

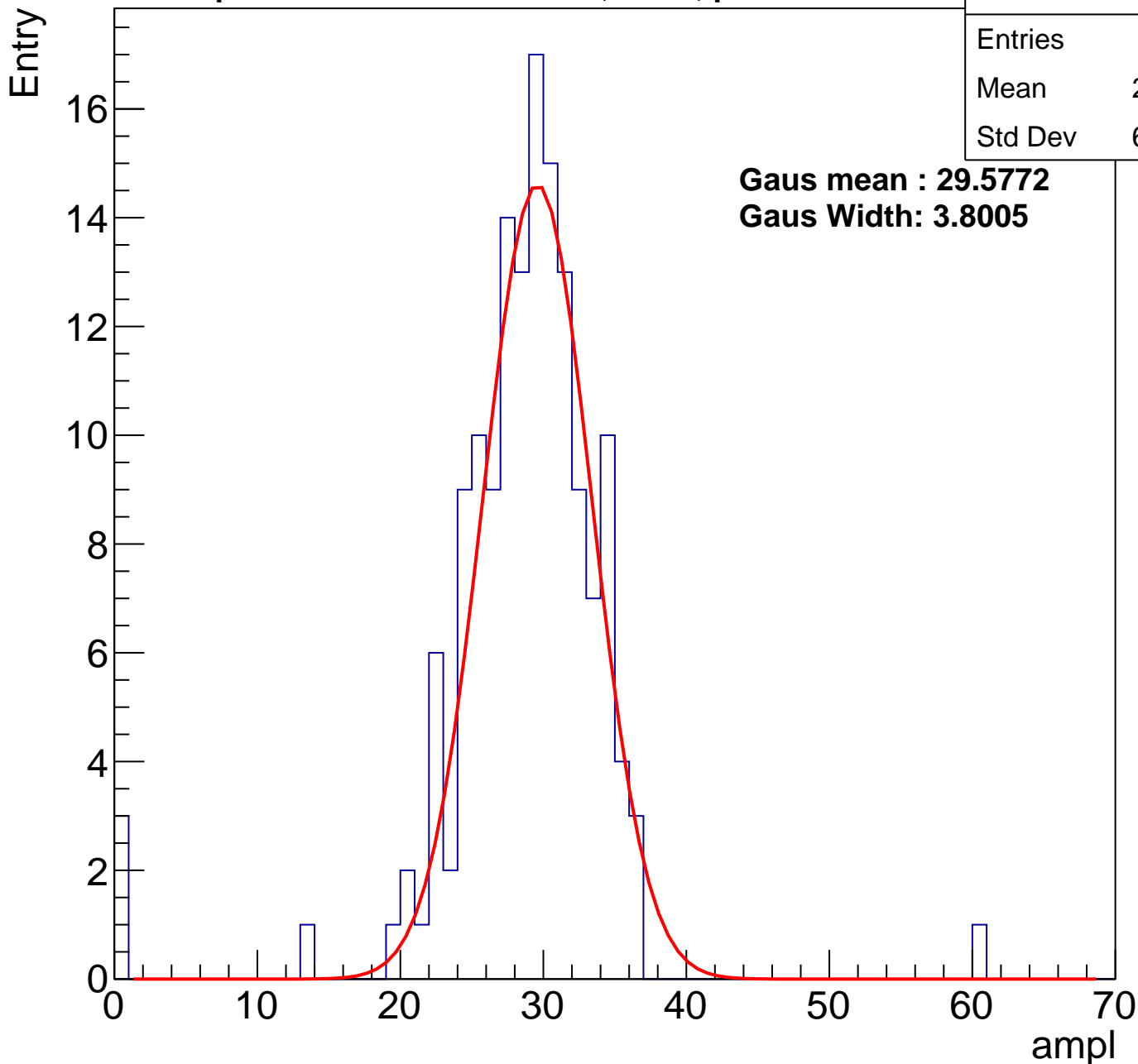
B0L101S, U24-ch0, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	28.17
Std Dev	6.146

Gaus mean : 29.5772

Gaus Width: 3.8005



B0L101S, U24-ch0, adc1

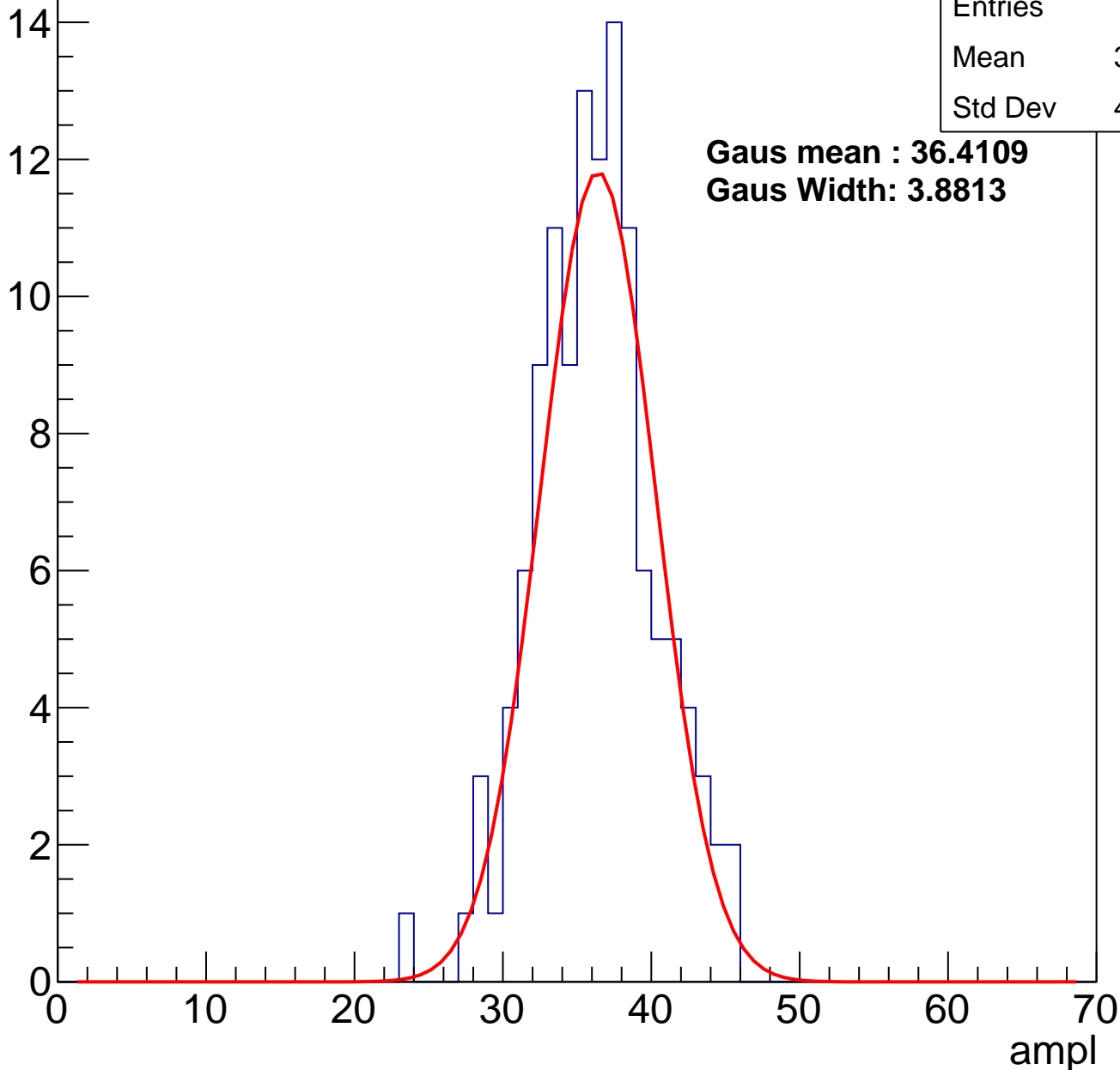
calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	35.76
Std Dev	4.016

Gaus mean : 36.4109

Gaus Width: 3.8813

Entry



B0L101S, U24-ch0, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

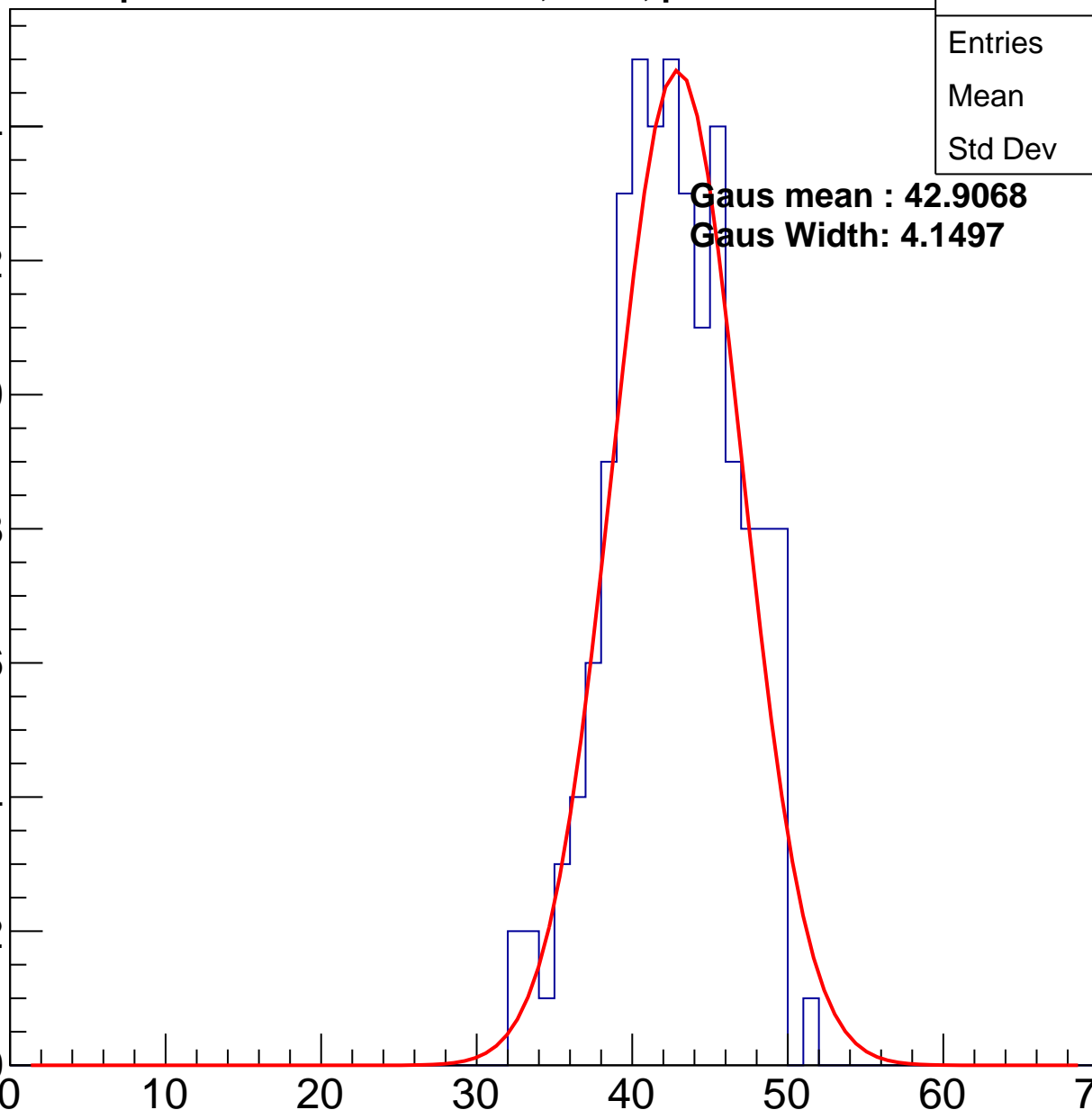
70

ampl

Entries	156
Mean	42.17
Std Dev	3.992

Gaus mean : 42.9068

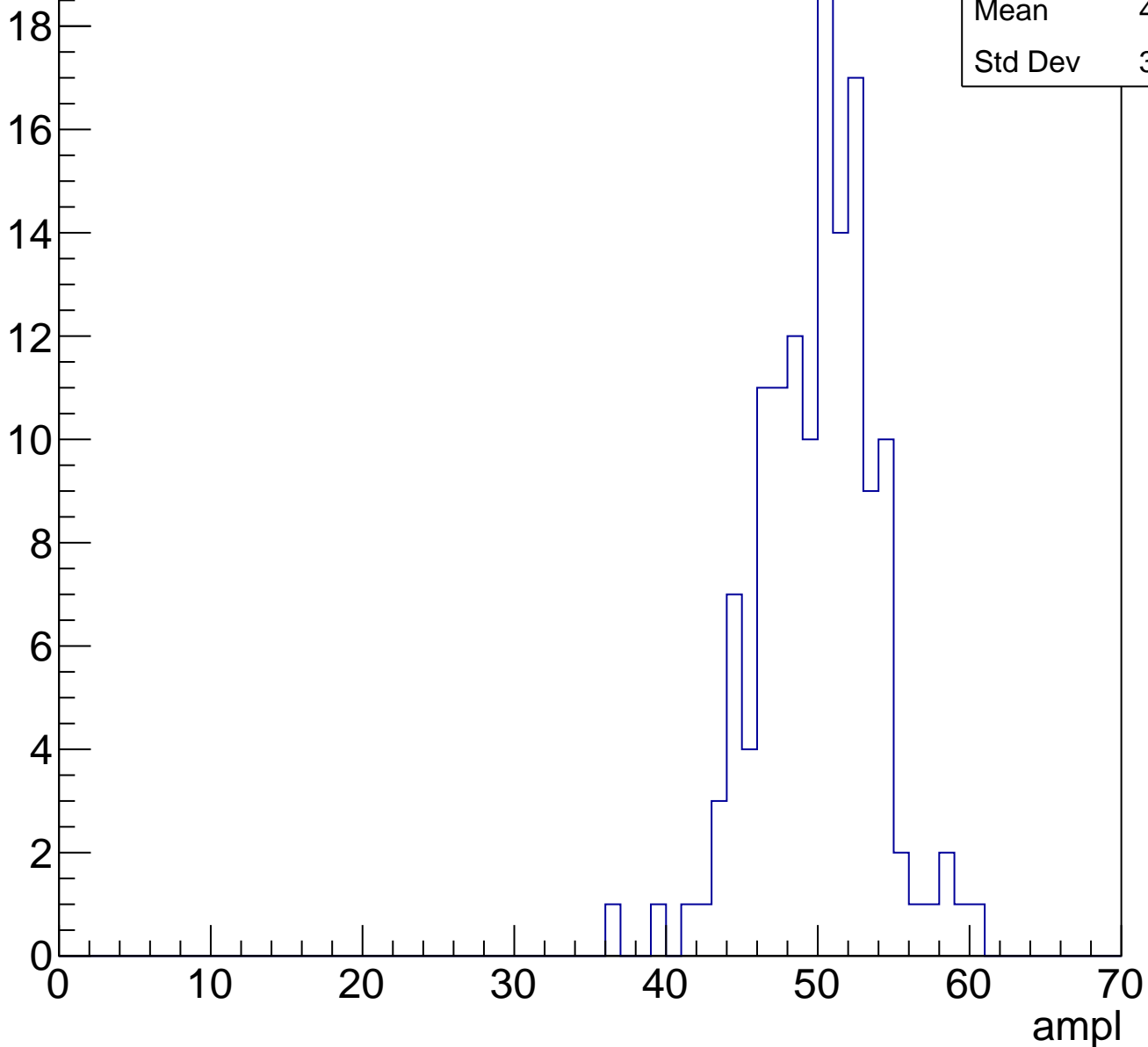
Gaus Width: 4.1497



B0L101S, U24-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch0, adc4

calib_packv5_042523_0143.root, FC#1, port C1

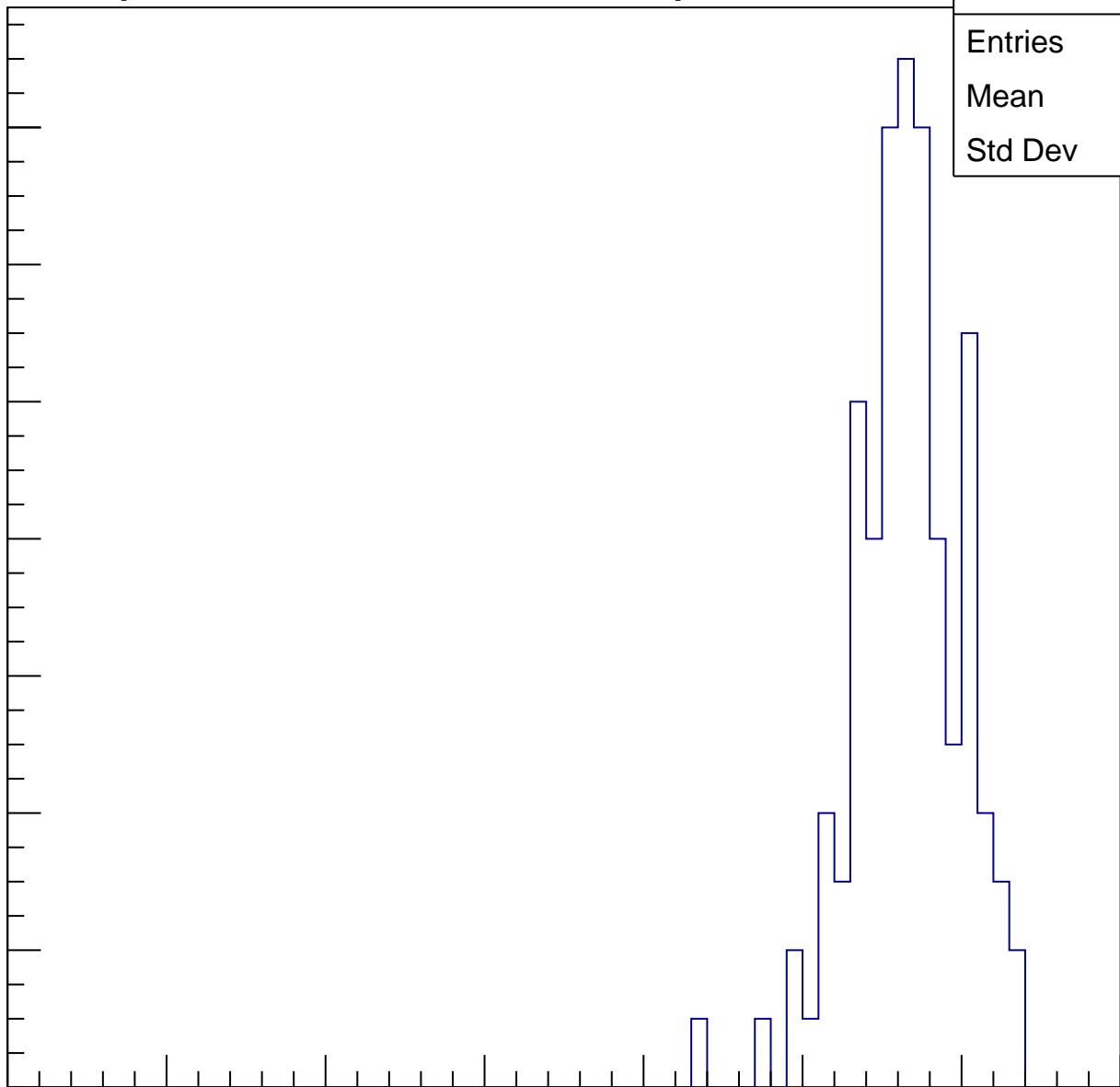
Entries	106
Mean	56.07
Std Dev	3.421

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch0, adc5

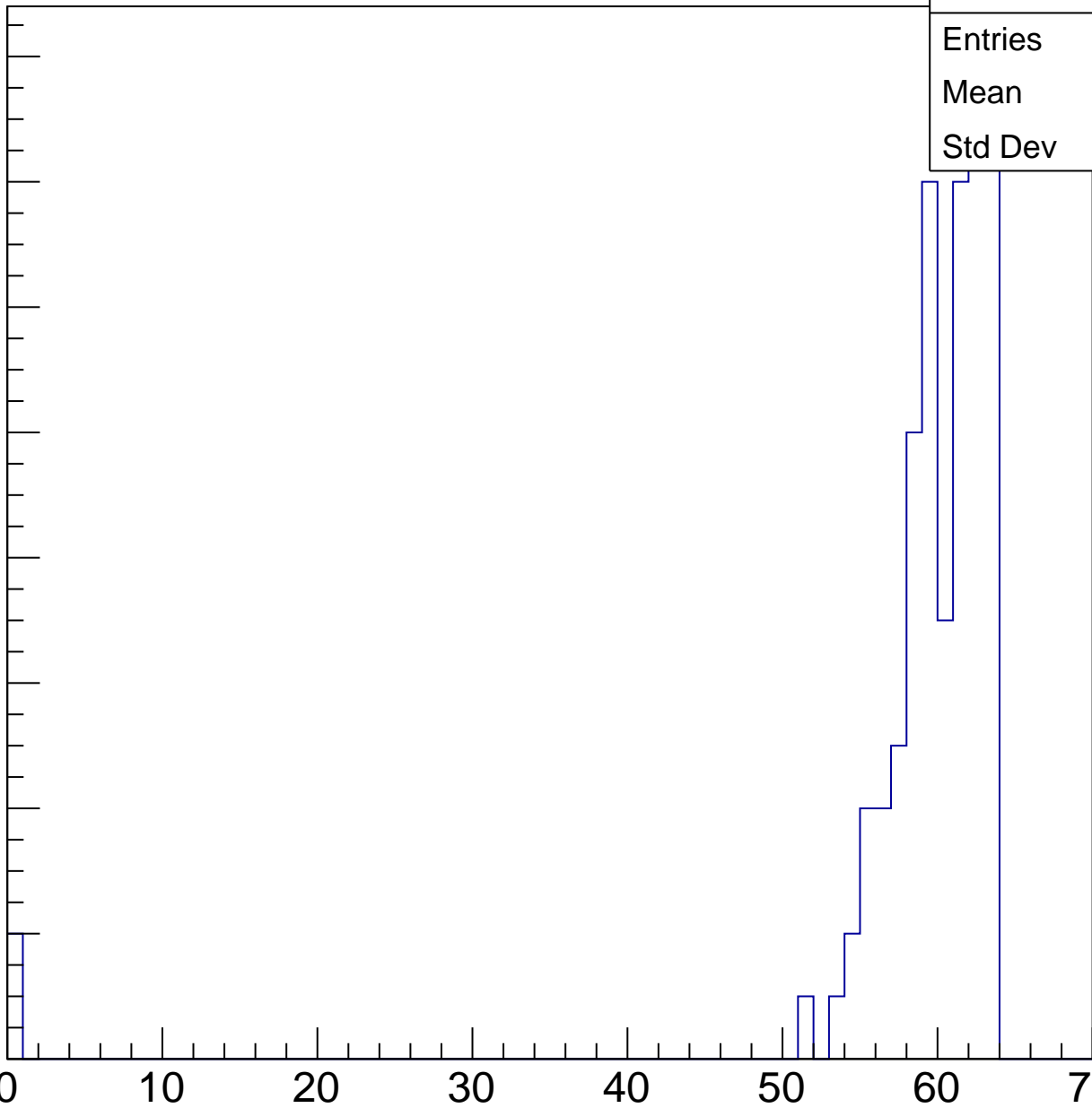
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.51
Std Dev	8.983

ampl



B0L101S, U24-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

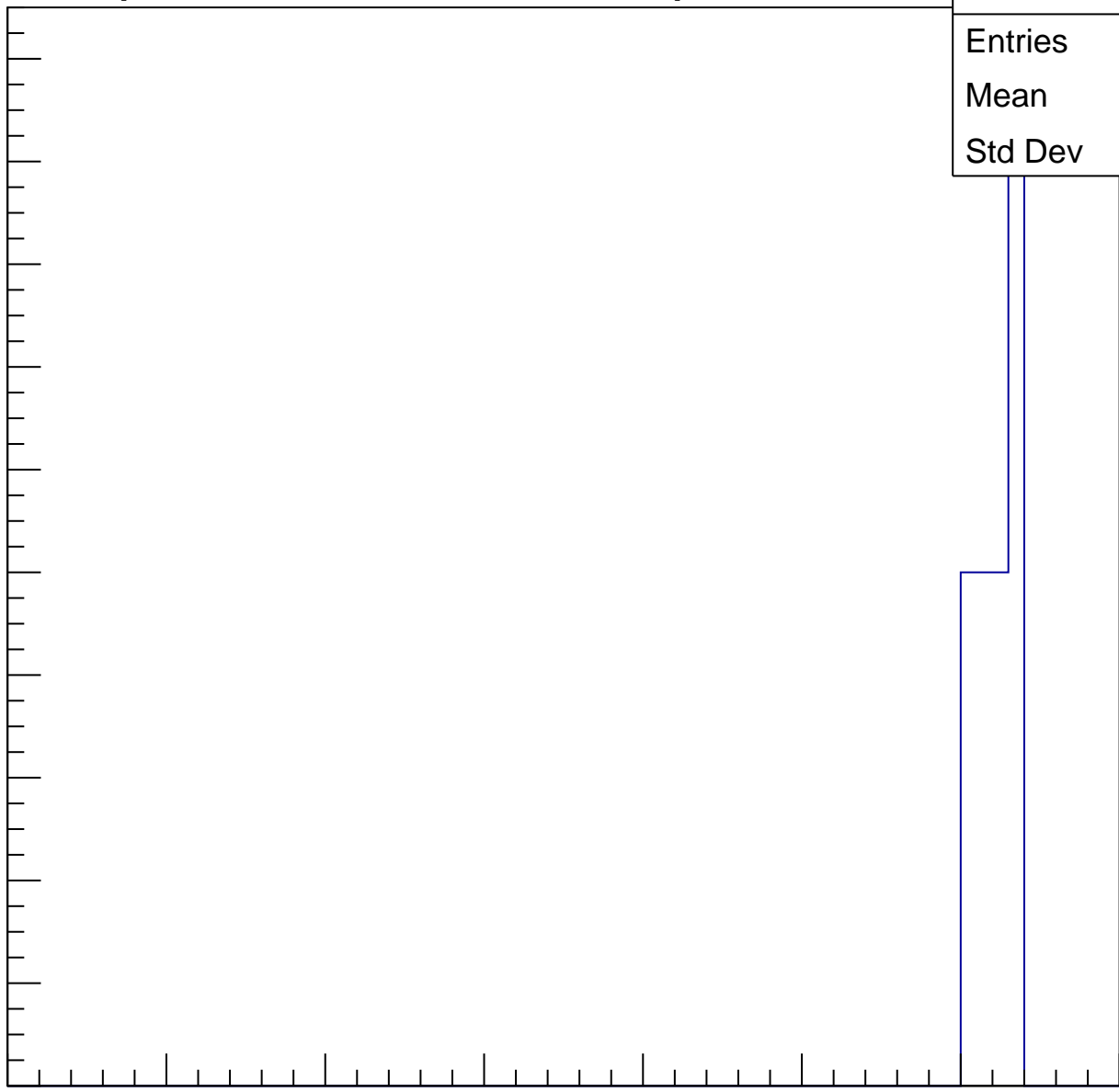
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



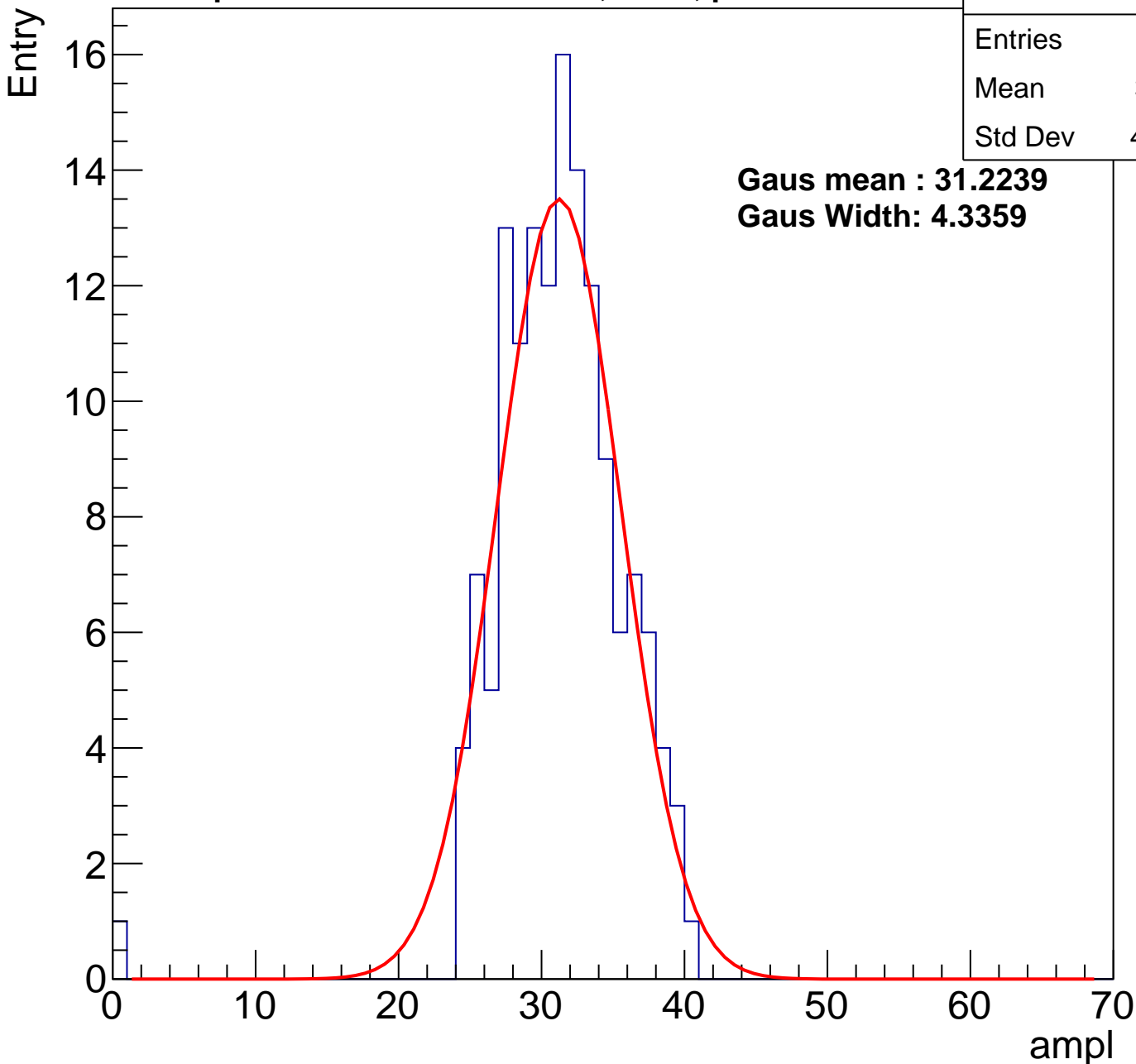
B0L101S, U24-ch1, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	30.81
Std Dev	4.563

Gaus mean : 31.2239

Gaus Width: 4.3359



B0L101S, U24-ch1, adc1

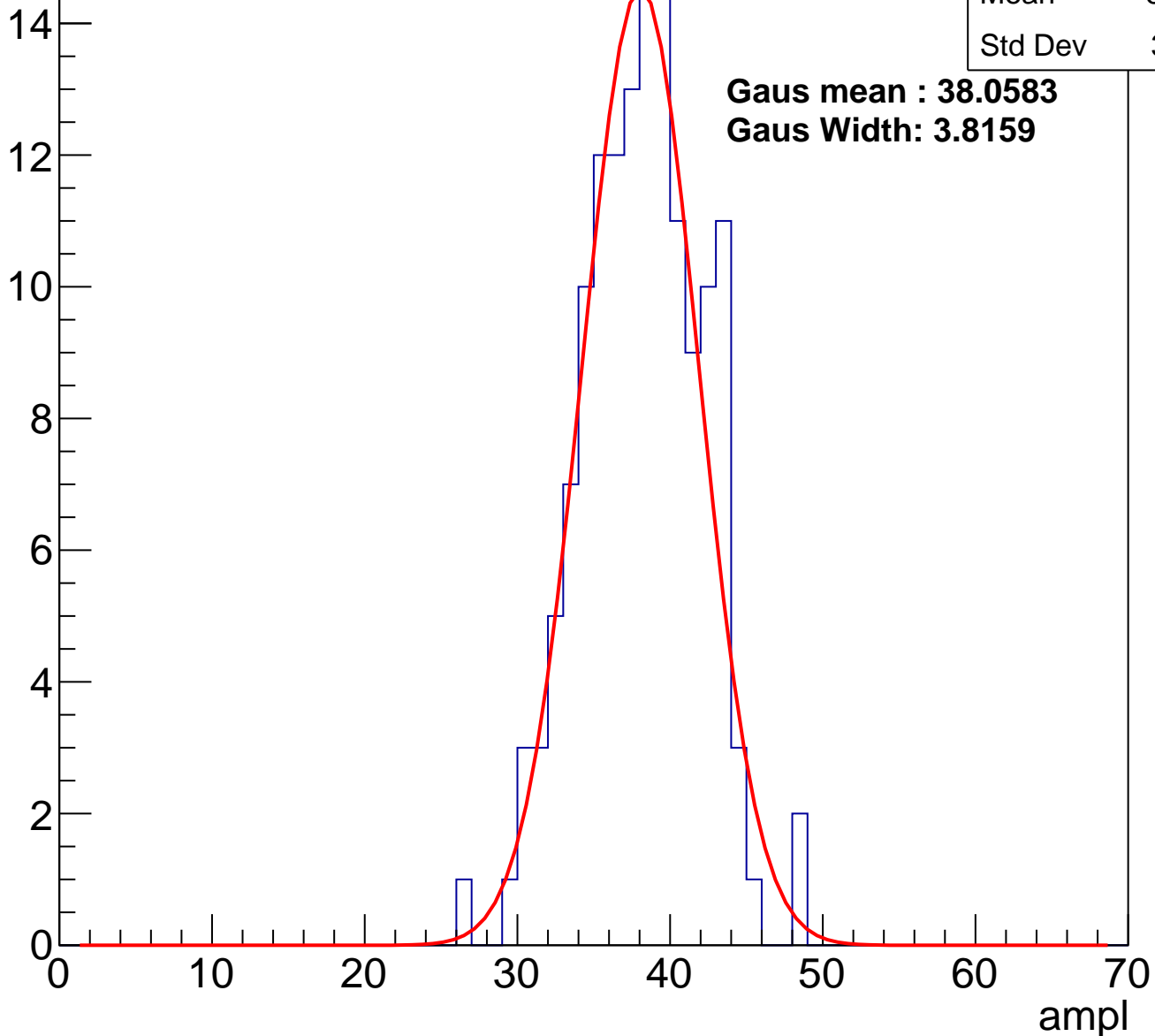
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	37.72
Std Dev	3.881

Gaus mean : 38.0583

Gaus Width: 3.8159

Entry



B0L101S, U24-ch1, adc2

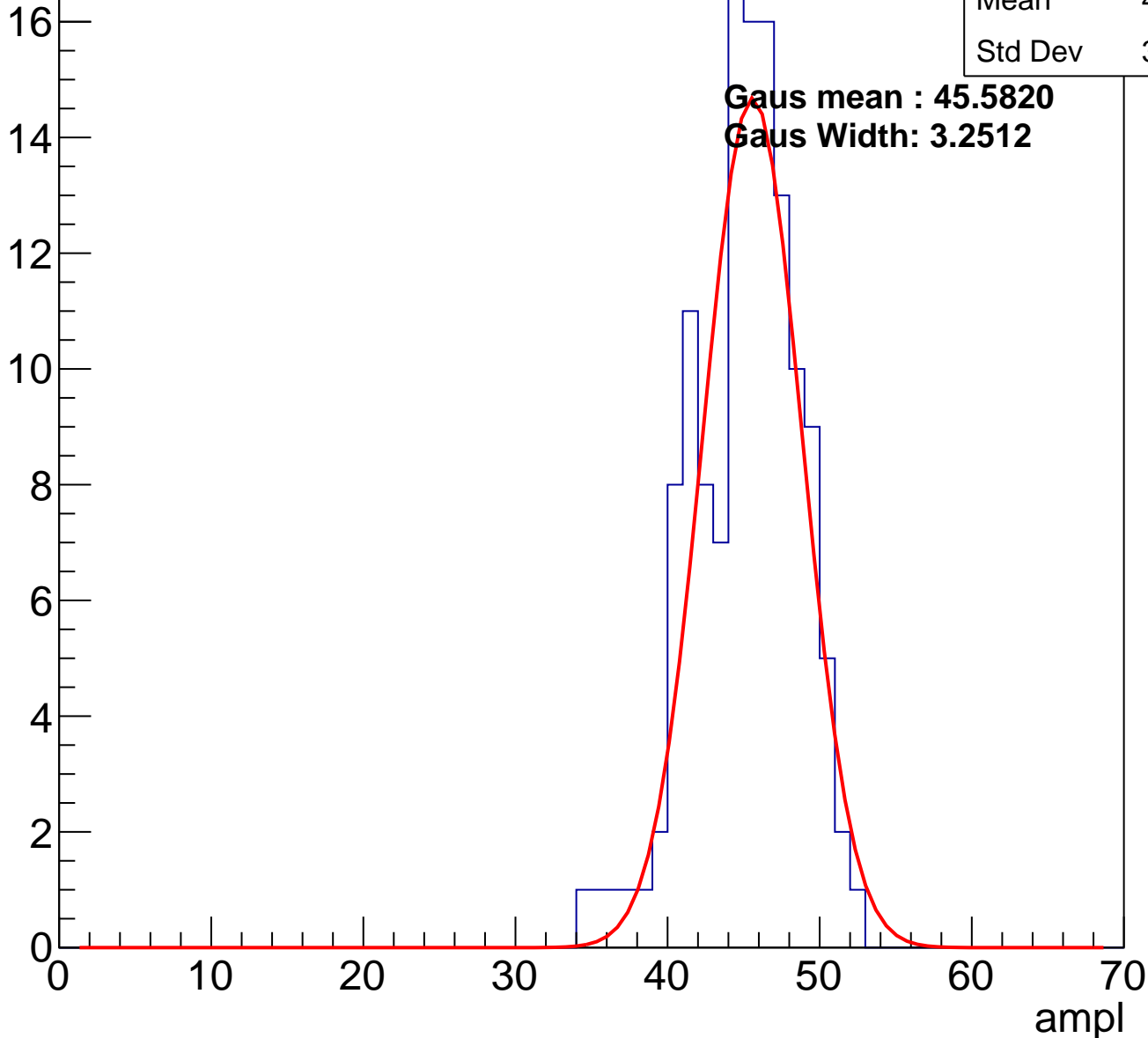
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	130
Mean	44.66
Std Dev	3.412

Gaus mean : 45.5820

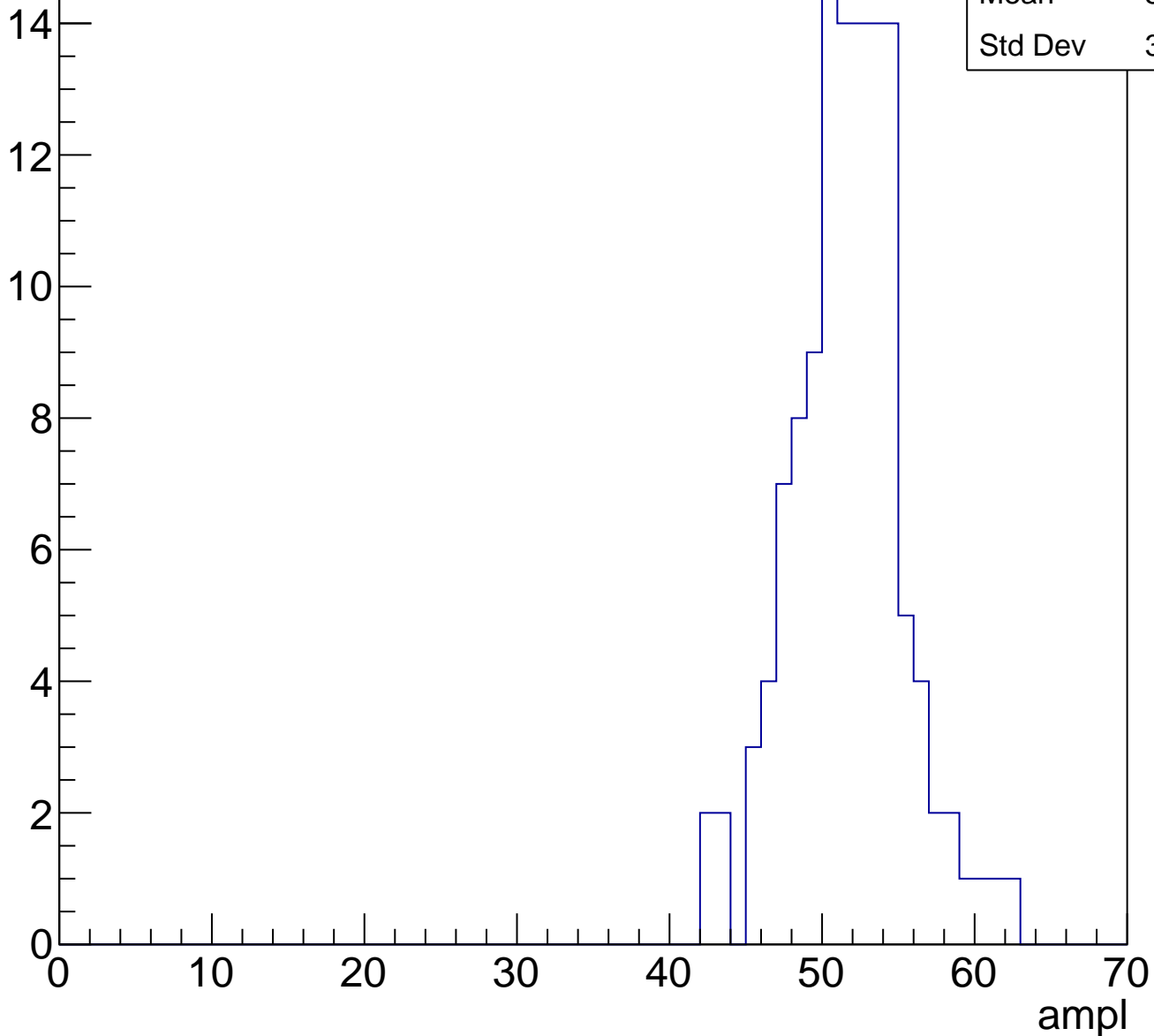
Gaus Width: 3.2512



B0L101S, U24-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch1, adc4

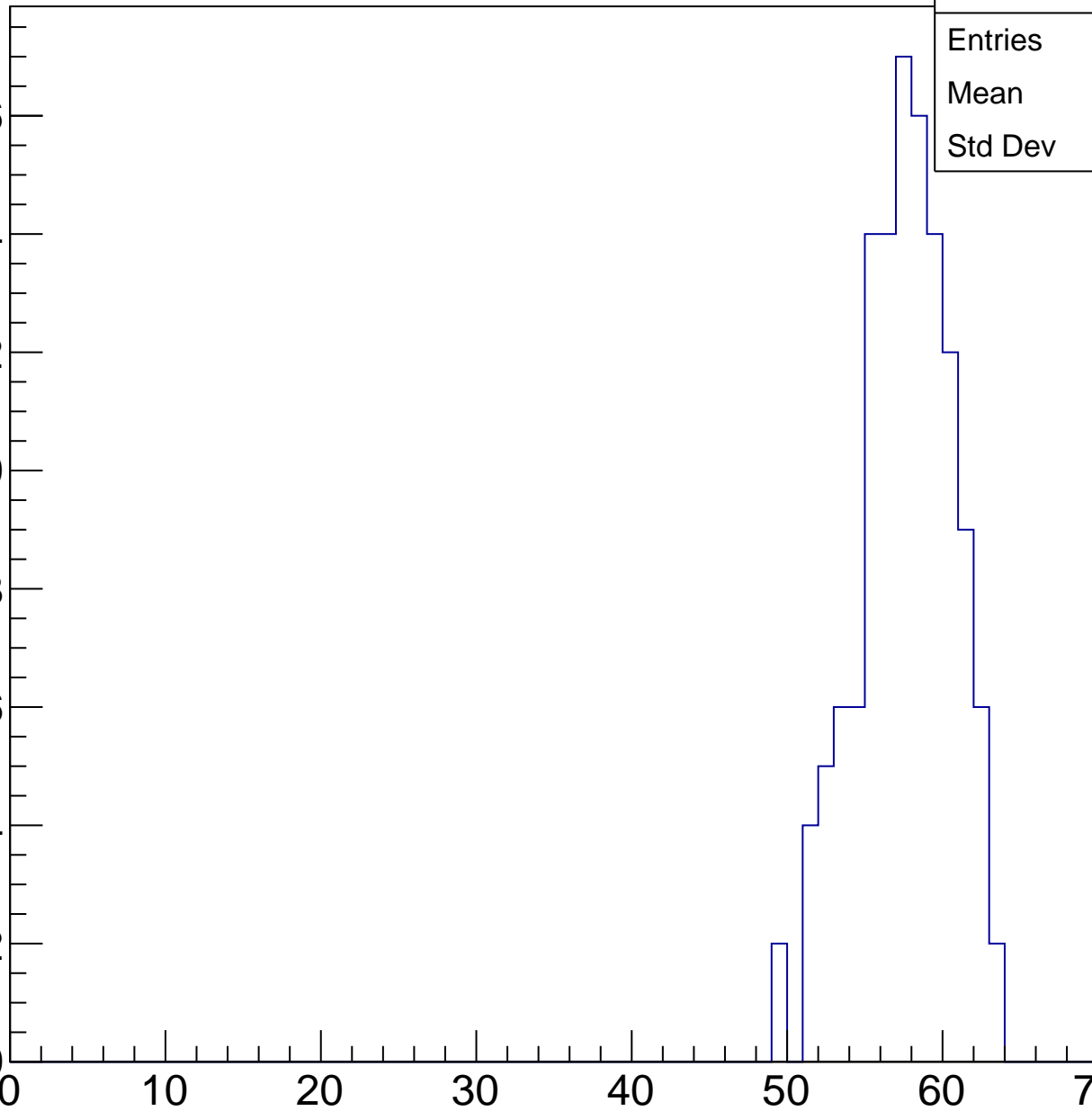
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	57.07
Std Dev	3.025

ampl



B0L101S, U24-ch1, adc5

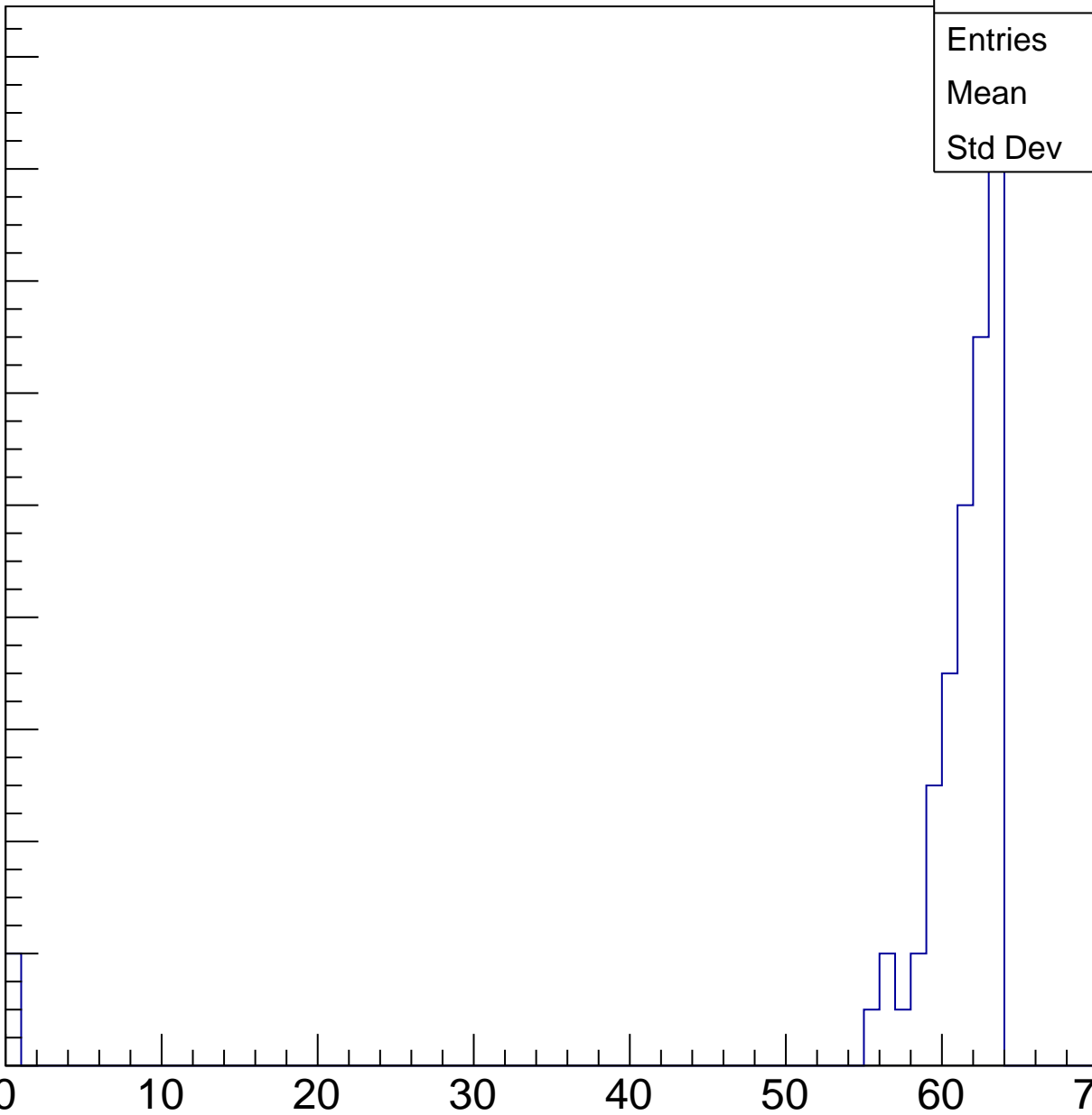
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	61
Mean	59.1
Std Dev	11.05

ampl



B0L101S, U24-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	29.65
Std Dev	3.707

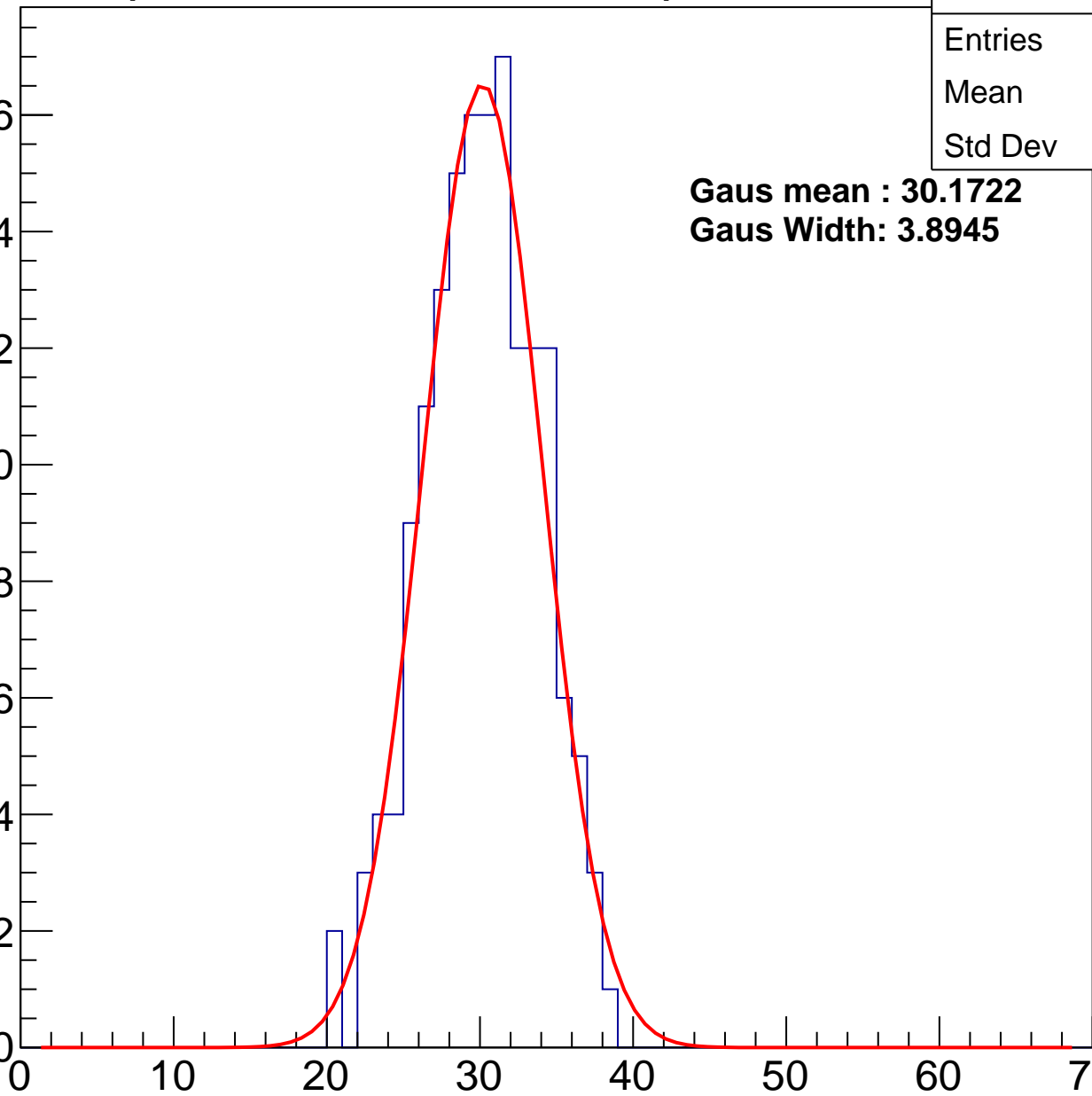
Gaus mean : 30.1722

Gaus Width: 3.8945

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch2, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	37.17
Std Dev	3.691

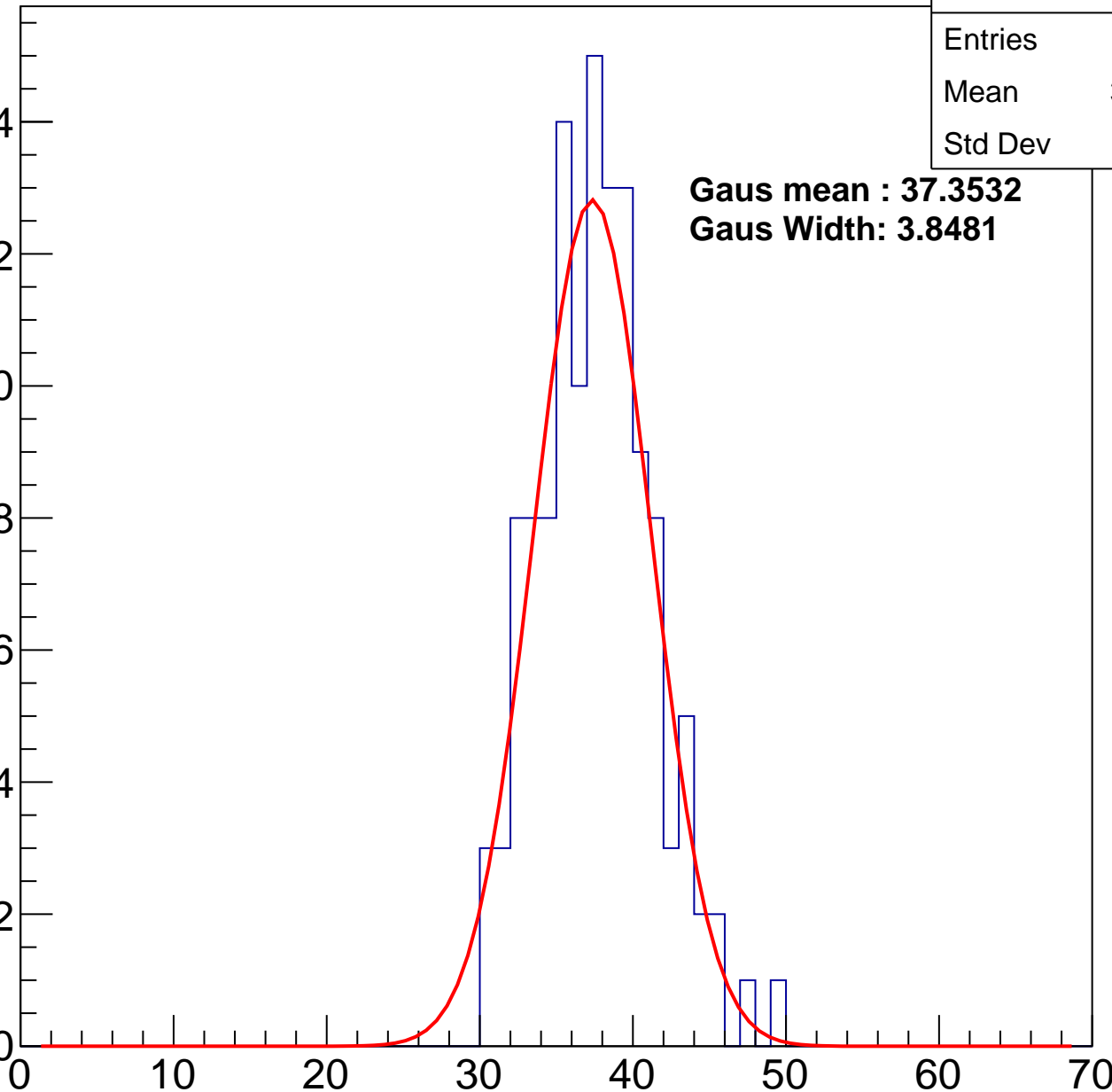
Gaus mean : 37.3532

Gaus Width: 3.8481

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch2, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

142

Mean

43.47

Std Dev

3.636

Gaus mean : 43.9676

Gaus Width: 3.7765

0

10

20

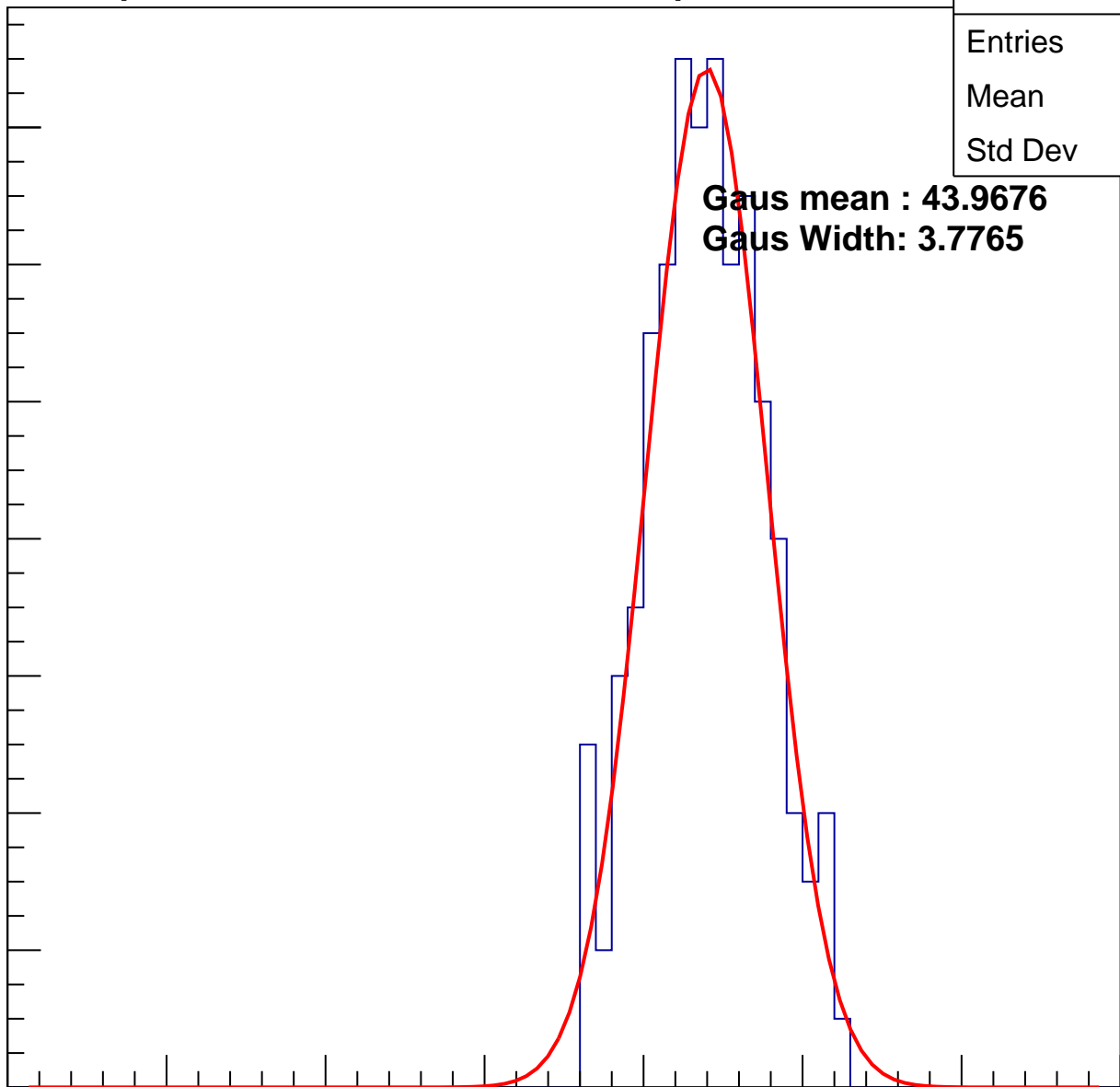
30

40

50

60

ampl



B0L101S, U24-ch2, adc3

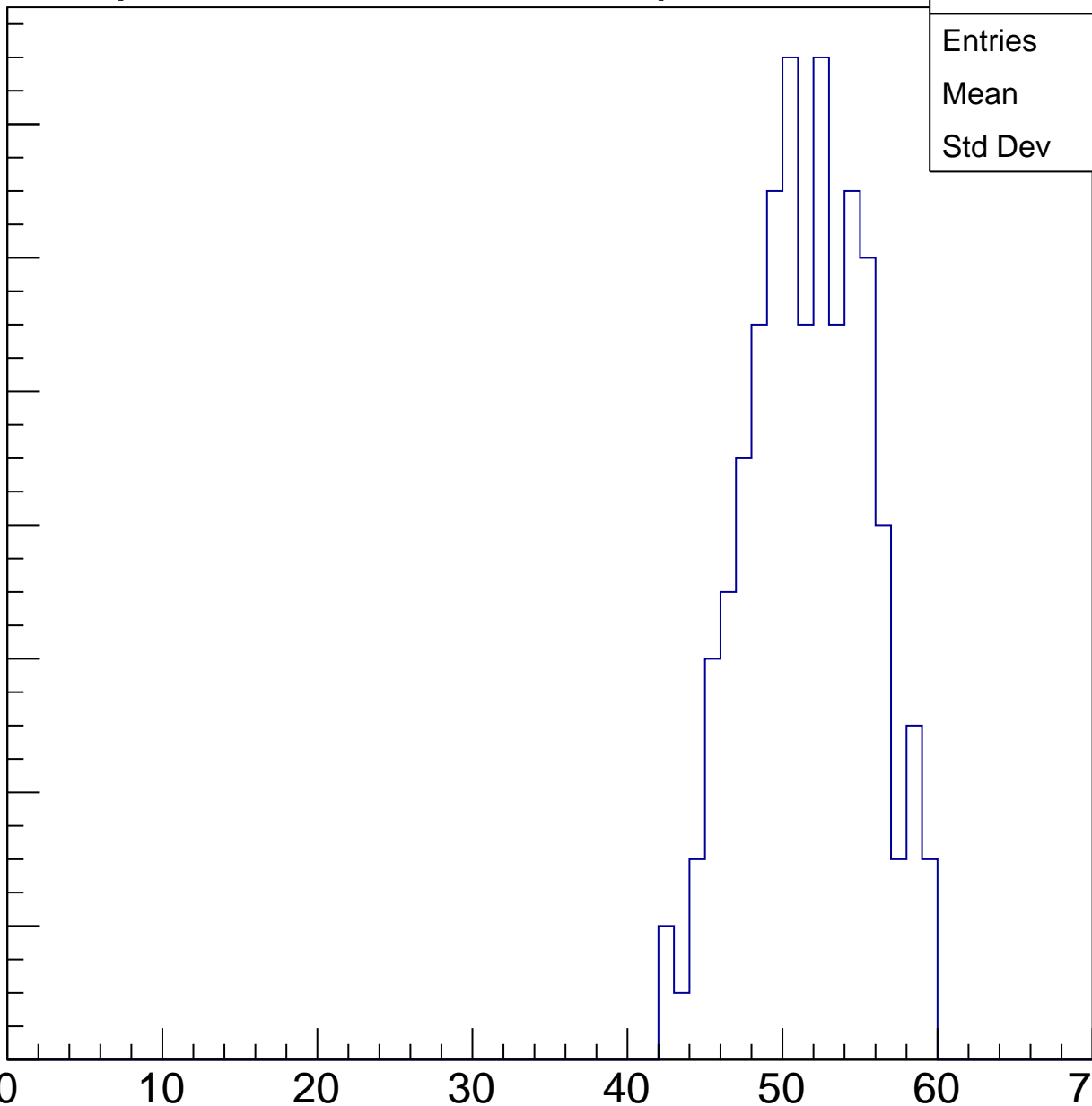
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	148
Mean	51.09
Std Dev	3.843

ampl



B0L101S, U24-ch2, adc4

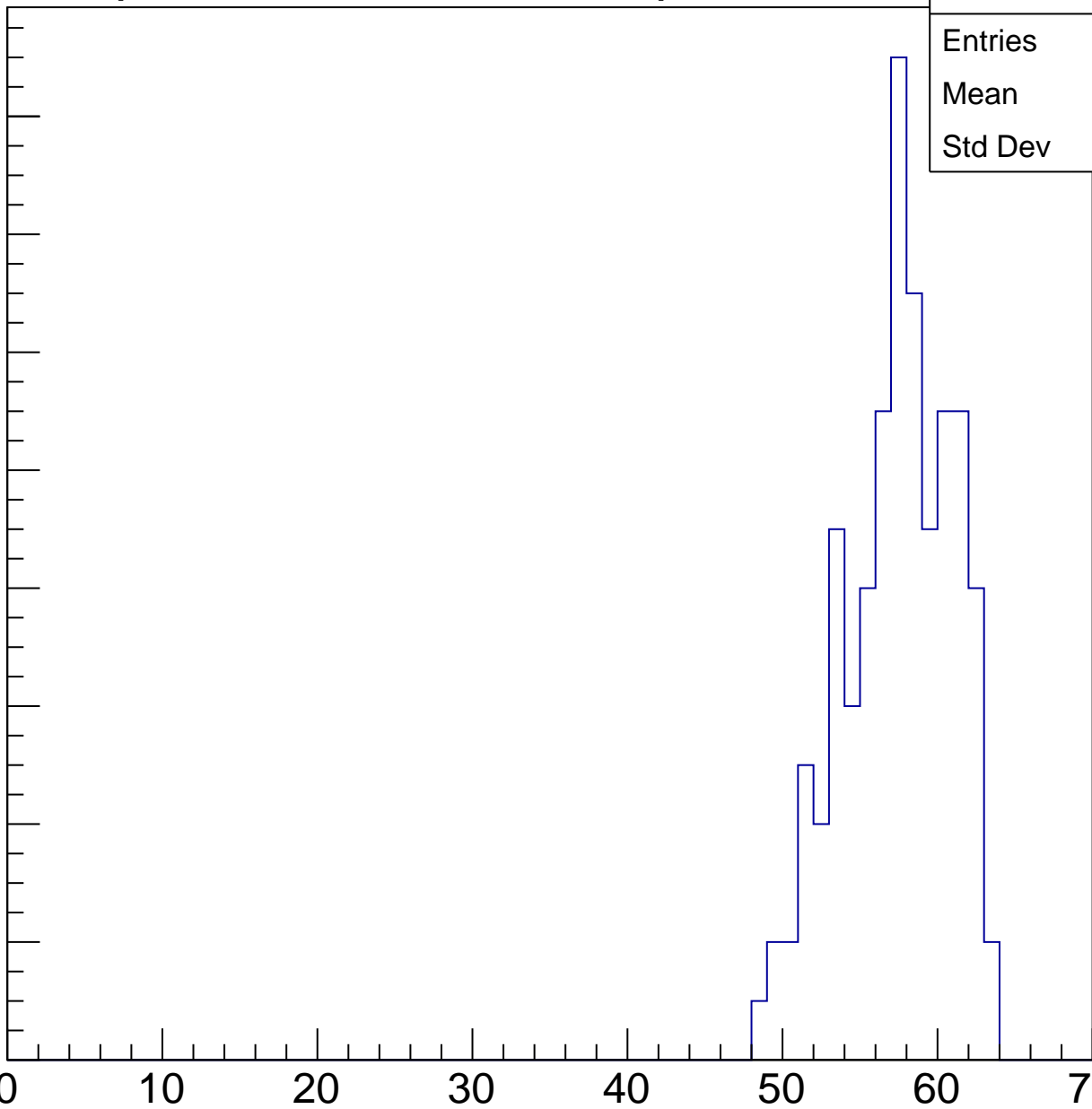
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	56.92
Std Dev	3.458

ampl



B0L101S, U24-ch2, adc5

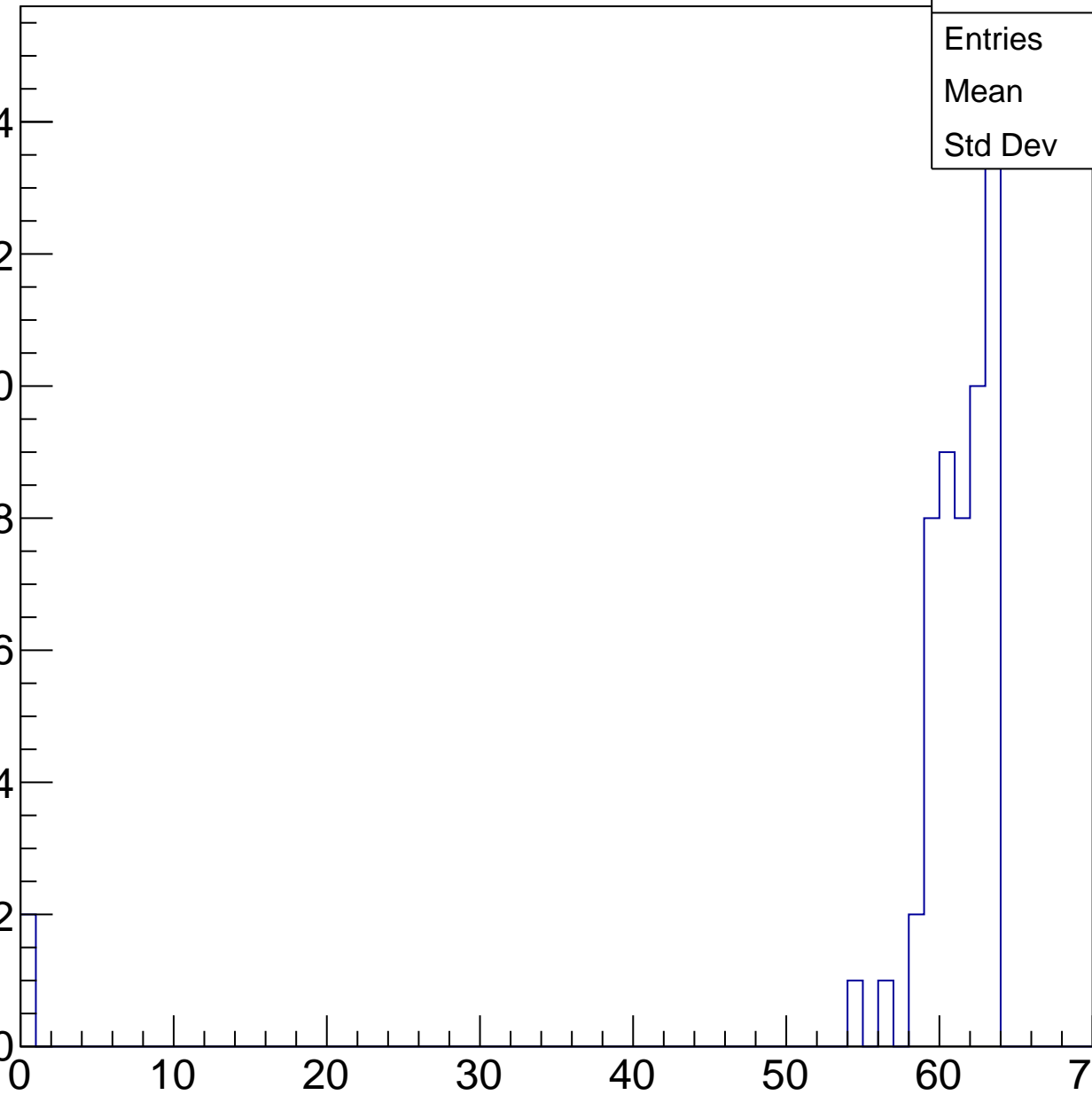
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	56
Mean	58.77
Std Dev	11.47

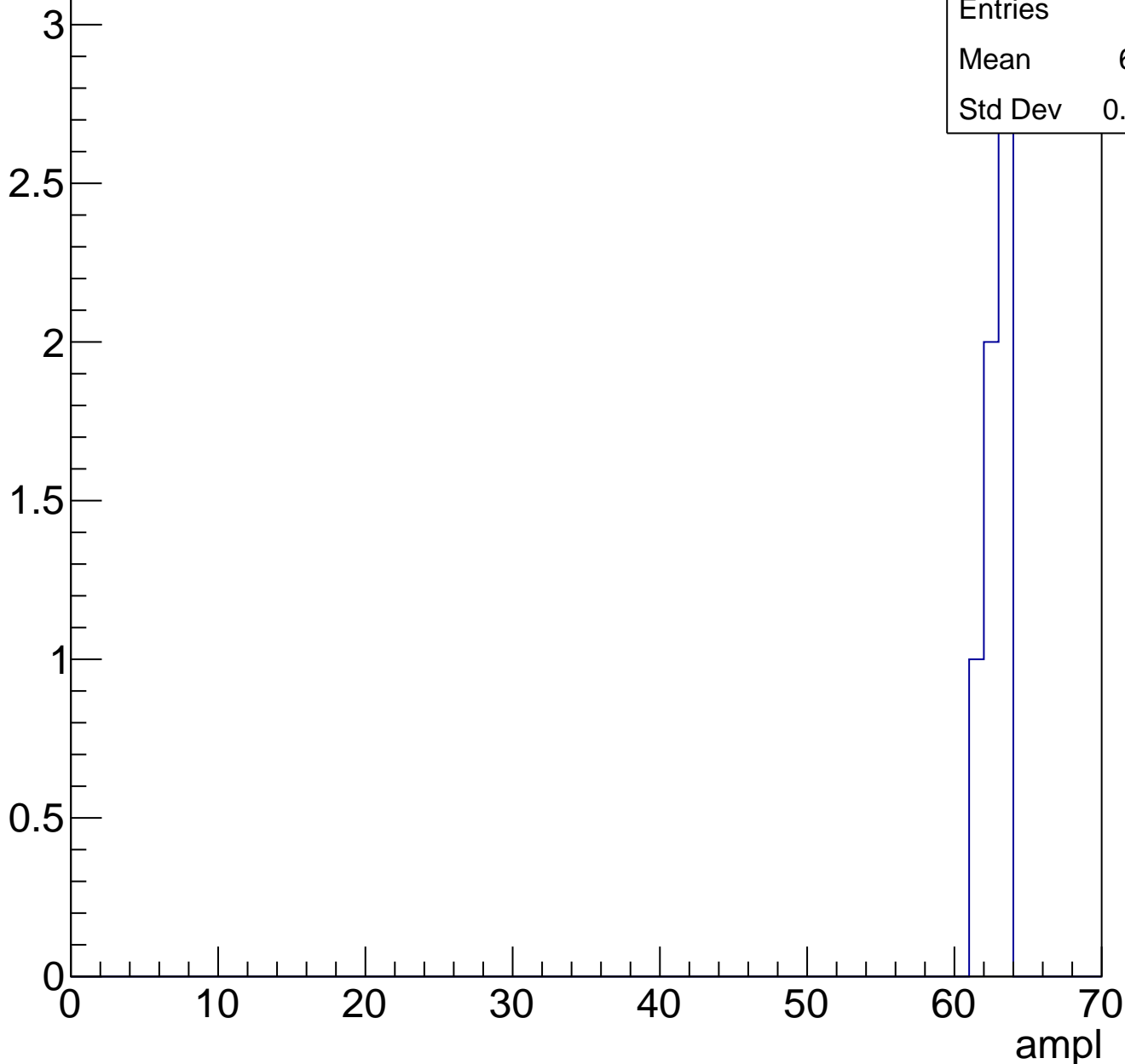
ampl



B0L101S, U24-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch3, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	30.48
Std Dev	4.259

Gaus mean : 31.4318

Gaus Width: 3.4363

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

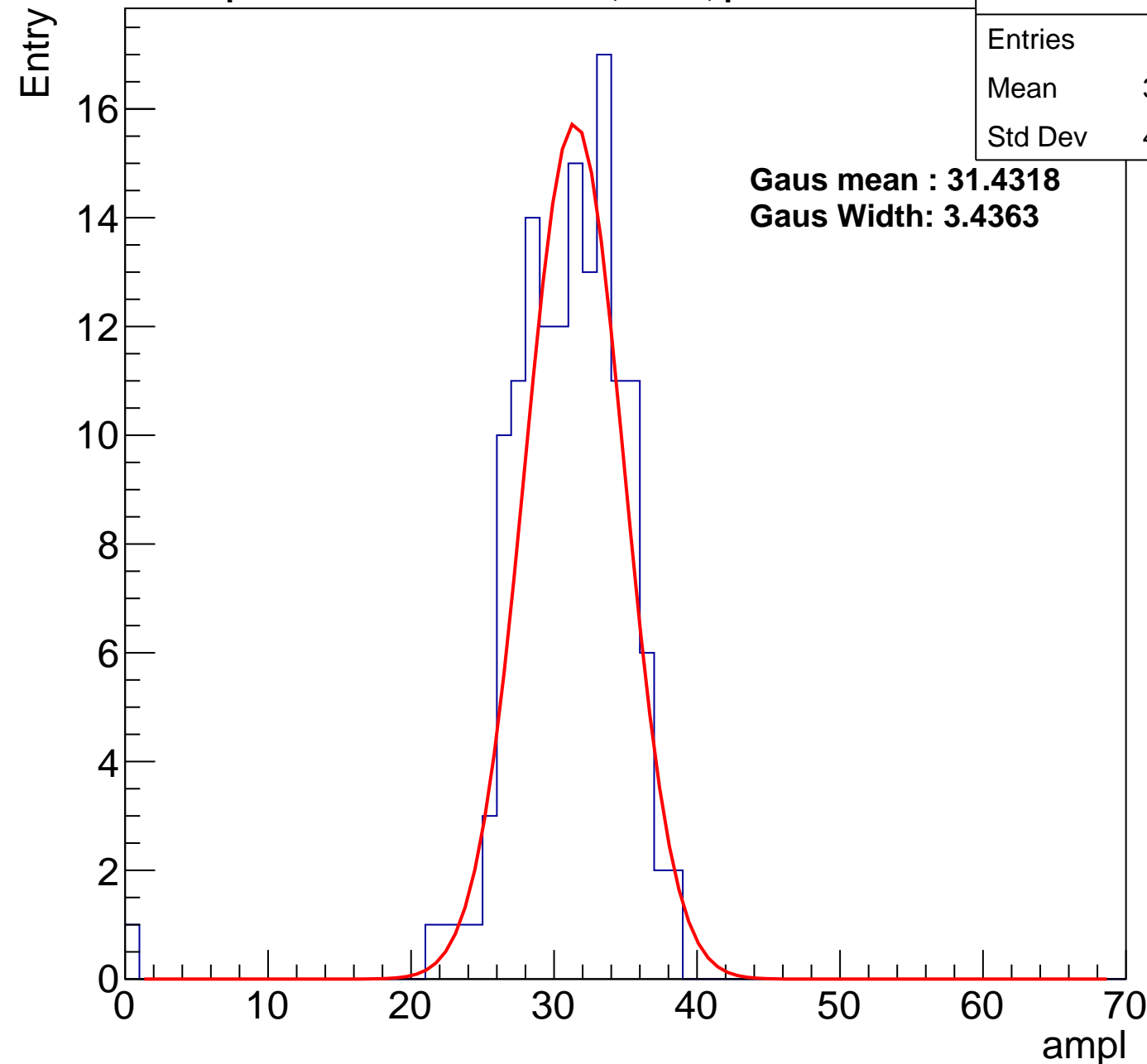
40

50

60

70

ampl



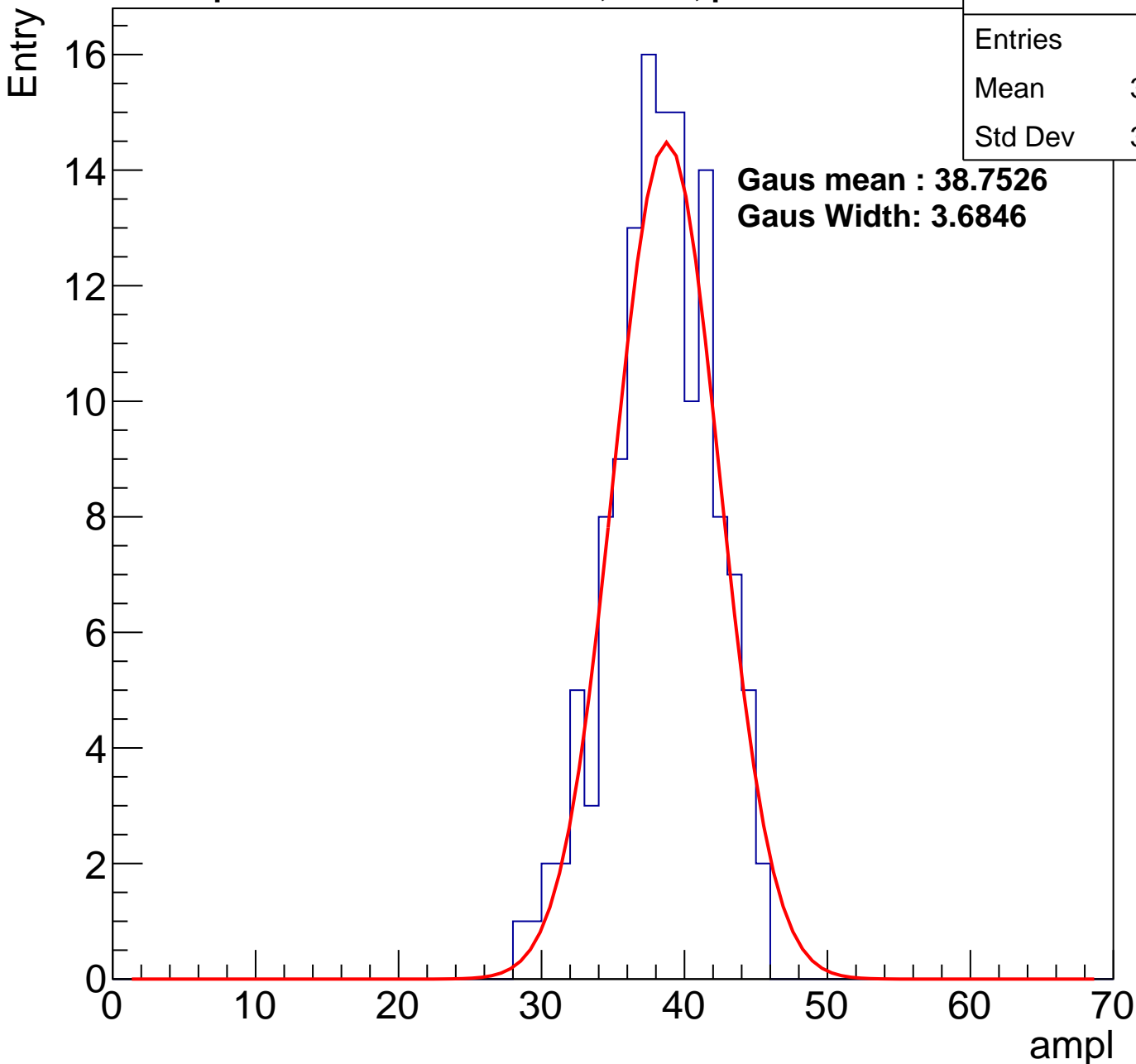
B0L101S, U24-ch3, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	37.95
Std Dev	3.536

Gaus mean : 38.7526

Gaus Width: 3.6846



B0L101S, U24-ch3, adc2

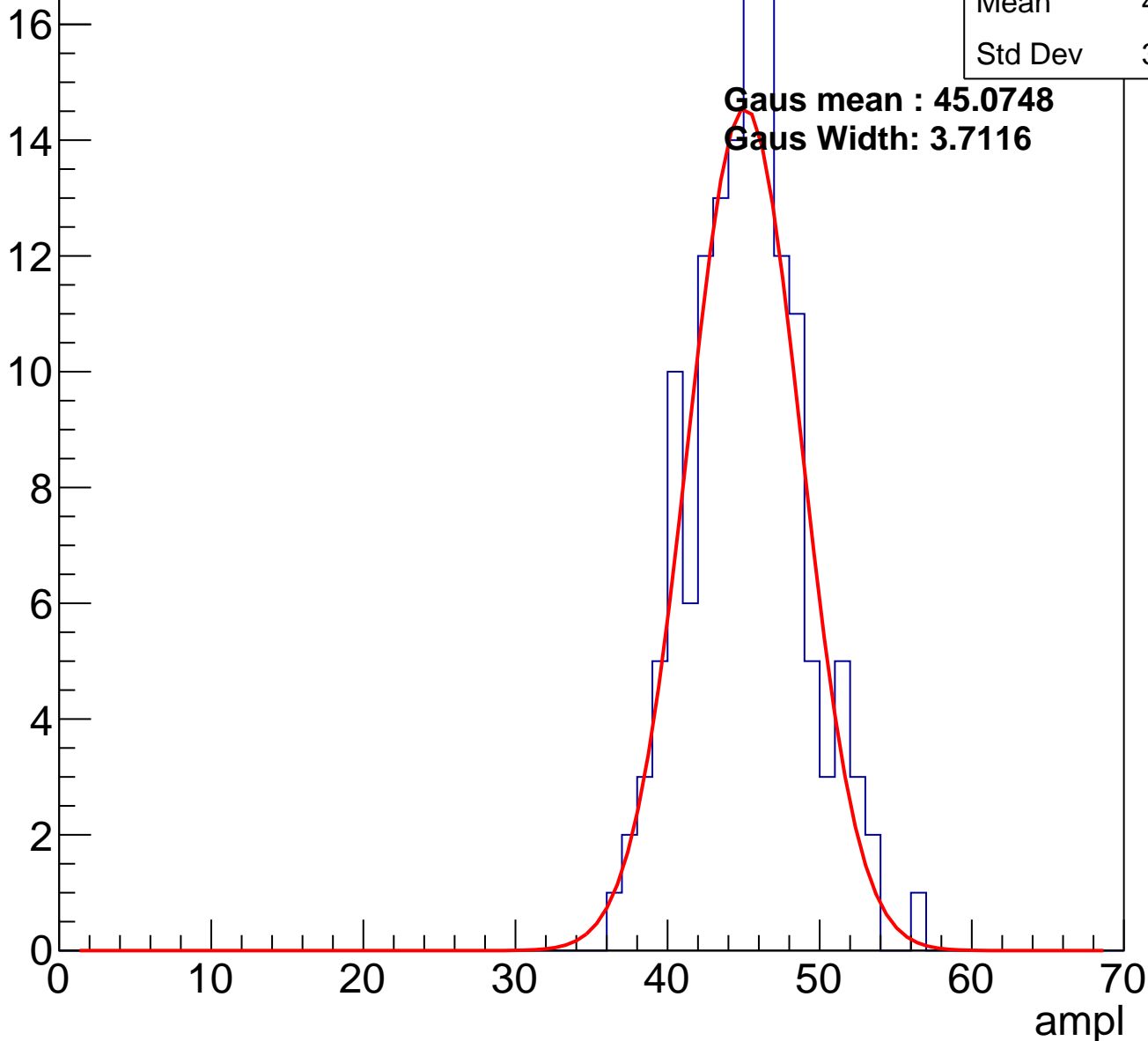
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	142
Mean	44.73
Std Dev	3.696

Gaus mean : 45.0748

Gaus Width: 3.7116



B0L101S, U24-ch3, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

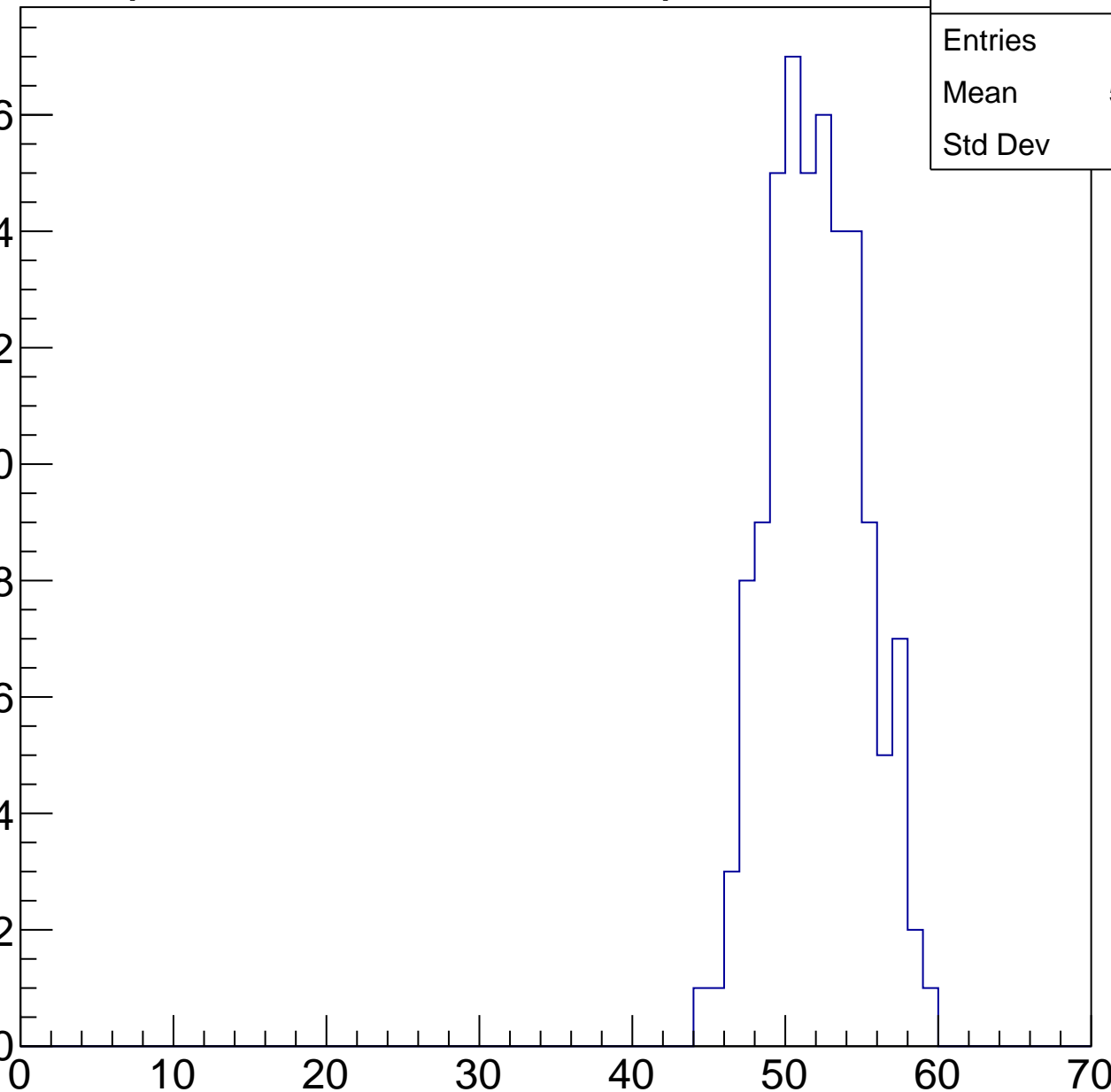
Mean

51.56

Std Dev

3.061

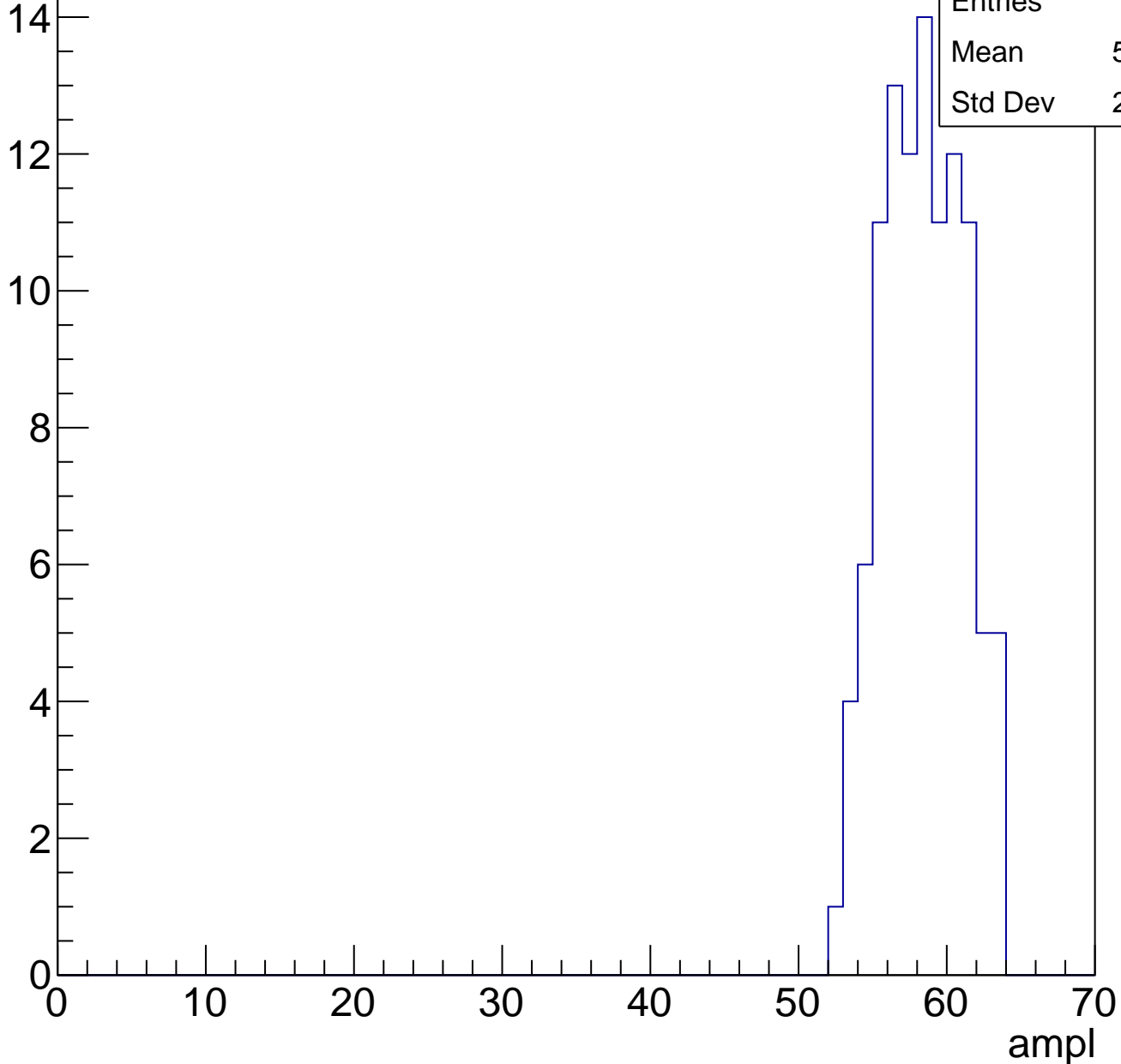
ampl



B0L101S, U24-ch3, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch3, adc5

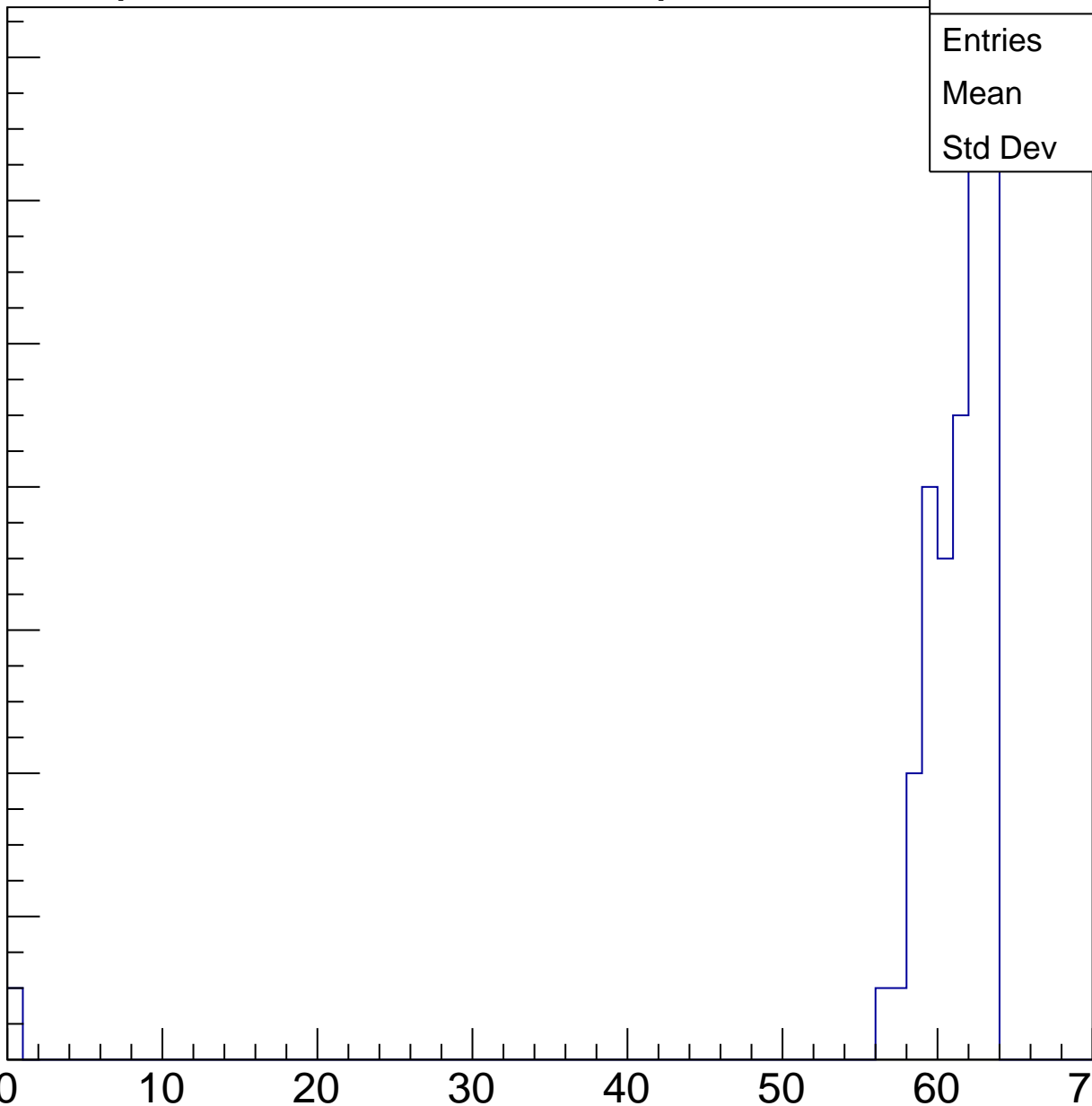
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.93
Std Dev	8.067

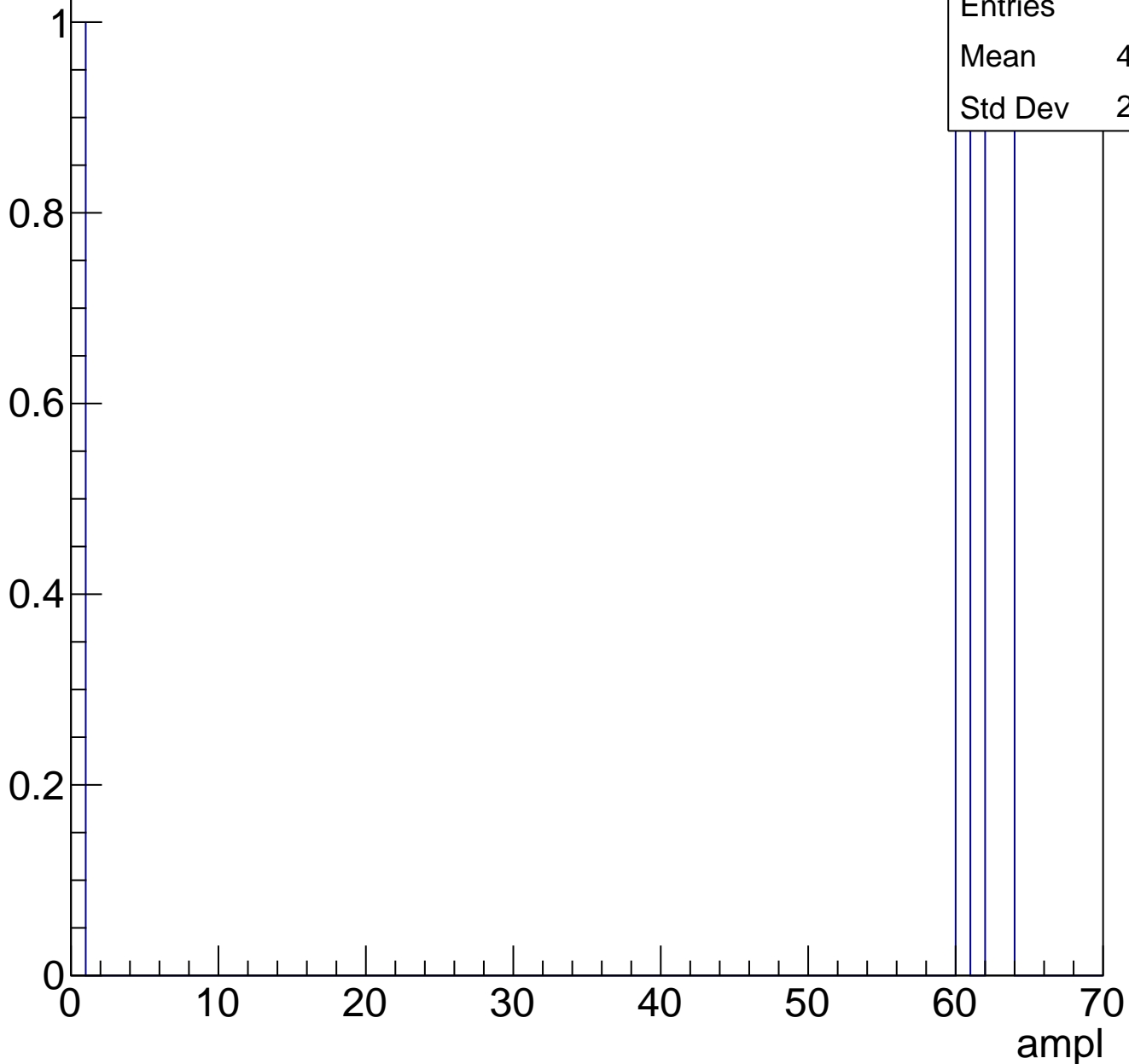
ampl



B0L101S, U24-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch4, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	30.02
Std Dev	4.568

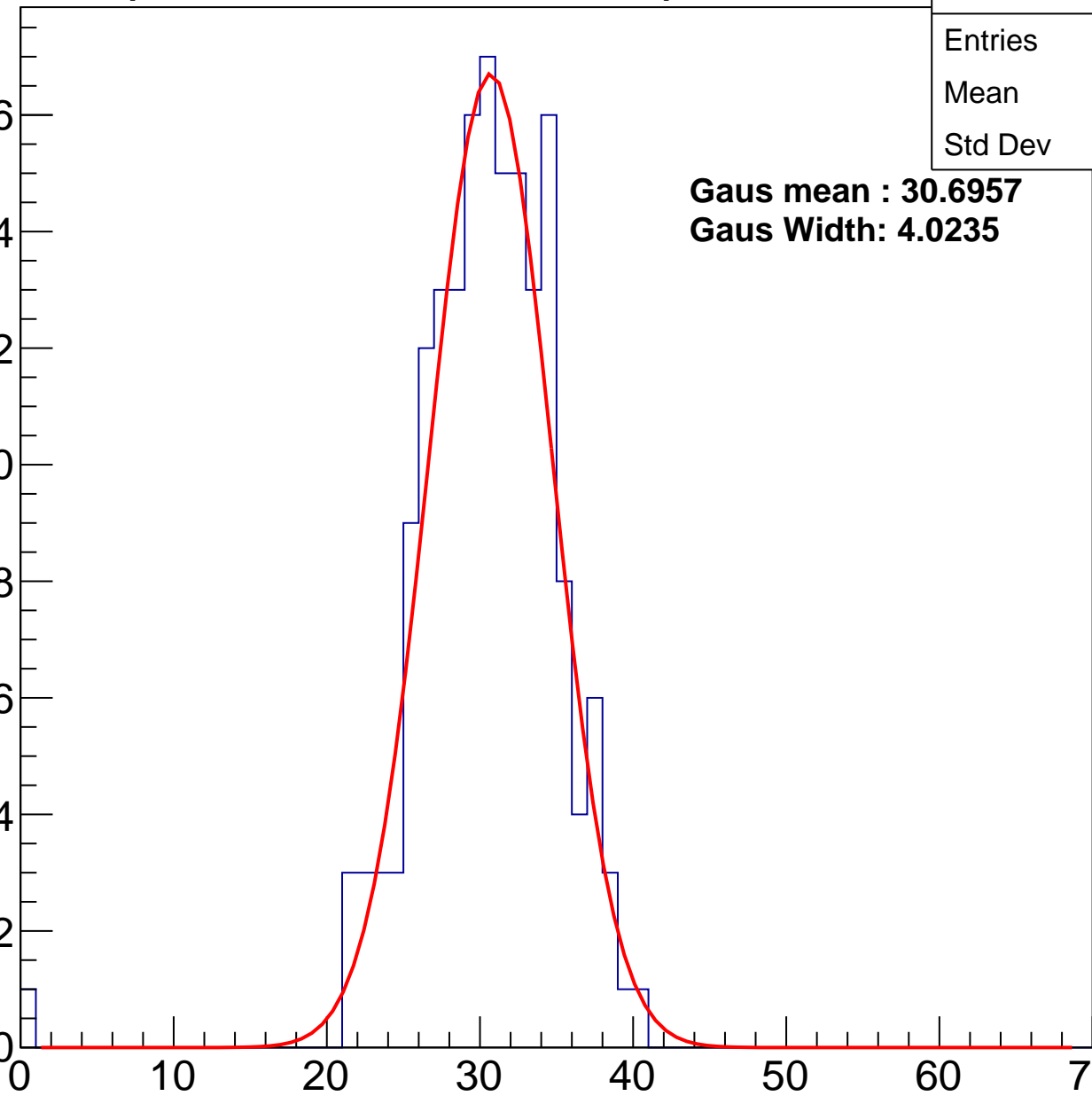
Gaus mean : 30.6957

Gaus Width: 4.0235

Entry

16
14
12
10
8
6
4
2
0

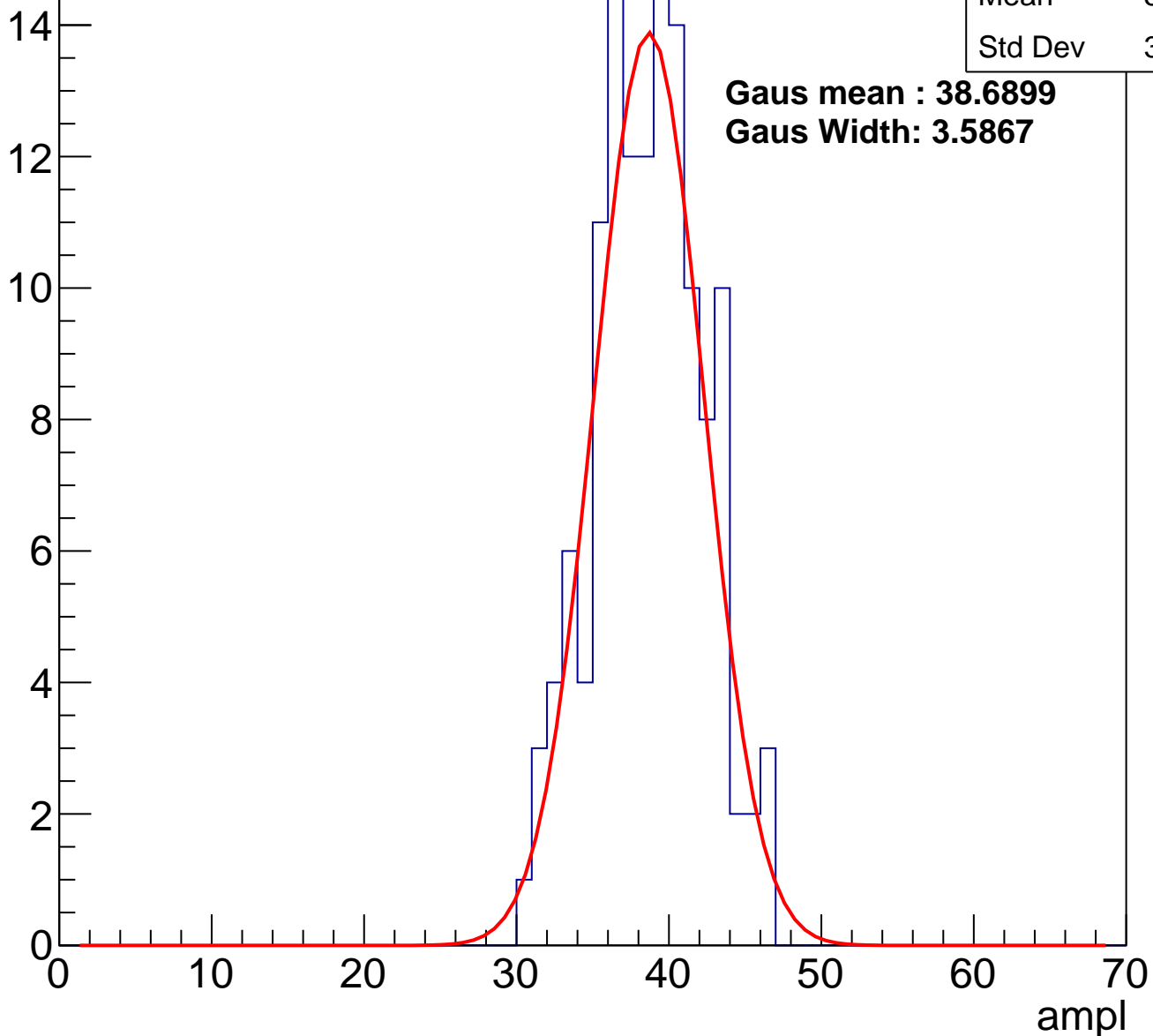
ampl



B0L101S, U24-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	132
Mean	38.23
Std Dev	3.518

B0L101S, U24-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

129

Mean

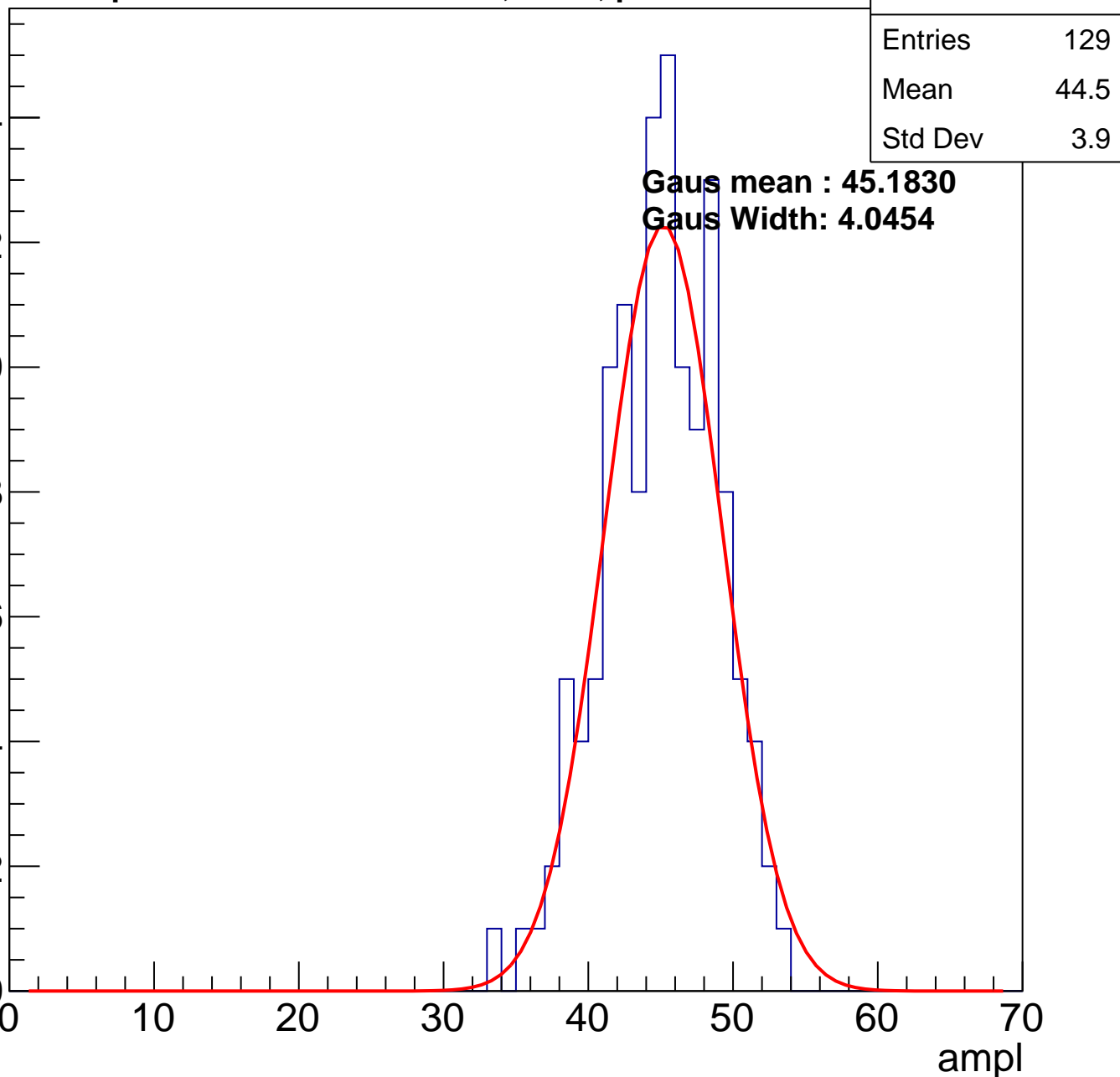
44.5

Std Dev

3.9

Gaus mean : 45.1830

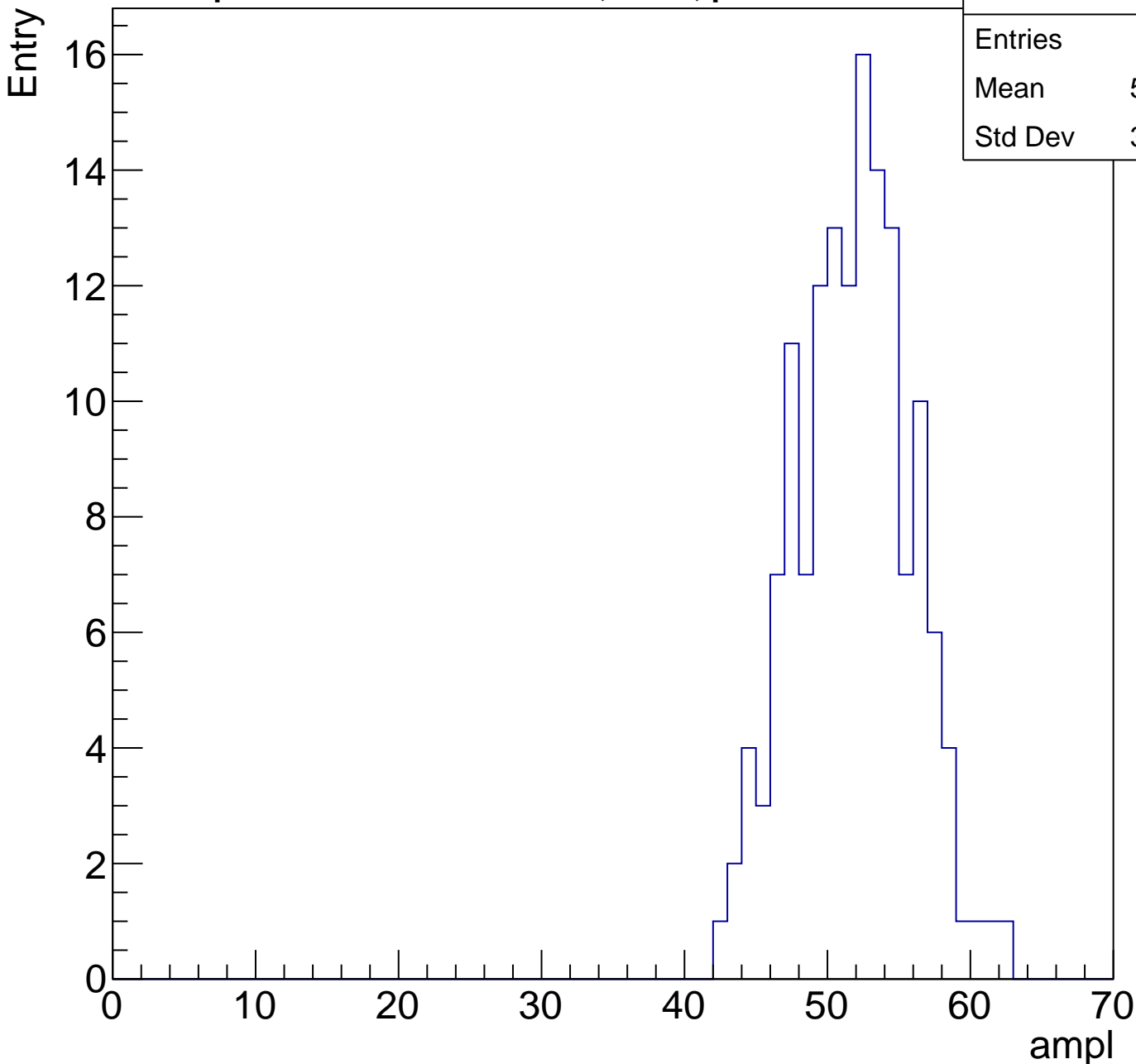
Gaus Width: 4.0454



B0L101S, U24-ch4, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	51.38
Std Dev	3.966



B0L101S, U24-ch4, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

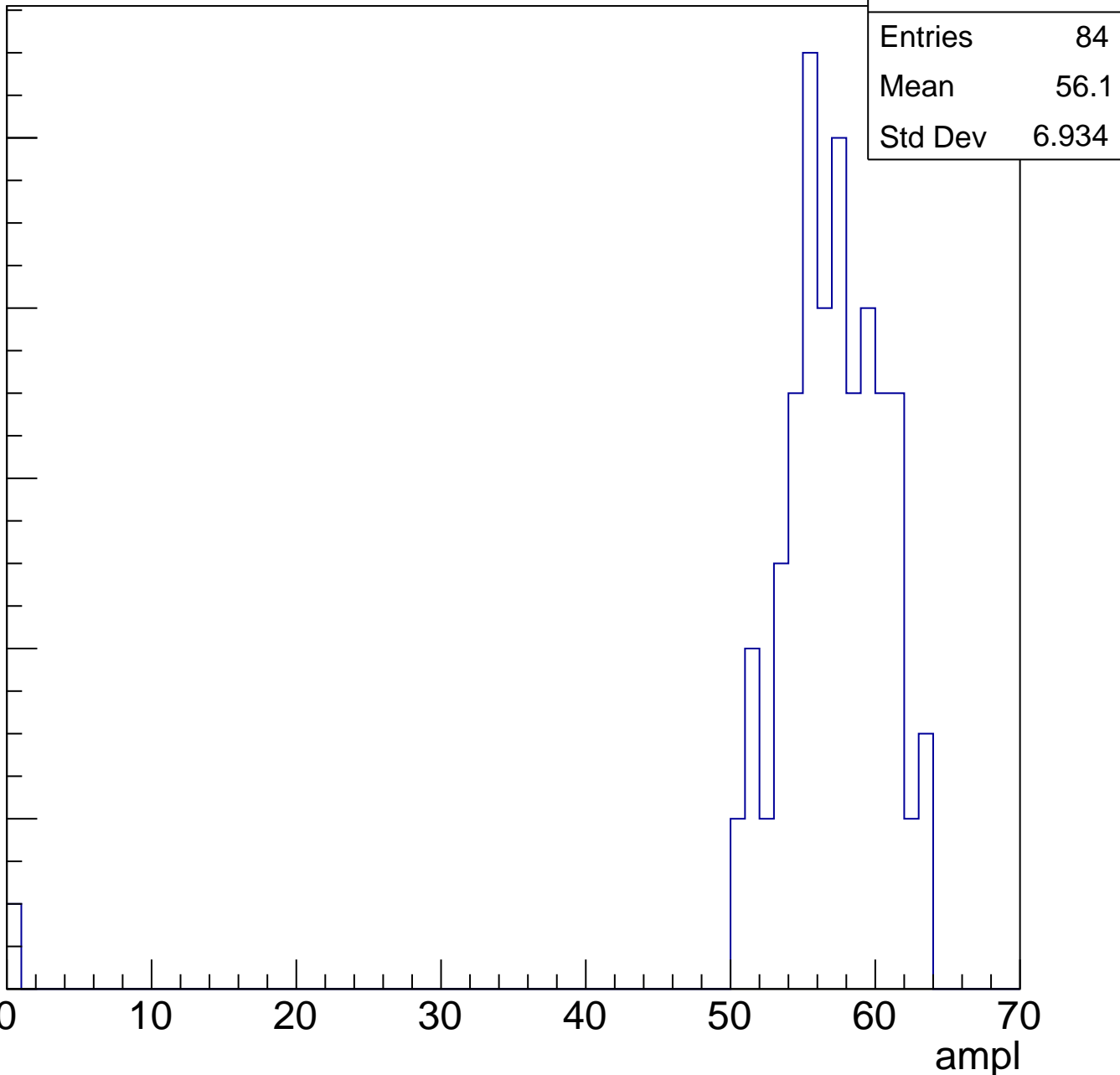
40

50

60

ampl

Entries	84
Mean	56.1
Std Dev	6.934



B0L101S, U24-ch4, adc5

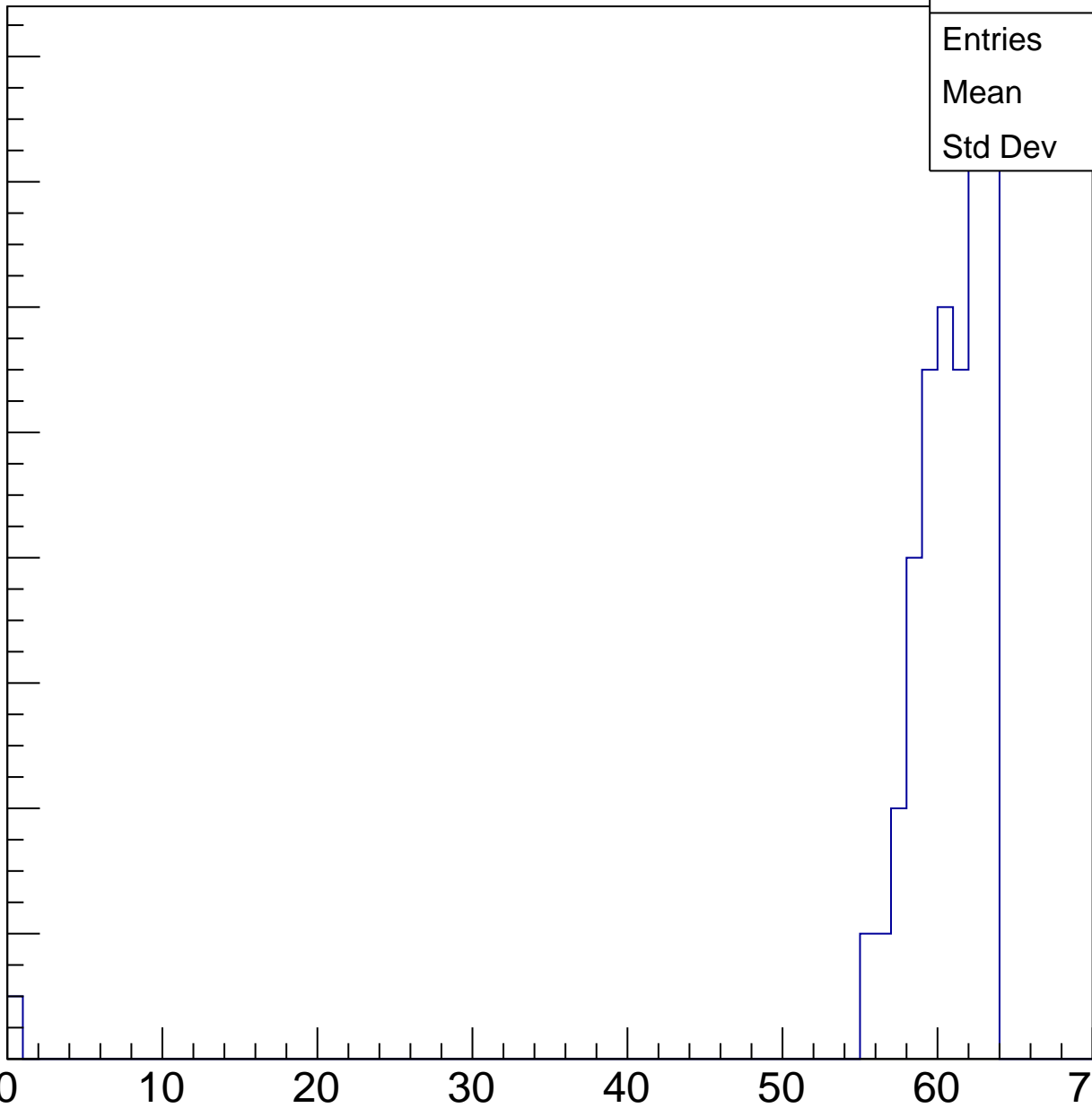
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.69
Std Dev	6.918

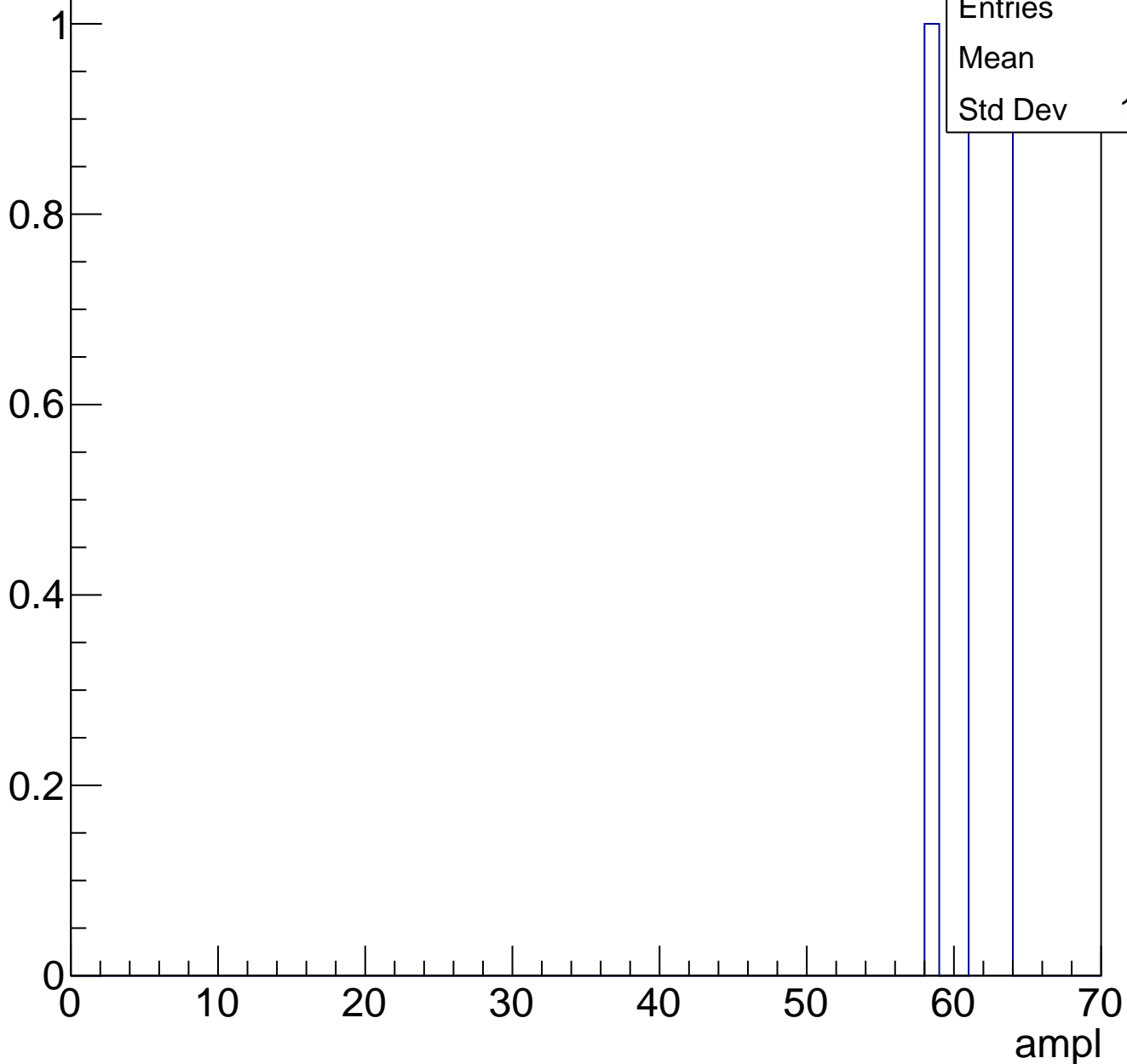
ampl



B0L101S, U24-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

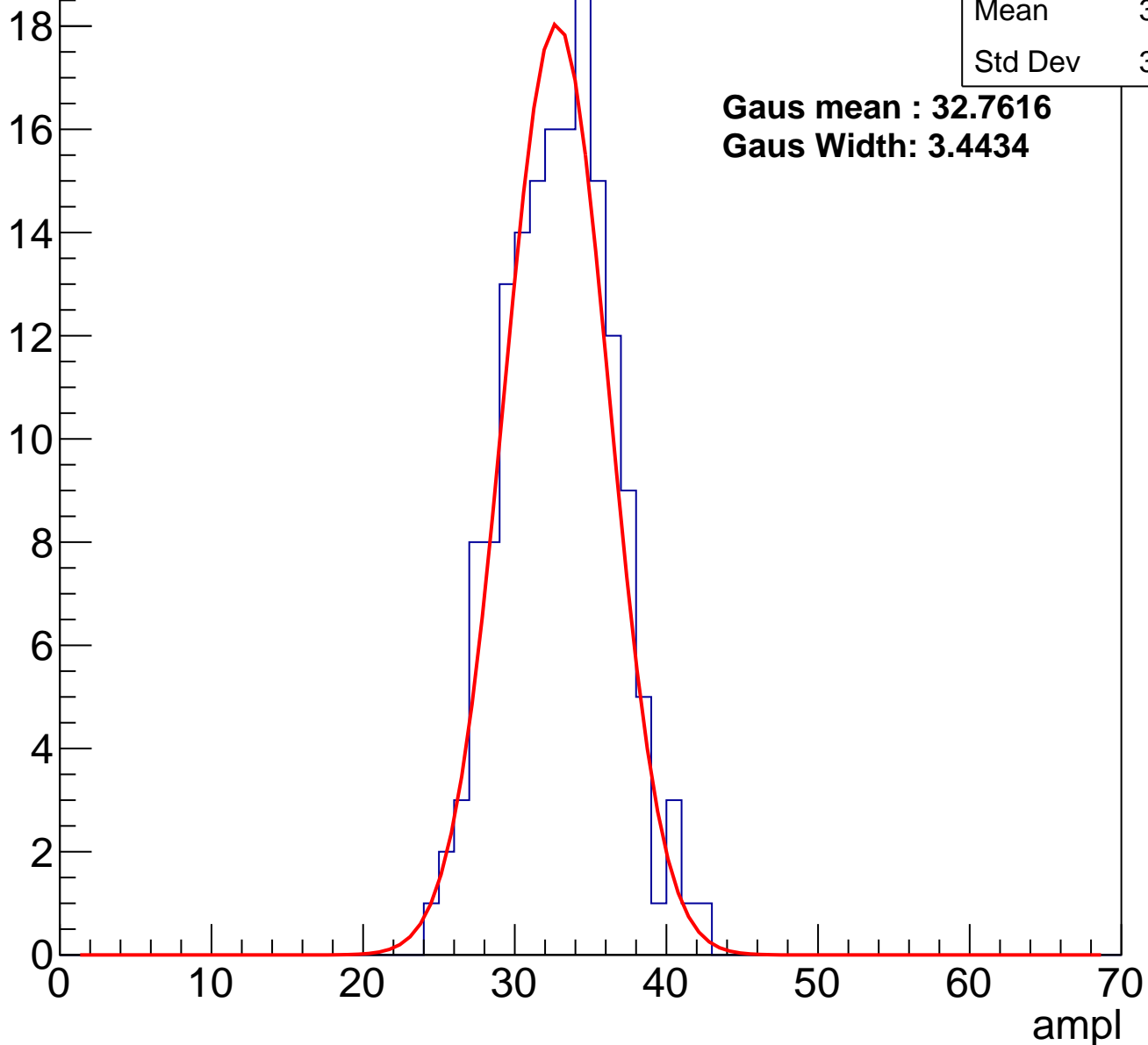


Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch5, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch5, adc1

calib_packv5_042523_0143.root, FC#1, port C1

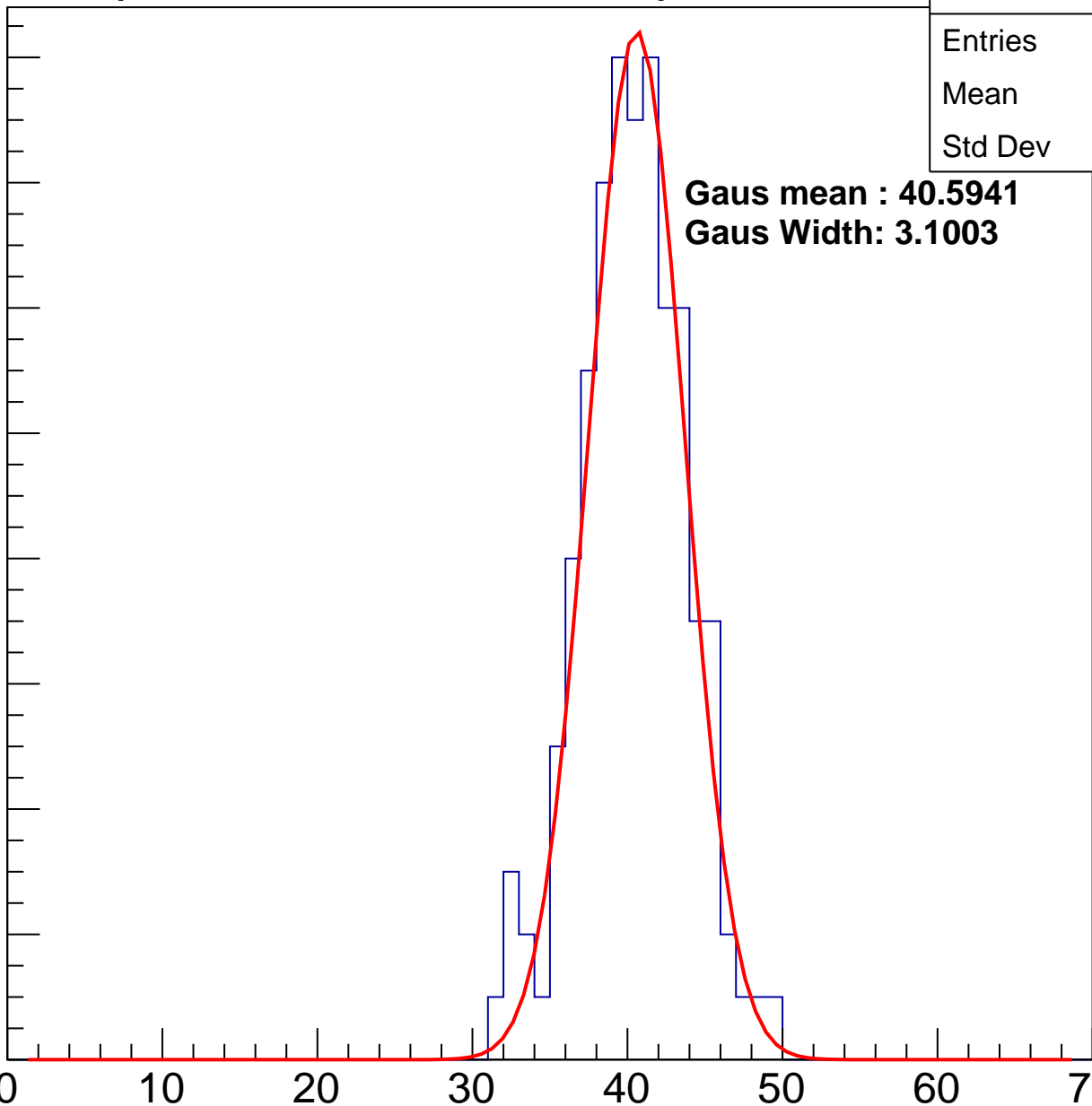
Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	39.91
Std Dev	3.394

Gaus mean : 40.5941
Gaus Width: 3.1003

0 10 20 30 40 50 60 70
ampl



B0L101S, U24-ch5, adc2

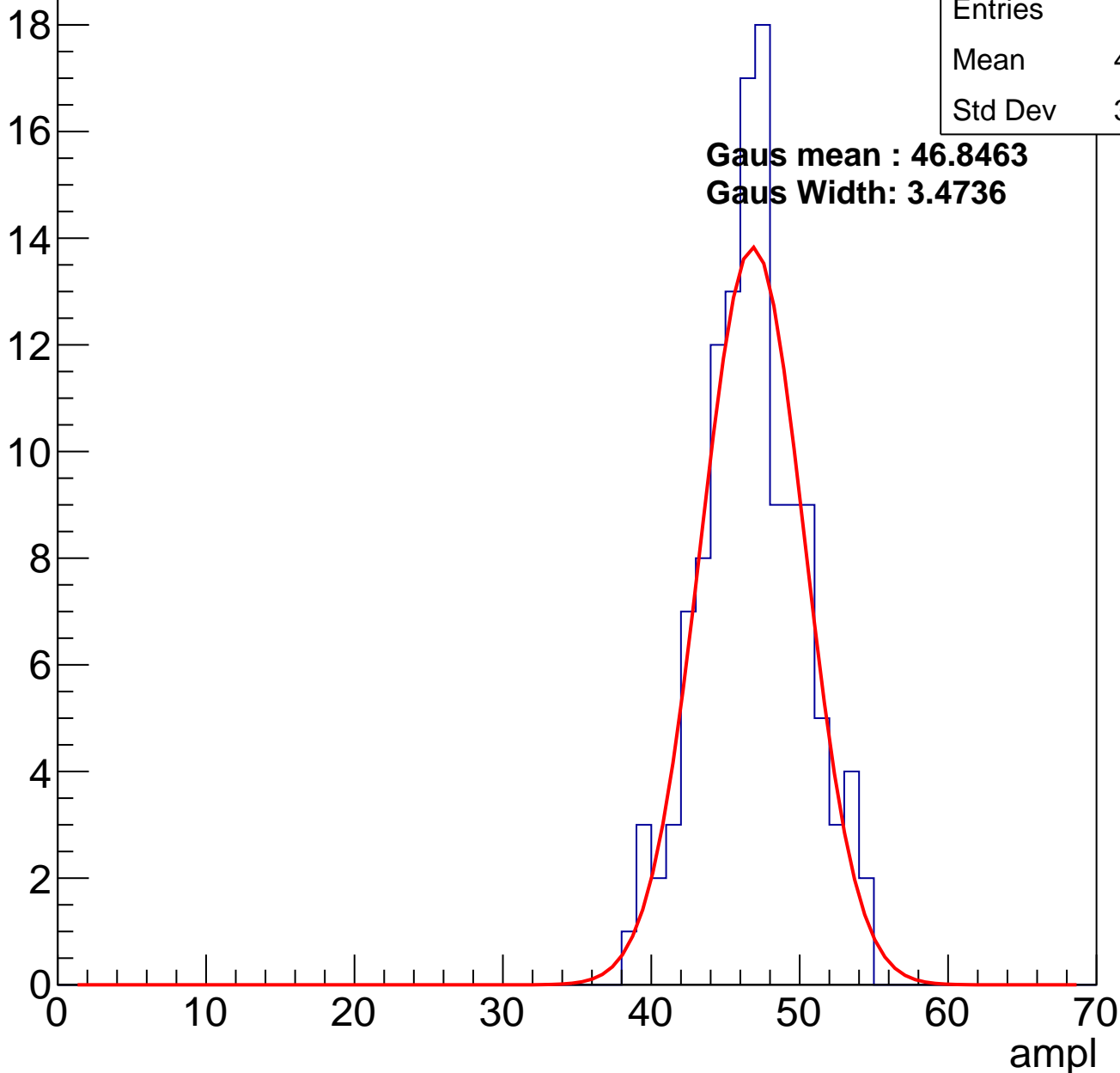
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	46.33
Std Dev	3.377

Gaus mean : 46.8463

Gaus Width: 3.4736

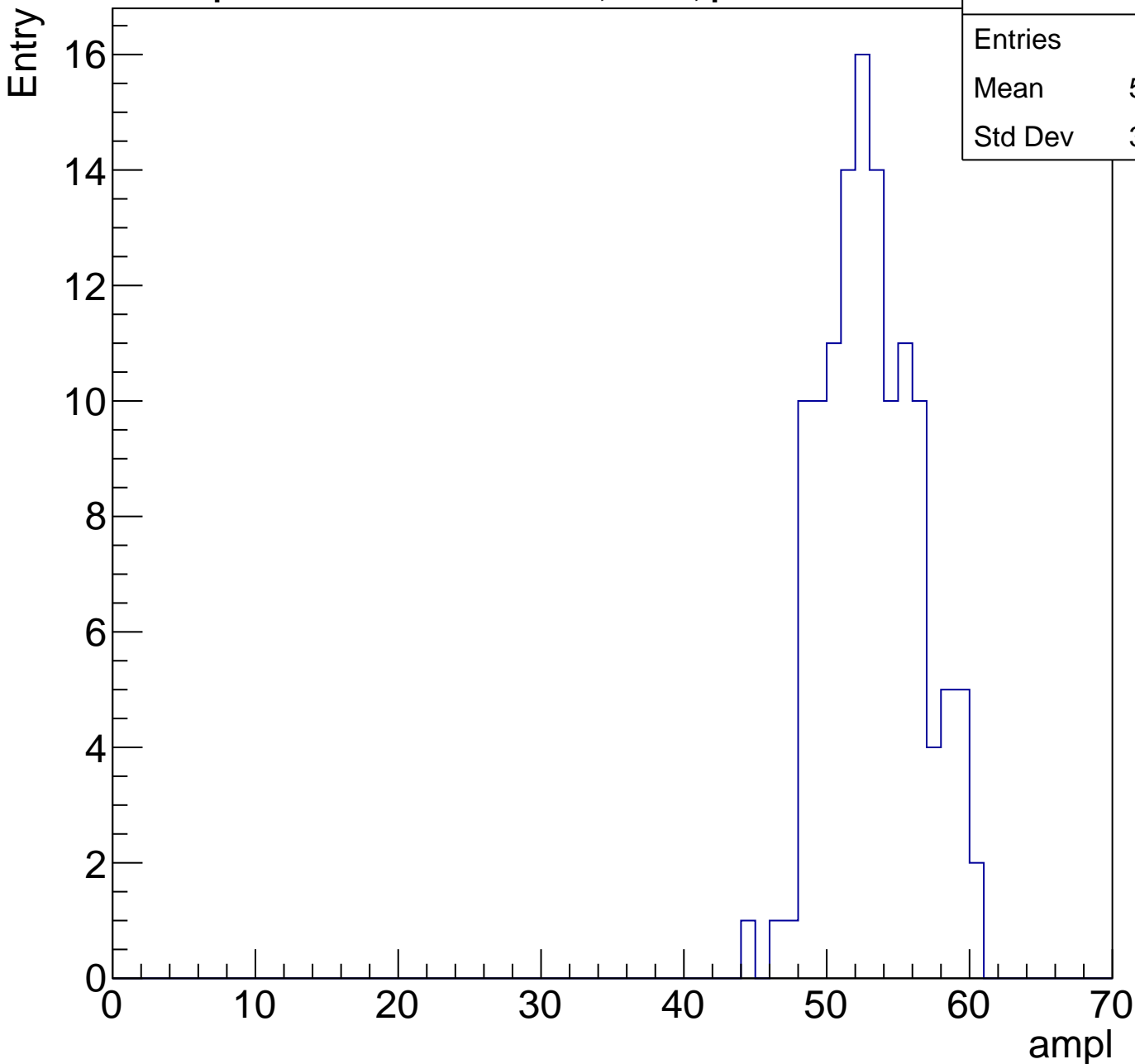
Entry



B0L101S, U24-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	52.66
Std Dev	3.279



B0L101S, U24-ch5, adc4

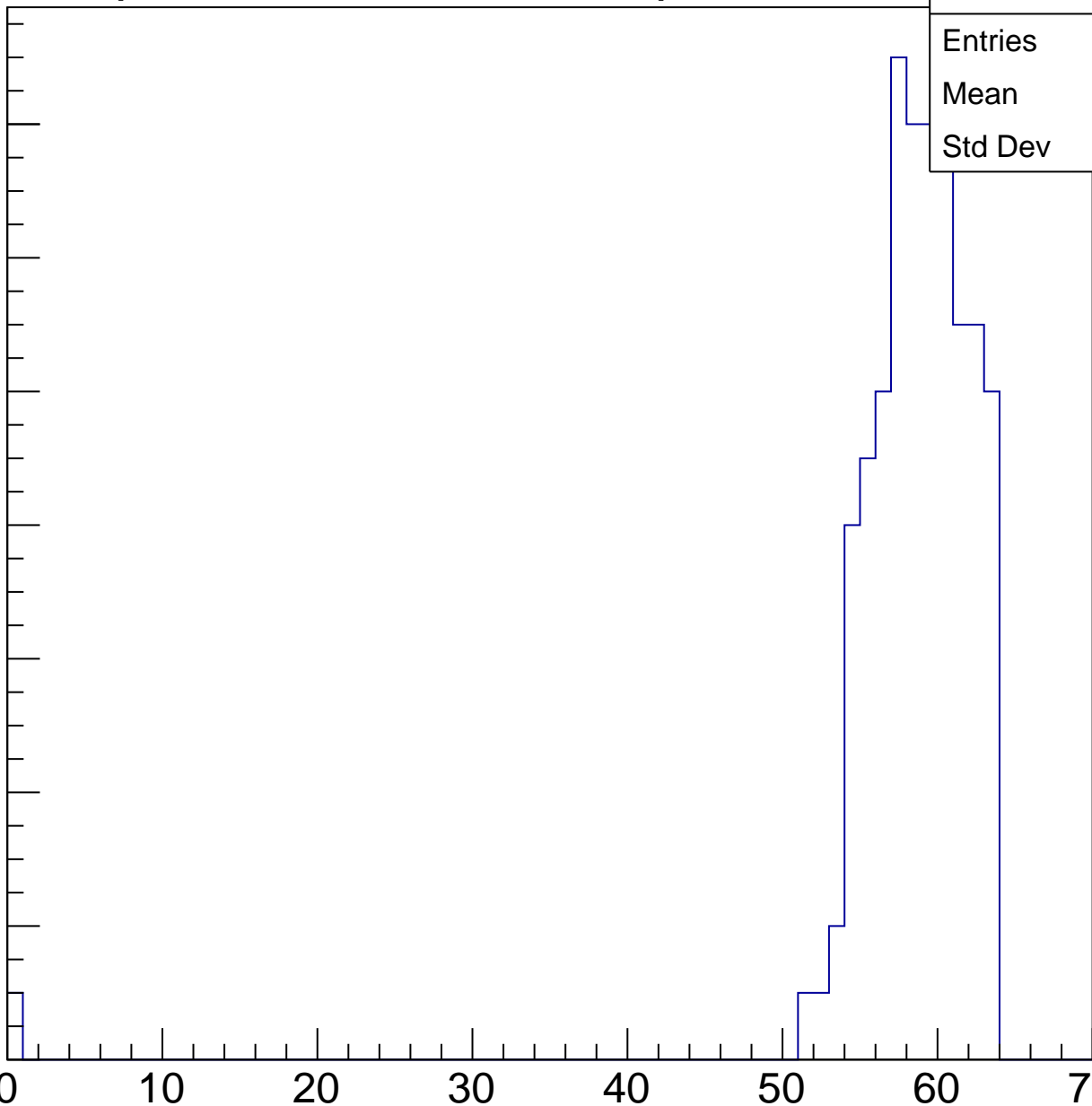
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	57.97
Std Dev	5.979

ampl



B0L101S, U24-ch5, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	32
Mean	59.66
Std Dev	10.8

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

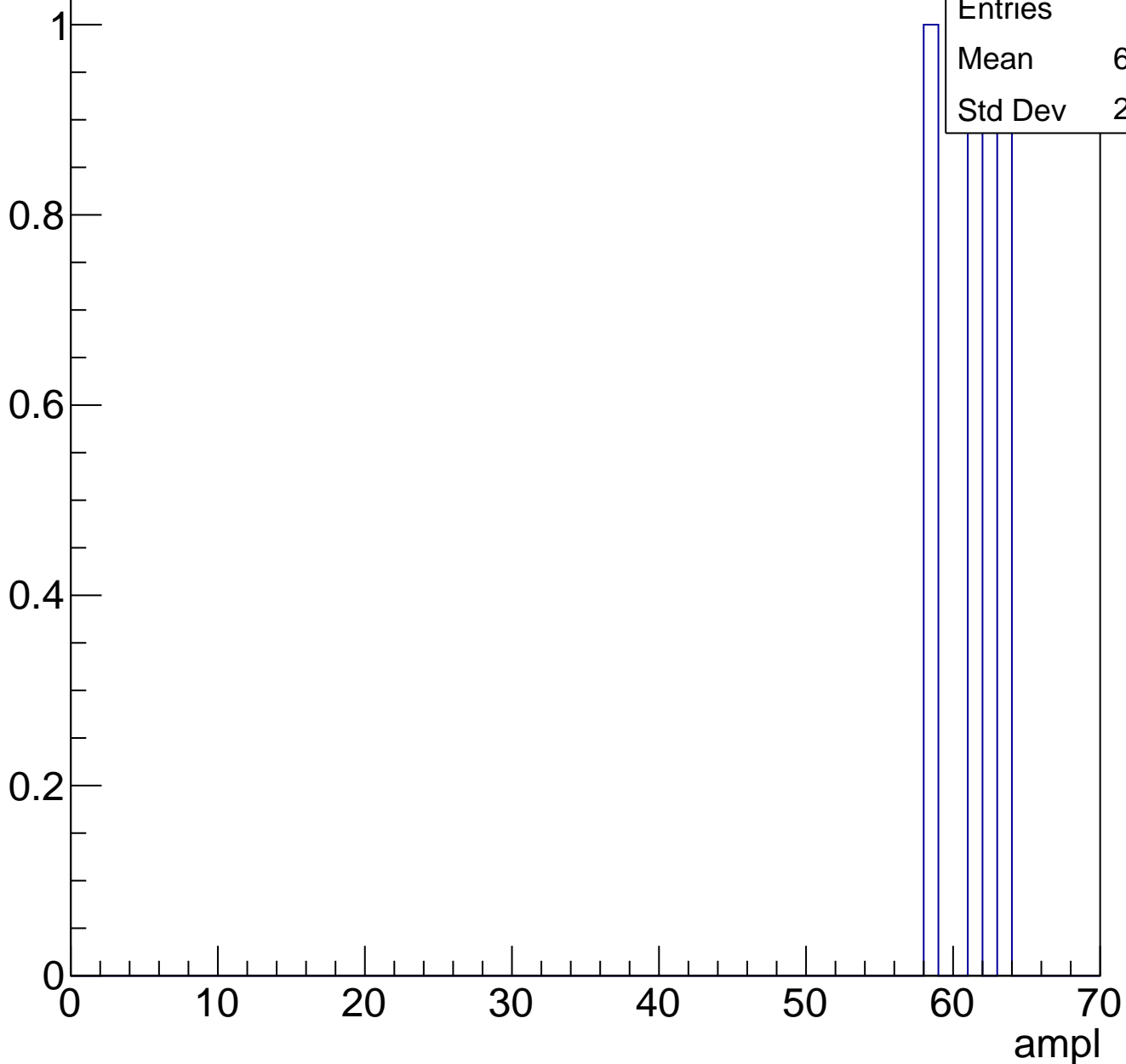
8

9

B0L101S, U24-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

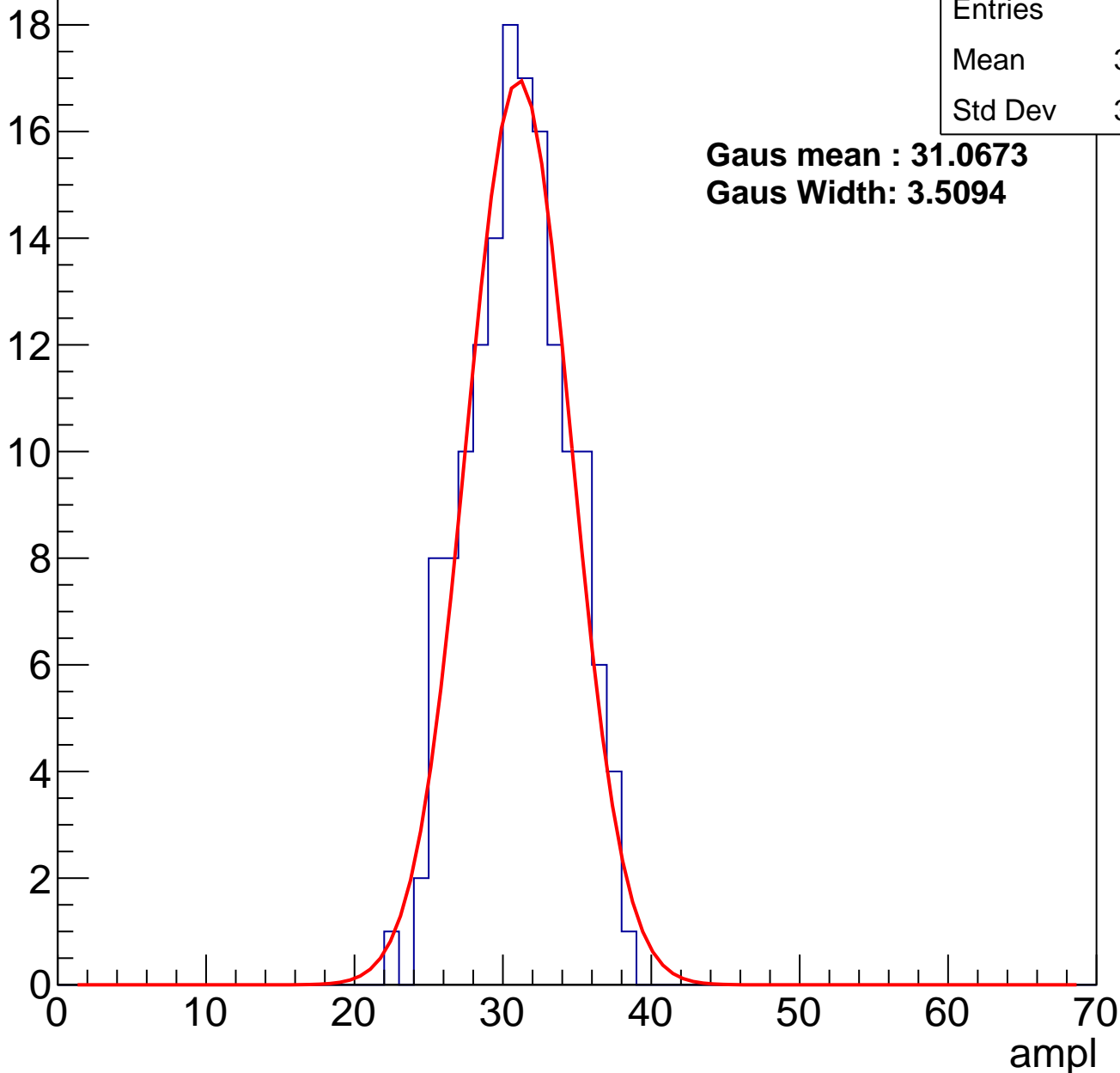


Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch6, adc0

calib_packv5_042523_0143.root, FC#1, port C1

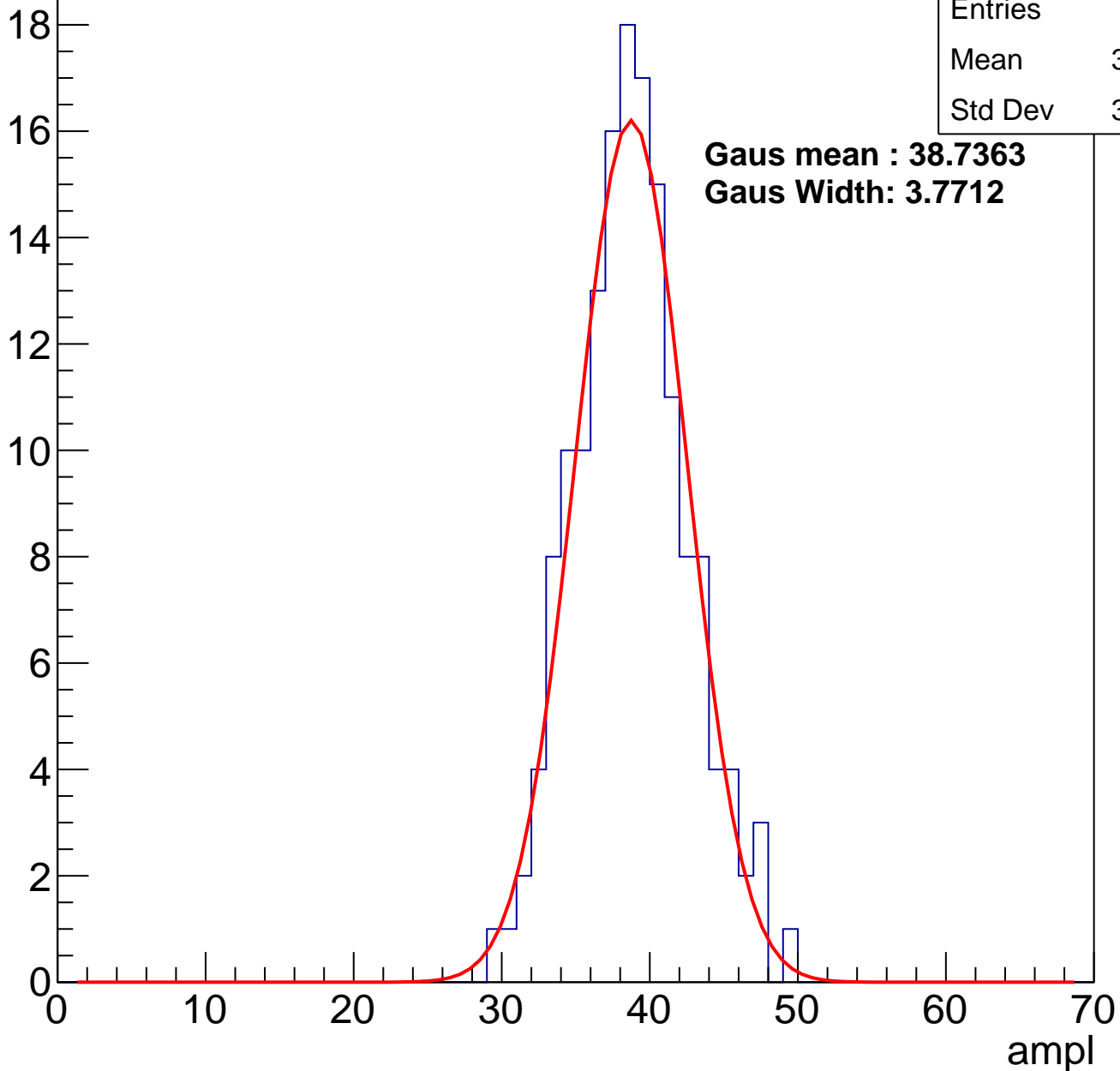
Entry



B0L101S, U24-ch6, adc1

calib_packv5_042523_0143.root, FC#1, port C1

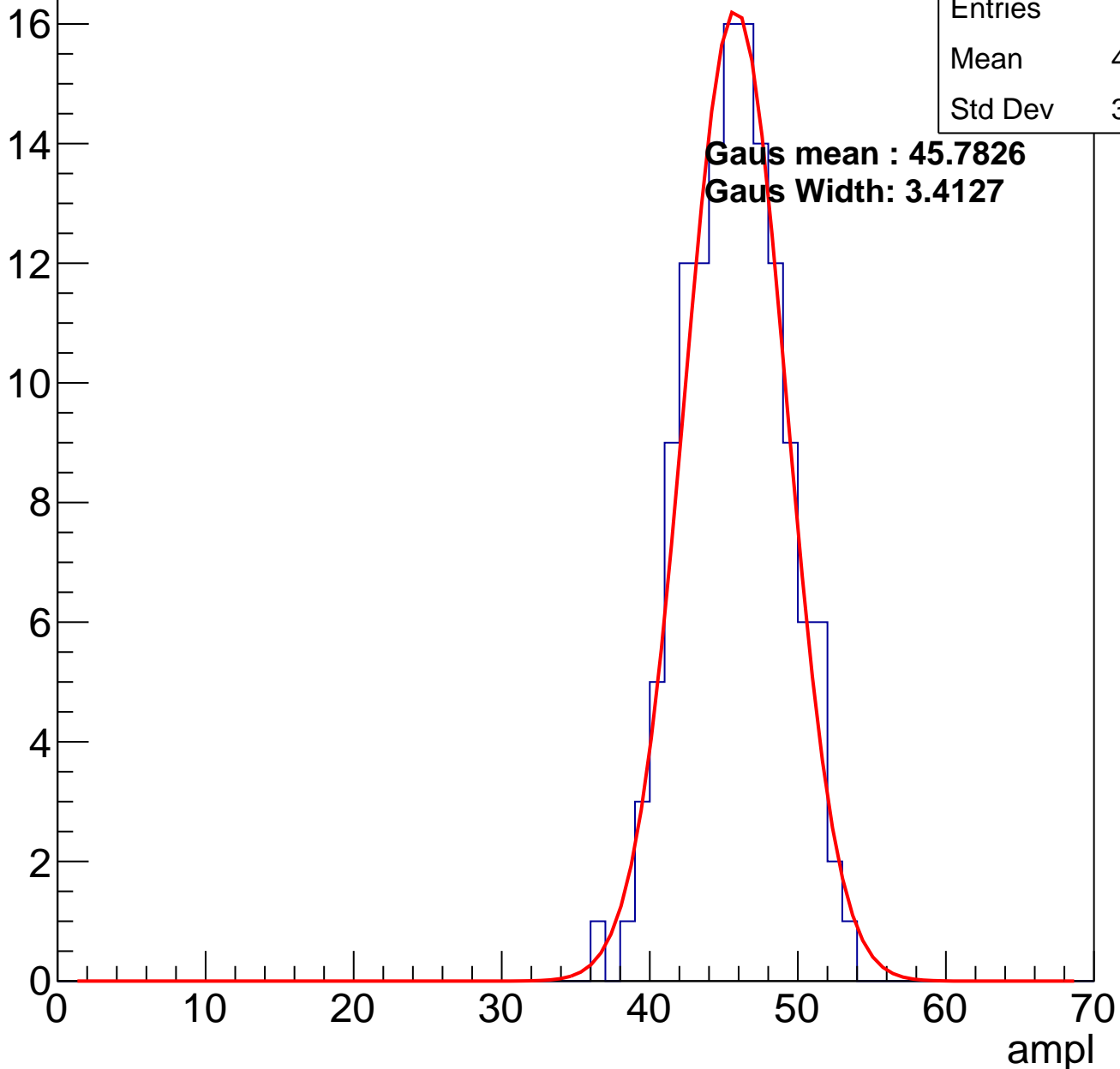
Entry



B0L101S, U24-ch6, adc2

calib_packv5_042523_0143.root, FC#1, port C1

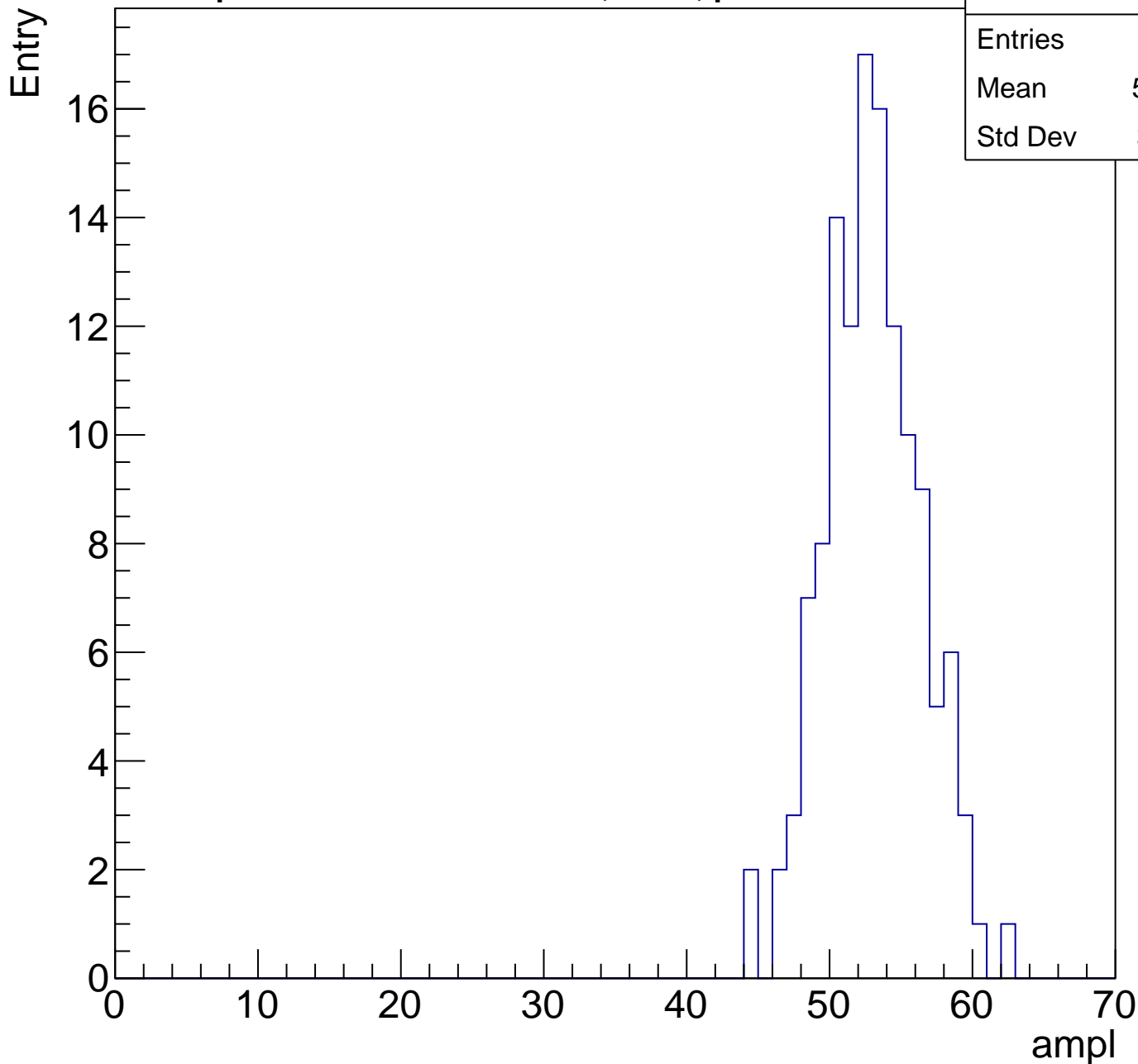
Entry



B0L101S, U24-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	52.55
Std Dev	3.351



B0L101S, U24-ch6, adc4

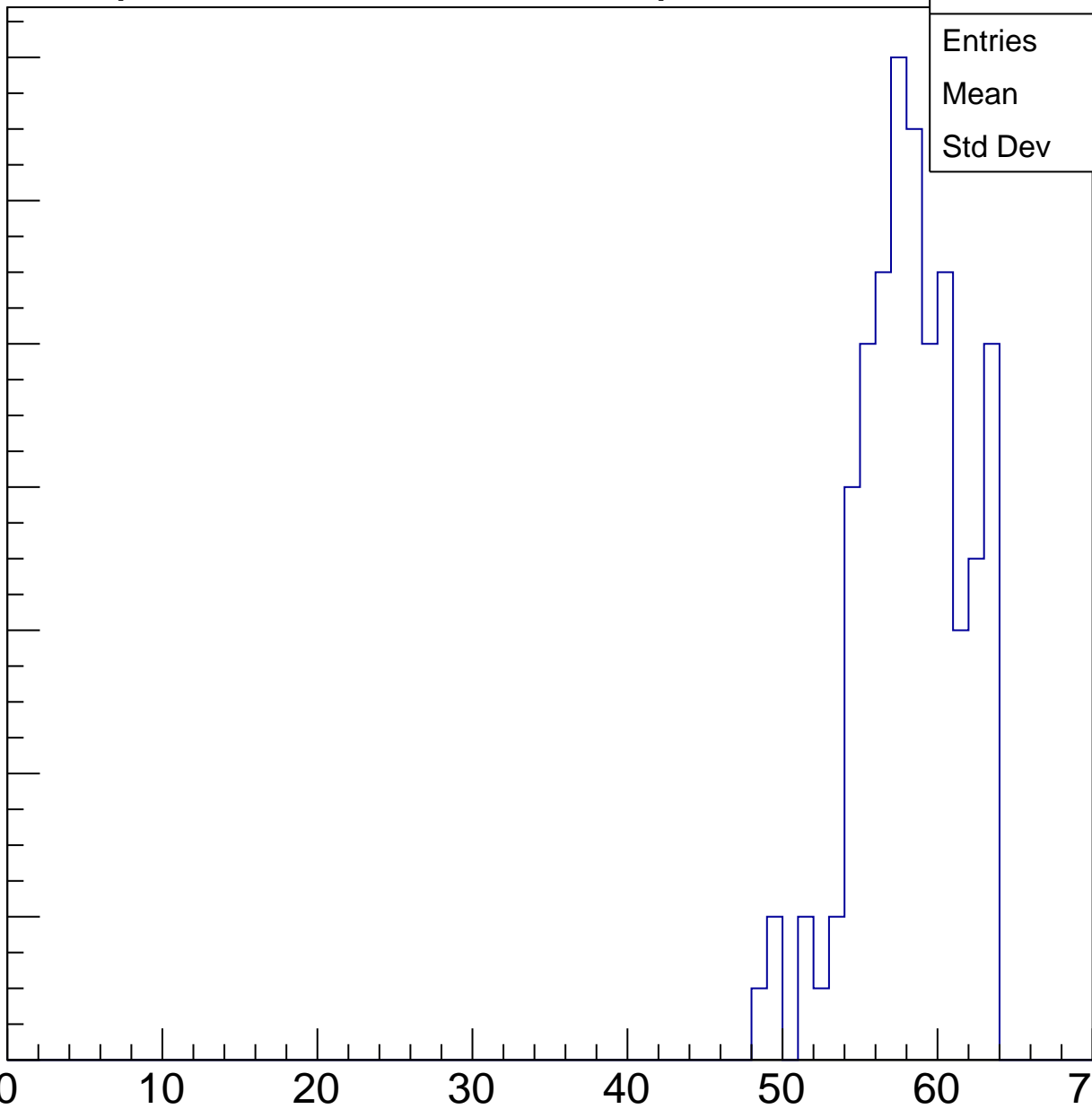
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	57.74
Std Dev	3.312

ampl



B0L101S, U24-ch6, adc5

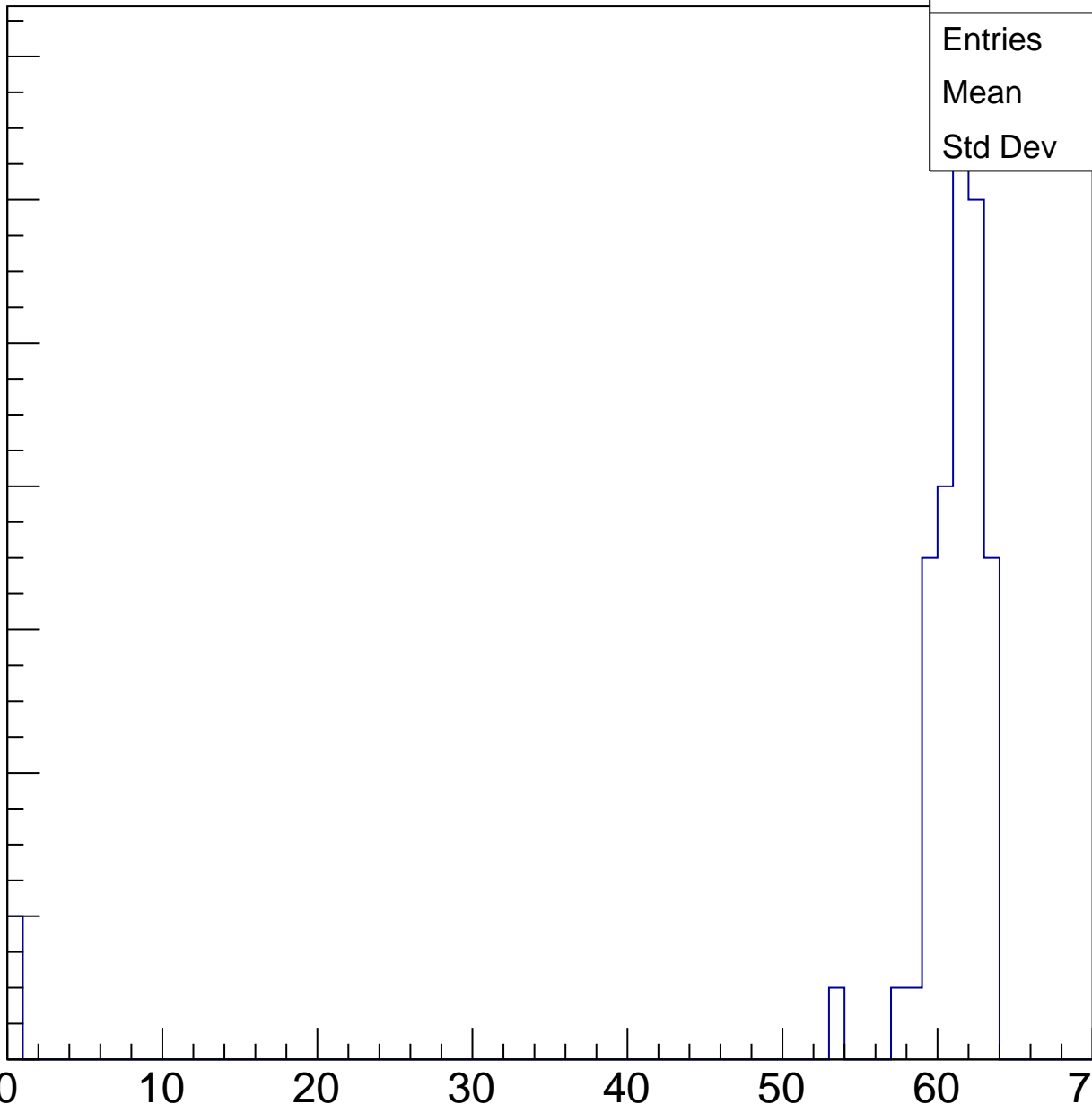
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	53
Mean	58.49
Std Dev	11.71

ampl



B0L101S, U24-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

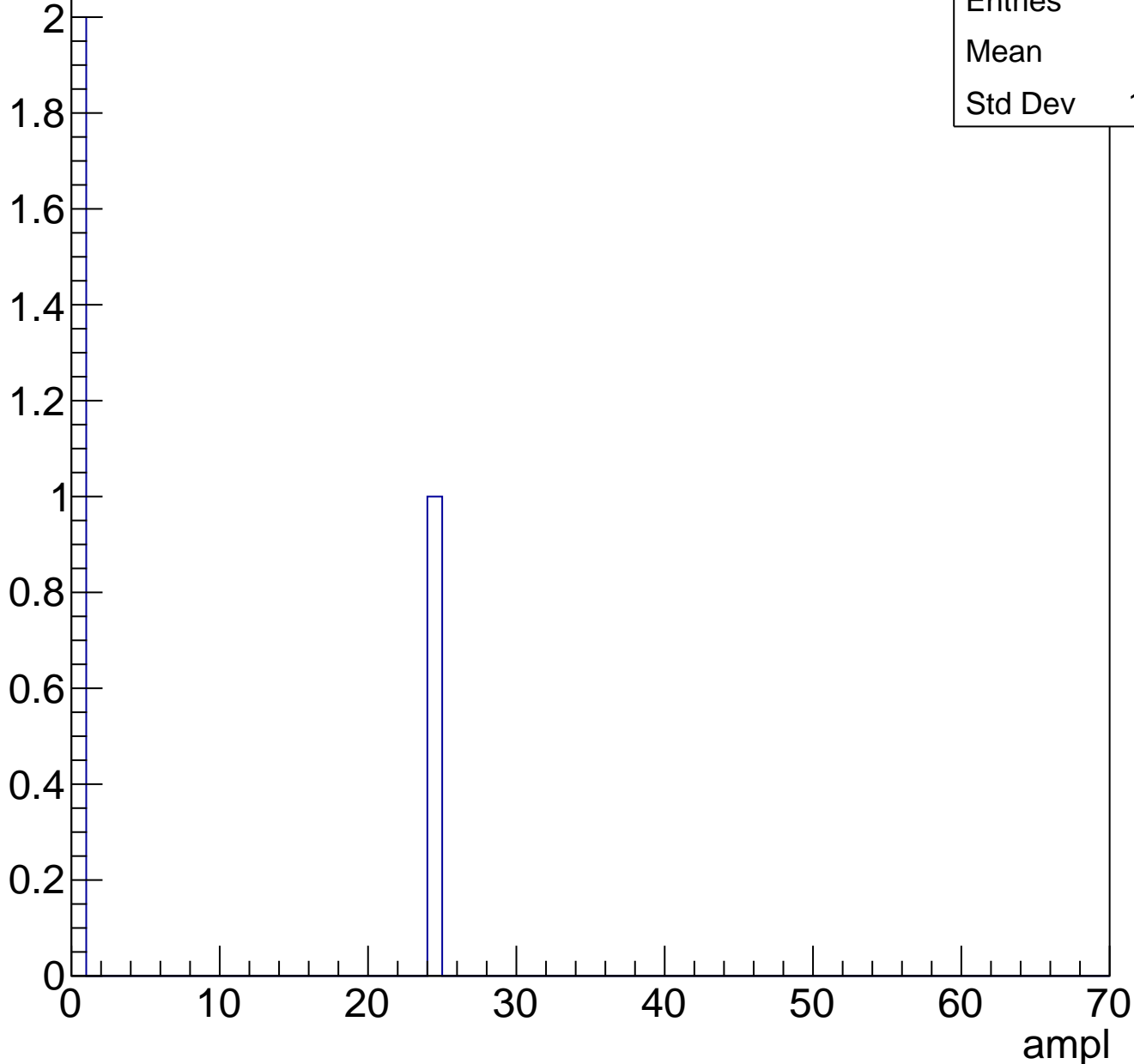
Entry



B0L101S, U24-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch7, adc0

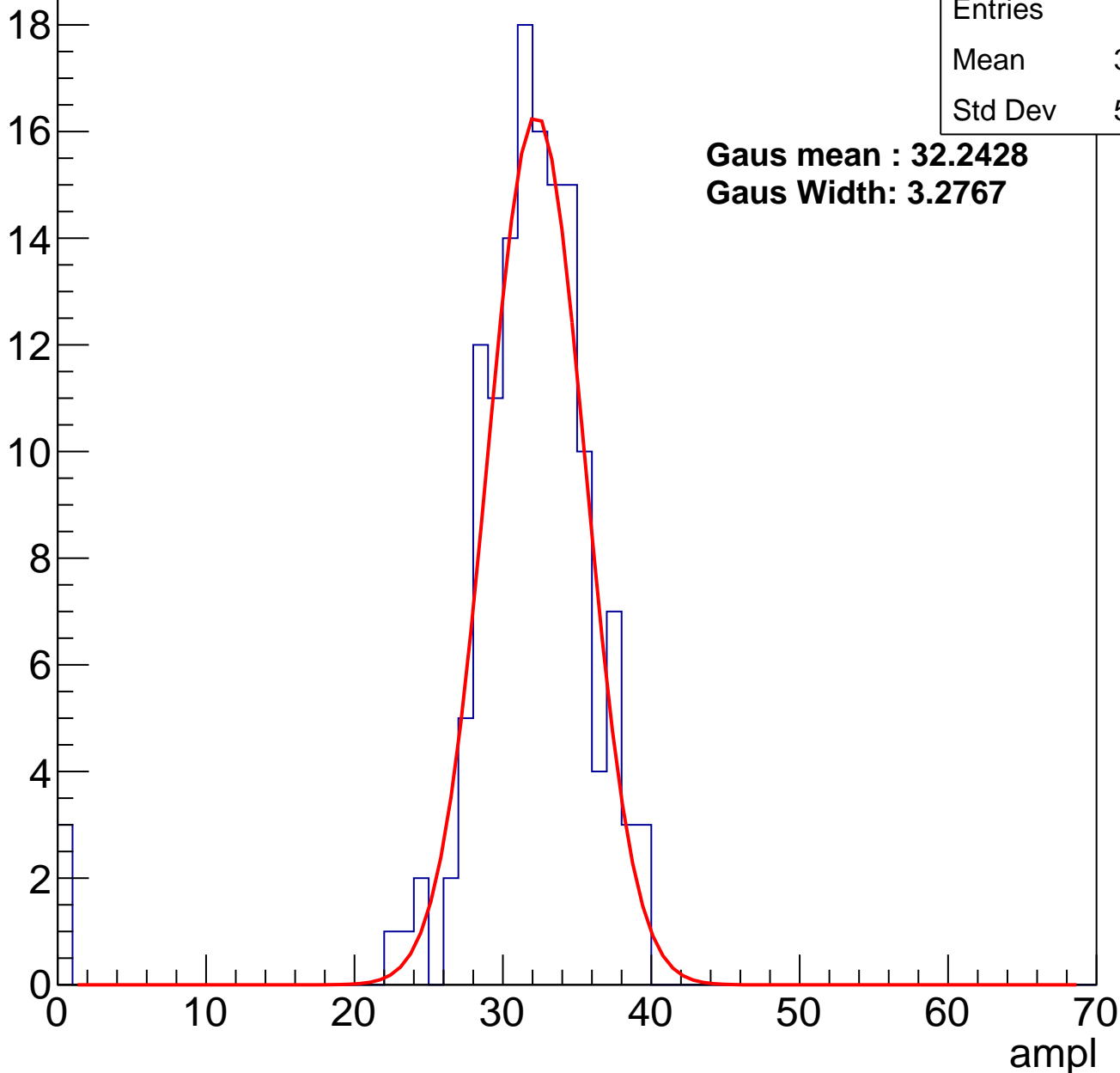
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	31.08
Std Dev	5.619

Gaus mean : 32.2428

Gaus Width: 3.2767

Entry



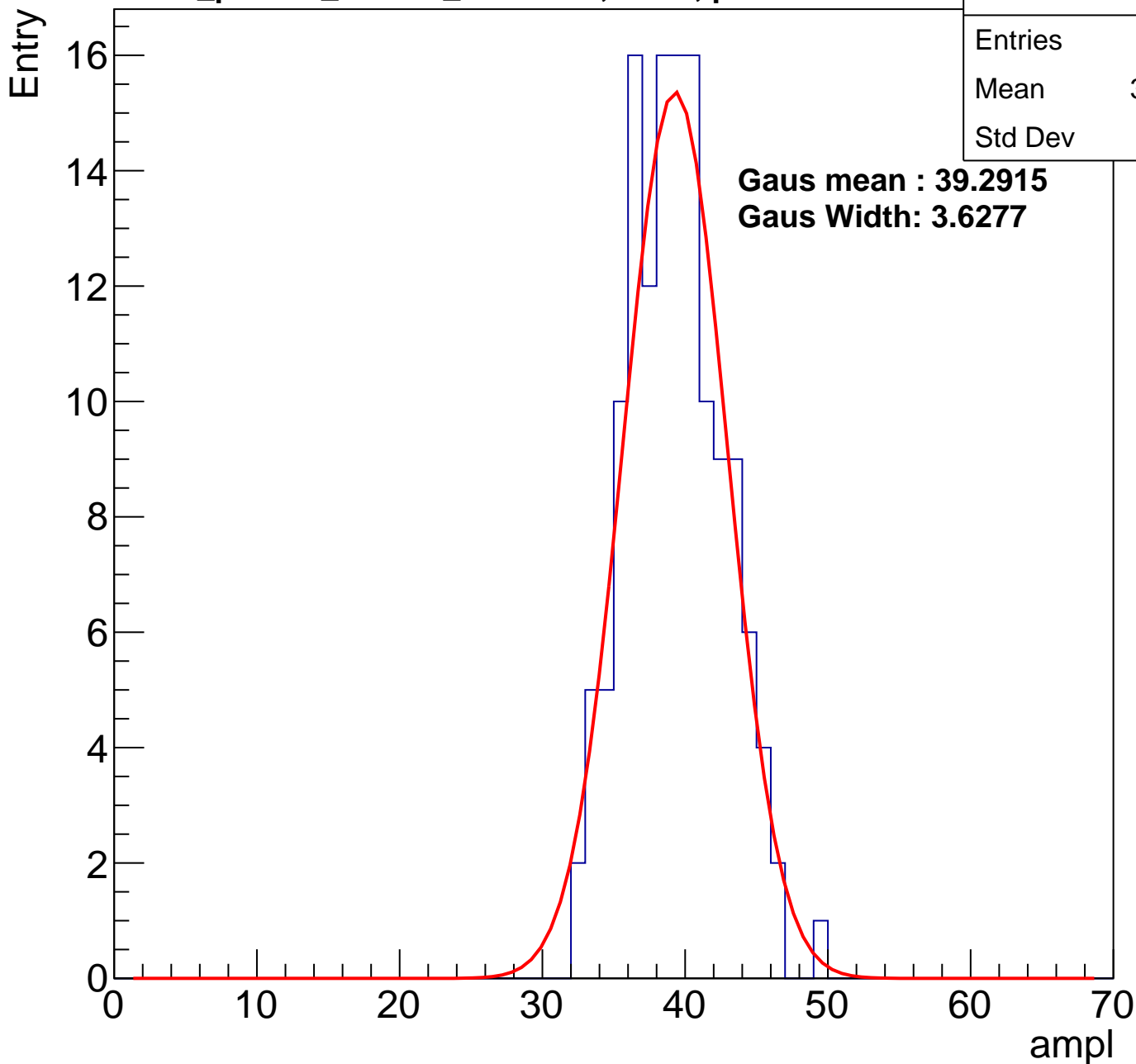
B0L101S, U24-ch7, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	38.86
Std Dev	3.33

Gaus mean : 39.2915

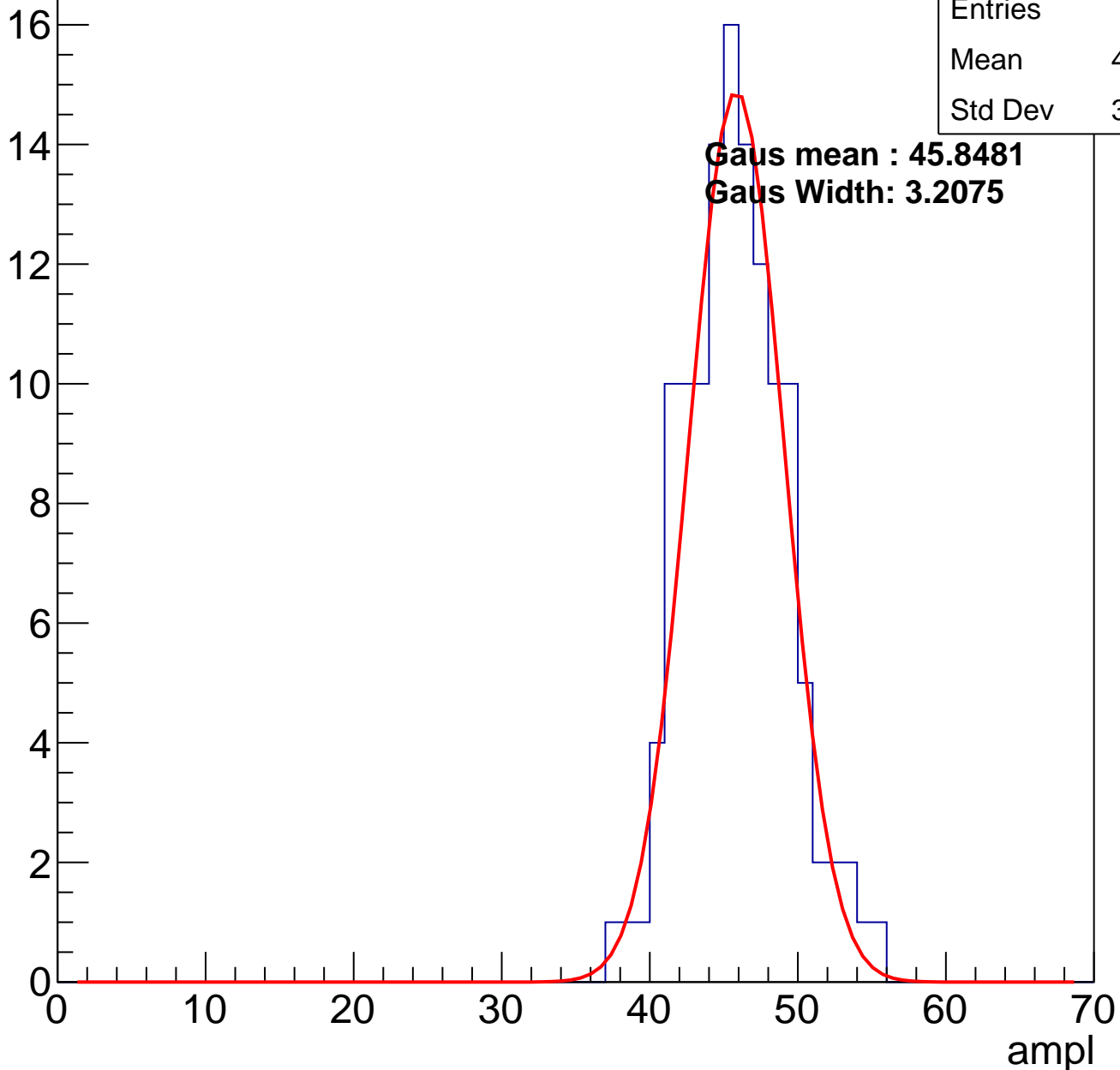
Gaus Width: 3.6277



B0L101S, U24-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

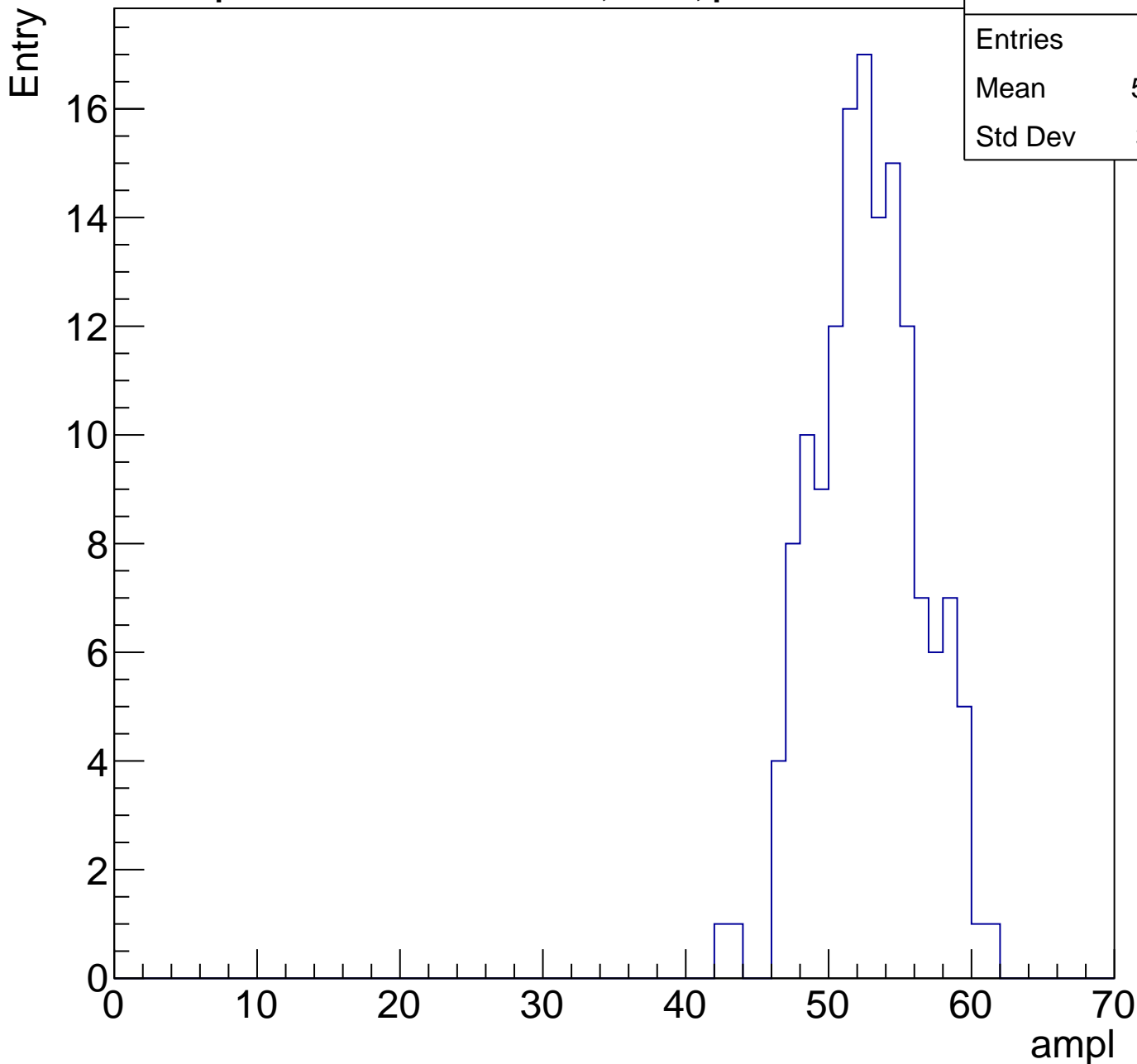


Entries	126
Mean	45.39
Std Dev	3.392

B0L101S, U24-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	52.29
Std Dev	3.611



B0L101S, U24-ch7, adc4

calib_packv5_042523_0143.root, FC#1, port C1

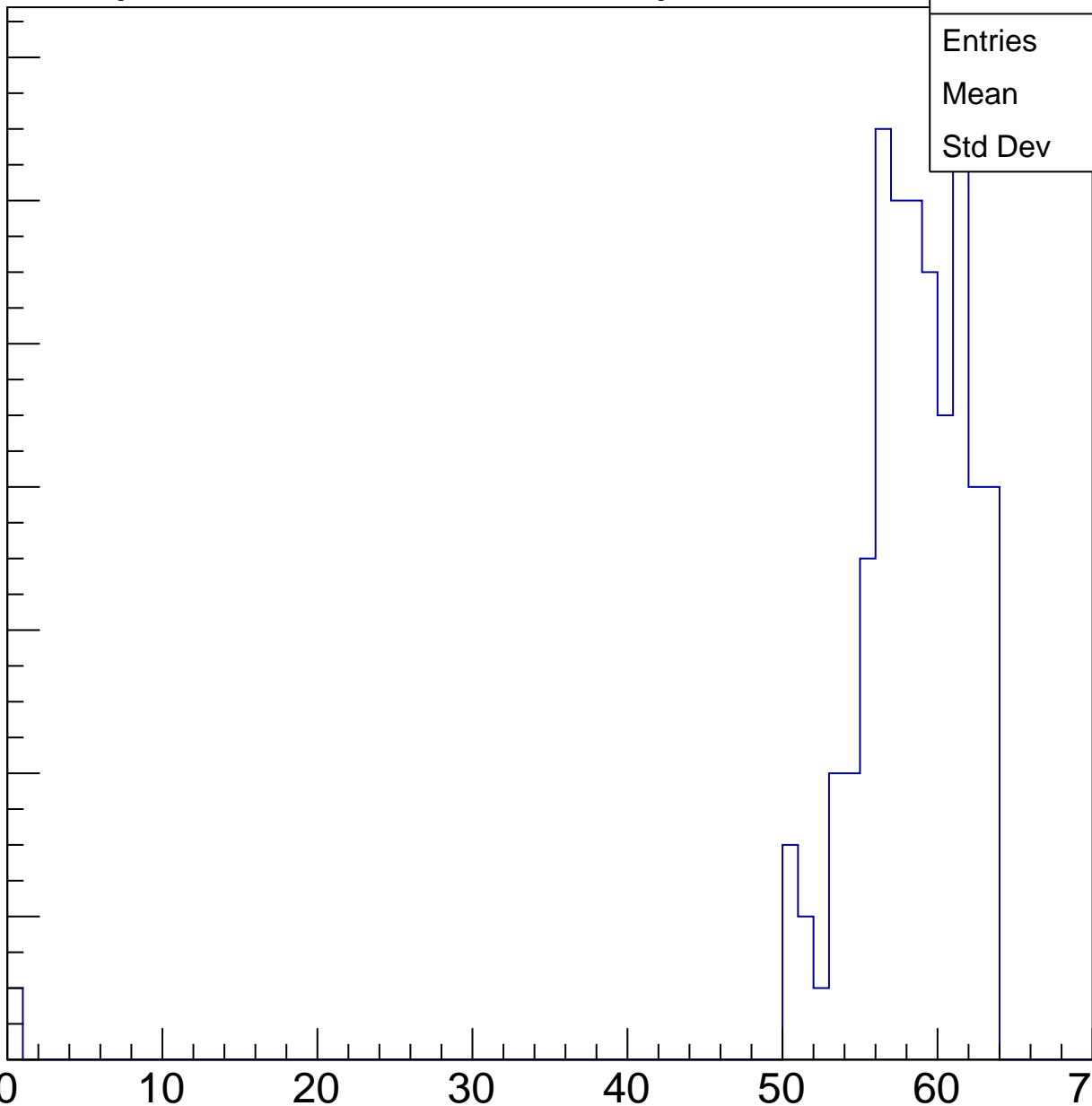
Entries	109
Mean	57.5
Std Dev	6.393

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch7, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

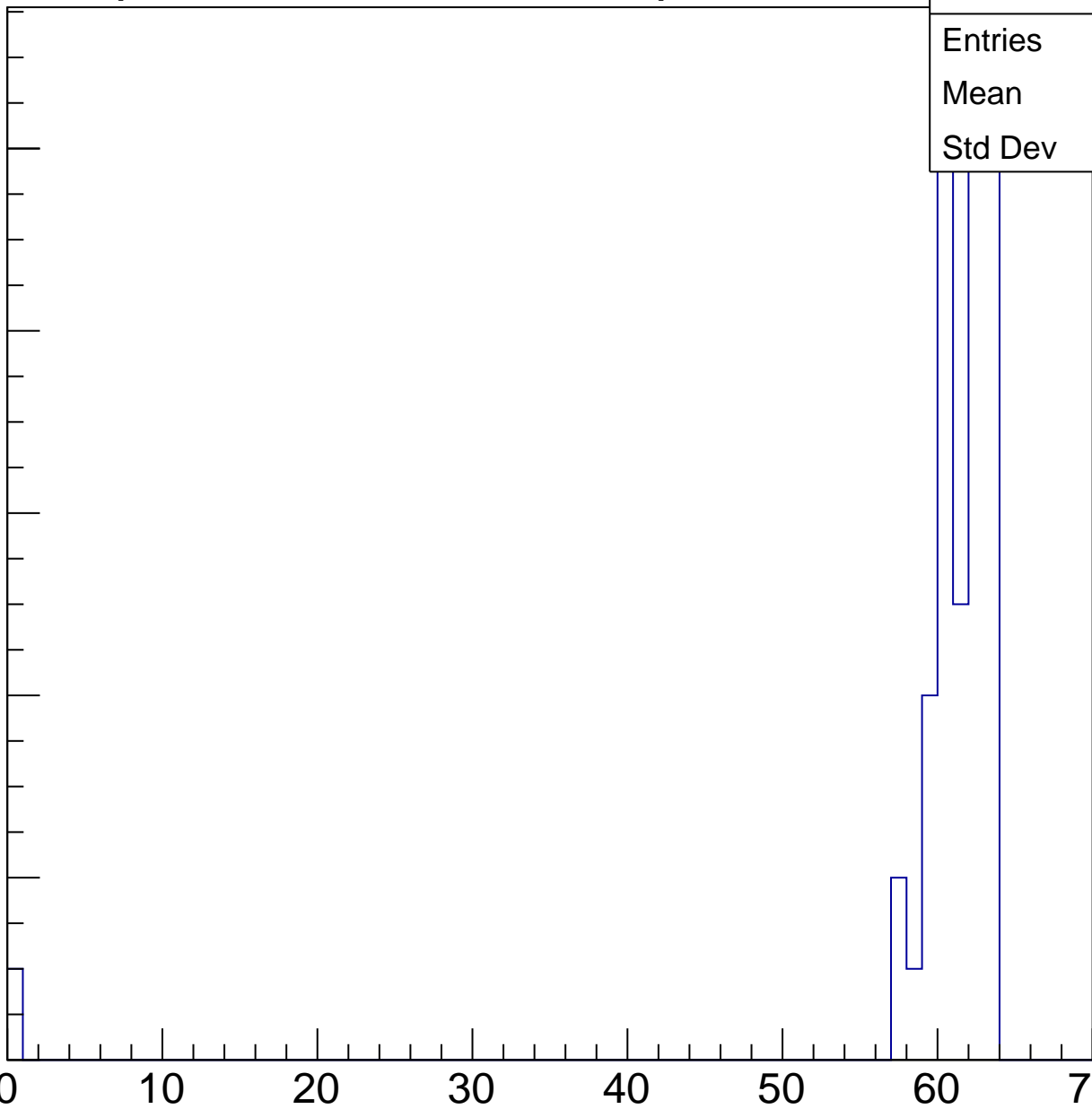
0

Entries	45
---------	----

Mean	59.73
------	-------

Std Dev	9.154
---------	-------

ampl



B0L101S, U24-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch8, adc0

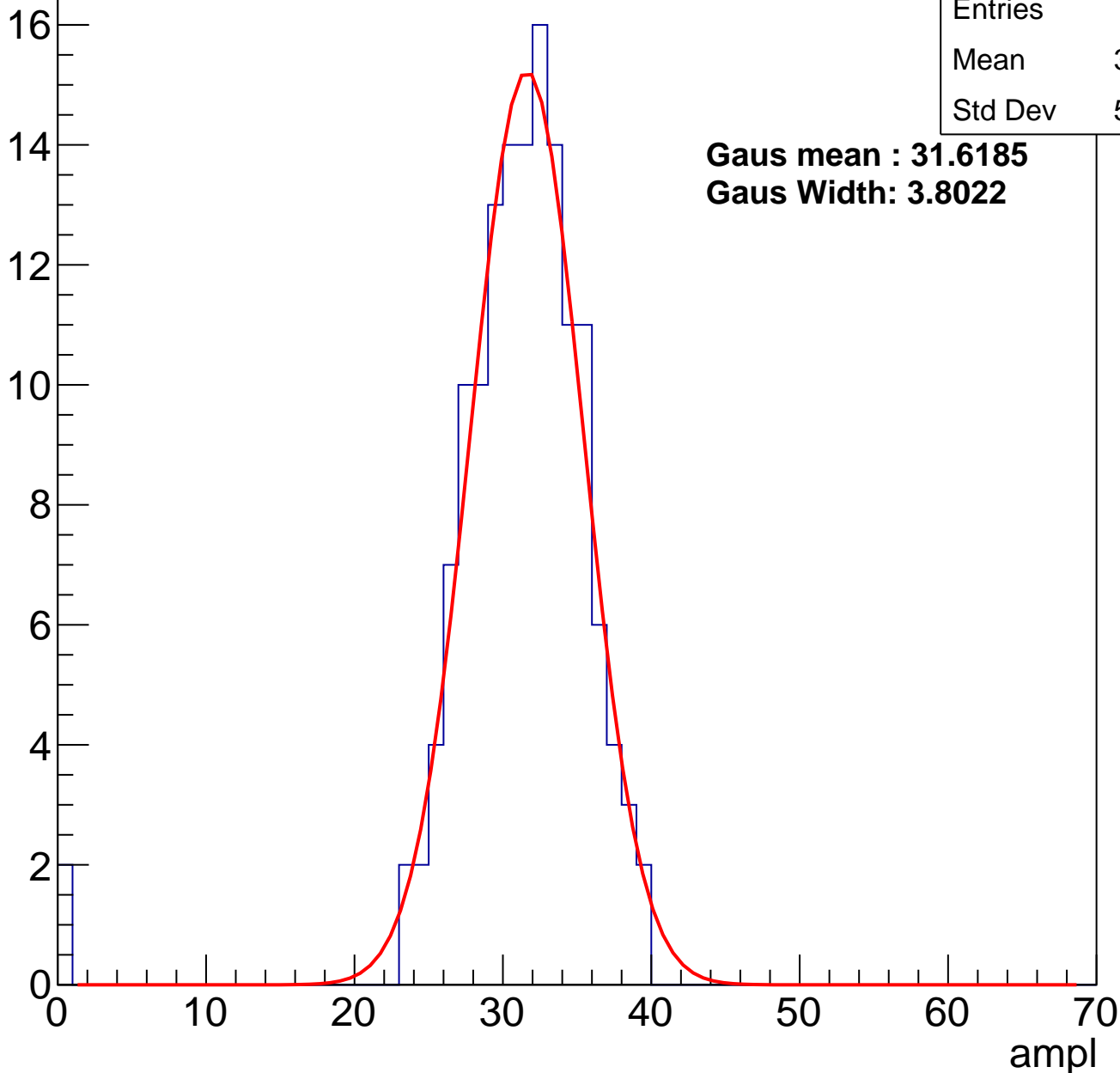
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	30.66
Std Dev	5.039

Gaus mean : 31.6185

Gaus Width: 3.8022

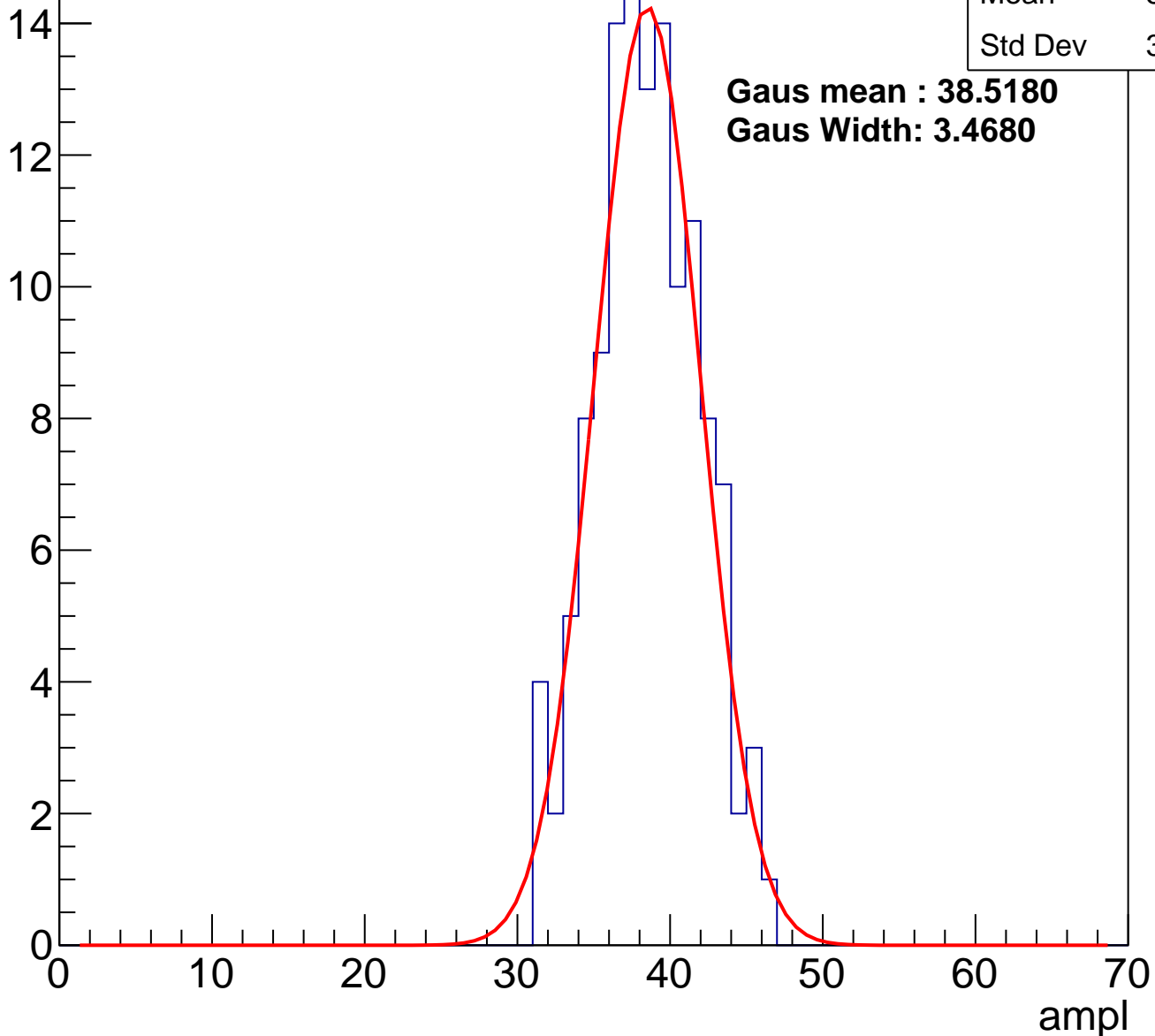
Entry



B0L101S, U24-ch8, adc1

calib_packv5_042523_0143.root, FC#1, port C1

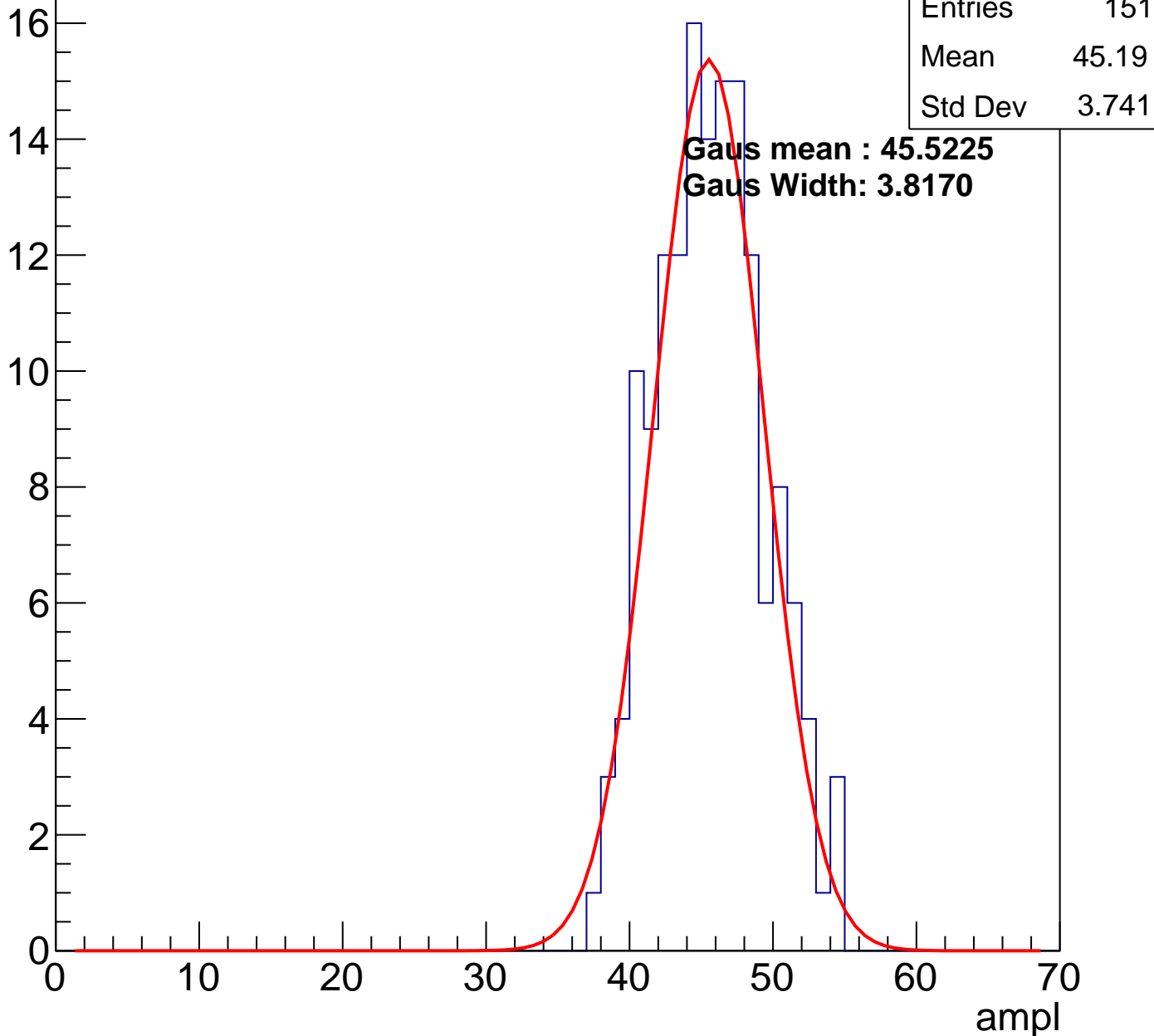
Entry



B0L101S, U24-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

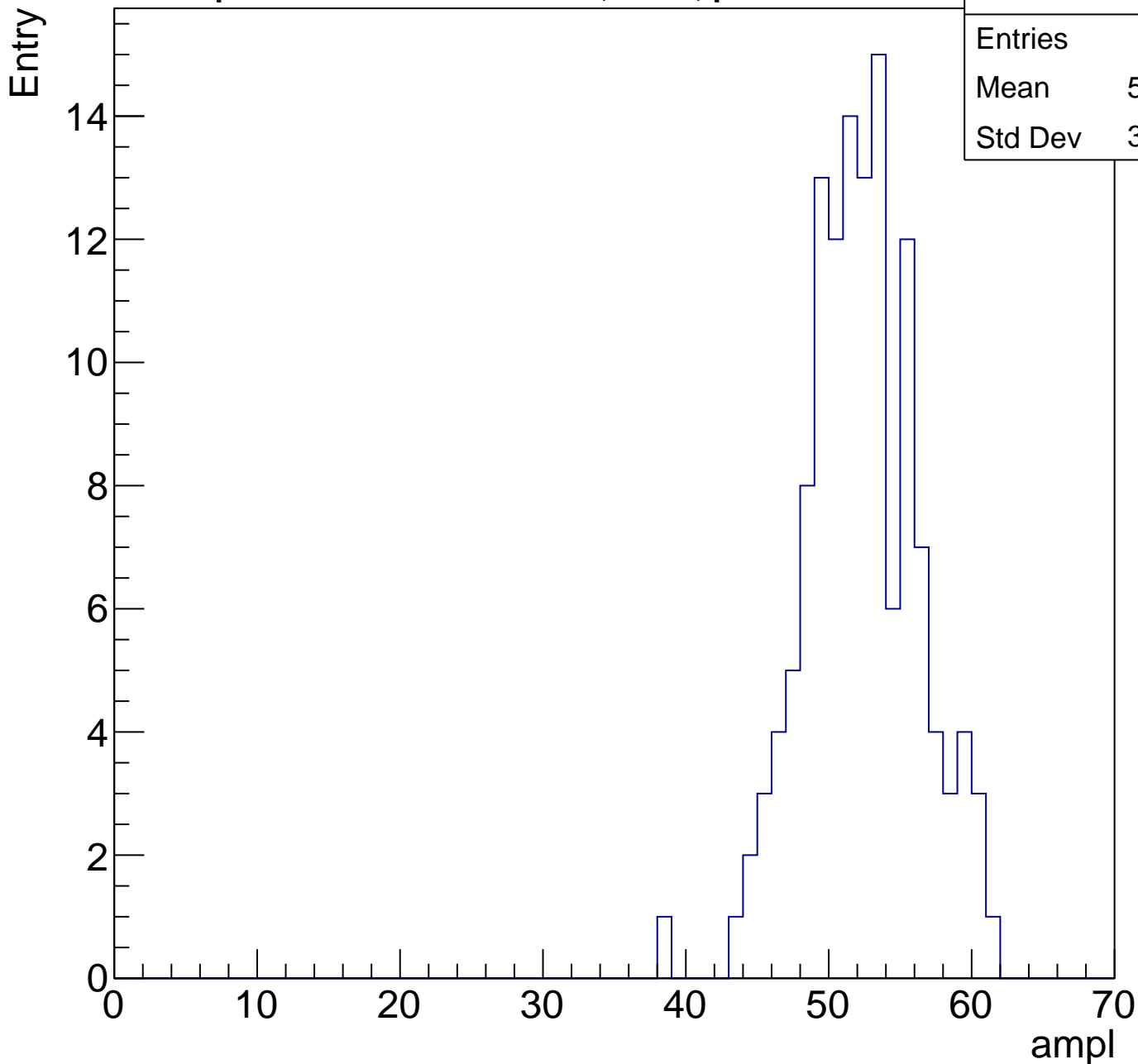
Entry



B0L101S, U24-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

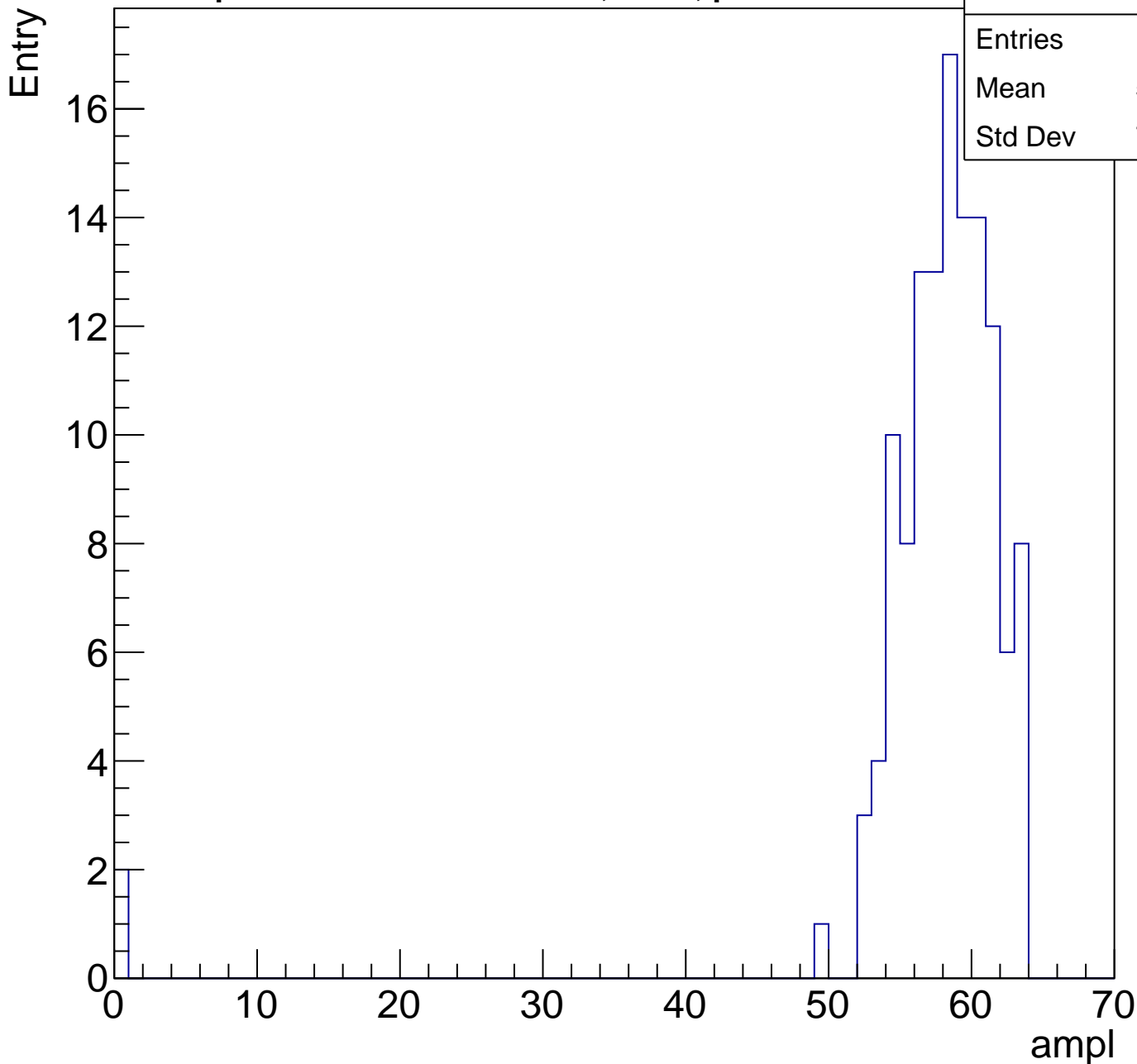
Entries	131
Mean	51.79
Std Dev	3.964



B0L101S, U24-ch8, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	57.01
Std Dev	7.831



B0L101S, U24-ch8, adc5

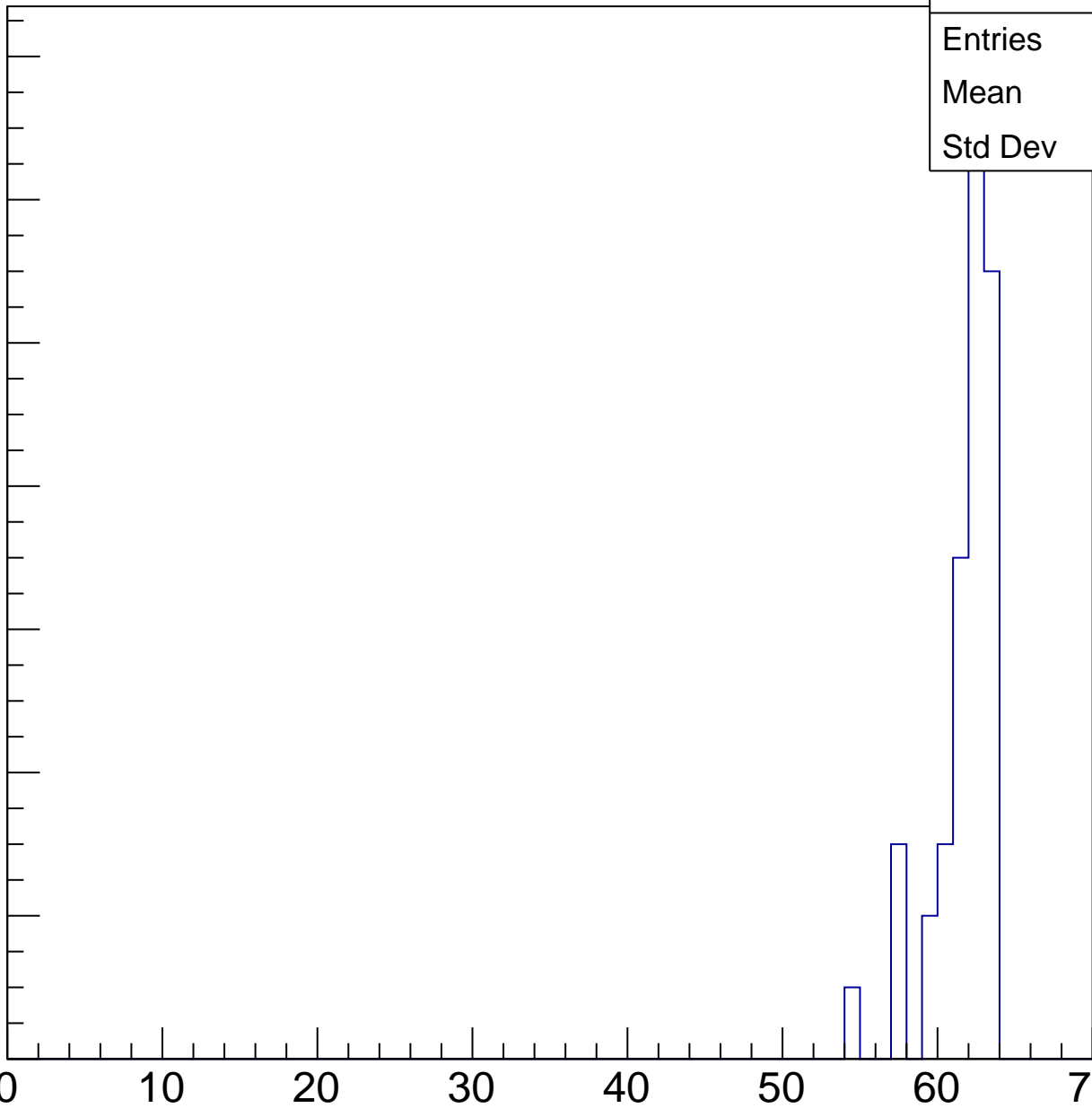
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	41
Mean	61.24
Std Dev	1.997

ampl



B0L101S, U24-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

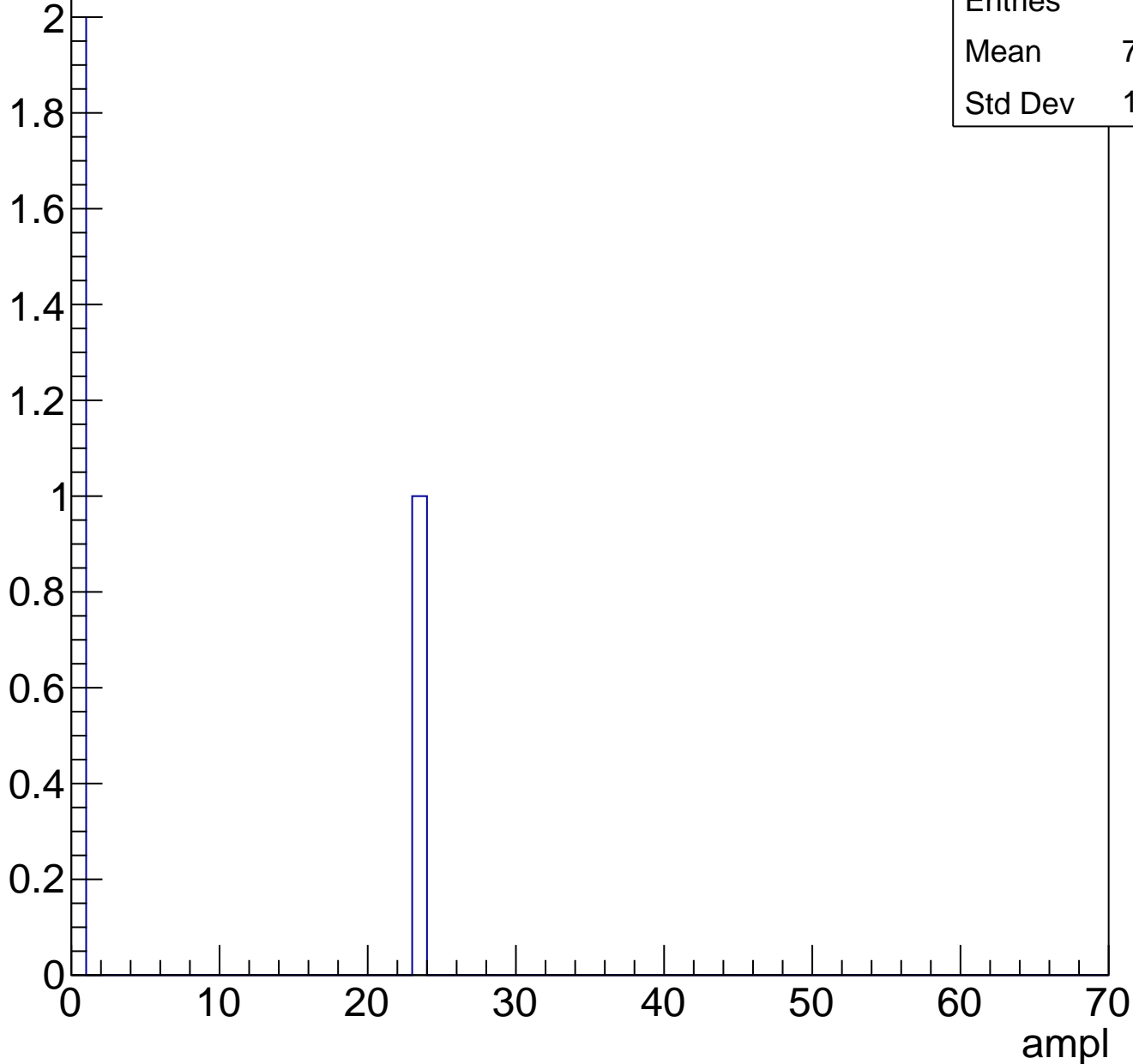
Entry



B0L101S, U24-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch9, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	30.34
Std Dev	4.016

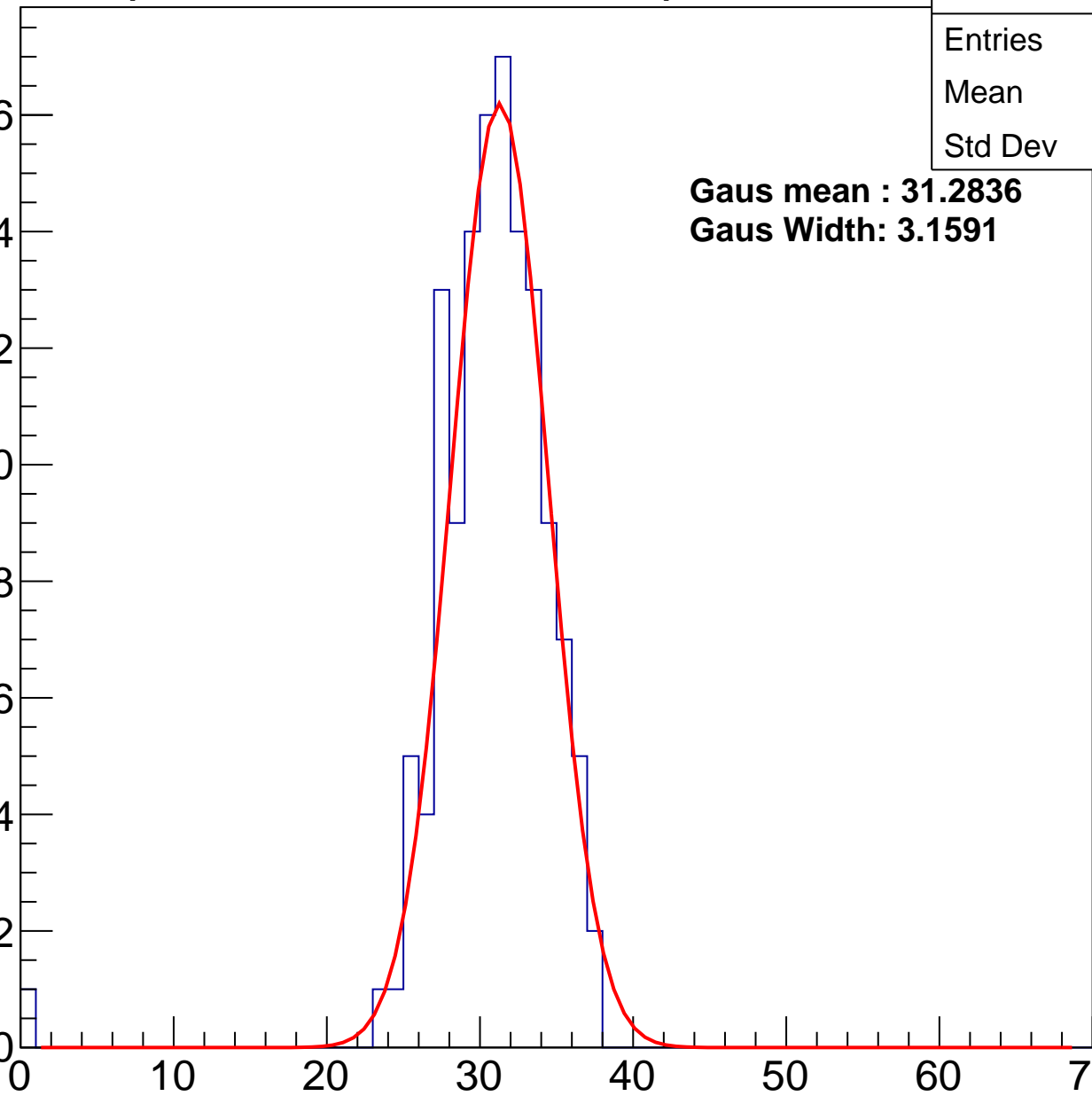
Gaus mean : 31.2836

Gaus Width: 3.1591

Entry

16
14
12
10
8
6
4
2
0

ampl



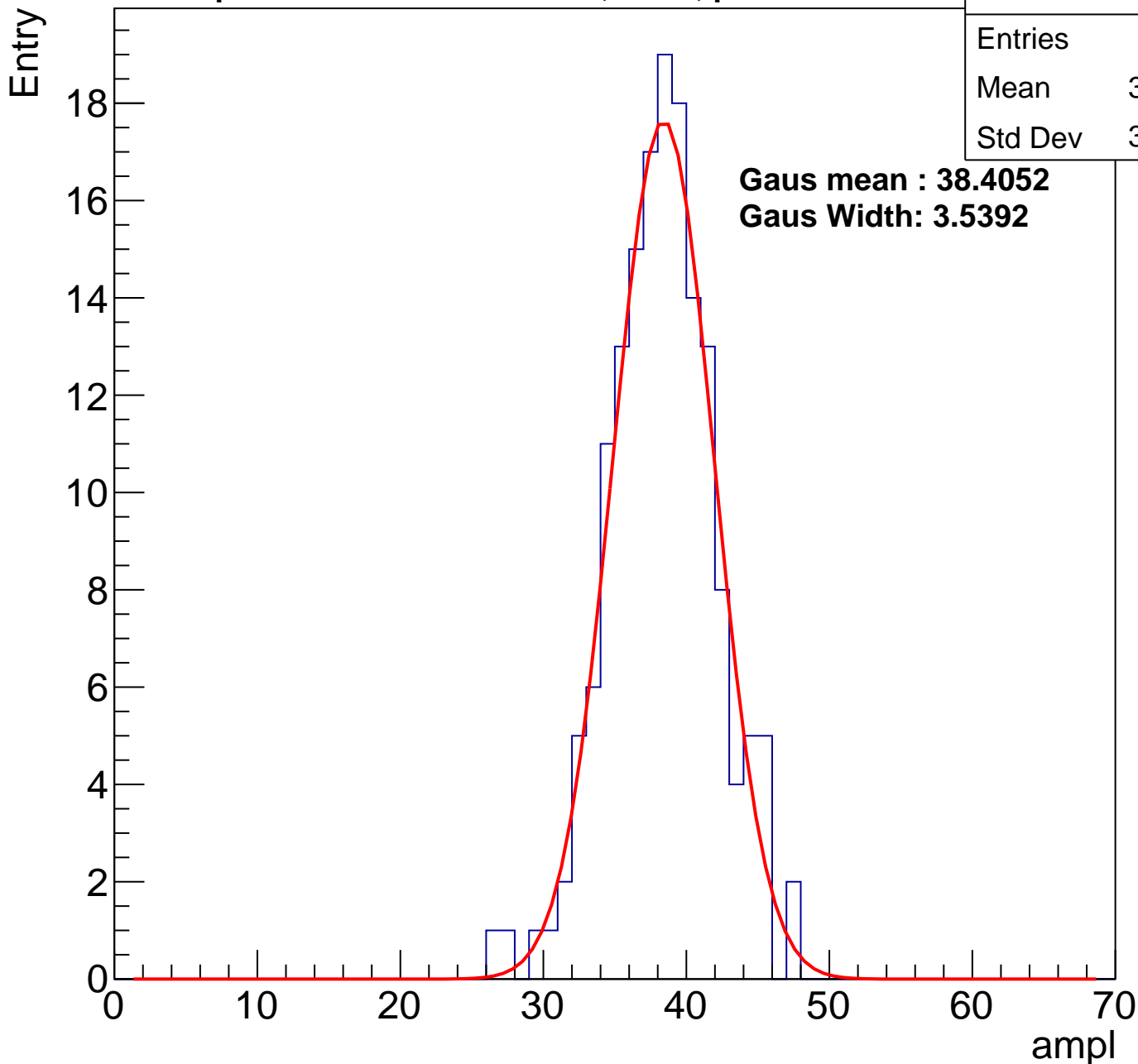
B0L101S, U24-ch9, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	37.85
Std Dev	3.705

Gaus mean : 38.4052

Gaus Width: 3.5392



B0L101S, U24-ch9, adc2

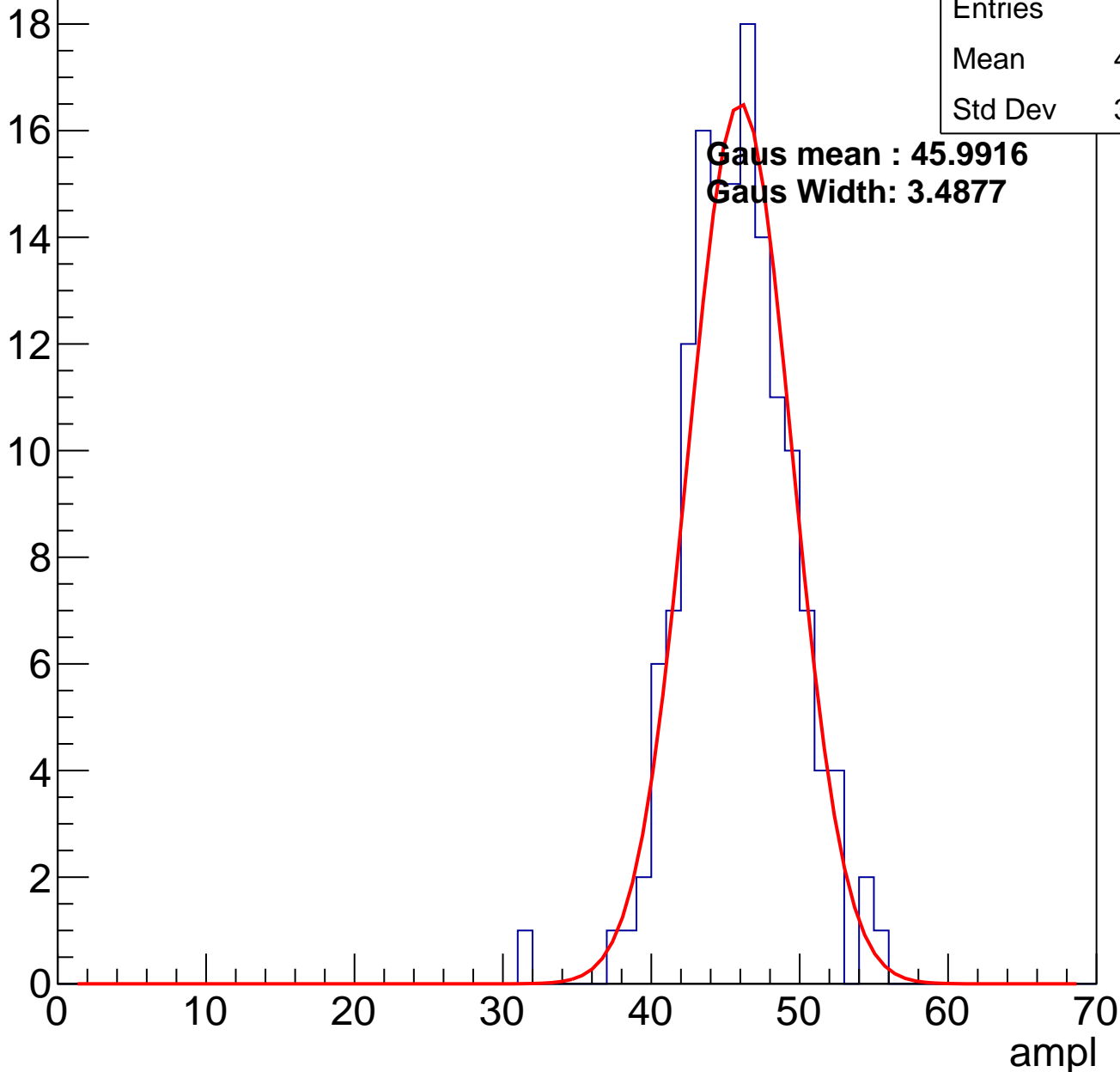
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	45.35
Std Dev	3.616

Gaus mean : 45.9916

Gaus Width: 3.4877

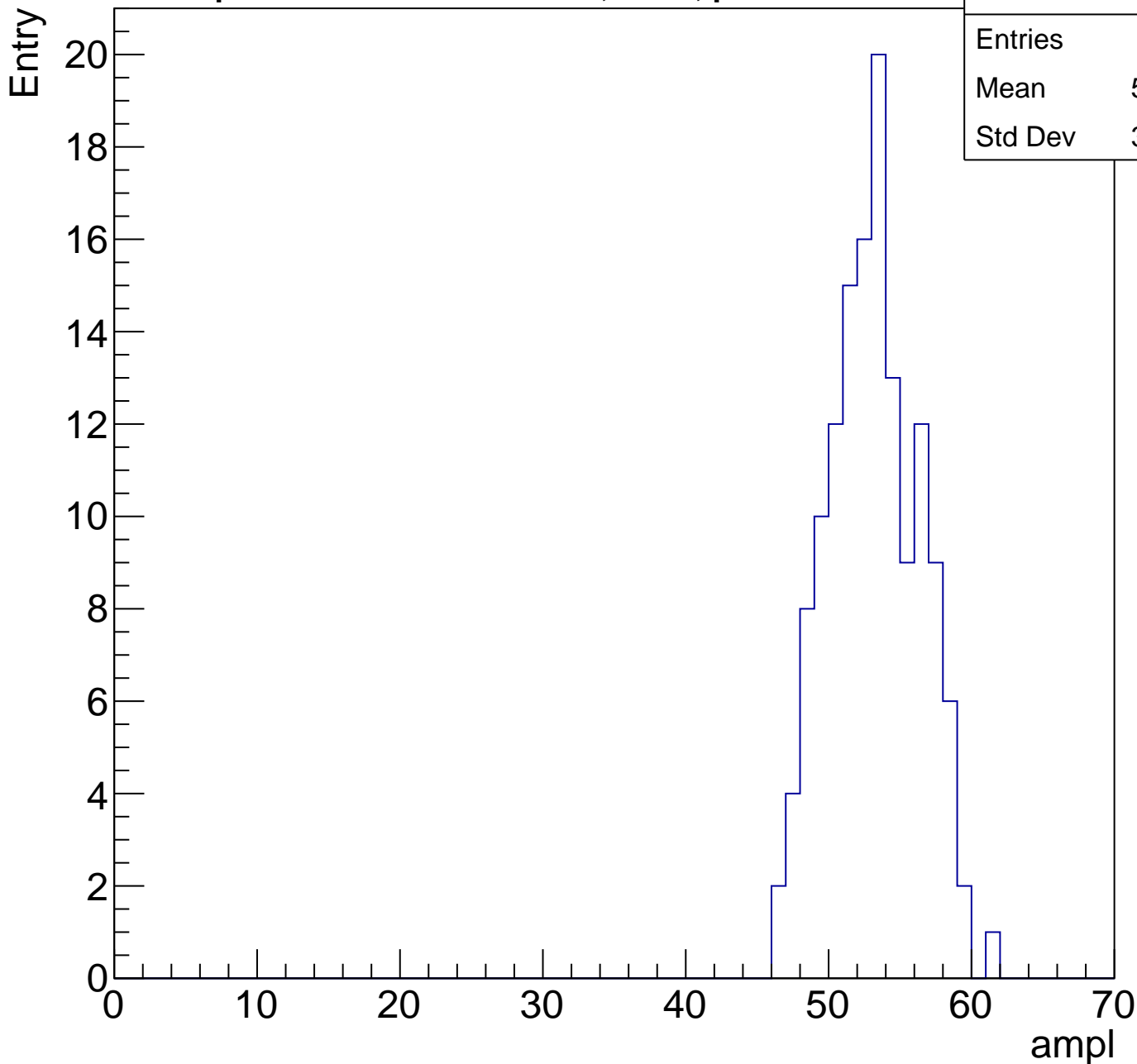
Entry



B0L101S, U24-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	52.66
Std Dev	3.125



B0L101S, U24-ch9, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	58.5
Std Dev	2.925

ampl

0 10 20 30 40 50 60 70

0

2

4

6

8

10

12

14

B0L101S, U24-ch9, adc5

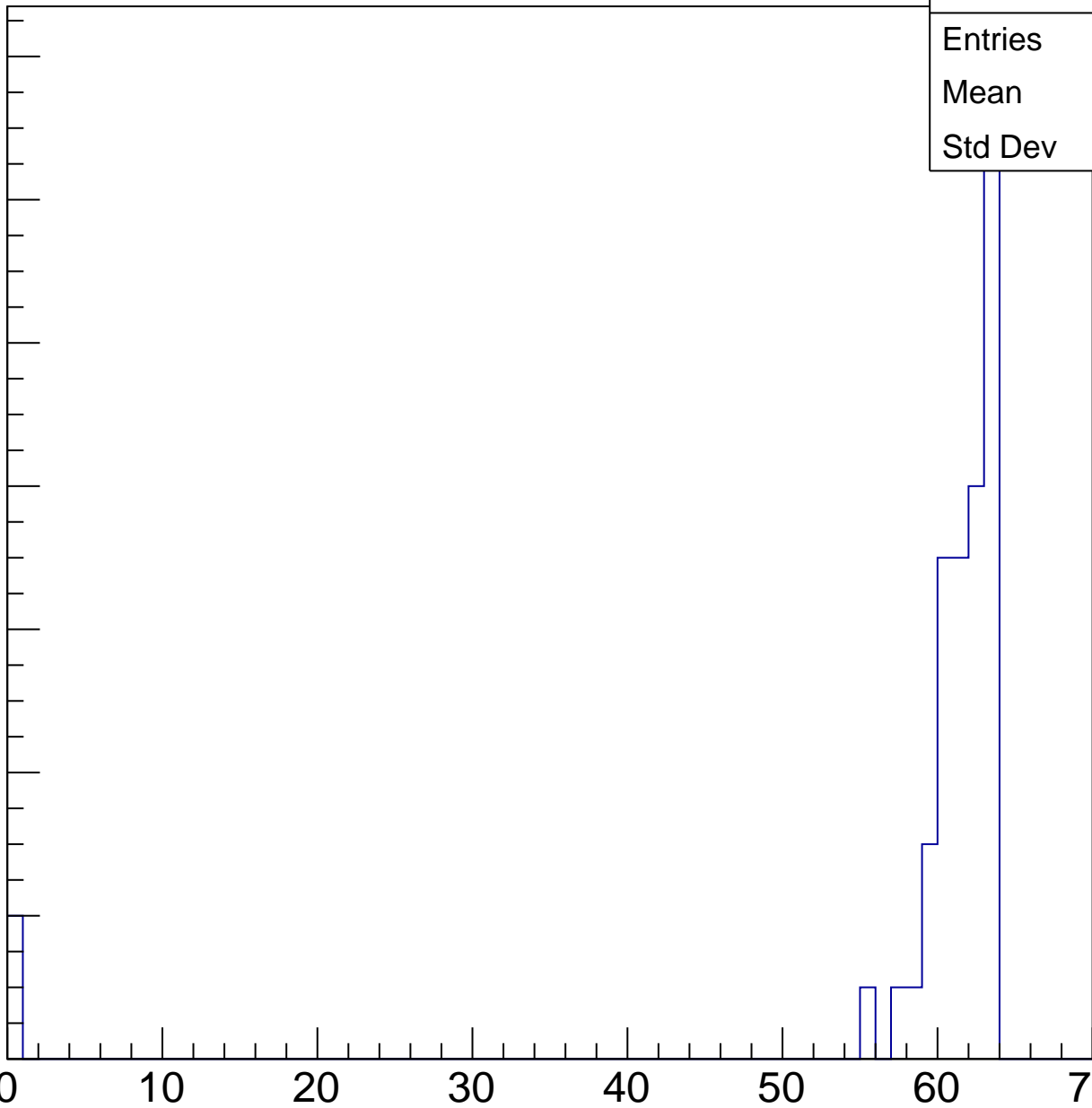
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	44
Mean	58.45
Std Dev	12.88

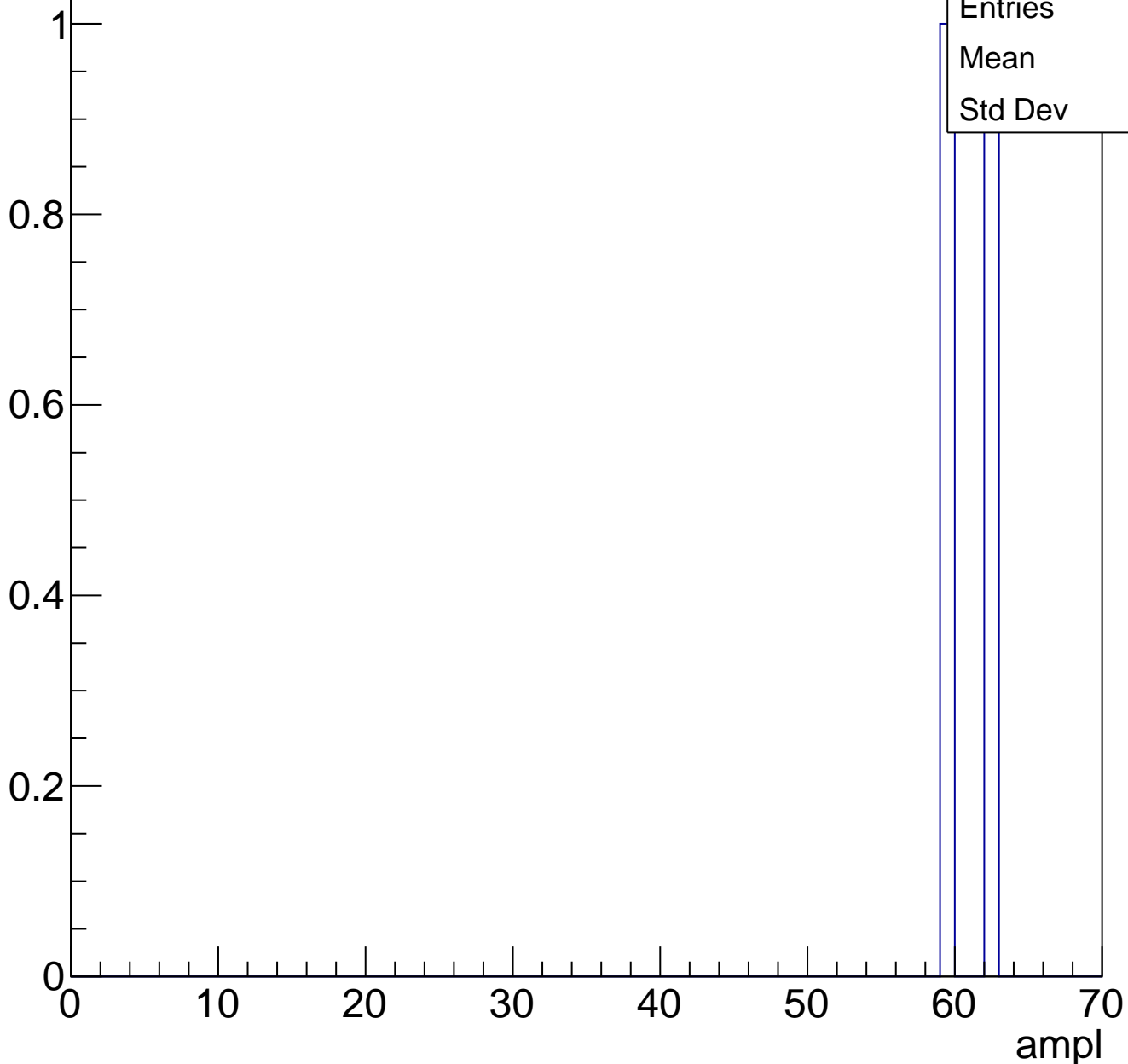
ampl



B0L101S, U24-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch10, adc0

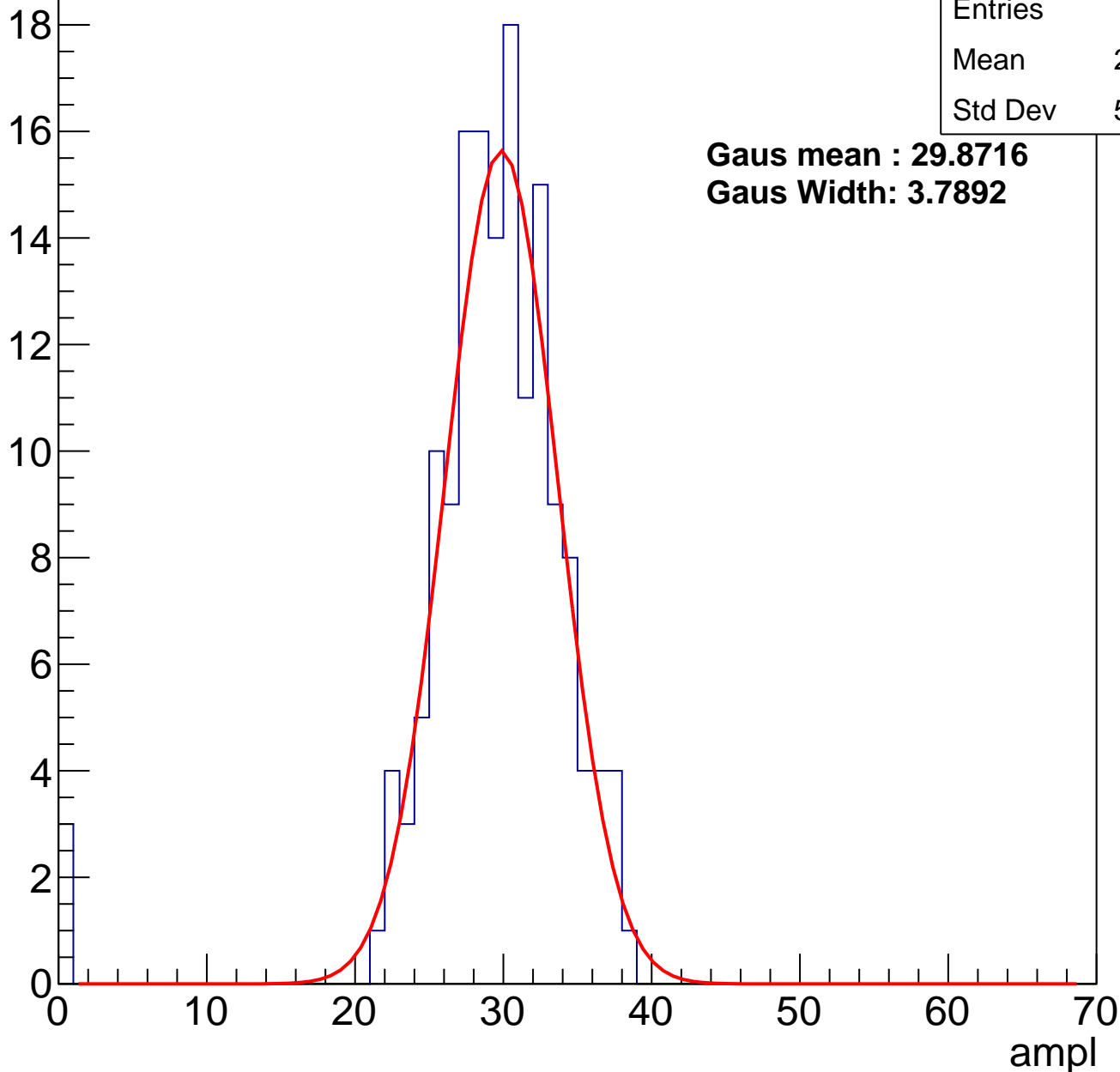
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	28.83
Std Dev	5.418

Gaus mean : 29.8716

Gaus Width: 3.7892

Entry

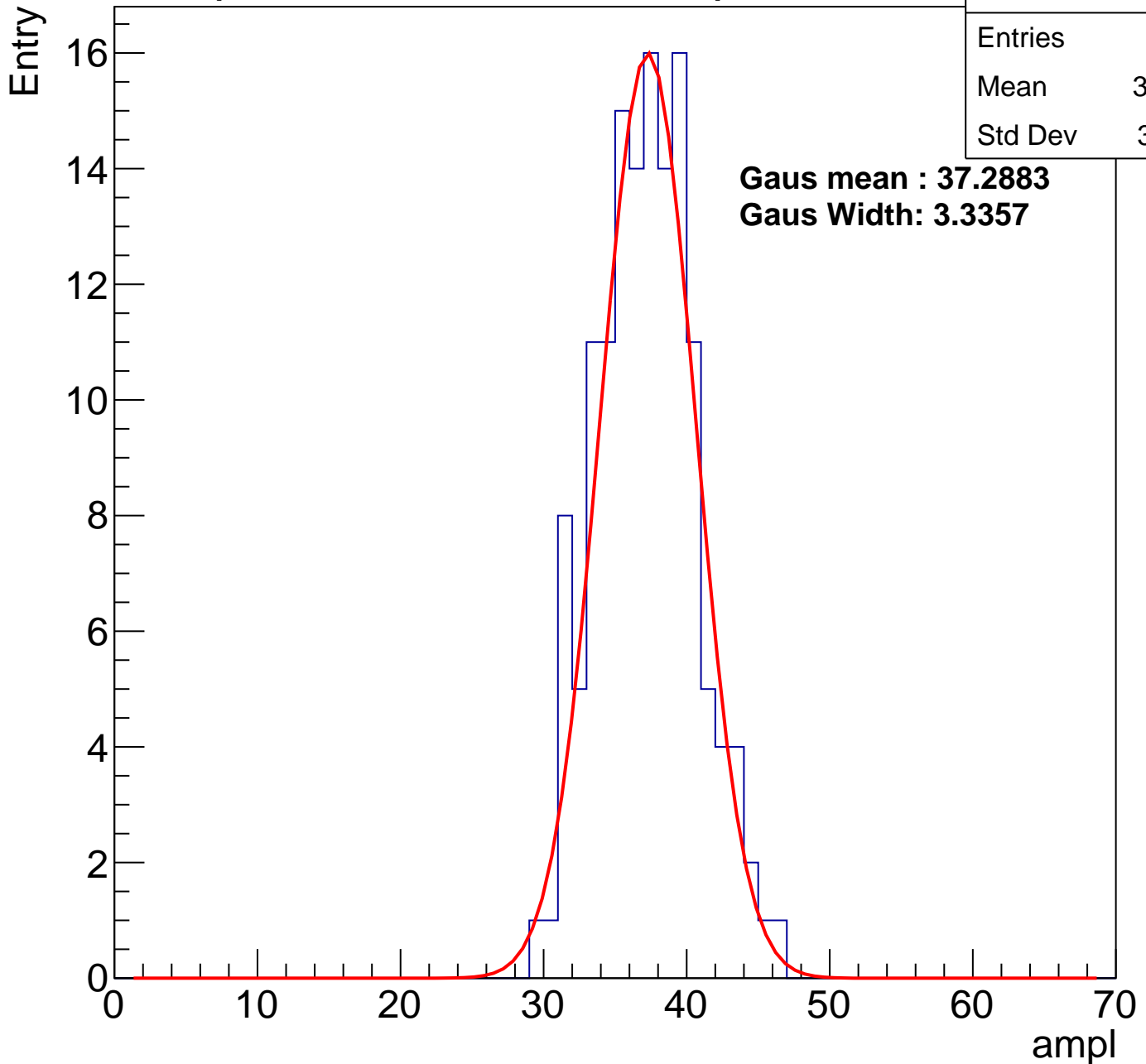


B0L101S, U24-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	36.75
Std Dev	3.391

Gaus mean : 37.2883
Gaus Width: 3.3357



B0L101S, U24-ch10, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries 96

Mean 42.94

Std Dev 3.4

Gaus mean : 43.1666

Gaus Width: 3.4913

0

10

20

30

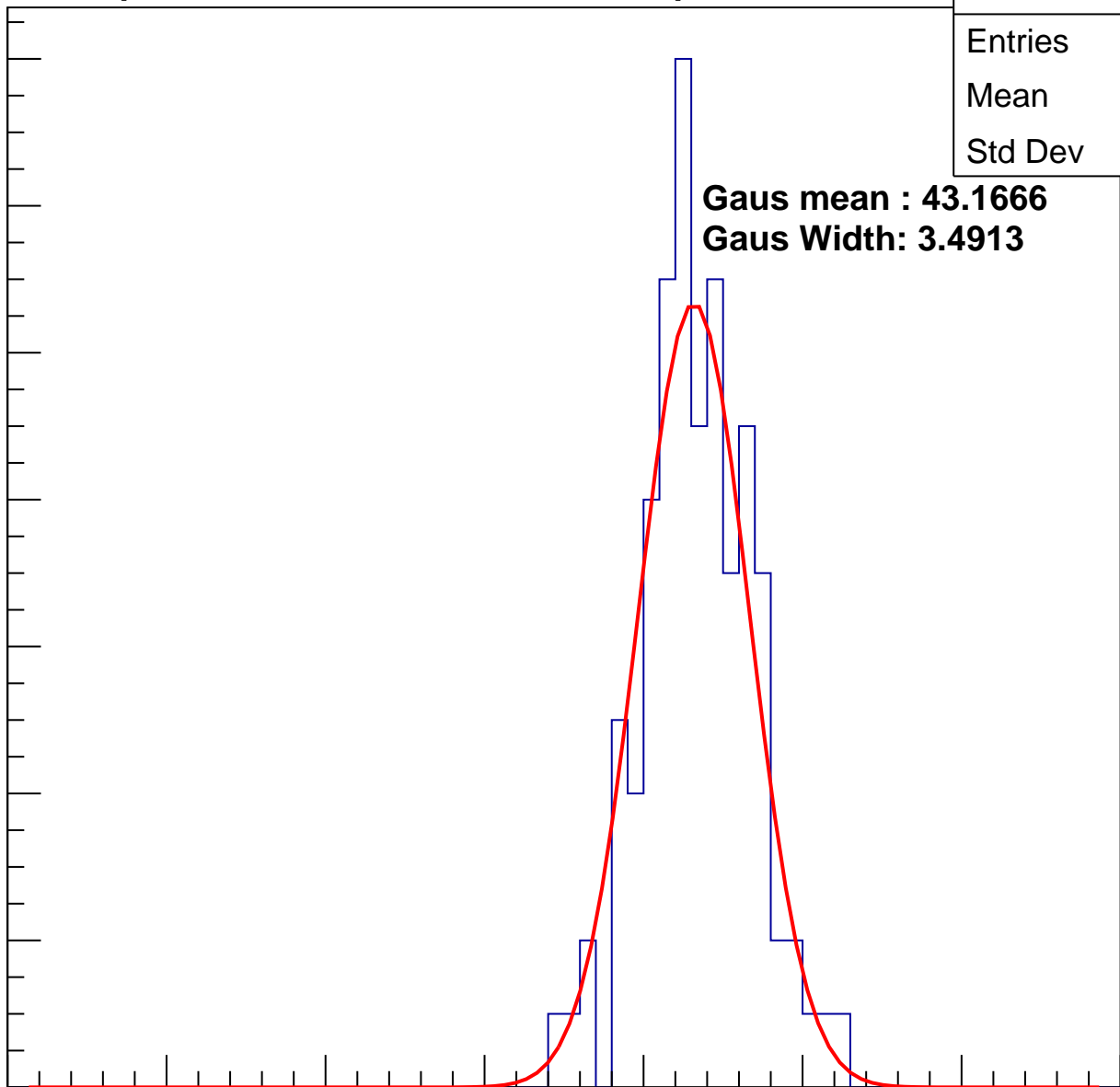
40

50

60

70

ampl

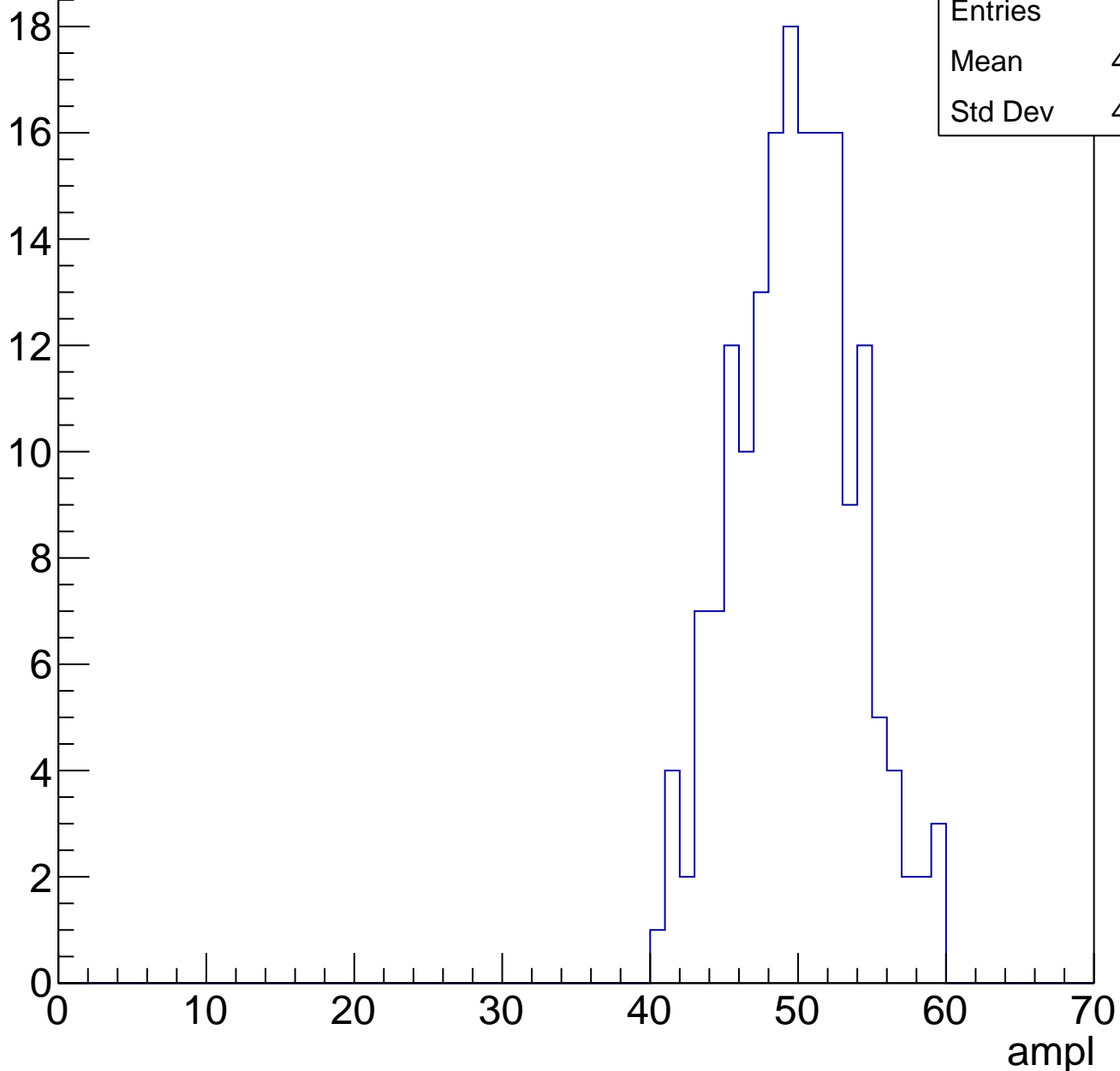


B0L101S, U24-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	49.35
Std Dev	4.017

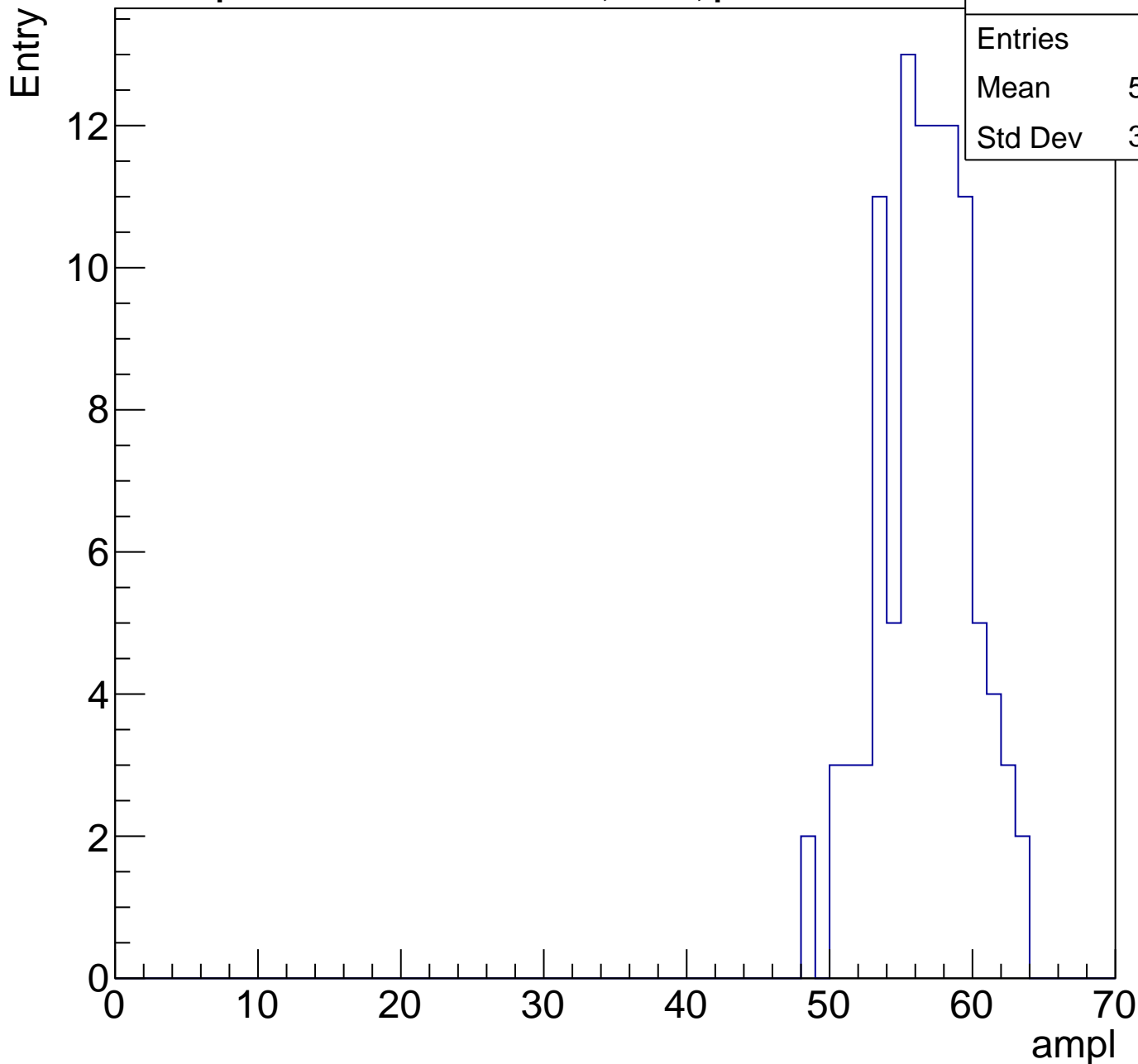
Entry



B0L101S, U24-ch10, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	56.24
Std Dev	3.194



B0L101S, U24-ch10, adc5

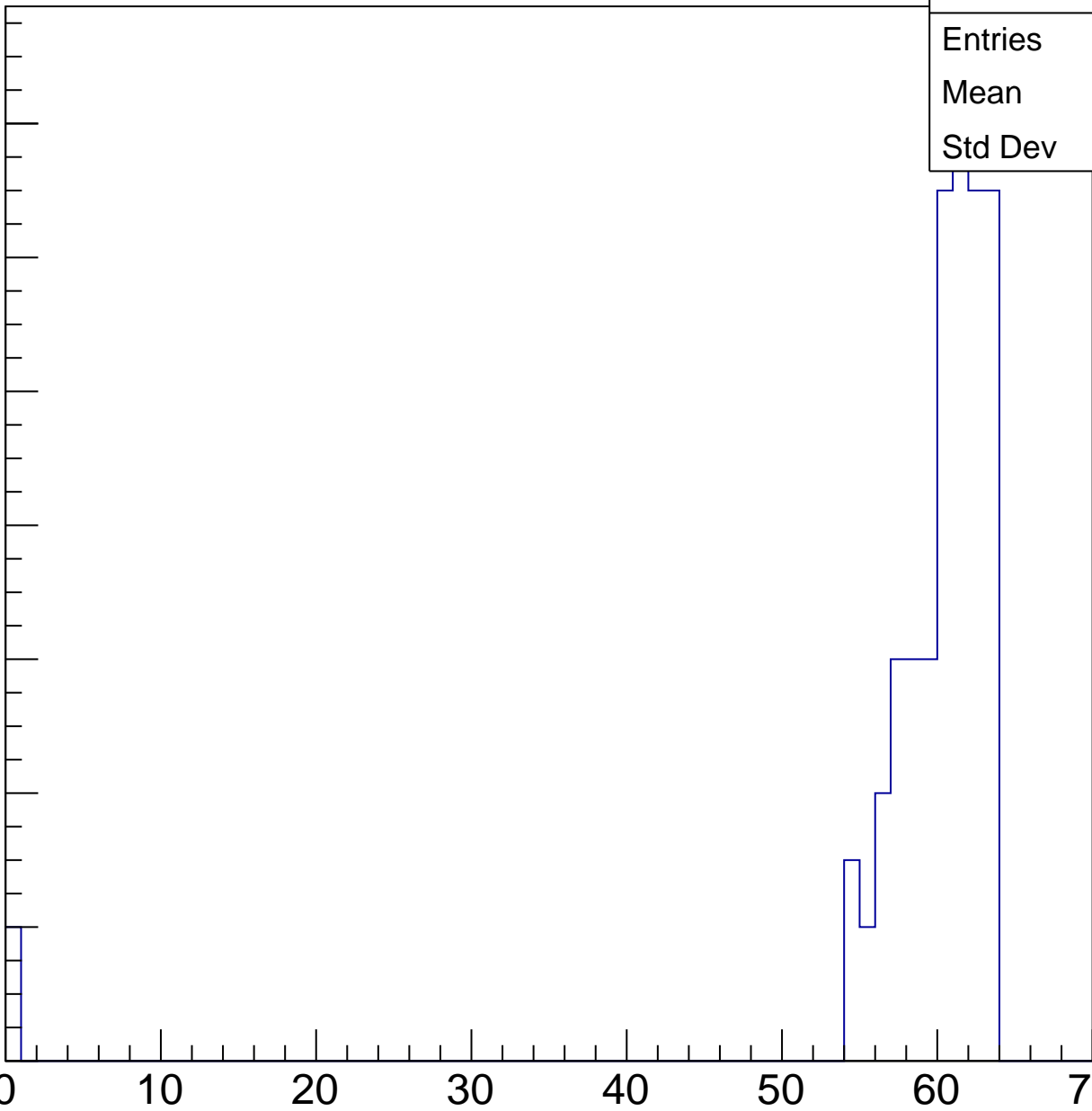
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	58.55
Std Dev	9.516

ampl

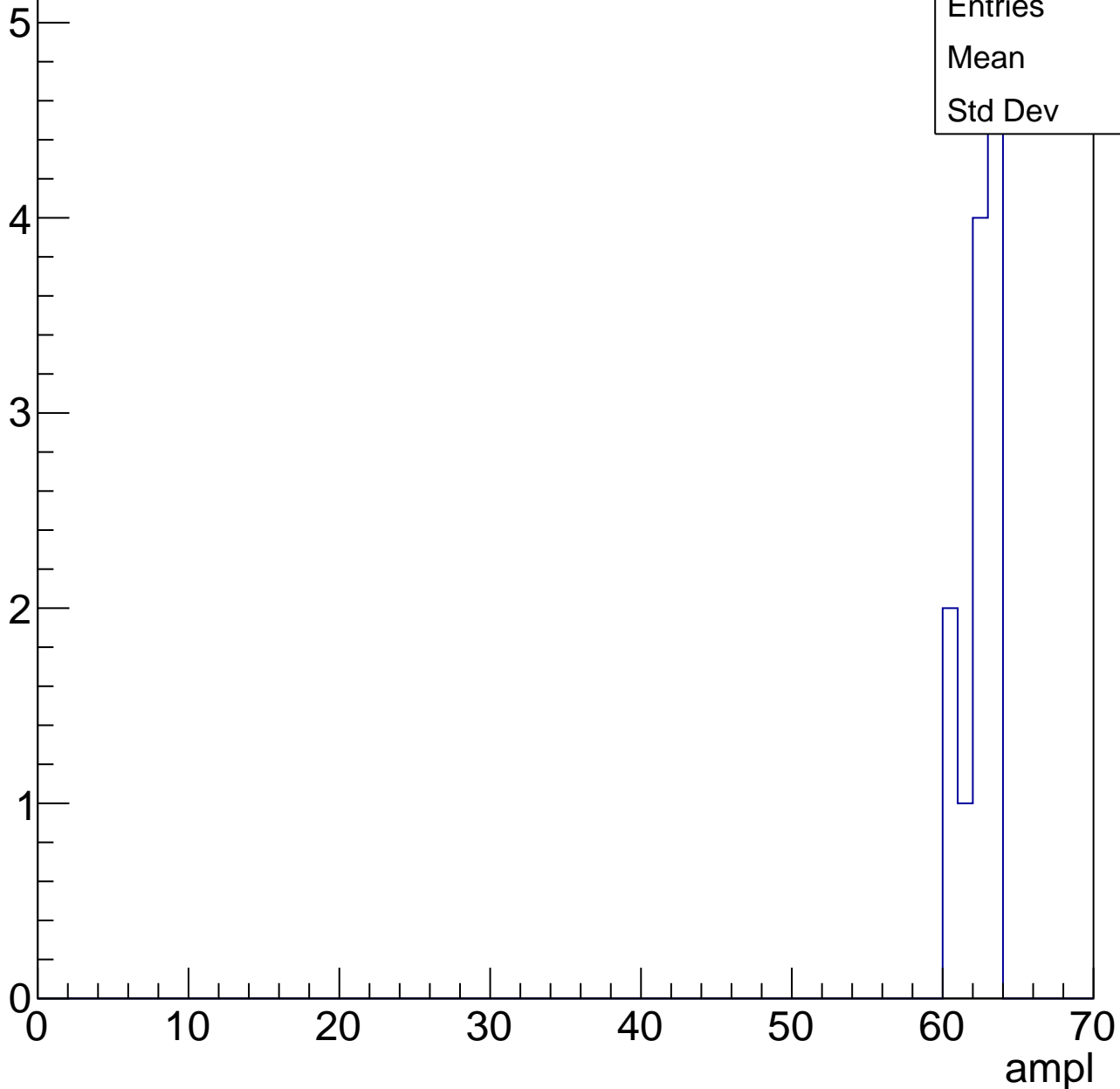


B0L101S, U24-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	62
Std Dev	1.08



B0L101S, U24-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

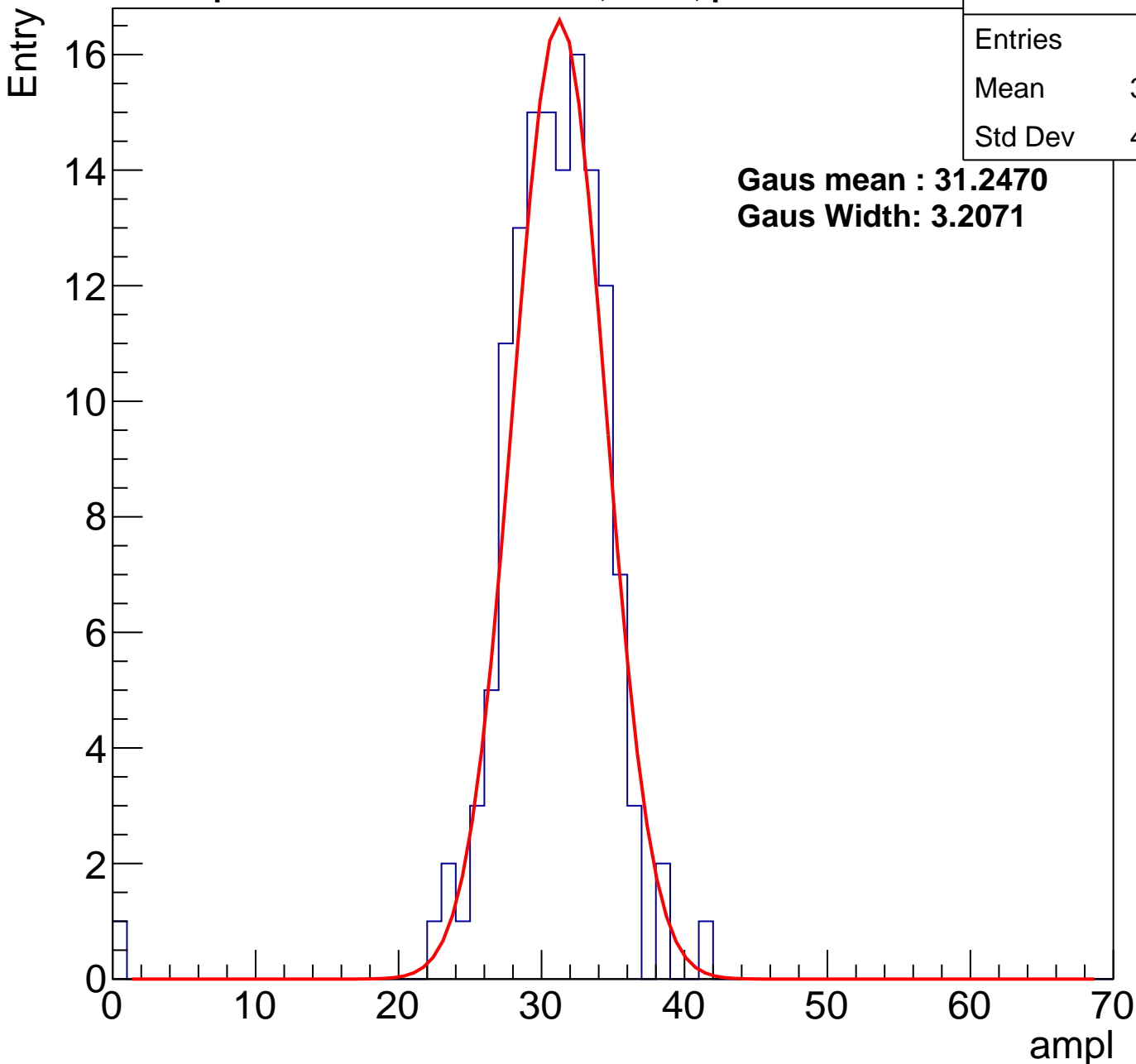
B0L101S, U24-ch11, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	30.36
Std Dev	4.139

Gaus mean : 31.2470

Gaus Width: 3.2071



B0L101S, U24-ch11, adc1

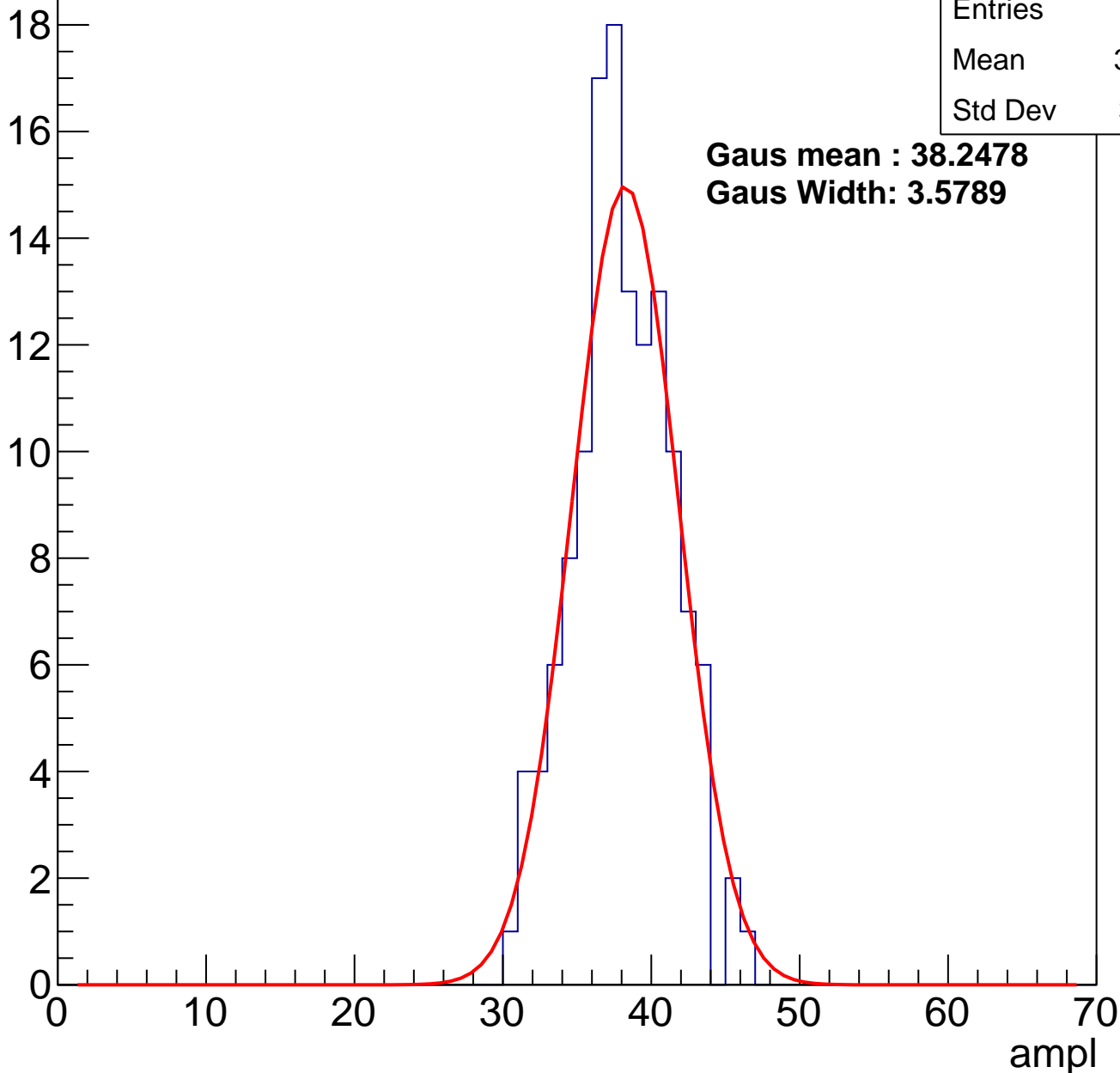
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	37.58
Std Dev	3.271

Gaus mean : 38.2478

Gaus Width: 3.5789

Entry



B0L101S, U24-ch11, adc2

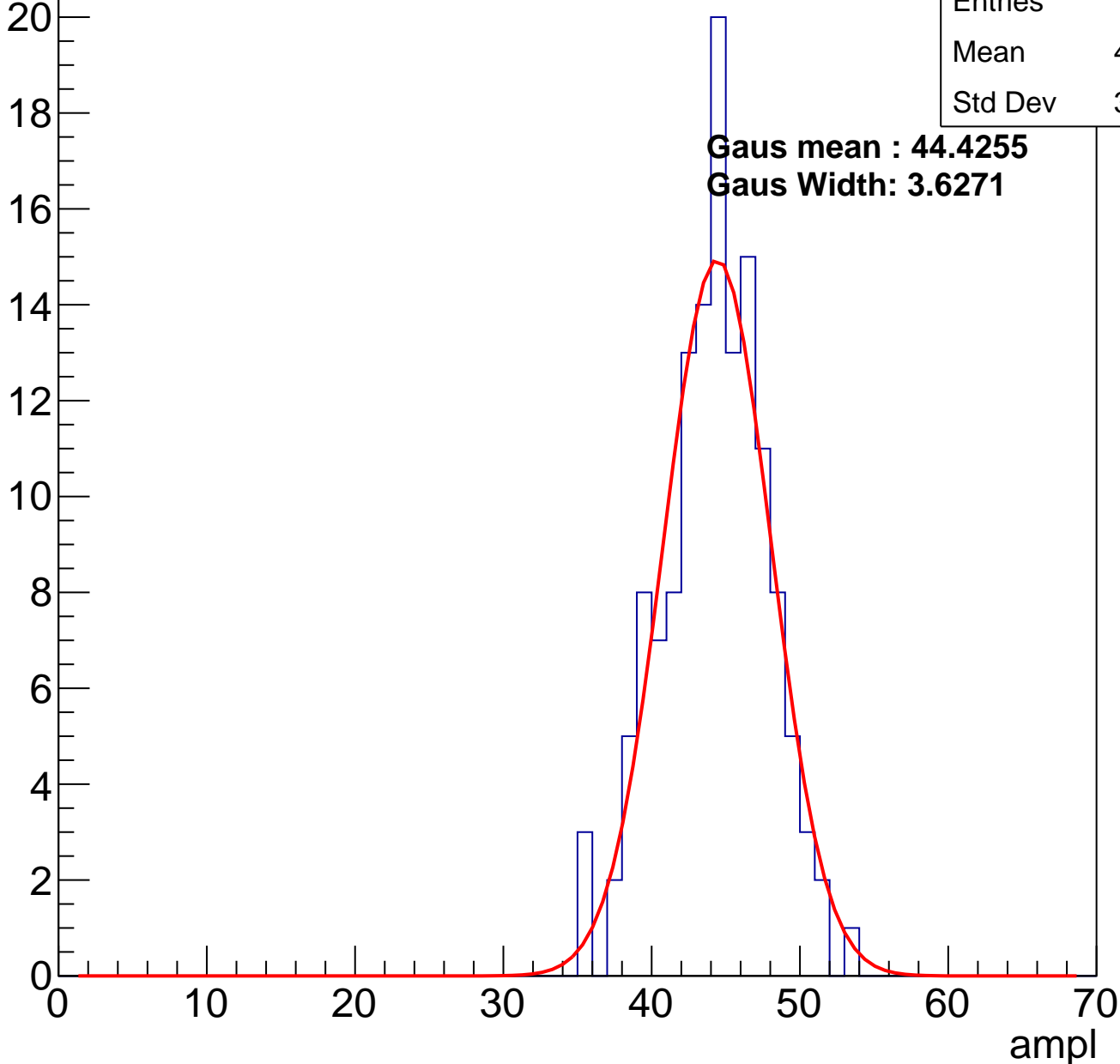
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	138
Mean	43.79
Std Dev	3.486

Gaus mean : 44.4255

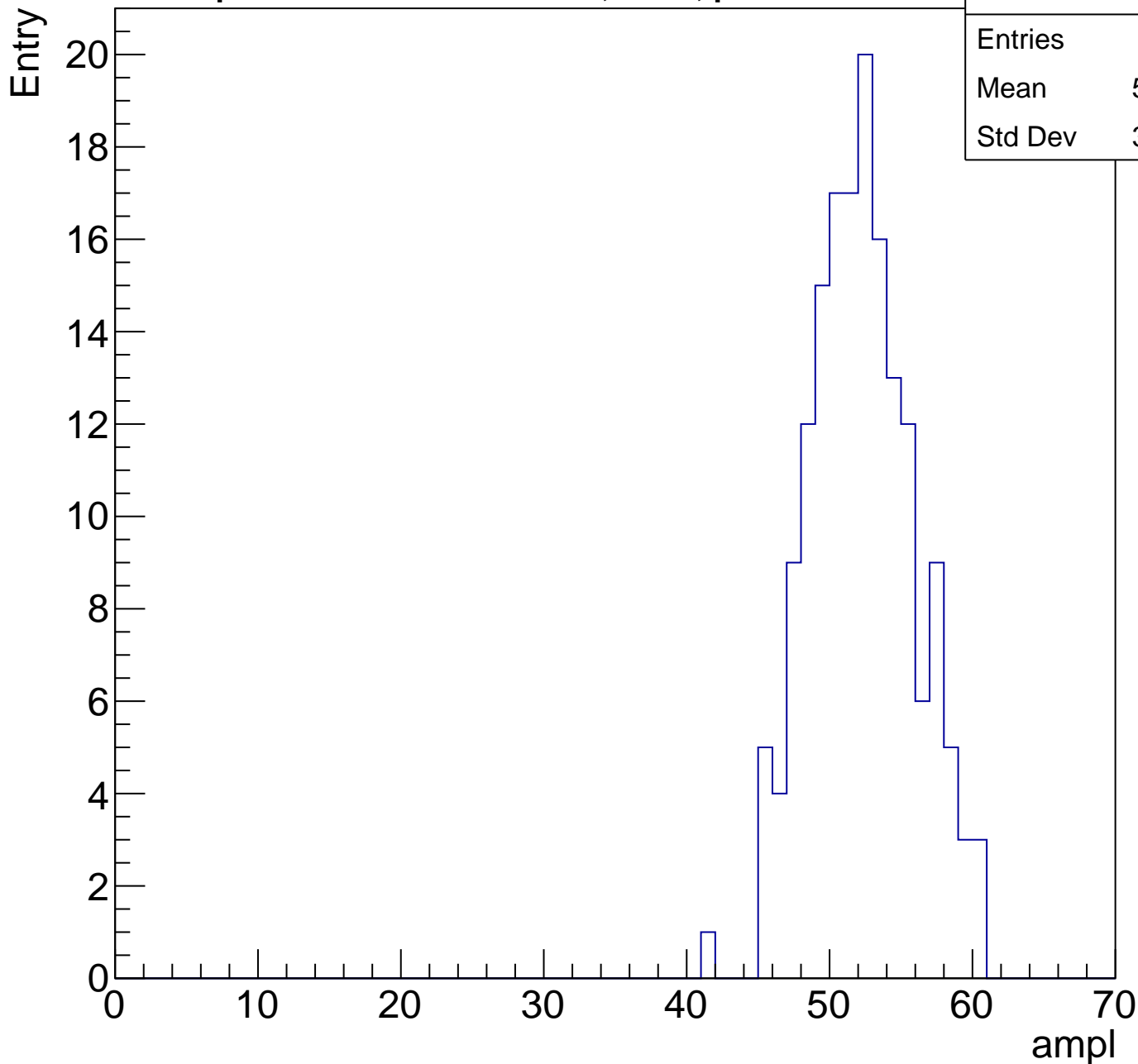
Gaus Width: 3.6271



B0L101S, U24-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	51.78
Std Dev	3.592



B0L101S, U24-ch11, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

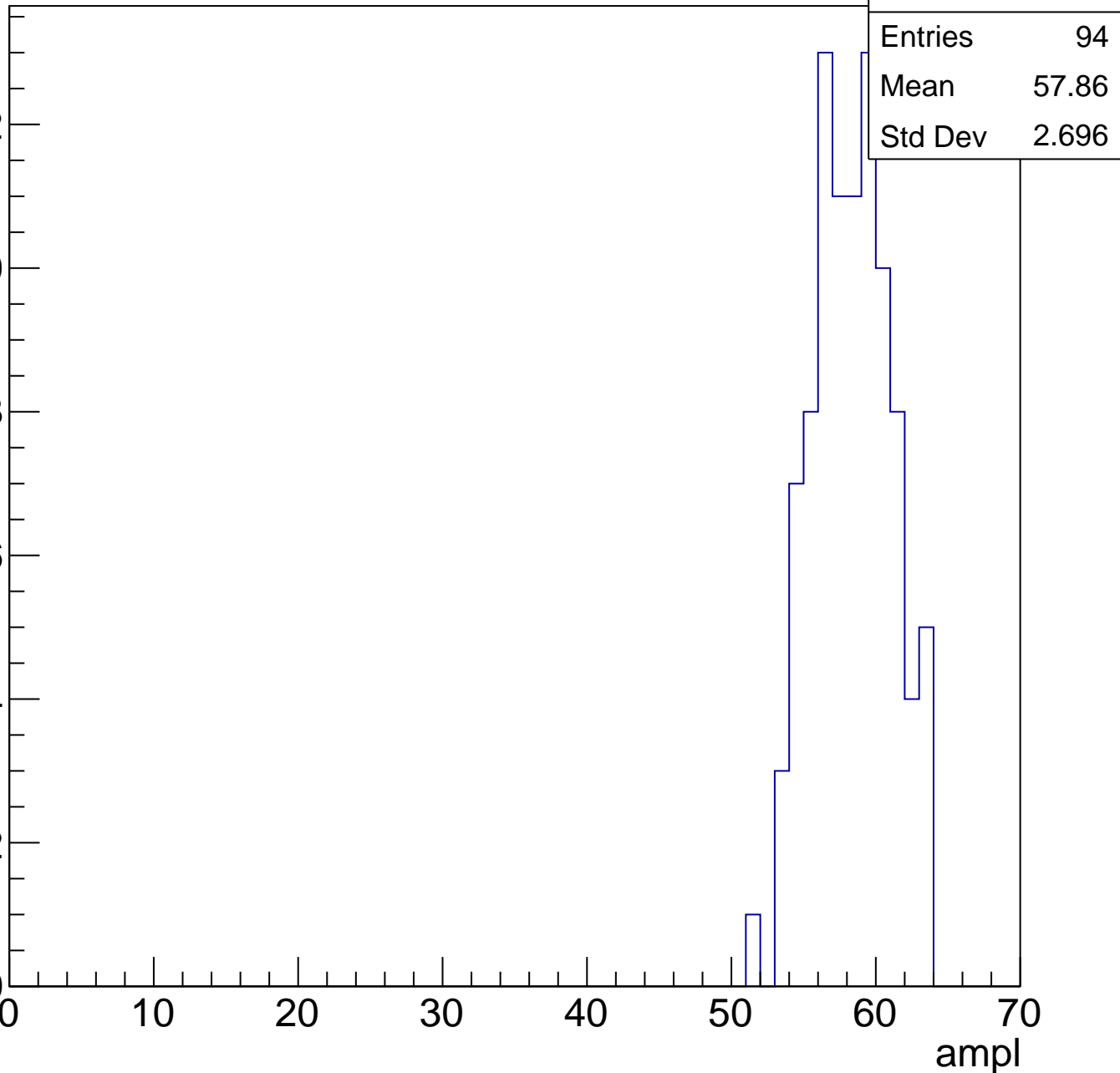
40

50

60

ampl

Entries	94
Mean	57.86
Std Dev	2.696



B0L101S, U24-ch11, adc5

calib_packv5_042523_0143.root, FC#1, port C1

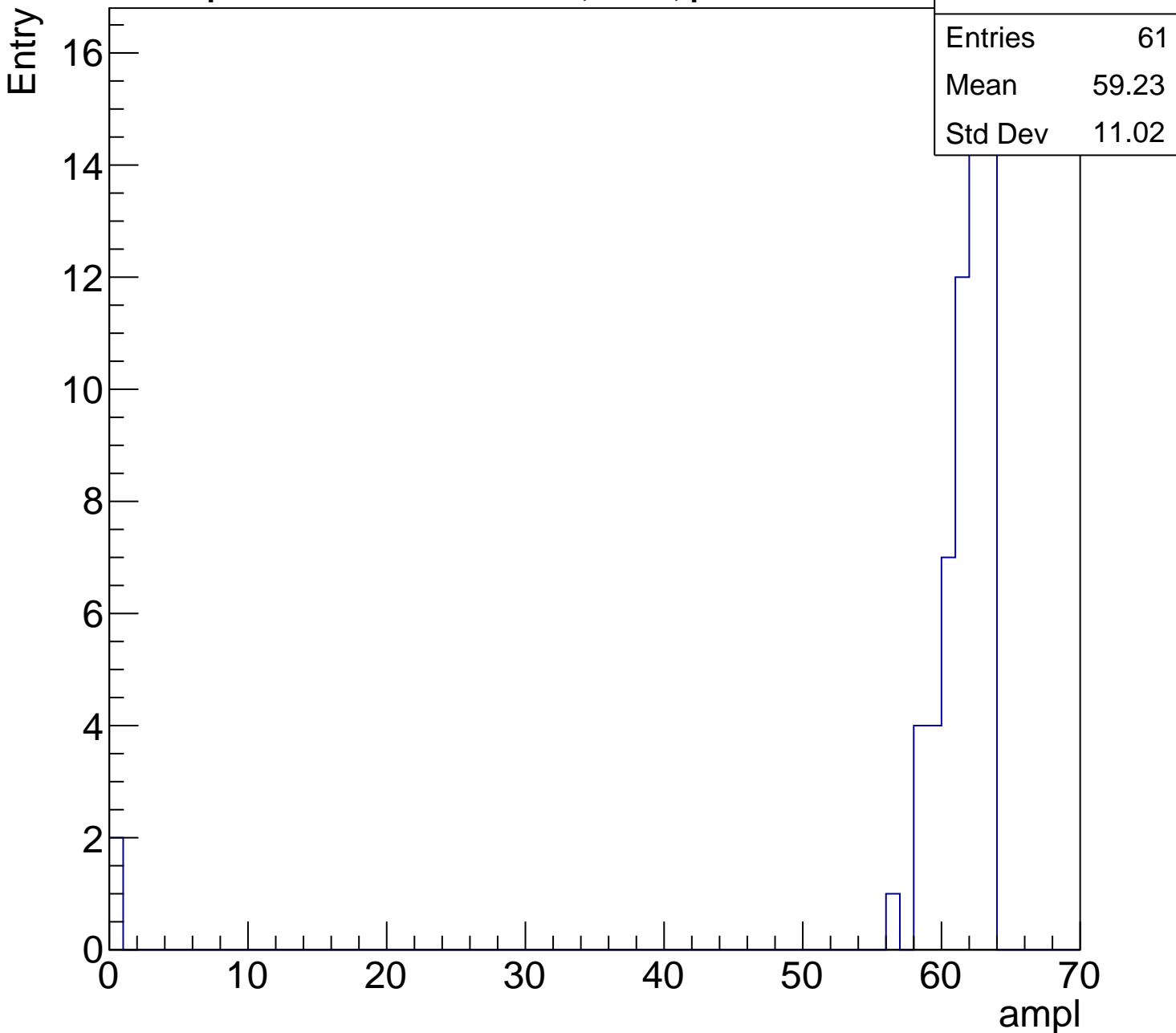
Entry

16
14
12
10
8
6
4
2
0

Entries	61
Mean	59.23
Std Dev	11.02

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

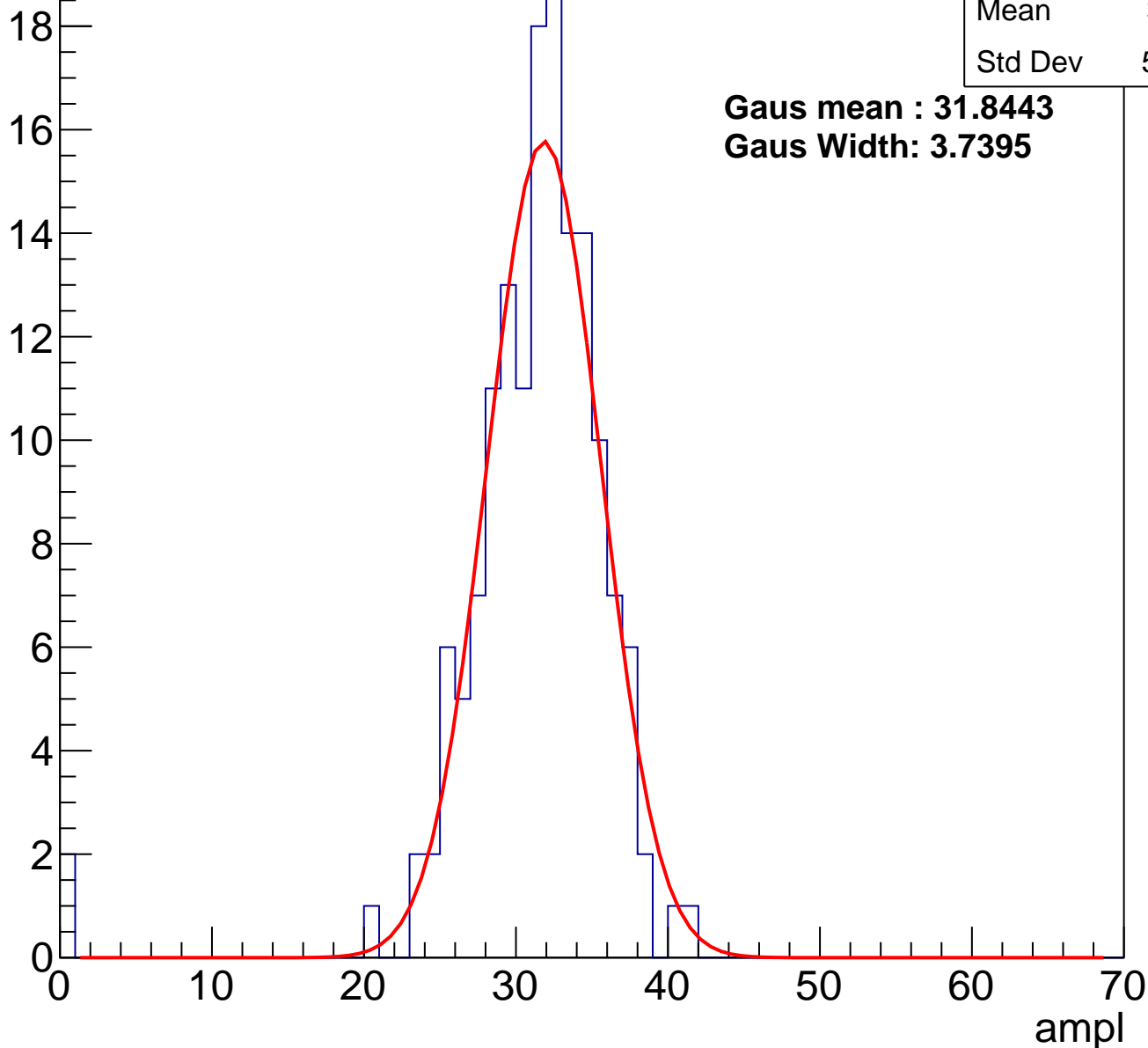


Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch12, adc0

calib_packv5_042523_0143.root, FC#1, port C1

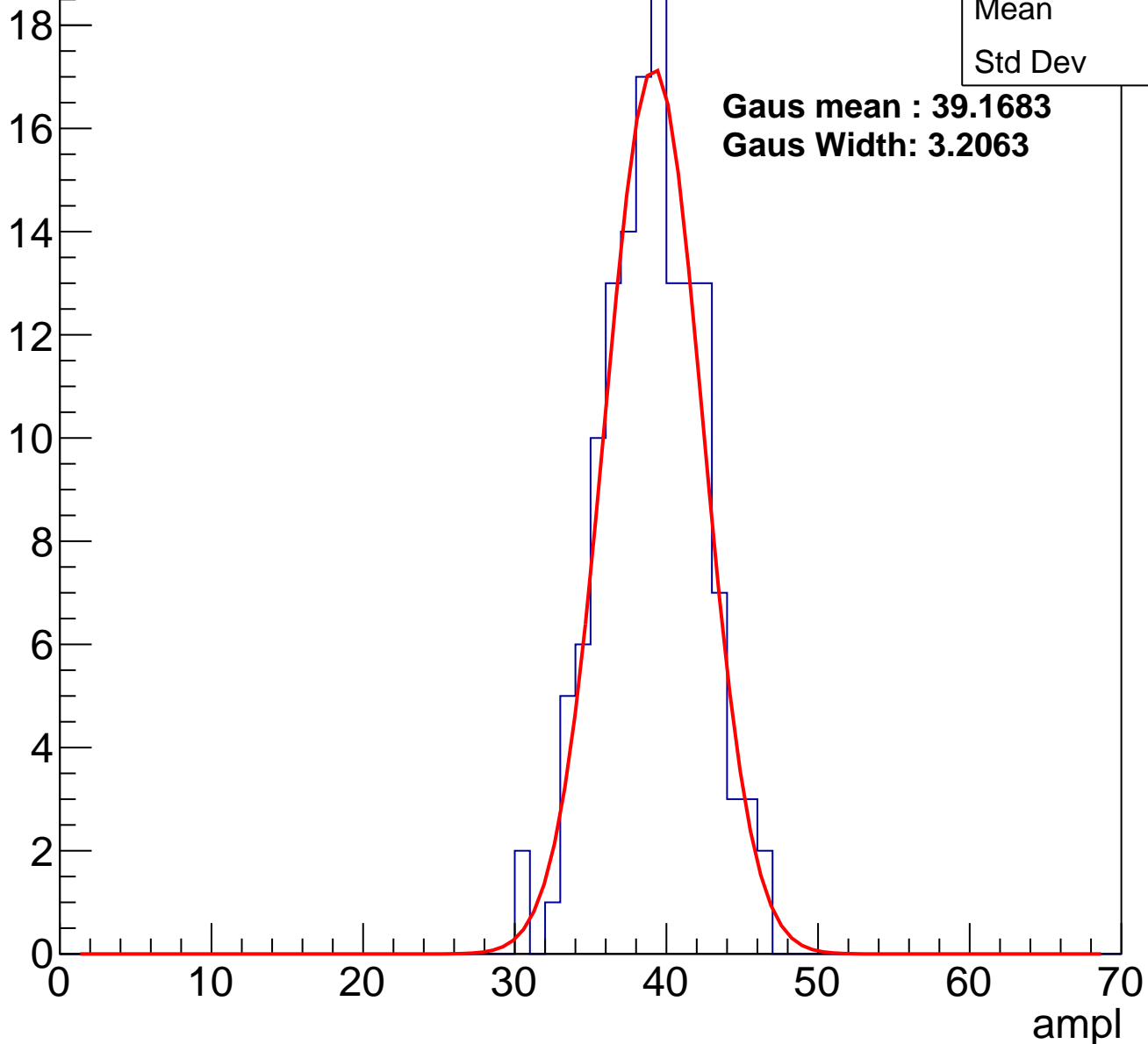
Entry



B0L101S, U24-ch12, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	141
Mean	38.6
Std Dev	3.2

B0L101S, U24-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

141

Mean

46.02

Std Dev

3.49

Gaus mean : 46.2316

Gaus Width: 3.2555

0

10

20

30

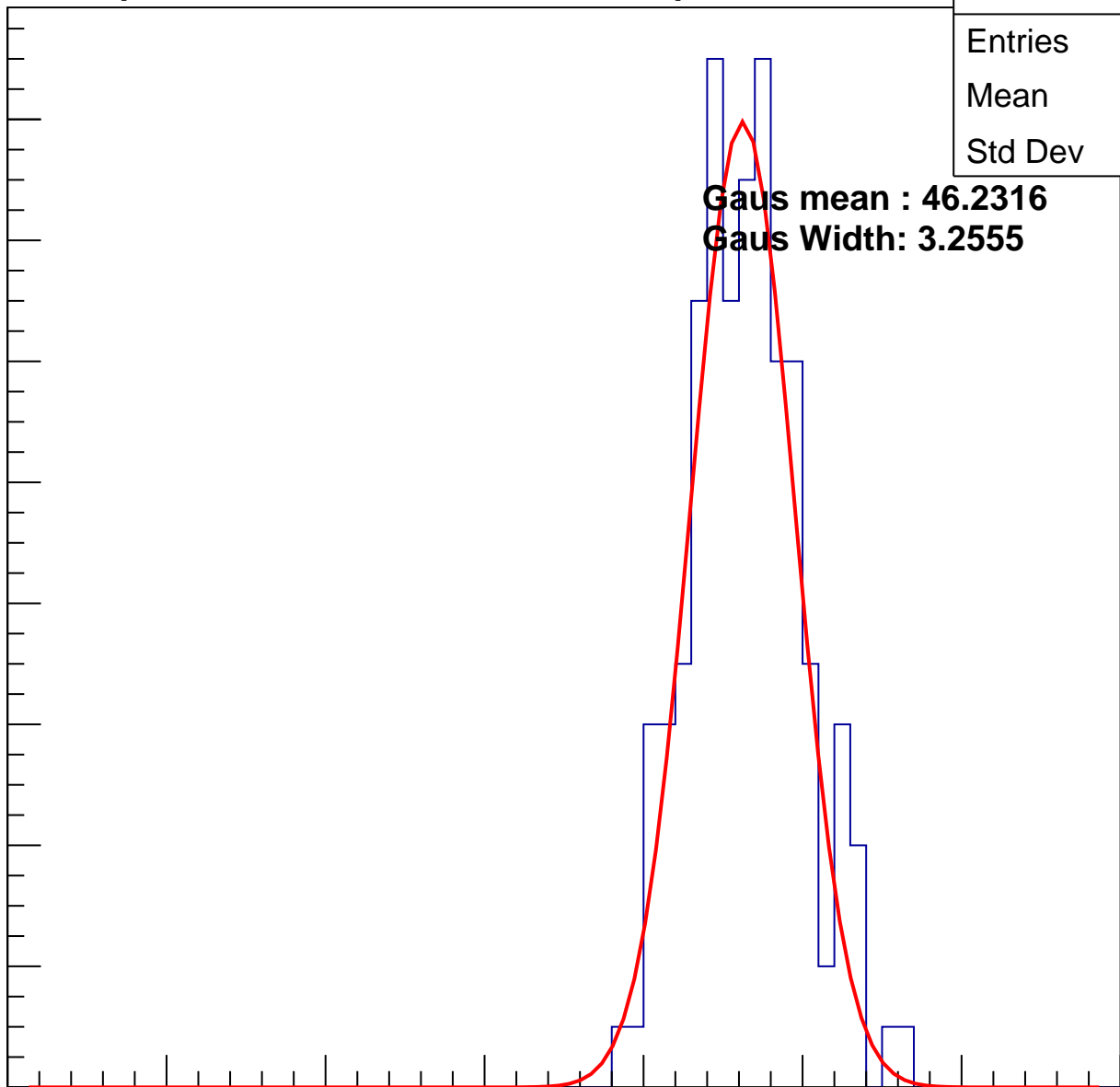
40

50

60

70

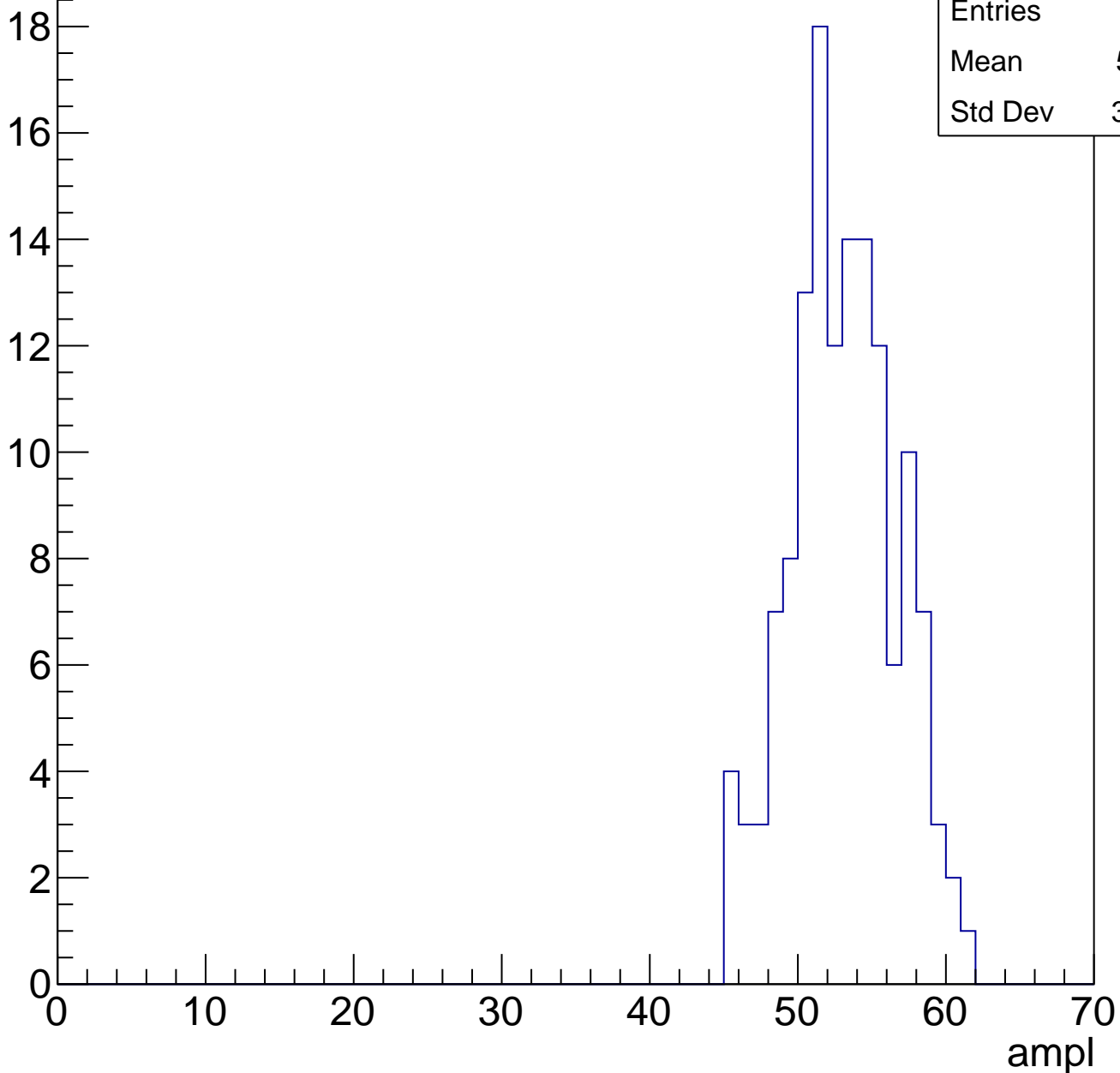
ampl



B0L101S, U24-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch12, adc4

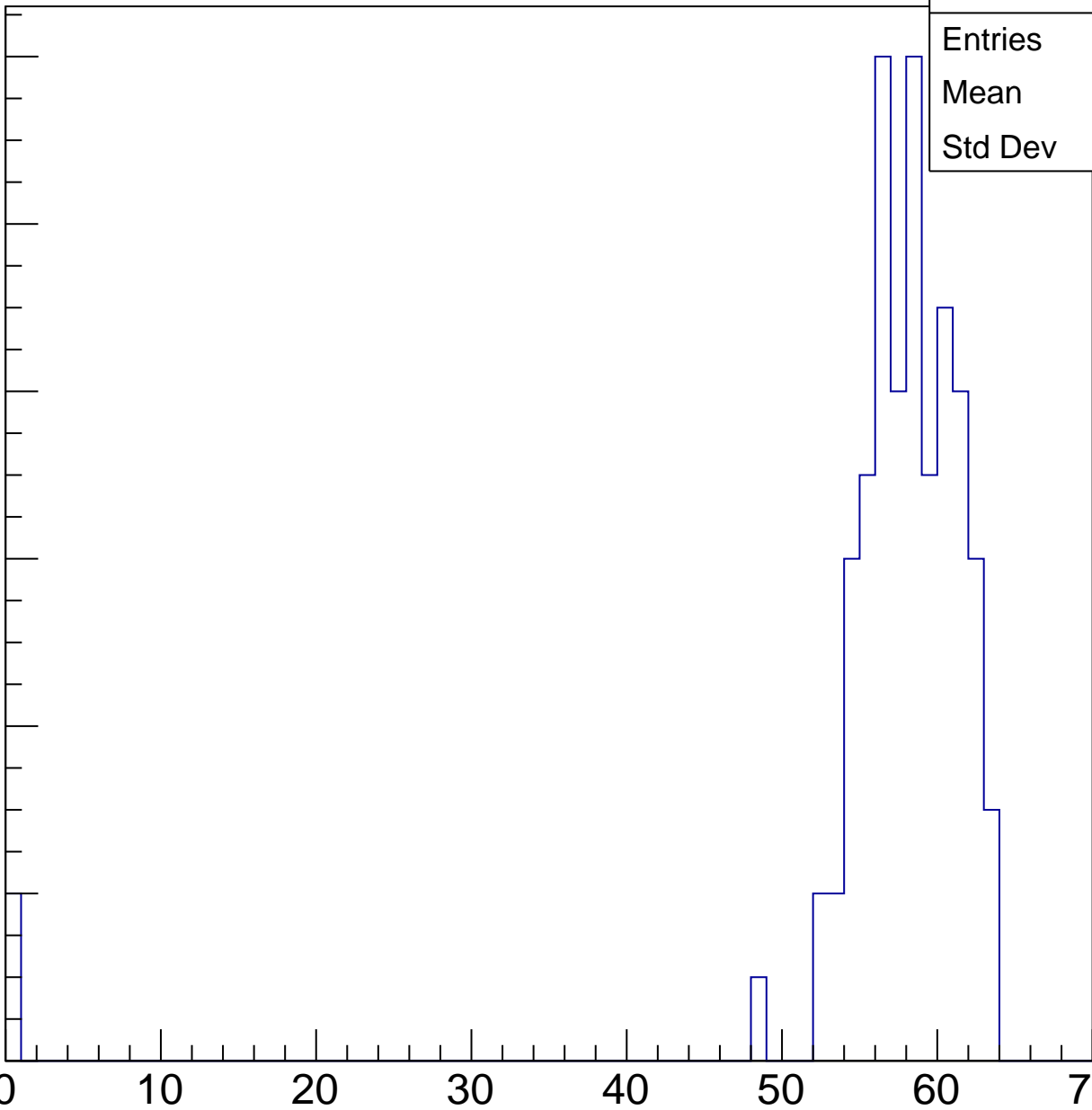
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	85
Mean	56.39
Std Dev	9.223

ampl



B0L101S, U24-ch12, adc5

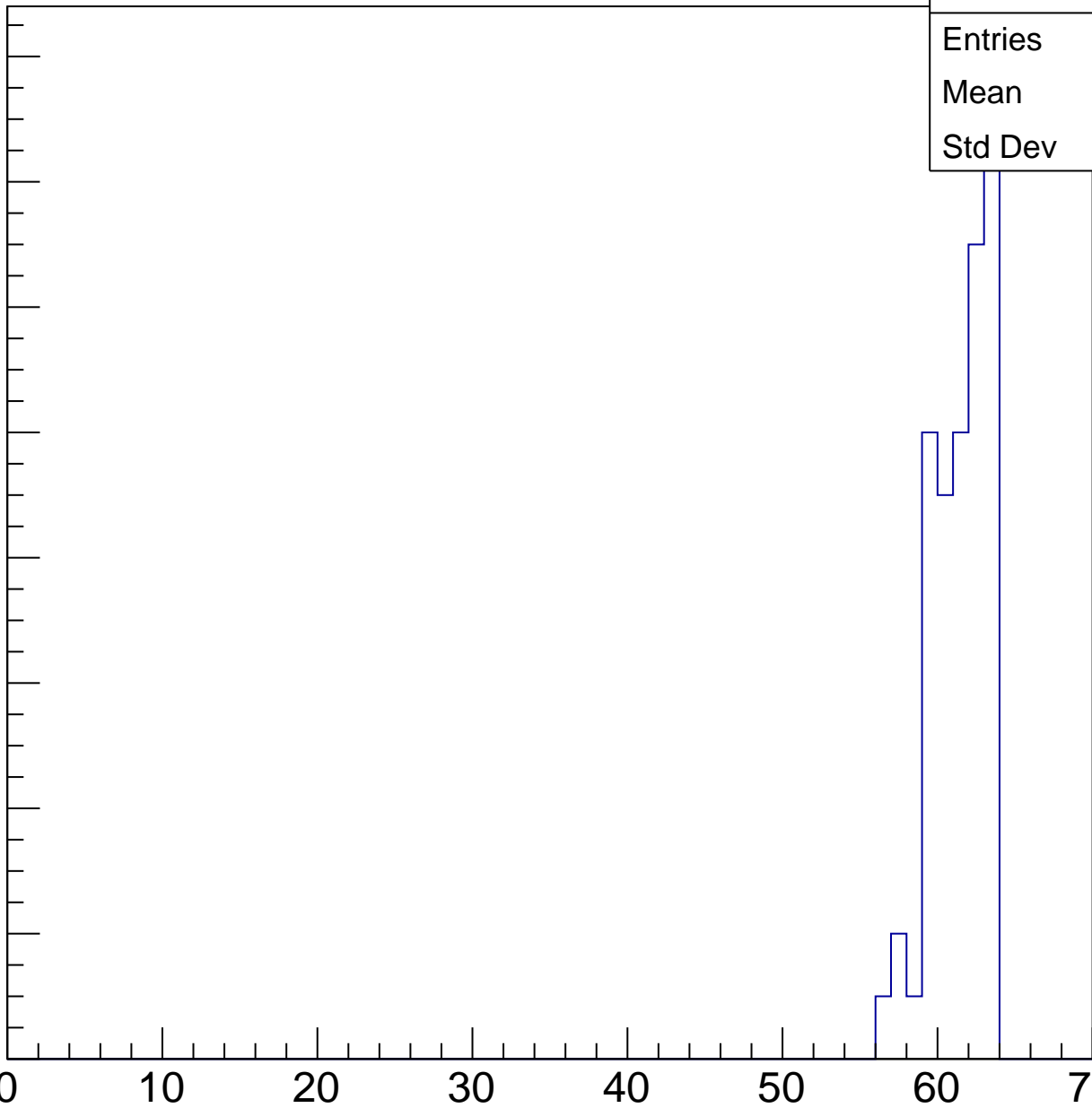
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	62
Mean	61
Std Dev	1.76

ampl



B0L101S, U24-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



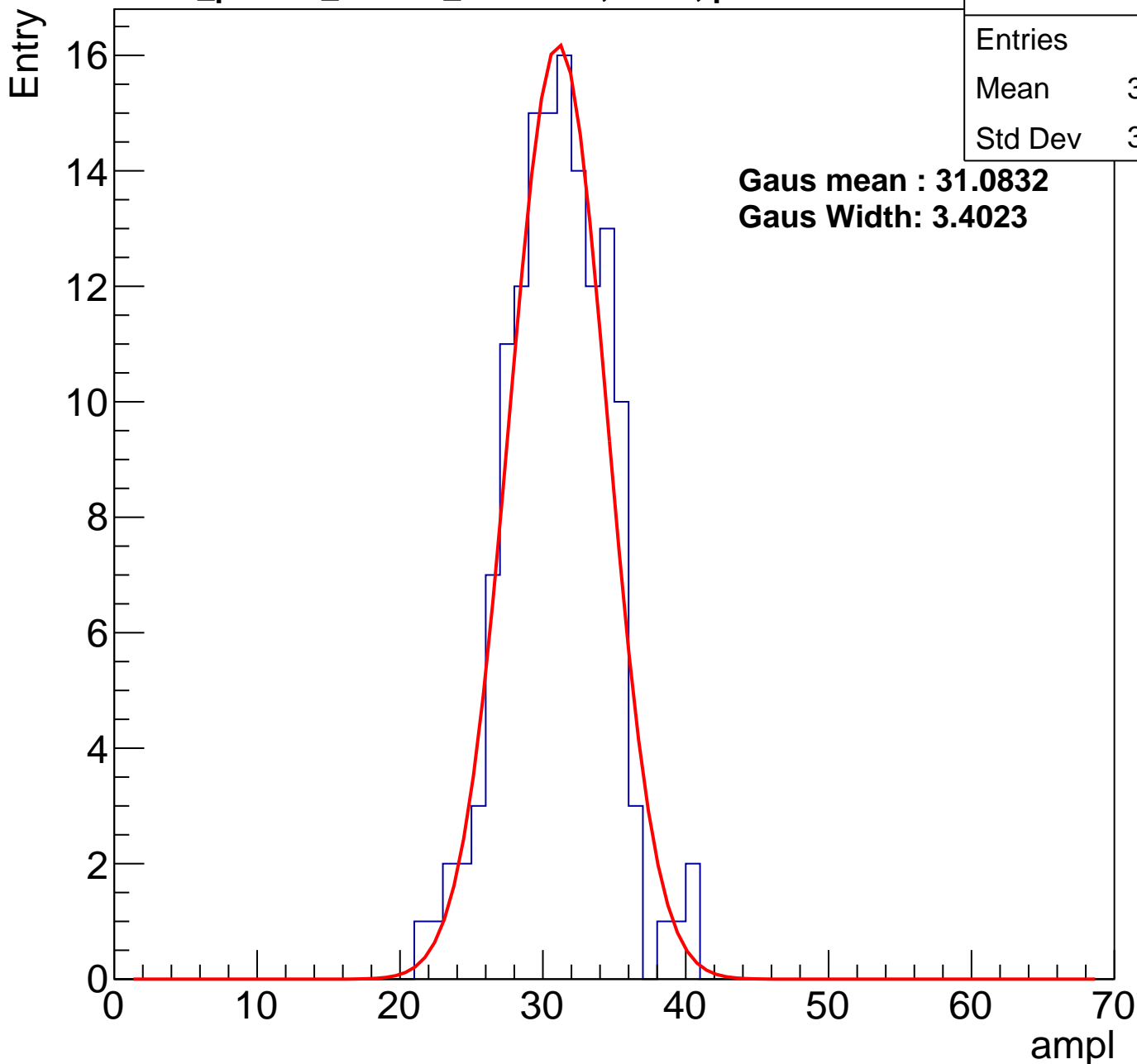
B0L101S, U24-ch13, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	30.56
Std Dev	3.483

Gaus mean : 31.0832

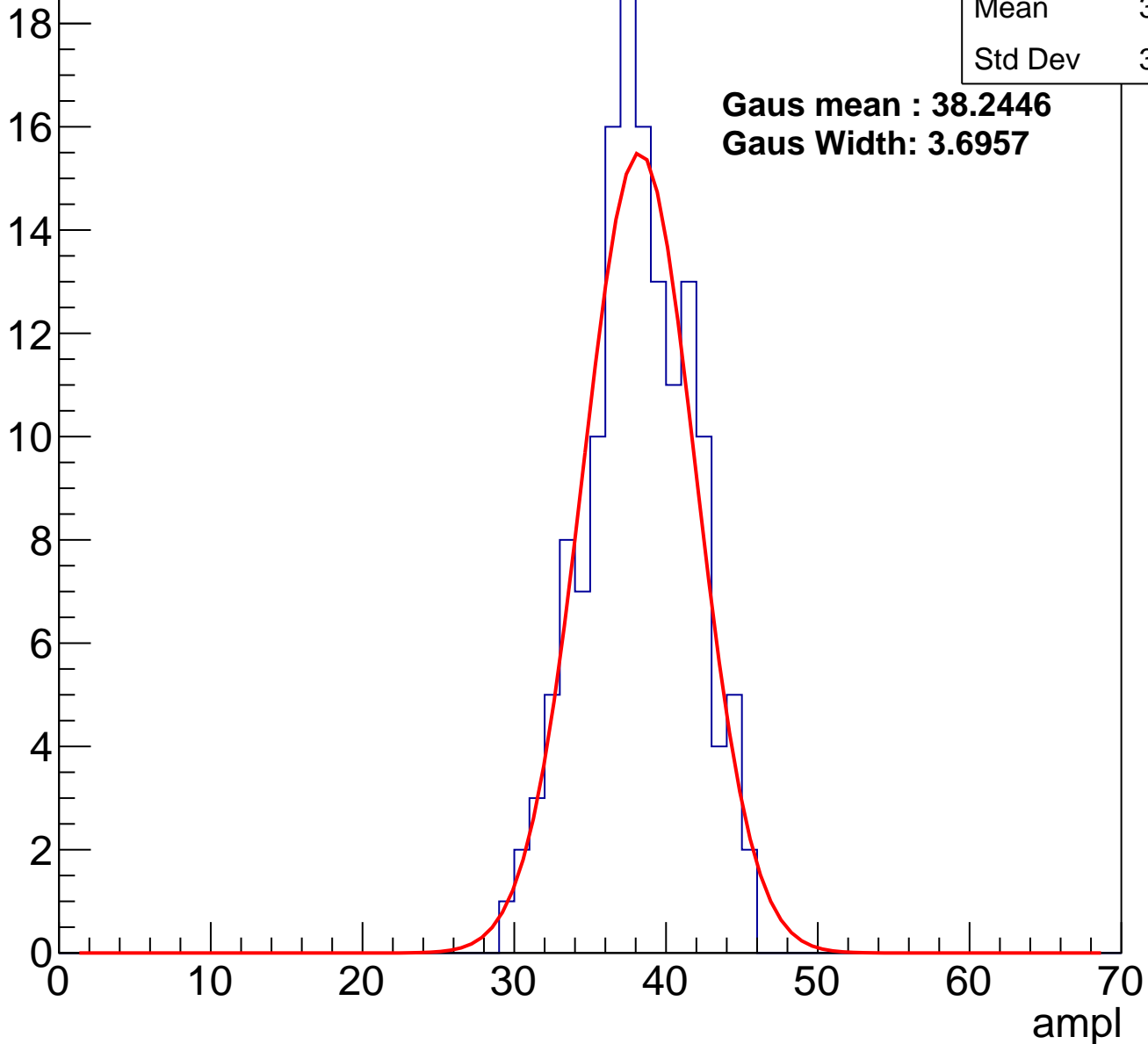
Gaus Width: 3.4023



B0L101S, U24-ch13, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch13, adc2

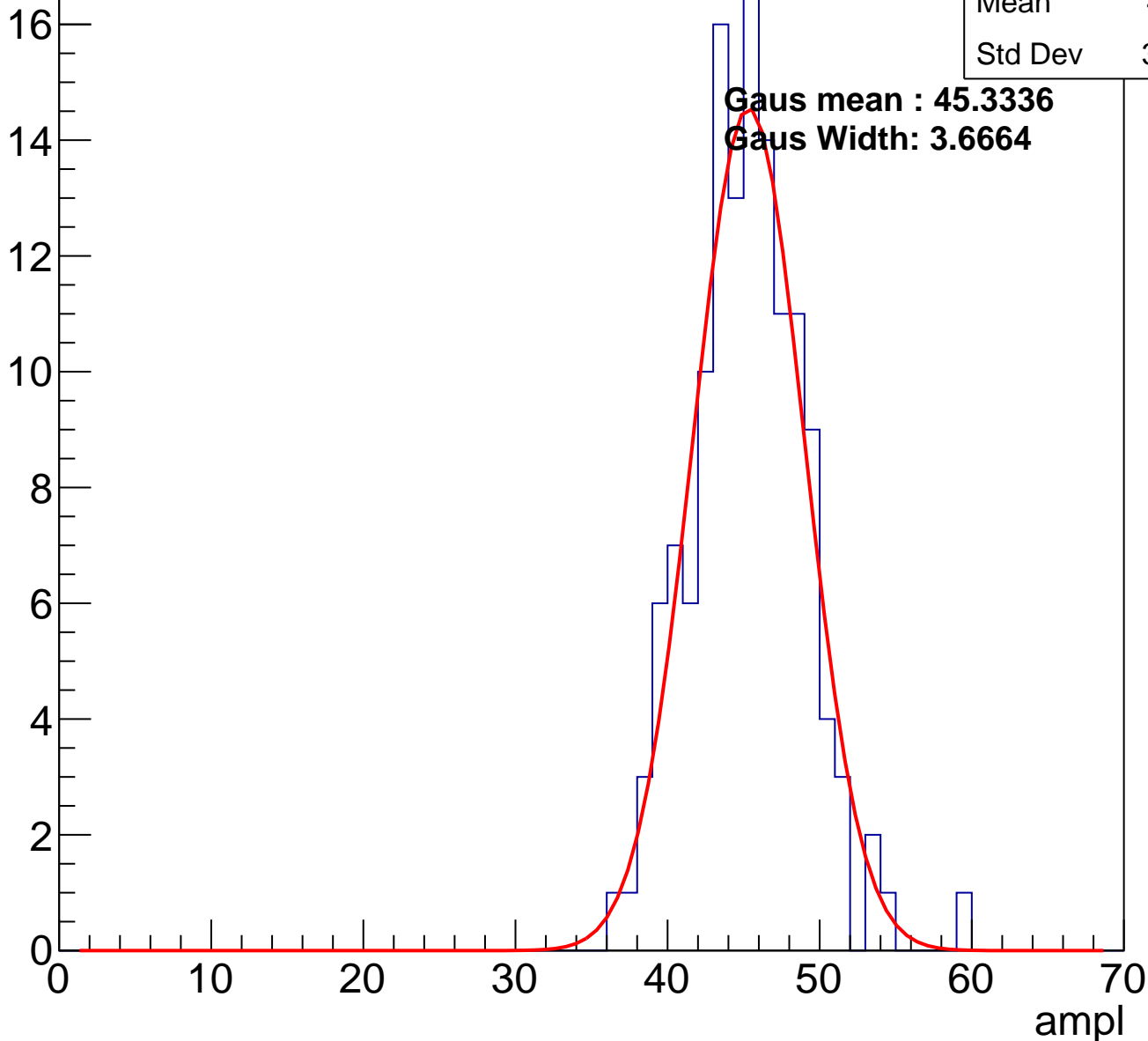
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	136
Mean	44.81
Std Dev	3.693

Gaus mean : 45.3336

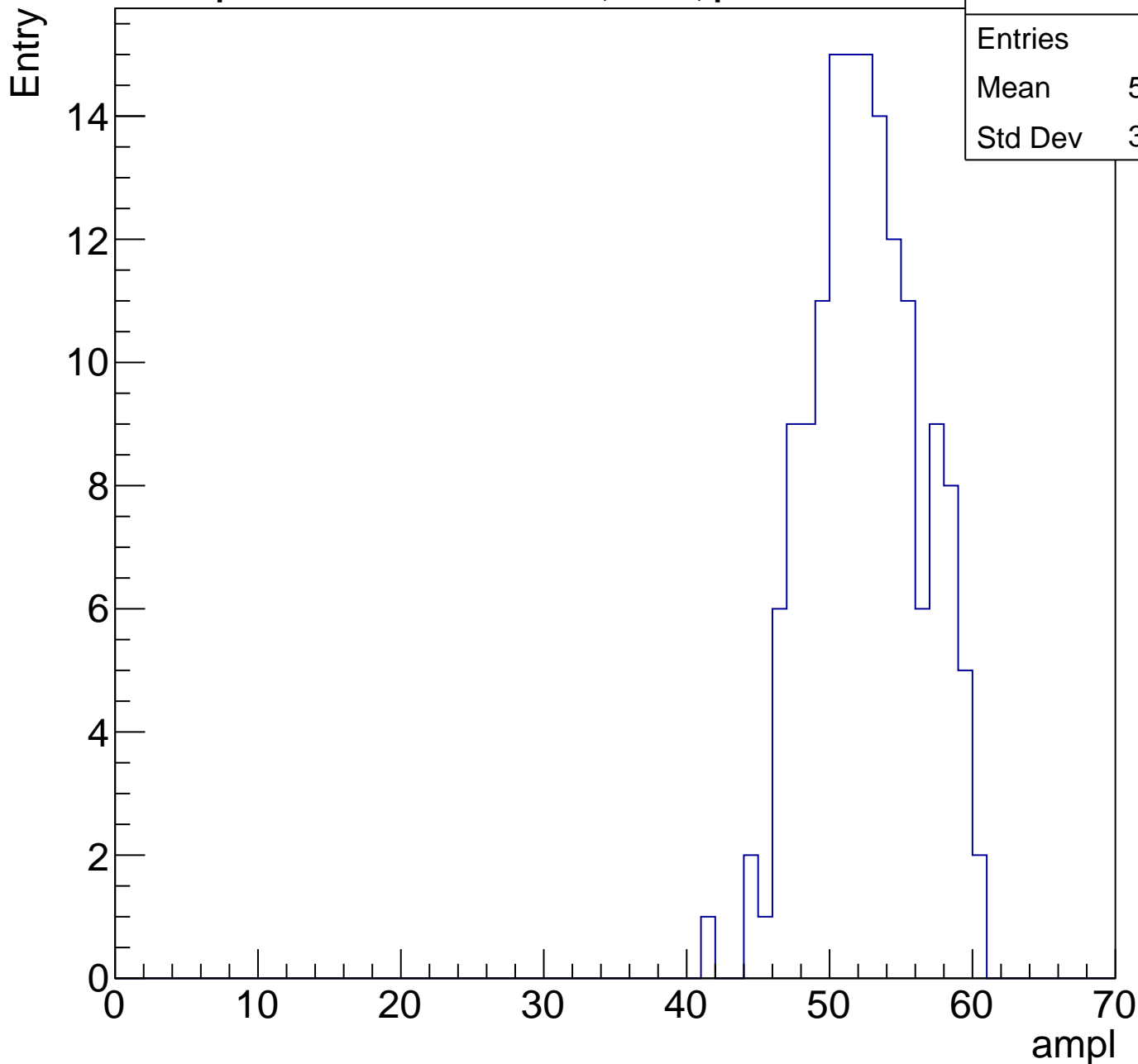
Gaus Width: 3.6664



B0L101S, U24-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	52.07
Std Dev	3.816



B0L101S, U24-ch13, adc4

calib_packv5_042523_0143.root, FC#1, port C1

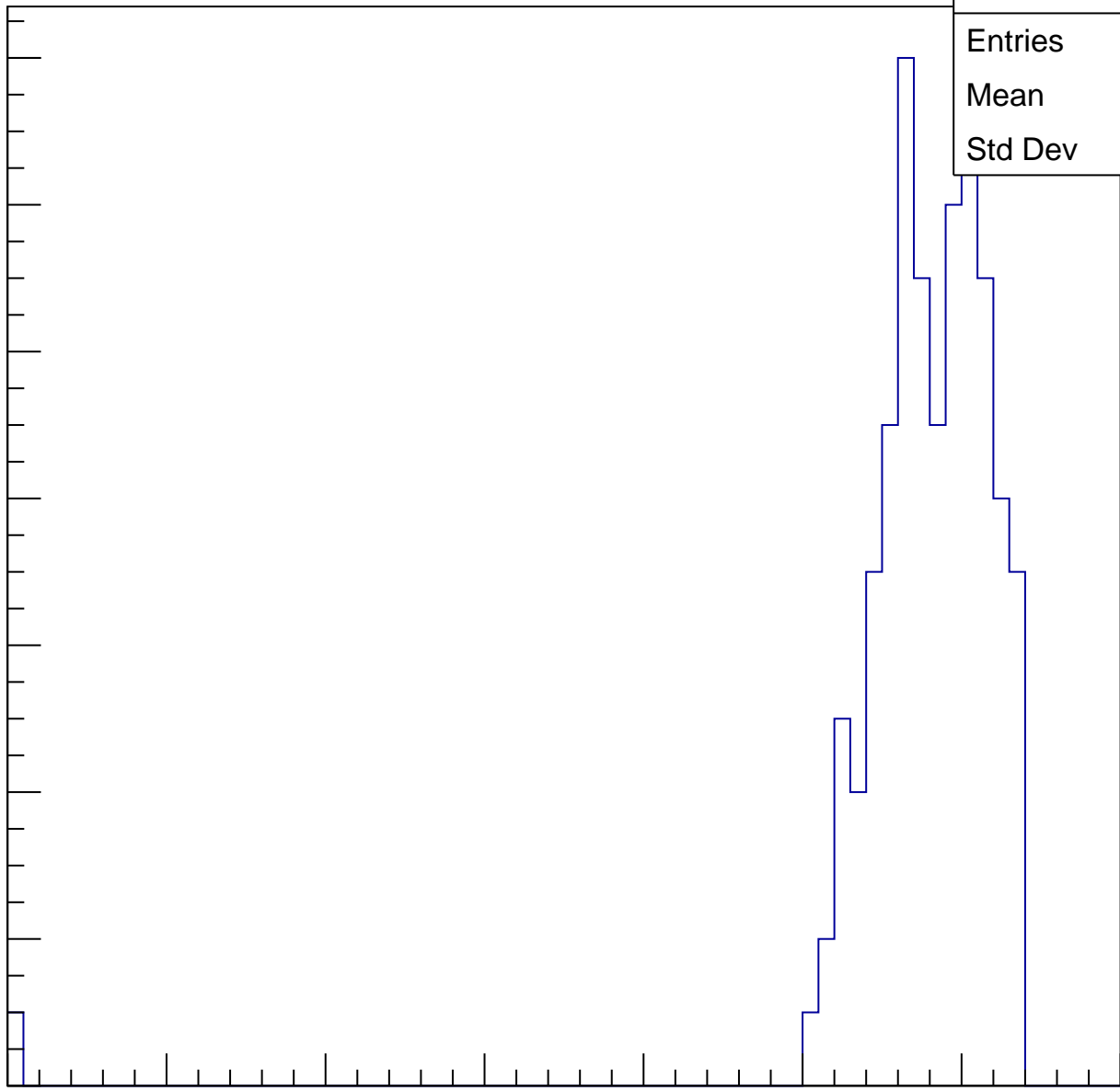
Entries	115
Mean	57.27
Std Dev	6.238

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch13, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

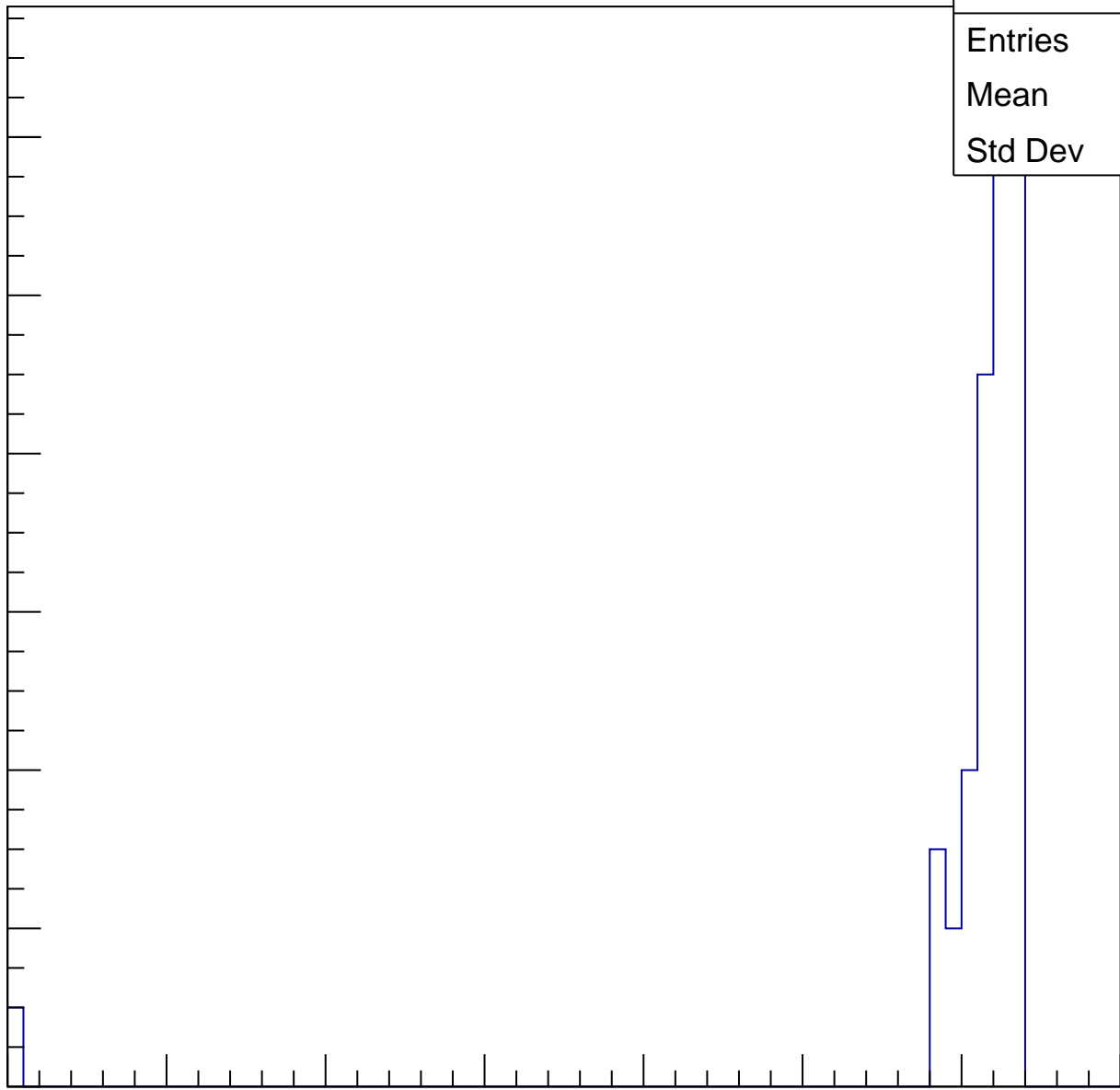
2

0

Entries	44
Mean	60.09
Std Dev	9.278

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L101S, U24-ch14, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	29.5
Std Dev	3.459

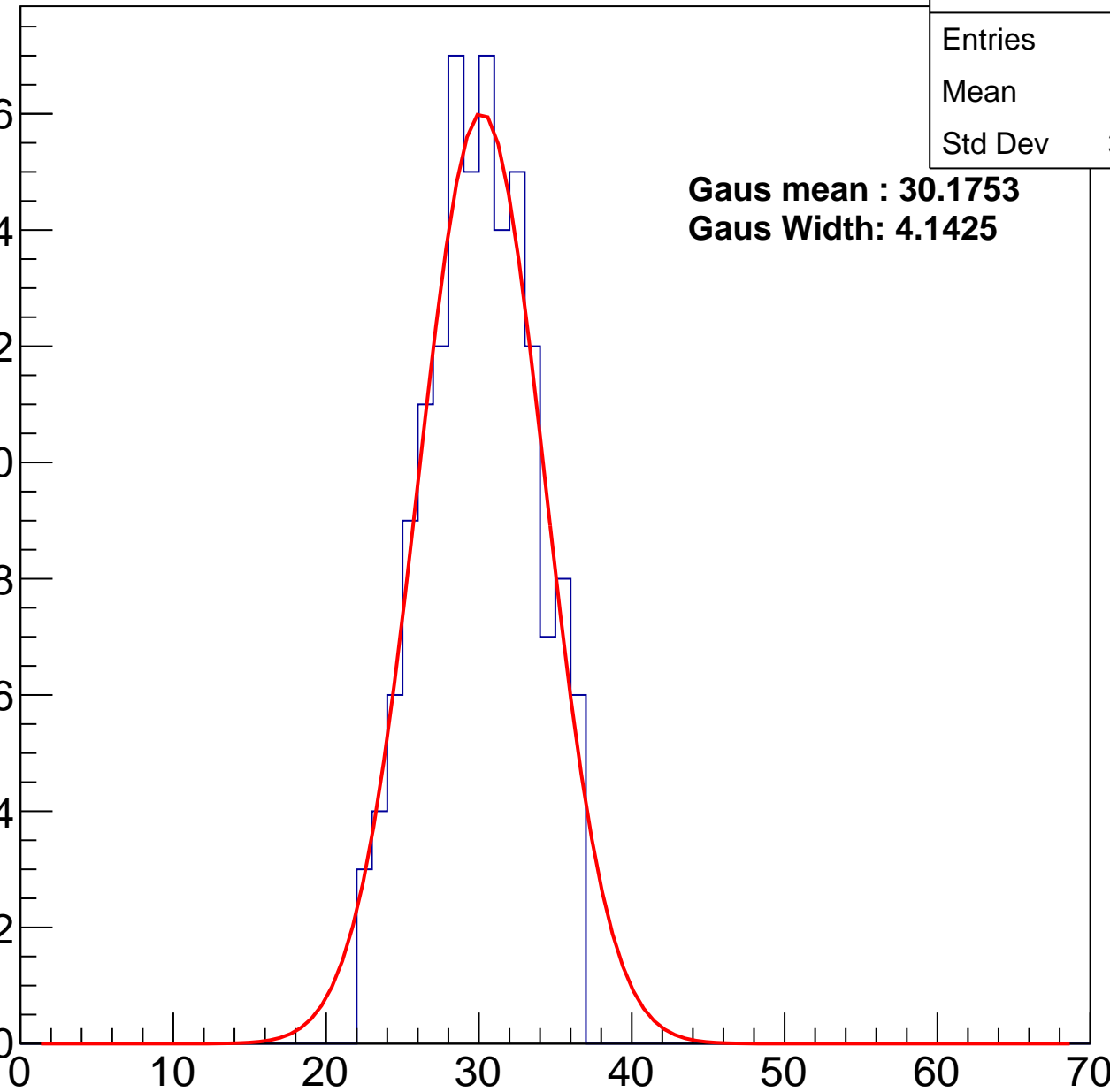
Gaus mean : 30.1753

Gaus Width: 4.1425

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch14, adc1

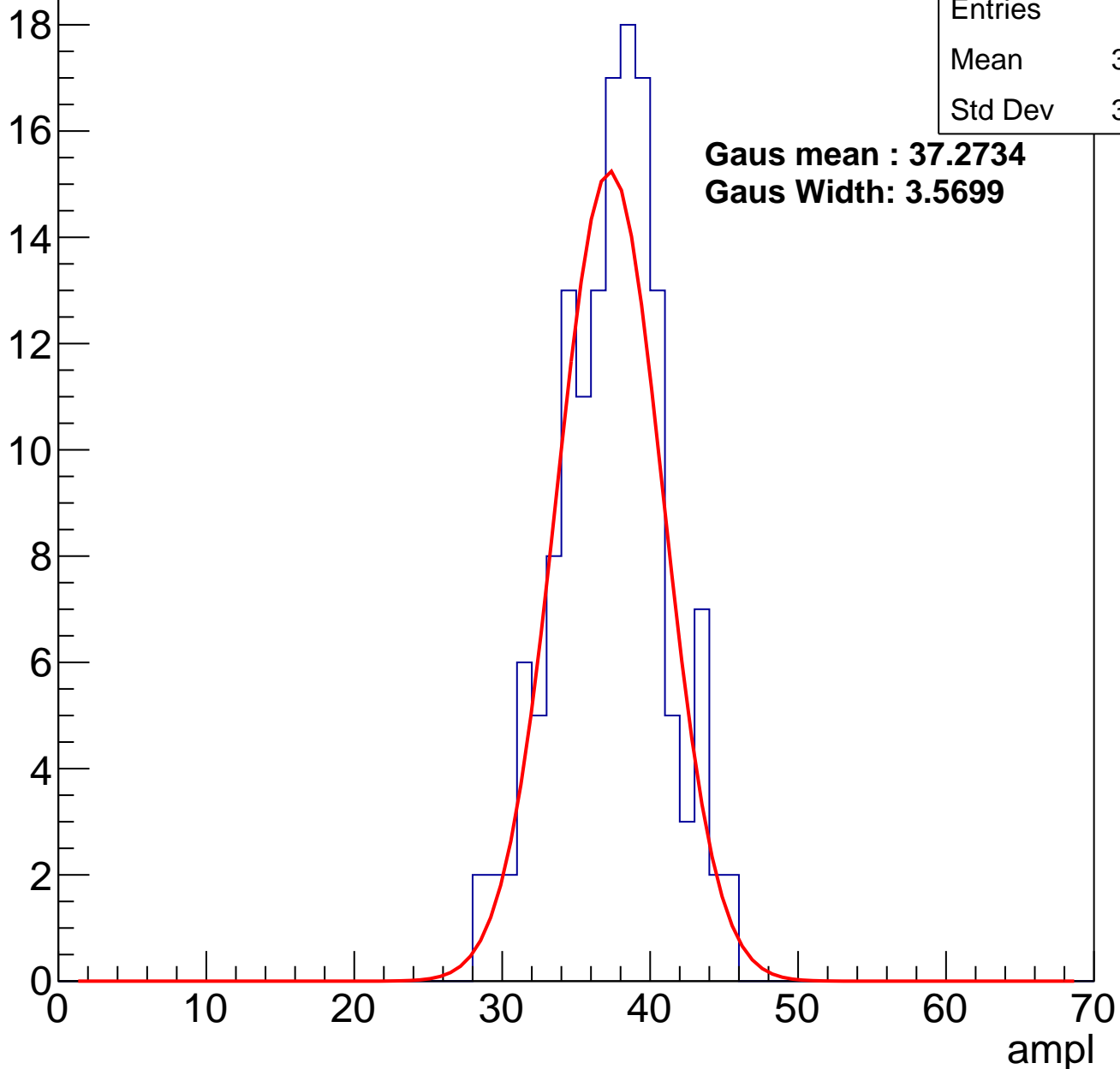
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	36.88
Std Dev	3.574

Gaus mean : 37.2734

Gaus Width: 3.5699

Entry



B0L101S, U24-ch14, adc2

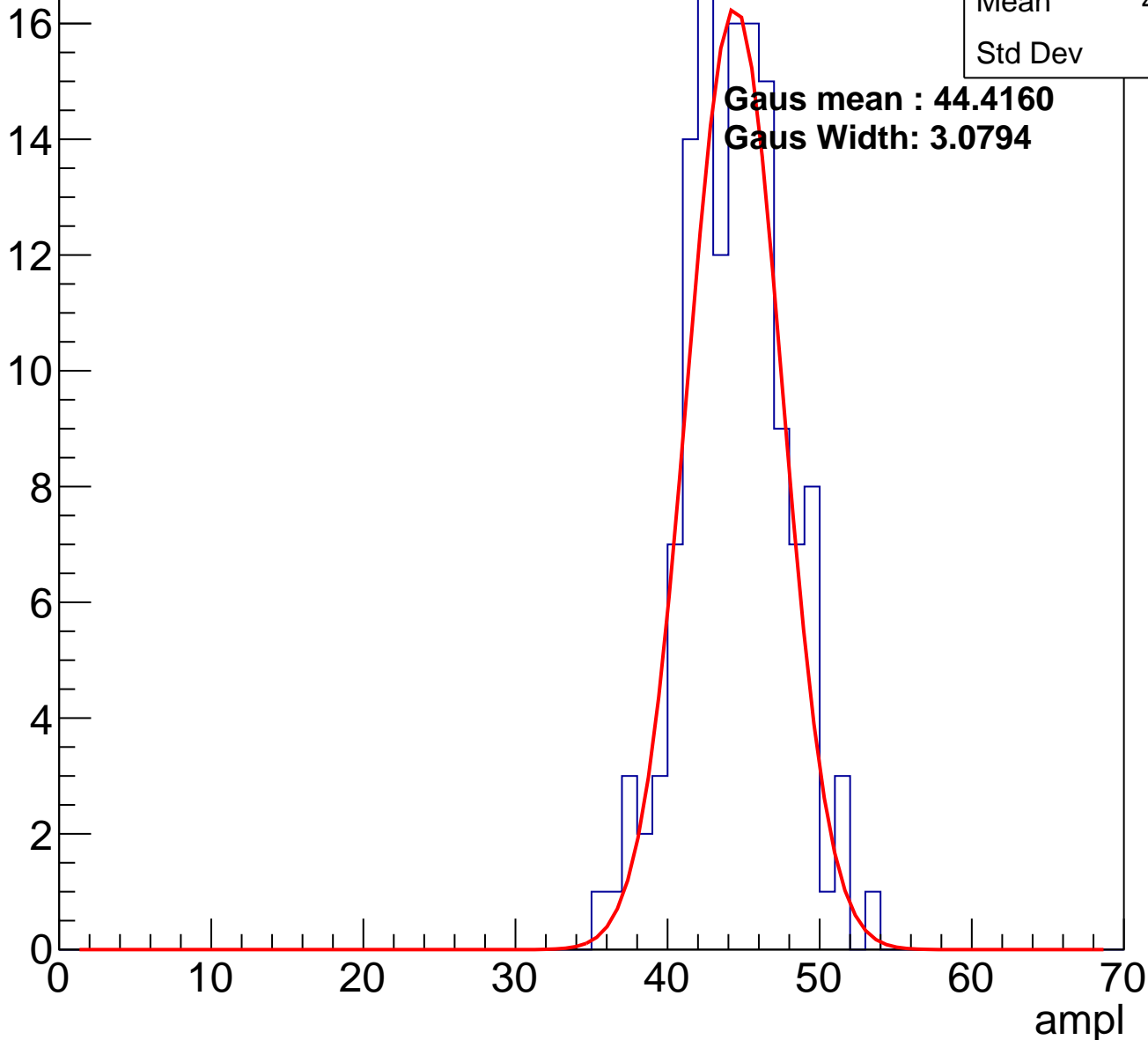
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	136
Mean	43.97
Std Dev	3.31

Gaus mean : 44.4160

Gaus Width: 3.0794



B0L101S, U24-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

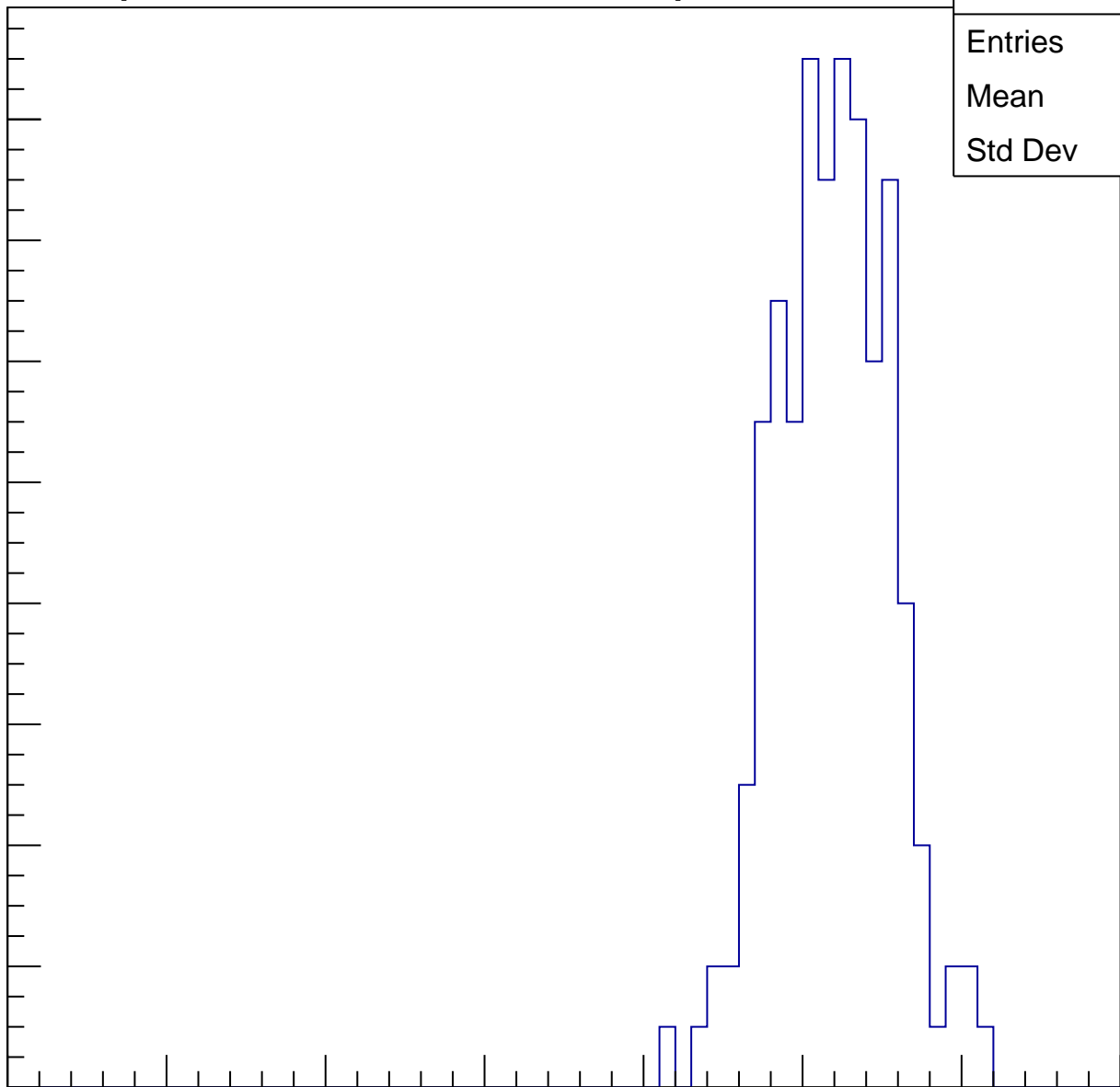
Entries	156
Mean	51.44
Std Dev	3.576

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch14, adc4

calib_packv5_042523_0143.root, FC#1, port C1

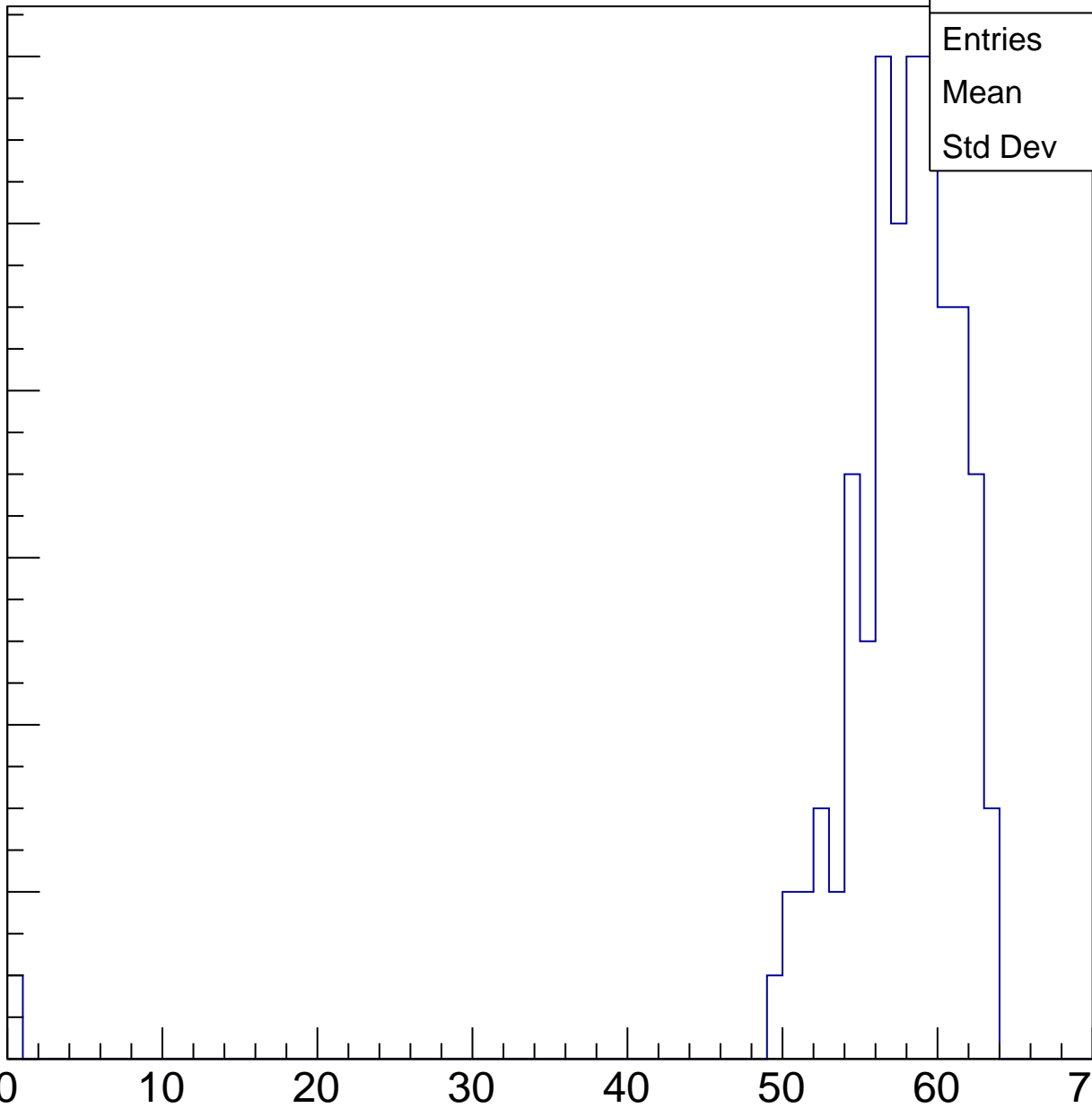
Entries	97
Mean	56.95
Std Dev	6.621

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch14, adc5

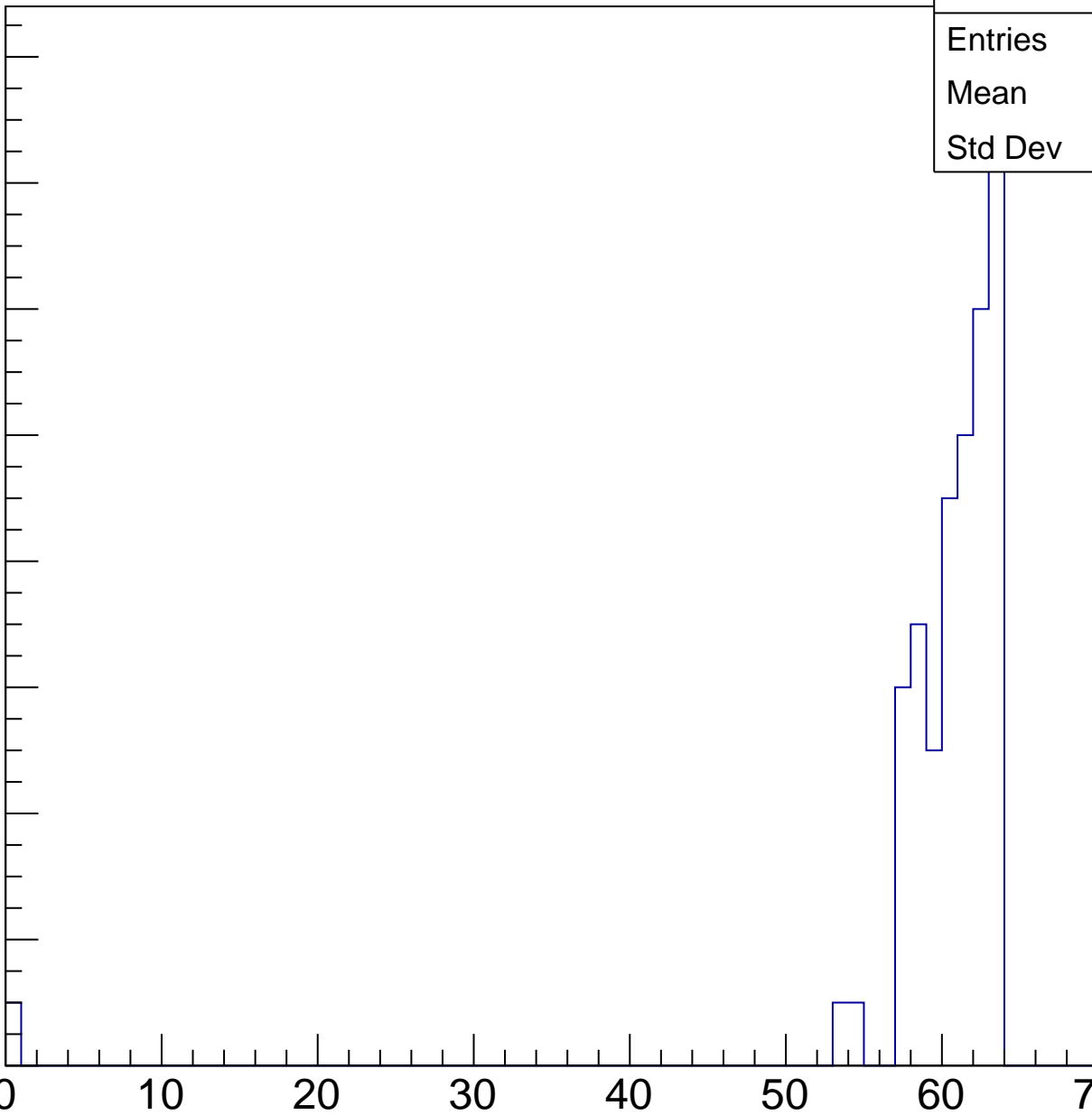
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	68
Mean	59.59
Std Dev	7.634

ampl



B0L101S, U24-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch15, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	28.9
Std Dev	4.318

Gaus mean : 29.7101

Gaus Width: 3.2712

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

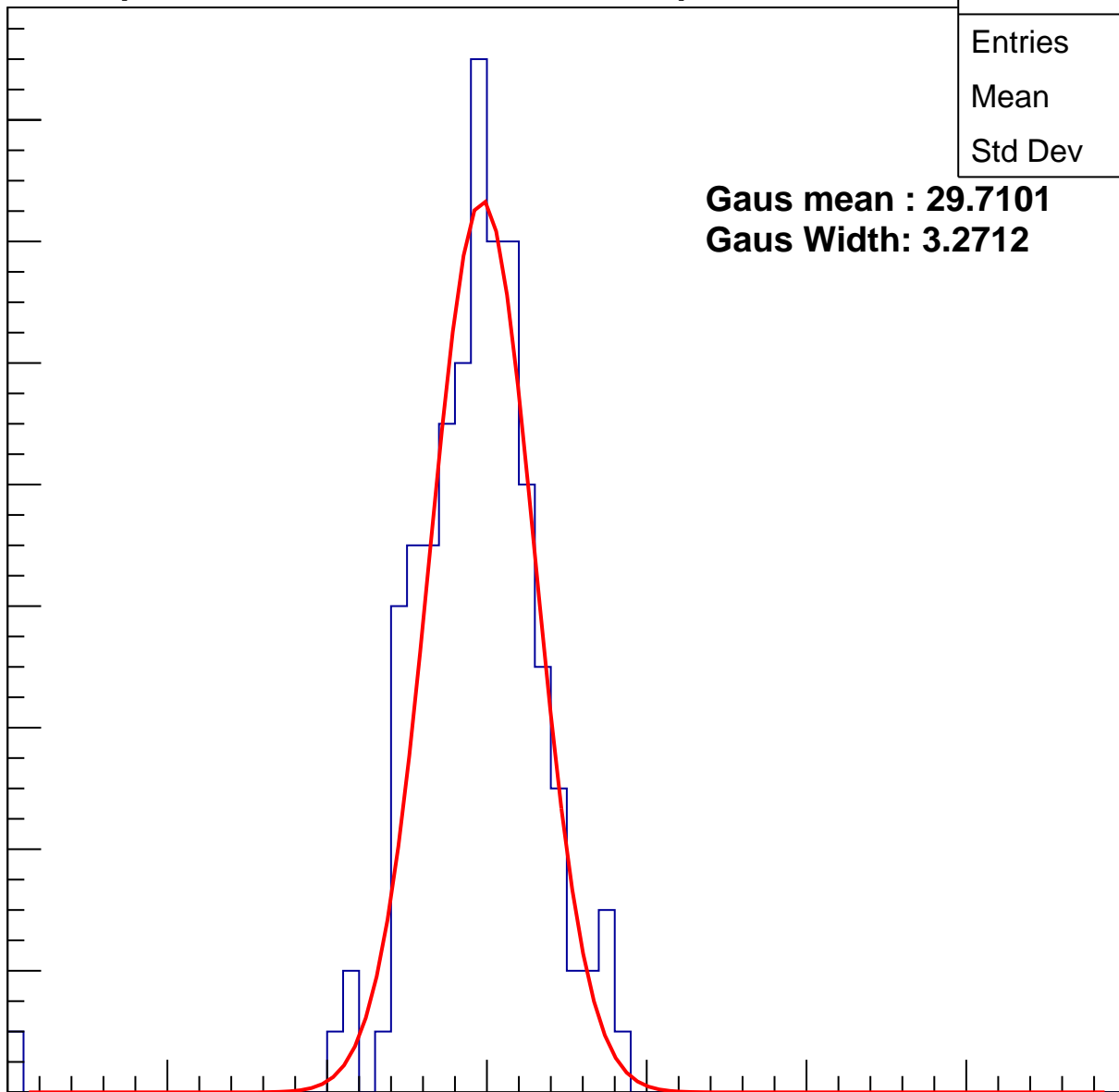
40

50

60

70

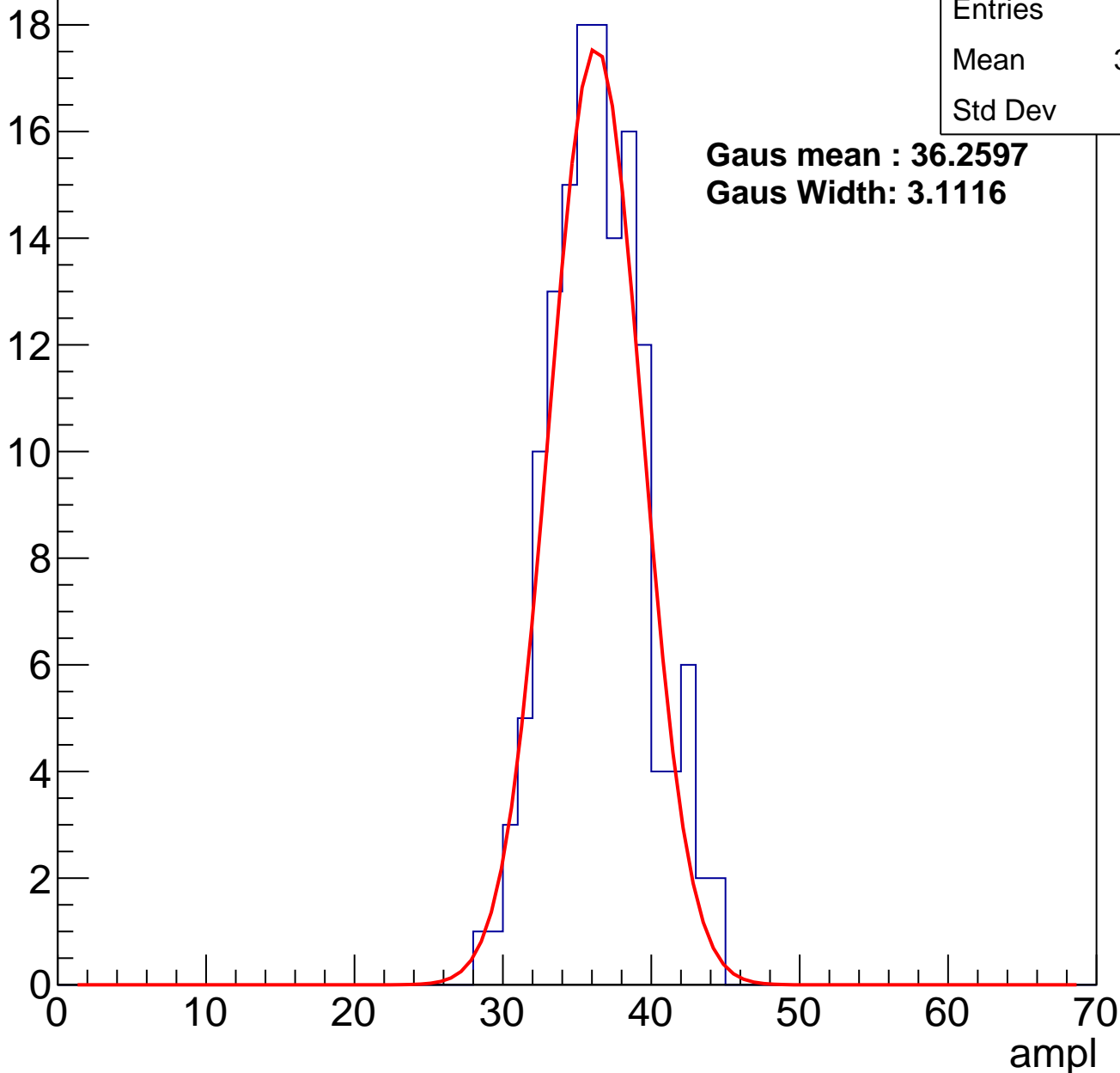
ampl



B0L101S, U24-ch15, adc1

calib_packv5_042523_0143.root, FC#1, port C1

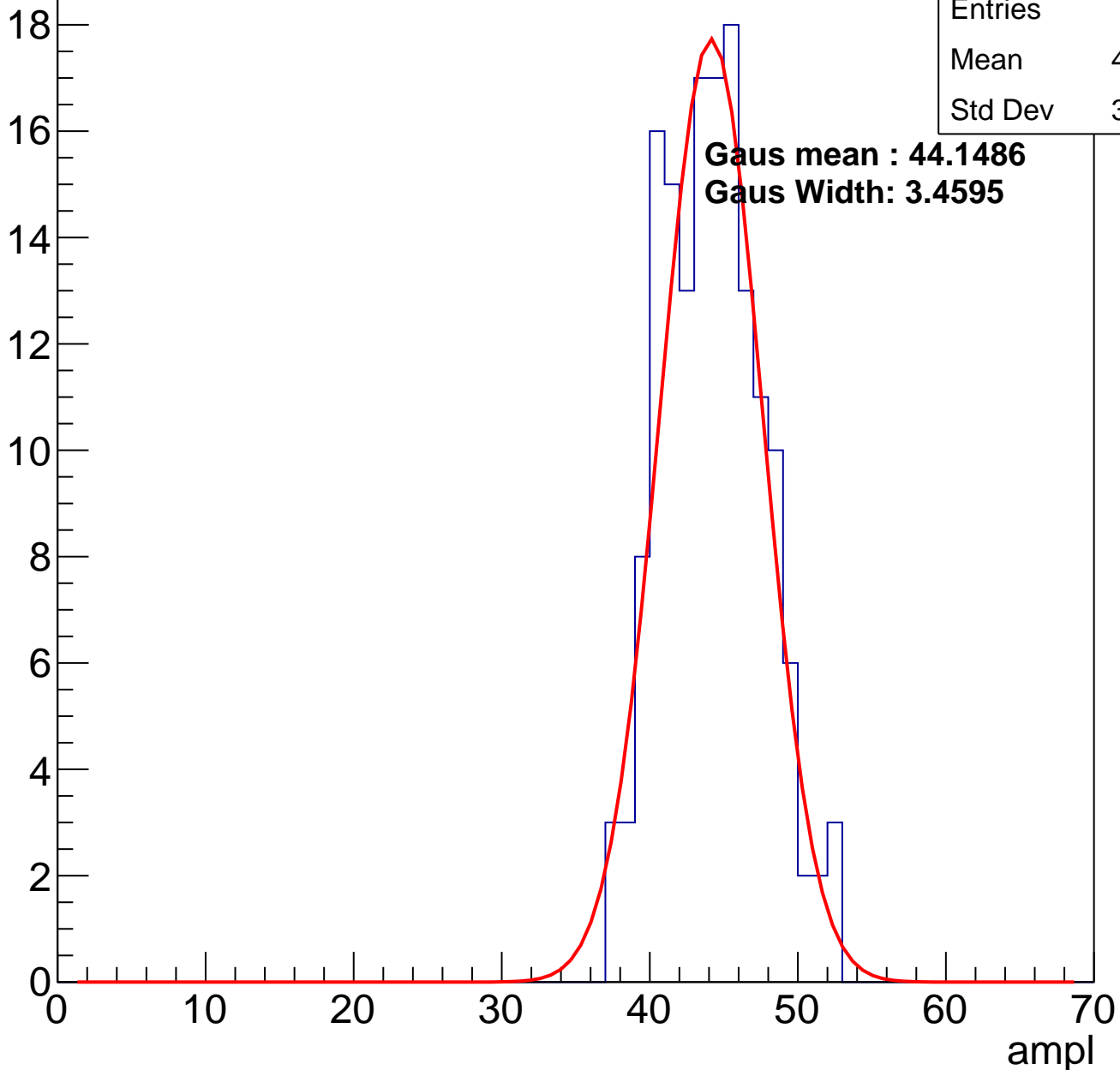
Entry



B0L101S, U24-ch15, adc2

calib_packv5_042523_0143.root, FC#1, port C1

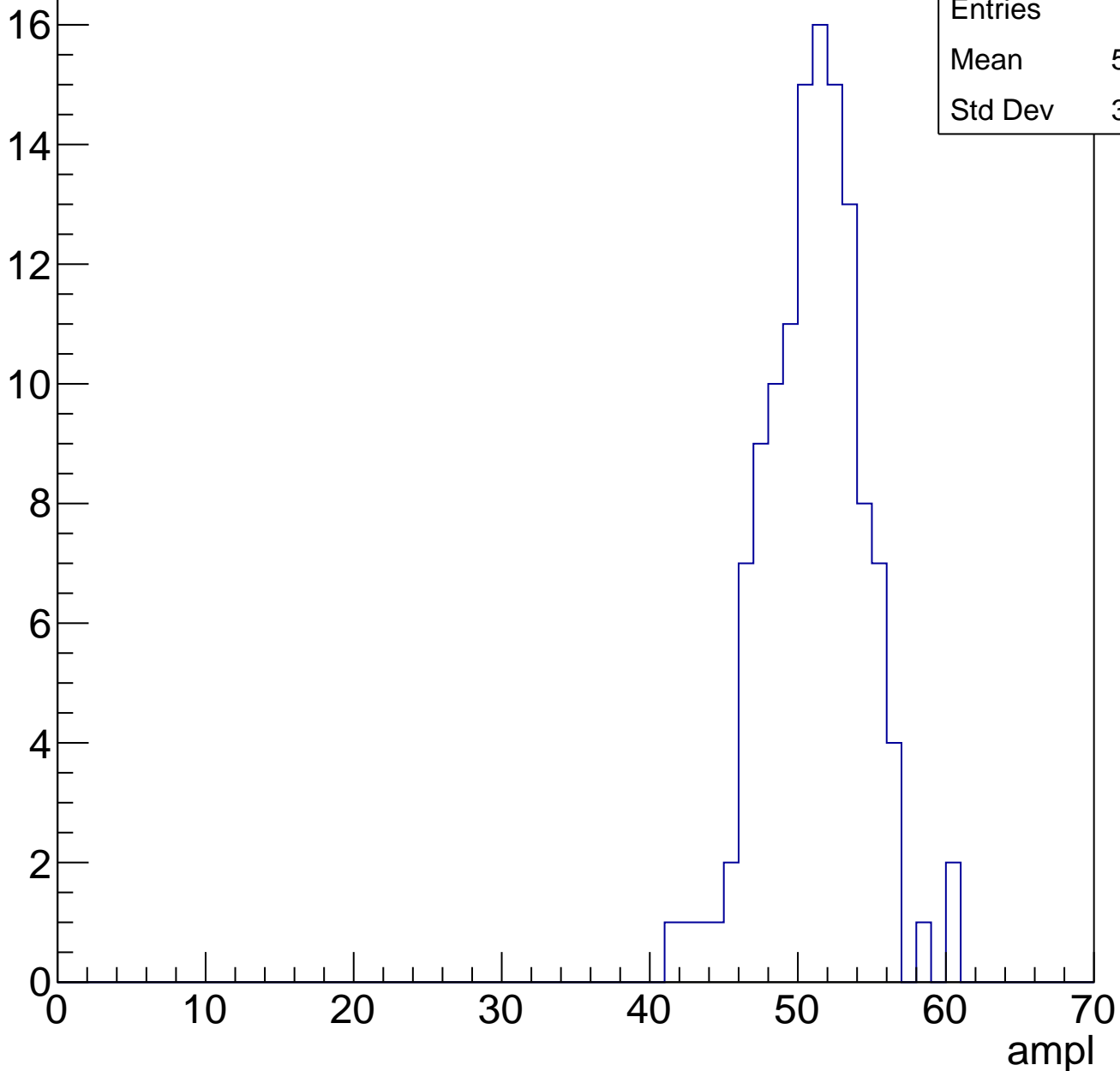
Entry



B0L101S, U24-ch15, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch15, adc4

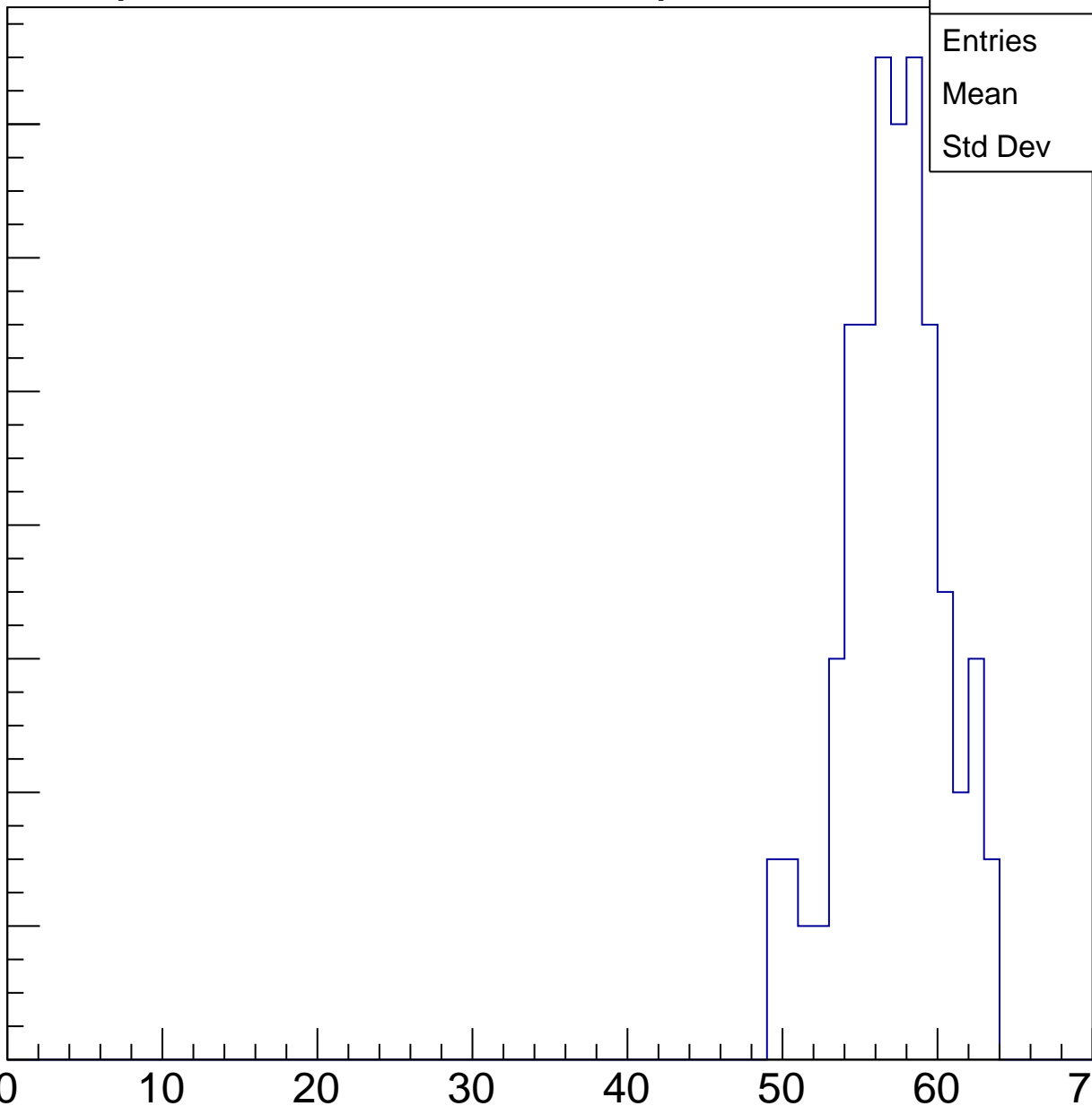
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	113
Mean	56.65
Std Dev	3.184

ampl



B0L101S, U24-ch15, adc5

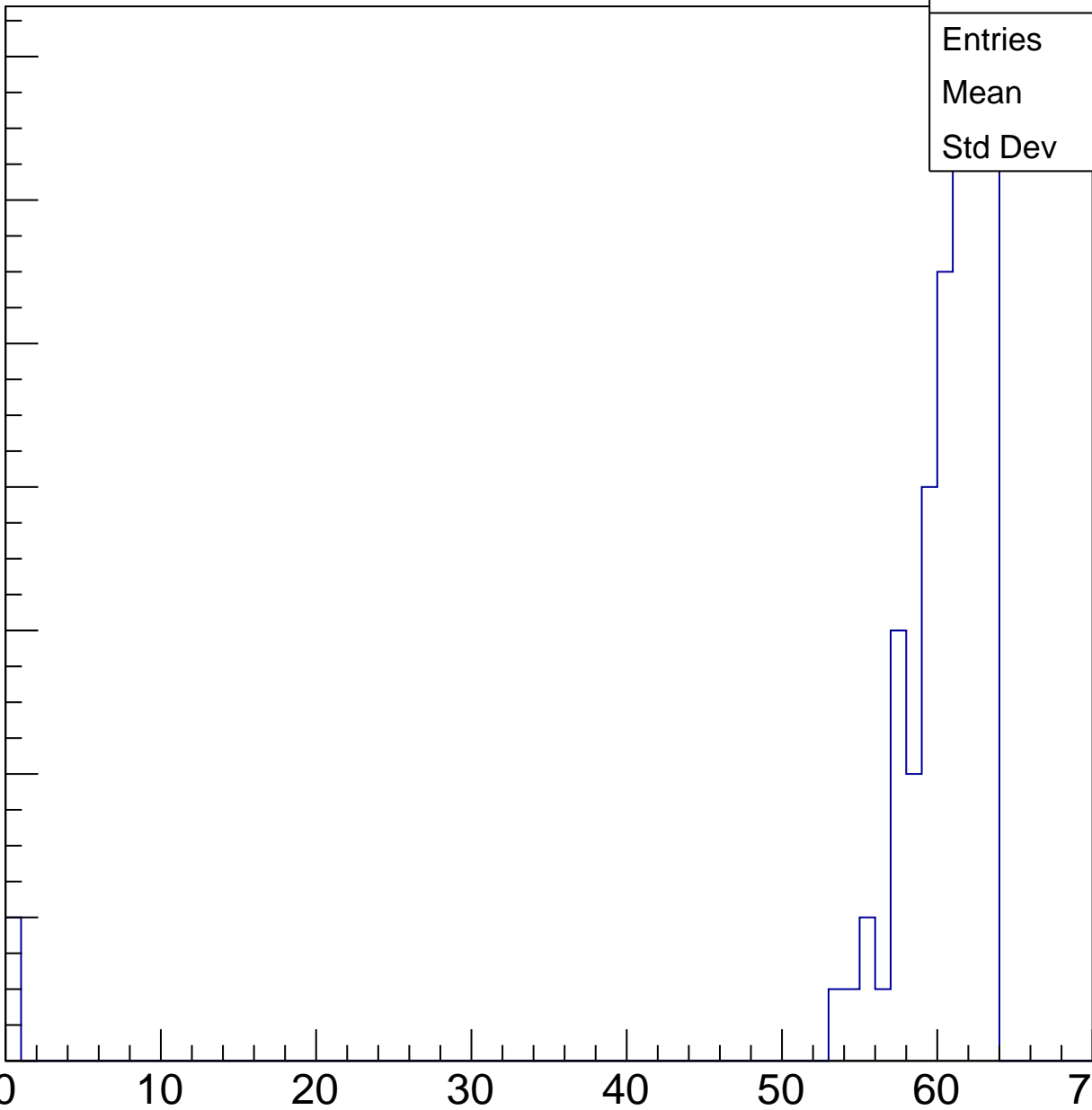
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	58.76
Std Dev	9.809

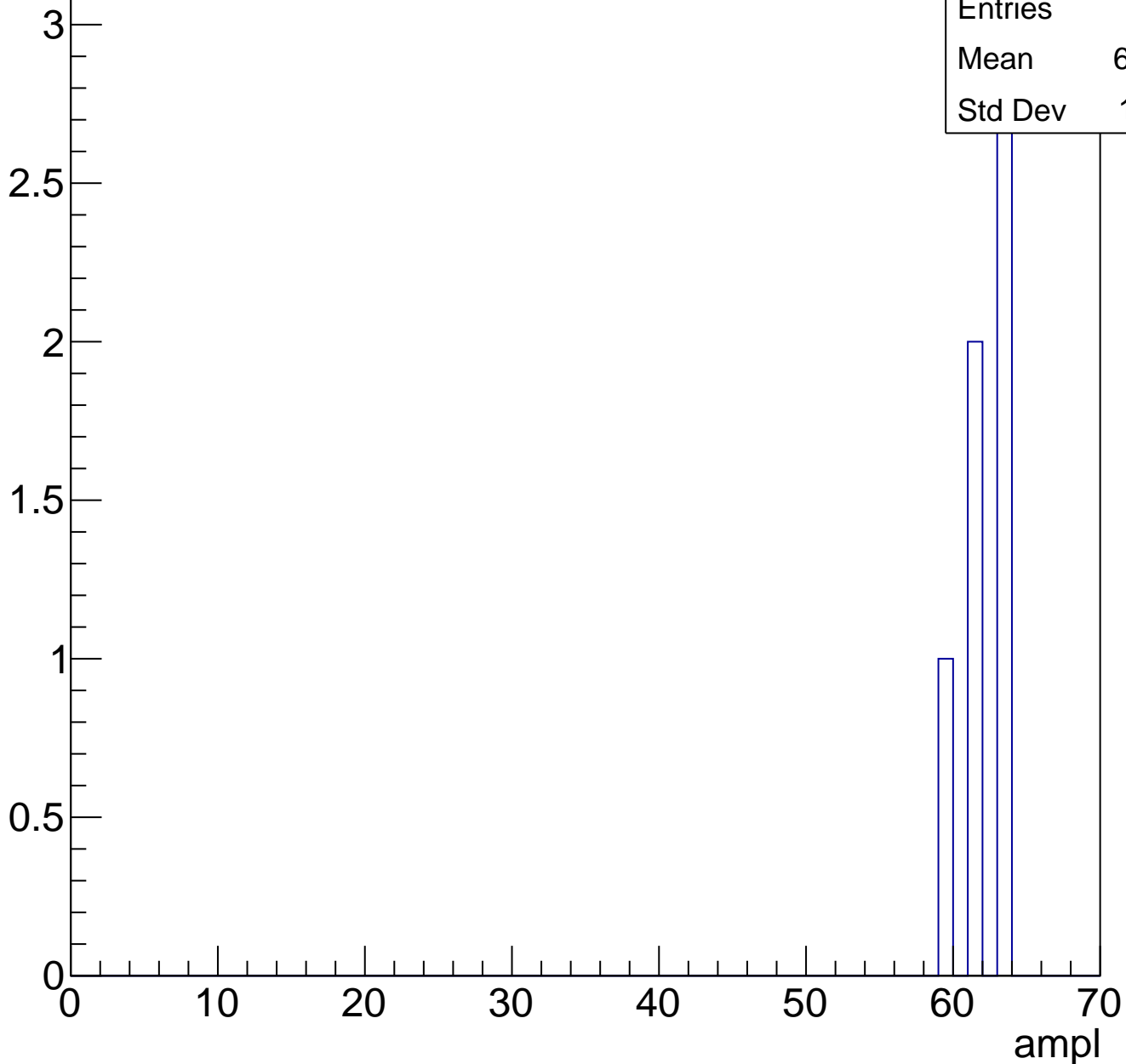
ampl



B0L101S, U24-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch16, adc0

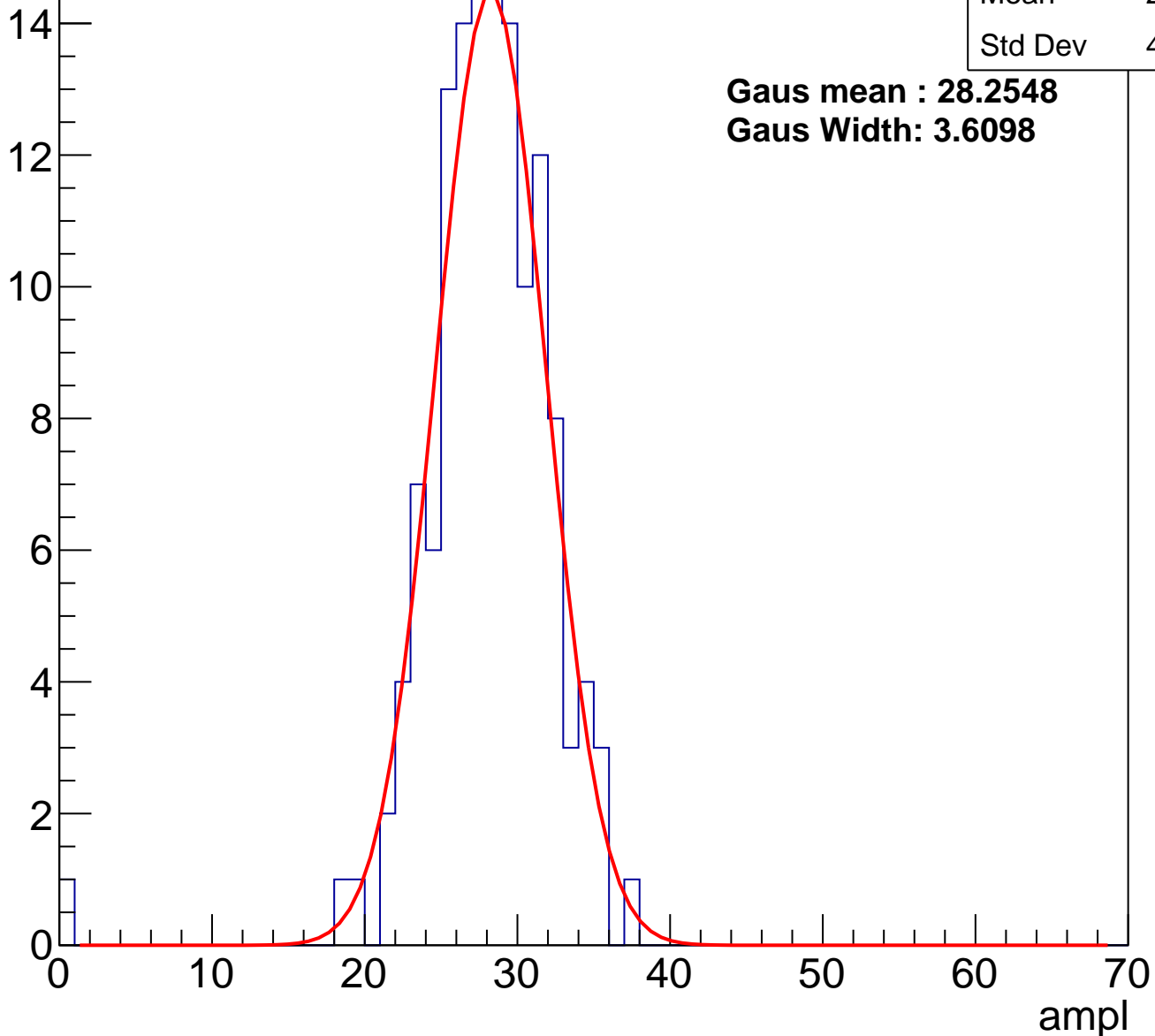
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	27.59
Std Dev	4.215

Gaus mean : 28.2548

Gaus Width: 3.6098

Entry



B0L101S, U24-ch16, adc1

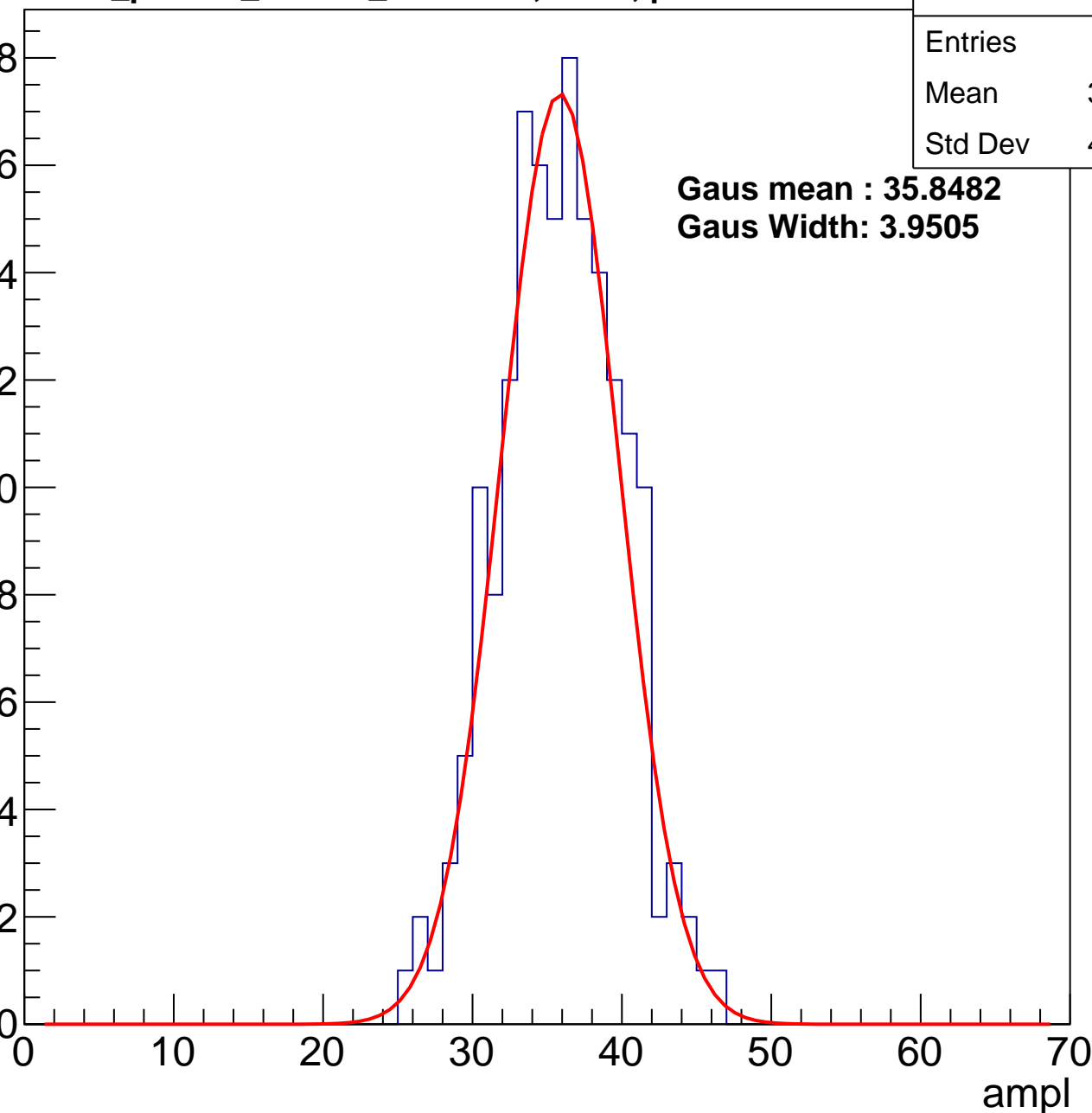
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	179
Mean	35.42
Std Dev	4.022

Gaus mean : 35.8482
Gaus Width: 3.9505



B0L101S, U24-ch16, adc2

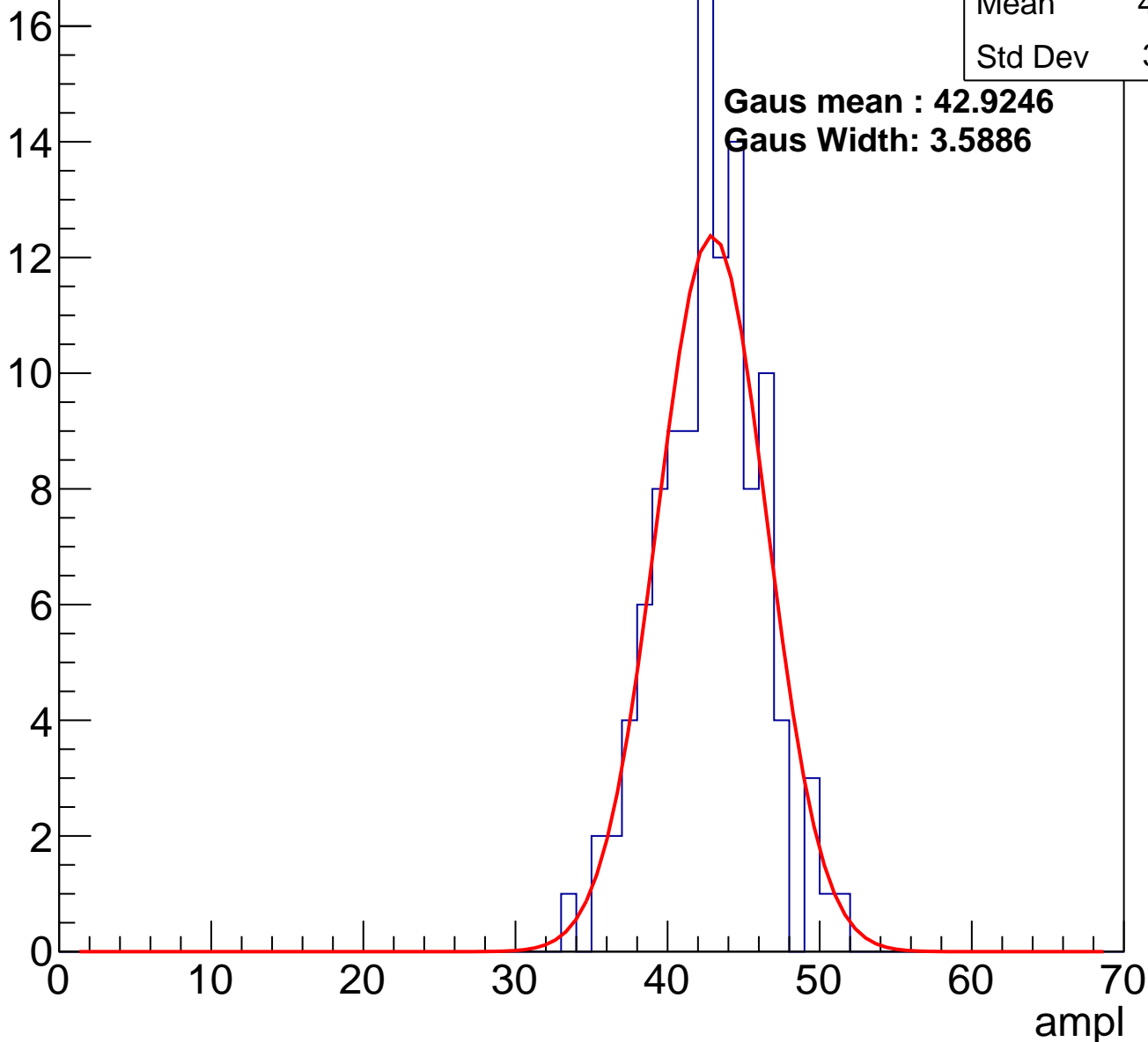
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	111
Mean	42.29
Std Dev	3.351

Gaus mean : 42.9246

Gaus Width: 3.5886



B0L101S, U24-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

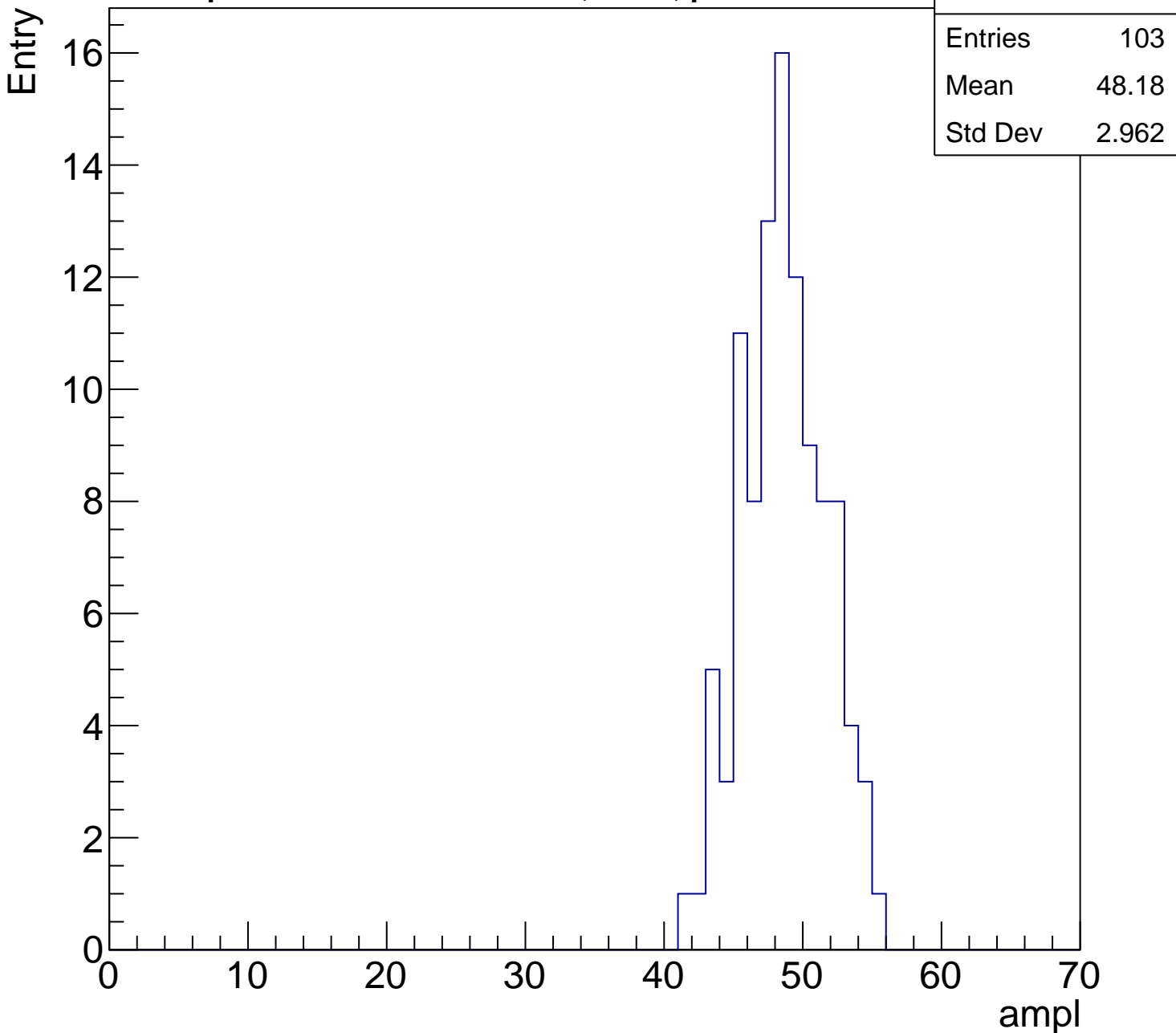
Entries	103
Mean	48.18
Std Dev	2.962

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch16, adc4

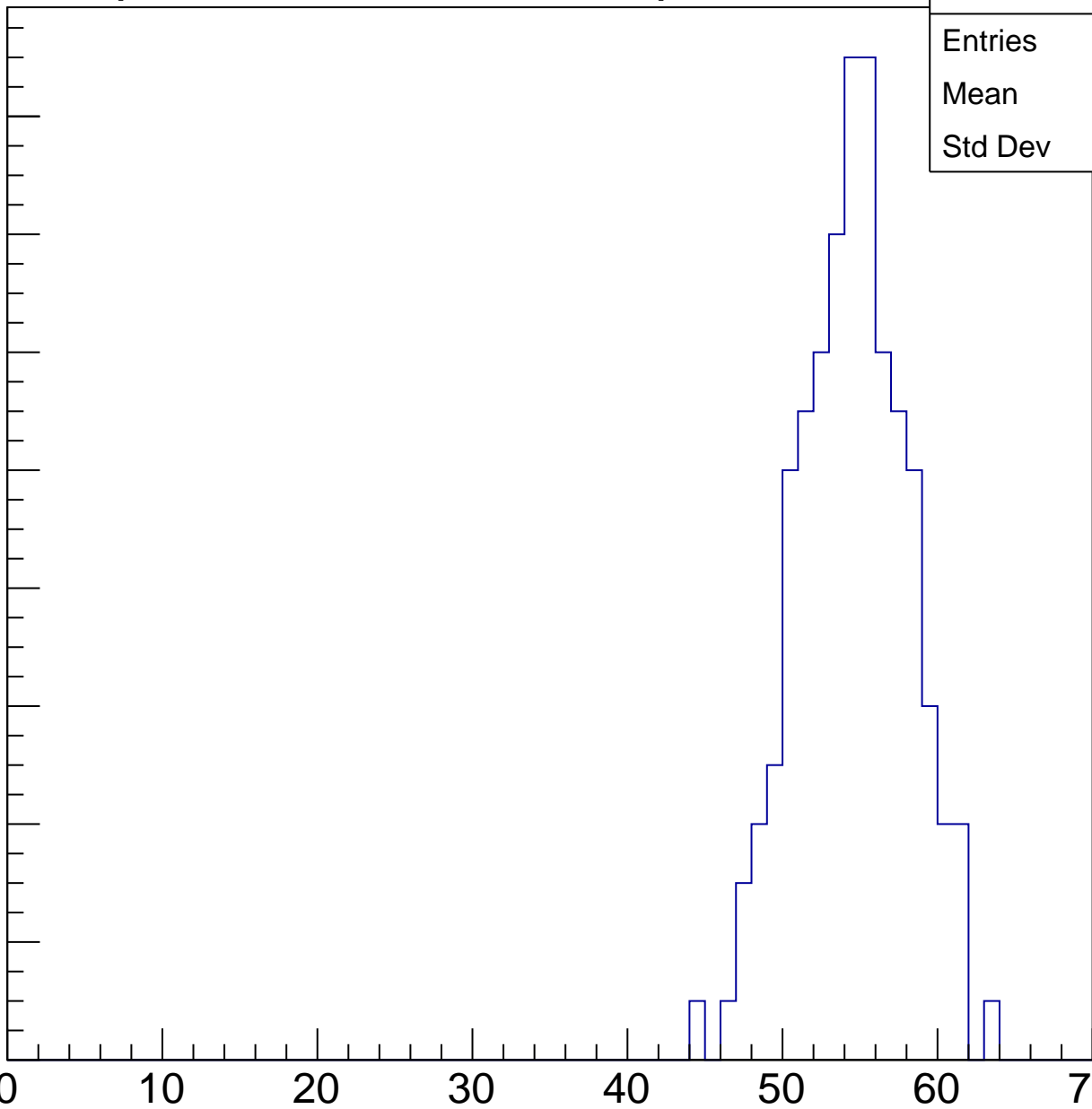
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	143
Mean	54.04
Std Dev	3.544

ampl



B0L101S, U24-ch16, adc5

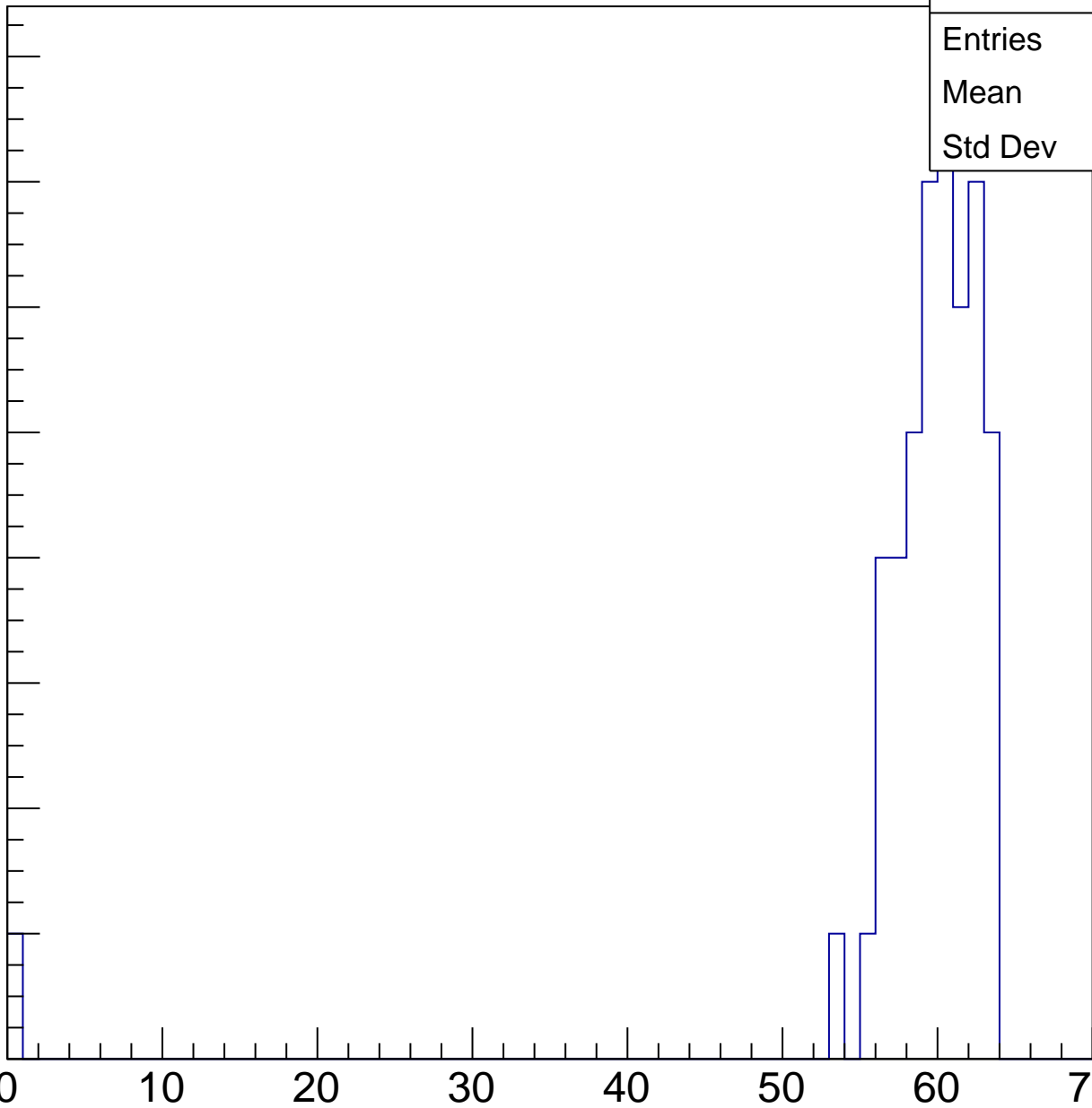
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.33
Std Dev	8.74

ampl

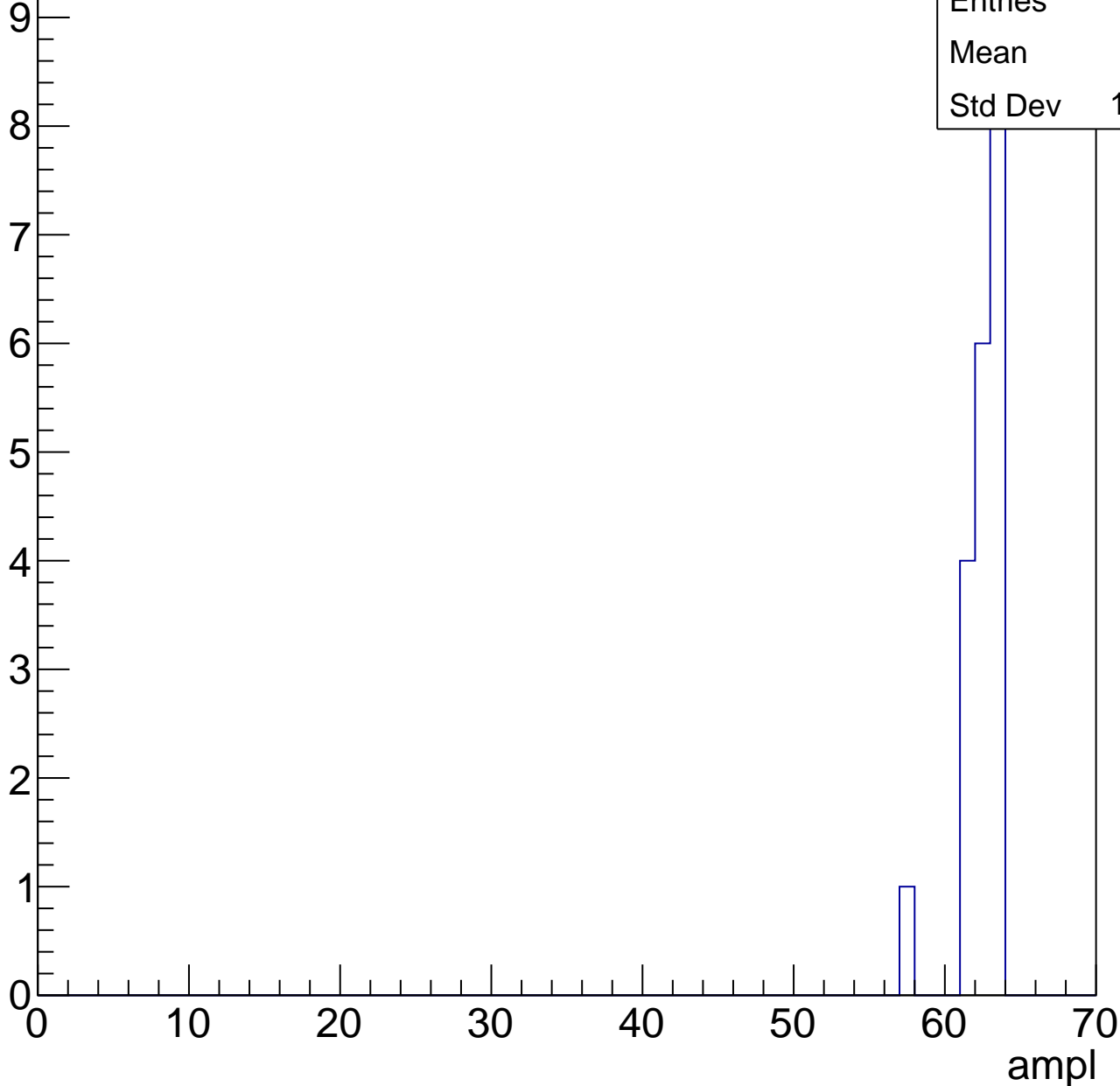


B0L101S, U24-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	62
Std Dev	1.378



B0L101S, U24-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch17, adc0

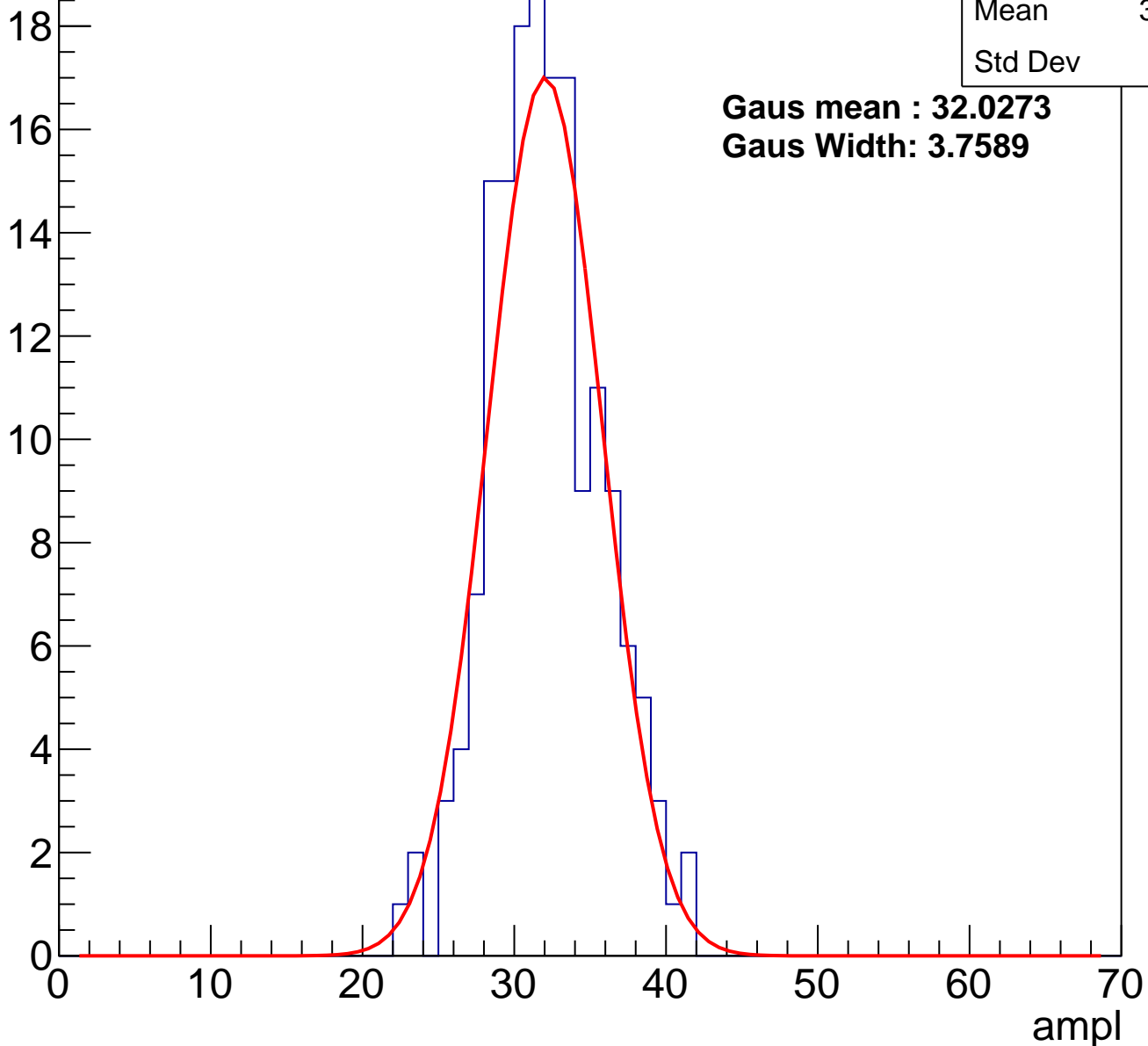
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	31.65
Std Dev	3.64

Gaus mean : 32.0273

Gaus Width: 3.7589

Entry

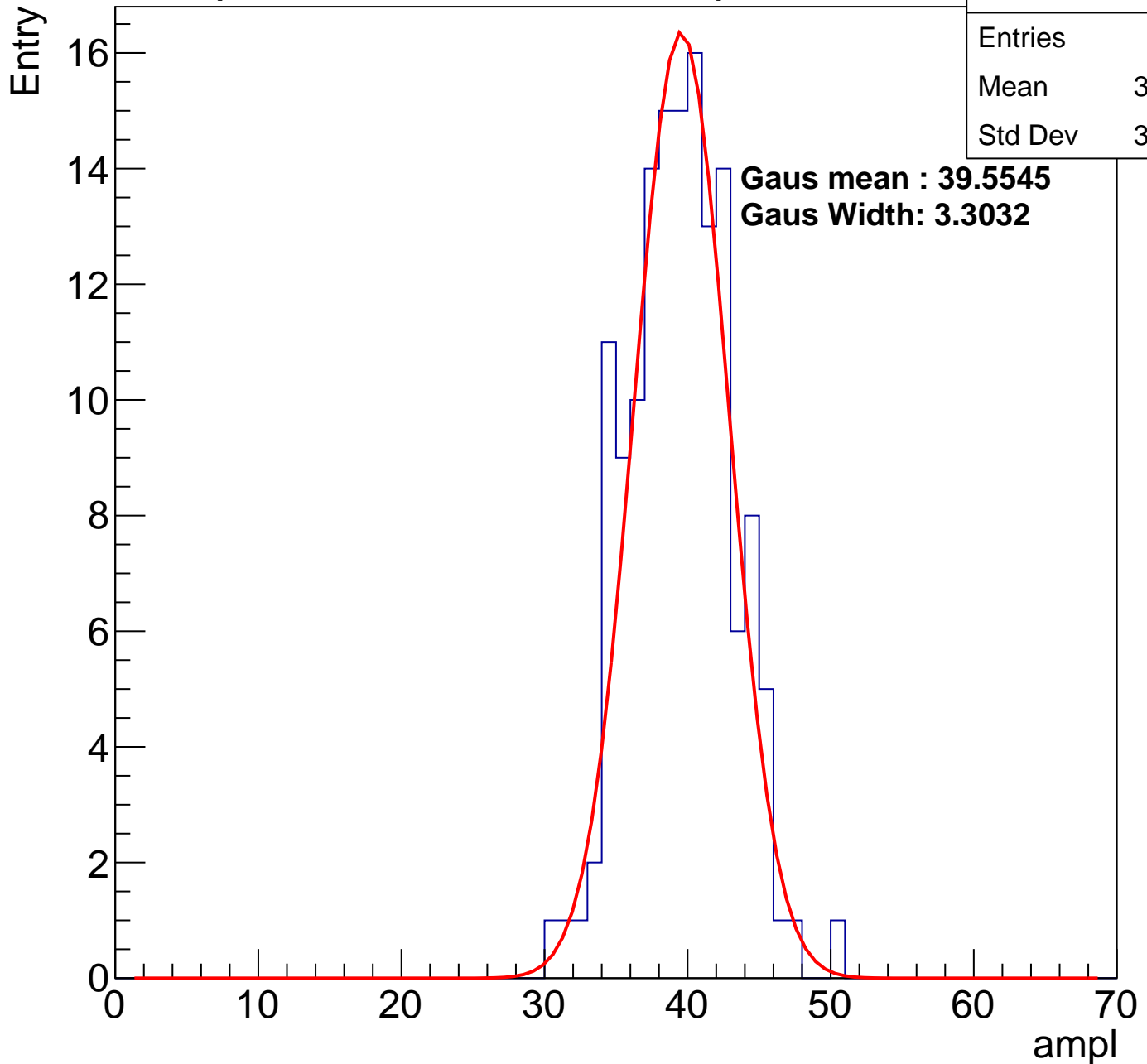


B0L101S, U24-ch17, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	39.03
Std Dev	3.486

Gaus mean : 39.5545
Gaus Width: 3.3032



B0L101S, U24-ch17, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	46.04
Std Dev	3.264

Gaus mean : 46.6217

Gaus Width: 3.4920

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

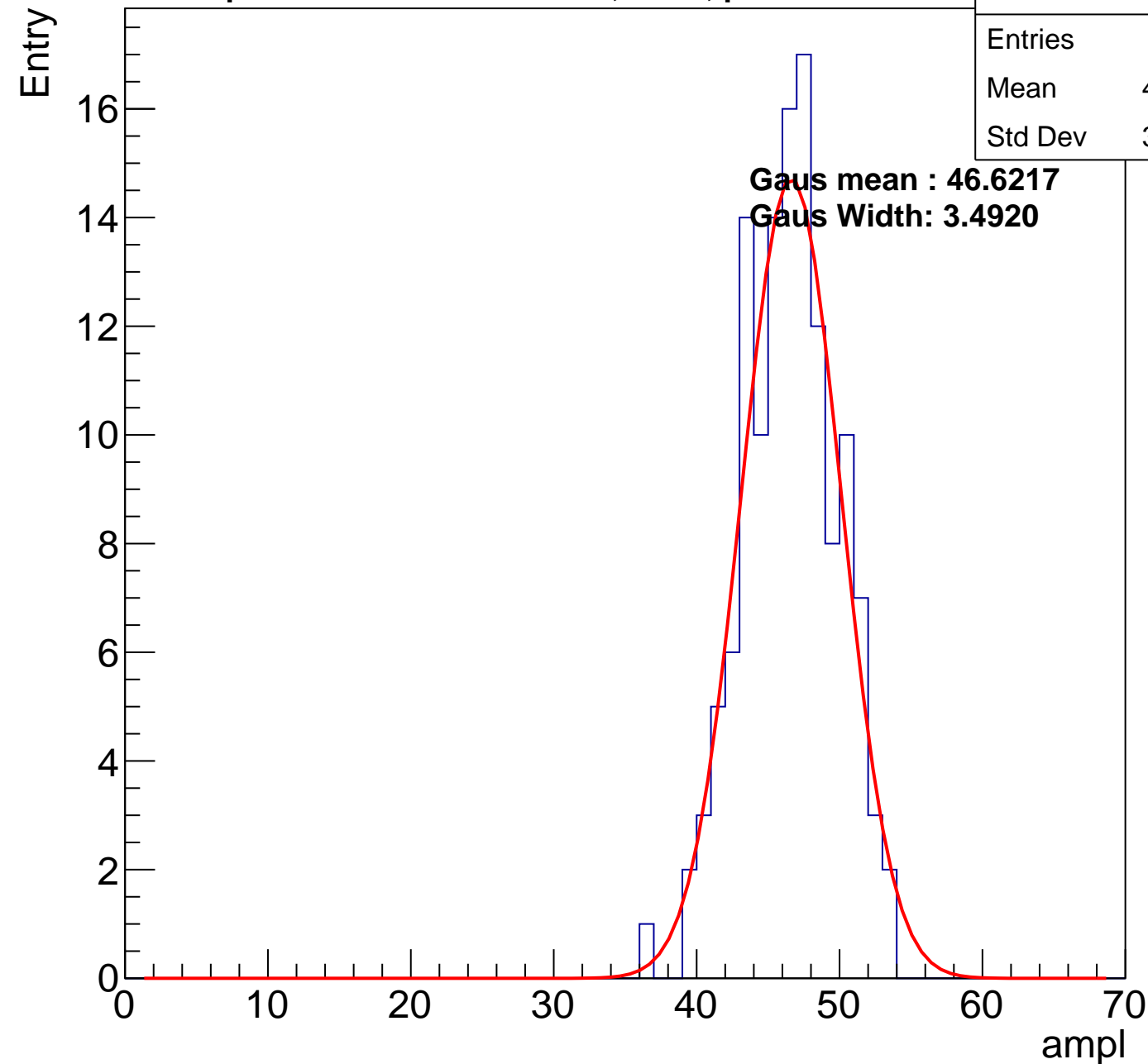
40

50

60

70

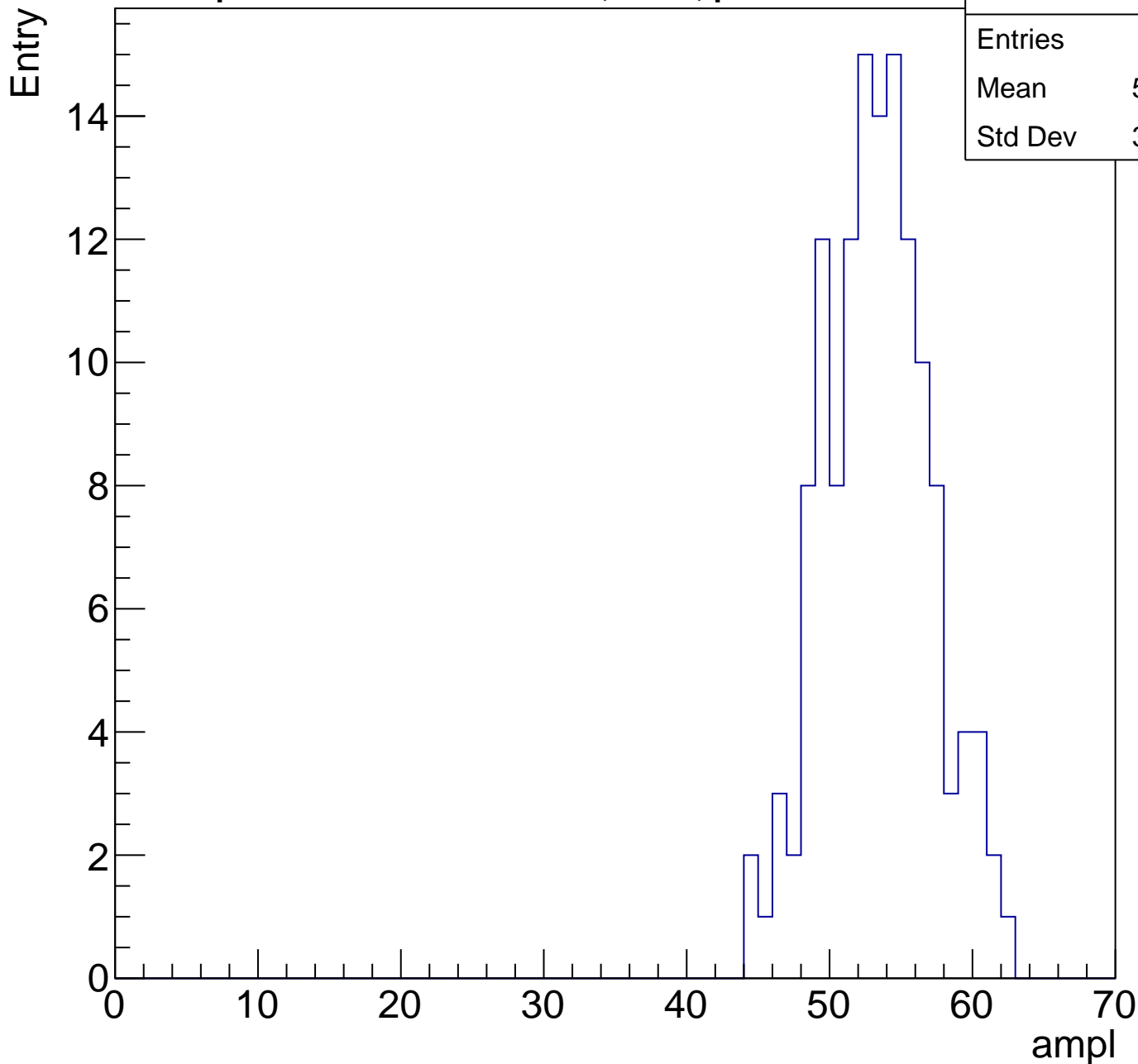
ampl



B0L101S, U24-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	52.88
Std Dev	3.713



B0L101S, U24-ch17, adc4

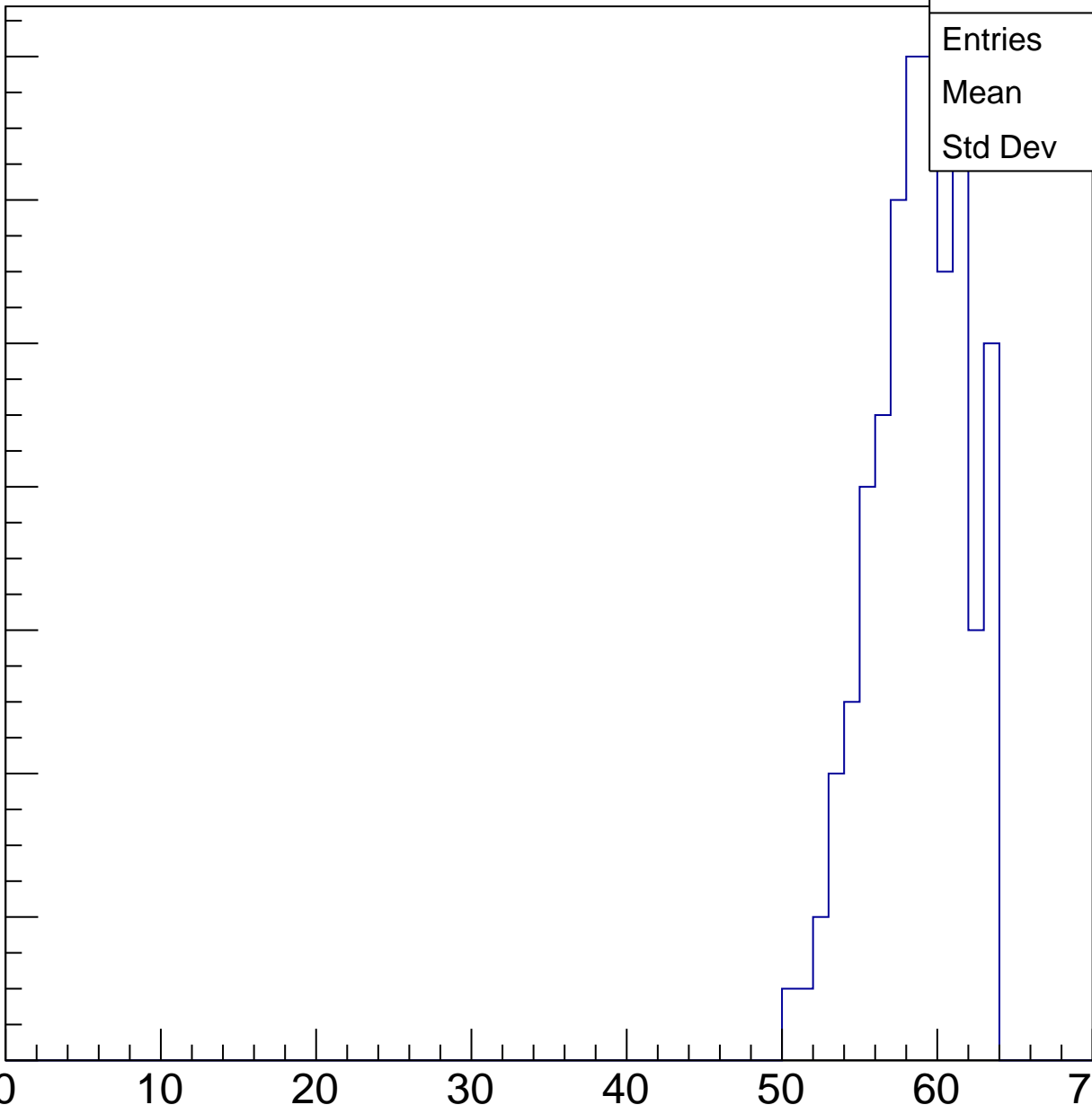
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	58.28
Std Dev	3.011

ampl



B0L101S, U24-ch17, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

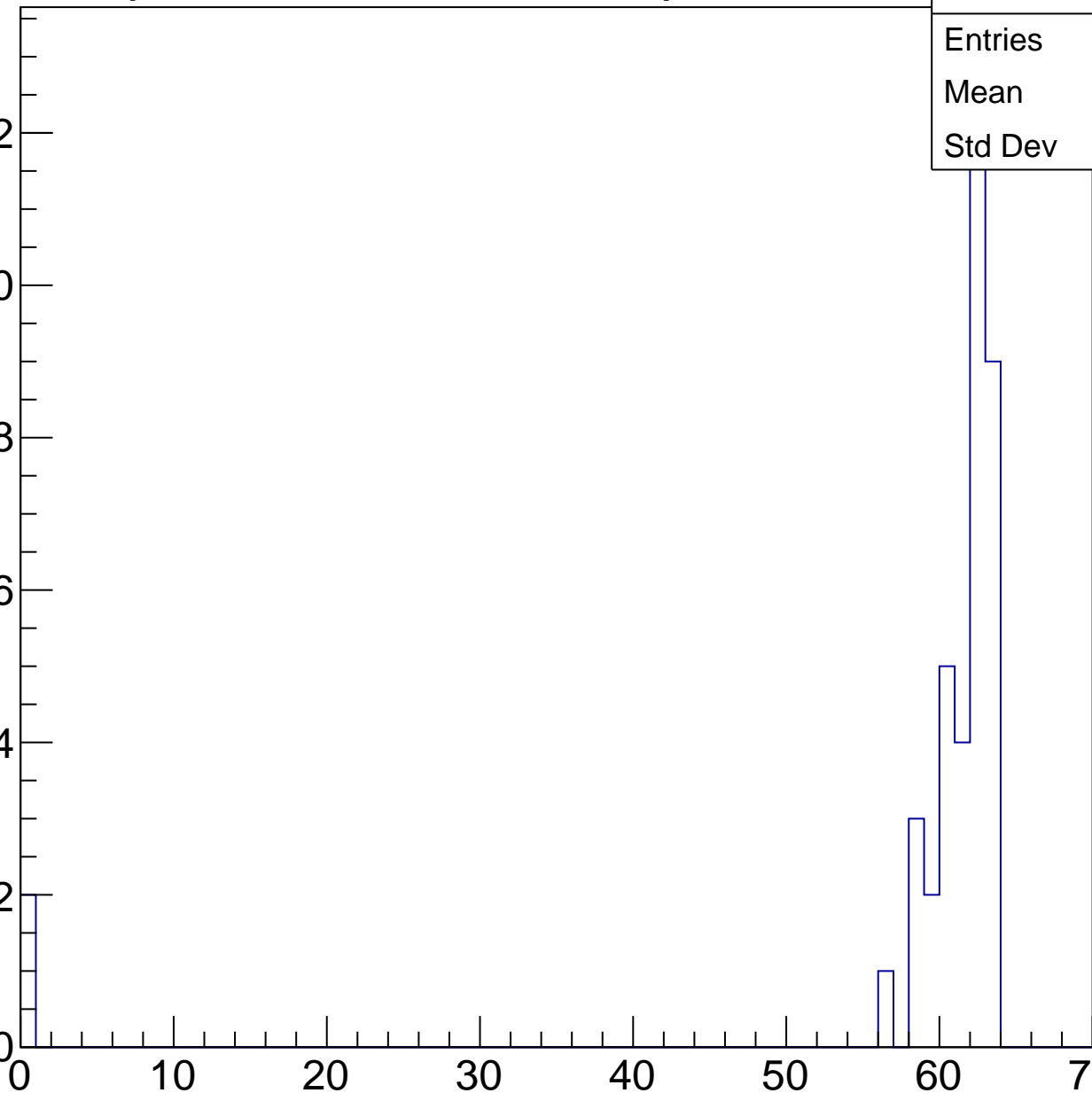
4

2

0

Entries	39
Mean	58.08
Std Dev	13.61

ampl



B0L101S, U24-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



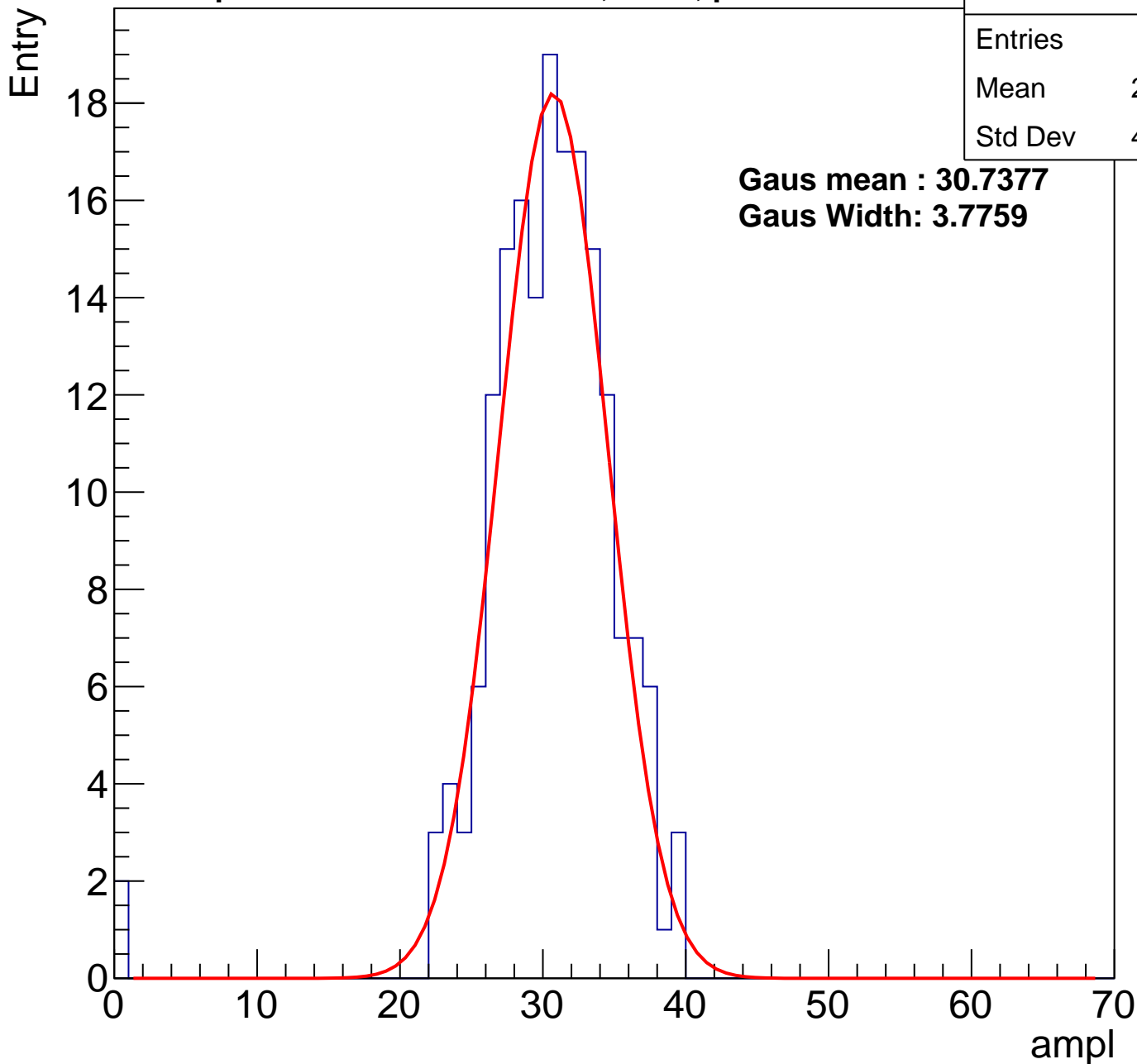
Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	29.99
Std Dev	4.896

Gaus mean : 30.7377
Gaus Width: 3.7759



B0L101S, U24-ch18, adc1

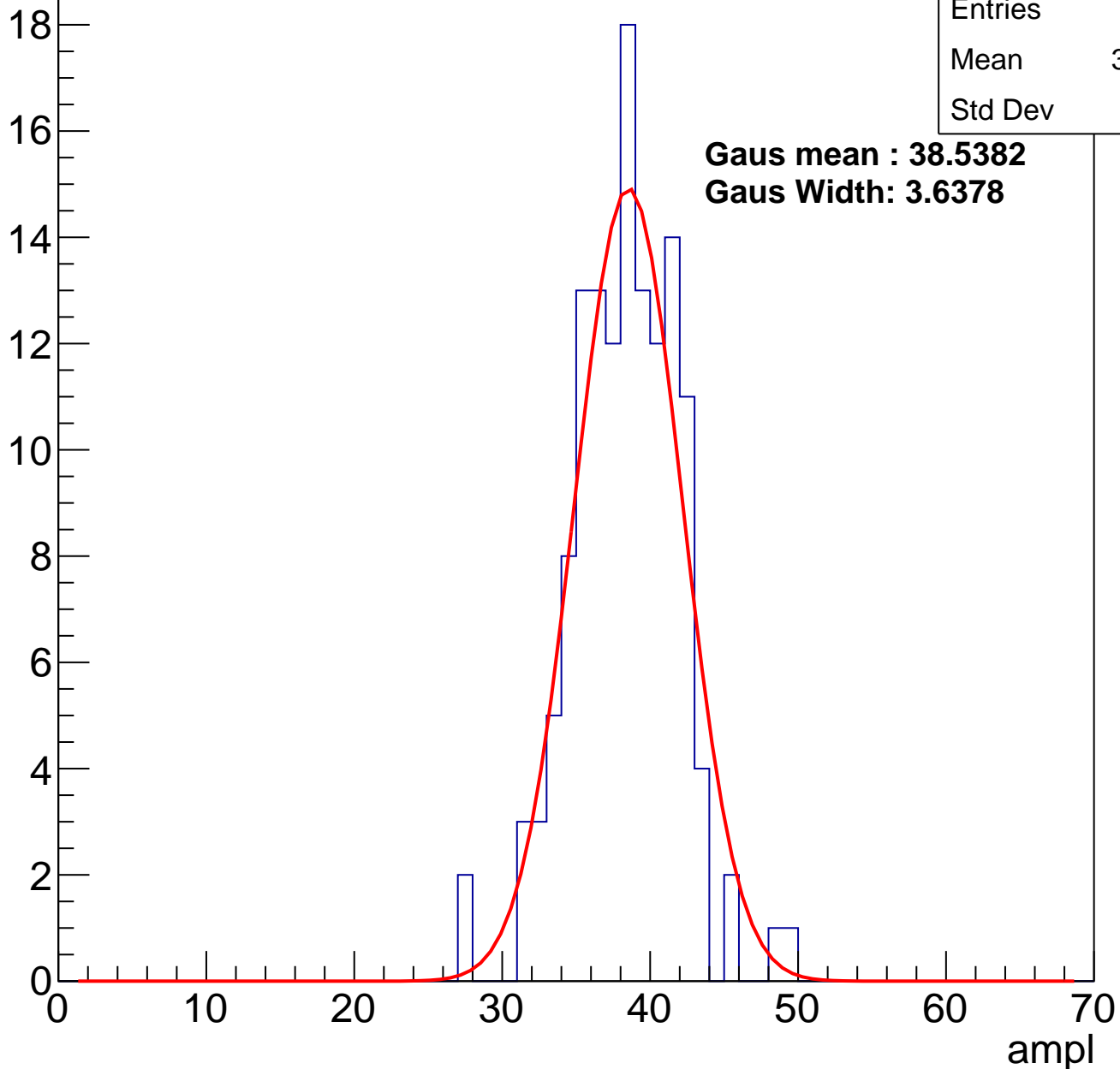
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	37.87
Std Dev	3.56

Gaus mean : 38.5382

Gaus Width: 3.6378

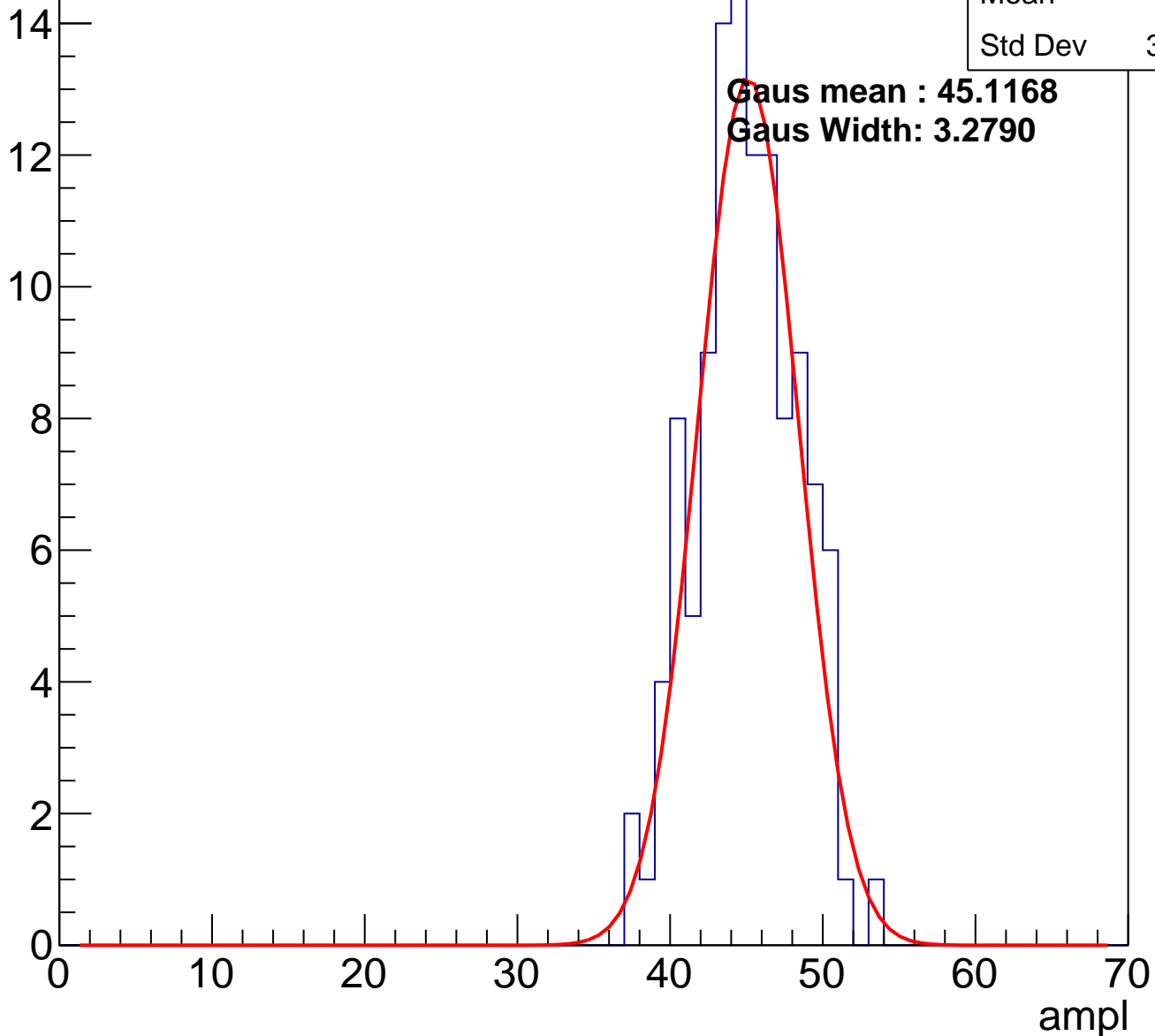
Entry



B0L101S, U24-ch18, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

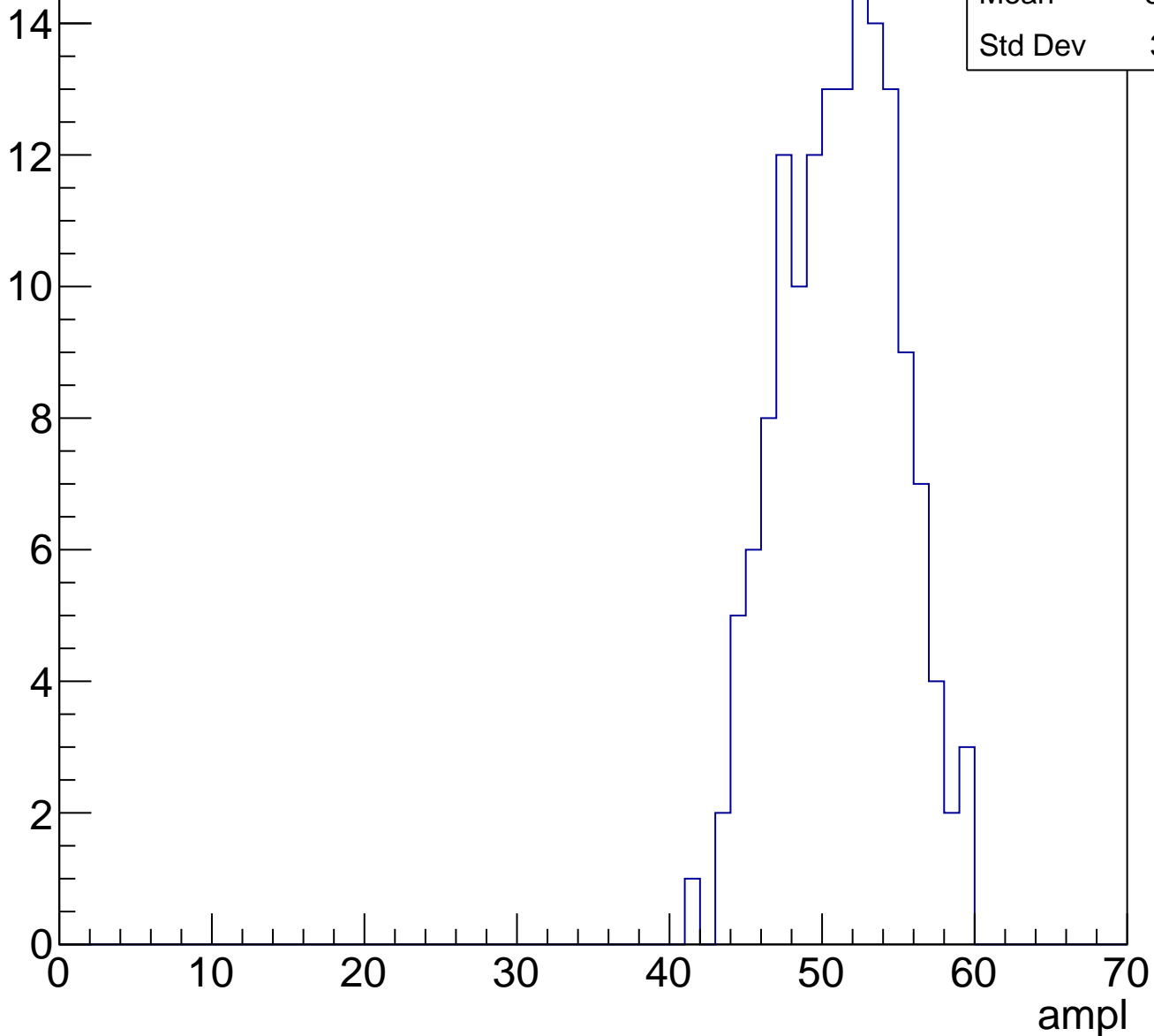


B0L101S, U24-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	50.75
Std Dev	3.811

Entry



B0L101S, U24-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

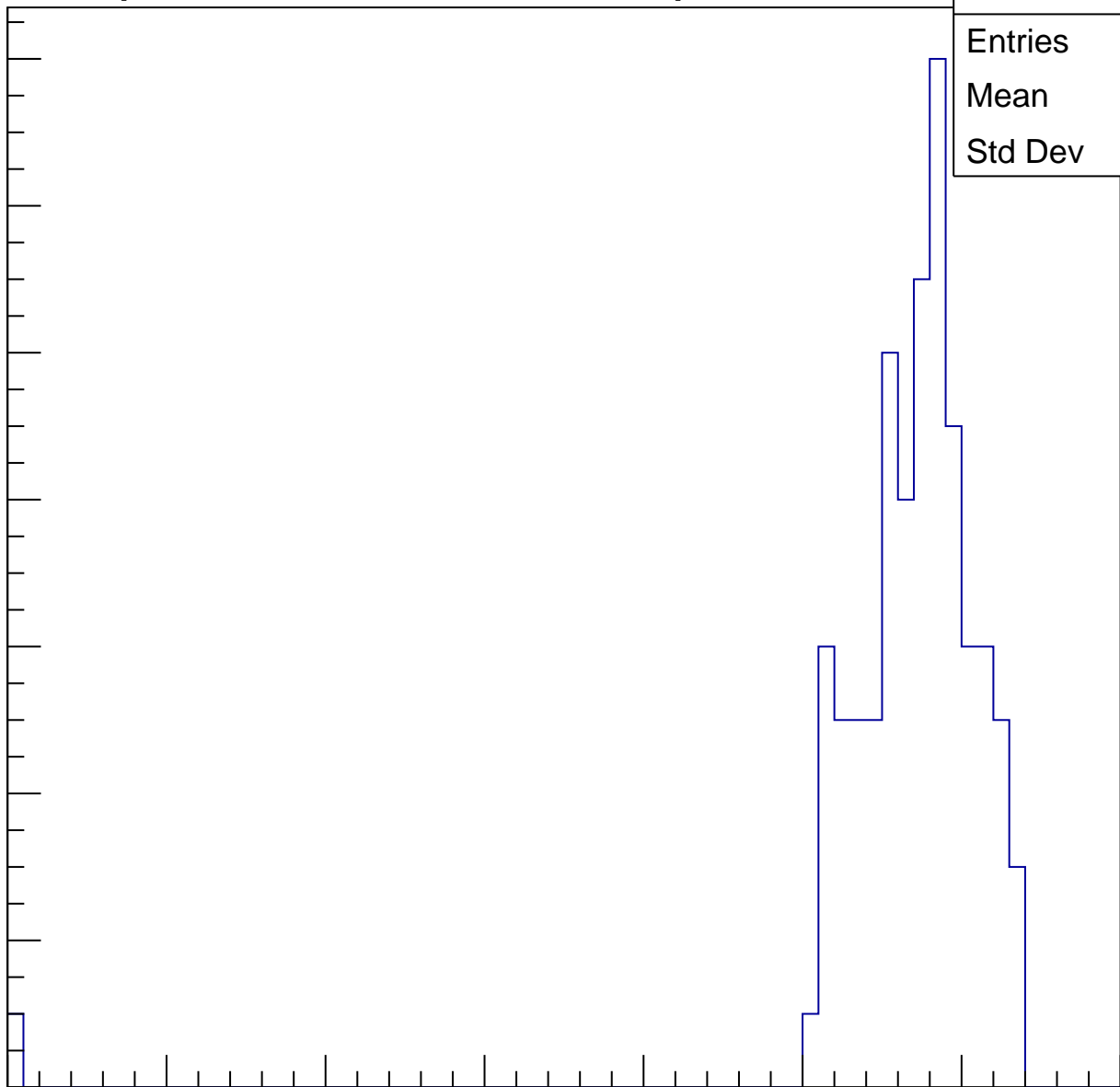
Entries	95
Mean	56.25
Std Dev	6.643

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch18, adc5

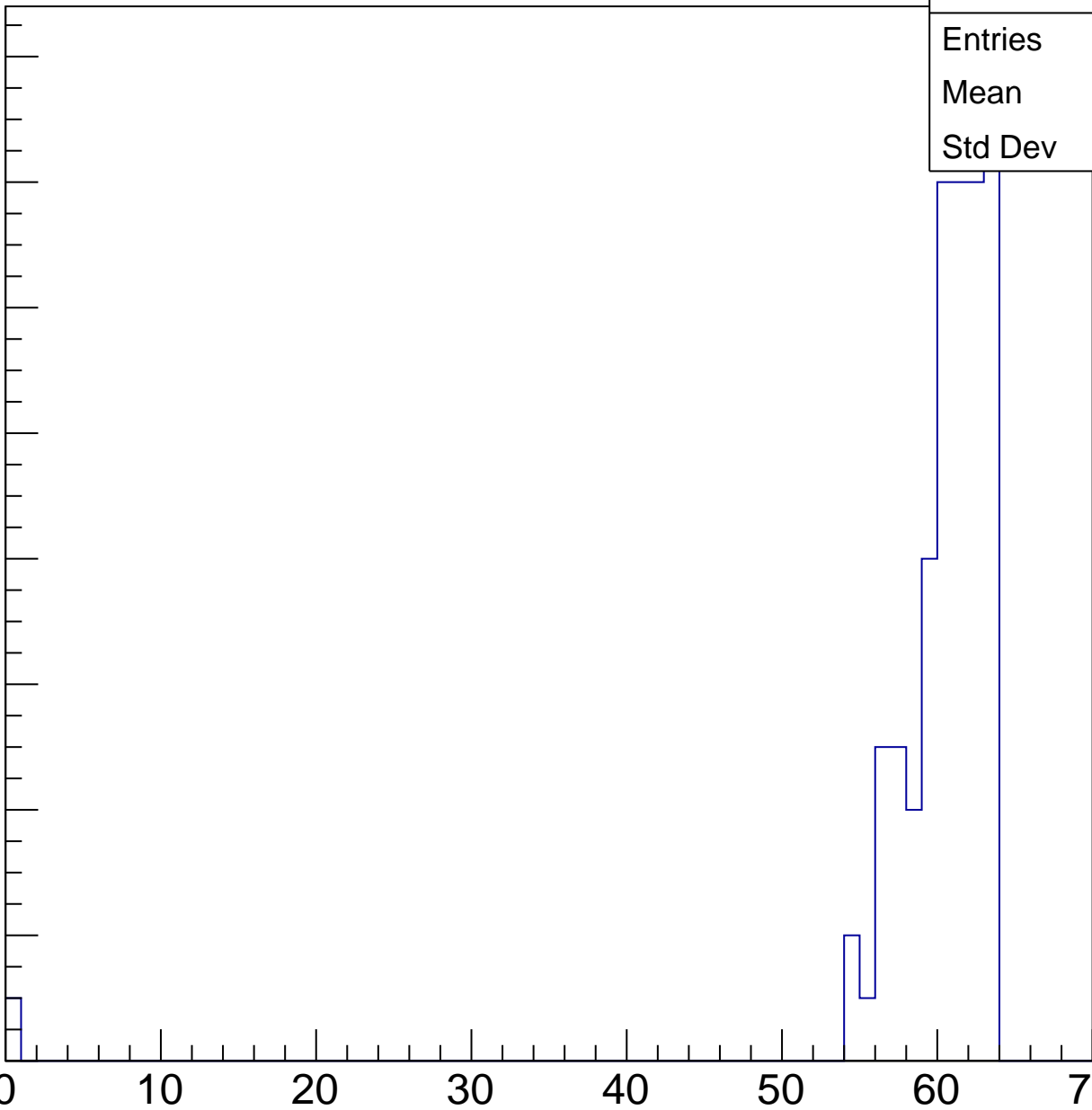
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.55
Std Dev	6.937

ampl



B0L101S, U24-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch19, adc0

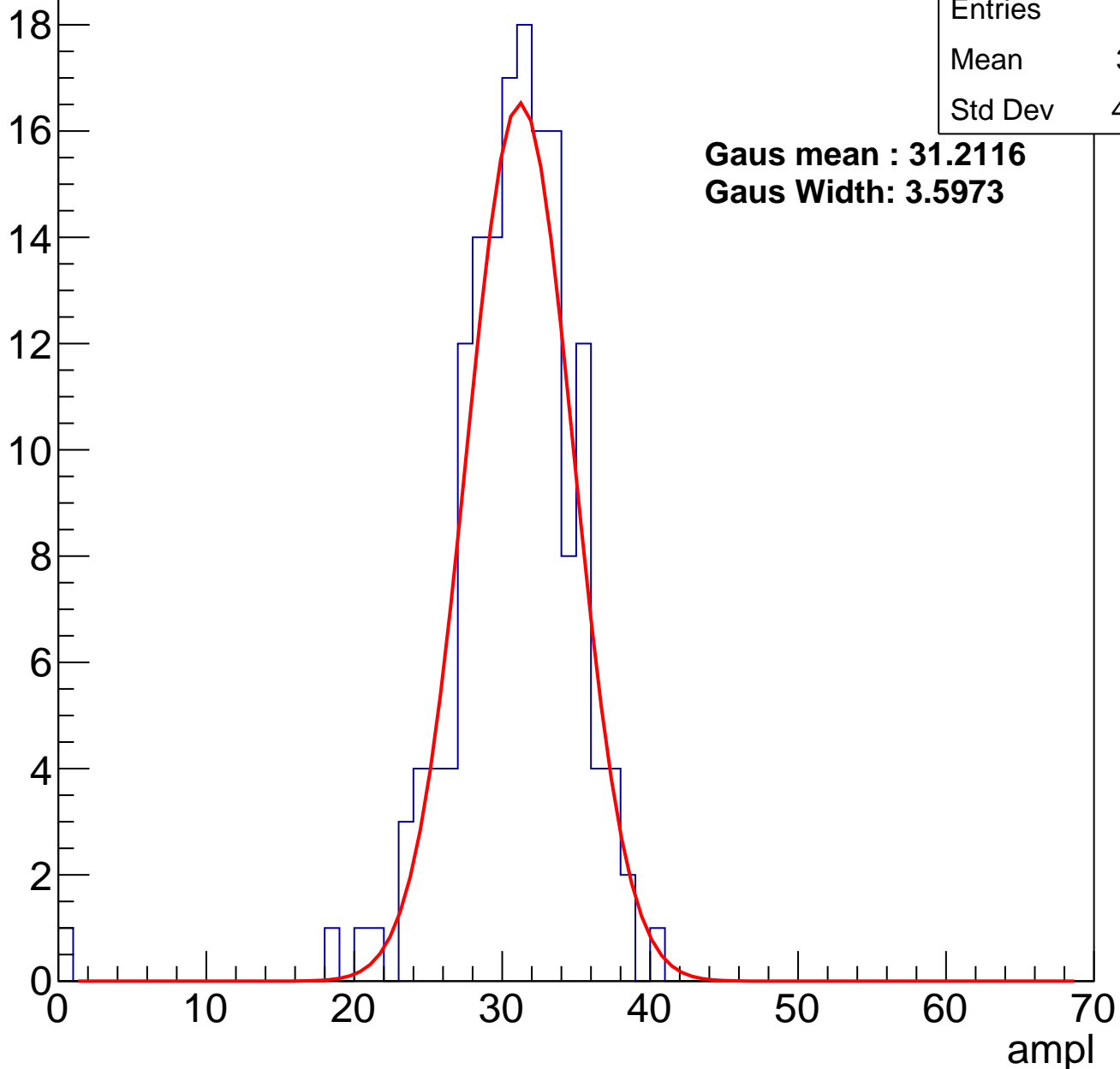
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	30.31
Std Dev	4.419

Gaus mean : 31.2116

Gaus Width: 3.5973

Entry



B0L101S, U24-ch19, adc1

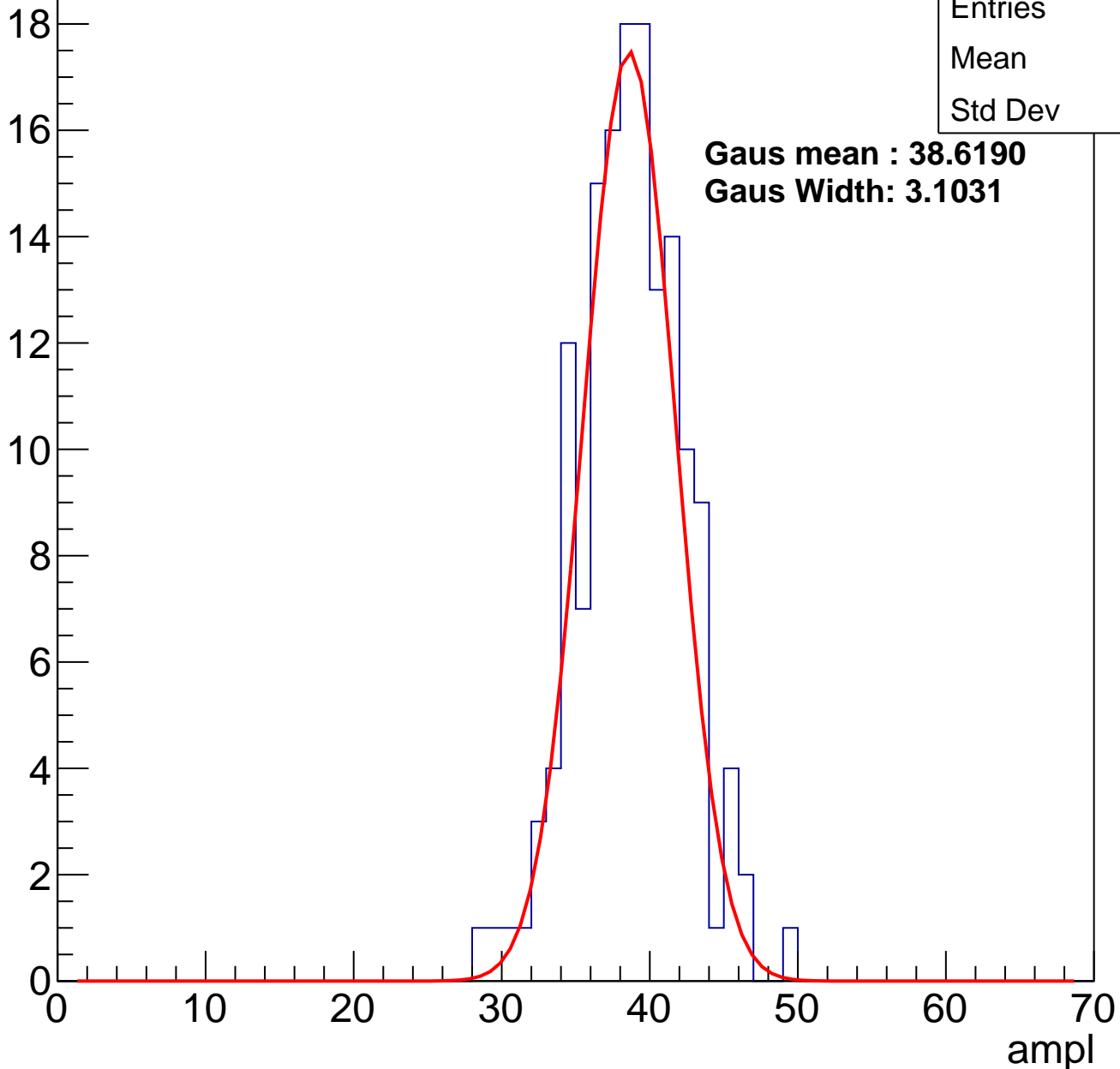
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	38.3
Std Dev	3.53

Gaus mean : 38.6190

Gaus Width: 3.1031

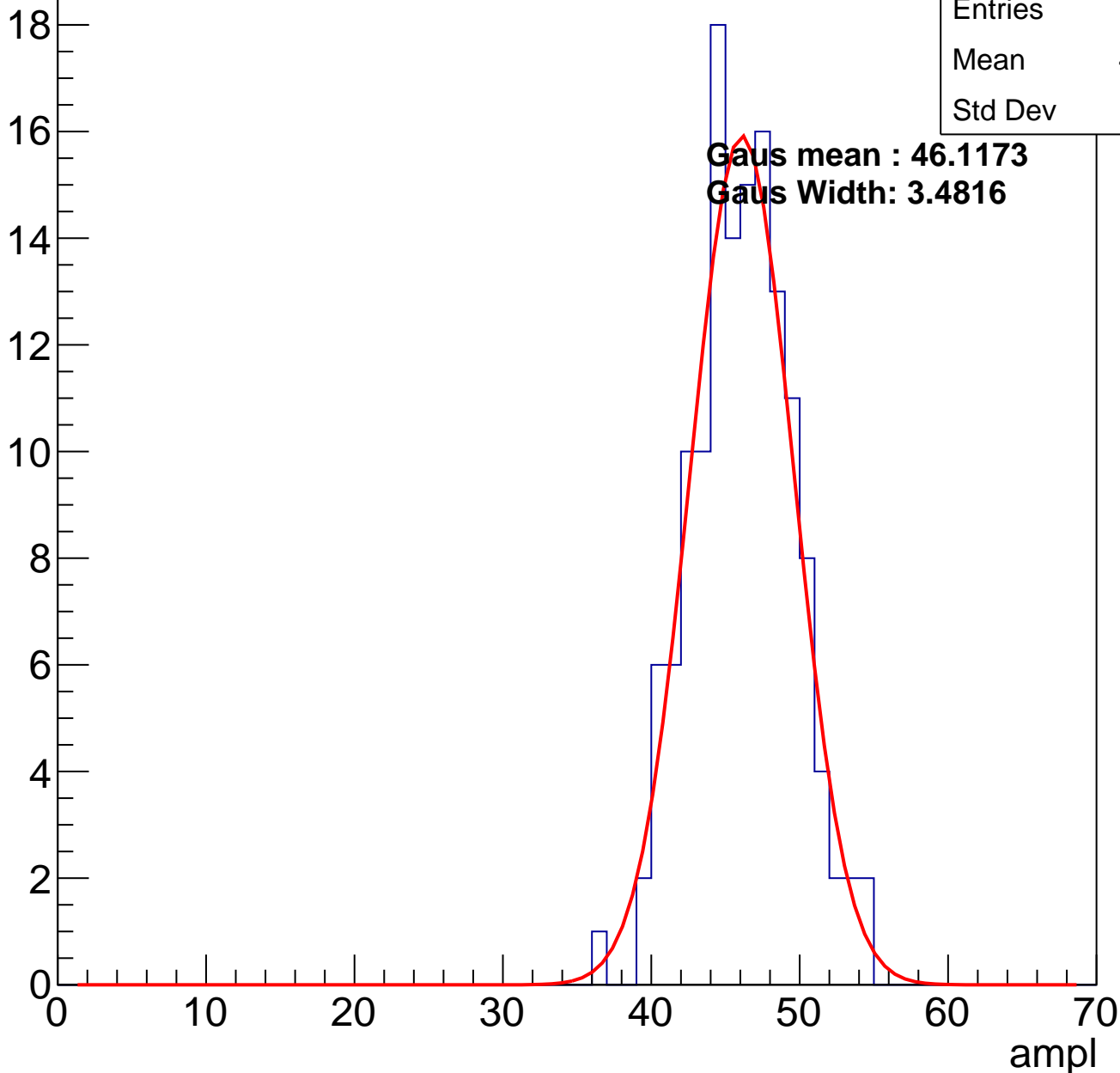
Entry



B0L101S, U24-ch19, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

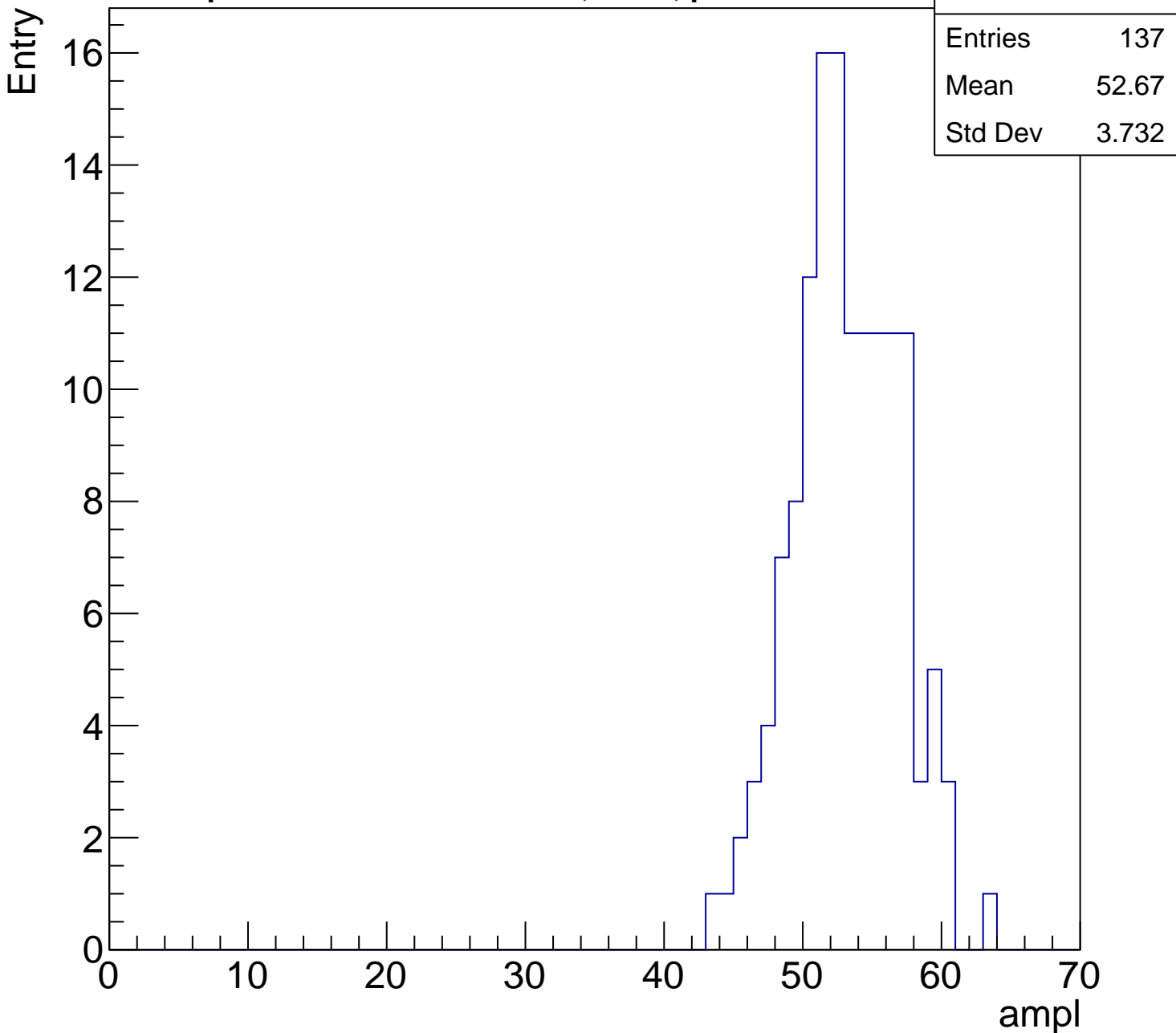
Entries	137
Mean	52.67
Std Dev	3.732

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

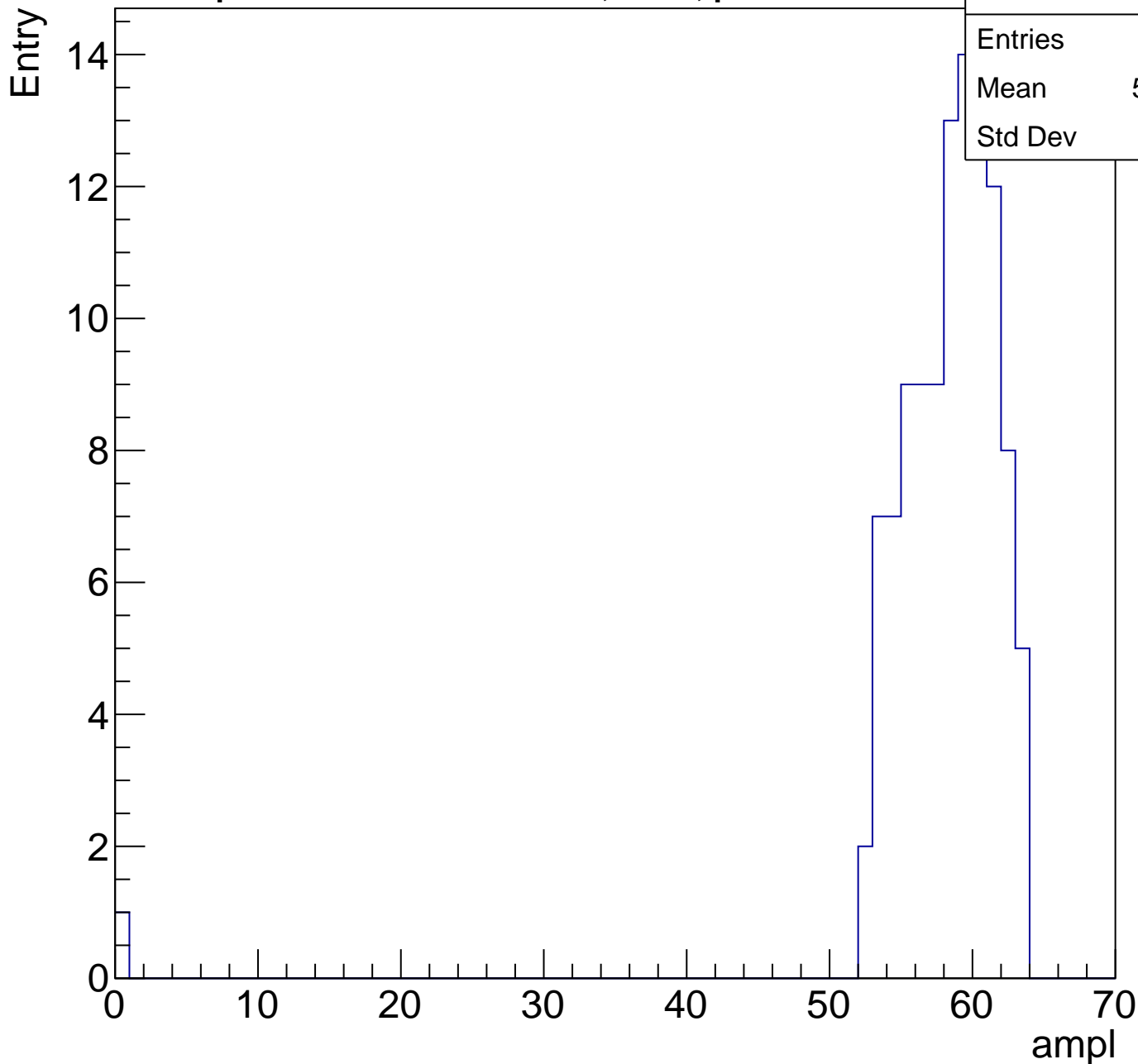
ampl



B0L101S, U24-ch19, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	57.53
Std Dev	6.22



B0L101S, U24-ch19, adc5

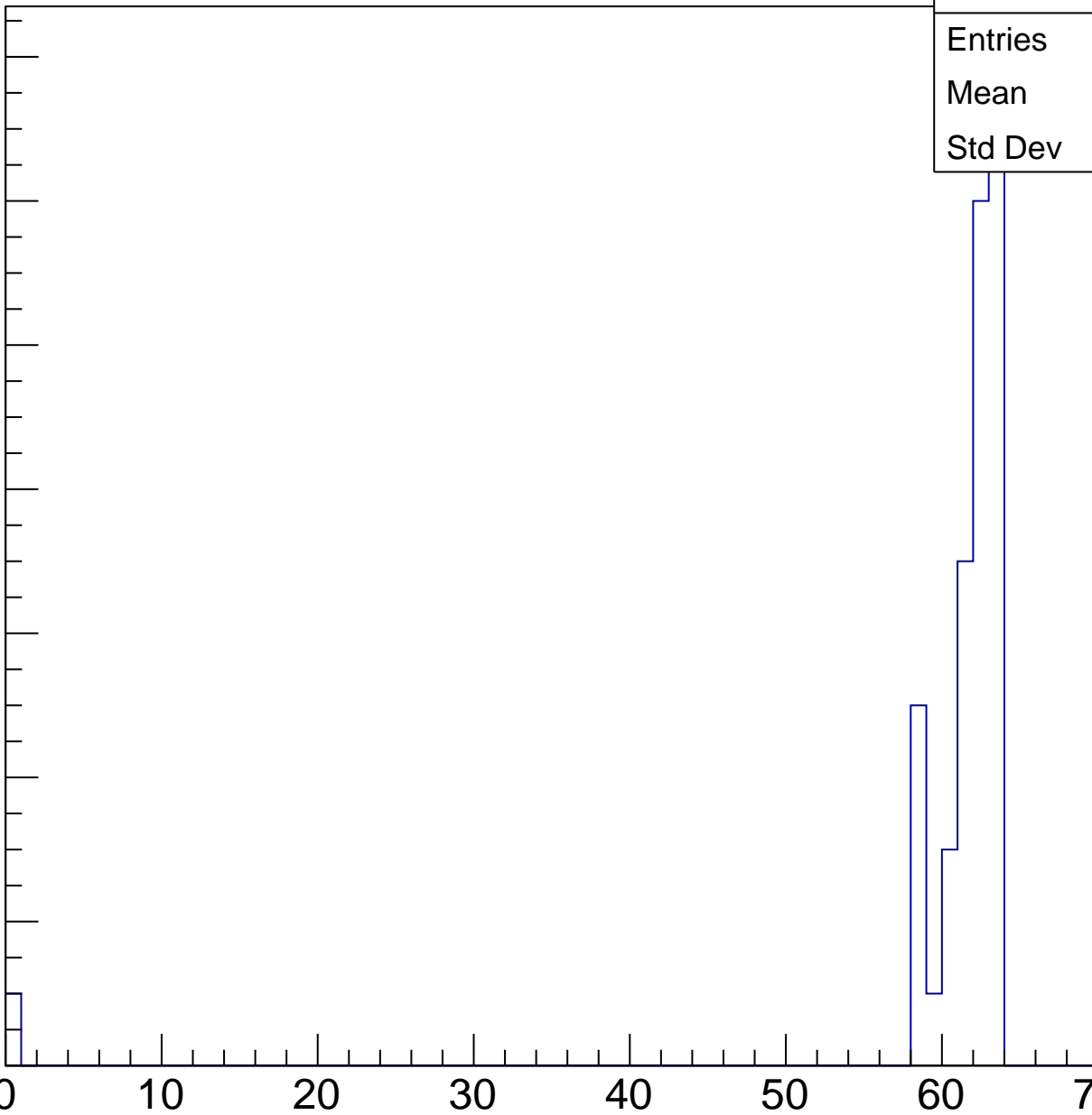
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	43
Mean	60.05
Std Dev	9.403

ampl



B0L101S, U24-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch20, adc0

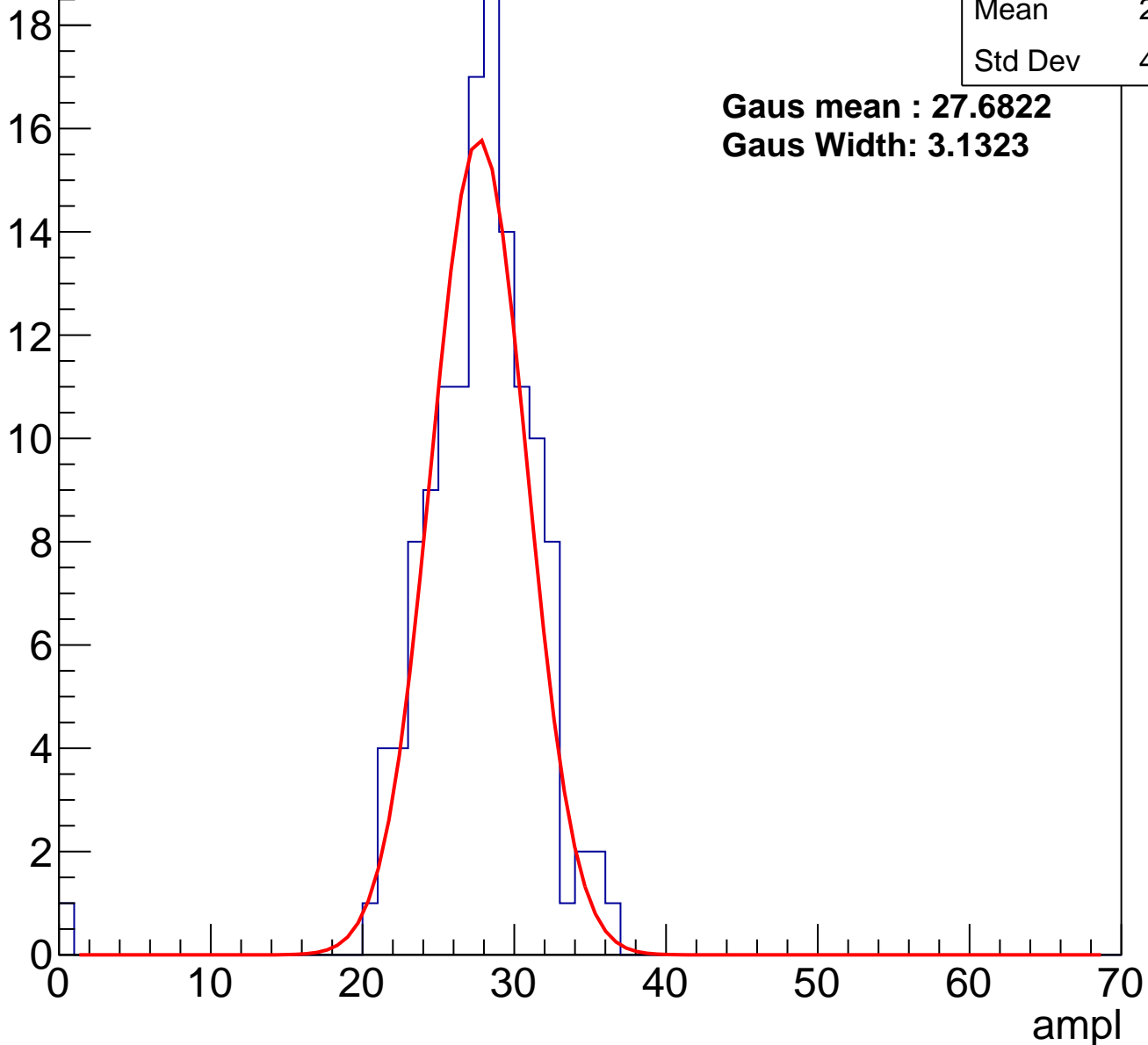
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	27.26
Std Dev	4.009

Gaus mean : 27.6822

Gaus Width: 3.1323

Entry



B0L101S, U24-ch20, adc1

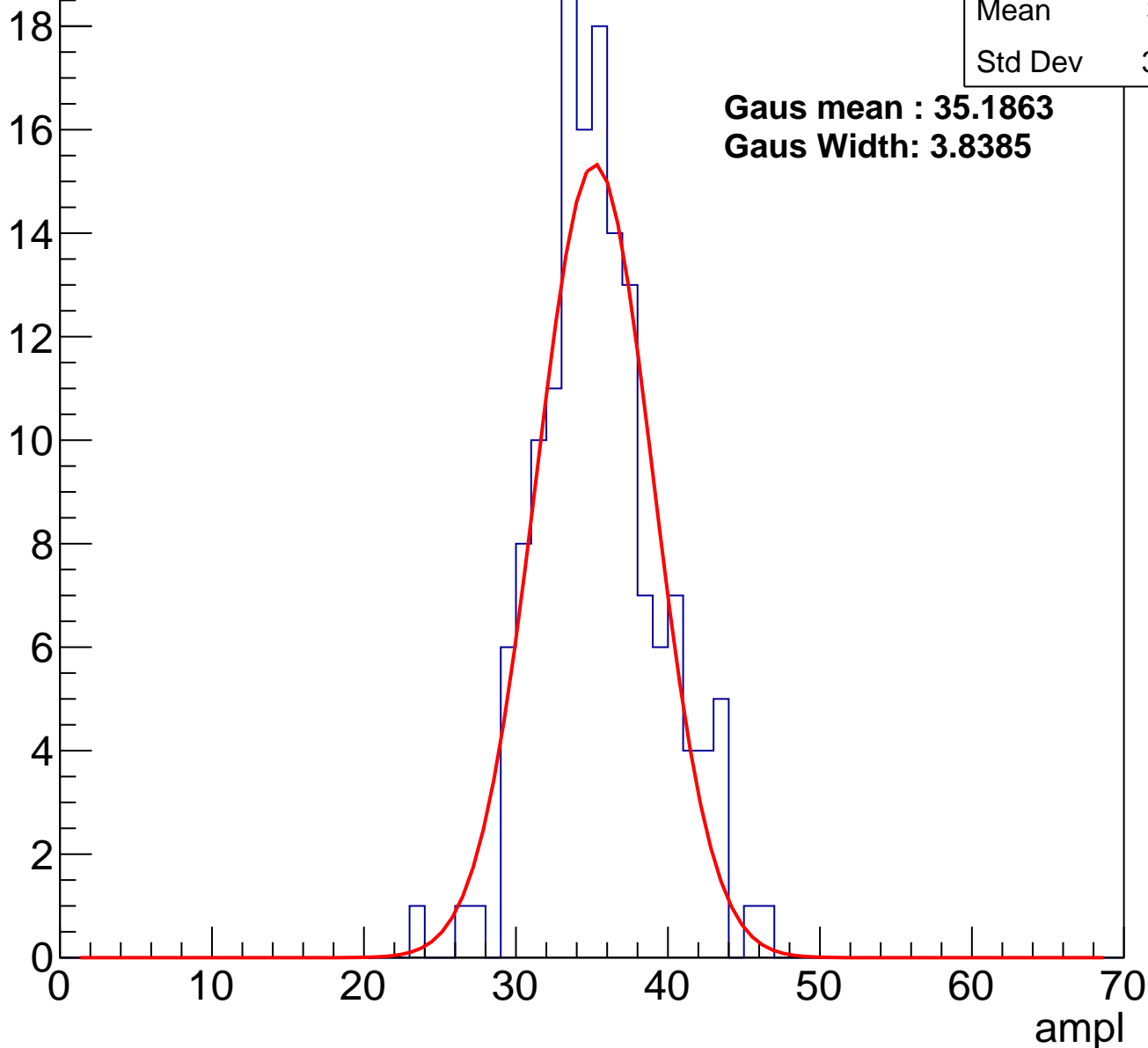
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	35.01
Std Dev	3.908

Gaus mean : 35.1863

Gaus Width: 3.8385

Entry



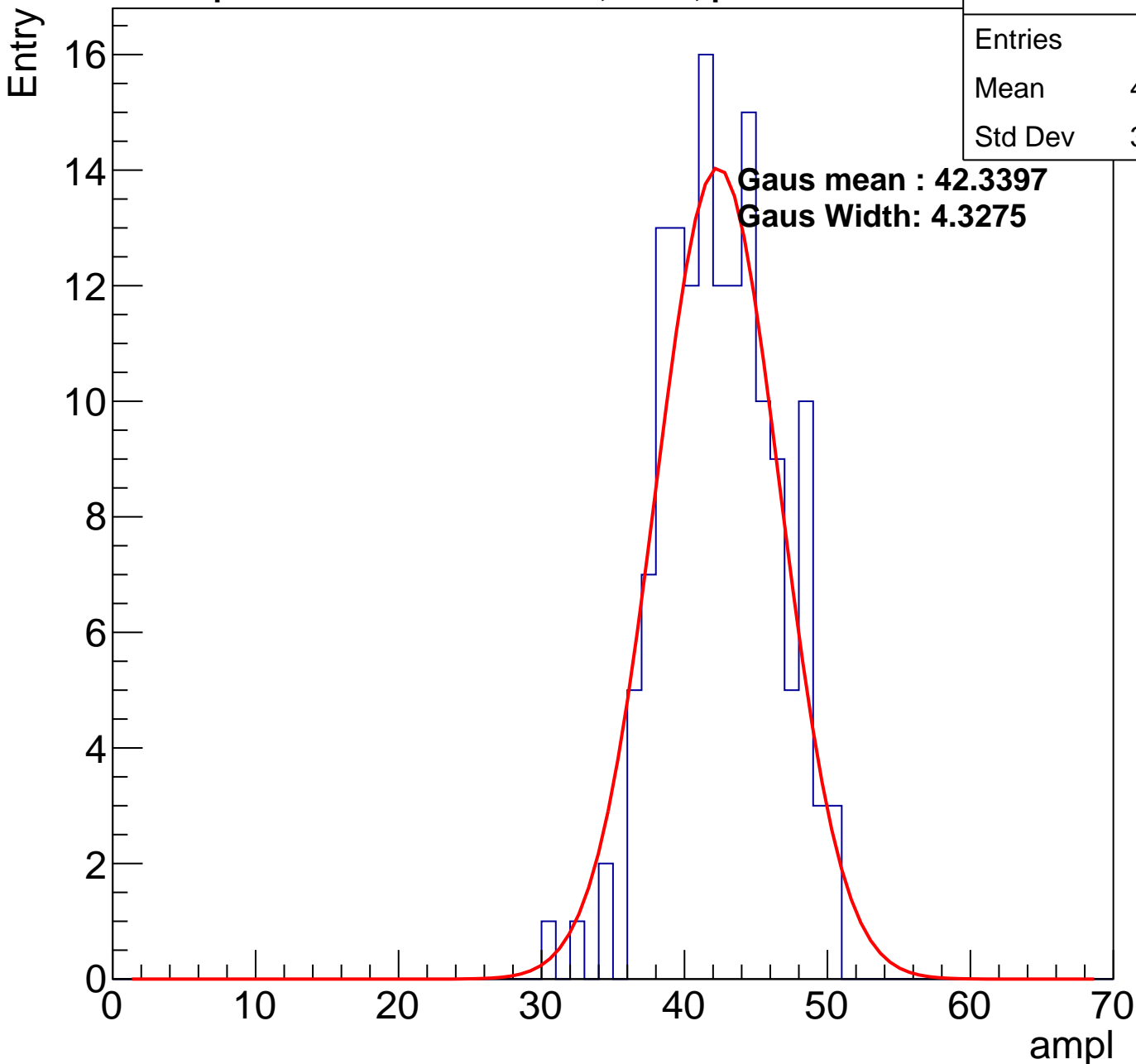
B0L101S, U24-ch20, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	42.03
Std Dev	3.882

Gaus mean : 42.3397

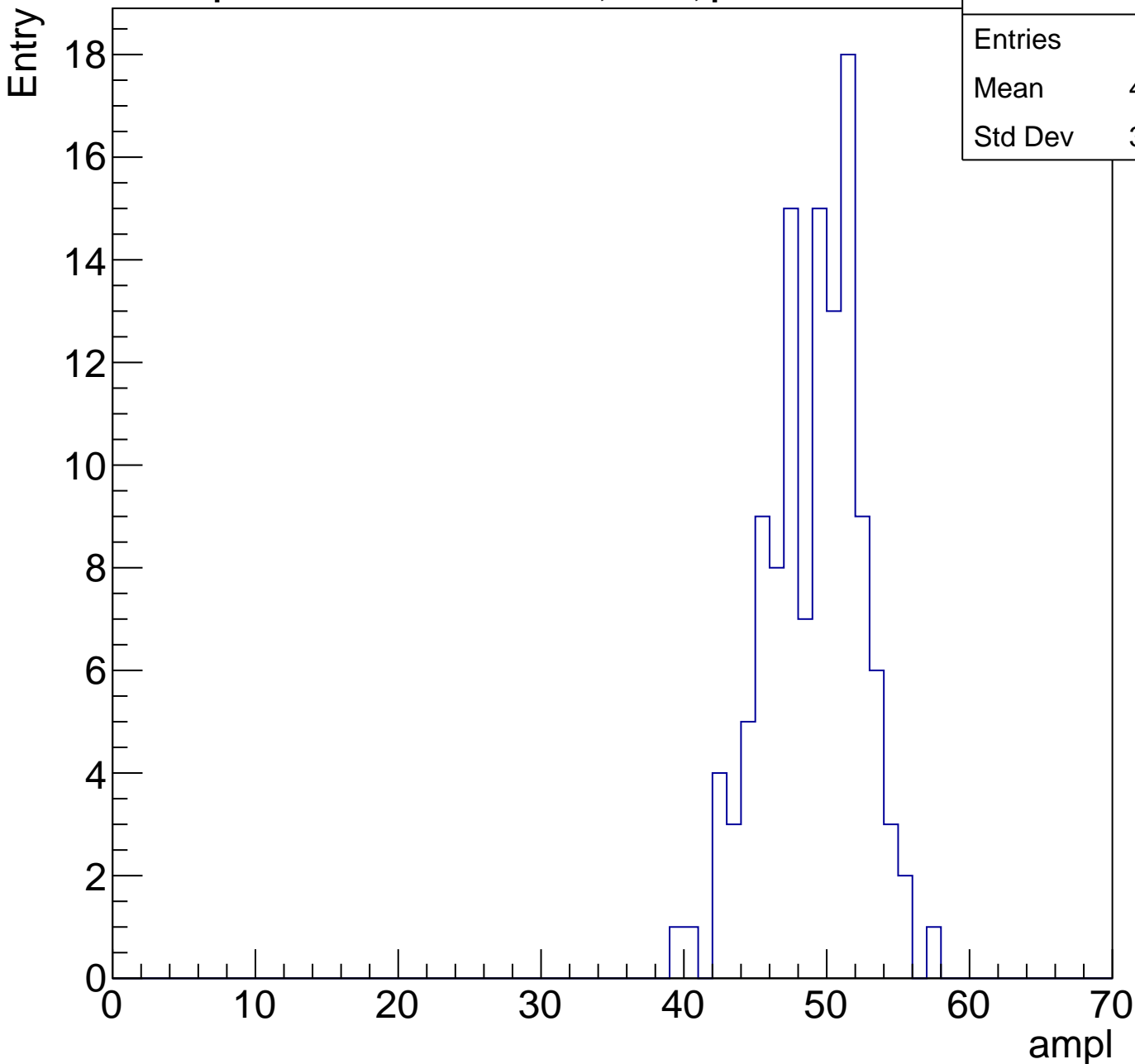
Gaus Width: 4.3275



B0L101S, U24-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

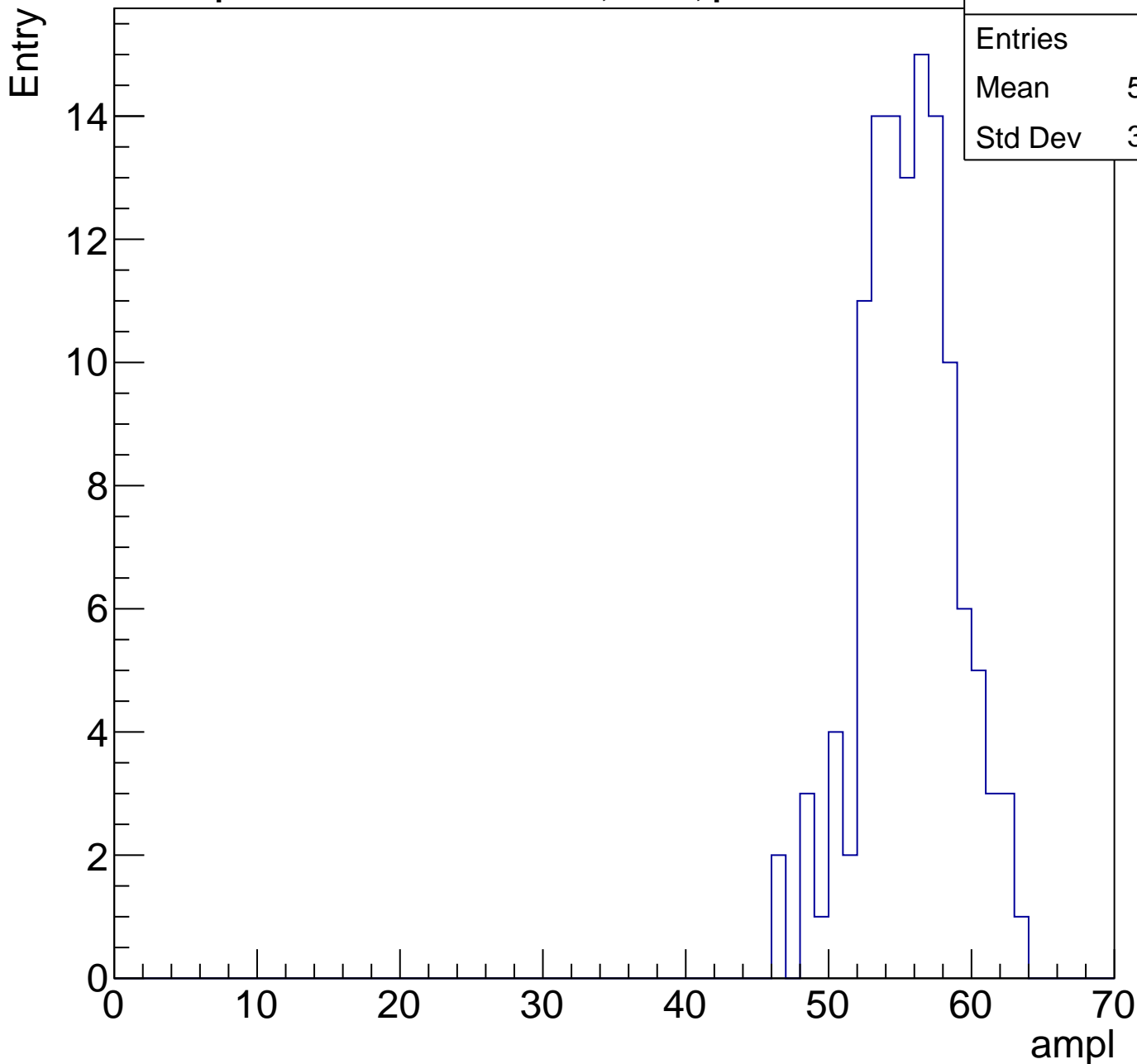
Entries	120
Mean	48.57
Std Dev	3.346



B0L101S, U24-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	55.17
Std Dev	3.332



B0L101S, U24-ch20, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries	98
Mean	58.92
Std Dev	6.535

ampl

0

10

20

30

40

50

60

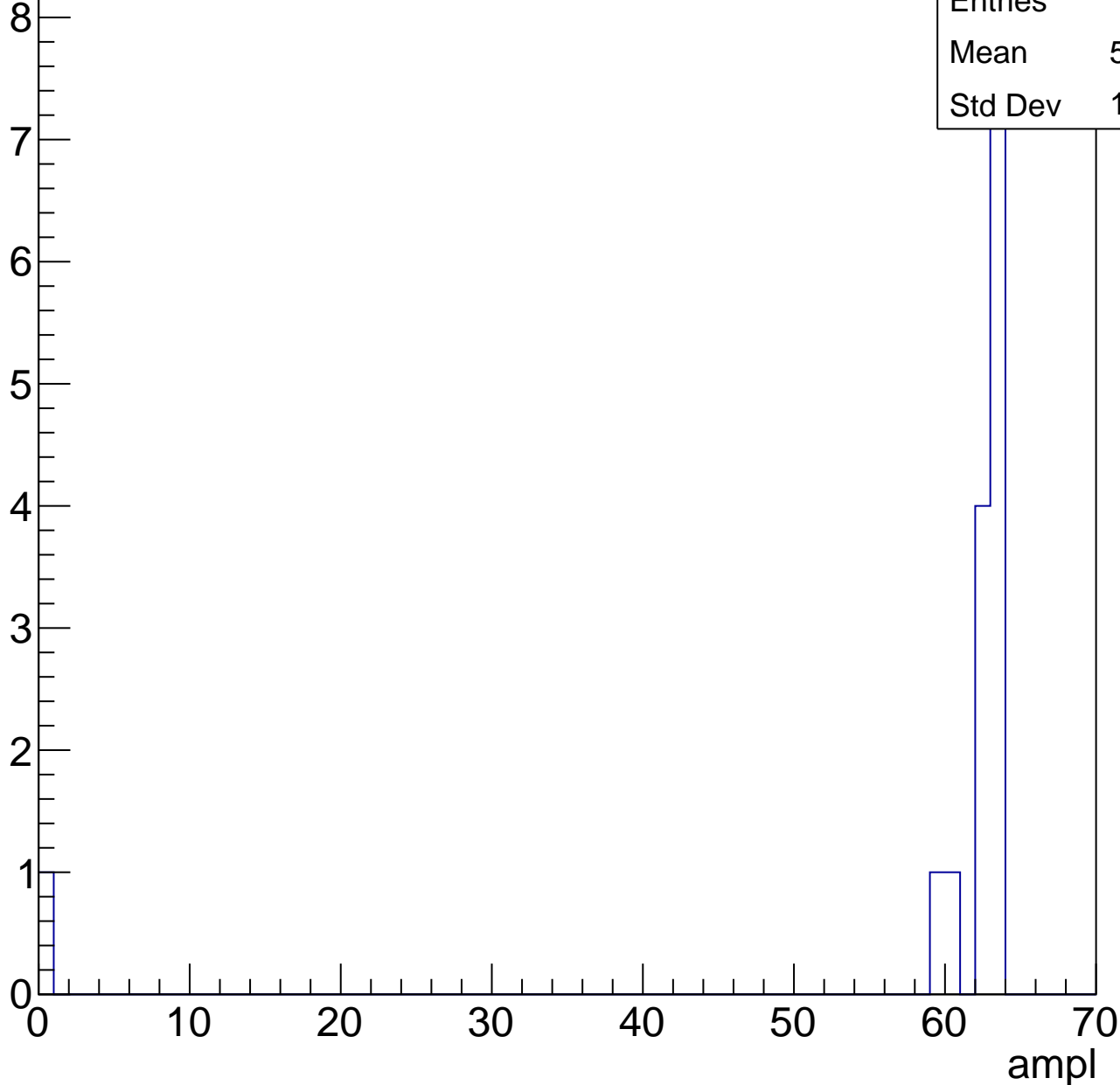
70

B0L101S, U24-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	58.07
Std Dev	15.56



B0L101S, U24-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



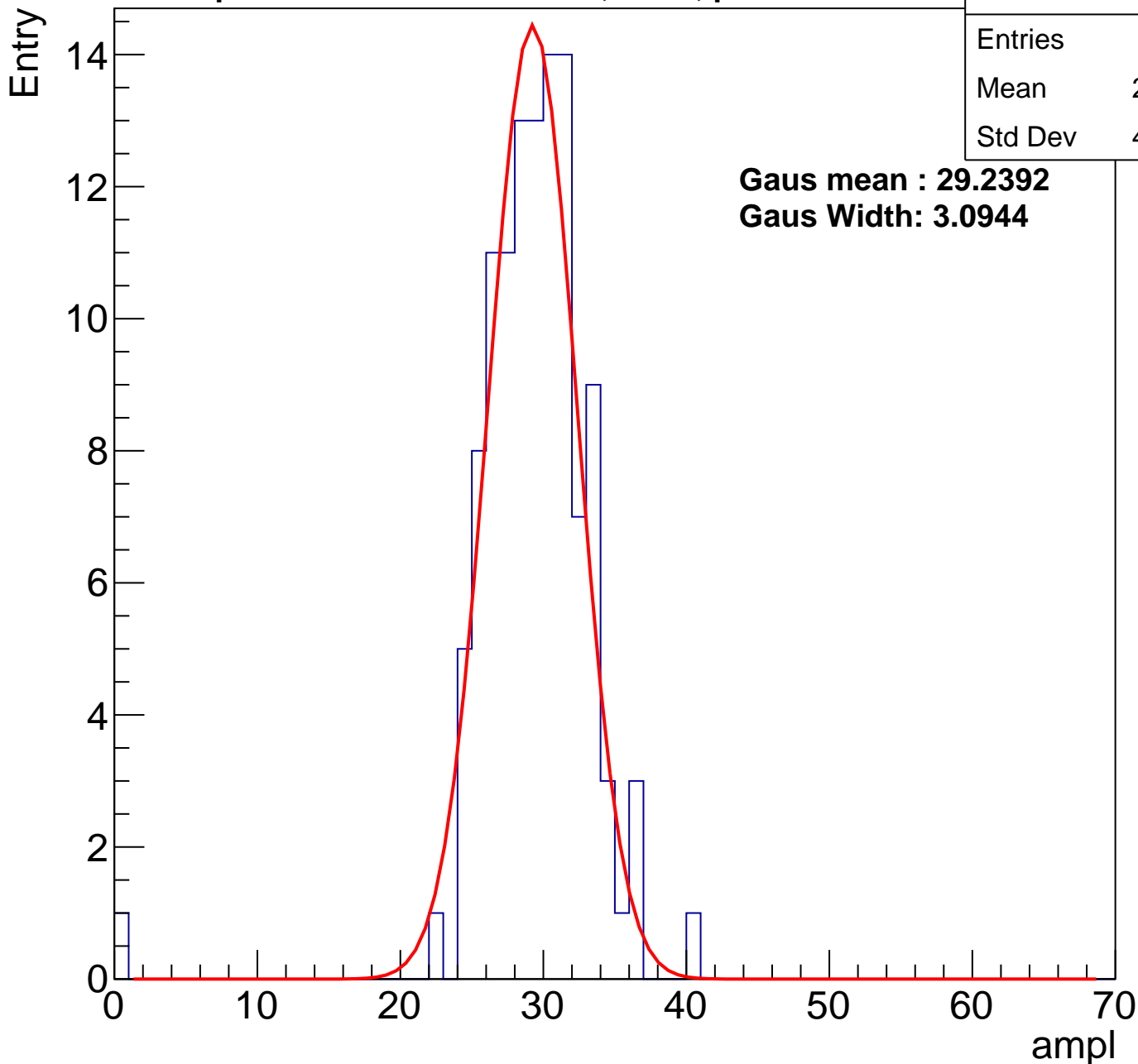
B0L101S, U24-ch21, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	28.92
Std Dev	4.122

Gaus mean : 29.2392

Gaus Width: 3.0944



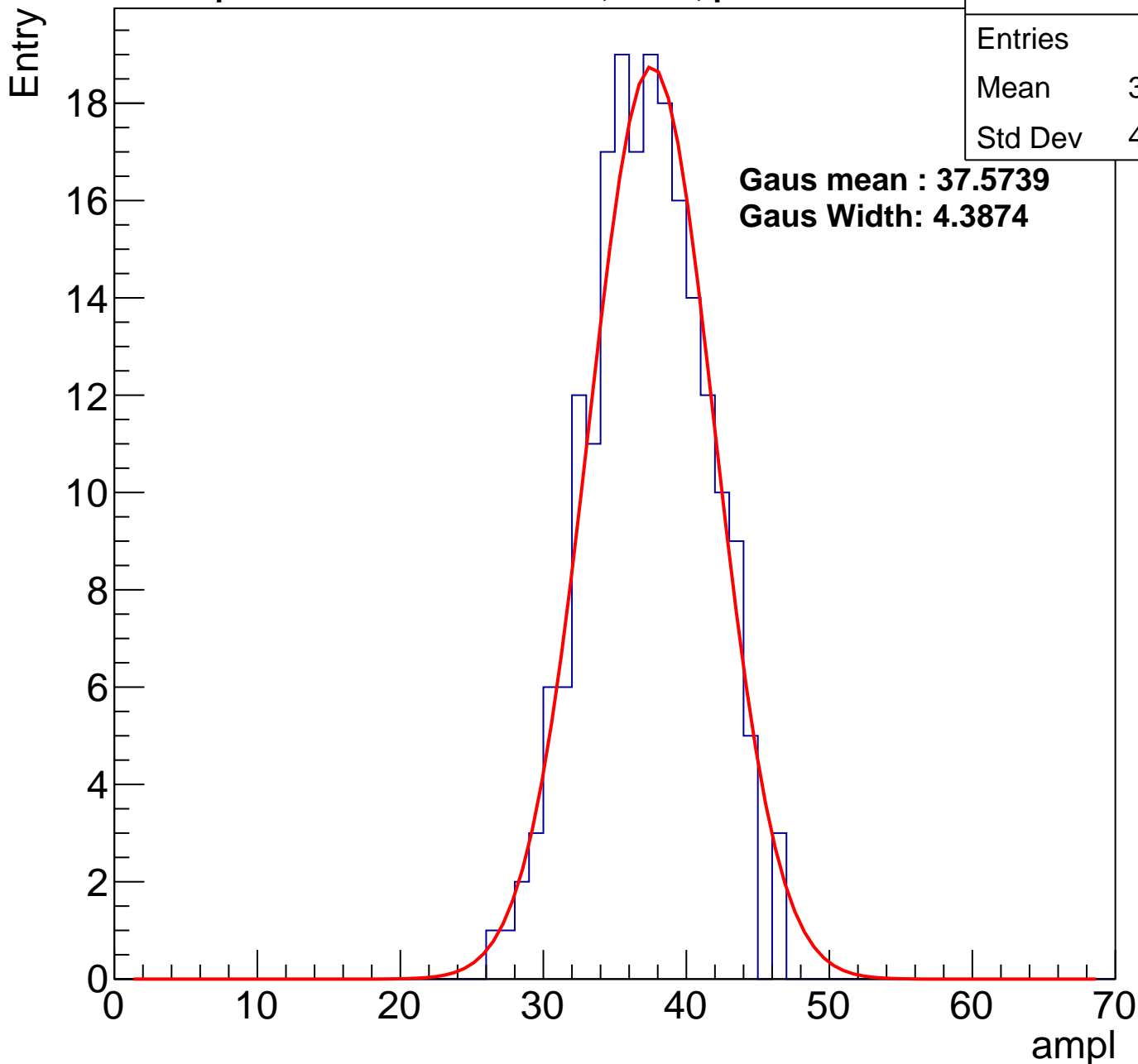
B0L101S, U24-ch21, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	201
Mean	36.78
Std Dev	4.027

Gaus mean : 37.5739

Gaus Width: 4.3874



B0L101S, U24-ch21, adc2

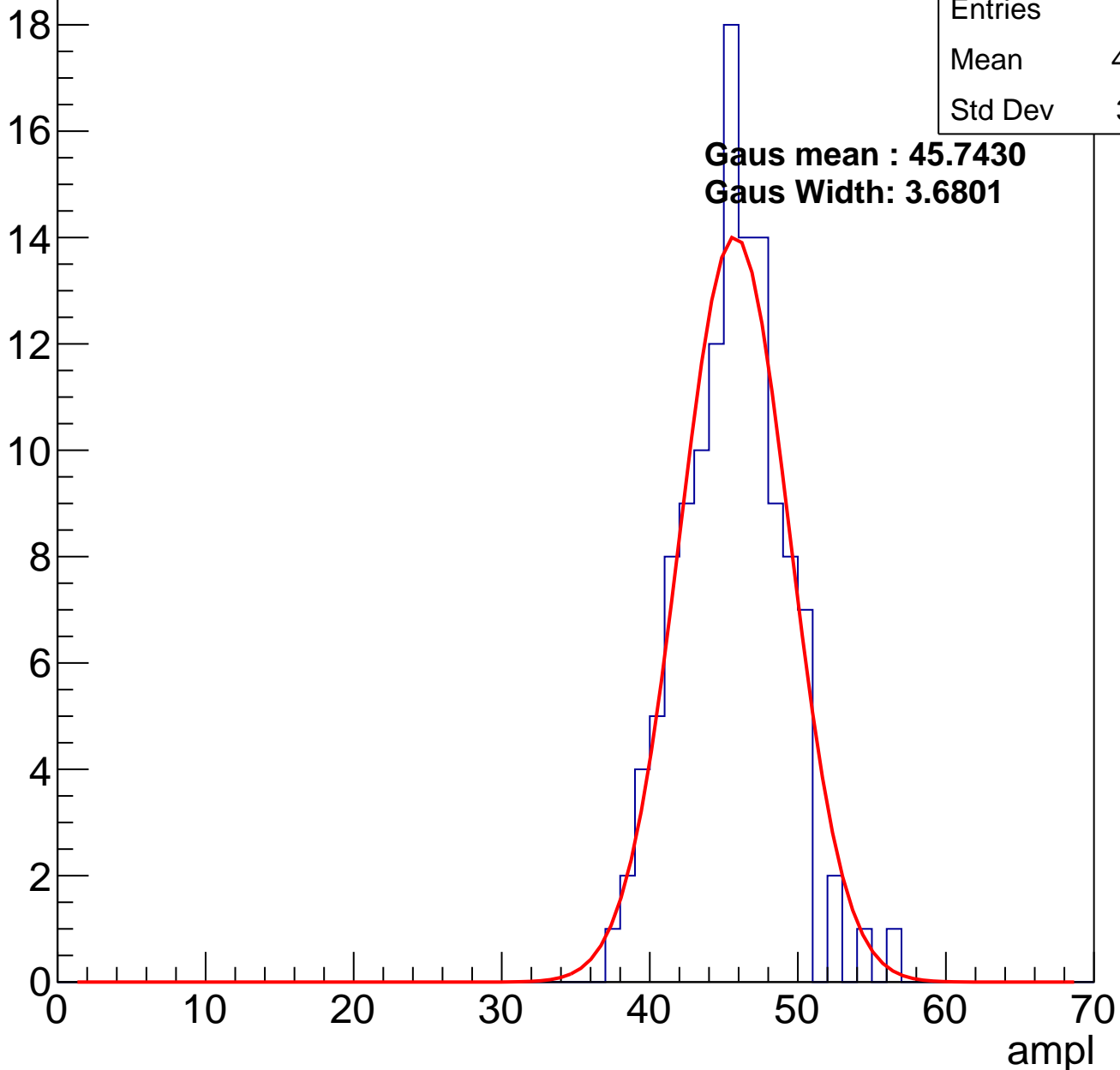
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	45.06
Std Dev	3.401

Gaus mean : 45.7430

Gaus Width: 3.6801

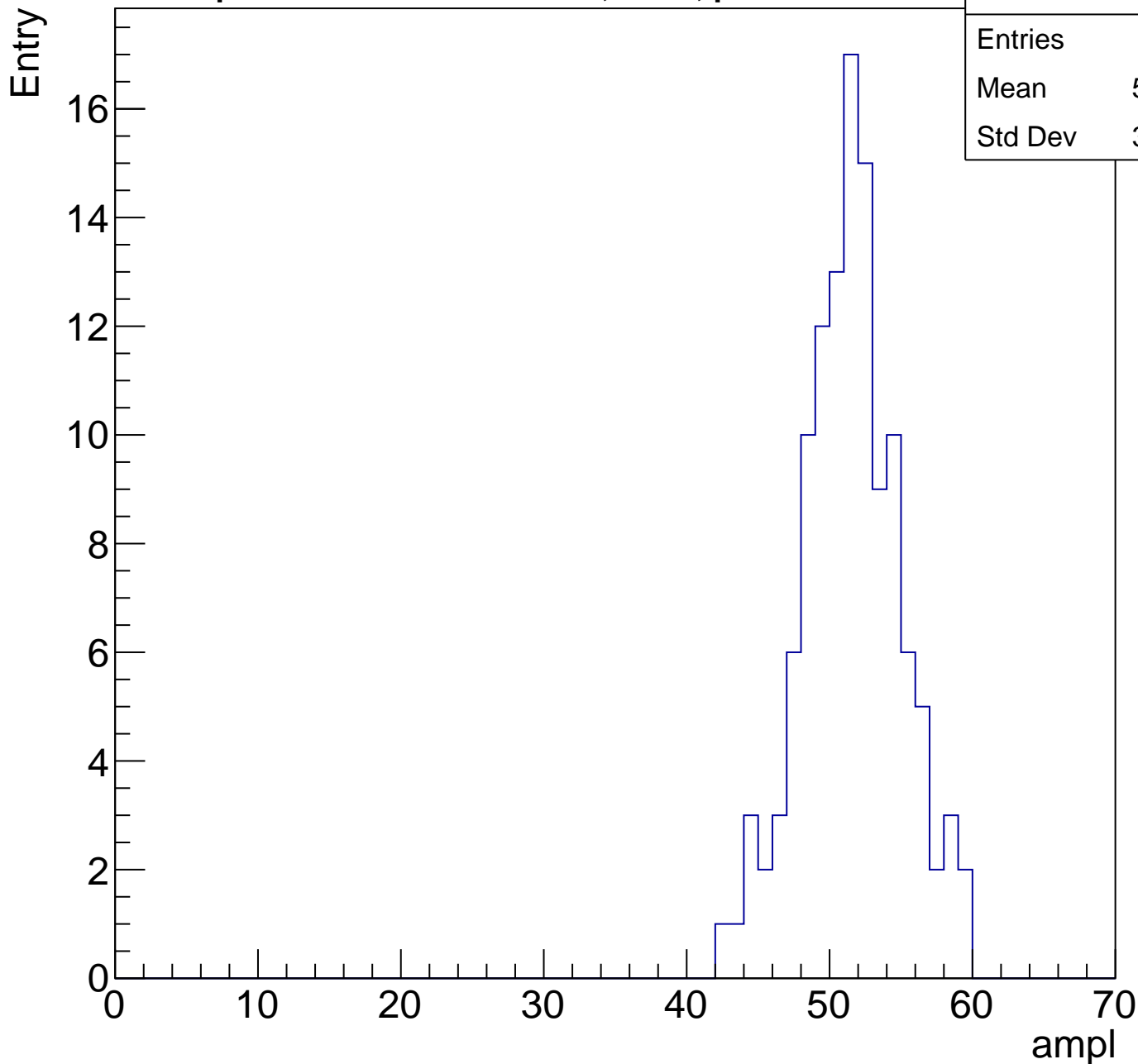
Entry



B0L101S, U24-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	51.04
Std Dev	3.409



B0L101S, U24-ch21, adc4

calib_packv5_042523_0143.root, FC#1, port C1

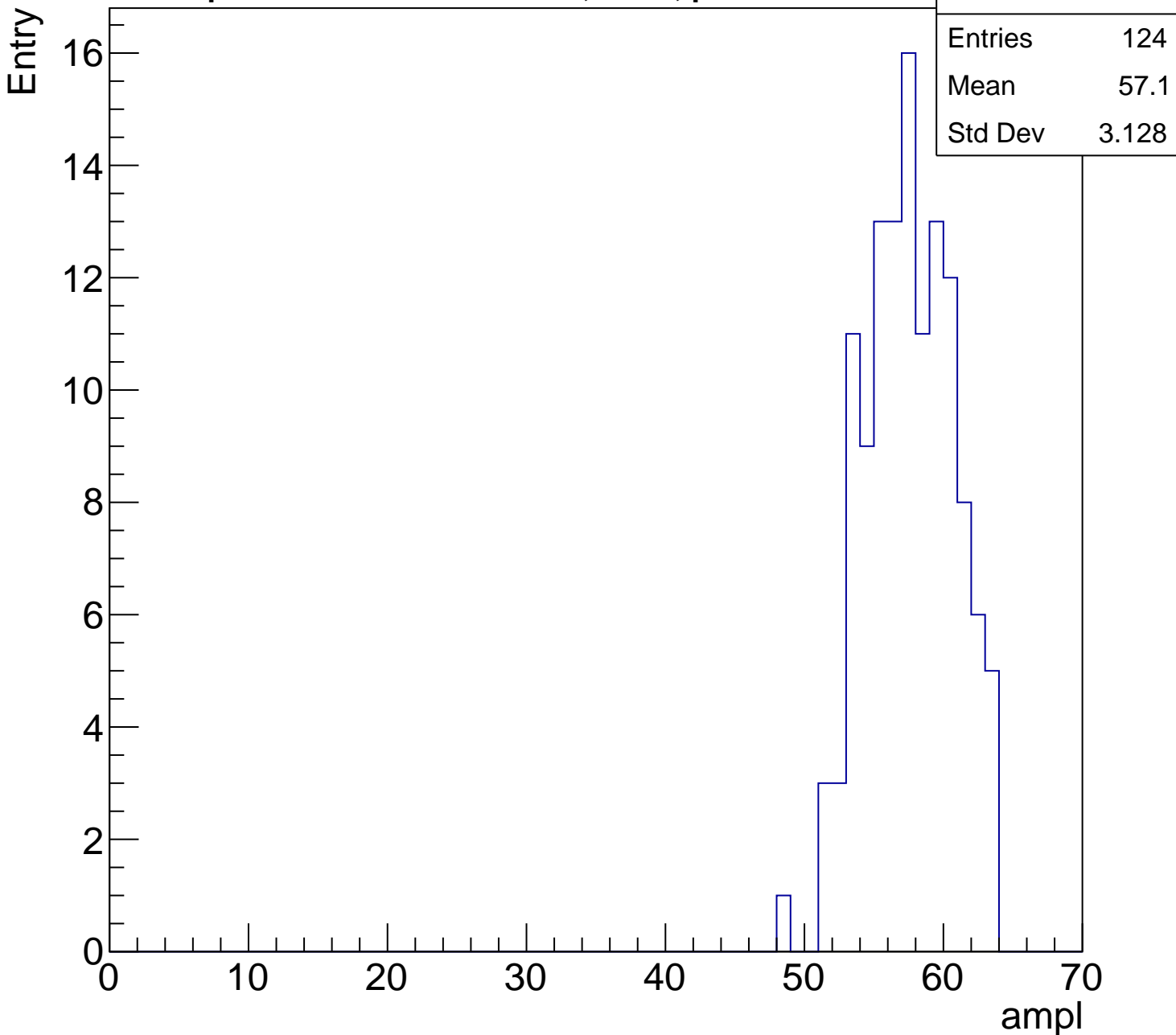
Entries	124
Mean	57.1
Std Dev	3.128

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch21, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	61
Mean	58.74
Std Dev	10.99

Entry

12

10

8

6

4

2

0

0

10

20

30

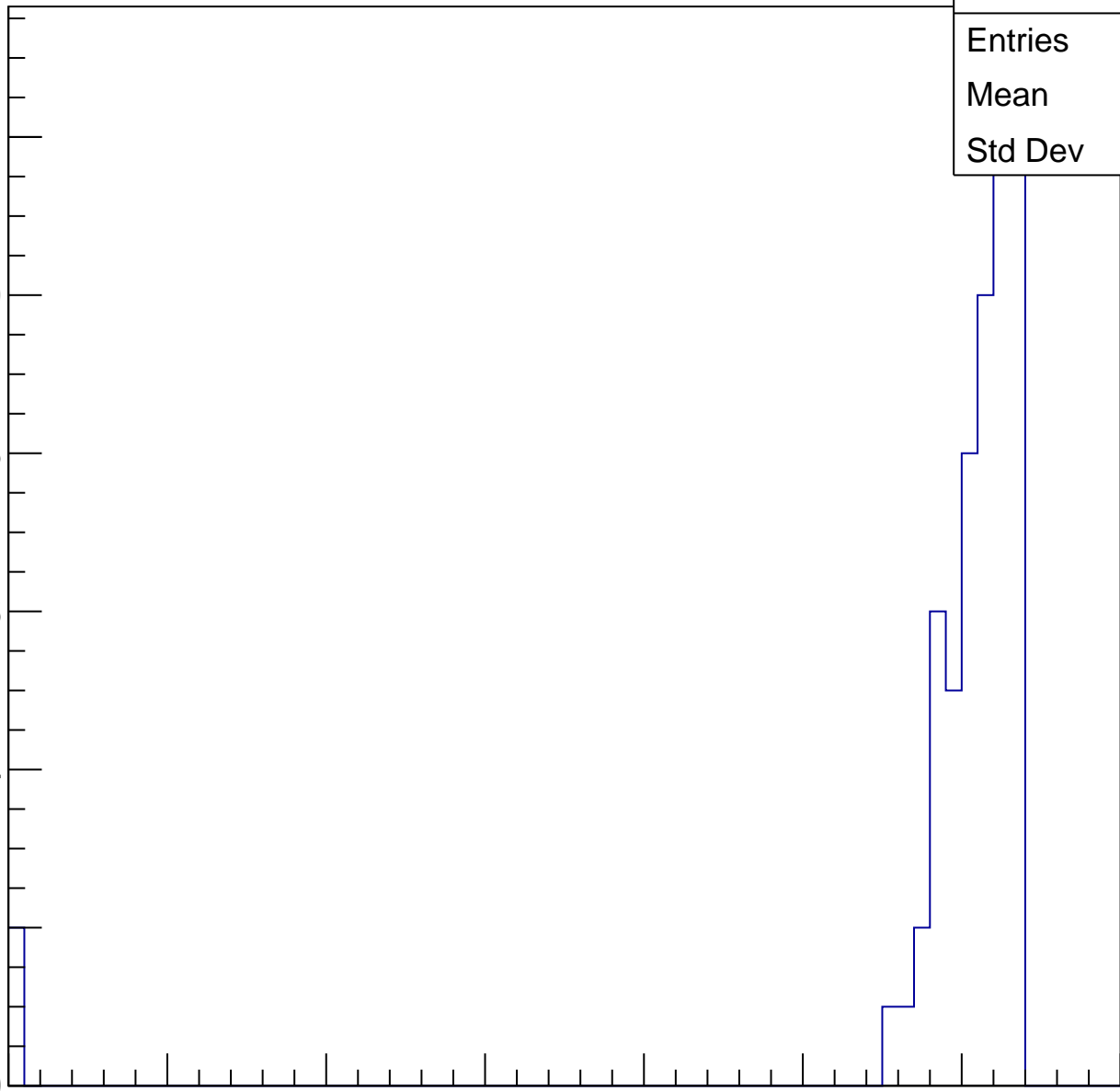
40

50

60

70

ampl



B0L101S, U24-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch22, adc0

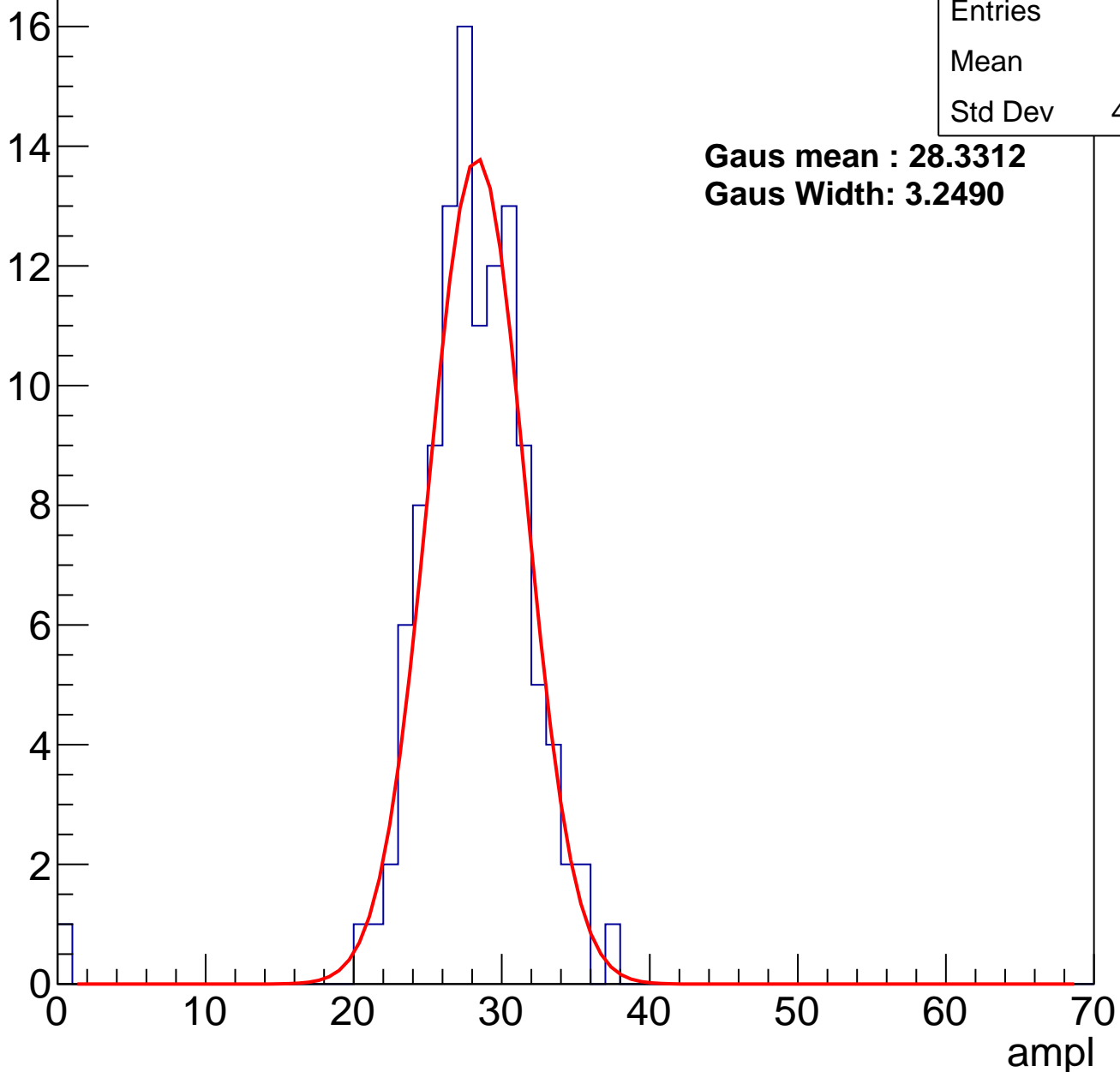
calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	27.6
Std Dev	4.104

Gaus mean : 28.3312

Gaus Width: 3.2490

Entry



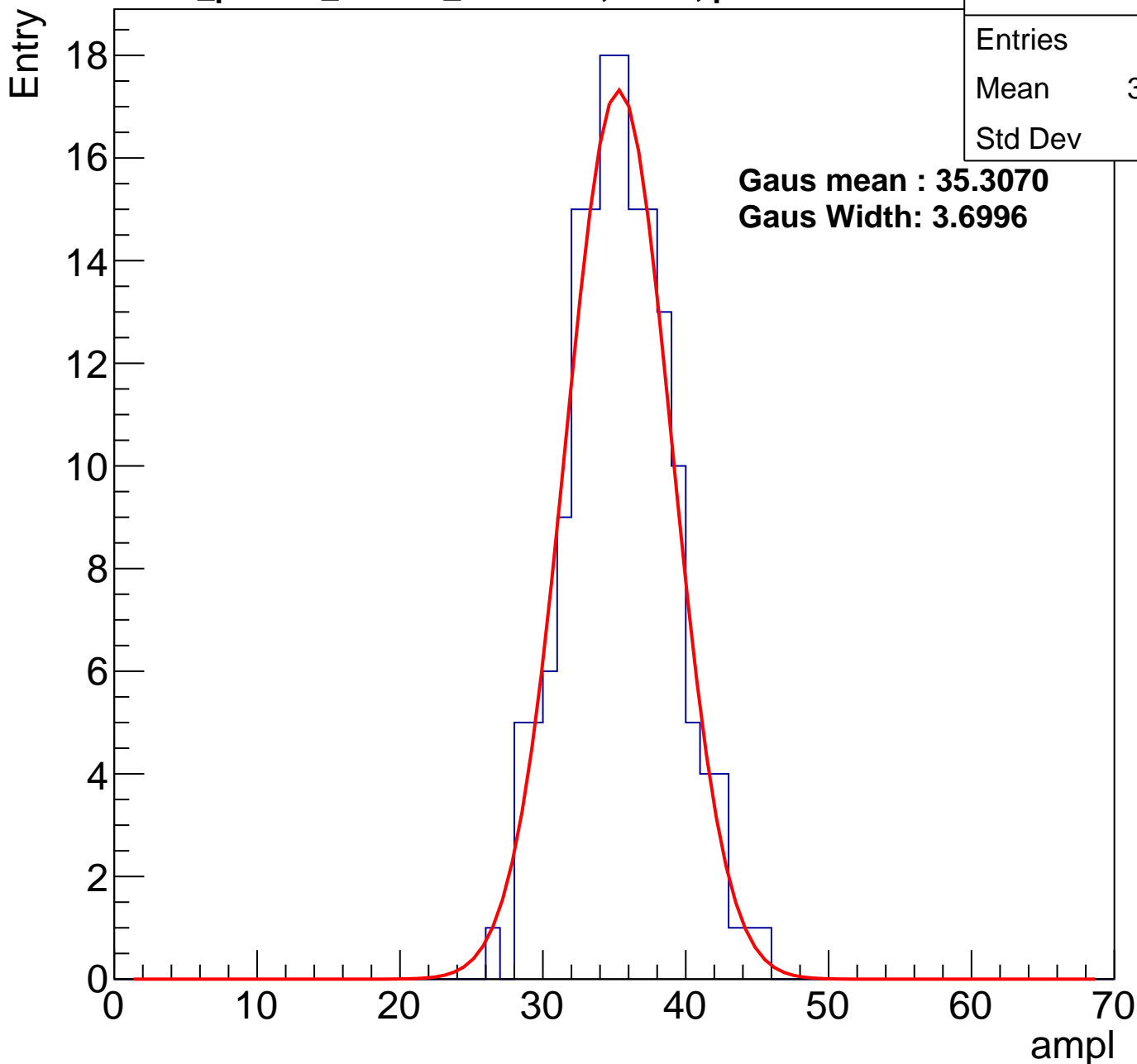
B0L101S, U24-ch22, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	34.97
Std Dev	3.59

Gaus mean : 35.3070

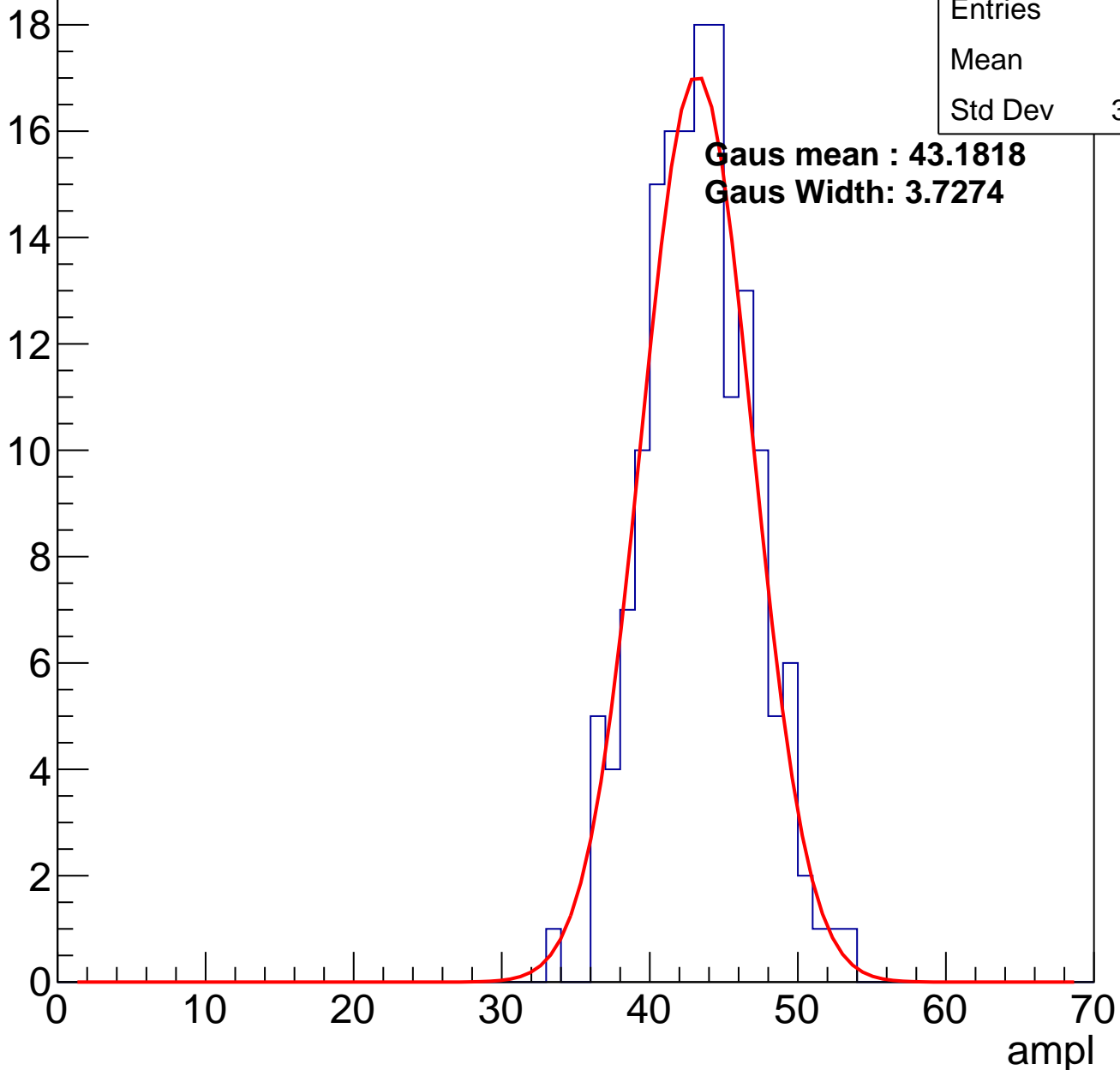
Gaus Width: 3.6996



B0L101S, U24-ch22, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

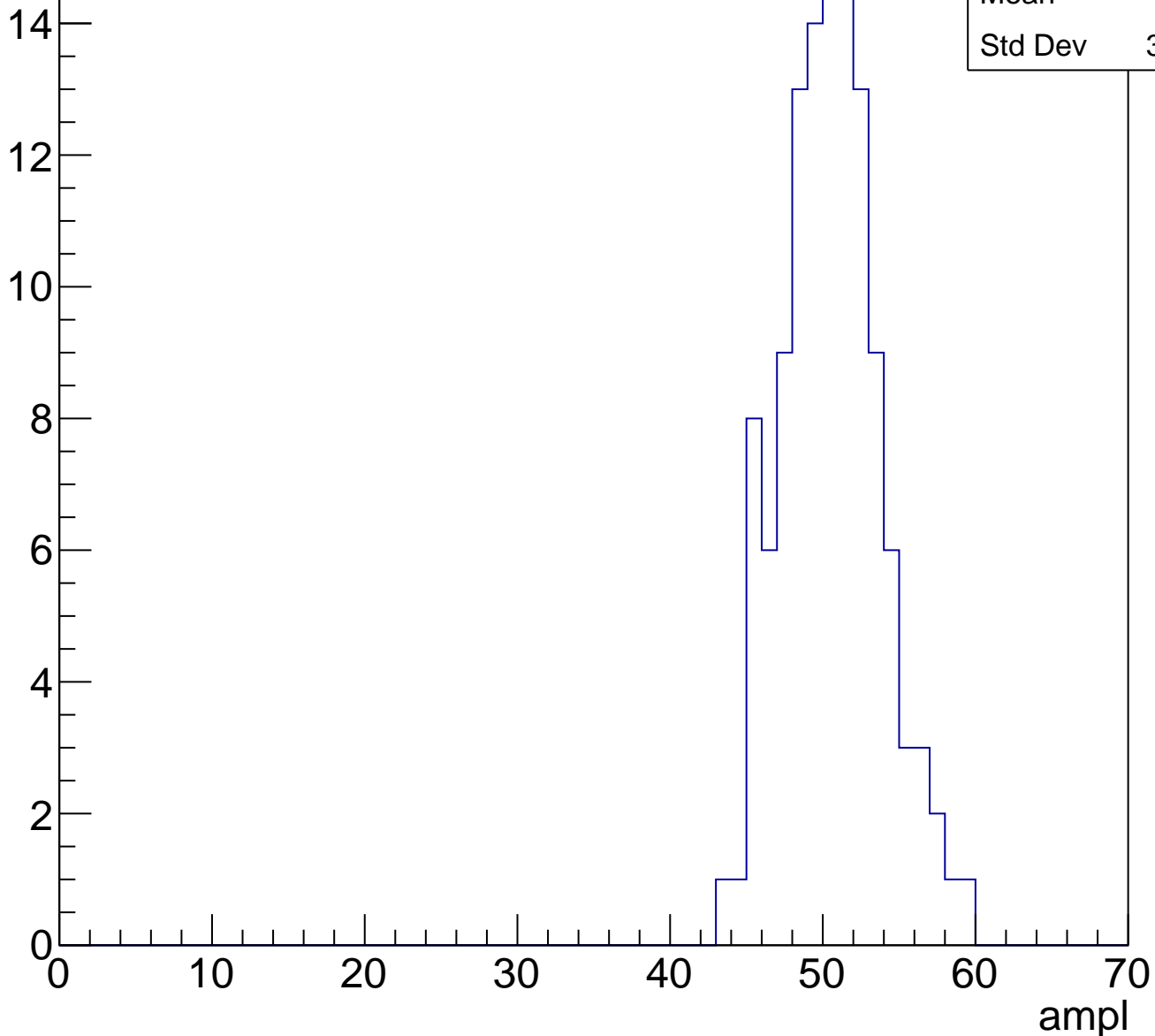


B0L101S, U24-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	50.1
Std Dev	3.158

Entry



B0L101S, U24-ch22, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

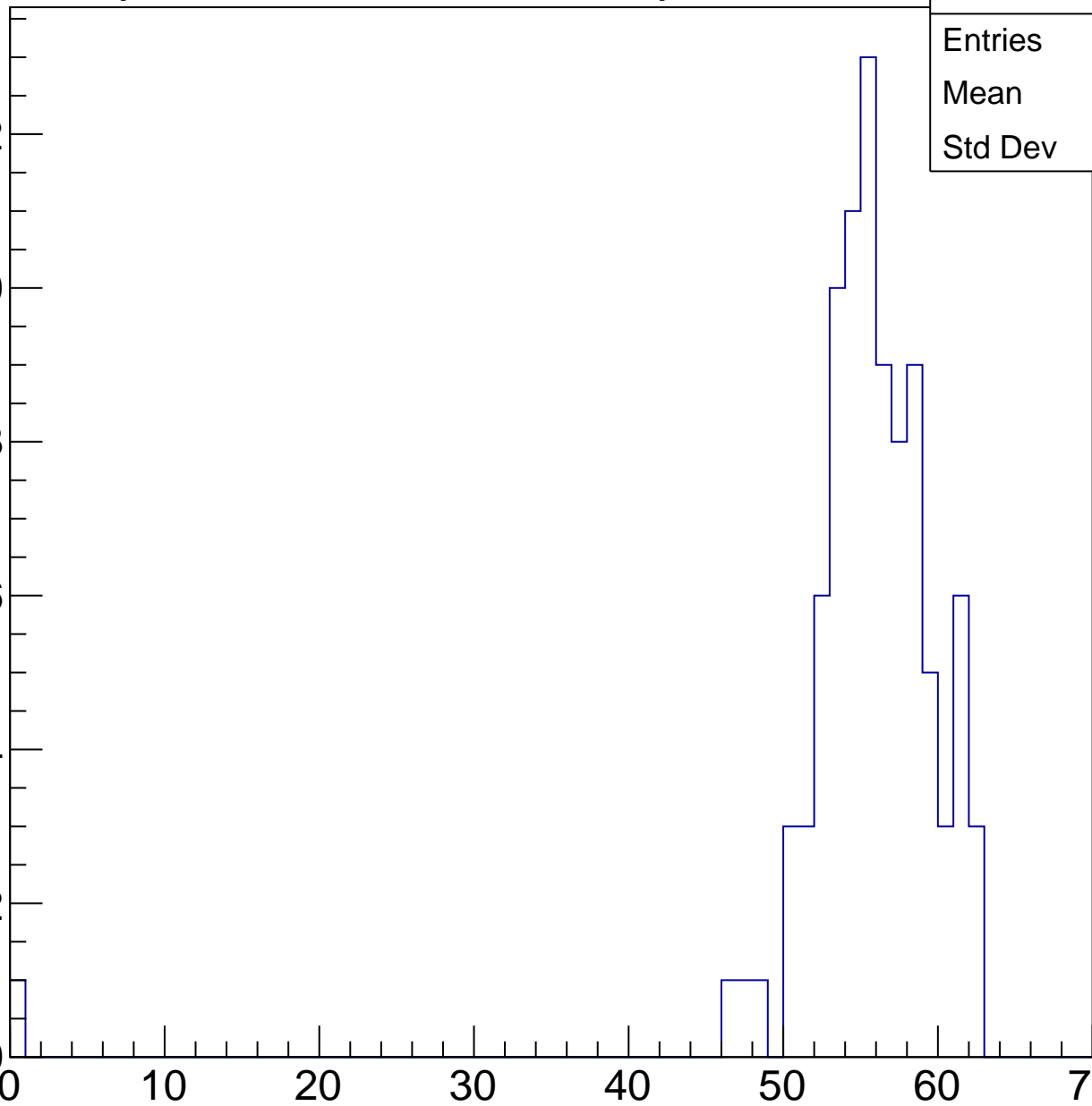
4

2

0

Entries	93
Mean	54.88
Std Dev	6.628

ampl



B0L101S, U24-ch22, adc5

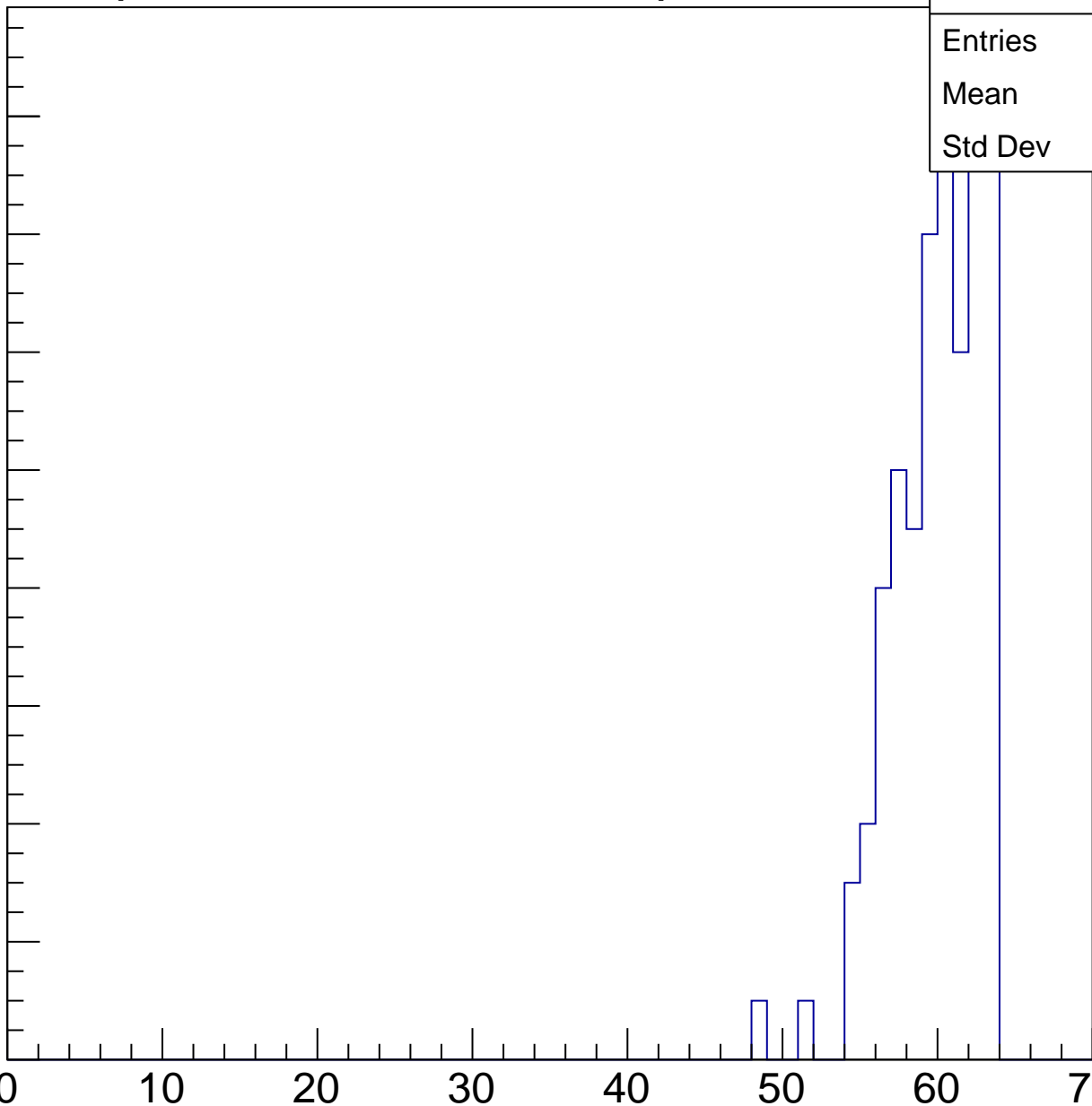
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	59.47
Std Dev	2.825

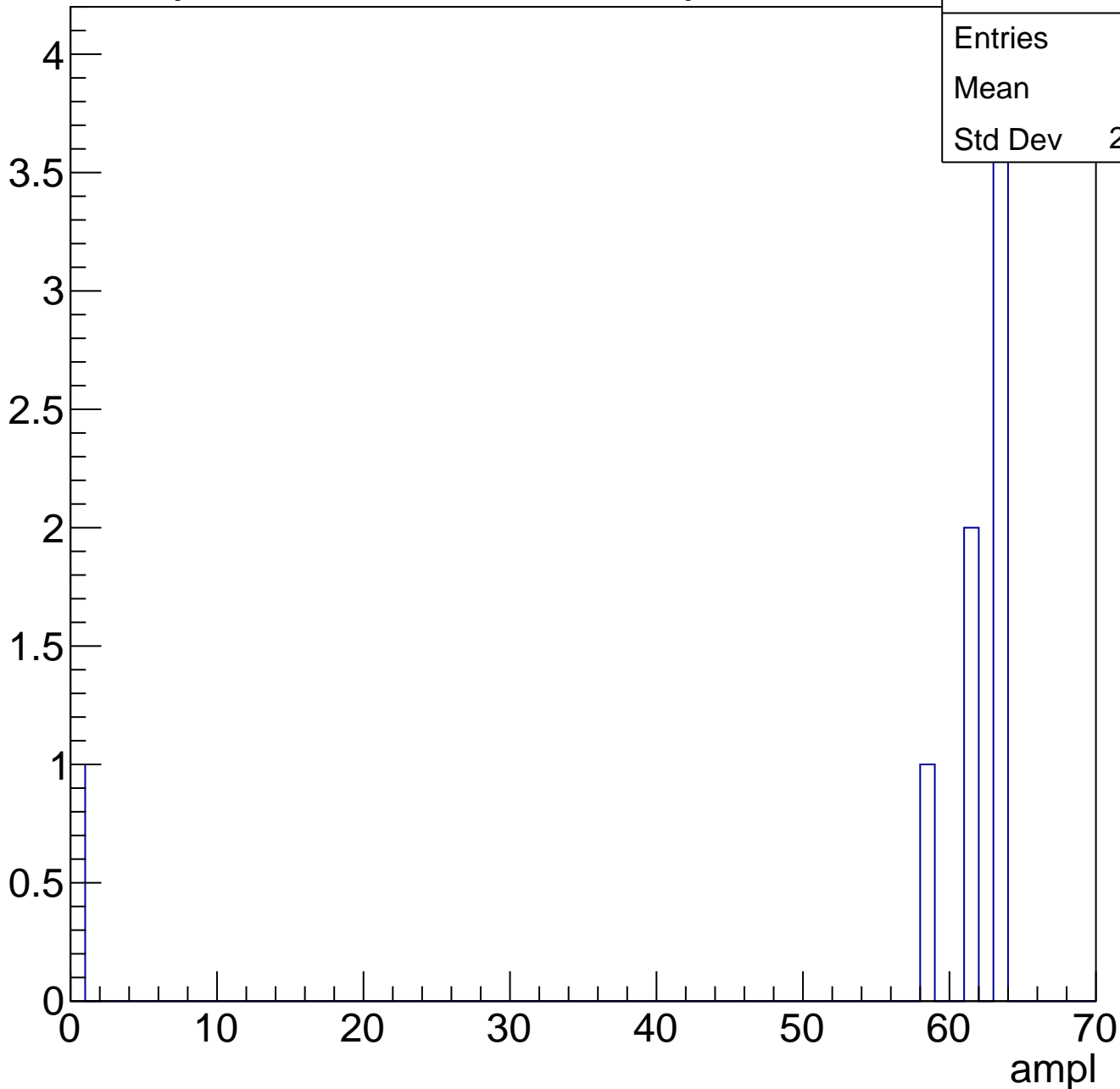
ampl



B0L101S, U24-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch23, adc0

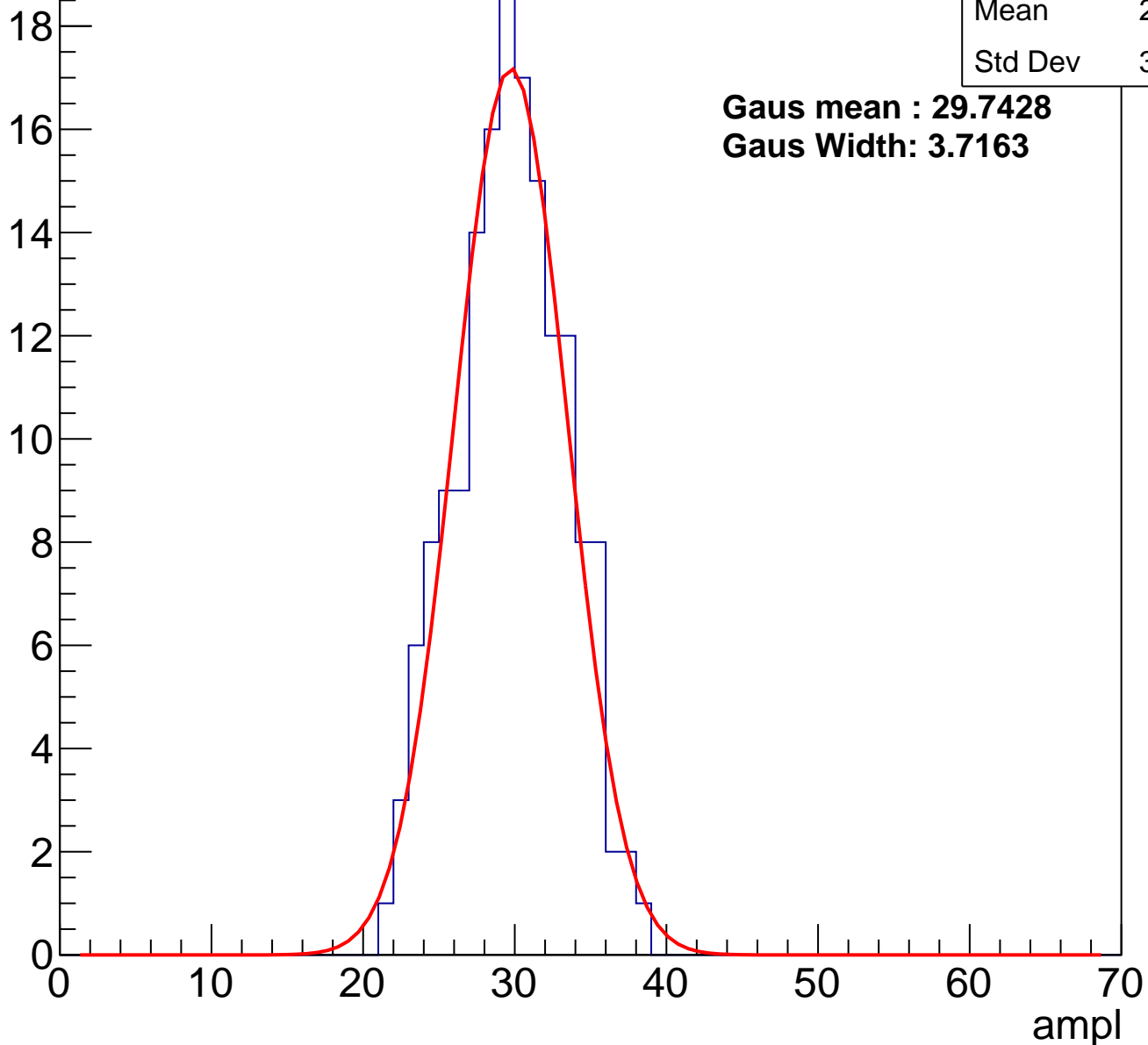
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	29.28
Std Dev	3.586

Gaus mean : 29.7428

Gaus Width: 3.7163

Entry



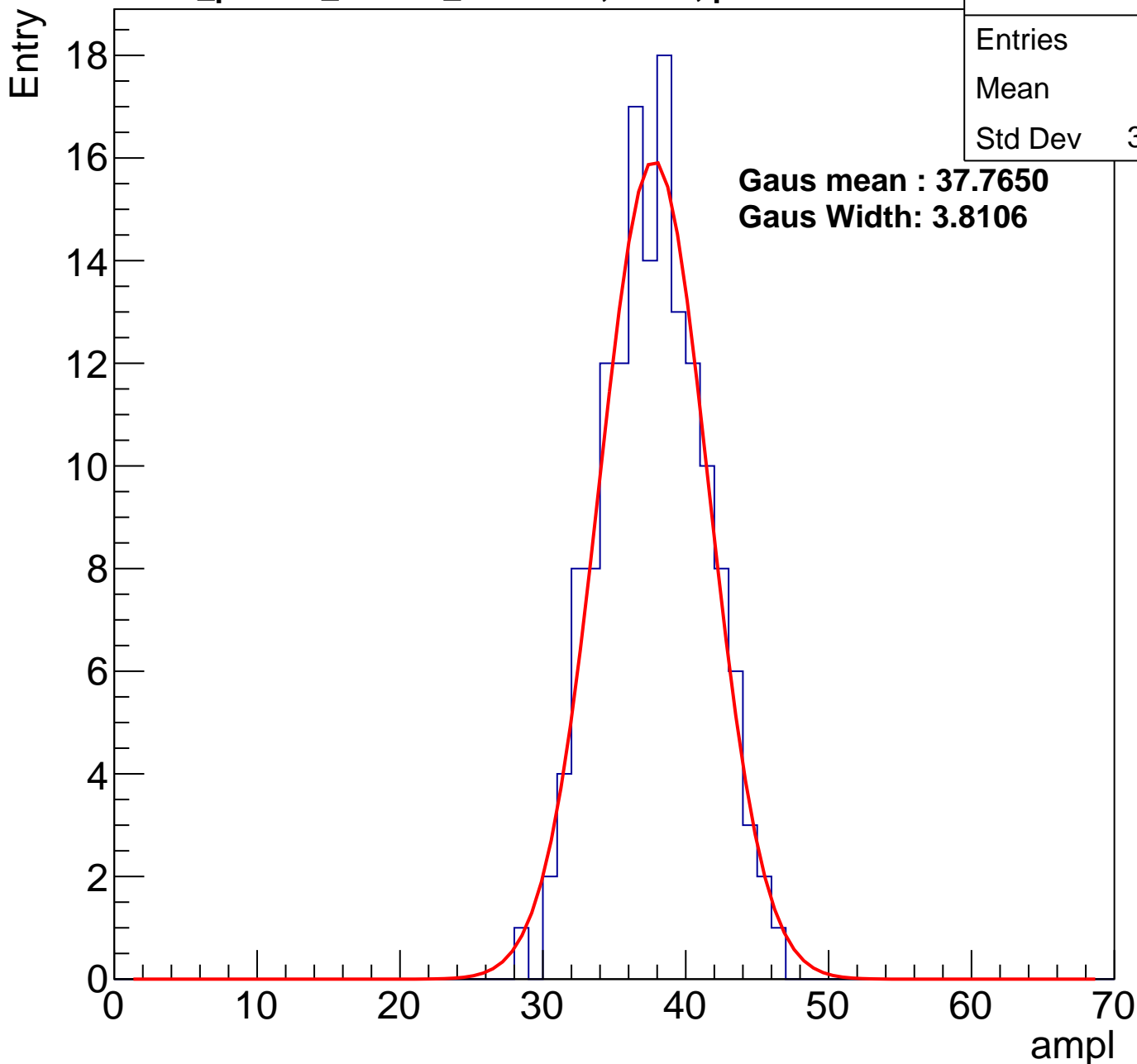
B0L101S, U24-ch23, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	37.3
Std Dev	3.566

Gaus mean : 37.7650

Gaus Width: 3.8106



B0L101S, U24-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

112

Mean

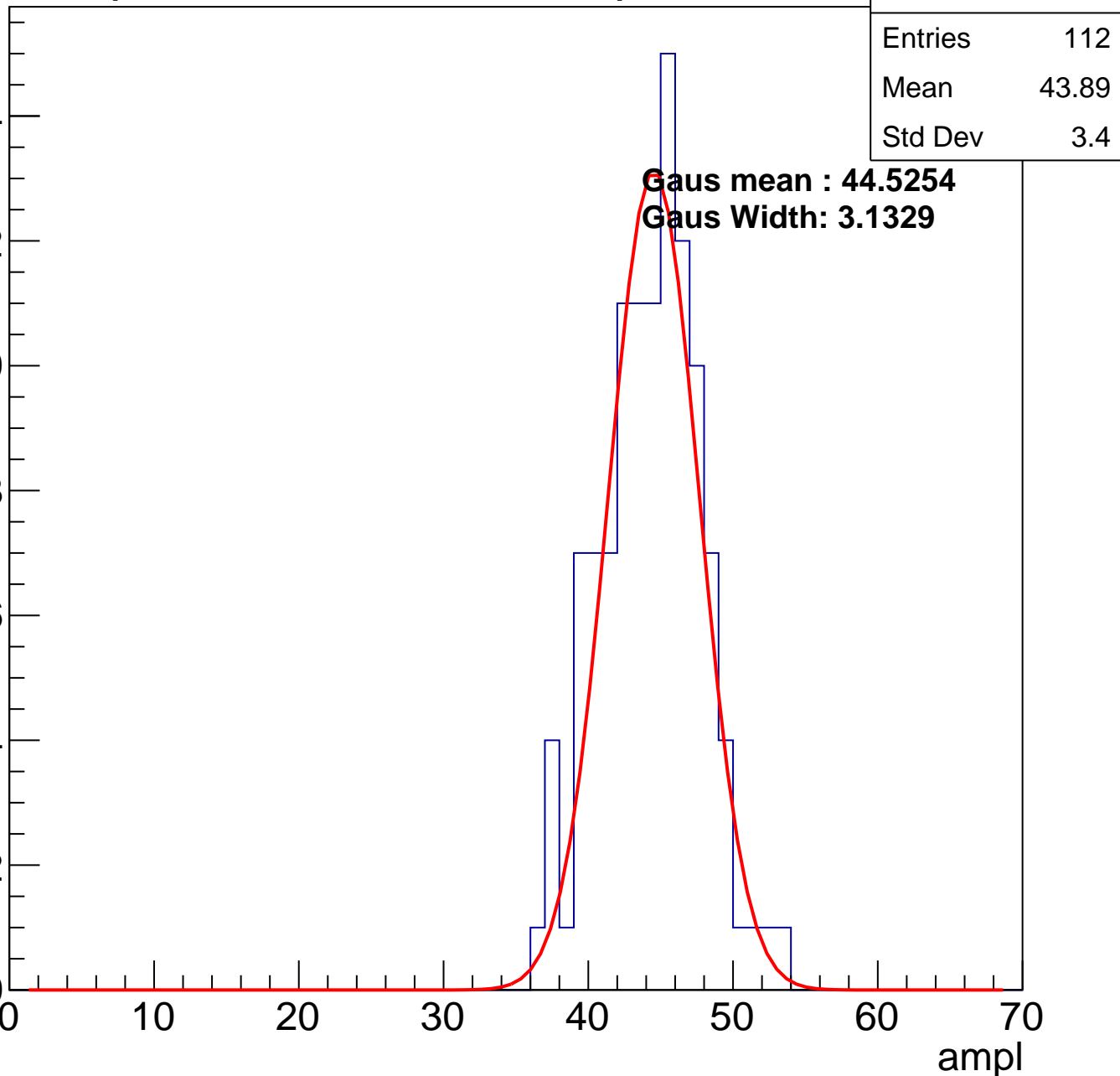
43.89

Std Dev

3.4

Gaus mean : 44.5254

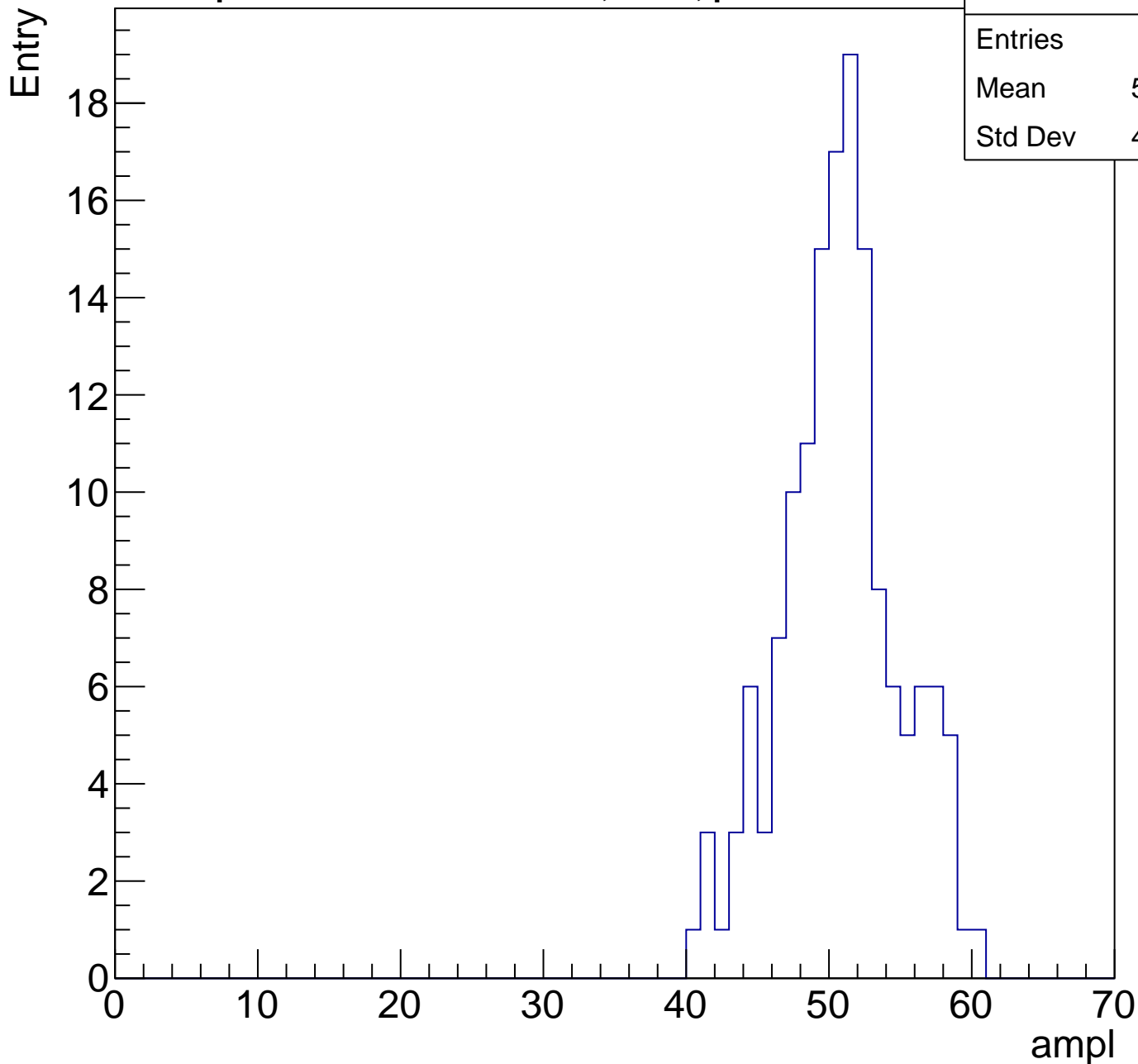
Gaus Width: 3.1329



B0L101S, U24-ch23, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	50.32
Std Dev	4.075



B0L101S, U24-ch23, adc4

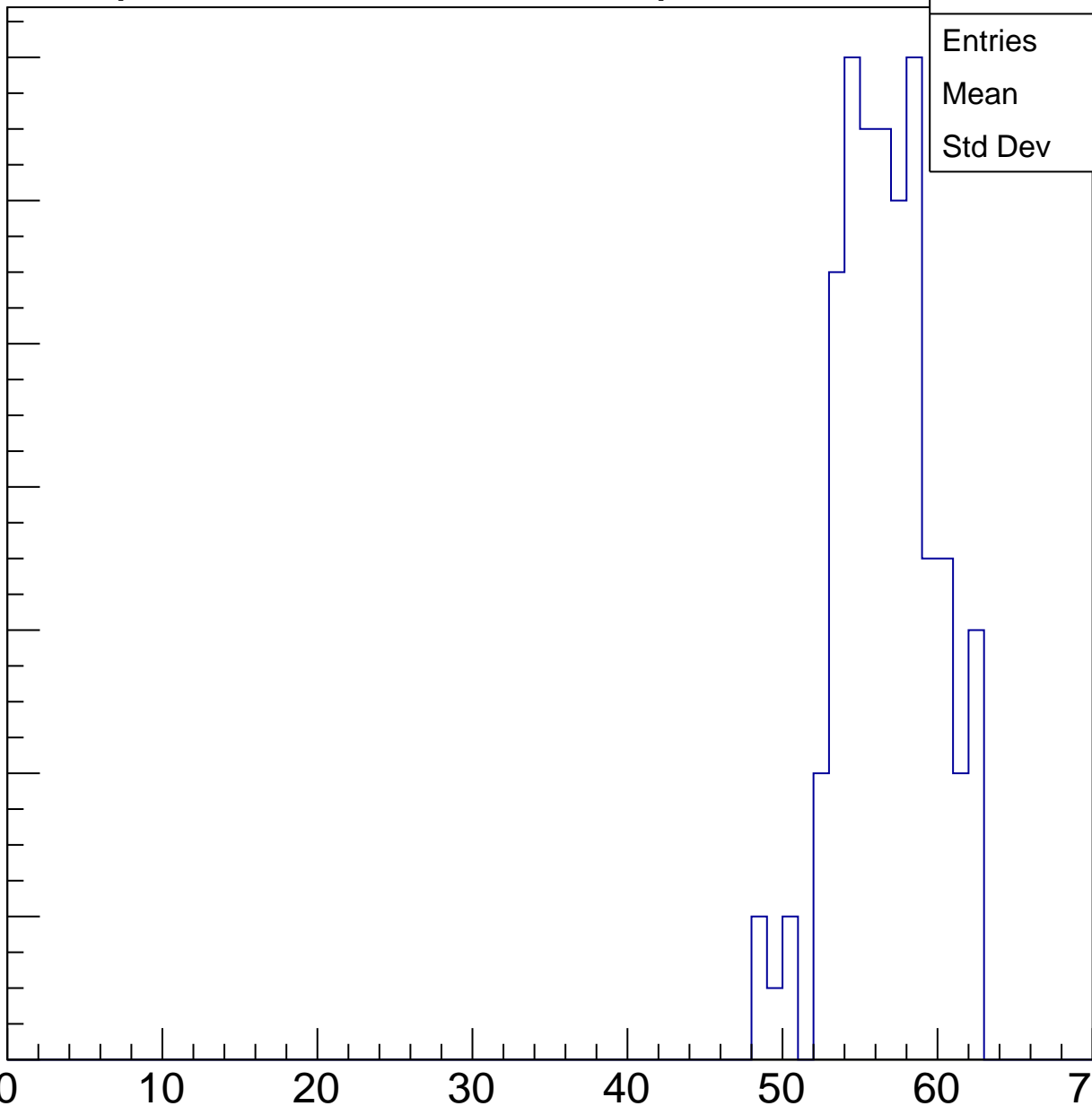
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	56.18
Std Dev	3.069

ampl



B0L101S, U24-ch23, adc5

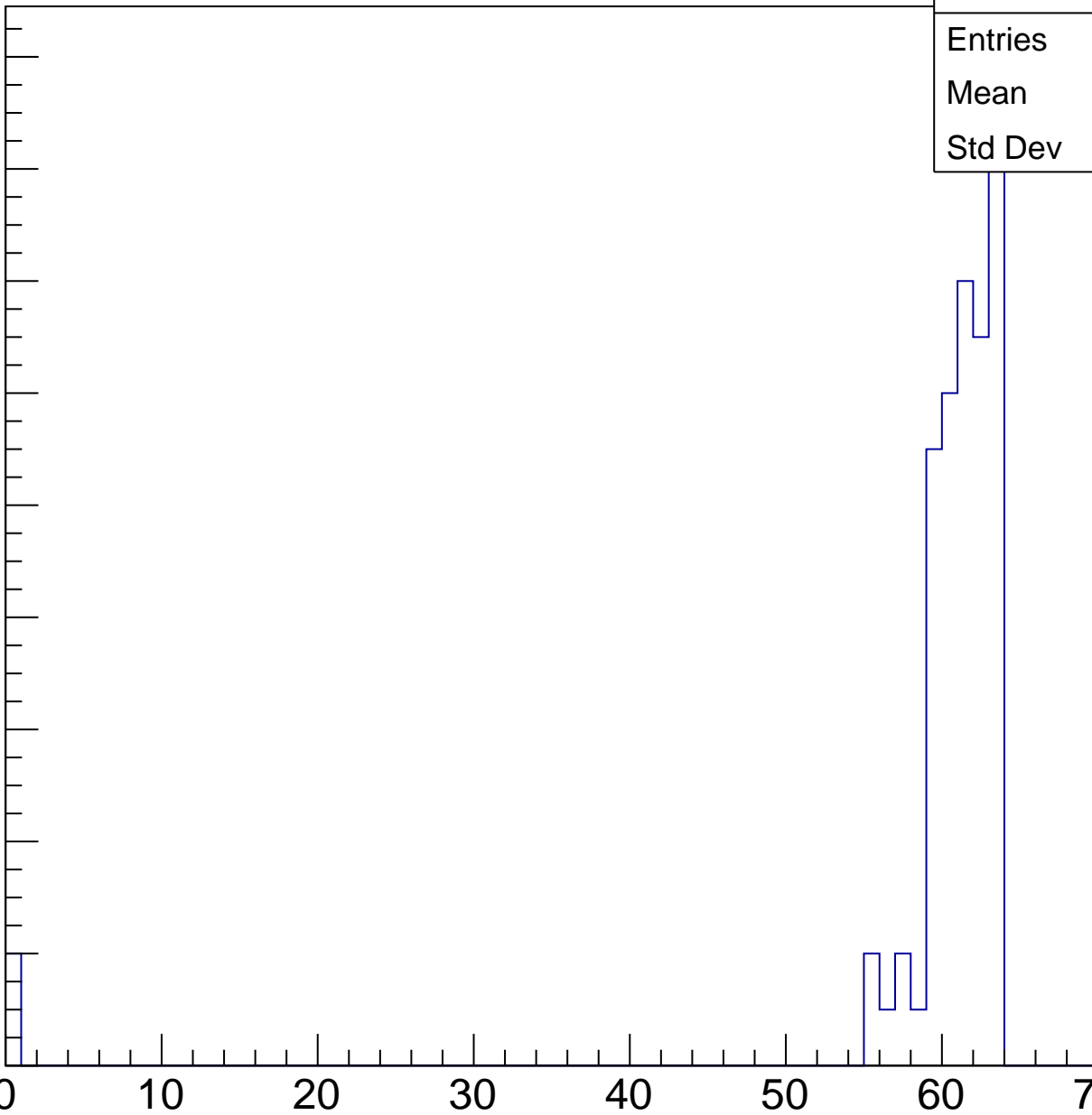
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.22
Std Dev	9.922

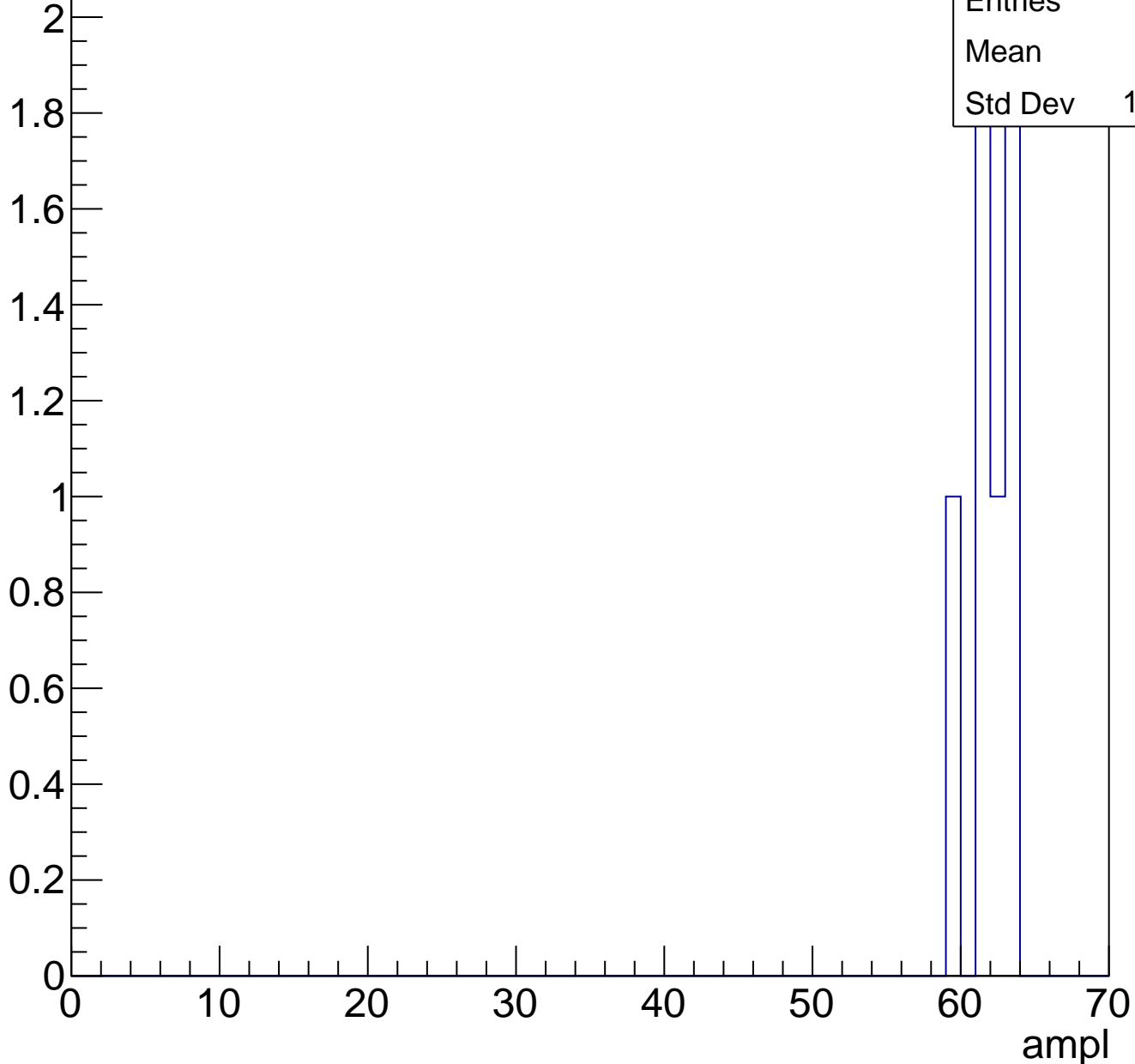
ampl



B0L101S, U24-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L101S, U24-ch24, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	29
Std Dev	4.325

Gaus mean : 29.6152

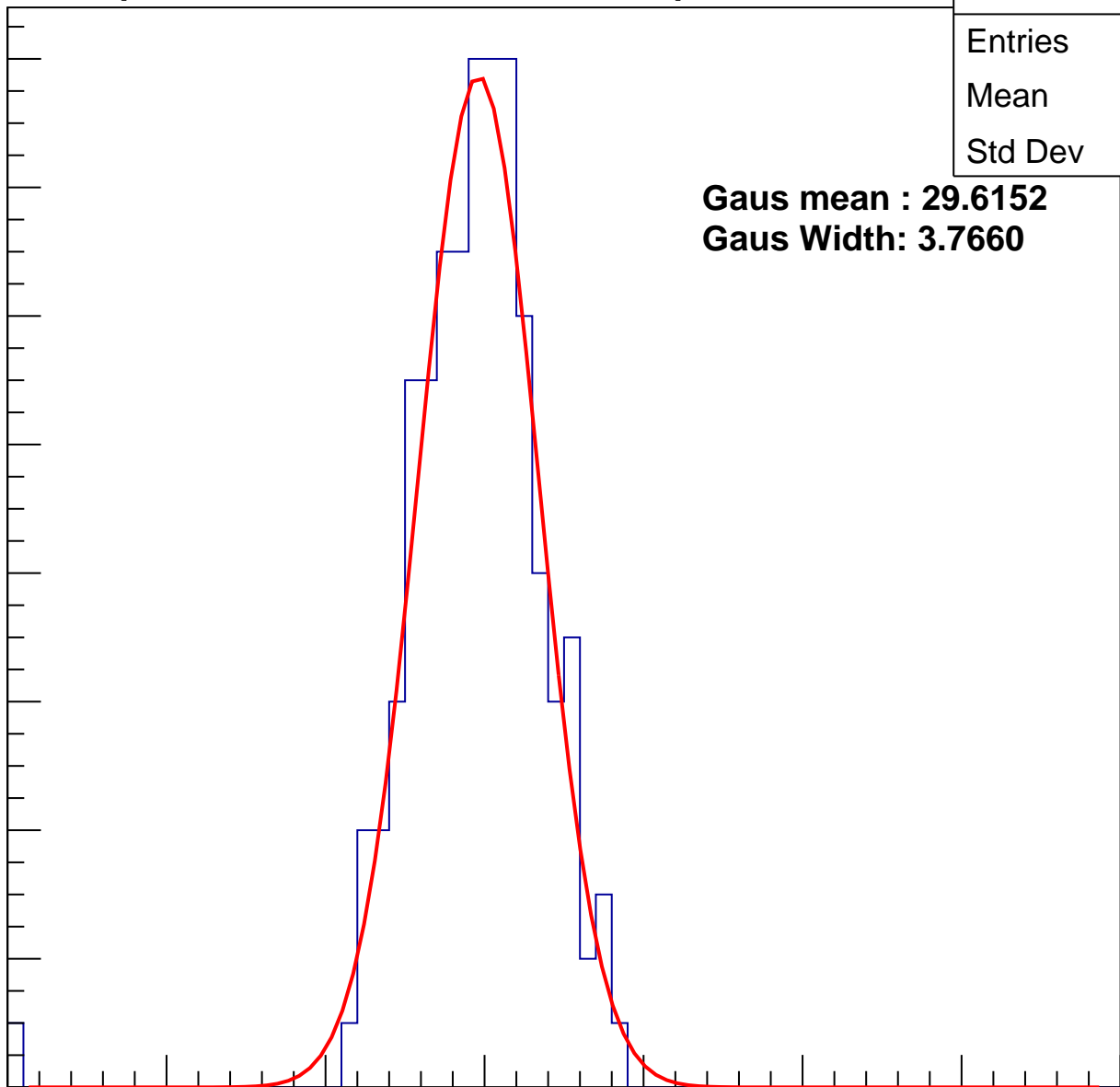
Gaus Width: 3.7660

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch24, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	131
Mean	36.11
Std Dev	3.524

Gaus mean : 36.4731

Gaus Width: 3.5327



B0L101S, U24-ch24, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

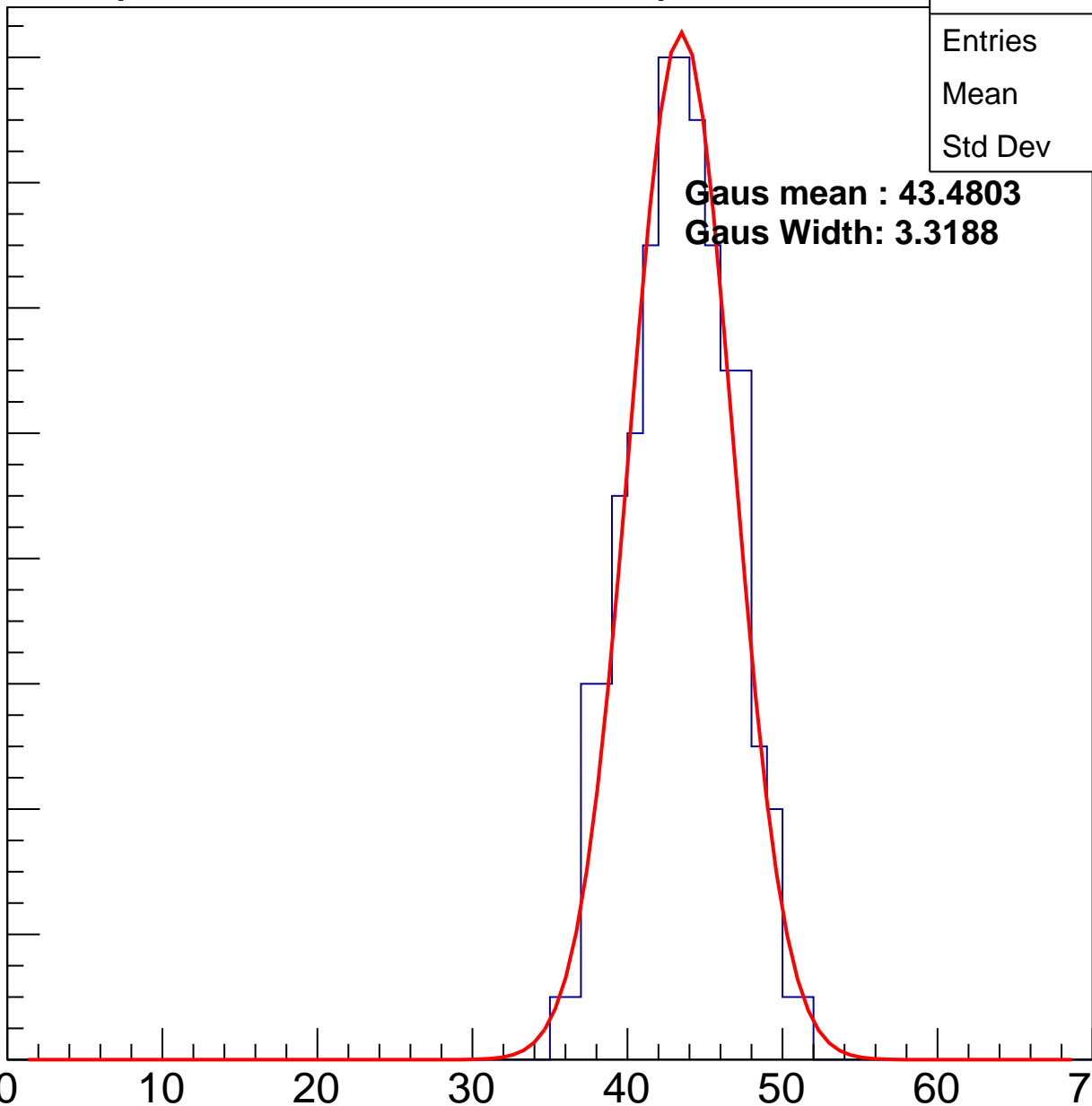
16
14
12
10
8
6
4
2
0

Entries	139
Mean	42.95
Std Dev	3.29

Gaus mean : 43.4803

Gaus Width: 3.3188

ampl



B0L101S, U24-ch24, adc3

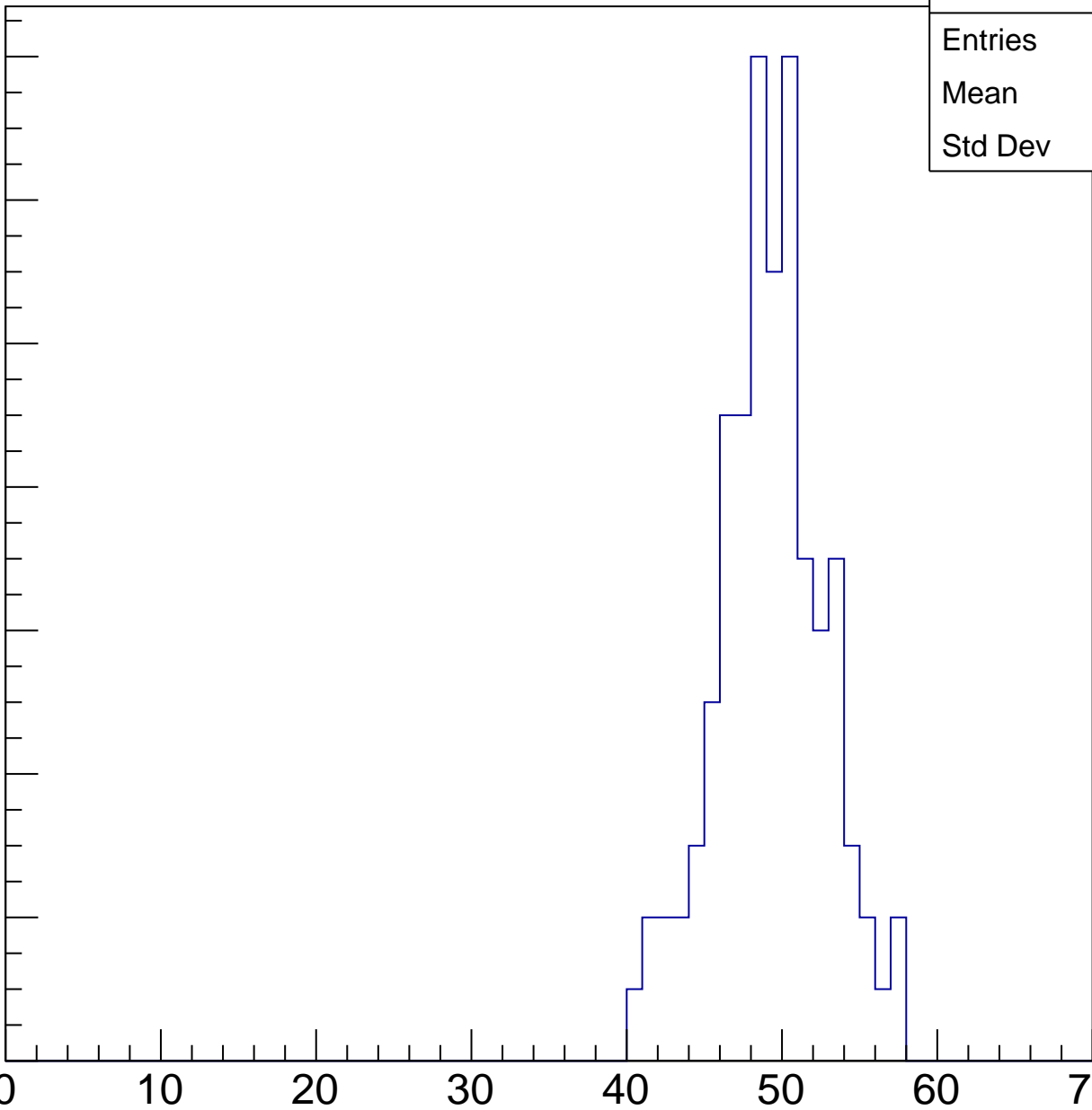
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	48.79
Std Dev	3.453

ampl



B0L101S, U24-ch24, adc4

calib_packv5_042523_0143.root, FC#1, port C1

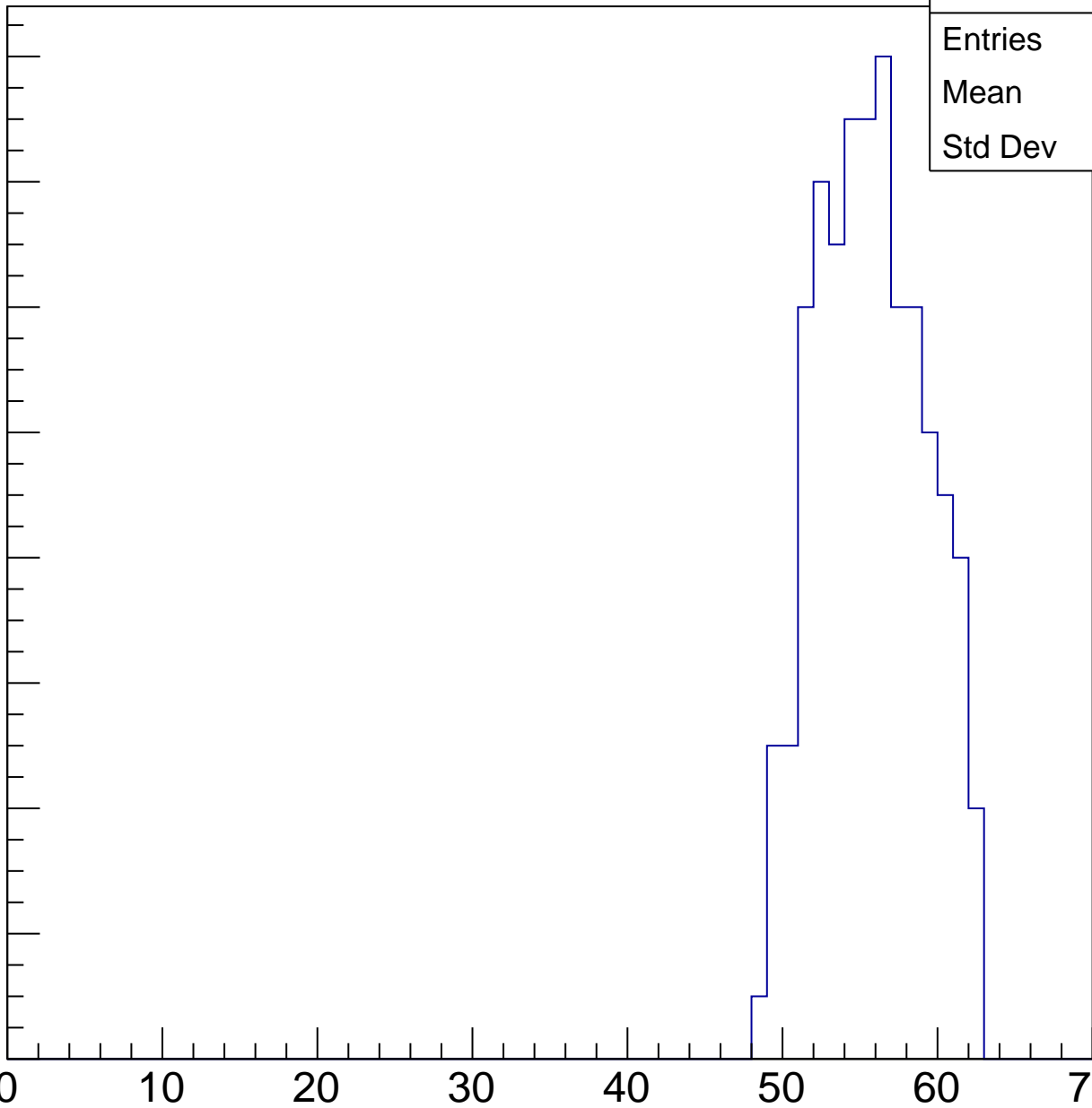
Entries	151
Mean	55.29
Std Dev	3.421

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch24, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

77

Mean

58.29

Std Dev

9.826

ampl

0

10

20

30

40

50

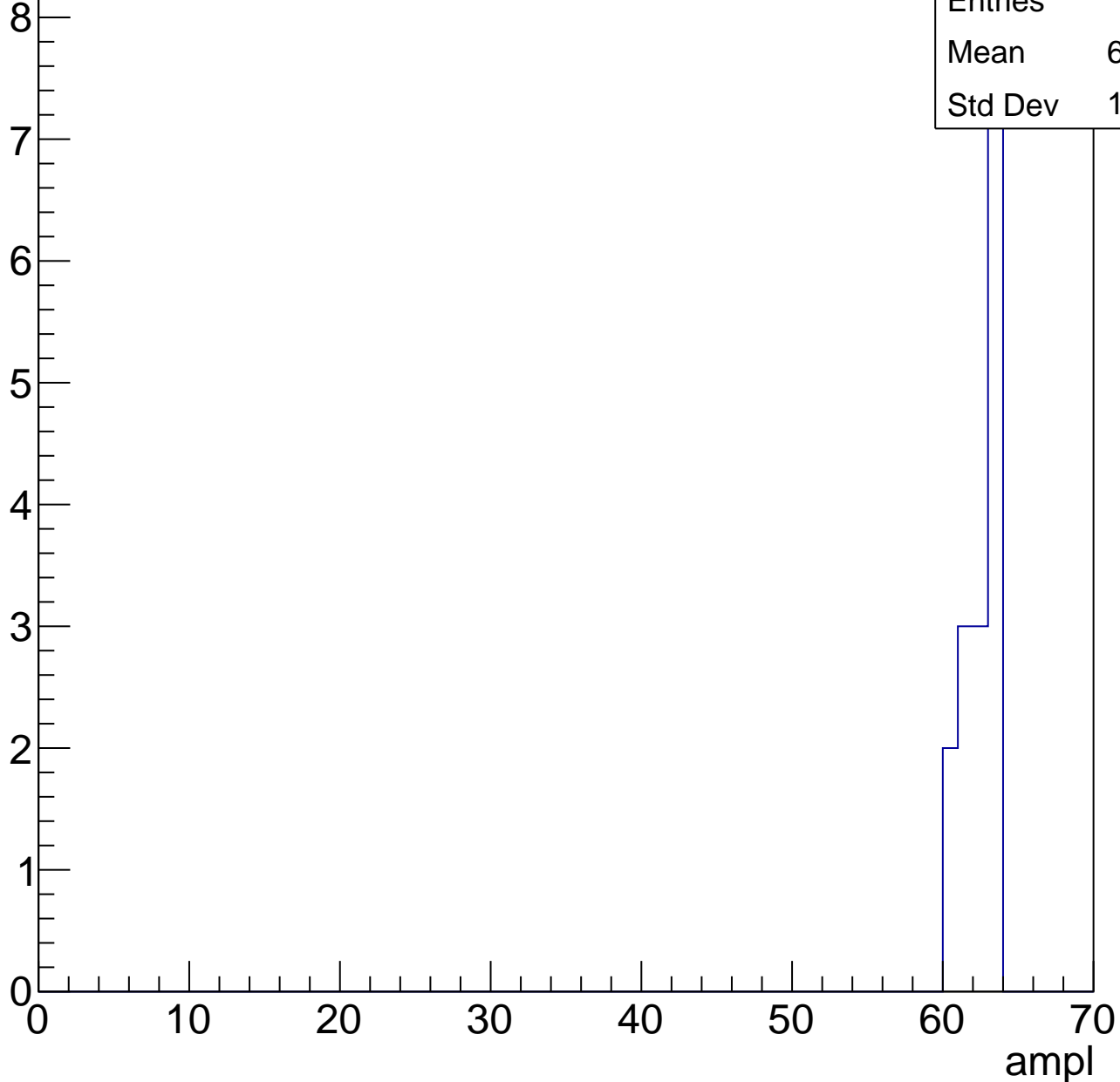
60

70

B0L101S, U24-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch25, adc0

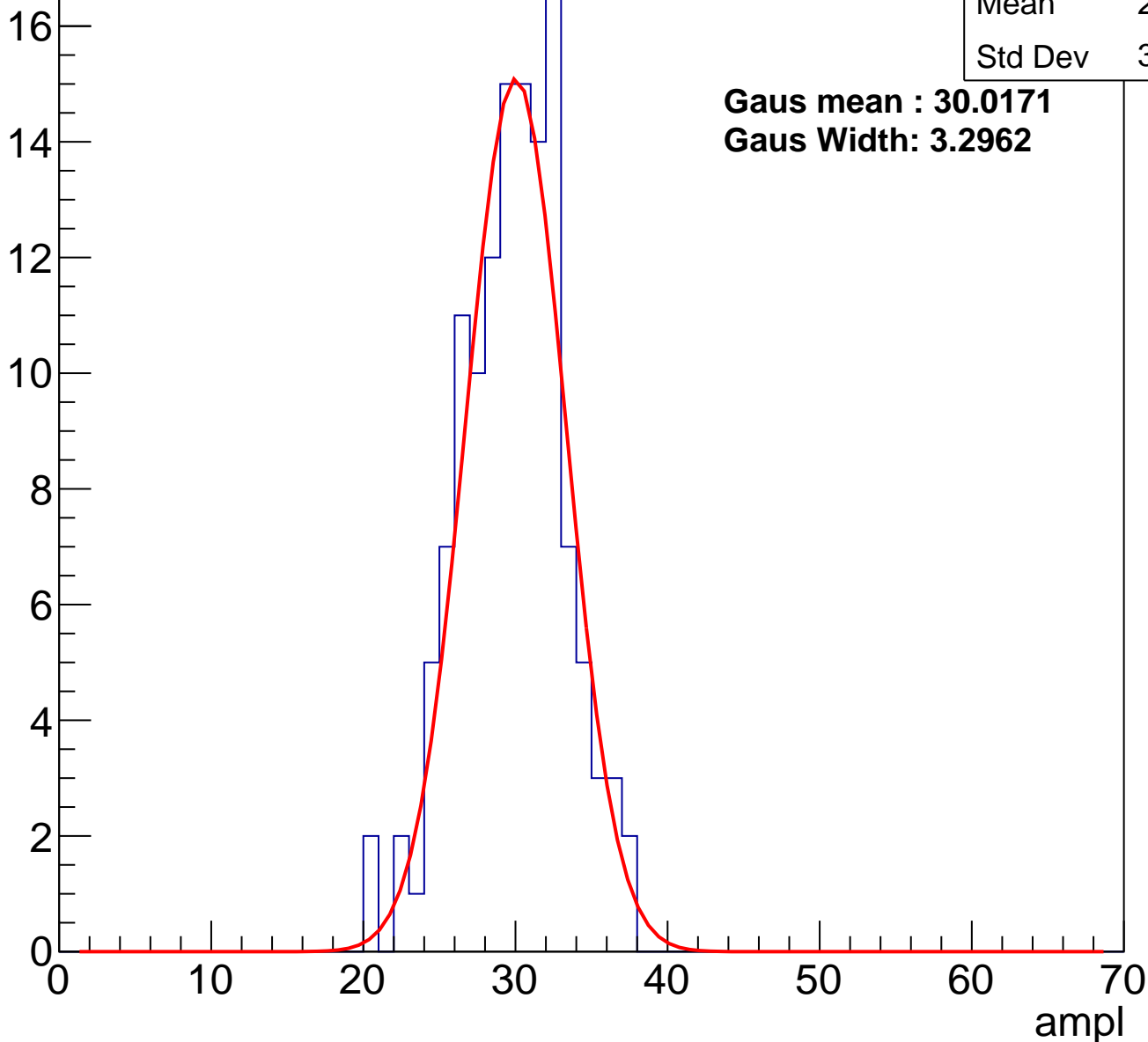
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	131
Mean	29.35
Std Dev	3.397

Gaus mean : 30.0171

Gaus Width: 3.2962

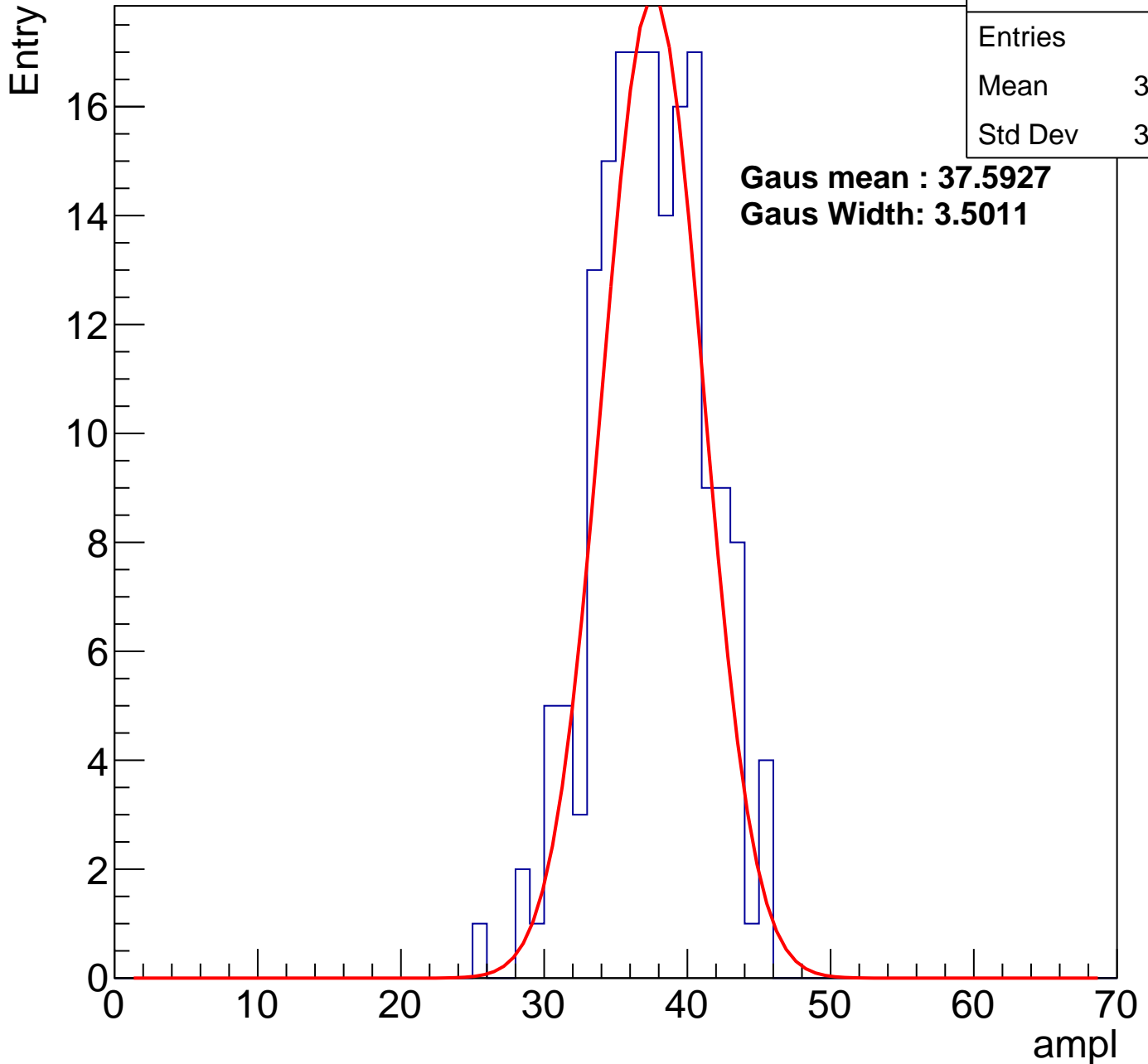


B0L101S, U24-ch25, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	36.99
Std Dev	3.784

Gaus mean : 37.5927
Gaus Width: 3.5011



B0L101S, U24-ch25, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

119

Mean

44.37

Std Dev

3.393

Gaus mean : 44.7688

Gaus Width: 3.0992

0

10

20

30

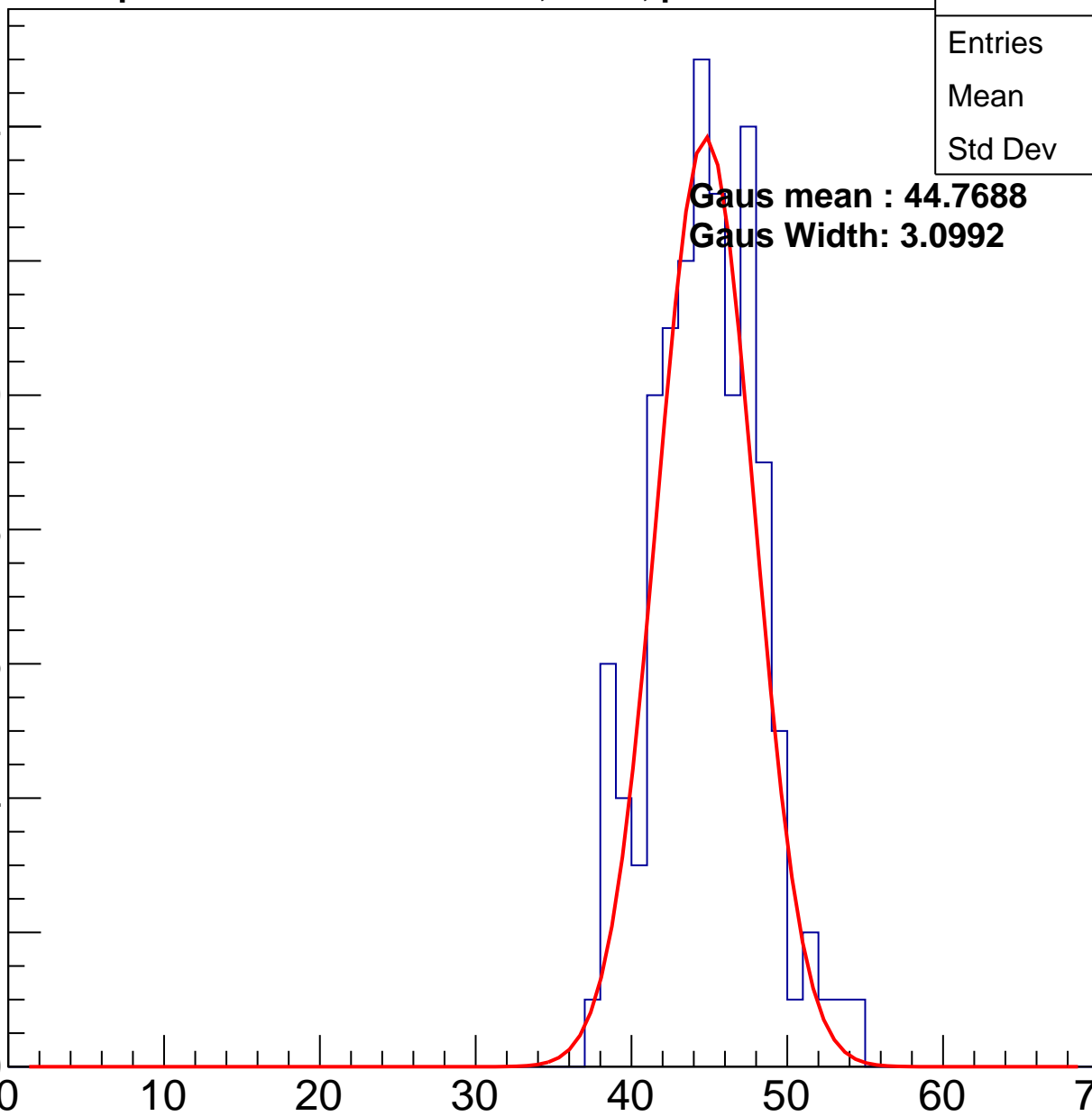
40

50

60

70

ampl

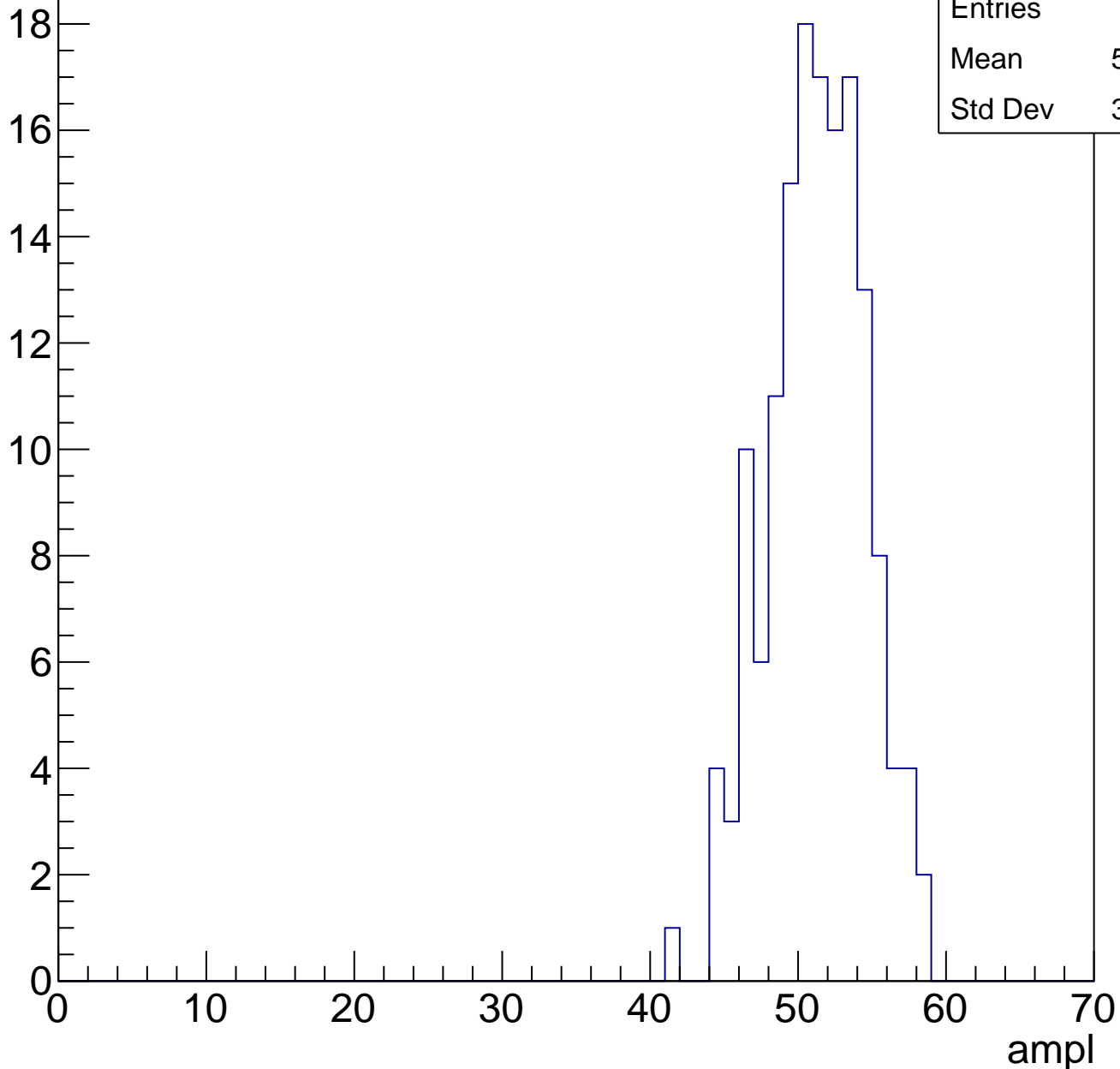


B0L101S, U24-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	50.79
Std Dev	3.268

Entry



B0L101S, U24-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

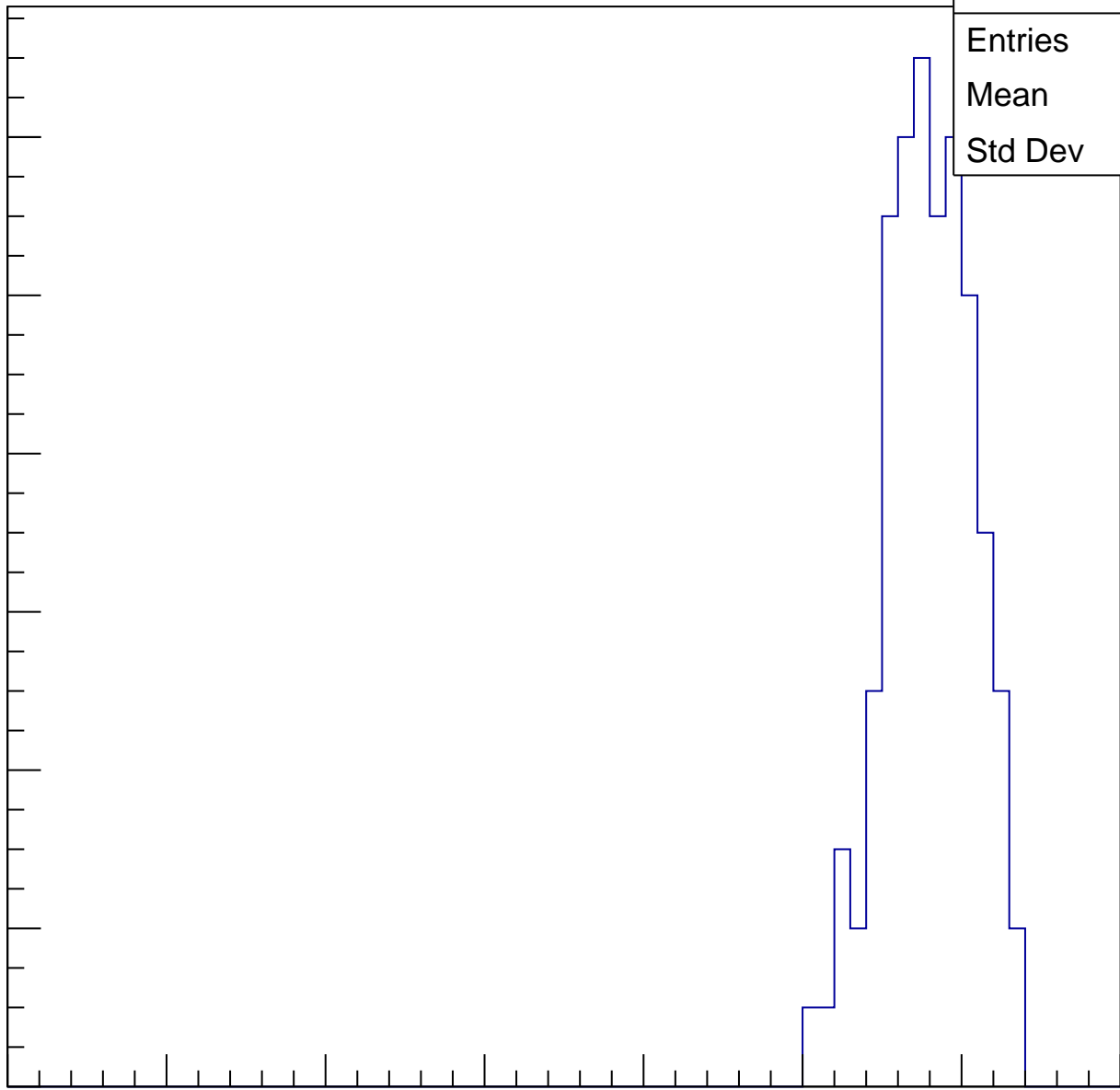
2

0

Entries	95
Mean	57.47
Std Dev	2.771

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch25, adc5

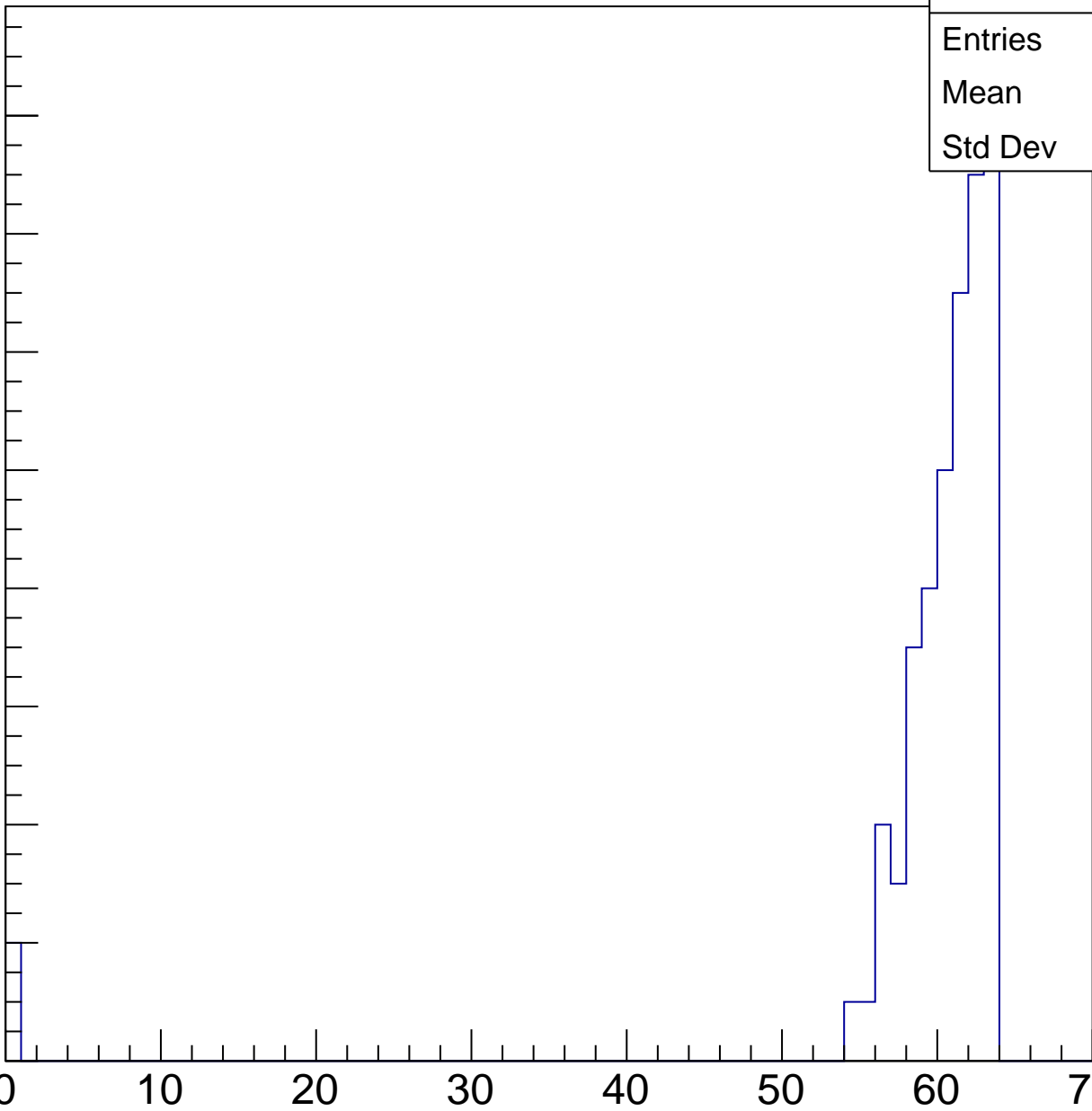
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.96
Std Dev	9.64

ampl



B0L101S, U24-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L101S, U24-ch26, adc0

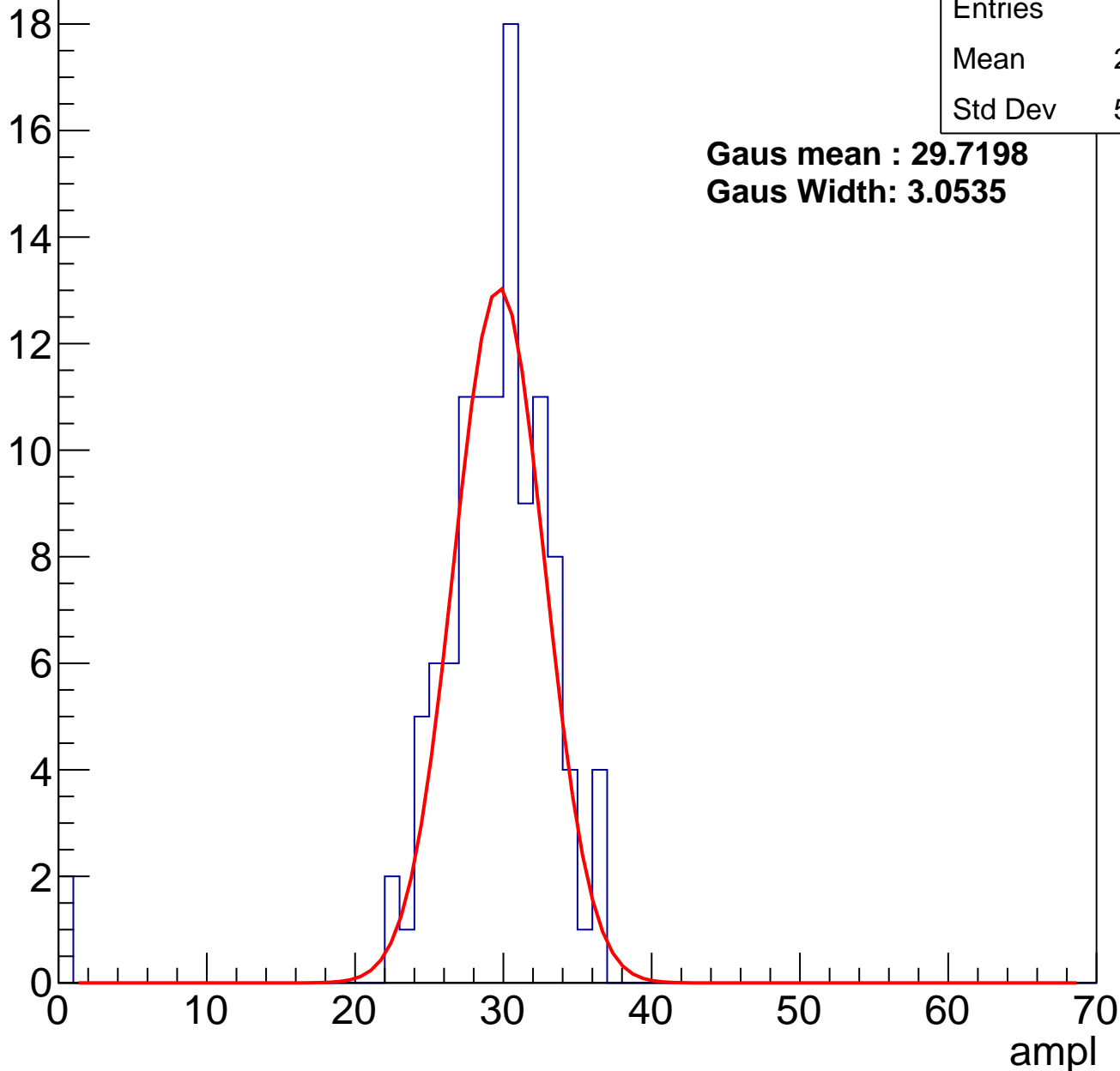
calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	28.79
Std Dev	5.004

Gaus mean : 29.7198

Gaus Width: 3.0535

Entry

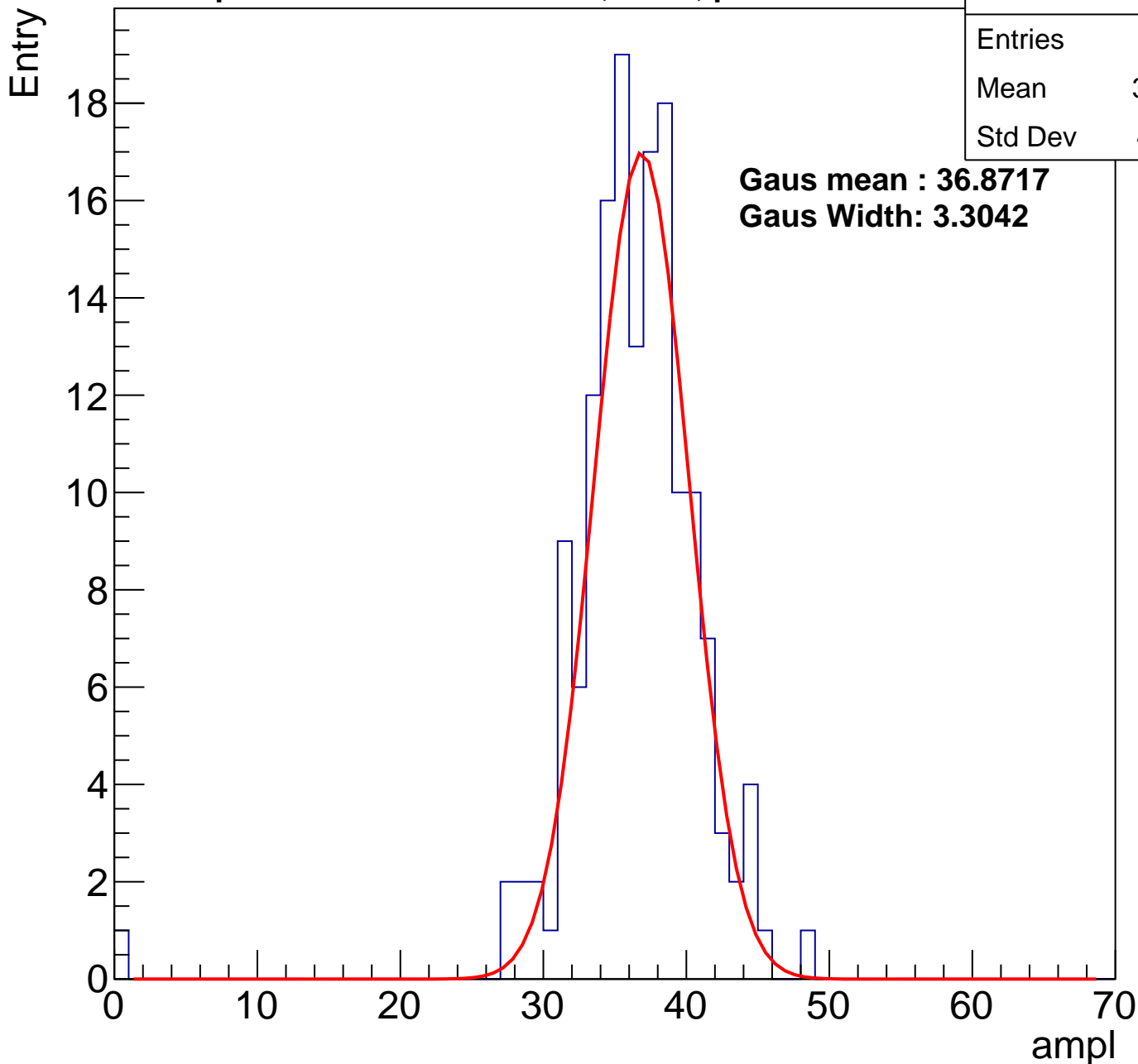


B0L101S, U24-ch26, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	35.98
Std Dev	4.691

Gaus mean : 36.8717
Gaus Width: 3.3042



B0L101S, U24-ch26, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

138

Mean

43.36

Std Dev

3.487

Gaus mean : 43.9249

Gaus Width: 3.2578

0

2

4

6

8

10

12

14

16

16

B0L101S, U24-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

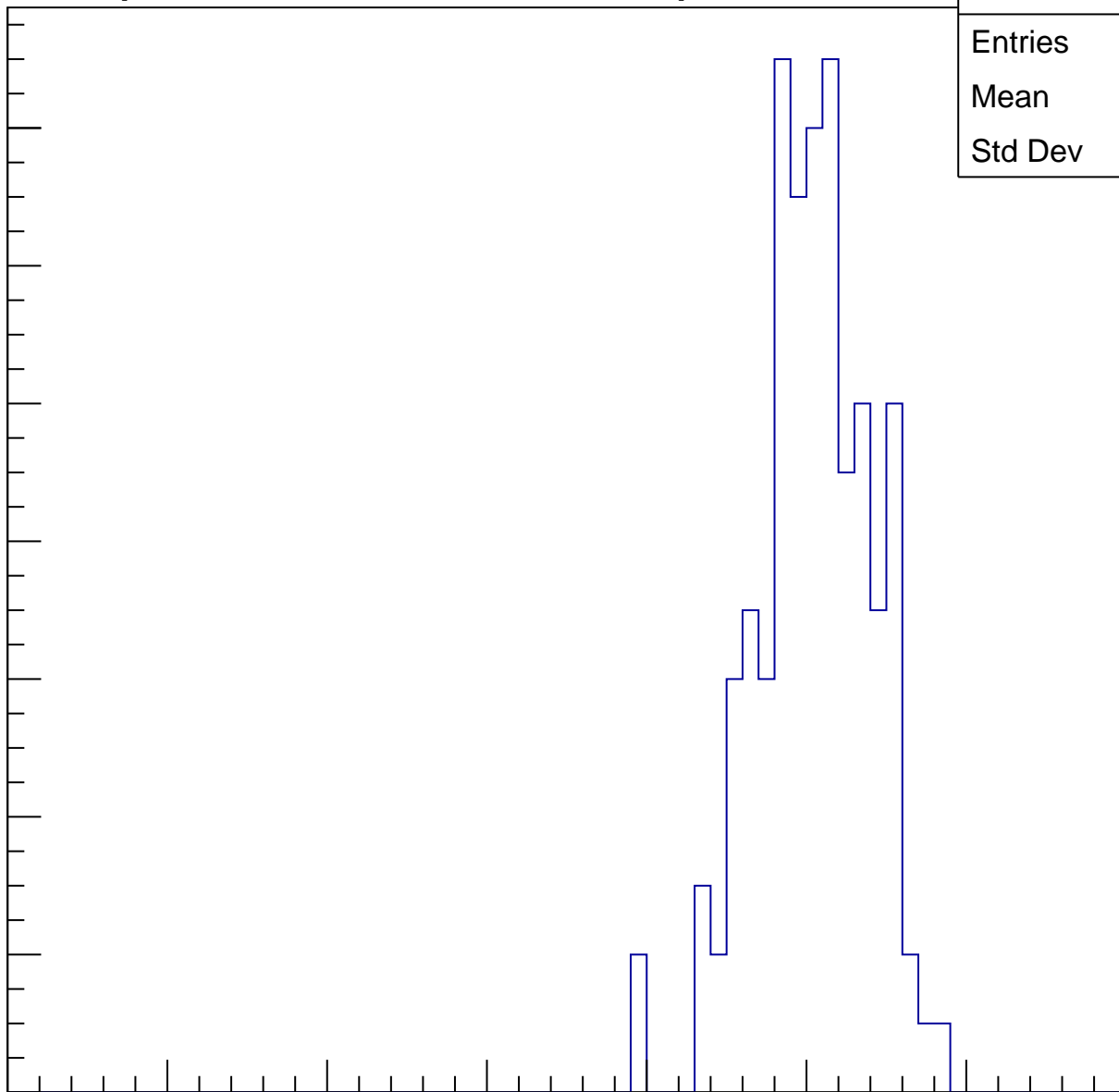
Entries	123
Mean	49.95
Std Dev	3.536

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch26, adc4

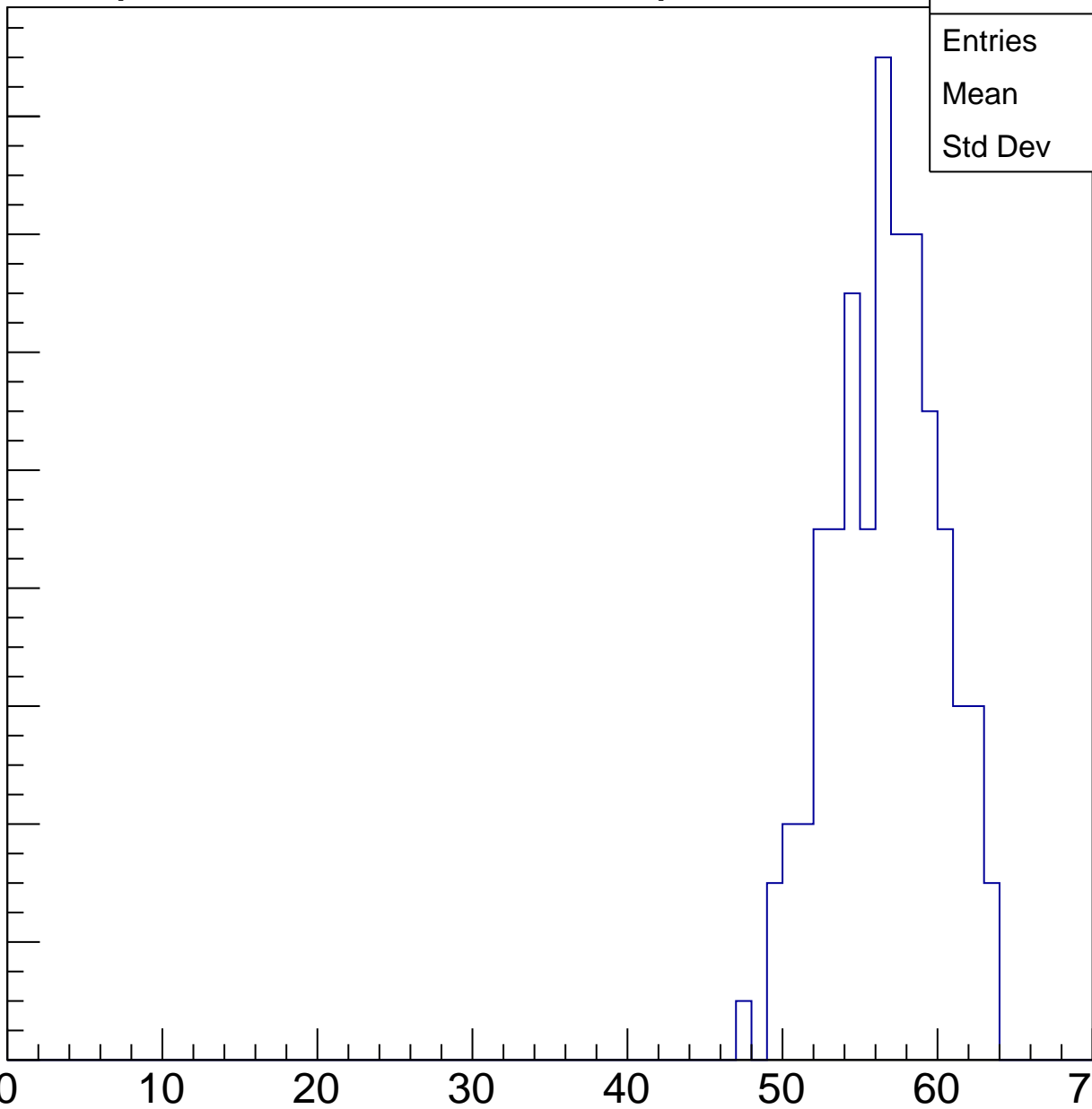
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	56.2
Std Dev	3.458

ampl



B0L101S, U24-ch26, adc5

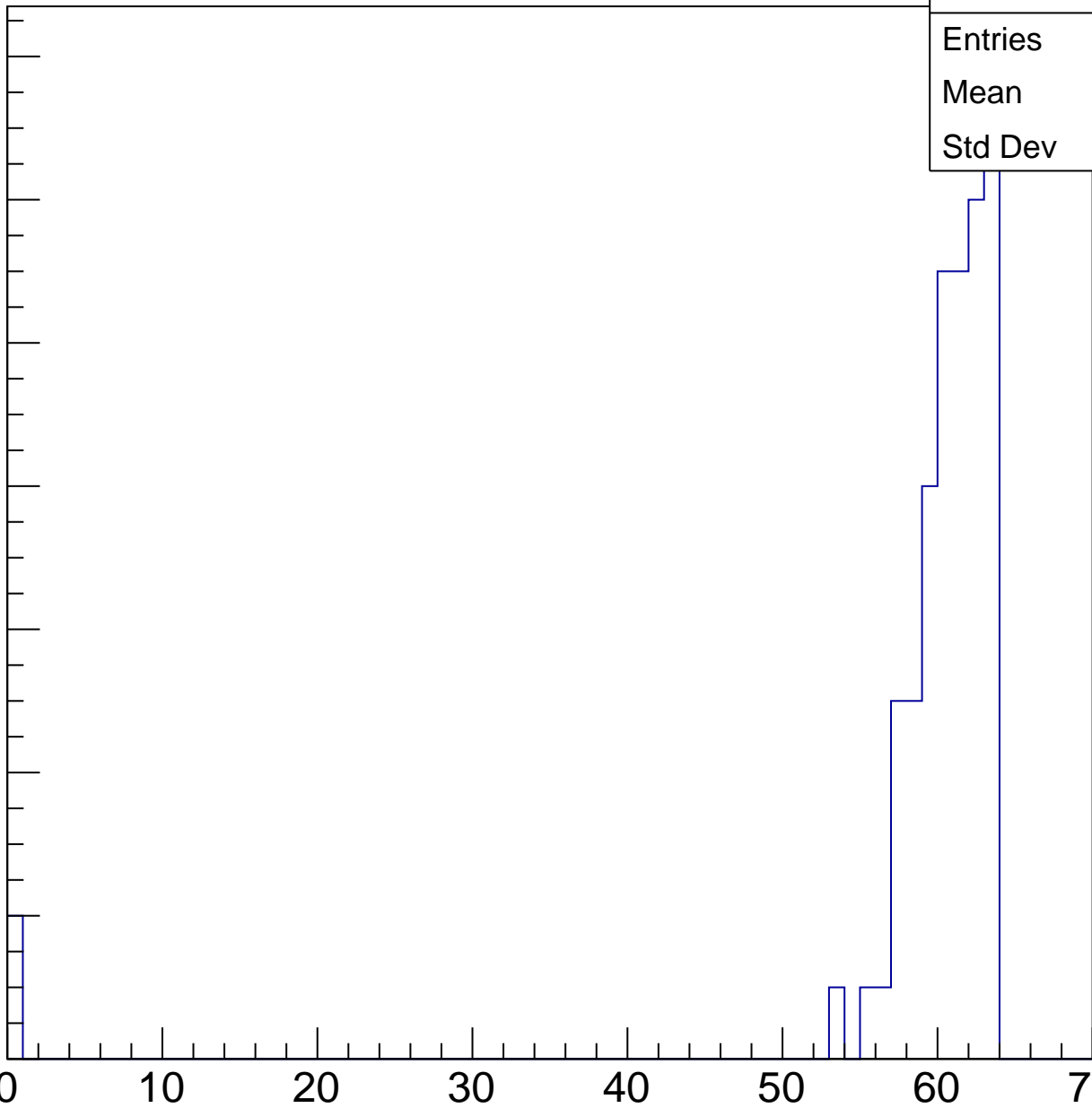
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.7
Std Dev	10.23

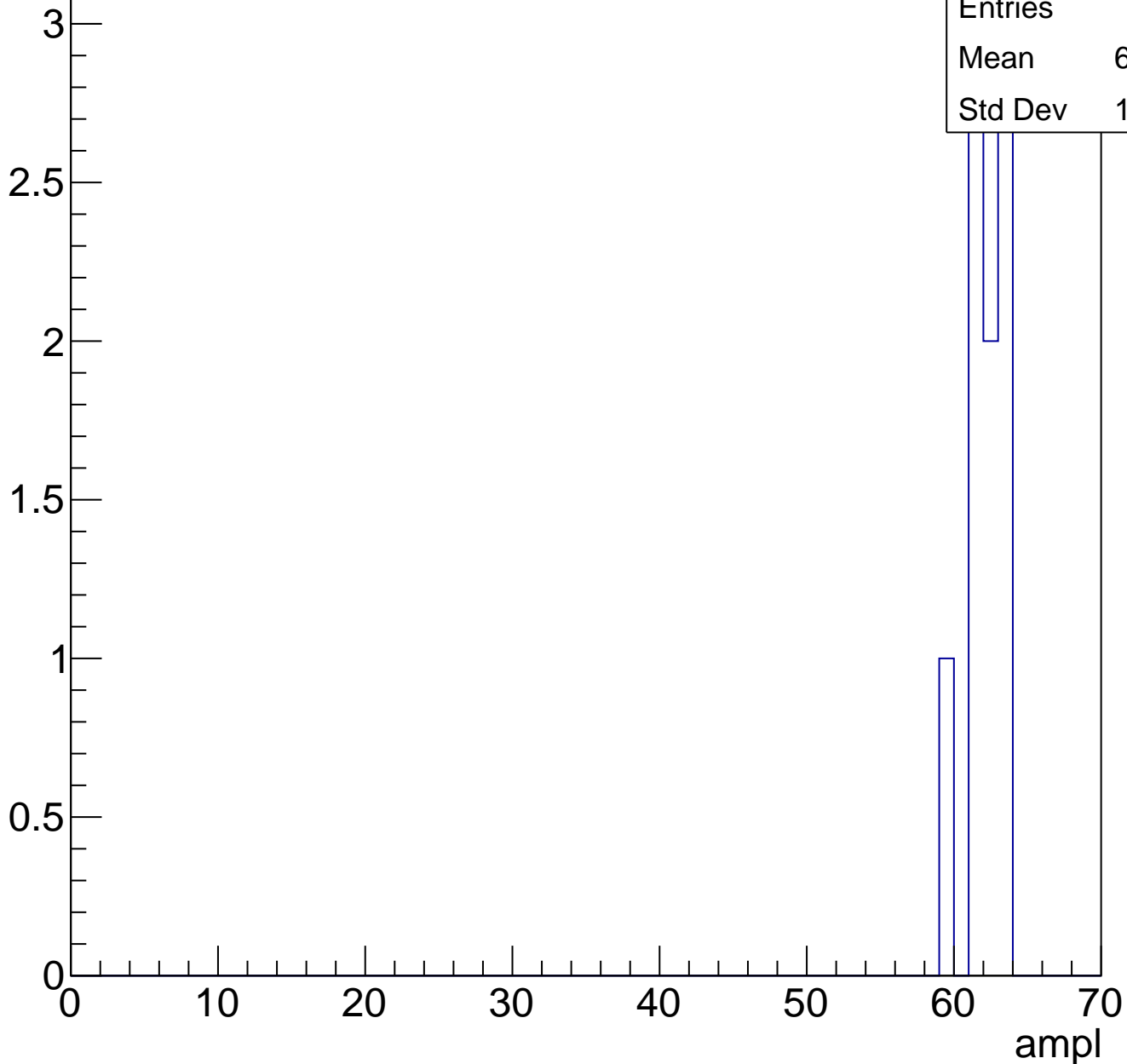
ampl



B0L101S, U24-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch27, adc0

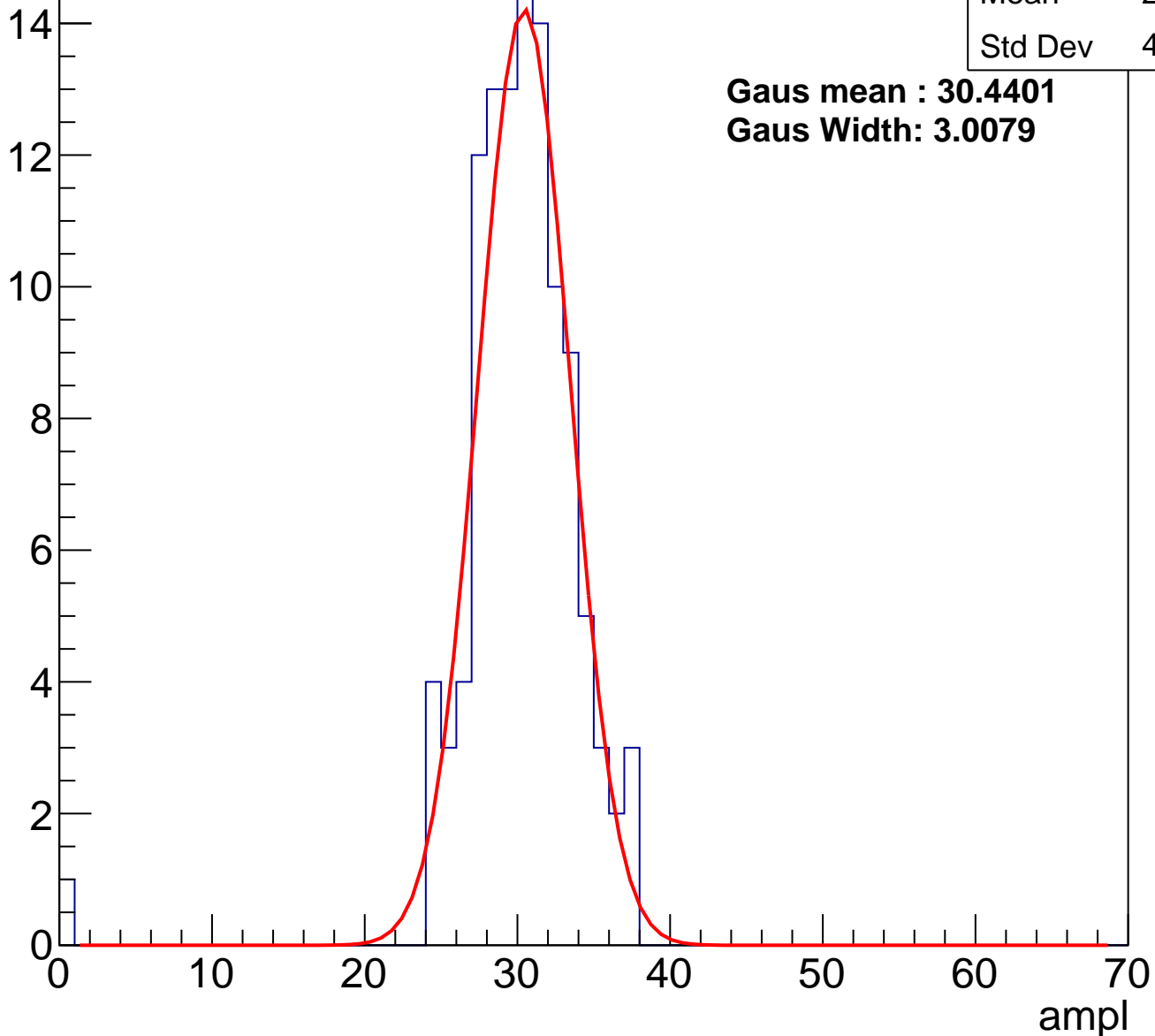
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	29.72
Std Dev	4.089

Gaus mean : 30.4401

Gaus Width: 3.0079

Entry



B0L101S, U24-ch27, adc1

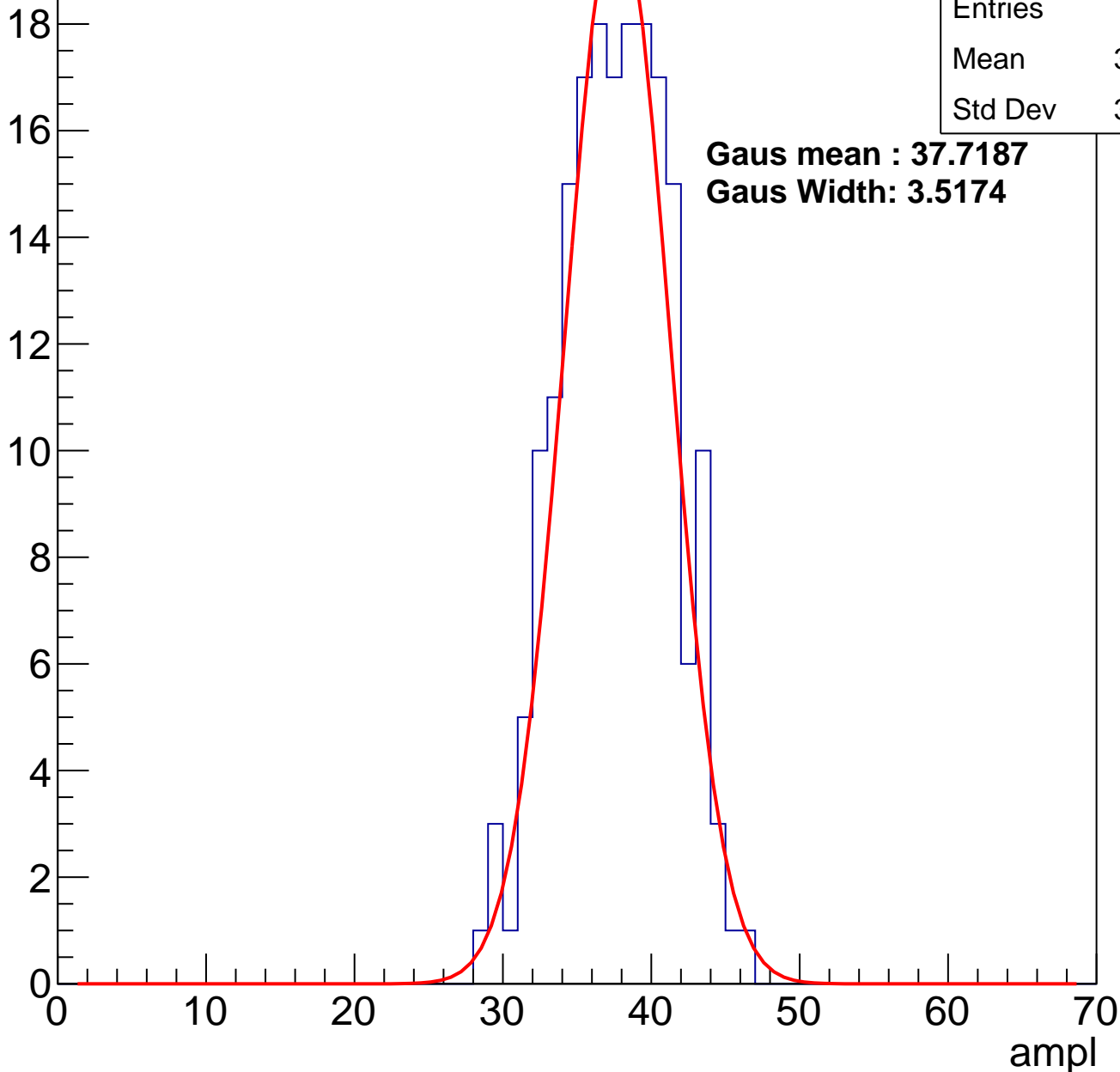
calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	37.17
Std Dev	3.604

Gaus mean : 37.7187

Gaus Width: 3.5174

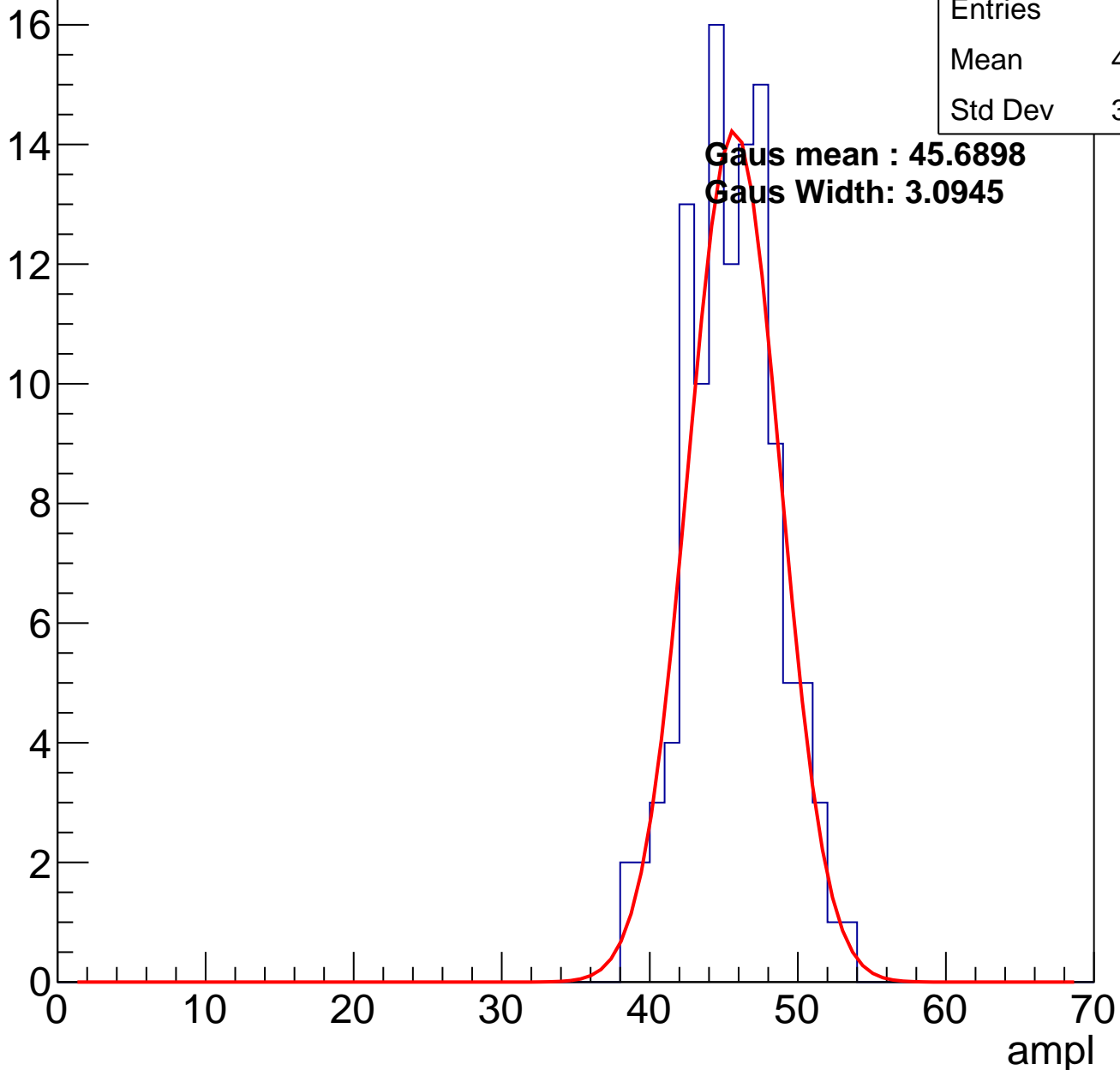
Entry



B0L101S, U24-ch27, adc2

calib_packv5_042523_0143.root, FC#1, port C1

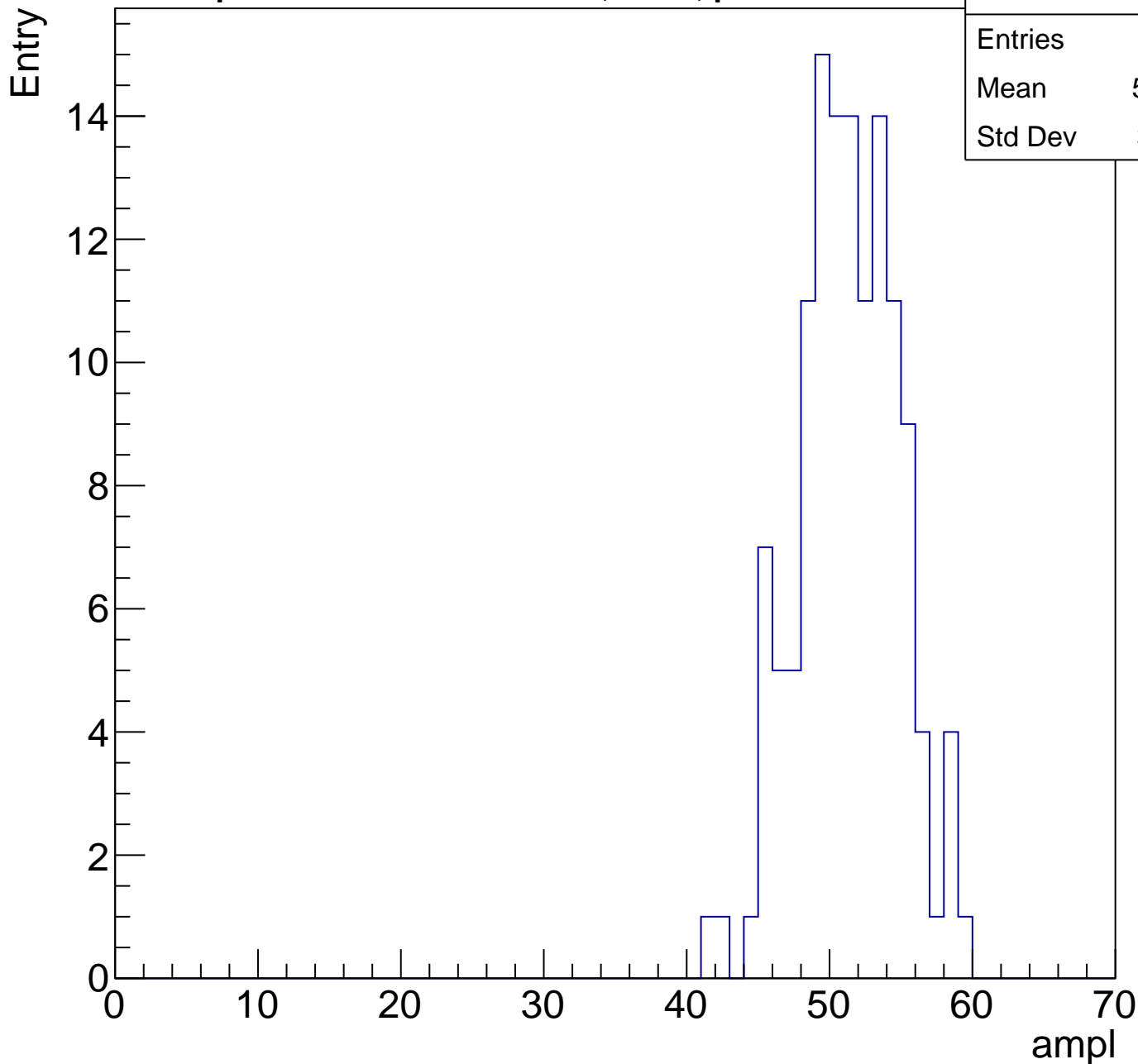
Entry



B0L101S, U24-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	50.84
Std Dev	3.481



B0L101S, U24-ch27, adc4

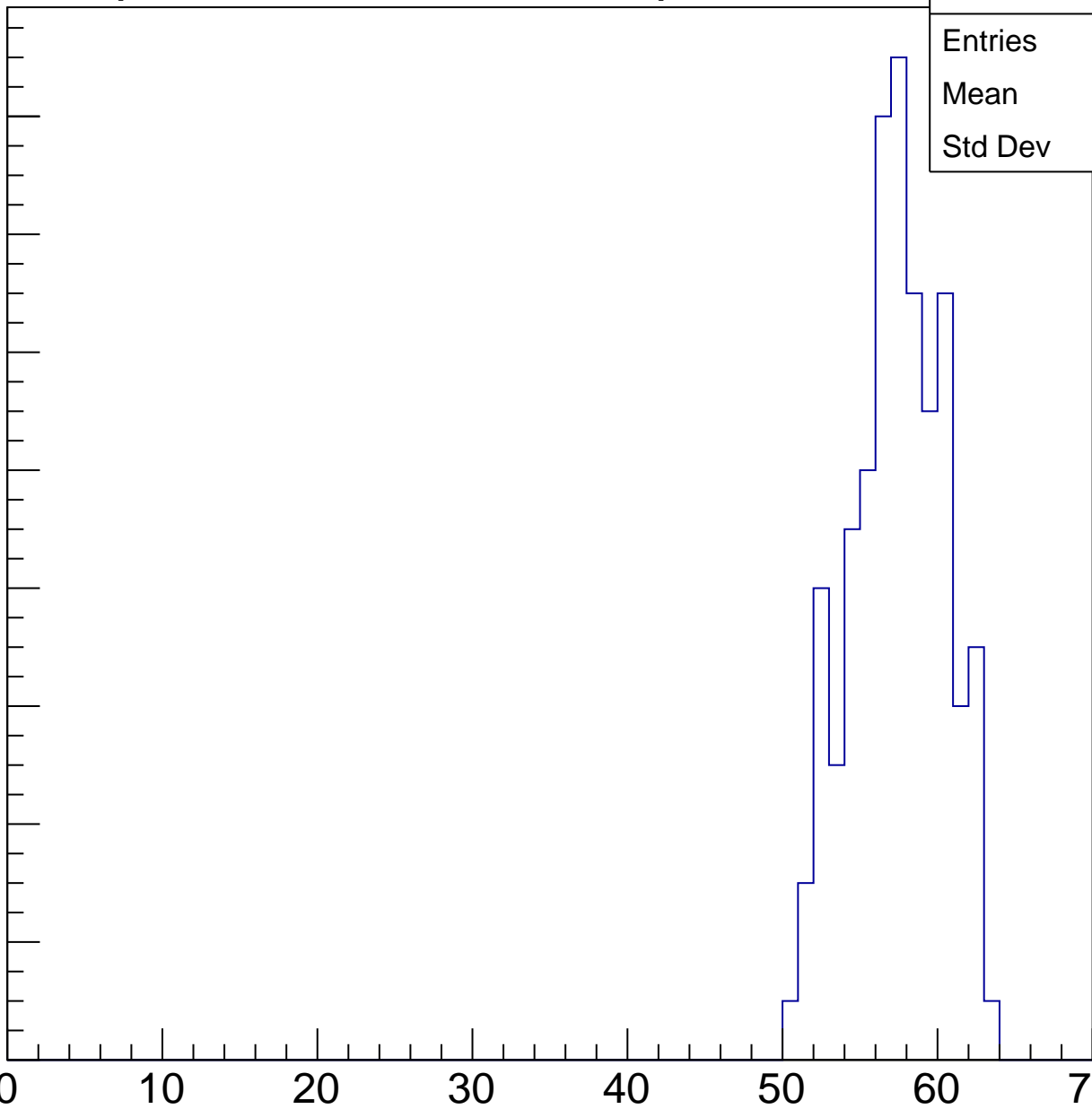
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	56.92
Std Dev	2.964

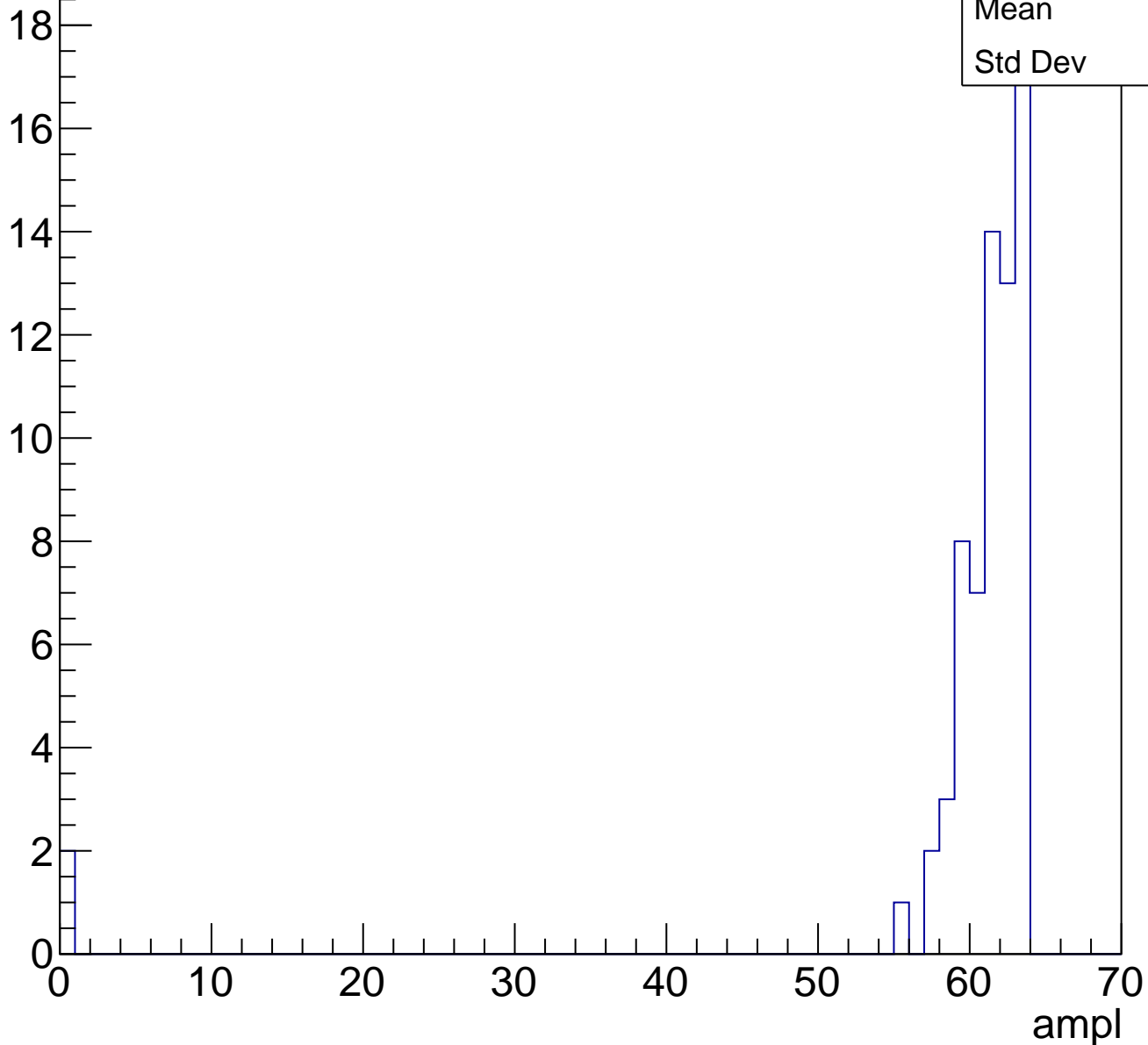
ampl



B0L101S, U24-ch27, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch28, adc0

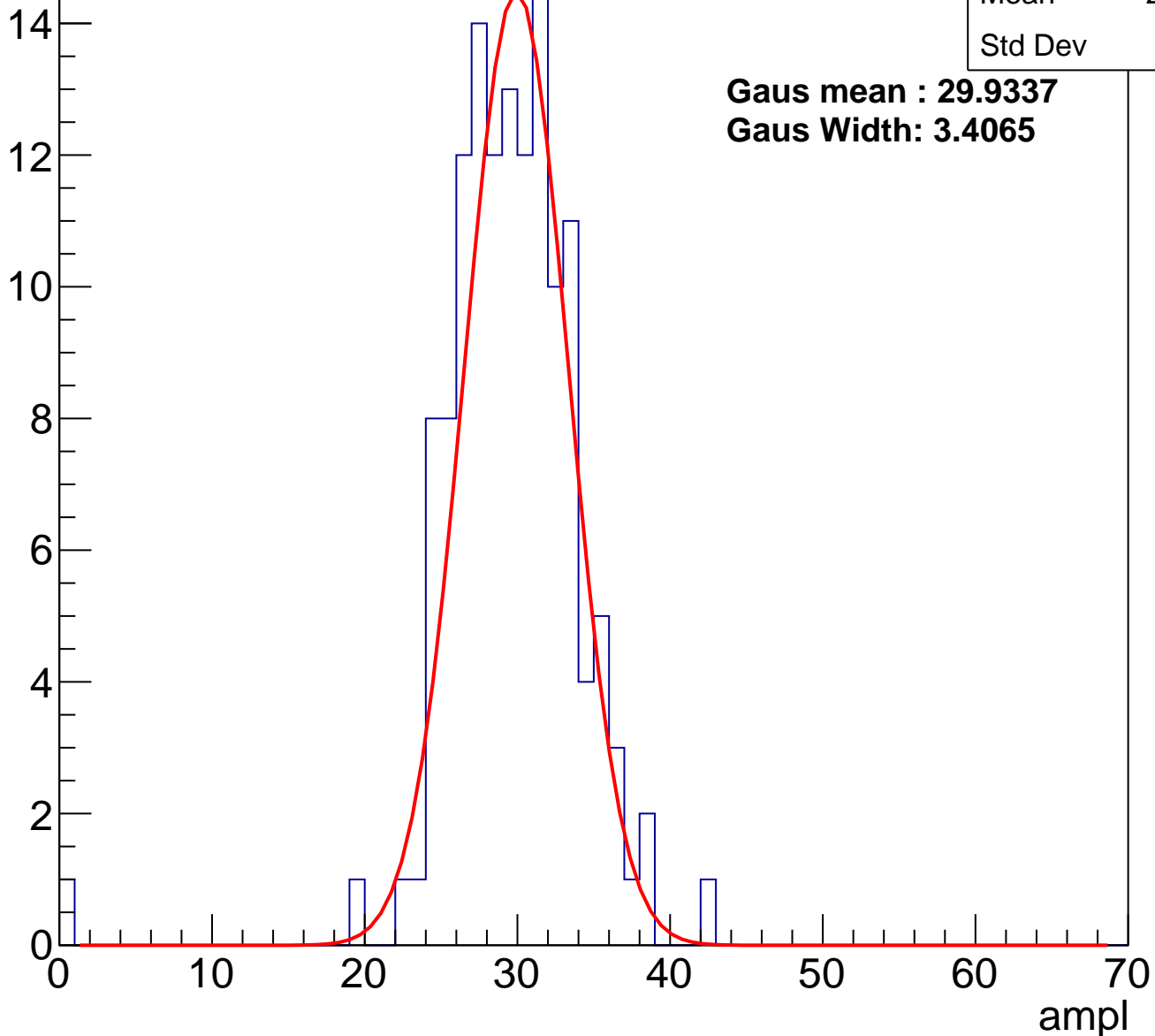
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	29.19
Std Dev	4.46

Gaus mean : 29.9337

Gaus Width: 3.4065

Entry



B0L101S, U24-ch28, adc1

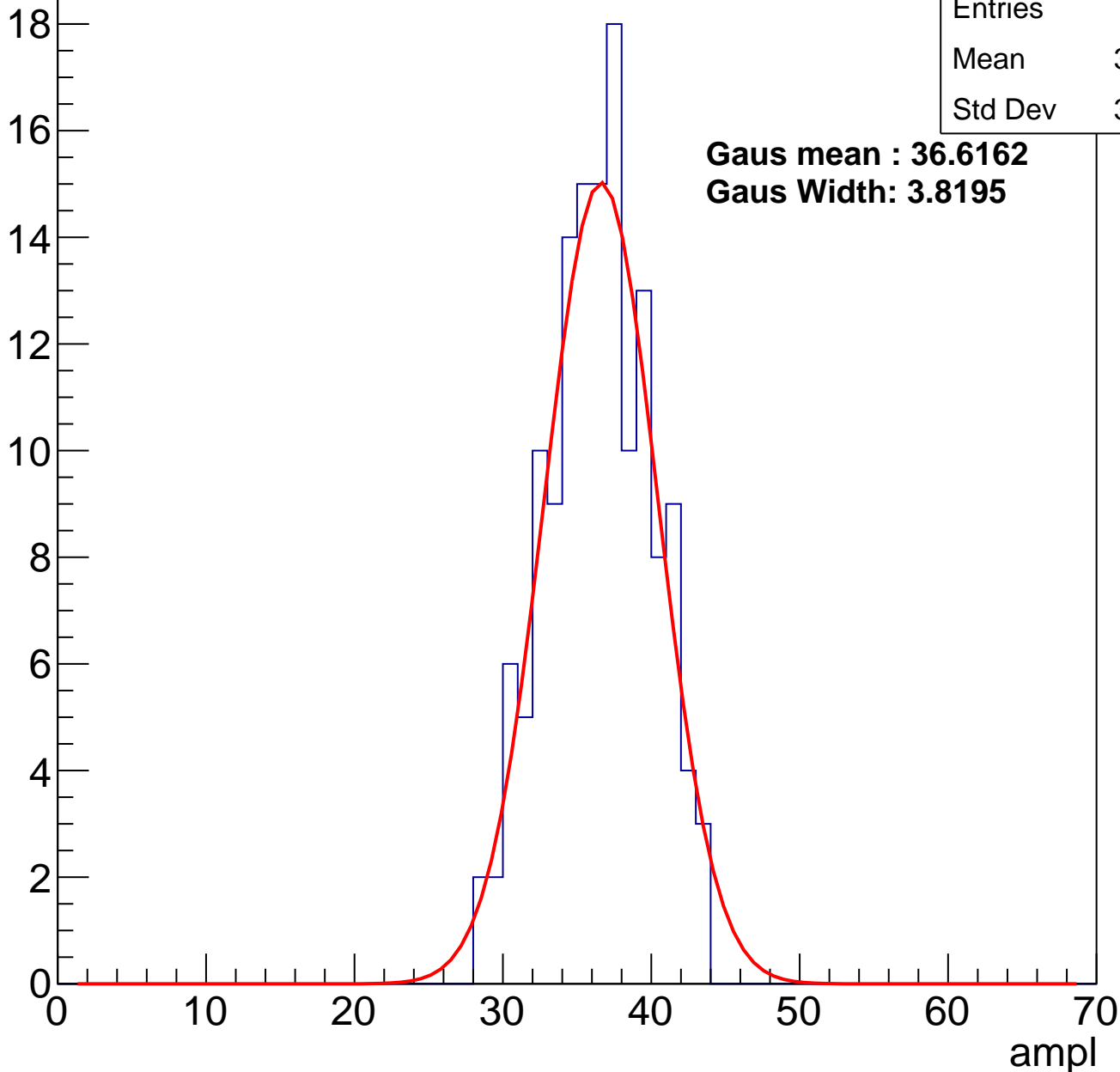
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	35.99
Std Dev	3.446

Gaus mean : 36.6162

Gaus Width: 3.8195

Entry



B0L101S, U24-ch28, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

123

Mean

42.76

Std Dev

3.131

Gaus mean : 43.3789

Gaus Width: 2.9899

0

10

20

30

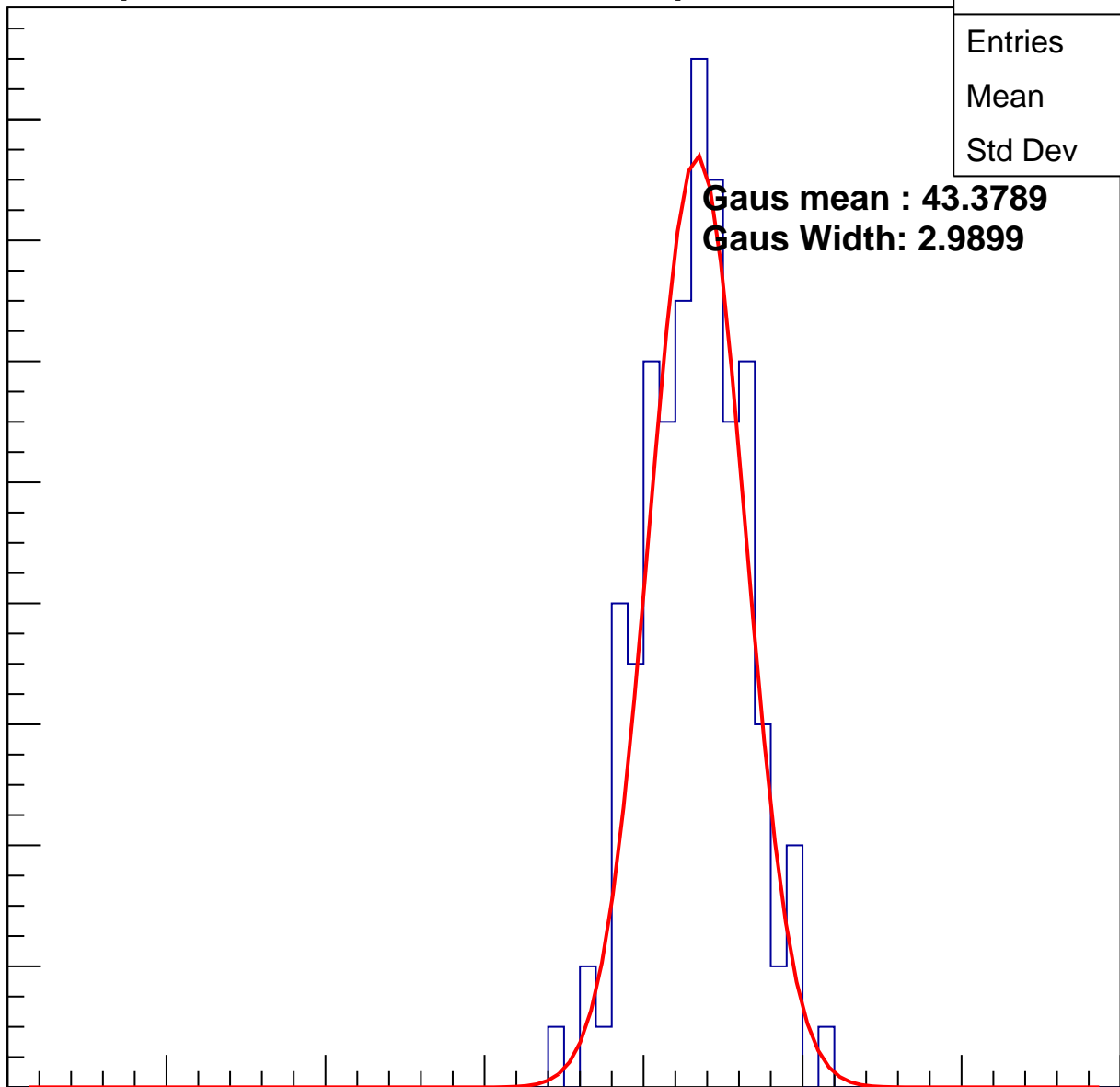
40

50

60

70

ampl

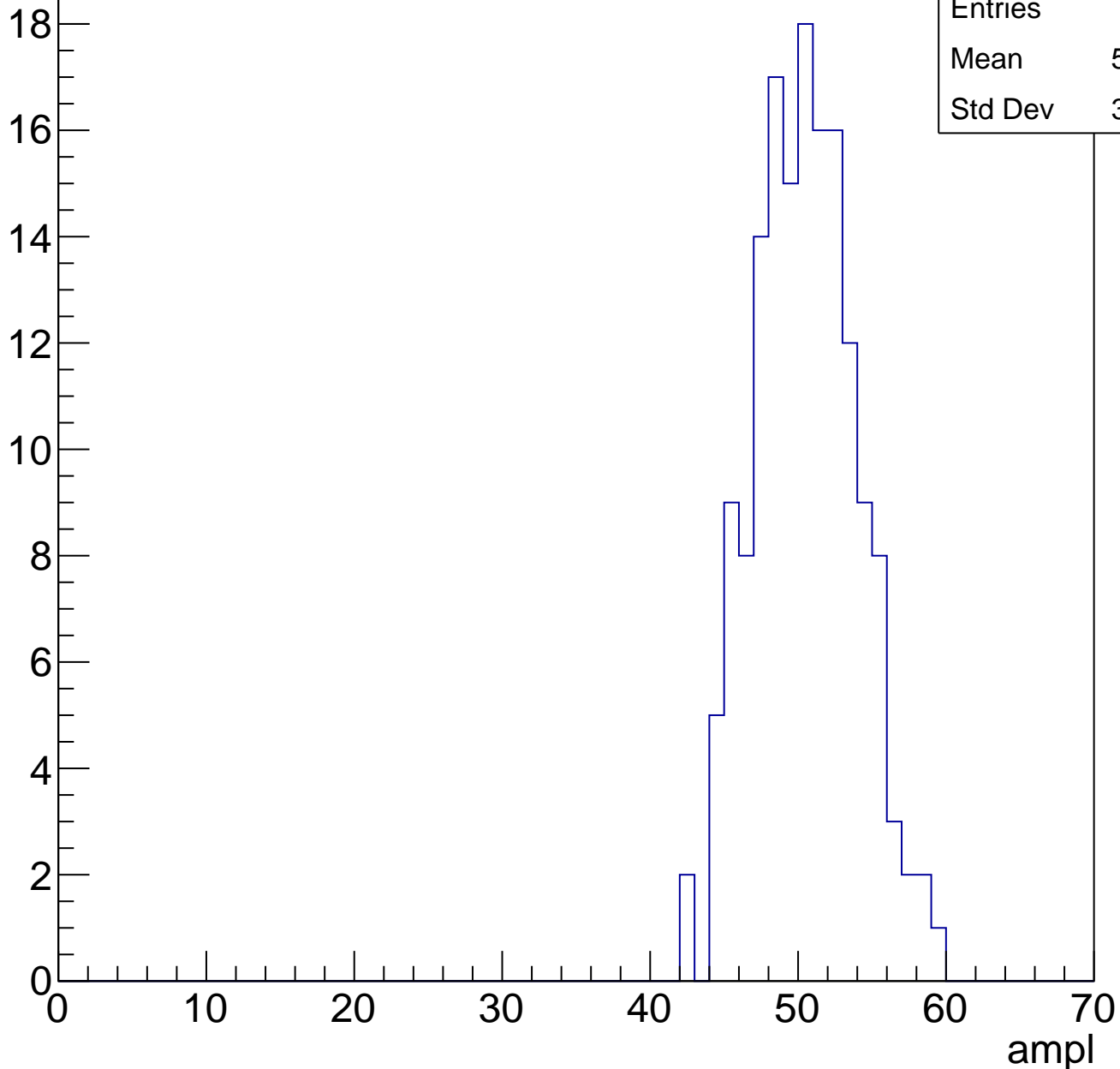


B0L101S, U24-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	50.02
Std Dev	3.402

Entry



B0L101S, U24-ch28, adc4

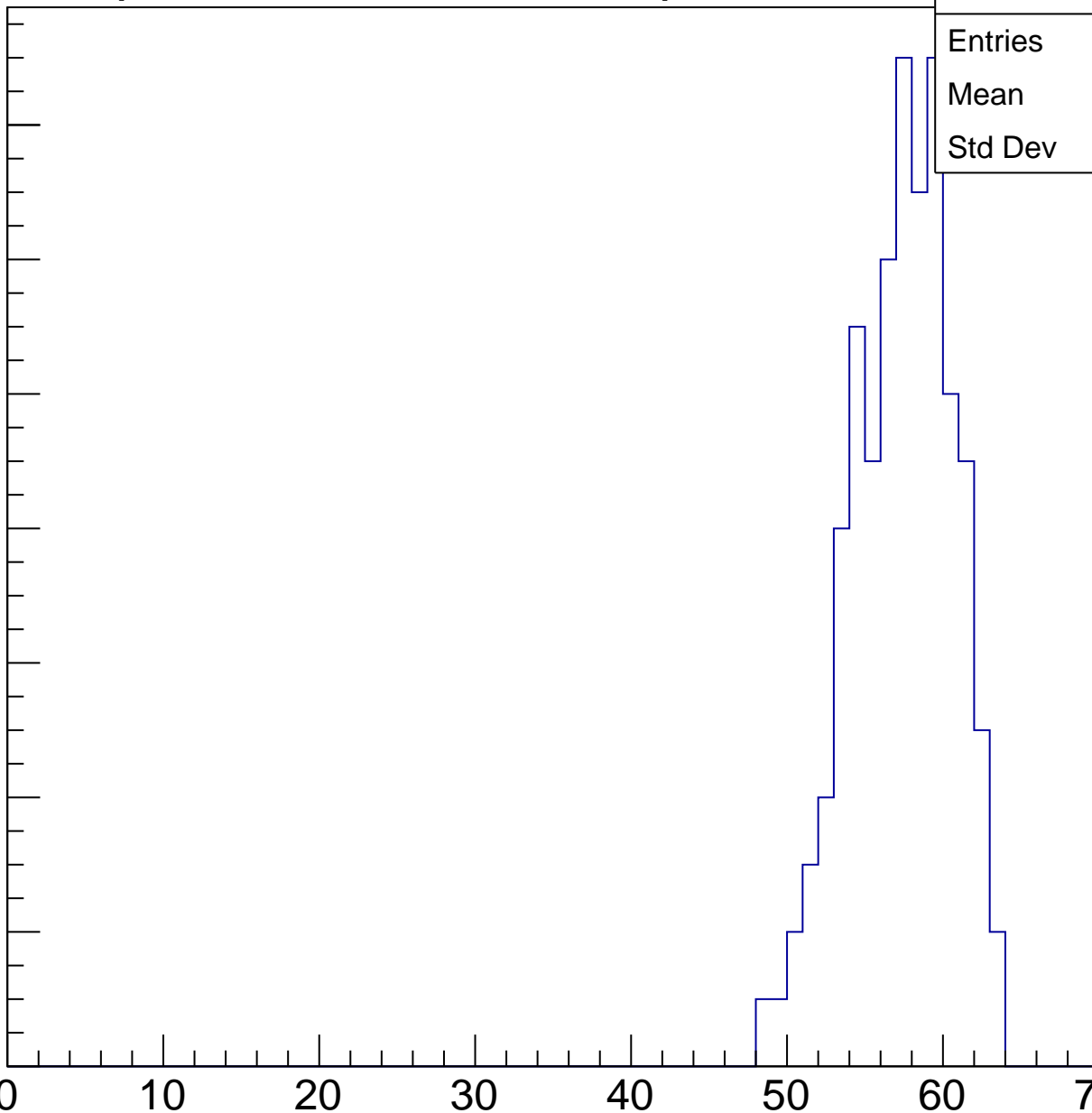
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	56.85
Std Dev	3.198

ampl



B0L101S, U24-ch28, adc5

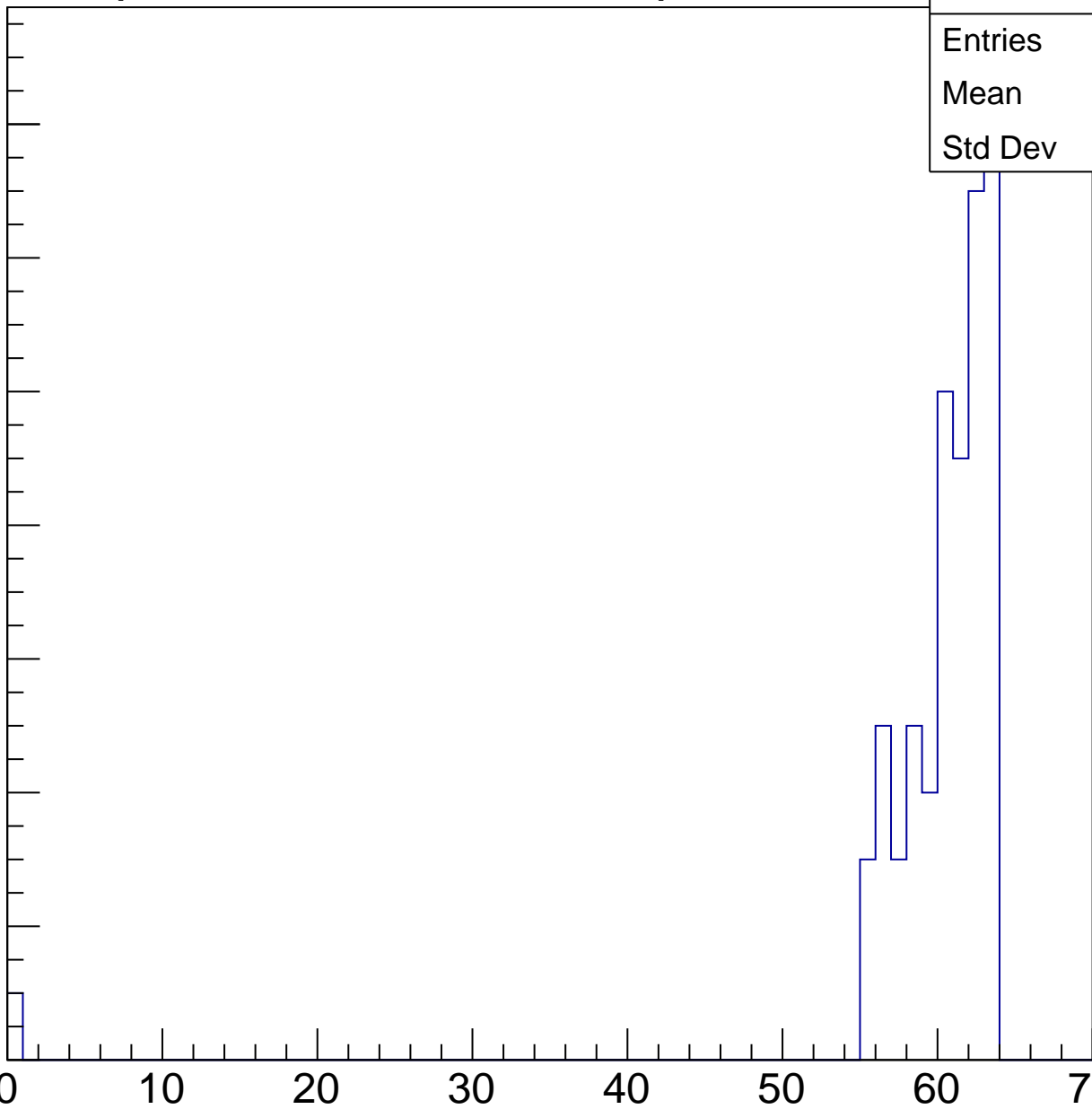
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	59.44
Std Dev	7.651

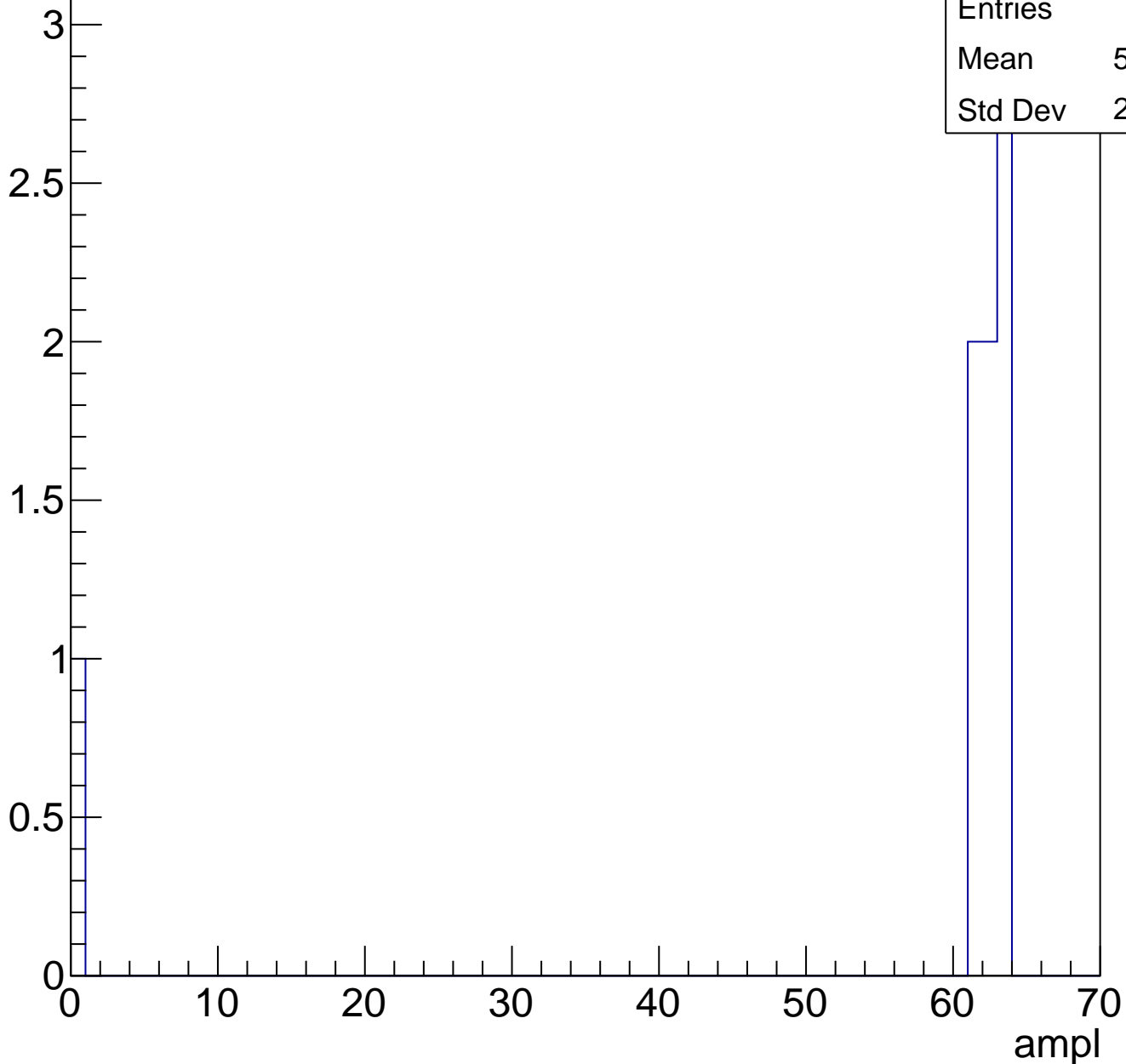
ampl



B0L101S, U24-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch29, adc0

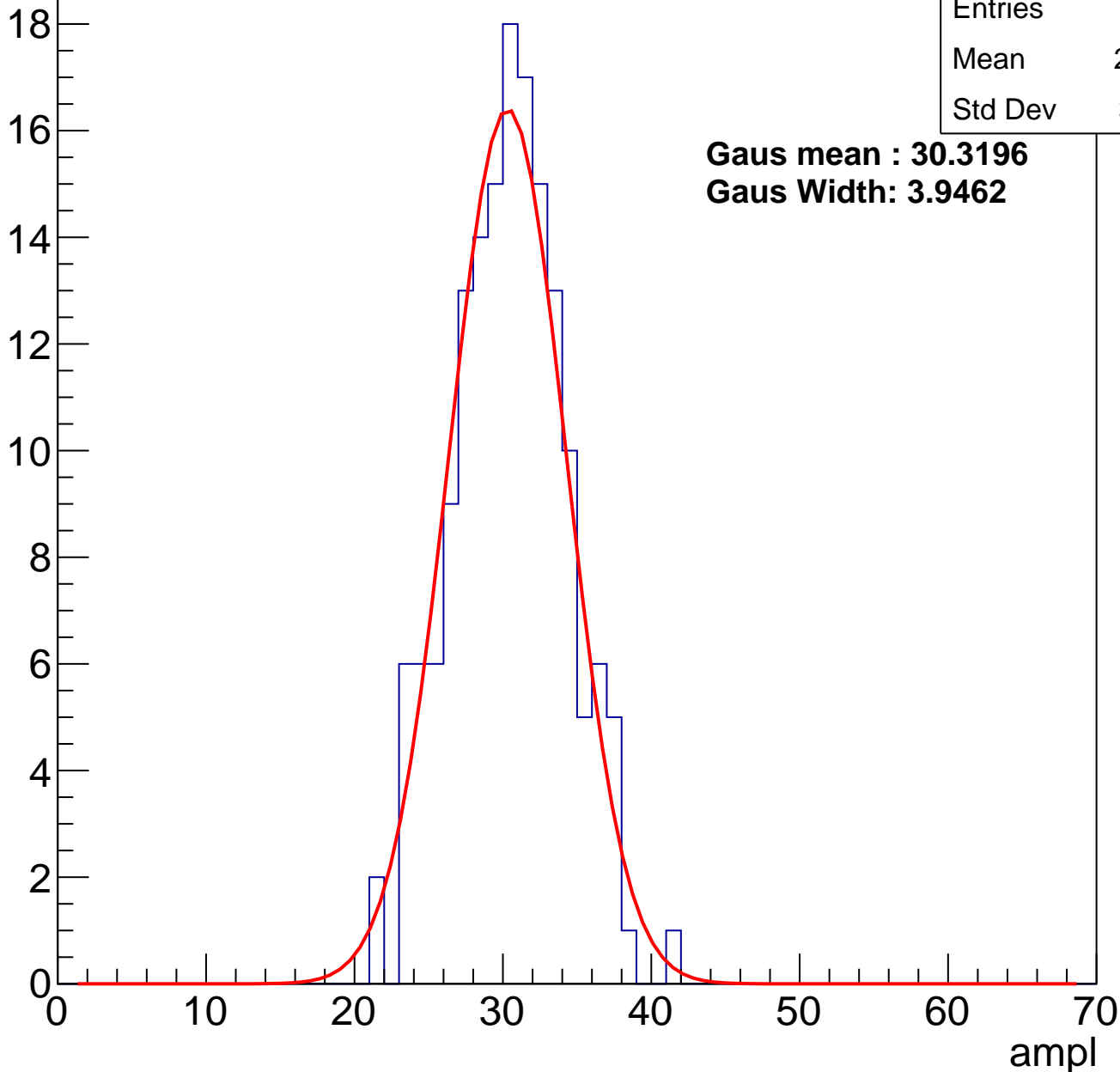
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	29.98
Std Dev	3.751

Gaus mean : 30.3196

Gaus Width: 3.9462

Entry



B0L101S, U24-ch29, adc1

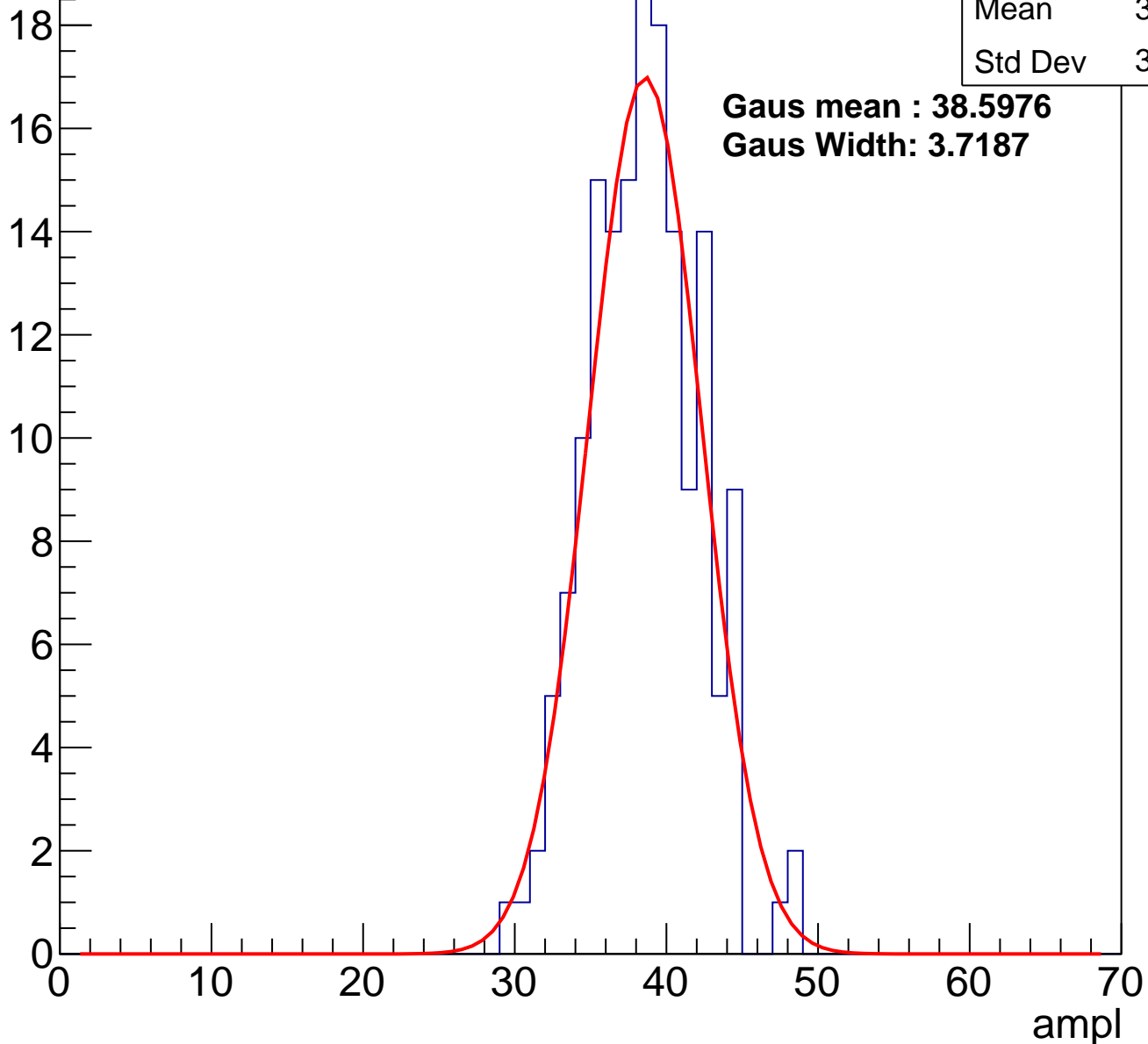
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	38.08
Std Dev	3.587

Gaus mean : 38.5976

Gaus Width: 3.7187

Entry



B0L101S, U24-ch29, adc2

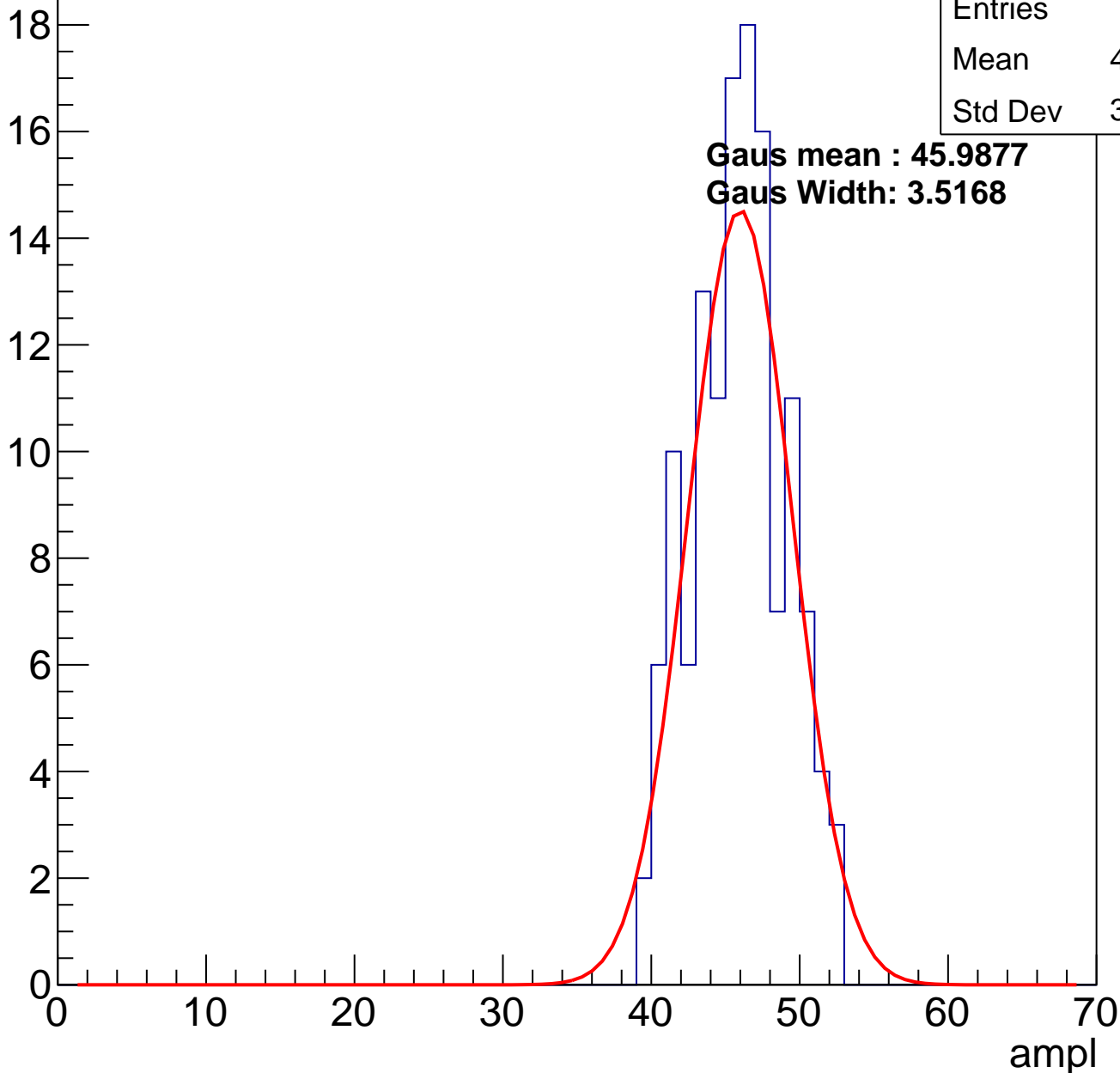
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	45.44
Std Dev	3.102

Gaus mean : 45.9877

Gaus Width: 3.5168

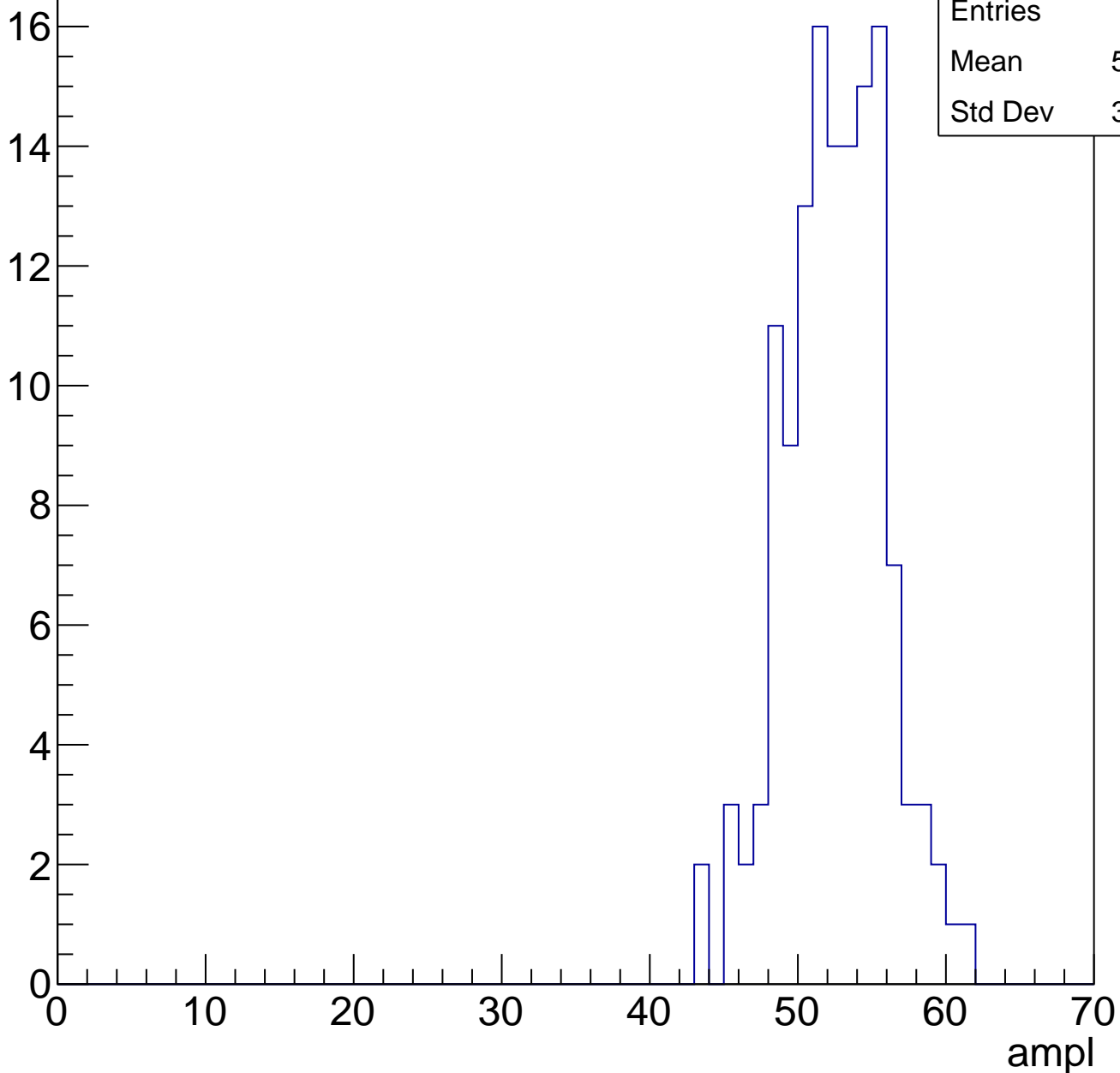
Entry



B0L101S, U24-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	135
Mean	52.04
Std Dev	3.374

B0L101S, U24-ch29, adc4

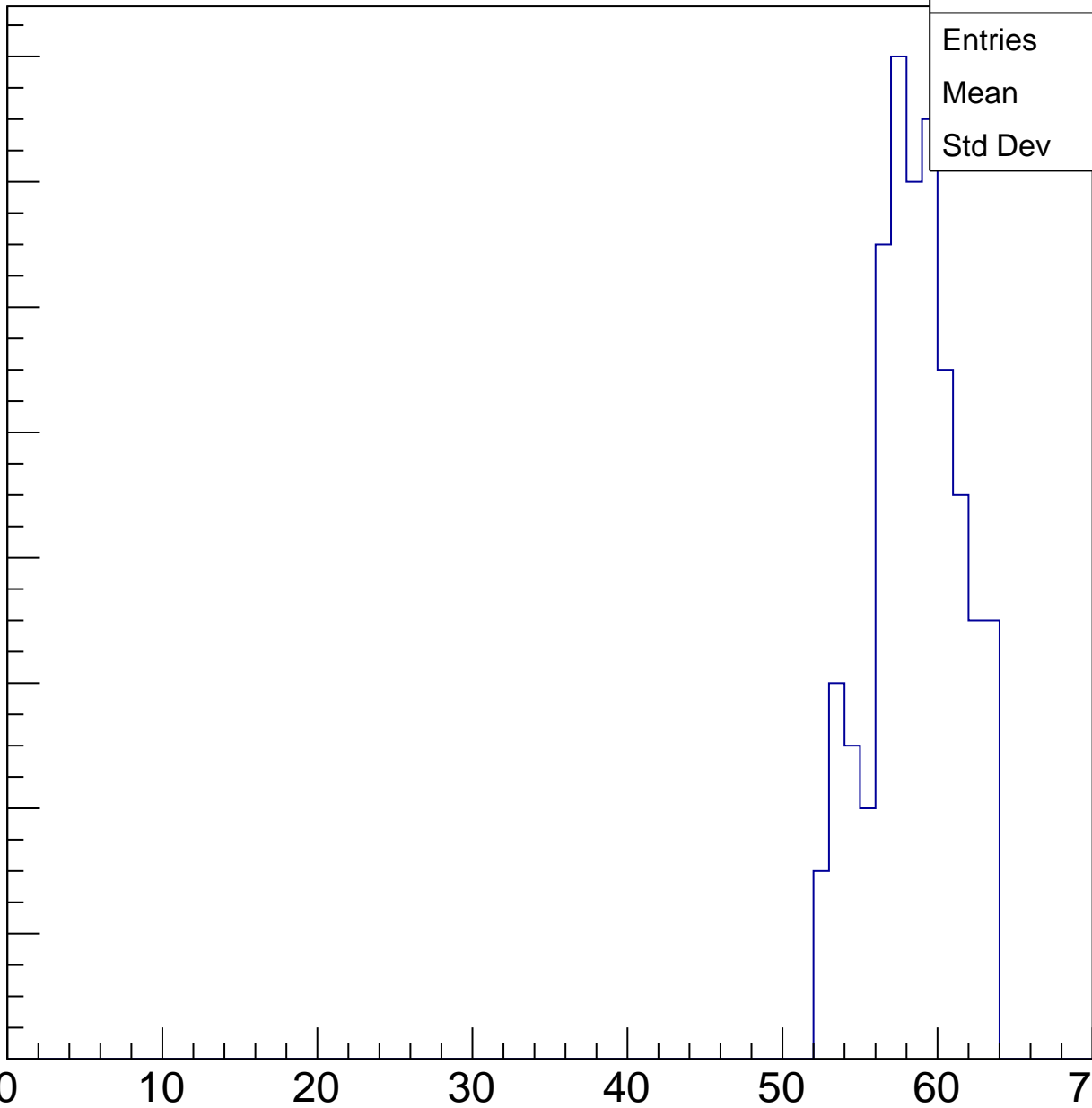
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.05
Std Dev	2.81

ampl



B0L101S, U24-ch29, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	52
Mean	58.85
Std Dev	11.87

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch30, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	28.14
Std Dev	4.879

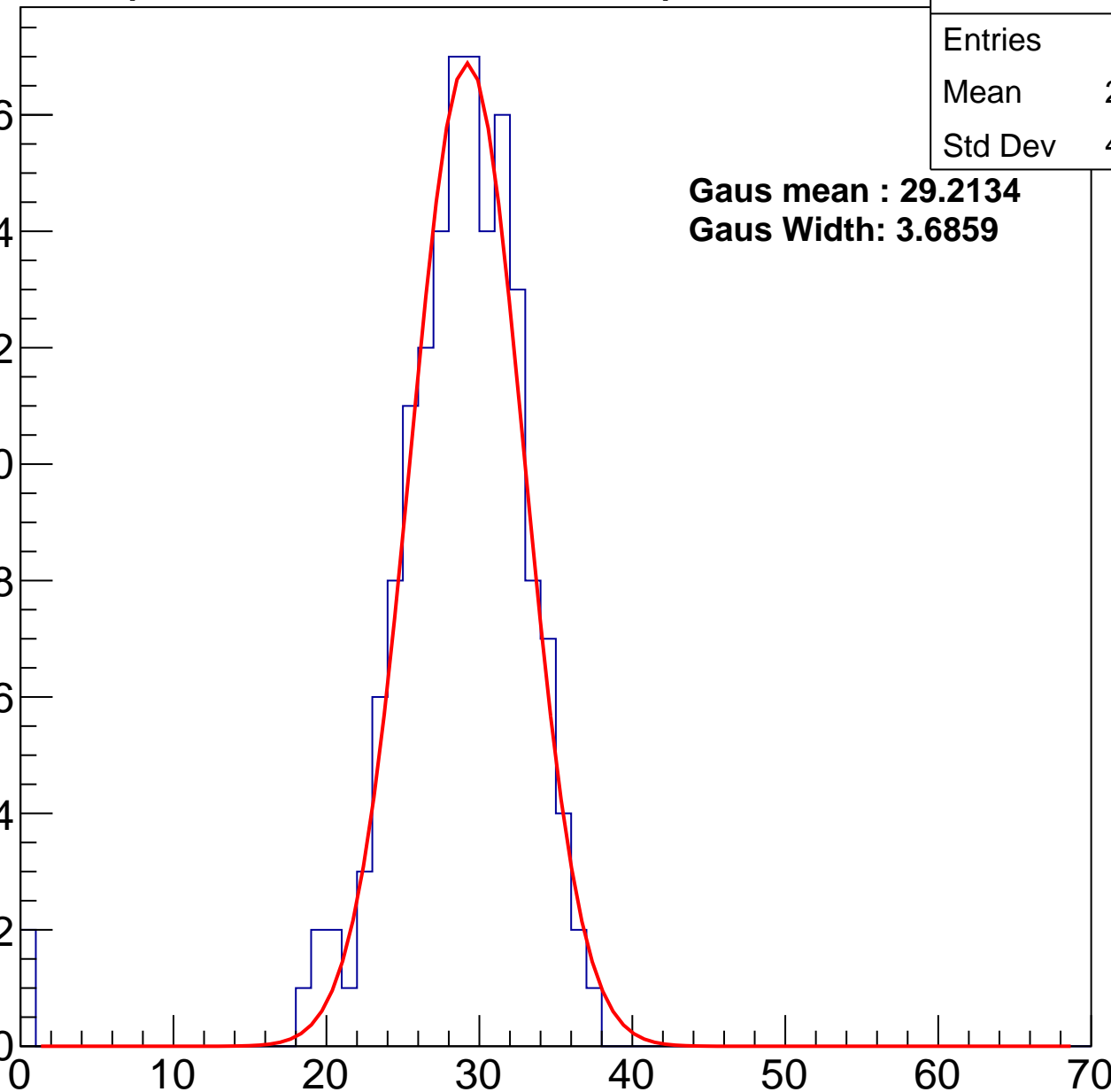
Gaus mean : 29.2134

Gaus Width: 3.6859

Entry

16
14
12
10
8
6
4
2
0

ampl



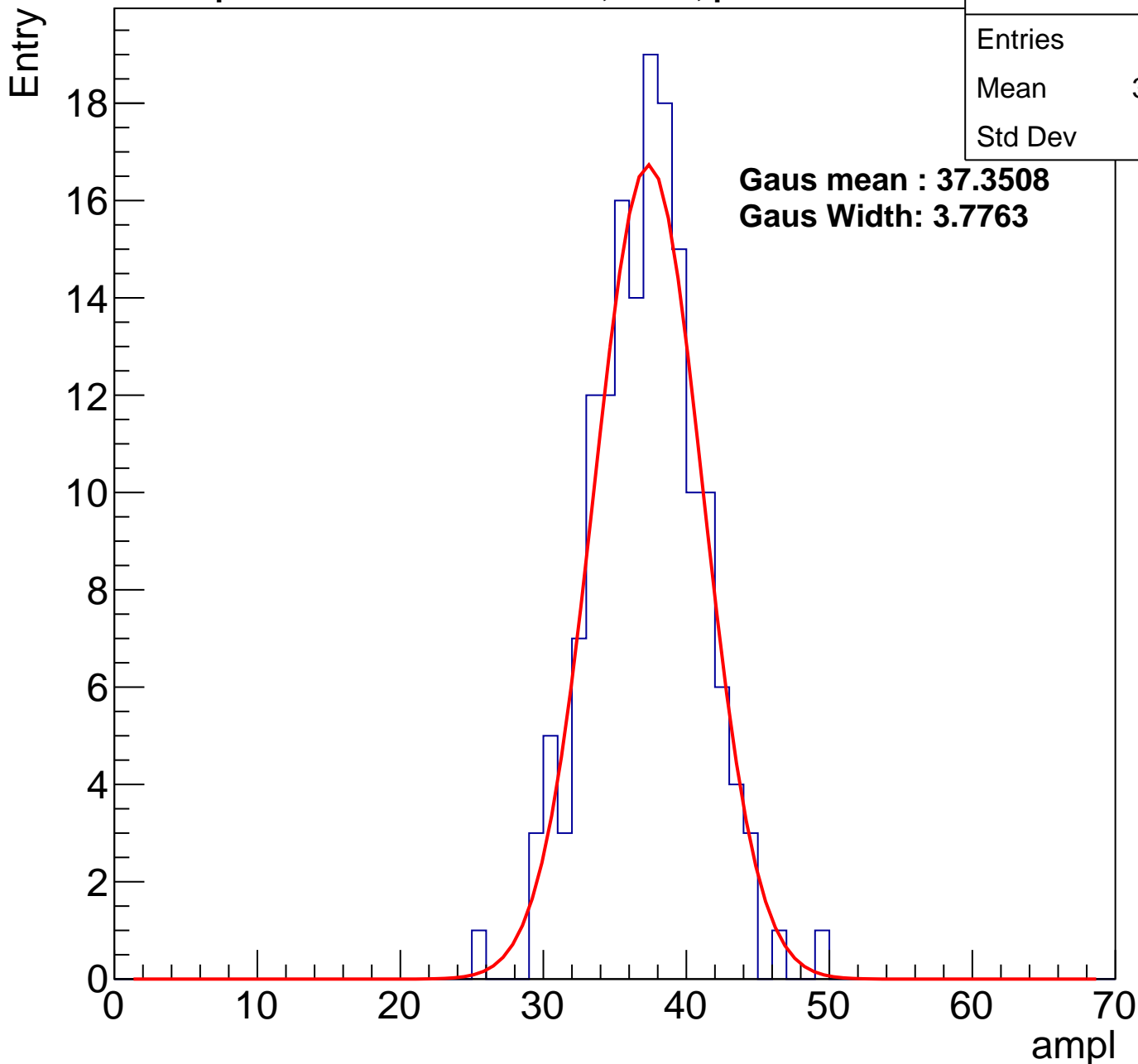
B0L101S, U24-ch30, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	36.75
Std Dev	3.73

Gaus mean : 37.3508

Gaus Width: 3.7763



B0L101S, U24-ch30, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	43.56
Std Dev	3.315

Gaus mean : 44.1737

Gaus Width: 3.3501

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

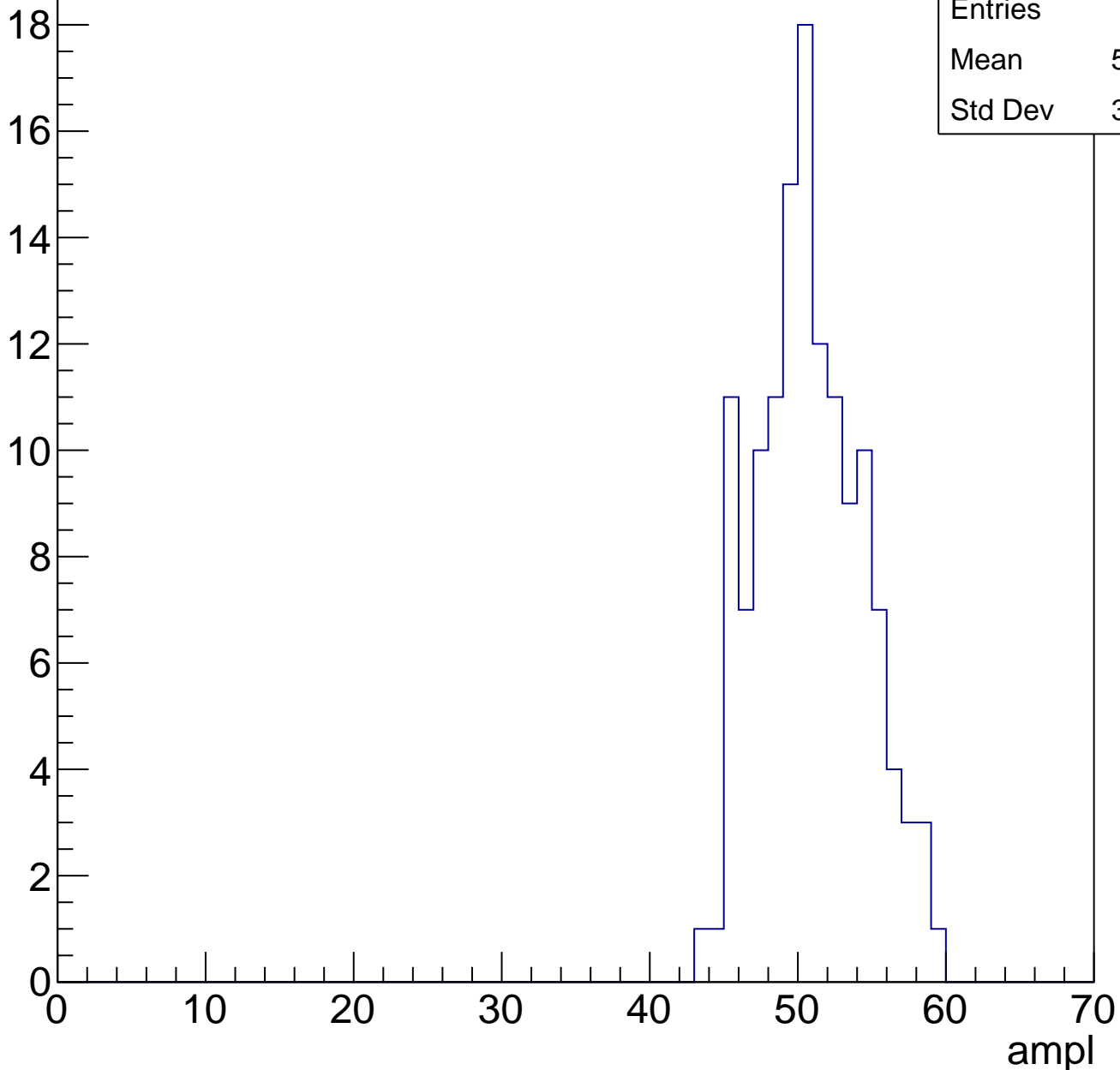
60

70

B0L101S, U24-ch30, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	134
Mean	50.38
Std Dev	3.496

B0L101S, U24-ch30, adc4

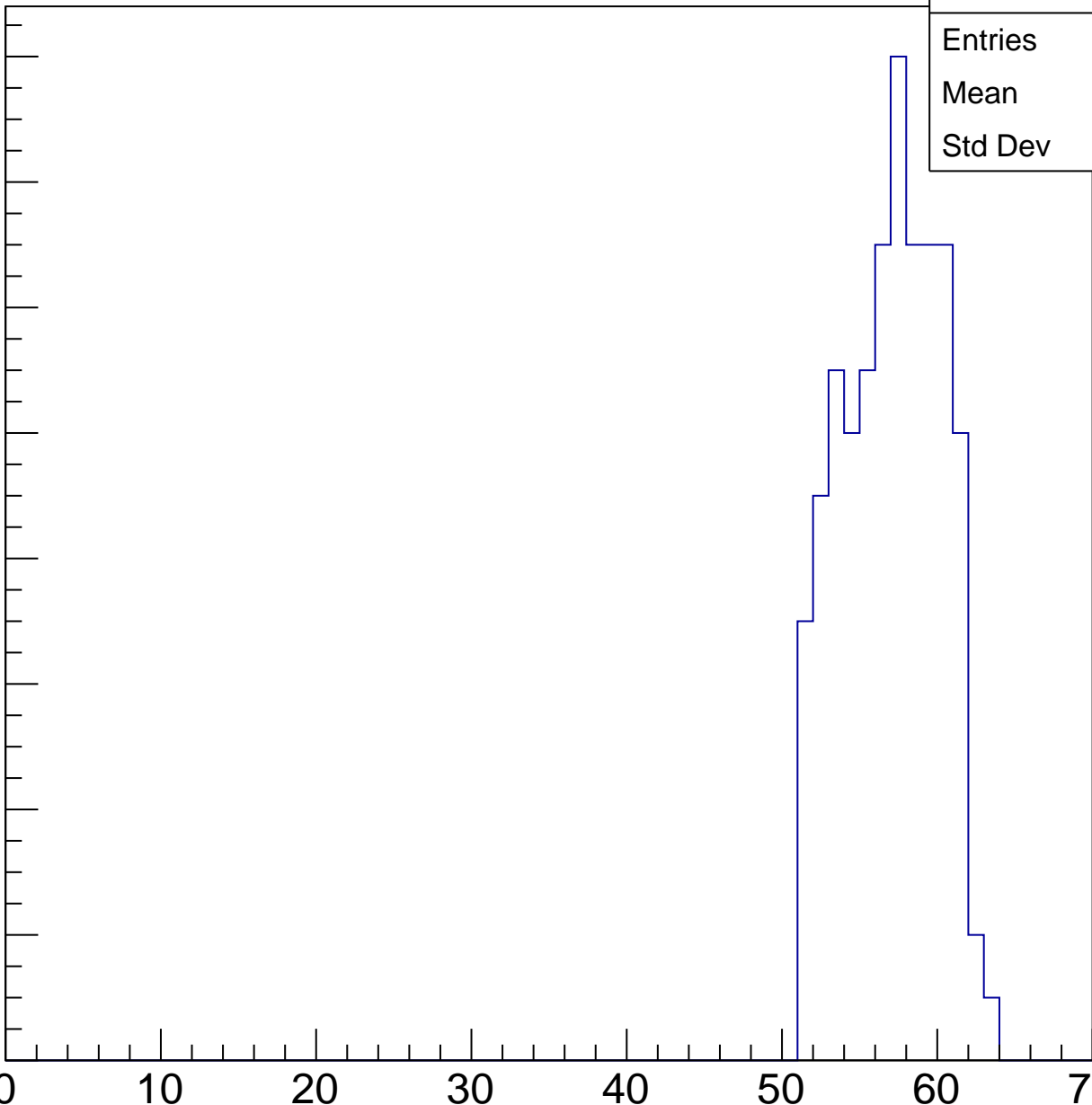
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	56.52
Std Dev	3.048

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch30, adc5

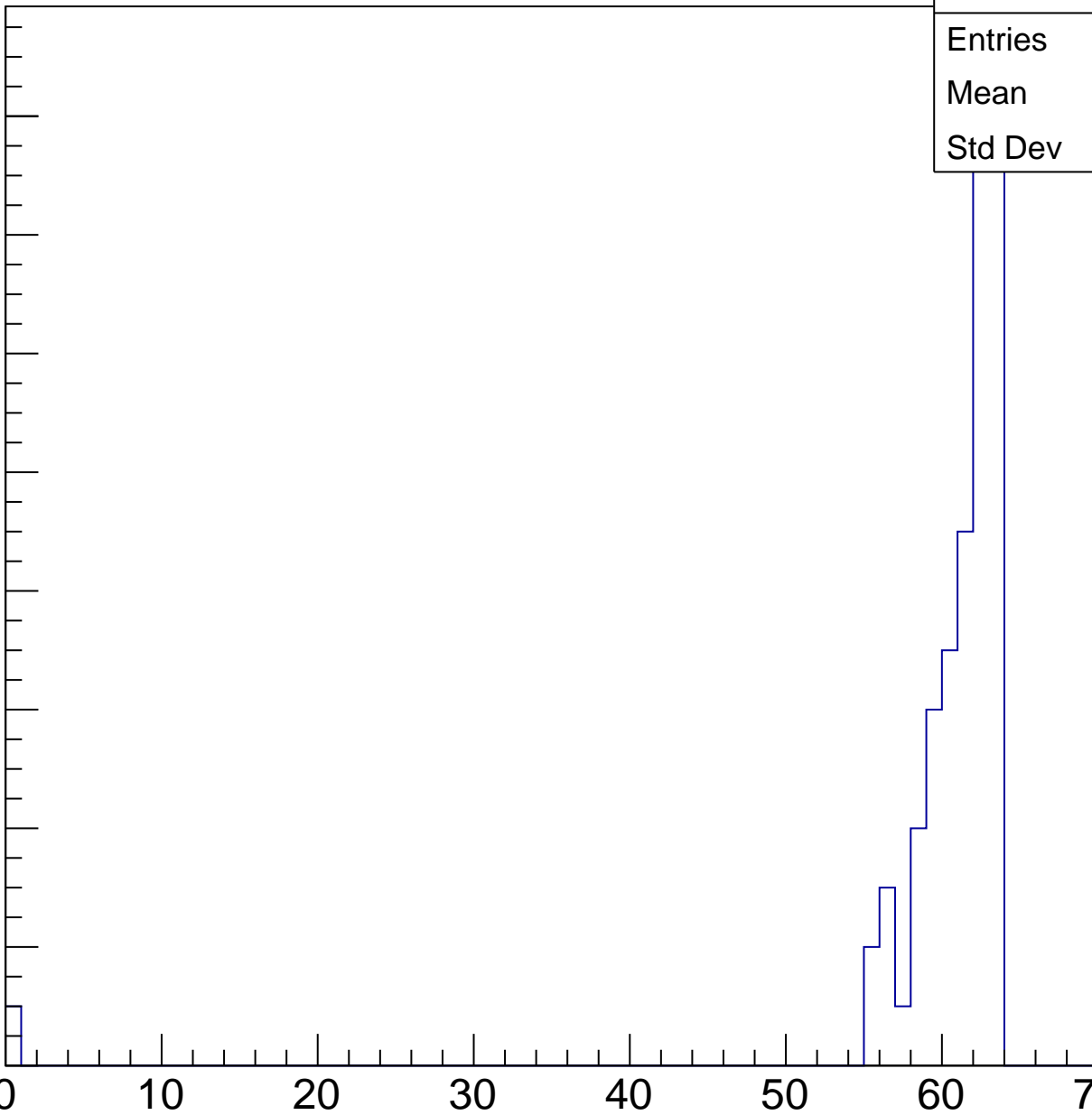
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	66
Mean	59.89
Std Dev	7.74

ampl



B0L101S, U24-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch31, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	29.92
Std Dev	4.158

Gaus mean : 30.5058

Gaus Width: 3.9381

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

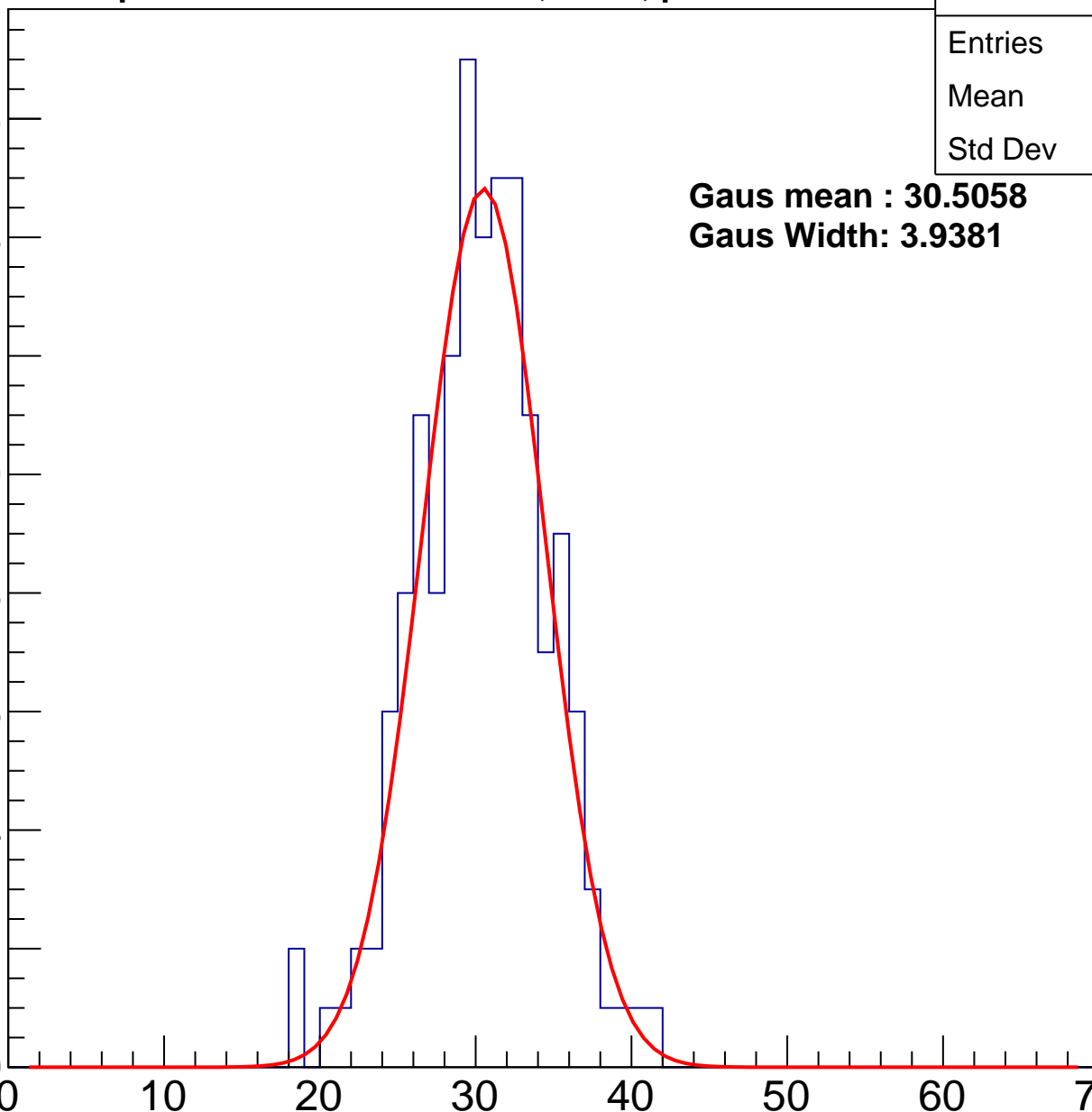
40

50

60

70

ampl



B0L101S, U24-ch31, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	36.87
Std Dev	4.762

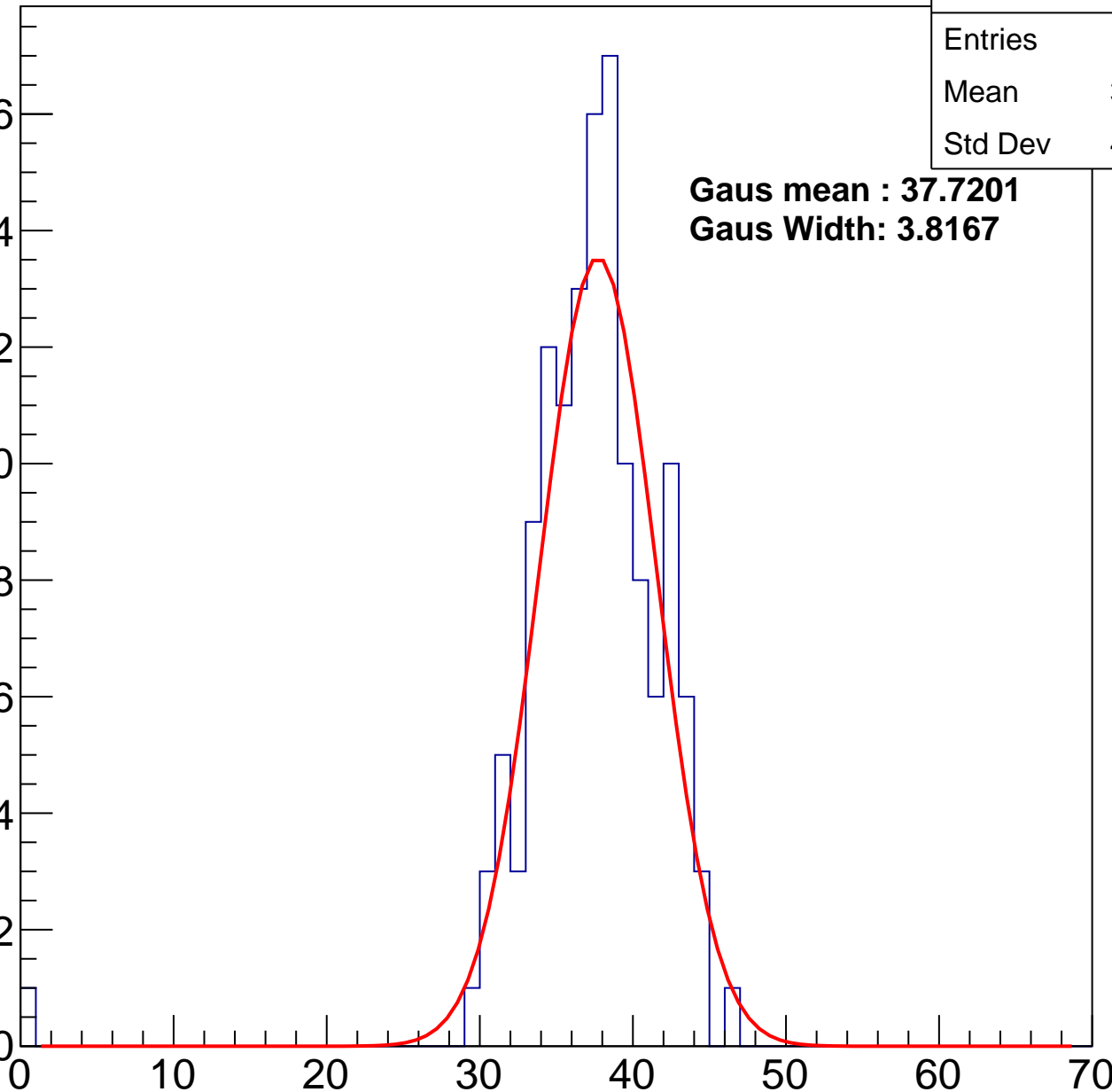
Gaus mean : 37.7201

Gaus Width: 3.8167

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch31, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	43.48
Std Dev	3.255

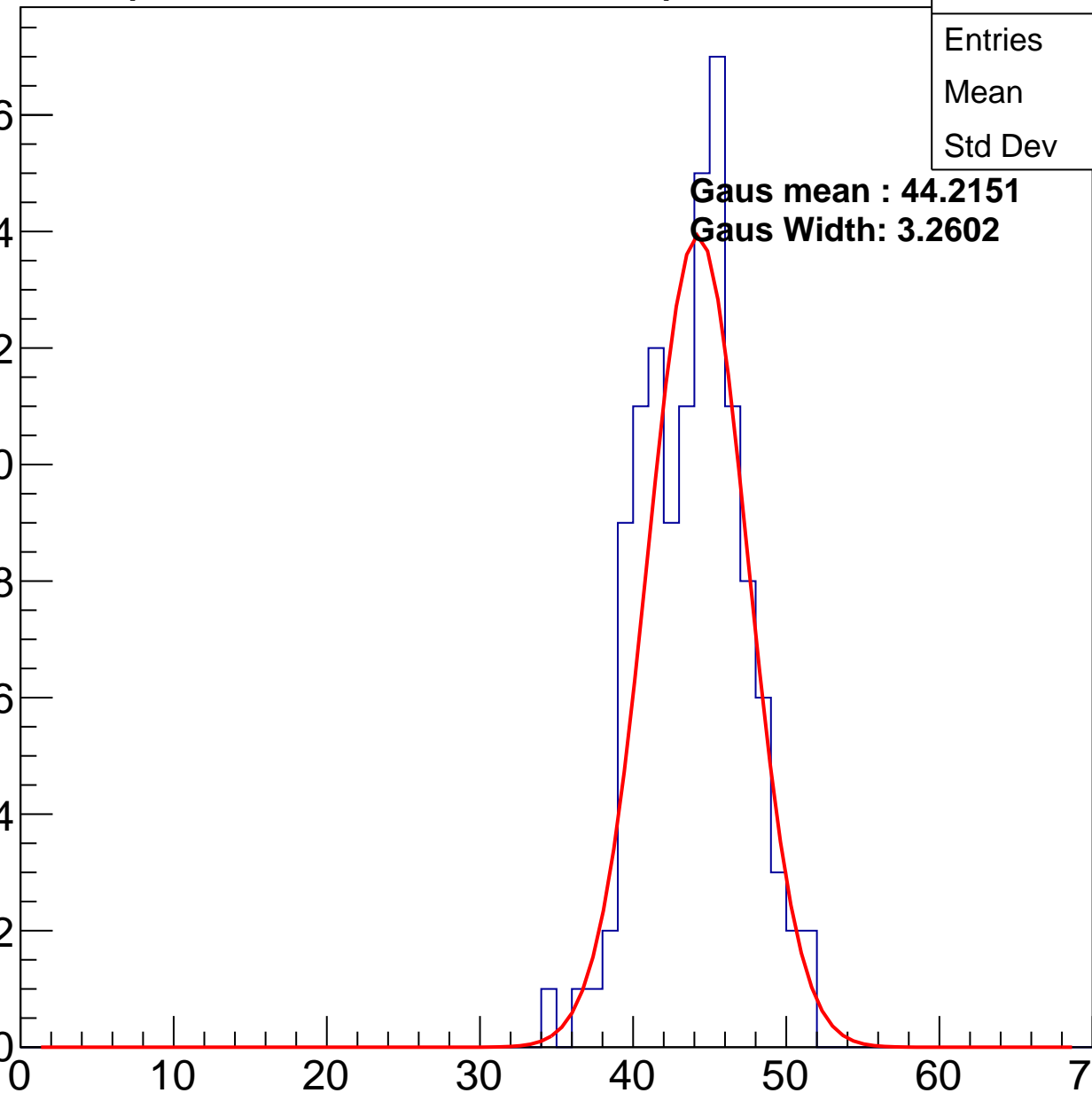
Gaus mean : 44.2151

Gaus Width: 3.2602

Entry

16
14
12
10
8
6
4
2
0

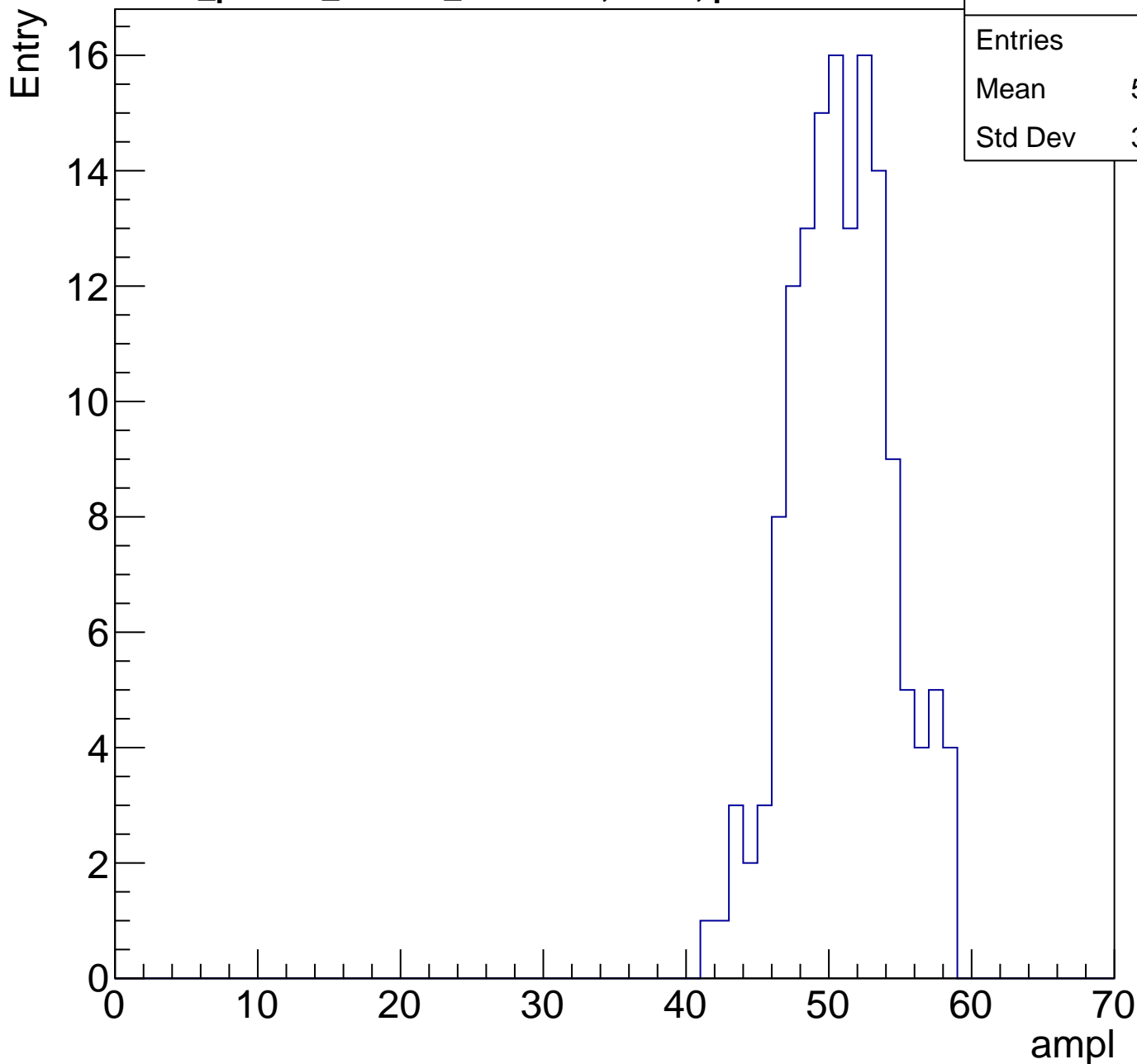
ampl



B0L101S, U24-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	50.45
Std Dev	3.565



B0L101S, U24-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

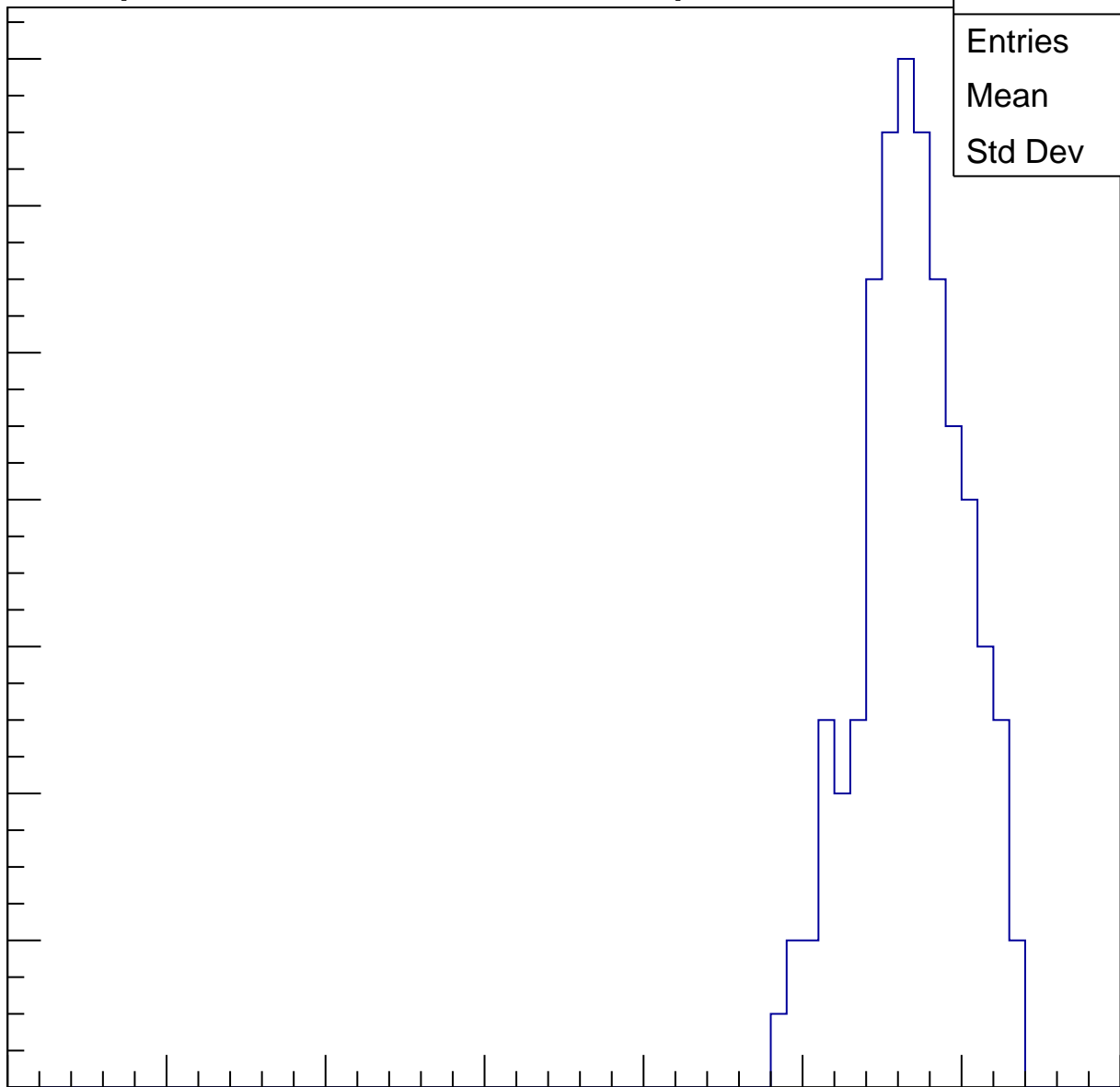
Entries	111
Mean	56.39
Std Dev	3.291

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch31, adc5

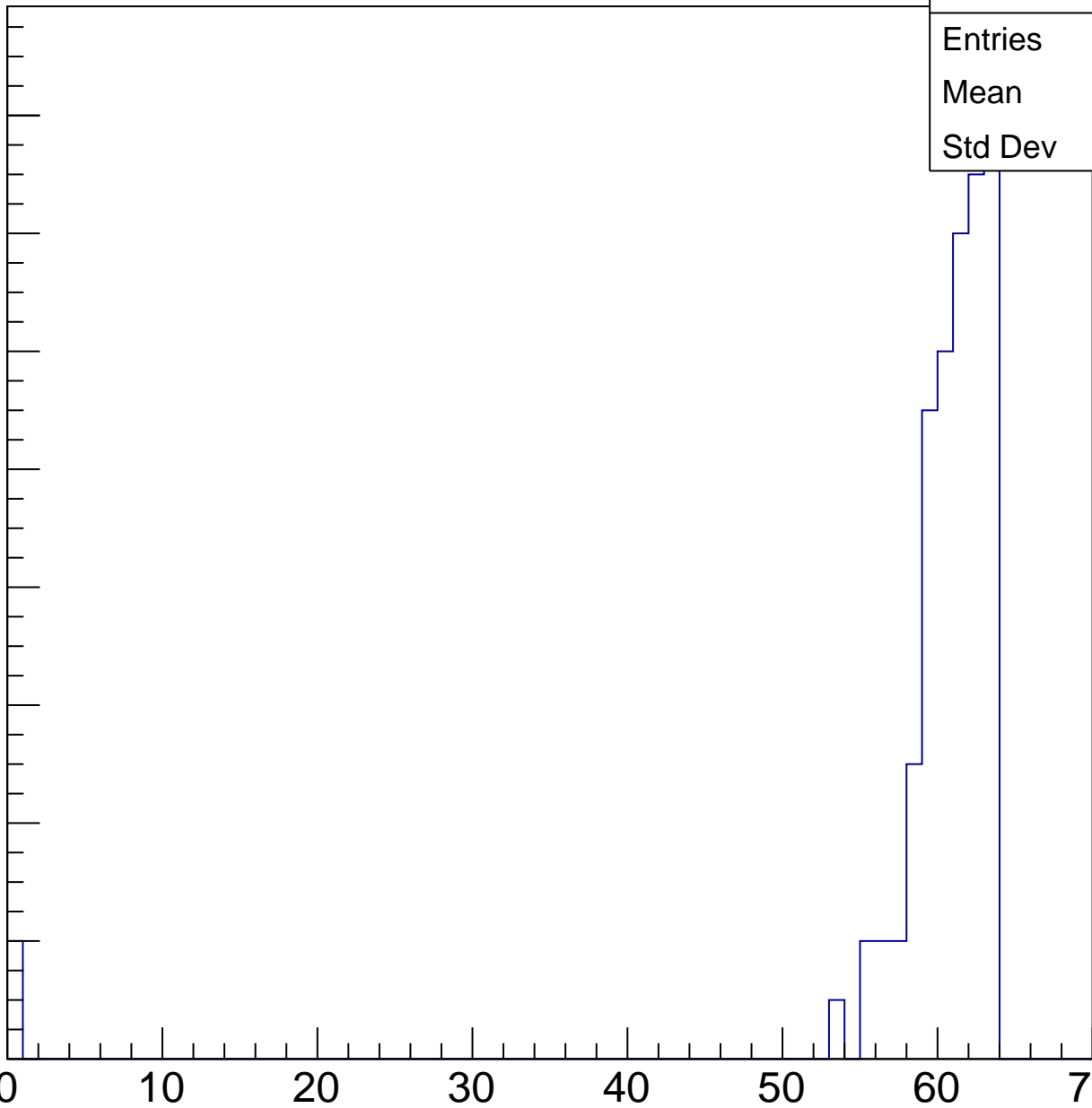
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.07
Std Dev	9.528

ampl



B0L101S, U24-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

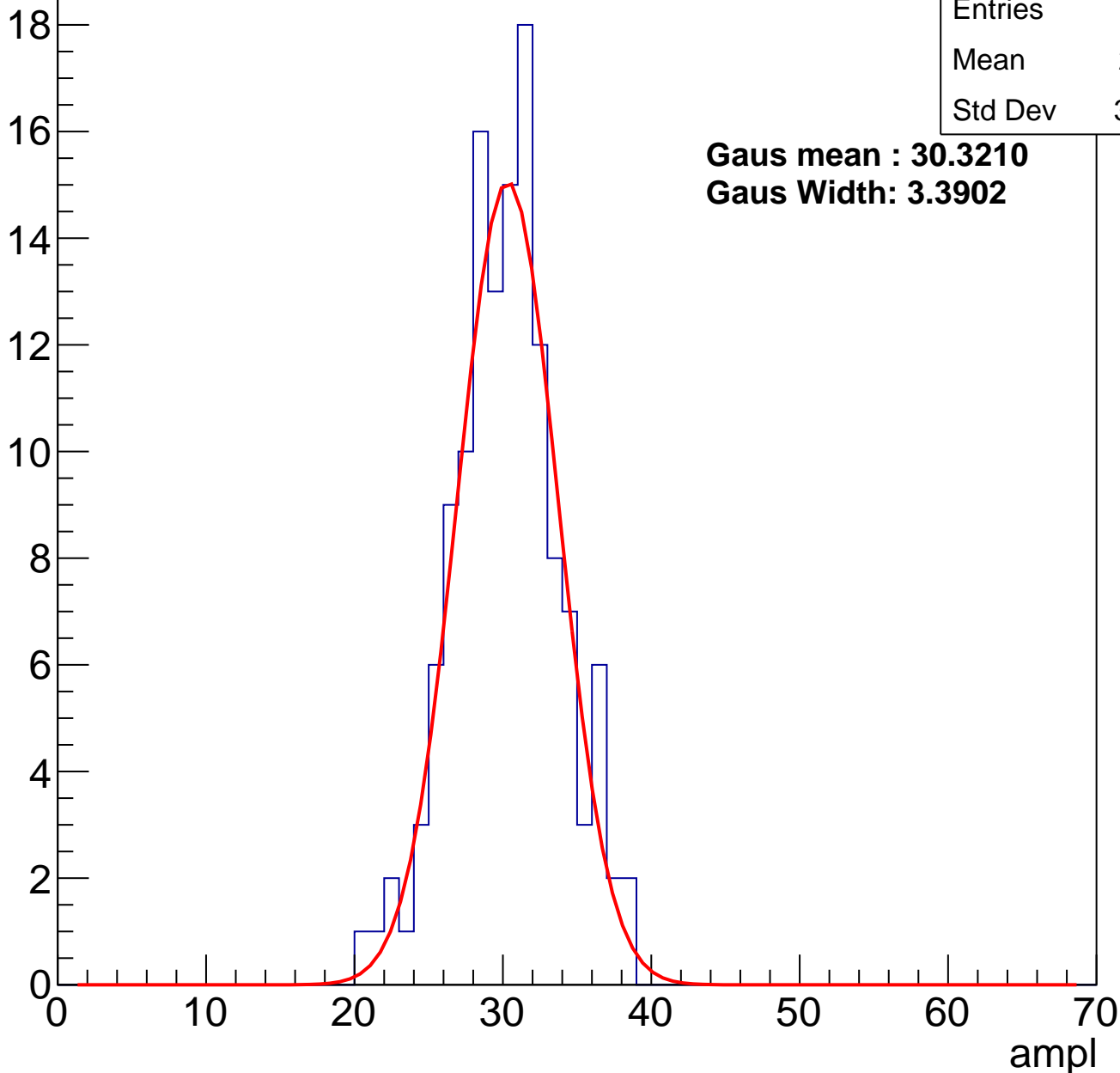


Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch32, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



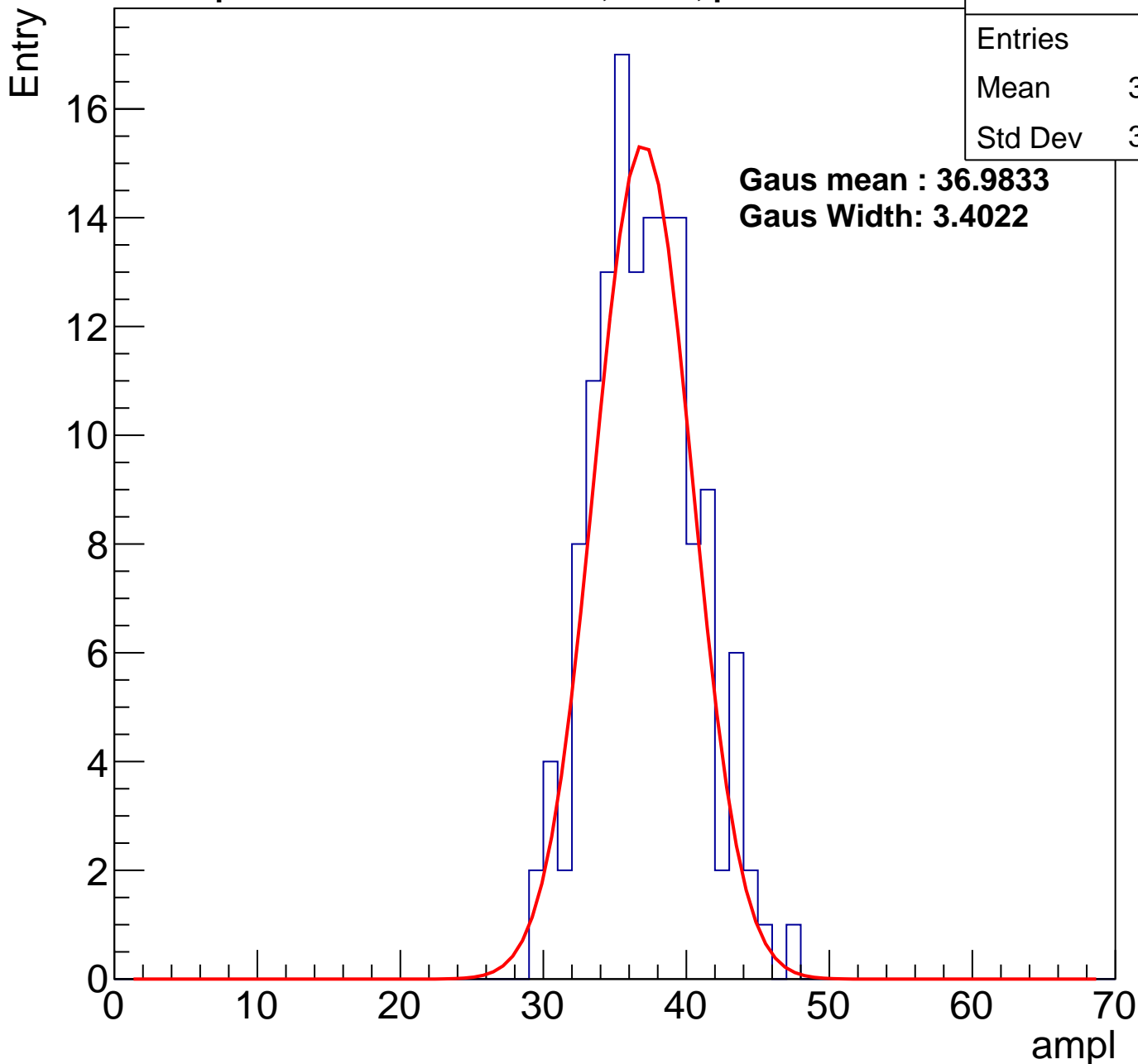
B0L101S, U24-ch32, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.67
Std Dev	3.542

Gaus mean : 36.9833

Gaus Width: 3.4022



B0L101S, U24-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

128

Mean

43.46

Std Dev

3.462

Gaus mean : 43.7077

Gaus Width: 3.4366

0

10

20

30

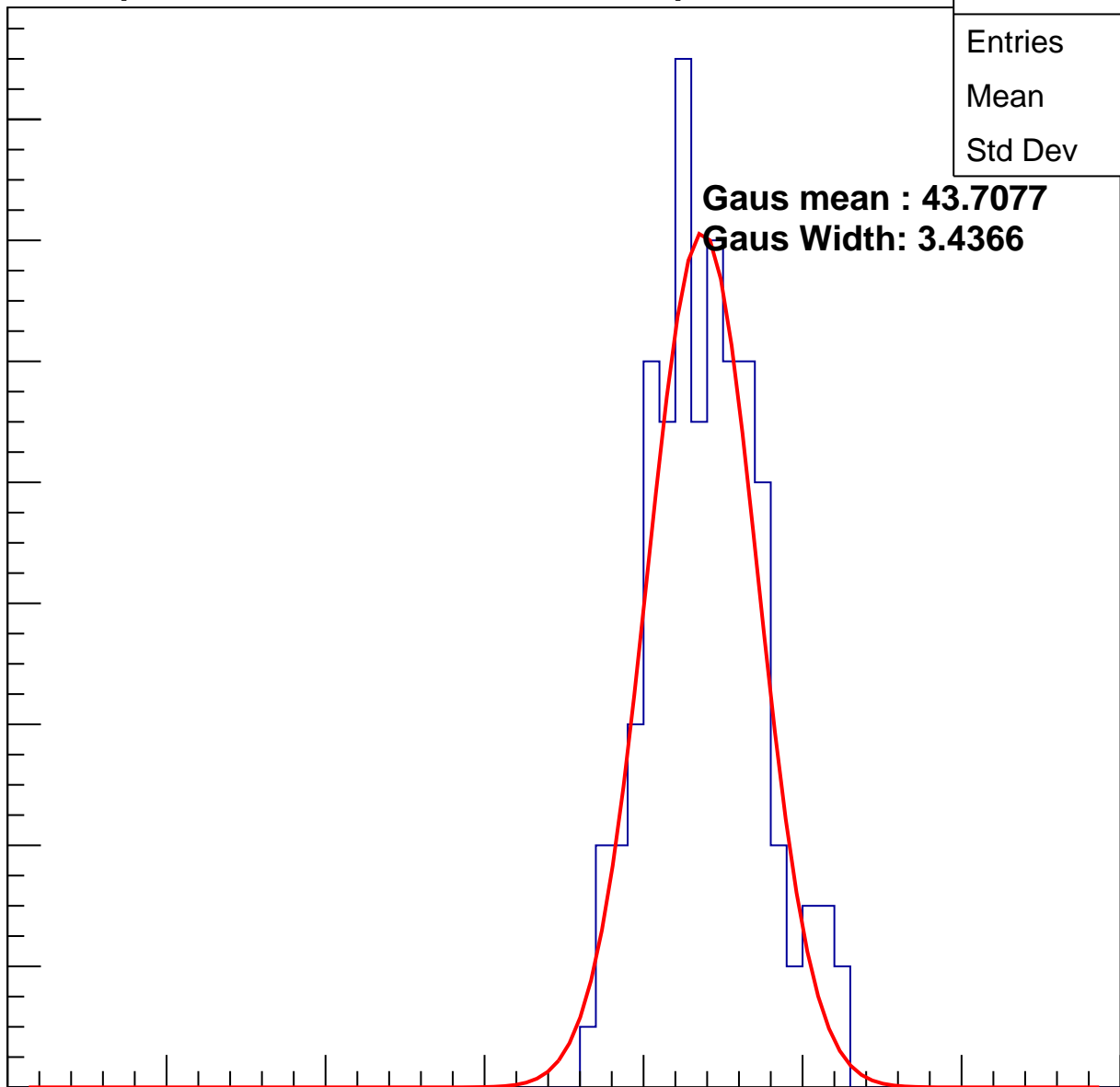
40

50

60

70

ampl



B0L101S, U24-ch32, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	50.91
Std Dev	3.859

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

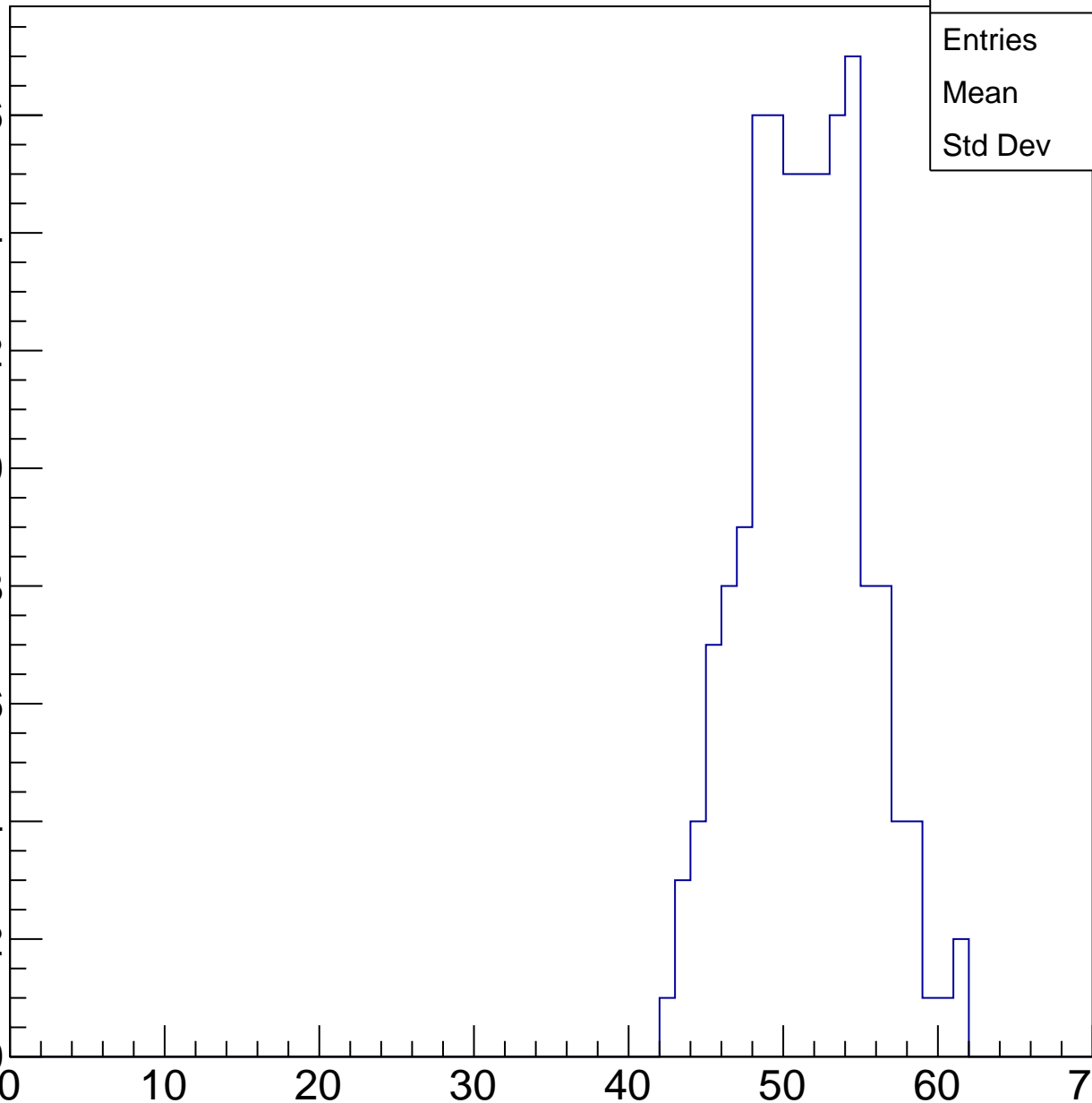
40

50

60

70

ampl



B0L101S, U24-ch32, adc4

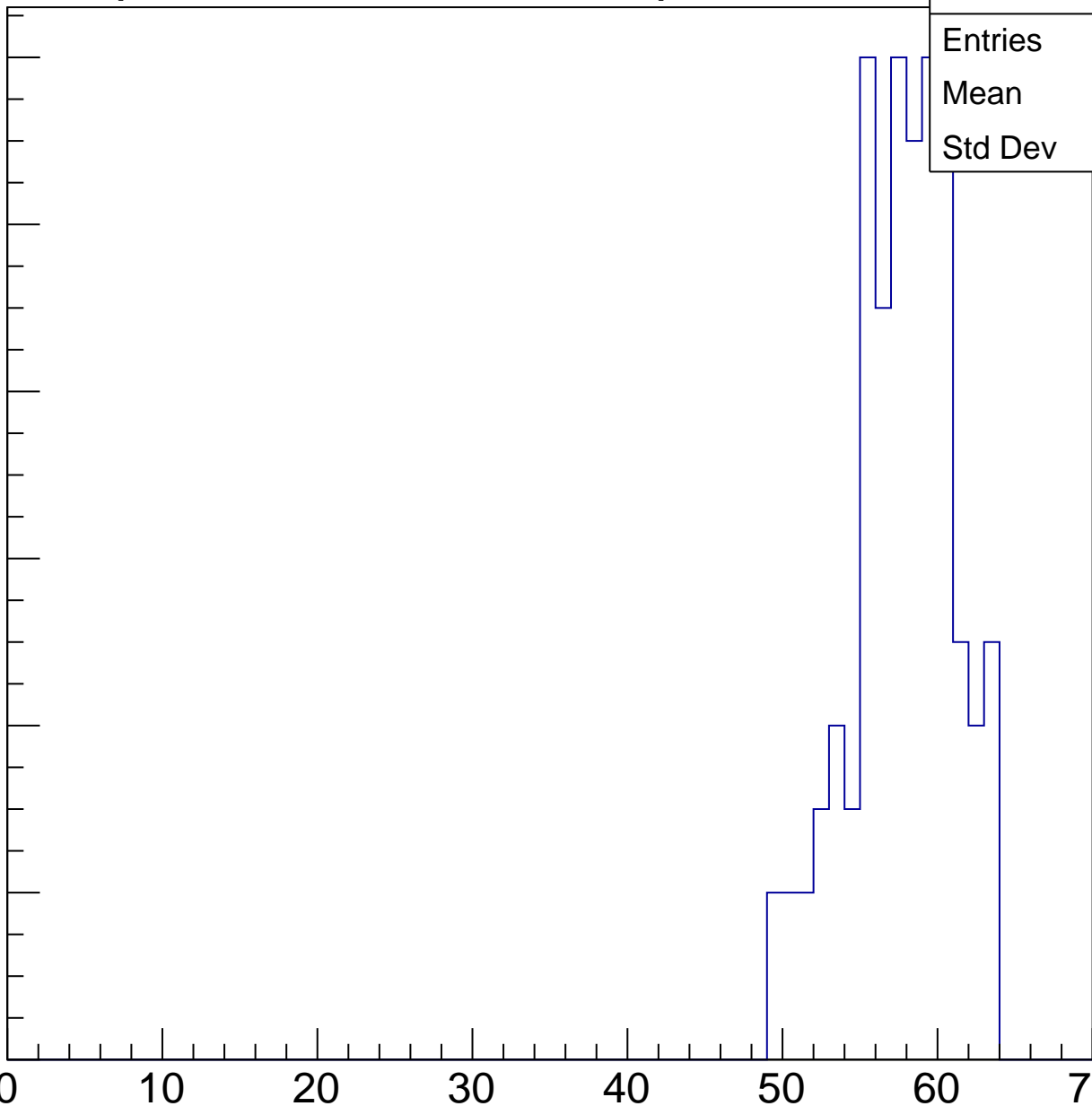
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	98
Mean	57.27
Std Dev	3.259

ampl



B0L101S, U24-ch32, adc5

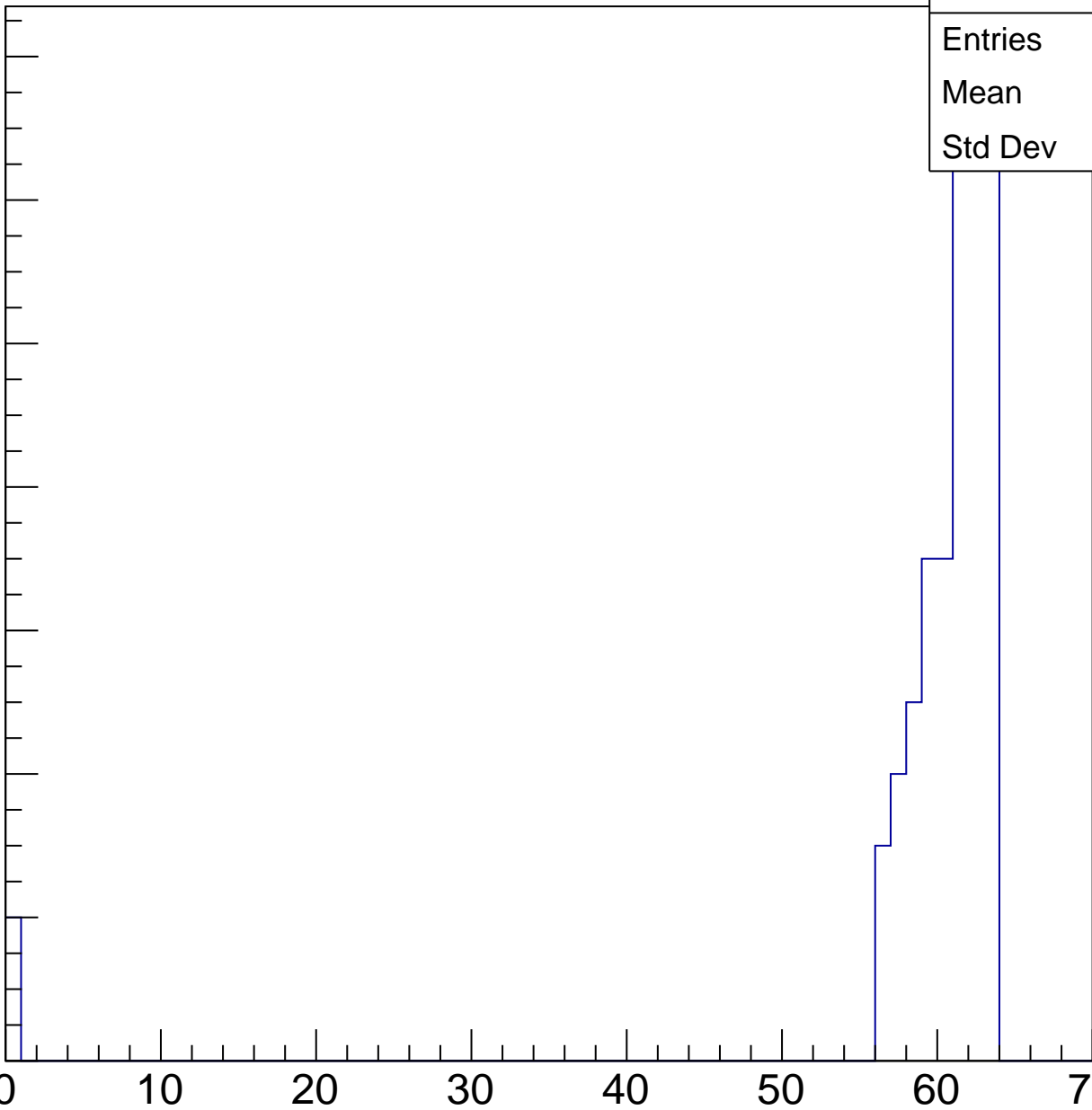
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	58.81
Std Dev	10.43

ampl



B0L101S, U24-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

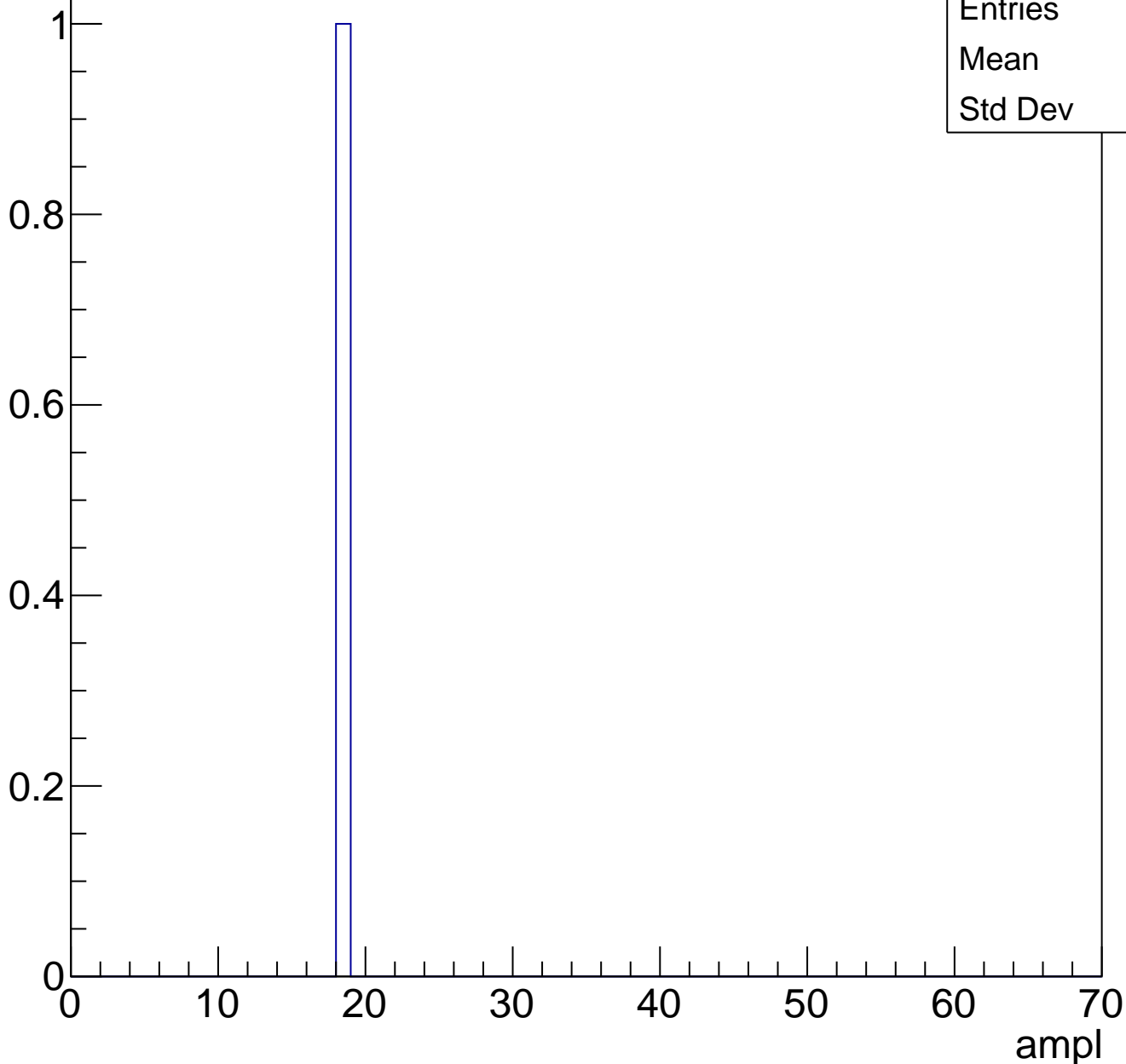
Entry



B0L101S, U24-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



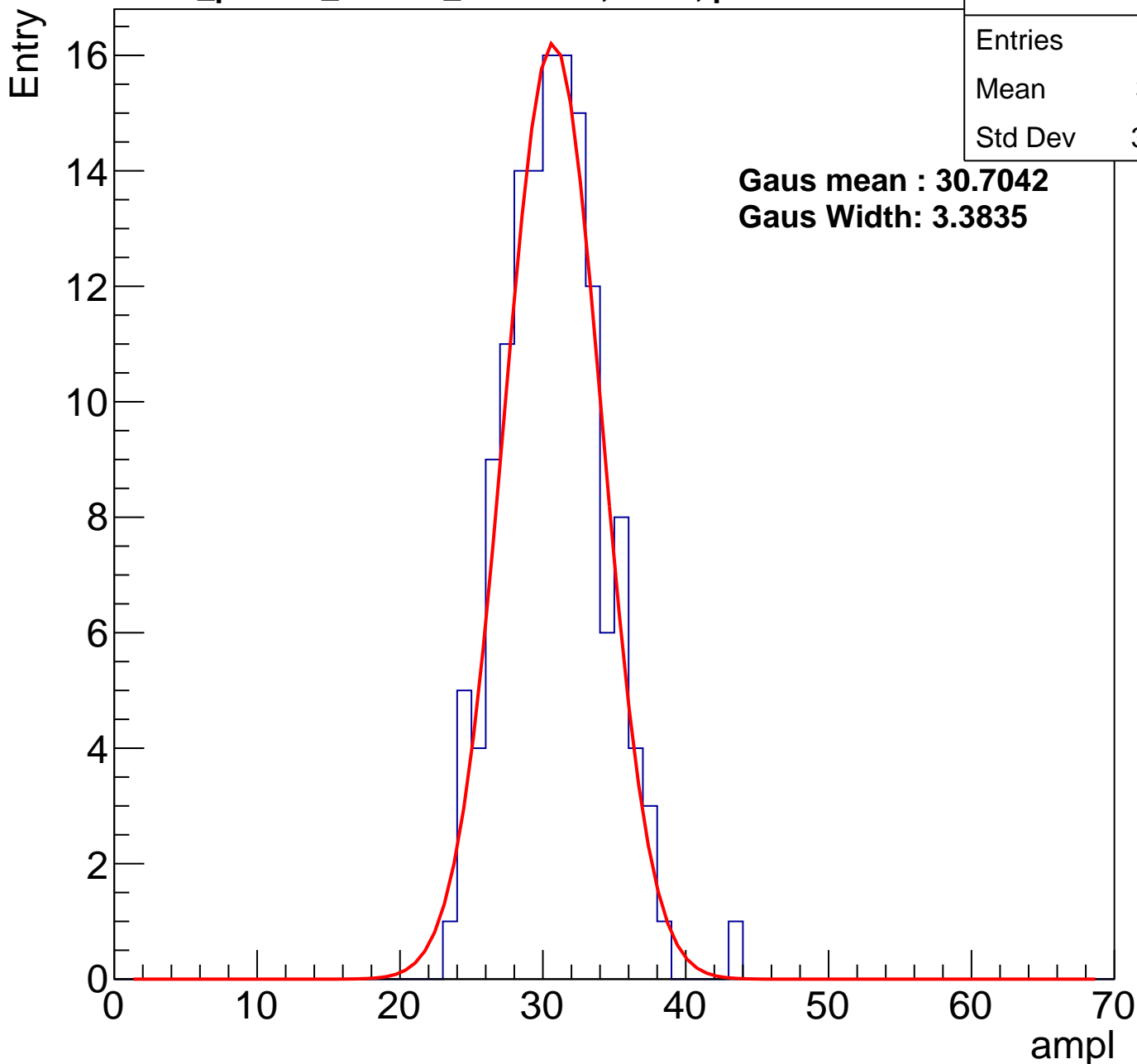
B0L101S, U24-ch33, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	30.31
Std Dev	3.414

Gaus mean : 30.7042

Gaus Width: 3.3835



B0L101S, U24-ch33, adc1

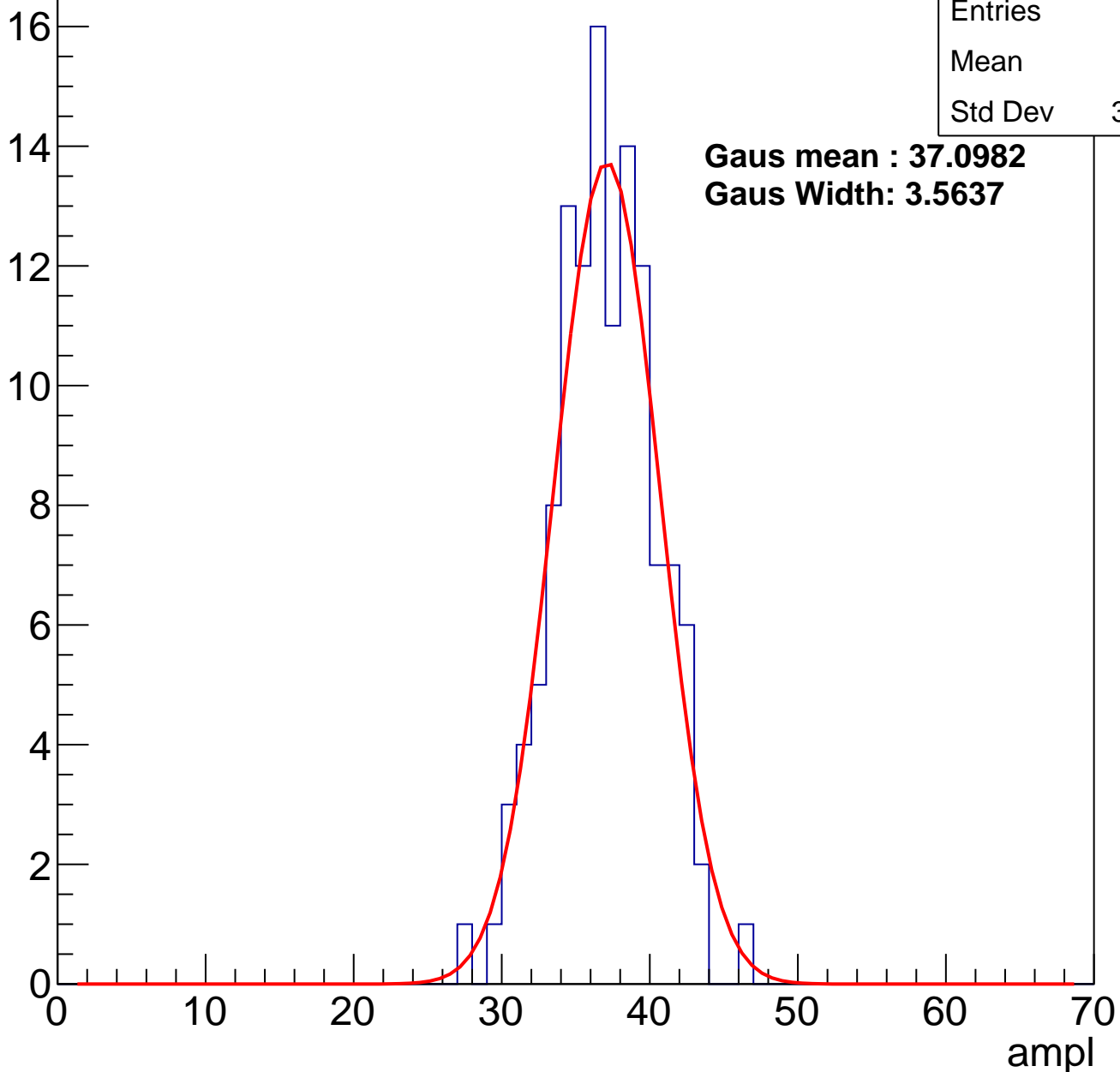
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.5
Std Dev	3.372

Gaus mean : 37.0982

Gaus Width: 3.5637

Entry



B0L101S, U24-ch33, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	43.15
Std Dev	3.413

Gaus mean : 43.3721

Gaus Width: 3.7620

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

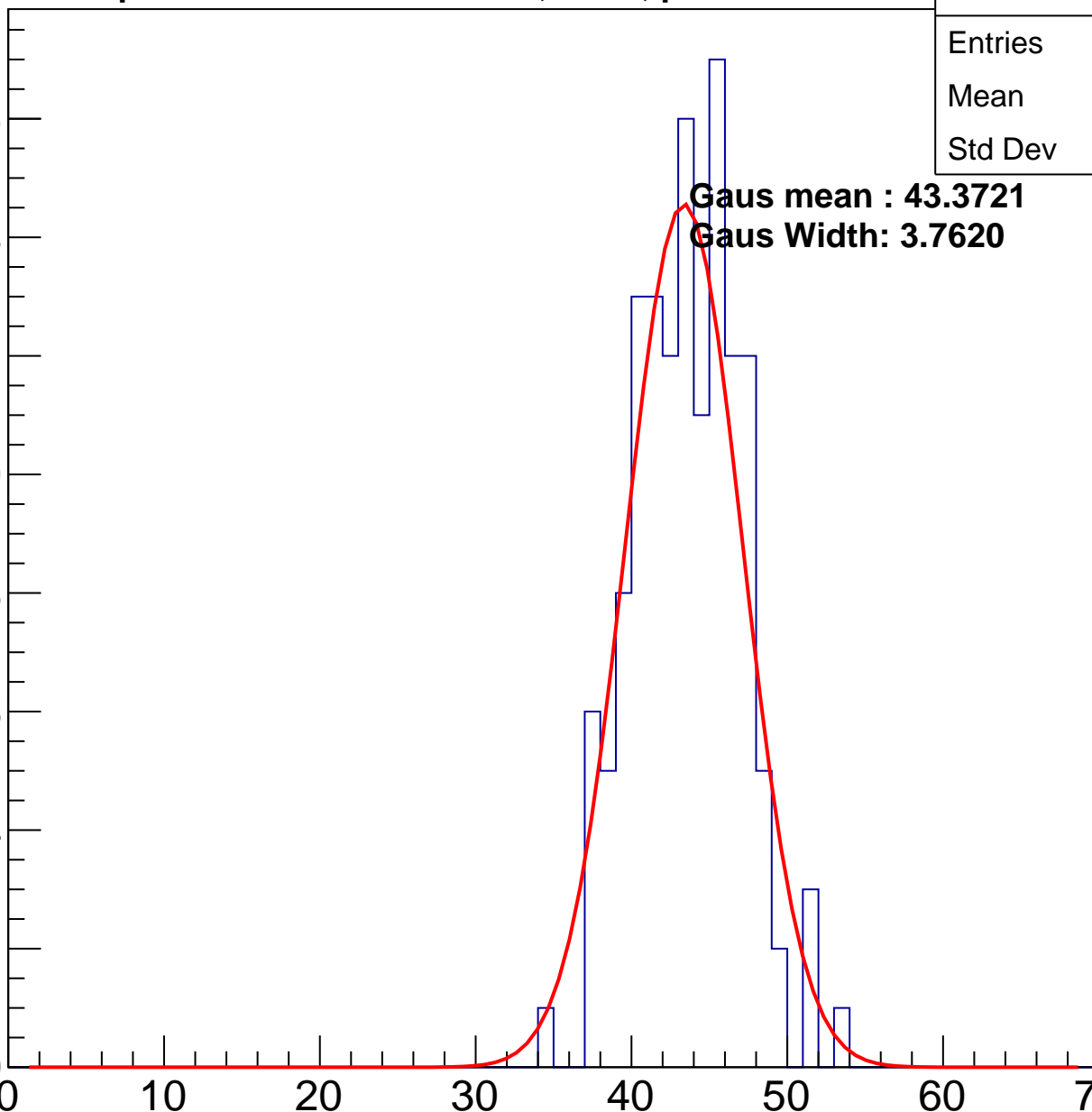
40

50

60

70

ampl



B0L101S, U24-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

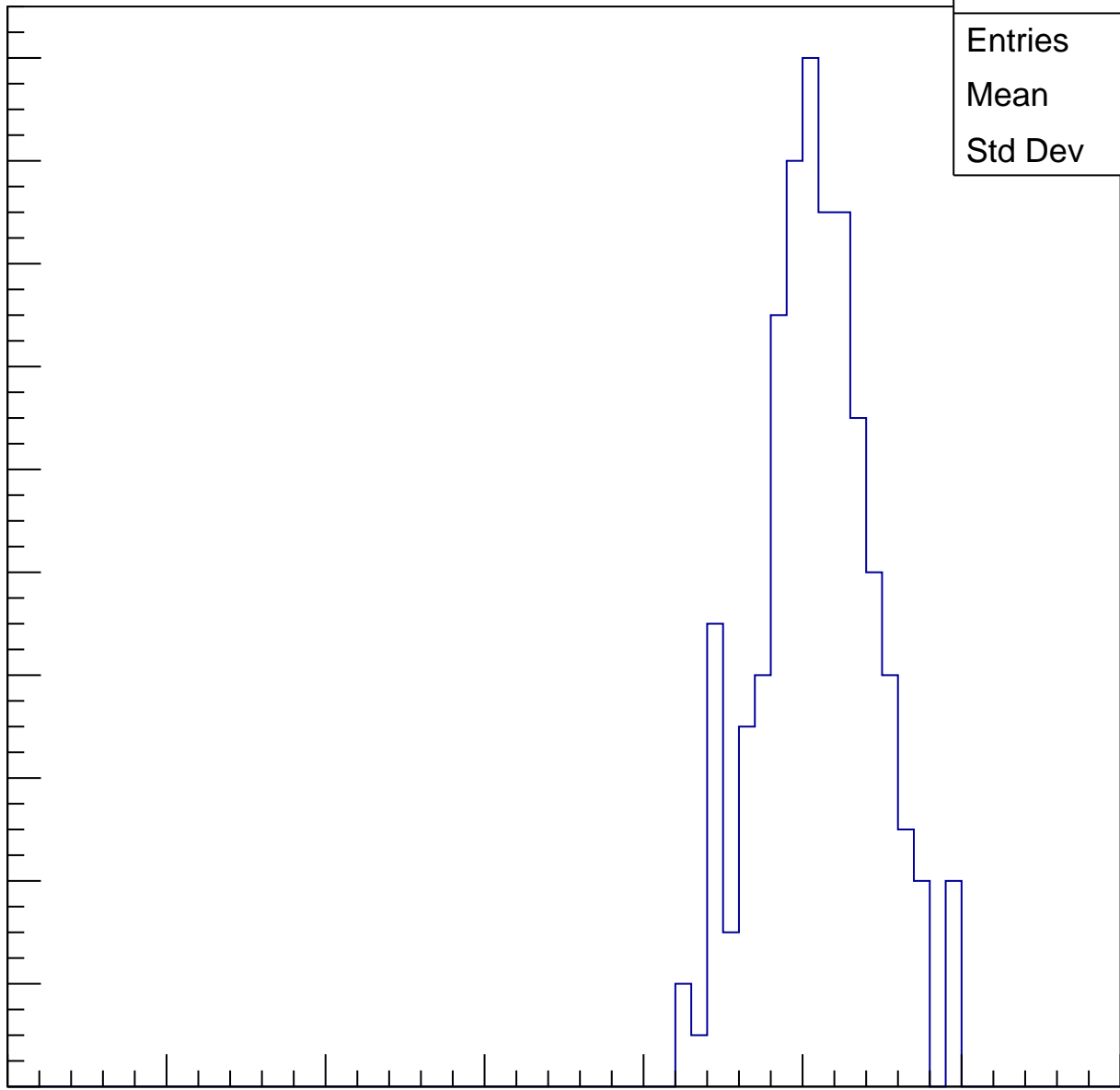
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	161
Mean	50.45
Std Dev	3.584

ampl

0 10 20 30 40 50 60 70

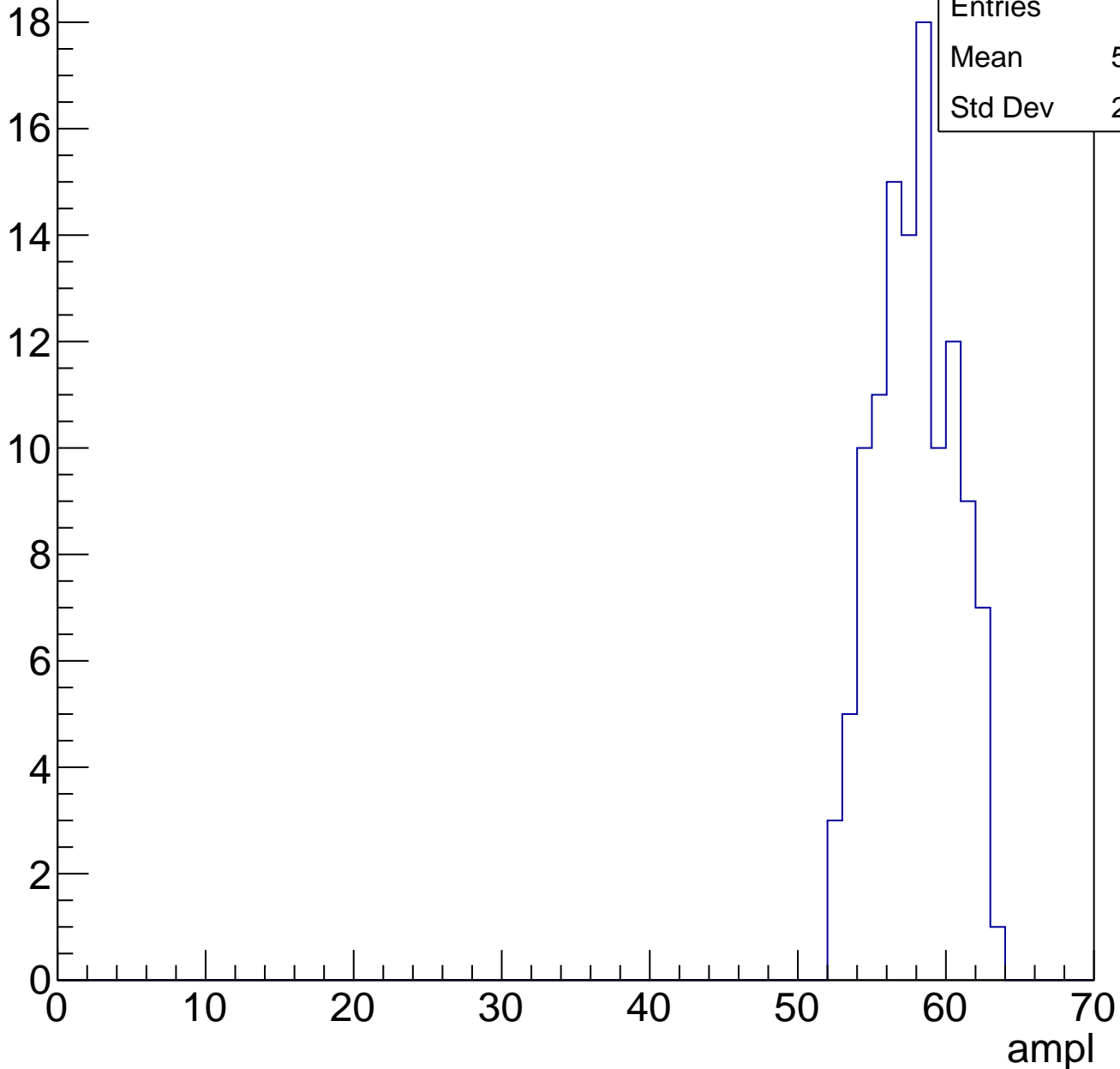


B0L101S, U24-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	57.43
Std Dev	2.644

Entry



B0L101S, U24-ch33, adc5

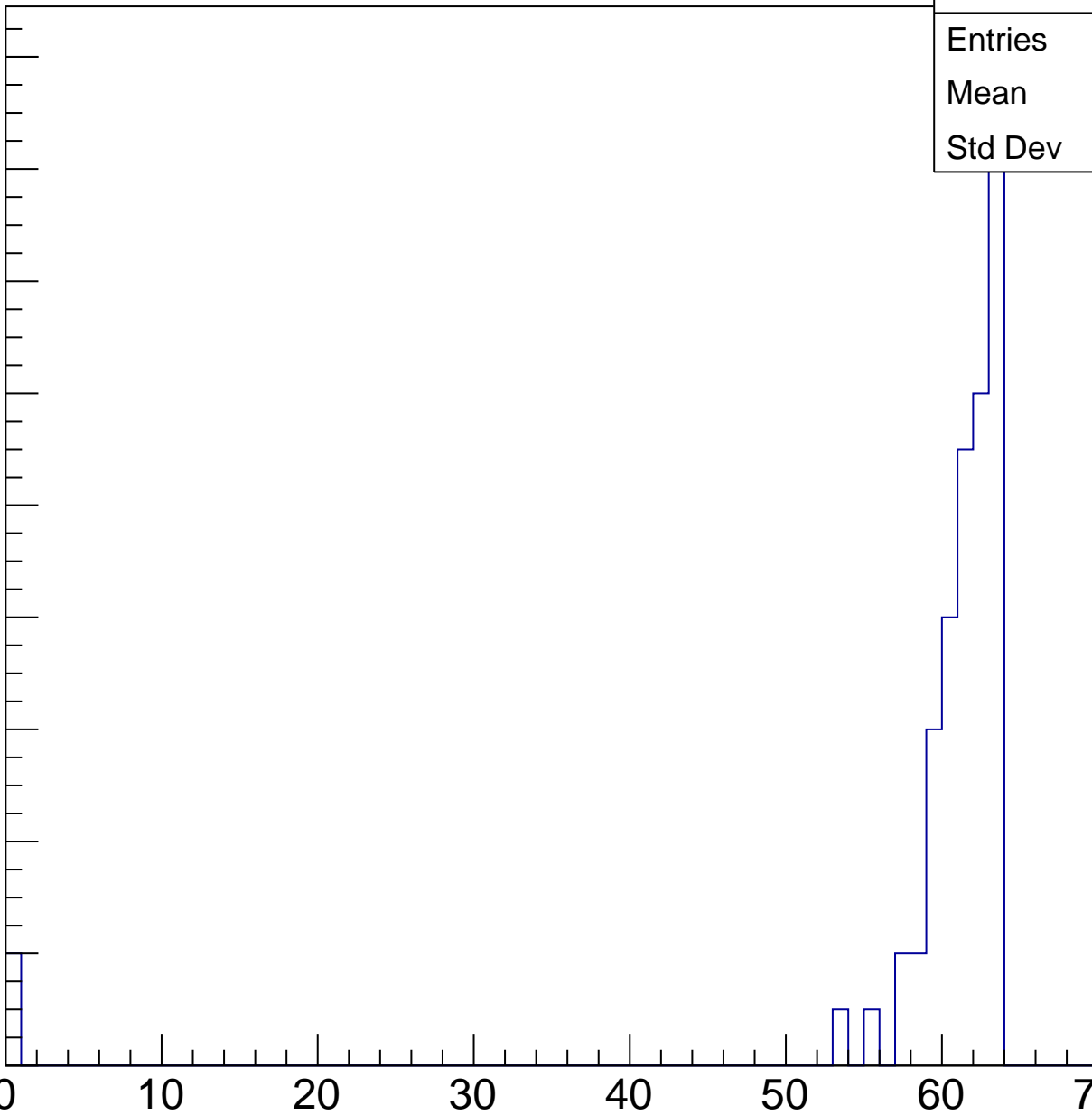
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	63
Mean	59.06
Std Dev	10.89

ampl



B0L101S, U24-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch34, adc0

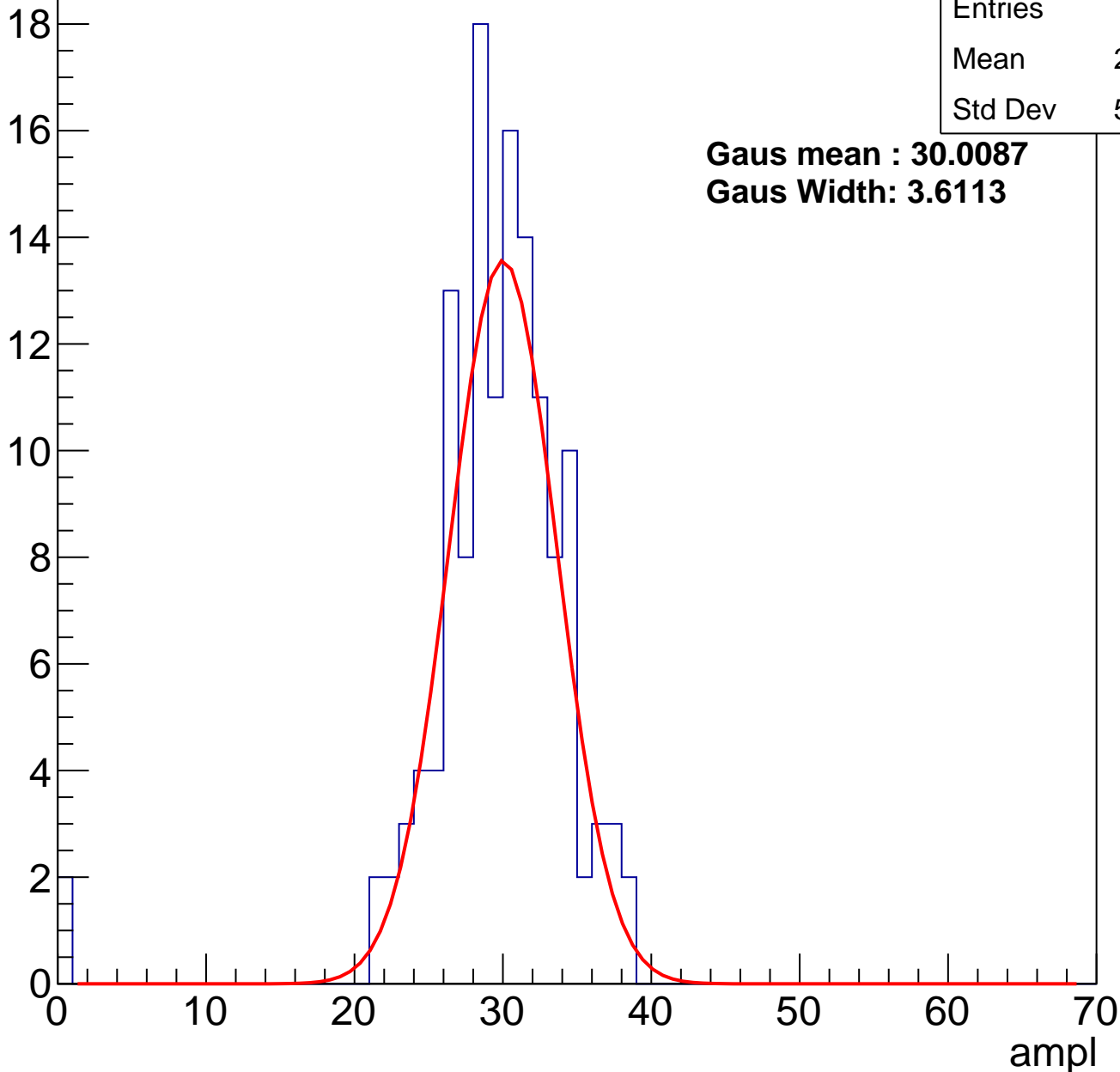
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	29.14
Std Dev	5.049

Gaus mean : 30.0087

Gaus Width: 3.6113

Entry



B0L101S, U24-ch34, adc1

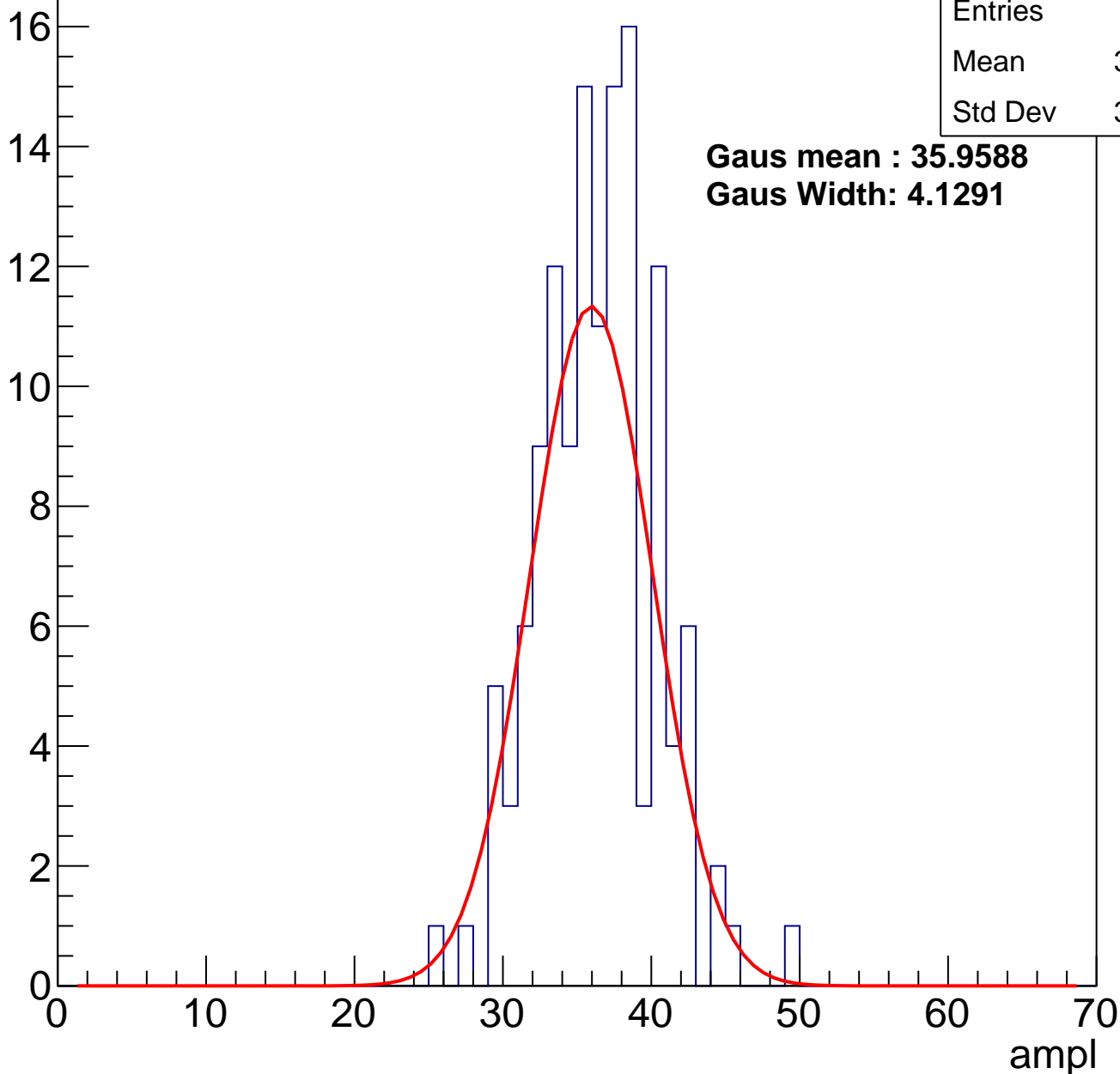
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	35.92
Std Dev	3.892

Gaus mean : 35.9588

Gaus Width: 4.1291

Entry



B0L101S, U24-ch34, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	42.28
Std Dev	3.608

Gaus mean : 43.3133

Gaus Width: 3.4307

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

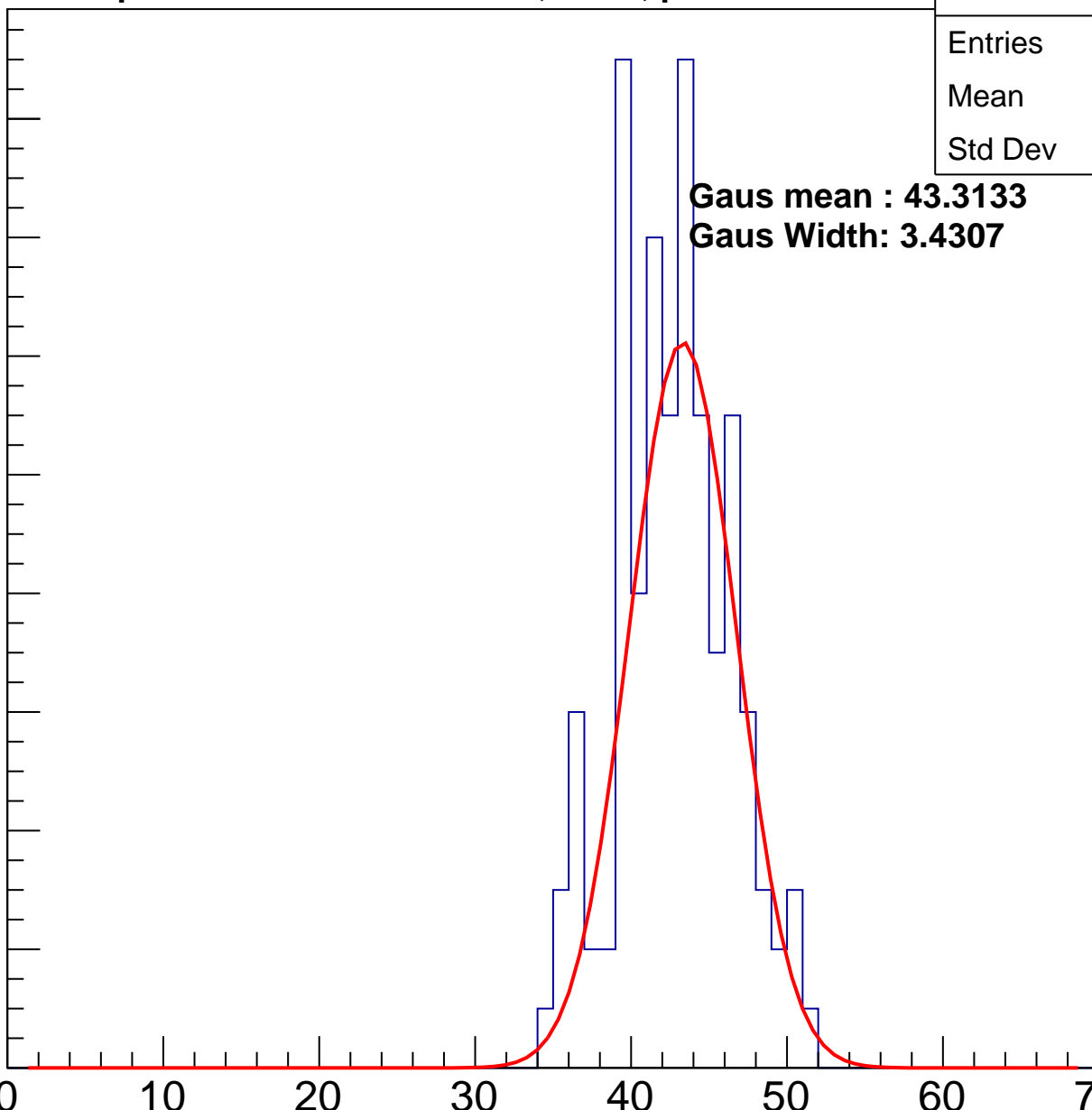
40

50

60

70

ampl



B0L101S, U24-ch34, adc3

calib_packv5_042523_0143.root, FC#1, port C1

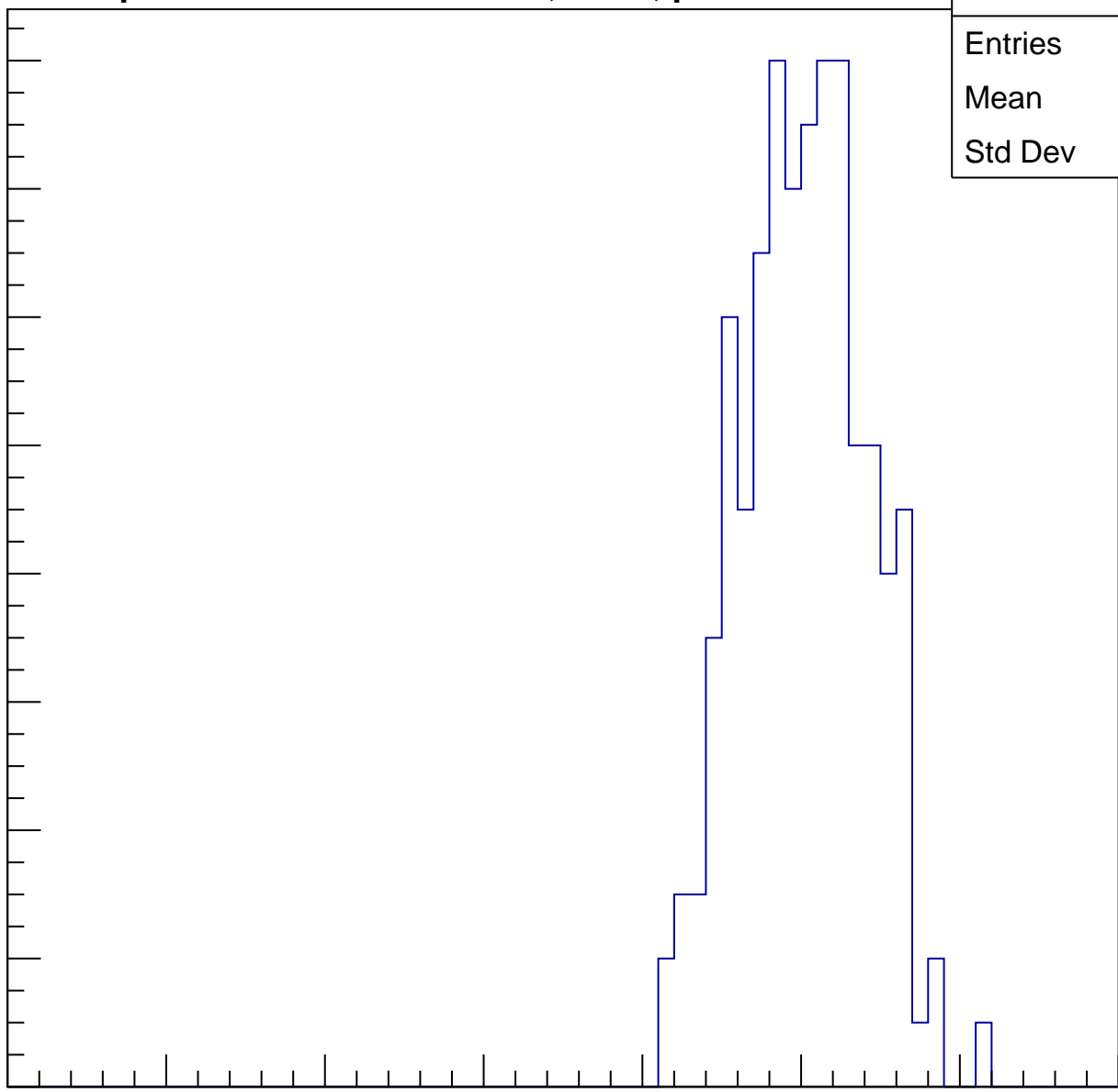
Entries	167
Mean	49.76
Std Dev	3.896

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch34, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	87
Mean	55.84
Std Dev	3.533

B0L101S, U24-ch34, adc5

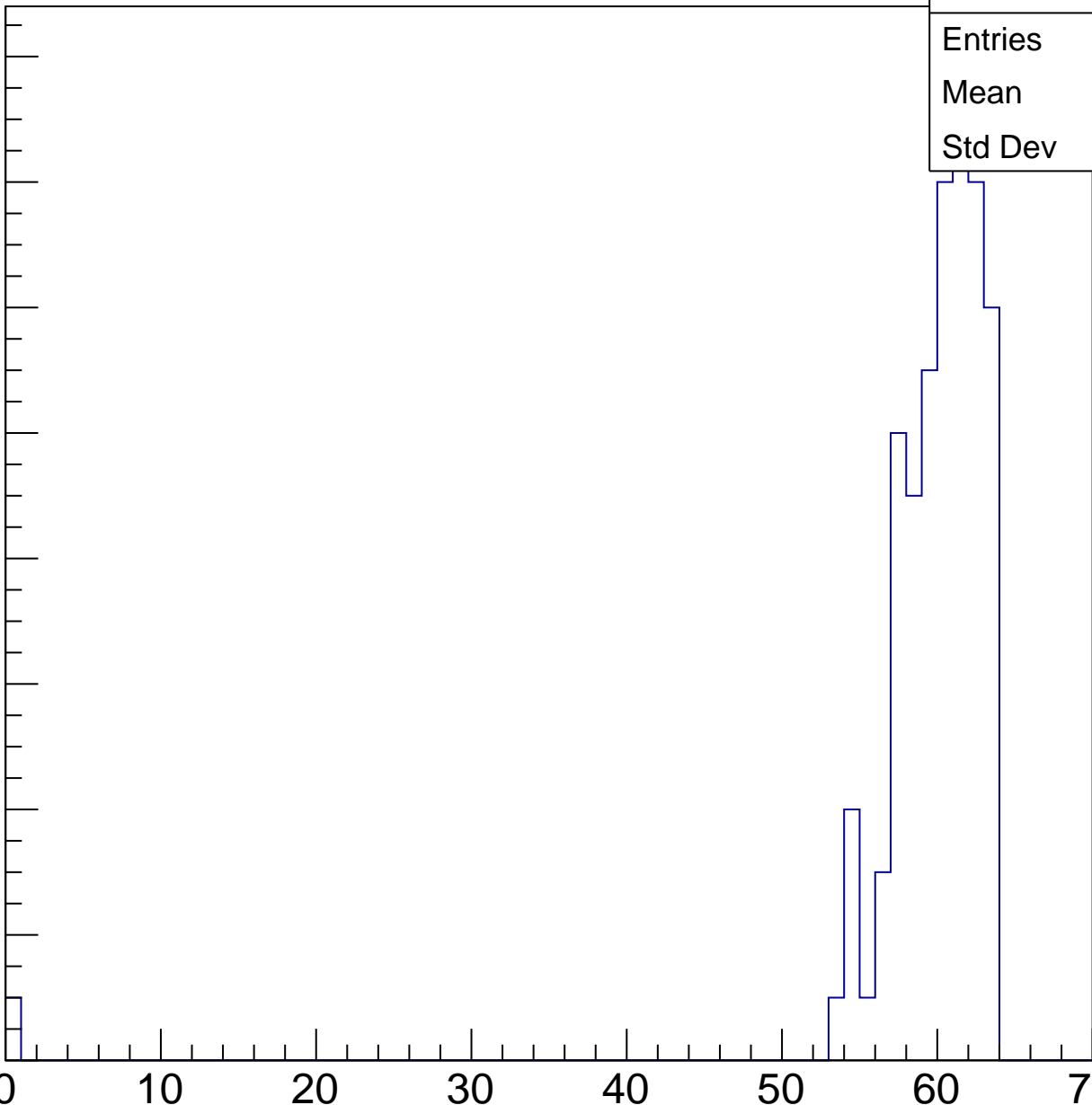
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.09
Std Dev	6.538

ampl

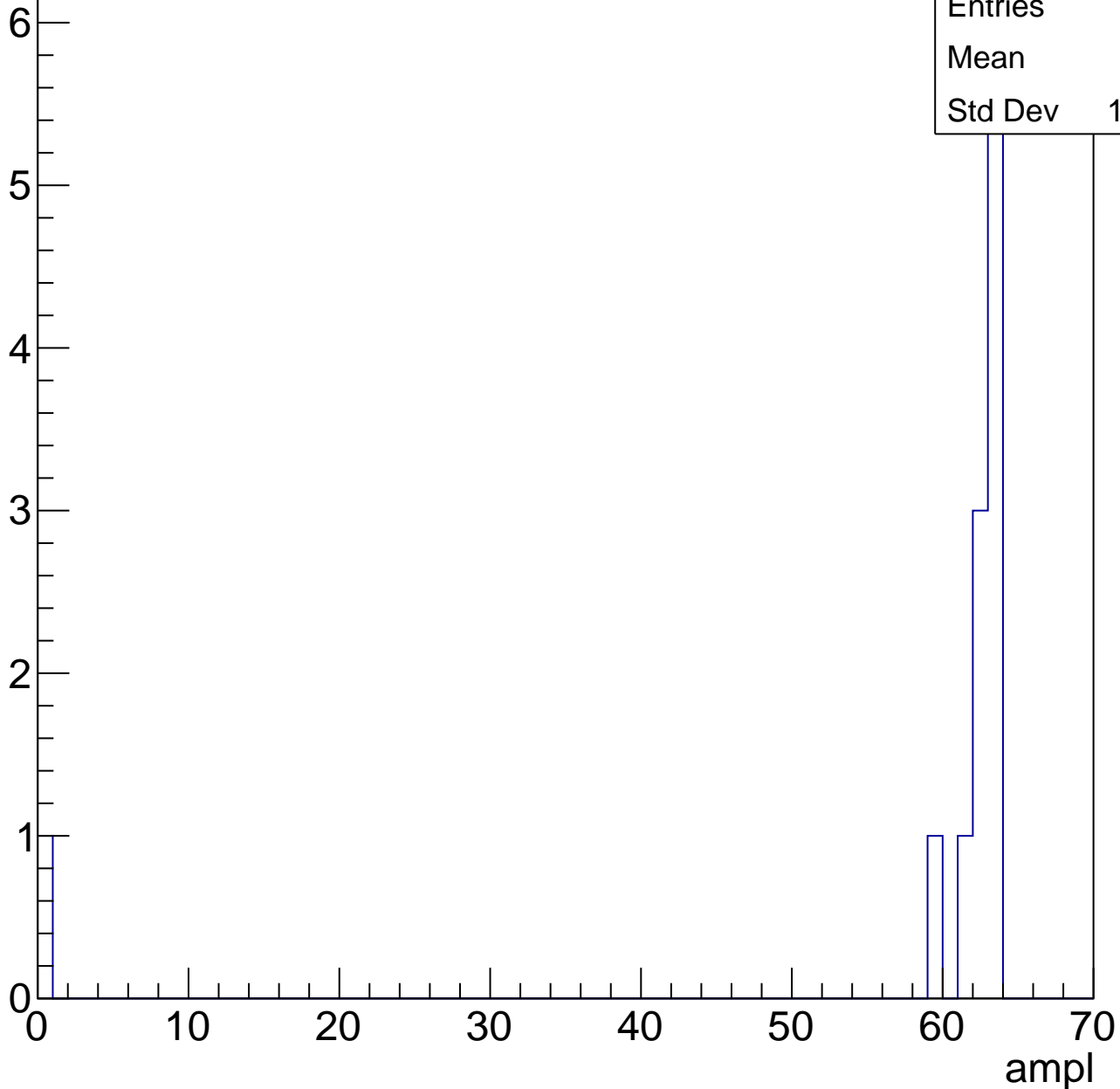


B0L101S, U24-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	57
Std Dev	17.22



B0L101S, U24-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch35, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	30.98
Std Dev	3.275

Gaus mean : 31.8142

Gaus Width: 3.4513

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

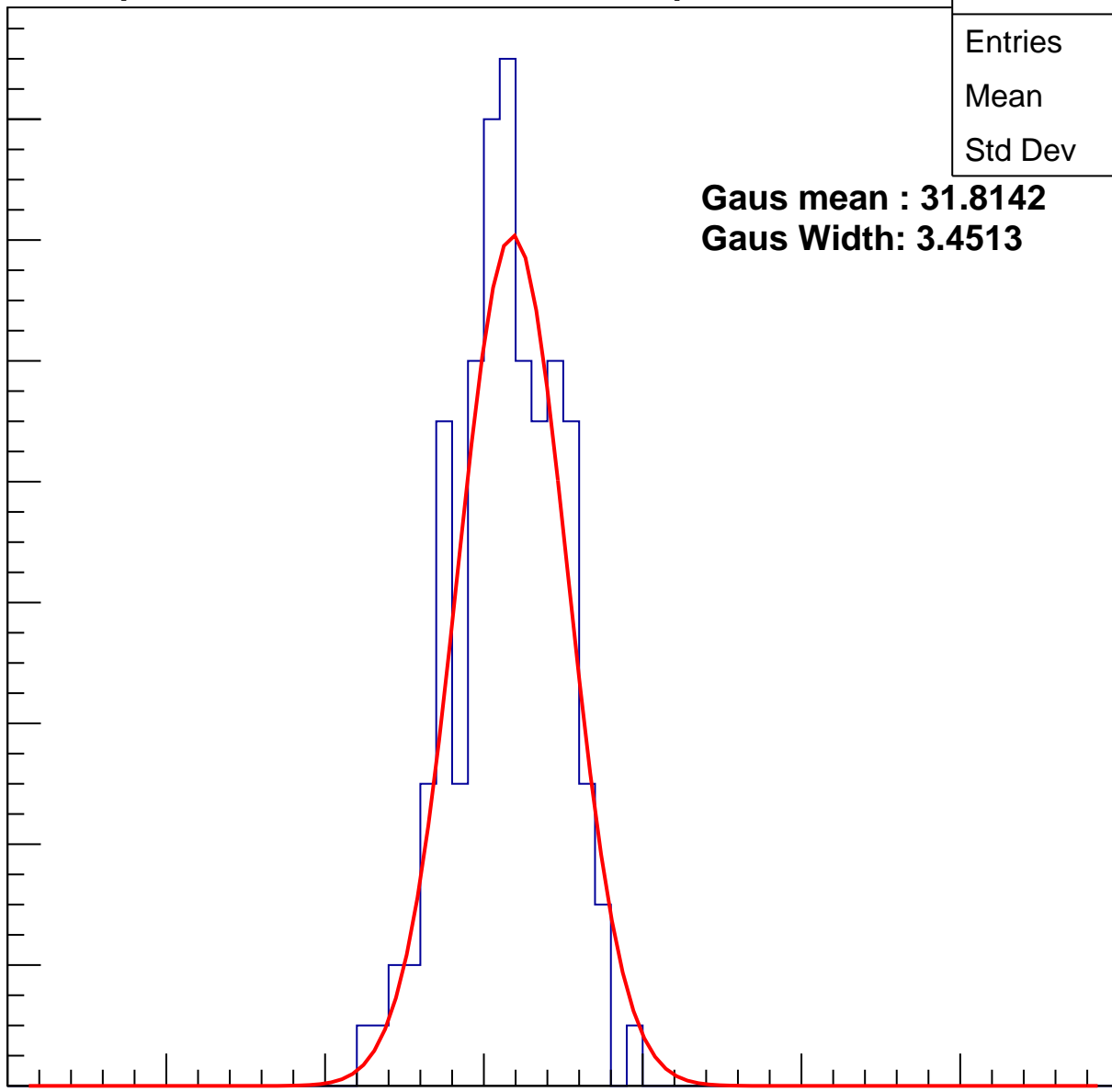
40

50

60

70

ampl



B0L101S, U24-ch35, adc1

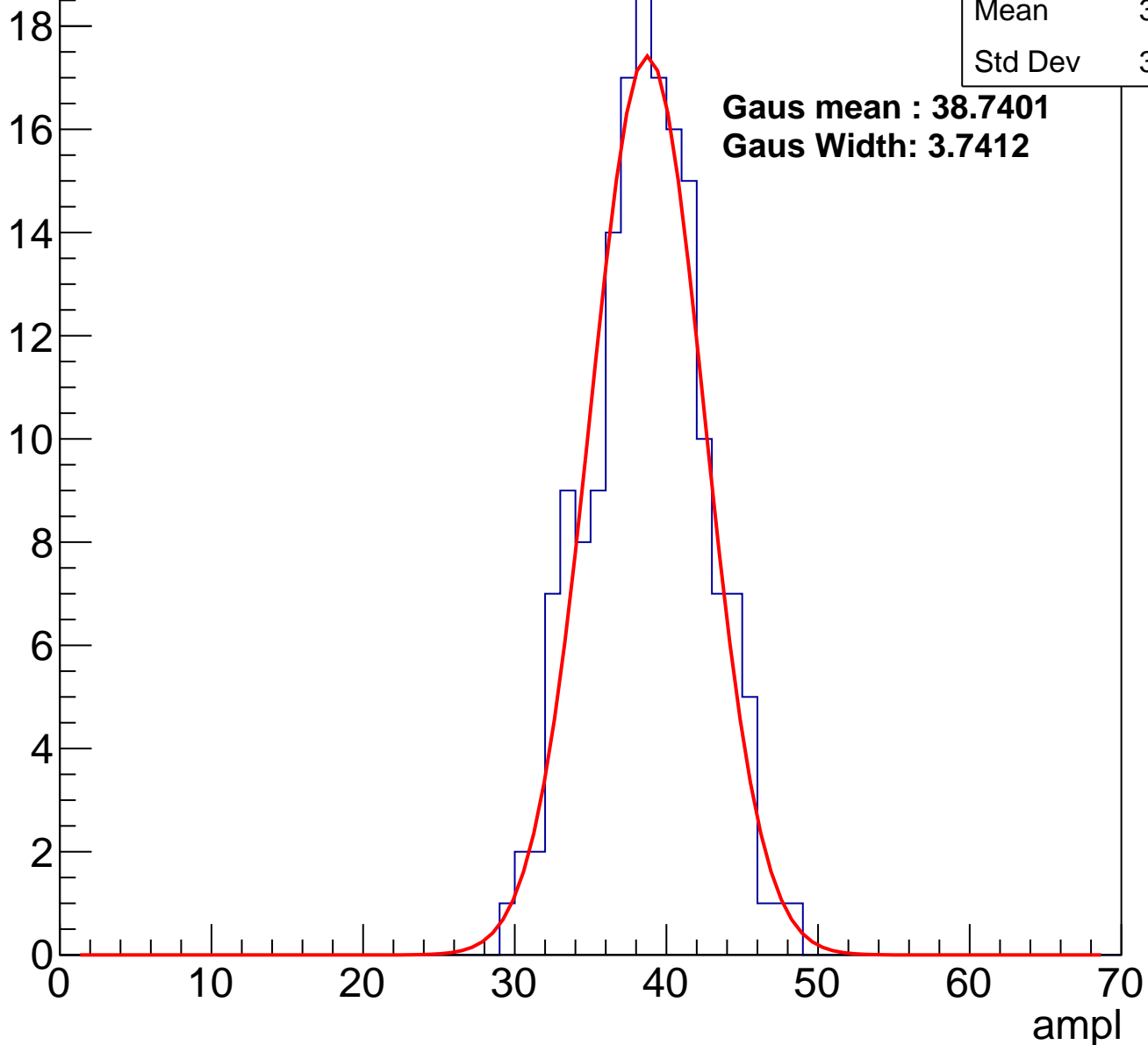
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	38.26
Std Dev	3.738

Gaus mean : 38.7401

Gaus Width: 3.7412

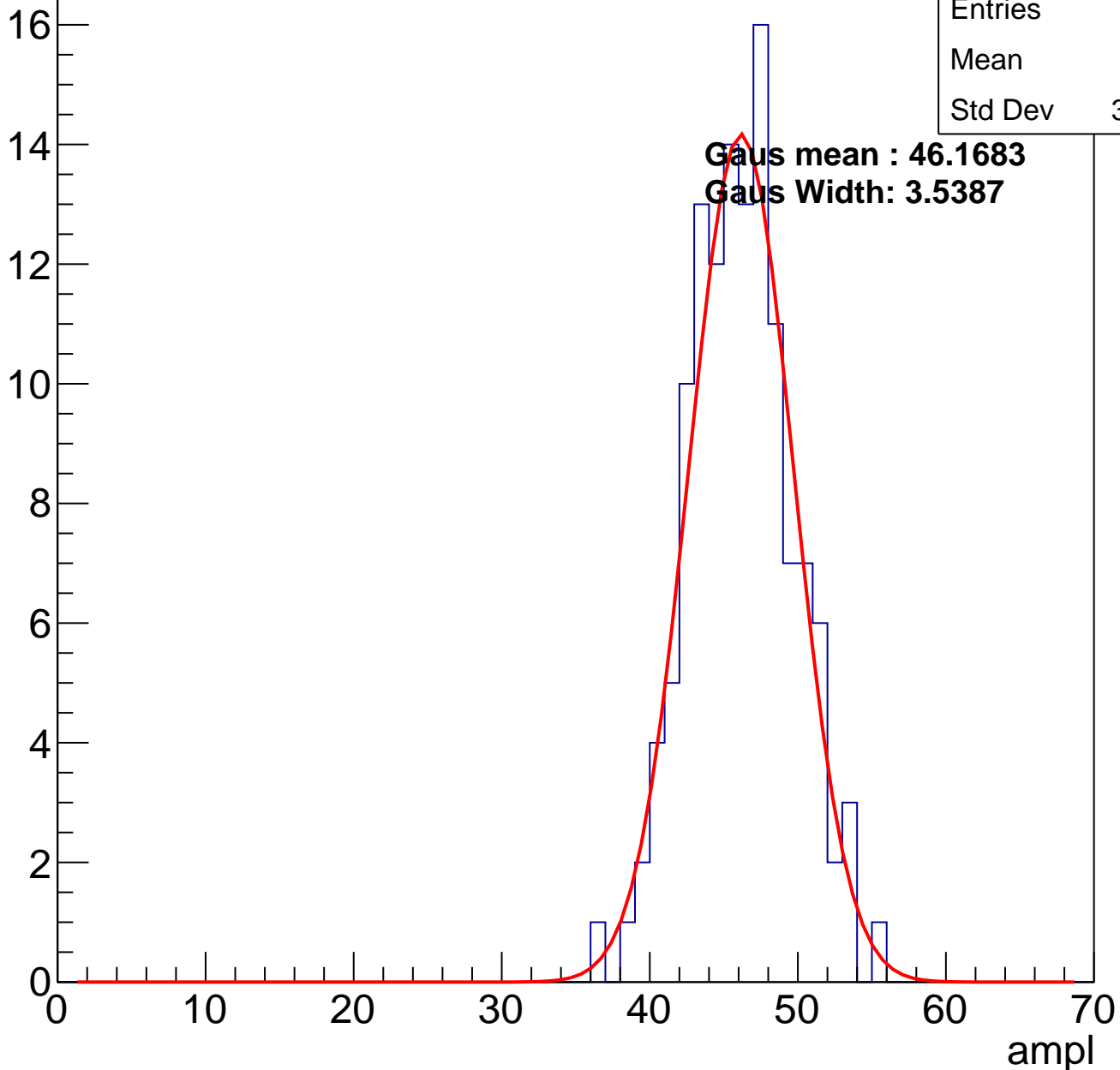
Entry



B0L101S, U24-ch35, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries	145
Mean	52.5
Std Dev	3.426

0

10

20

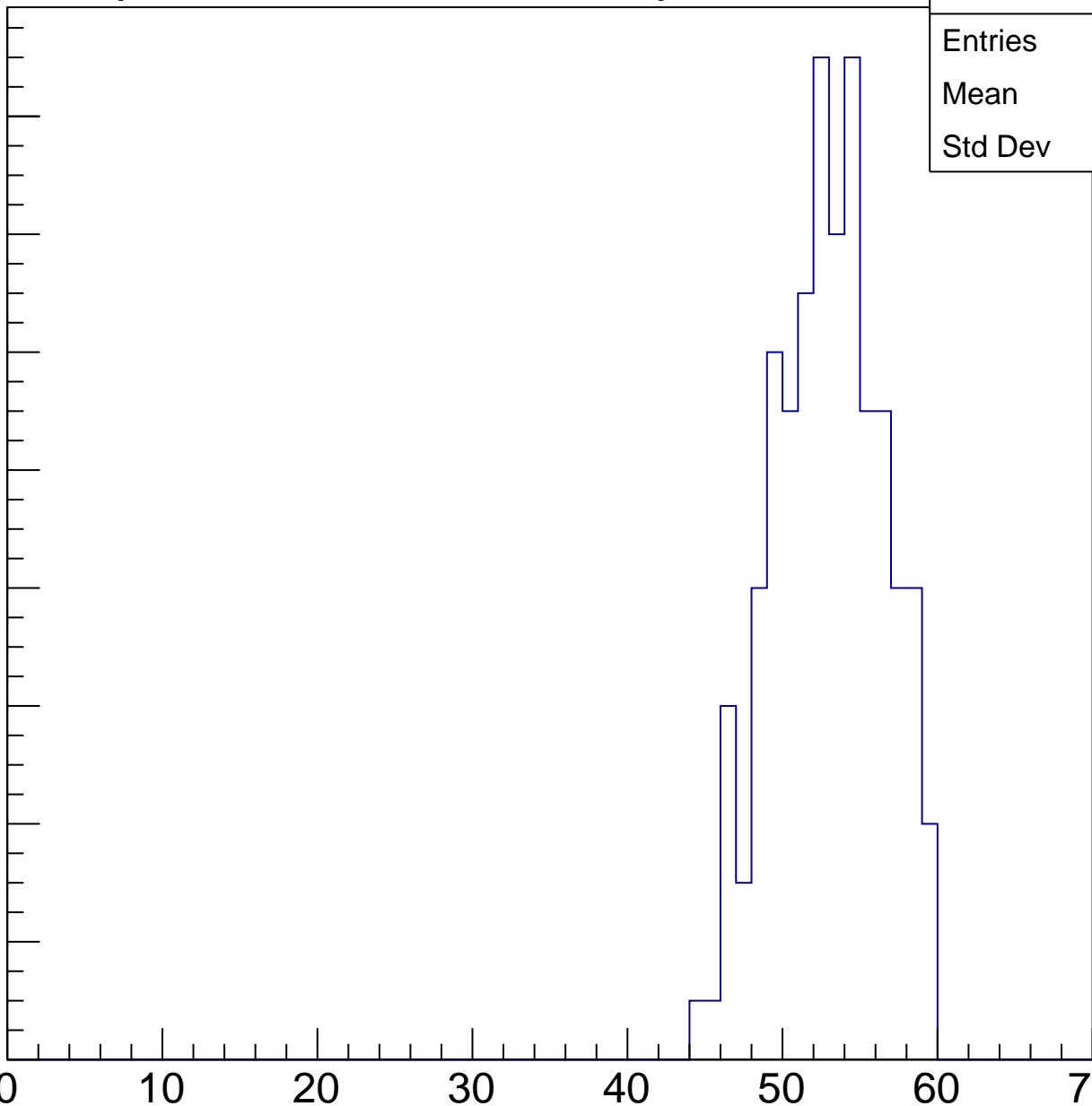
30

40

50

60

ampl



B0L101S, U24-ch35, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

108

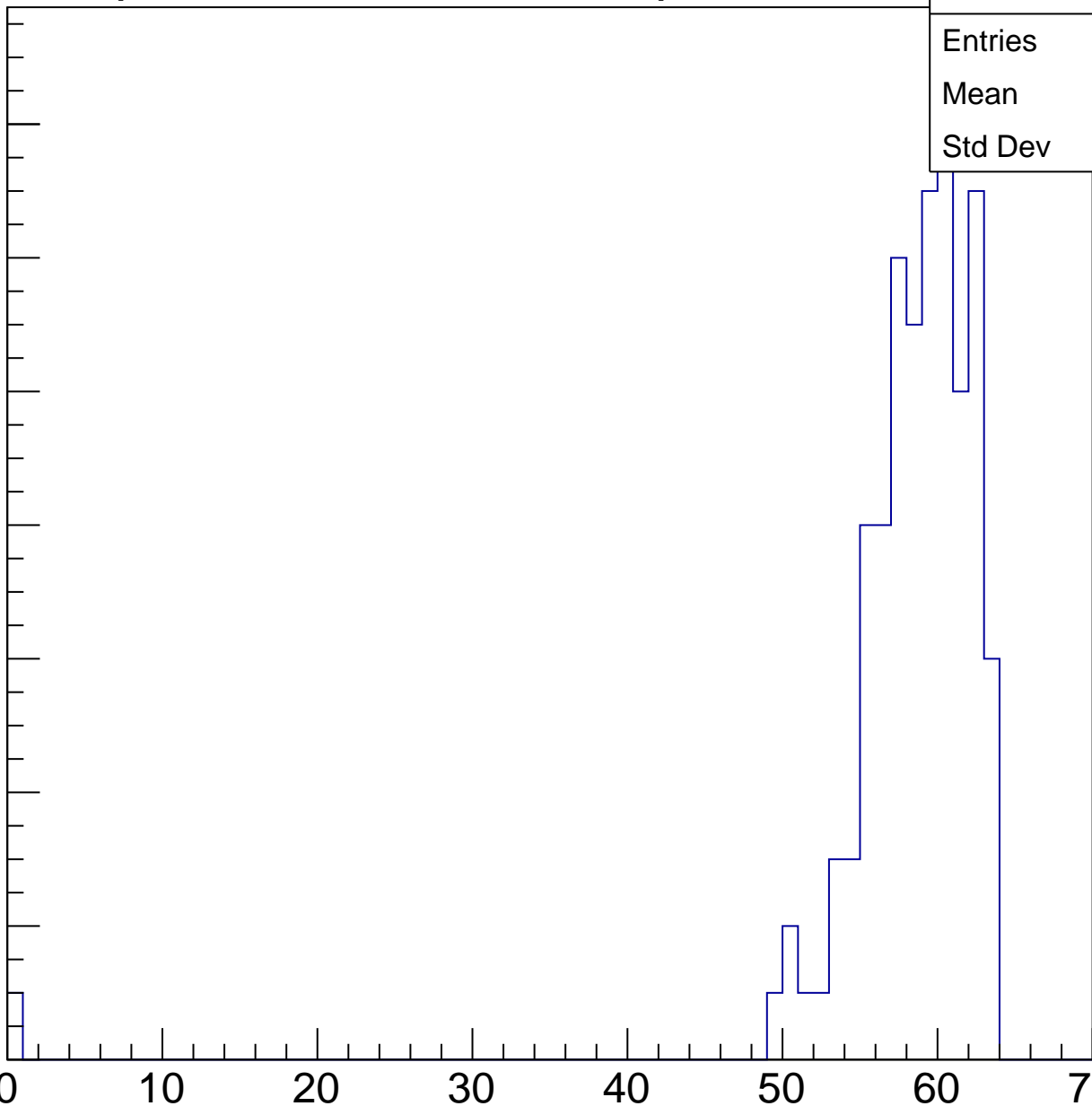
Mean

57.81

Std Dev

6.393

ampl



B0L101S, U24-ch35, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	39
Mean	59.72
Std Dev	9.821

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

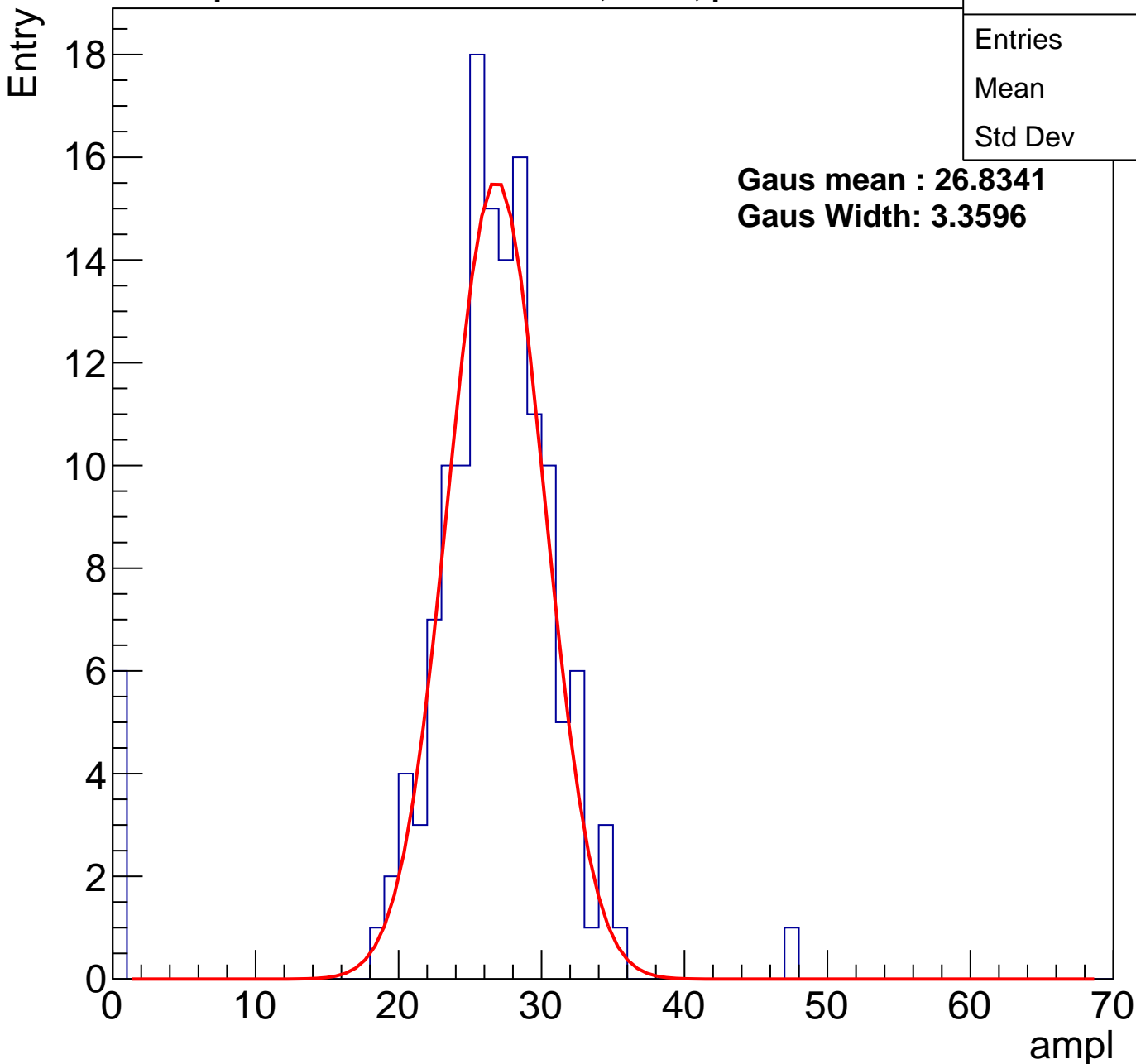
B0L101S, U24-ch36, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	25.5
Std Dev	6.51

Gaus mean : 26.8341

Gaus Width: 3.3596

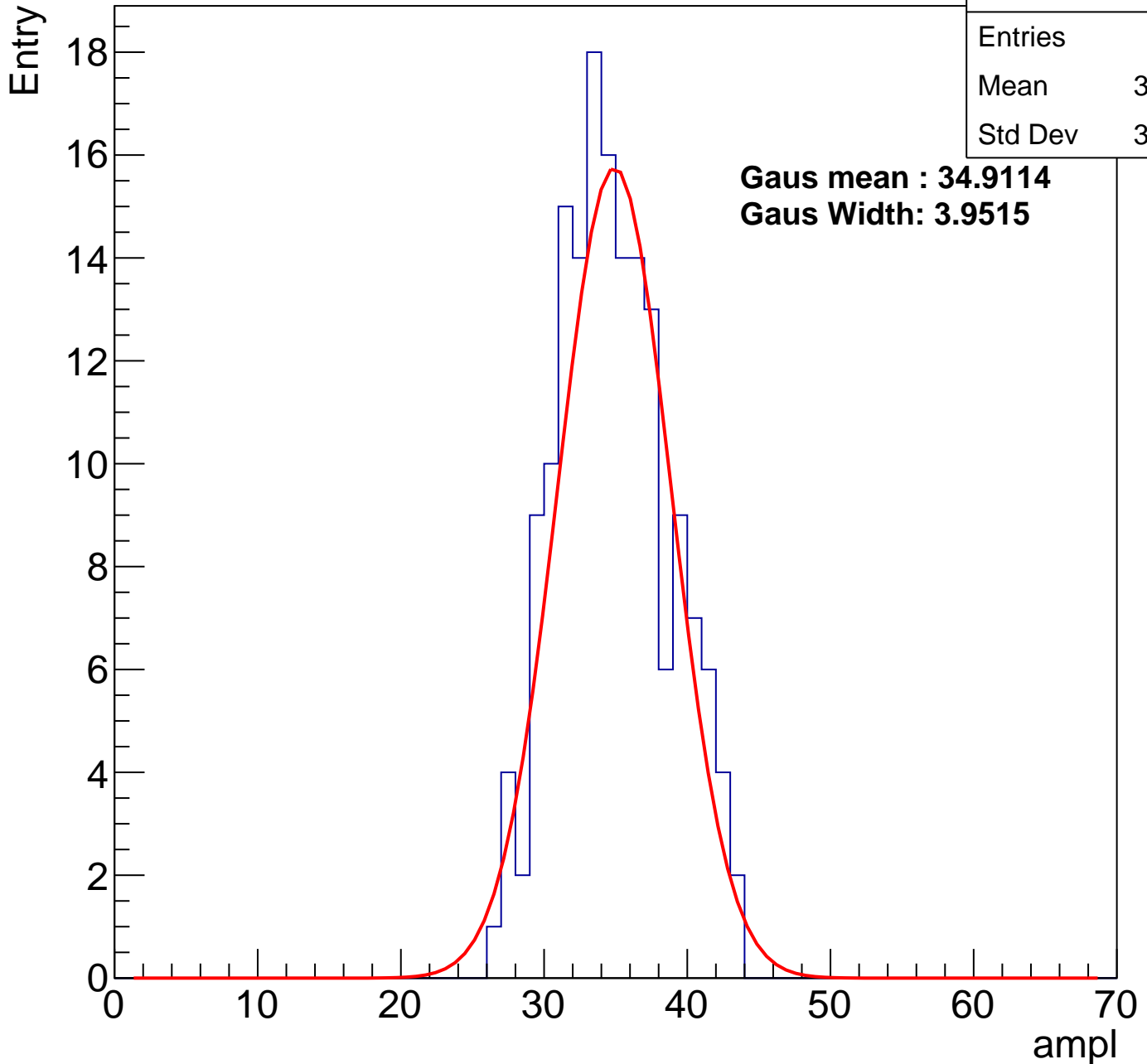


B0L101S, U24-ch36, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	34.37
Std Dev	3.806

Gaus mean : 34.9114
Gaus Width: 3.9515



B0L101S, U24-ch36, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

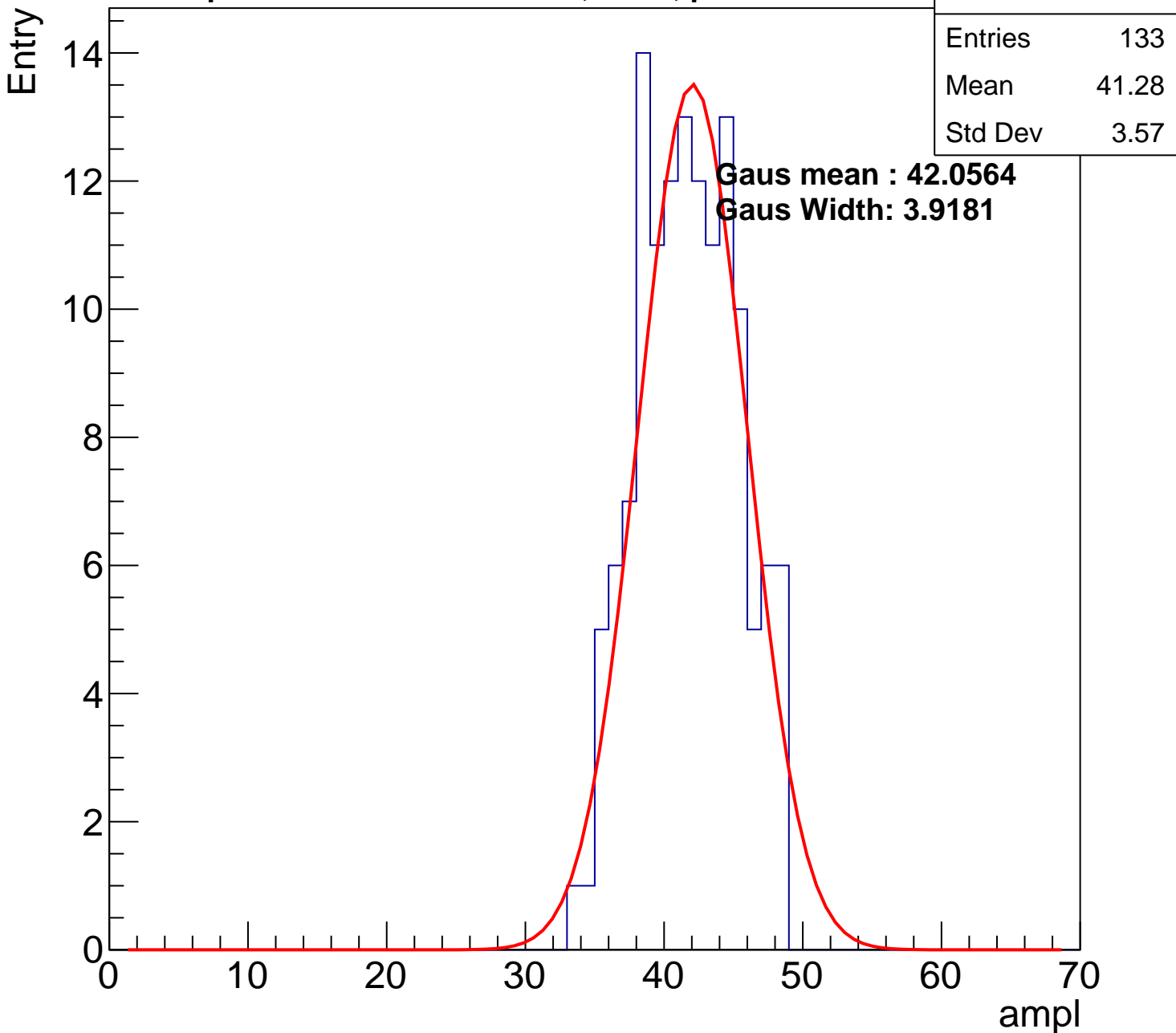
14
12
10
8
6
4
2
0

Entries	133
Mean	41.28
Std Dev	3.57

Gaus mean : 42.0564
Gaus Width: 3.9181

ampl

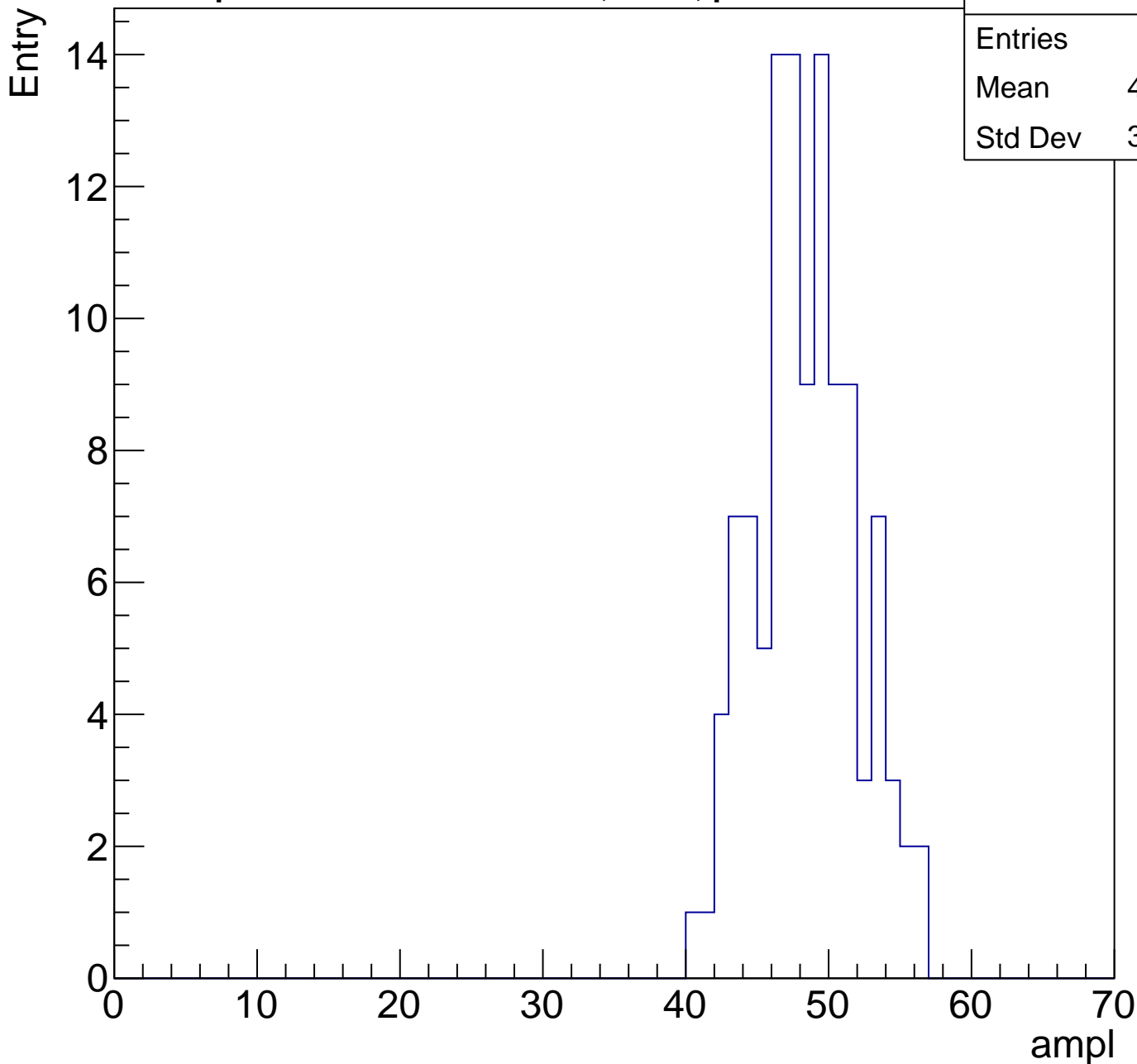
0 10 20 30 40 50 60 70



B0L101S, U24-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	47.95
Std Dev	3.483



B0L101S, U24-ch36, adc4

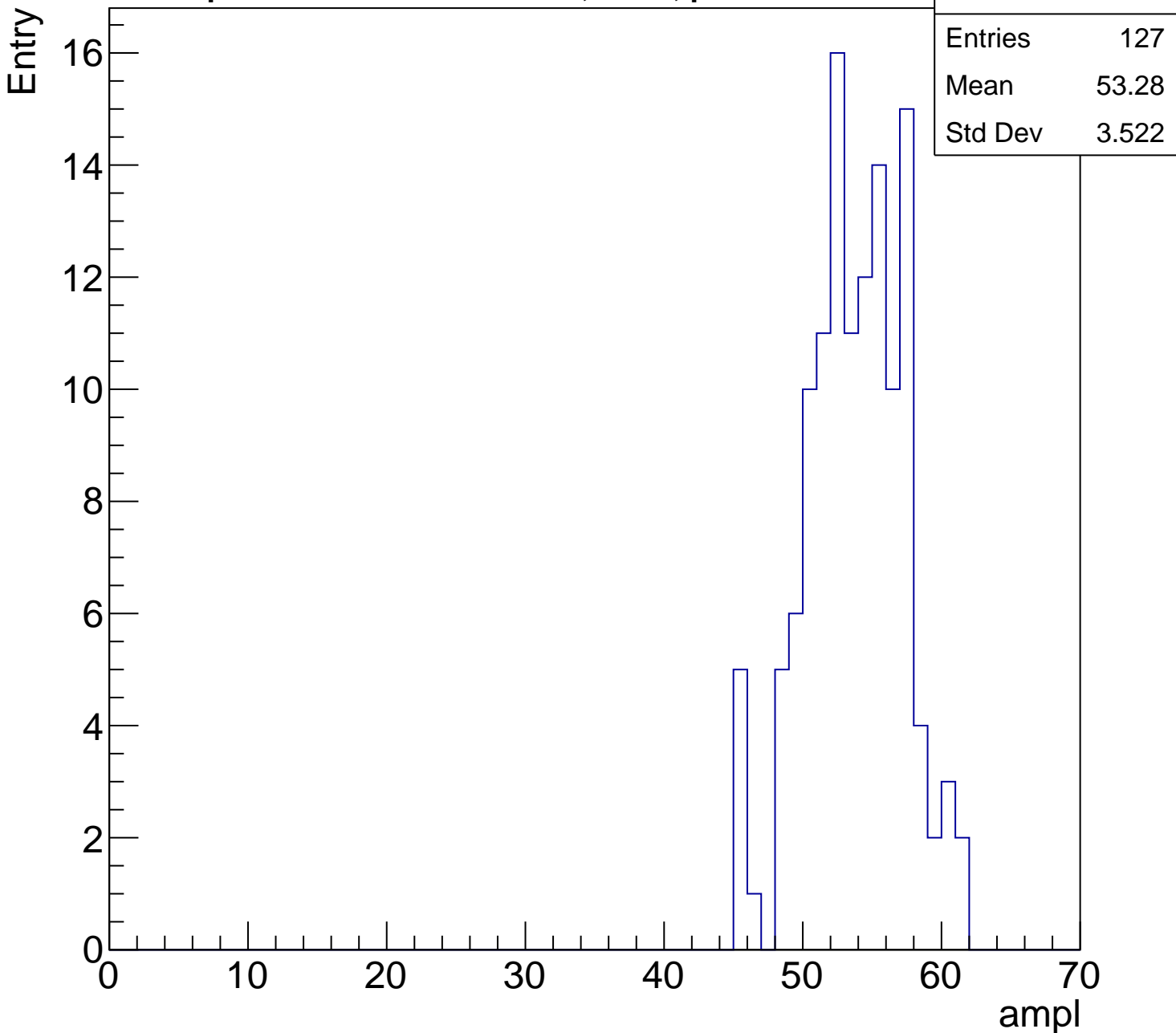
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	53.28
Std Dev	3.522

ampl



B0L101S, U24-ch36, adc5

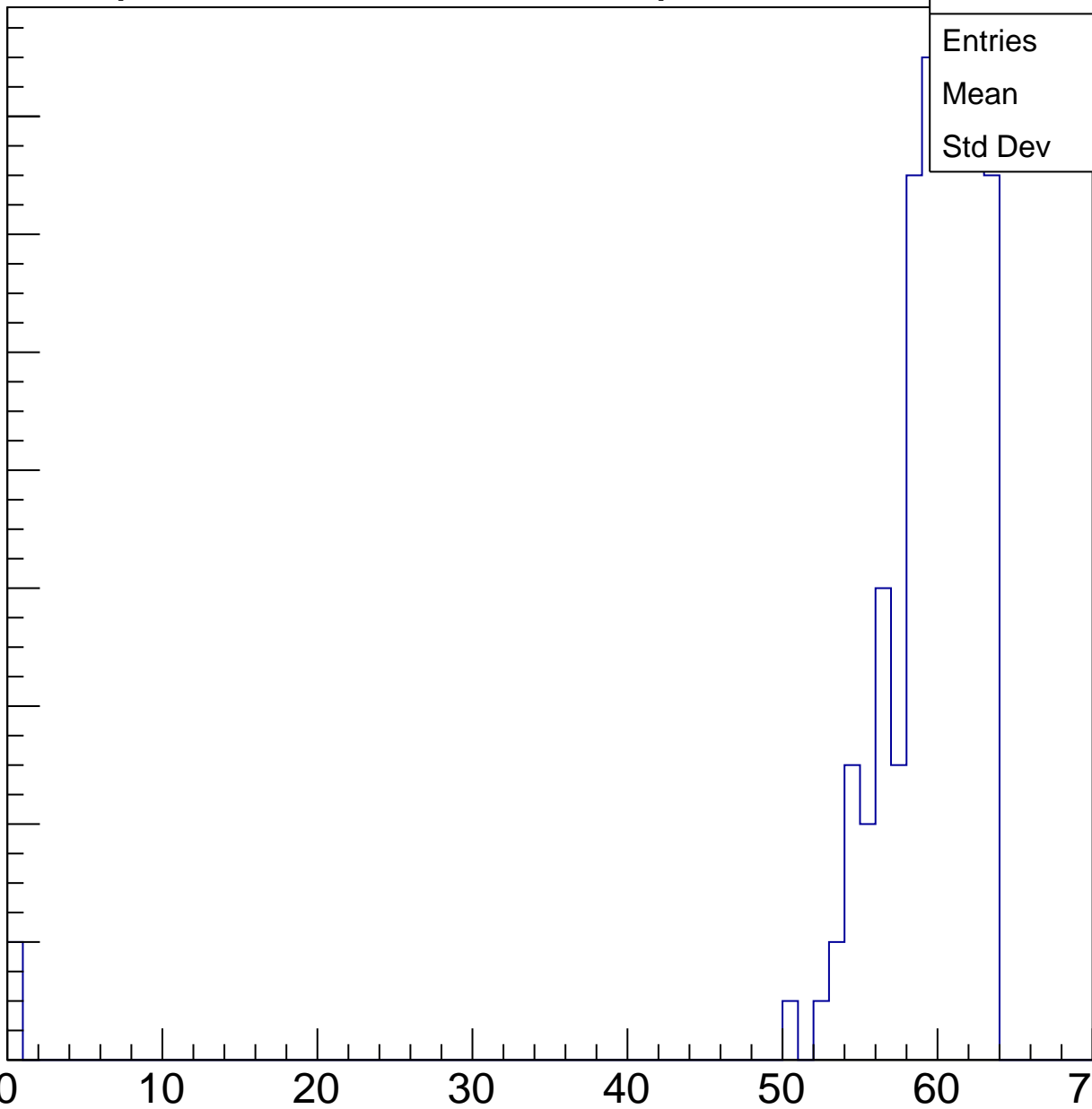
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	58.37
Std Dev	7.971

ampl

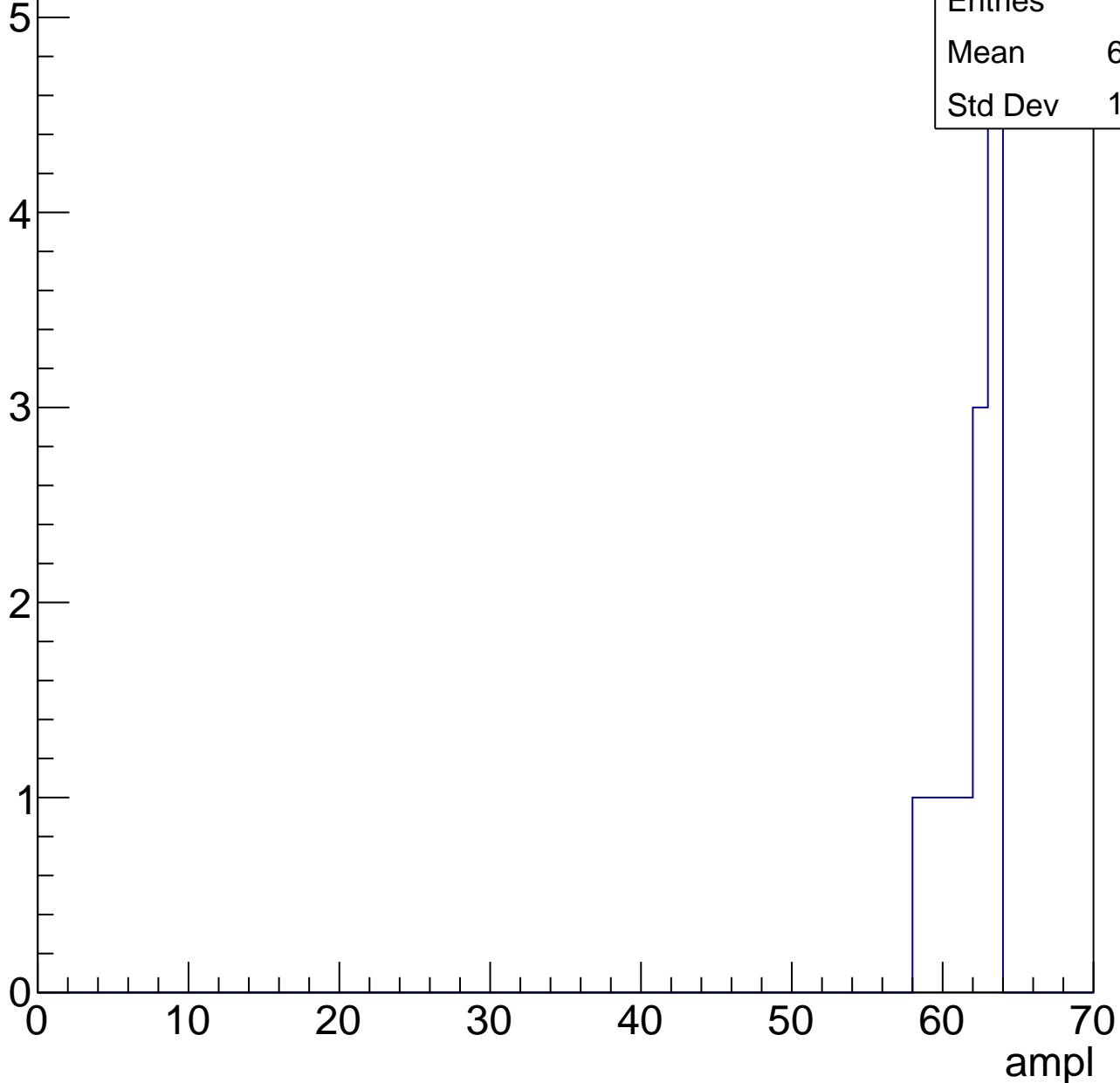


B0L101S, U24-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.58
Std Dev	1.656



B0L101S, U24-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch37, adc0

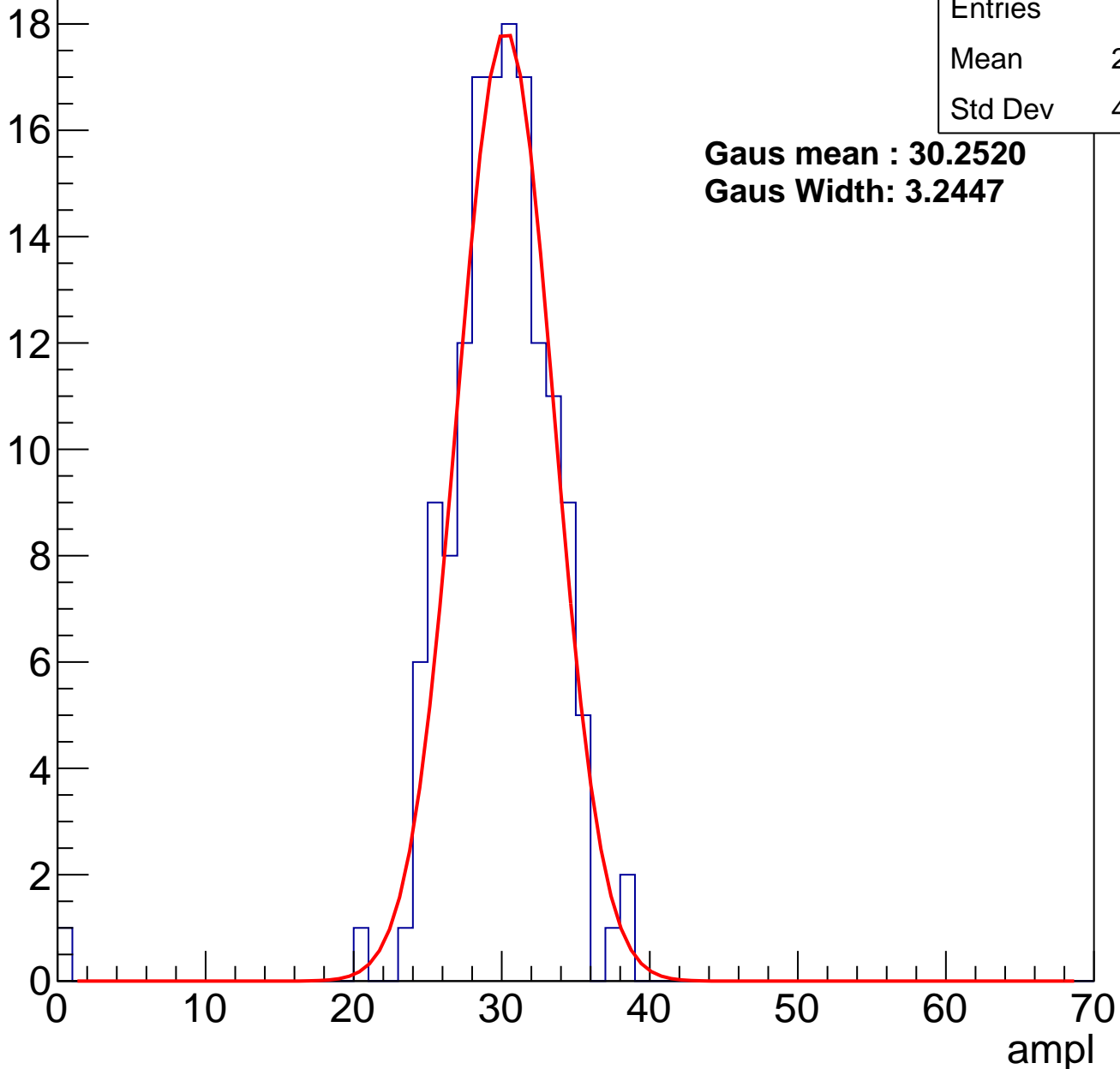
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.39
Std Dev	4.013

Gaus mean : 30.2520

Gaus Width: 3.2447

Entry



B0L101S, U24-ch37, adc1

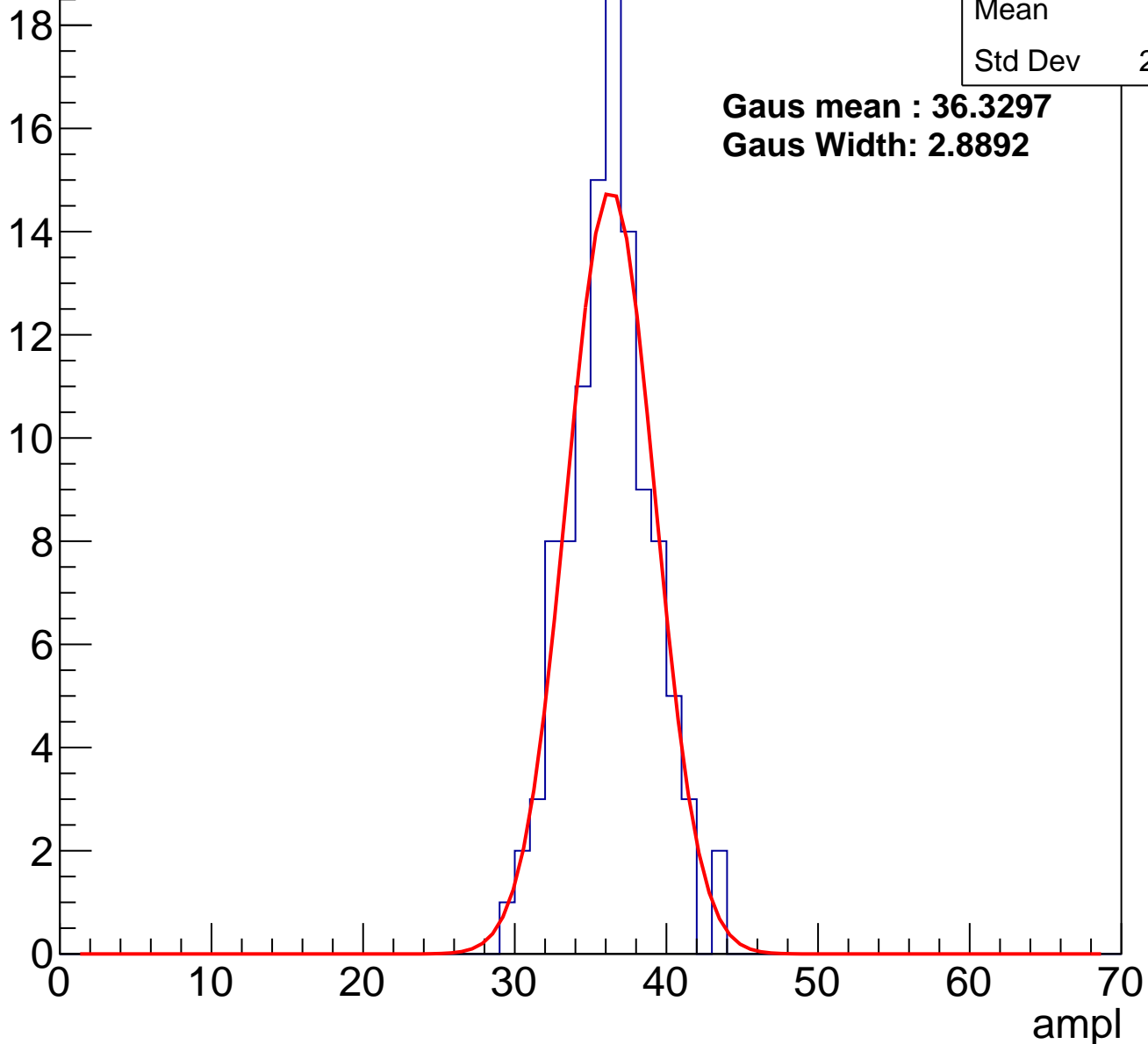
calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	35.8
Std Dev	2.765

Gaus mean : 36.3297

Gaus Width: 2.8892

Entry



B0L101S, U24-ch37, adc2

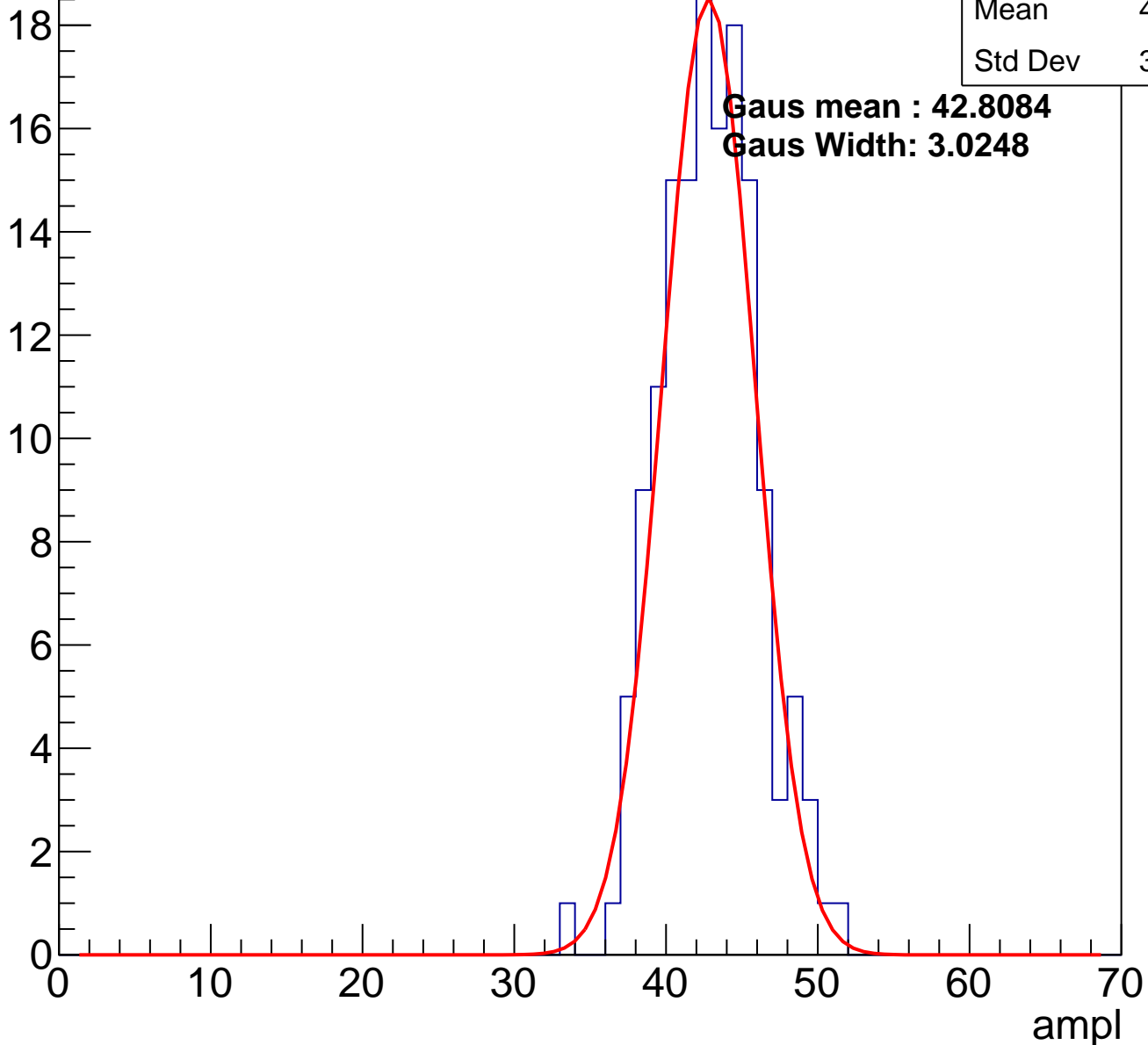
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	42.42
Std Dev	3.138

Gaus mean : 42.8084

Gaus Width: 3.0248

Entry



B0L101S, U24-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

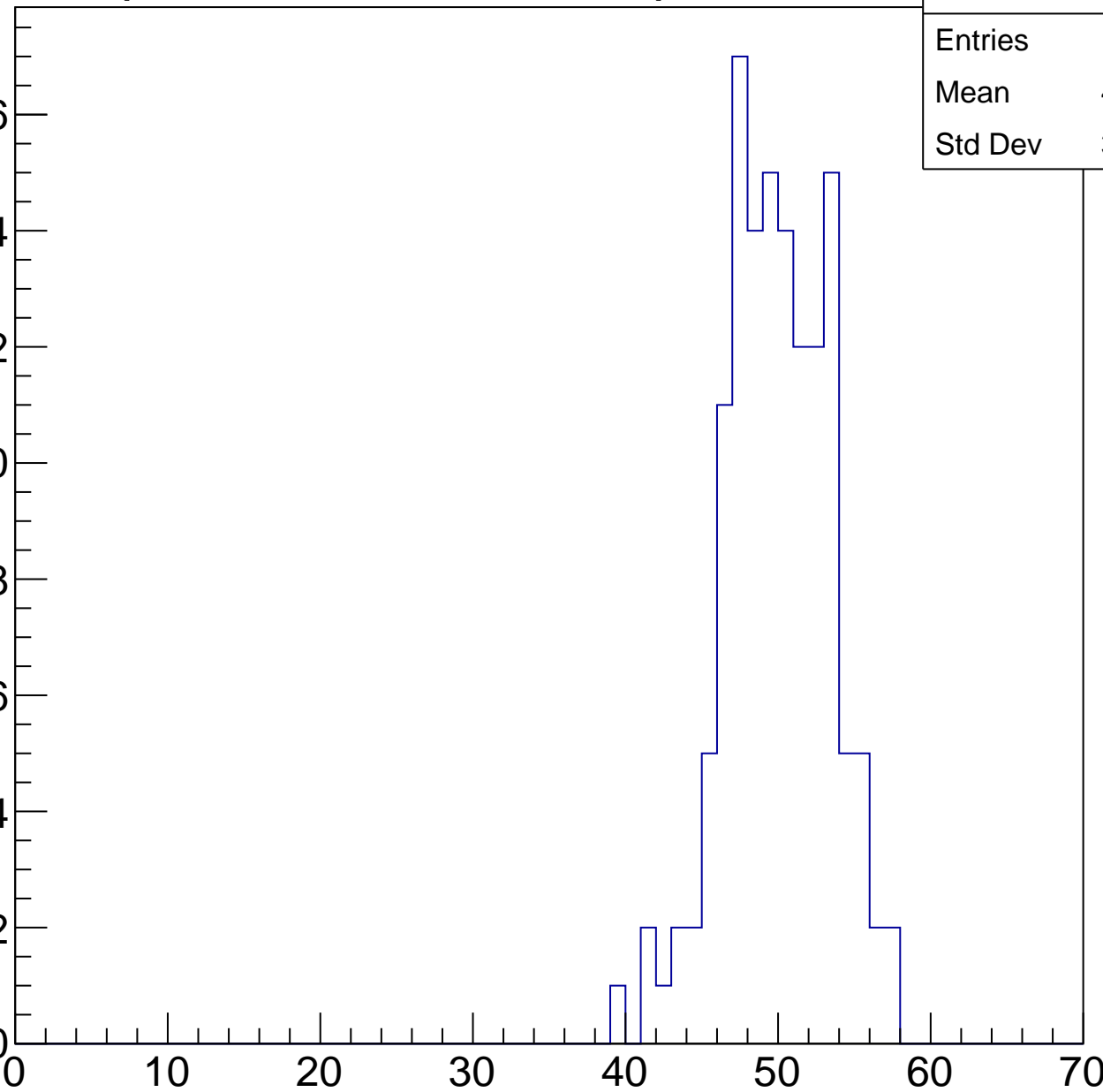
Mean

49.46

Std Dev

3.387

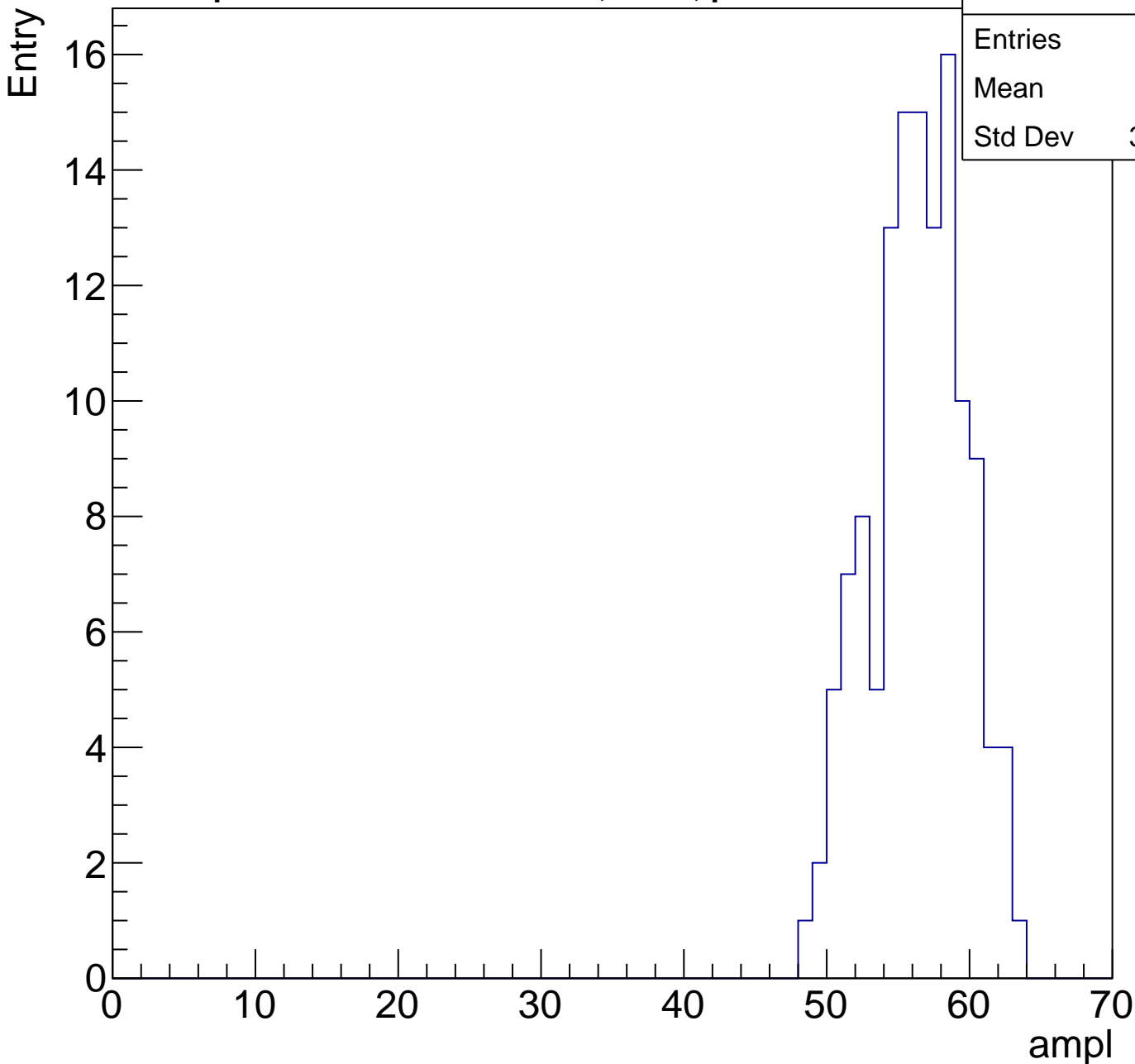
ampl



B0L101S, U24-ch37, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	55.9
Std Dev	3.259



B0L101S, U24-ch37, adc5

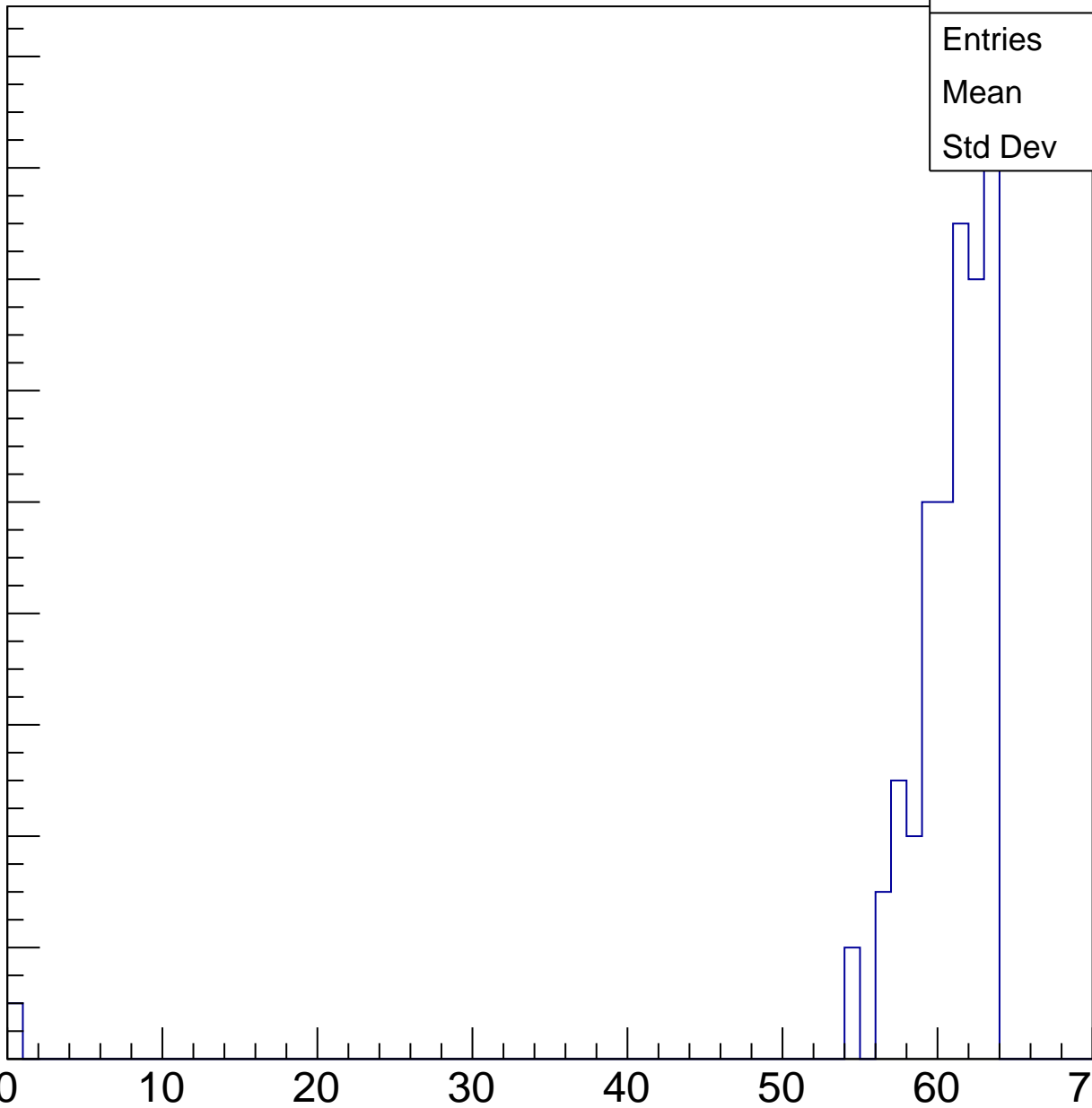
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	82
Mean	59.76
Std Dev	7.001

ampl



B0L101S, U24-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

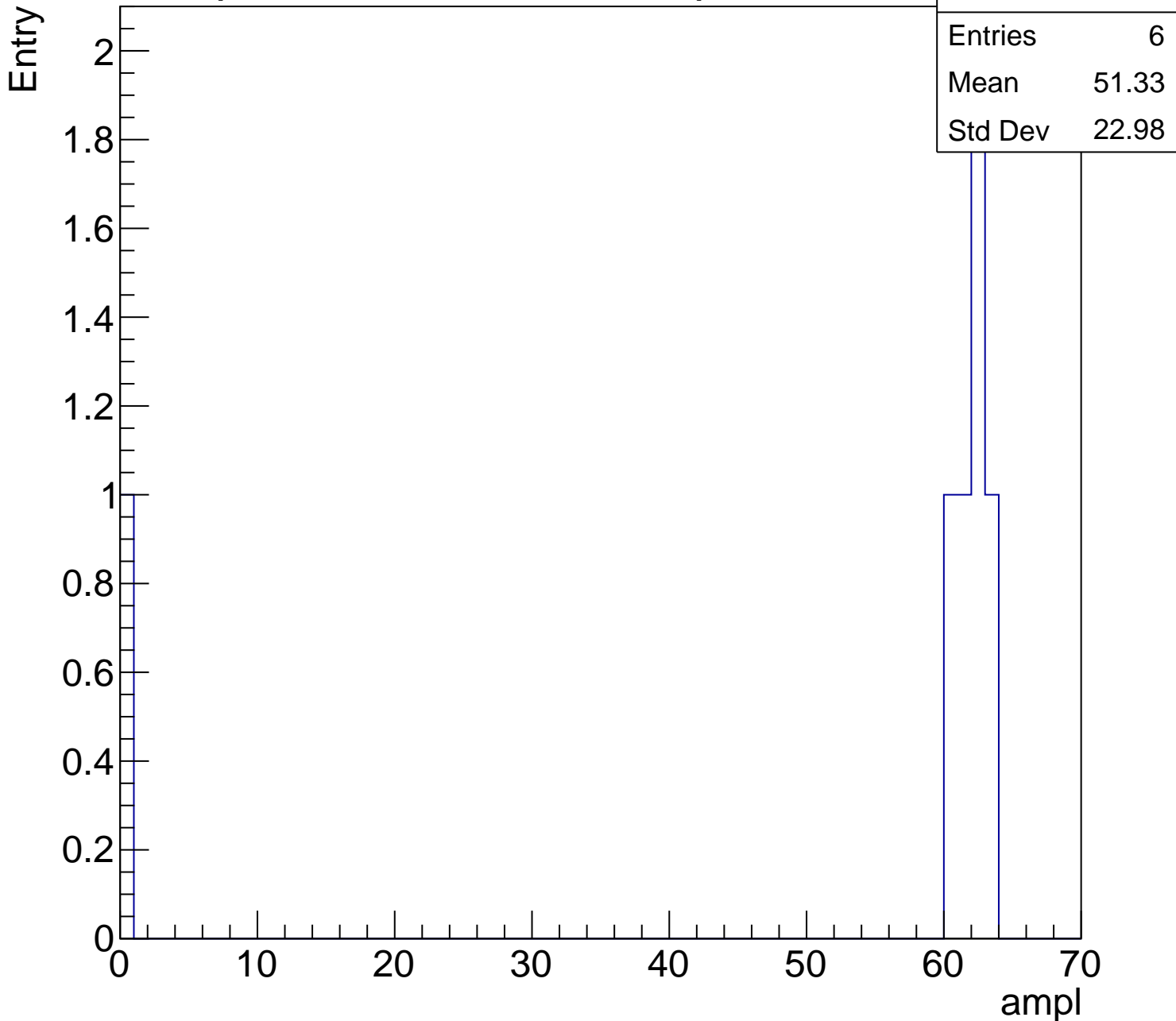
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.33
Std Dev	22.98

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

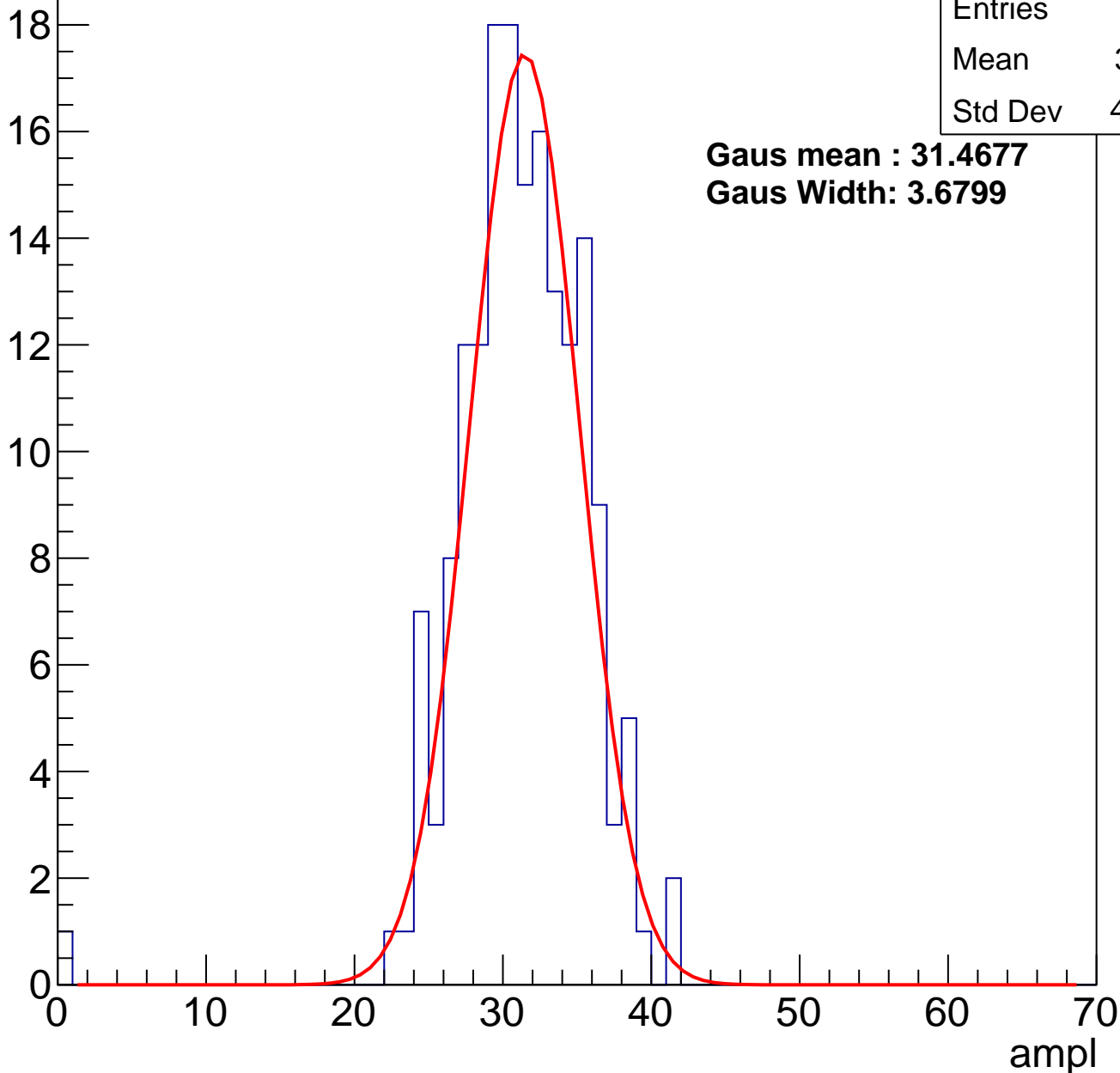


Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch38, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

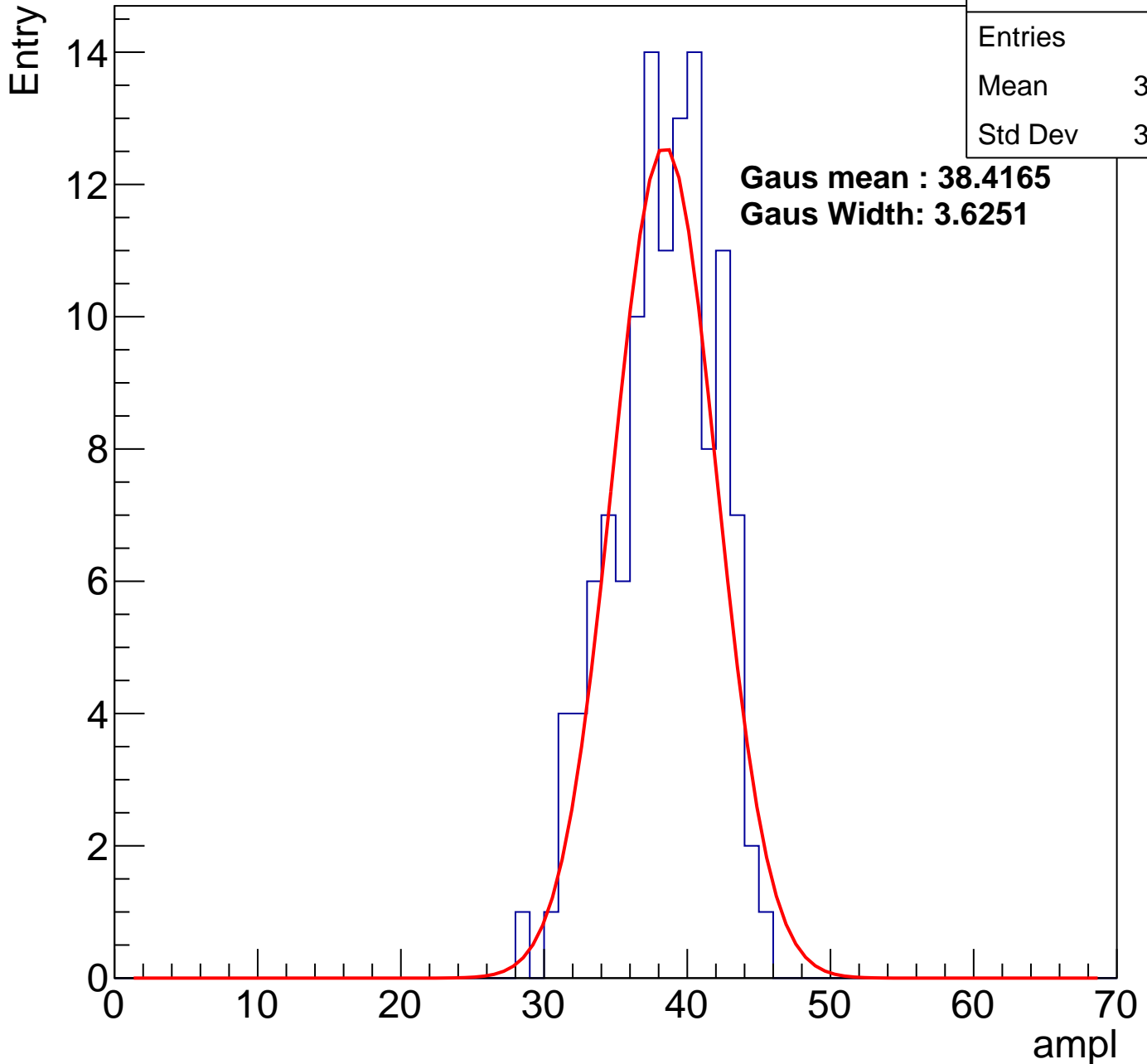


B0L101S, U24-ch38, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	37.86
Std Dev	3.517

Gaus mean : 38.4165
Gaus Width: 3.6251



B0L101S, U24-ch38, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

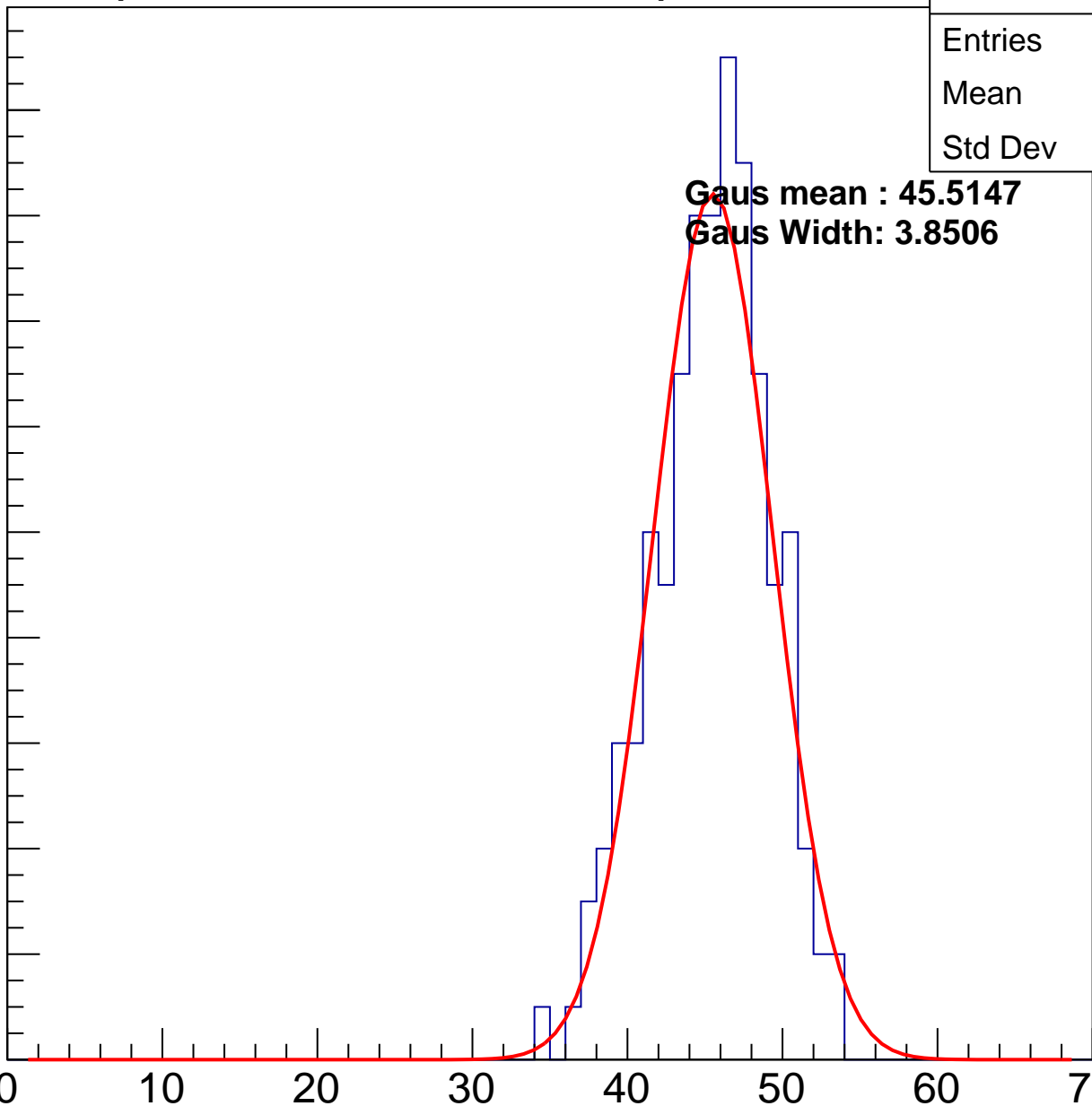
18
16
14
12
10
8
6
4
2
0

Entries	161
Mean	44.91
Std Dev	3.723

Gaus mean : 45.5147

Gaus Width: 3.8506

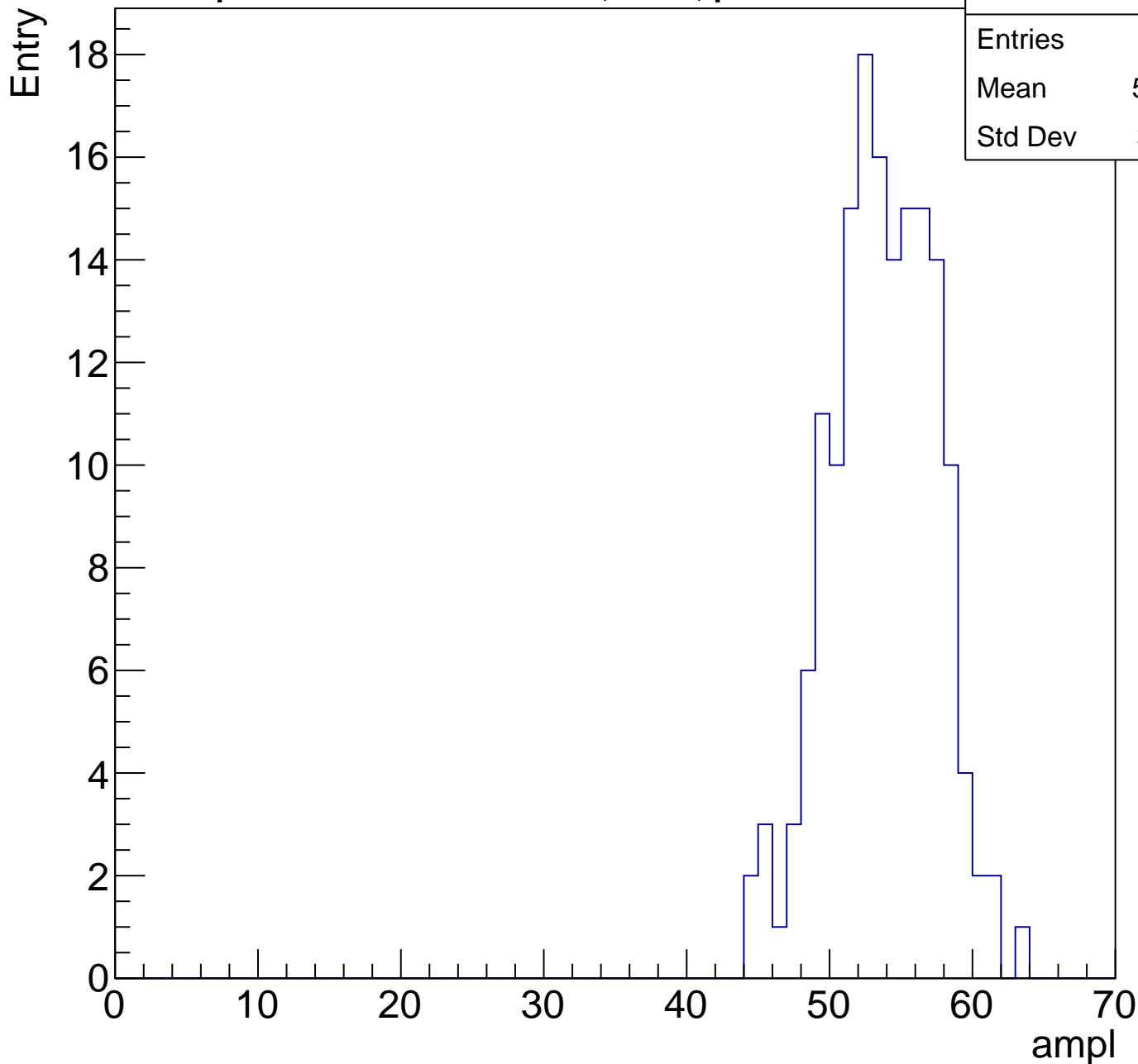
ampl



B0L101S, U24-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	53.25
Std Dev	3.641



B0L101S, U24-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

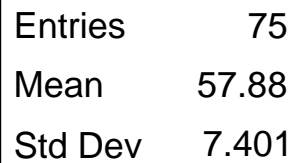
50

60

70

ampl

Entries	75
Mean	57.88
Std Dev	7.401



B0L101S, U24-ch38, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

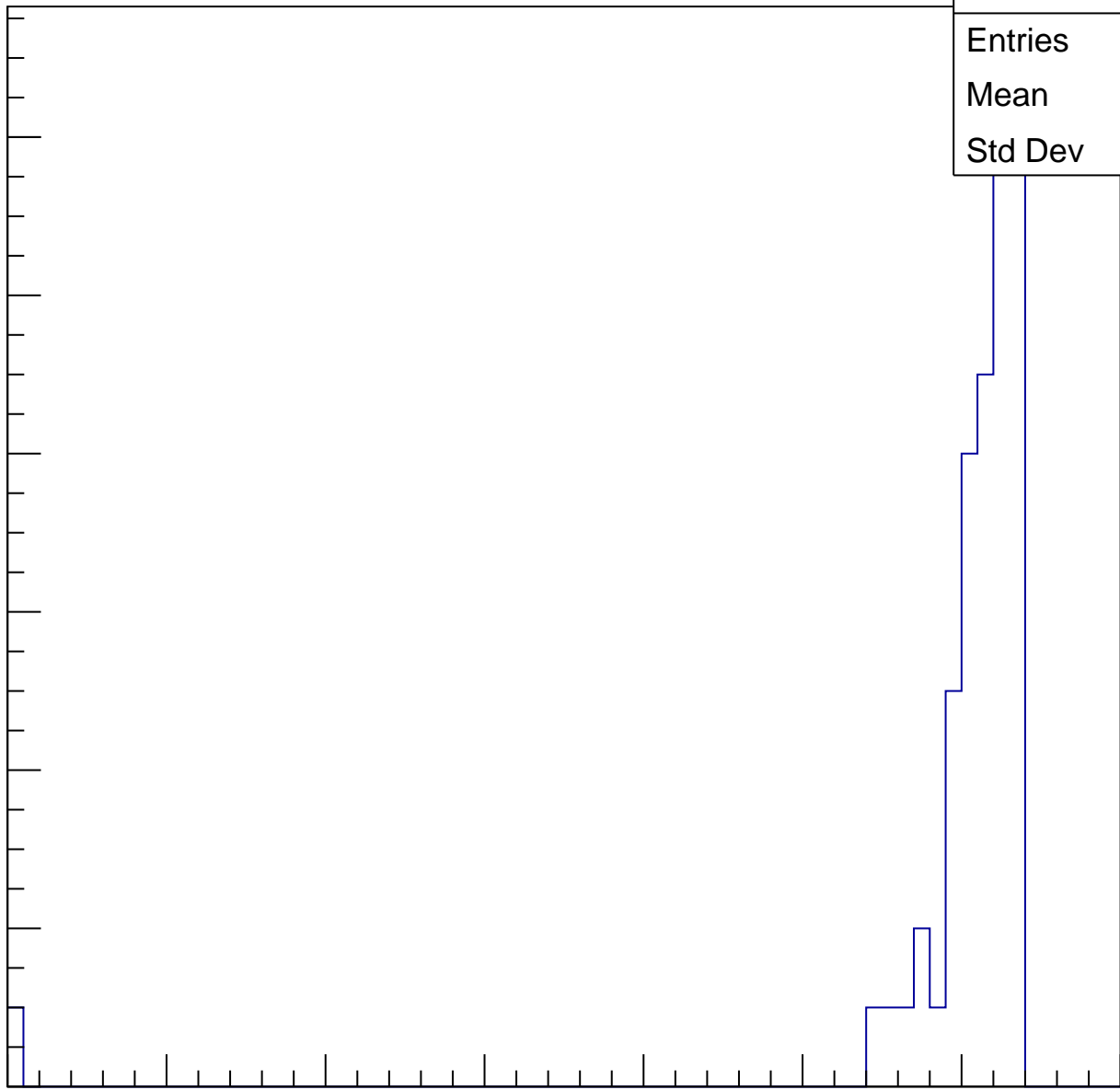
2

0

Entries	54
Mean	59.7
Std Dev	8.469

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch39, adc0

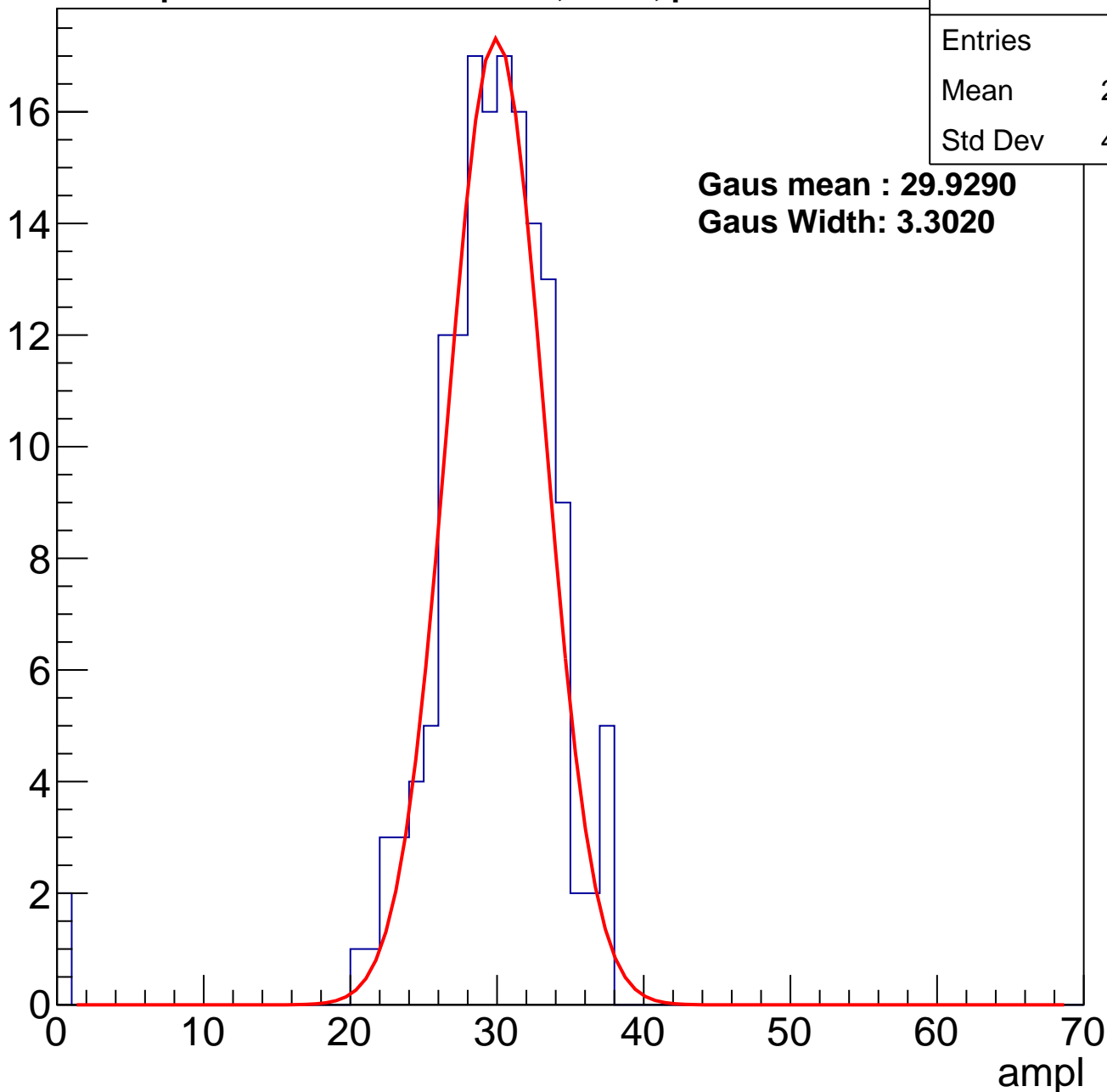
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	29.15
Std Dev	4.809

Gaus mean : 29.9290

Gaus Width: 3.3020

Entry



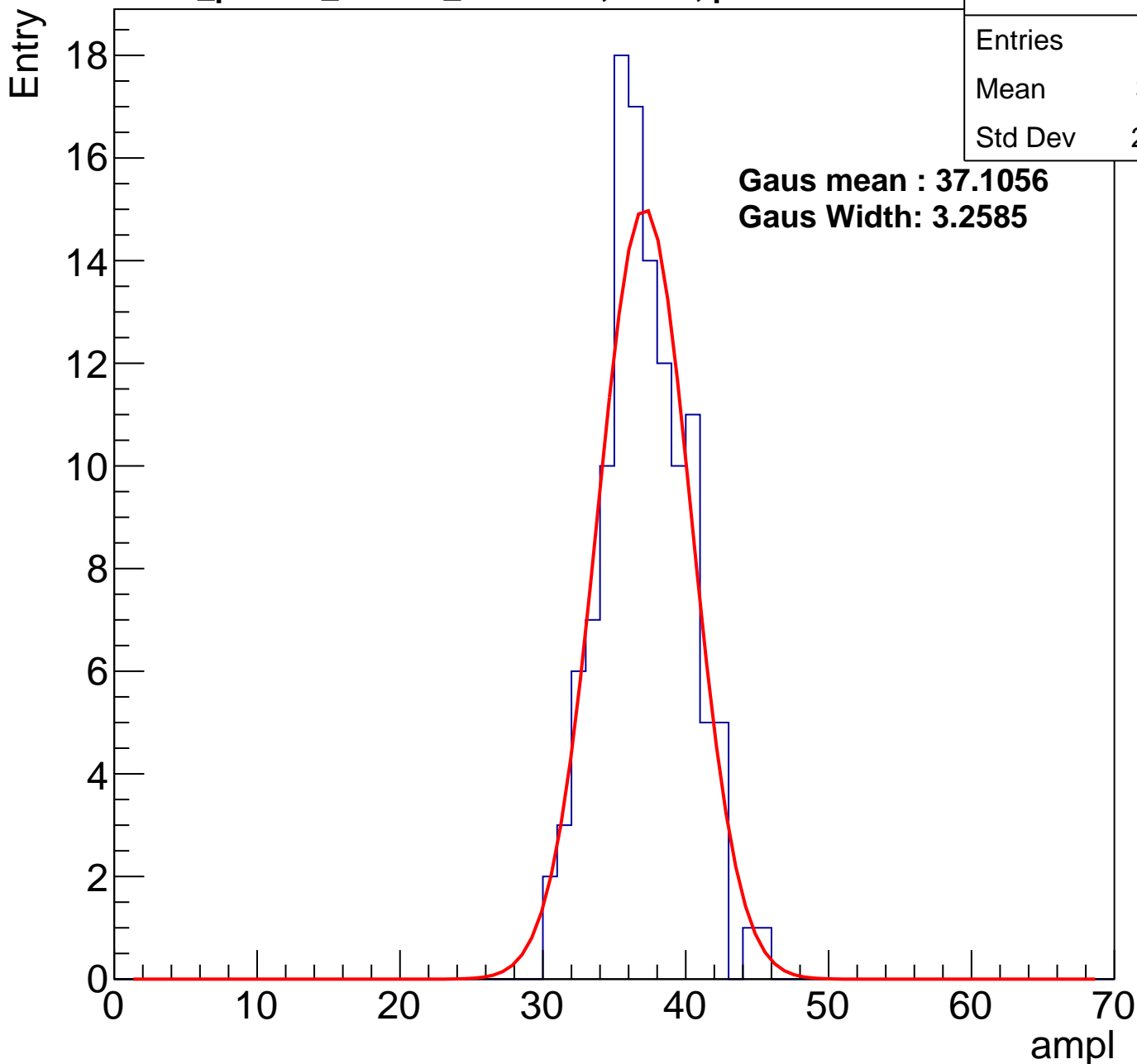
B0L101S, U24-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	36.61
Std Dev	2.999

Gaus mean : 37.1056

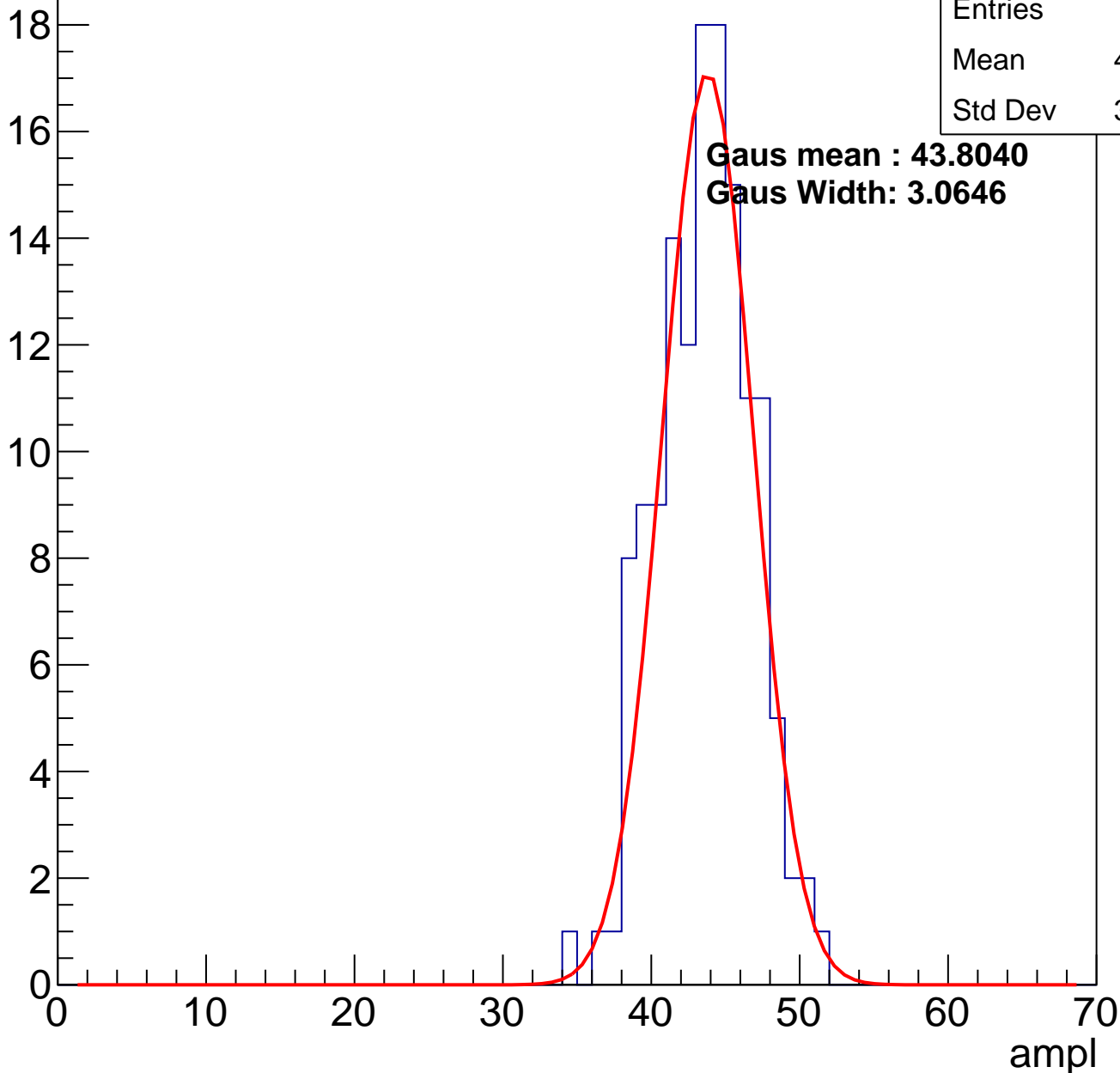
Gaus Width: 3.2585



B0L101S, U24-ch39, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

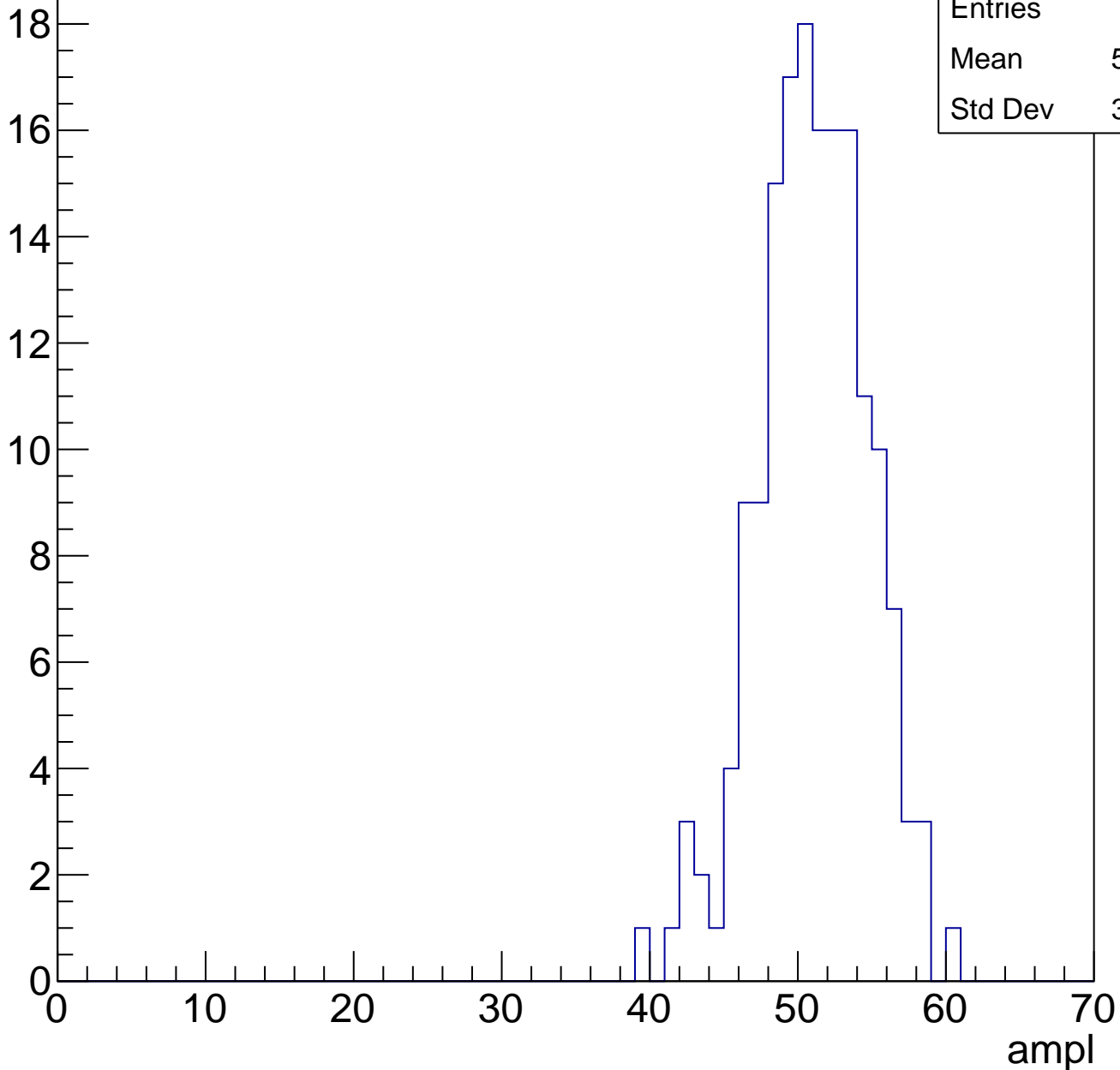


B0L101S, U24-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	50.57
Std Dev	3.689

Entry



B0L101S, U24-ch39, adc4

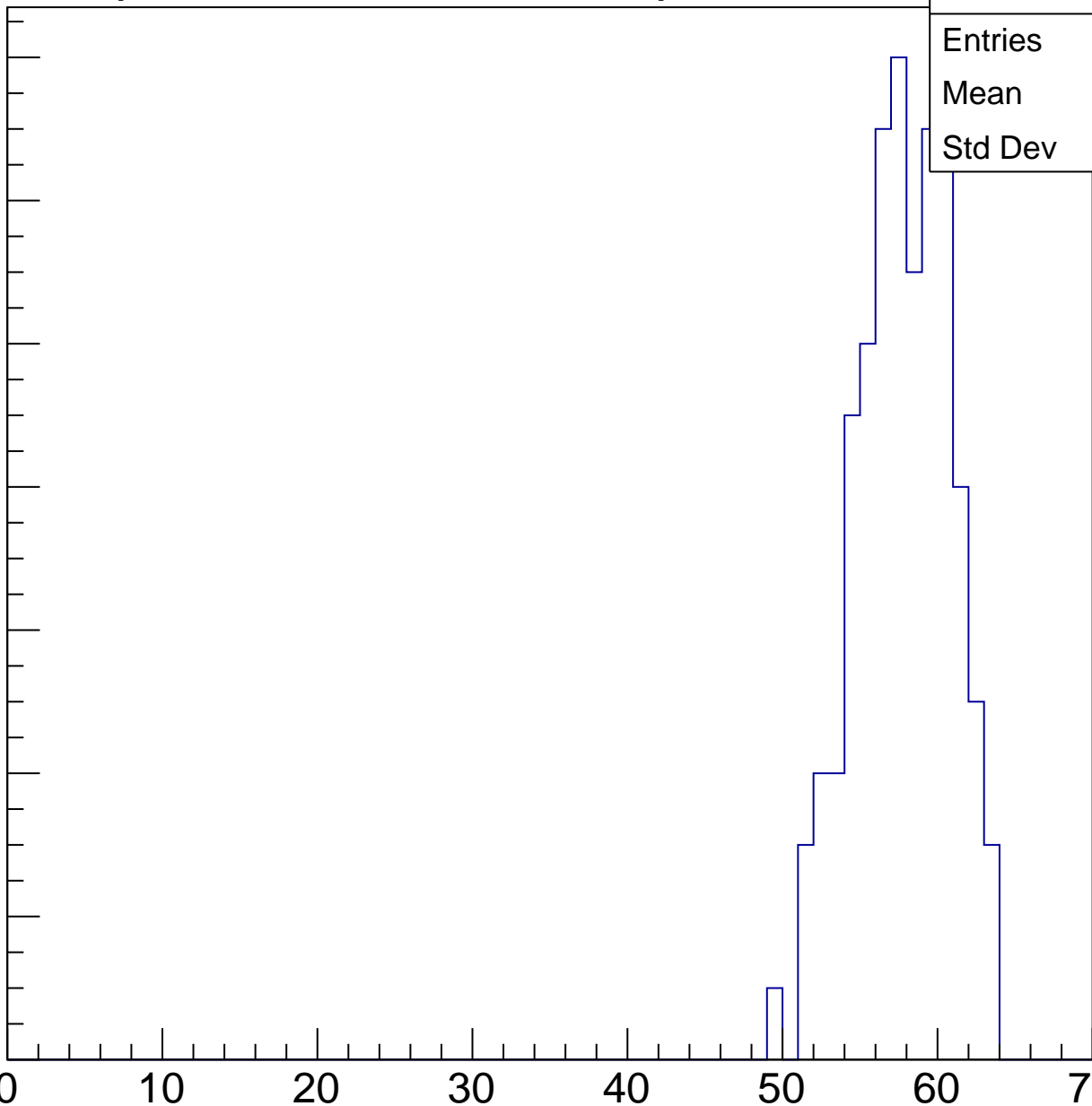
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	57.26
Std Dev	3.007

ampl



B0L101S, U24-ch39, adc5

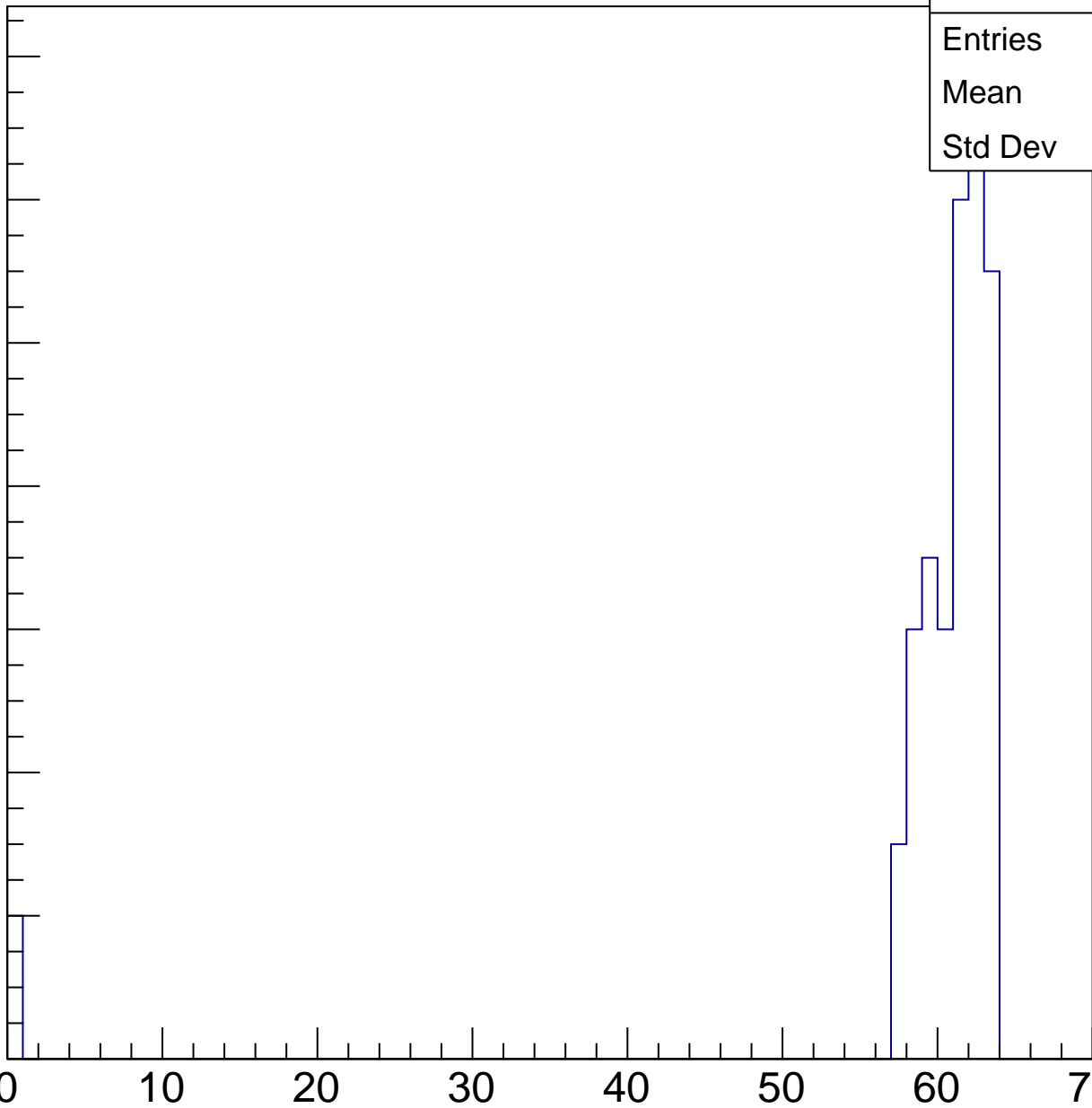
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	58.77
Std Dev	10.96

ampl

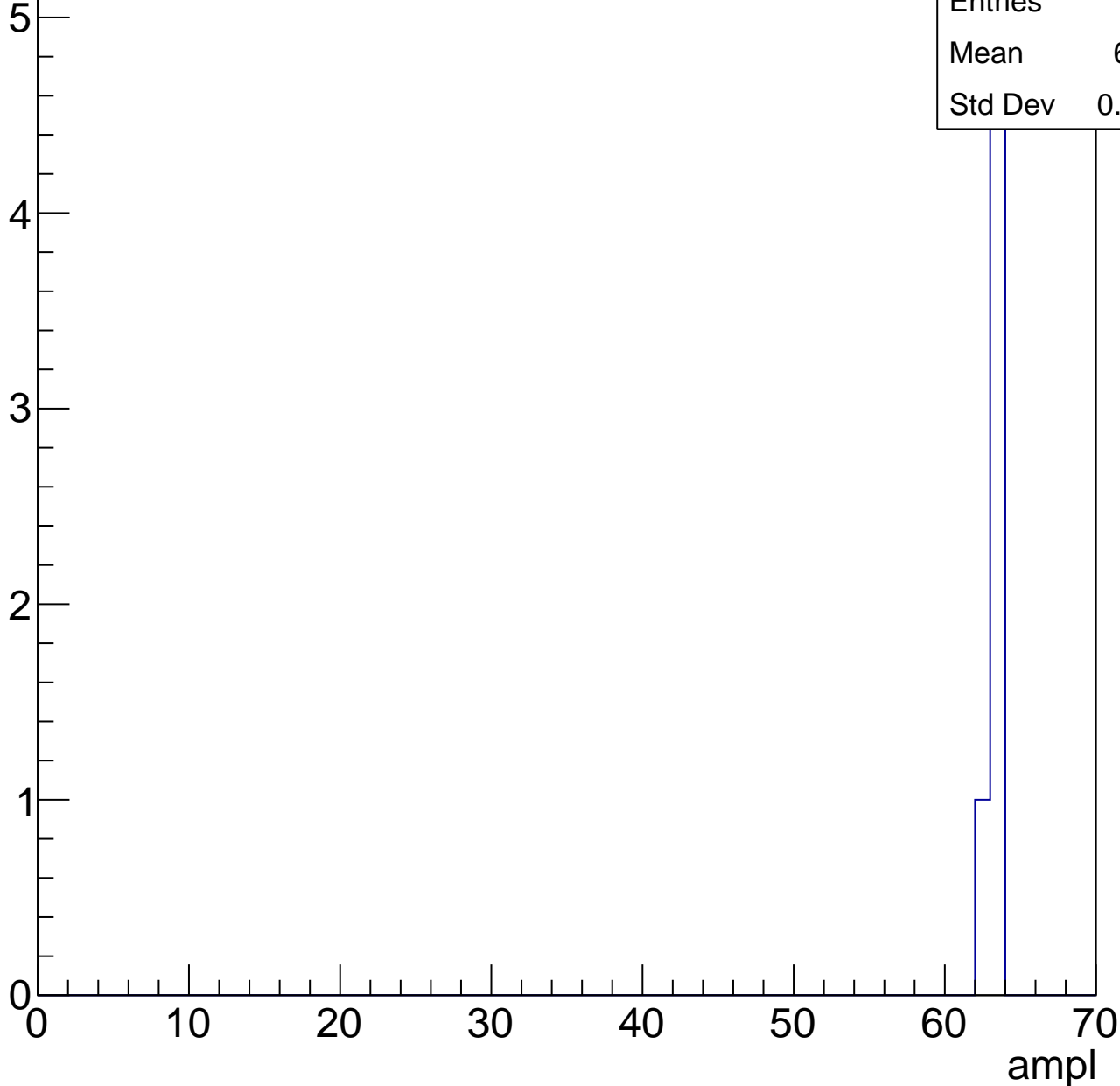


B0L101S, U24-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B0L101S, U24-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

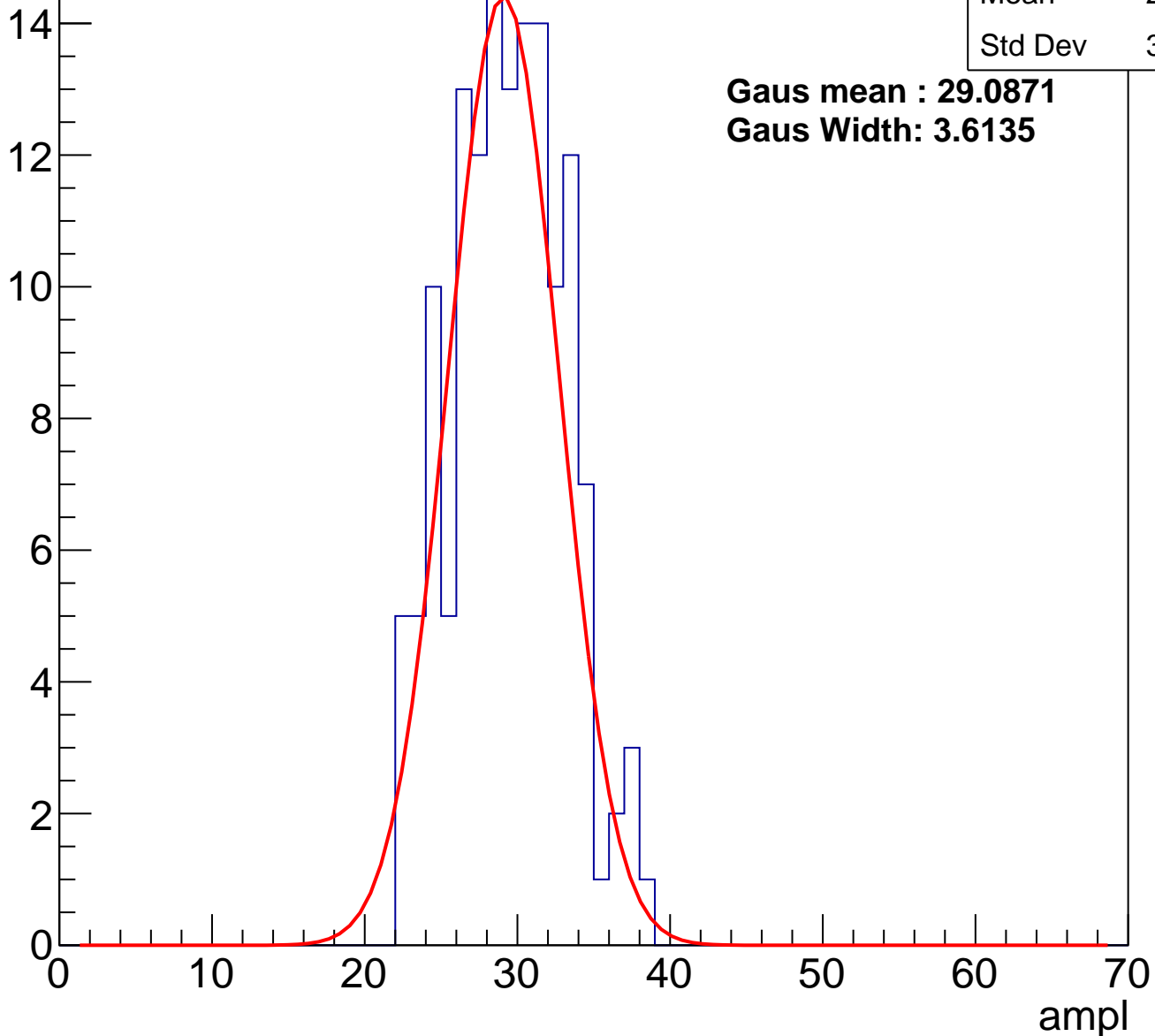
Entry



B0L101S, U24-ch40, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	142
Mean	28.96
Std Dev	3.628

Gaus mean : 29.0871

Gaus Width: 3.6135

B0L101S, U24-ch40, adc1

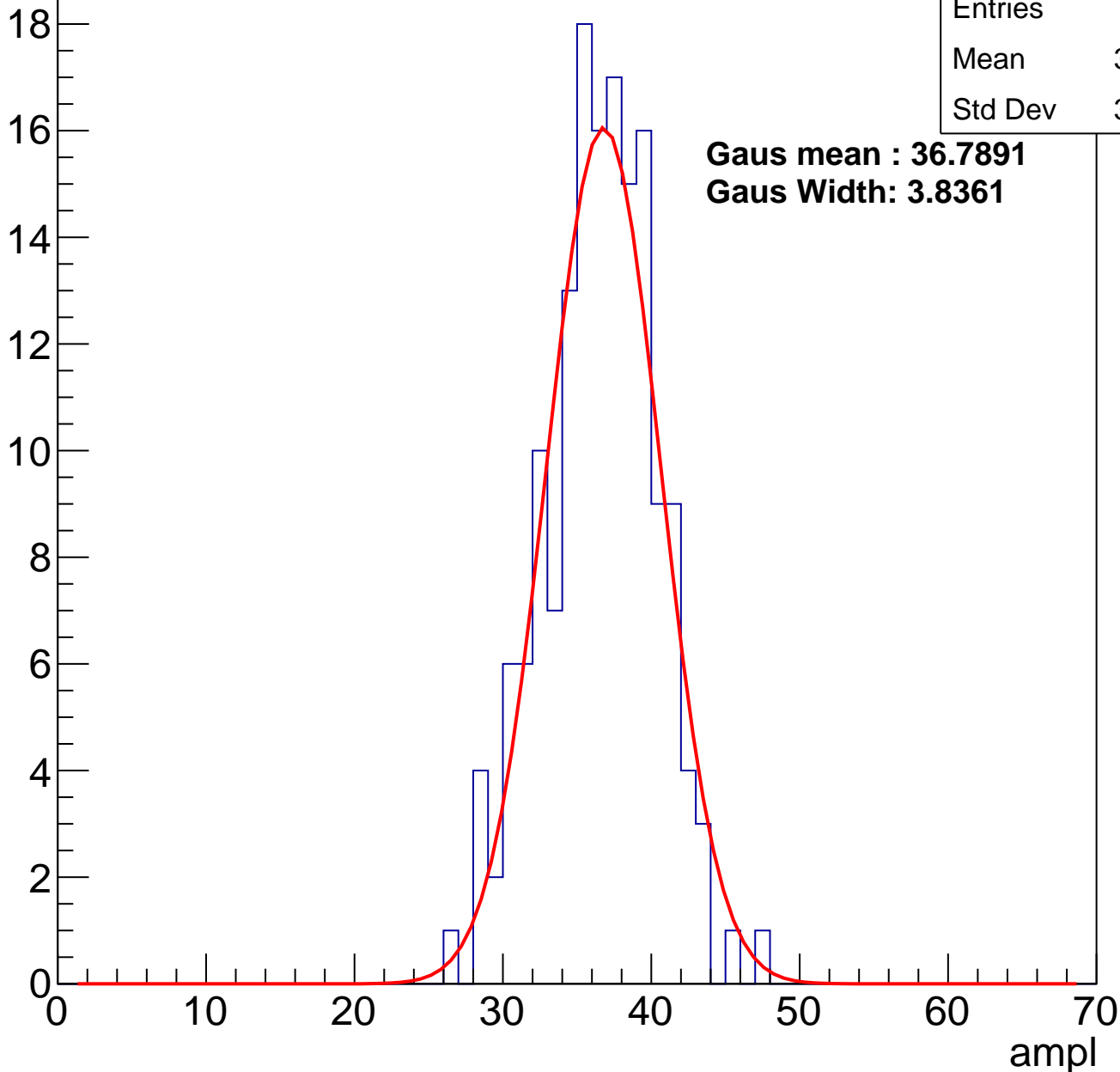
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	36.09
Std Dev	3.727

Gaus mean : 36.7891

Gaus Width: 3.8361

Entry



B0L101S, U24-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

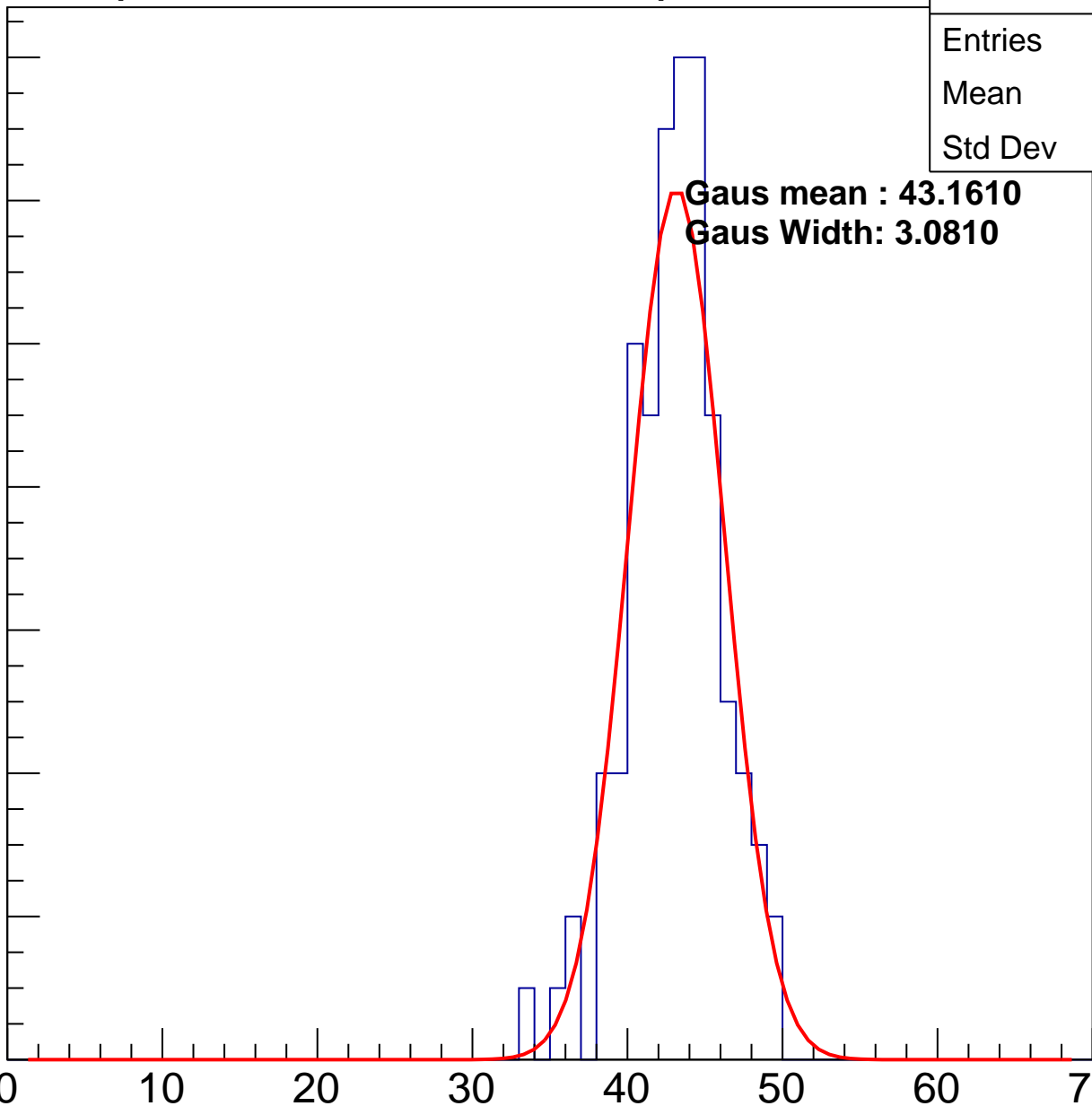
14
12
10
8
6
4
2
0

Entries	95
Mean	42.59
Std Dev	3

Gaus mean : 43.1610

Gaus Width: 3.0810

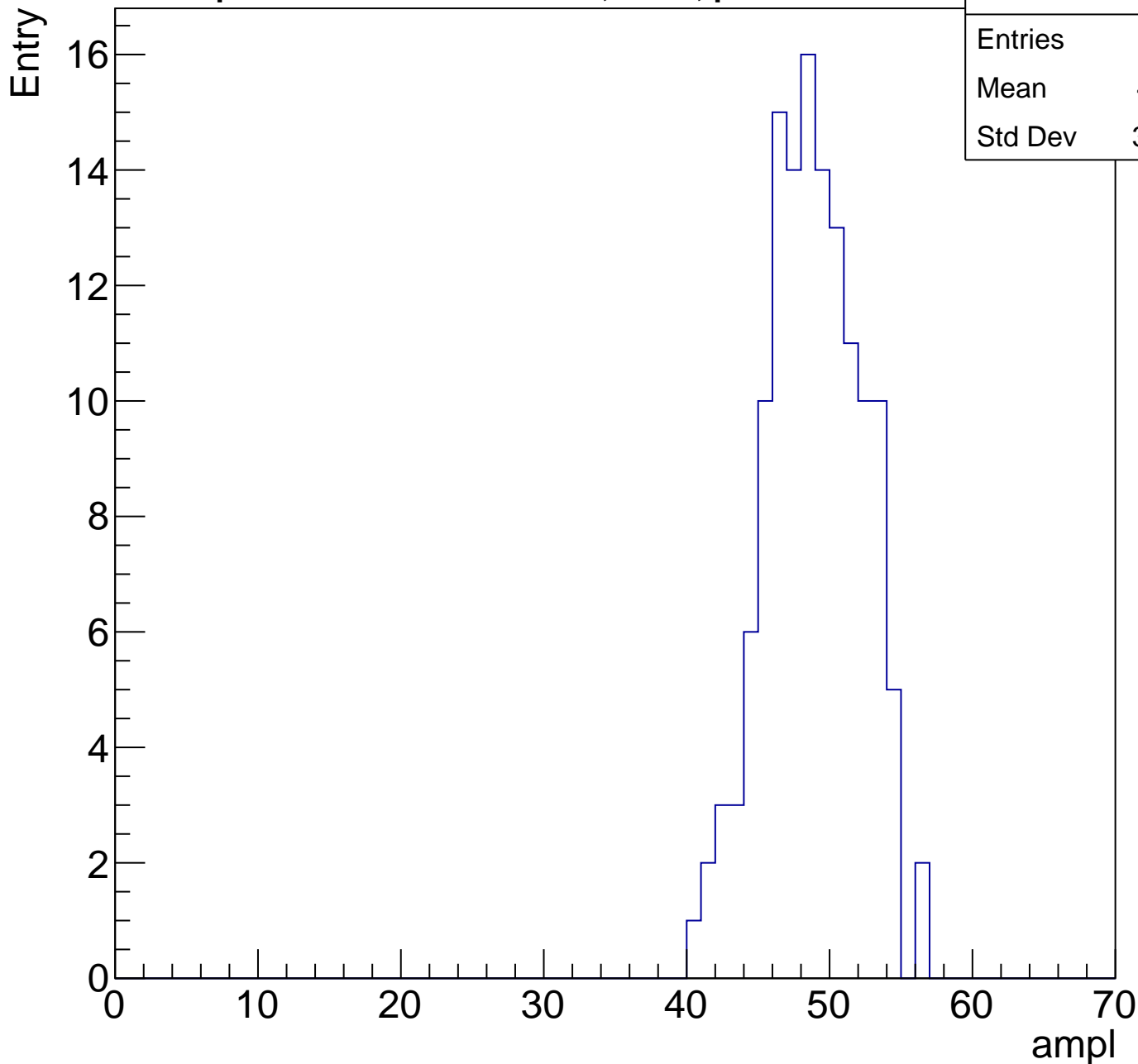
ampl



B0L101S, U24-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

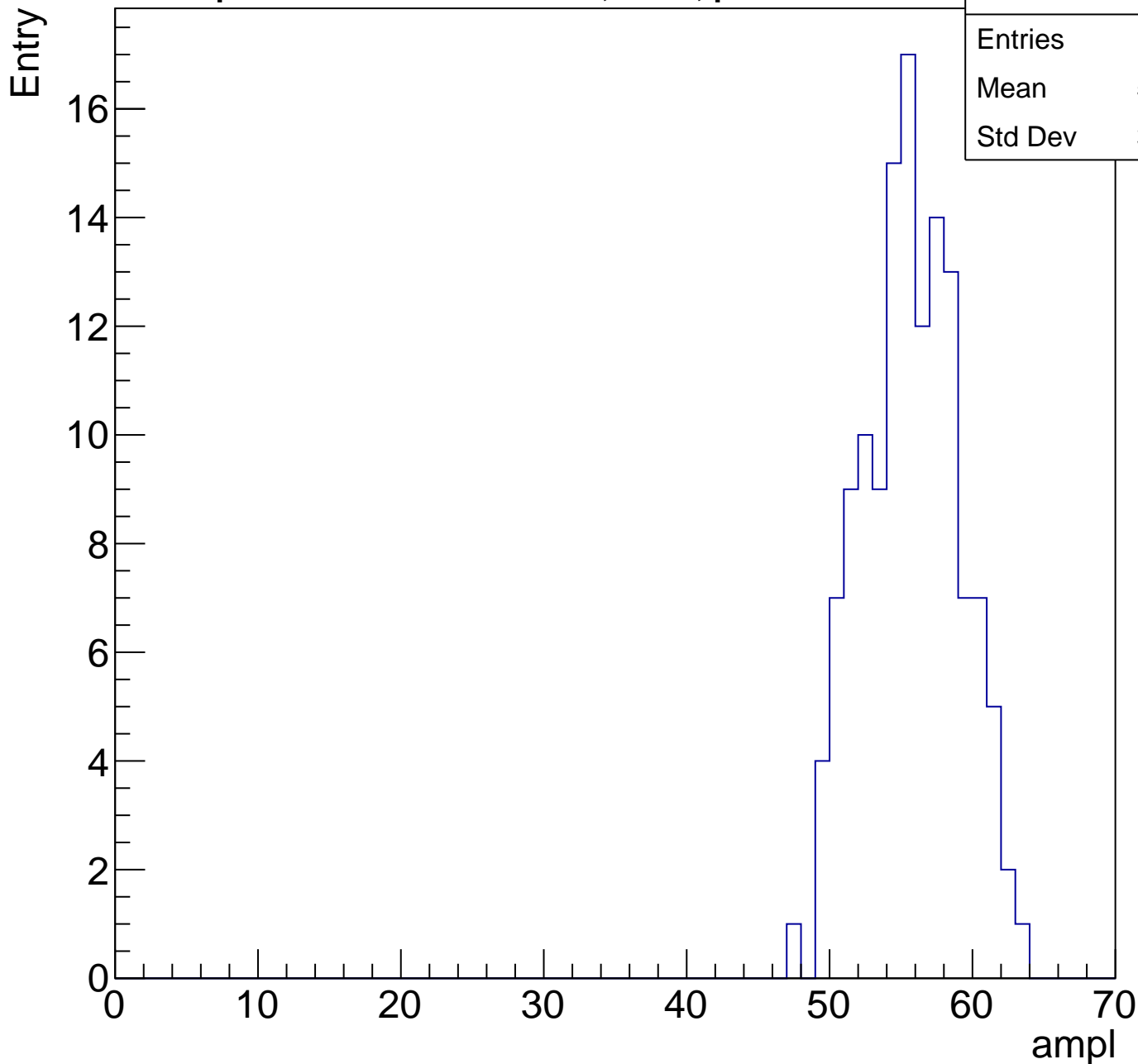
Entries	135
Mean	48.41
Std Dev	3.294



B0L101S, U24-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	55.21
Std Dev	3.331



B0L101S, U24-ch40, adc5

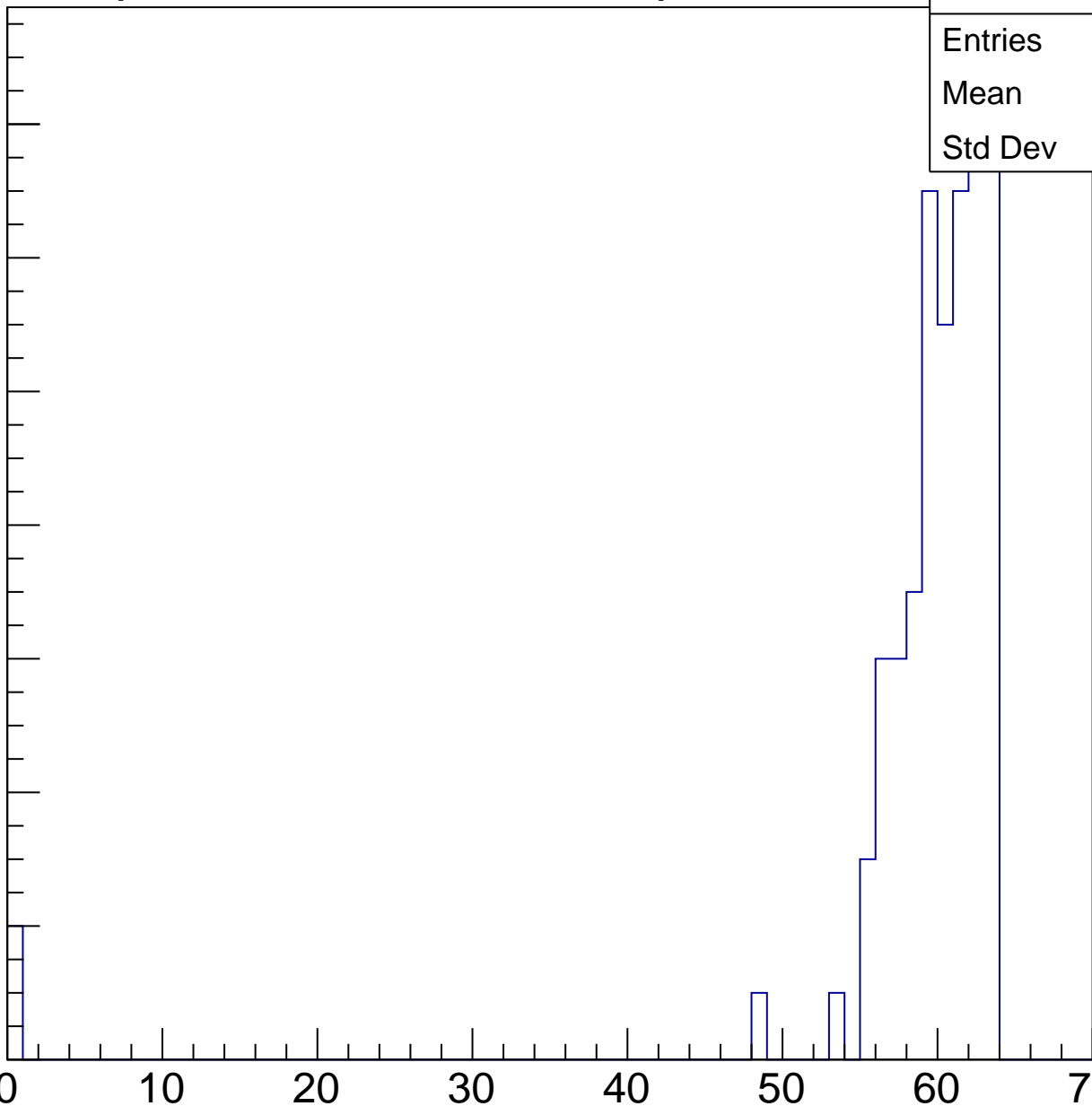
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	58.5
Std Dev	9.119

ampl

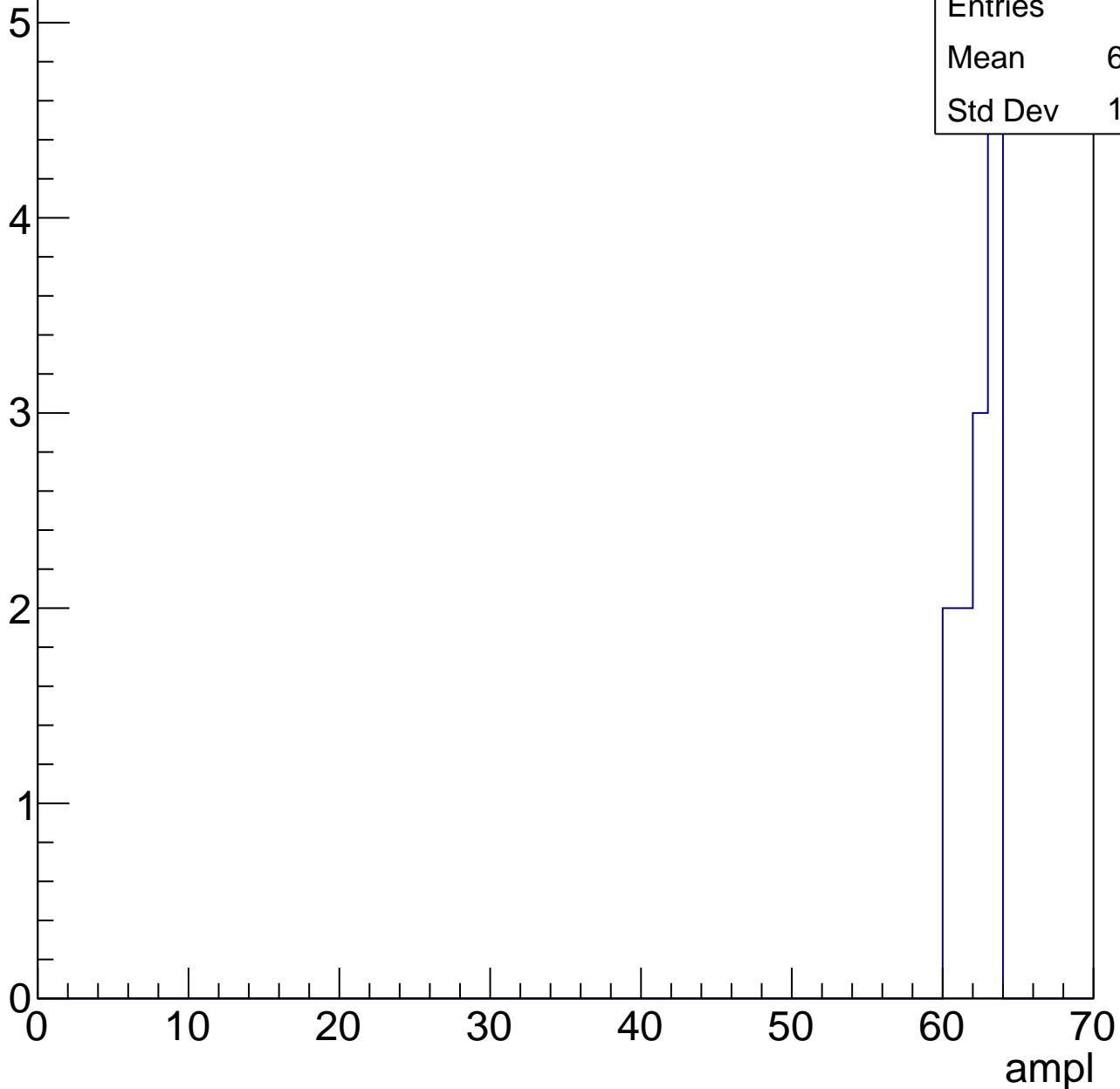


B0L101S, U24-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.92
Std Dev	1.115



B0L101S, U24-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch41, adc0

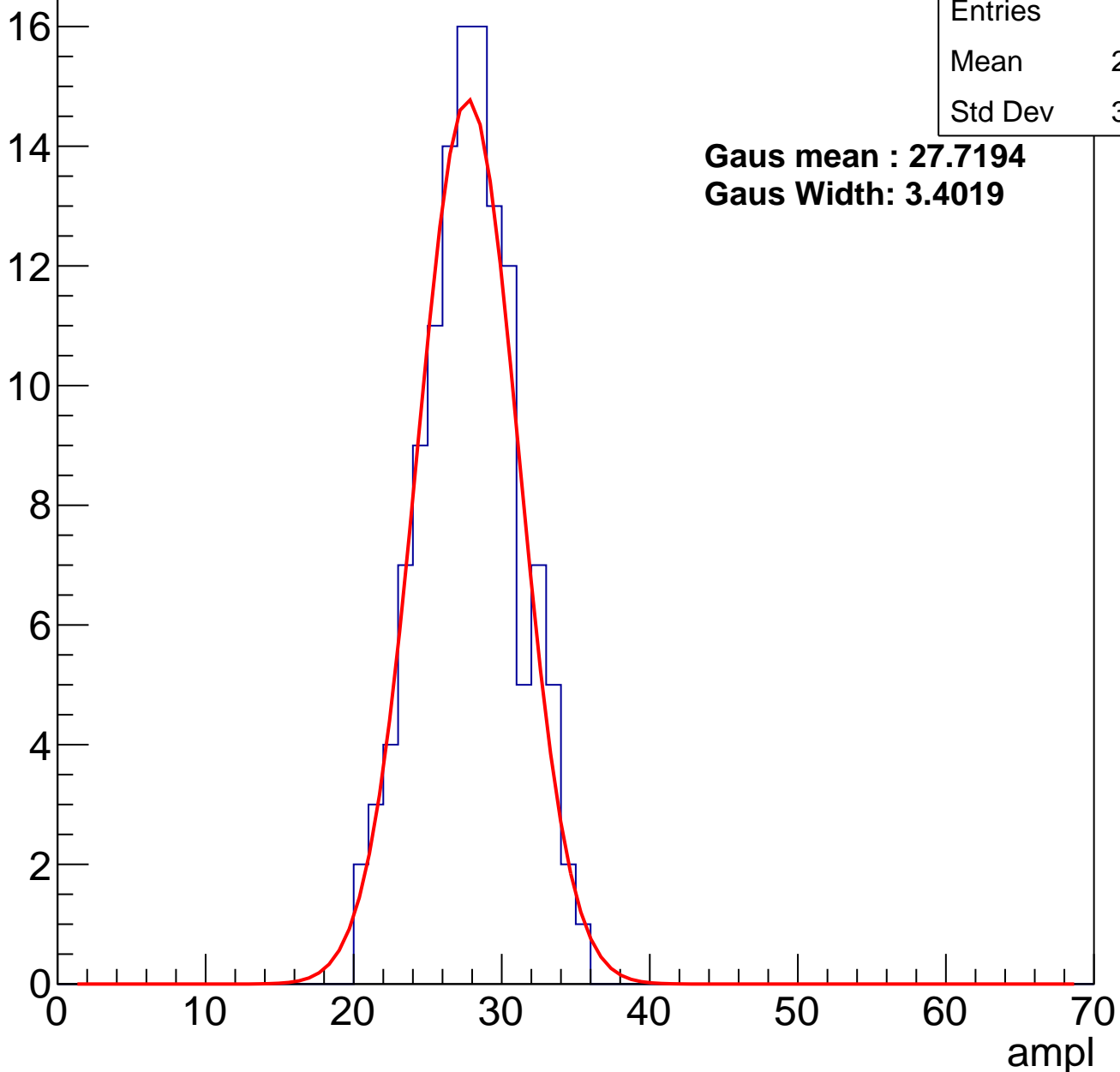
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	27.33
Std Dev	3.219

Gaus mean : 27.7194

Gaus Width: 3.4019

Entry



B0L101S, U24-ch41, adc1

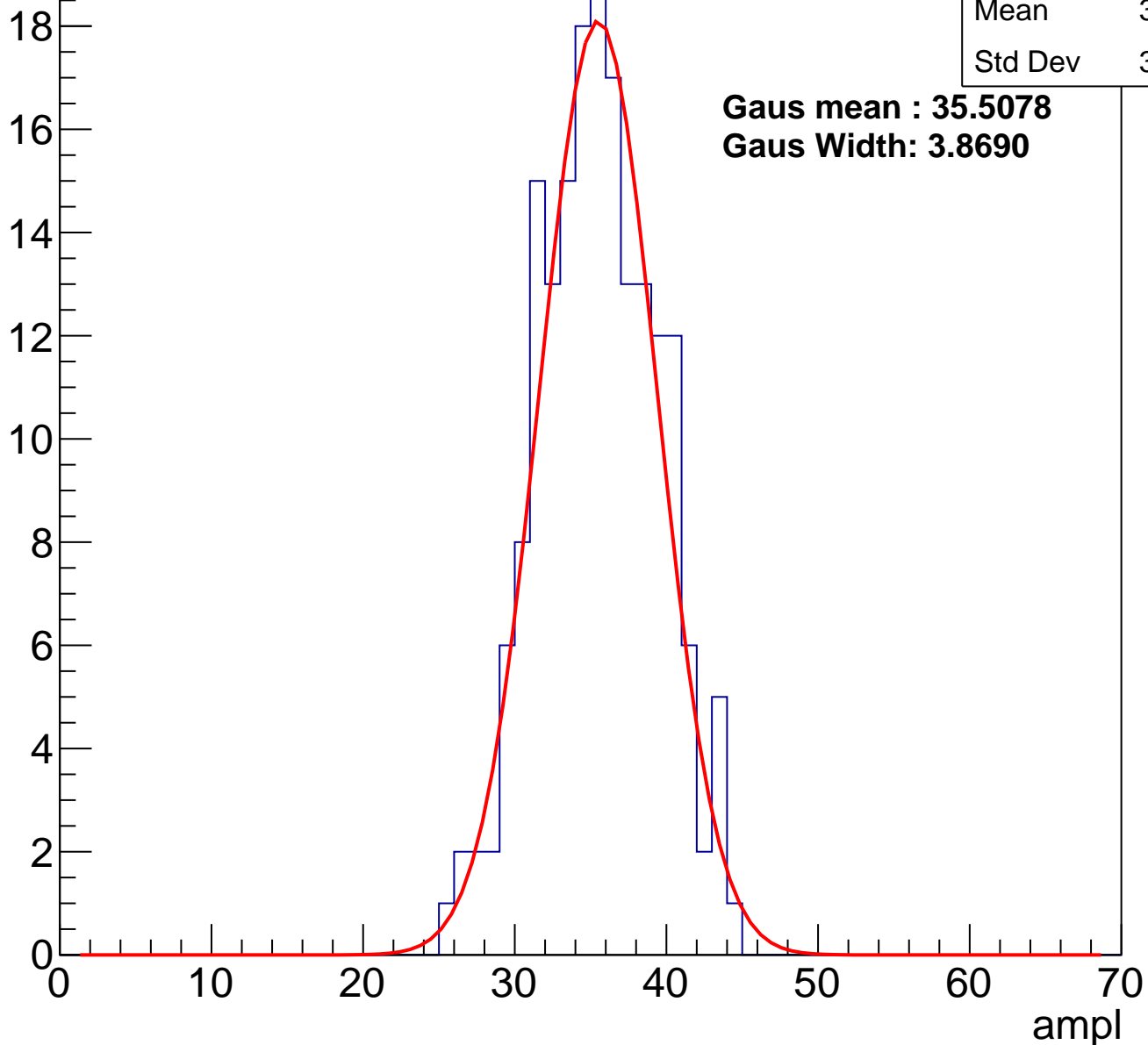
calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	35.04
Std Dev	3.874

Gaus mean : 35.5078

Gaus Width: 3.8690

Entry



B0L101S, U24-ch41, adc2

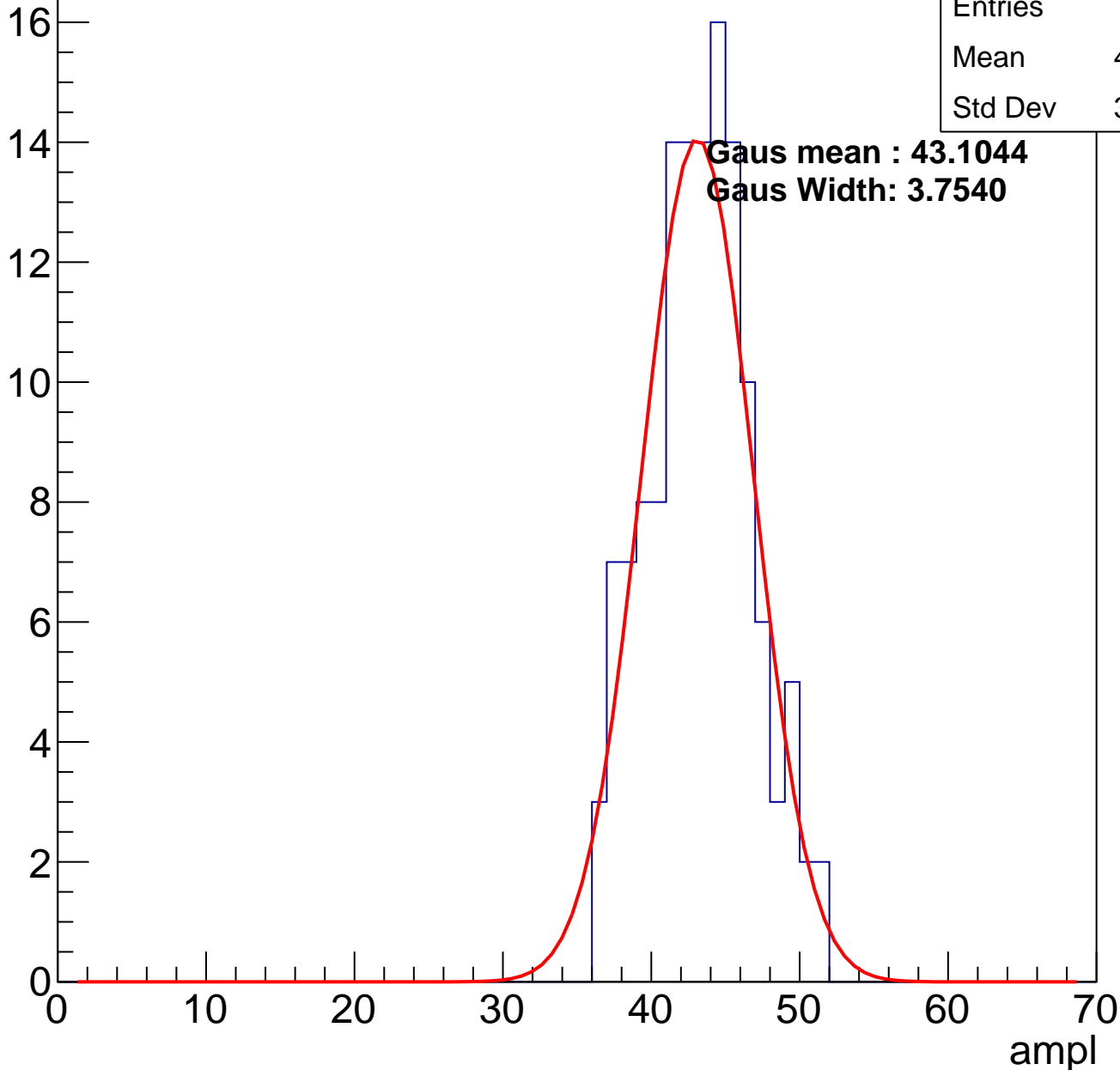
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	42.83
Std Dev	3.446

Gaus mean : 43.1044

Gaus Width: 3.7540

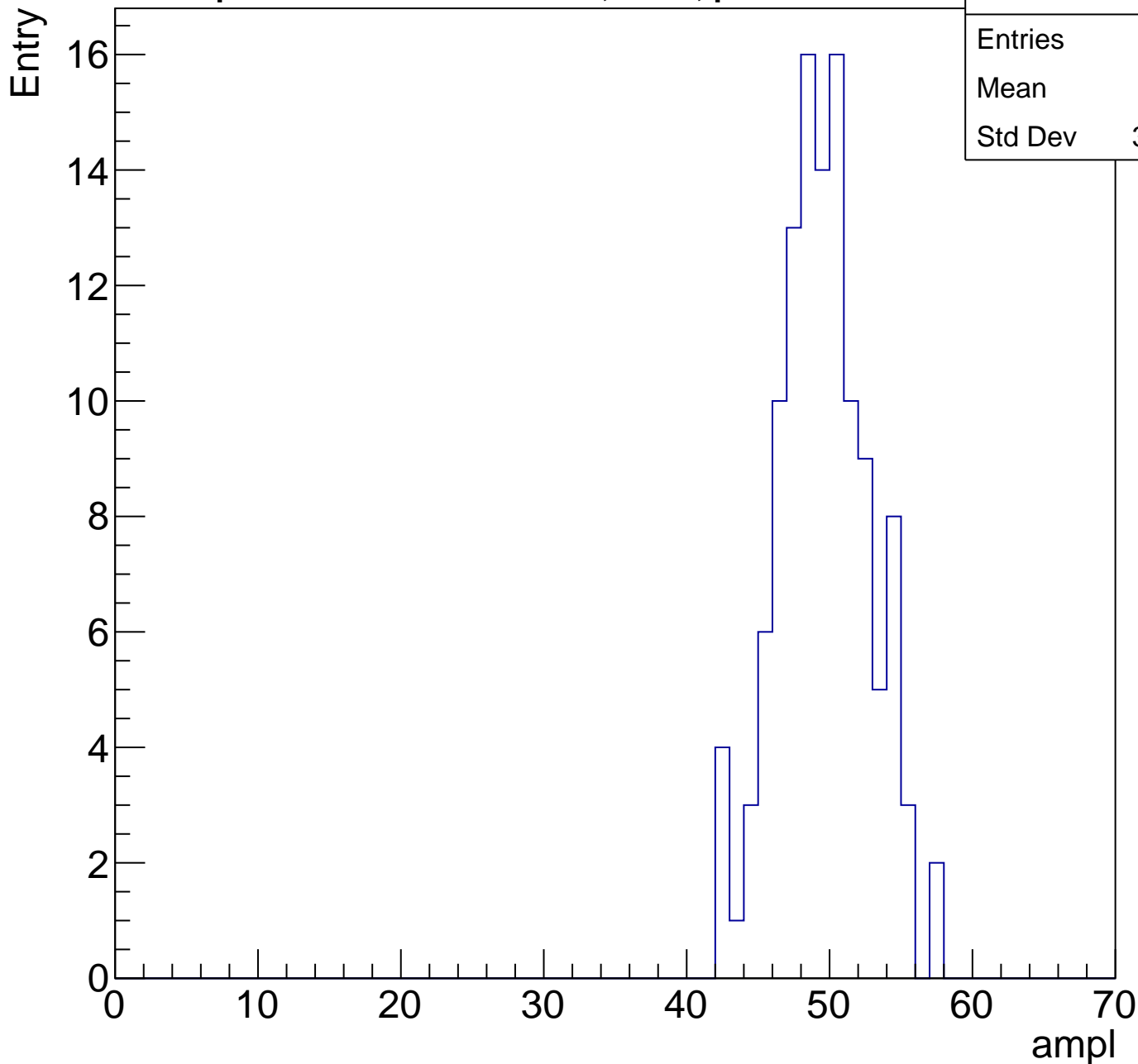
Entry



B0L101S, U24-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	49.1
Std Dev	3.179



B0L101S, U24-ch41, adc4

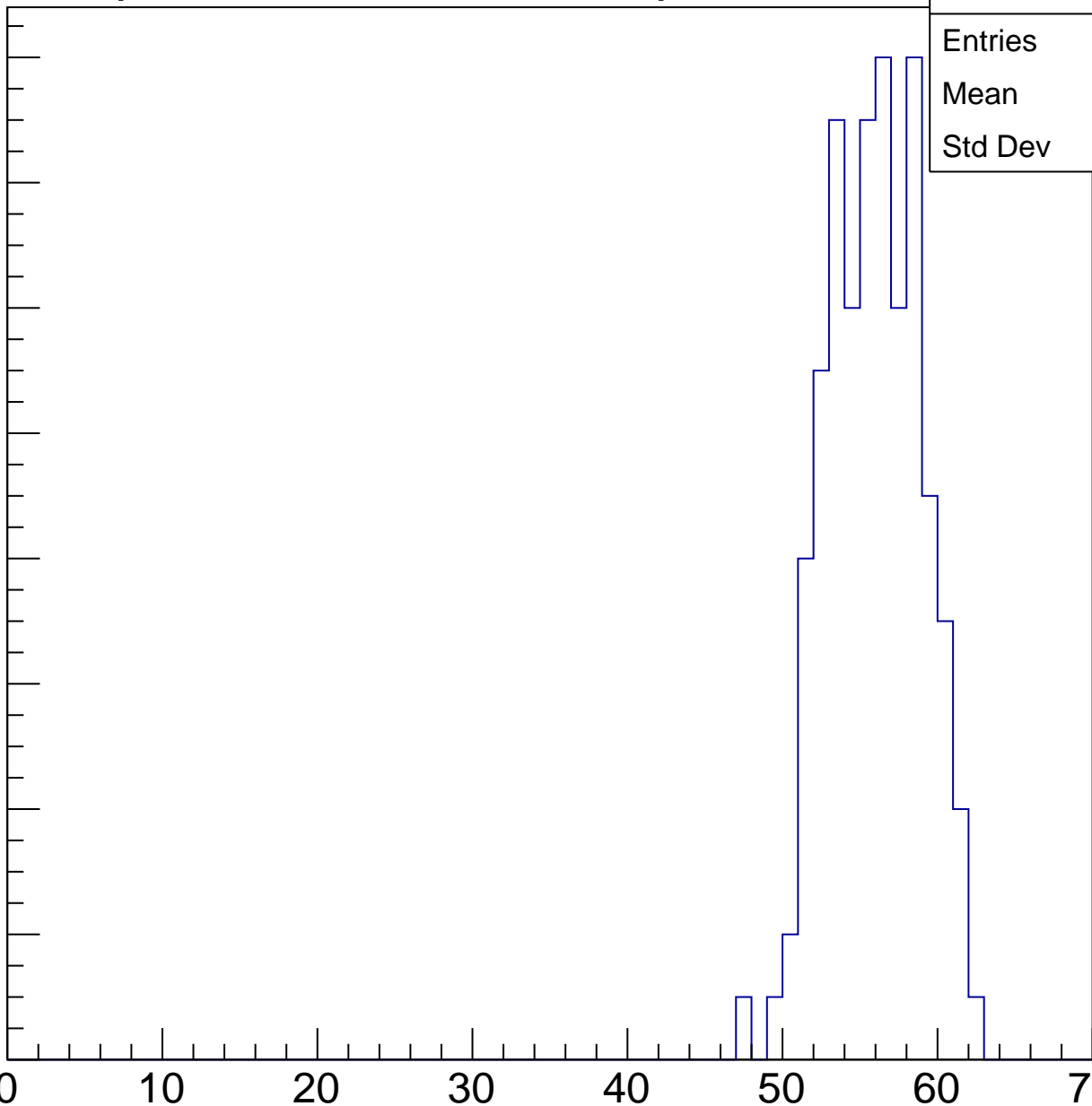
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	55.45
Std Dev	2.964

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch41, adc5

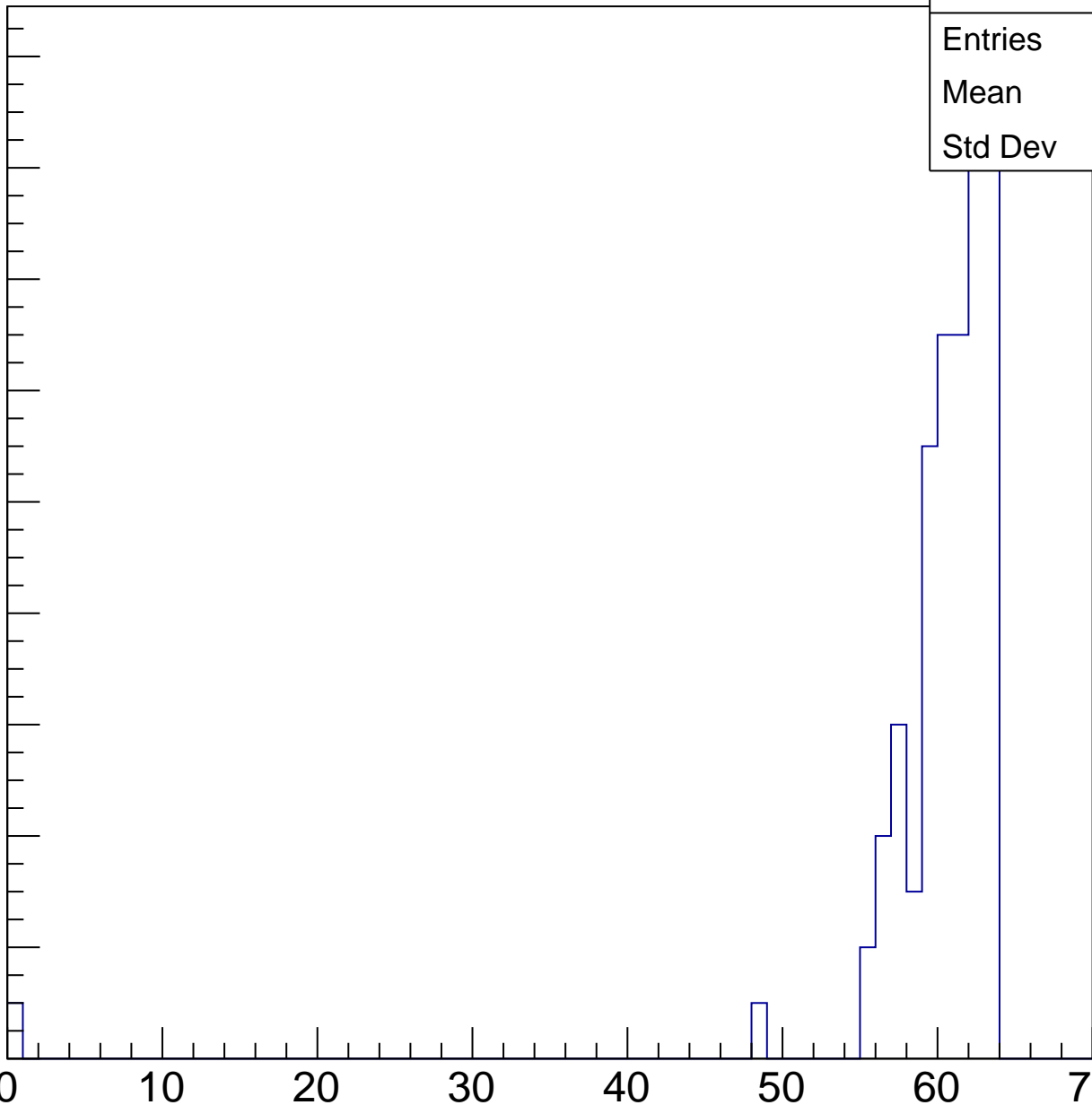
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.64
Std Dev	6.842

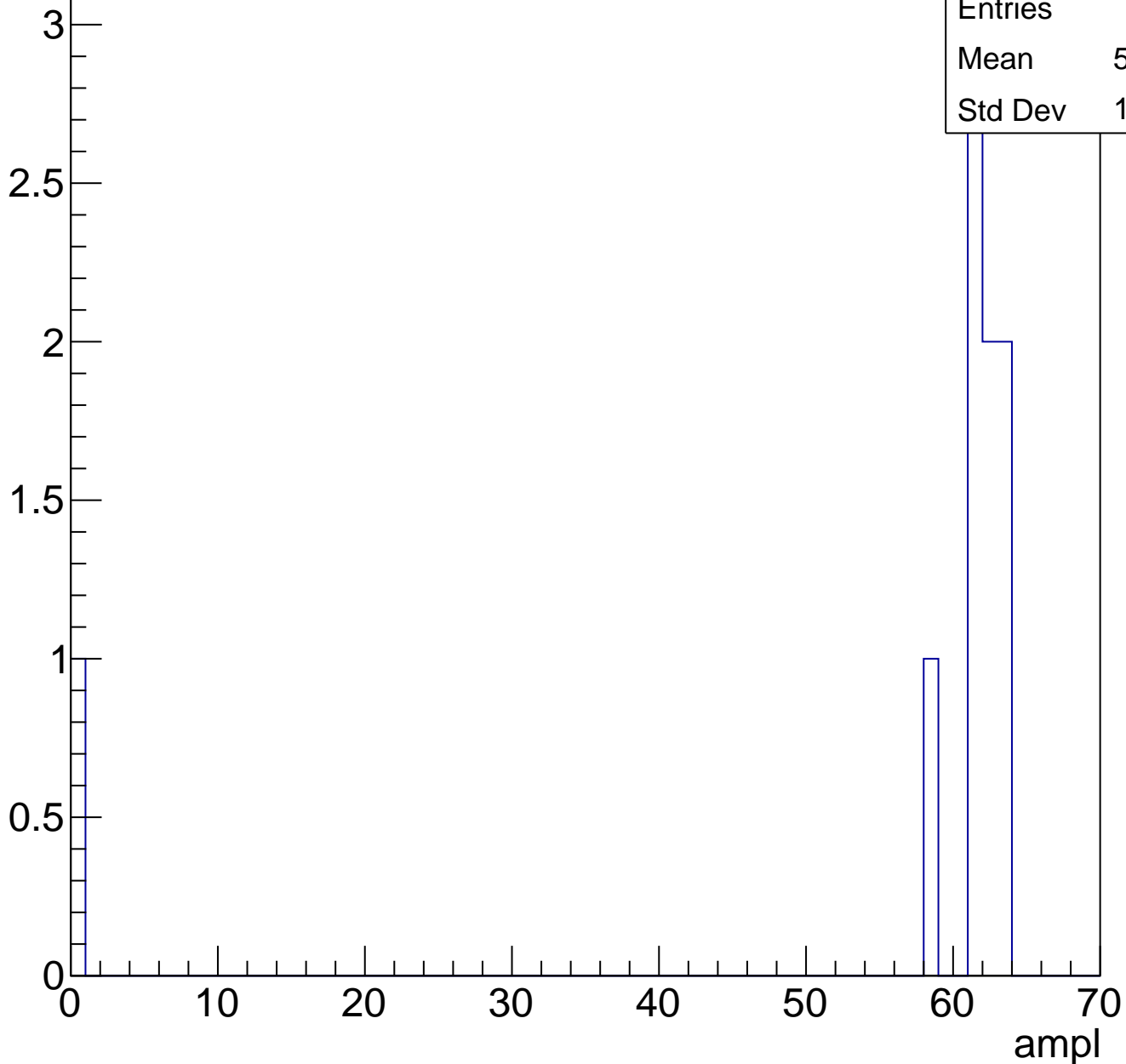
ampl



B0L101S, U24-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch42, adc0

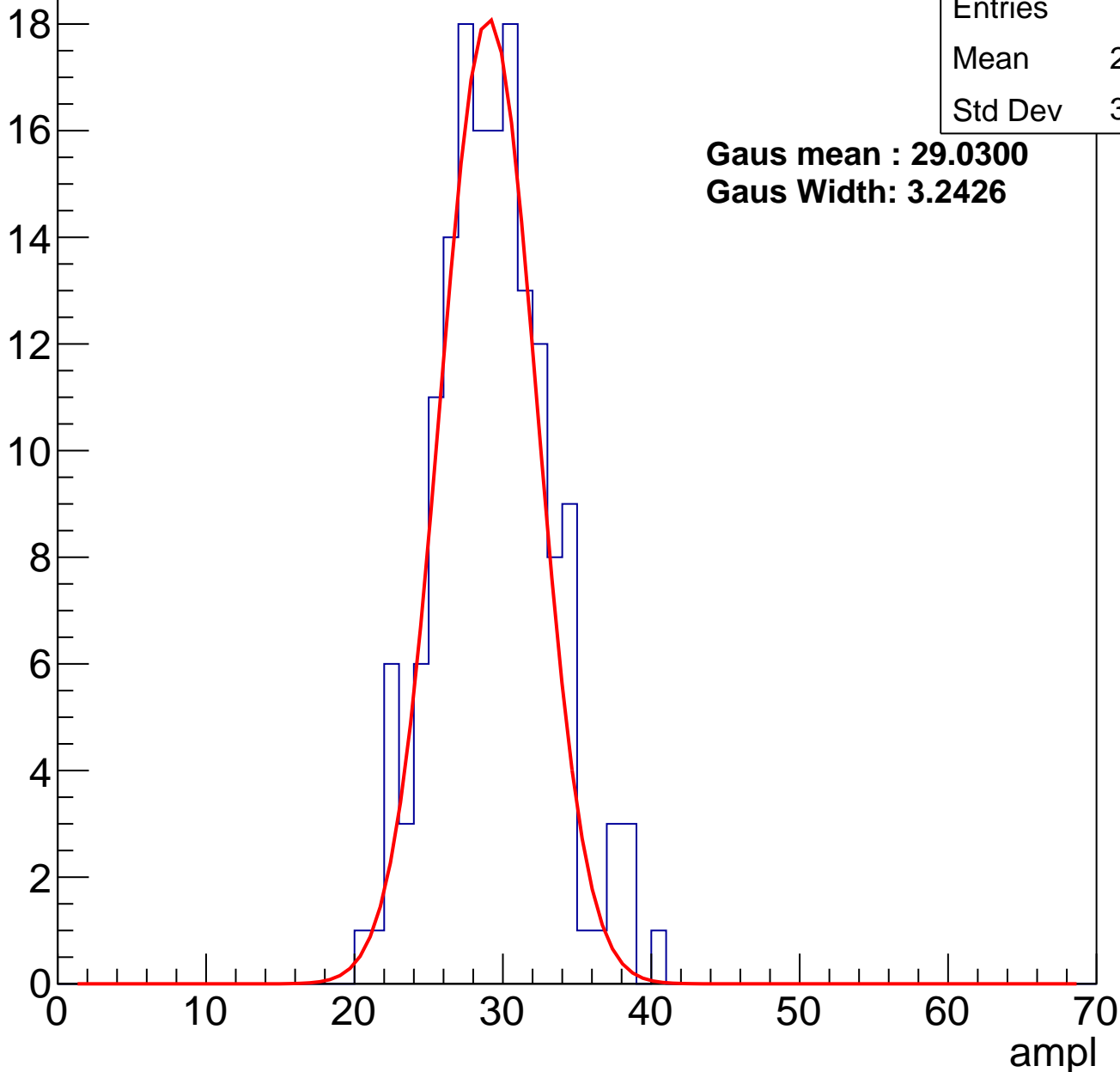
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	28.92
Std Dev	3.745

Gaus mean : 29.0300

Gaus Width: 3.2426

Entry



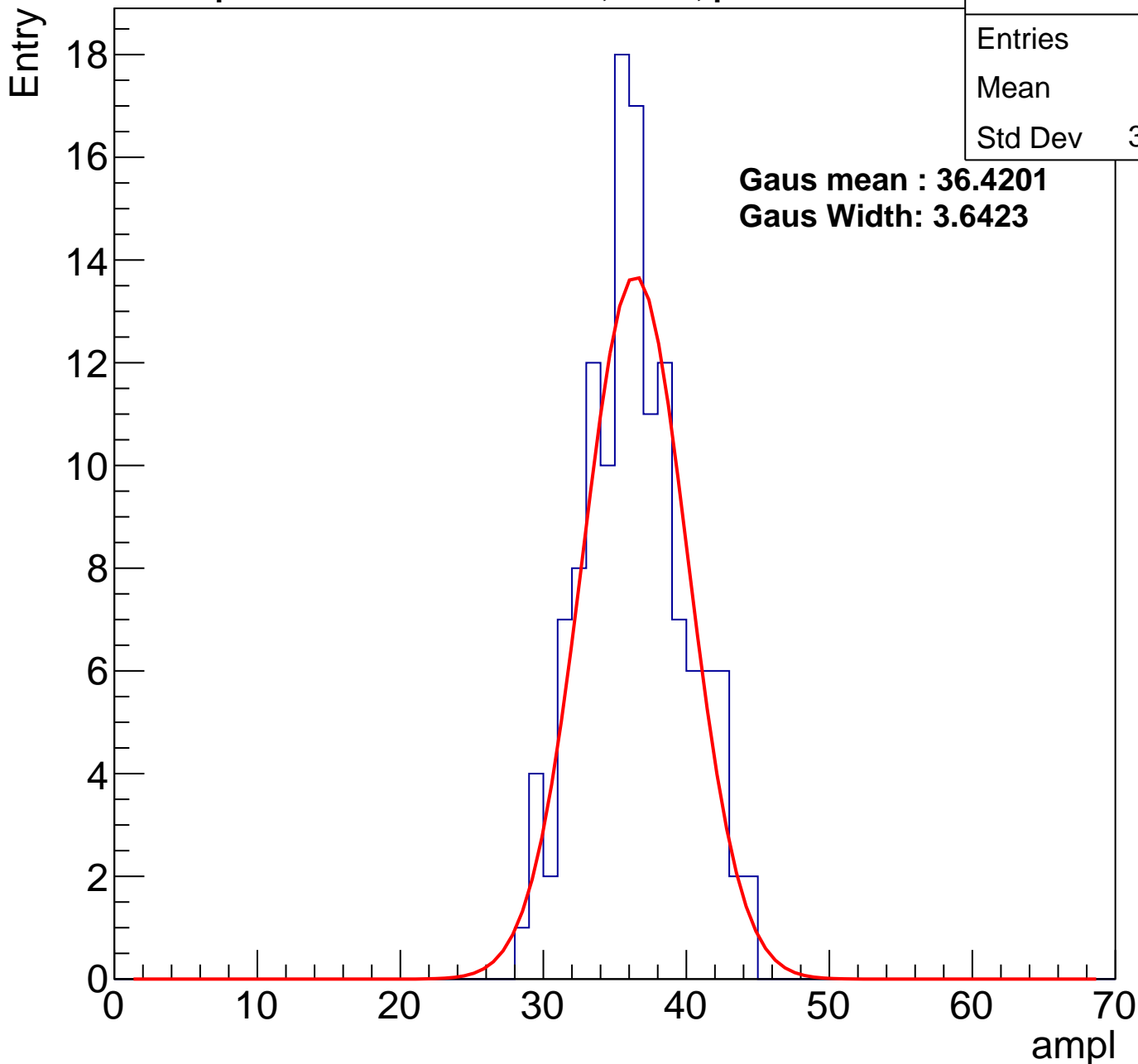
B0L101S, U24-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	35.9
Std Dev	3.505

Gaus mean : 36.4201

Gaus Width: 3.6423



B0L101S, U24-ch42, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

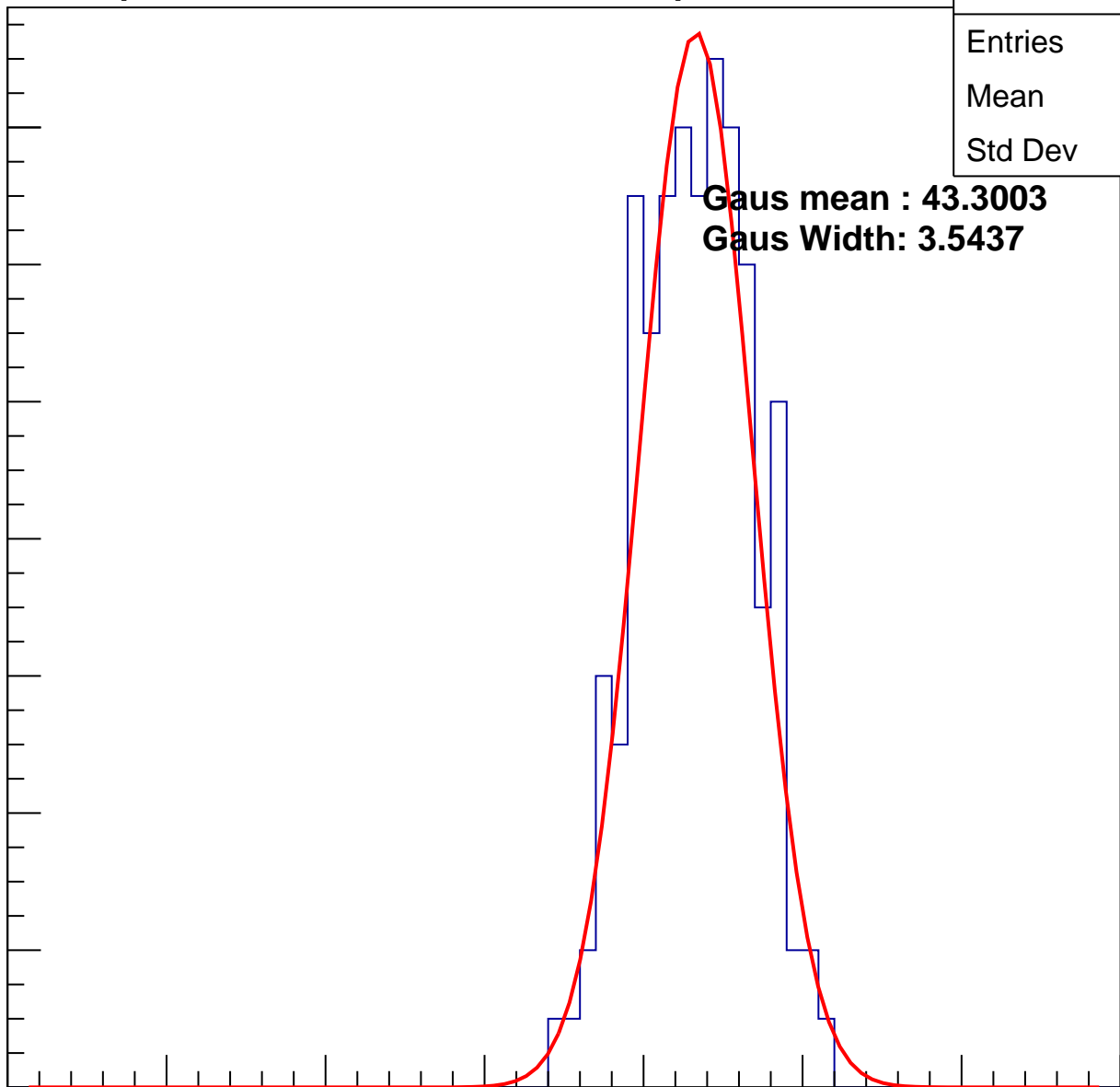
70

ampl

Entries	142
Mean	42.82
Std Dev	3.488

Gaus mean : 43.3003

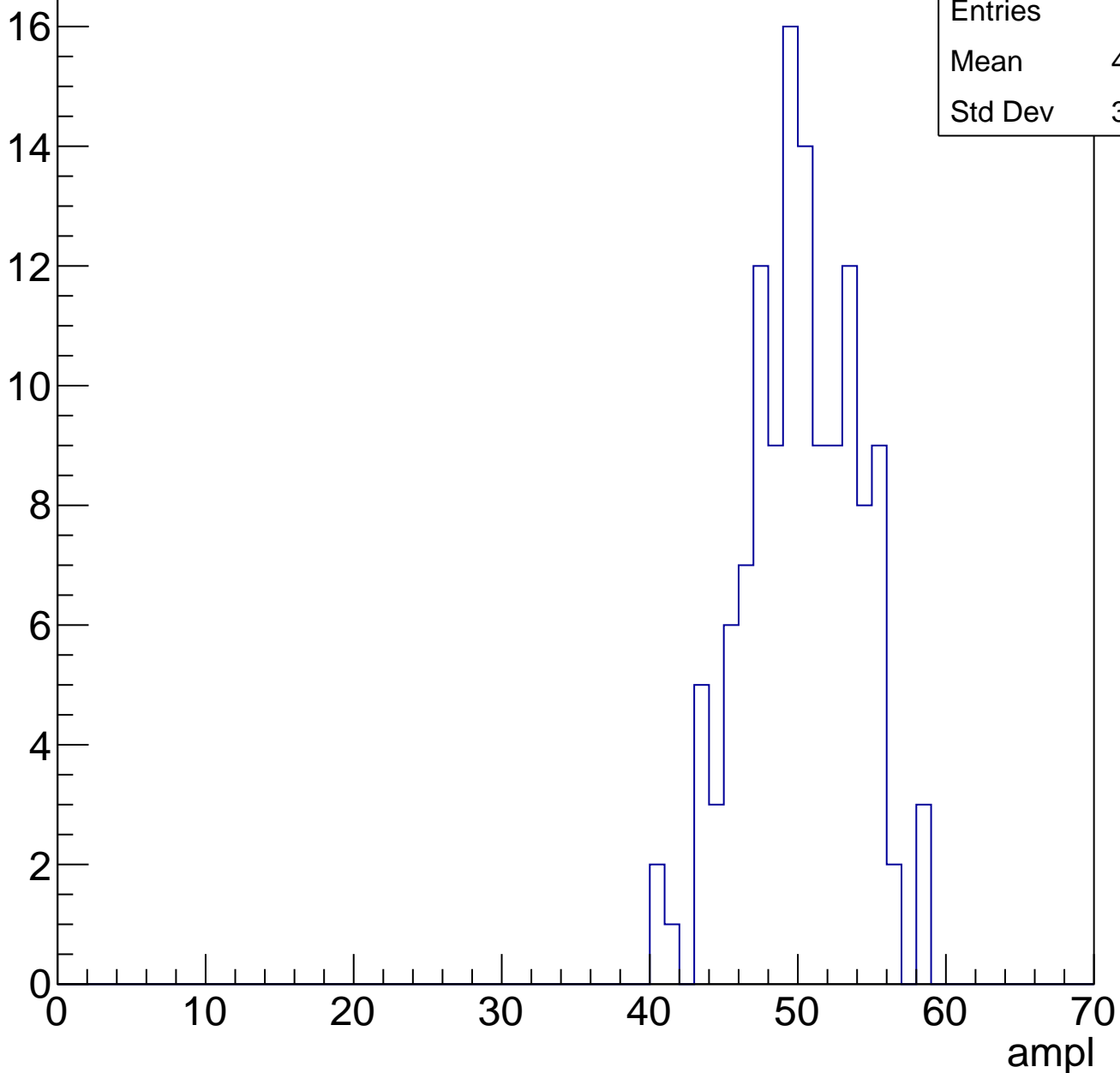
Gaus Width: 3.5437



B0L101S, U24-ch42, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch42, adc4

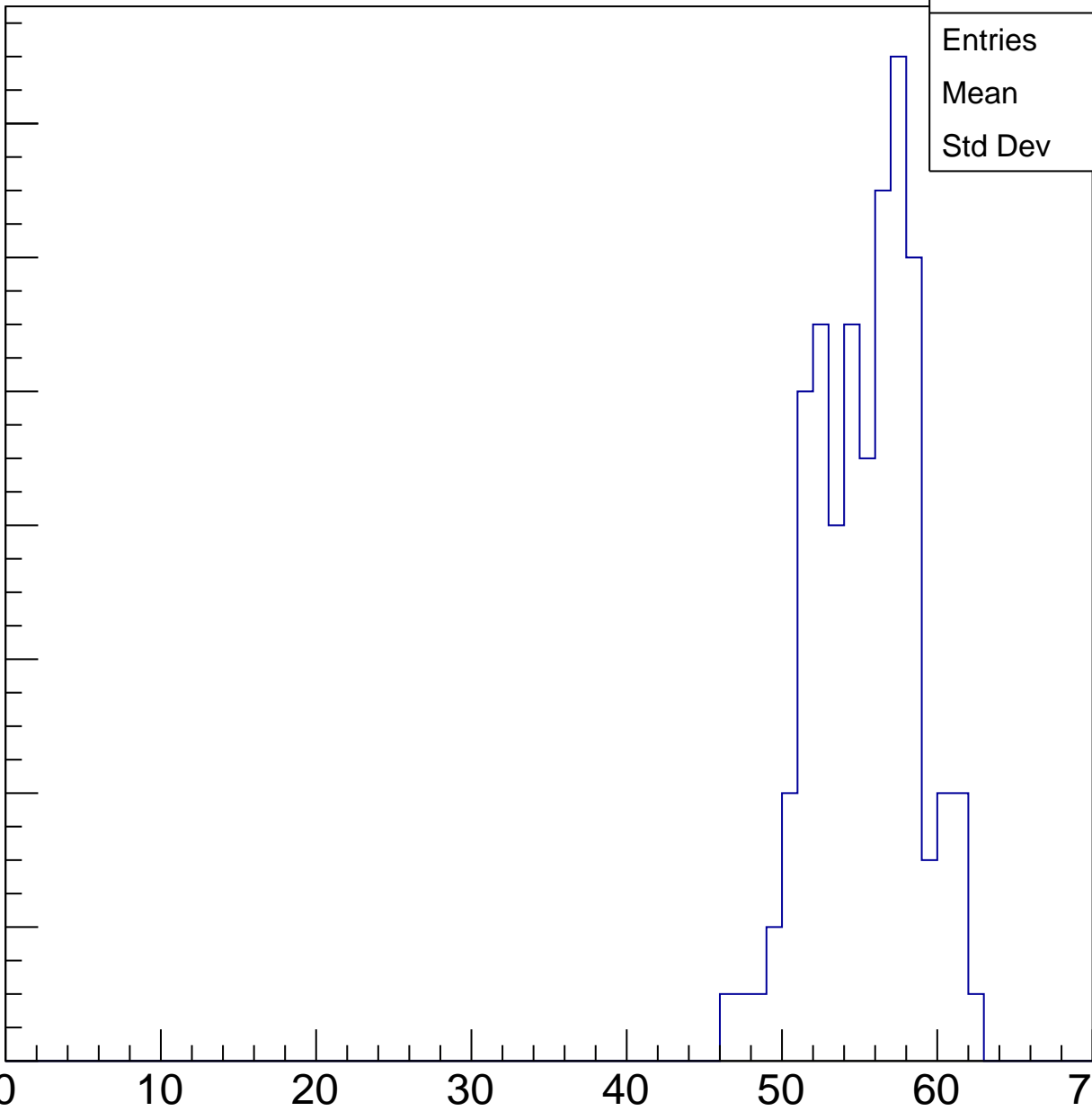
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	54.87
Std Dev	3.287

ampl



B0L101S, U24-ch42, adc5

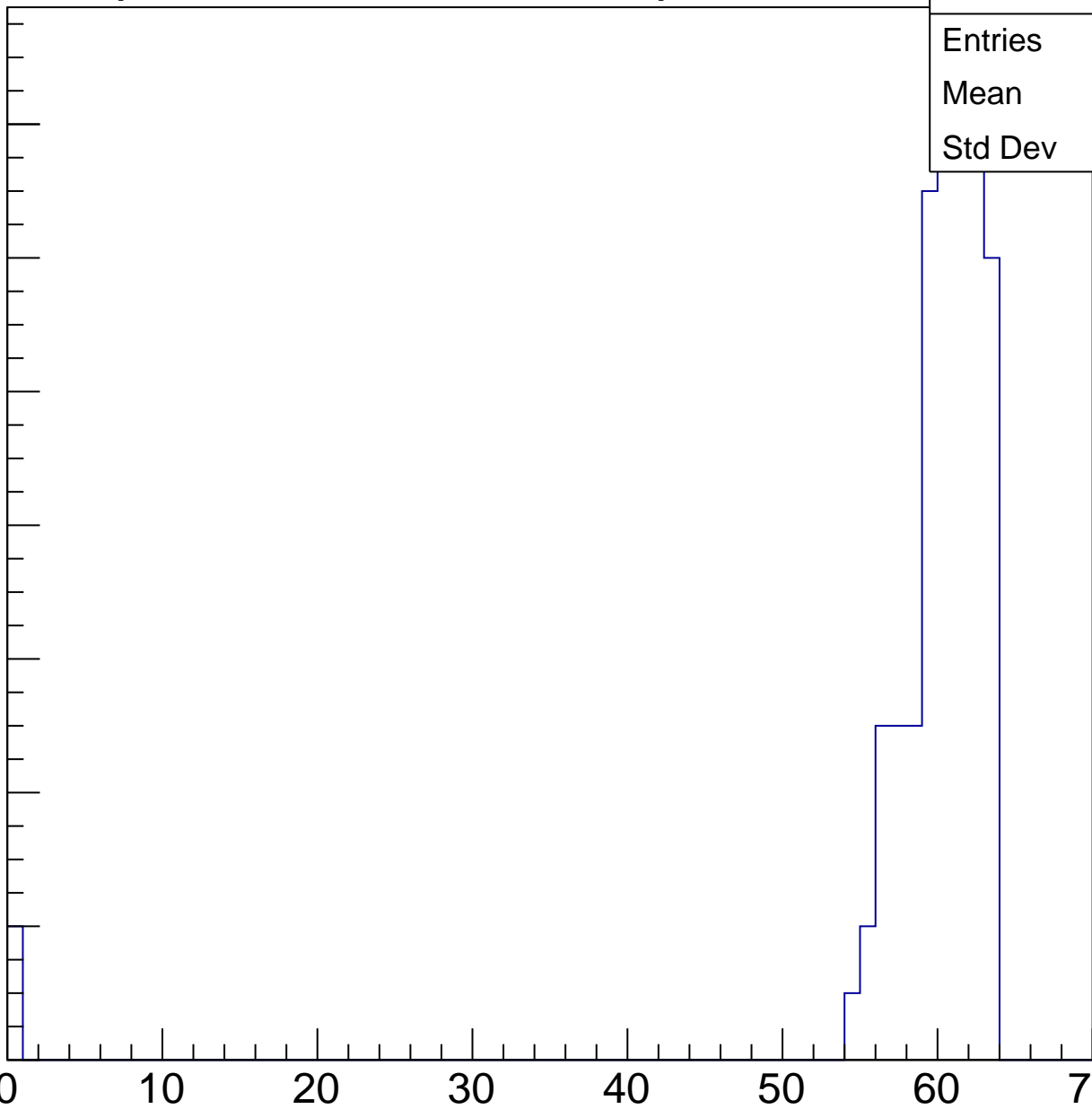
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	58.73
Std Dev	9.17

ampl

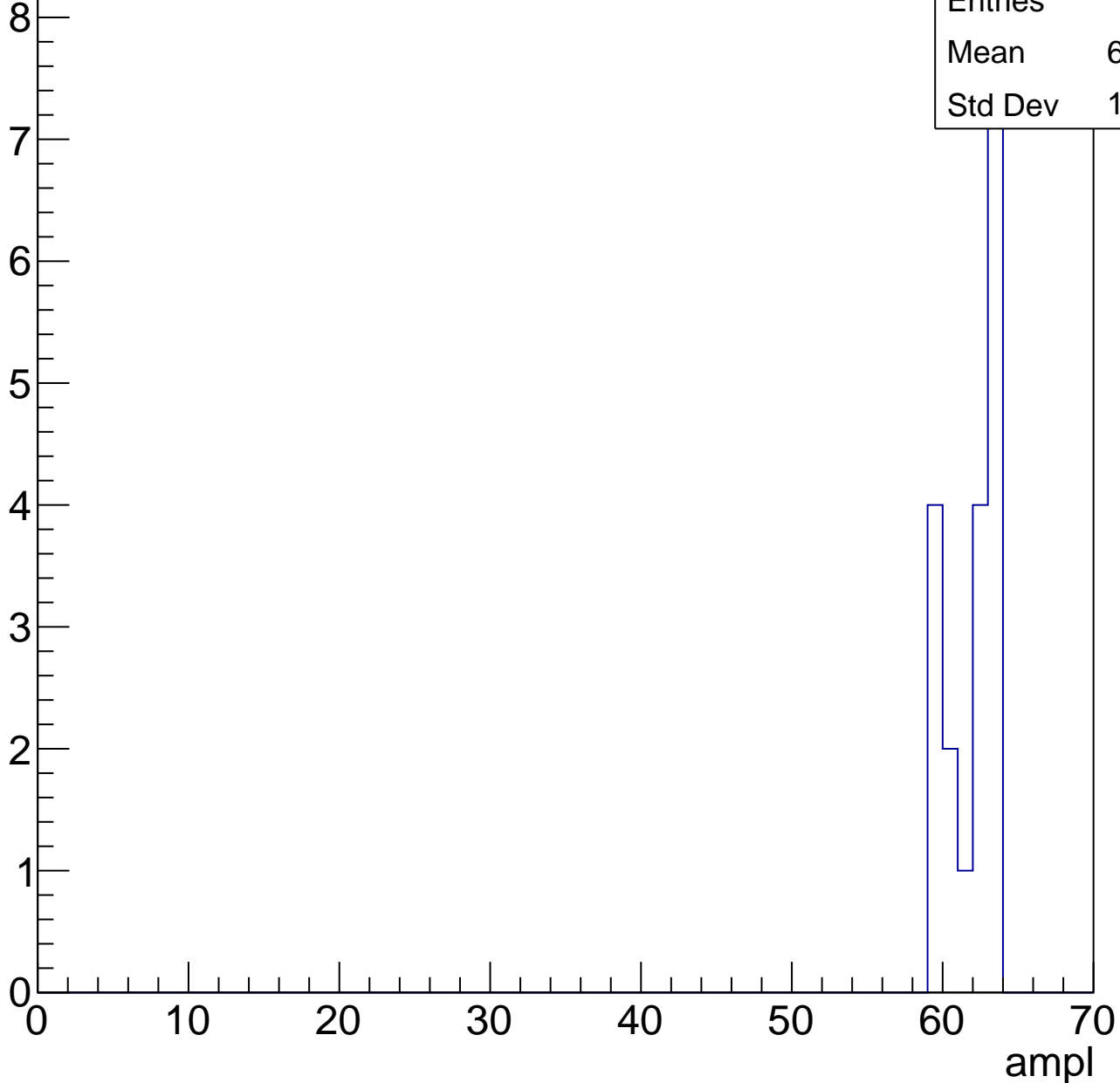


B0L101S, U24-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

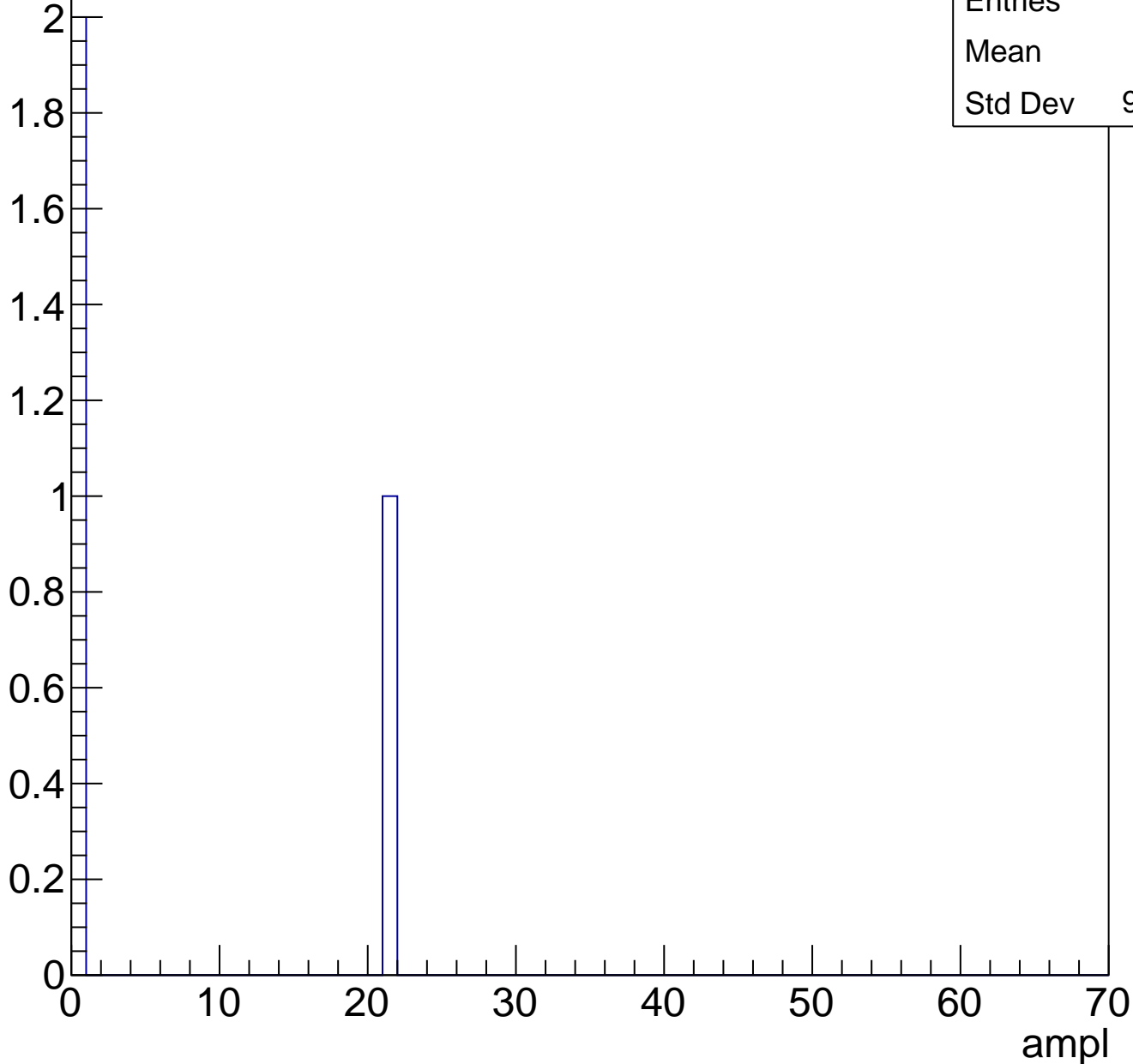
Entries	19
Mean	61.53
Std Dev	1.602



B0L101S, U24-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch43, adc0

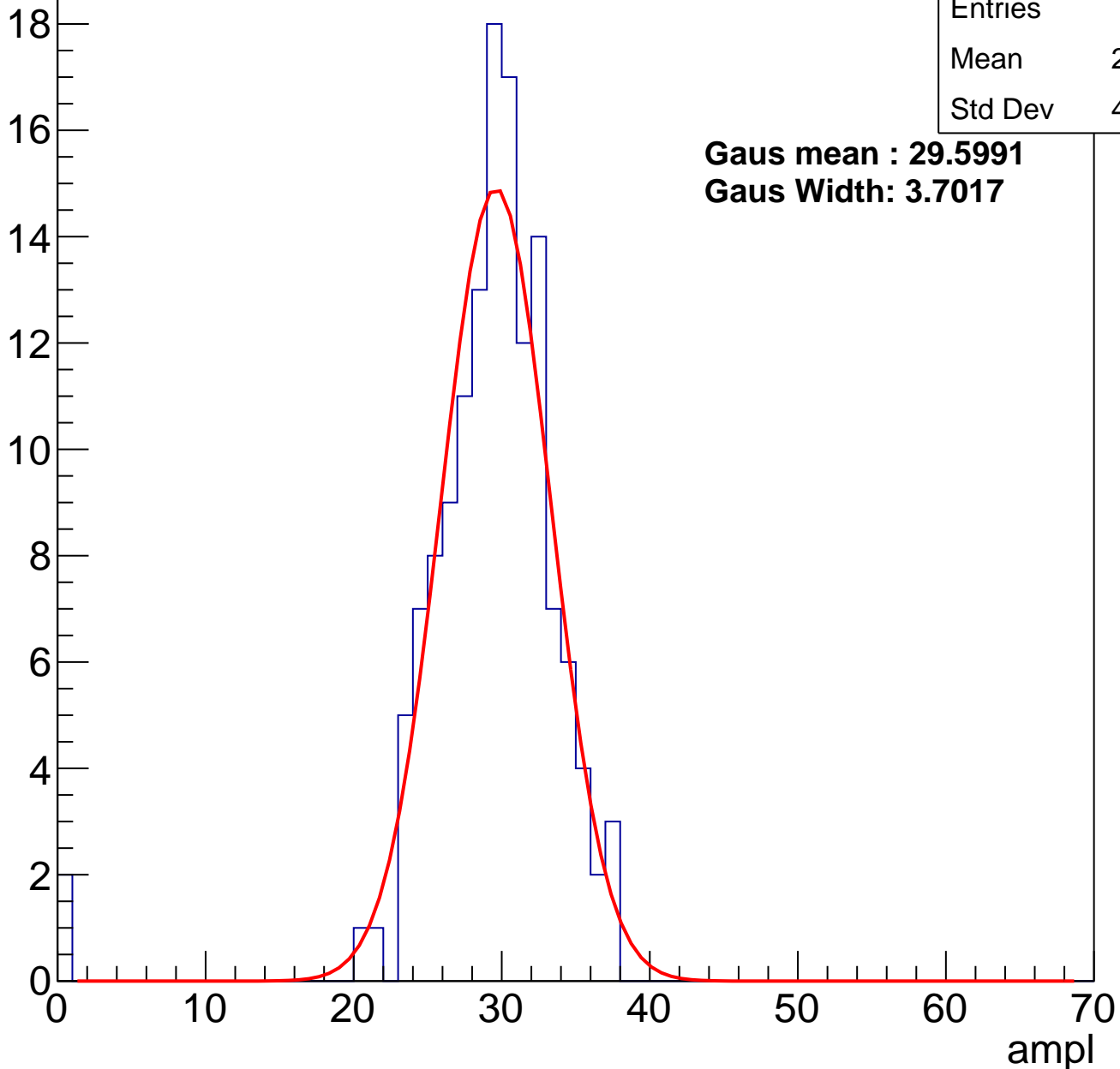
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	28.78
Std Dev	4.876

Gaus mean : 29.5991

Gaus Width: 3.7017

Entry



B0L101S, U24-ch43, adc1

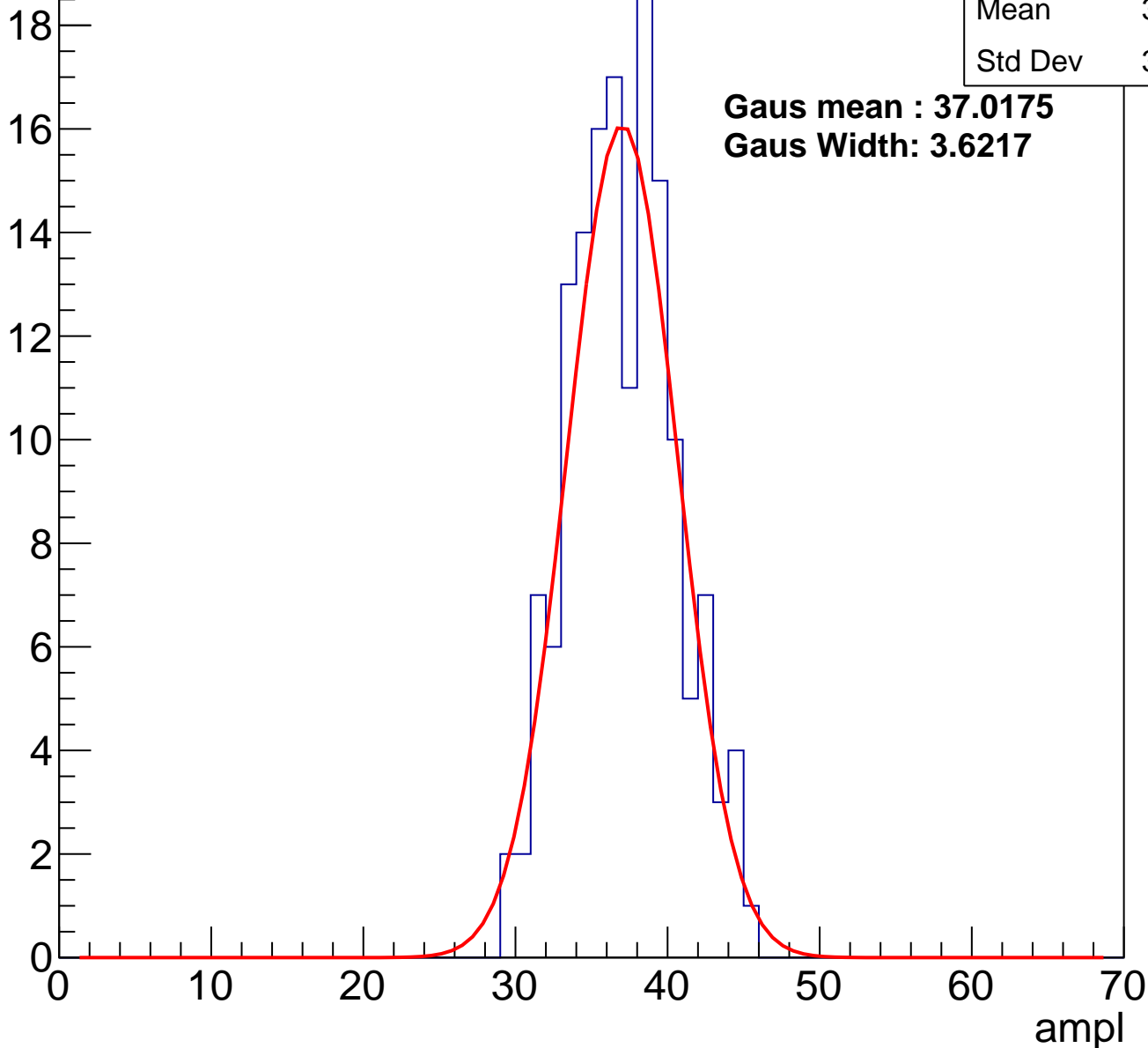
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	36.62
Std Dev	3.454

Gaus mean : 37.0175

Gaus Width: 3.6217

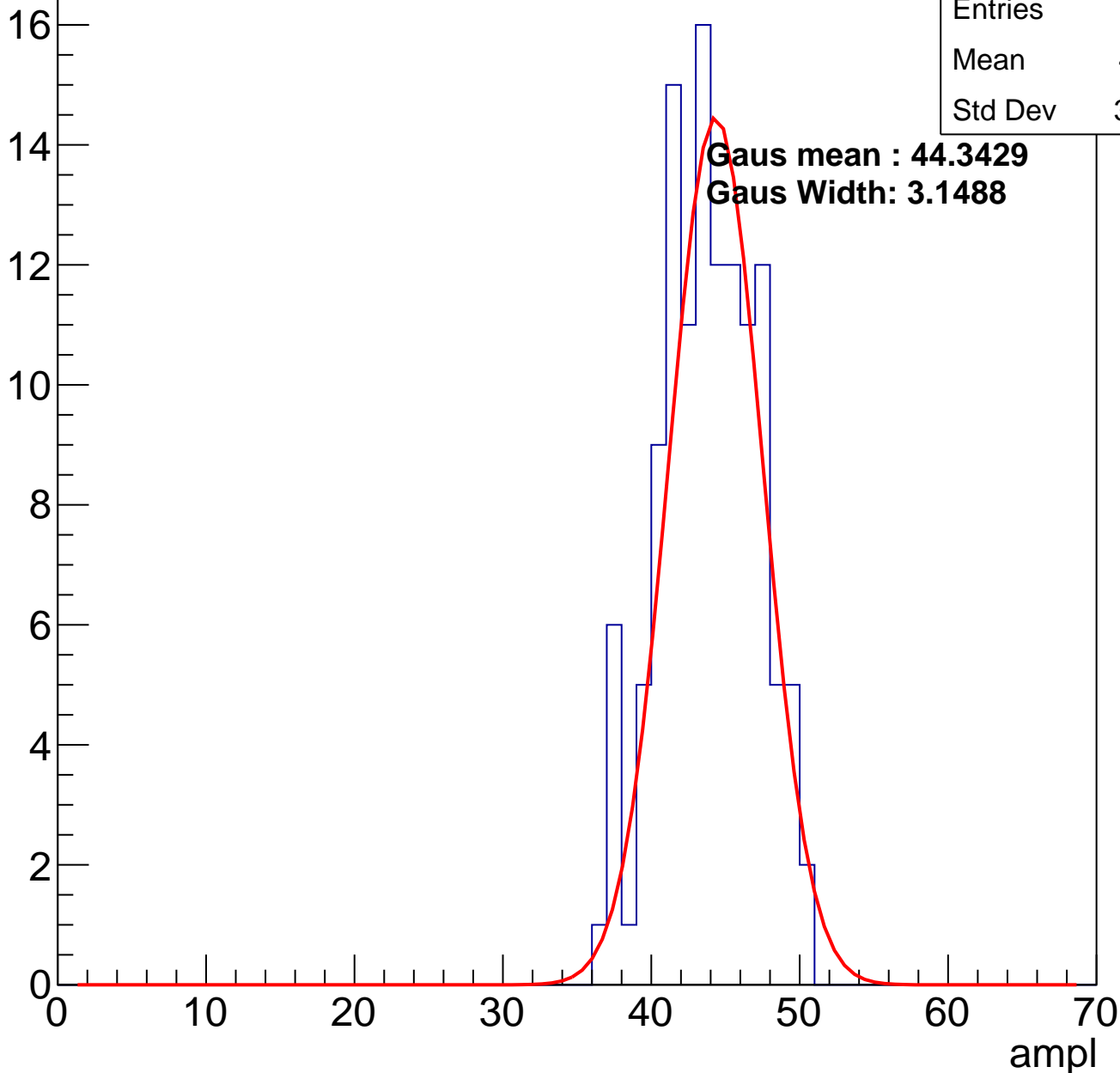
Entry



B0L101S, U24-ch43, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

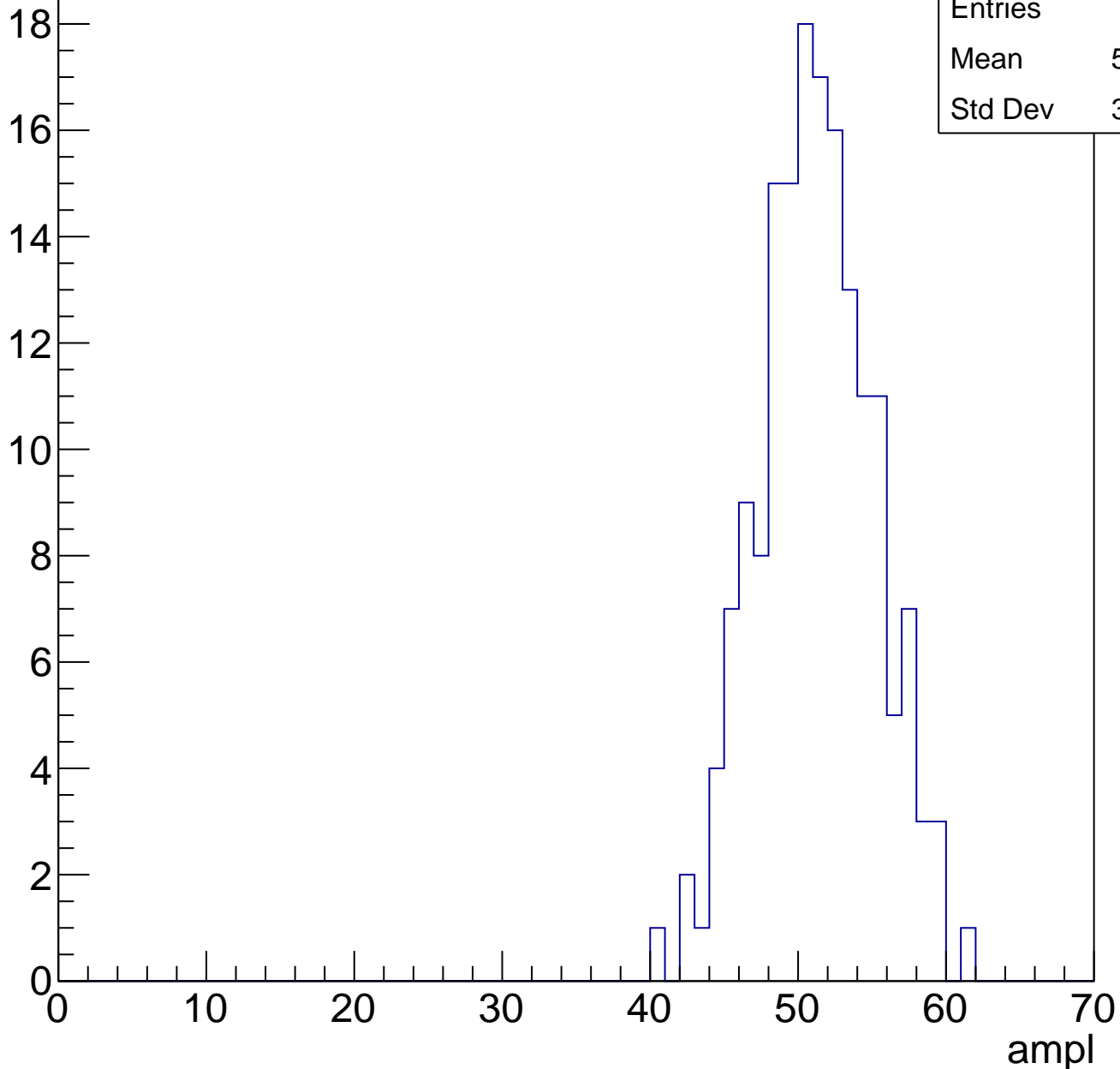


B0L101S, U24-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	50.78
Std Dev	3.879

Entry



B0L101S, U24-ch43, adc4

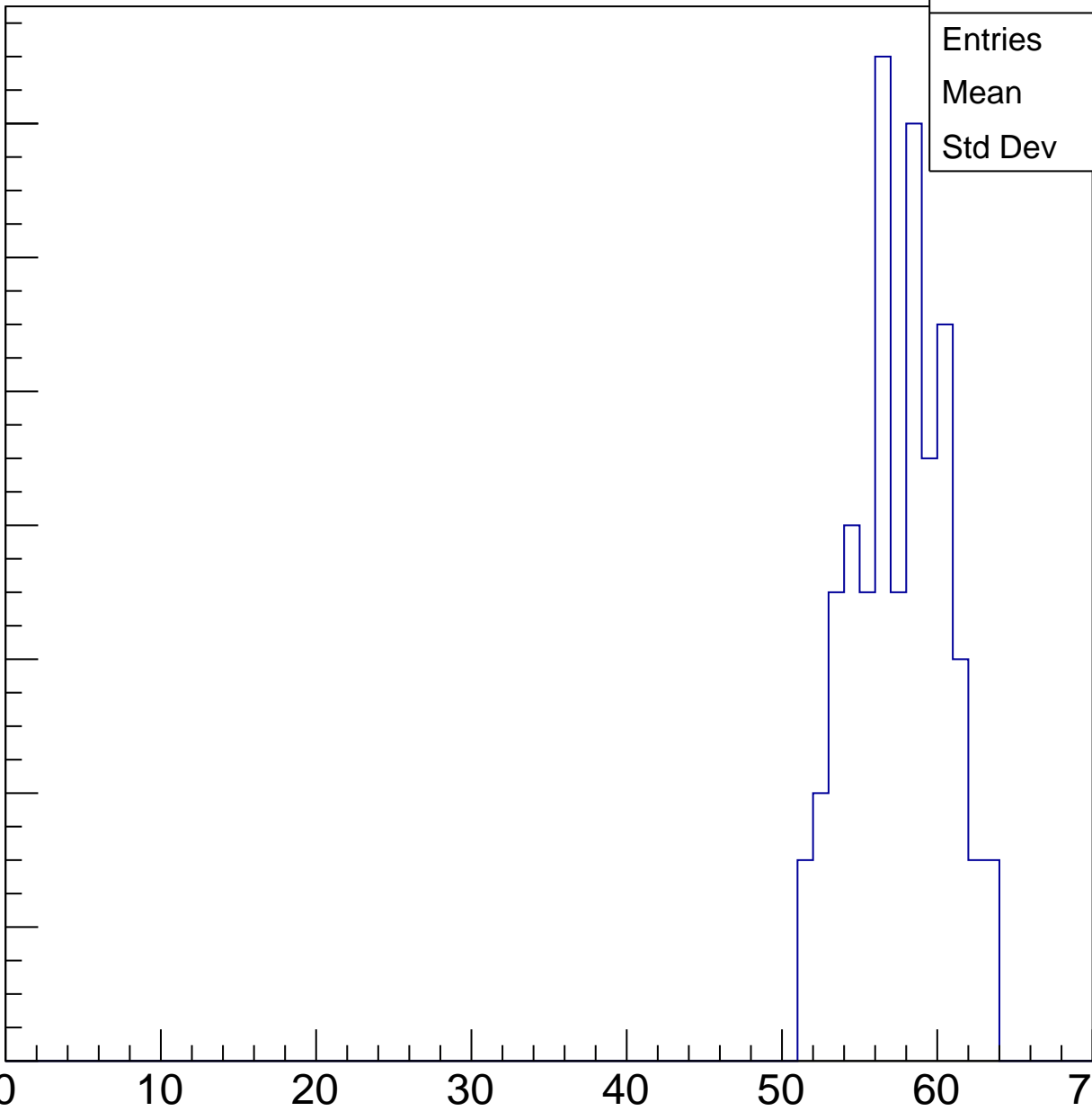
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	57.03
Std Dev	2.983

ampl



B0L101S, U24-ch43, adc5

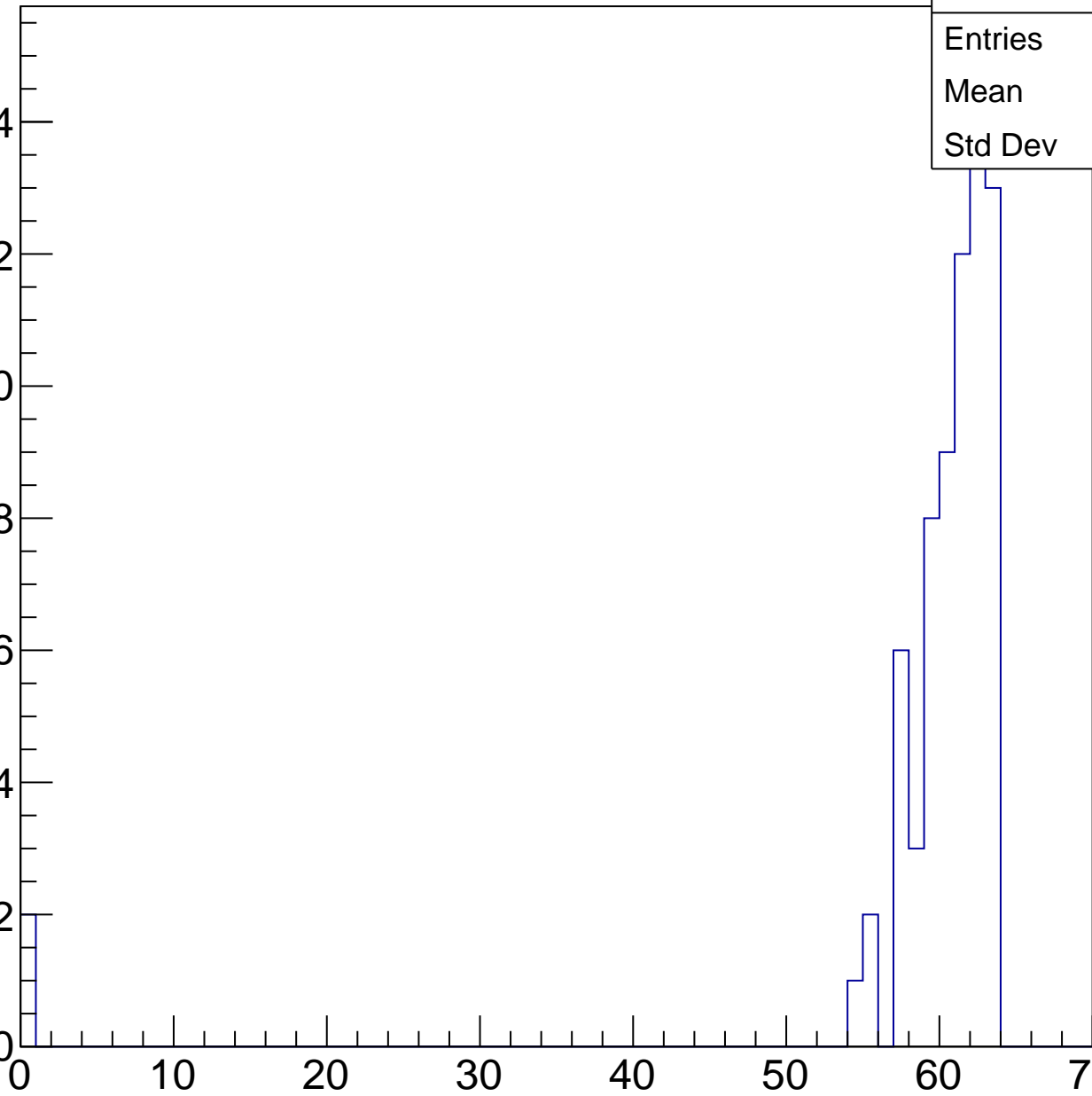
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.77
Std Dev	10.24

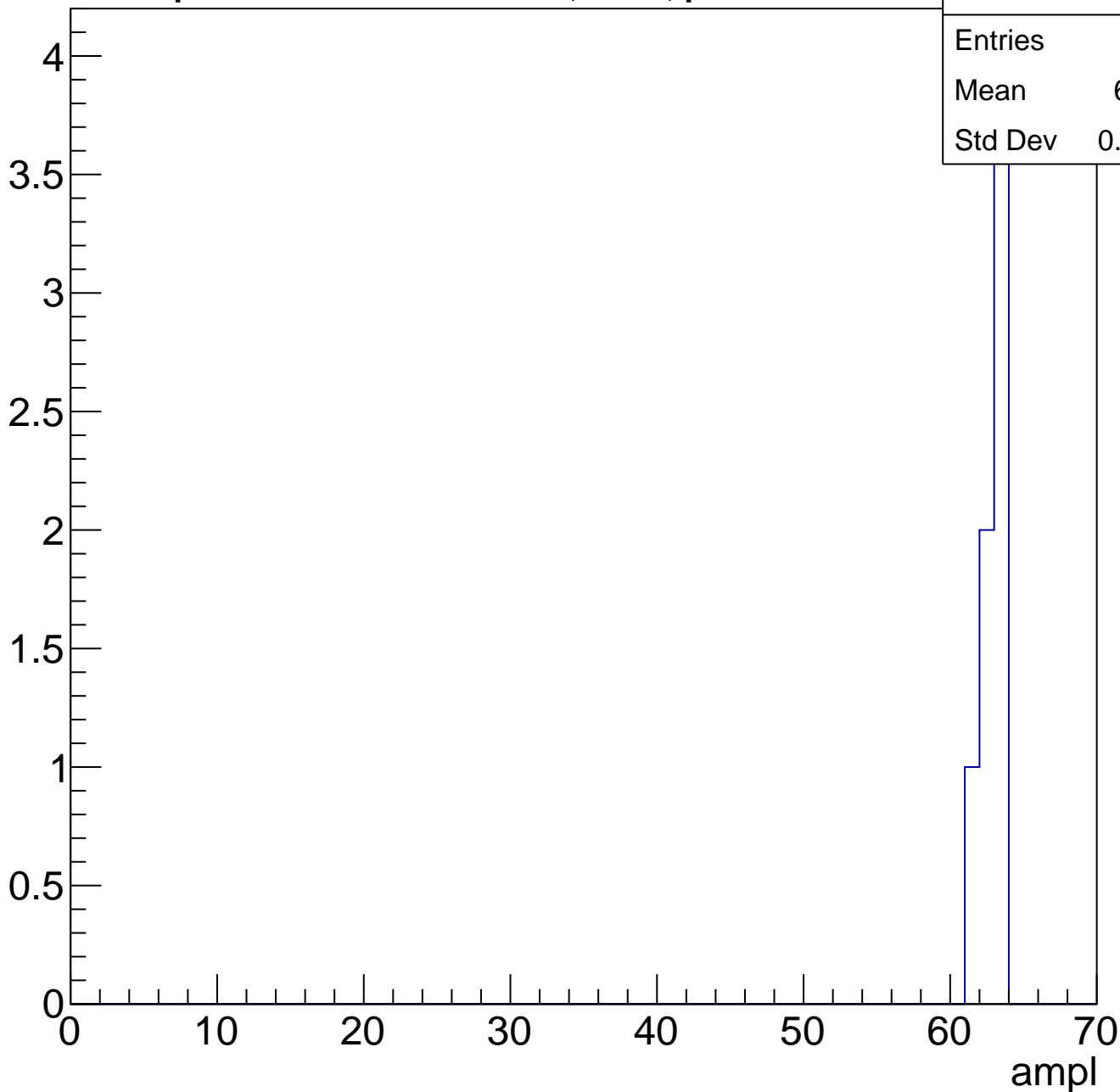
ampl



B0L101S, U24-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch44, adc0

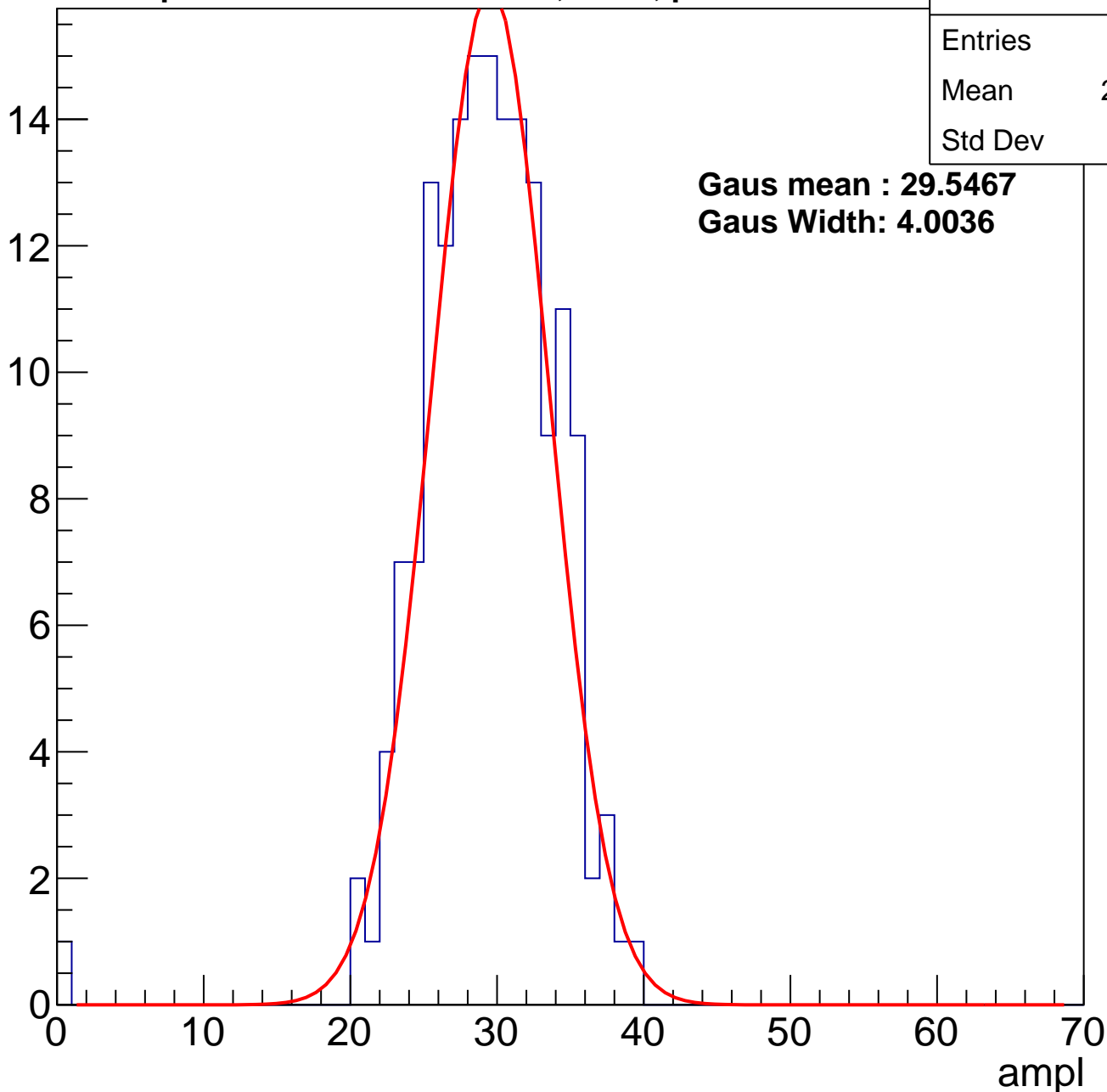
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	28.95
Std Dev	4.55

Gaus mean : 29.5467

Gaus Width: 4.0036

Entry



B0L101S, U24-ch44, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

154

Mean

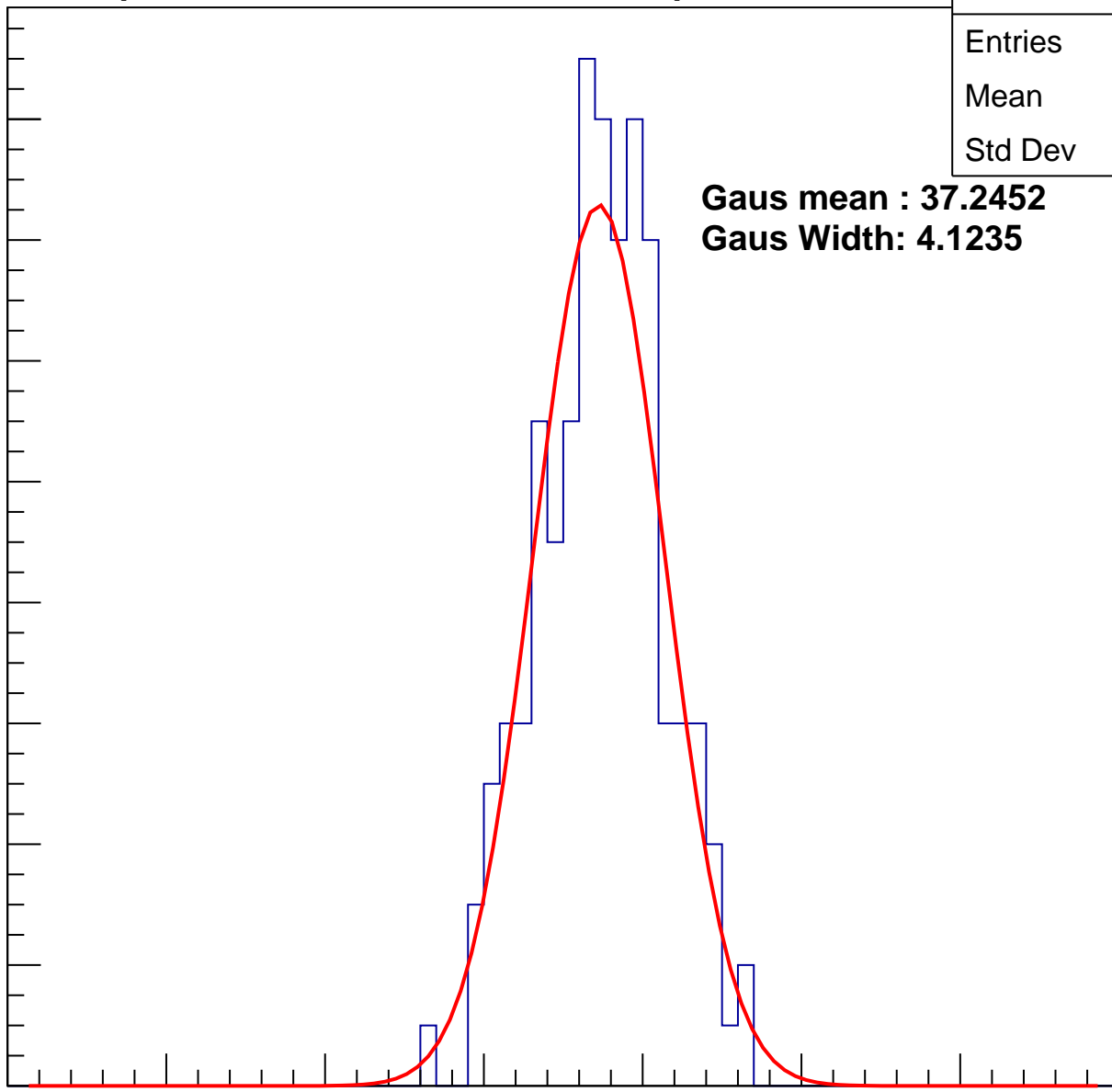
36.91

Std Dev

3.889

Gaus mean : 37.2452

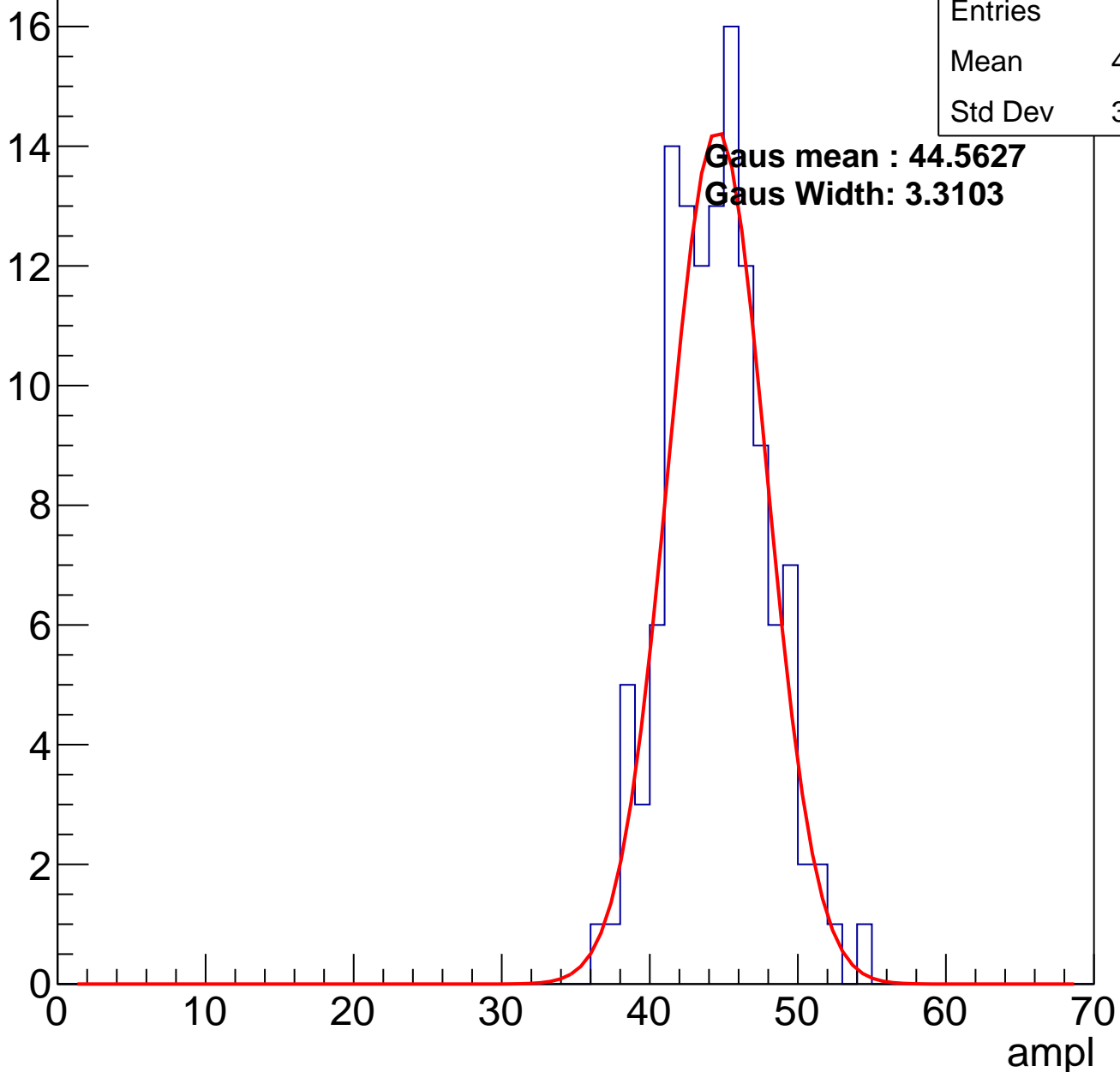
Gaus Width: 4.1235



B0L101S, U24-ch44, adc2

calib_packv5_042523_0143.root, FC#1, port C1

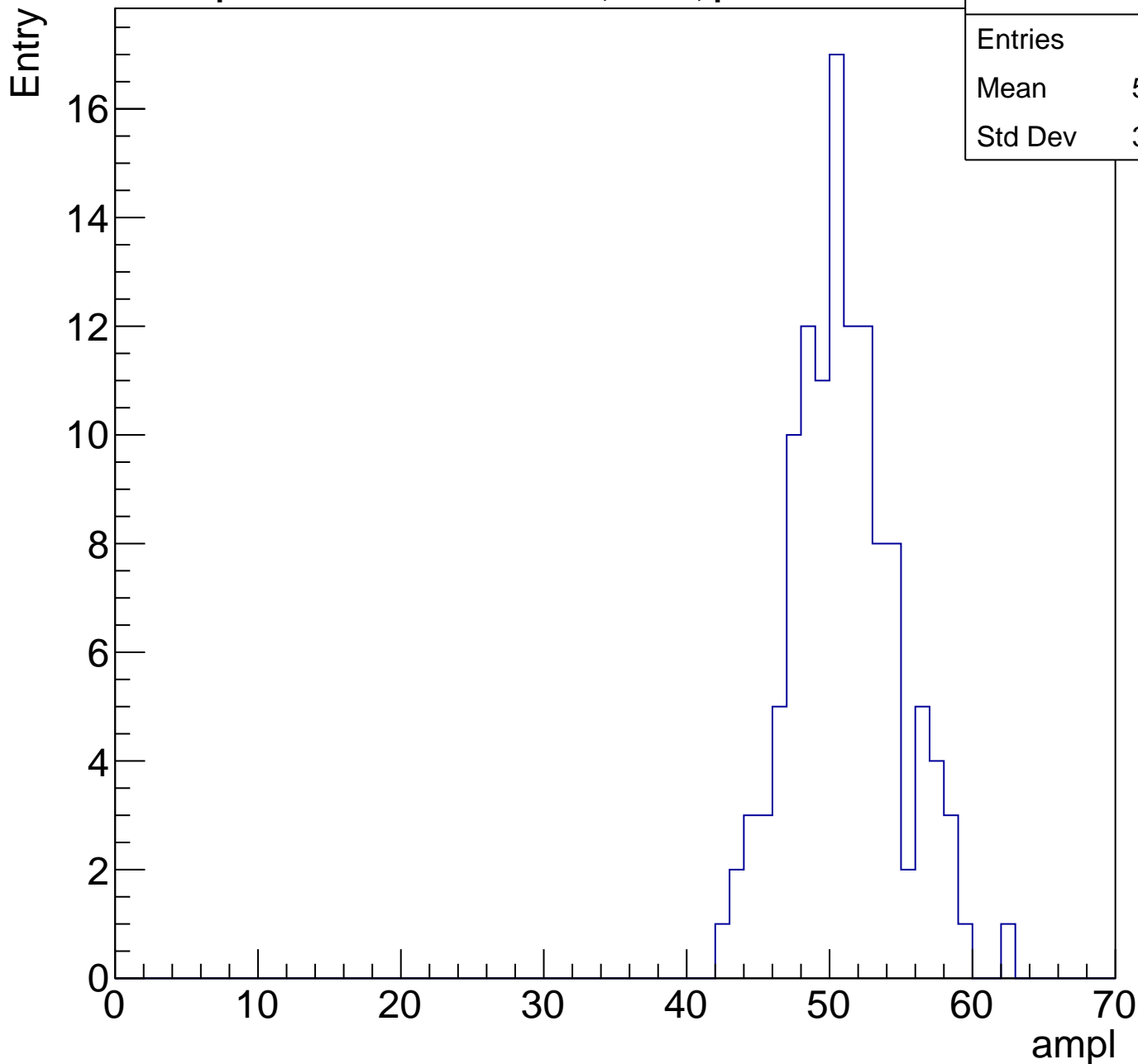
Entry



B0L101S, U24-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	50.54
Std Dev	3.699



B0L101S, U24-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

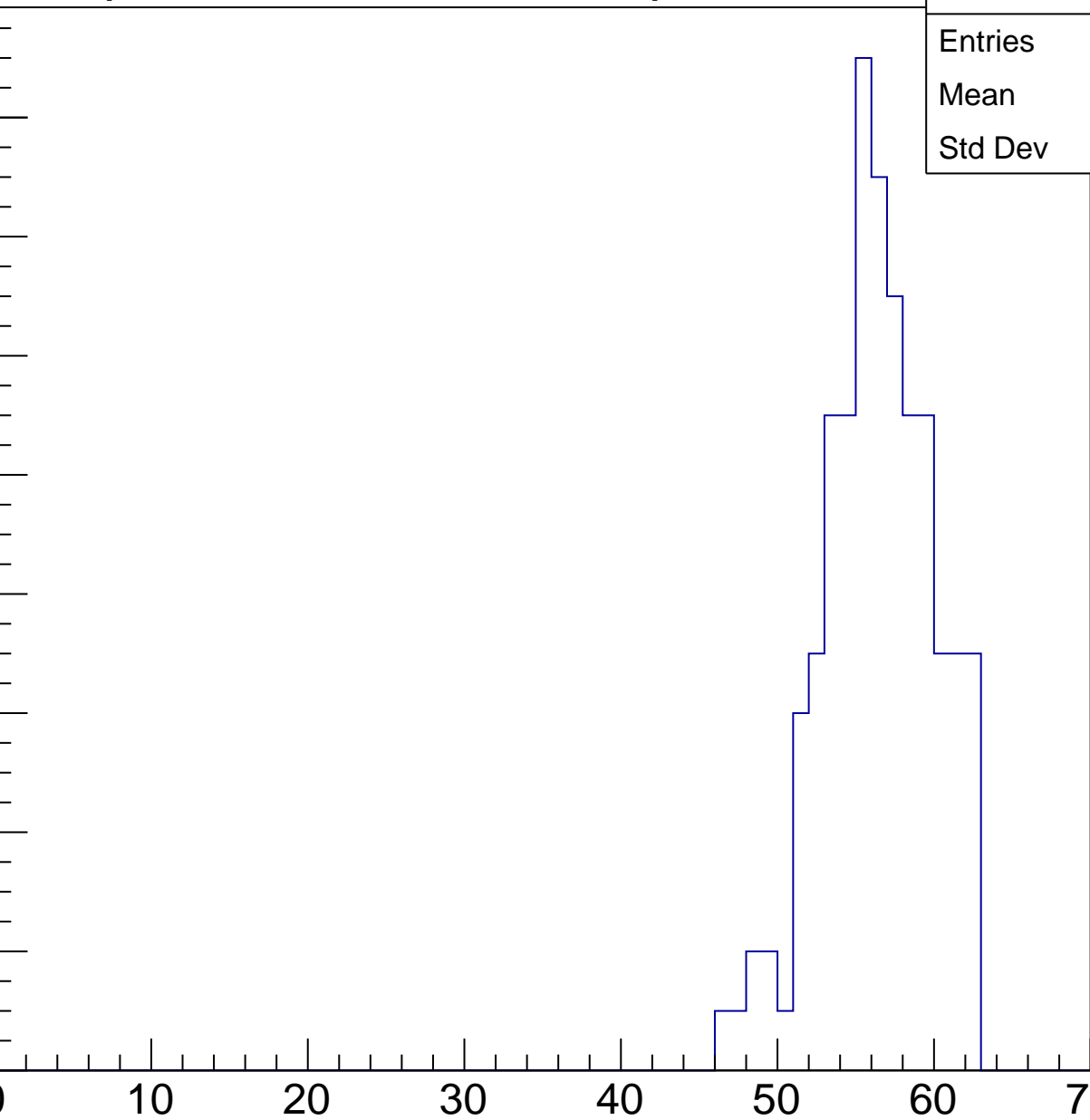
Entries	130
Mean	55.91
Std Dev	3.463

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch44, adc5

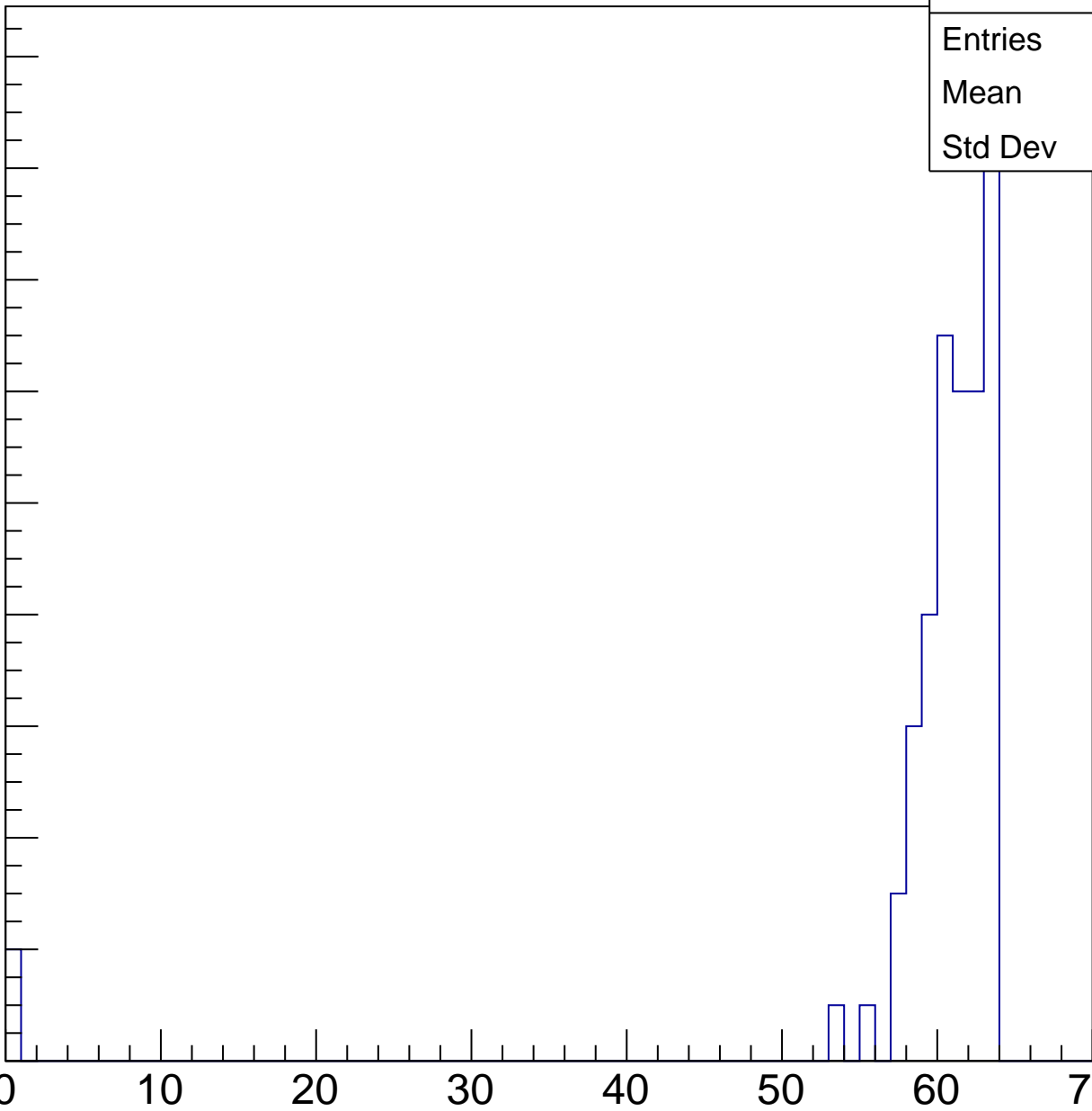
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.07
Std Dev	9.926

ampl



B0L101S, U24-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch45, adc0

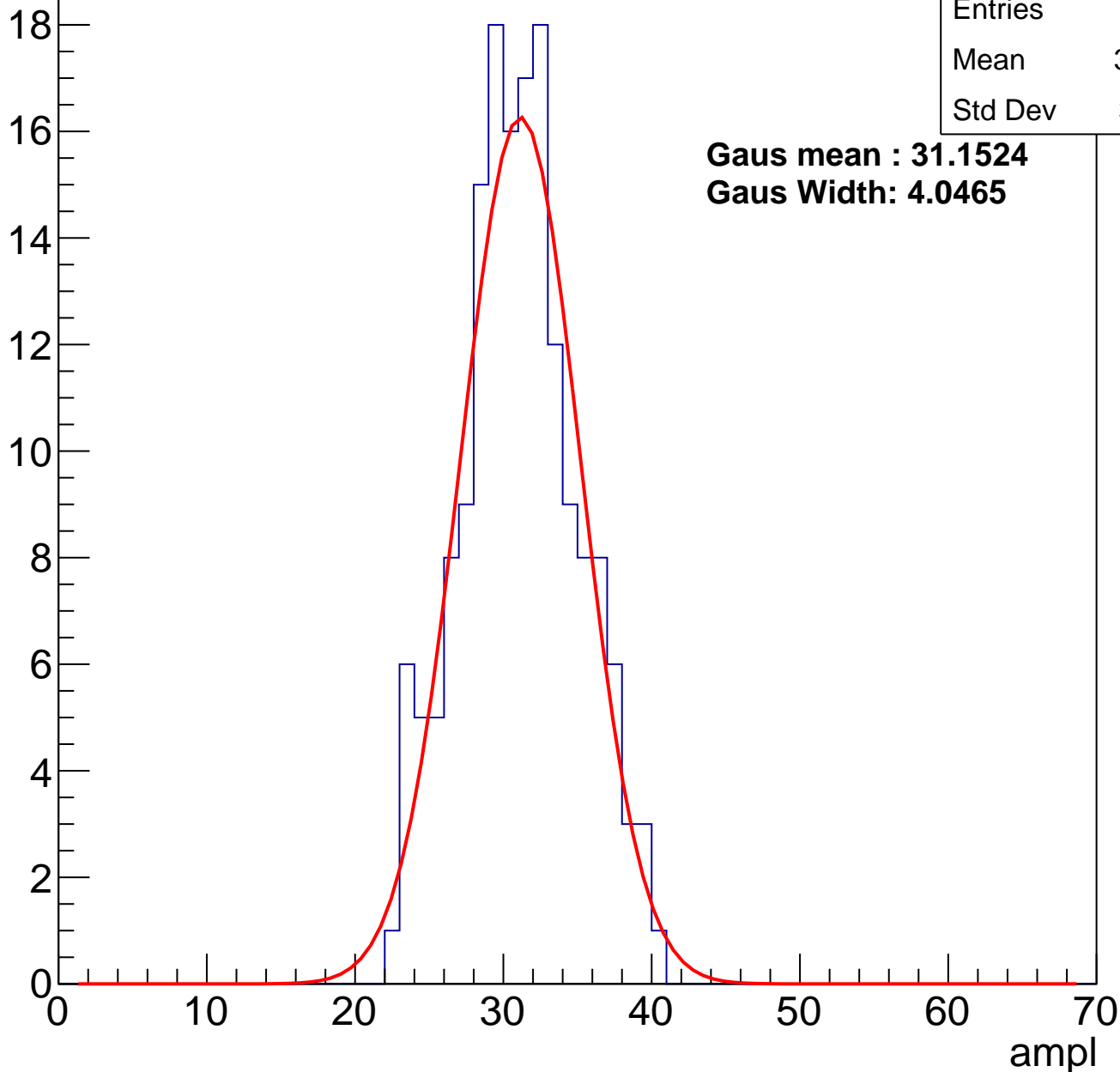
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	30.62
Std Dev	3.891

Gaus mean : 31.1524

Gaus Width: 4.0465

Entry

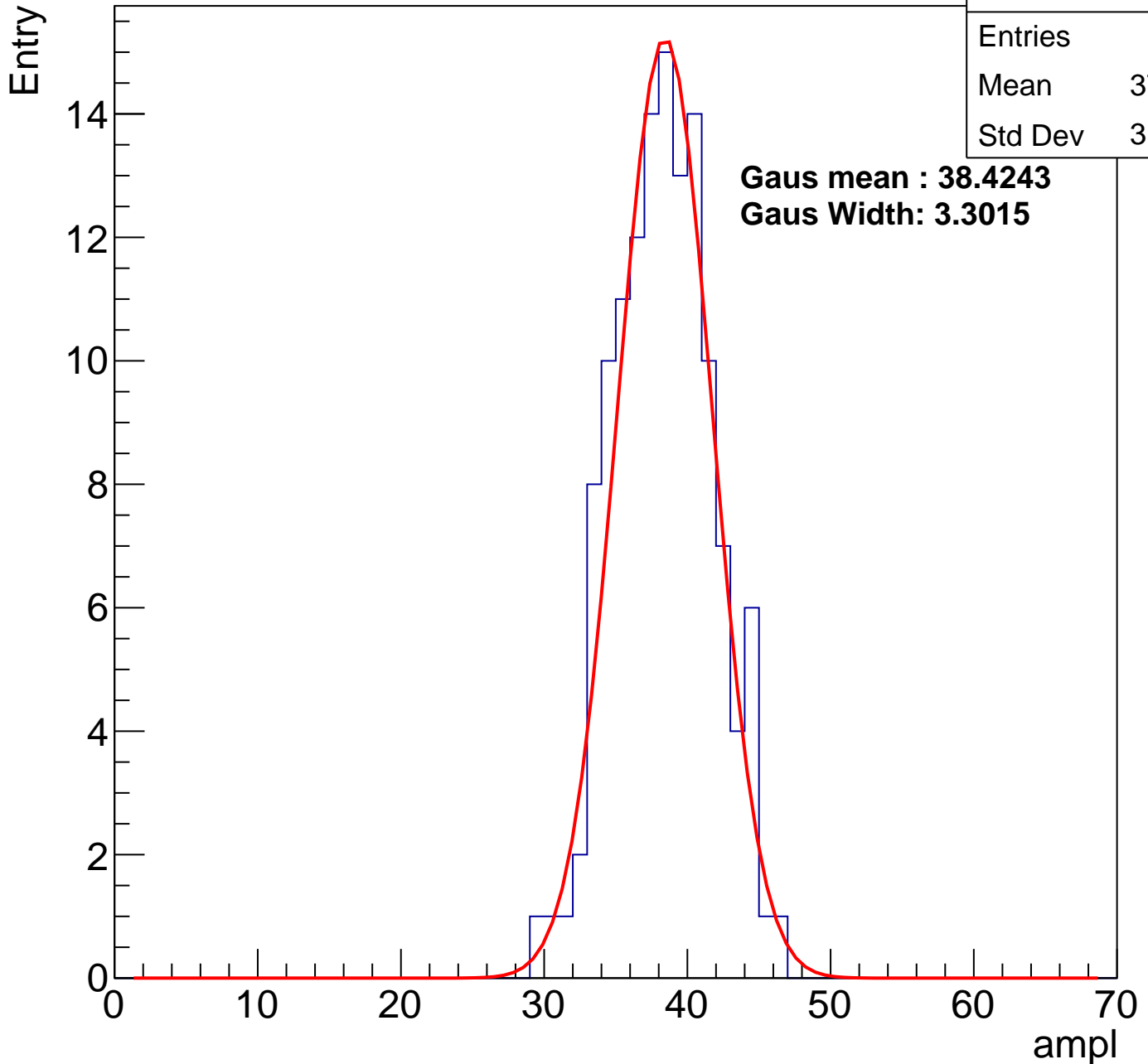


B0L101S, U24-ch45, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	37.87
Std Dev	3.367

Gaus mean : 38.4243
Gaus Width: 3.3015



B0L101S, U24-ch45, adc2

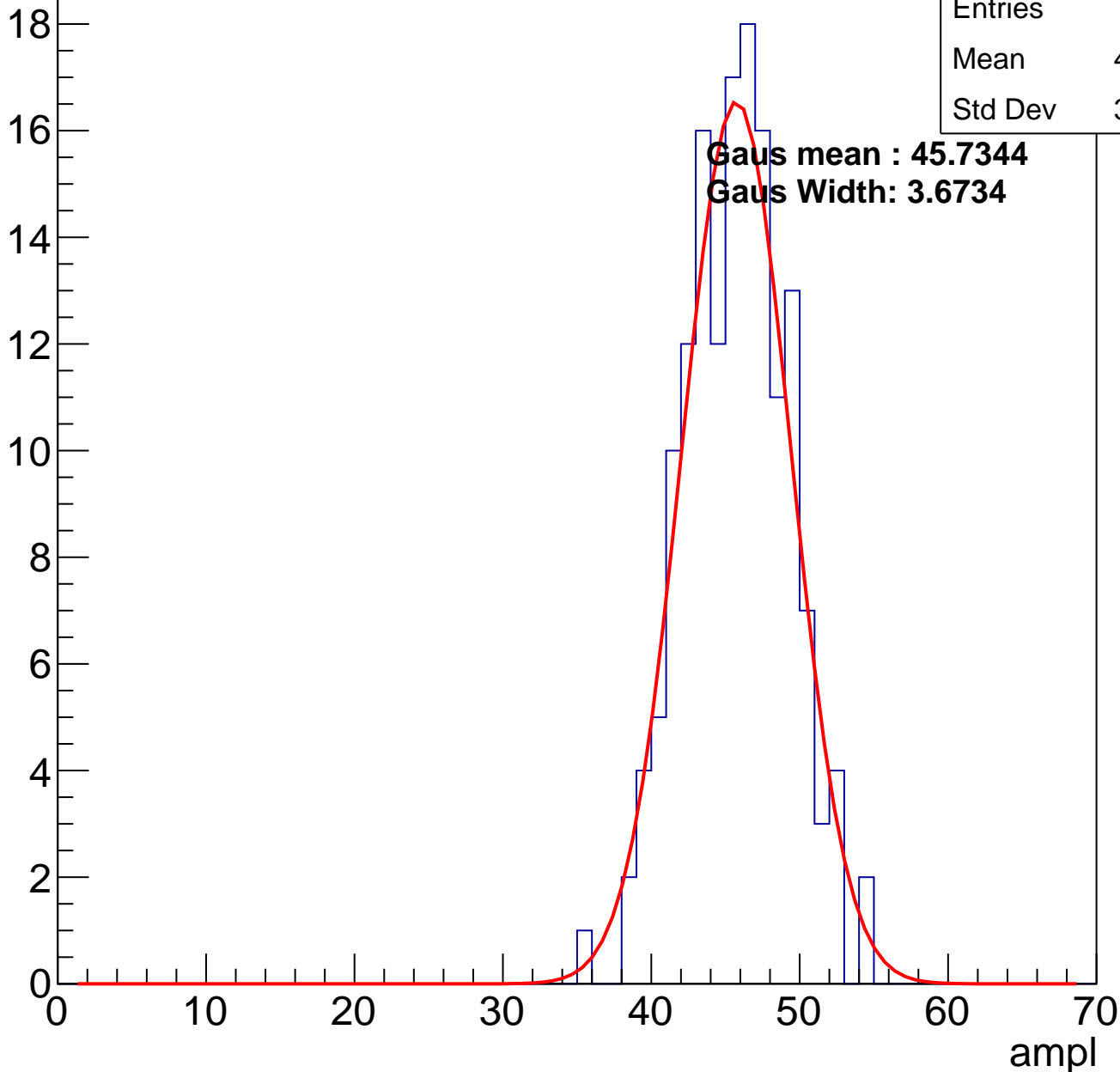
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	45.27
Std Dev	3.458

Gaus mean : 45.7344

Gaus Width: 3.6734

Entry



B0L101S, U24-ch45, adc3

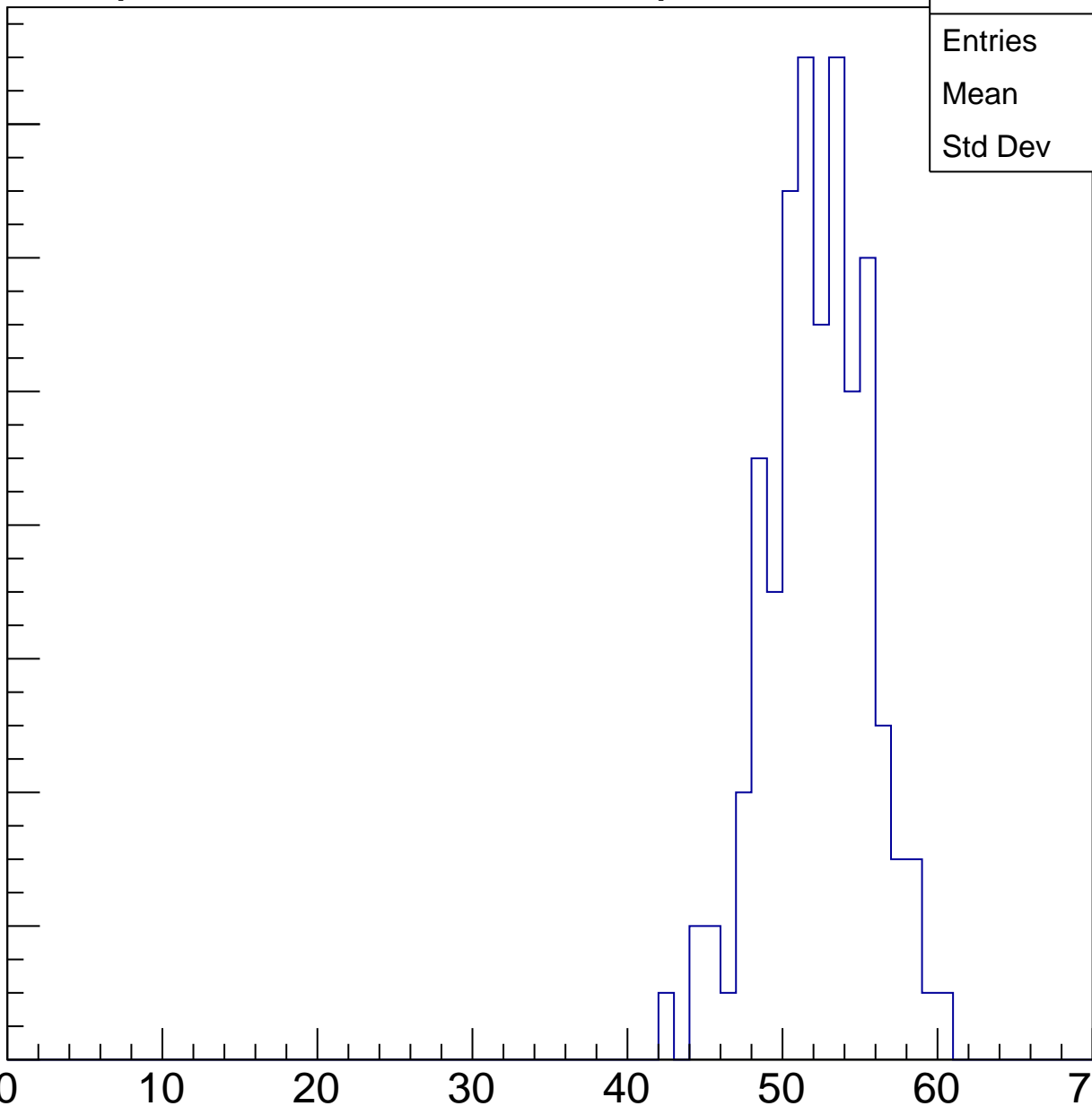
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	51.78
Std Dev	3.323

ampl



B0L101S, U24-ch45, adc4

calib_packv5_042523_0143.root, FC#1, port C1

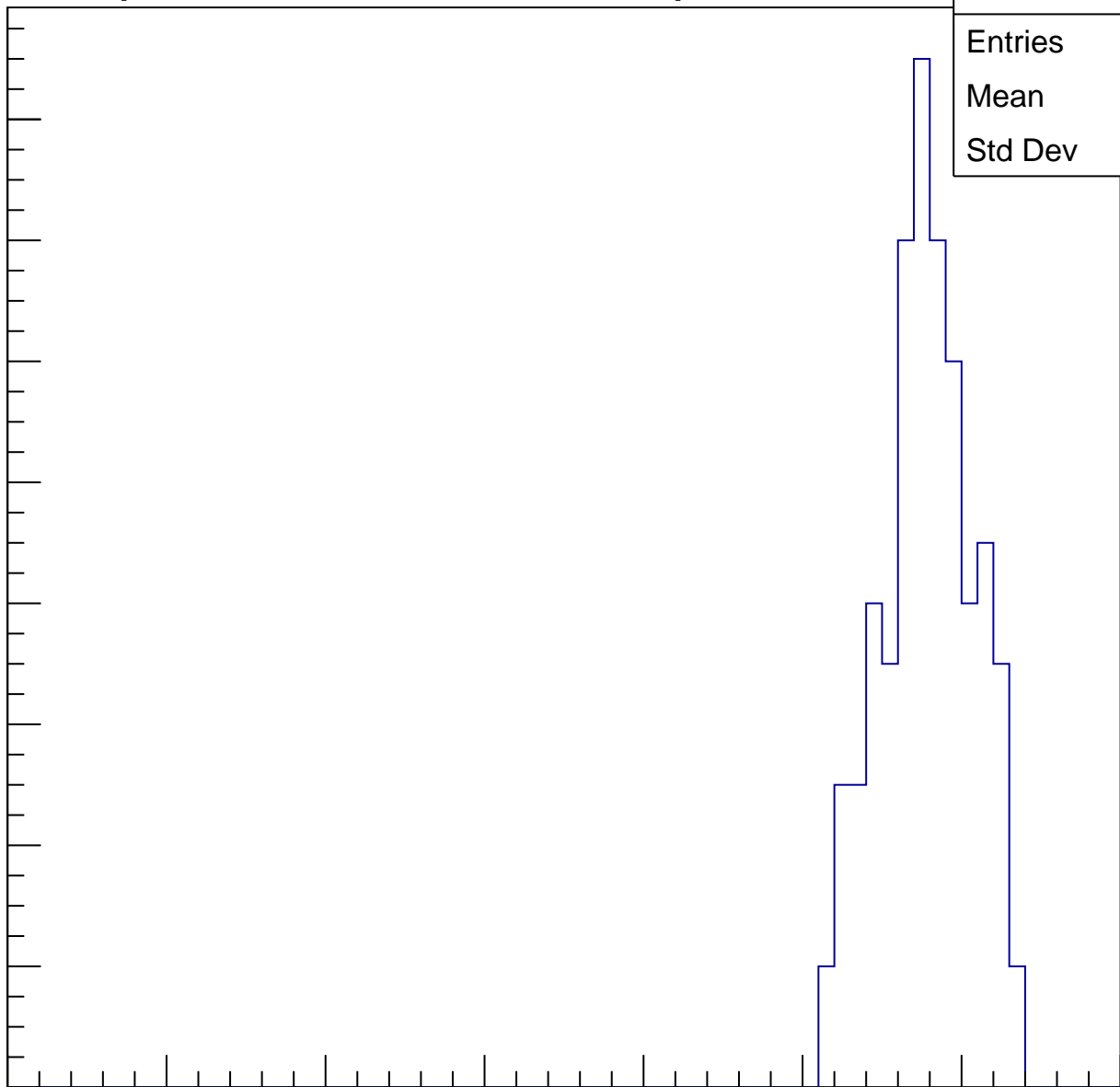
Entries	110
Mean	57.33
Std Dev	2.867

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch45, adc5

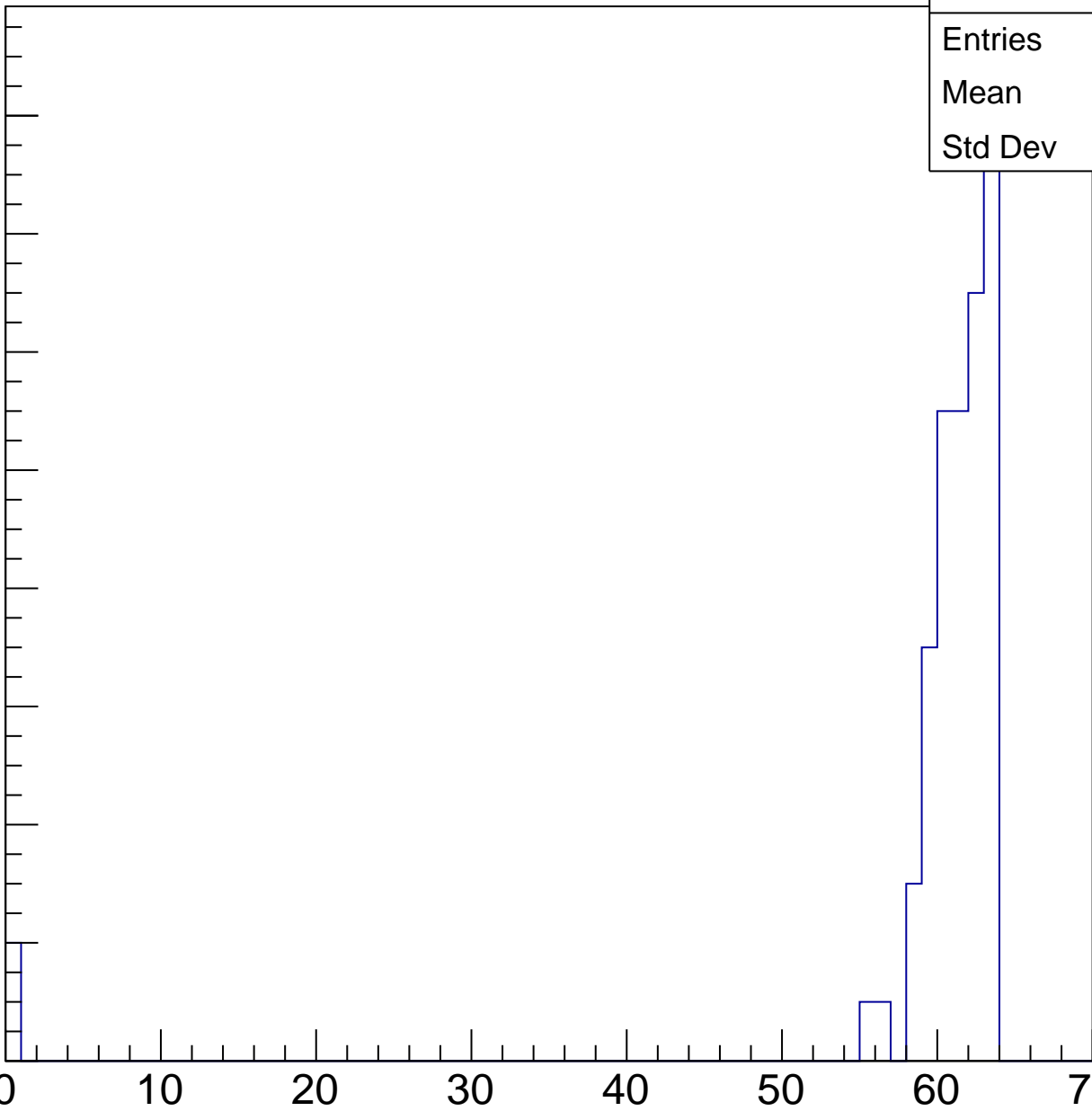
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	66
Mean	59.18
Std Dev	10.61

ampl



B0L101S, U24-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch46, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	28.32
Std Dev	4.234

Gaus mean : 29.3235

Gaus Width: 3.8613

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

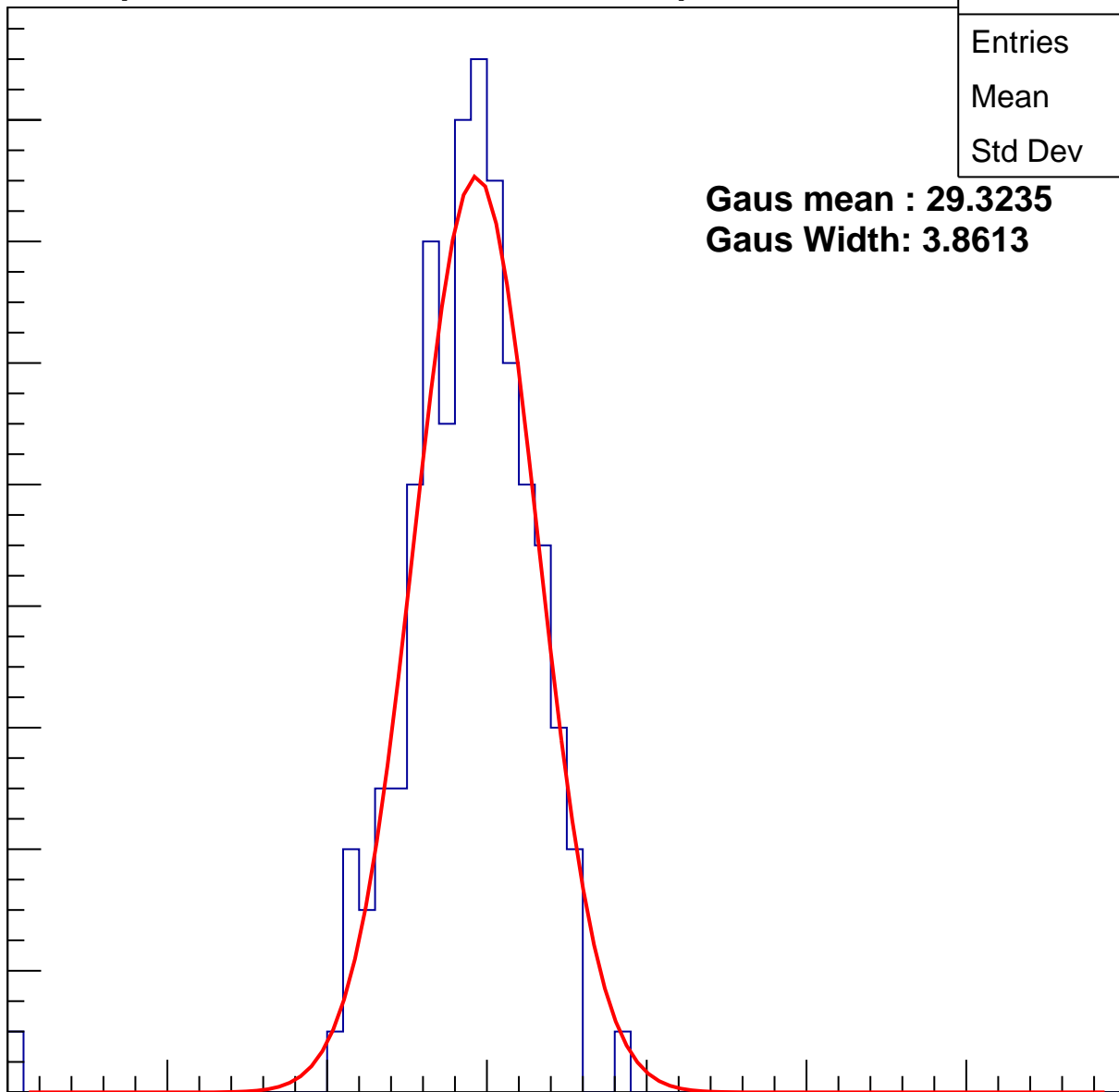
40

50

60

70

ampl



B0L101S, U24-ch46, adc1

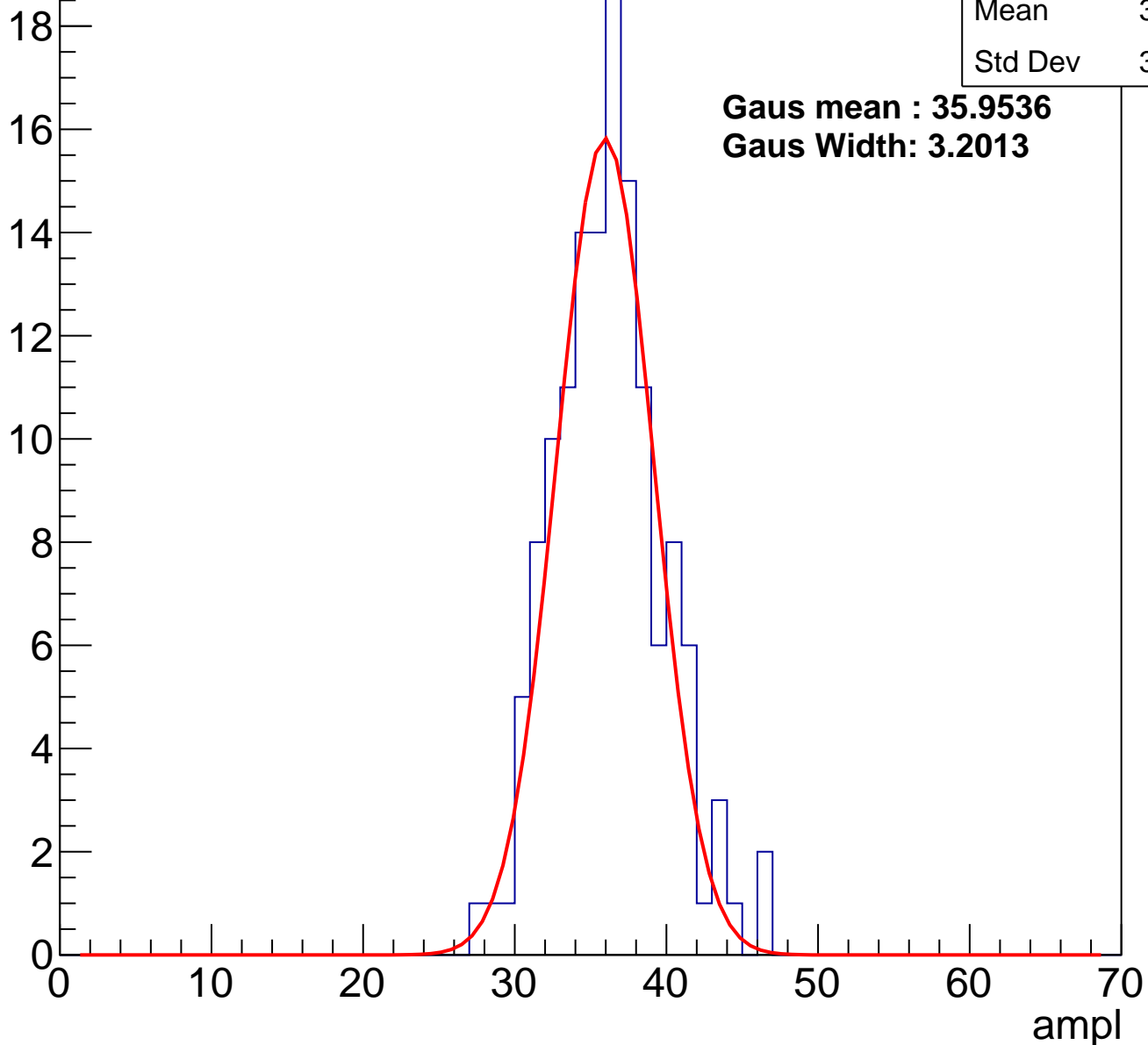
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	35.73
Std Dev	3.555

Gaus mean : 35.9536

Gaus Width: 3.2013

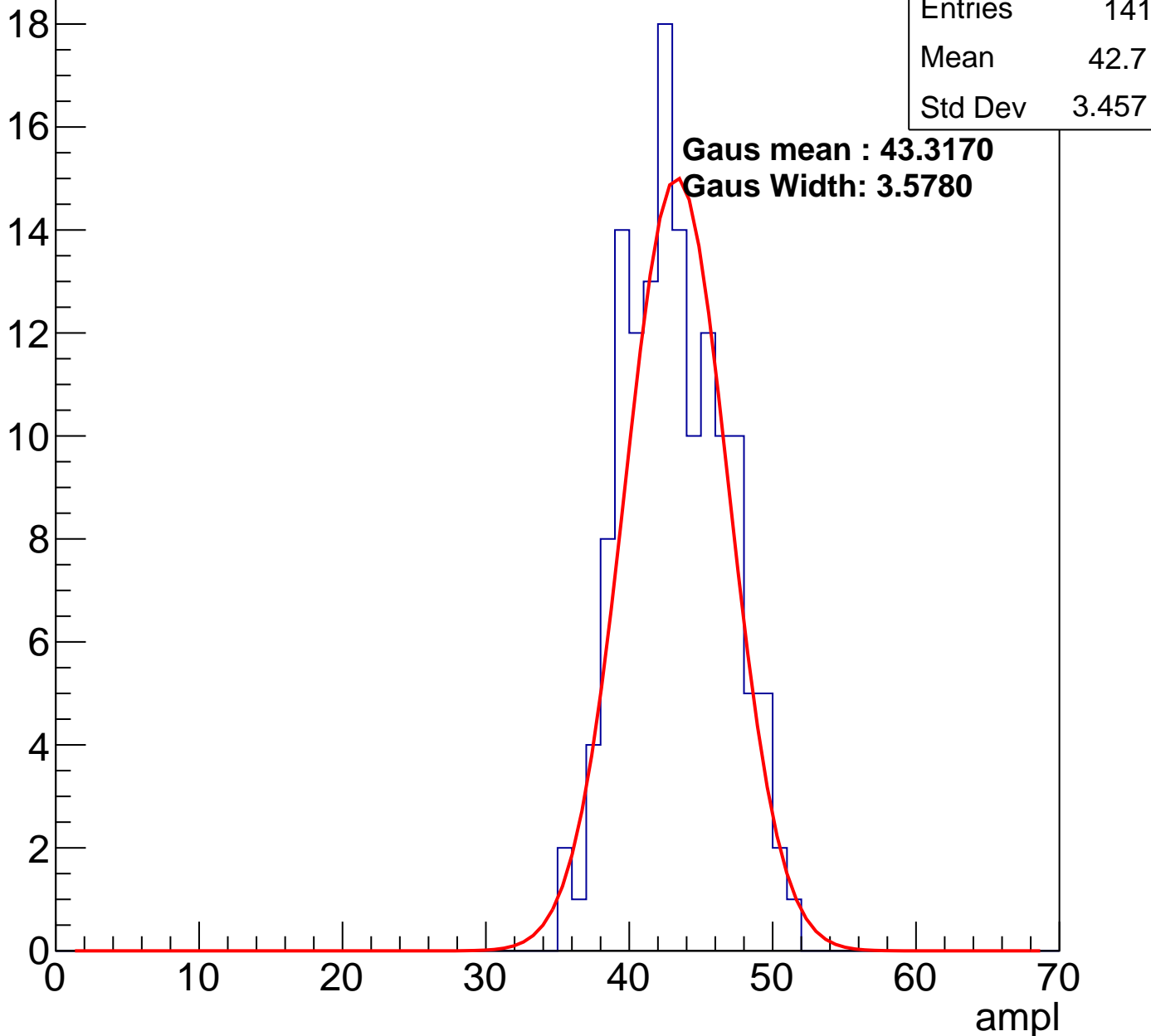
Entry



B0L101S, U24-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

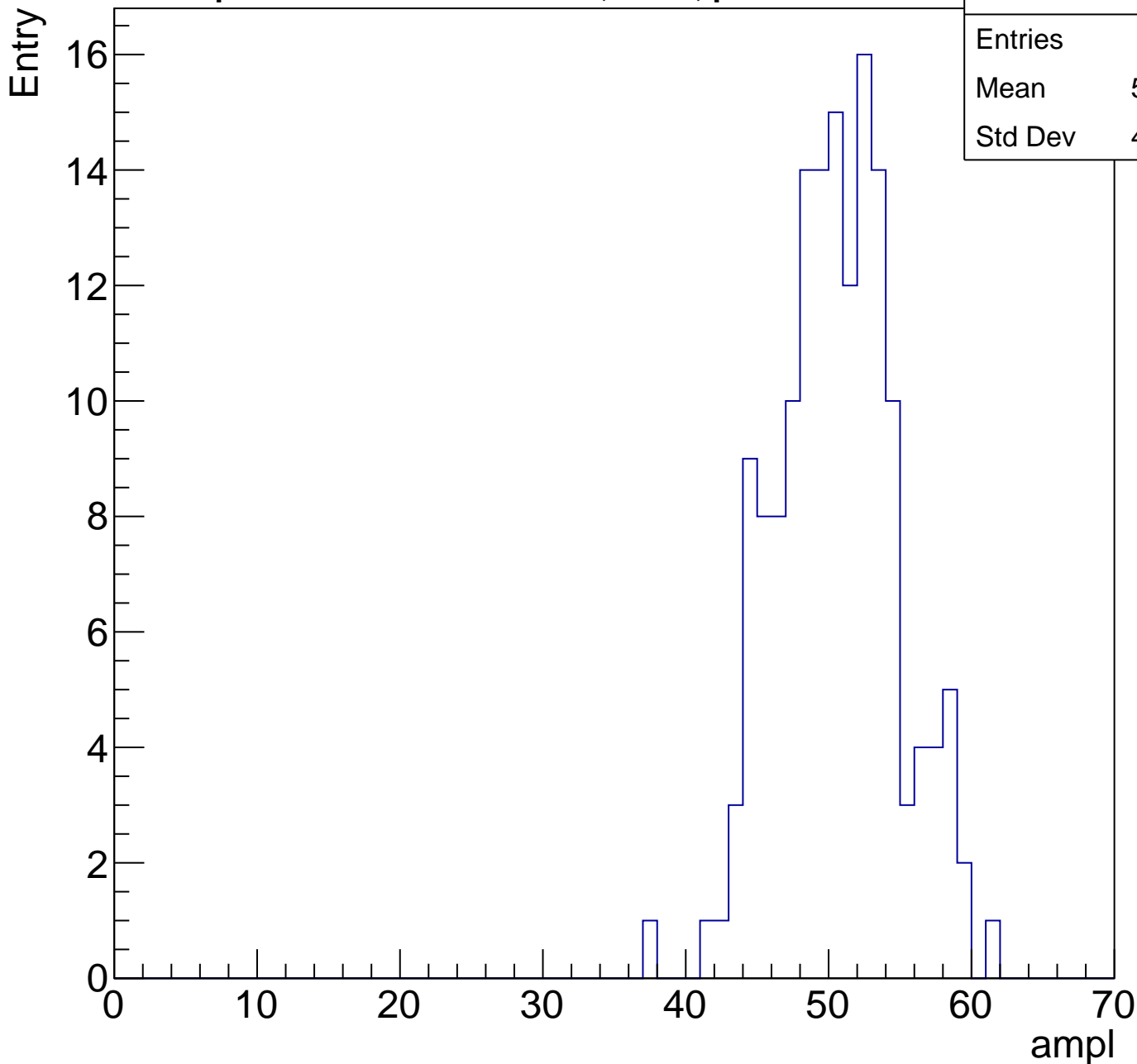
Entry



B0L101S, U24-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	50.08
Std Dev	4.136



B0L101S, U24-ch46, adc4

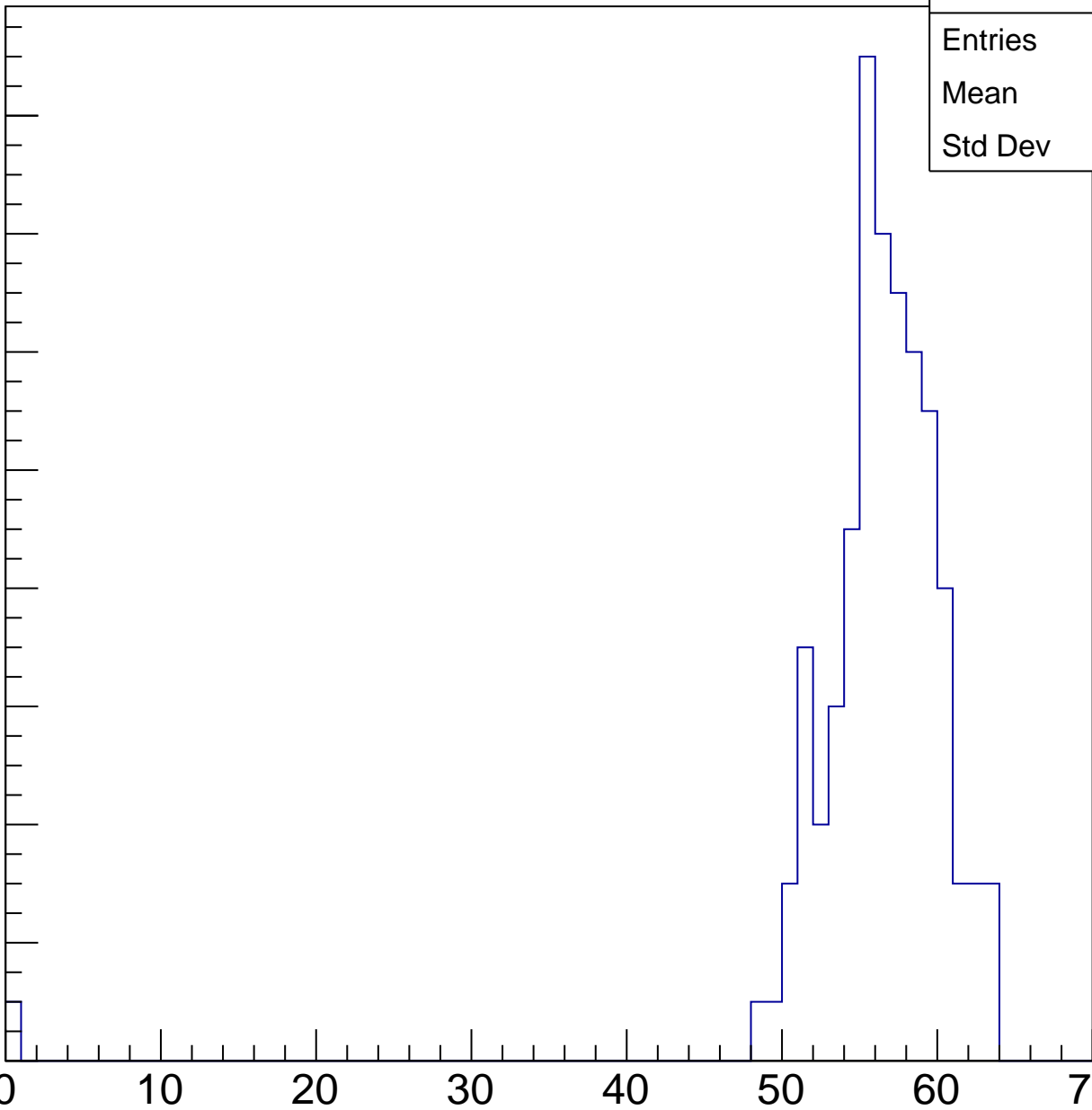
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	55.68
Std Dev	6.095

ampl



B0L101S, U24-ch46, adc5

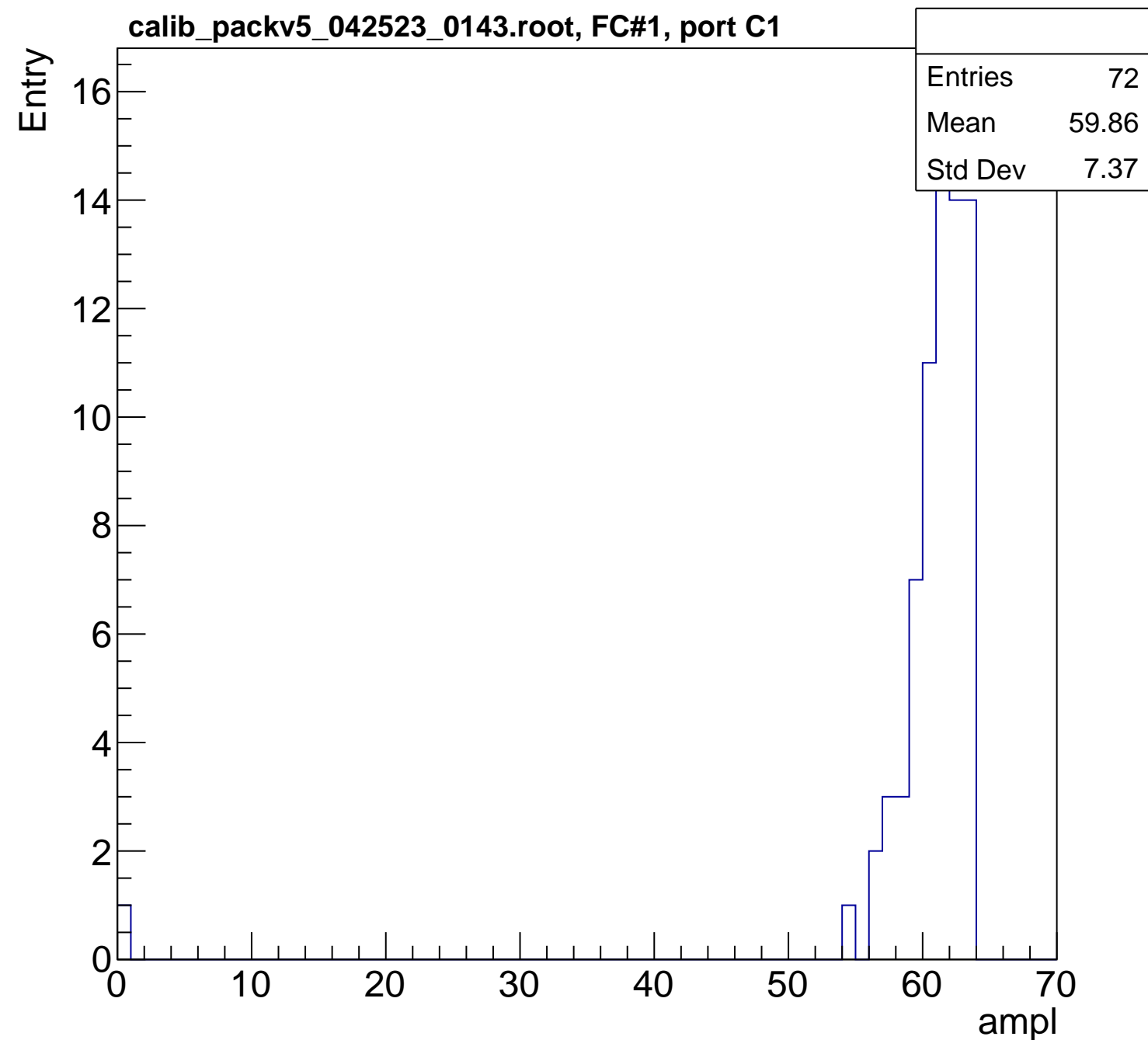
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	72
Mean	59.86
Std Dev	7.37

ampl



B0L101S, U24-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch47, adc0

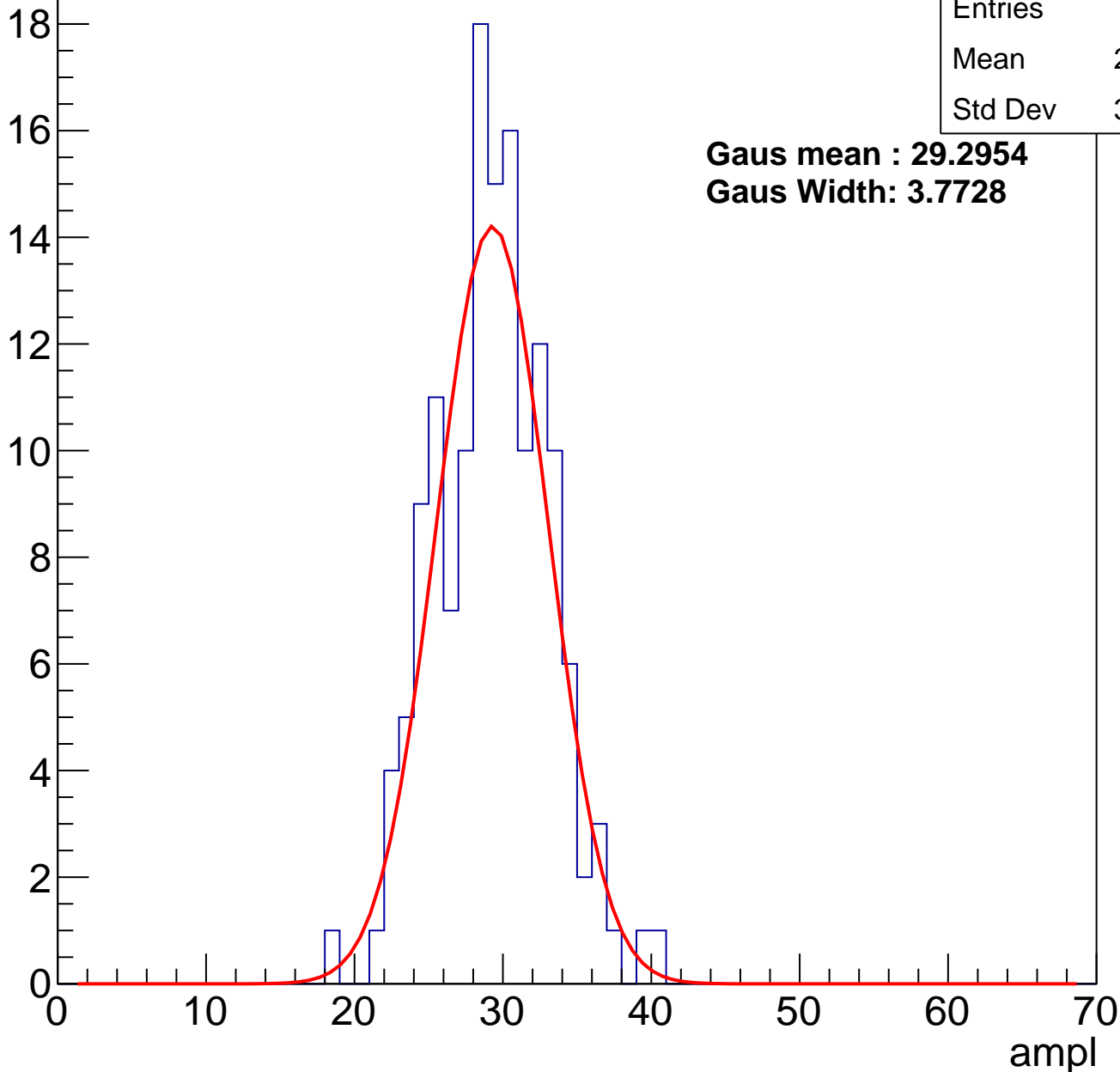
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	28.85
Std Dev	3.794

Gaus mean : 29.2954

Gaus Width: 3.7728

Entry



B0L101S, U24-ch47, adc1

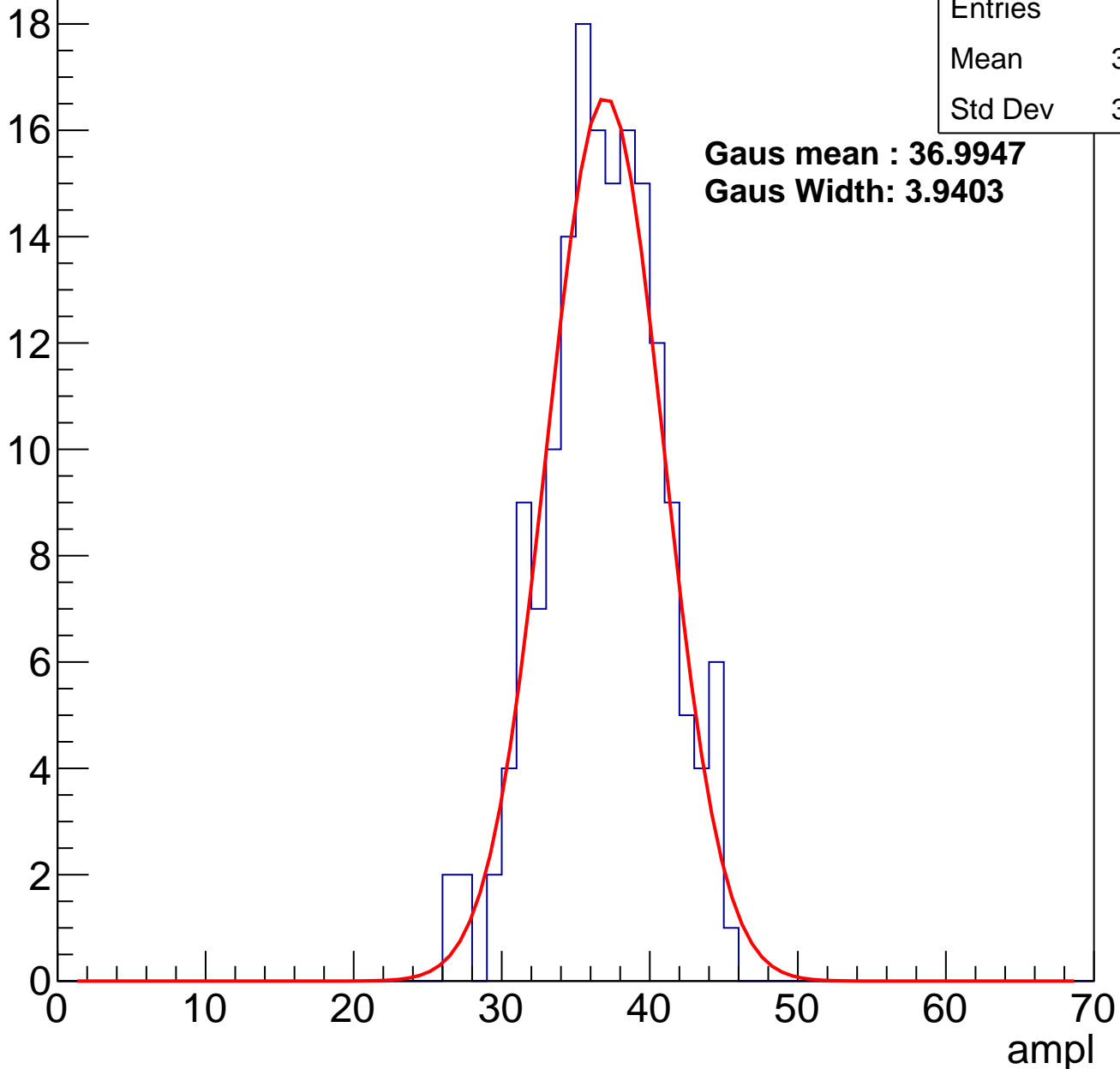
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	36.45
Std Dev	3.902

Gaus mean : 36.9947

Gaus Width: 3.9403

Entry



B0L101S, U24-ch47, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

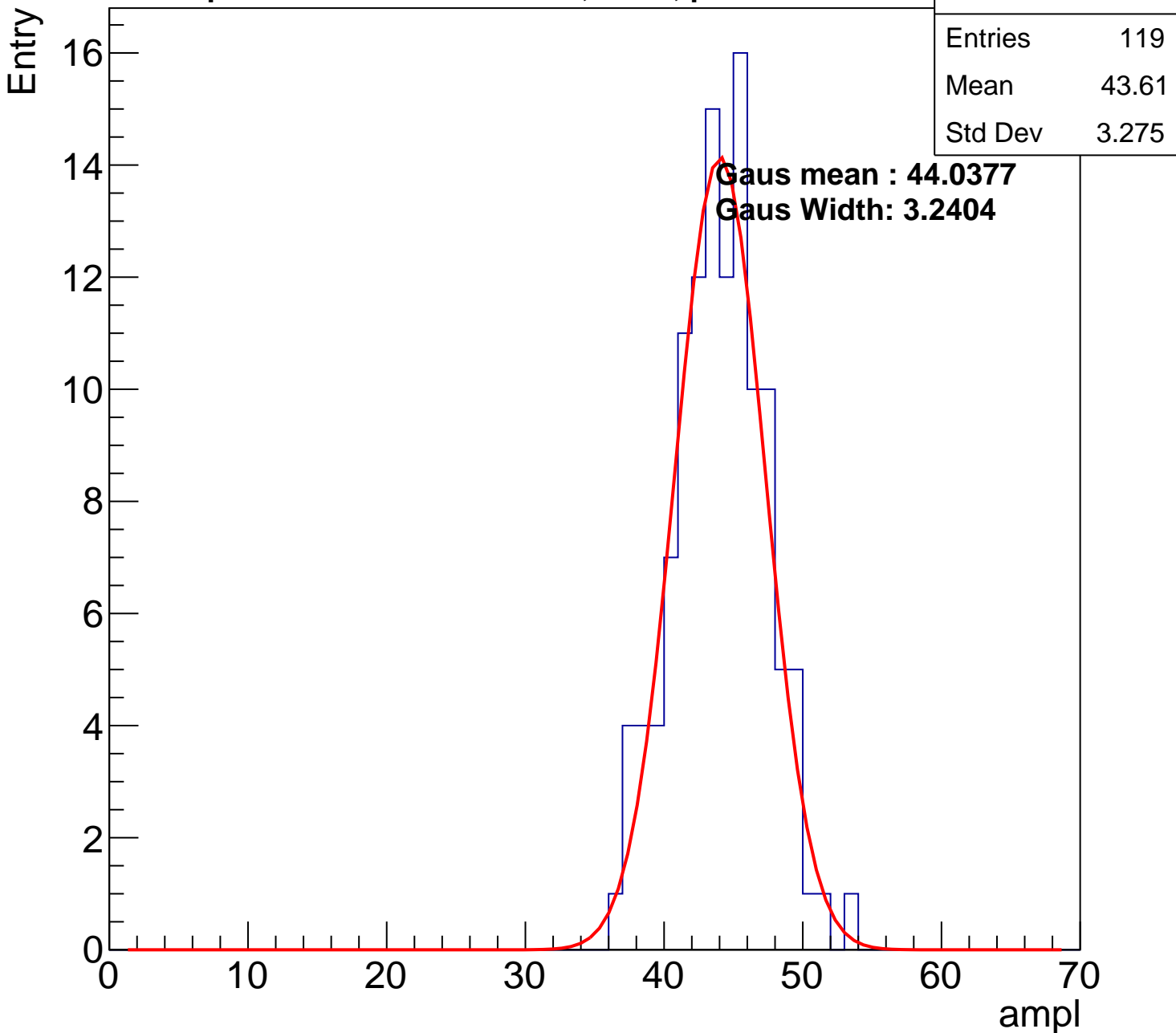
16
14
12
10
8
6
4
2
0

Entries	119
Mean	43.61
Std Dev	3.275

Gaus mean : 44.0377
Gaus Width: 3.2404

ampl

0 10 20 30 40 50 60 70

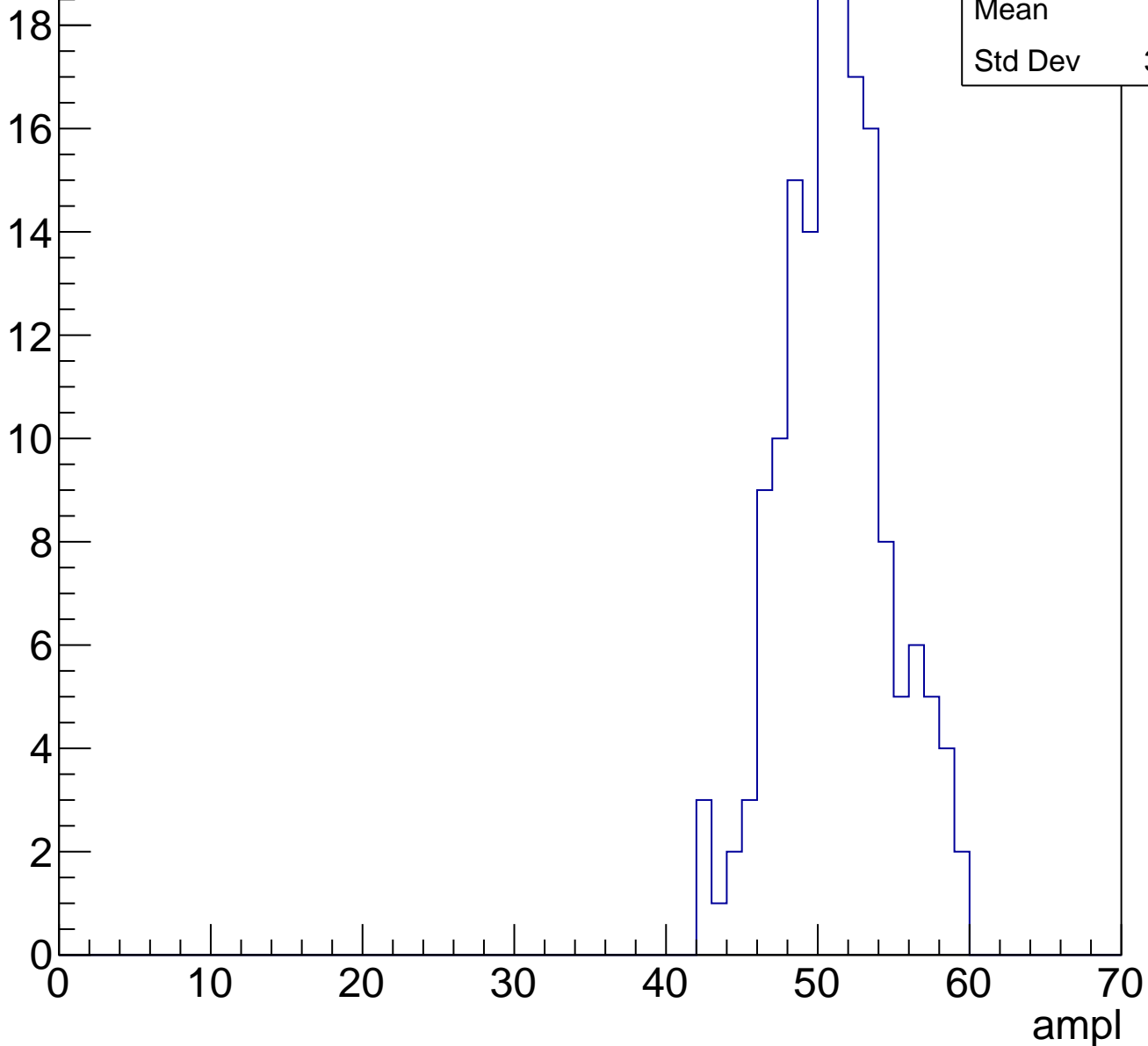


B0L101S, U24-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	50.7
Std Dev	3.541

Entry



B0L101S, U24-ch47, adc4

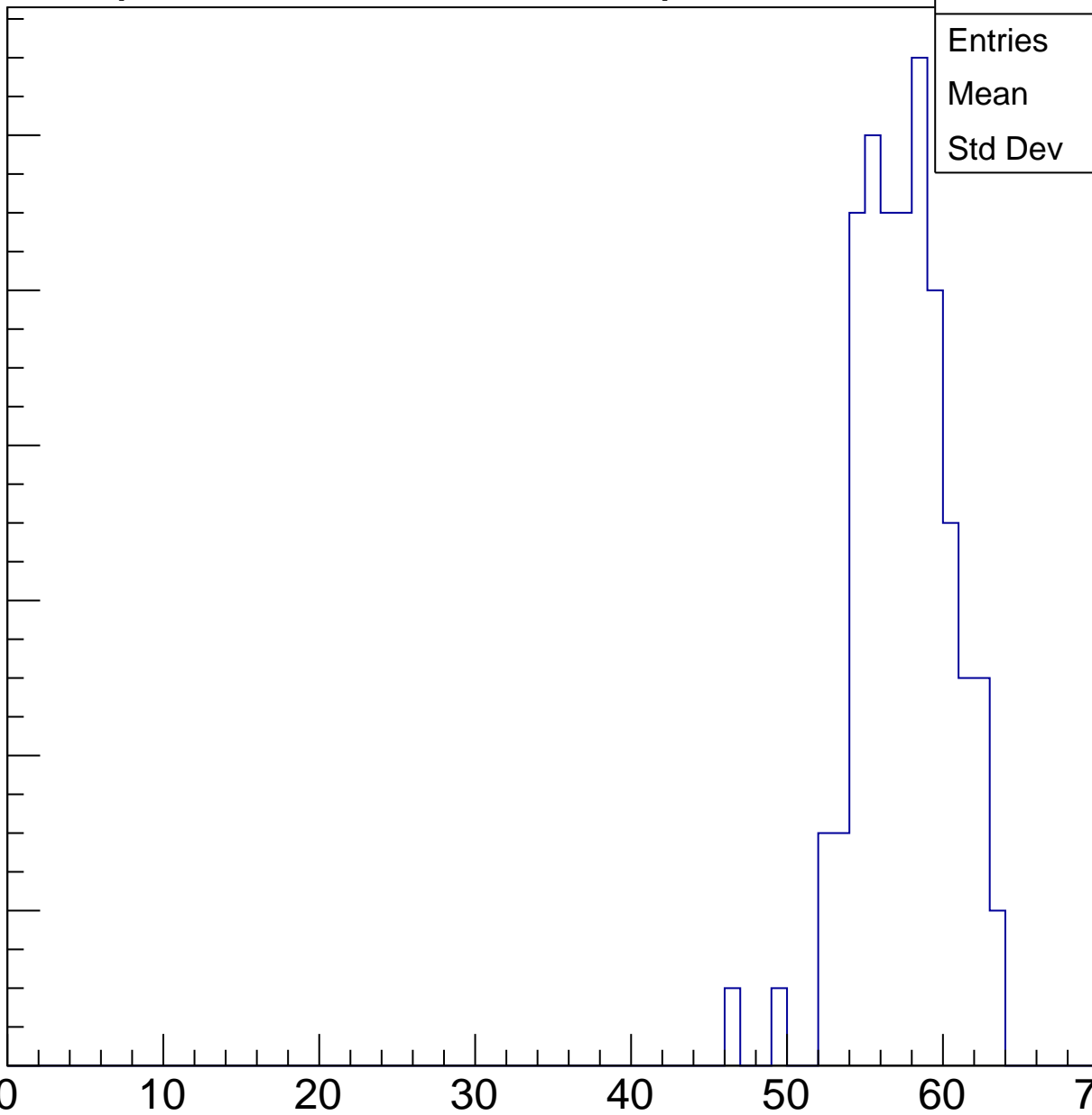
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	95
Mean	56.97
Std Dev	3.007

ampl



B0L101S, U24-ch47, adc5

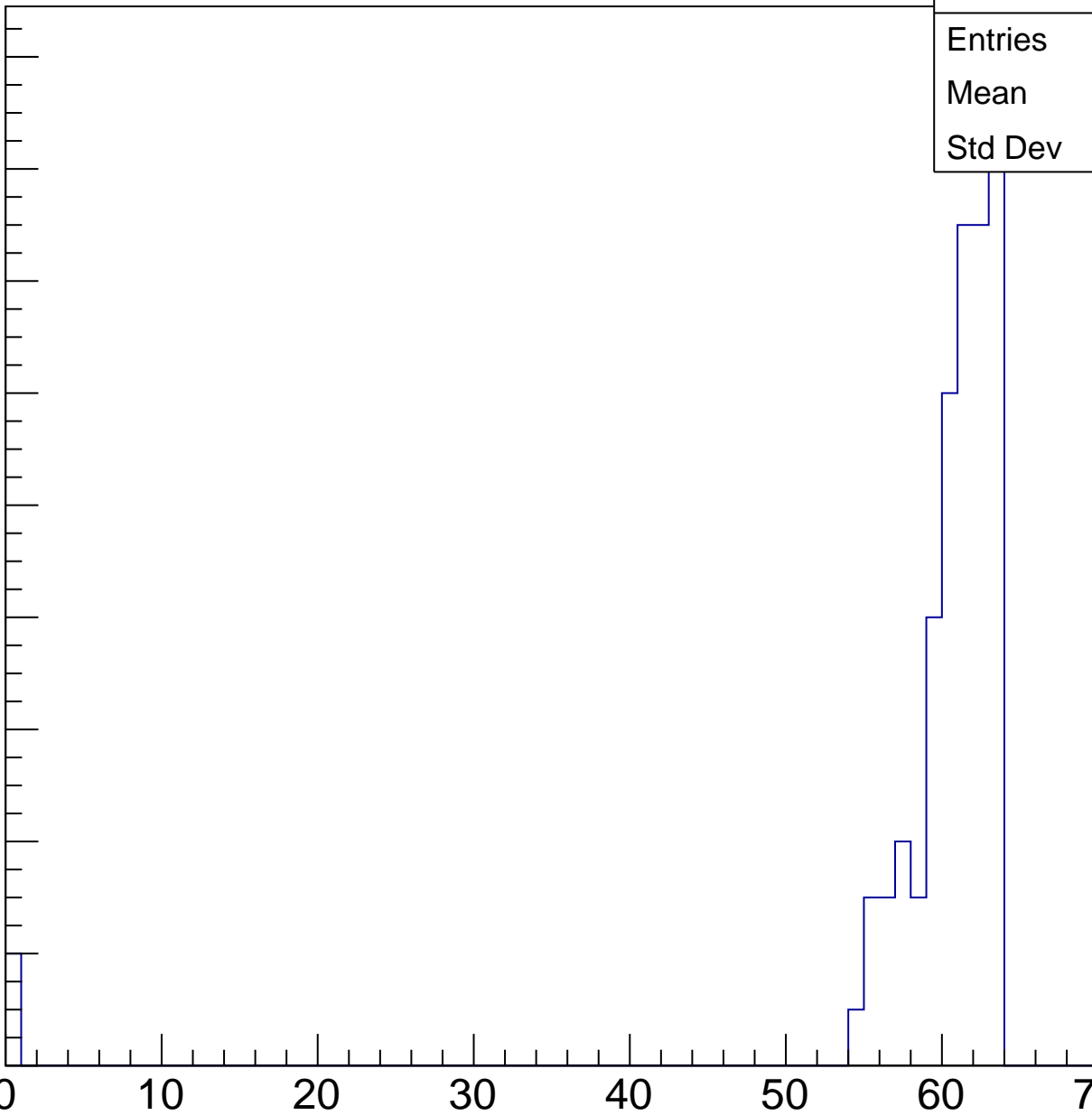
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.05
Std Dev	9.497

ampl



B0L101S, U24-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

60

Std Dev

0

B0L101S, U24-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch48, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	30.82
Std Dev	5.033

Gaus mean : 31.5563

Gaus Width: 3.8485

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

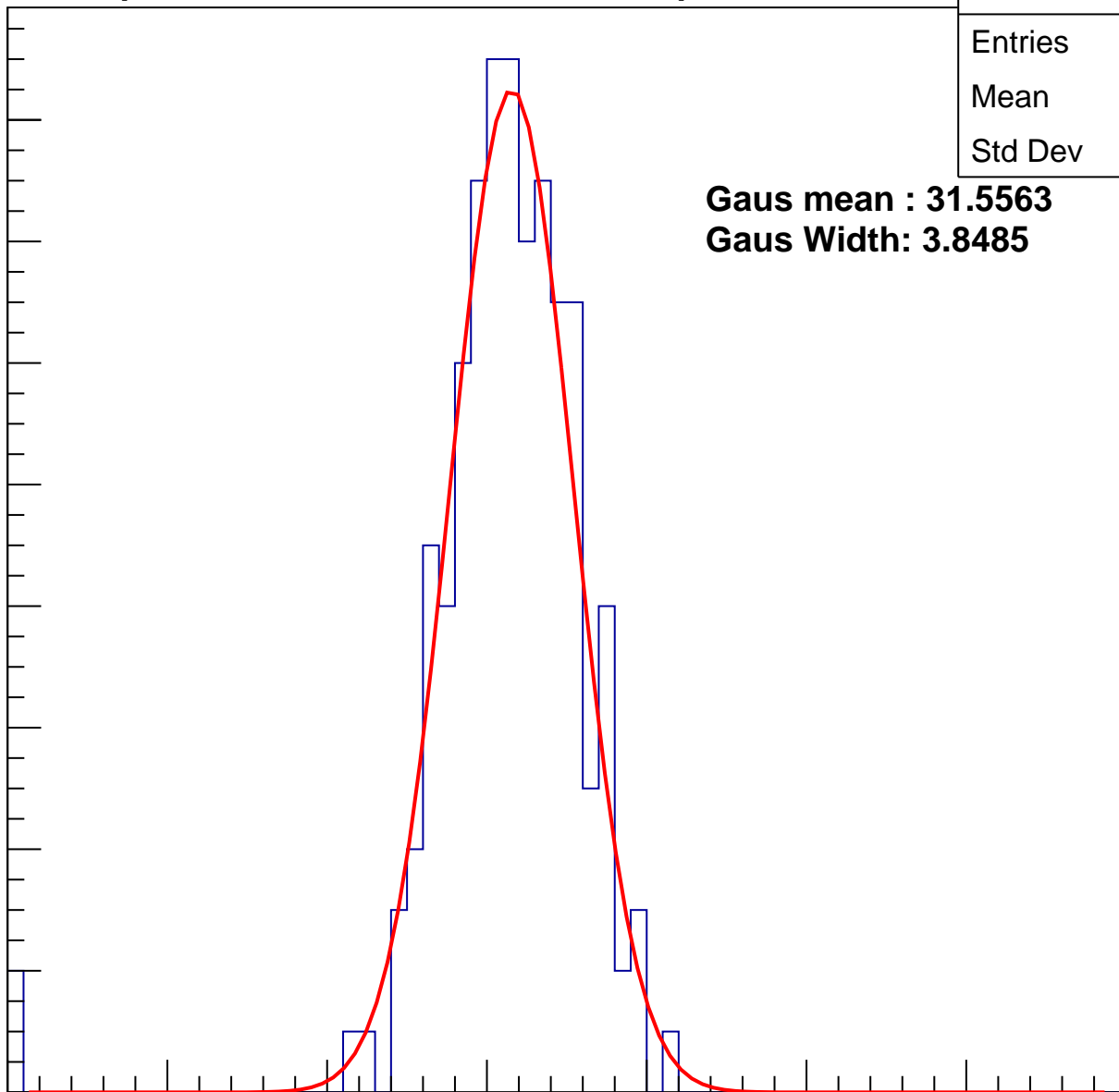
40

50

60

70

ampl



B0L101S, U24-ch48, adc1

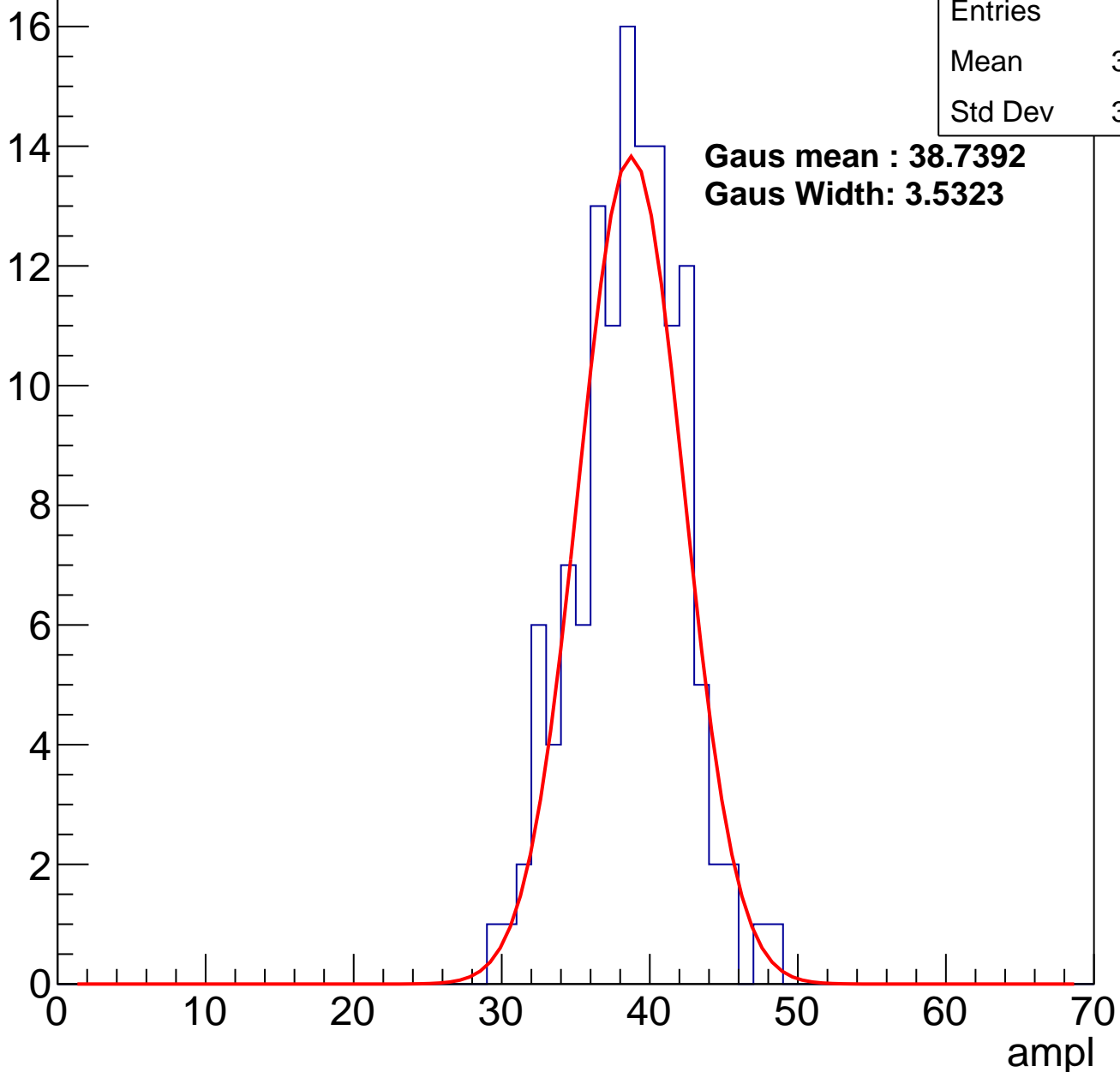
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	38.18
Std Dev	3.545

Gaus mean : 38.7392

Gaus Width: 3.5323

Entry



B0L101S, U24-ch48, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

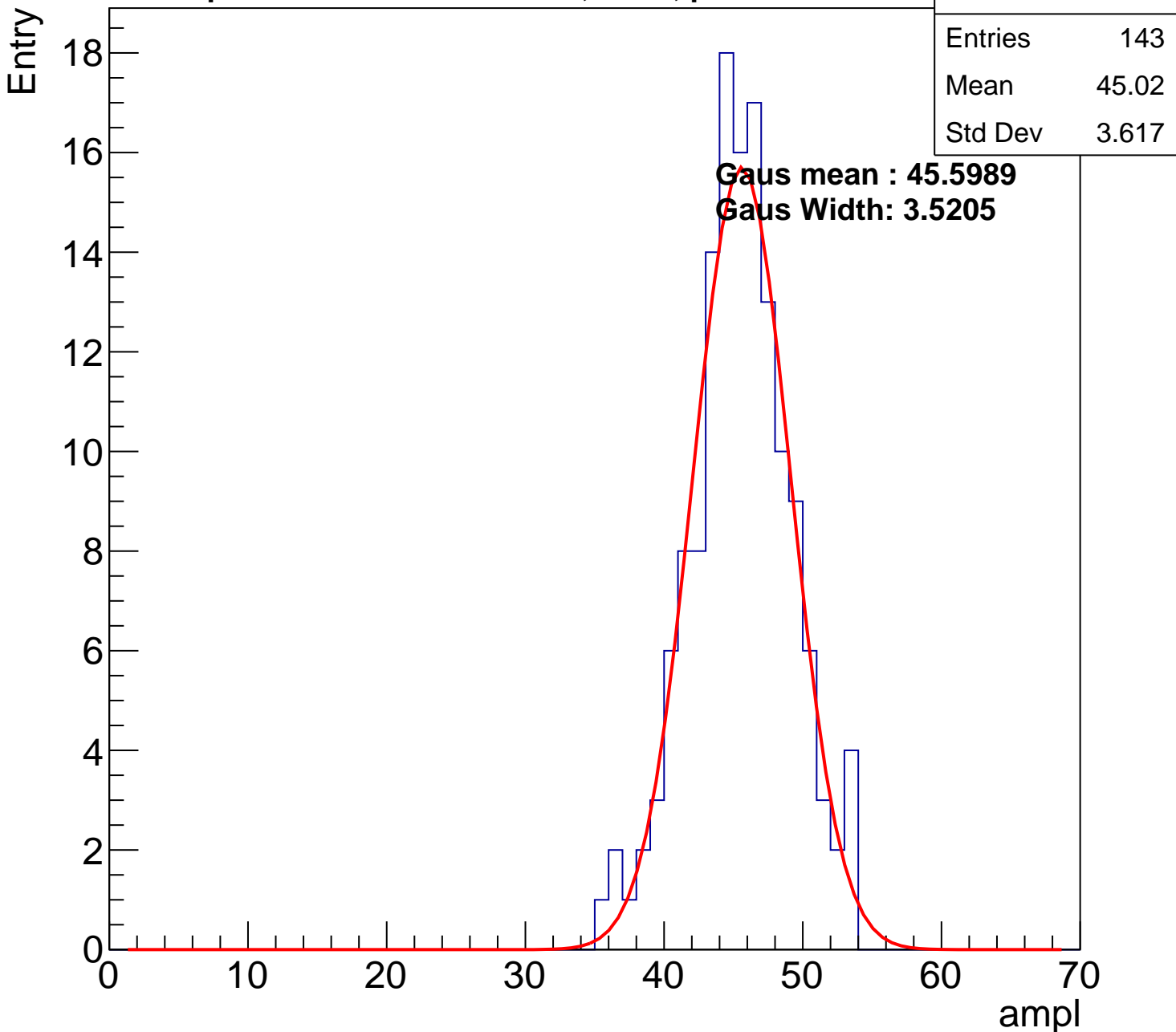
Entries	143
Mean	45.02
Std Dev	3.617

Gaus mean : 45.5989

Gaus Width: 3.5205

ampl

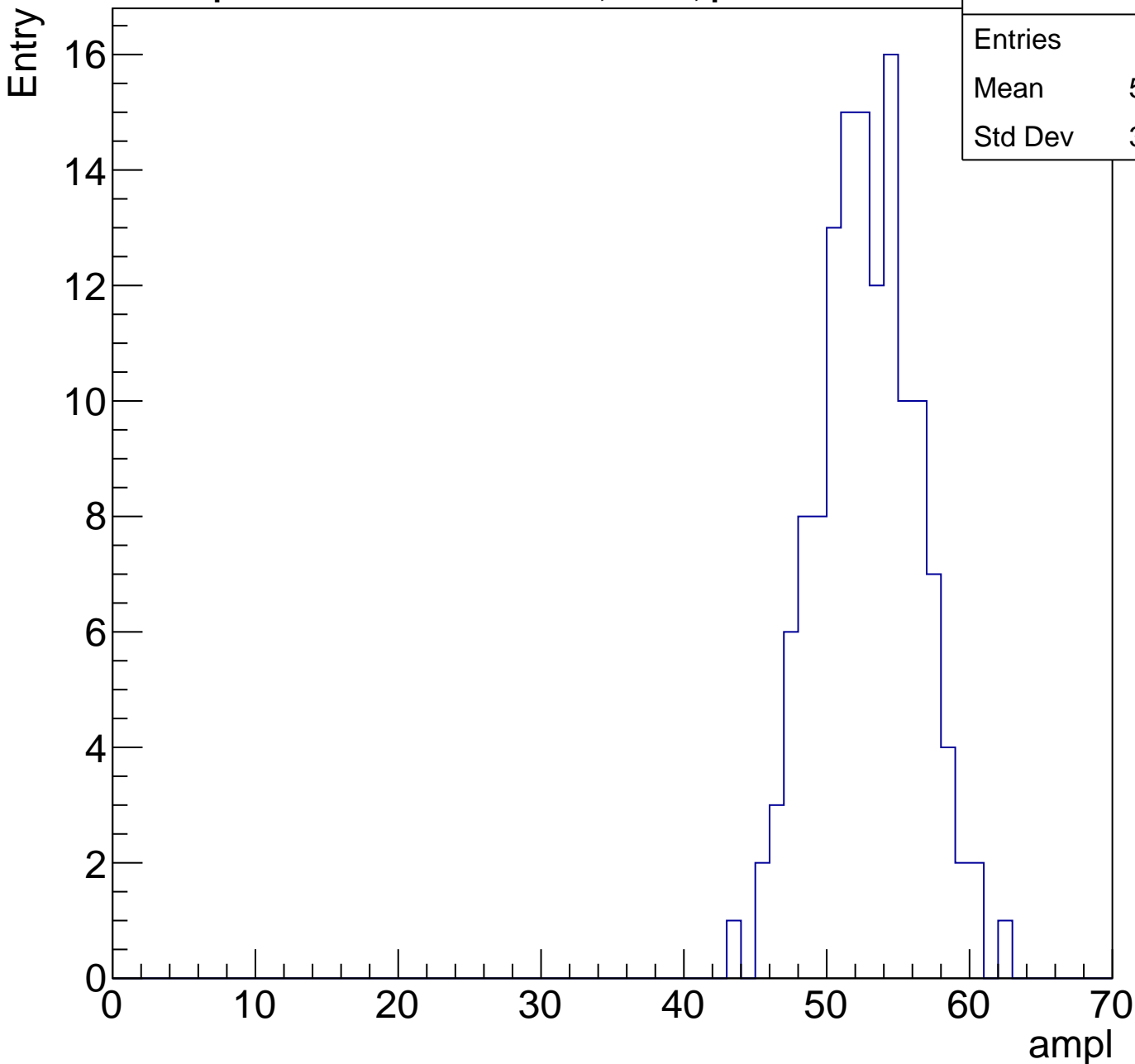
0 10 20 30 40 50 60 70



B0L101S, U24-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	52.33
Std Dev	3.507



B0L101S, U24-ch48, adc4

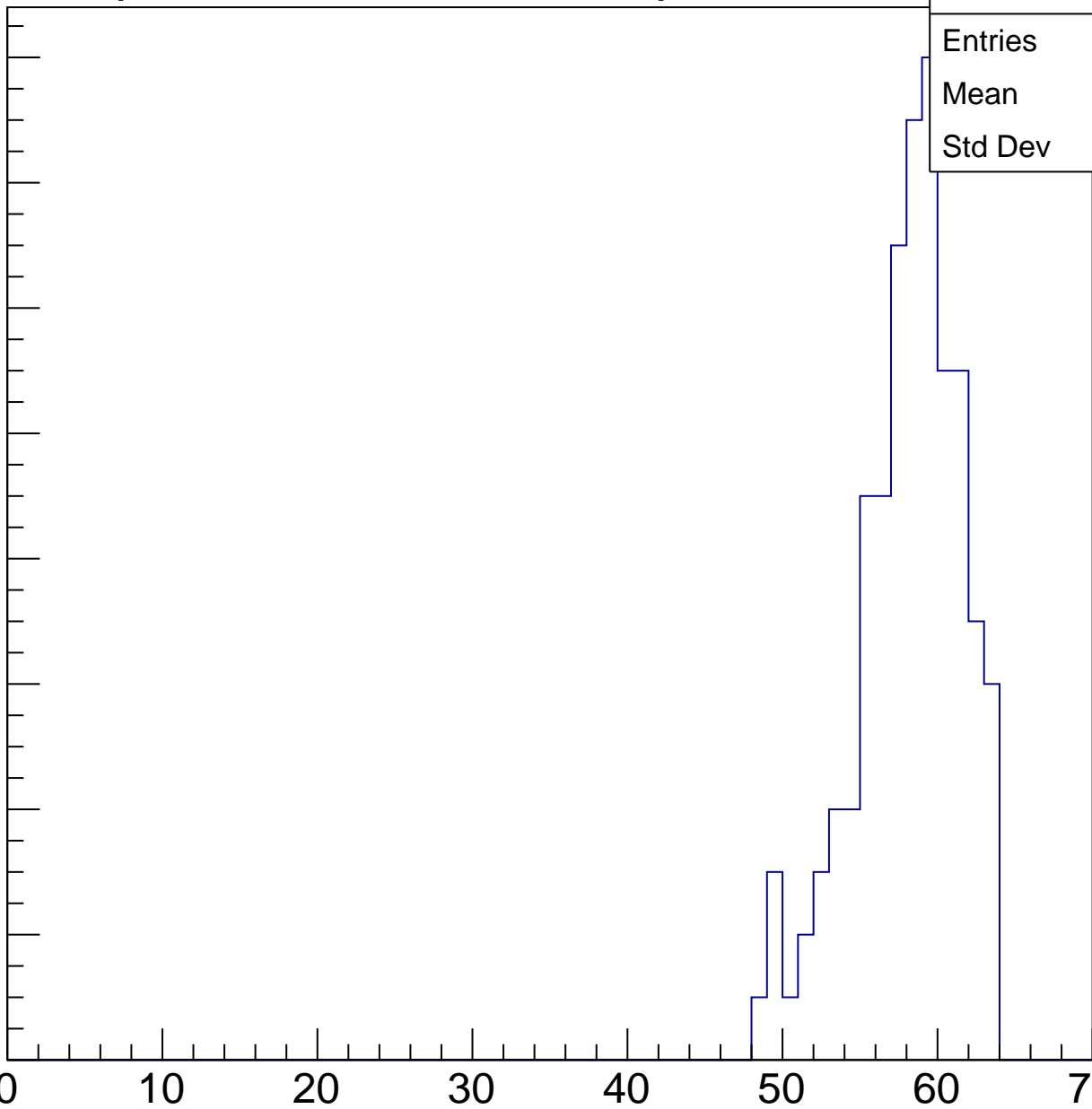
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	57.63
Std Dev	3.379

ampl



B0L101S, U24-ch48, adc5

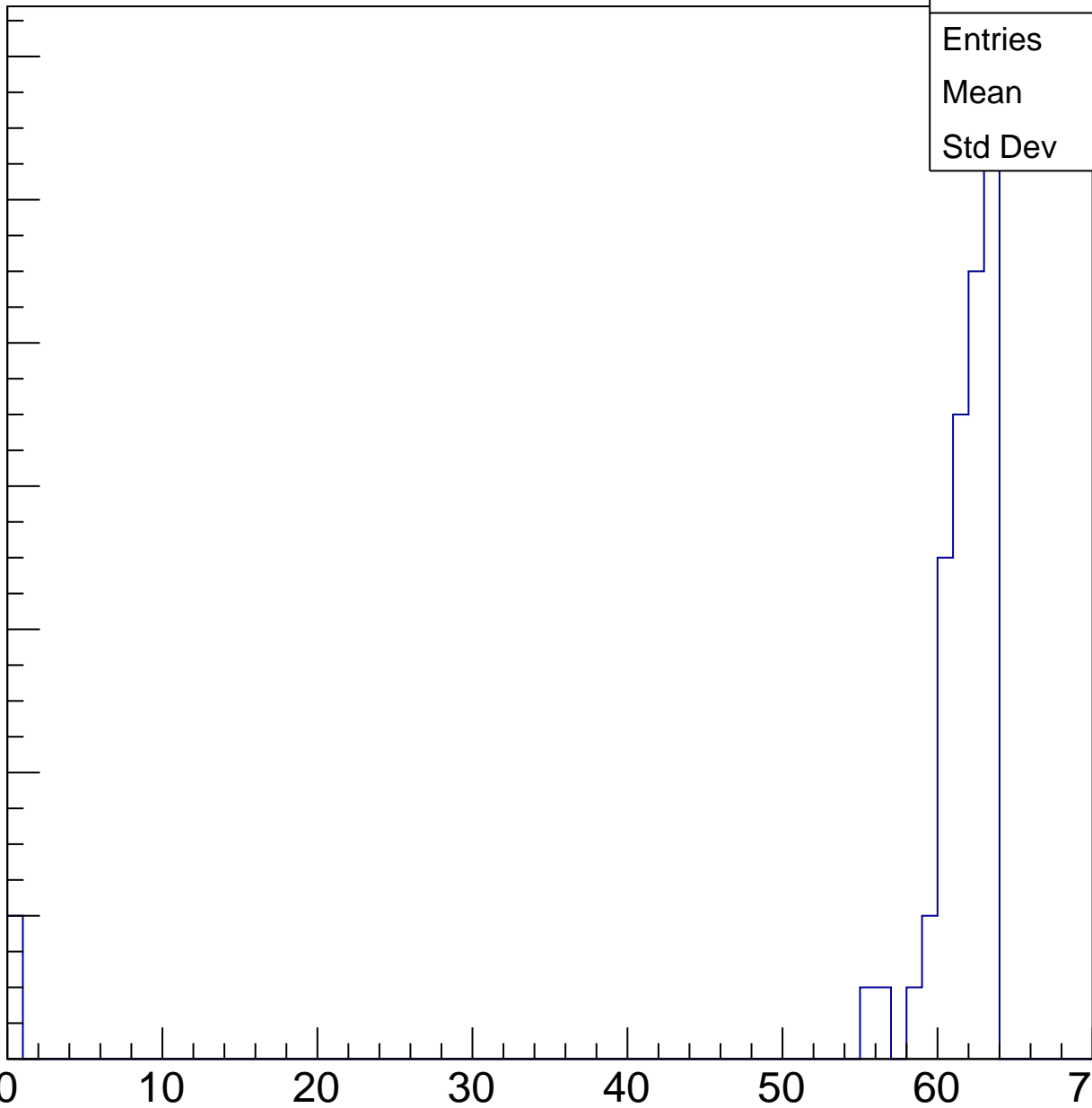
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	48
Mean	58.75
Std Dev	12.38

ampl



B0L101S, U24-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

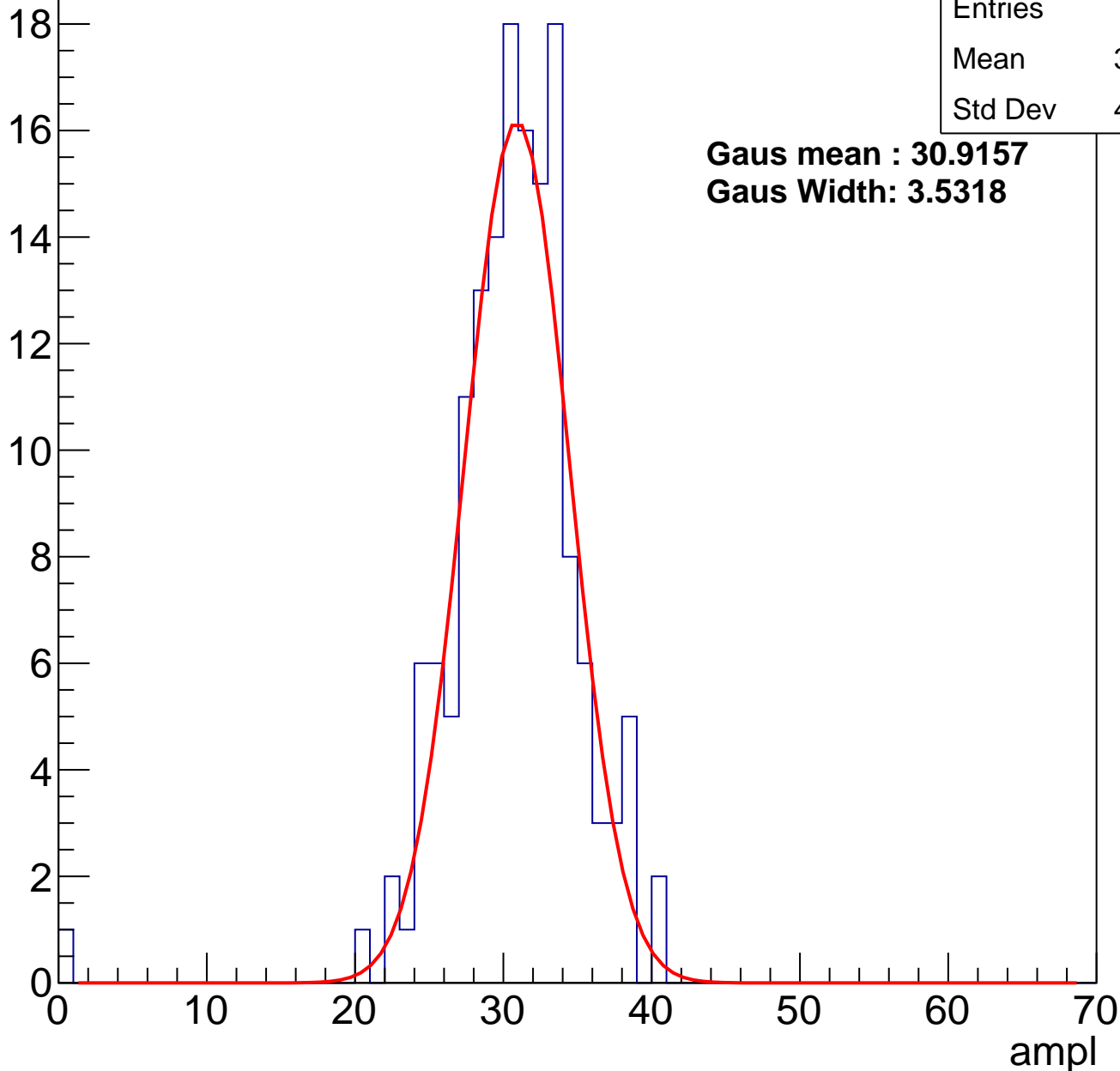


Entries	3
Mean	0
Std Dev	0

B0L101S, U24-ch49, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch49, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	37.55
Std Dev	3.485

Gaus mean : 38.4218

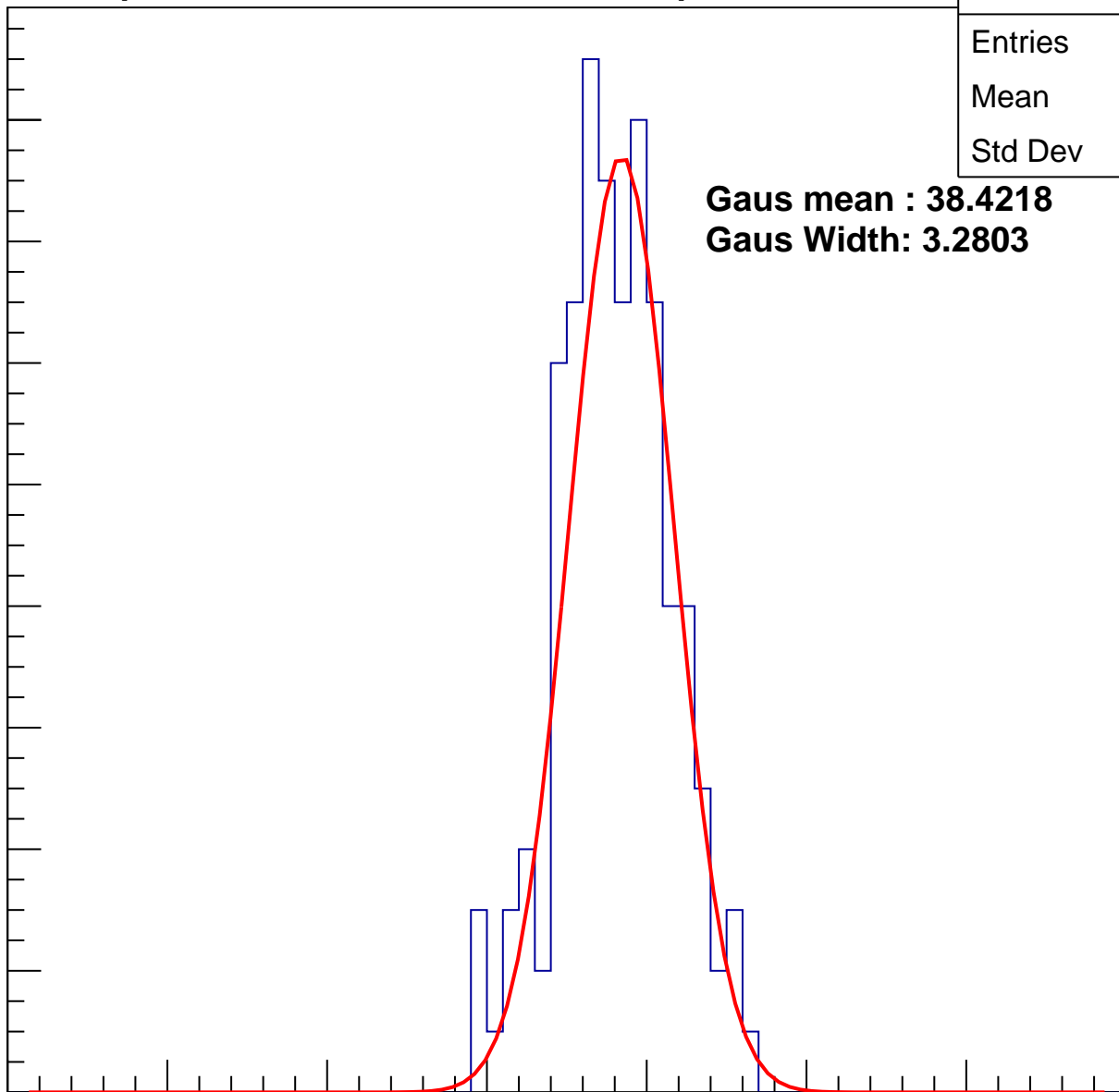
Gaus Width: 3.2803

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

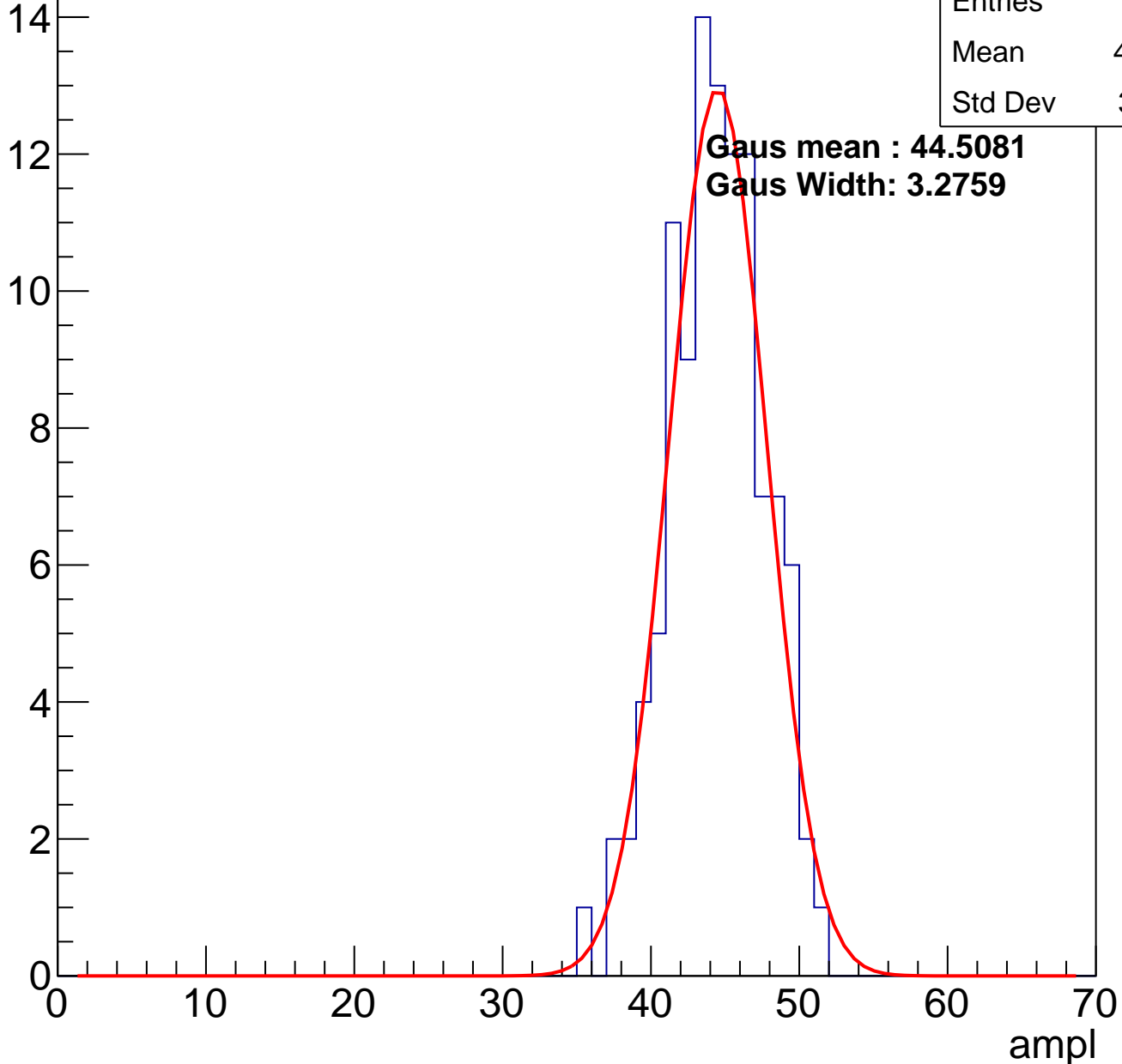
ampl



B0L101S, U24-ch49, adc2

calib_packv5_042523_0143.root, FC#1, port C1

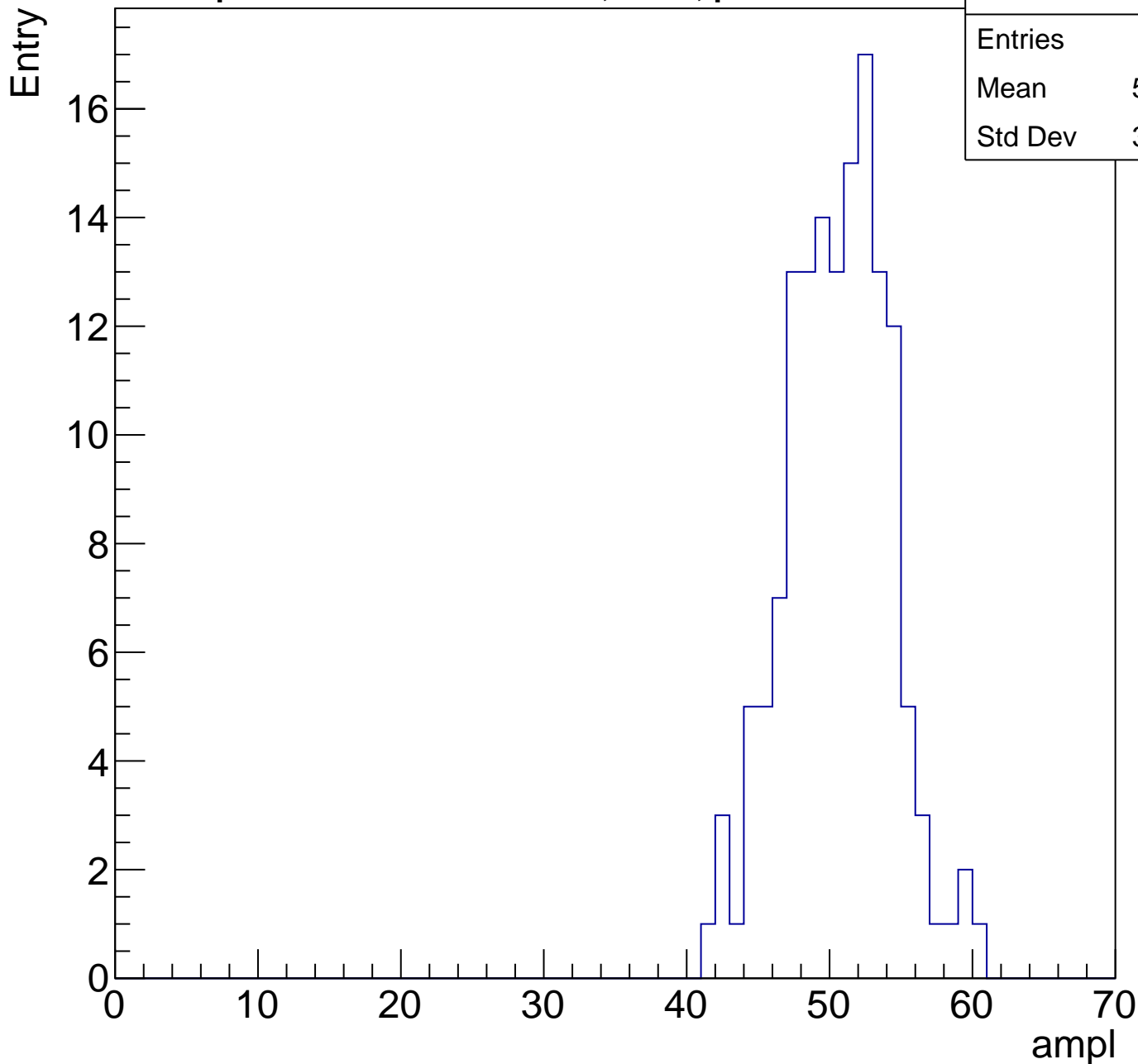
Entry



B0L101S, U24-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	50.14
Std Dev	3.638



B0L101S, U24-ch49, adc4

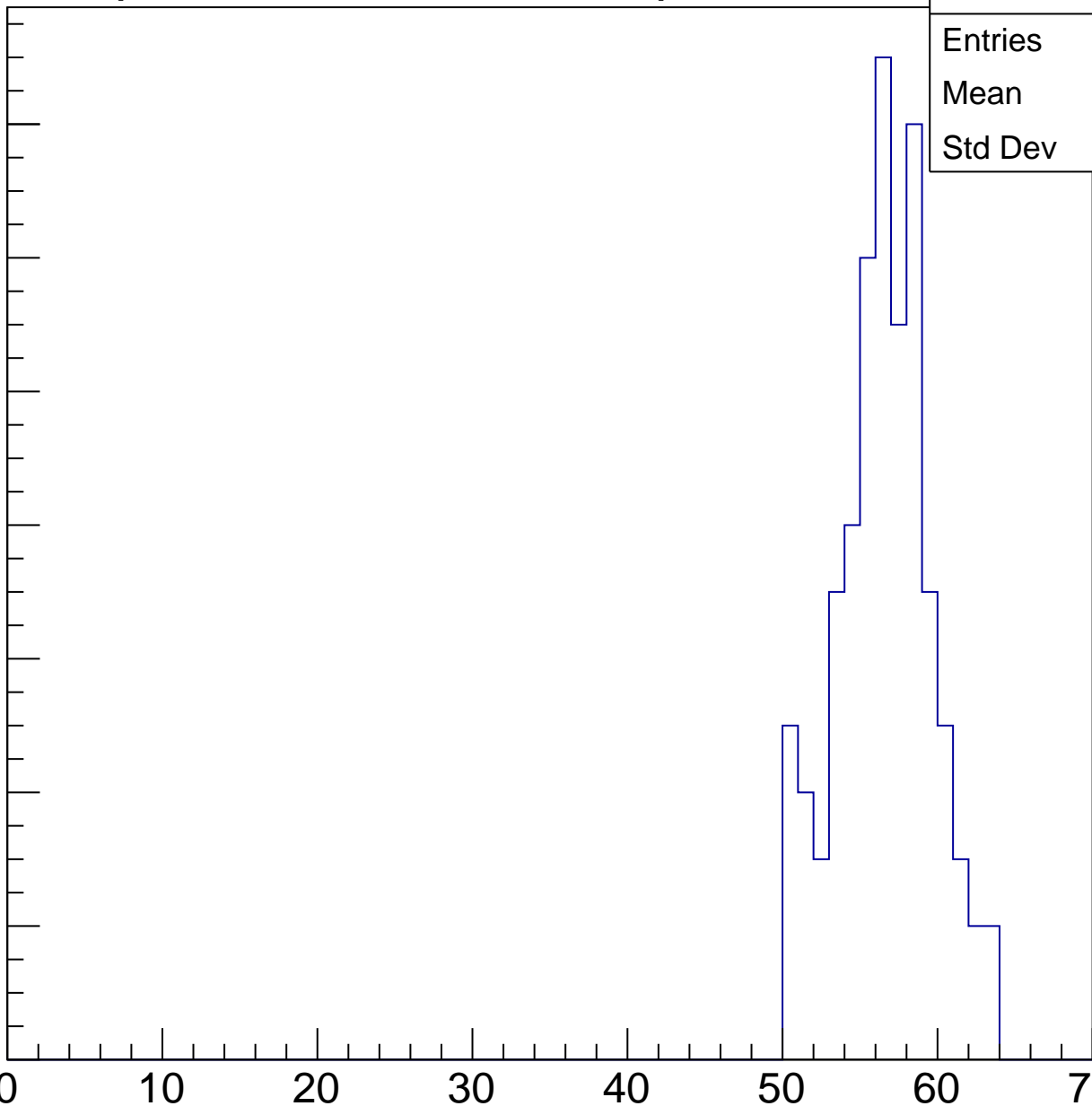
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	56.1
Std Dev	3.012

ampl



B0L101S, U24-ch49, adc5

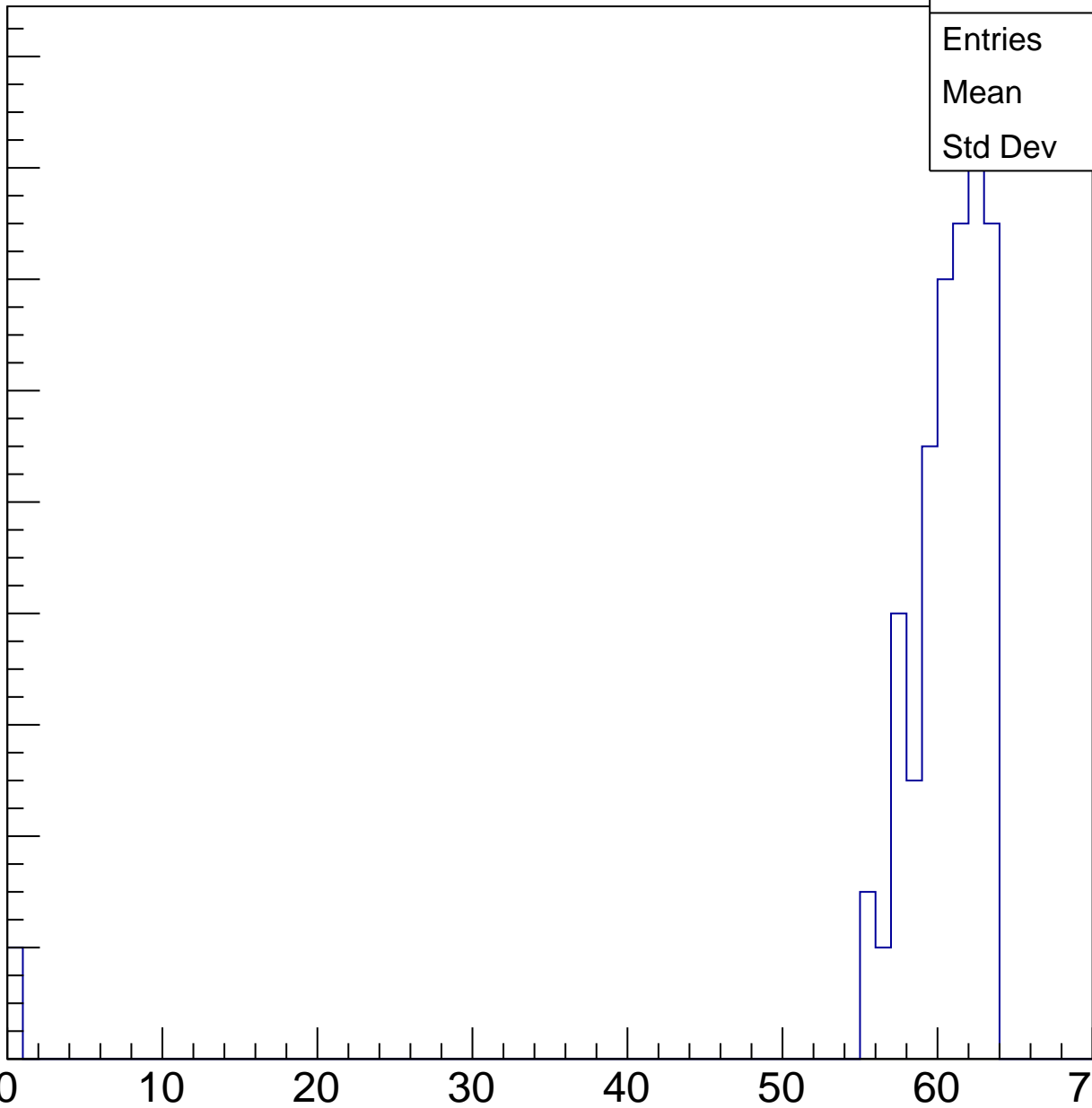
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.01
Std Dev	9.005

ampl



B0L101S, U24-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

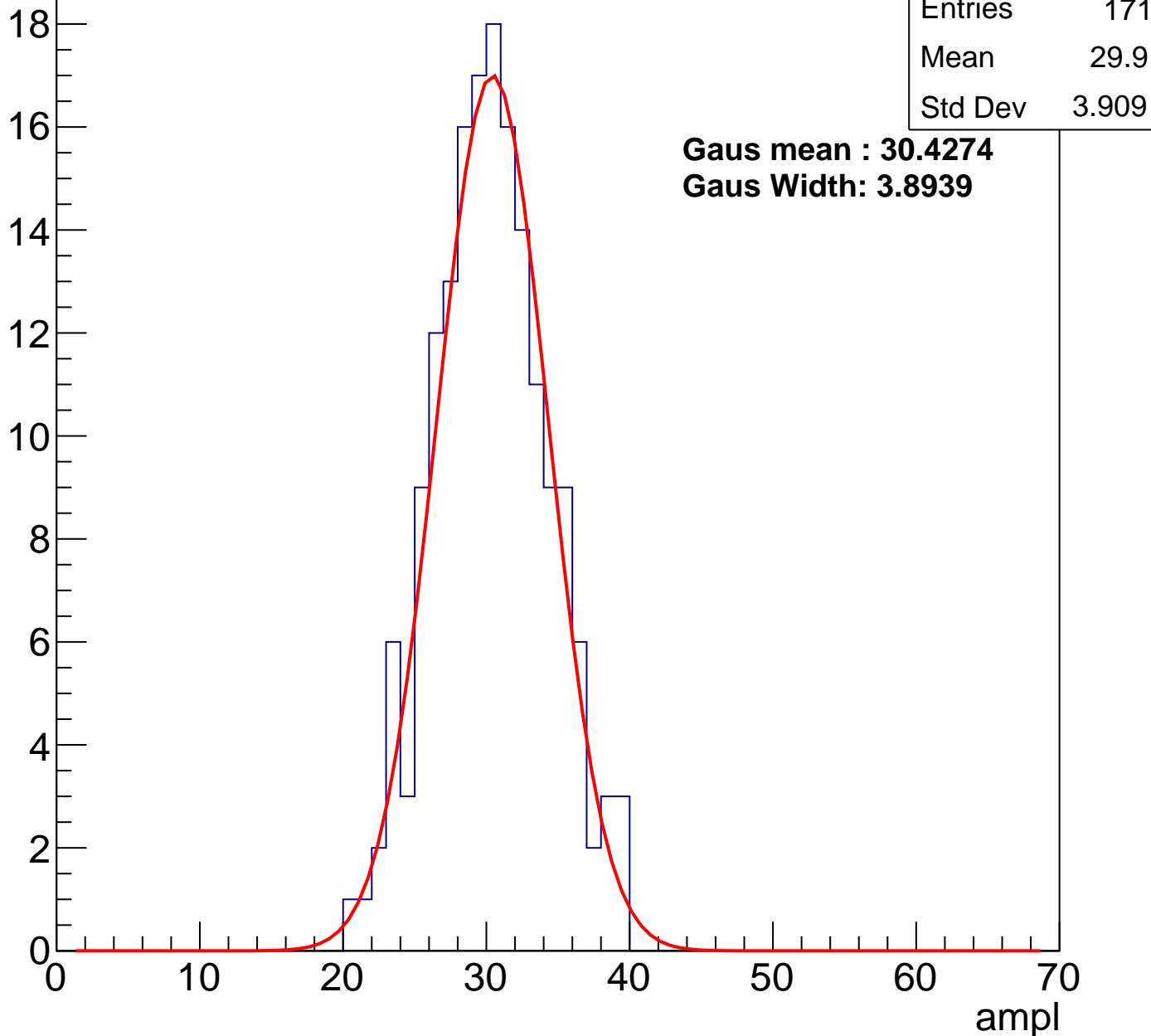


Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch50, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch50, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

122

Mean

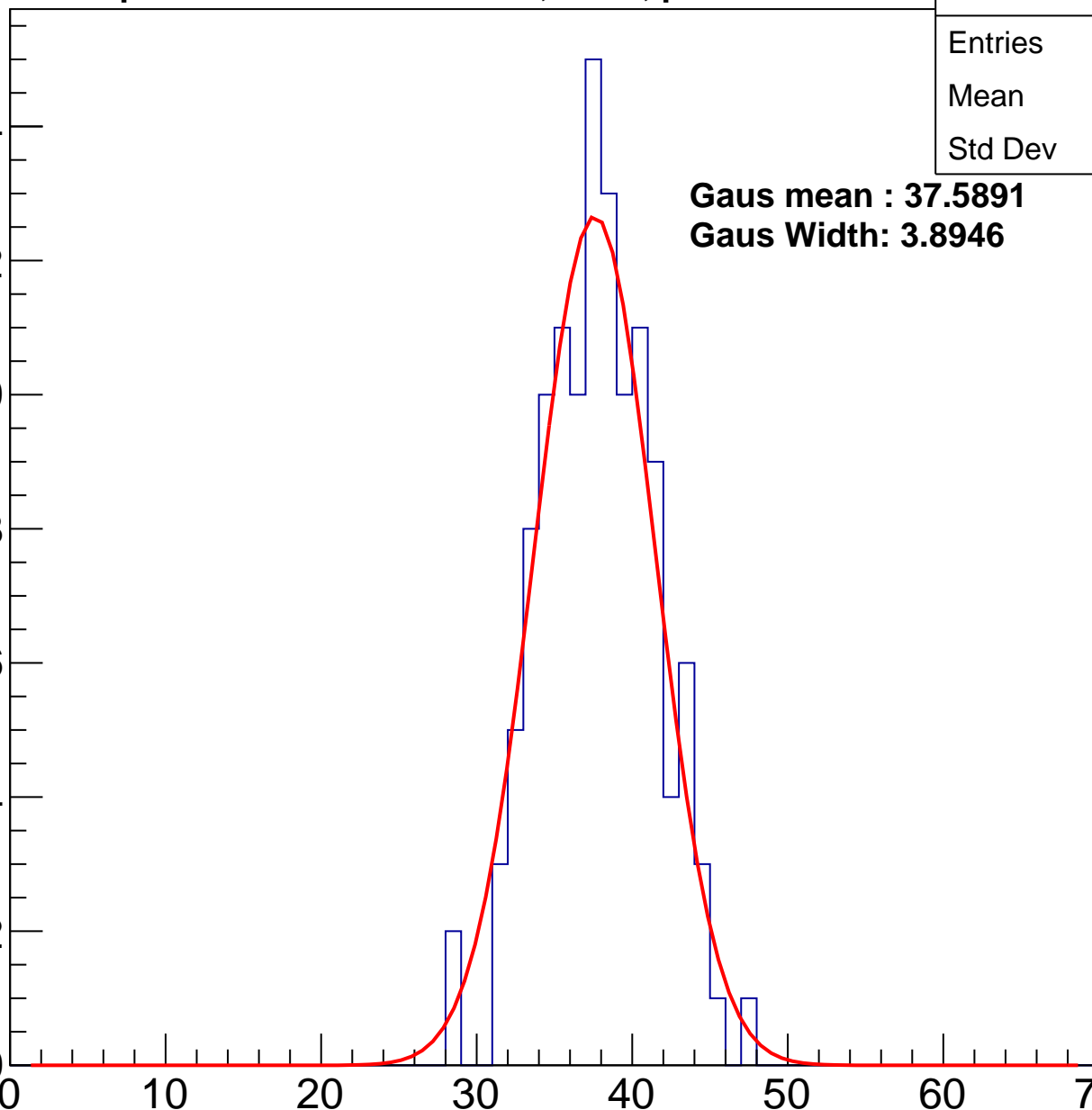
37.34

Std Dev

3.594

Gaus mean : 37.5891

Gaus Width: 3.8946



B0L101S, U24-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	43.44
Std Dev	3.855

Gaus mean : 44.0598

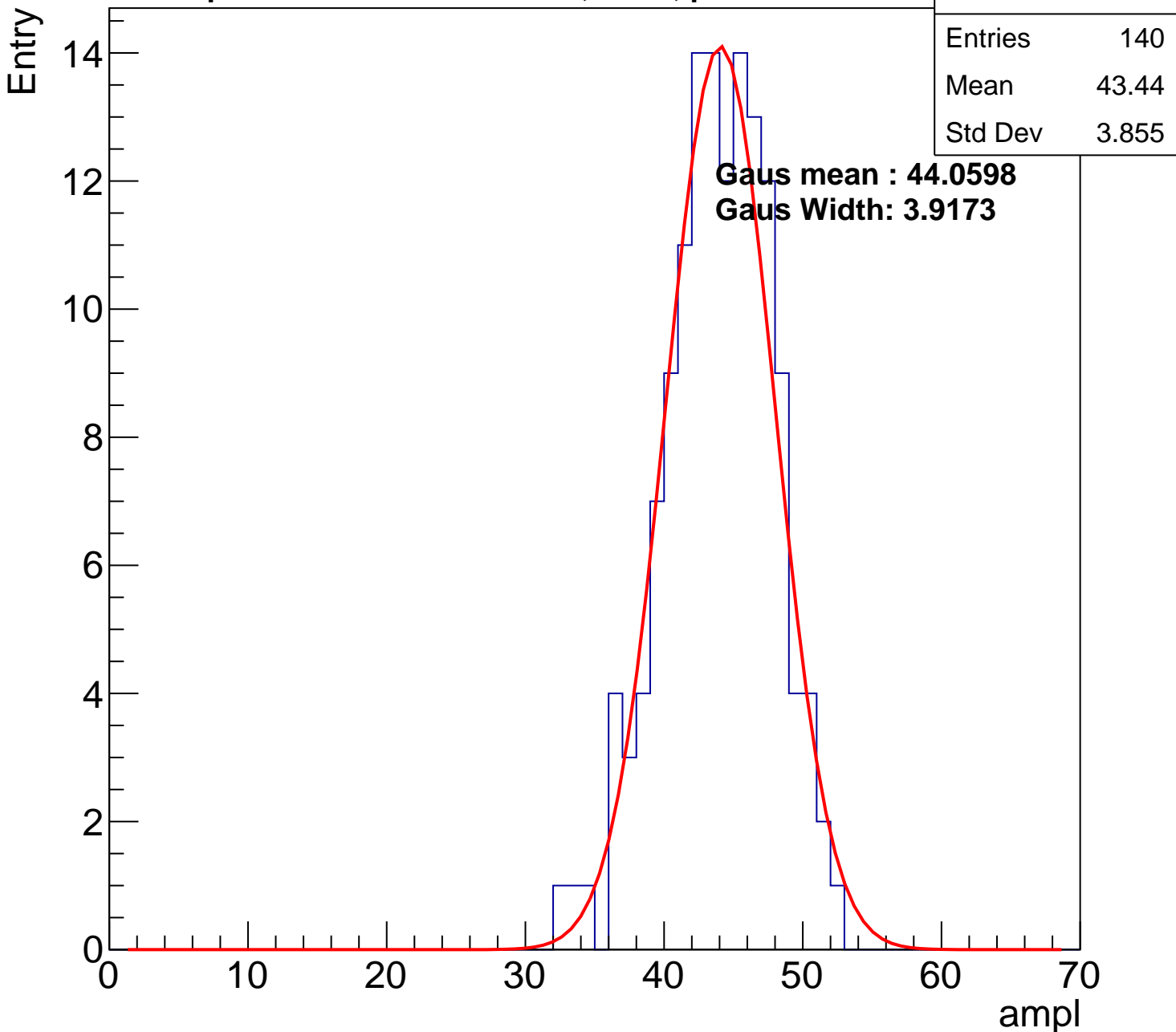
Gaus Width: 3.9173

Entry

14
12
10
8
6
4
2
0

ampl

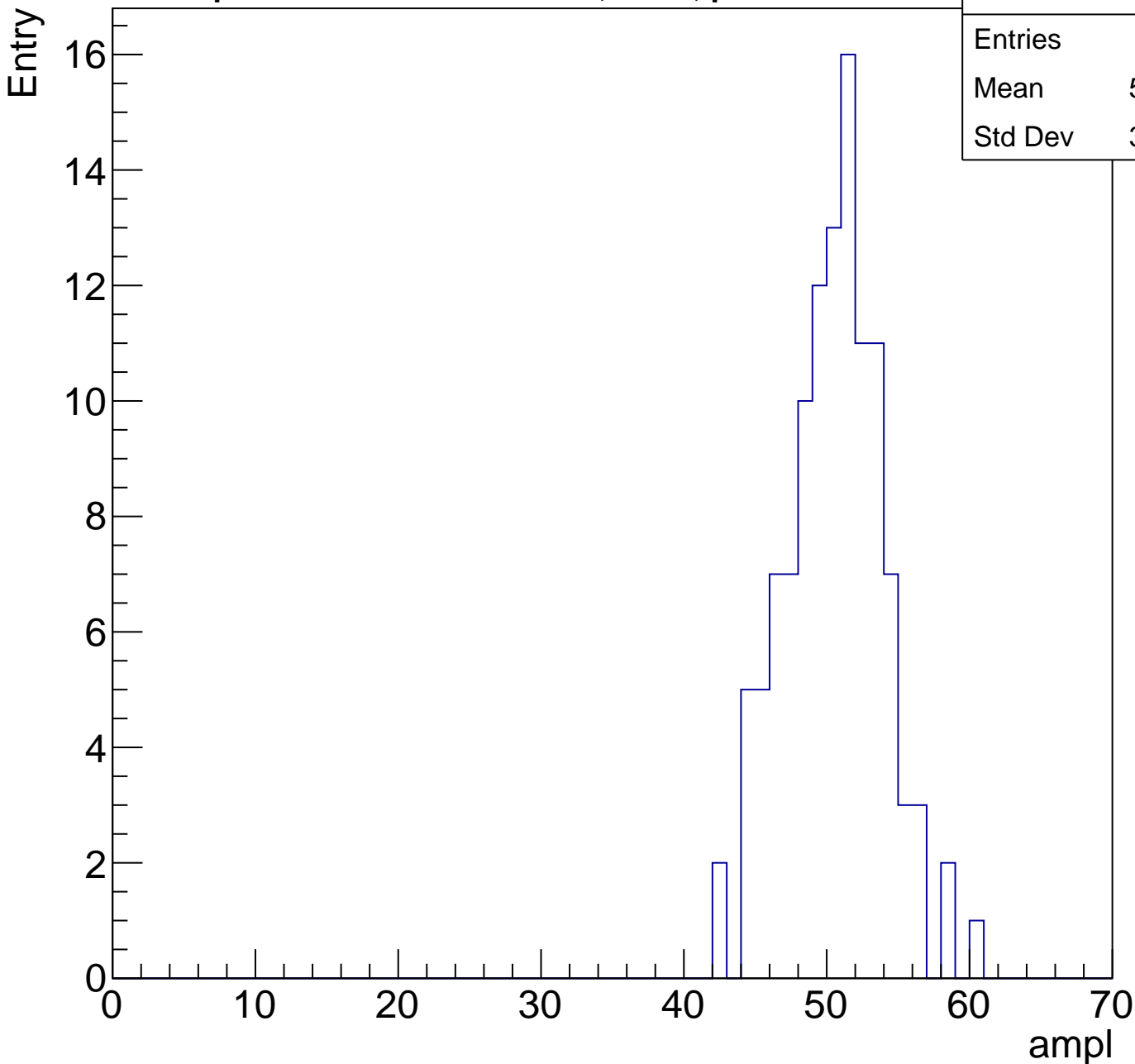
0 10 20 30 40 50 60 70



B0L101S, U24-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

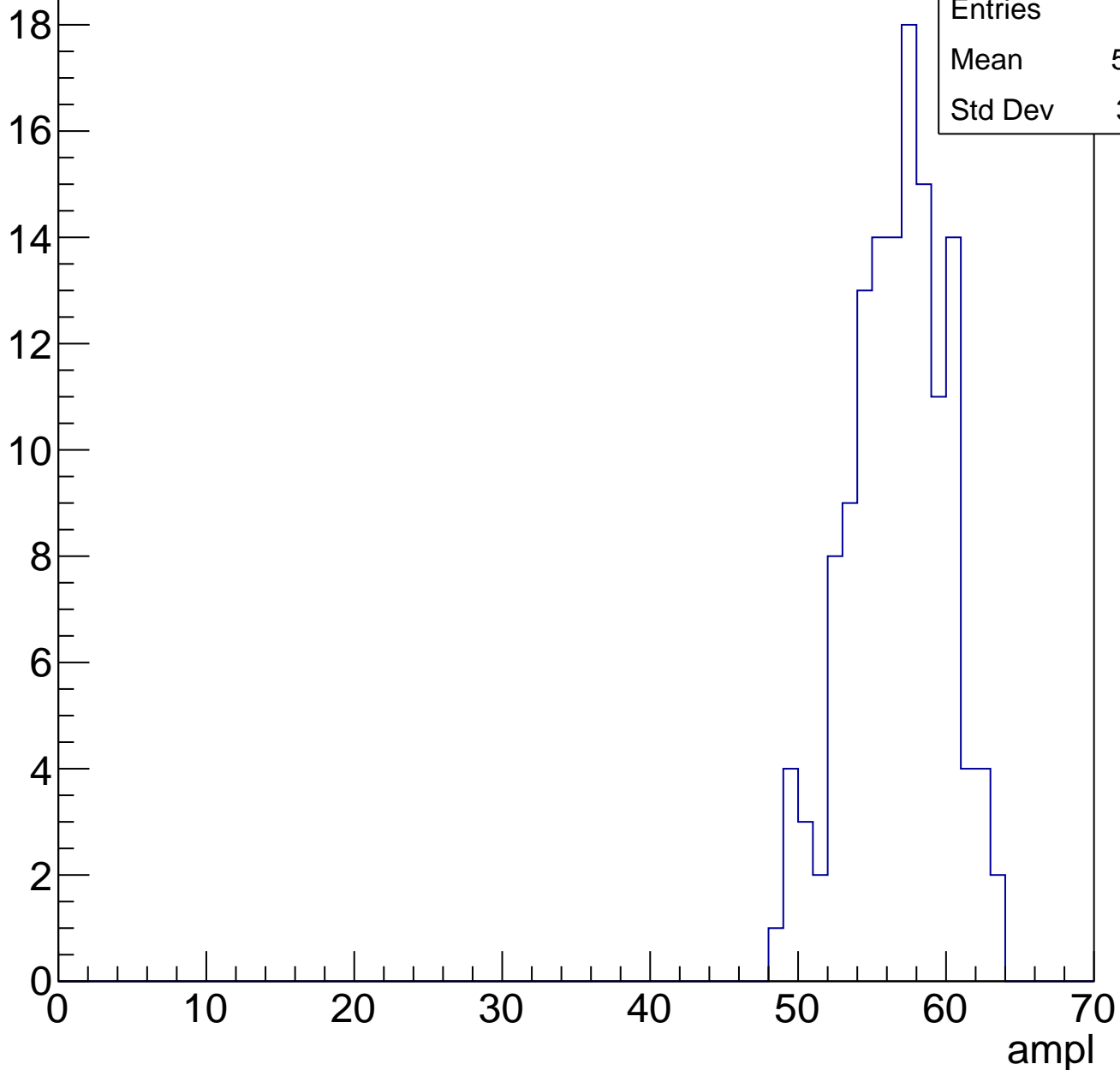
Entries	115
Mean	50.05
Std Dev	3.416



B0L101S, U24-ch50, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch50, adc5

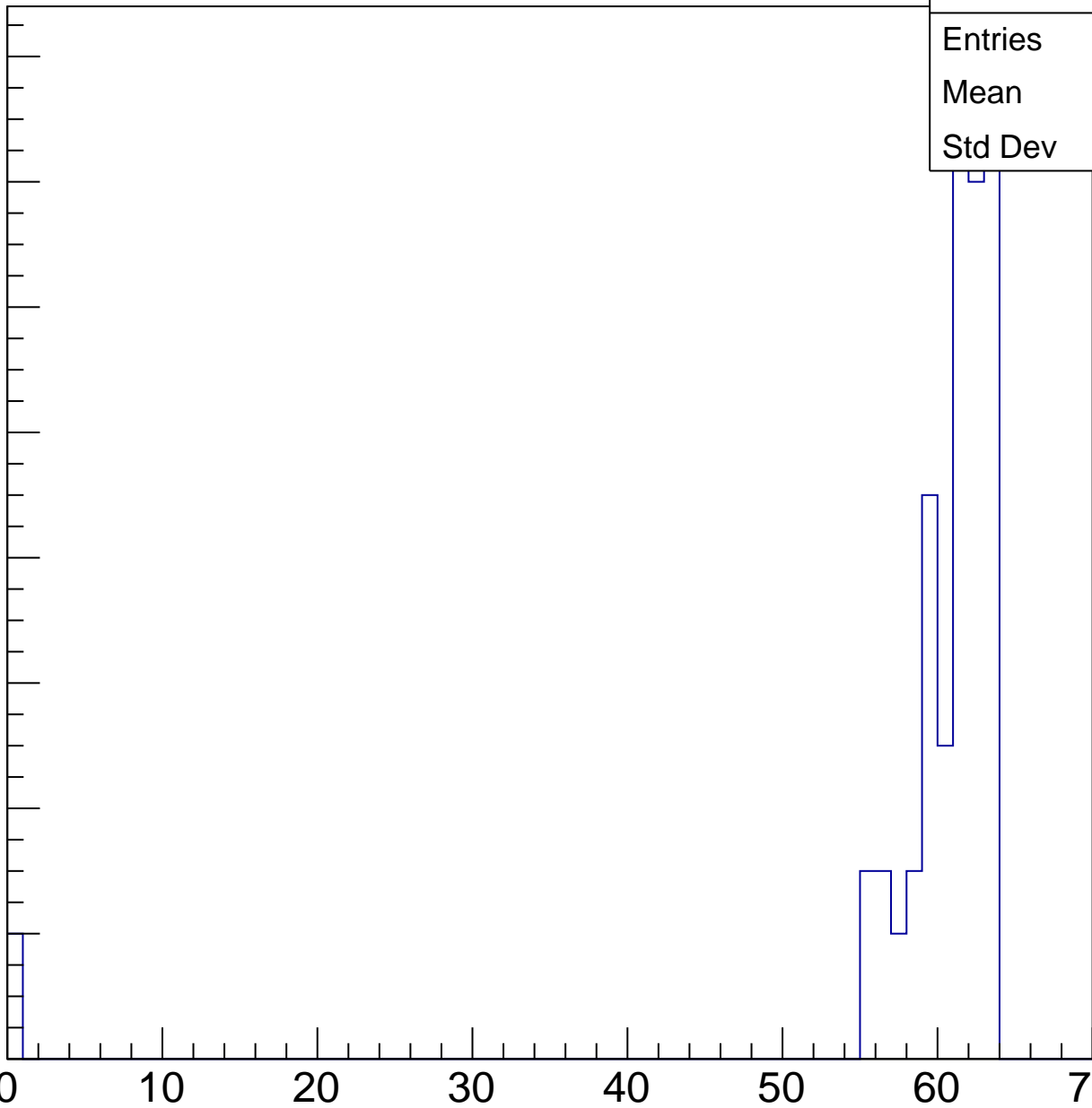
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	72
Mean	58.9
Std Dev	10.19

ampl



B0L101S, U24-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch51, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	28.49
Std Dev	3.607

Gaus mean : 29.0862

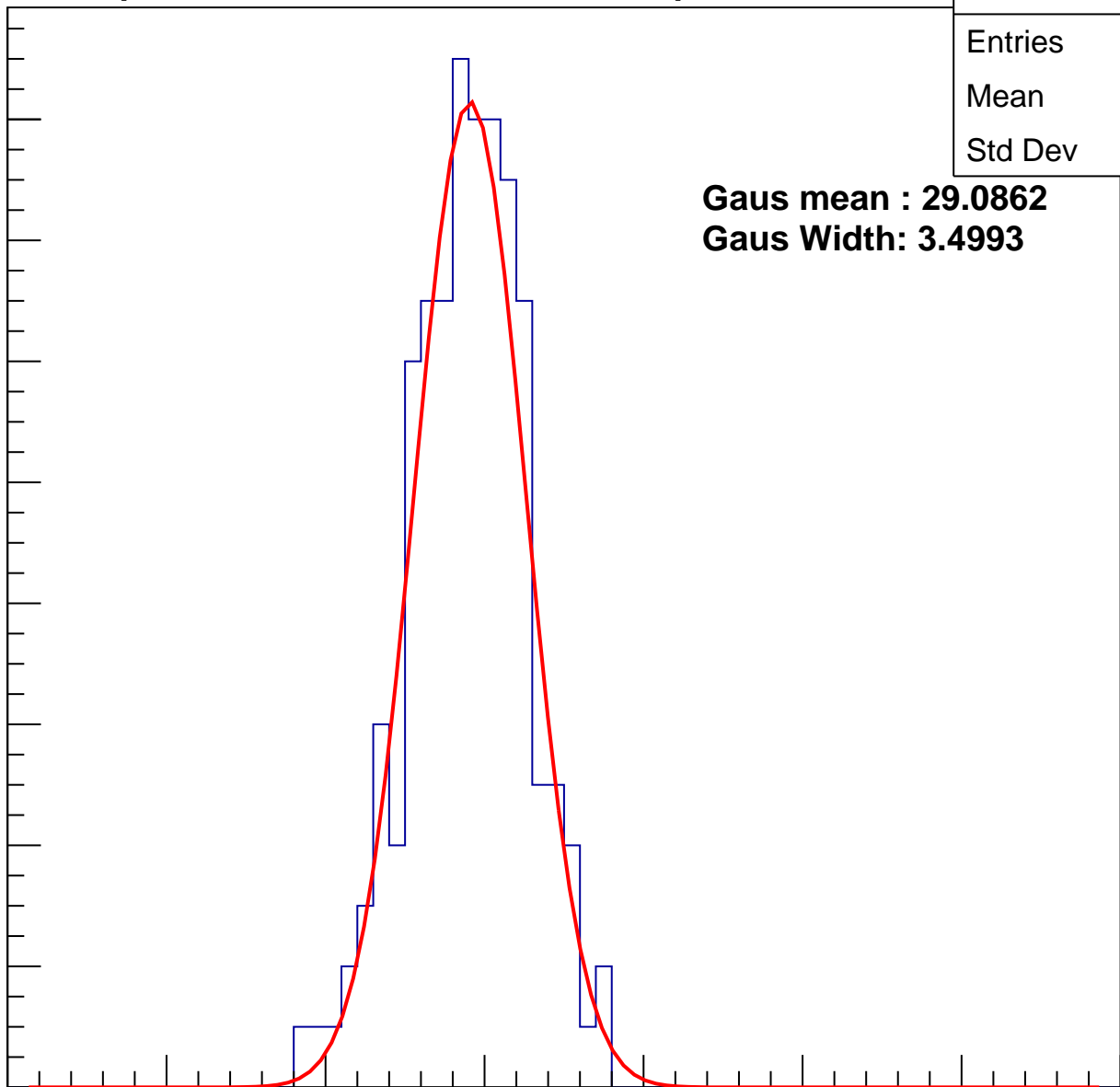
Gaus Width: 3.4993

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



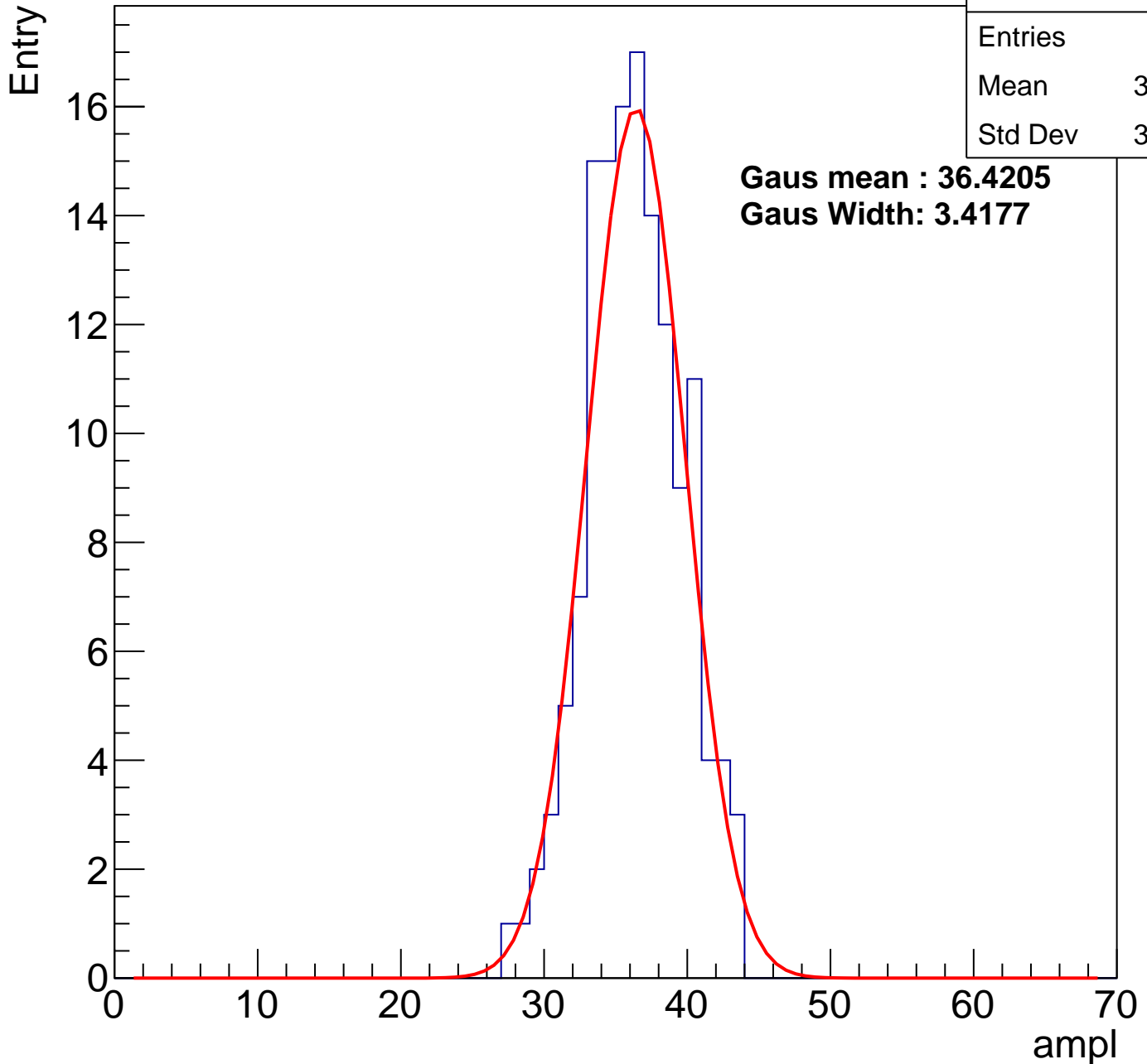
B0L101S, U24-ch51, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	35.86
Std Dev	3.292

Gaus mean : 36.4205

Gaus Width: 3.4177



B0L101S, U24-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

42.77

Std Dev

3.135

Gaus mean : 43.0478

Gaus Width: 3.2998

0

10

20

30

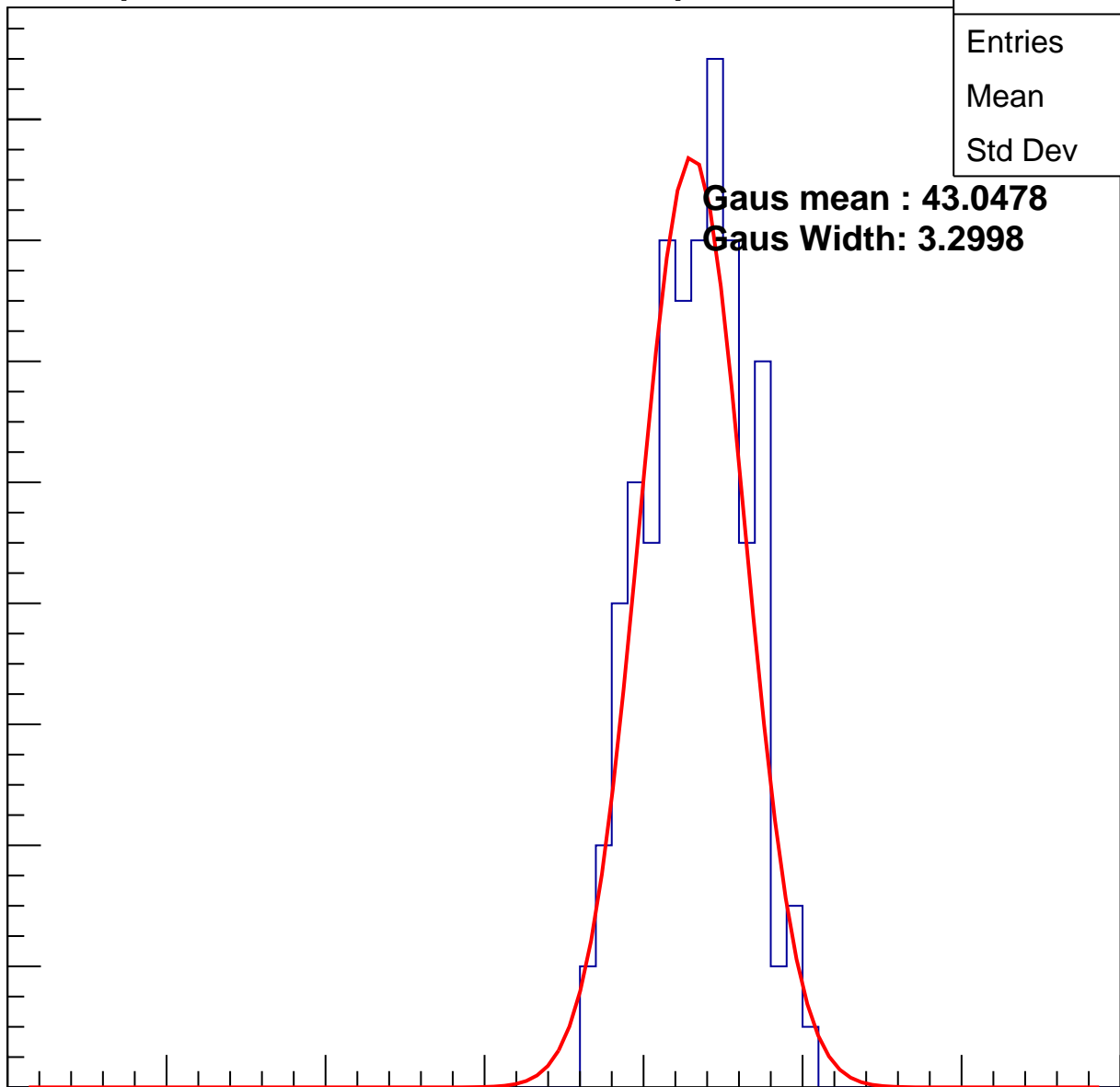
40

50

60

70

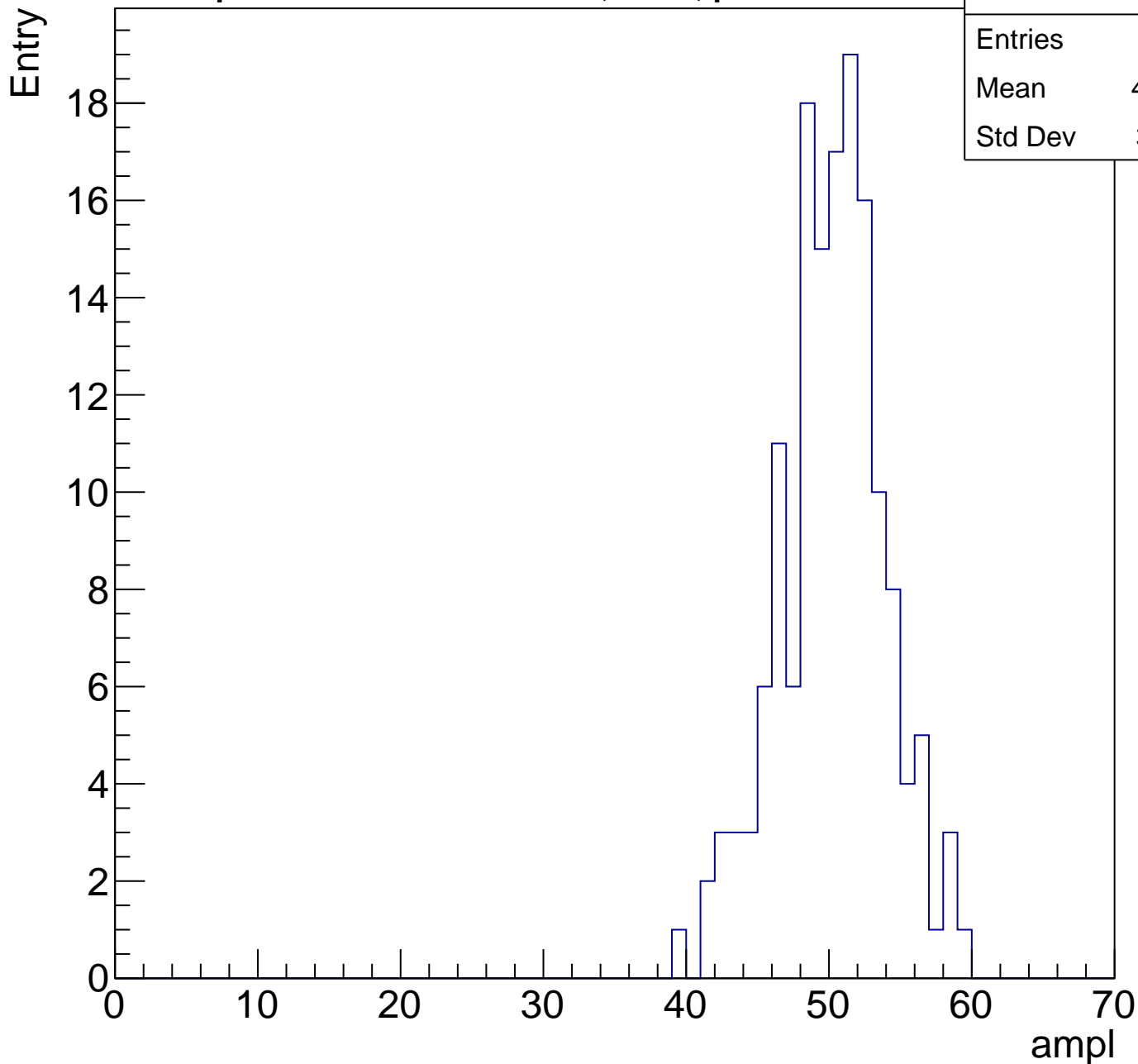
ampl



B0L101S, U24-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	49.79
Std Dev	3.711



B0L101S, U24-ch51, adc4

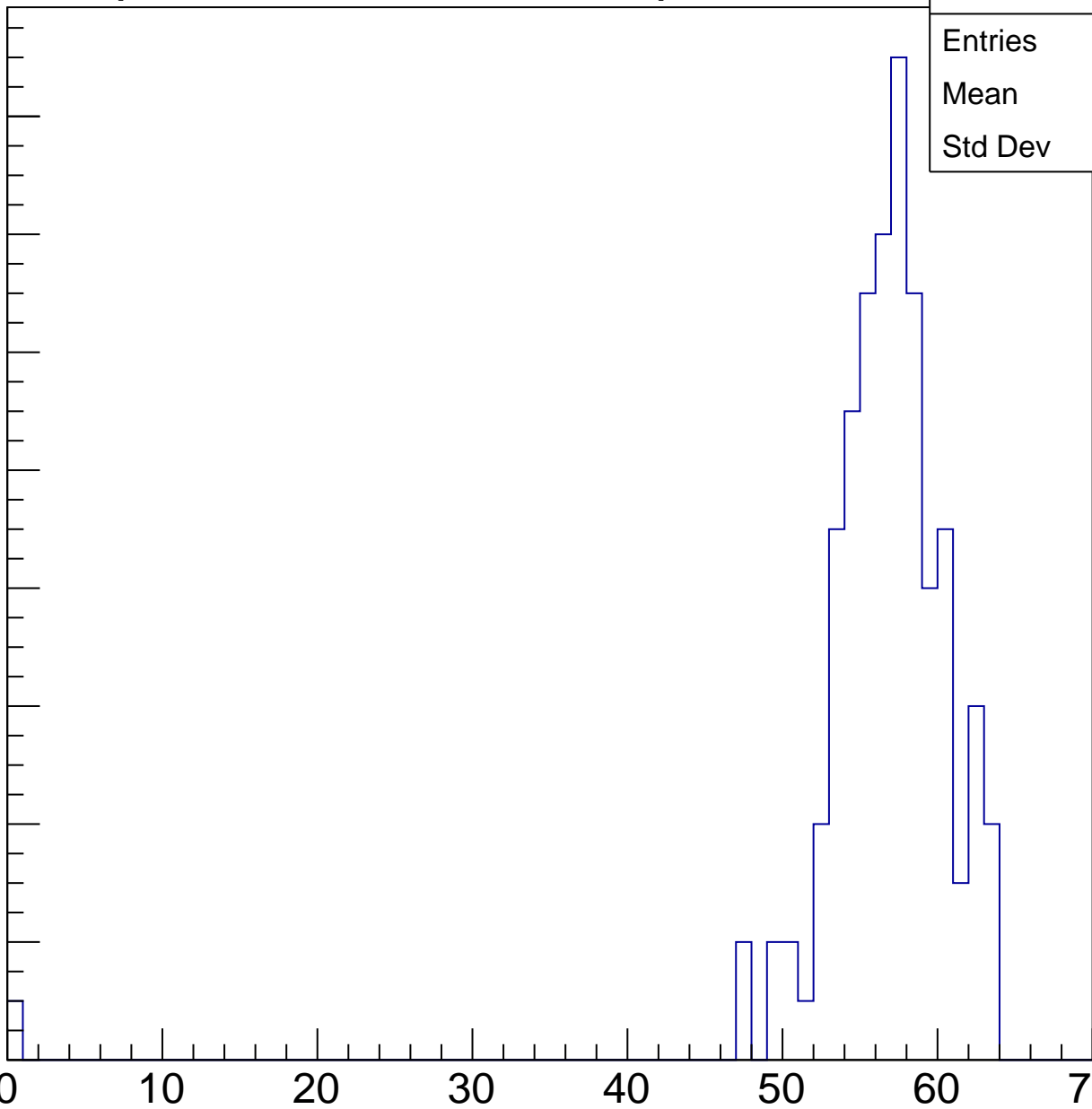
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	55.99
Std Dev	6.127

ampl



B0L101S, U24-ch51, adc5

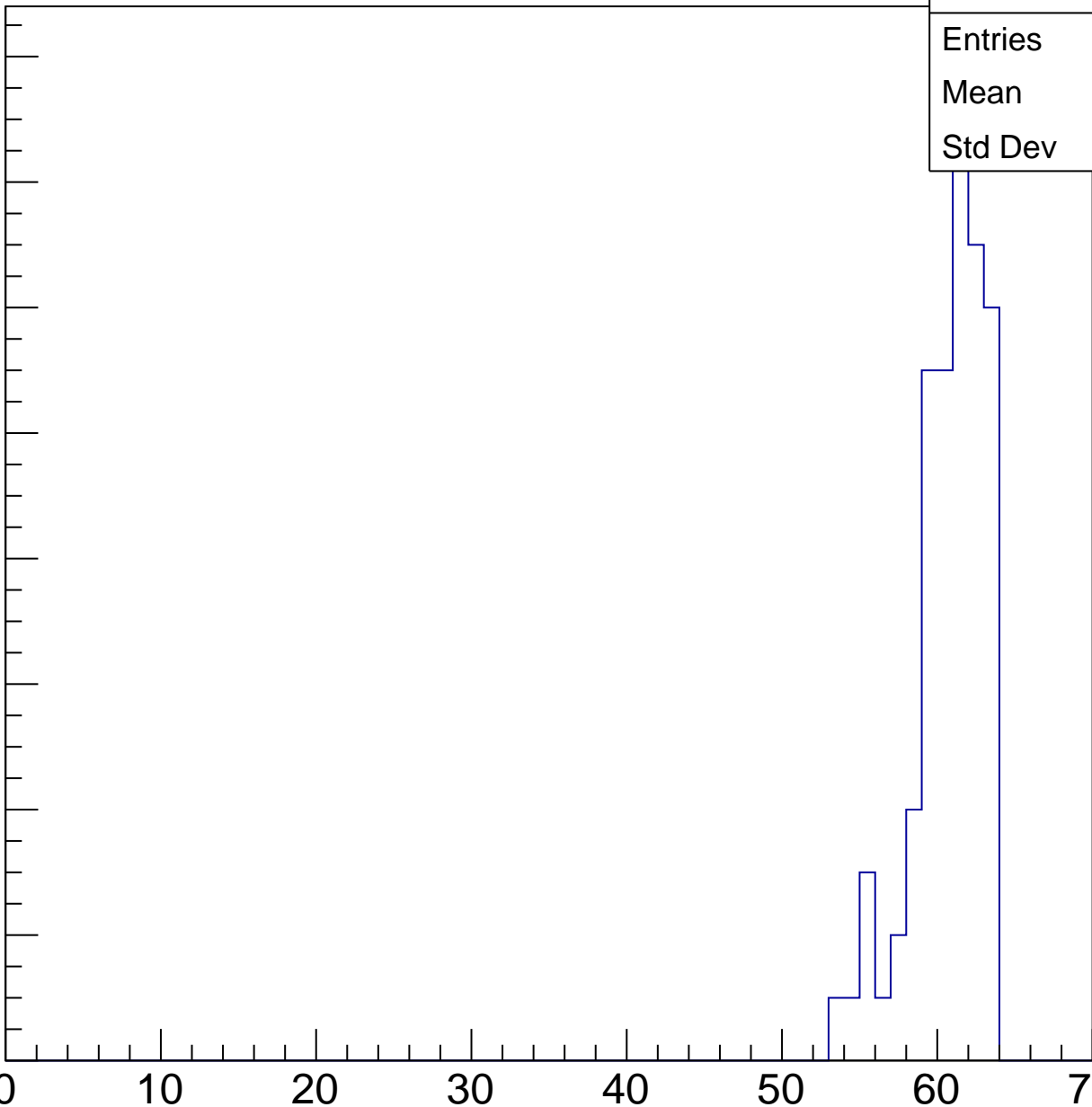
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	60.28
Std Dev	2.284

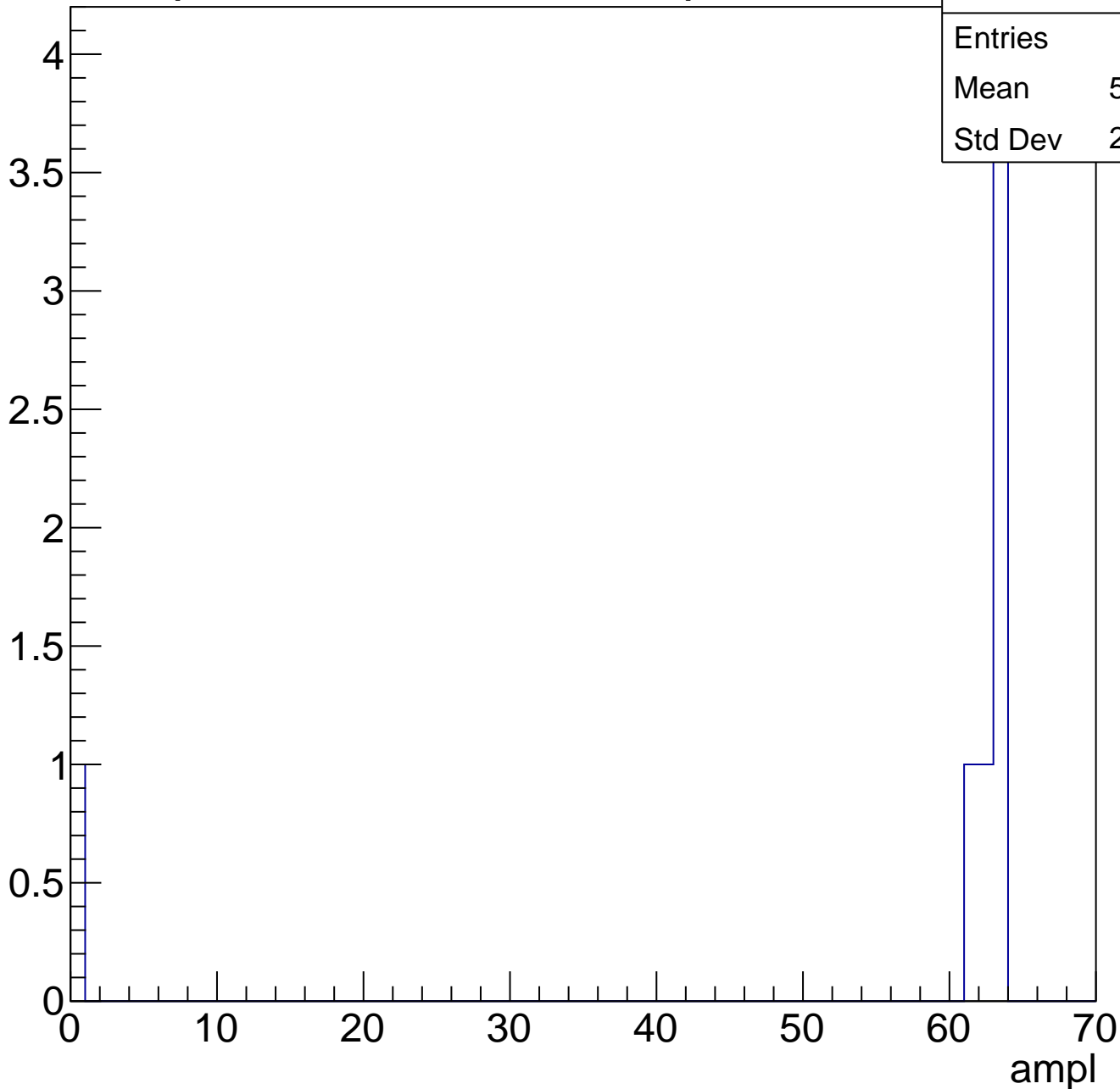
ampl



B0L101S, U24-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L101S, U24-ch52, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.31
Std Dev	4.394

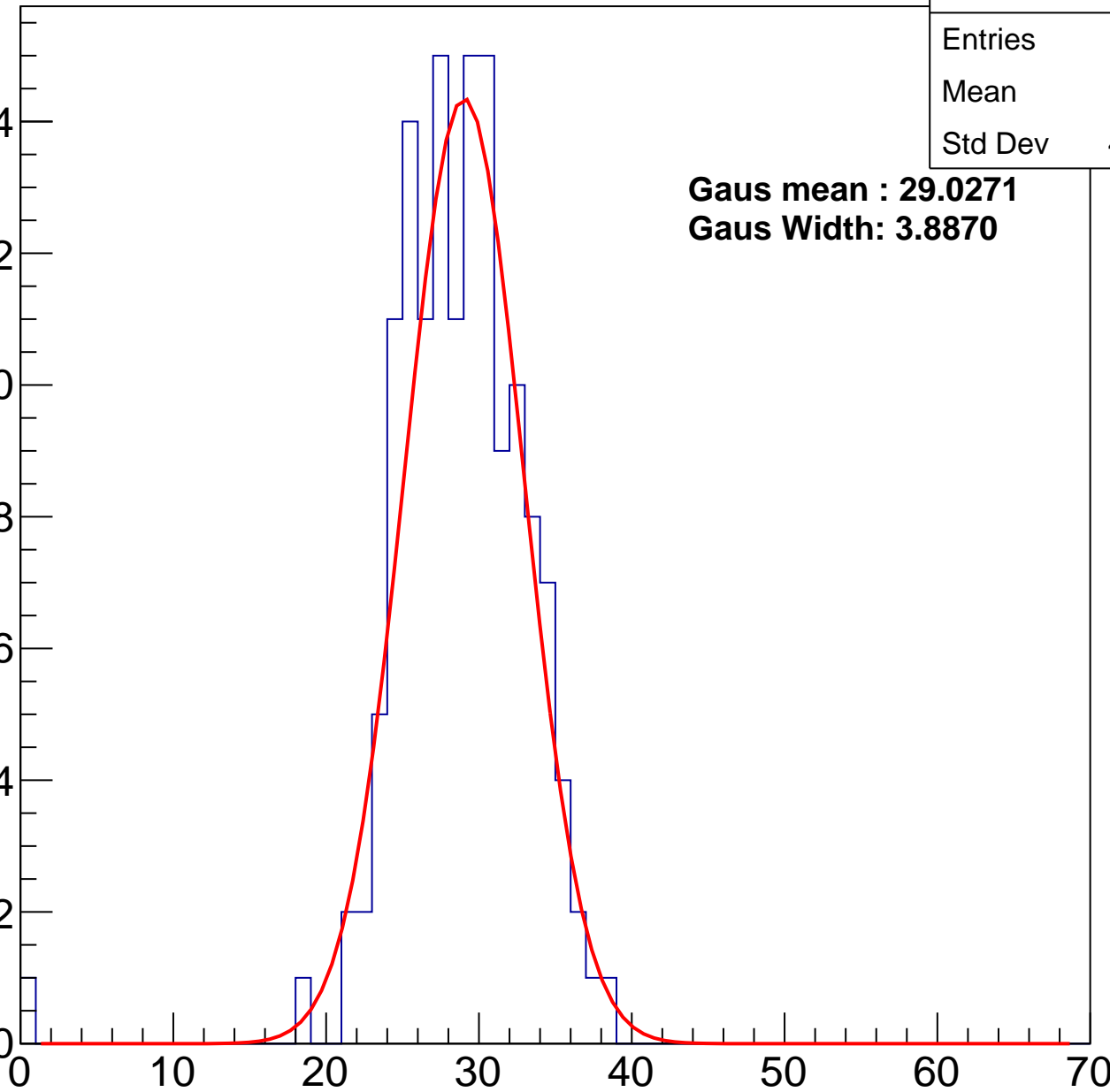
Gaus mean : 29.0271

Gaus Width: 3.8870

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch52, adc1

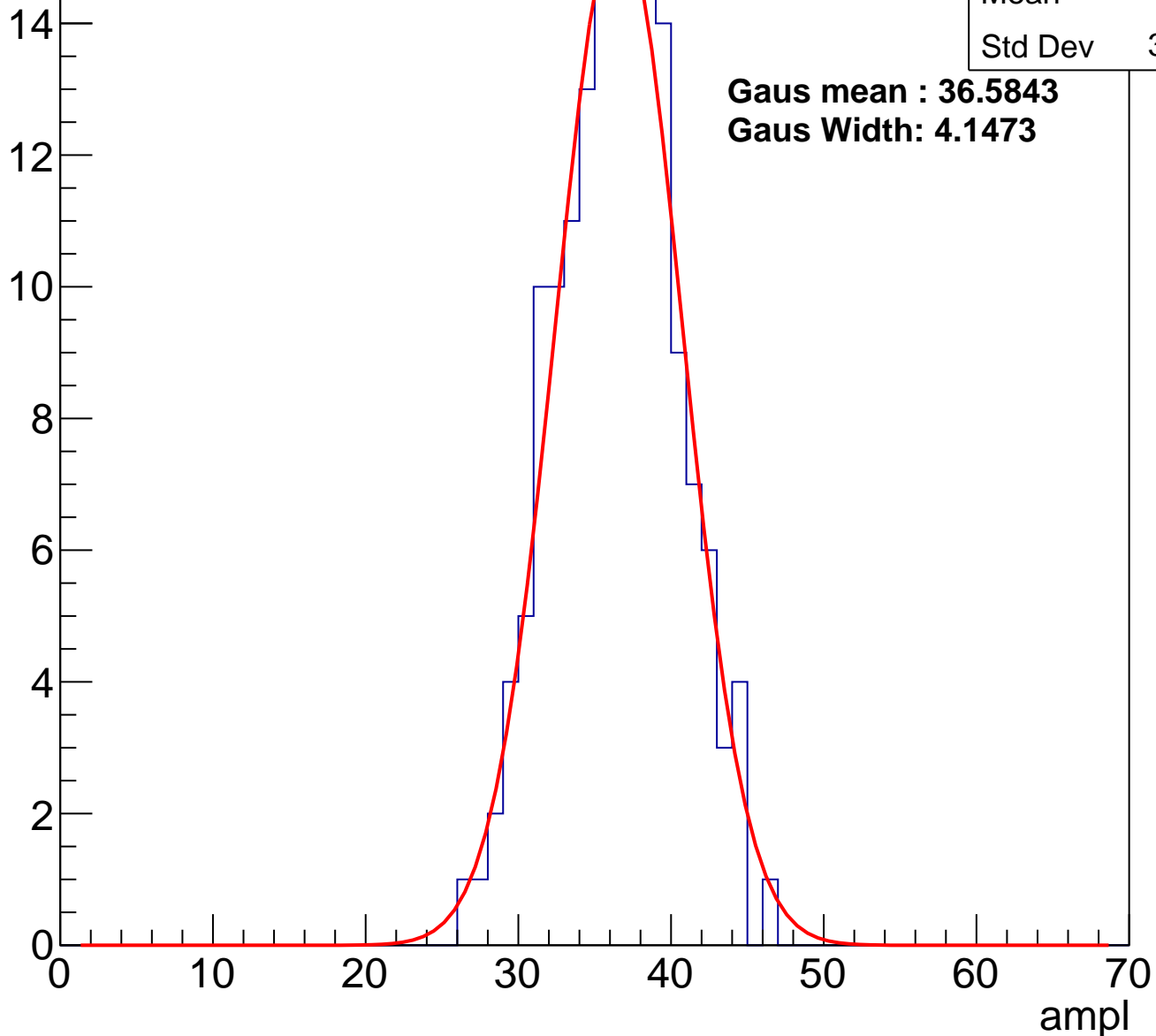
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	36
Std Dev	3.941

Gaus mean : 36.5843

Gaus Width: 4.1473

Entry



B0L101S, U24-ch52, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

107

Mean

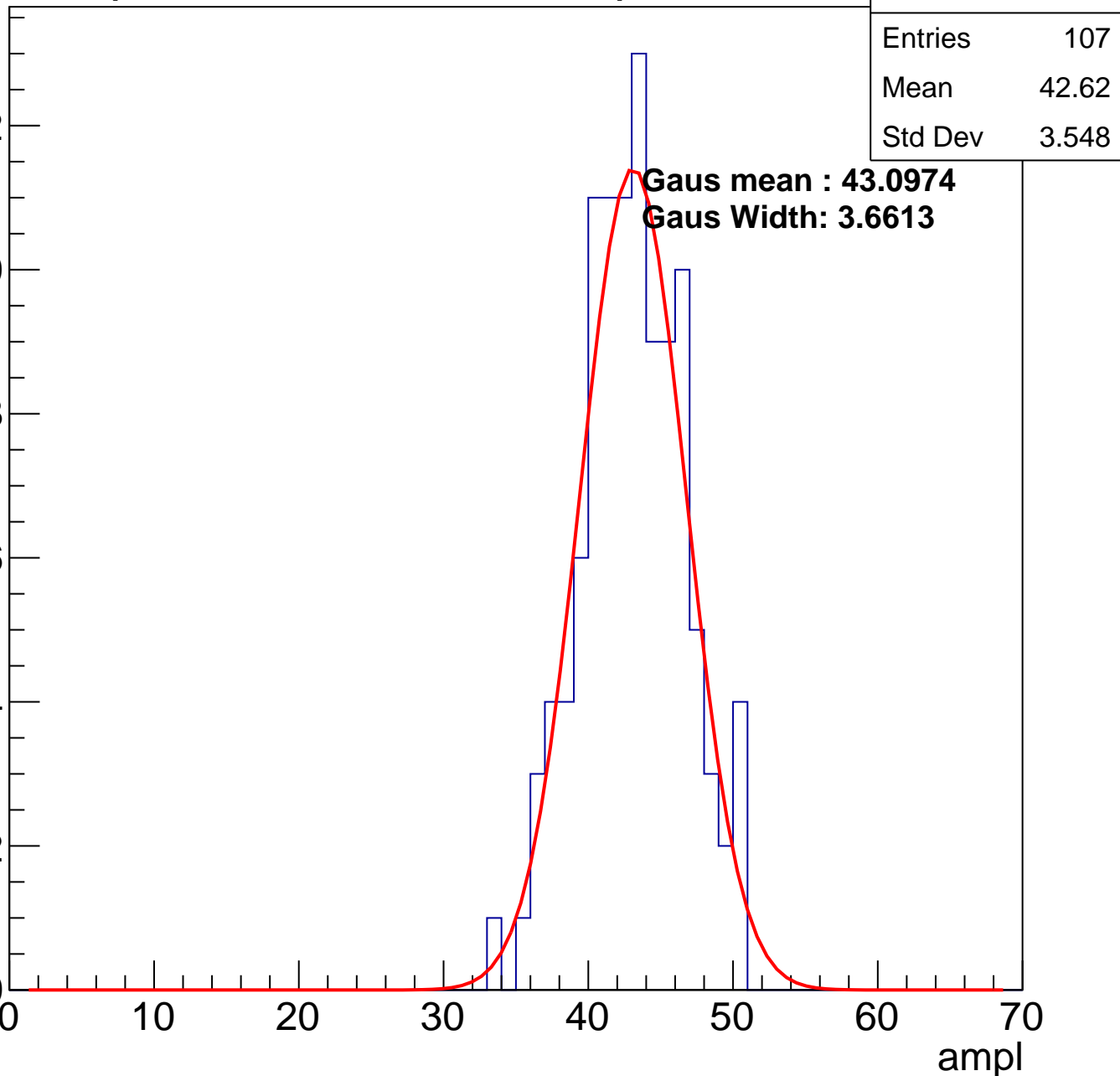
42.62

Std Dev

3.548

Gaus mean : 43.0974

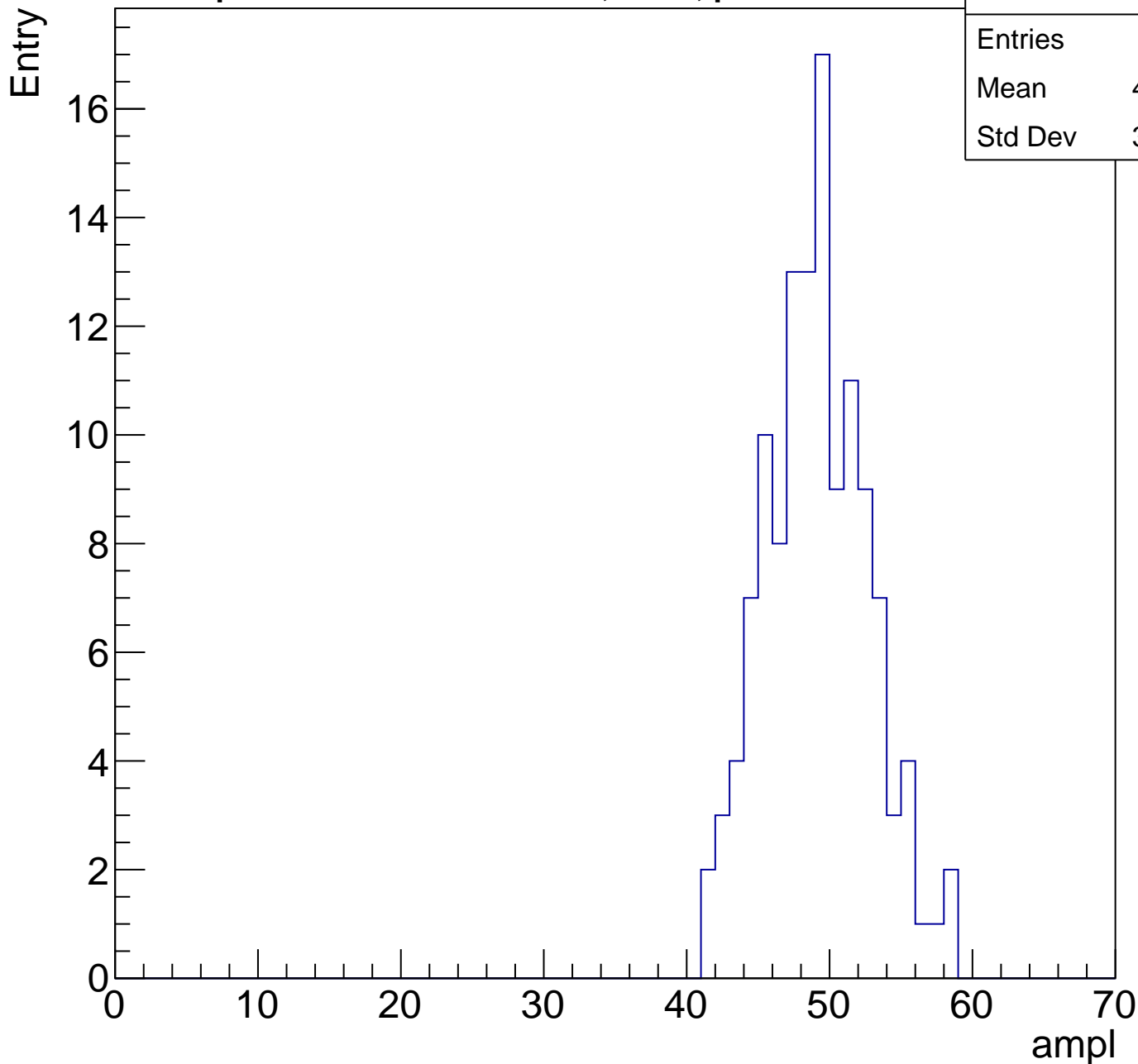
Gaus Width: 3.6613



B0L101S, U24-ch52, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	48.67
Std Dev	3.612



B0L101S, U24-ch52, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

140

Mean

54.99

Std Dev

3.729

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch52, adc5

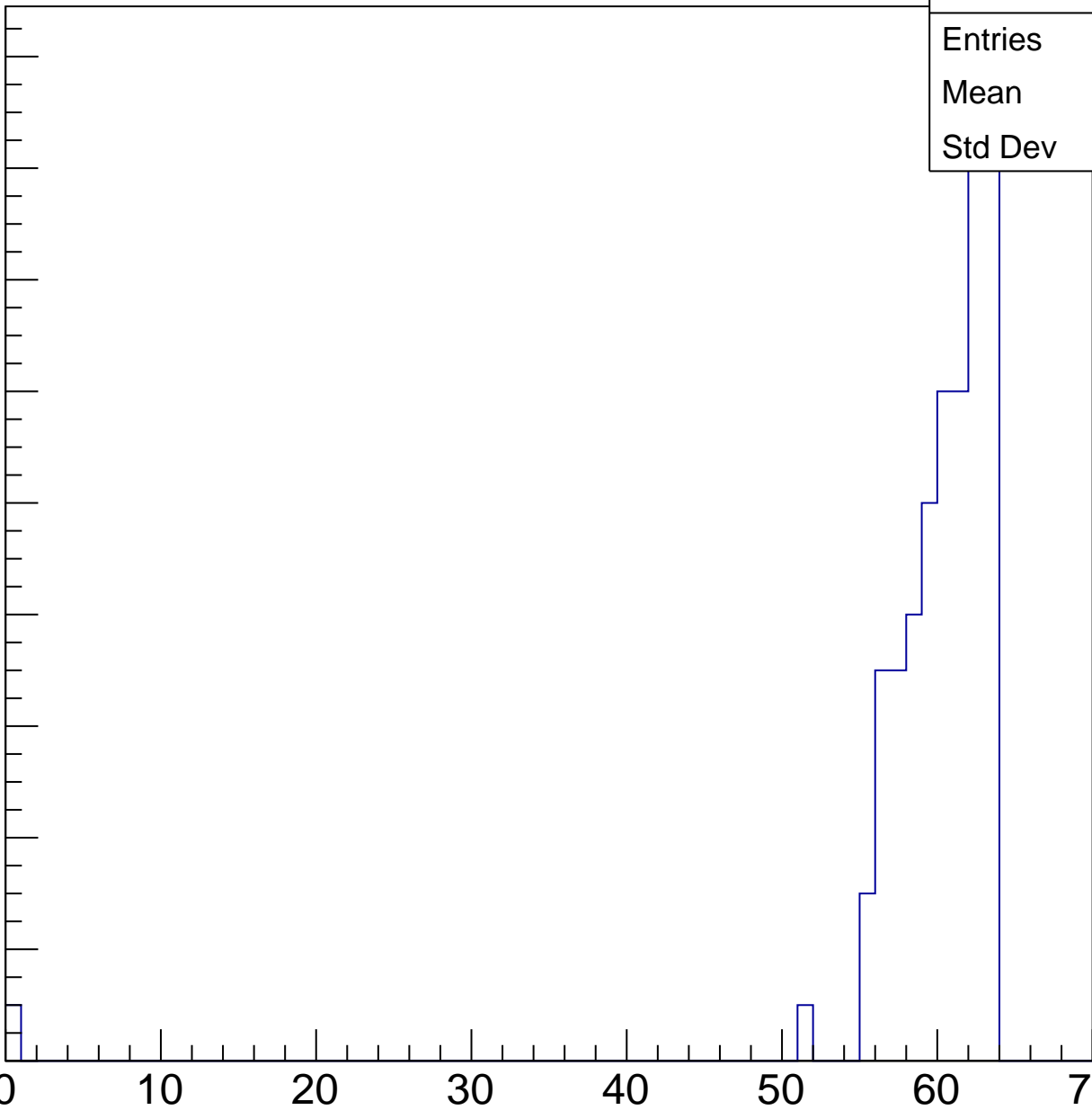
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.39
Std Dev	6.598

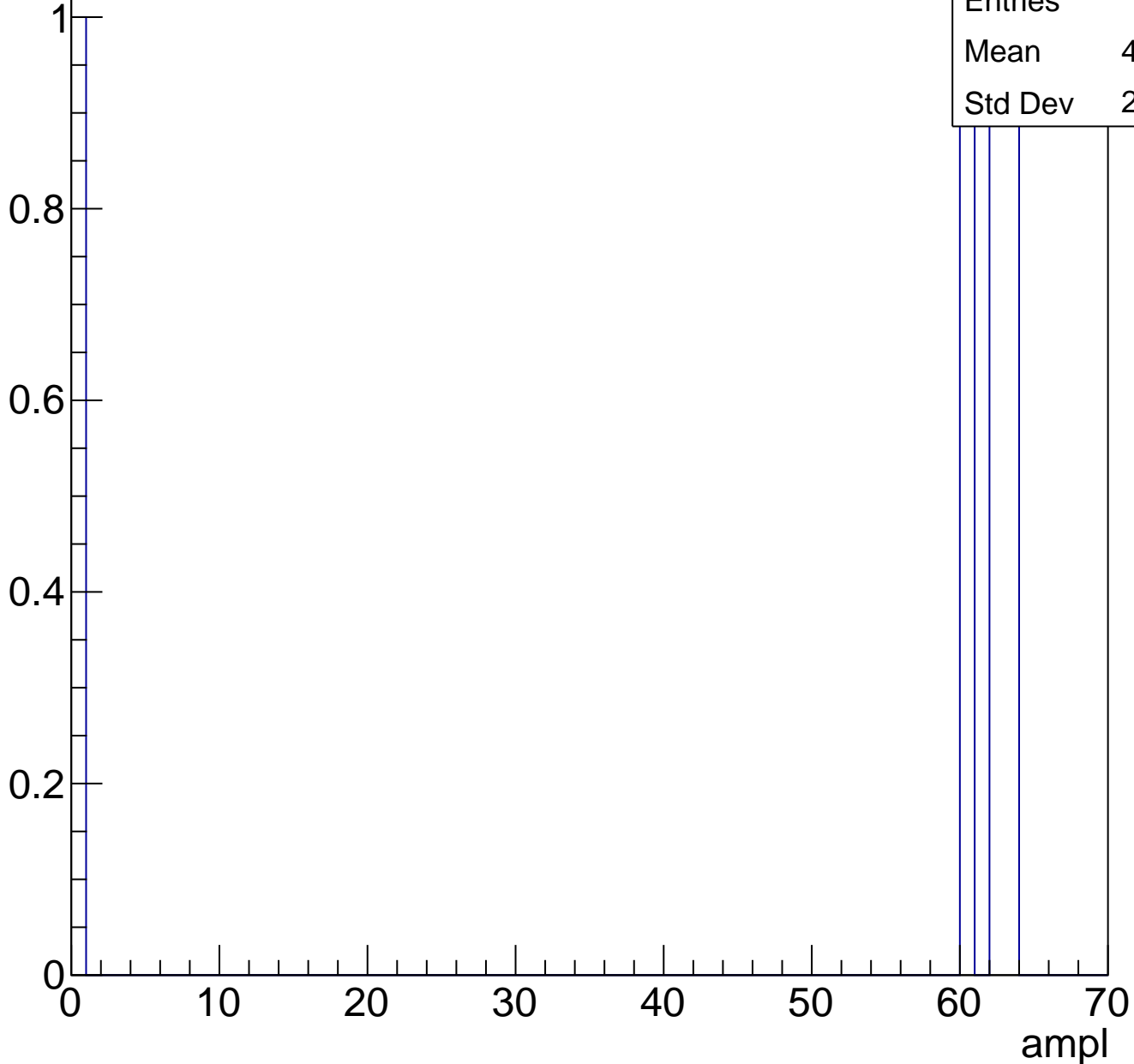
ampl



B0L101S, U24-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch53, adc0

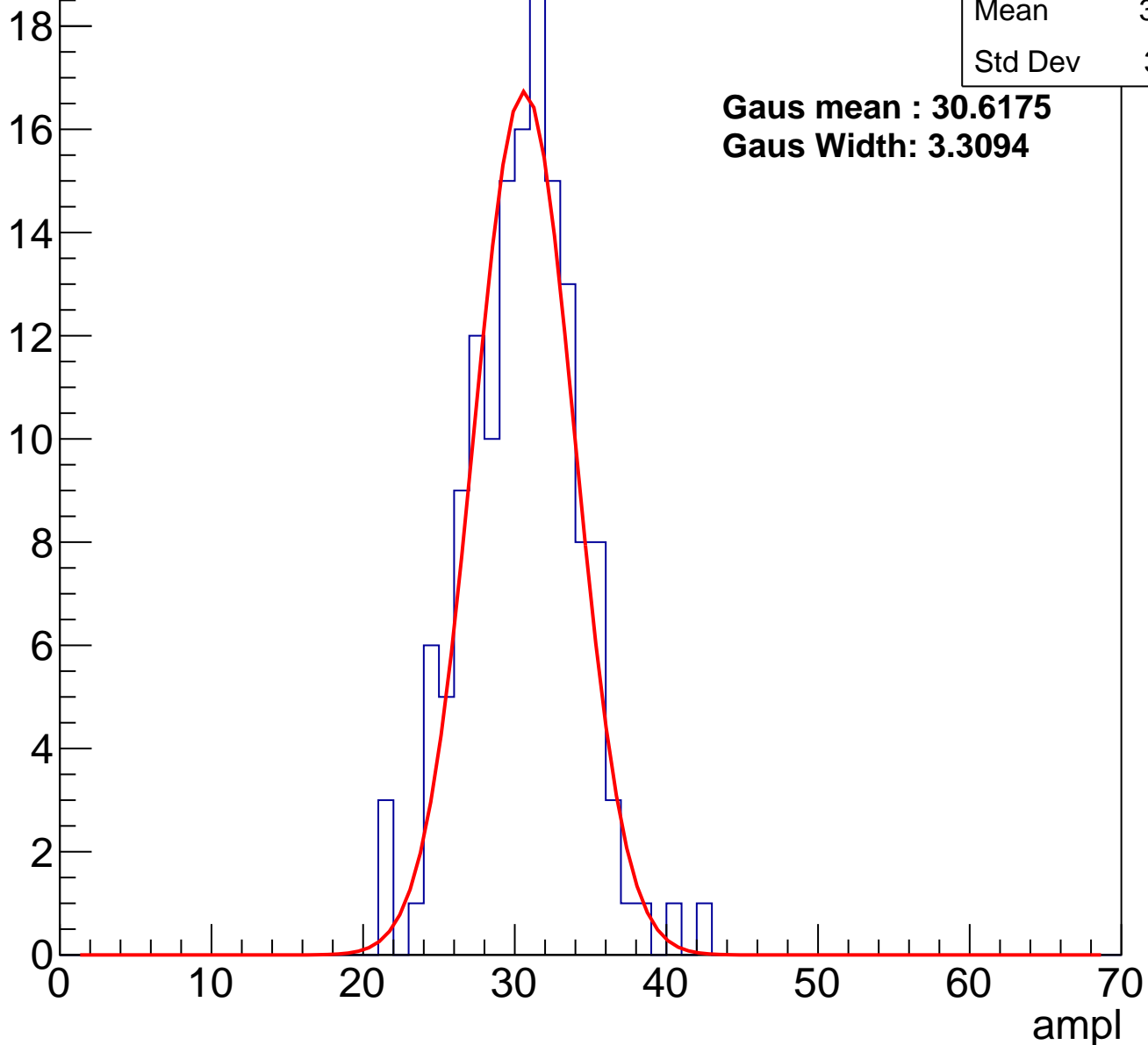
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	30.09
Std Dev	3.621

Gaus mean : 30.6175

Gaus Width: 3.3094

Entry



B0L101S, U24-ch53, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	37.74
Std Dev	3.42

Gaus mean : 38.1760

Gaus Width: 3.2942

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

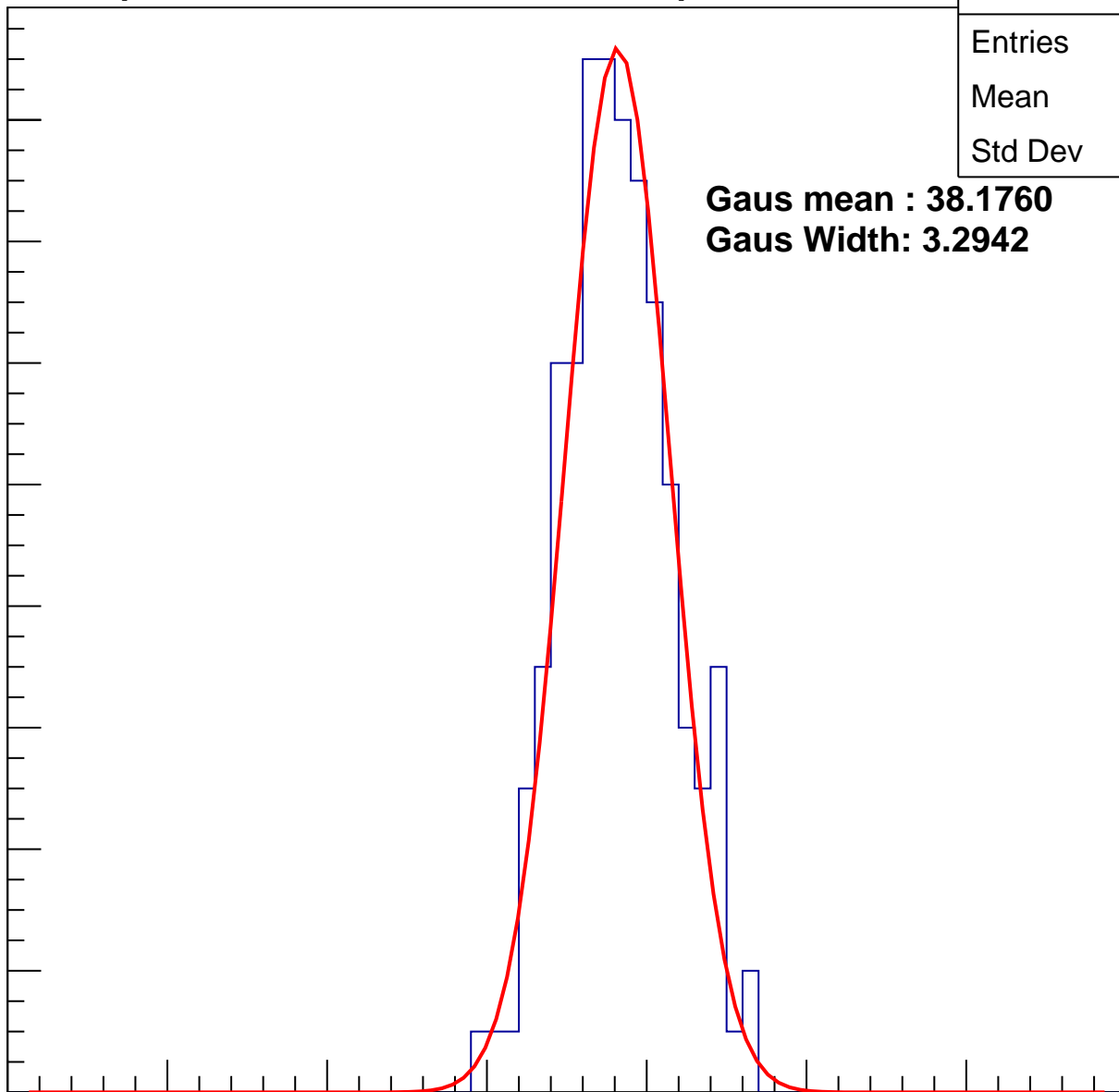
40

50

60

70

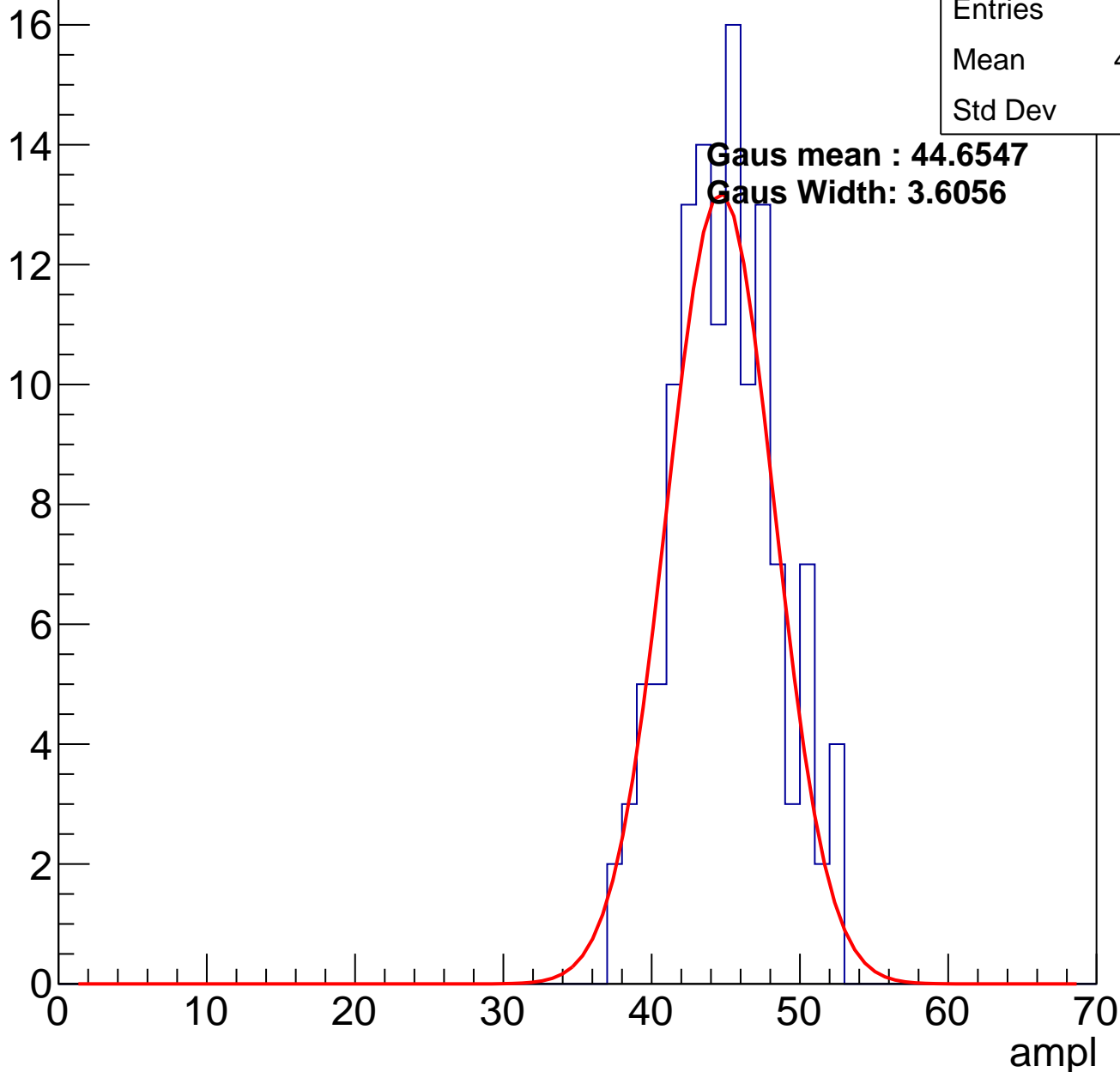
ampl



B0L101S, U24-ch53, adc2

calib_packv5_042523_0143.root, FC#1, port C1

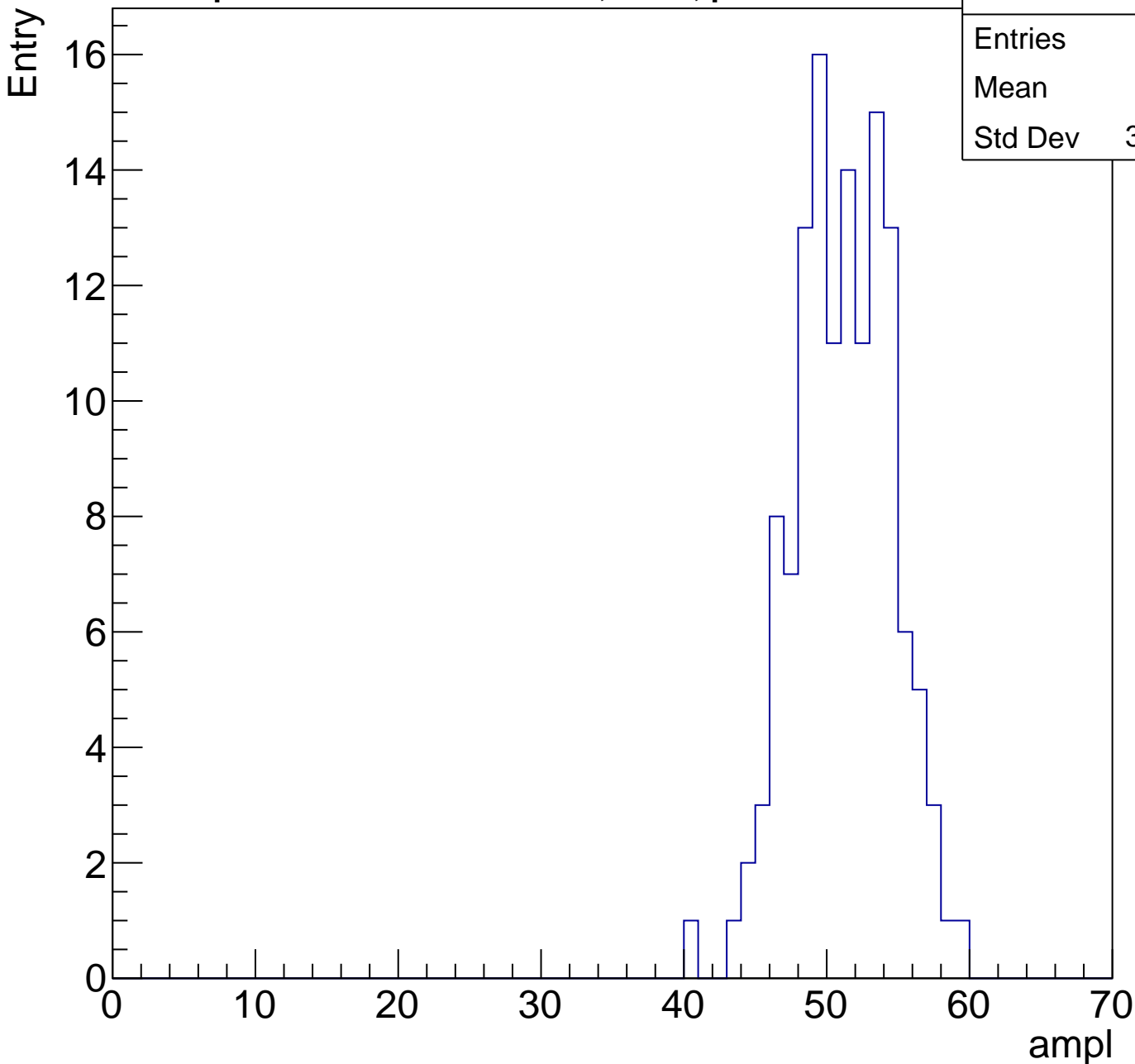
Entry



B0L101S, U24-ch53, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	50.7
Std Dev	3.399

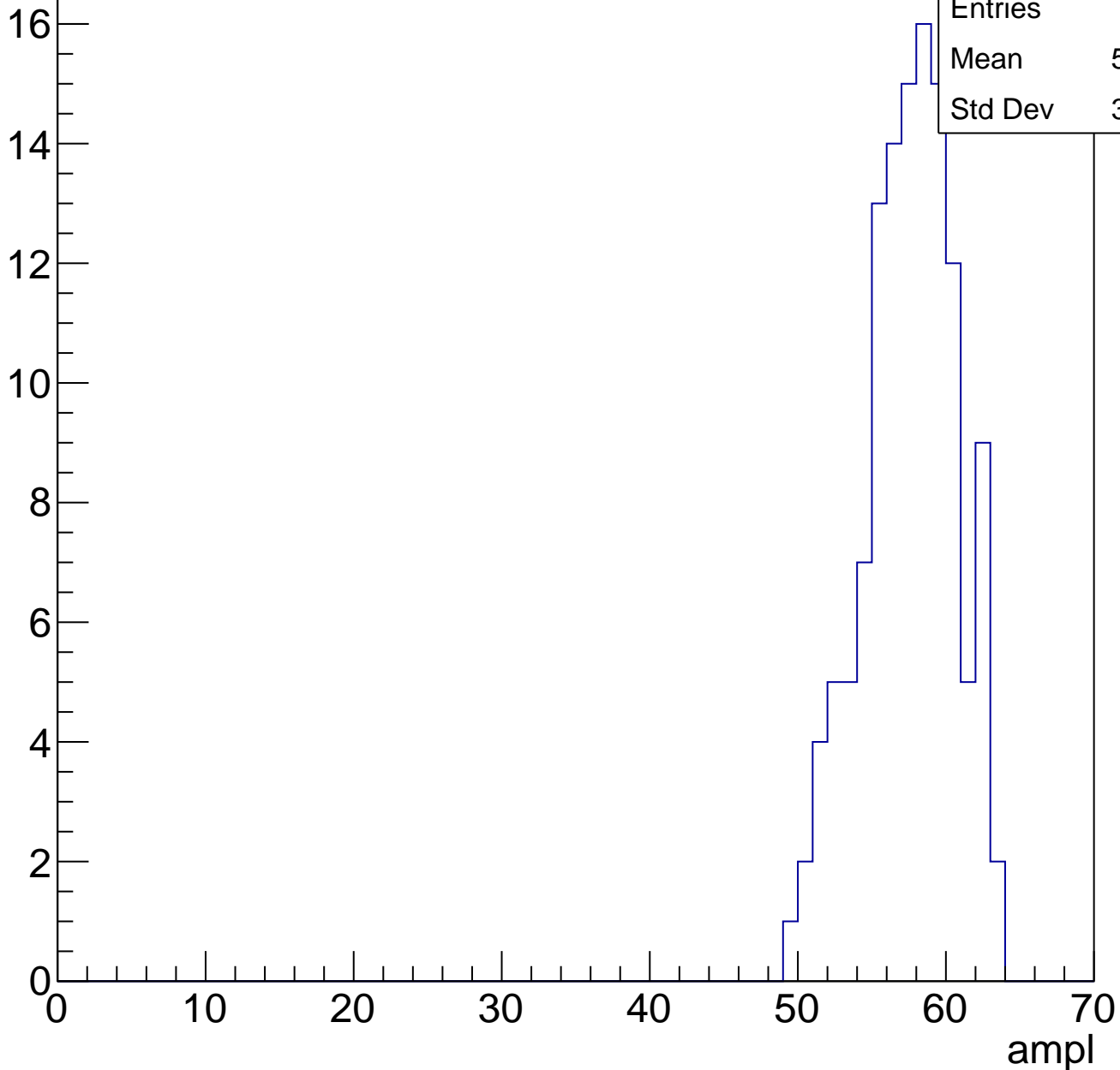


B0L101S, U24-ch53, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	57.06
Std Dev	3.099

Entry



B0L101S, U24-ch53, adc5

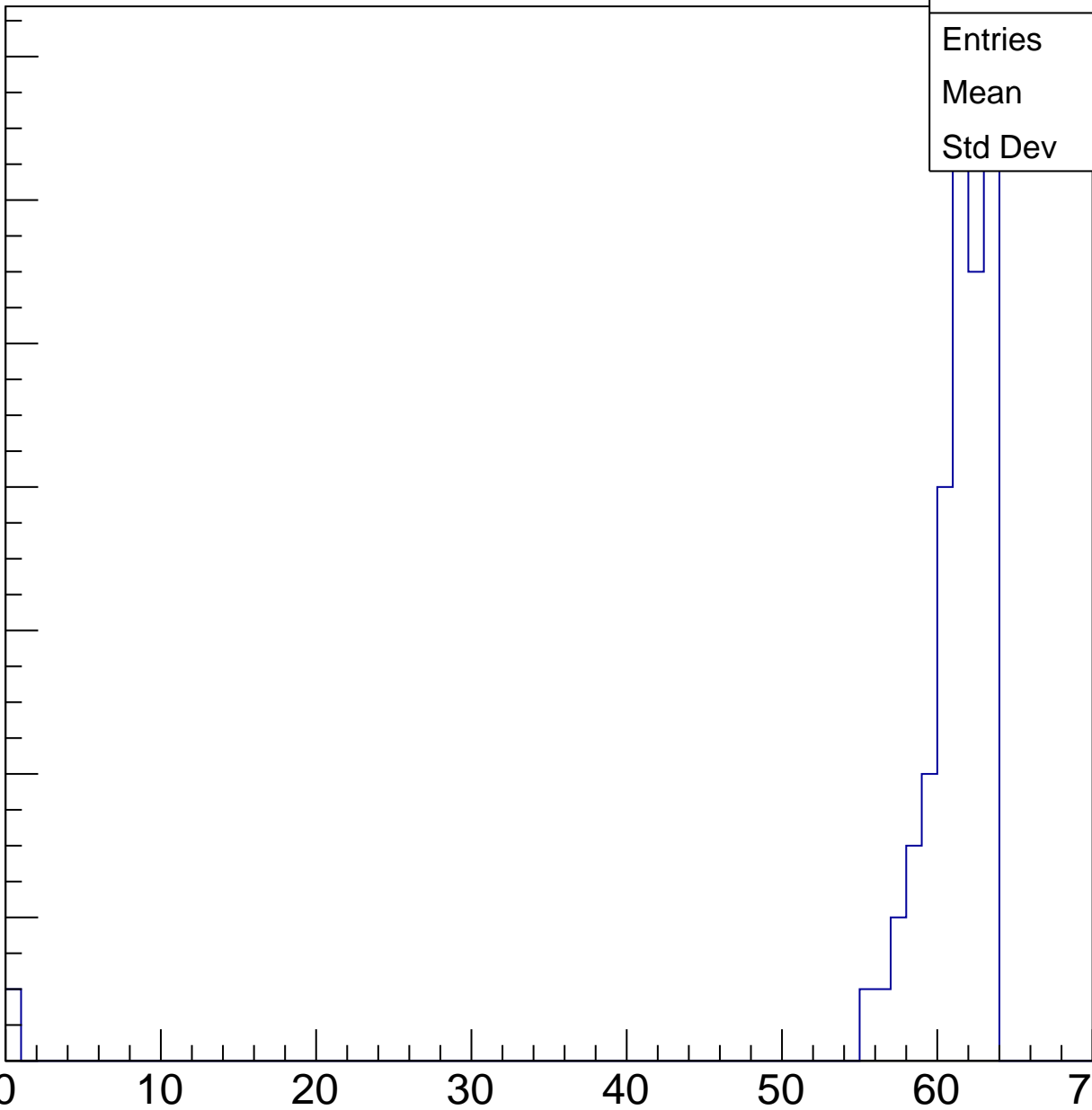
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.88
Std Dev	8.087

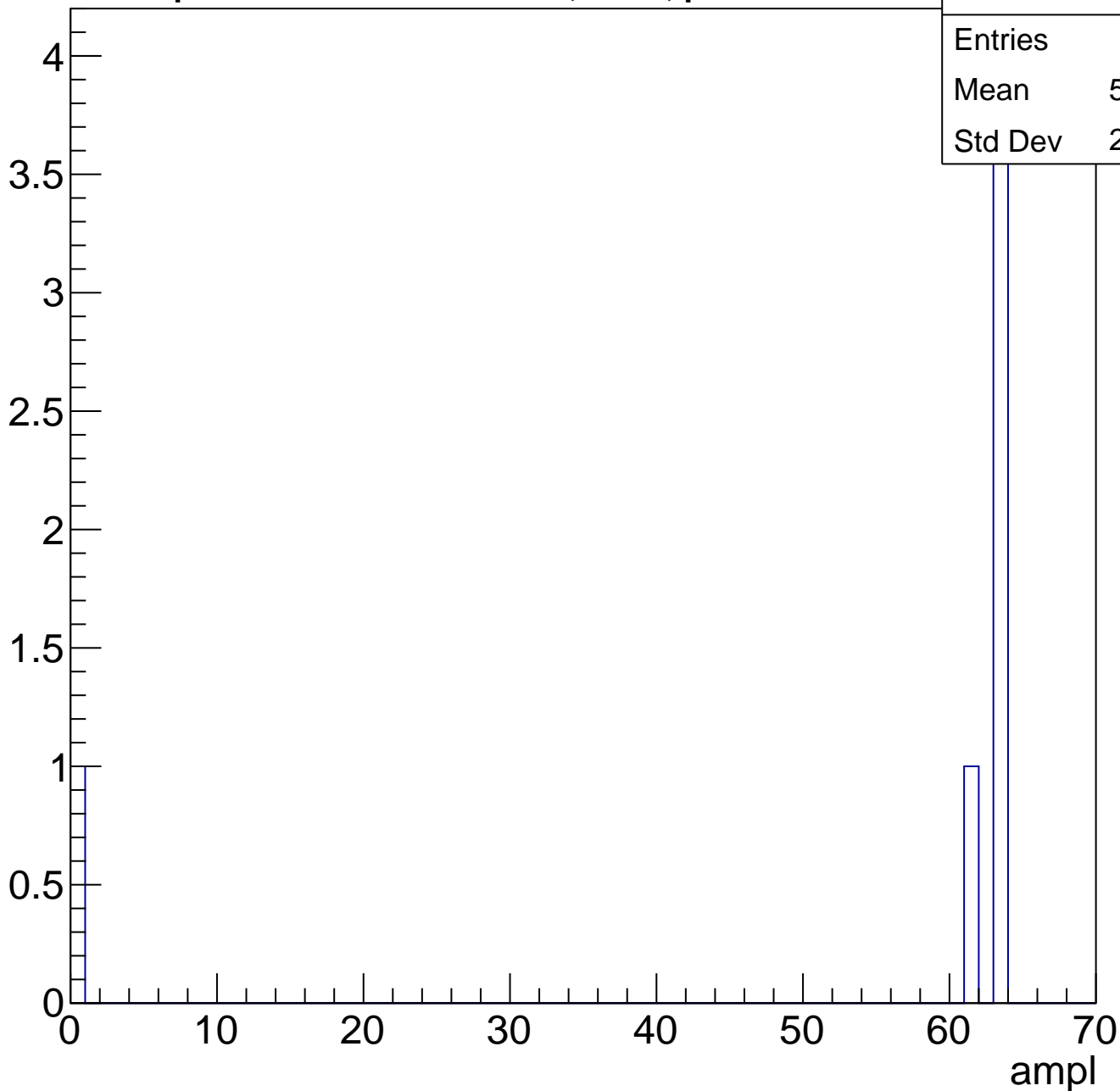
ampl



B0L101S, U24-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

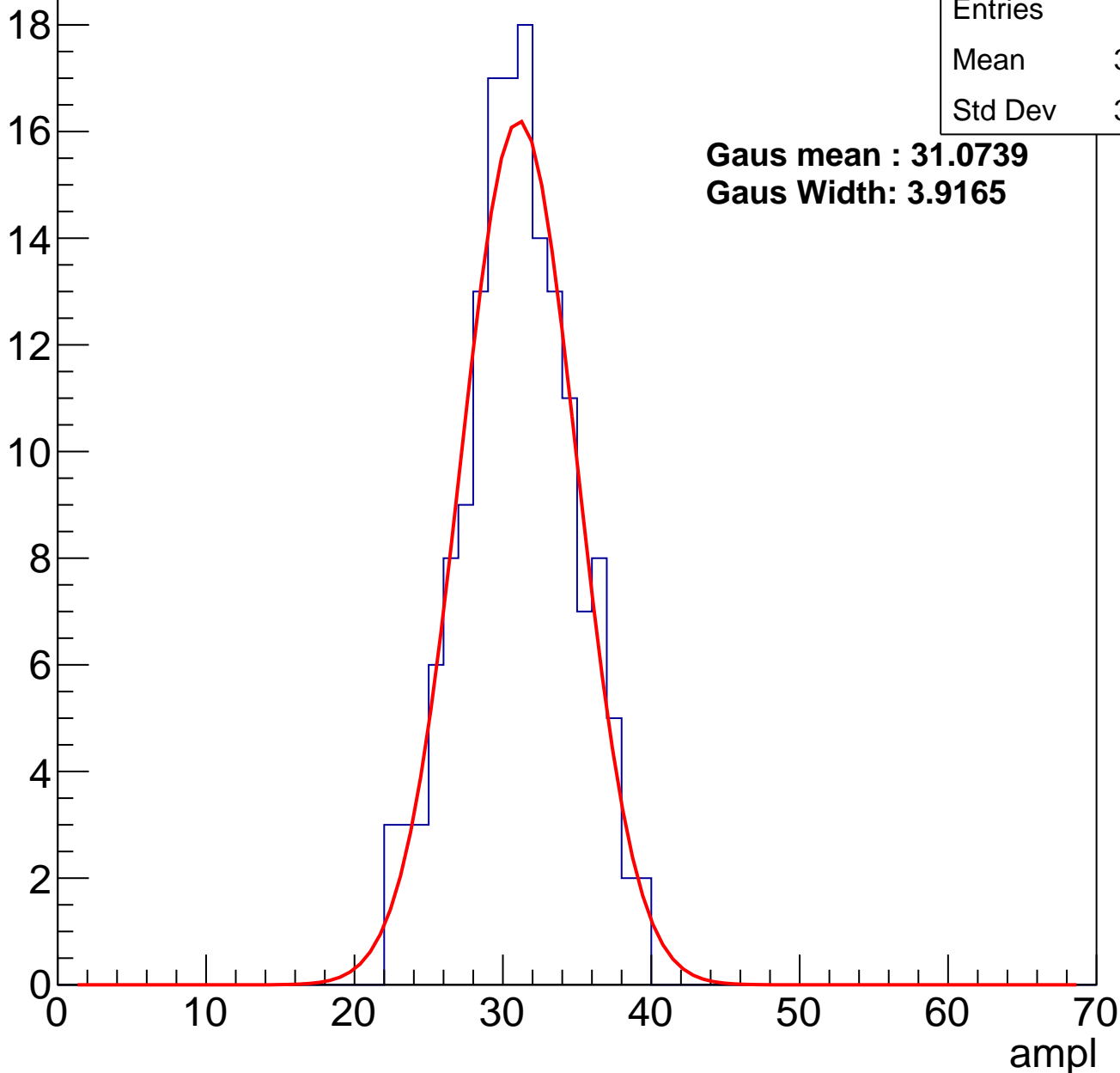


Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch54, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch54, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	37.56
Std Dev	4.794

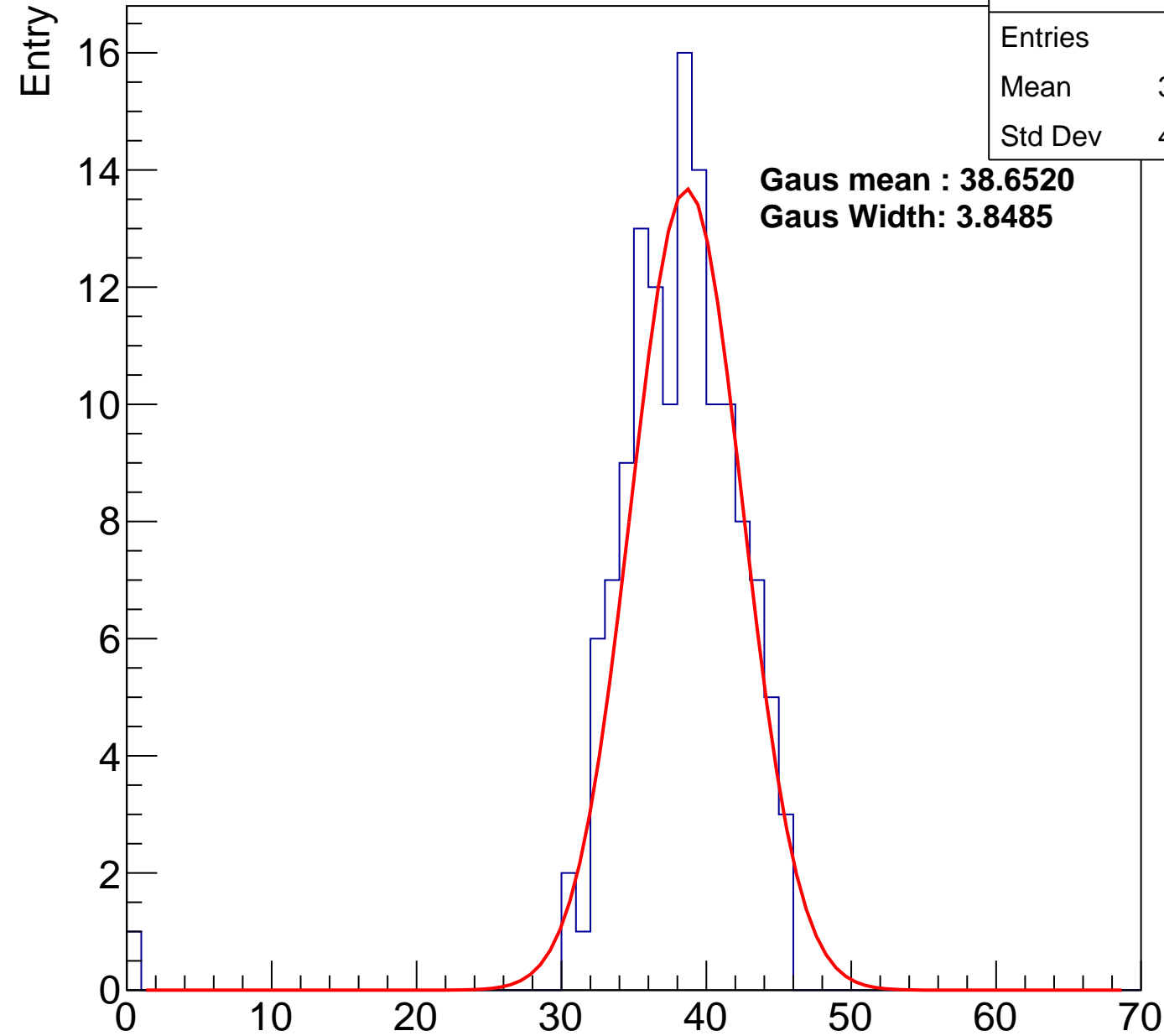
Gaus mean : 38.6520

Gaus Width: 3.8485

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	127
Mean	44.27
Std Dev	3.612

Gaus mean : 44.8844

Gaus Width: 3.8875

0

2

4

6

8

10

12

Entry

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	44.27
Std Dev	3.612

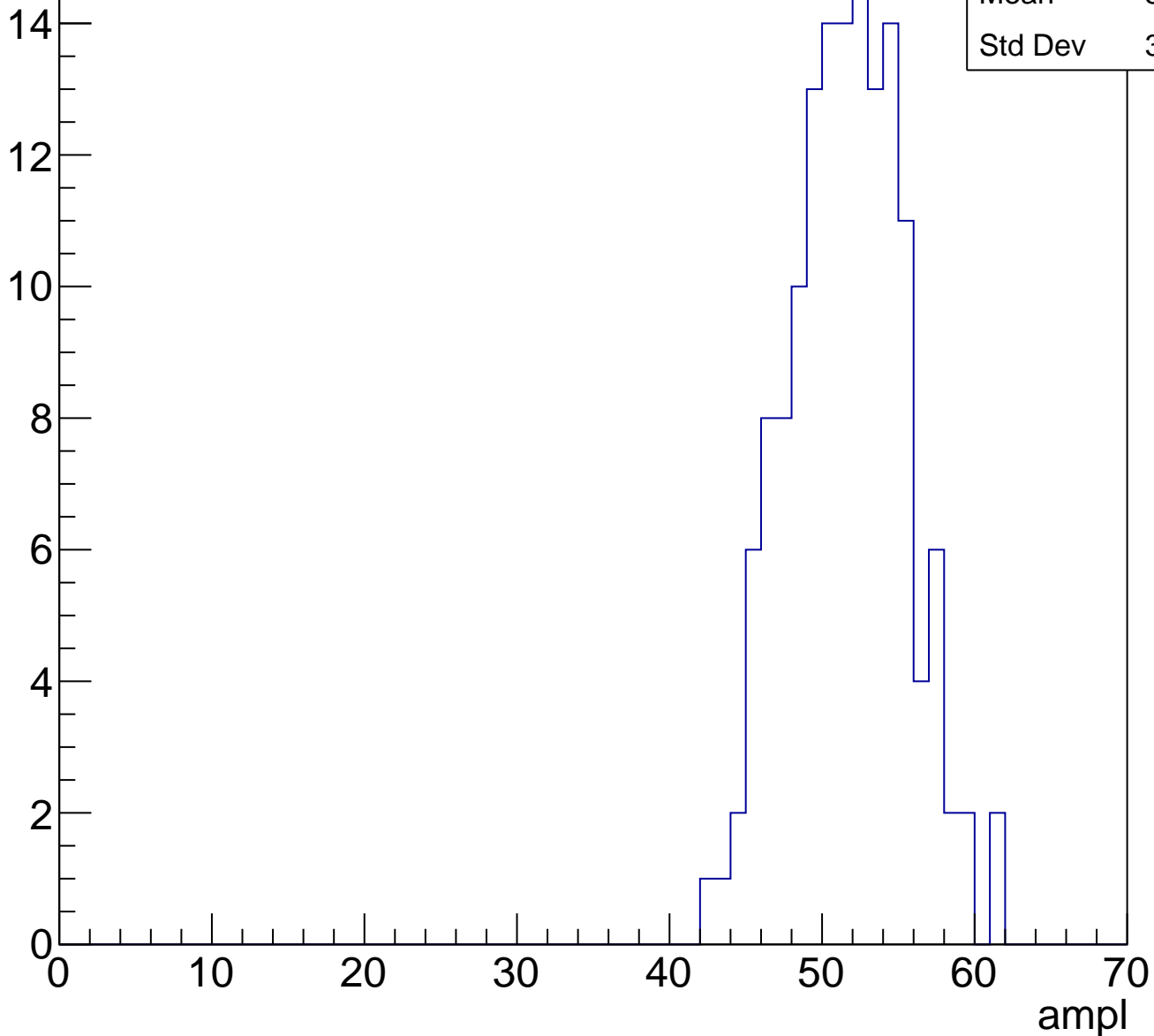
Gaus mean : 44.8844

Gaus Width: 3.8875

B0L101S, U24-ch54, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	146
Mean	51.16
Std Dev	3.747

B0L101S, U24-ch54, adc4

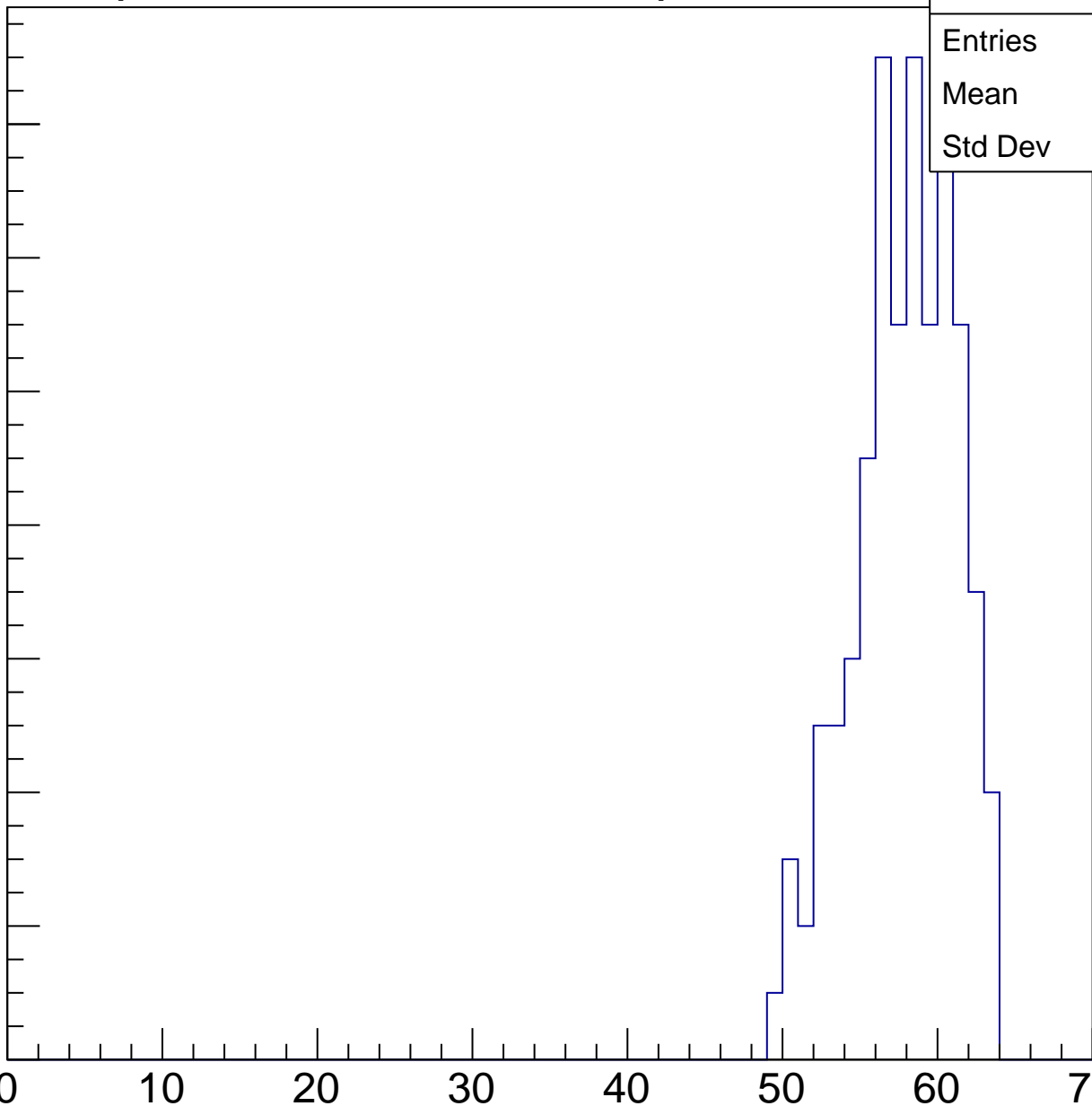
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	57.4
Std Dev	3.244

ampl



B0L101S, U24-ch54, adc5

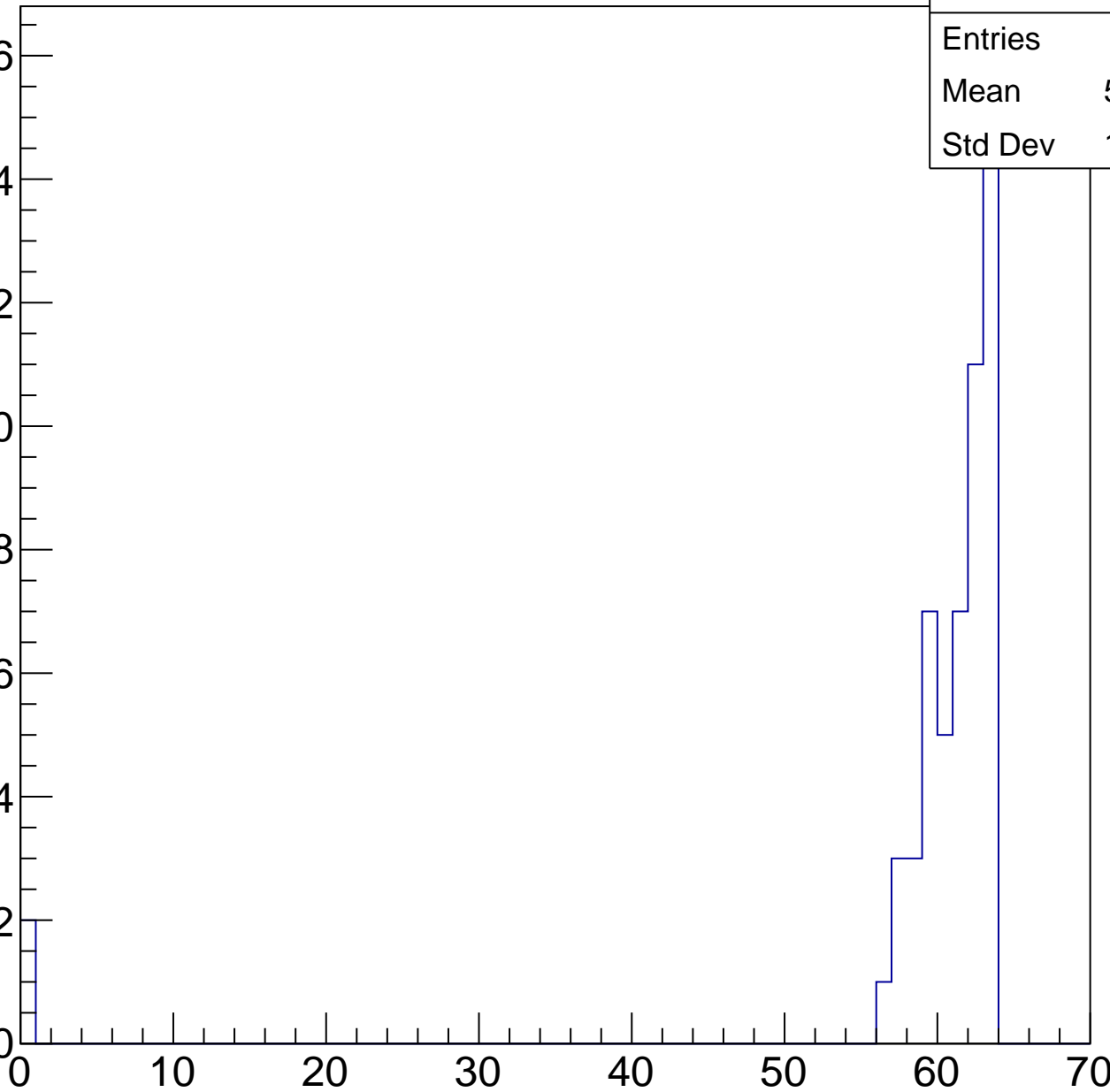
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	55
Mean	58.75
Std Dev	11.58

ampl



B0L101S, U24-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch55, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	30.12
Std Dev	3.529

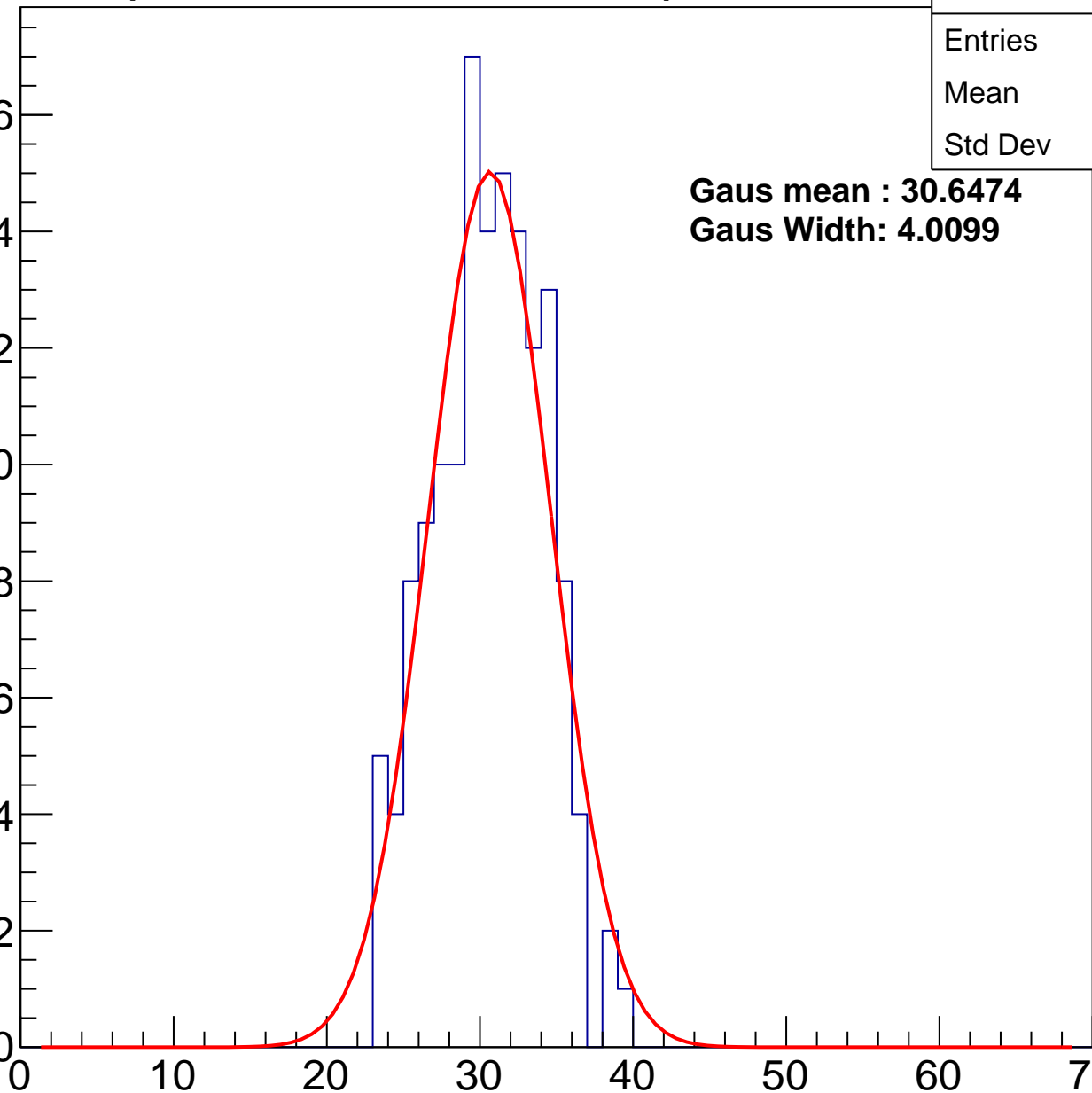
Gaus mean : 30.6474

Gaus Width: 4.0099

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch55, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

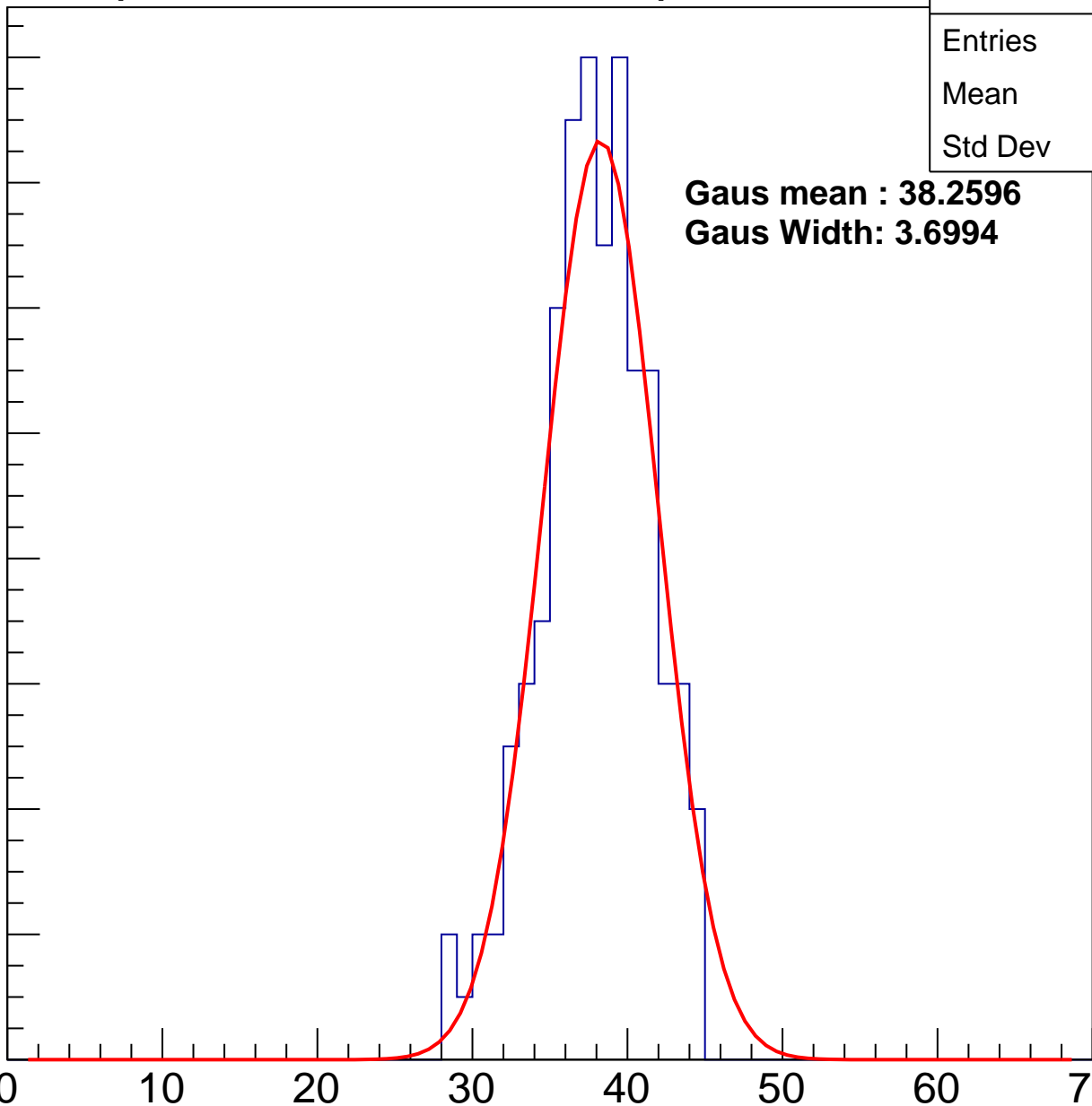
16
14
12
10
8
6
4
2
0

Entries	135
Mean	37.41
Std Dev	3.48

Gaus mean : 38.2596

Gaus Width: 3.6994

0 10 20 30 40 50 60 70
ampl



B0L101S, U24-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	43.77
Std Dev	3.529

Gaus mean : 44.8017

Gaus Width: 3.6958

Entry

16

14

12

10

8

6

4

2

0

0

10

20

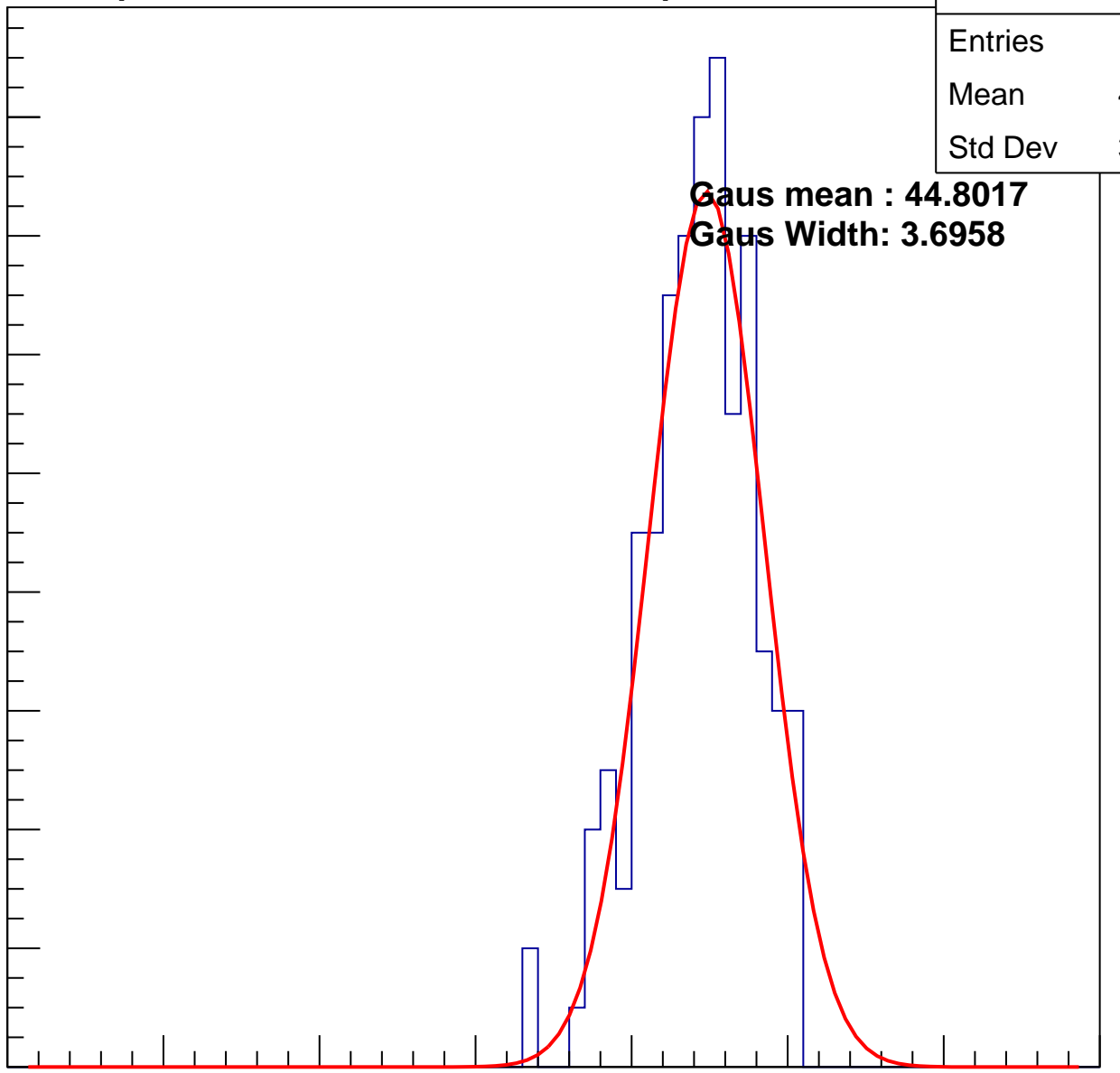
30

40

50

60

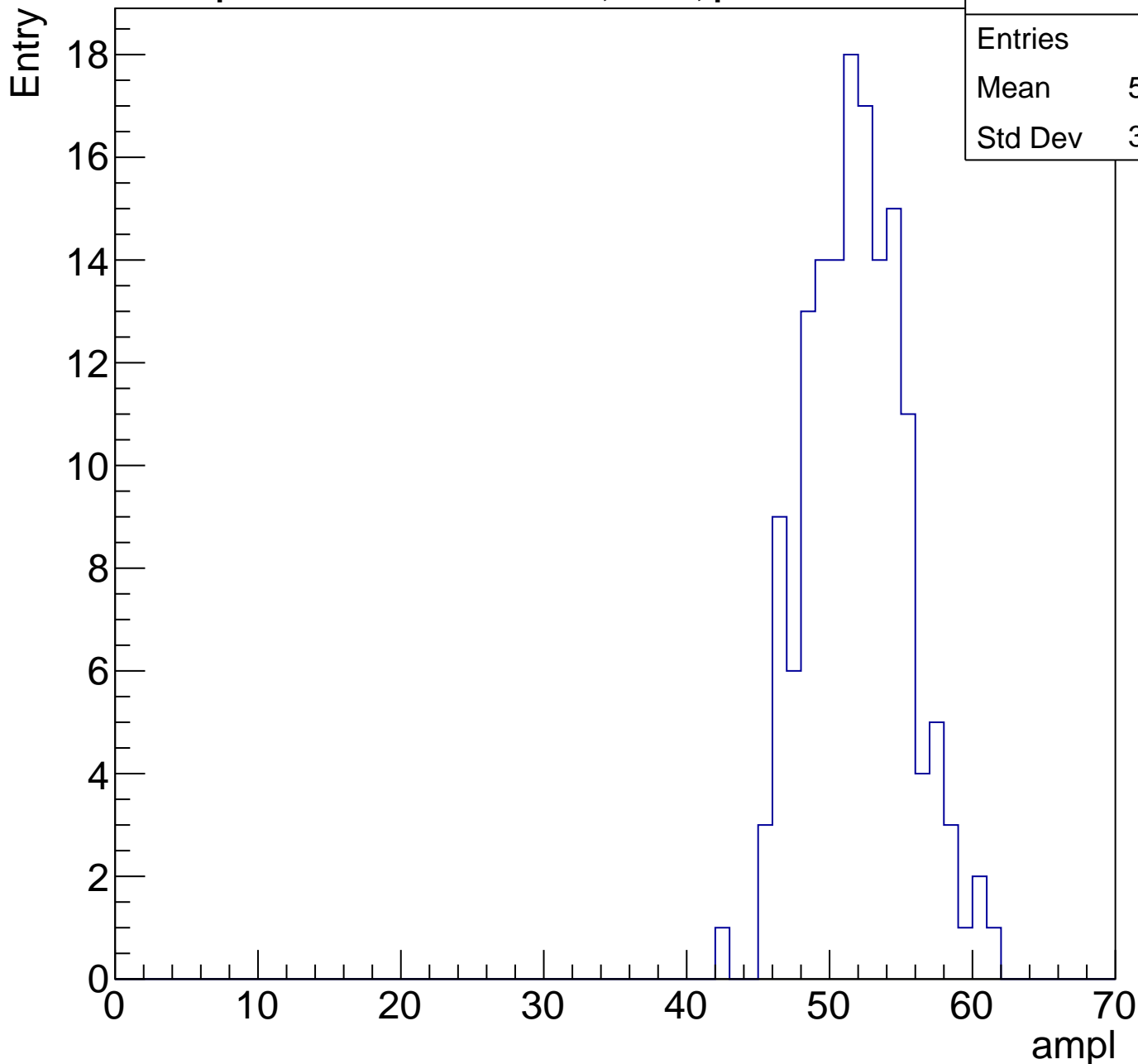
ampl



B0L101S, U24-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

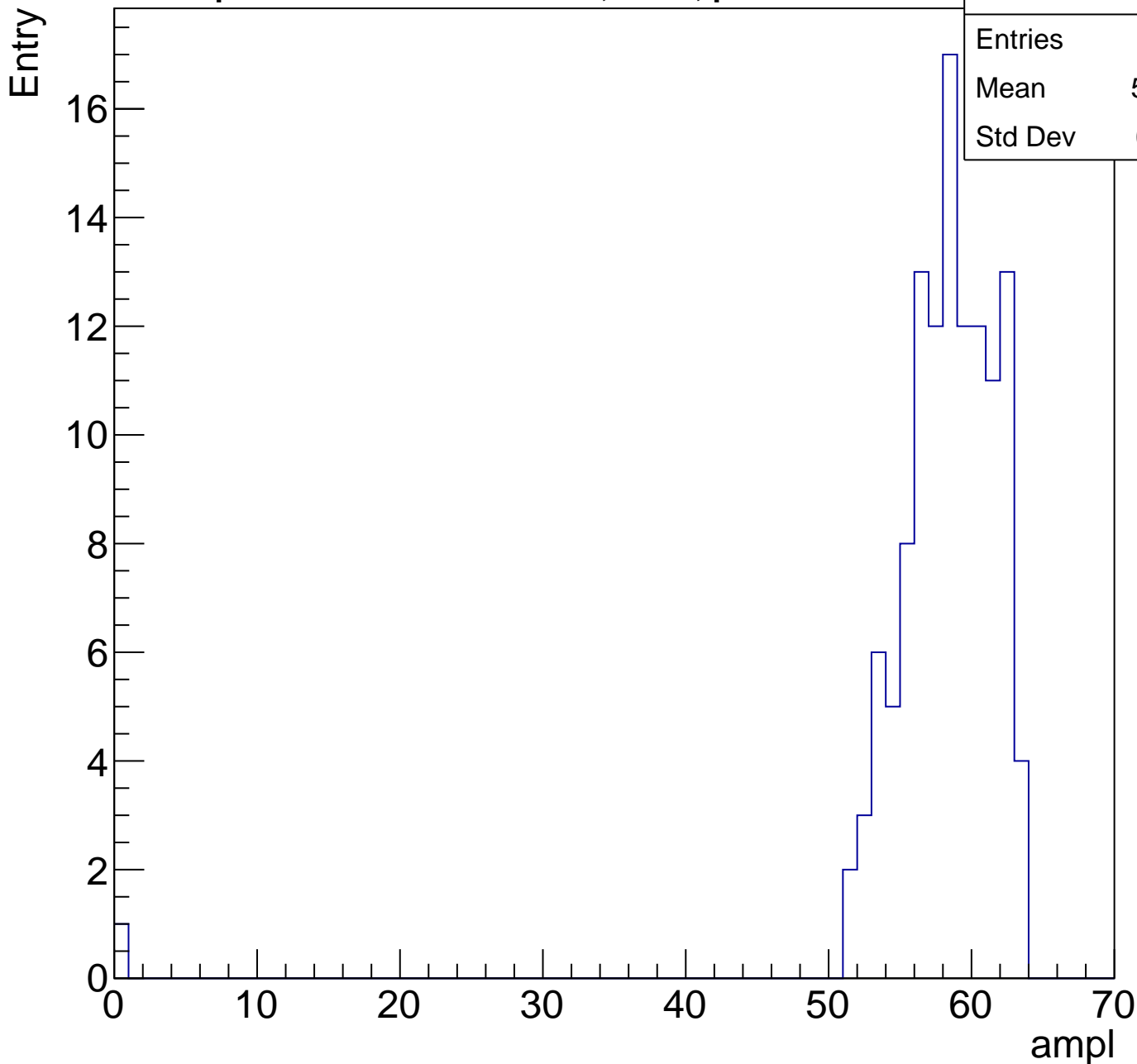
Entries	151
Mean	51.42
Std Dev	3.459



B0L101S, U24-ch55, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	57.49
Std Dev	6.061



B0L101S, U24-ch55, adc5

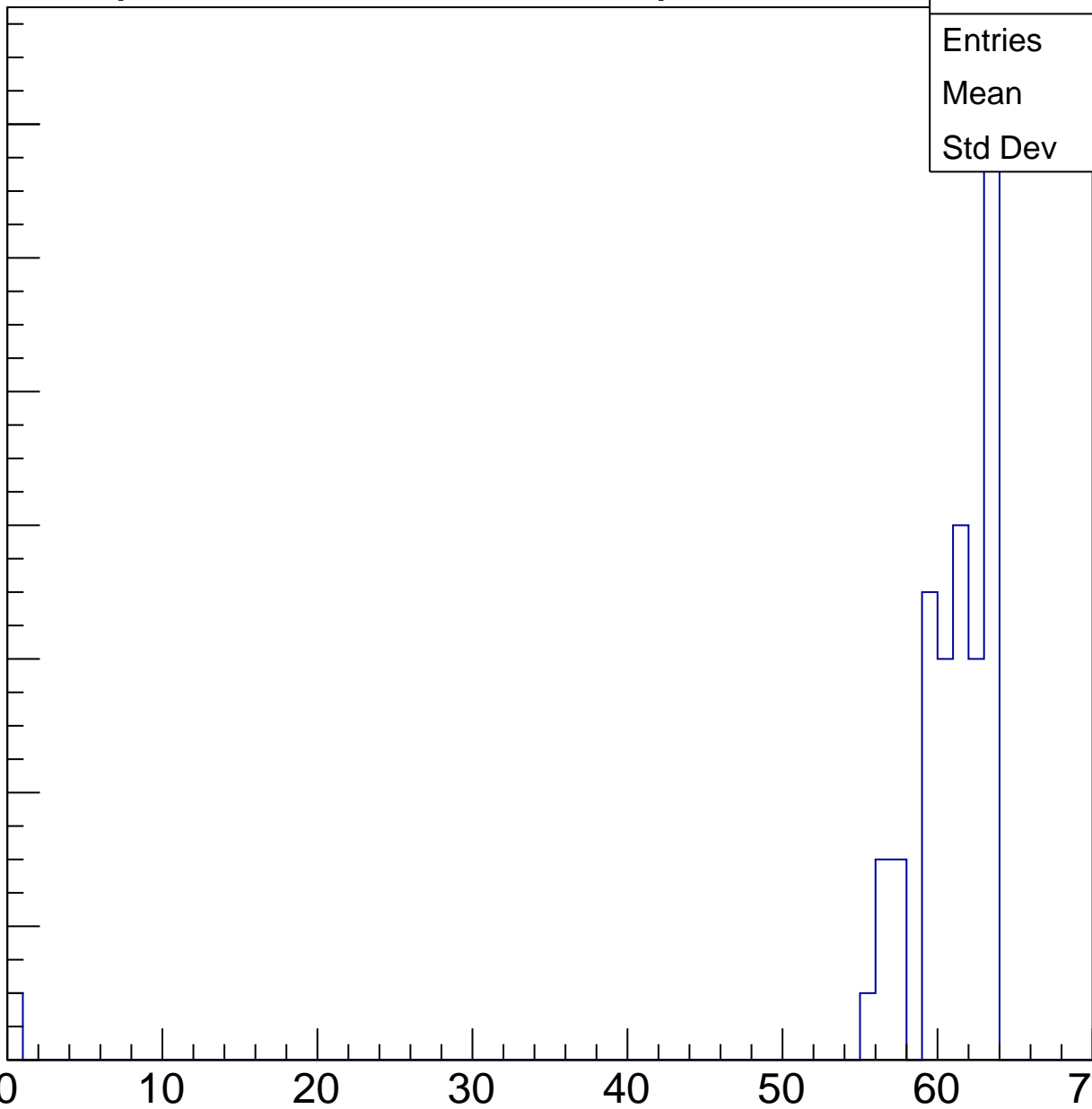
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	50
Mean	59.44
Std Dev	8.784

ampl



B0L101S, U24-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch56, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	29.58
Std Dev	5.066

Gaus mean : 30.7222

Gaus Width: 4.0609

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

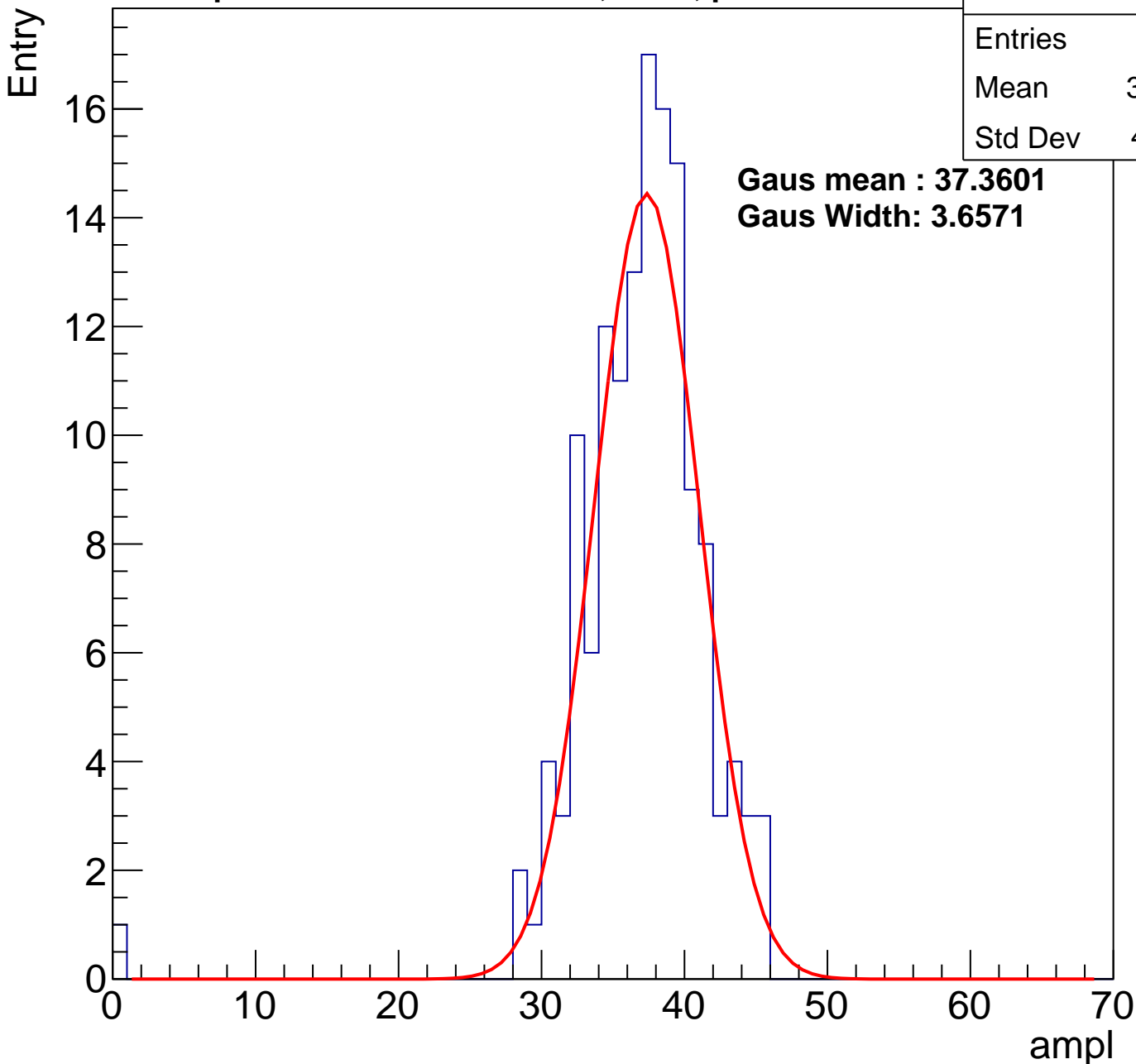
70

B0L101S, U24-ch56, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.54
Std Dev	4.781

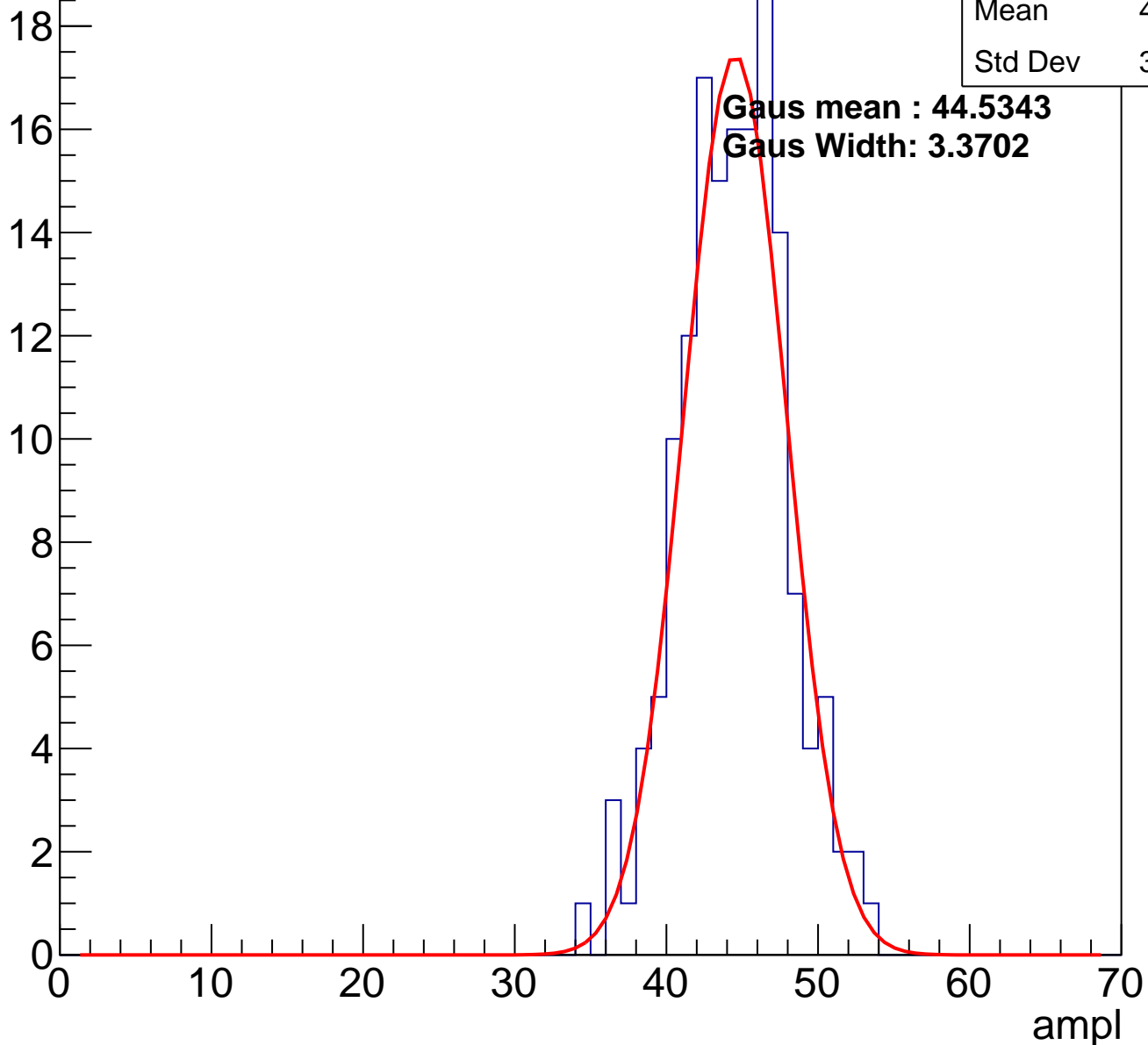
Gaus mean : 37.3601
Gaus Width: 3.6571



B0L101S, U24-ch56, adc2

calib_packv5_042523_0143.root, FC#1, port C1

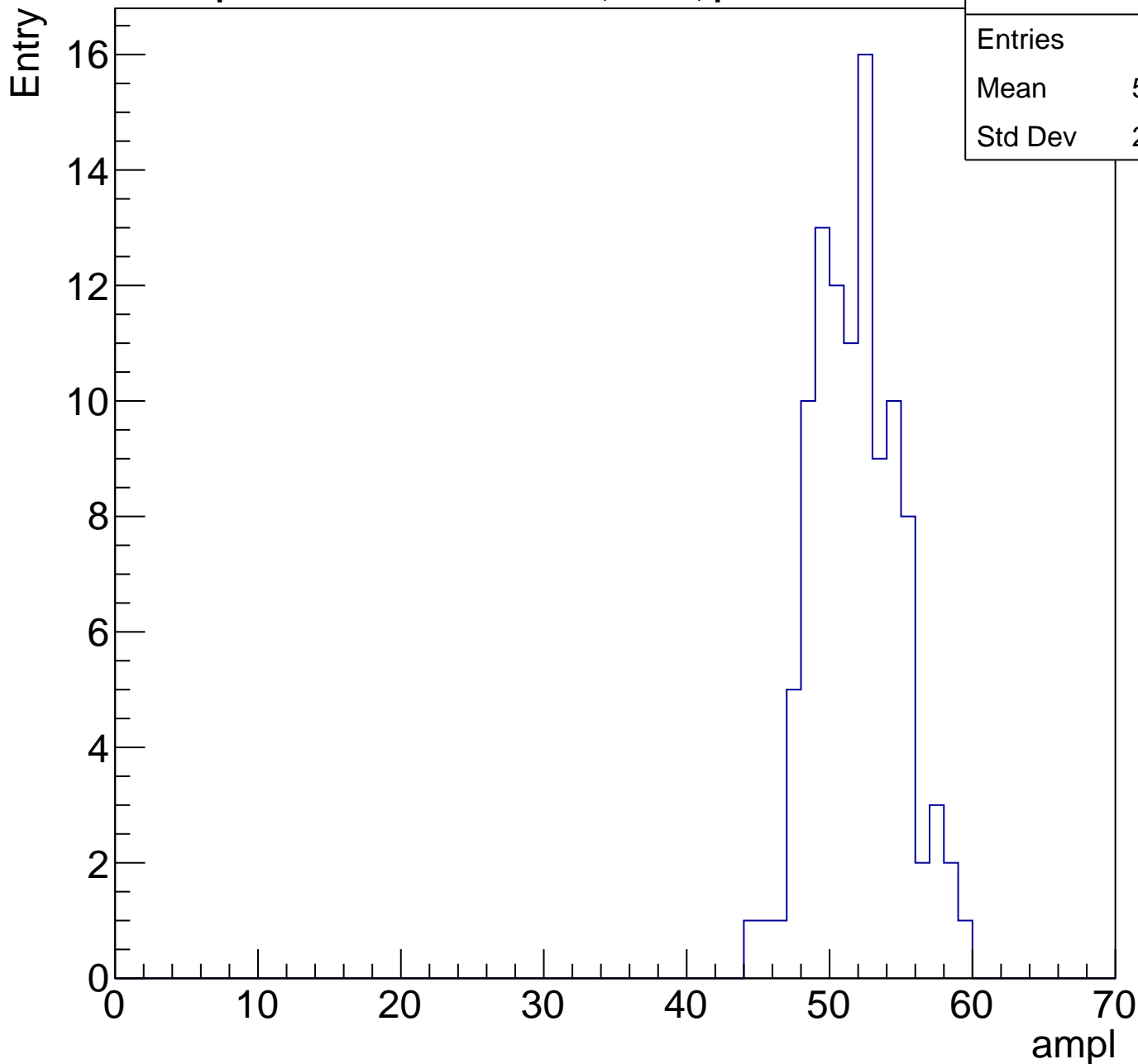
Entry



B0L101S, U24-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	51.38
Std Dev	2.958



B0L101S, U24-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

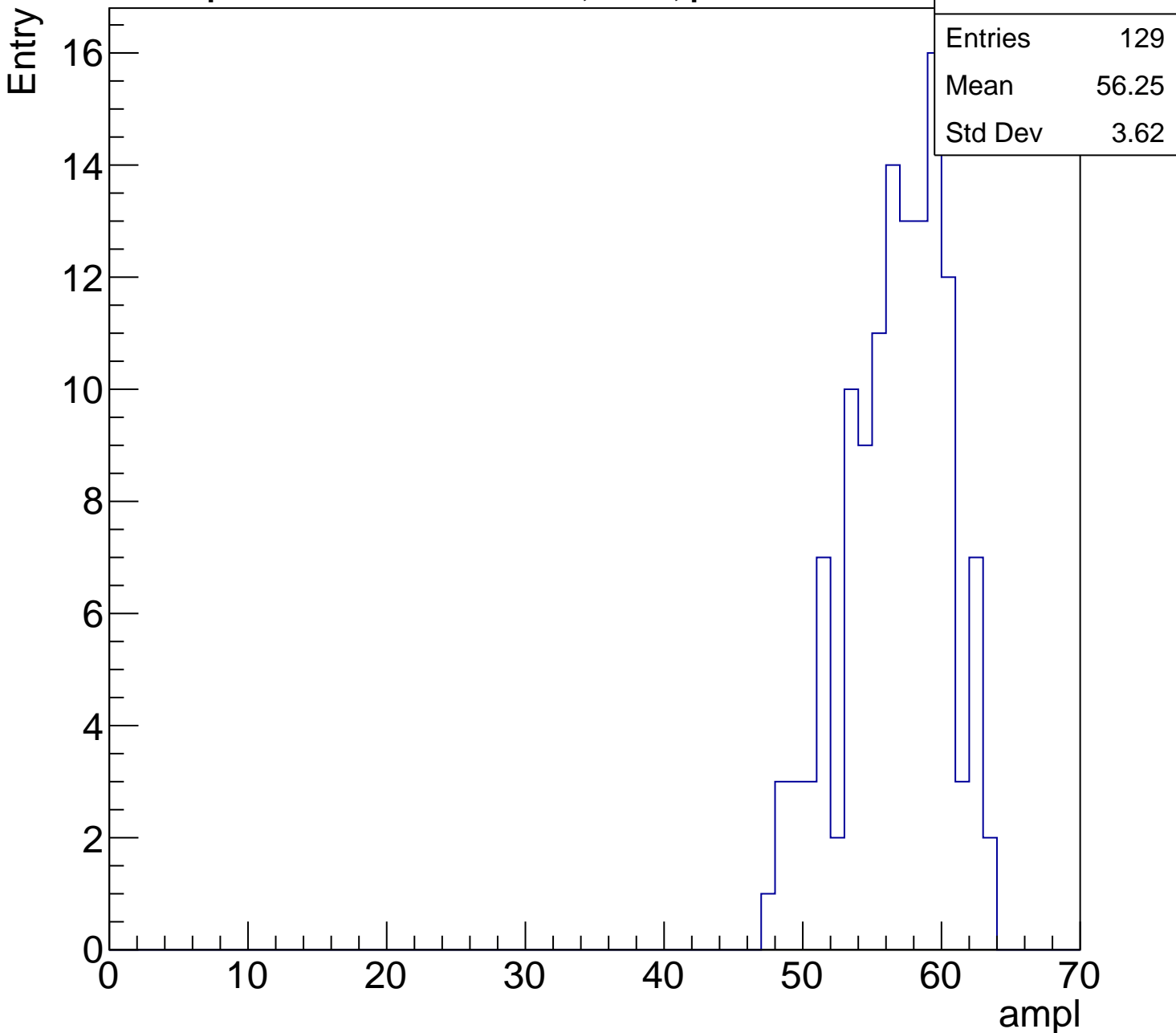
Entries	129
Mean	56.25
Std Dev	3.62

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch56, adc5

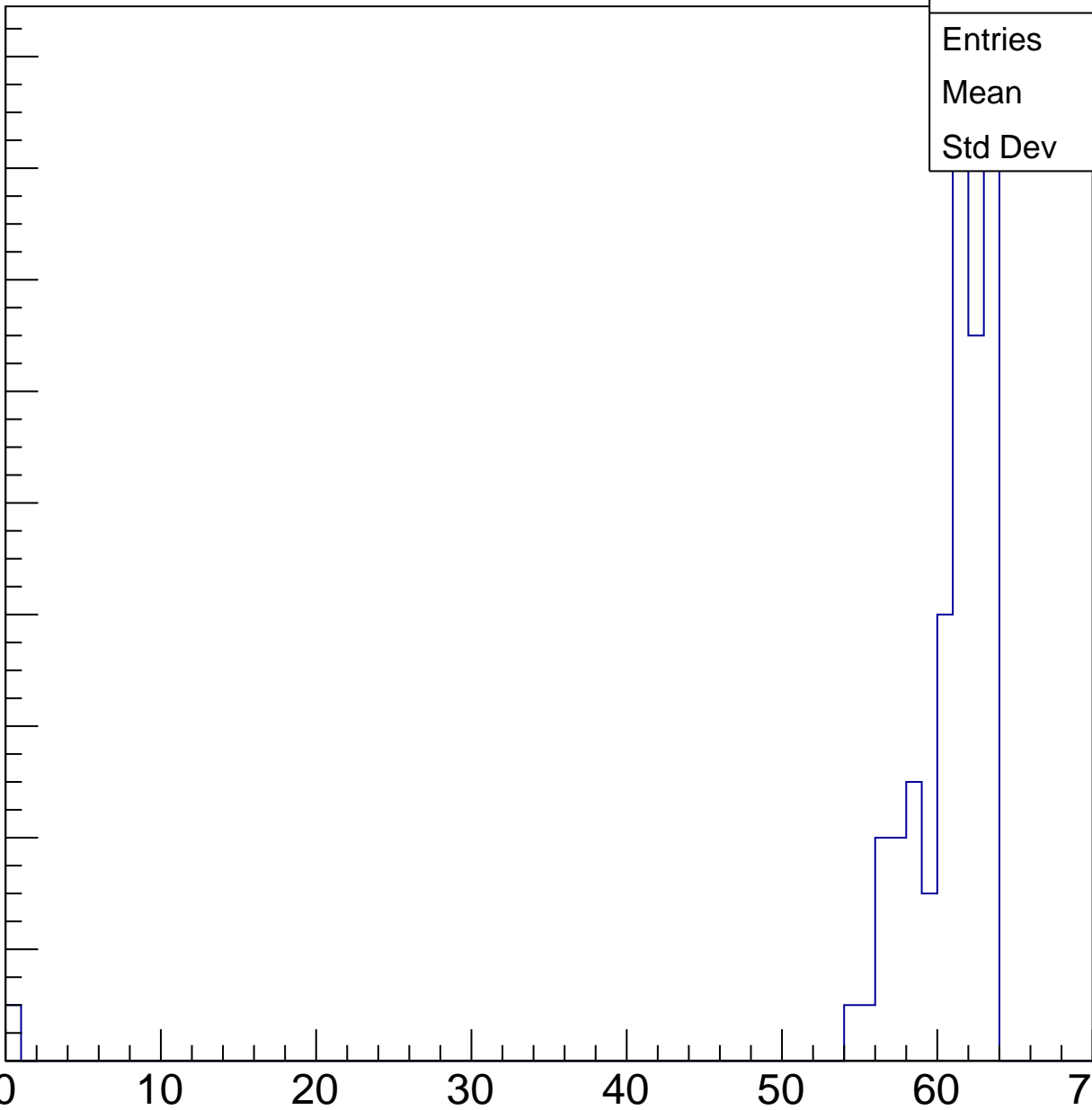
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.78
Std Dev	7.362

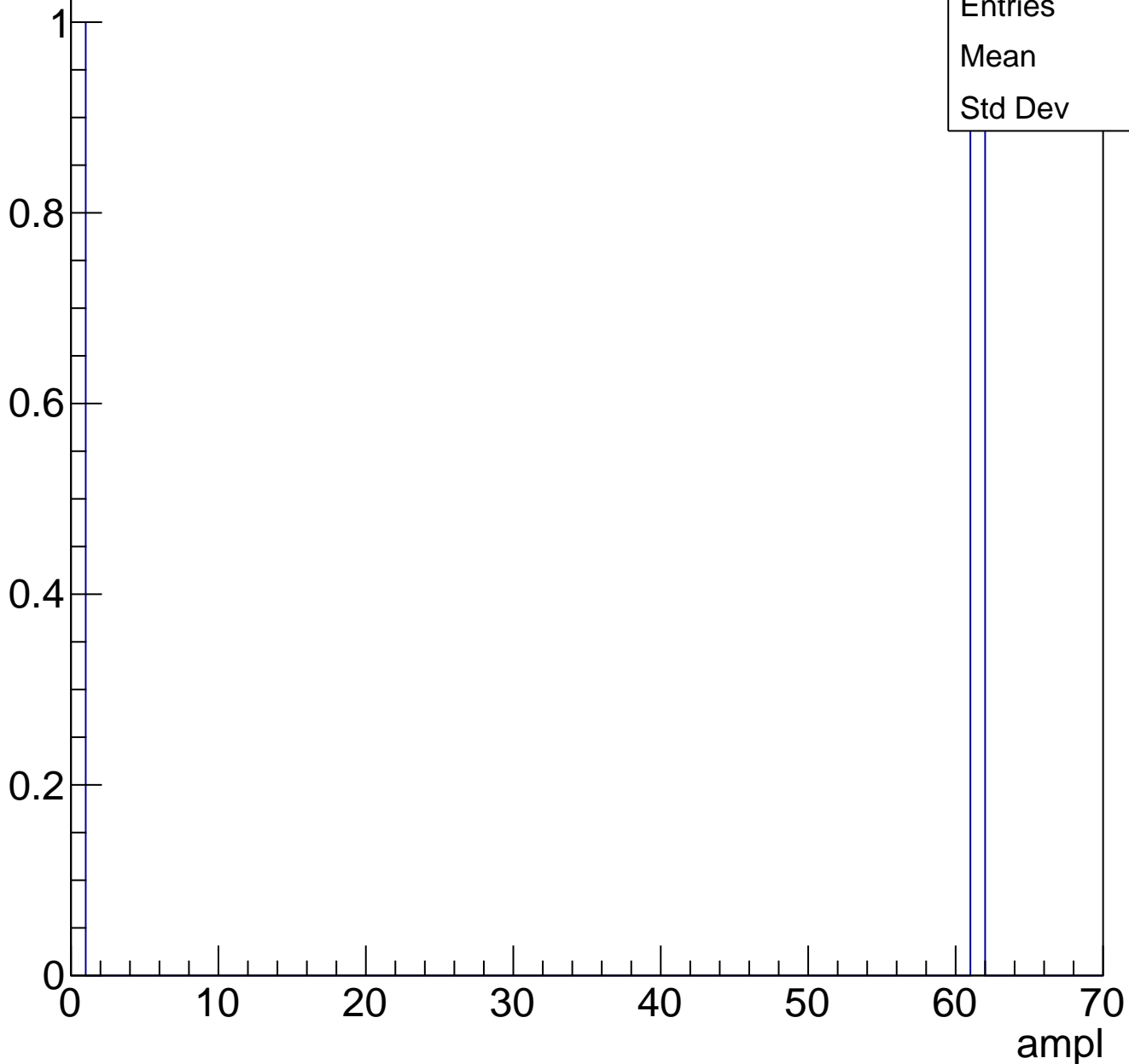
ampl



B0L101S, U24-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch57, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	30.11
Std Dev	4.295

Gaus mean : 30.9609

Gaus Width: 3.1227

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

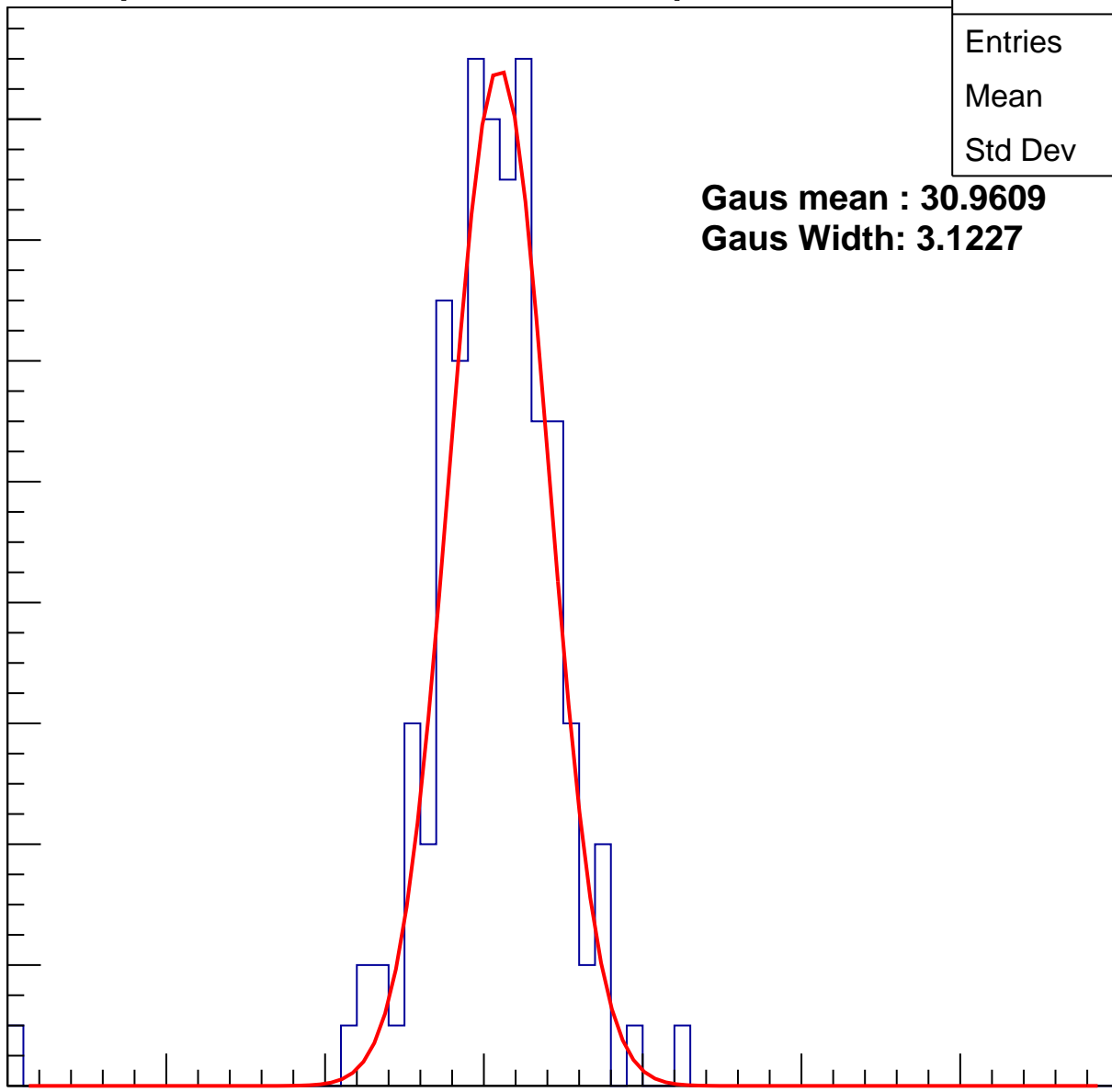
40

50

60

70

ampl



B0L101S, U24-ch57, adc1

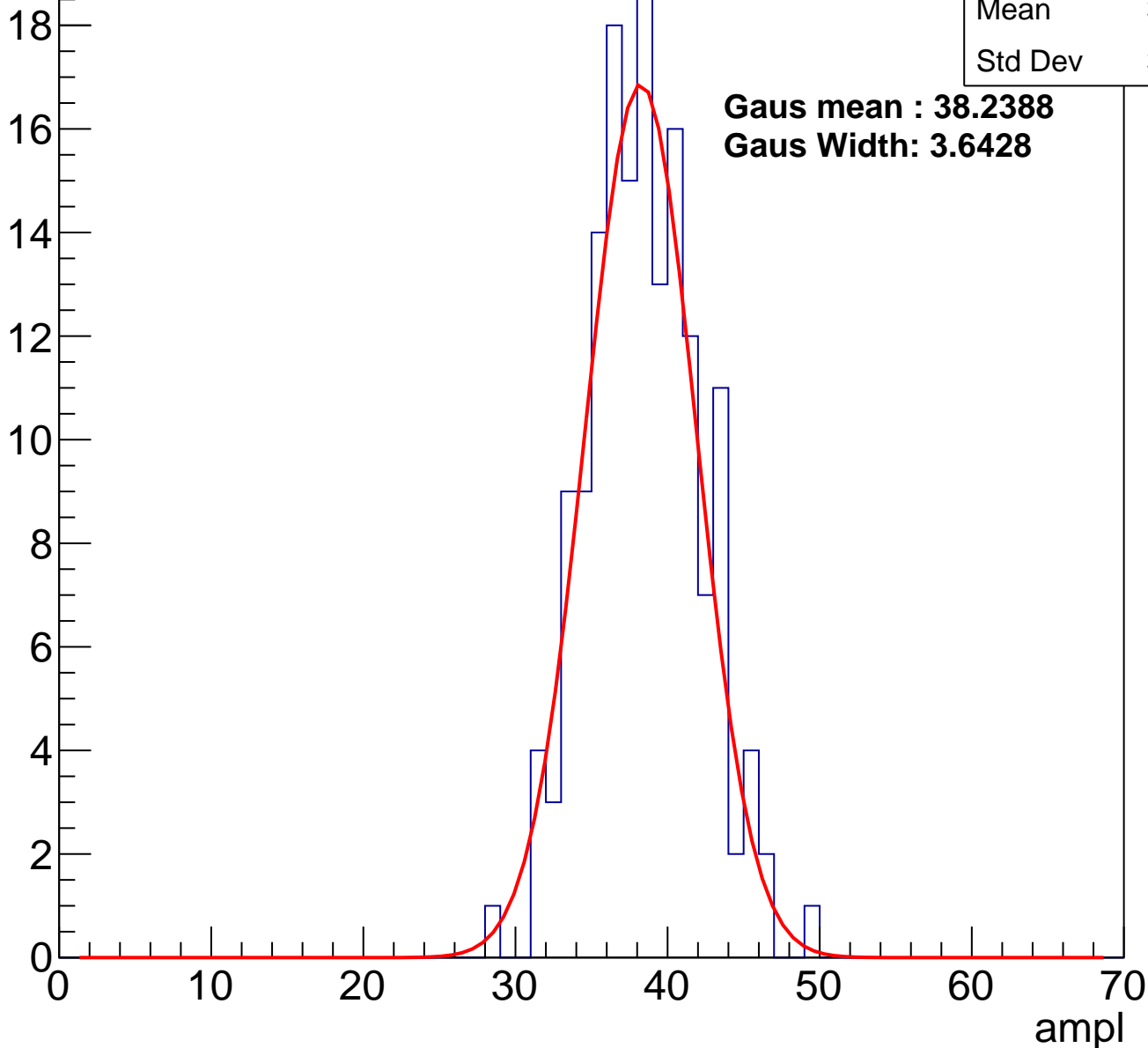
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	38.01
Std Dev	3.601

Gaus mean : 38.2388

Gaus Width: 3.6428

Entry



B0L101S, U24-ch57, adc2

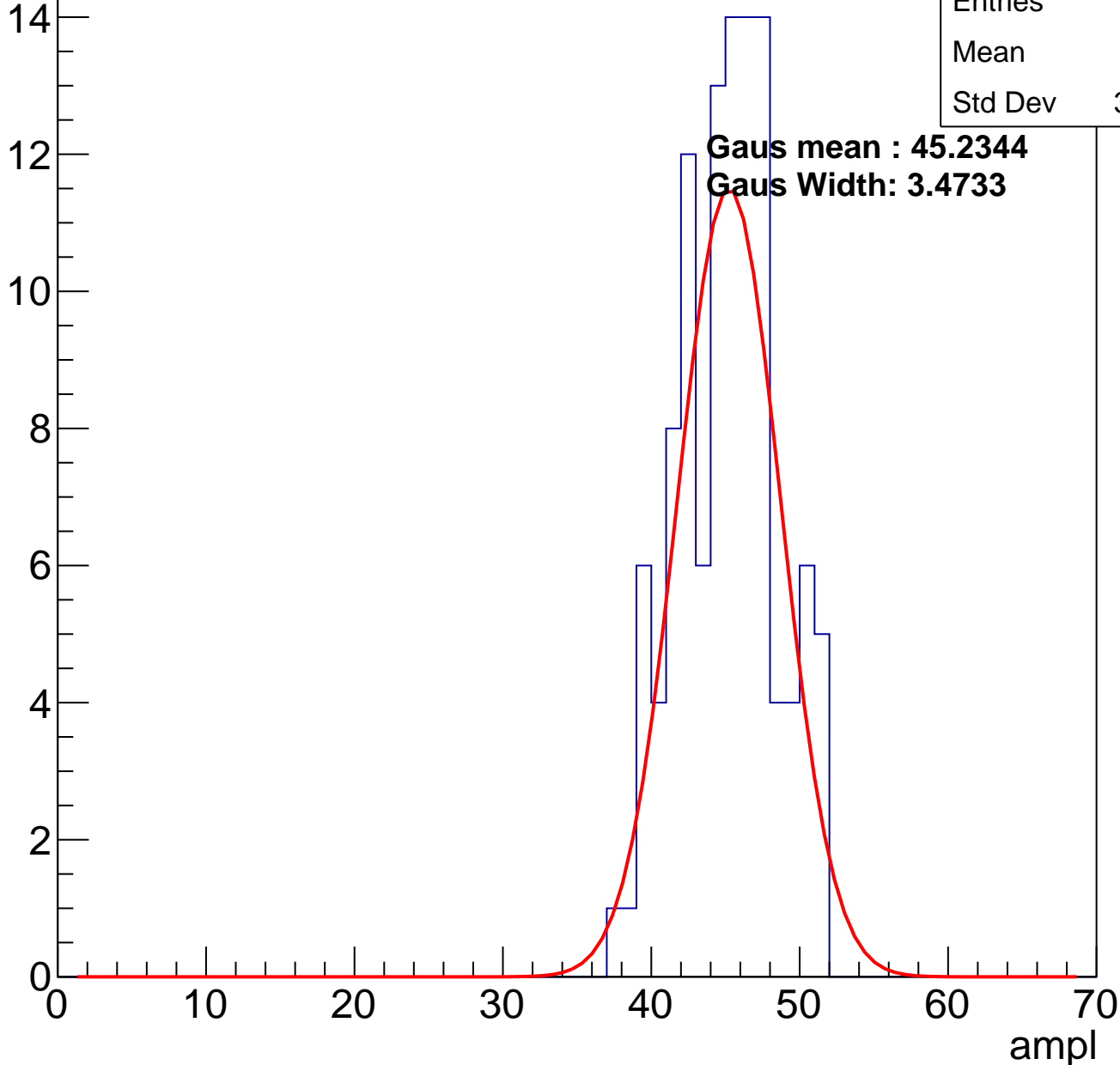
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	44.7
Std Dev	3.262

Gaus mean : 45.2344

Gaus Width: 3.4733

Entry



B0L101S, U24-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

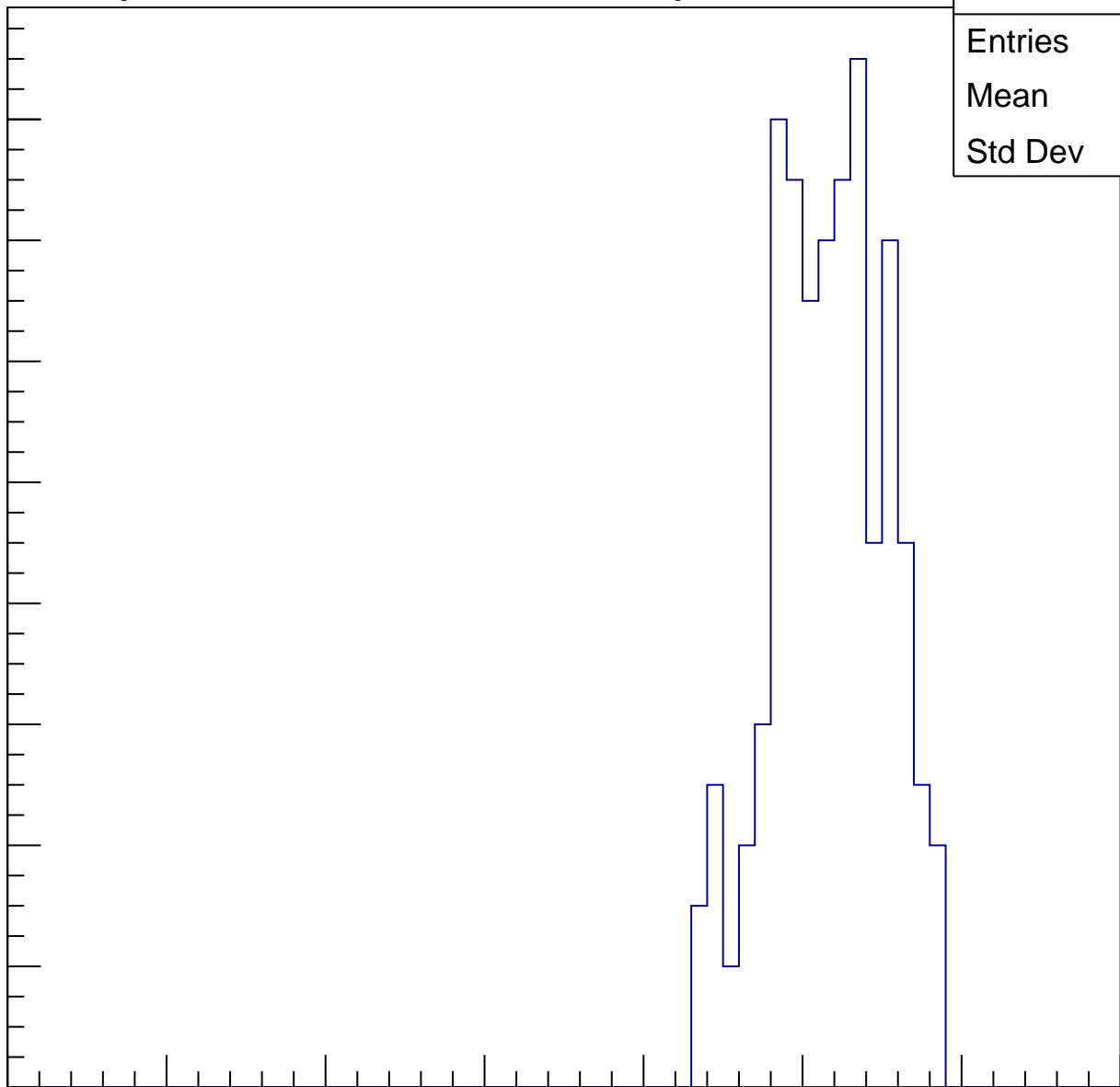
Entries	151
Mean	51.19
Std Dev	3.552

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch57, adc4

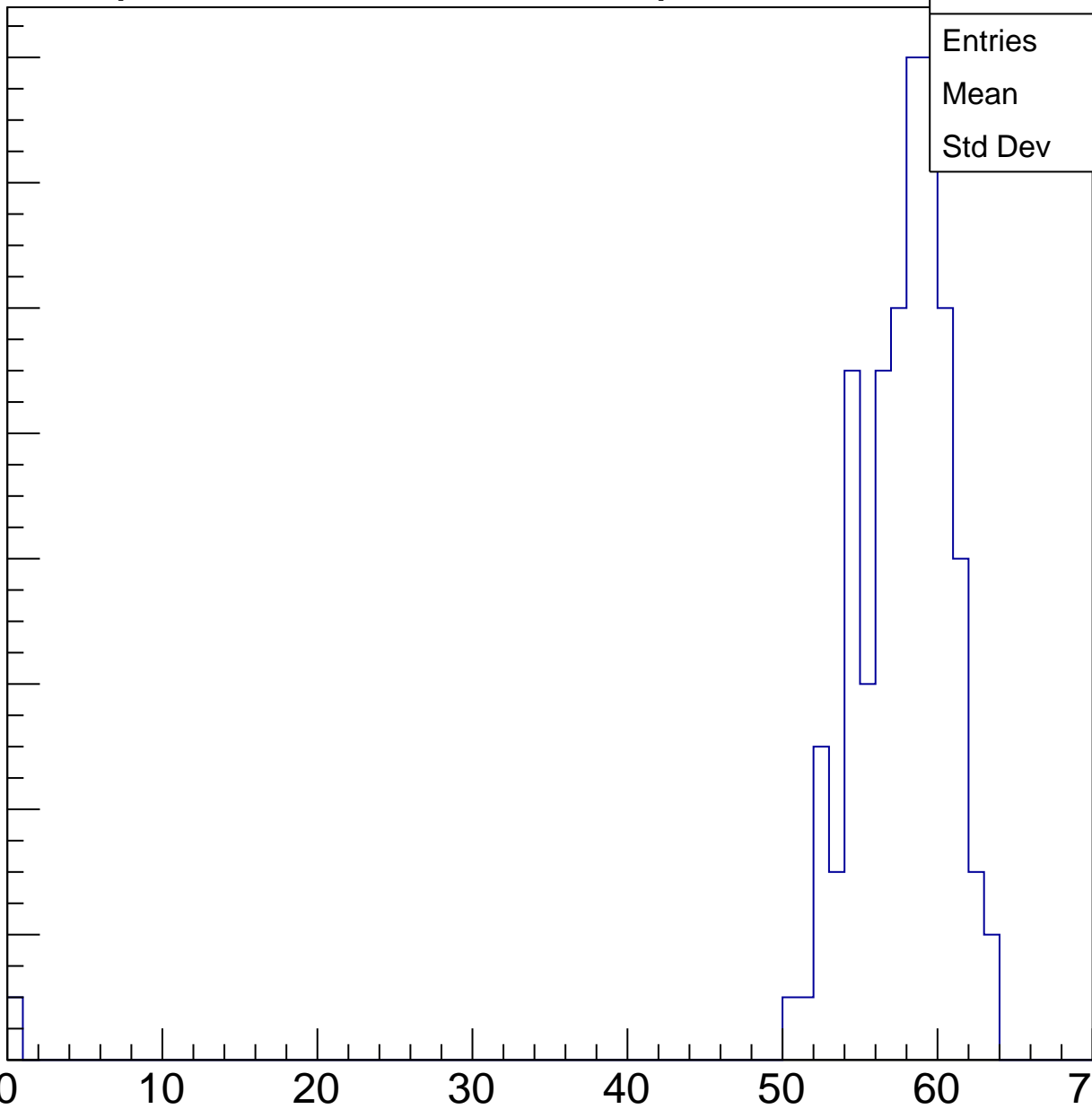
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	108
Mean	56.81
Std Dev	6.162

ampl



B0L101S, U24-ch57, adc5

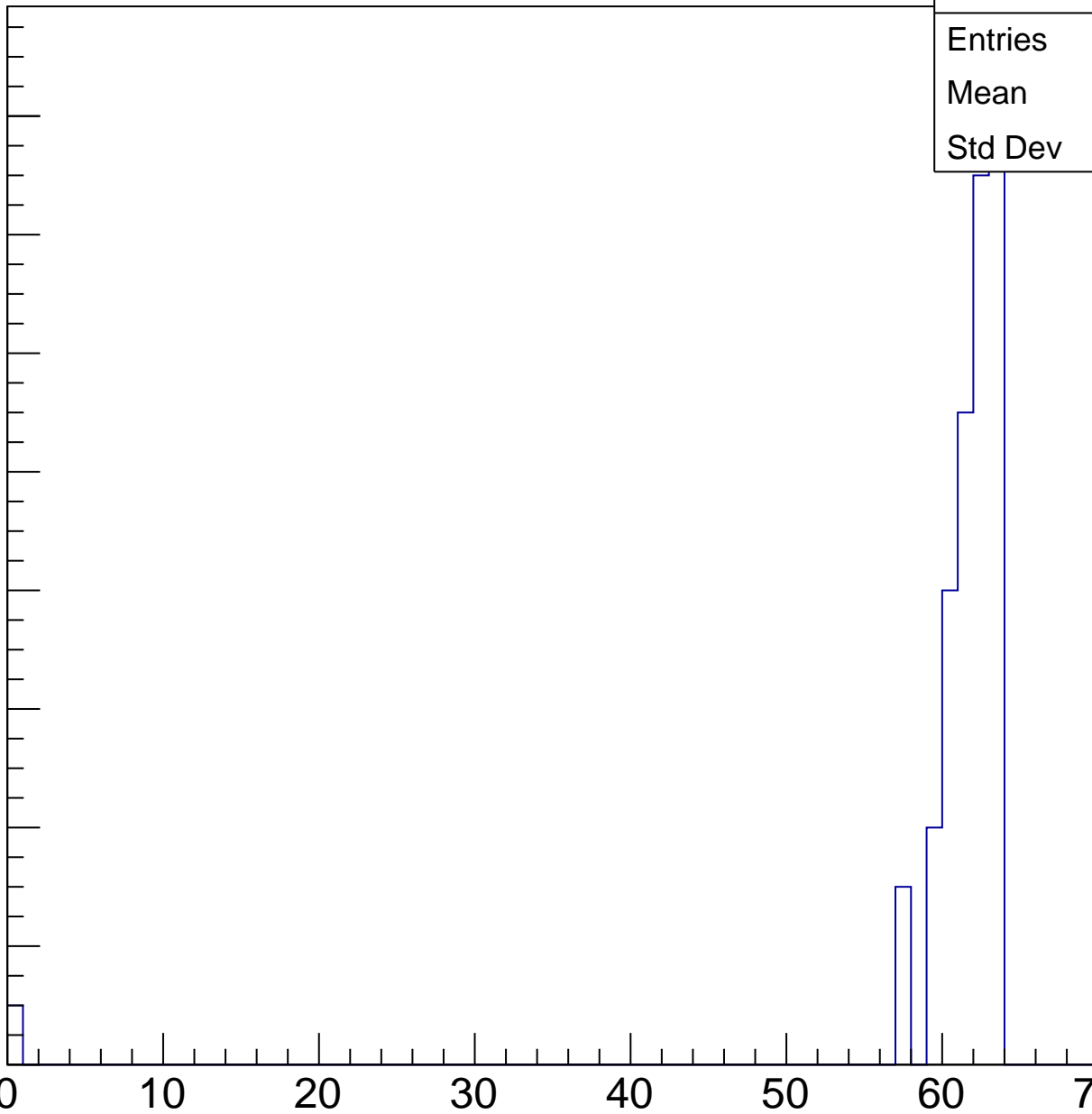
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	59
Mean	60.32
Std Dev	8.077

ampl



B0L101S, U24-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch58, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.18
Std Dev	3.893

Gaus mean : 29.5687

Gaus Width: 3.8832

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

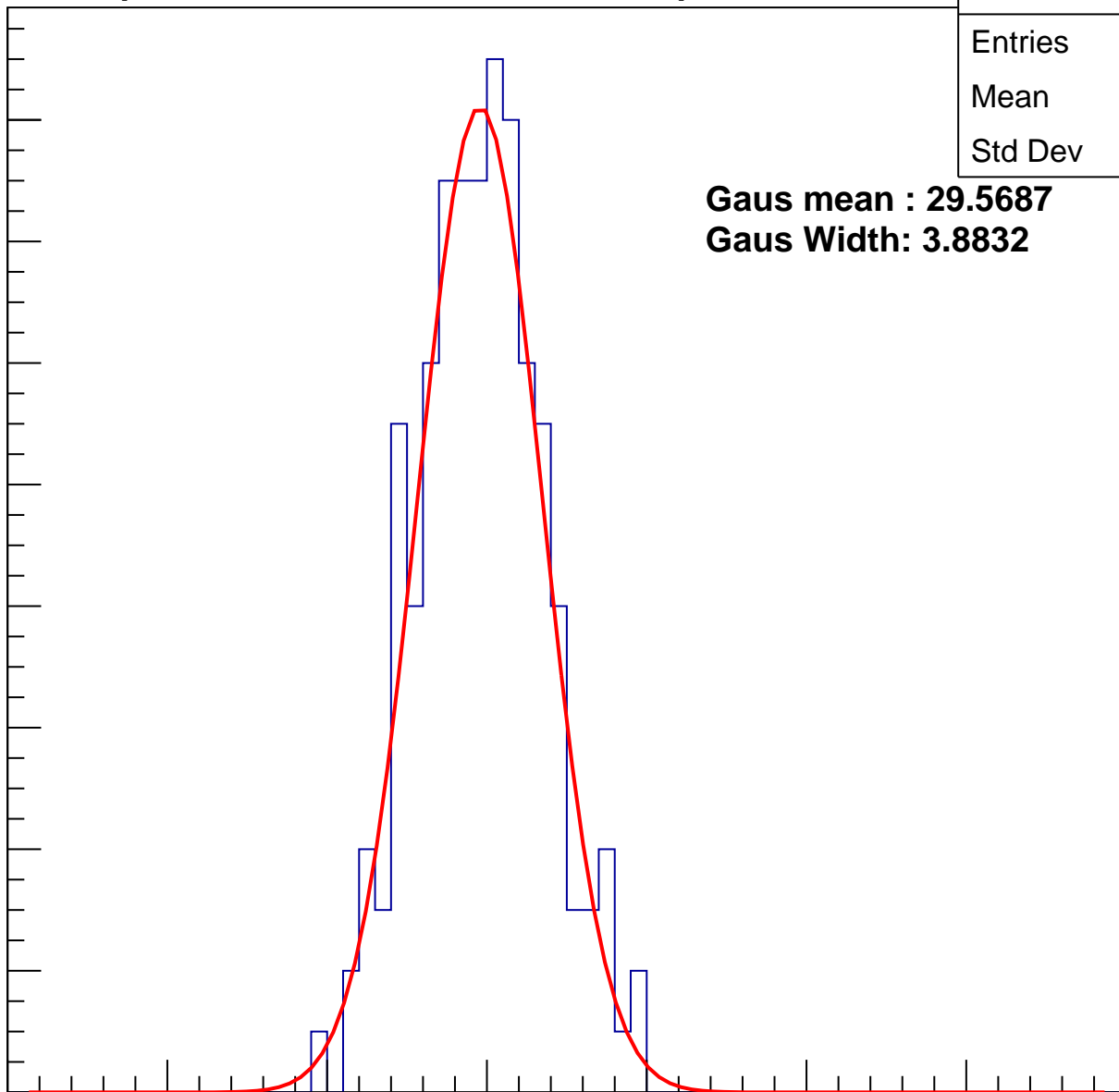
40

50

60

70

ampl

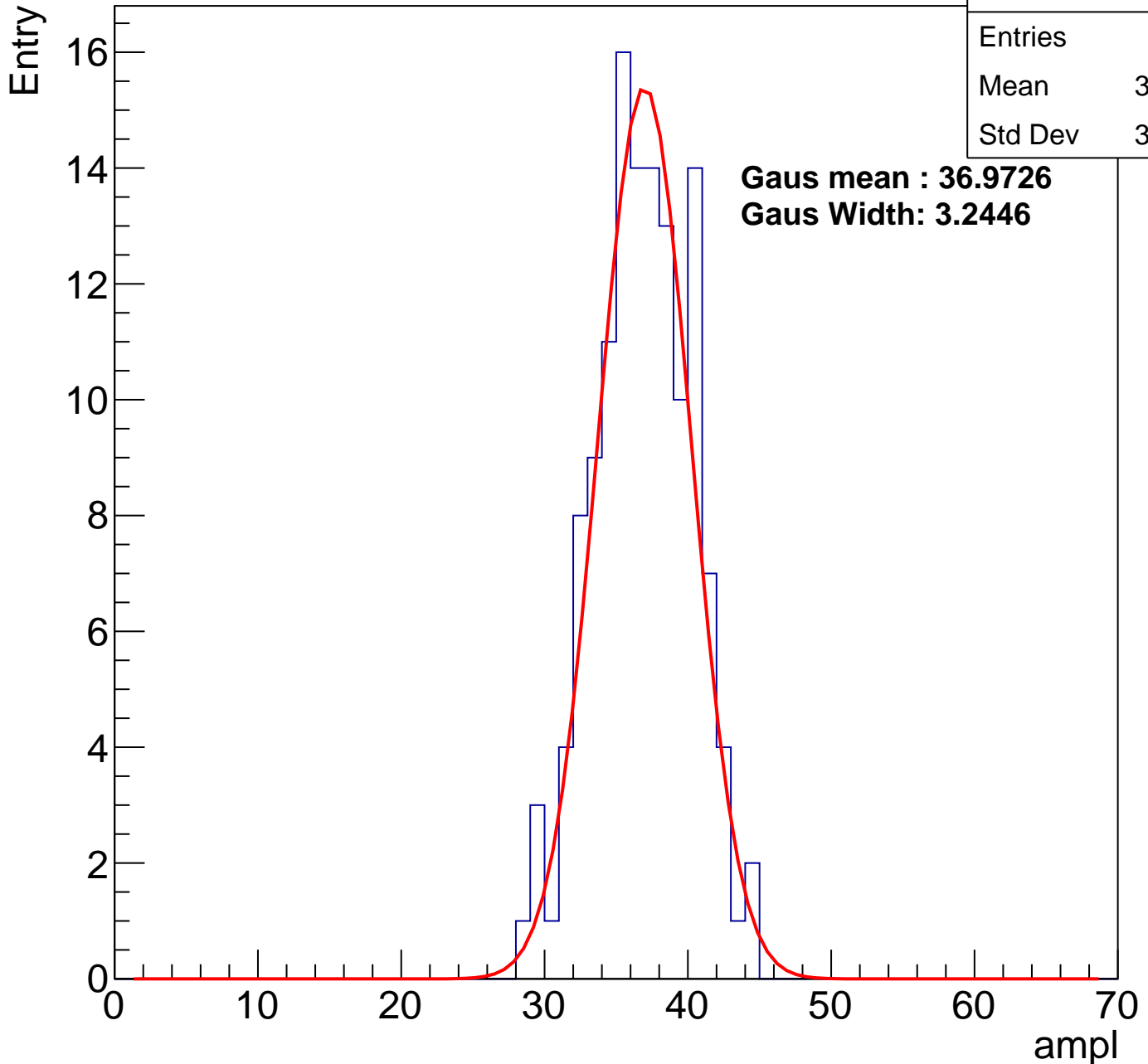


B0L101S, U24-ch58, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	36.42
Std Dev	3.335

Gaus mean : 36.9726
Gaus Width: 3.2446



B0L101S, U24-ch58, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

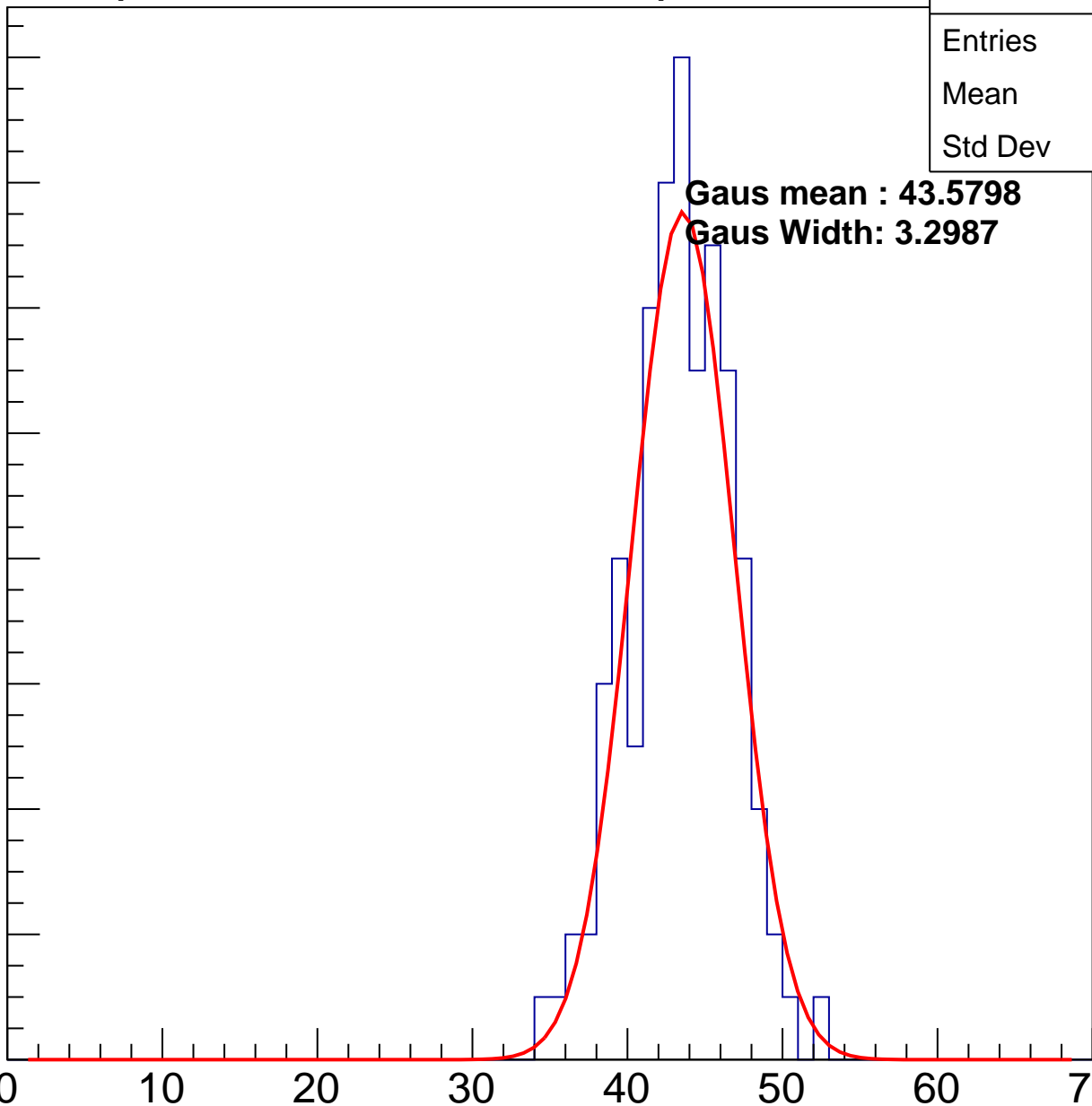
16
14
12
10
8
6
4
2
0

Entries	118
Mean	42.93
Std Dev	3.308

Gaus mean : 43.5798

Gaus Width: 3.2987

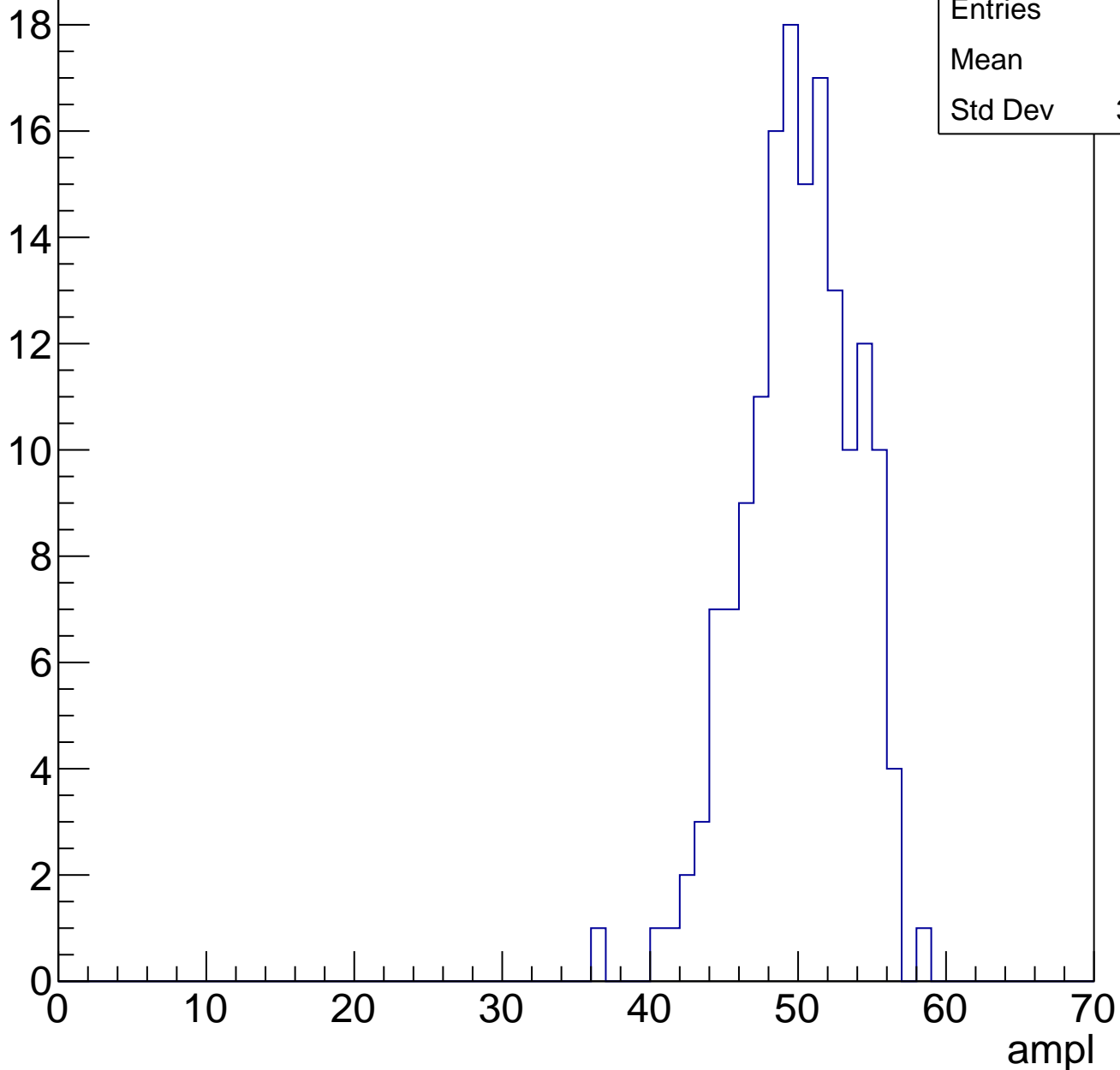
ampl



B0L101S, U24-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

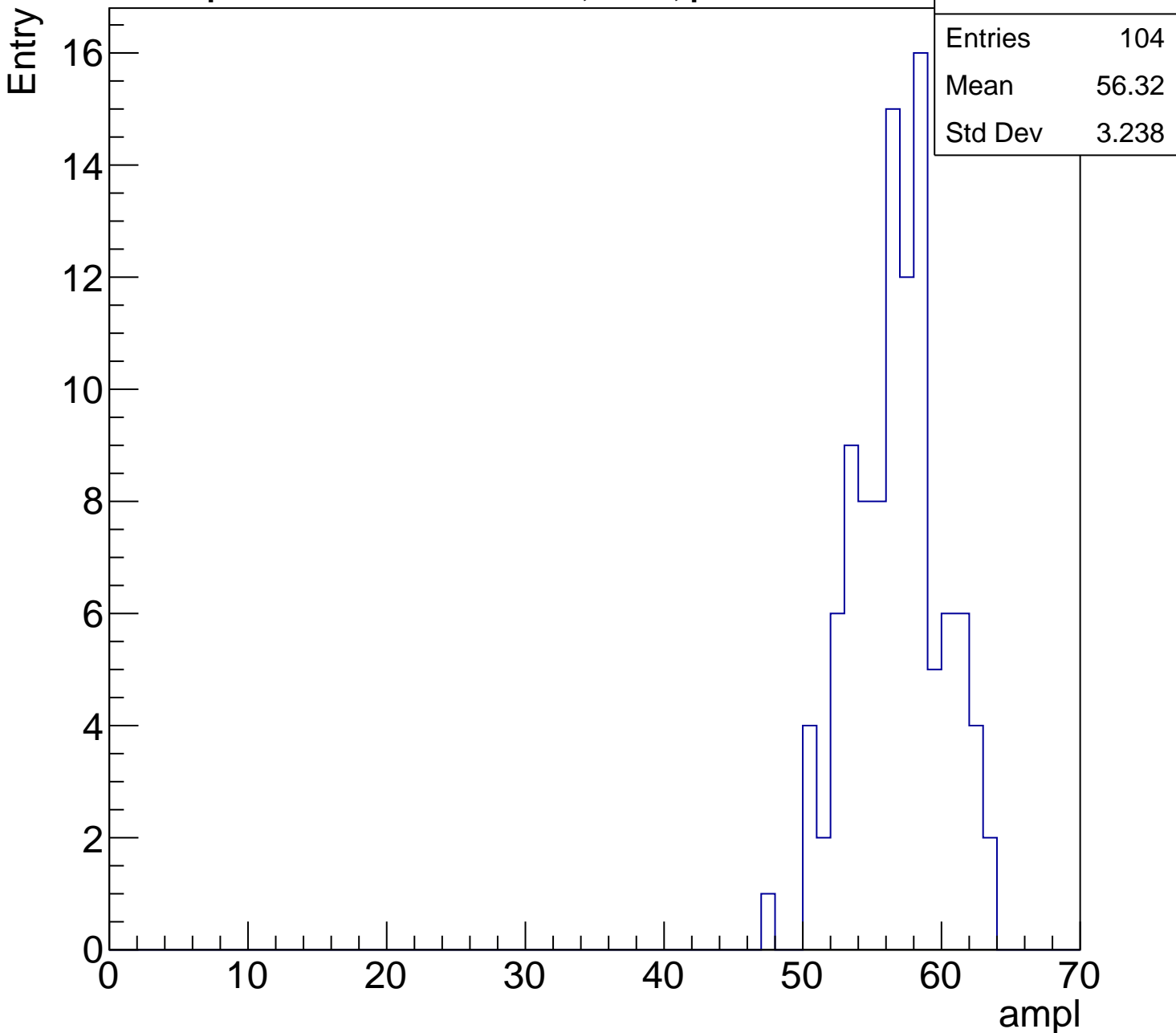
Entries	104
Mean	56.32
Std Dev	3.238

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

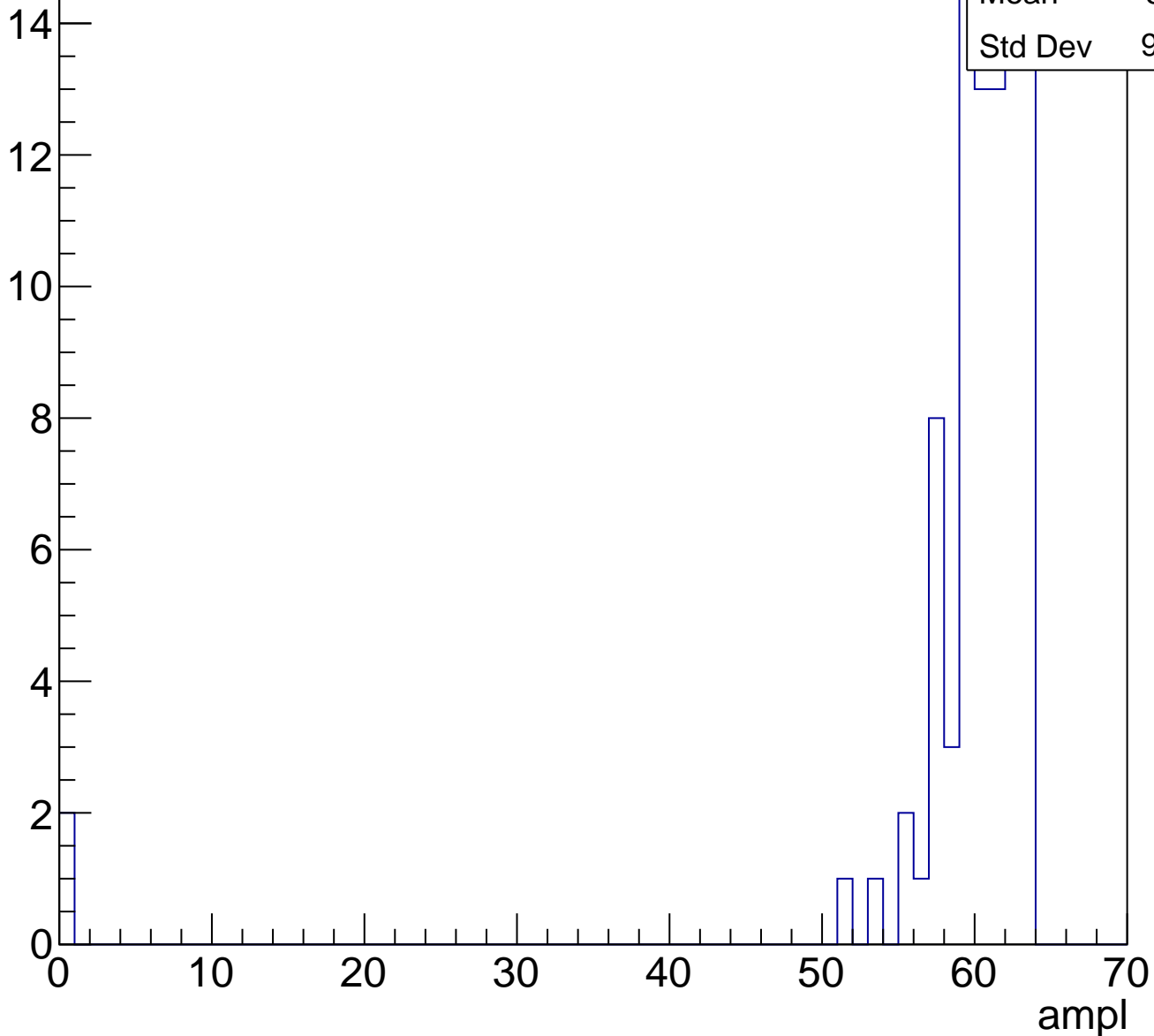
ampl



B0L101S, U24-ch58, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

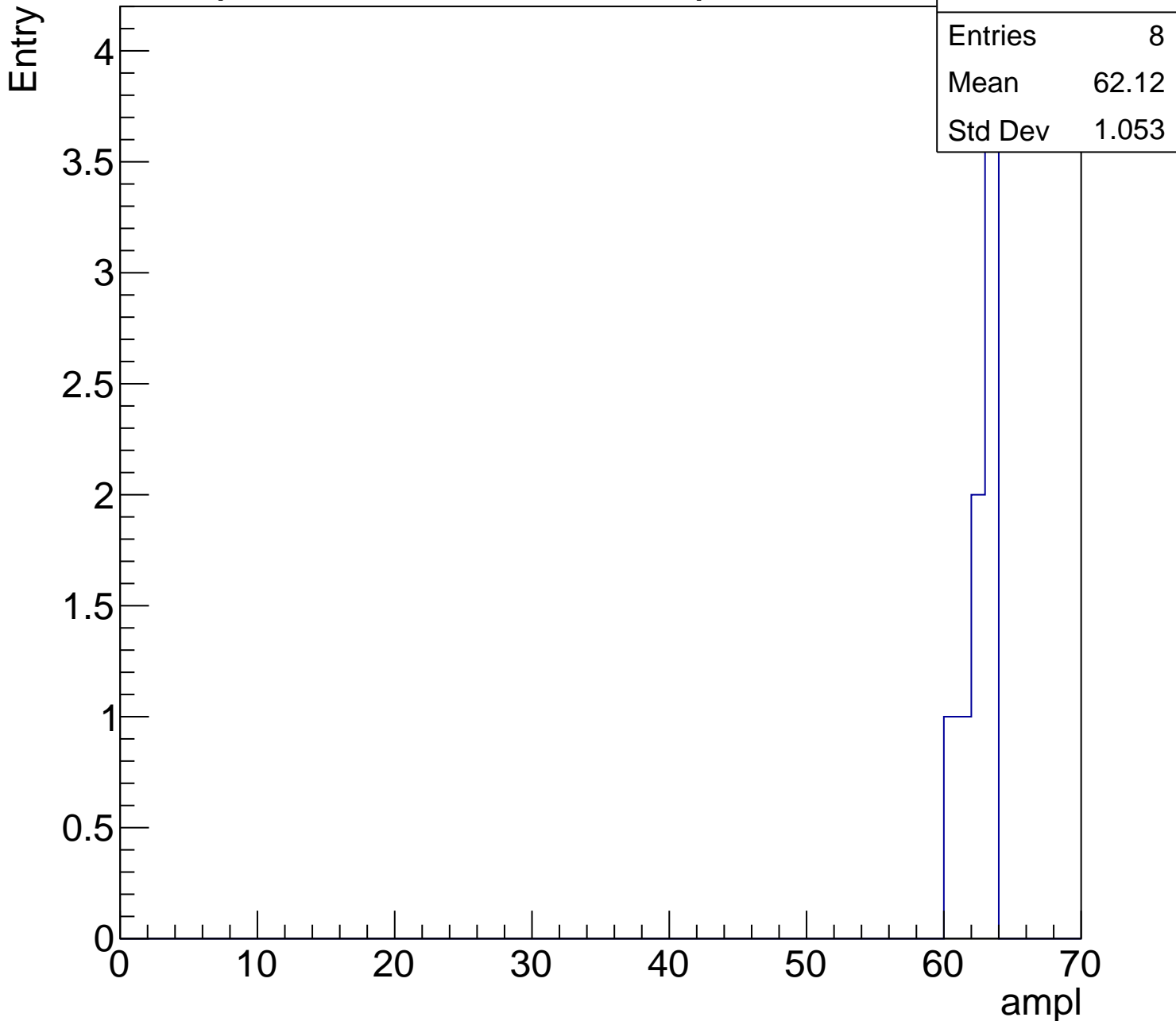
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	62.12
Std Dev	1.053

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

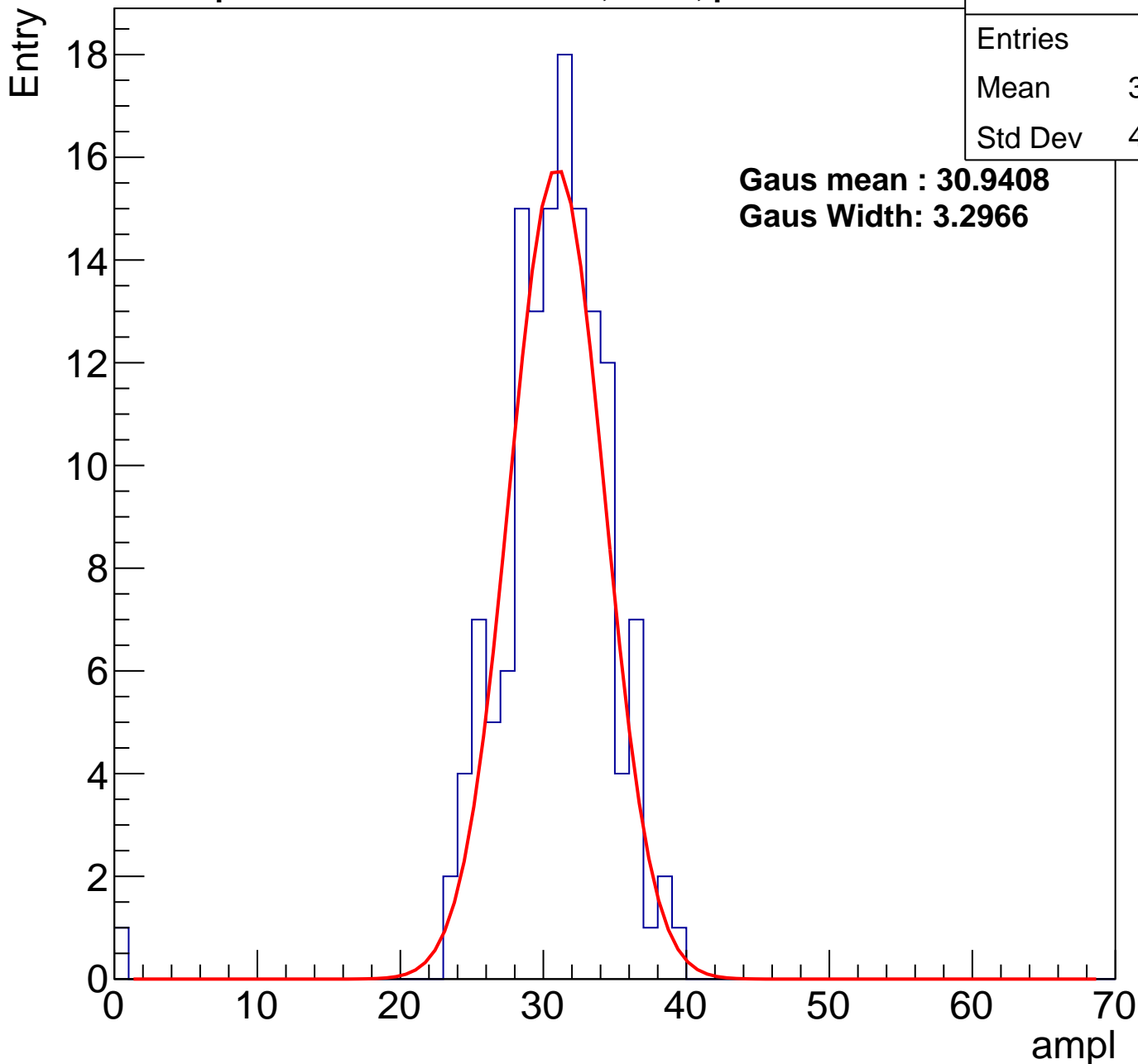
B0L101S, U24-ch59, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	30.32
Std Dev	4.216

Gaus mean : 30.9408

Gaus Width: 3.2966



B0L101S, U24-ch59, adc1

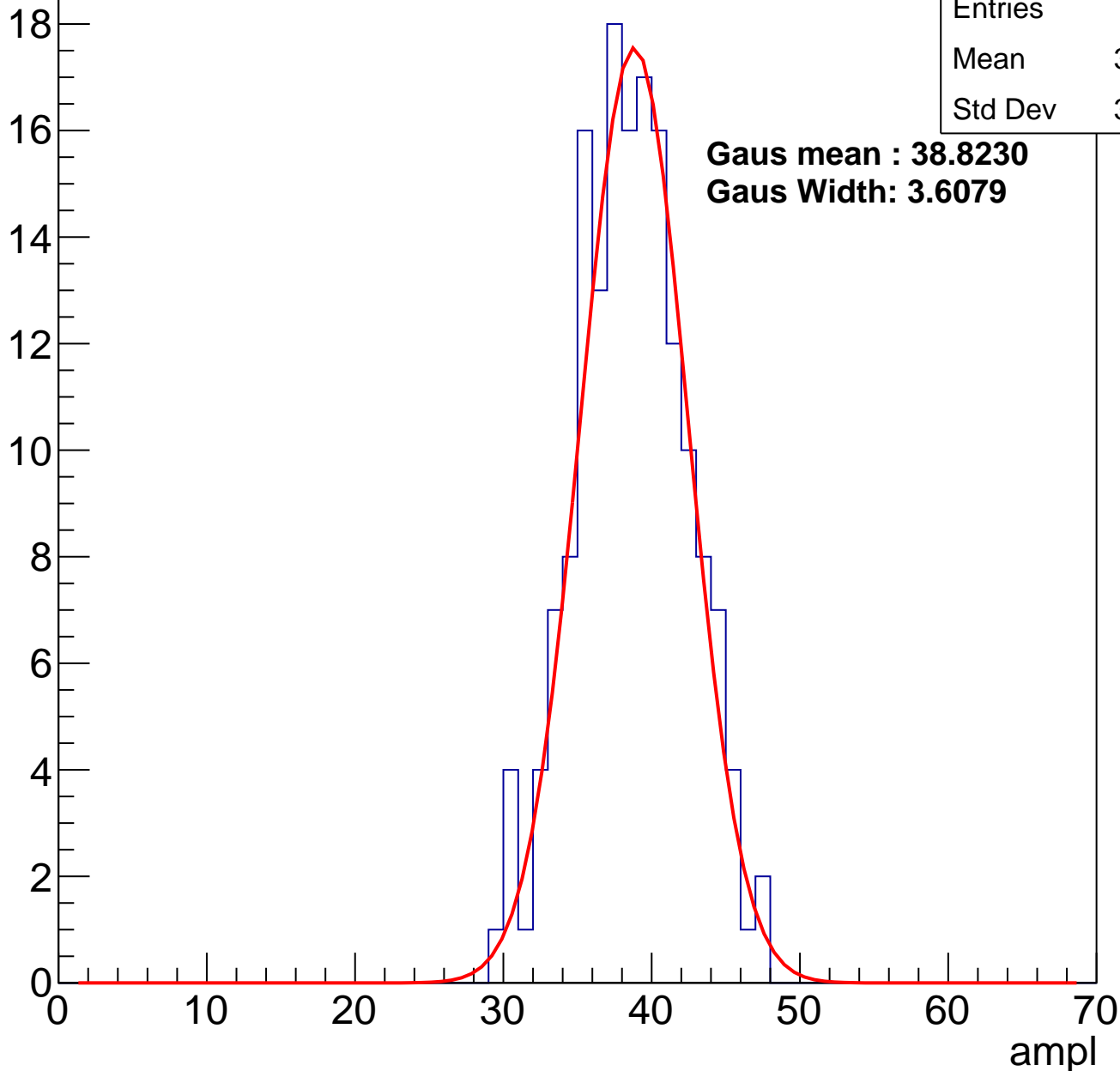
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	38.18
Std Dev	3.705

Gaus mean : 38.8230

Gaus Width: 3.6079

Entry



B0L101S, U24-ch59, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

109

Mean

44.94

Std Dev

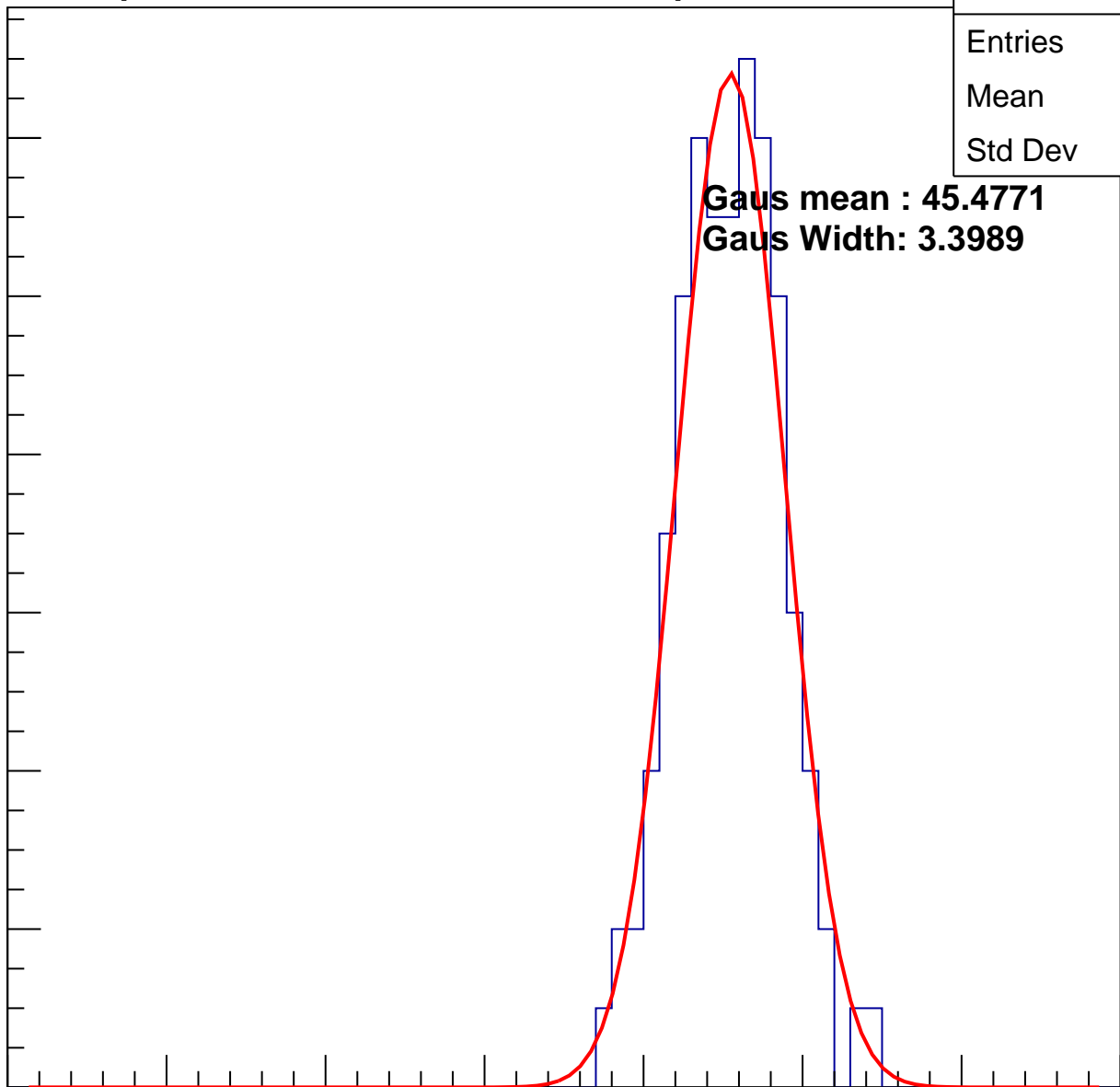
3.26

Gaus mean : 45.4771

Gaus Width: 3.3989

0 10 20 30 40 50 60 70

ampl

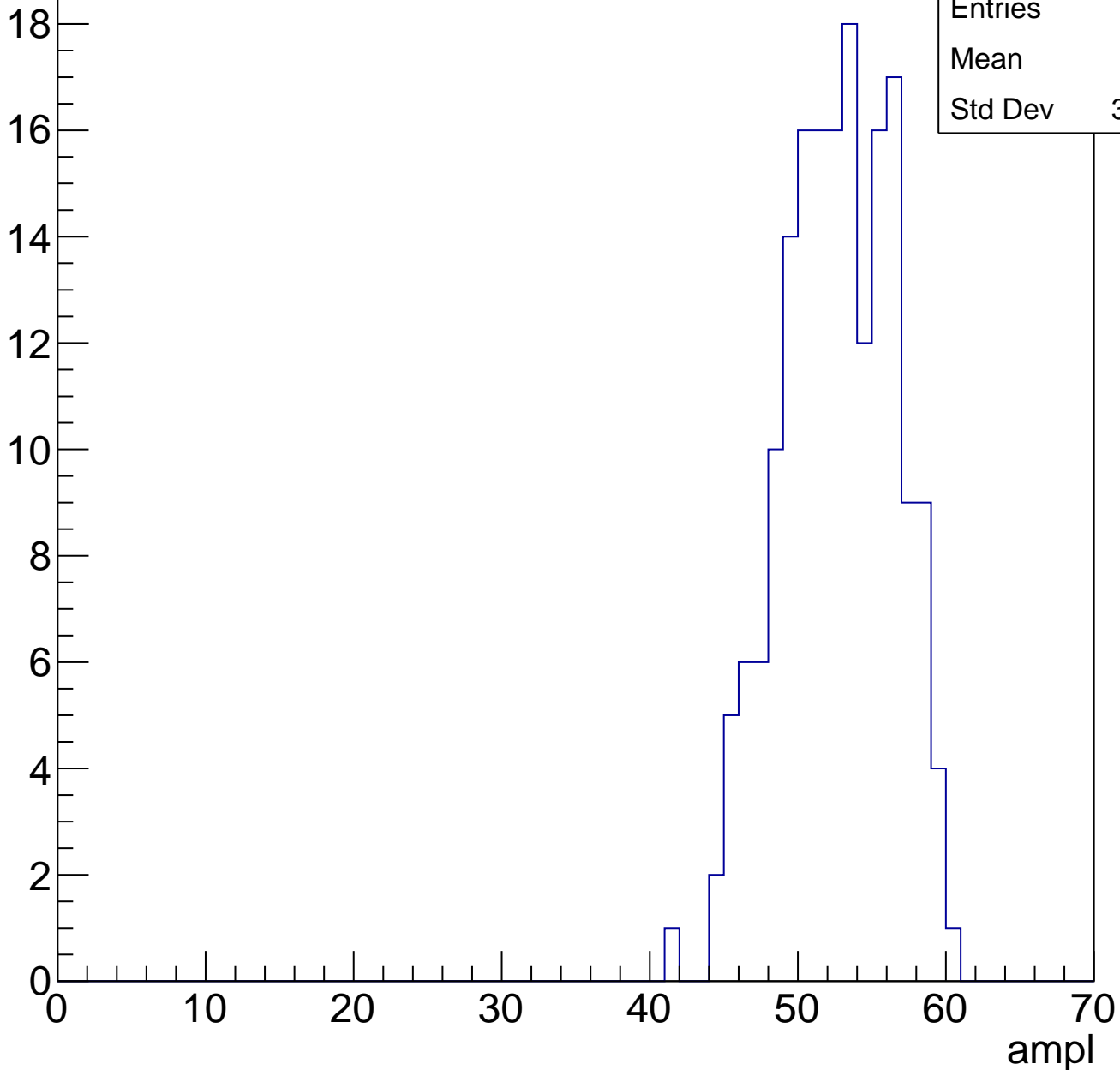


B0L101S, U24-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	52.2
Std Dev	3.755

Entry



B0L101S, U24-ch59, adc4

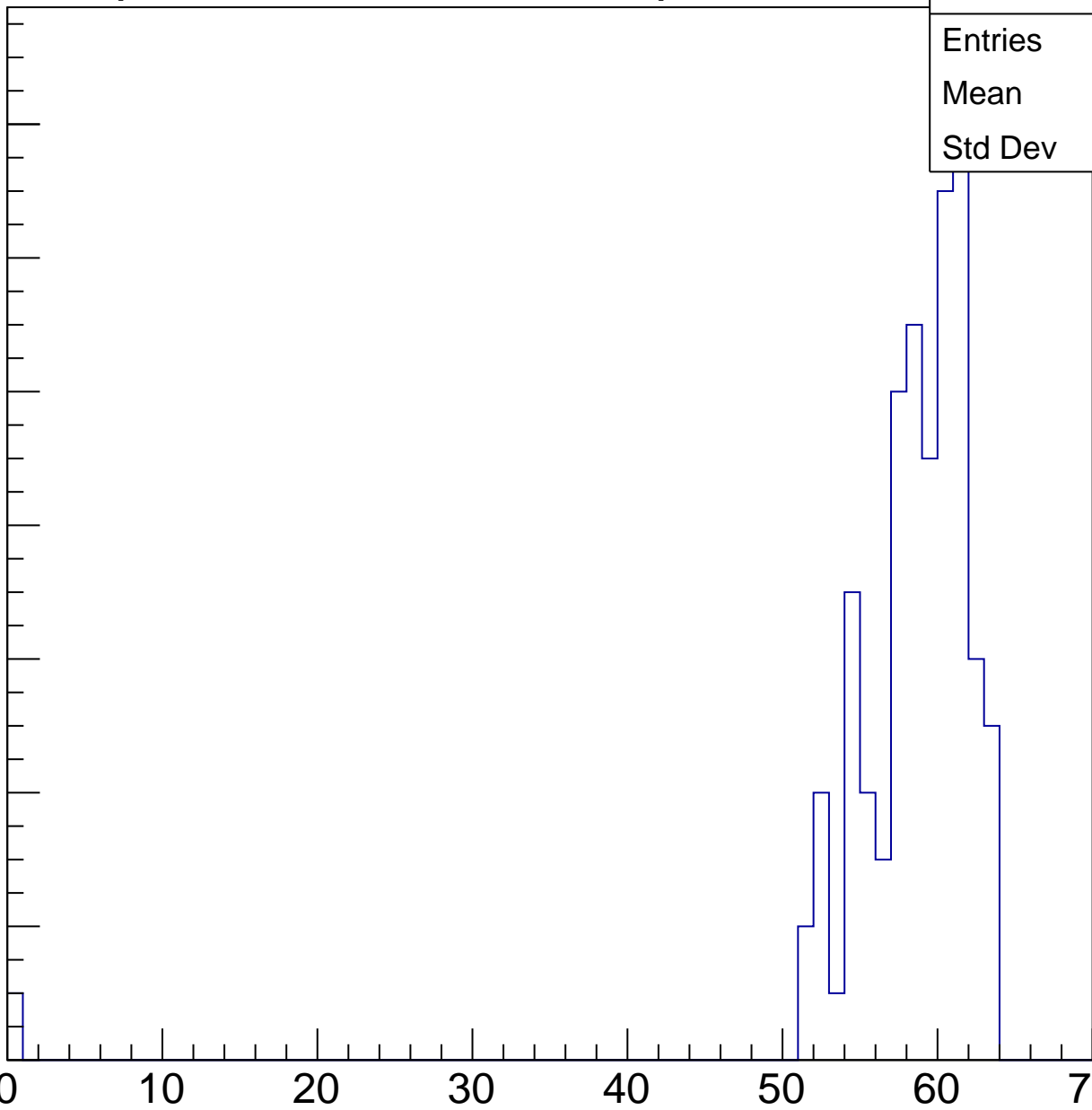
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	57.69
Std Dev	6.801

ampl



B0L101S, U24-ch59, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	46
Mean	60
Std Dev	9.077

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch60, adc0

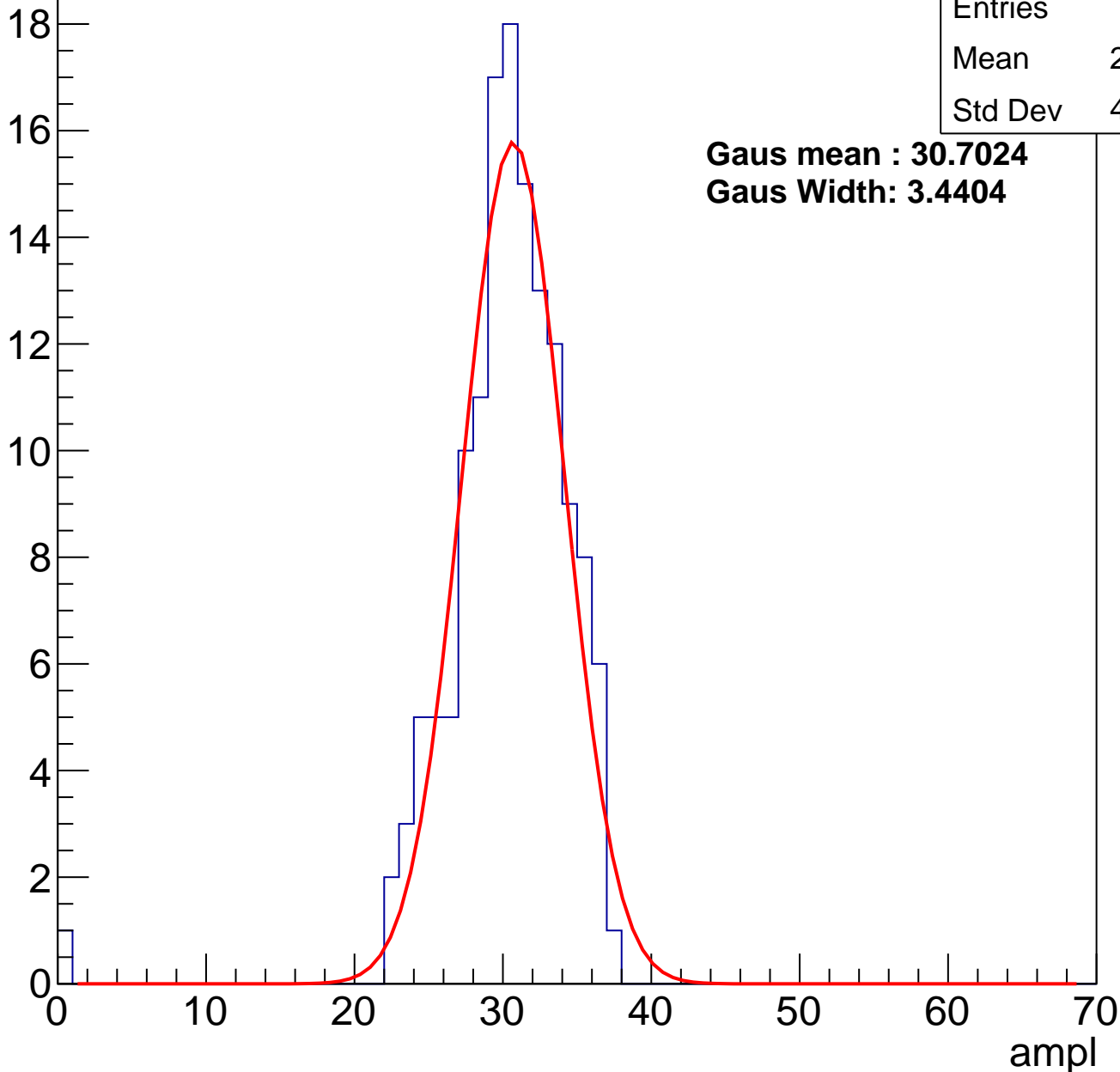
calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	29.89
Std Dev	4.205

Gaus mean : 30.7024

Gaus Width: 3.4404

Entry



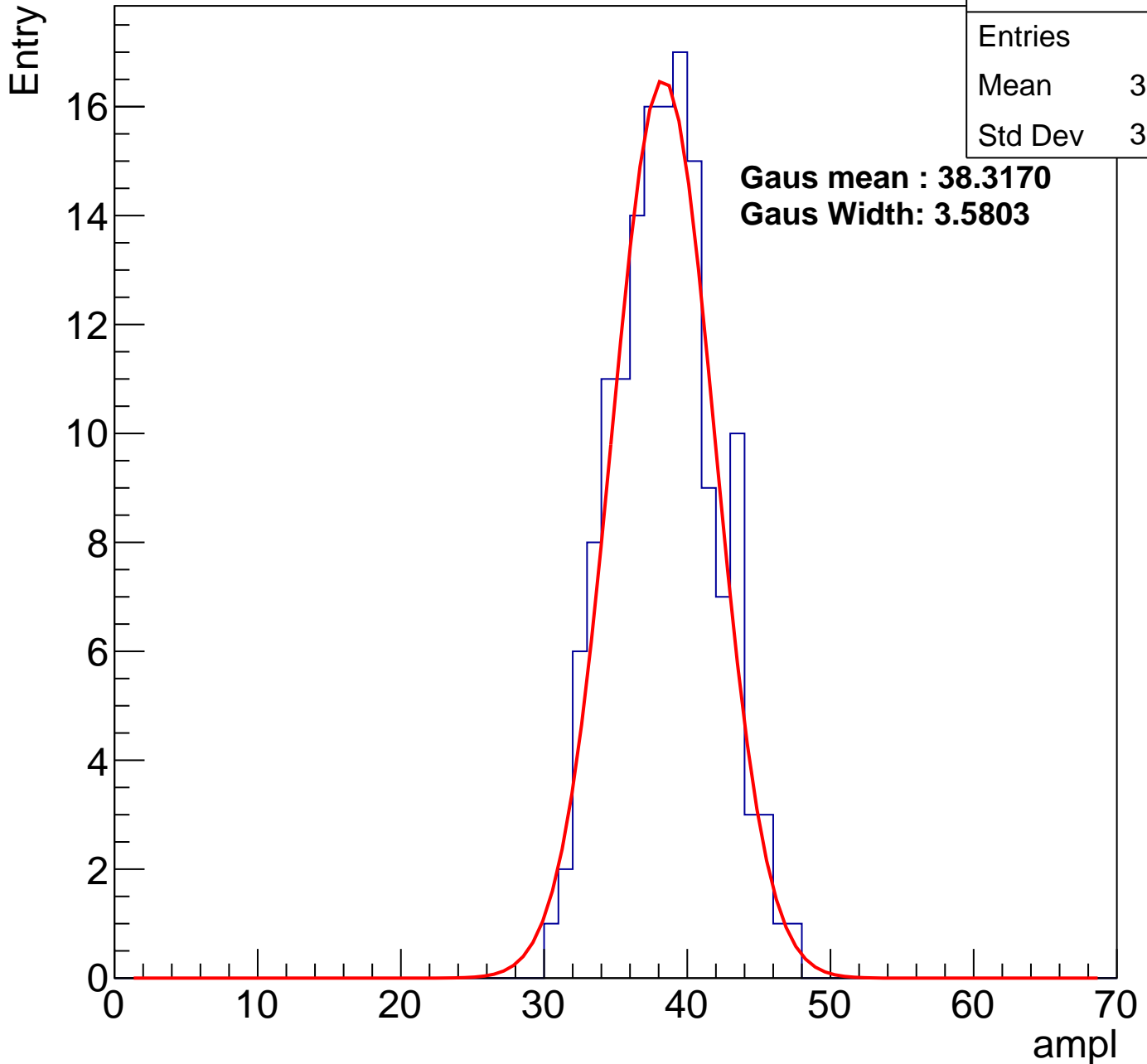
B0L101S, U24-ch60, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	37.93
Std Dev	3.495

Gaus mean : 38.3170

Gaus Width: 3.5803



B0L101S, U24-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

130

Mean

44.56

Std Dev

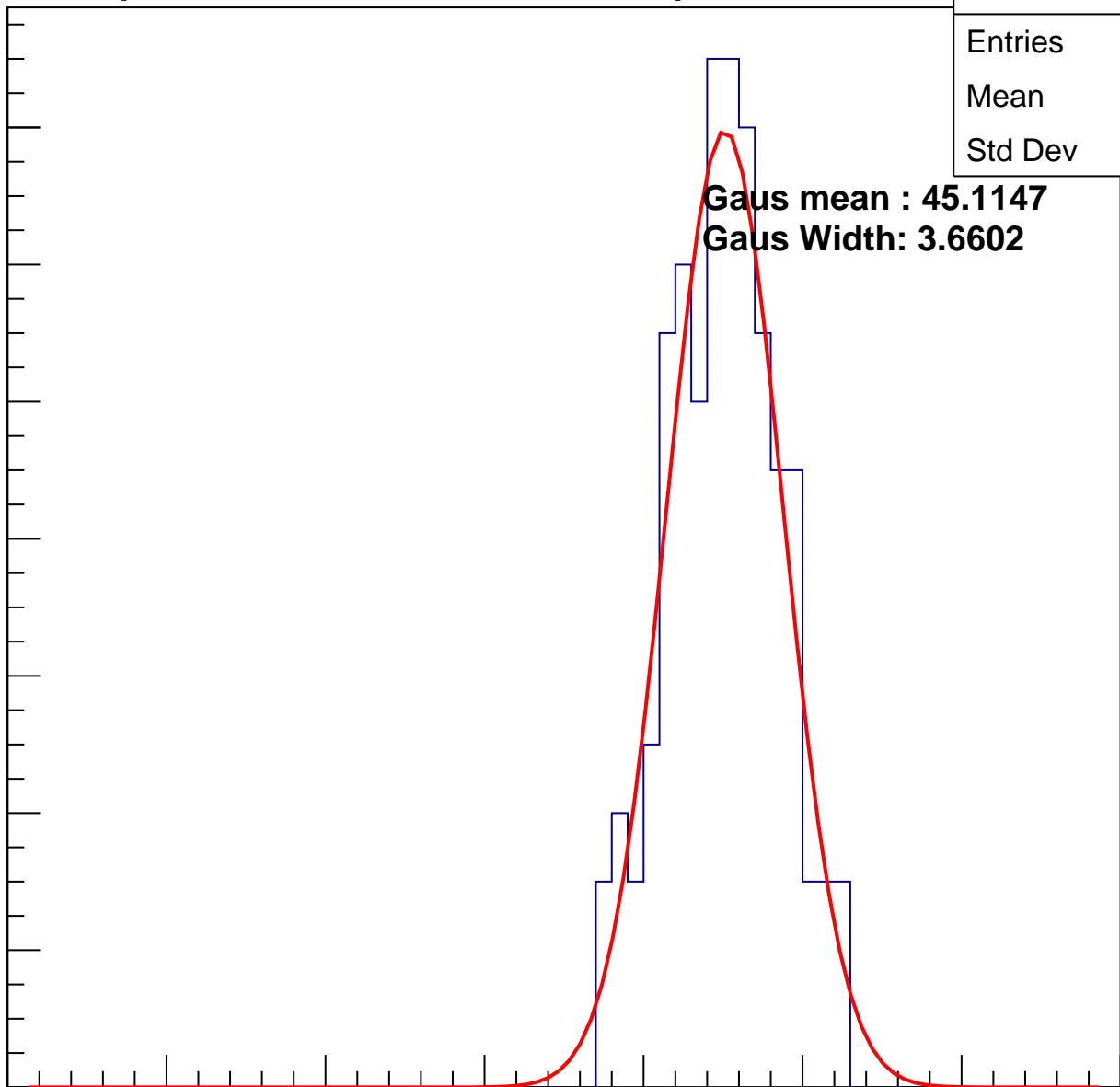
3.451

Gaus mean : 45.1147

Gaus Width: 3.6602

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch60, adc3

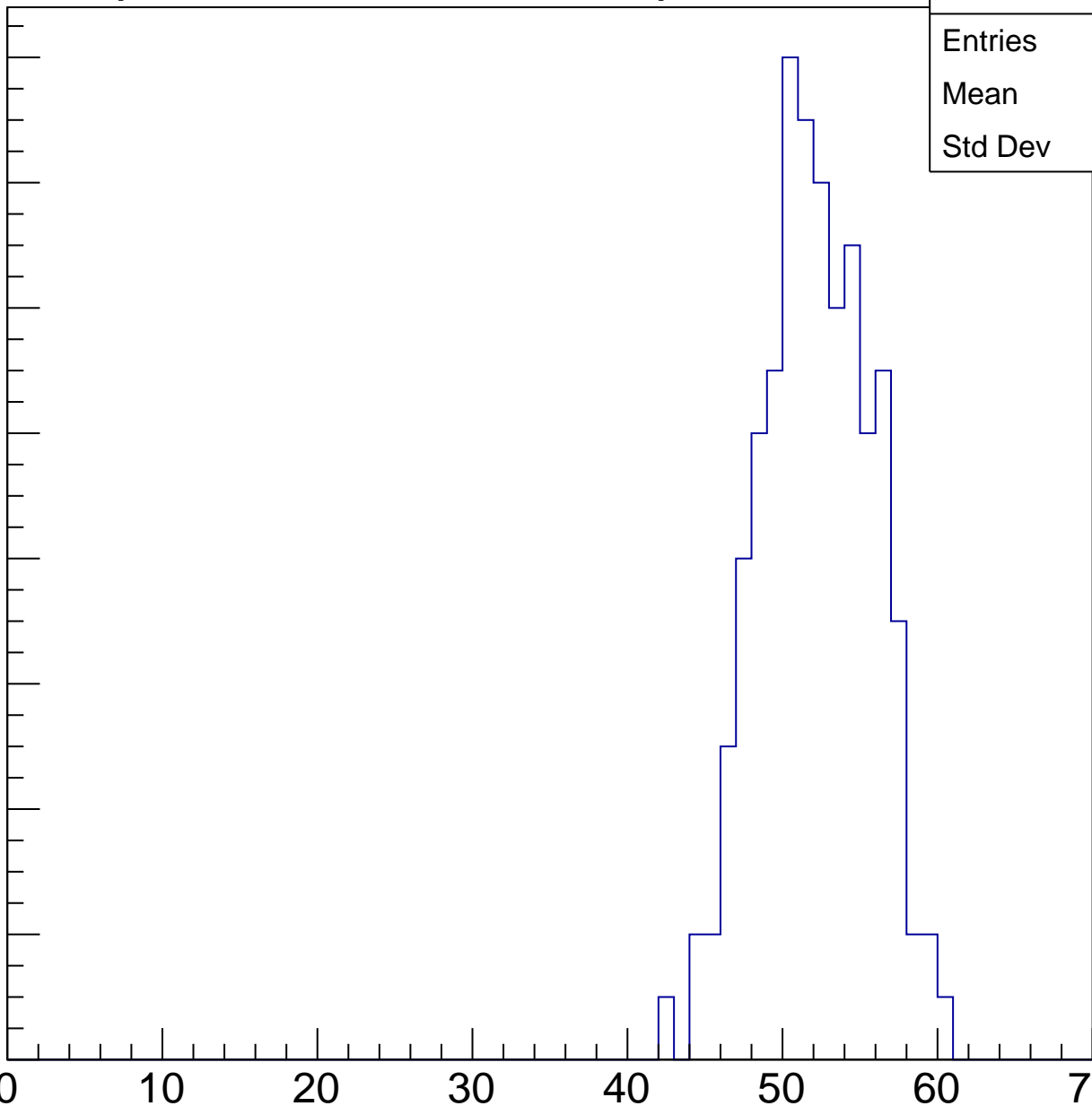
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	142
Mean	51.65
Std Dev	3.527

ampl



B0L101S, U24-ch60, adc4

calib_packv5_042523_0143.root, FC#1, port C1

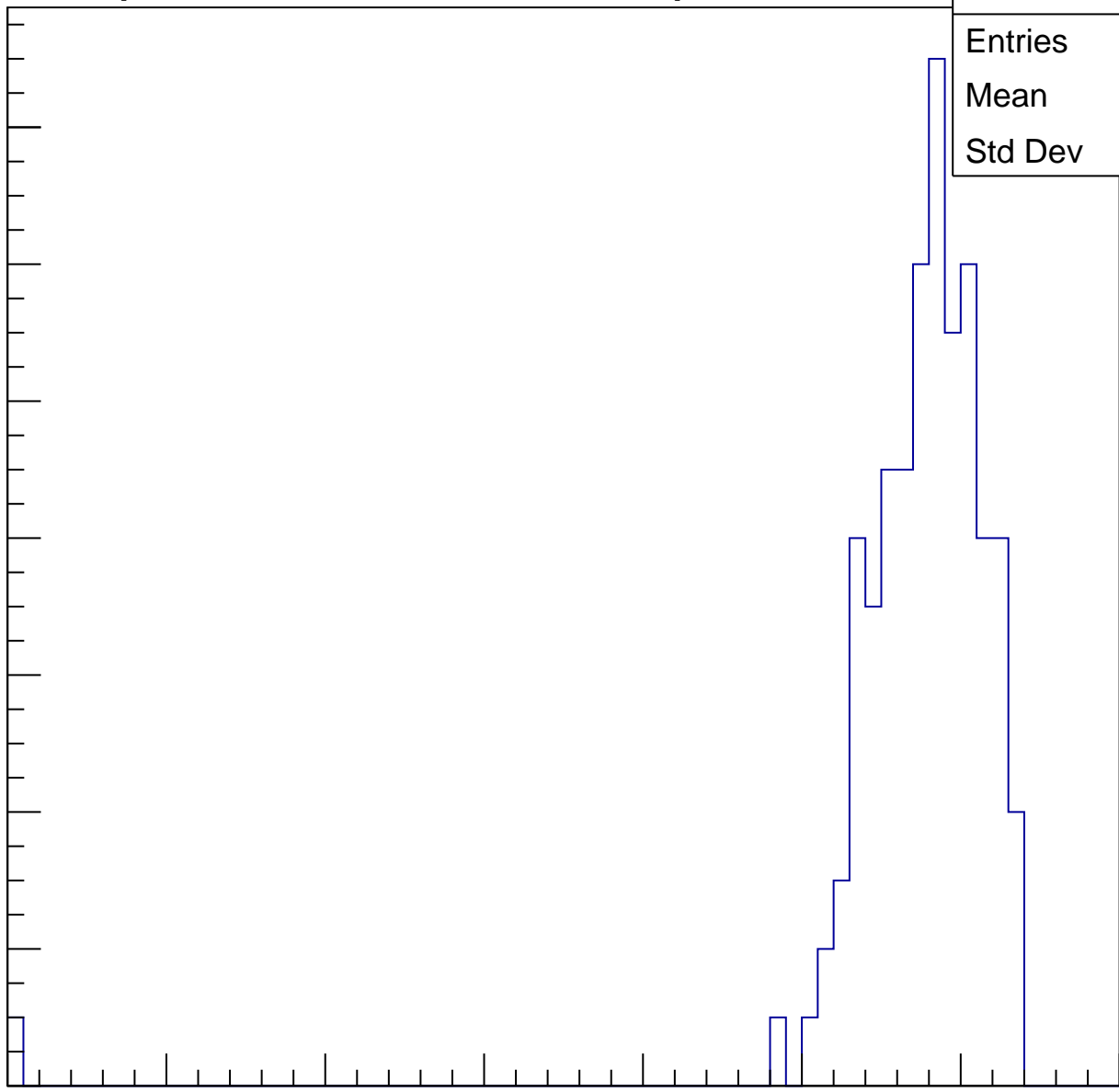
Entries	111
Mean	56.9
Std Dev	6.296

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch60, adc5

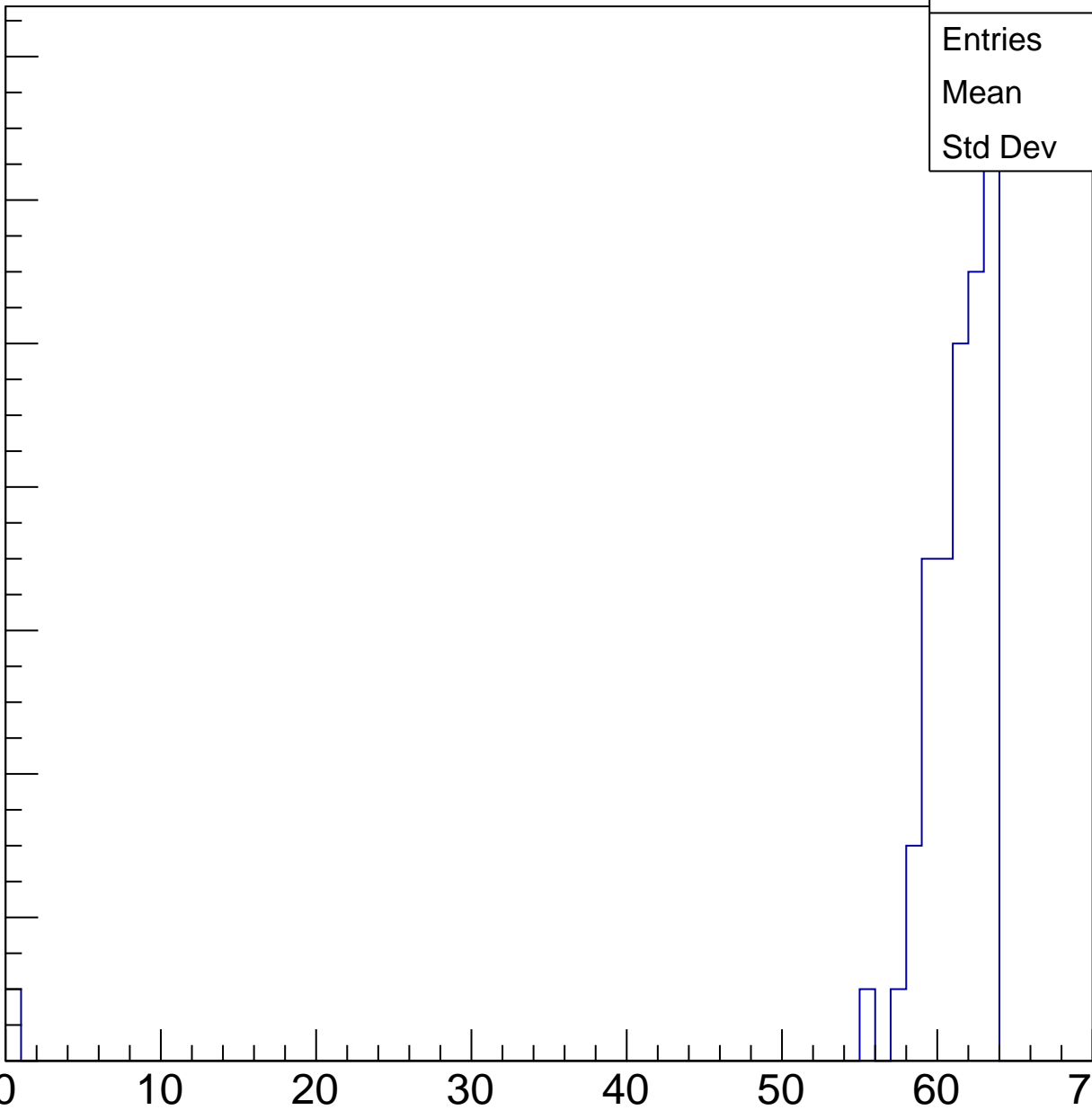
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	55
Mean	59.87
Std Dev	8.347

ampl



B0L101S, U24-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

B0L101S, U24-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

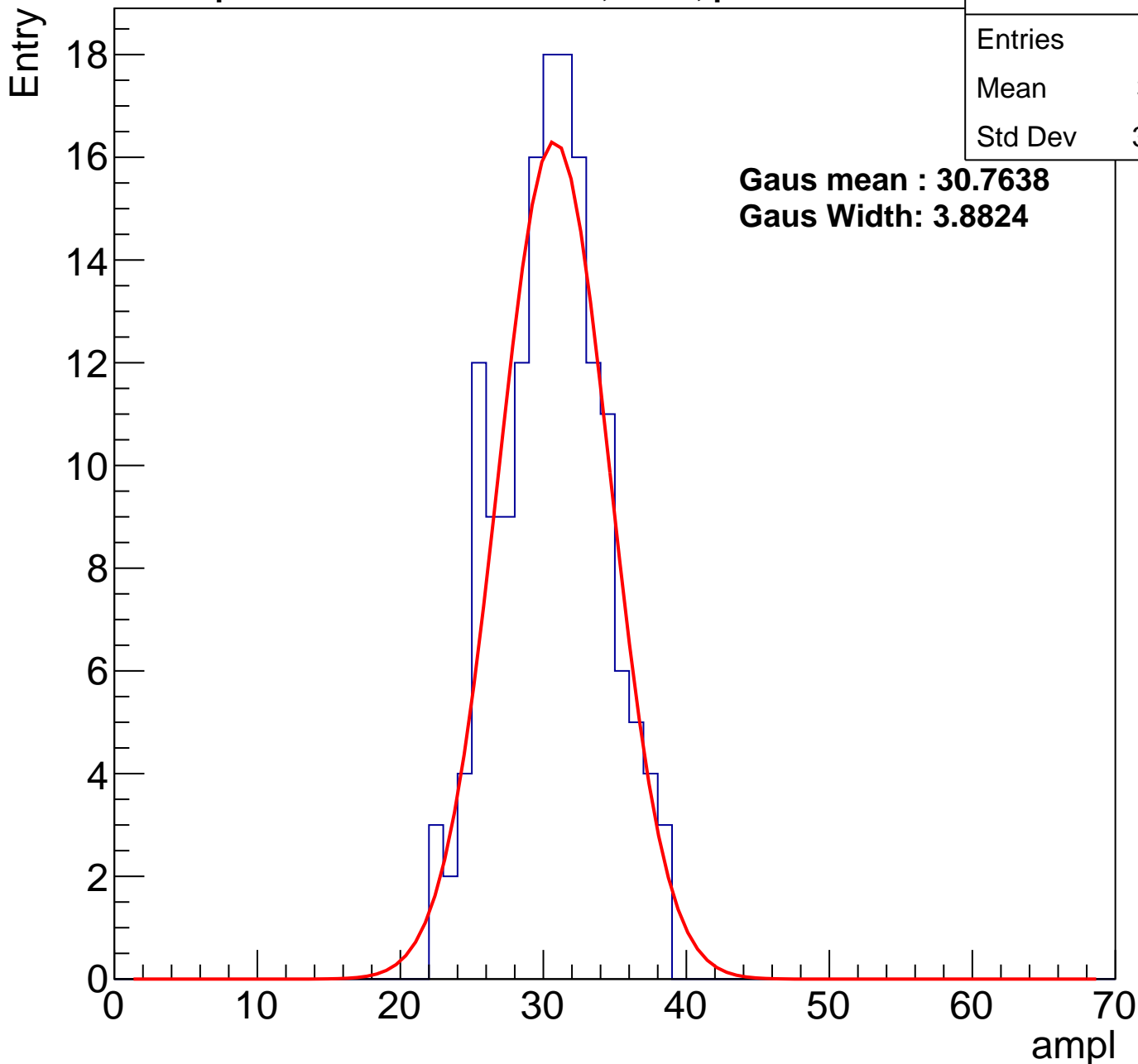
B0L101S, U24-ch61, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	30.11
Std Dev	3.627

Gaus mean : 30.7638

Gaus Width: 3.8824



B0L101S, U24-ch61, adc1

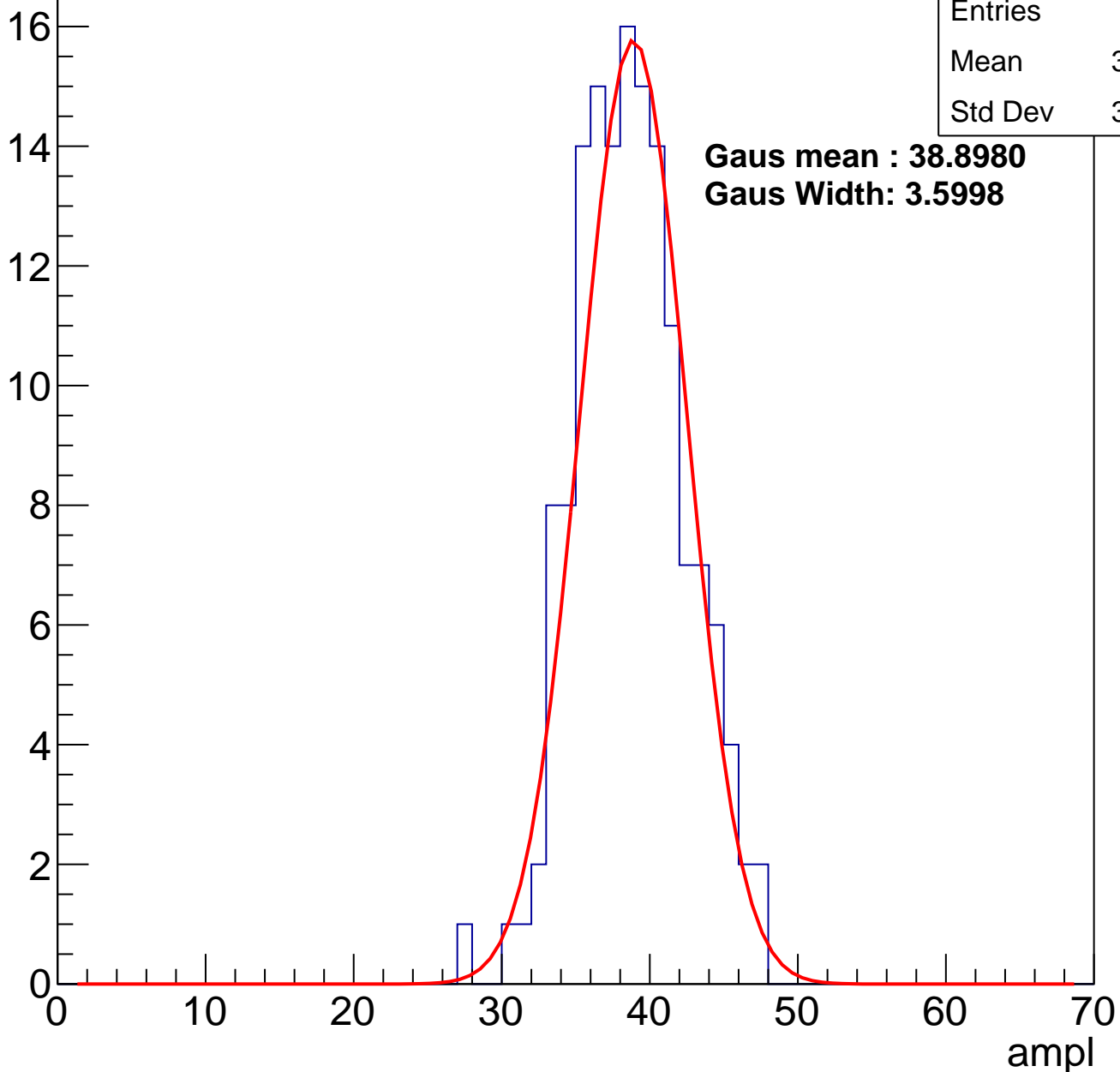
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	38.28
Std Dev	3.659

Gaus mean : 38.8980

Gaus Width: 3.5998

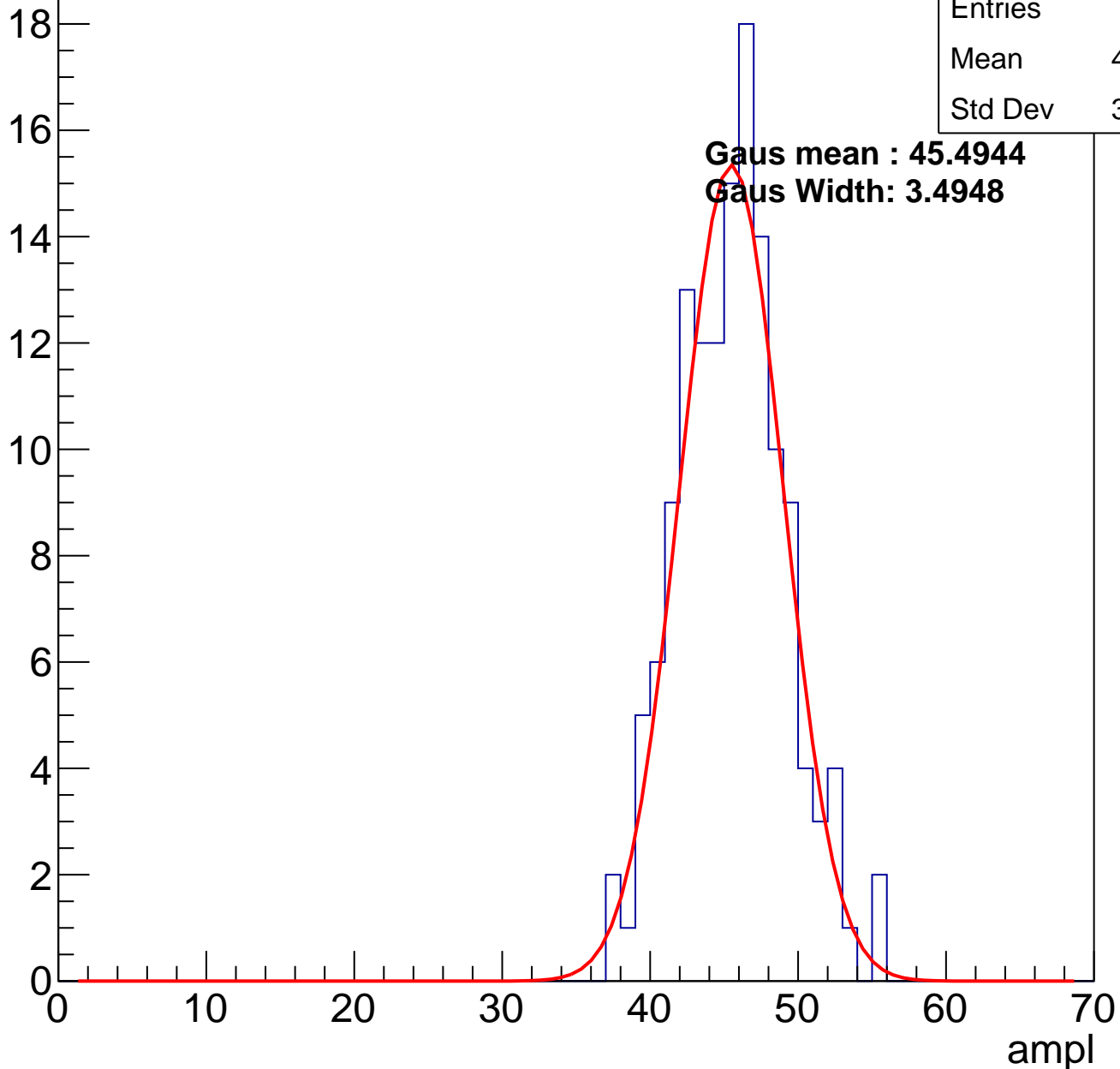
Entry



B0L101S, U24-ch61, adc2

calib_packv5_042523_0143.root, FC#1, port C1

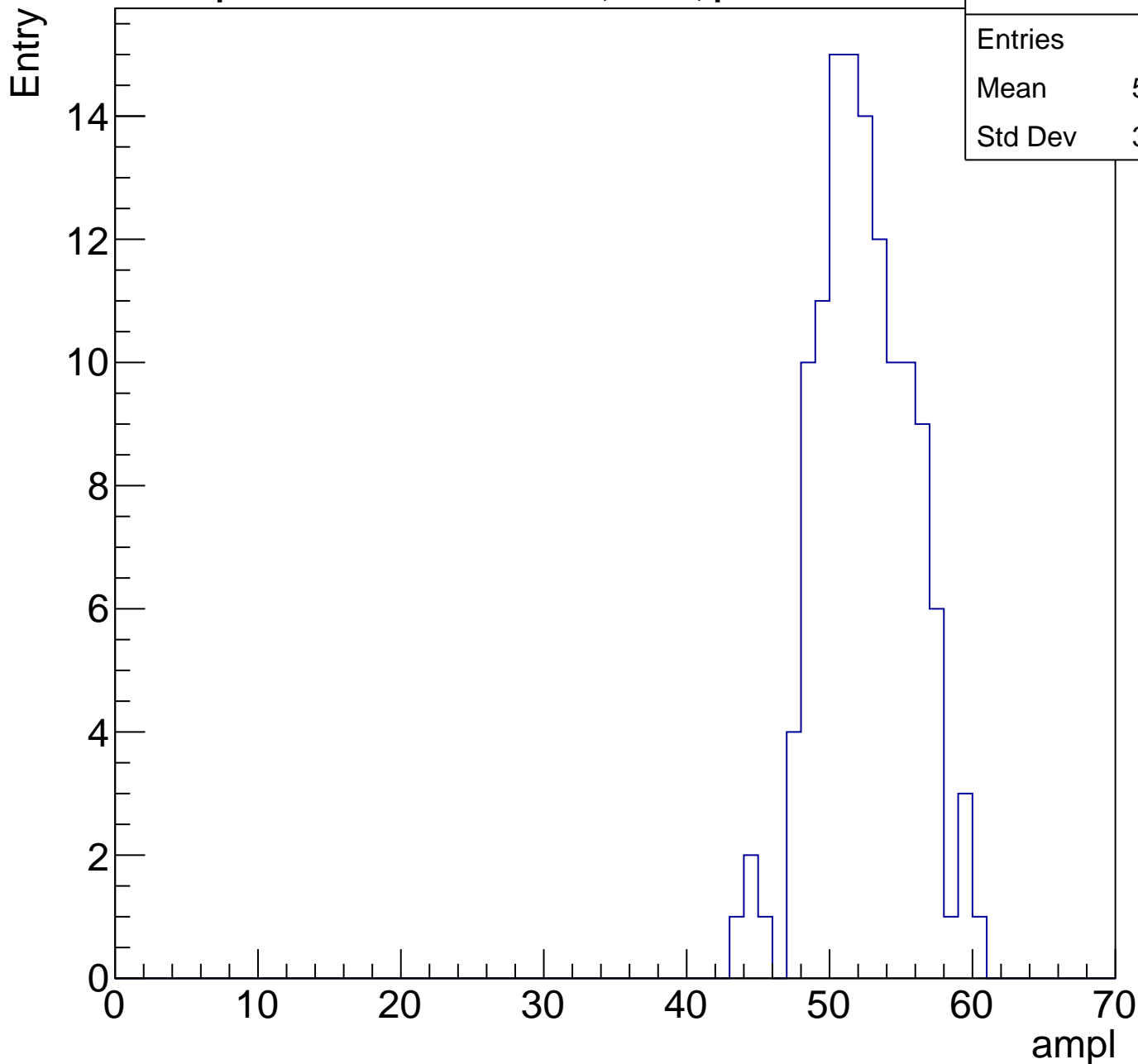
Entry



B0L101S, U24-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	51.94
Std Dev	3.316



B0L101S, U24-ch61, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	56.81
Std Dev	6.304

Entry

12

10

8

6

4

2

0

0

10

20

30

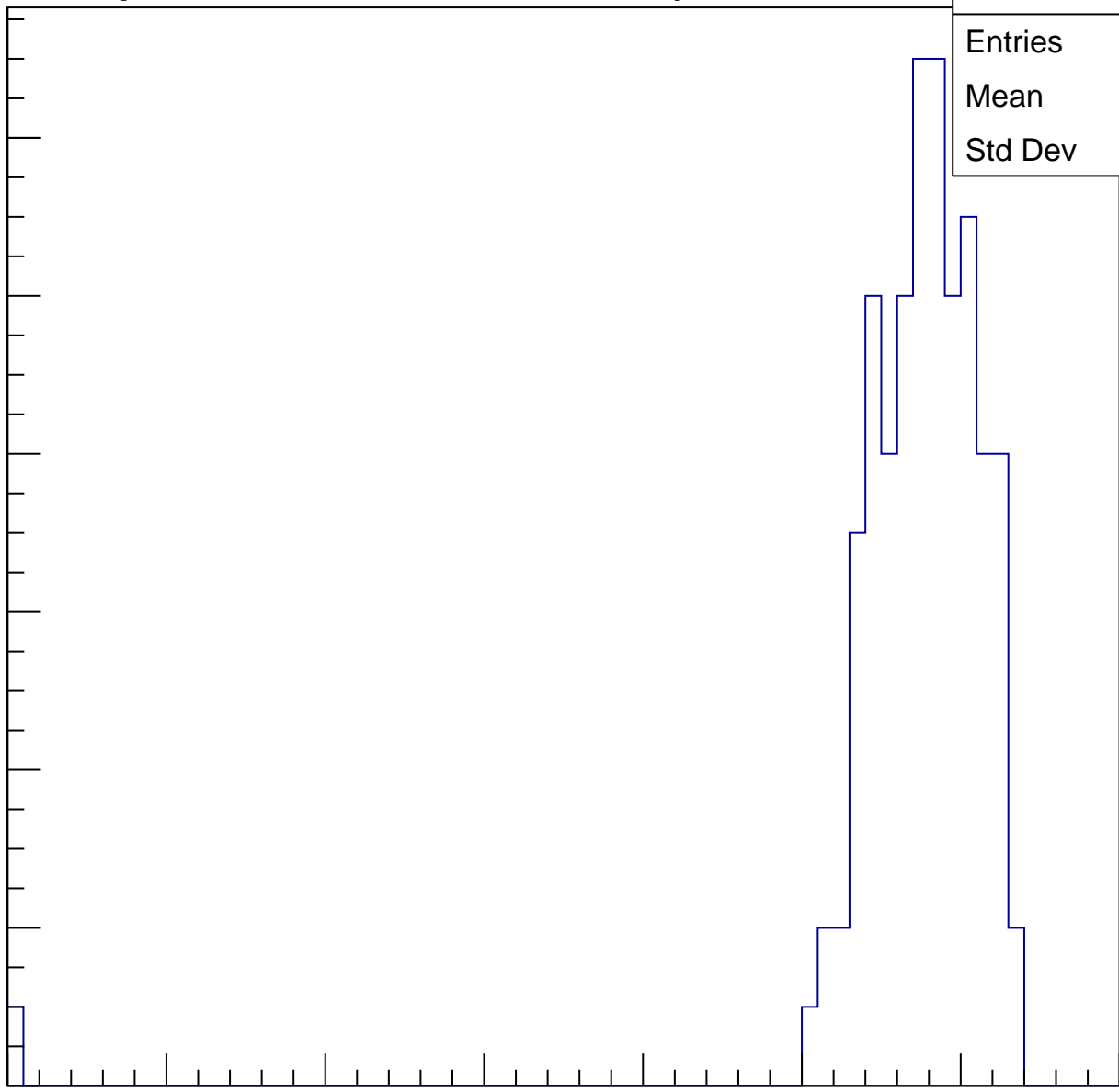
40

50

60

70

ampl



B0L101S, U24-ch61, adc5

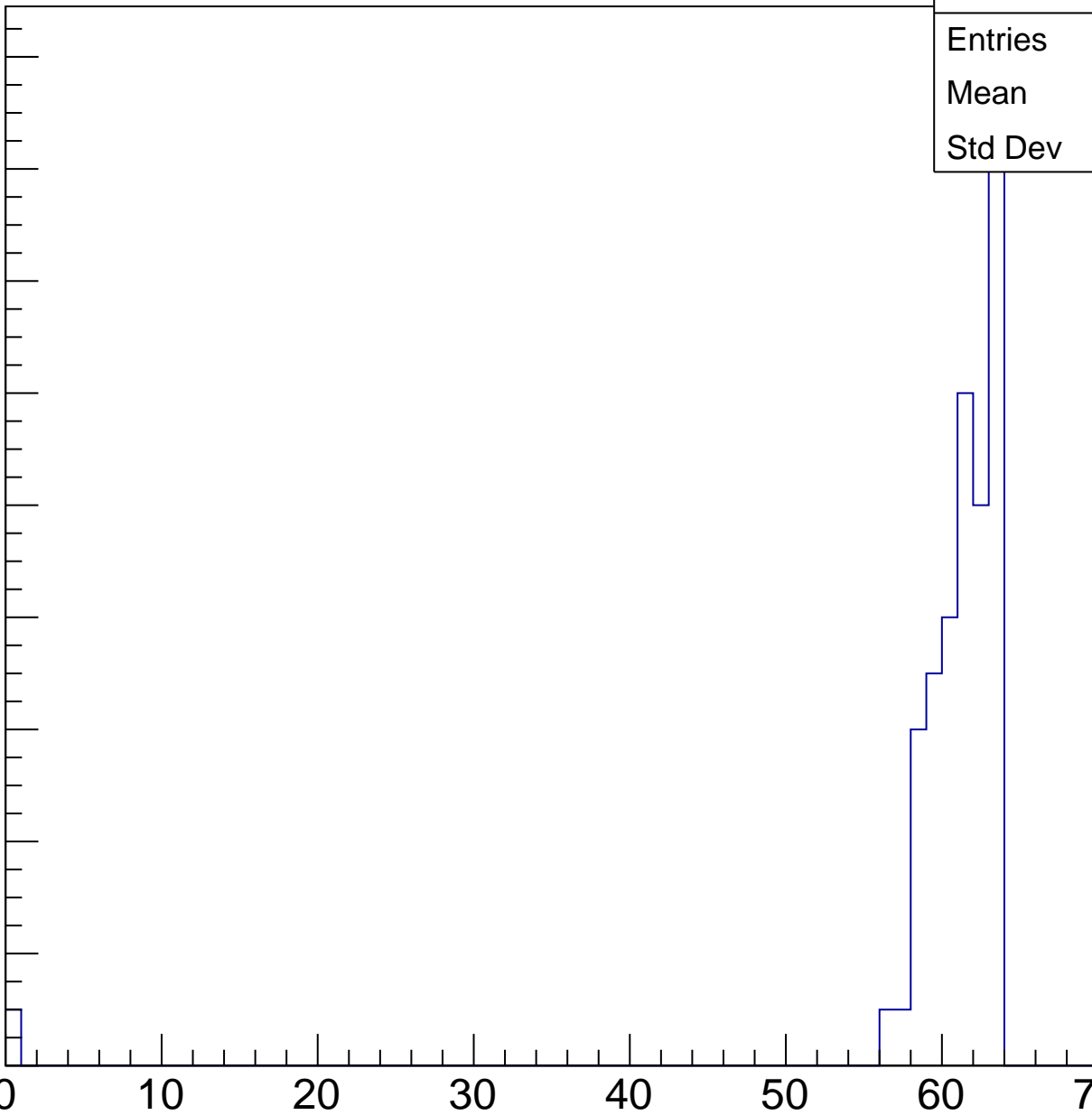
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	64
Mean	60
Std Dev	7.776

ampl



B0L101S, U24-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch62, adc0

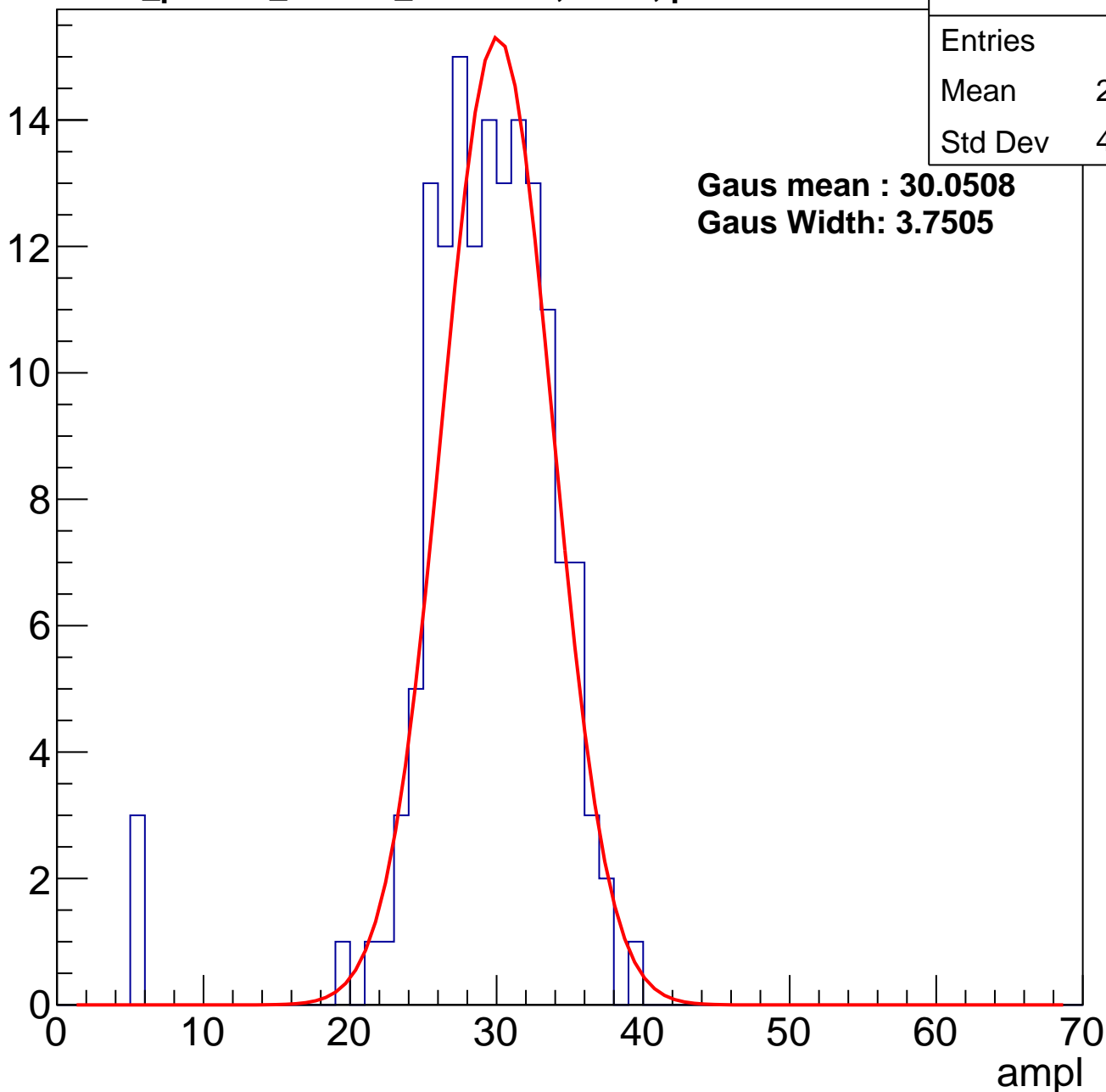
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	28.85
Std Dev	4.965

Gaus mean : 30.0508

Gaus Width: 3.7505

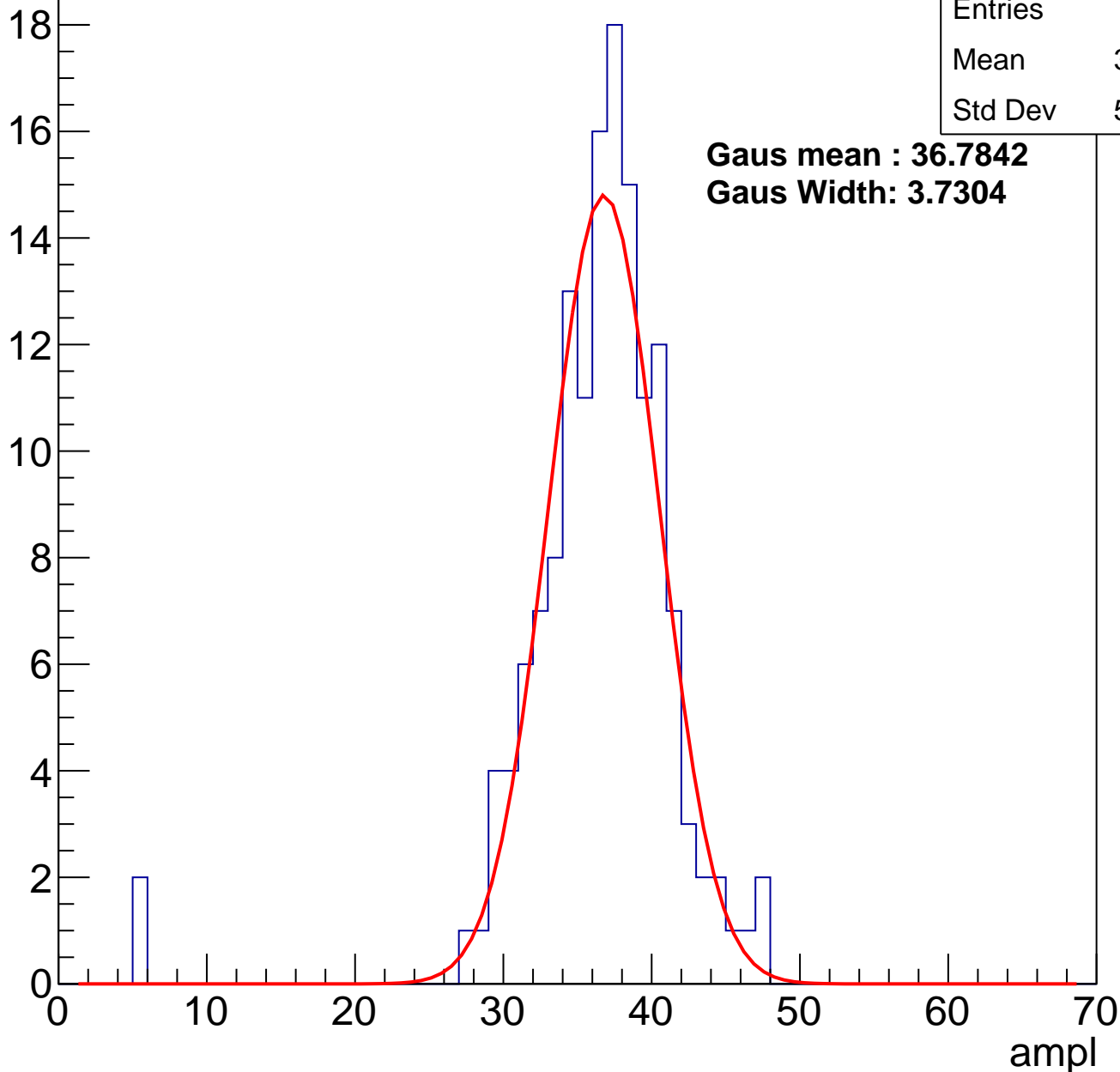
Entry



B0L101S, U24-ch62, adc1

calib_packv5_042523_0143.root, FC#1, port C1

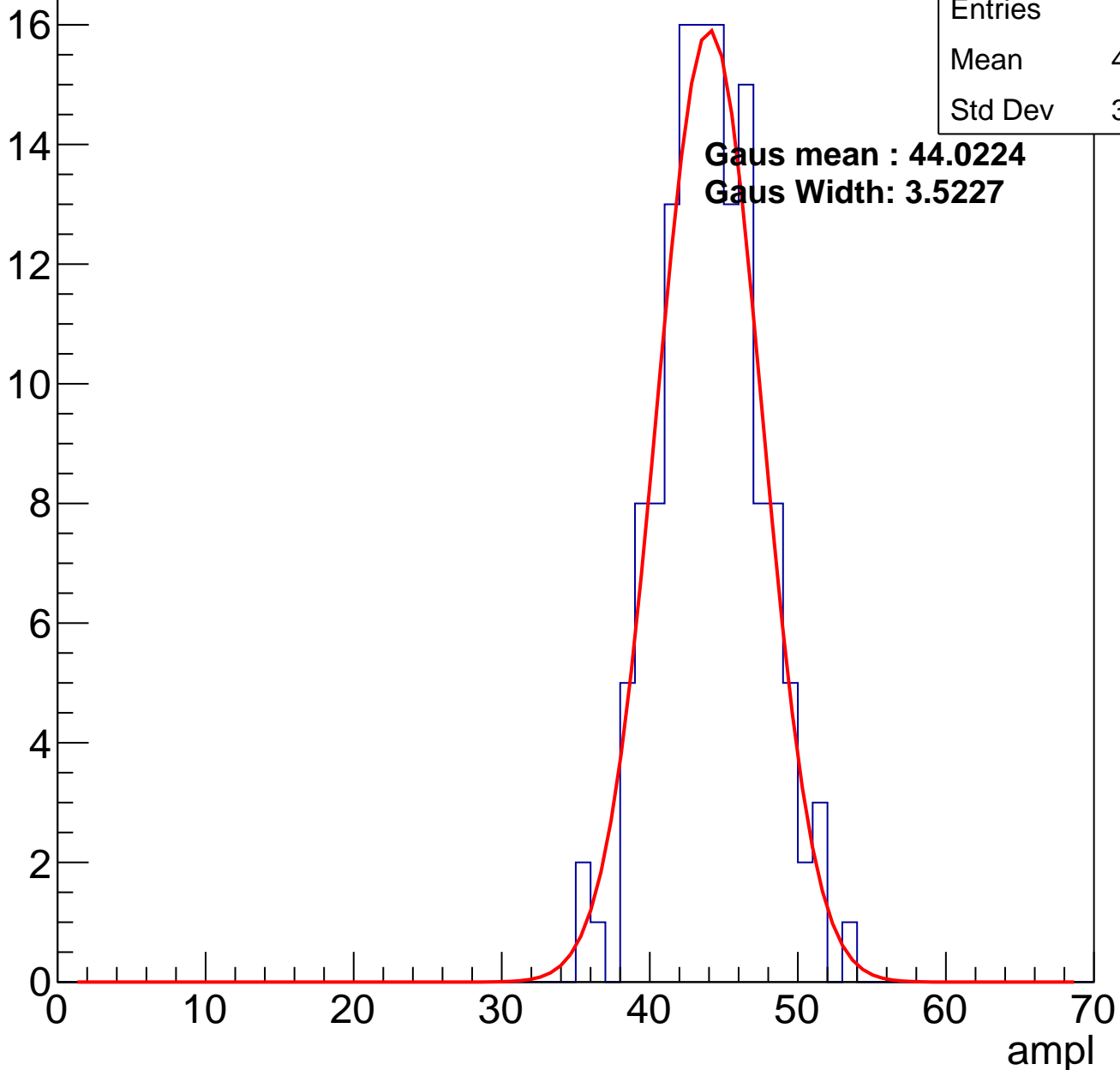
Entry



B0L101S, U24-ch62, adc2

calib_packv5_042523_0143.root, FC#1, port C1

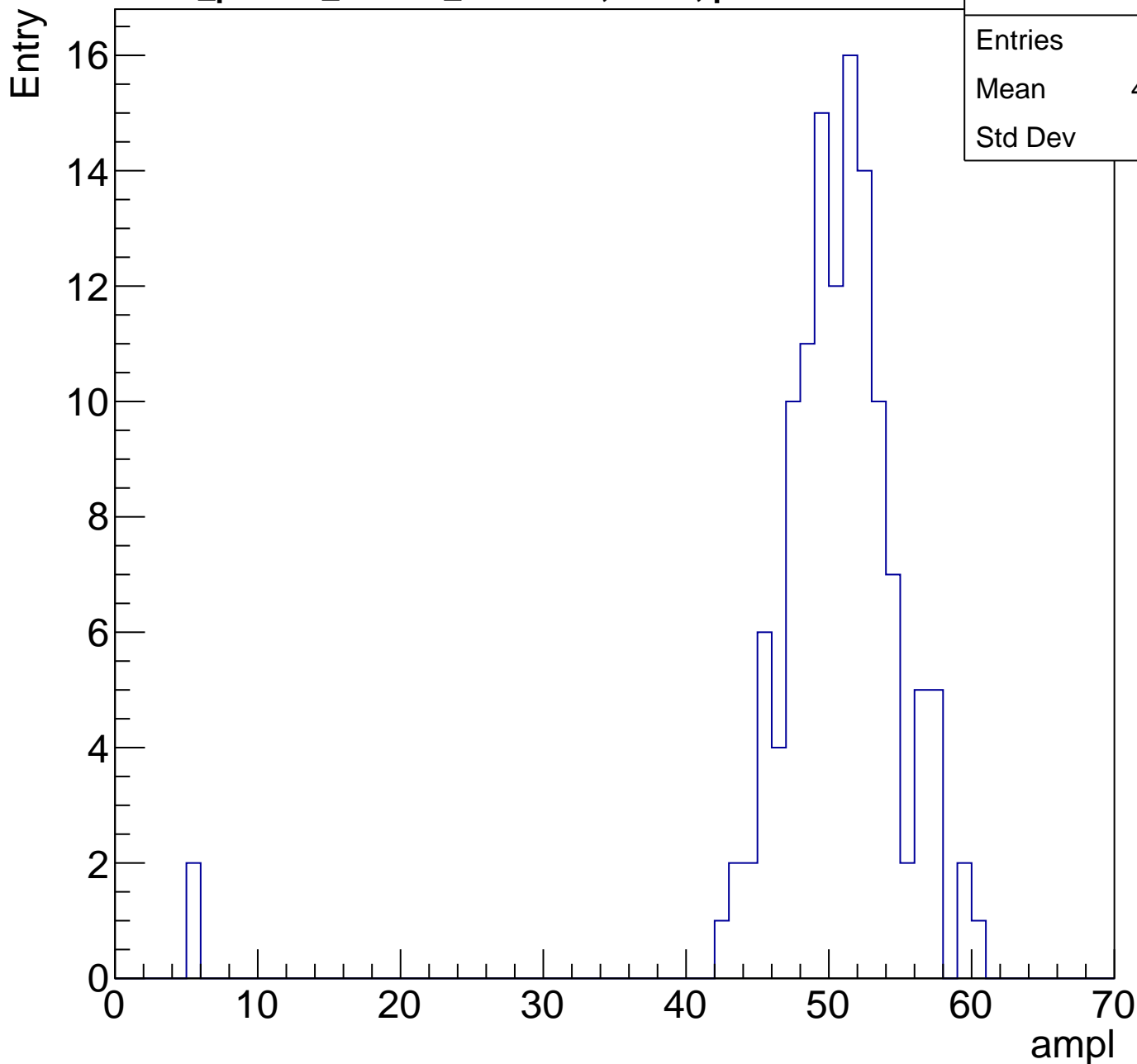
Entry



B0L101S, U24-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	49.75
Std Dev	6.67



B0L101S, U24-ch62, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

133

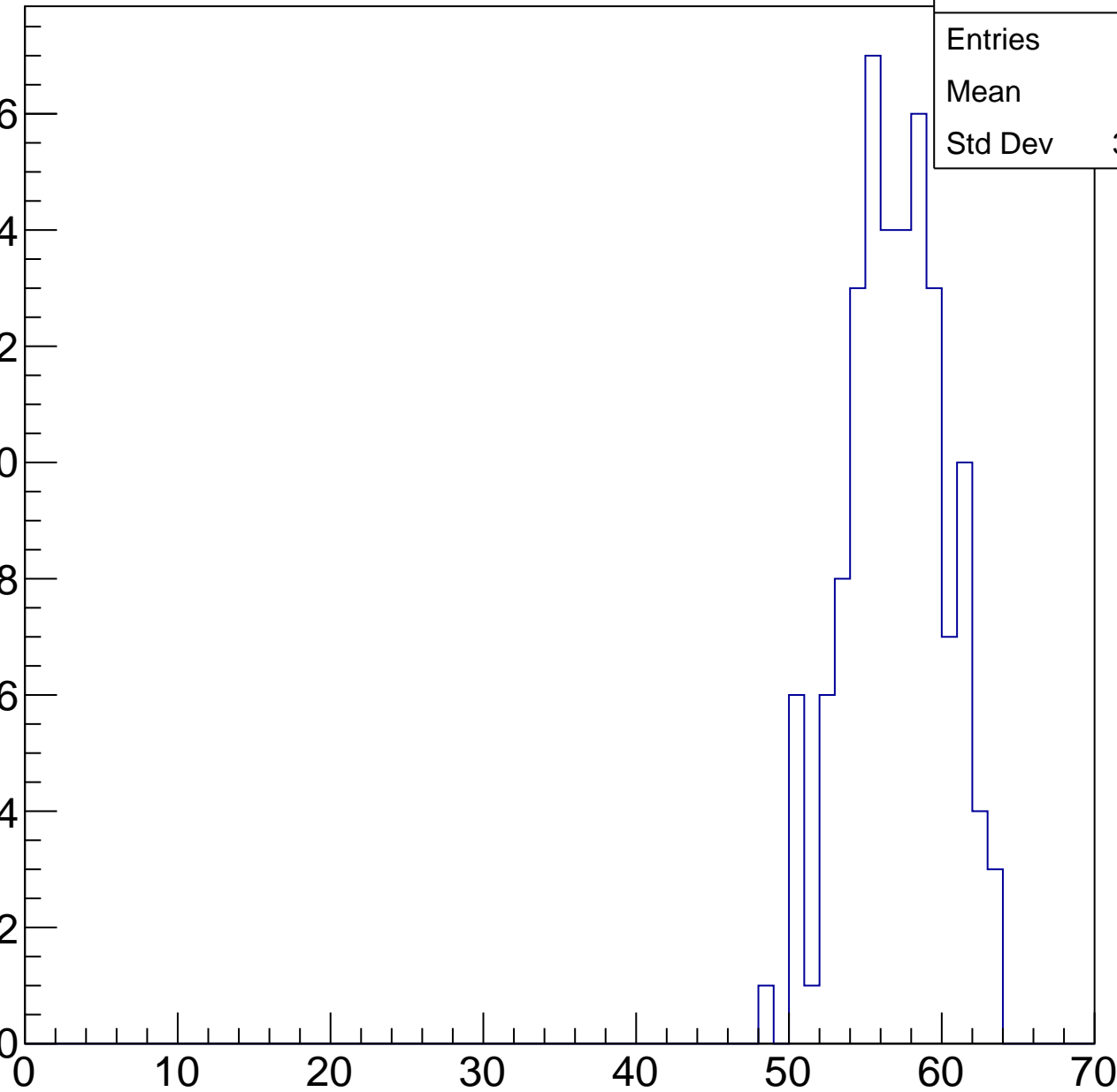
Mean

56.51

Std Dev

3.192

ampl



B0L101S, U24-ch62, adc5

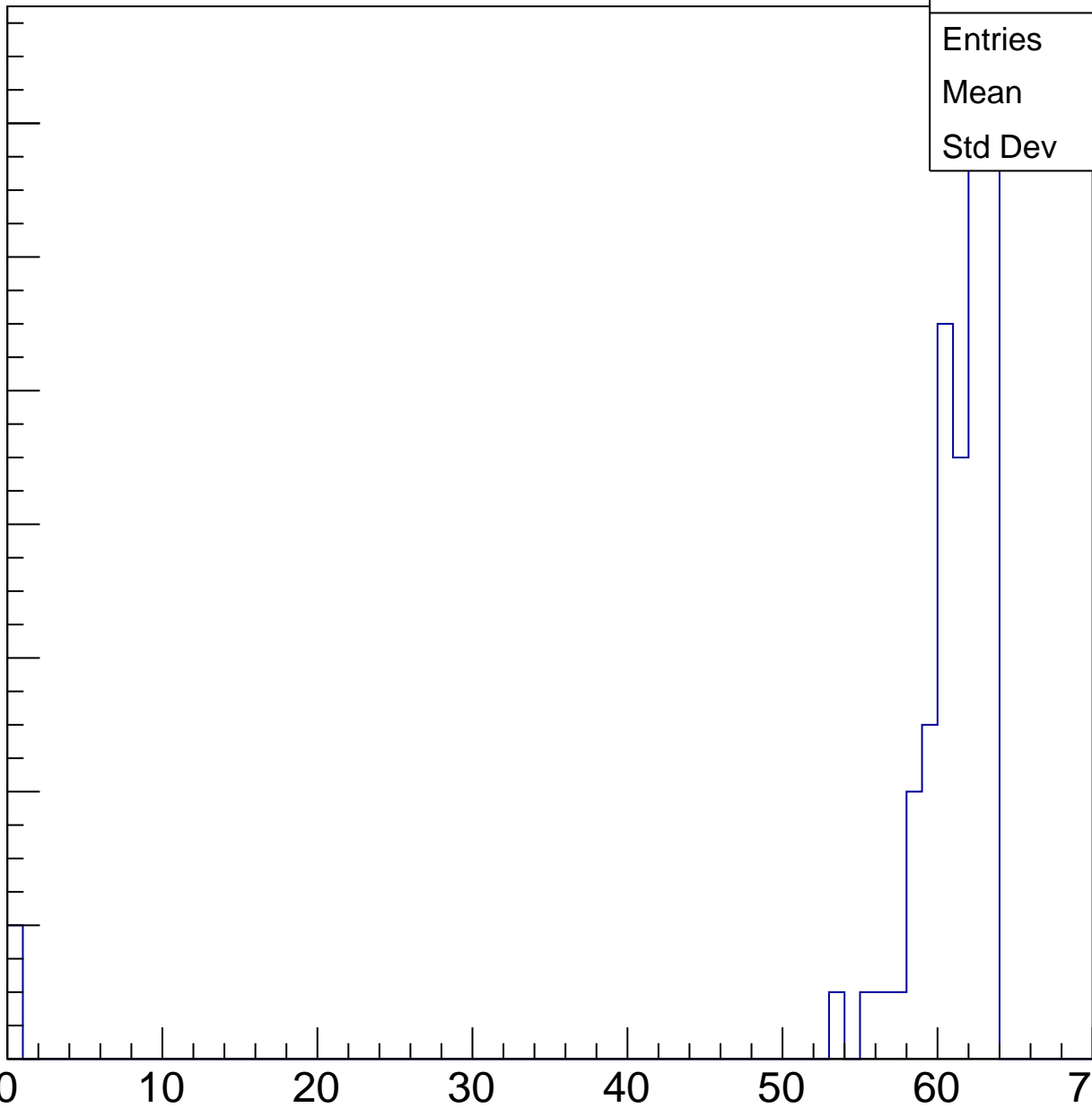
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.95
Std Dev	10.71

ampl



B0L101S, U24-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

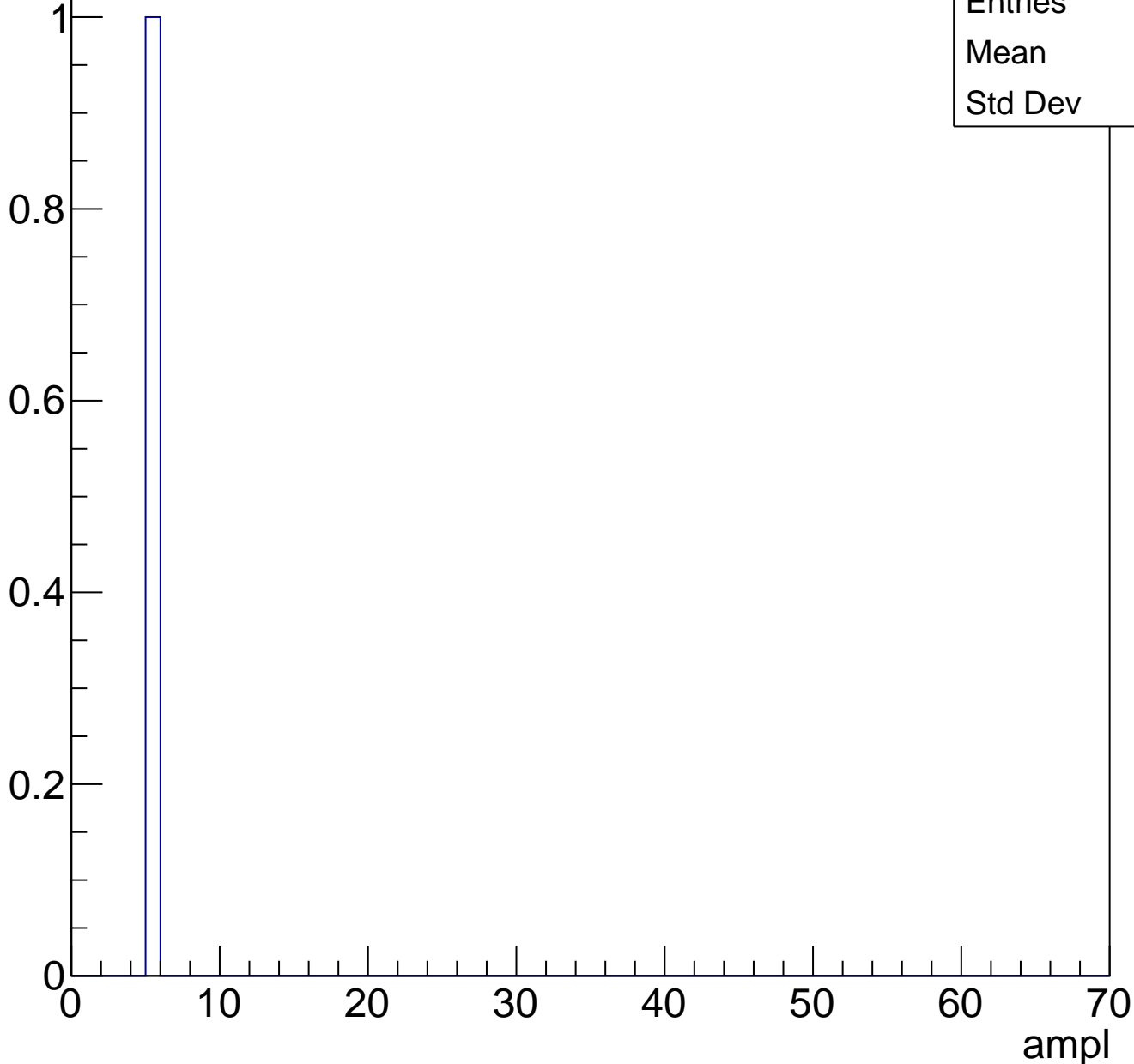
Entry



B0L101S, U24-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch63, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	29.44
Std Dev	4.843

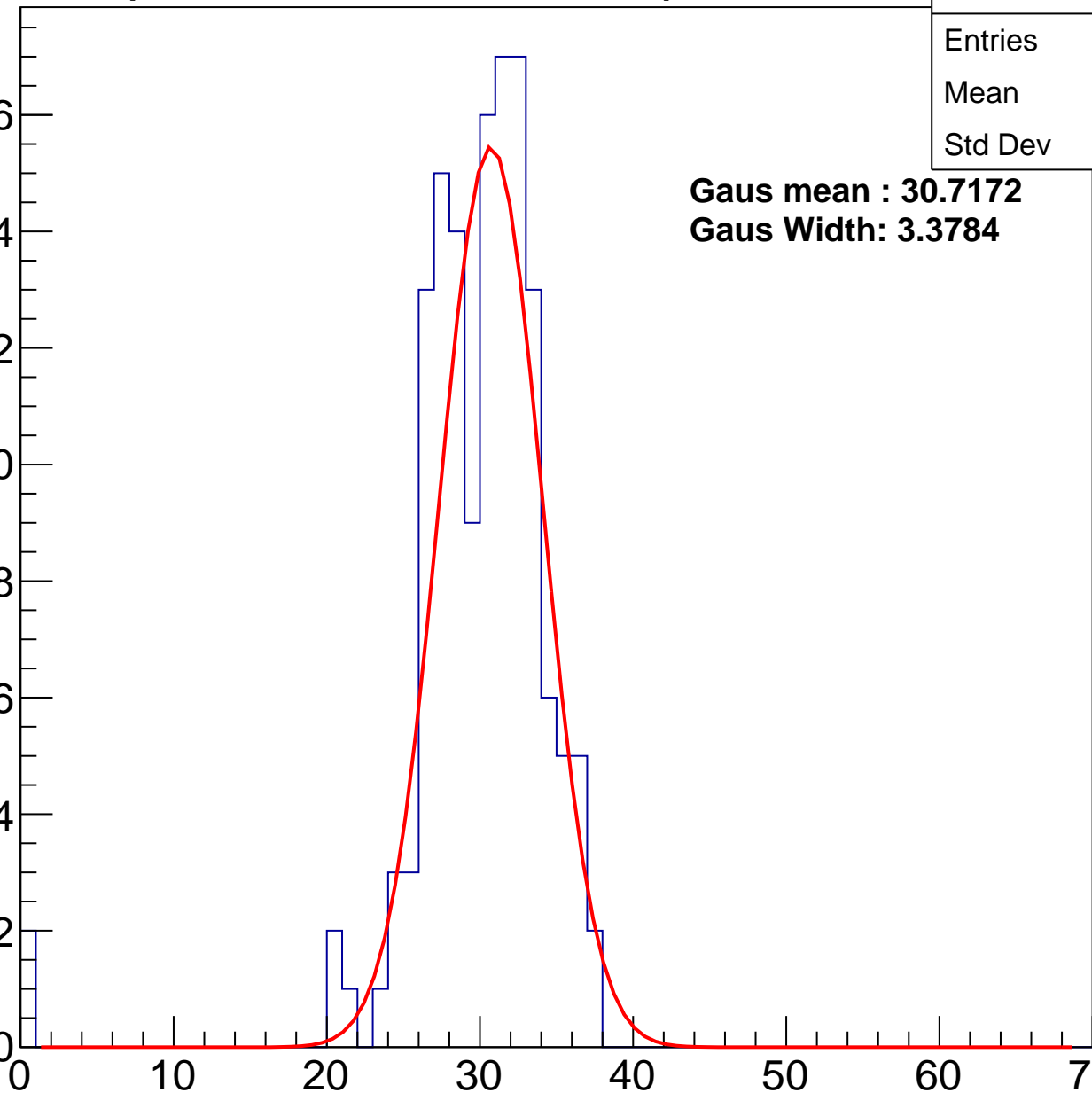
Gaus mean : 30.7172

Gaus Width: 3.3784

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch63, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	36.83
Std Dev	3.278

Gaus mean : 37.6938

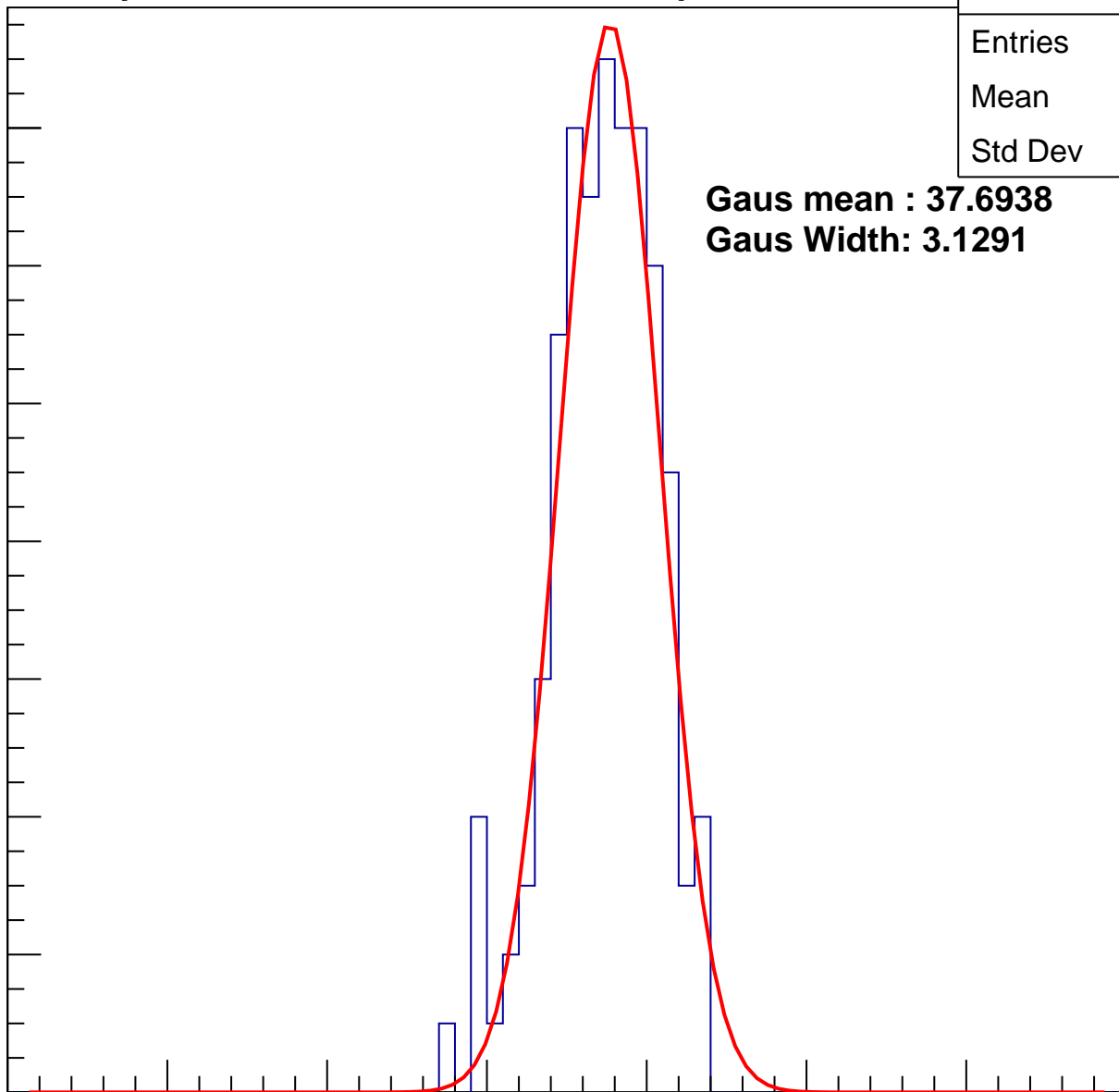
Gaus Width: 3.1291

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch63, adc2

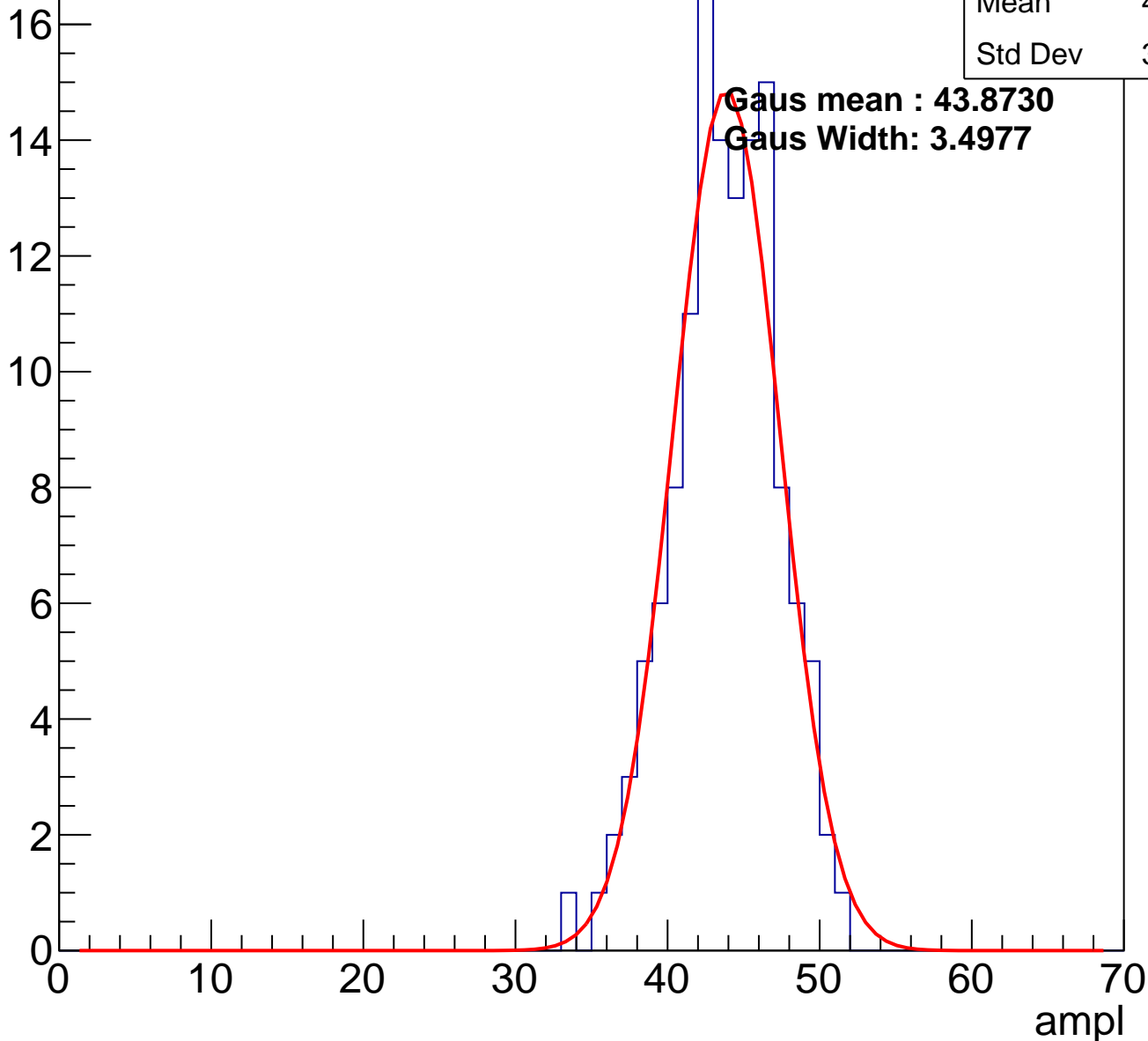
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	132
Mean	43.29
Std Dev	3.412

Gaus mean : 43.8730

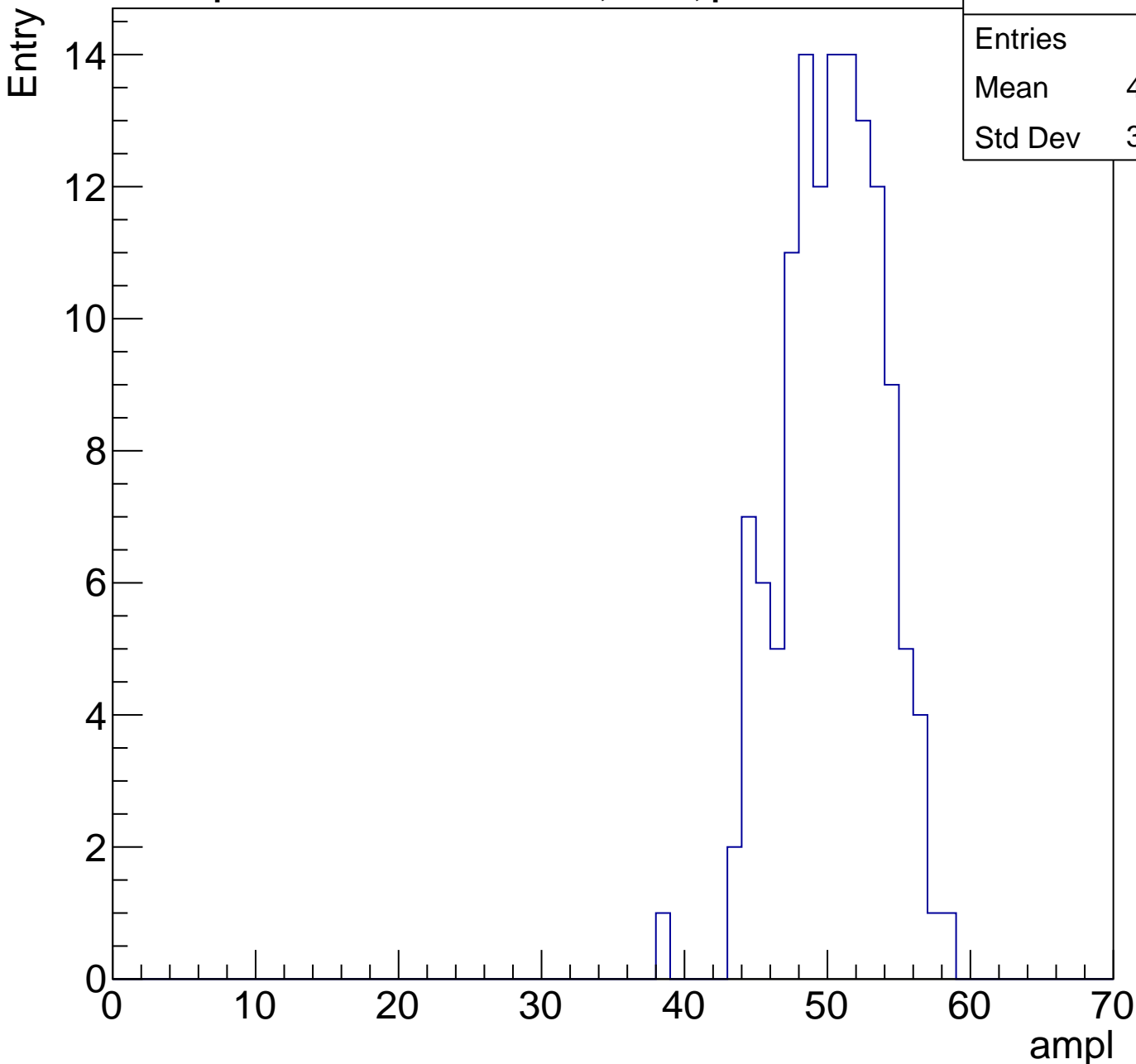
Gaus Width: 3.4977



B0L101S, U24-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

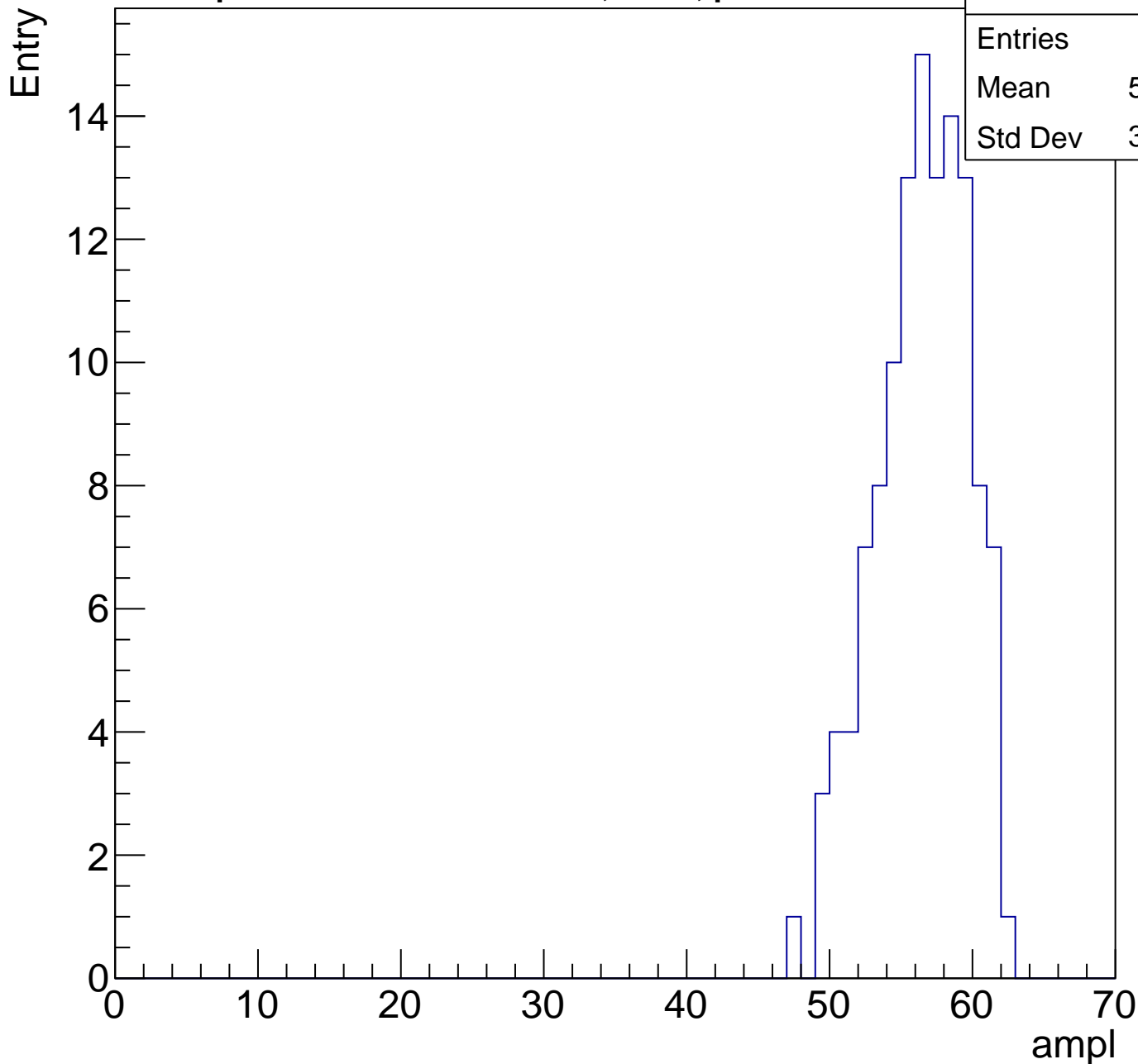
Entries	131
Mean	49.89
Std Dev	3.483



B0L101S, U24-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	55.95
Std Dev	3.193

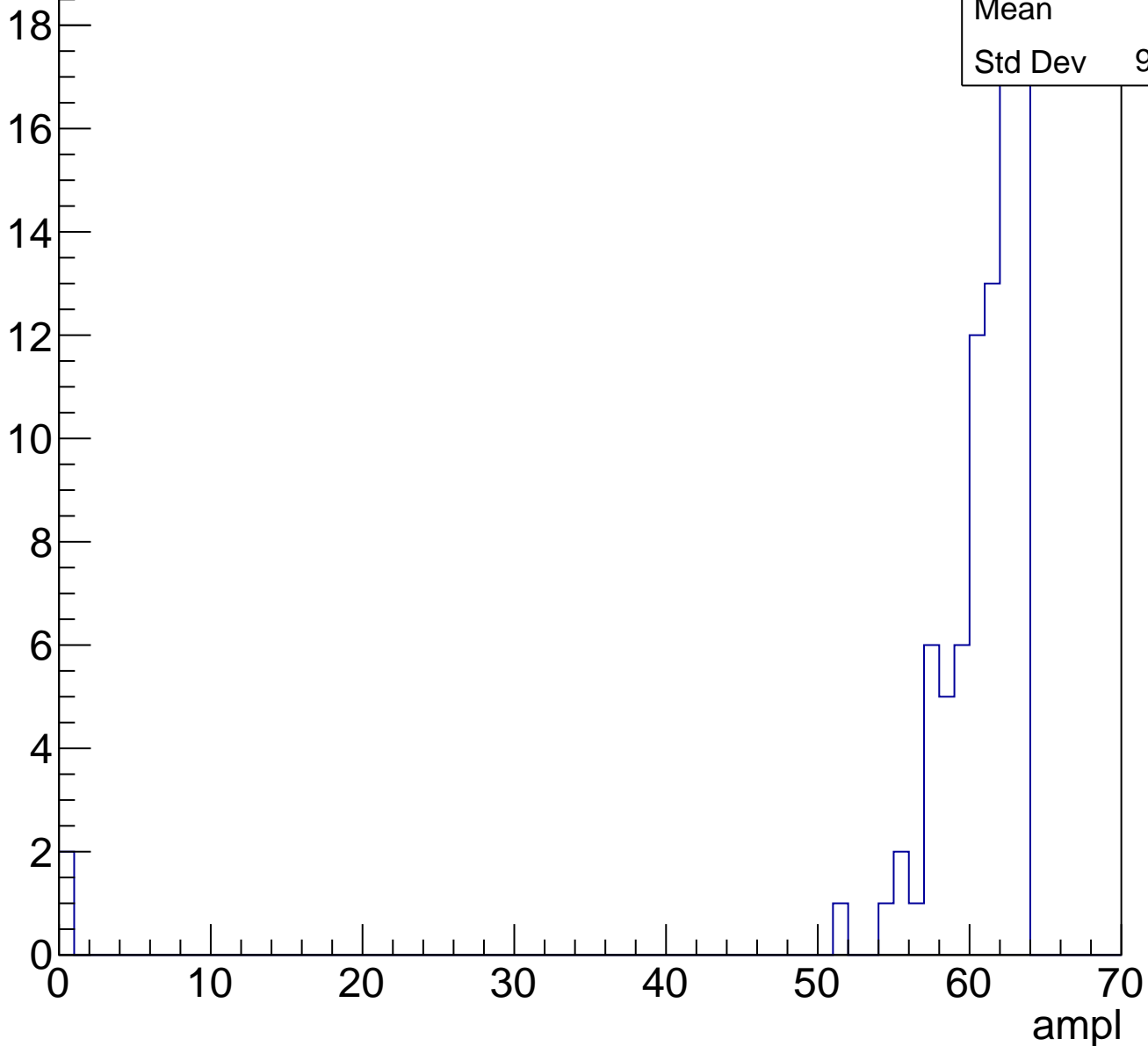


B0L101S, U24-ch63, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	86
Mean	59.1
Std Dev	9.432

Entry



B0L101S, U24-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

61.33

Std Dev

1.7

B0L101S, U24-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch64, adc0

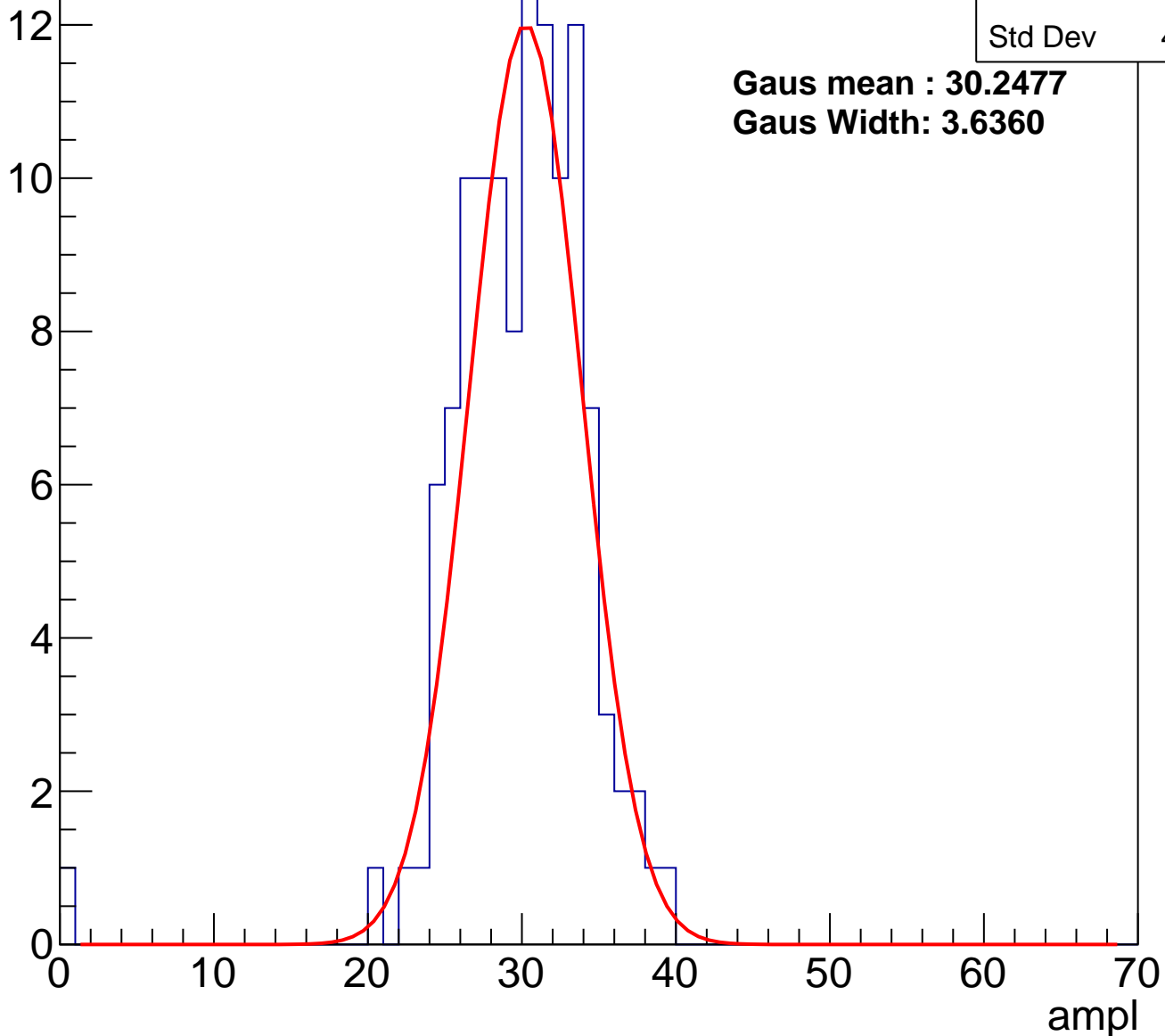
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	29.41
Std Dev	4.531

Gaus mean : 30.2477

Gaus Width: 3.6360

Entry



B0L101S, U24-ch64, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	35.49
Std Dev	4.956

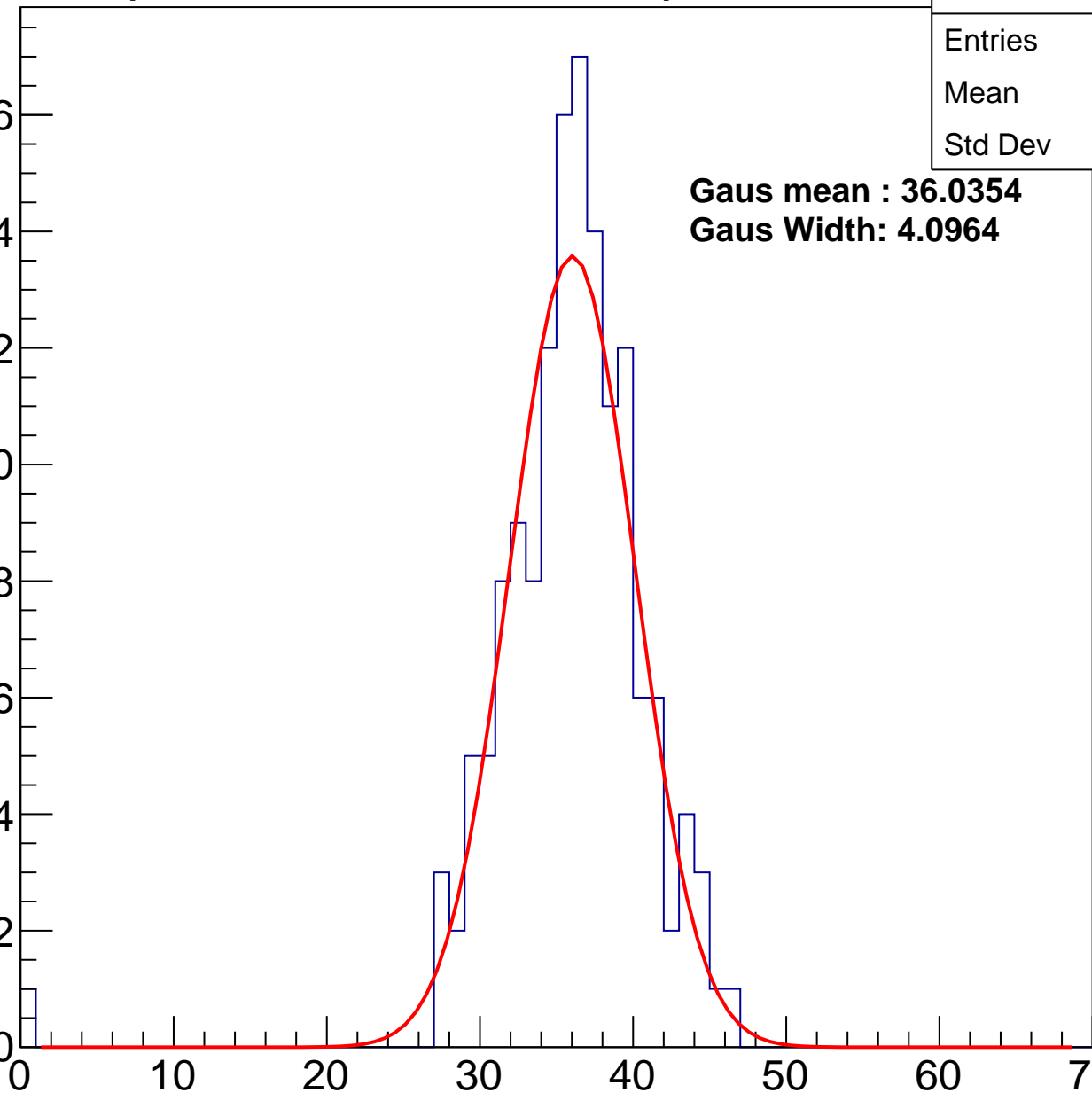
Gaus mean : 36.0354

Gaus Width: 4.0964

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch64, adc2

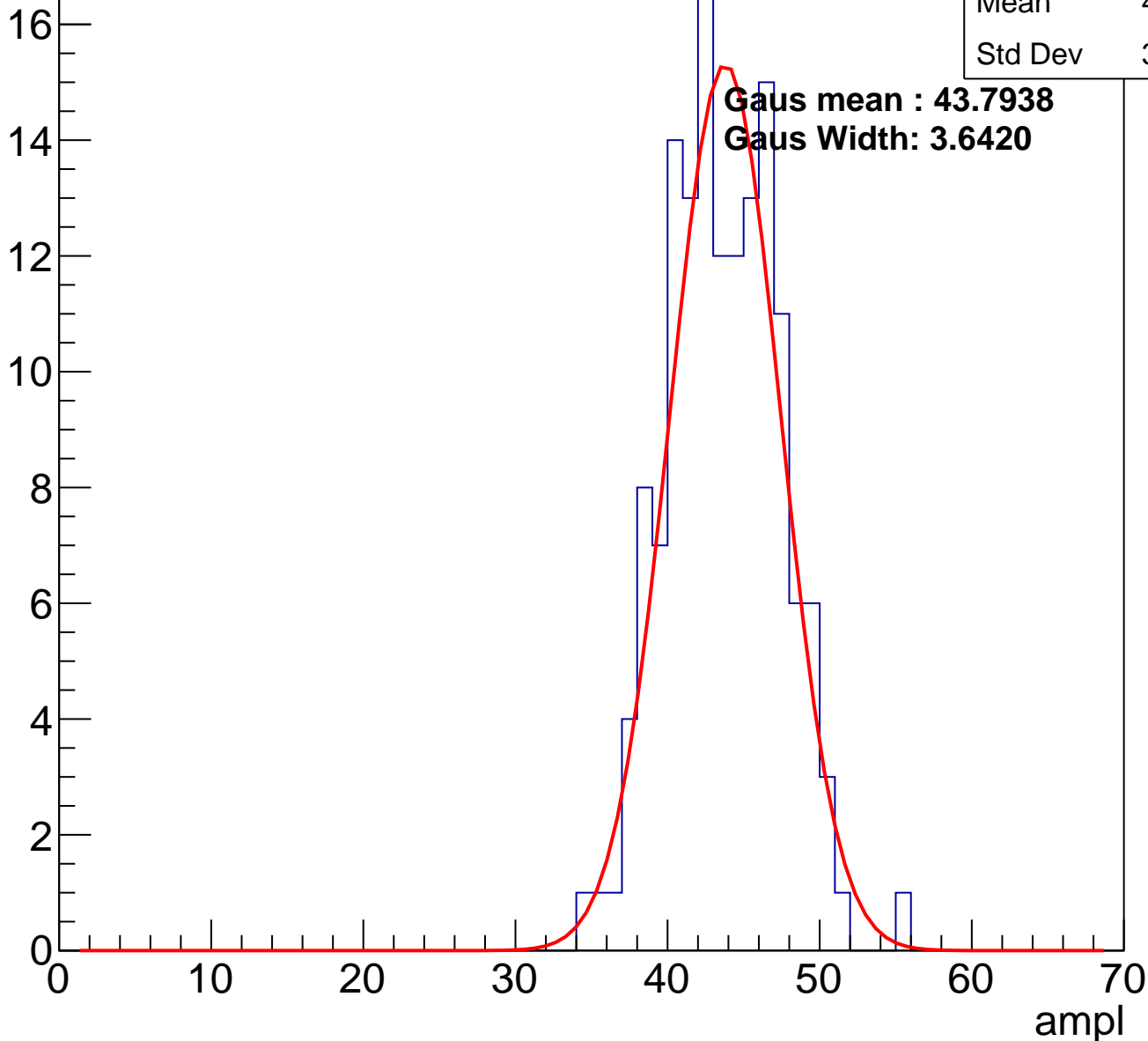
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	146
Mean	43.23
Std Dev	3.639

Gaus mean : 43.7938

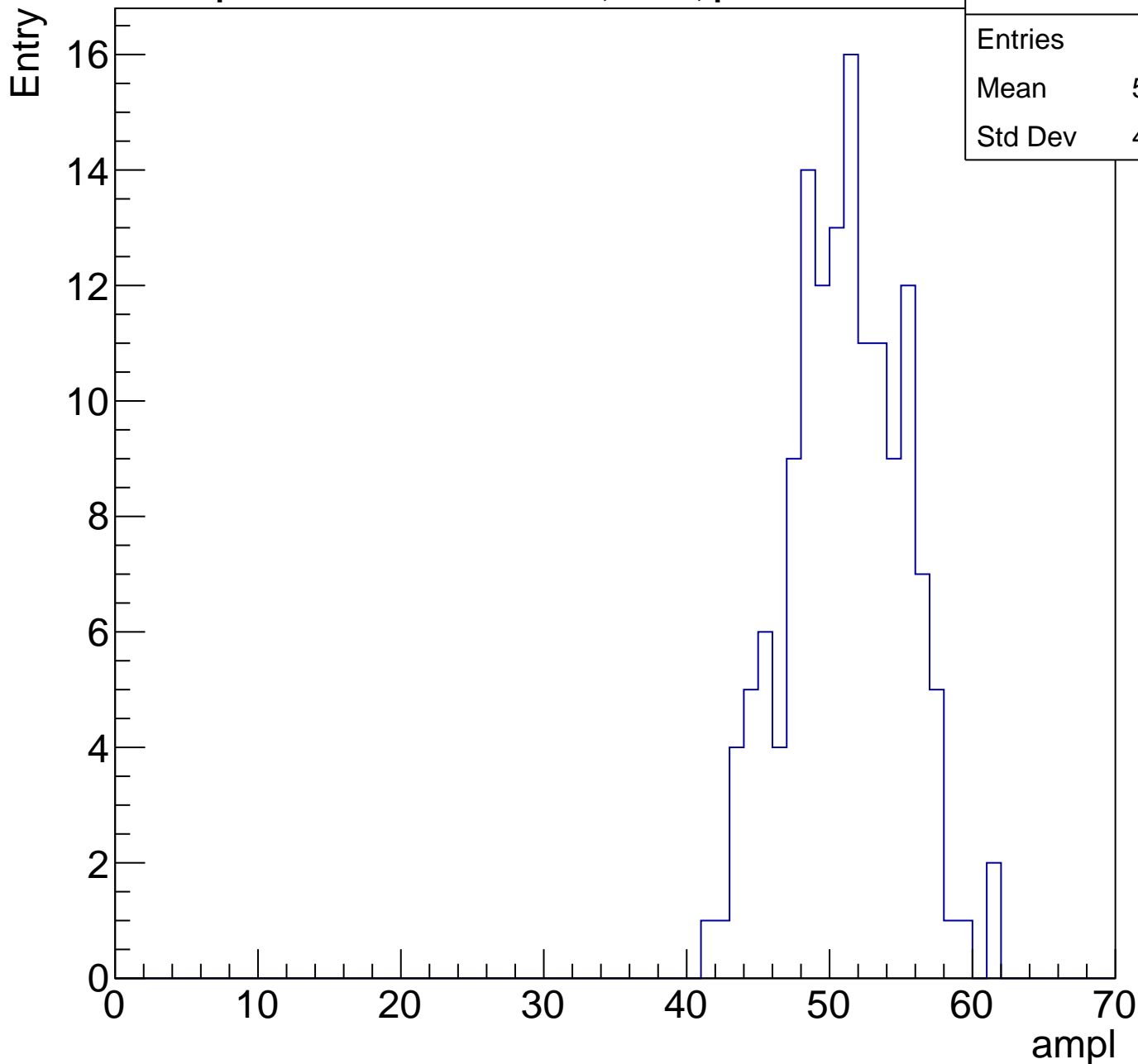
Gaus Width: 3.6420



B0L101S, U24-ch64, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	50.66
Std Dev	4.004



B0L101S, U24-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	121
Mean	56.57
Std Dev	3.478

B0L101S, U24-ch64, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	59
Mean	59.56
Std Dev	8.058

ampl

0 10 20 30 40 50 60 70

0

2

4

6

8

10

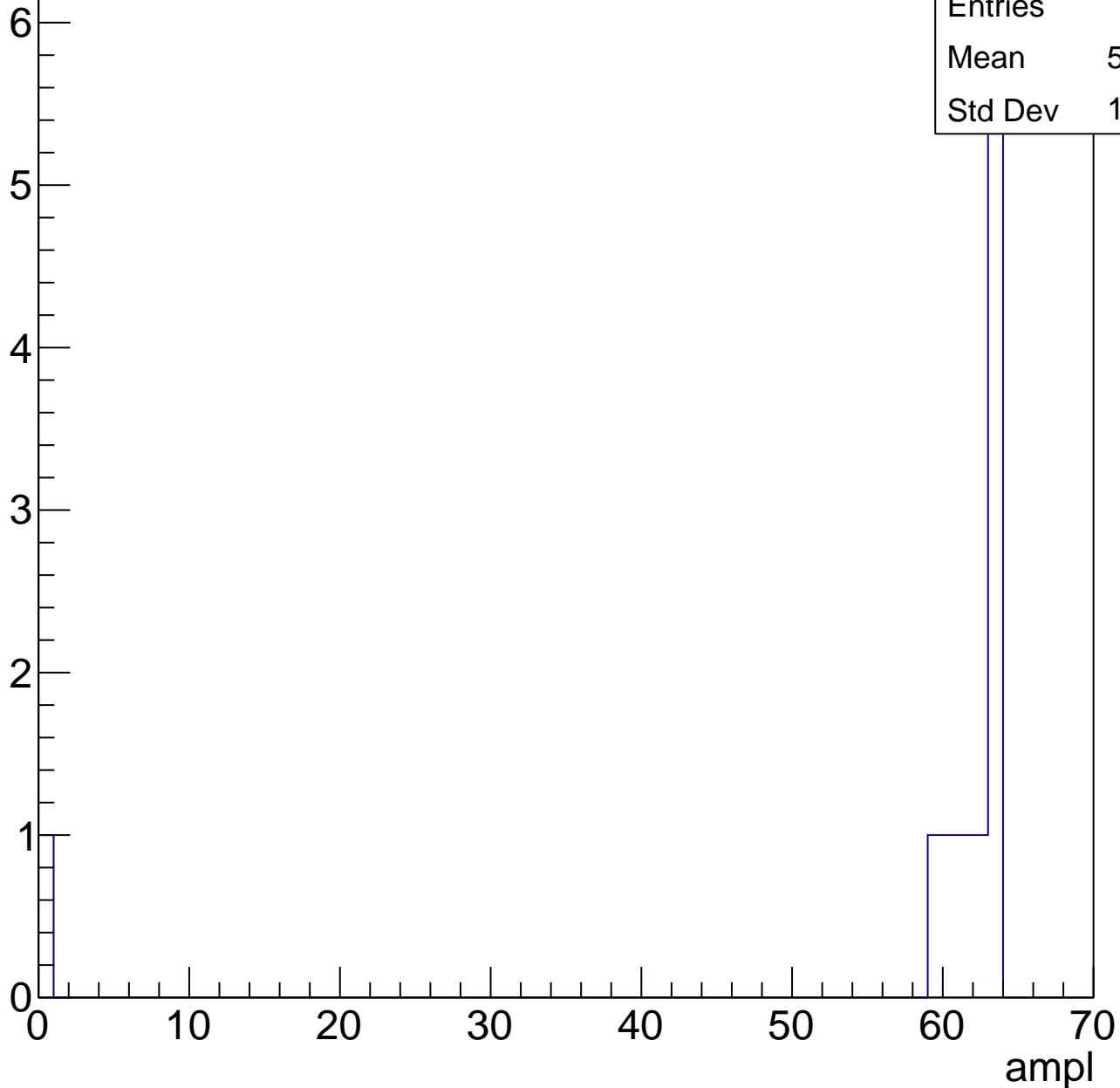
12

B0L101S, U24-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	56.36
Std Dev	17.87



B0L101S, U24-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

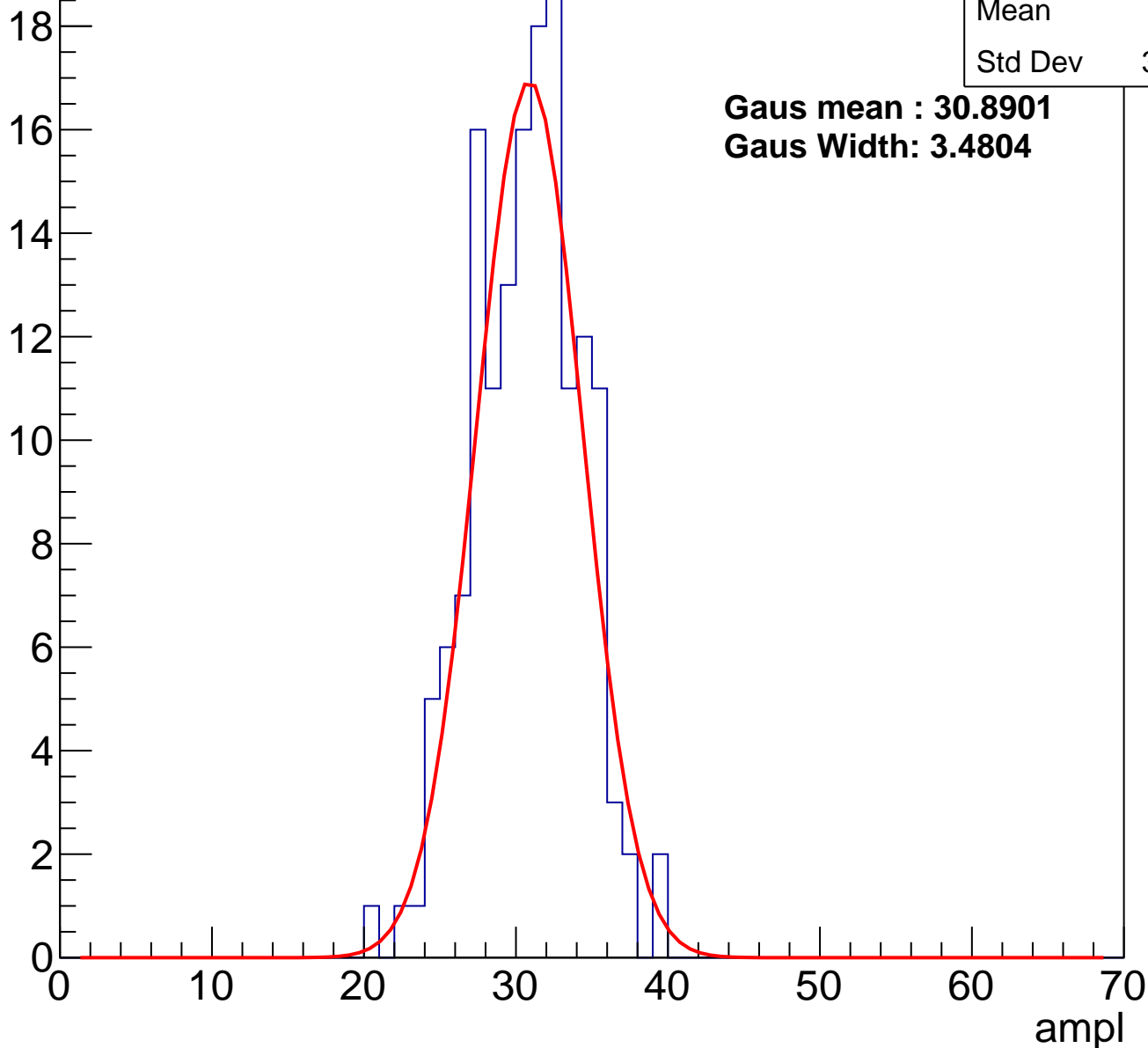
B0L101S, U24-ch65, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	30.3
Std Dev	3.474

Gaus mean : 30.8901
Gaus Width: 3.4804

Entry



B0L101S, U24-ch65, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	37.29
Std Dev	2.905

Gaus mean : 37.9206

Gaus Width: 2.7498

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

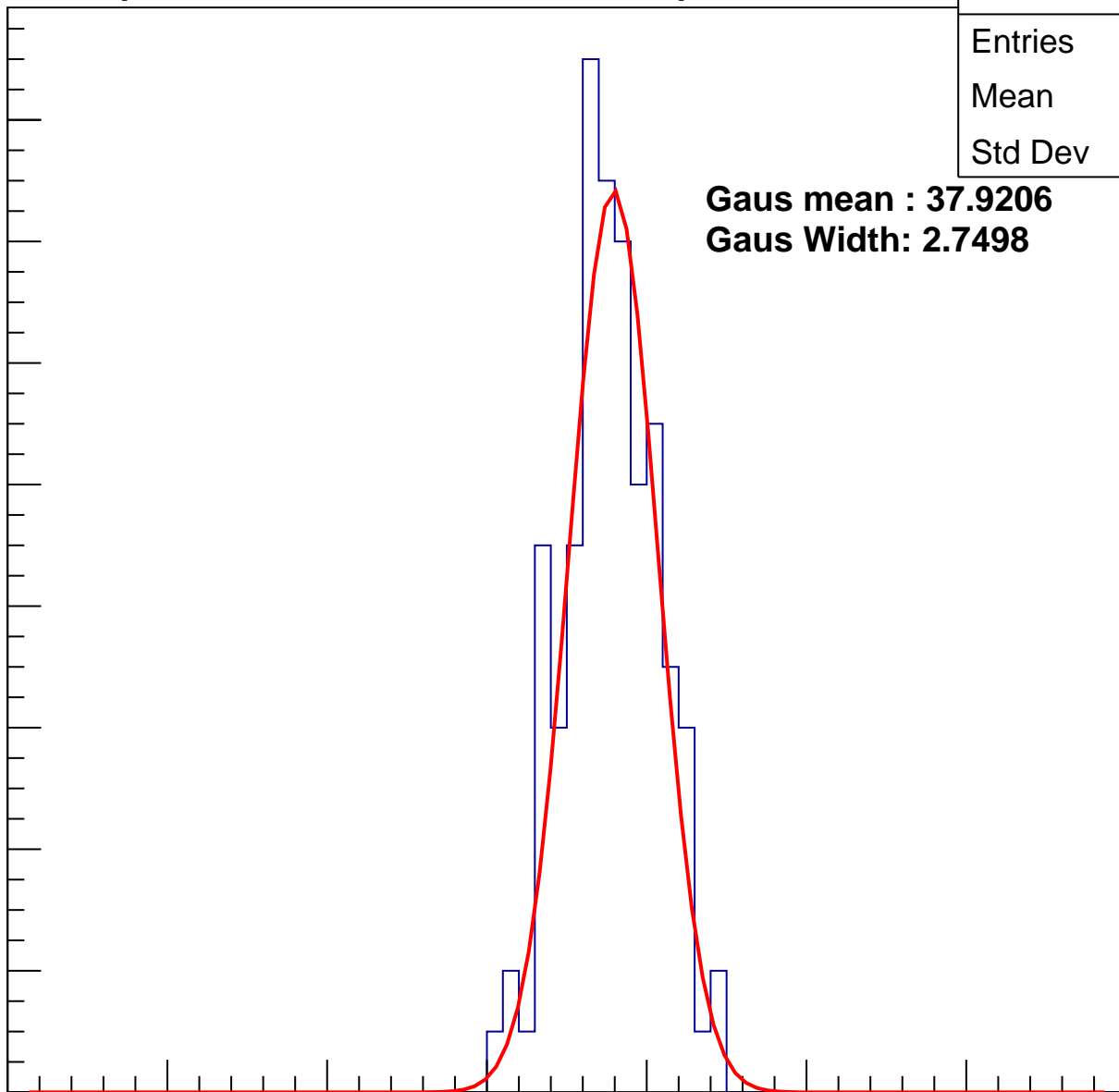
40

50

60

70

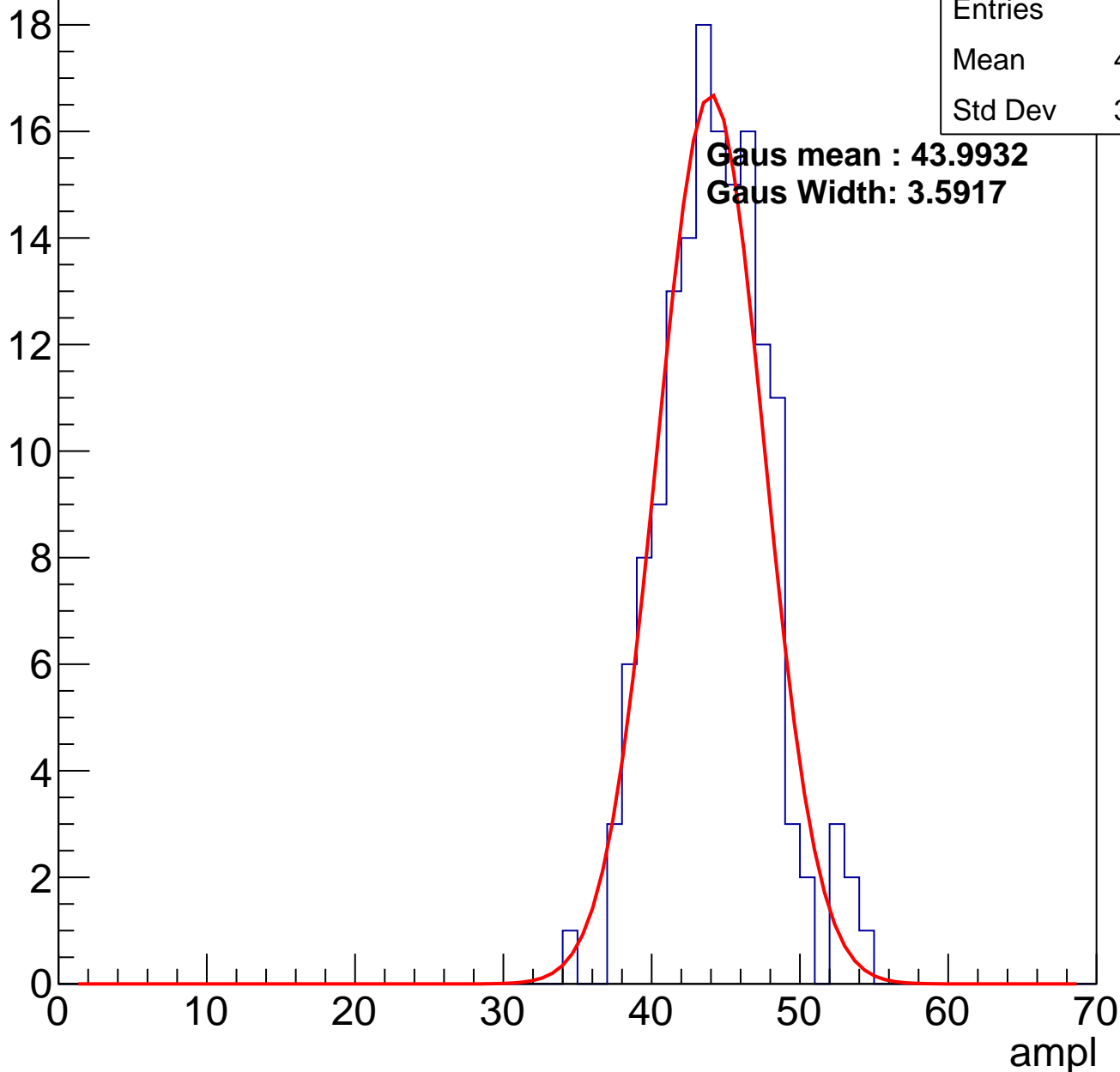
ampl



B0L101S, U24-ch65, adc2

calib_packv5_042523_0143.root, FC#1, port C1

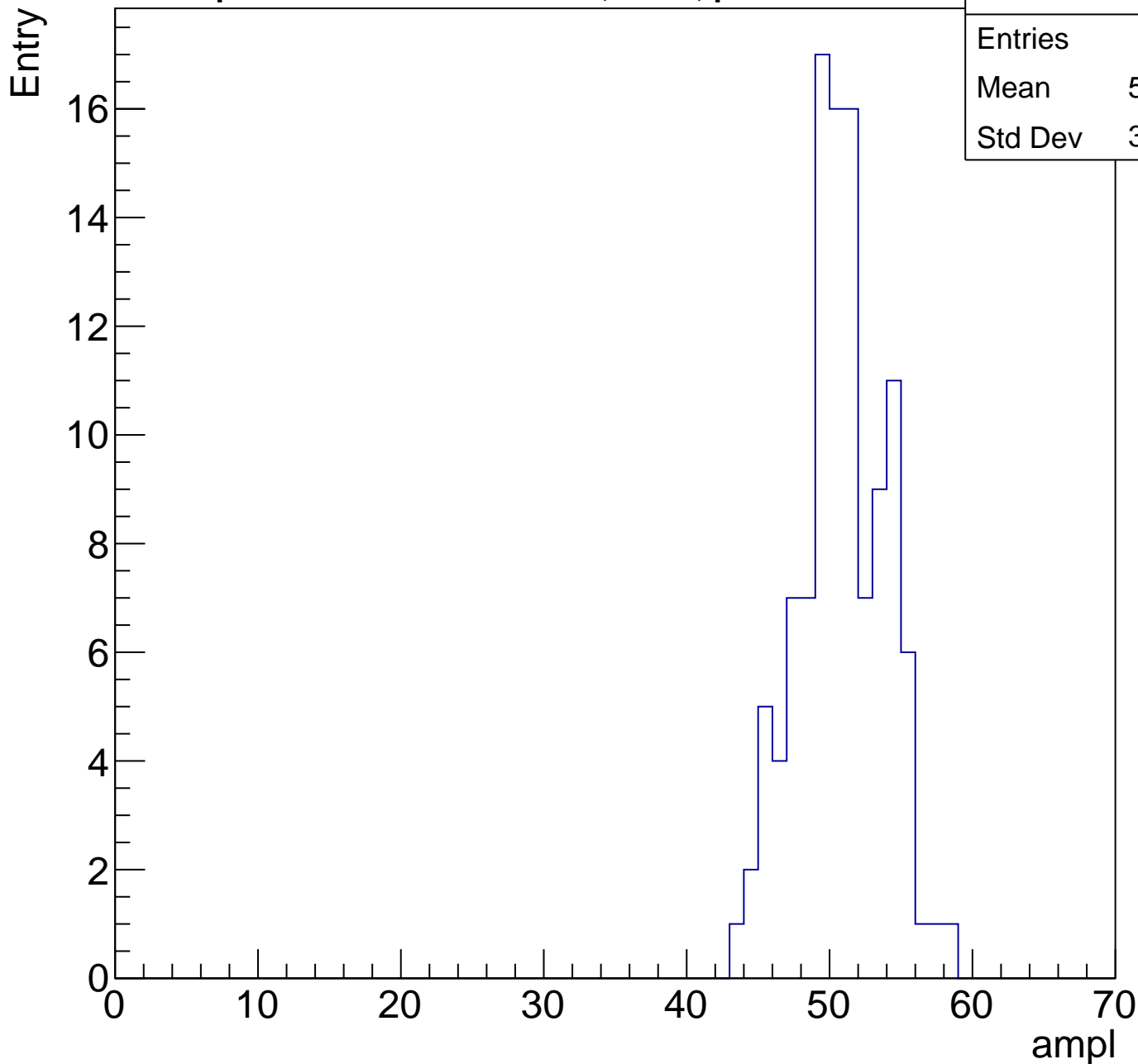
Entry



B0L101S, U24-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

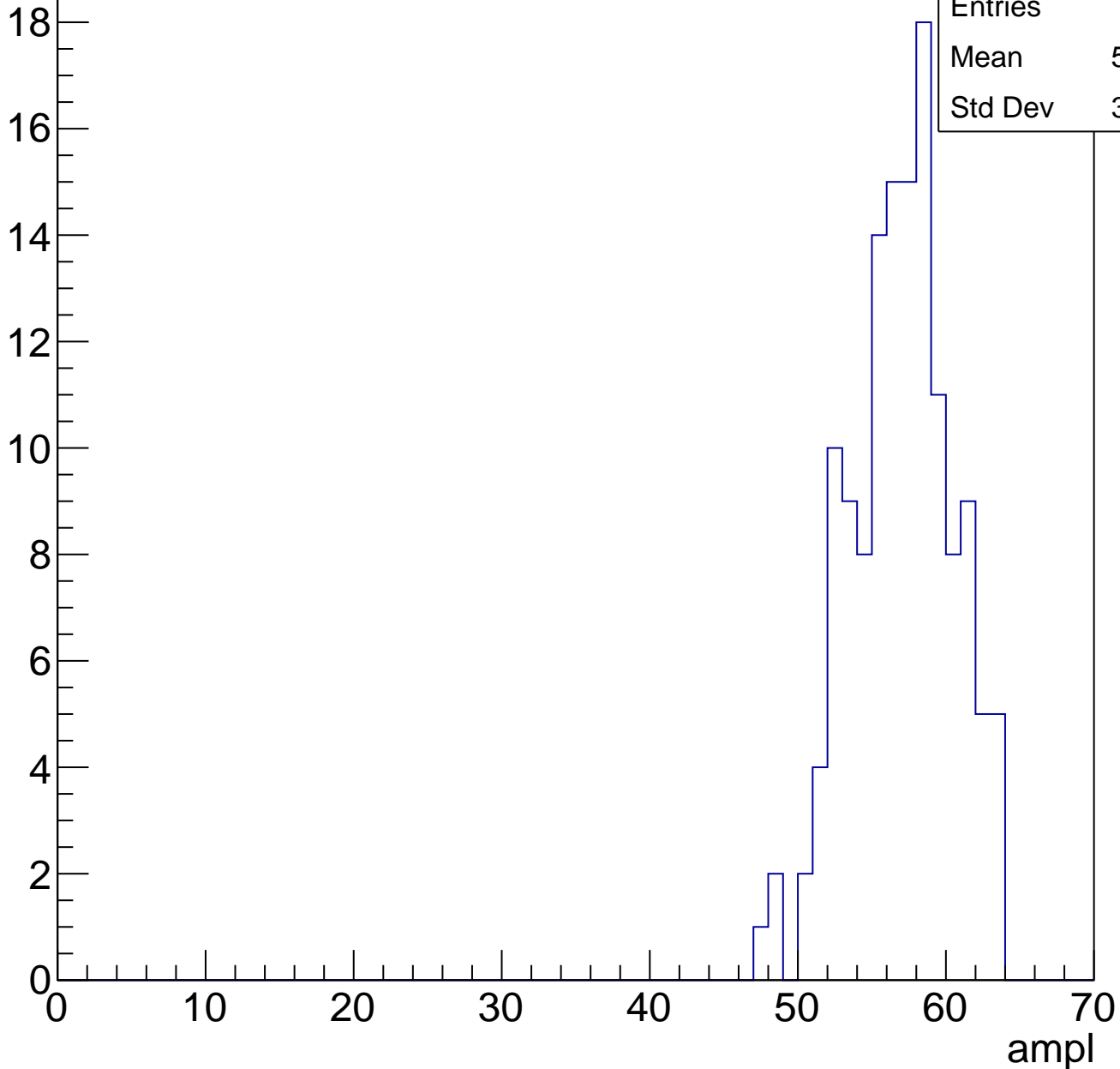
Entries	111
Mean	50.36
Std Dev	3.007



B0L101S, U24-ch65, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch65, adc5

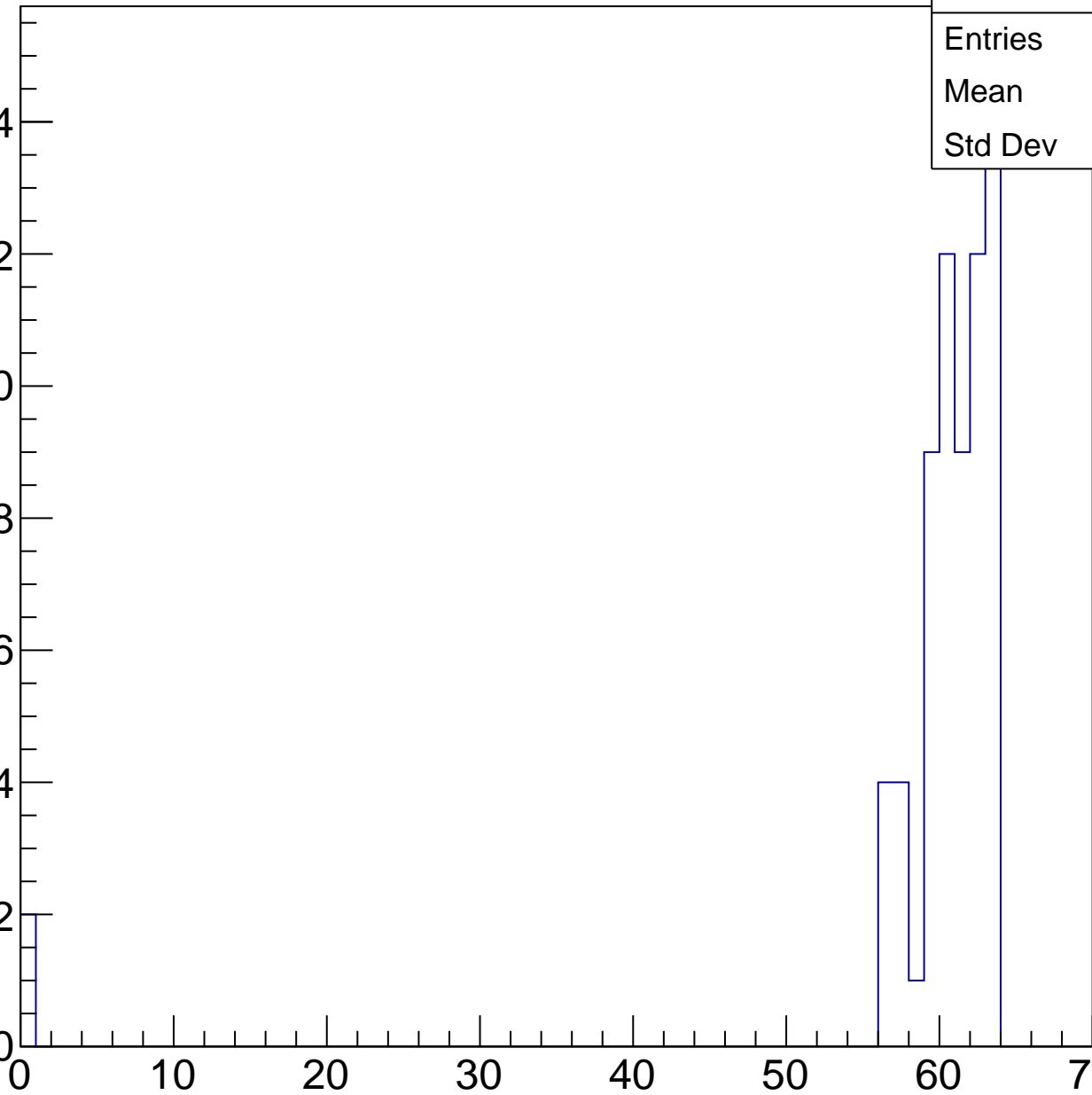
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	58.81
Std Dev	10.44

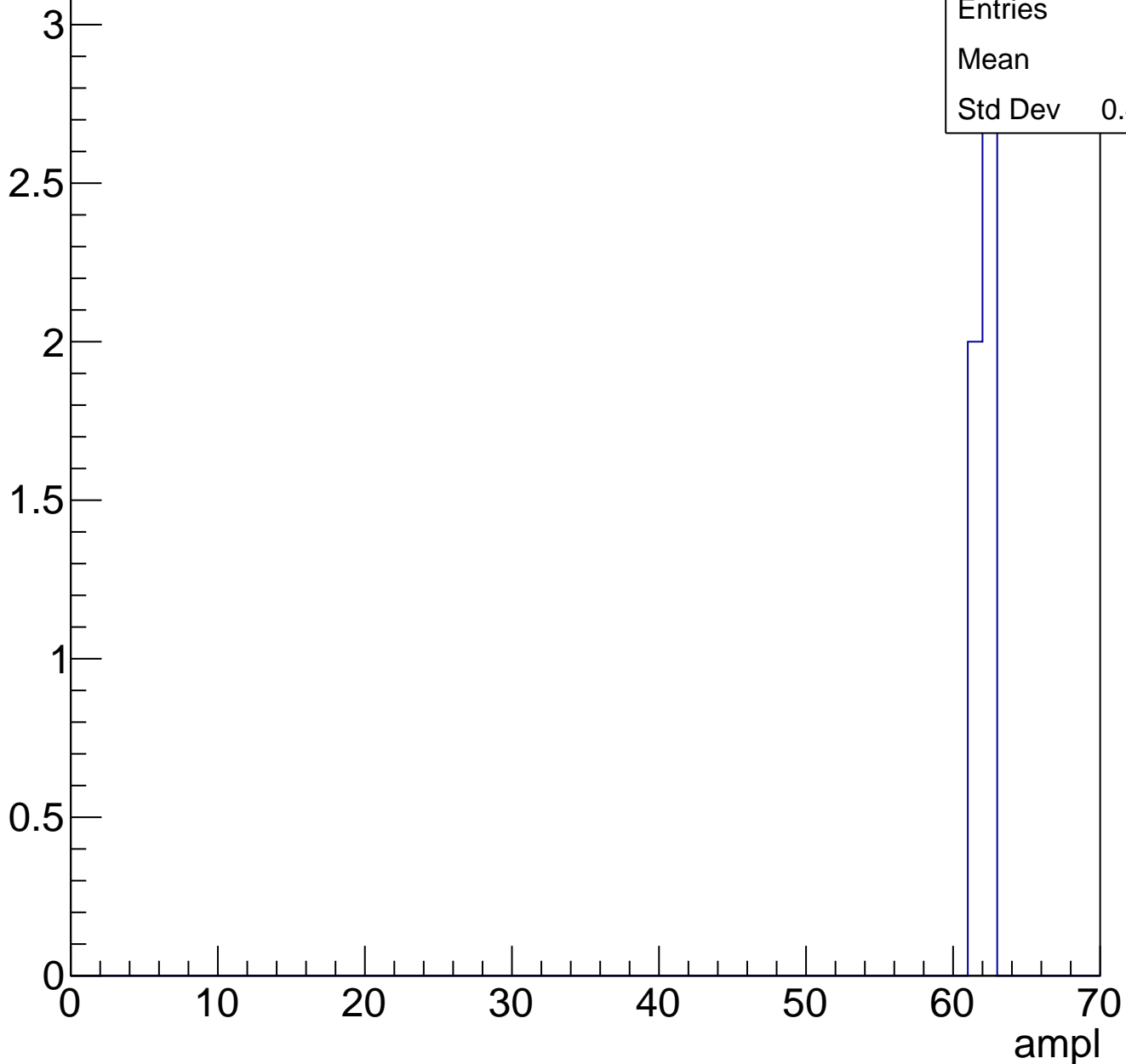
ampl



B0L101S, U24-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch66, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	28.69
Std Dev	3.832

Gaus mean : 28.8749

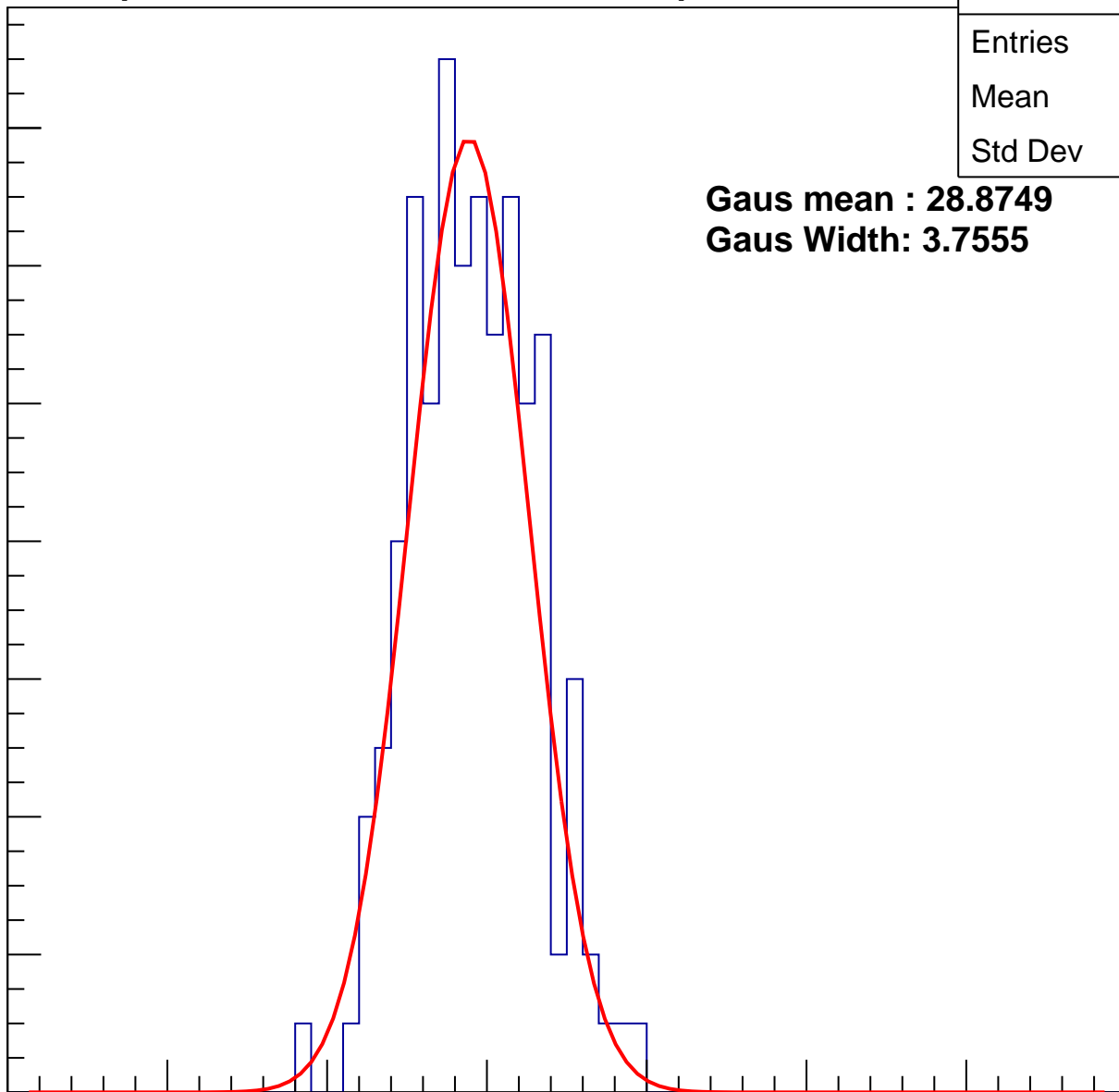
Gaus Width: 3.7555

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



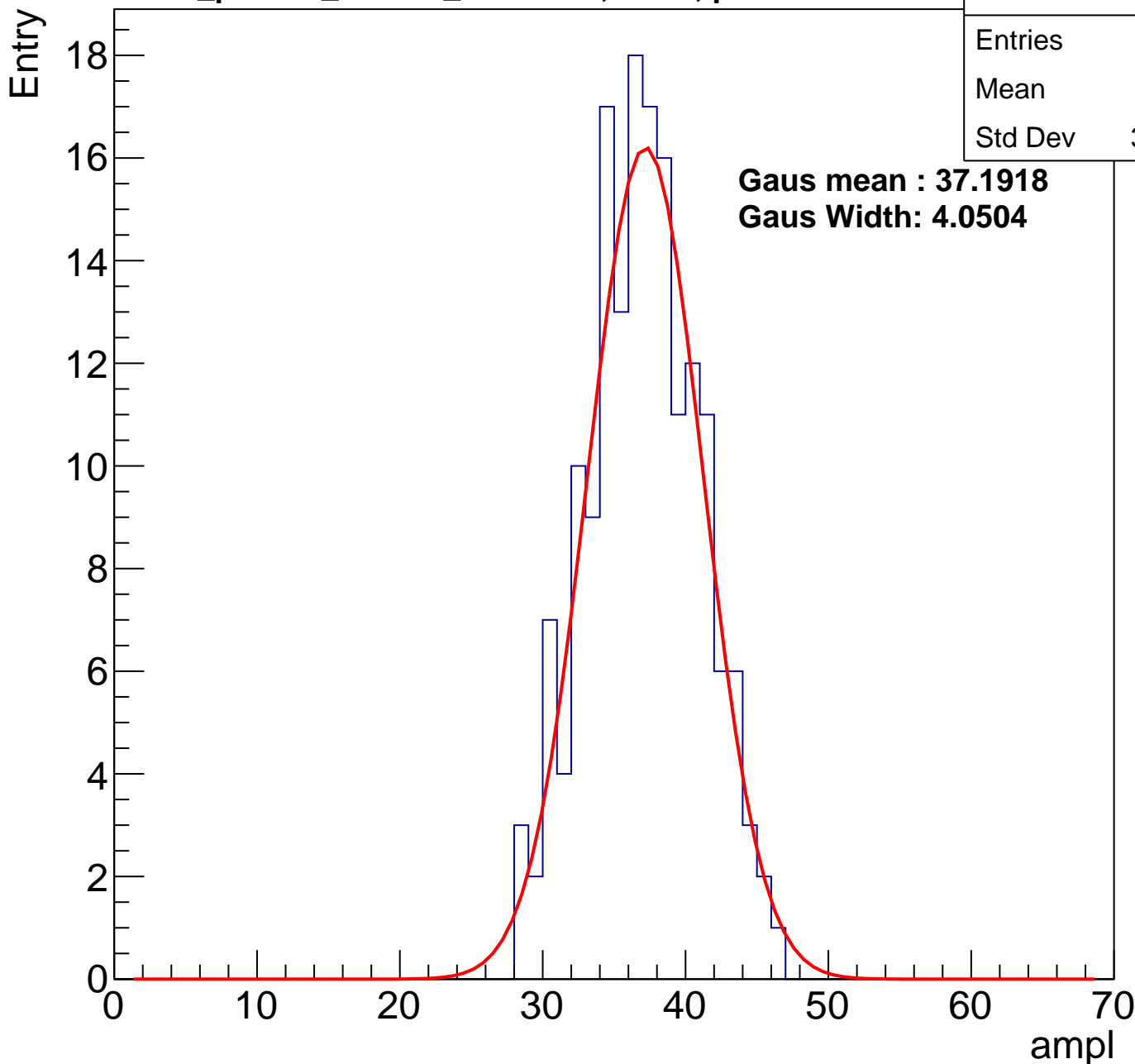
B0L101S, U24-ch66, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	36.6
Std Dev	3.869

Gaus mean : 37.1918

Gaus Width: 4.0504



B0L101S, U24-ch66, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

126

Mean

43.9

Std Dev

3.788

Gaus mean : 44.4723

Gaus Width: 3.8628

0

2

4

6

8

10

12

14

Entry

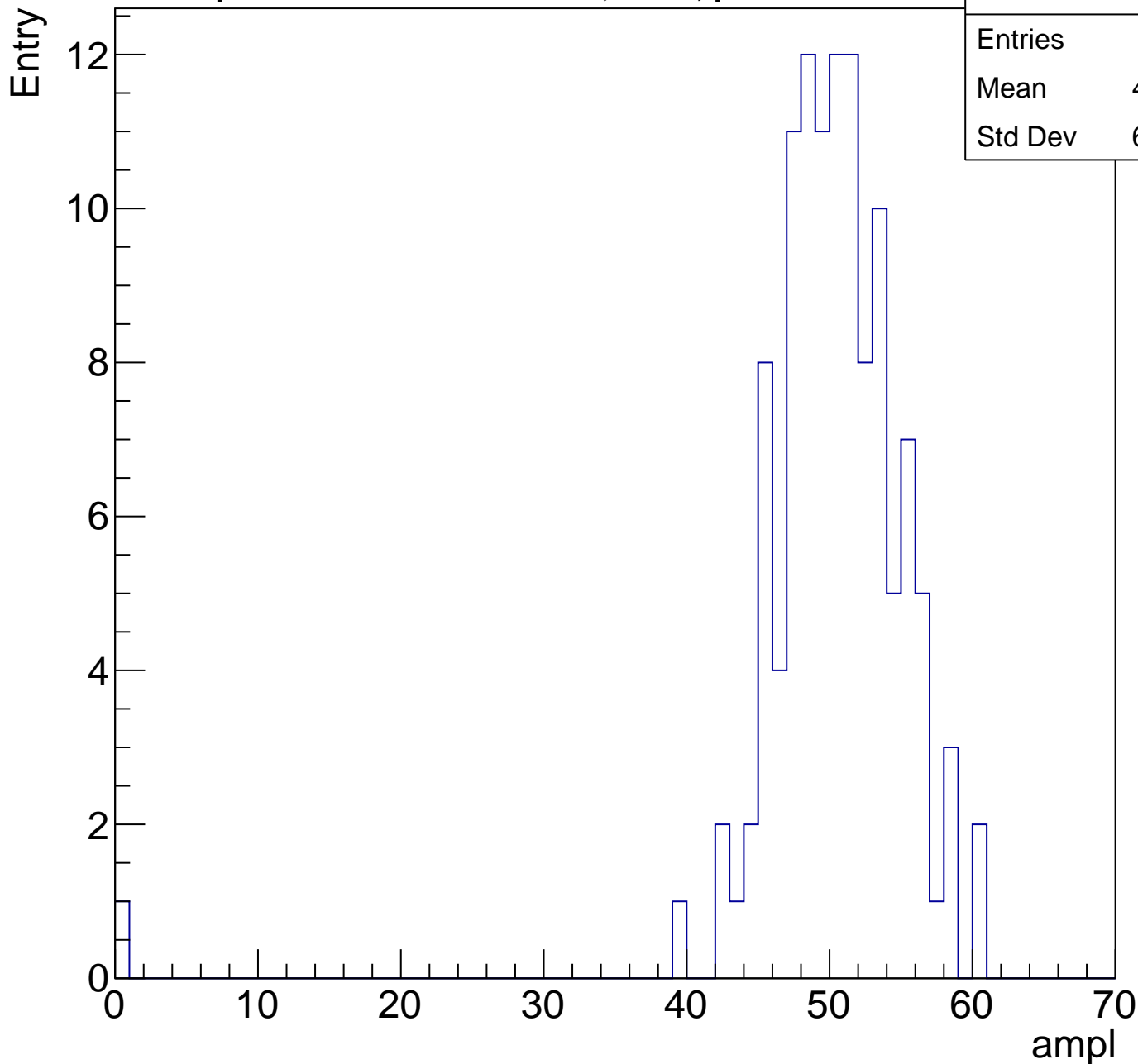
calib_packv5_042523_0143.root, FC#1, port C1

B0L101S, U24-ch66, adc2

B0L101S, U24-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	49.78
Std Dev	6.034



B0L101S, U24-ch66, adc4

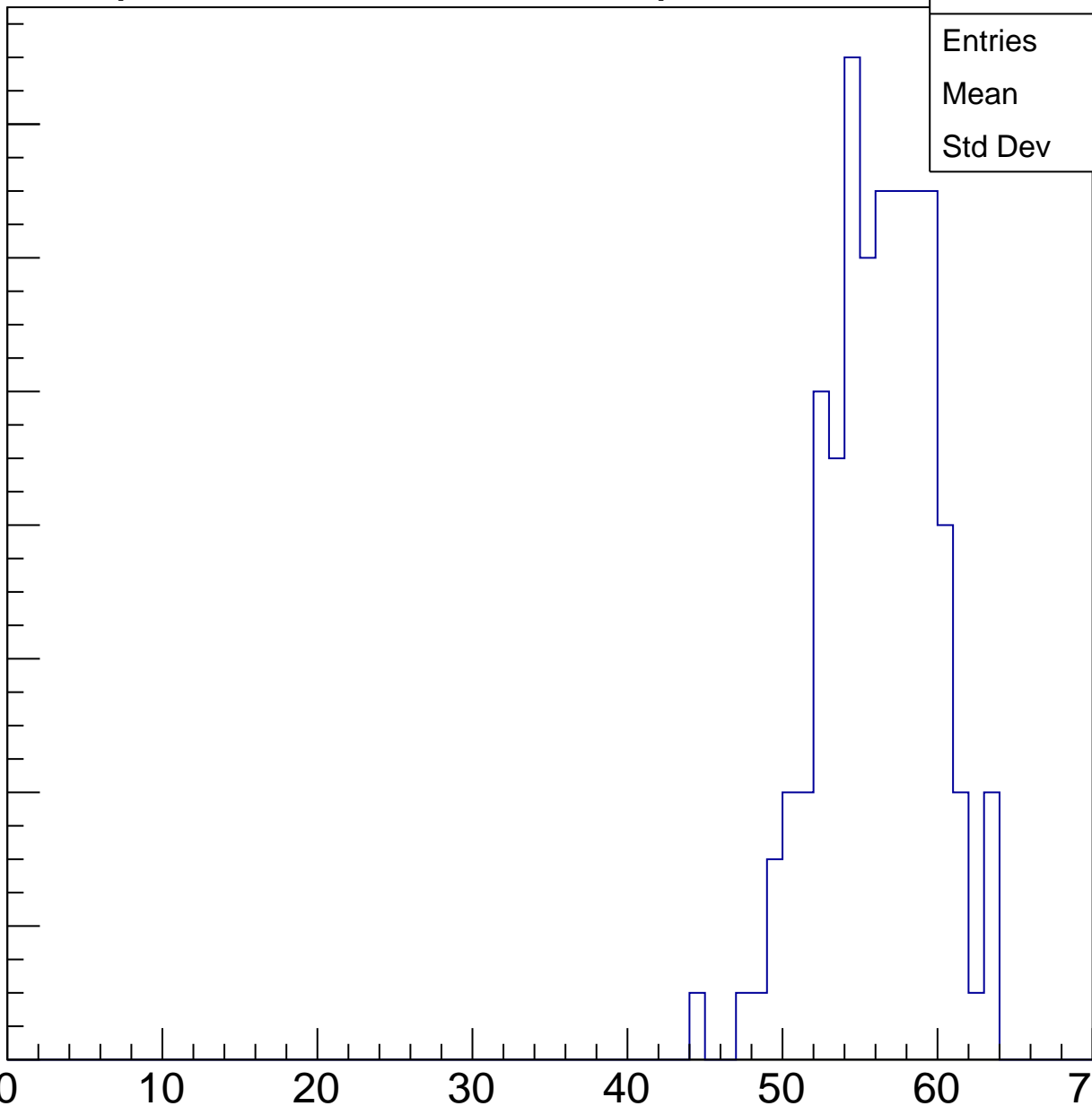
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	129
Mean	55.7
Std Dev	3.547

ampl



B0L101S, U24-ch66, adc5

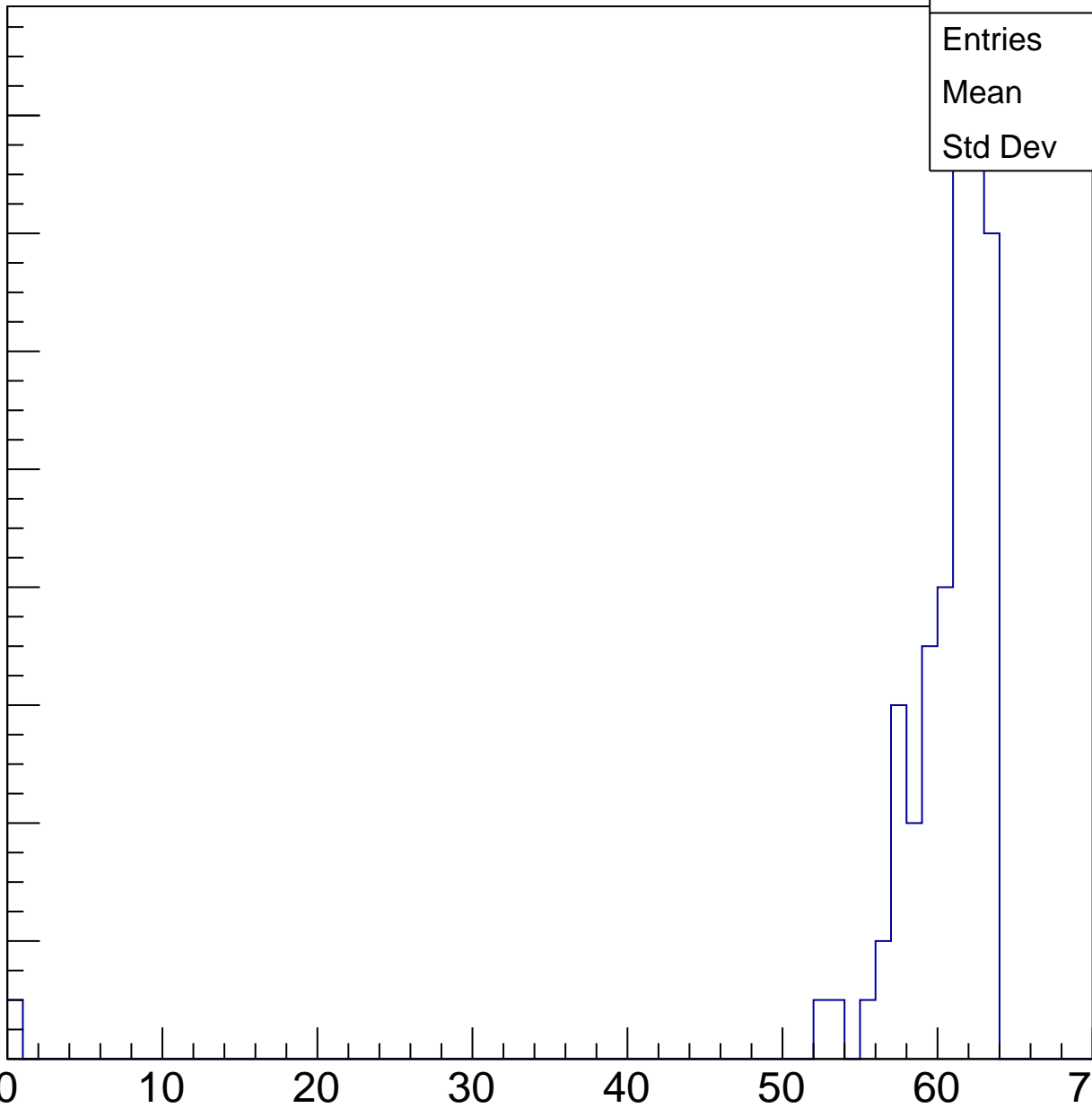
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.63
Std Dev	7.202

ampl



B0L101S, U24-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

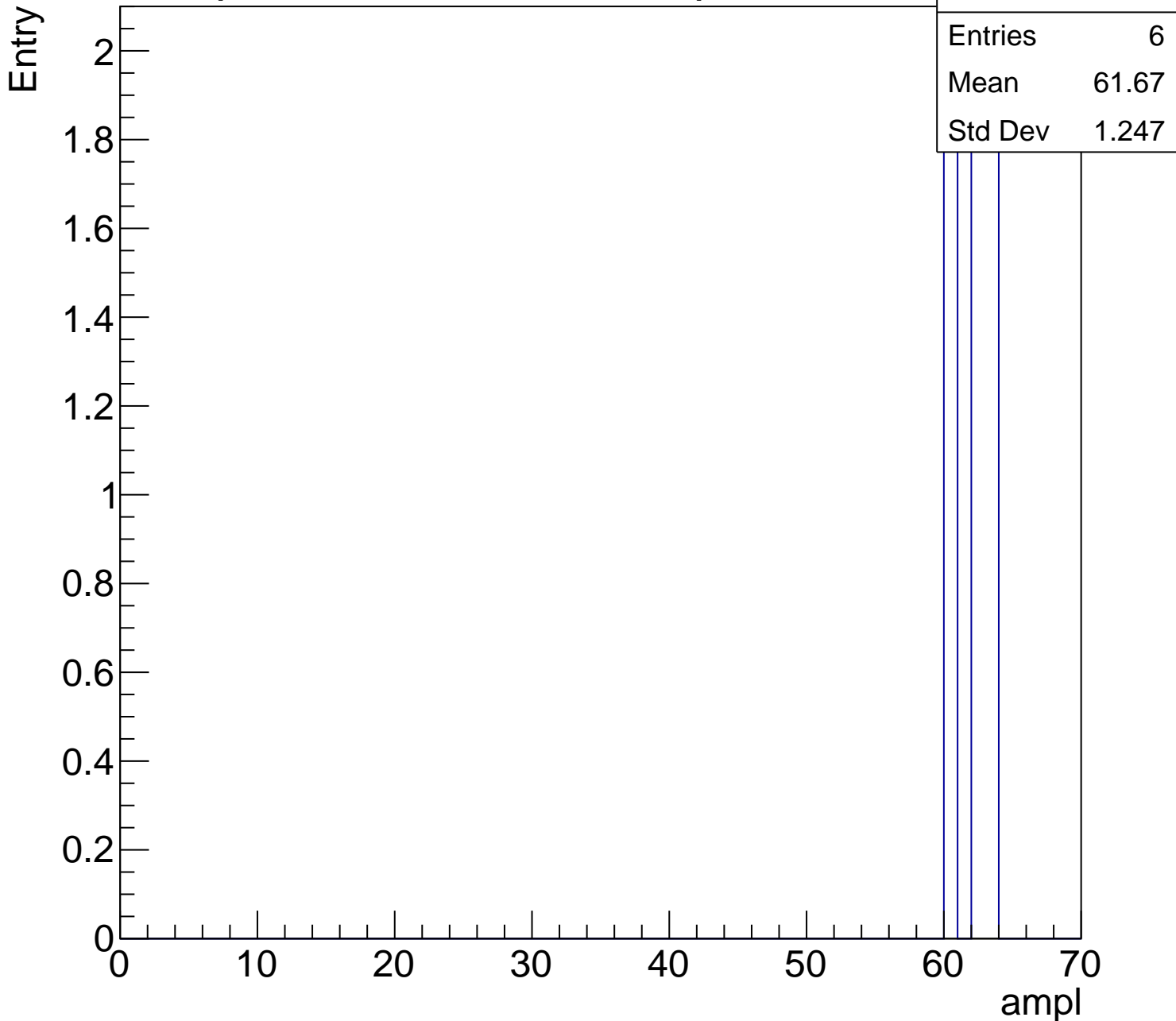
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.247

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

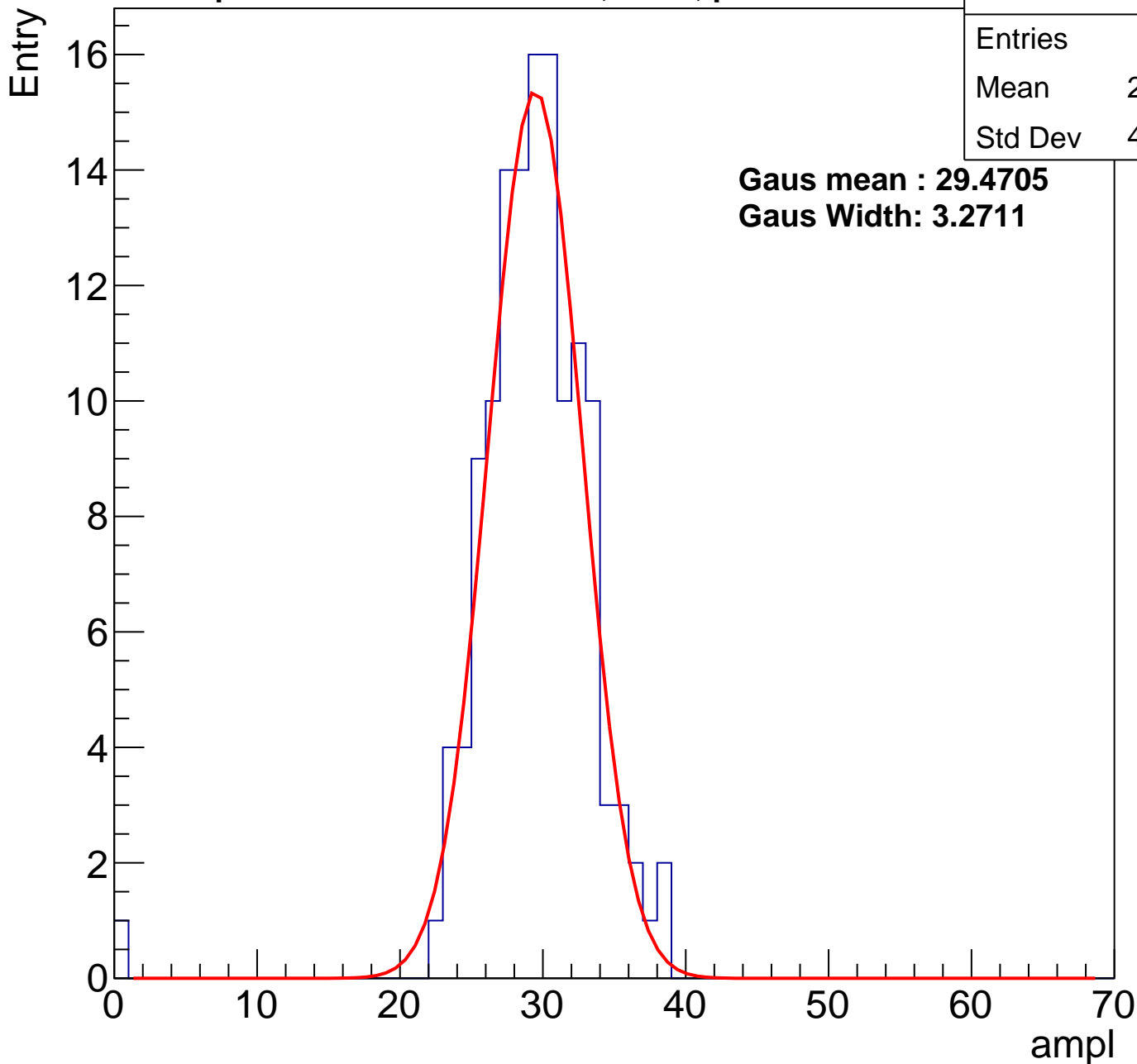
B0L101S, U24-ch67, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	28.95
Std Dev	4.153

Gaus mean : 29.4705

Gaus Width: 3.2711



B0L101S, U24-ch67, adc1

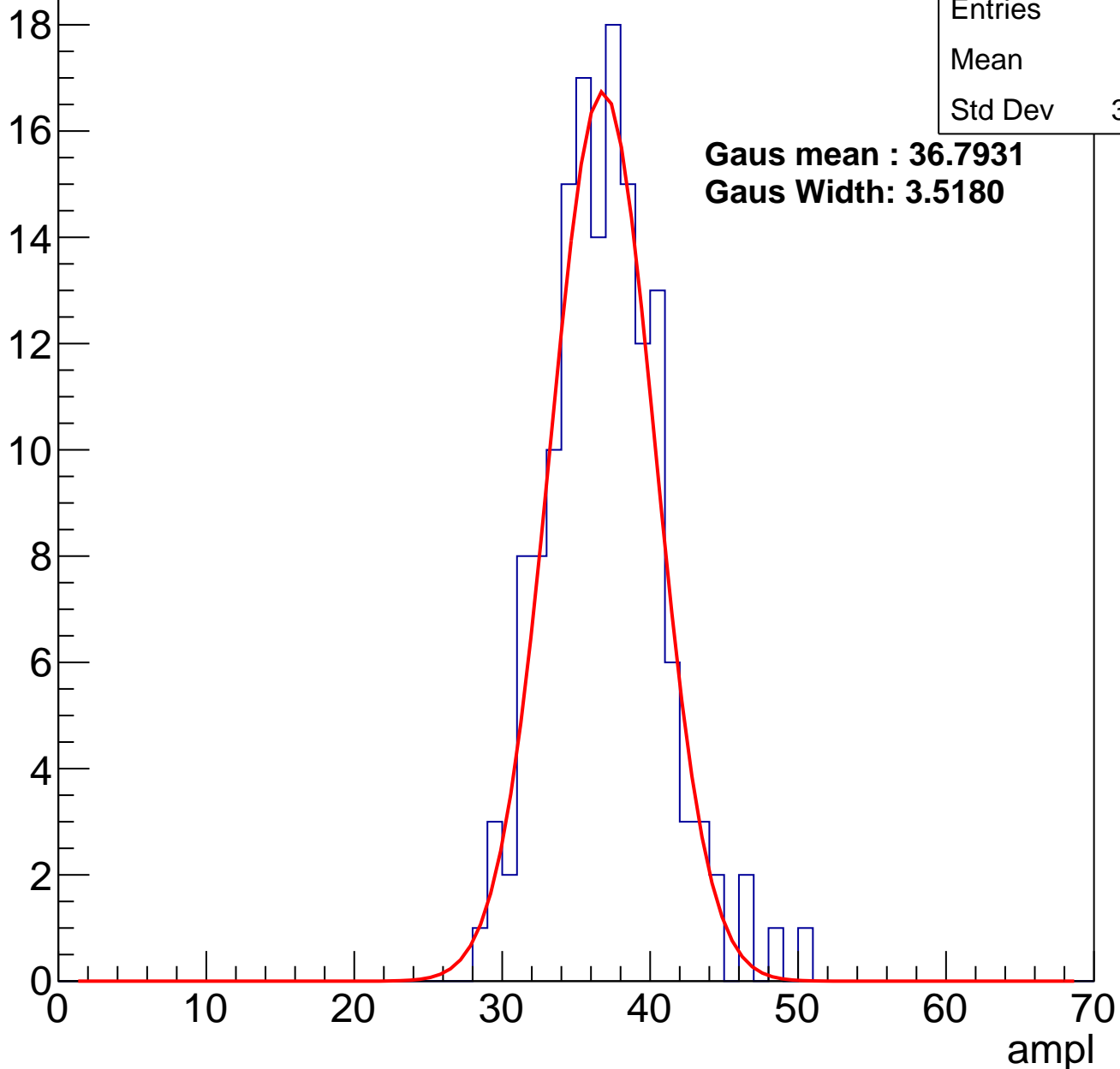
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	36.5
Std Dev	3.785

Gaus mean : 36.7931

Gaus Width: 3.5180

Entry



B0L101S, U24-ch67, adc2

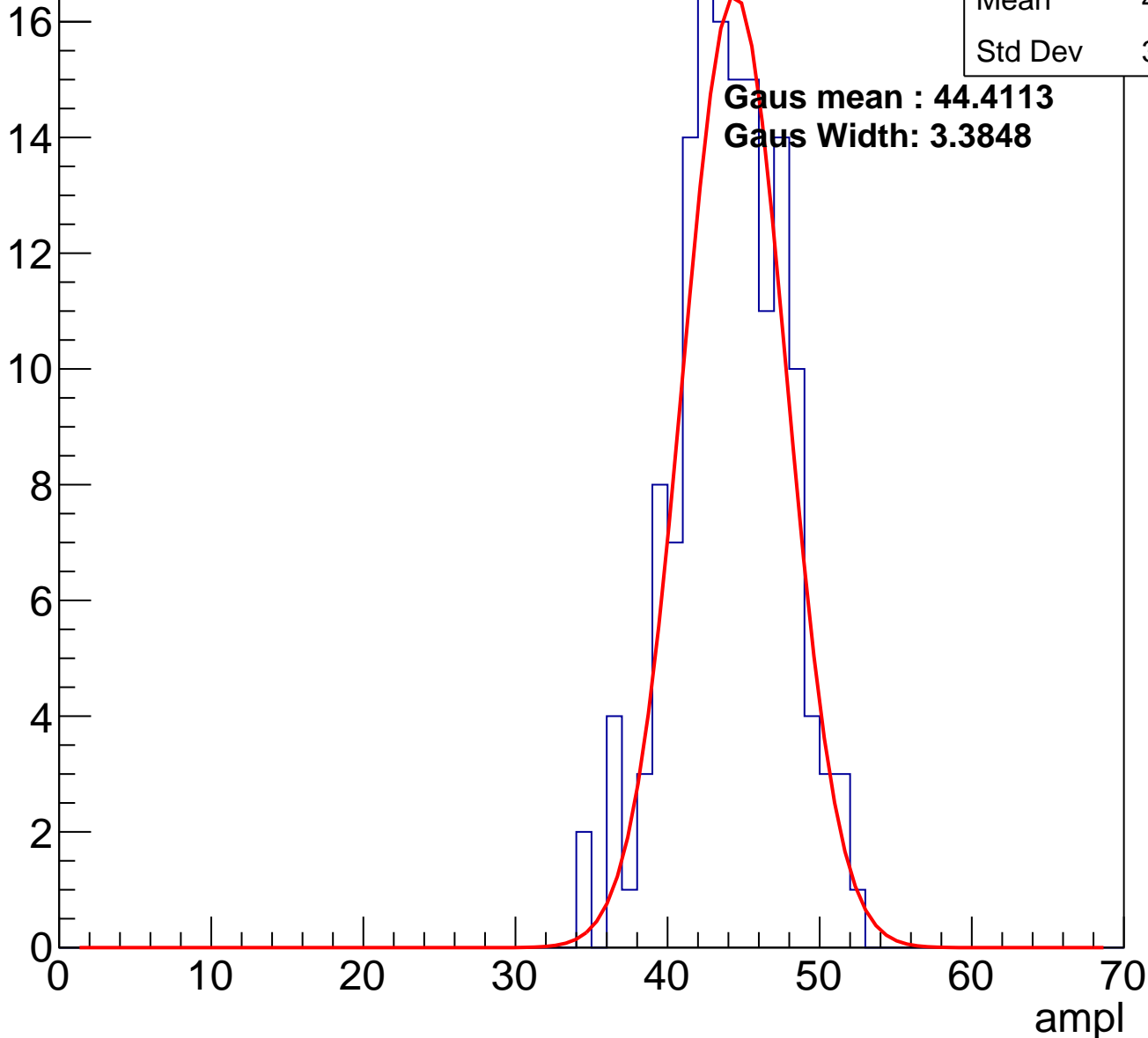
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	148
Mean	43.66
Std Dev	3.565

Gaus mean : 44.4113

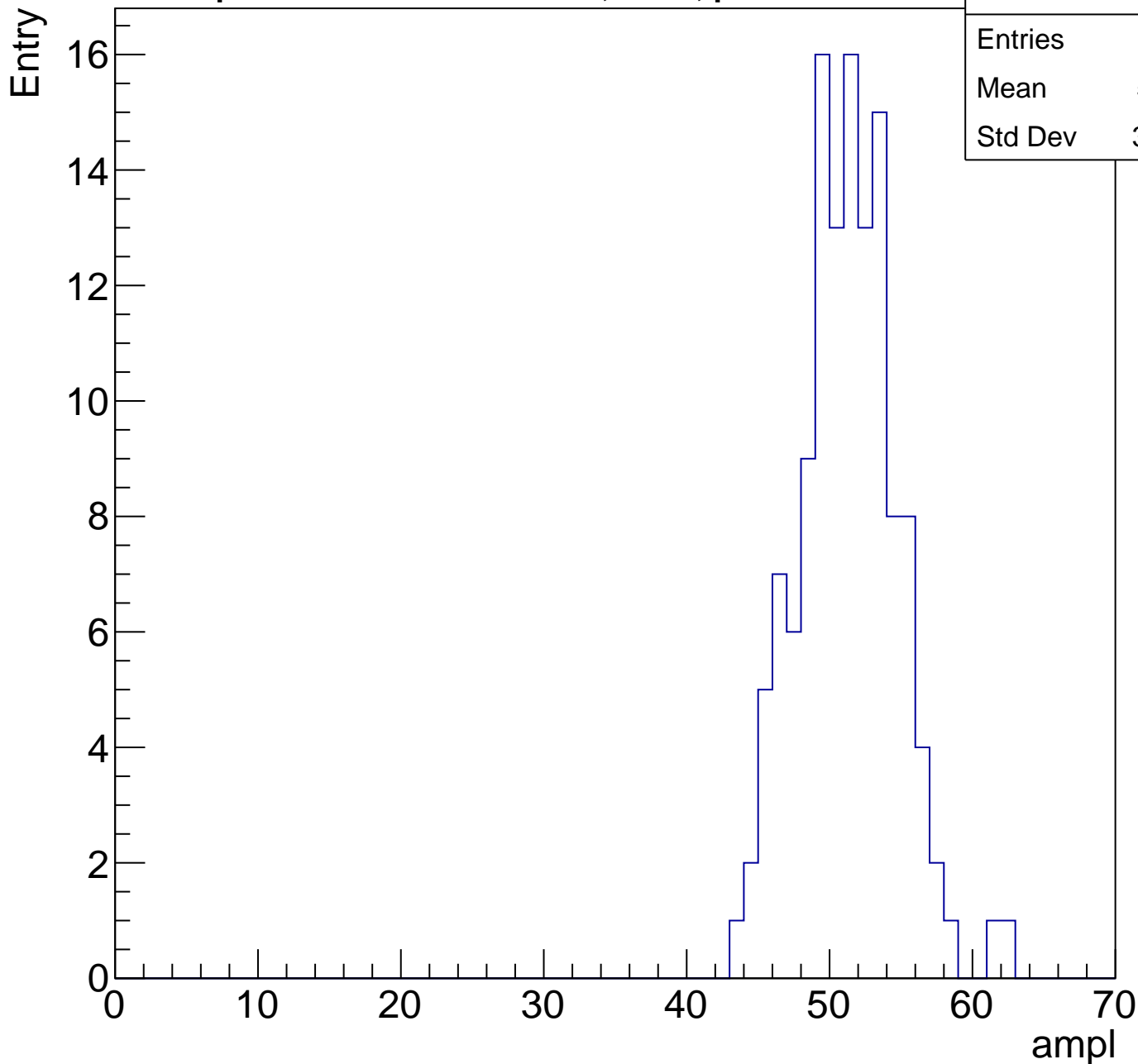
Gaus Width: 3.3848



B0L101S, U24-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	50.81
Std Dev	3.402



B0L101S, U24-ch67, adc4

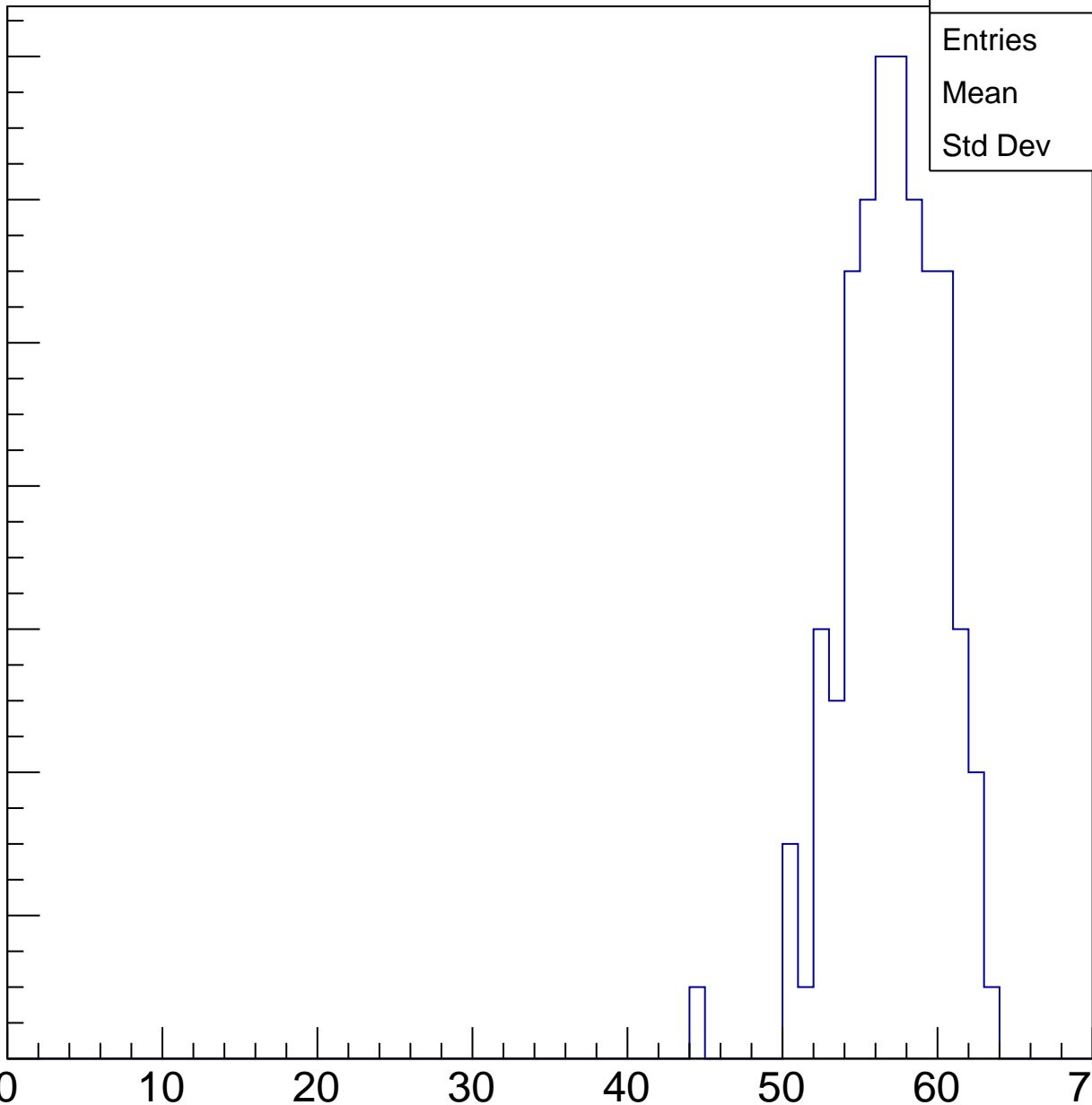
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	112
Mean	56.61
Std Dev	3.152

ampl



B0L101S, U24-ch67, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	72
Mean	58.69
Std Dev	10.13

ampl

0

10

20

30

40

50

60

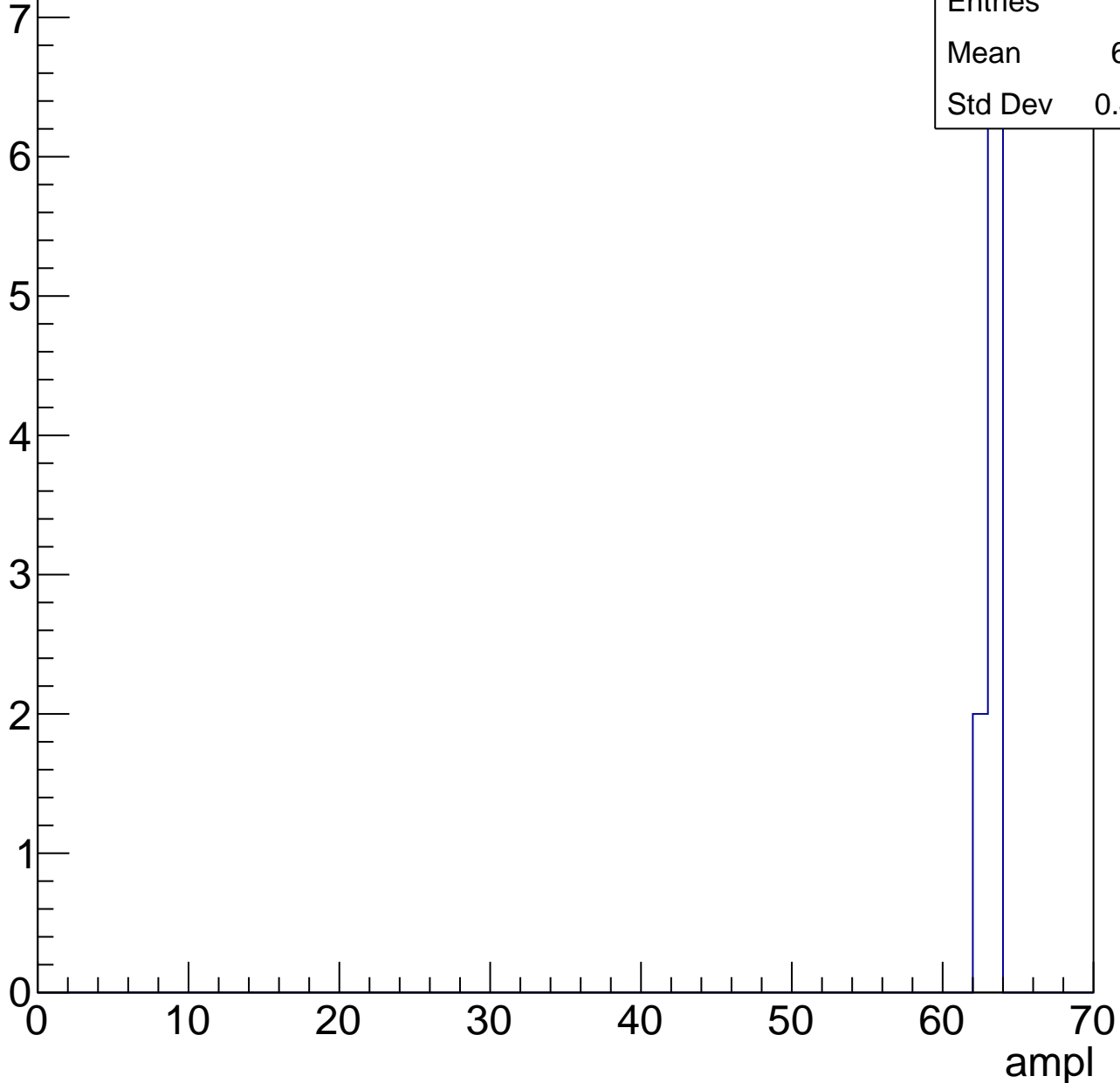
70

B0L101S, U24-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	62.78
Std Dev	0.4157



B0L101S, U24-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch68, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	28.4
Std Dev	5.004

Gaus mean : 29.1648

Gaus Width: 3.4734

Entry

12

10

8

6

4

2

0

0

10

20

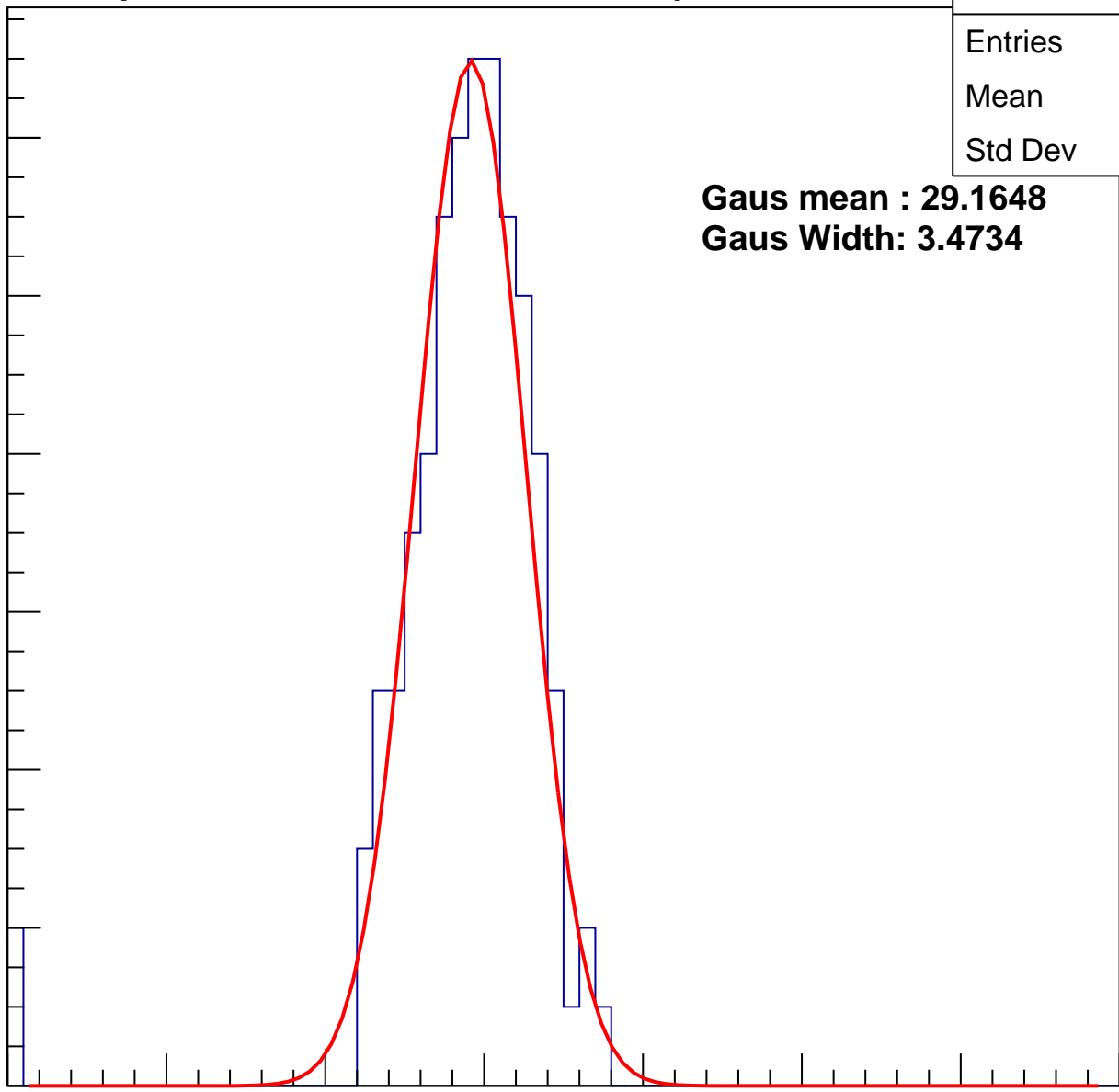
30

50

60

70

ampl



B0L101S, U24-ch68, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	35.17
Std Dev	3.879

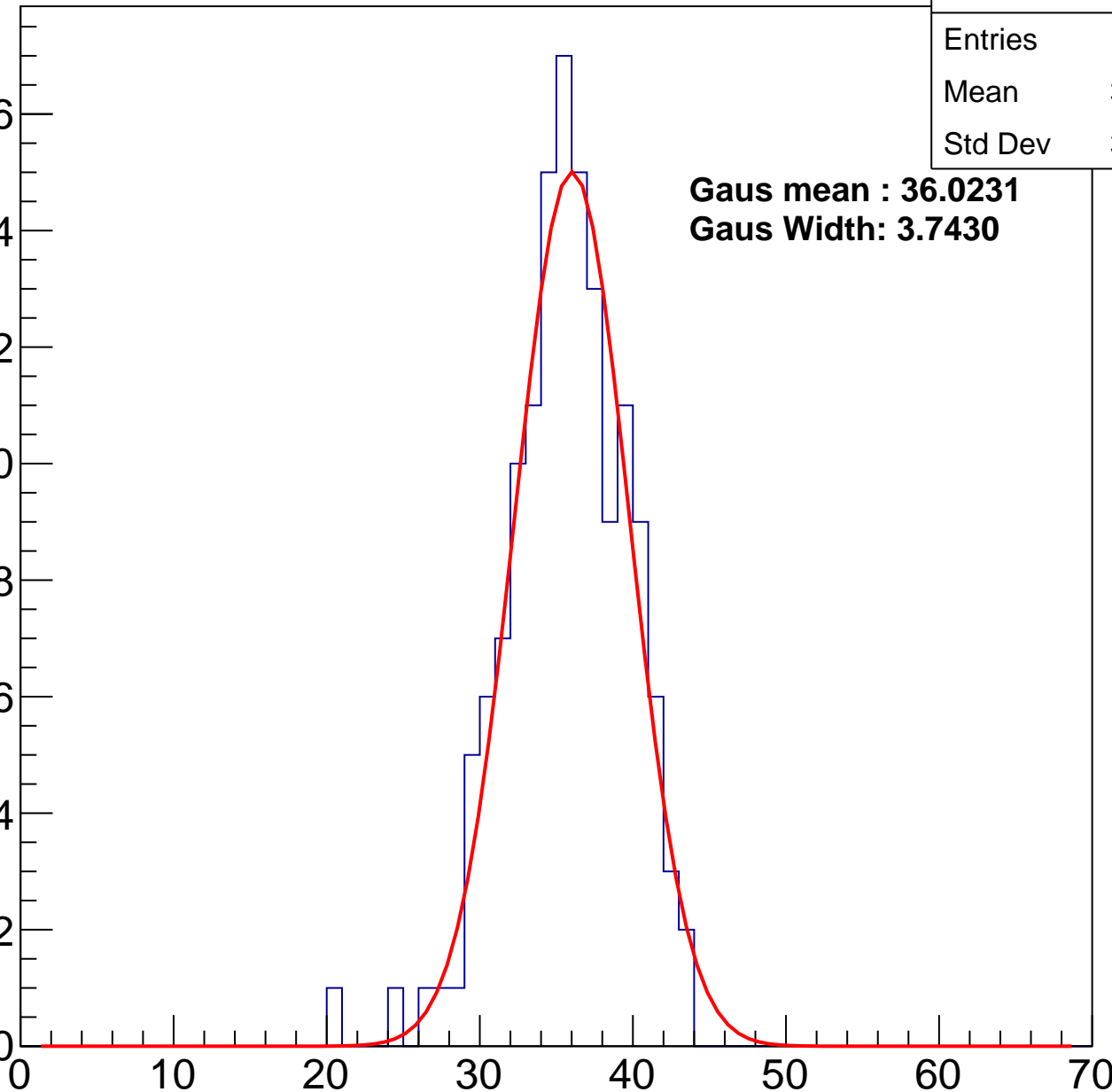
Gaus mean : 36.0231

Gaus Width: 3.7430

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch68, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	42.35
Std Dev	4.974

Gaus mean : 43.0468

Gaus Width: 3.6925

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

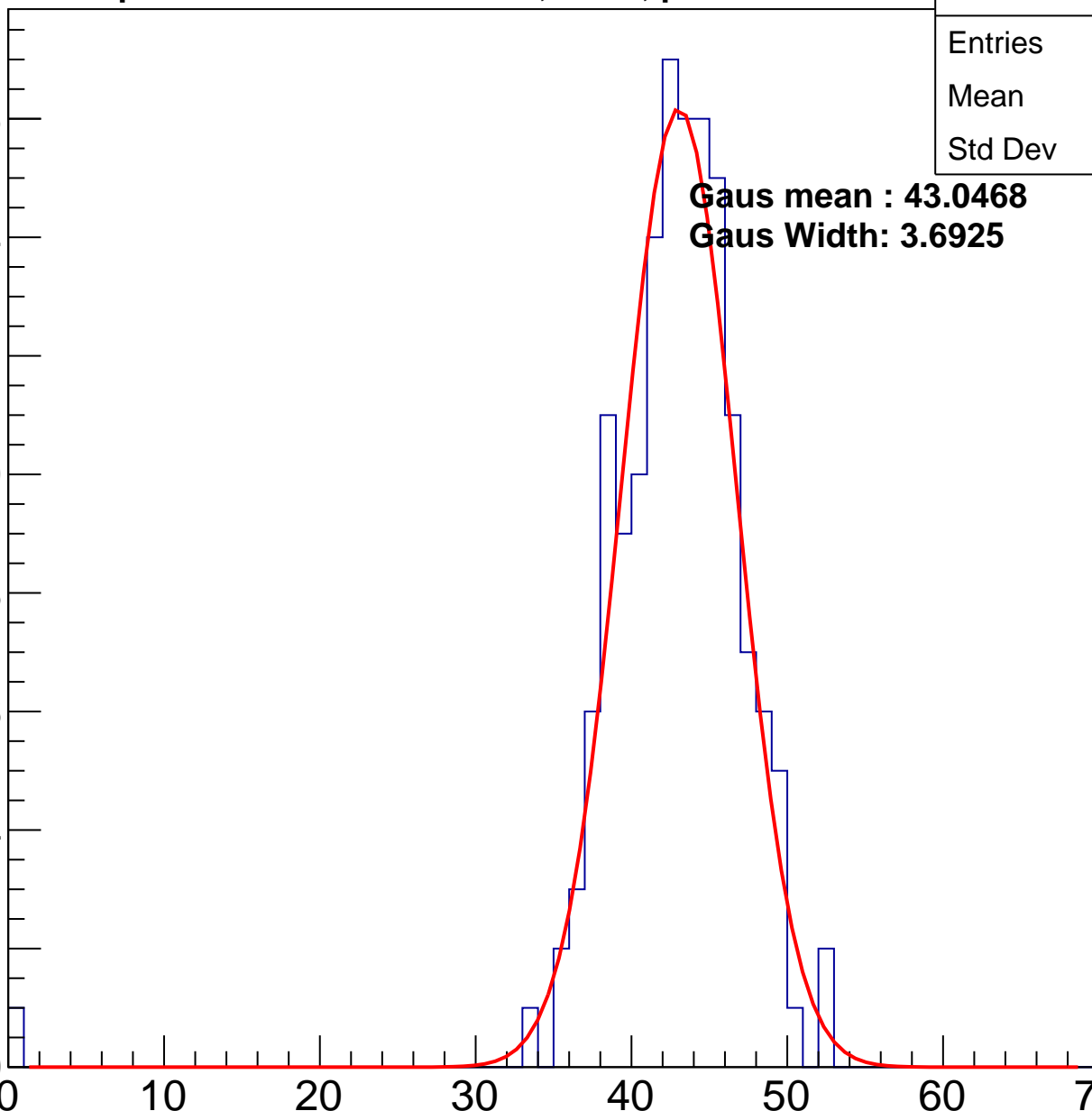
40

50

60

70

ampl



B0L101S, U24-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

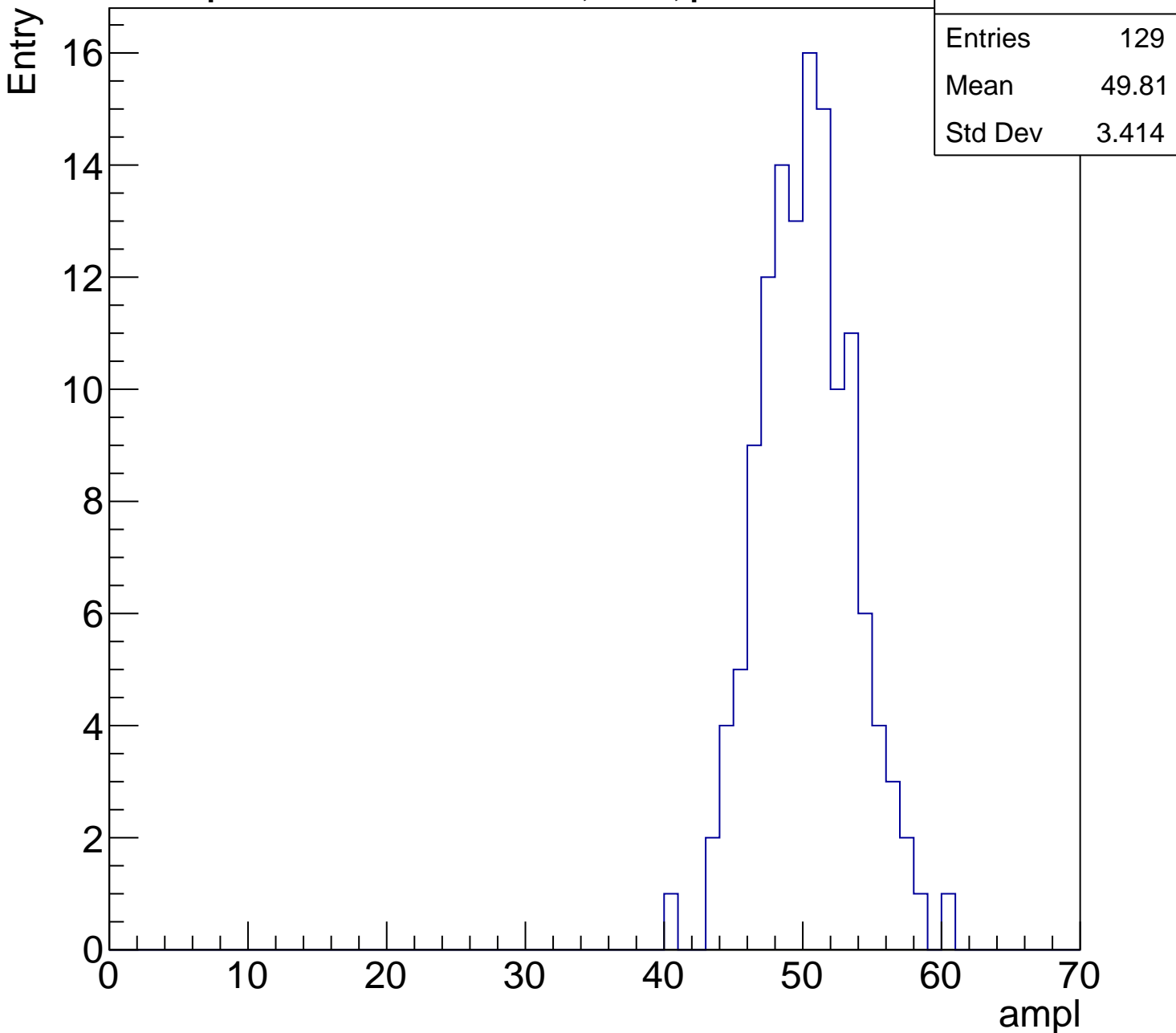
Entries	129
Mean	49.81
Std Dev	3.414

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch68, adc4

calib_packv5_042523_0143.root, FC#1, port C1

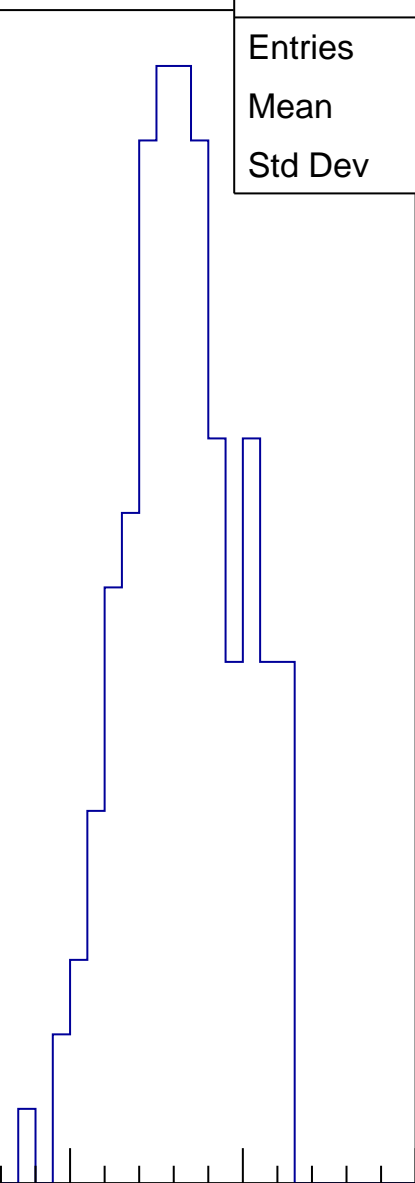
Entries	127
Mean	56.03
Std Dev	3.315

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch68, adc5

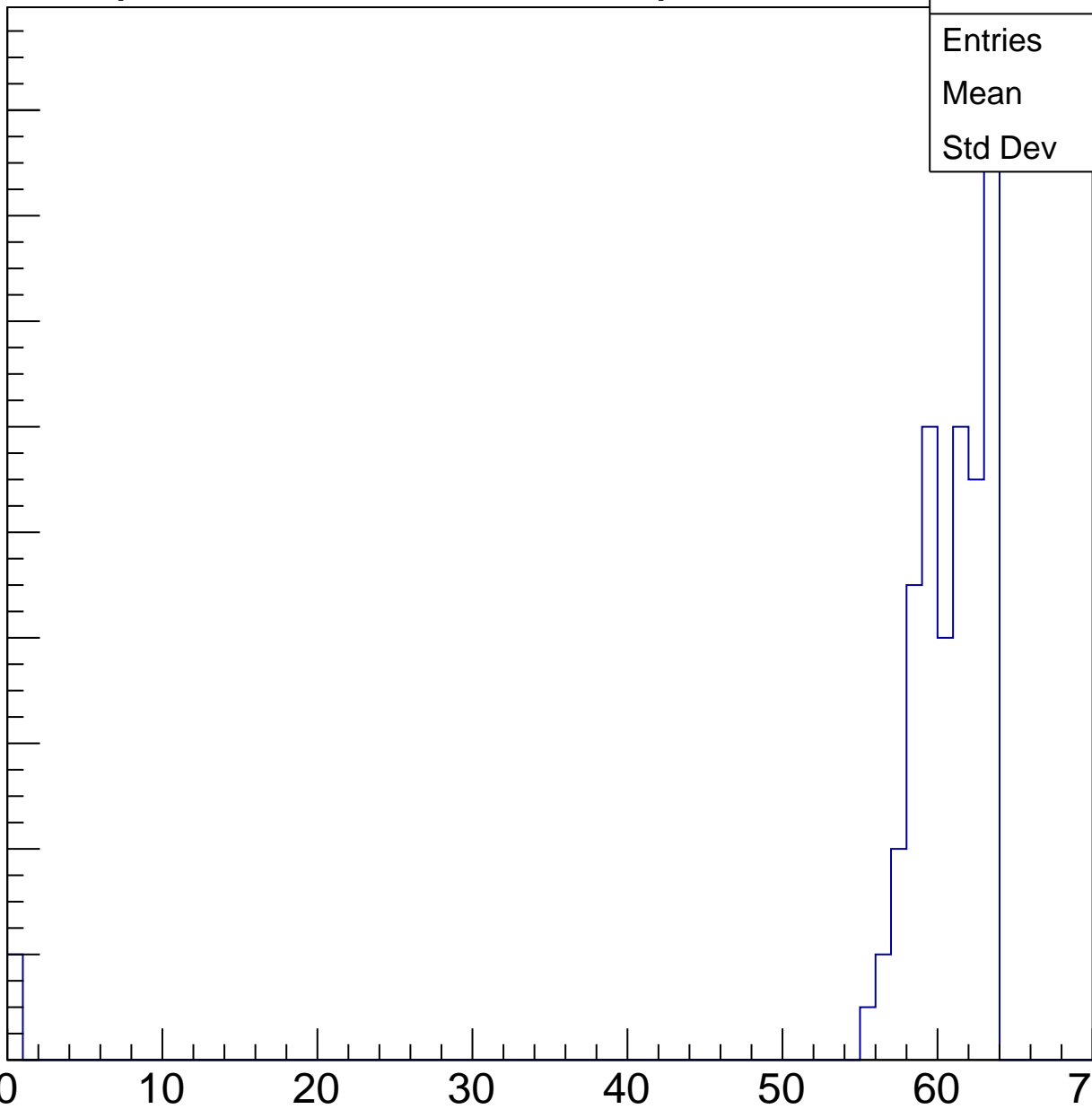
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	80
Mean	58.95
Std Dev	9.669

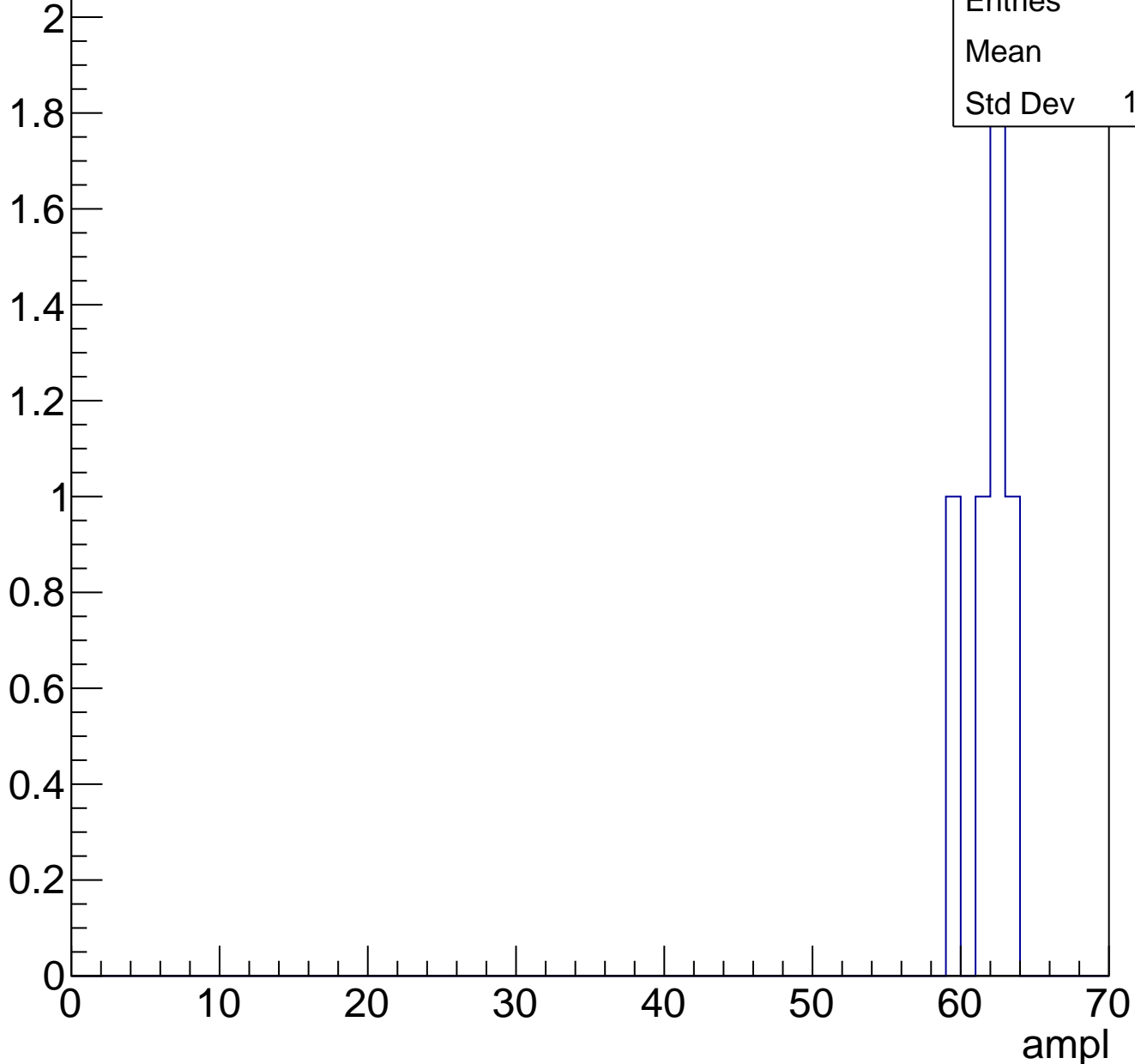
ampl



B0L101S, U24-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



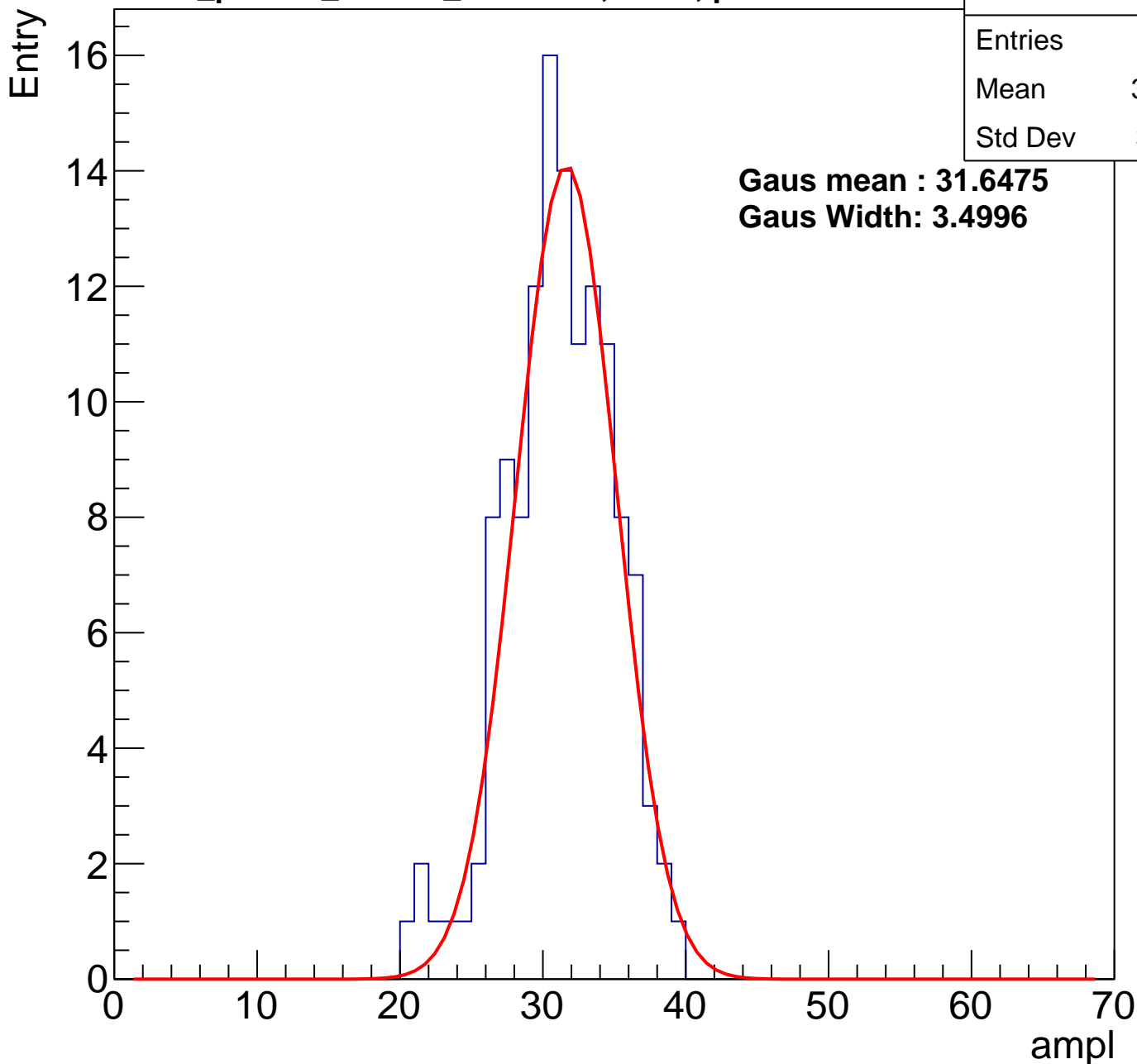
B0L101S, U24-ch69, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	30.75
Std Dev	3.711

Gaus mean : 31.6475

Gaus Width: 3.4996



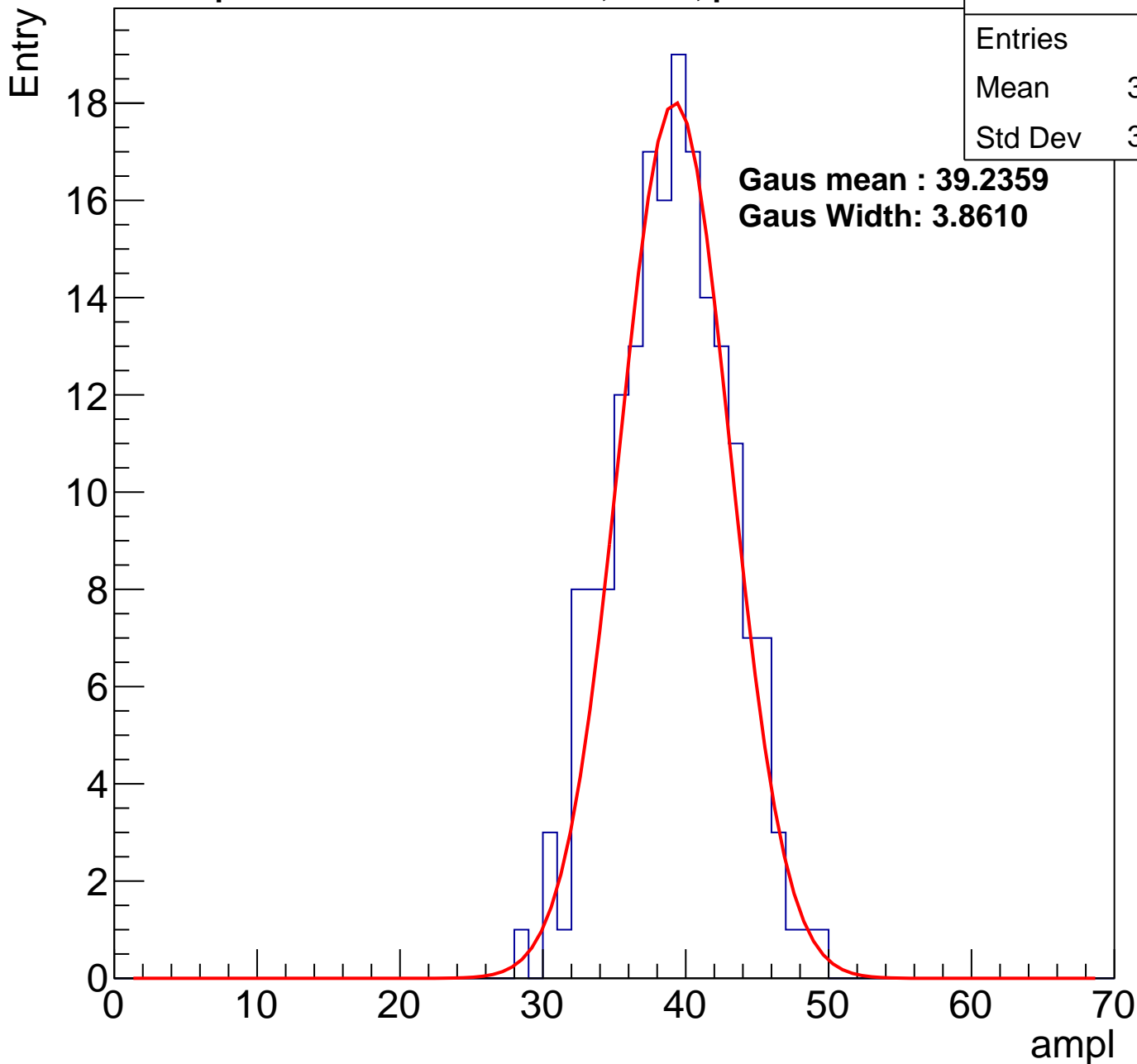
B0L101S, U24-ch69, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	181
Mean	38.59
Std Dev	3.972

Gaus mean : 39.2359

Gaus Width: 3.8610



B0L101S, U24-ch69, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	45.45
Std Dev	3.204

Gaus mean : 45.7013

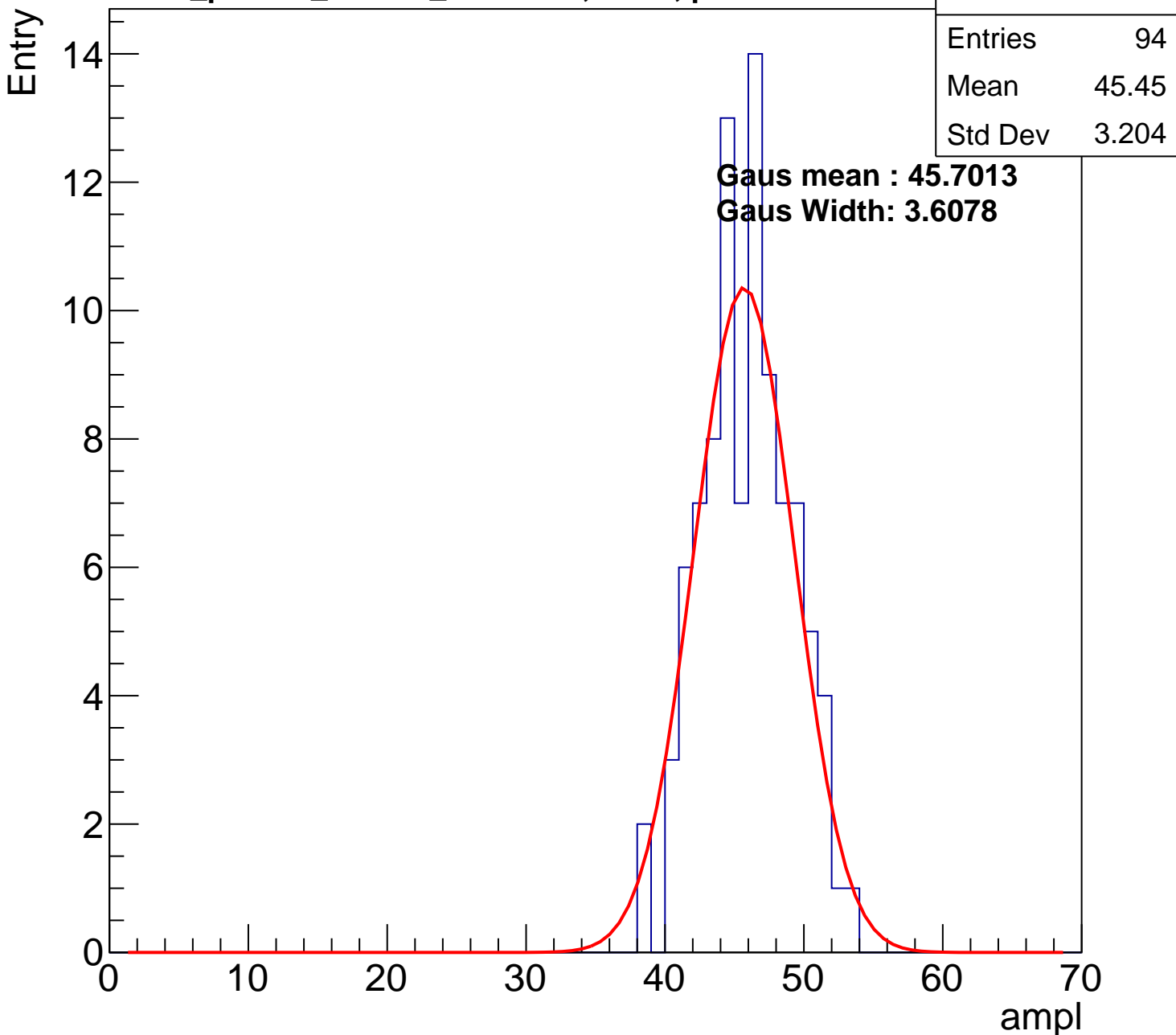
Gaus Width: 3.6078

Entry

14
12
10
8
6
4
2
0

ampl

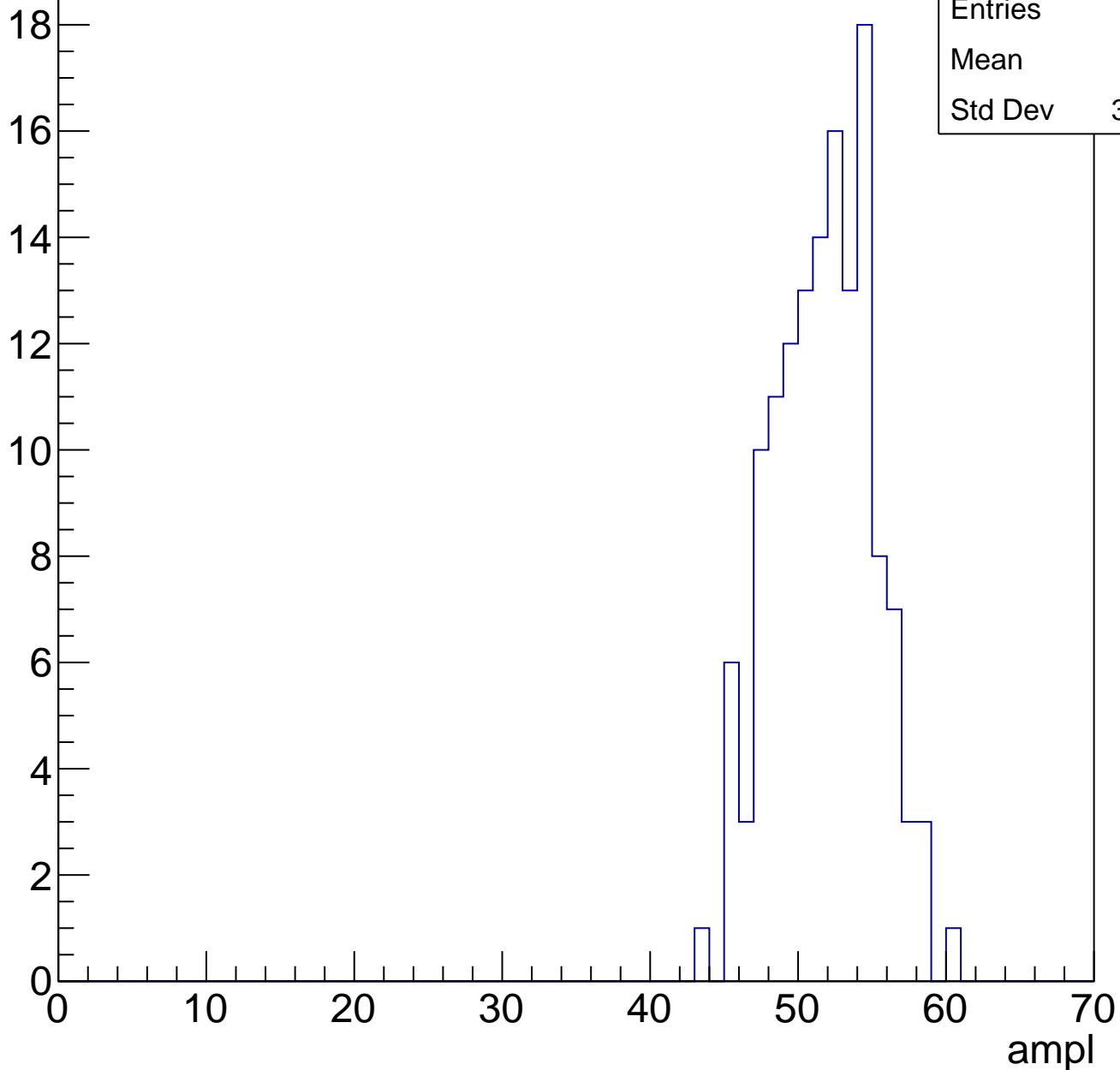
0 10 20 30 40 50 60 70



B0L101S, U24-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch69, adc4

calib_packv5_042523_0143.root, FC#1, port C1

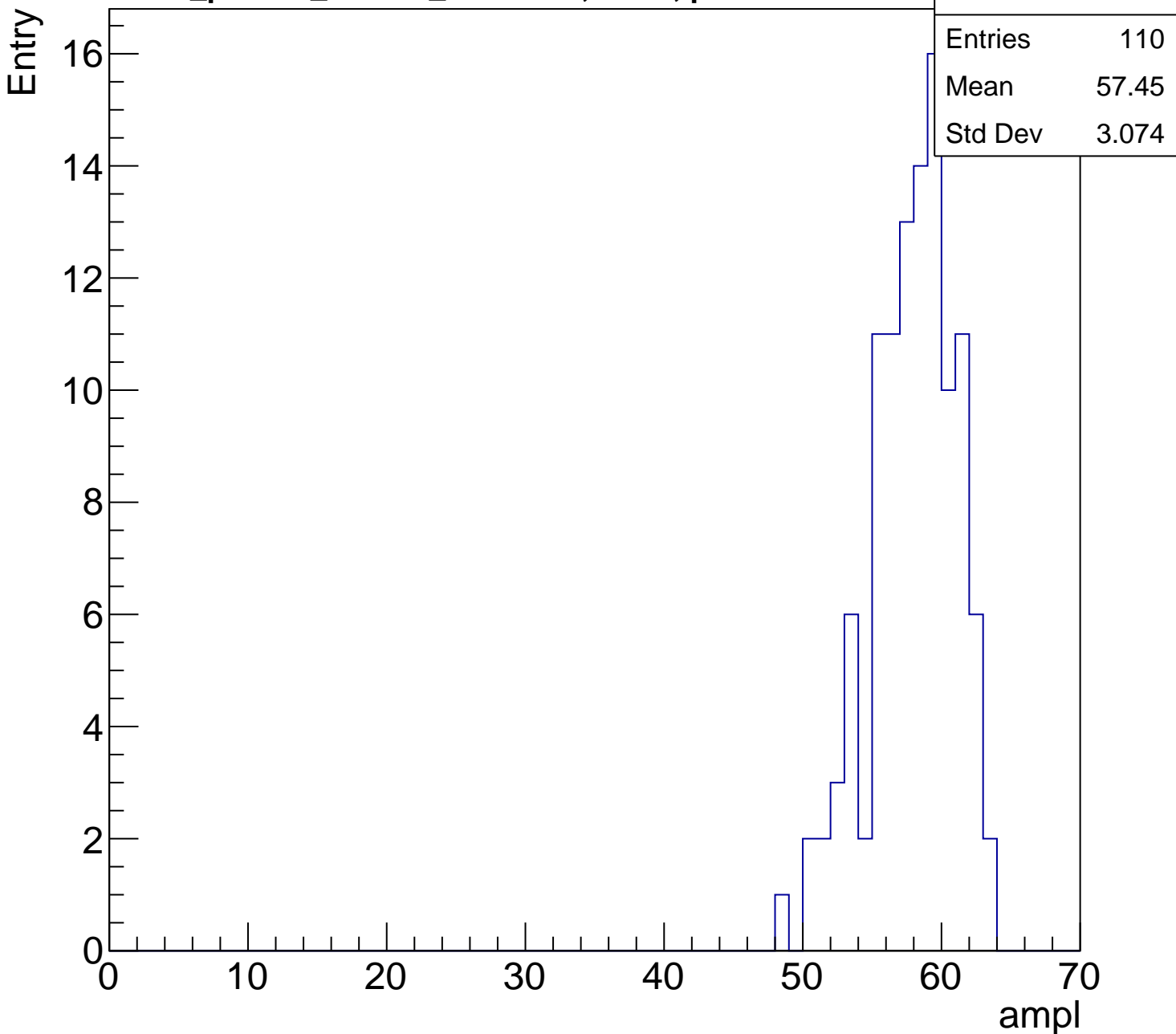
Entries	110
Mean	57.45
Std Dev	3.074

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch69, adc5

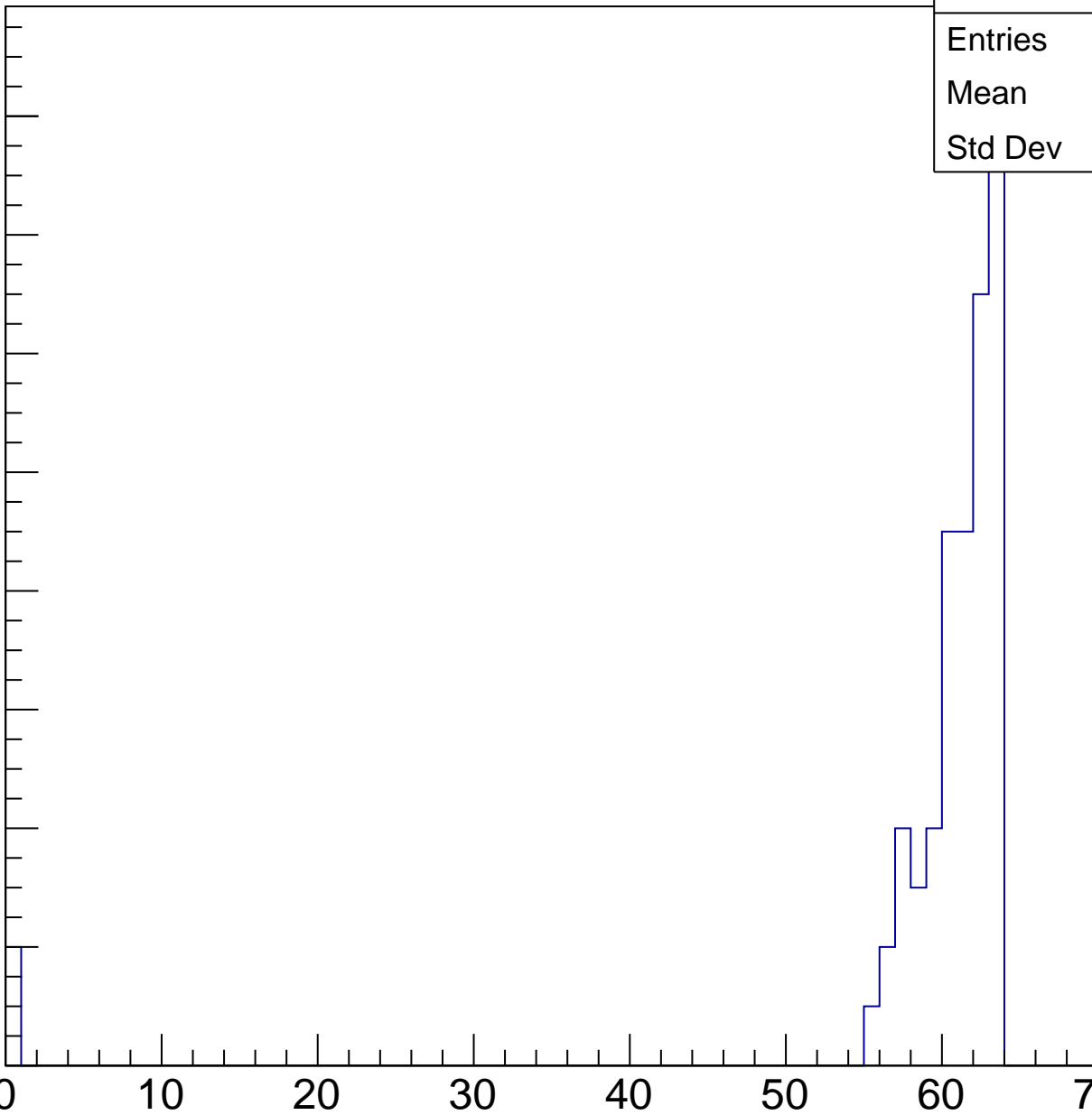
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	64
Mean	58.92
Std Dev	10.79

ampl



B0L101S, U24-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch70, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	27.73
Std Dev	5.588

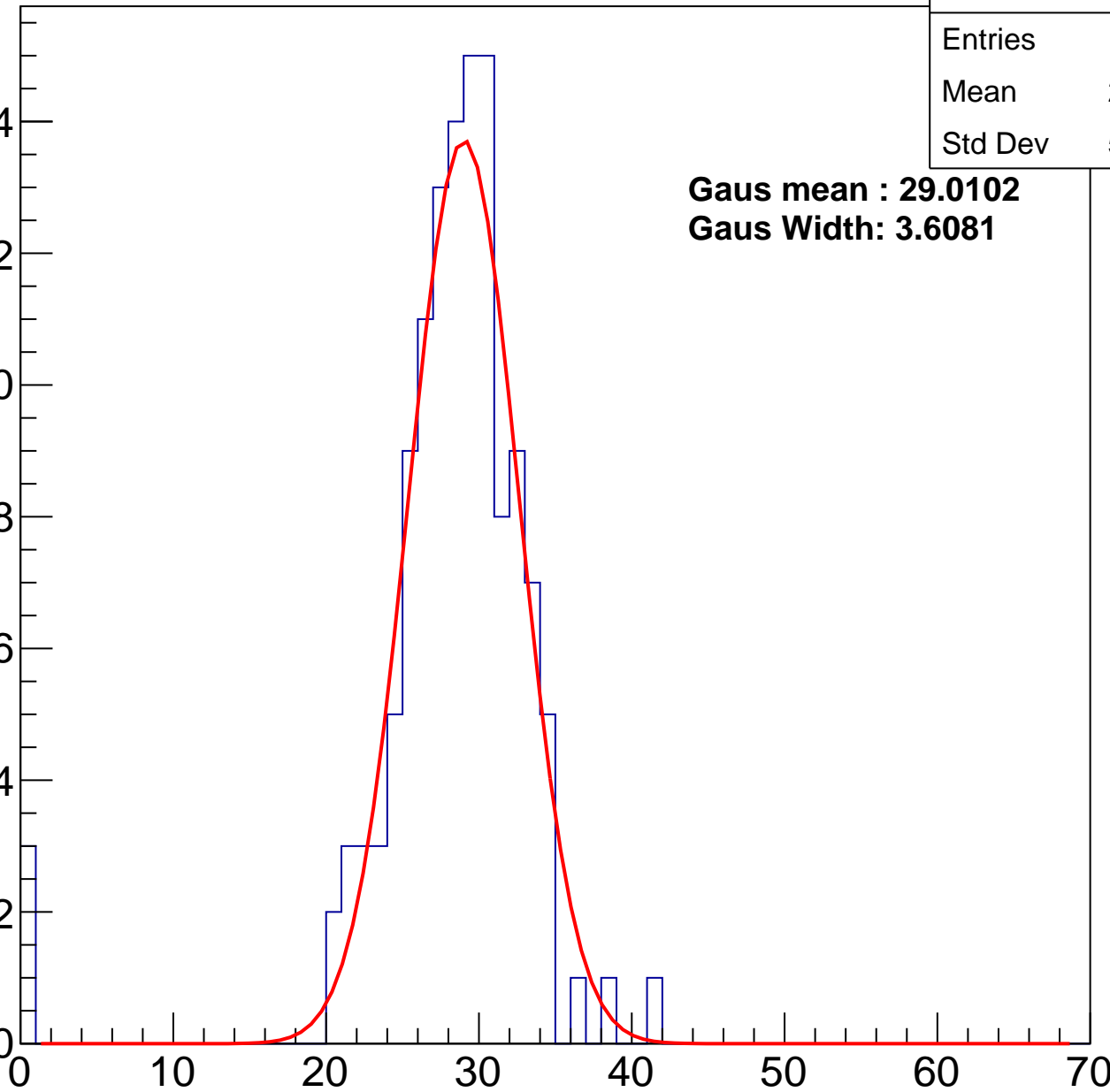
Gaus mean : 29.0102

Gaus Width: 3.6081

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch70, adc1

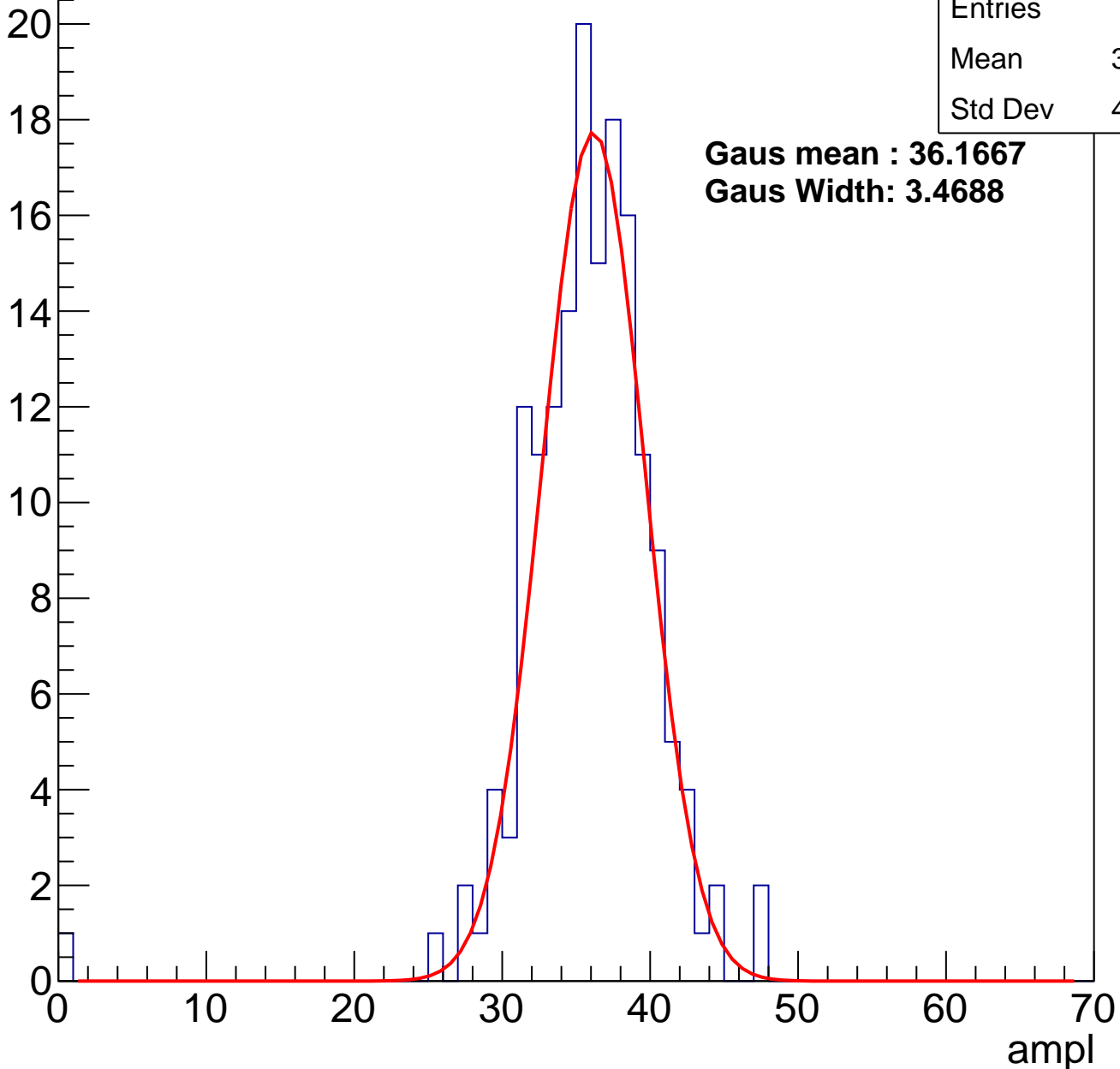
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	35.43
Std Dev	4.663

Gaus mean : 36.1667

Gaus Width: 3.4688

Entry



B0L101S, U24-ch70, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	43.52
Std Dev	3.637

Gaus mean : 44.0766

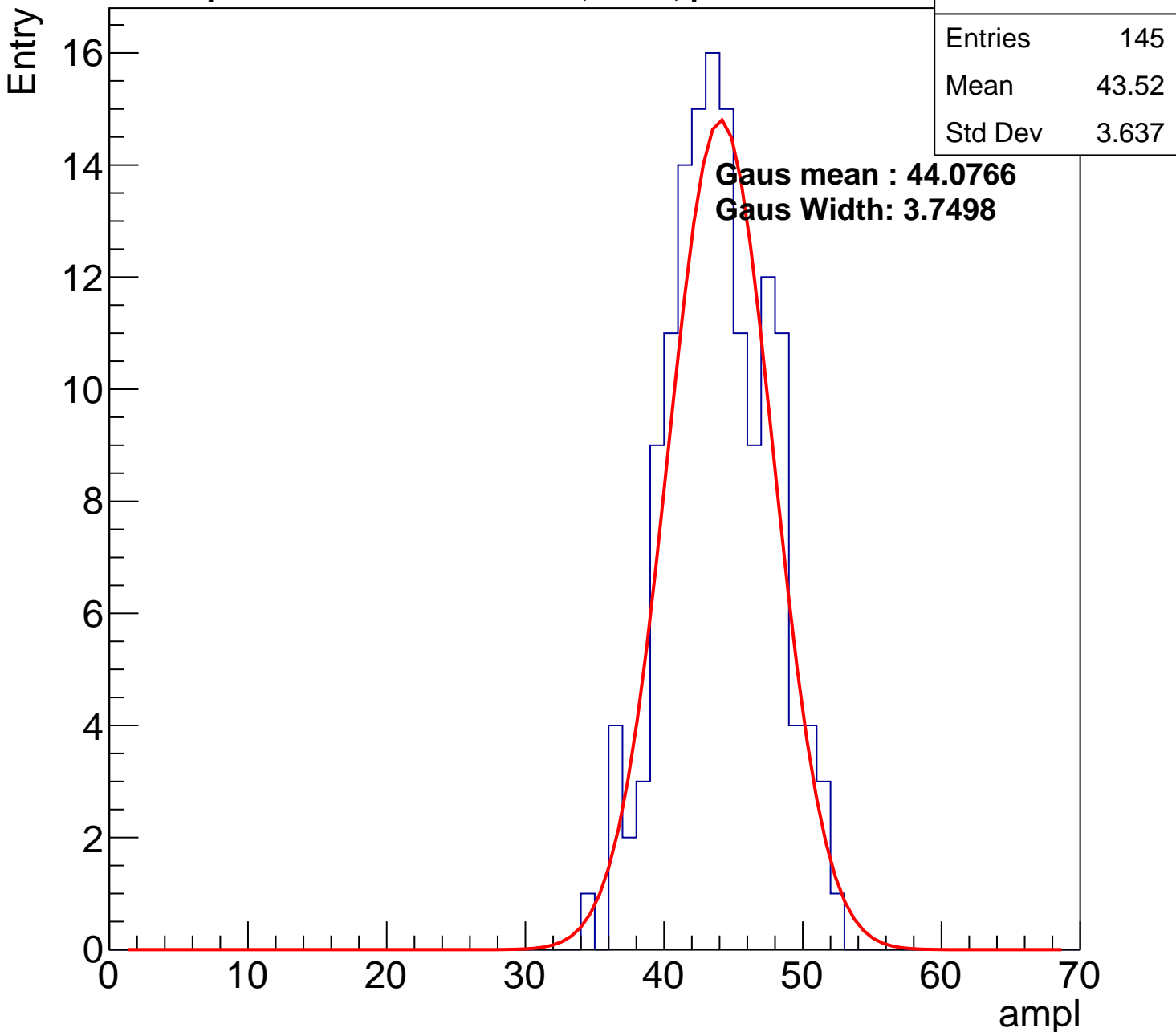
Gaus Width: 3.7498

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch70, adc3

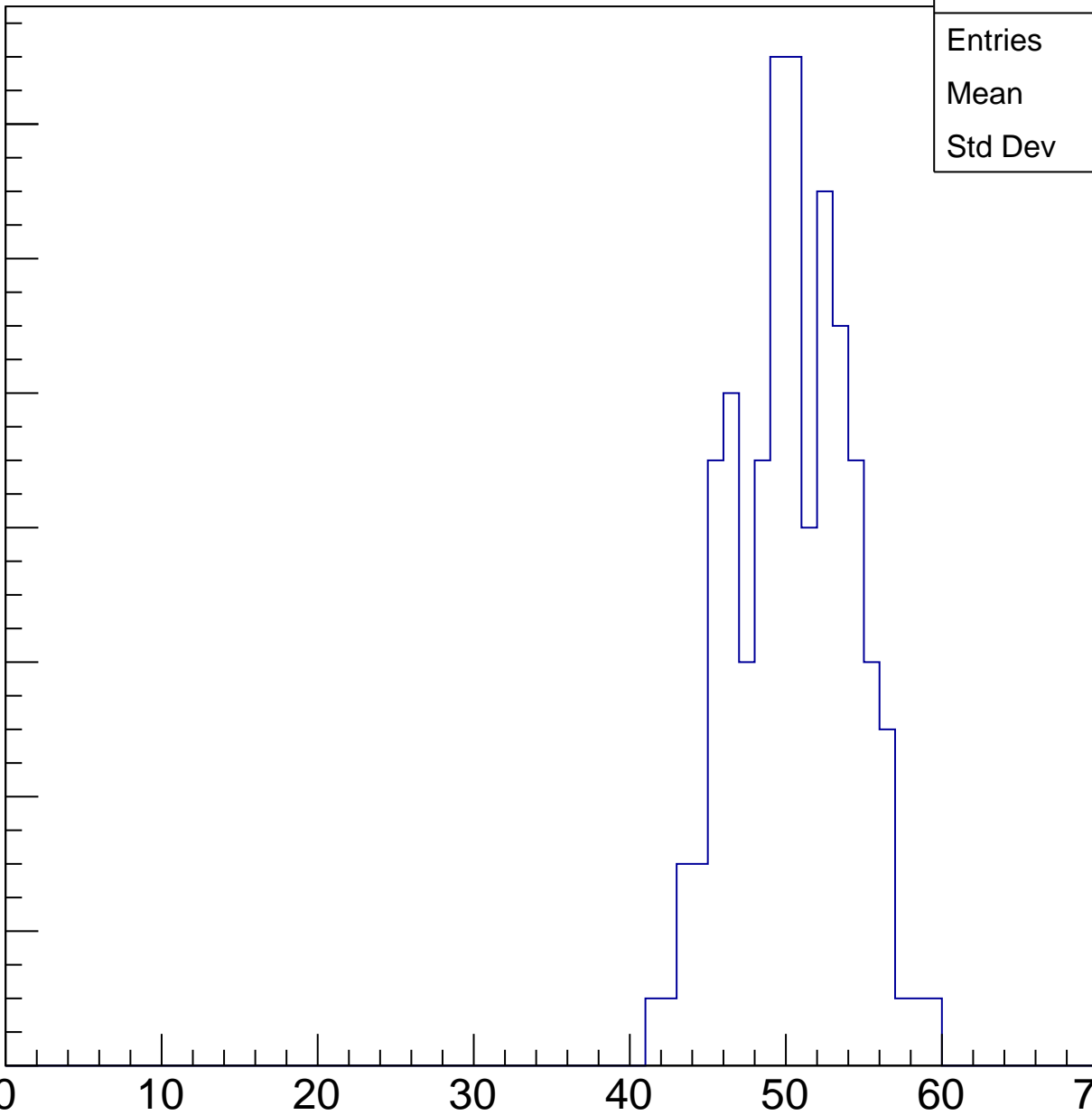
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	49.96
Std Dev	3.677

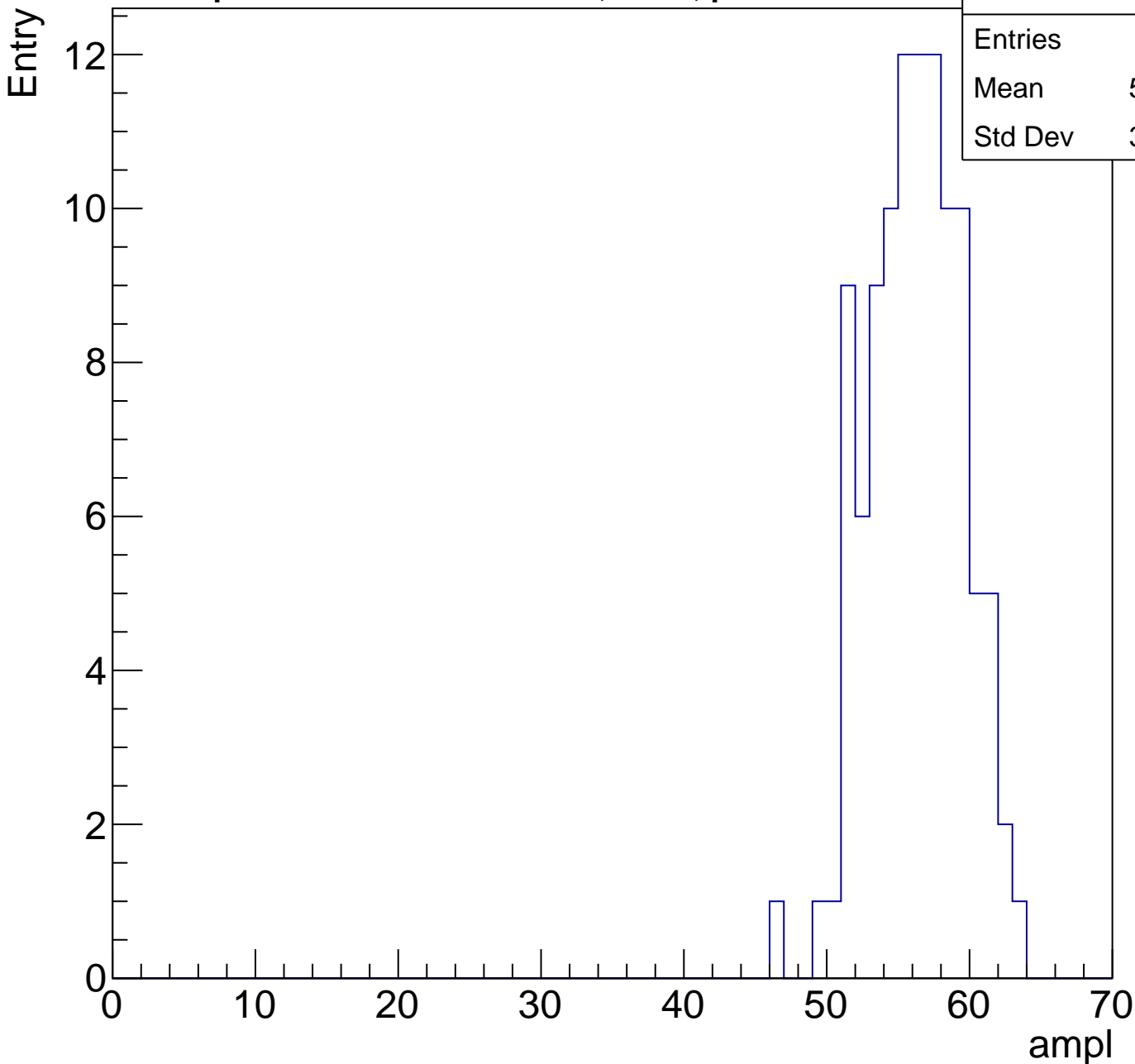
ampl



B0L101S, U24-ch70, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	55.76
Std Dev	3.223



B0L101S, U24-ch70, adc5

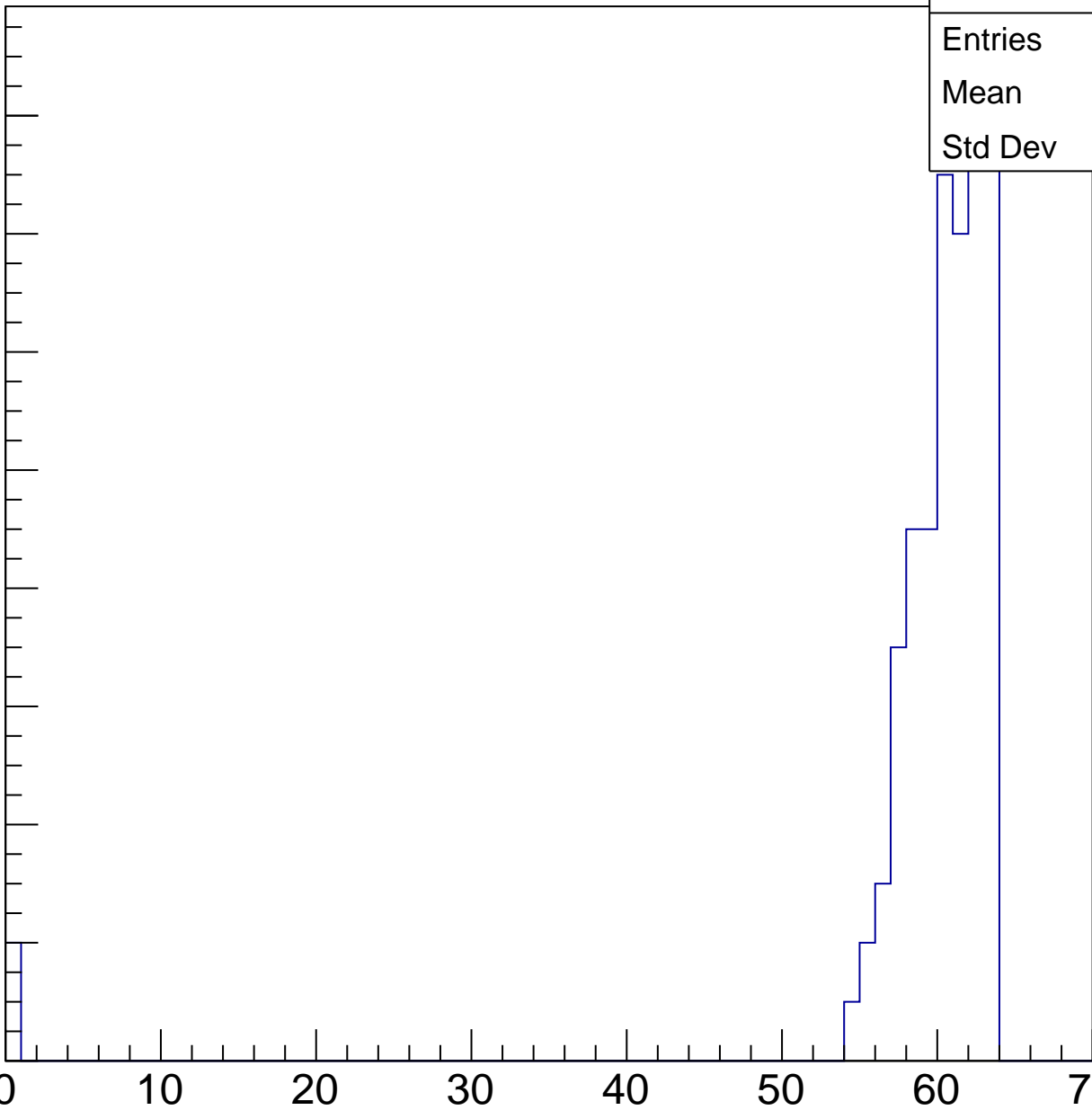
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.99
Std Dev	8.885

ampl



B0L101S, U24-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

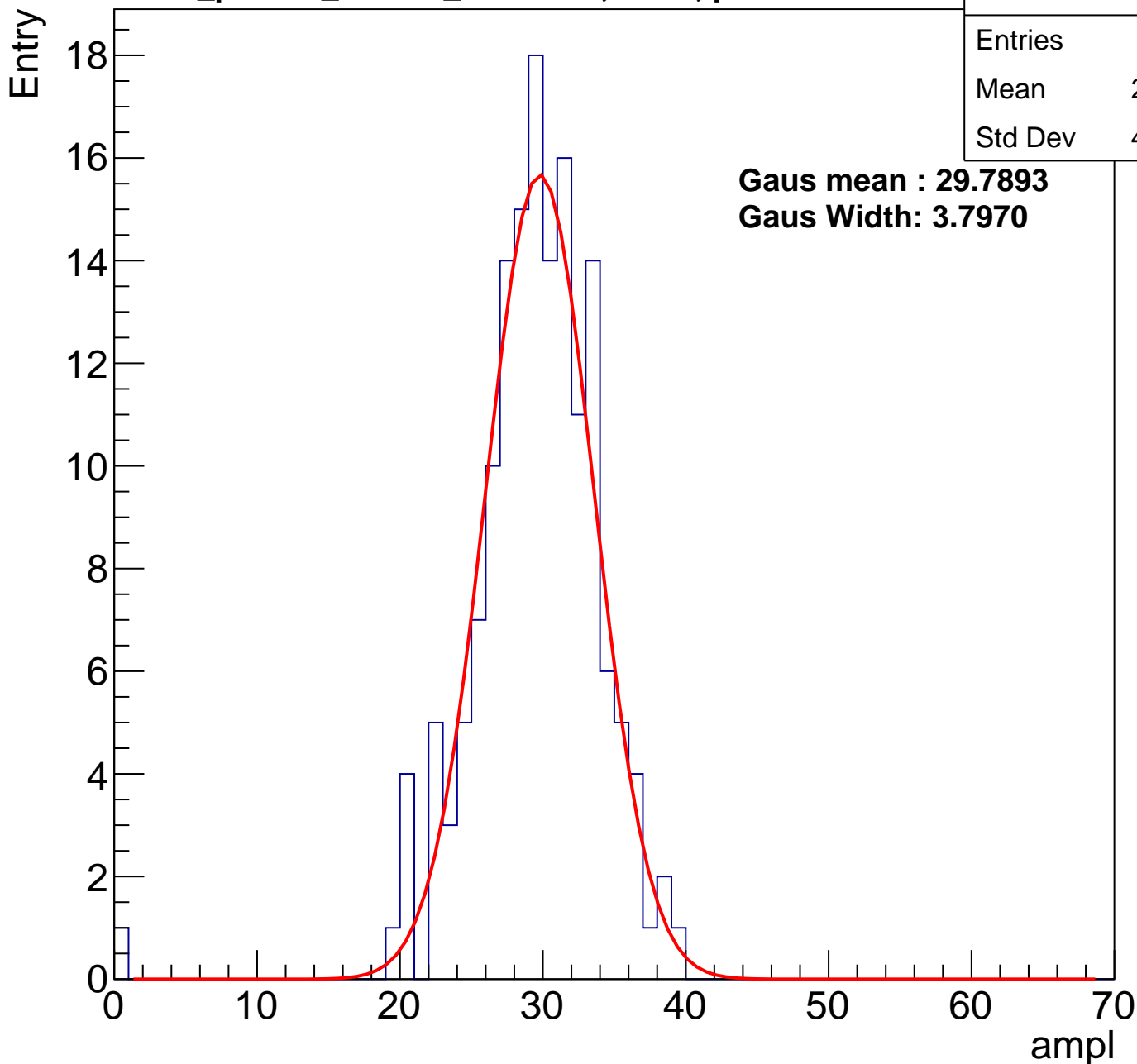
B0L101S, U24-ch71, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	29.03
Std Dev	4.563

Gaus mean : 29.7893

Gaus Width: 3.7970



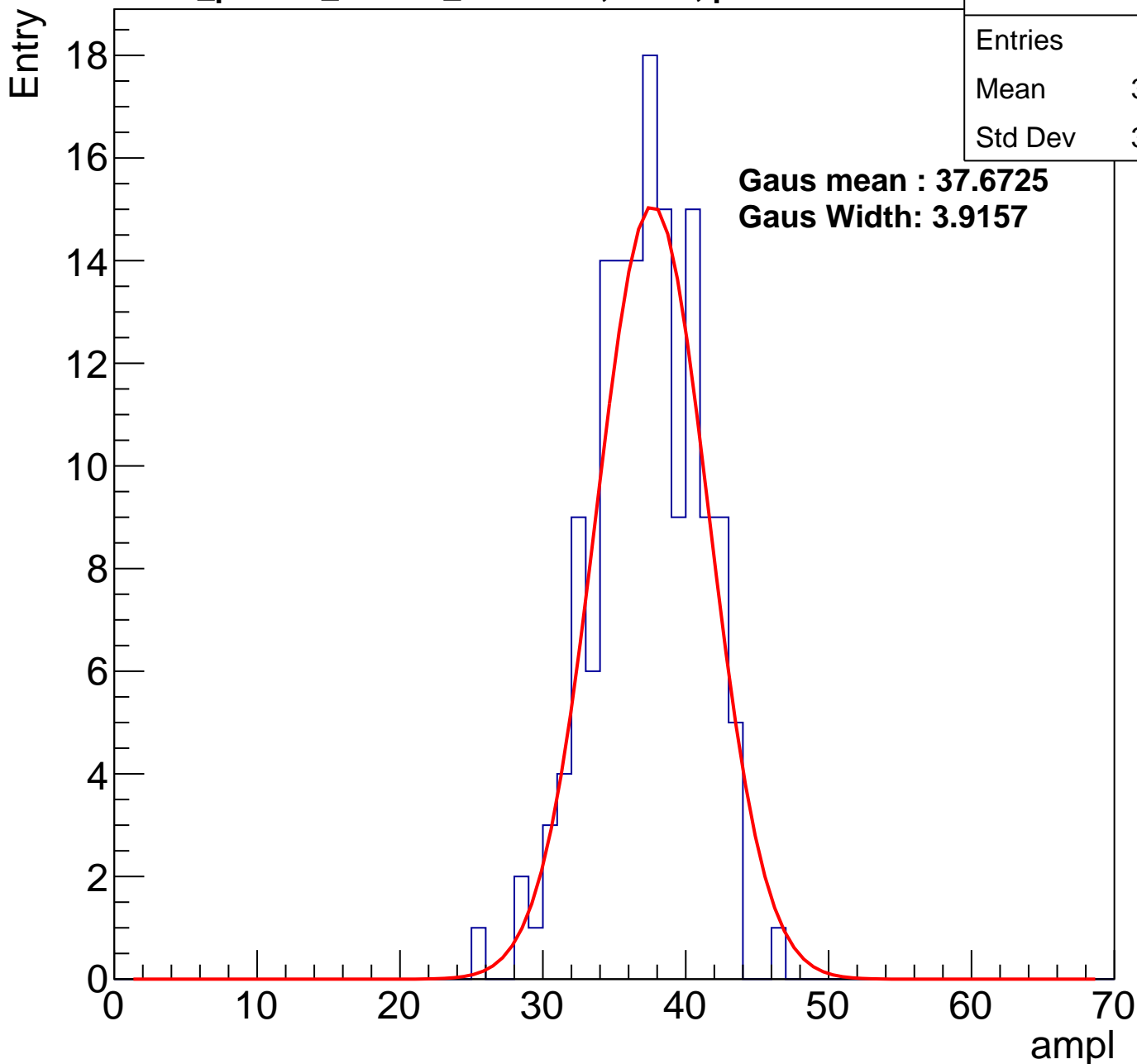
B0L101S, U24-ch71, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	36.74
Std Dev	3.649

Gaus mean : 37.6725

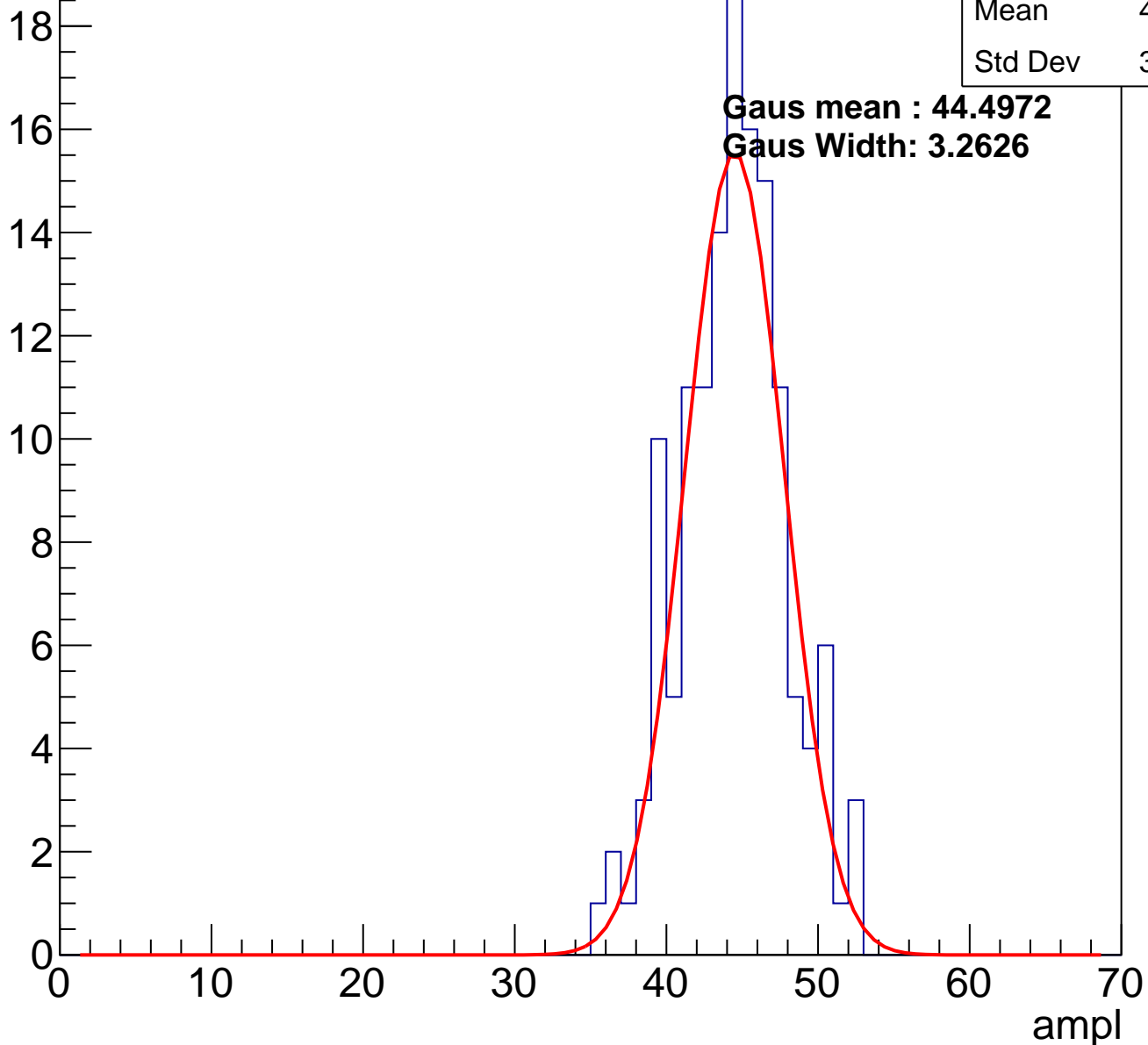
Gaus Width: 3.9157



B0L101S, U24-ch71, adc2

calib_packv5_042523_0143.root, FC#1, port C1

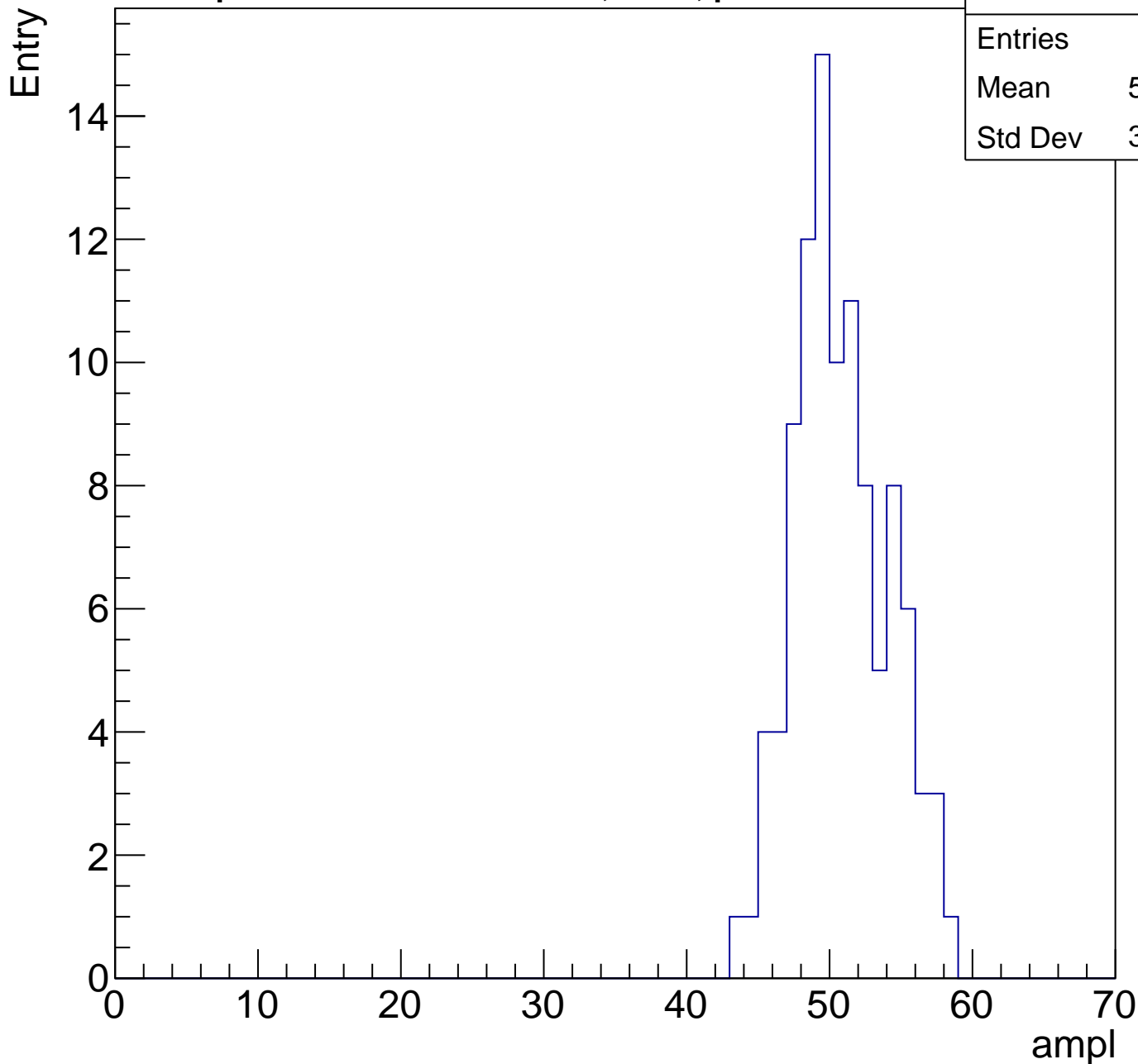
Entry



B0L101S, U24-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

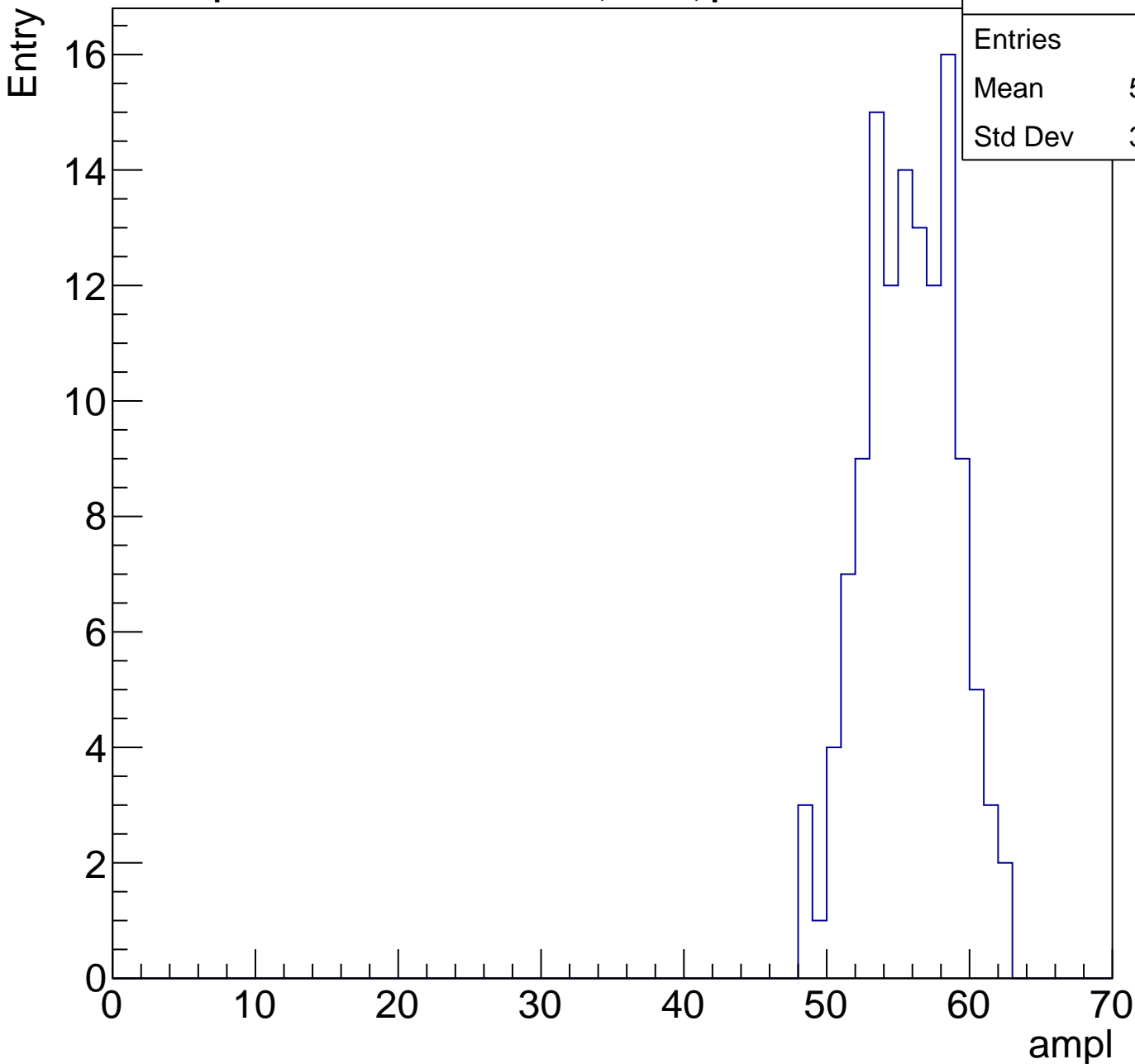
Entries	101
Mean	50.36
Std Dev	3.247



B0L101S, U24-ch71, adc4

calib_packv5_042523_0143.root, FC#1, port C1

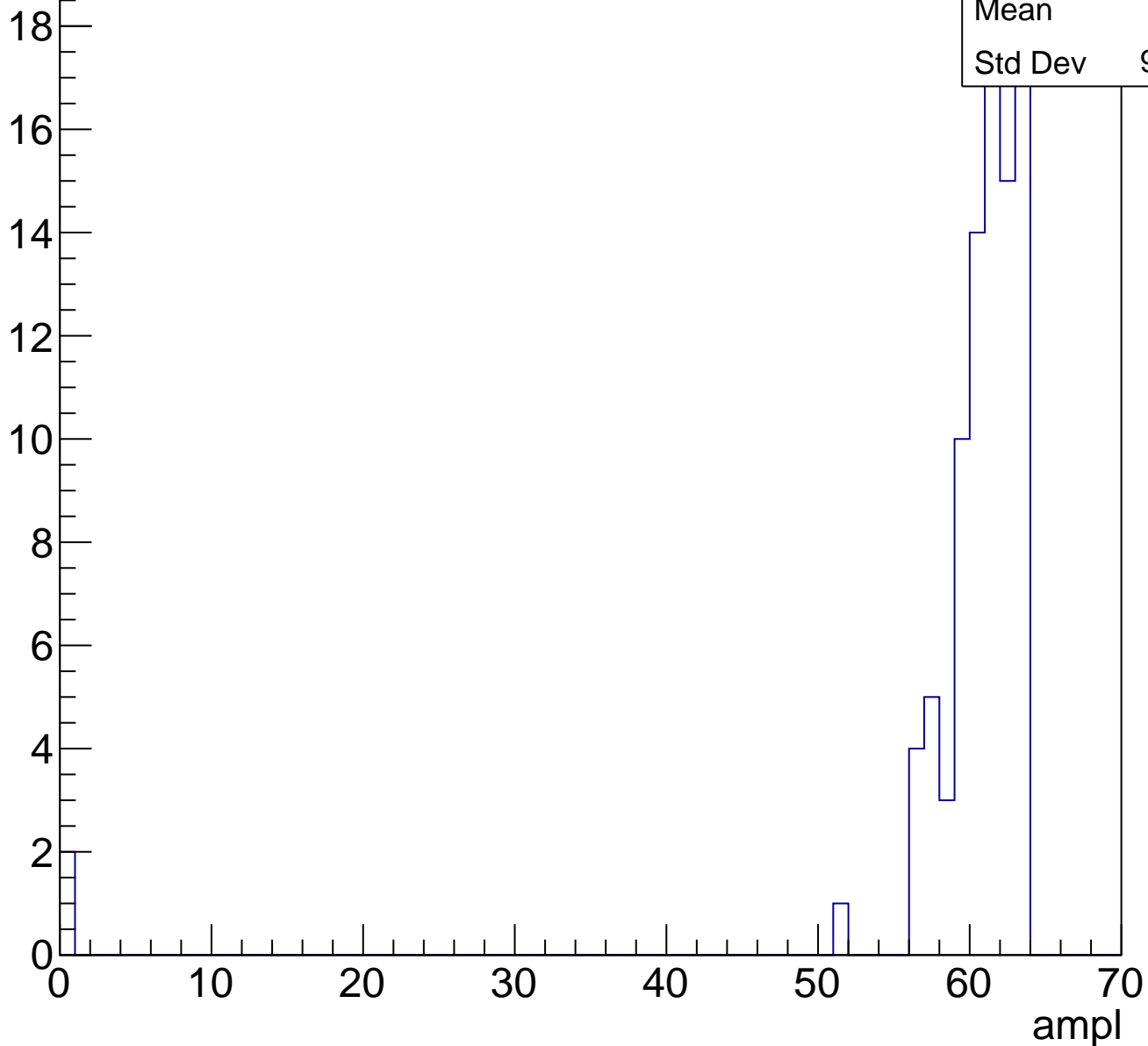
Entries	125
Mean	55.27
Std Dev	3.123



B0L101S, U24-ch71, adc5

calib_packv5_042523_0143.root, FC#1, port C1

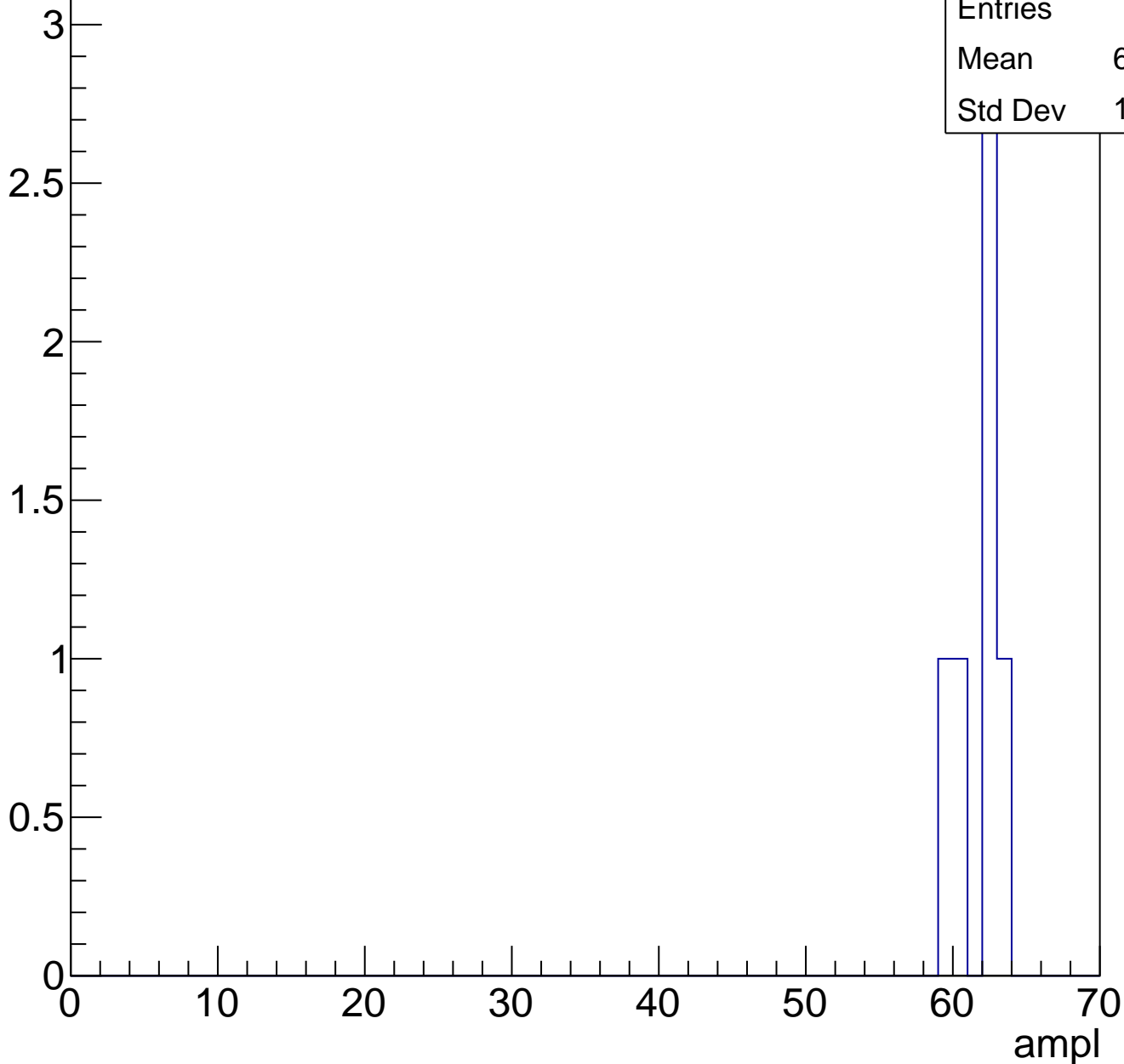
Entry



B0L101S, U24-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L101S, U24-ch72, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	28.56
Std Dev	3.433

Gaus mean : 28.8744

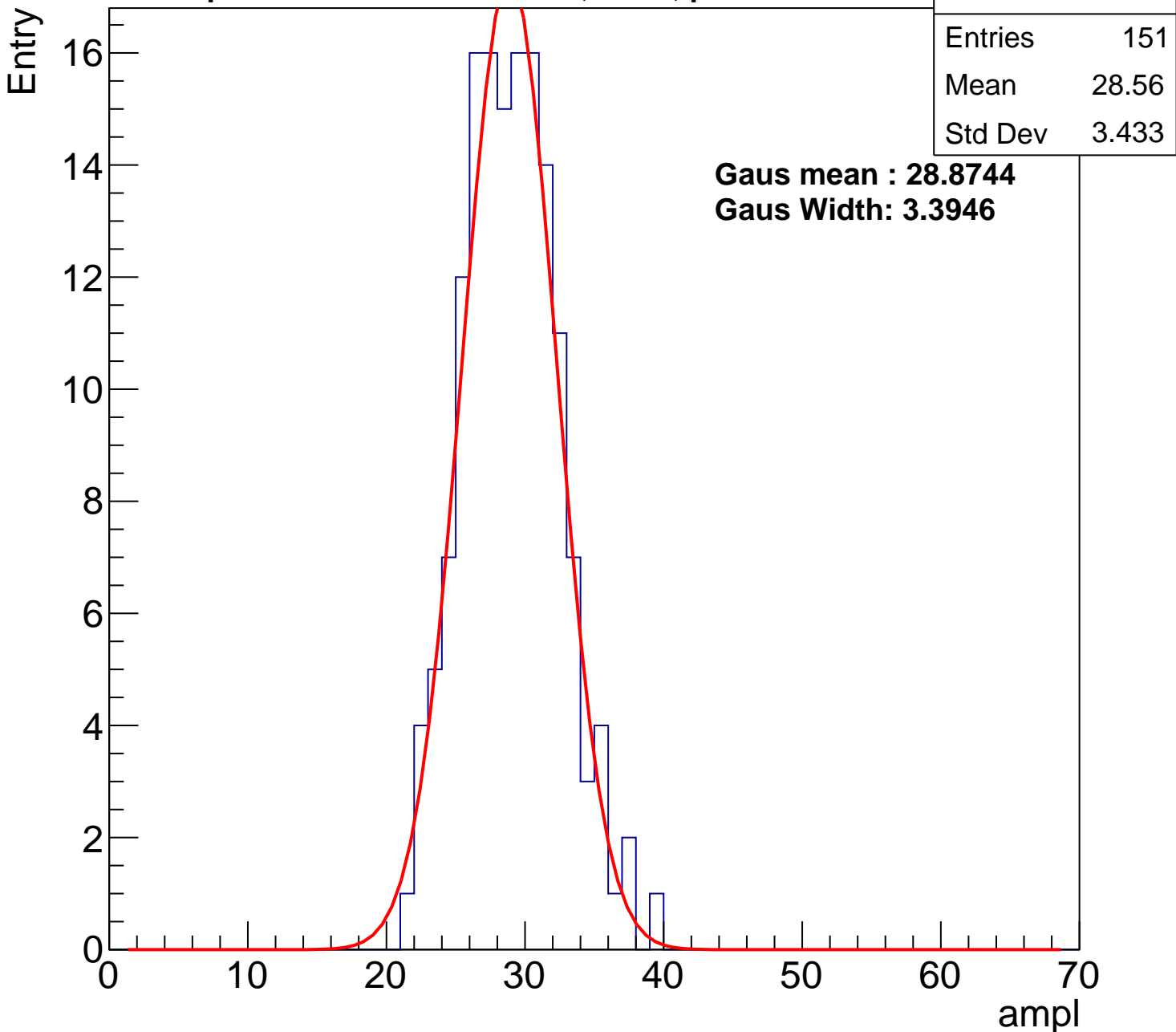
Gaus Width: 3.3946

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch72, adc1

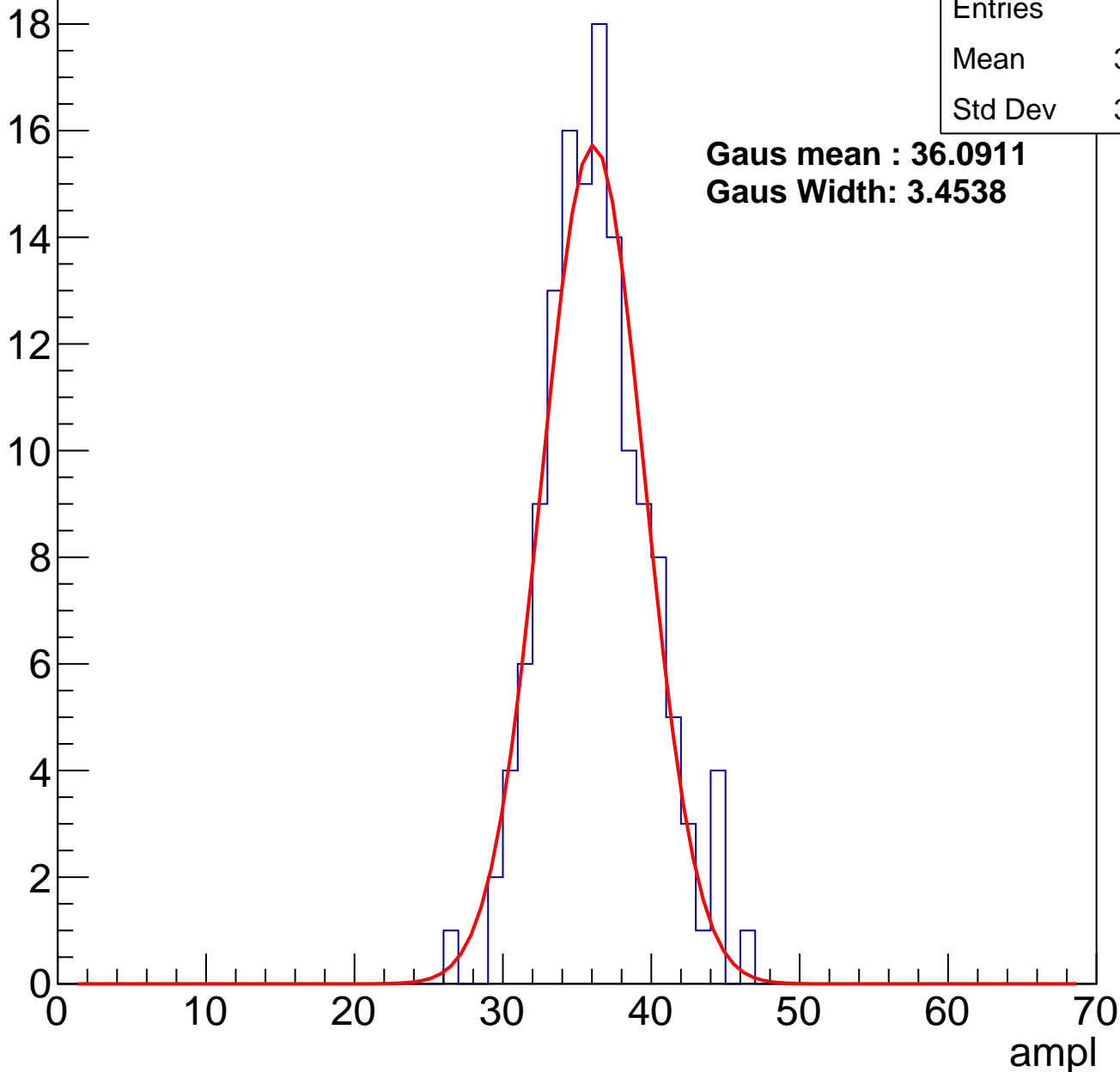
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	35.89
Std Dev	3.529

Gaus mean : 36.0911

Gaus Width: 3.4538

Entry



B0L101S, U24-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

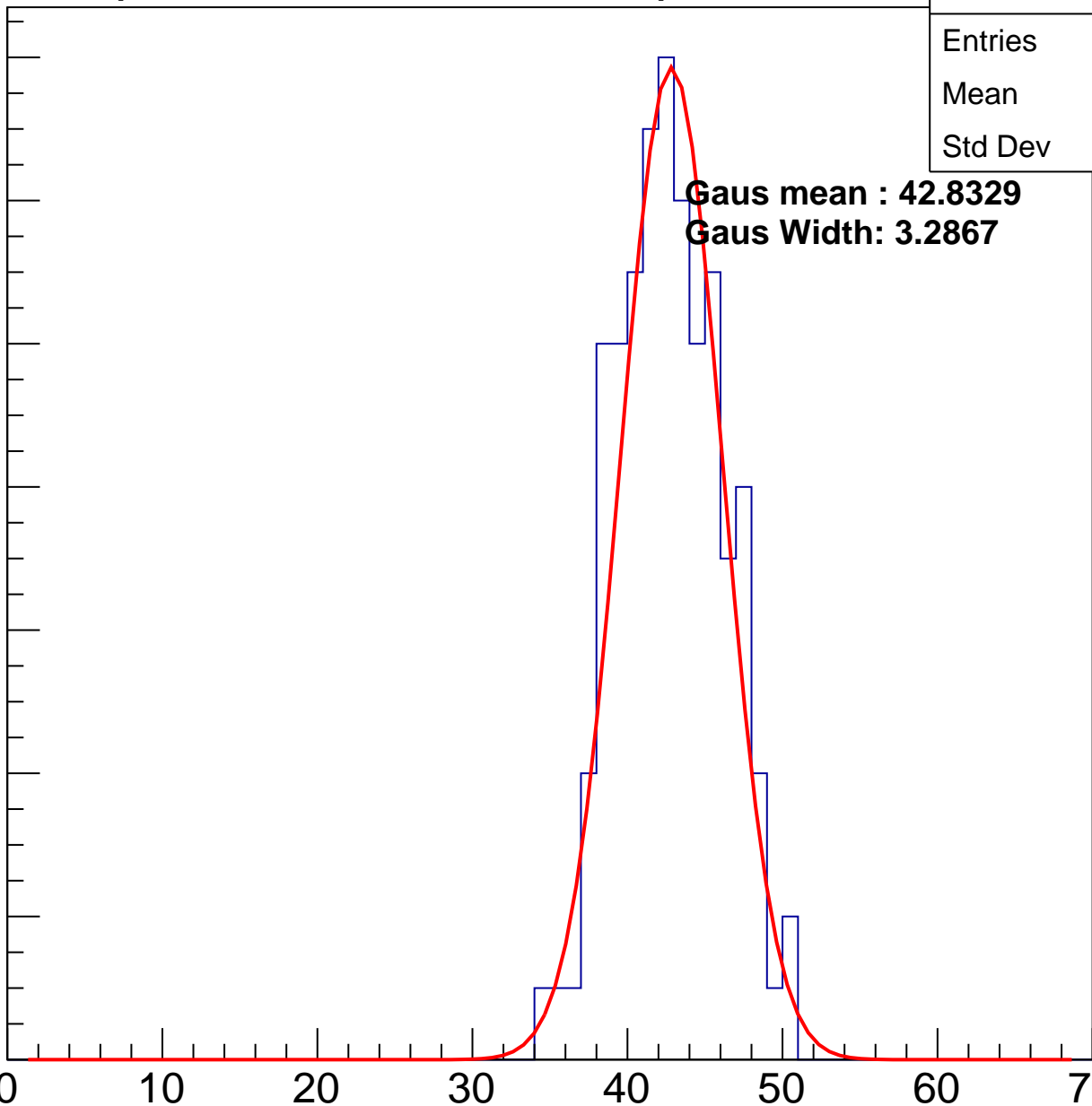
14
12
10
8
6
4
2
0

Entries	120
Mean	42.28
Std Dev	3.33

Gaus mean : 42.8329

Gaus Width: 3.2867

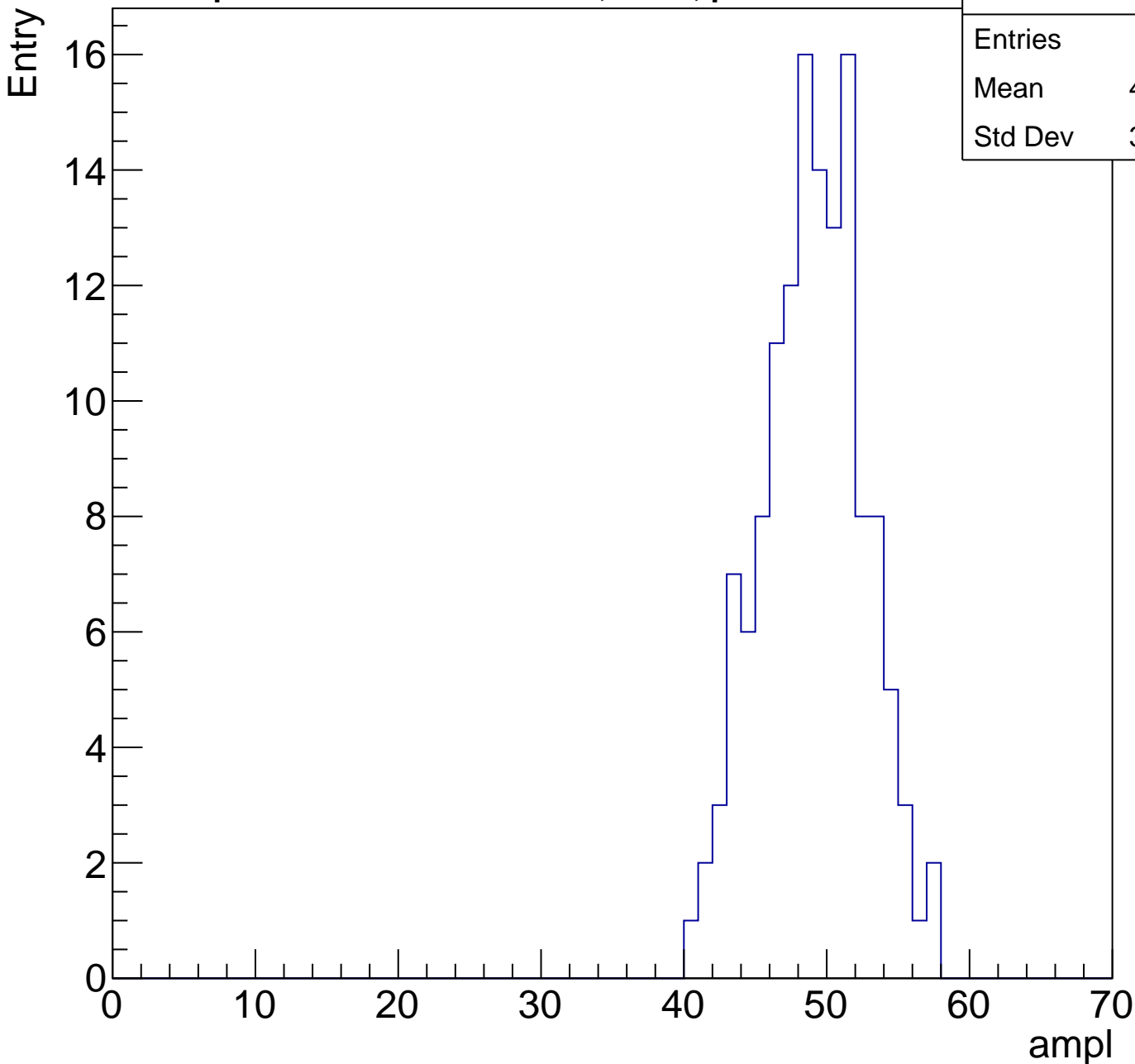
ampl



B0L101S, U24-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	48.59
Std Dev	3.549



B0L101S, U24-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

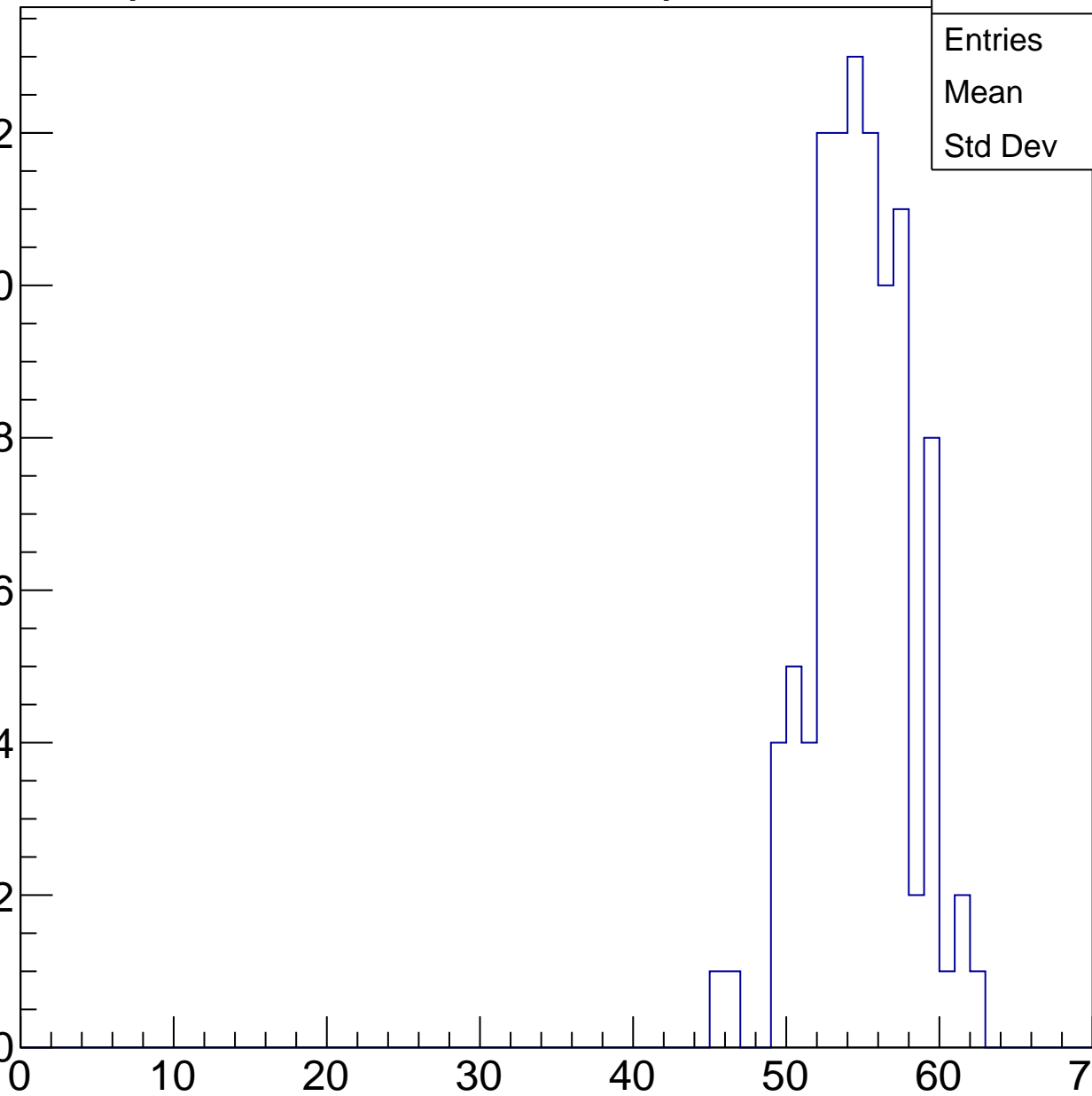
4

2

0

Entries	99
Mean	54.36
Std Dev	3.161

ampl



B0L101S, U24-ch72, adc5

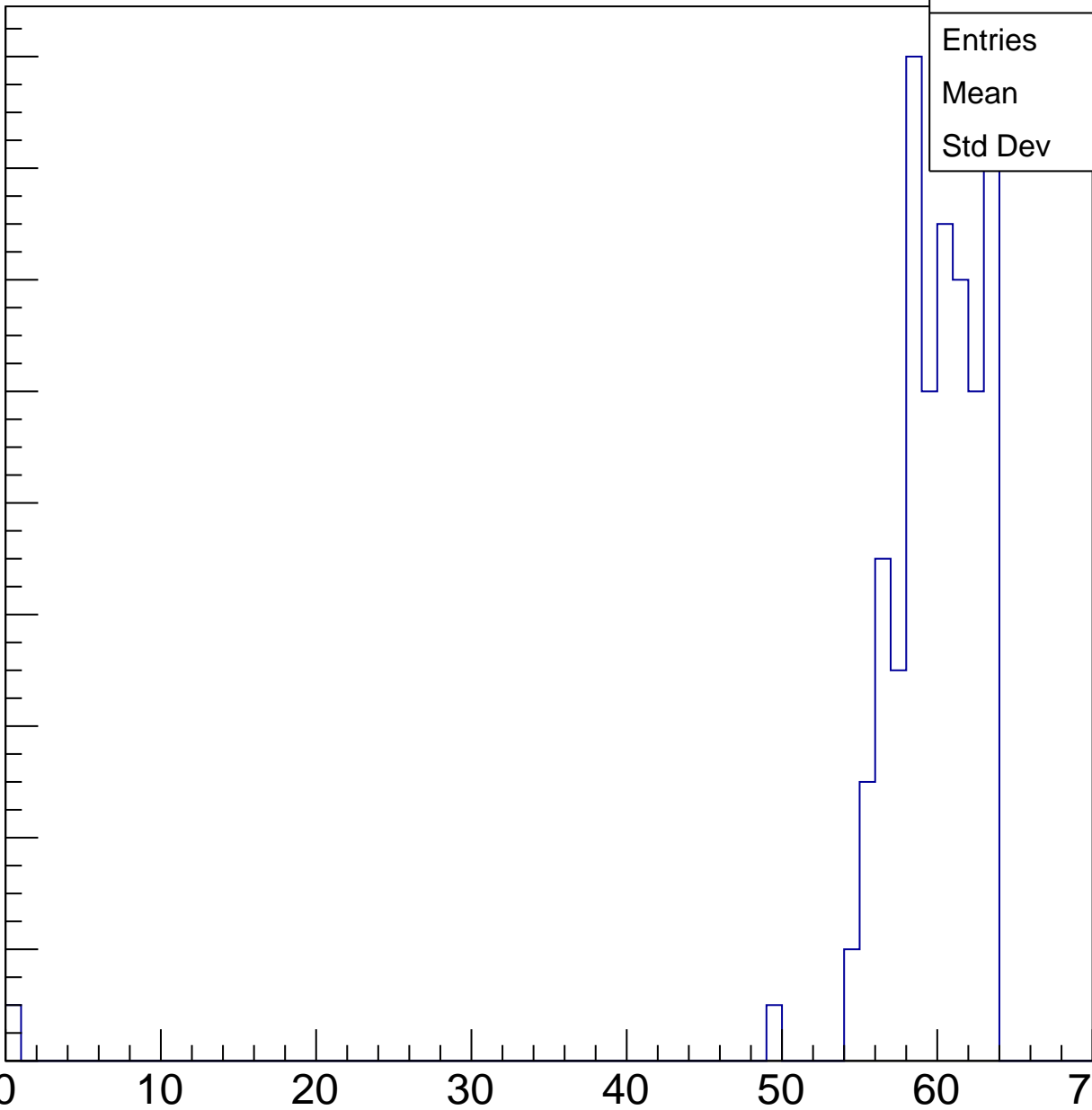
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	112
Mean	58.87
Std Dev	6.175

ampl

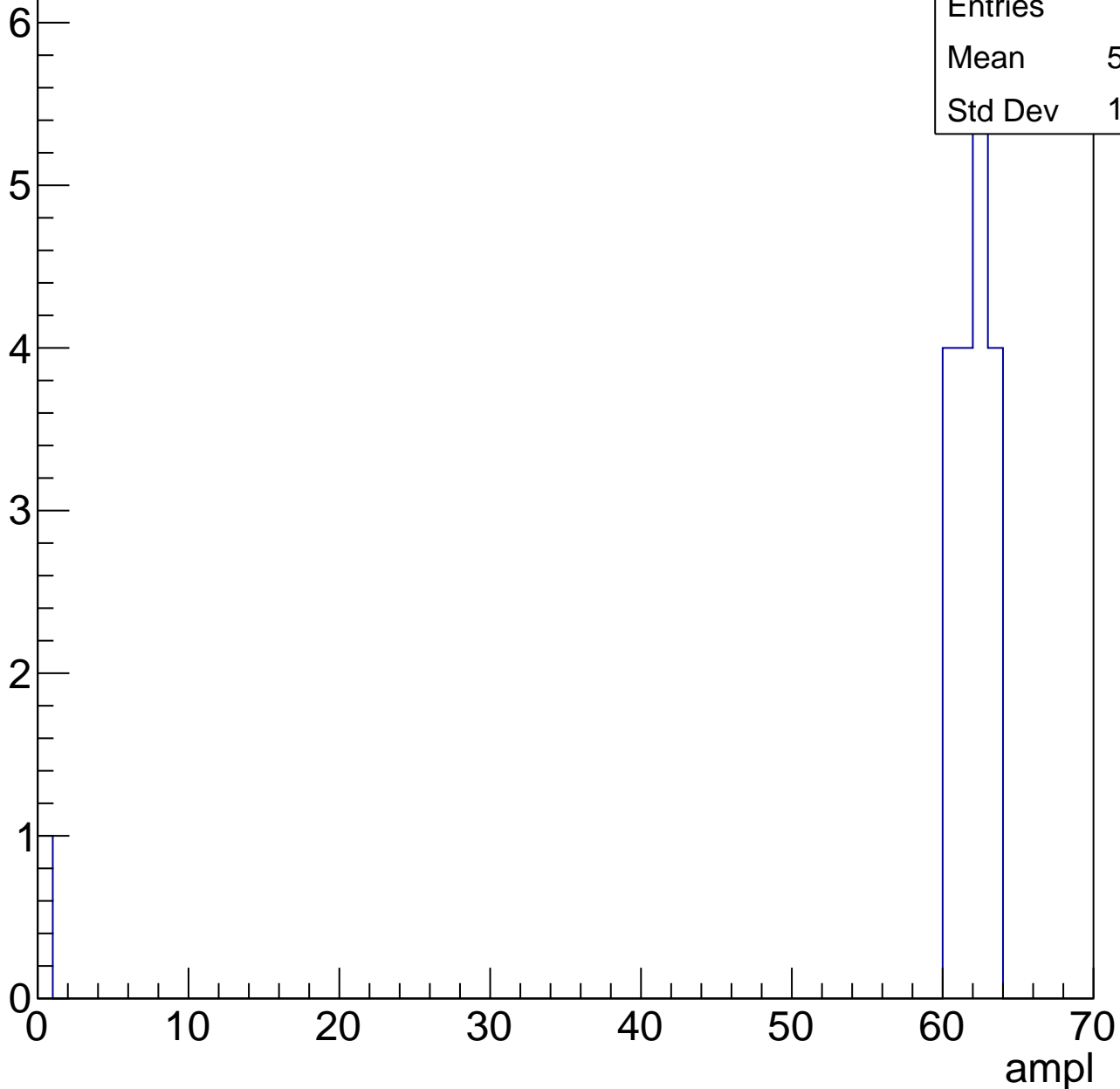


B0L101S, U24-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

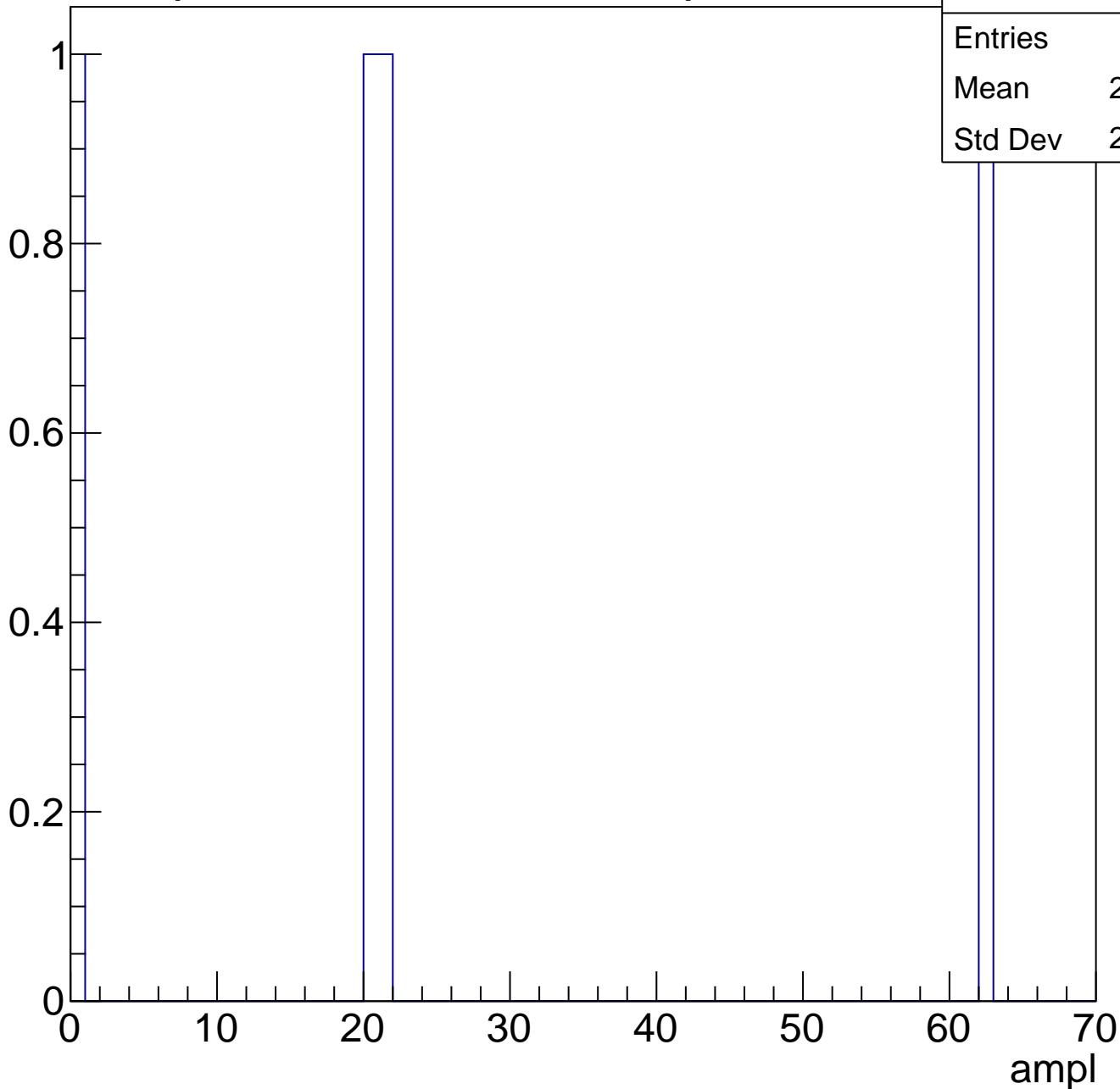
Entries	19
Mean	58.32
Std Dev	13.78



B0L101S, U24-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



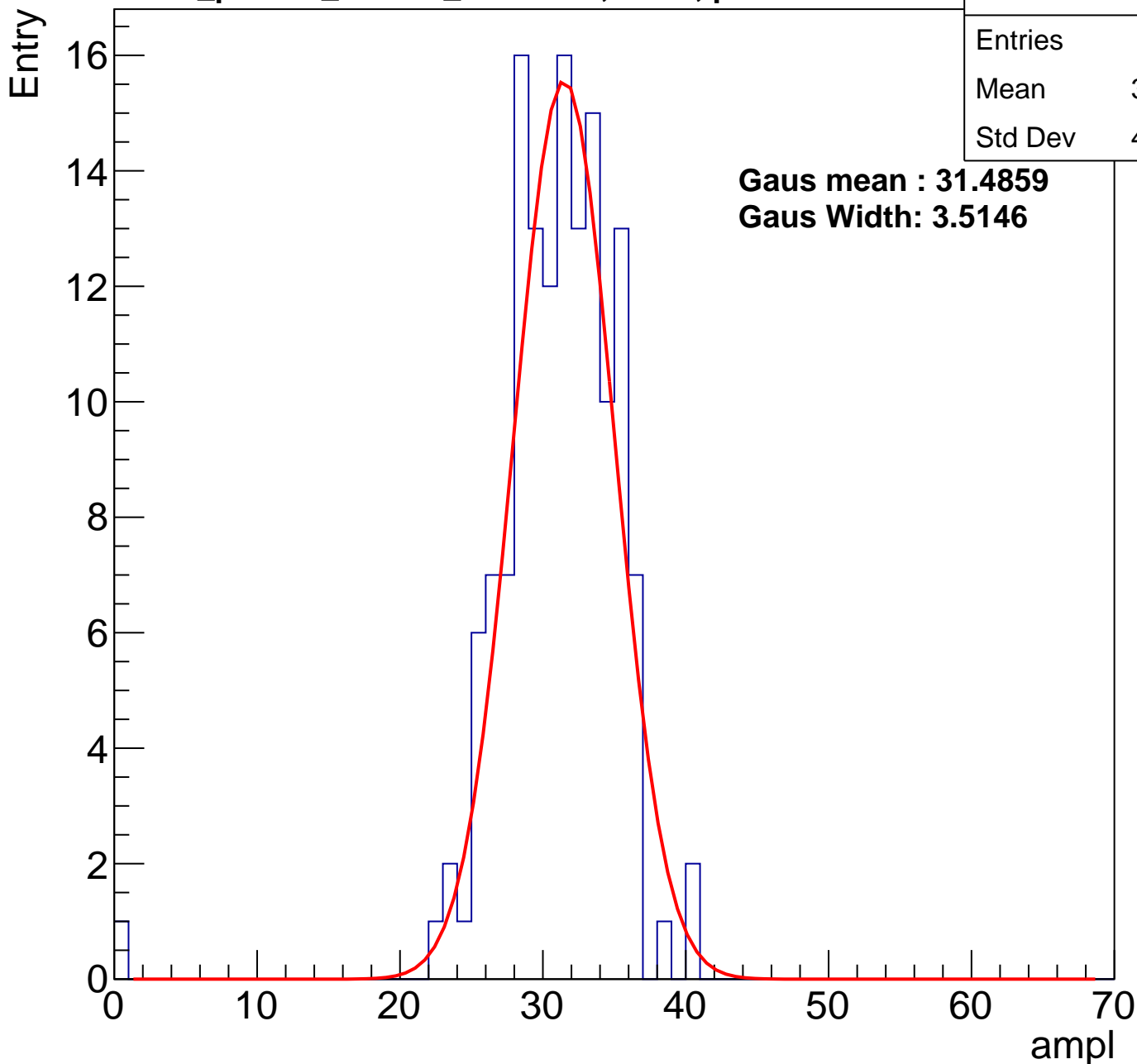
B0L101S, U24-ch73, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	30.56
Std Dev	4.312

Gaus mean : 31.4859

Gaus Width: 3.5146



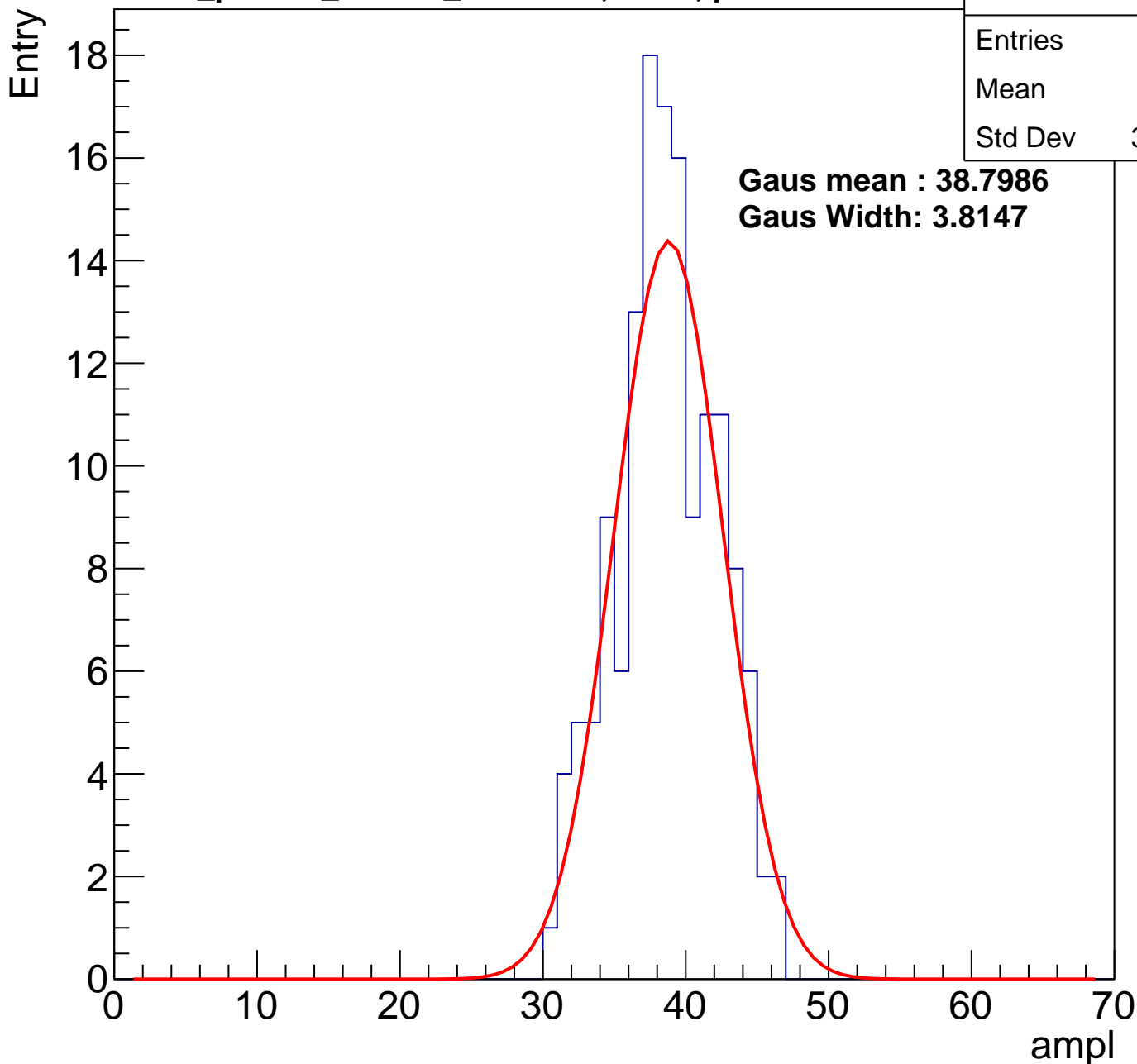
B0L101S, U24-ch73, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	38.2
Std Dev	3.544

Gaus mean : 38.7986

Gaus Width: 3.8147



B0L101S, U24-ch73, adc2

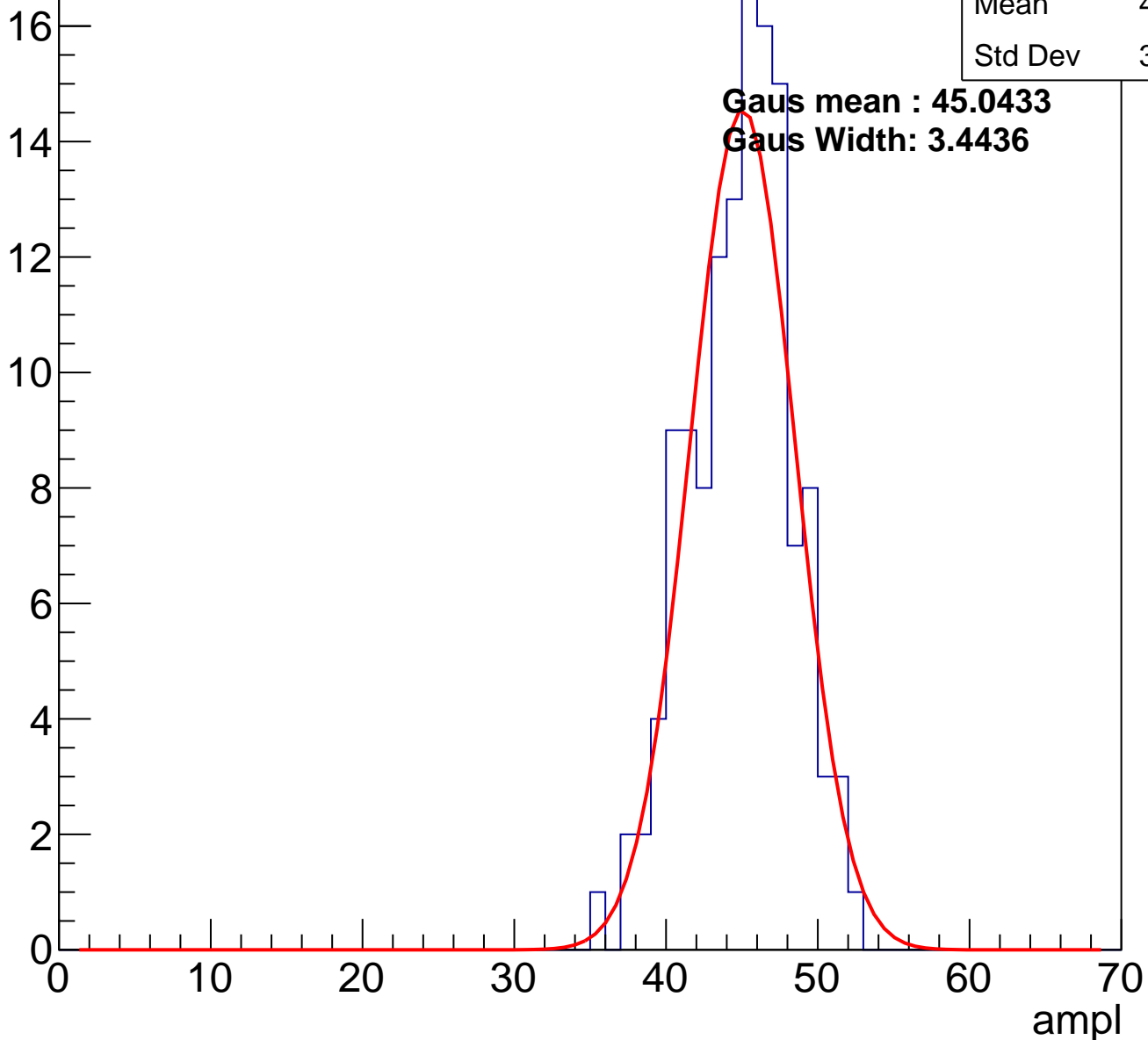
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	130
Mean	44.48
Std Dev	3.324

Gaus mean : 45.0433

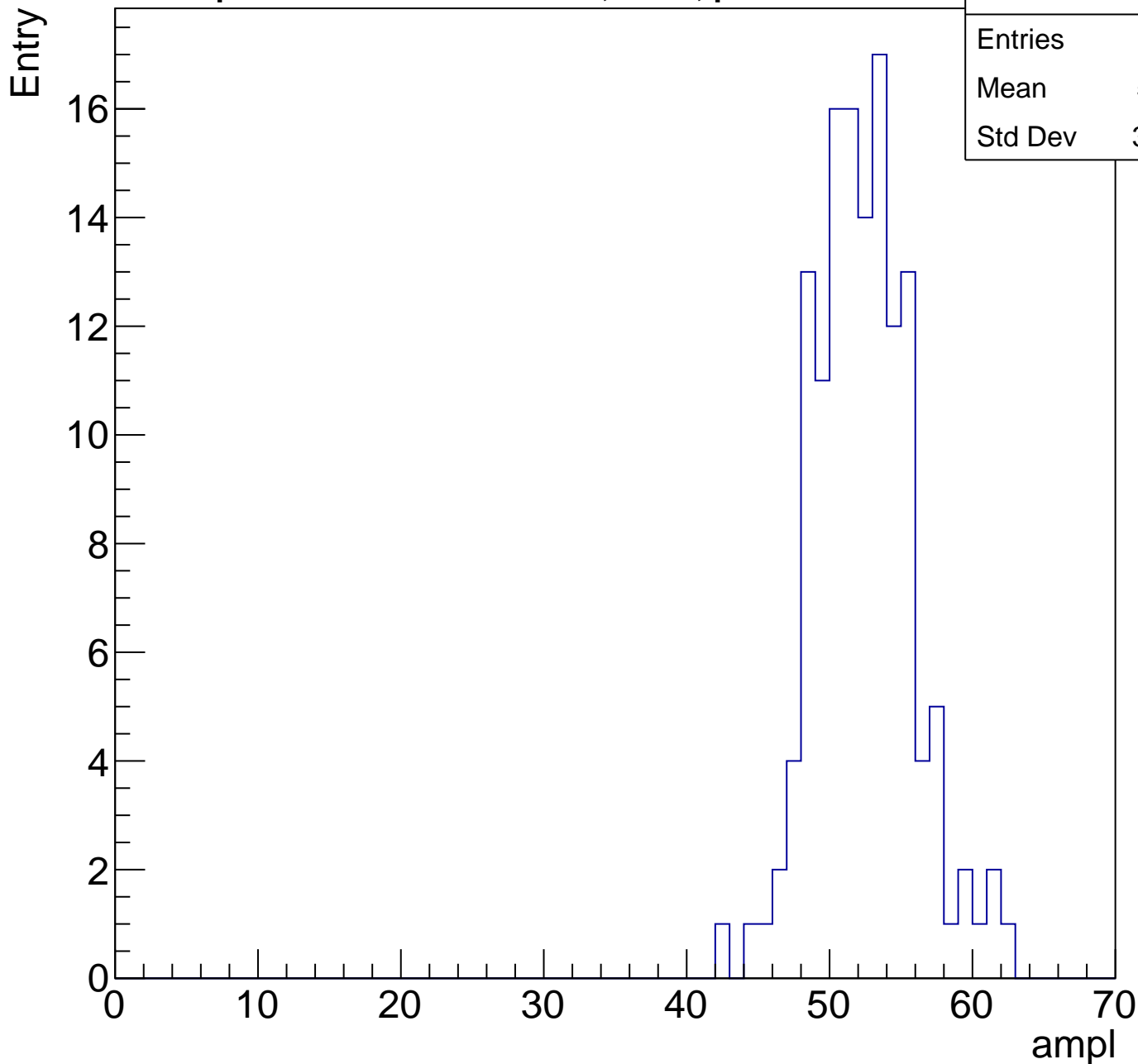
Gaus Width: 3.4436



B0L101S, U24-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

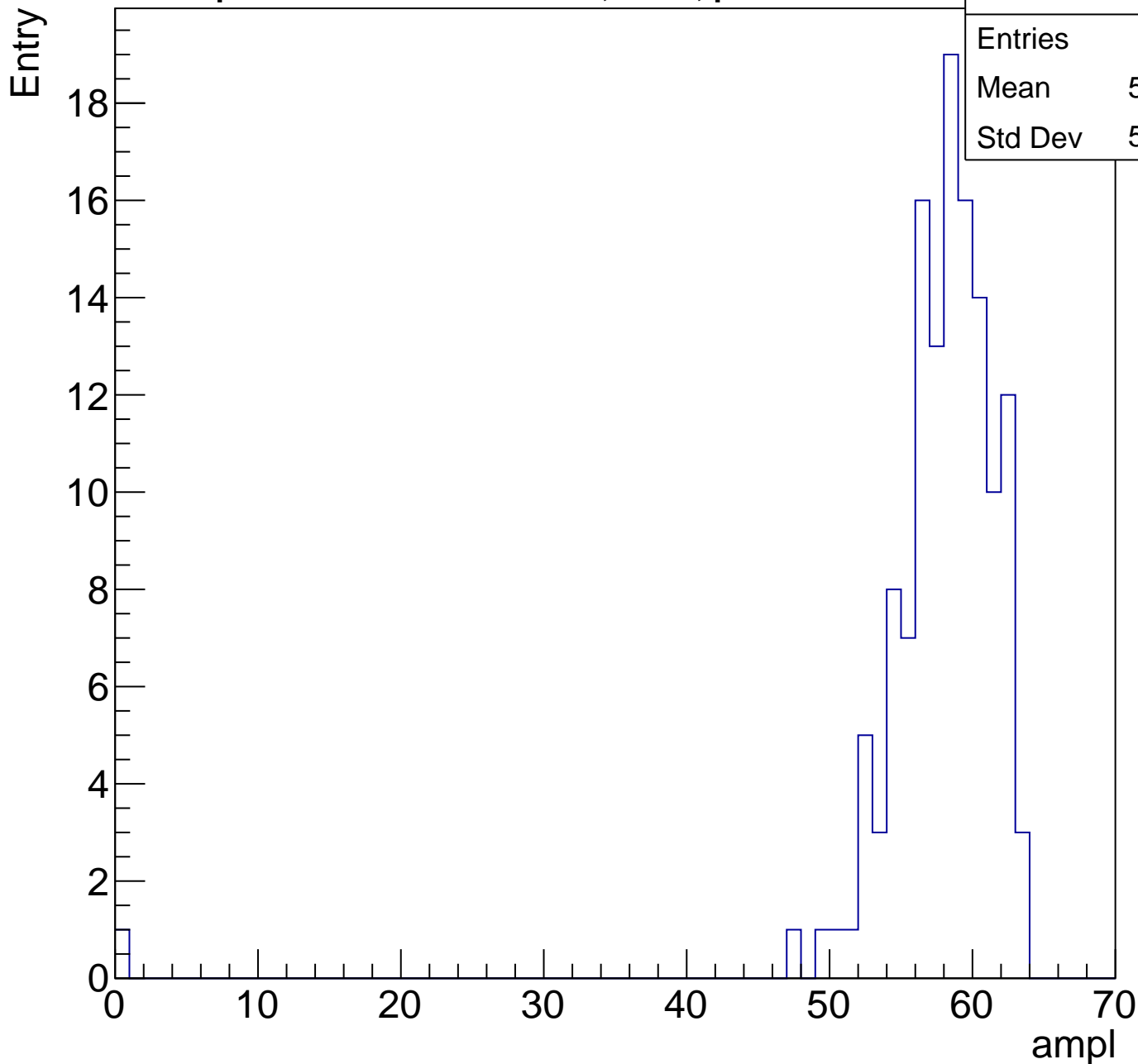
Entries	137
Mean	51.91
Std Dev	3.419



B0L101S, U24-ch73, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	57.24
Std Dev	5.897



B0L101S, U24-ch73, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	39
Mean	61.54
Std Dev	1.631

ampl

0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B0L101S, U24-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch74, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	28.36
Std Dev	4.944

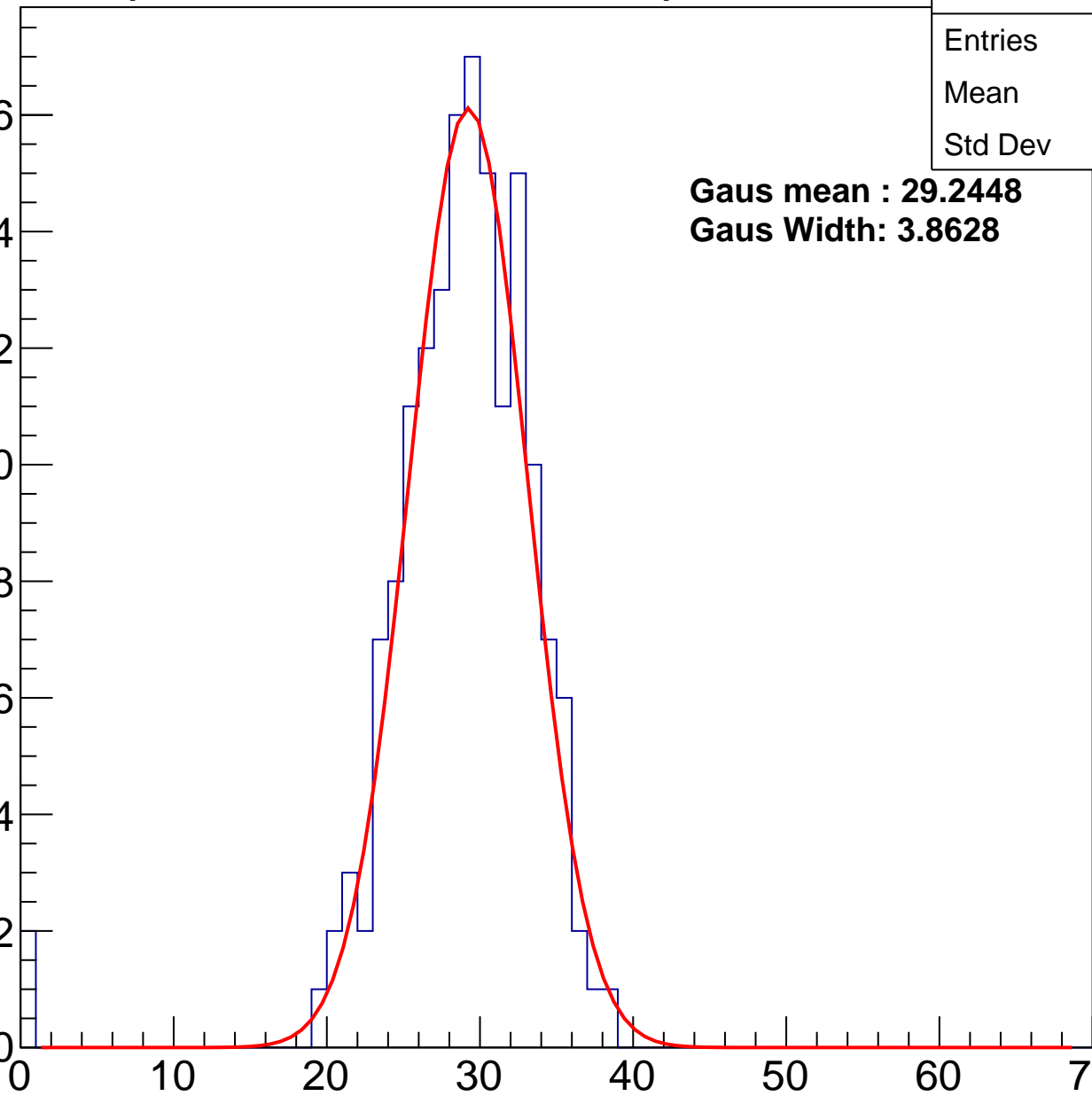
Gaus mean : 29.2448

Gaus Width: 3.8628

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	35.49
Std Dev	3.502

Gaus mean : 35.9569

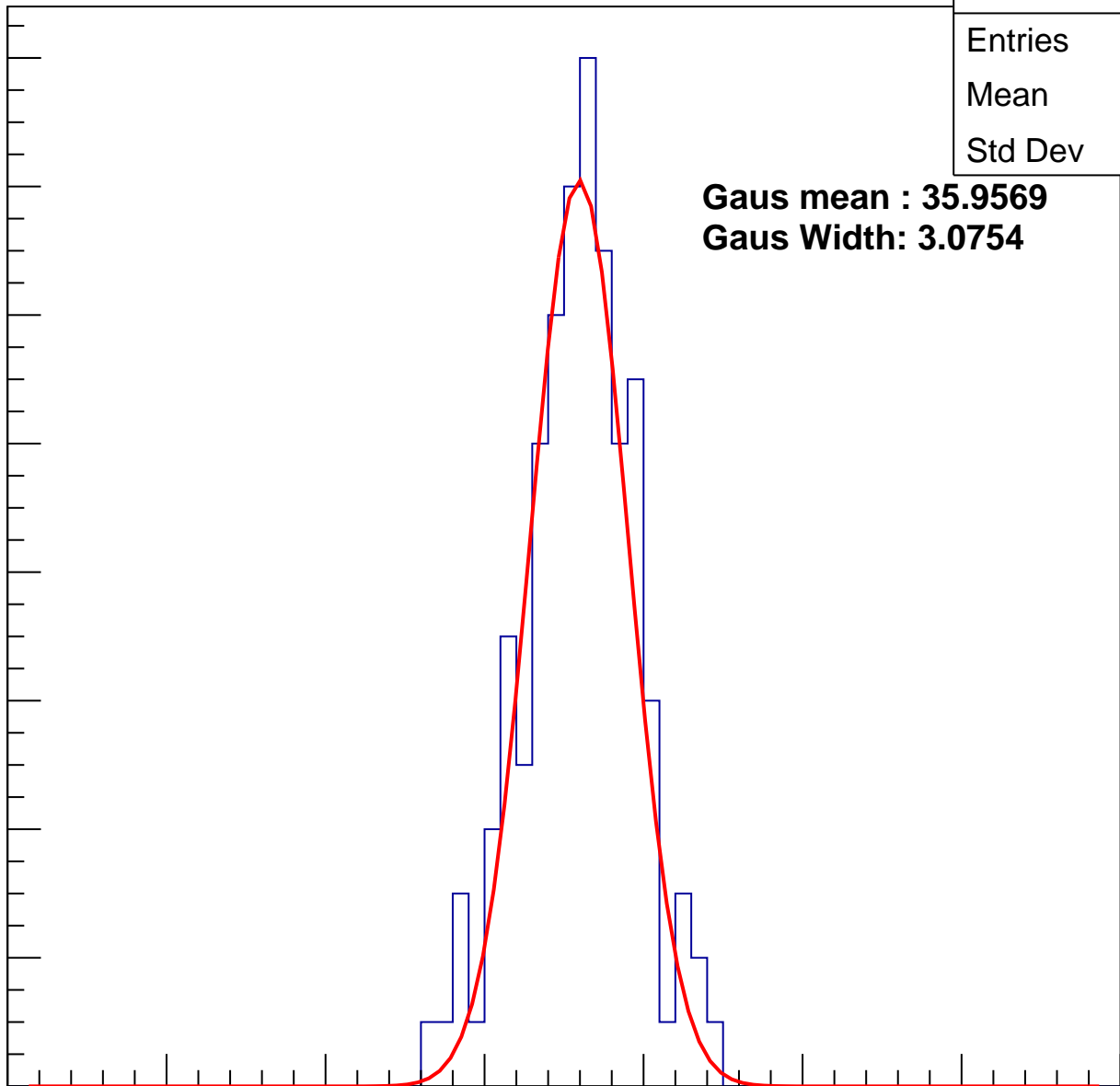
Gaus Width: 3.0754

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch74, adc2

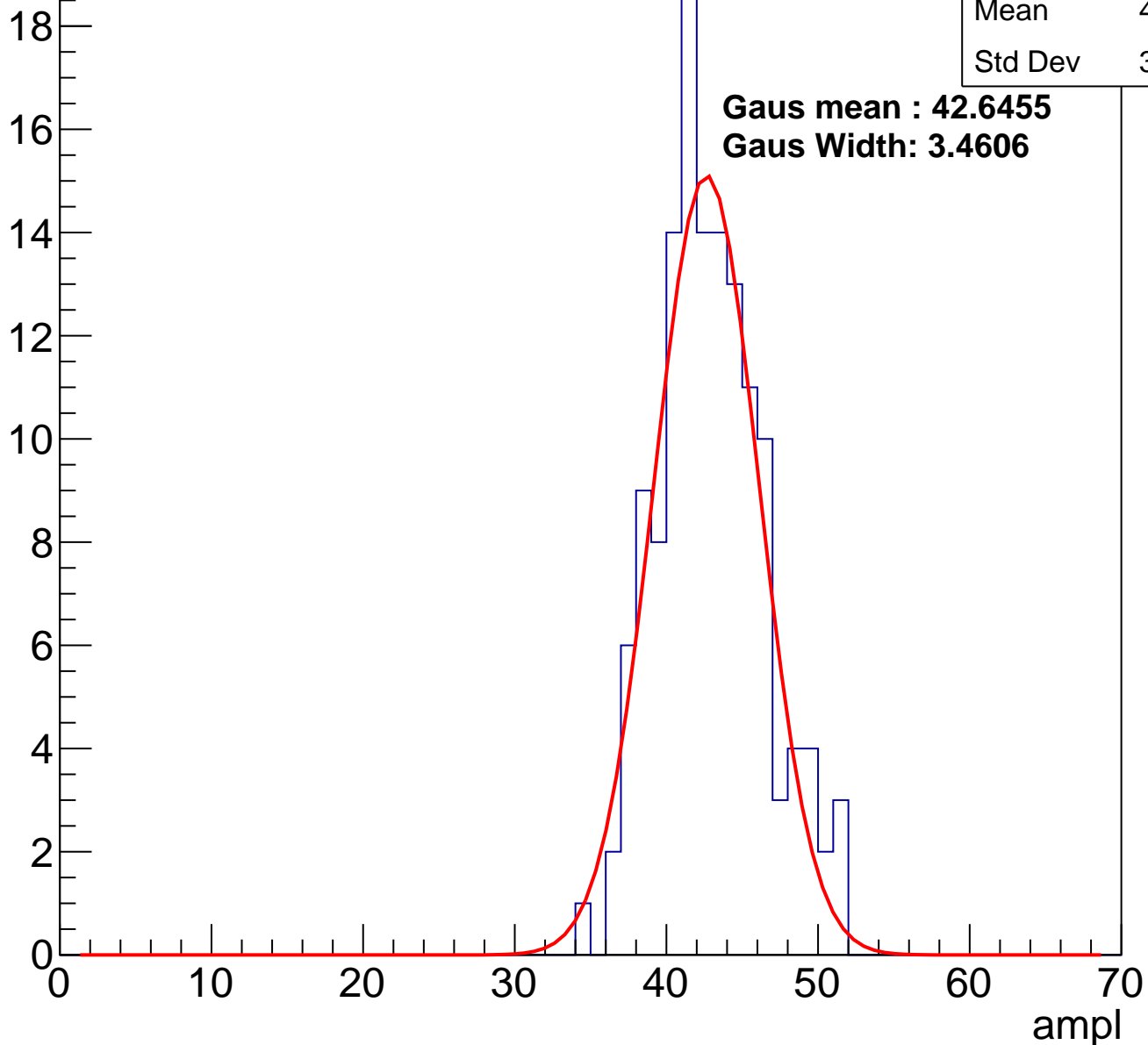
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	42.48
Std Dev	3.477

Gaus mean : 42.6455

Gaus Width: 3.4606

Entry



B0L101S, U24-ch74, adc3

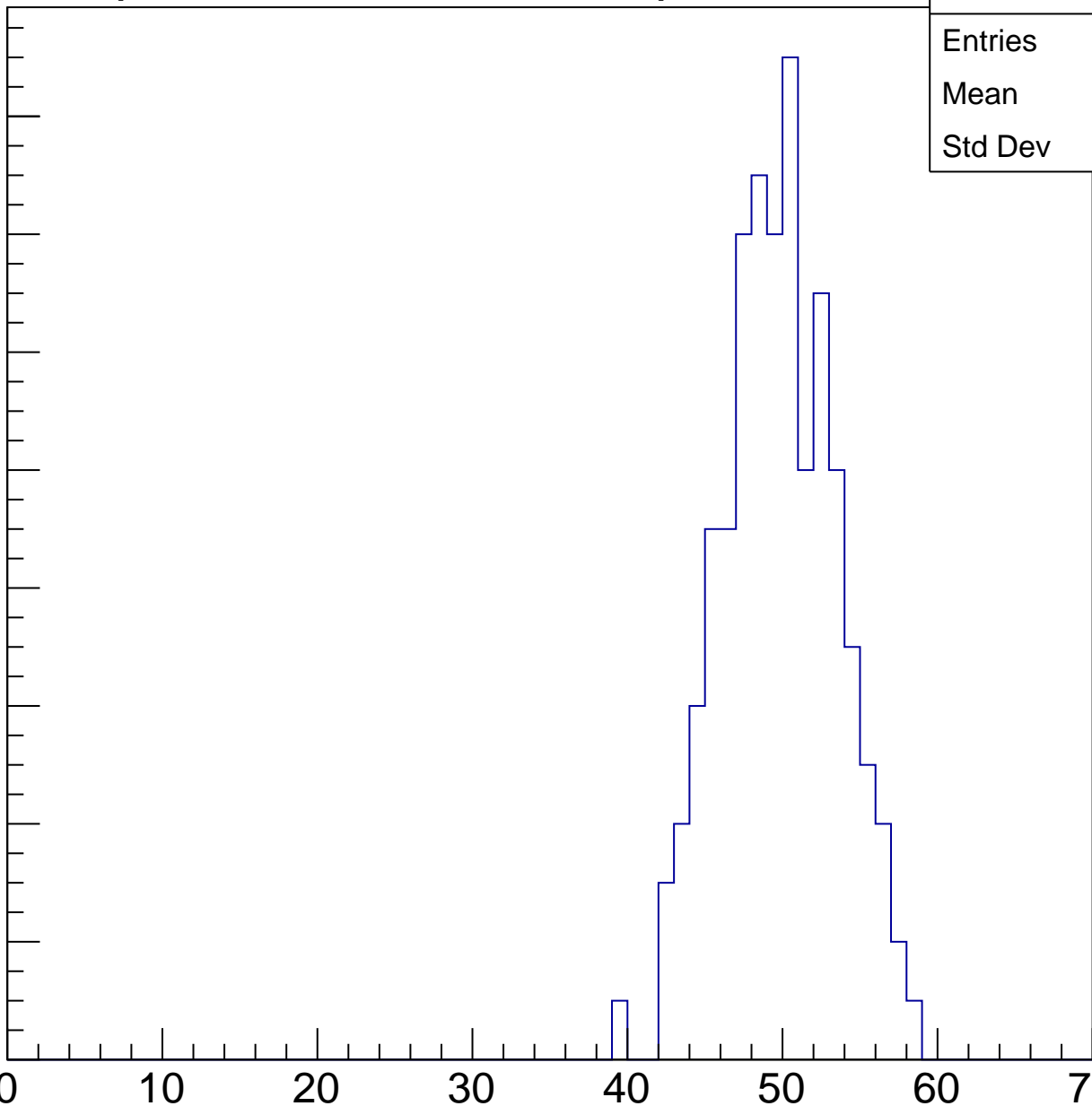
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	144
Mean	49.3
Std Dev	3.65

ampl



B0L101S, U24-ch74, adc4

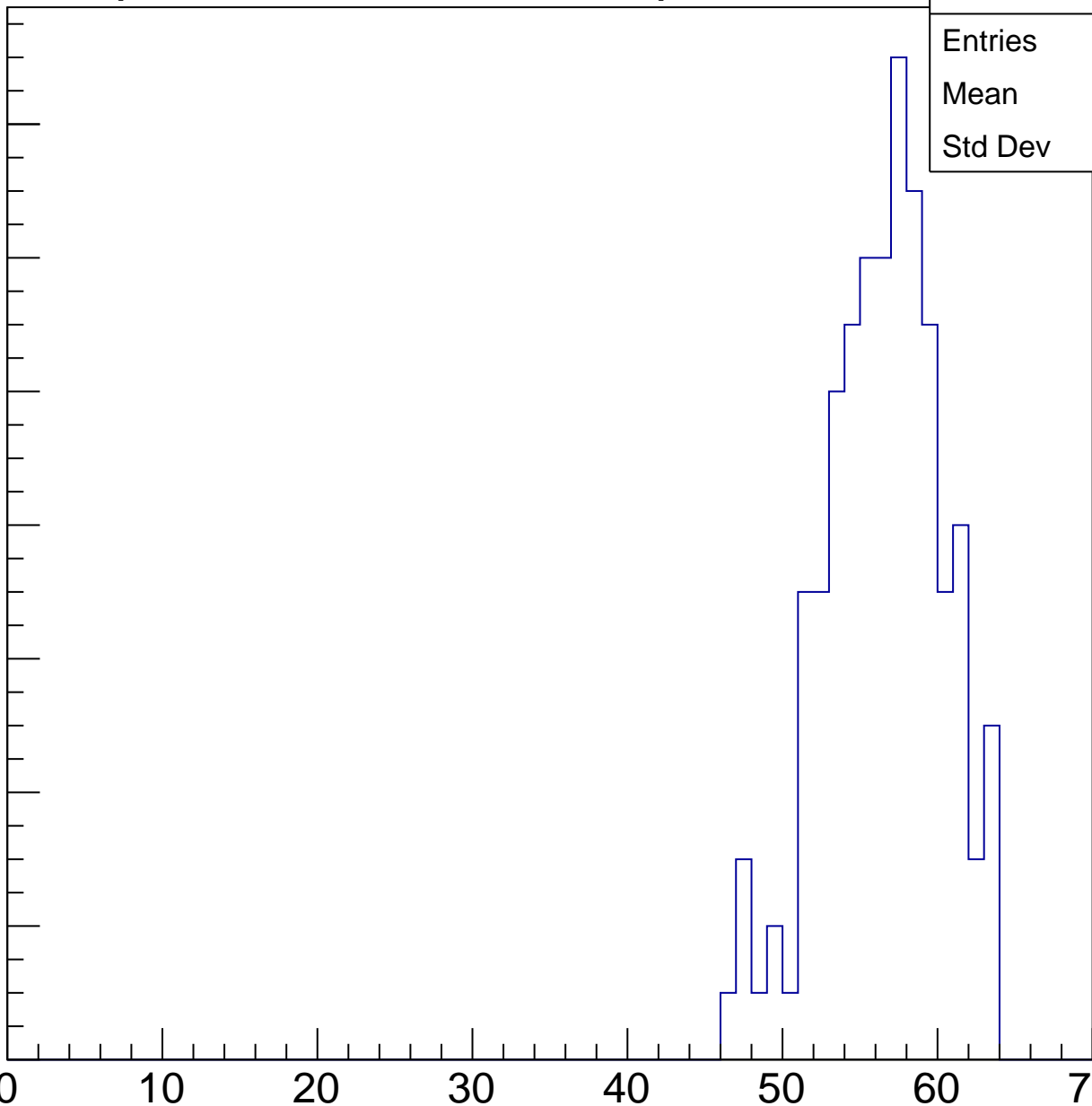
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	129
Mean	56.02
Std Dev	3.741

ampl



B0L101S, U24-ch74, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	69
Mean	59.12
Std Dev	7.556

ampl

0

10

20

30

40

50

60

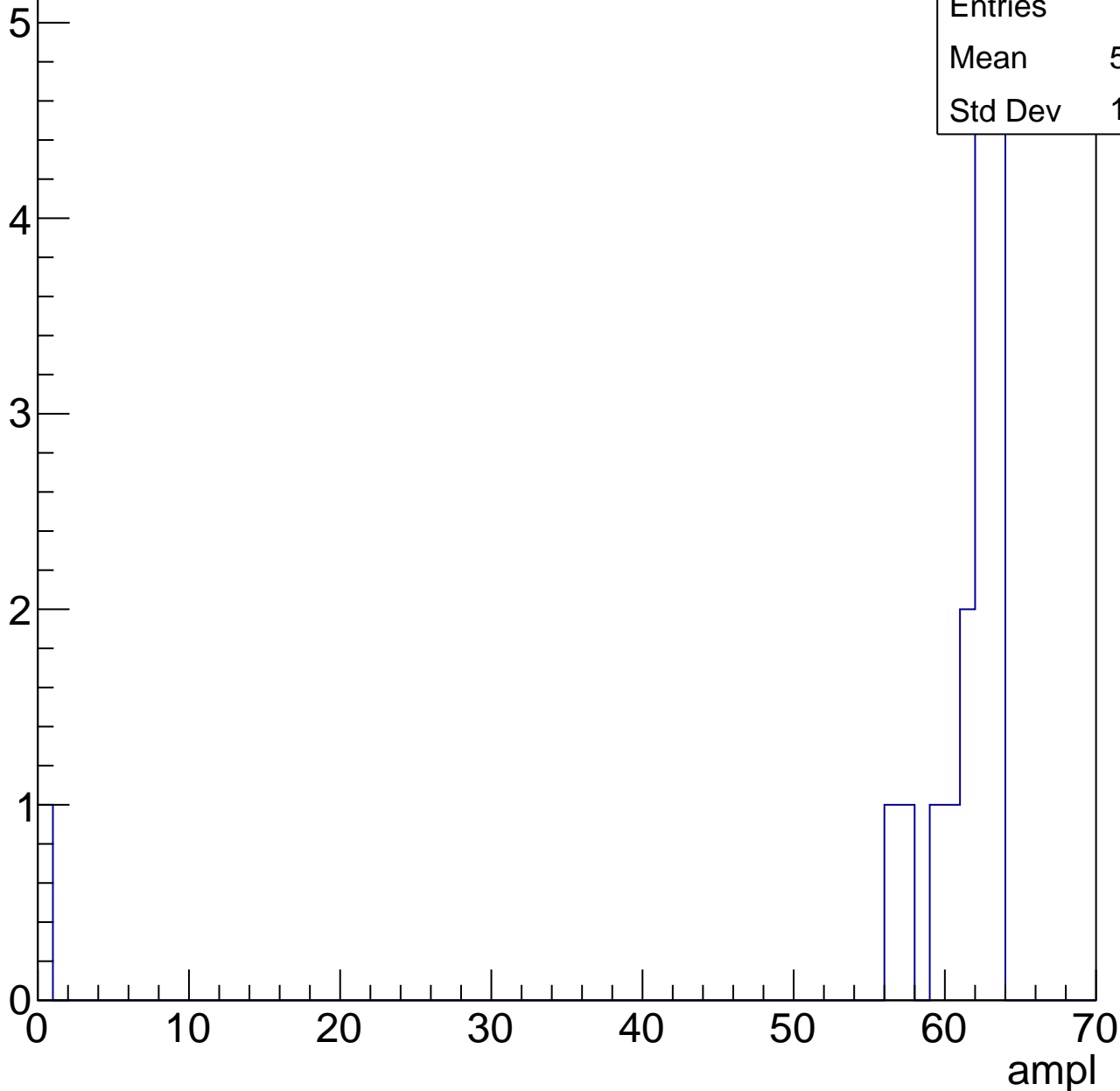
70

B0L101S, U24-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	57.59
Std Dev	14.54



B0L101S, U24-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch75, adc0

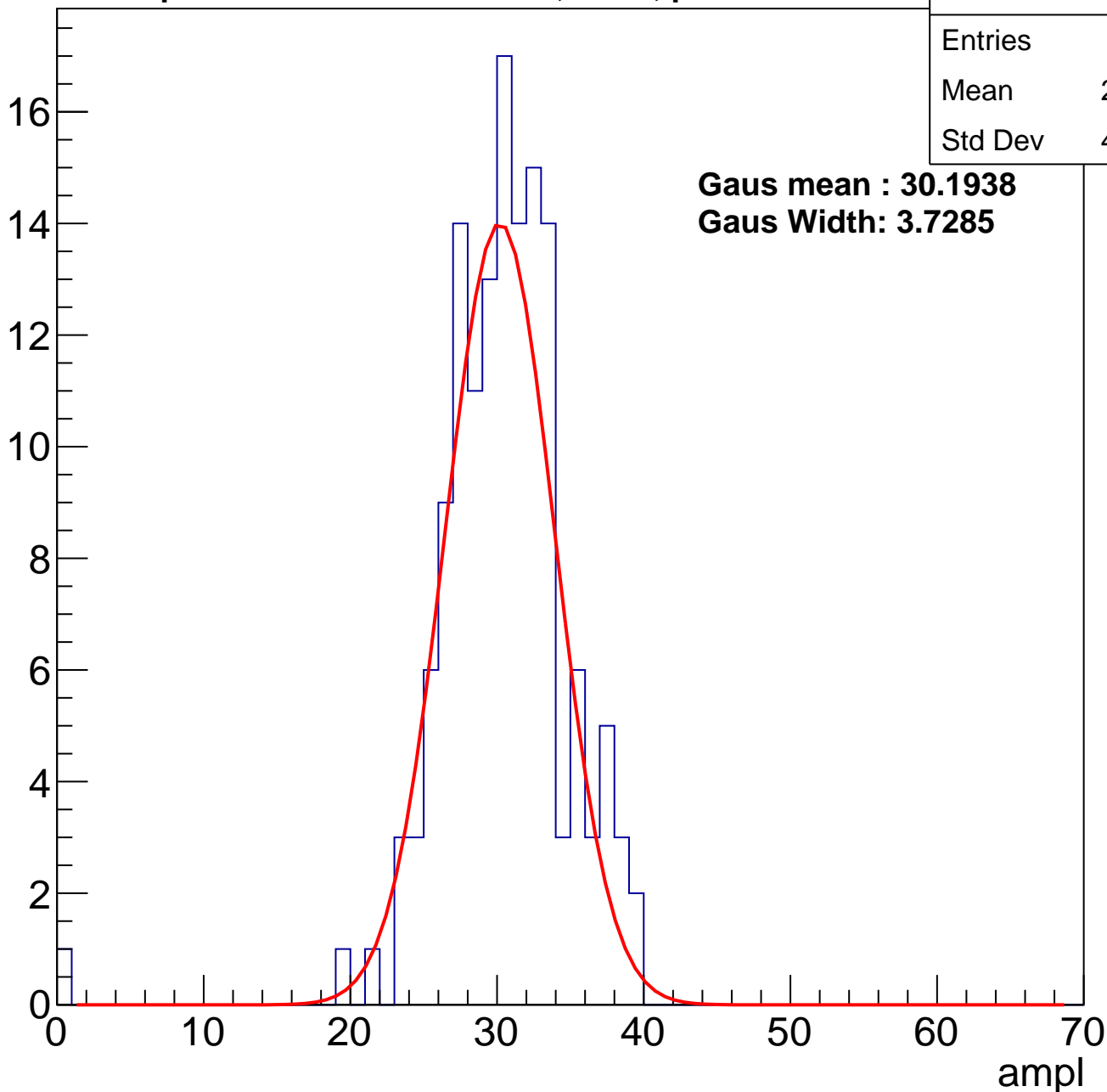
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	29.94
Std Dev	4.517

Gaus mean : 30.1938

Gaus Width: 3.7285

Entry



B0L101S, U24-ch75, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.67
Std Dev	3.632

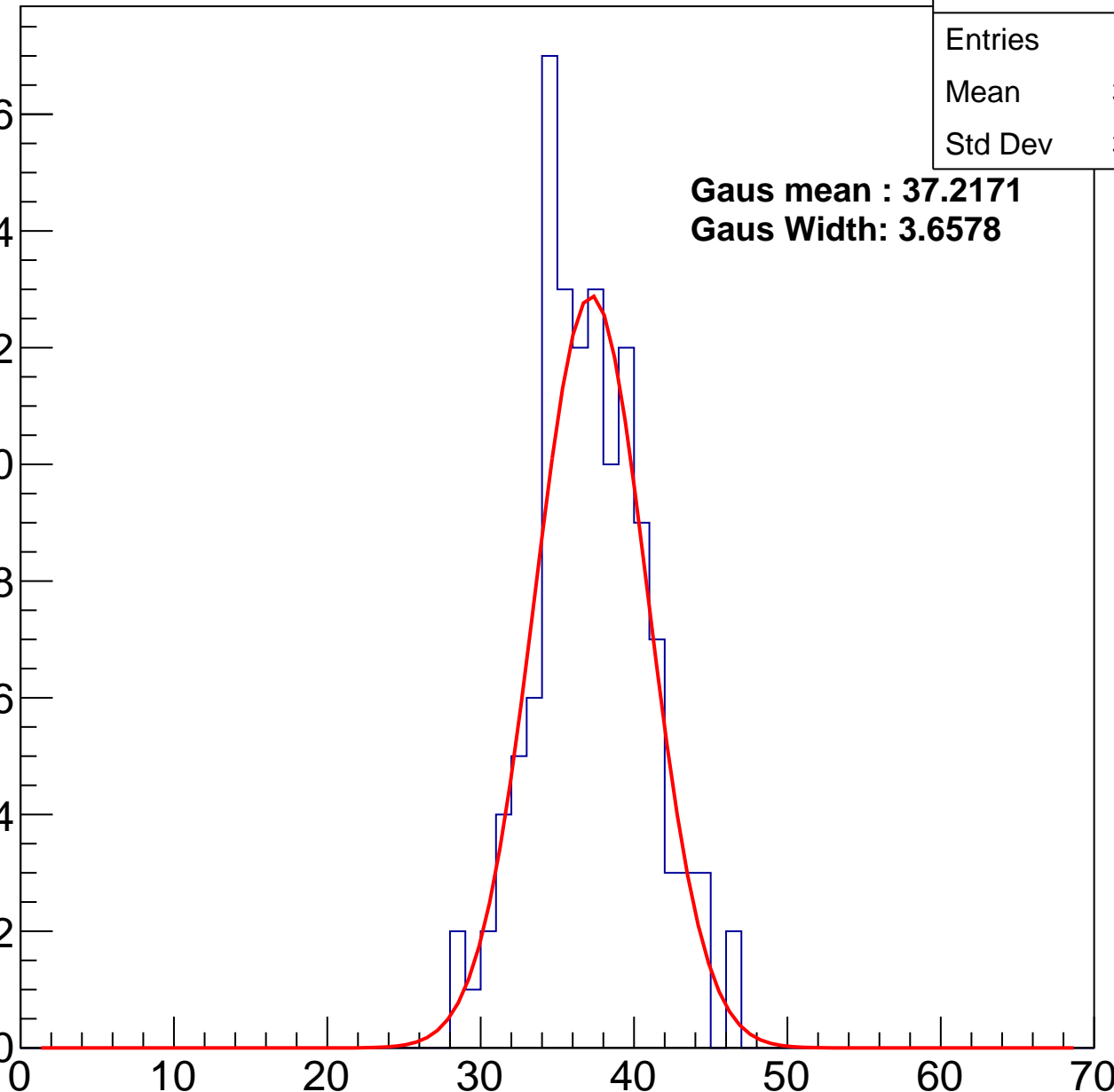
Gaus mean : 37.2171

Gaus Width: 3.6578

Entry

16
14
12
10
8
6
4
2
0

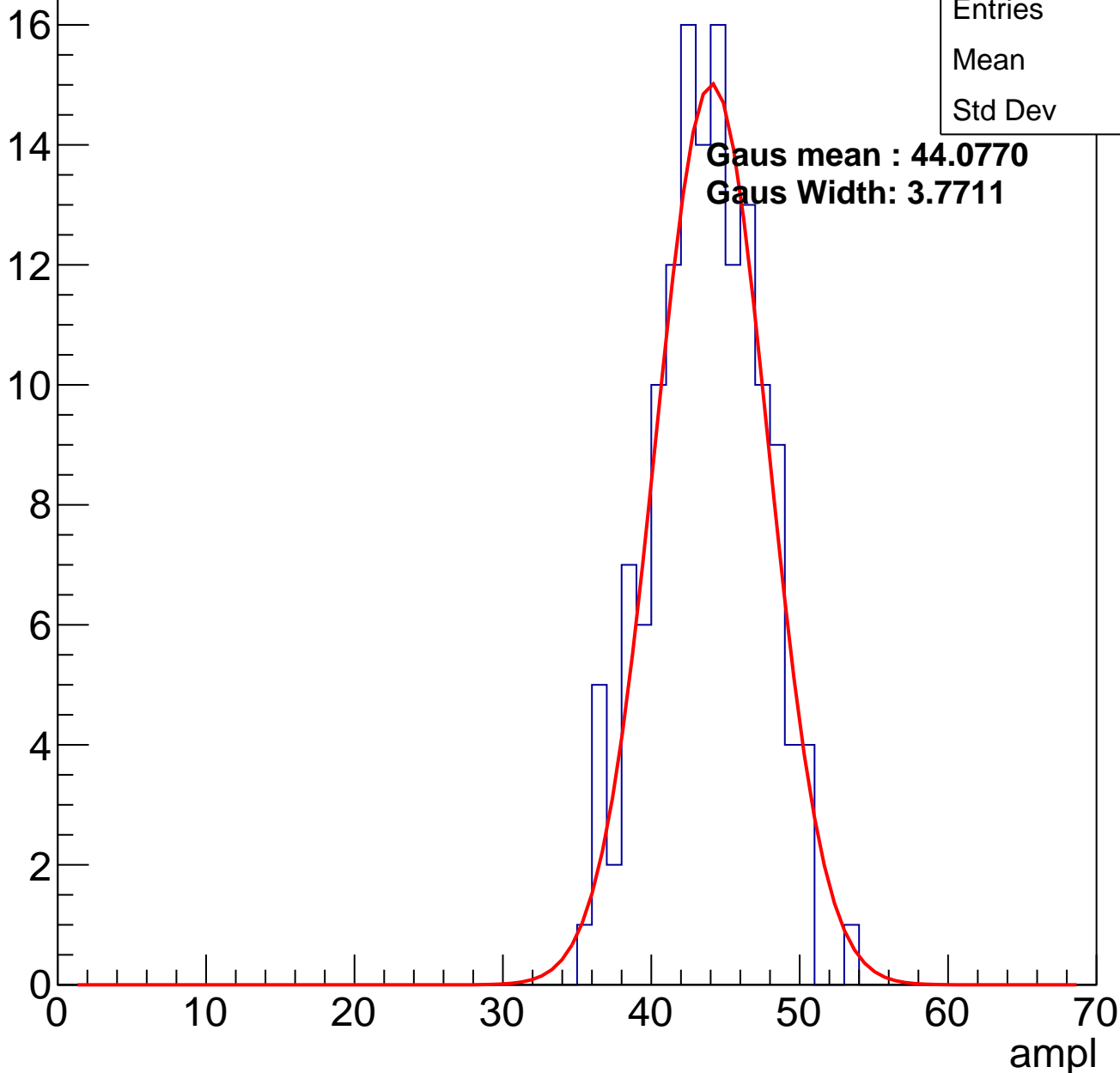
ampl



B0L101S, U24-ch75, adc2

calib_packv5_042523_0143.root, FC#1, port C1

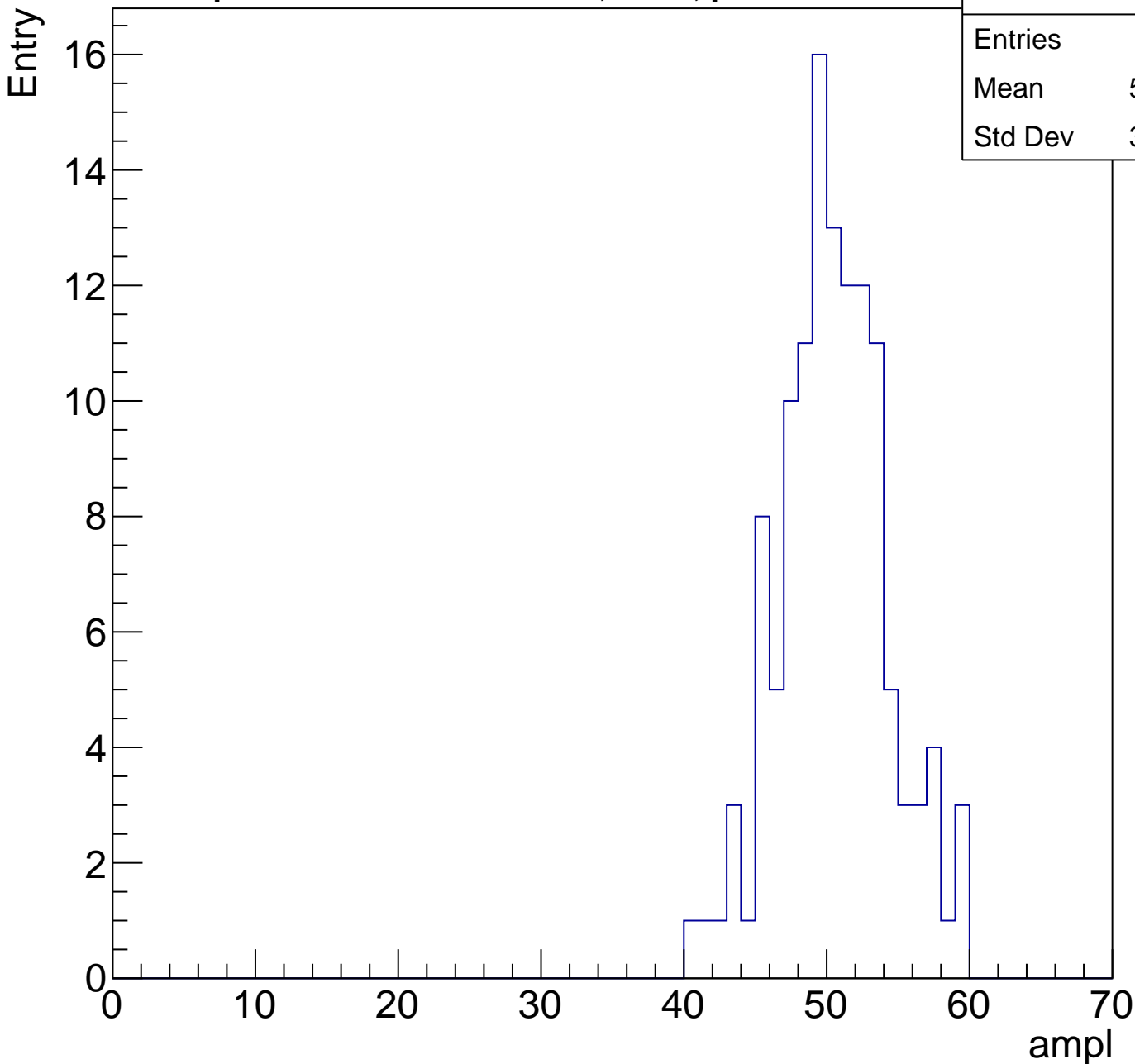
Entry



B0L101S, U24-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	50.02
Std Dev	3.792



B0L101S, U24-ch75, adc4

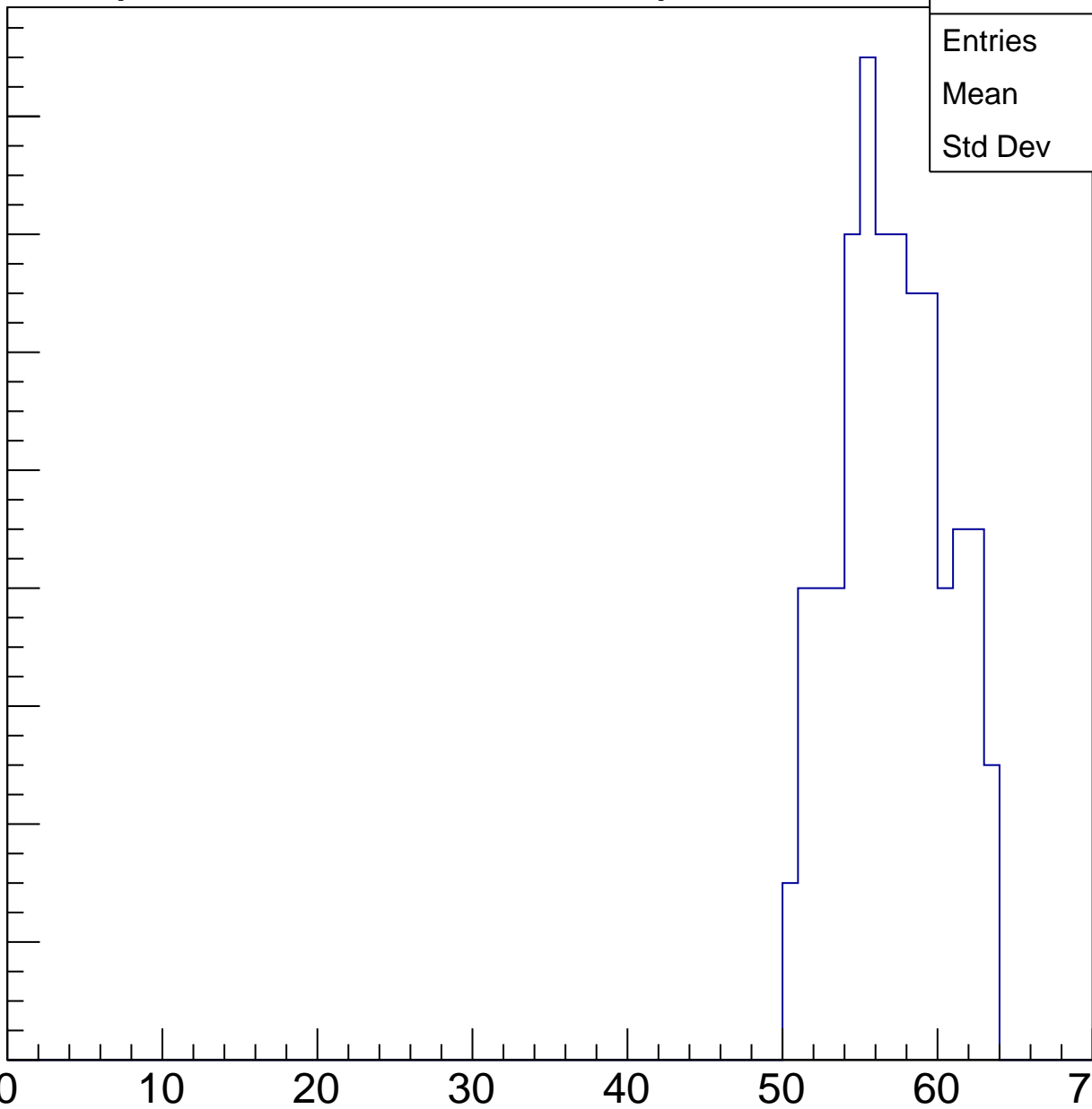
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	143
Mean	56.6
Std Dev	3.381

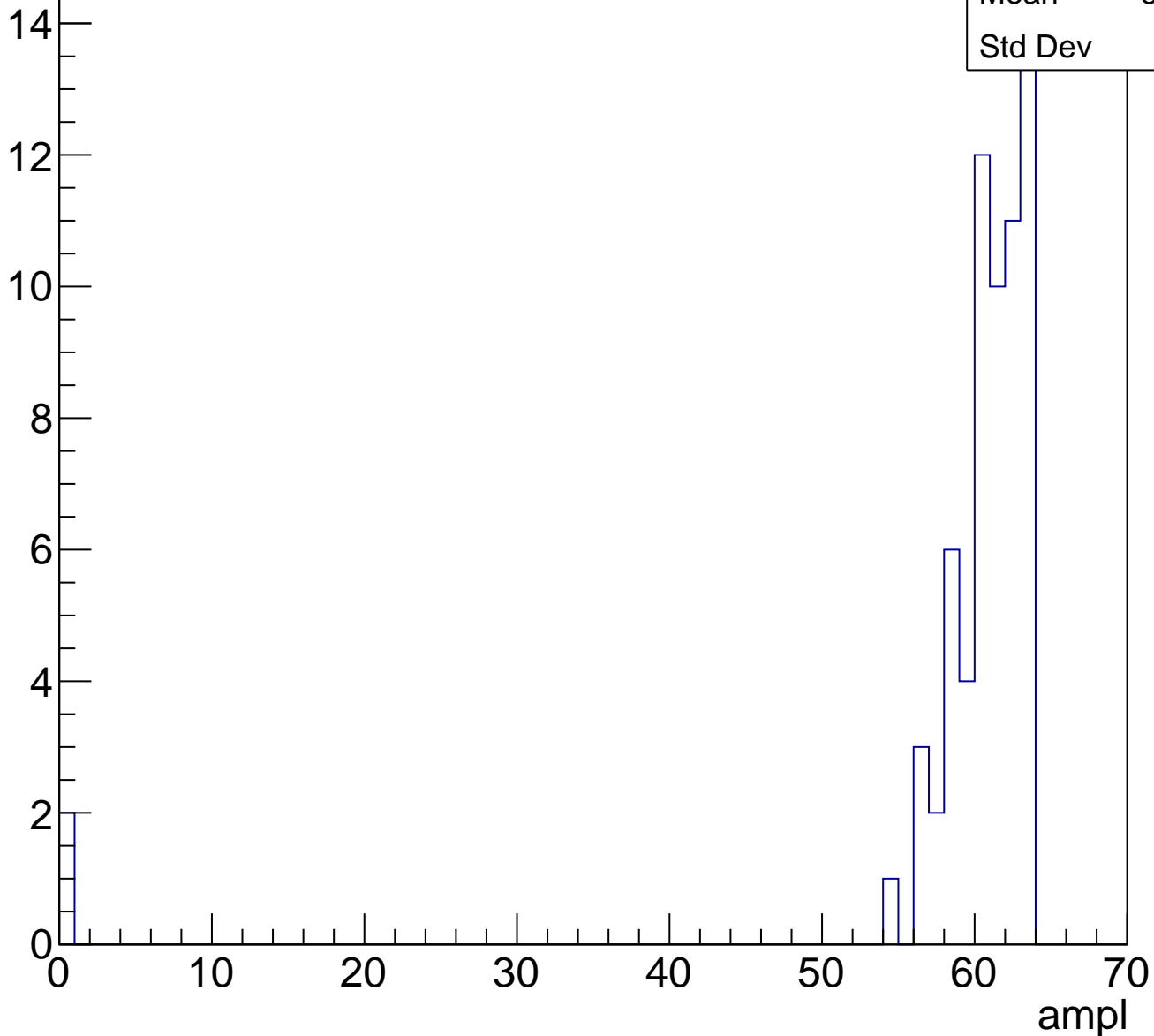
ampl



B0L101S, U24-ch75, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch76, adc0

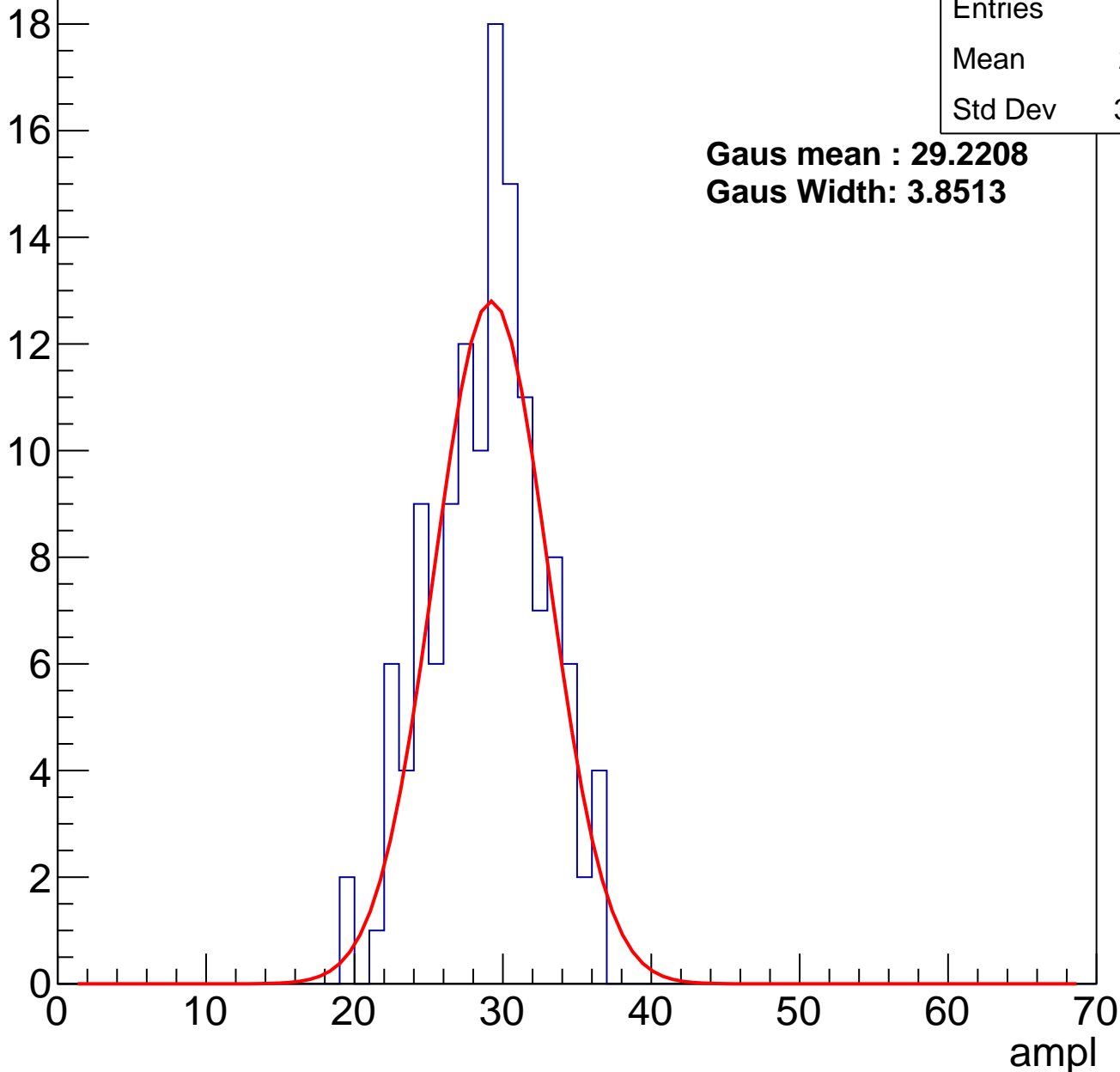
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	28.51
Std Dev	3.719

Gaus mean : 29.2208

Gaus Width: 3.8513

Entry



B0L101S, U24-ch76, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	35.63
Std Dev	4.077

Gaus mean : 36.5663

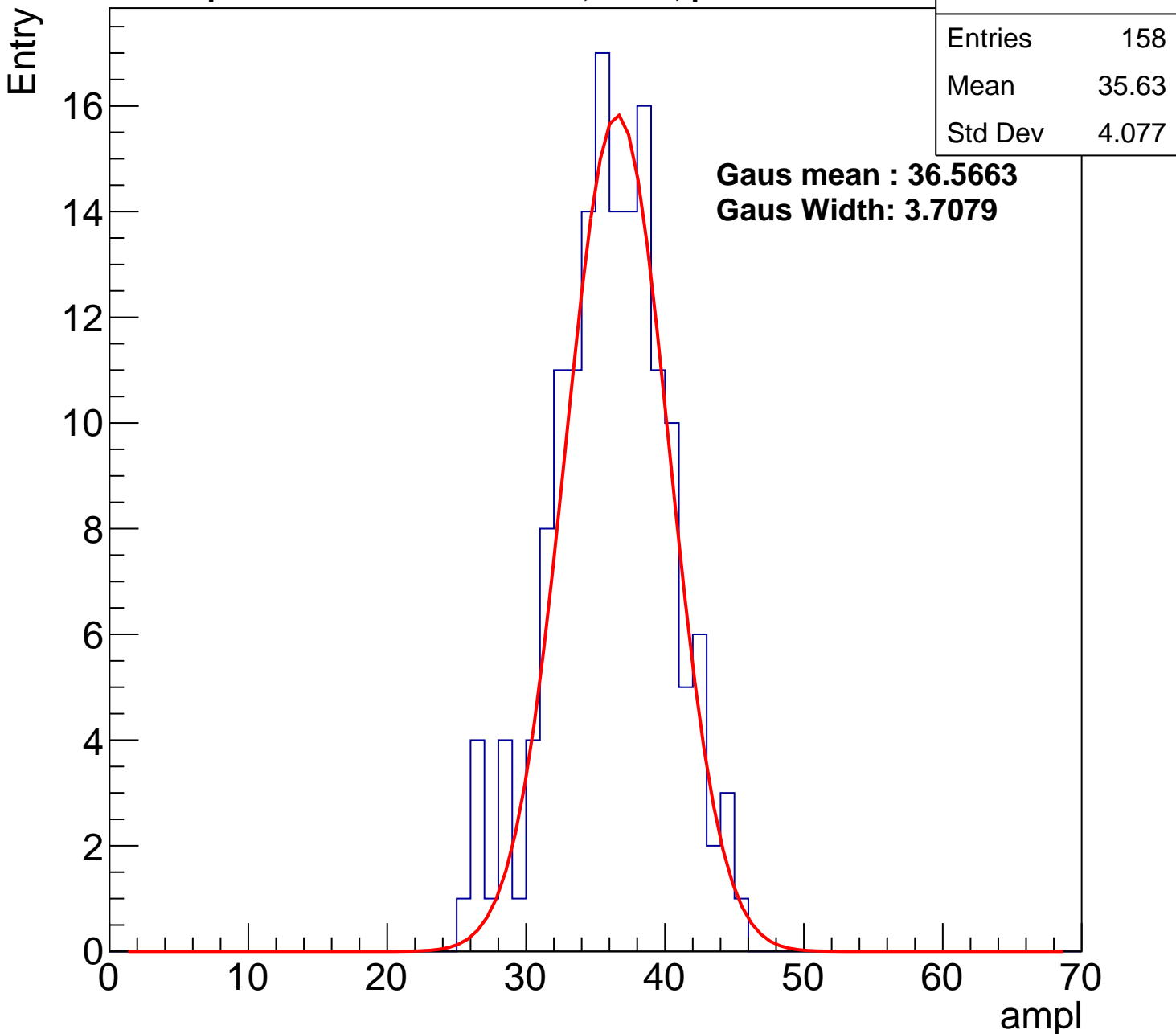
Gaus Width: 3.7079

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch76, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

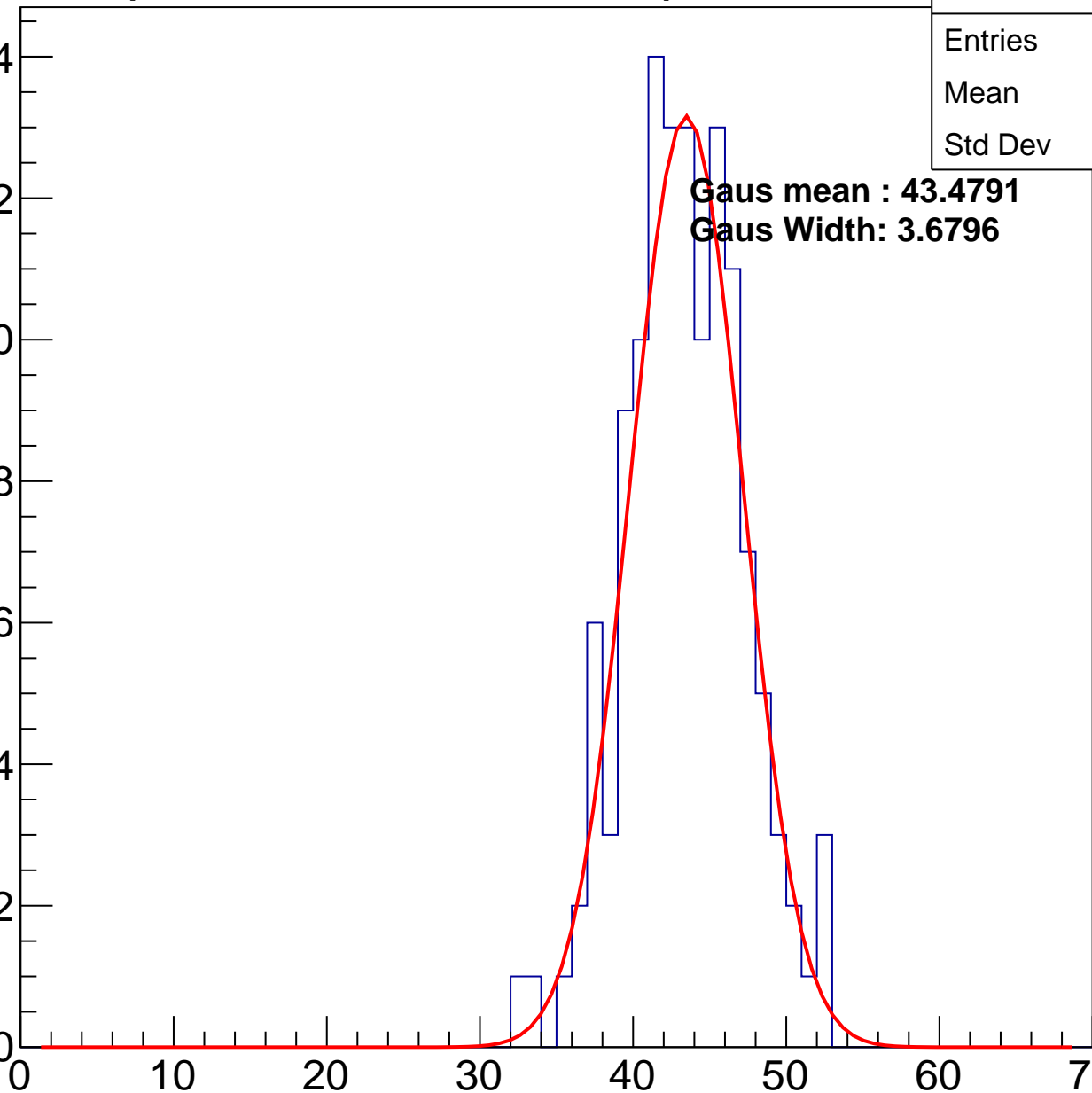
14
12
10
8
6
4
2
0

Entries	128
Mean	42.91
Std Dev	3.839

Gaus mean : 43.4791

Gaus Width: 3.6796

ampl



B0L101S, U24-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	49.34
Std Dev	3.737

Entry

16

14

12

10

8

6

4

2

0

0

10

20

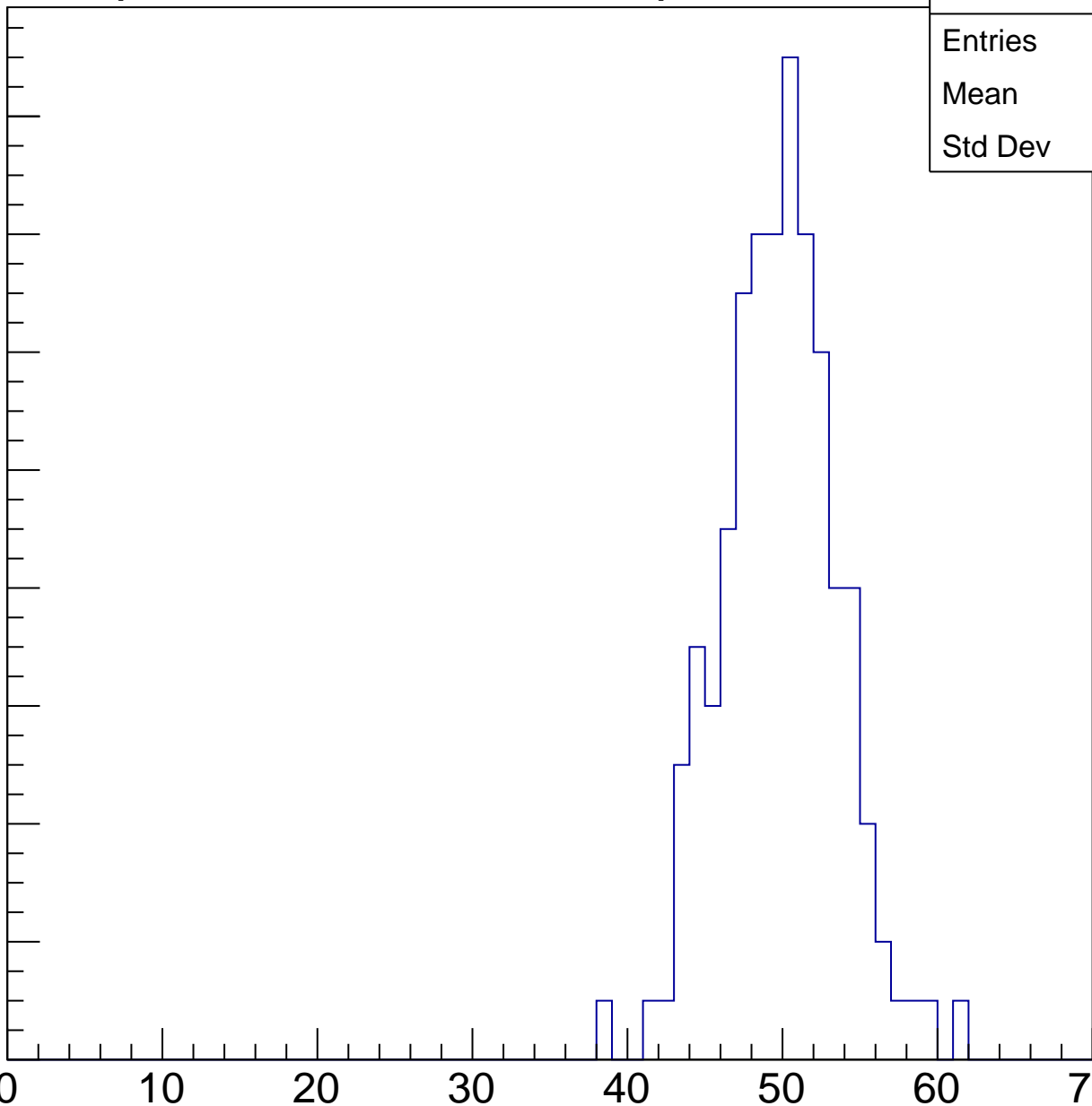
30

40

50

60

ampl

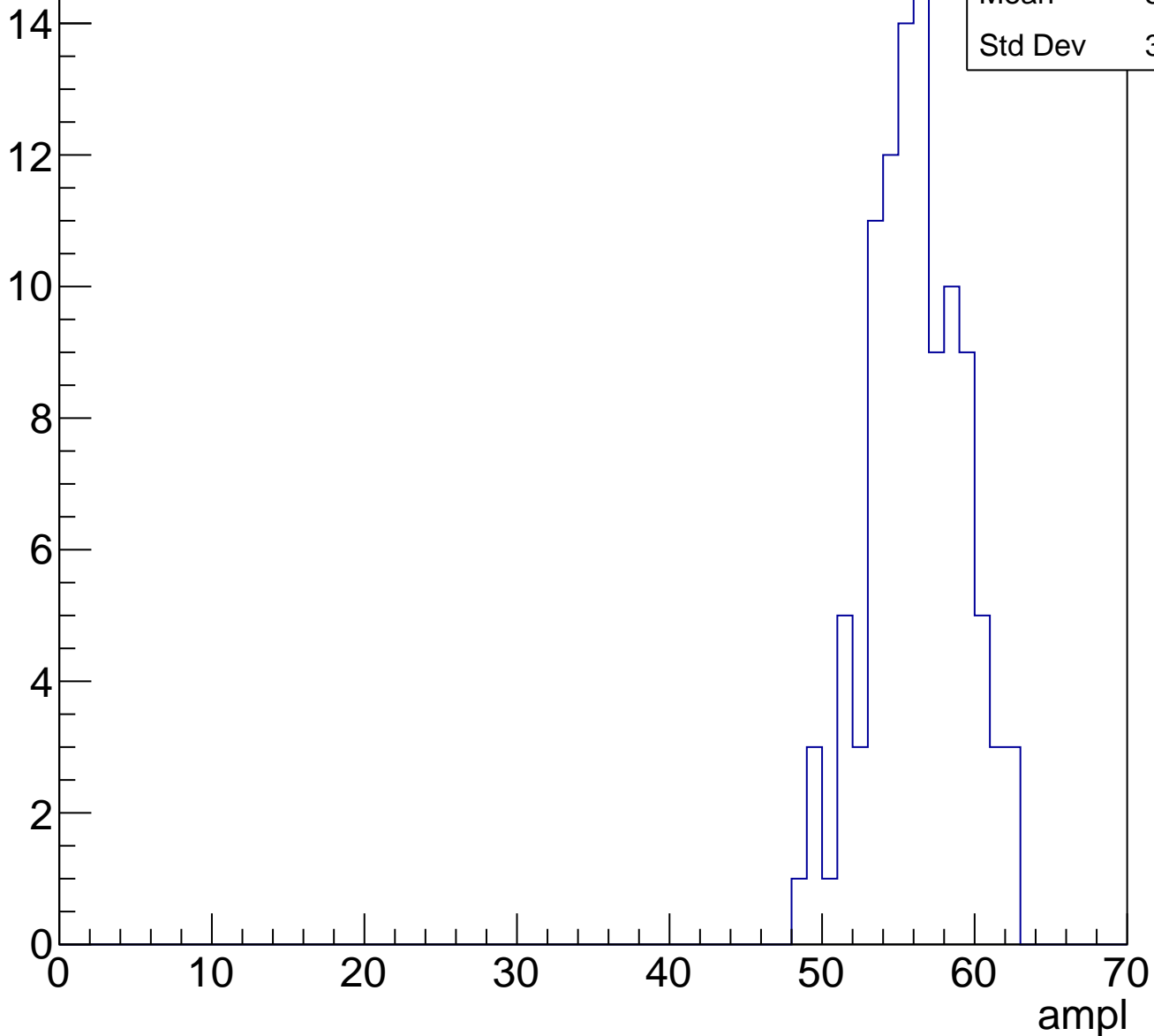


B0L101S, U24-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	55.67
Std Dev	3.043

Entry



B0L101S, U24-ch76, adc5

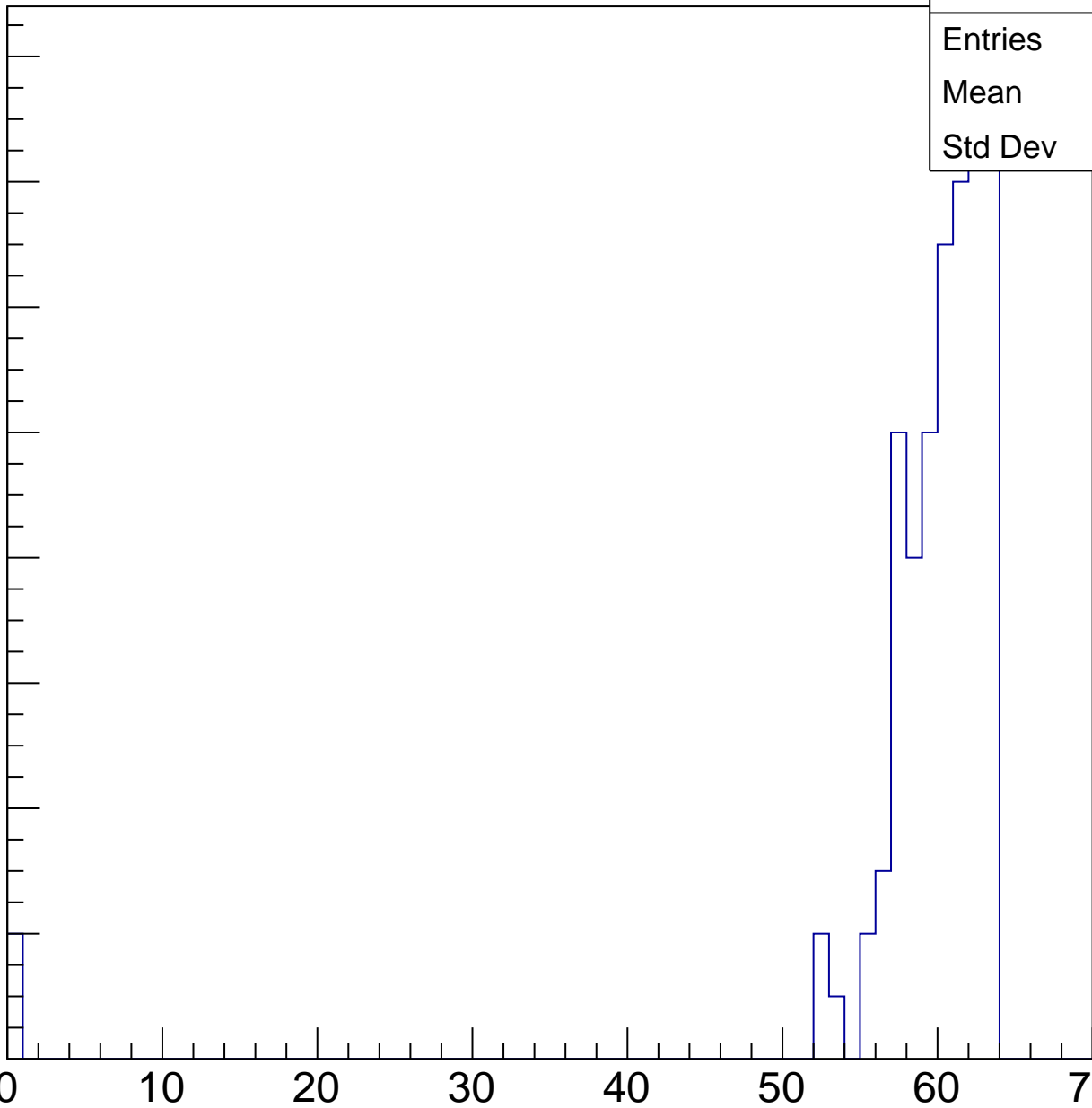
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.69
Std Dev	8.888

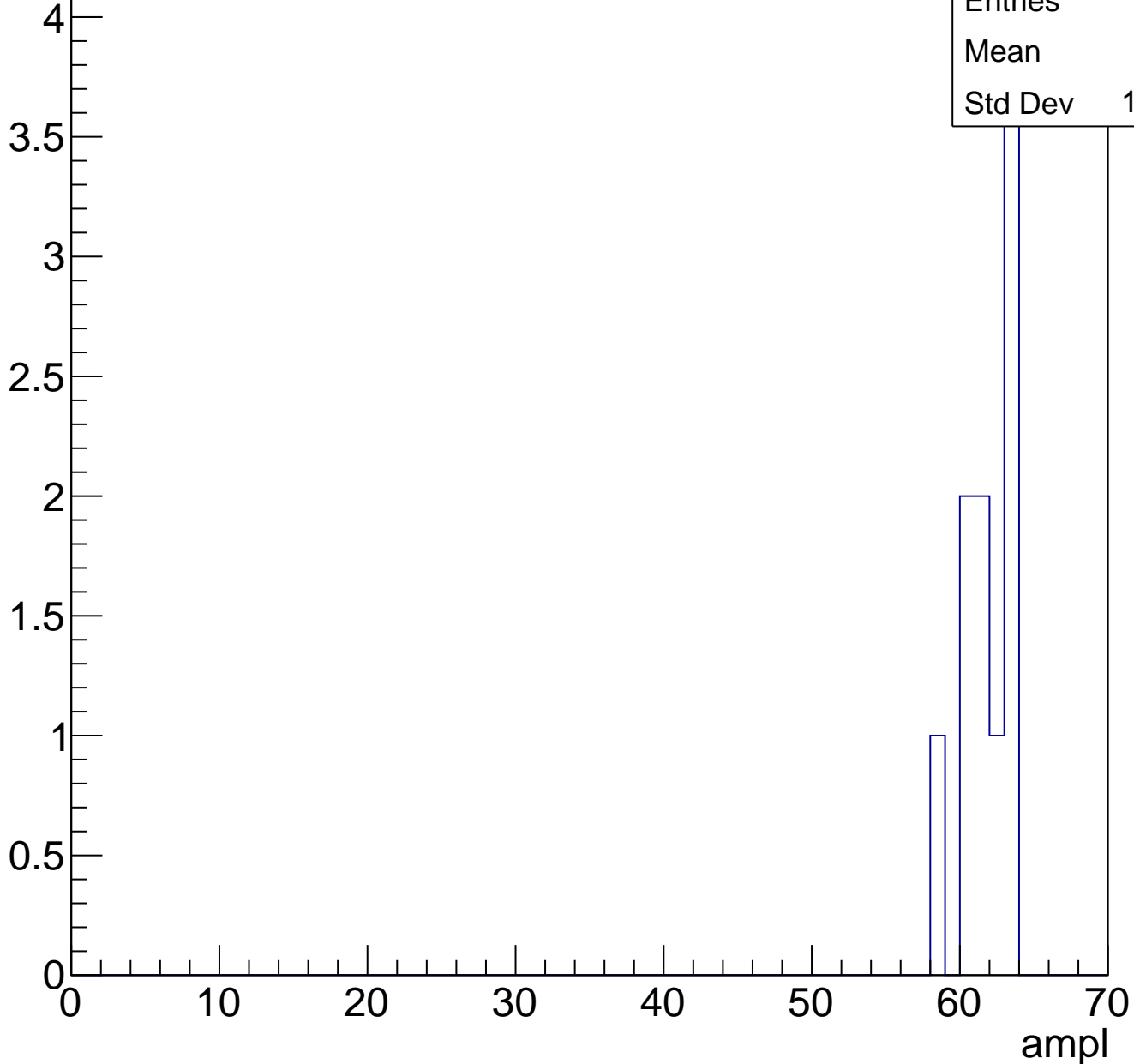
ampl



B0L101S, U24-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch77, adc0

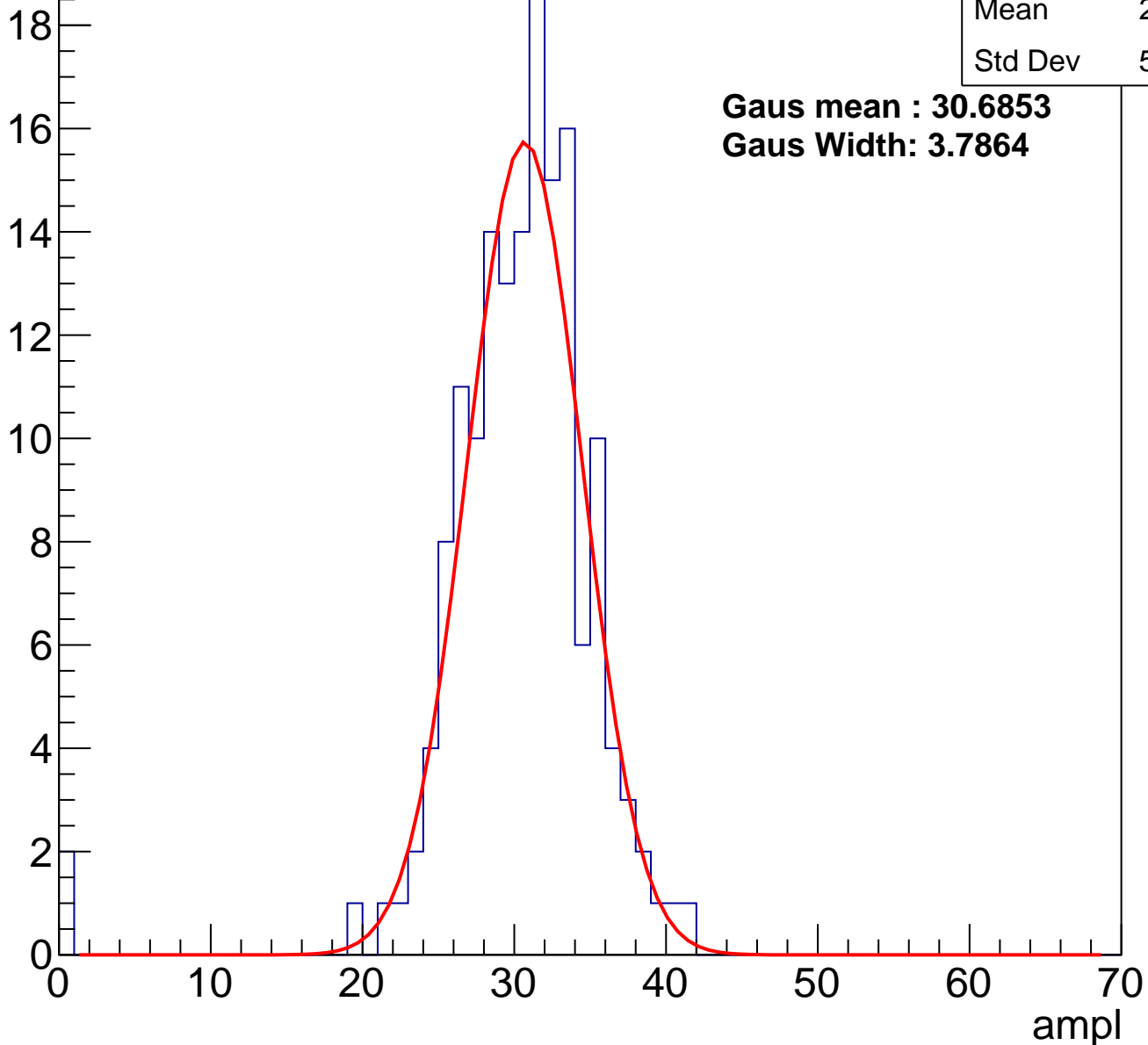
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	29.88
Std Dev	5.097

Gaus mean : 30.6853

Gaus Width: 3.7864

Entry



B0L101S, U24-ch77, adc1

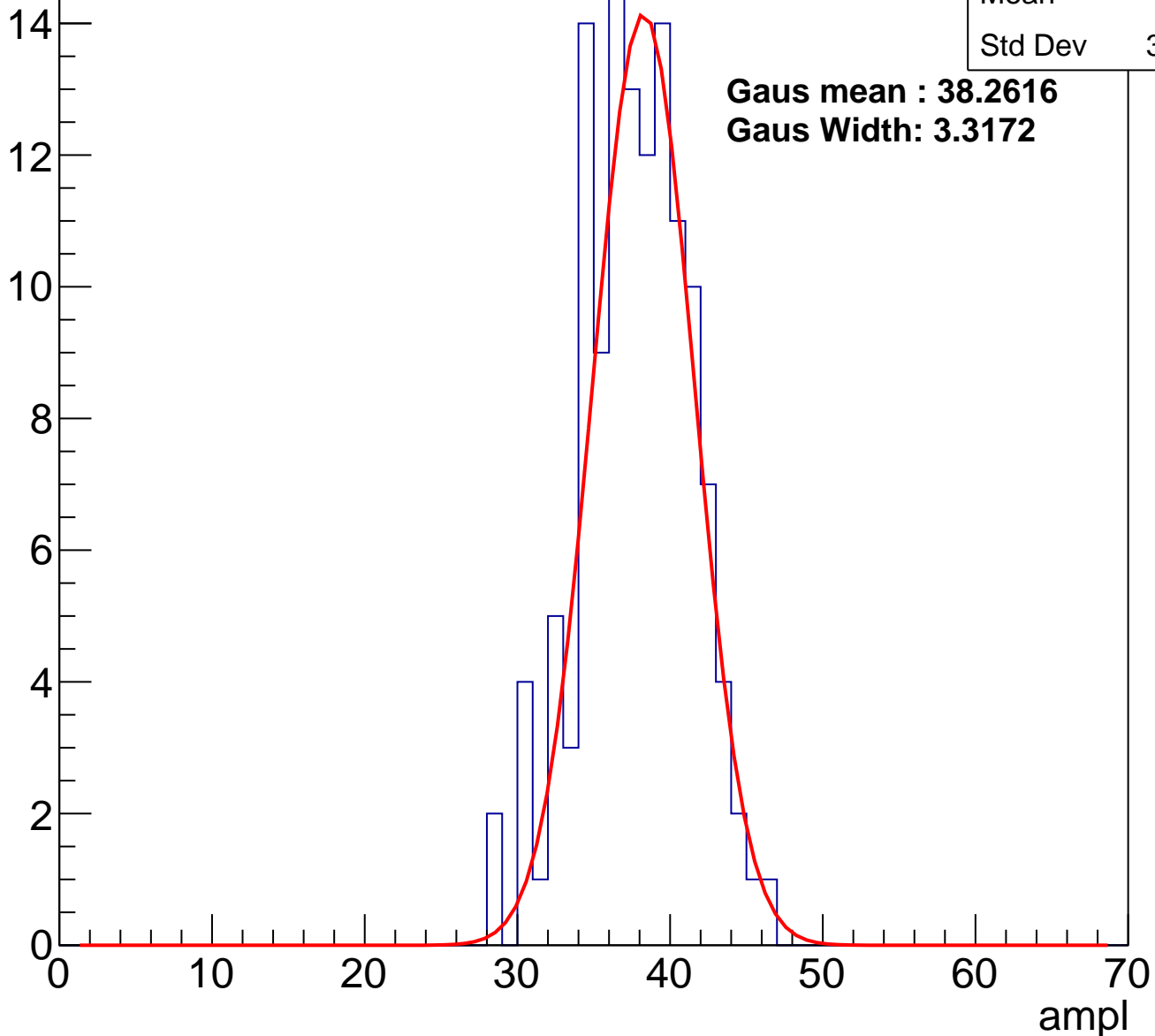
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	37.3
Std Dev	3.559

Gaus mean : 38.2616

Gaus Width: 3.3172

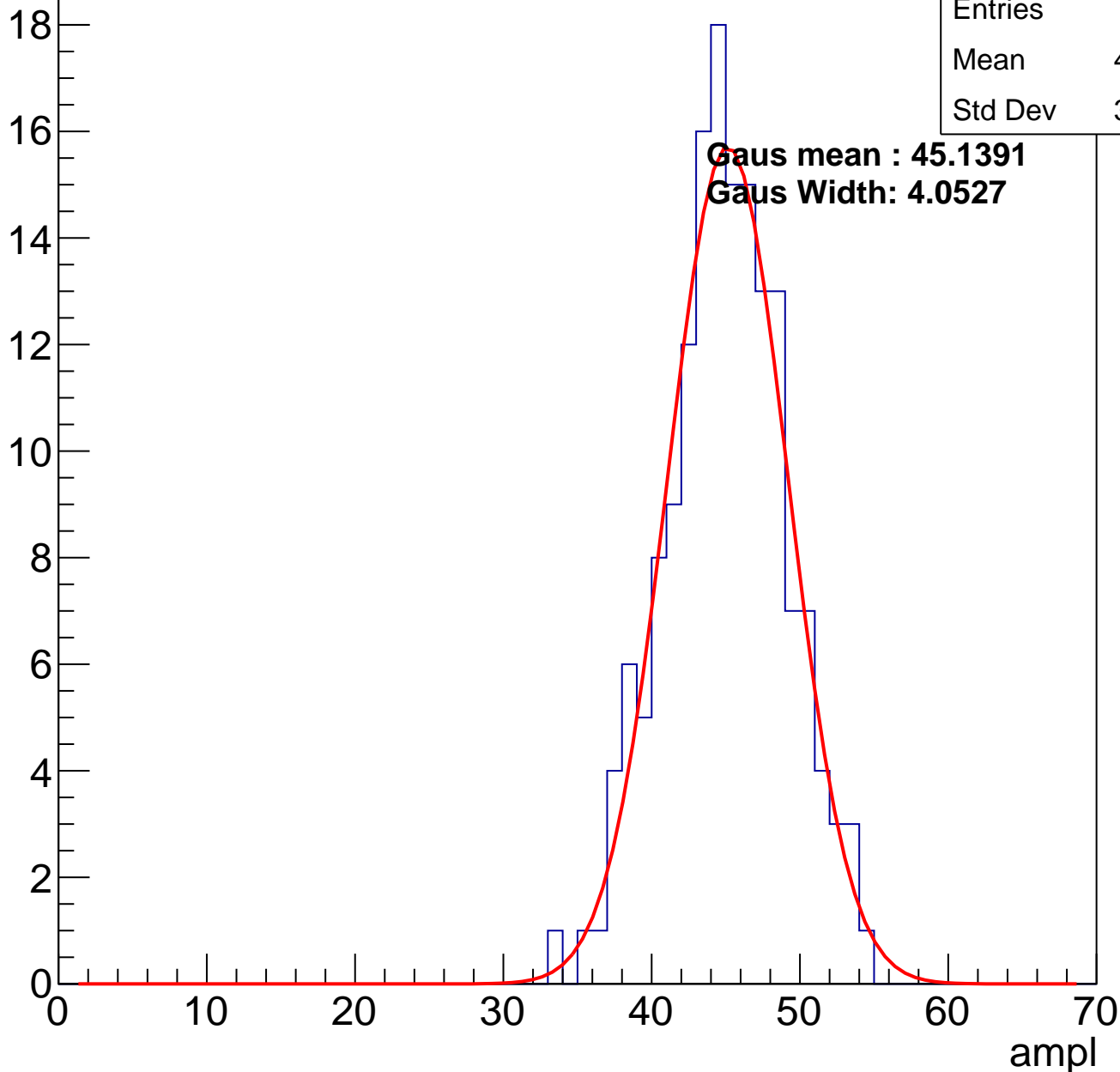
Entry



B0L101S, U24-ch77, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

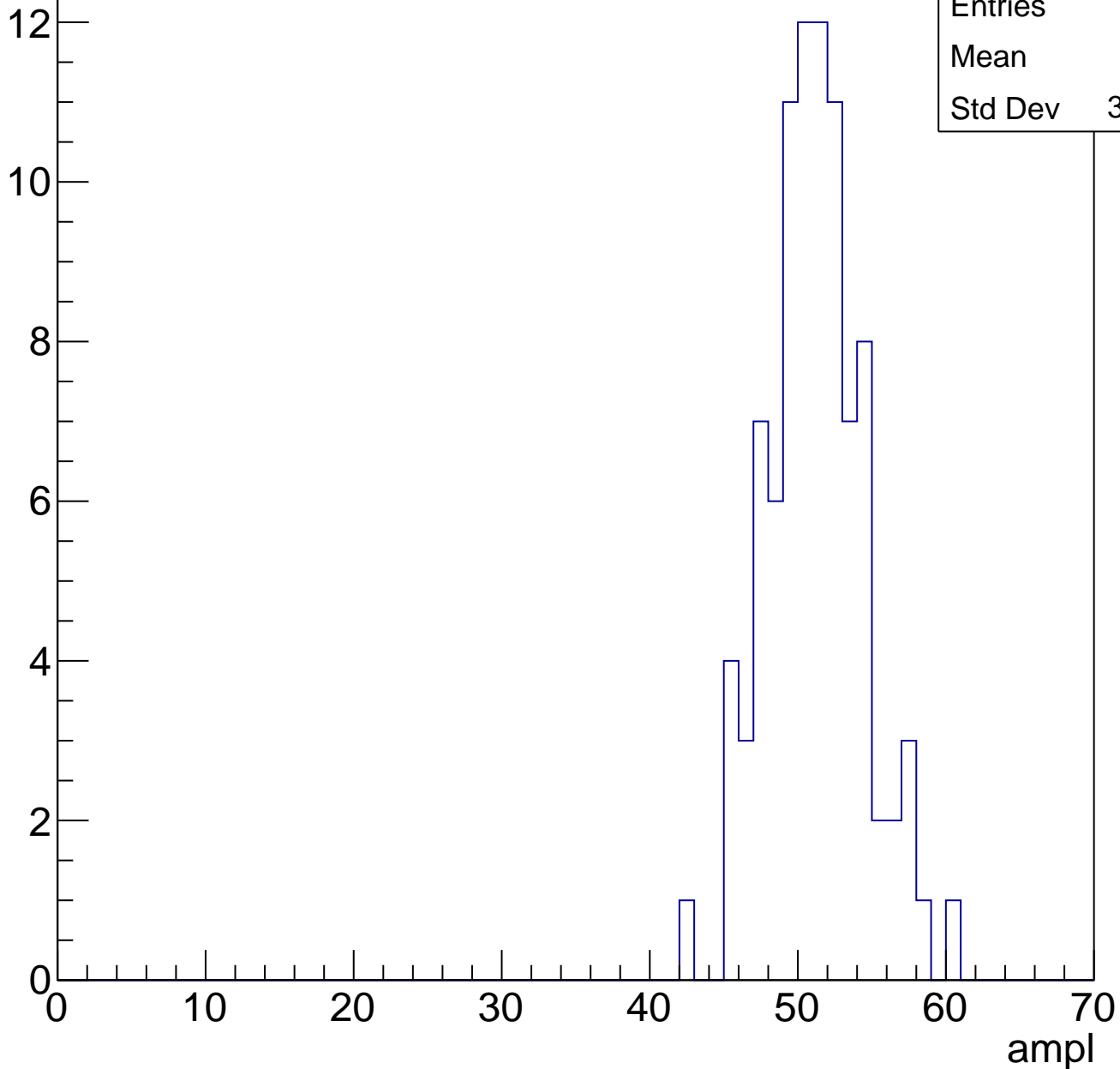


B0L101S, U24-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	91
Mean	50.7
Std Dev	3.222

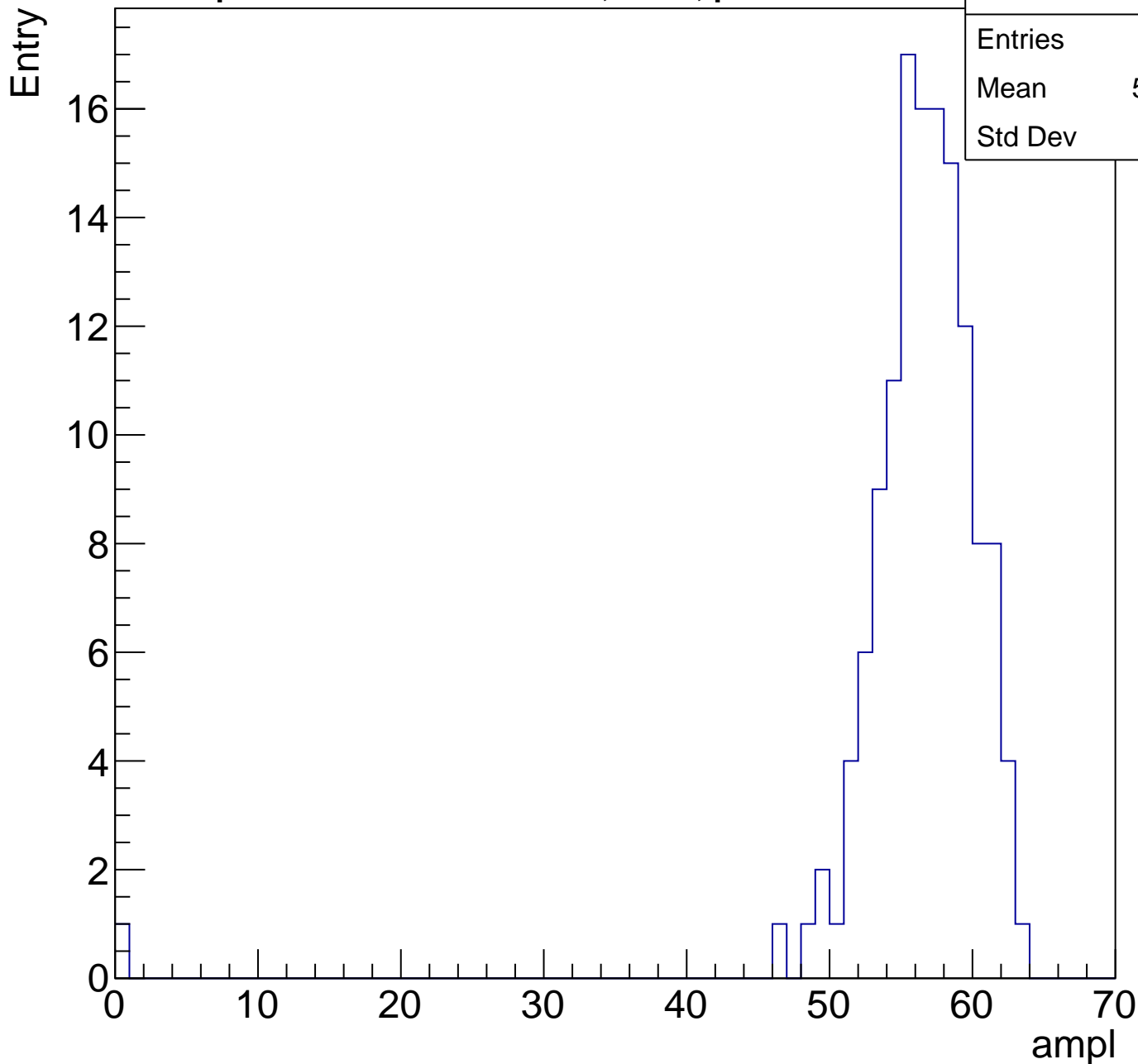
Entry



B0L101S, U24-ch77, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	55.86
Std Dev	5.8



B0L101S, U24-ch77, adc5

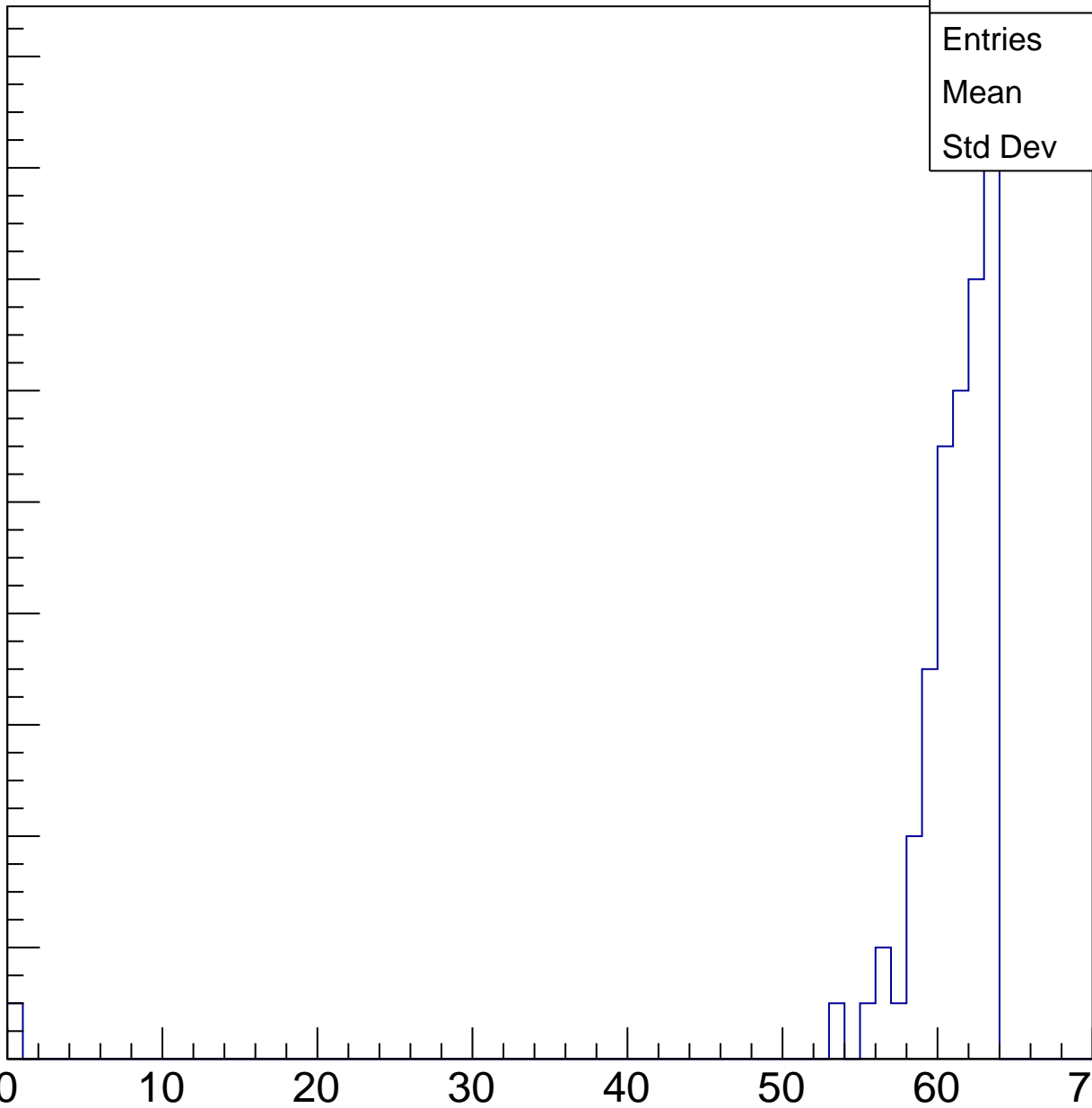
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	72
Mean	59.94
Std Dev	7.423

ampl



B0L101S, U24-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

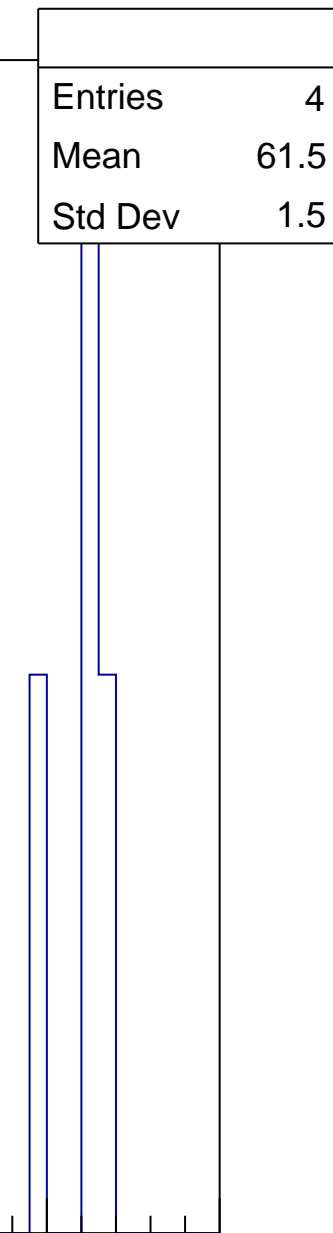
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.5
Std Dev	1.5

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch78, adc0

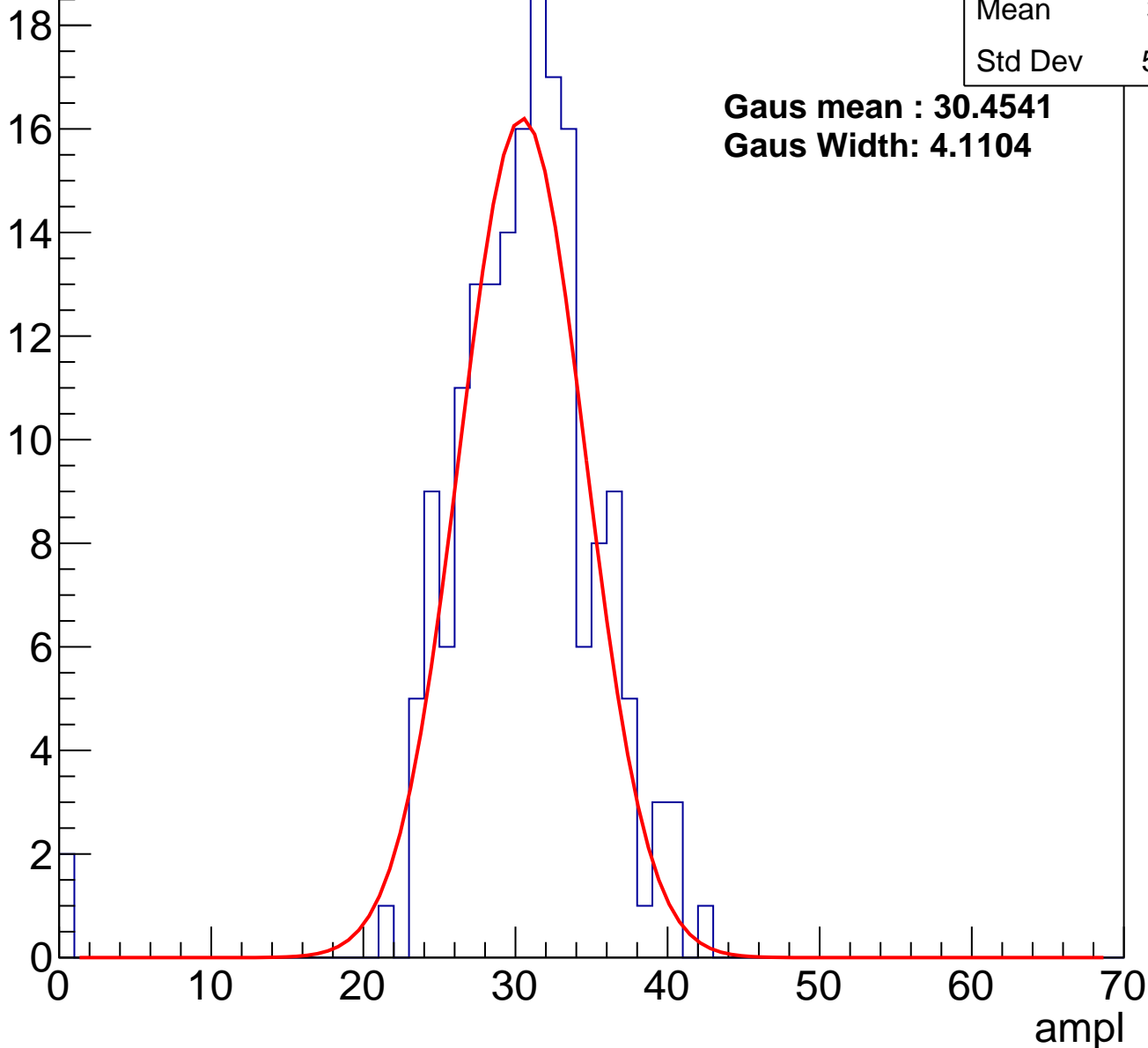
calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	30.11
Std Dev	5.195

Gaus mean : 30.4541

Gaus Width: 4.1104

Entry



B0L101S, U24-ch78, adc1

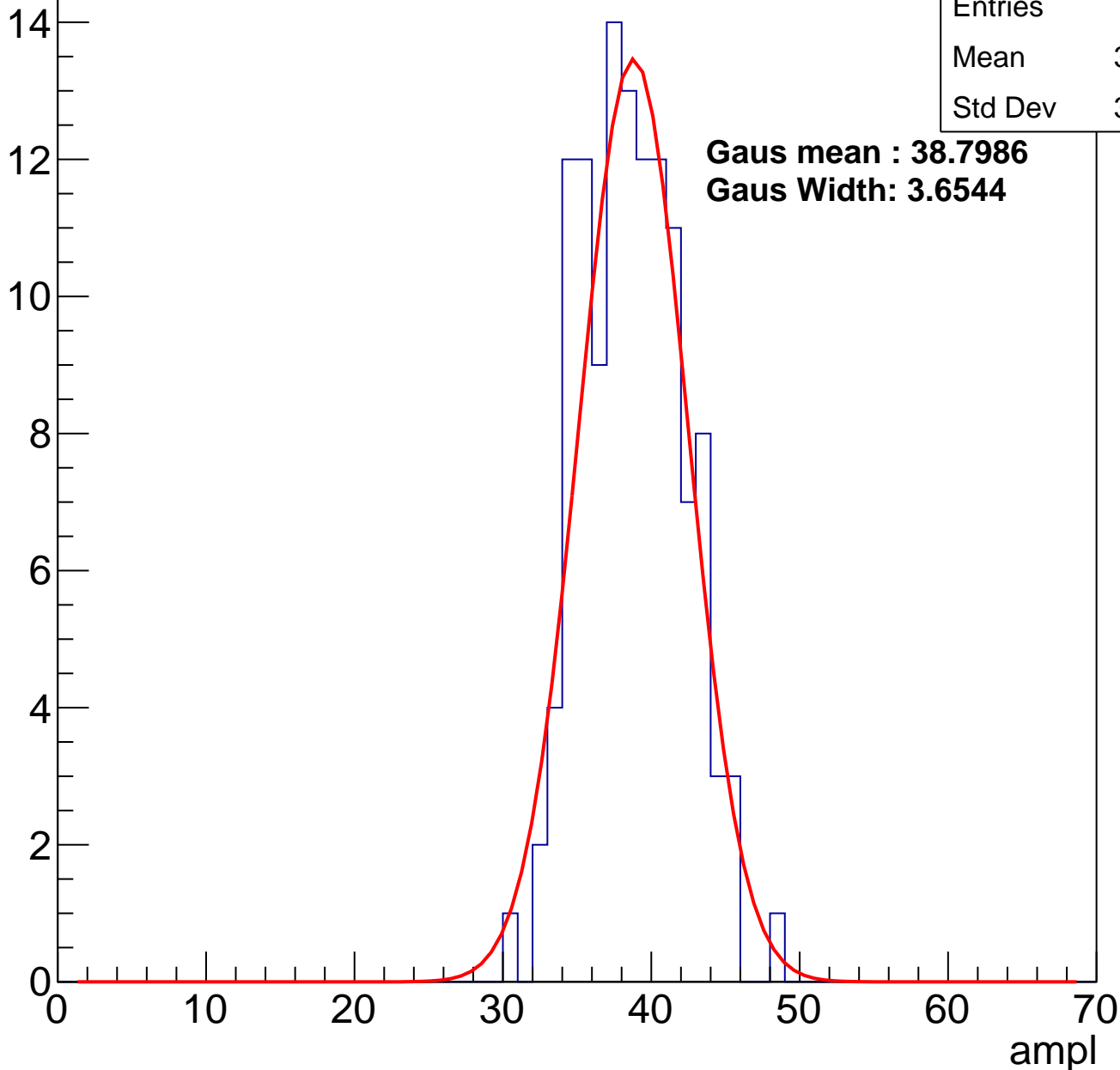
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	38.24
Std Dev	3.366

Gaus mean : 38.7986

Gaus Width: 3.6544

Entry



B0L101S, U24-ch78, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

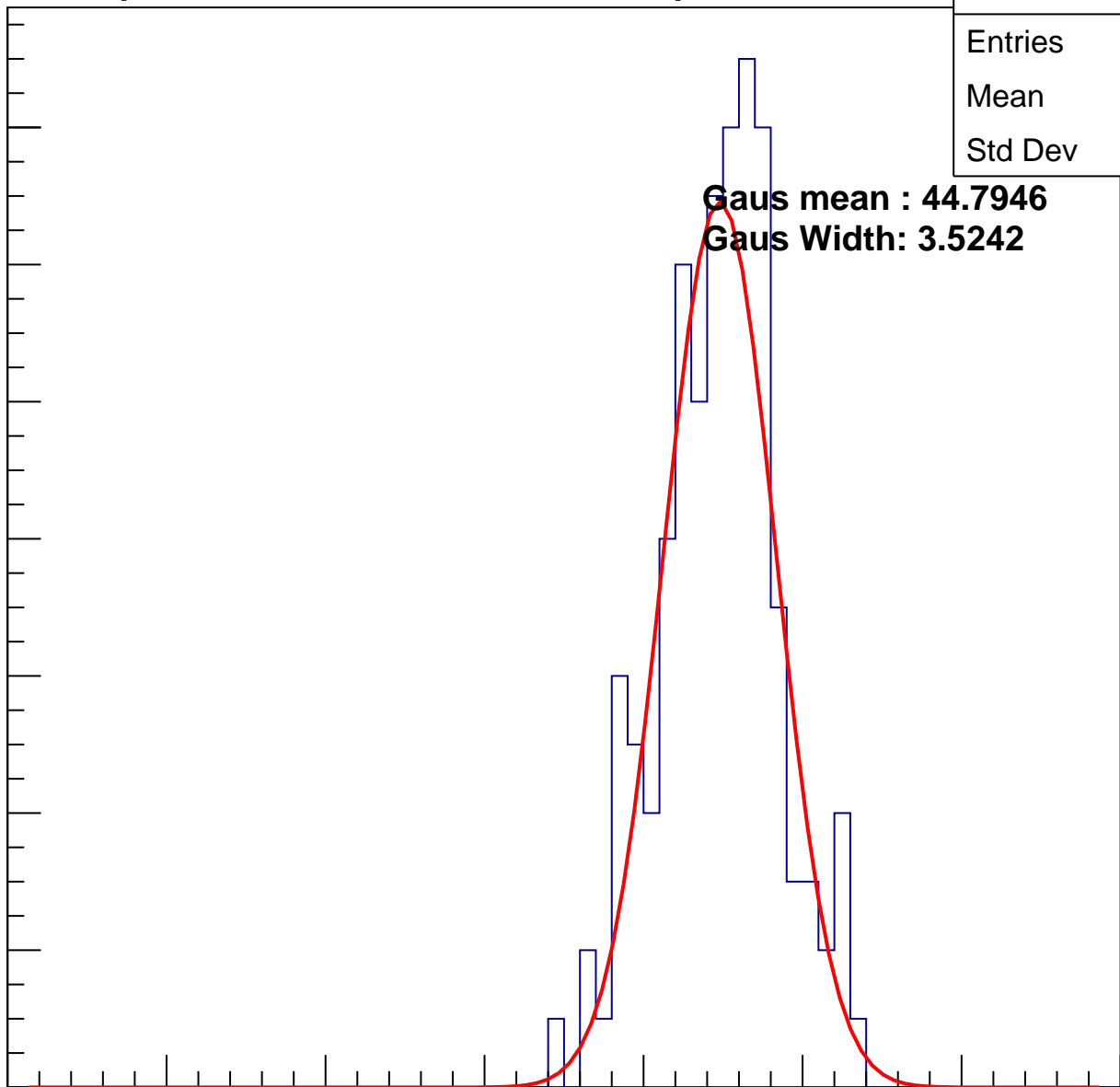
70

ampl

Entries	125
Mean	44.27
Std Dev	3.723

Gaus mean : 44.7946

Gaus Width: 3.5242



B0L101S, U24-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

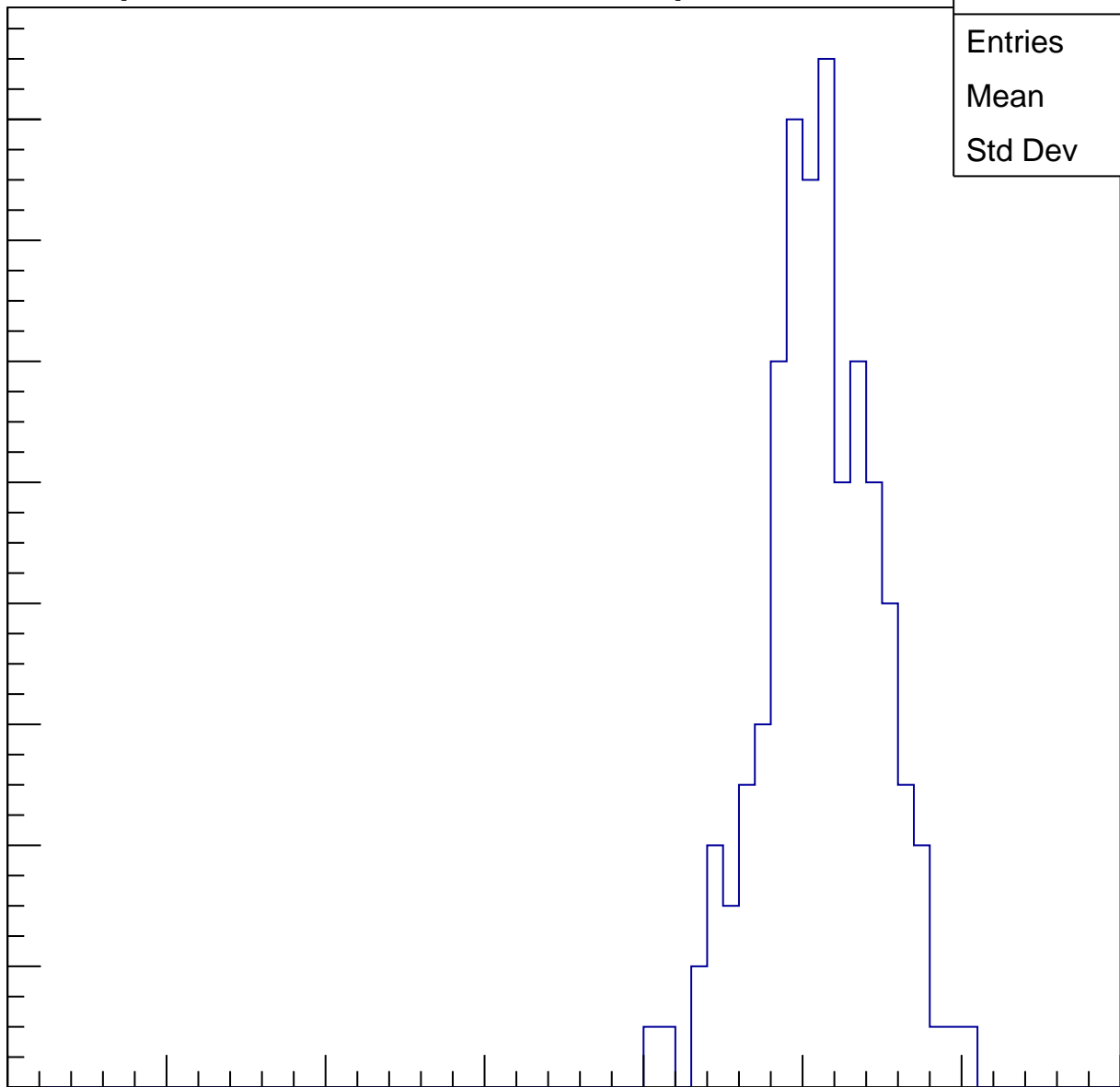
Entries	134
Mean	50.66
Std Dev	3.649

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch78, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

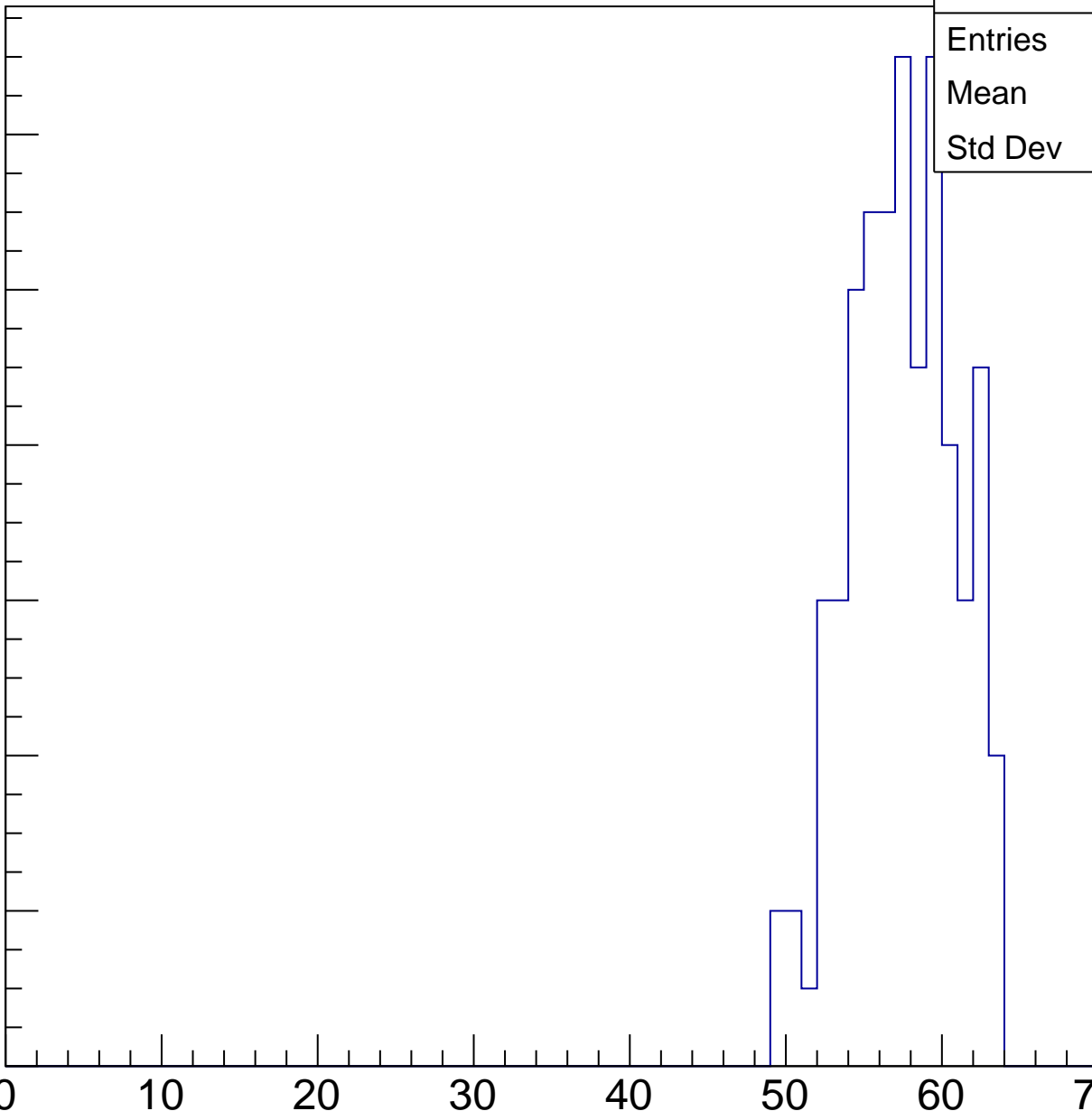
40

50

60

ampl

Entries	111
Mean	56.99
Std Dev	3.357



B0L101S, U24-ch78, adc5

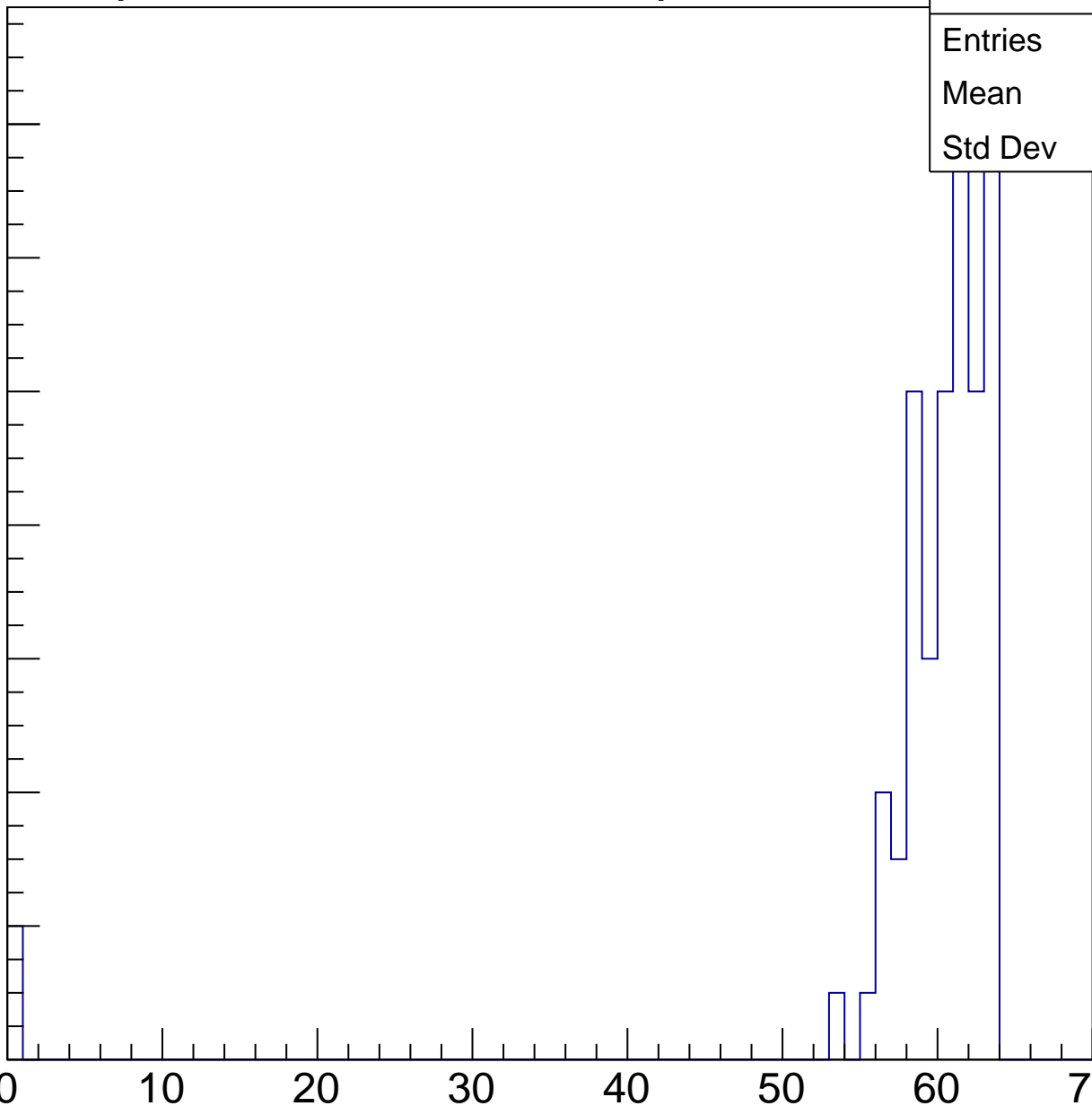
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	58.63
Std Dev	9.906

ampl



B0L101S, U24-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	1.247

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch79, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	28.4
Std Dev	4.971

Gaus mean : 29.3753

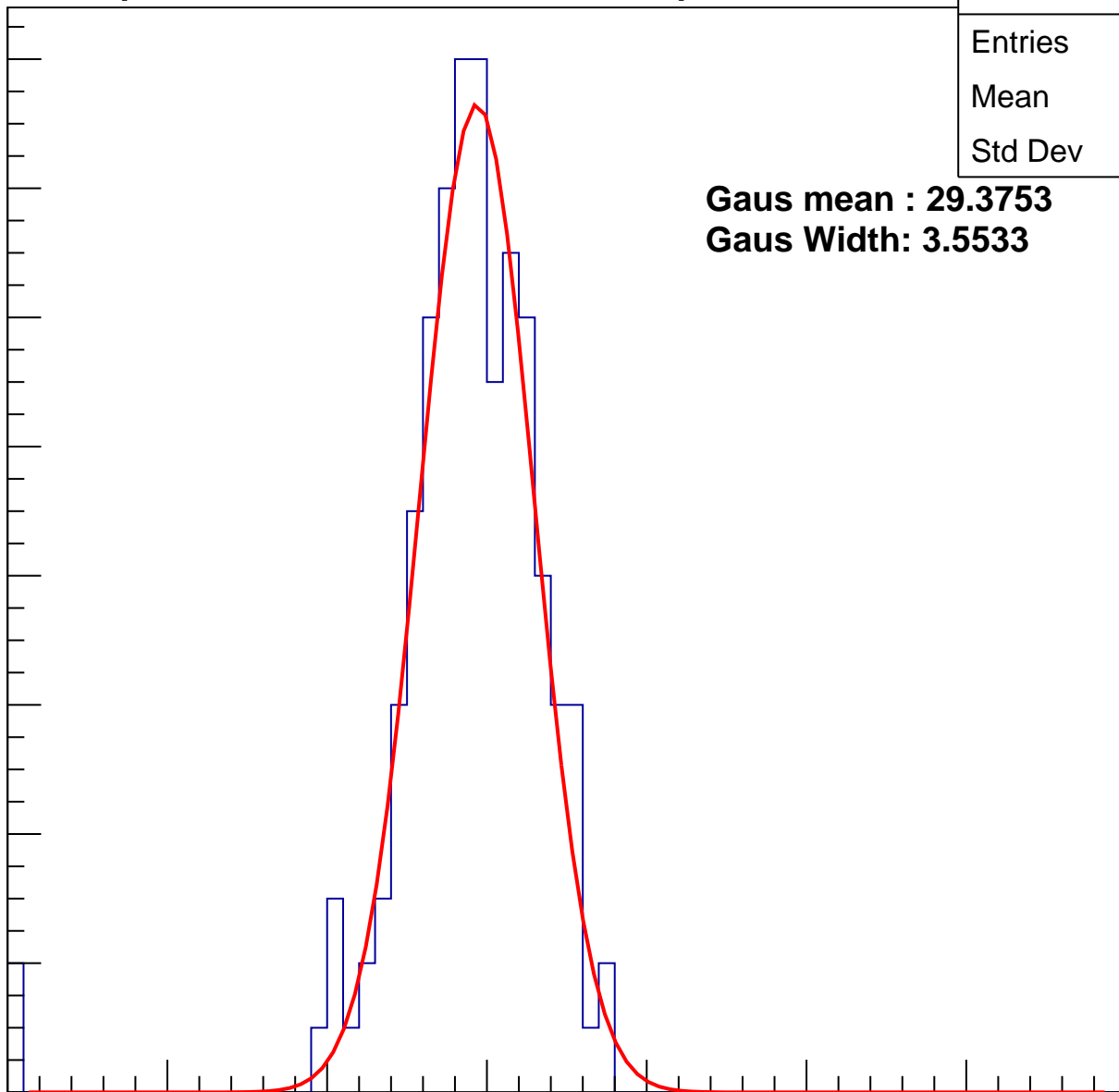
Gaus Width: 3.5533

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch79, adc1

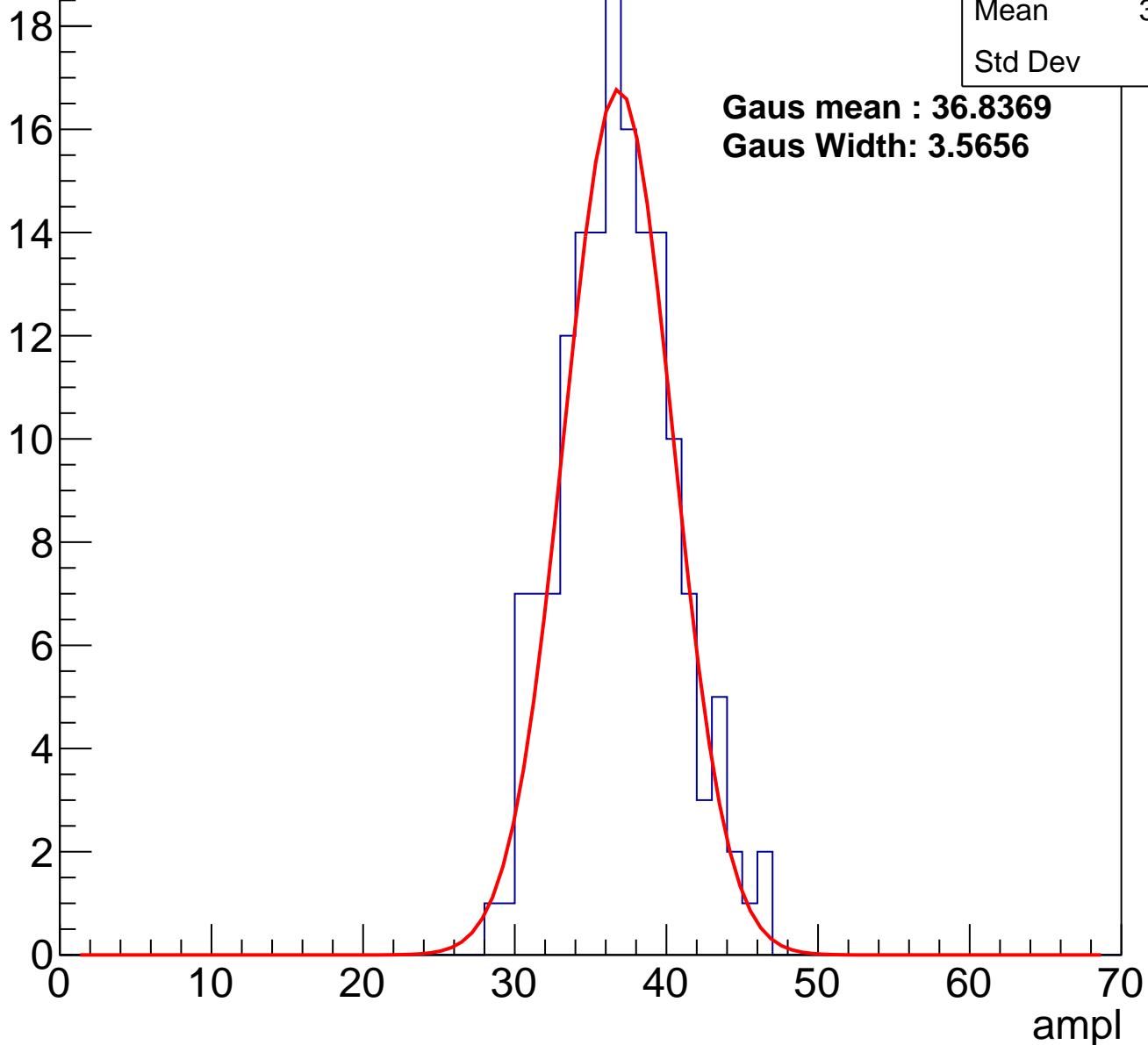
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	36.39
Std Dev	3.67

Gaus mean : 36.8369

Gaus Width: 3.5656

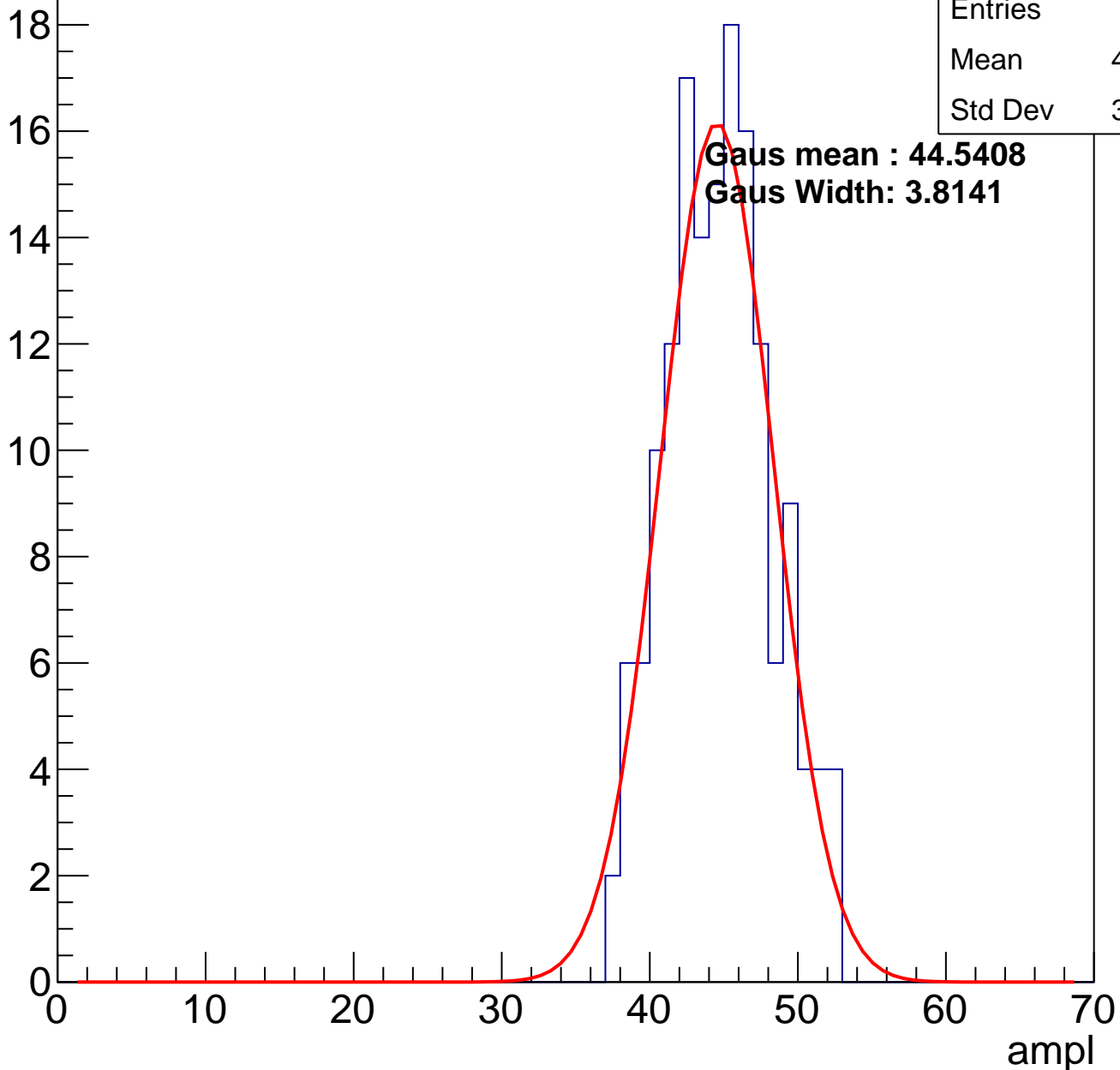
Entry



B0L101S, U24-ch79, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

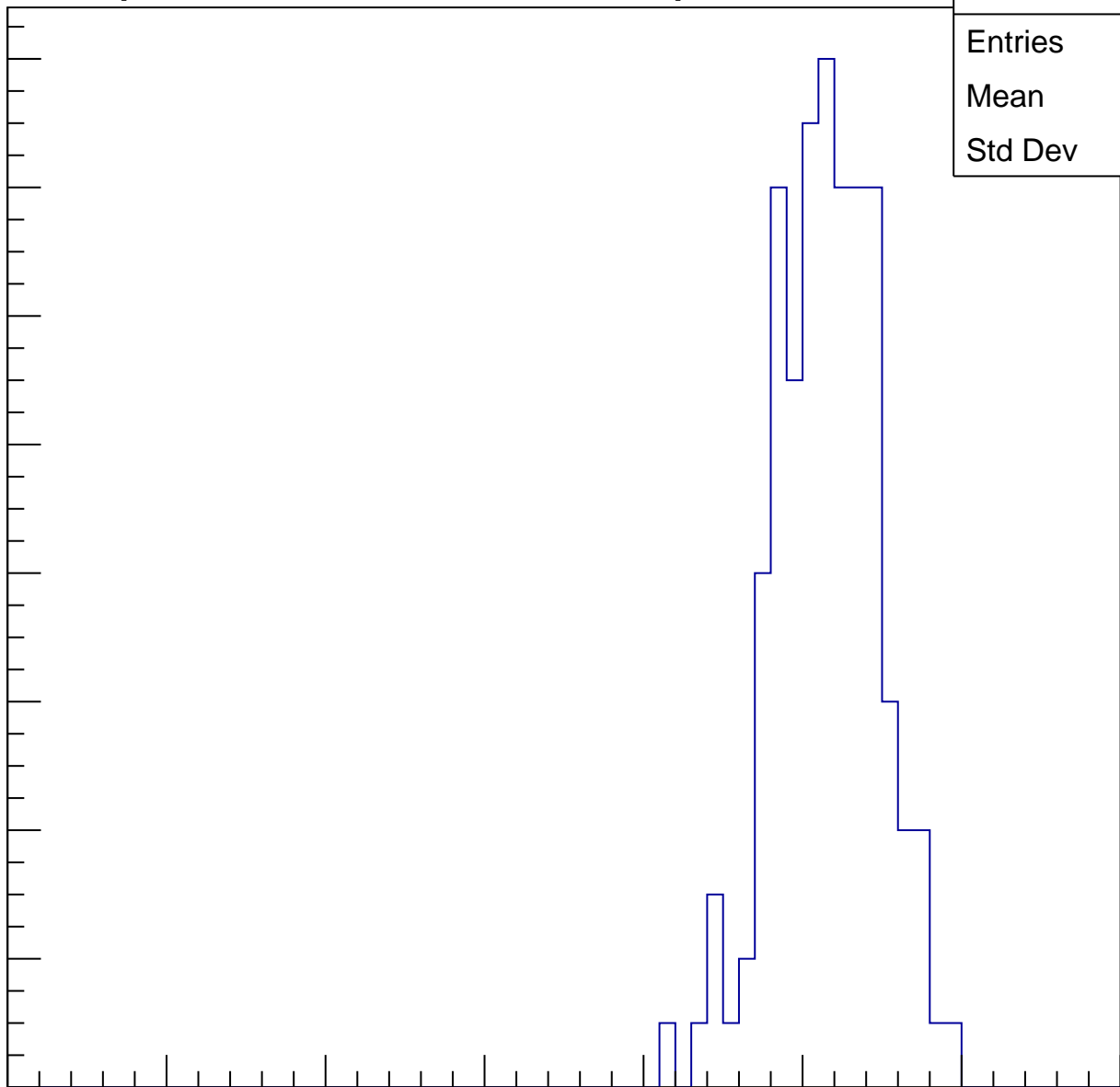
Entries	130
Mean	51.01
Std Dev	3.233

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch79, adc4

calib_packv5_042523_0143.root, FC#1, port C1

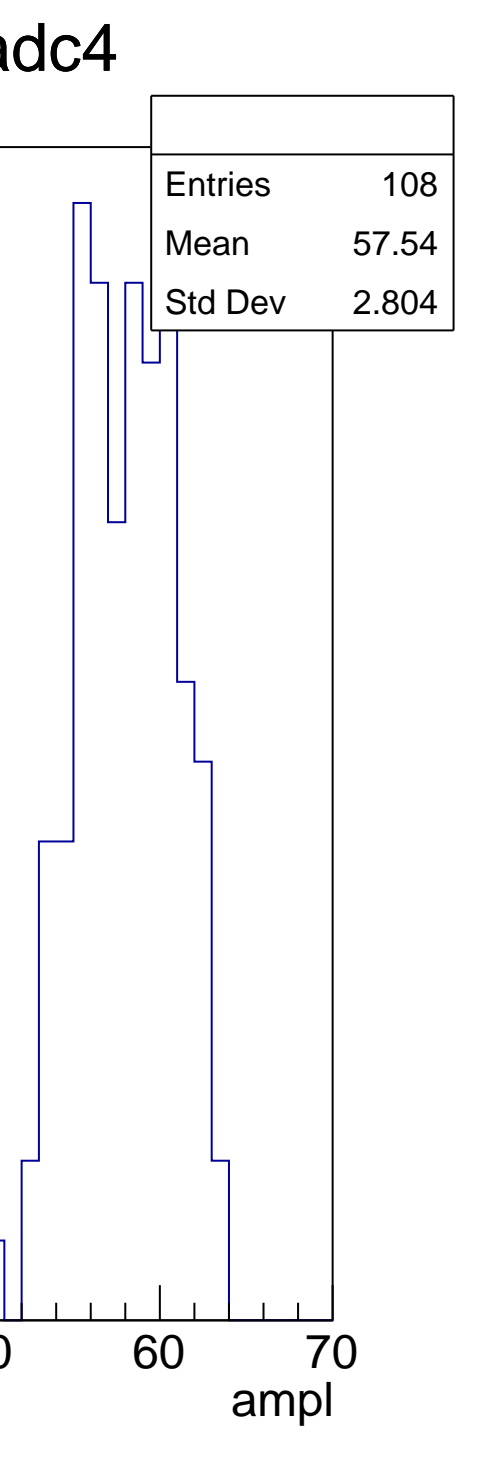
Entries	108
Mean	57.54
Std Dev	2.804

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch79, adc5

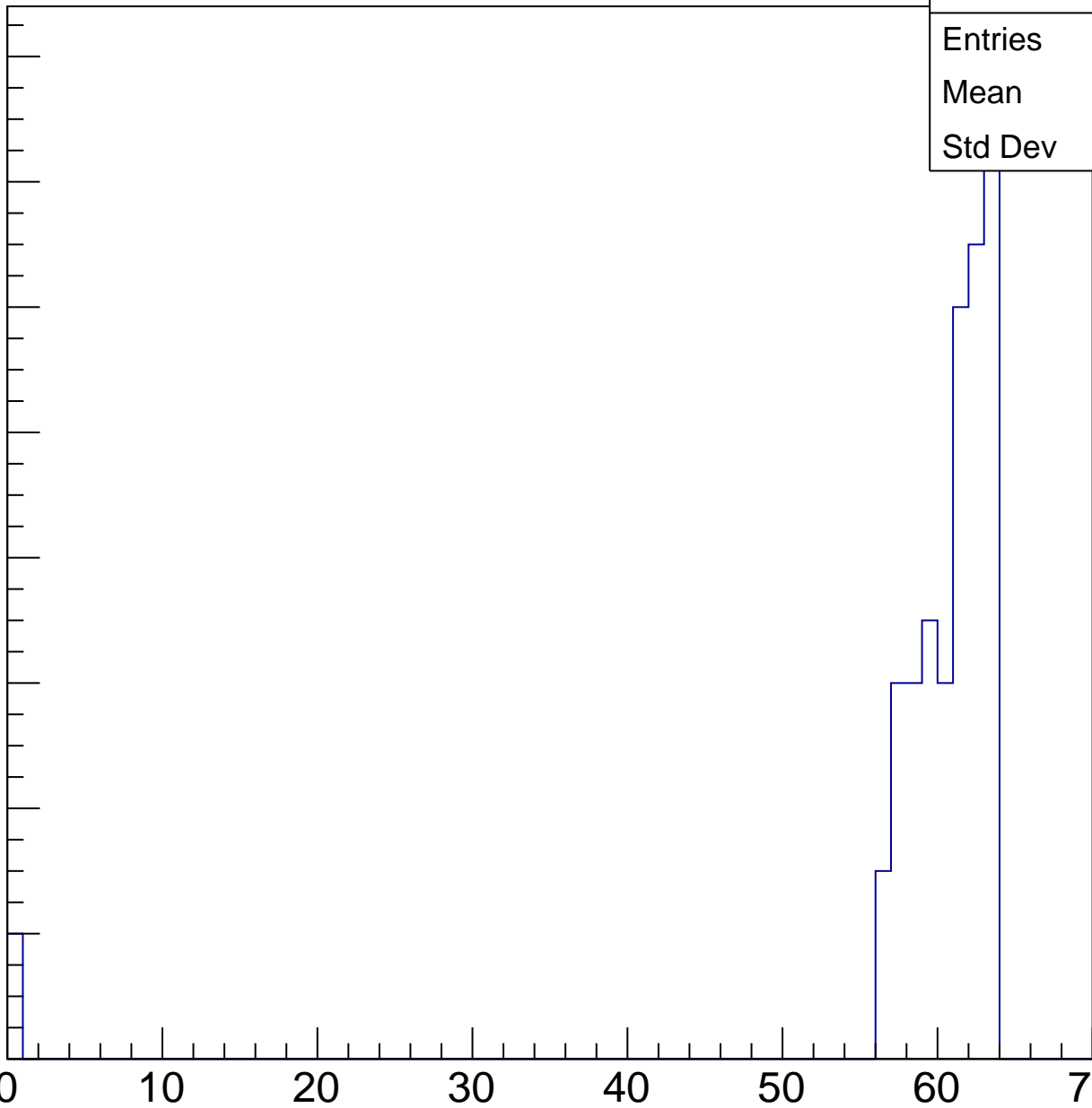
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	71
Mean	58.83
Std Dev	10.24

ampl



B0L101S, U24-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

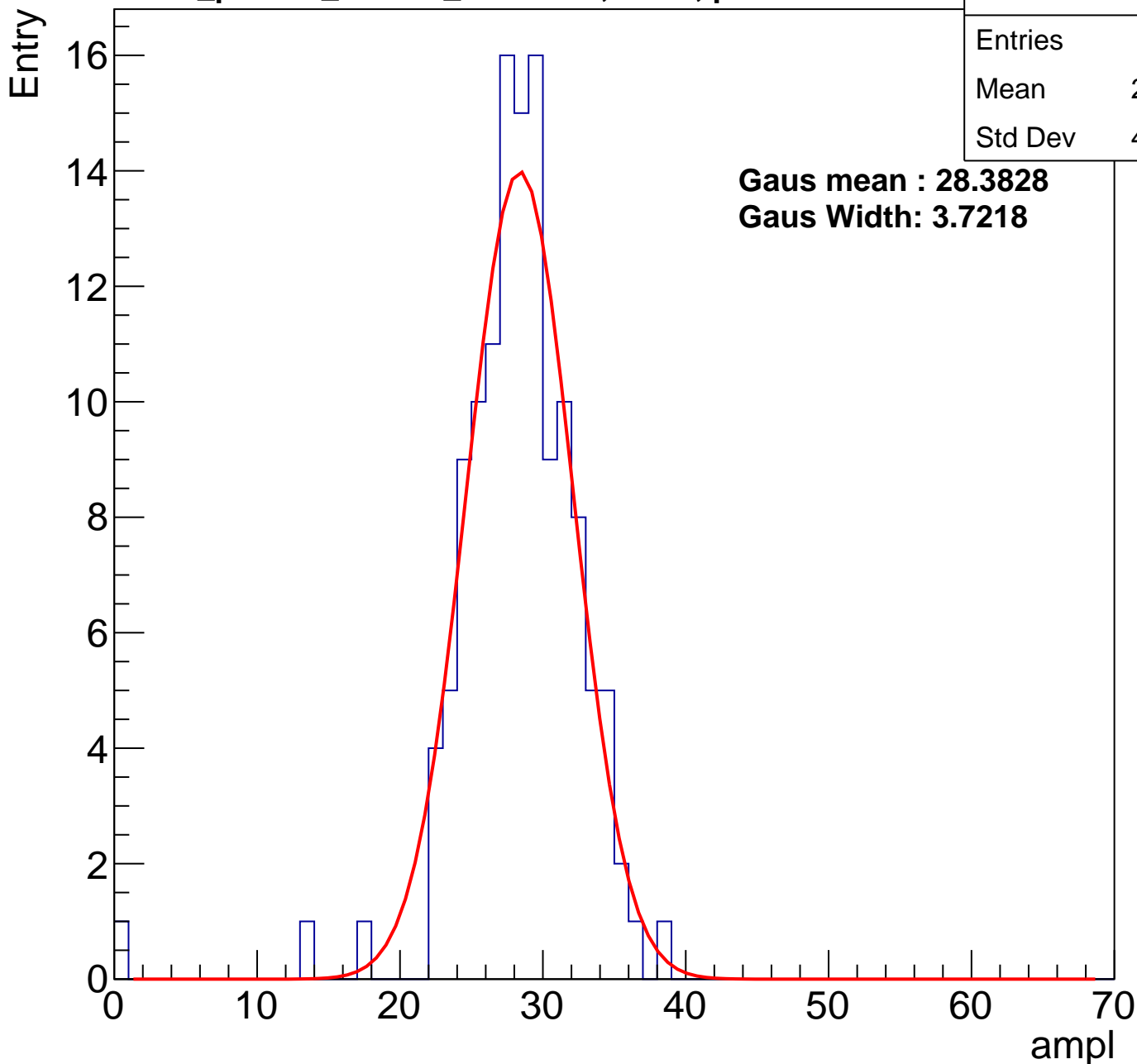
B0L101S, U24-ch80, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	27.82
Std Dev	4.408

Gaus mean : 28.3828

Gaus Width: 3.7218



B0L101S, U24-ch80, adc1

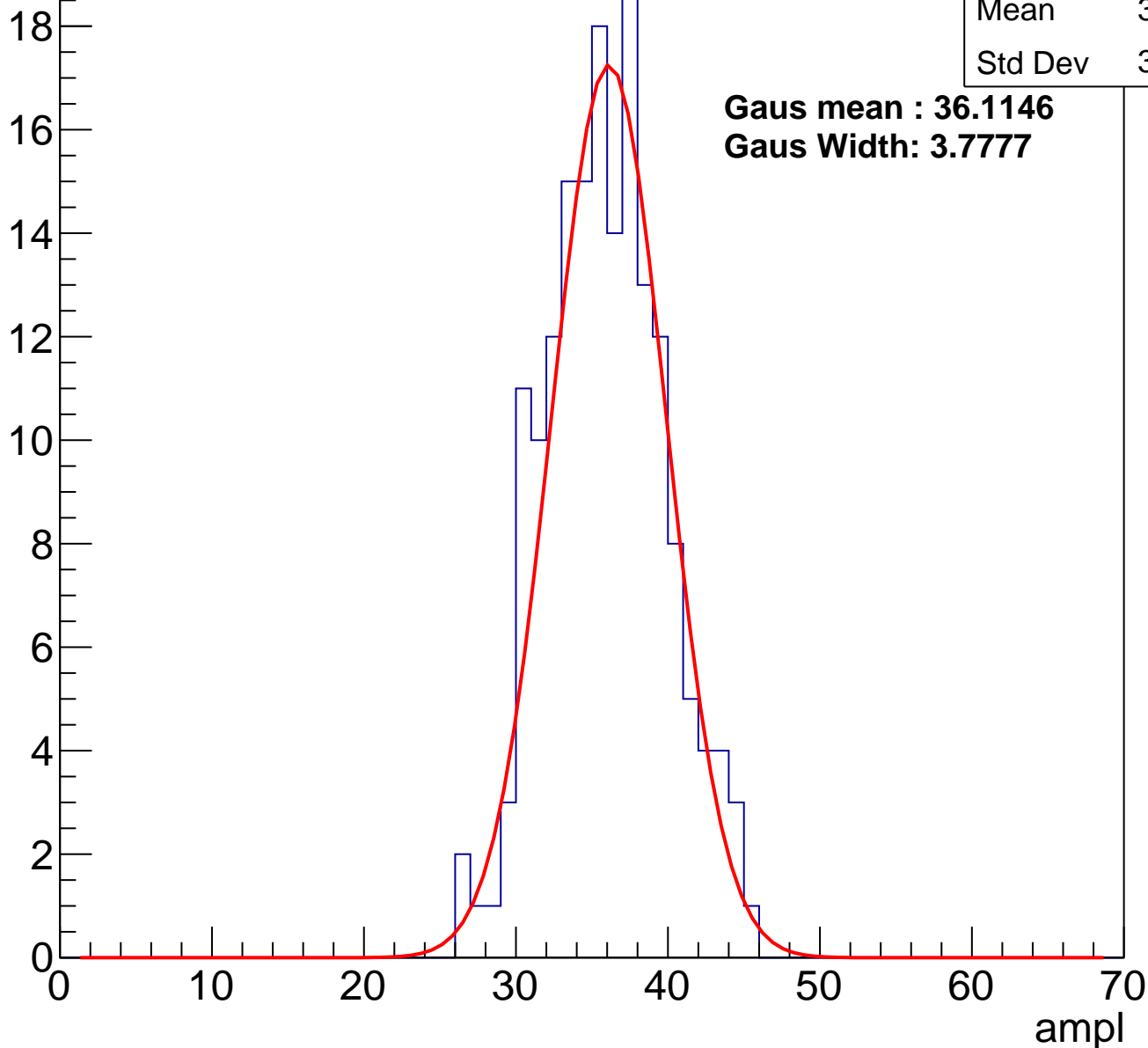
calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	35.46
Std Dev	3.853

Gaus mean : 36.1146

Gaus Width: 3.7777

Entry



B0L101S, U24-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

115

Mean

42.74

Std Dev

3.444

Gaus mean : 43.3992

Gaus Width: 3.3265

0

10

20

30

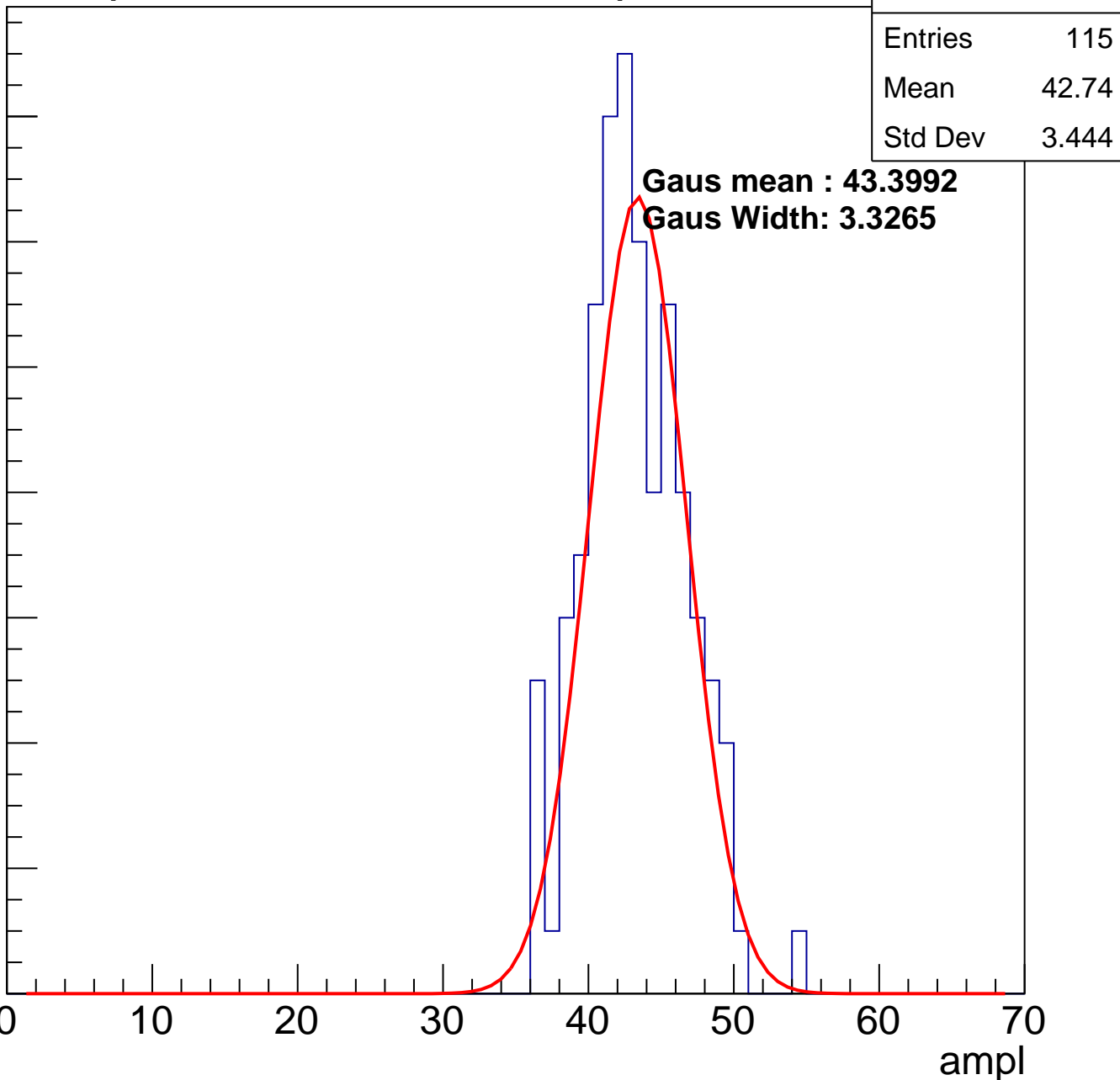
40

50

60

70

ampl

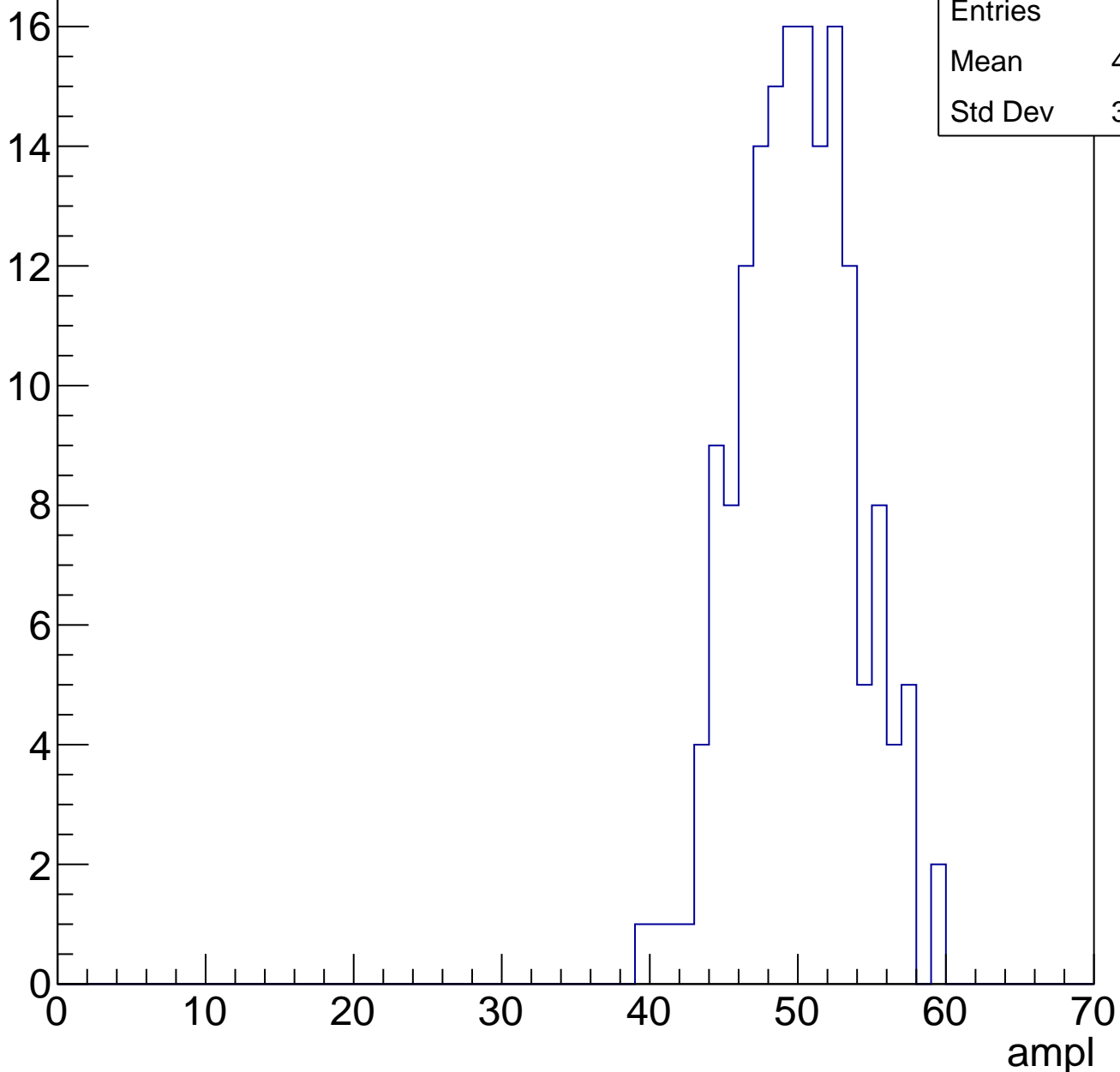


B0L101S, U24-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	49.53
Std Dev	3.874

Entry



B0L101S, U24-ch80, adc4

calib_packv5_042523_0143.root, FC#1, port C1

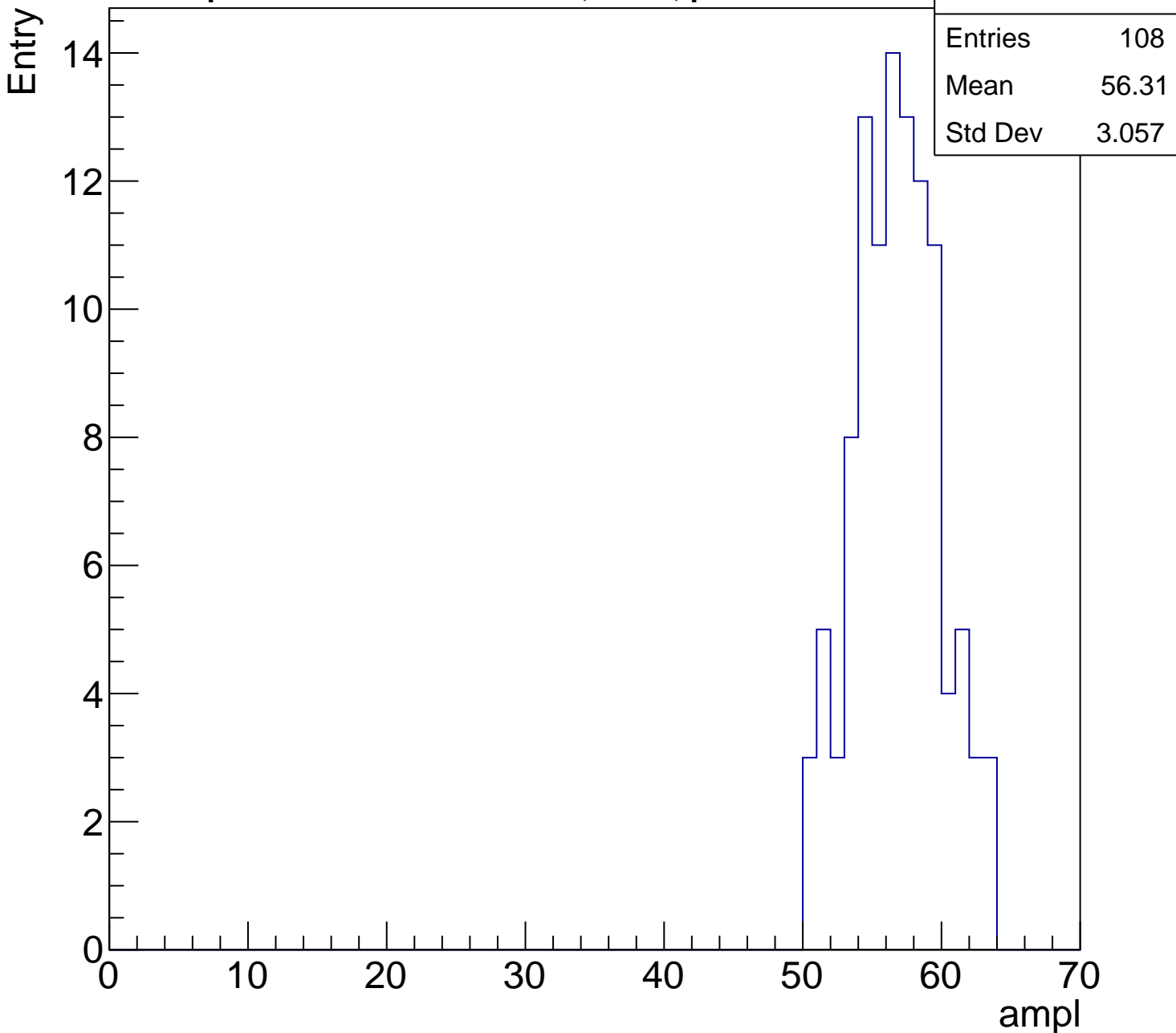
Entries	108
Mean	56.31
Std Dev	3.057

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch80, adc5

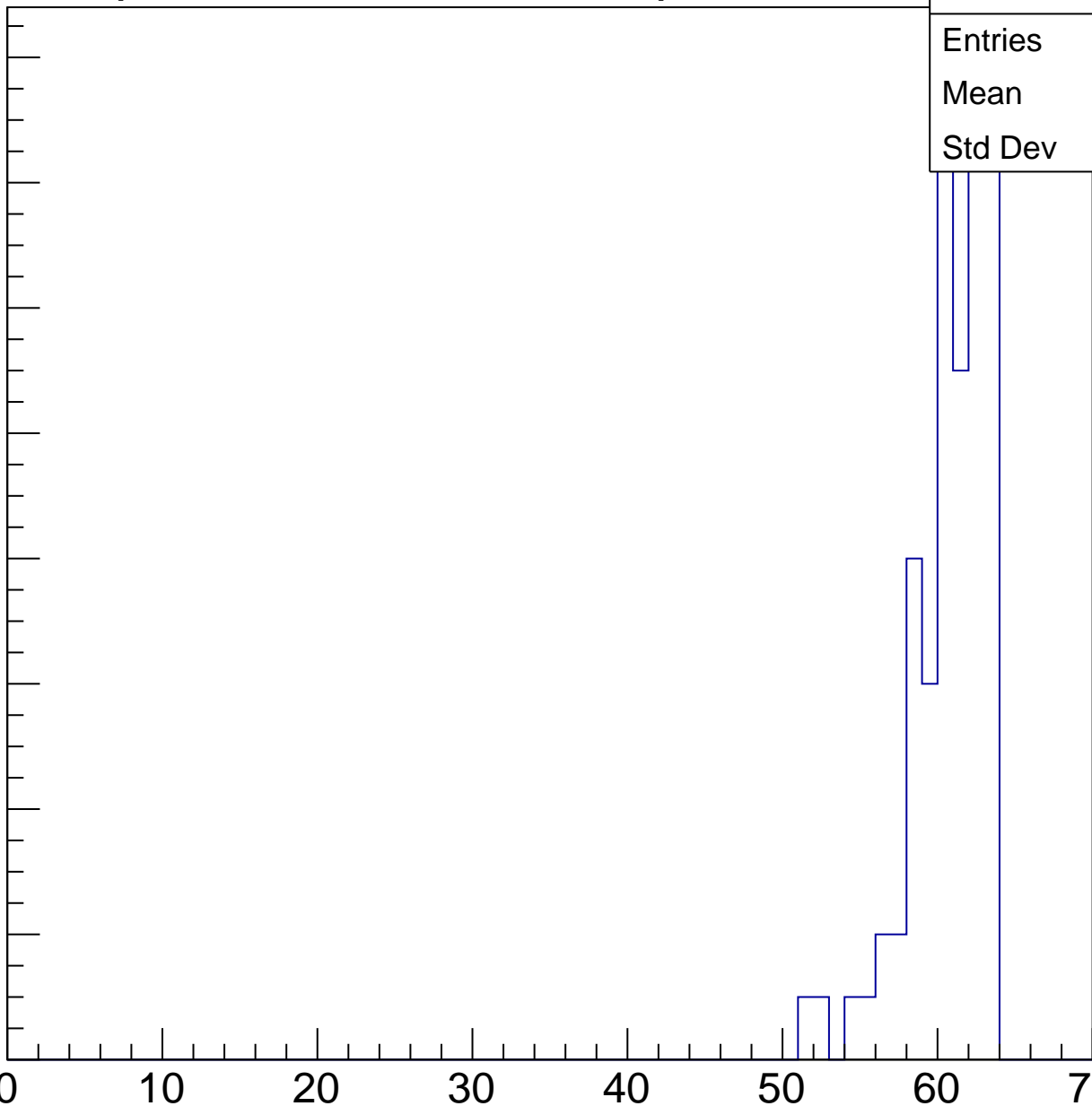
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	60.3
Std Dev	2.507

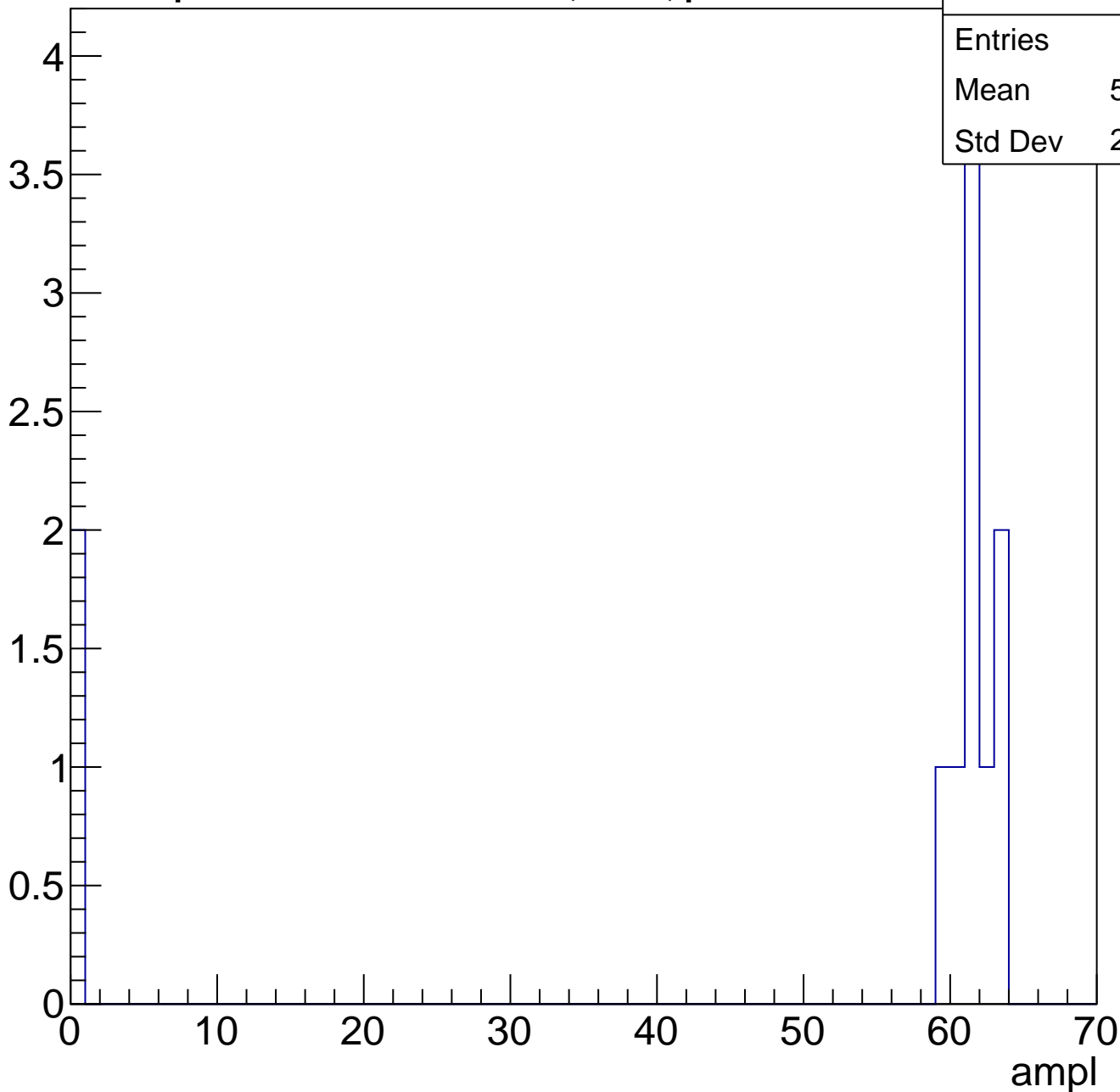
ampl



B0L101S, U24-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch81, adc0

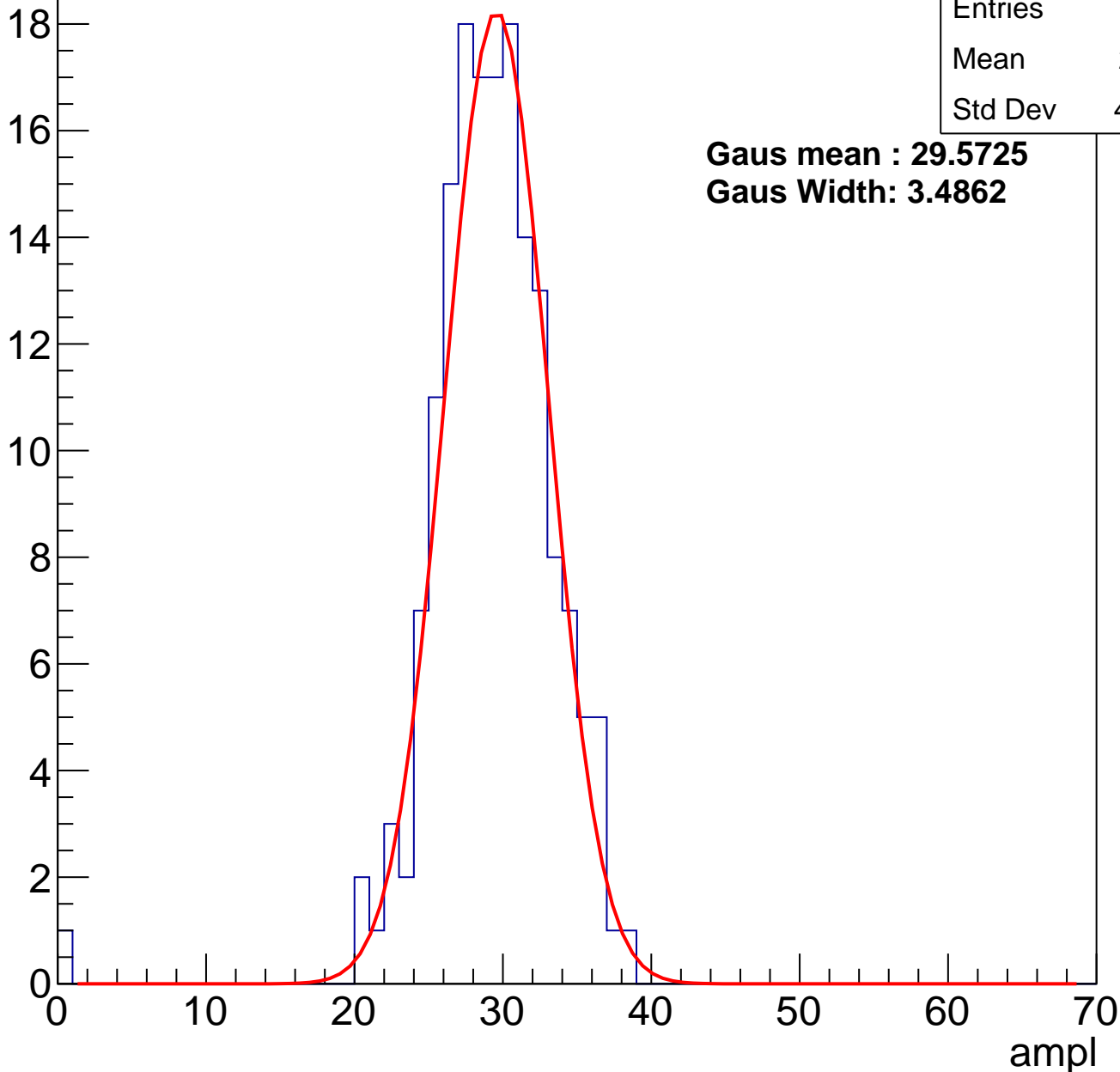
calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	28.81
Std Dev	4.194

Gaus mean : 29.5725

Gaus Width: 3.4862

Entry



B0L101S, U24-ch81, adc1

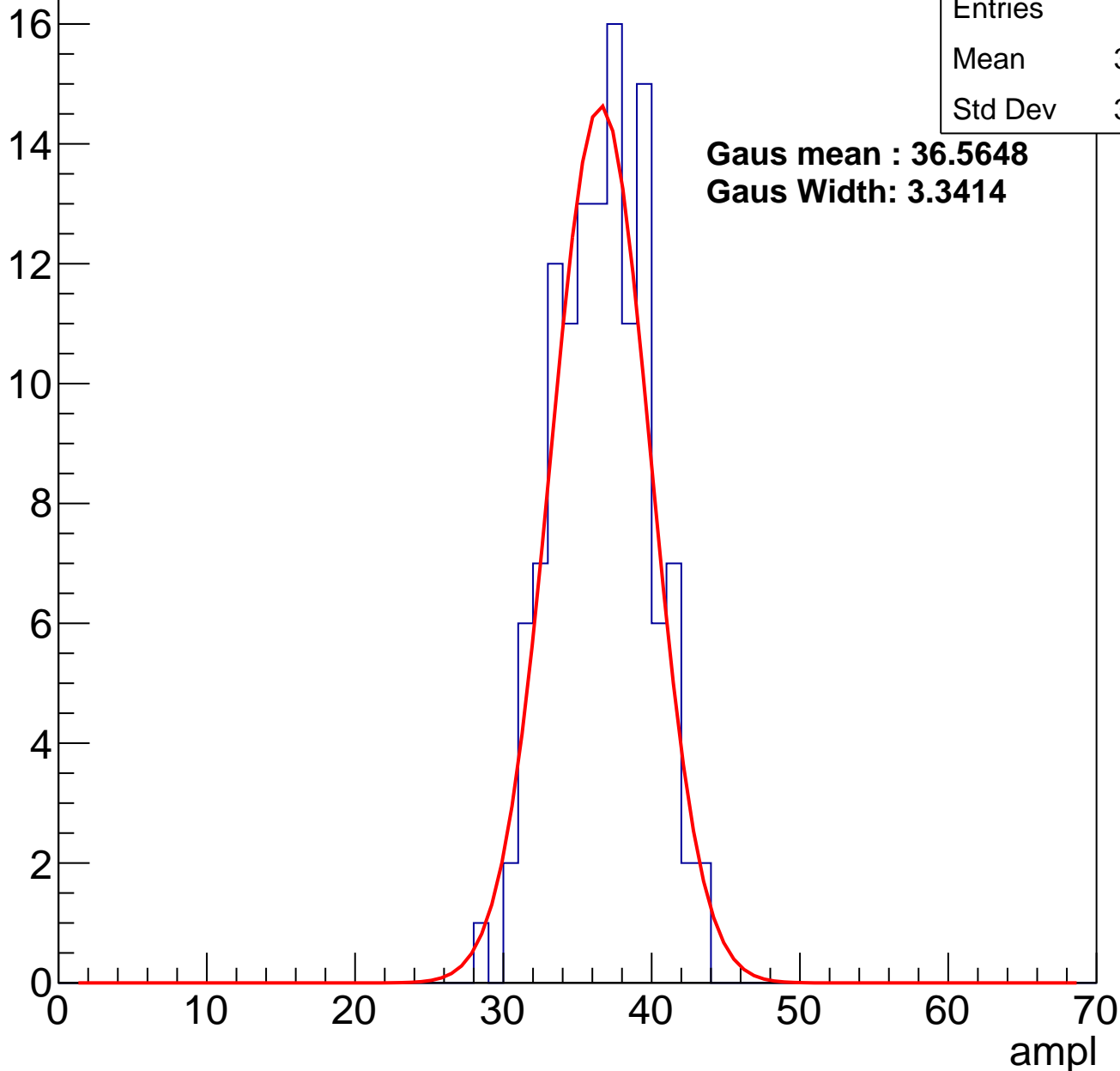
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.15
Std Dev	3.106

Gaus mean : 36.5648

Gaus Width: 3.3414

Entry



B0L101S, U24-ch81, adc2

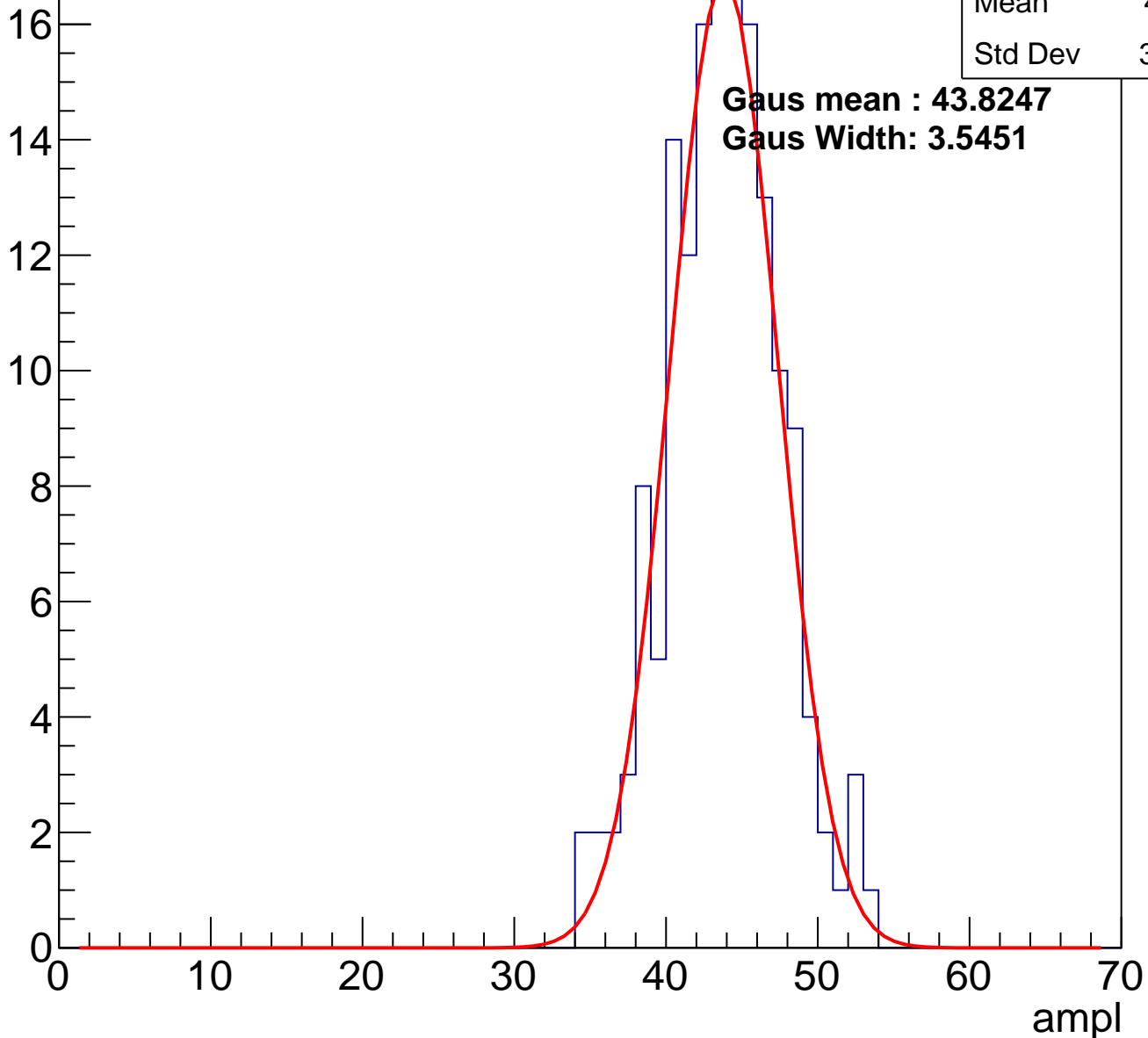
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	157
Mean	43.31
Std Dev	3.729

Gaus mean : 43.8247

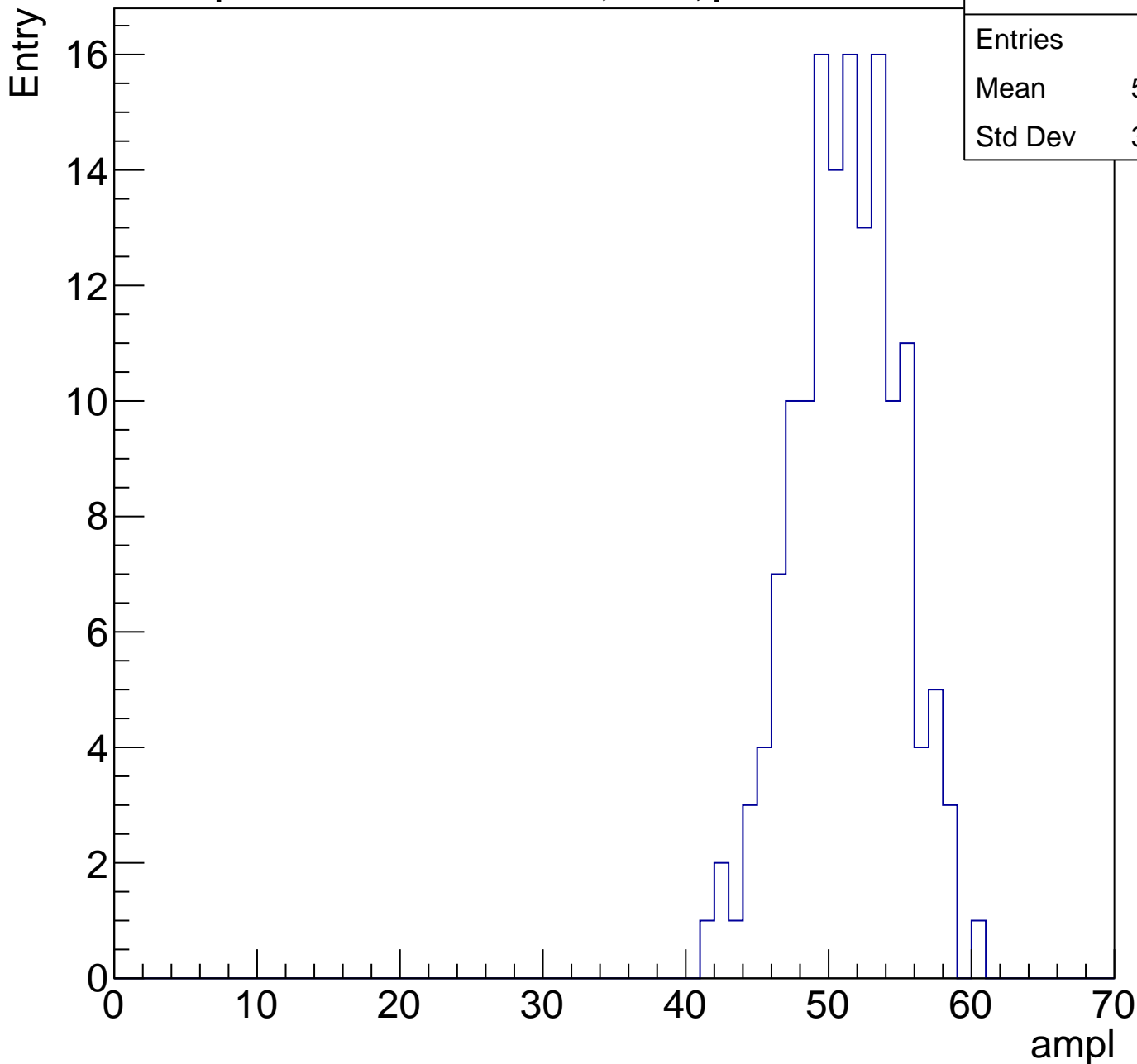
Gaus Width: 3.5451



B0L101S, U24-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

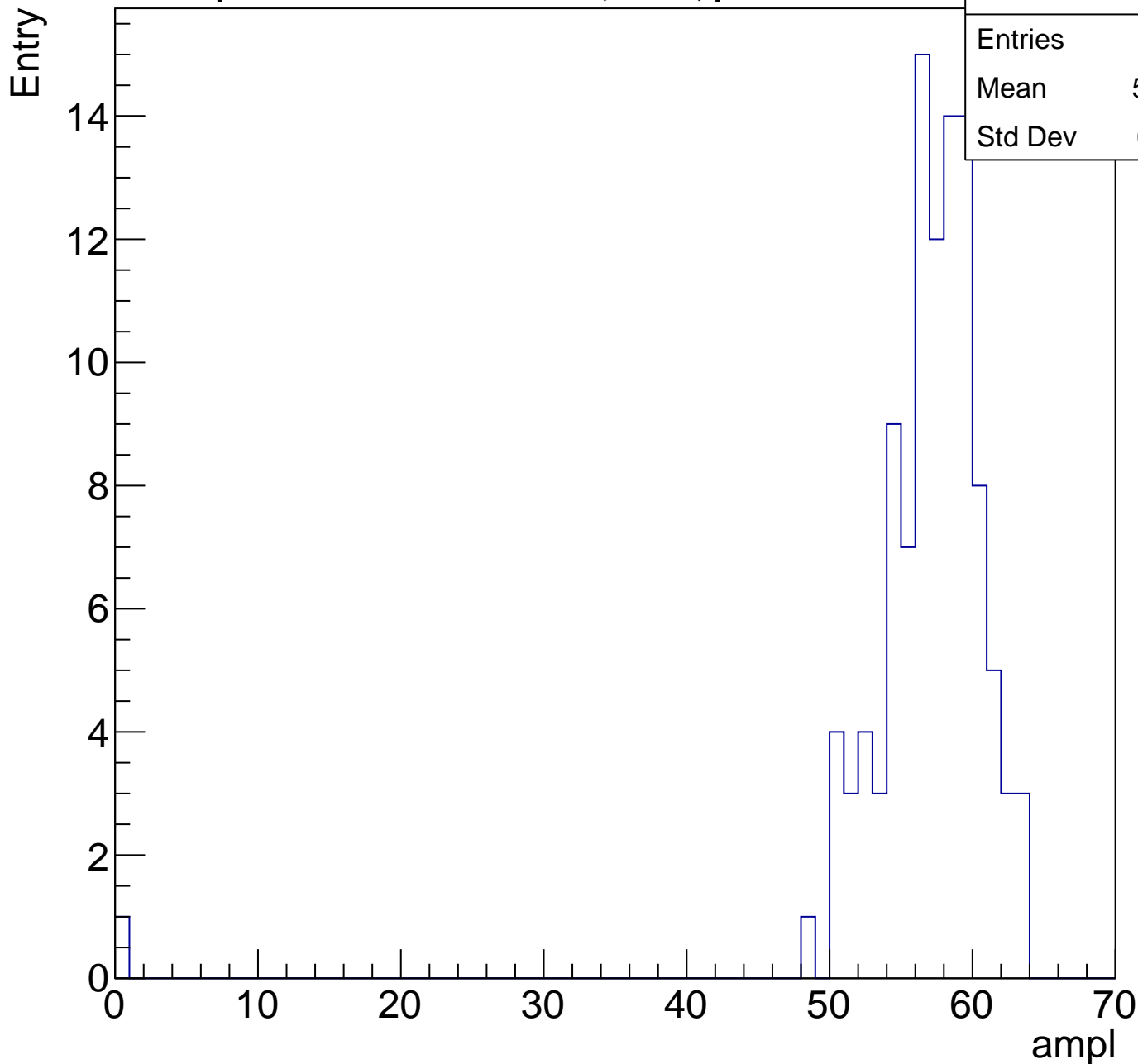
Entries	147
Mean	50.78
Std Dev	3.655



B0L101S, U24-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	56.24
Std Dev	6.331



B0L101S, U24-ch81, adc5

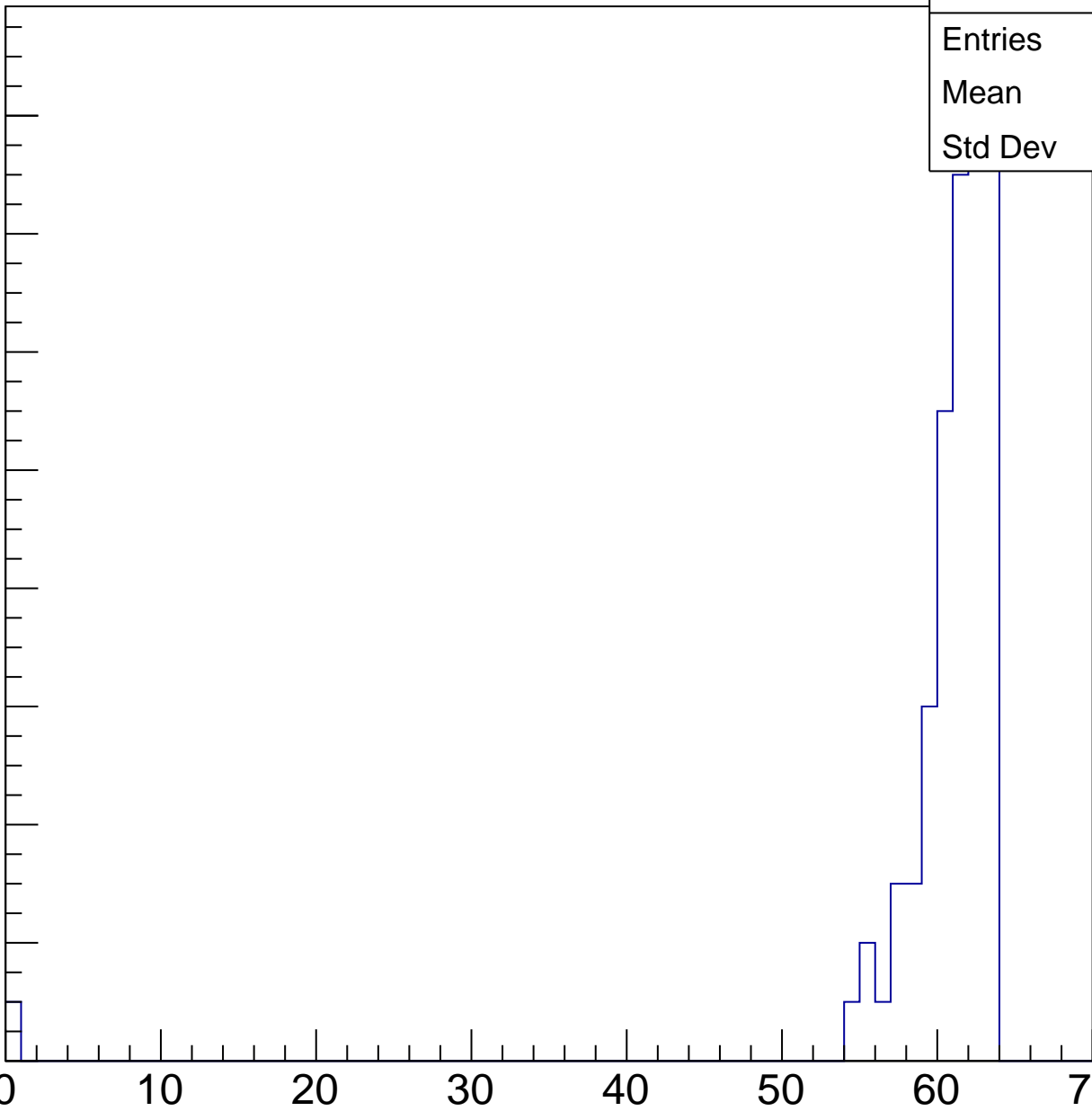
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.95
Std Dev	7.231

ampl



B0L101S, U24-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



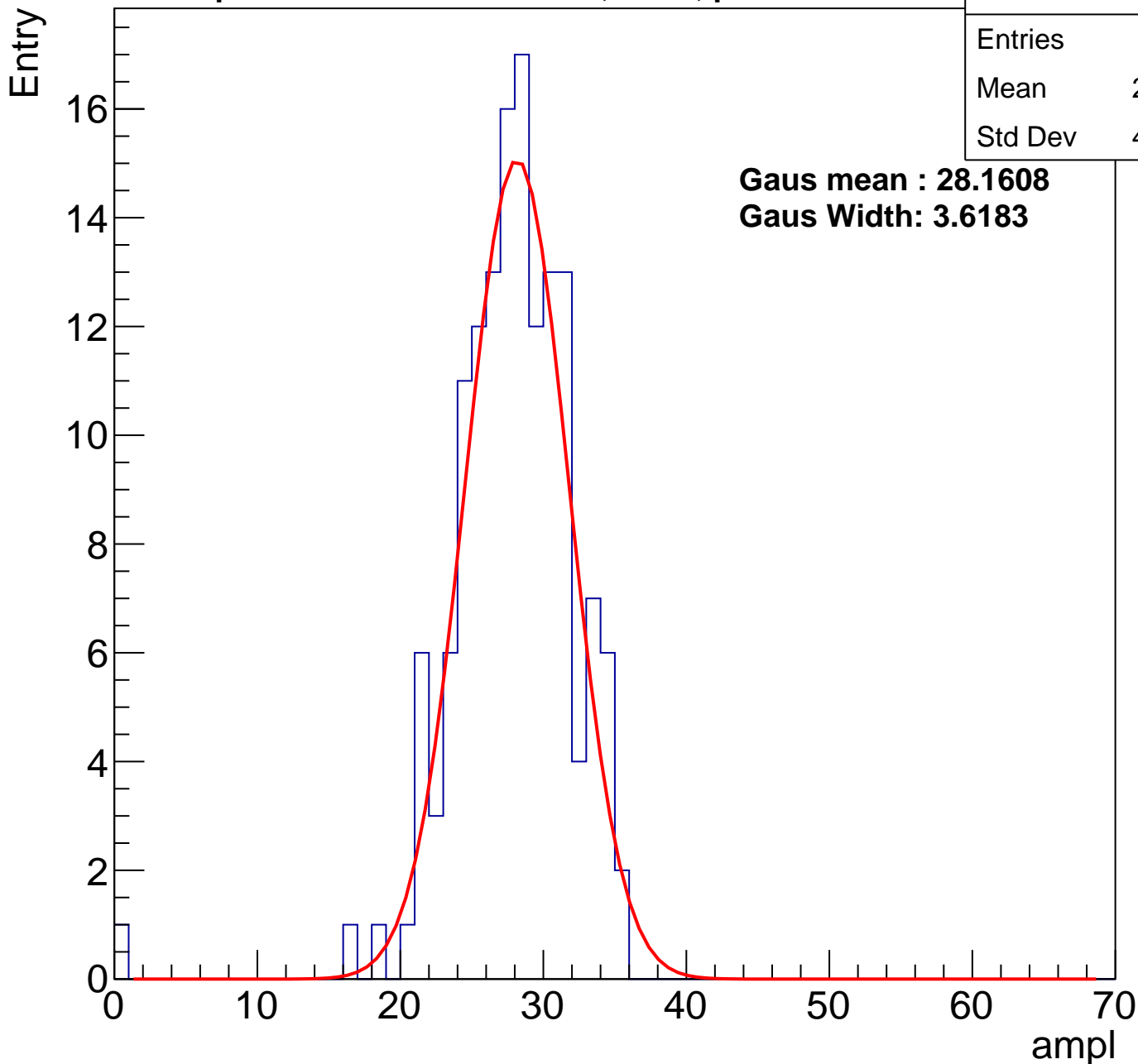
B0L101S, U24-ch82, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	27.37
Std Dev	4.292

Gaus mean : 28.1608

Gaus Width: 3.6183



B0L101S, U24-ch82, adc1

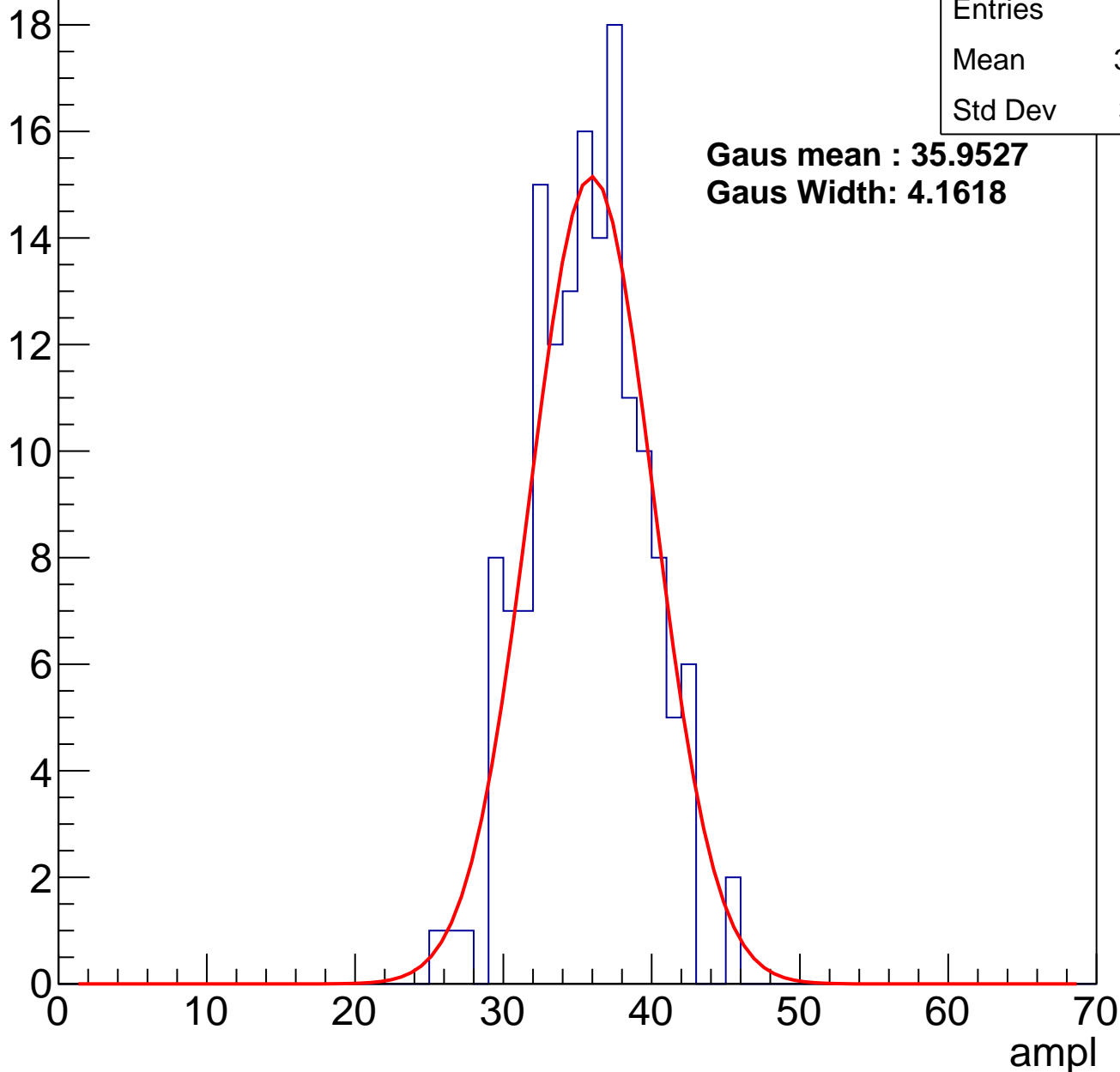
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	35.23
Std Dev	3.791

Gaus mean : 35.9527

Gaus Width: 4.1618

Entry



B0L101S, U24-ch82, adc2

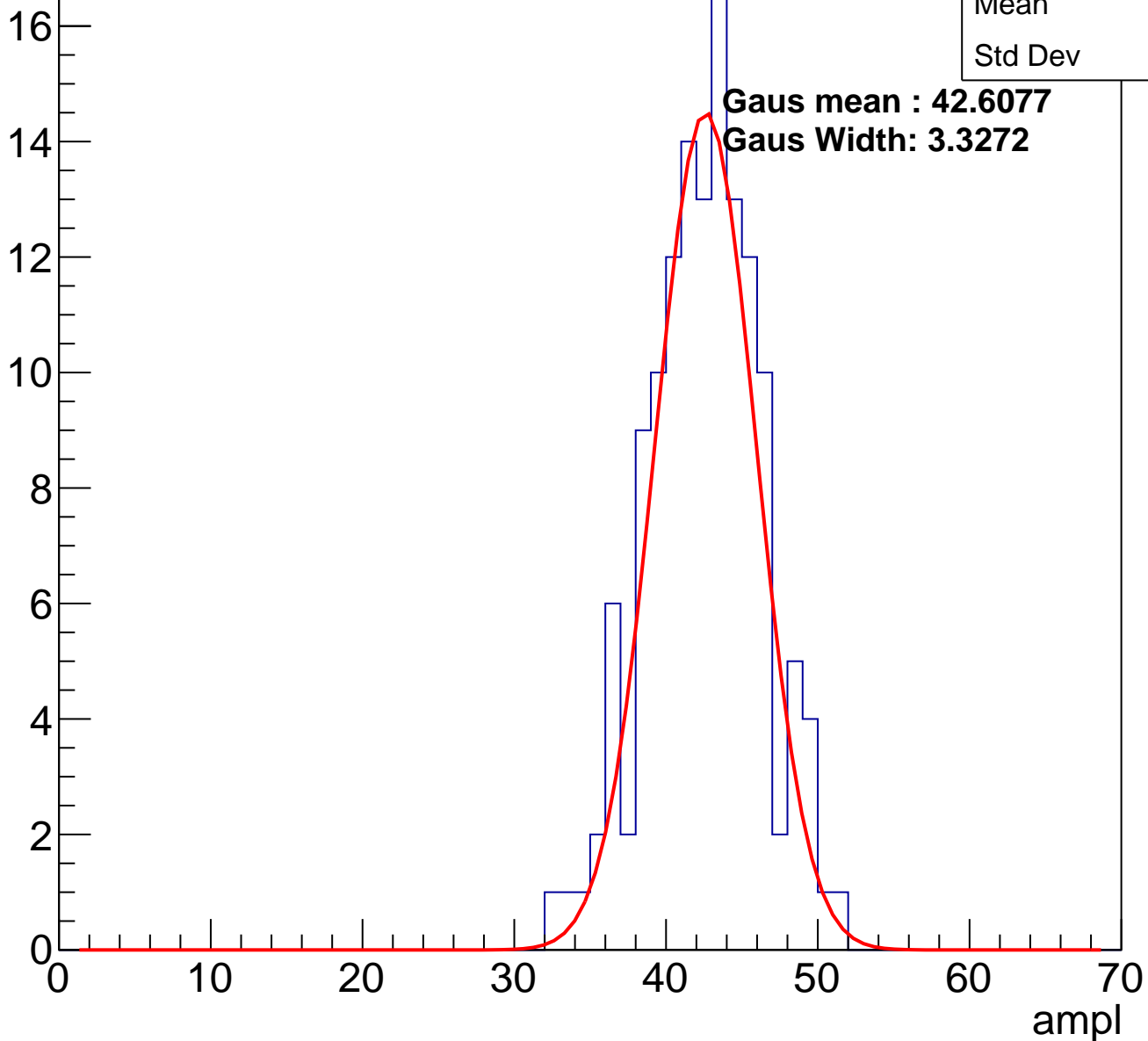
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	42.1
Std Dev	3.65

Entry

Gaus mean : 42.6077

Gaus Width: 3.3272

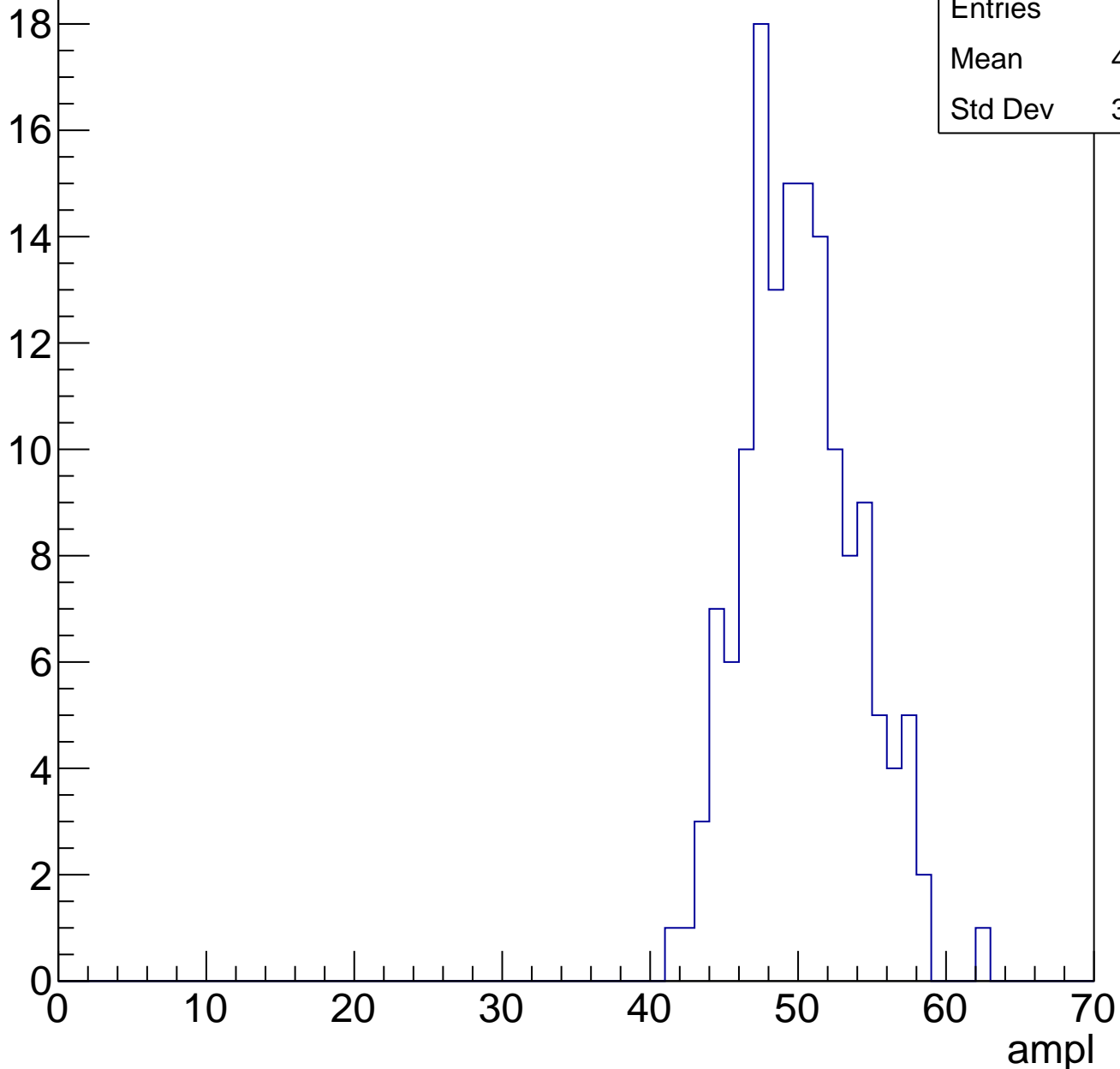


B0L101S, U24-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	49.73
Std Dev	3.805

Entry

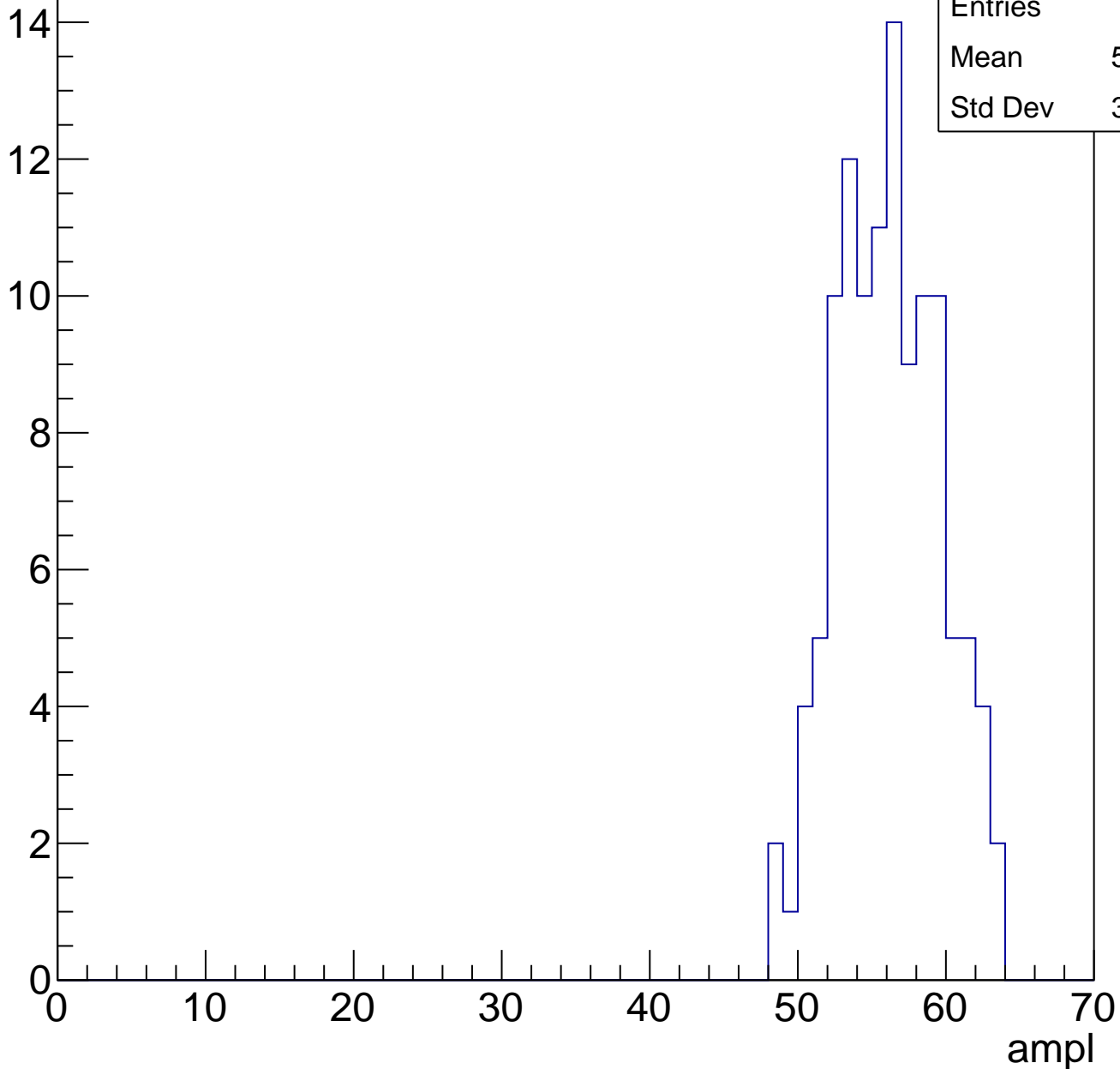


B0L101S, U24-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	55.68
Std Dev	3.427

Entry



B0L101S, U24-ch82, adc5

calib_packv5_042523_0143.root, FC#1, port C1

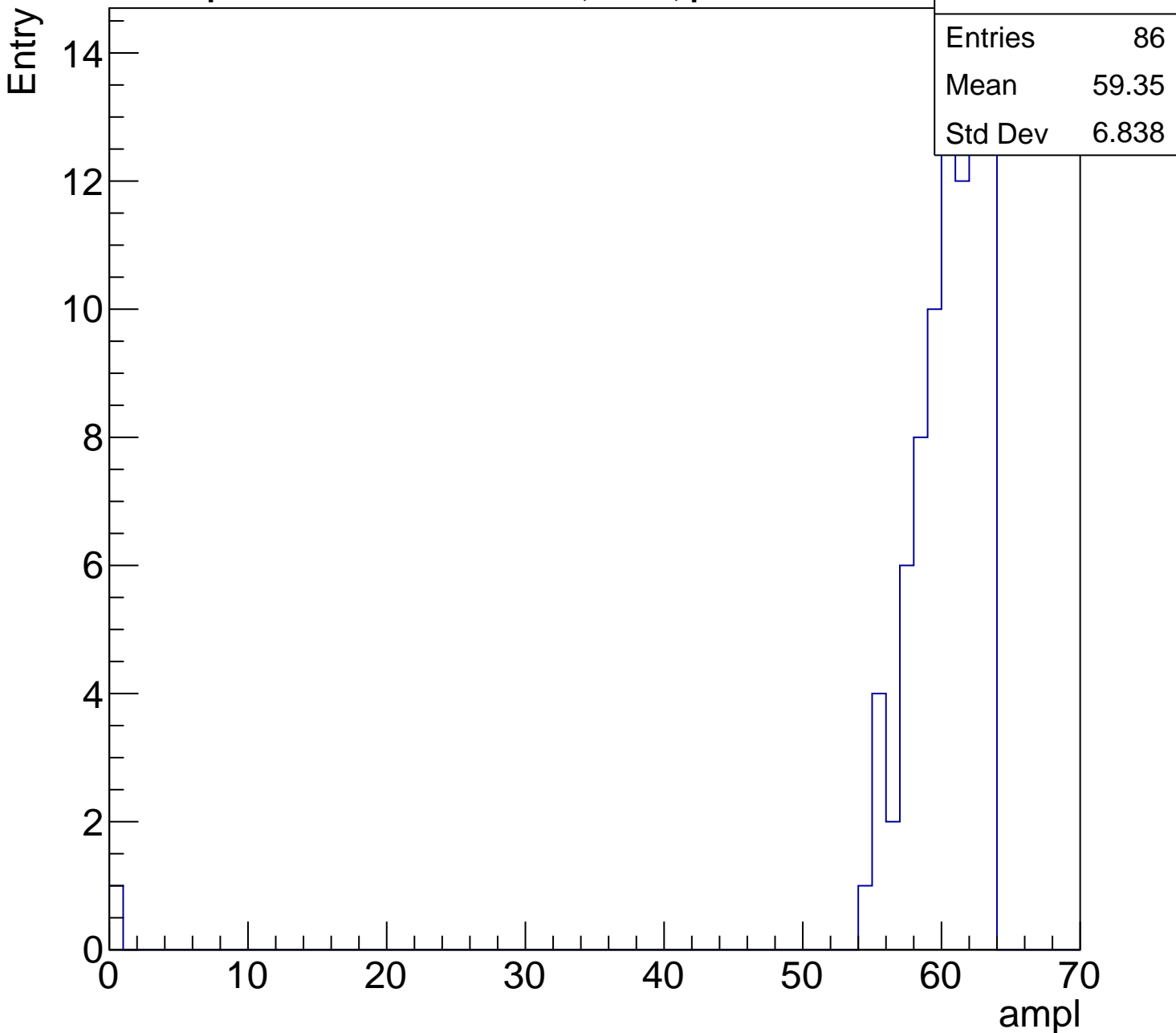
Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	59.35
Std Dev	6.838

ampl

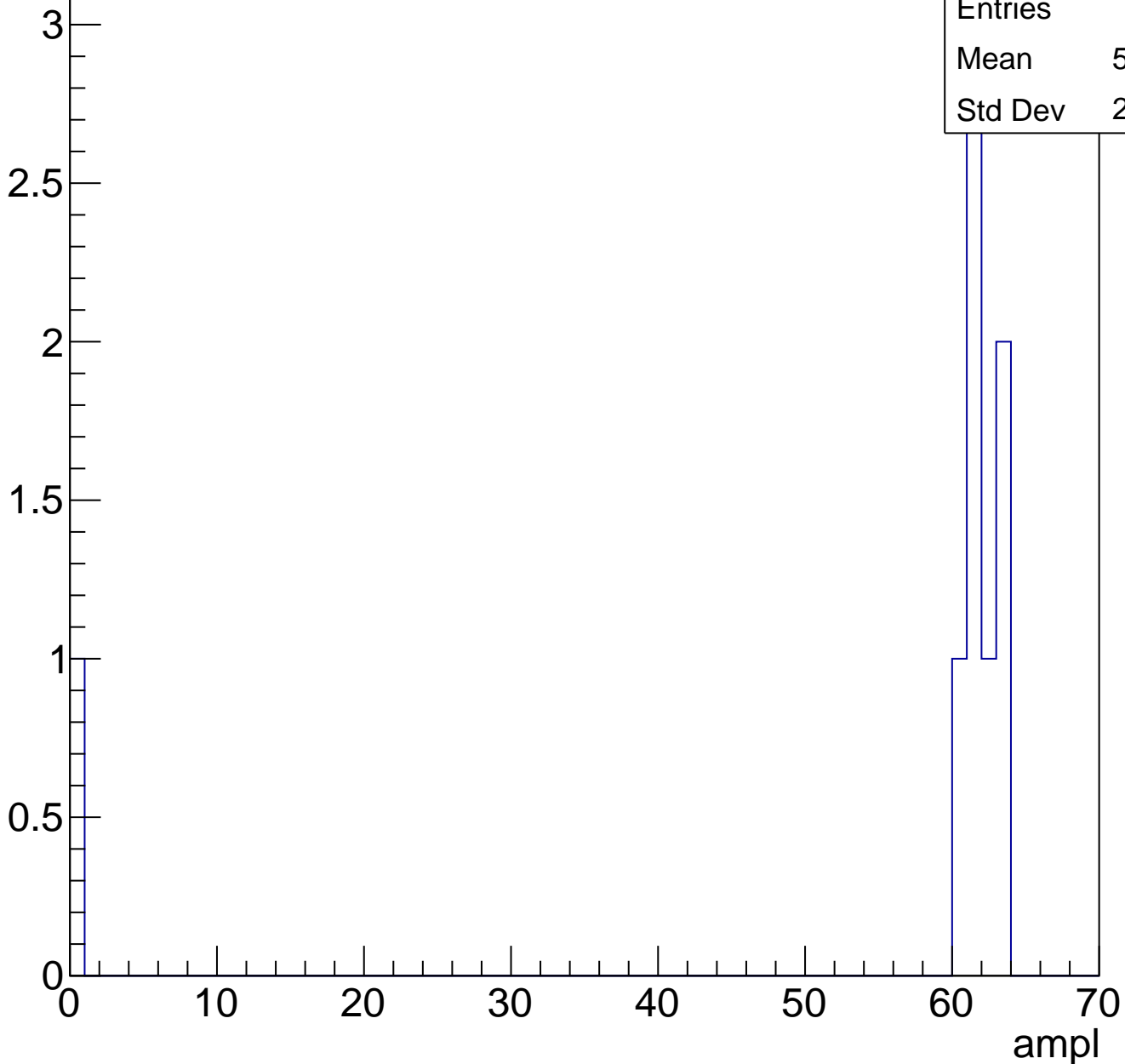
0 10 20 30 40 50 60 70



B0L101S, U24-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch83, adc0

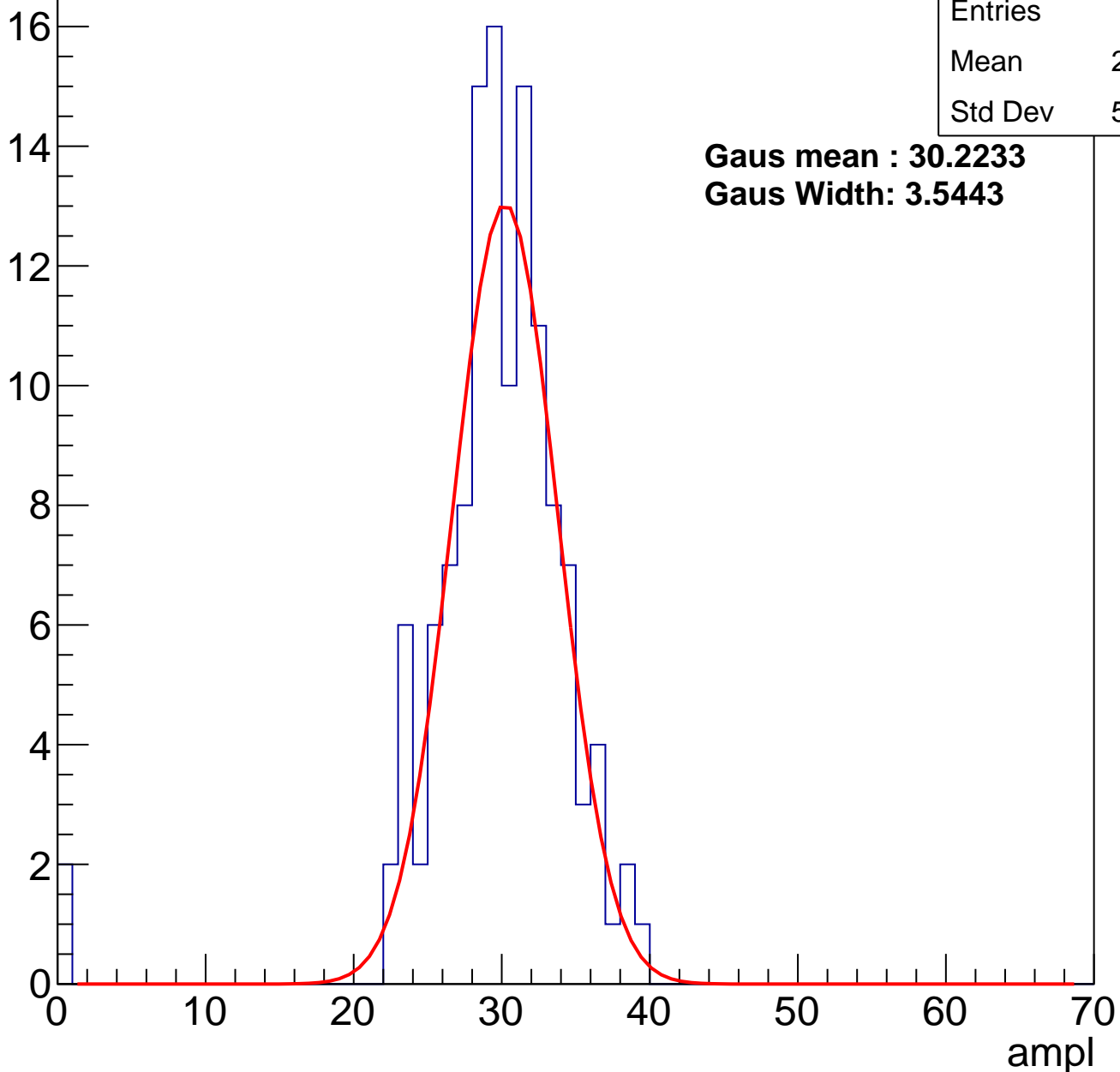
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.22
Std Dev	5.155

Gaus mean : 30.2233

Gaus Width: 3.5443

Entry



B0L101S, U24-ch83, adc1

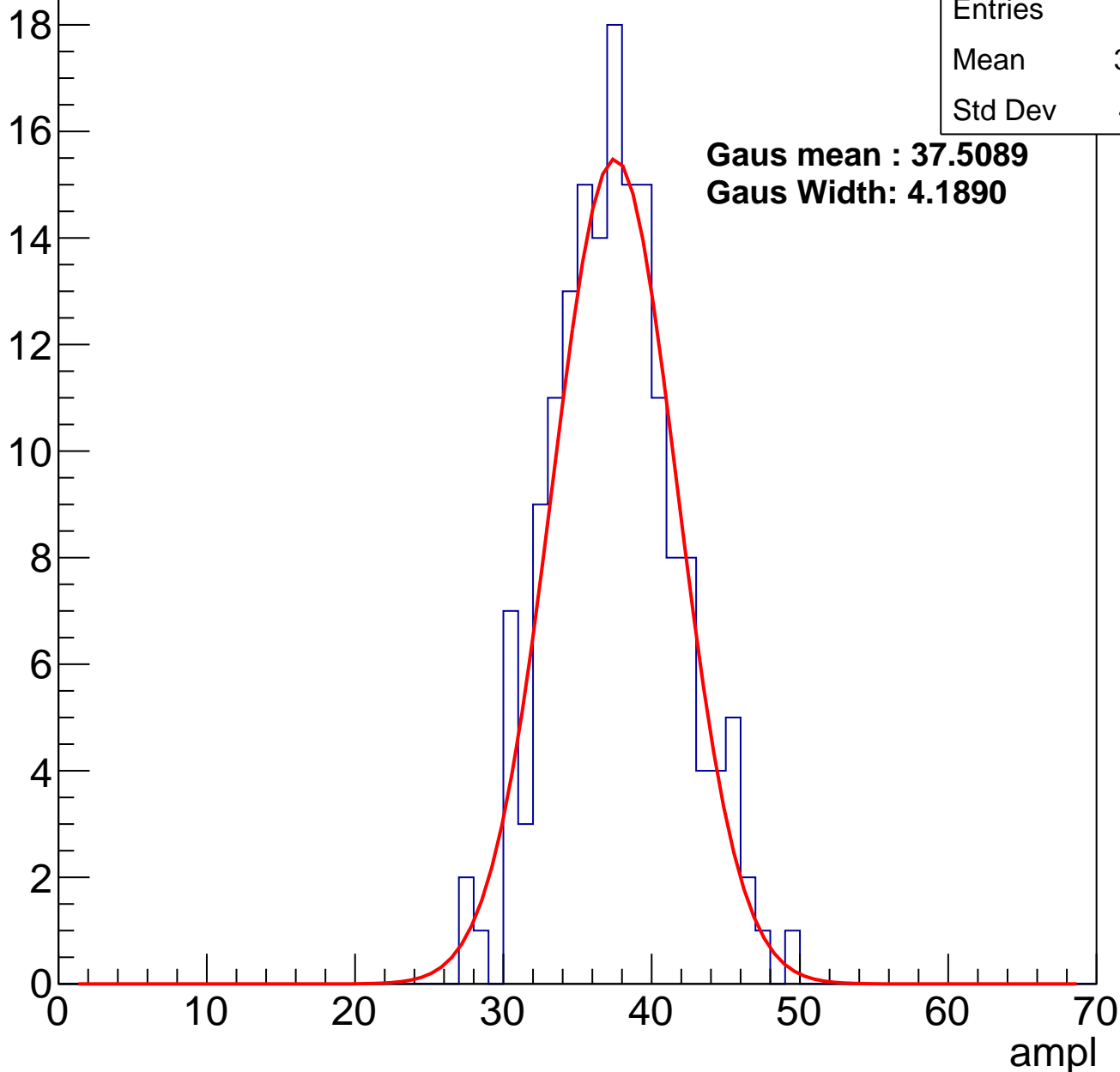
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	37.08
Std Dev	4.161

Gaus mean : 37.5089

Gaus Width: 4.1890

Entry



B0L101S, U24-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

123

Mean

43.84

Std Dev

3.729

Gaus mean : 44.2681

Gaus Width: 3.4416

0

10

20

30

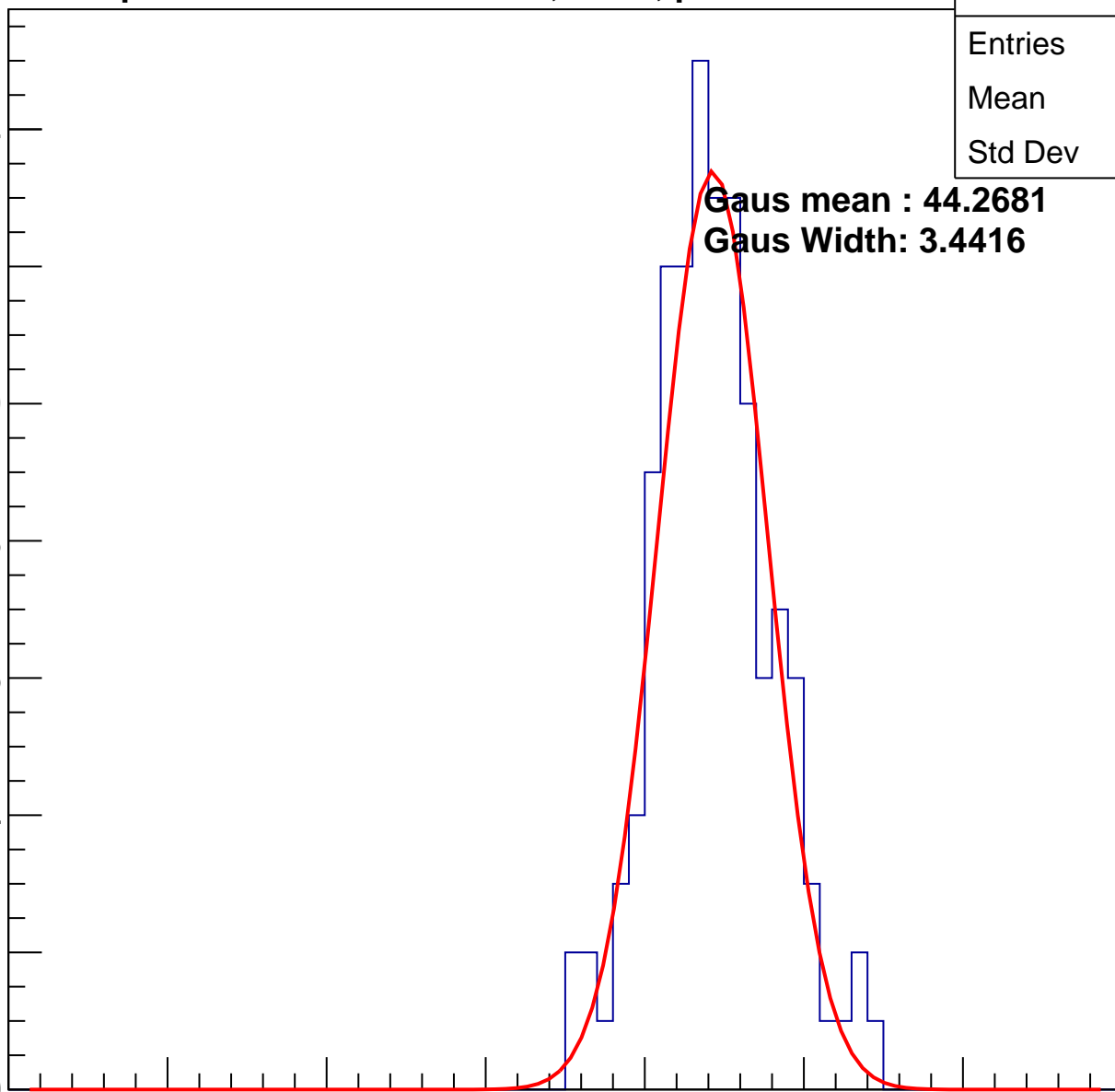
40

50

60

70

ampl

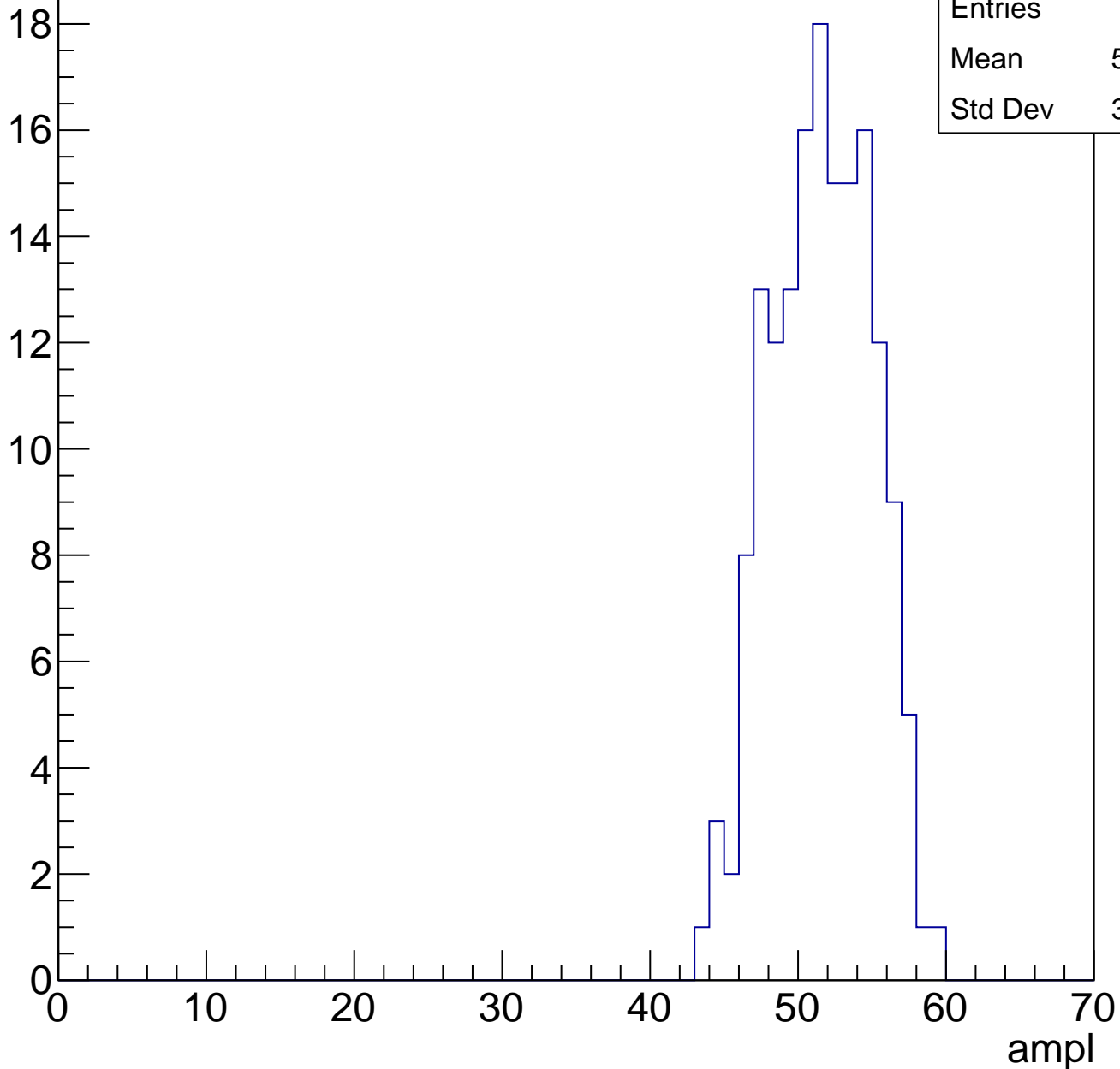


B0L101S, U24-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	51.12
Std Dev	3.348

Entry



B0L101S, U24-ch83, adc4

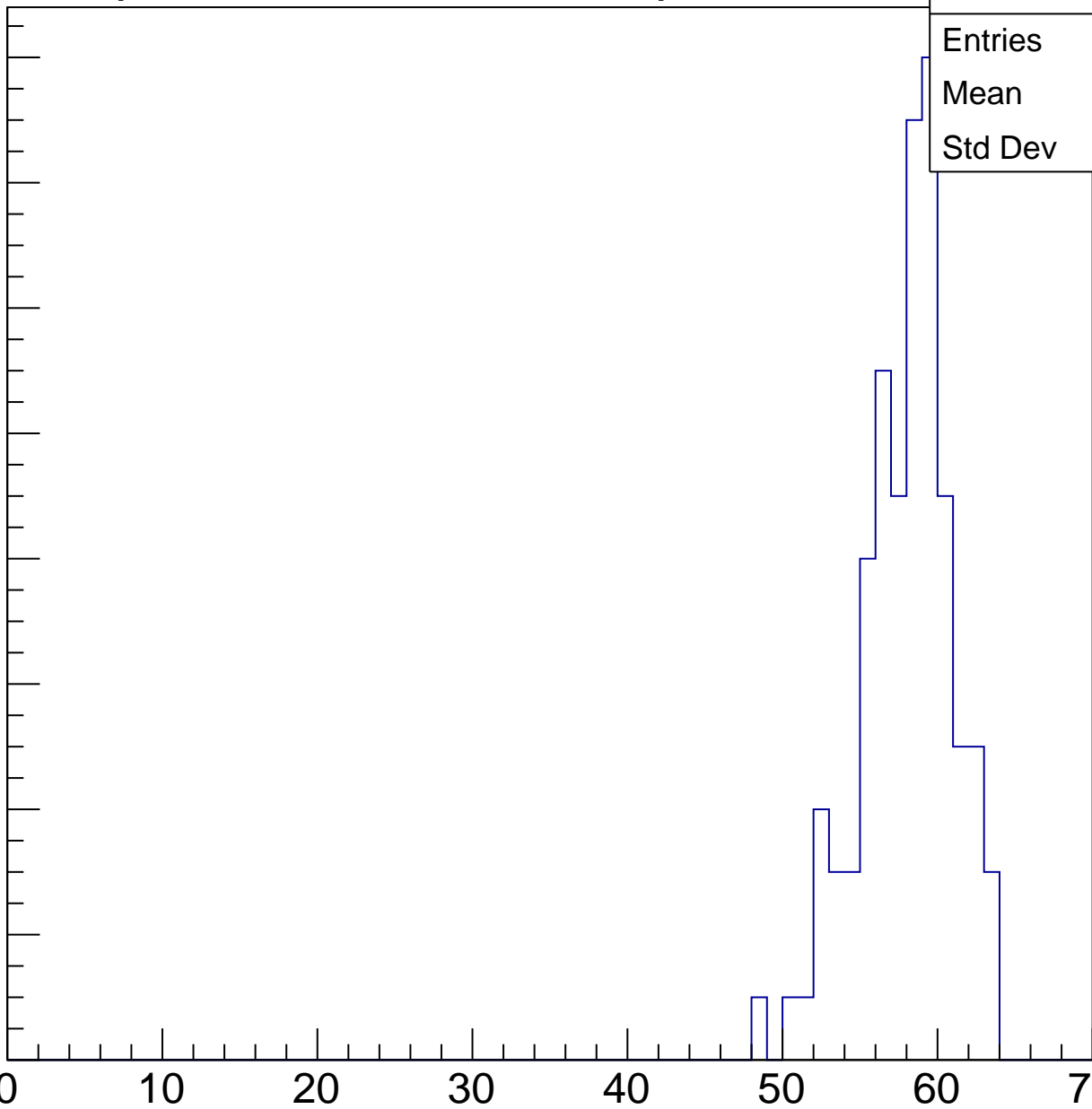
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	57.5
Std Dev	2.978

ampl



B0L101S, U24-ch83, adc5

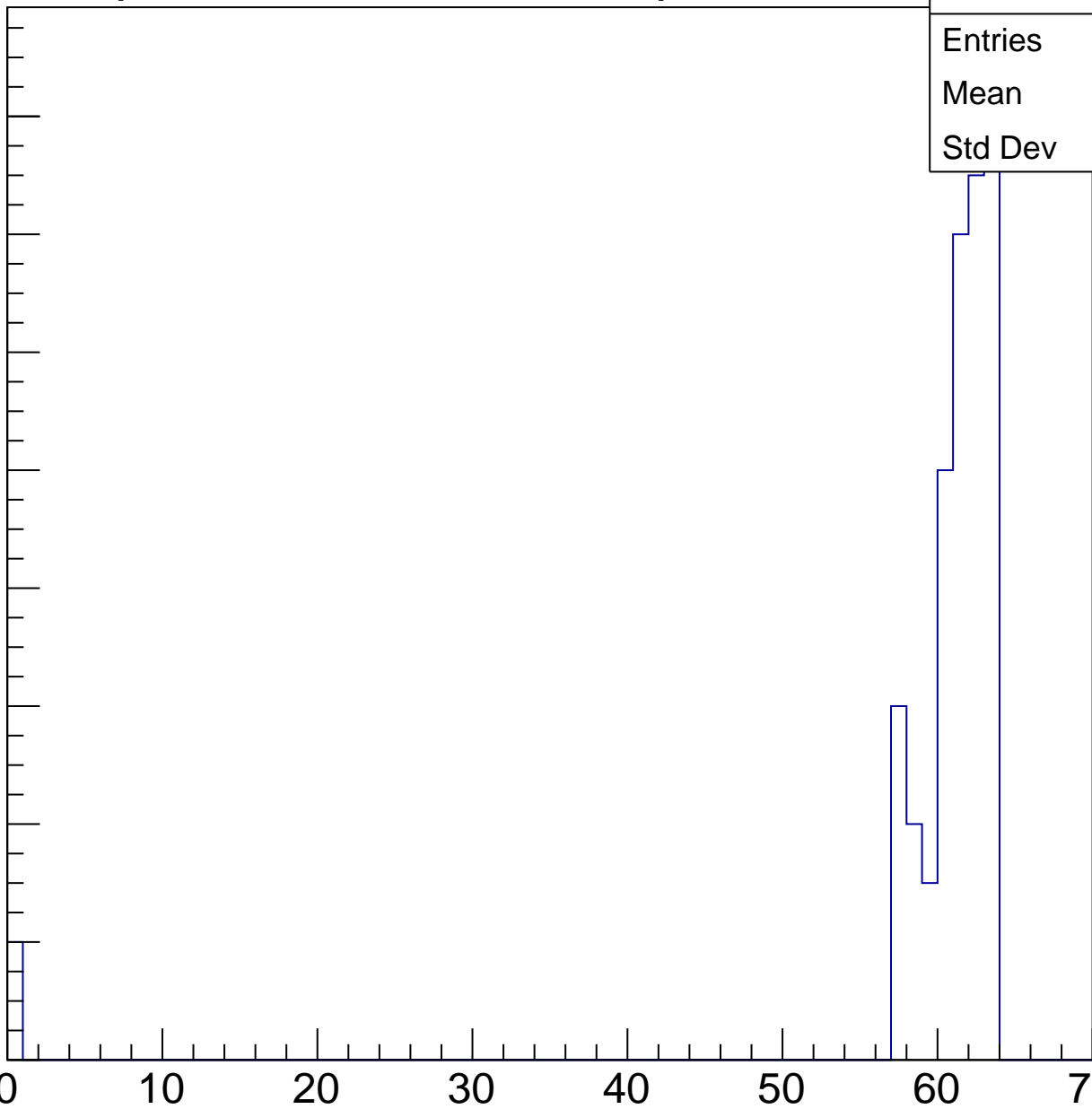
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	71
Mean	59.24
Std Dev	10.25

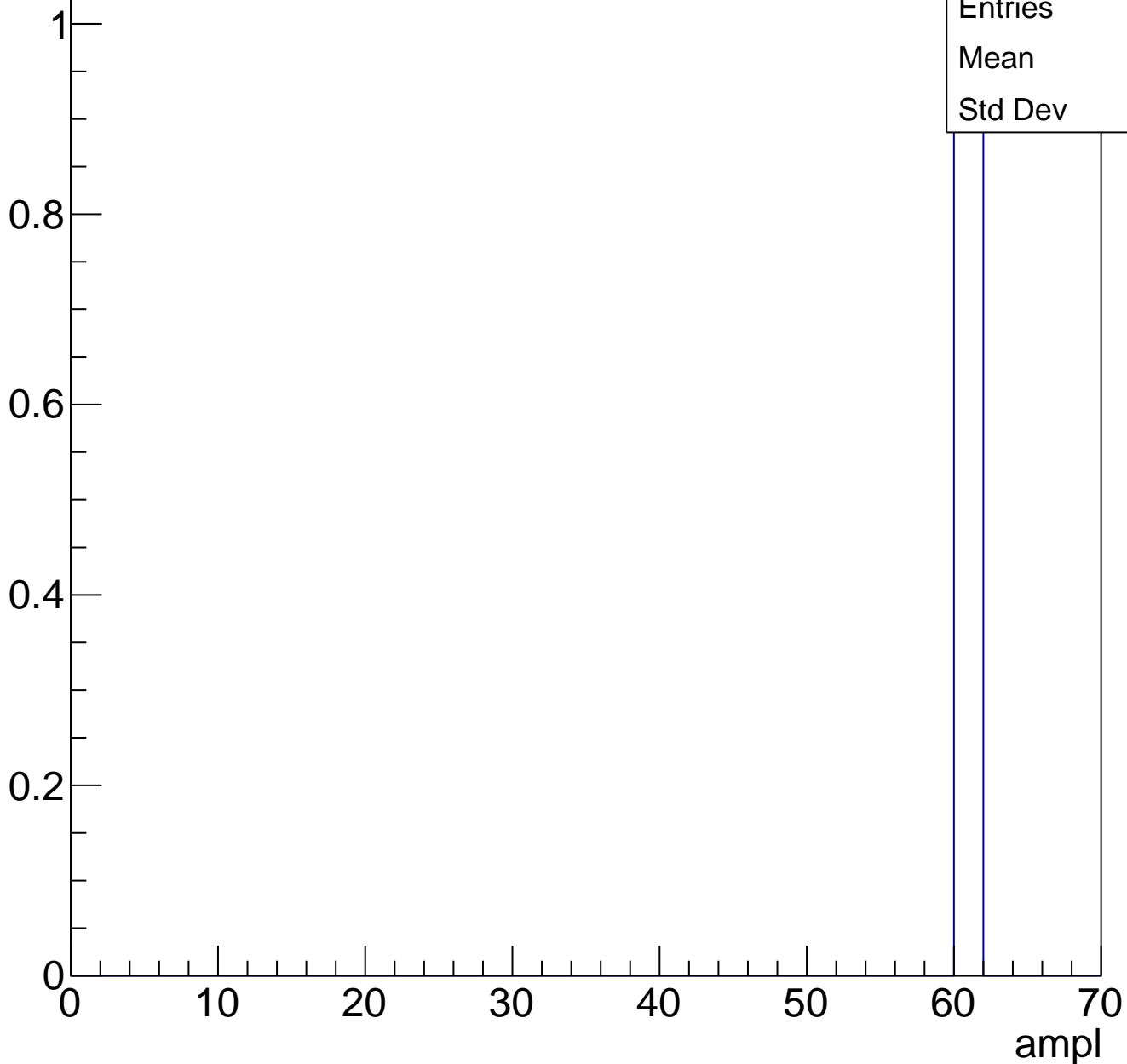
ampl



B0L101S, U24-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch84, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	27.89
Std Dev	4.456

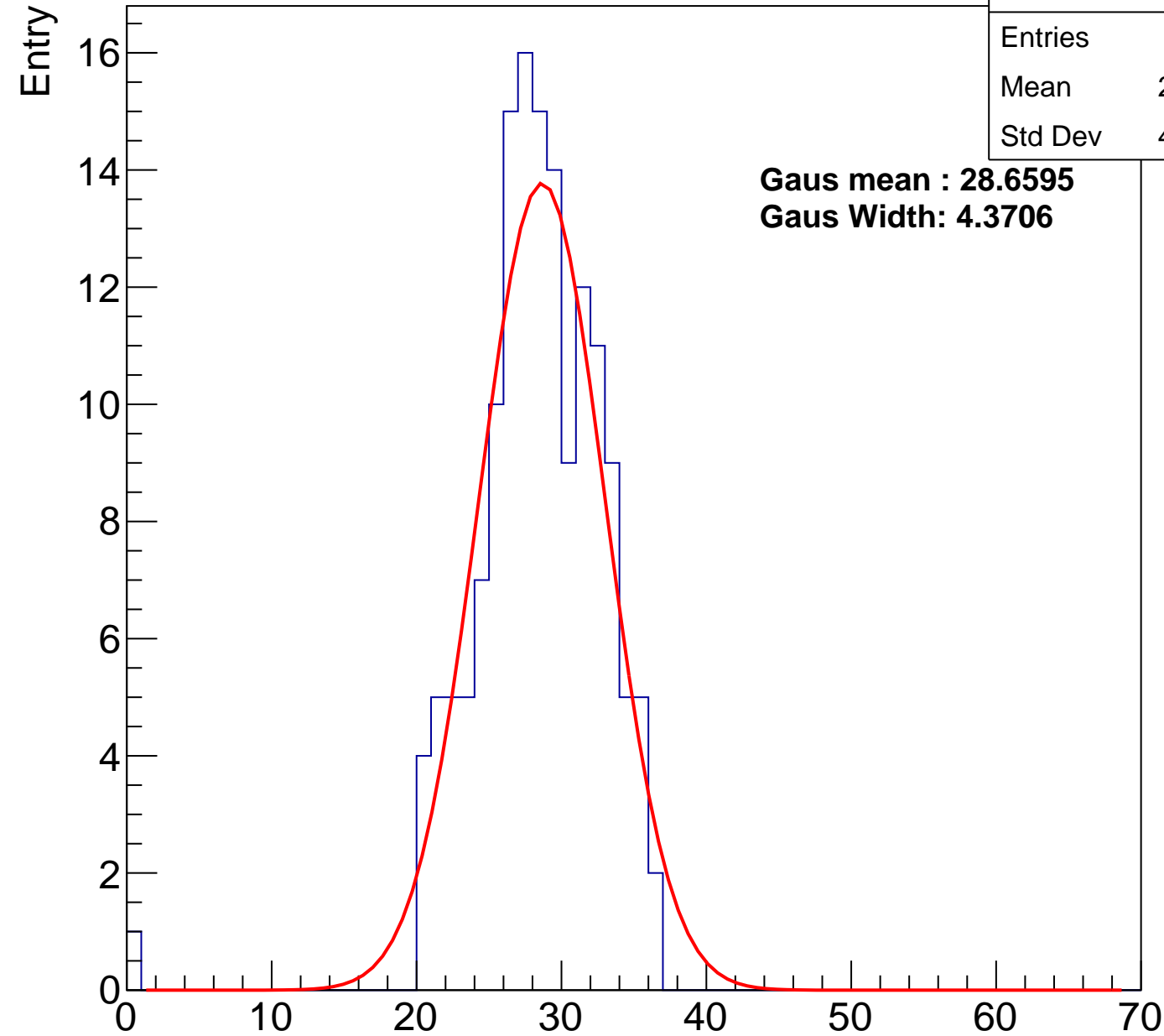
Gaus mean : 28.6595

Gaus Width: 4.3706

Entry

16
14
12
10
8
6
4
2
0

ampl



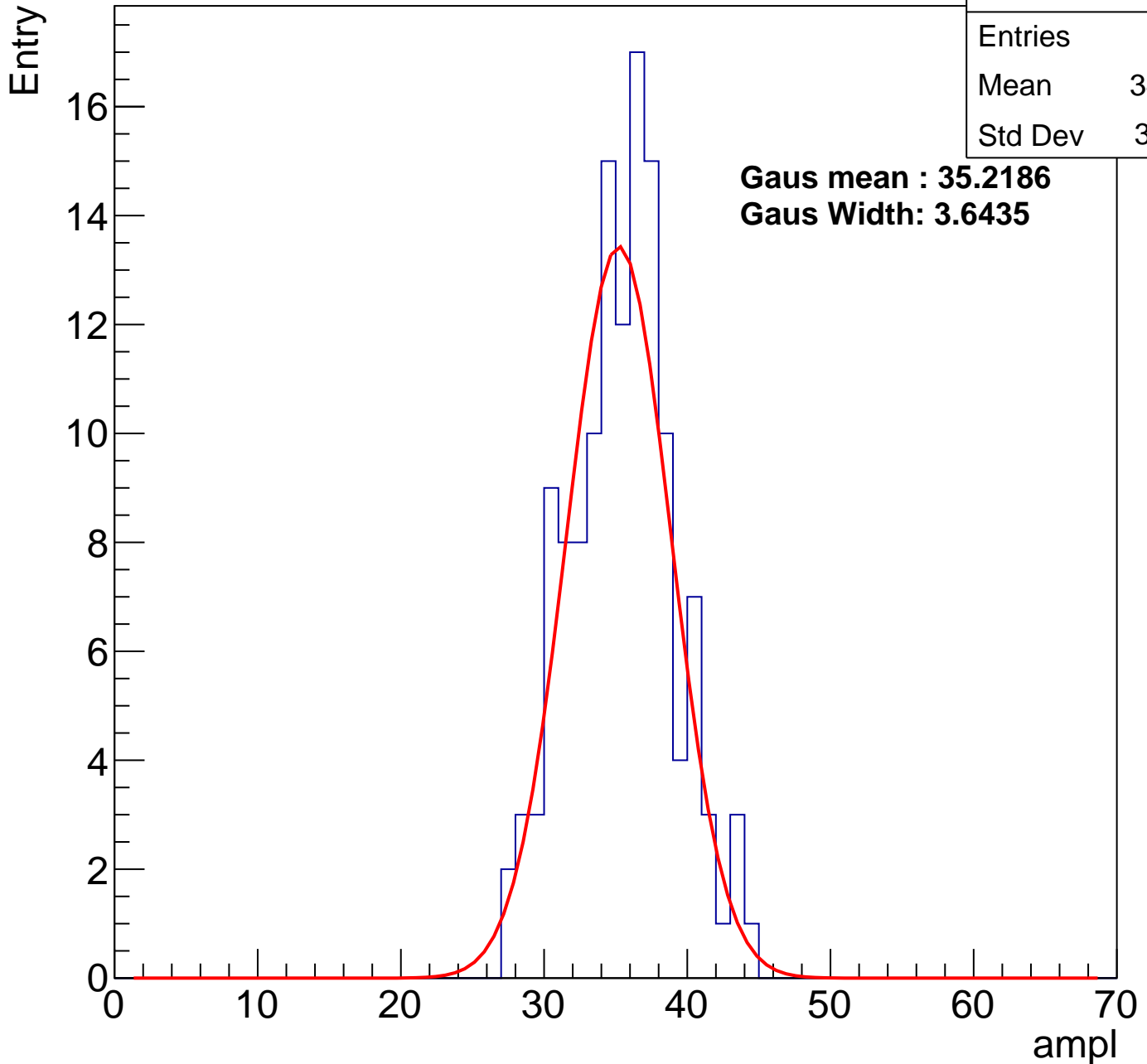
B0L101S, U24-ch84, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	34.96
Std Dev	3.601

Gaus mean : 35.2186

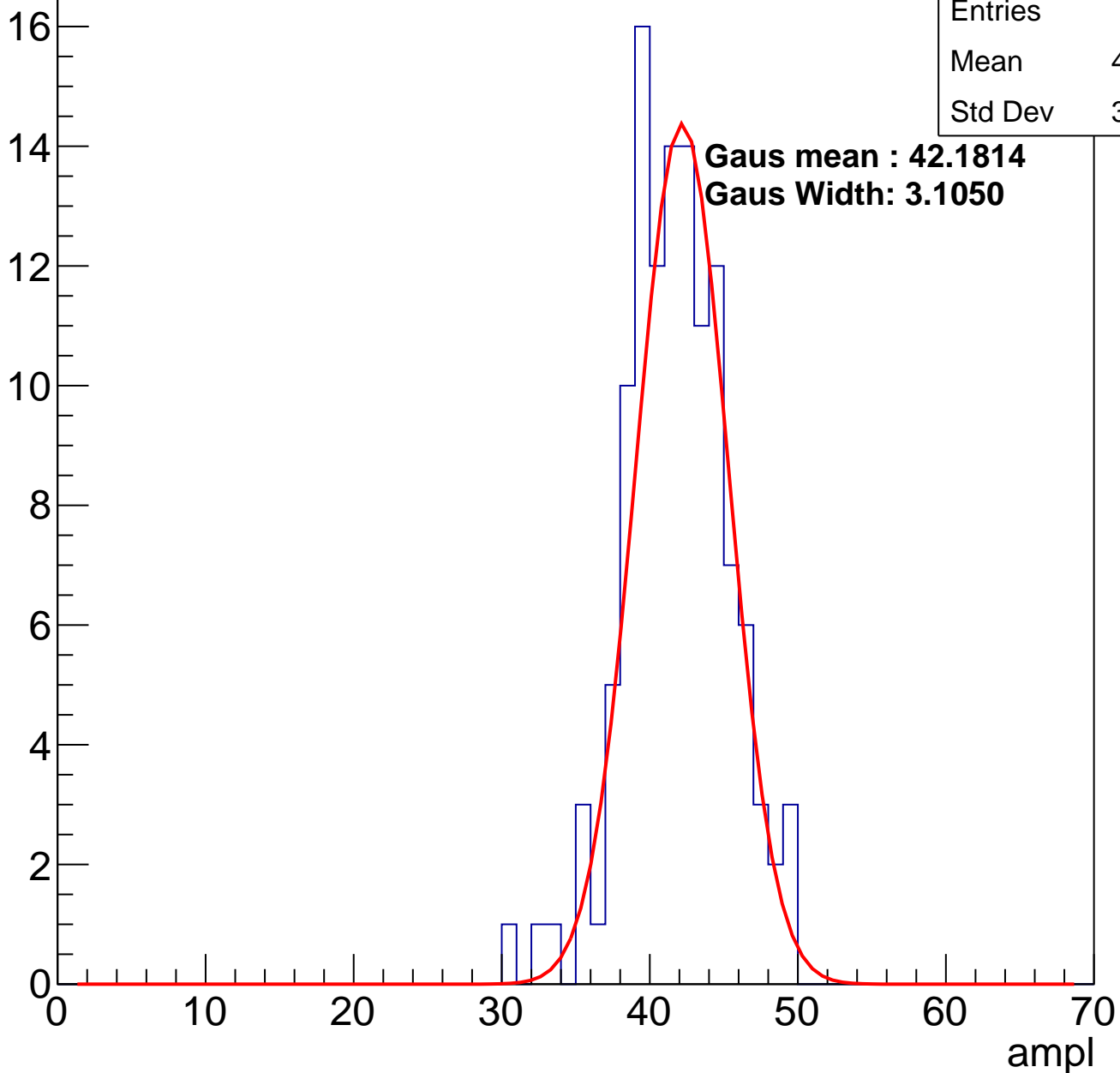
Gaus Width: 3.6435



B0L101S, U24-ch84, adc2

calib_packv5_042523_0143.root, FC#1, port C1

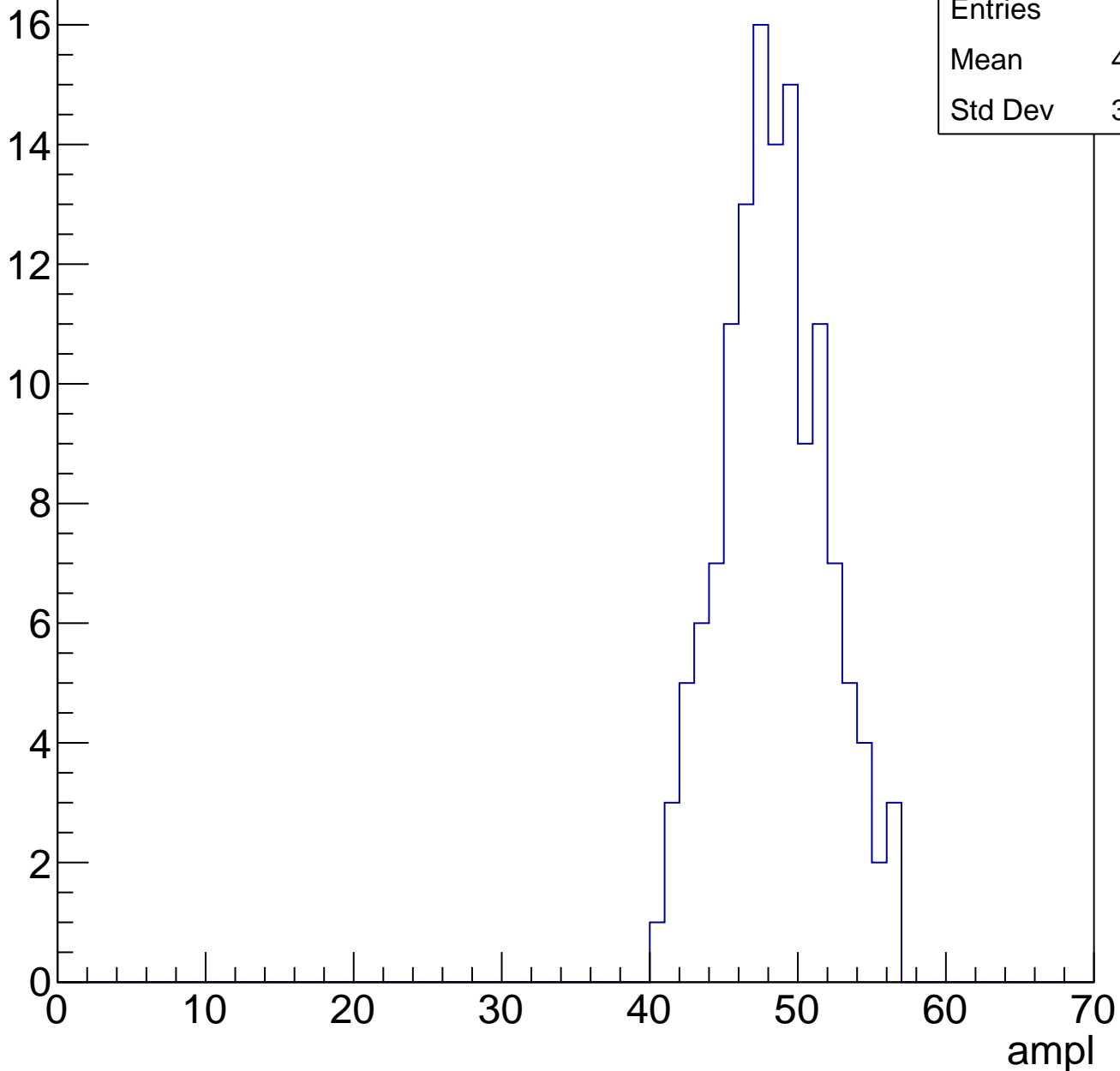
Entry



B0L101S, U24-ch84, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch84, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	54.08
Std Dev	3.618

Entry

12

10

8

6

4

2

0

0

10

20

30

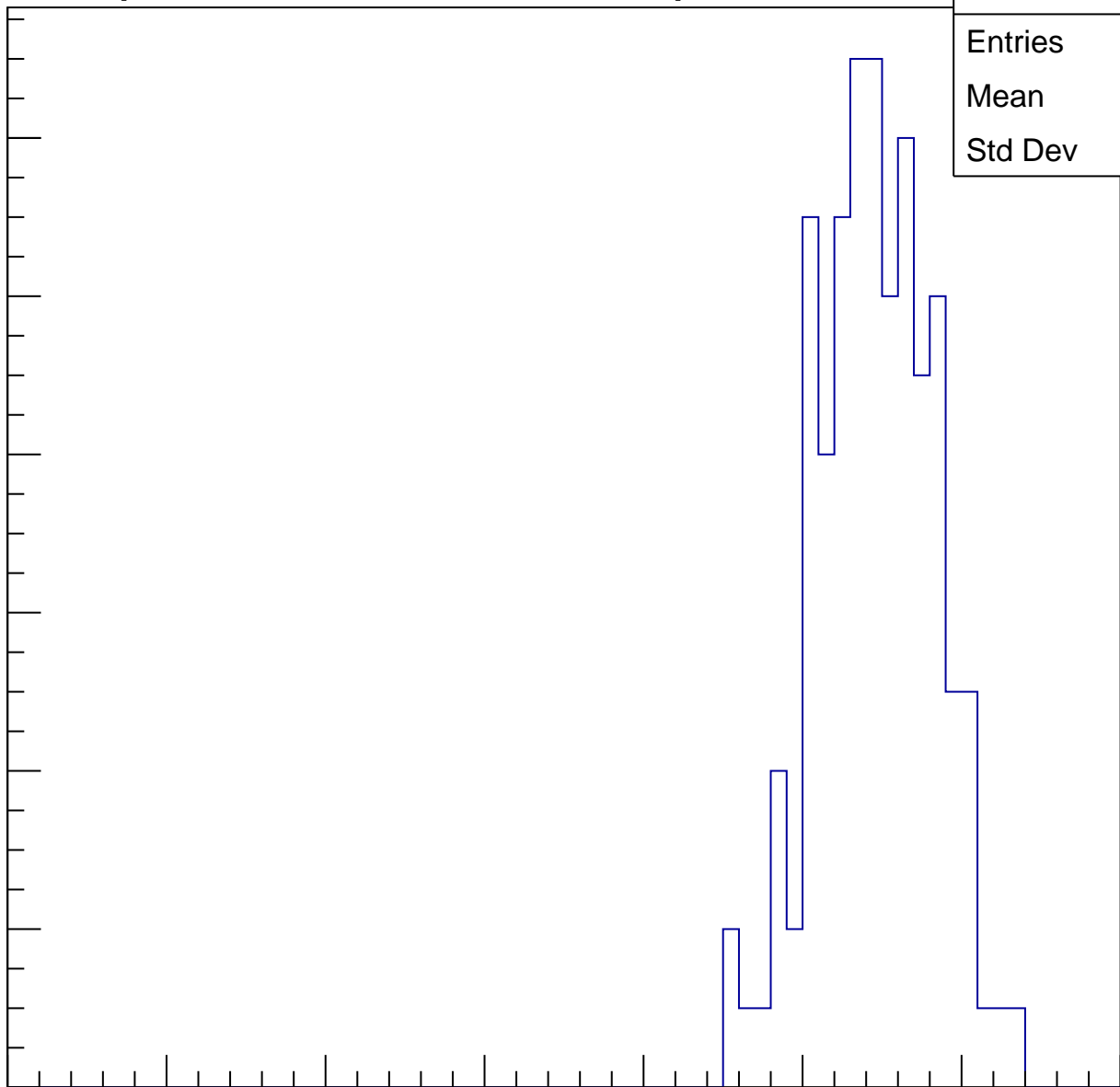
40

50

60

70

ampl



B0L101S, U24-ch84, adc5

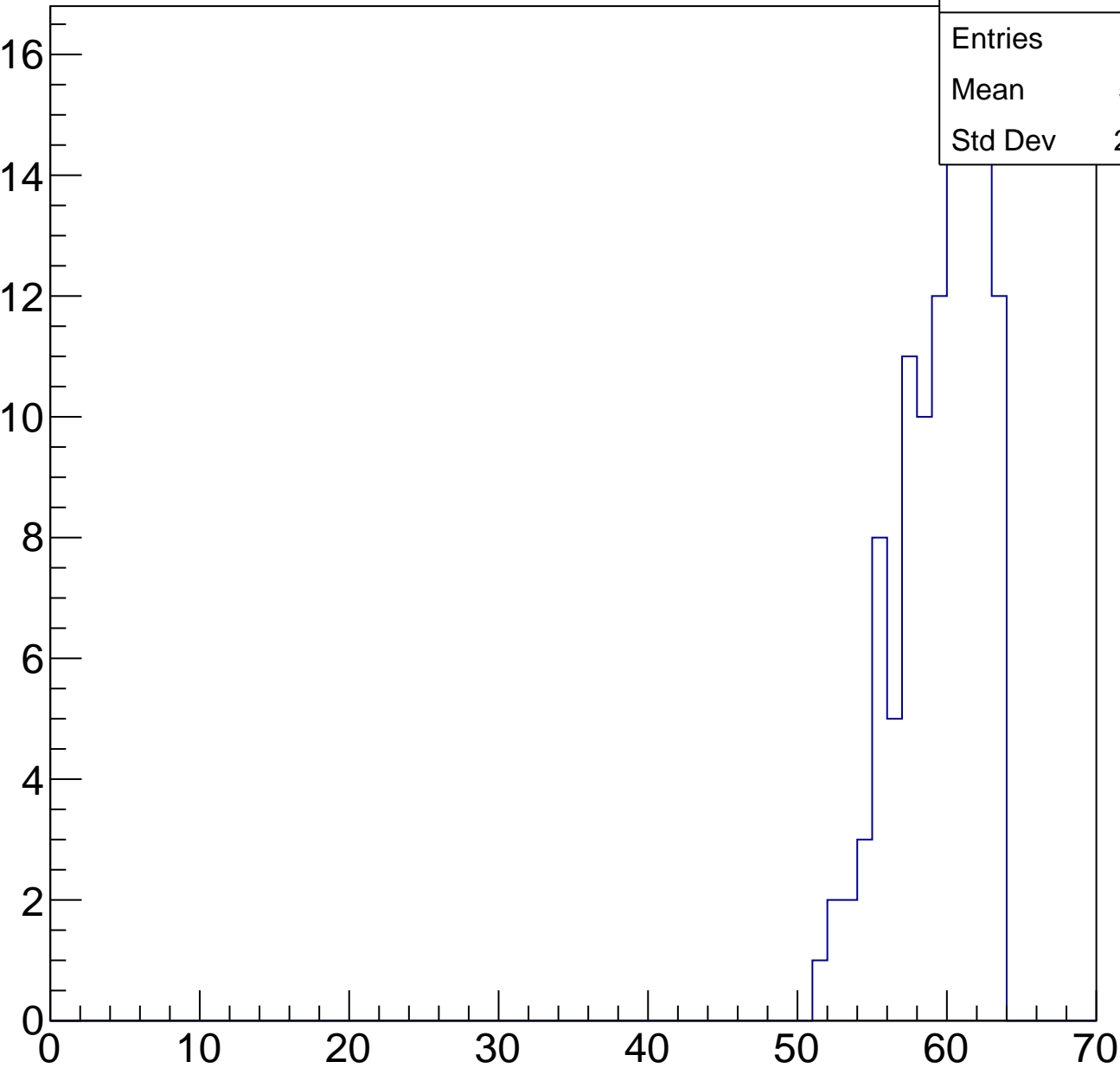
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	59.11
Std Dev	2.898

ampl

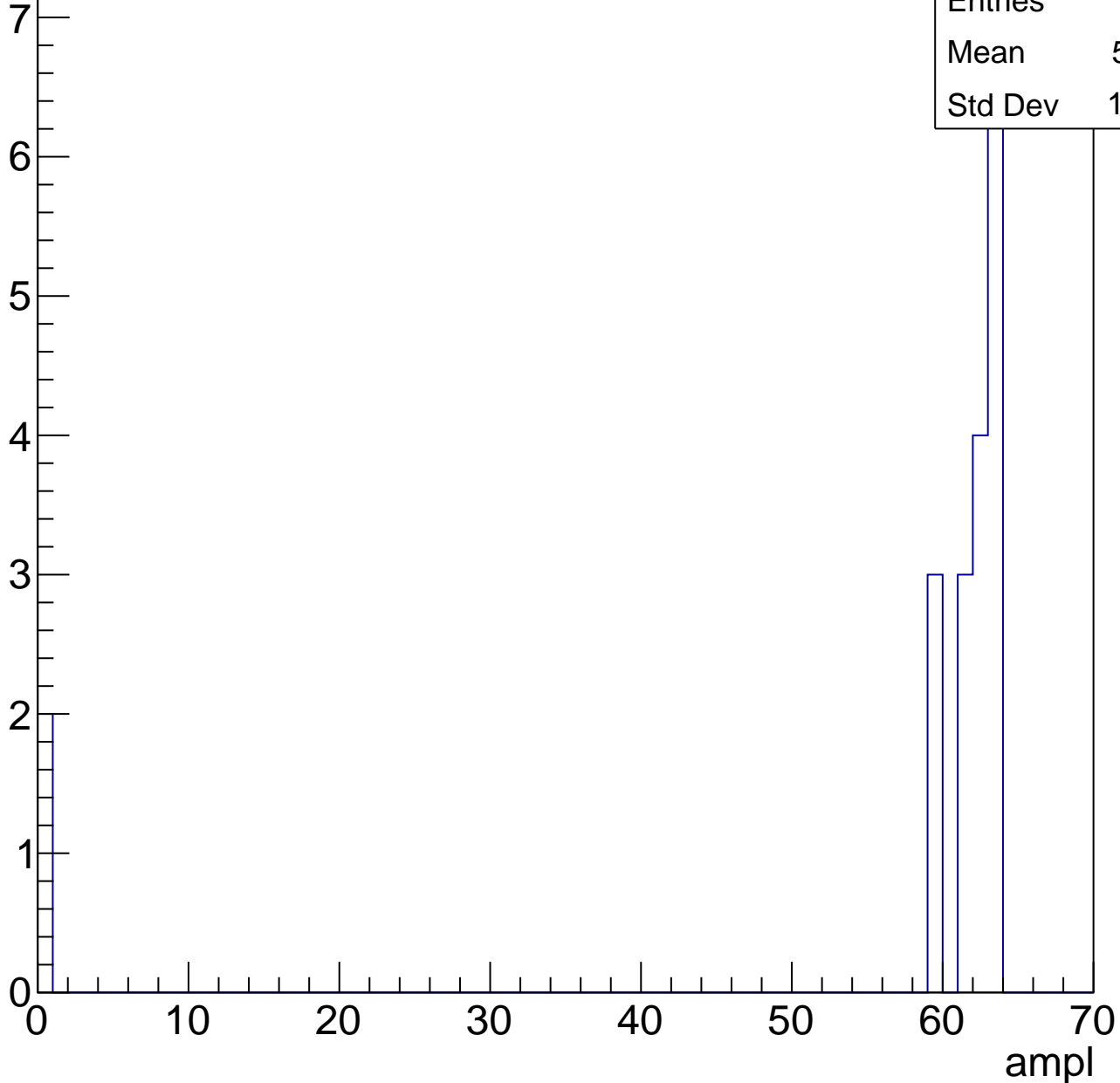


B0L101S, U24-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	55.21
Std Dev	18.99



B0L101S, U24-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch85, adc0

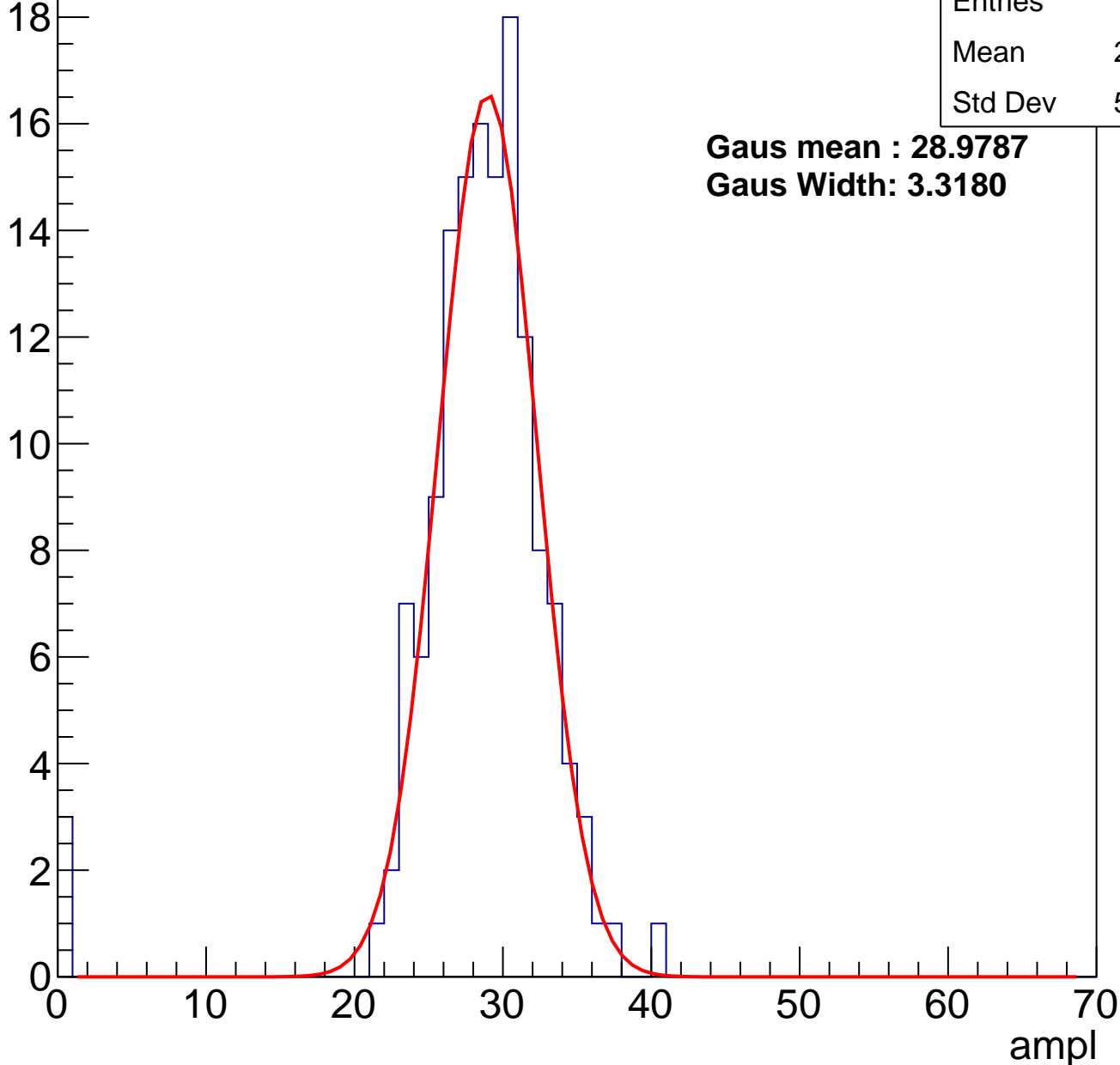
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	27.97
Std Dev	5.272

Gaus mean : 28.9787

Gaus Width: 3.3180

Entry

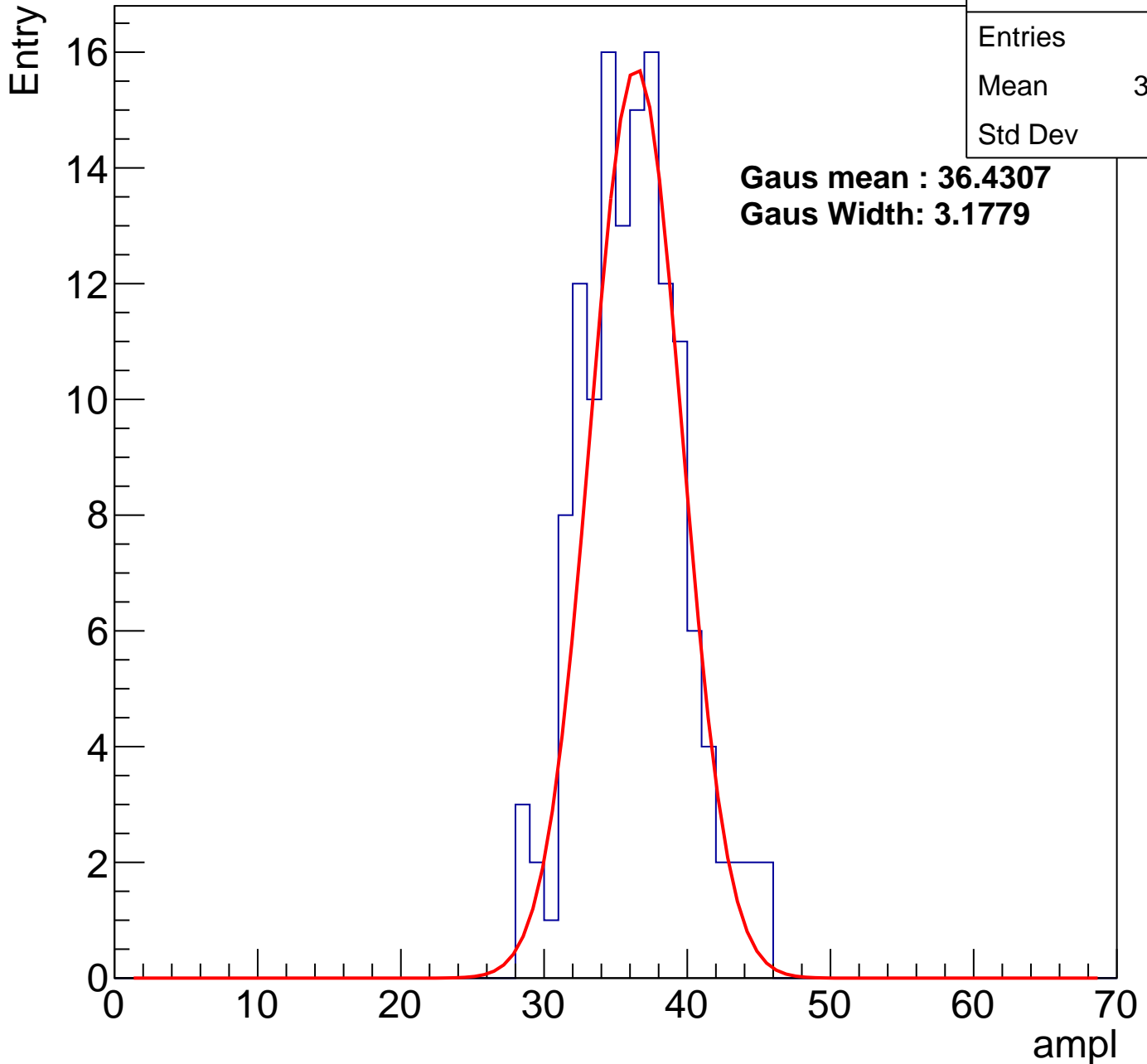


B0L101S, U24-ch85, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	35.78
Std Dev	3.52

Gaus mean : 36.4307
Gaus Width: 3.1779



B0L101S, U24-ch85, adc2

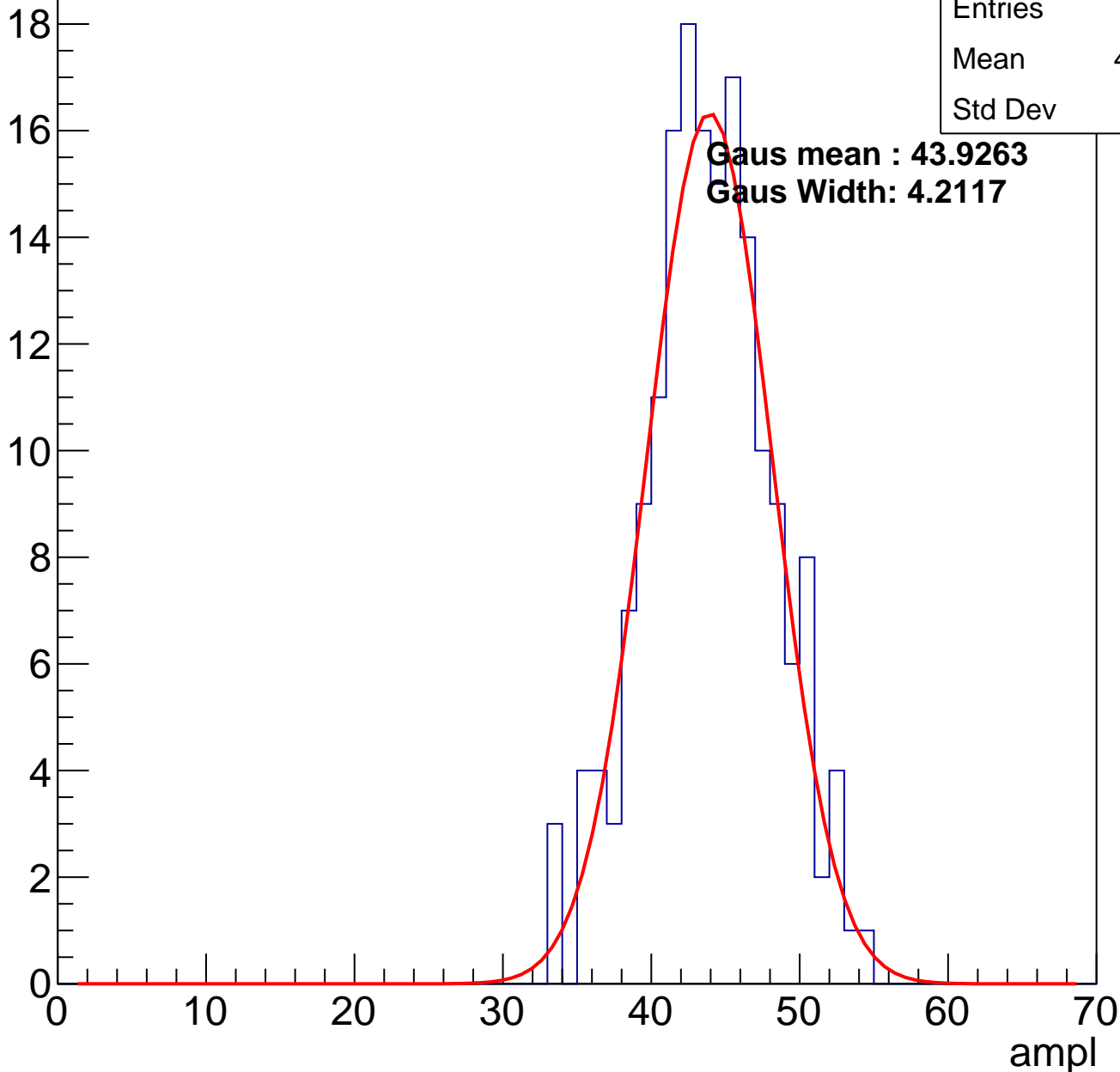
calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	43.44
Std Dev	4.24

Gaus mean : 43.9263

Gaus Width: 4.2117

Entry



B0L101S, U24-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

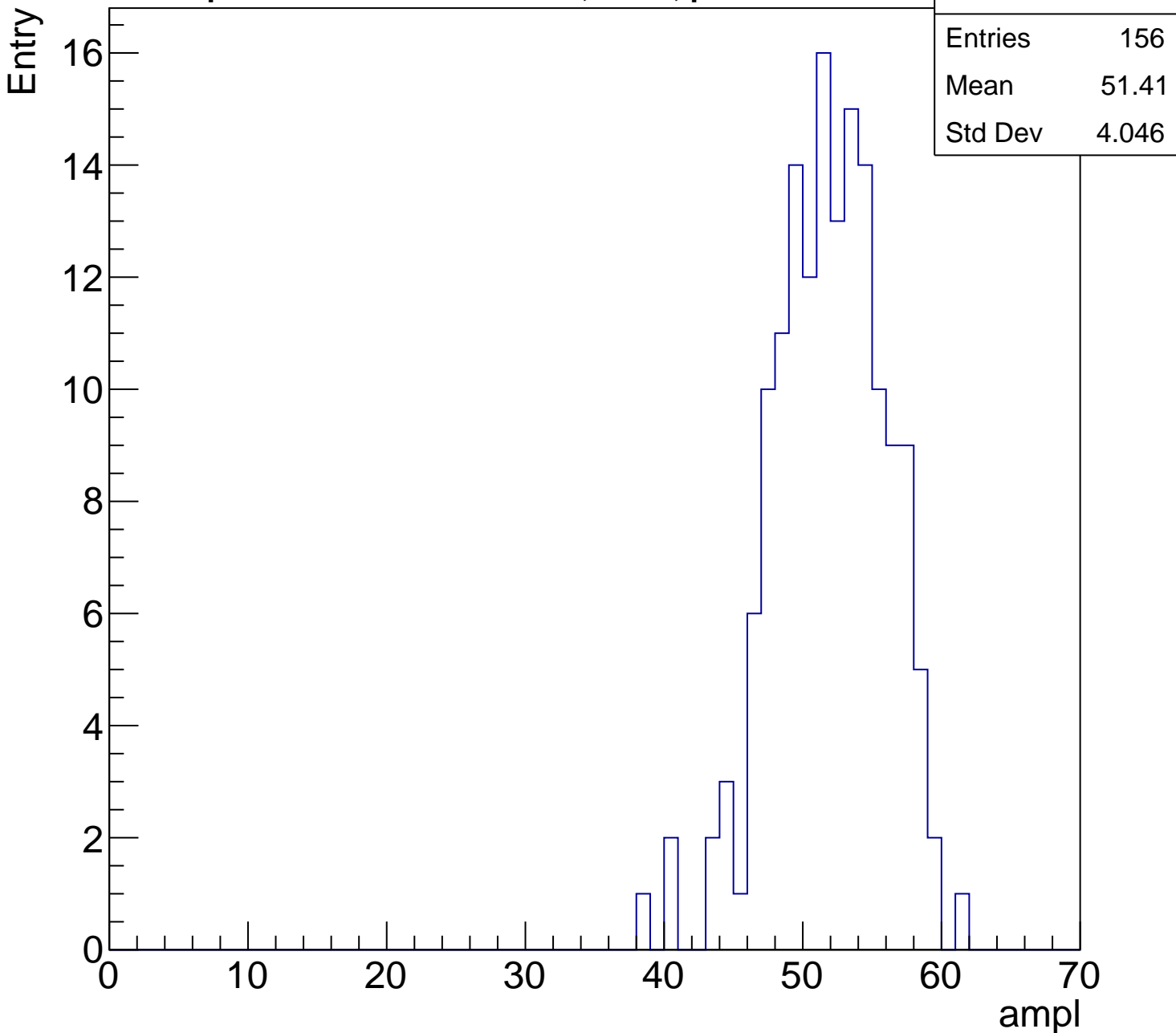
Entries	156
Mean	51.41
Std Dev	4.046

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

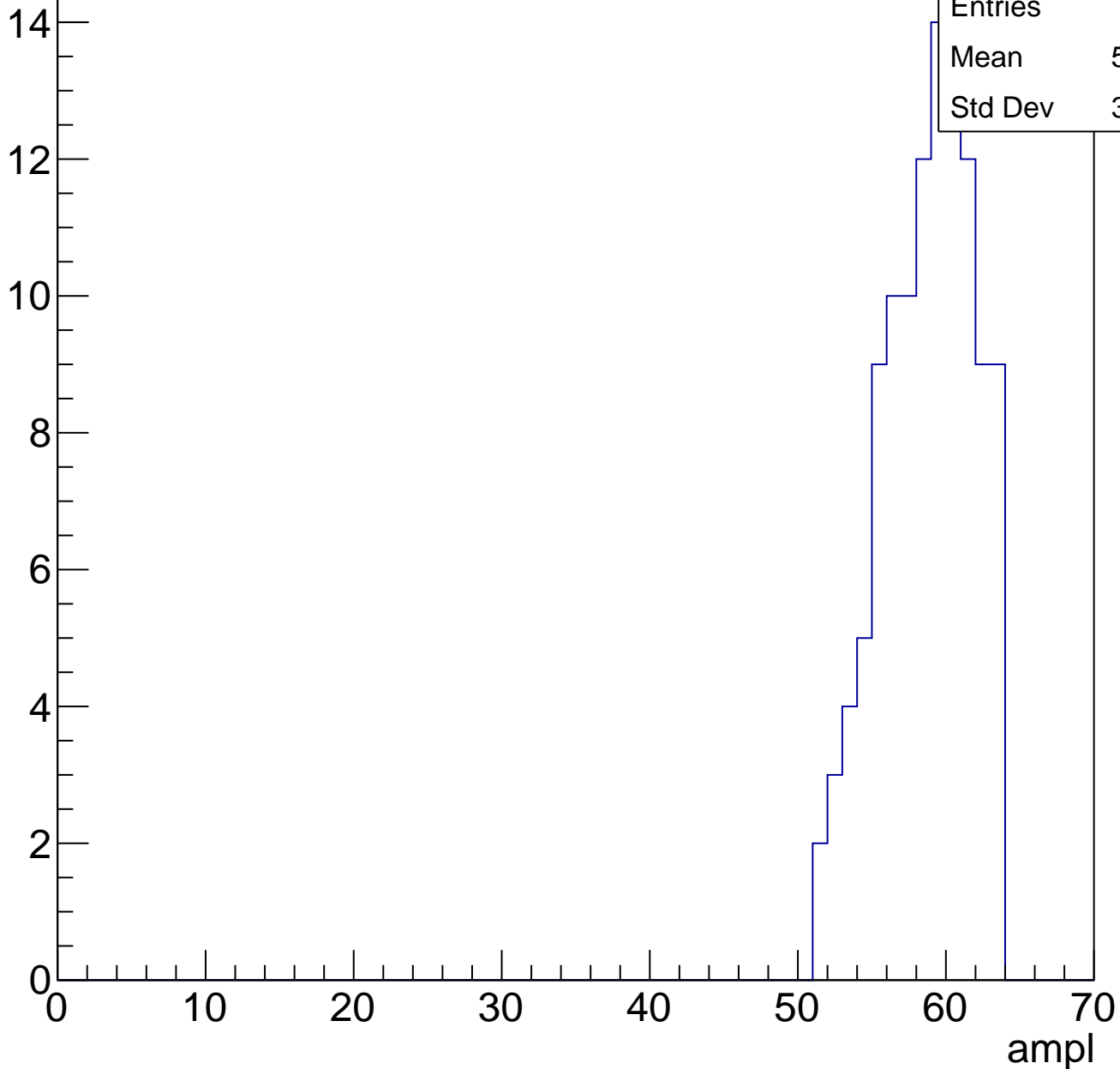
ampl



B0L101S, U24-ch85, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

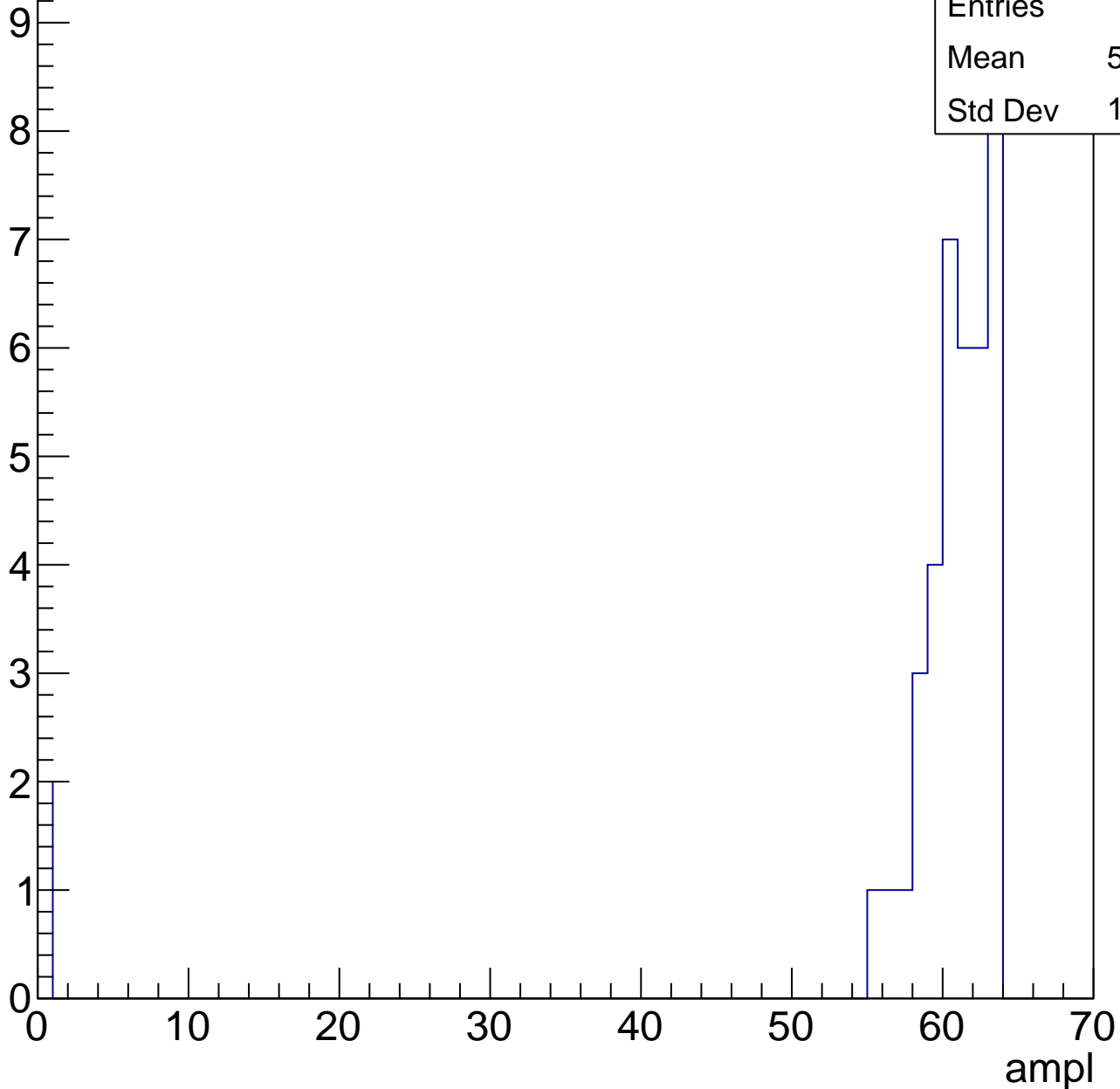


B0L101S, U24-ch85, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	40
Mean	57.58
Std Dev	13.36

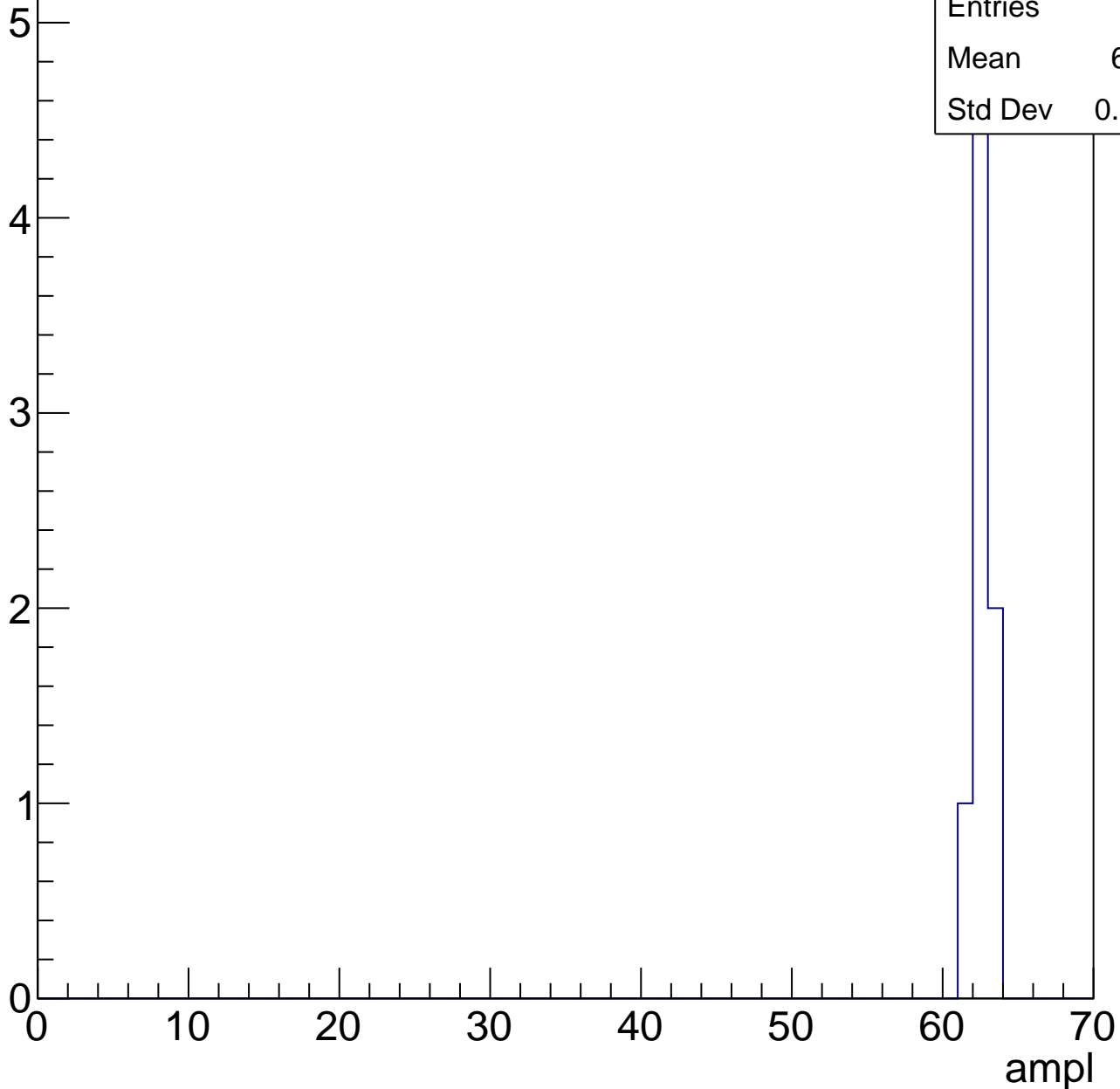


B0L101S, U24-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	62.12
Std Dev	0.5995



B0L101S, U24-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch86, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

132

Mean

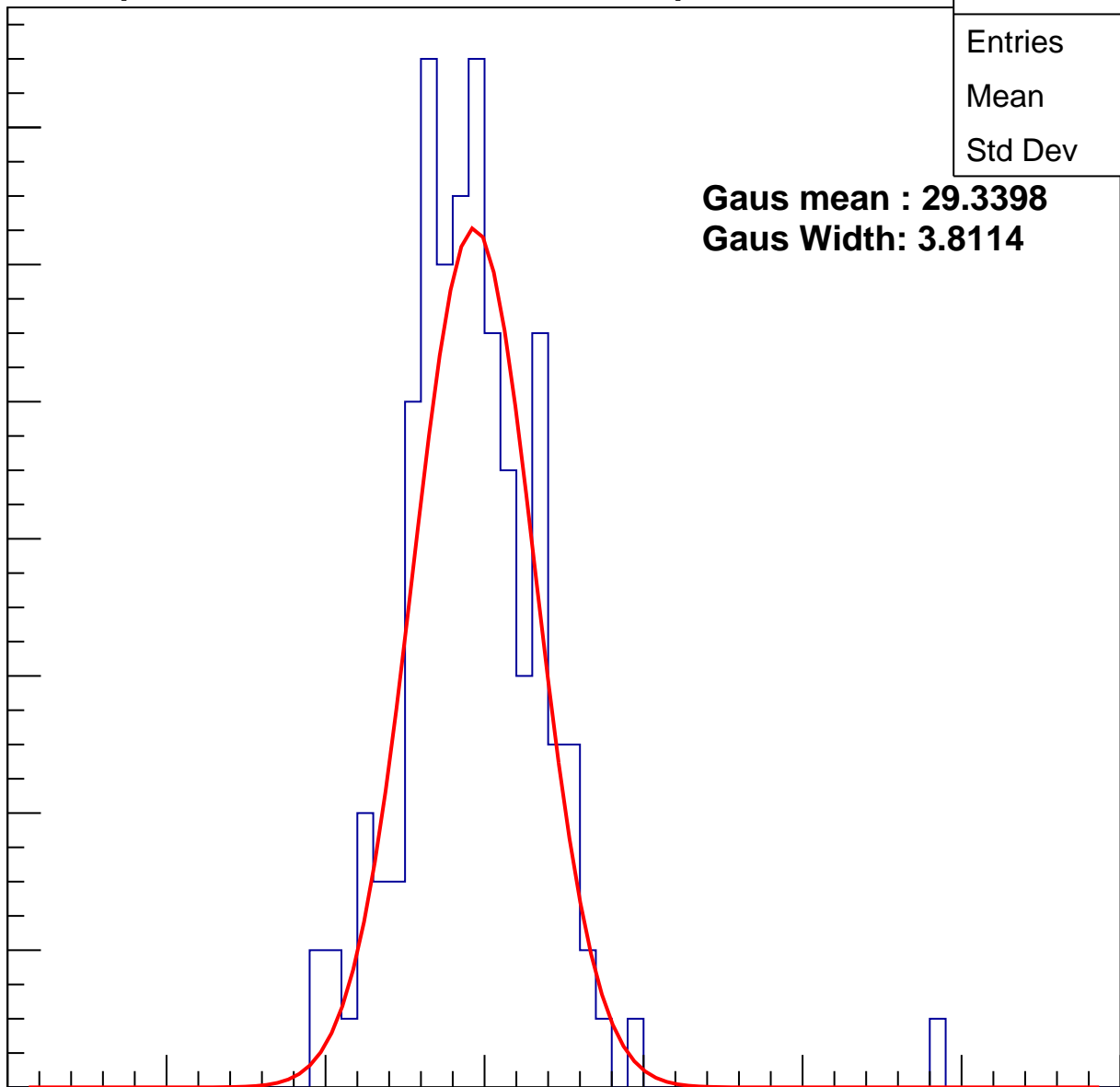
28.83

Std Dev

4.64

Gaus mean : 29.3398

Gaus Width: 3.8114



B0L101S, U24-ch86, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	34.82
Std Dev	3.86

Gaus mean : 35.6840

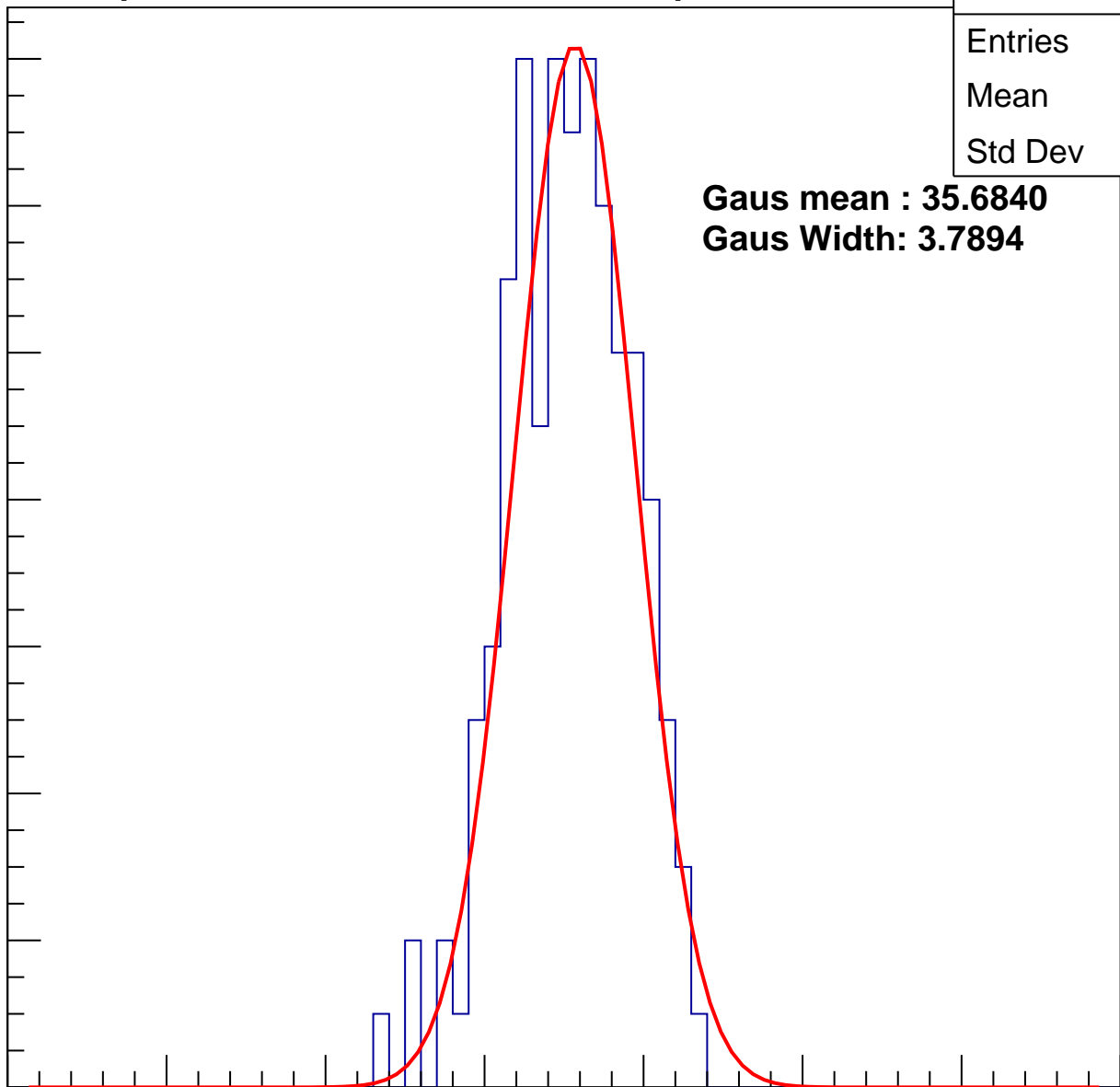
Gaus Width: 3.7894

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

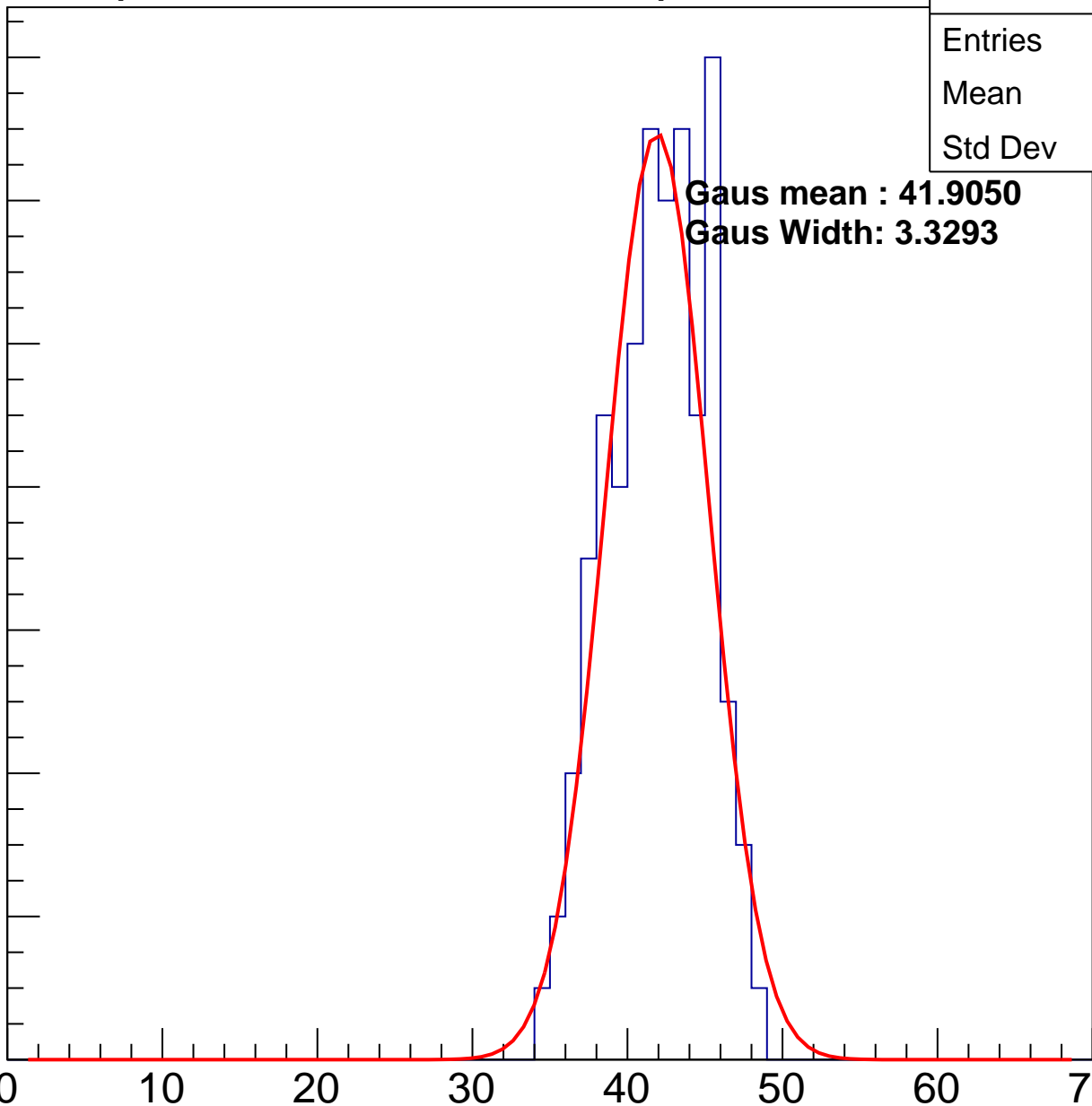
Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	41.46
Std Dev	3.124

Gaus mean : 41.9050
Gaus Width: 3.3293

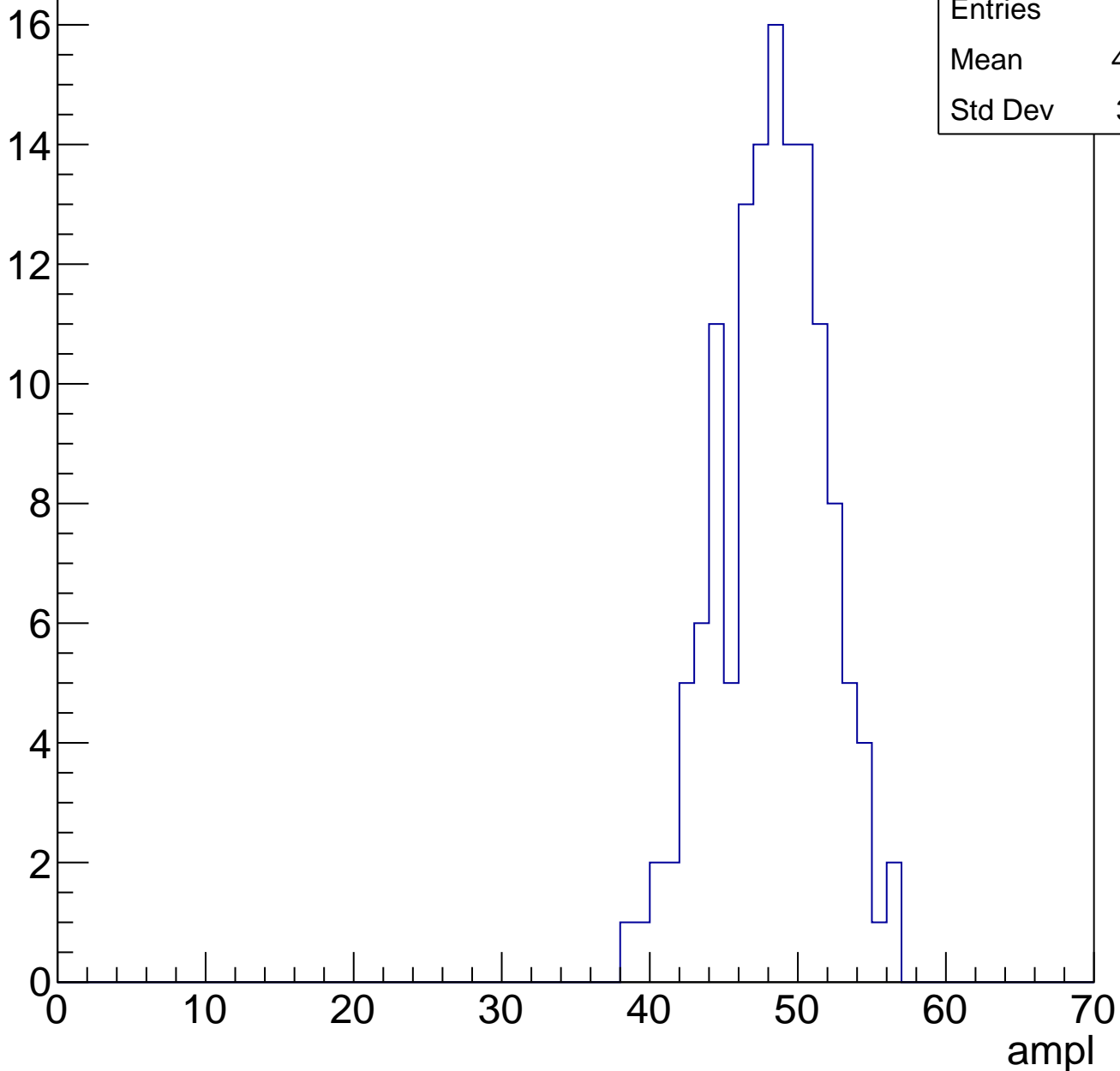
0 10 20 30 40 50 60 70
ampl



B0L101S, U24-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

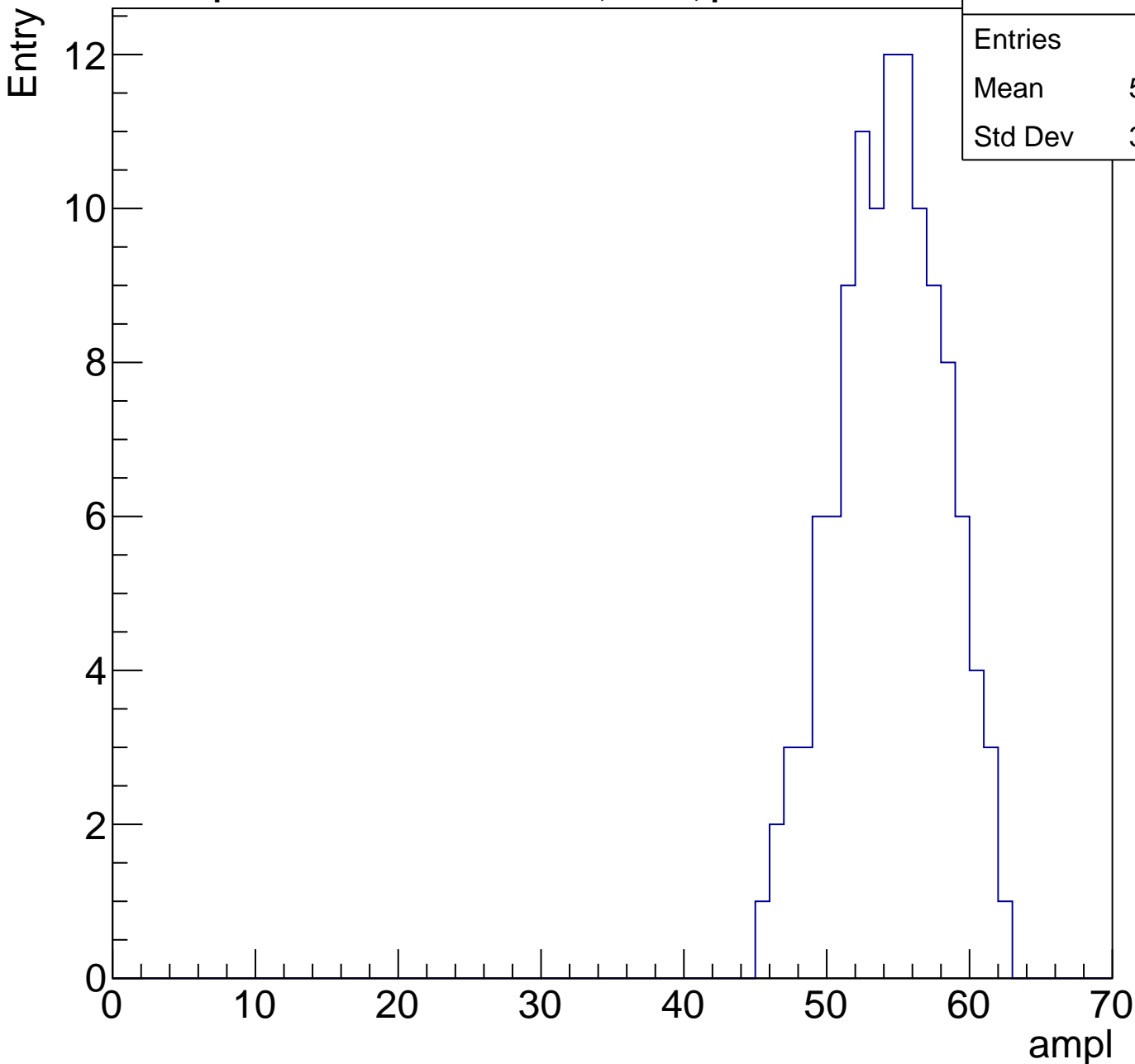


Entries	135
Mean	47.79
Std Dev	3.611

B0L101S, U24-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	53.97
Std Dev	3.727



B0L101S, U24-ch86, adc5

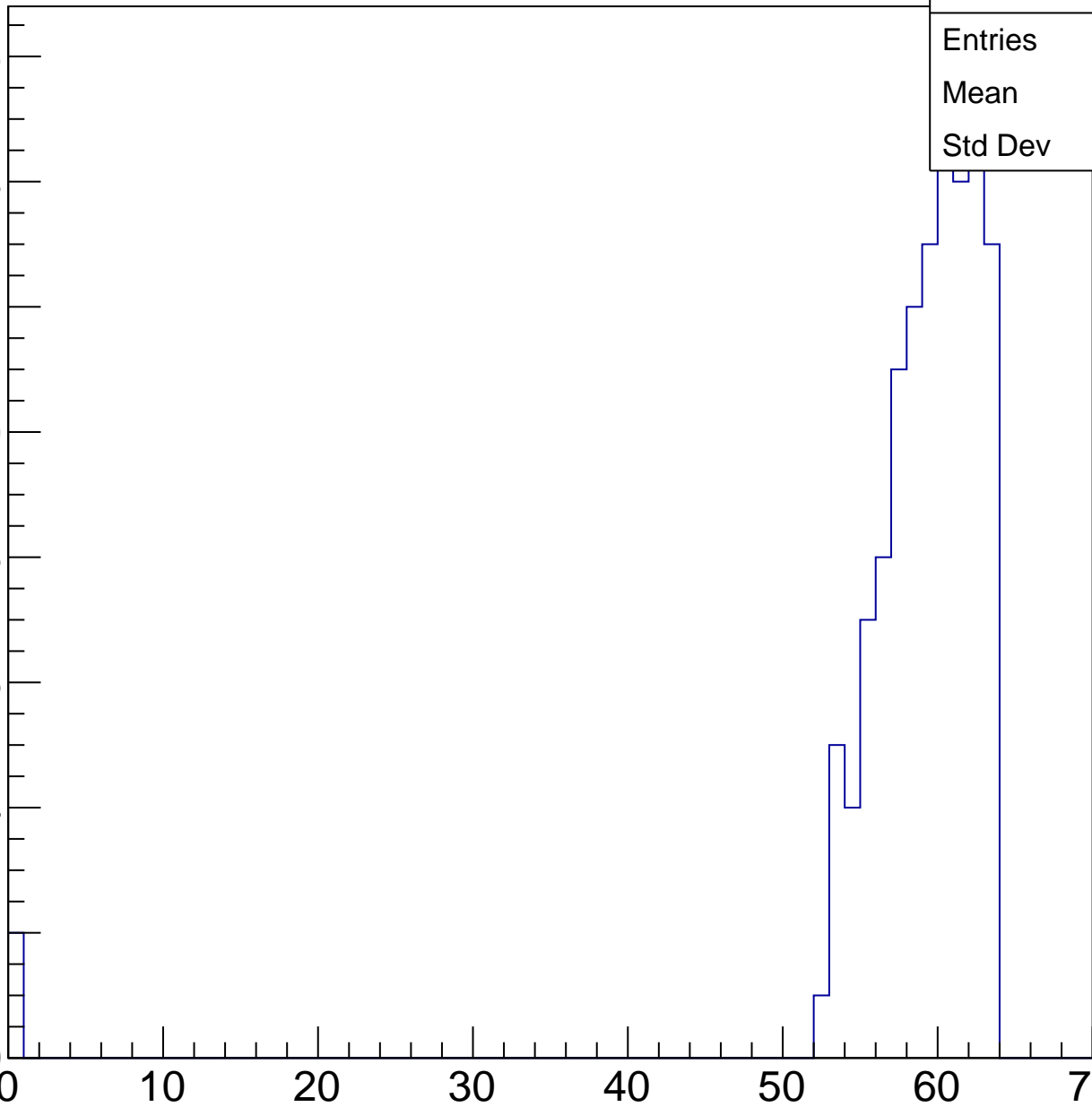
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	58.04
Std Dev	8.018

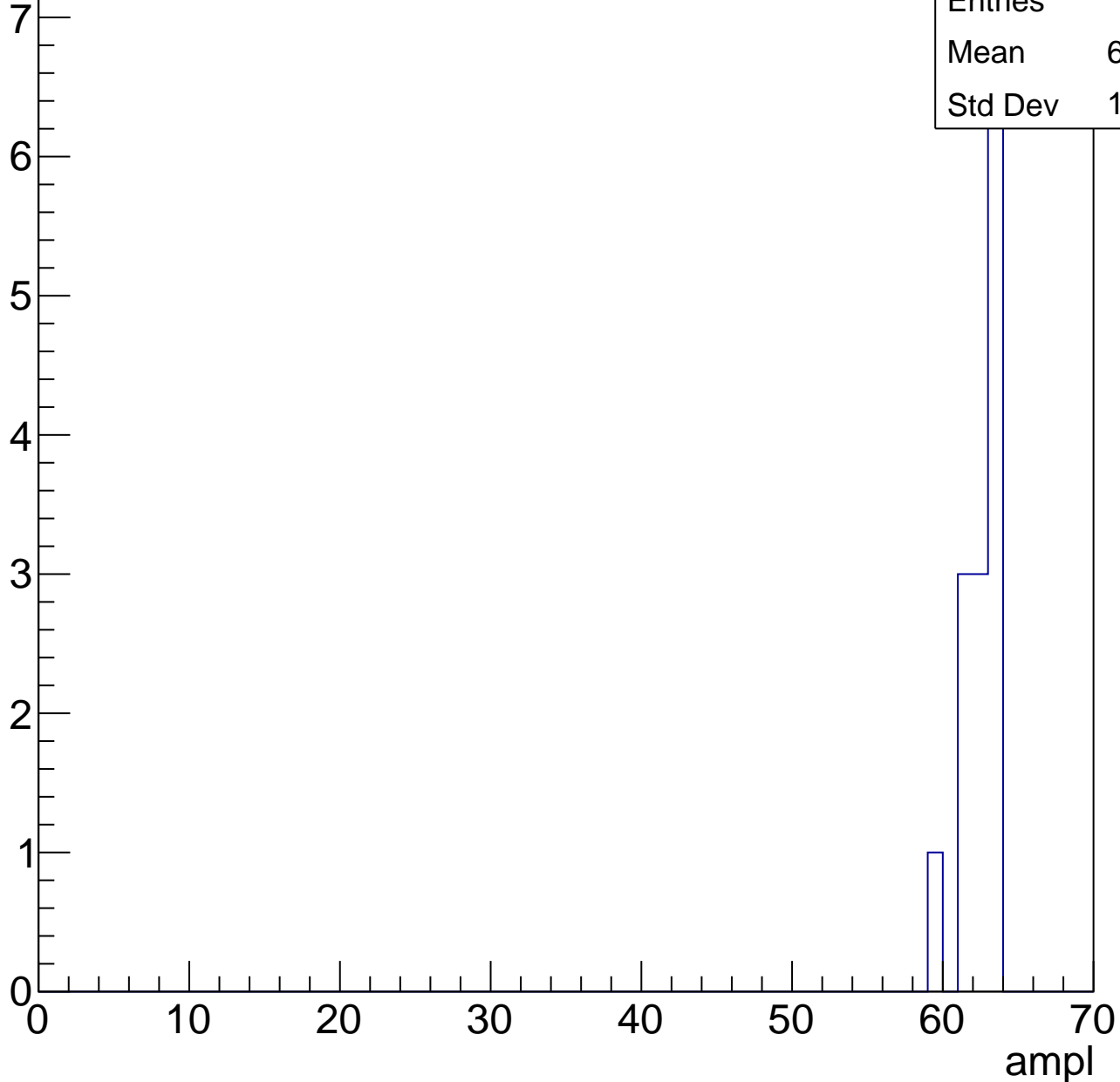
ampl



B0L101S, U24-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch87, adc0

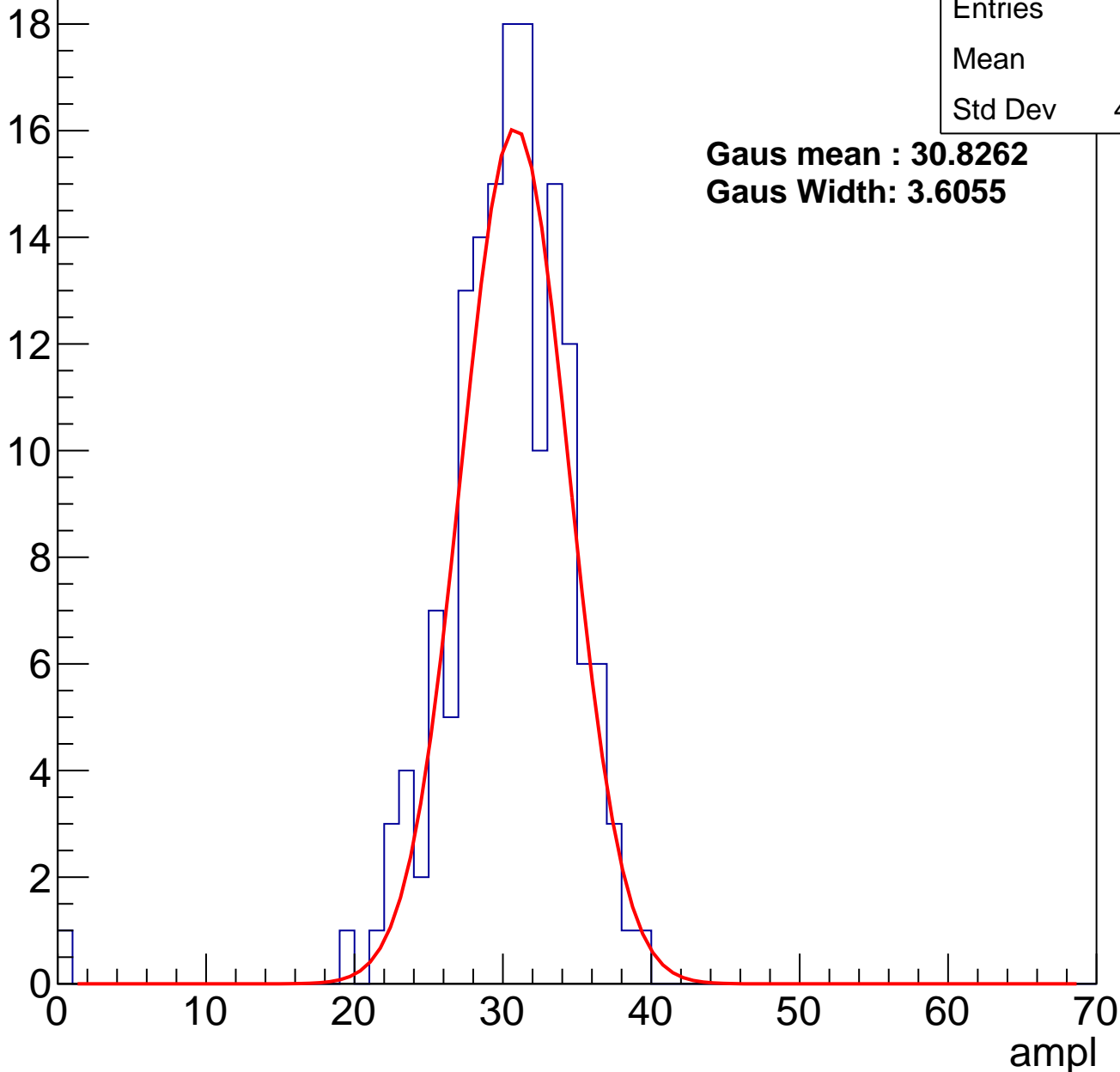
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	29.9
Std Dev	4.419

Gaus mean : 30.8262

Gaus Width: 3.6055

Entry



B0L101S, U24-ch87, adc1

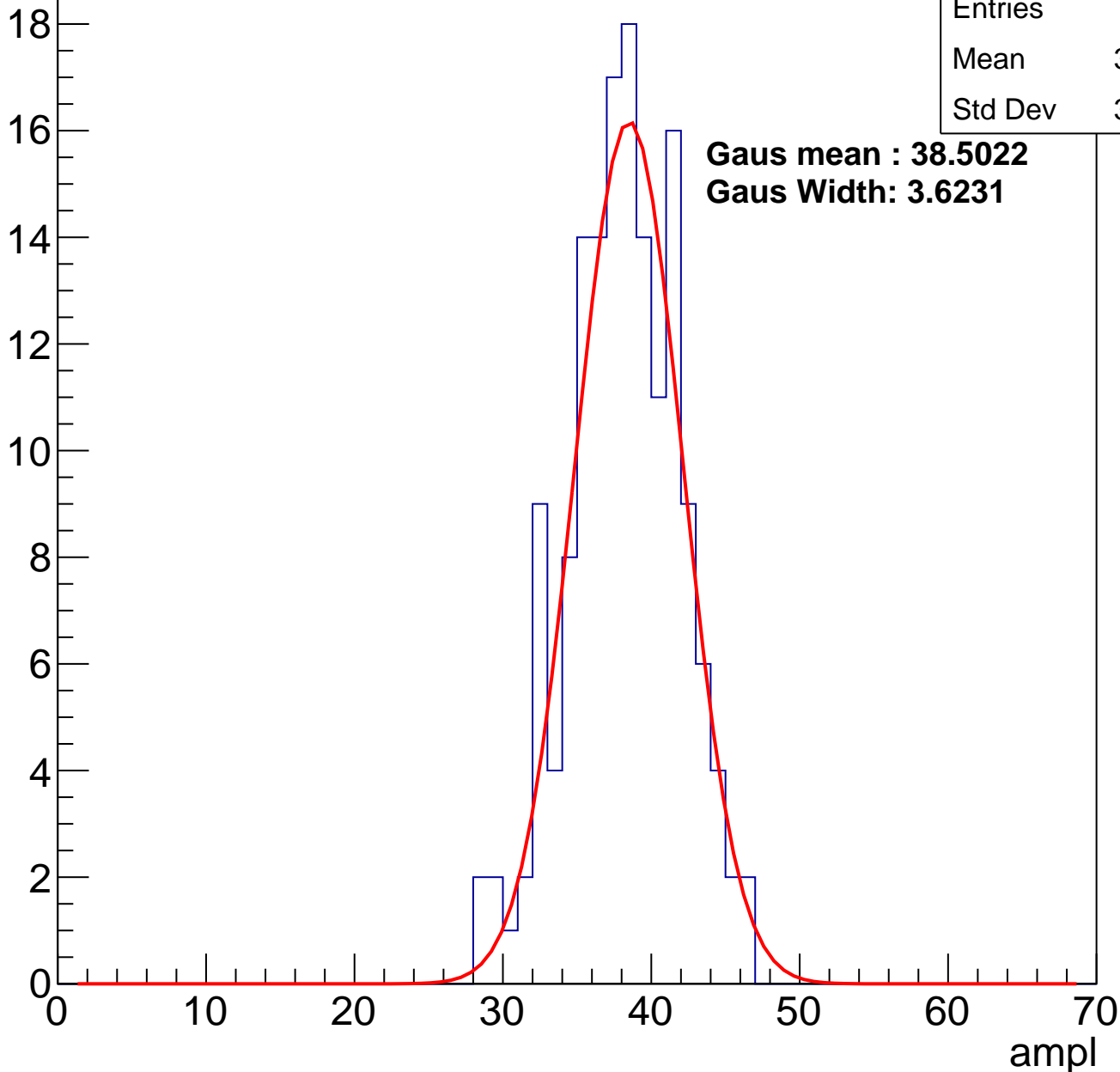
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	37.68
Std Dev	3.703

Gaus mean : 38.5022

Gaus Width: 3.6231

Entry



B0L101S, U24-ch87, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	98
Mean	44.77
Std Dev	3.452

Gaus mean : 45.0267

Gaus Width: 3.5346

0

10

20

30

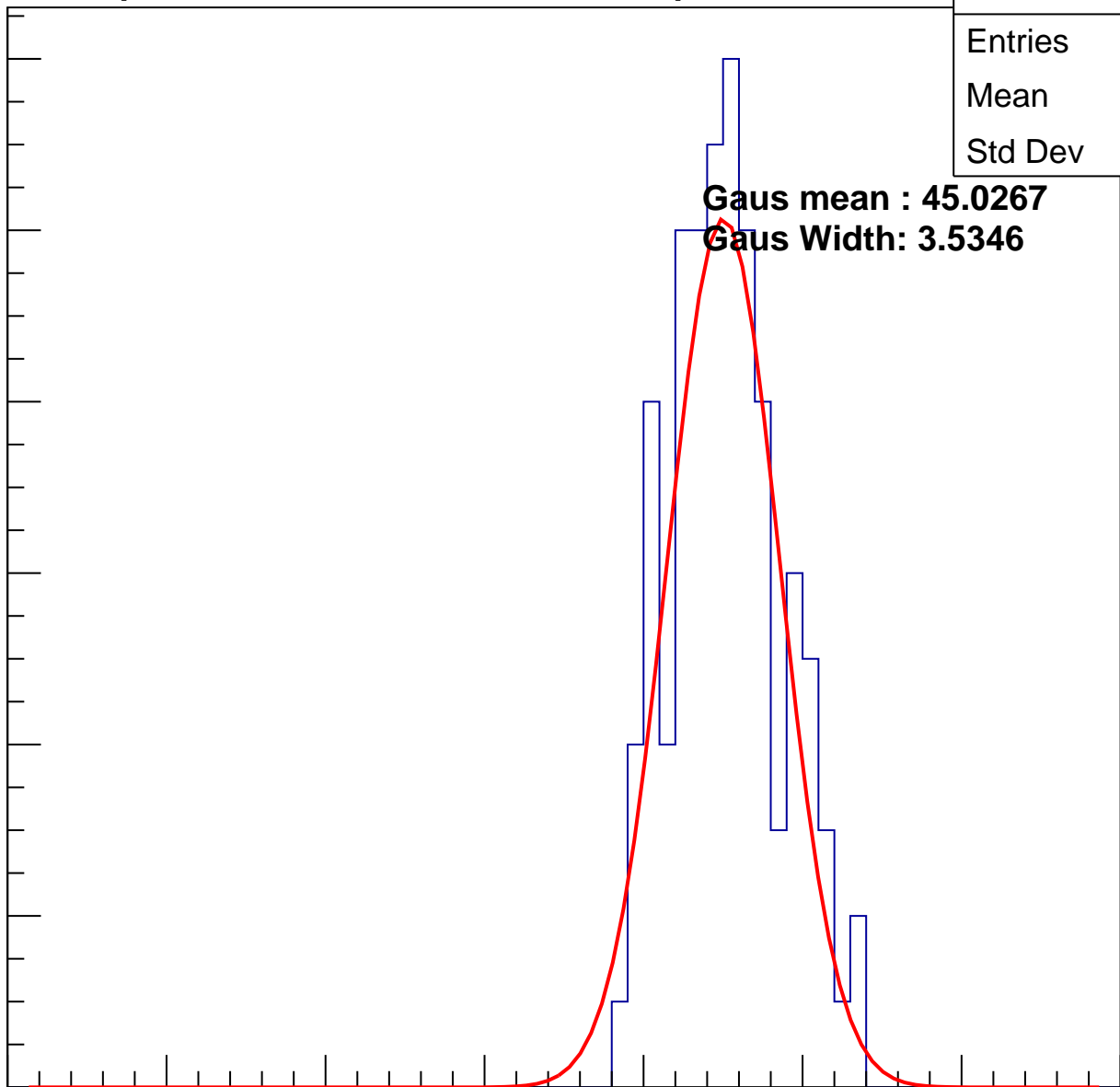
40

50

60

70

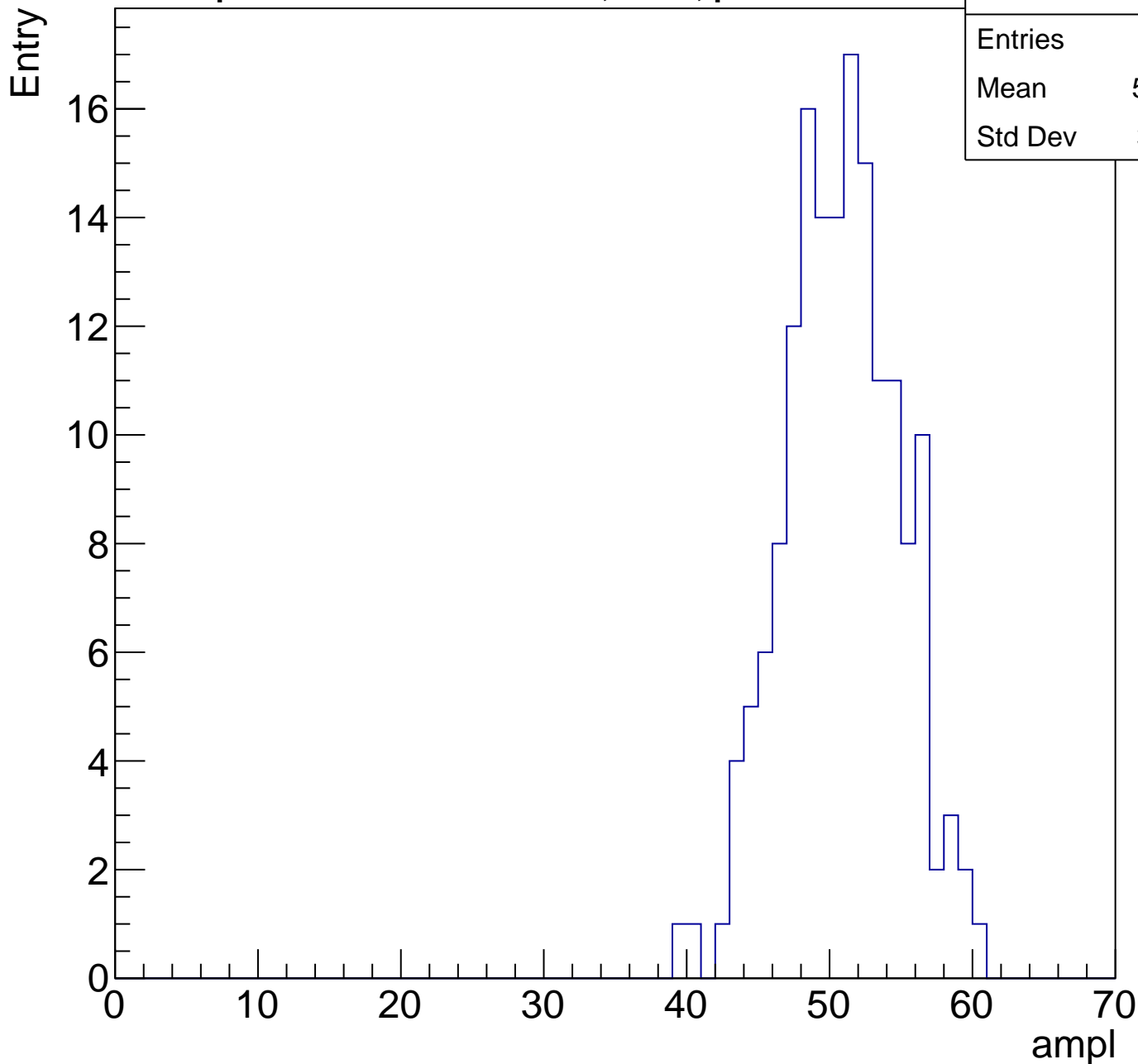
ampl



B0L101S, U24-ch87, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	50.36
Std Dev	3.981

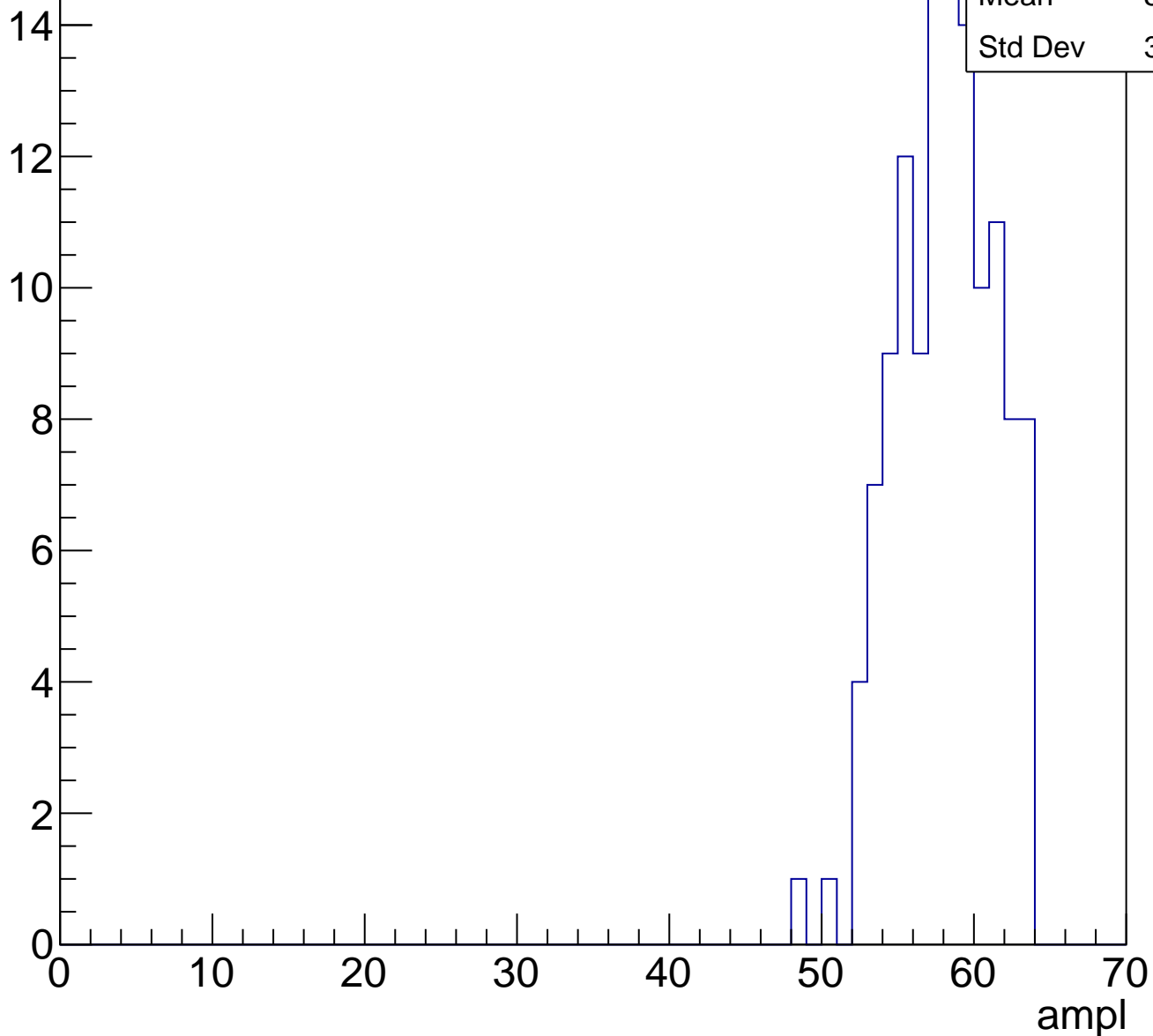


B0L101S, U24-ch87, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	57.65
Std Dev	3.178

Entry



B0L101S, U24-ch87, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

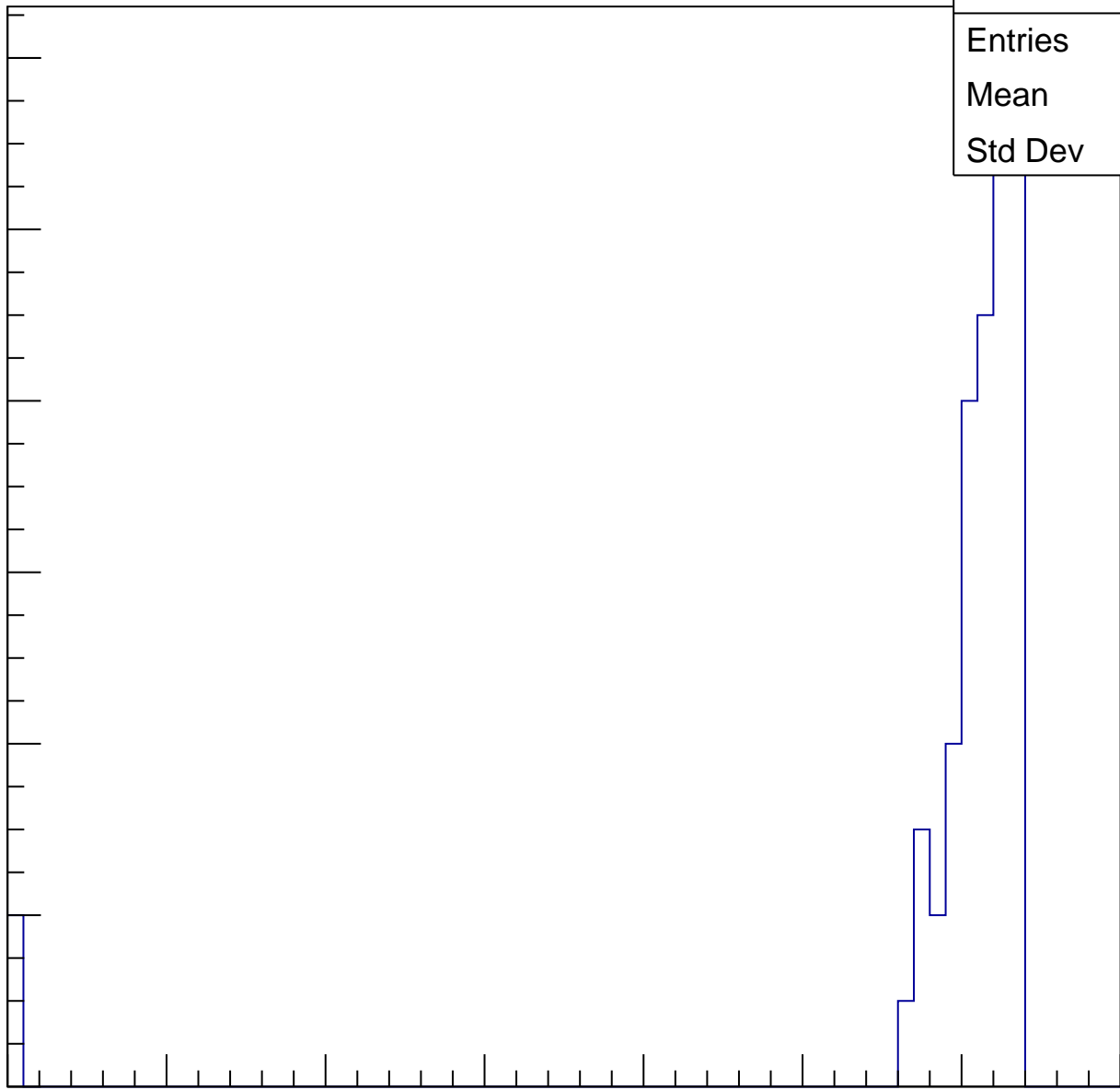
2

0

Entries	52
Mean	58.58
Std Dev	11.86

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch88, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	26.36
Std Dev	4.838

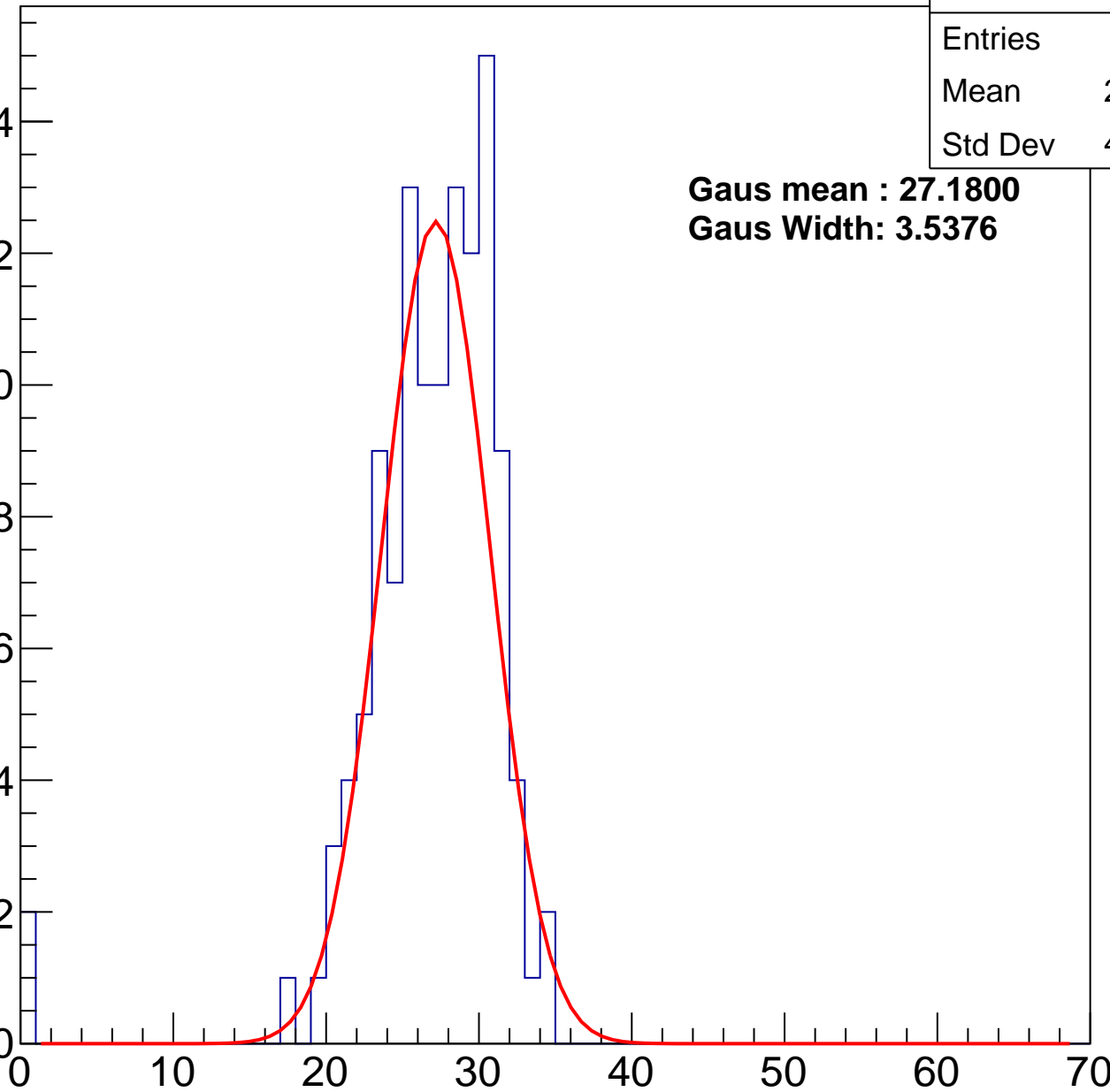
Gaus mean : 27.1800

Gaus Width: 3.5376

Entry

14
12
10
8
6
4
2
0

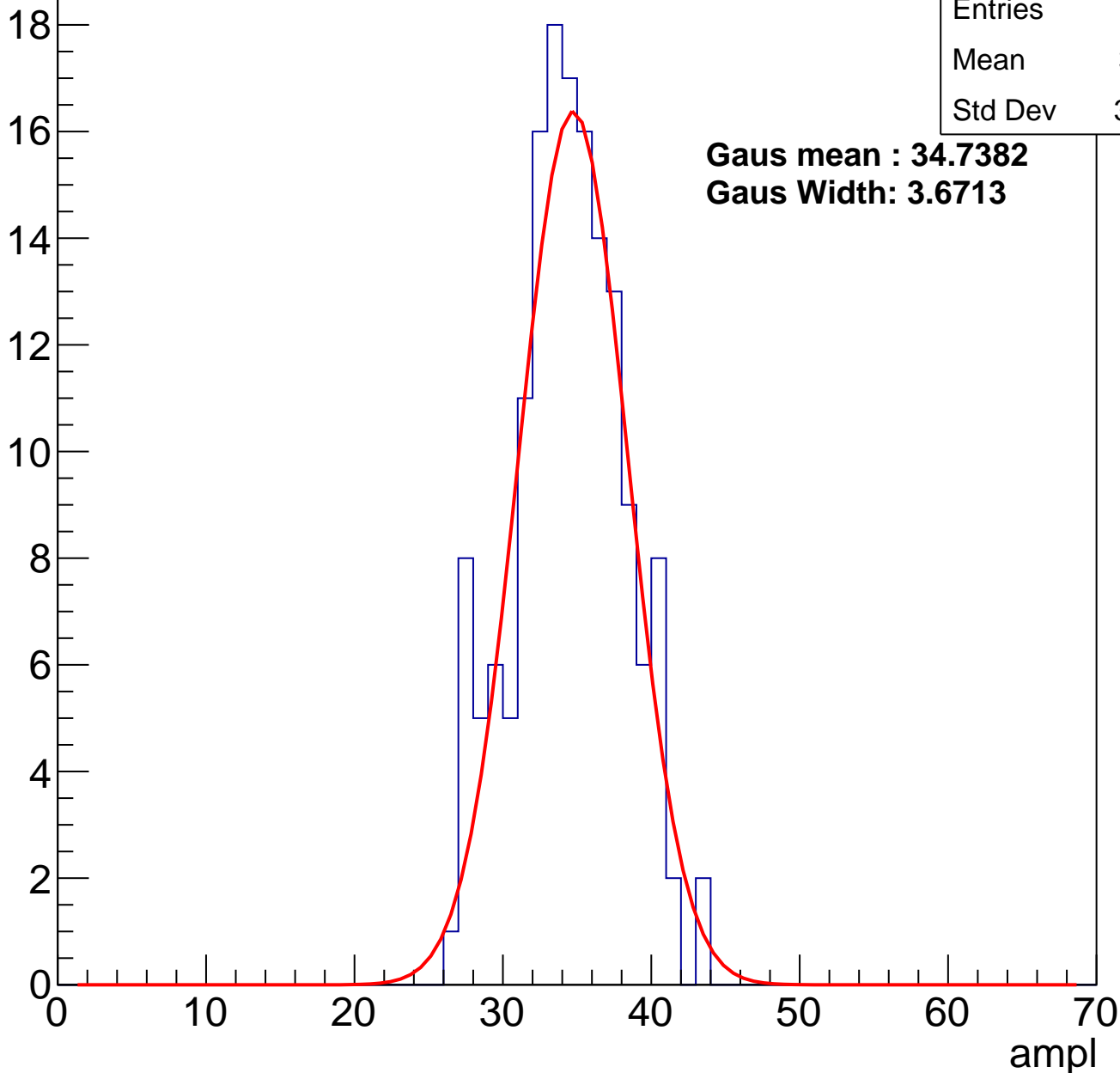
ampl



B0L101S, U24-ch88, adc1

calib_packv5_042523_0143.root, FC#1, port C1

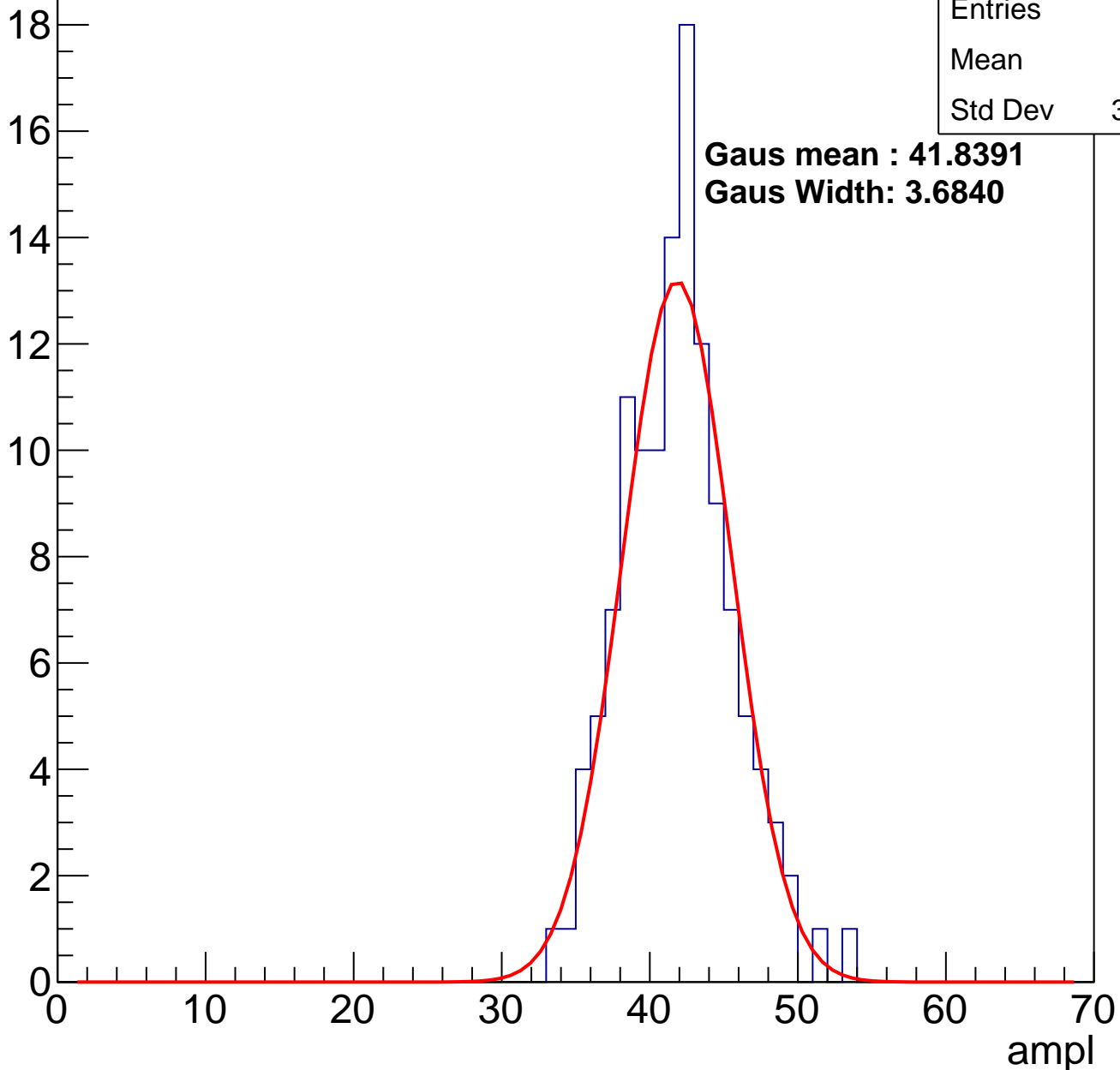
Entry



B0L101S, U24-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

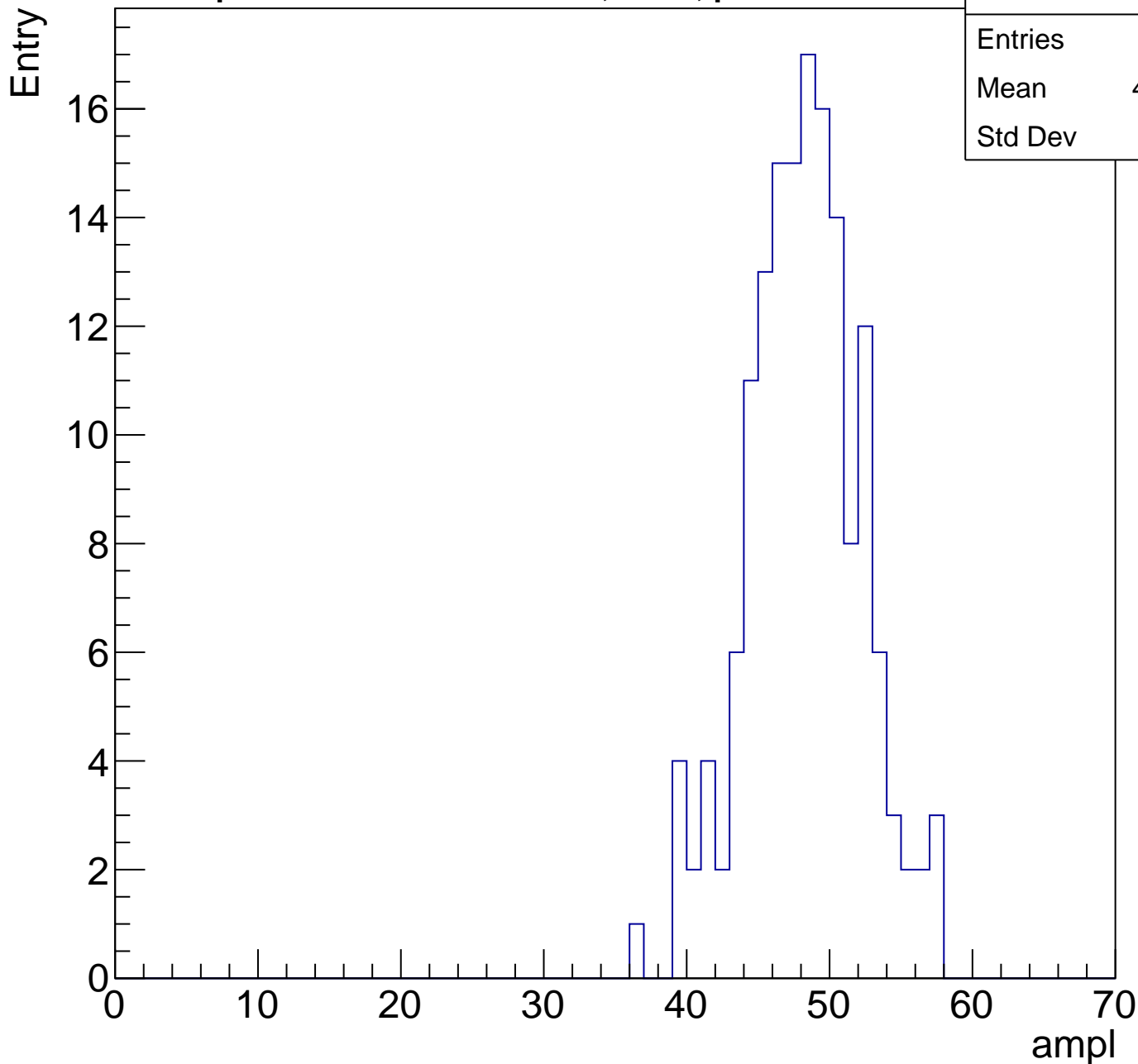
Entry



B0L101S, U24-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	47.74
Std Dev	3.95



B0L101S, U24-ch88, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	54.52
Std Dev	3.106

Entry

12

10

8

6

4

2

0

0

10

20

30

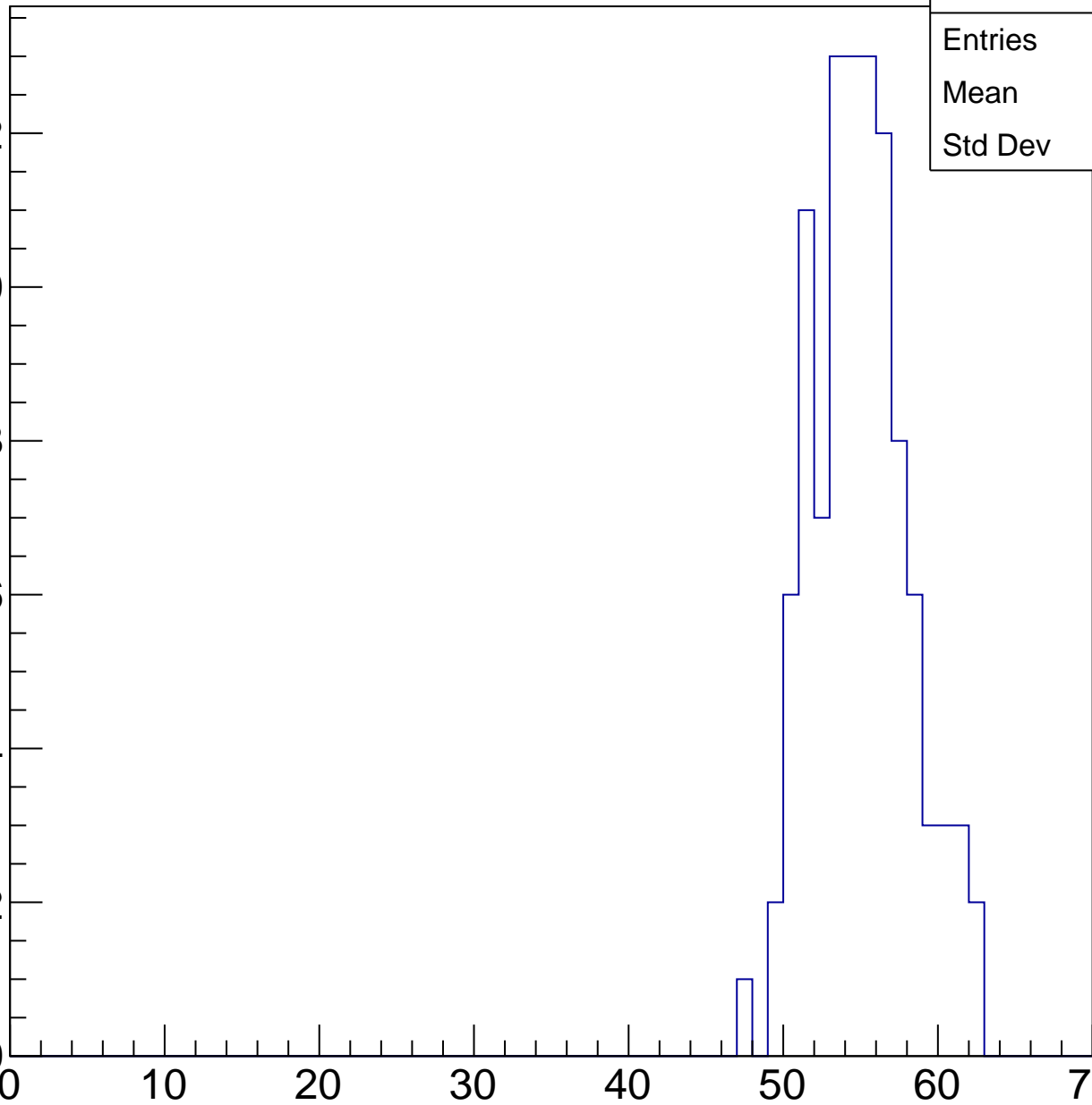
40

50

60

70

ampl



B0L101S, U24-ch88, adc5

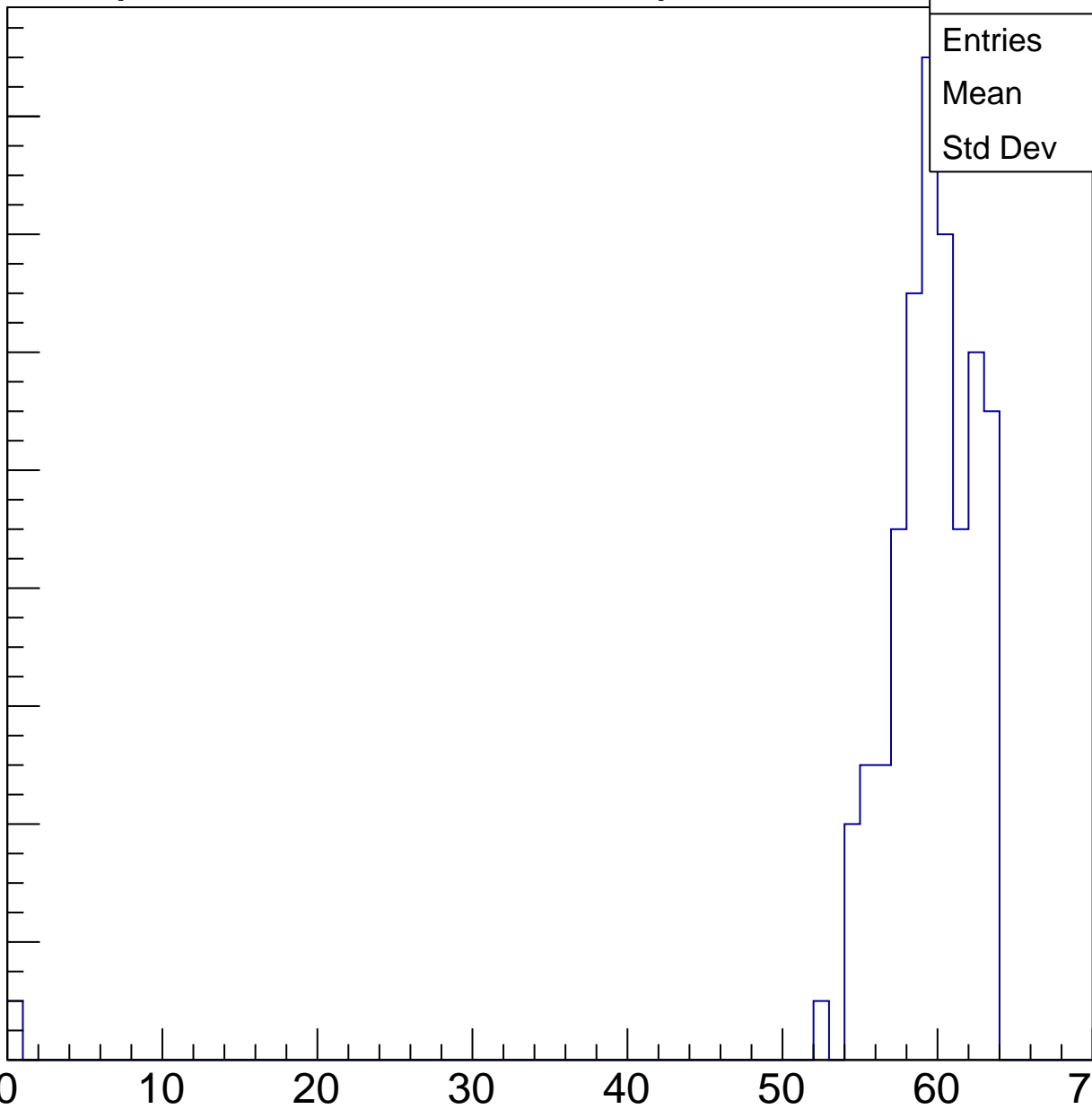
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.6
Std Dev	6.389

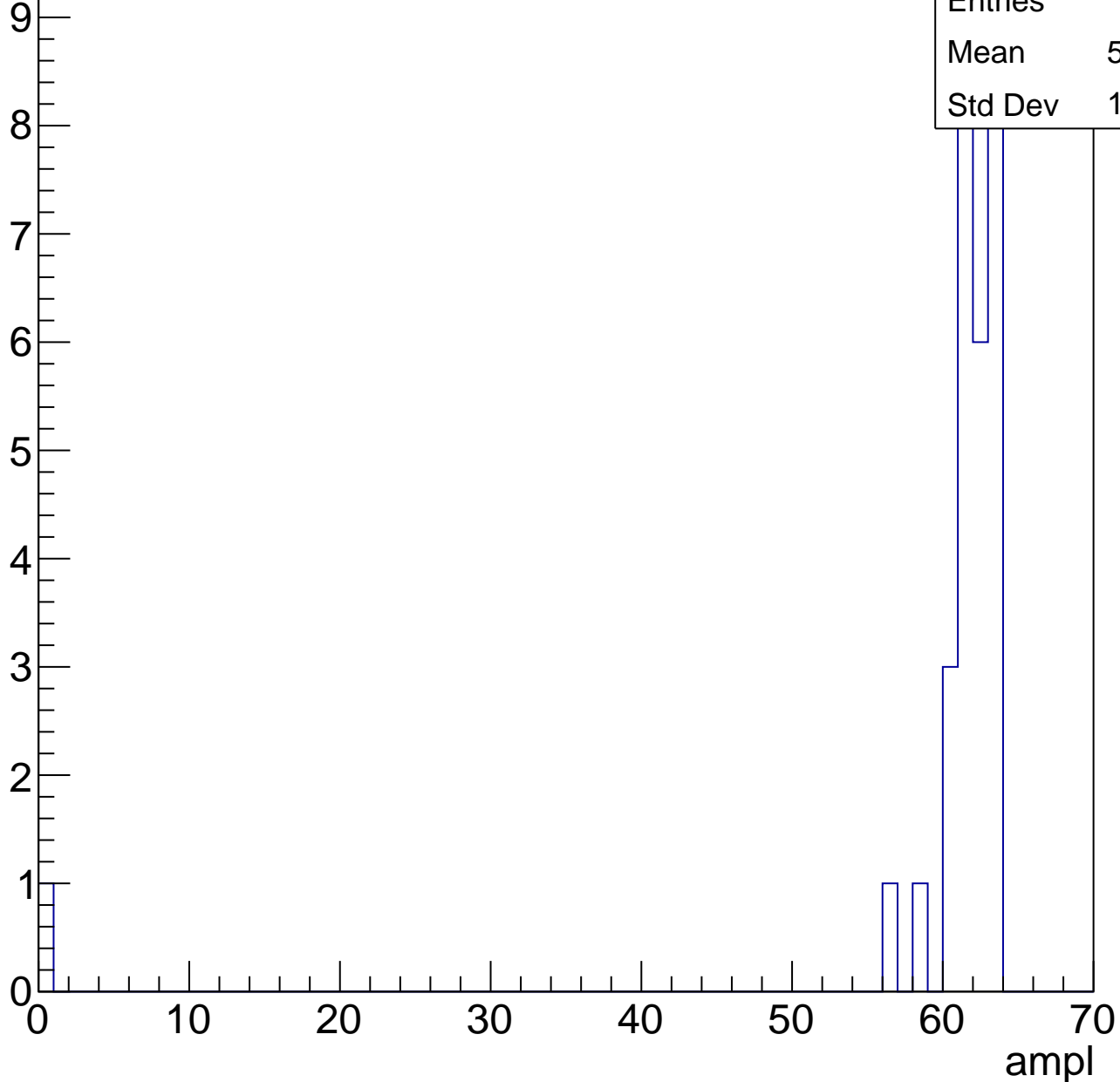
ampl



B0L101S, U24-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch89, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	28.01
Std Dev	4.698

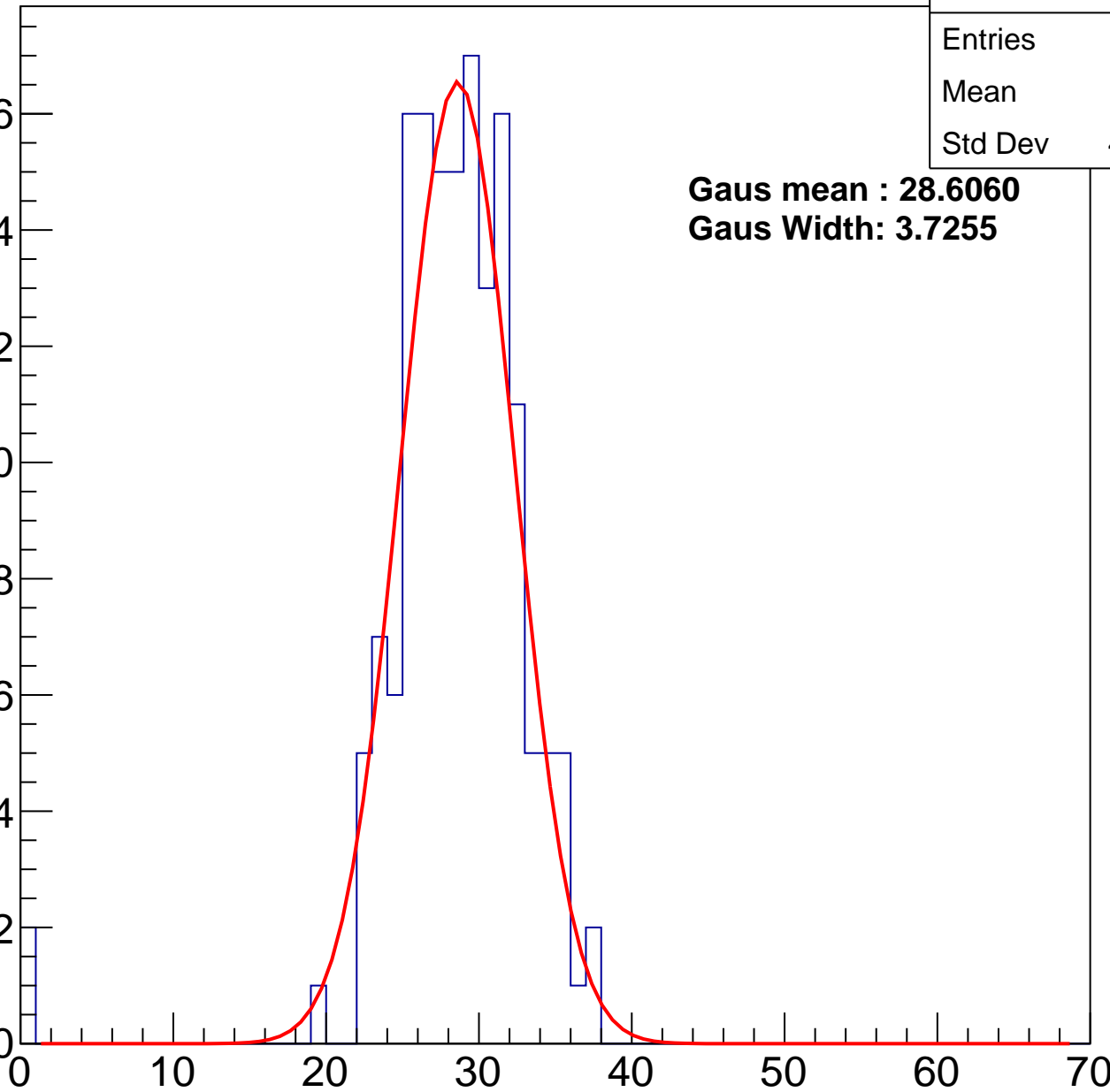
Gaus mean : 28.6060

Gaus Width: 3.7255

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch89, adc1

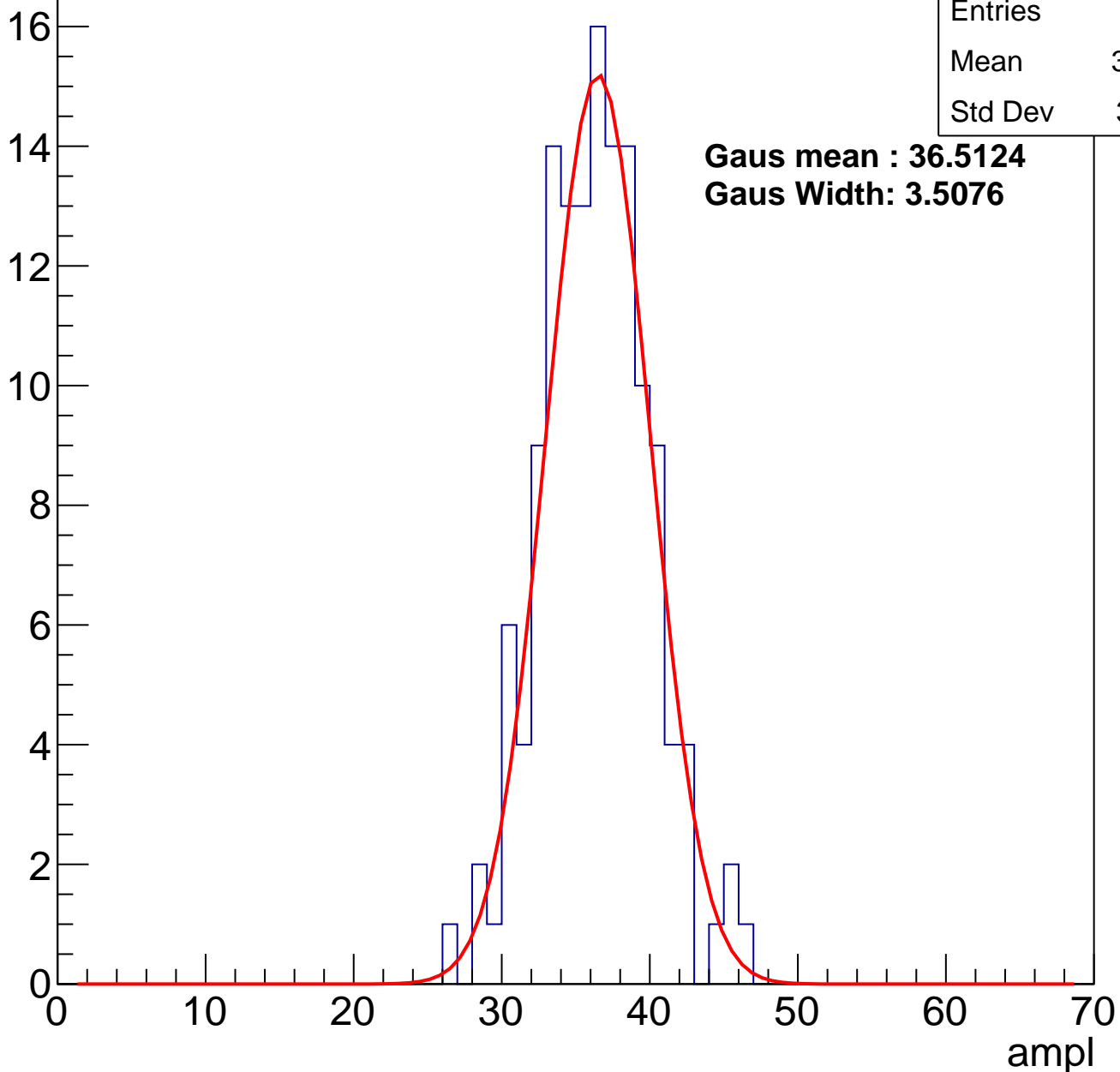
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	35.87
Std Dev	3.611

Gaus mean : 36.5124

Gaus Width: 3.5076

Entry



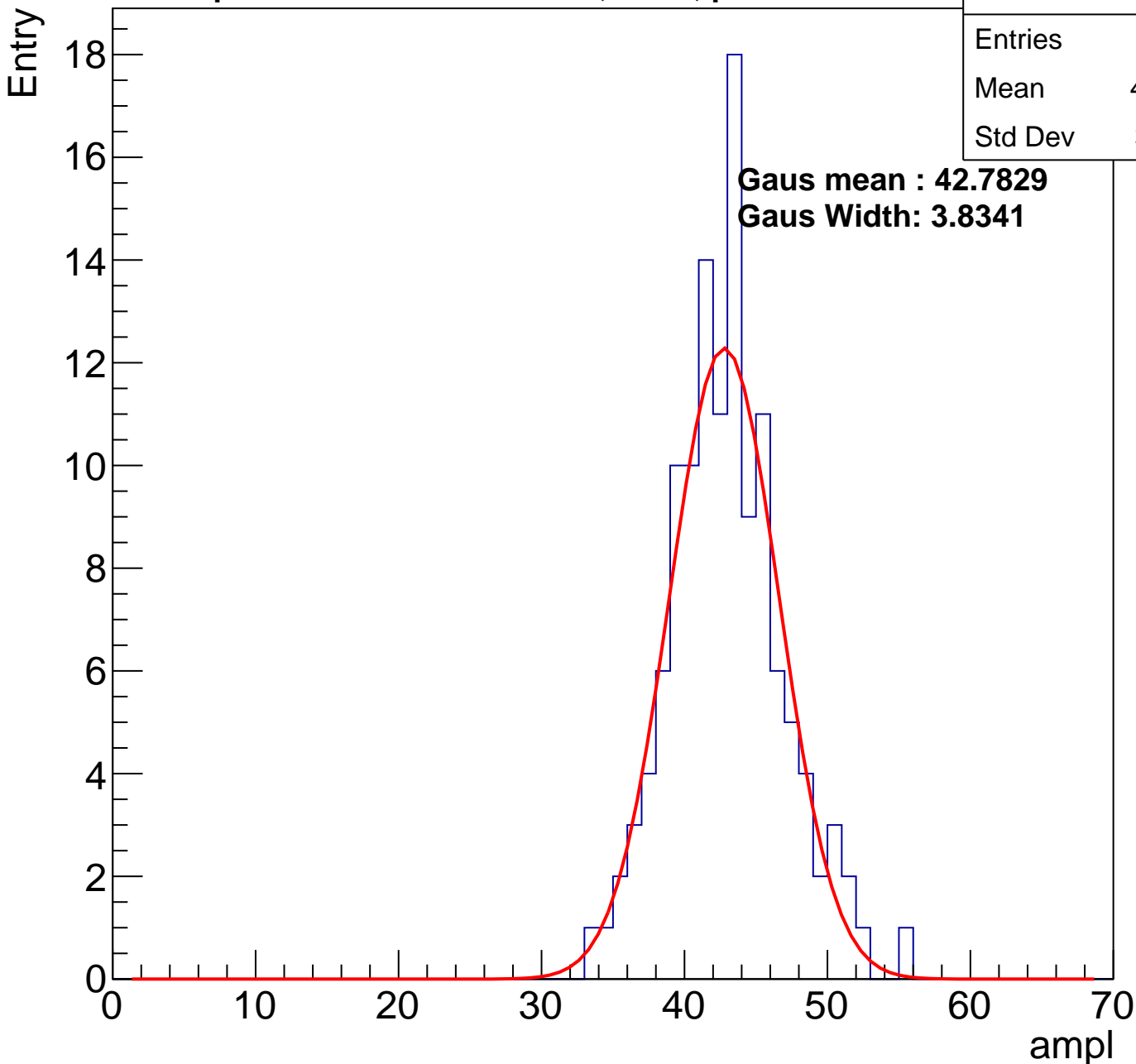
B0L101S, U24-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	42.52
Std Dev	3.911

Gaus mean : 42.7829

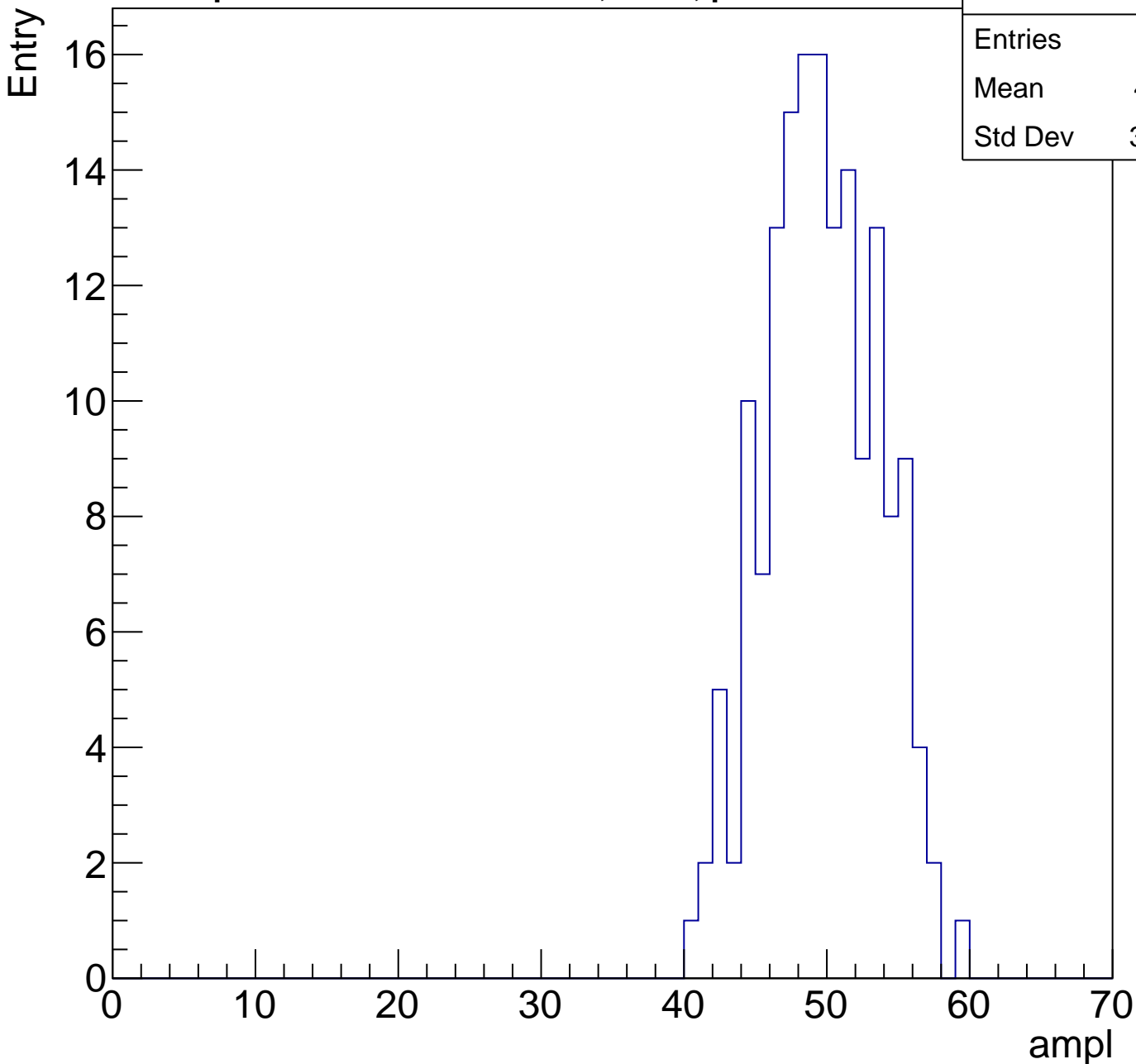
Gaus Width: 3.8341



B0L101S, U24-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	49.21
Std Dev	3.857

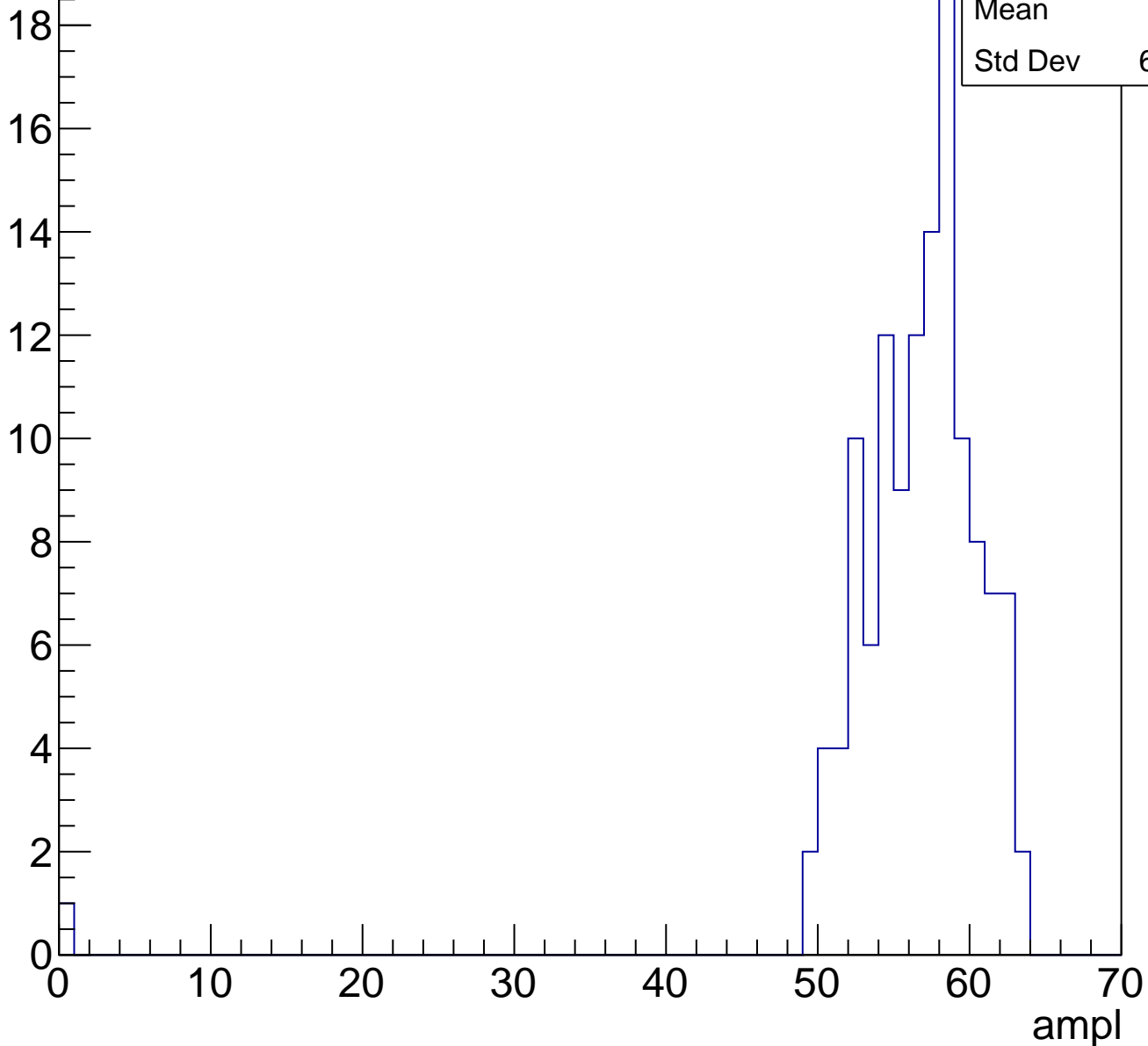


B0L101S, U24-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	56
Std Dev	6.007

Entry



B0L101S, U24-ch89, adc5

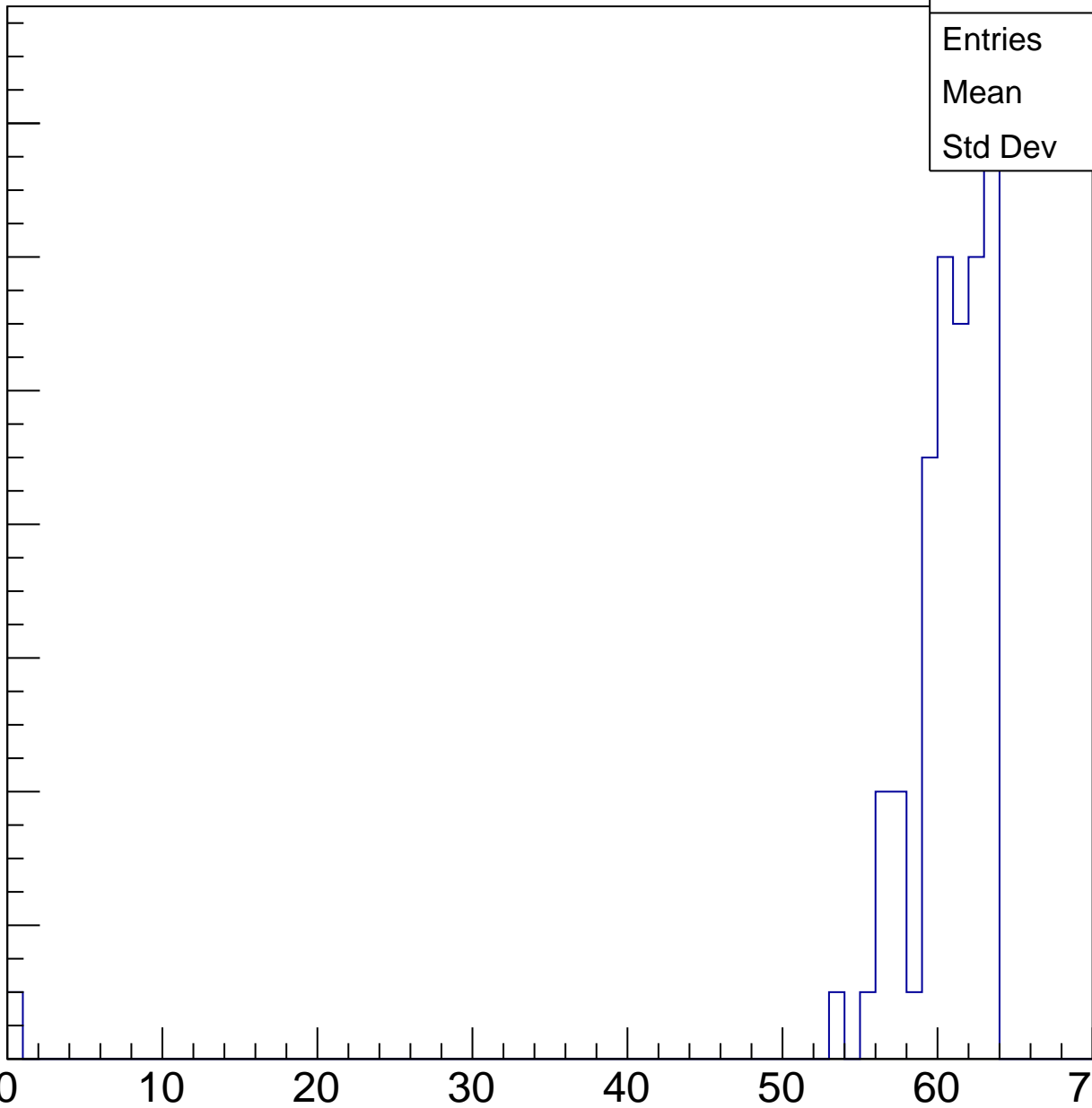
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	59.56
Std Dev	7.475

ampl



B0L101S, U24-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch90, adc0

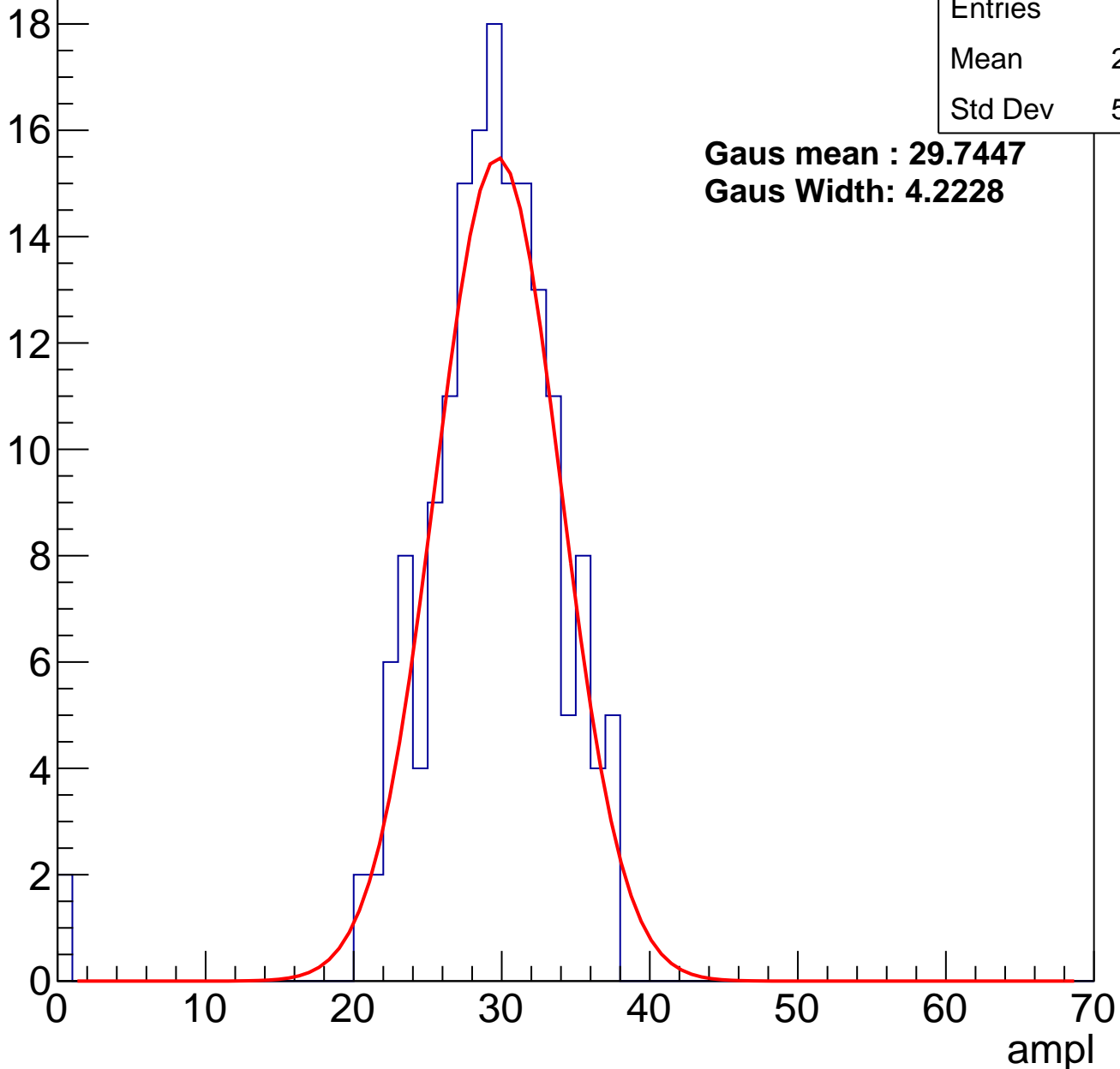
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	28.72
Std Dev	5.019

Gaus mean : 29.7447

Gaus Width: 4.2228

Entry



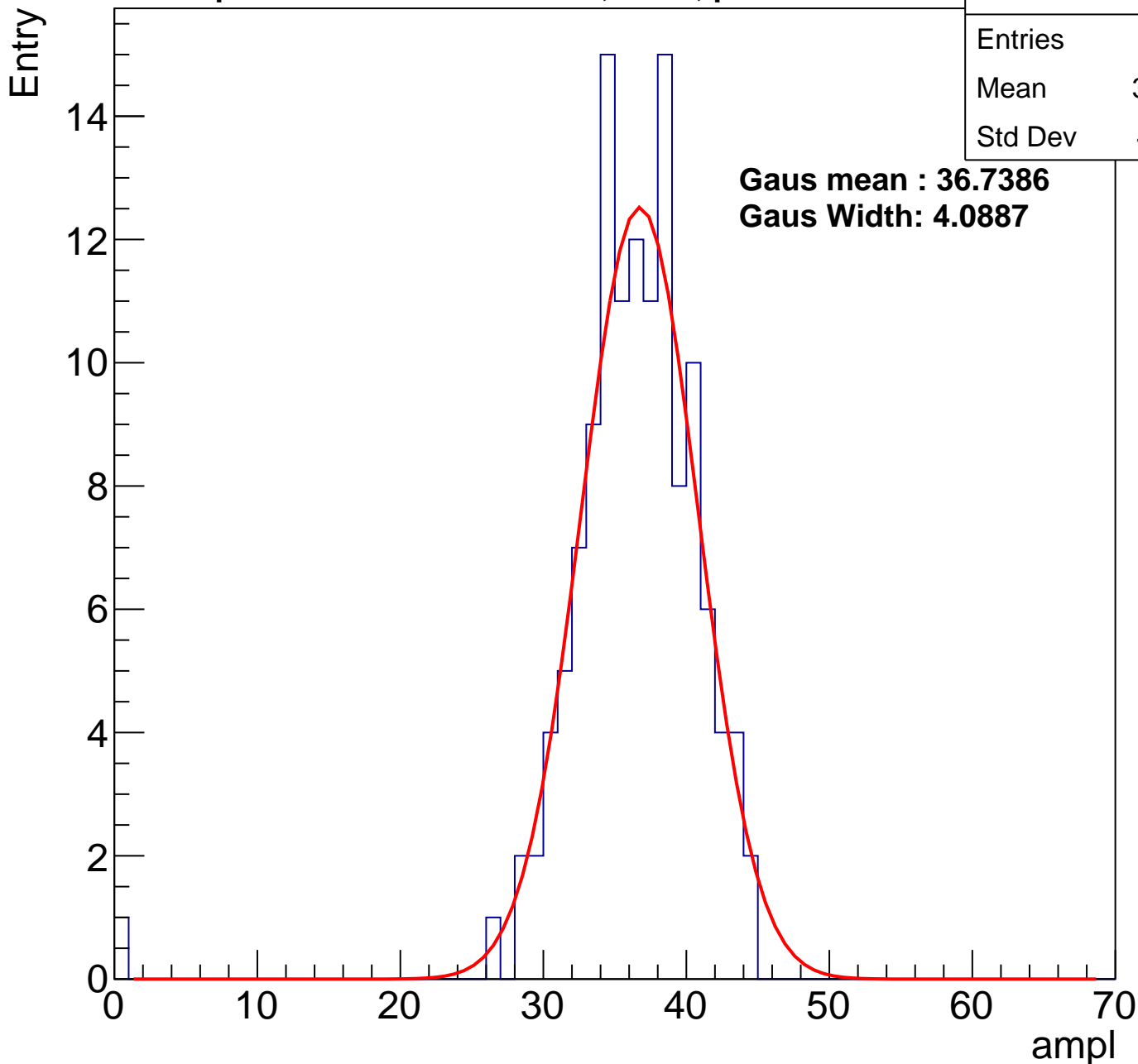
B0L101S, U24-ch90, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	35.86
Std Dev	4.881

Gaus mean : 36.7386

Gaus Width: 4.0887



B0L101S, U24-ch90, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	112
Mean	42.11
Std Dev	3.172

Gaus mean : 42.5829

Gaus Width: 3.2981

0

B0L101S, U24-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

150

Mean

49.1

Std Dev

3.629

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch90, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	55.86
Std Dev	3.523

Entry

14

12

10

8

6

4

2

0

0

10

20

30

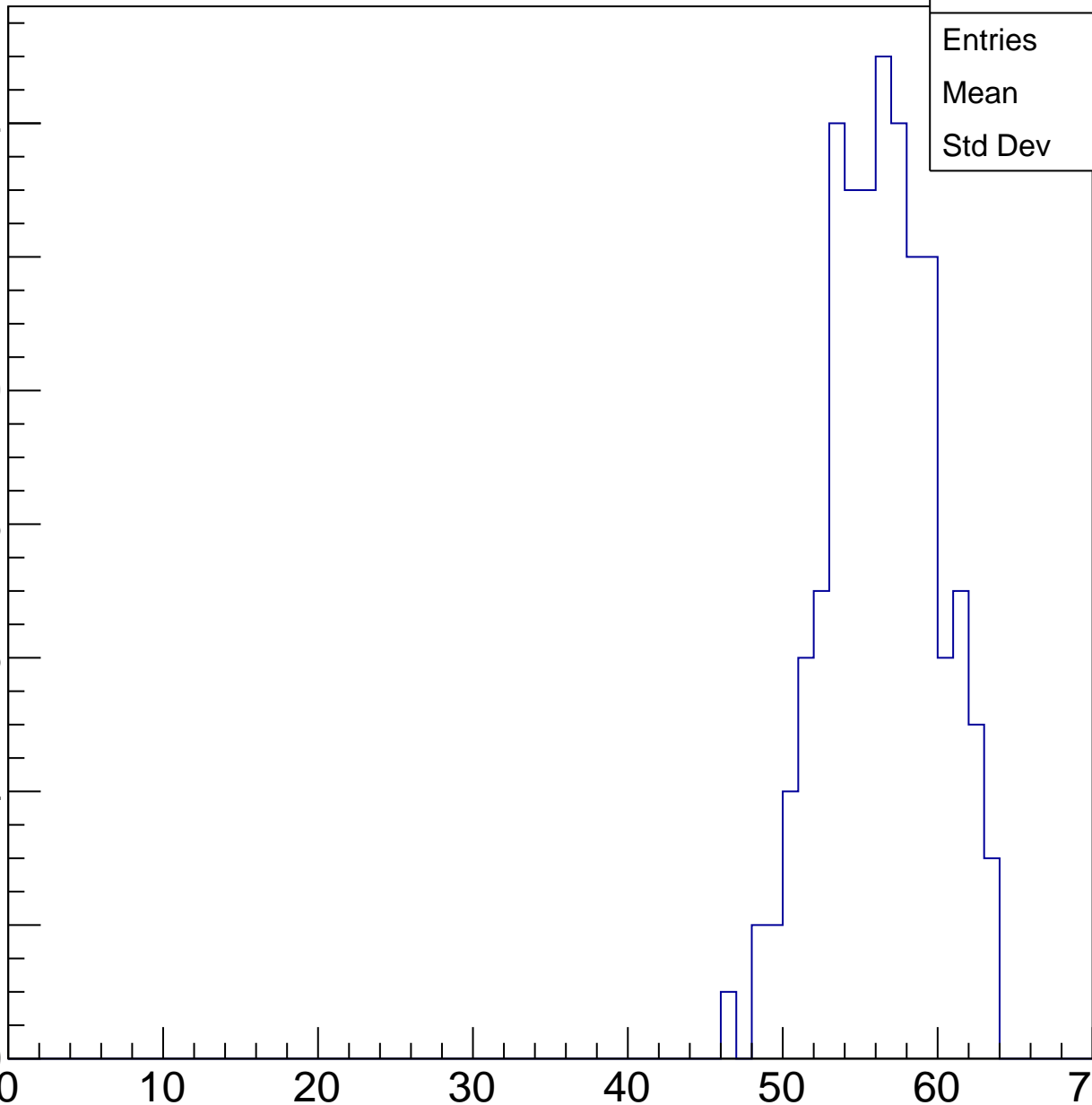
40

50

60

70

ampl



B0L101S, U24-ch90, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	66
Mean	59.3
Std Dev	7.795

ampl

0

10

20

30

40

50

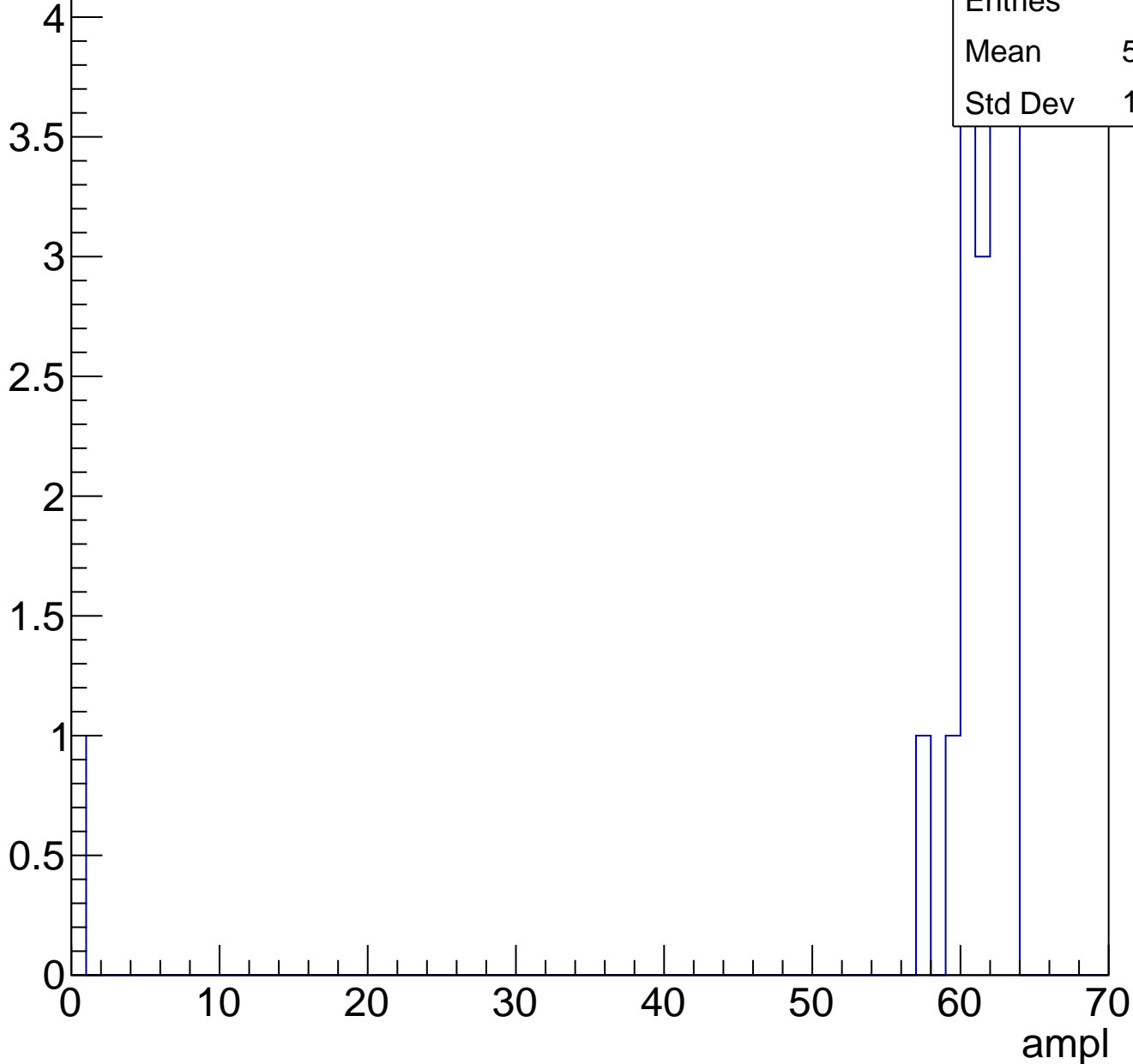
60

70

B0L101S, U24-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch91, adc0

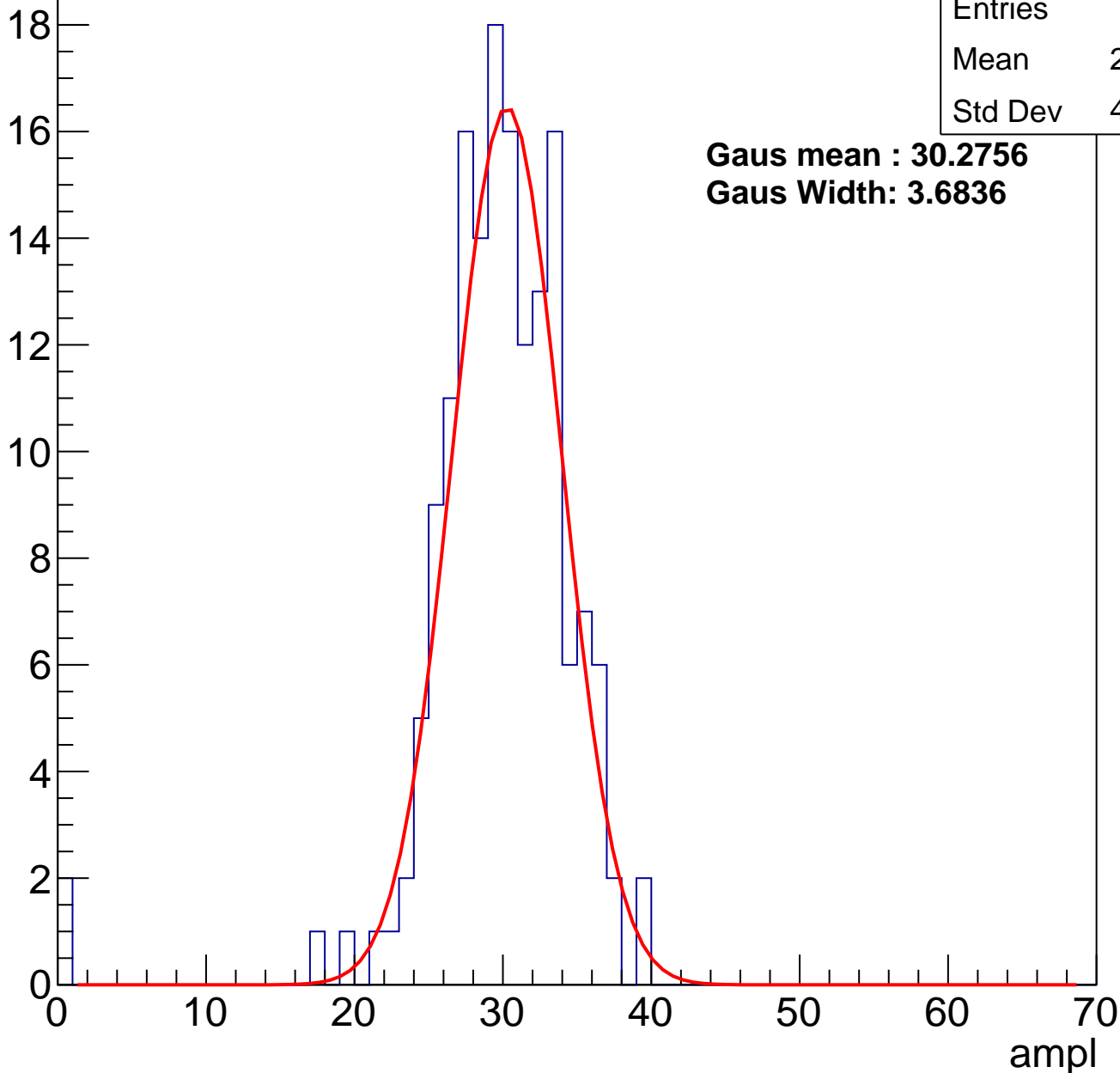
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	29.29
Std Dev	4.987

Gaus mean : 30.2756

Gaus Width: 3.6836

Entry



B0L101S, U24-ch91, adc1

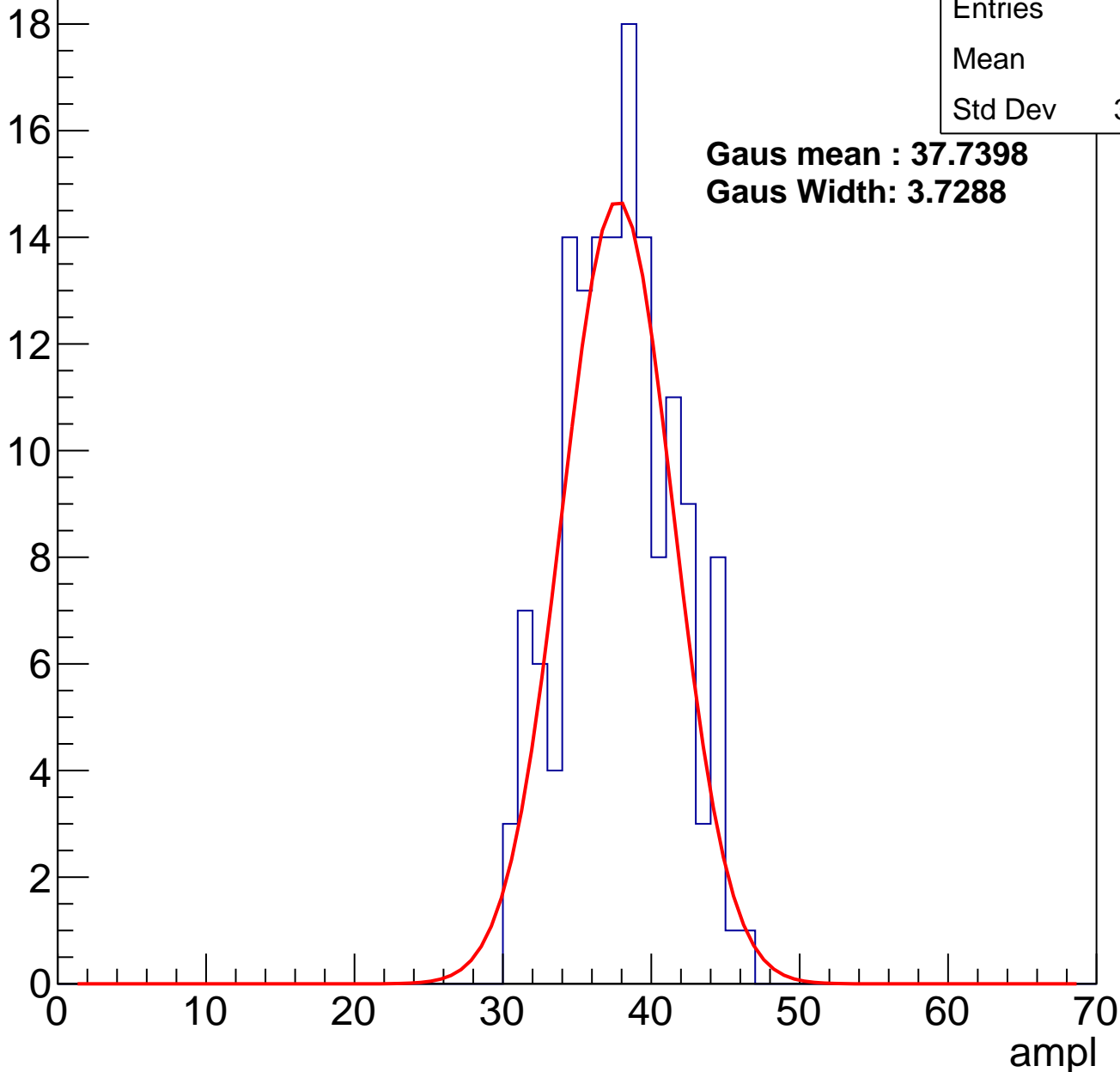
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	37.4
Std Dev	3.652

Gaus mean : 37.7398

Gaus Width: 3.7288

Entry



B0L101S, U24-ch91, adc2

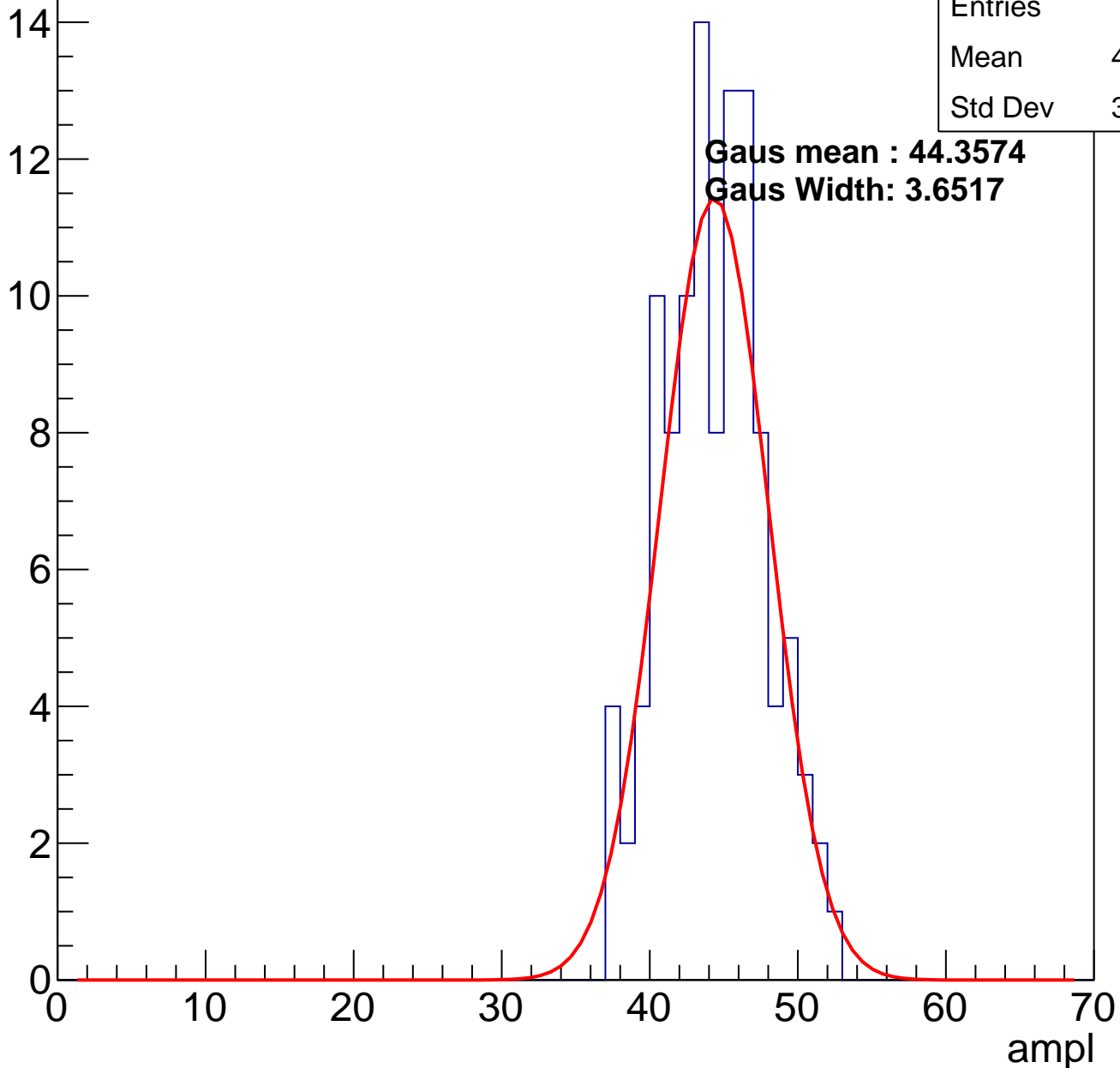
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	43.87
Std Dev	3.384

Gaus mean : 44.3574

Gaus Width: 3.6517

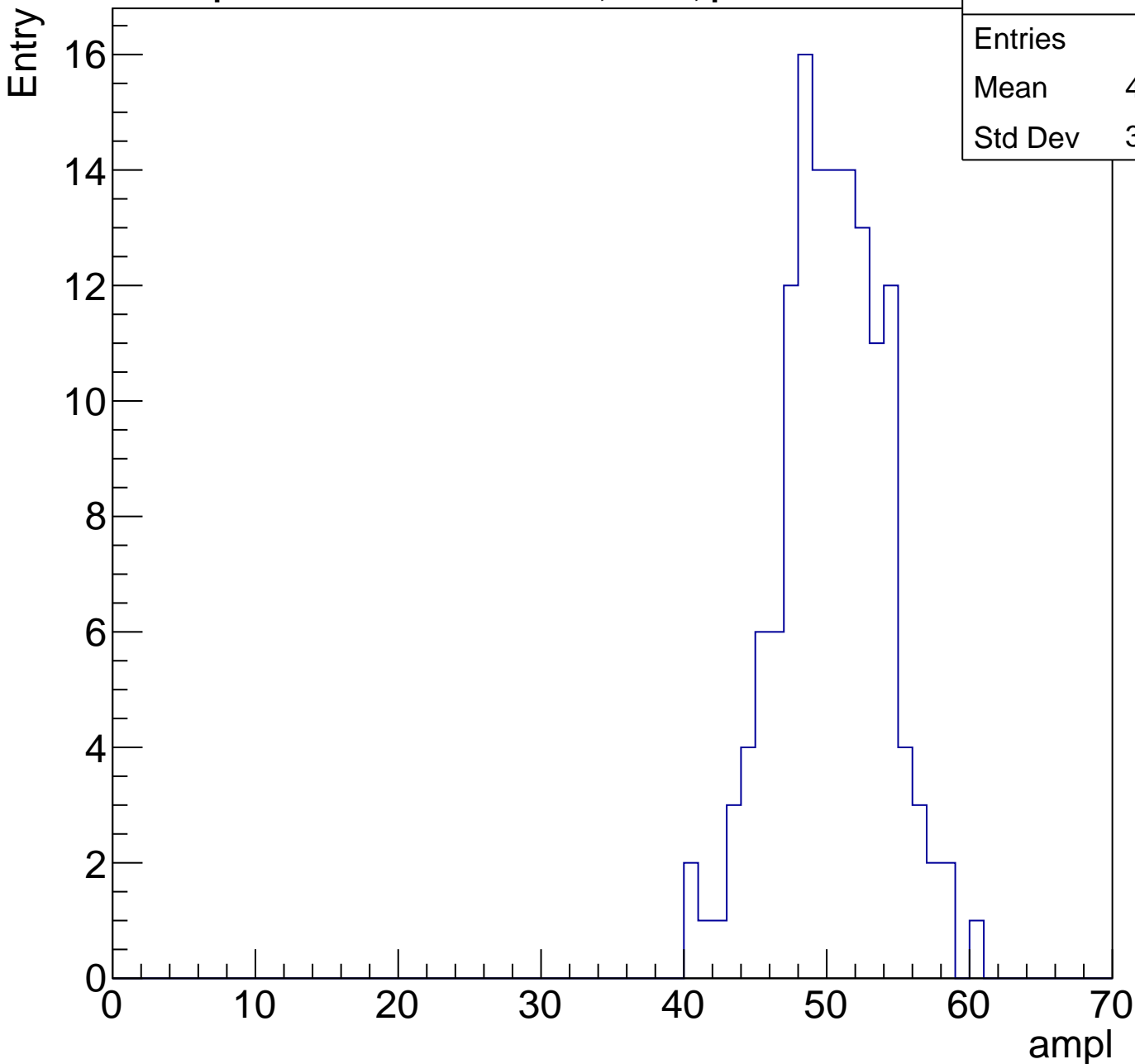
Entry



B0L101S, U24-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	49.87
Std Dev	3.725



B0L101S, U24-ch91, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	136
Mean	56.97
Std Dev	3.261

B0L101S, U24-ch91, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	61
Mean	58.52
Std Dev	11.03

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch92, adc0

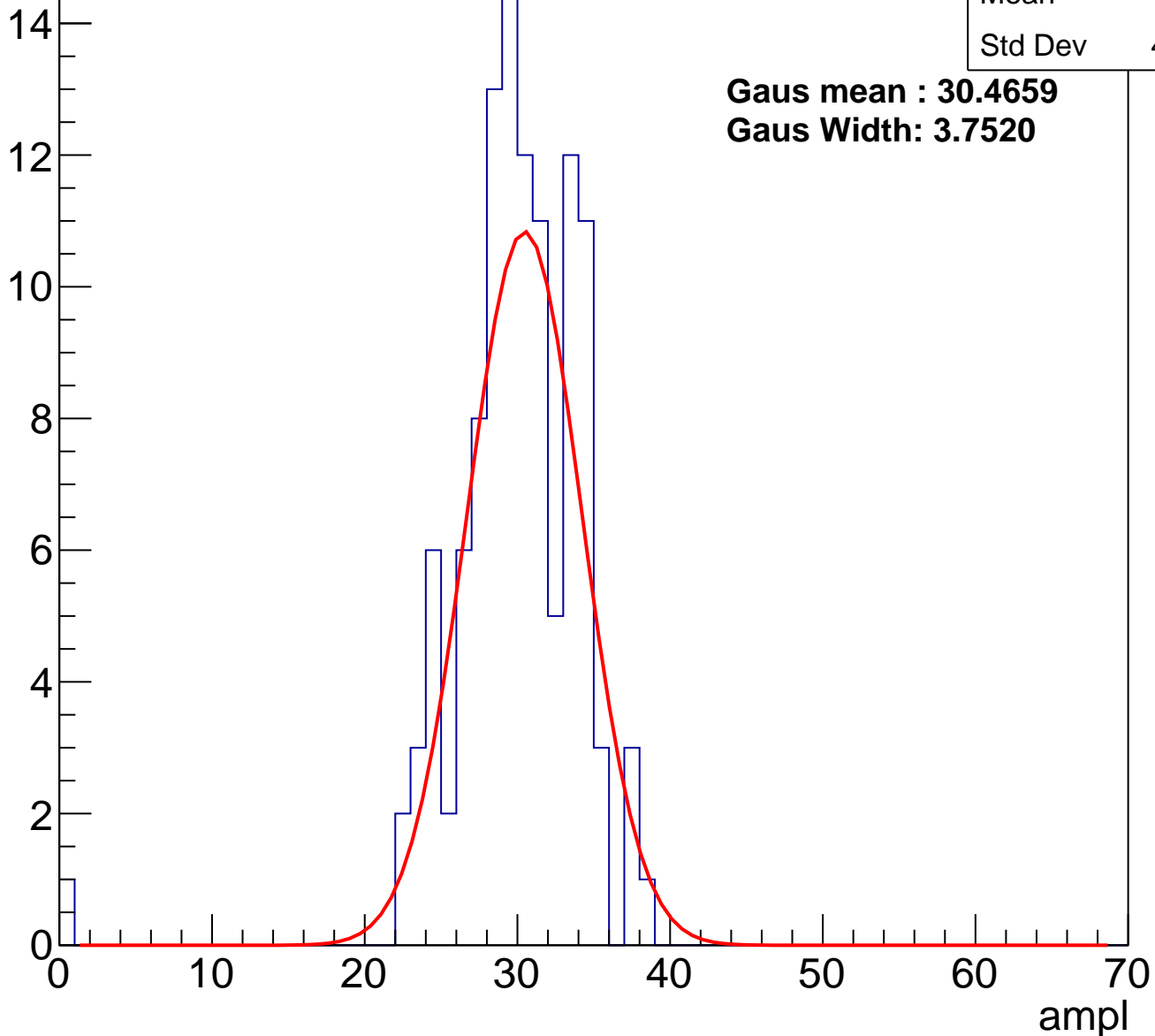
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	29.5
Std Dev	4.441

Gaus mean : 30.4659

Gaus Width: 3.7520

Entry



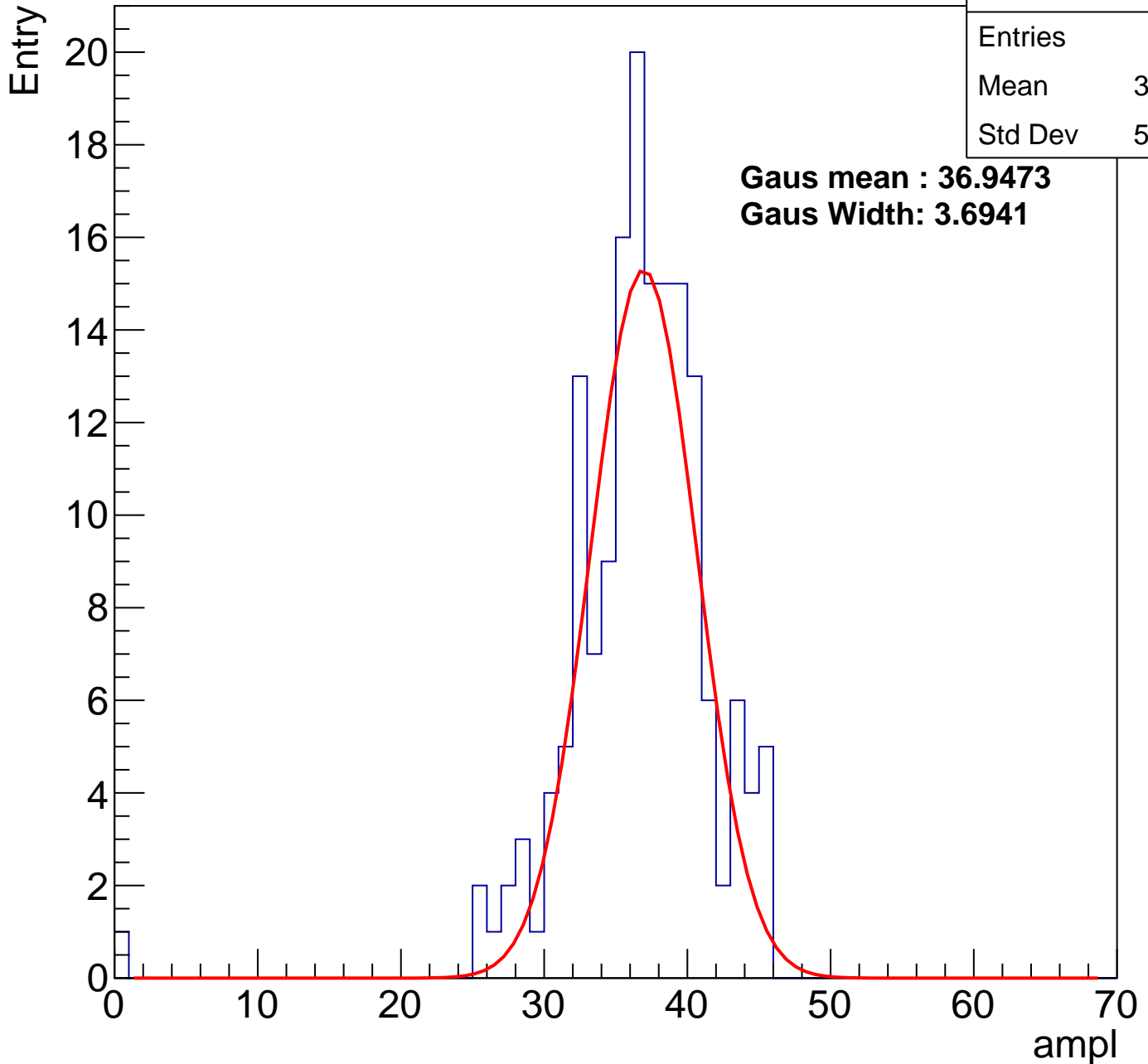
B0L101S, U24-ch92, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	36.18
Std Dev	5.059

Gaus mean : 36.9473

Gaus Width: 3.6941



B0L101S, U24-ch92, adc2

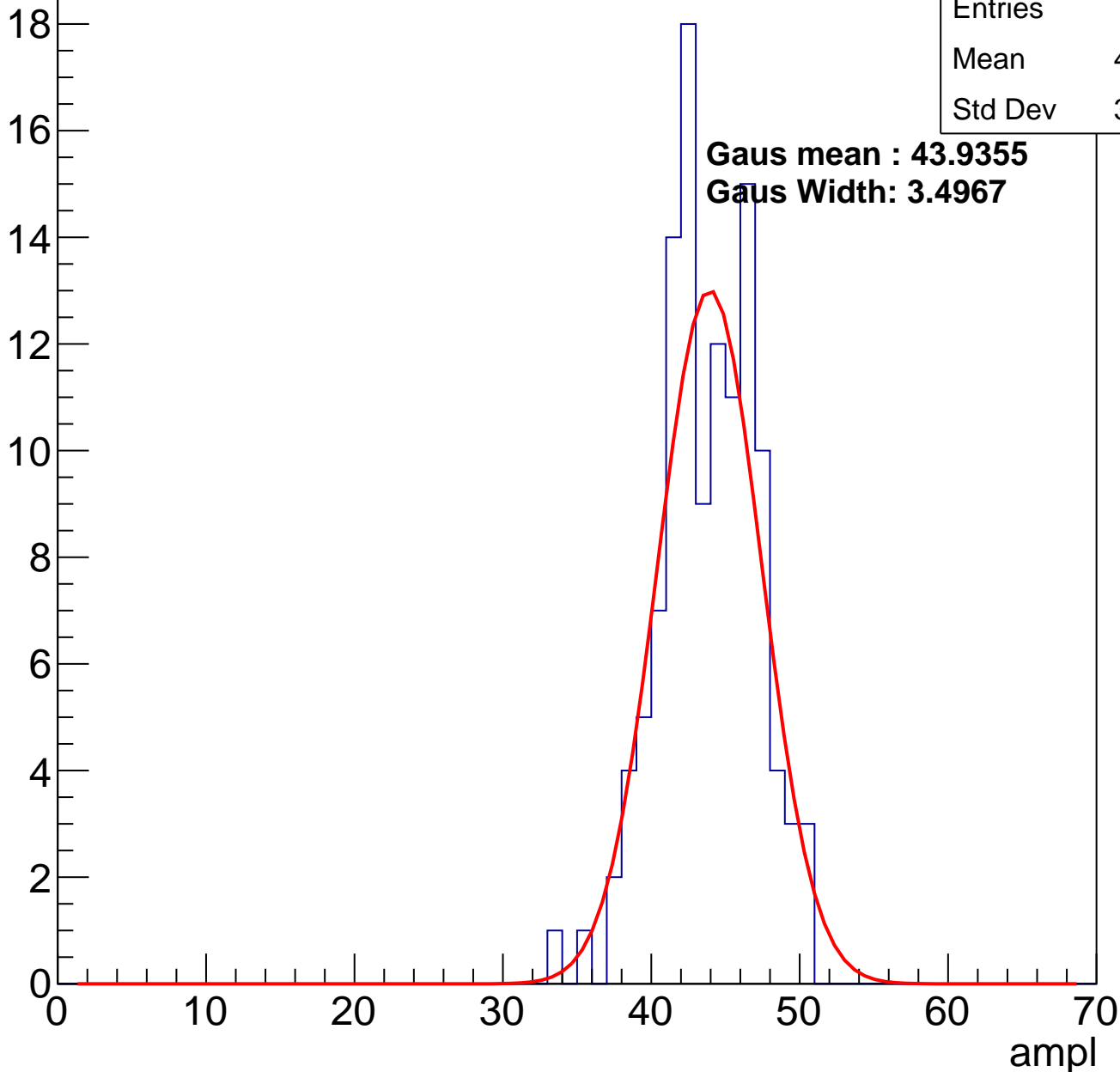
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	119
Mean	43.34
Std Dev	3.234

Gaus mean : 43.9355

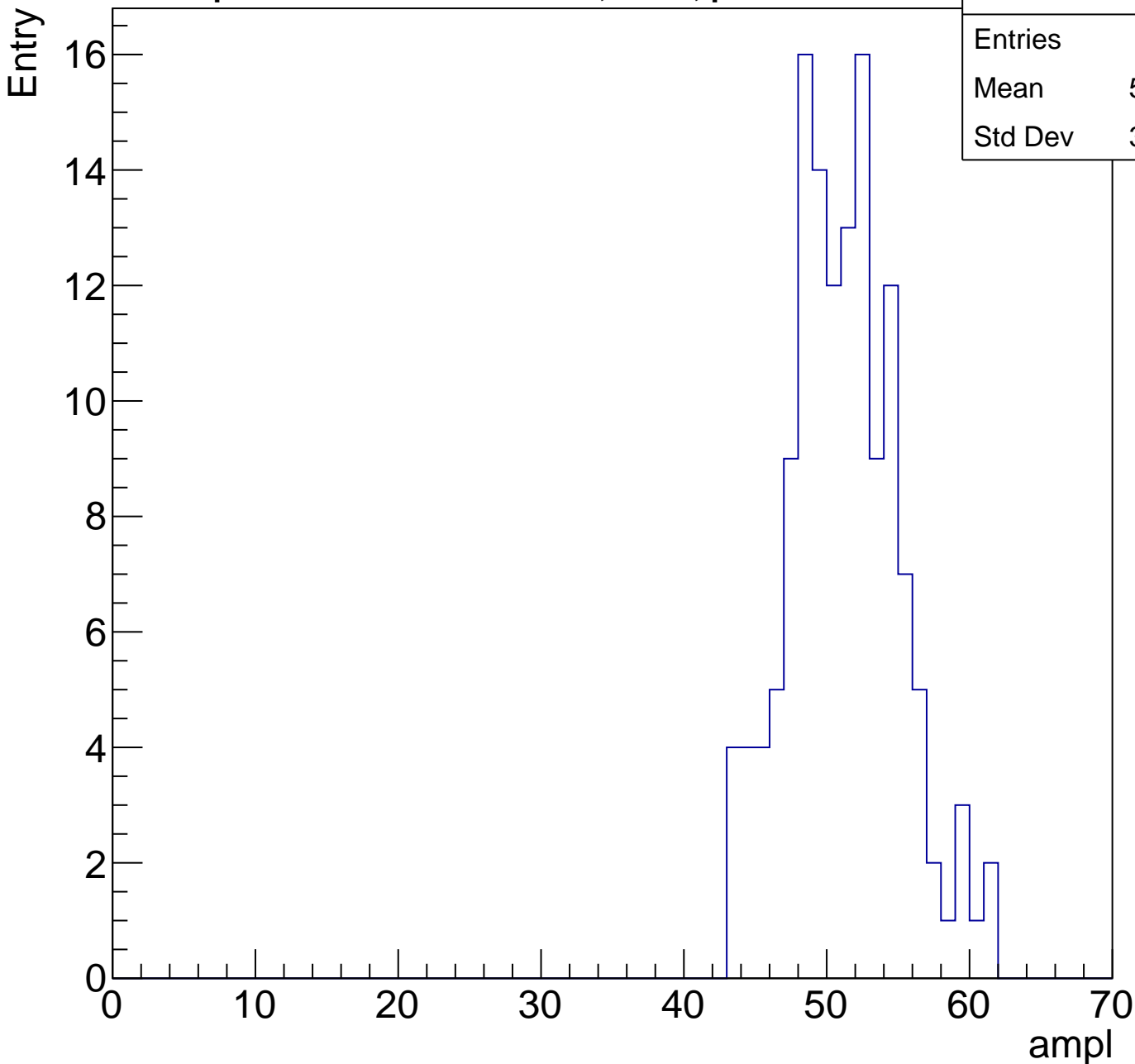
Gaus Width: 3.4967



B0L101S, U24-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	50.73
Std Dev	3.847



B0L101S, U24-ch92, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

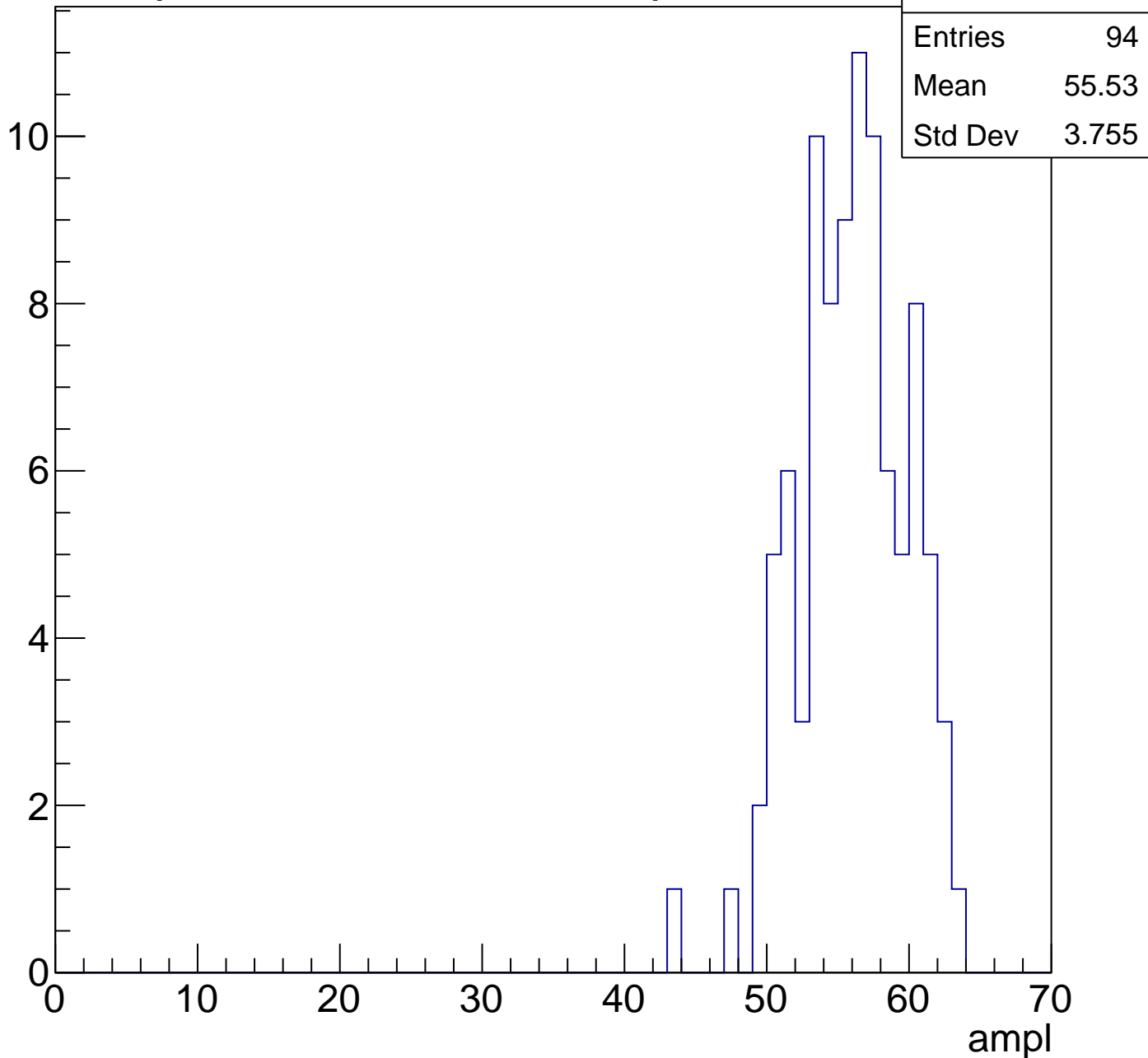
40

50

60

ampl

Entries	94
Mean	55.53
Std Dev	3.755



B0L101S, U24-ch92, adc5

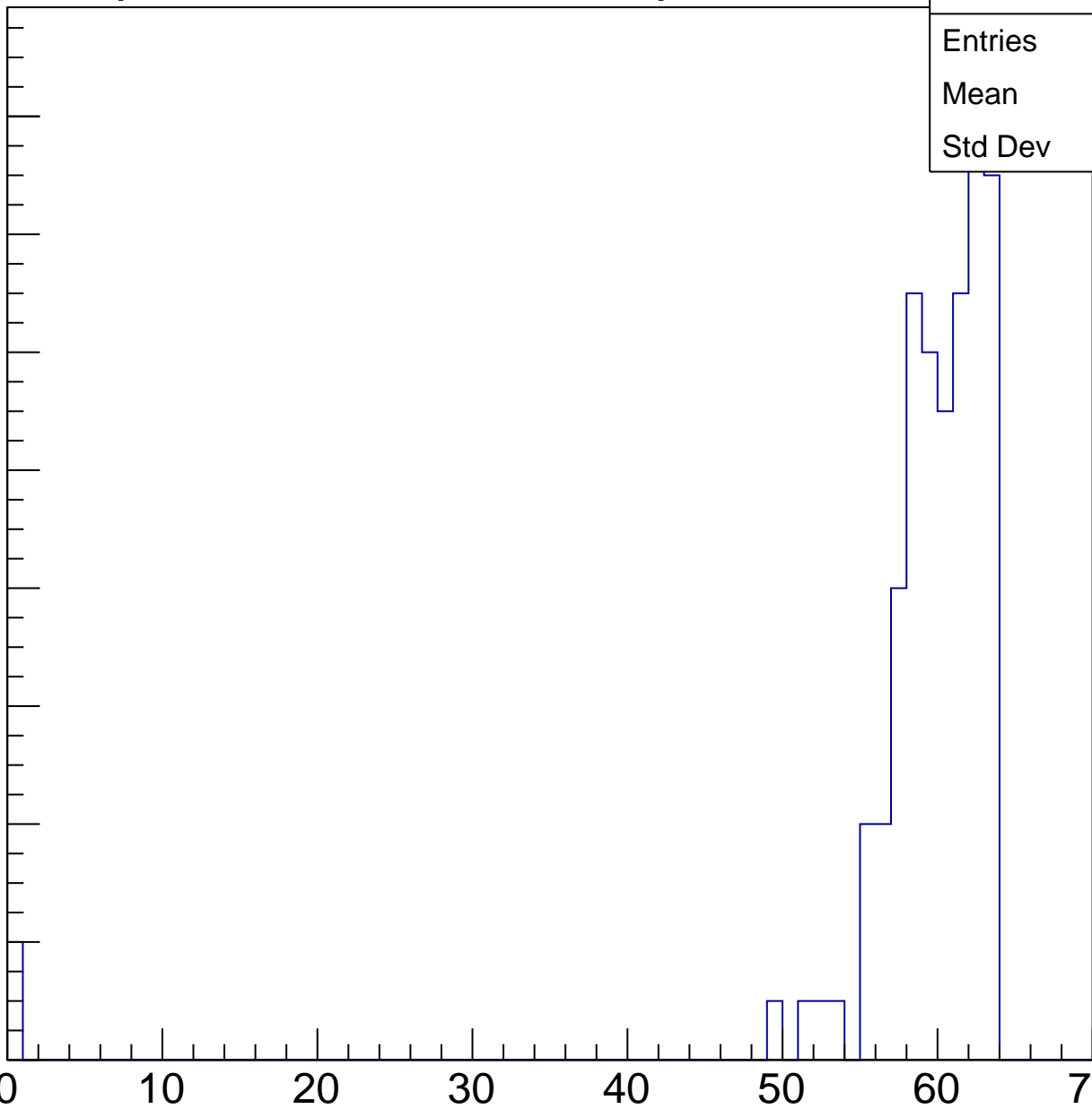
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.44
Std Dev	8.691

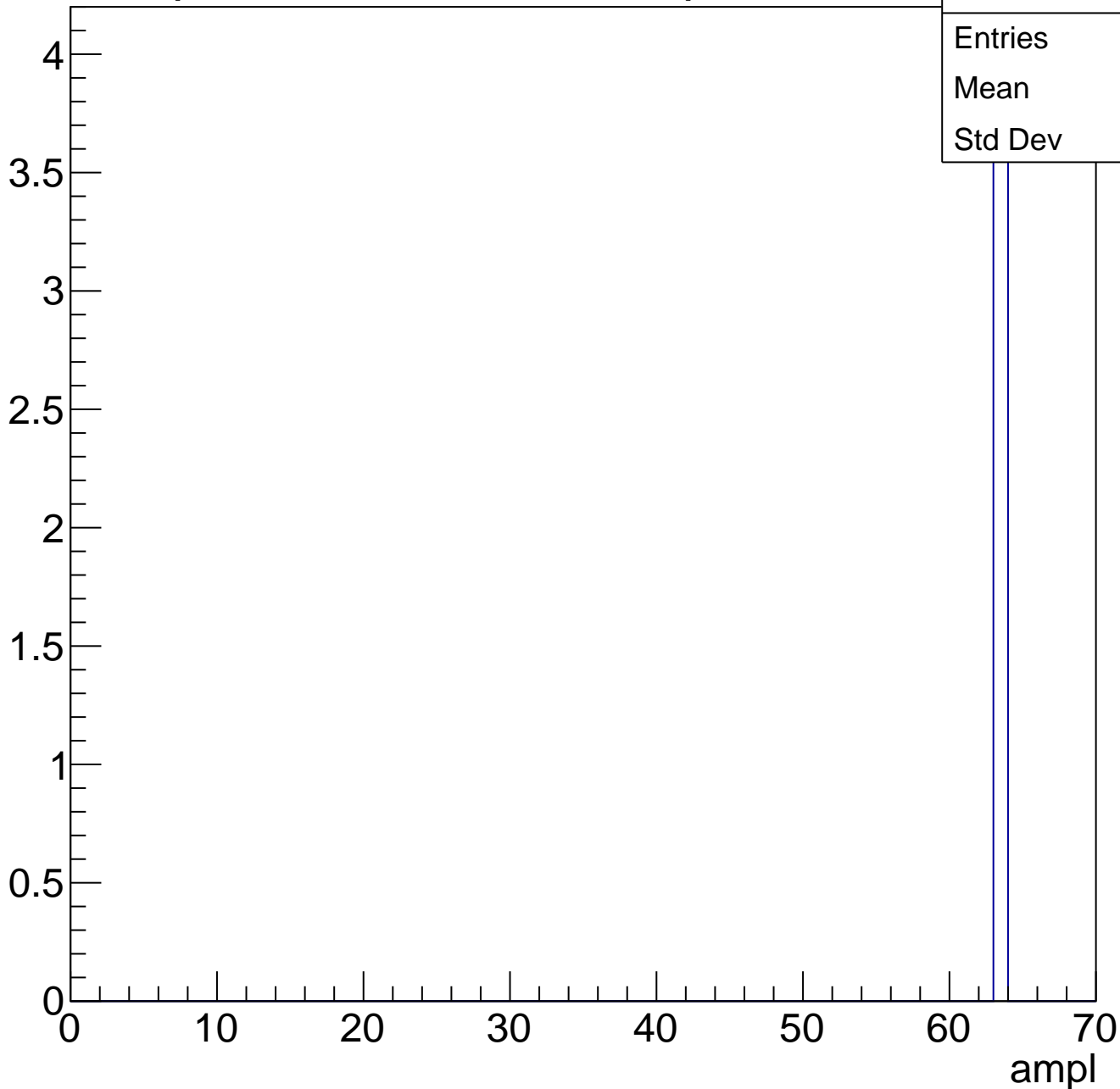
ampl



B0L101S, U24-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	63
Std Dev	0

B0L101S, U24-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch93, adc0

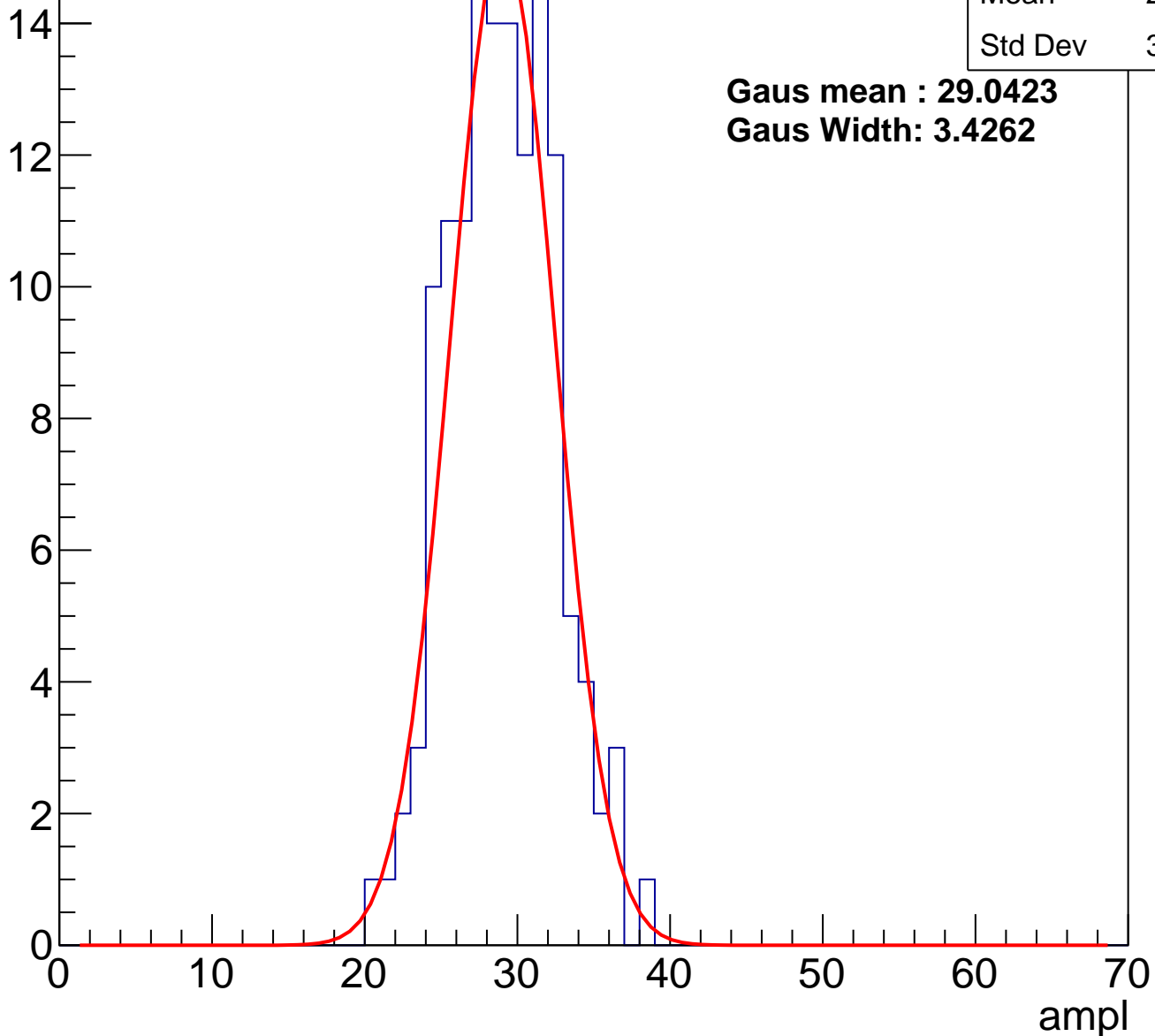
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	28.56
Std Dev	3.412

Gaus mean : 29.0423

Gaus Width: 3.4262

Entry



B0L101S, U24-ch93, adc1

calib_packv5_042523_0143.root, FC#1, port C1

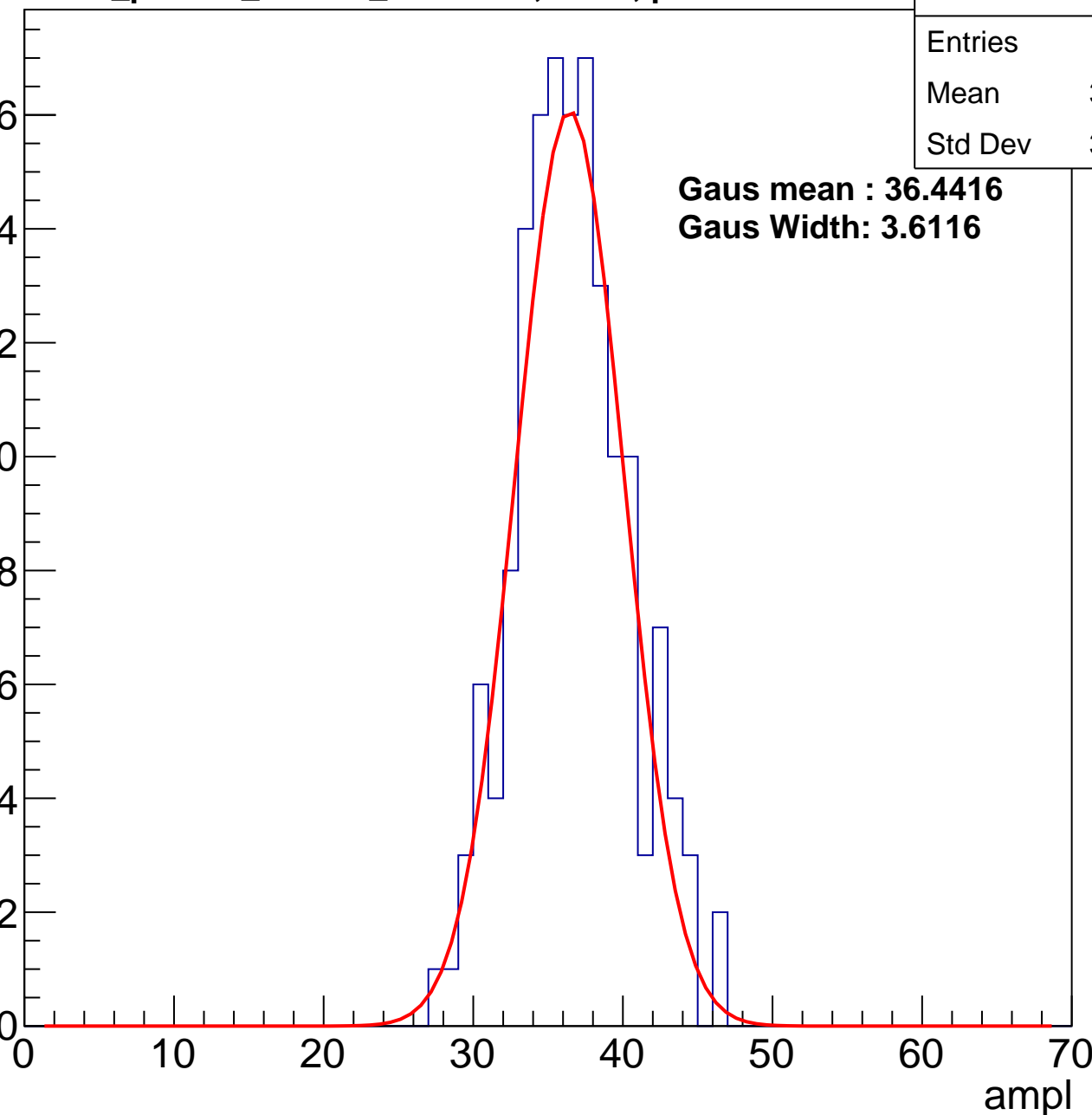
Entry

16
14
12
10
8
6
4
2
0

Entries	155
Mean	36.16
Std Dev	3.744

Gaus mean : 36.4416

Gaus Width: 3.6116



B0L101S, U24-ch93, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

138

Mean

43.03

Std Dev

3.593

Gaus mean : 43.7071

Gaus Width: 3.4612

0

10

20

30

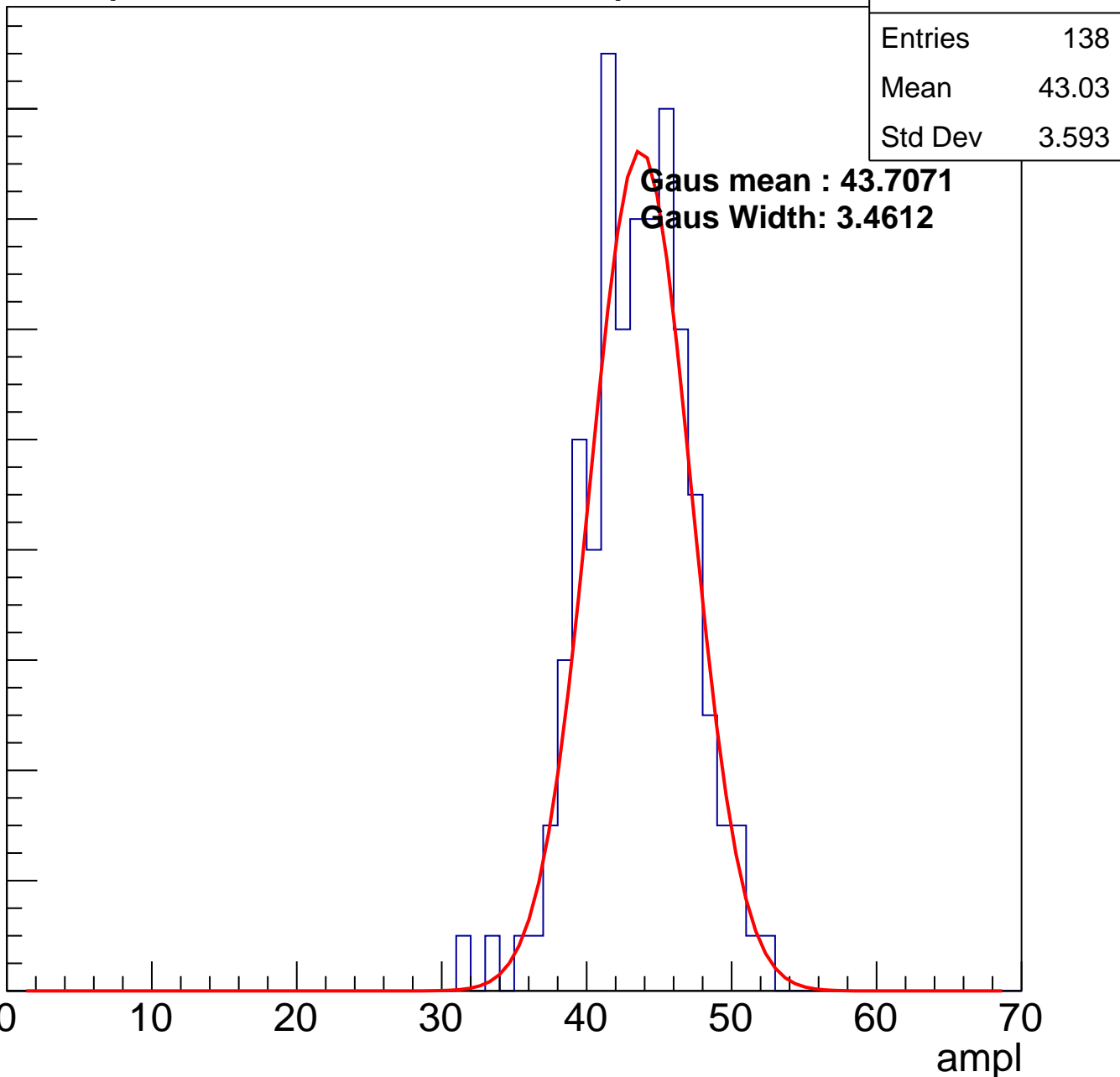
40

50

60

70

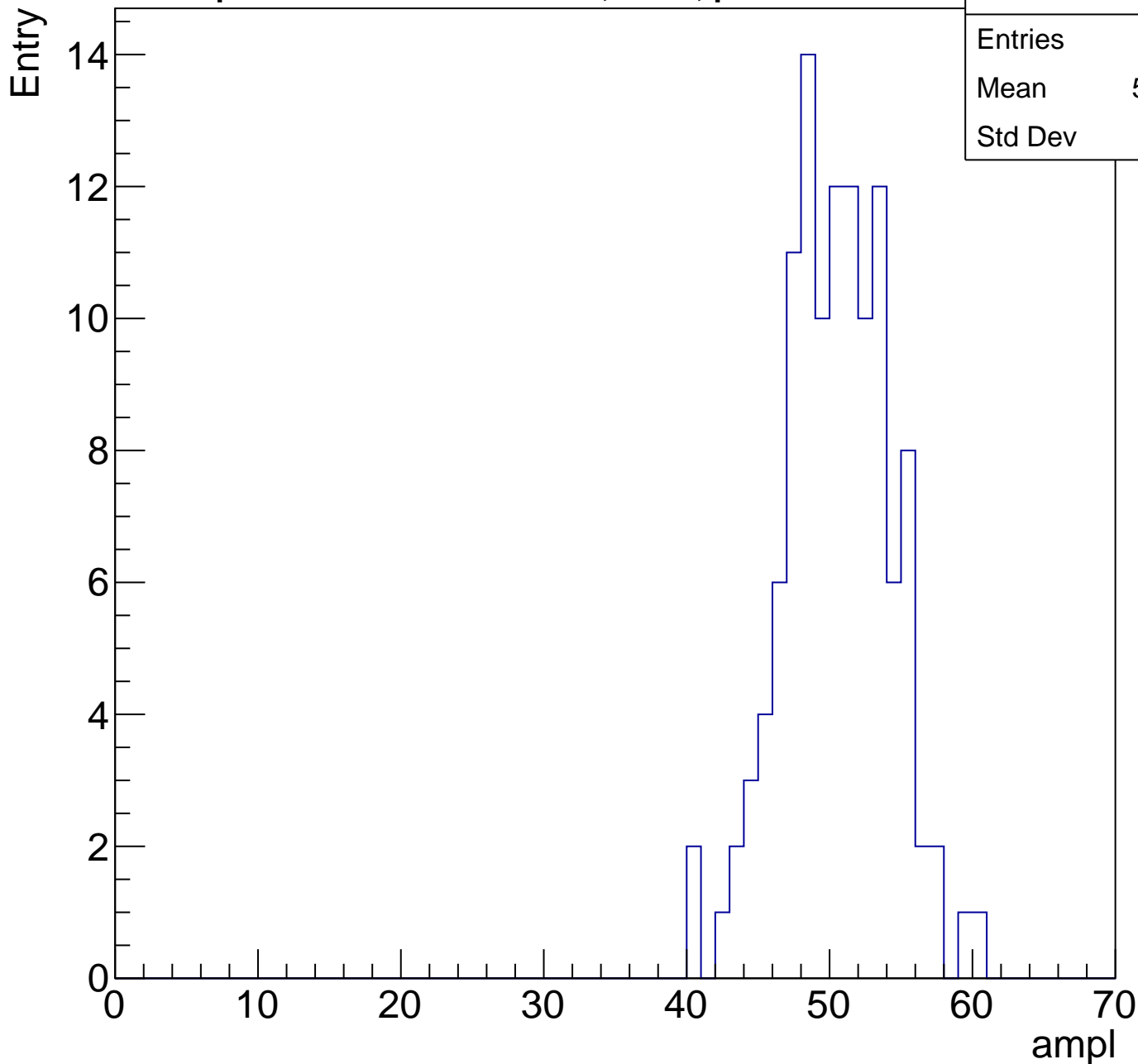
ampl



B0L101S, U24-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

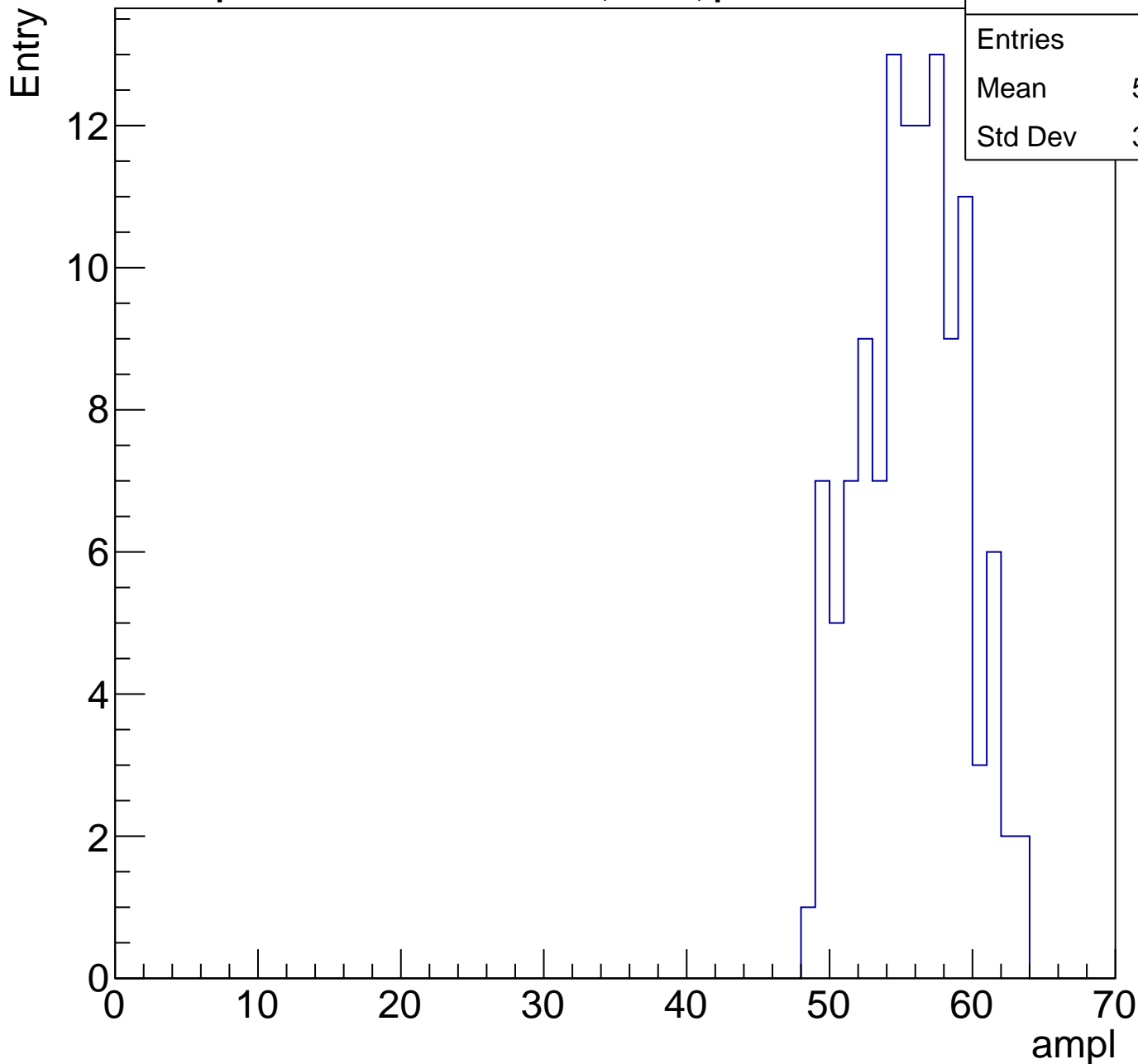
Entries	119
Mean	50.02
Std Dev	3.71



B0L101S, U24-ch93, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	55.29
Std Dev	3.536



B0L101S, U24-ch93, adc5

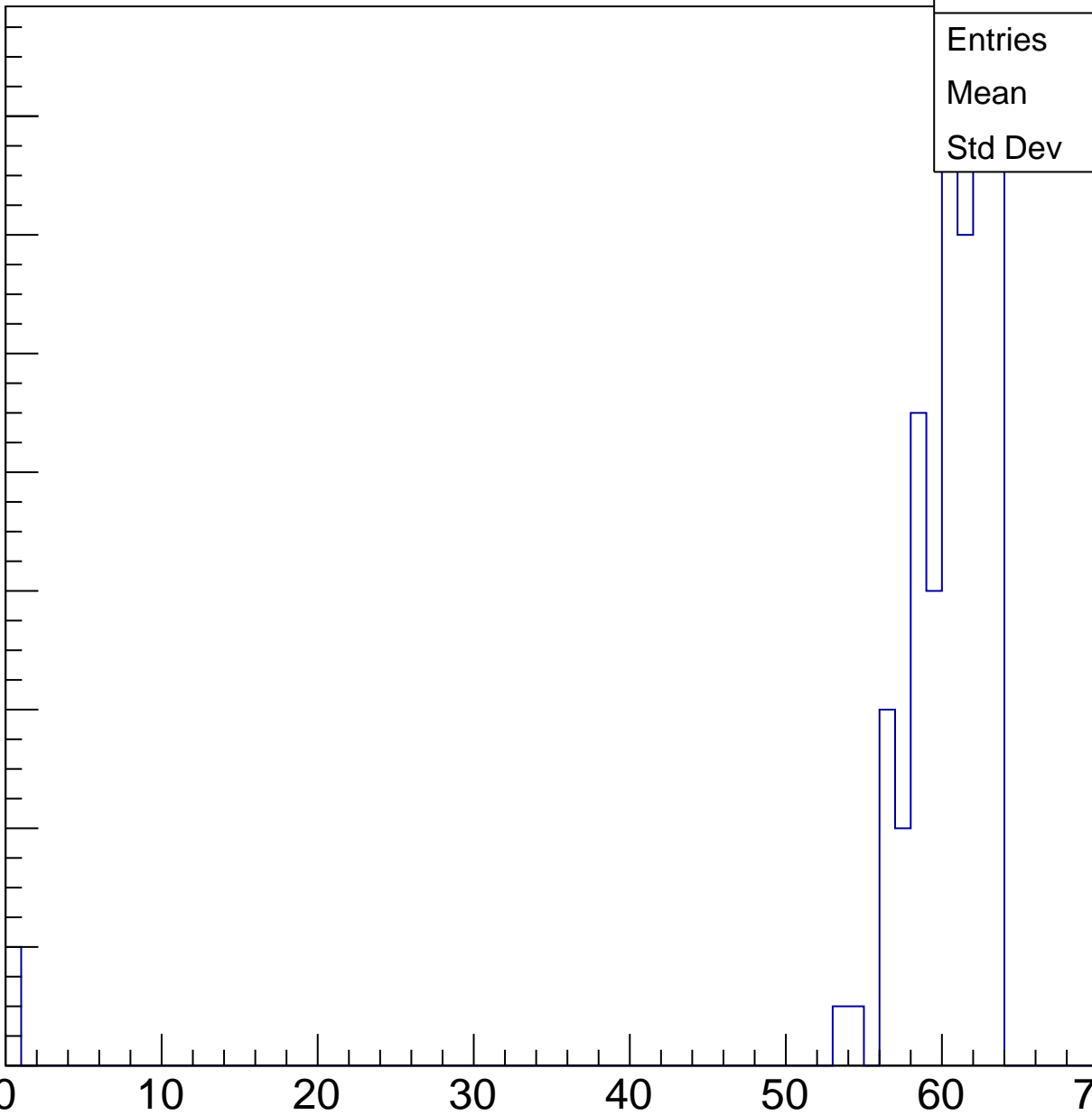
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.93
Std Dev	8.888

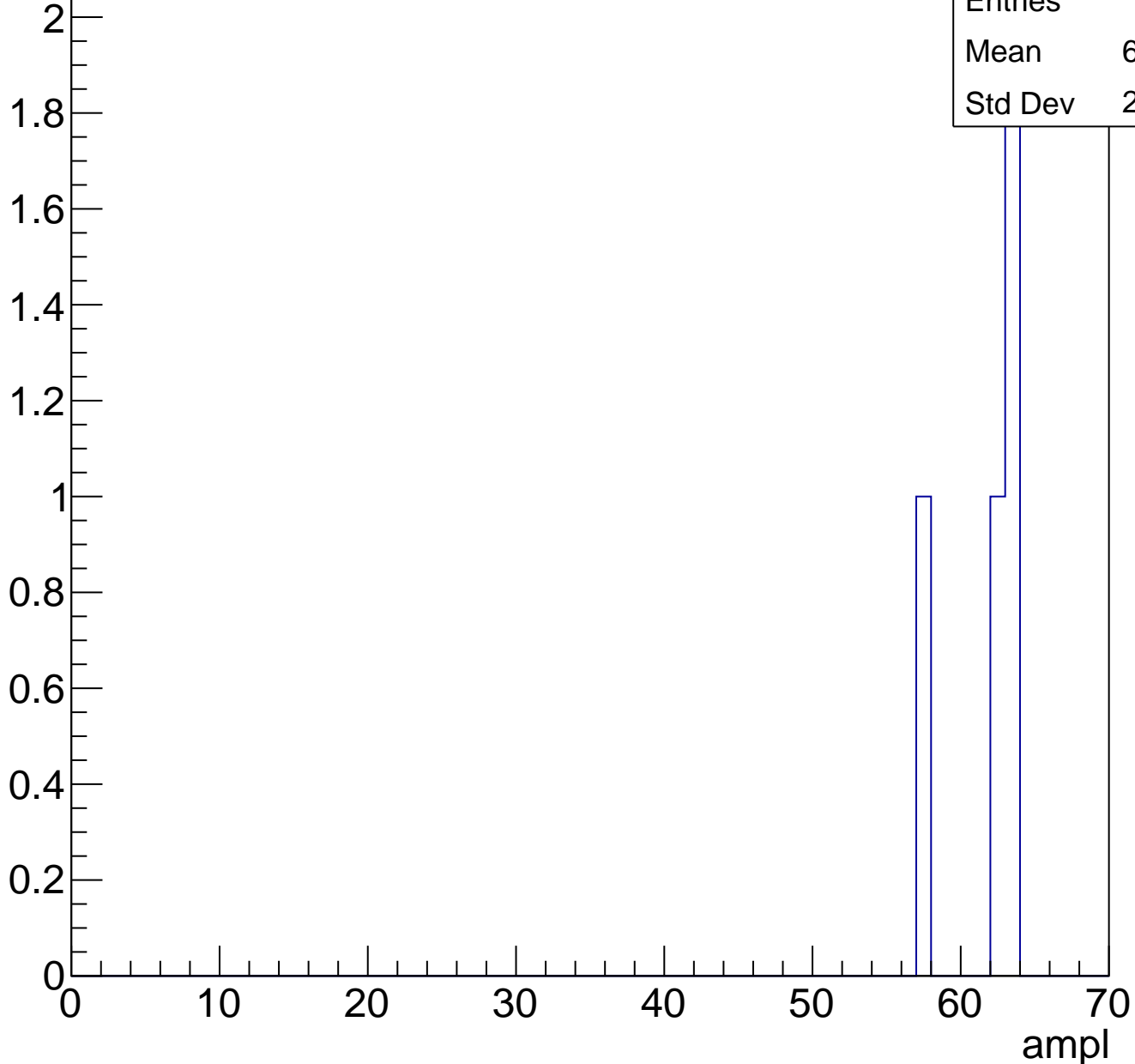
ampl



B0L101S, U24-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch94, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	29.47
Std Dev	3.751

Gaus mean : 29.8754

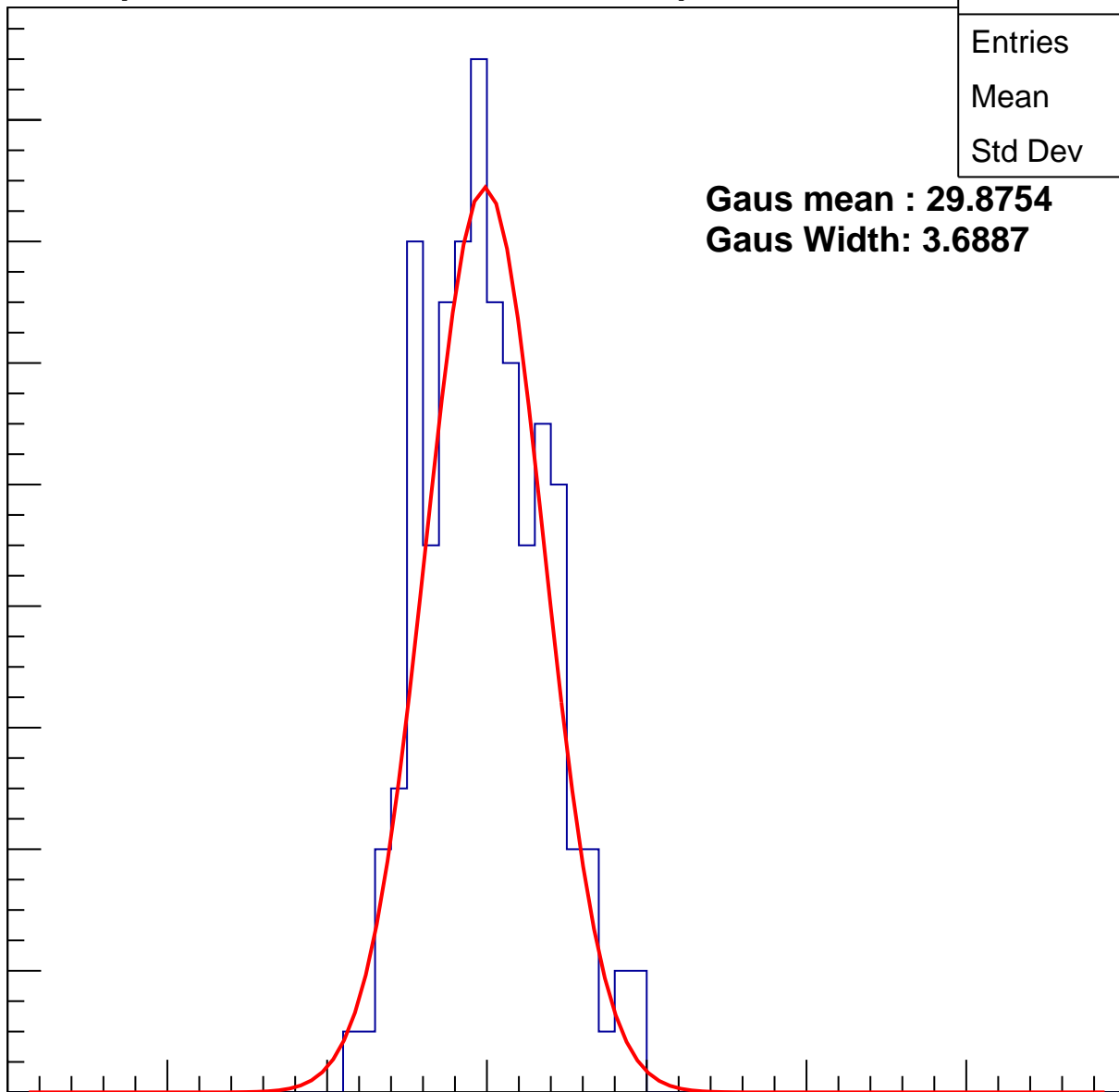
Gaus Width: 3.6887

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch94, adc1

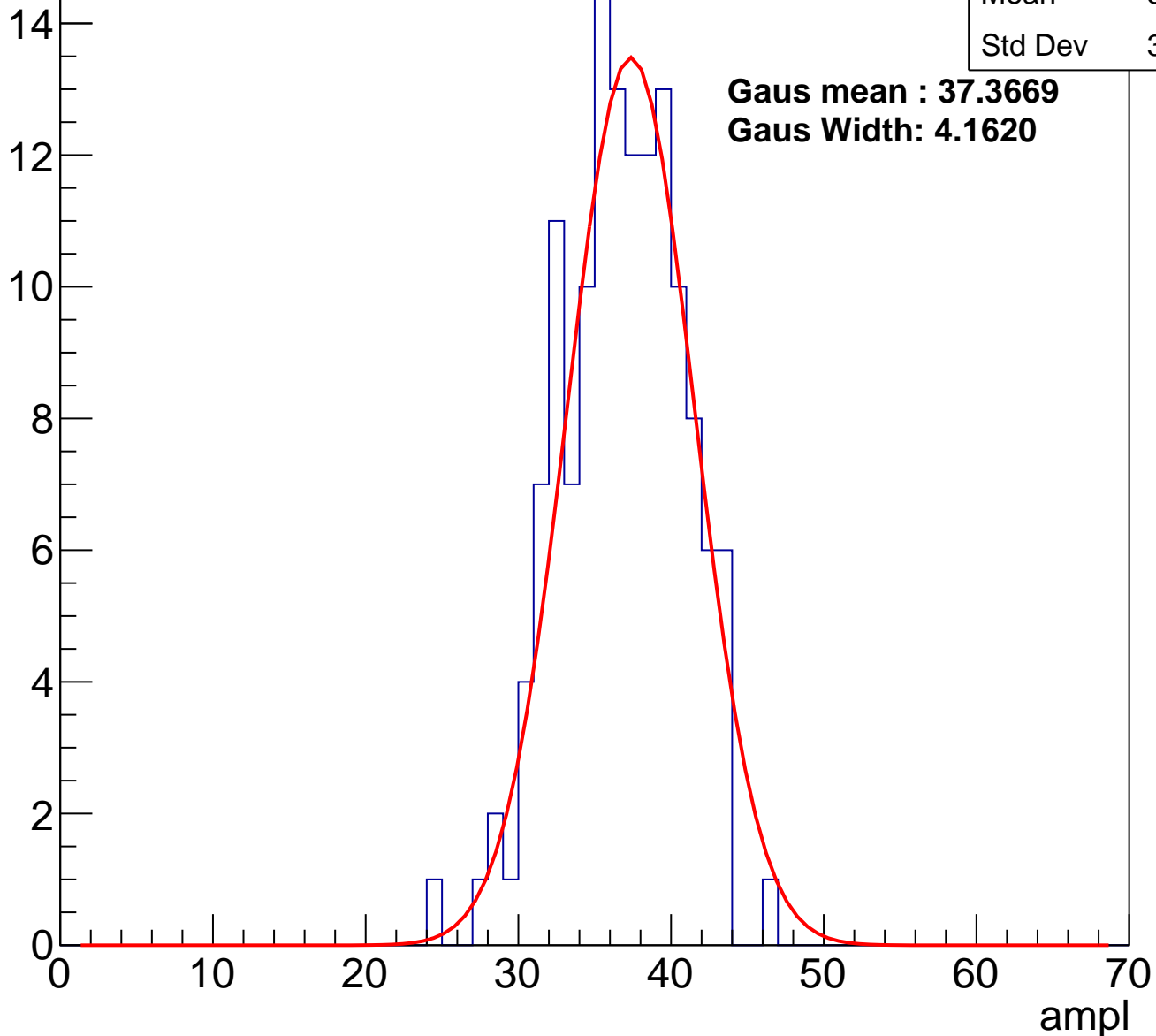
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	36.29
Std Dev	3.919

Gaus mean : 37.3669

Gaus Width: 4.1620

Entry



B0L101S, U24-ch94, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	110
Mean	42.43
Std Dev	3.735

Gaus mean : 42.7280

Gaus Width: 3.6222

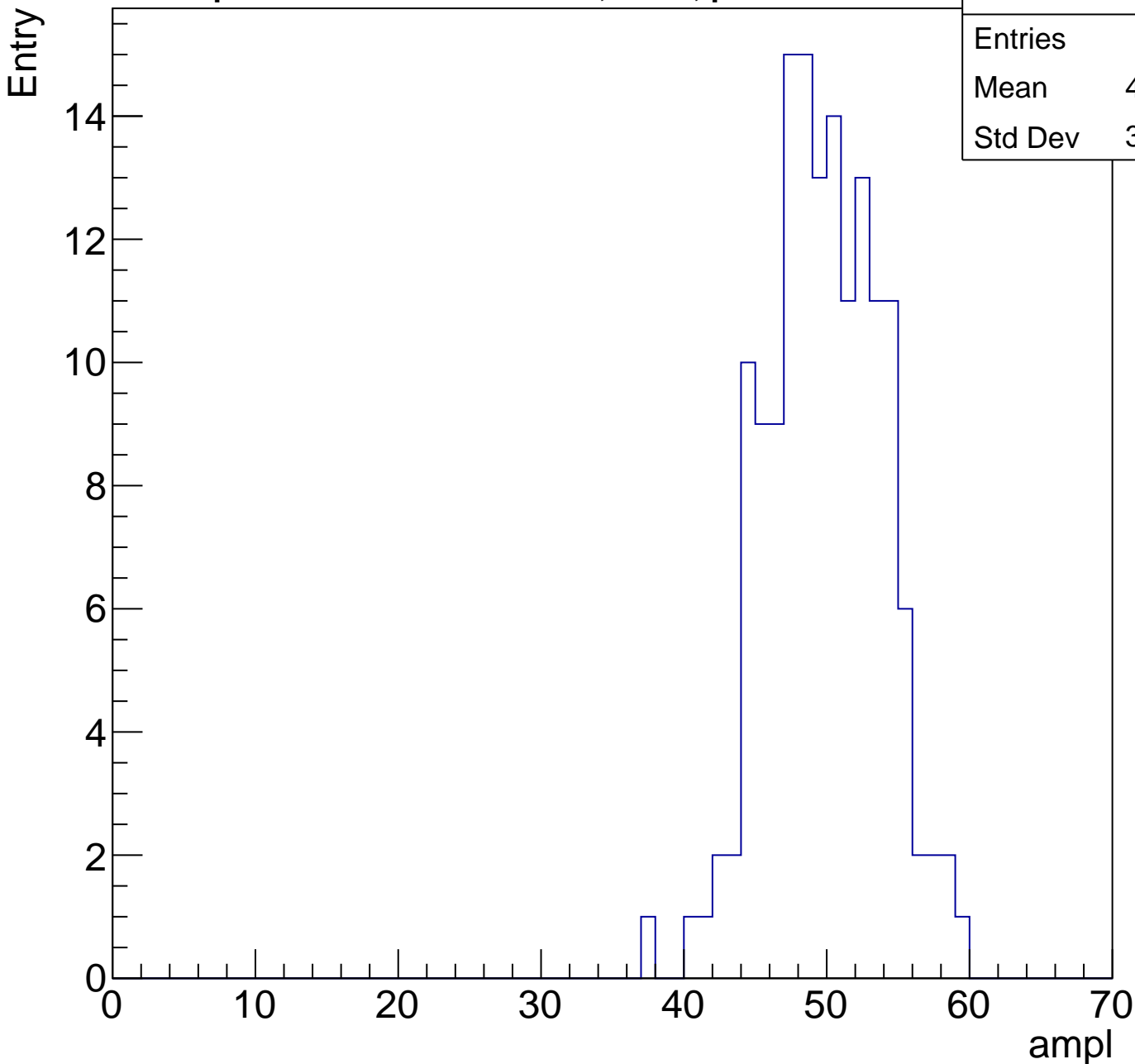


ampl (bin center)	Entry
33	2
35	1
37	6
39	5
41	11
43	12
45	11
47	9
49	4
51	5
53	1

B0L101S, U24-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	49.36
Std Dev	3.919



B0L101S, U24-ch94, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	55.84
Std Dev	6.046

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

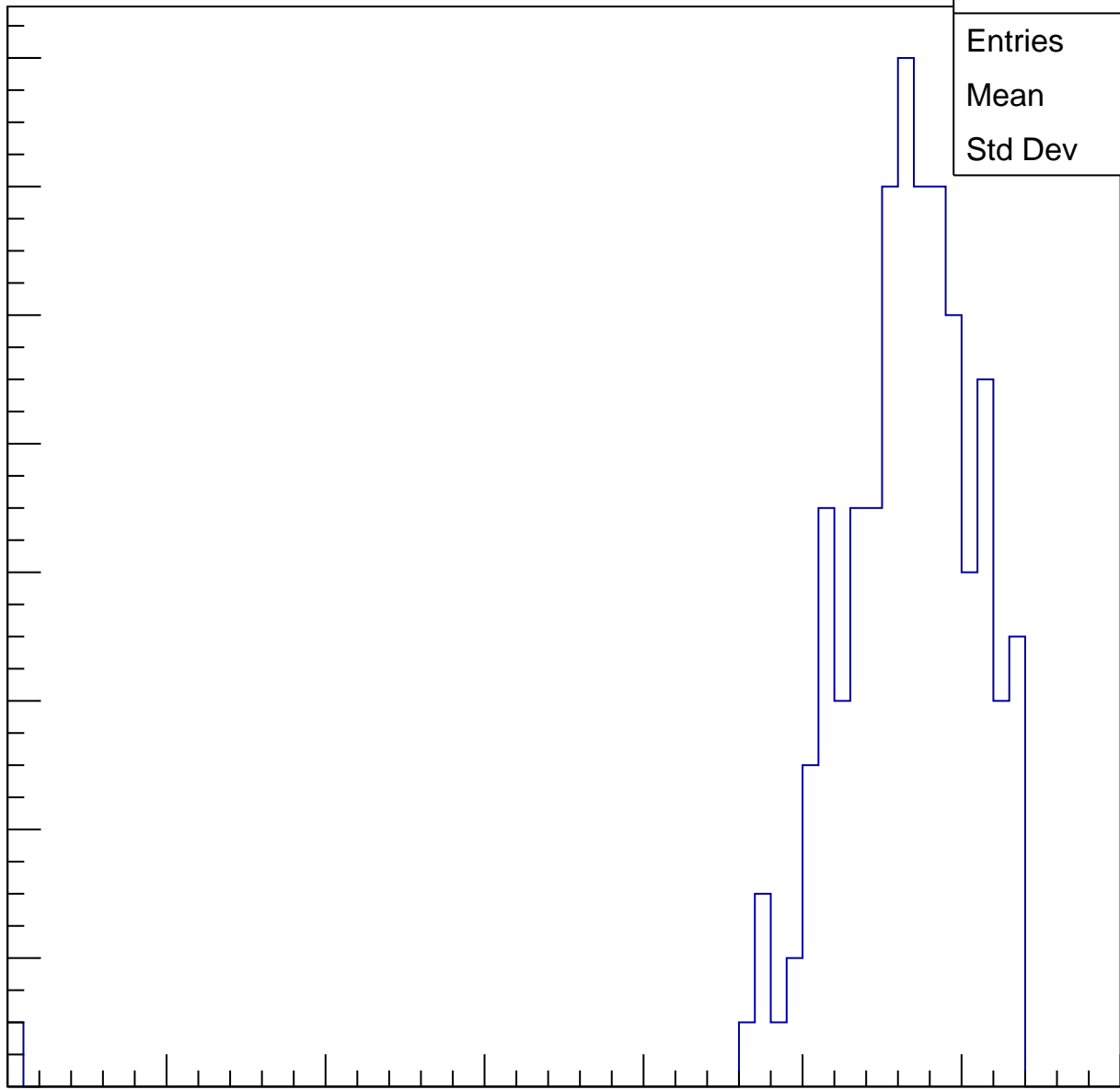
40

50

60

70

ampl



B0L101S, U24-ch94, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

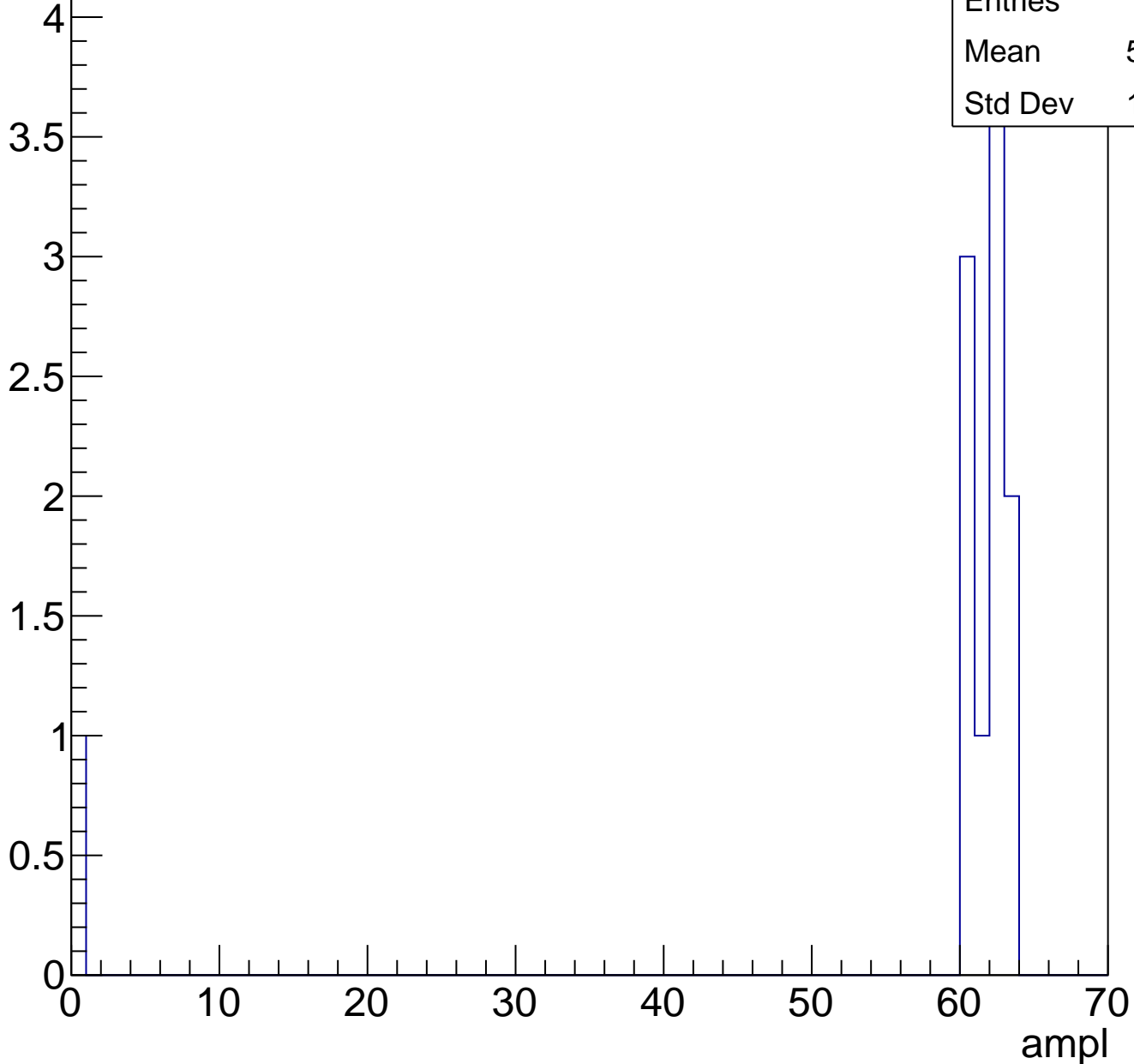
ampl

Entries	55
Mean	60.47
Std Dev	2.026

B0L101S, U24-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	11
Mean	55.91
Std Dev	17.71

B0L101S, U24-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch95, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	28.57
Std Dev	4.901

Gaus mean : 29.1089

Gaus Width: 3.4289

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

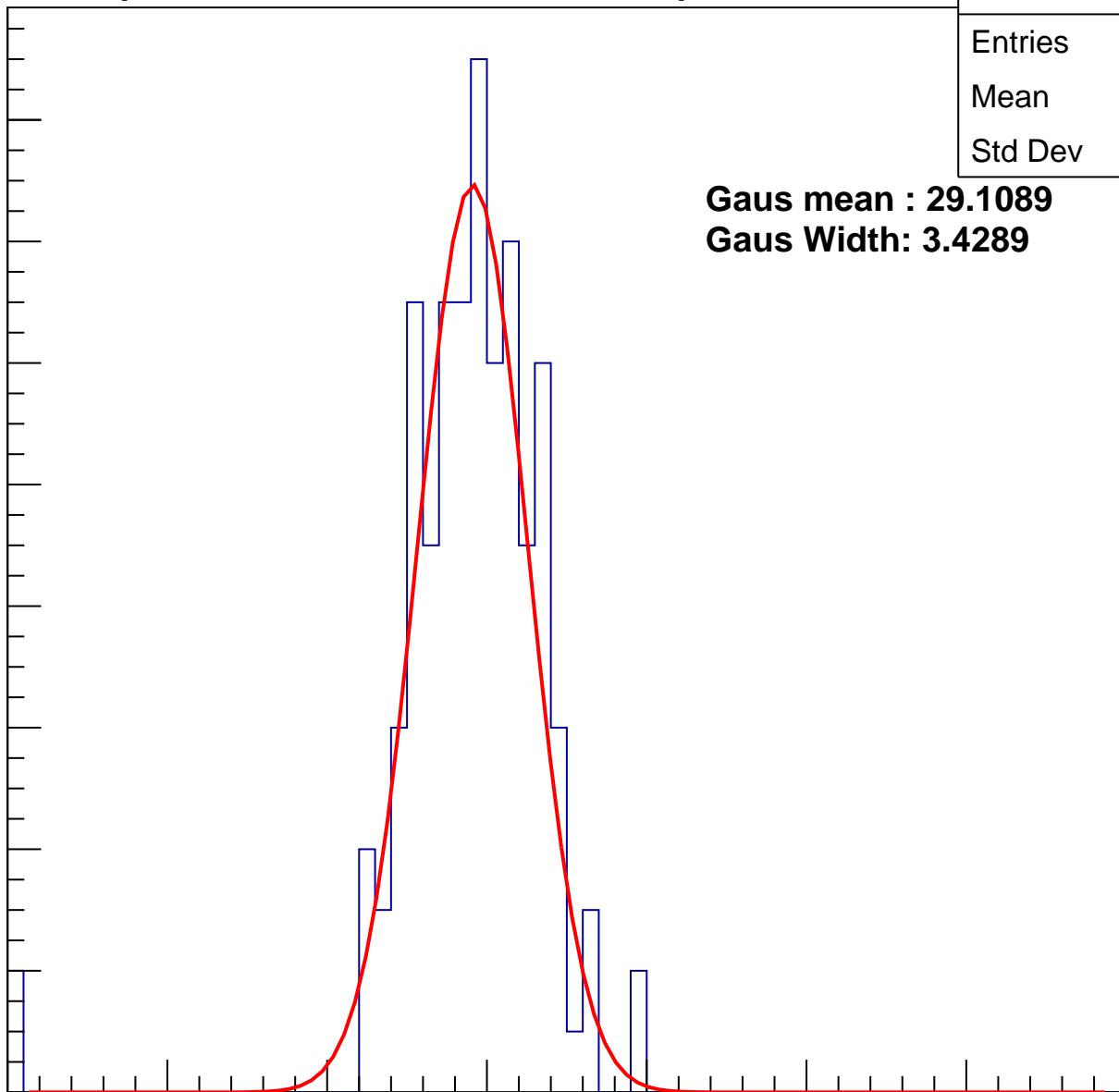
40

50

60

70

ampl



B0L101S, U24-ch95, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	35.52
Std Dev	3.355

Gaus mean : 35.8183

Gaus Width: 3.8549

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

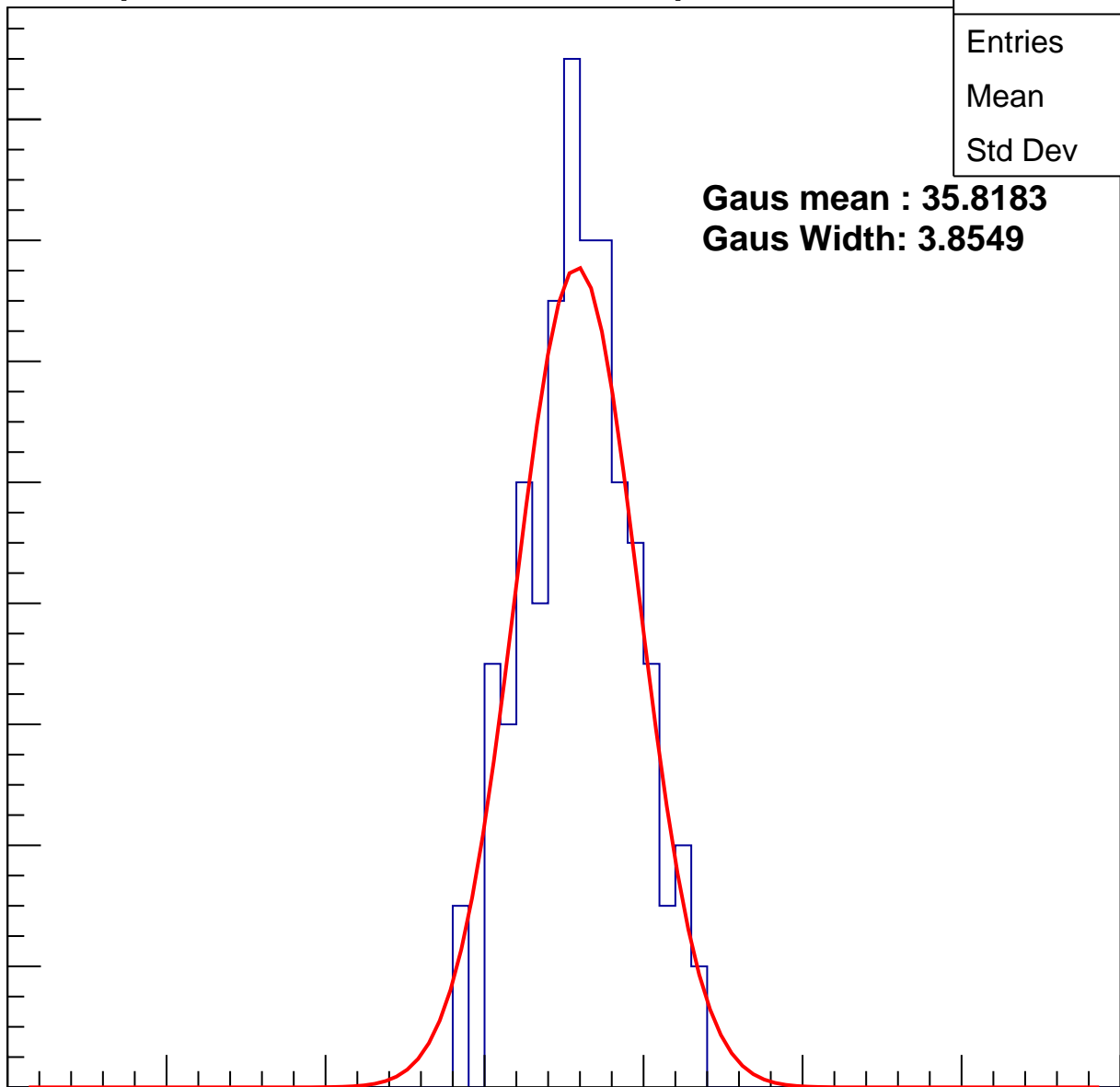
40

50

60

70

ampl



B0L101S, U24-ch95, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

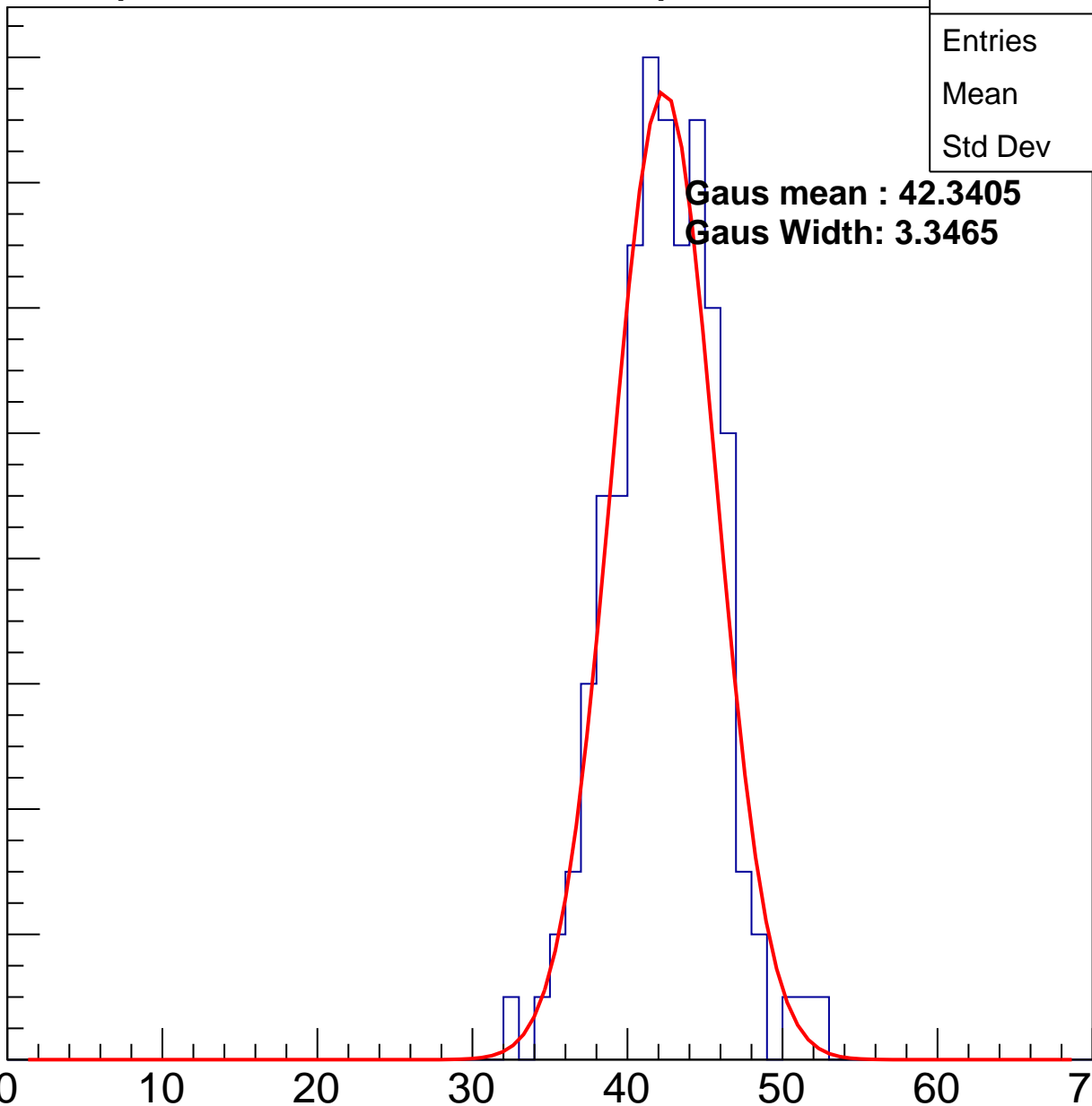
16
14
12
10
8
6
4
2
0

Entries	133
Mean	41.91
Std Dev	3.408

Gaus mean : 42.3405

Gaus Width: 3.3465

ampl

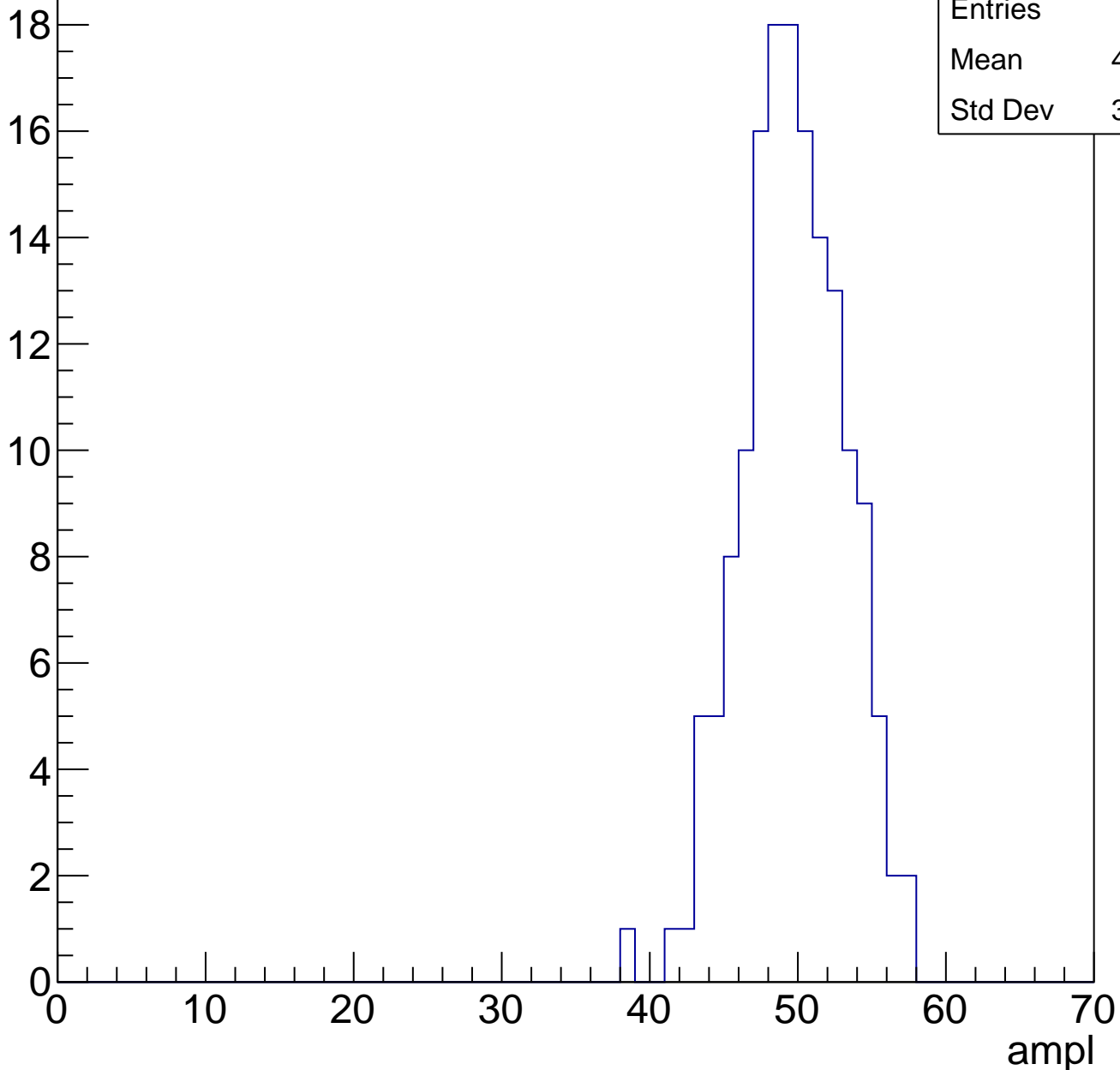


B0L101S, U24-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	49.23
Std Dev	3.429

Entry



B0L101S, U24-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	56.16
Std Dev	3.24

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

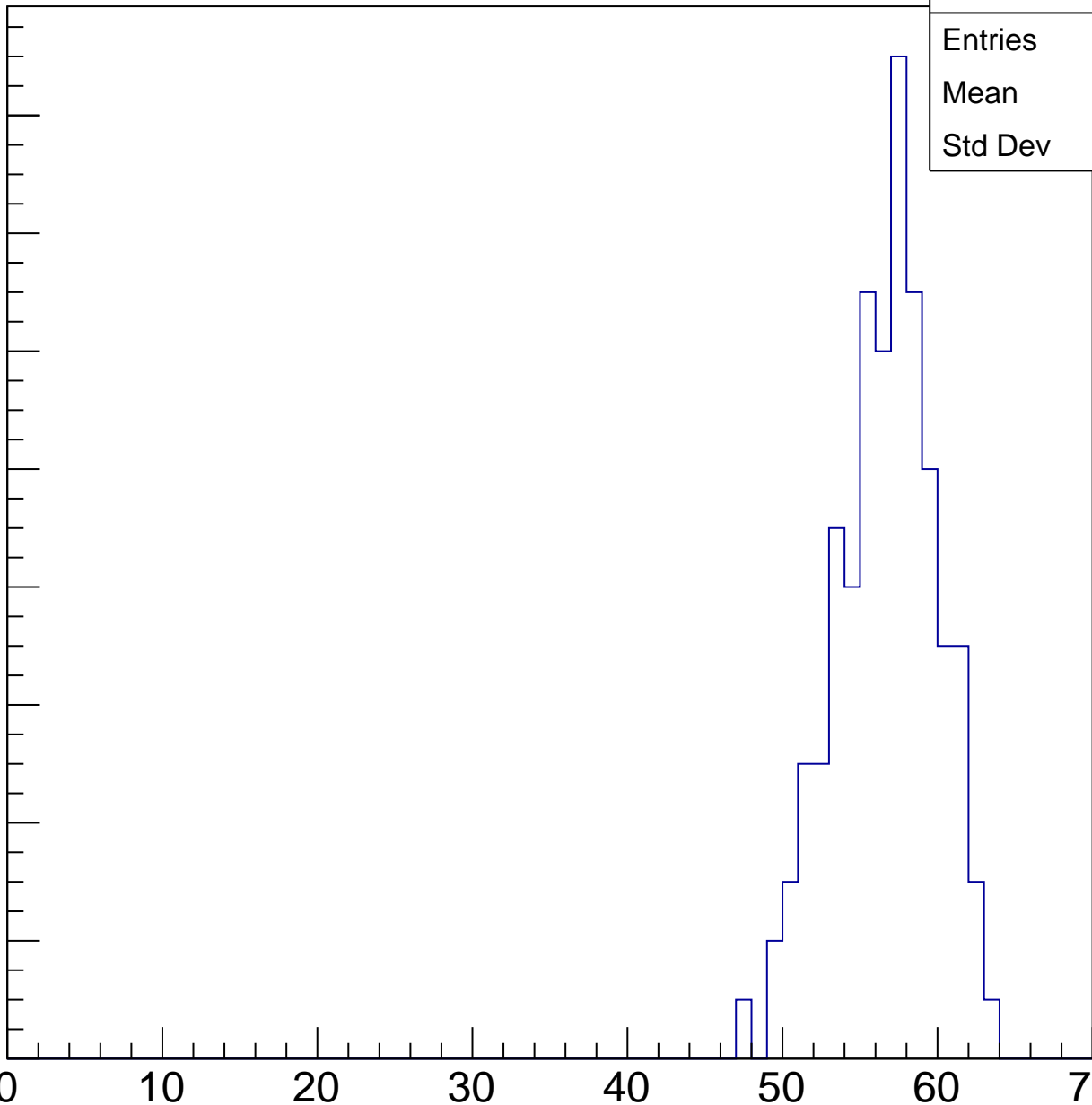
40

50

60

70

ampl



B0L101S, U24-ch95, adc5

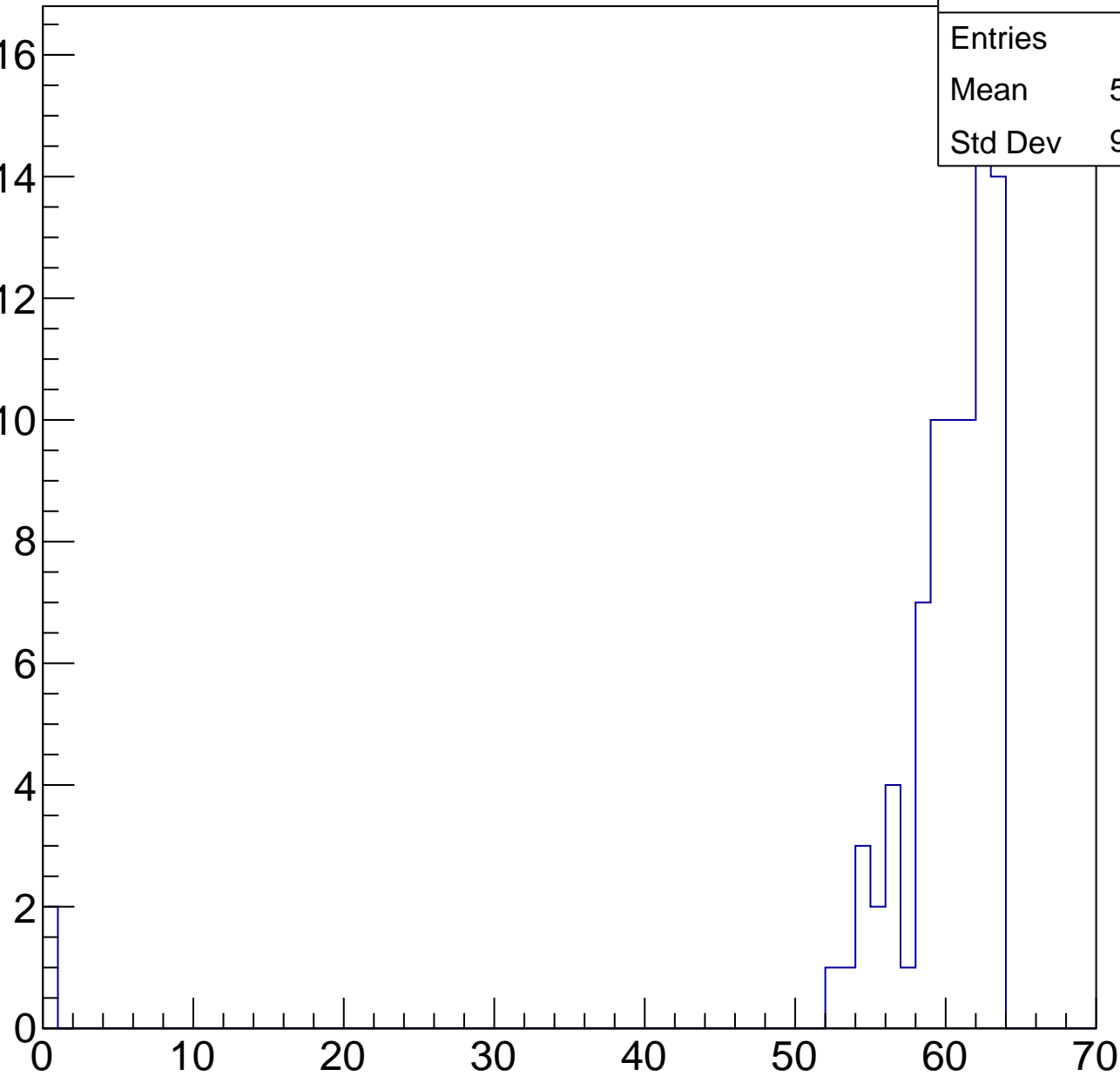
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.49
Std Dev	9.685

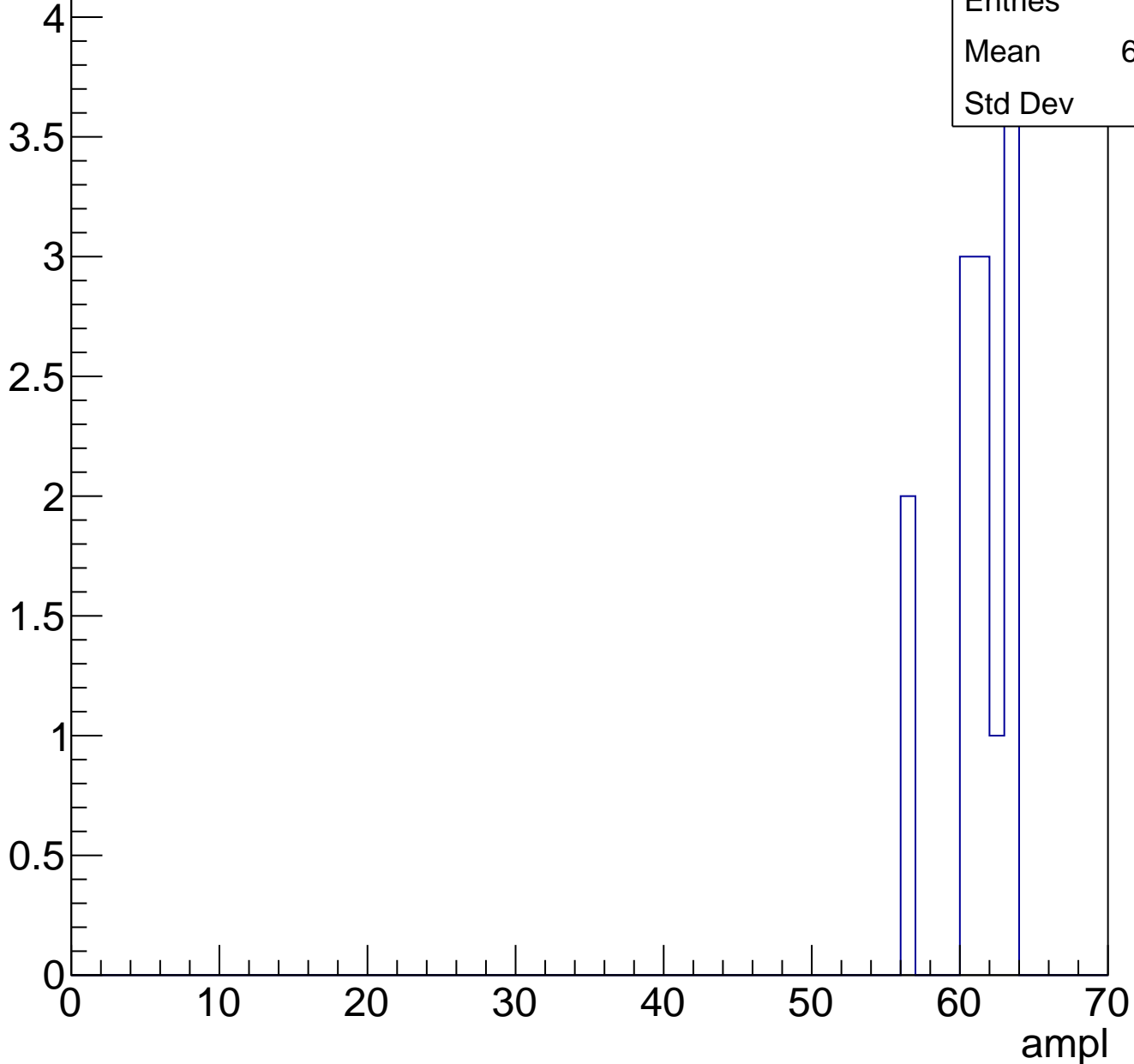
ampl



B0L101S, U24-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch96, adc0

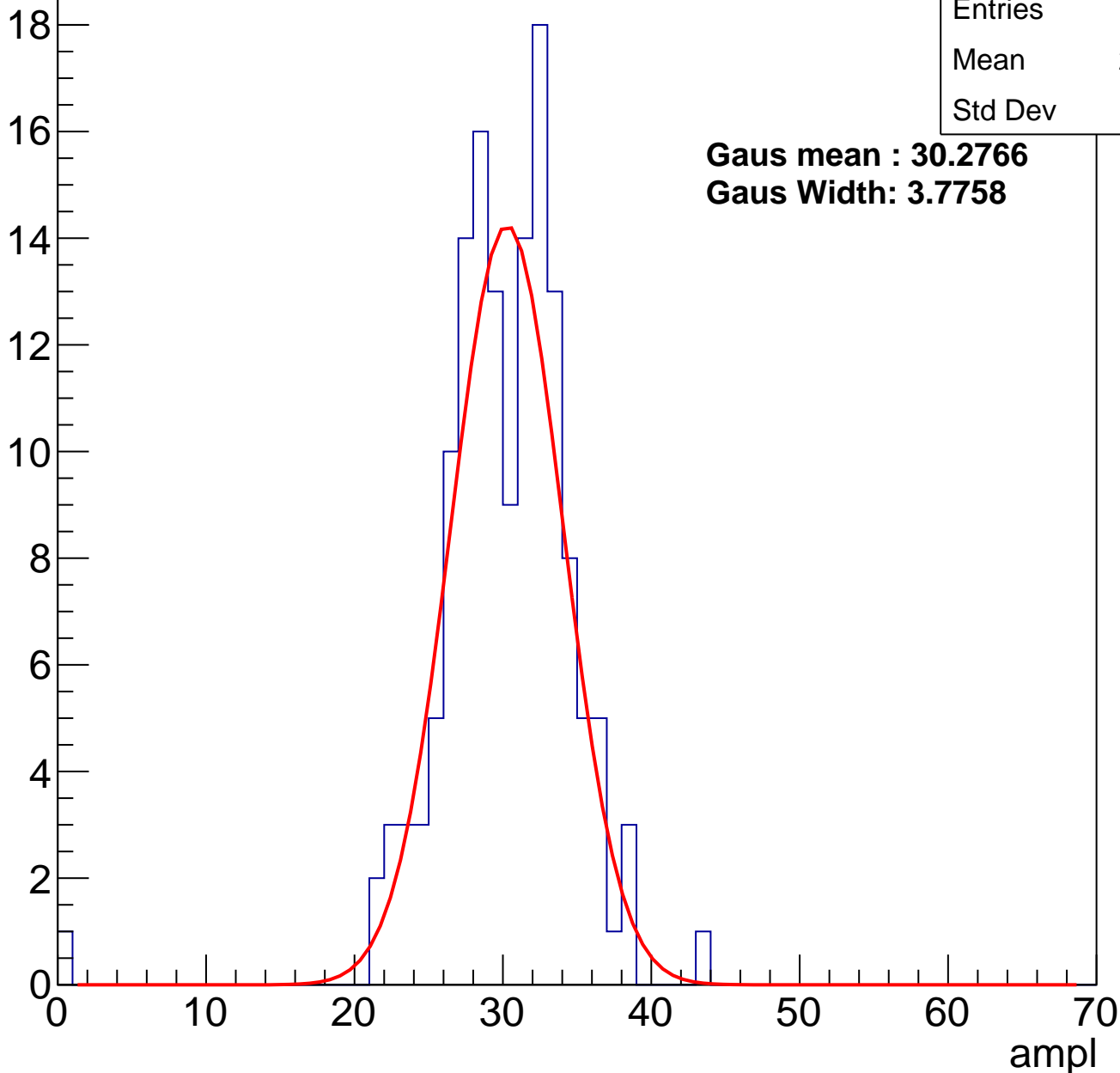
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.71
Std Dev	4.54

Gaus mean : 30.2766

Gaus Width: 3.7758

Entry



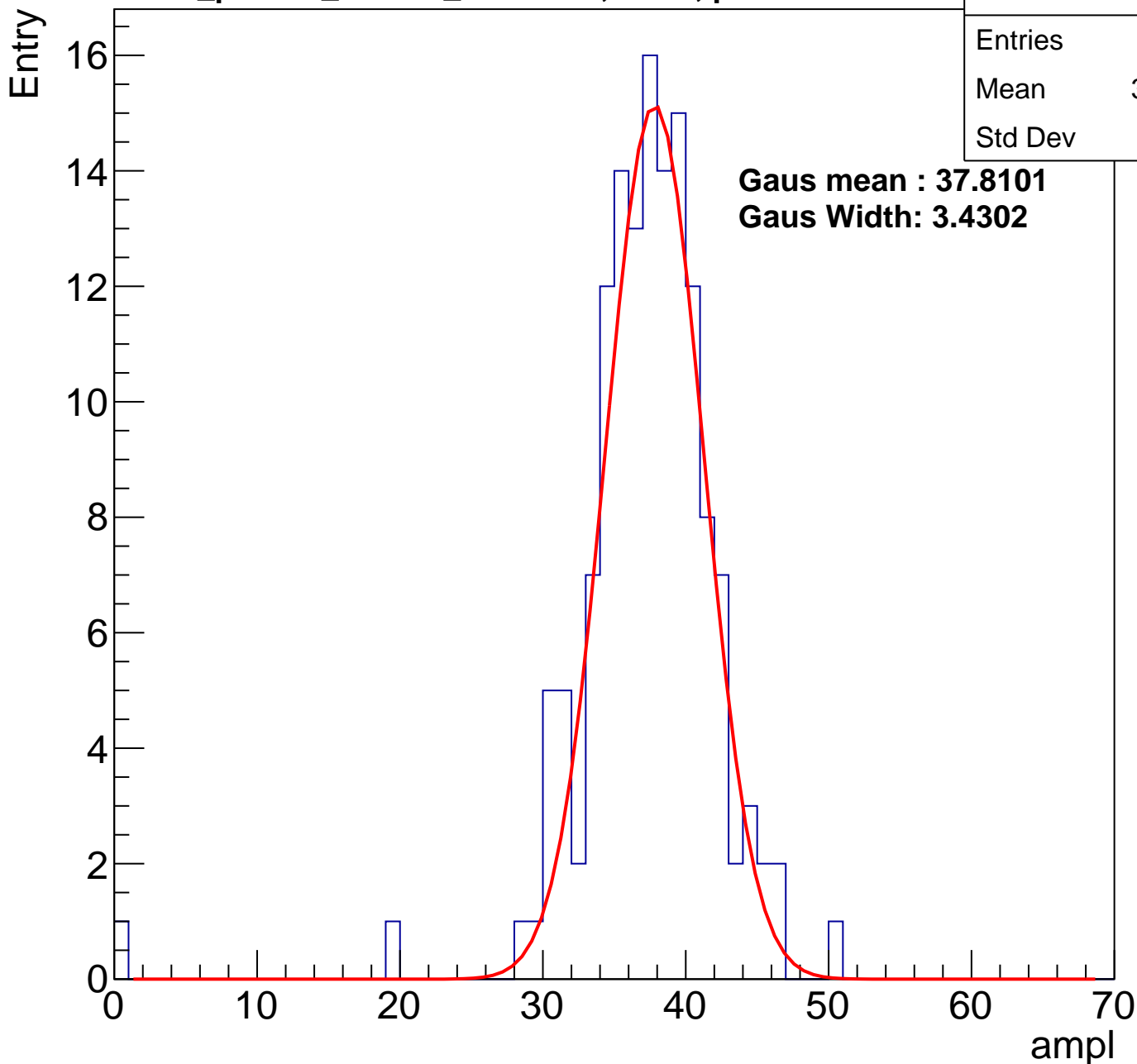
B0L101S, U24-ch96, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	36.83
Std Dev	5.11

Gaus mean : 37.8101

Gaus Width: 3.4302



B0L101S, U24-ch96, adc2

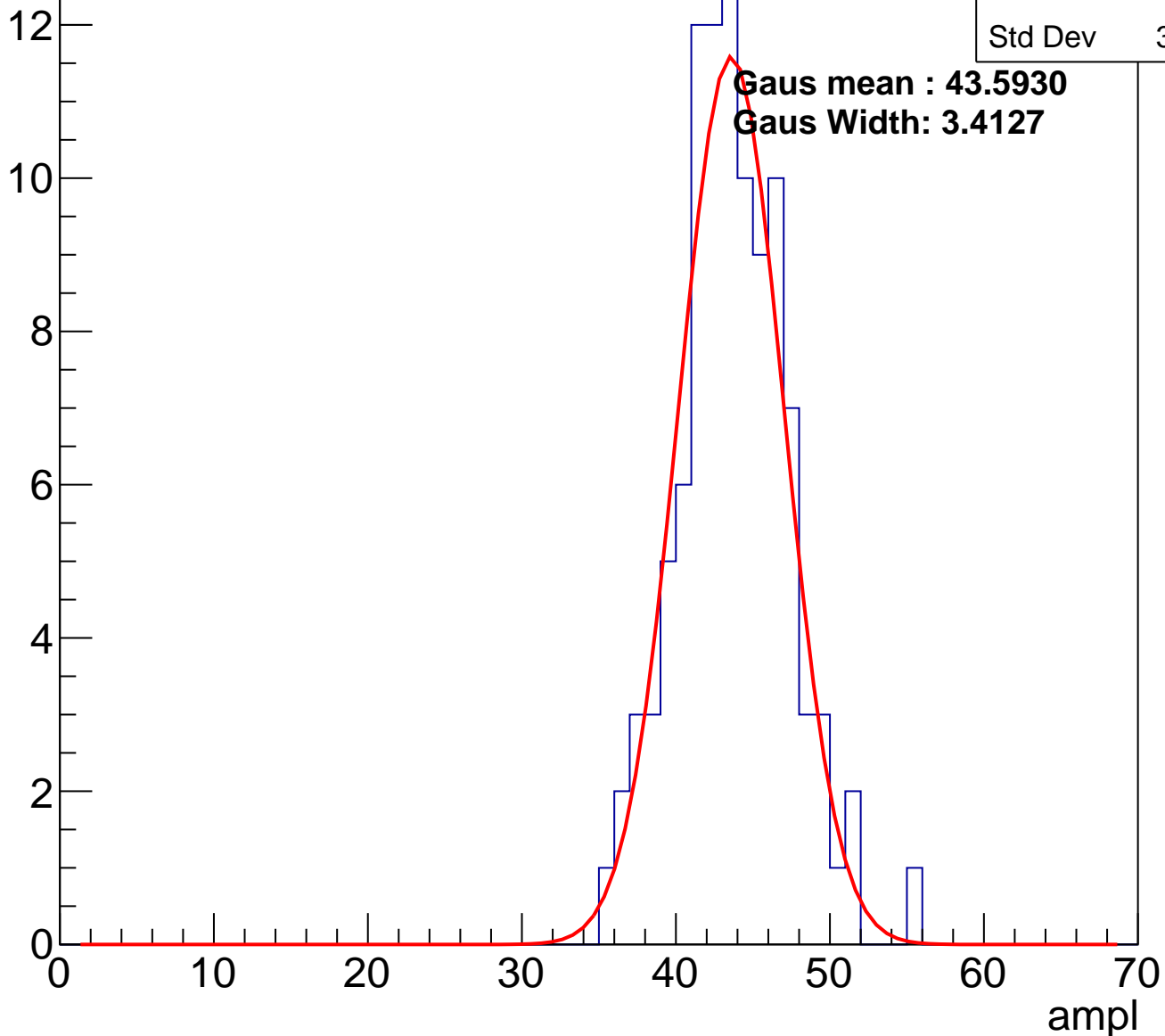
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	43.24
Std Dev	3.532

Gaus mean : 43.5930

Gaus Width: 3.4127

Entry



B0L101S, U24-ch96, adc3

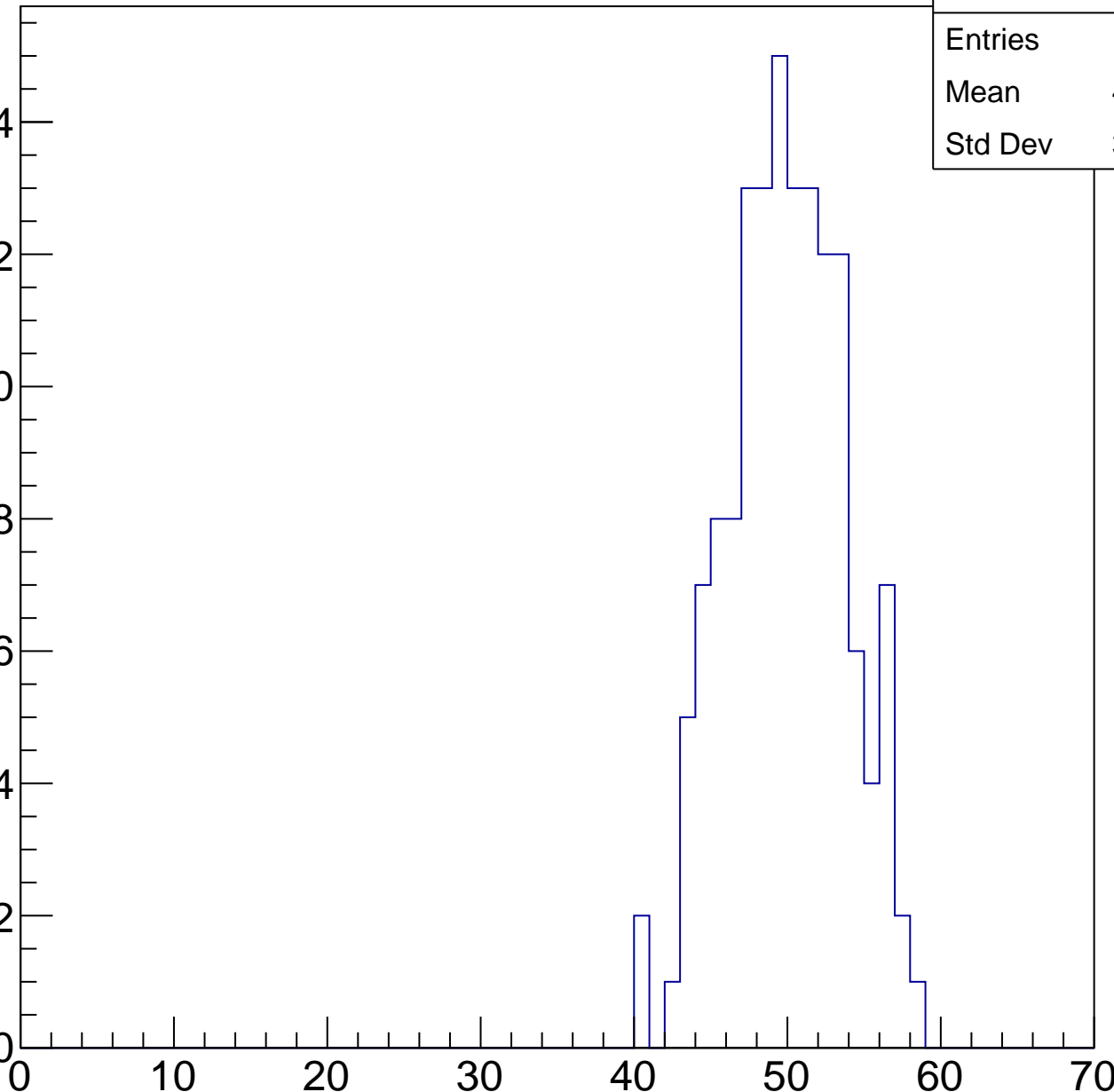
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	142
Mean	49.46
Std Dev	3.764

ampl



B0L101S, U24-ch96, adc4

calib_packv5_042523_0143.root, FC#1, port C1

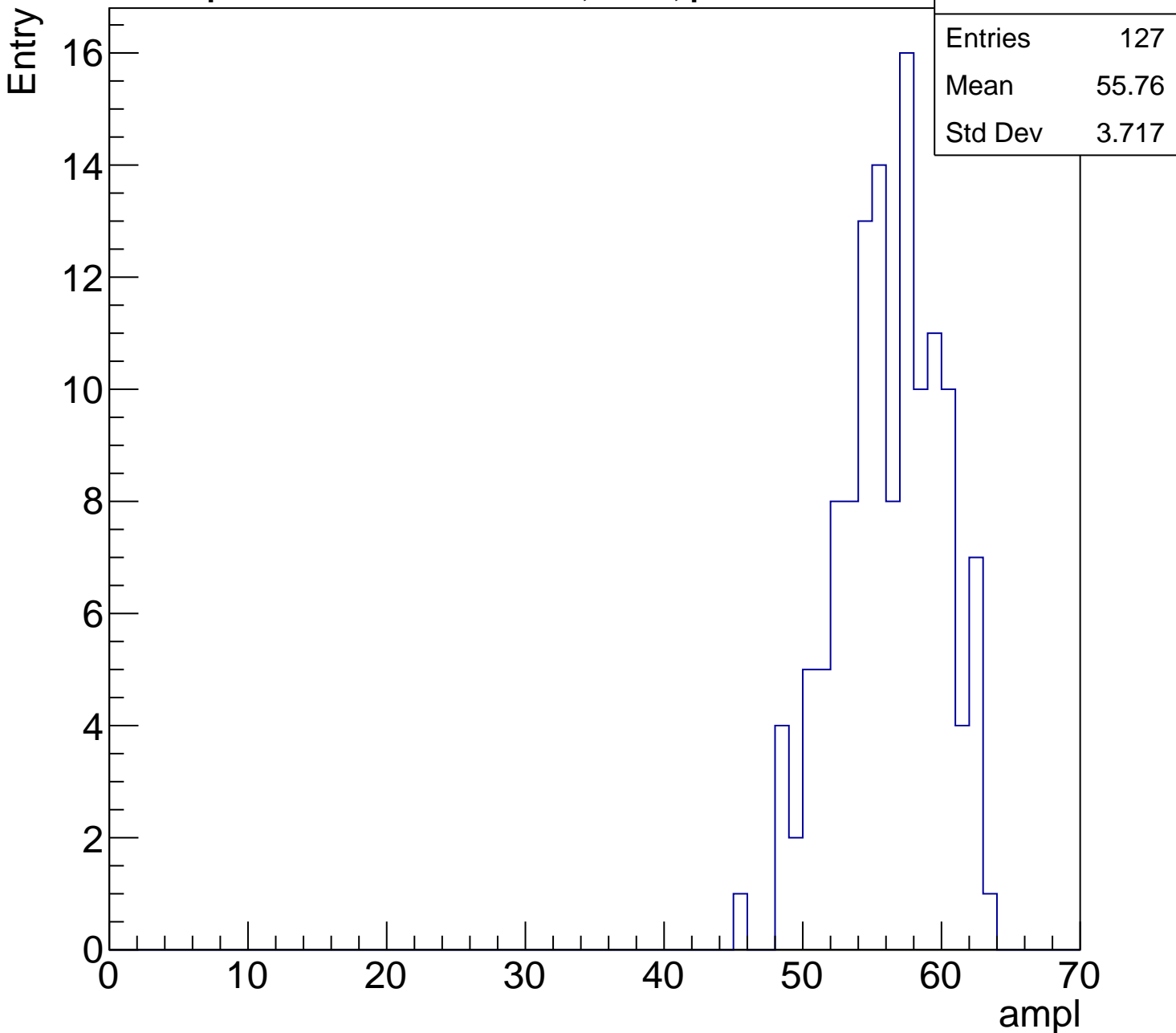
Entries	127
Mean	55.76
Std Dev	3.717

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch96, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

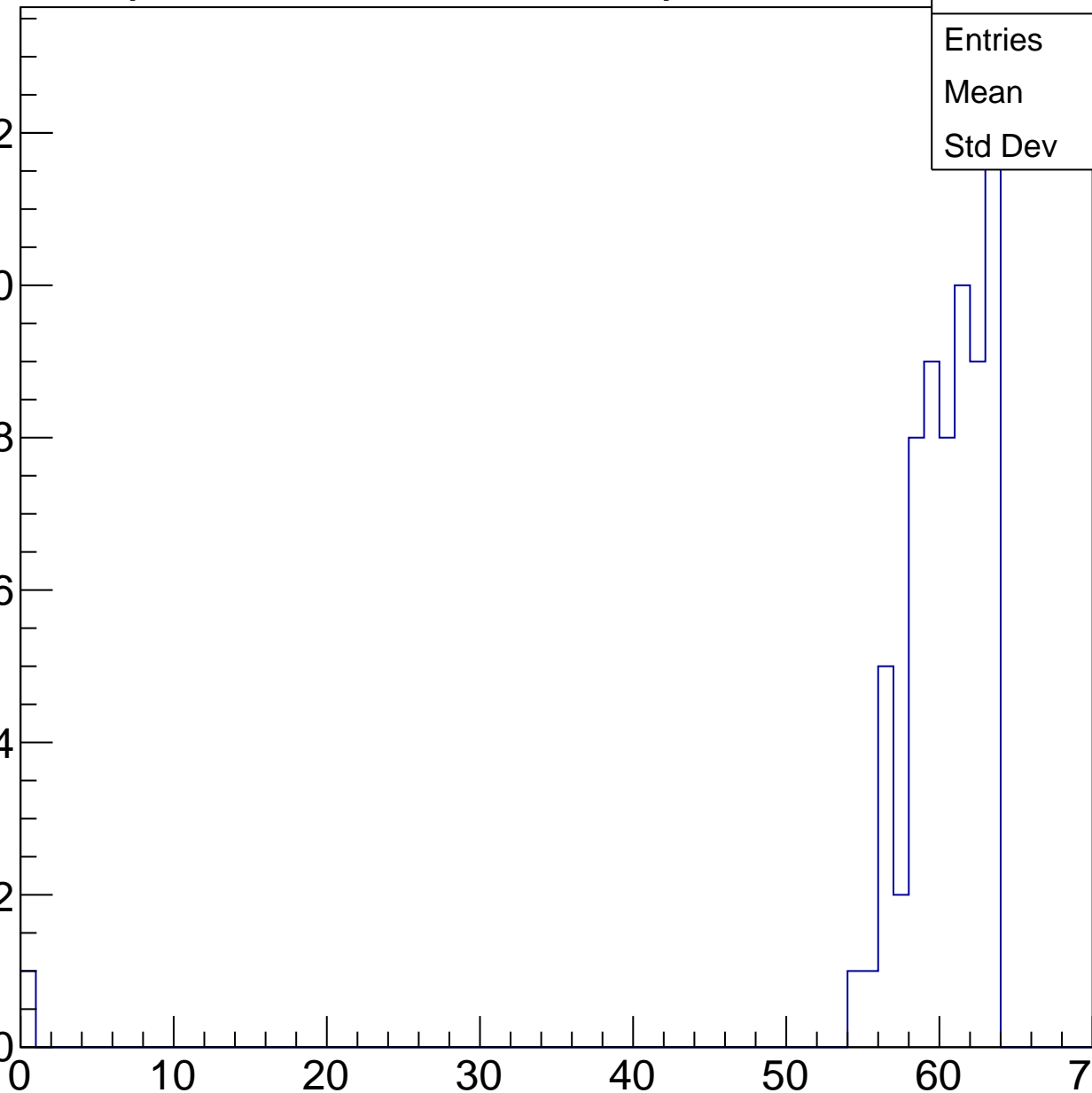
4

2

0

Entries	67
Mean	59.18
Std Dev	7.647

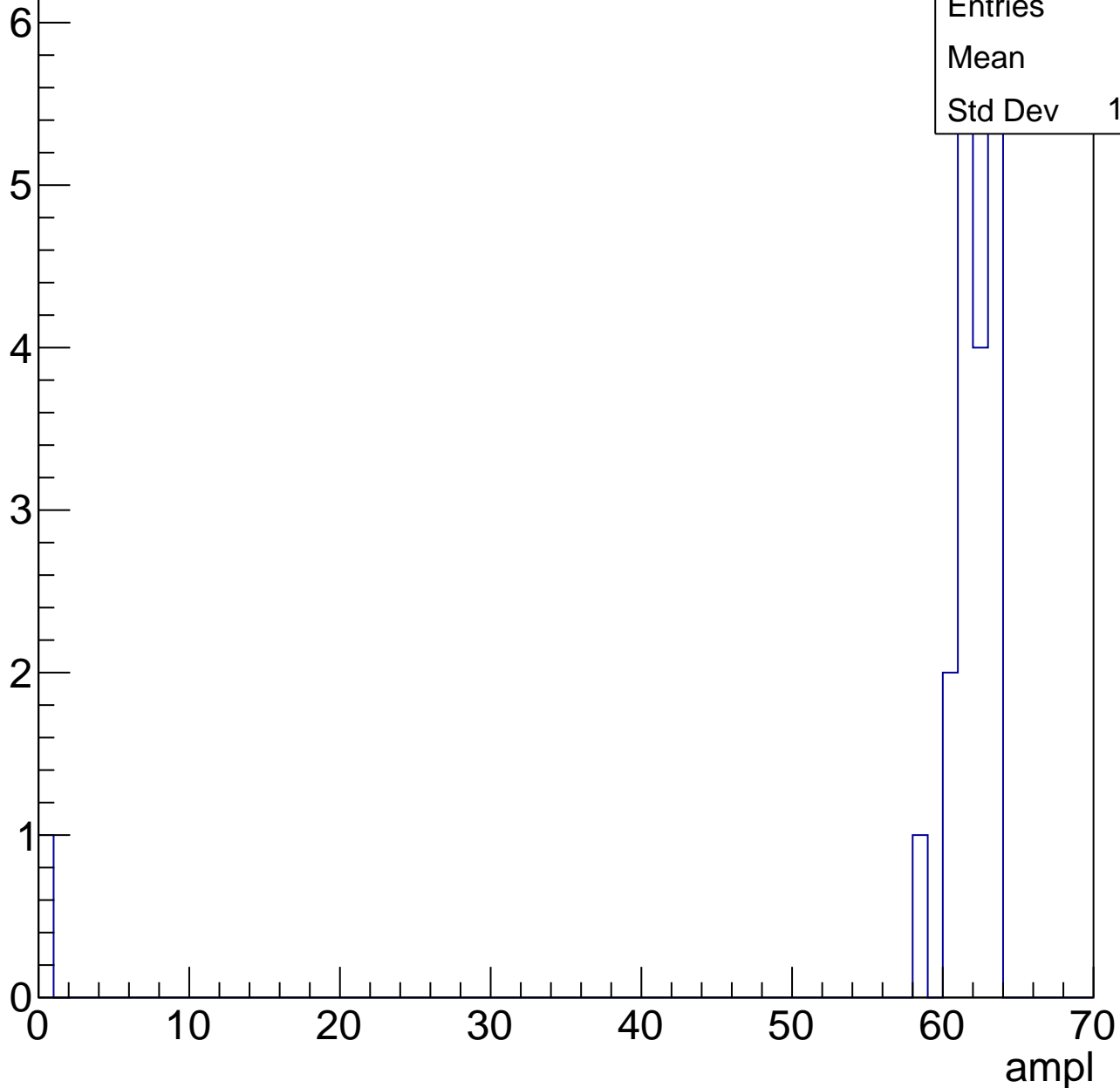
ampl



B0L101S, U24-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch97, adc0

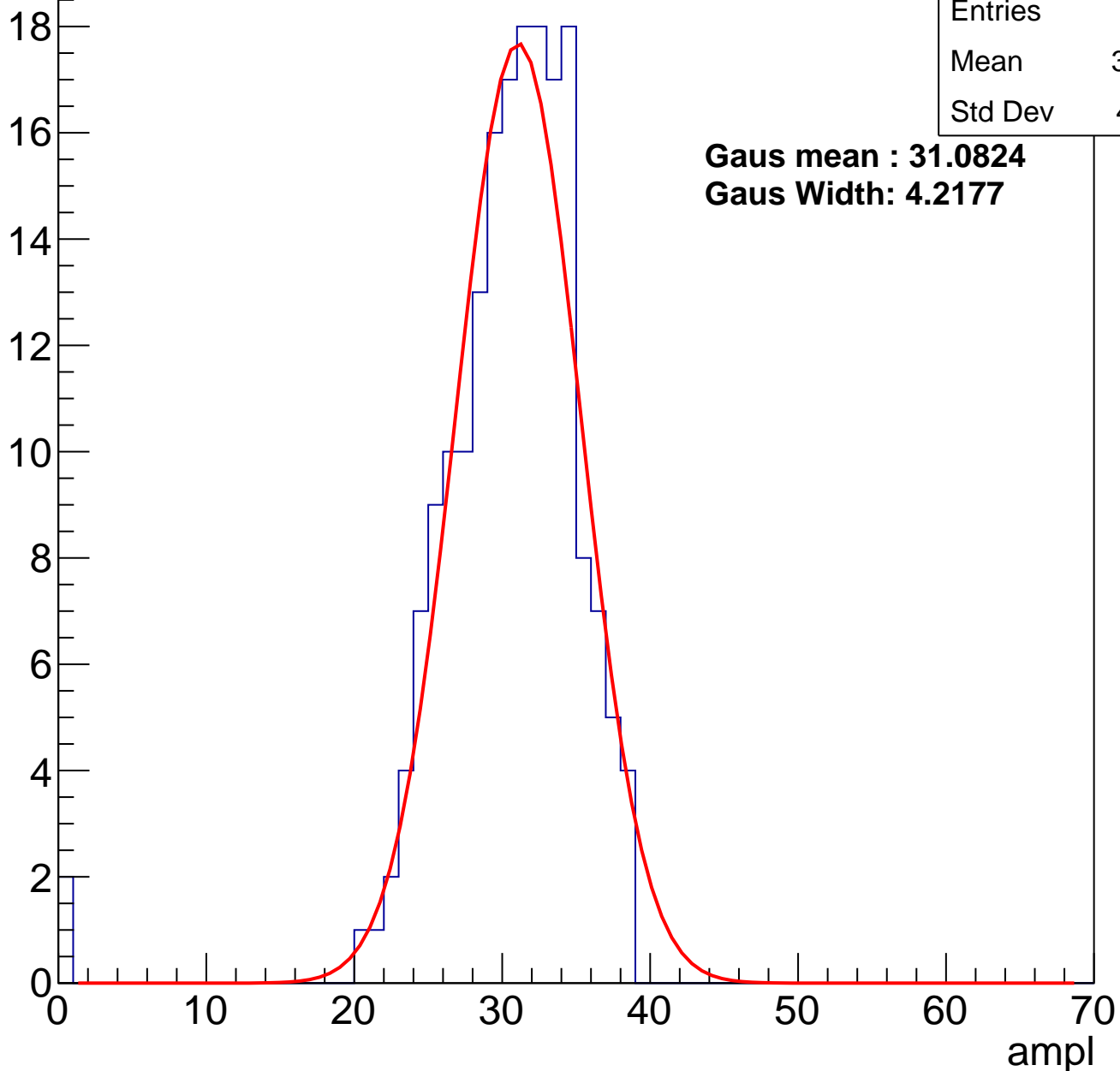
calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	30.02
Std Dev	4.951

Gaus mean : 31.0824

Gaus Width: 4.2177

Entry



B0L101S, U24-ch97, adc1

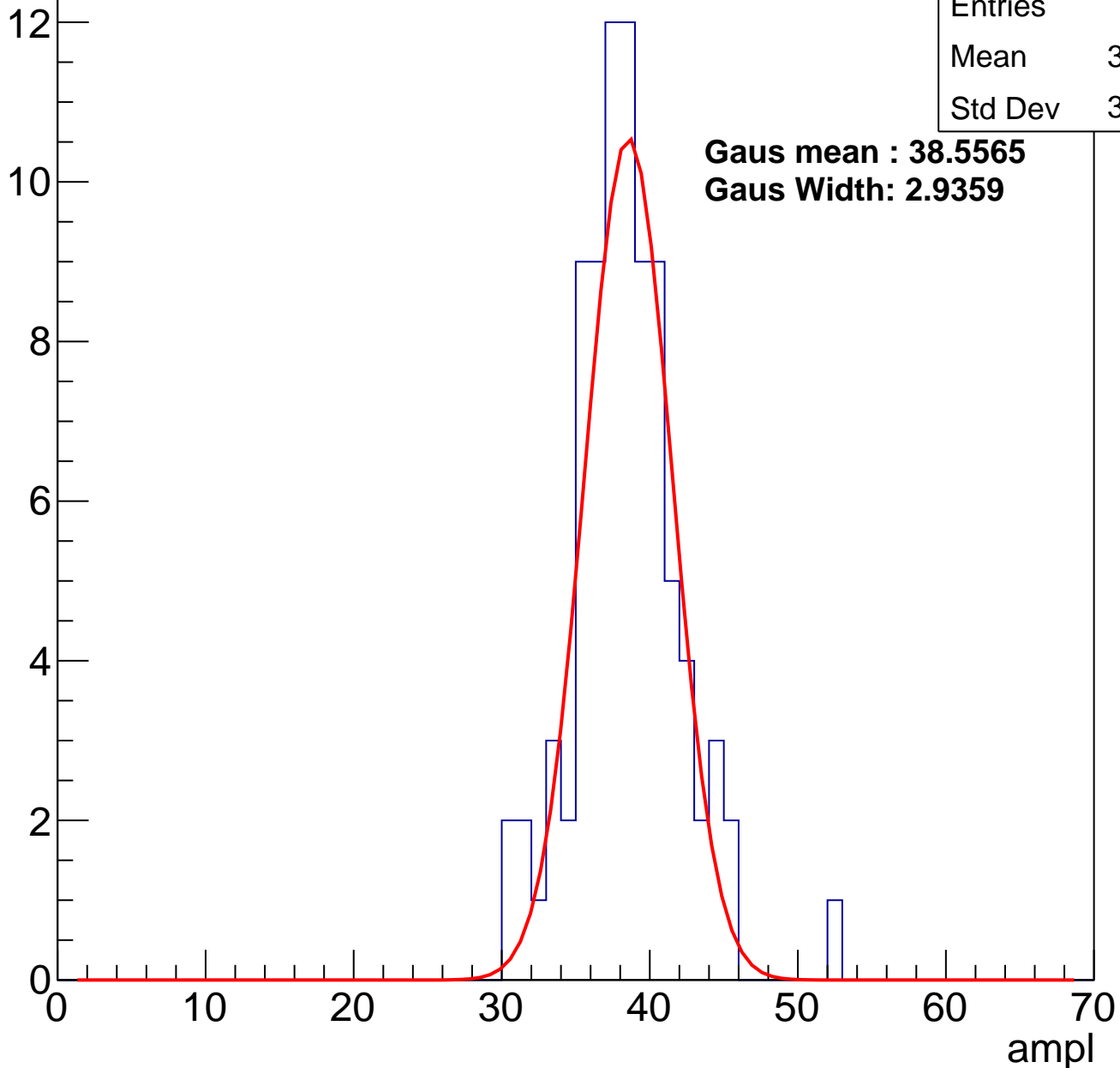
calib_packv5_042523_0143.root, FC#1, port C1

Entries	87
Mean	37.98
Std Dev	3.562

Gaus mean : 38.5565

Gaus Width: 2.9359

Entry



B0L101S, U24-ch97, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	43.47
Std Dev	4.044

Gaus mean : 43.9623

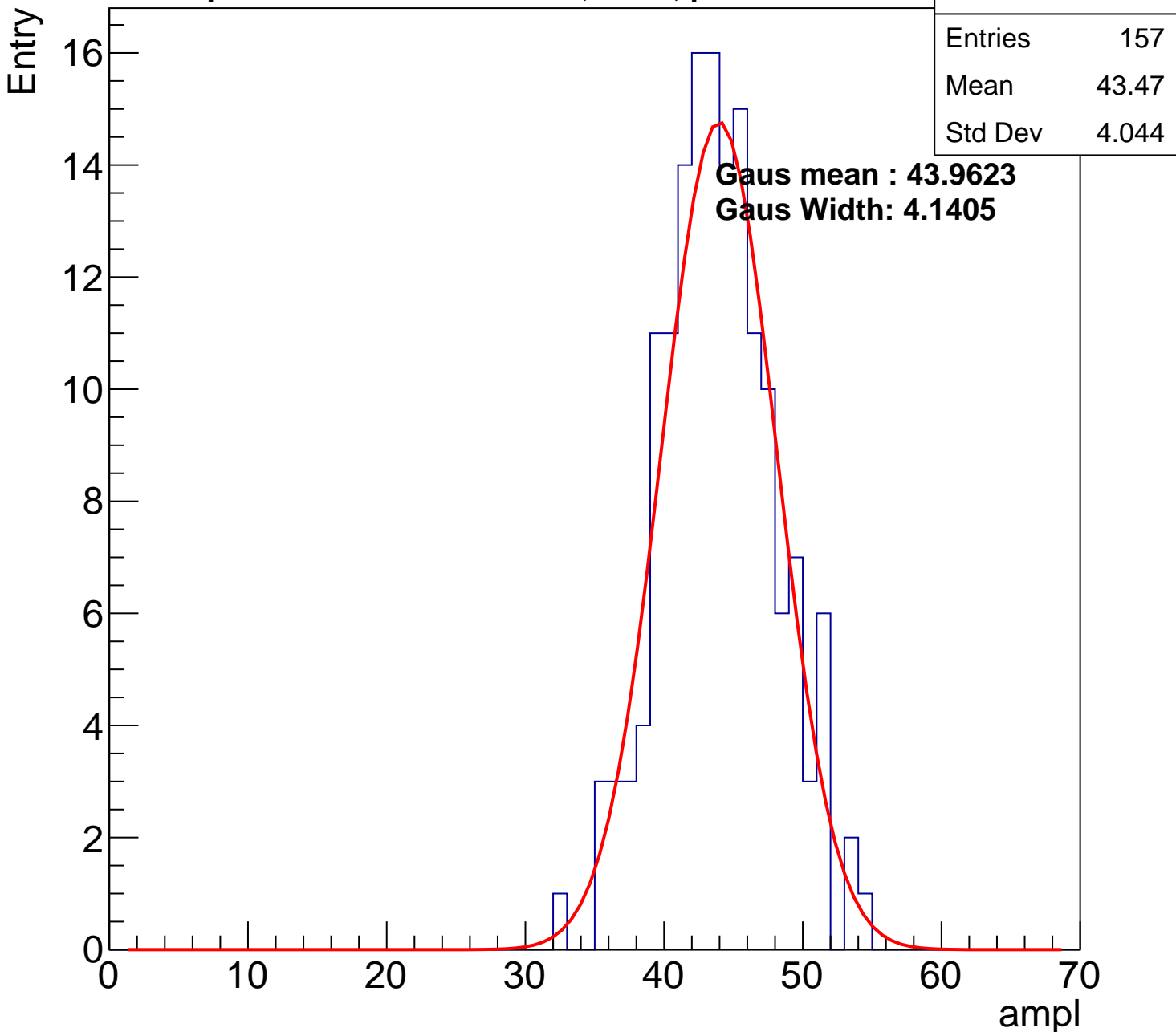
Gaus Width: 4.1405

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

50.51

Std Dev

3.895

ampl

0

10

20

30

40

50

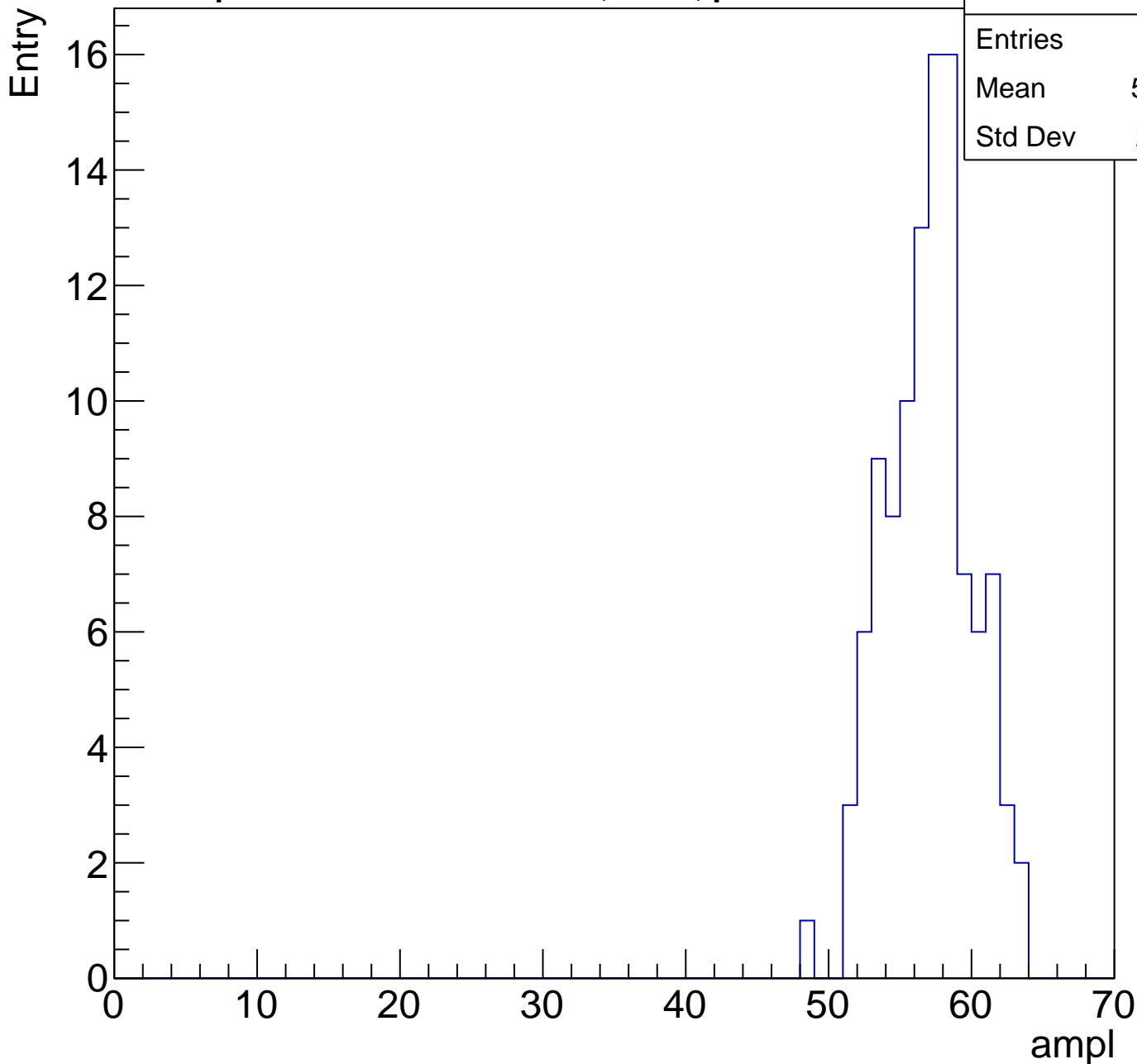
60

70

B0L101S, U24-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	56.56
Std Dev	2.971



B0L101S, U24-ch97, adc5

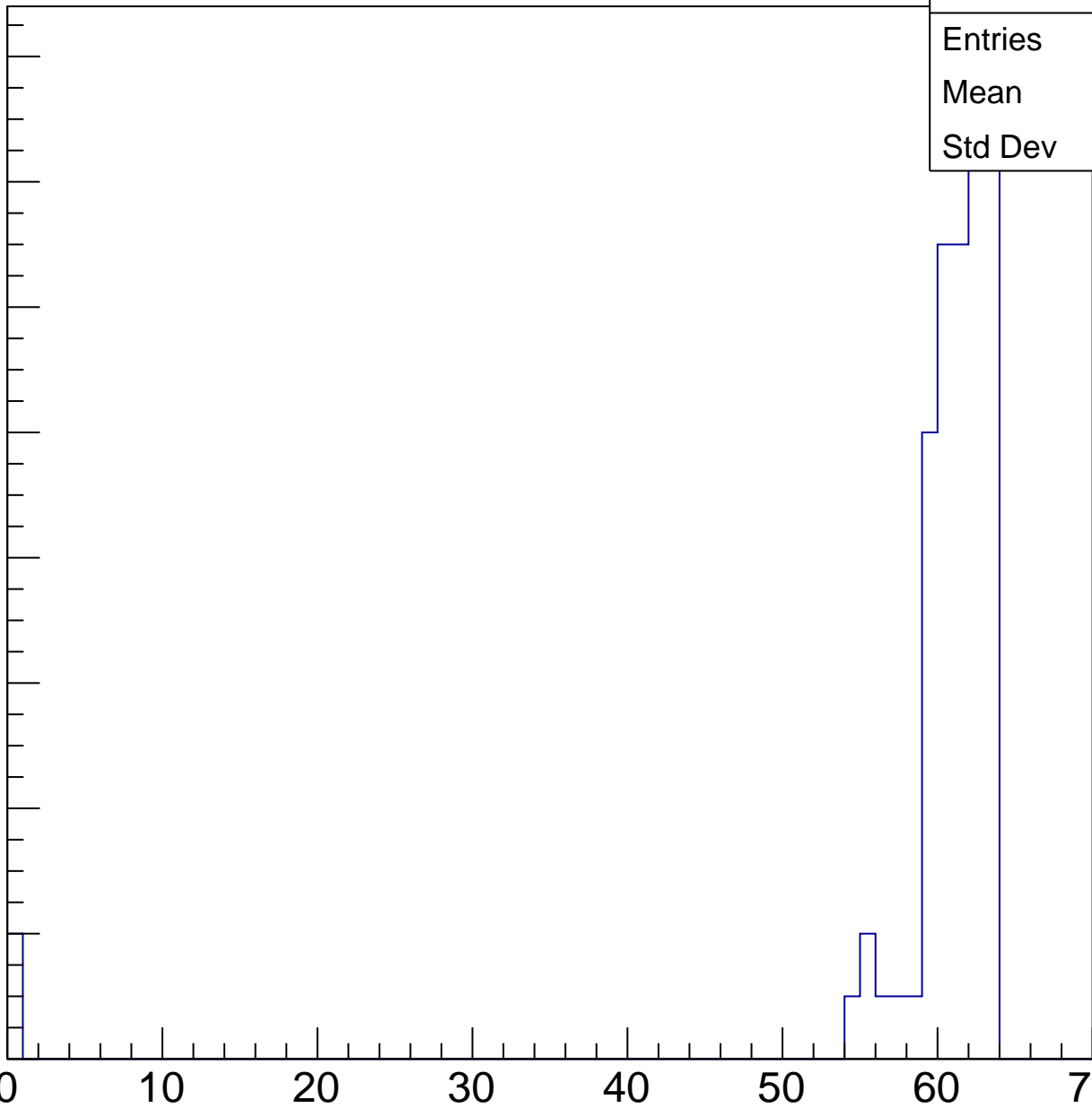
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	59.15
Std Dev	9.992

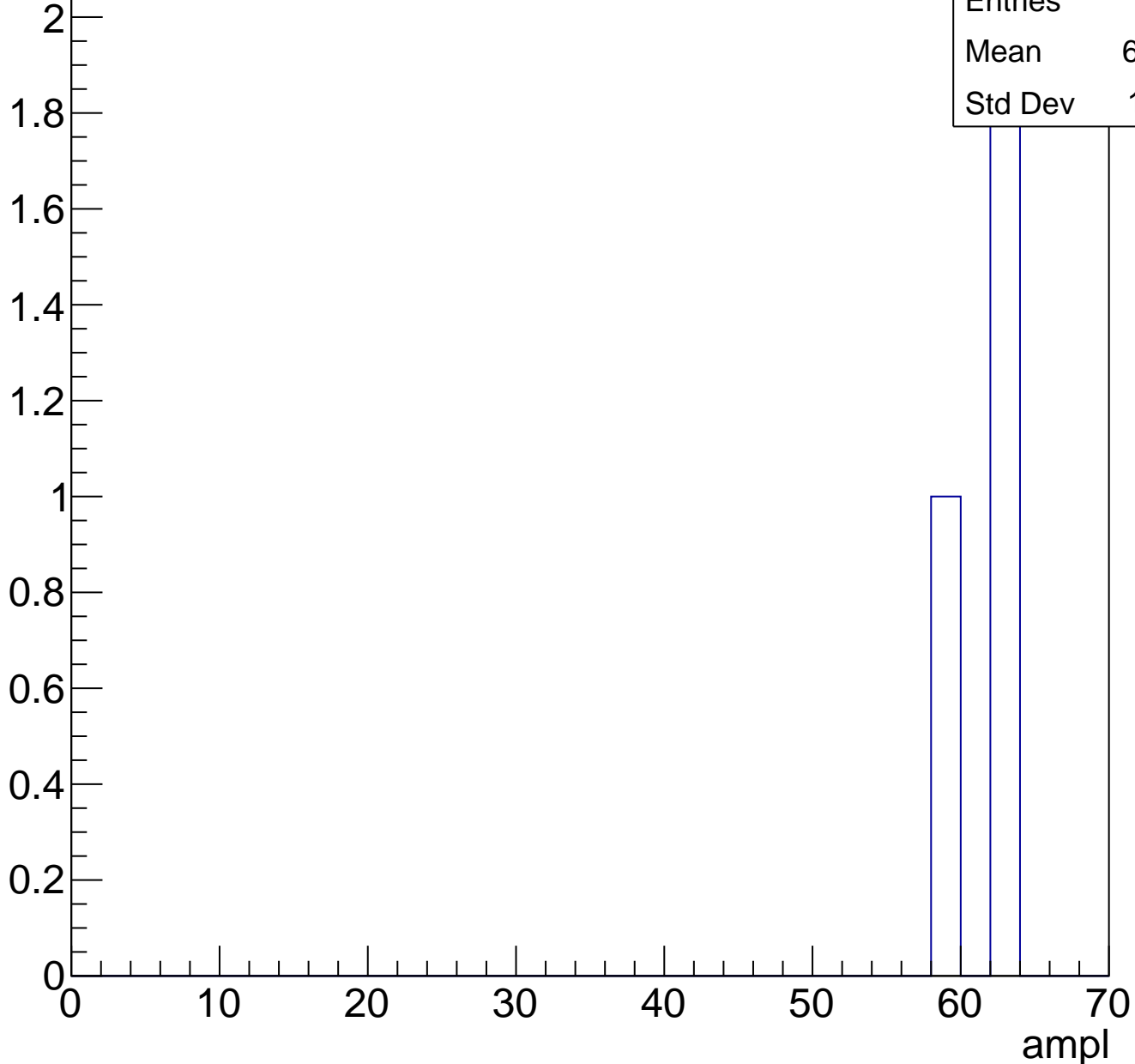
ampl



B0L101S, U24-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

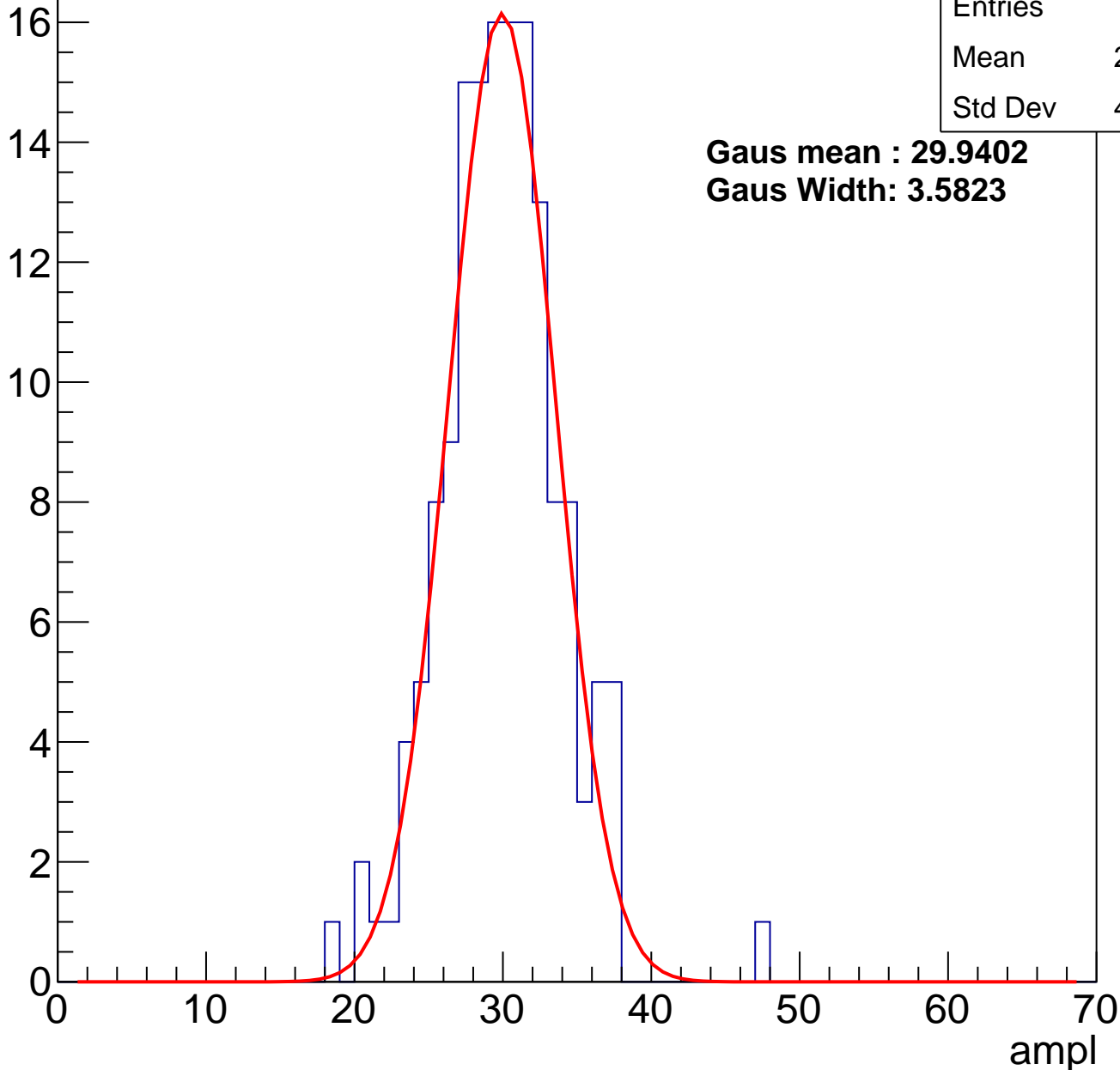


Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch98, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch98, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	37.11
Std Dev	3.895

Gaus mean : 37.3491

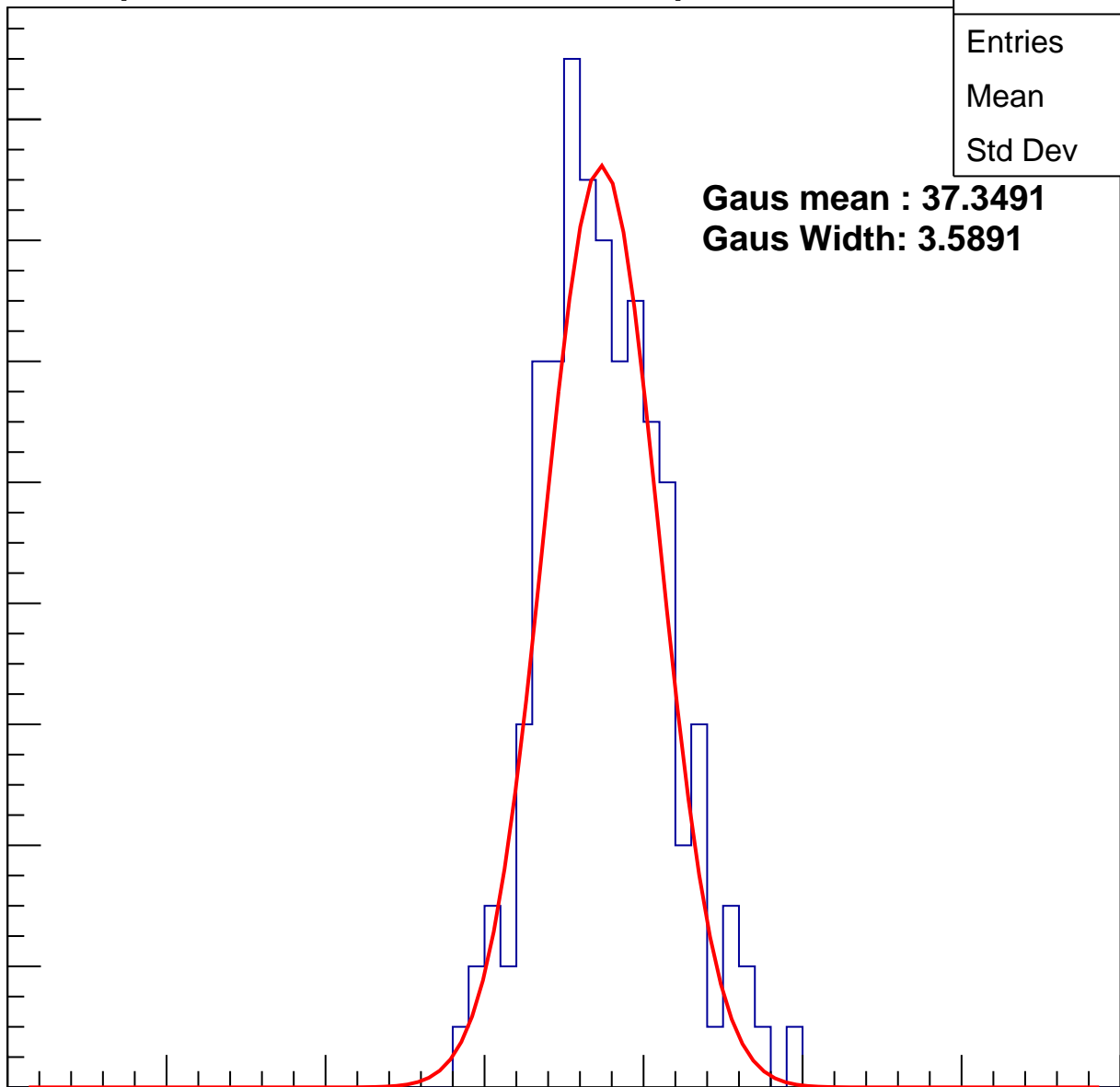
Gaus Width: 3.5891

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch98, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries	121
Mean	43.83
Std Dev	3.722

Gaus mean : 44.0469

Gaus Width: 3.7594

0

10

20

30

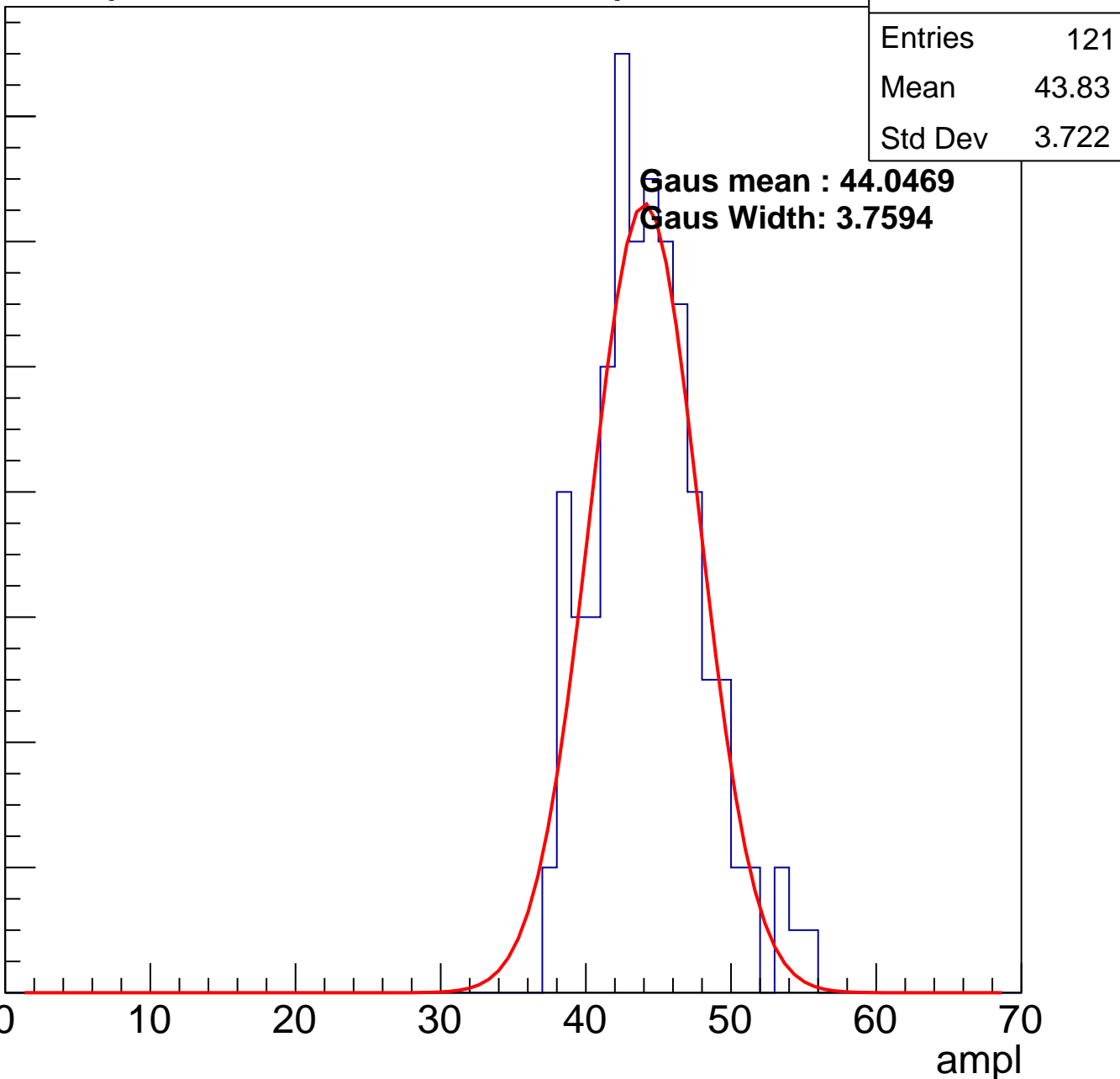
40

50

60

70

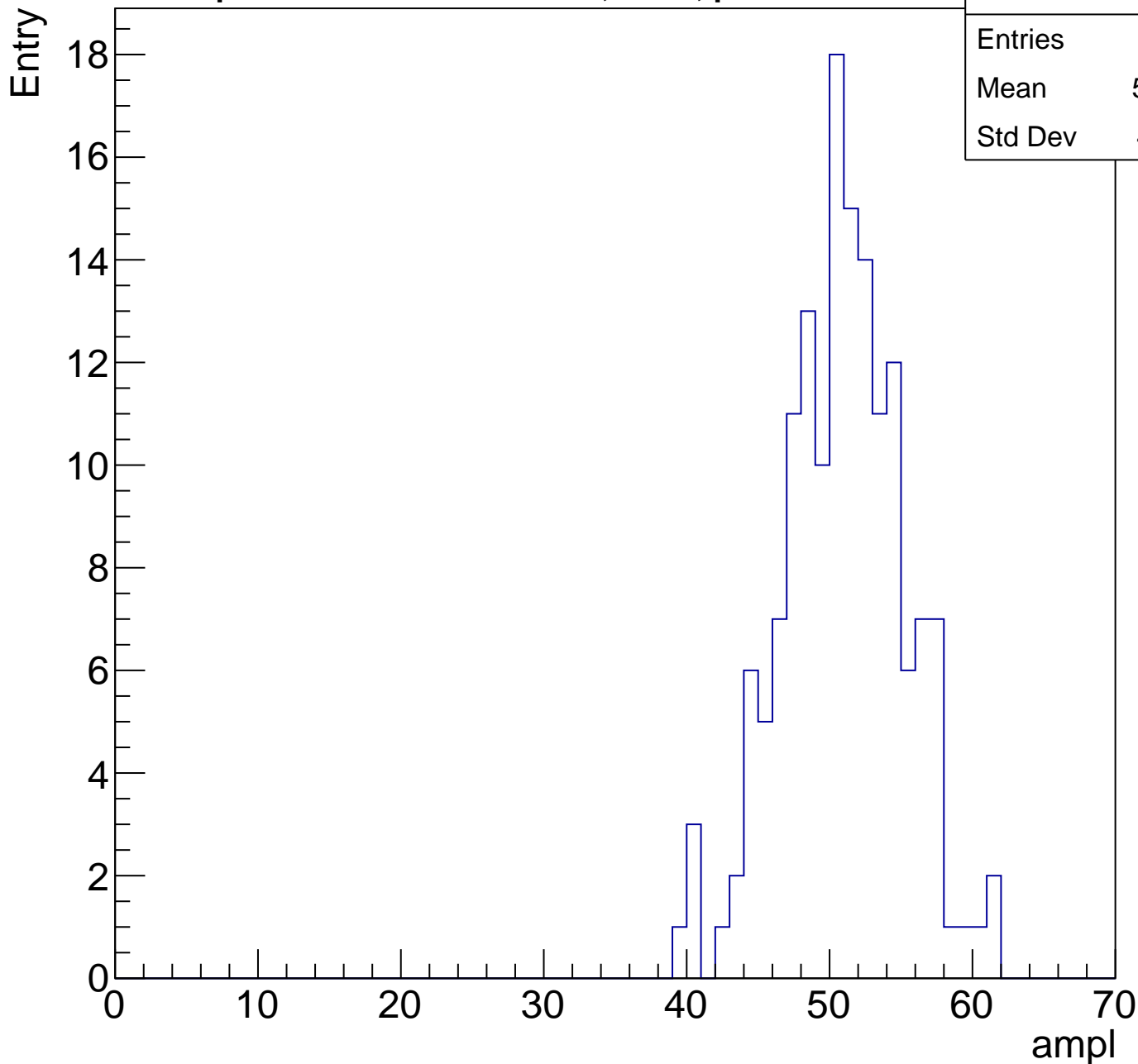
ampl



B0L101S, U24-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	50.47
Std Dev	4.231



B0L101S, U24-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

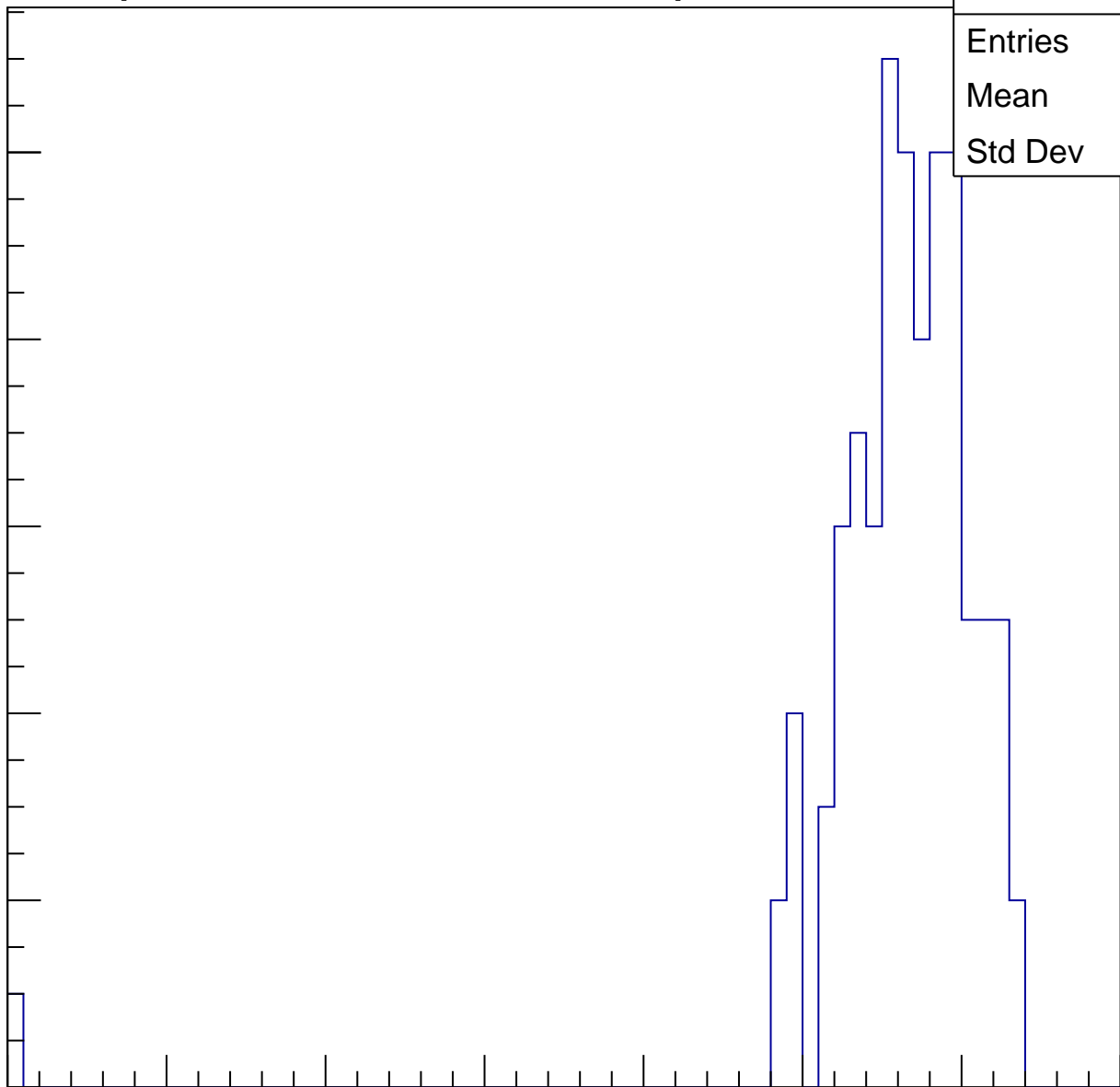
50

60

70

ampl

Entries	95
Mean	55.62
Std Dev	6.756



B0L101S, U24-ch98, adc5

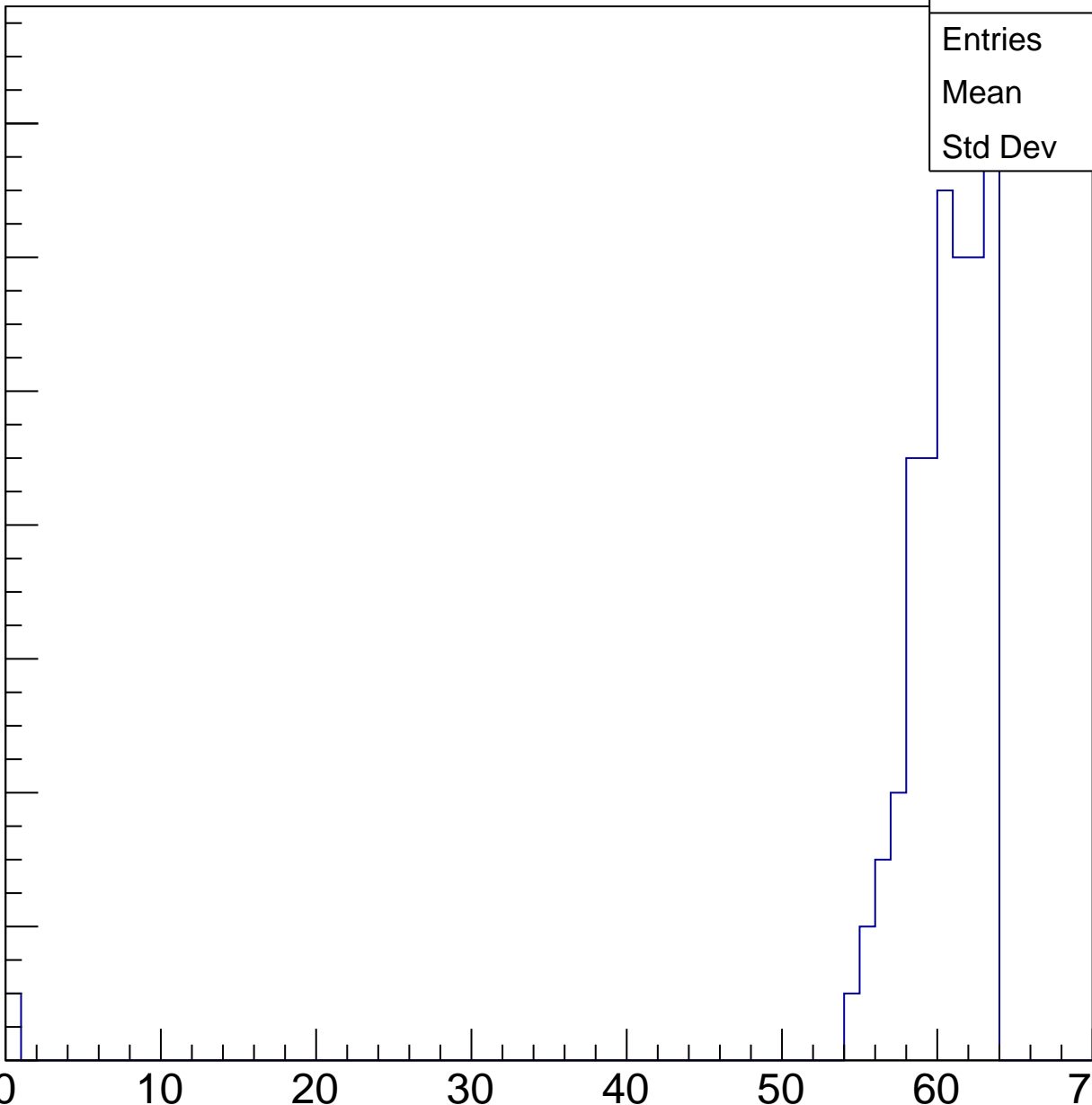
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	59.43
Std Dev	7.013

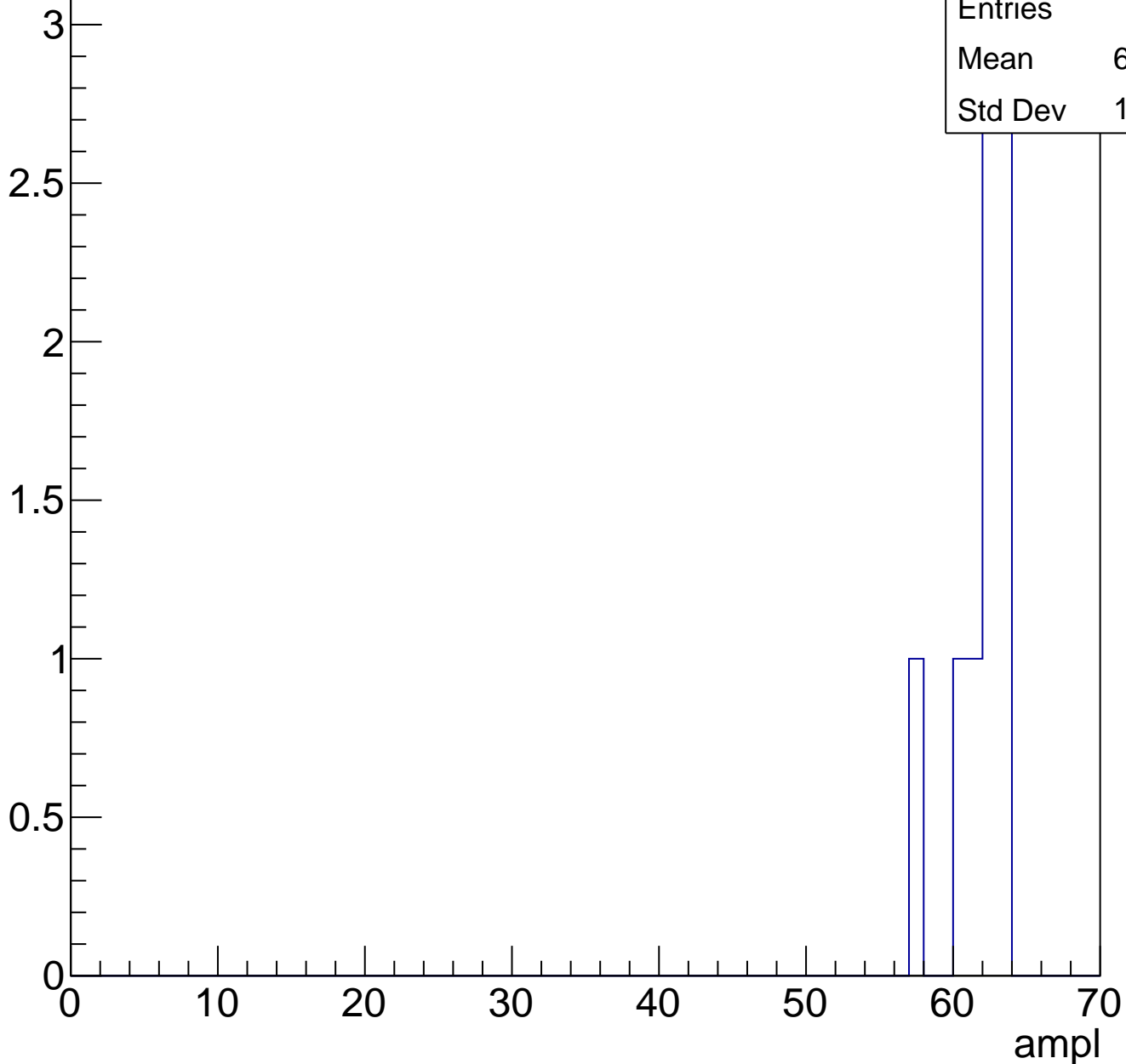
ampl



B0L101S, U24-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch99, adc0

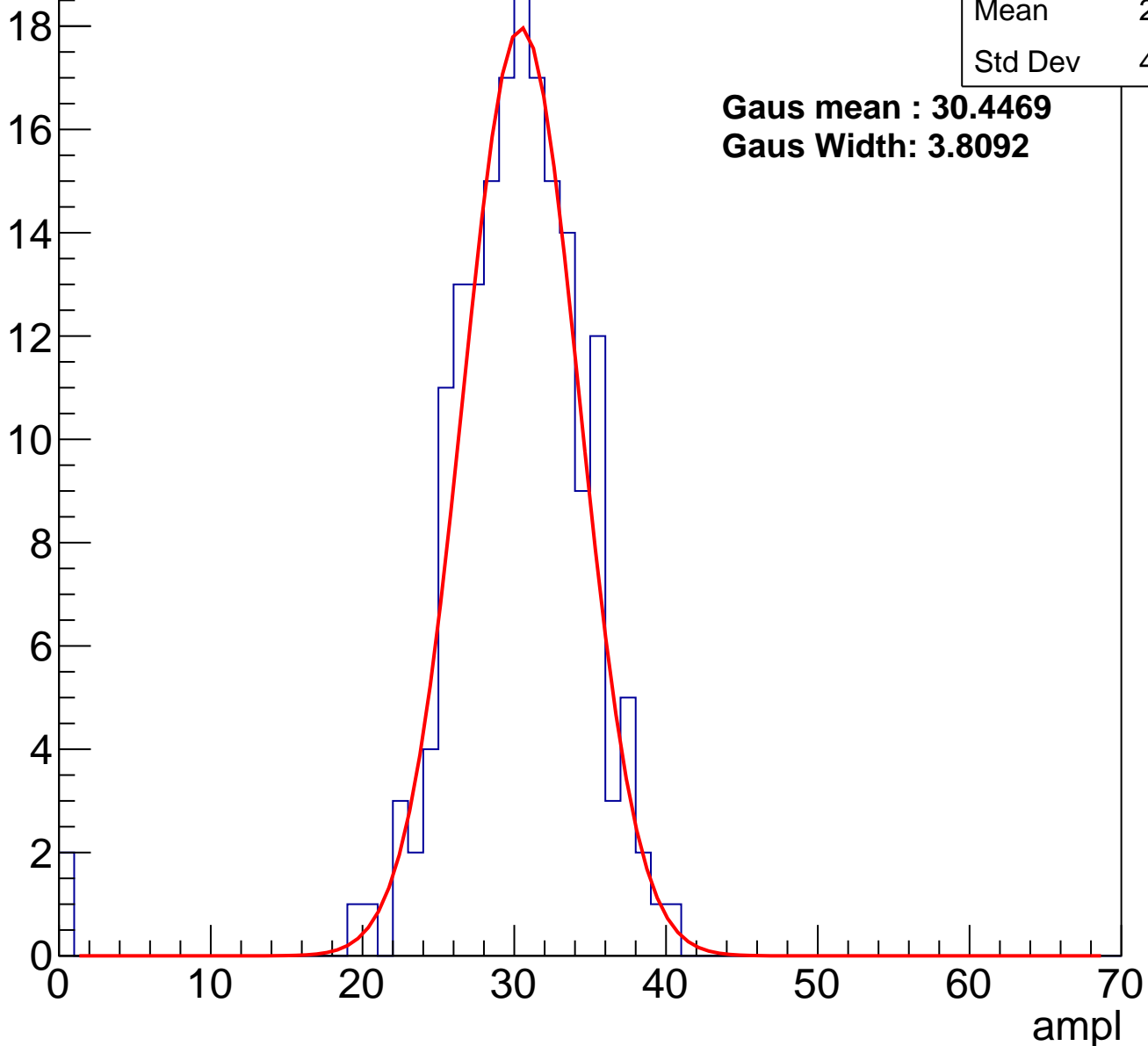
calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	29.65
Std Dev	4.955

Gaus mean : 30.4469

Gaus Width: 3.8092

Entry



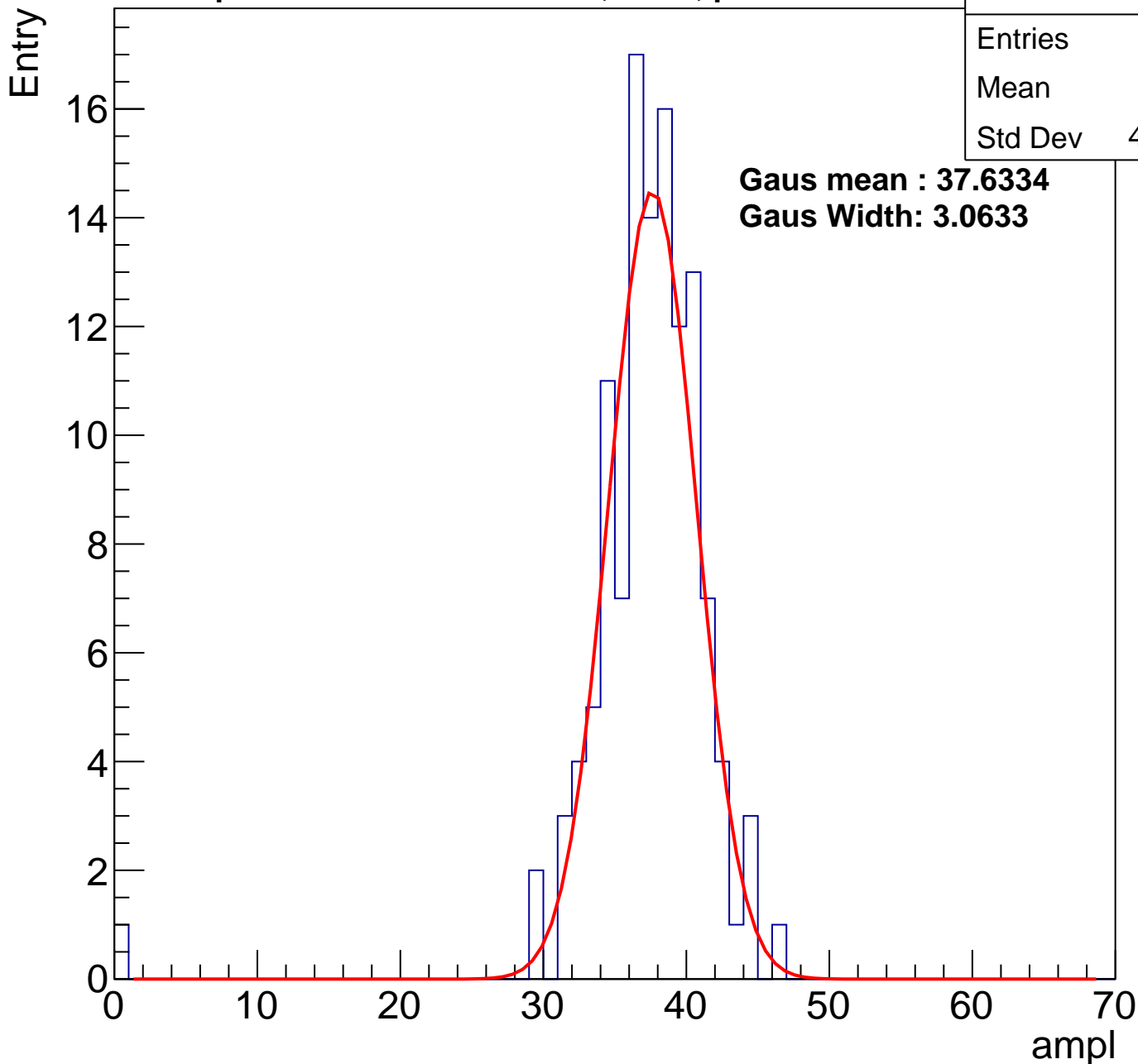
B0L101S, U24-ch99, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	36.9
Std Dev	4.622

Gaus mean : 37.6334

Gaus Width: 3.0633



B0L101S, U24-ch99, adc2

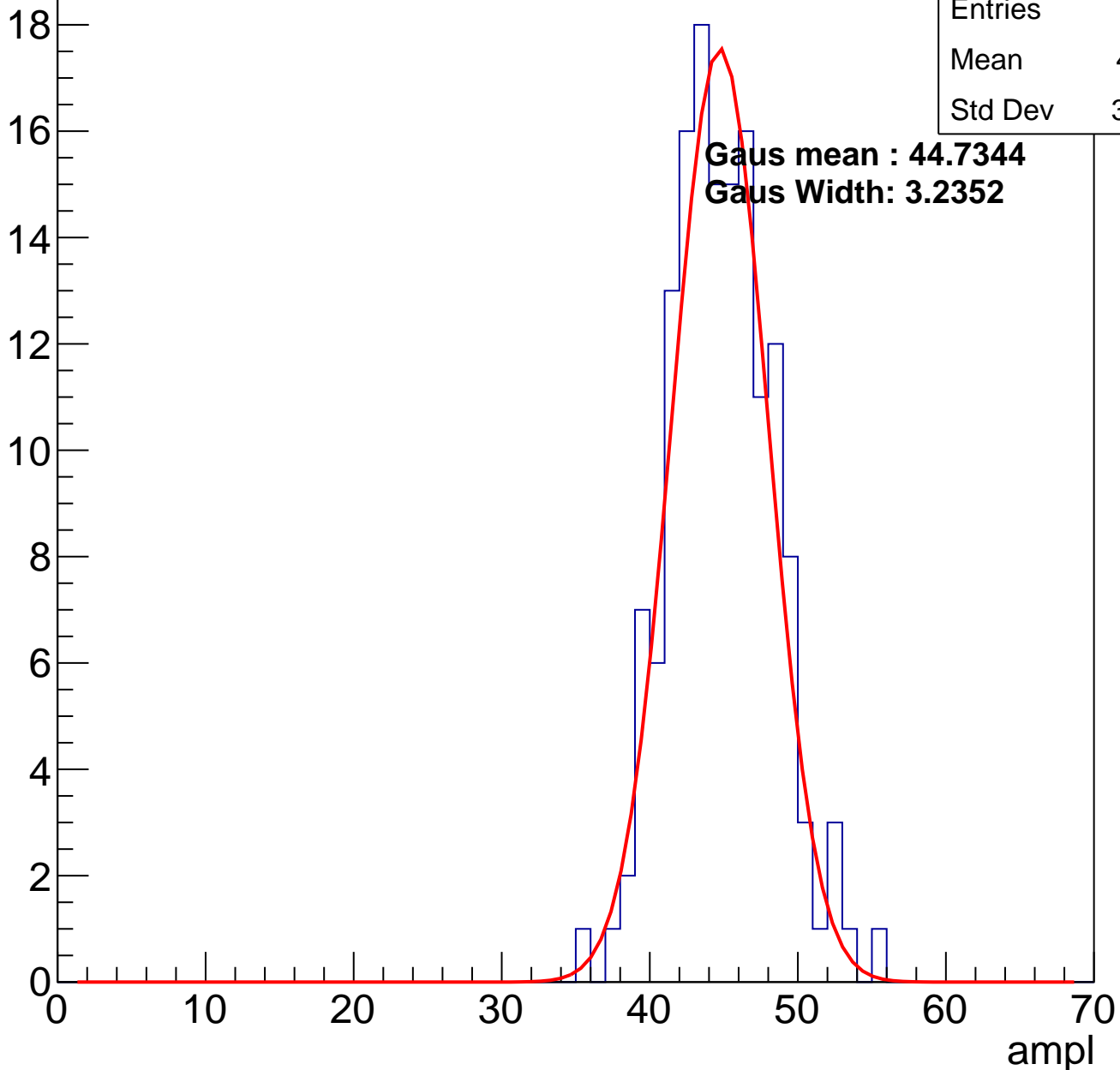
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	44.41
Std Dev	3.433

Gaus mean : 44.7344

Gaus Width: 3.2352

Entry



B0L101S, U24-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

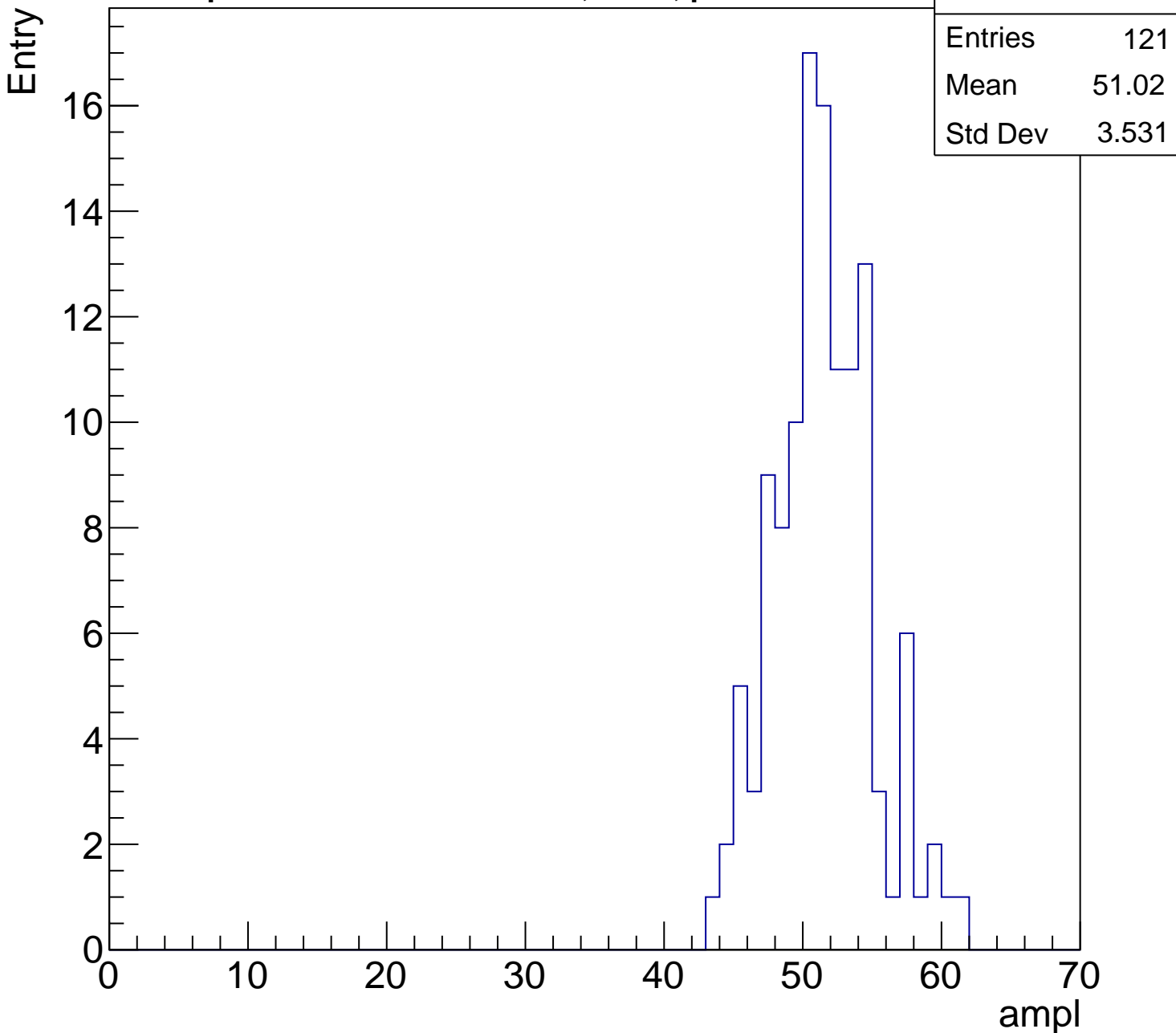
Entries	121
Mean	51.02
Std Dev	3.531

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

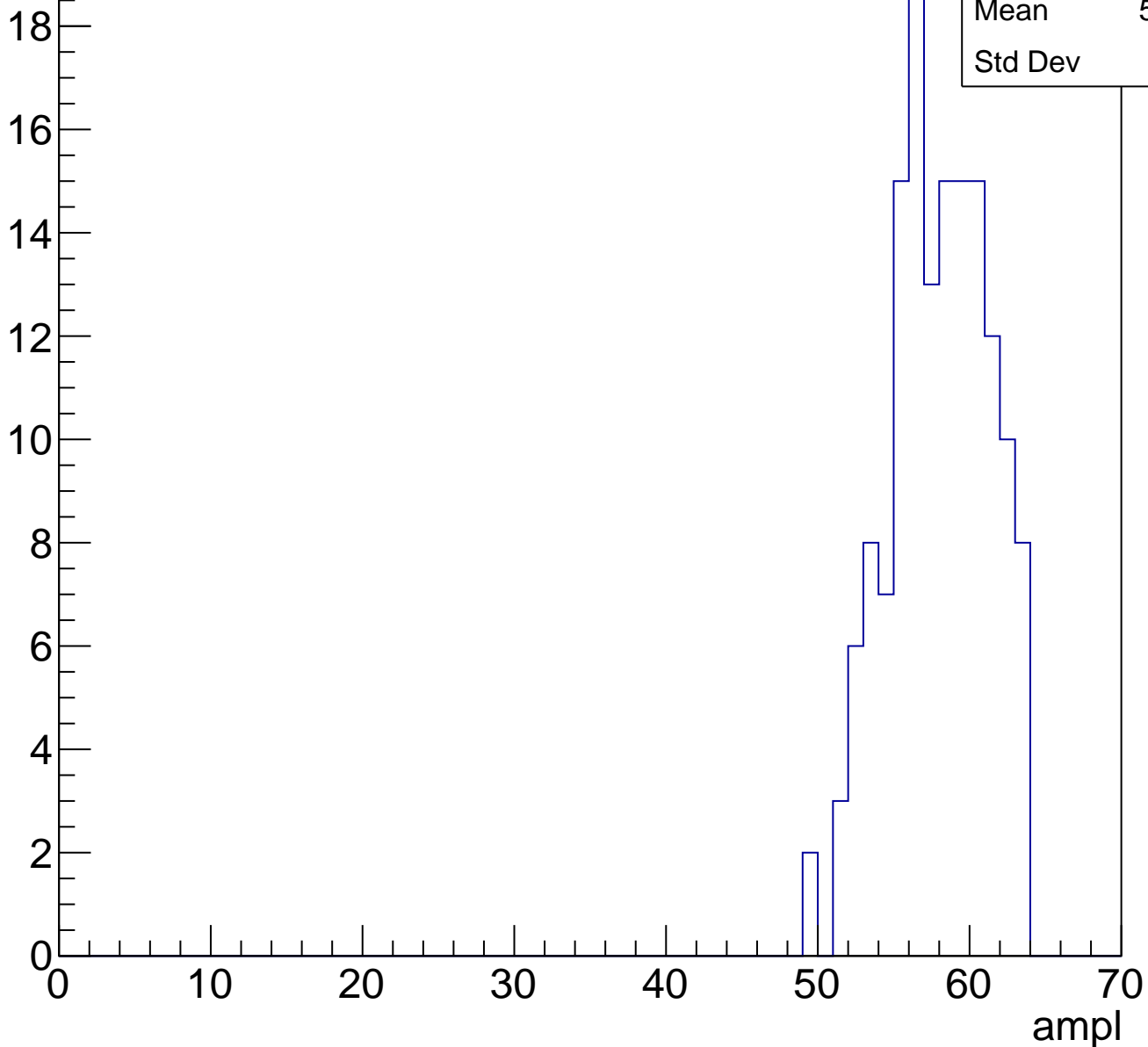
ampl



B0L101S, U24-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch99, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

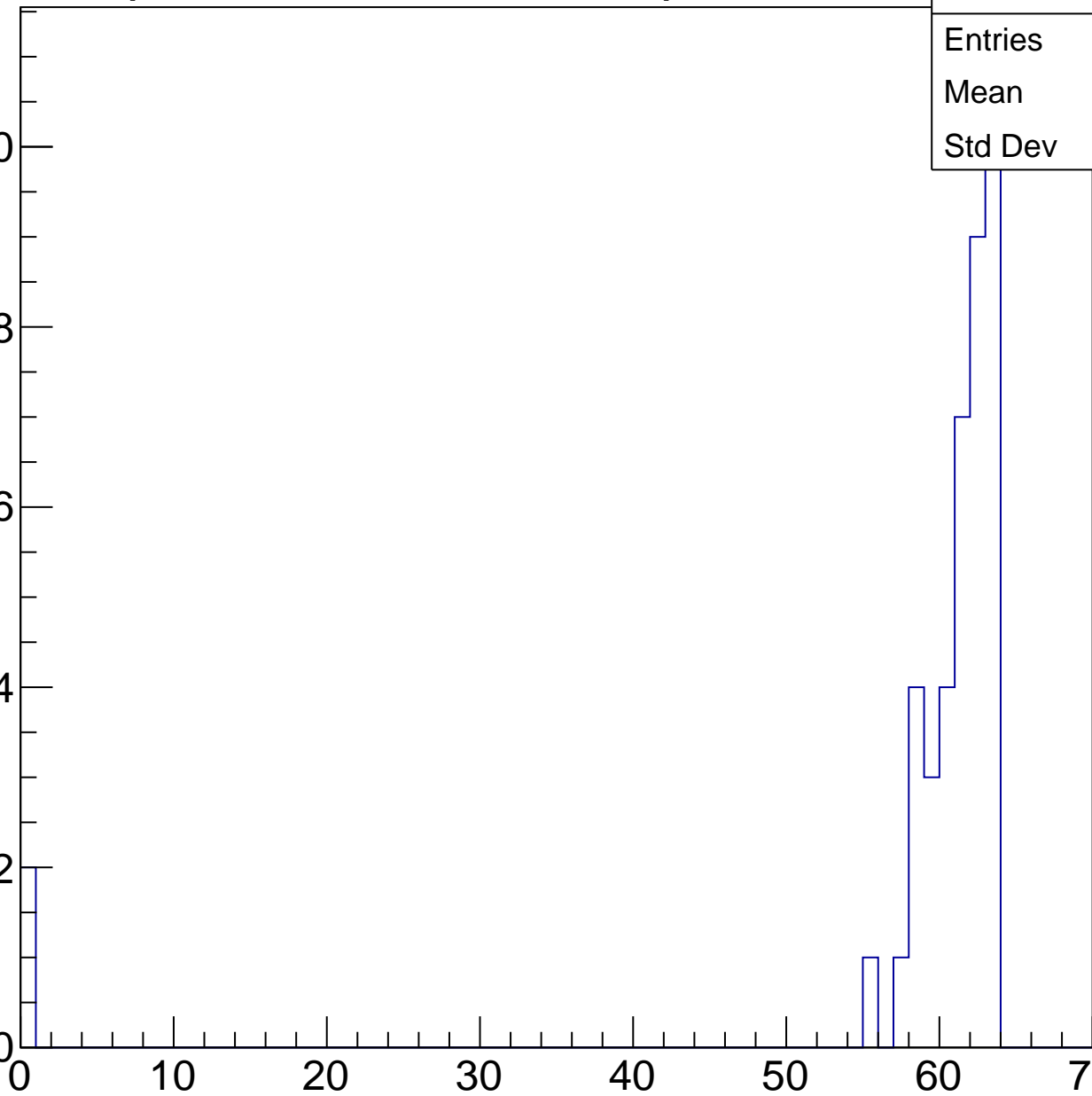
4

2

0

Entries	42
Mean	58.07
Std Dev	13.13

ampl



B0L101S, U24-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch100, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	27.79
Std Dev	4.95

Gaus mean : 28.1704

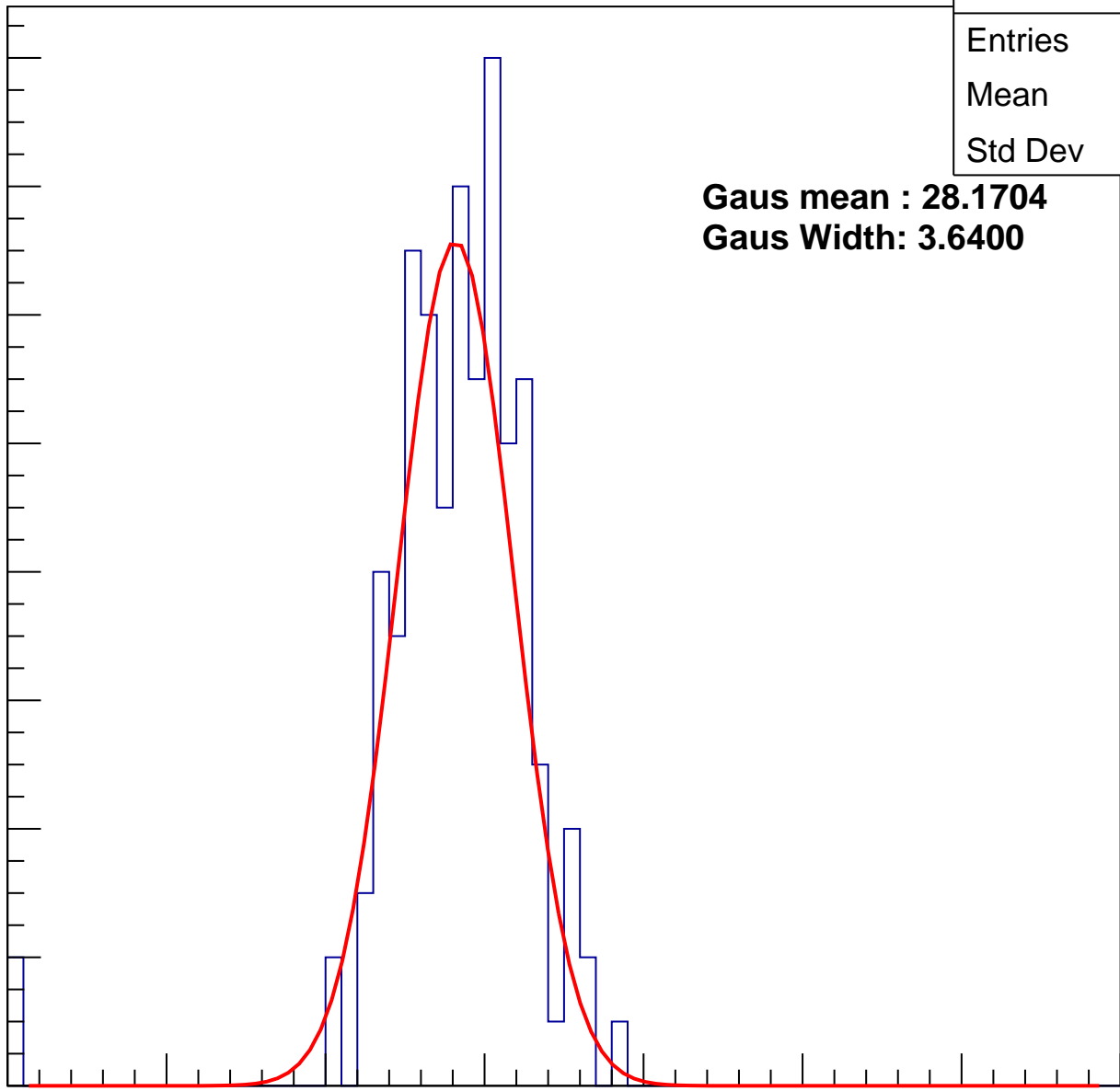
Gaus Width: 3.6400

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch100, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

127

Mean

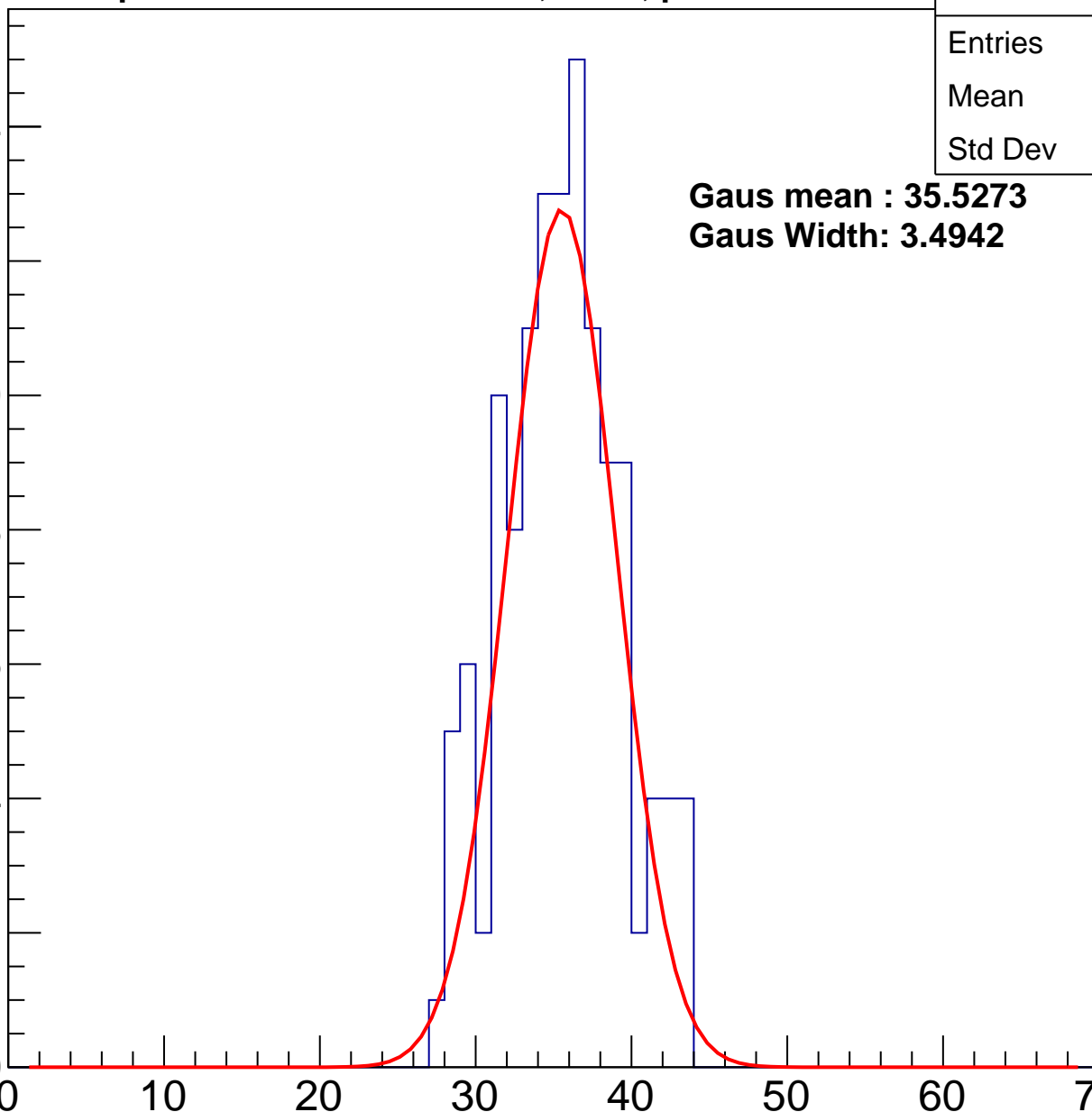
35.05

Std Dev

3.758

Gaus mean : 35.5273

Gaus Width: 3.4942



B0L101S, U24-ch100, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

147

Mean

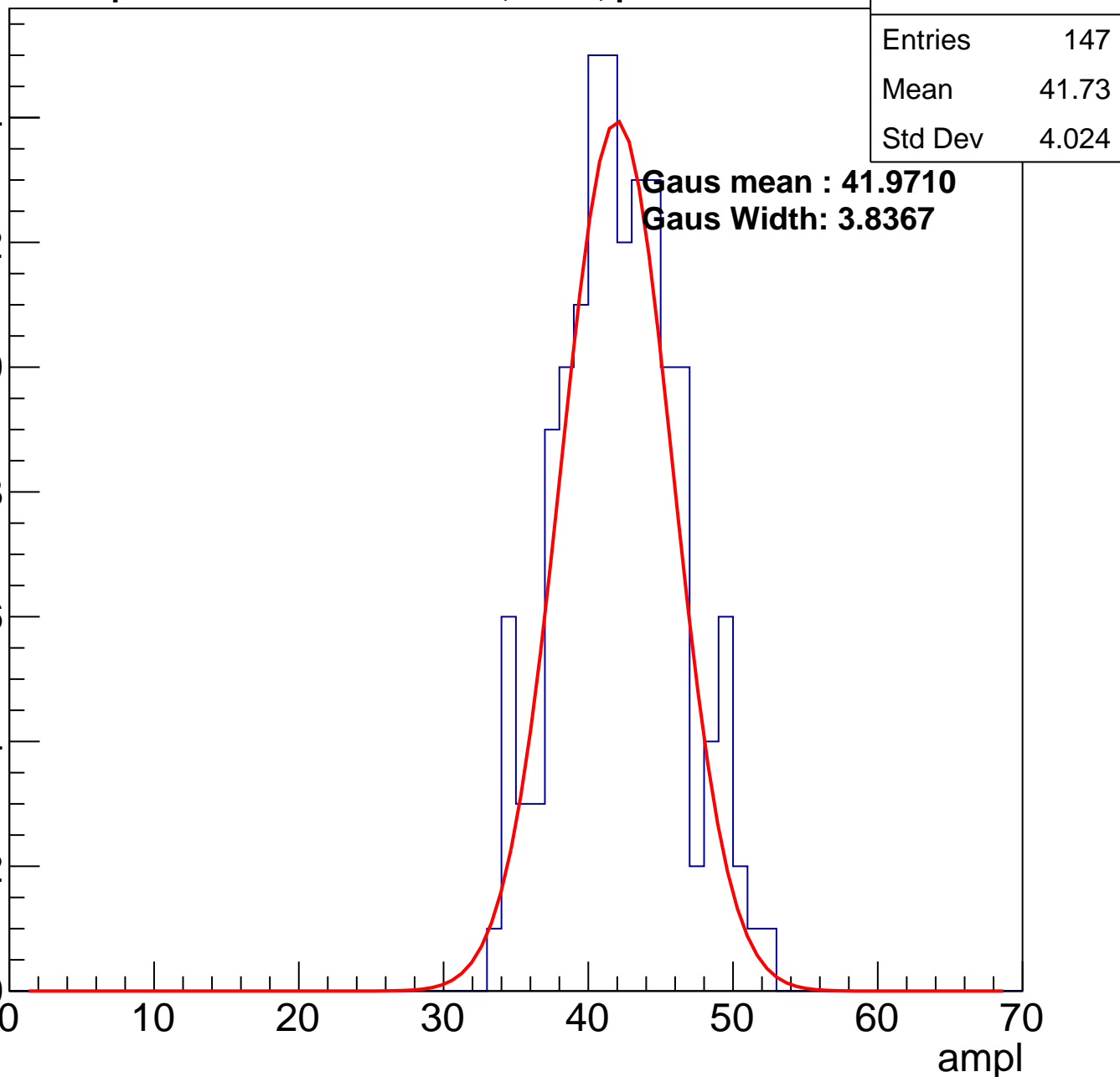
41.73

Std Dev

4.024

Gaus mean : 41.9710

Gaus Width: 3.8367



B0L101S, U24-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

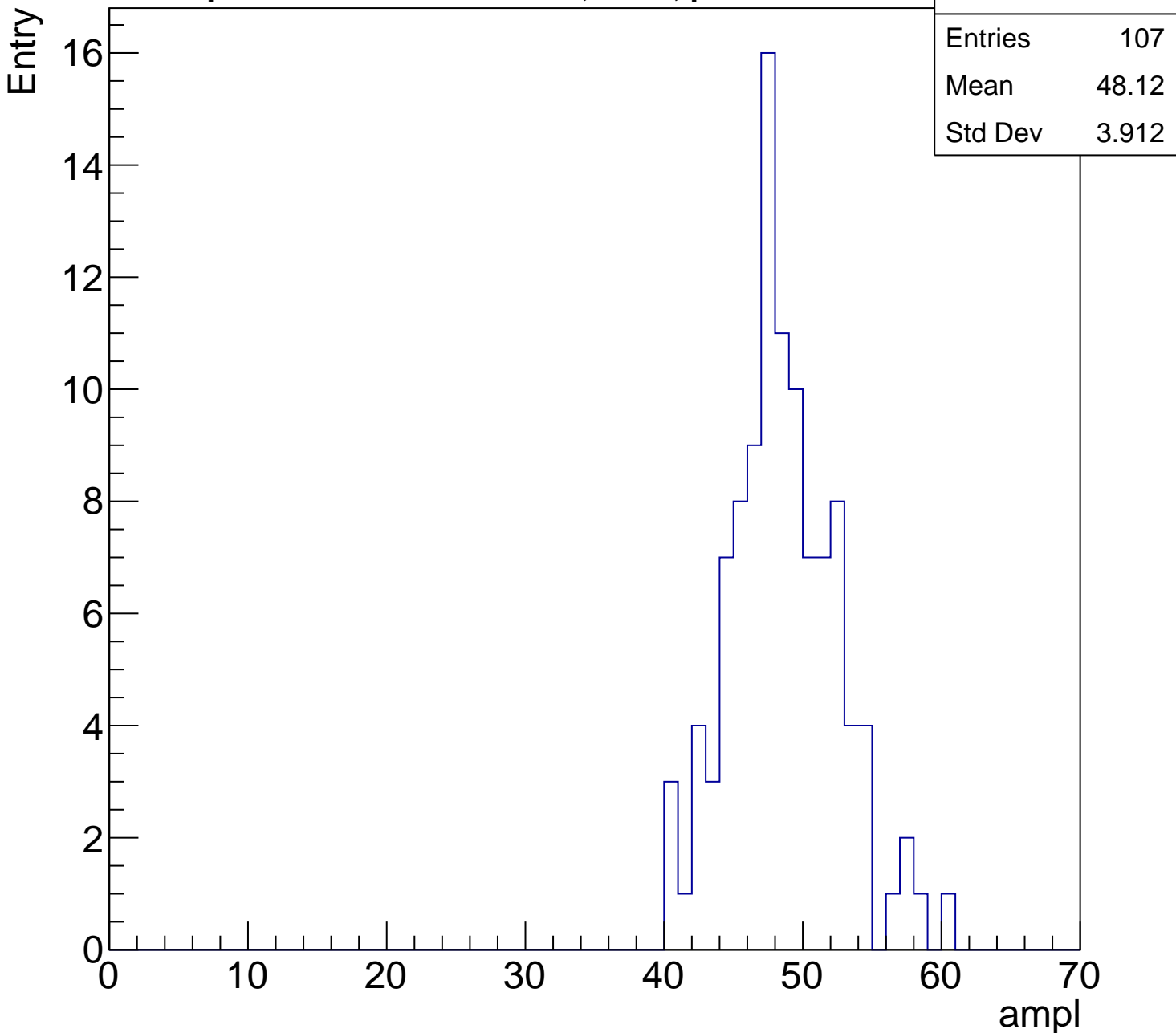
Entries	107
Mean	48.12
Std Dev	3.912

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch100, adc4

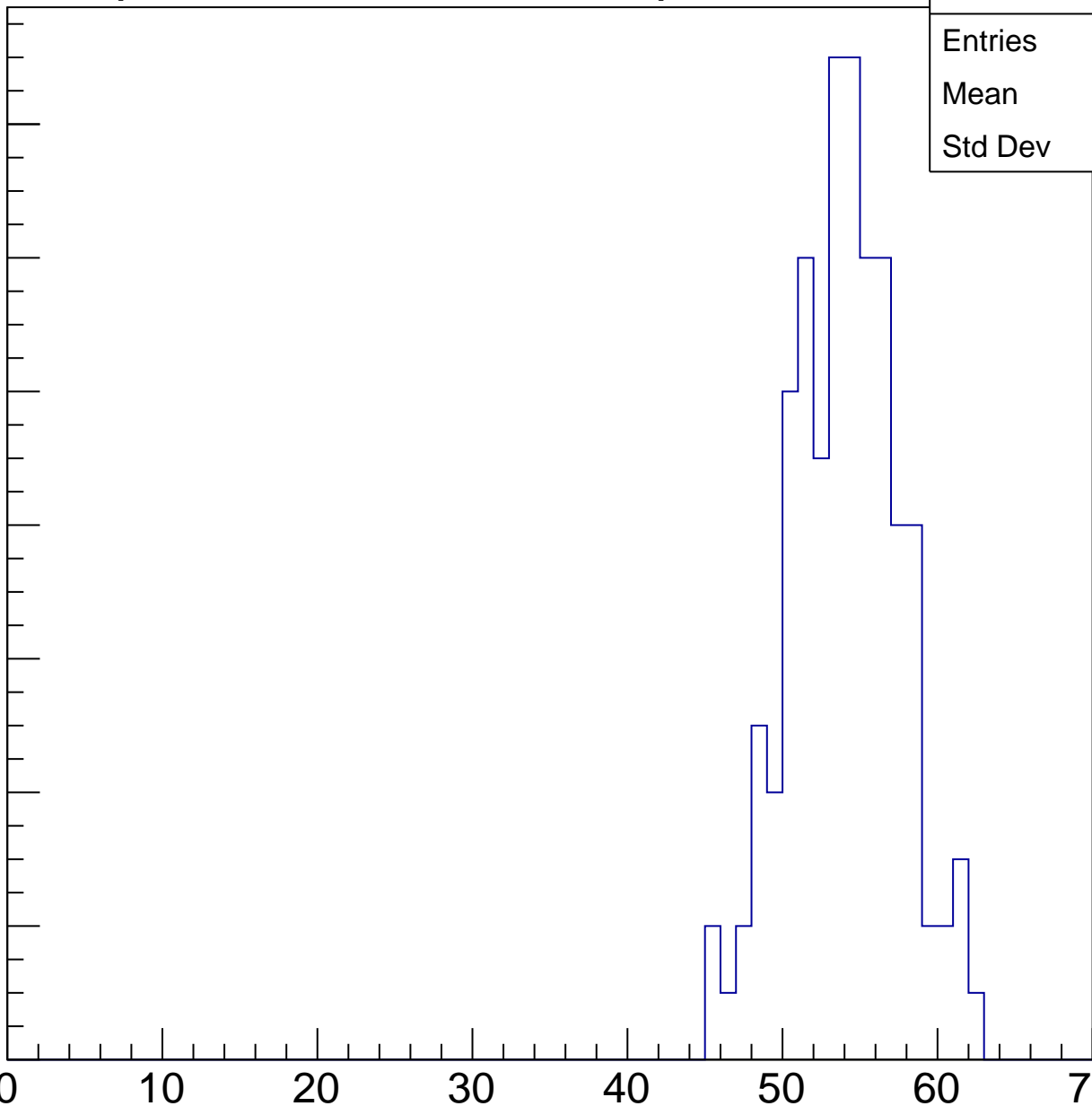
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	123
Mean	53.54
Std Dev	3.481

ampl



B0L101S, U24-ch100, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	58.28
Std Dev	8.108

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

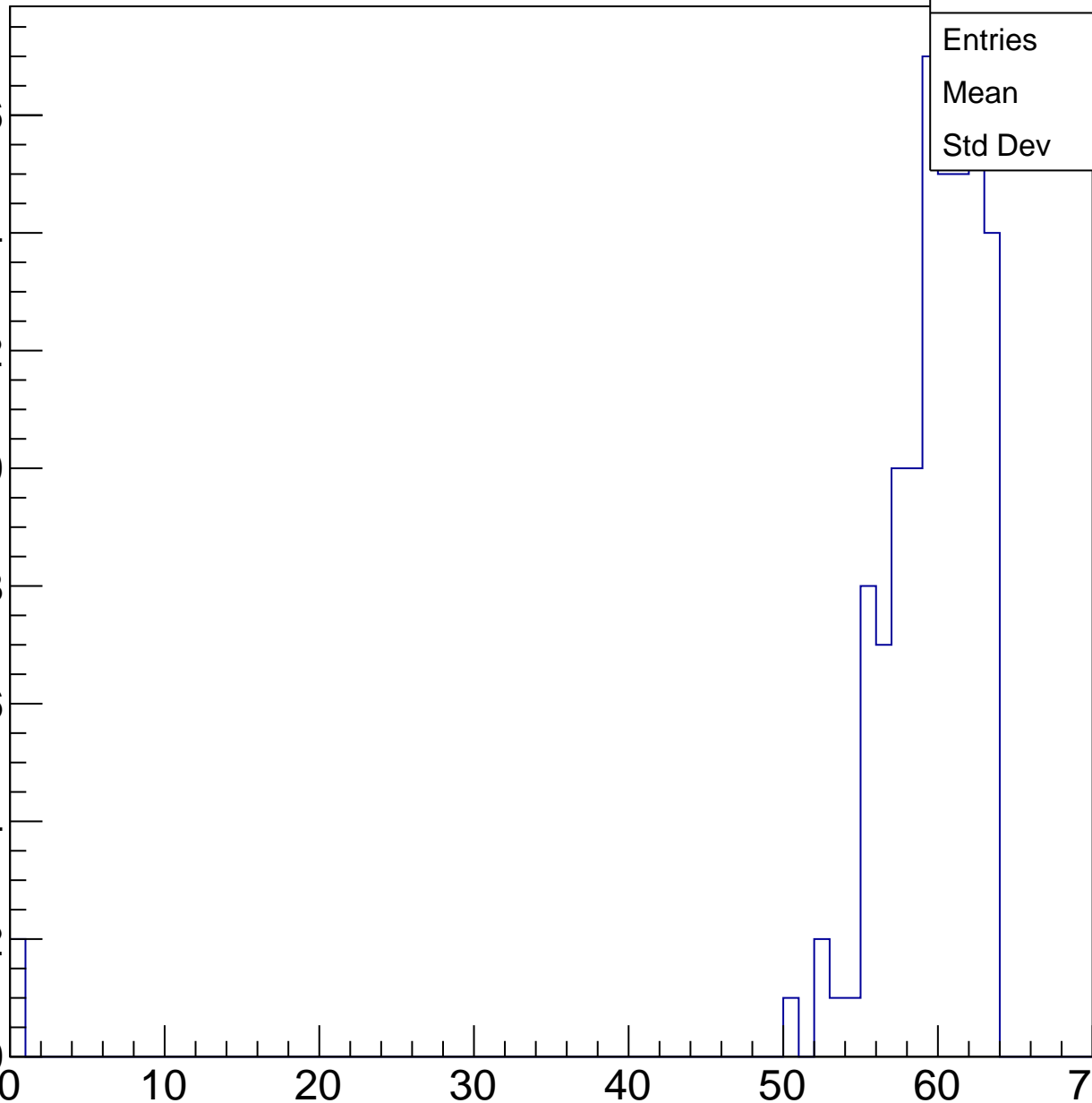
40

50

60

70

ampl

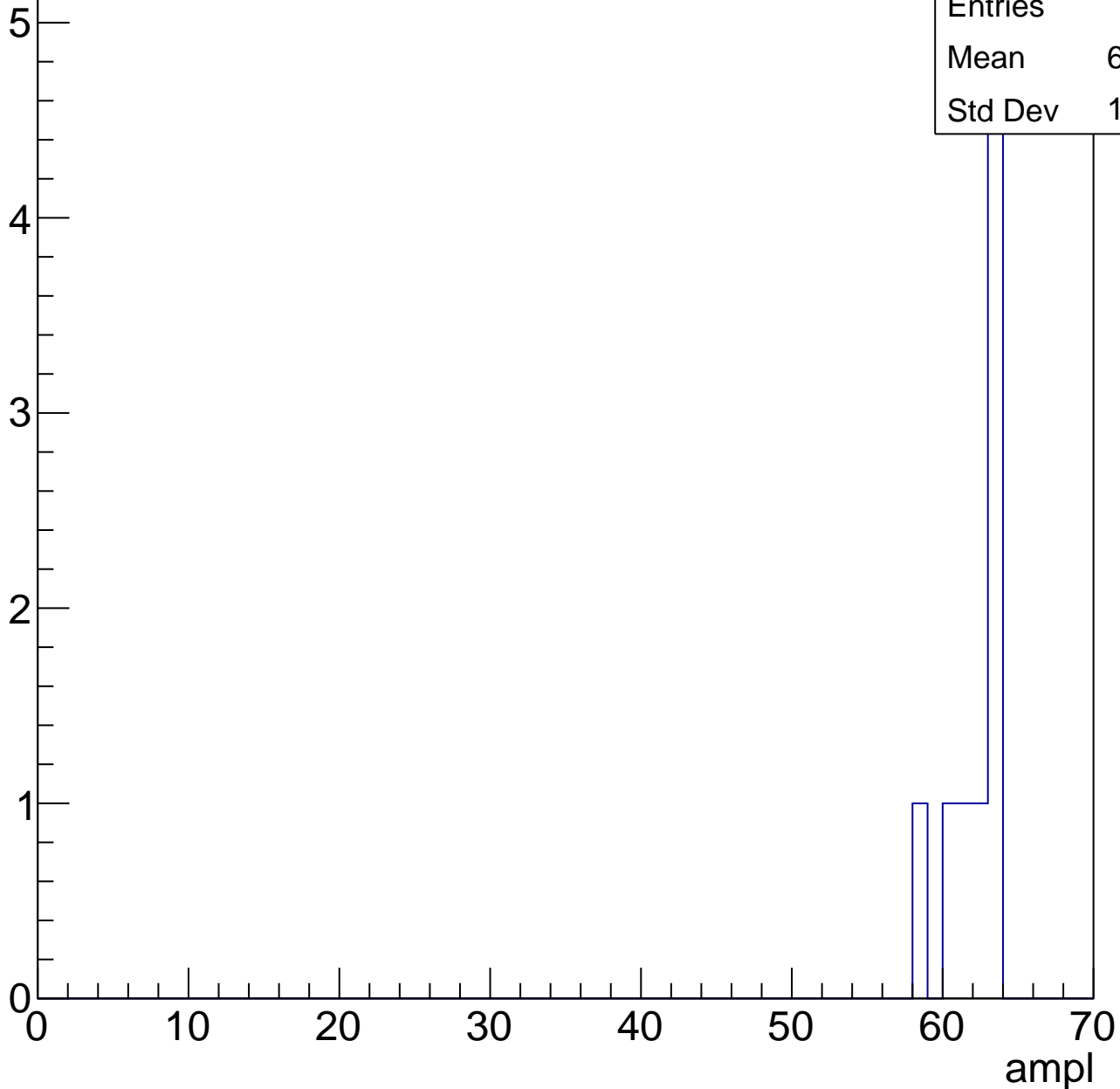


B0L101S, U24-ch100, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	61.78
Std Dev	1.685



B0L101S, U24-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

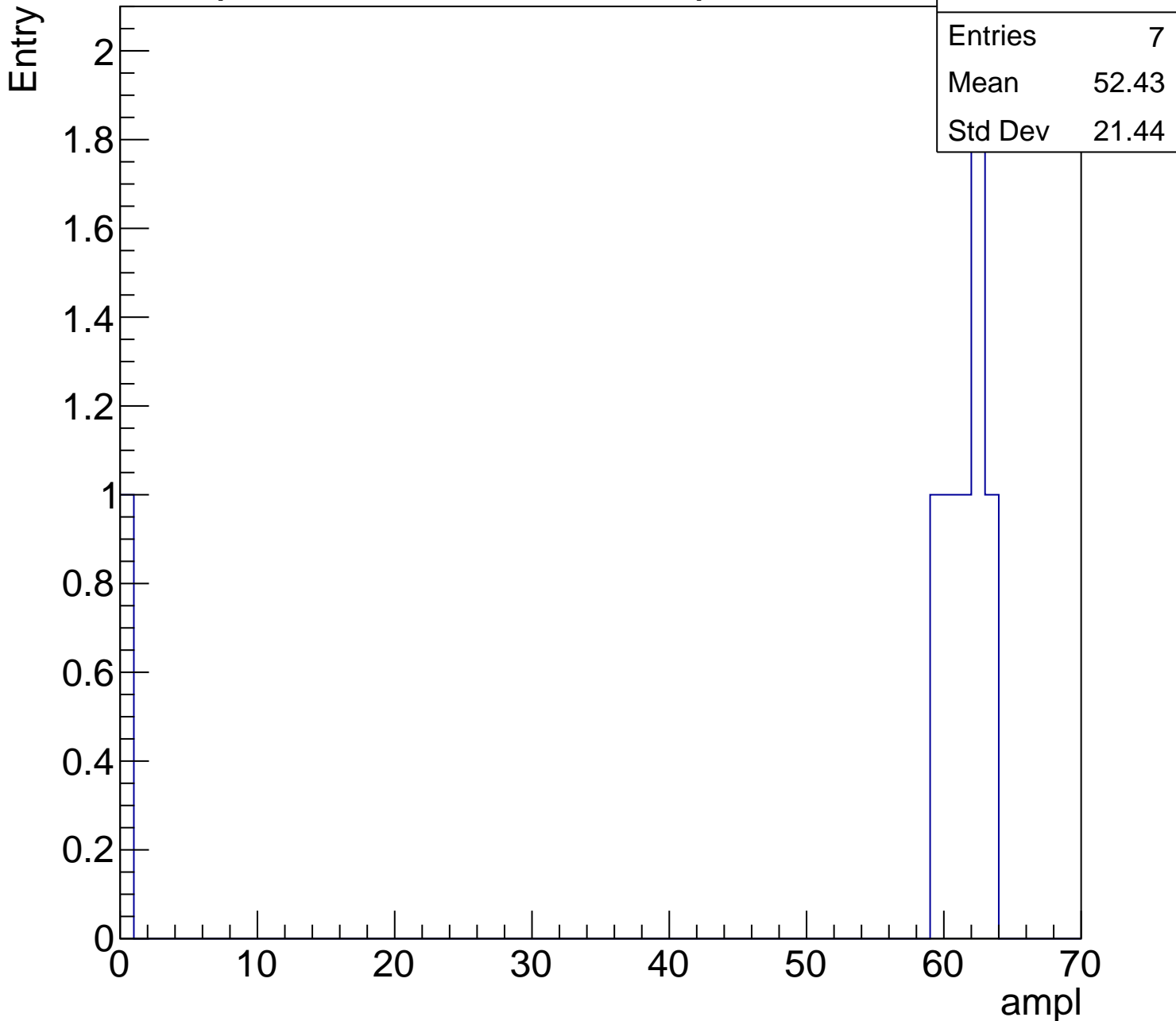
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.43
Std Dev	21.44

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch101, adc0

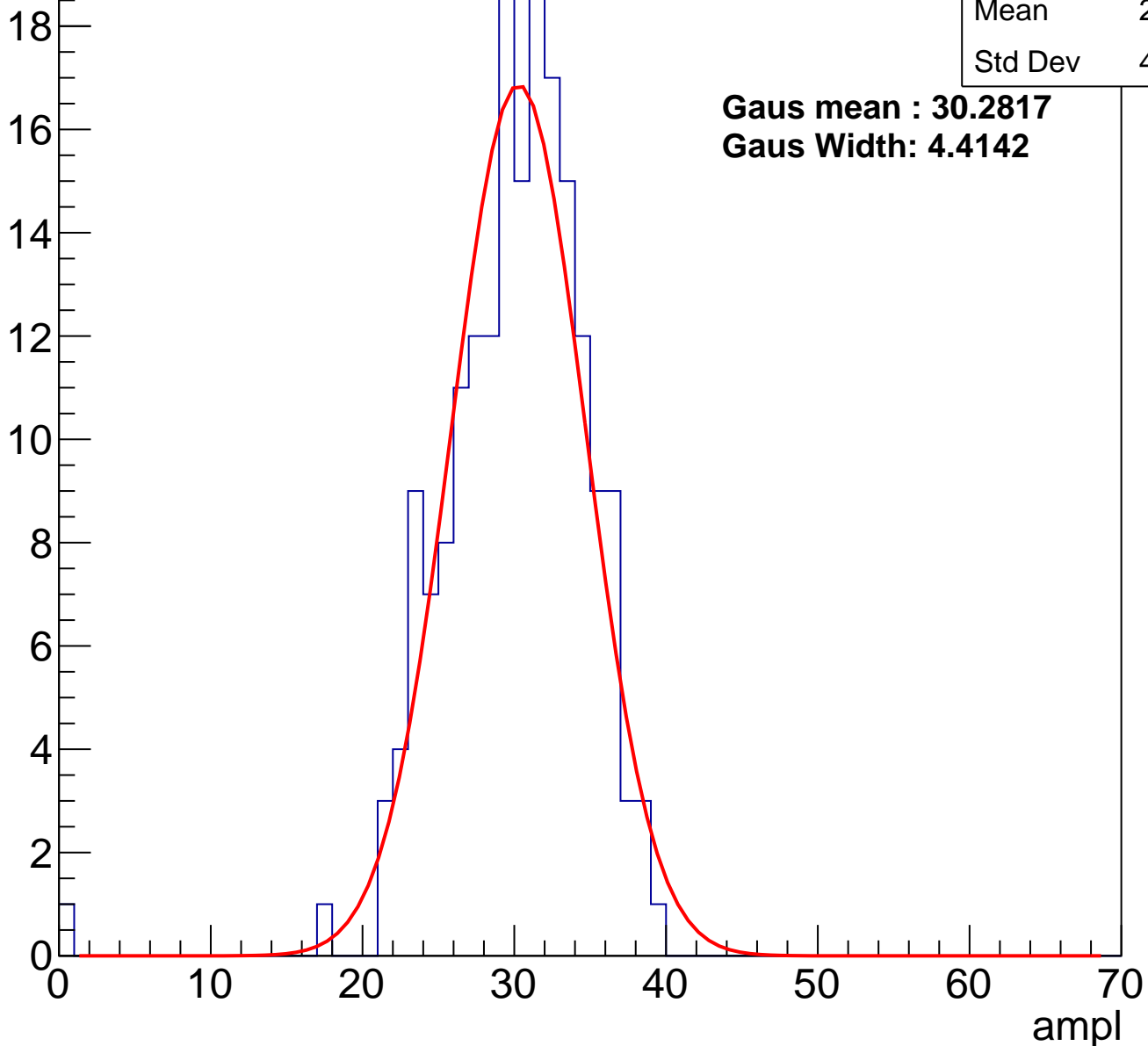
calib_packv5_042523_0143.root, FC#1, port C1

Entries	190
Mean	29.63
Std Dev	4.684

Gaus mean : 30.2817

Gaus Width: 4.4142

Entry



B0L101S, U24-ch101, adc1

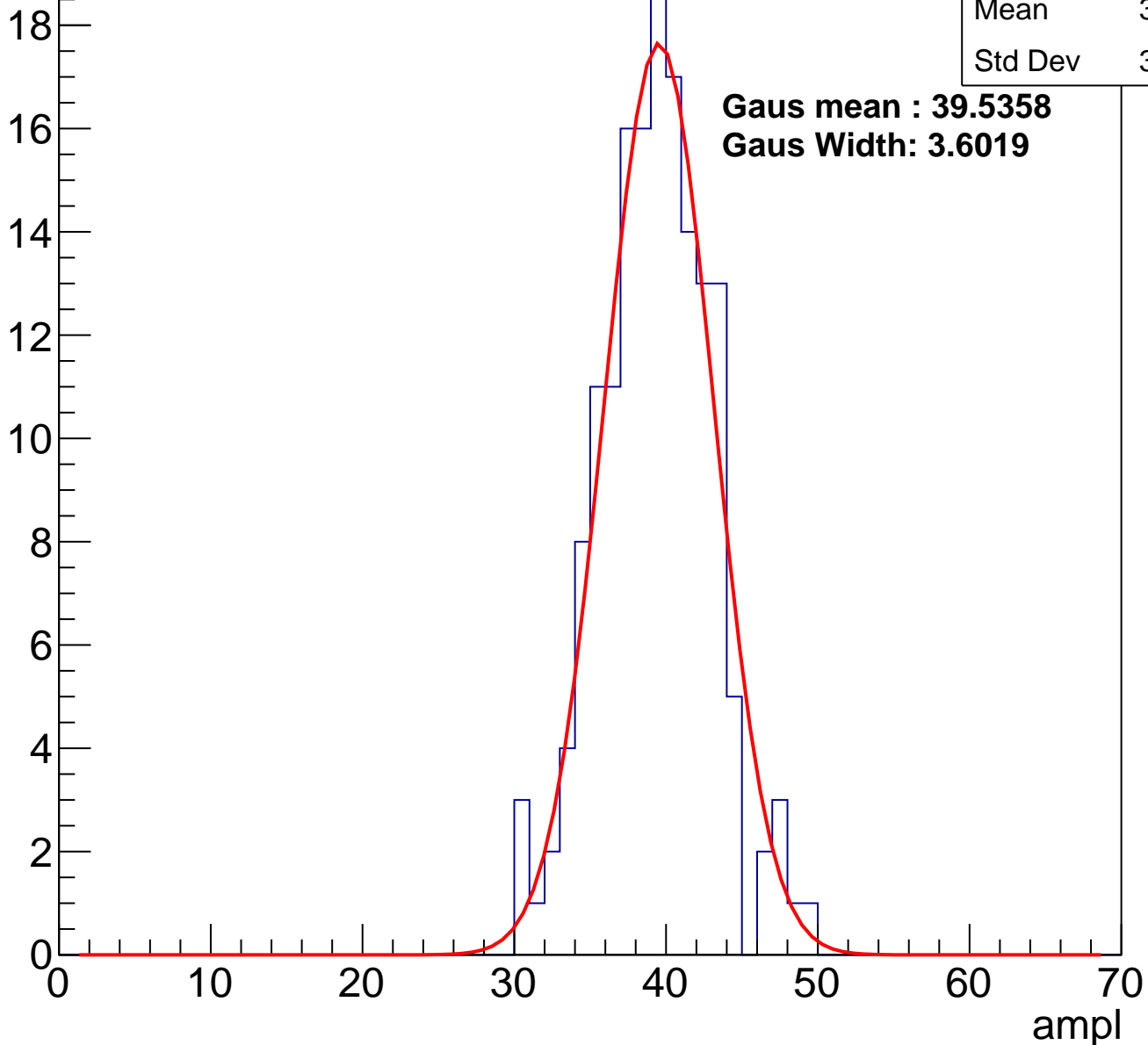
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	38.88
Std Dev	3.619

Gaus mean : 39.5358

Gaus Width: 3.6019

Entry



B0L101S, U24-ch101, adc2

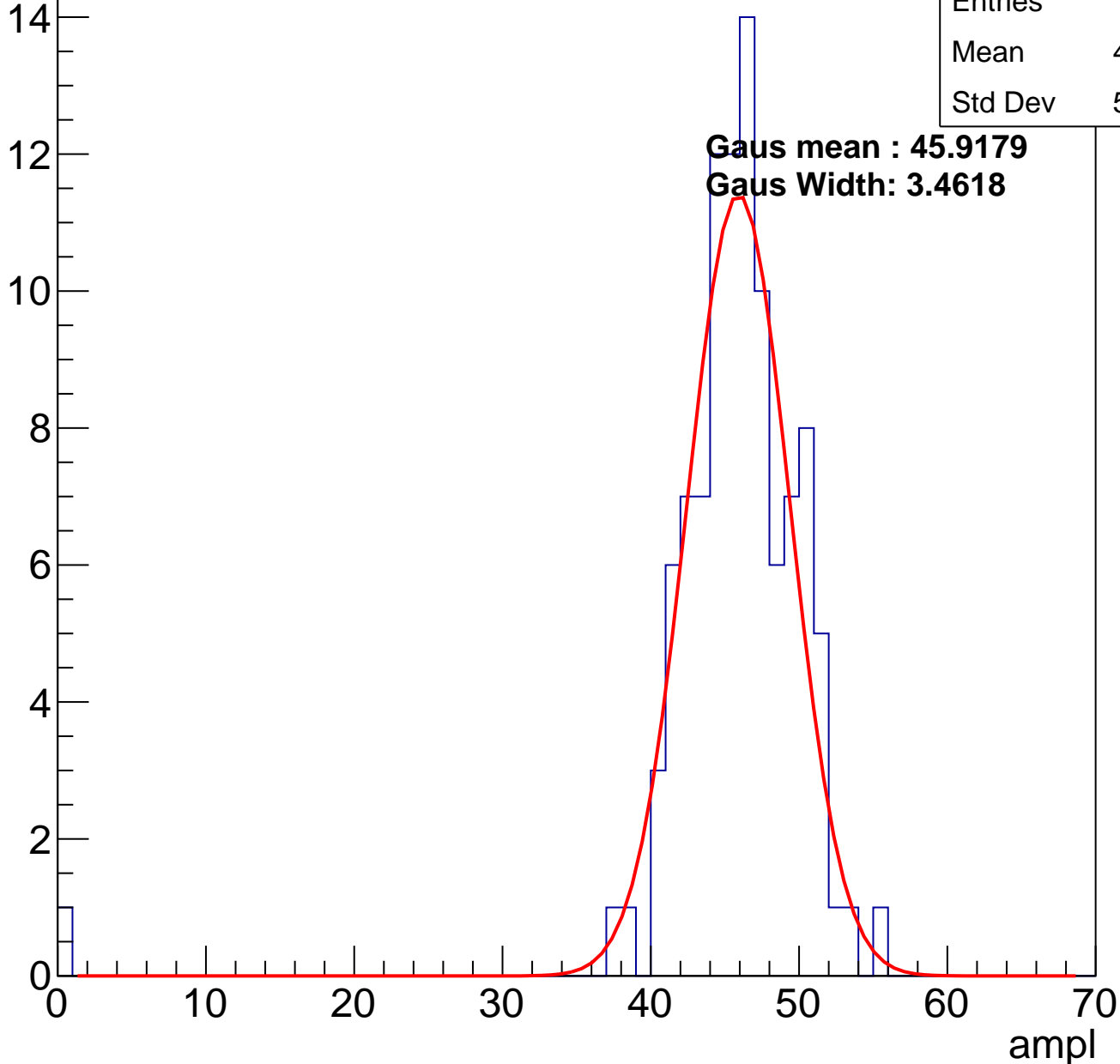
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	103
Mean	45.28
Std Dev	5.589

Gaus mean : 45.9179

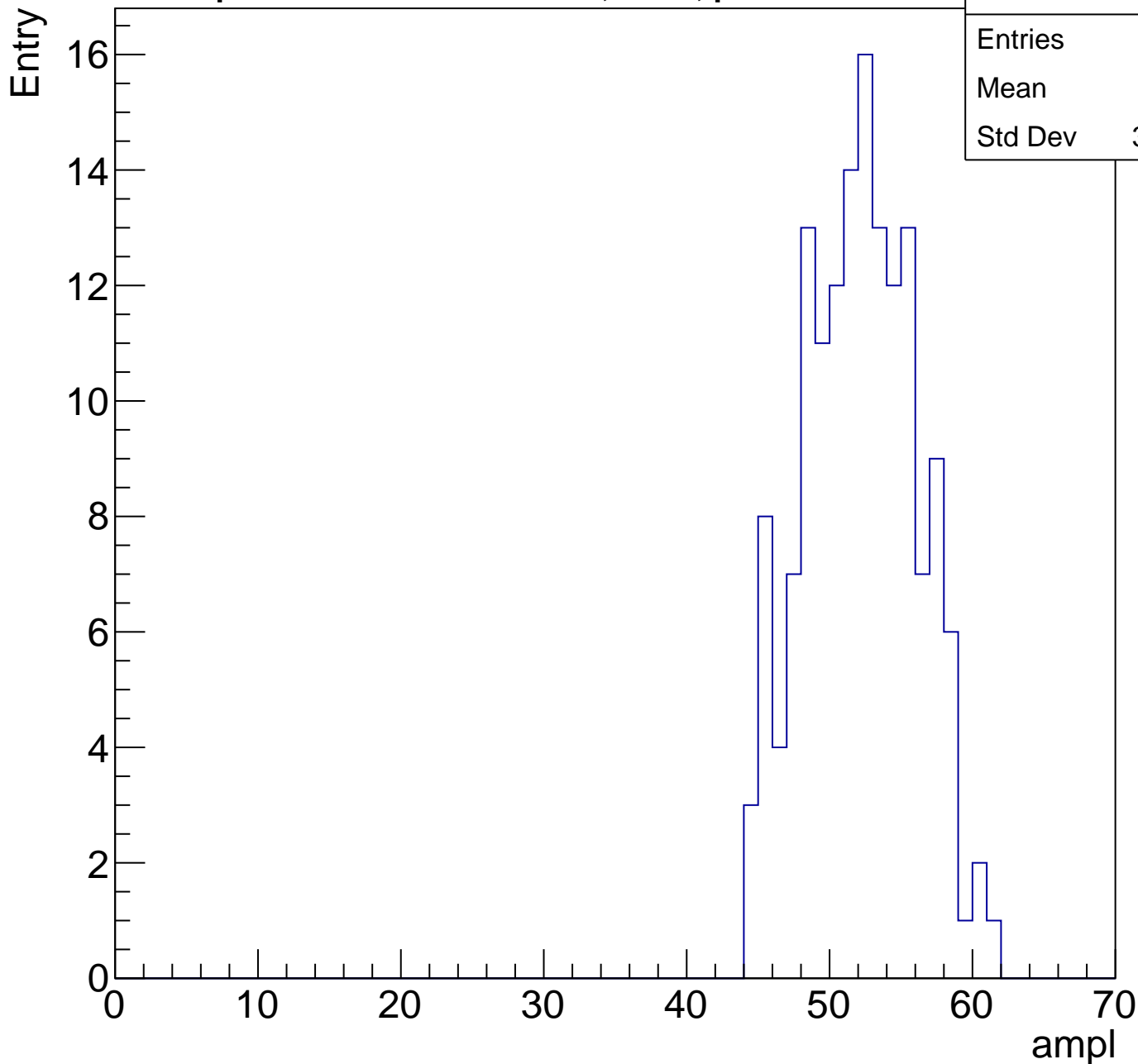
Gaus Width: 3.4618



B0L101S, U24-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	51.7
Std Dev	3.845



B0L101S, U24-ch101, adc4

calib_packv5_042523_0143.root, FC#1, port C1

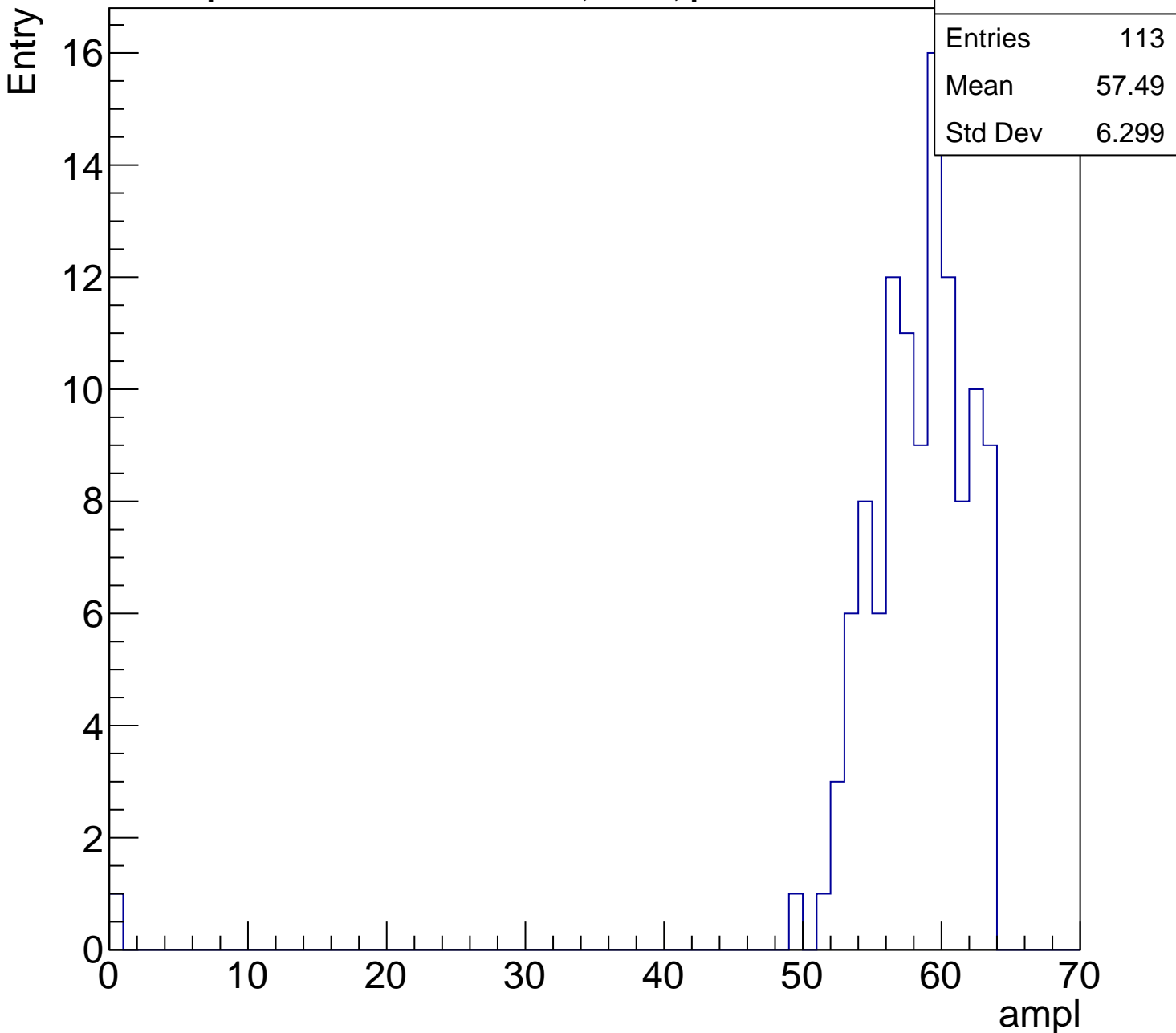
Entries	113
Mean	57.49
Std Dev	6.299

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

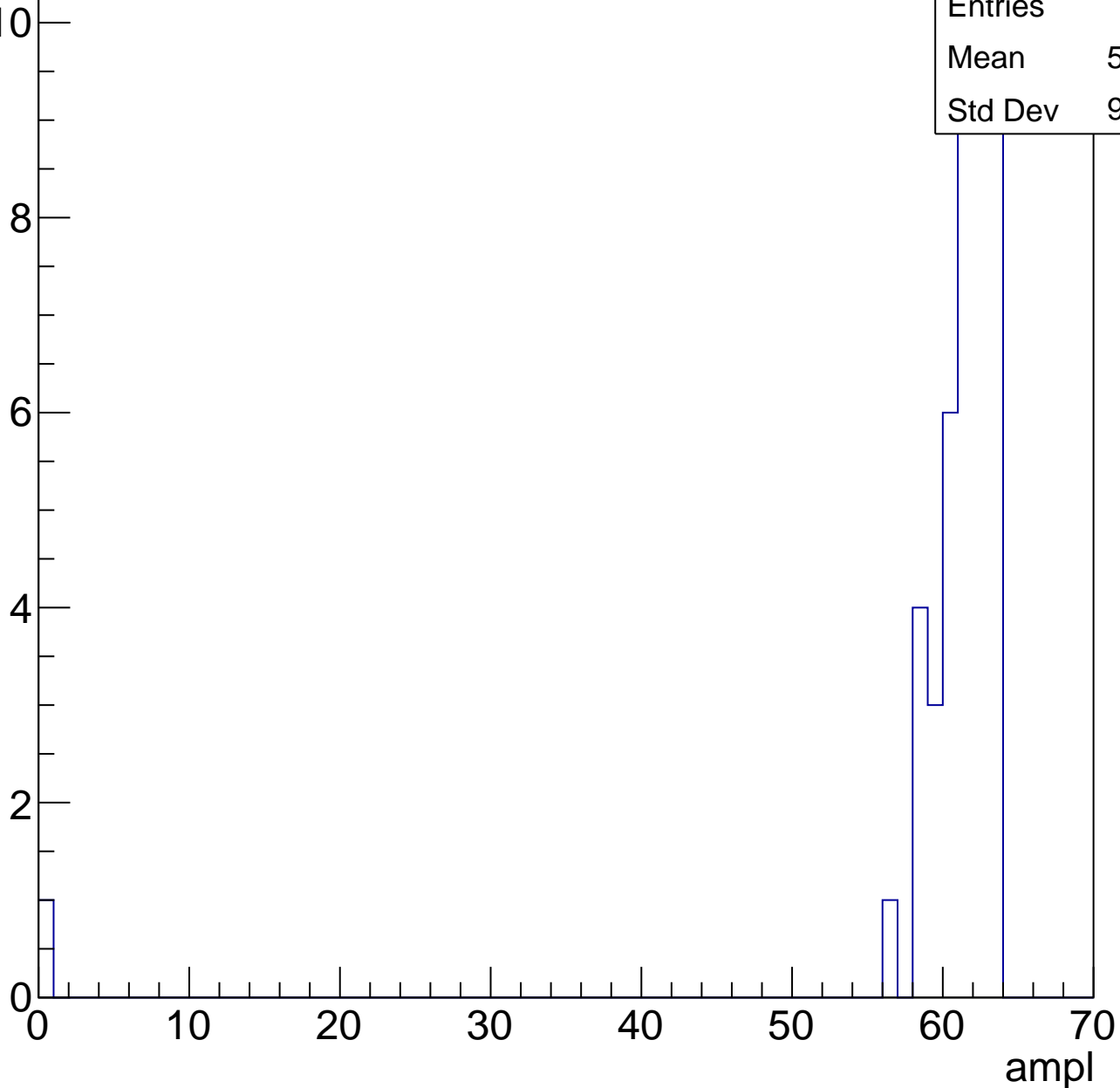


B0L101S, U24-ch101, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	43
Mean	59.53
Std Dev	9.342



B0L101S, U24-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.855

ampl

0 10 20 30 40 50 60 70

0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2

B0L101S, U24-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U24-ch102, adc0

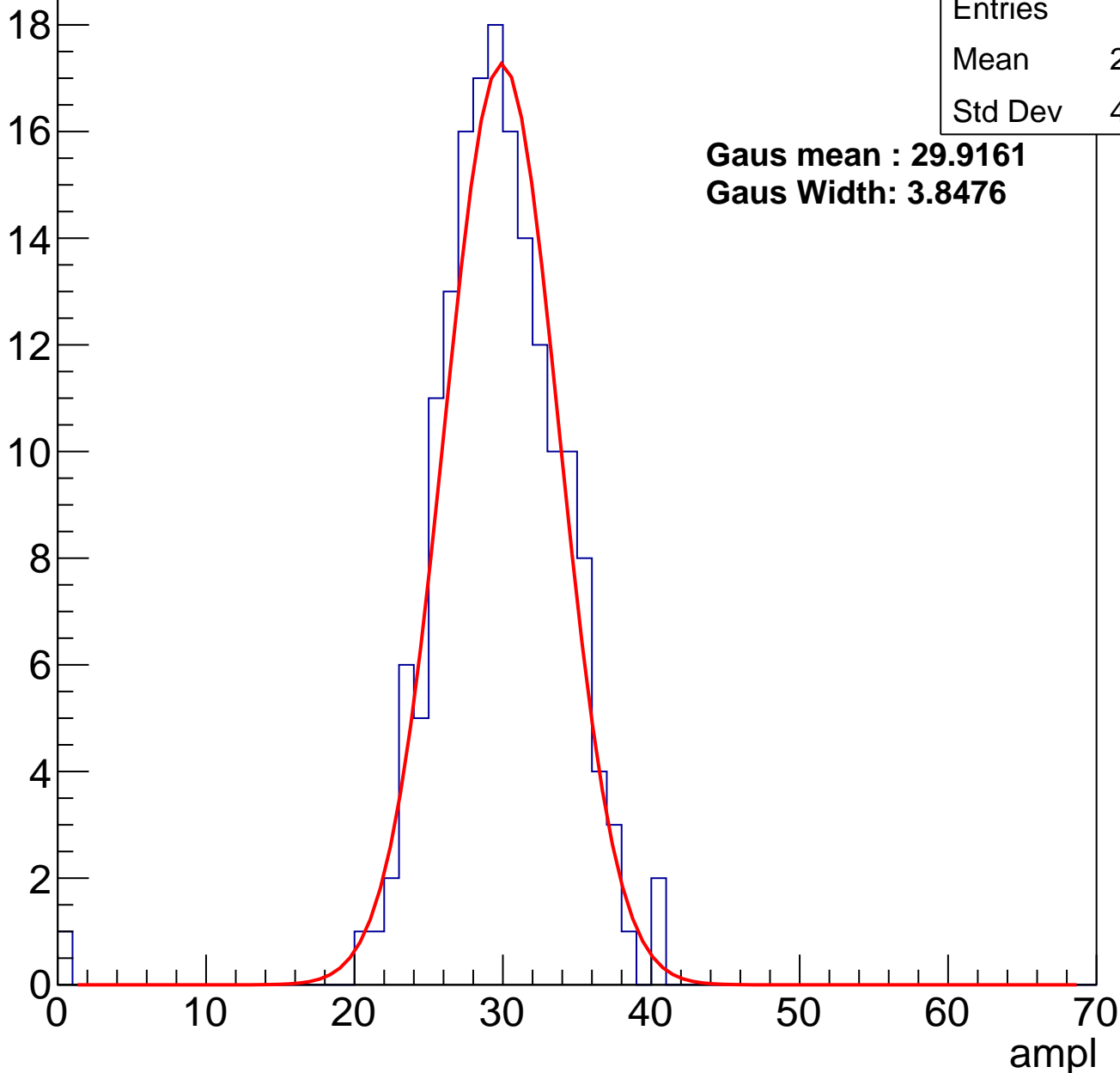
calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	29.28
Std Dev	4.442

Gaus mean : 29.9161

Gaus Width: 3.8476

Entry



B0L101S, U24-ch102, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

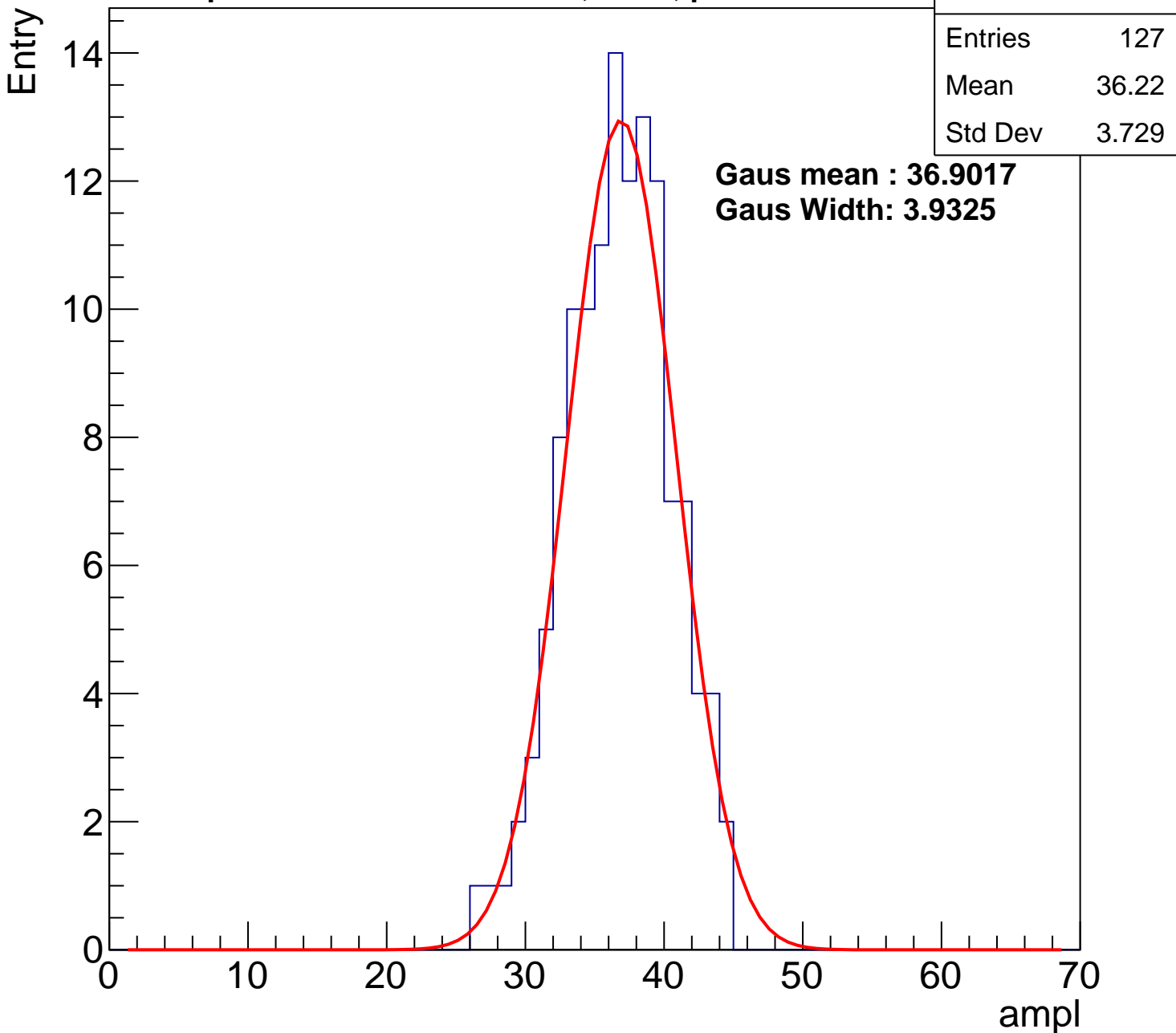
14
12
10
8
6
4
2
0

Entries	127
Mean	36.22
Std Dev	3.729

Gaus mean : 36.9017
Gaus Width: 3.9325

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

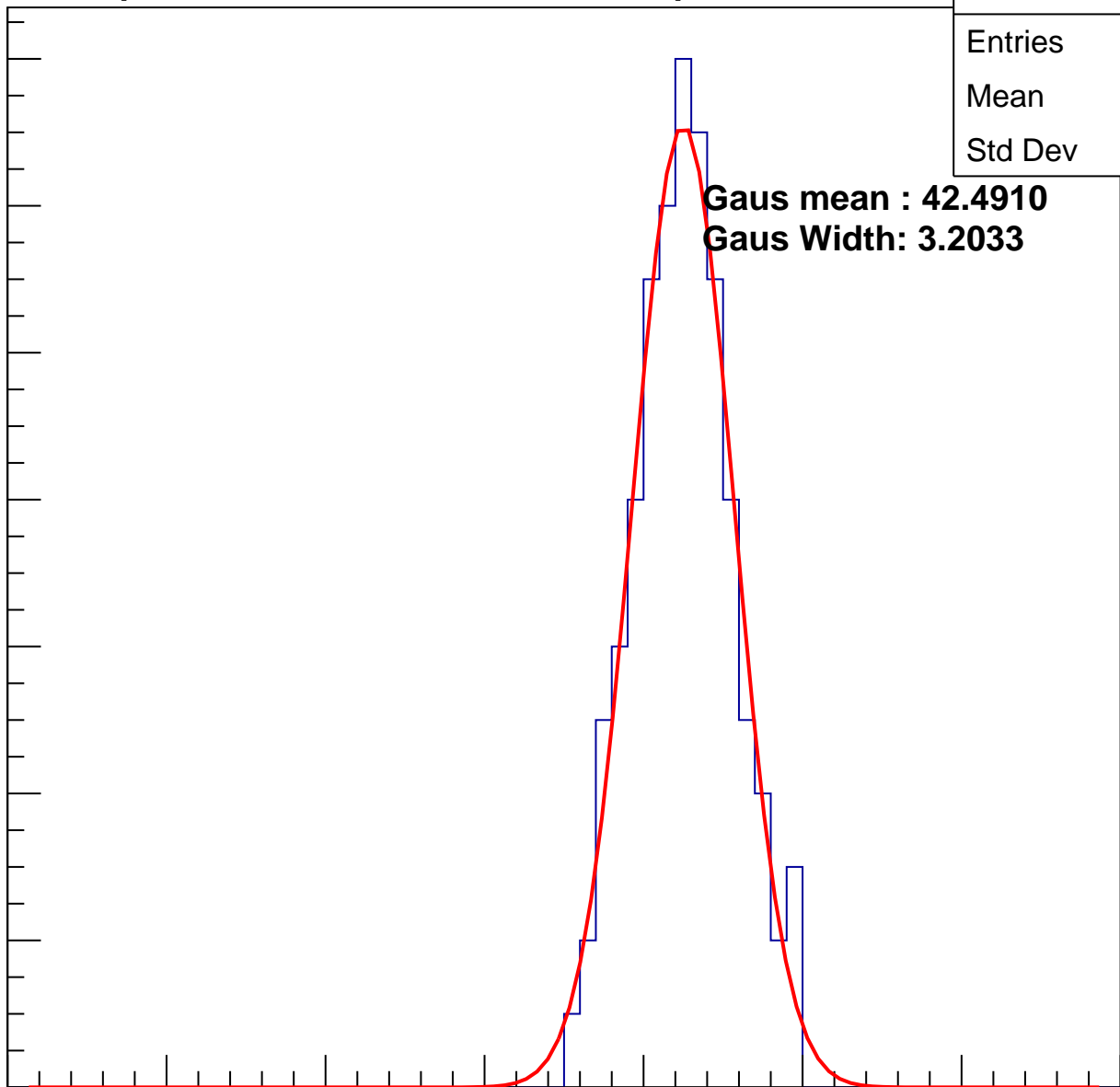
Entries	105
Mean	42.06
Std Dev	3.082

Gaus mean : 42.4910

Gaus Width: 3.2033

0 10 20 30 40 50 60 70

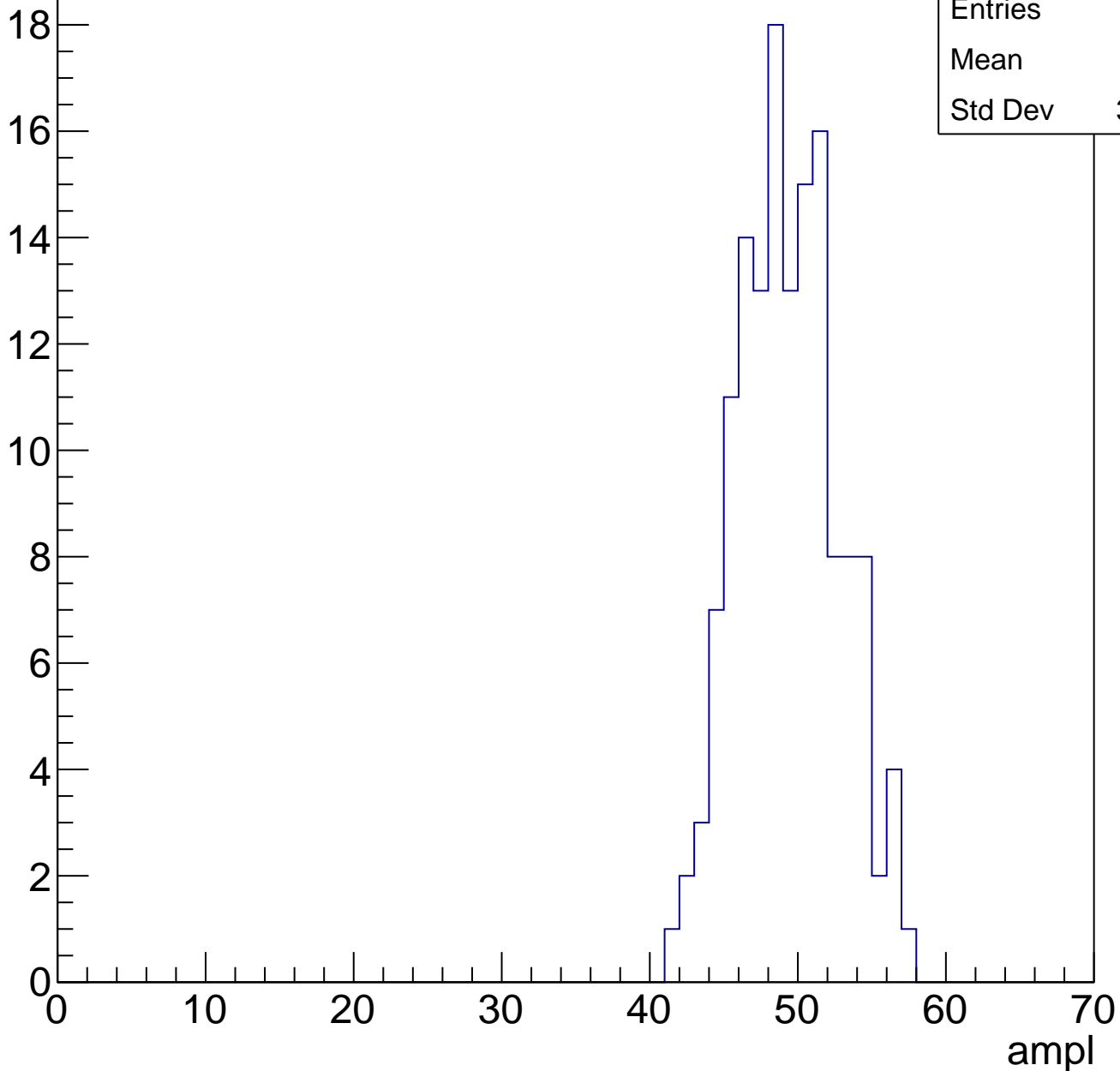
ampl



B0L101S, U24-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch102, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	55.13
Std Dev	4.644

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch102, adc5

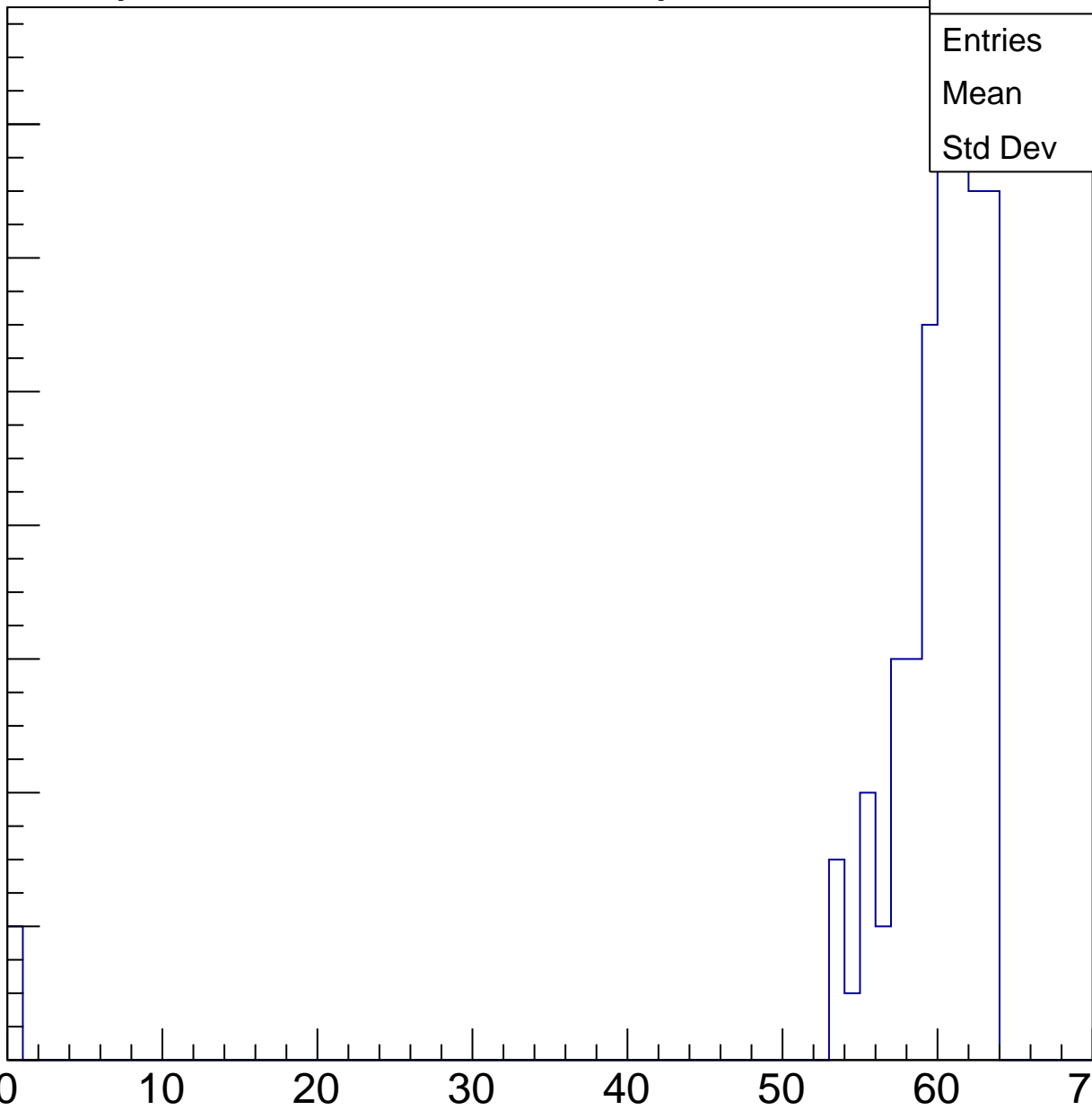
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.48
Std Dev	9.176

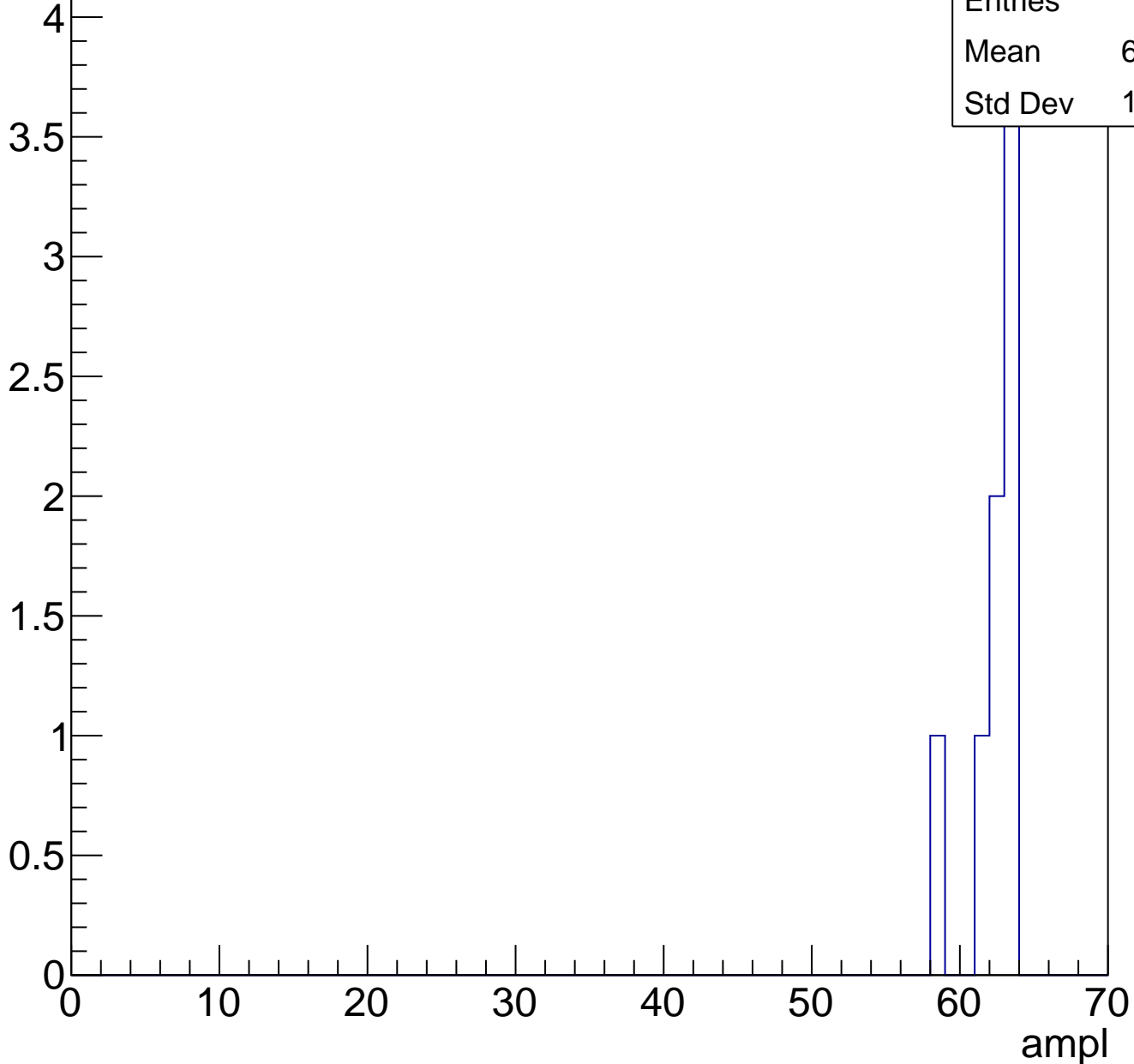
ampl



B0L101S, U24-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

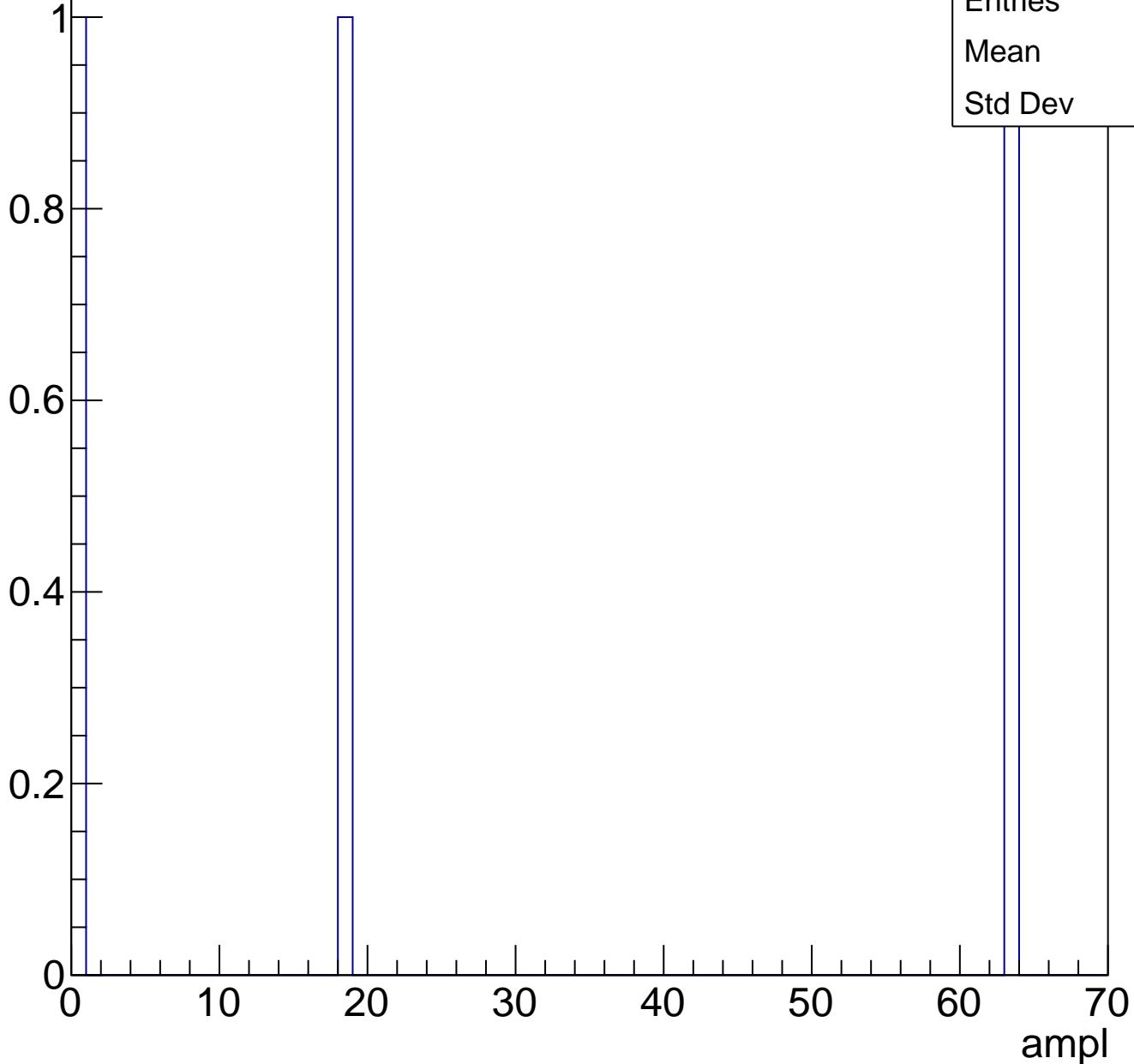
Entry



B0L101S, U24-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	27
Std Dev	26.5

B0L101S, U24-ch103, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.11
Std Dev	4.234

Gaus mean : 29.8546

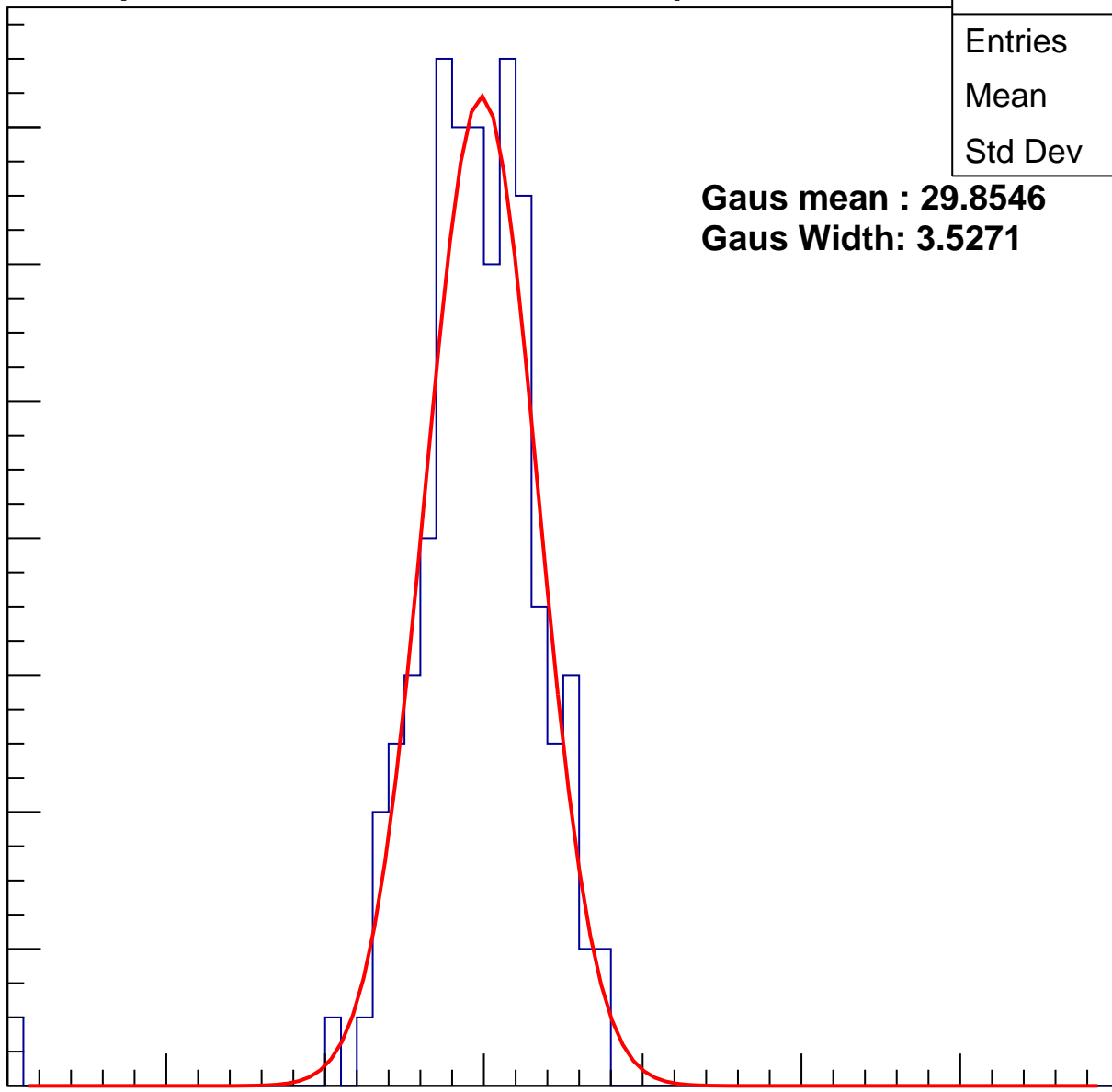
Gaus Width: 3.5271

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch103, adc1

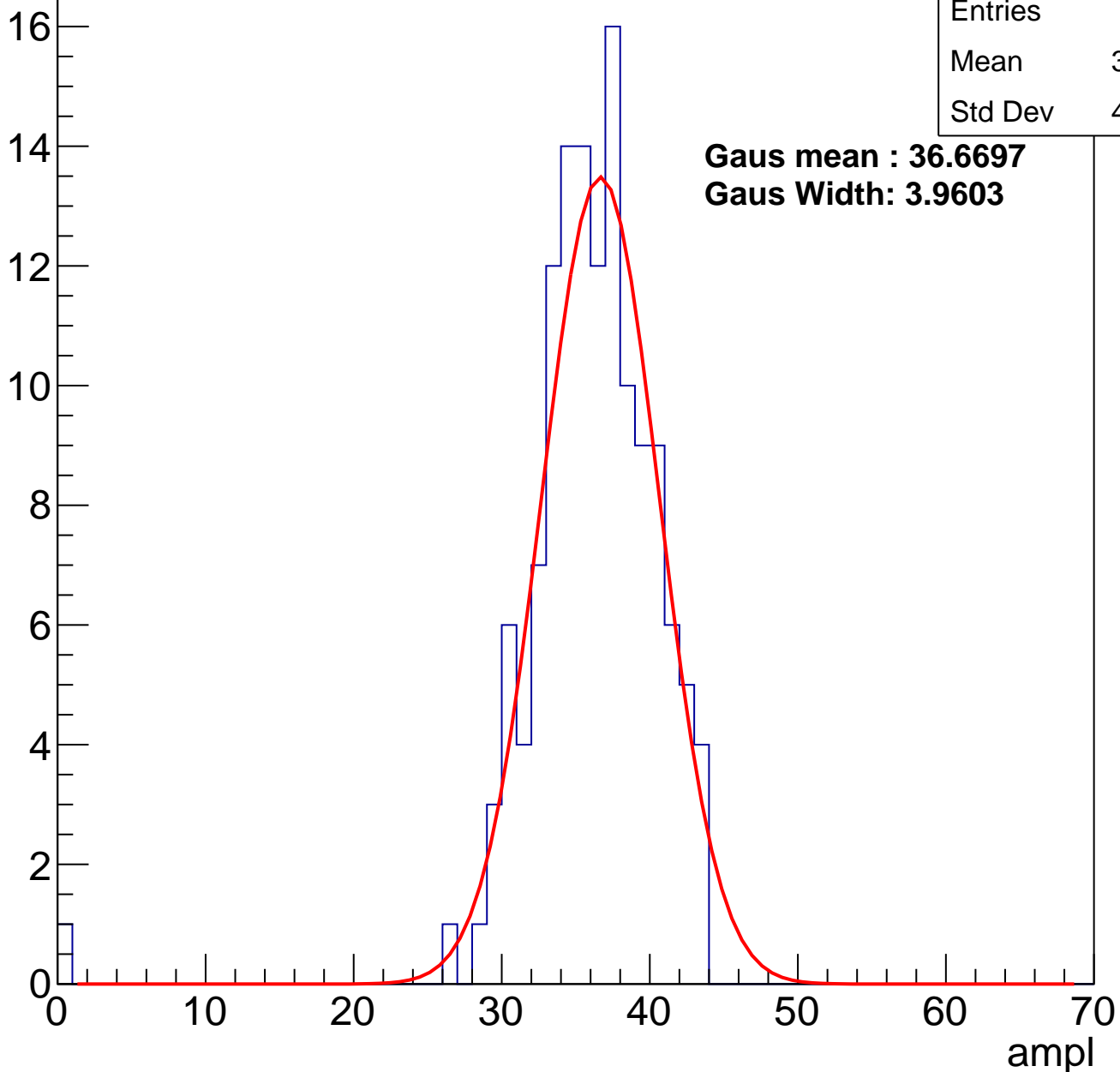
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	35.63
Std Dev	4.733

Gaus mean : 36.6697

Gaus Width: 3.9603

Entry



B0L101S, U24-ch103, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

136

Mean

42.32

Std Dev

3.479

Gaus mean : 42.9120

Gaus Width: 3.4137

0

10

20

30

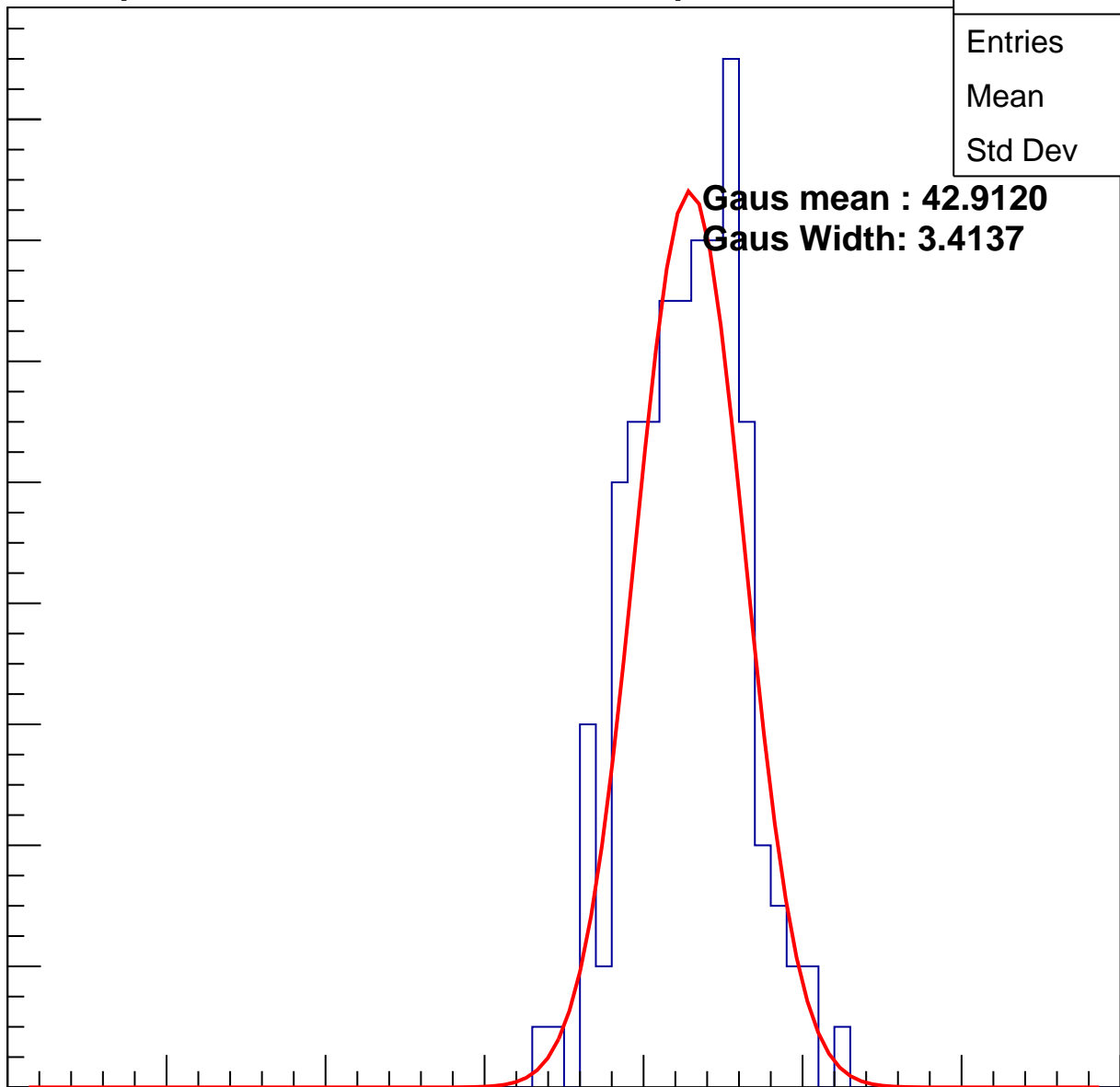
40

50

60

70

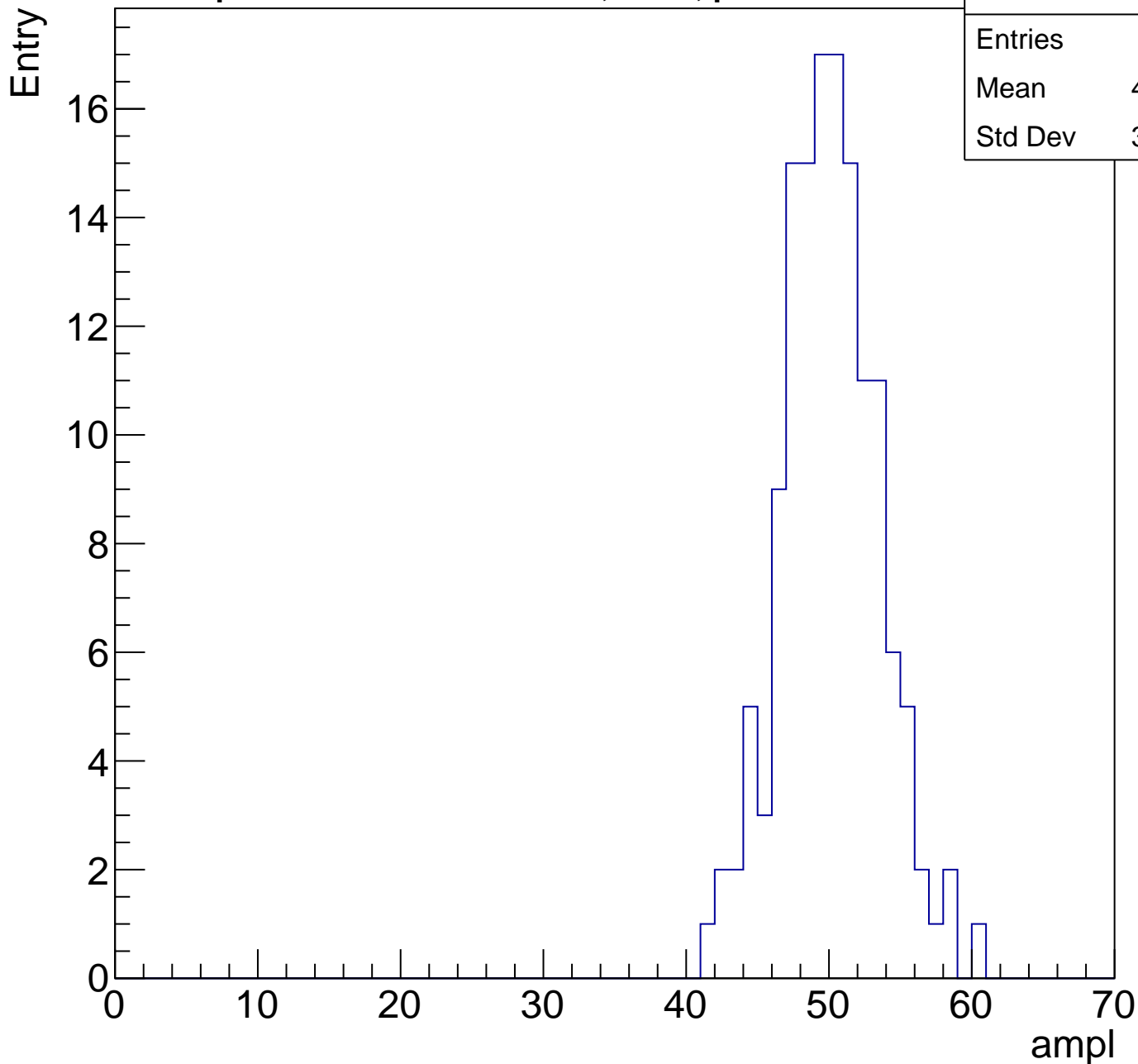
ampl



B0L101S, U24-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	49.66
Std Dev	3.412

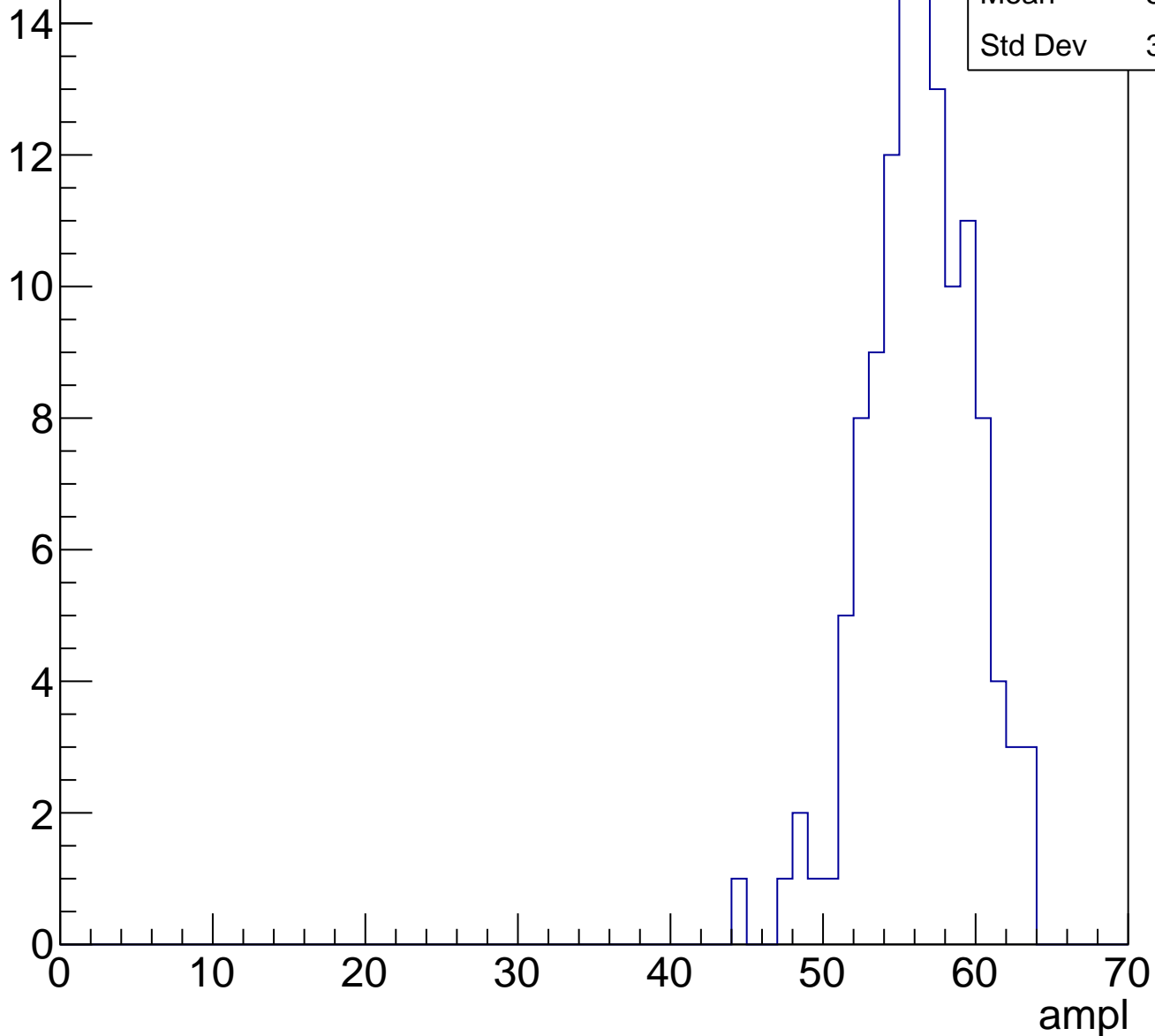


B0L101S, U24-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	55.87
Std Dev	3.469

Entry



B0L101S, U24-ch103, adc5

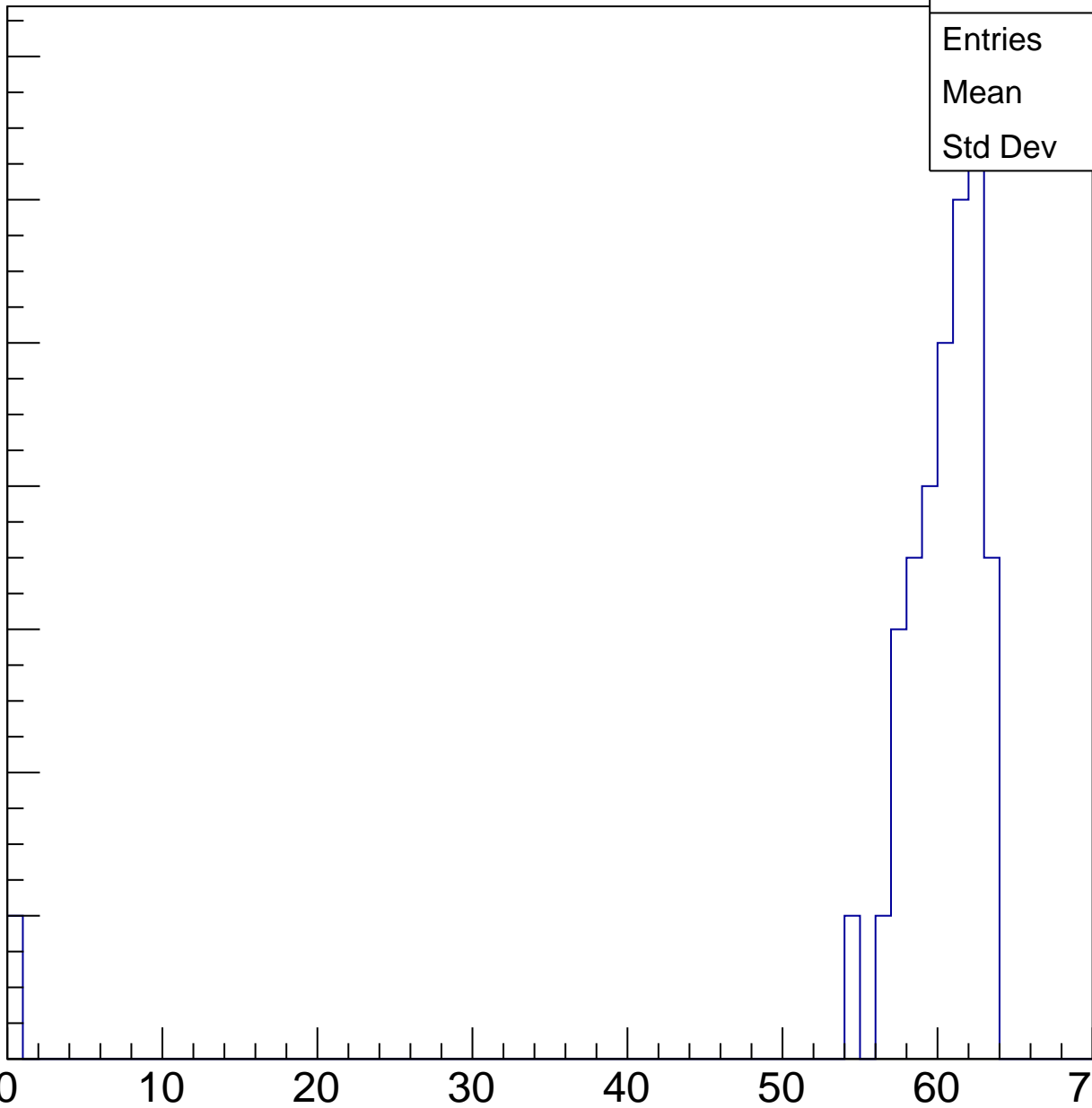
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	58.3
Std Dev	10.23

ampl

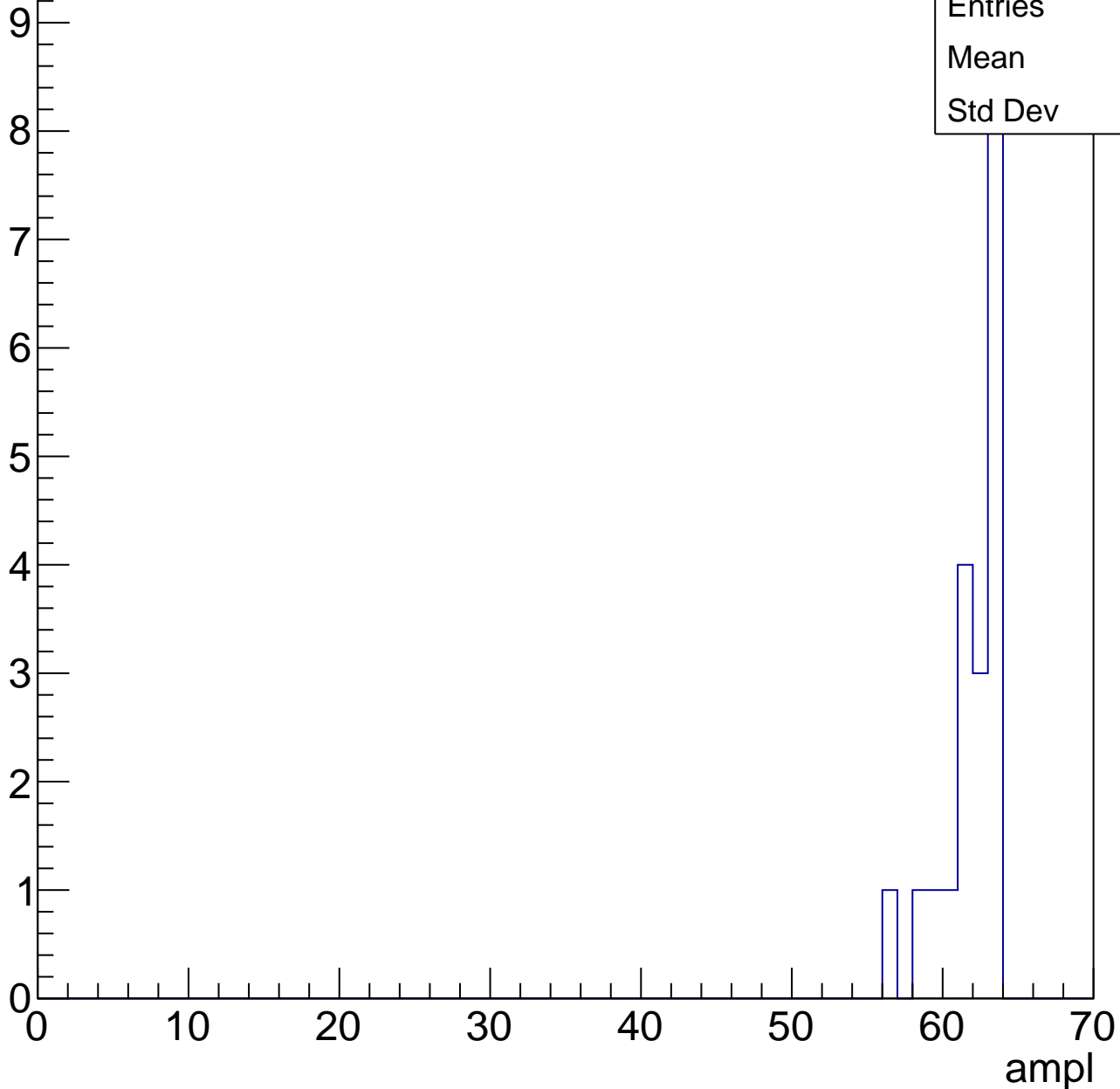


B0L101S, U24-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	61.5
Std Dev	1.91



B0L101S, U24-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch104, adc0

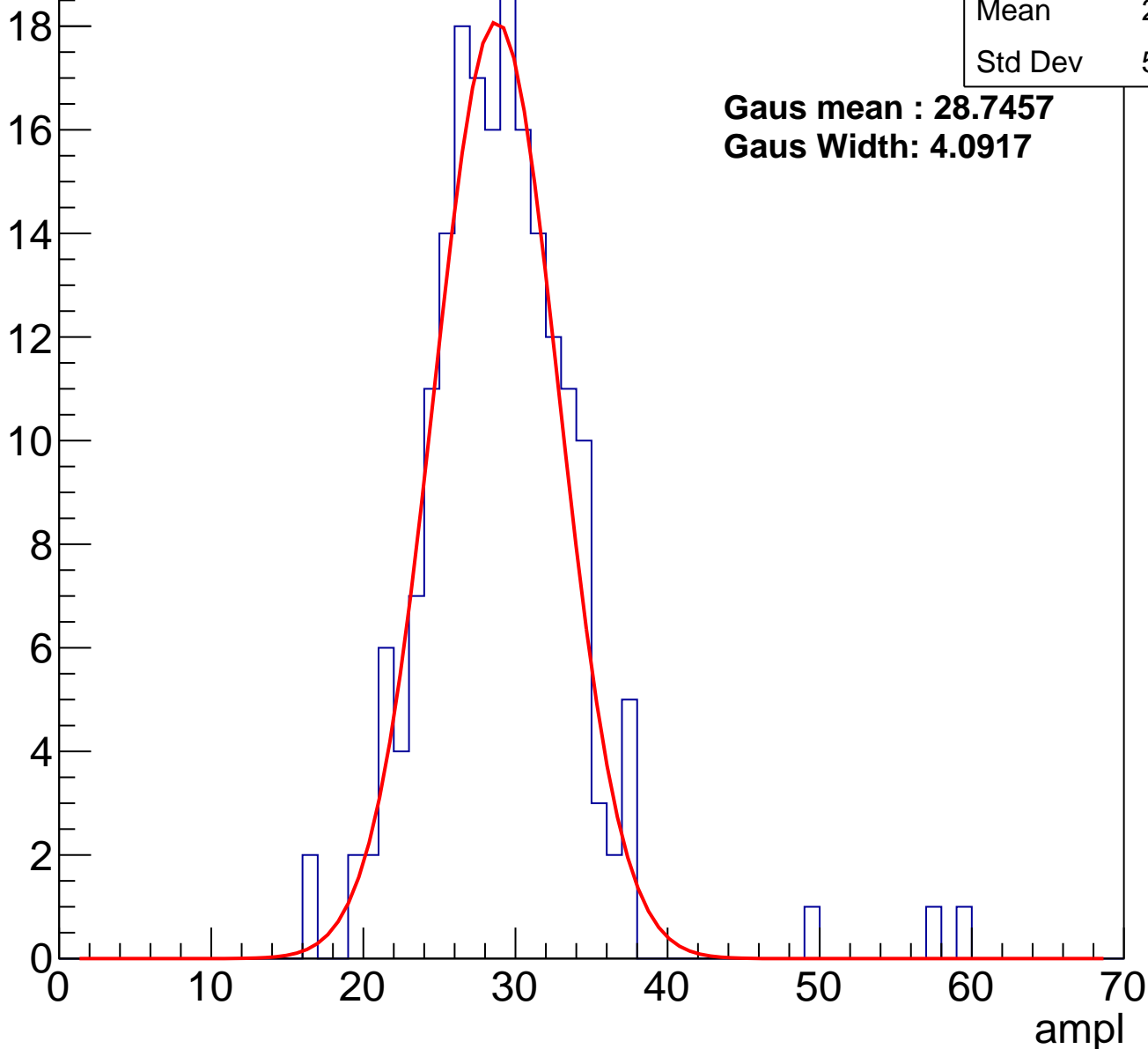
calib_packv5_042523_0143.root, FC#1, port C1

Entries	194
Mean	28.62
Std Dev	5.308

Gaus mean : 28.7457

Gaus Width: 4.0917

Entry



B0L101S, U24-ch104, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

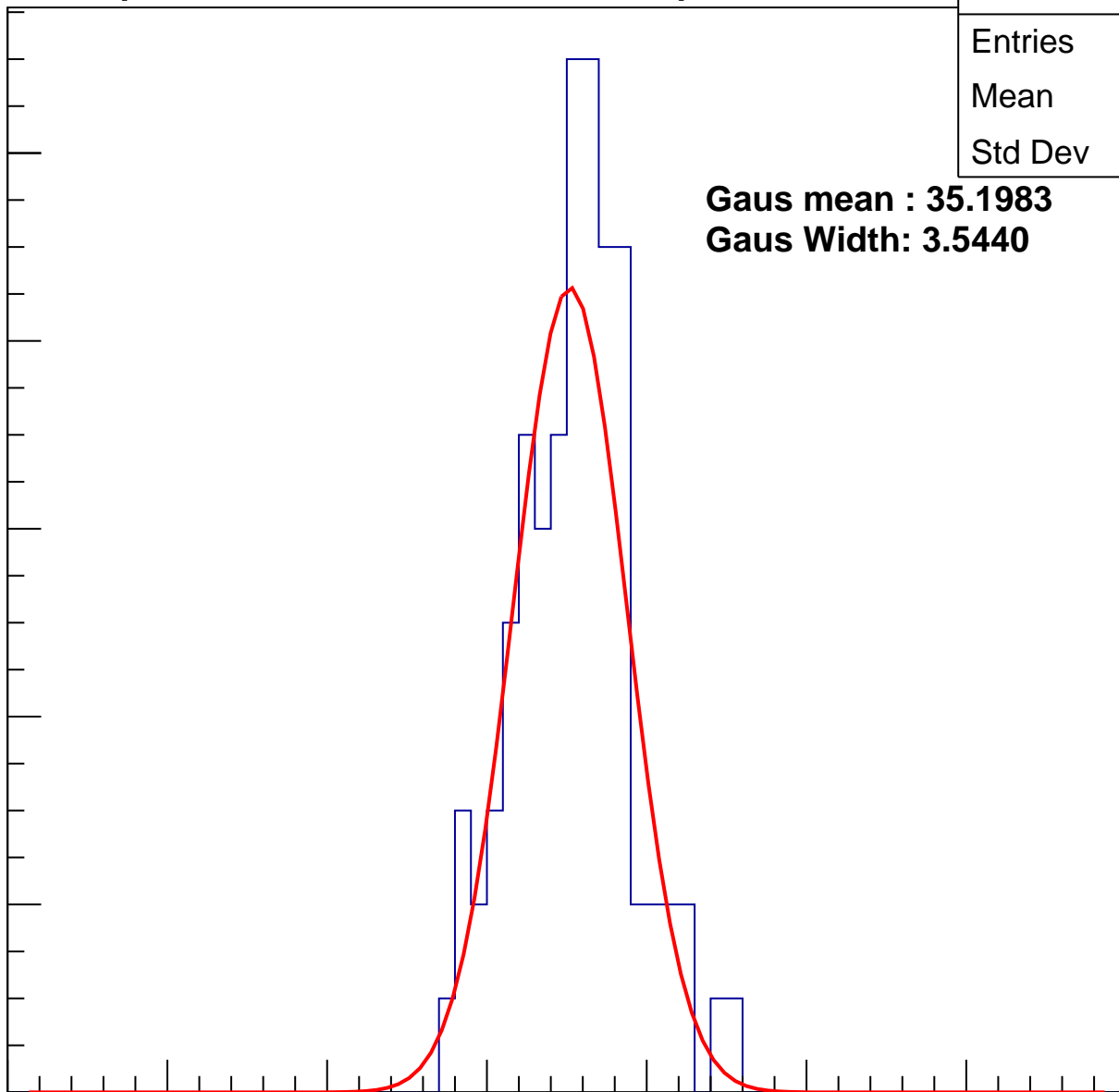
70

ampl

Entries	84
Mean	35.04
Std Dev	3.607

Gaus mean : 35.1983

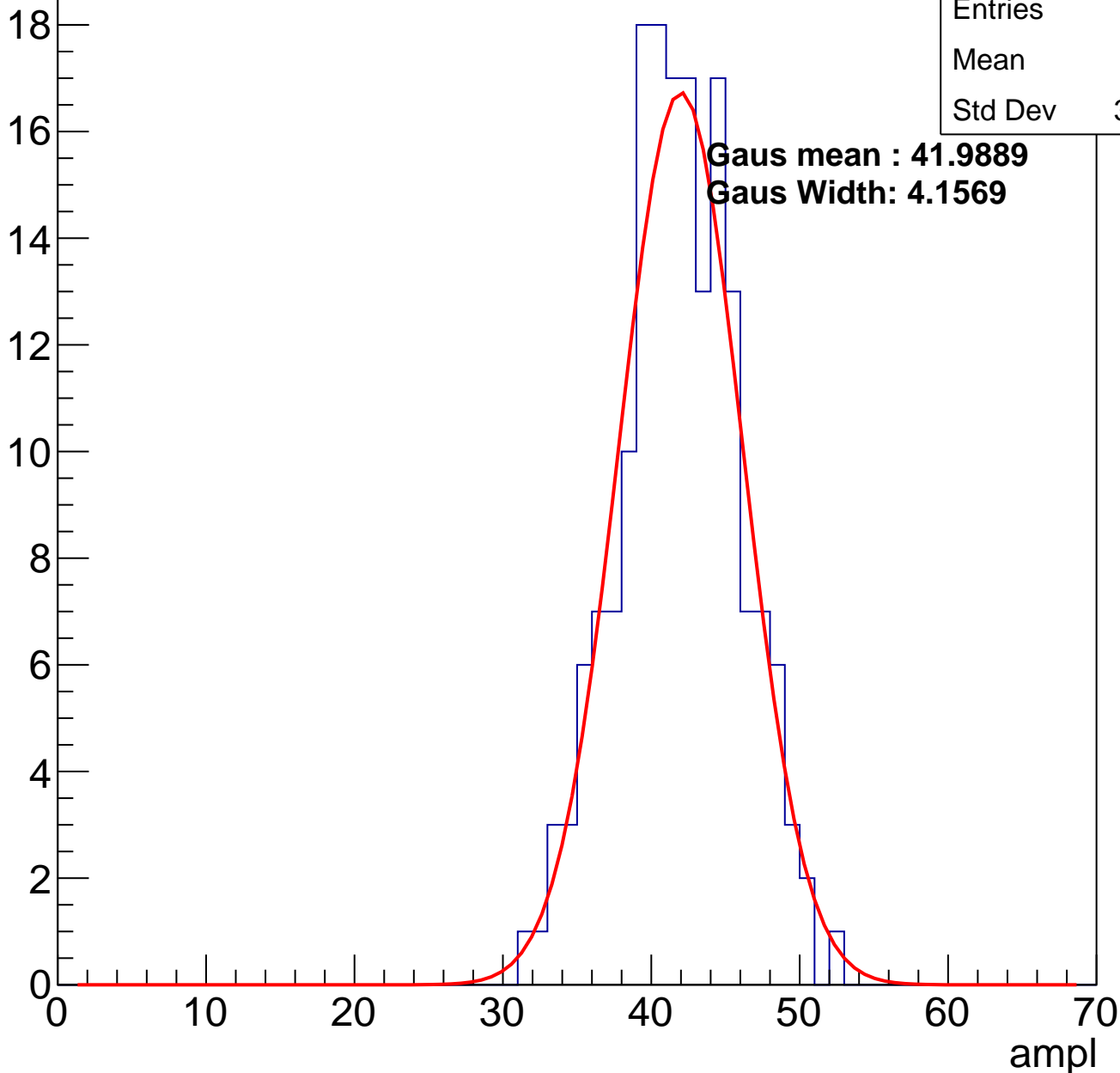
Gaus Width: 3.5440



B0L101S, U24-ch104, adc2

calib_packv5_042523_0143.root, FC#1, port C1

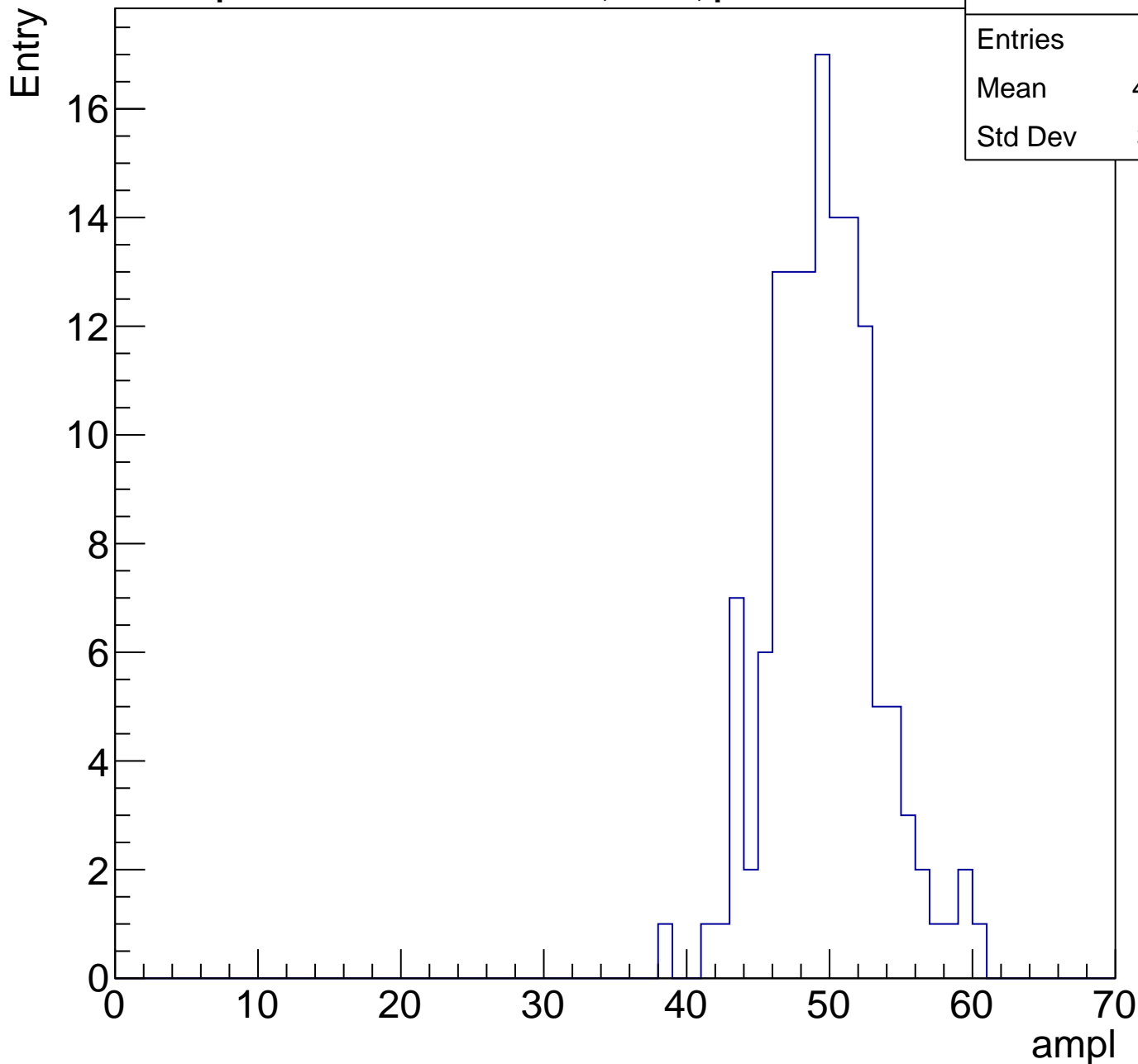
Entry



B0L101S, U24-ch104, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	49.17
Std Dev	3.731



B0L101S, U24-ch104, adc4

calib_packv5_042523_0143.root, FC#1, port C1

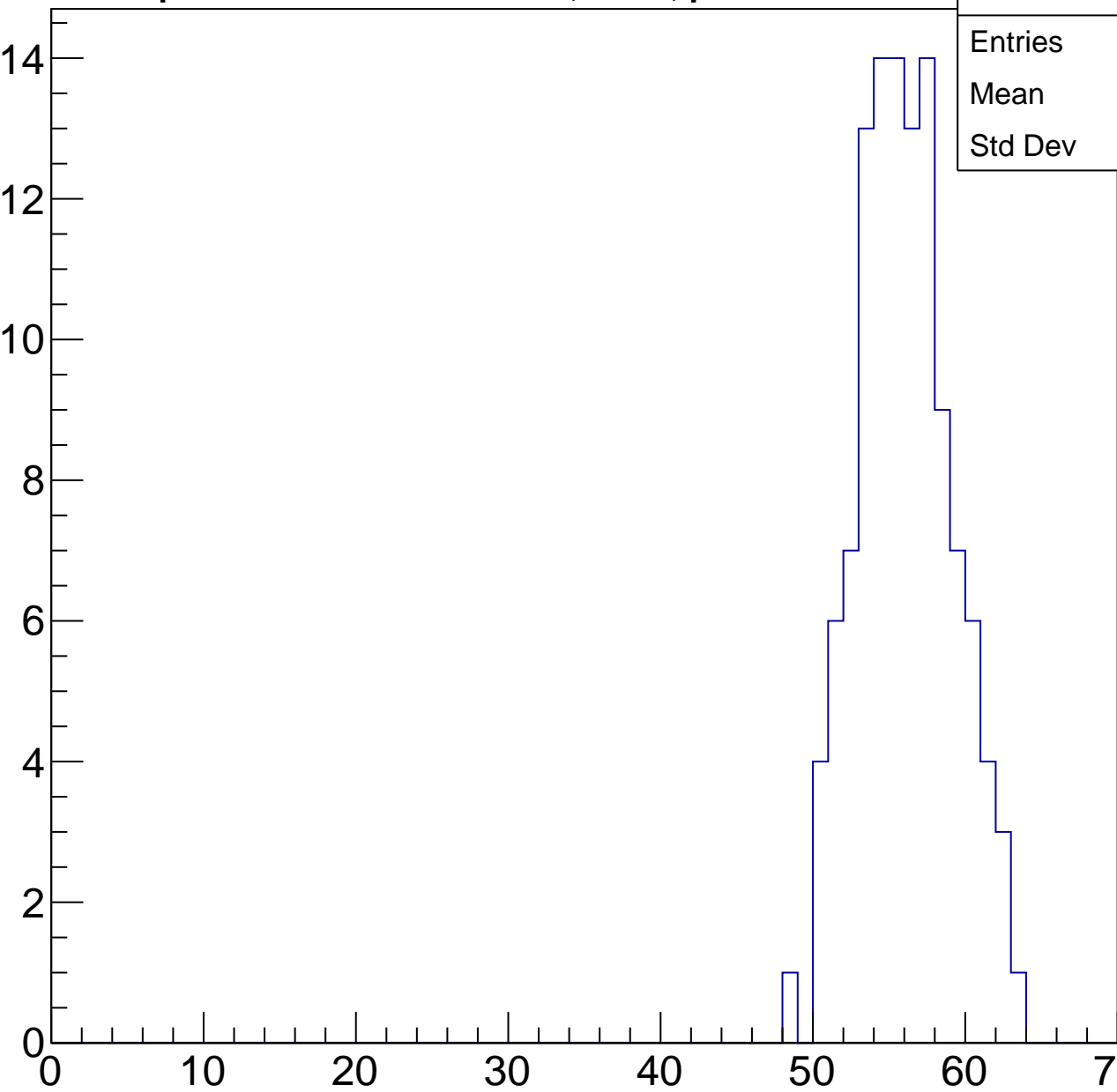
Entries	116
Mean	55.58
Std Dev	3.083

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch104, adc5

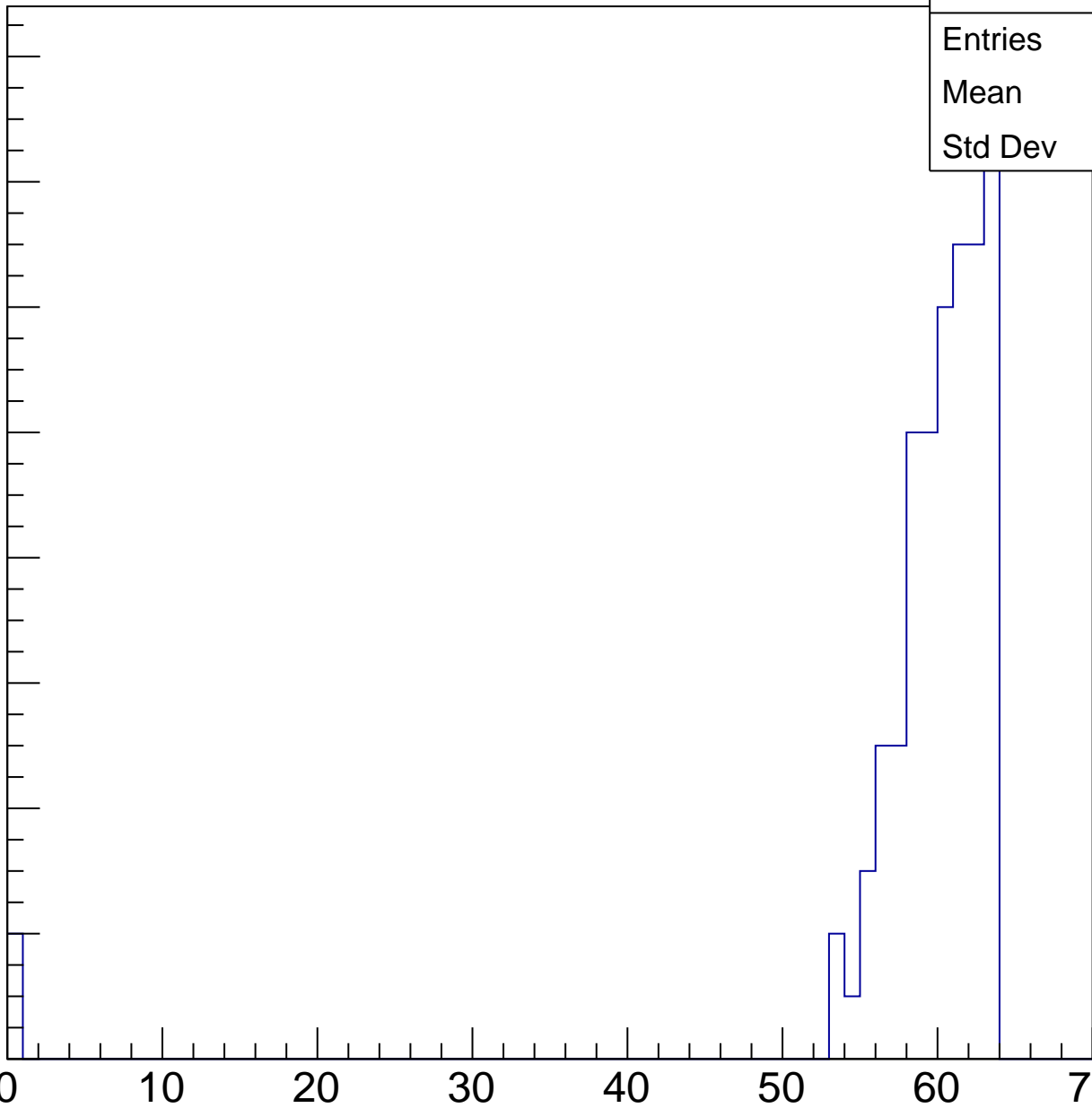
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.55
Std Dev	9.089

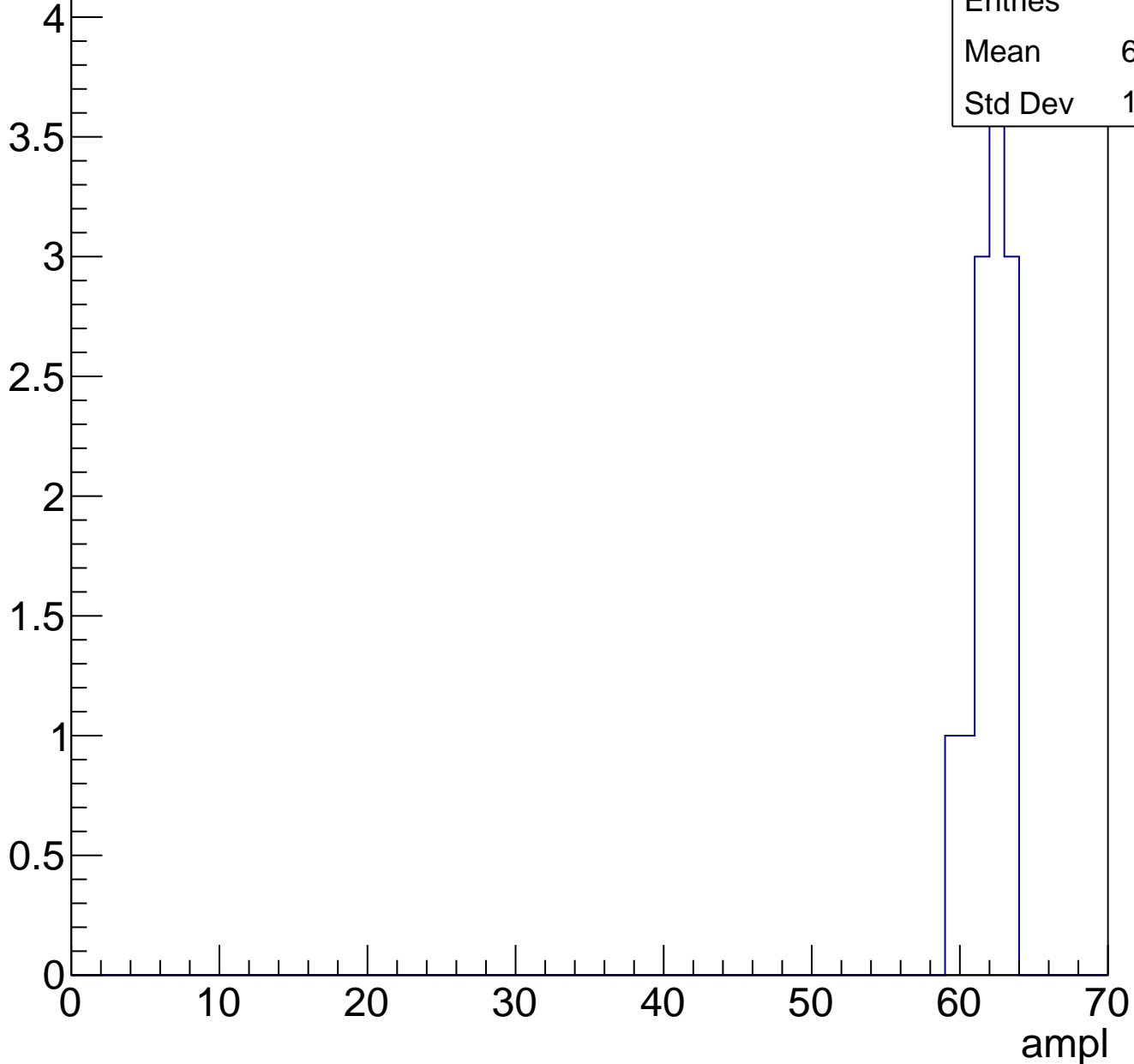
ampl



B0L101S, U24-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch105, adc0

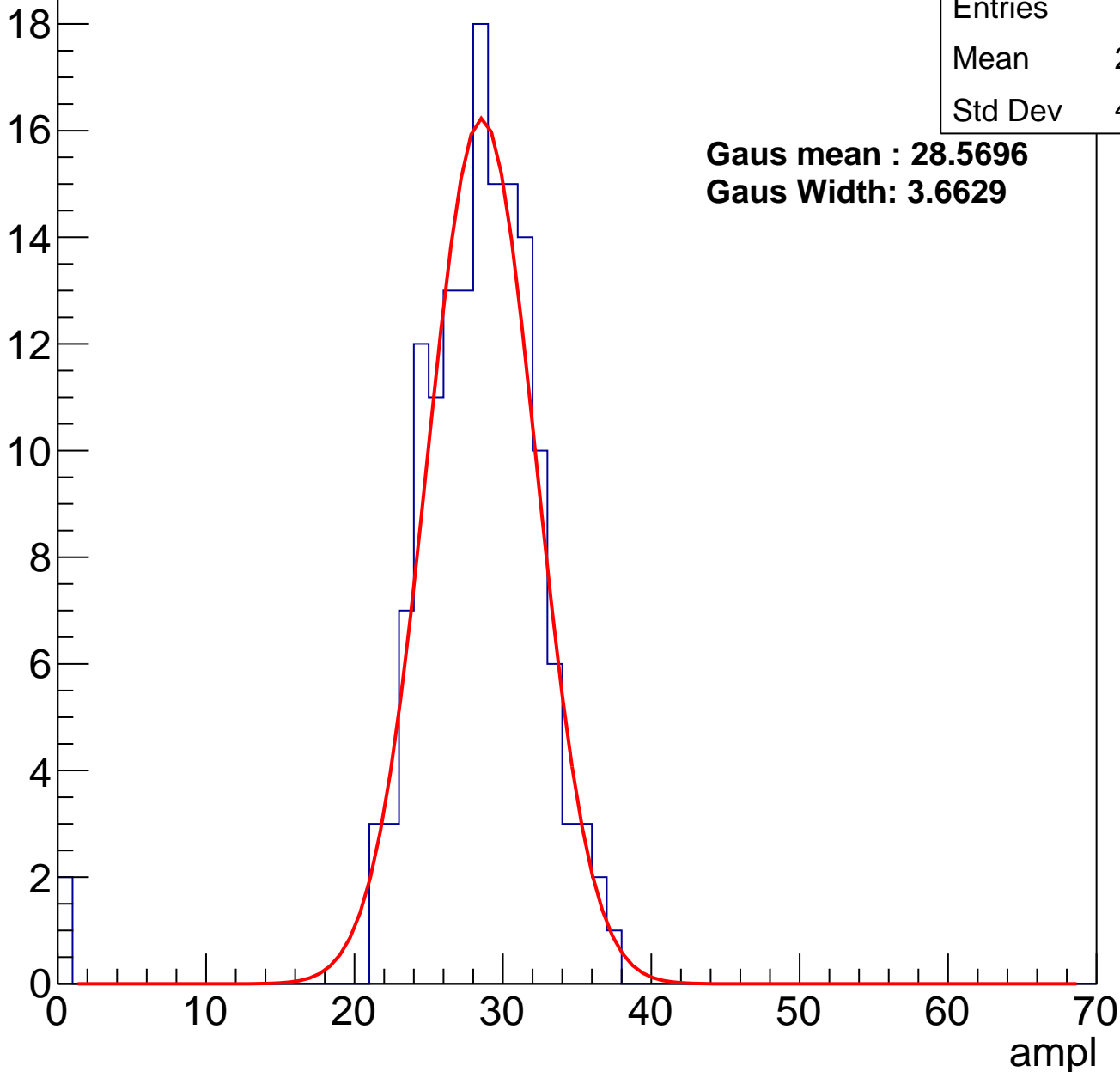
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	27.81
Std Dev	4.691

Gaus mean : 28.5696

Gaus Width: 3.6629

Entry



B0L101S, U24-ch105, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

159

Mean

35.64

Std Dev

3.838

Gaus mean : 36.2315

Gaus Width: 3.7471

0

10

20

30

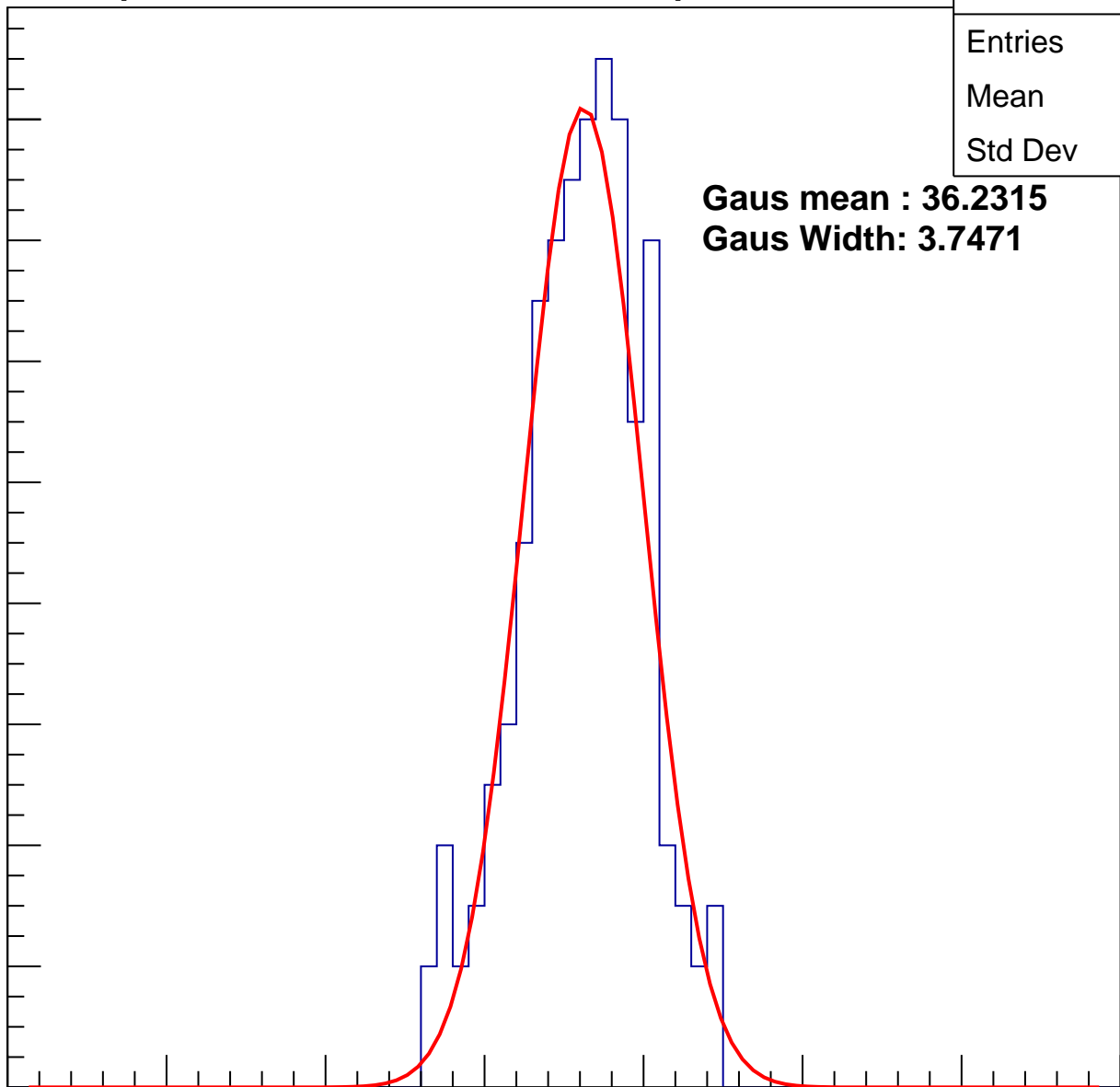
40

50

60

70

ampl



B0L101S, U24-ch105, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	42.75
Std Dev	3.817

Gaus mean : 43.5123

Gaus Width: 3.3887

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

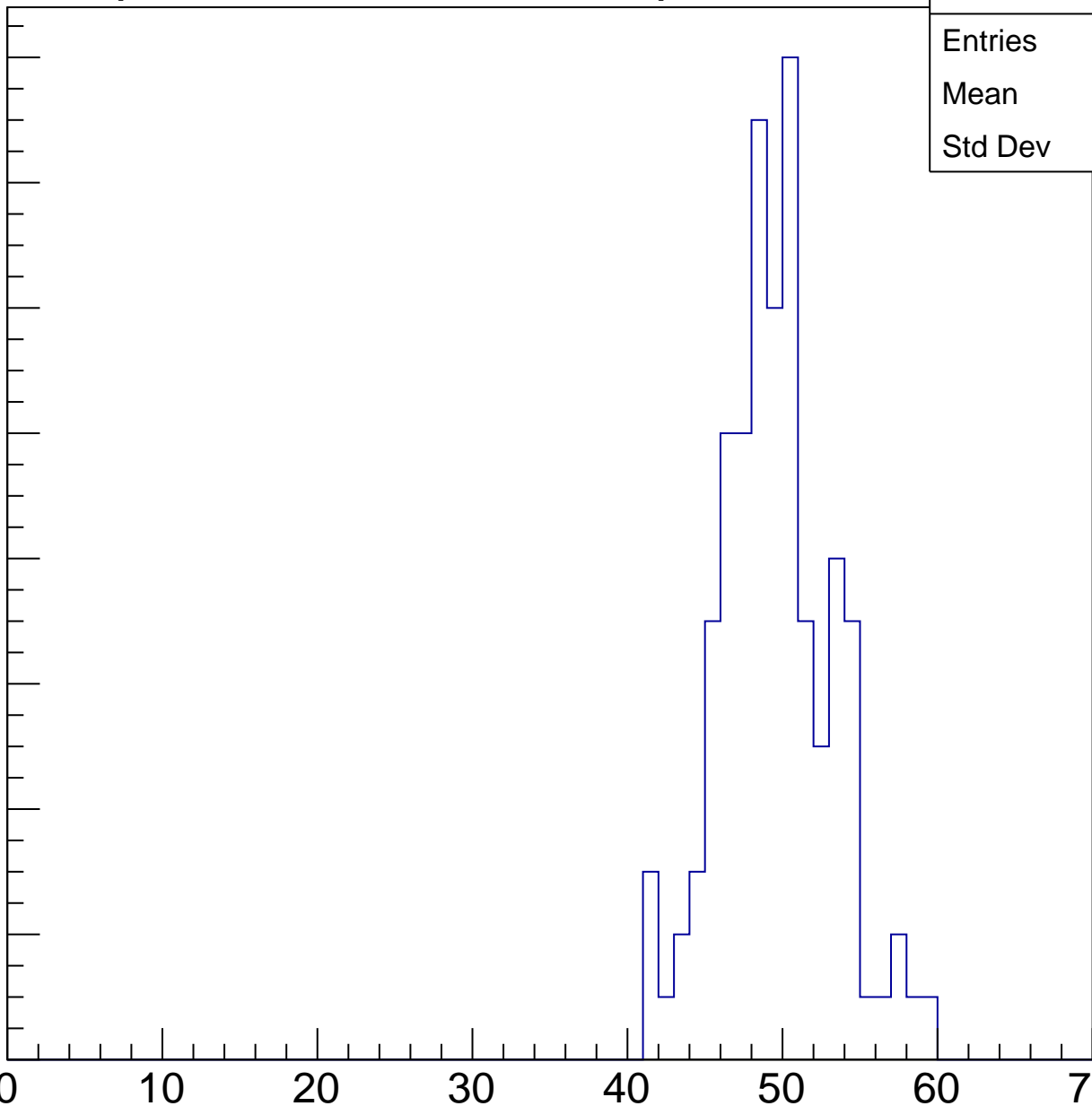
Entries	112
Mean	49.08
Std Dev	3.574

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

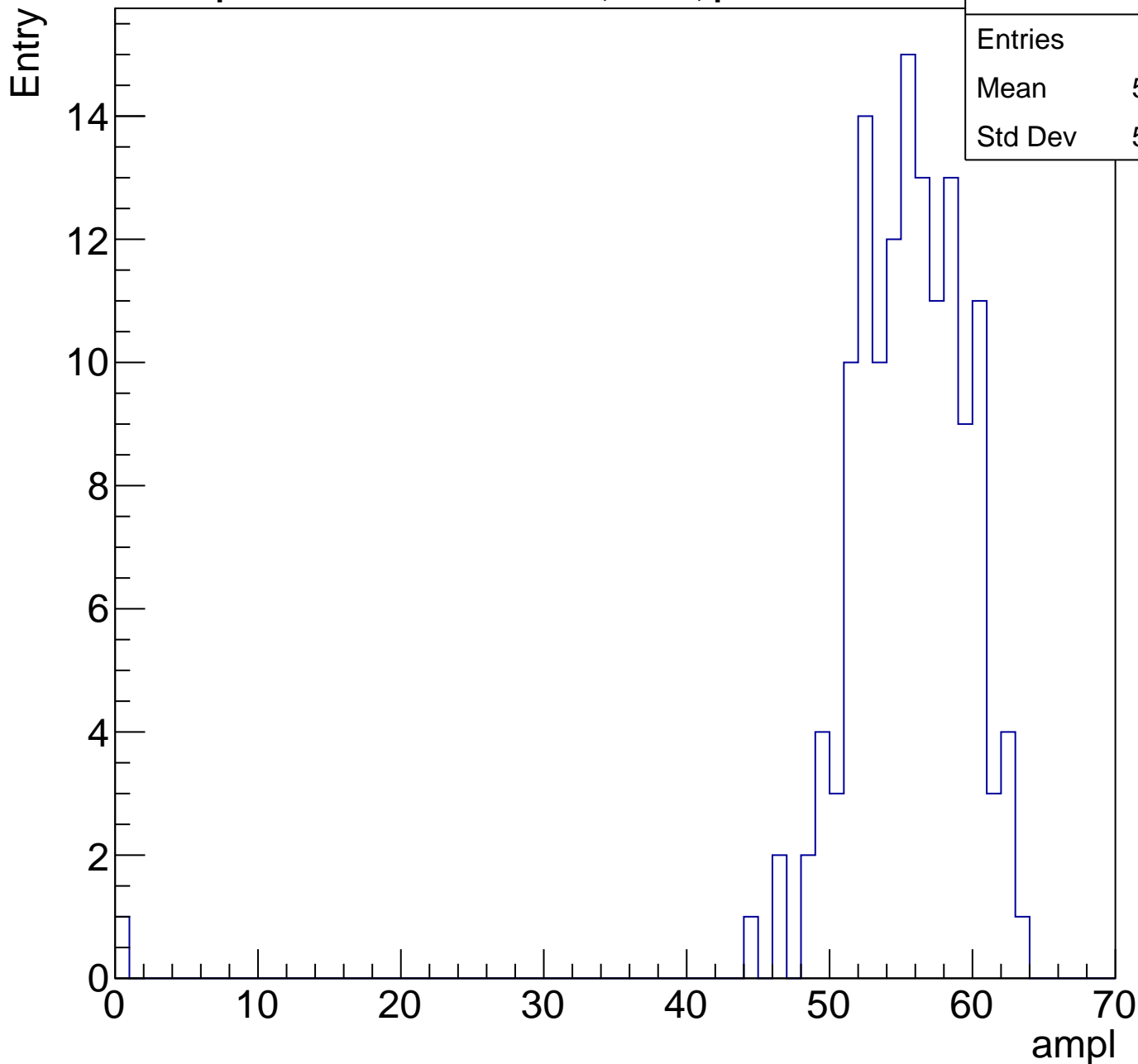
ampl



B0L101S, U24-ch105, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	54.77
Std Dev	5.947



B0L101S, U24-ch105, adc5

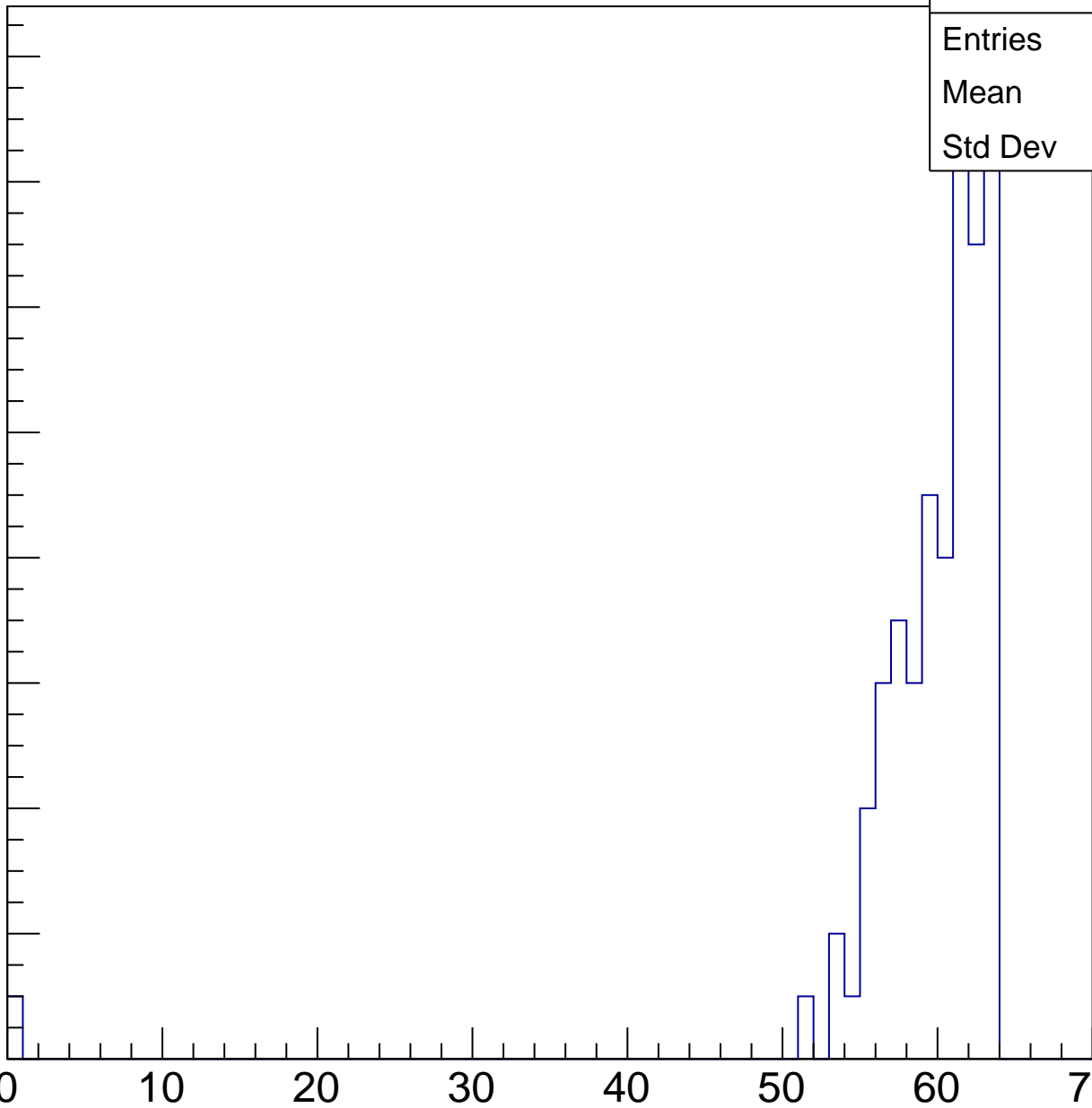
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.03
Std Dev	6.898

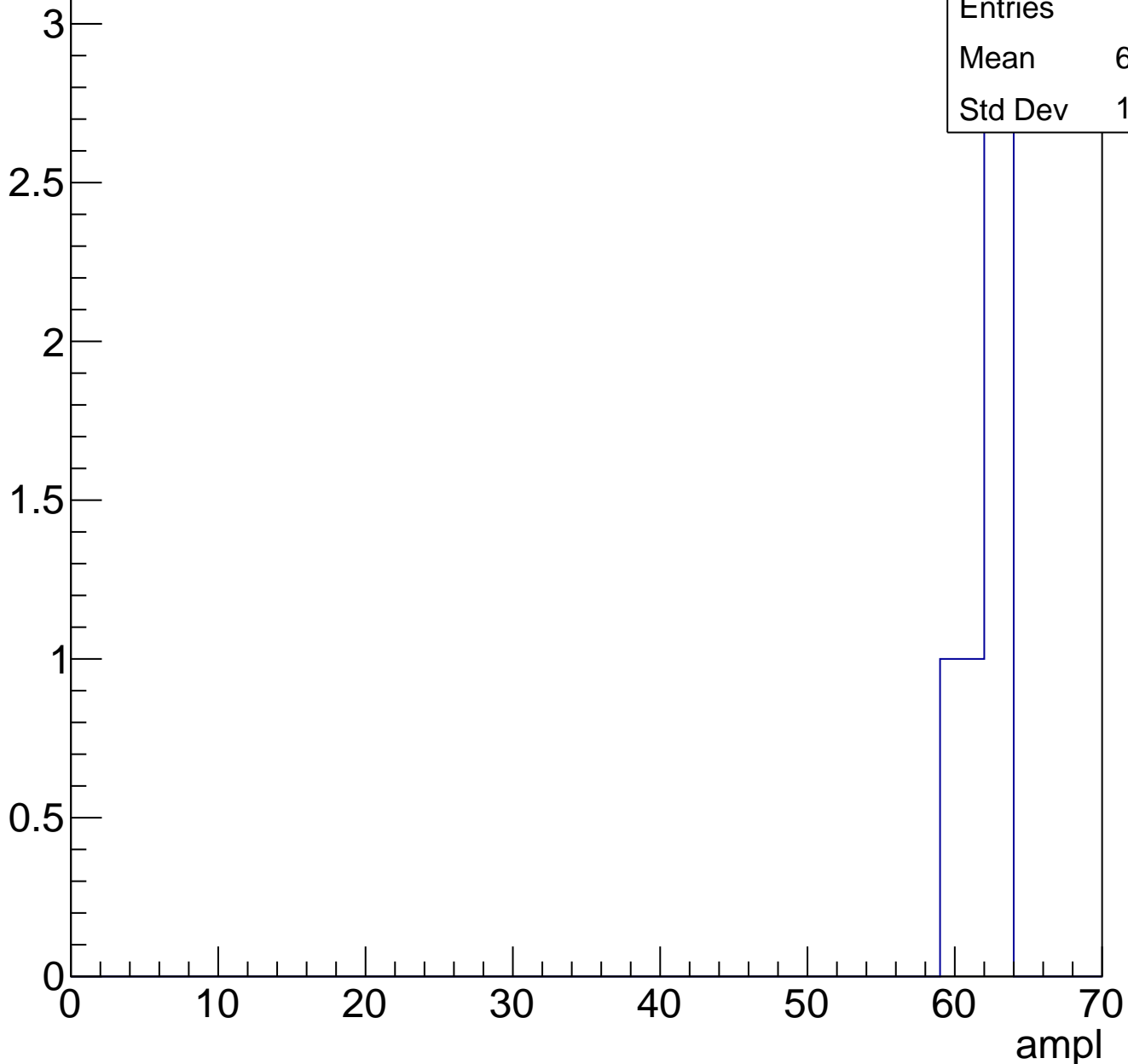
ampl



B0L101S, U24-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

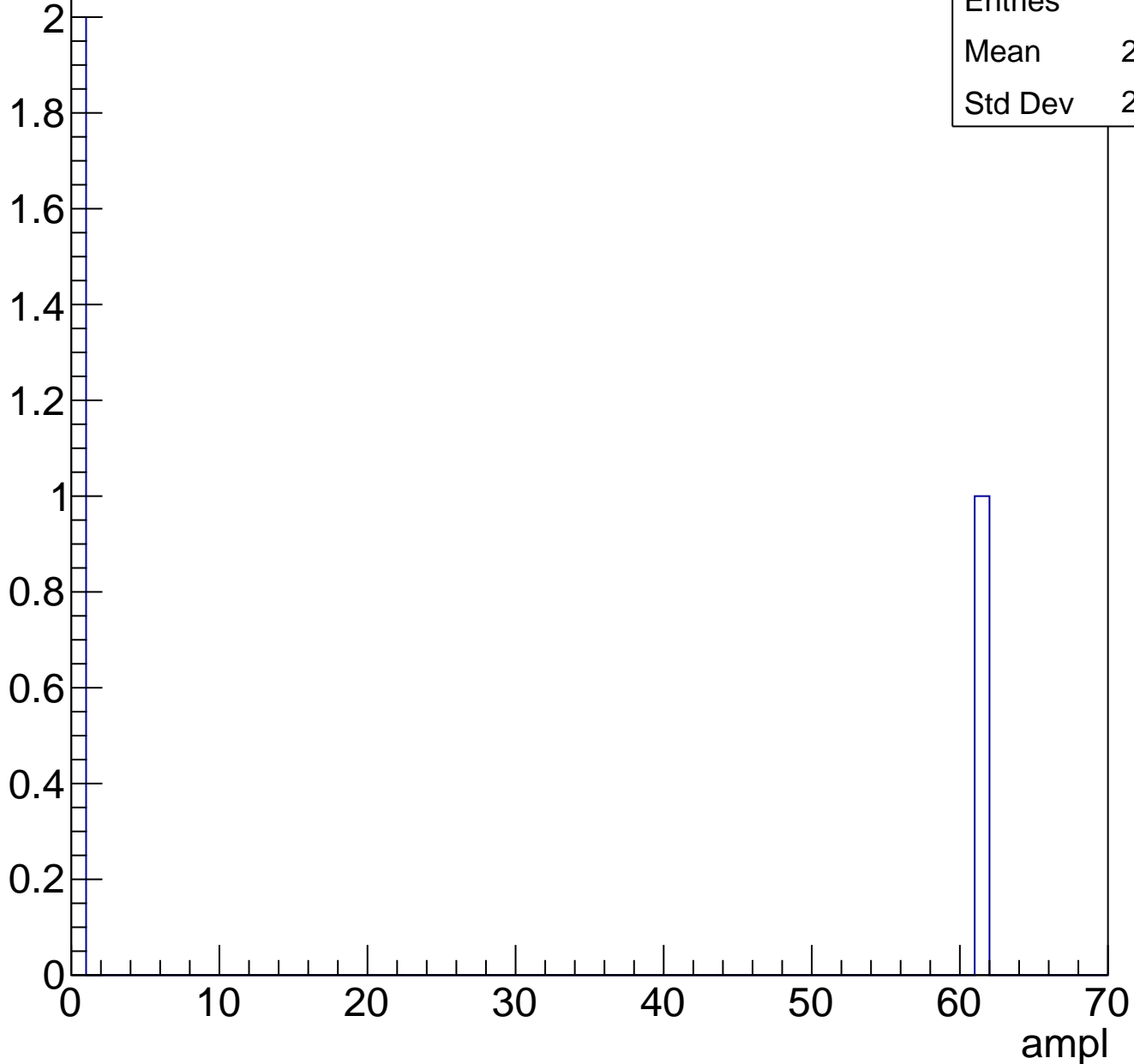


Entries	9
Mean	61.67
Std Dev	1.333

B0L101S, U24-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch106, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.49
Std Dev	5.74

Gaus mean : 30.2504

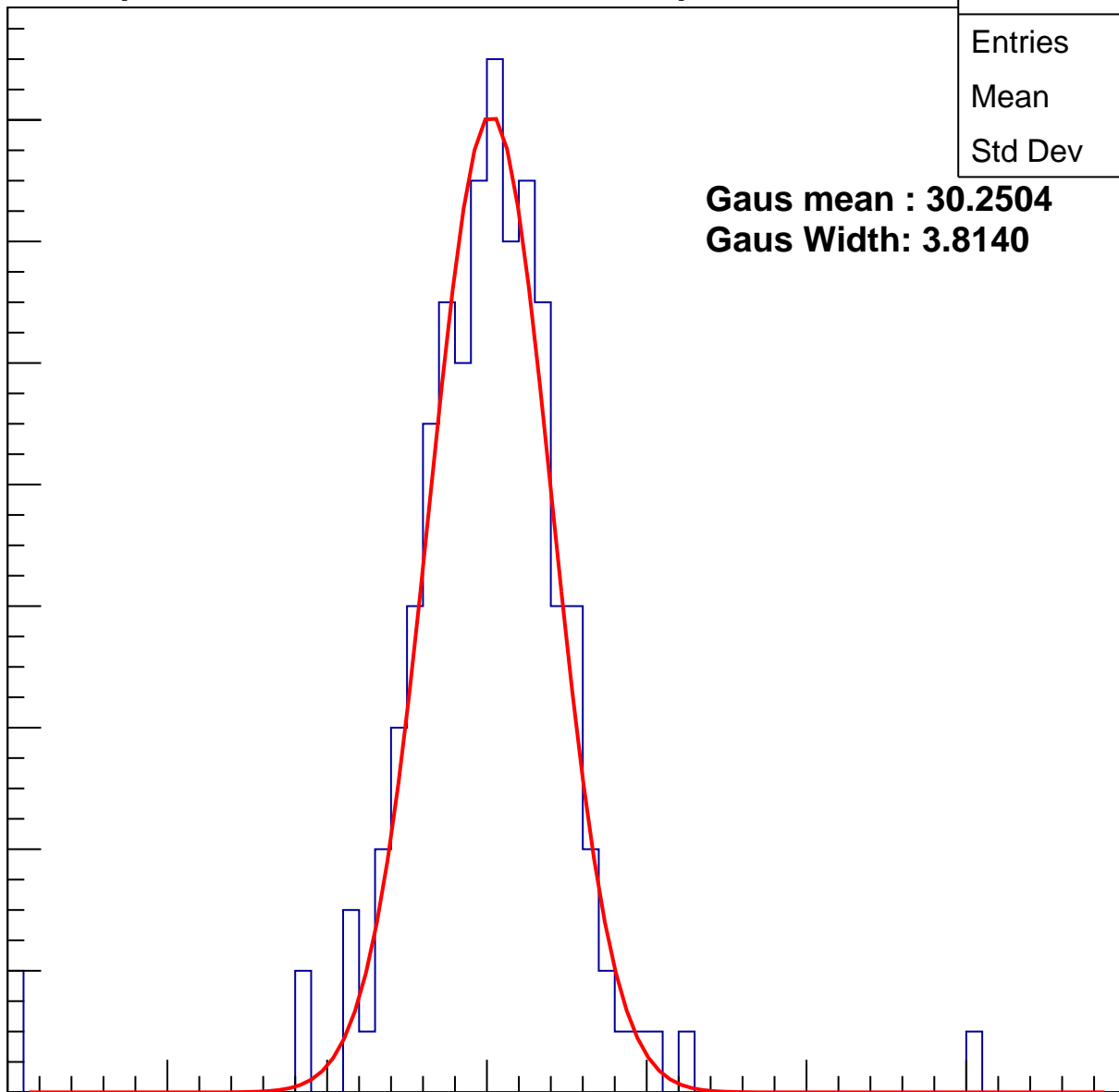
Gaus Width: 3.8140

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch106, adc1

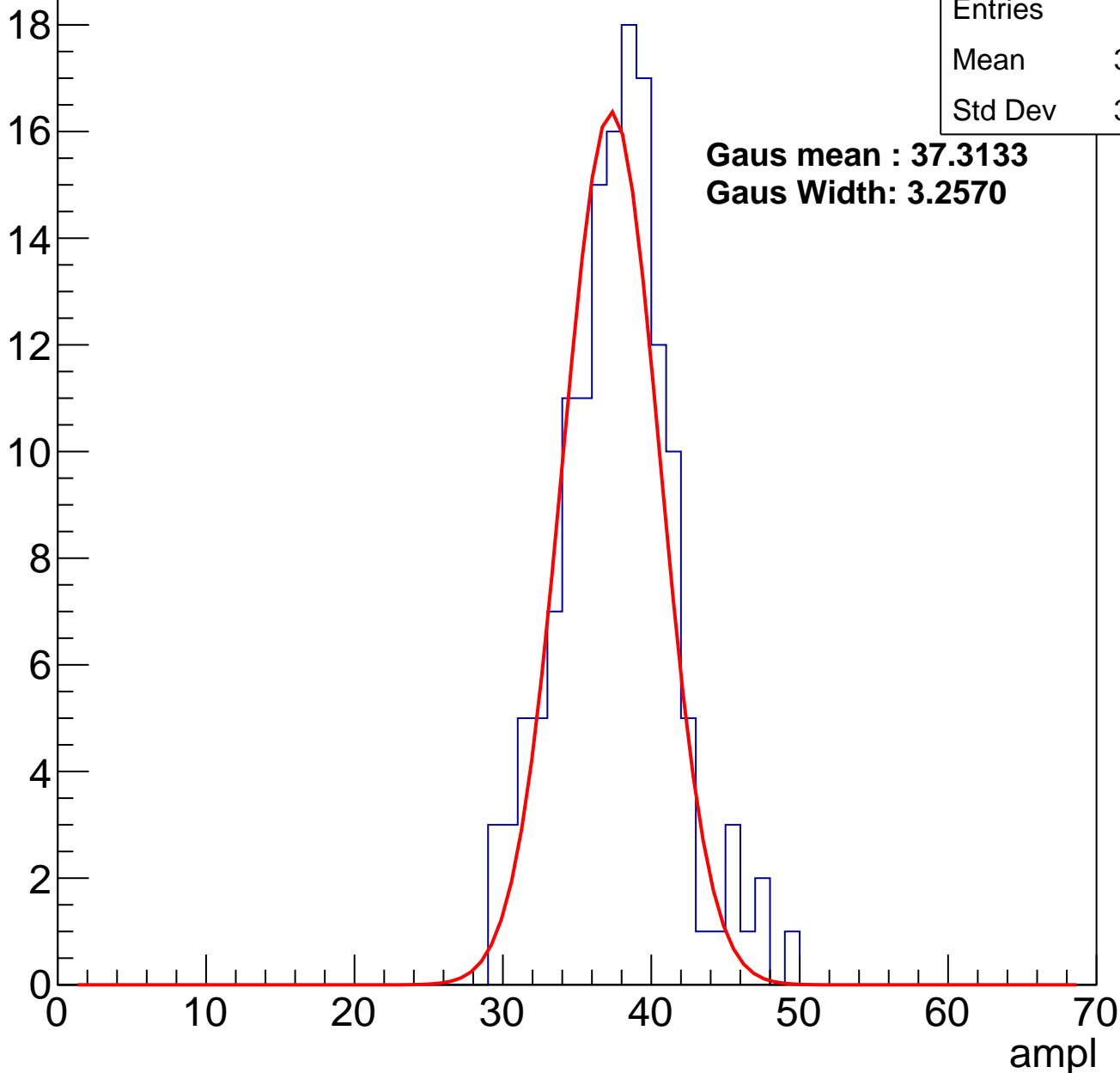
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	37.22
Std Dev	3.768

Gaus mean : 37.3133

Gaus Width: 3.2570

Entry



B0L101S, U24-ch106, adc2

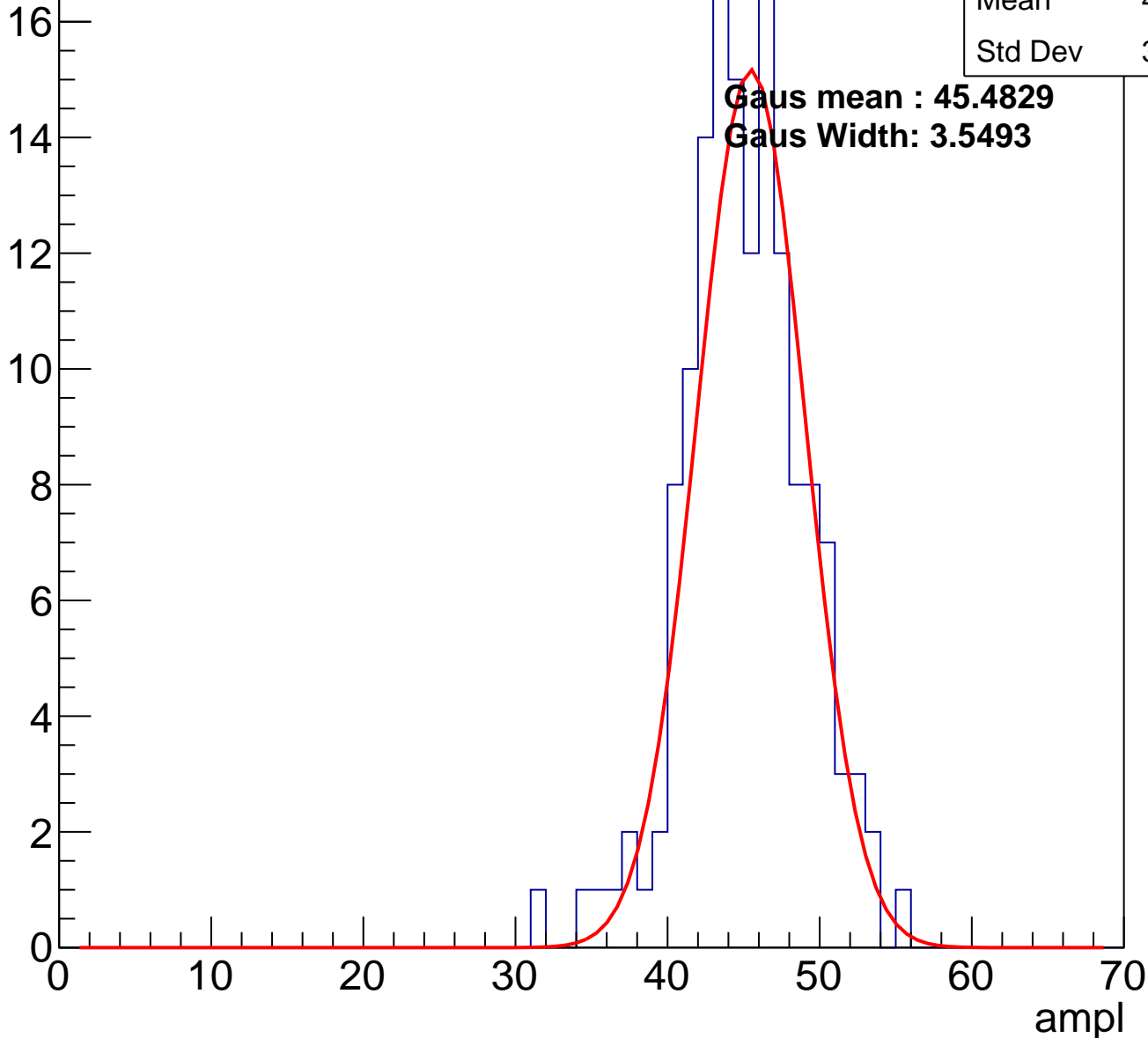
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	146
Mean	44.64
Std Dev	3.899

Gaus mean : 45.4829

Gaus Width: 3.5493



B0L101S, U24-ch106, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	84
Mean	50.15
Std Dev	3.614

B0L101S, U24-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	55.64
Std Dev	3.832

Entry

12

10

8

6

4

2

0

0

10

20

30

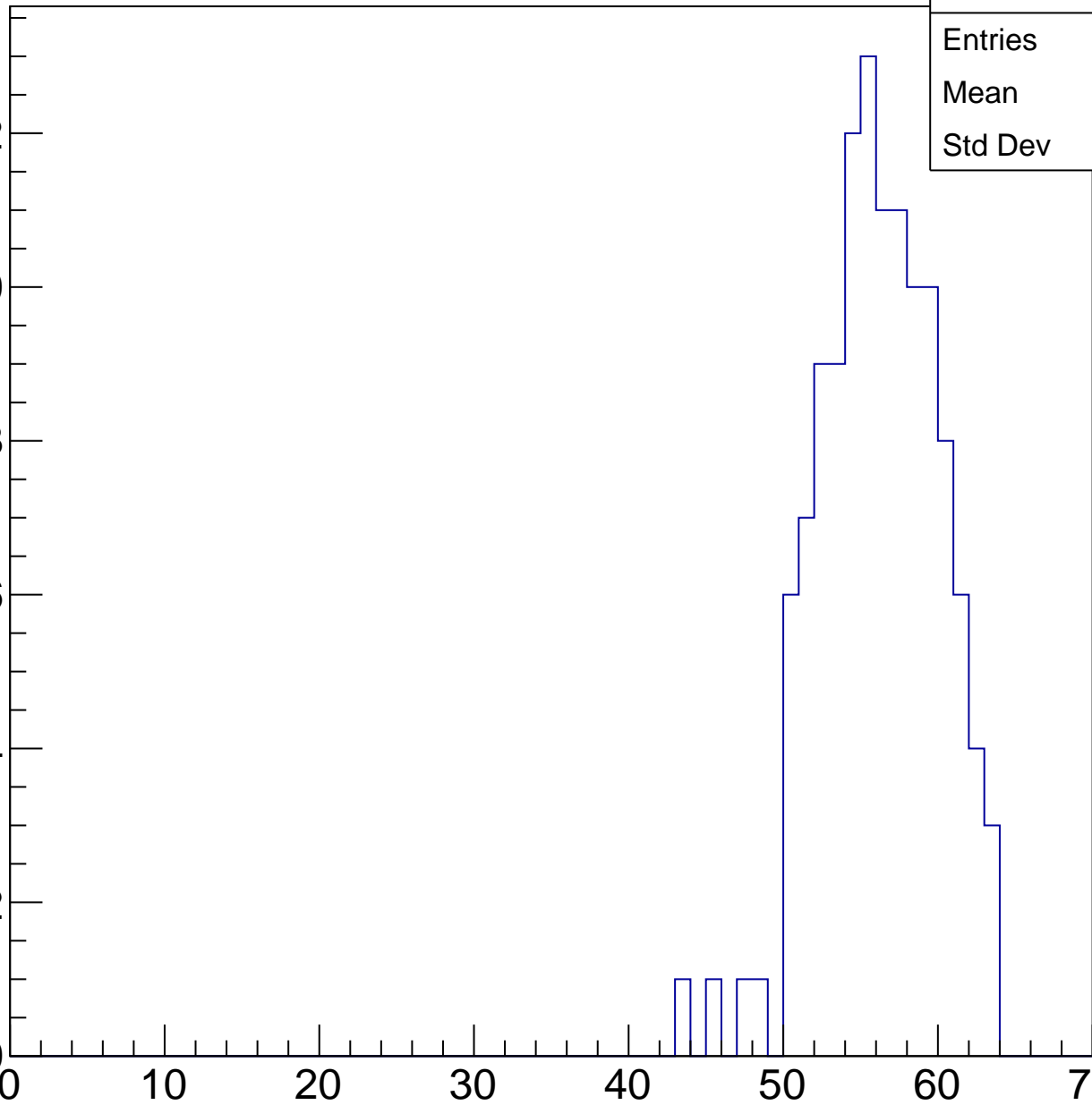
40

50

60

70

ampl



B0L101S, U24-ch106, adc5

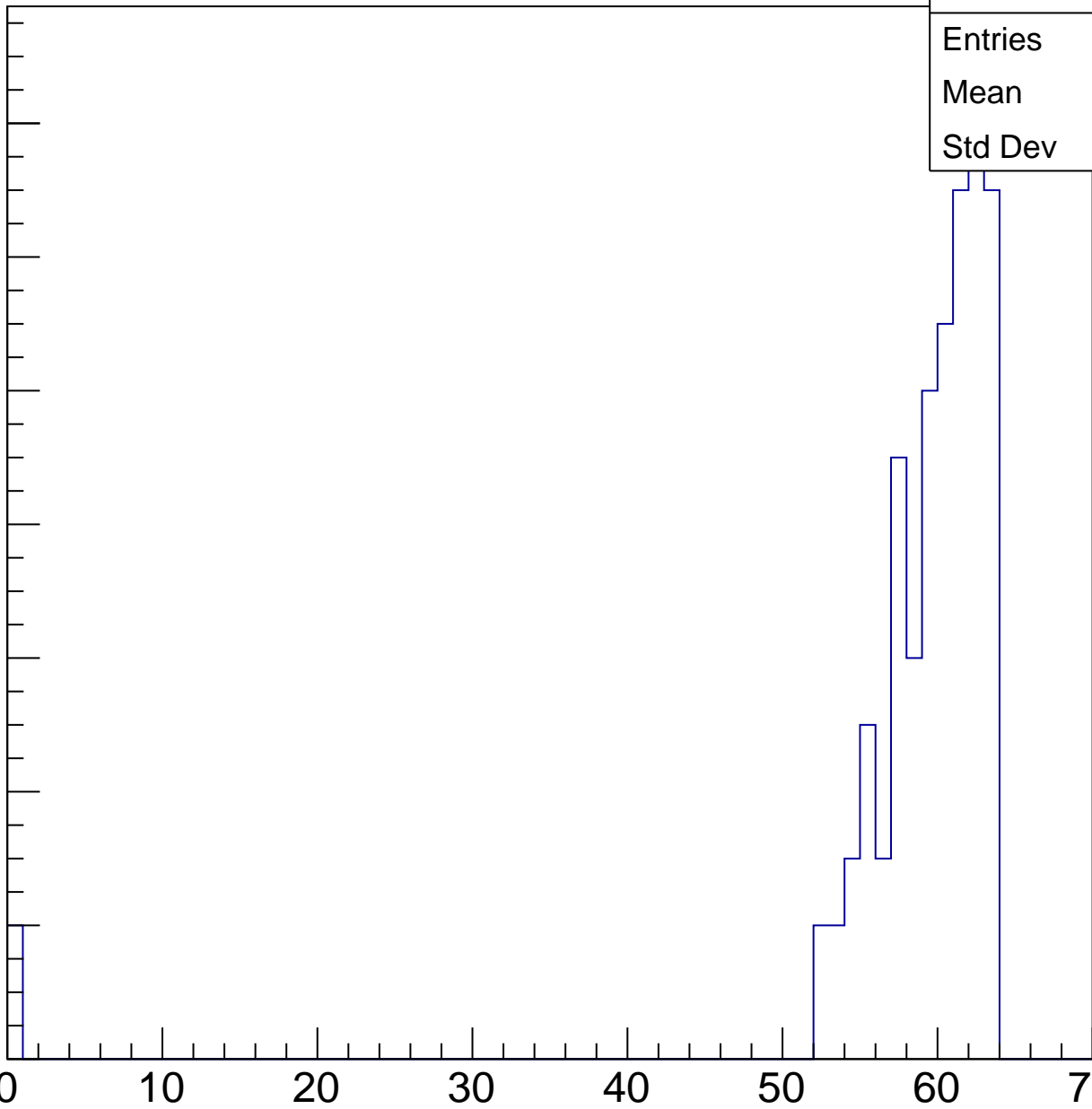
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	58.17
Std Dev	9.049

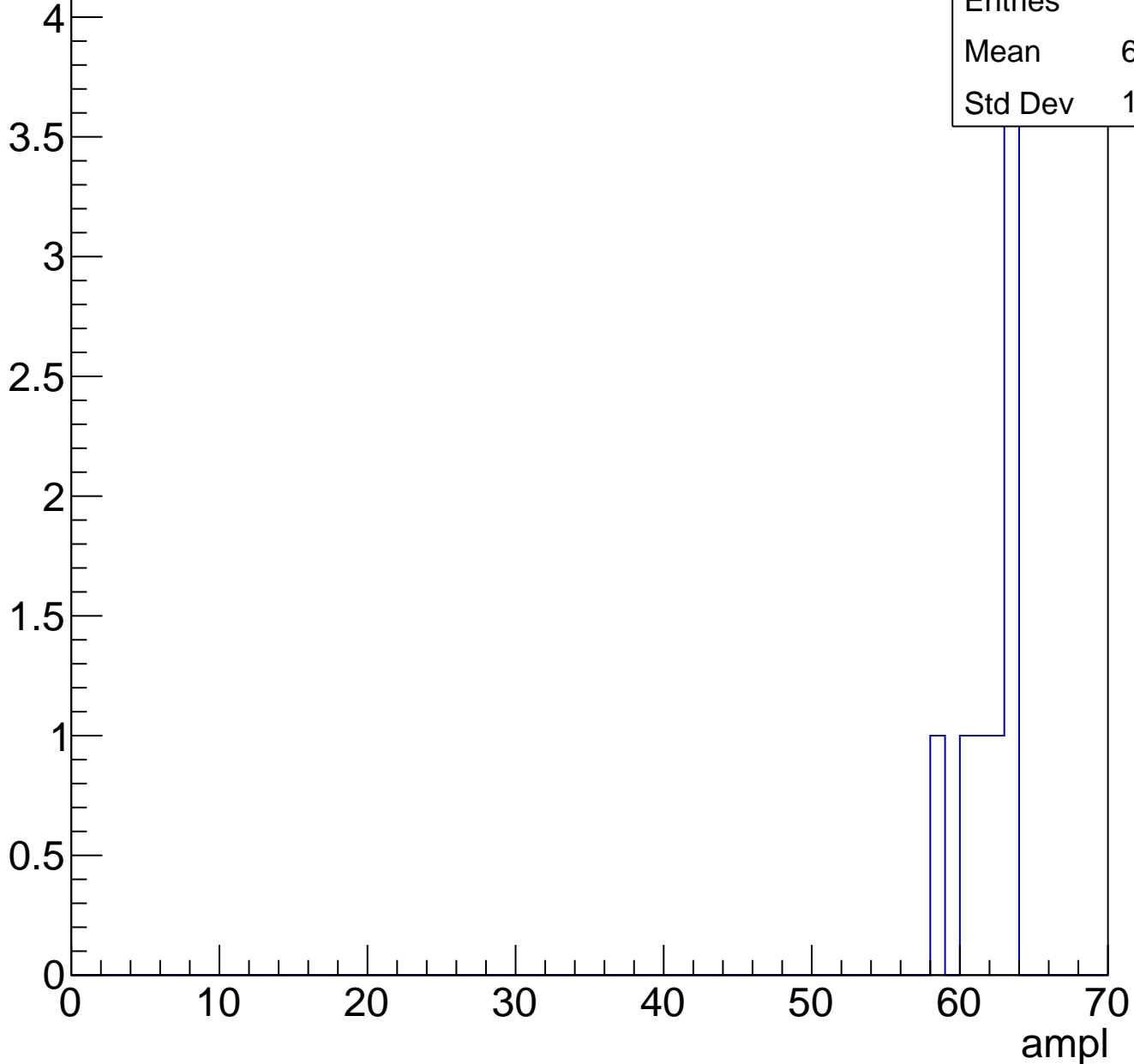
ampl



B0L101S, U24-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

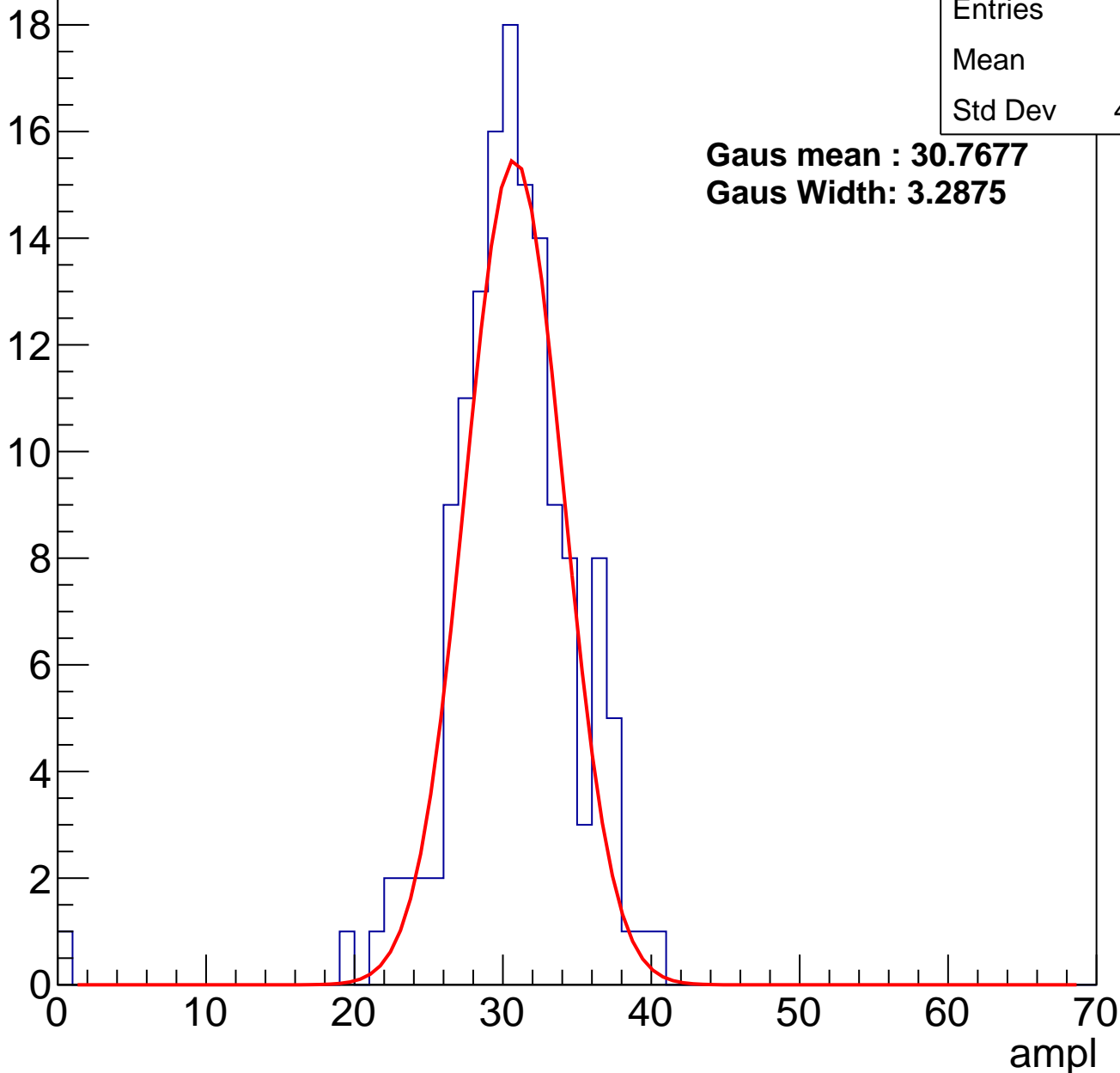
B0L101S, U24-ch107, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	30.1
Std Dev	4.498

Gaus mean : 30.7677
Gaus Width: 3.2875

Entry



B0L101S, U24-ch107, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	38.03
Std Dev	3.704

Gaus mean : 38.2142

Gaus Width: 3.9704

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

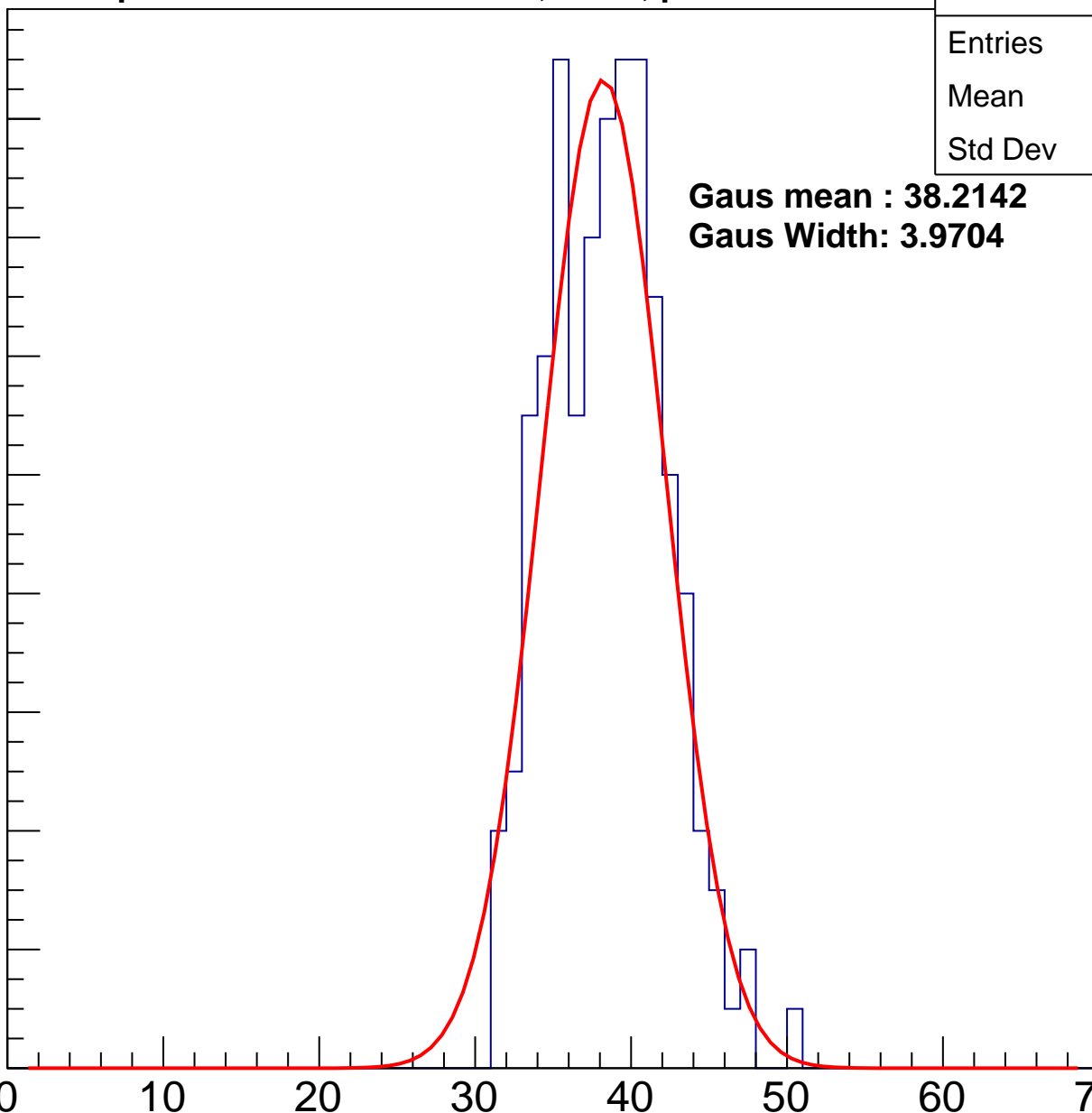
40

50

60

70

ampl



B0L101S, U24-ch107, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

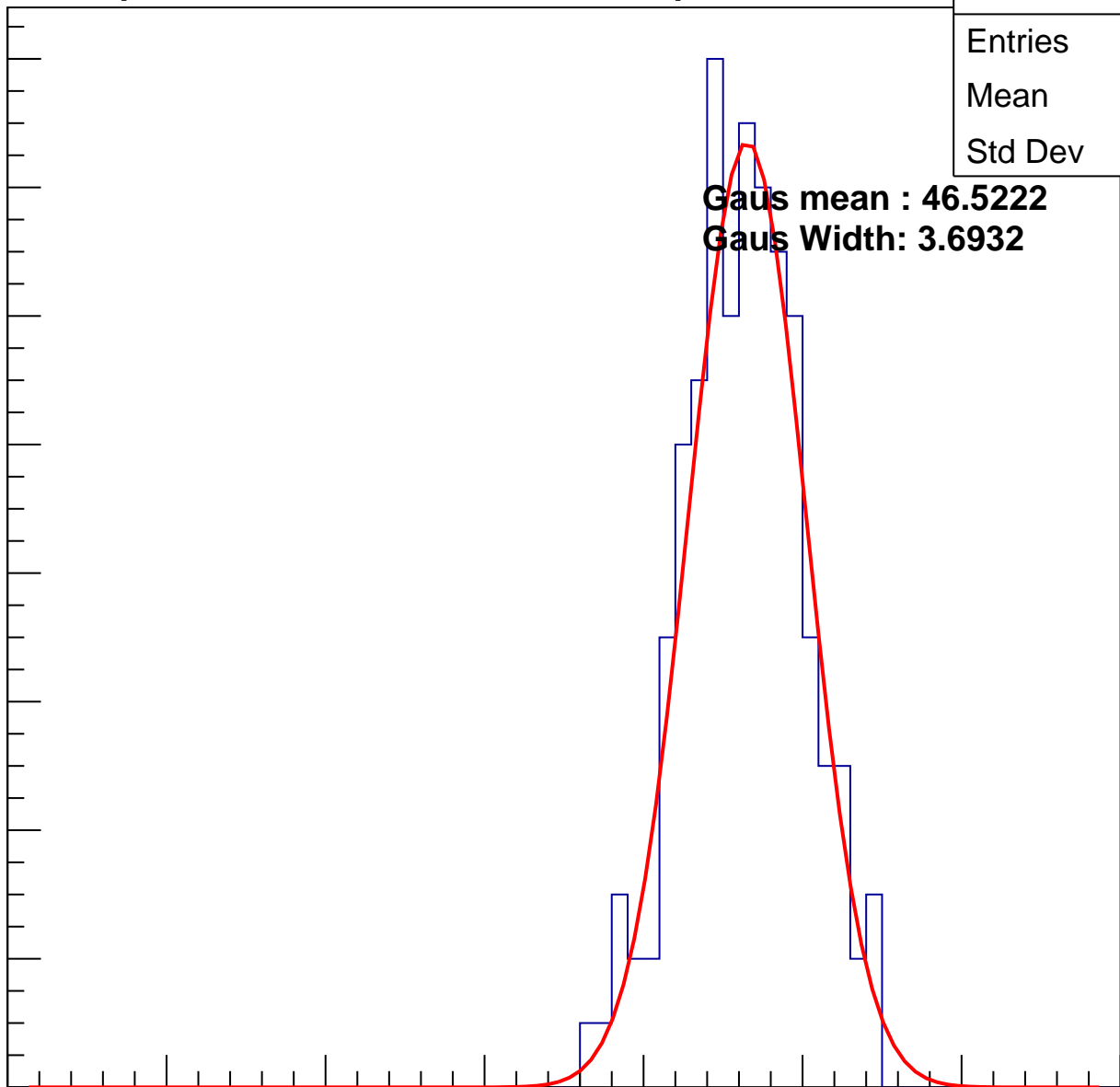
Entries	141
Mean	45.83
Std Dev	3.7

Gaus mean : 46.5222

Gaus Width: 3.6932

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch107, adc3

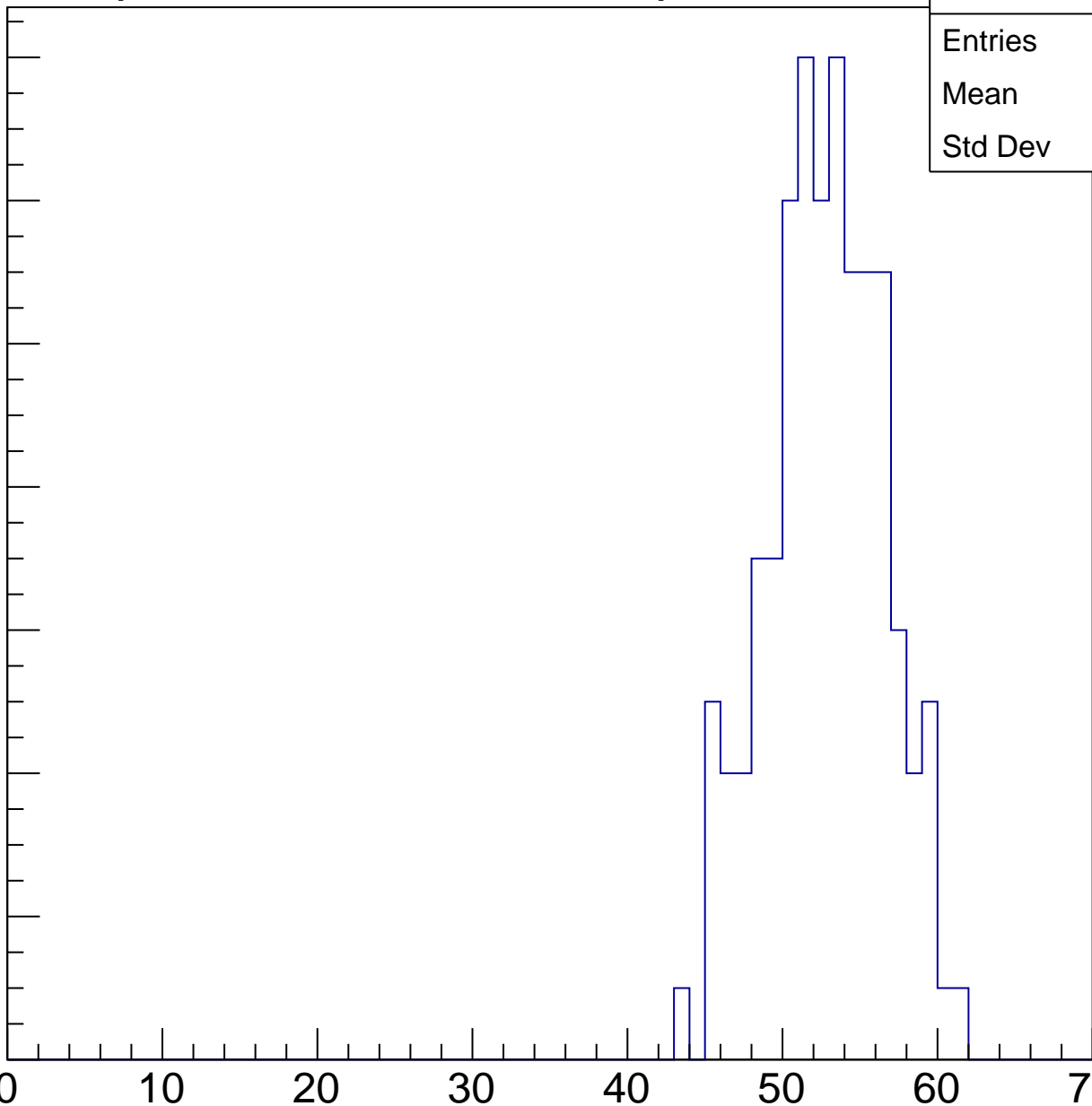
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	130
Mean	52.34
Std Dev	3.73

ampl



B0L101S, U24-ch107, adc4

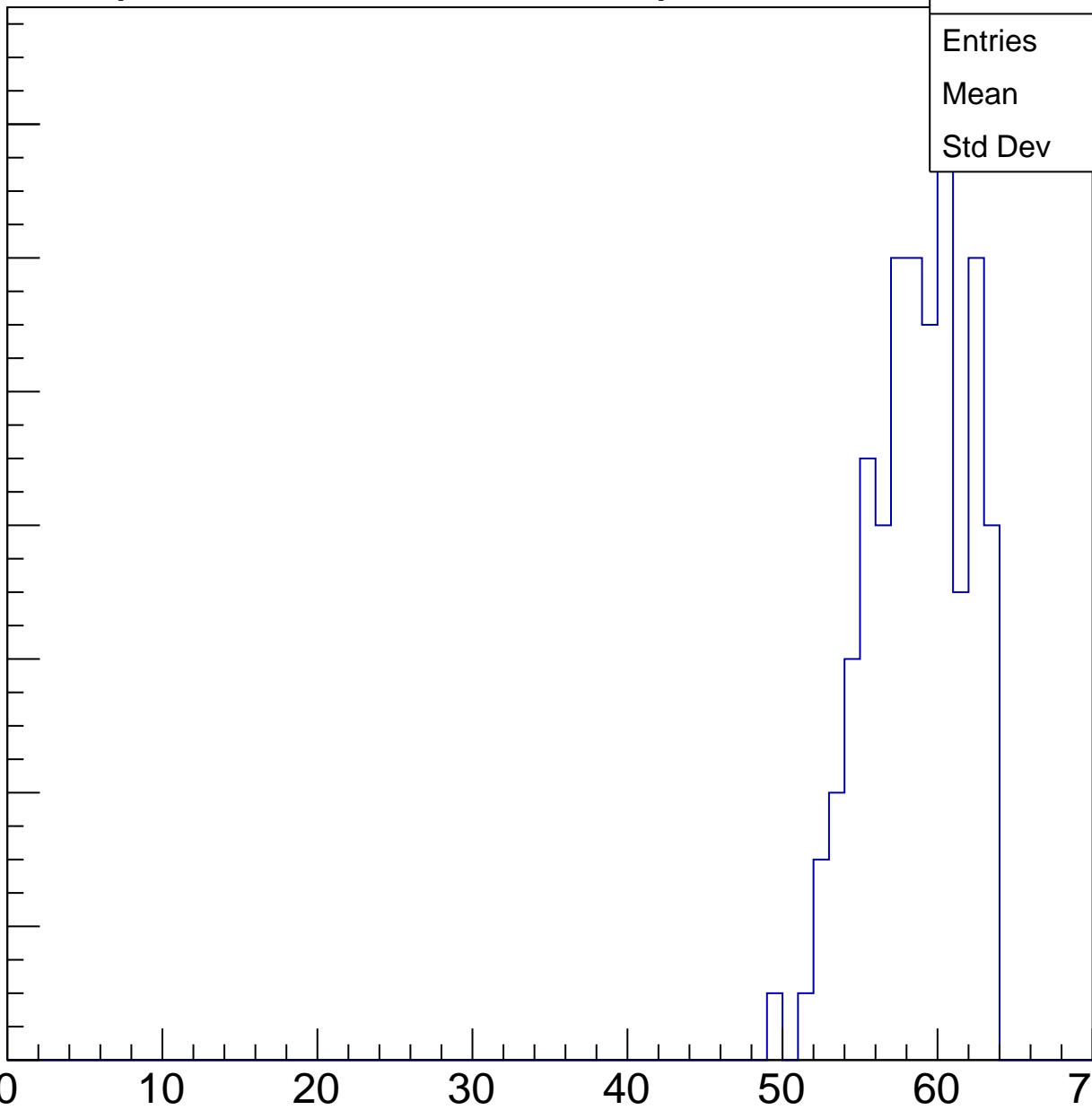
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	58.16
Std Dev	3.151

ampl



B0L101S, U24-ch107, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

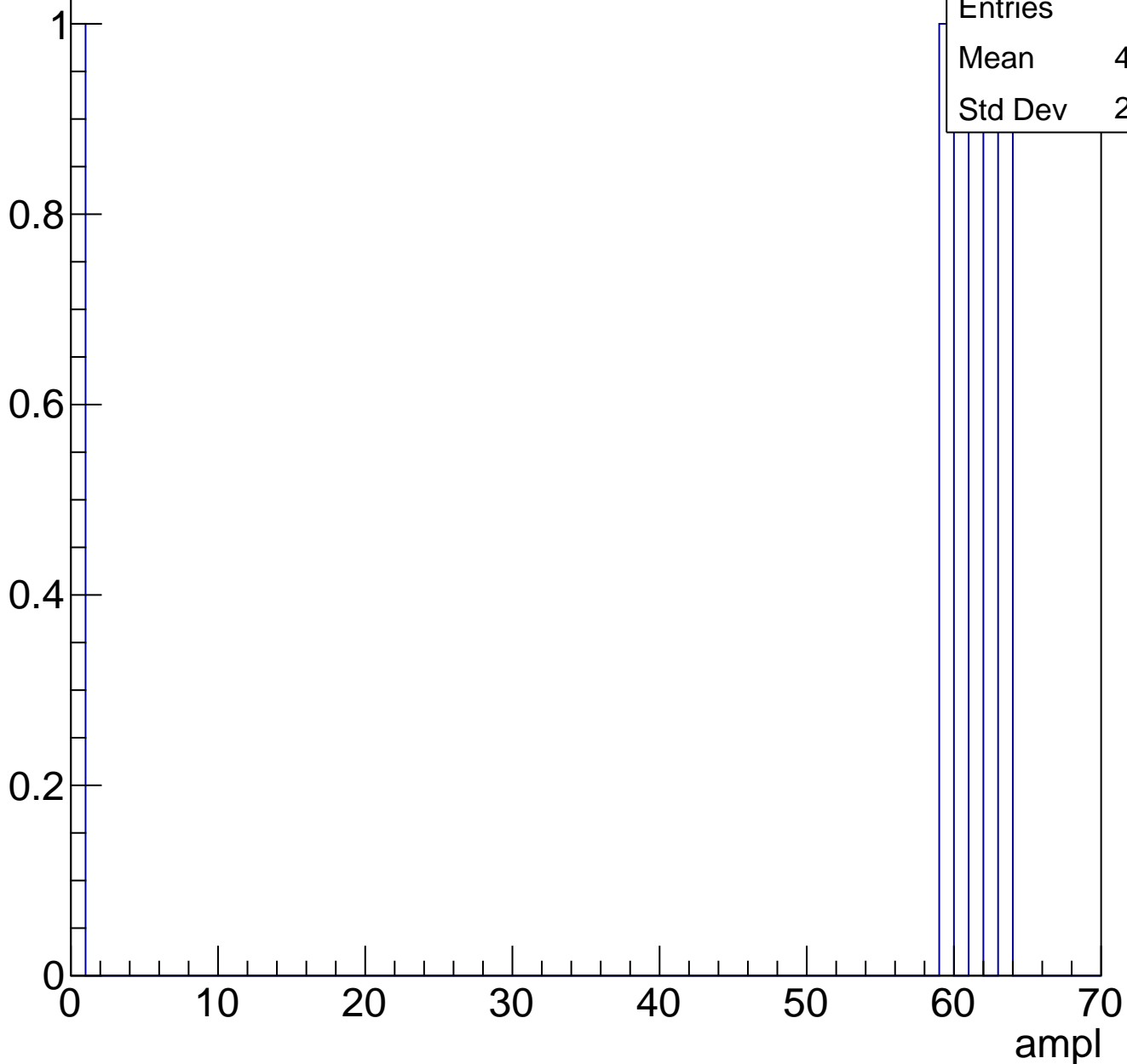
ampl

Entries	45
Mean	59.53
Std Dev	9.171

B0L101S, U24-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch108, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	28.38
Std Dev	5.271

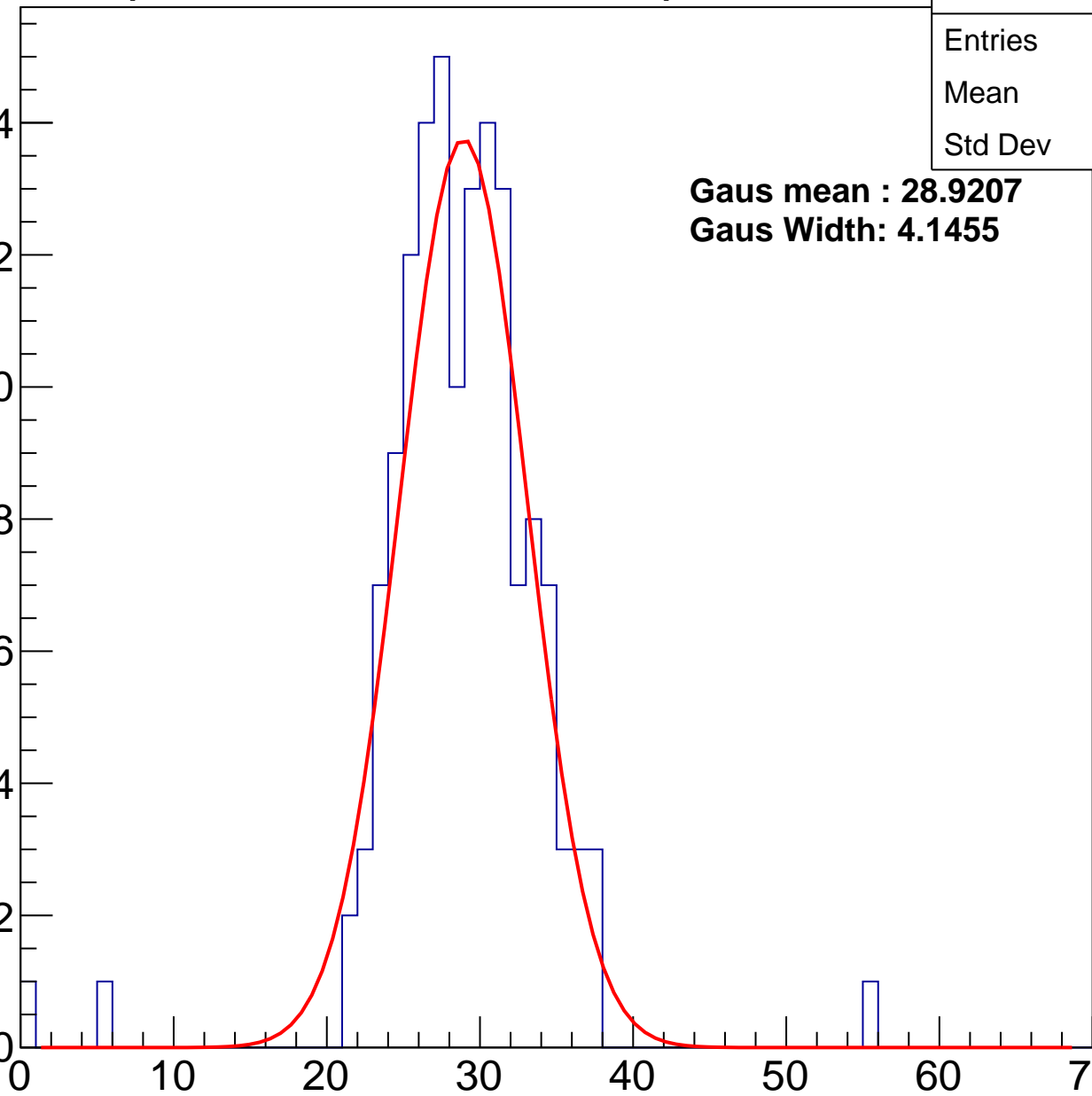
Gaus mean : 28.9207

Gaus Width: 4.1455

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch108, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

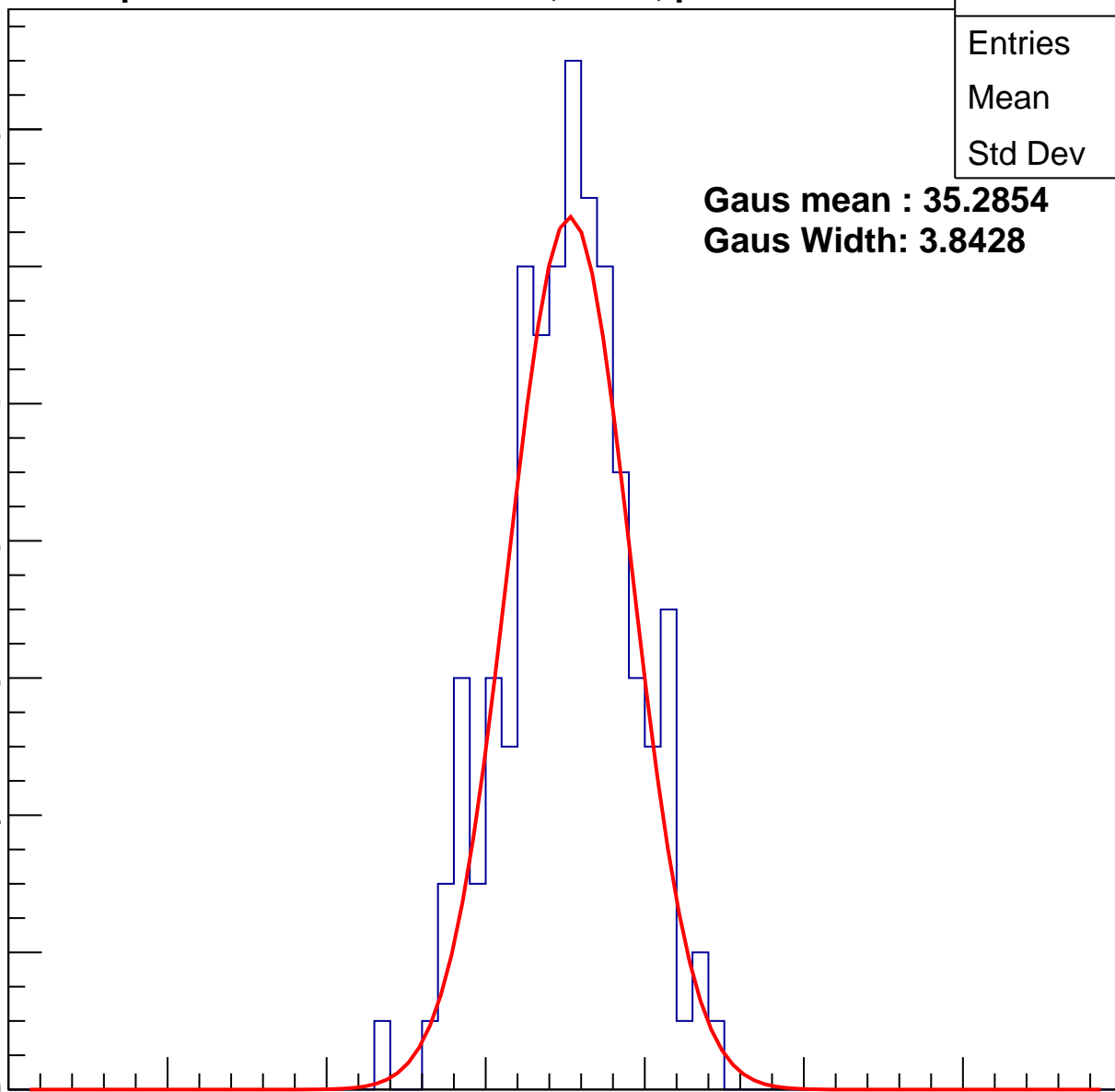
70

ampl

Entries	131
Mean	34.71
Std Dev	3.98

Gaus mean : 35.2854

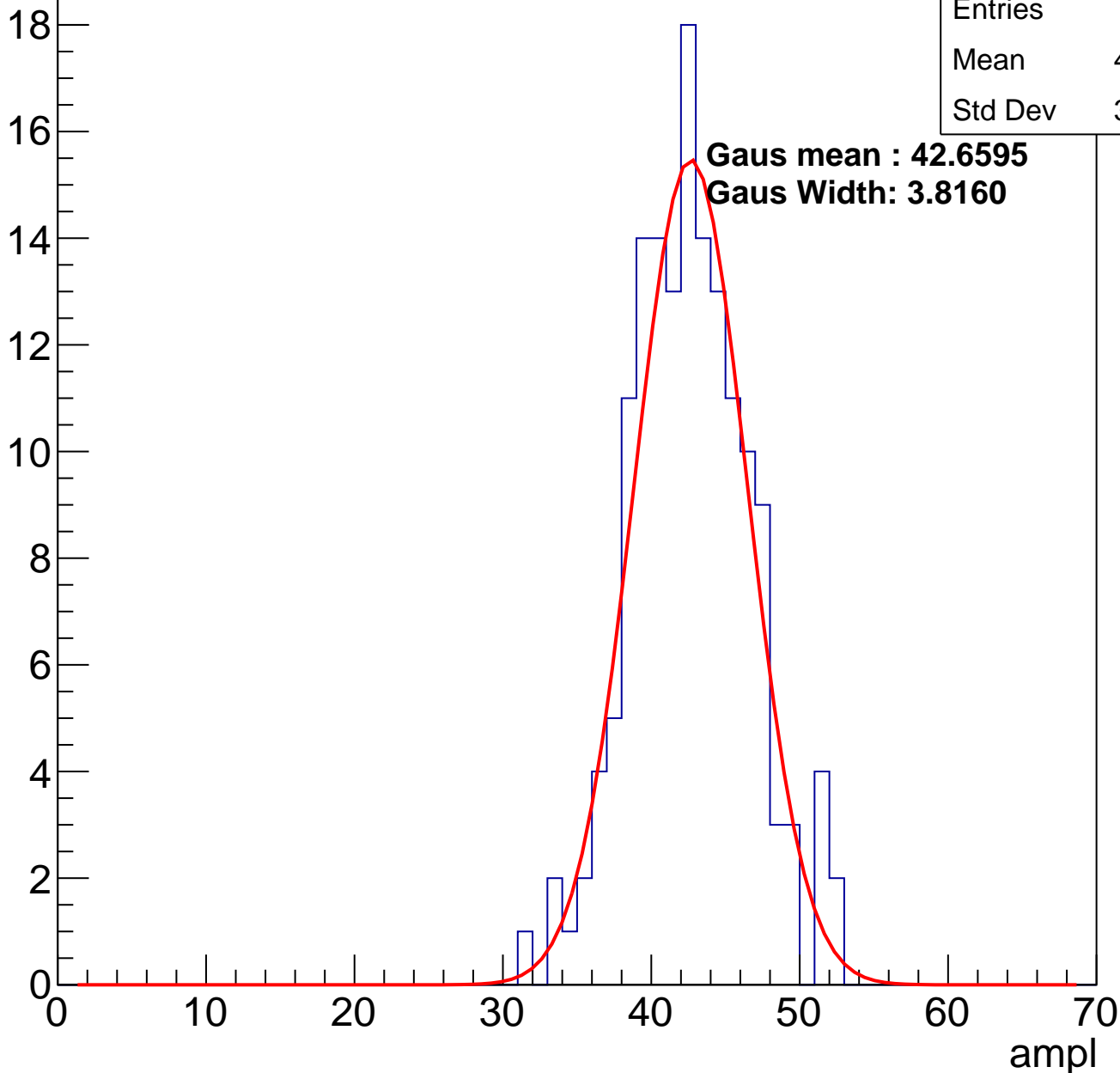
Gaus Width: 3.8428



B0L101S, U24-ch108, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch108, adc3

calib_packv5_042523_0143.root, FC#1, port C1

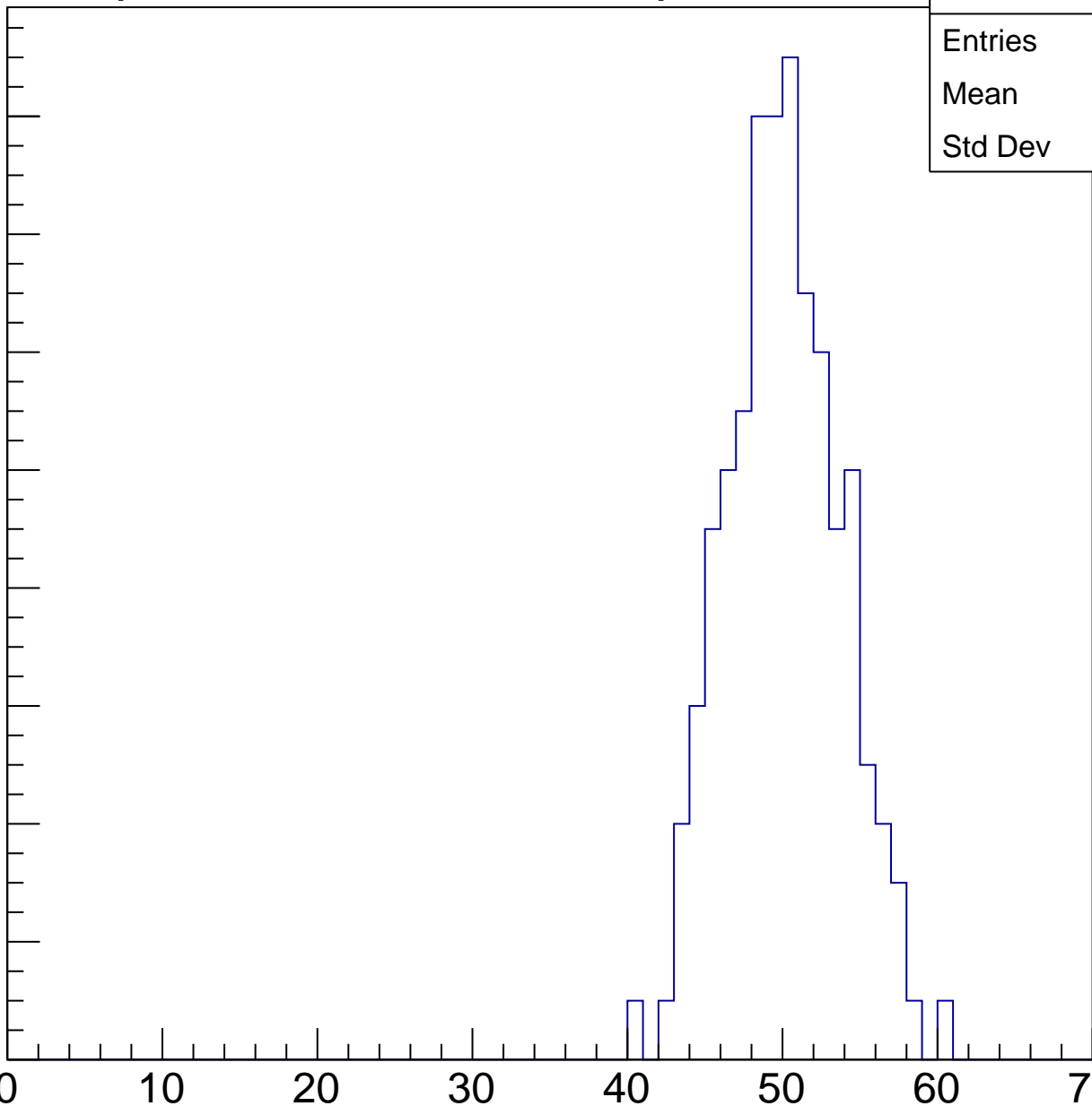
Entries	149
Mean	49.62
Std Dev	3.665

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch108, adc4

calib_packv5_042523_0143.root, FC#1, port C1

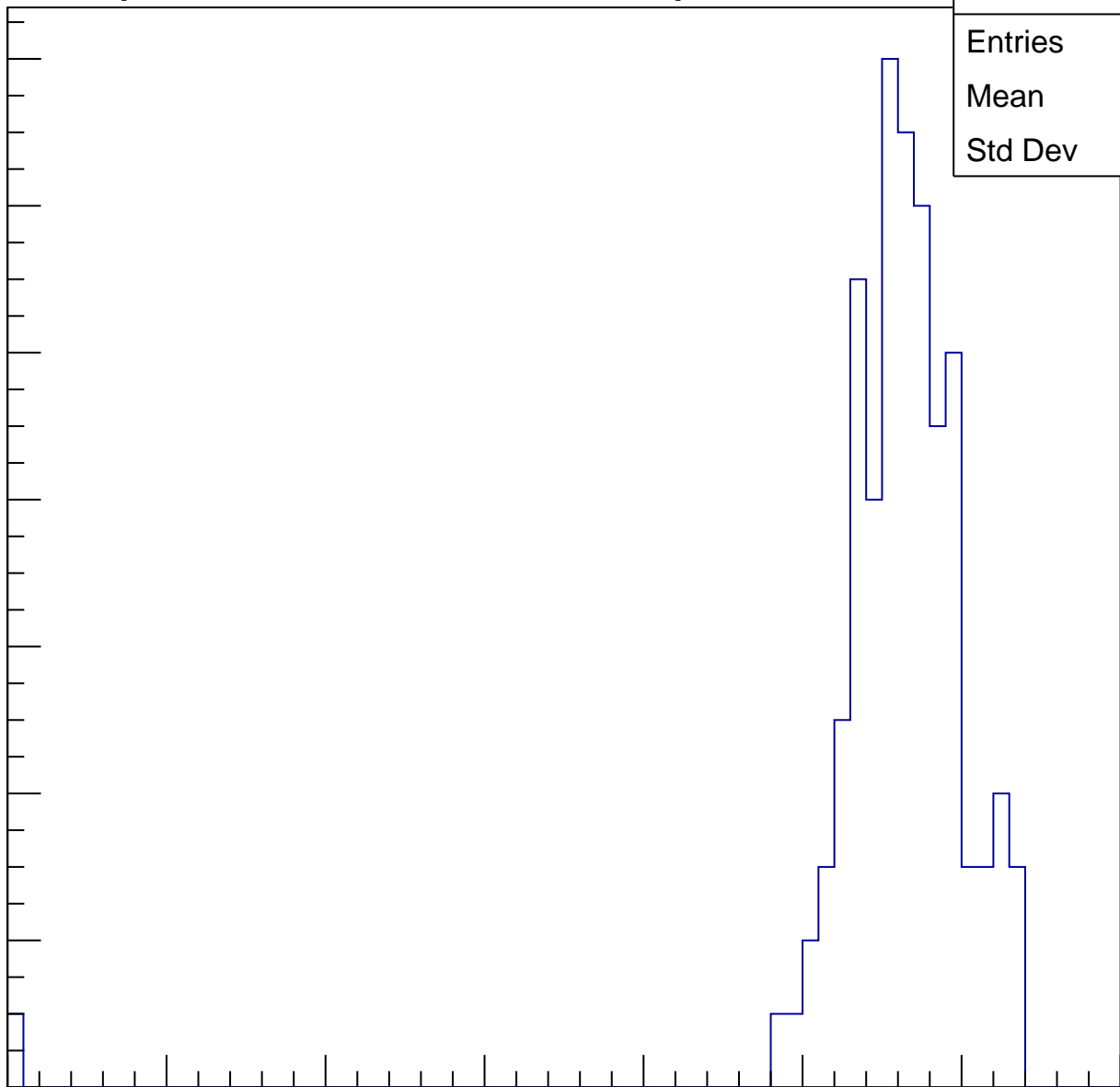
Entries	103
Mean	55.52
Std Dev	6.347

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch108, adc5

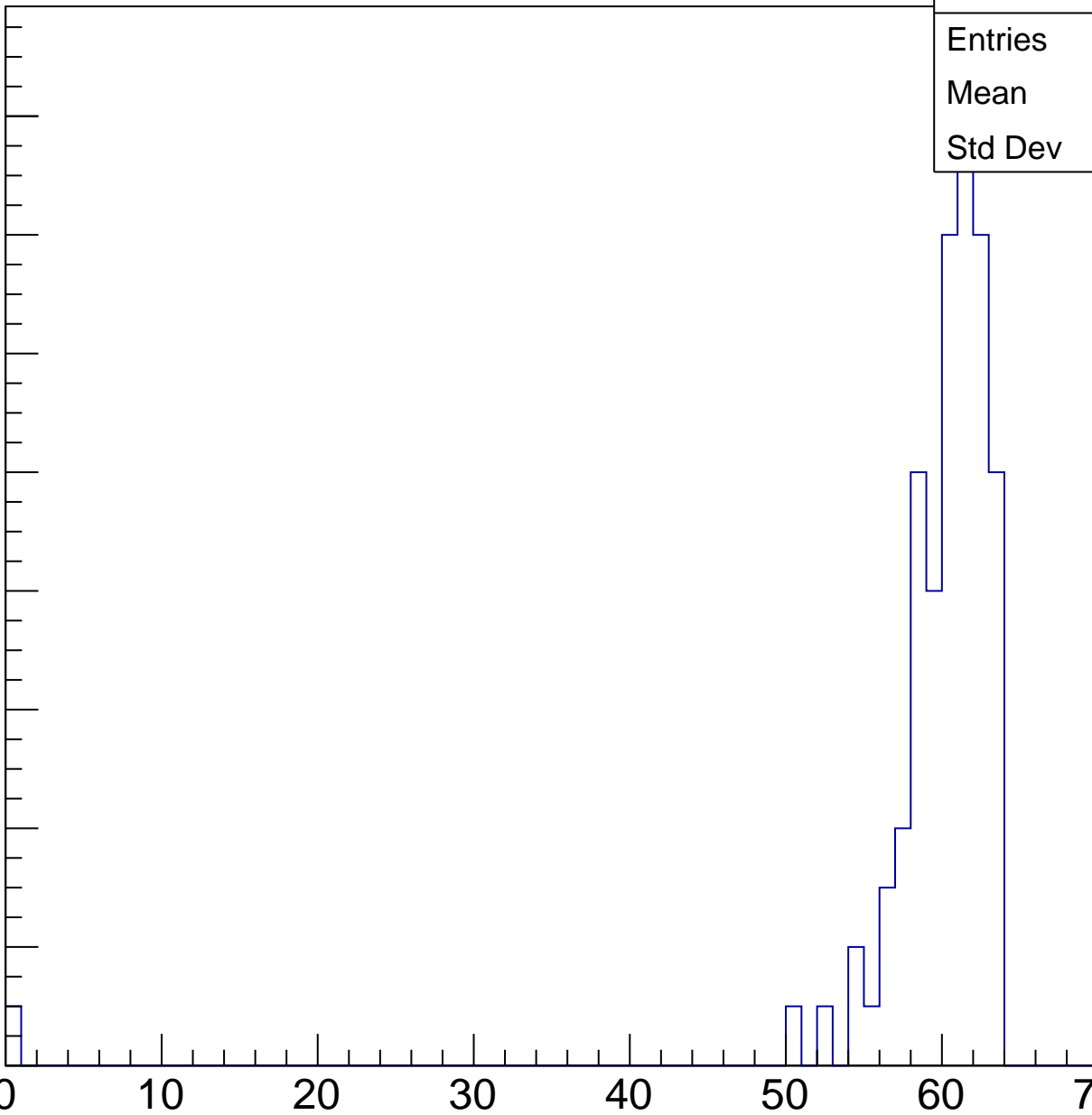
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.16
Std Dev	6.901

ampl

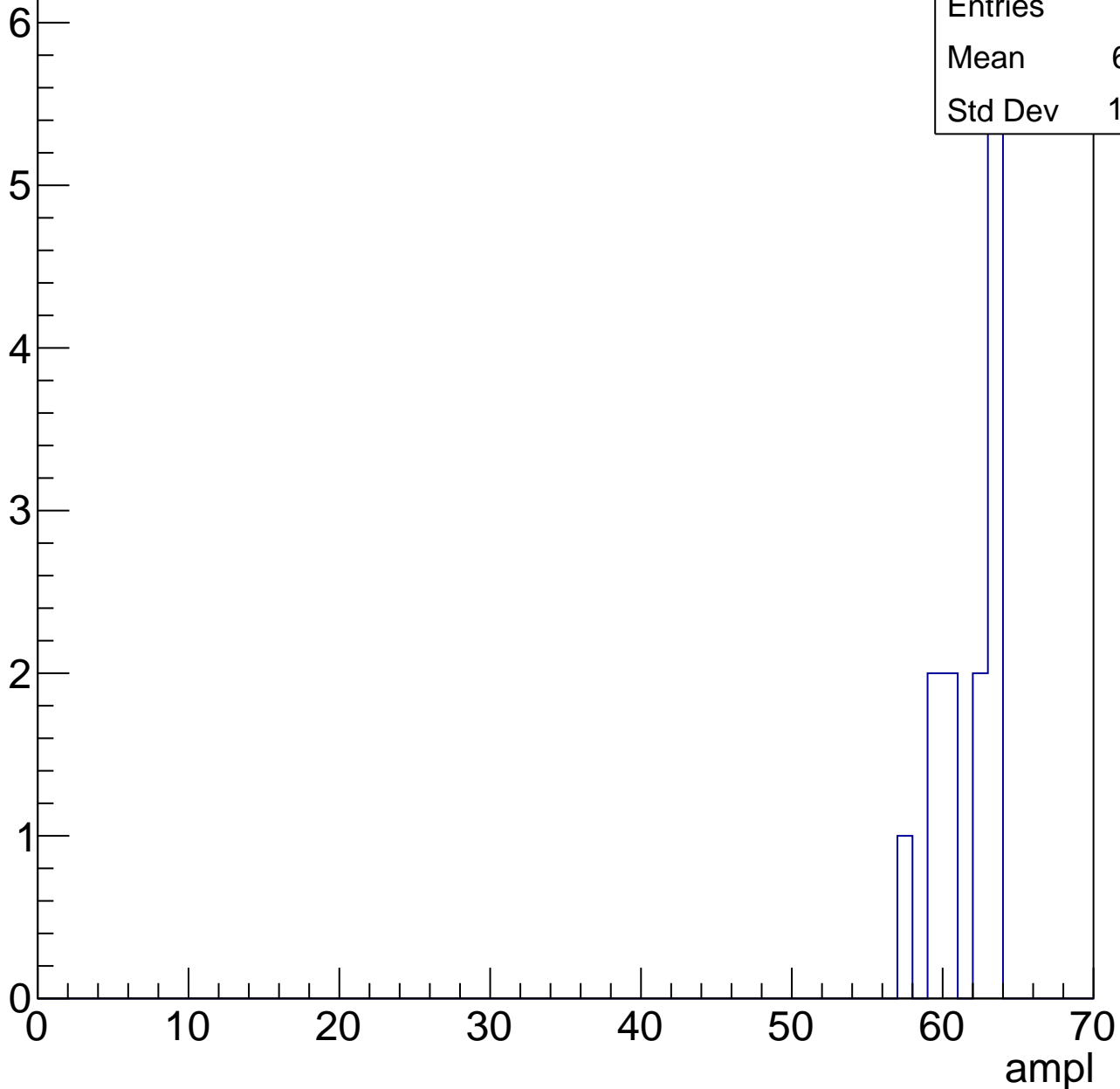


B0L101S, U24-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	61.31
Std Dev	1.976



B0L101S, U24-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch109, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

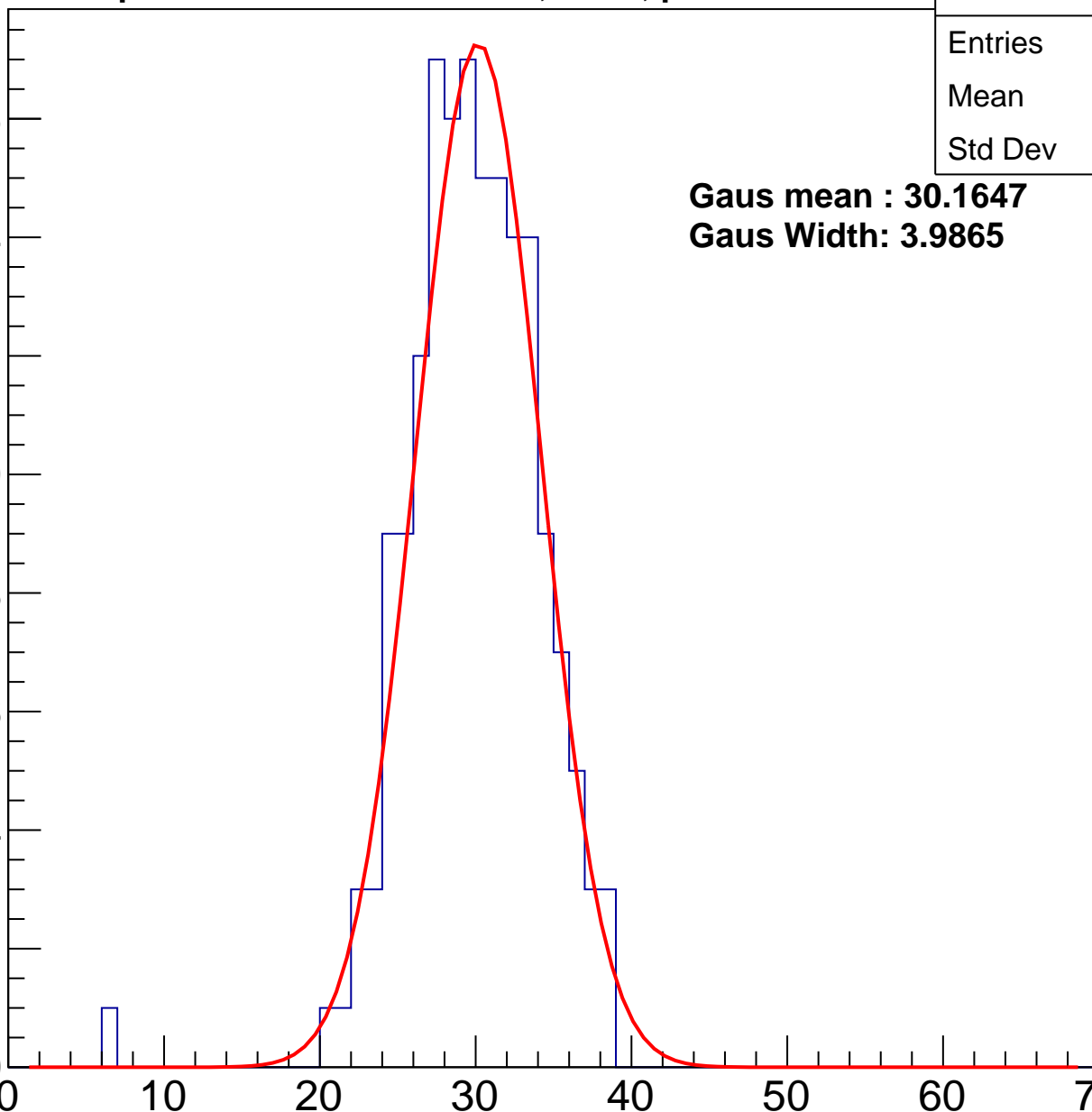
70

ampl

Entries	174
Mean	29.4
Std Dev	4.183

Gaus mean : 30.1647

Gaus Width: 3.9865



B0L101S, U24-ch109, adc1

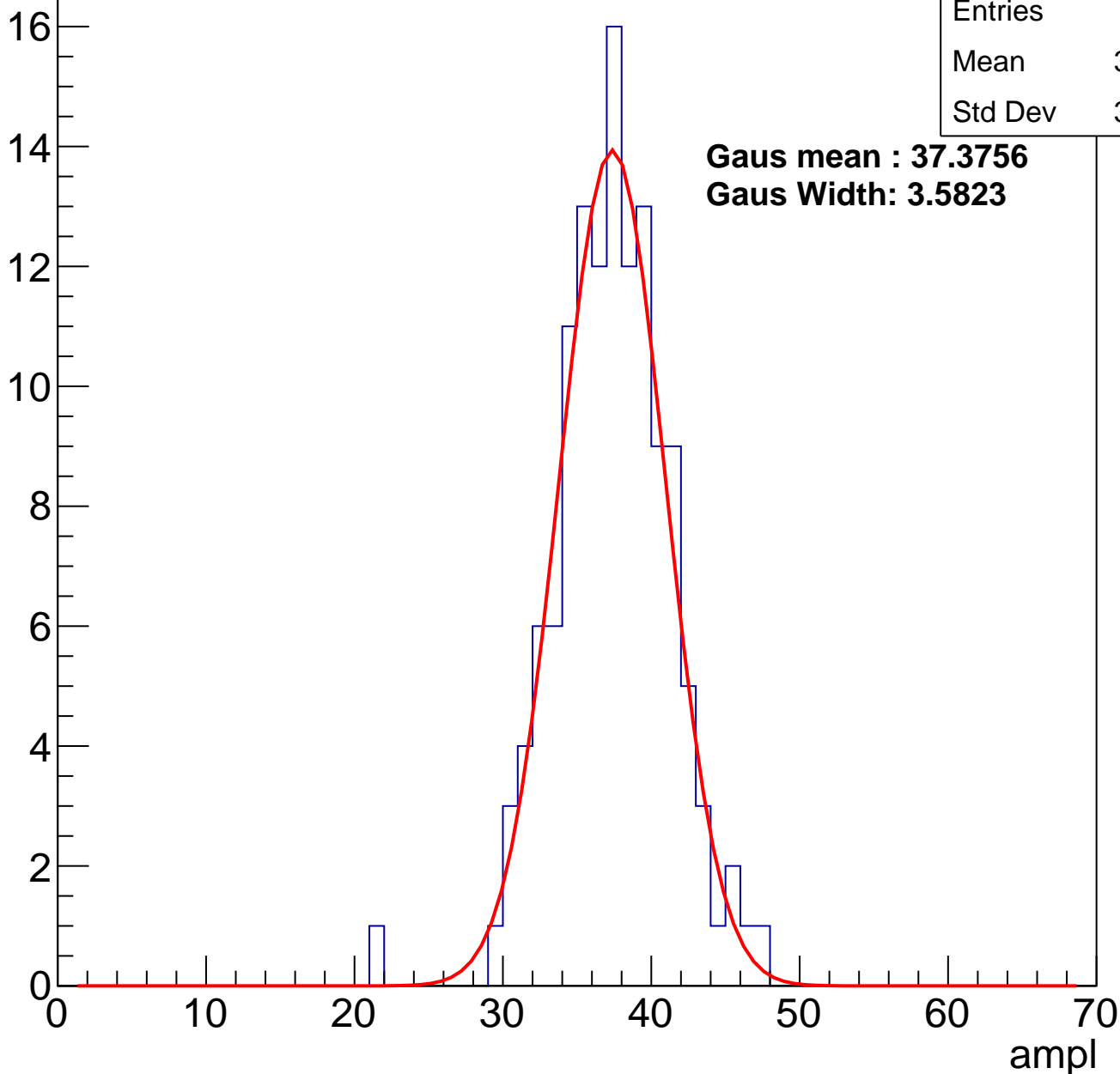
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	36.94
Std Dev	3.837

Gaus mean : 37.3756

Gaus Width: 3.5823

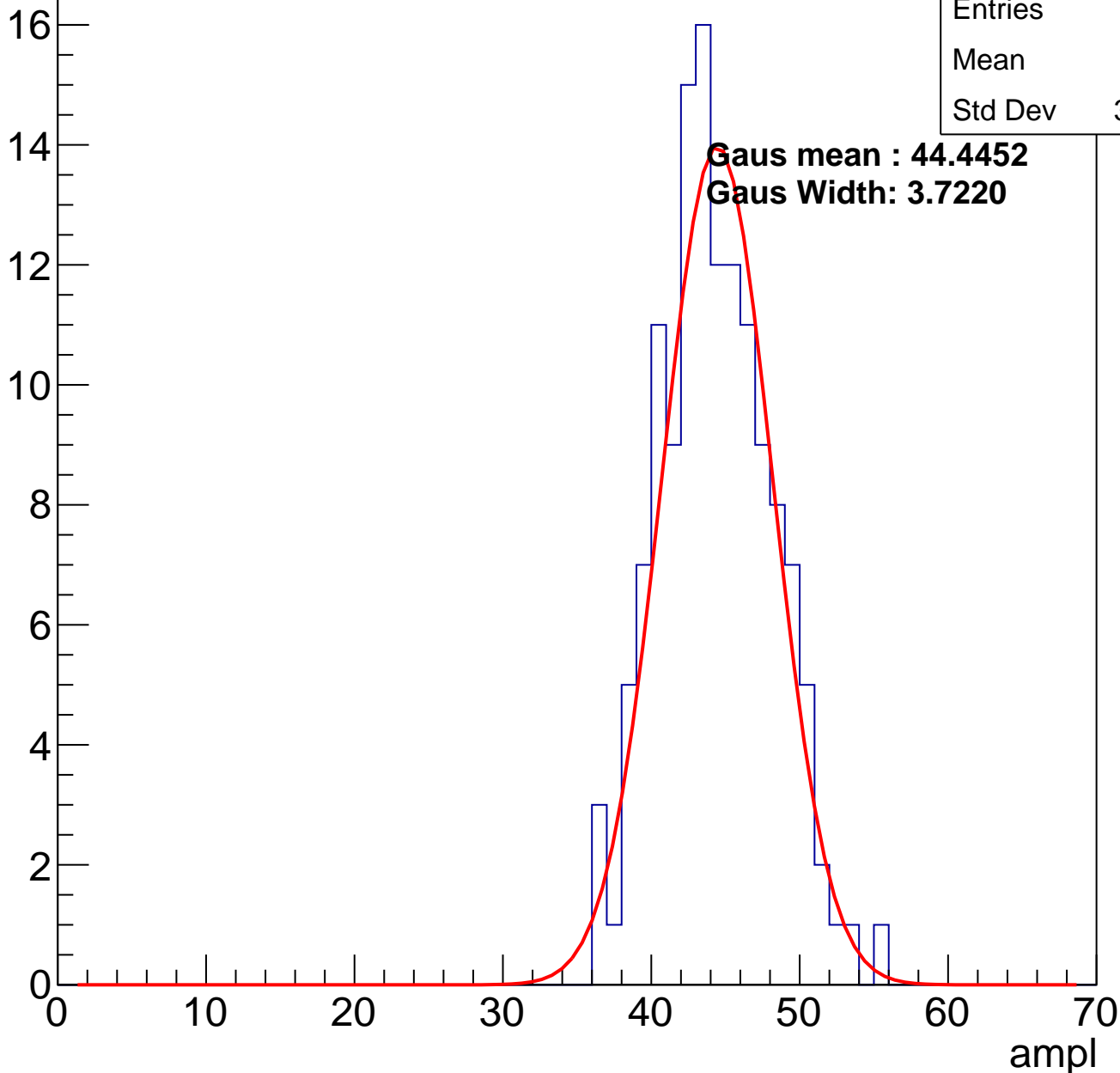
Entry



B0L101S, U24-ch109, adc2

calib_packv5_042523_0143.root, FC#1, port C1

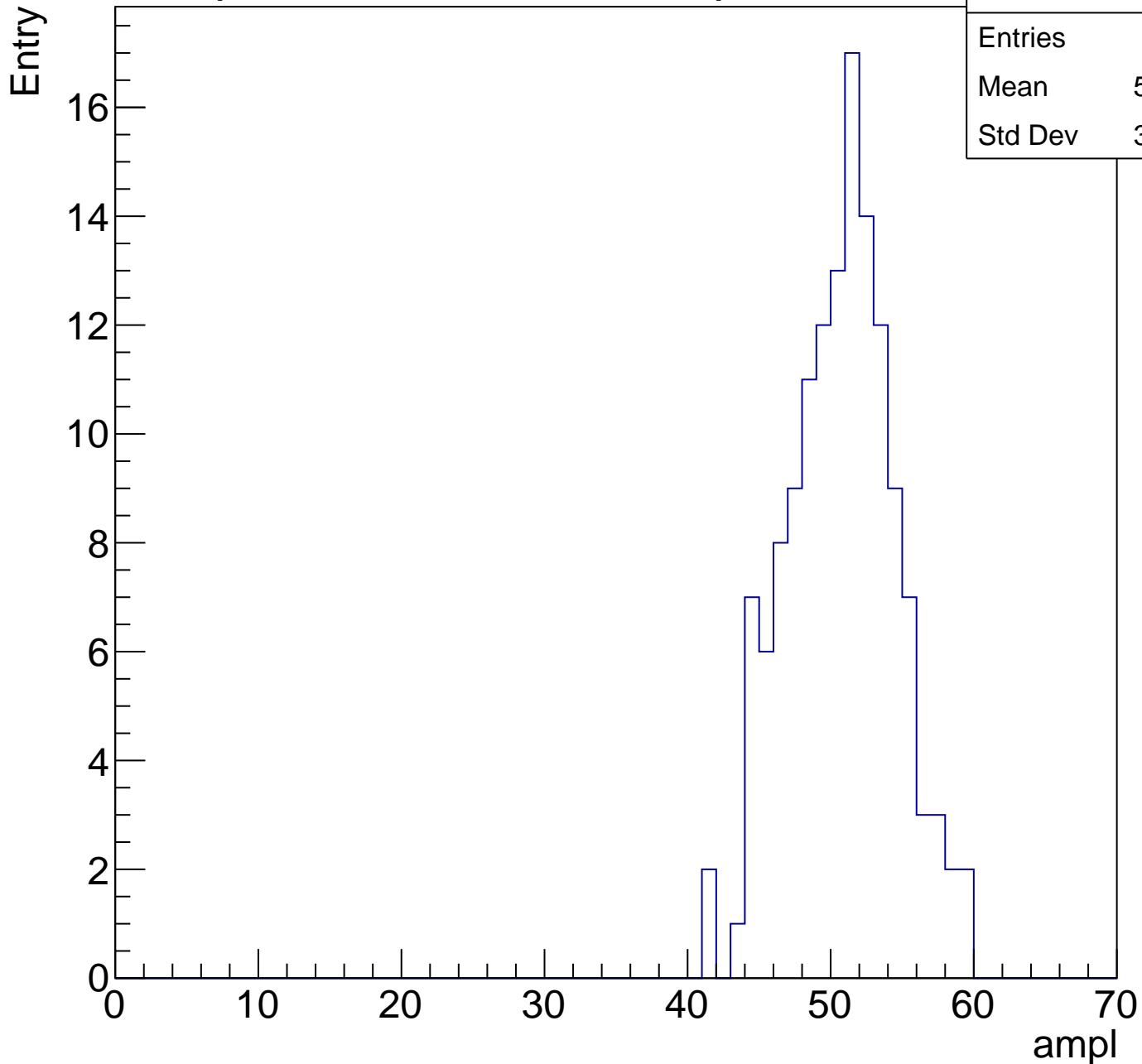
Entry



B0L101S, U24-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	50.25
Std Dev	3.724



B0L101S, U24-ch109, adc4

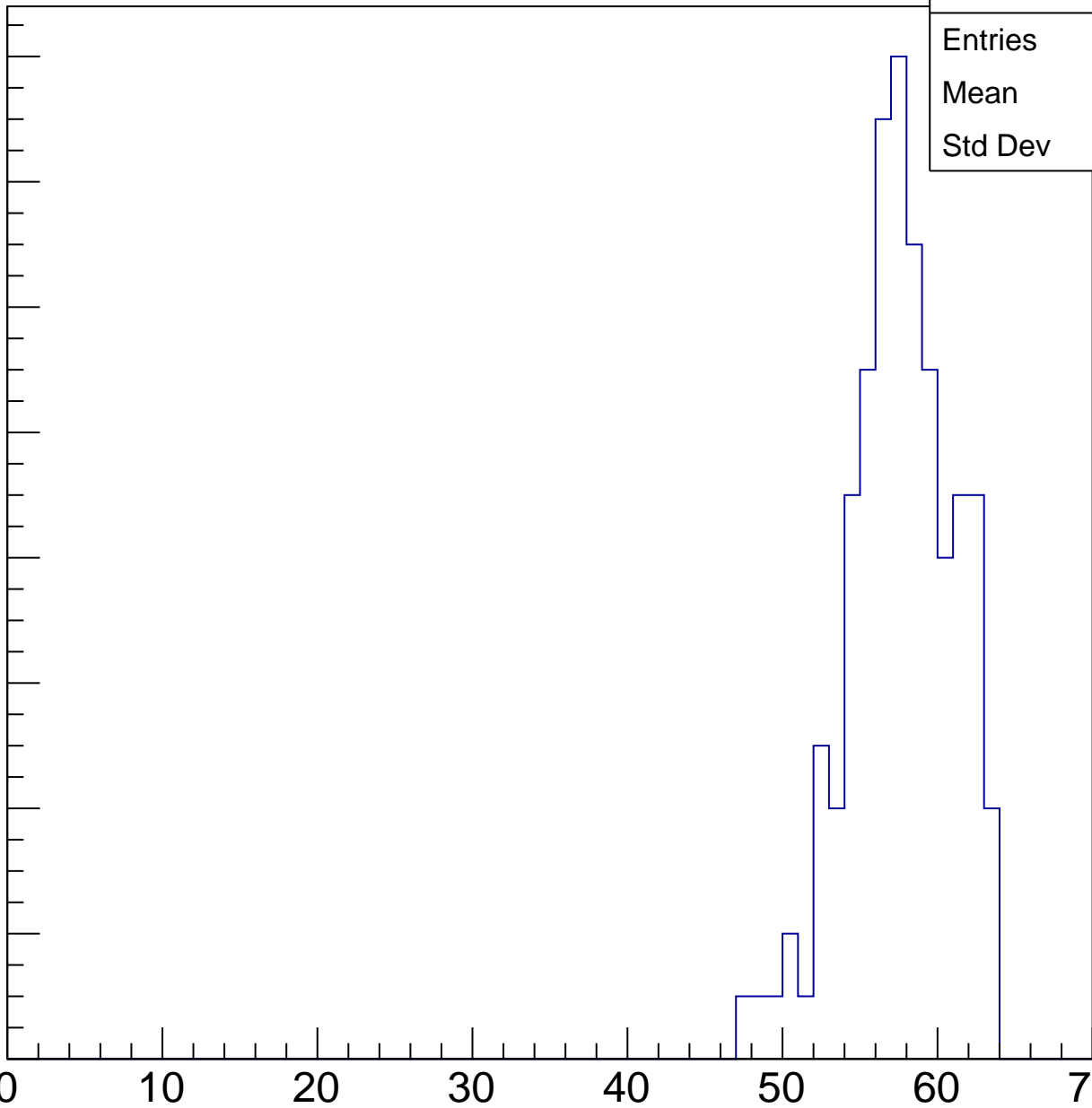
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	57.1
Std Dev	3.353

ampl



B0L101S, U24-ch109, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

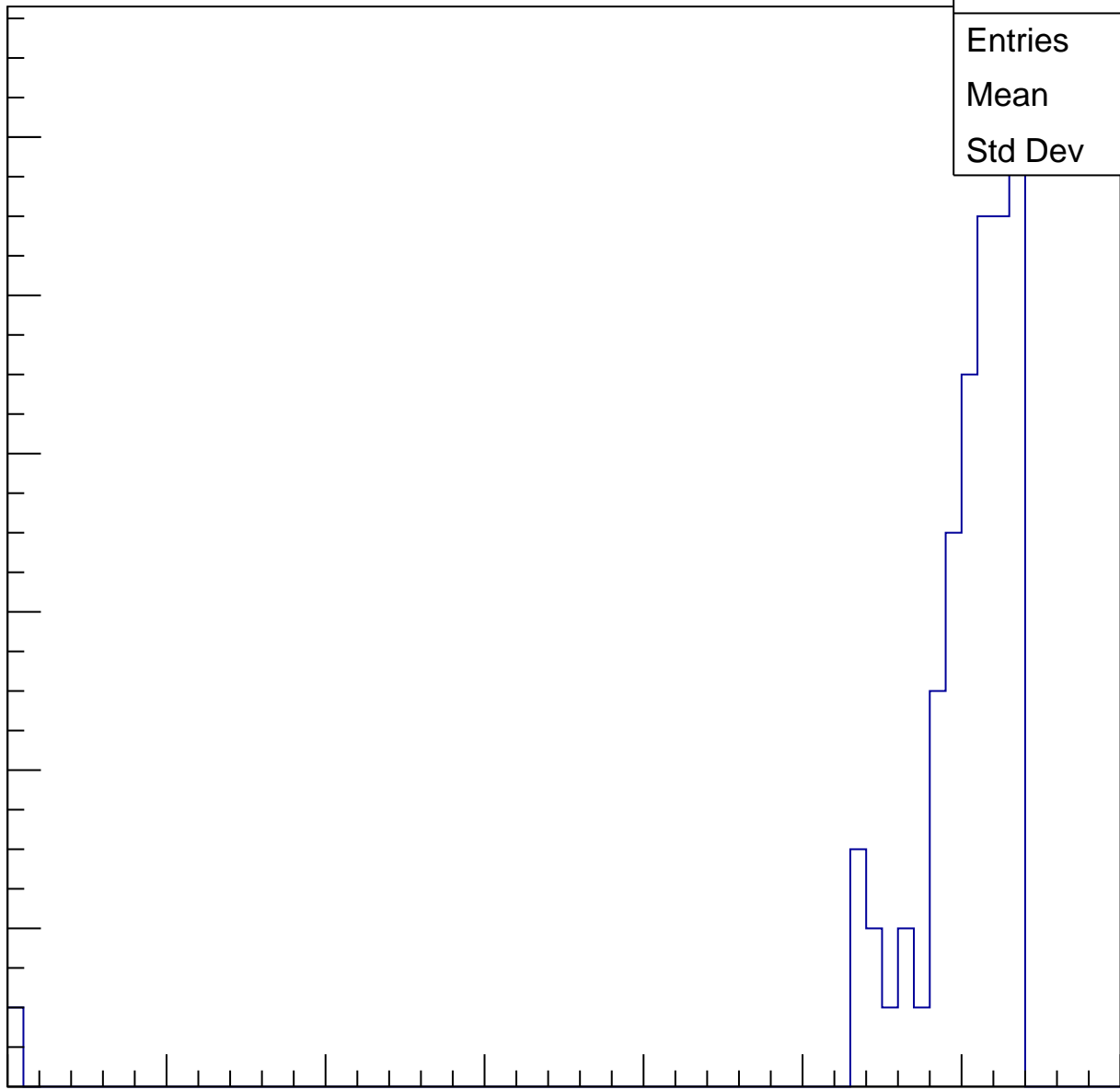
Entries 66

Mean 59.18

Std Dev 7.824

ampl

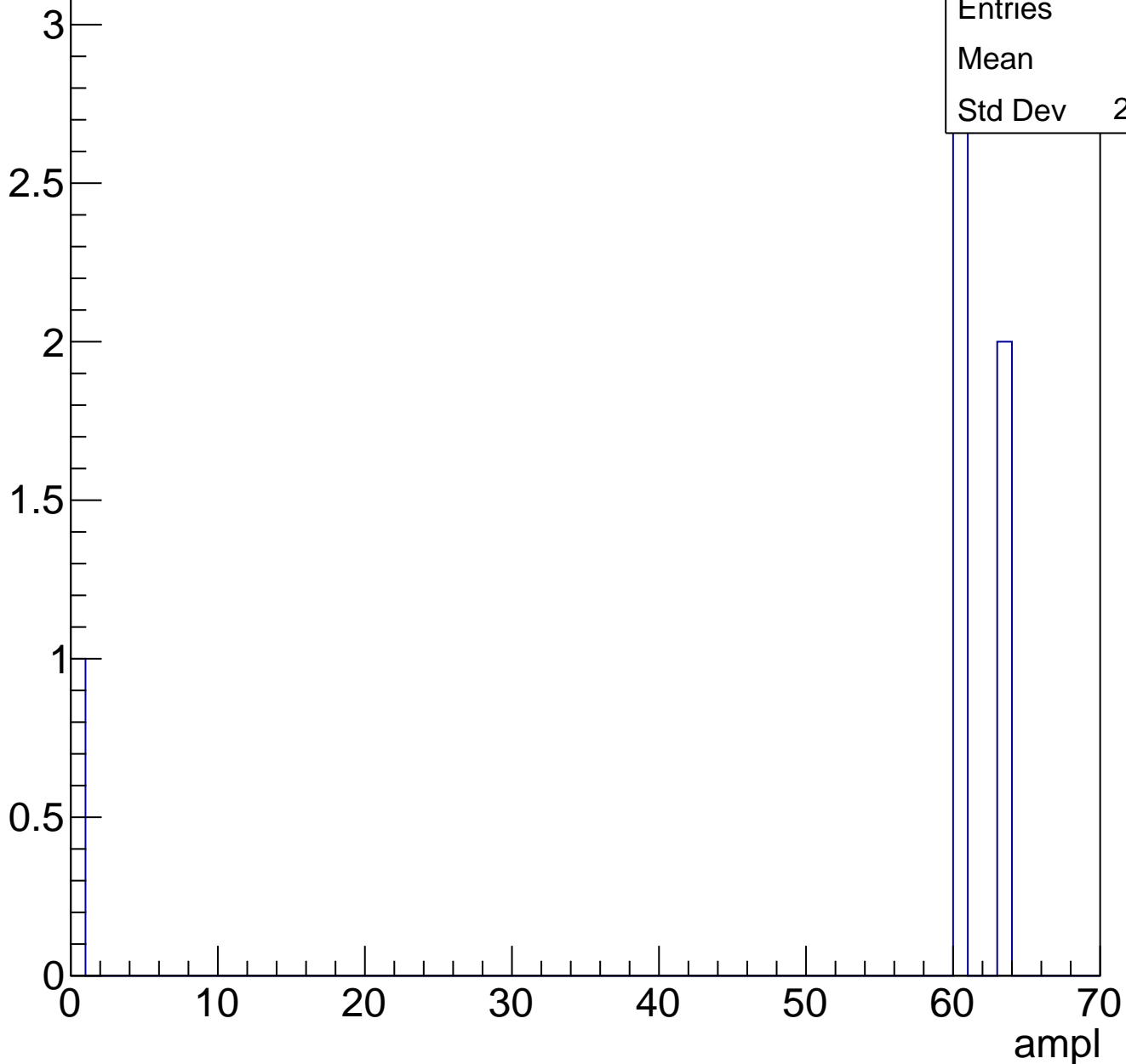
0 10 20 30 40 50 60 70



B0L101S, U24-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch110, adc0

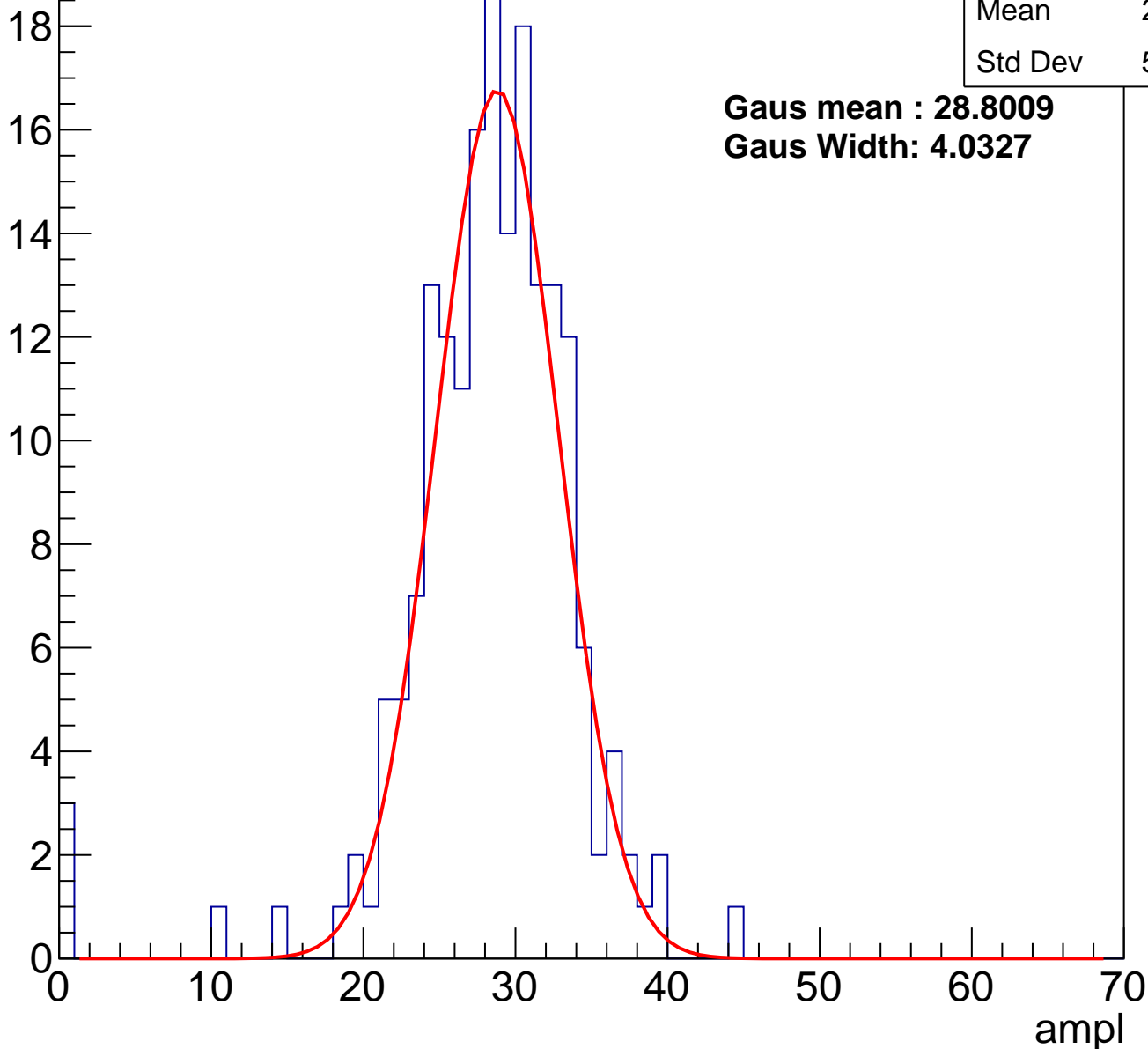
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	27.84
Std Dev	5.794

Gaus mean : 28.8009

Gaus Width: 4.0327

Entry



B0L101S, U24-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

125

Mean

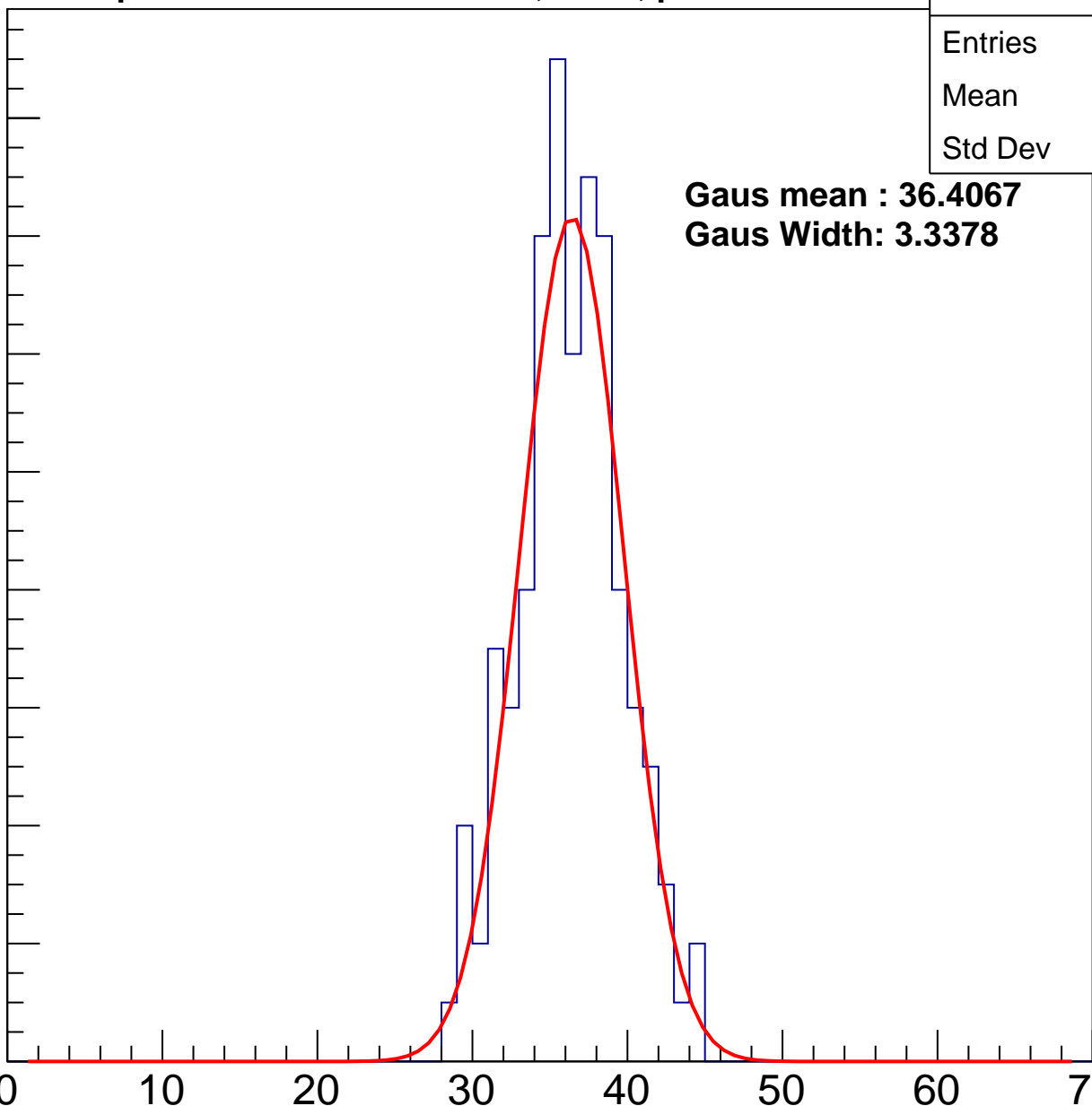
35.85

Std Dev

3.34

Gaus mean : 36.4067

Gaus Width: 3.3378



B0L101S, U24-ch110, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

117

Mean

41.71

Std Dev

5.356

Gaus mean : 42.8093

Gaus Width: 3.7937

0

10

20

30

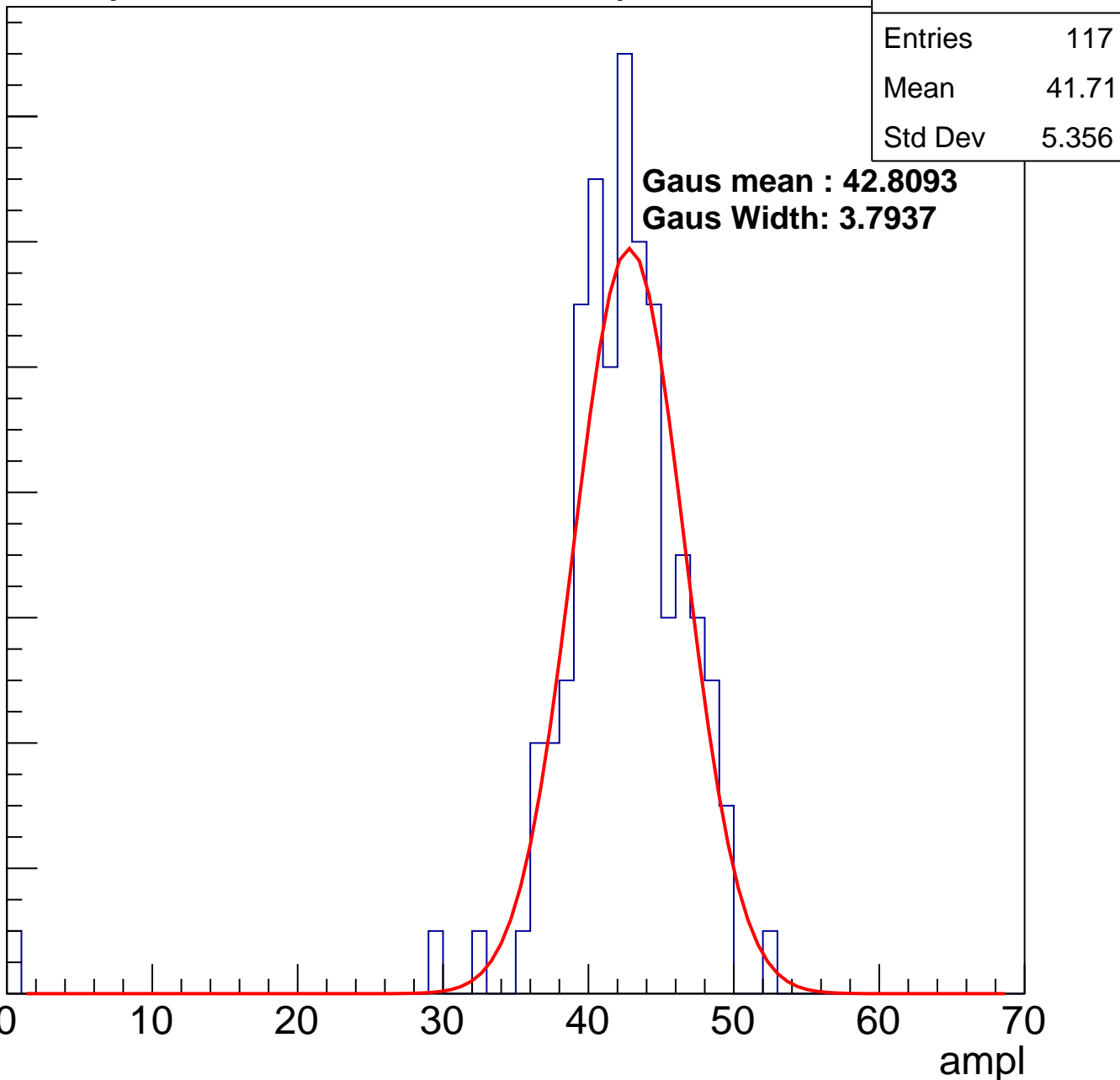
40

50

60

70

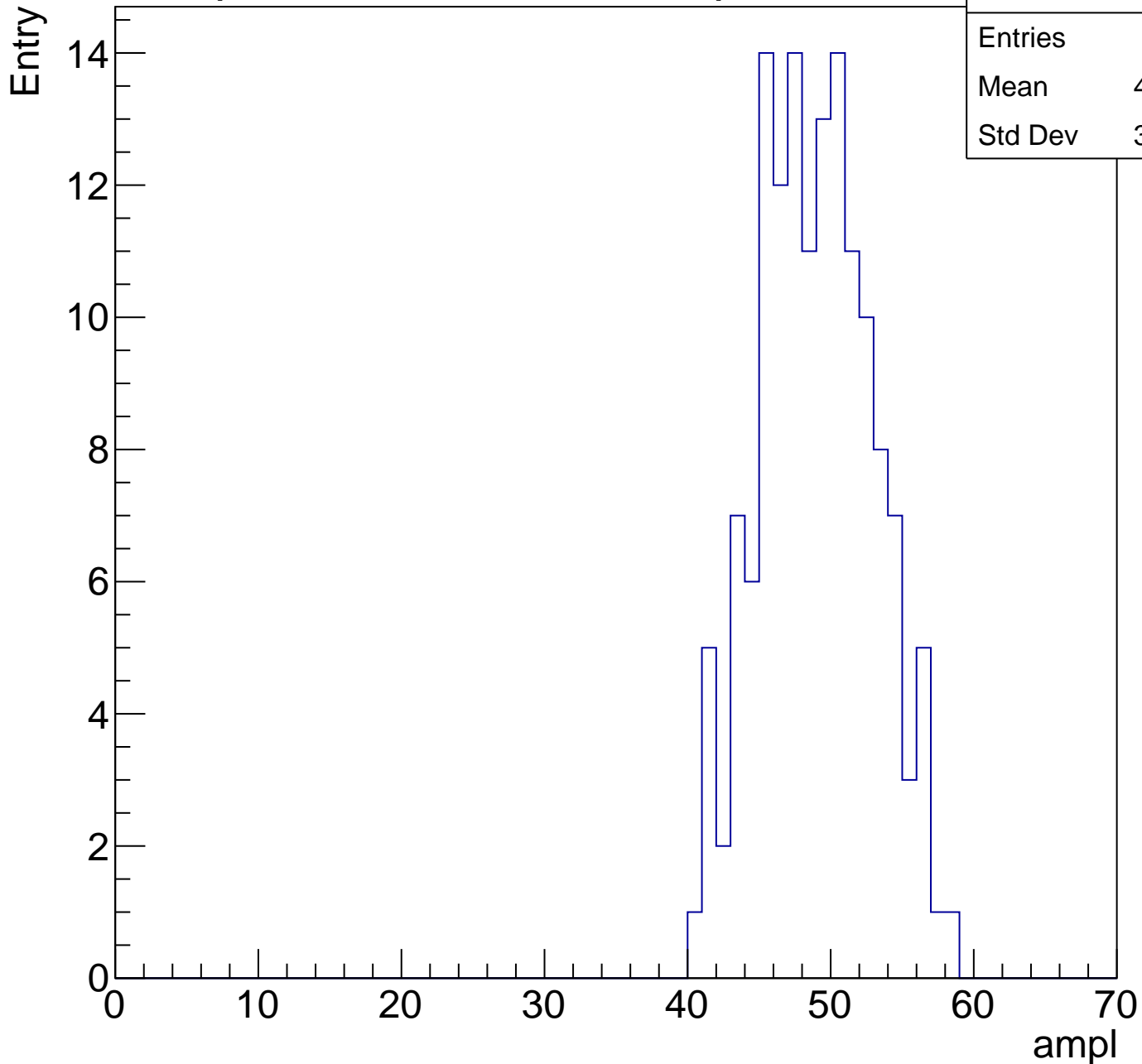
ampl



B0L101S, U24-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	48.57
Std Dev	3.914



B0L101S, U24-ch110, adc4

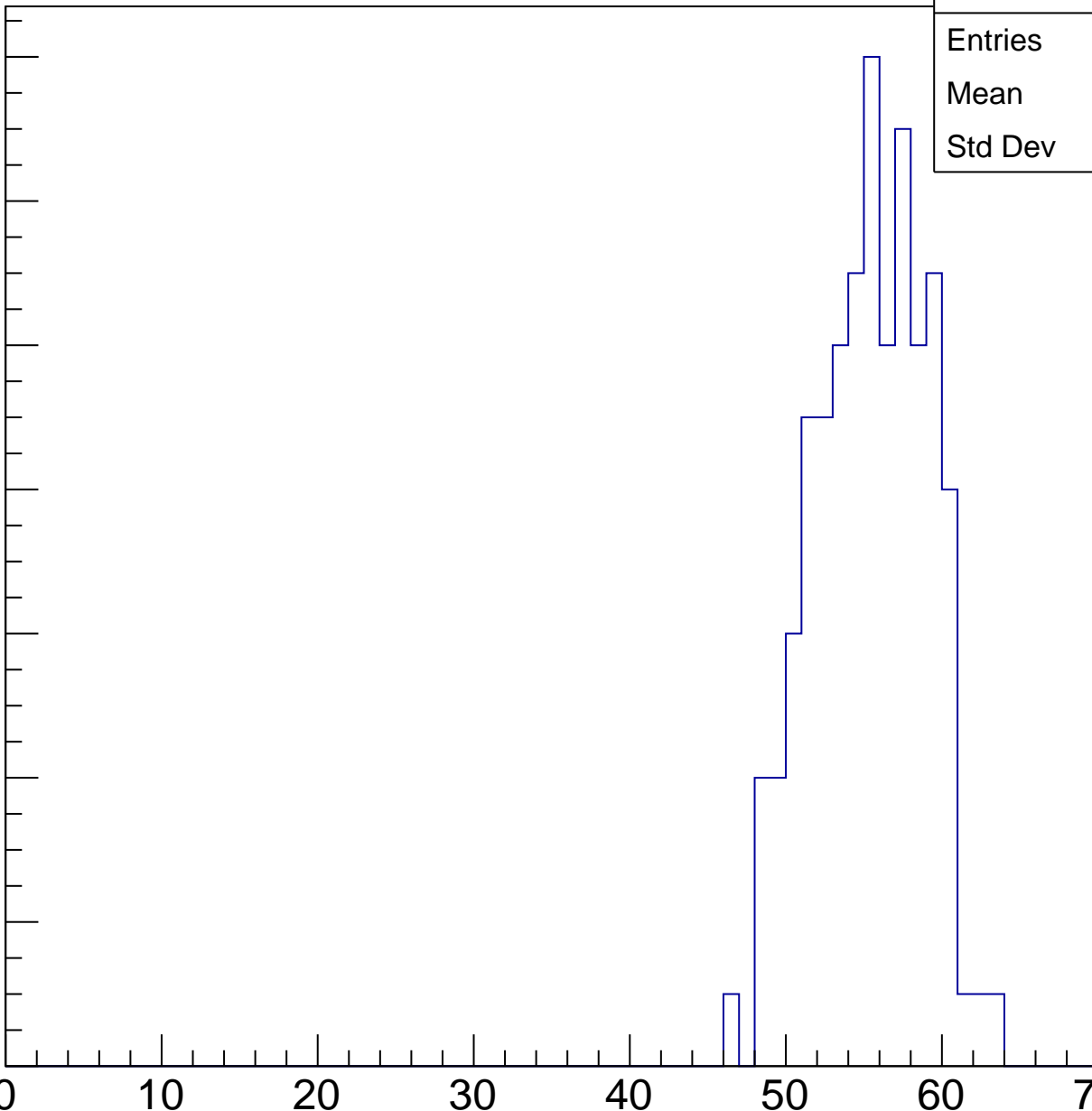
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	54.89
Std Dev	3.518

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch110, adc5

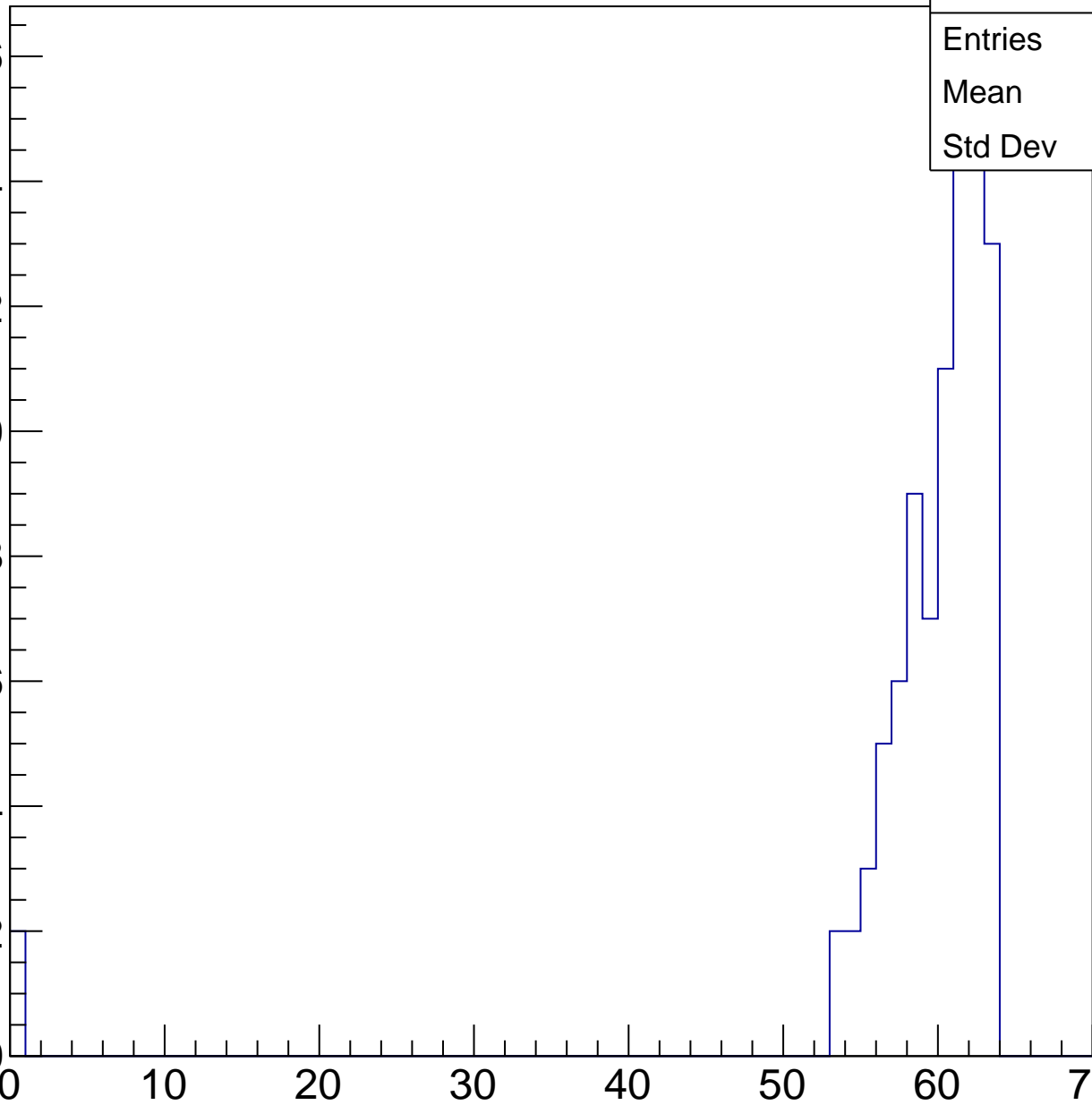
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.48
Std Dev	9.144

ampl

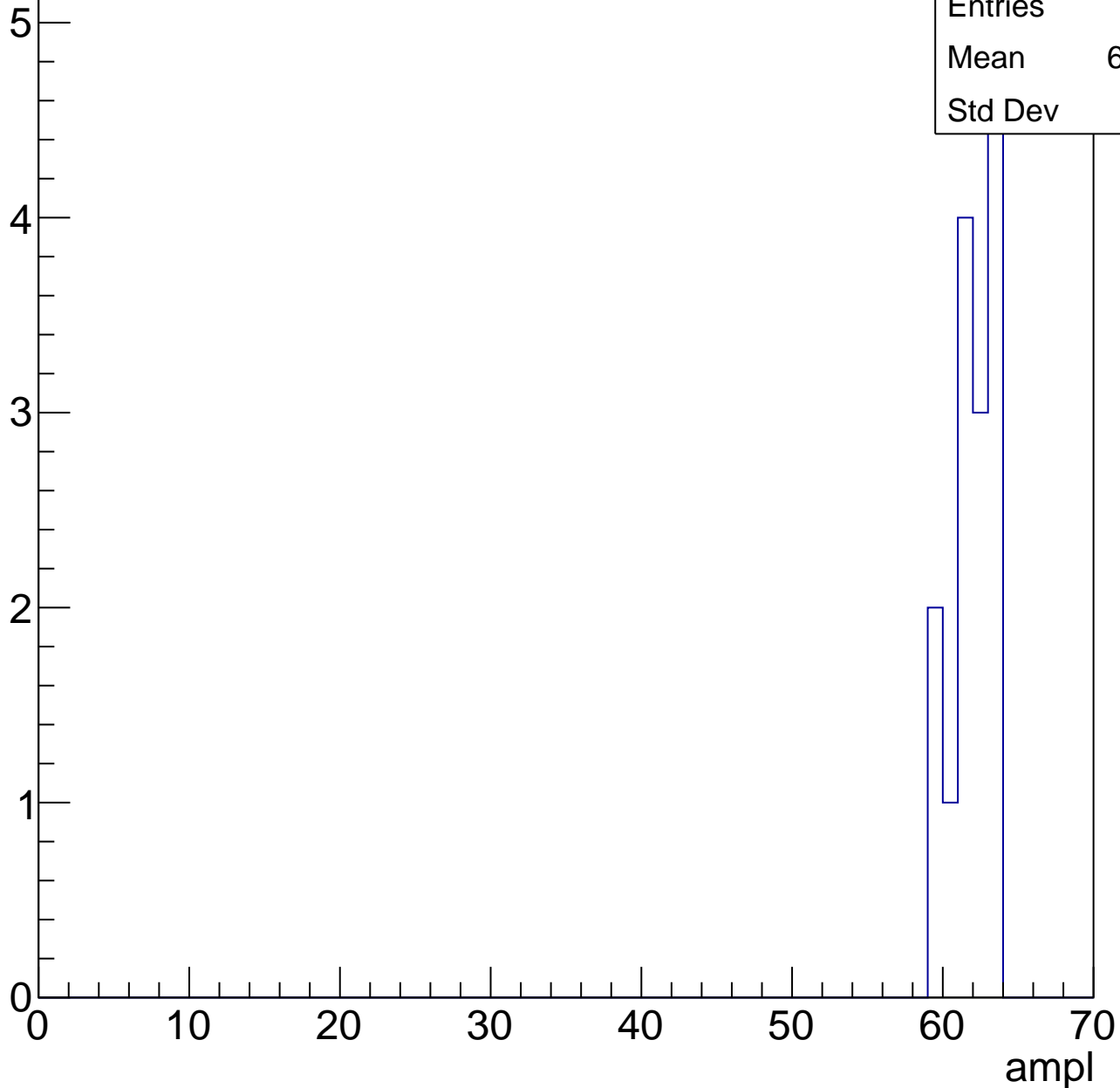


B0L101S, U24-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

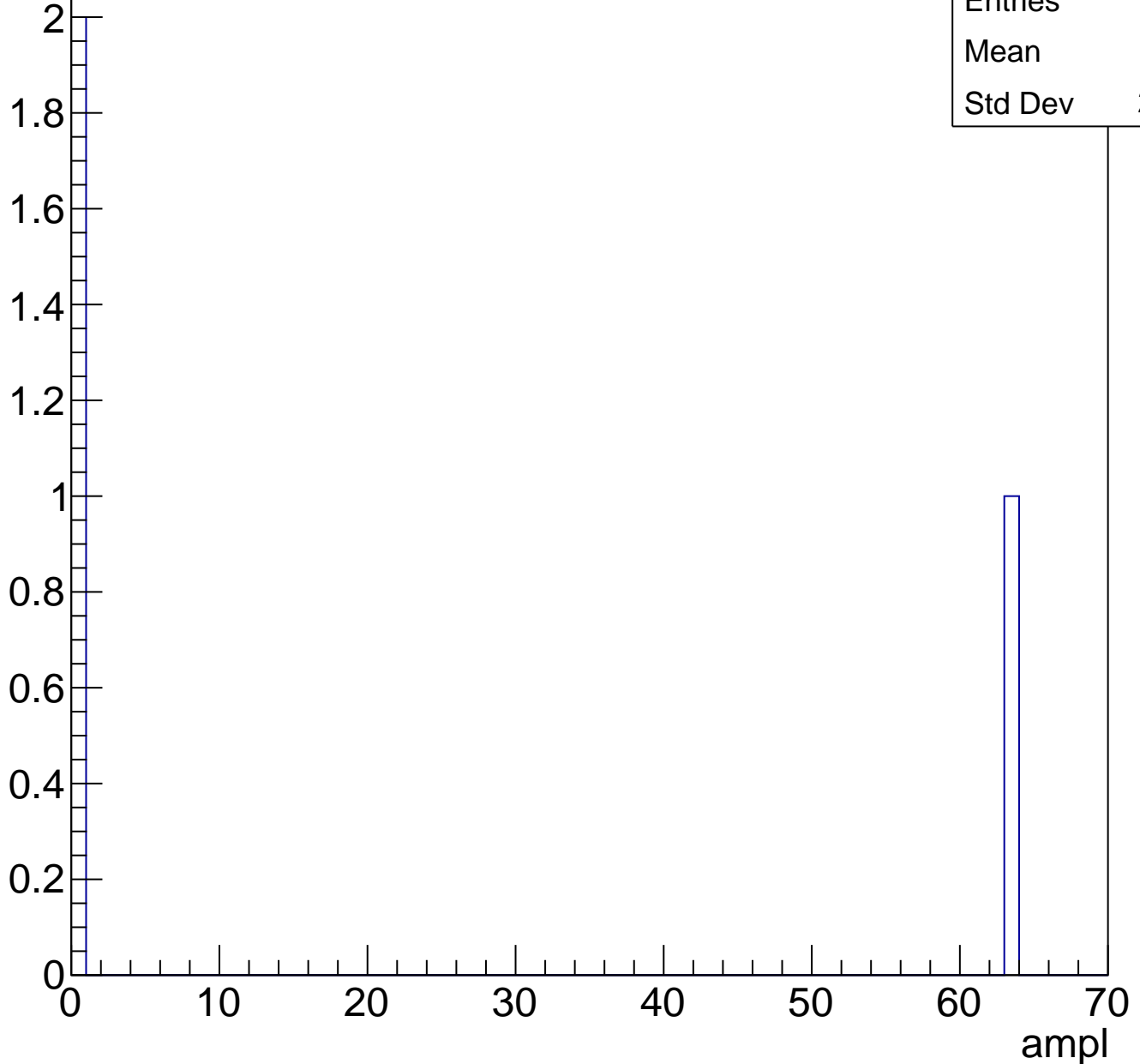
Entries	15
Mean	61.53
Std Dev	1.36



B0L101S, U24-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch111, adc0

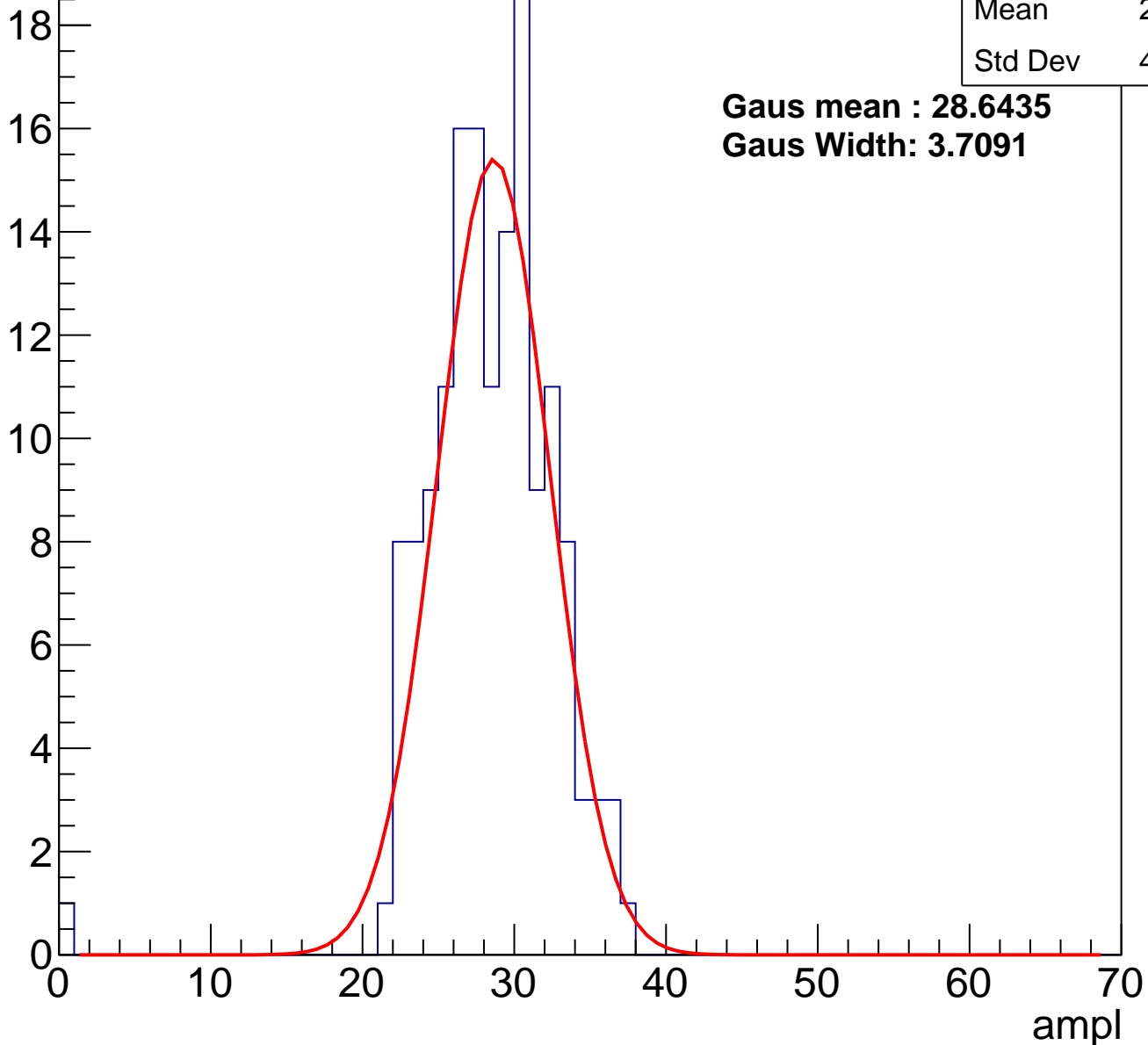
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	27.97
Std Dev	4.229

Gaus mean : 28.6435

Gaus Width: 3.7091

Entry



B0L101S, U24-ch111, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	35.97
Std Dev	3.768

Gaus mean : 36.8941

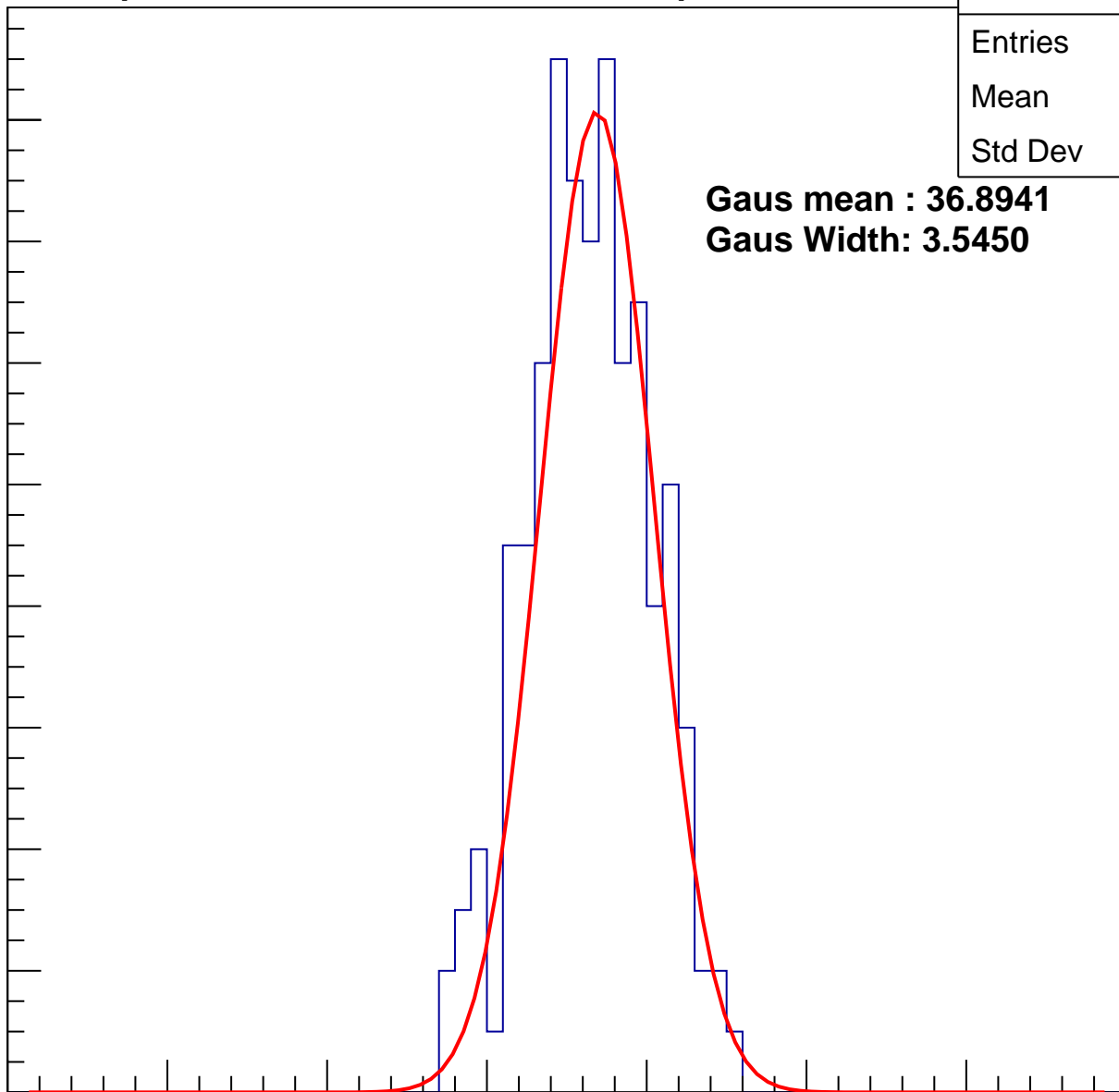
Gaus Width: 3.5450

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

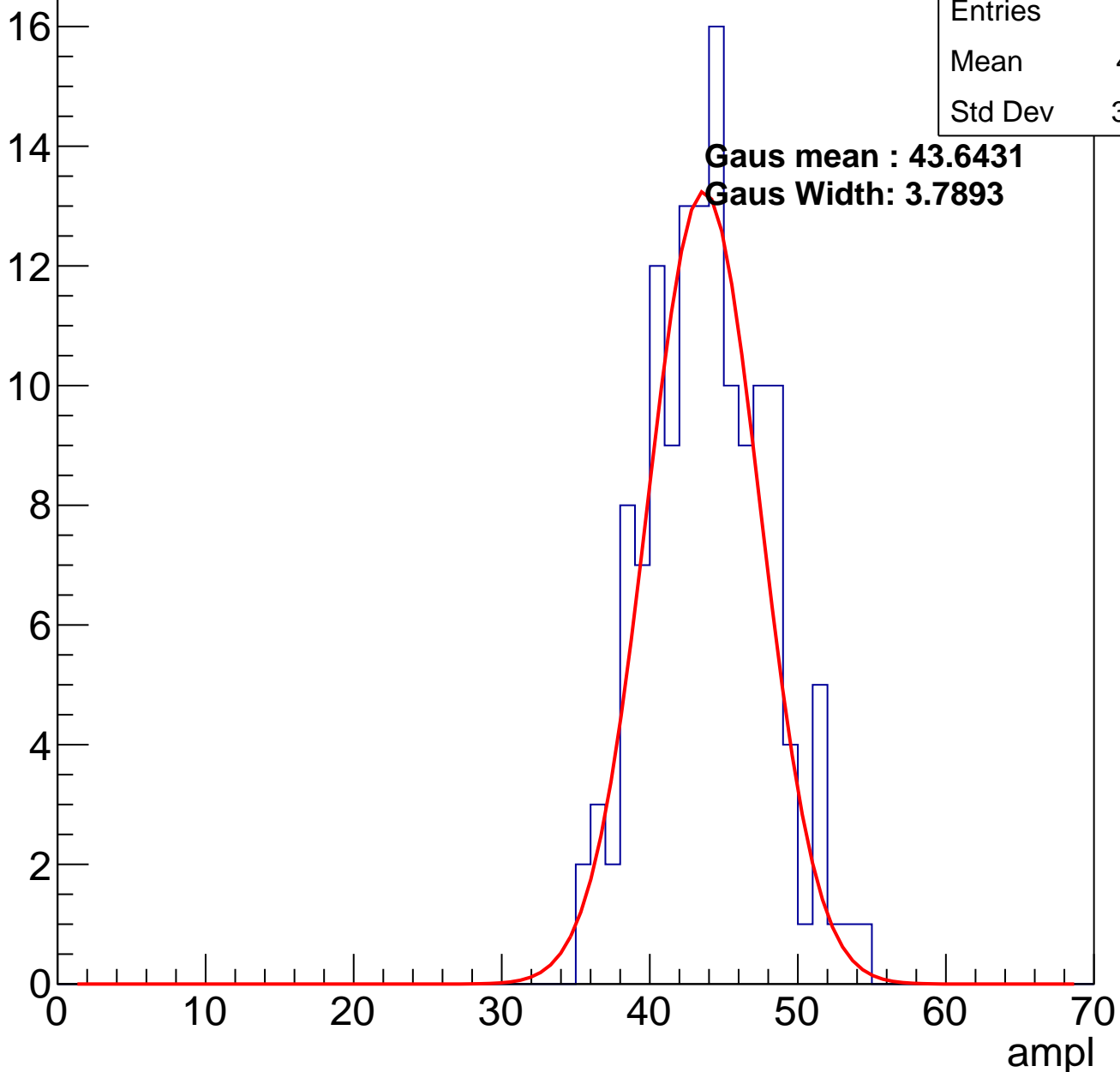
ampl



B0L101S, U24-ch111, adc2

calib_packv5_042523_0143.root, FC#1, port C1

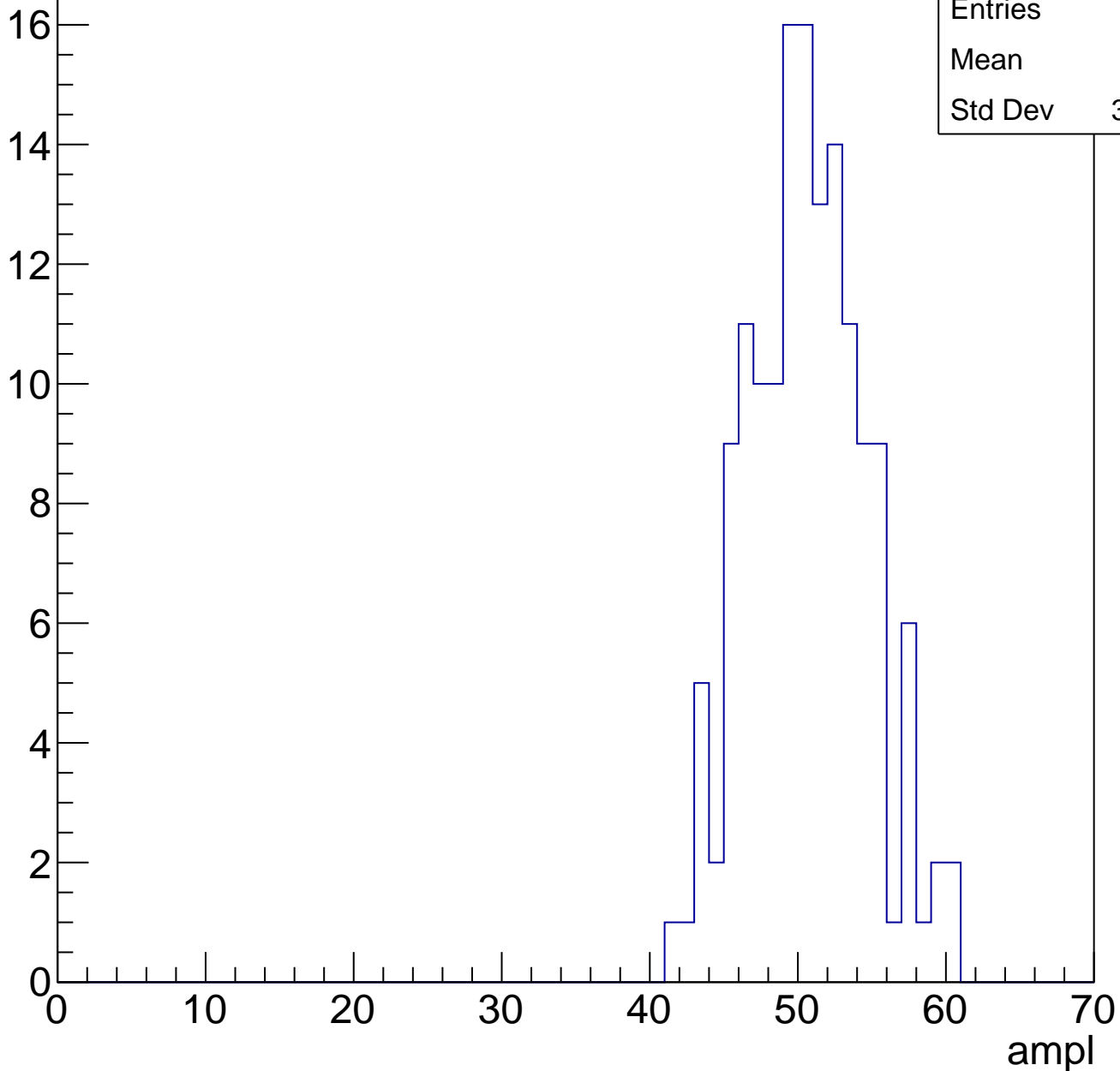
Entry



B0L101S, U24-ch111, adc3

calib_packv5_042523_0143.root, FC#1, port C1

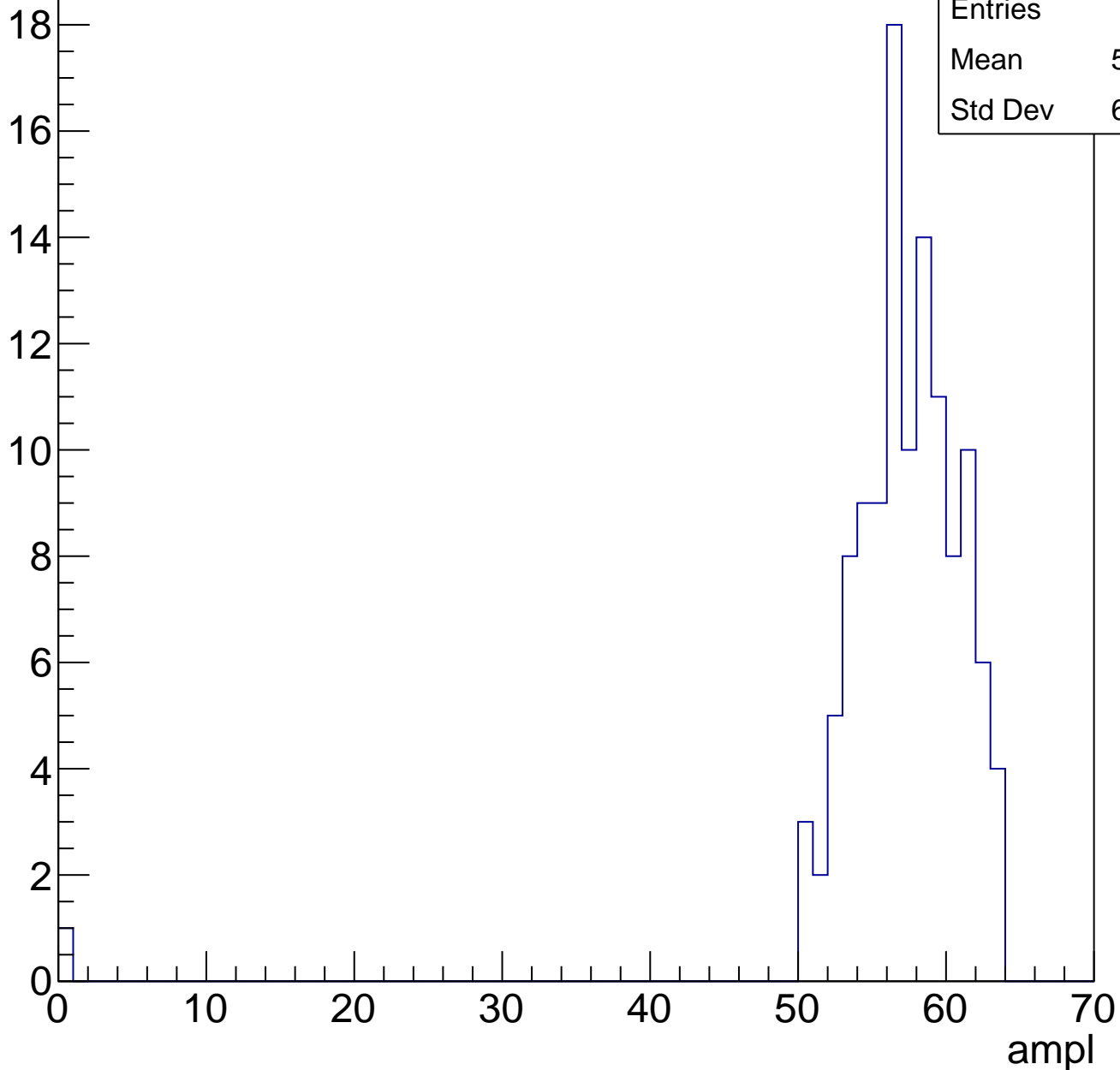
Entry



B0L101S, U24-ch111, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

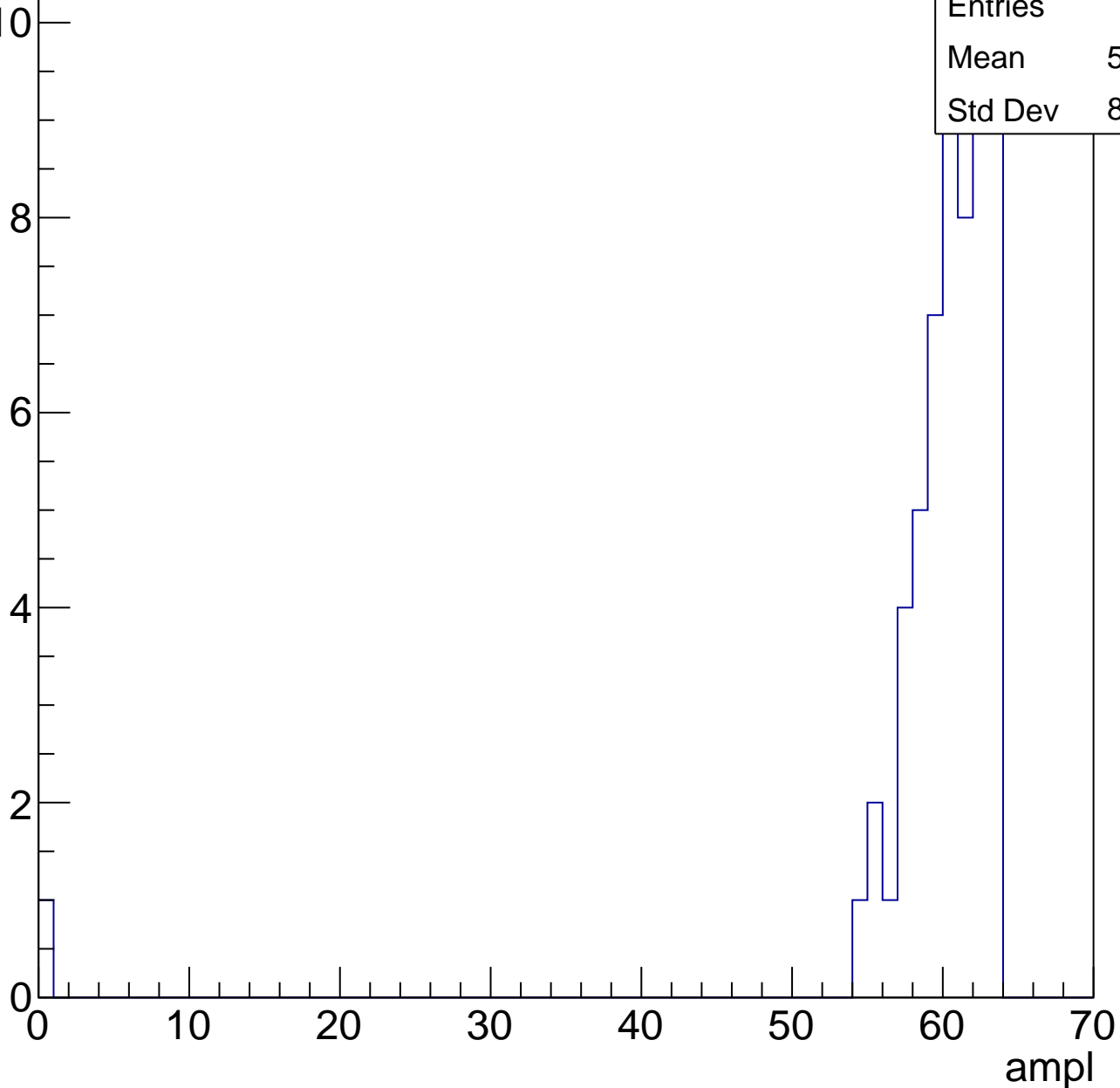


B0L101S, U24-ch111, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	58
Mean	59.09
Std Dev	8.148

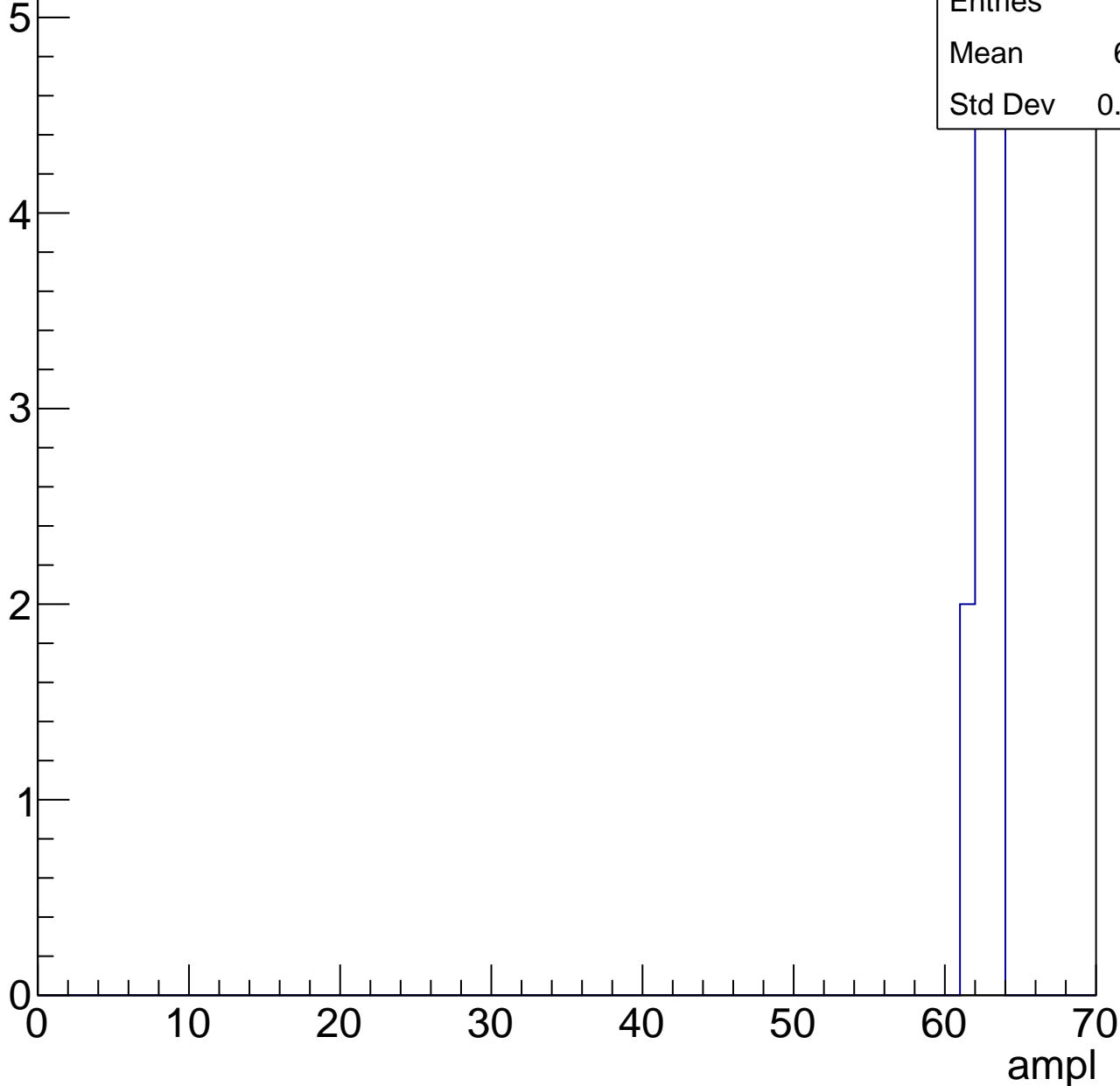


B0L101S, U24-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	62.25
Std Dev	0.7217



B0L101S, U24-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch112, adc0

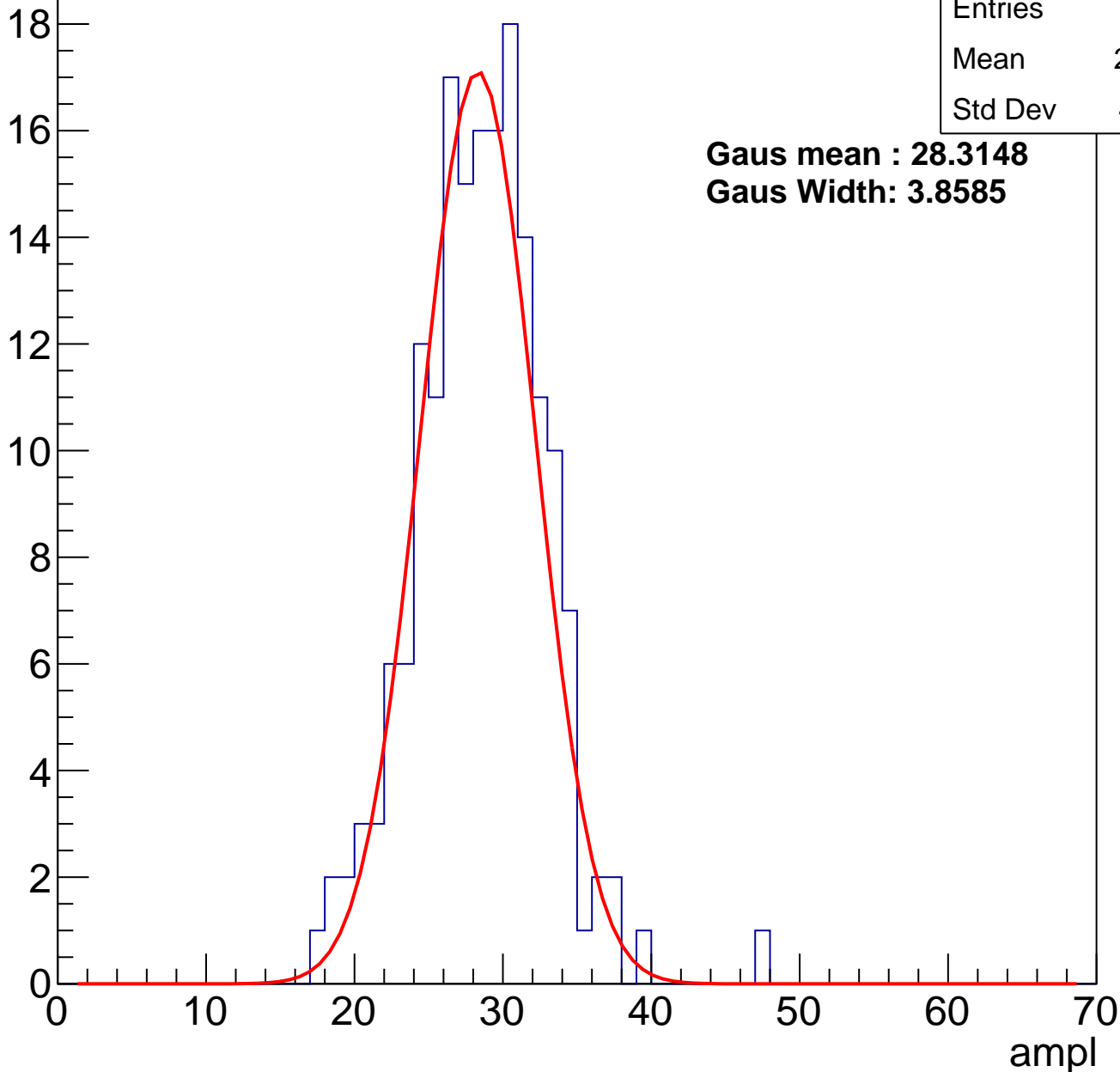
calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	28.06
Std Dev	4.301

Gaus mean : 28.3148

Gaus Width: 3.8585

Entry



B0L101S, U24-ch112, adc1

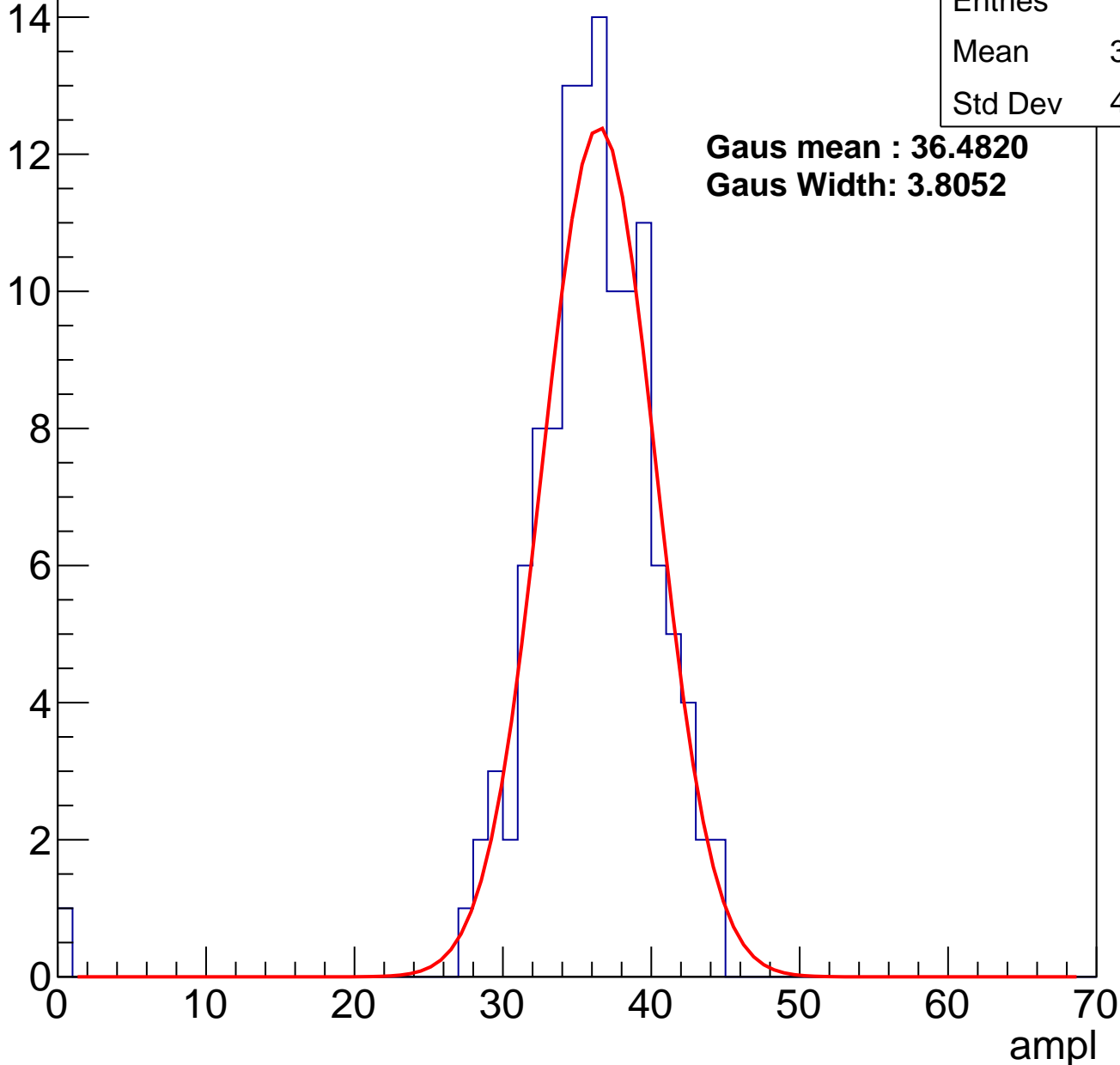
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	35.56
Std Dev	4.858

Gaus mean : 36.4820

Gaus Width: 3.8052

Entry



B0L101S, U24-ch112, adc2

calib_packv5_042523_0143.root, FC#1, port C1

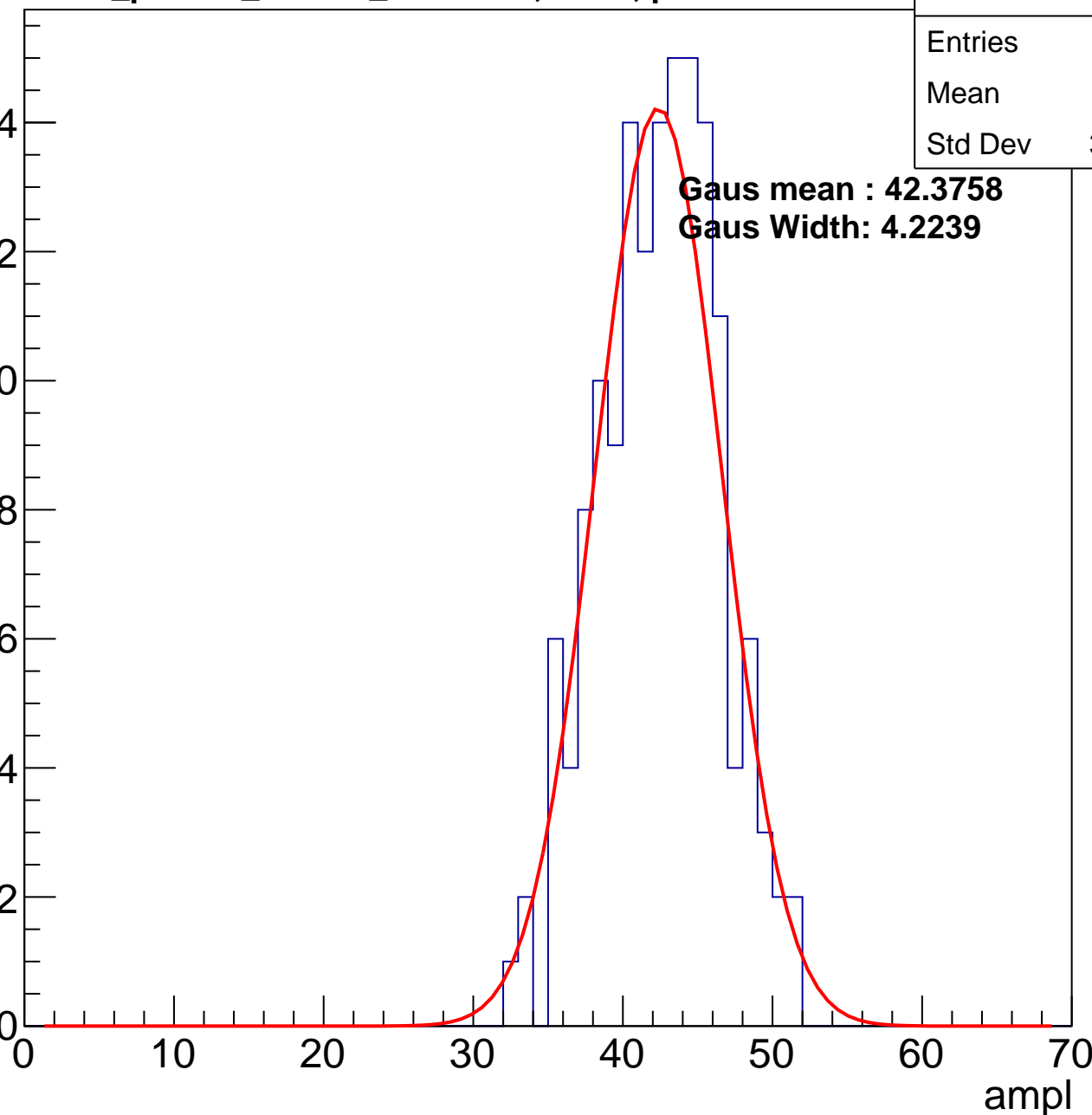
Entry

14
12
10
8
6
4
2
0

Entries	152
Mean	42.01
Std Dev	3.923

Gaus mean : 42.3758

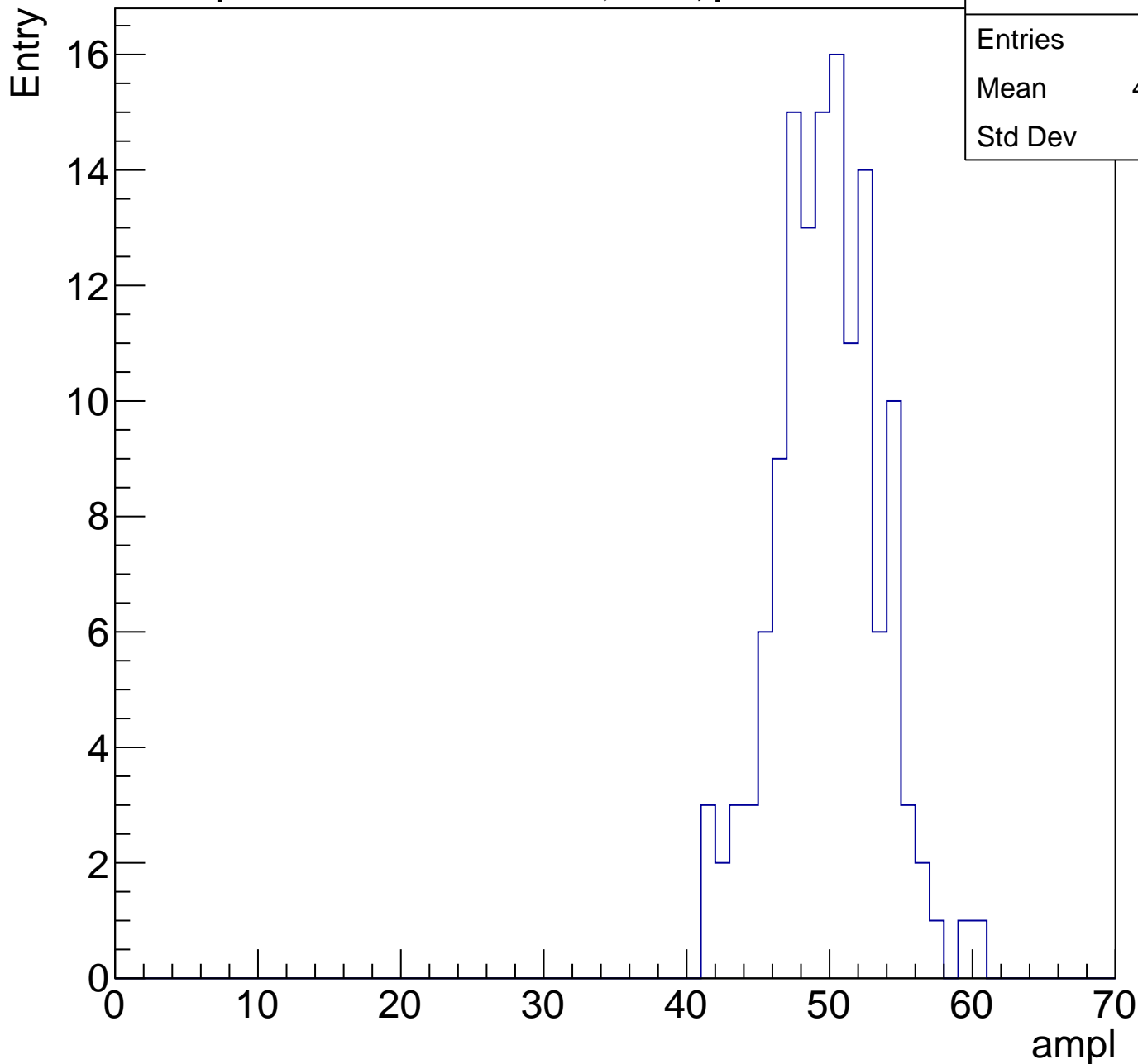
Gaus Width: 4.2239



B0L101S, U24-ch112, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	49.37
Std Dev	3.59



B0L101S, U24-ch112, adc4

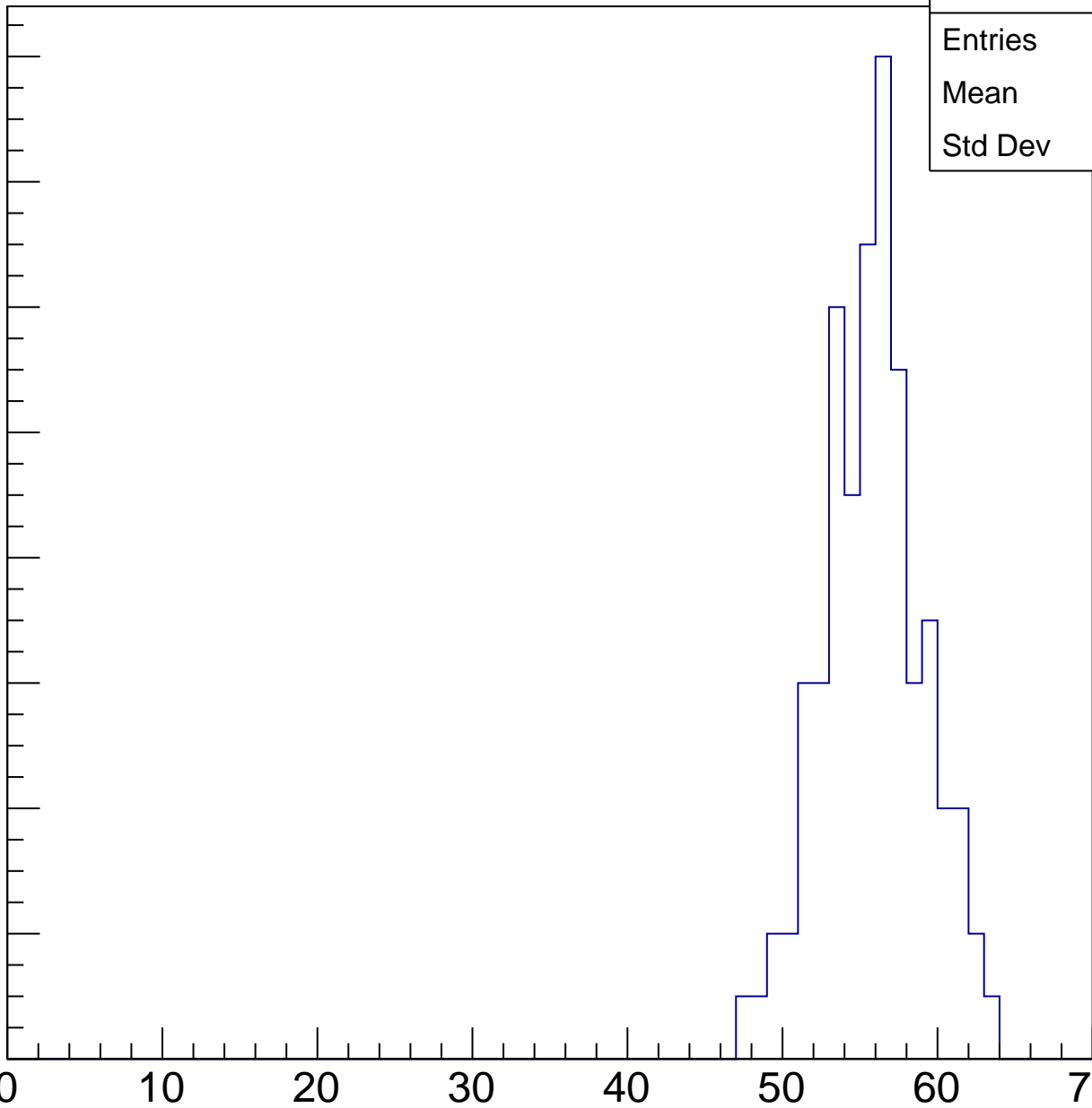
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	55.37
Std Dev	3.208

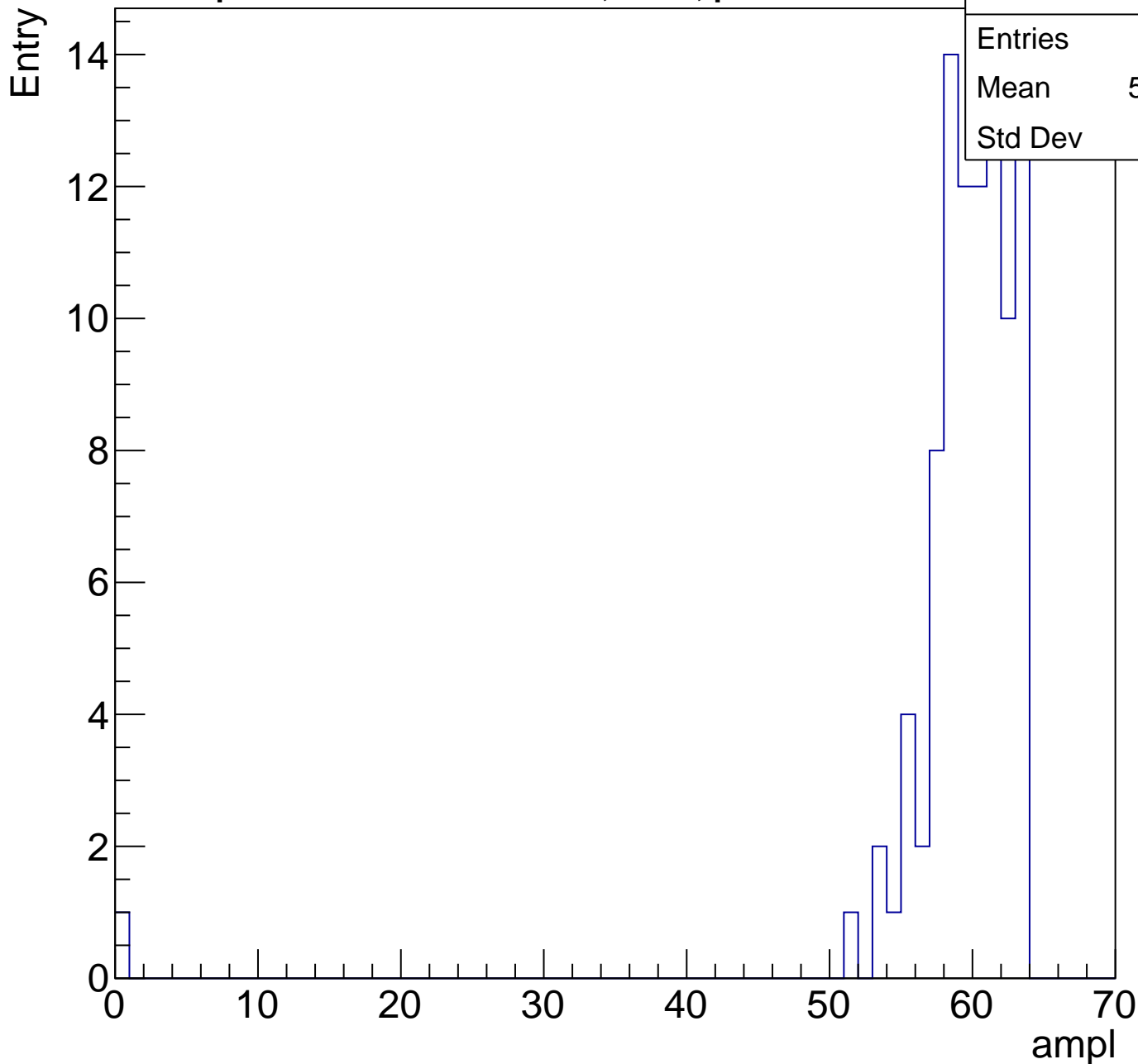
ampl



B0L101S, U24-ch112, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	58.89
Std Dev	6.61

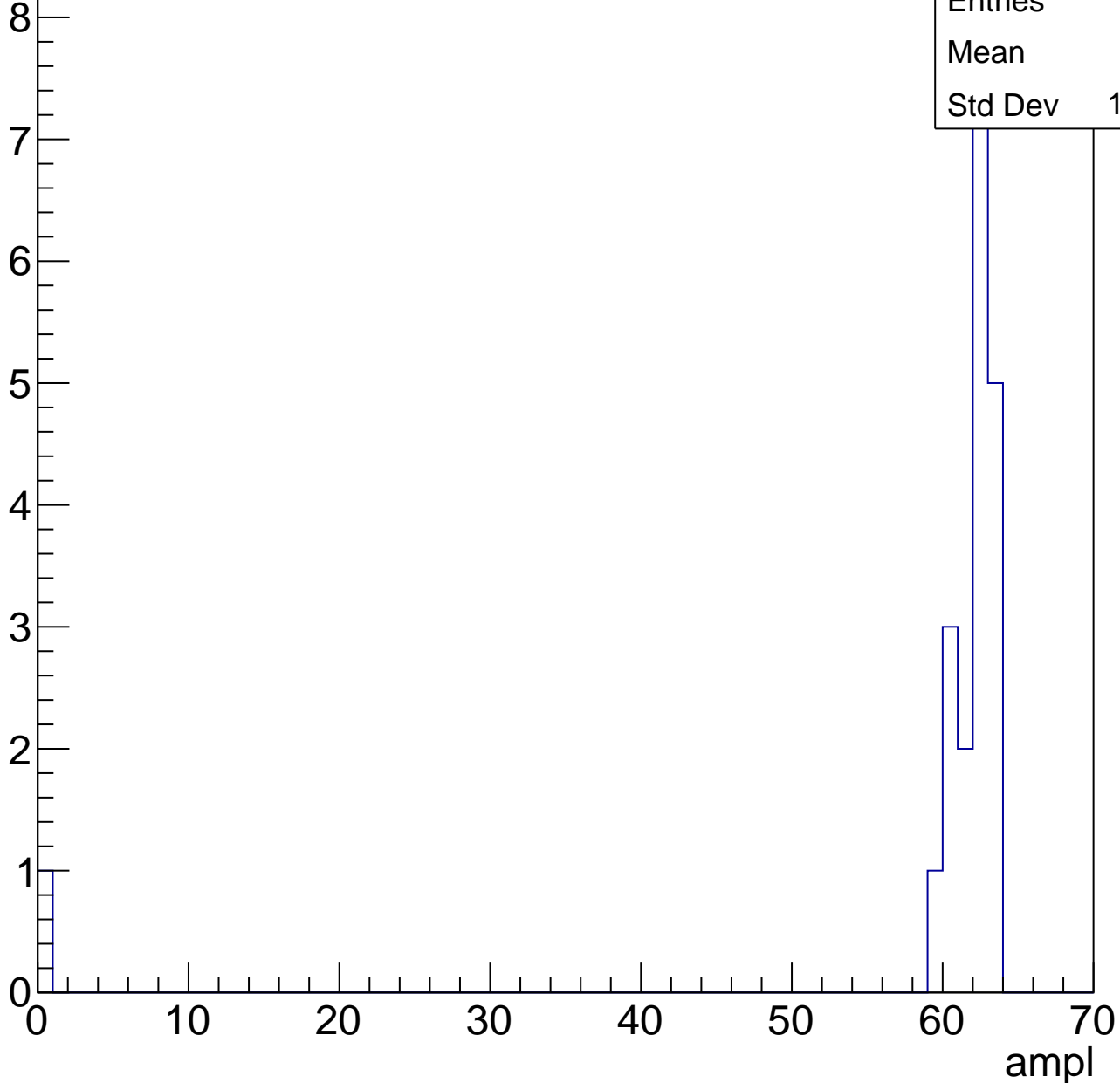


B0L101S, U24-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	58.6
Std Dev	13.49



B0L101S, U24-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

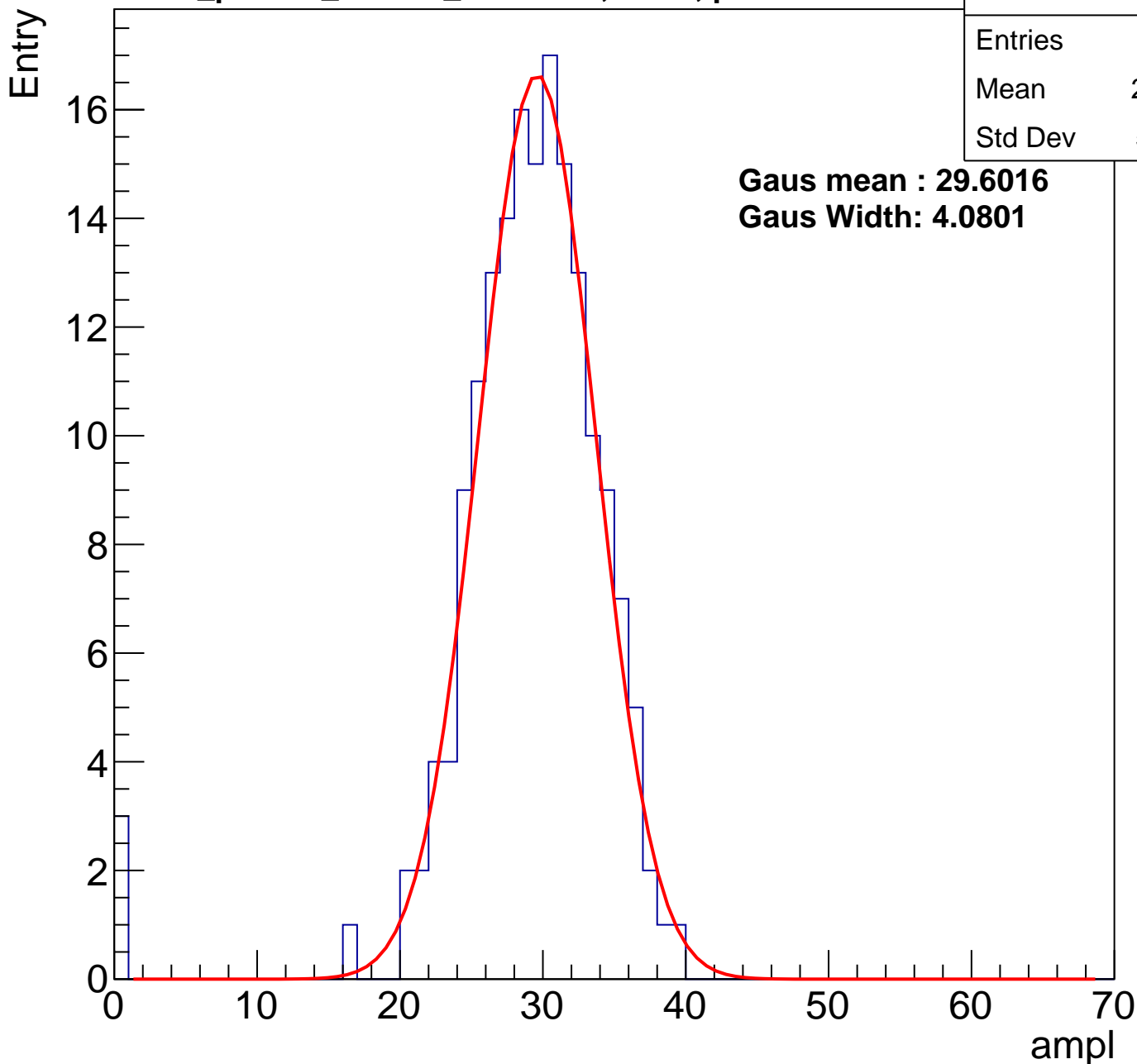
B0L101S, U24-ch113, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	28.57
Std Dev	5.501

Gaus mean : 29.6016

Gaus Width: 4.0801



B0L101S, U24-ch113, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	36.78
Std Dev	3.535

Gaus mean : 37.1882

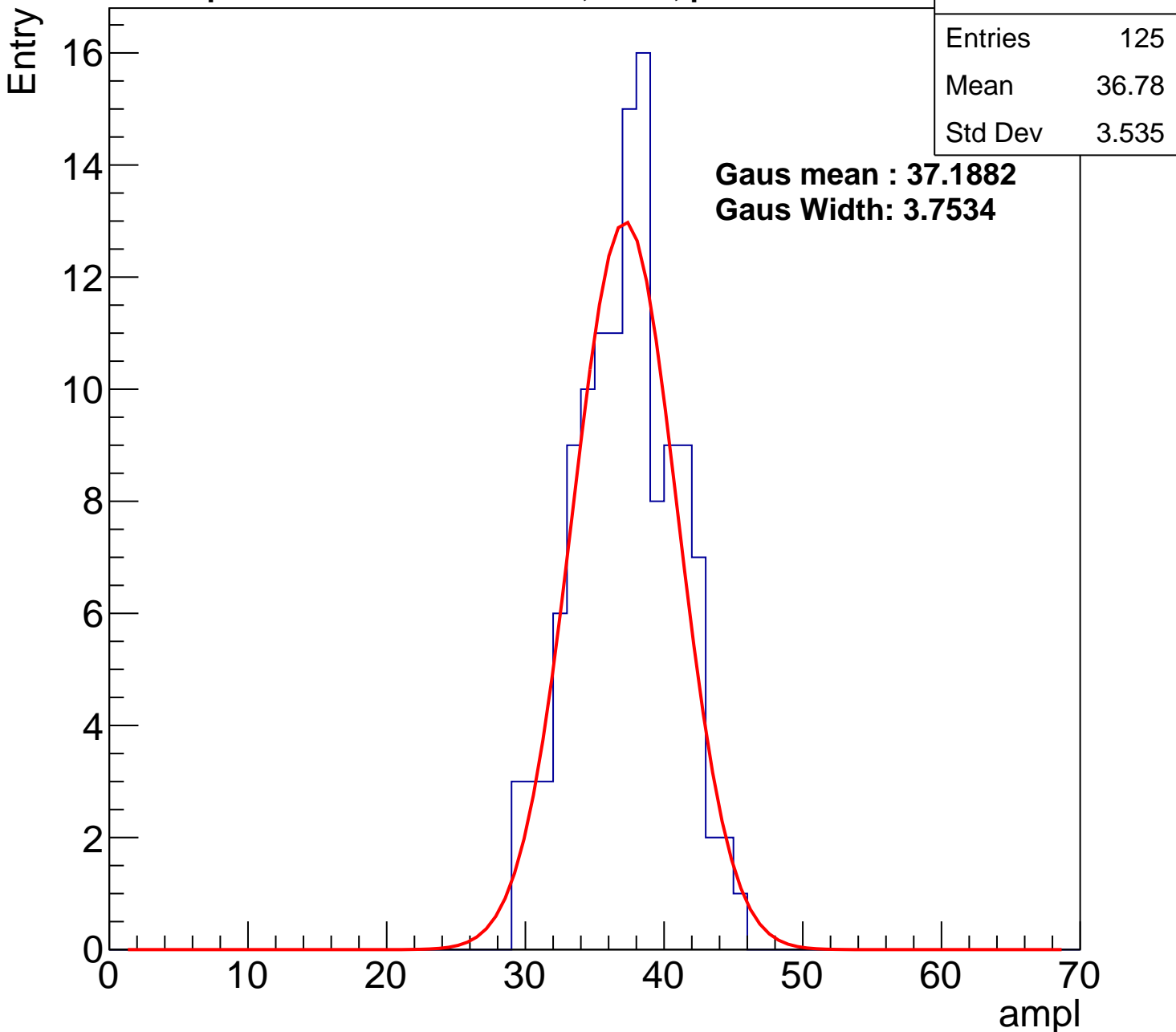
Gaus Width: 3.7534

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch113, adc2

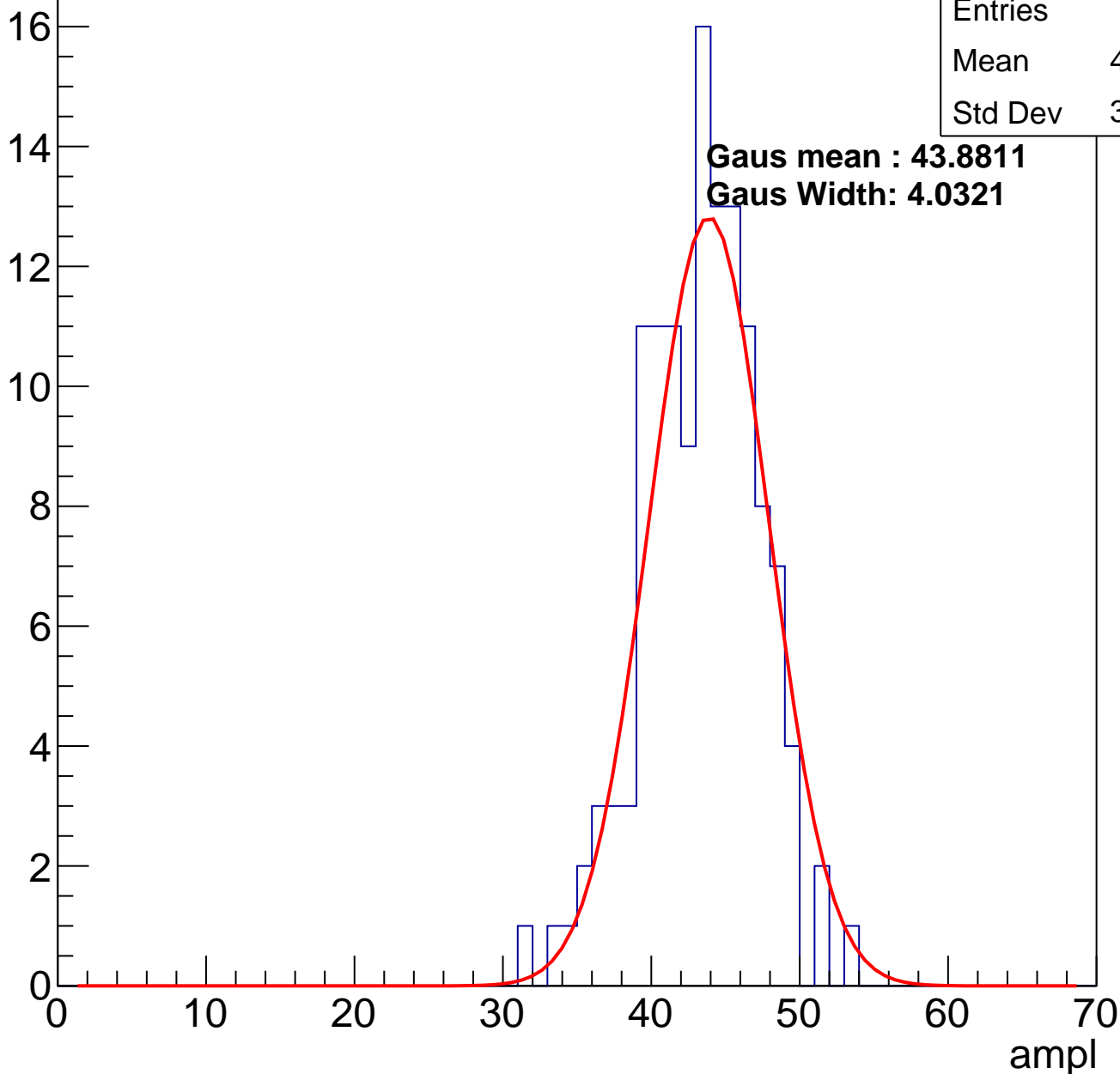
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	131
Mean	42.85
Std Dev	3.855

Gaus mean : 43.8811

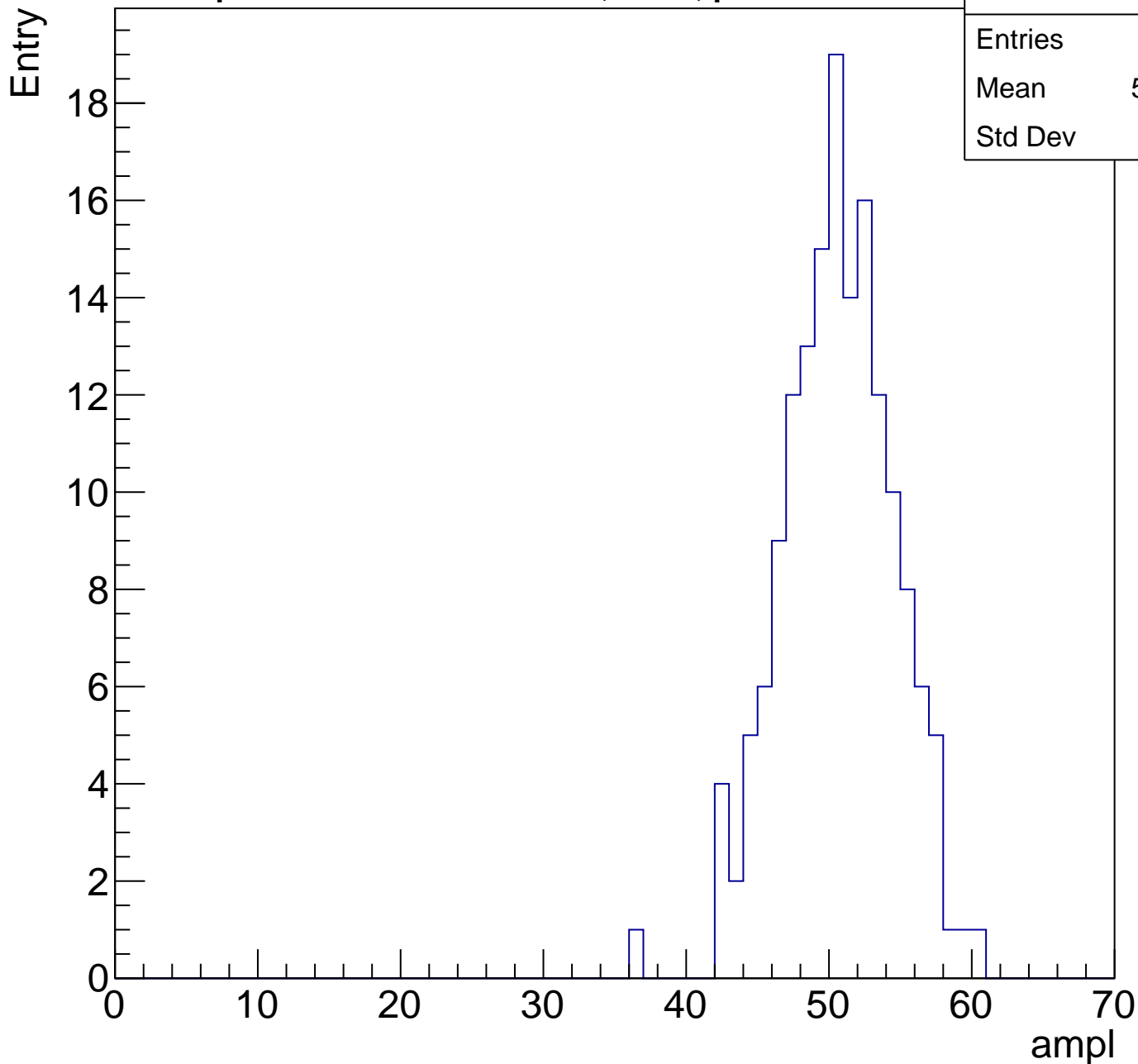
Gaus Width: 4.0321



B0L101S, U24-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

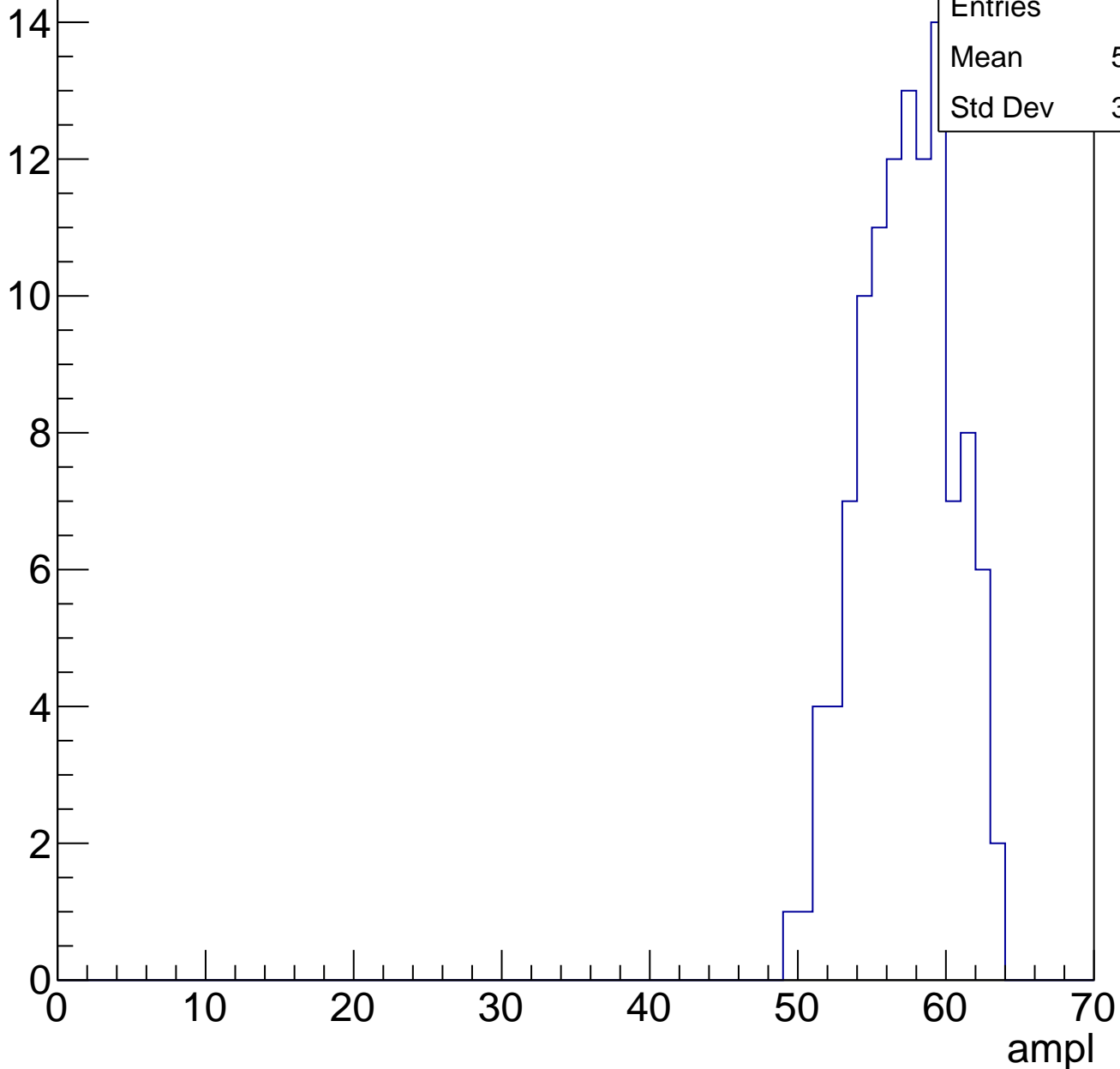
Entries	160
Mean	50.17
Std Dev	3.91



B0L101S, U24-ch113, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch113, adc5

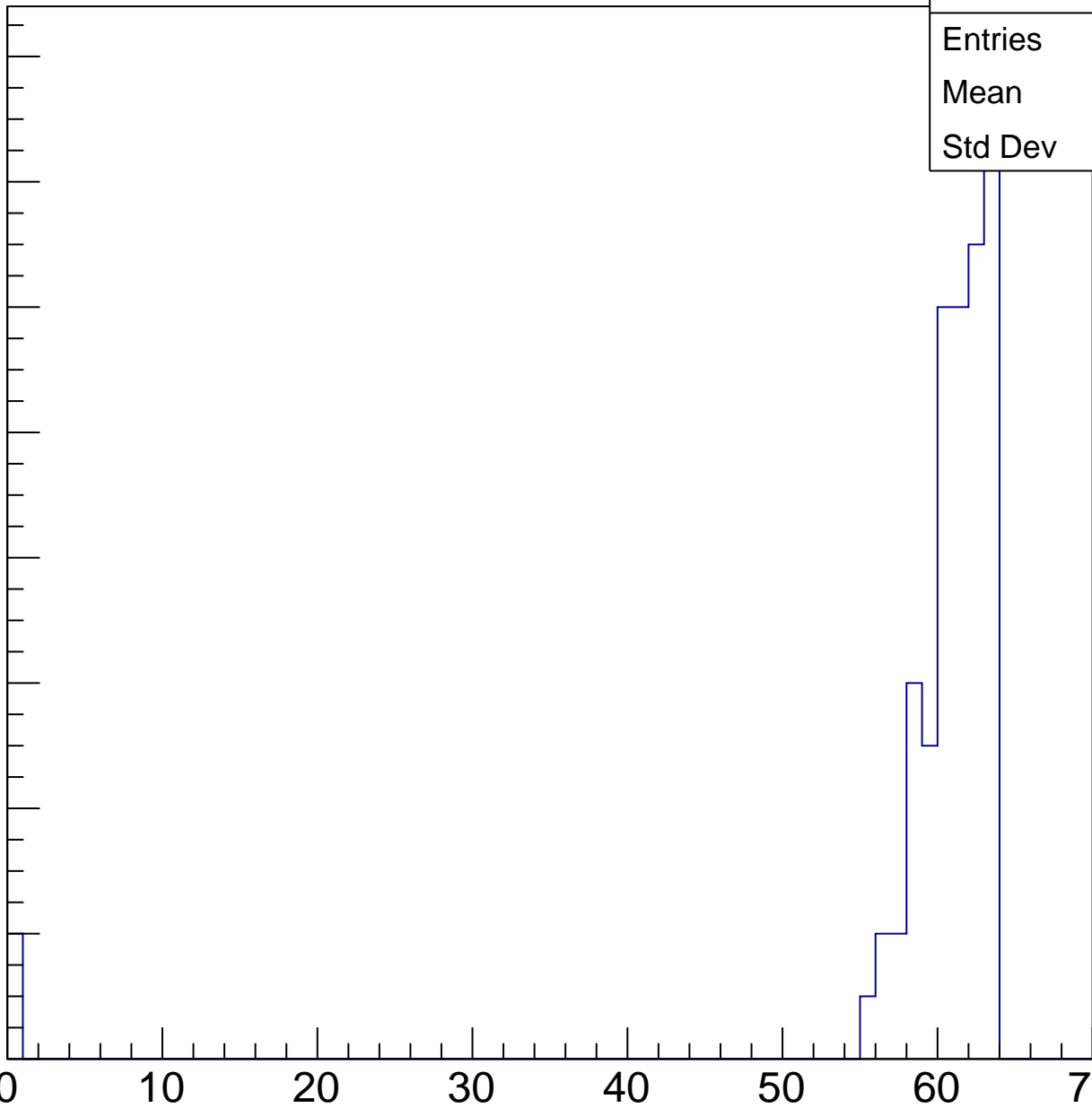
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	71
Mean	59.01
Std Dev	10.24

ampl



B0L101S, U24-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

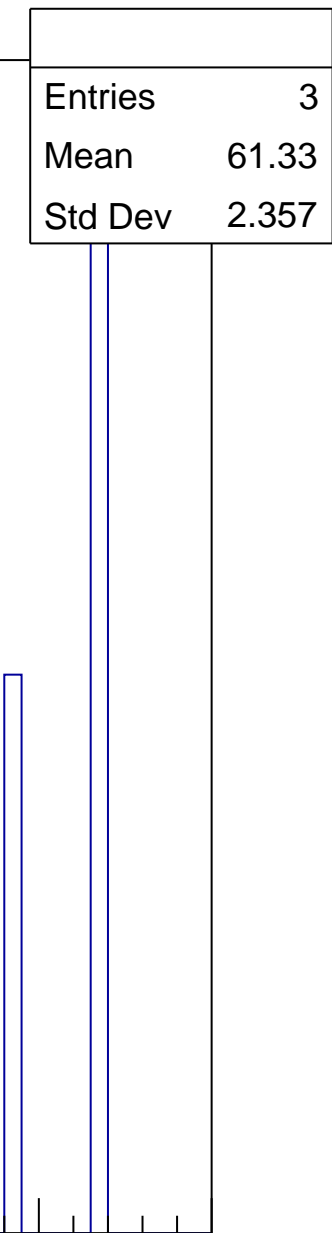
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	2.357

0 10 20 30 40 50 60 70

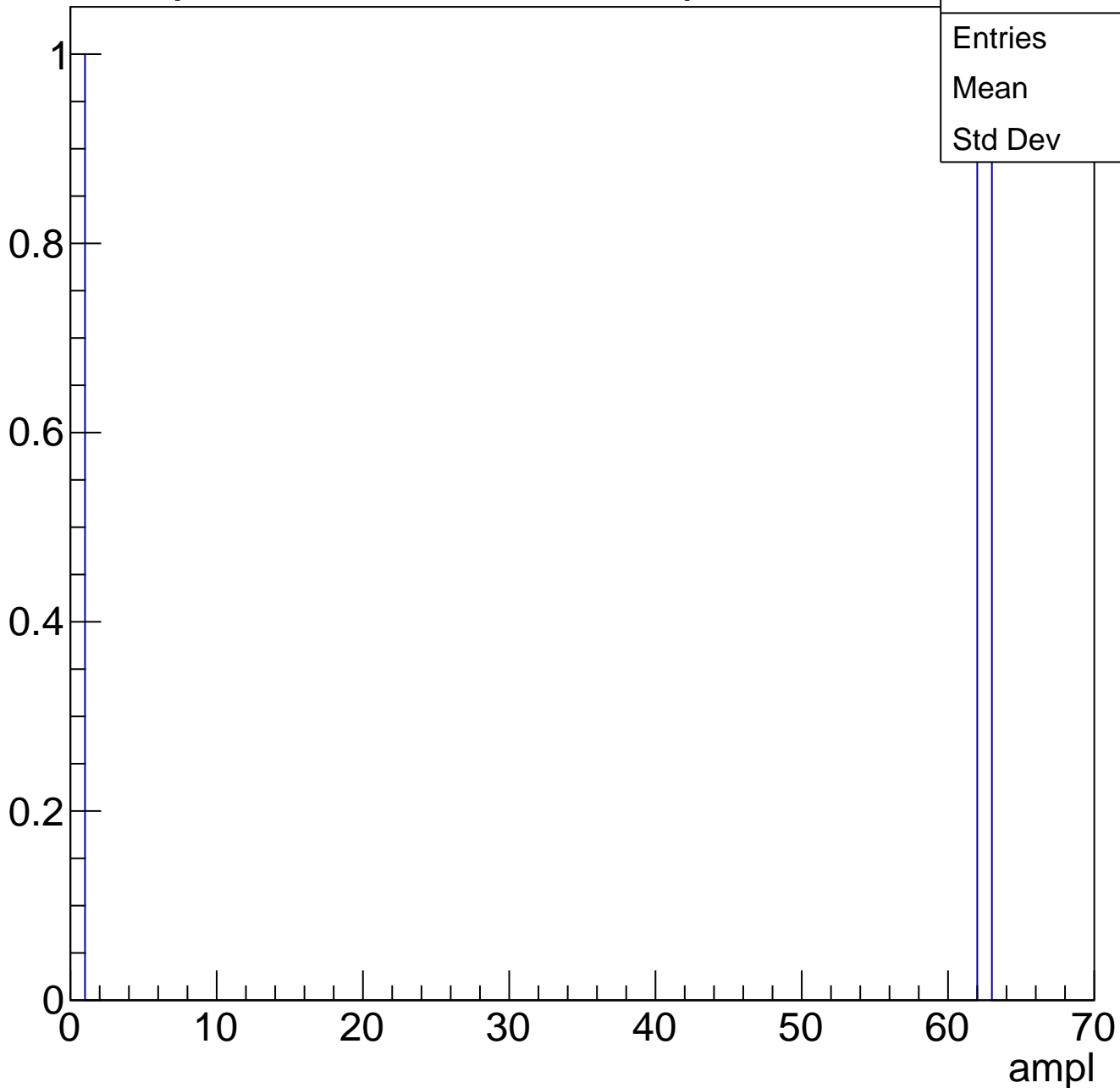
ampl



B0L101S, U24-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

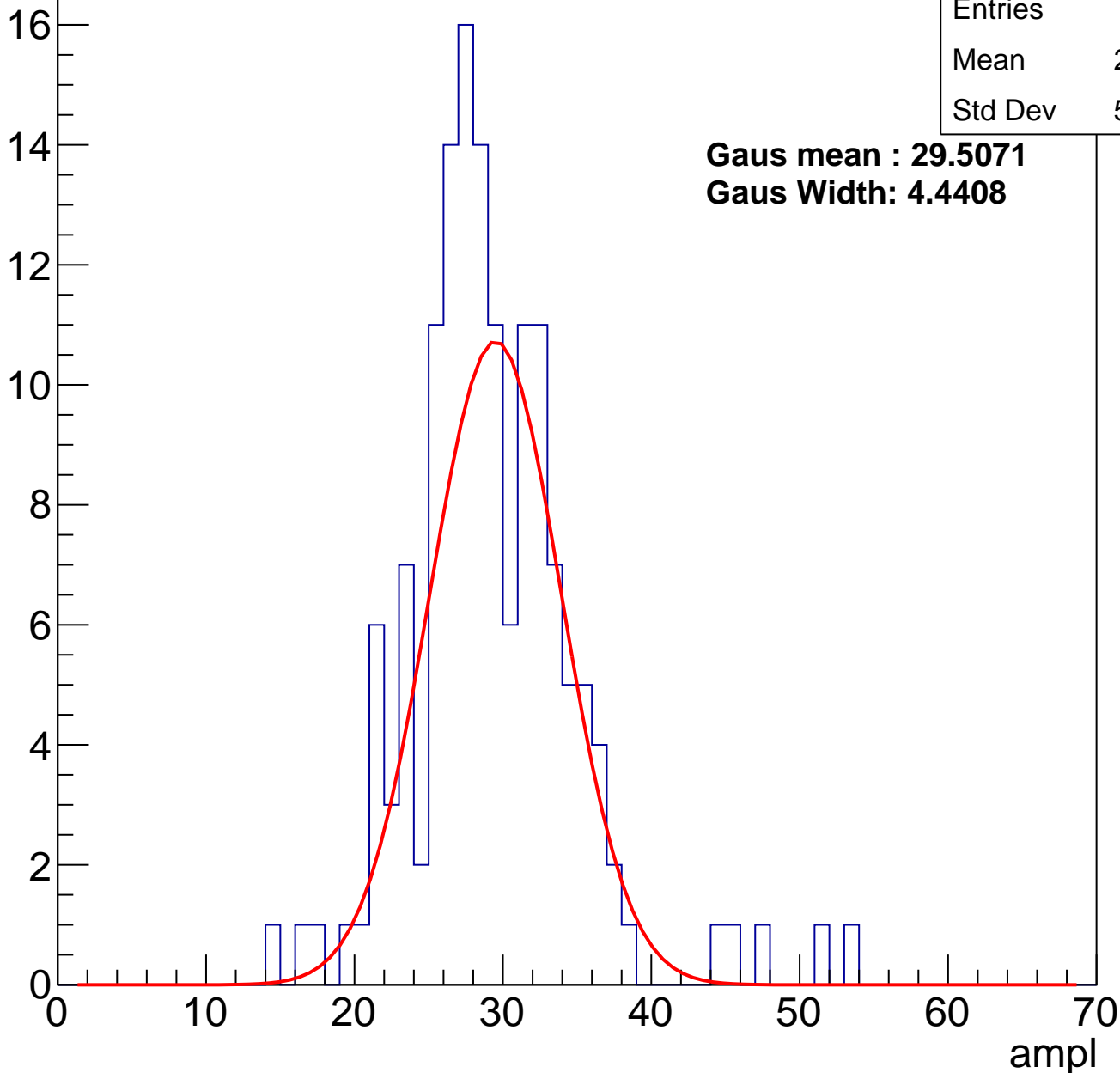
Entry



B0L101S, U24-ch114, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch114, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

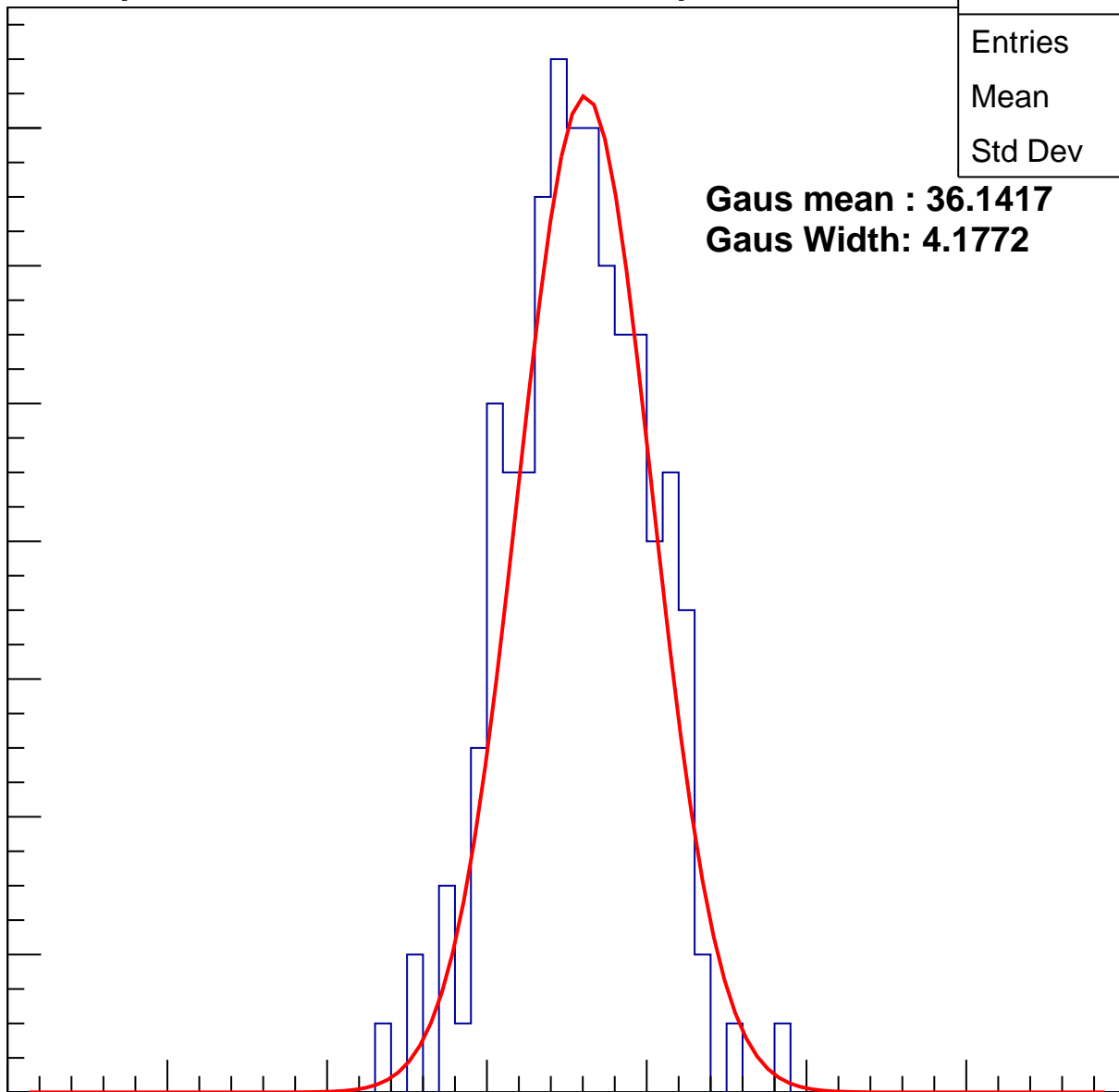
70

ampl

Entries	158
Mean	35.32
Std Dev	4.278

Gaus mean : 36.1417

Gaus Width: 4.1772



B0L101S, U24-ch114, adc2

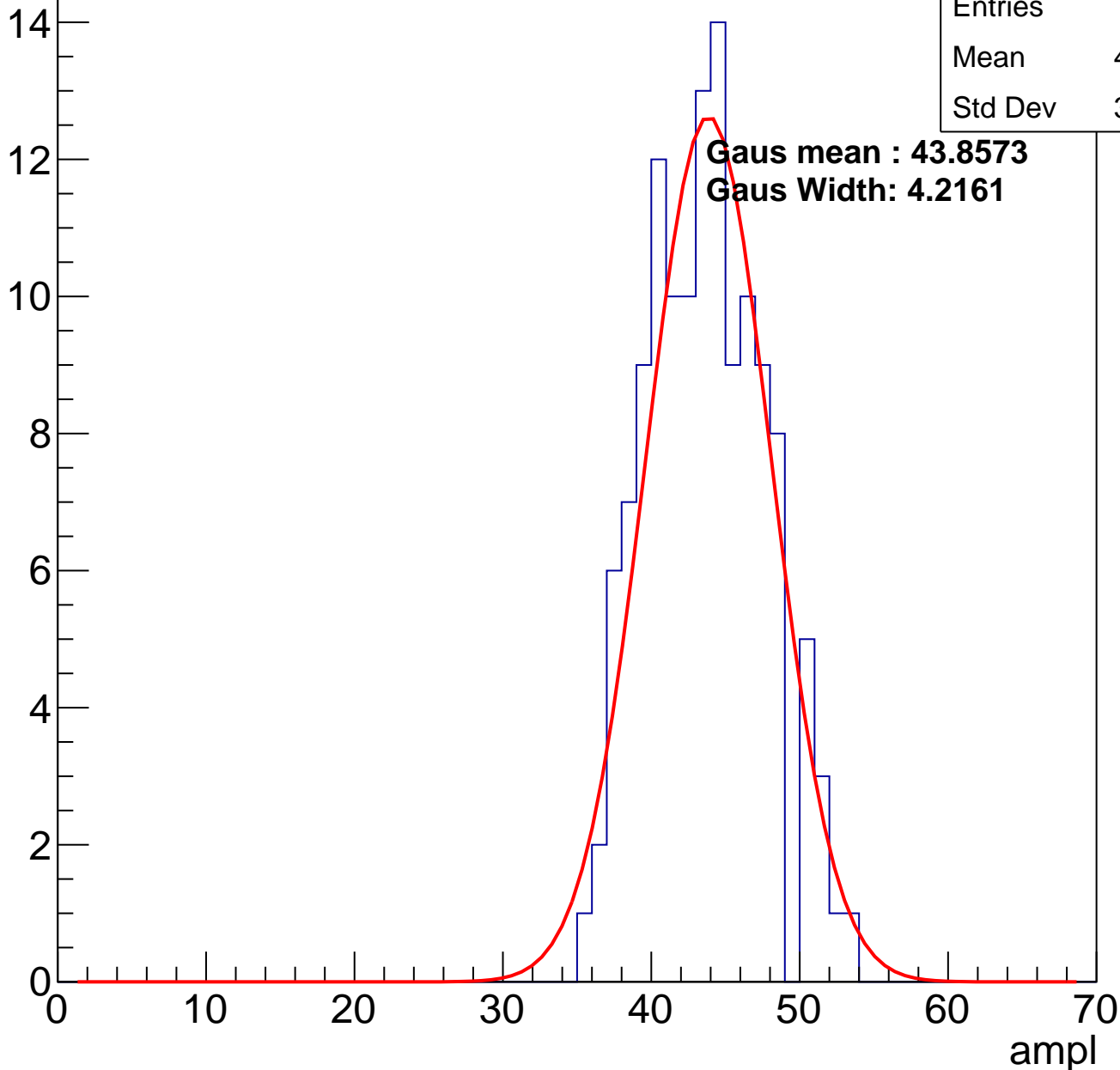
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	130
Mean	43.16
Std Dev	3.877

Gaus mean : 43.8573

Gaus Width: 4.2161



B0L101S, U24-ch114, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	48.95
Std Dev	3.855

Entry

16
14
12
10
8
6
4
2
0

0

10

20

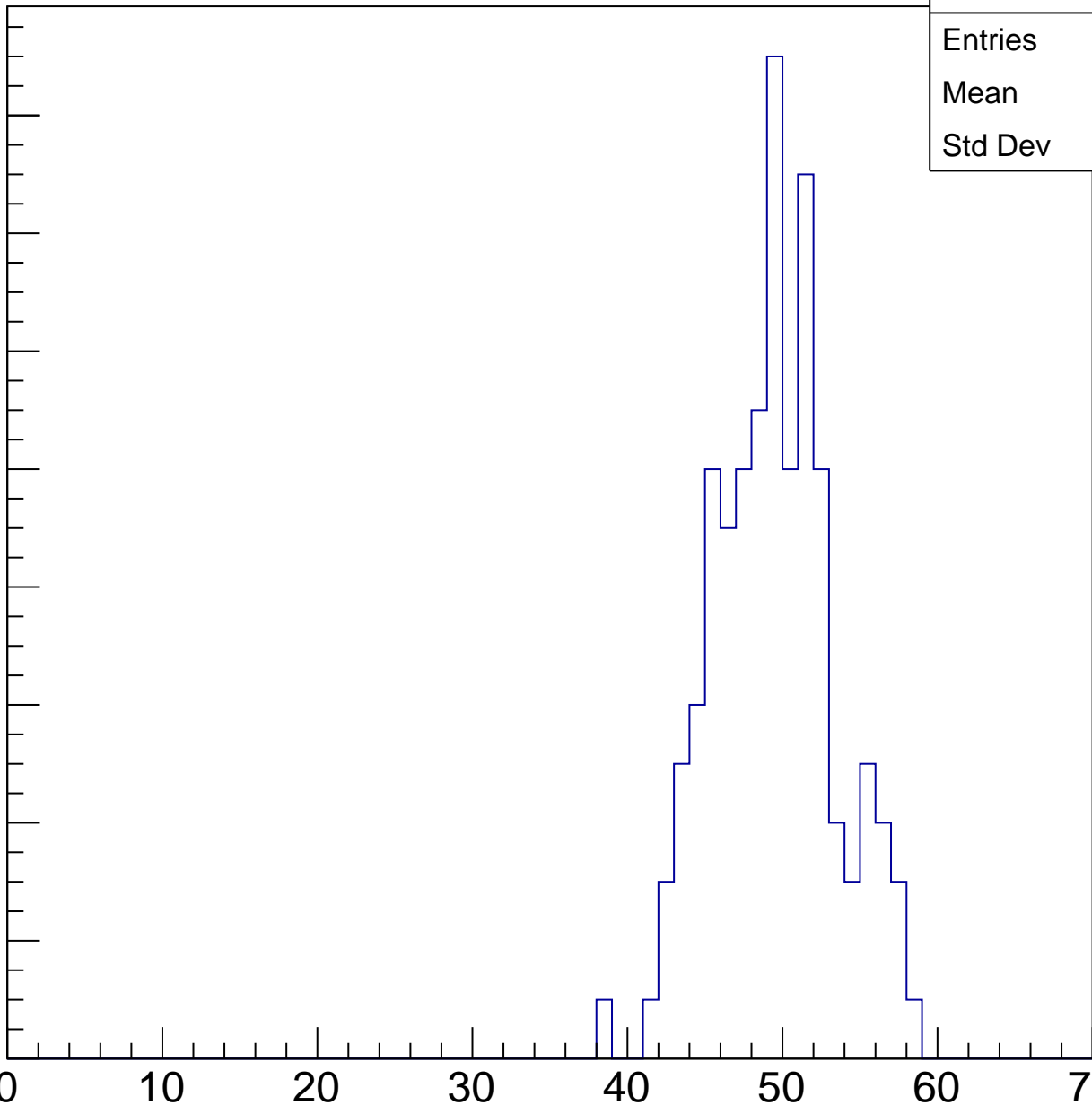
30

40

50

60

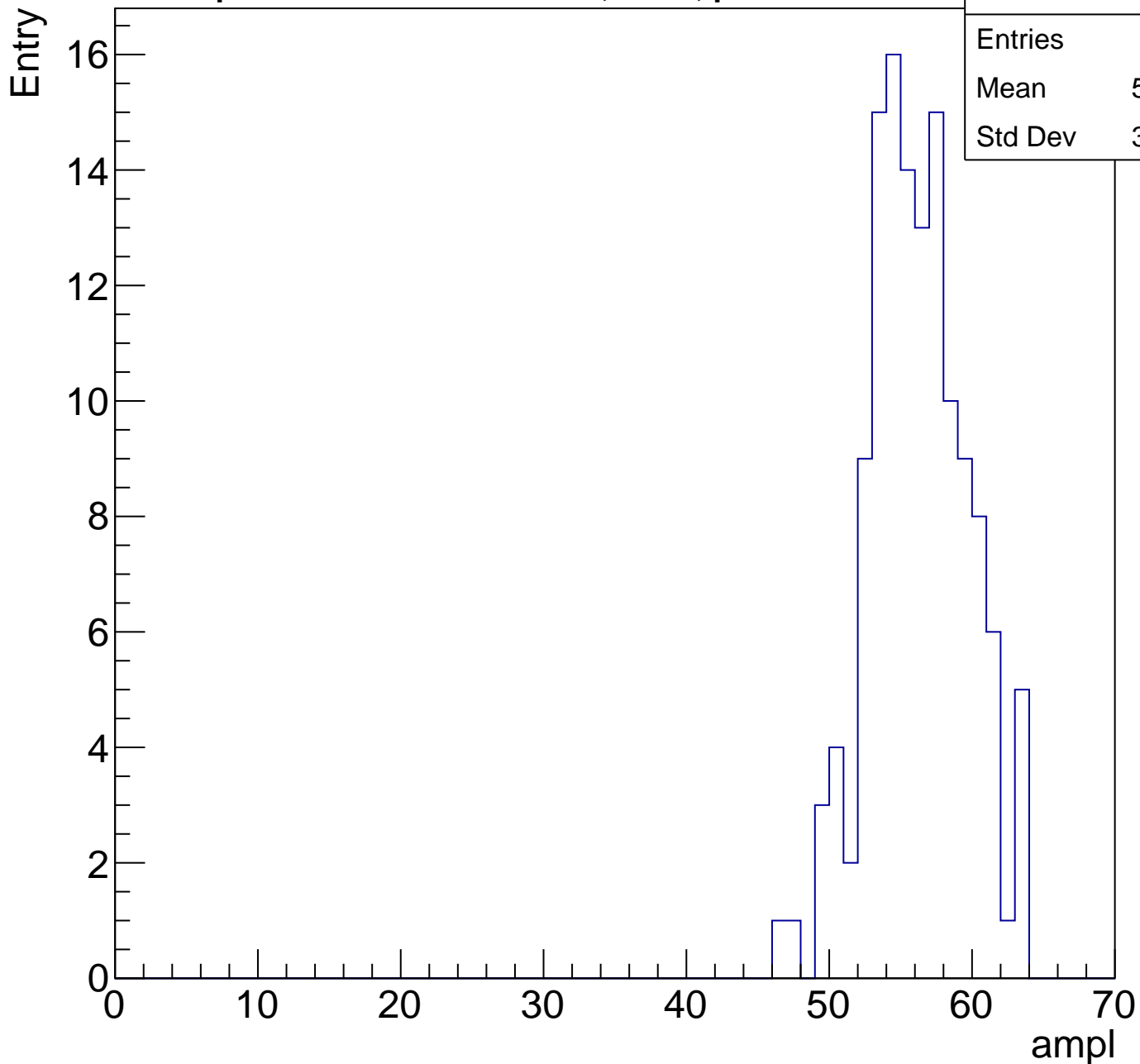
ampl



B0L101S, U24-ch114, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	55.73
Std Dev	3.442



B0L101S, U24-ch114, adc5

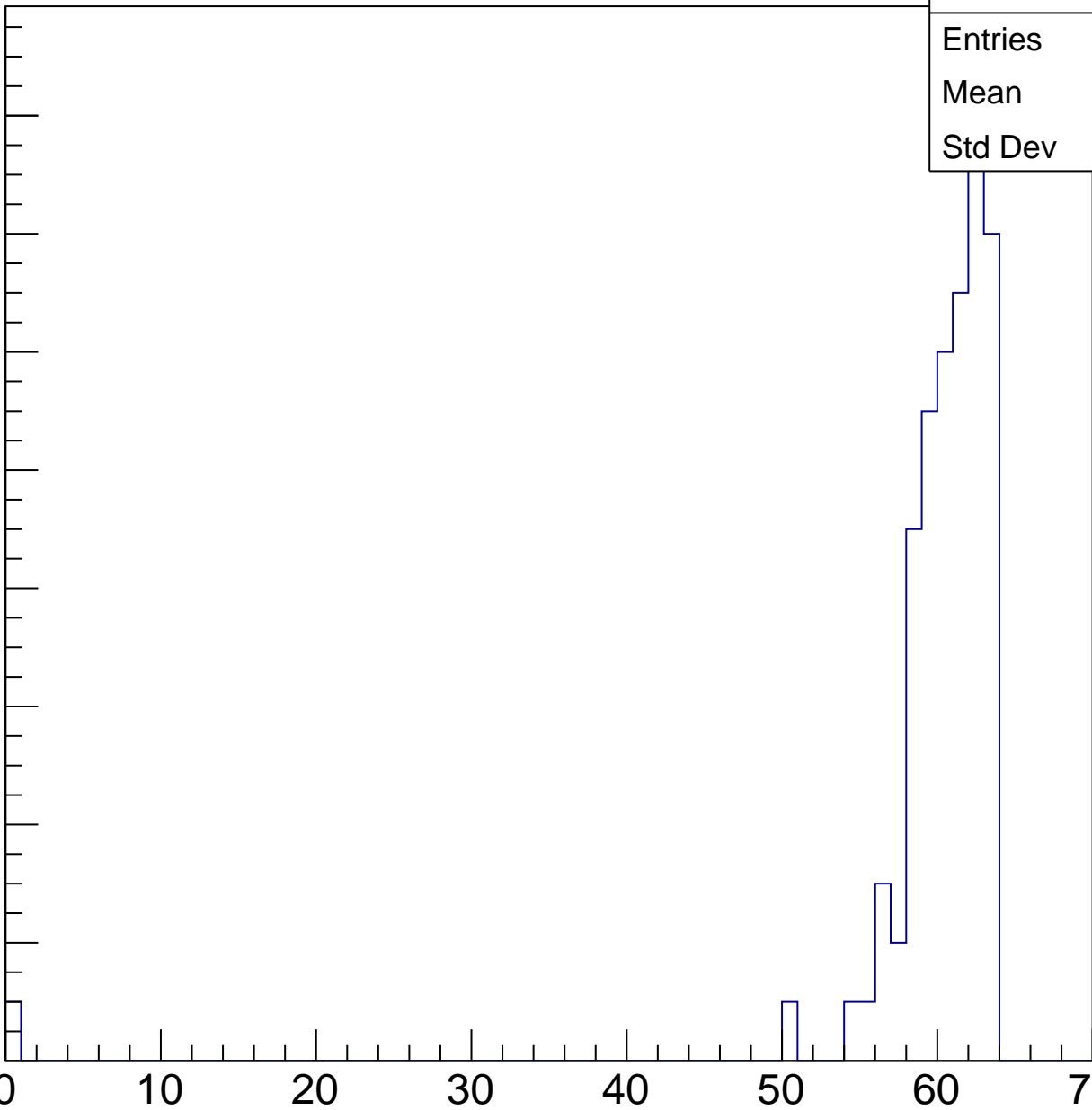
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.54
Std Dev	6.914

ampl



B0L101S, U24-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	49.6
Std Dev	24.81

B0L101S, U24-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch115, adc0

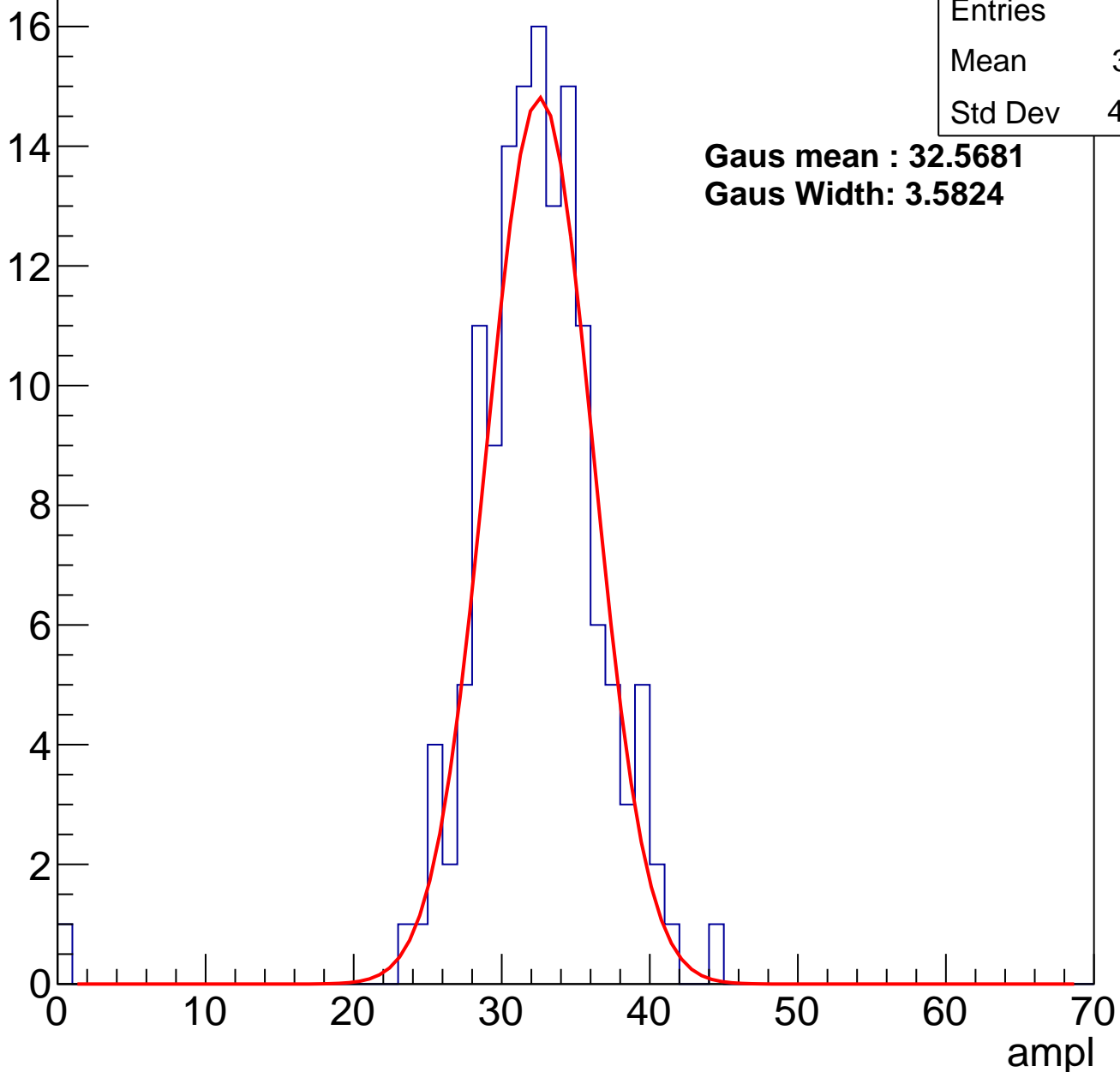
calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	31.91
Std Dev	4.603

Gaus mean : 32.5681

Gaus Width: 3.5824

Entry



B0L101S, U24-ch115, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

154

Mean

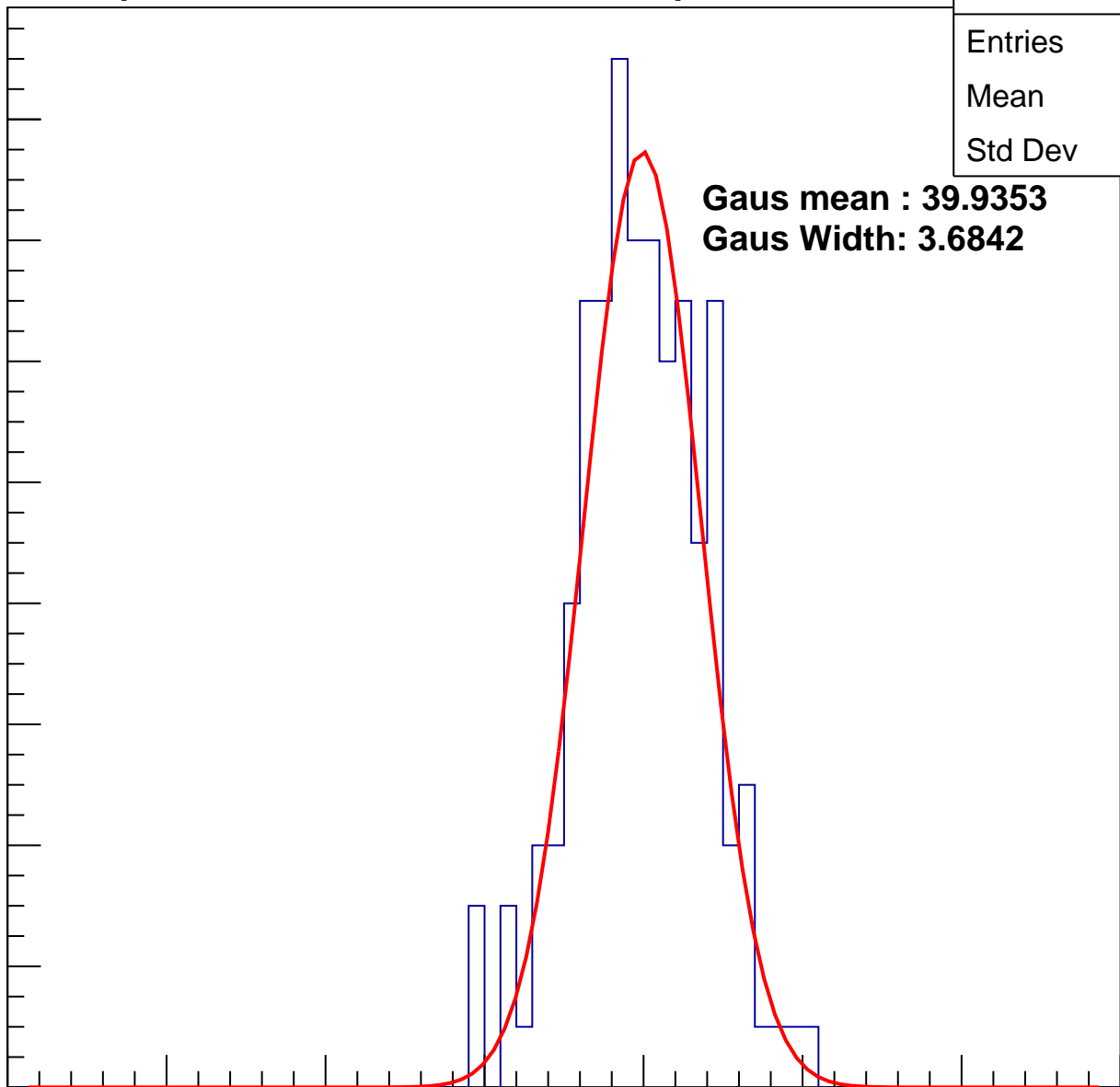
39.36

Std Dev

4.014

Gaus mean : 39.9353

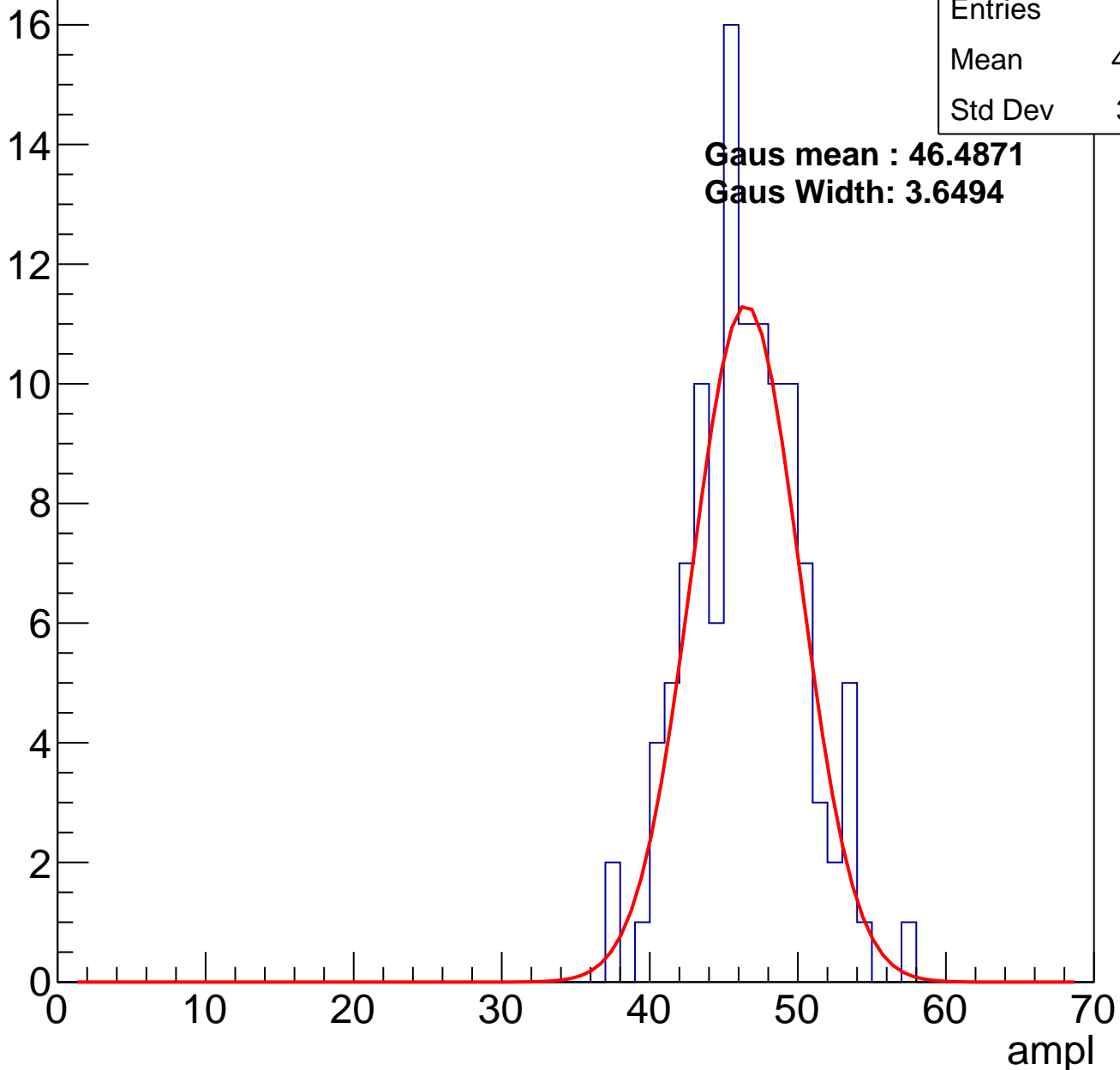
Gaus Width: 3.6842



B0L101S, U24-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

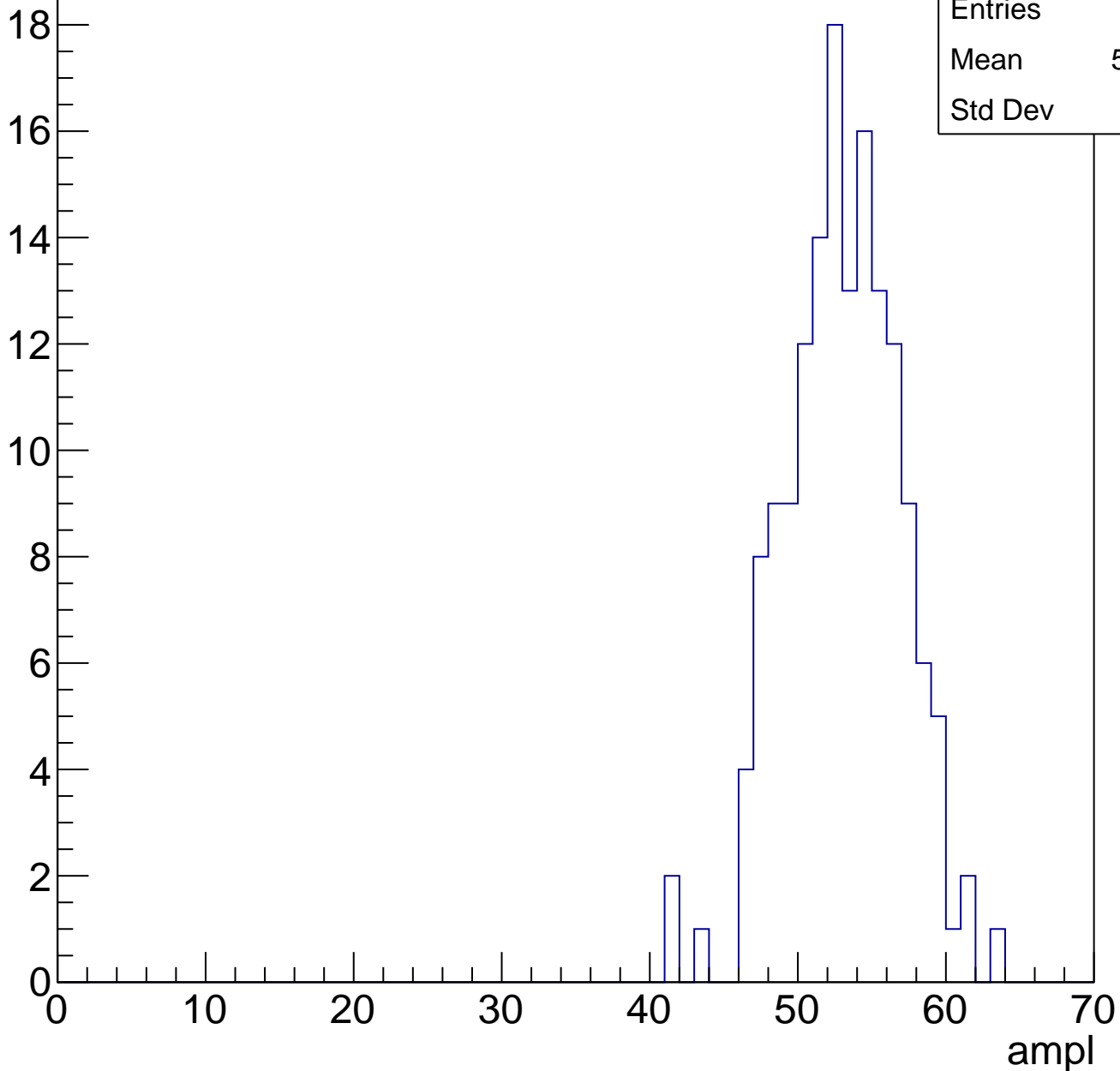


B0L101S, U24-ch115, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	52.57
Std Dev	3.87

Entry



B0L101S, U24-ch115, adc4

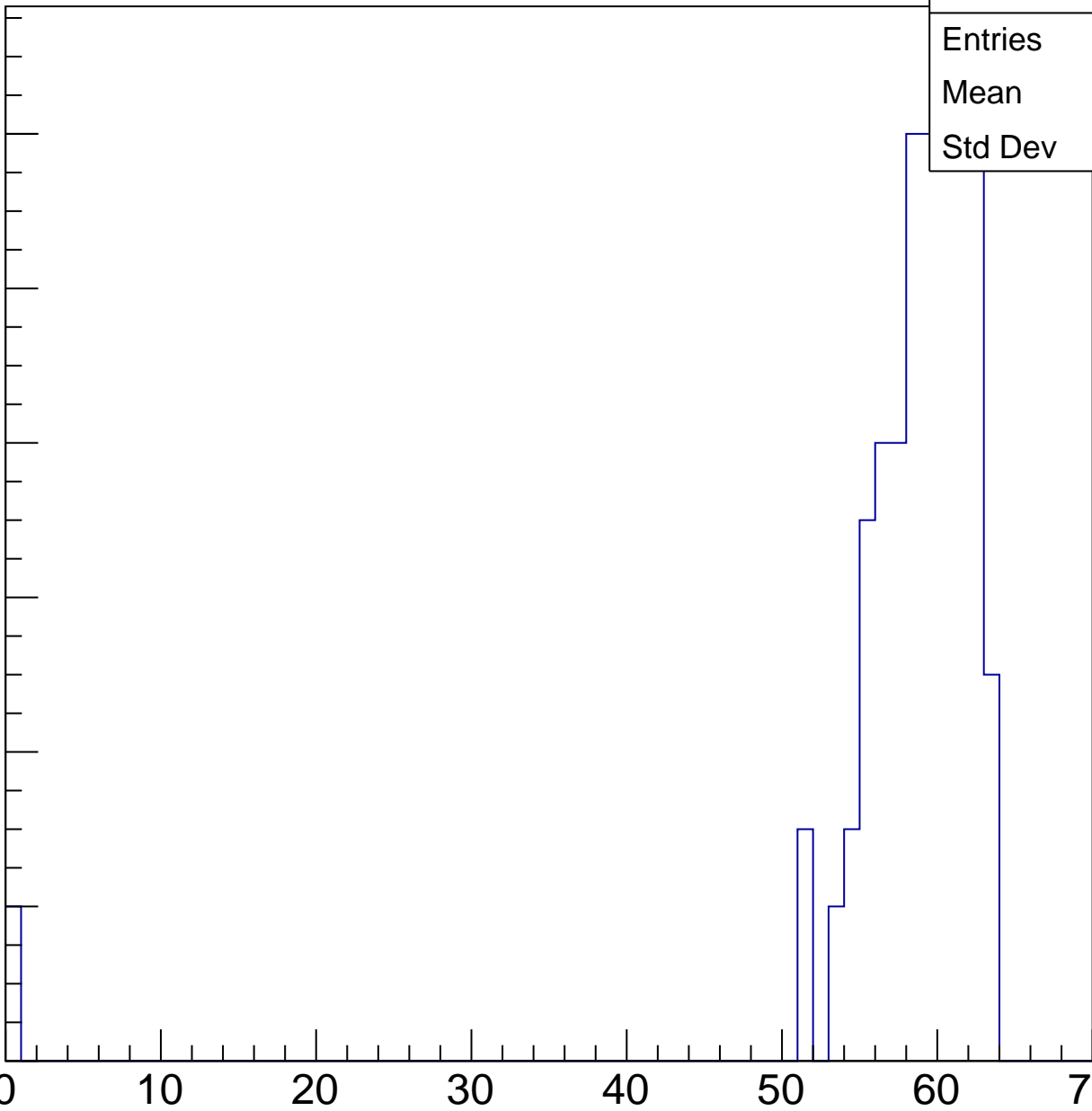
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	101
Mean	57.5
Std Dev	8.655

ampl



B0L101S, U24-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

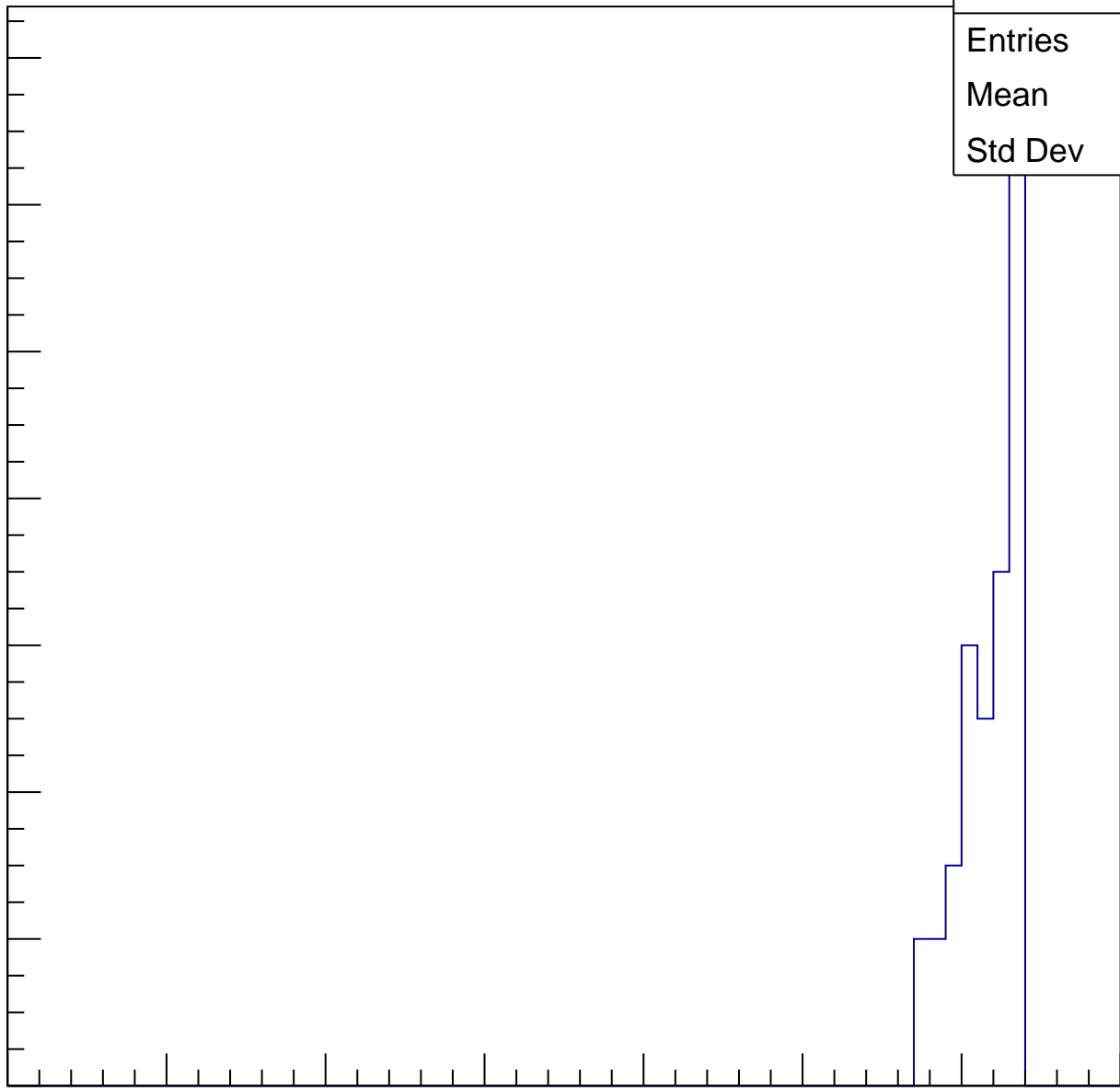
Entries	39
Mean	61.23
Std Dev	1.818

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch116, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	27.31
Std Dev	6.166

Gaus mean : 29.2327

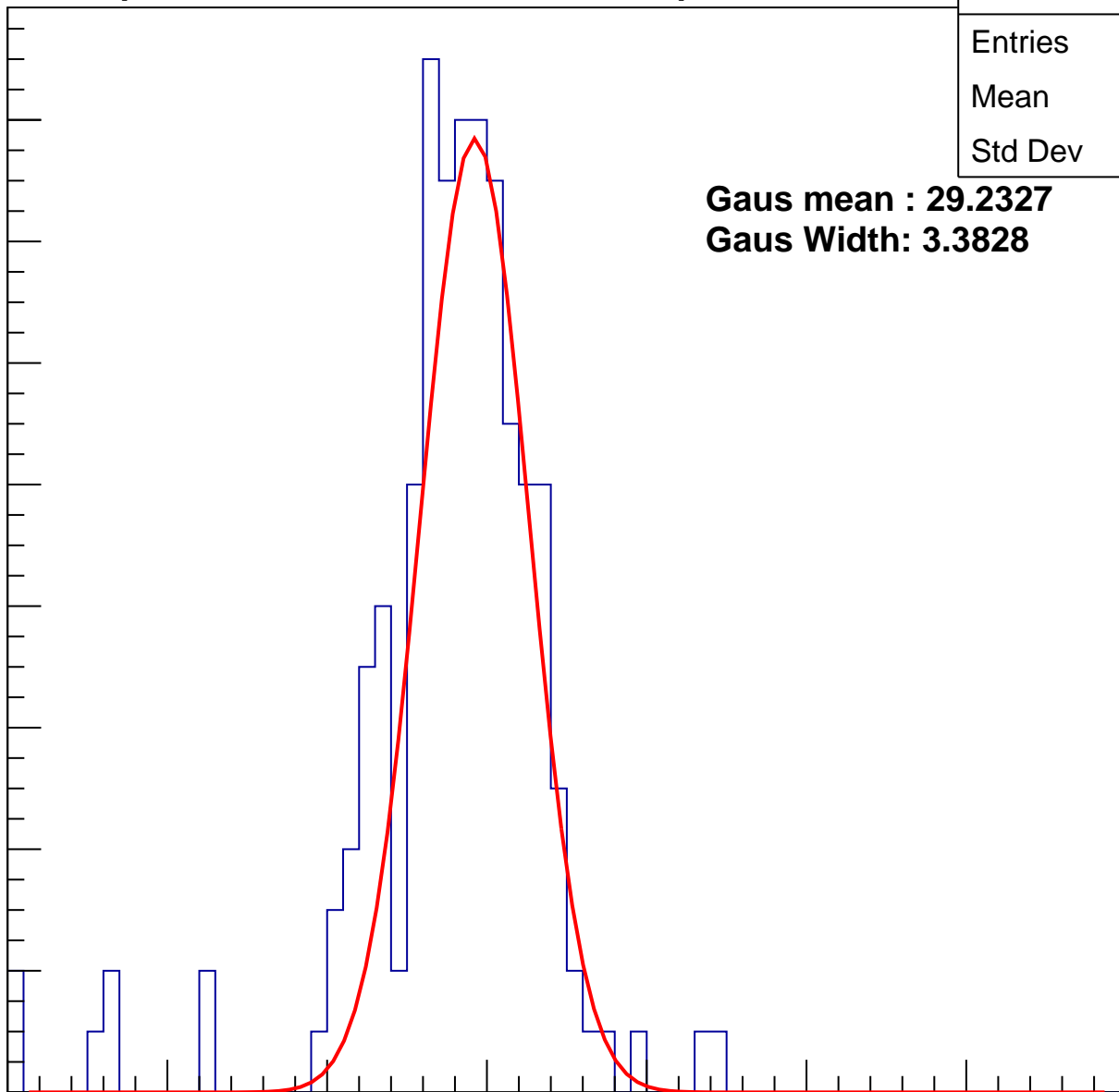
Gaus Width: 3.3828

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



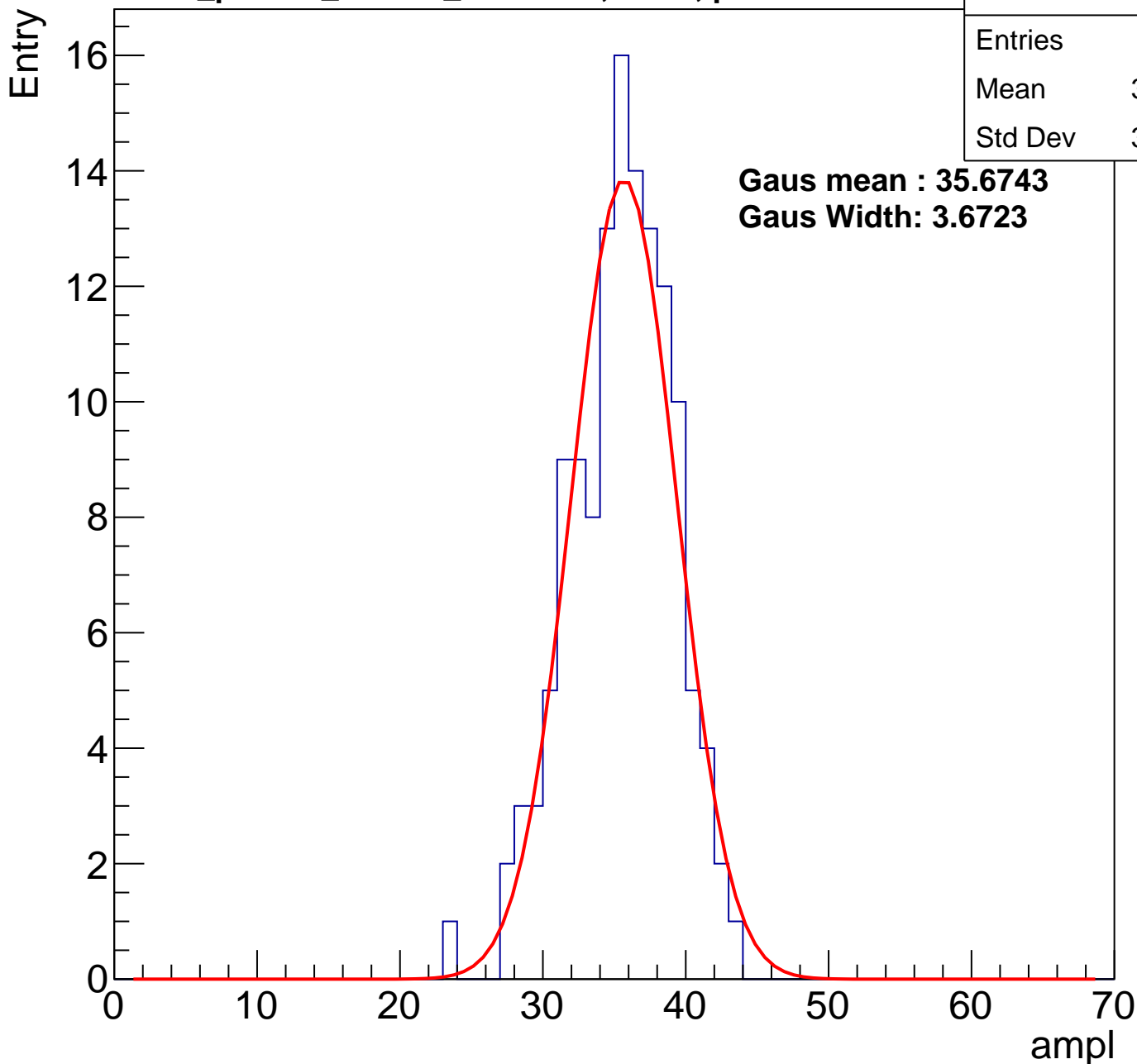
B0L101S, U24-ch116, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	35.02
Std Dev	3.602

Gaus mean : 35.6743

Gaus Width: 3.6723



B0L101S, U24-ch116, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

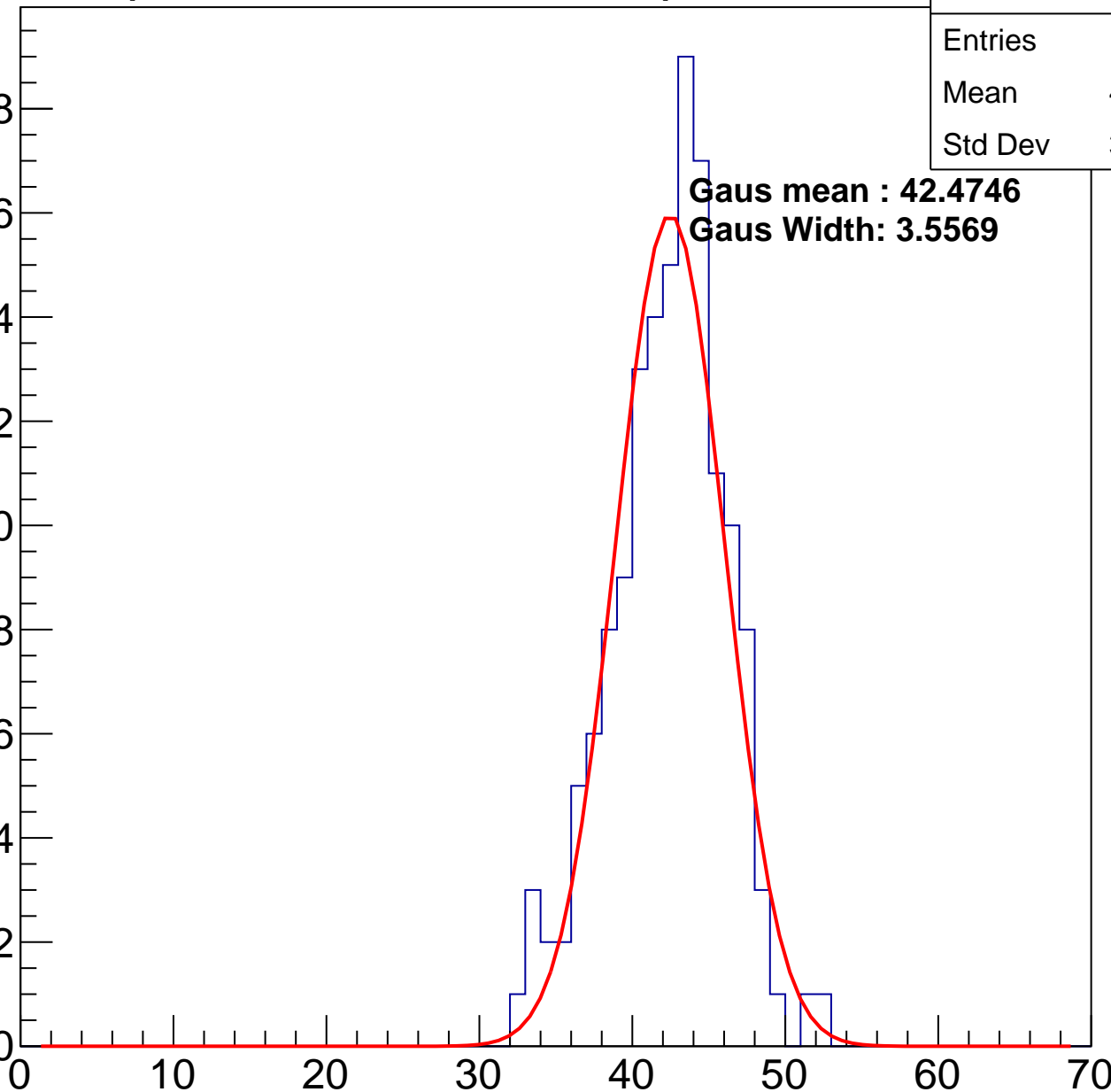
18
16
14
12
10
8
6
4
2
0

Entries	149
Mean	41.89
Std Dev	3.715

Gaus mean : 42.4746

Gaus Width: 3.5569

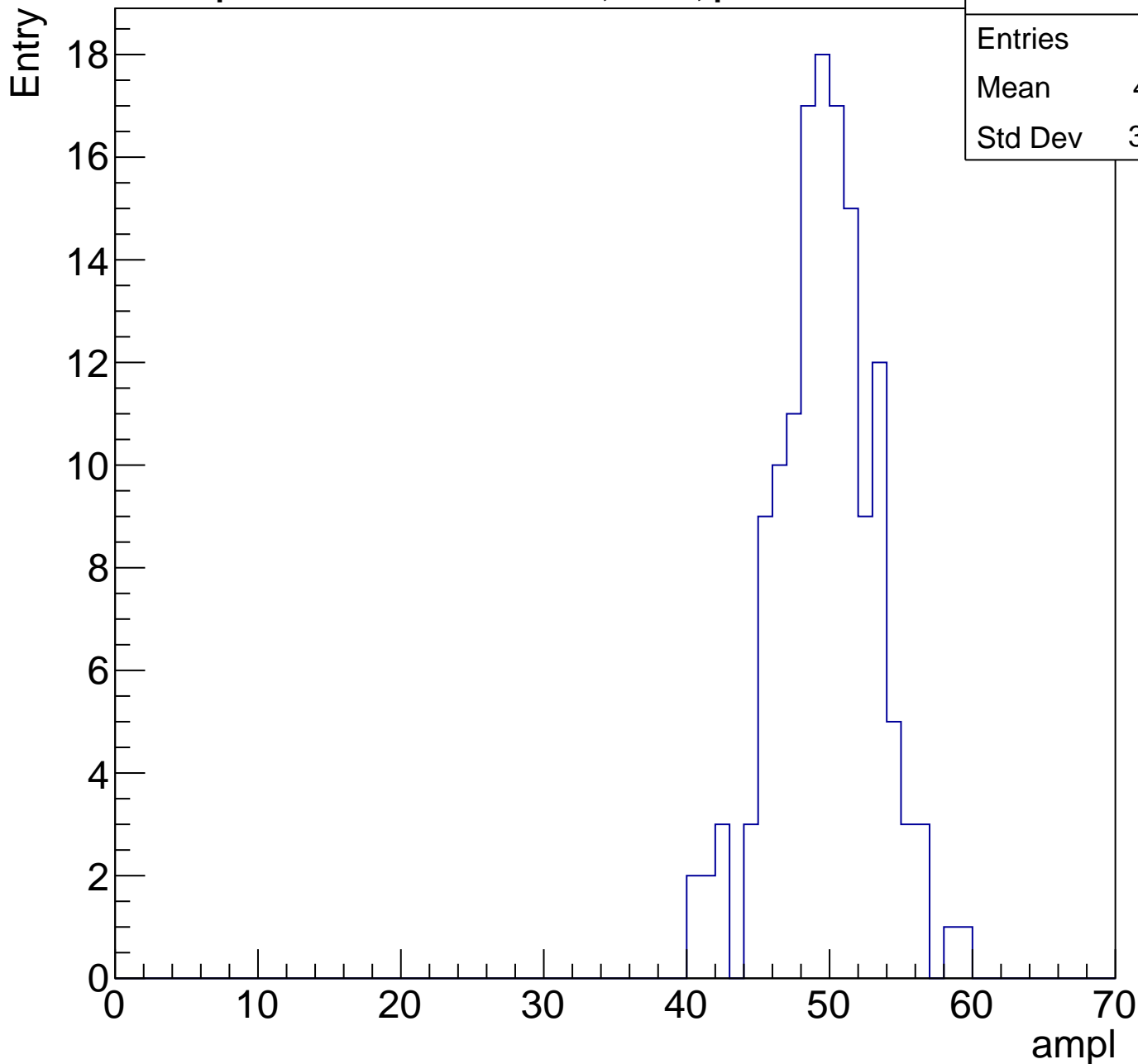
ampl



B0L101S, U24-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	49.21
Std Dev	3.484

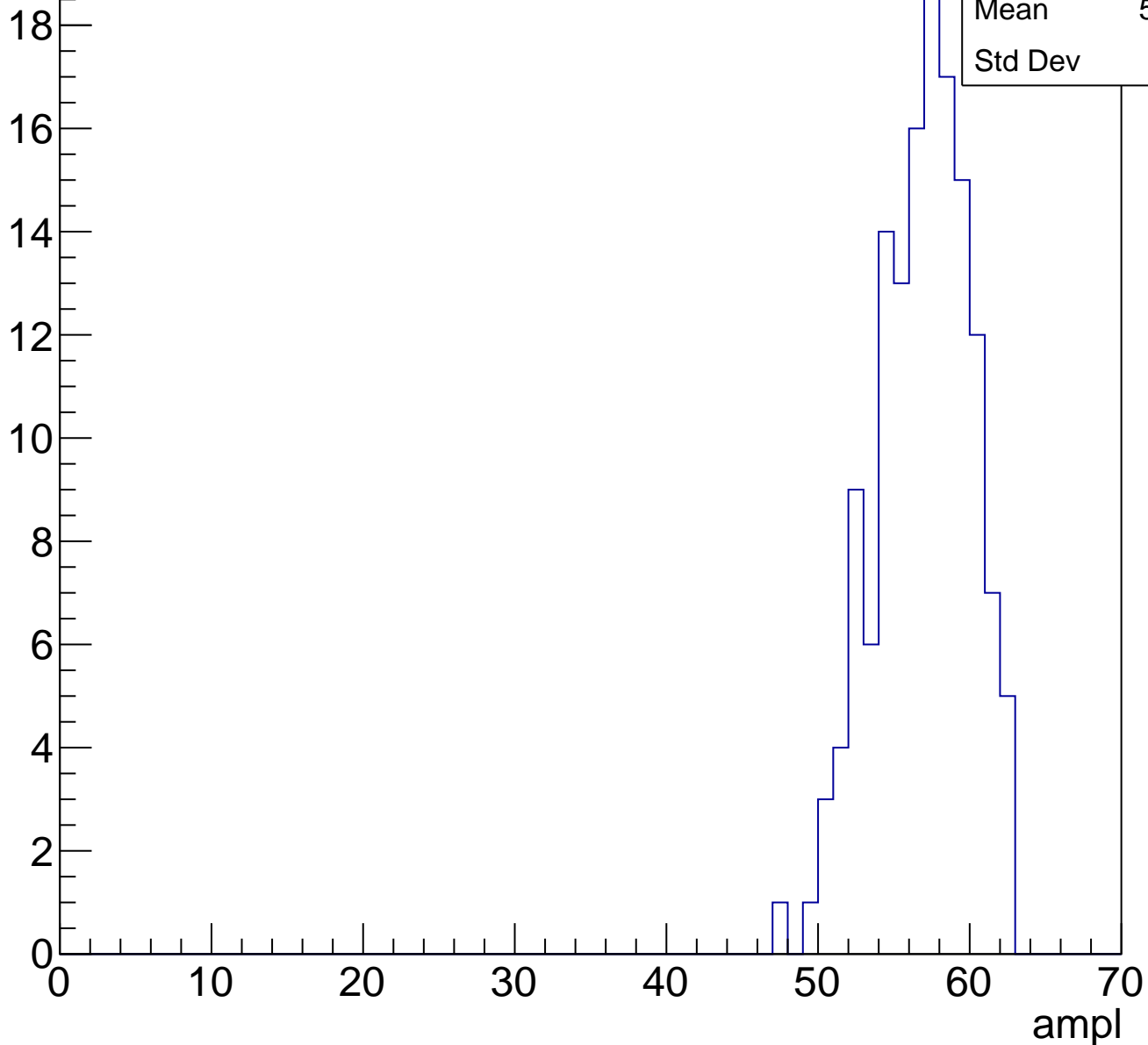


B0L101S, U24-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	56.44
Std Dev	3.08

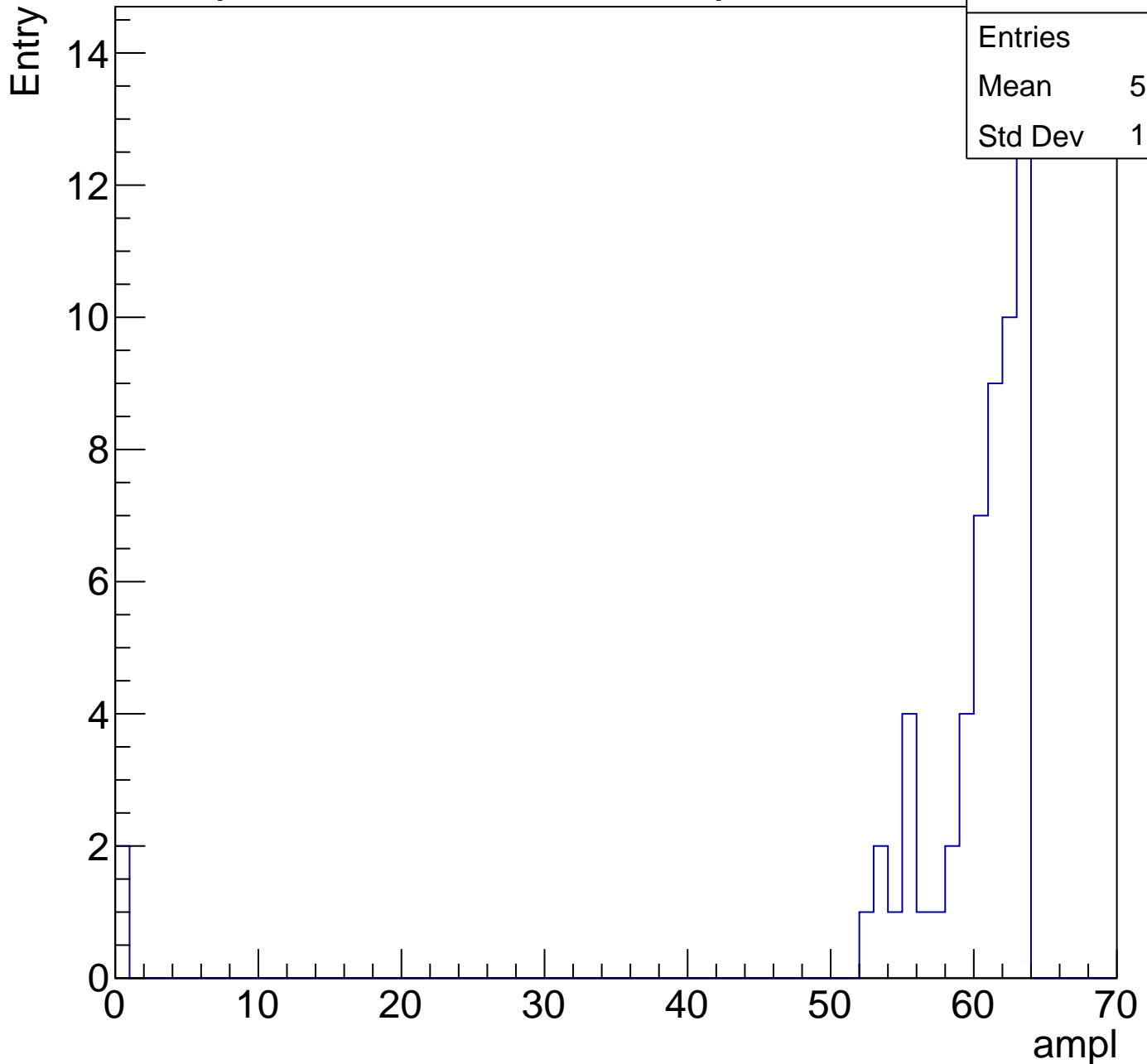
Entry



B0L101S, U24-ch116, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	58
Mean	58.07
Std Dev	11.37

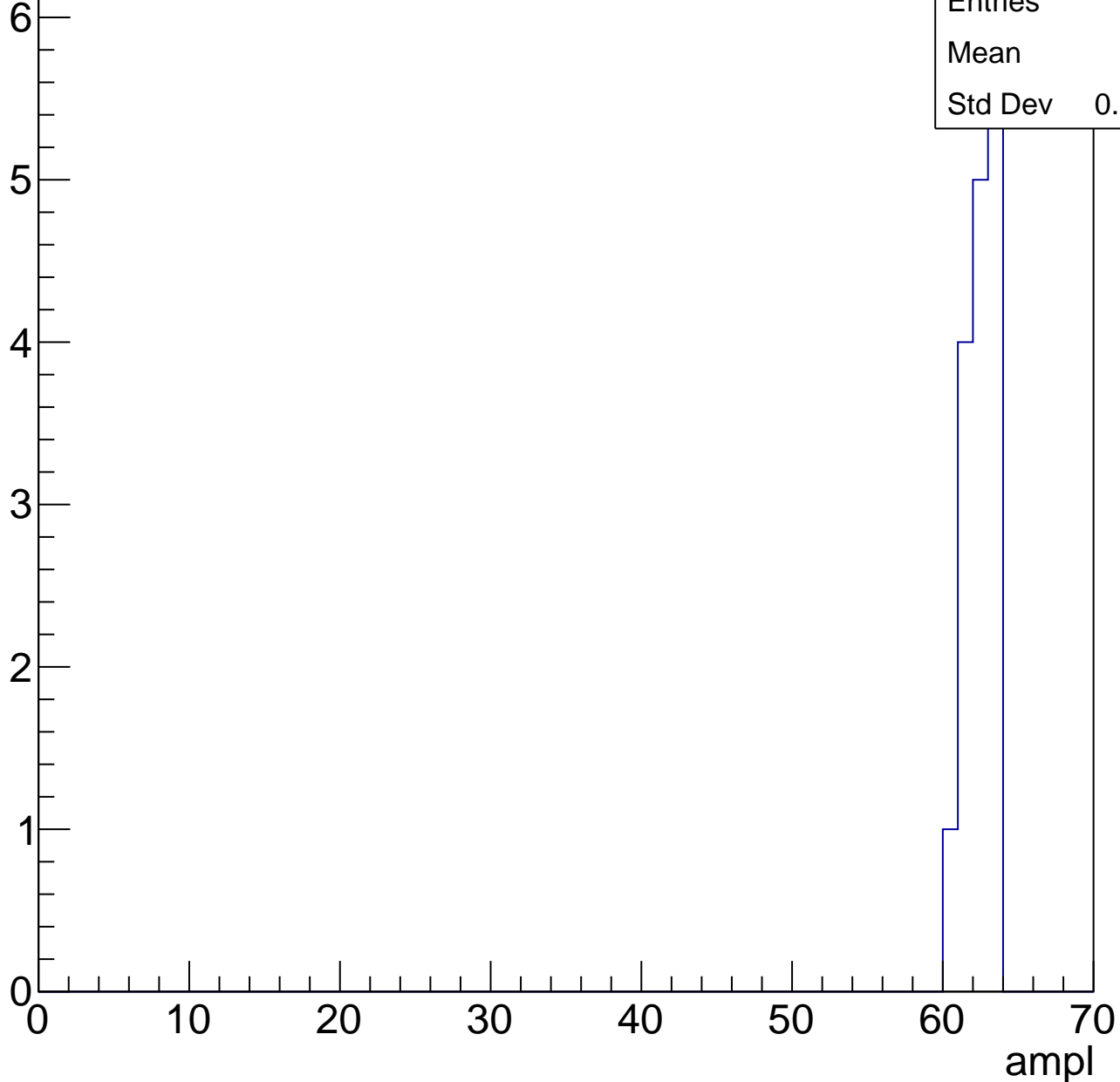


B0L101S, U24-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	62
Std Dev	0.9354



B0L101S, U24-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U24-ch117, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	30.79
Std Dev	3.877

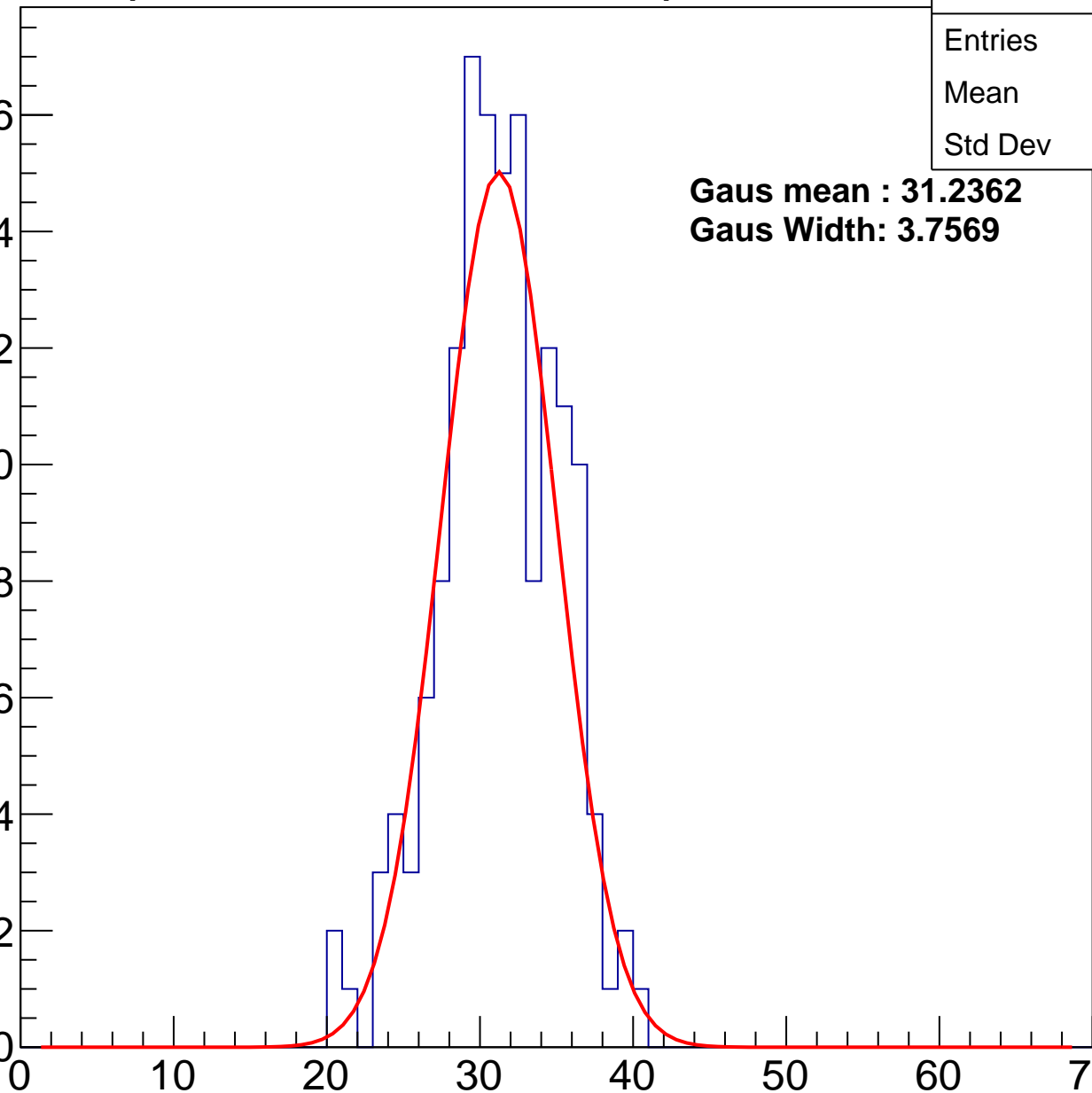
Gaus mean : 31.2362

Gaus Width: 3.7569

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch117, adc1

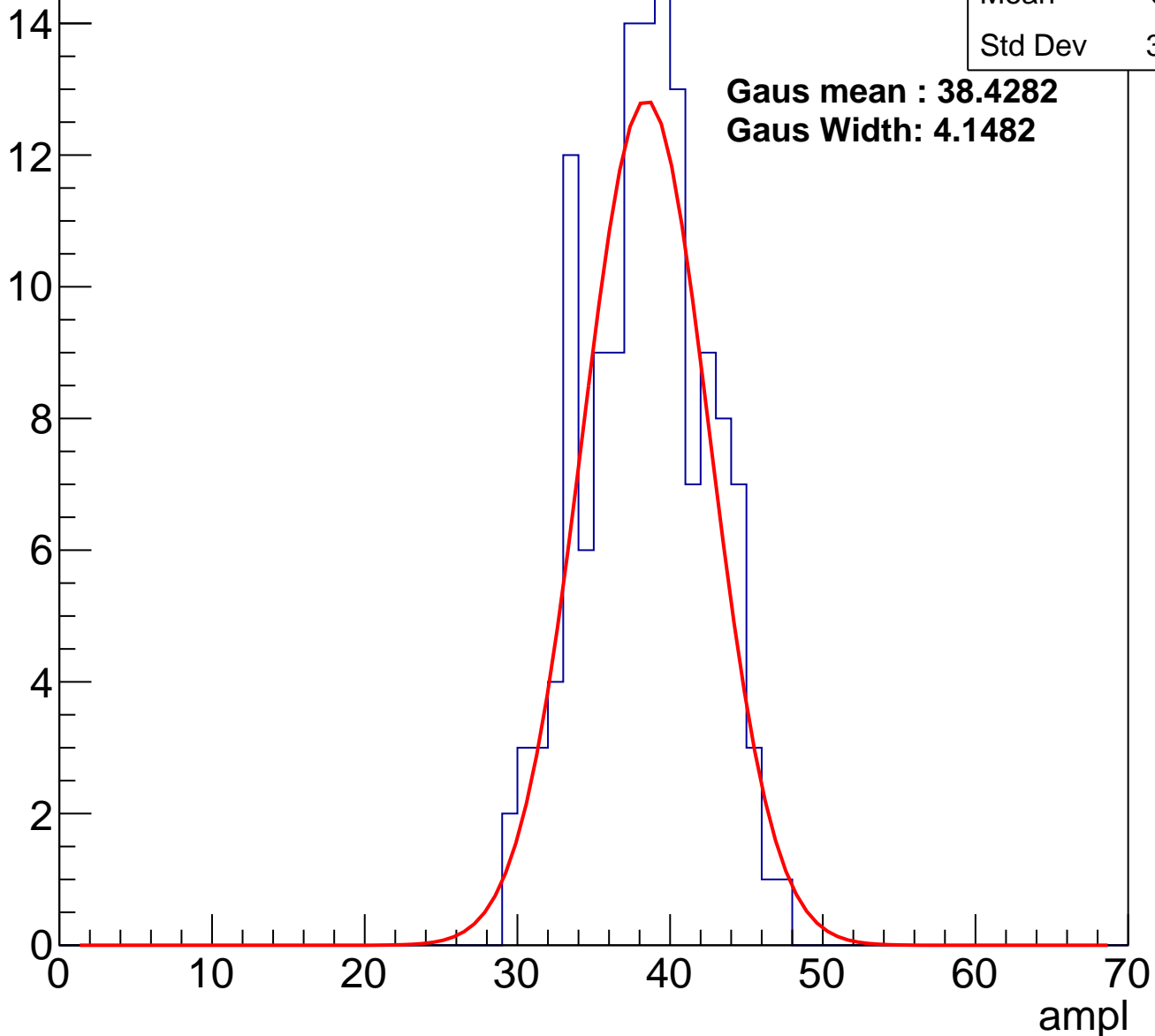
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	37.91
Std Dev	3.954

Gaus mean : 38.4282

Gaus Width: 4.1482

Entry



B0L101S, U24-ch117, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

148

Mean

44.88

Std Dev

3.921

Gaus mean : 45.5575

Gaus Width: 4.0849

0

10

20

30

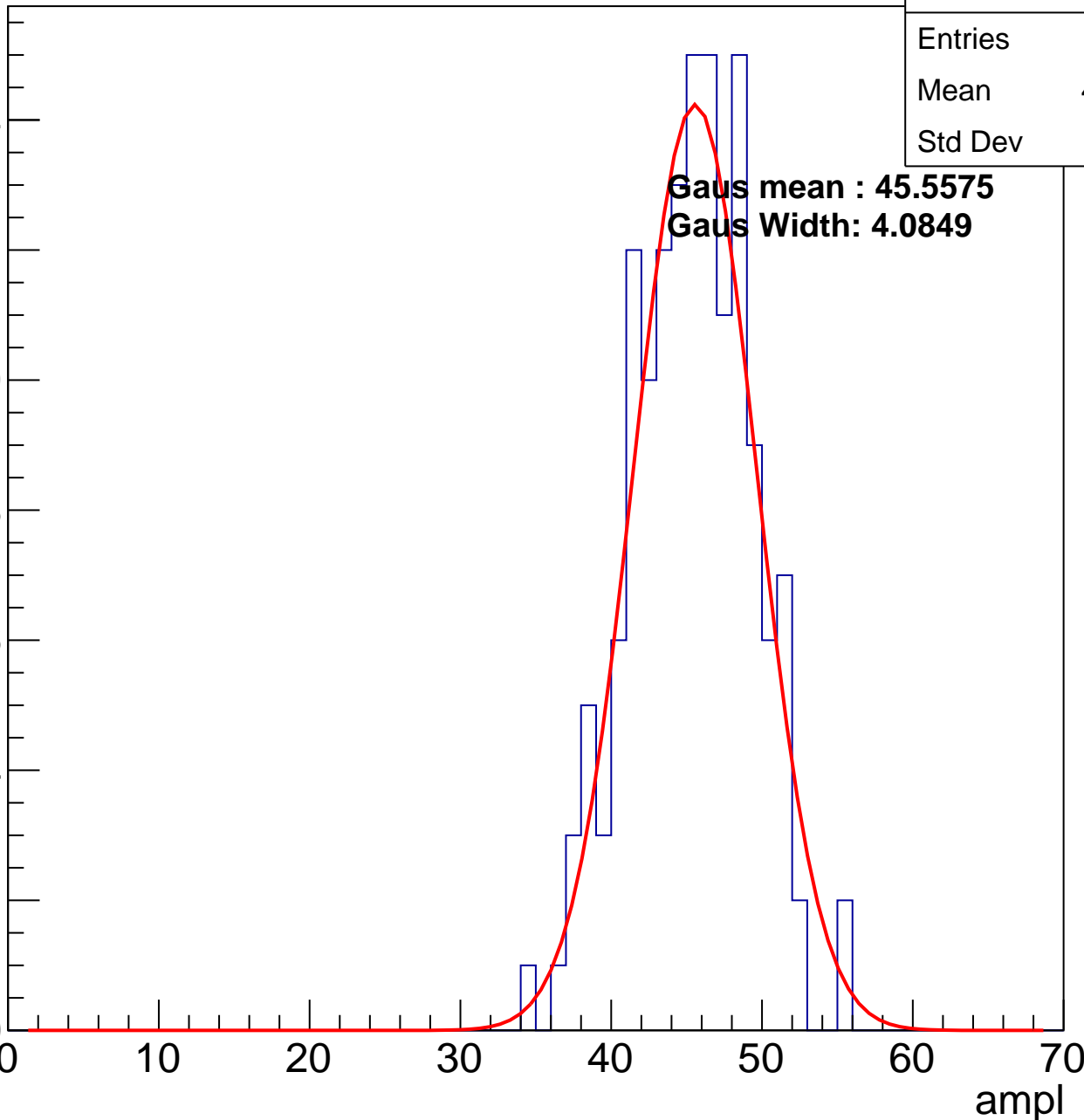
40

50

60

70

ampl



B0L101S, U24-ch117, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	51.54
Std Dev	3.698

Entry

12

10

8

6

4

2

0

0

10

20

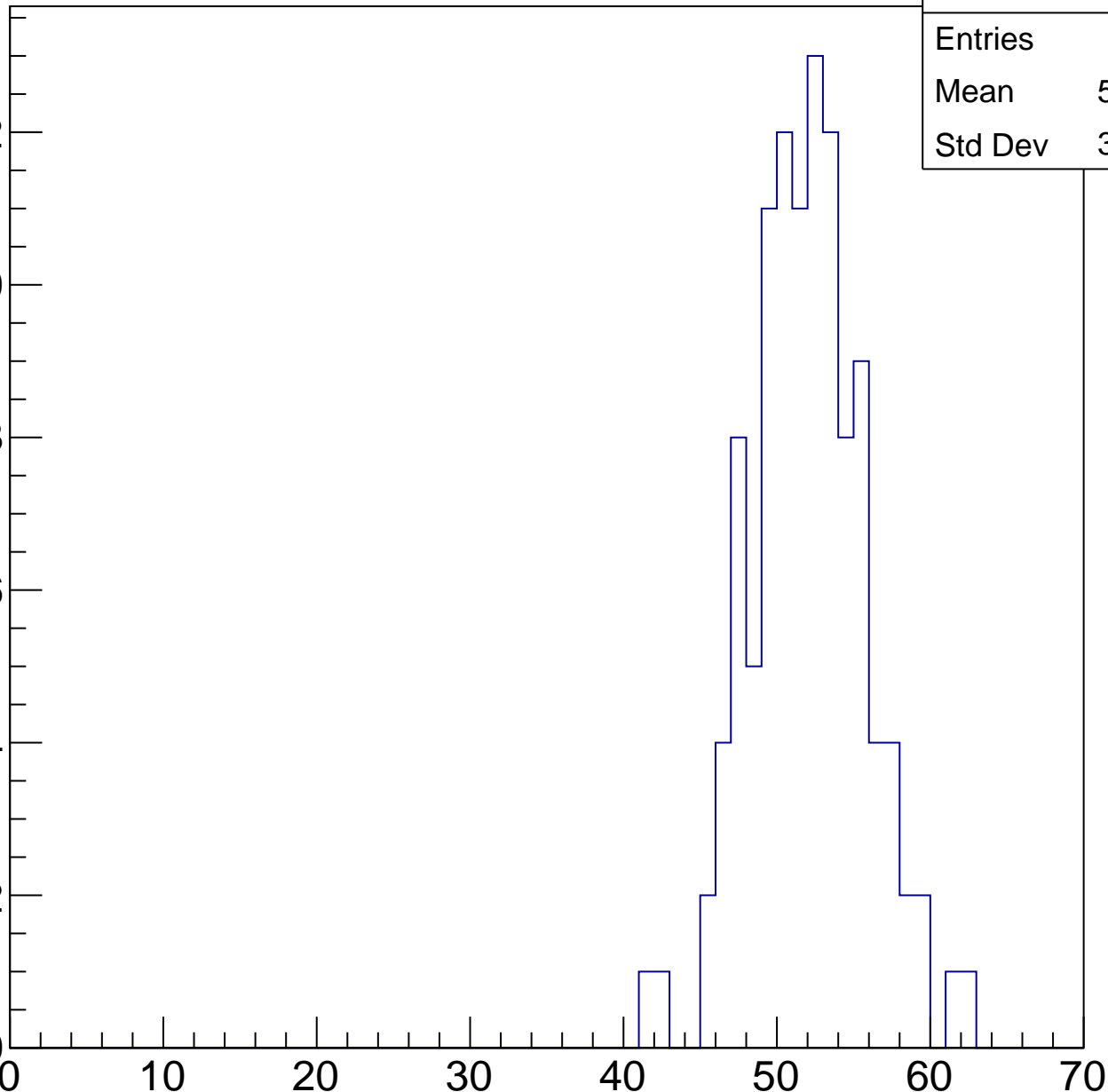
30

40

50

60

ampl



B0L101S, U24-ch117, adc4

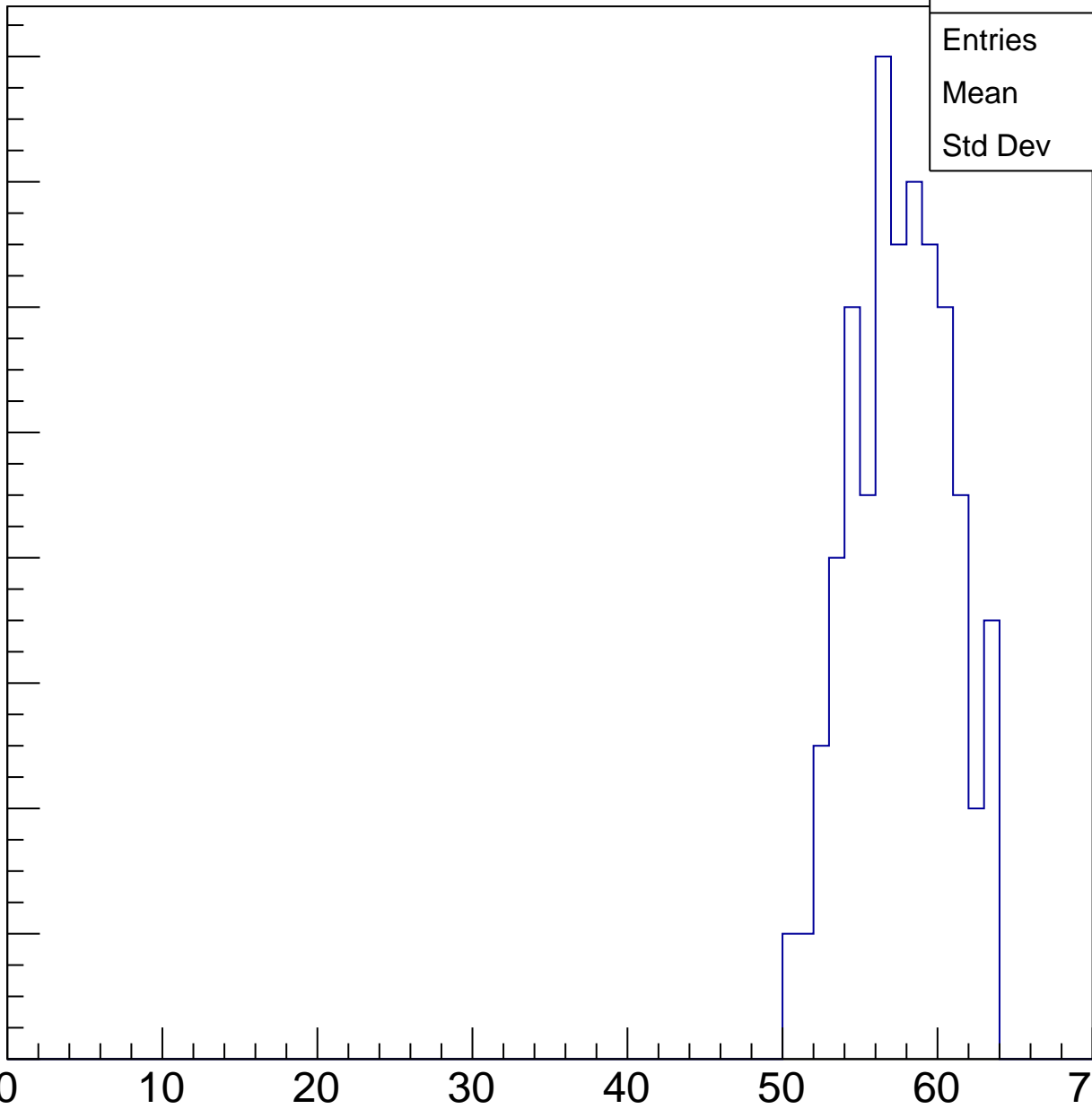
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	57.17
Std Dev	3.149

ampl



B0L101S, U24-ch117, adc5

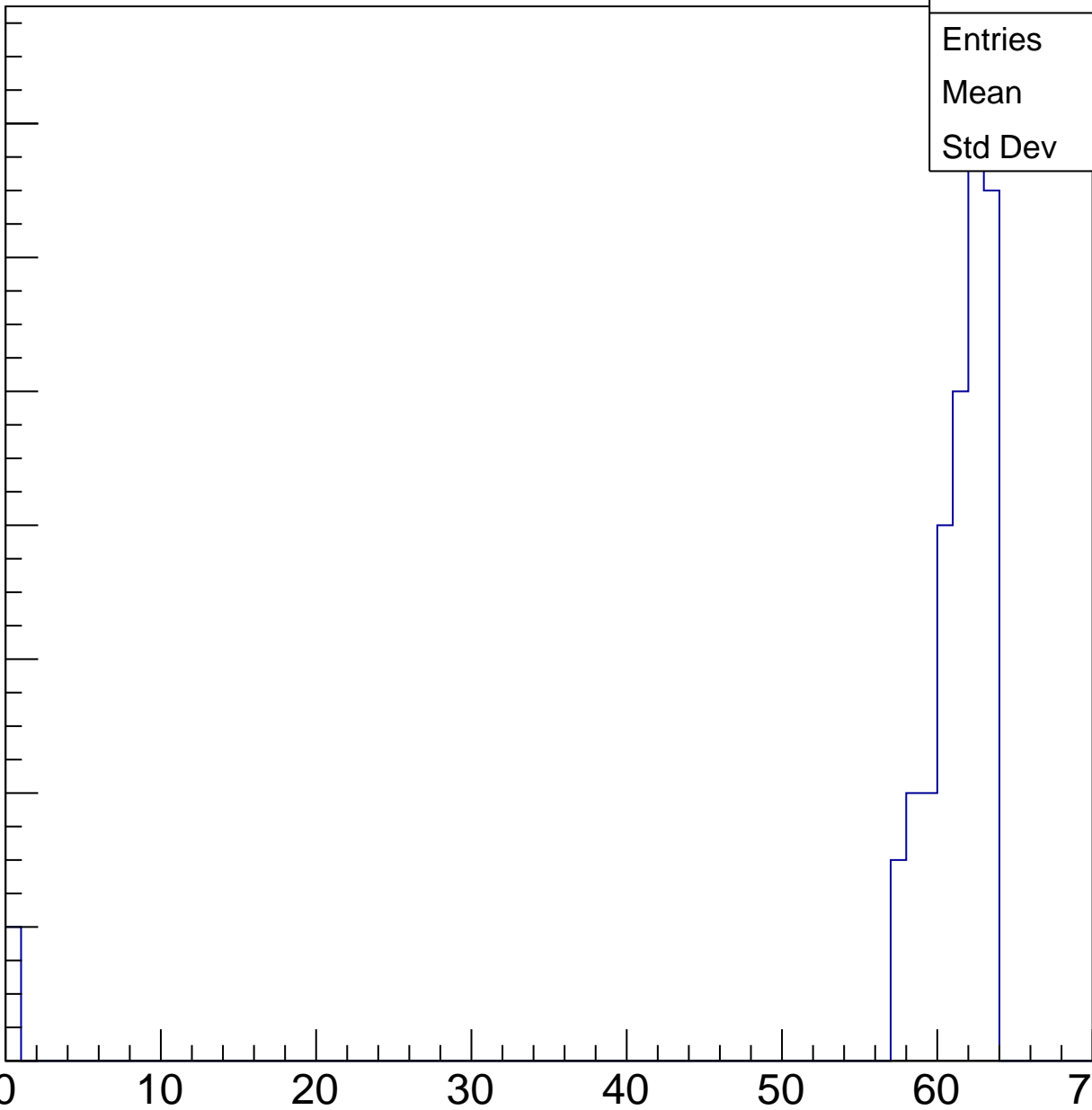
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	58.95
Std Dev	11.18

ampl



B0L101S, U24-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	1
Mean	59
Std Dev	0

B0L101S, U24-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch118, adc0

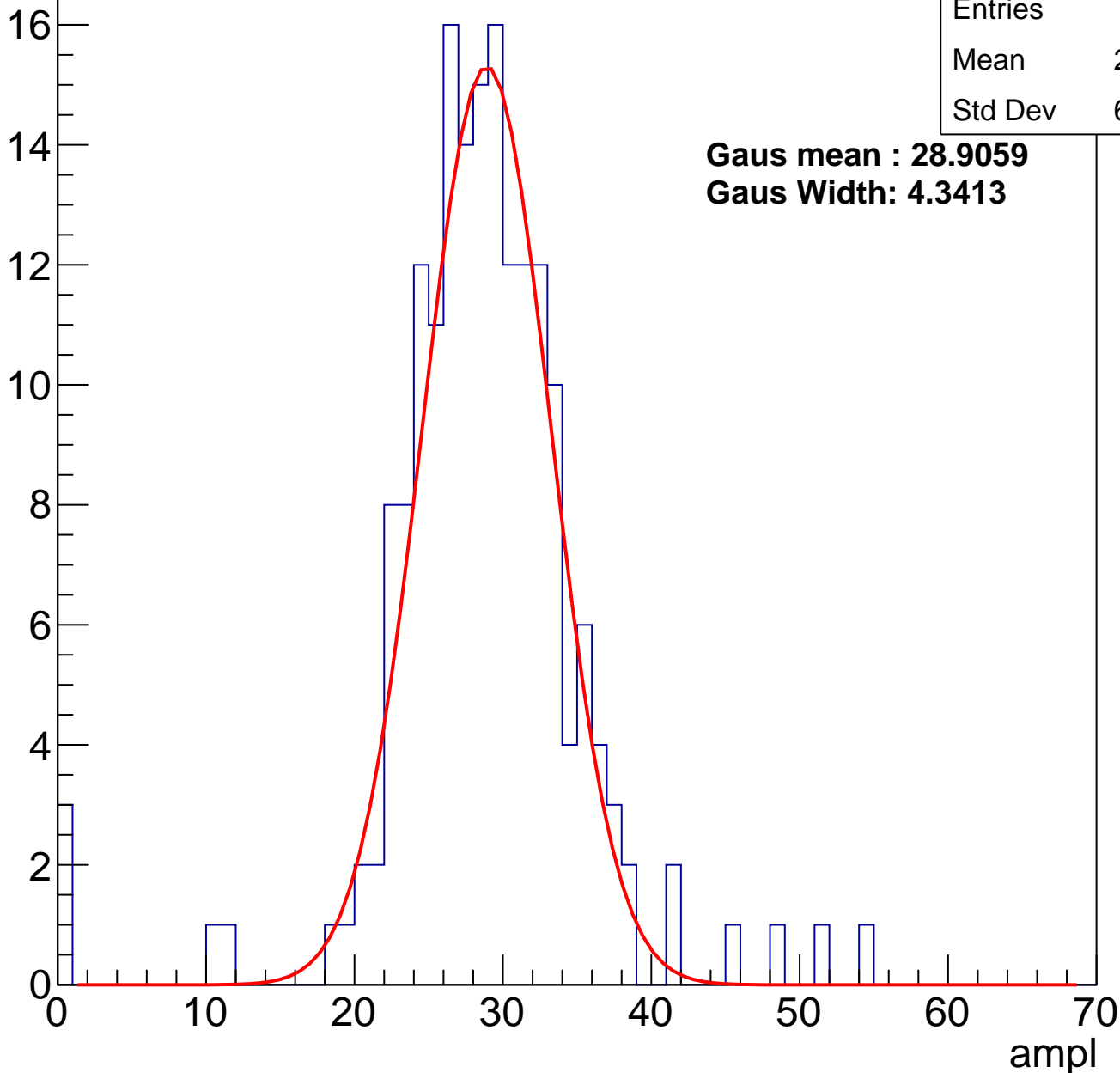
calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	28.26
Std Dev	6.712

Gaus mean : 28.9059

Gaus Width: 4.3413

Entry



B0L101S, U24-ch118, adc1

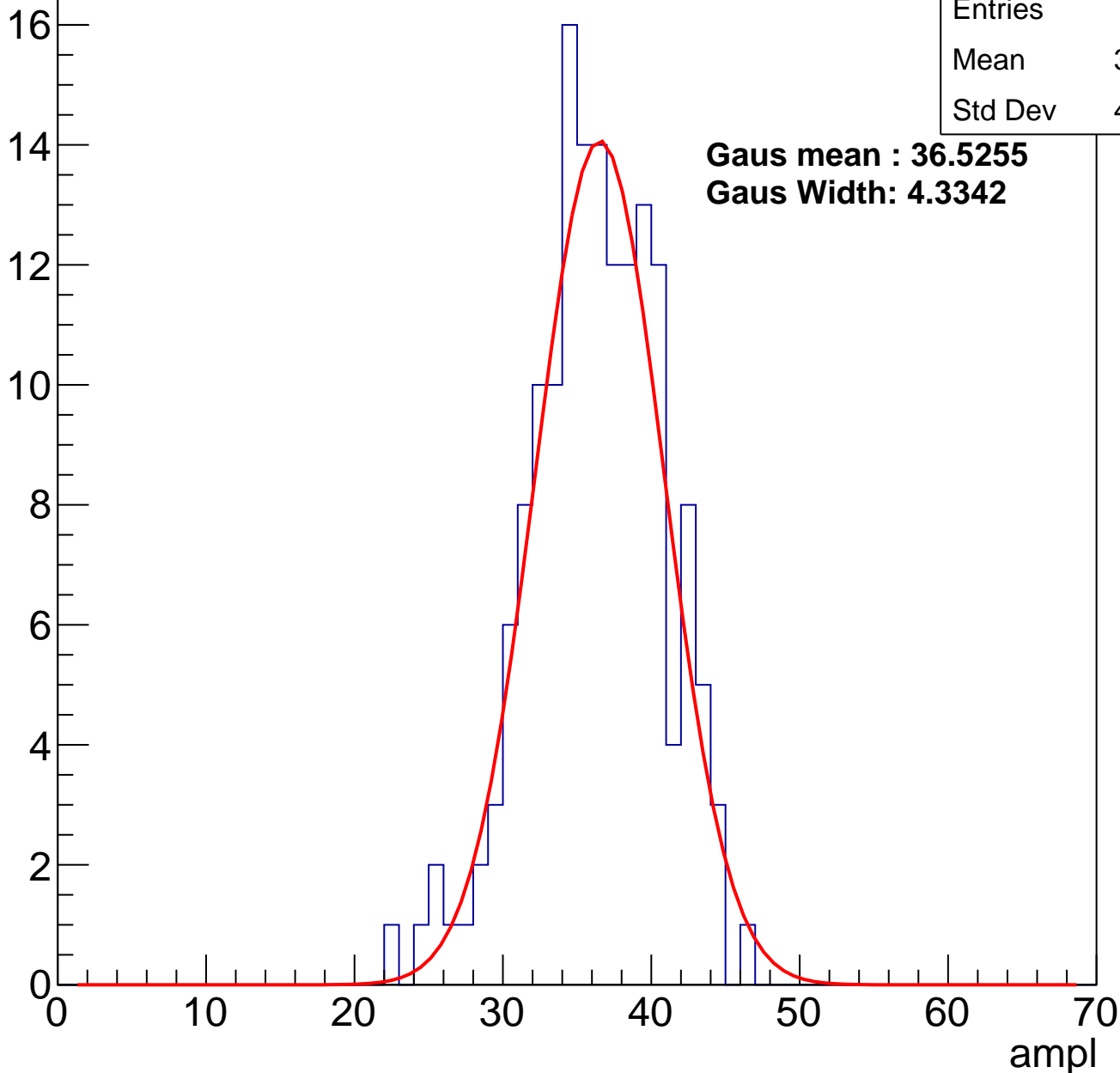
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	35.77
Std Dev	4.393

Gaus mean : 36.5255

Gaus Width: 4.3342

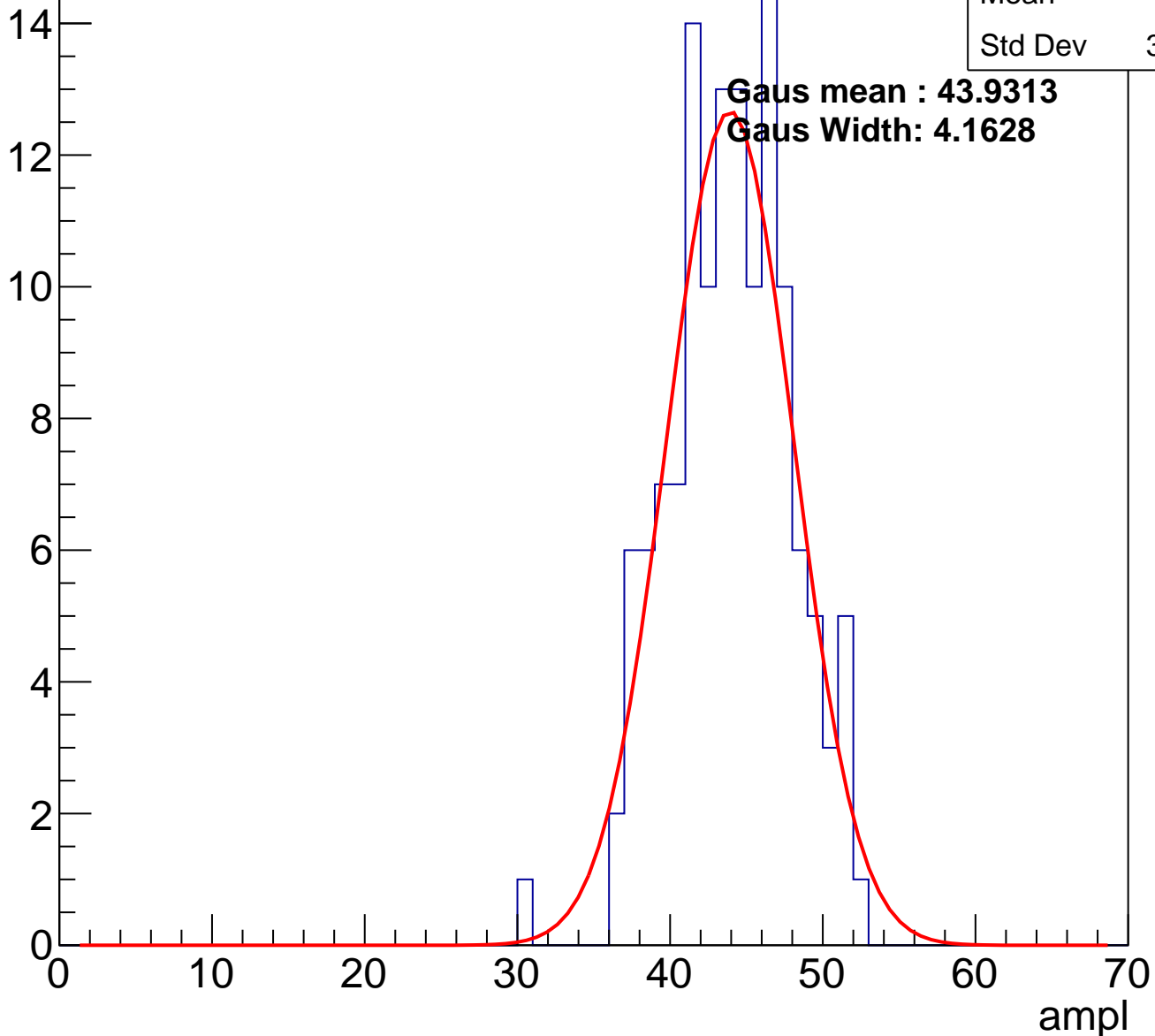
Entry



B0L101S, U24-ch118, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

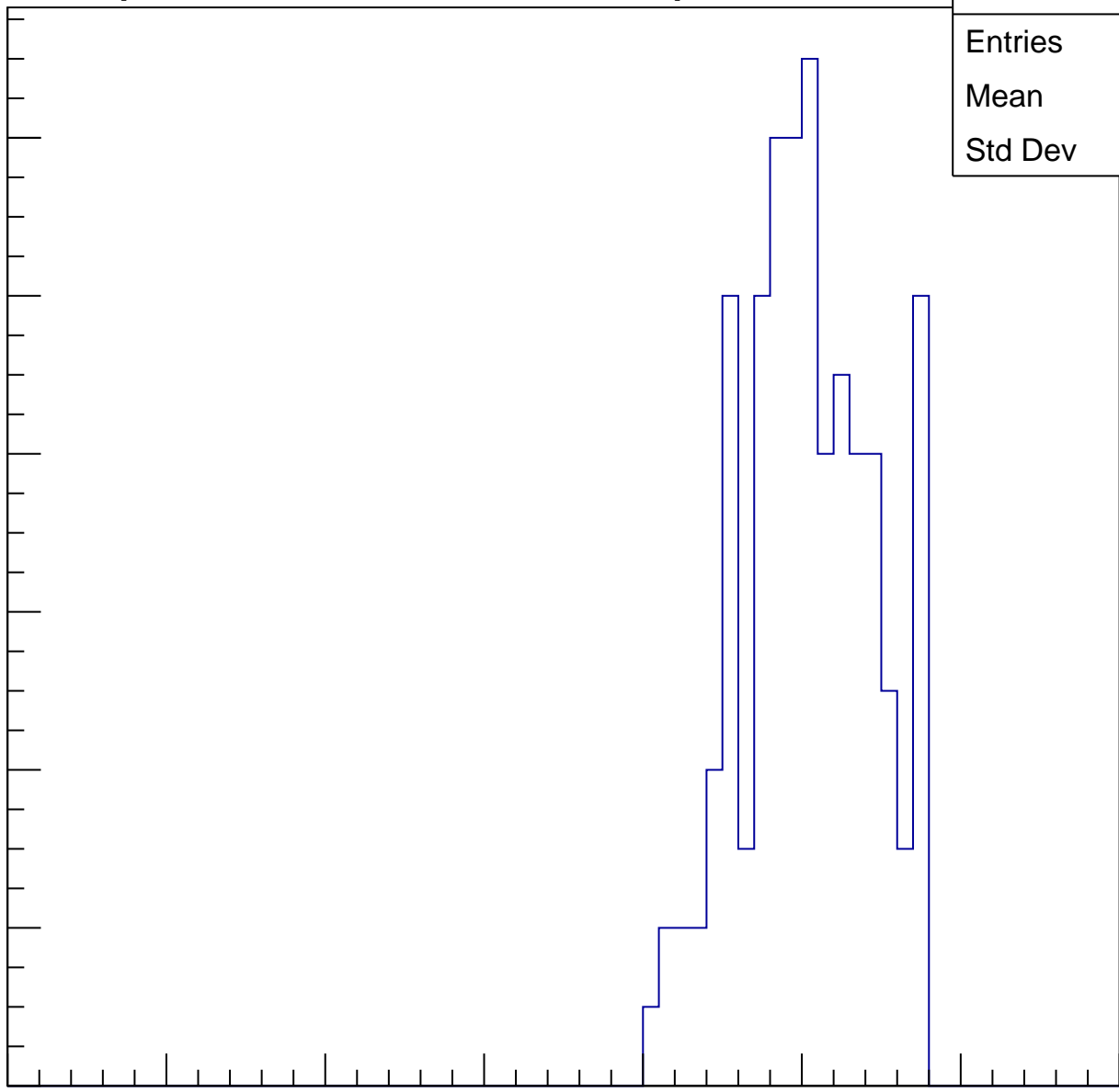
2

0

0 10 20 30 40 50 60 70

ampl

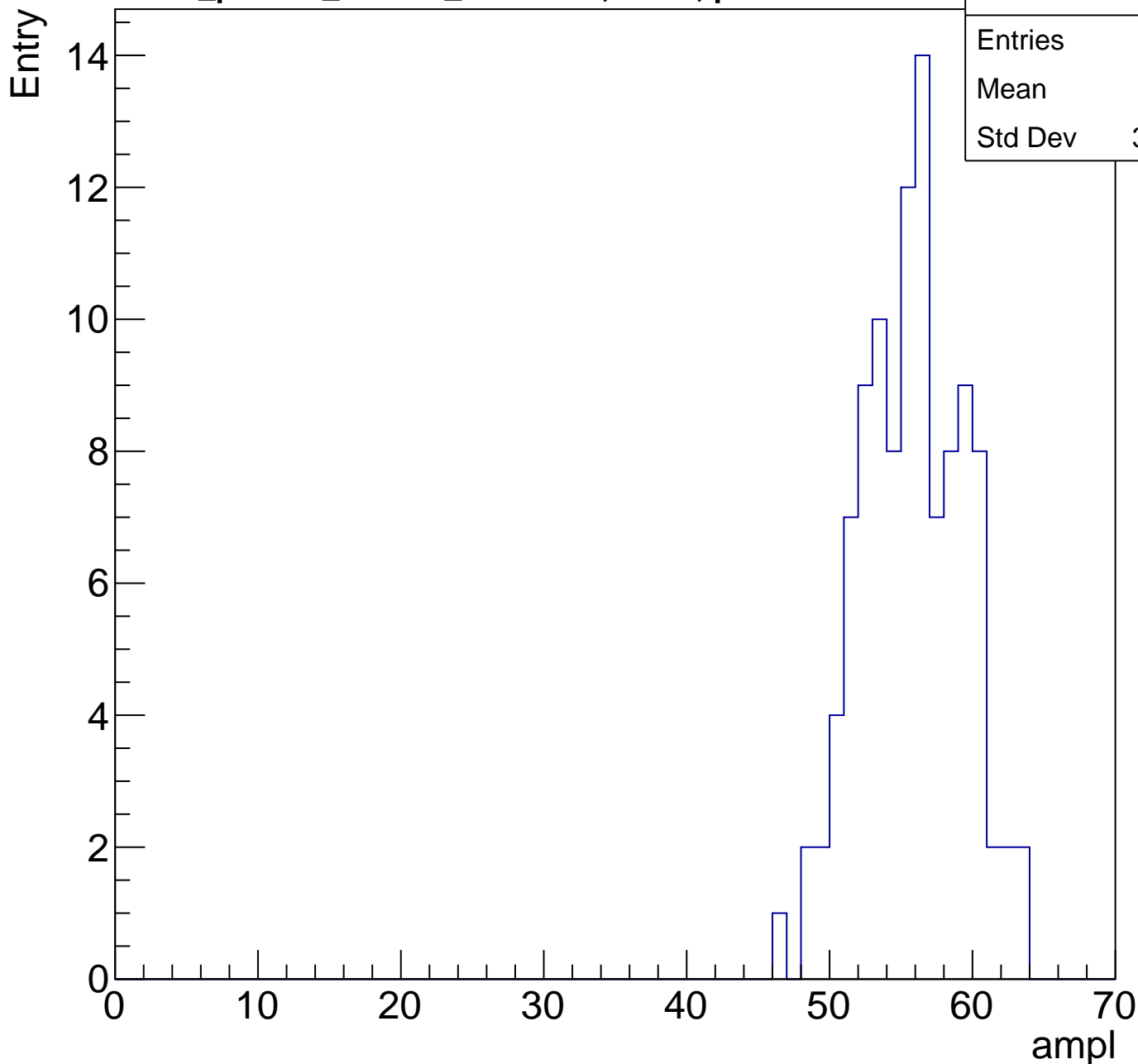
Entries	122
Mean	49.88
Std Dev	4.094



B0L101S, U24-ch118, adc4

calib_packv5_042523_0143.root, FC#1, port C1

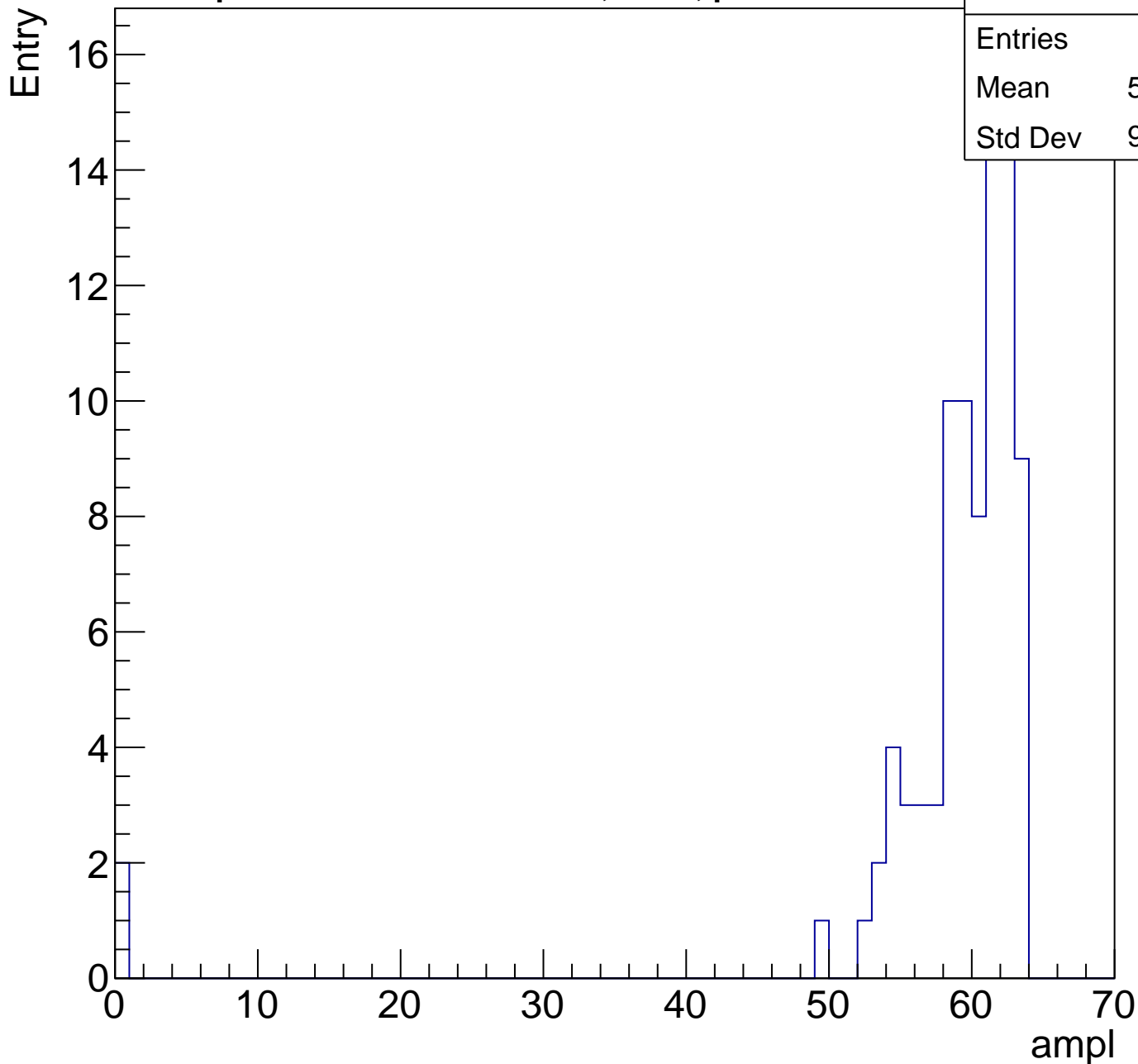
Entries	107
Mean	55.3
Std Dev	3.537



B0L101S, U24-ch118, adc5

calib_packv5_042523_0143.root, FC#1, port C1

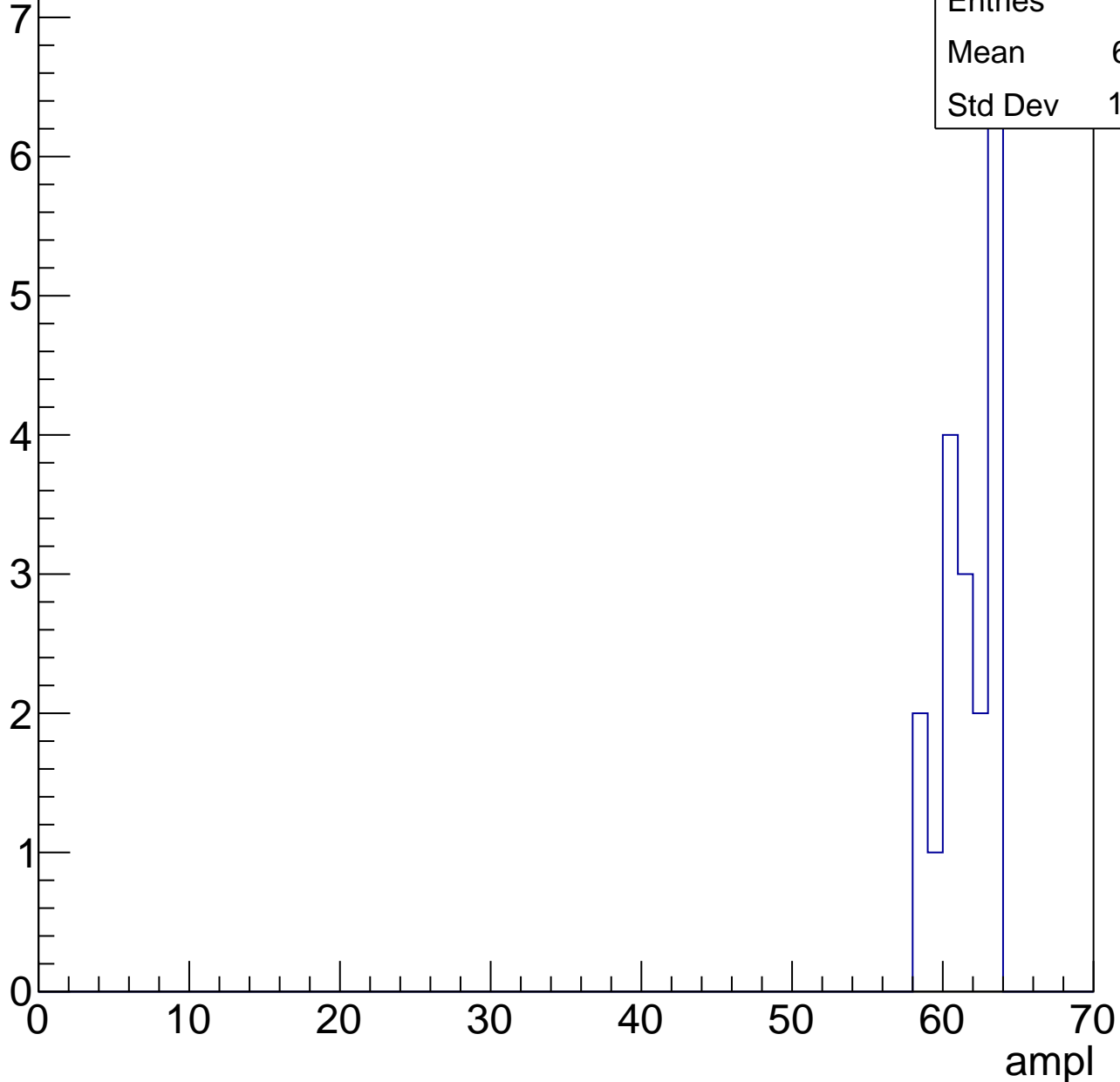
Entries	87
Mean	58.06
Std Dev	9.382



B0L101S, U24-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L101S, U24-ch119, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	28.09
Std Dev	4.694

Gaus mean : 28.9079

Gaus Width: 4.3591

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U24-ch119, adc1

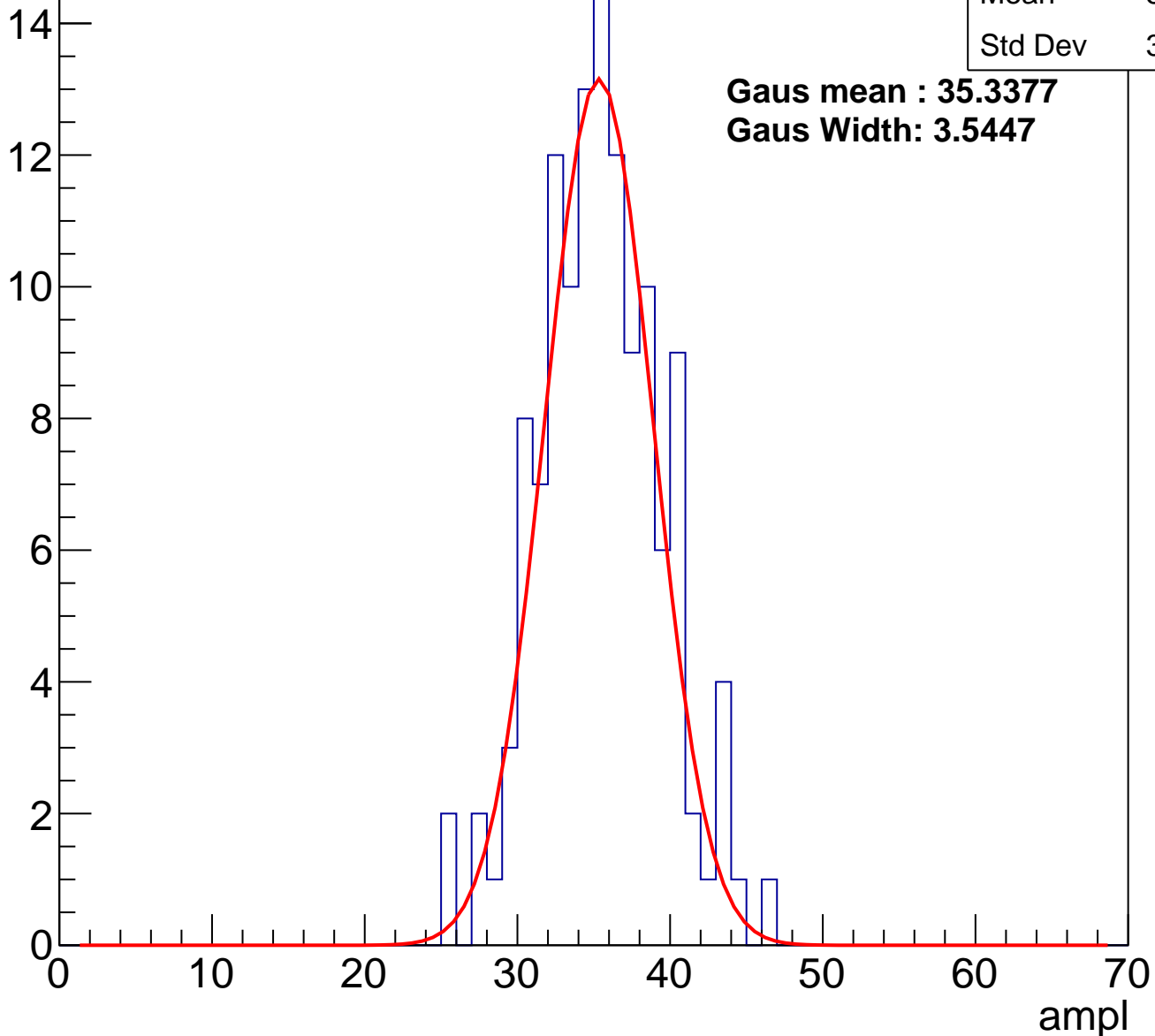
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	35.02
Std Dev	3.949

Gaus mean : 35.3377

Gaus Width: 3.5447

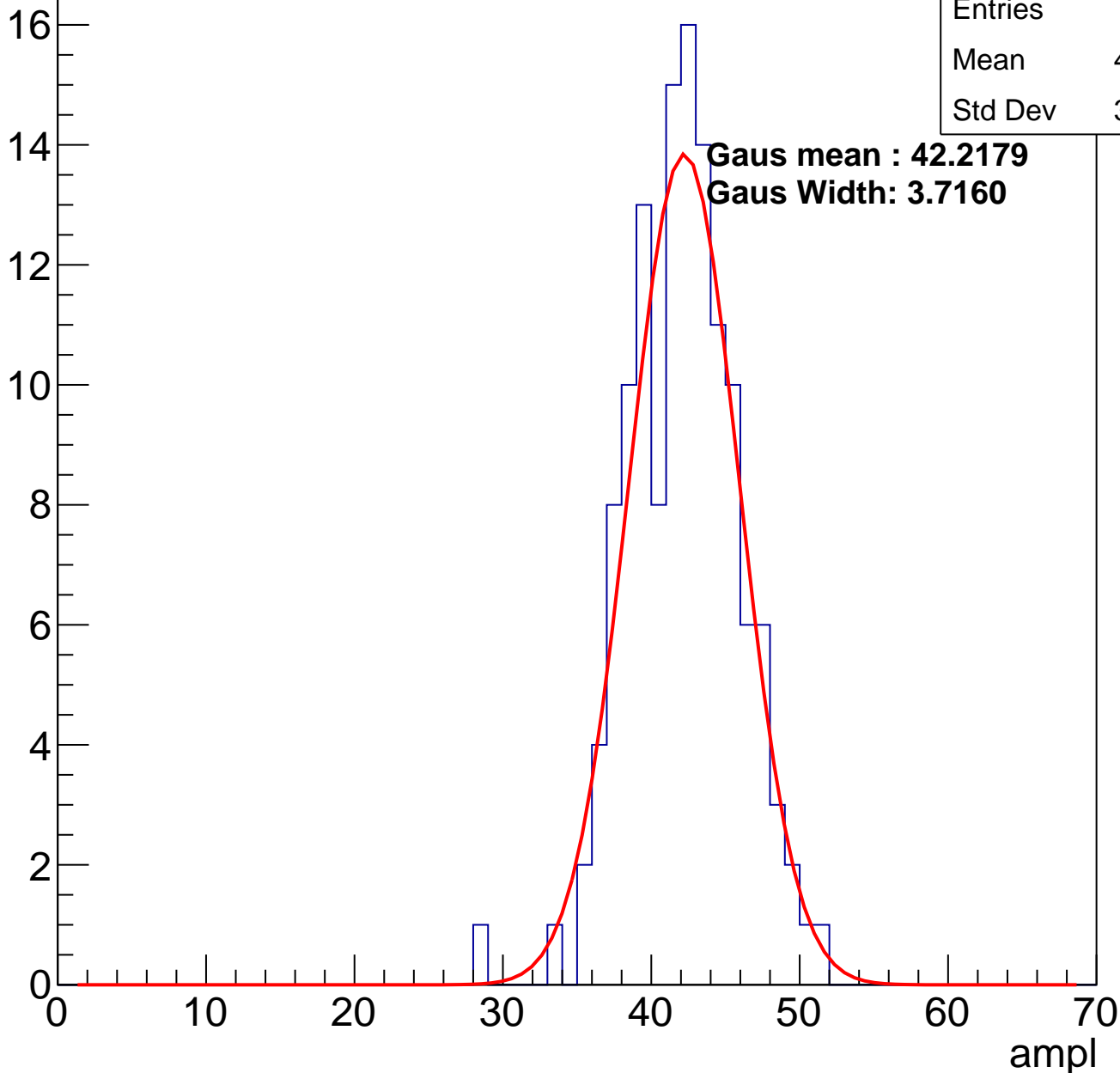
Entry



B0L101S, U24-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

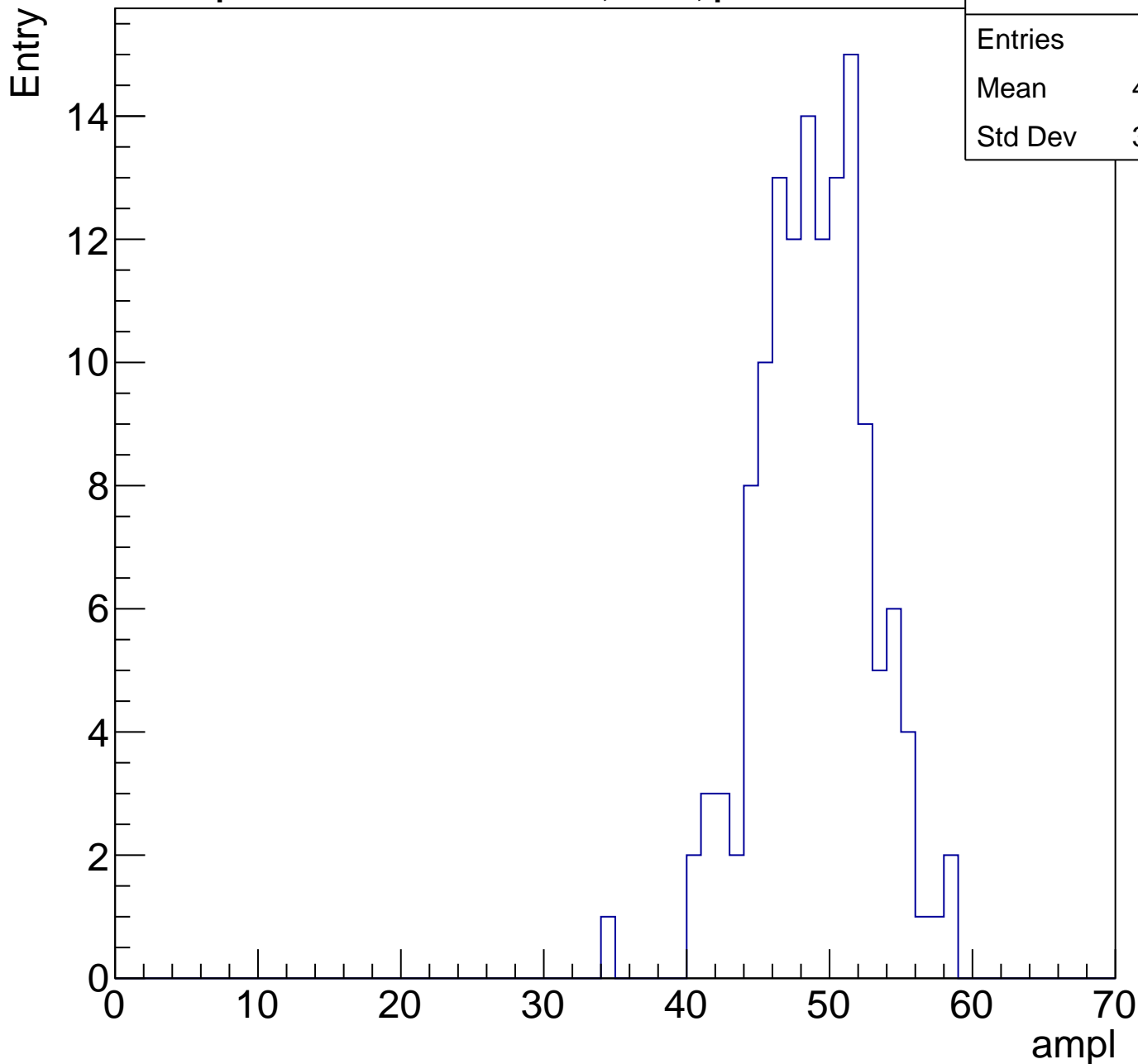
Entry



B0L101S, U24-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	48.49
Std Dev	3.933

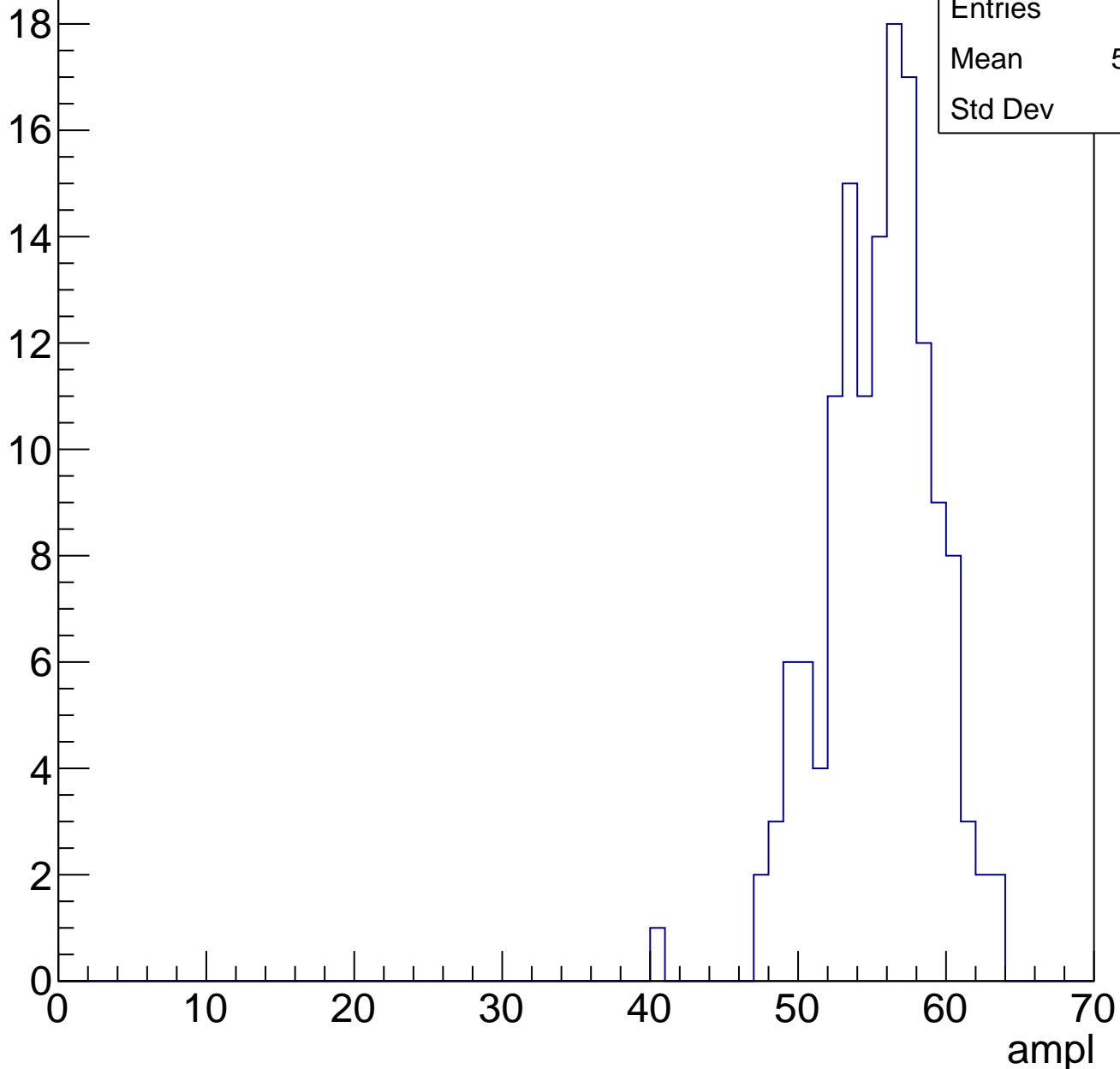


B0L101S, U24-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	55.03
Std Dev	3.7

Entry



B0L101S, U24-ch119, adc5

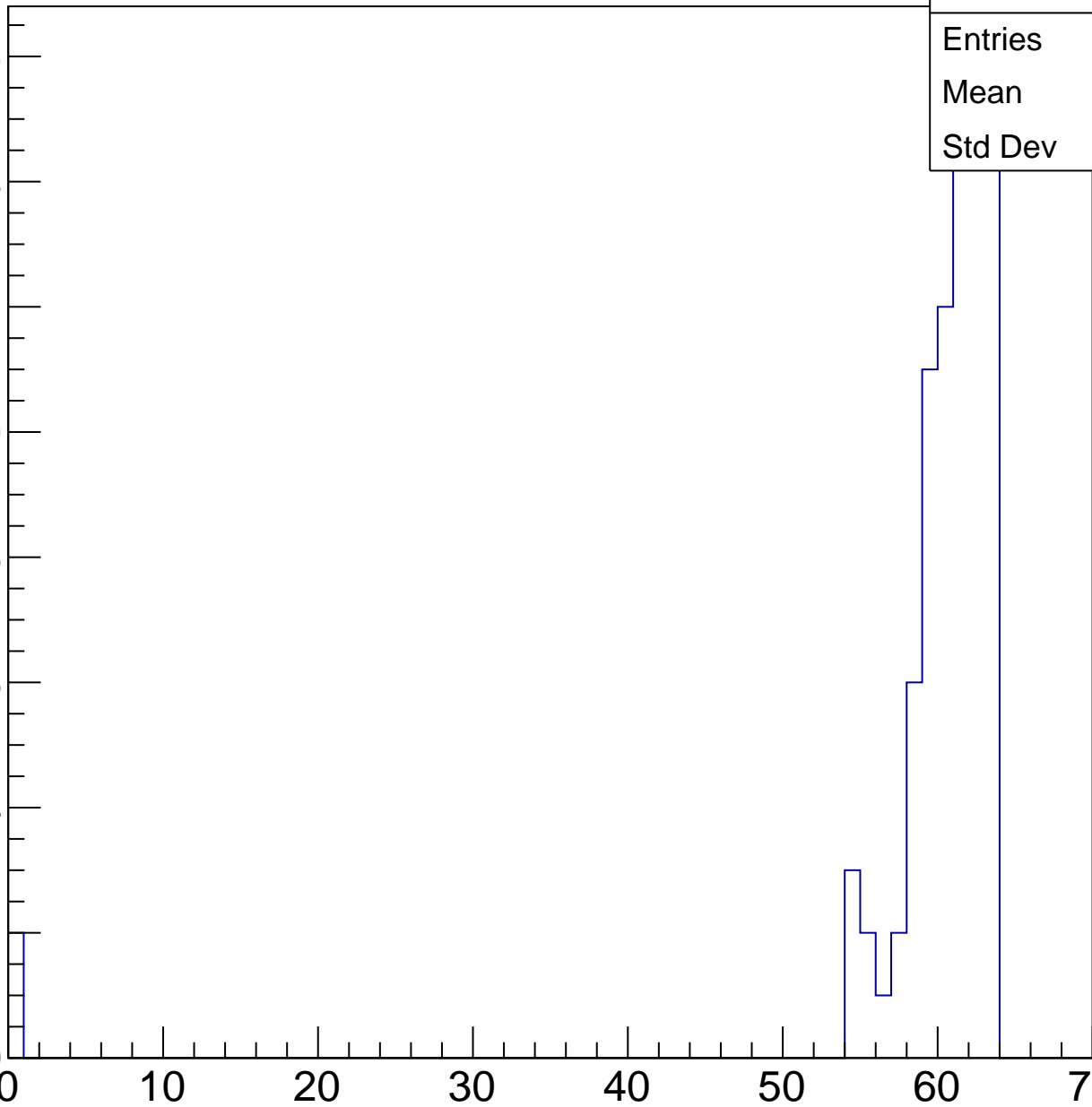
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.94
Std Dev	9.42

ampl



B0L101S, U24-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



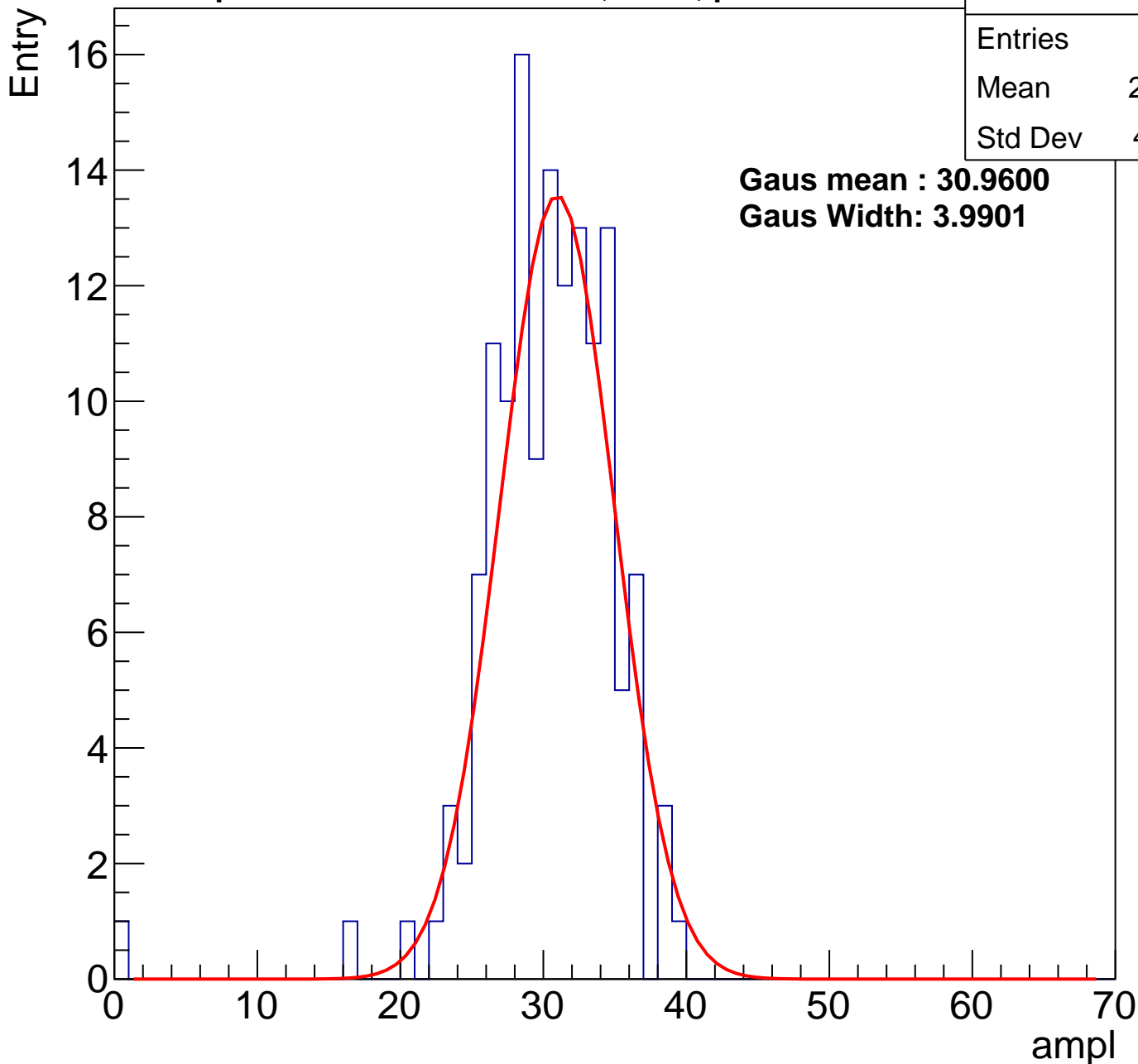
B0L101S, U24-ch120, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	29.84
Std Dev	4.621

Gaus mean : 30.9600

Gaus Width: 3.9901



B0L101S, U24-ch120, adc1

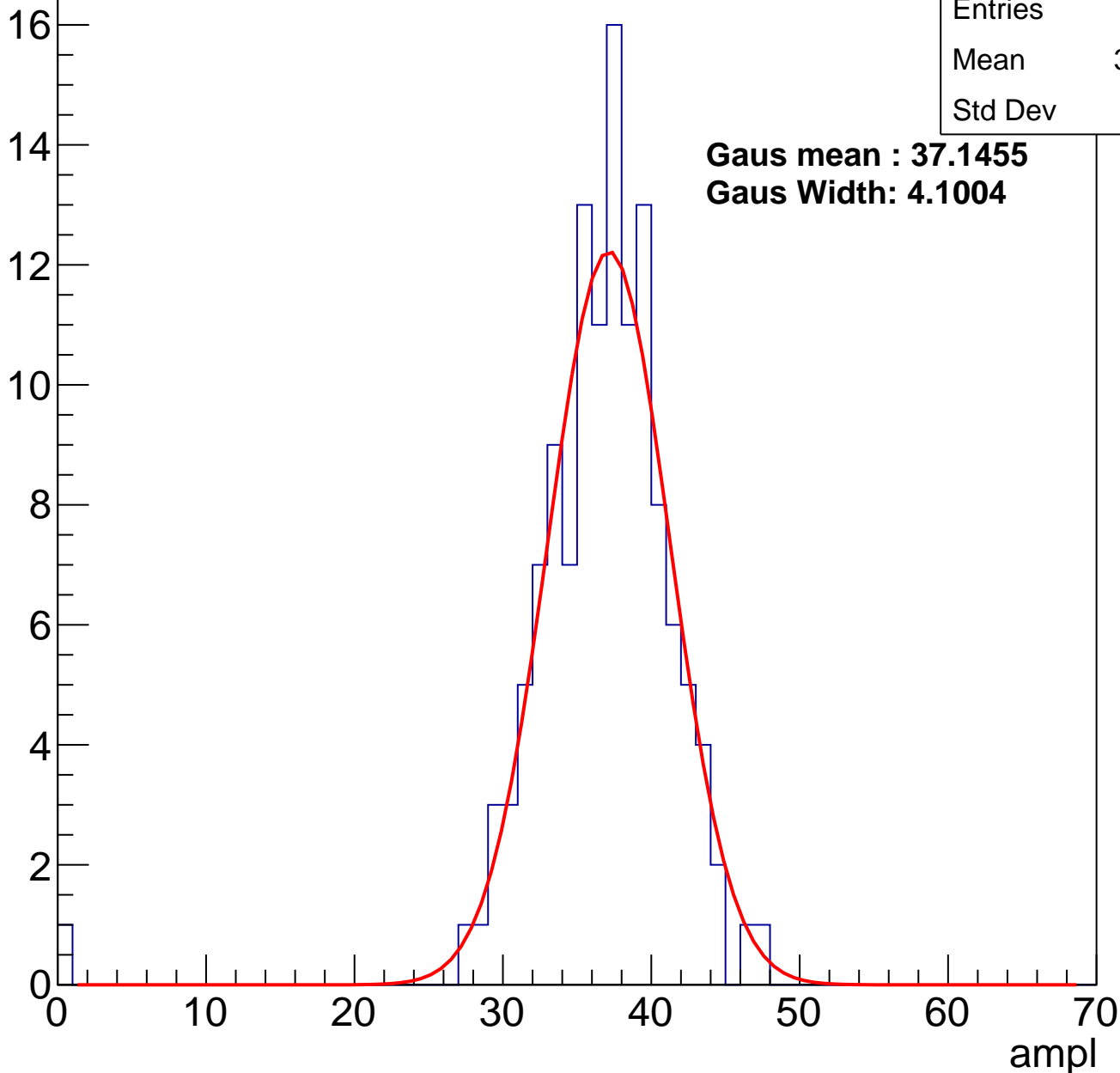
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	36.27
Std Dev	5.02

Gaus mean : 37.1455

Gaus Width: 4.1004

Entry



B0L101S, U24-ch120, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

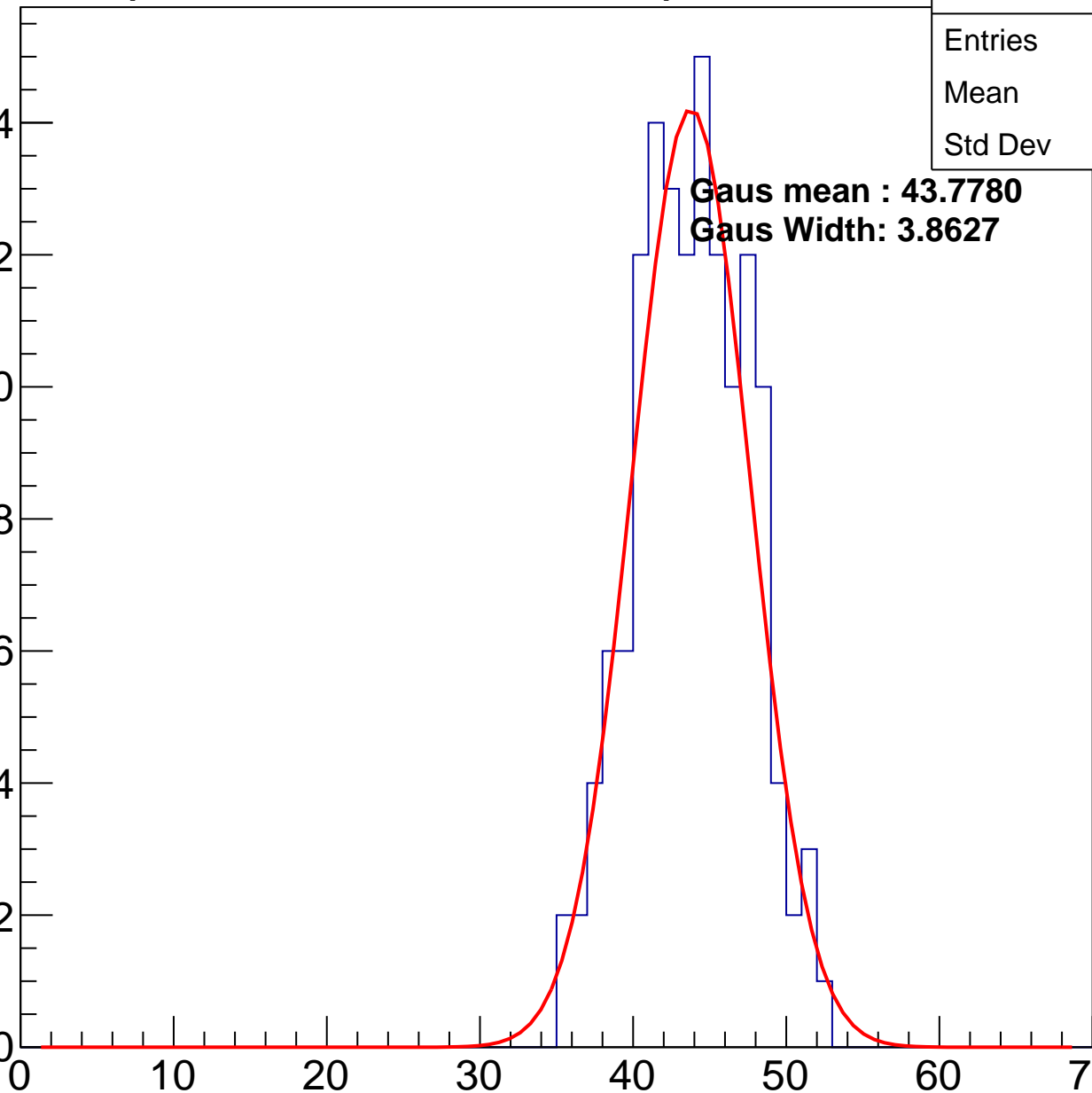
14
12
10
8
6
4
2
0

Entries	140
Mean	43.38
Std Dev	3.667

Gaus mean : 43.7780

Gaus Width: 3.8627

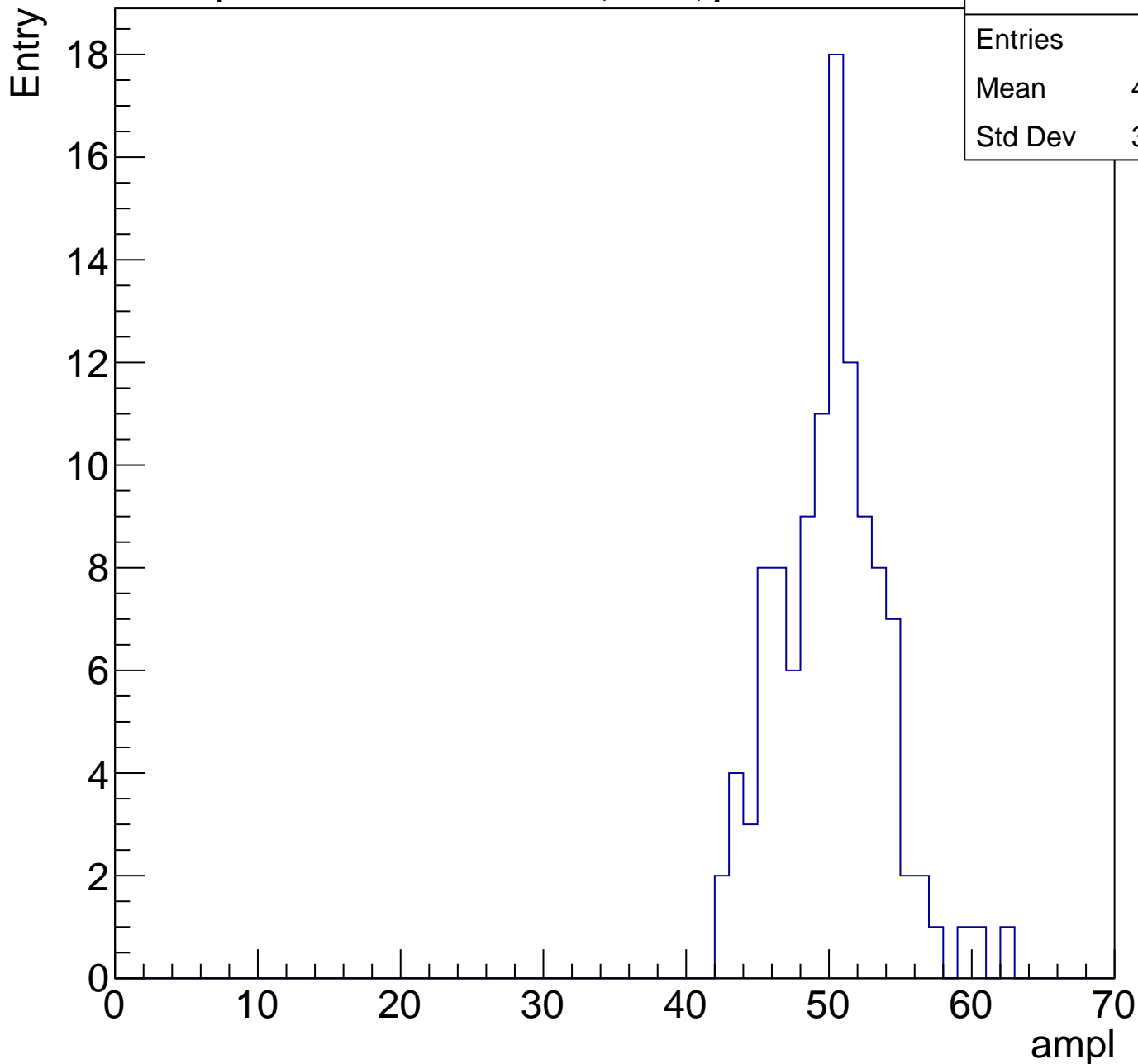
ampl



B0L101S, U24-ch120, adc3

calib_packv5_042523_0143.root, FC#1, port C1

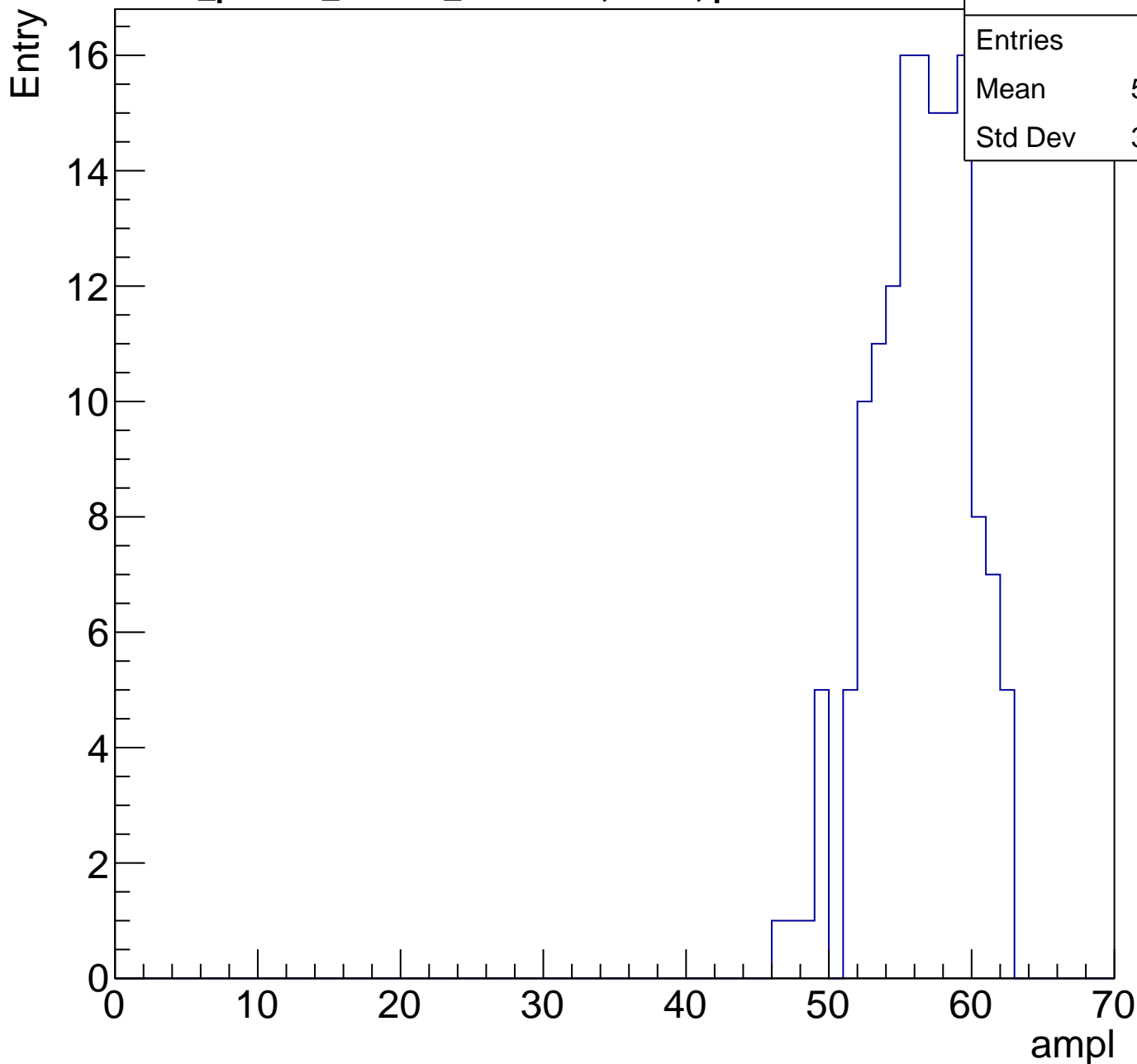
Entries	113
Mean	49.65
Std Dev	3.734



B0L101S, U24-ch120, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	55.93
Std Dev	3.374



B0L101S, U24-ch120, adc5

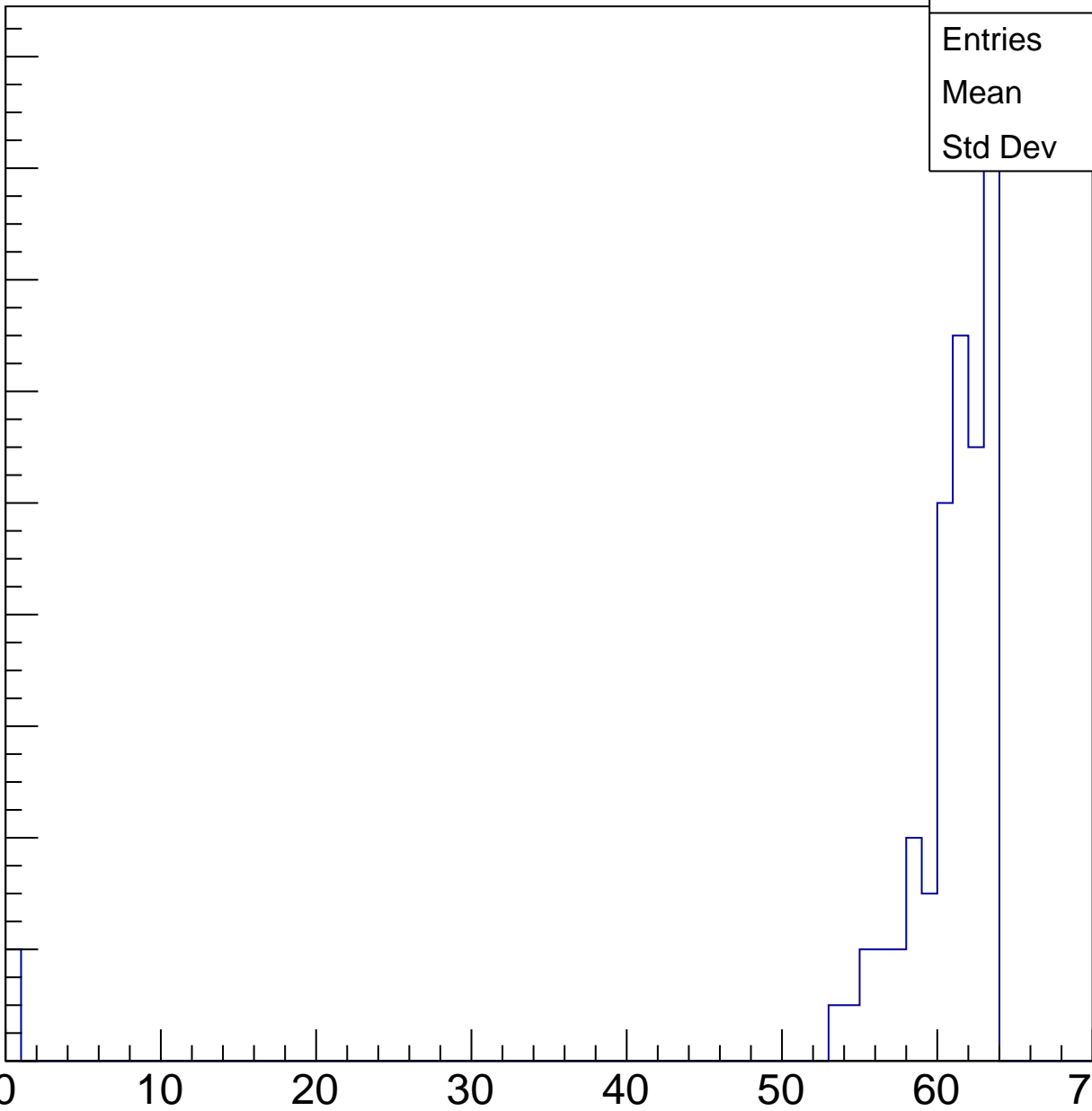
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.86
Std Dev	10.45

ampl



B0L101S, U24-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

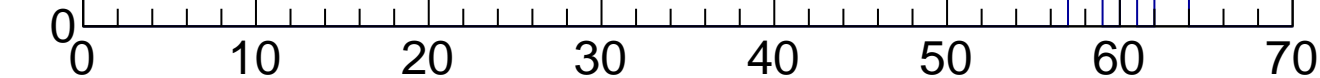
8

Mean

60.12

Std Dev

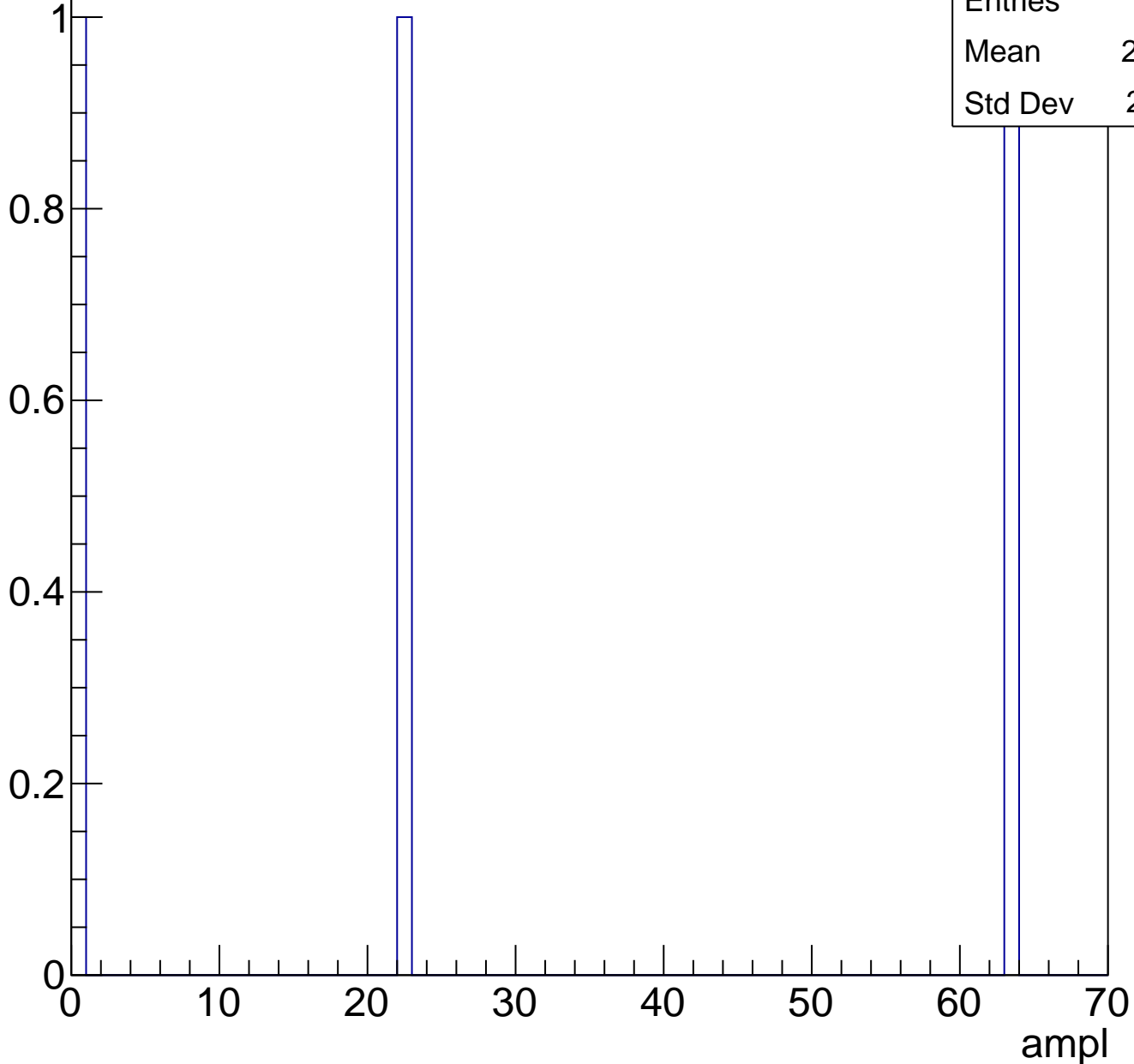
2.315



B0L101S, U24-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B0L101S, U24-ch121, adc0

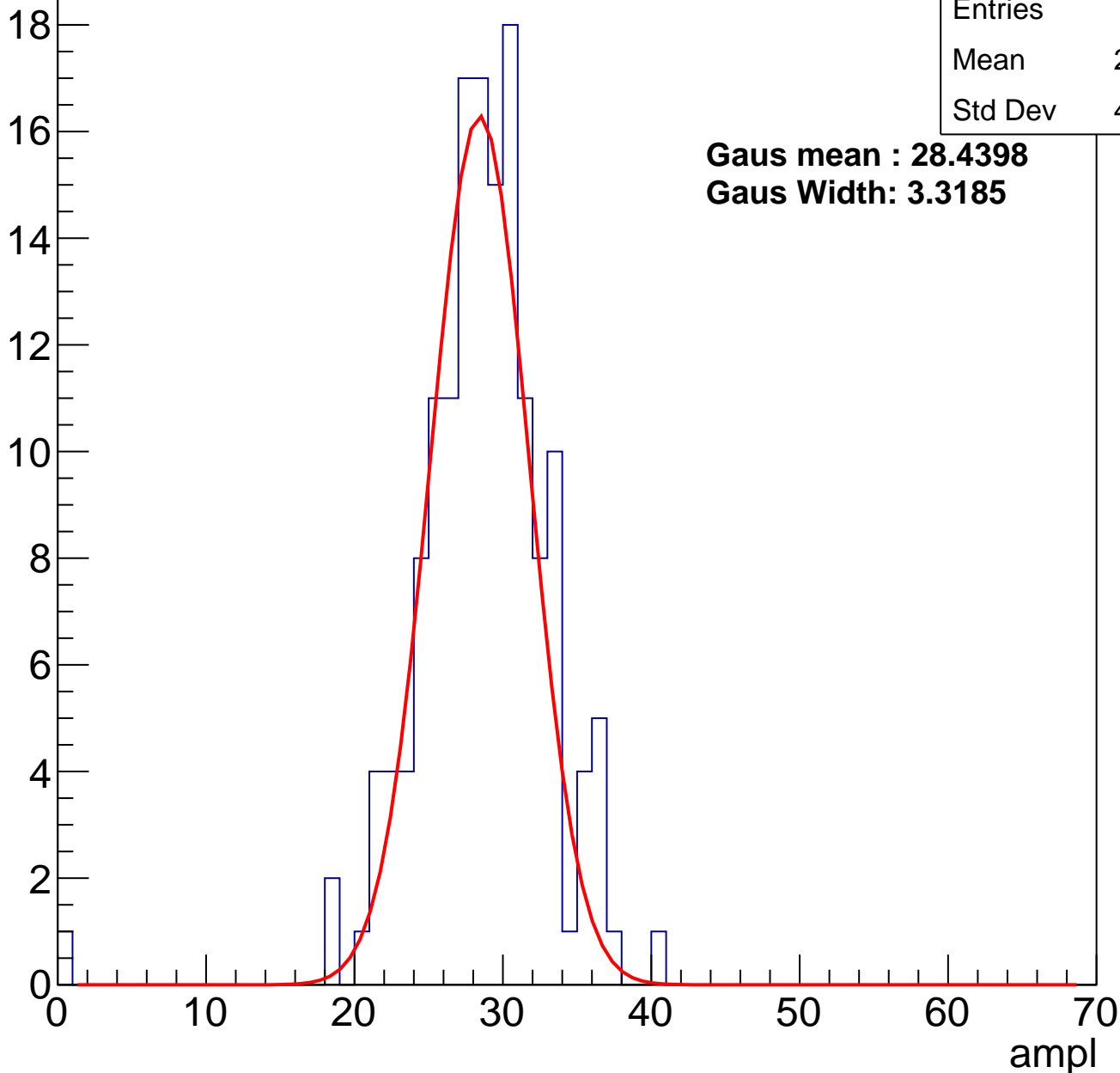
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	28.19
Std Dev	4.495

Gaus mean : 28.4398

Gaus Width: 3.3185

Entry



B0L101S, U24-ch121, adc1

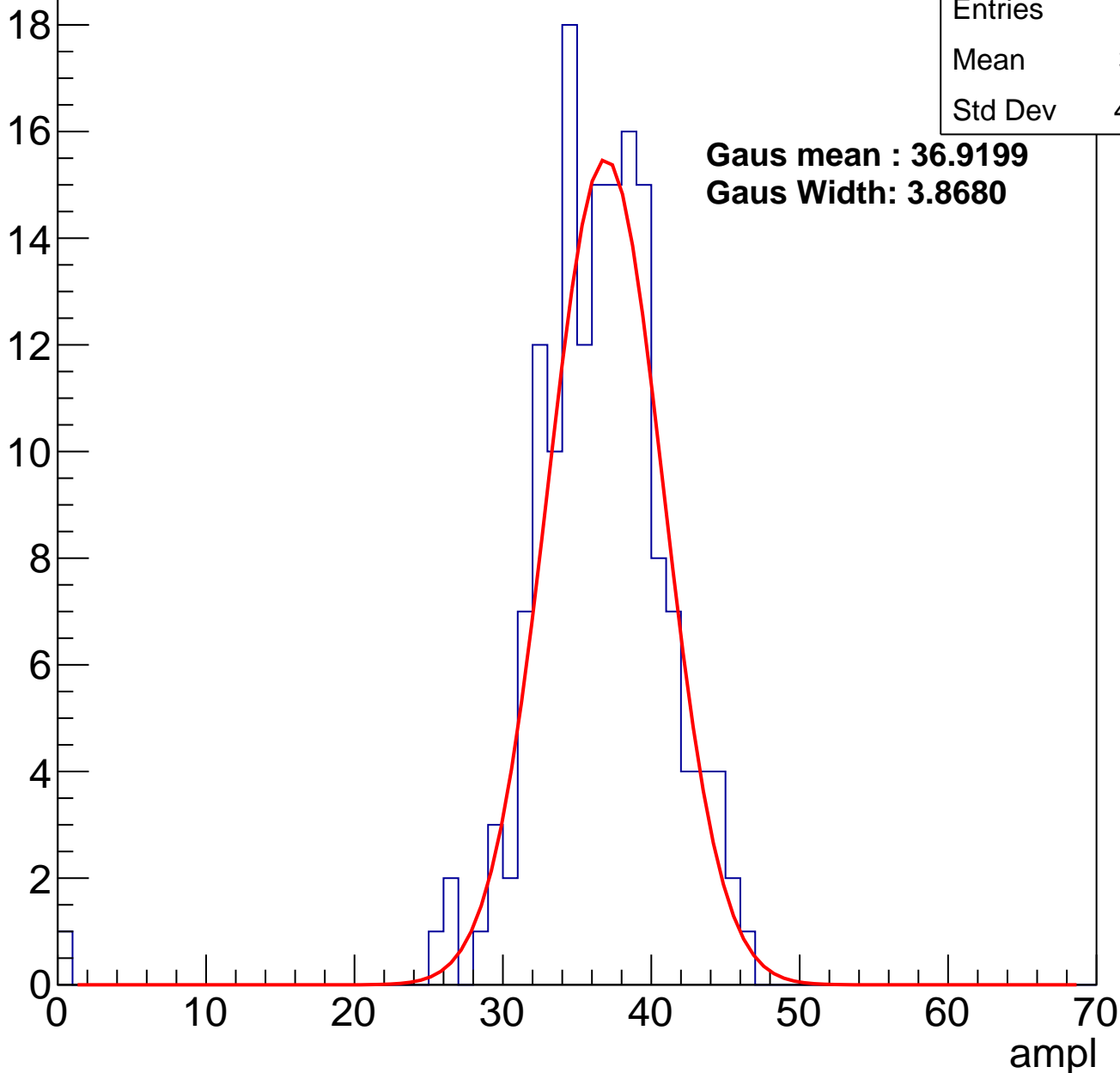
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	36.01
Std Dev	4.884

Gaus mean : 36.9199

Gaus Width: 3.8680

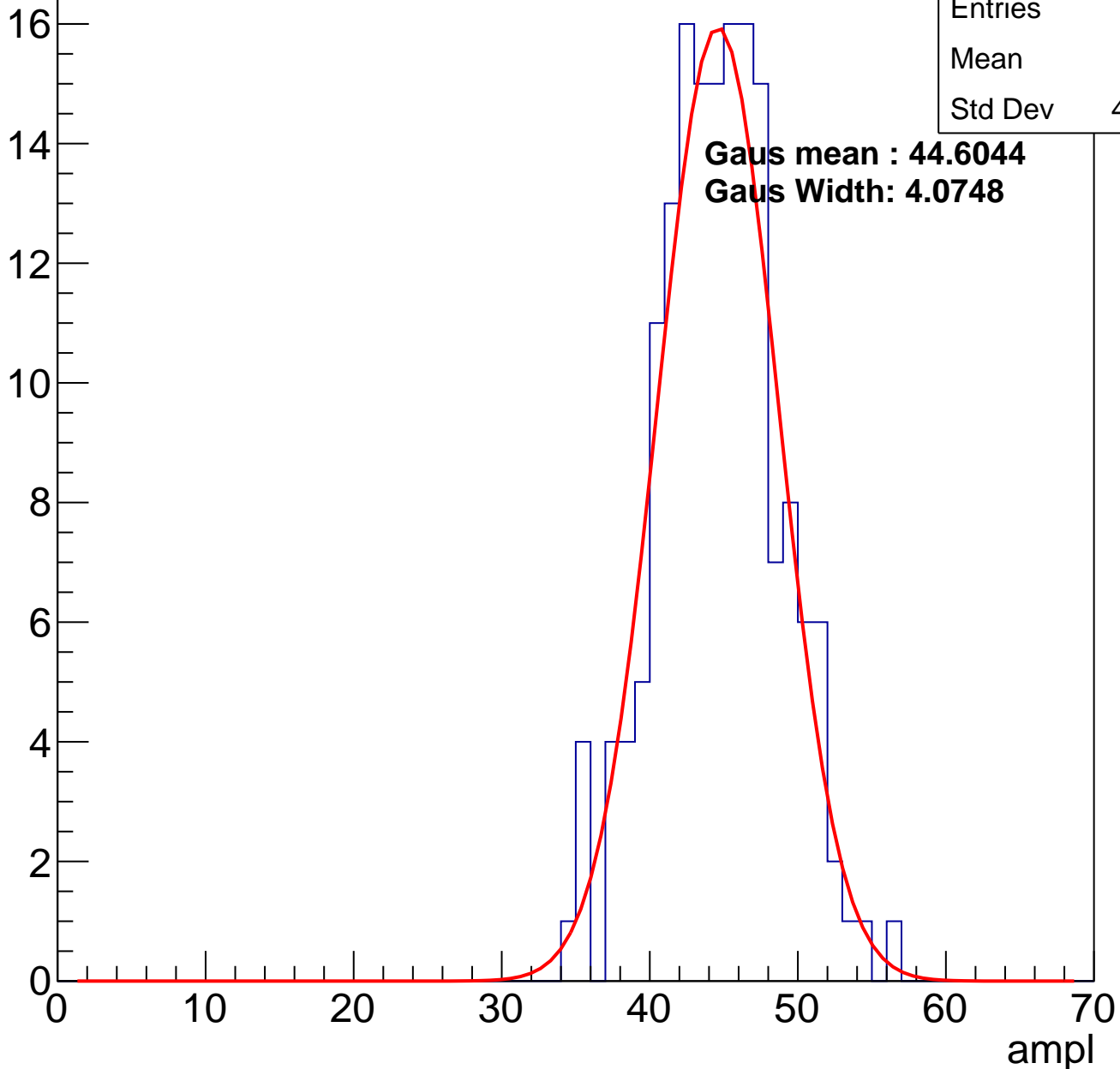
Entry



B0L101S, U24-ch121, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch121, adc3

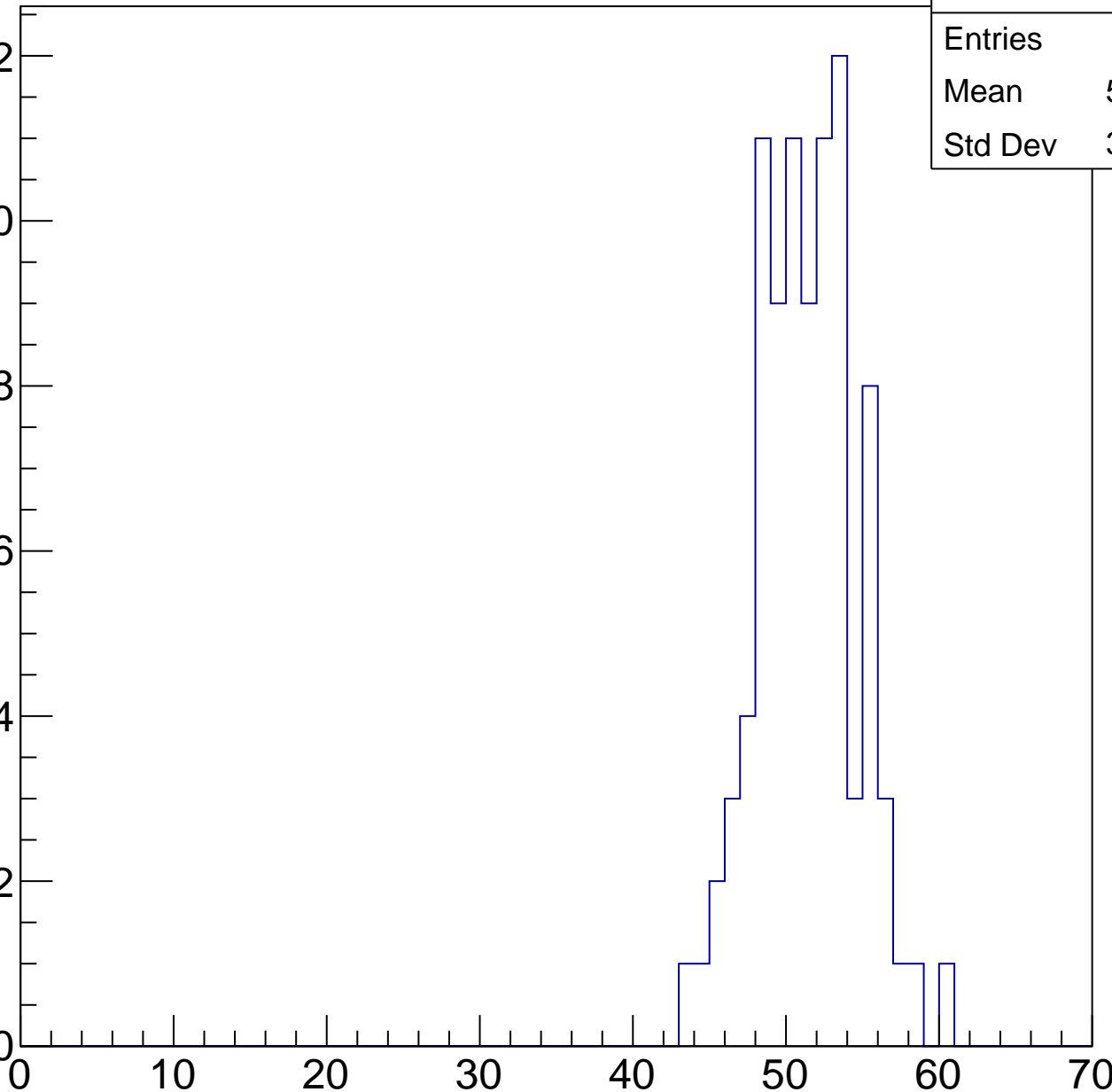
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	91
Mean	50.92
Std Dev	3.187

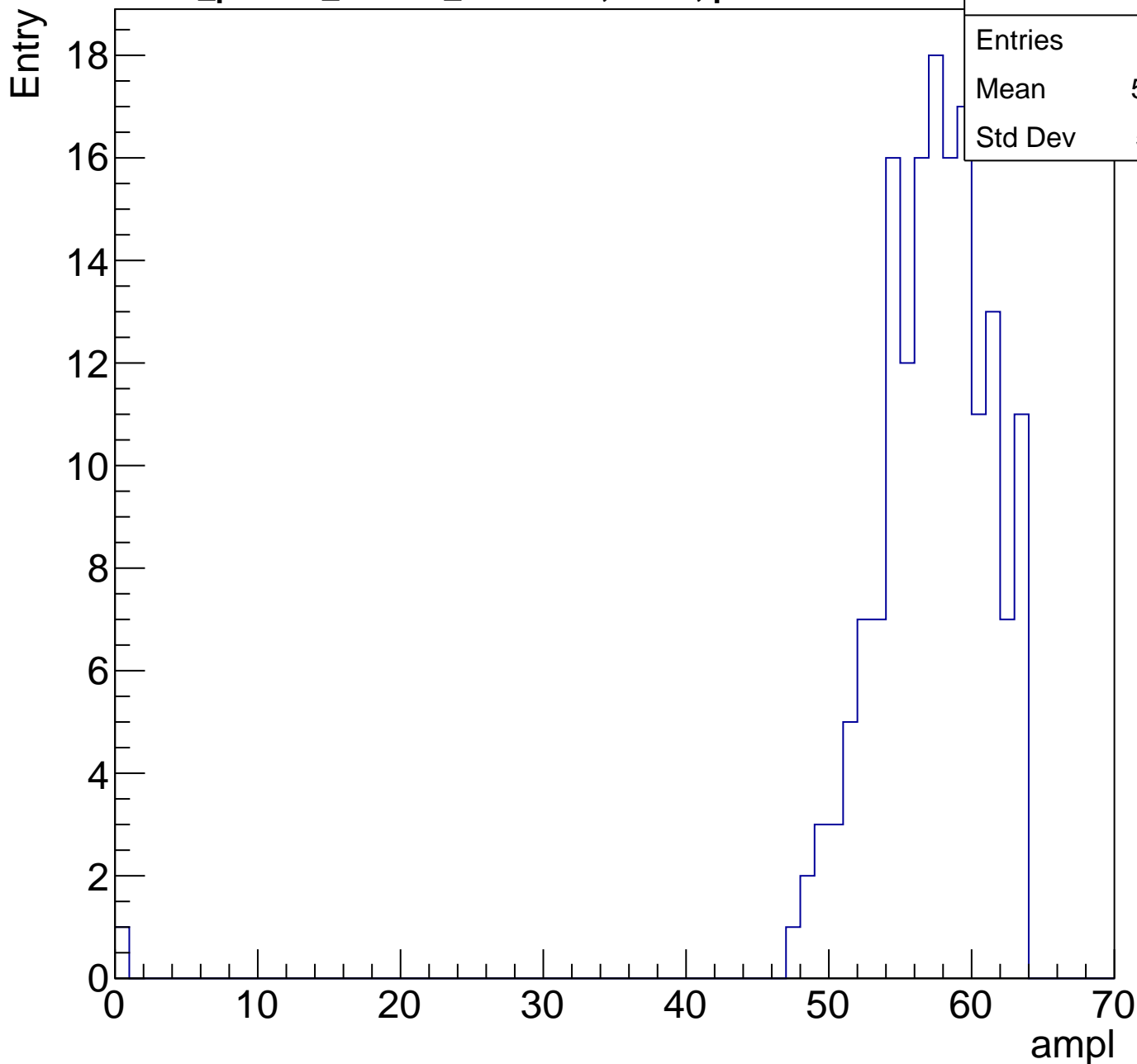
ampl



B0L101S, U24-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	56.55
Std Dev	5.731



B0L101S, U24-ch121, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

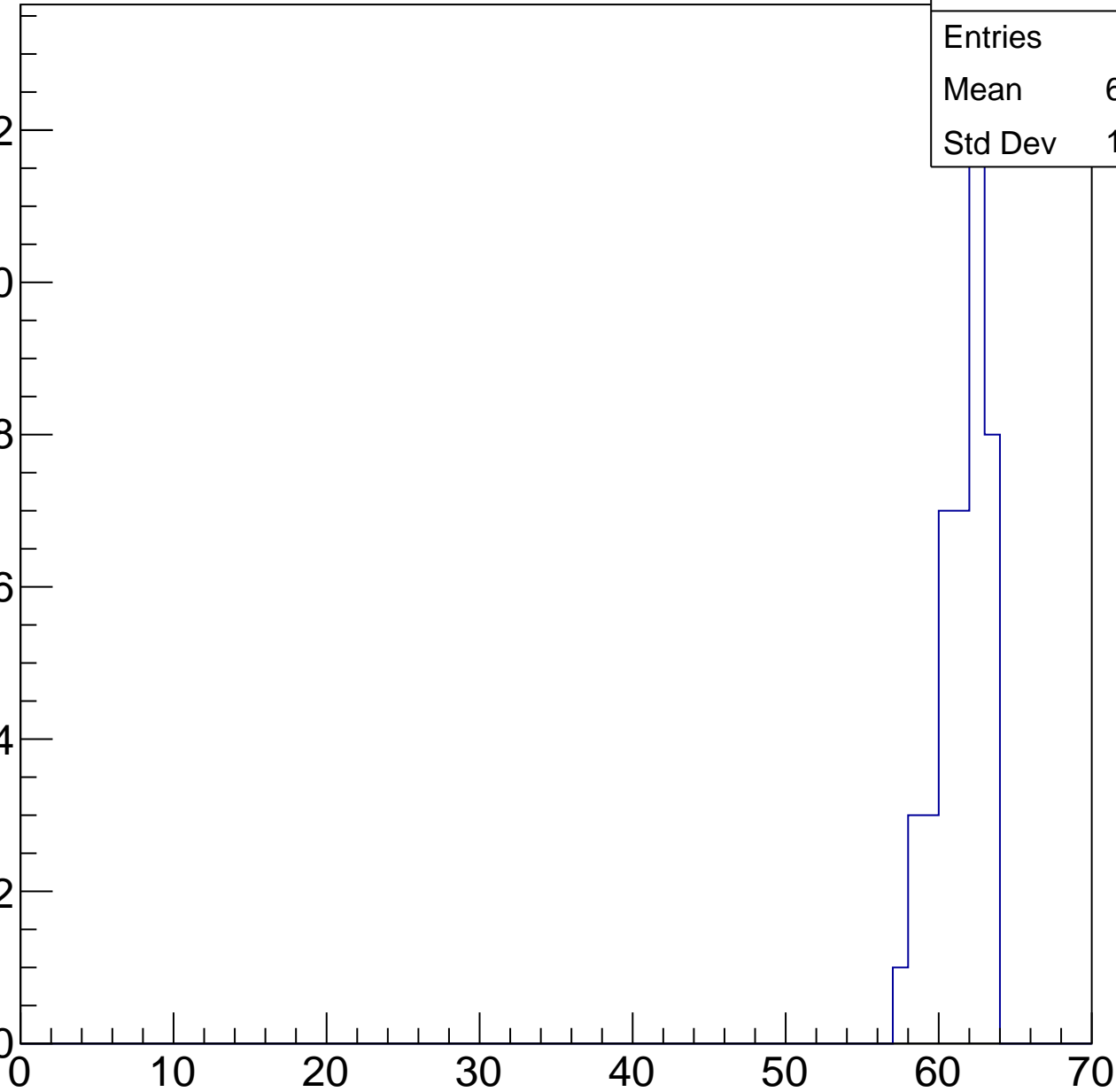
4

2

0

Entries	42
Mean	61.07
Std Dev	1.595

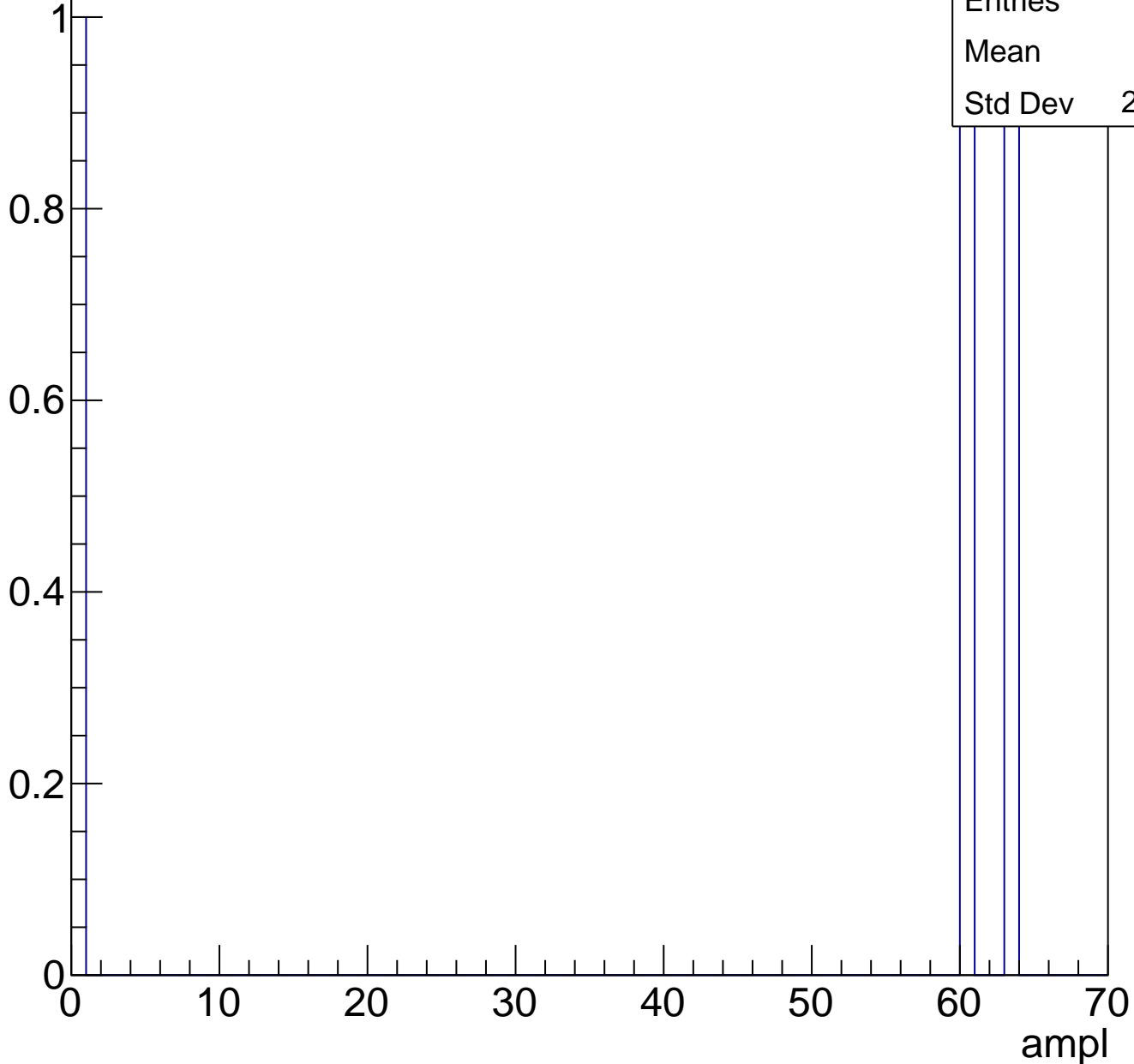
ampl



B0L101S, U24-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

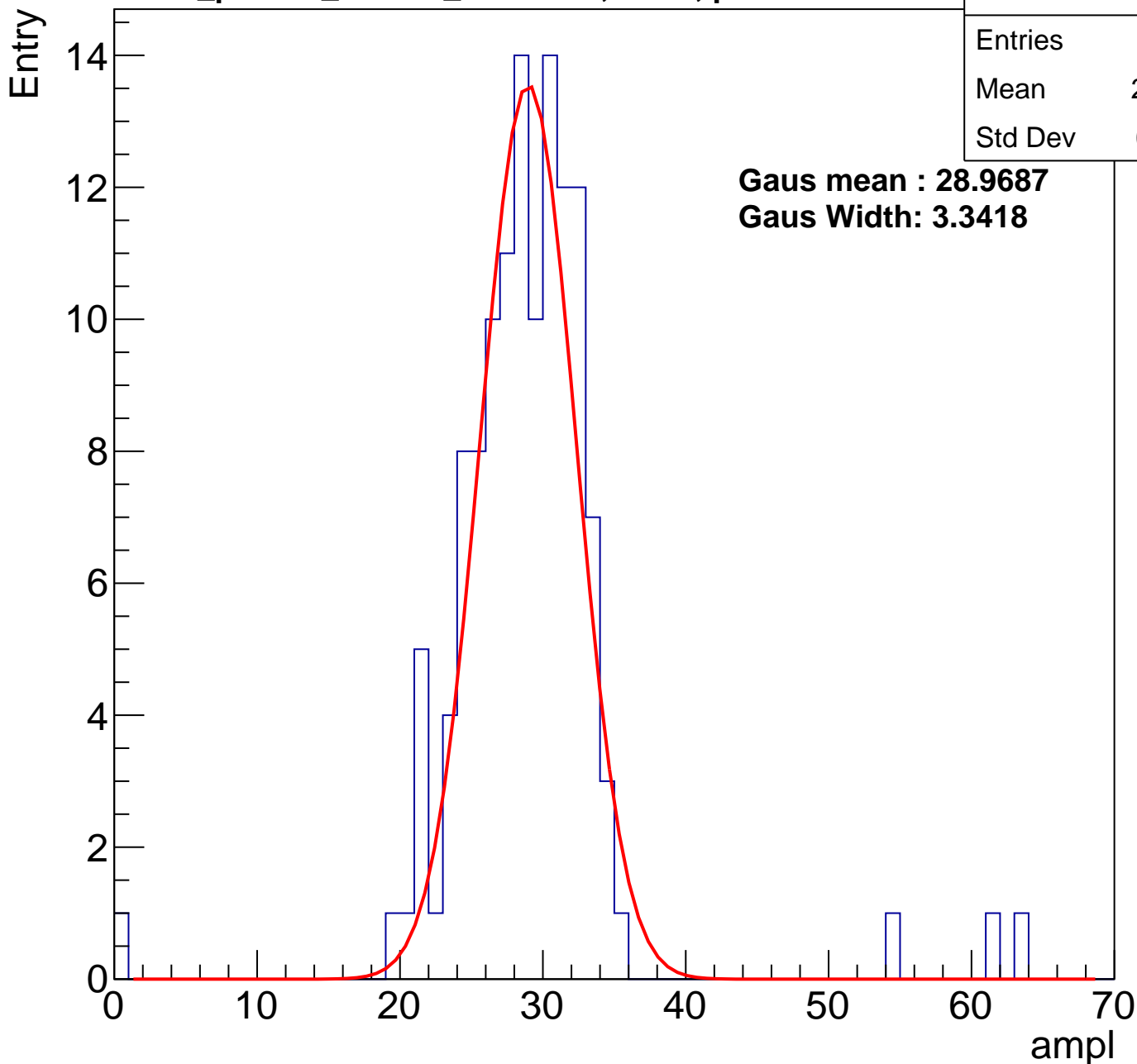
B0L101S, U24-ch122, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	28.66
Std Dev	6.421

Gaus mean : 28.9687

Gaus Width: 3.3418



B0L101S, U24-ch122, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	34.56
Std Dev	4.008

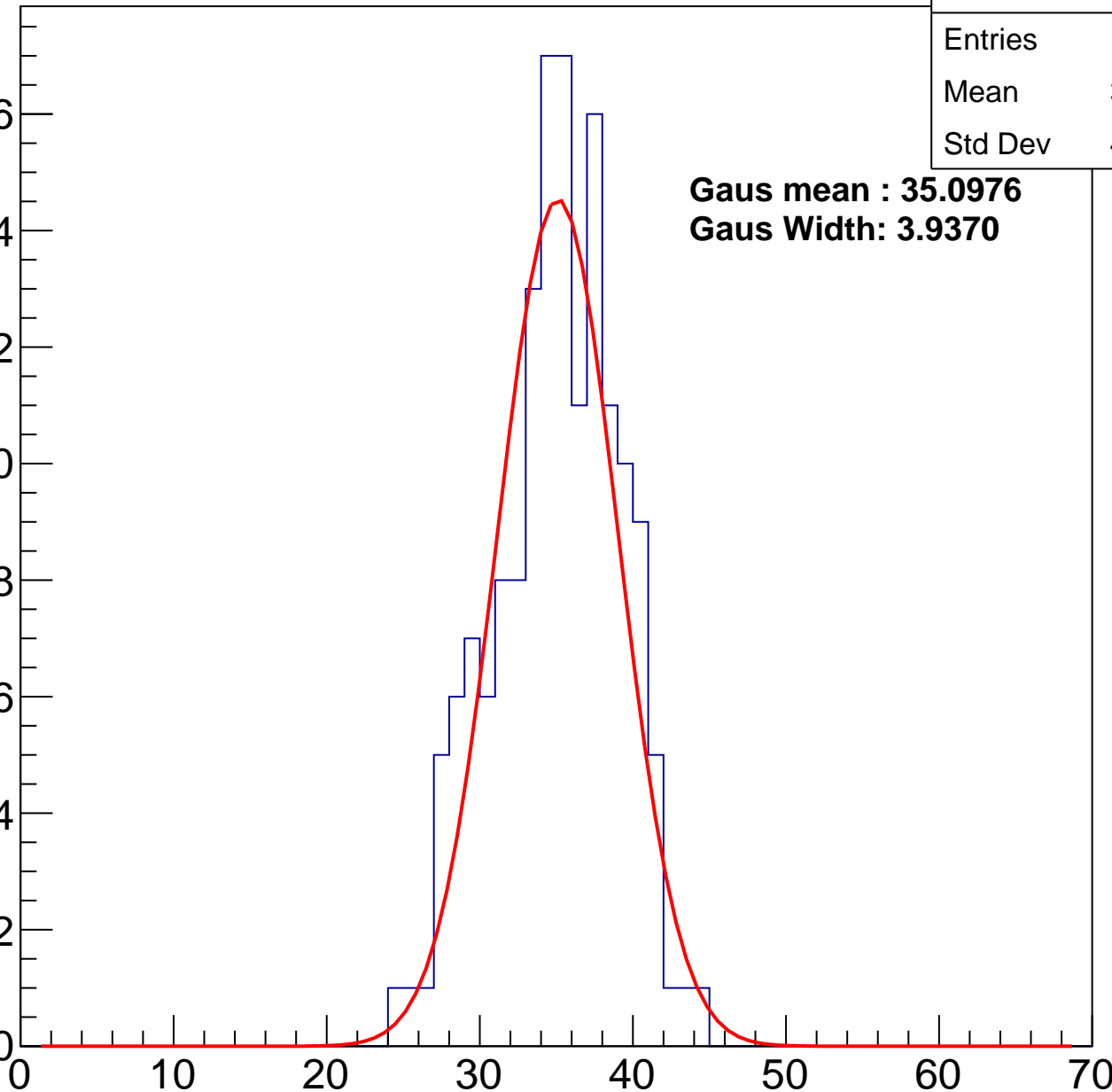
Gaus mean : 35.0976

Gaus Width: 3.9370

Entry

16
14
12
10
8
6
4
2
0

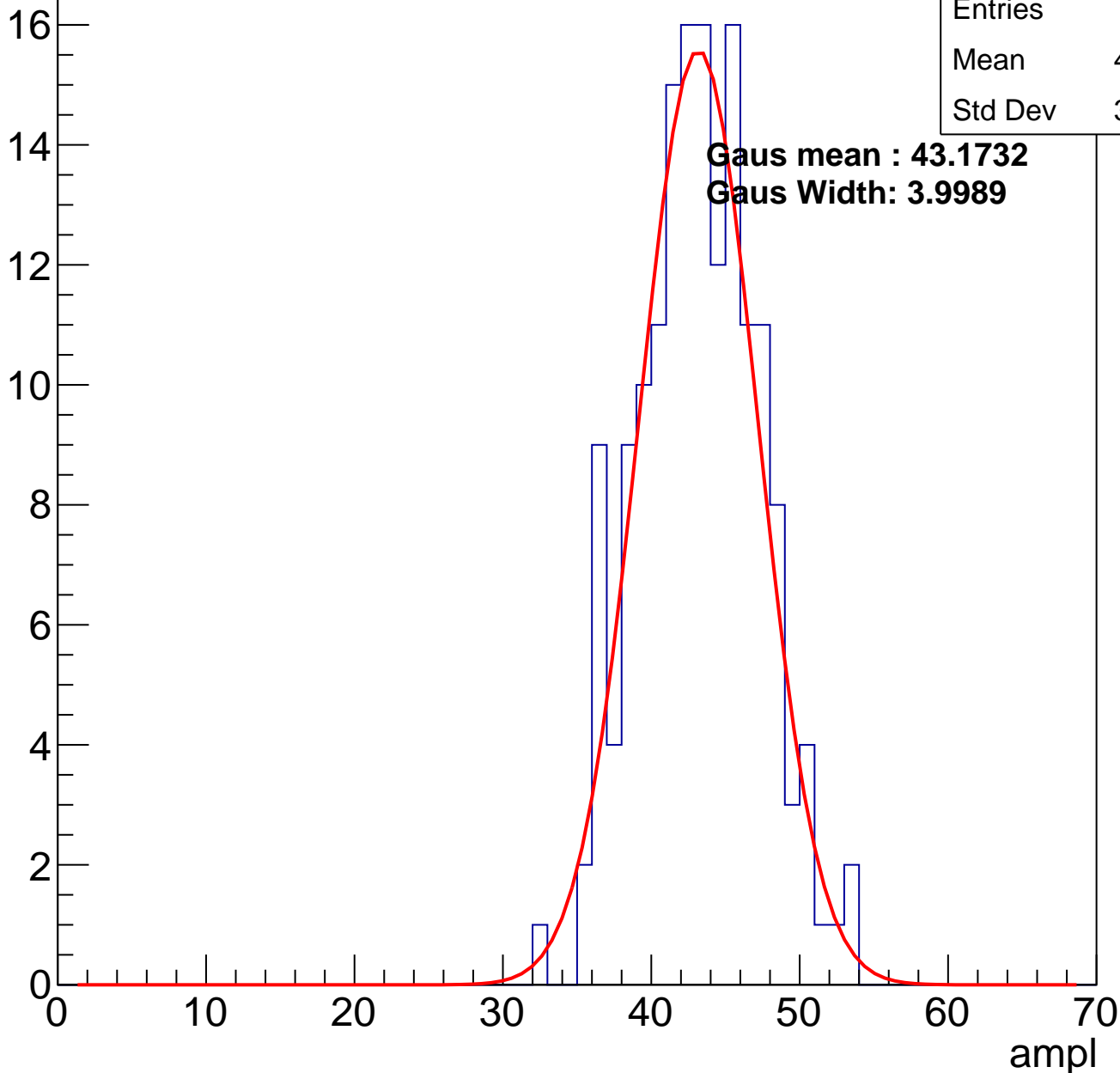
ampl



B0L101S, U24-ch122, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch122, adc3

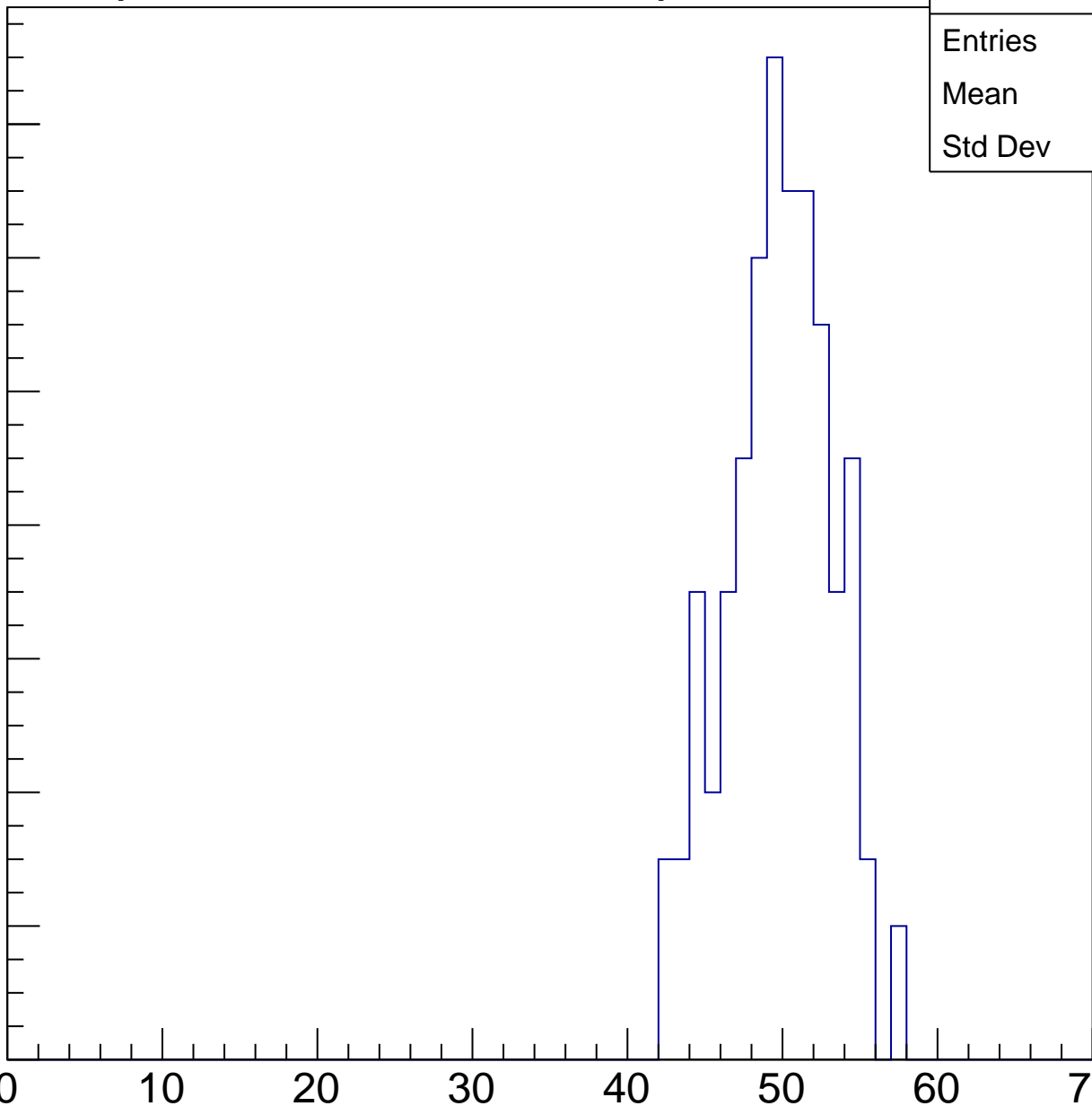
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	118
Mean	49.32
Std Dev	3.357

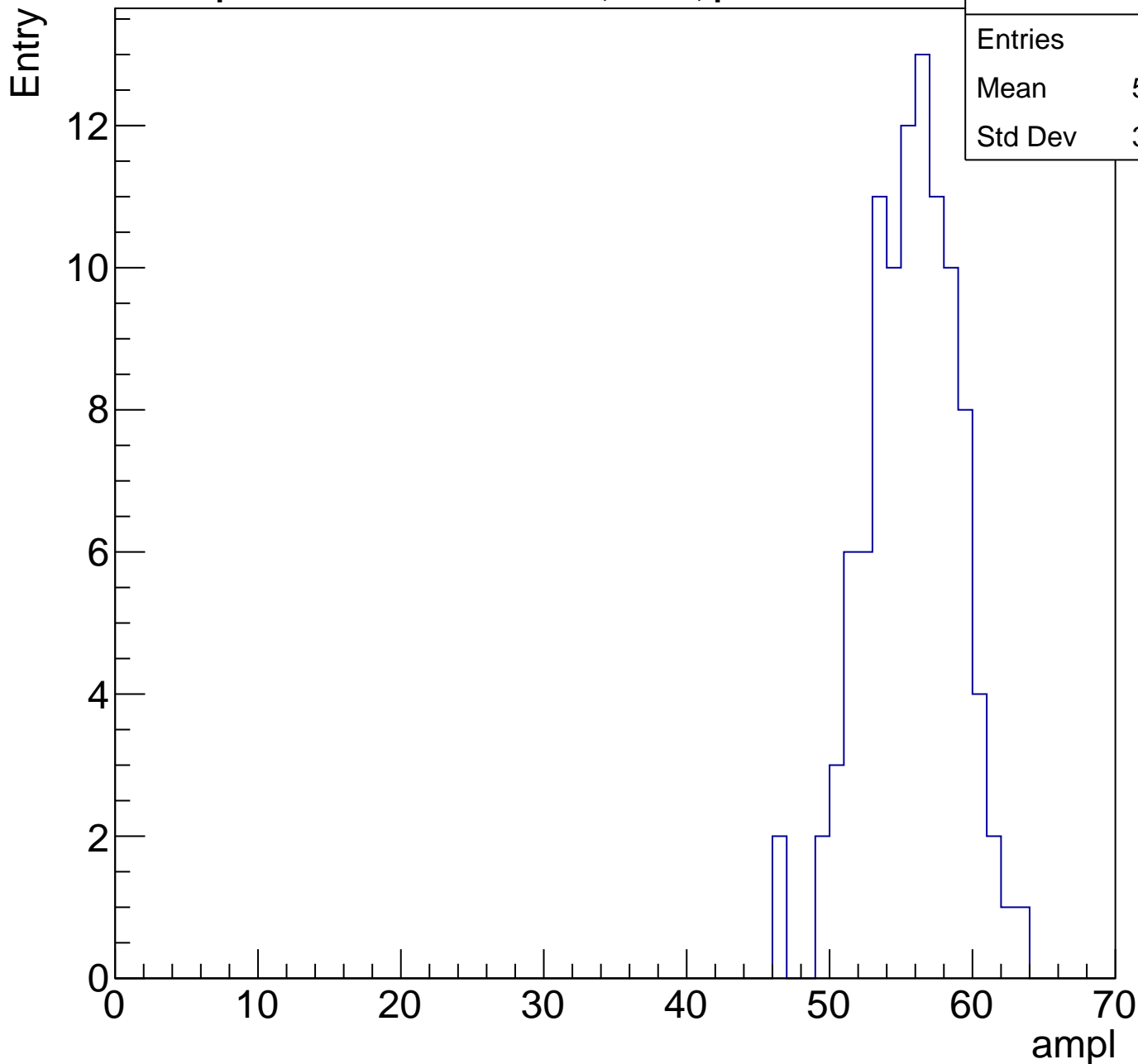
ampl



B0L101S, U24-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	55.25
Std Dev	3.234



B0L101S, U24-ch122, adc5

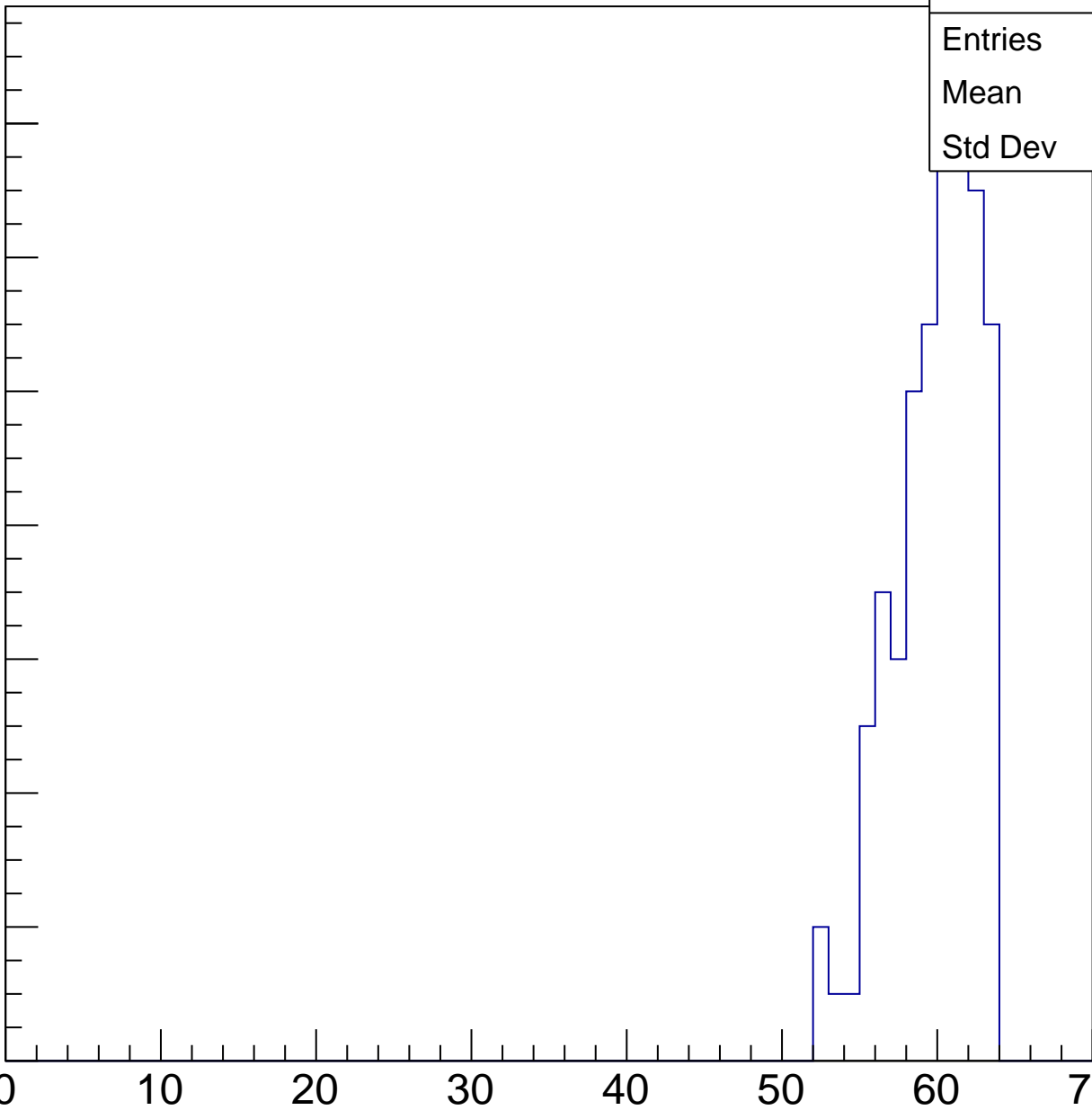
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	59.41
Std Dev	2.662

ampl

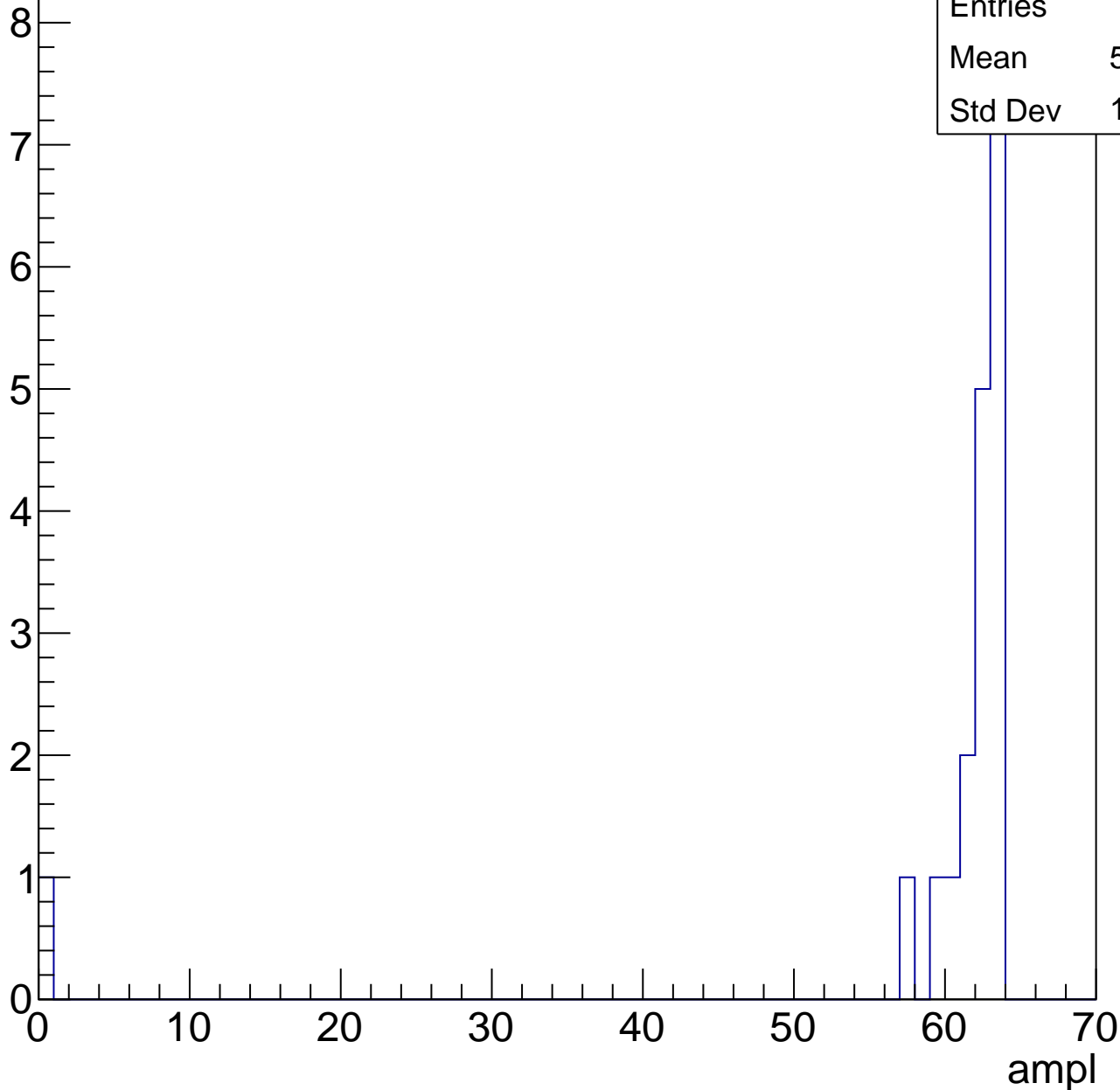


B0L101S, U24-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

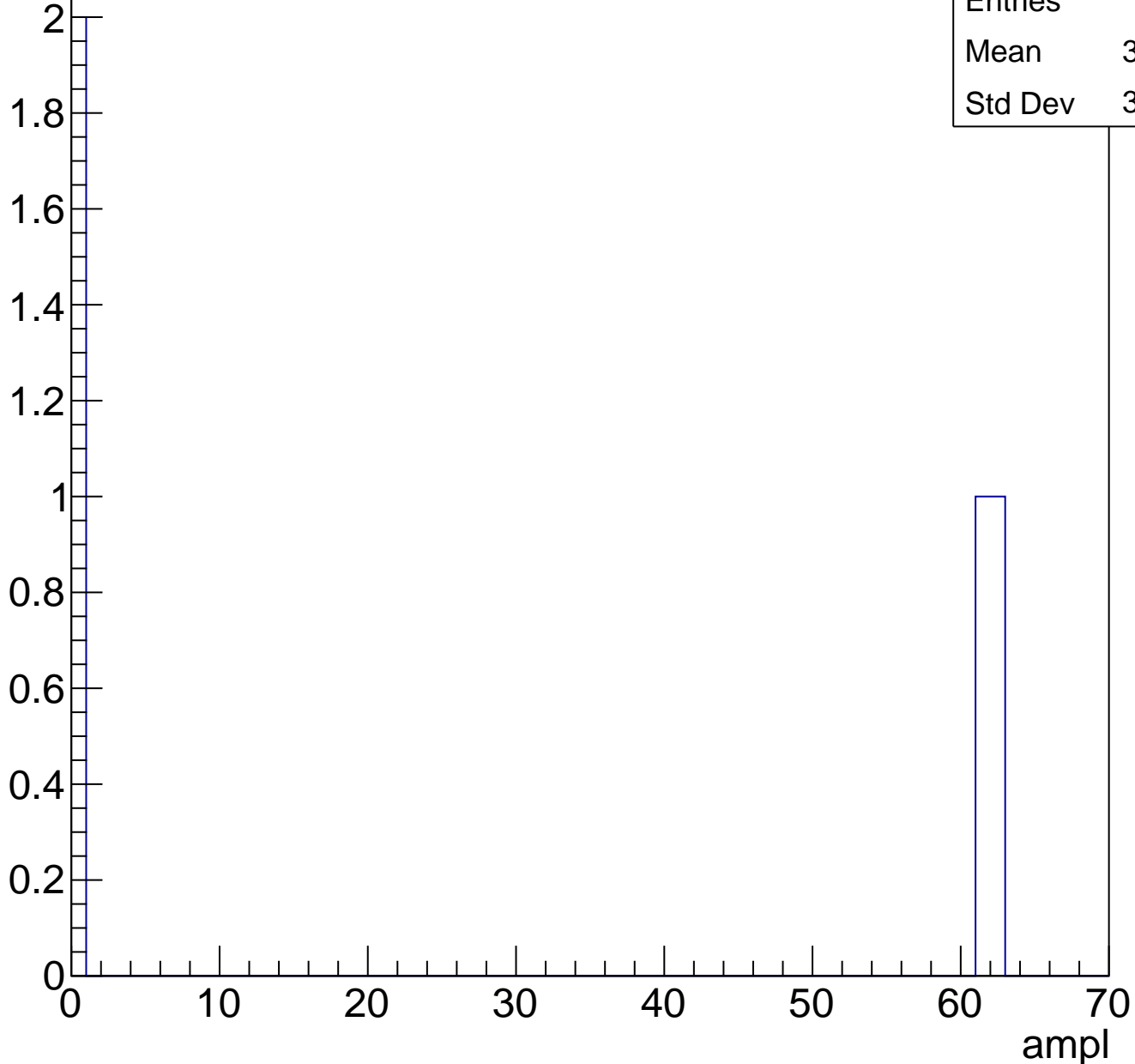
Entries	19
Mean	58.53
Std Dev	13.88



B0L101S, U24-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	30.75
Std Dev	30.75

B0L101S, U24-ch123, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	29.03
Std Dev	4.969

Gaus mean : 29.5039

Gaus Width: 3.6393

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

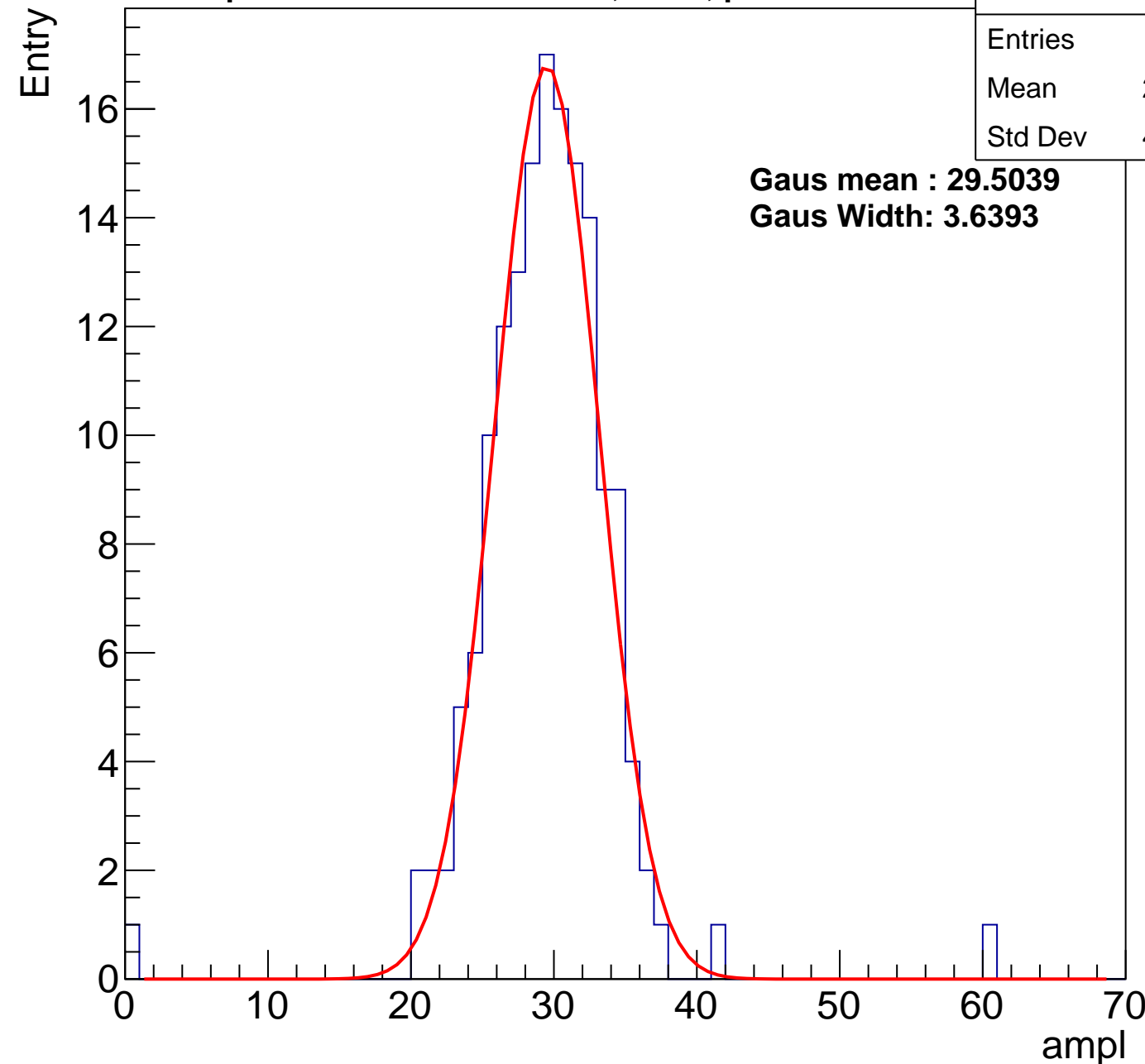
40

50

60

70

ampl



B0L101S, U24-ch123, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	36.09
Std Dev	4.607

Gaus mean : 36.8336

Gaus Width: 3.2792

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

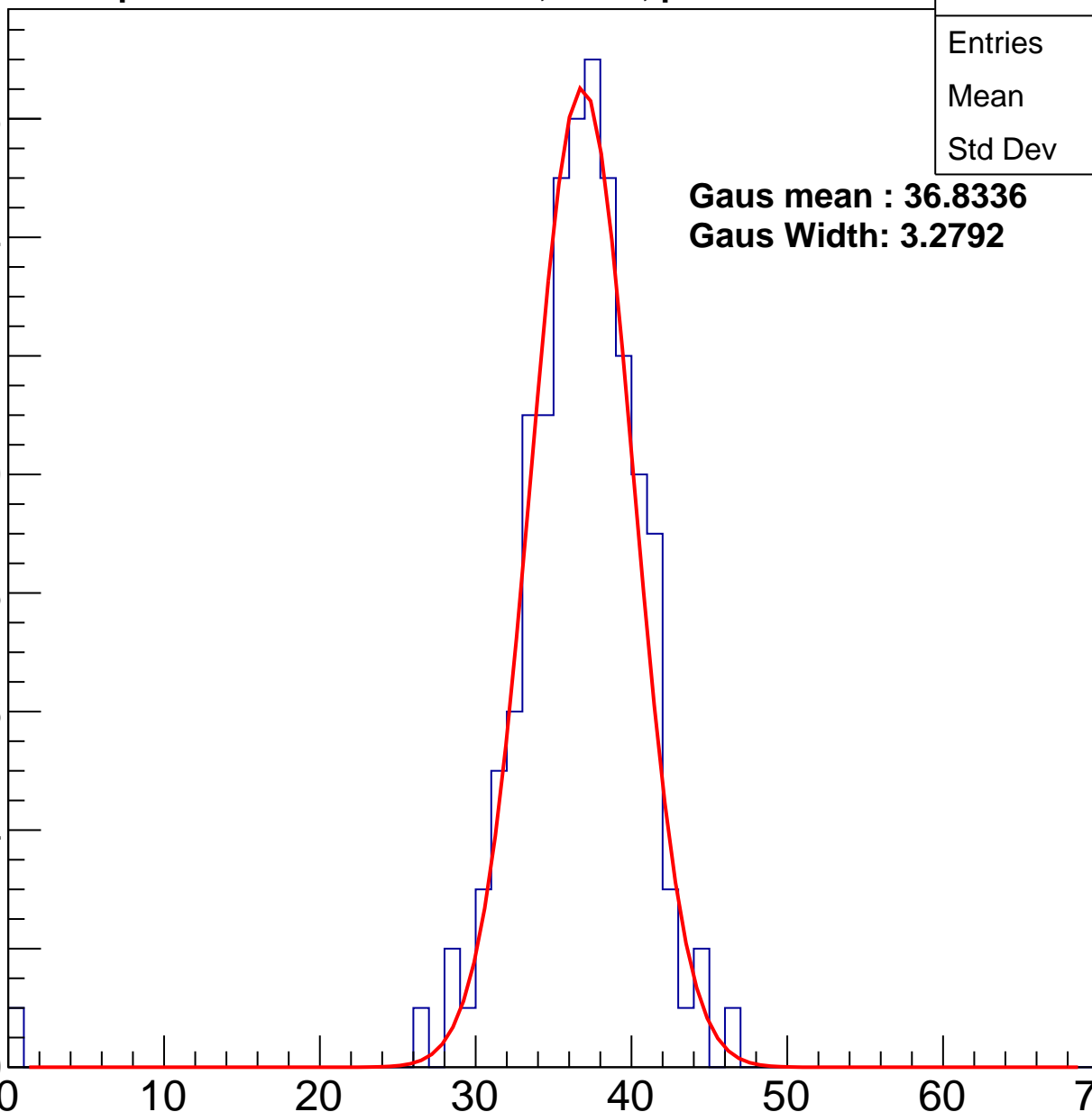
40

50

60

70

ampl



B0L101S, U24-ch123, adc2

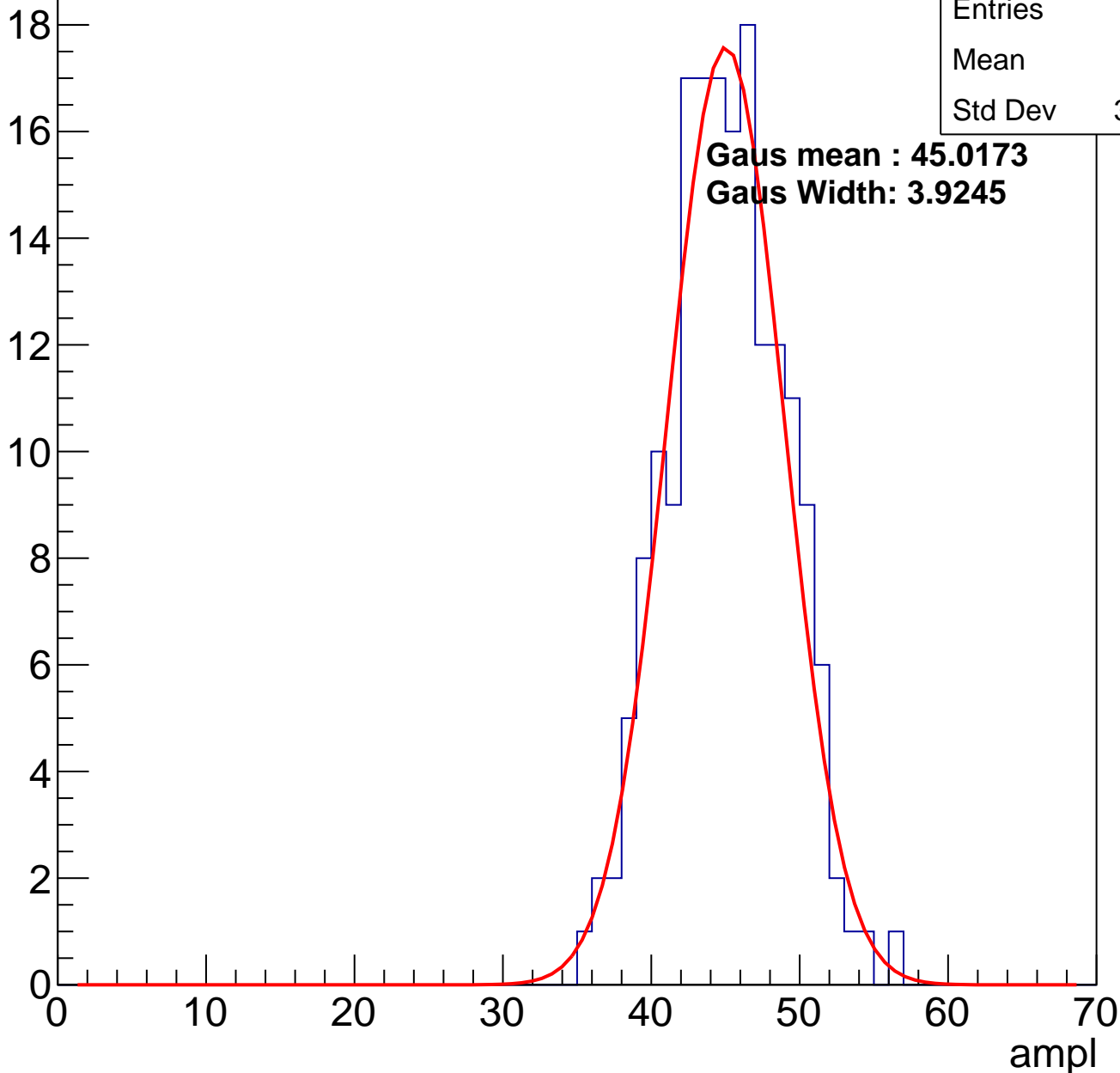
calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	44.6
Std Dev	3.893

Gaus mean : 45.0173

Gaus Width: 3.9245

Entry



B0L101S, U24-ch123, adc3

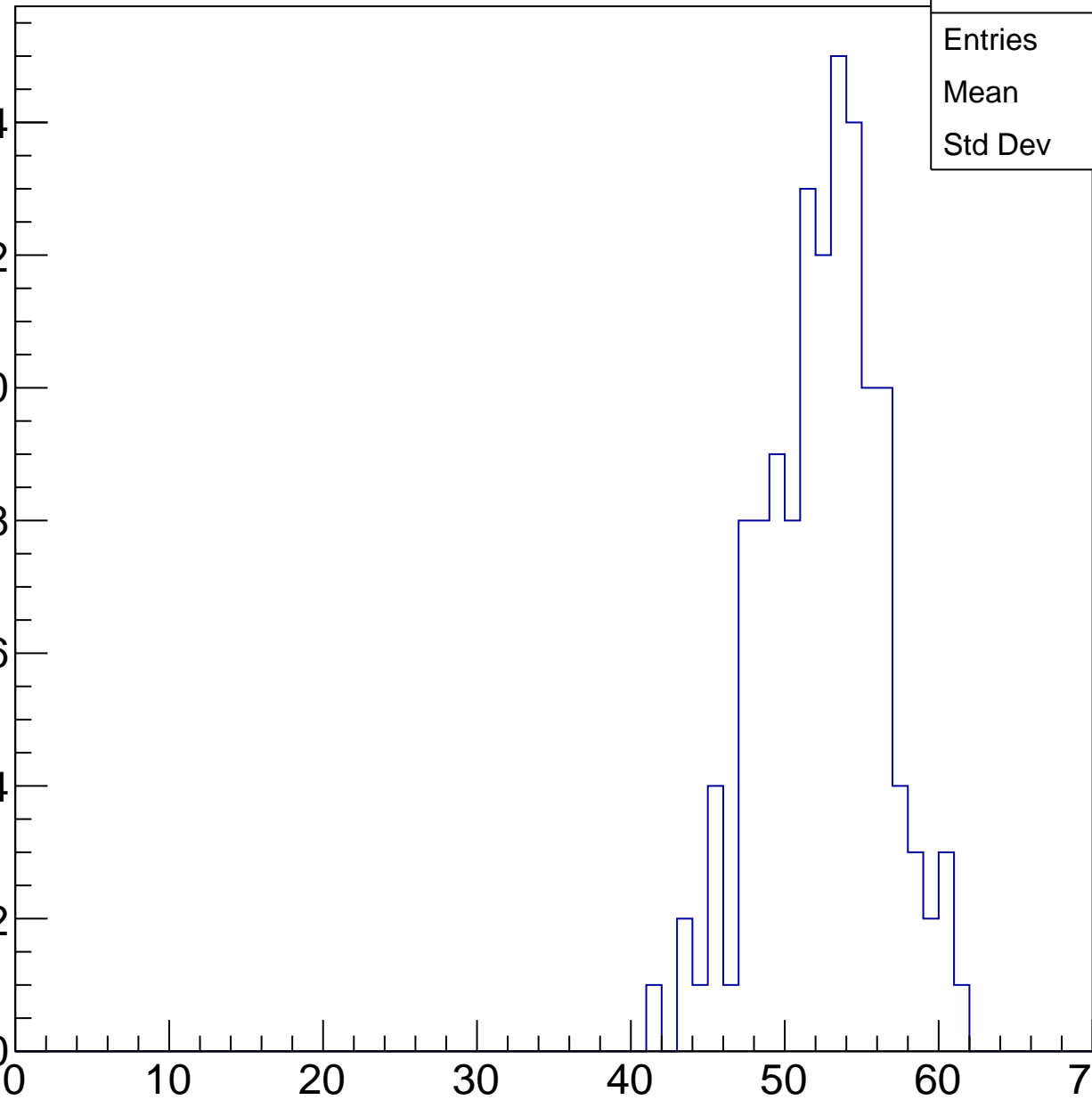
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	51.99
Std Dev	3.871

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch123, adc4

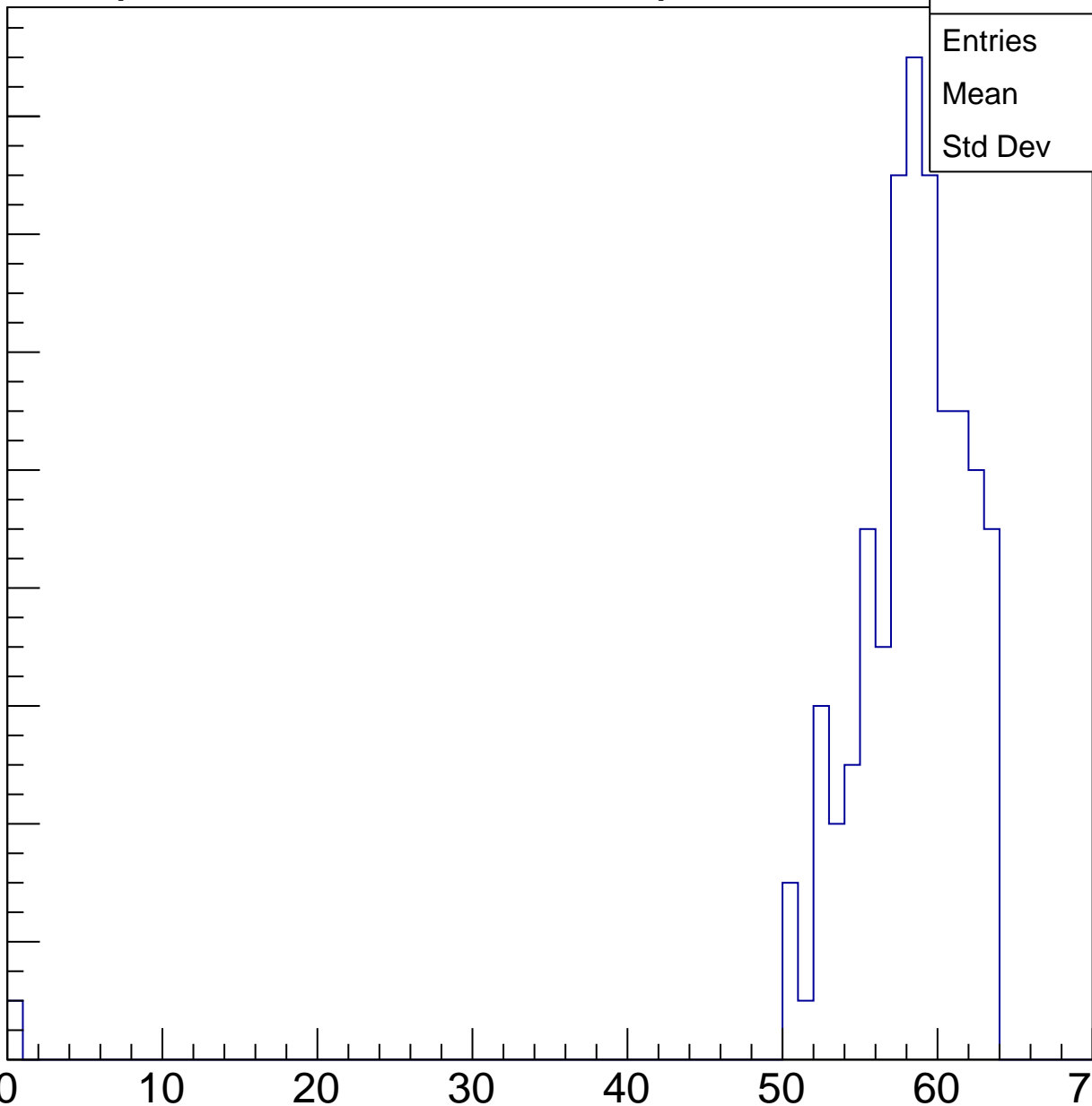
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	57.47
Std Dev	6.112

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U24-ch123, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	42
Mean	59.5
Std Dev	9.508

Entry

10

8

6

4

2

0

0

10

20

30

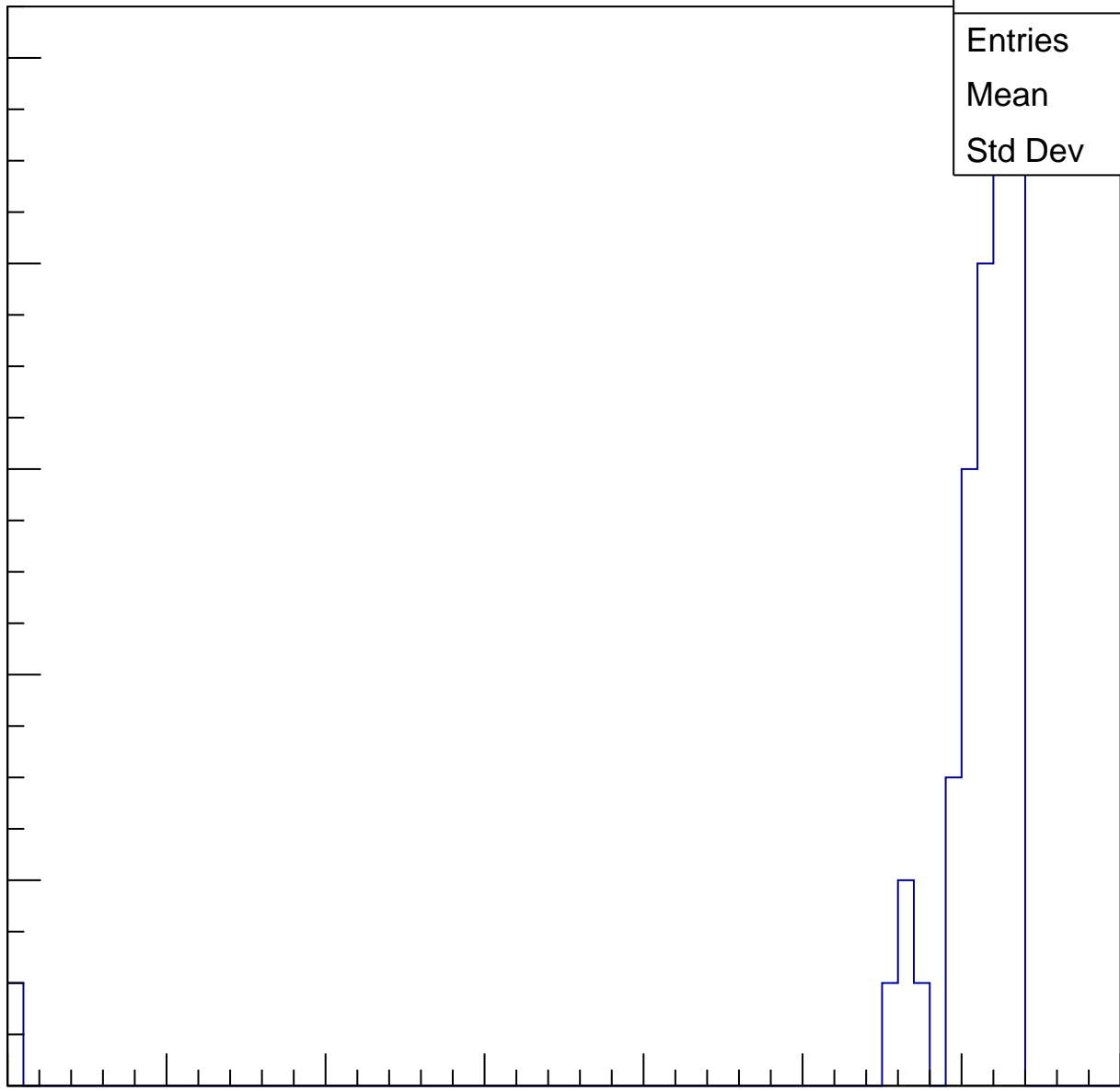
40

50

60

70

ampl



B0L101S, U24-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L101S, U24-ch124, adc0

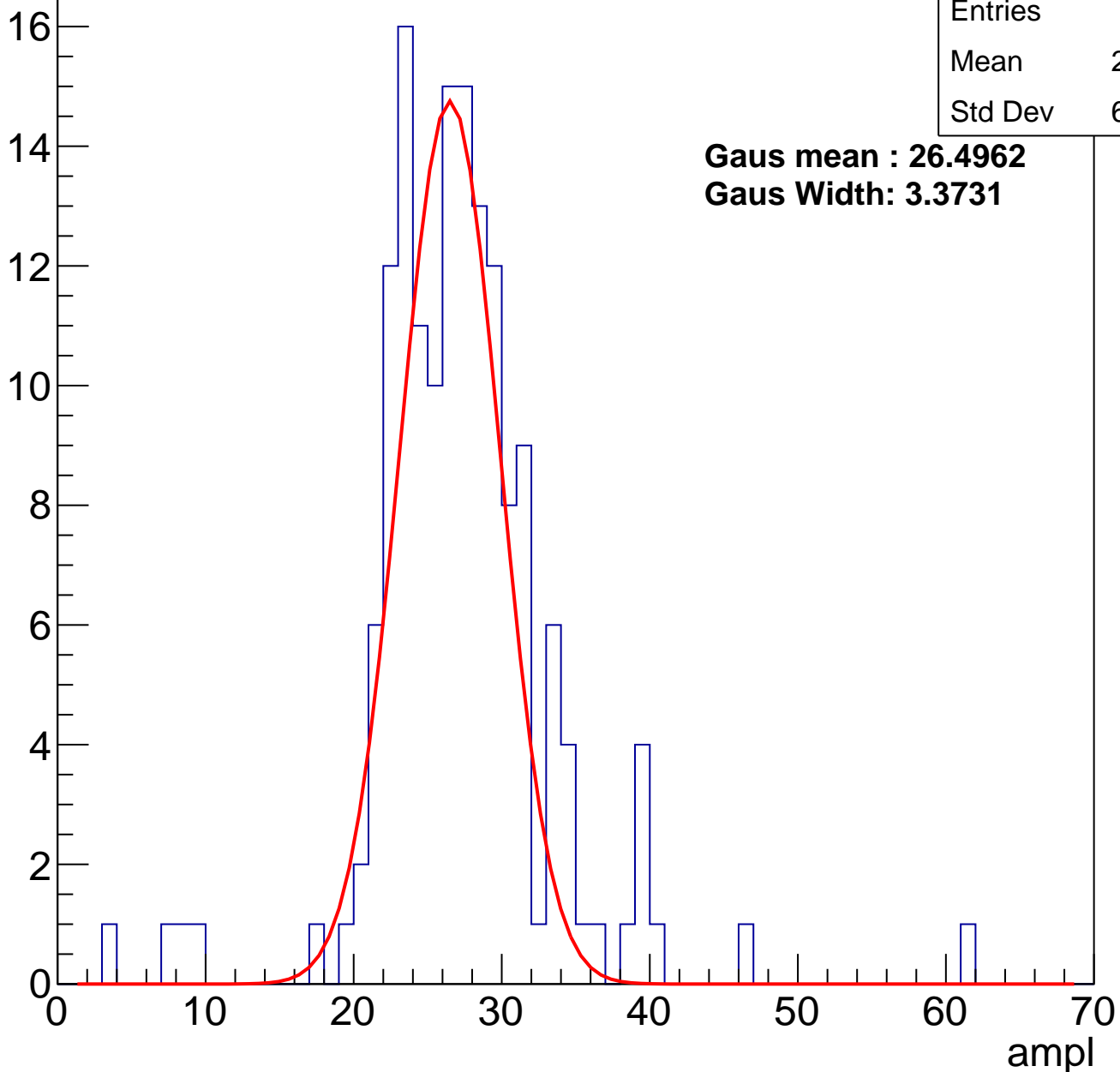
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	26.79
Std Dev	6.245

Gaus mean : 26.4962

Gaus Width: 3.3731

Entry



B0L101S, U24-ch124, adc1

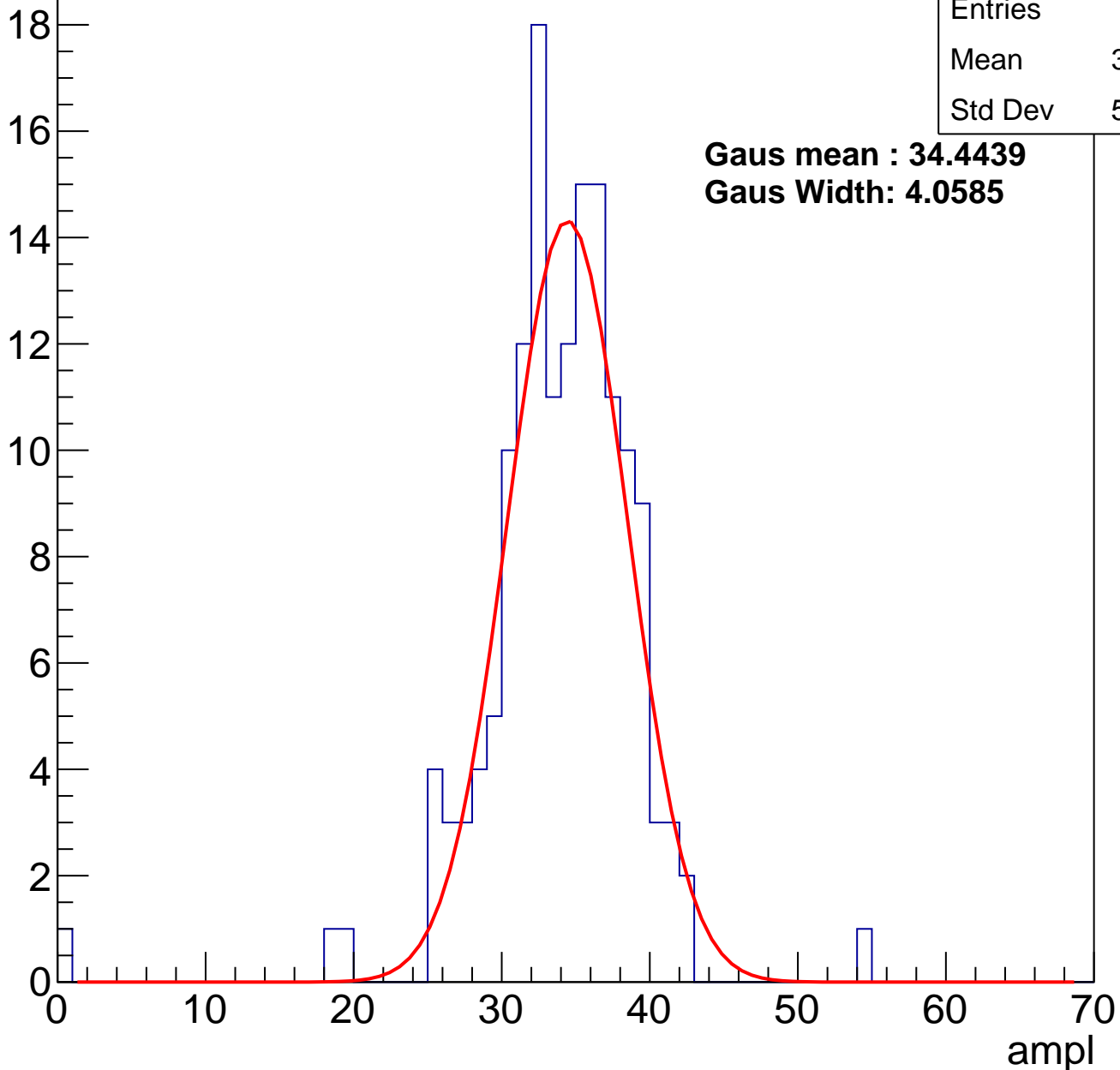
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	33.48
Std Dev	5.235

Gaus mean : 34.4439

Gaus Width: 4.0585

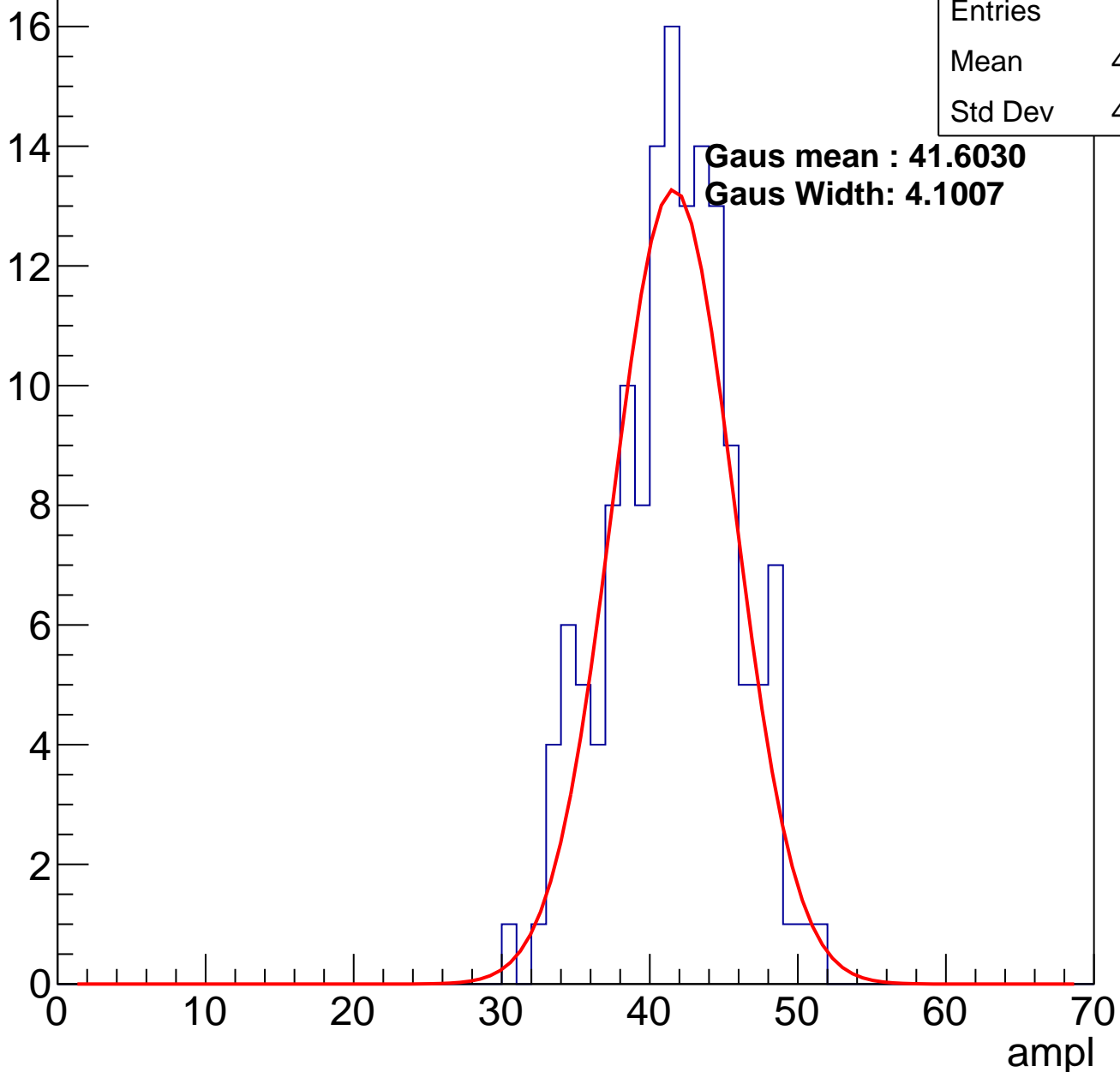
Entry



B0L101S, U24-ch124, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

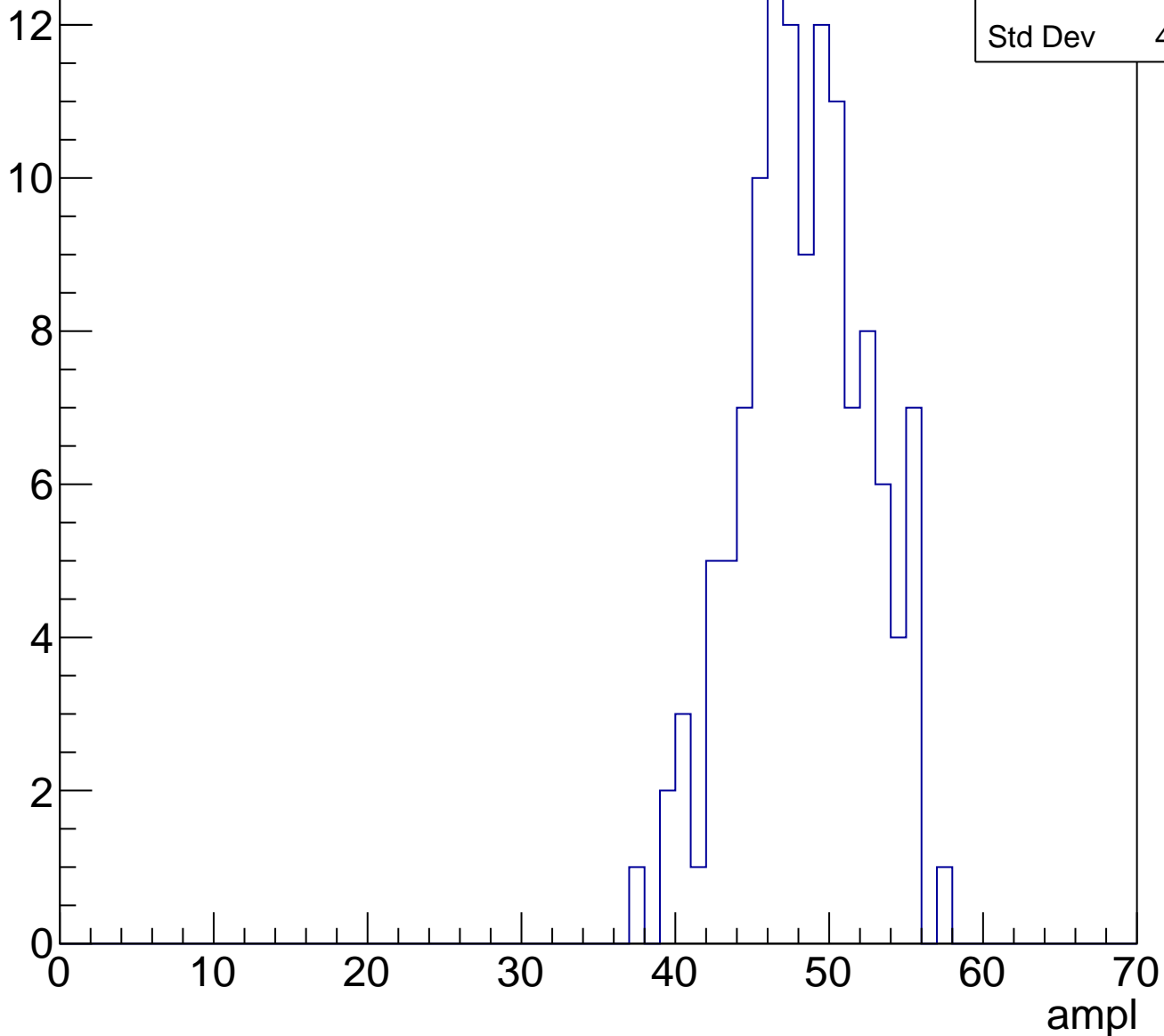


B0L101S, U24-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	47.9
Std Dev	4.075

Entry

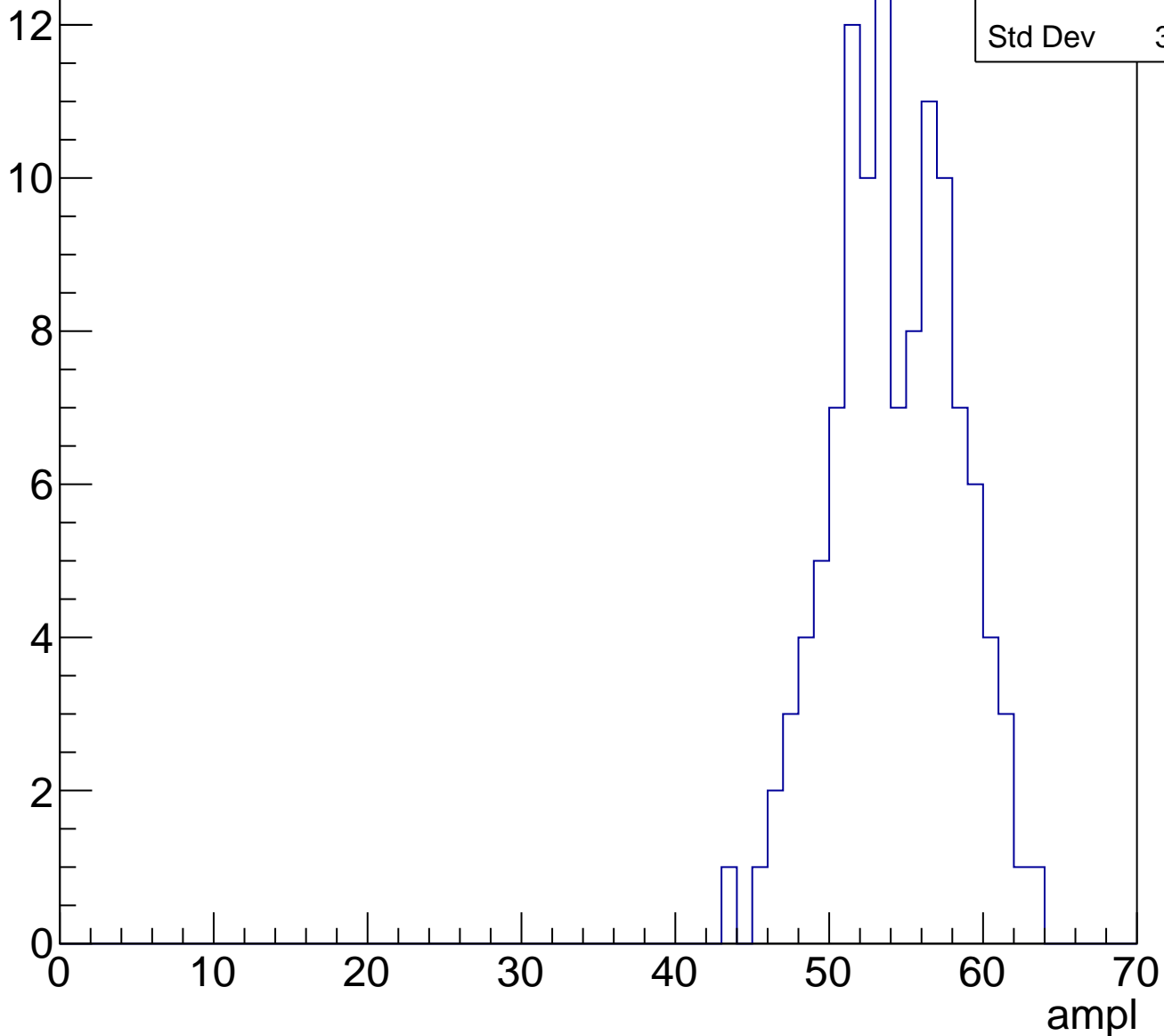


B0L101S, U24-ch124, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	53.8
Std Dev	3.998

Entry



B0L101S, U24-ch124, adc5

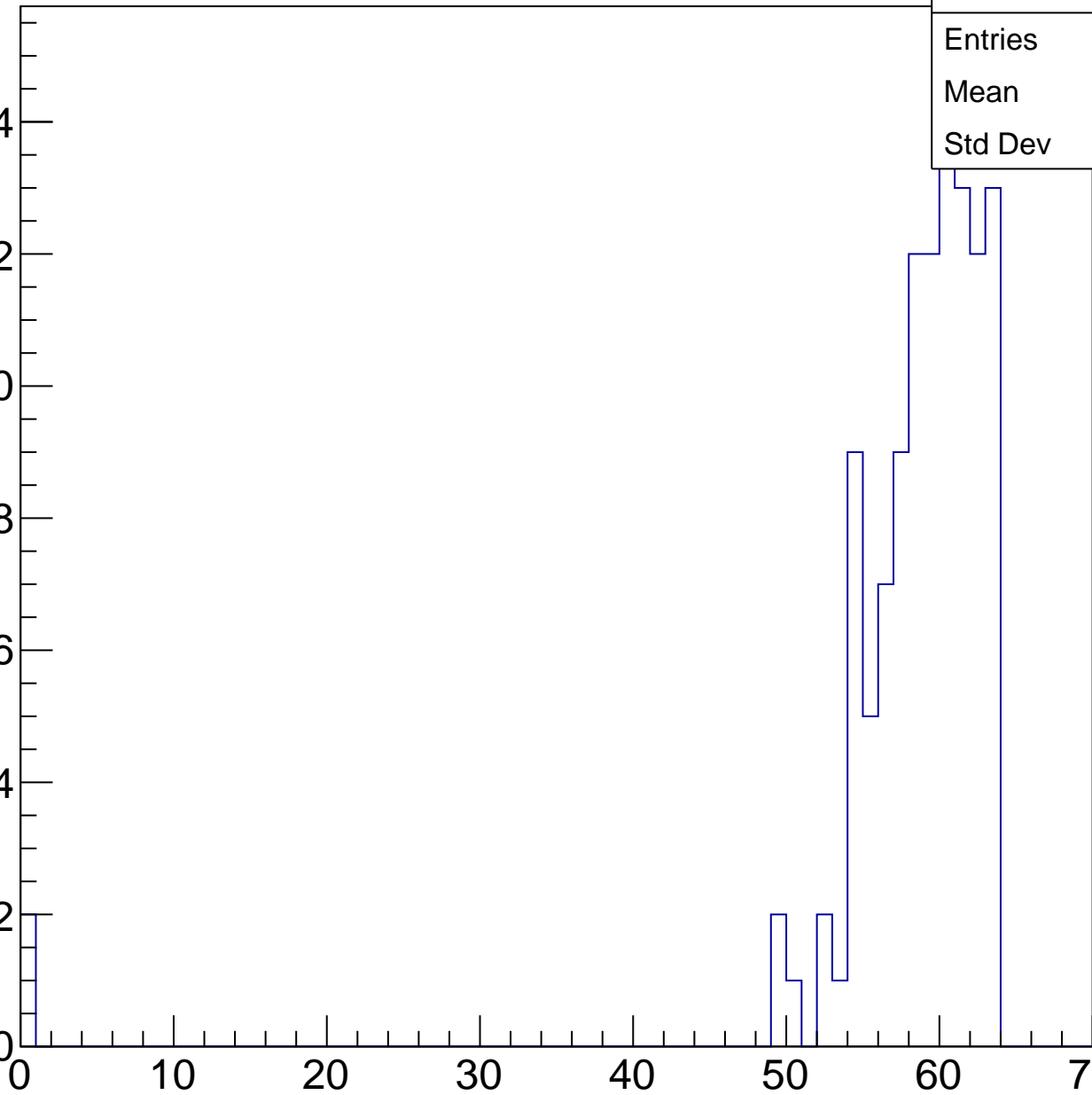
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	57.66
Std Dev	8.325

ampl

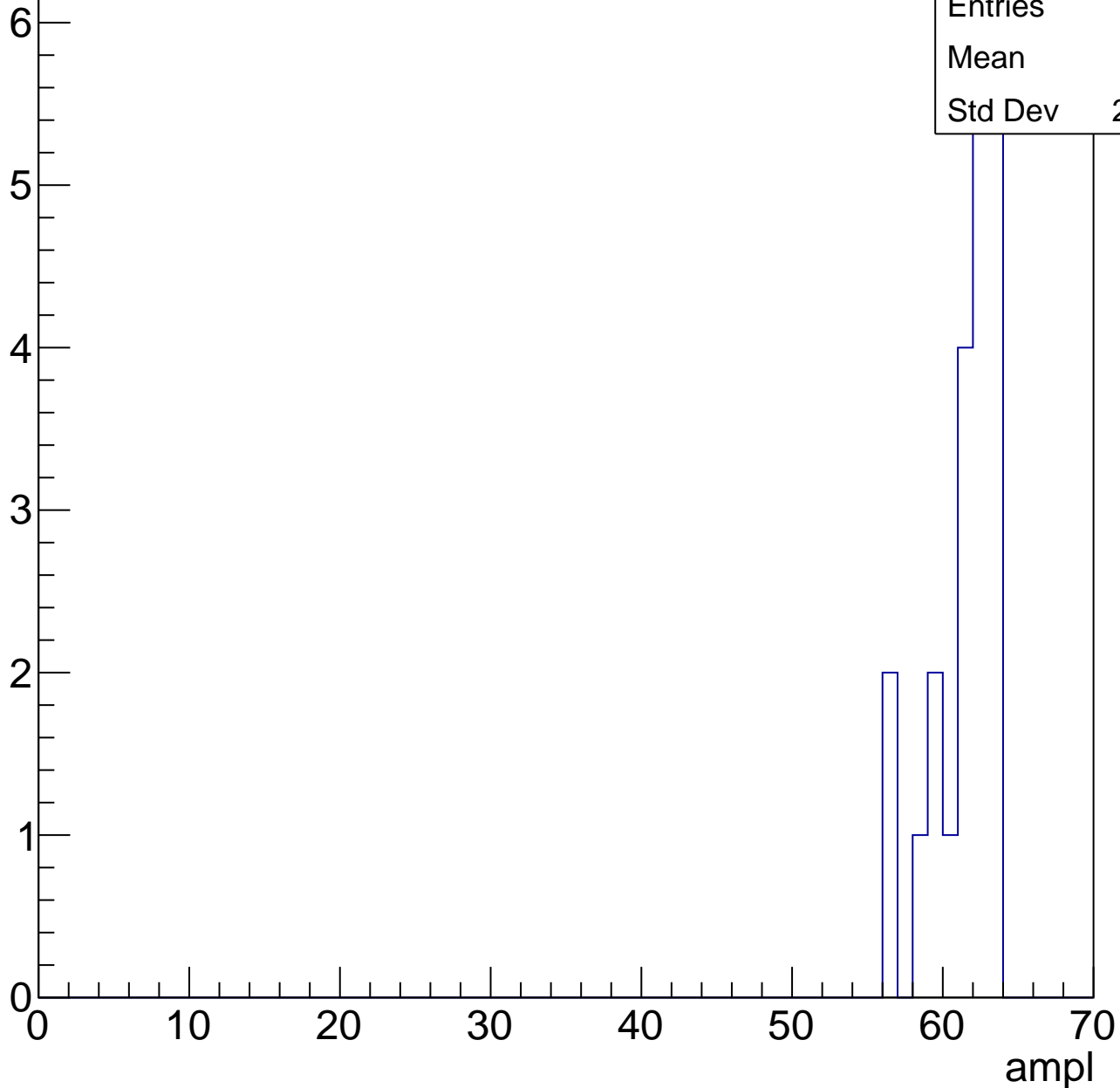


B0L101S, U24-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	22
Mean	61
Std Dev	2.111



B0L101S, U24-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch125, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

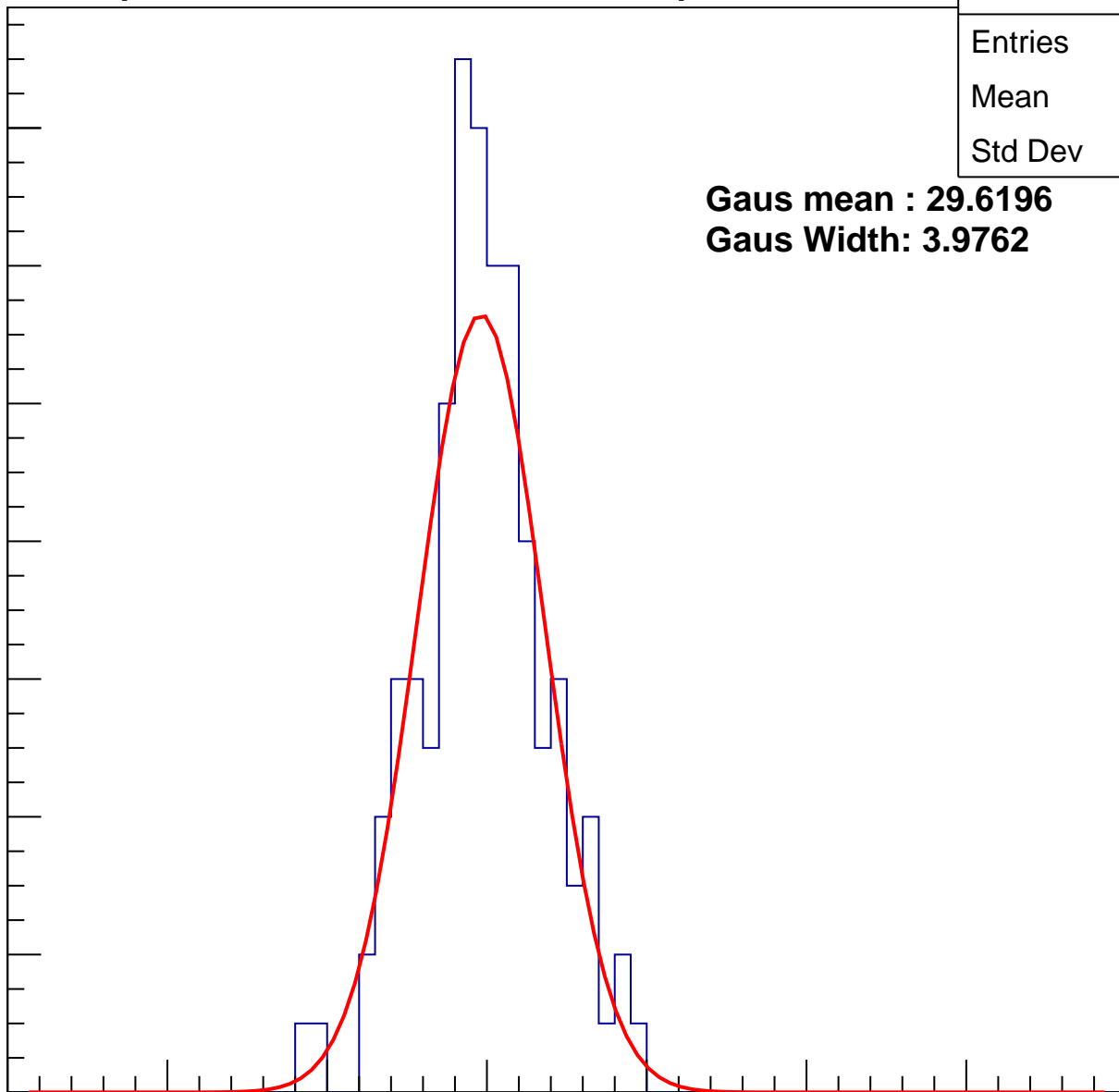
70

ampl

Entries	118
Mean	29.25
Std Dev	3.899

Gaus mean : 29.6196

Gaus Width: 3.9762



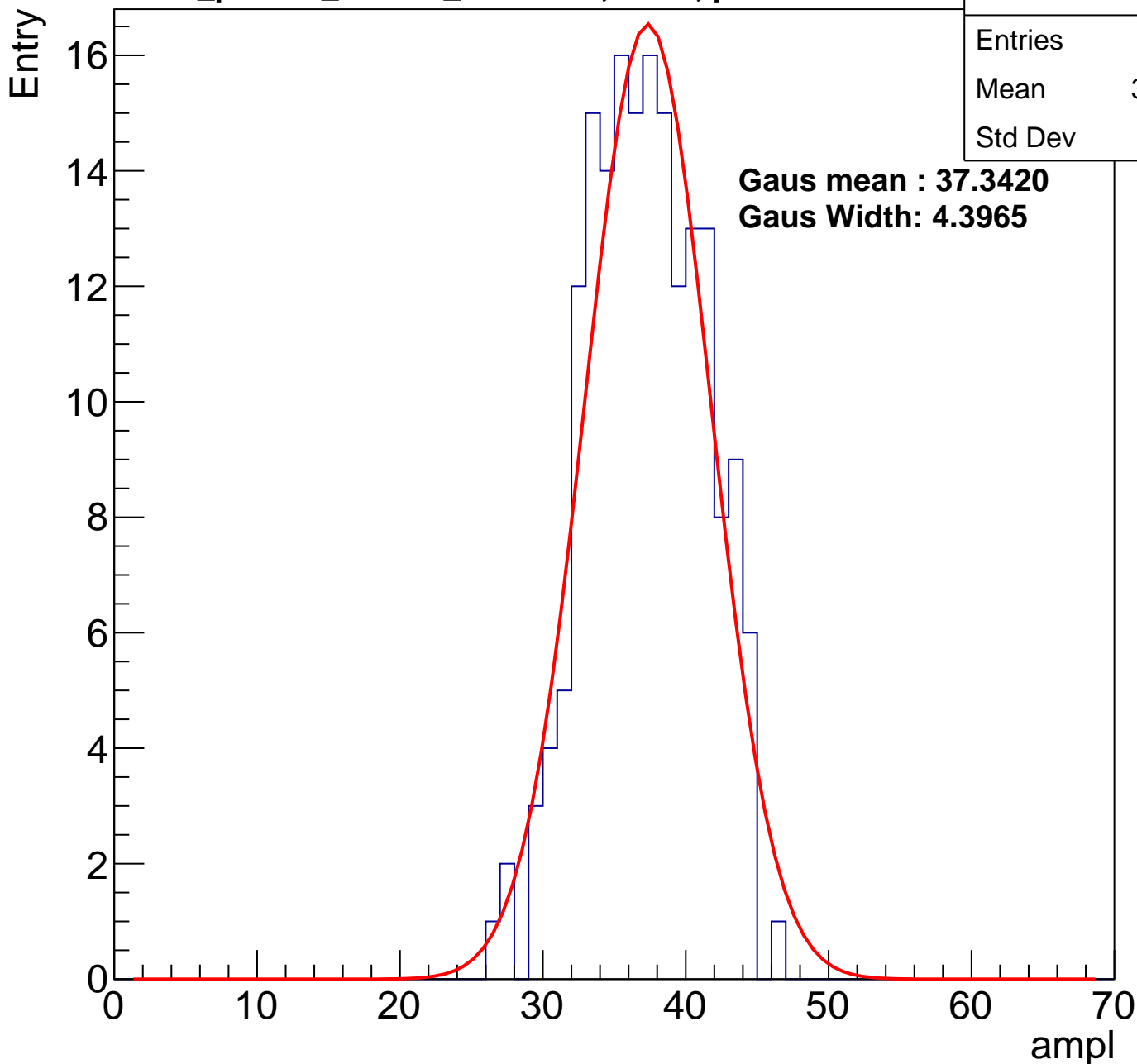
B0L101S, U24-ch125, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	36.74
Std Dev	4.02

Gaus mean : 37.3420

Gaus Width: 4.3965



B0L101S, U24-ch125, adc2

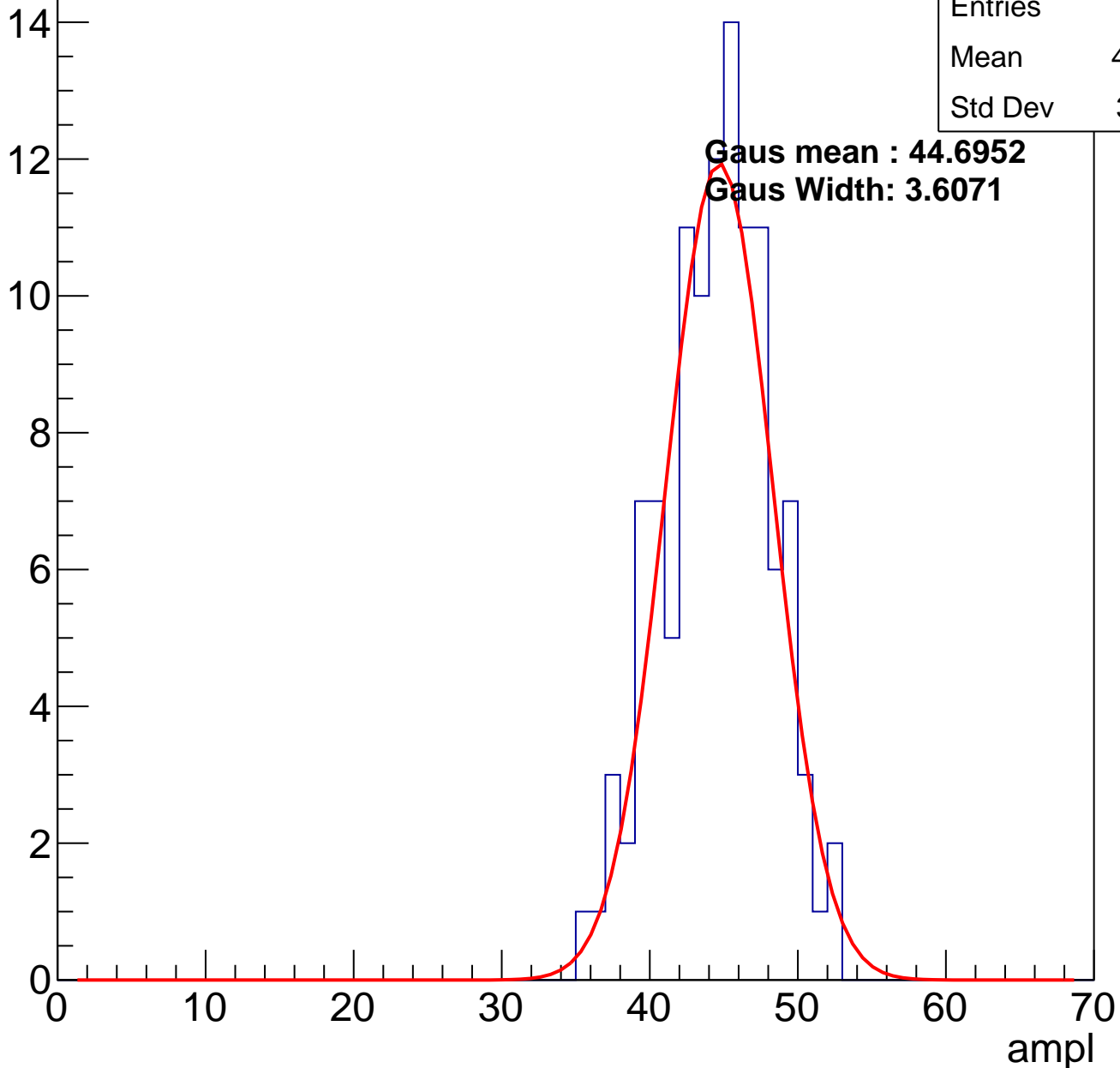
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	114
Mean	44.08
Std Dev	3.581

Gaus mean : 44.6952

Gaus Width: 3.6071



B0L101S, U24-ch125, adc3

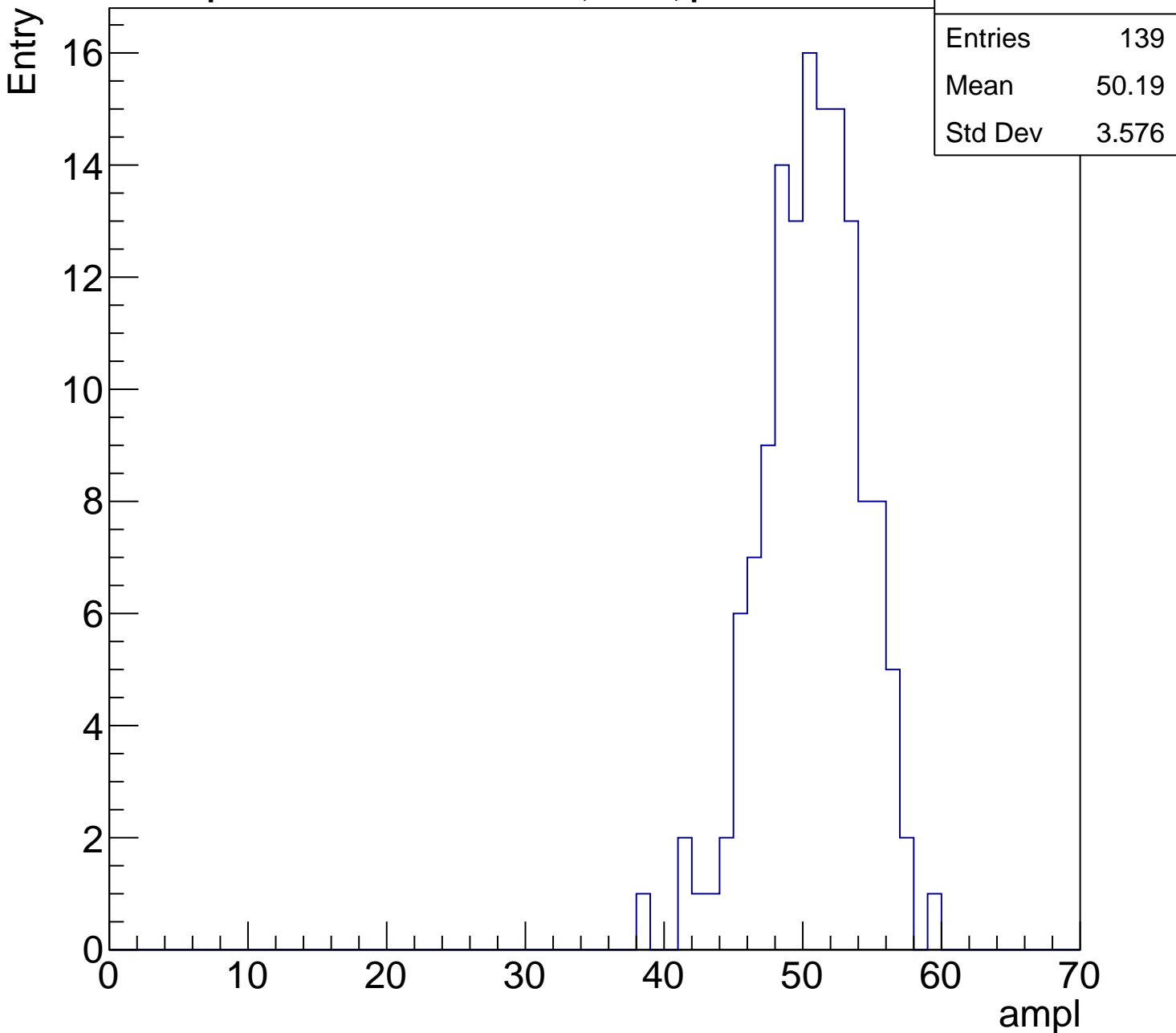
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	139
Mean	50.19
Std Dev	3.576

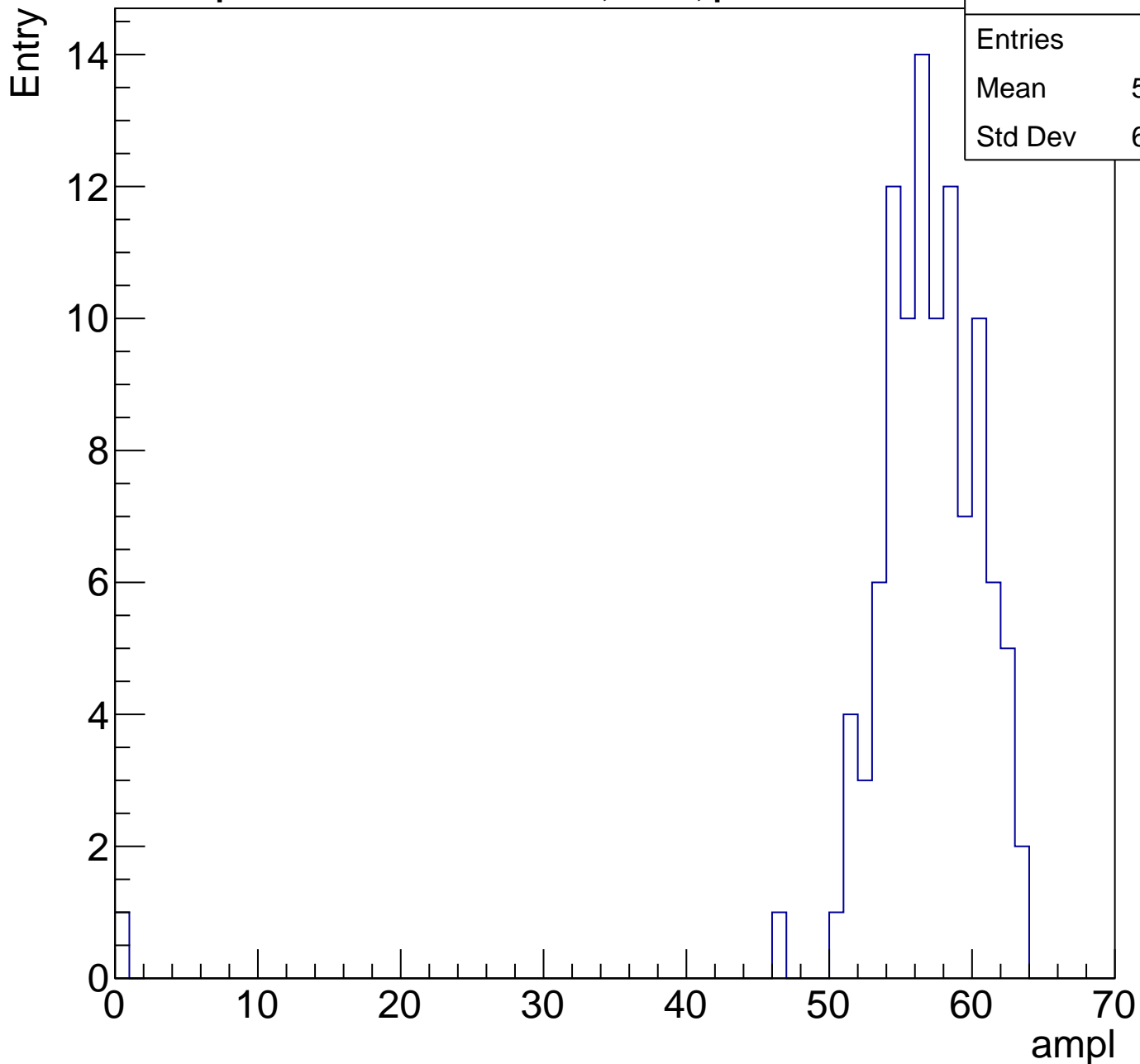
ampl



B0L101S, U24-ch125, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	56.12
Std Dev	6.385



B0L101S, U24-ch125, adc5

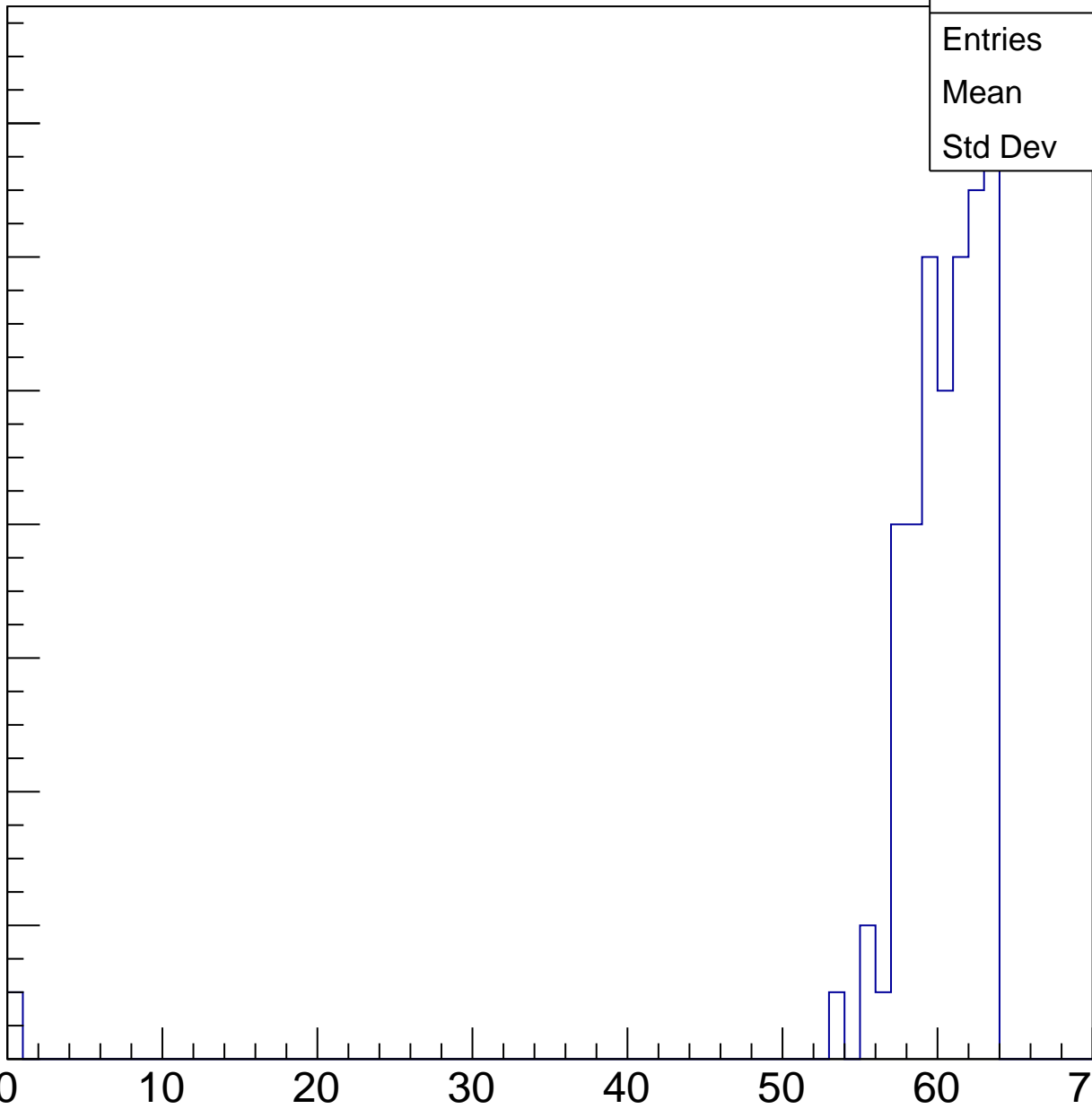
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	59.4
Std Dev	6.942

ampl



B0L101S, U24-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	62
Std Dev	0.8165

ampl

B0L101S, U24-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L101S, U24-ch126, adc0

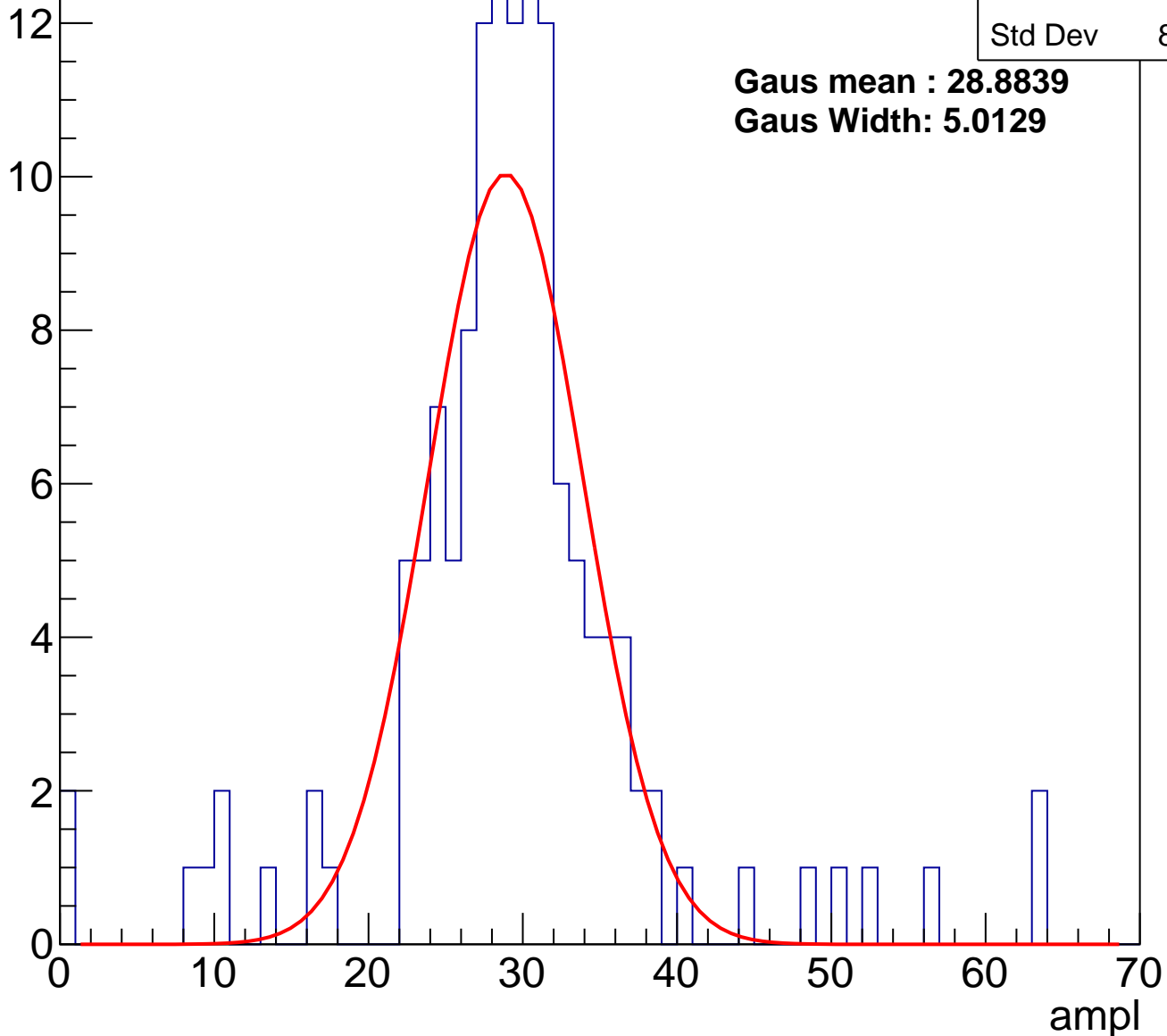
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	28.95
Std Dev	8.729

Gaus mean : 28.8839

Gaus Width: 5.0129

Entry

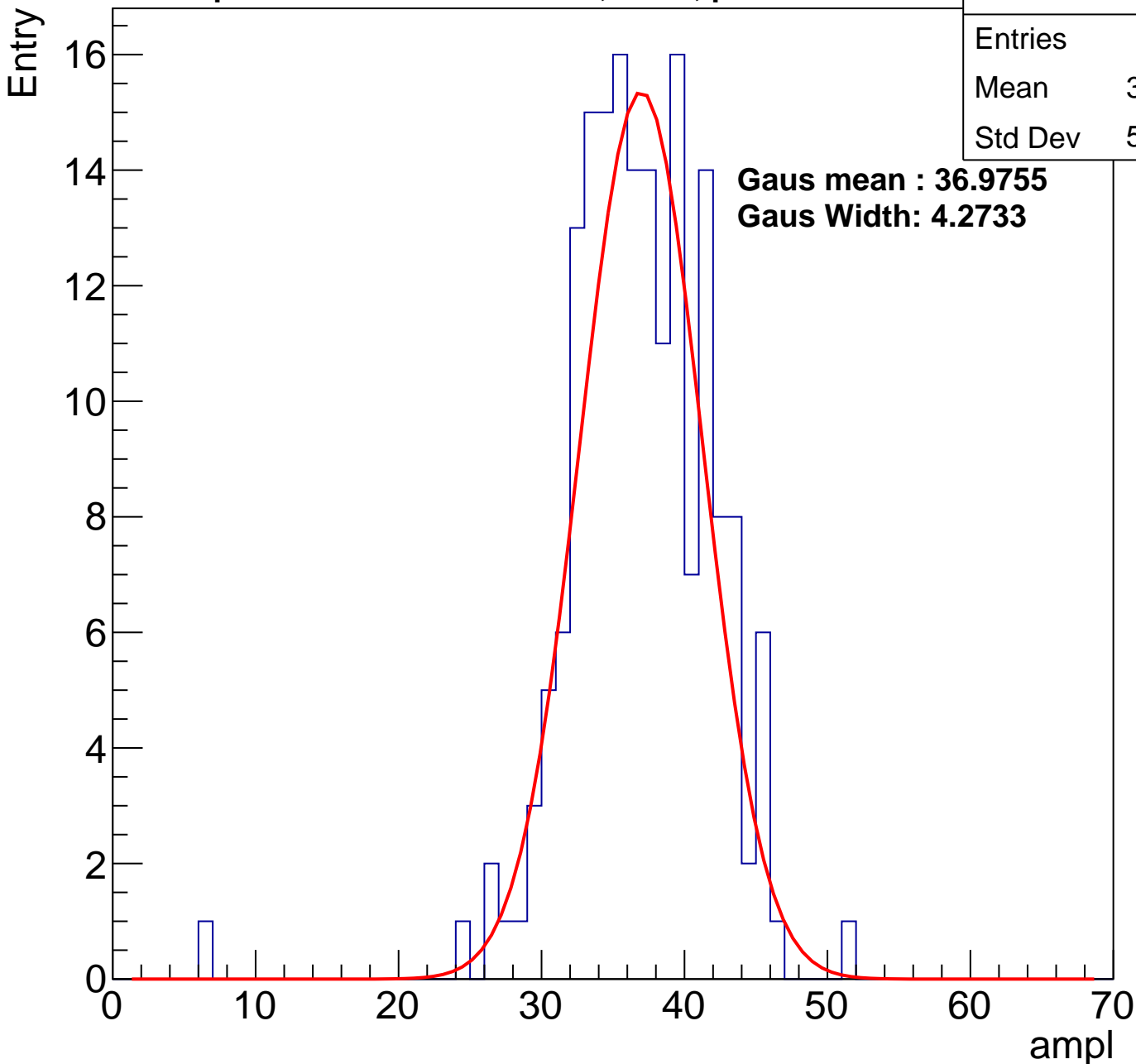


B0L101S, U24-ch126, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	181
Mean	36.43
Std Dev	5.005

Gaus mean : 36.9755
Gaus Width: 4.2733



B0L101S, U24-ch126, adc2

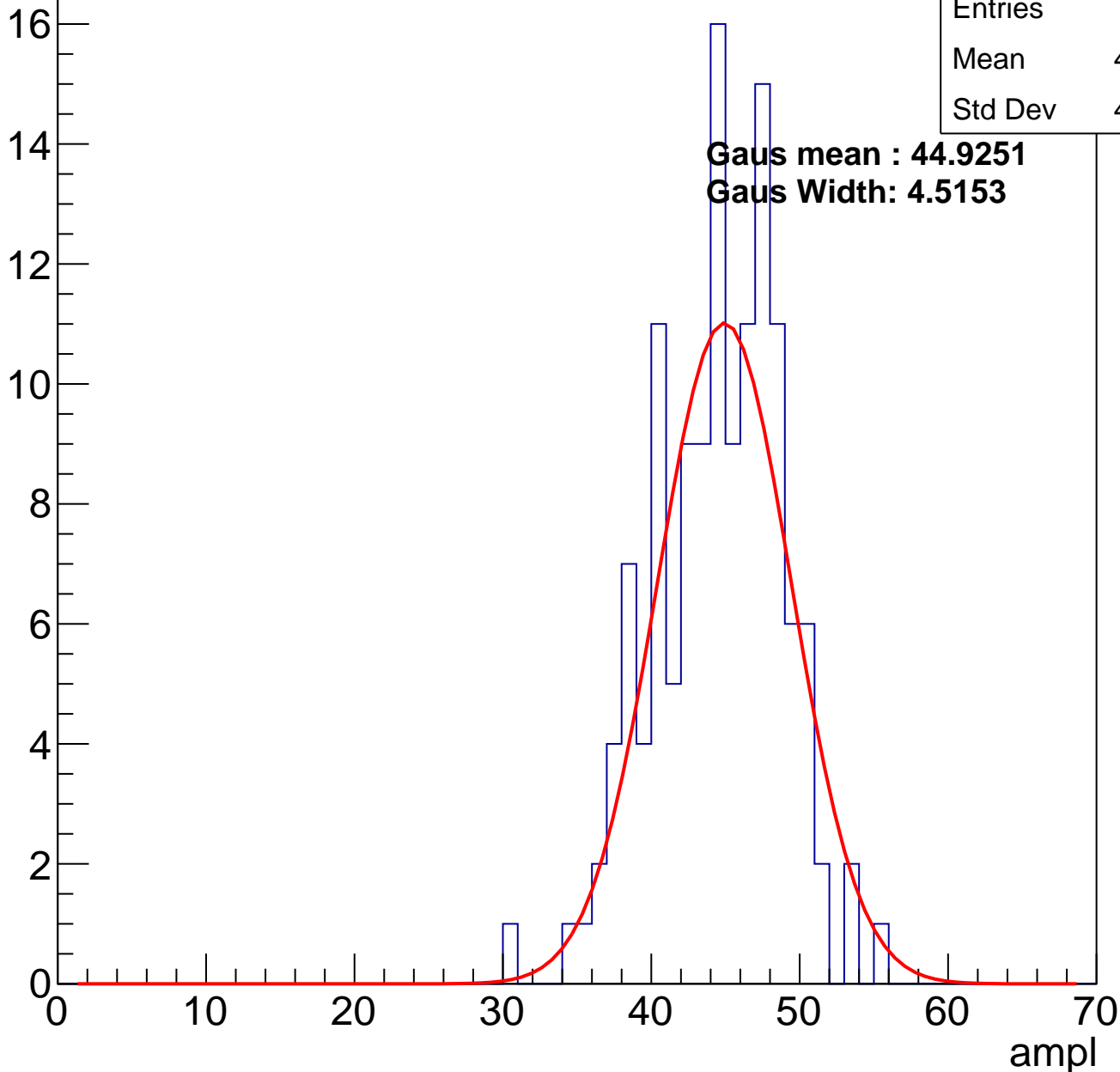
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	133
Mean	44.03
Std Dev	4.274

Gaus mean : 44.9251

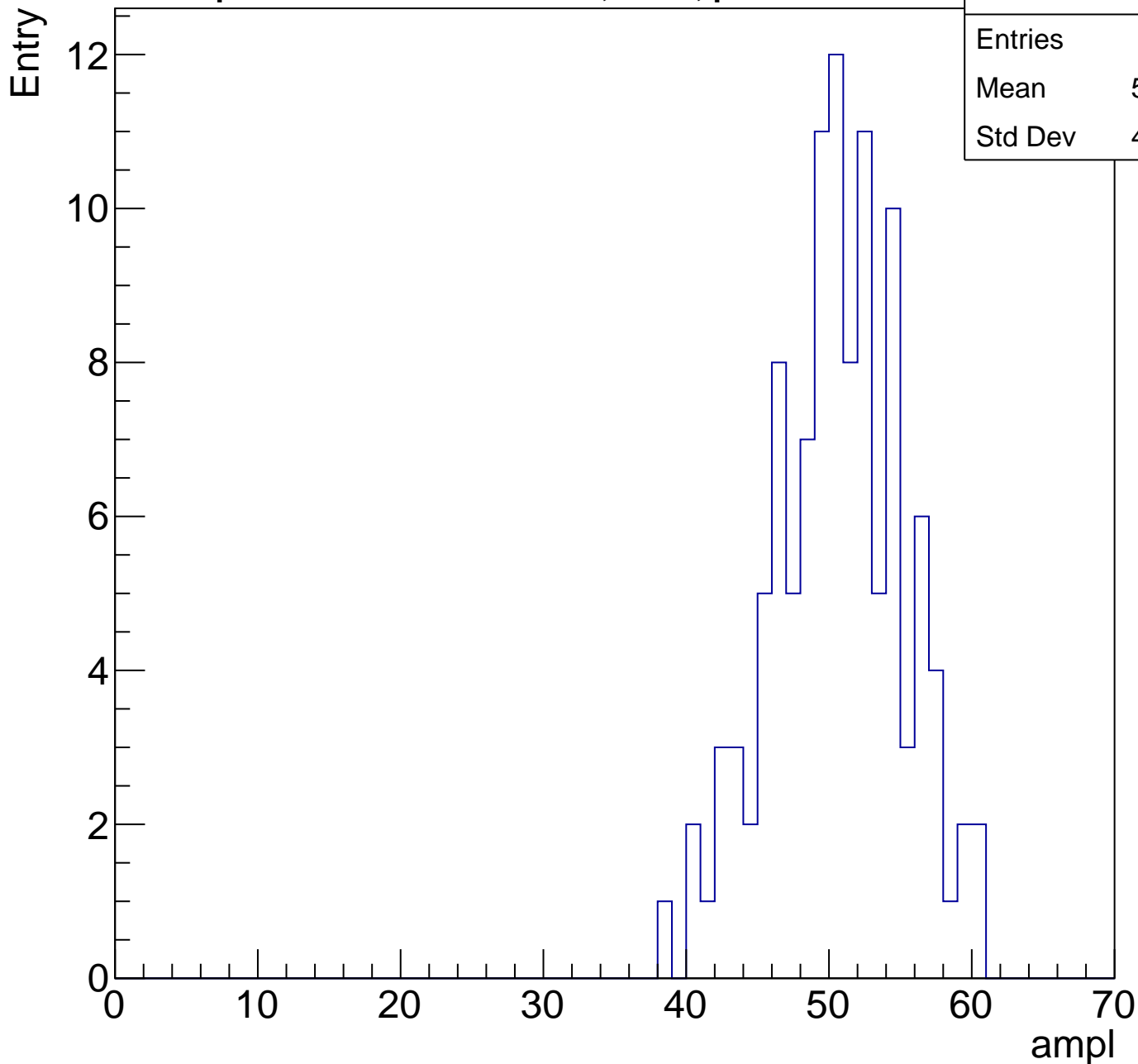
Gaus Width: 4.5153



B0L101S, U24-ch126, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	50.13
Std Dev	4.572



B0L101S, U24-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

124

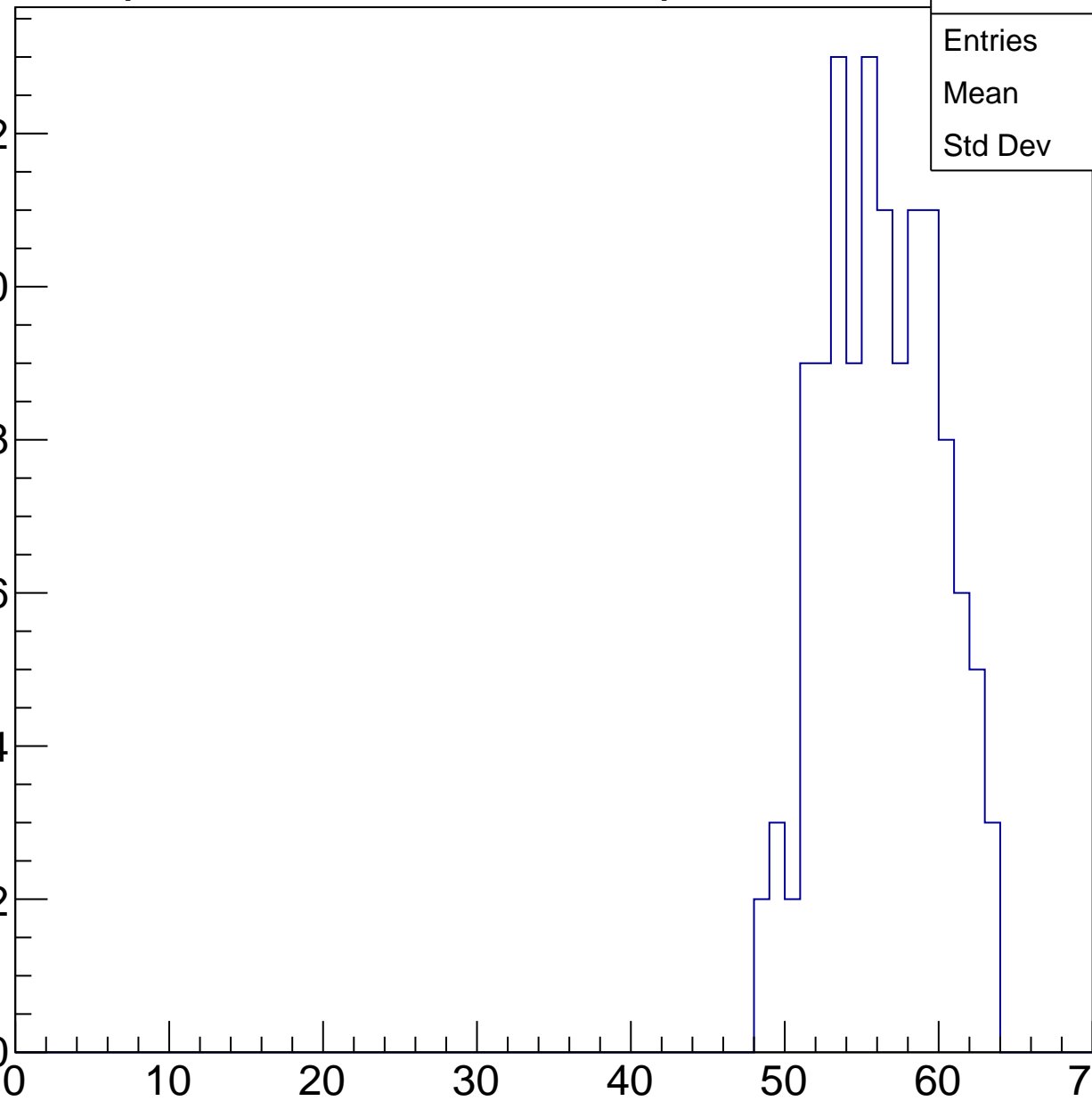
Mean

55.81

Std Dev

3.631

ampl



B0L101S, U24-ch126, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

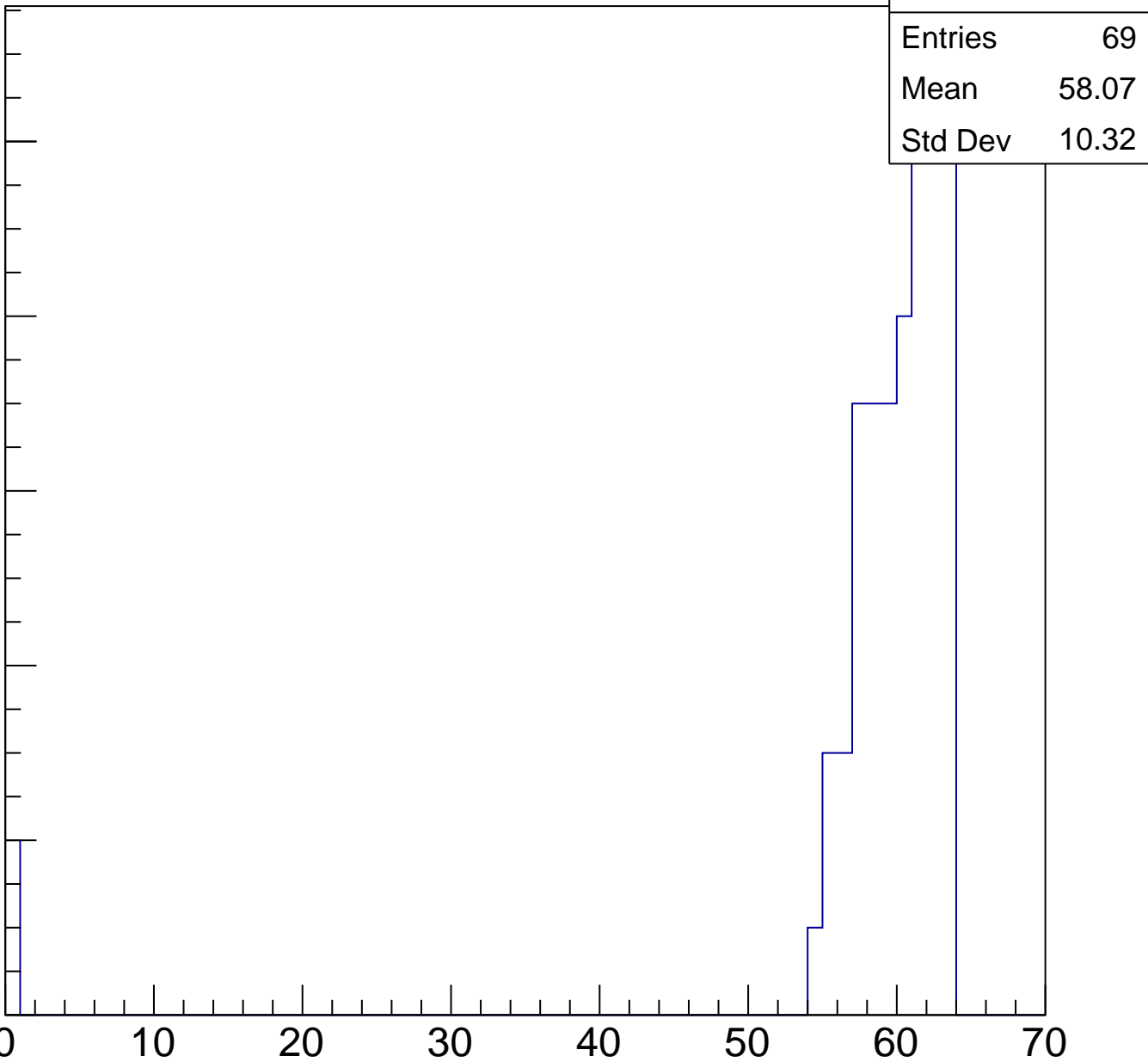
0

Entries 69

Mean 58.07

Std Dev 10.32

ampl

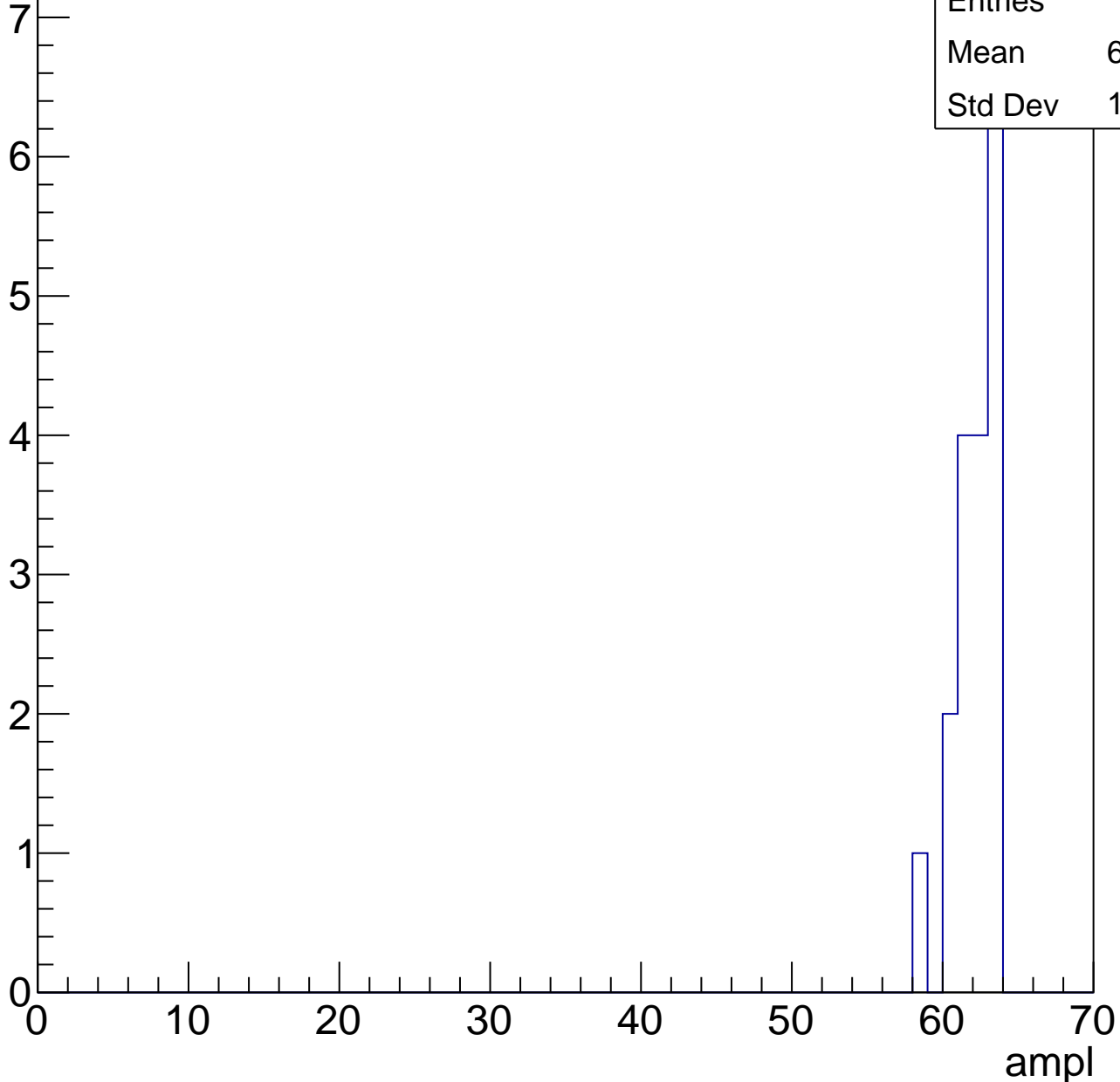


B0L101S, U24-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	61.72
Std Dev	1.366



B0L101S, U24-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U24-ch127, adc0

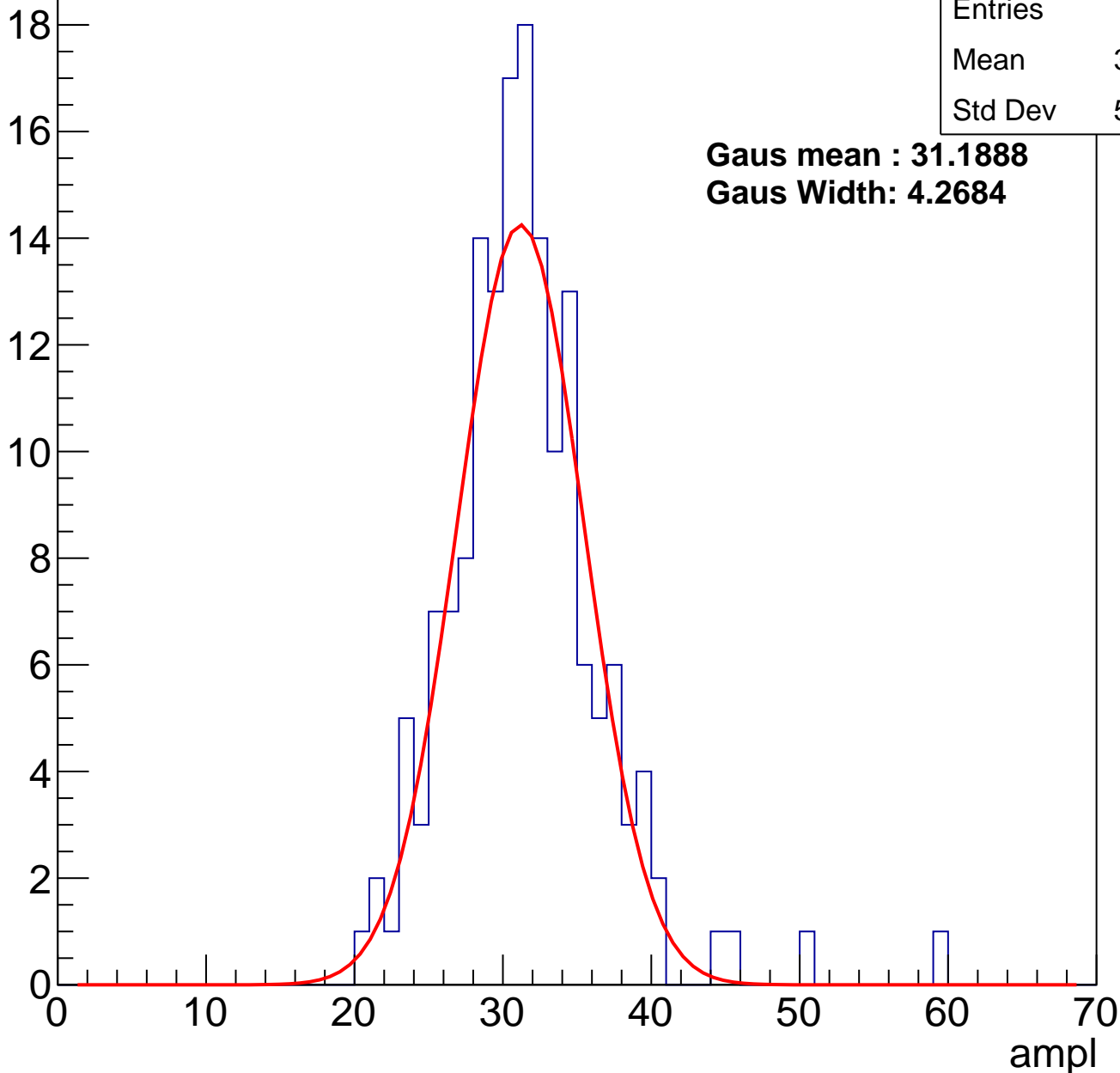
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	31.05
Std Dev	5.137

Gaus mean : 31.1888

Gaus Width: 4.2684

Entry



B0L101S, U24-ch127, adc1

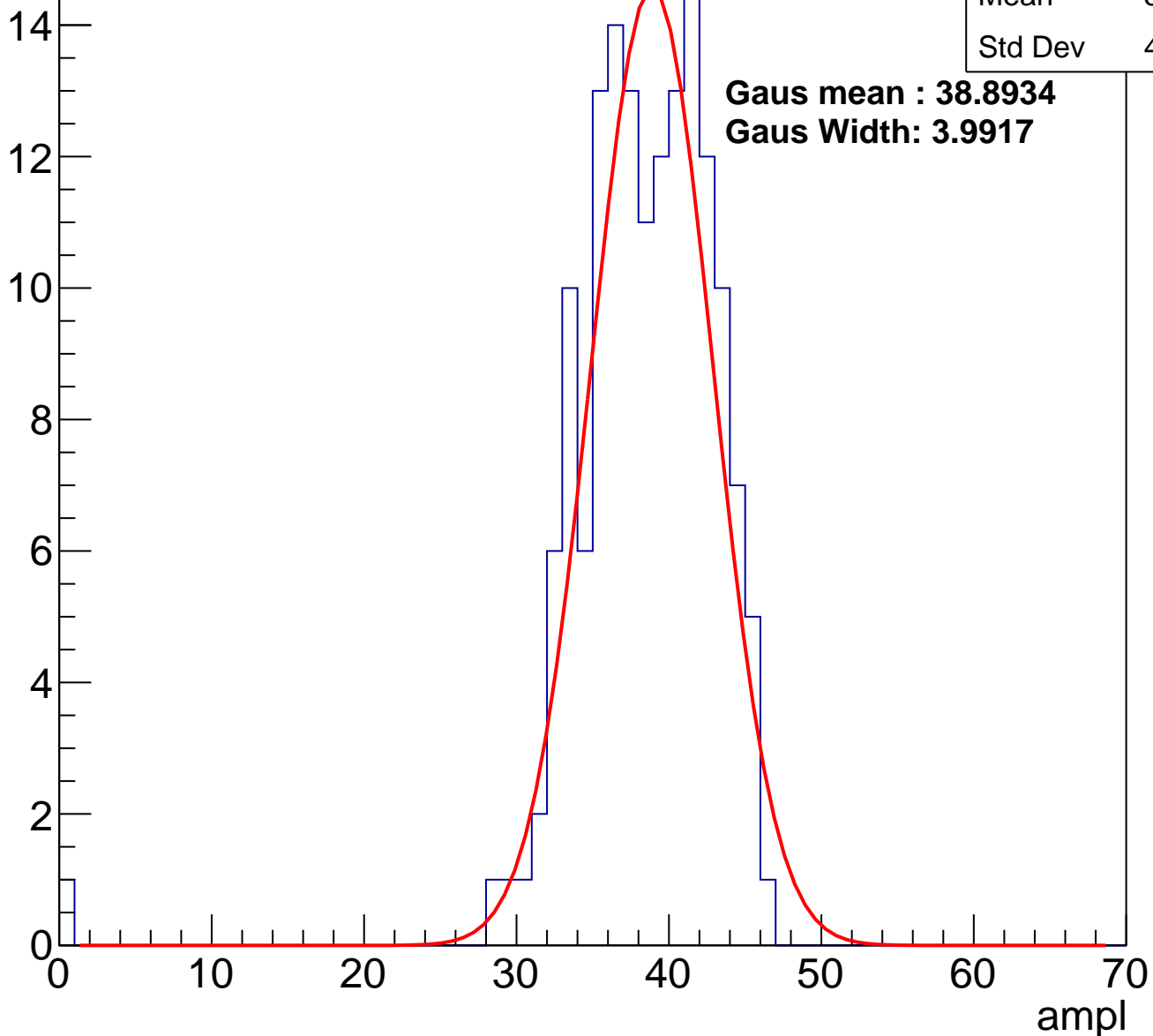
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	37.98
Std Dev	4.926

Gaus mean : 38.8934

Gaus Width: 3.9917

Entry



B0L101S, U24-ch127, adc2

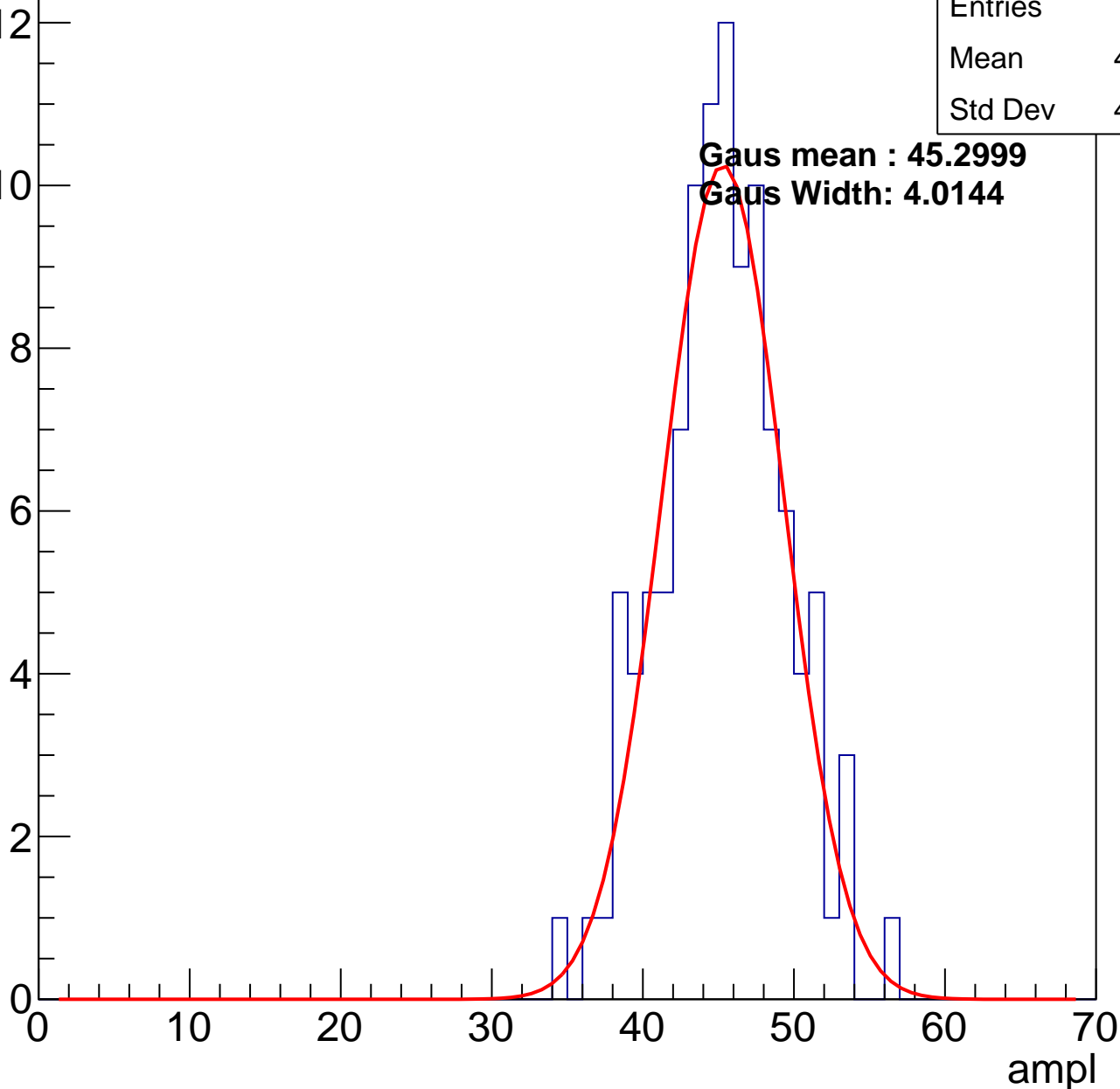
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	108
Mean	44.83
Std Dev	4.106

Gaus mean : 45.2999

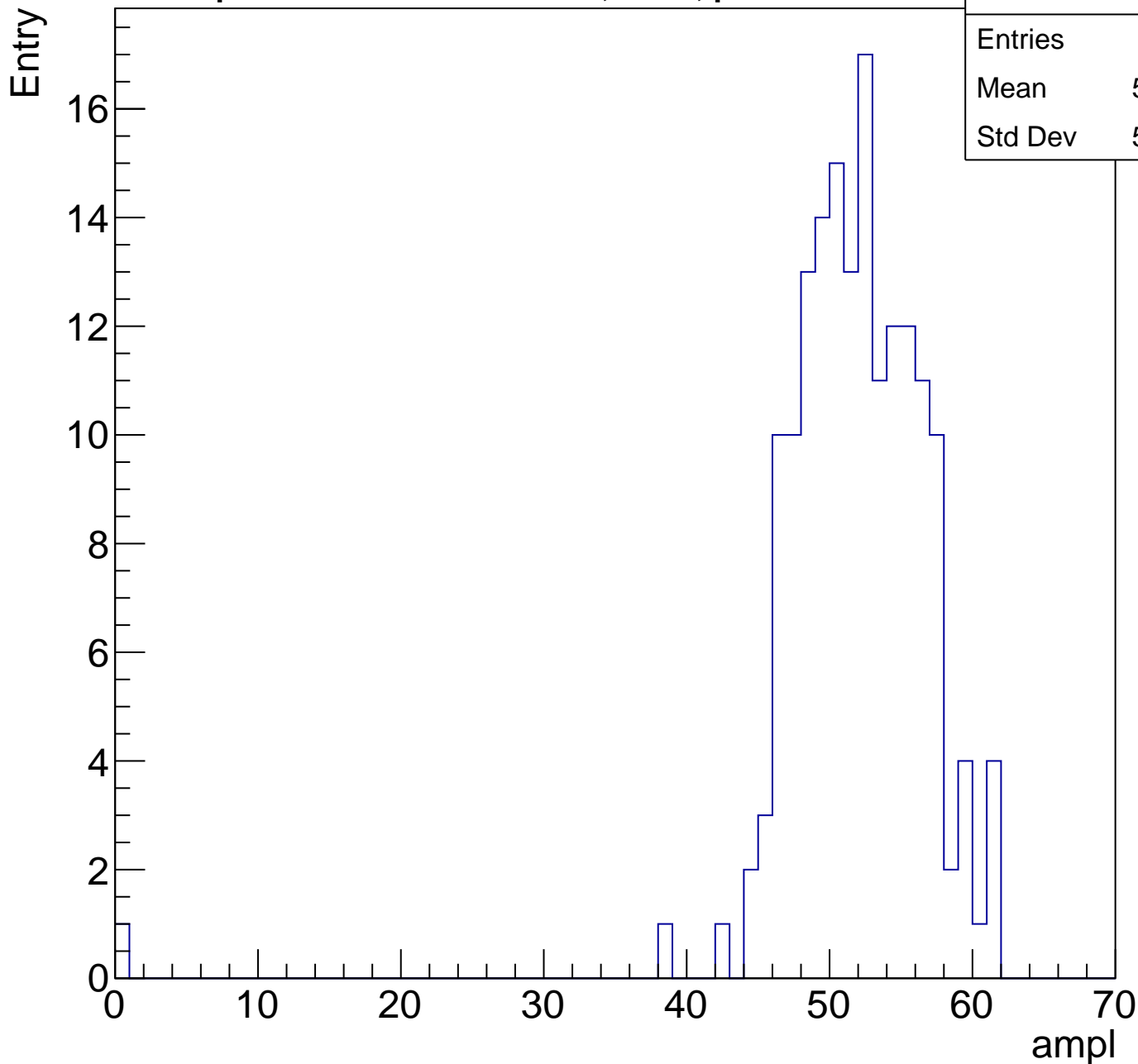
Gaus Width: 4.0144



B0L101S, U24-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	51.34
Std Dev	5.722



B0L101S, U24-ch127, adc4

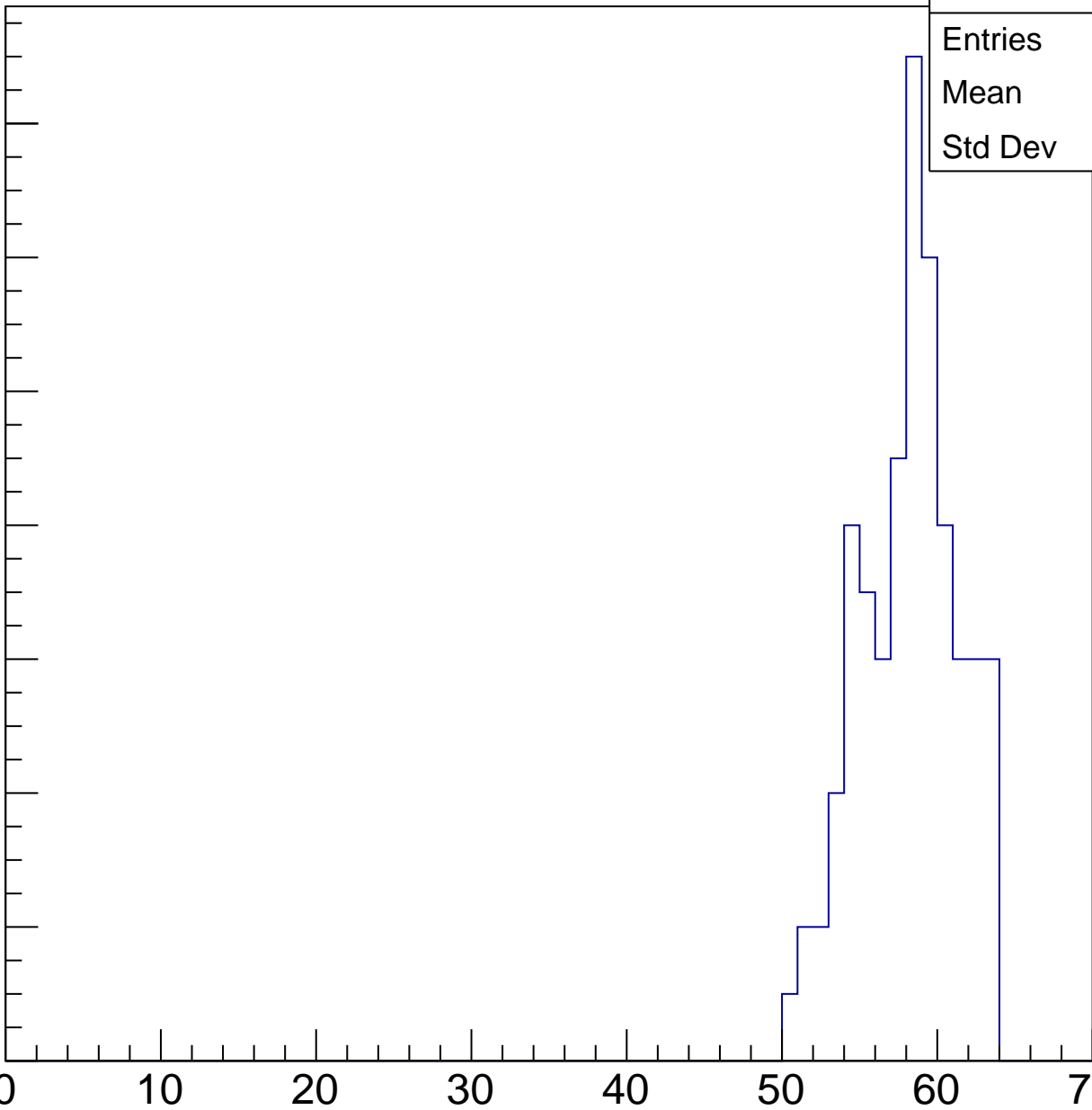
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	57.7
Std Dev	3.116

ampl



B0L101S, U24-ch127, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	59
Mean	58.49
Std Dev	11.21

Entry

12

10

8

6

4

2

0

0

10

20

30

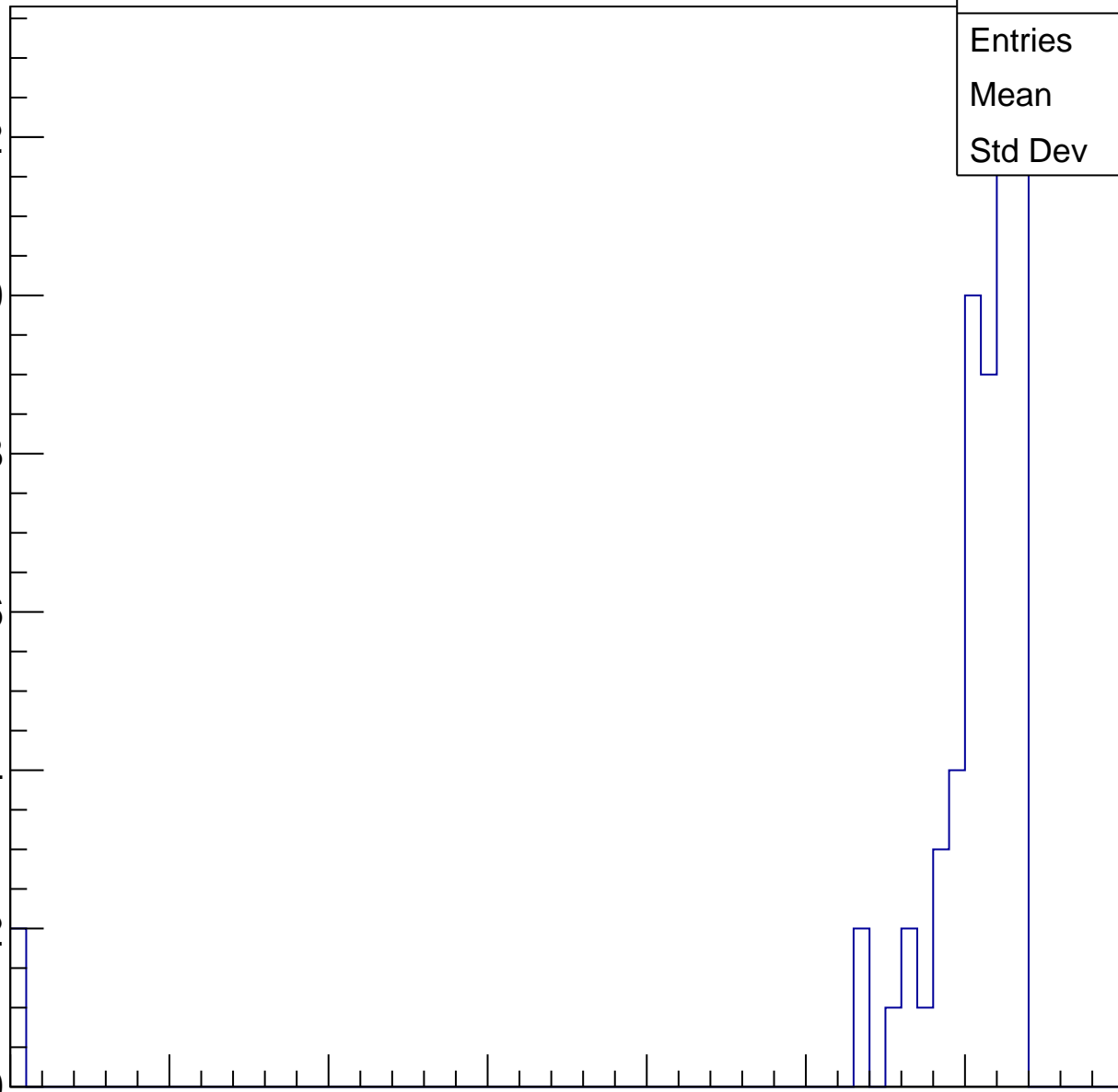
40

50

60

70

ampl



B0L101S, U24-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

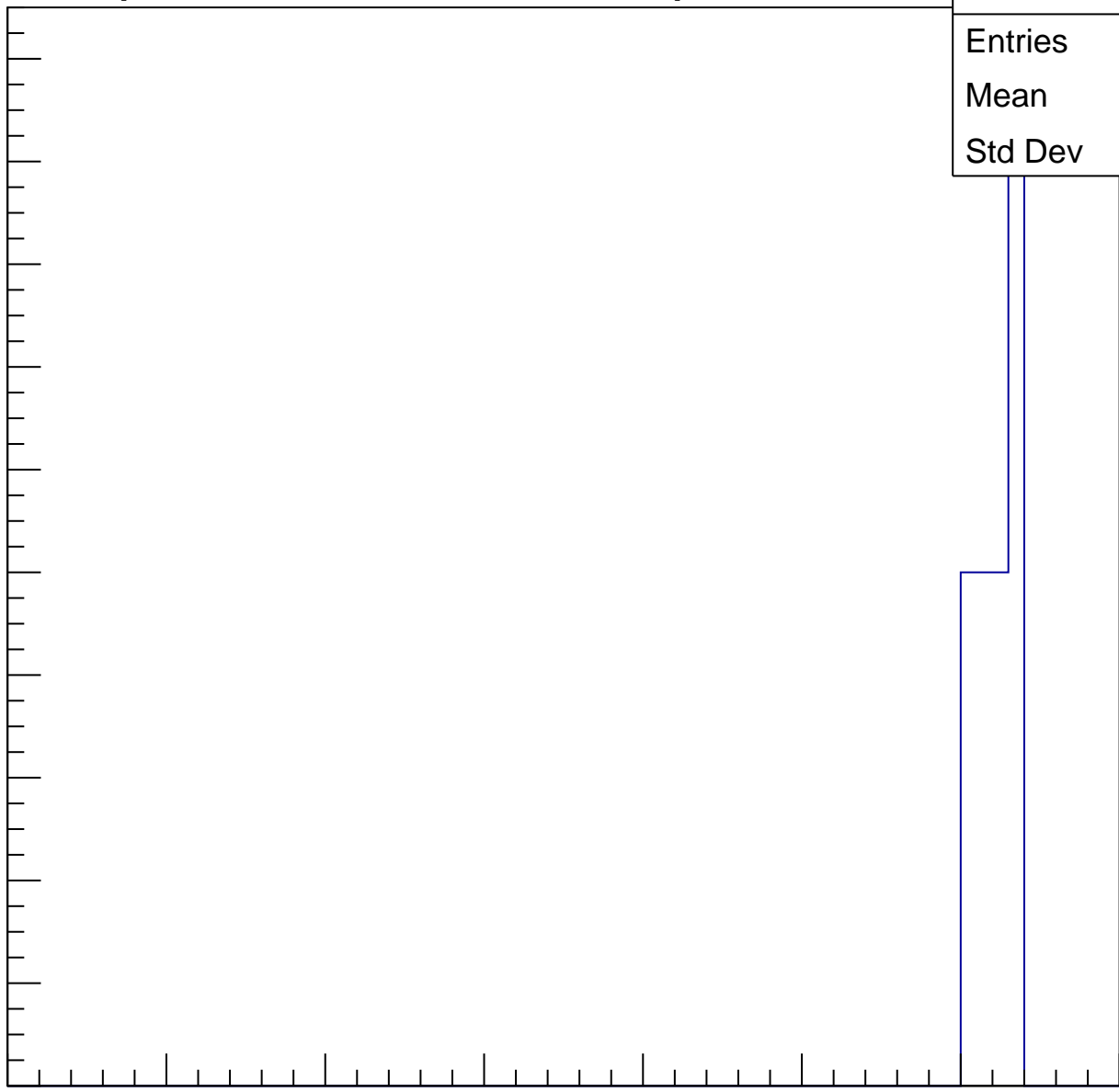
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

0 10 20 30 40 50 60 70

ampl



B0L101S, U24-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

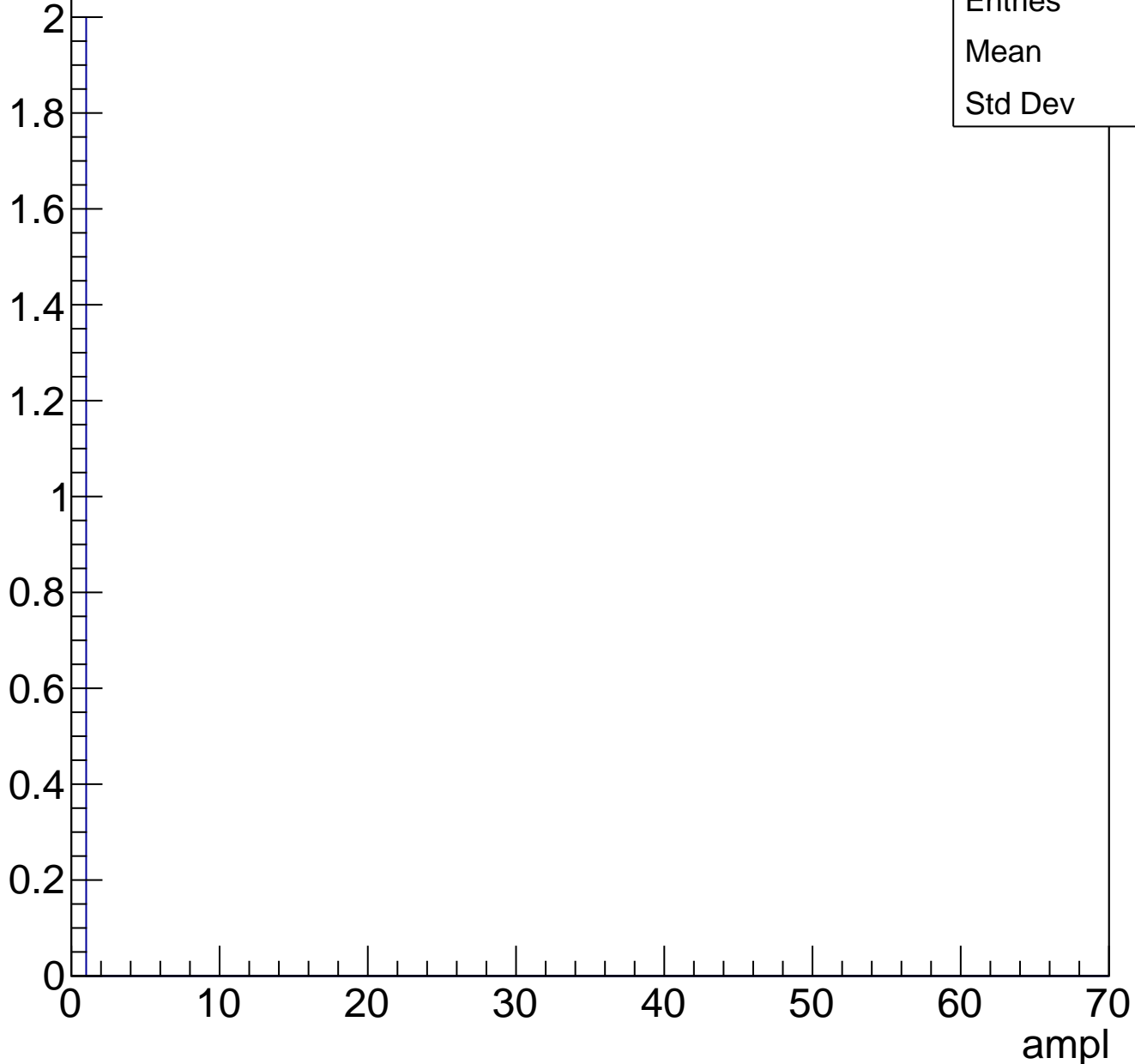


Entries	2
Mean	0
Std Dev	0

B0L101S, U24-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0