

B0L102S, U24-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 27.0732

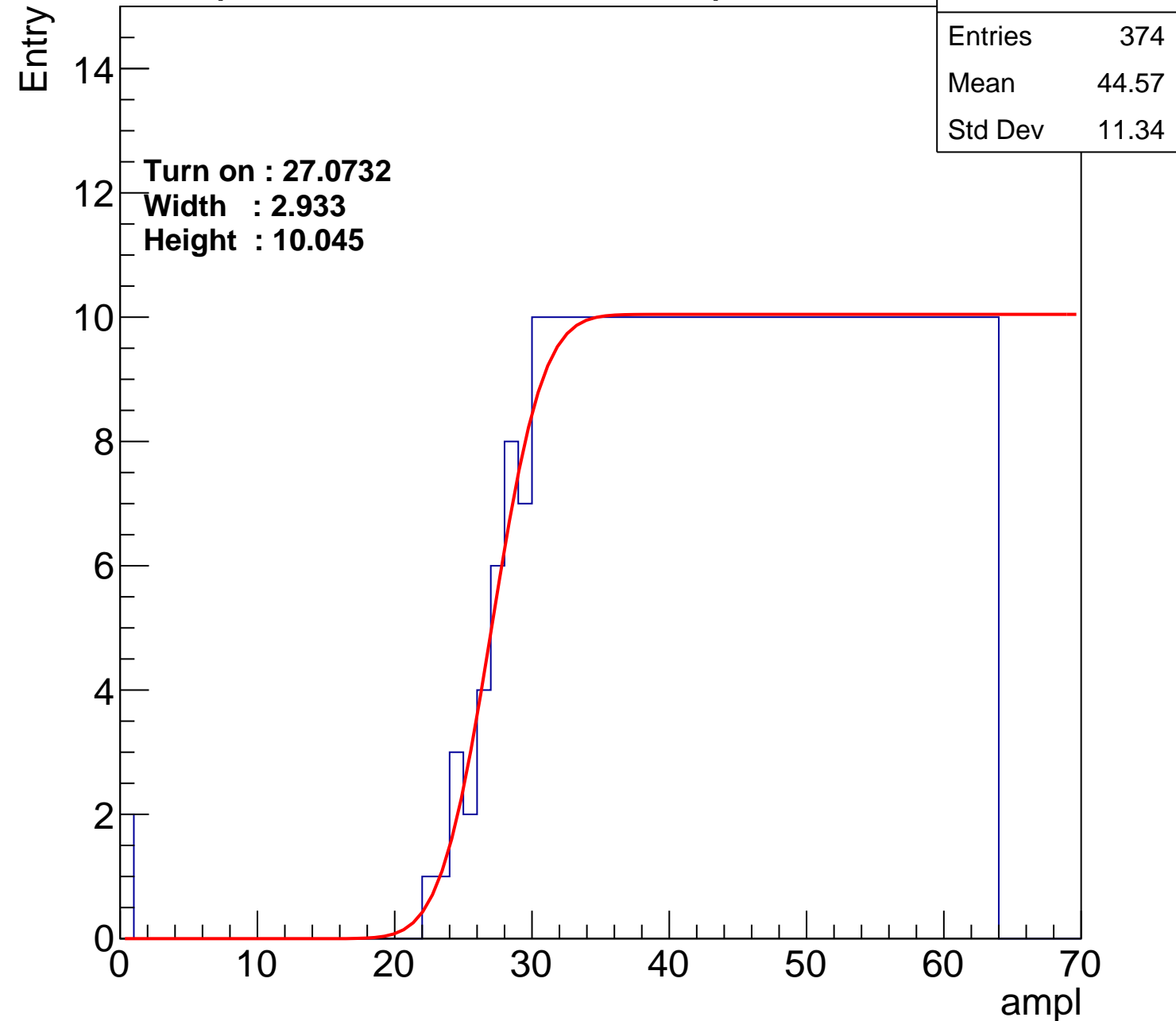
Width : 2.933

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.85
Std Dev	11.67

Turn on : 25.7512

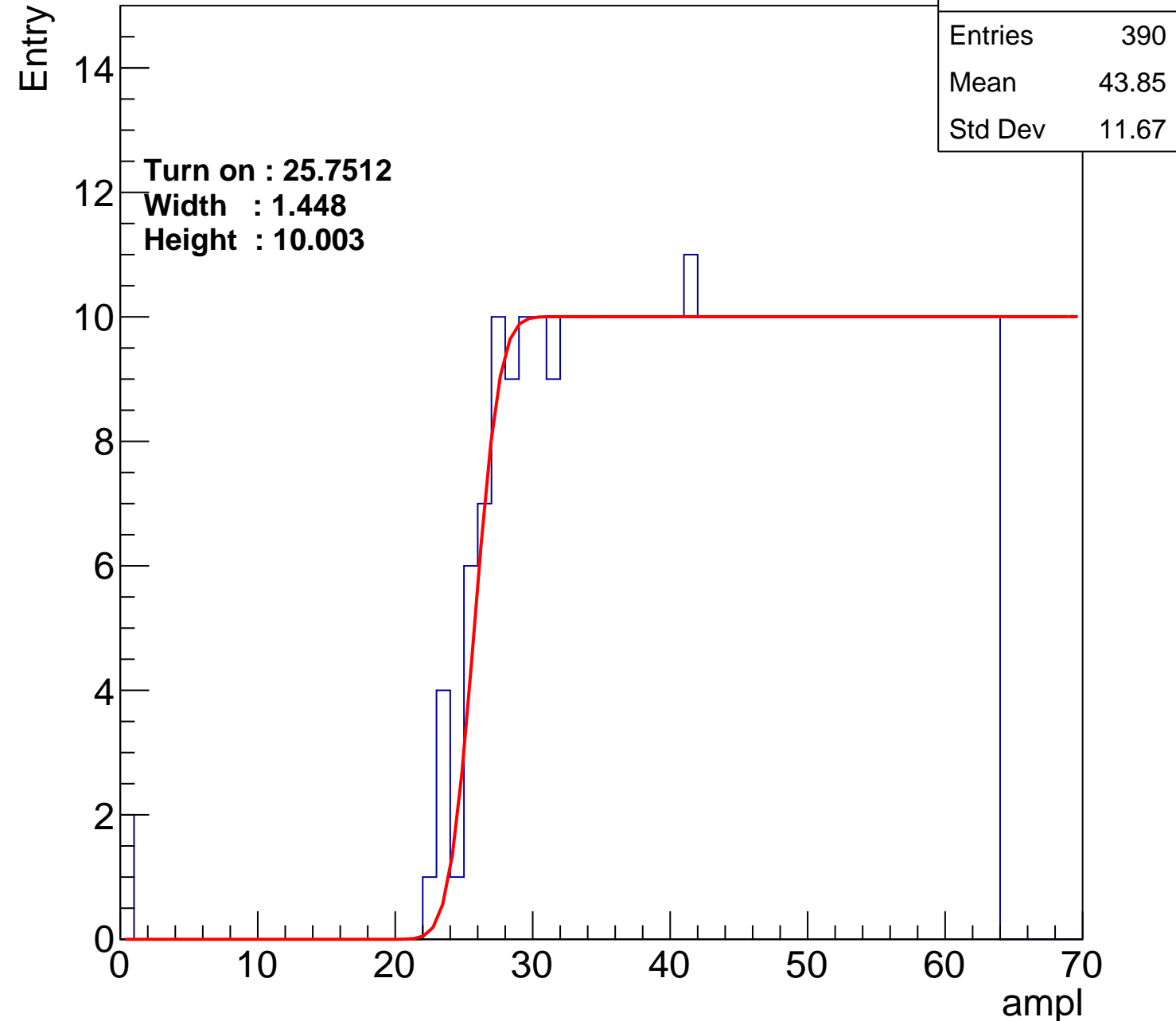
Width : 1.448

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 26.0512

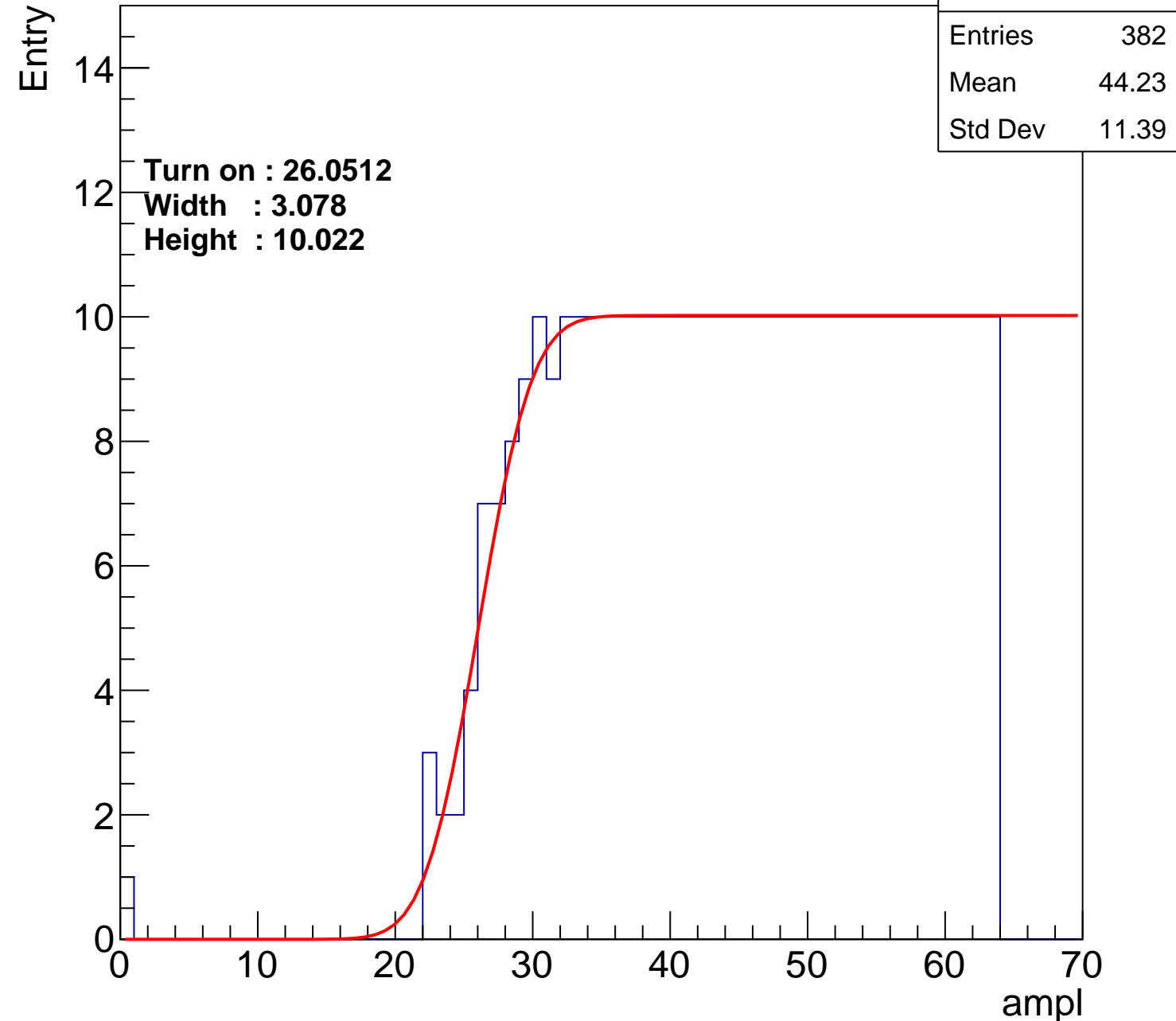
Width : 3.078

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch3

calib_packv5_042523_0143.root, FC#12, port B1

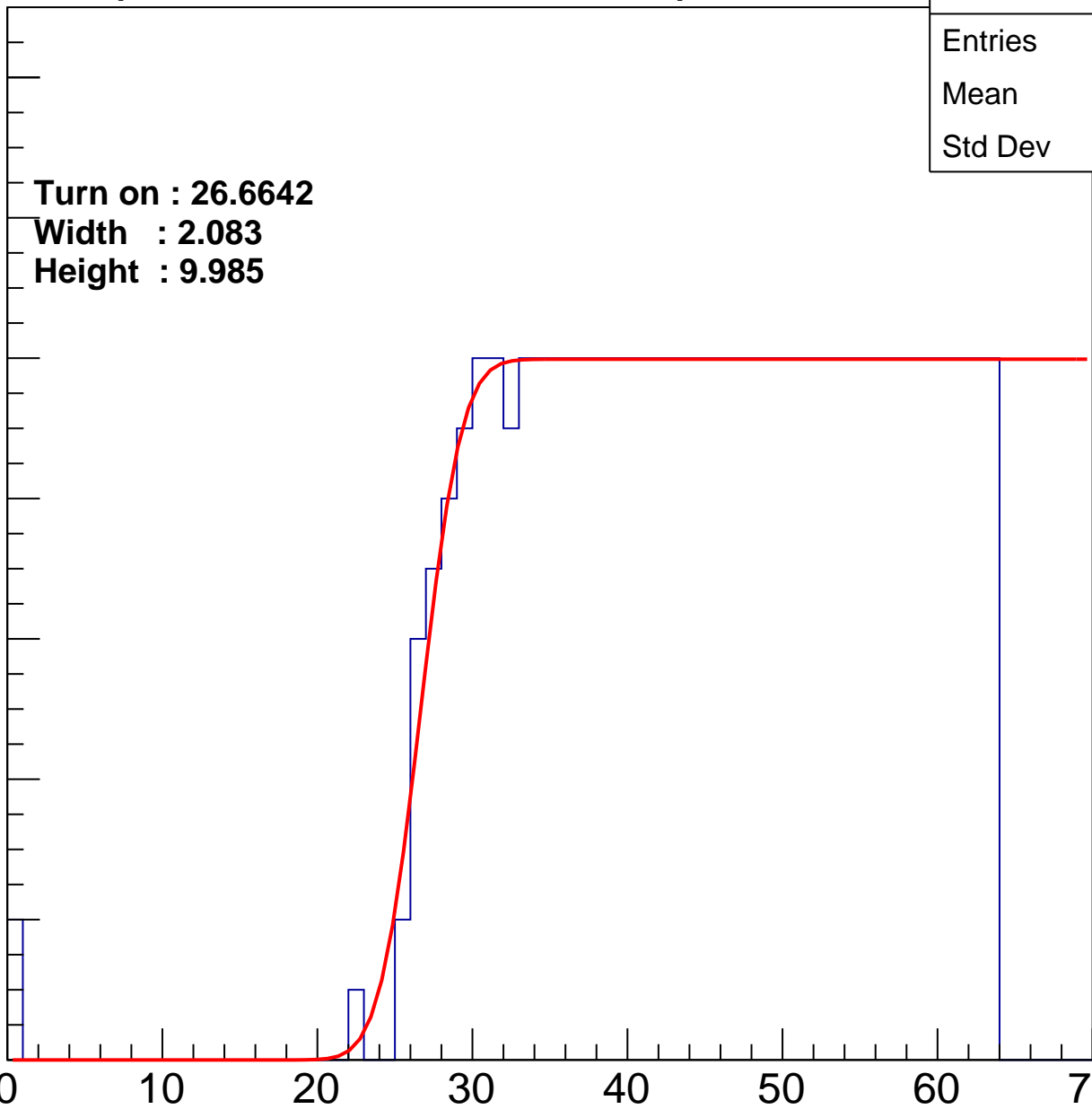
Entry

14
12
10
8
6
4
2
0

Turn on : 26.6642
Width : 2.083
Height : 9.985

Entries	374
Mean	44.6
Std Dev	11.29

ampl



B0L102S, U24-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.97
Std Dev	10.99

Turn on : 28.1344

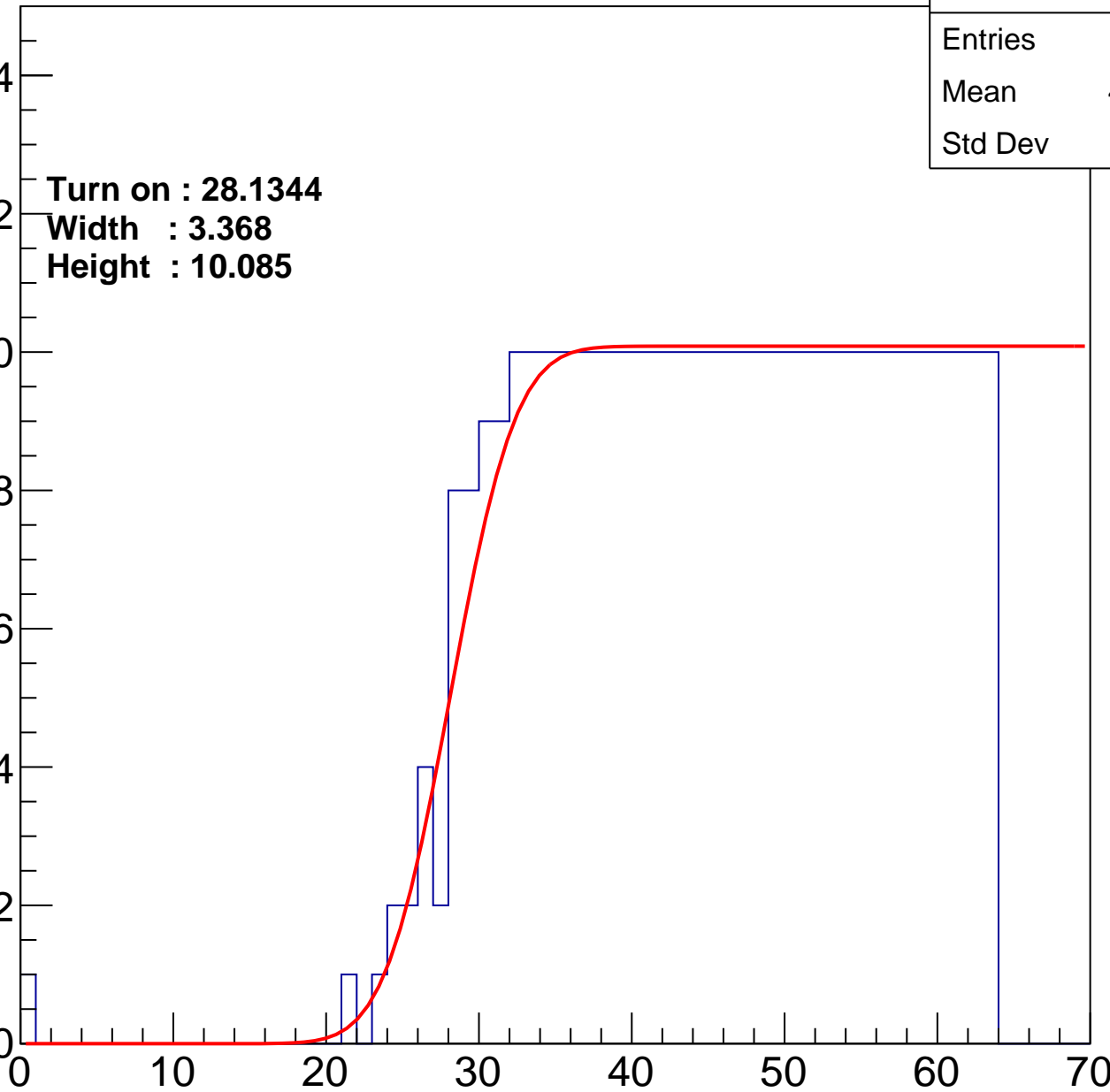
Width : 3.368

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch5

calib_packv5_042523_0143.root, FC#12, port B1

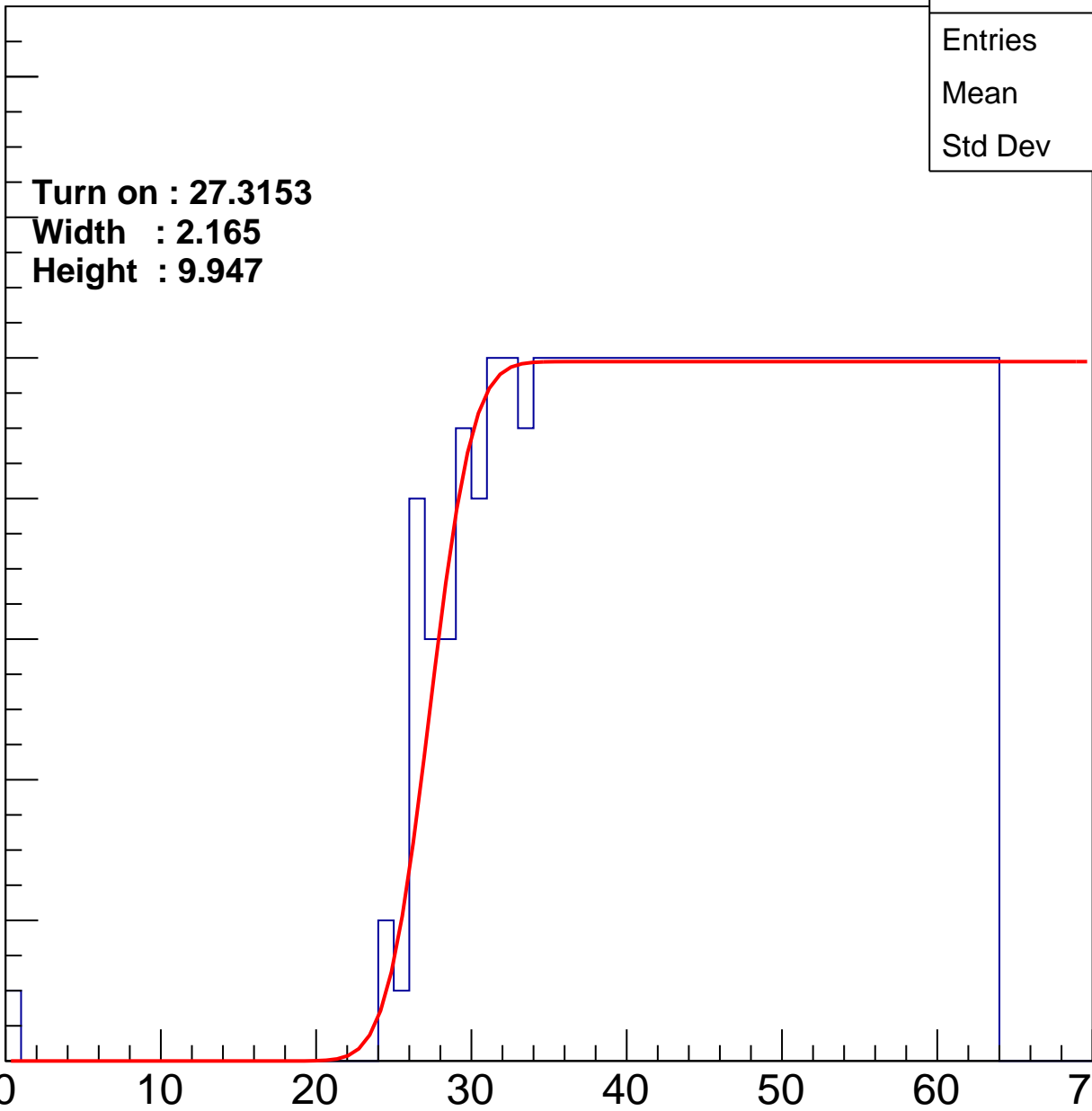
Entry

14
12
10
8
6
4
2
0

Turn on : 27.3153
Width : 2.165
Height : 9.947

Entries	370
Mean	44.84
Std Dev	11.04

ampl



B0L102S, U24-ch6

calib_packv5_042523_0143.root, FC#12, port B1

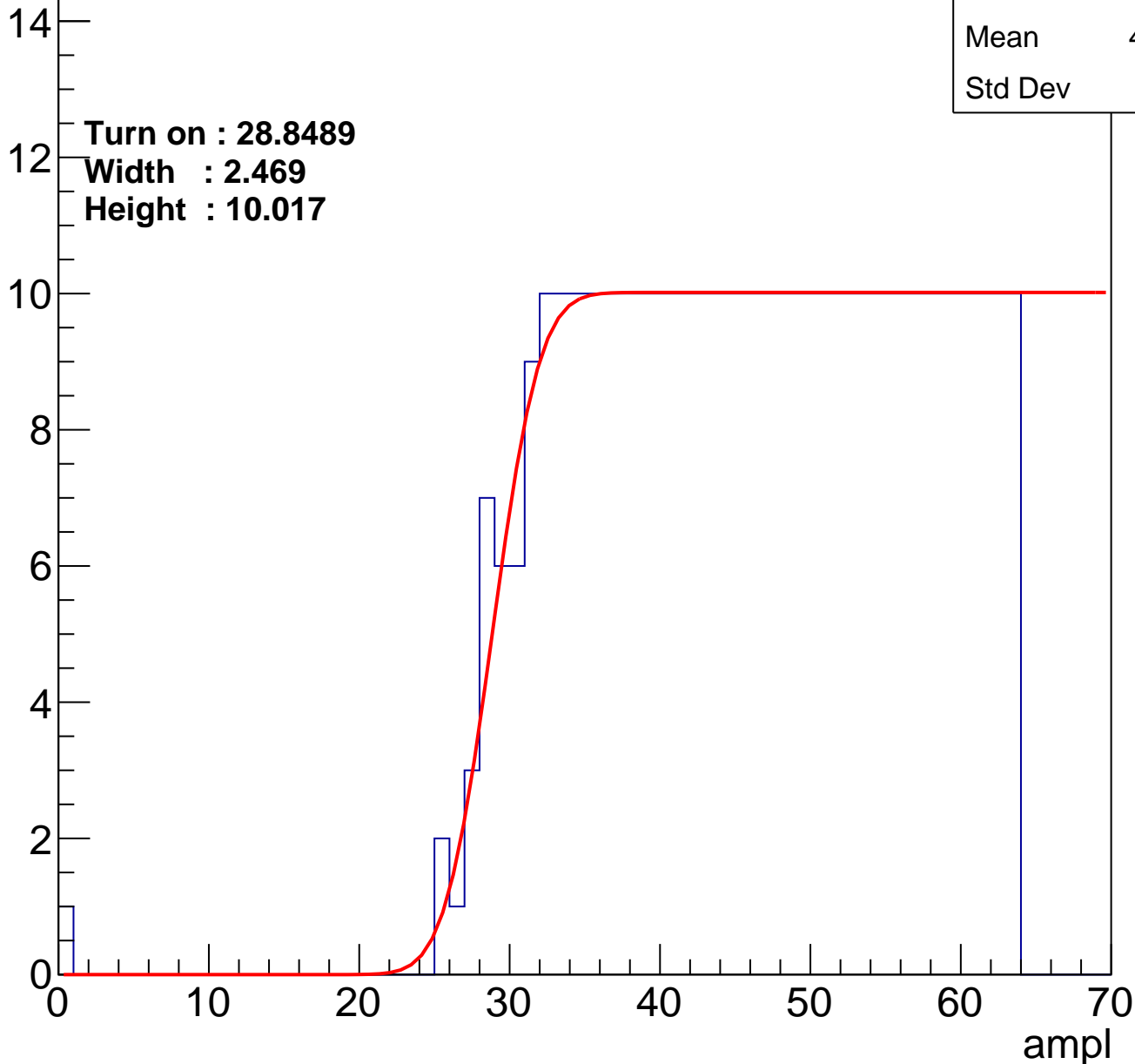
Entry

Entries	355
Mean	45.59
Std Dev	10.61

Turn on : 28.8489

Width : 2.469

Height : 10.017



B0L102S, U24-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.01
Std Dev	11.84

Turn on : 26.0979

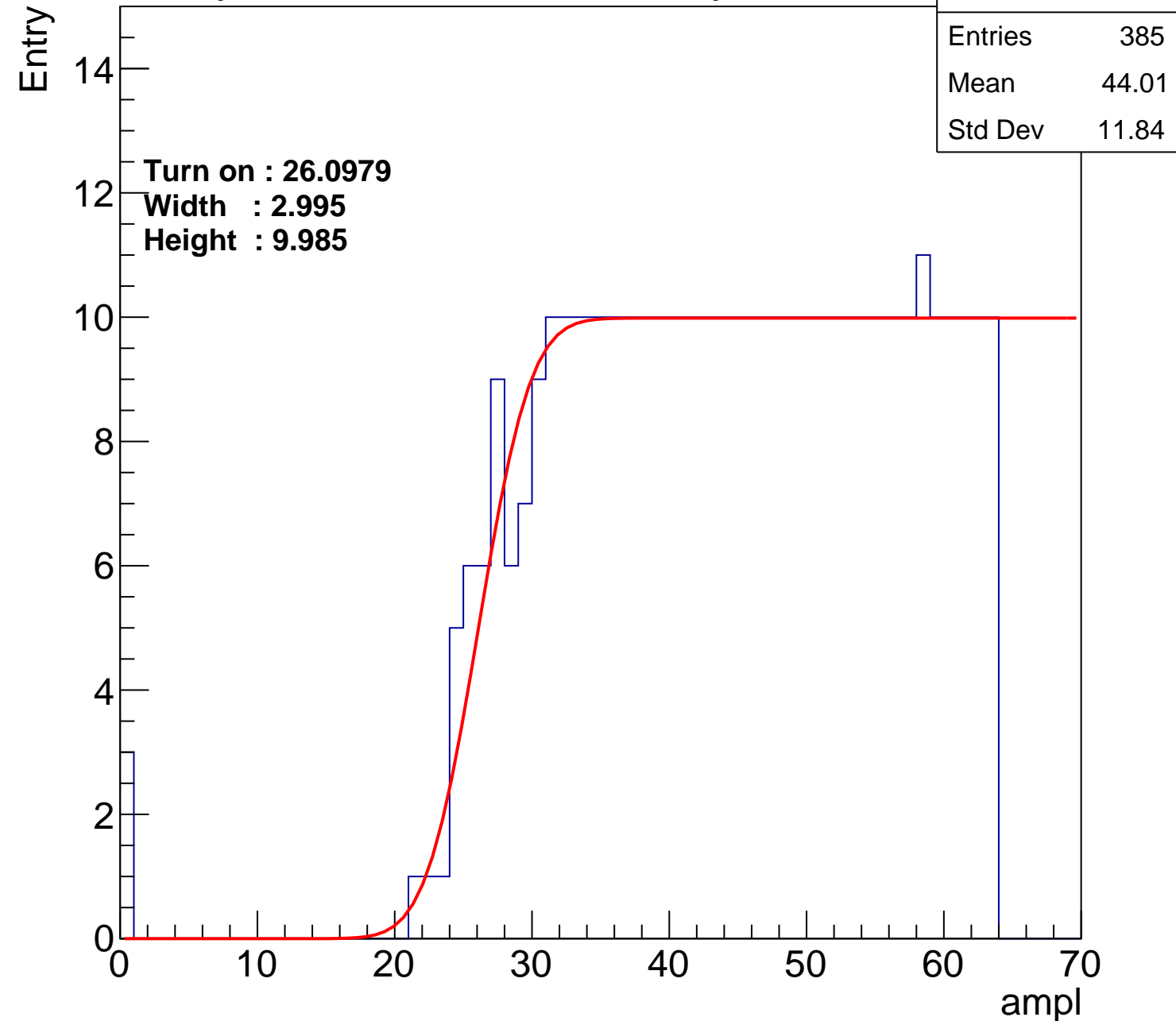
Width : 2.995

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch8

calib_packv5_042523_0143.root, FC#12, port B1

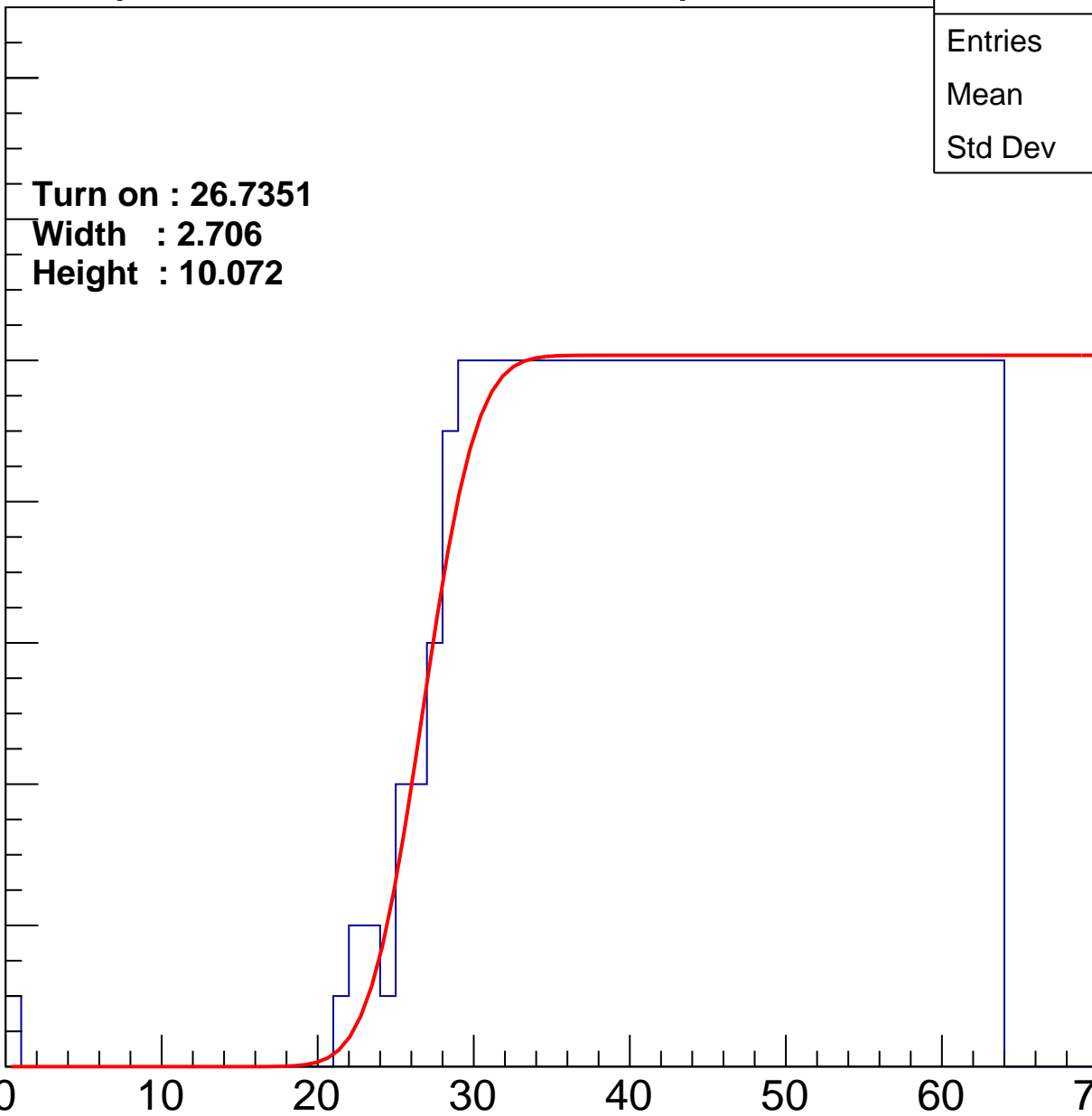
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7351
Width : 2.706
Height : 10.072

Entries	380
Mean	44.35
Std Dev	11.3

ampl



B0L102S, U24-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.97
Std Dev	11.62

Turn on : 25.6132

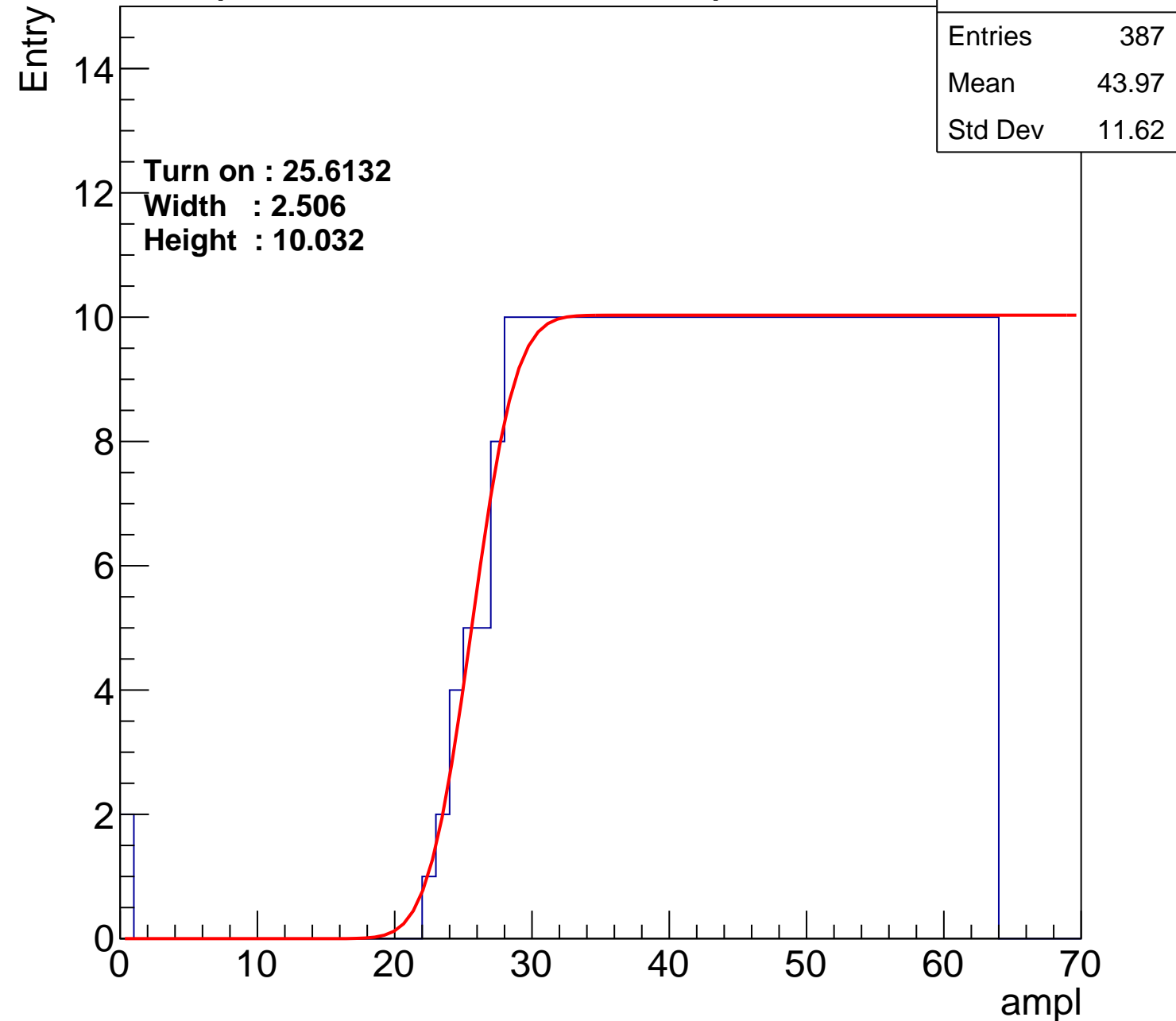
Width : 2.506

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.96
Std Dev	11.69

Turn on : 25.5547

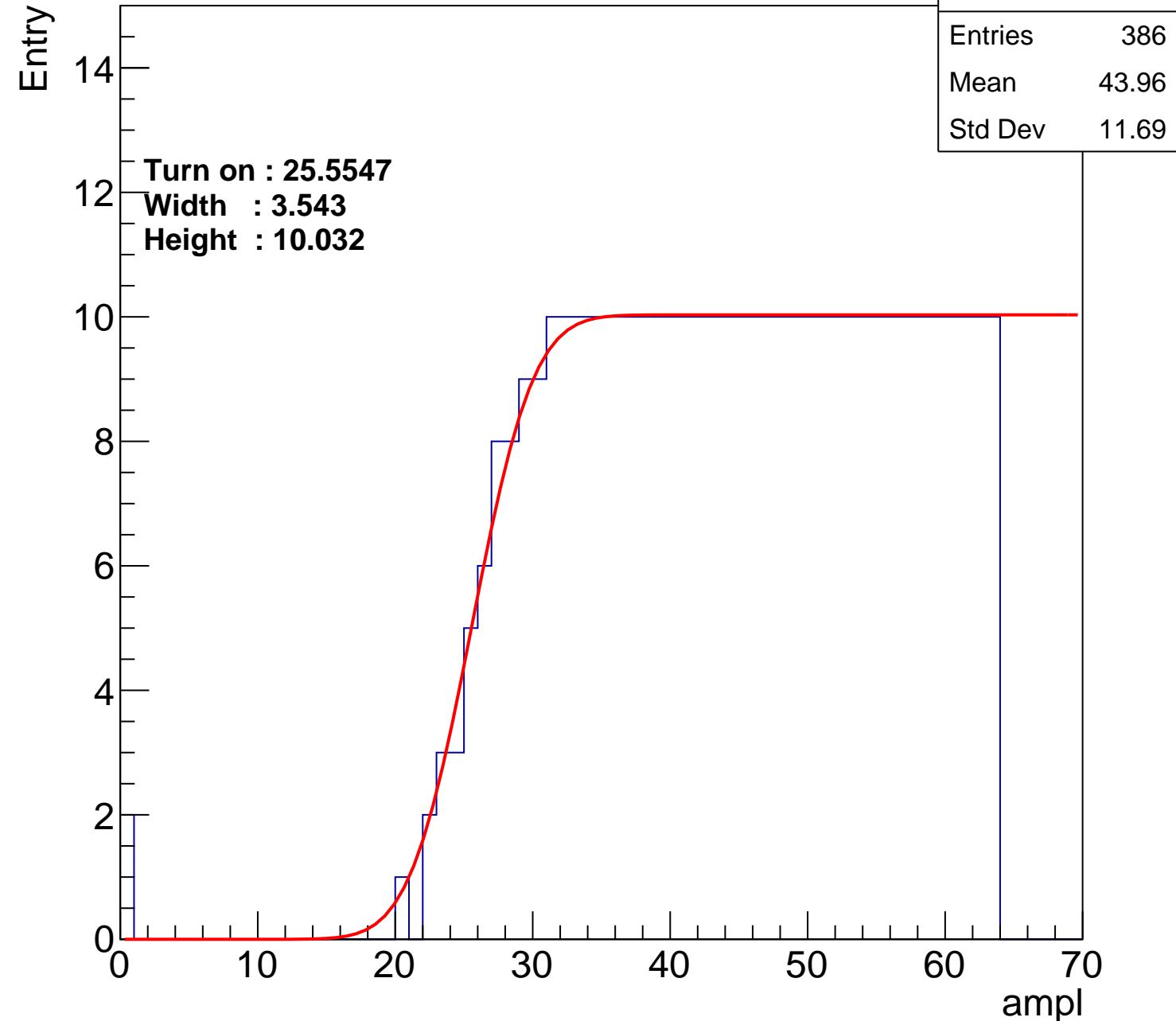
Width : 3.543

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch11

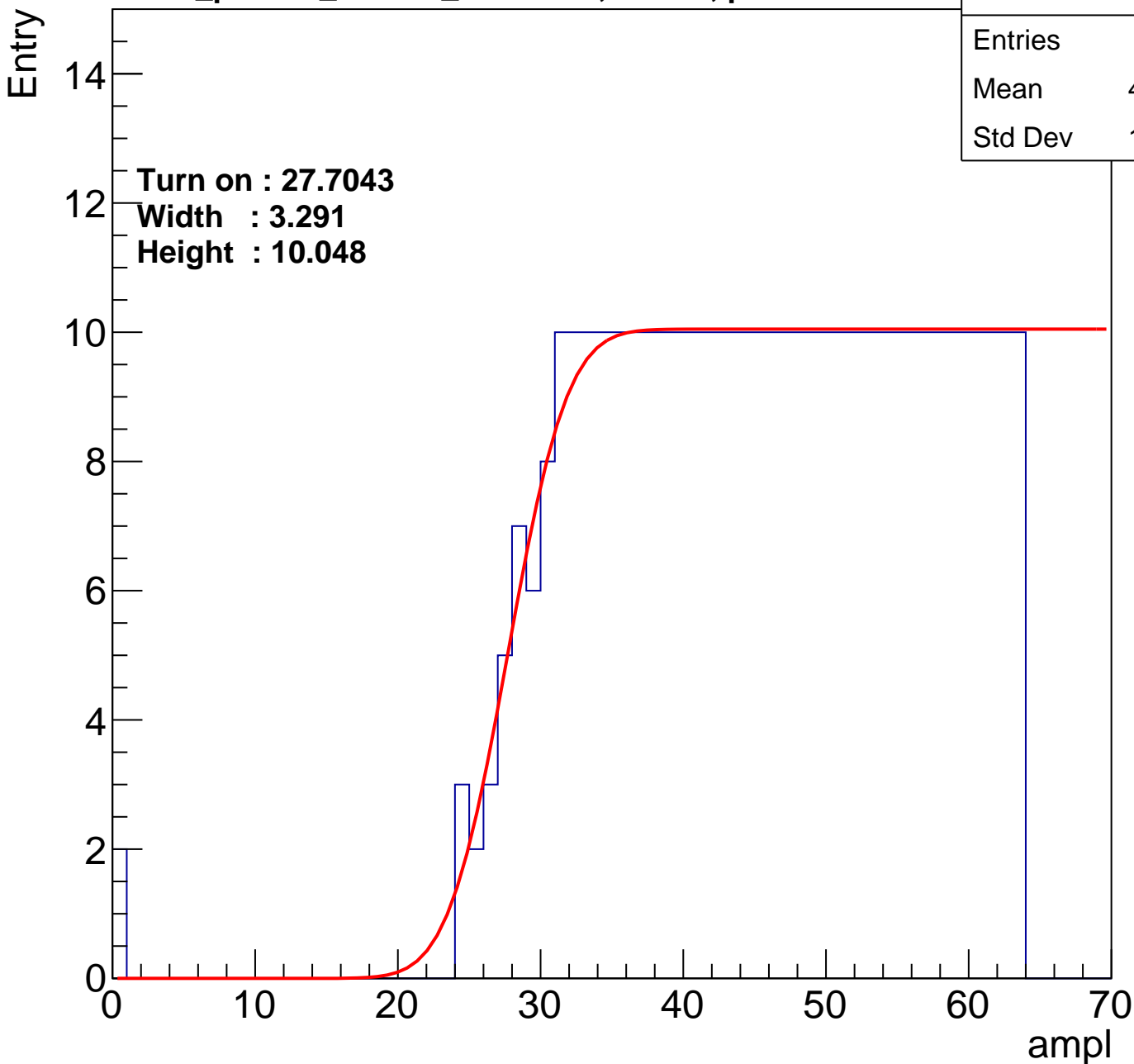
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 27.7043

Width : 3.291

Height : 10.048

Entries	366
Mean	44.96
Std Dev	11.15



B0L102S, U24-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.67
Std Dev	11.28

Turn on : 26.8935

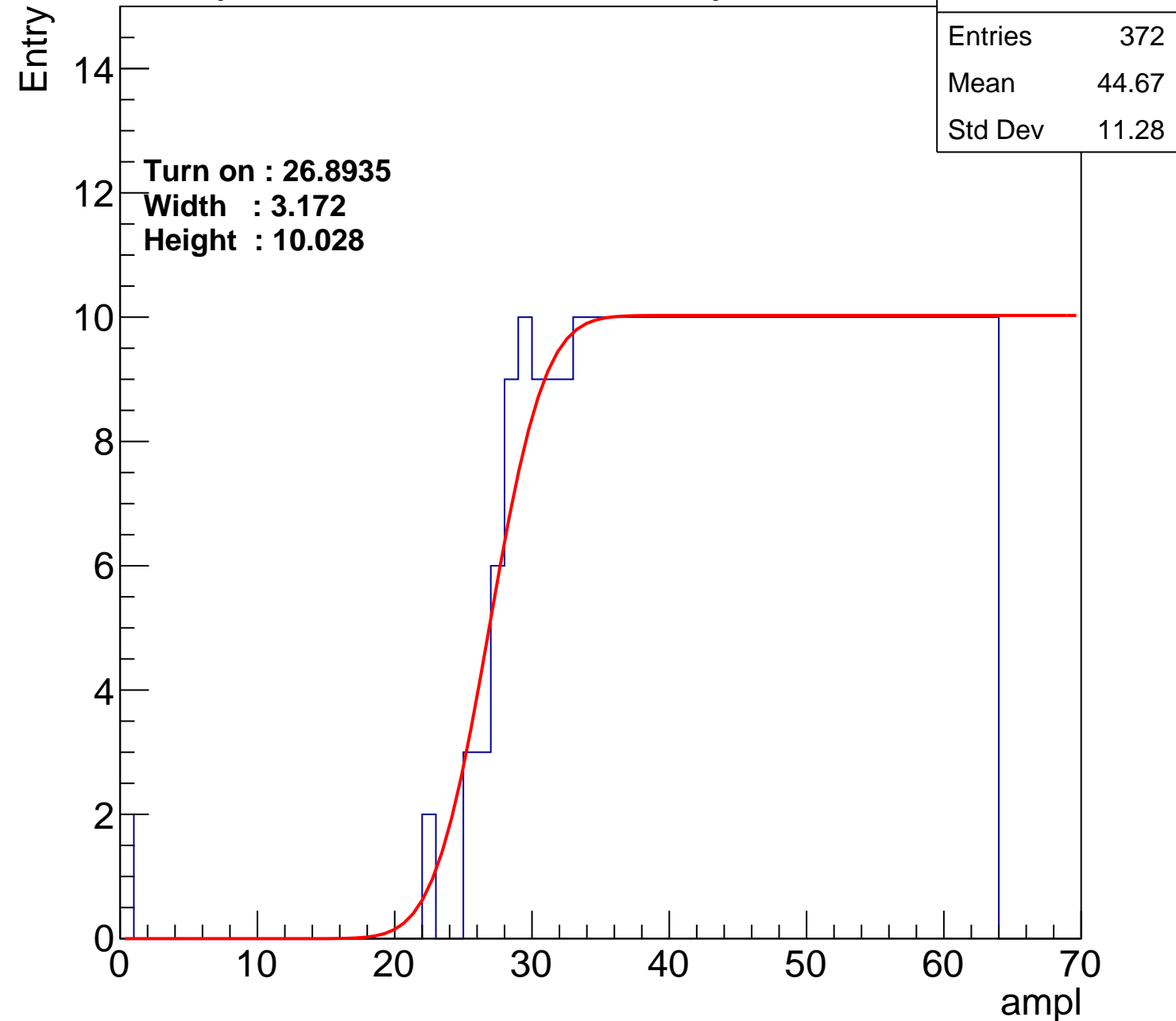
Width : 3.172

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch13

calib_packv5_042523_0143.root, FC#12, port B1

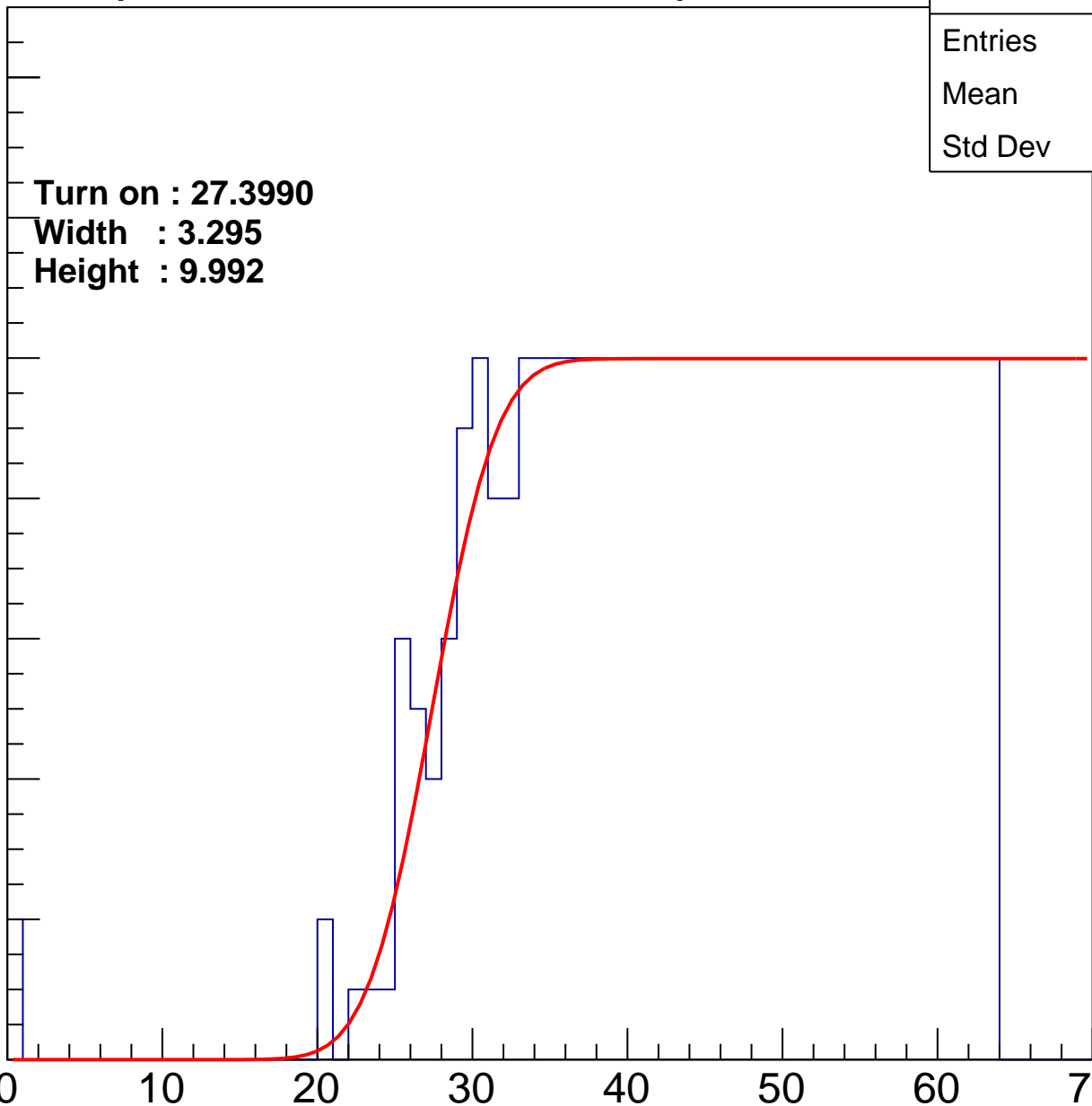
Entry

14
12
10
8
6
4
2
0

Turn on : 27.3990
Width : 3.295
Height : 9.992

Entries	373
Mean	44.53
Std Dev	11.46

ampl



B0L102S, U24-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.66
Std Dev	11.79

Turn on : 24.8101

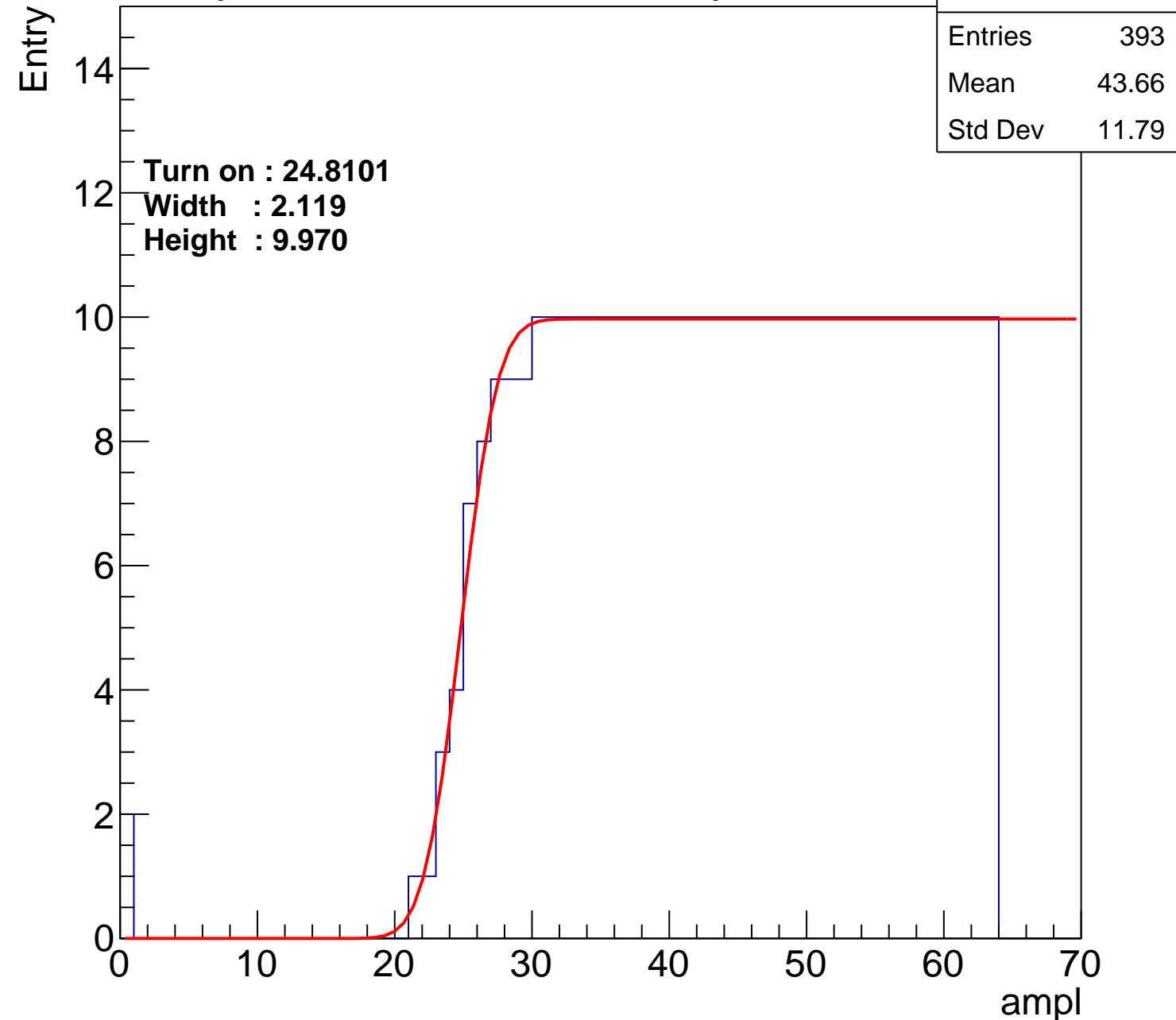
Width : 2.119

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.93
Std Dev	12.38

Turn on : 24.1158

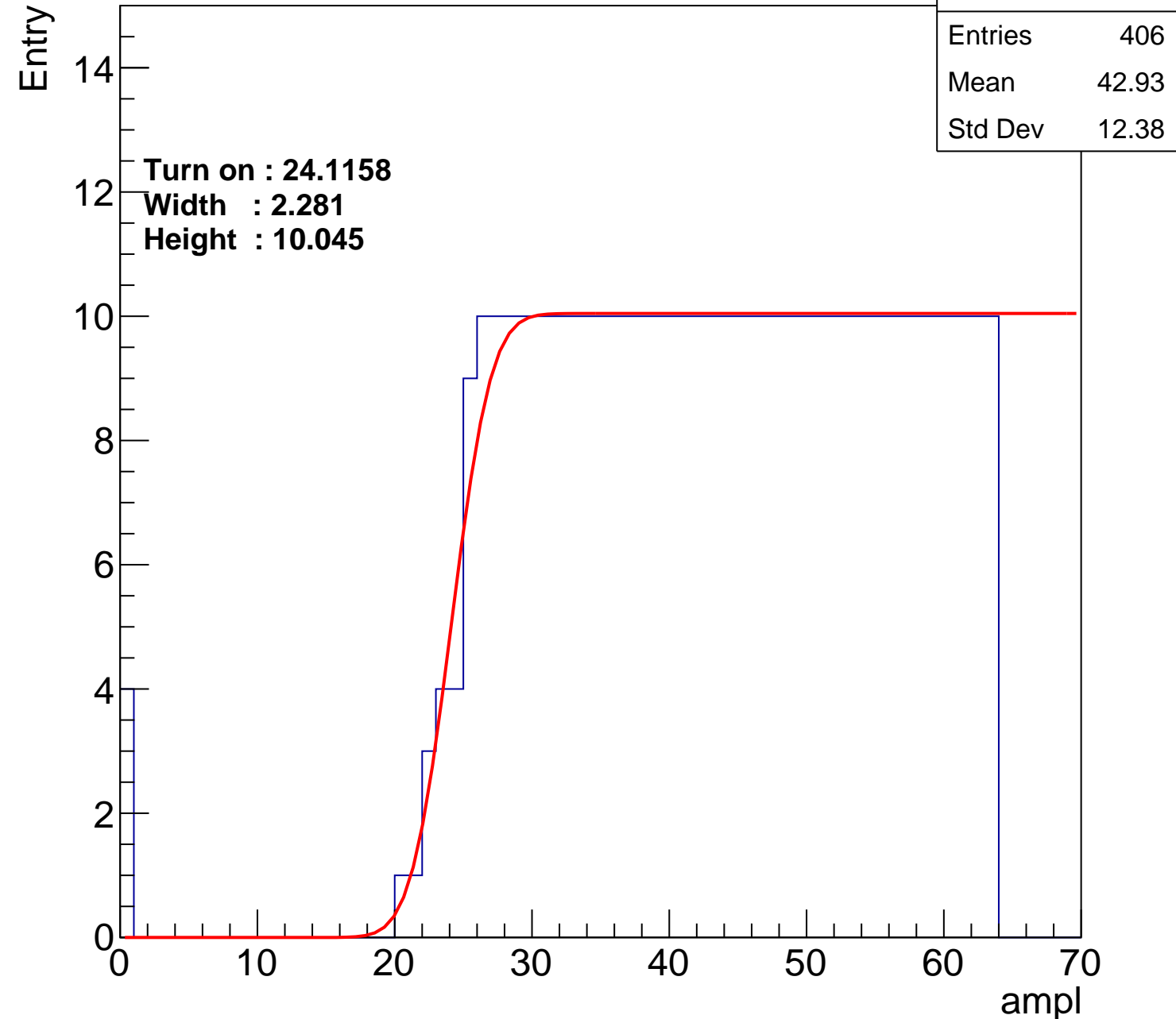
Width : 2.281

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	356
Mean	45.53
Std Dev	10.68

Turn on : 29.0282

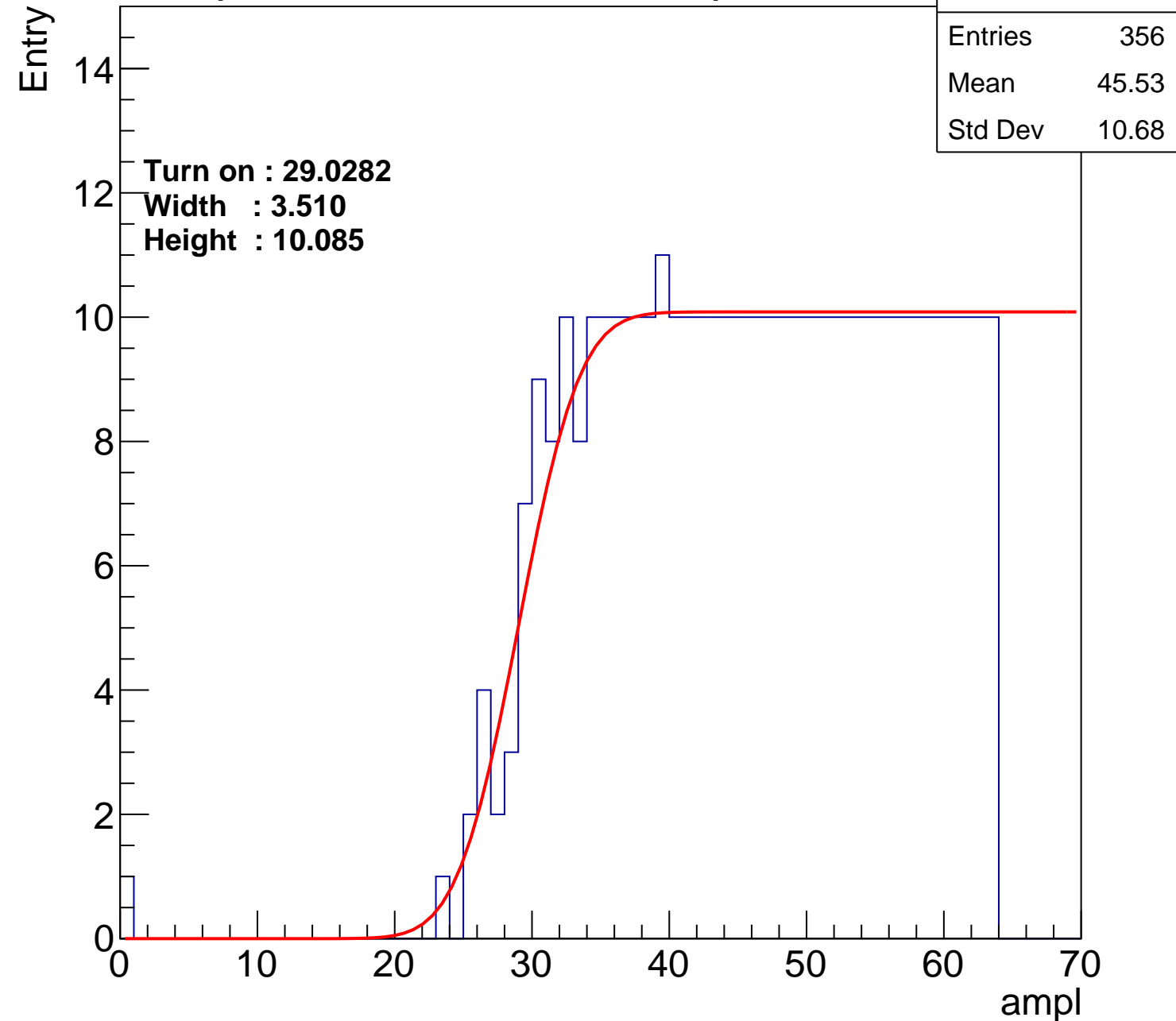
Width : 3.510

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.38
Std Dev	10.69

Turn on : 28.1817

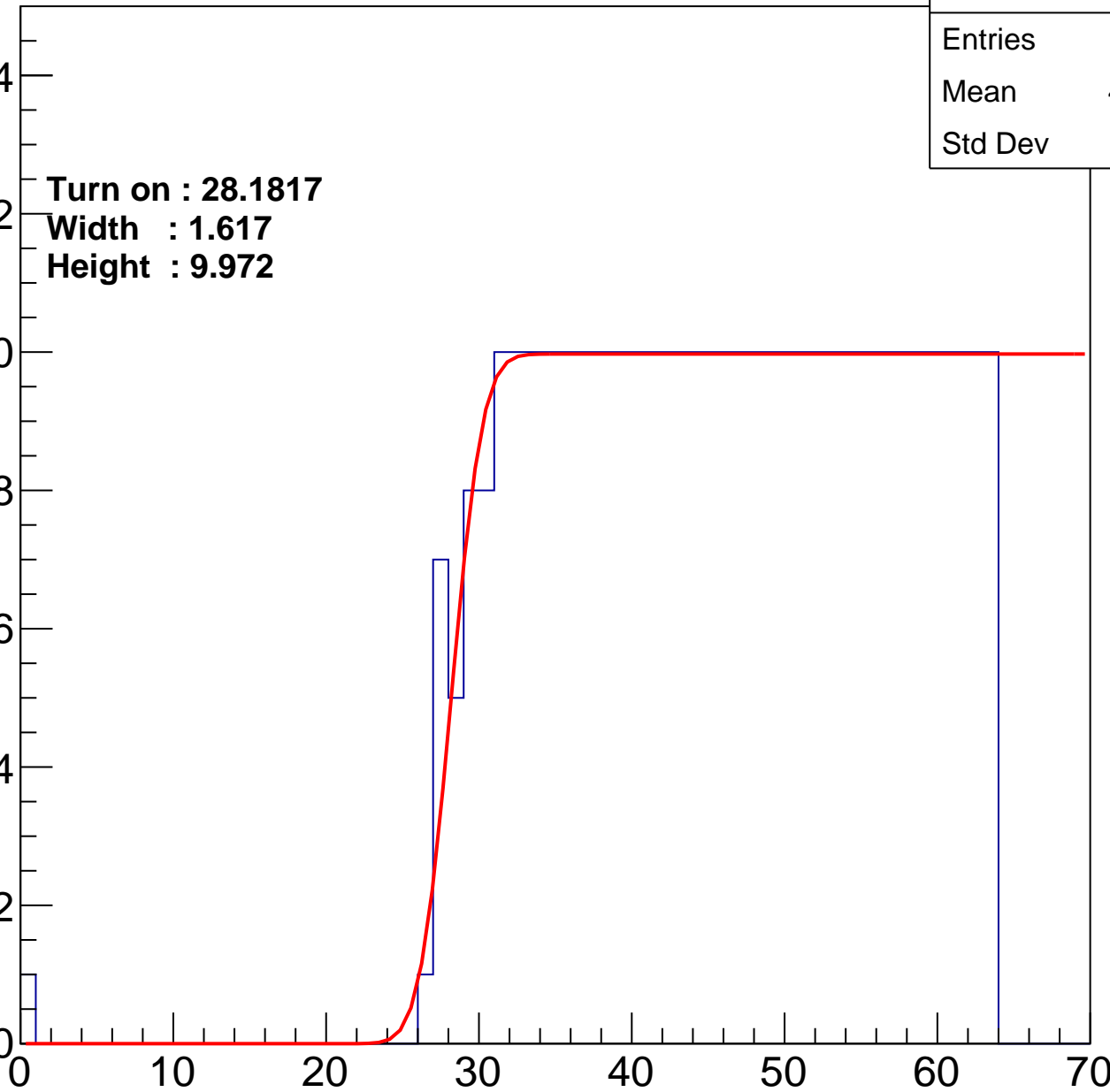
Width : 1.617

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch18

calib_packv5_042523_0143.root, FC#12, port B1

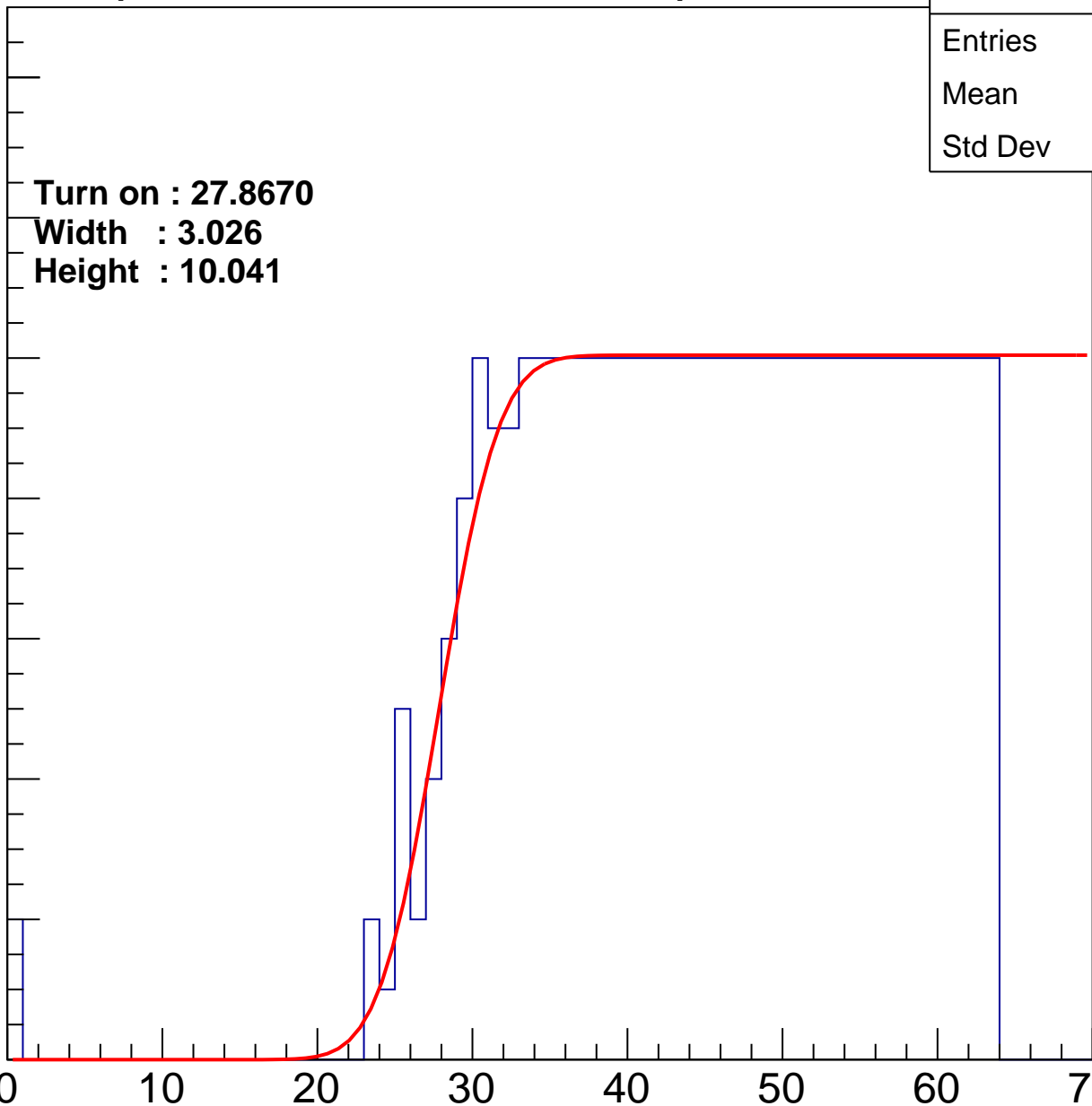
Entry

14
12
10
8
6
4
2
0

Turn on : 27.8670
Width : 3.026
Height : 10.041

Entries	368
Mean	44.84
Std Dev	11.23

ampl



B0L102S, U24-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.75
Std Dev	11.2

Turn on : 28.0903

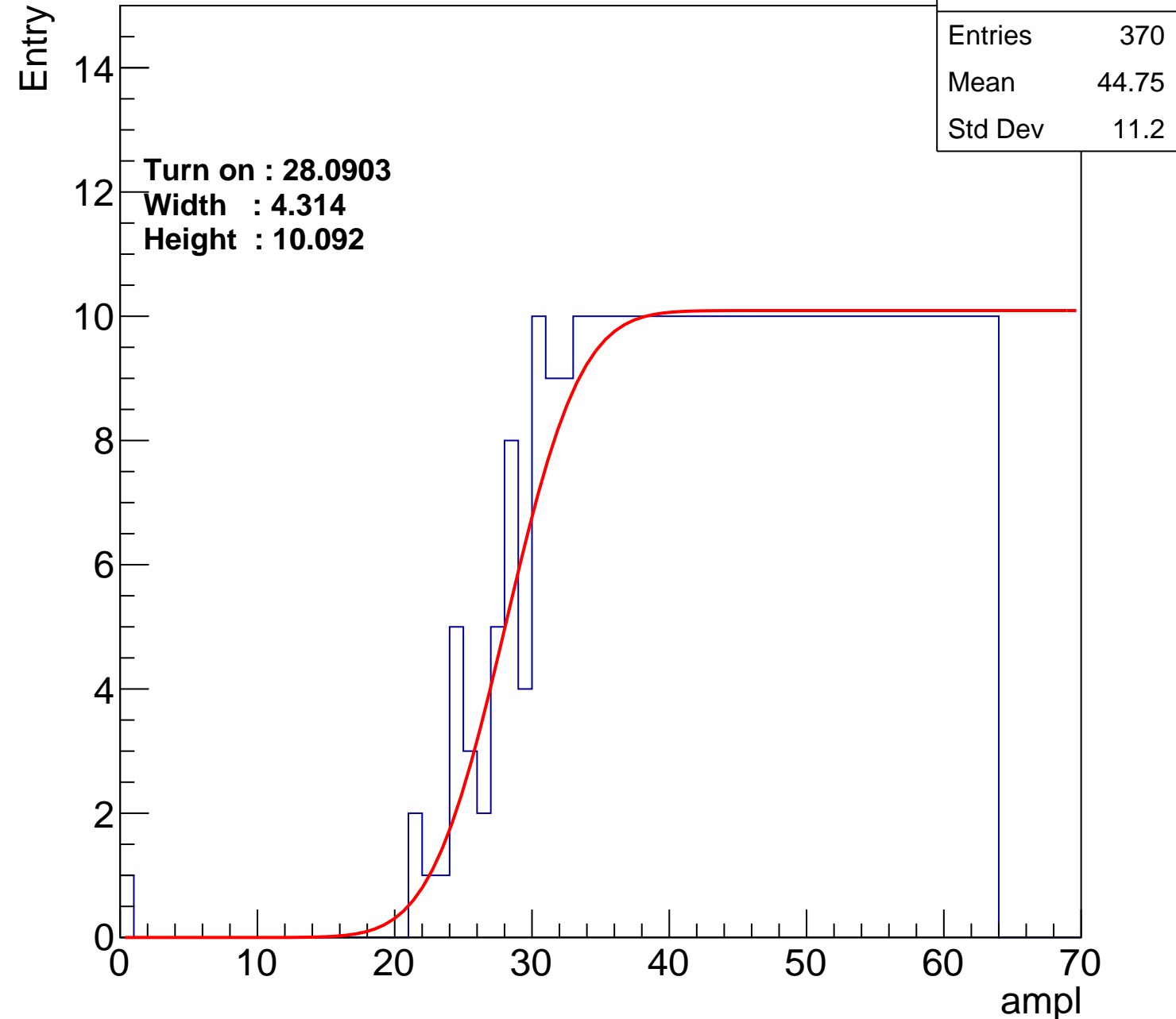
Width : 4.314

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.94
Std Dev	11.12

Turn on : 27.3515

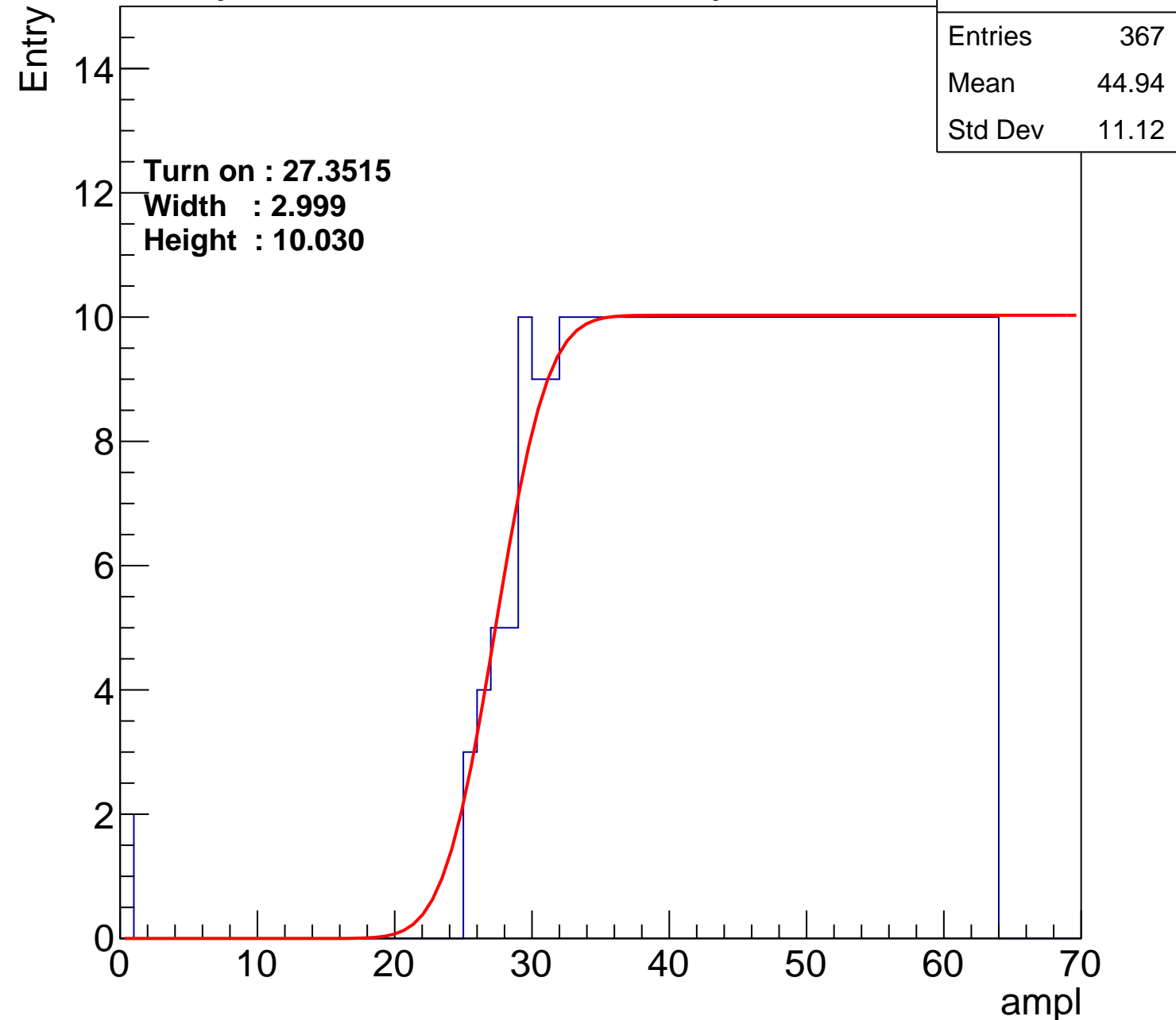
Width : 2.999

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.4
Std Dev	12.23

Turn on : 25.0175

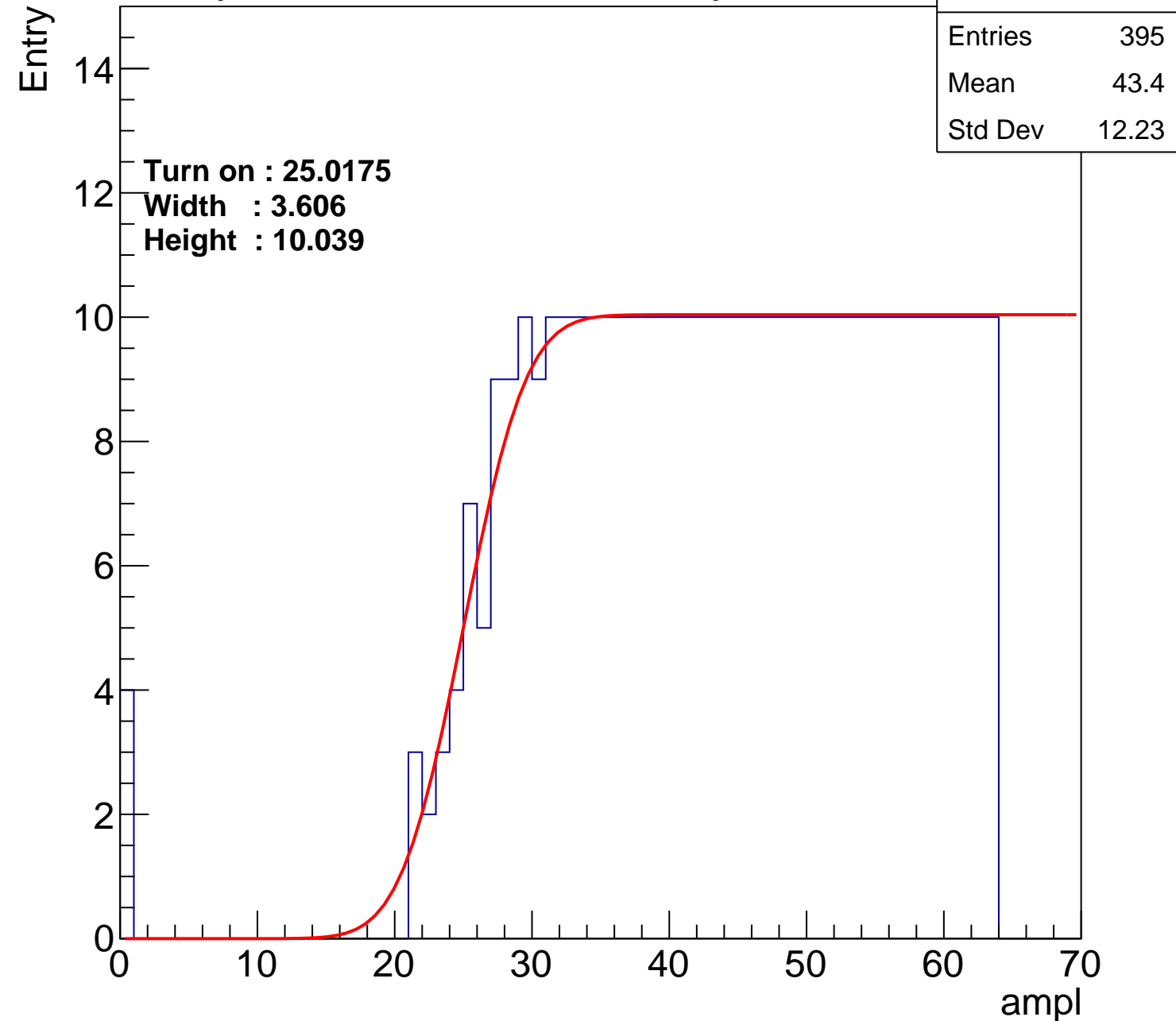
Width : 3.606

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.02
Std Dev	11.78

Turn on : 26.4144

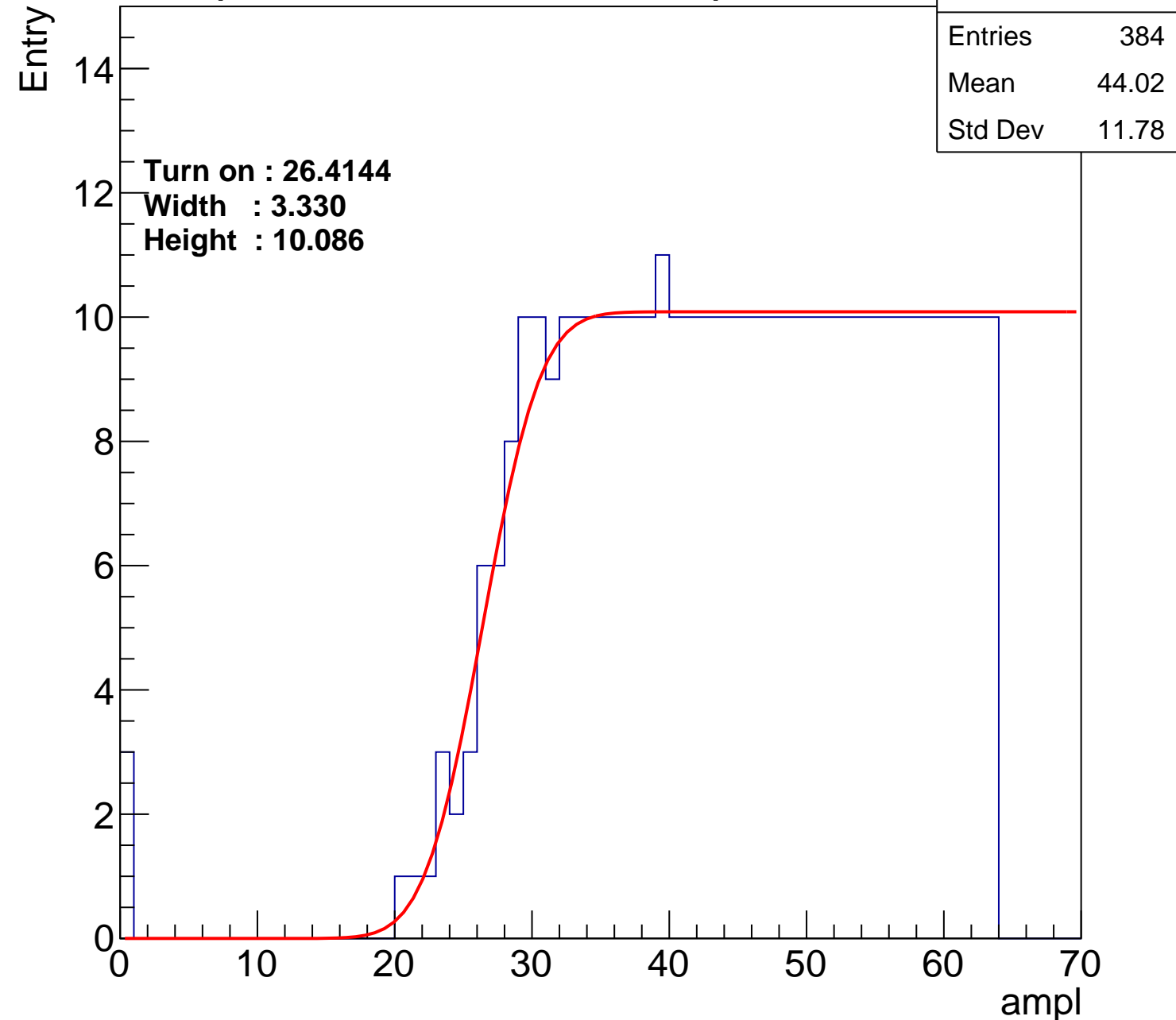
Width : 3.330

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.28
Std Dev	11.97

Turn on : 24.2397

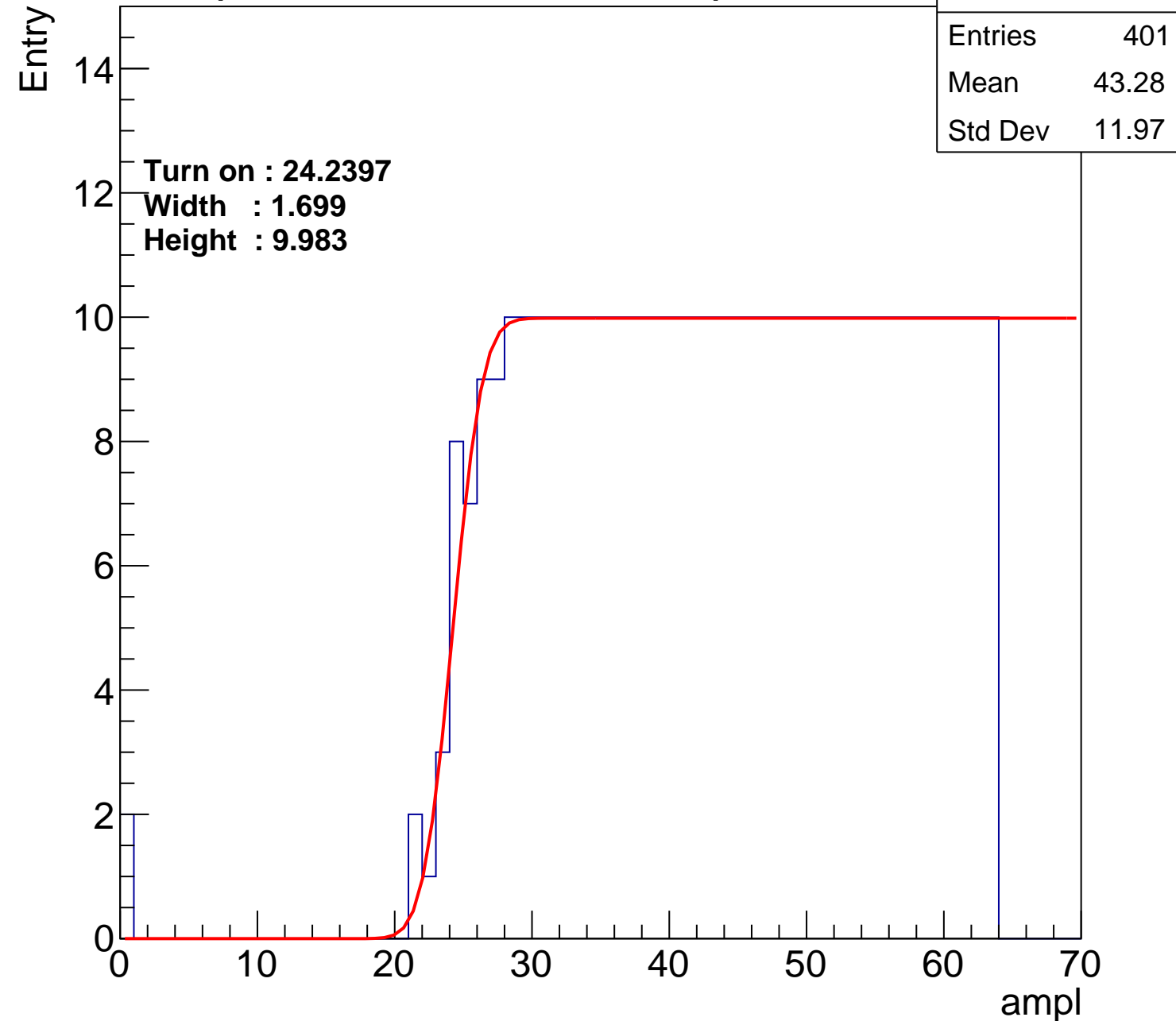
Width : 1.699

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch24

calib_packv5_042523_0143.root, FC#12, port B1

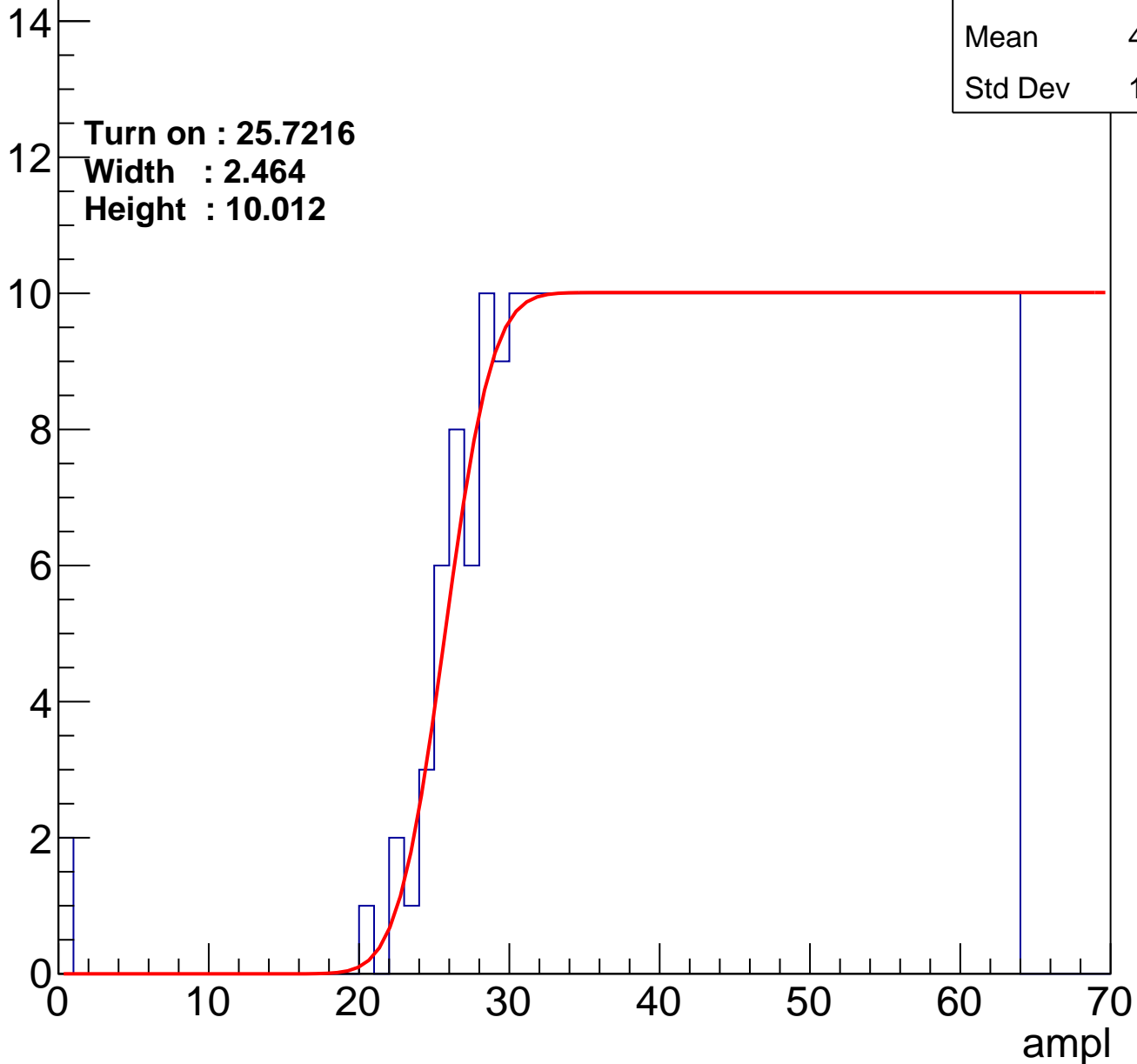
Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.7216

Width : 2.464

Height : 10.012

Entry



B0L102S, U24-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.77
Std Dev	11.11

Turn on : 27.2574

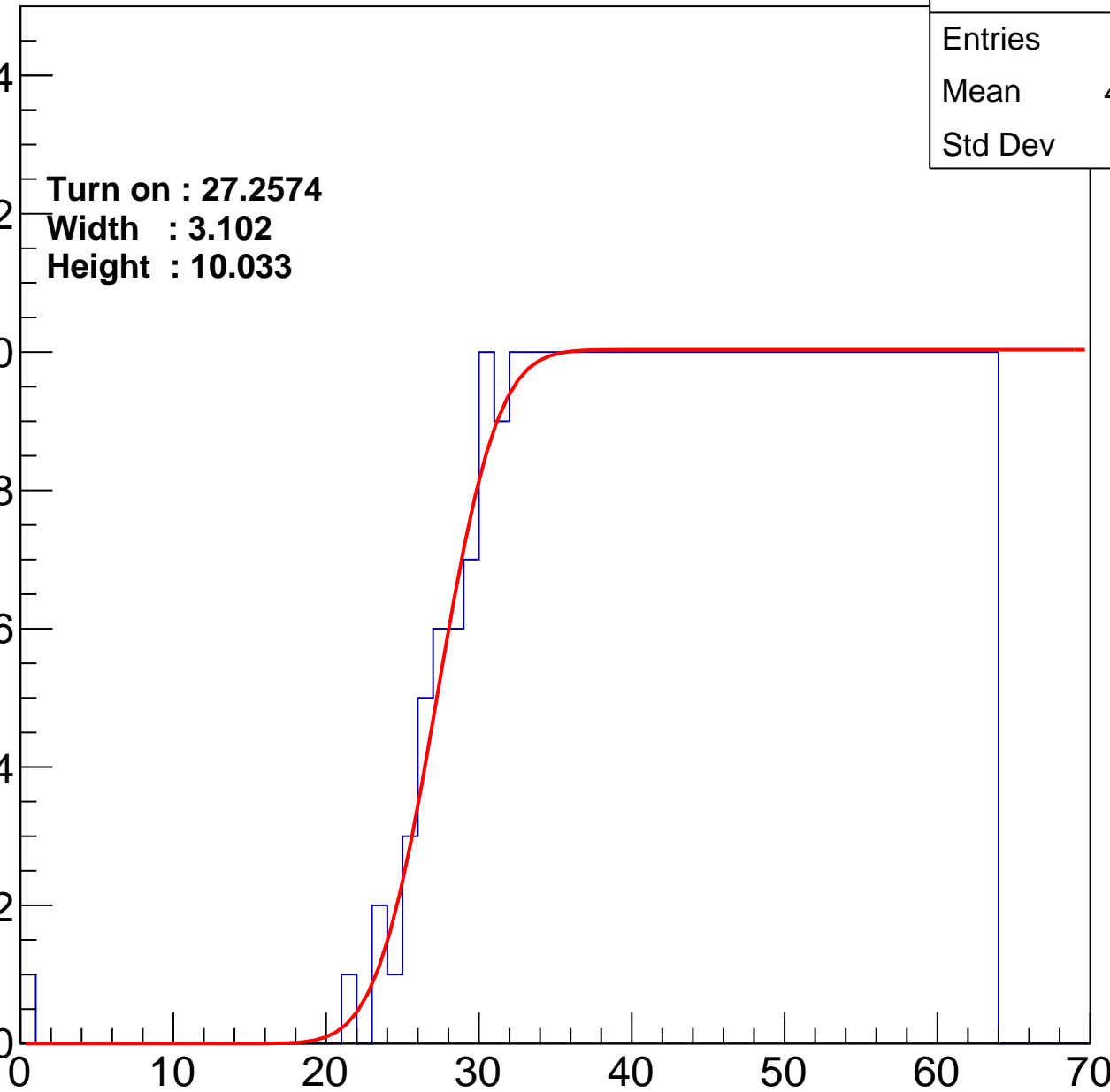
Width : 3.102

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch26

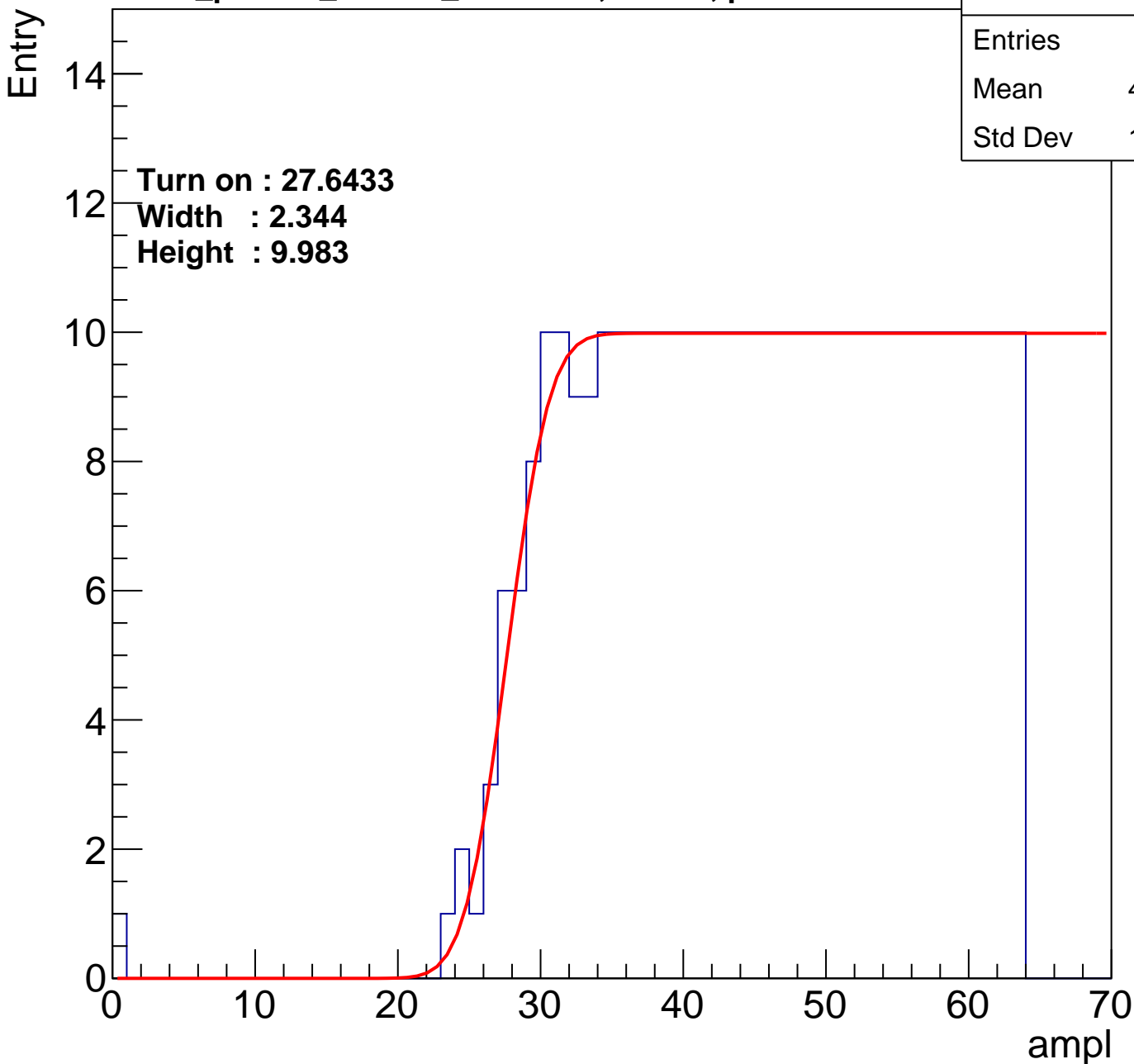
calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	45.03
Std Dev	10.94

Turn on : 27.6433

Width : 2.344

Height : 9.983



B0L102S, U24-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.5584

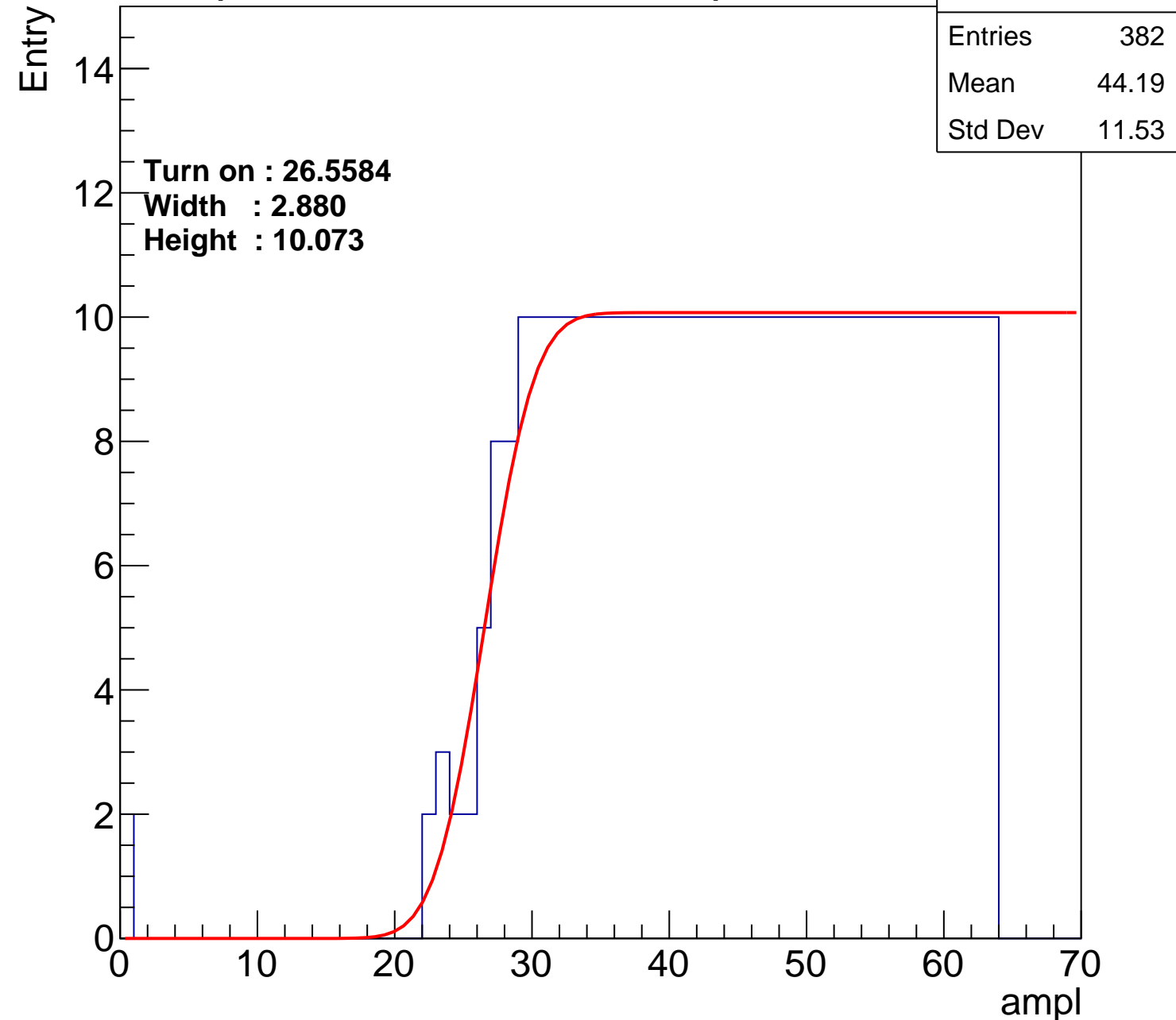
Width : 2.880

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 27.6772

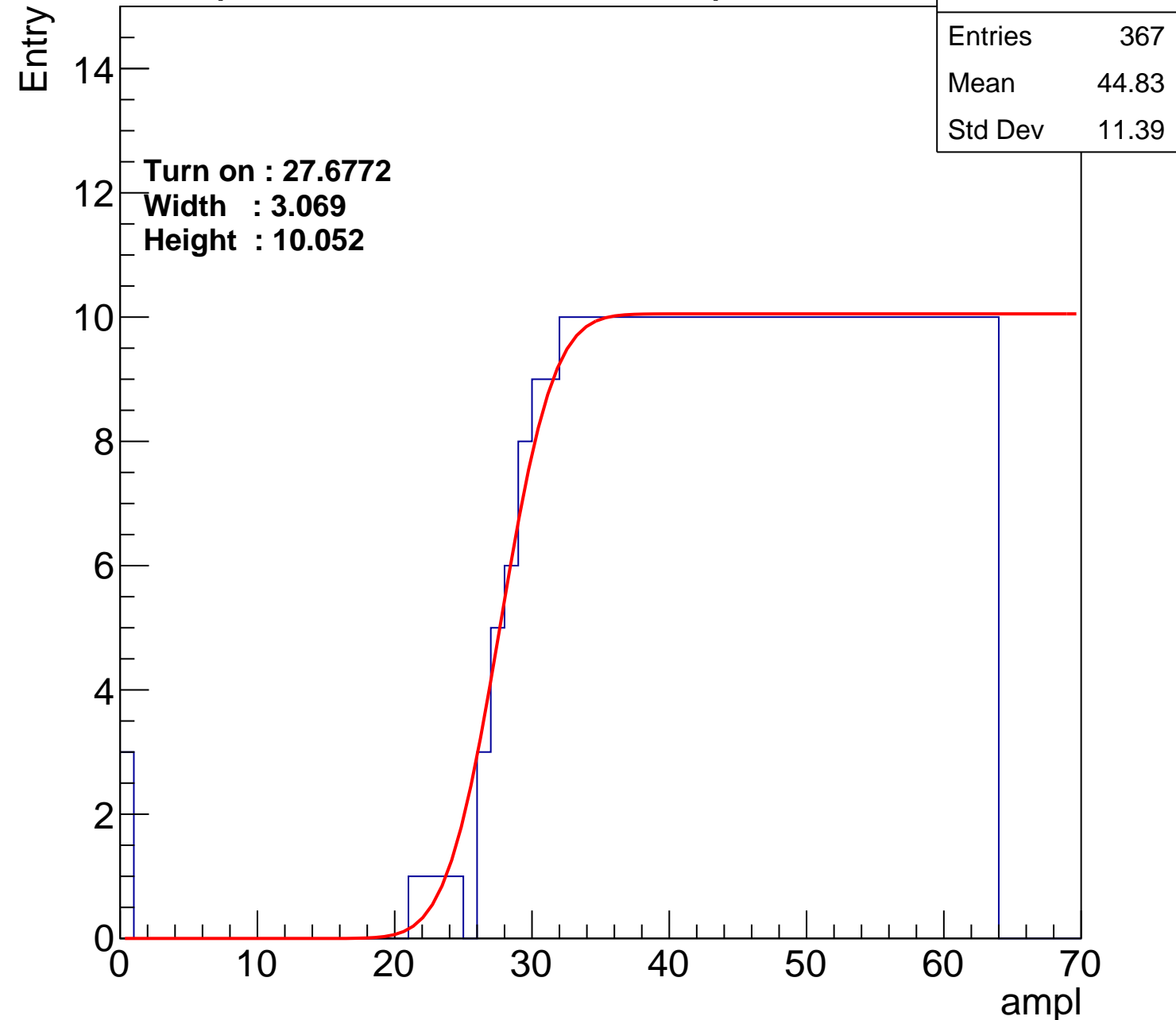
Width : 3.069

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.8
Std Dev	11.58

Turn on : 27.5518

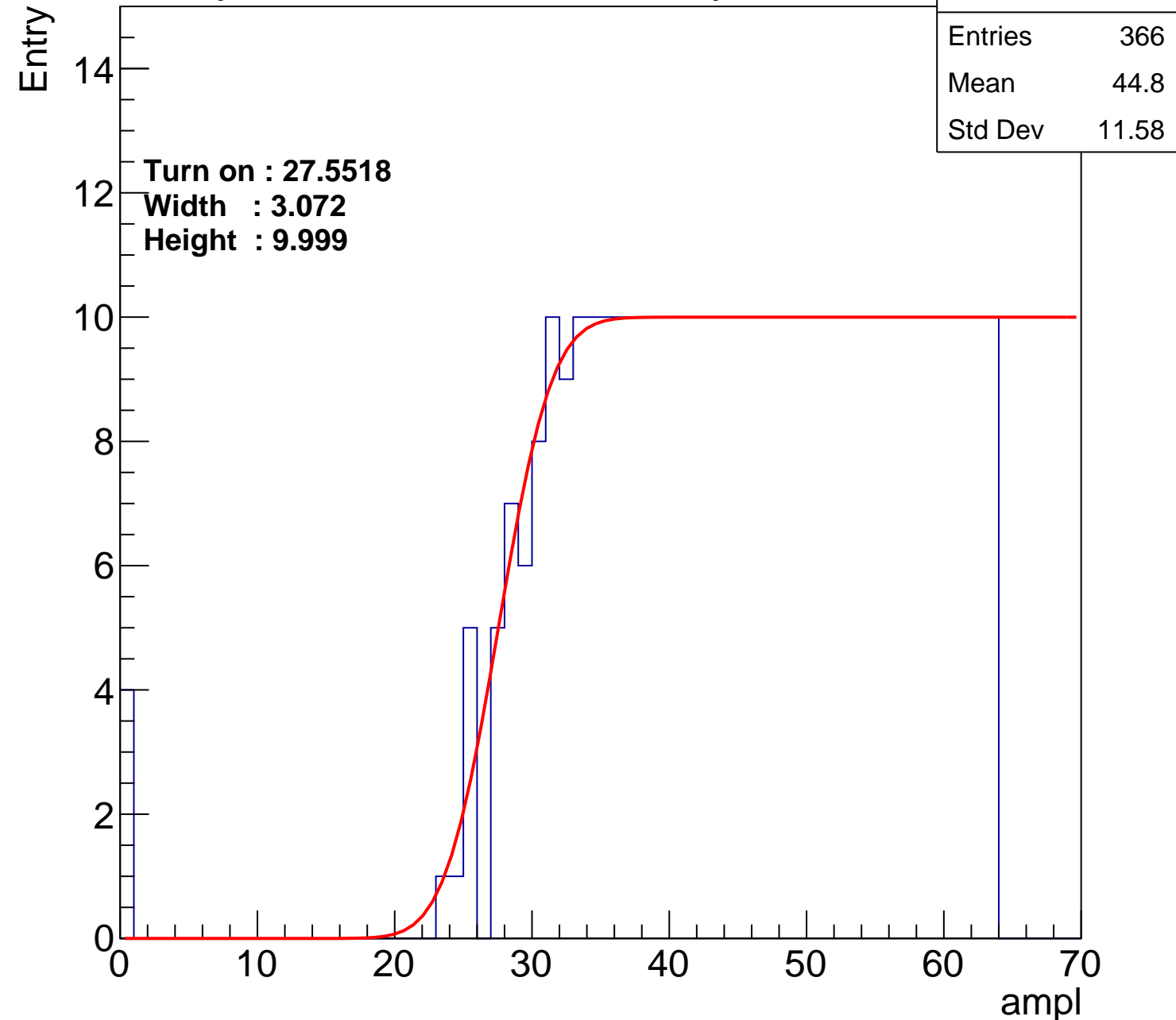
Width : 3.072

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch30

calib_packv5_042523_0143.root, FC#12, port B1

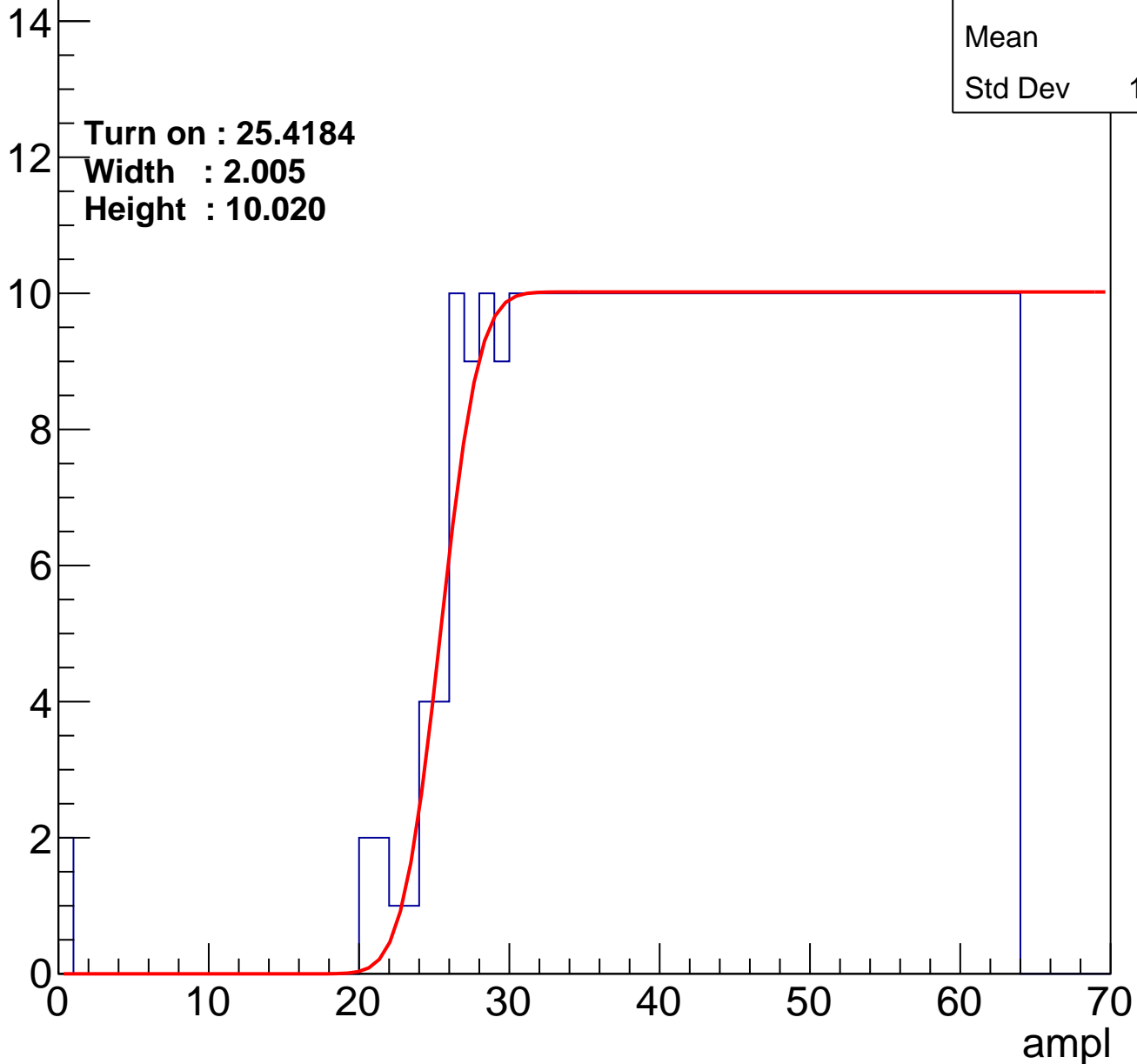
Entries	394
Mean	43.6
Std Dev	11.84

Turn on : 25.4184

Width : 2.005

Height : 10.020

Entry



B0L102S, U24-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.28
Std Dev	11.37

Turn on : 26.1725

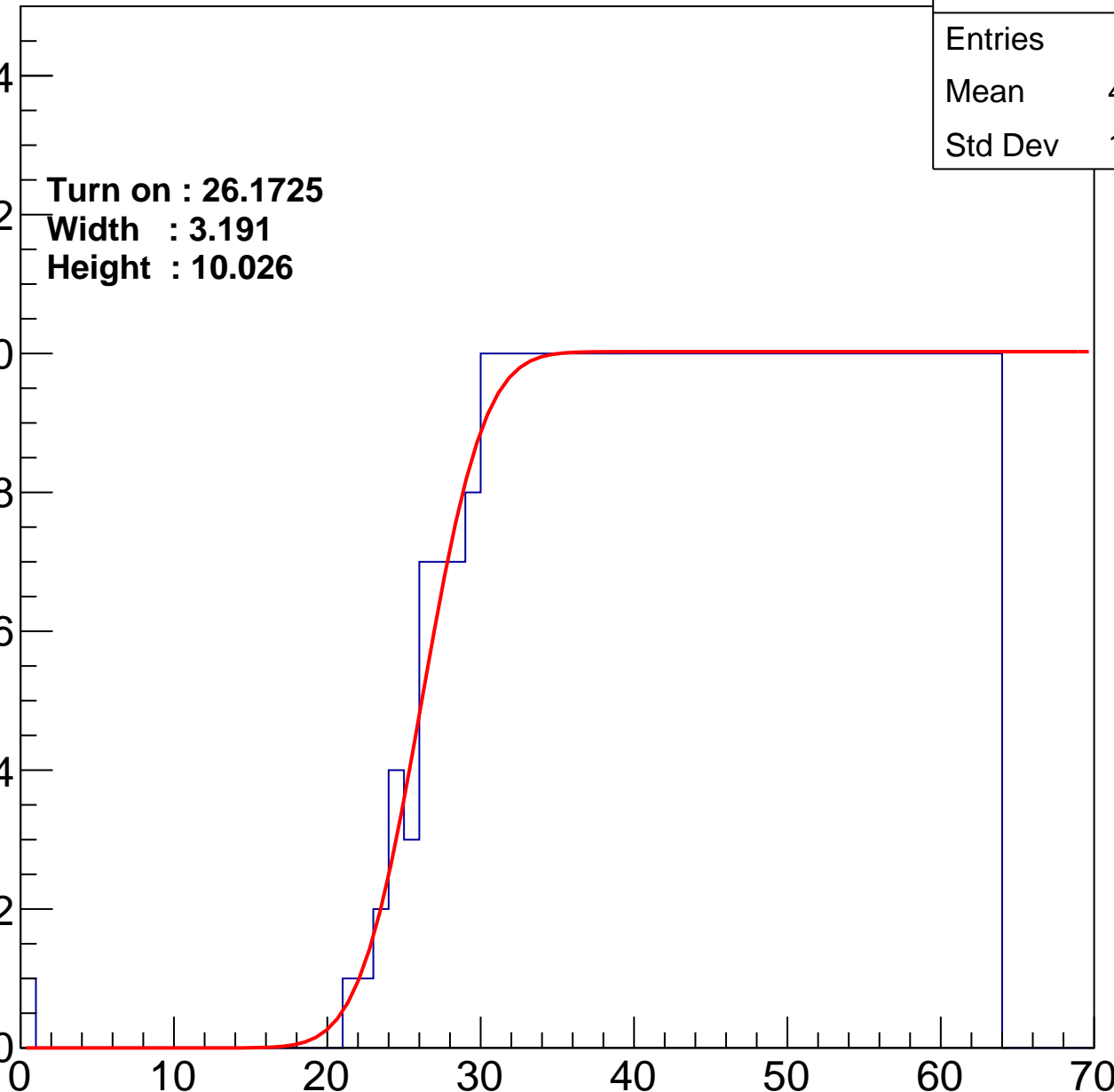
Width : 3.191

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.54
Std Dev	11.76

Turn on : 24.6946

Width : 2.888

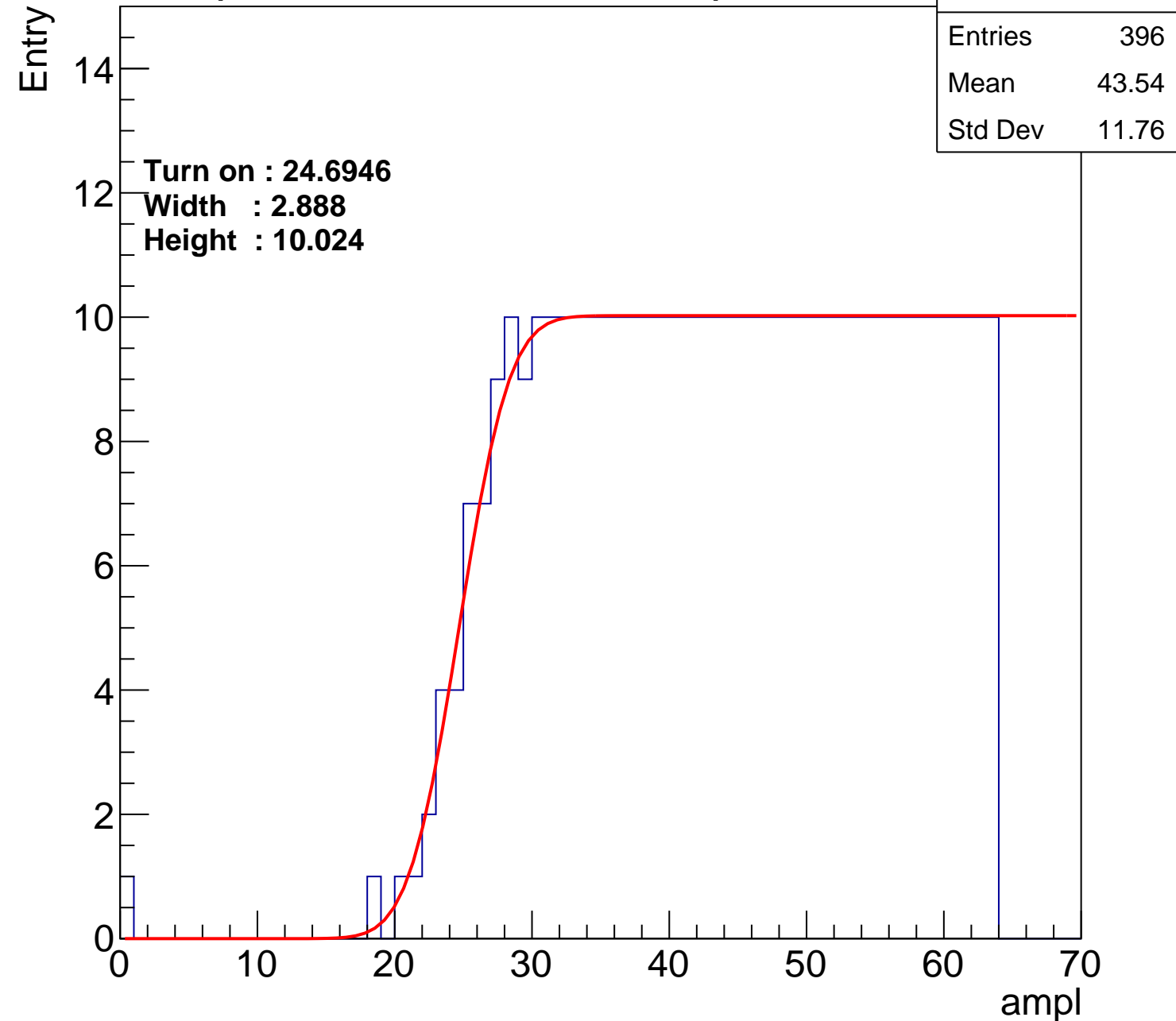
Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U24-ch33

calib_packv5_042523_0143.root, FC#12, port B1

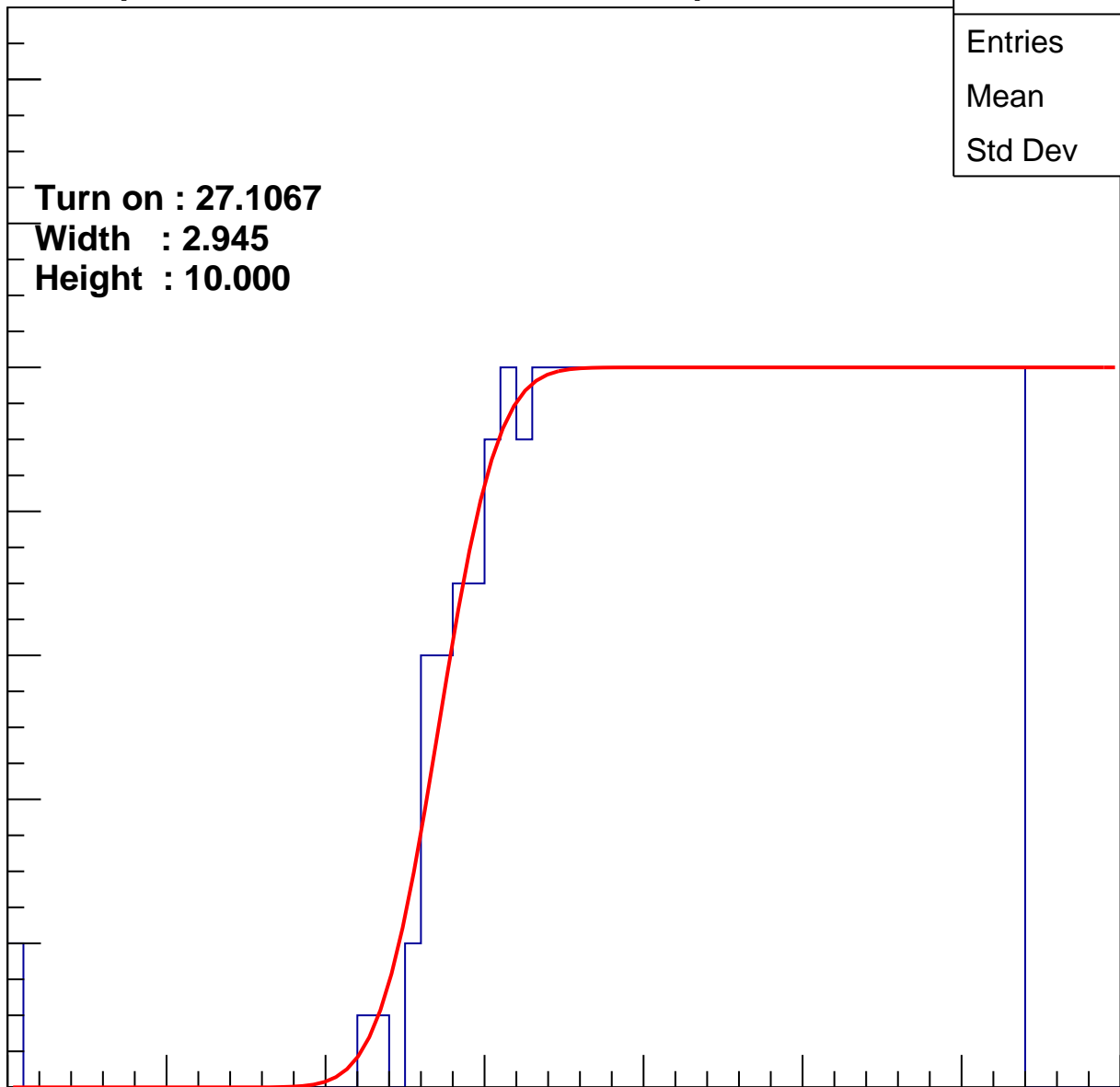
Entry

14
12
10
8
6
4
2
0

Turn on : 27.1067
Width : 2.945
Height : 10.000

Entries	370
Mean	44.76
Std Dev	11.25

ampl



B0L102S, U24-ch34

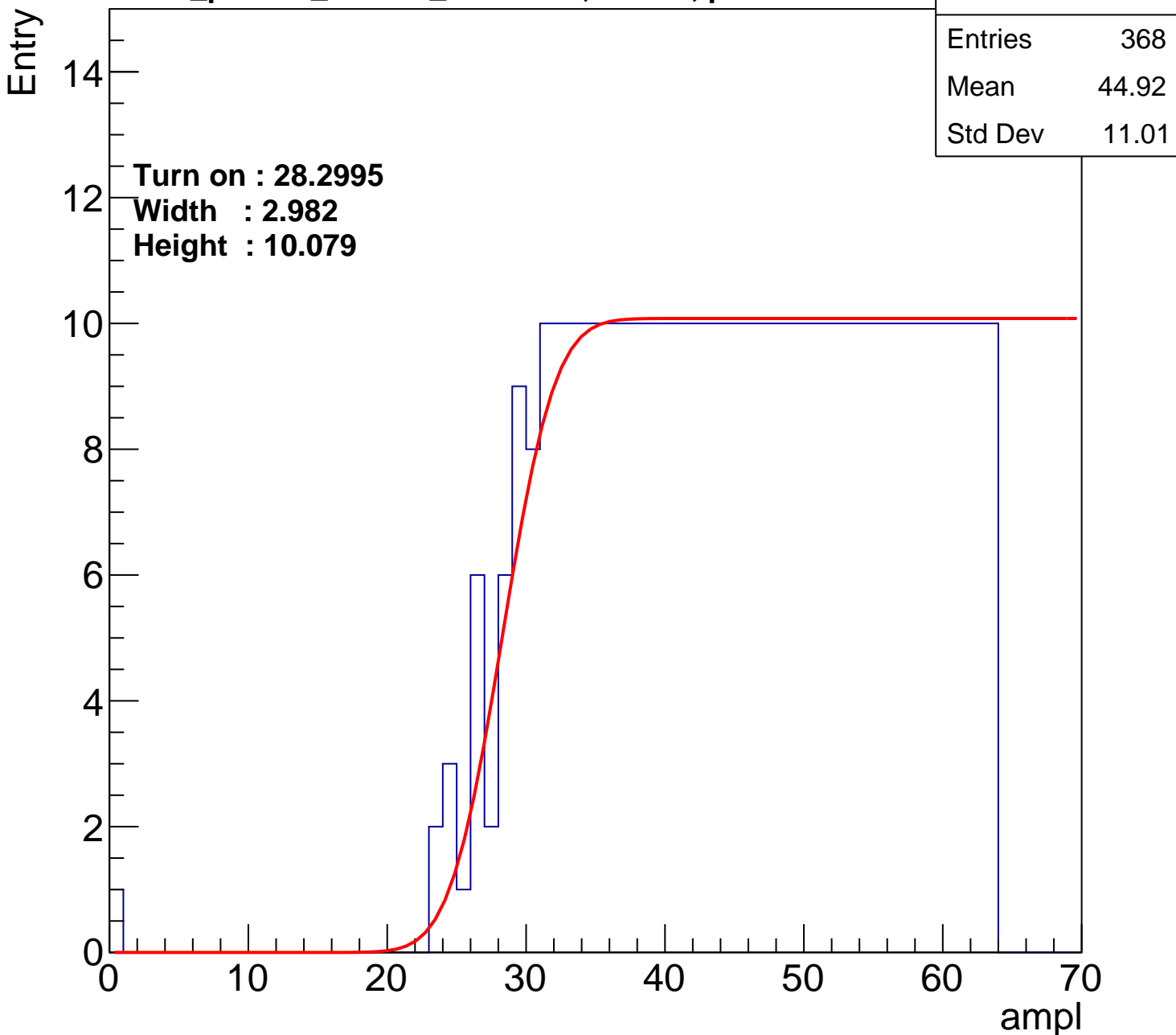
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 28.2995

Width : 2.982

Height : 10.079

Entries	368
Mean	44.92
Std Dev	11.01



B0L102S, U24-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.6768

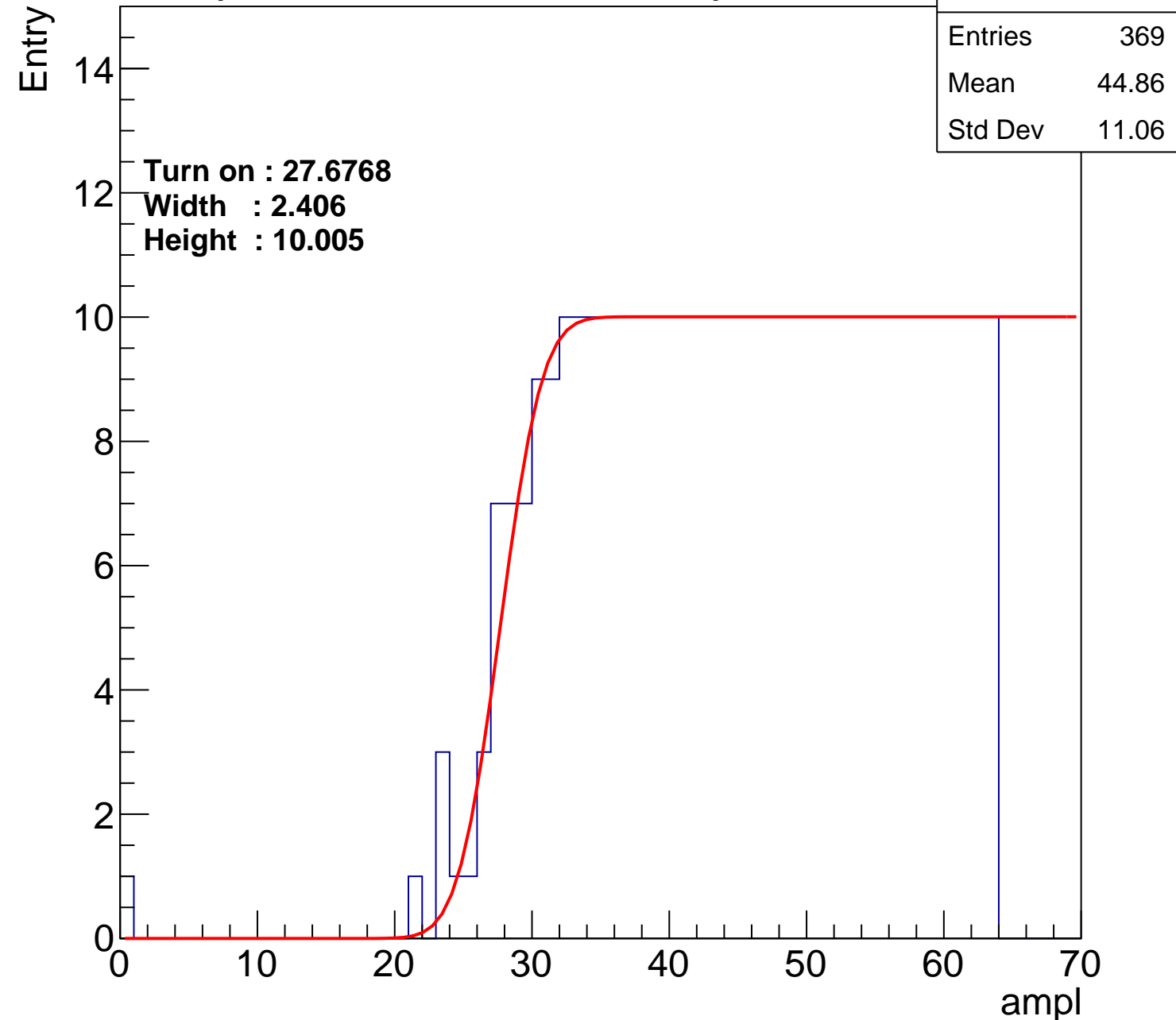
Width : 2.406

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 25.7632

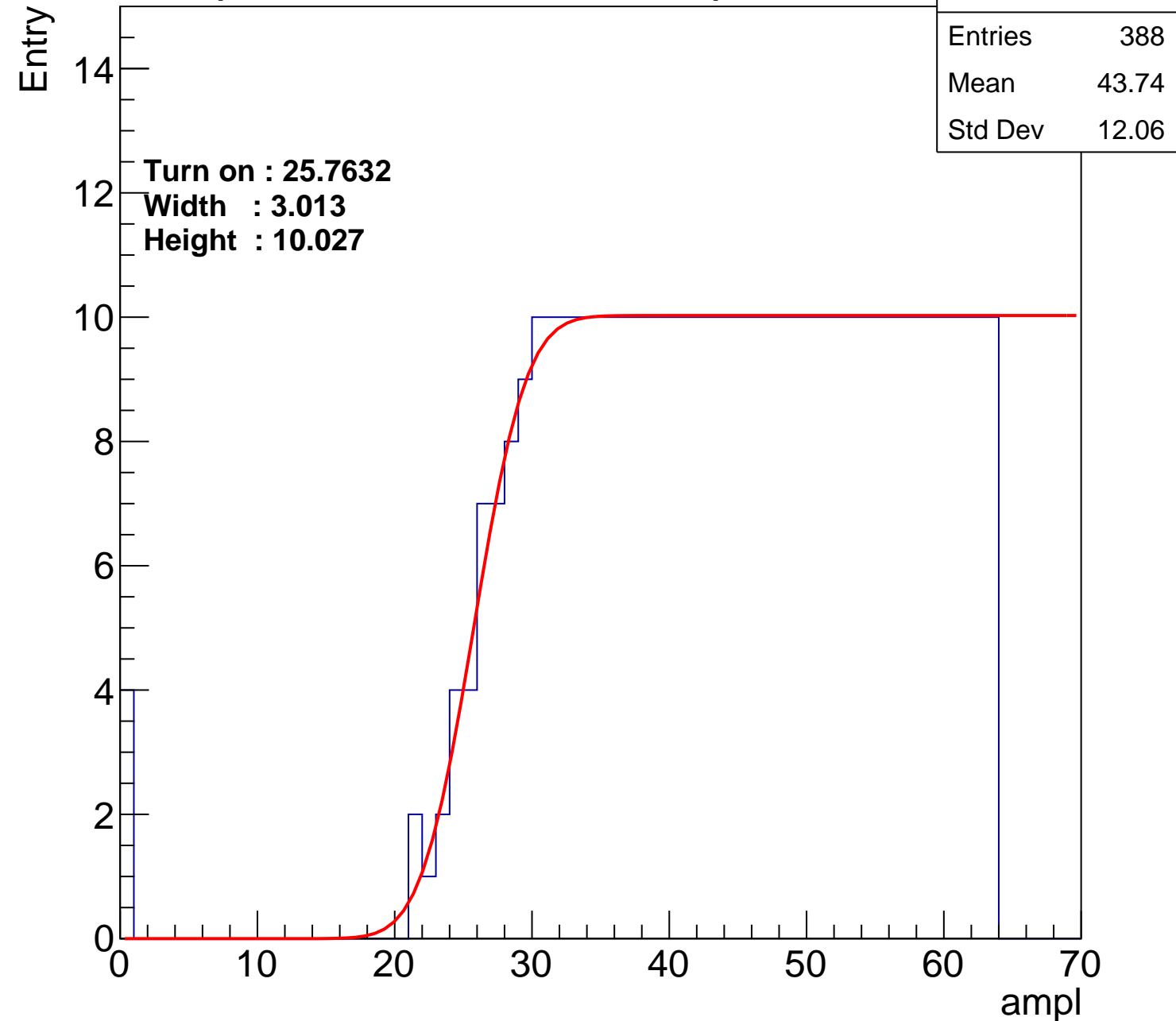
Width : 3.013

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.16
Std Dev	11.52

Turn on : 25.8489

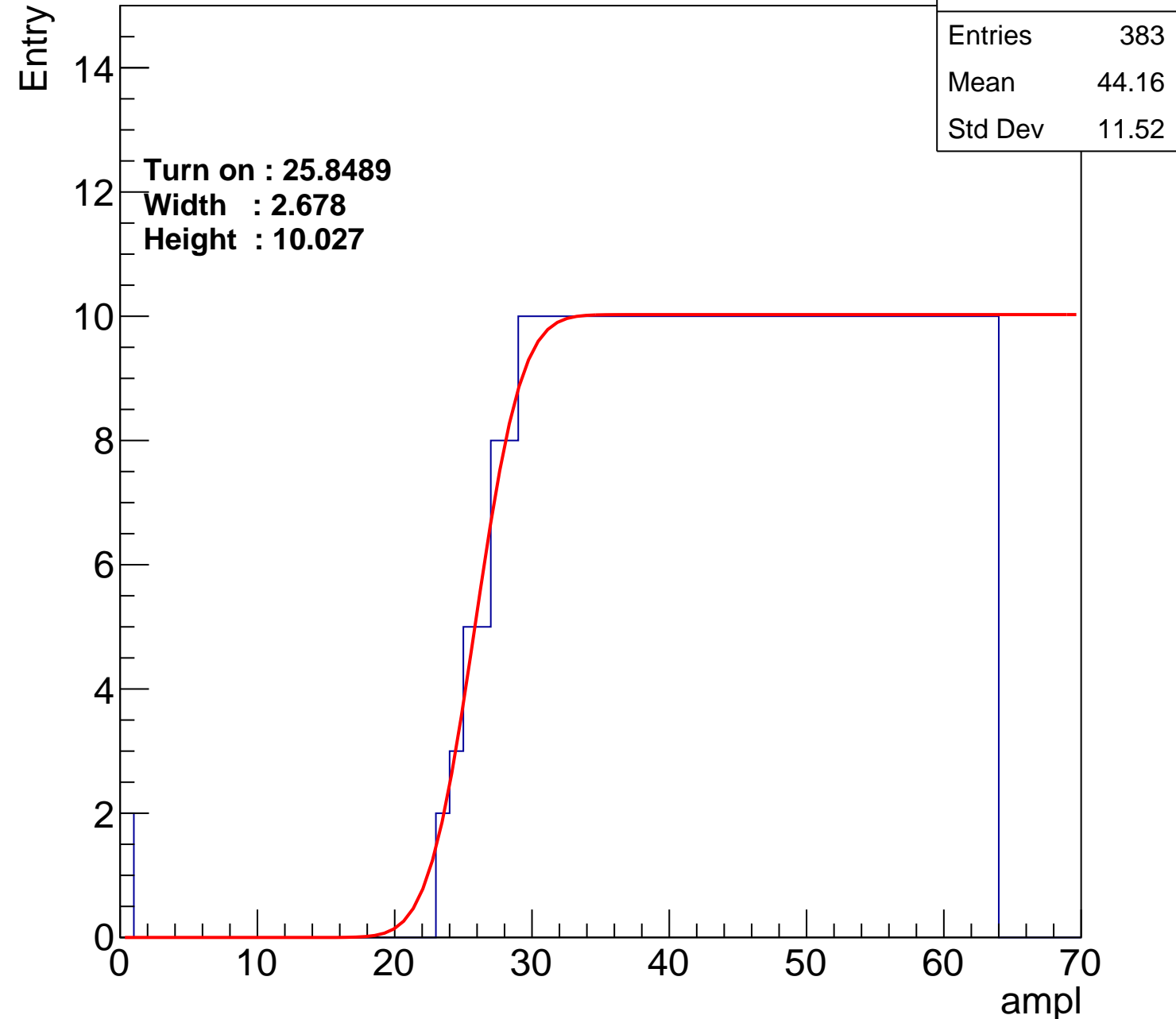
Width : 2.678

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.48
Std Dev	11.92

Turn on : 24.8566

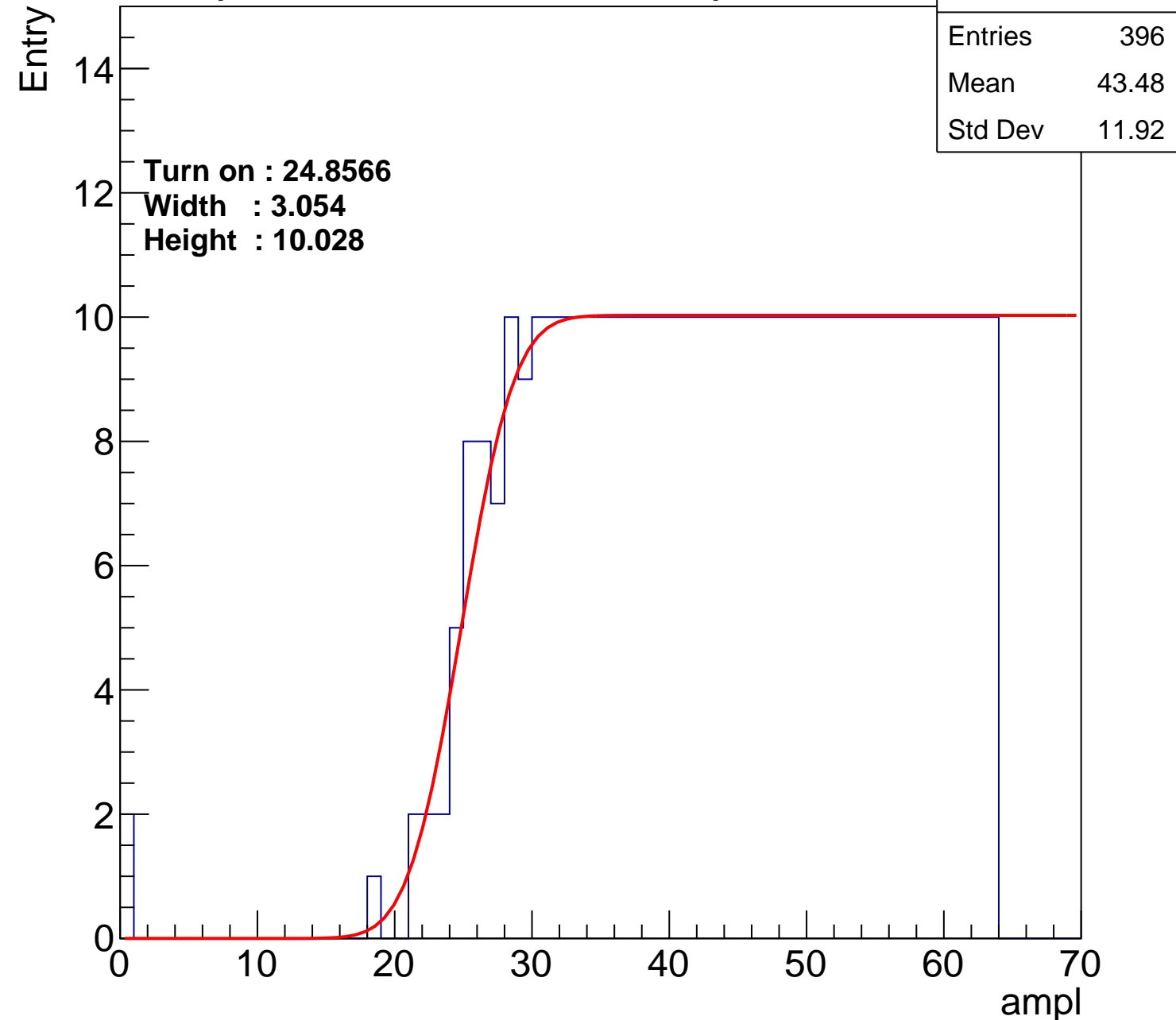
Width : 3.054

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch39

calib_packv5_042523_0143.root, FC#12, port B1

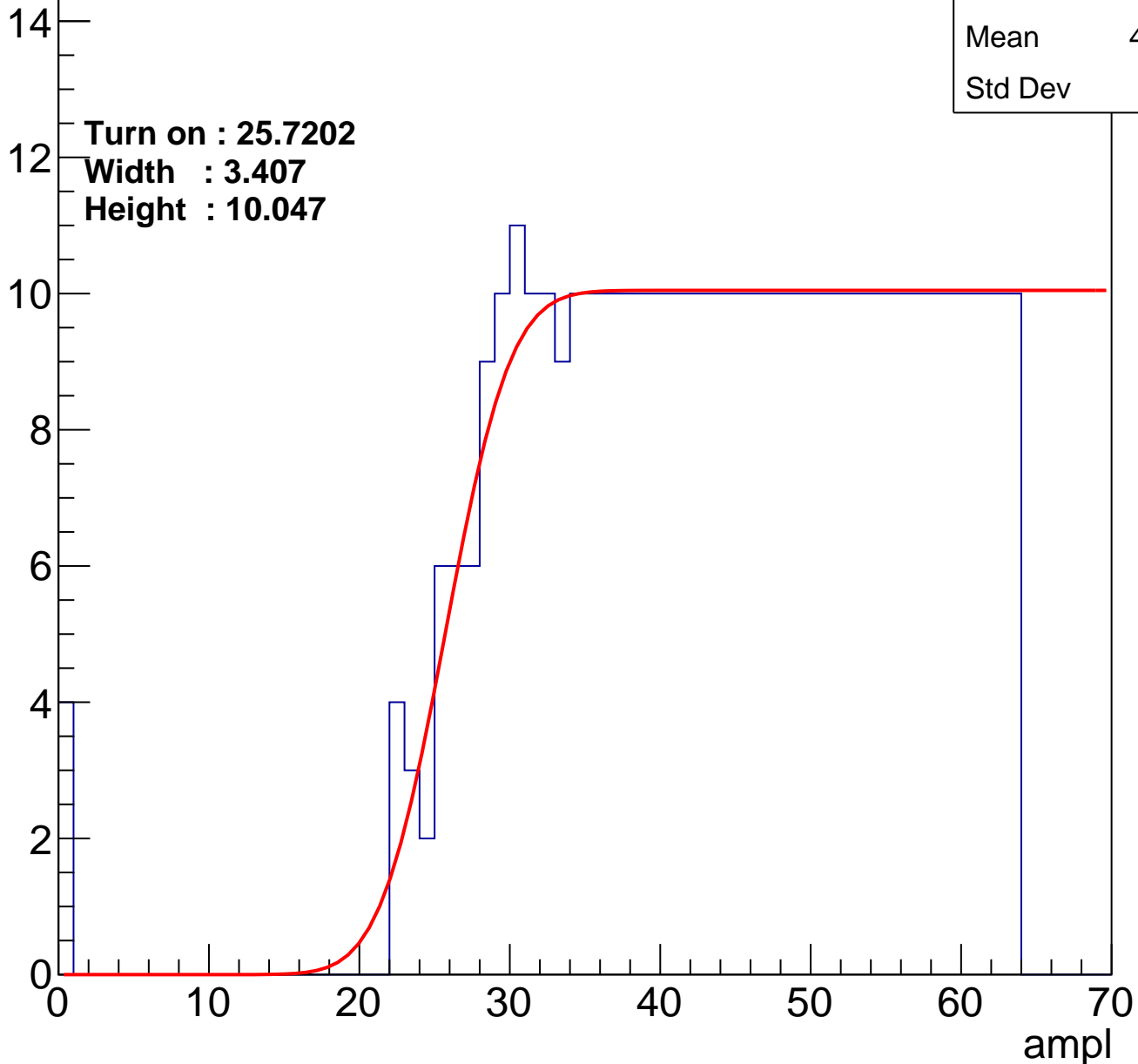
Entries	390
Mean	43.65
Std Dev	12.1

Turn on : 25.7202

Width : 3.407

Height : 10.047

Entry



B0L102S, U24-ch40

calib_packv5_042523_0143.root, FC#12, port B1

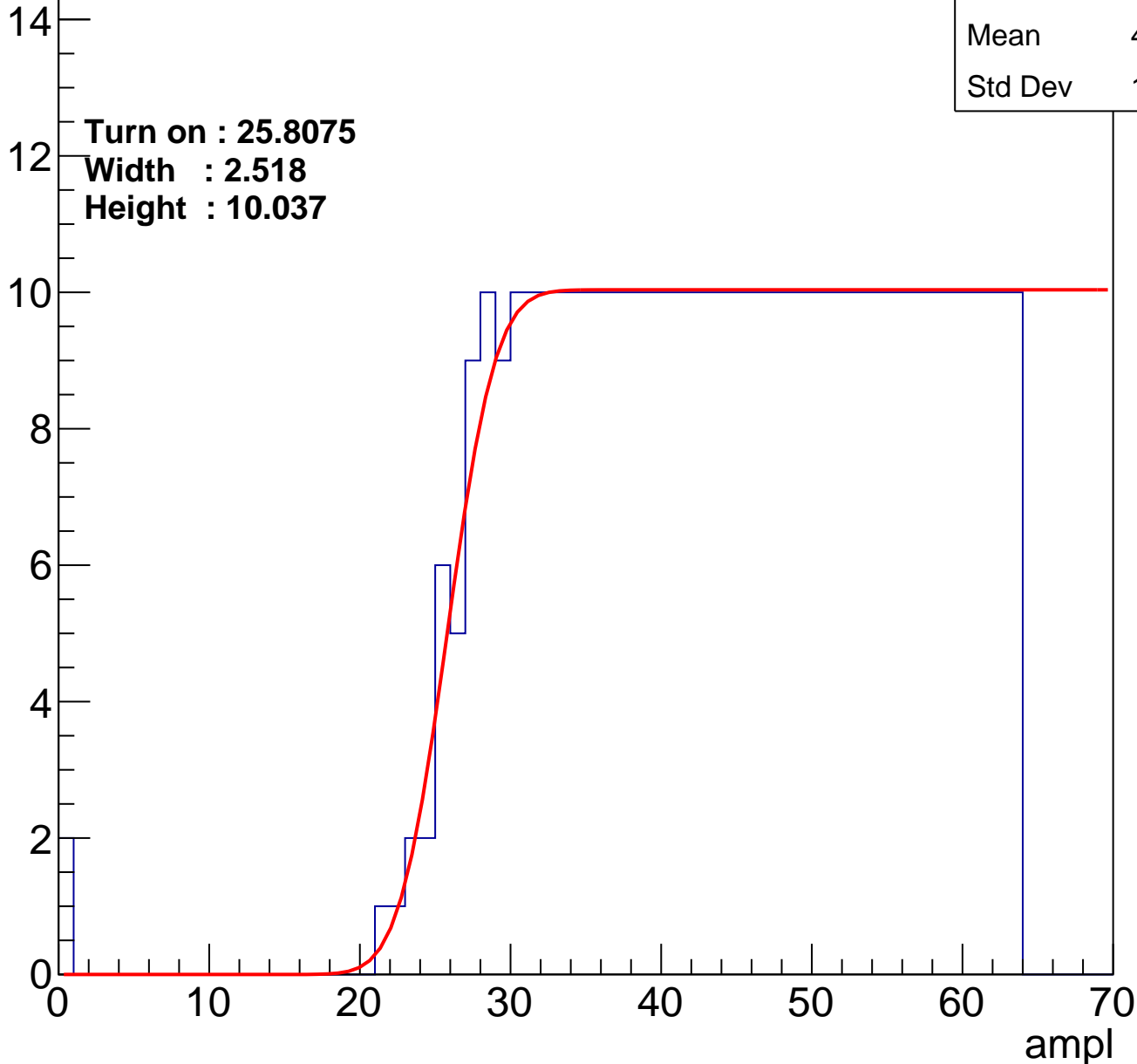
Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.8075

Width : 2.518

Height : 10.037

Entry



B0L102S, U24-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.46
Std Dev	11.43

Turn on : 27.0668

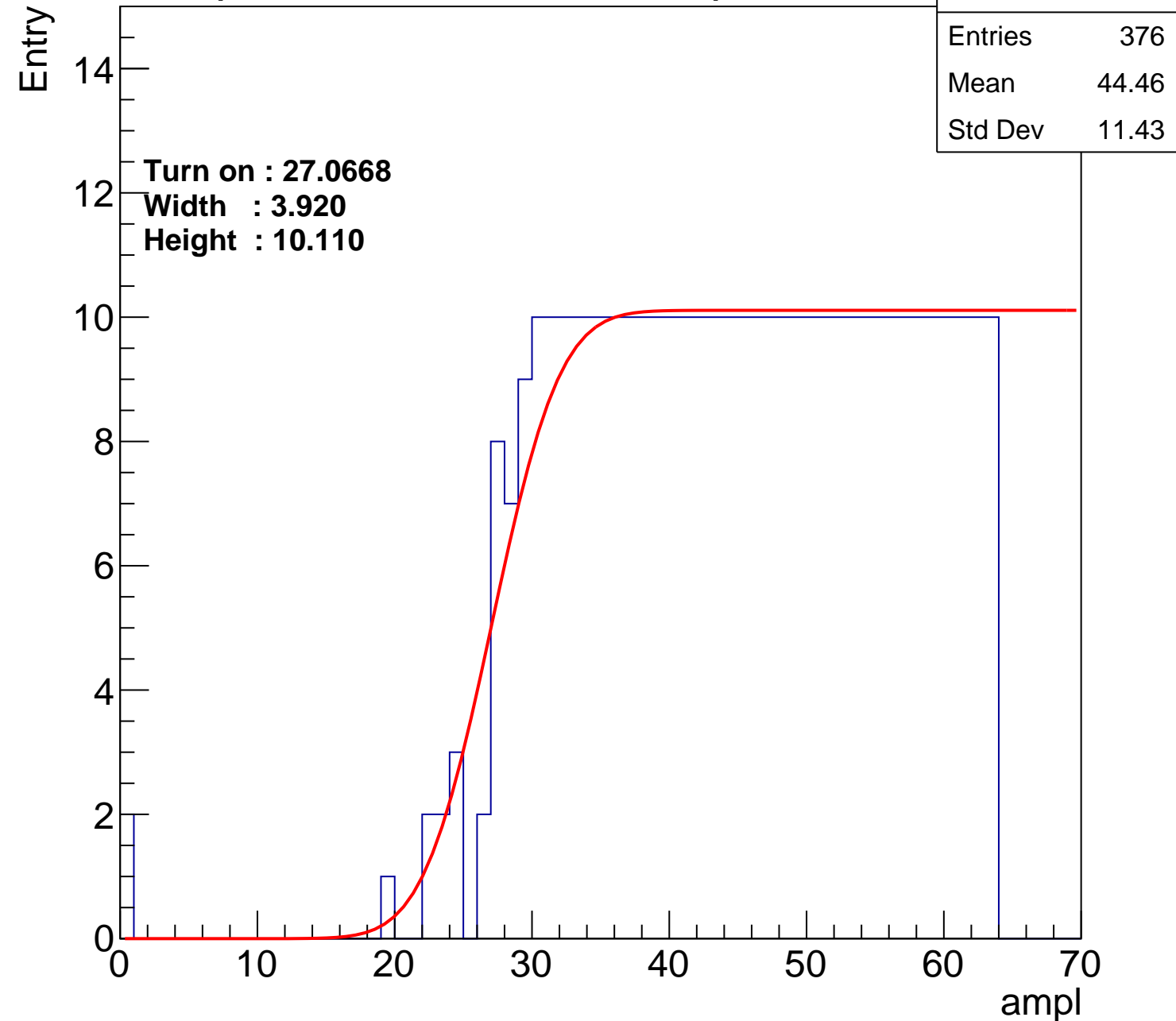
Width : 3.920

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch42

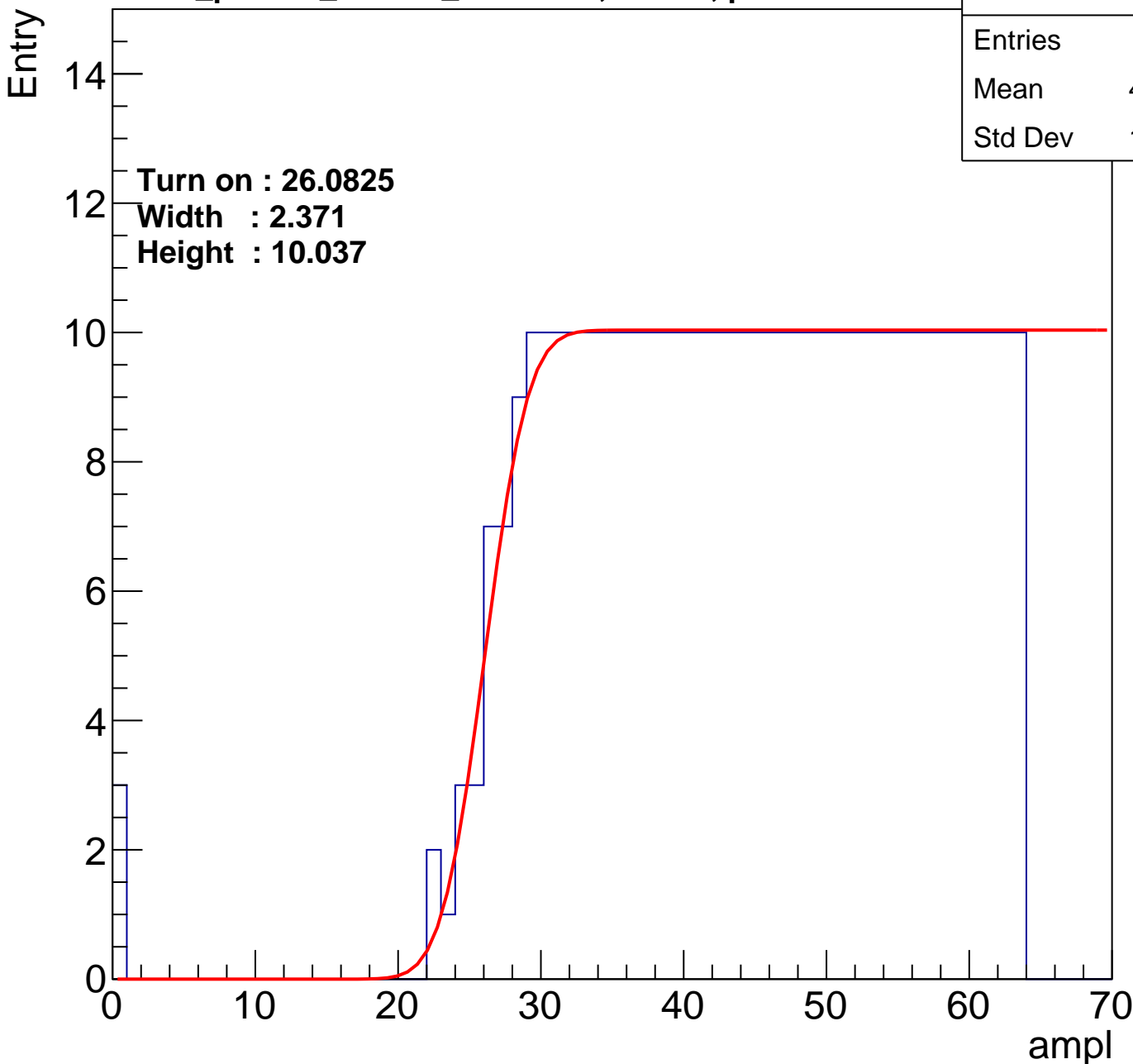
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 26.0825

Width : 2.371

Height : 10.037

Entries	385
Mean	43.99
Std Dev	11.75



B0L102S, U24-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.82
Std Dev	11.97

Turn on : 25.8292

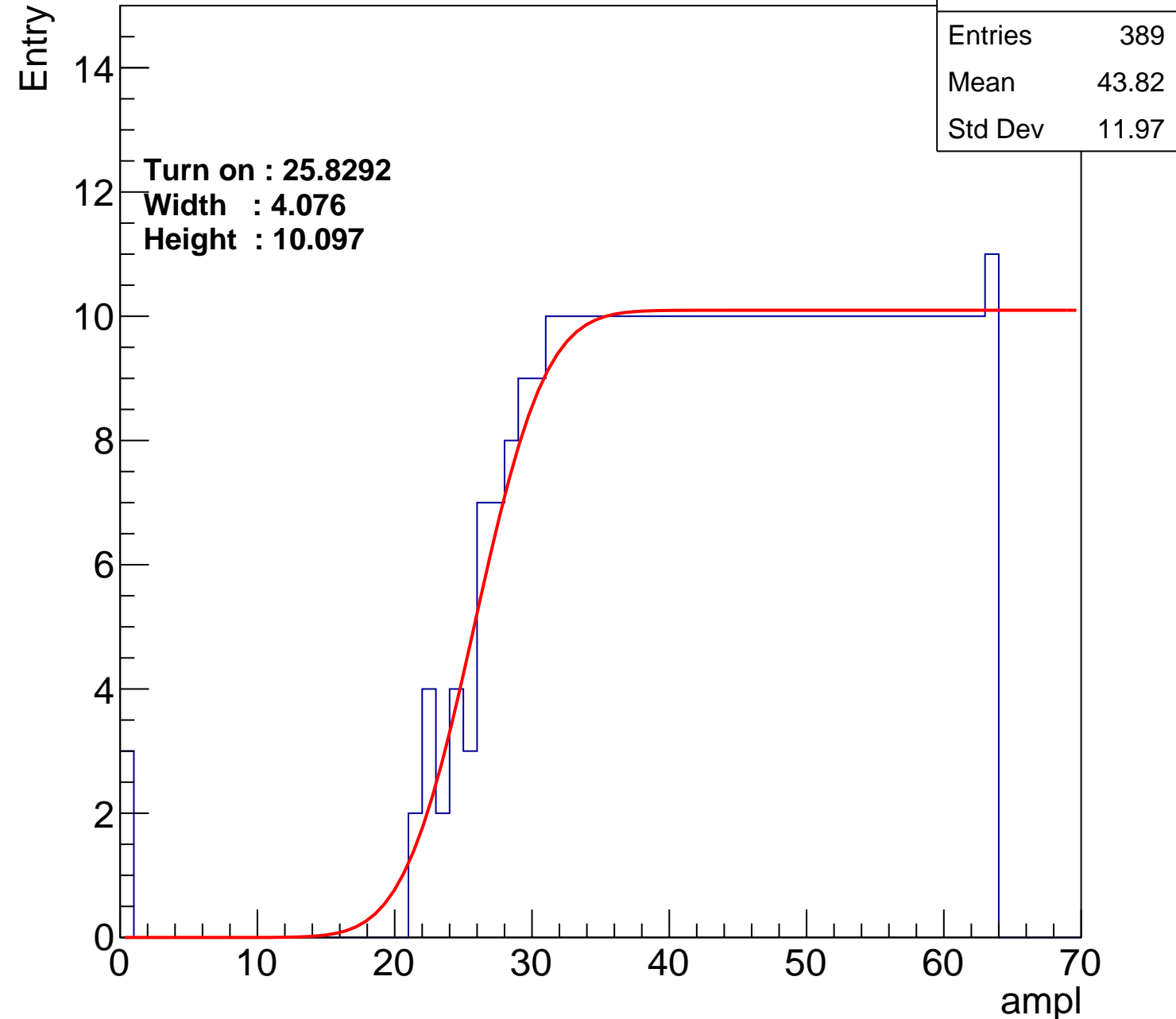
Width : 4.076

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch44

calib_packv5_042523_0143.root, FC#12, port B1

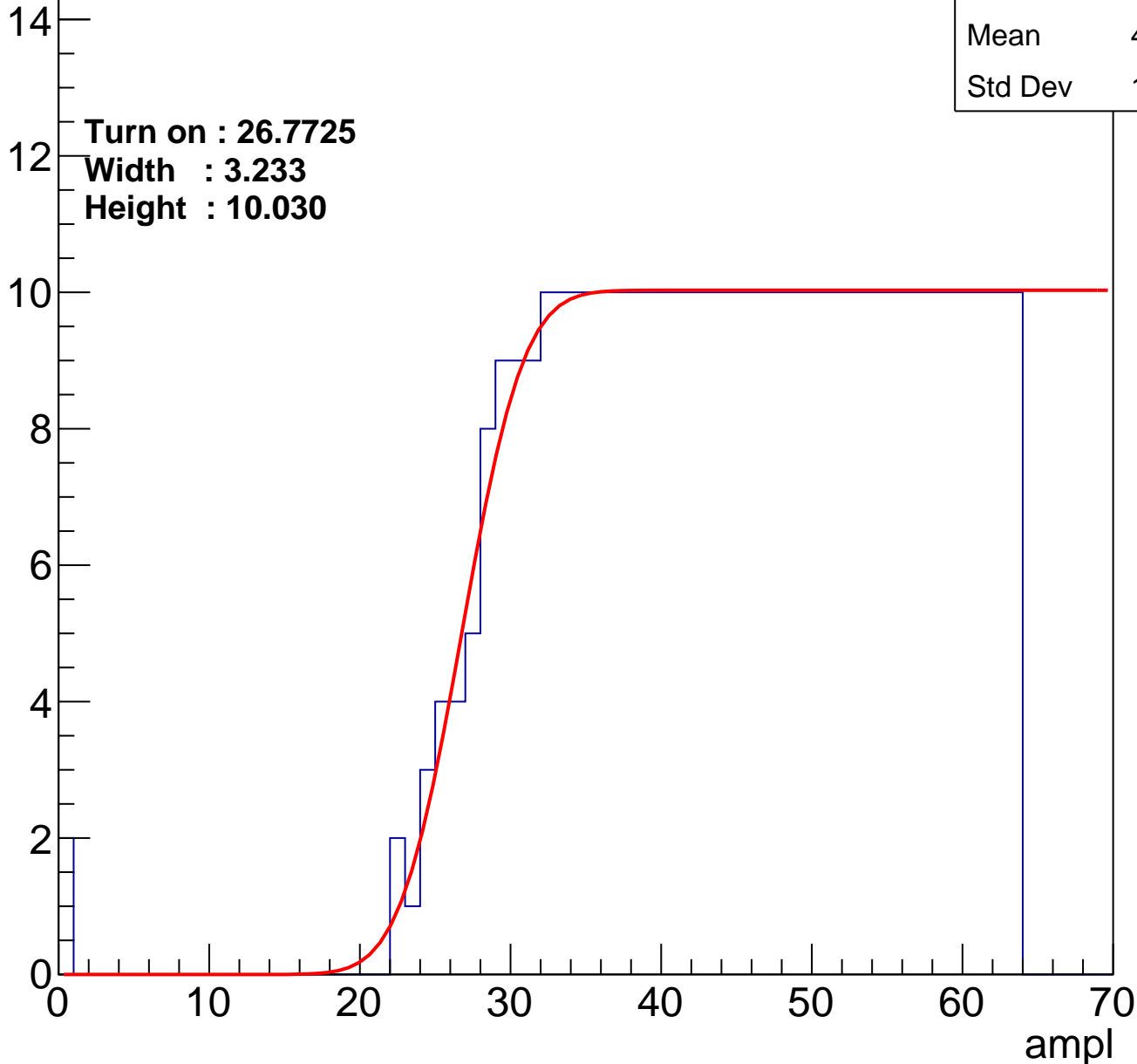
Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 26.7725

Width : 3.233

Height : 10.030

Entry



B0L102S, U24-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	353
Mean	45.4
Std Dev	11.34

Turn on : 29.3008

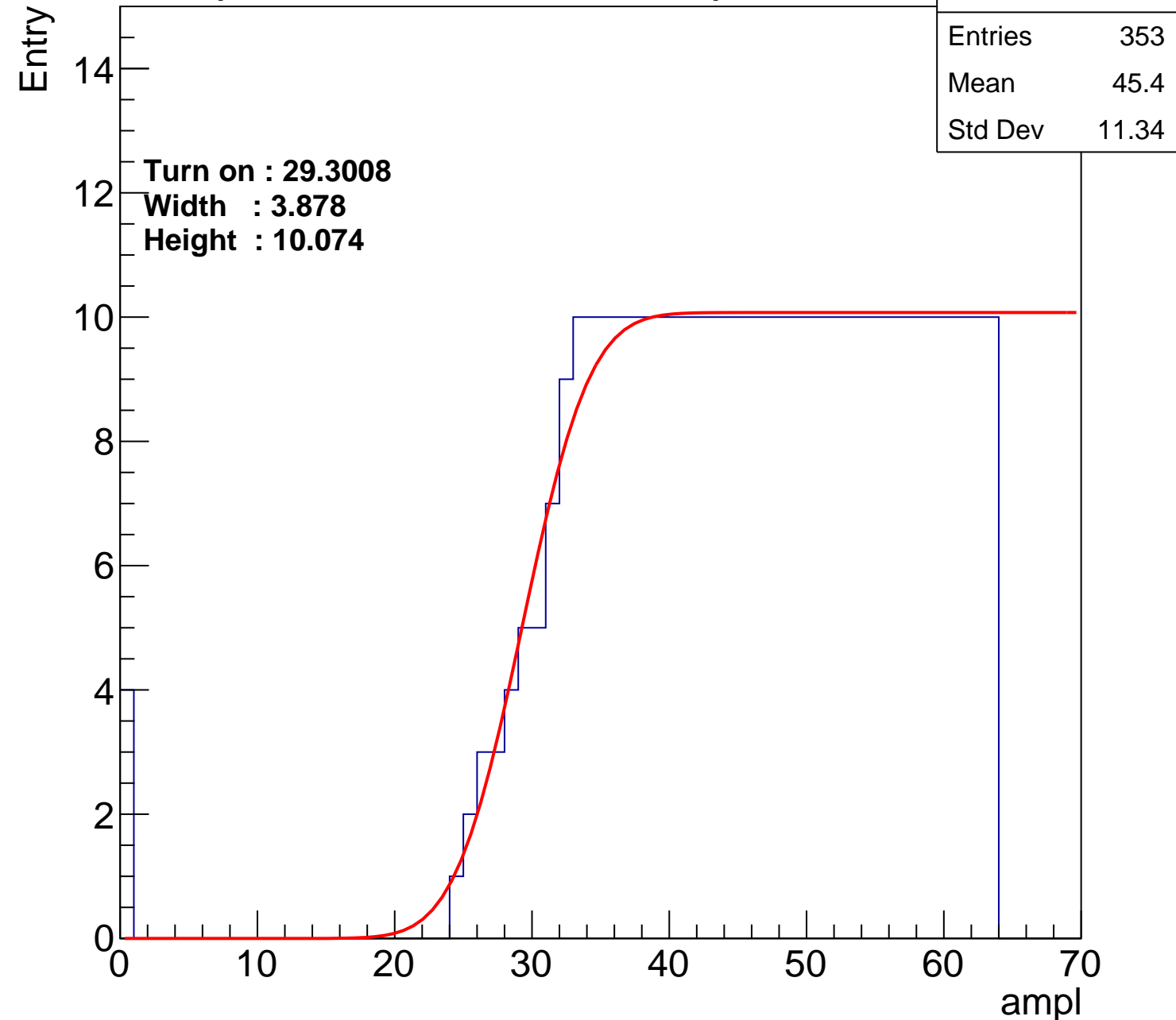
Width : 3.878

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.02
Std Dev	11.82

Turn on : 26.4865

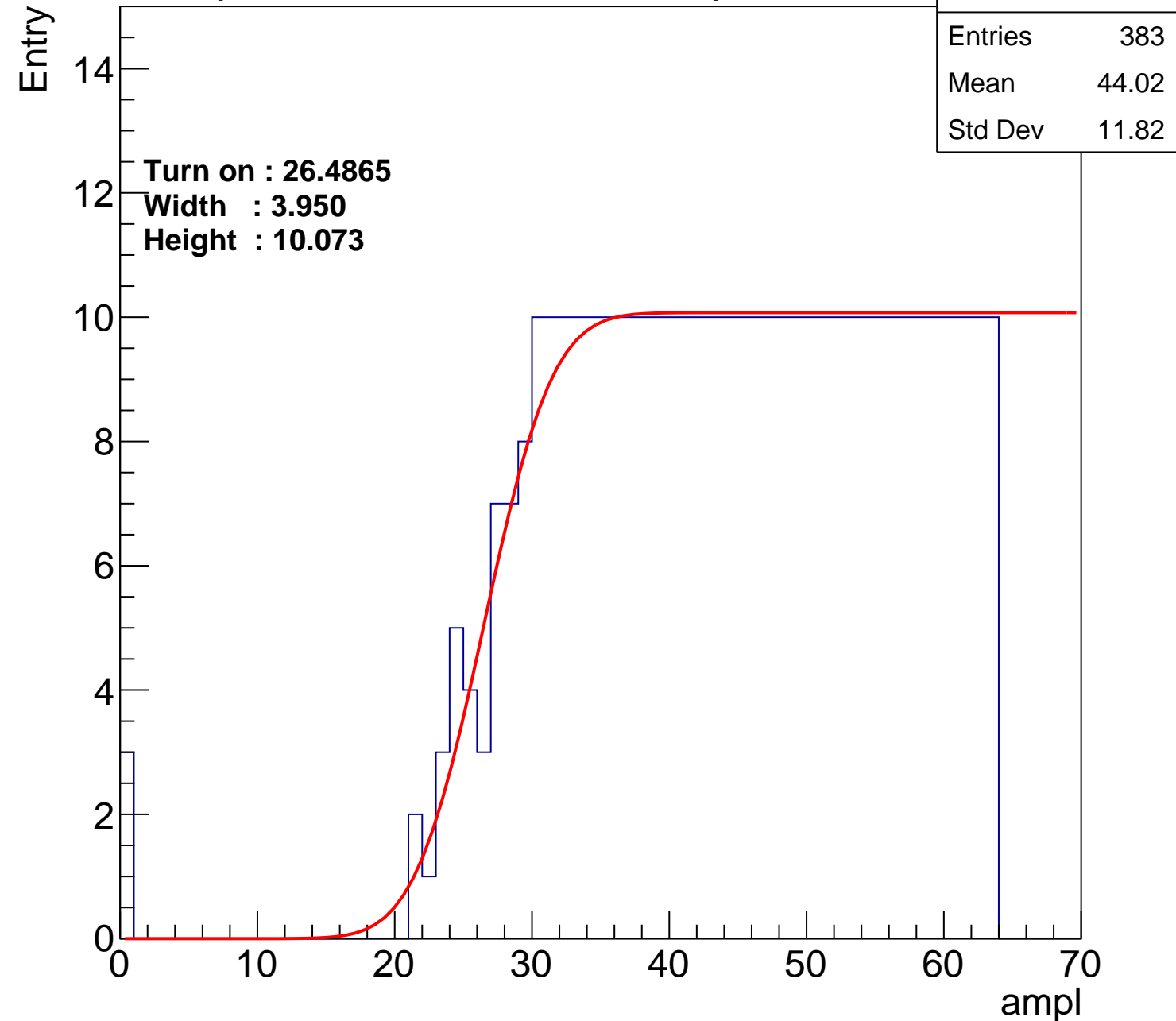
Width : 3.950

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.72
Std Dev	11.32

Turn on : 27.6348

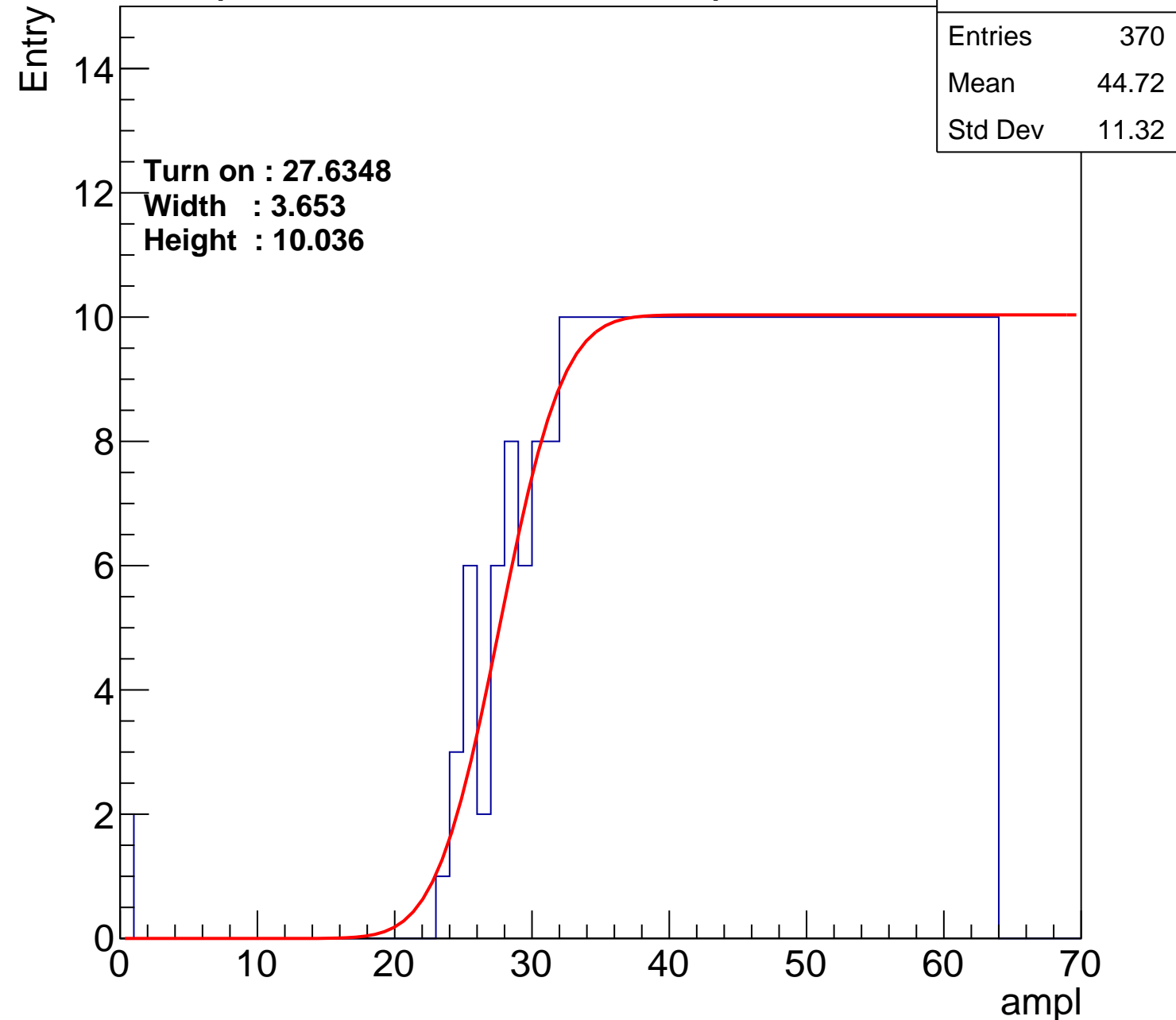
Width : 3.653

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.24
Std Dev	12.26

Turn on : 24.7692

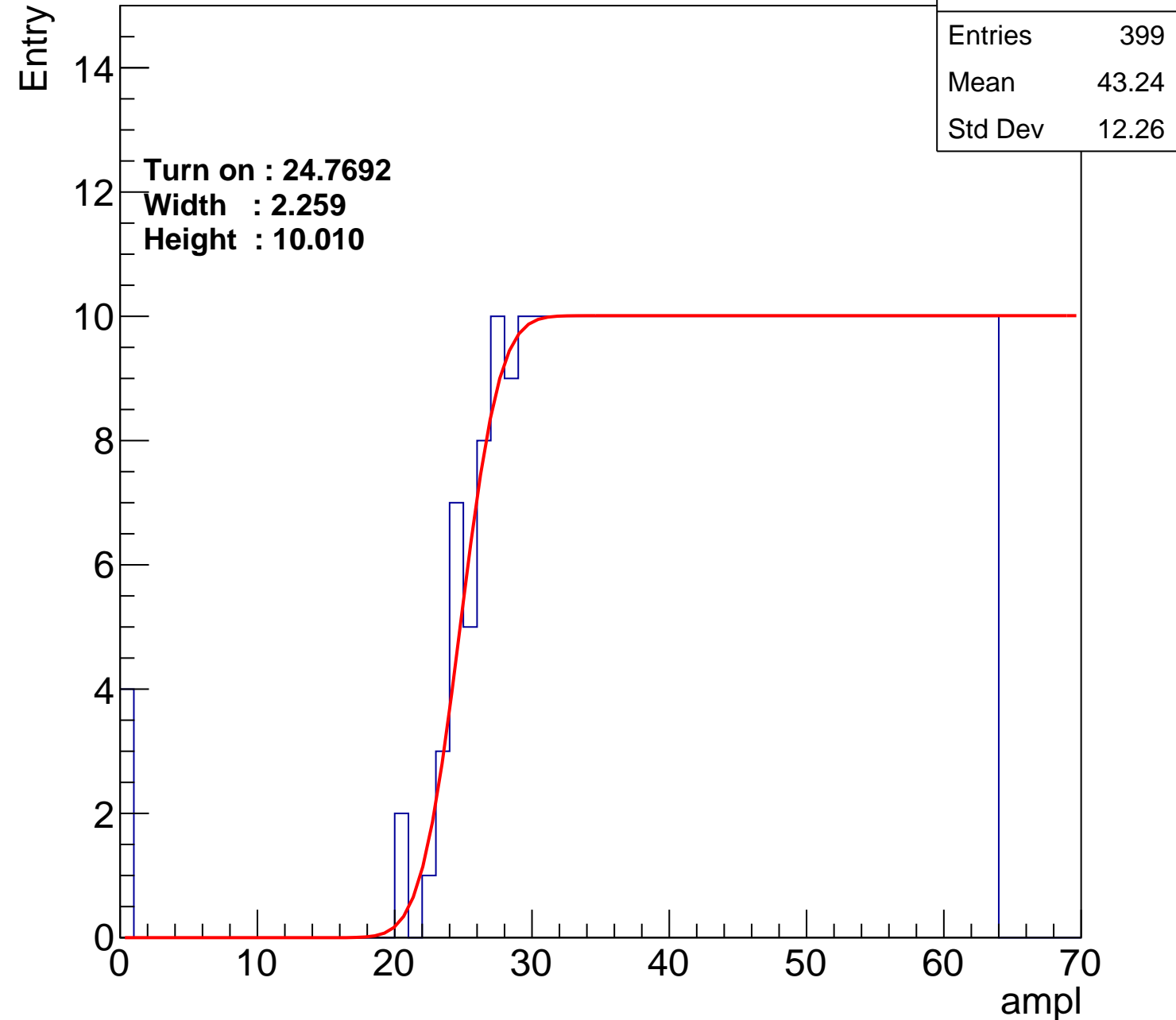
Width : 2.259

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 25.3228

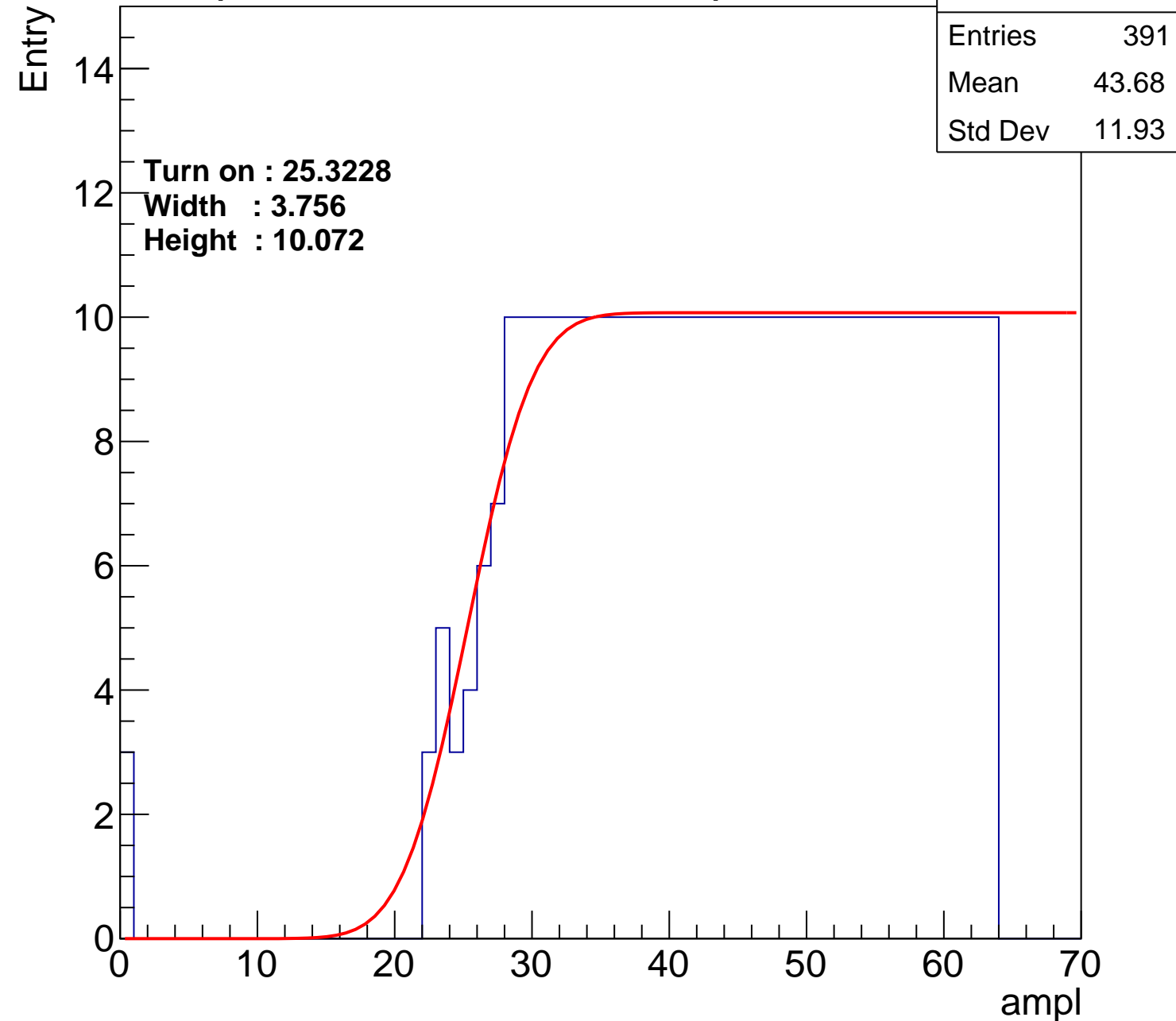
Width : 3.756

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.19
Std Dev	10.84

Turn on : 28.3475

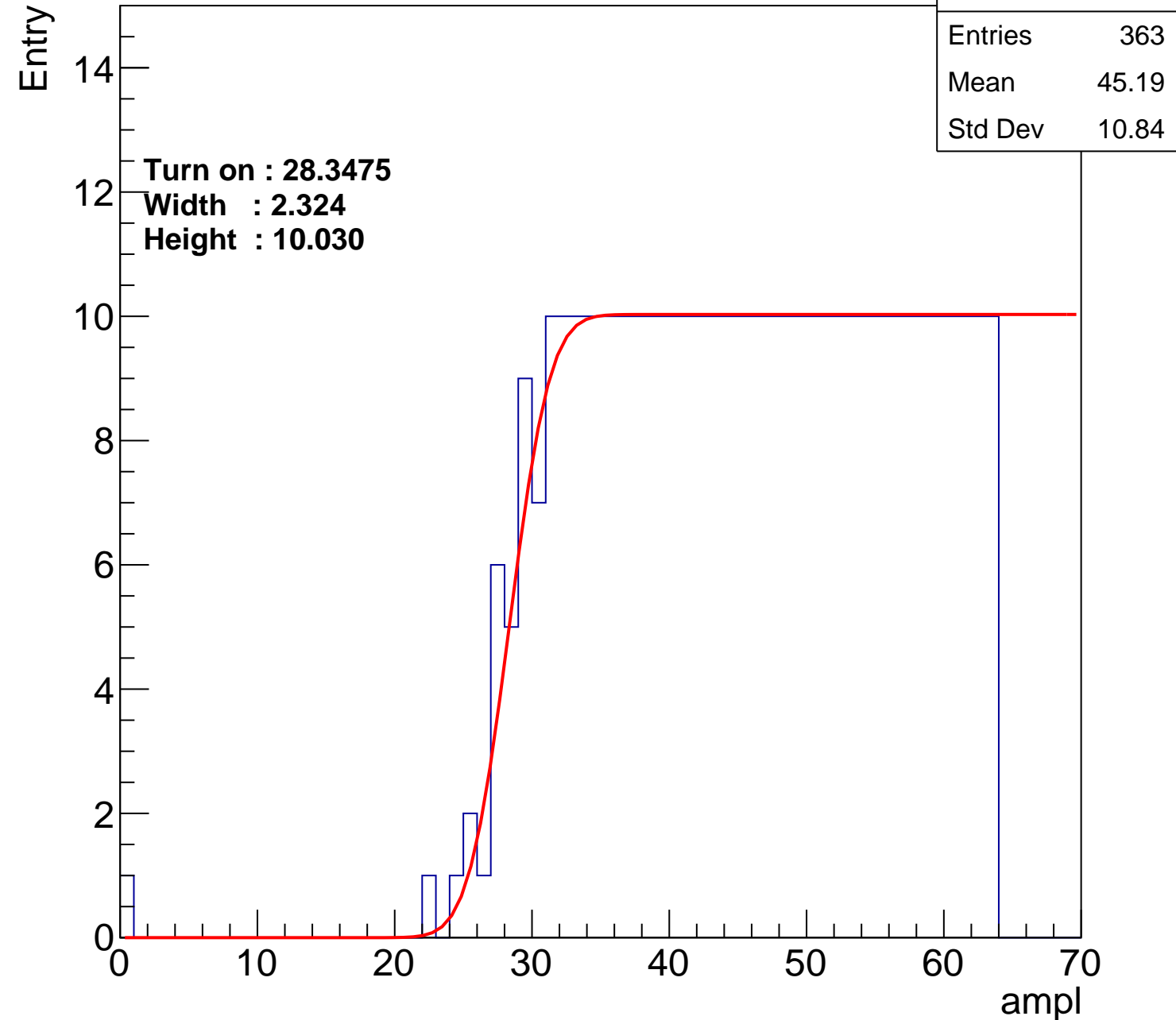
Width : 2.324

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.56
Std Dev	11.62

Turn on : 27.6601

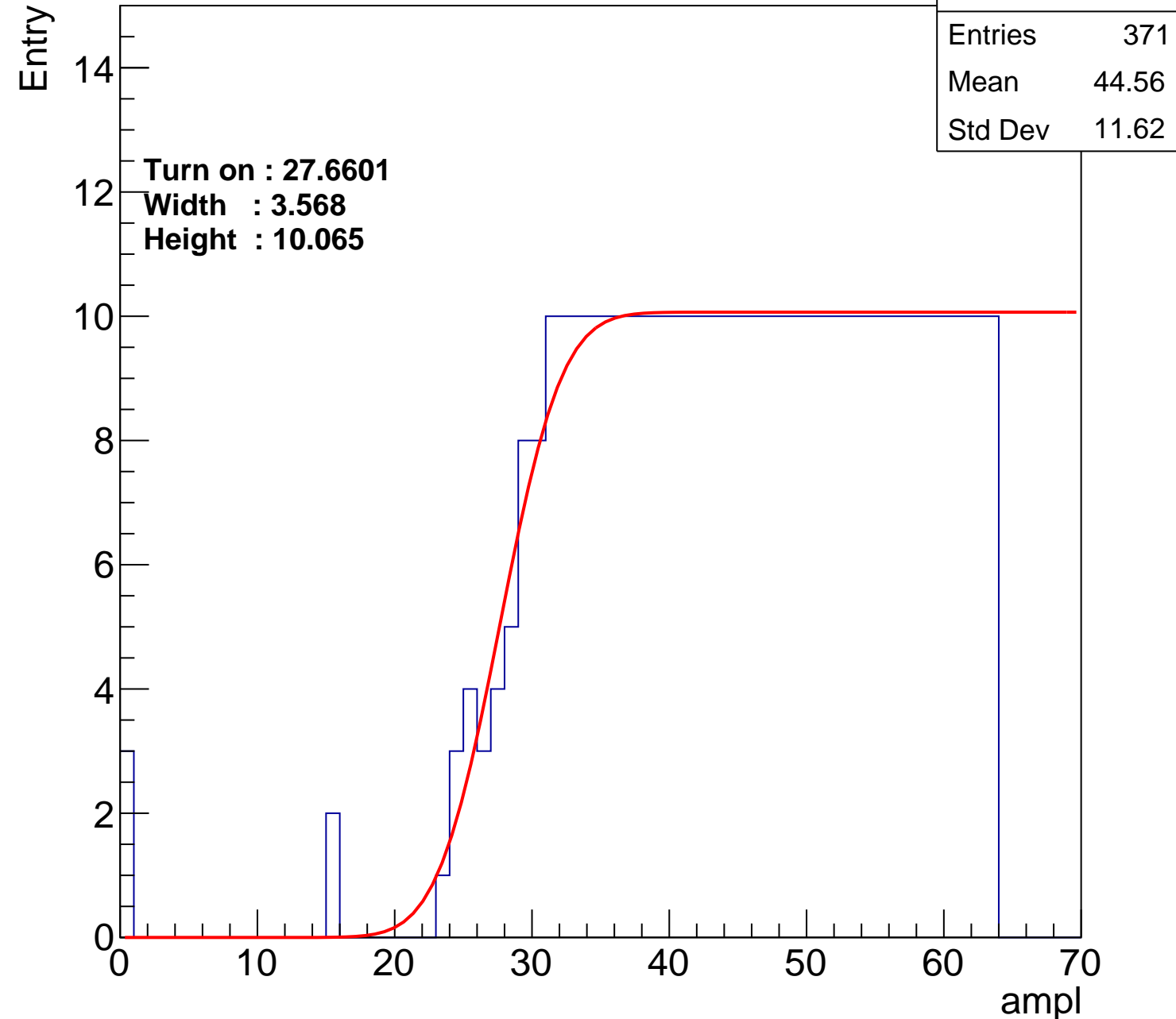
Width : 3.568

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch52

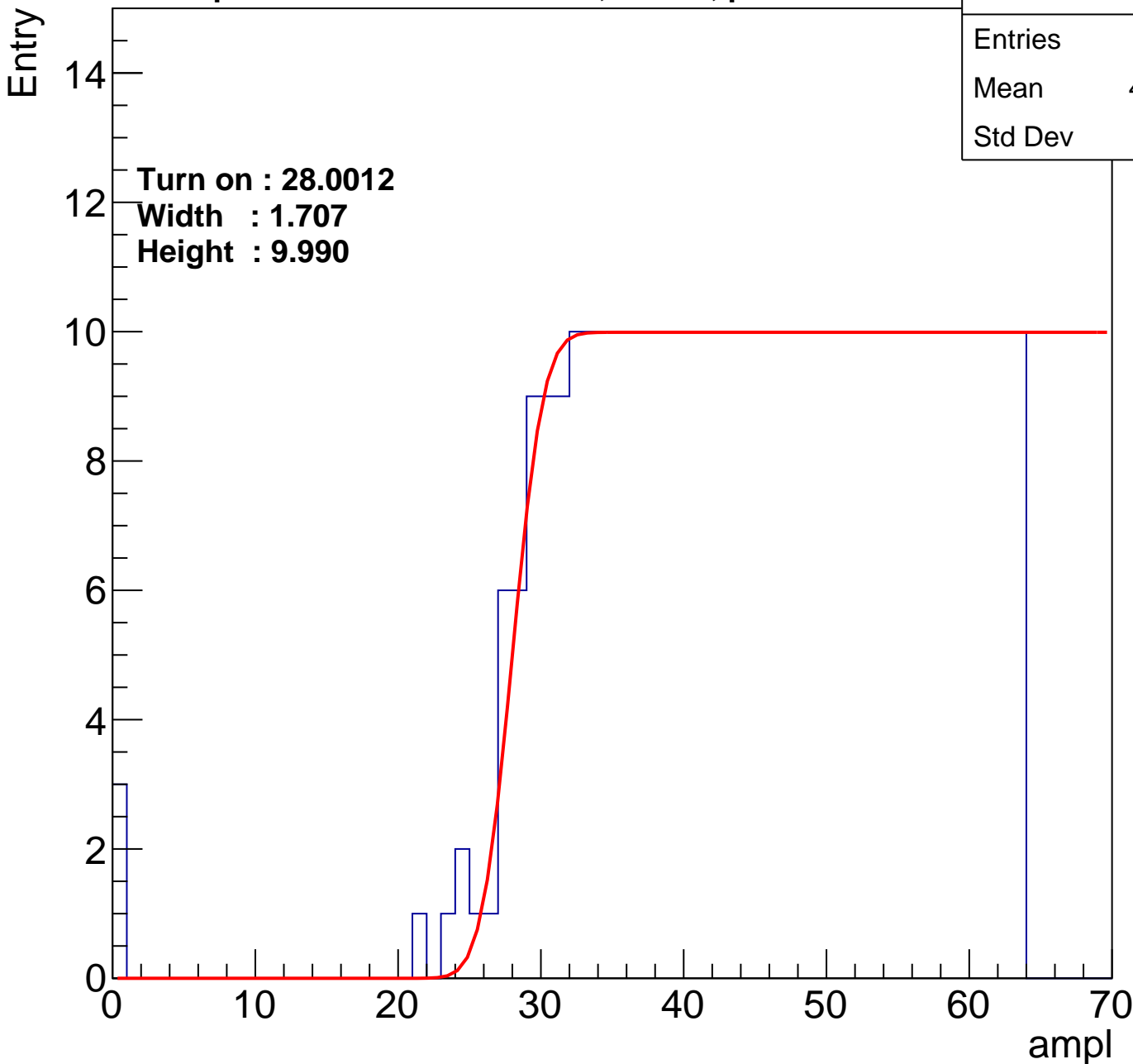
calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.79
Std Dev	11.4

Turn on : 28.0012

Width : 1.707

Height : 9.990



B0L102S, U24-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.64
Std Dev	11.19

Turn on : 26.5393

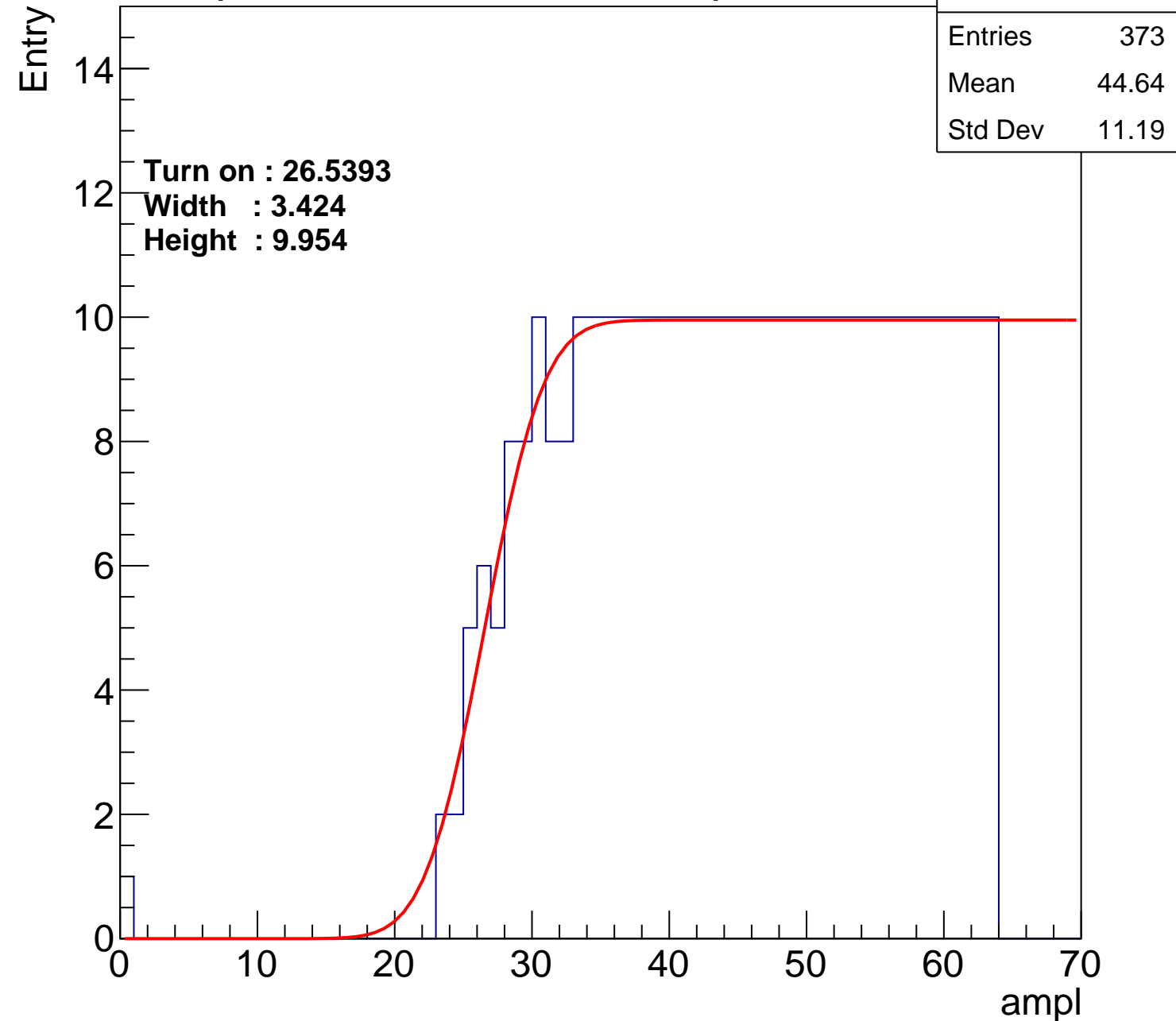
Width : 3.424

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch54

calib_packv5_042523_0143.root, FC#12, port B1

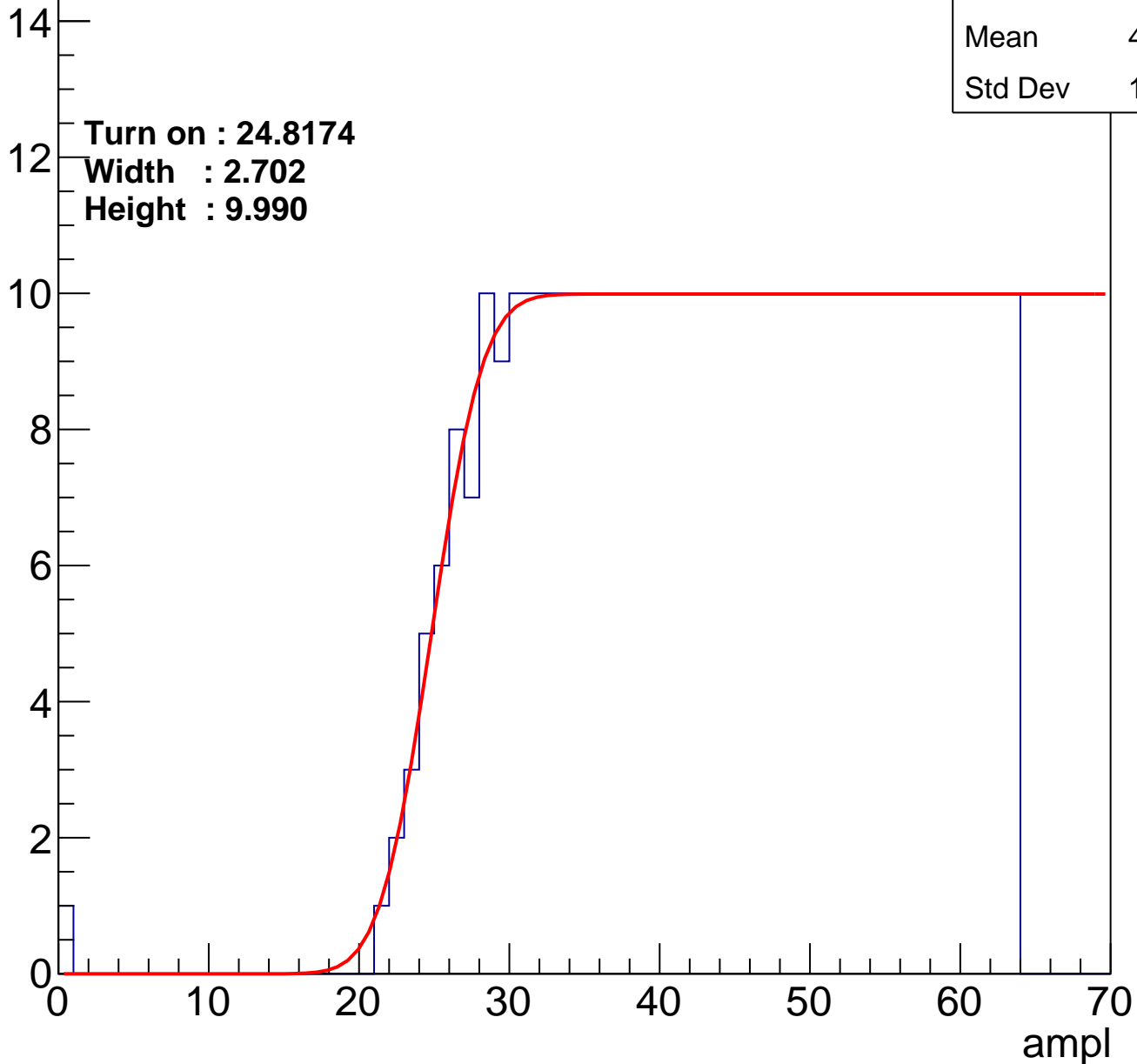
Entries	392
Mean	43.76
Std Dev	11.62

Turn on : 24.8174

Width : 2.702

Height : 9.990

Entry



B0L102S, U24-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.5249

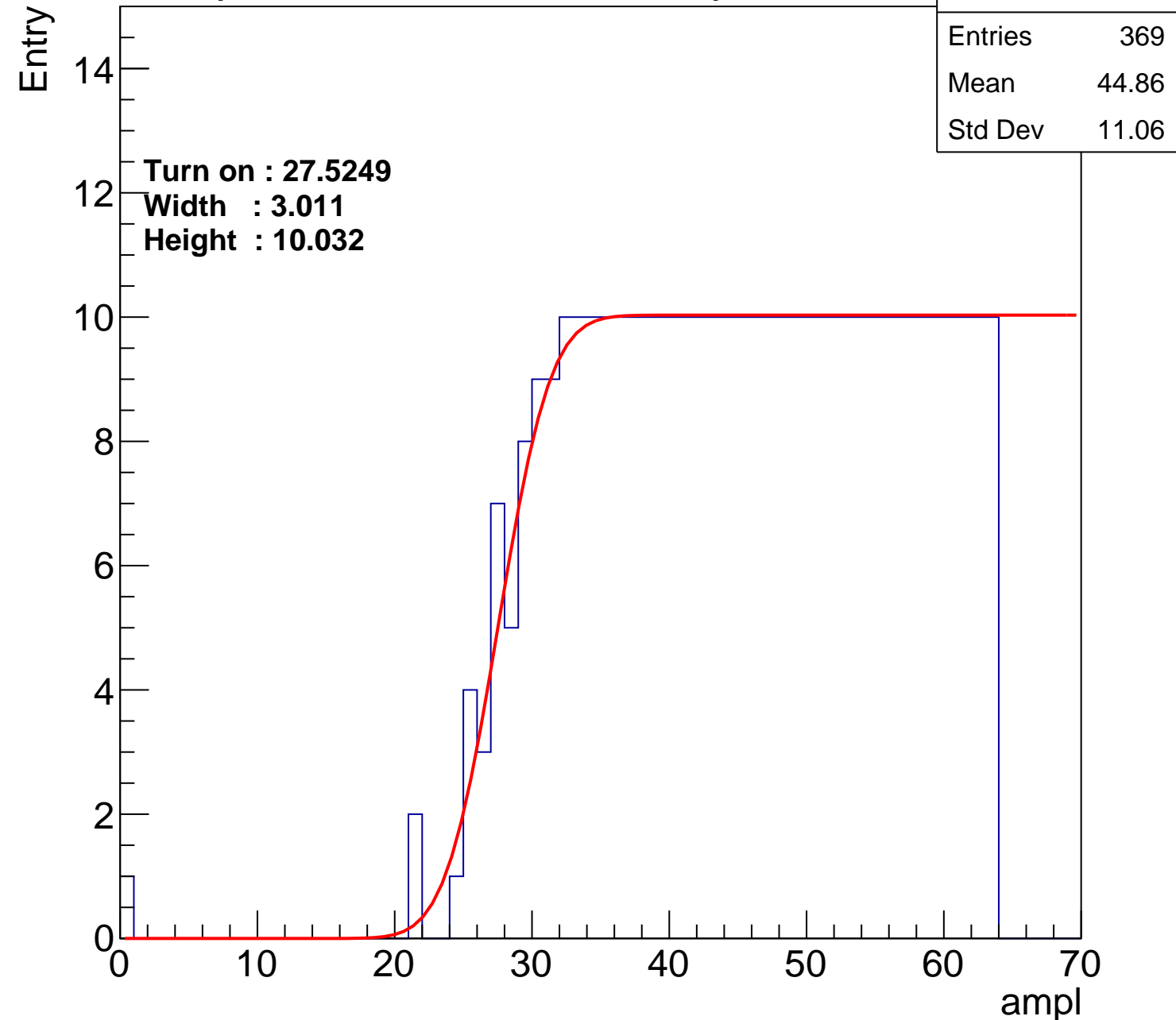
Width : 3.011

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch56

calib_packv5_042523_0143.root, FC#12, port B1

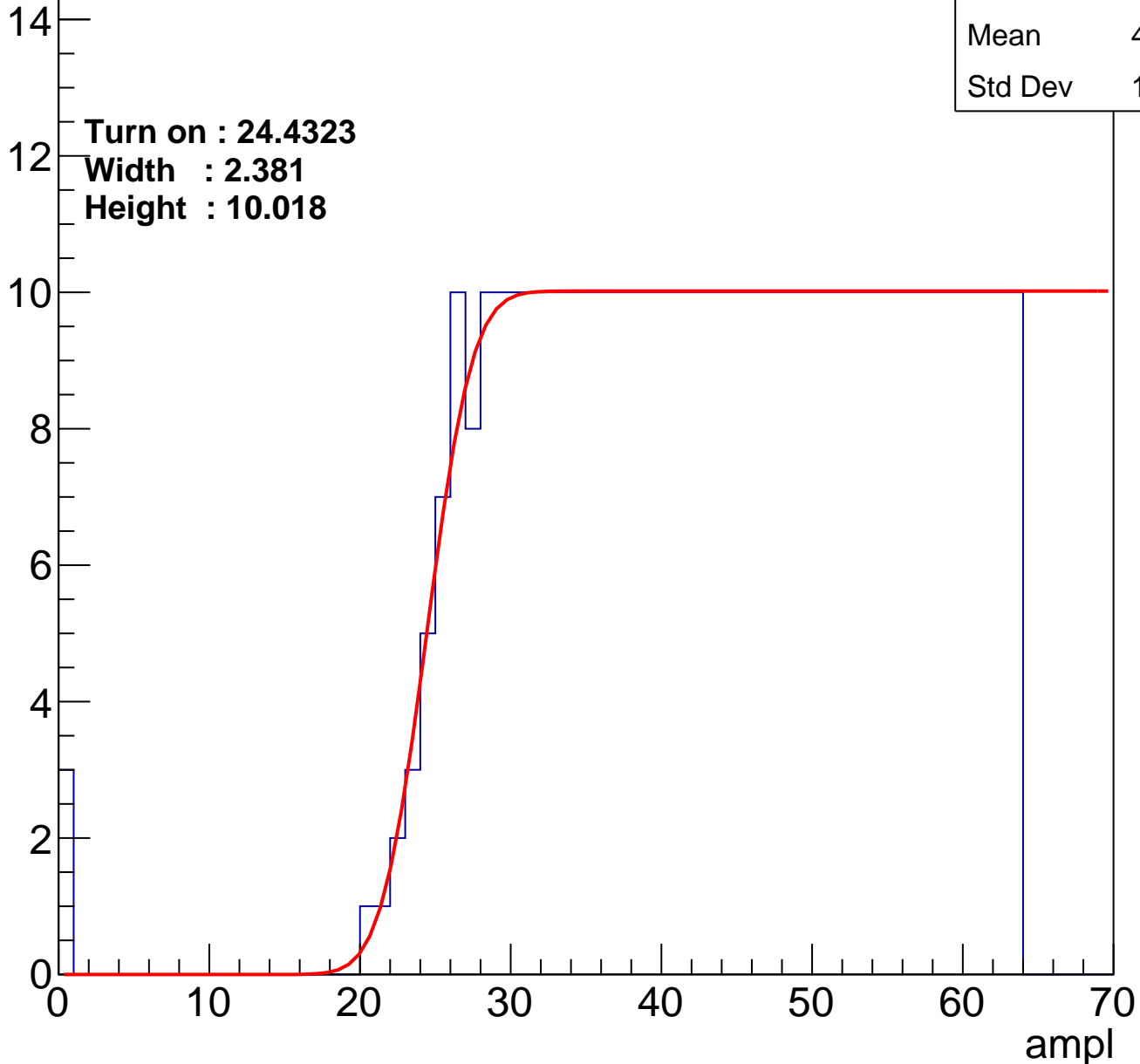
Entries	400
Mean	43.26
Std Dev	12.12

Turn on : 24.4323

Width : 2.381

Height : 10.018

Entry



B0L102S, U24-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.74
Std Dev	11.08

Turn on : 26.1953

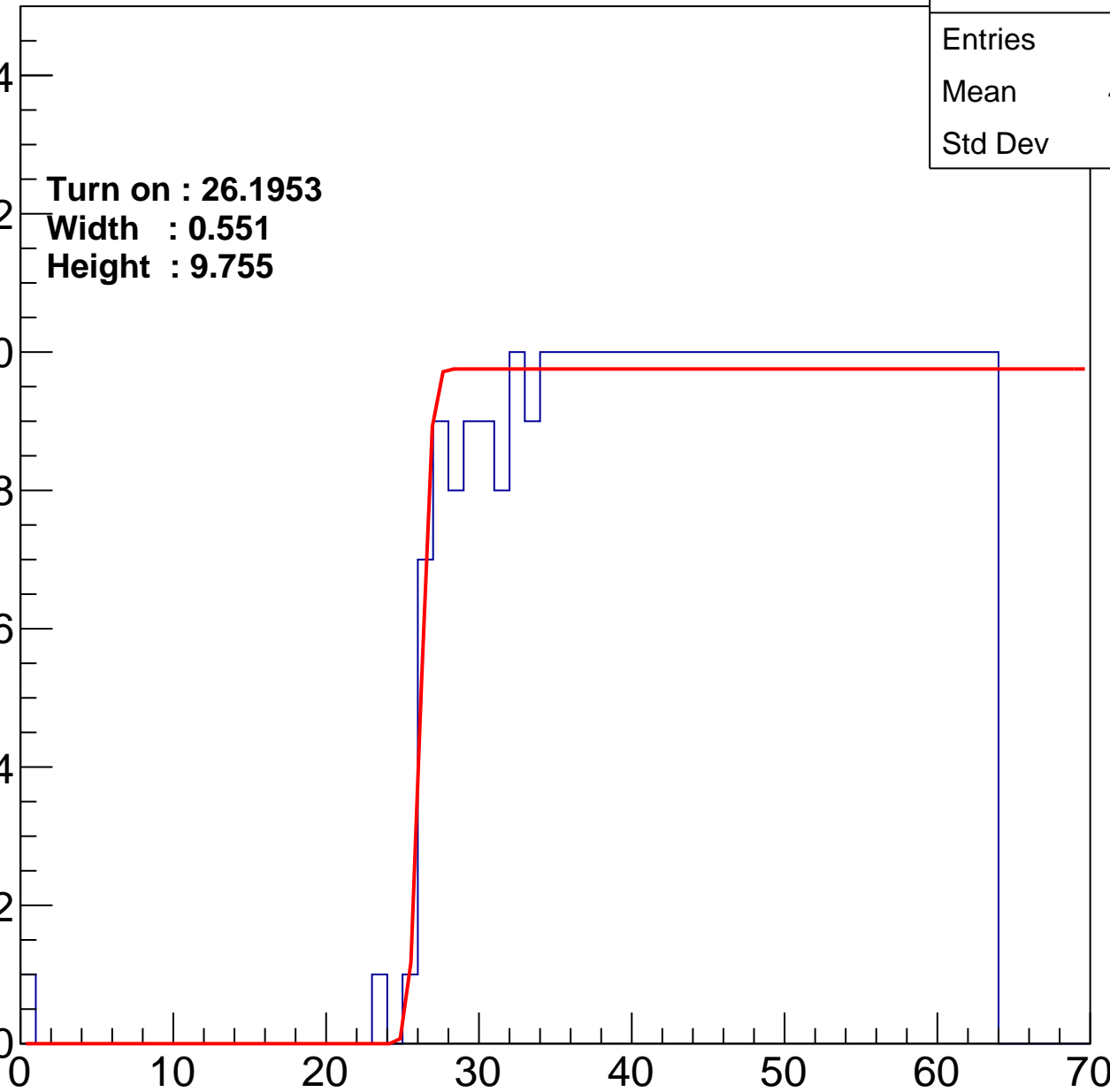
Width : 0.551

Height : 9.755

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.42
Std Dev	12.24

Turn on : 24.9222

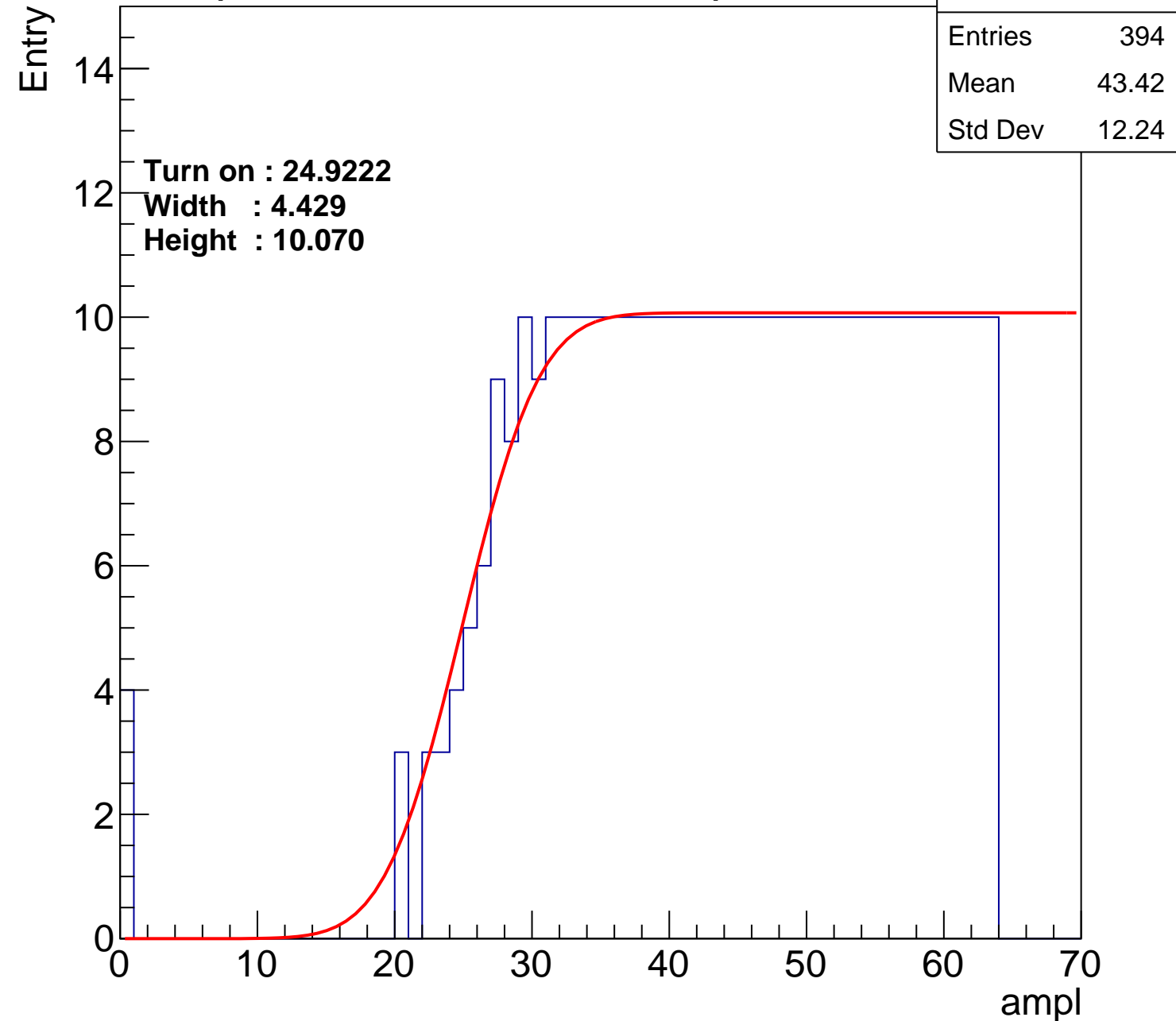
Width : 4.429

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.12
Std Dev	11.45

Turn on : 25.6203

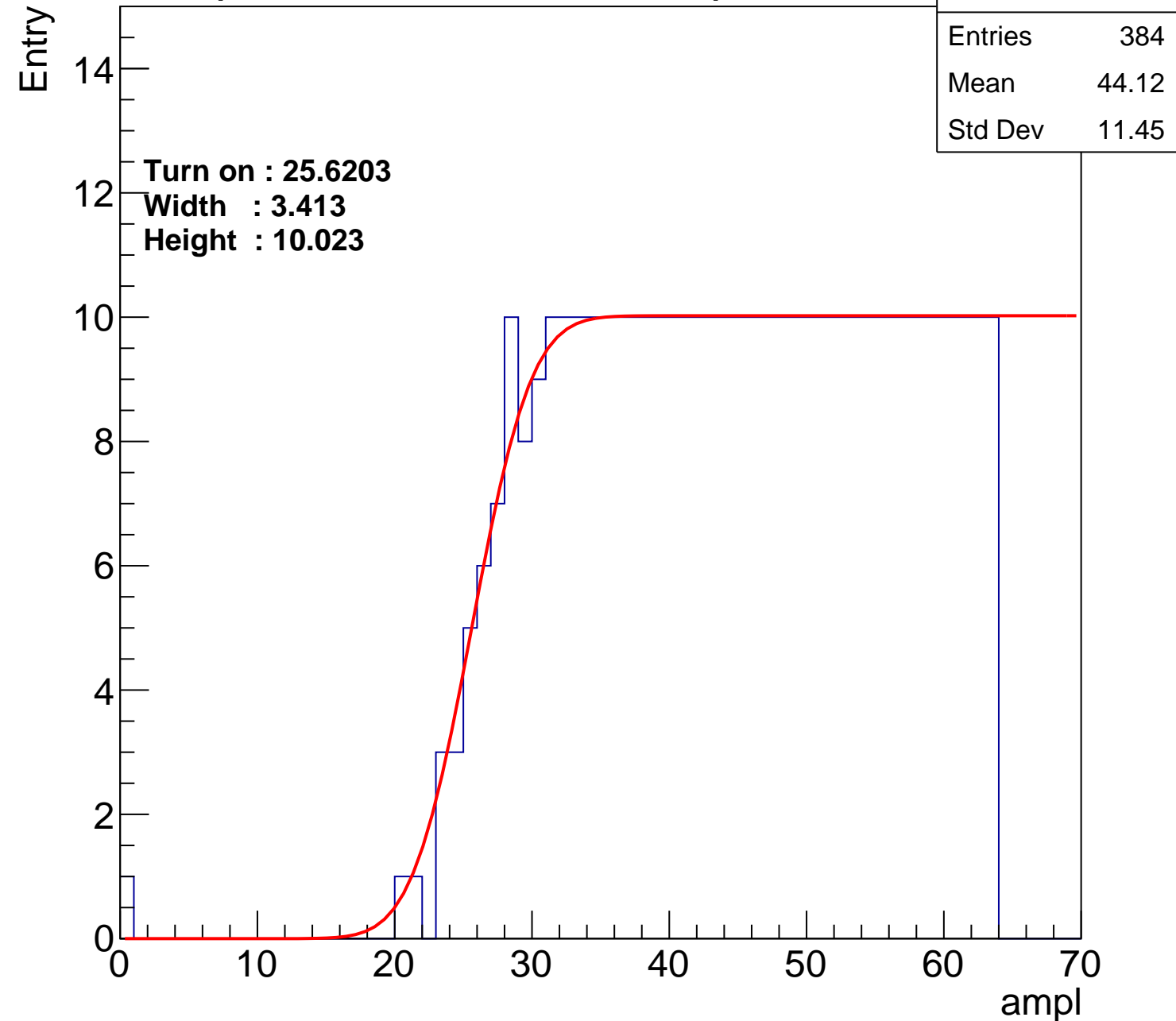
Width : 3.413

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch60

calib_packv5_042523_0143.root, FC#12, port B1

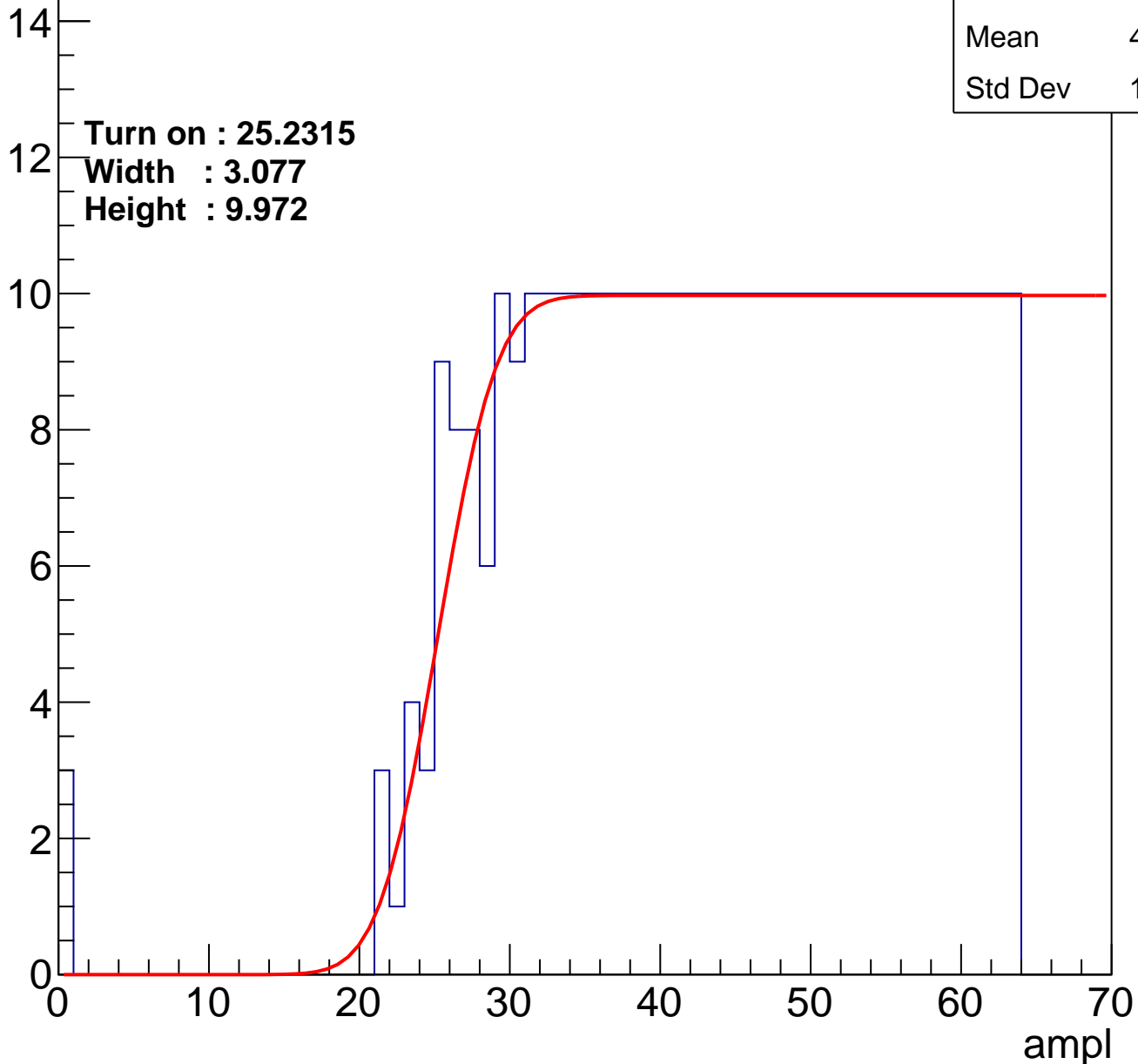
Entries	394
Mean	43.49
Std Dev	12.06

Turn on : 25.2315

Width : 3.077

Height : 9.972

Entry



B0L102S, U24-ch61

calib_packv5_042523_0143.root, FC#12, port B1

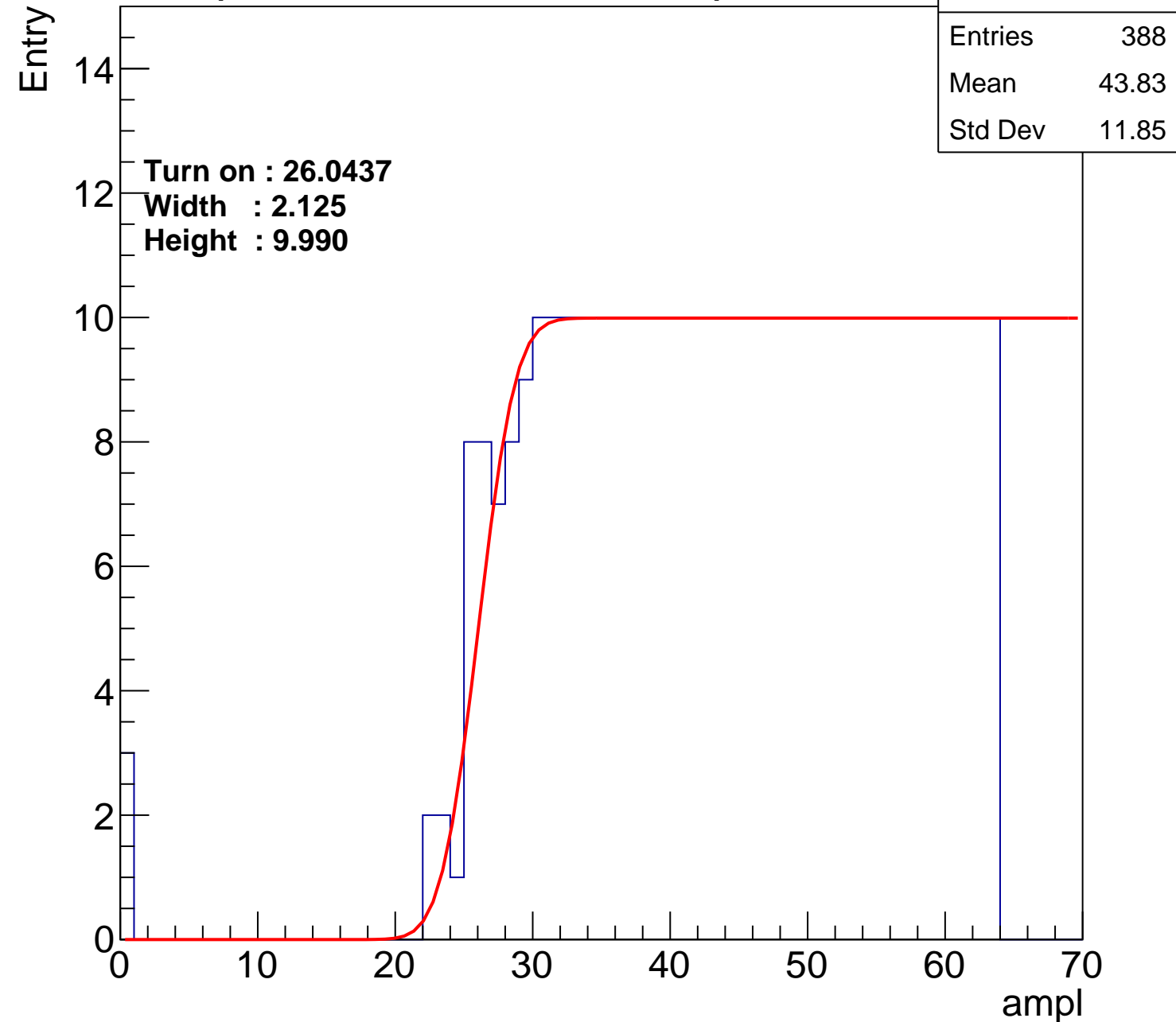
Entry

14
12
10
8
6
4
2
0

Turn on : 26.0437
Width : 2.125
Height : 9.990

Entries	388
Mean	43.83
Std Dev	11.85

ampl



B0L102S, U24-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.2390

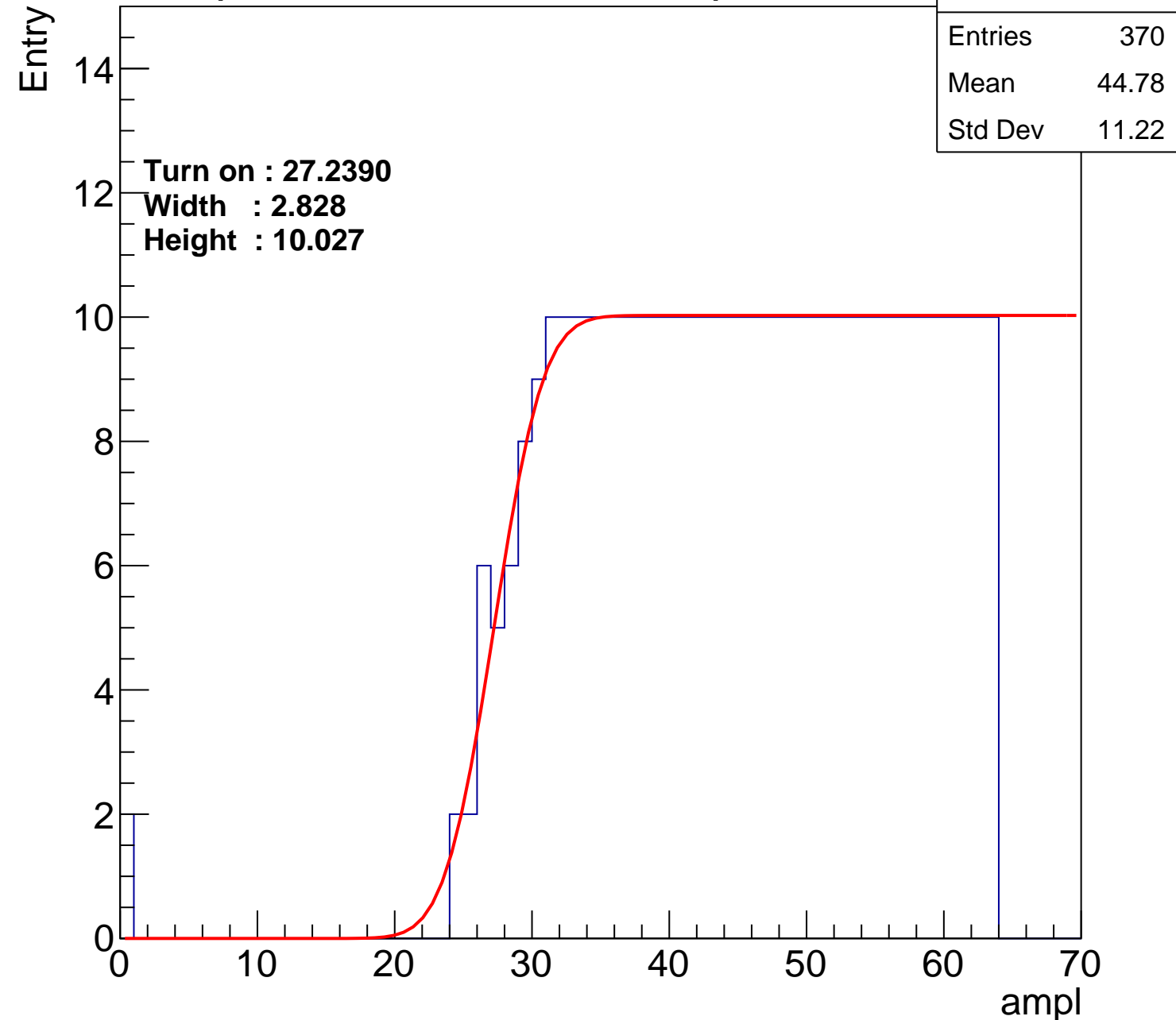
Width : 2.828

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.87
Std Dev	11.04

Turn on : 26.9340

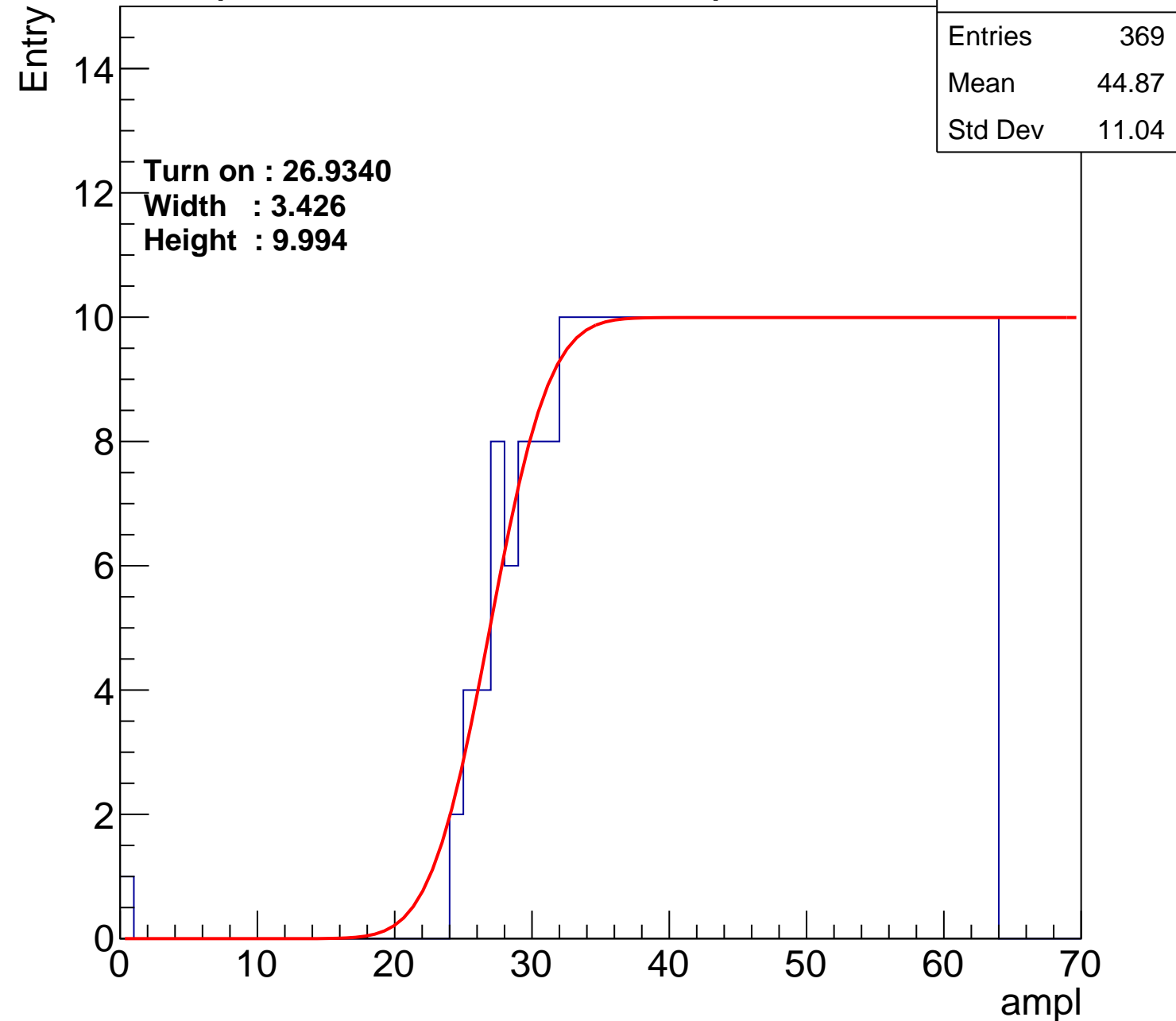
Width : 3.426

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 27.0480

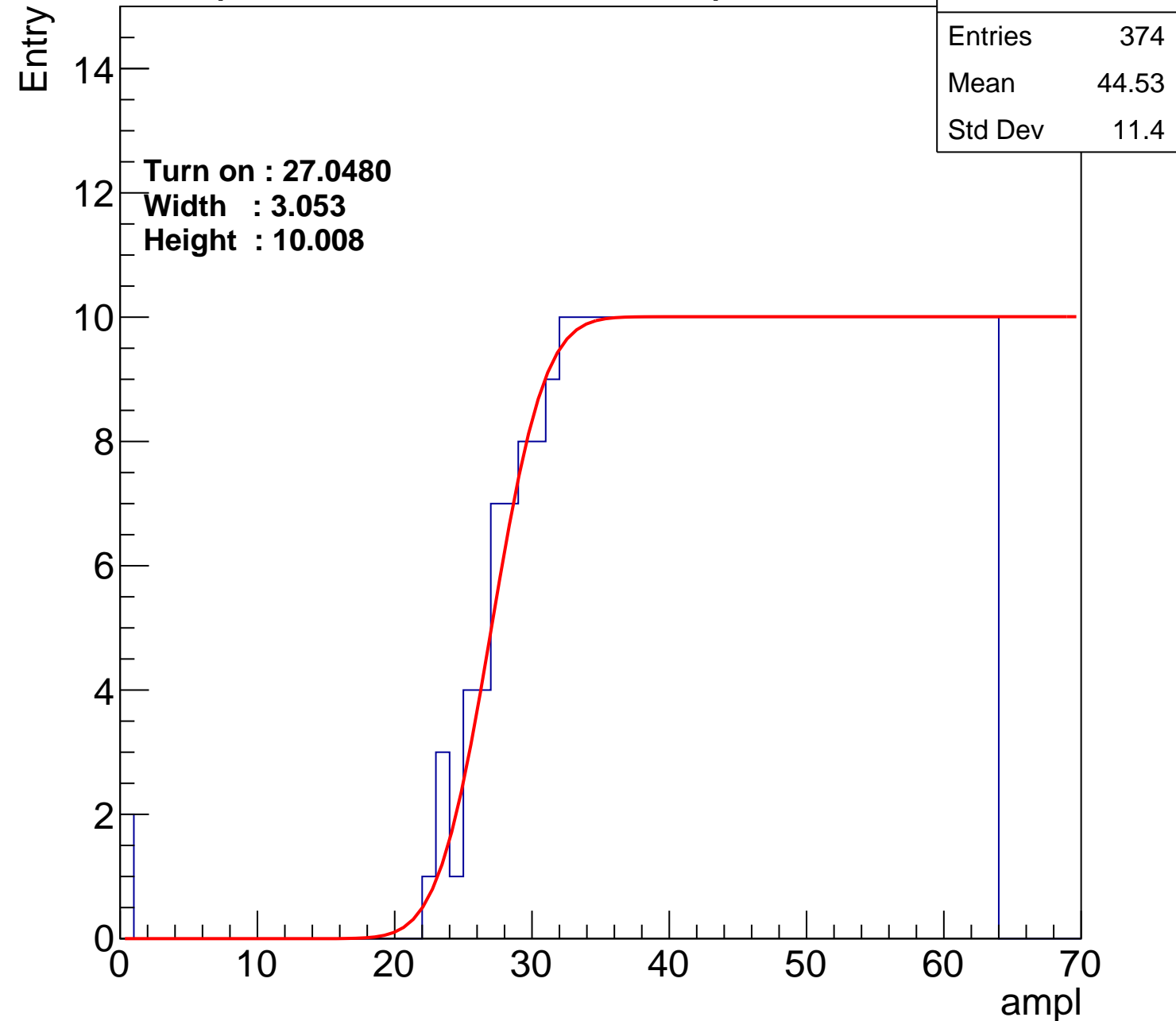
Width : 3.053

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 27.9721

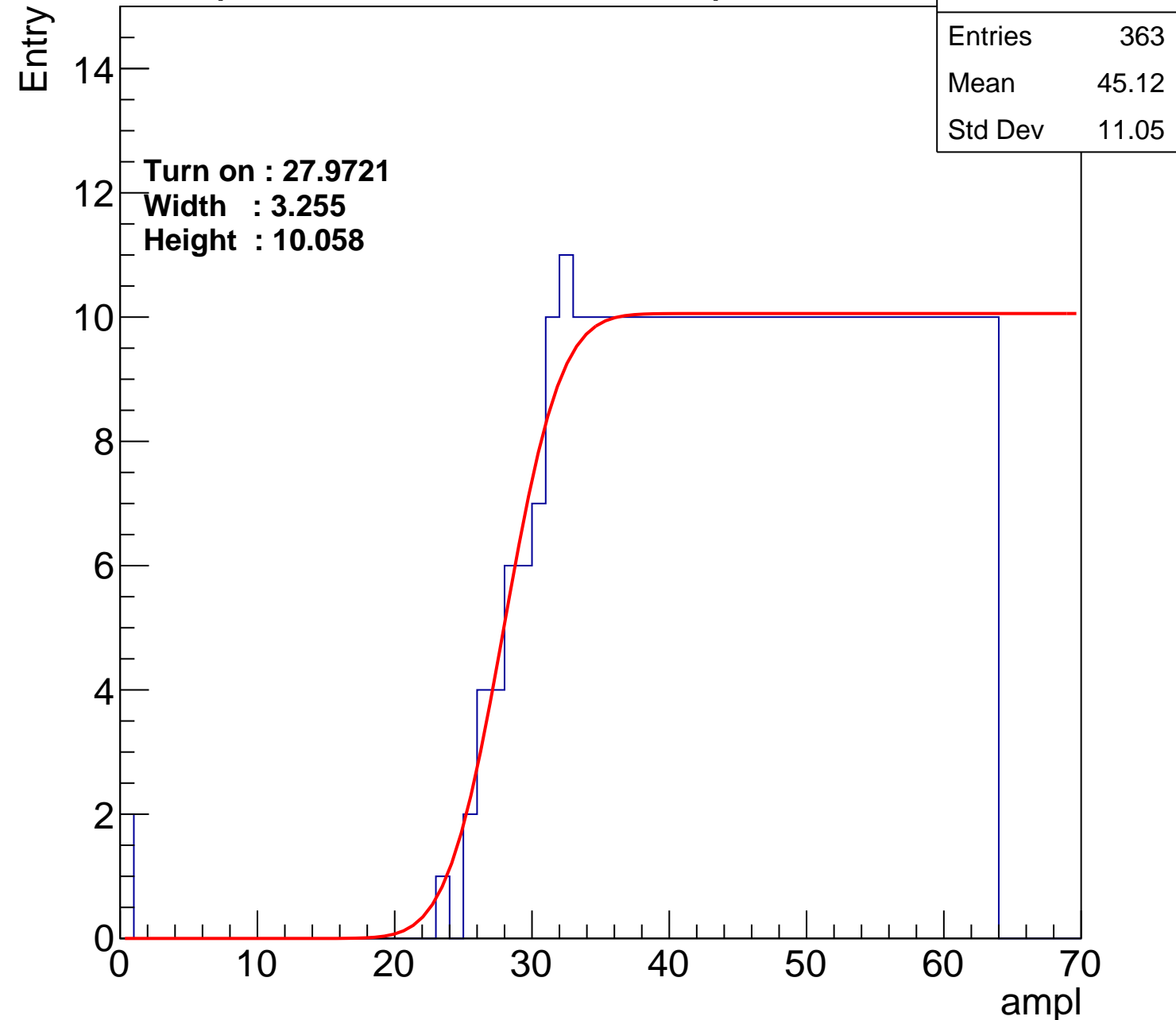
Width : 3.255

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.13
Std Dev	11.04

Turn on : 27.8937

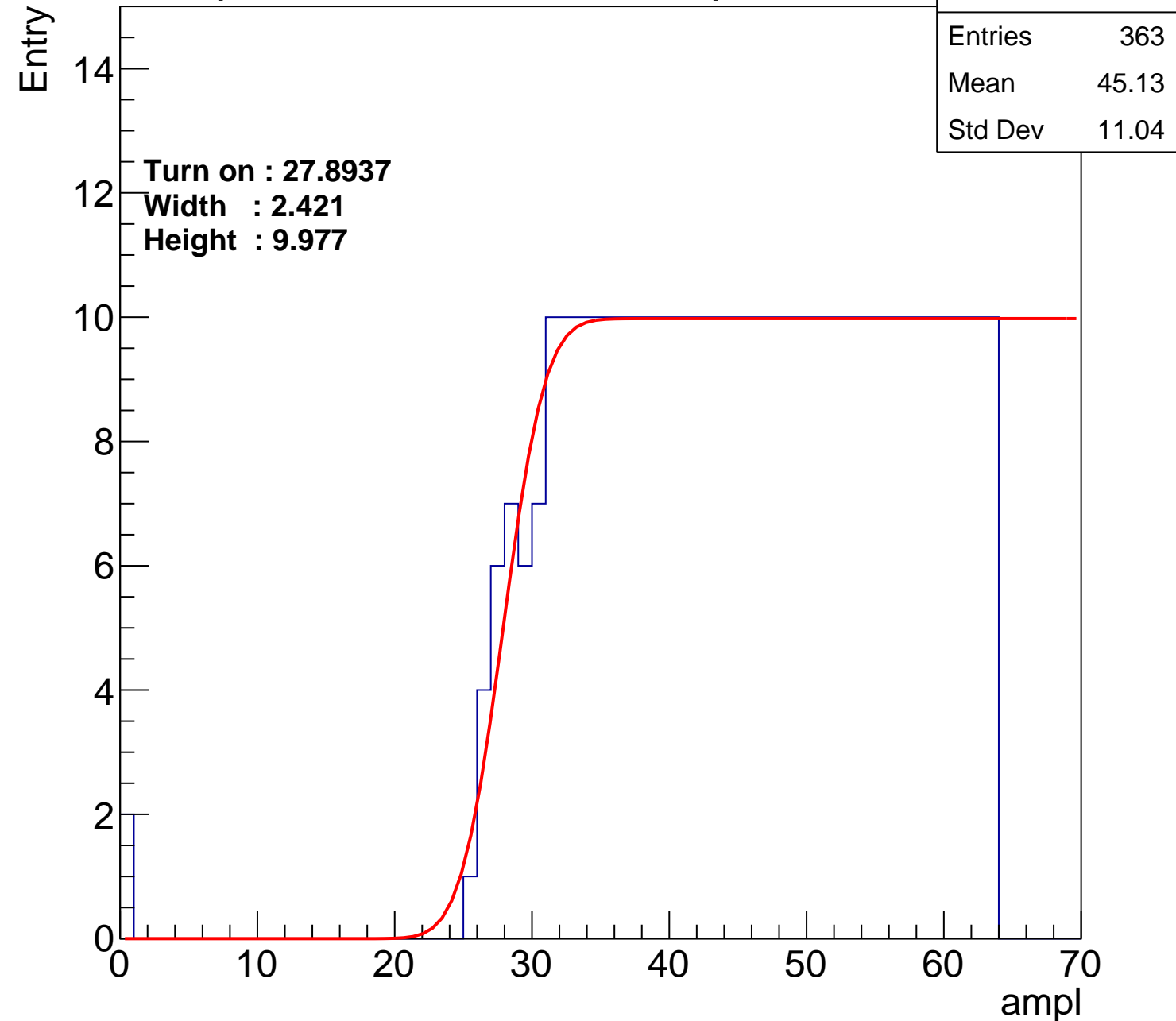
Width : 2.421

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.34
Std Dev	11.64

Turn on : 26.6301

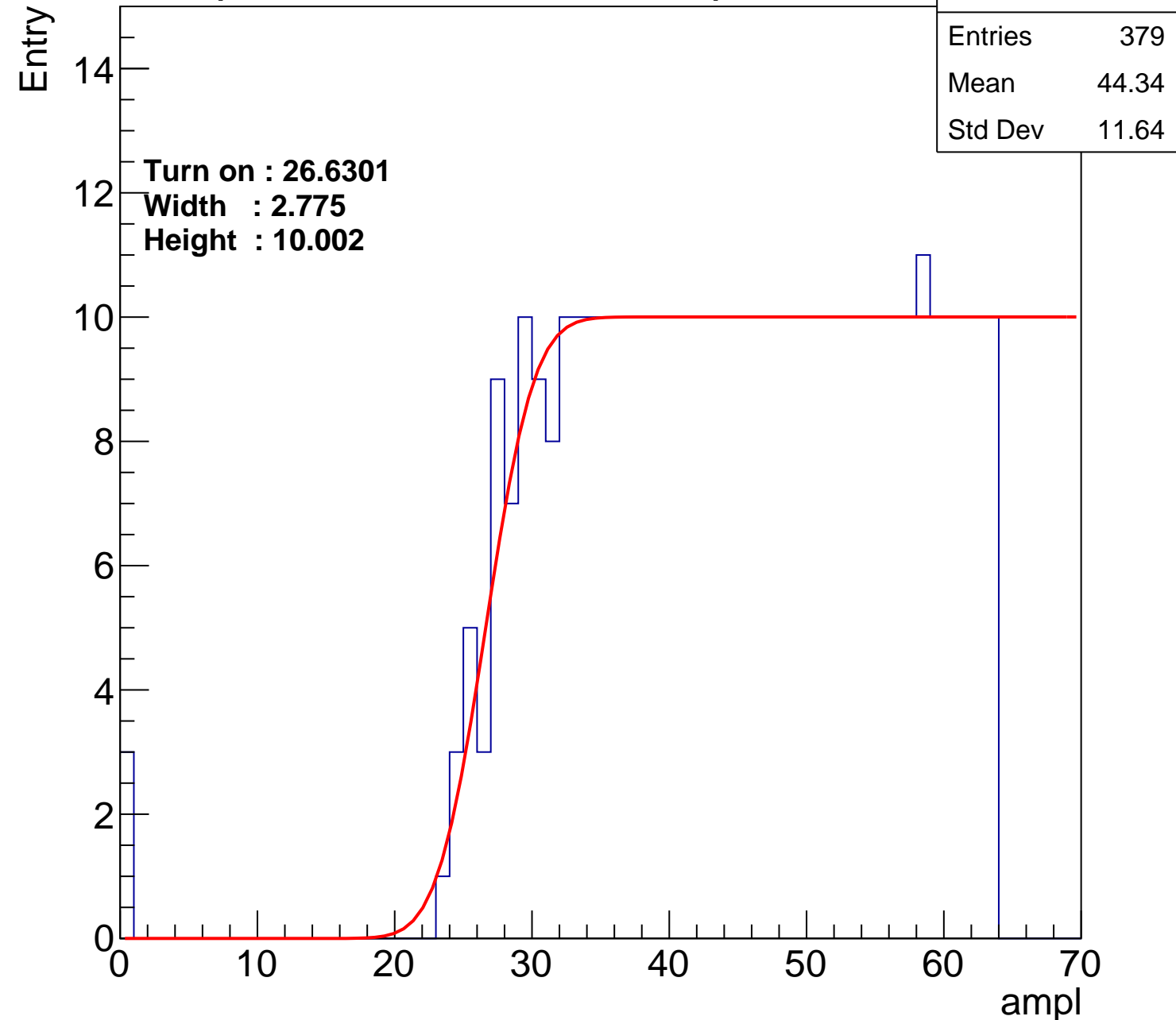
Width : 2.775

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.34
Std Dev	11.75

Turn on : 26.3154

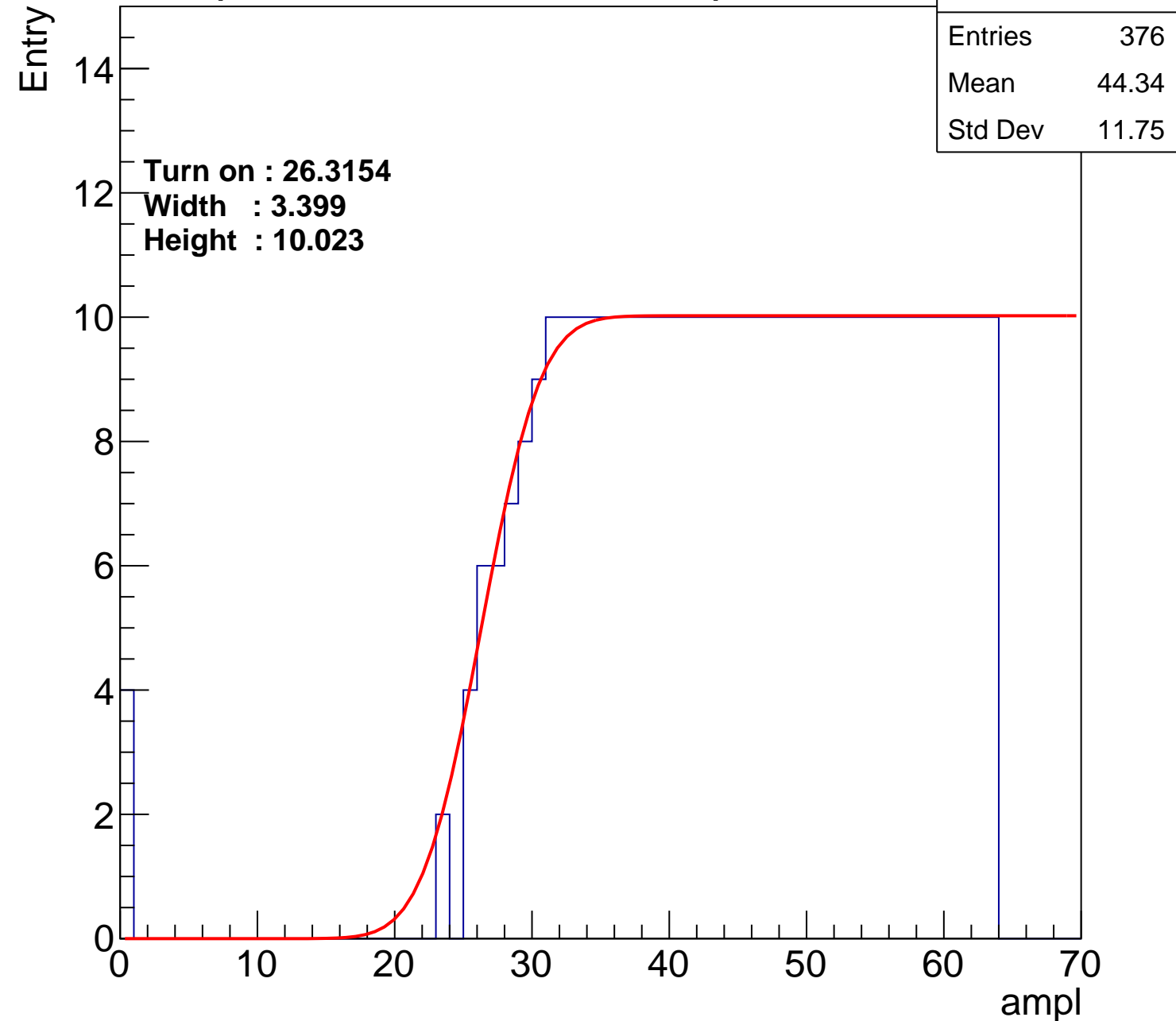
Width : 3.399

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.69
Std Dev	11.32

Turn on : 28.0245

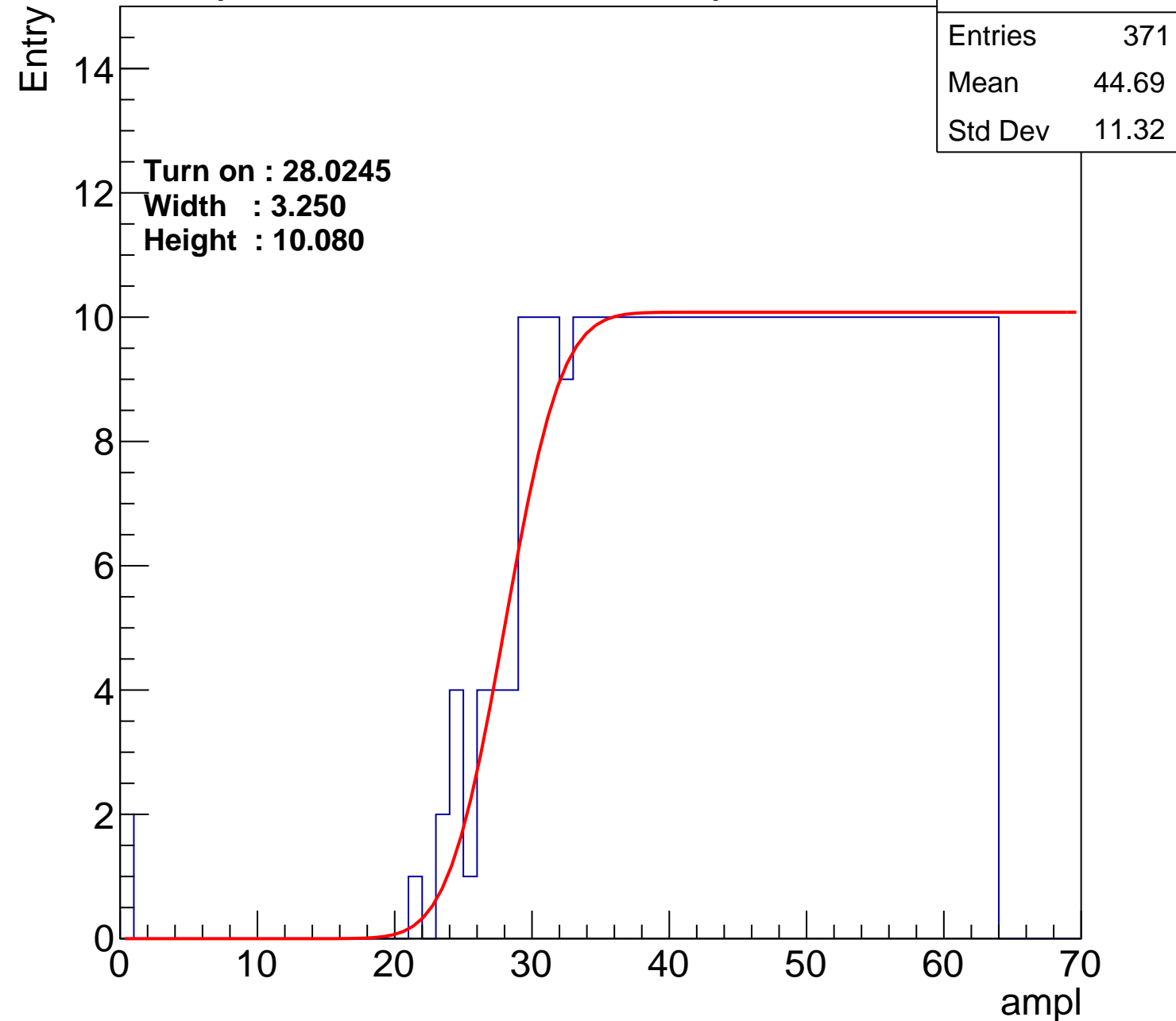
Width : 3.250

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch70

calib_packv5_042523_0143.root, FC#12, port B1

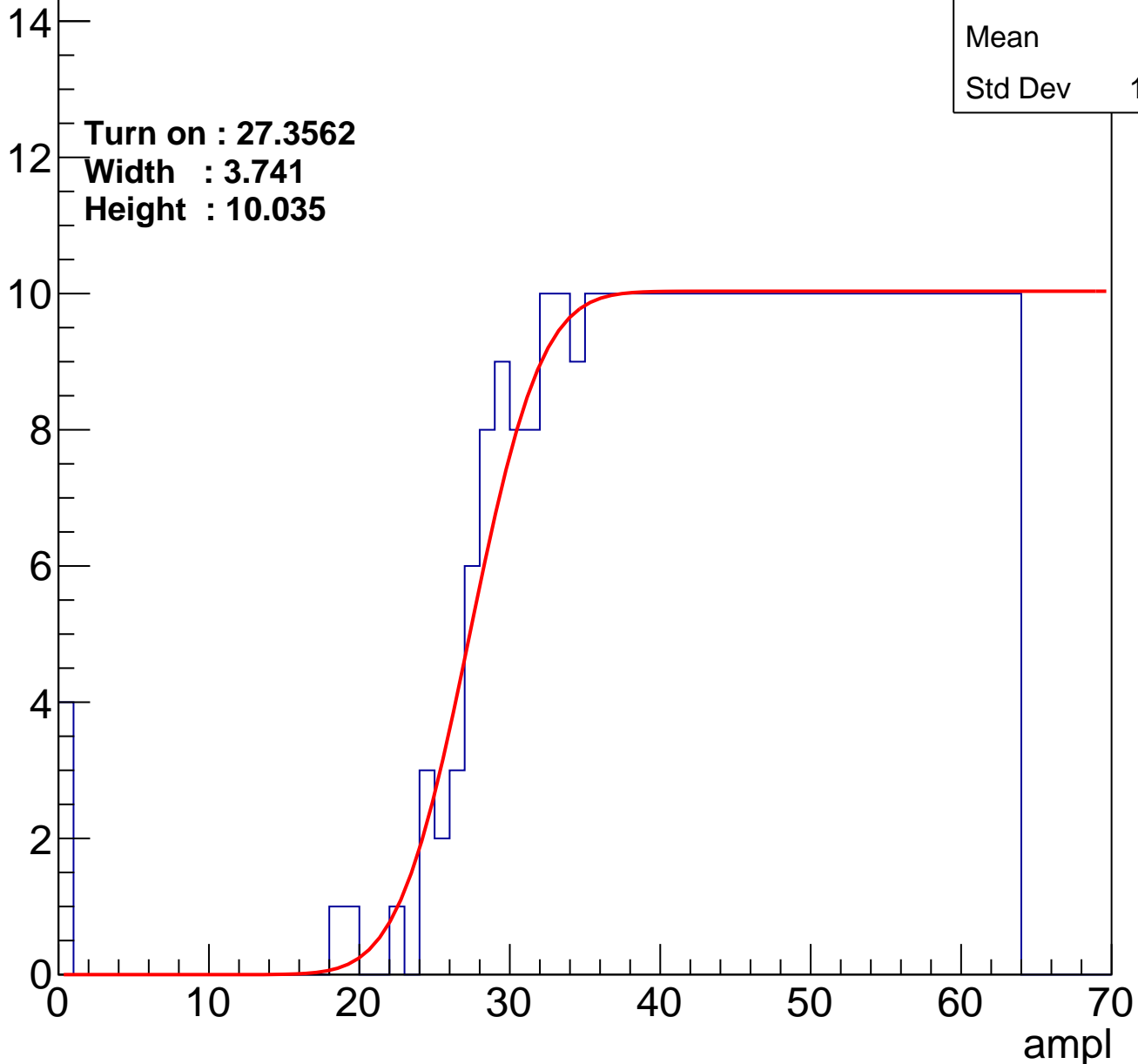
Entries	373
Mean	44.4
Std Dev	11.83

Turn on : 27.3562

Width : 3.741

Height : 10.035

Entry



B0L102S, U24-ch71

calib_packv5_042523_0143.root, FC#12, port B1

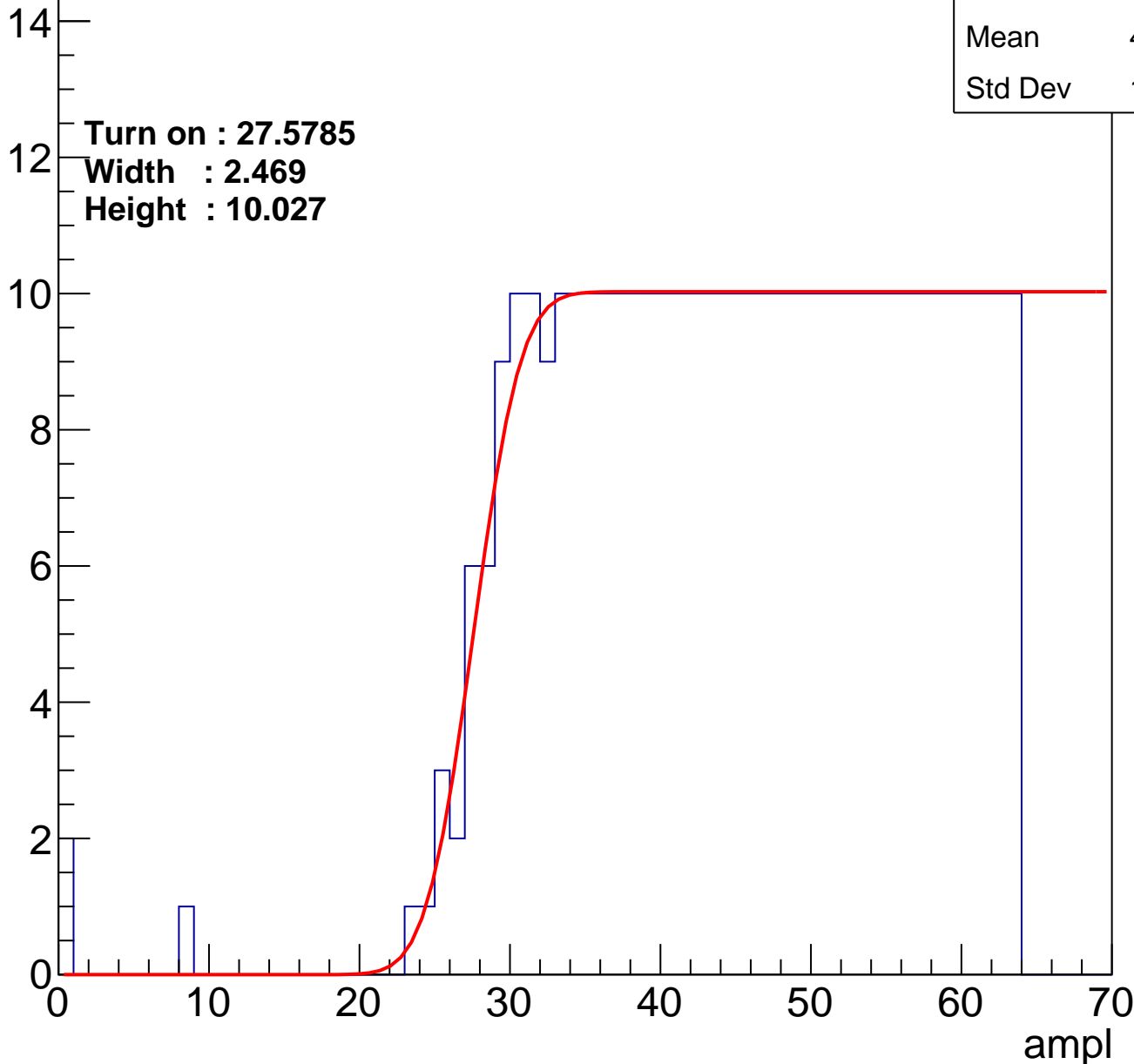
Entries	370
Mean	44.73
Std Dev	11.34

Turn on : 27.5785

Width : 2.469

Height : 10.027

Entry



B0L102S, U24-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.12
Std Dev	12.24

Turn on : 24.4432

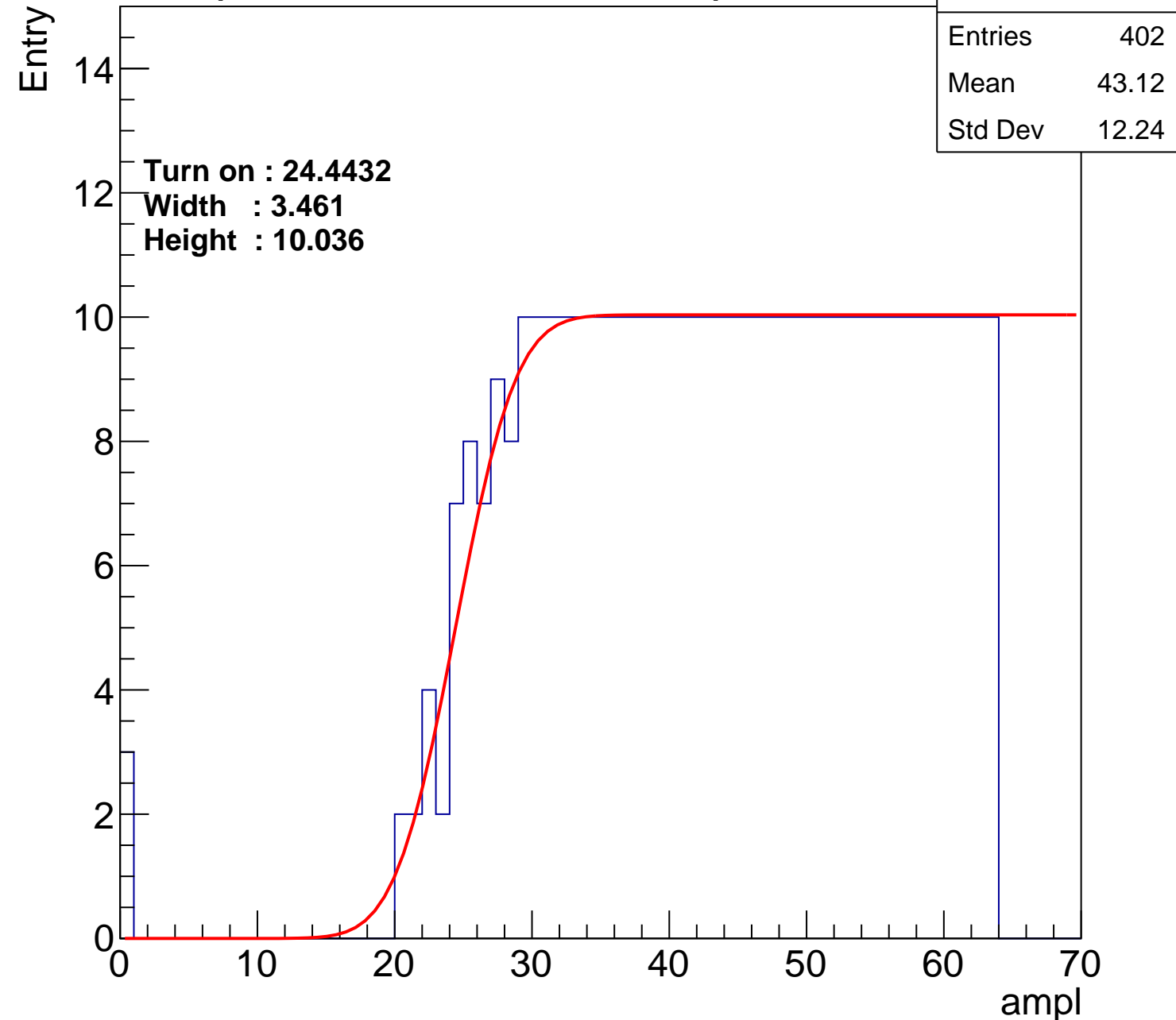
Width : 3.461

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.02
Std Dev	11.81

Turn on : 26.2525

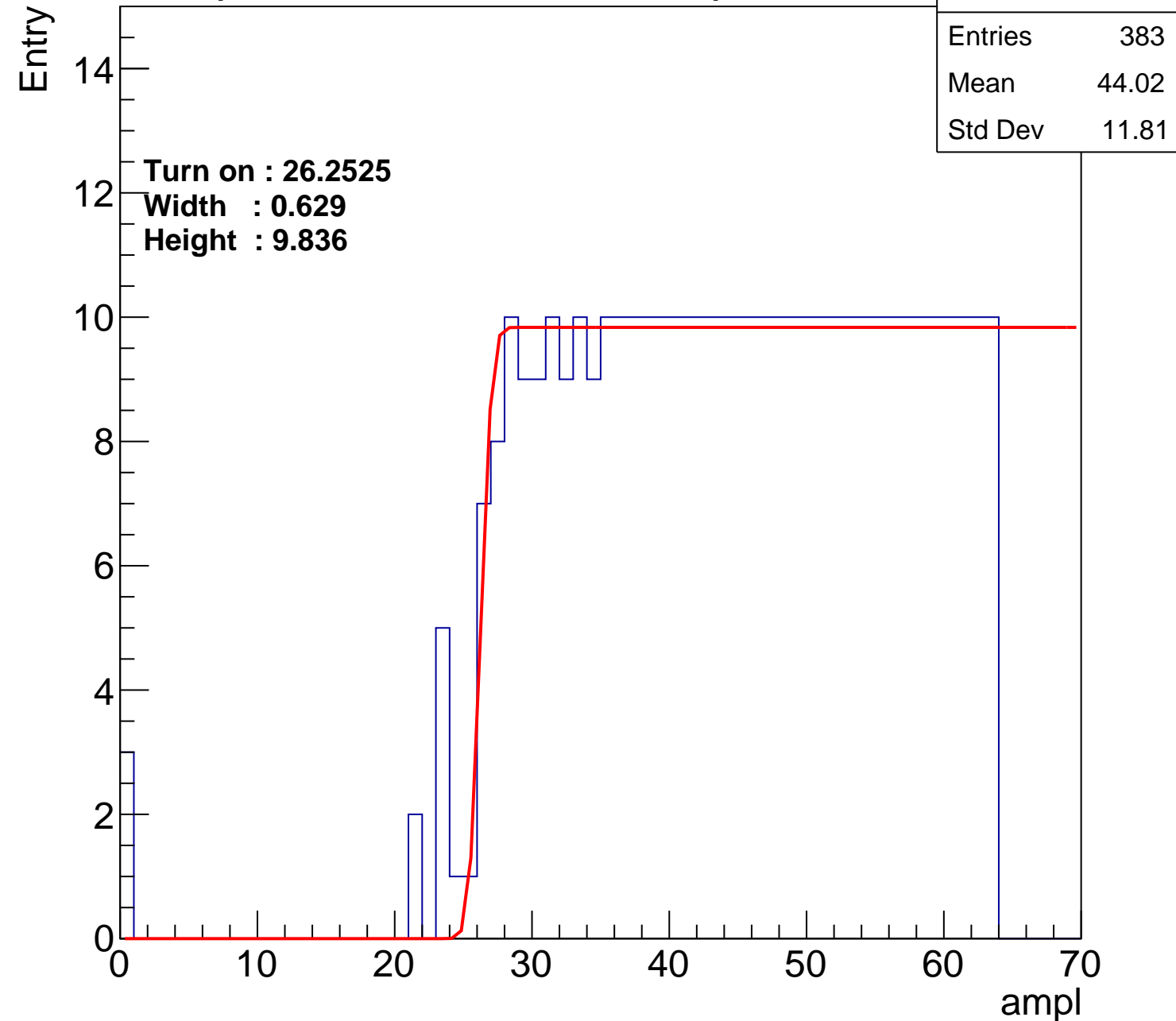
Width : 0.629

Height : 9.836

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.09
Std Dev	11.51

Turn on : 25.7648

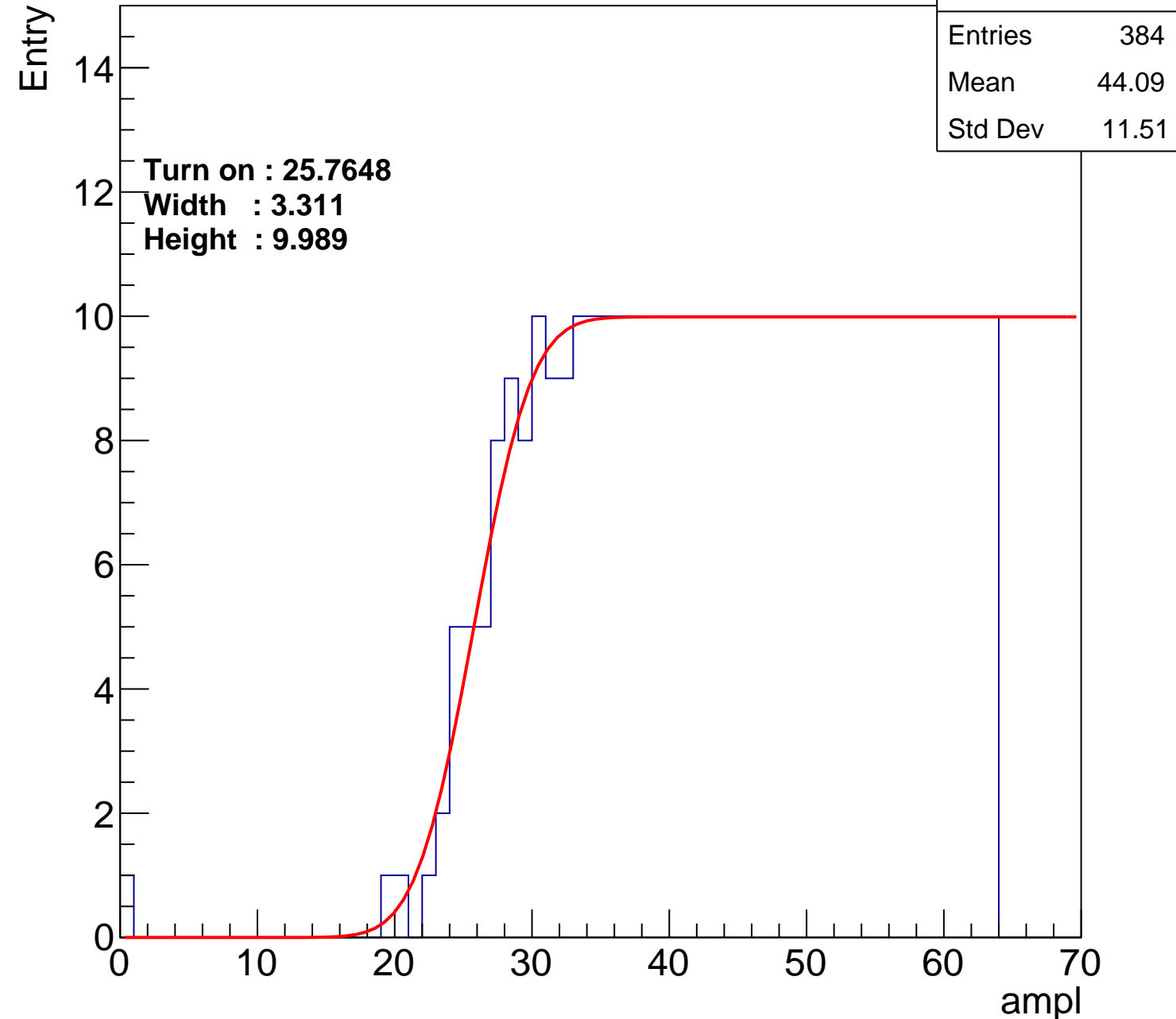
Width : 3.311

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.27
Std Dev	11.68

Turn on : 26.4222

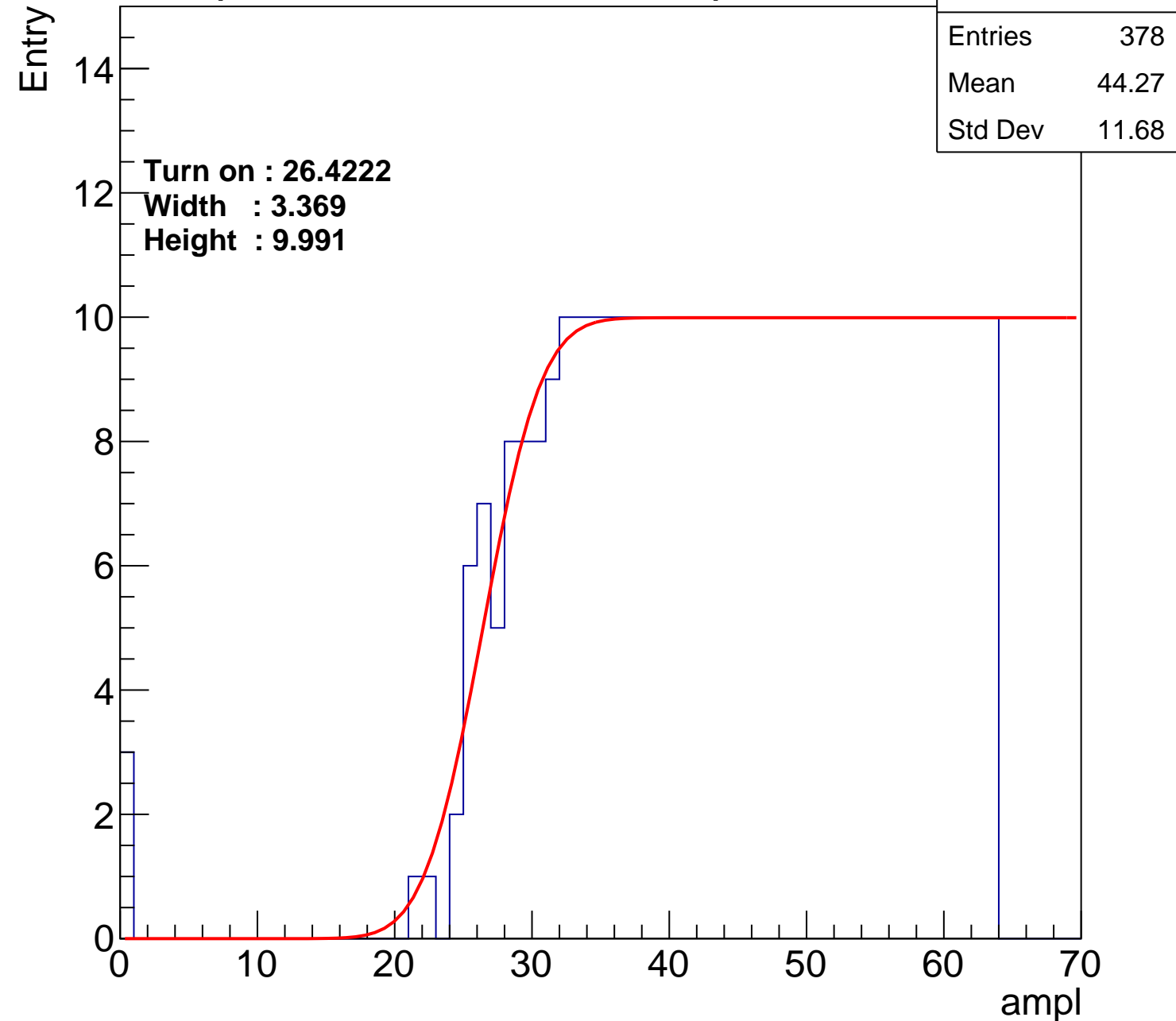
Width : 3.369

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.49
Std Dev	12.06

Turn on : 24.8626

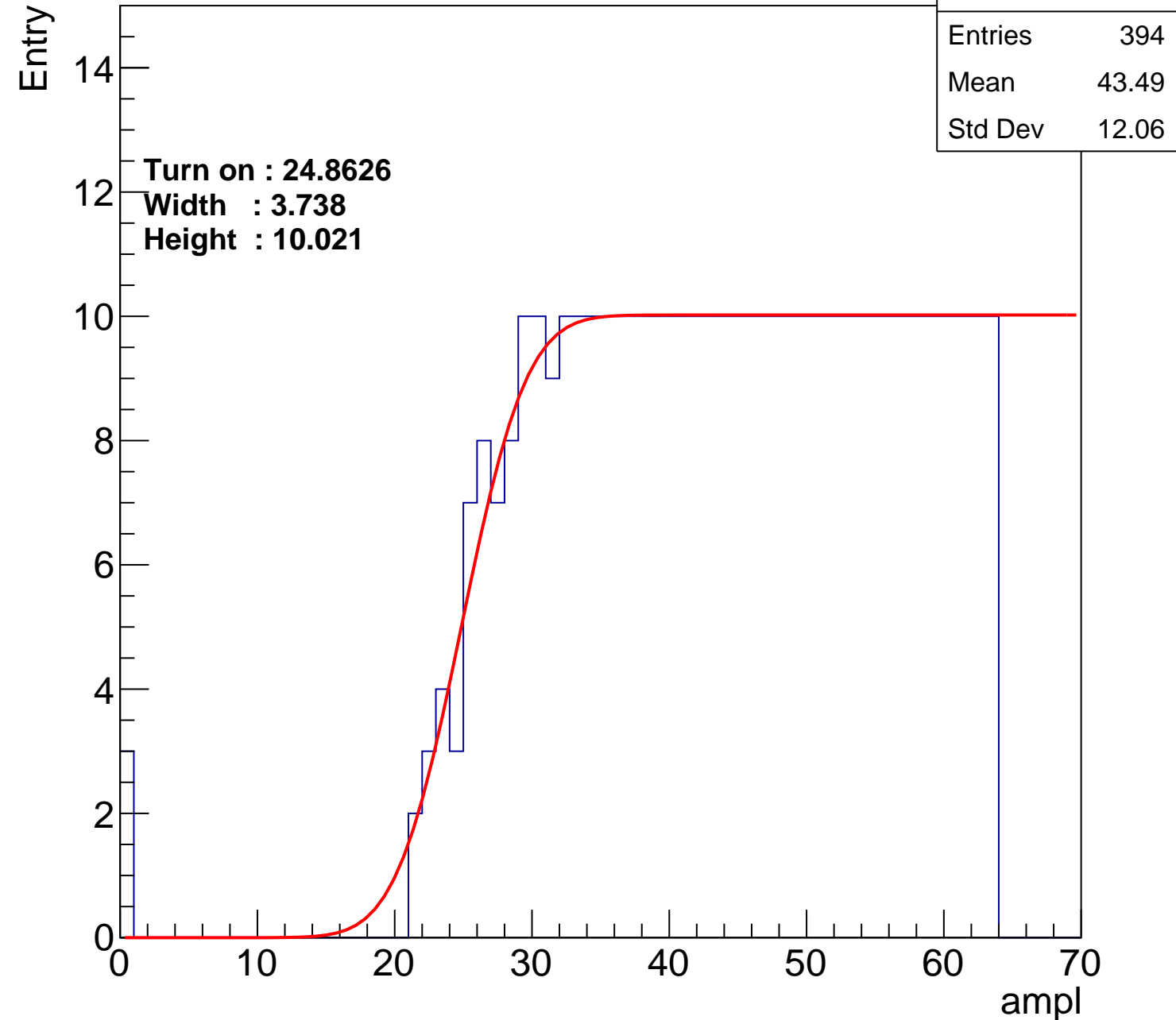
Width : 3.738

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch77

calib_packv5_042523_0143.root, FC#12, port B1

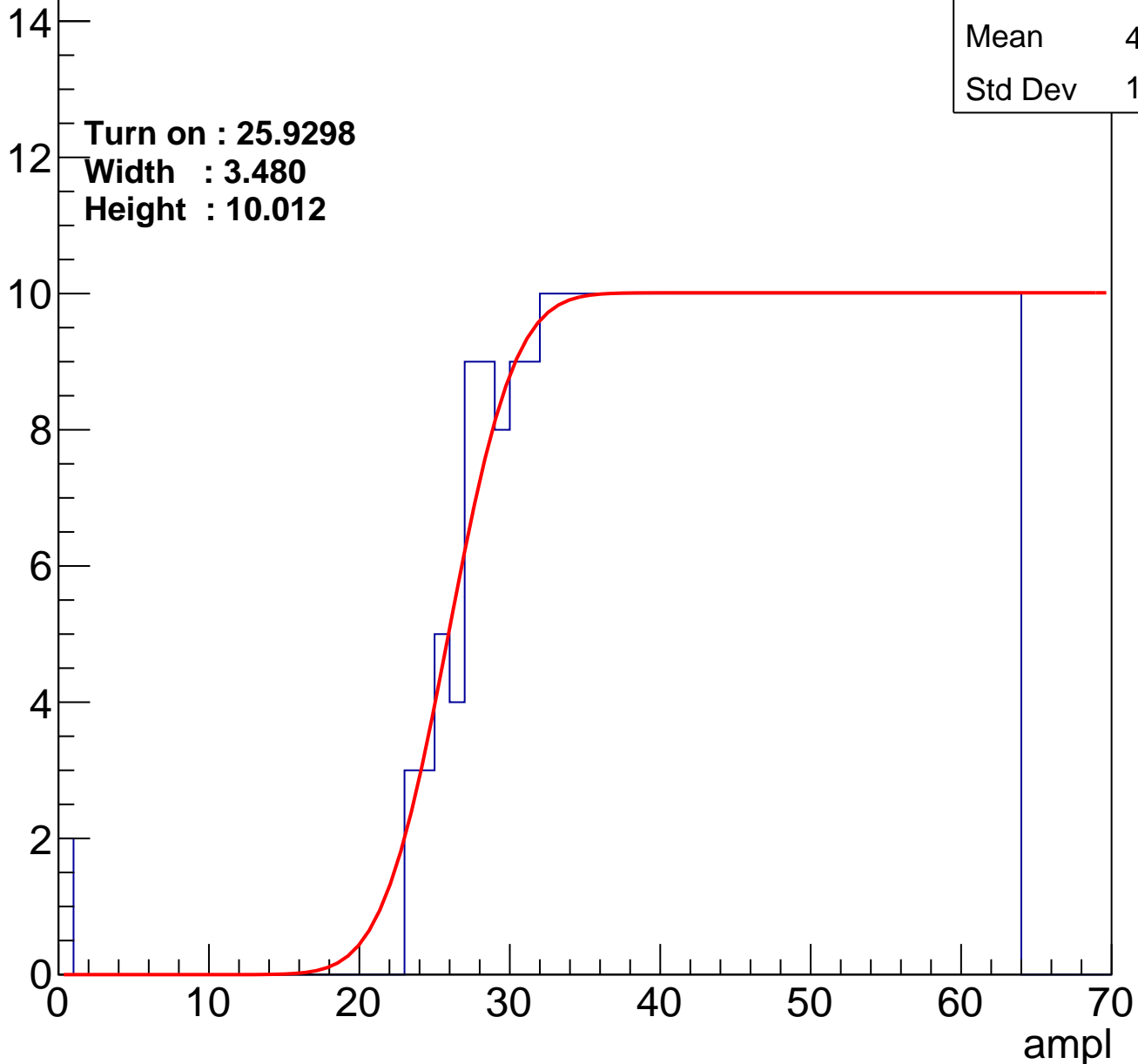
Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 25.9298

Width : 3.480

Height : 10.012

Entry



B0L102S, U24-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.93
Std Dev	11.73

Turn on : 25.9479

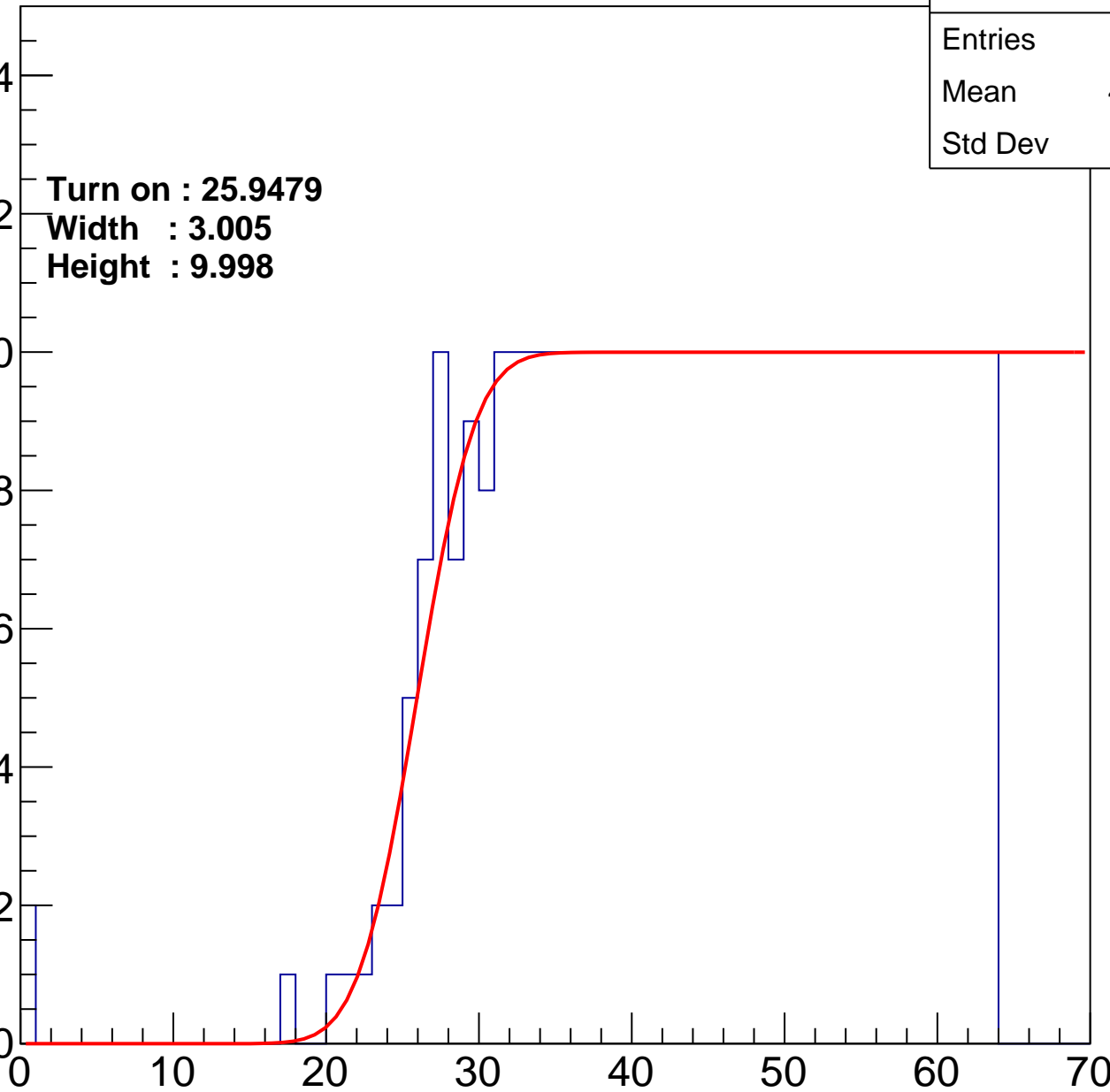
Width : 3.005

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.12
Std Dev	11.64

Turn on : 26.7121

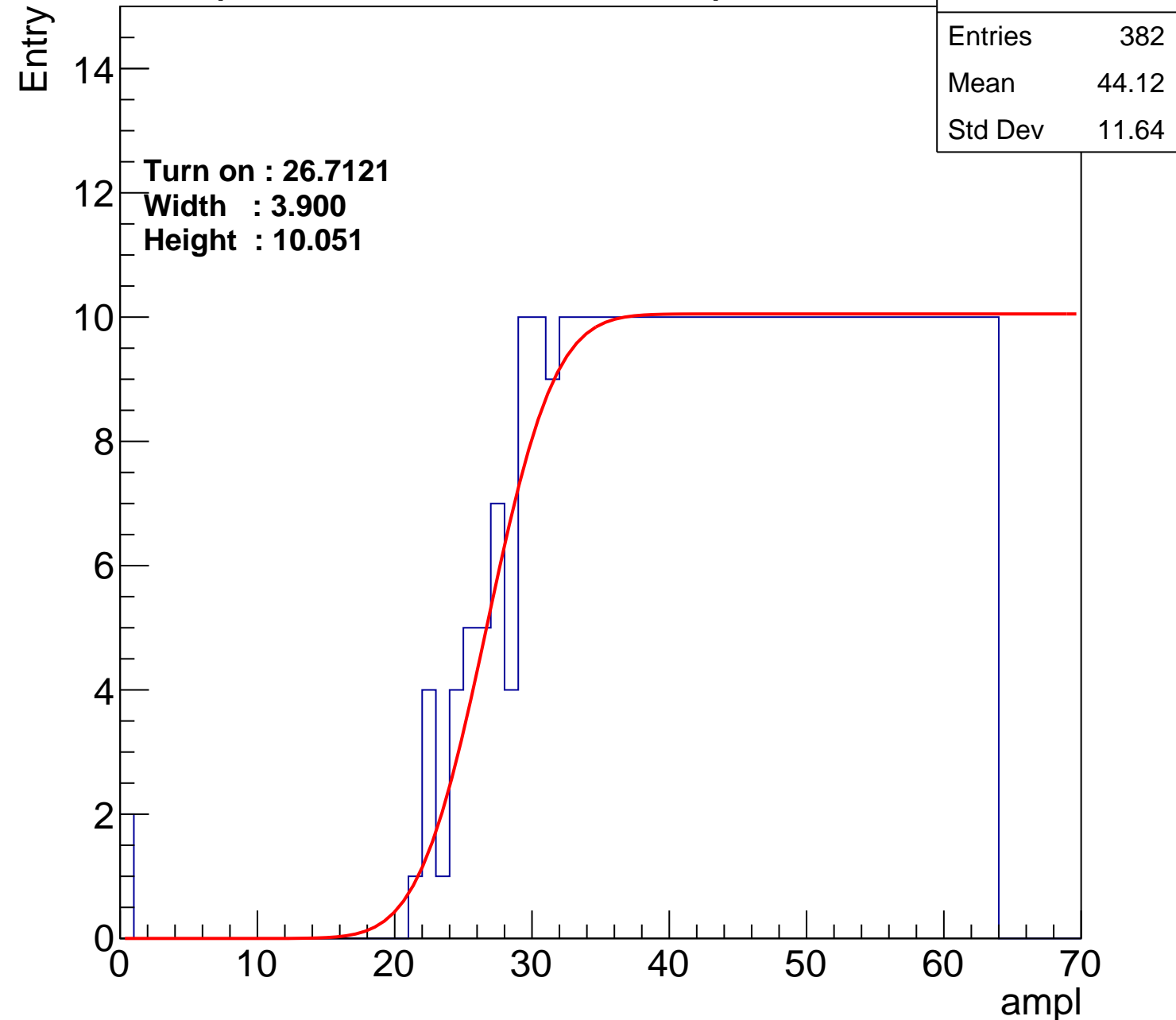
Width : 3.900

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.35
Std Dev	11.44

Turn on : 27.0161

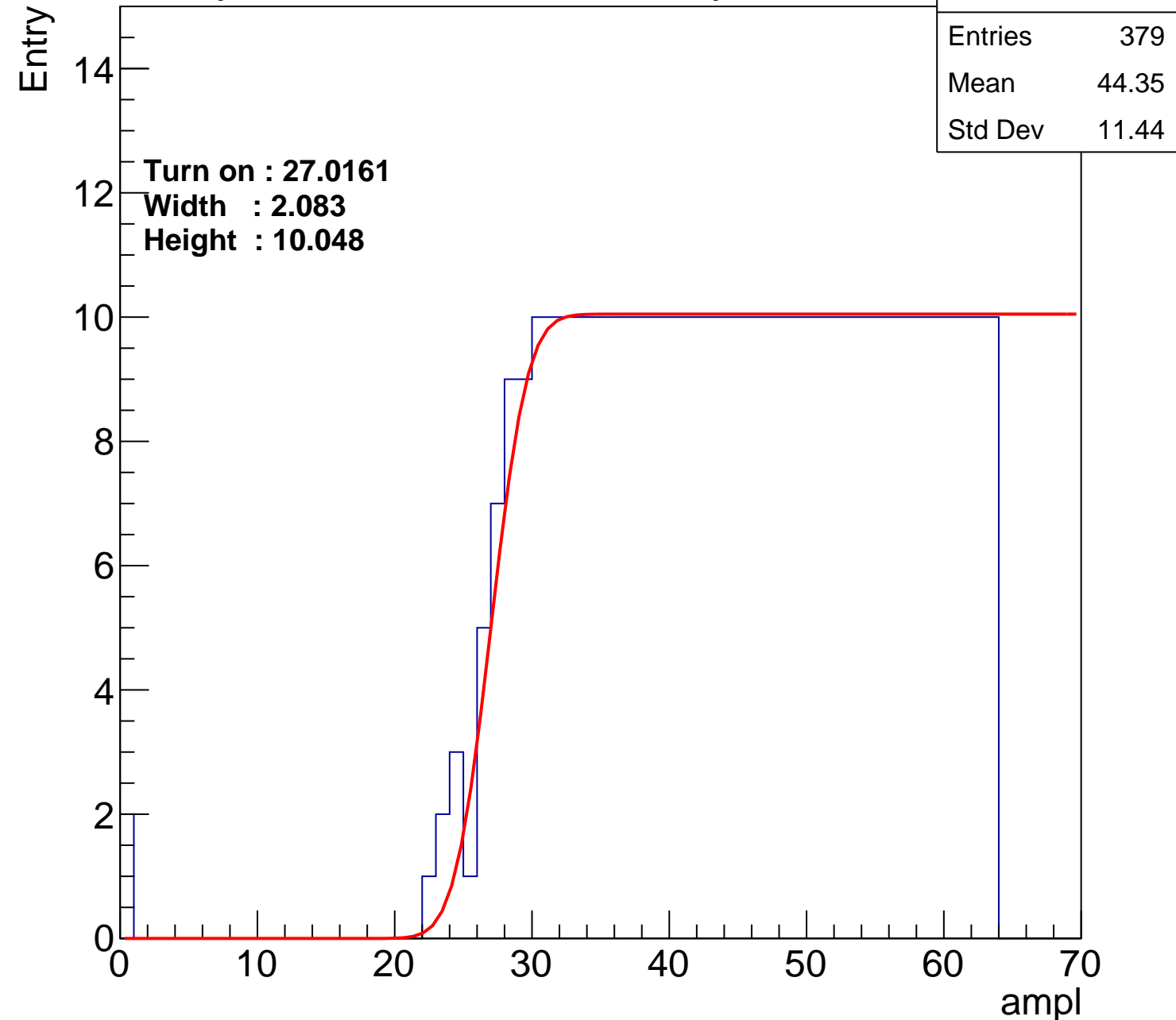
Width : 2.083

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 27.2647

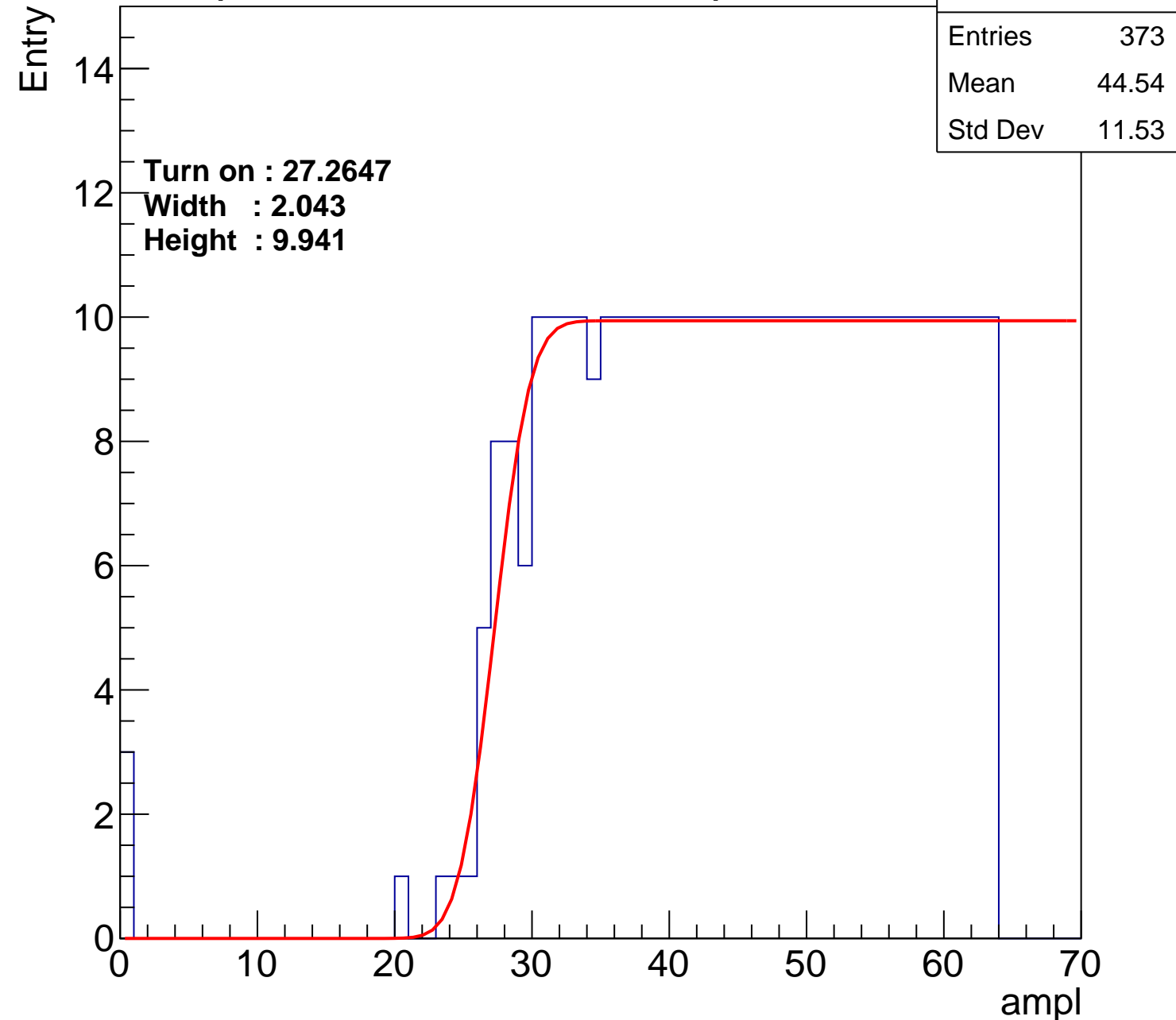
Width : 2.043

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.27
Std Dev	11.38

Turn on : 26.0225

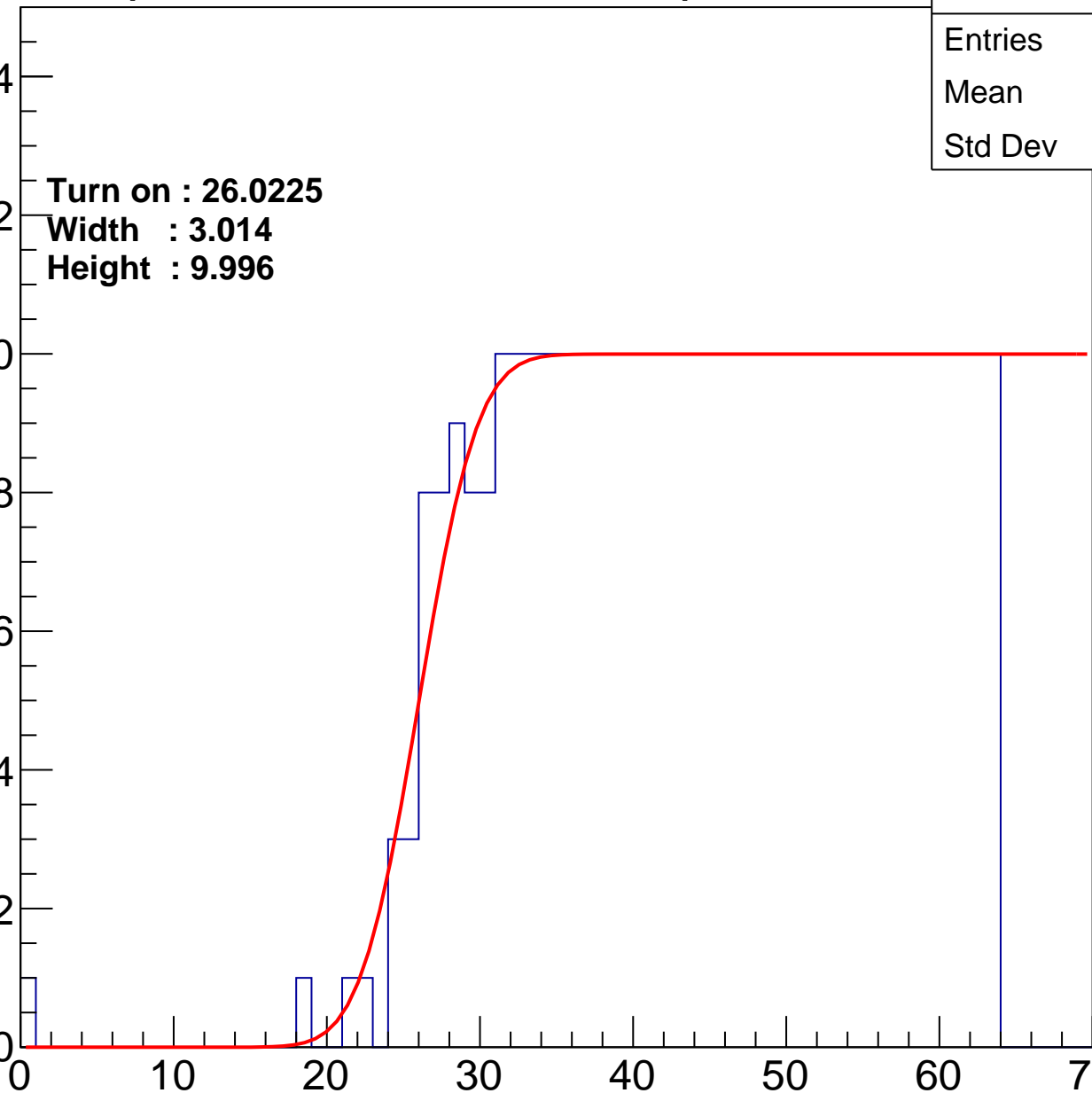
Width : 3.014

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch83

calib_packv5_042523_0143.root, FC#12, port B1

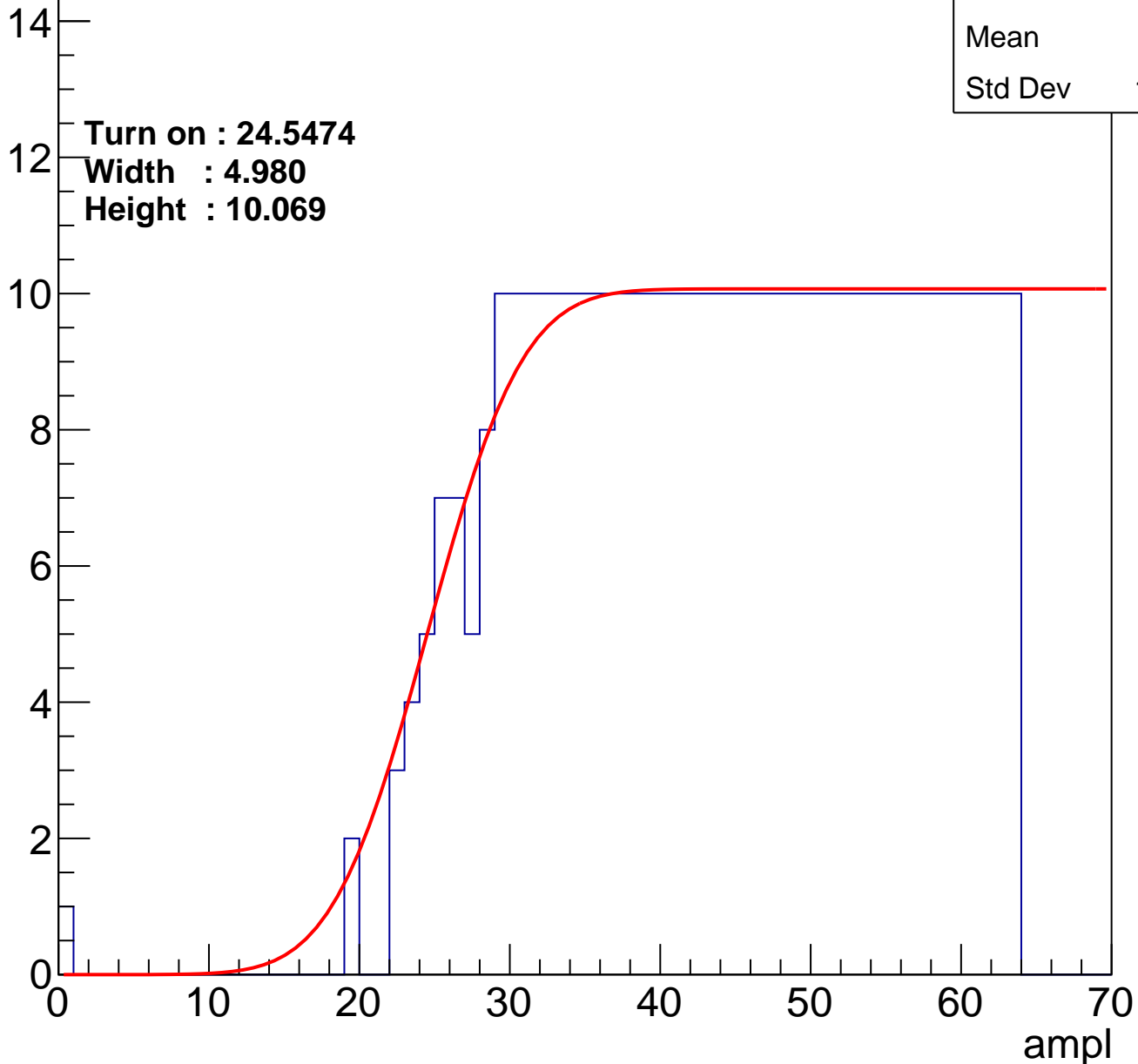
Entries	392
Mean	43.7
Std Dev	11.71

Turn on : 24.5474

Width : 4.980

Height : 10.069

Entry



B0L102S, U24-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.19
Std Dev	10.94

Turn on : 28.3524

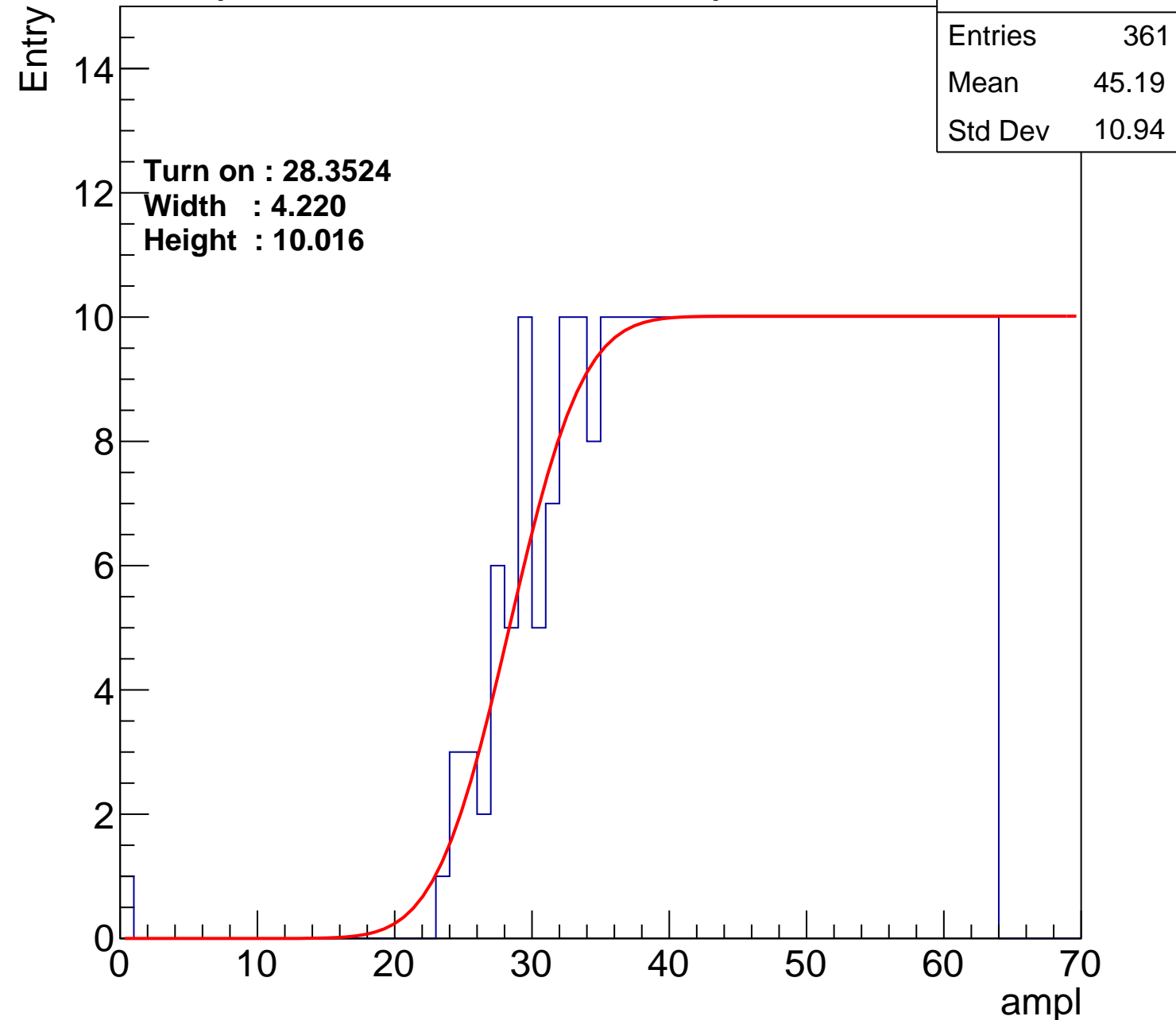
Width : 4.220

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.9
Std Dev	11.86

Turn on : 26.3705

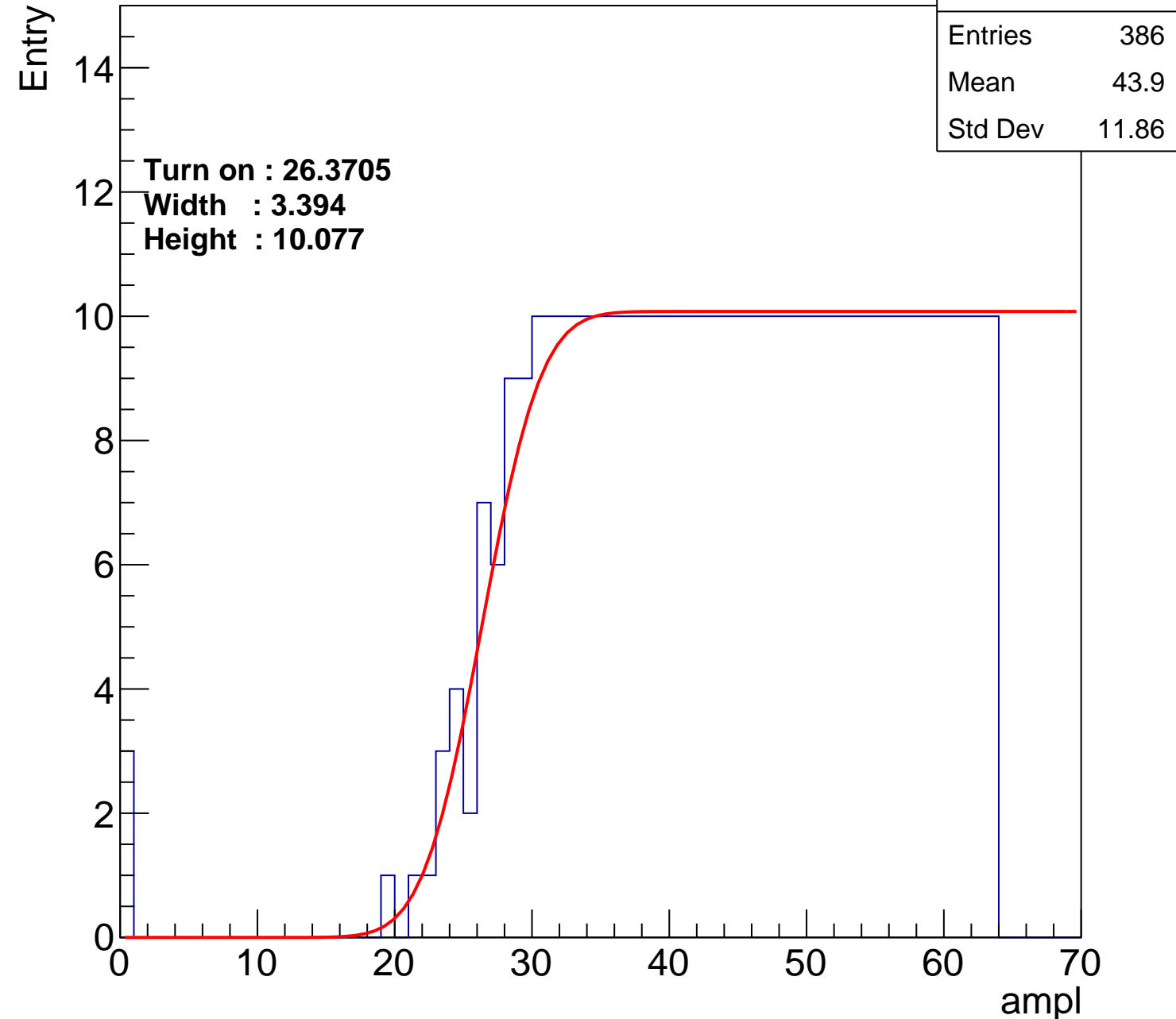
Width : 3.394

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch86

calib_packv5_042523_0143.root, FC#12, port B1

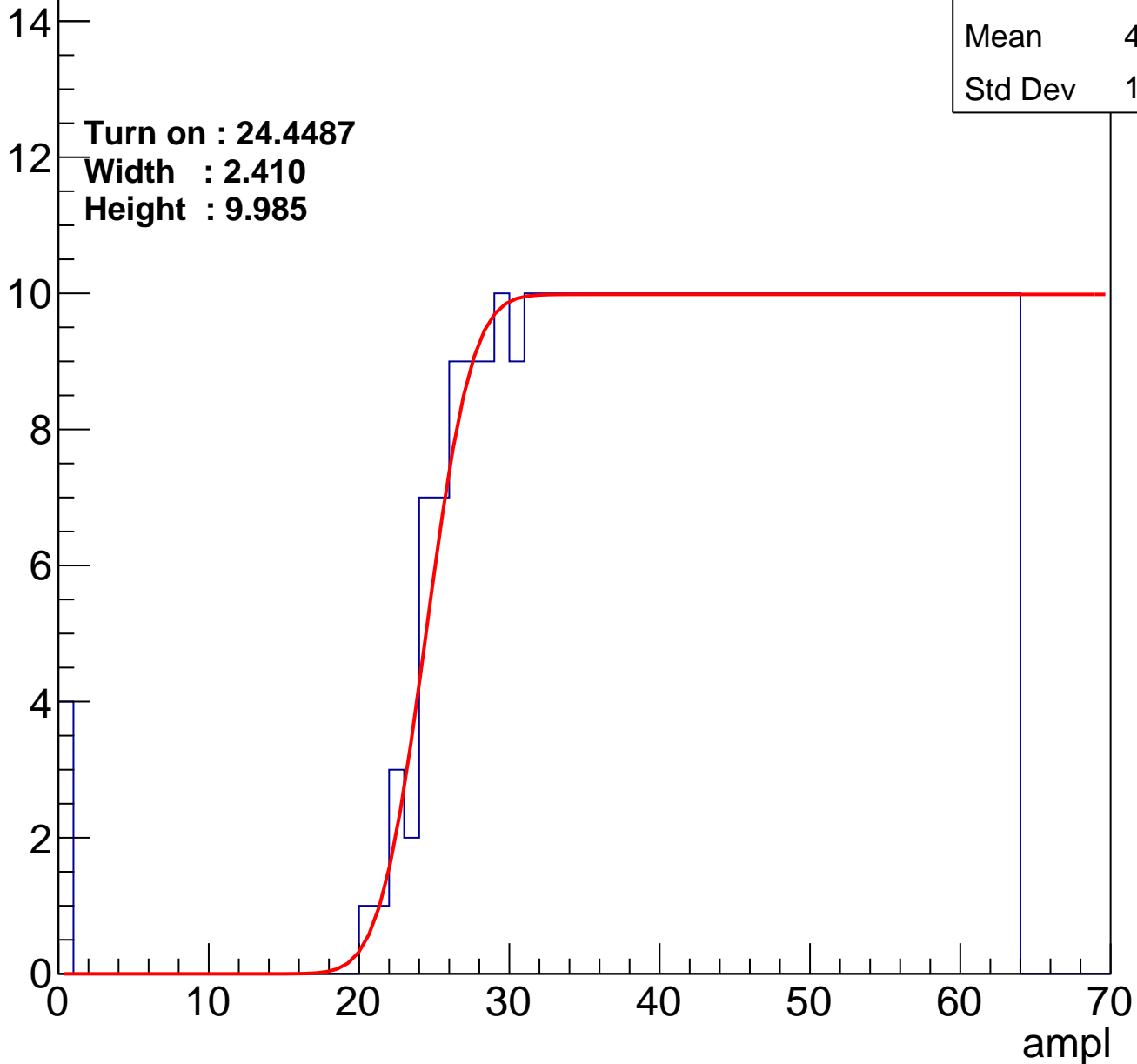
Entries	401
Mean	43.13
Std Dev	12.33

Turn on : 24.4487

Width : 2.410

Height : 9.985

Entry



B0L102S, U24-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.64
Std Dev	11.82

Turn on : 25.1432

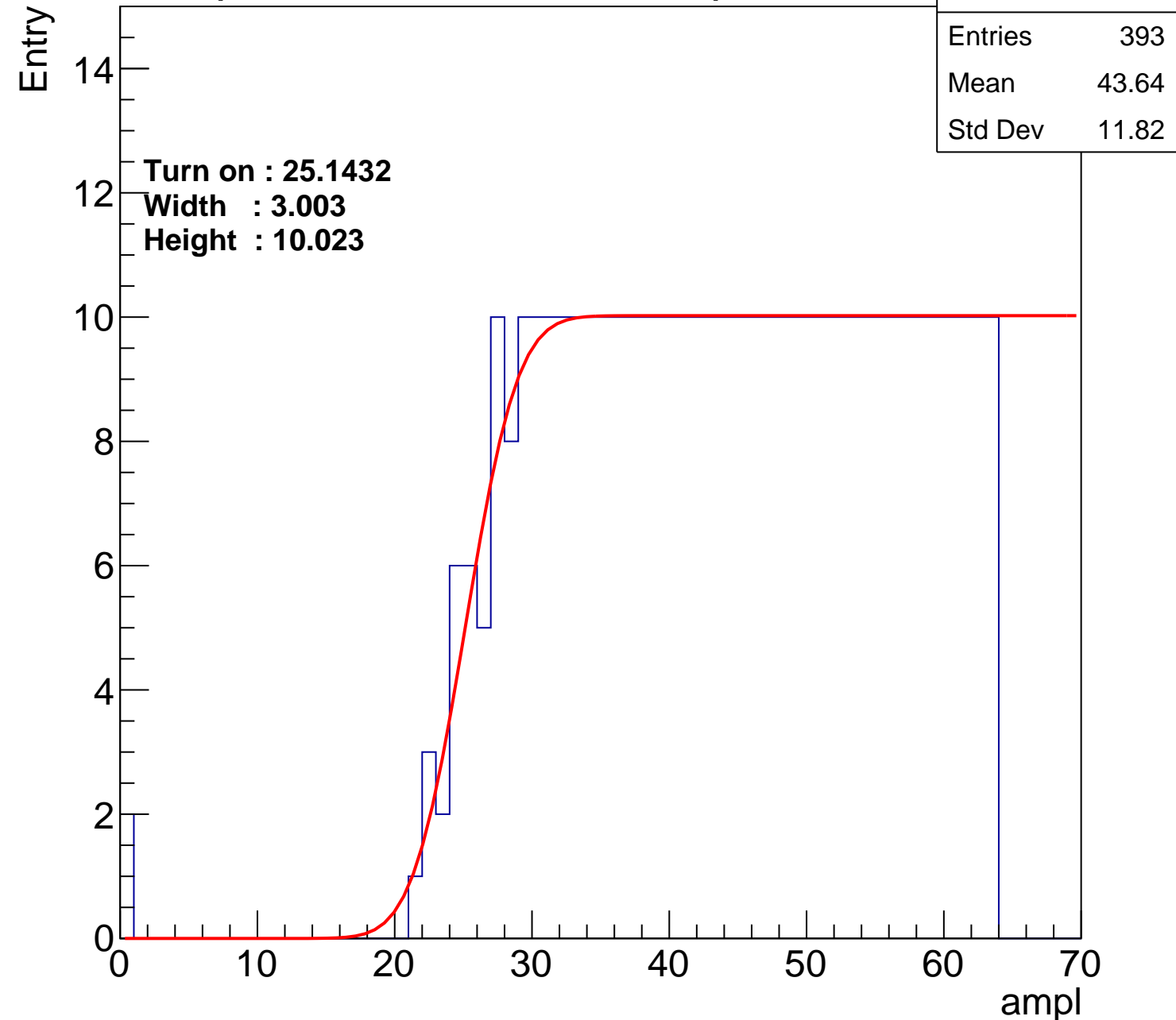
Width : 3.003

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.67
Std Dev	11.89

Turn on : 25.2282

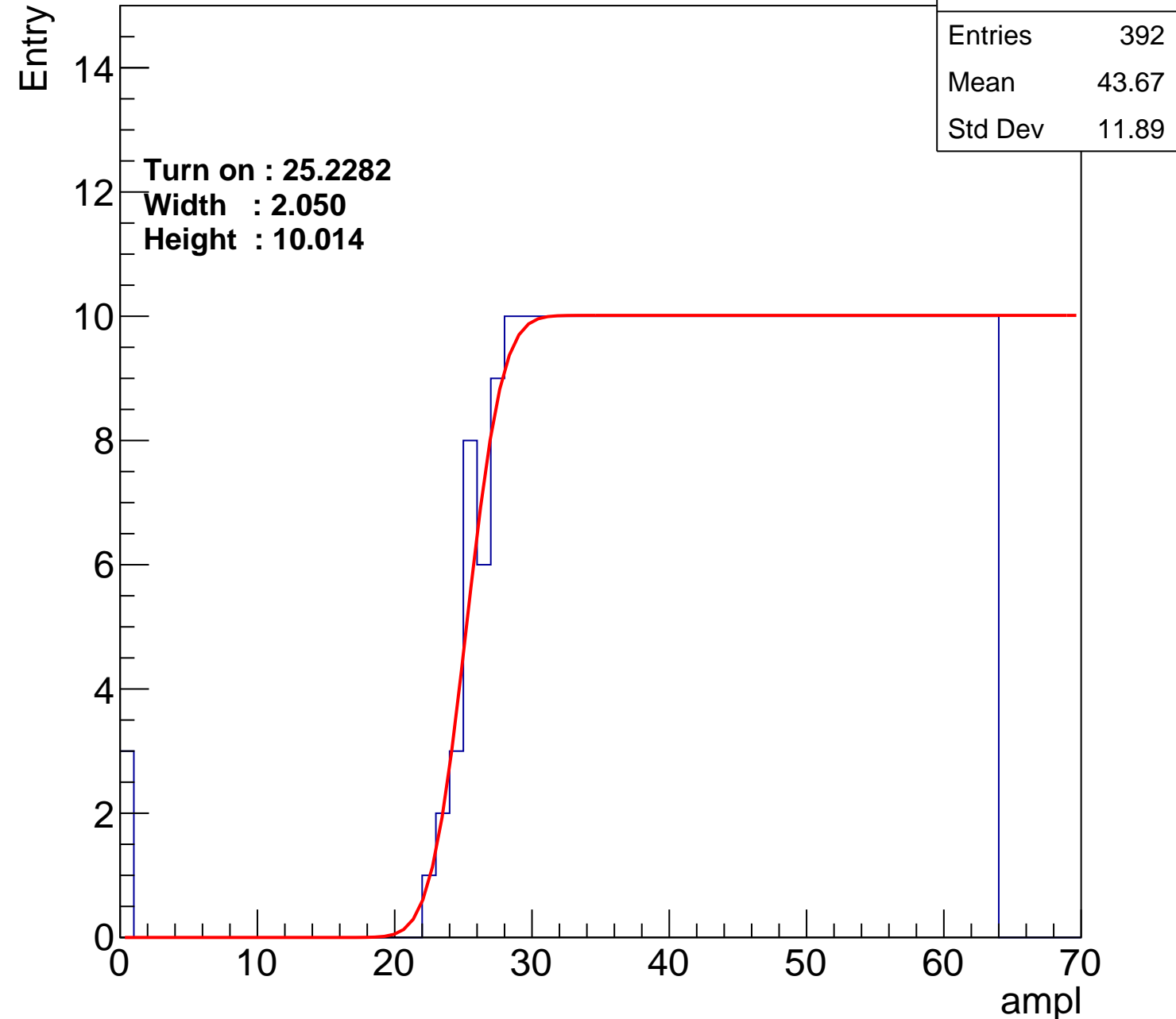
Width : 2.050

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.35
Std Dev	11.61

Turn on : 27.0875

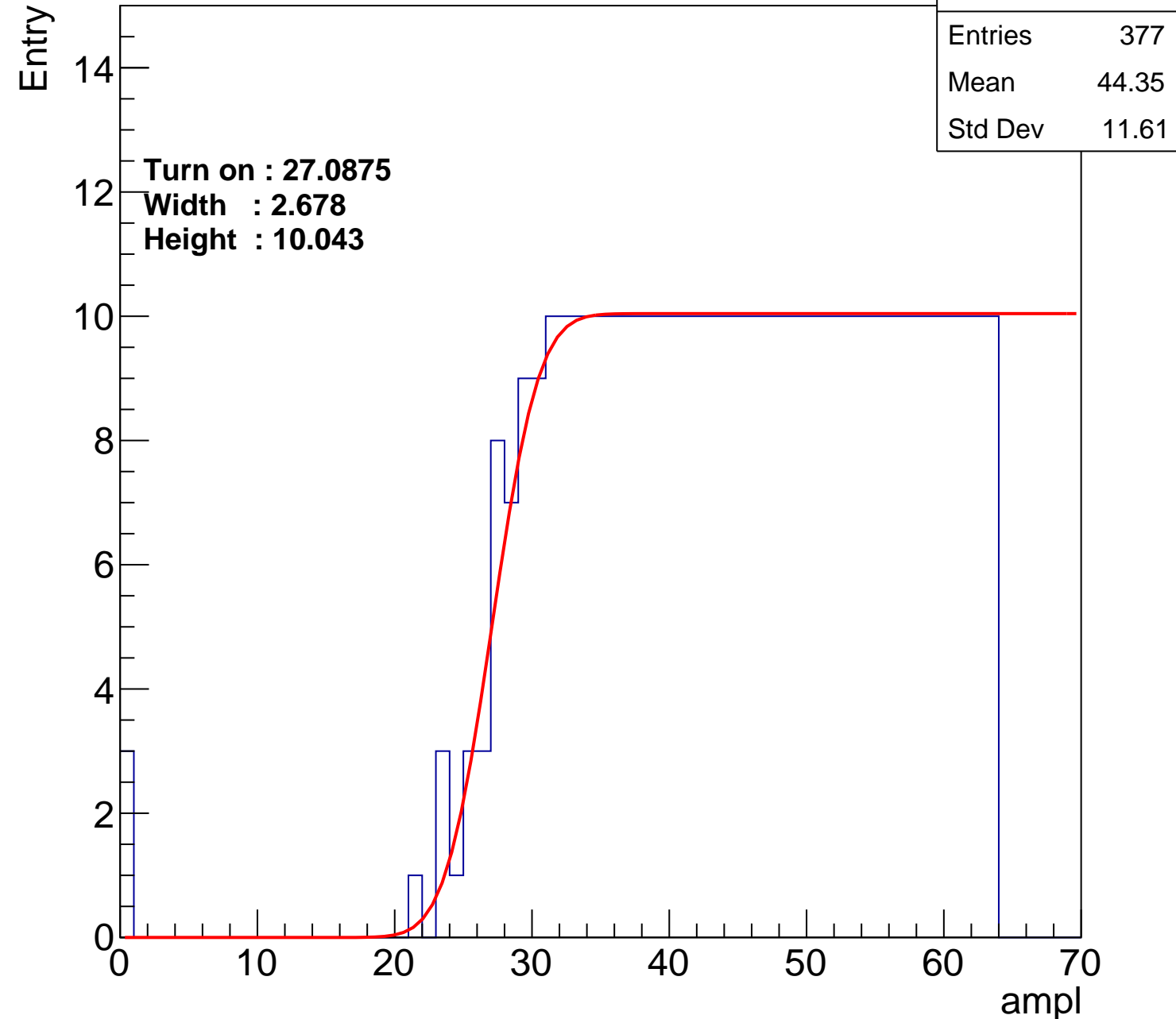
Width : 2.678

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.82
Std Dev	12.3

Turn on : 23.7630

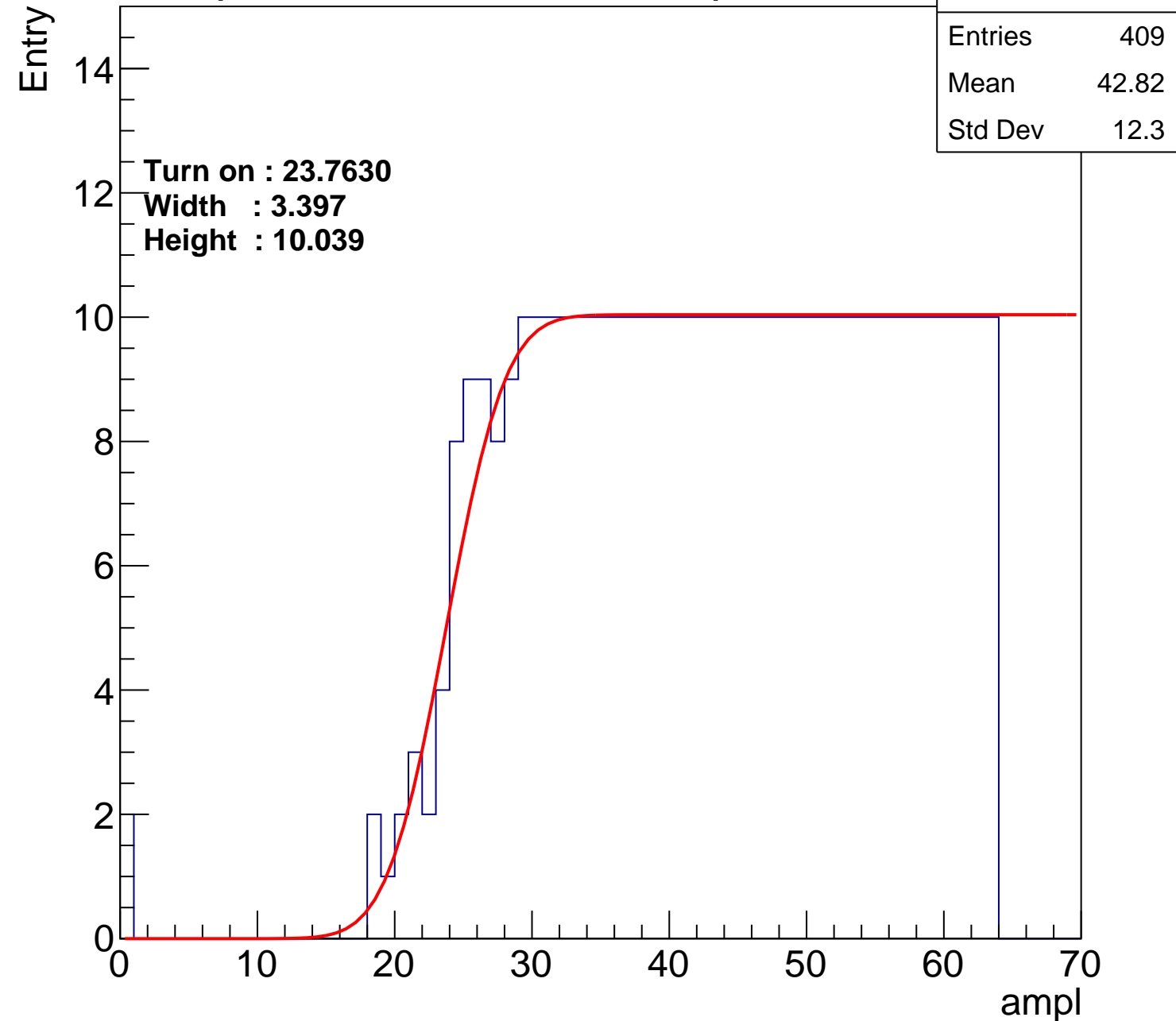
Width : 3.397

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.77
Std Dev	11.42

Turn on : 27.6066

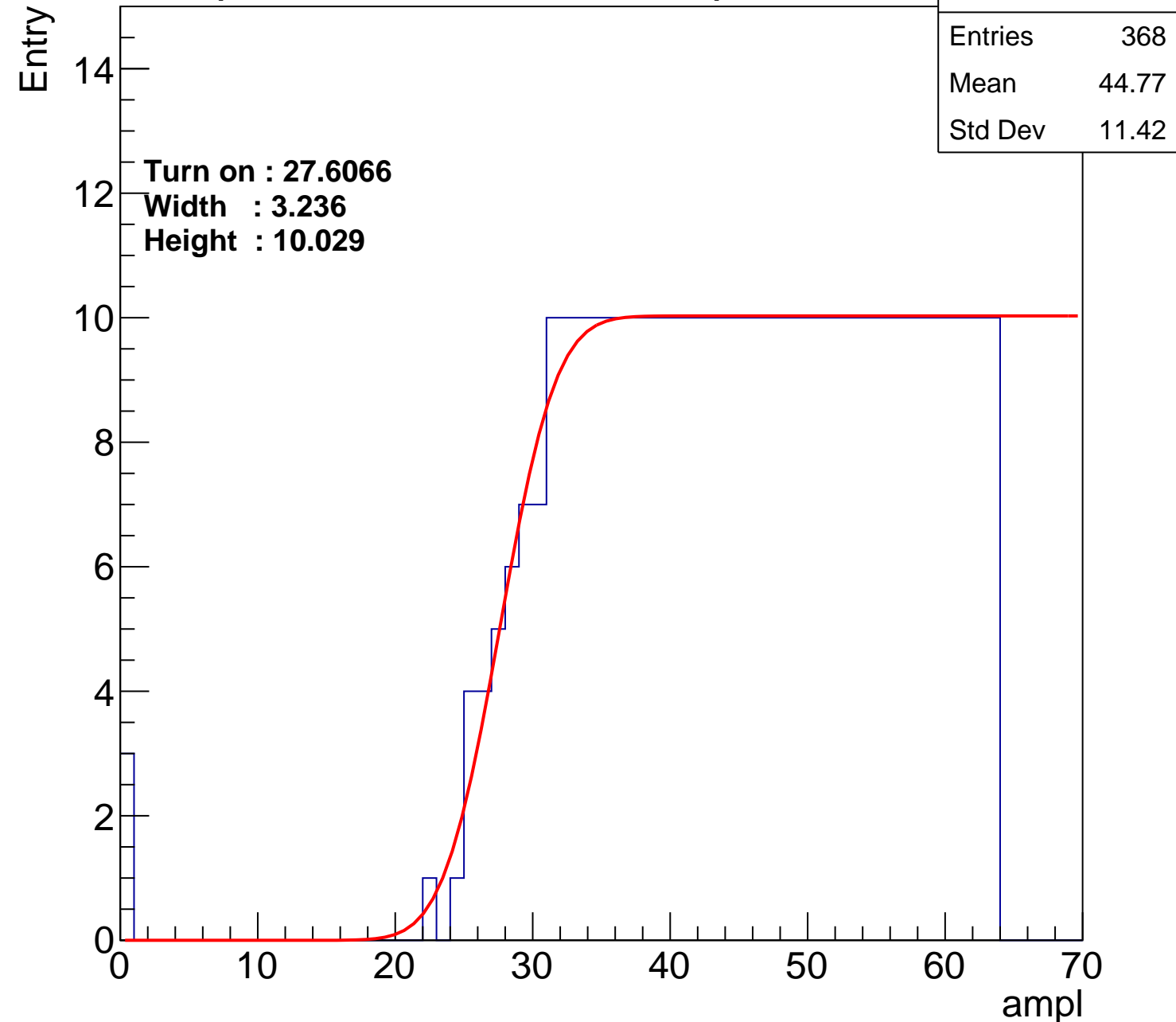
Width : 3.236

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.34
Std Dev	11.36

Turn on : 26.4968

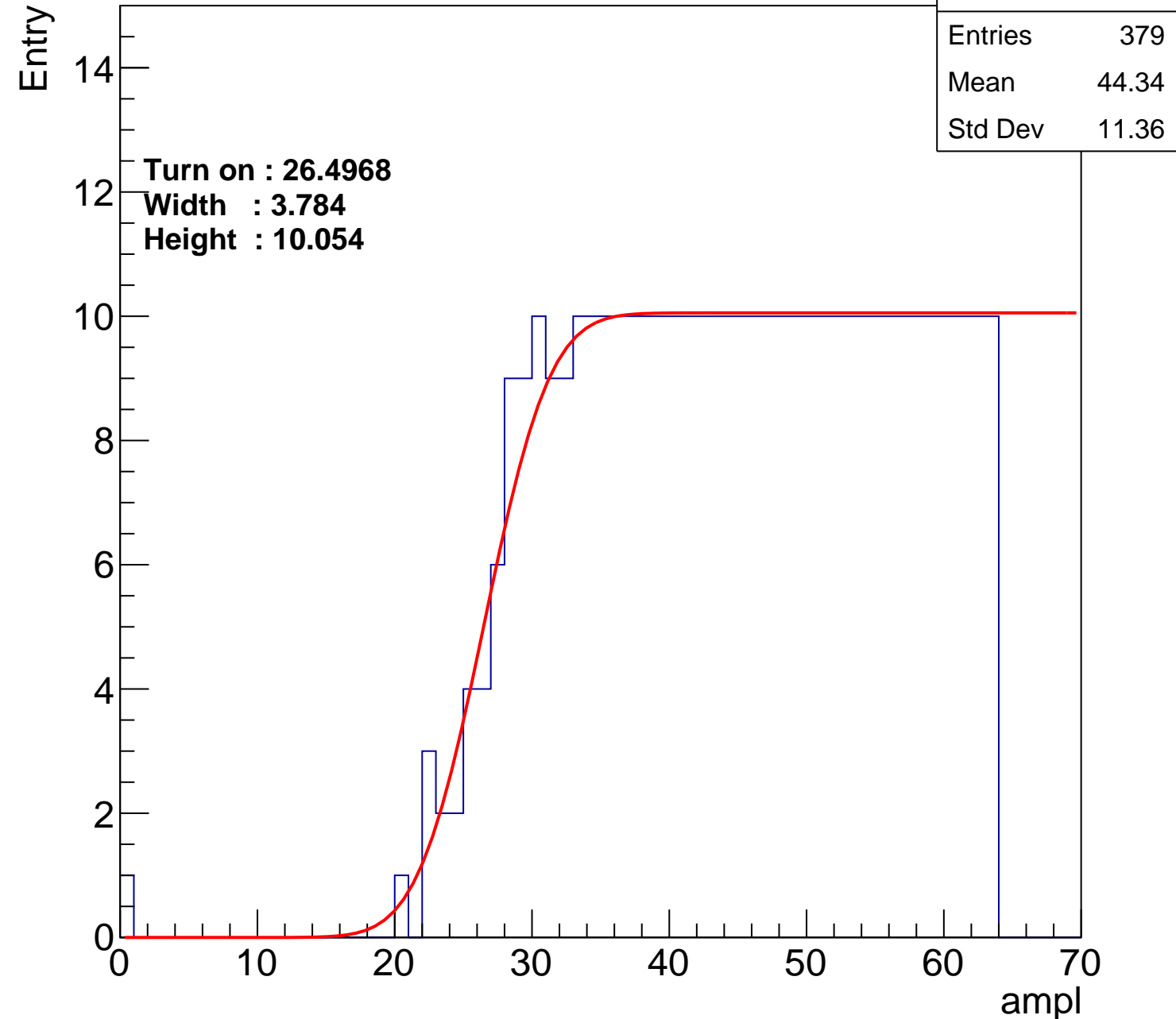
Width : 3.784

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.8
Std Dev	11.6

Turn on : 24.5838

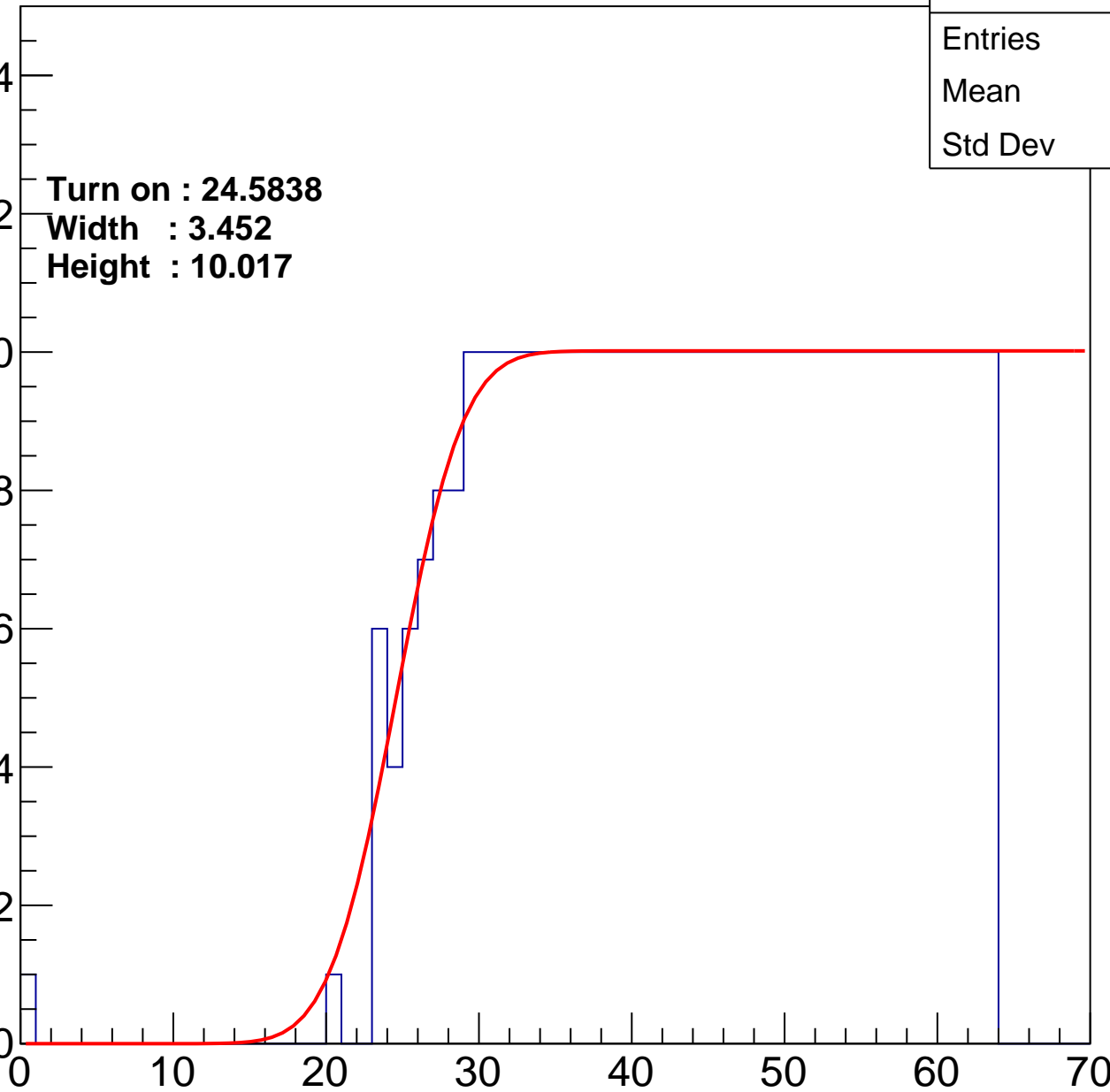
Width : 3.452

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.32
Std Dev	11.66

Turn on : 26.2001

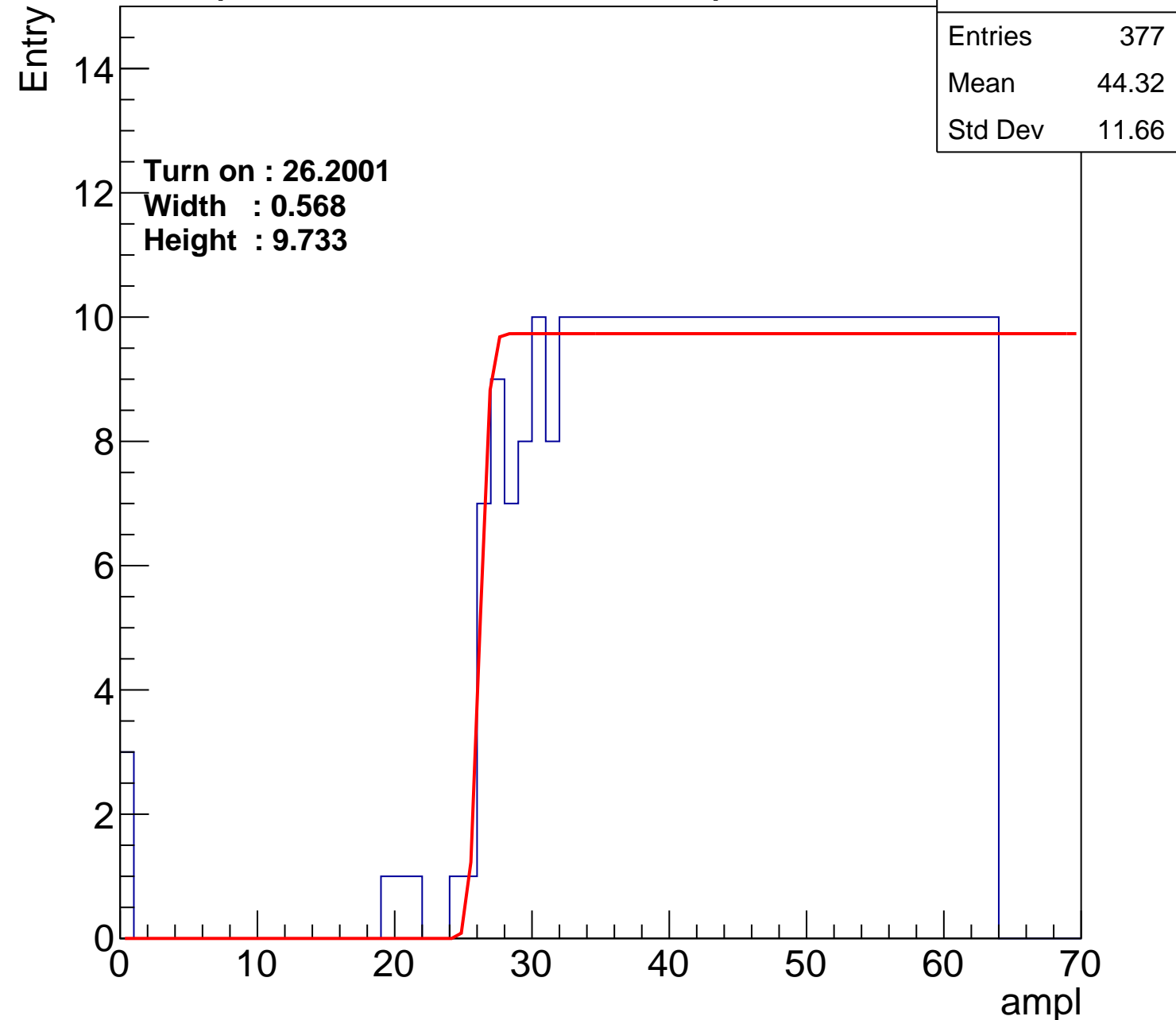
Width : 0.568

Height : 9.733

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.79
Std Dev	11.13

Turn on : 26.4459

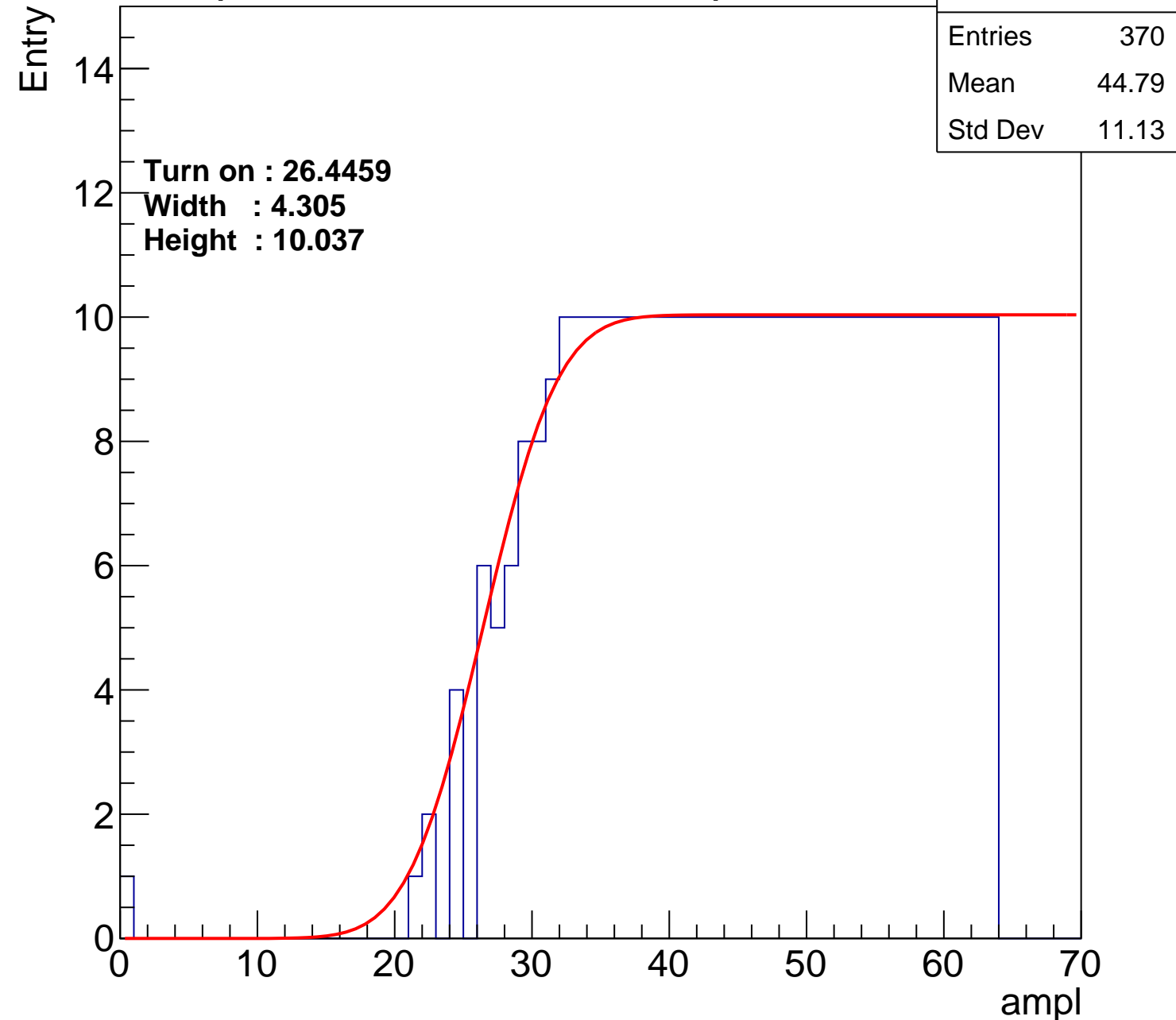
Width : 4.305

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.2175

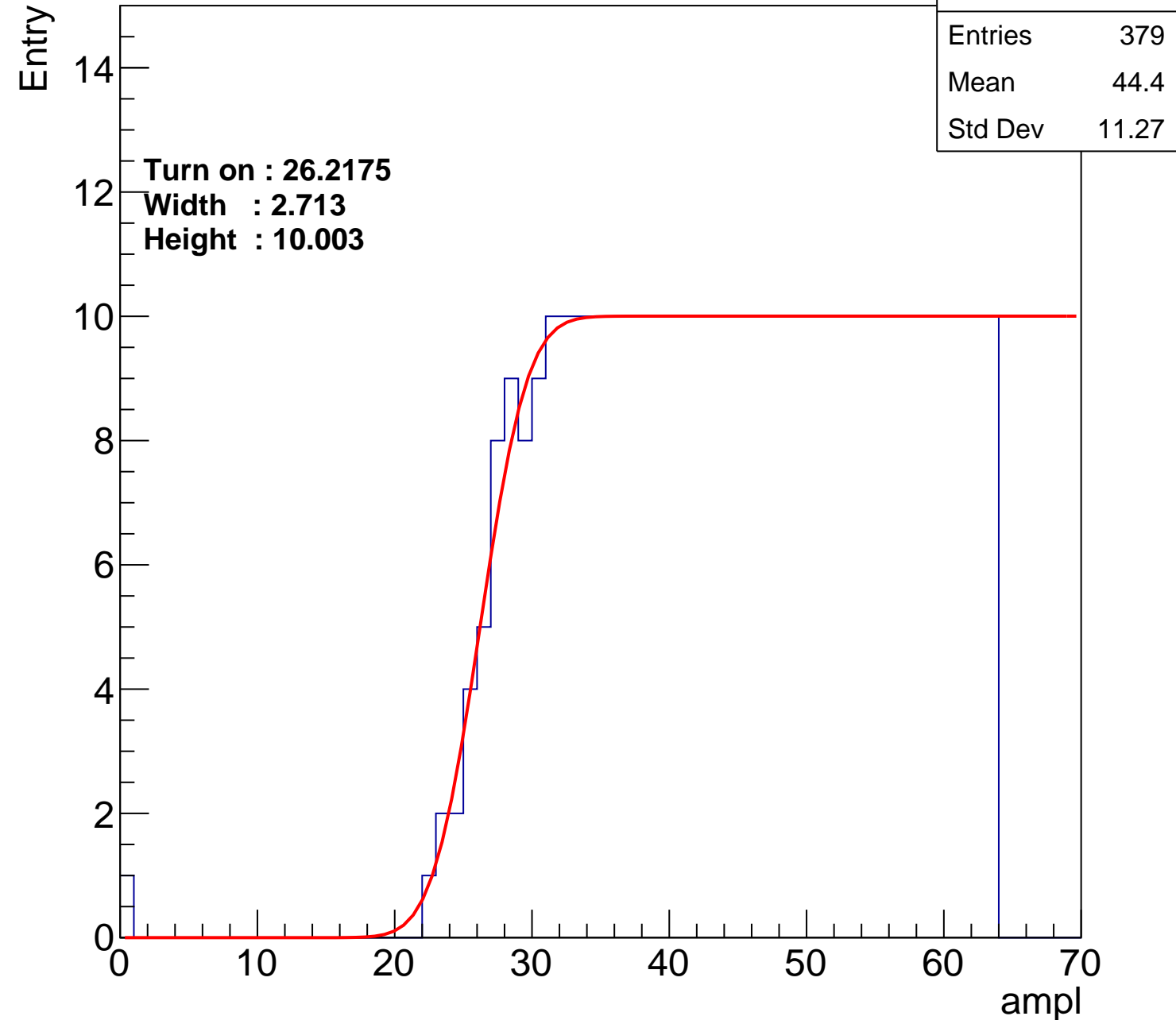
Width : 2.713

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.42
Std Dev	12.2

Turn on : 25.0310

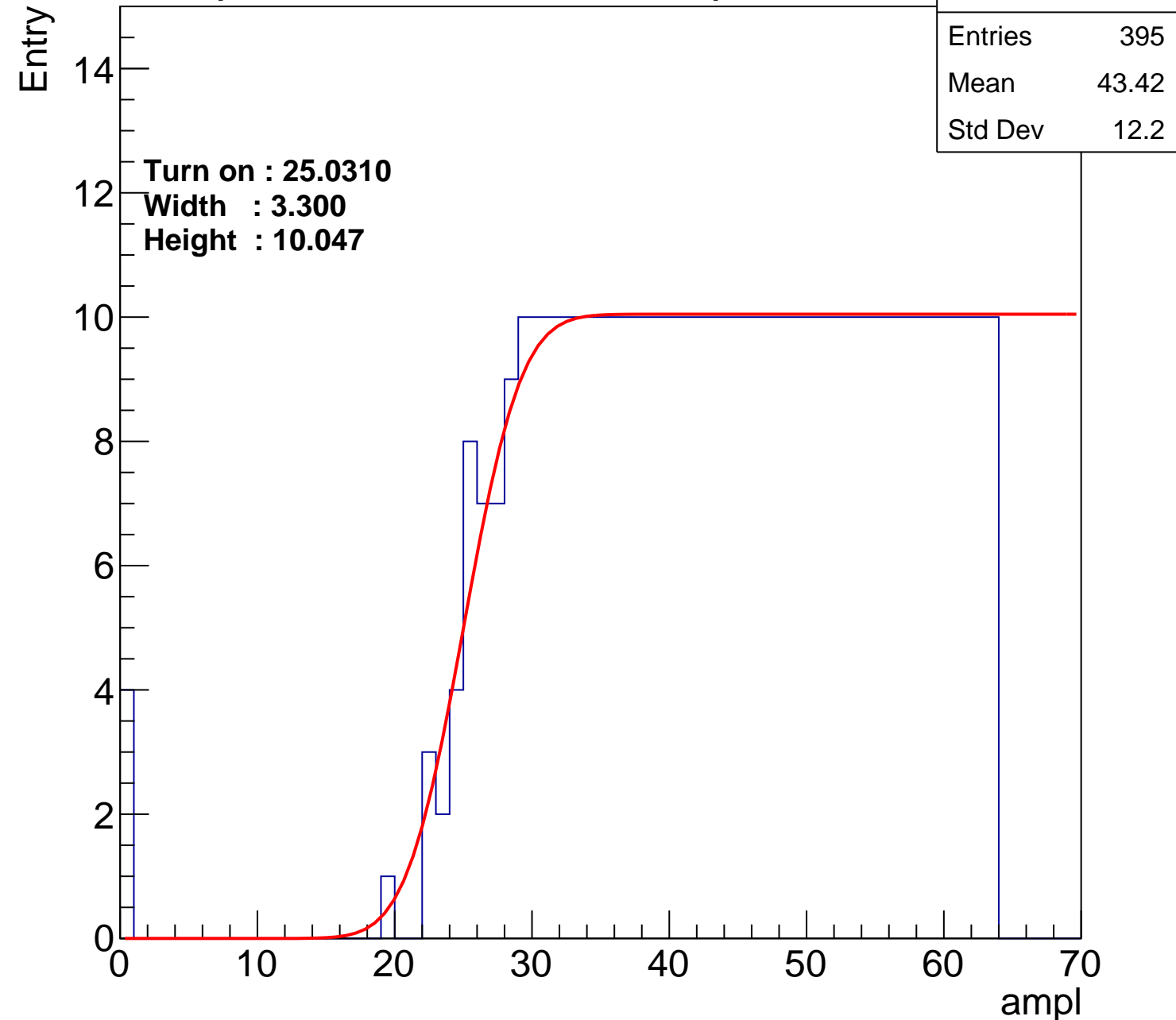
Width : 3.300

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch98

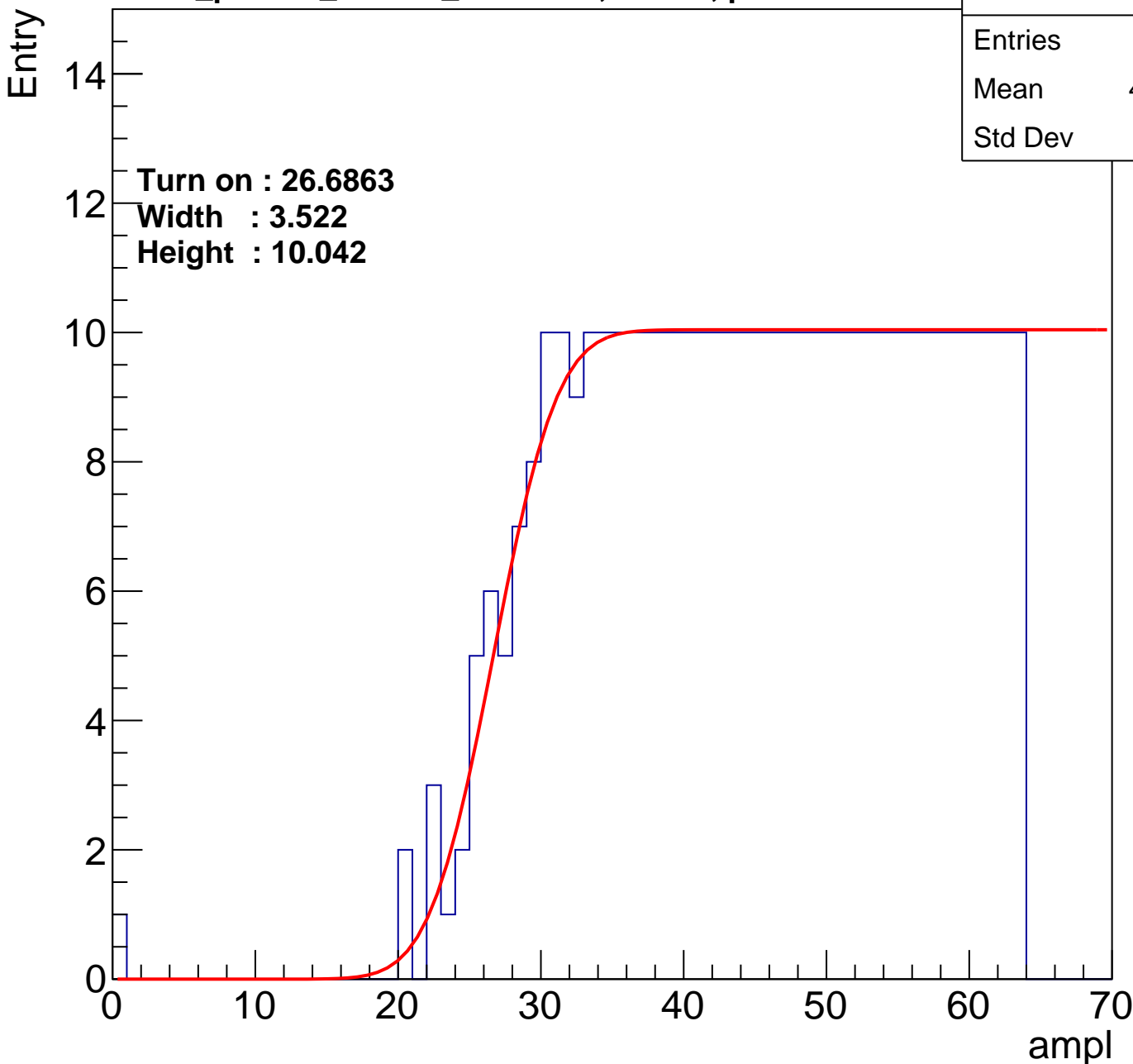
calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.32
Std Dev	11.4

Turn on : 26.6863

Width : 3.522

Height : 10.042



B0L102S, U24-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.6
Std Dev	11.84

Turn on : 24.8558

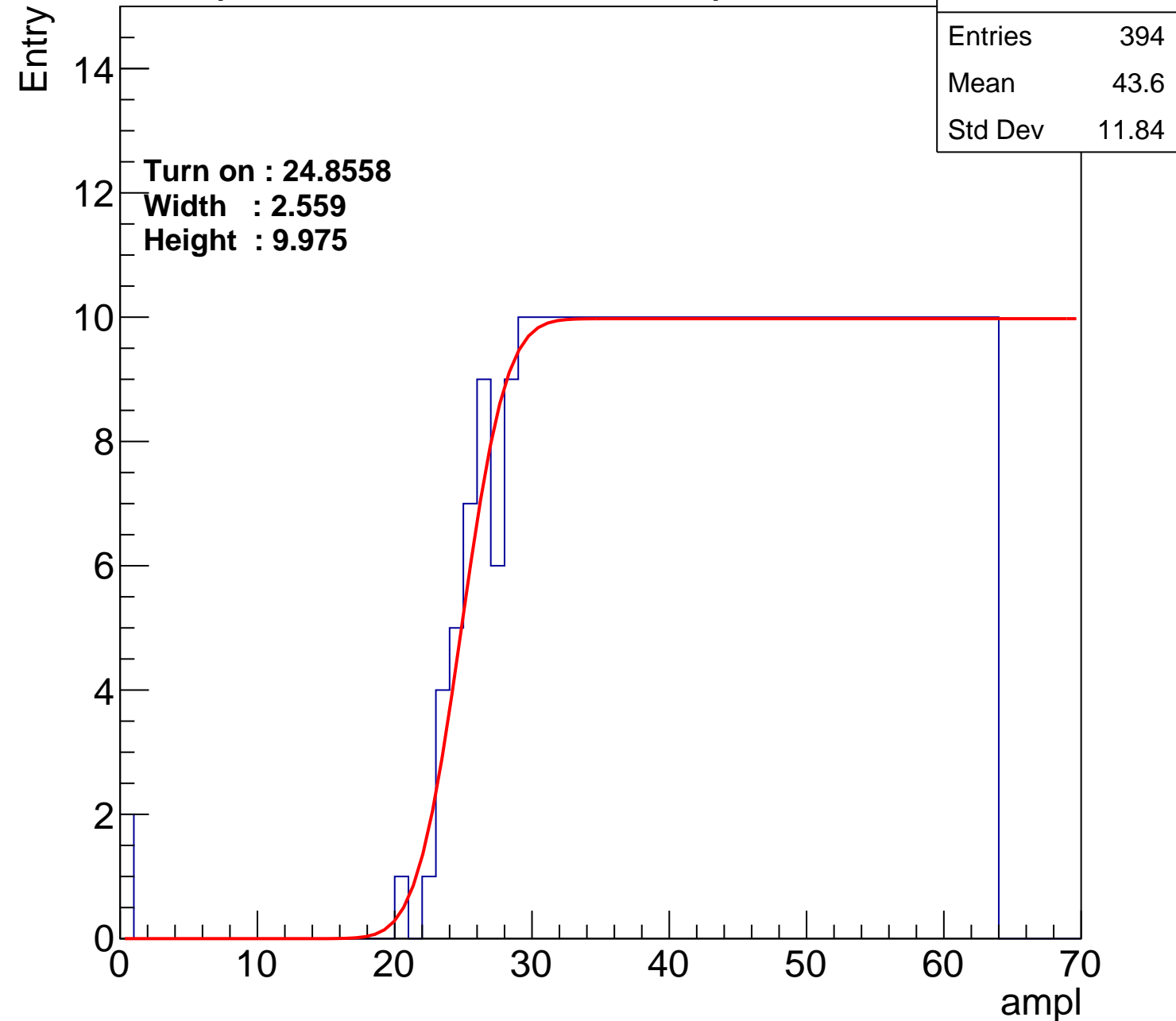
Width : 2.559

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.12
Std Dev	11.3

Turn on : 28.7378

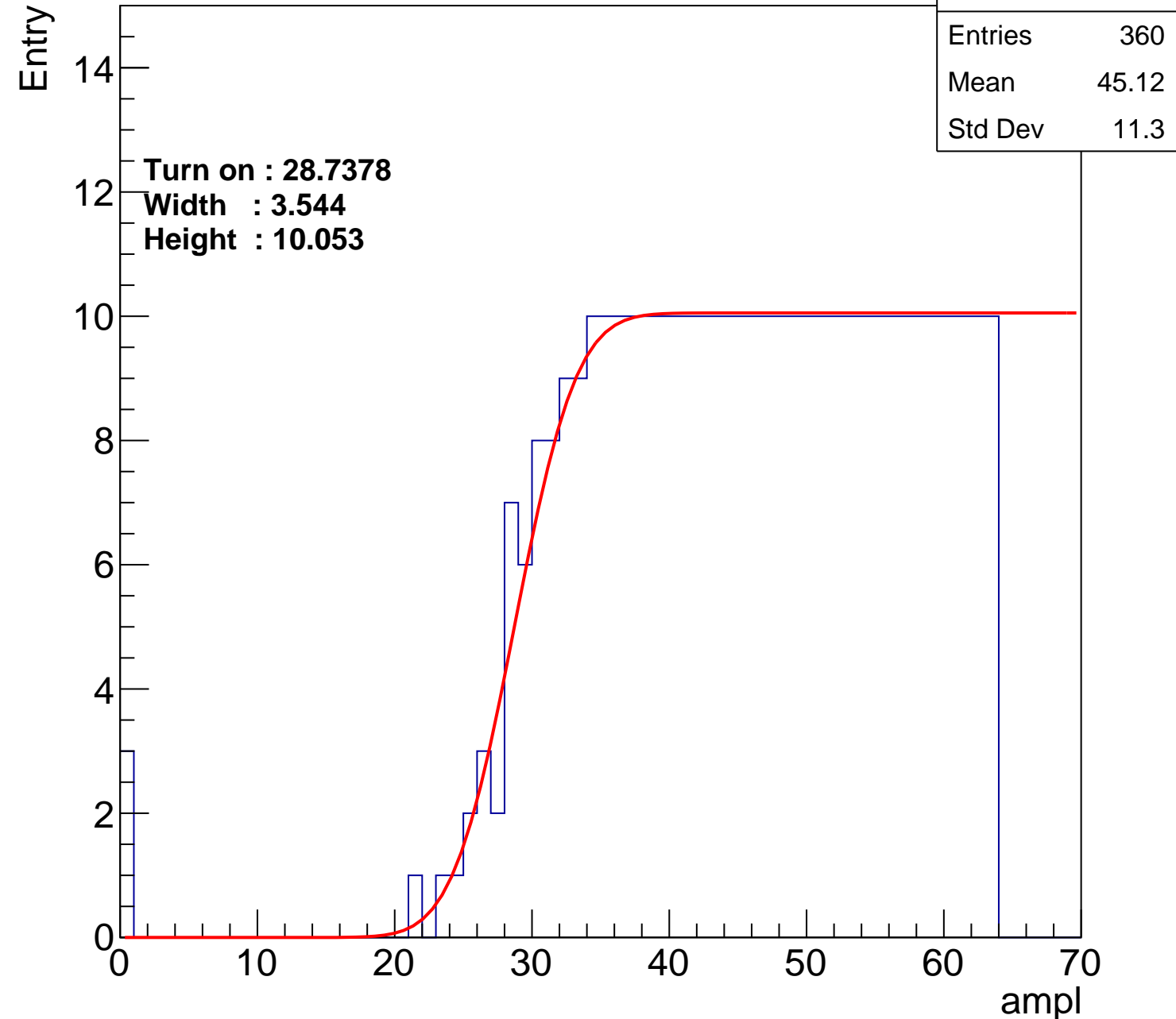
Width : 3.544

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.44
Std Dev	12.09

Turn on : 24.7767

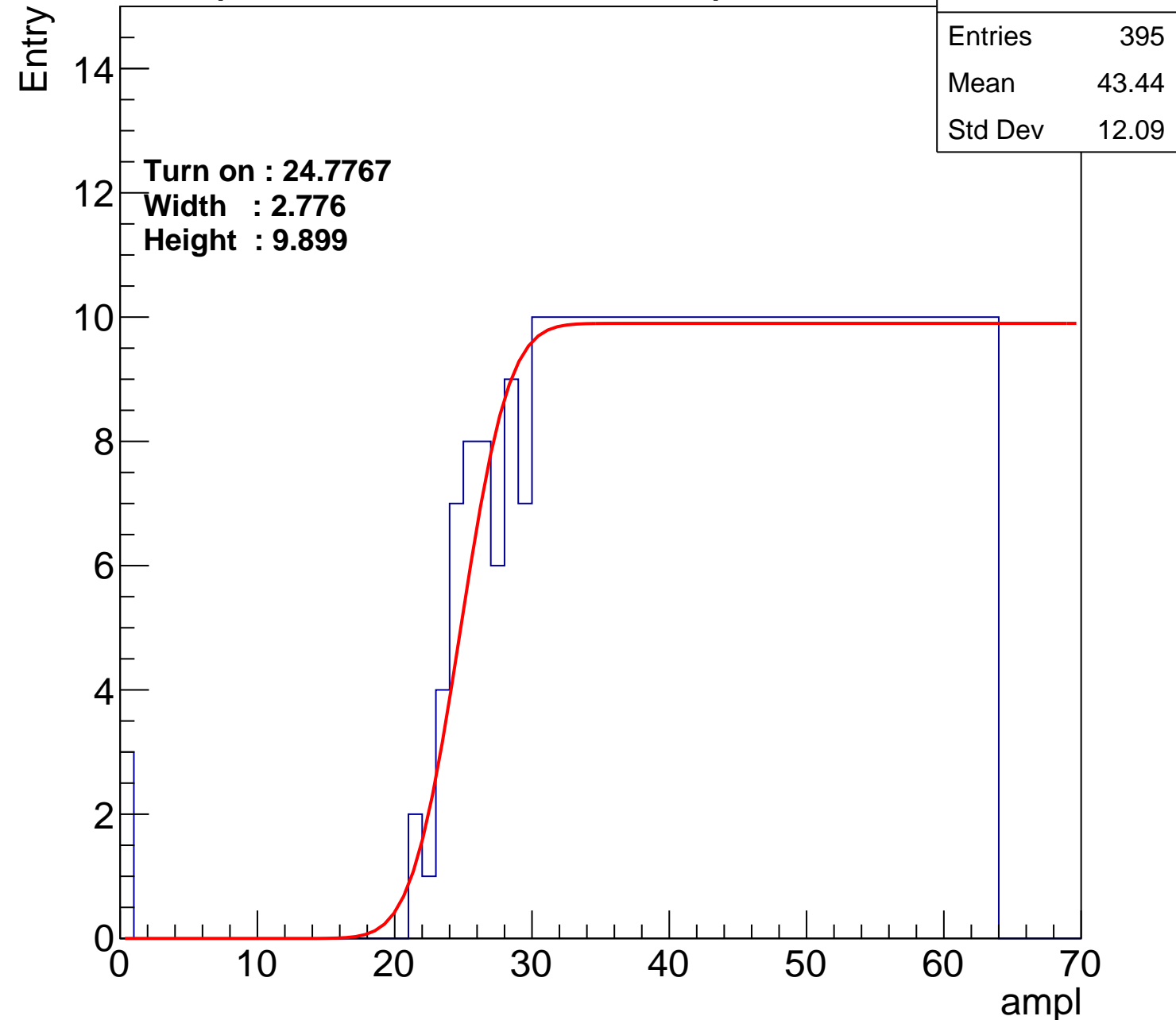
Width : 2.776

Height : 9.899

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch102

calib_packv5_042523_0143.root, FC#12, port B1

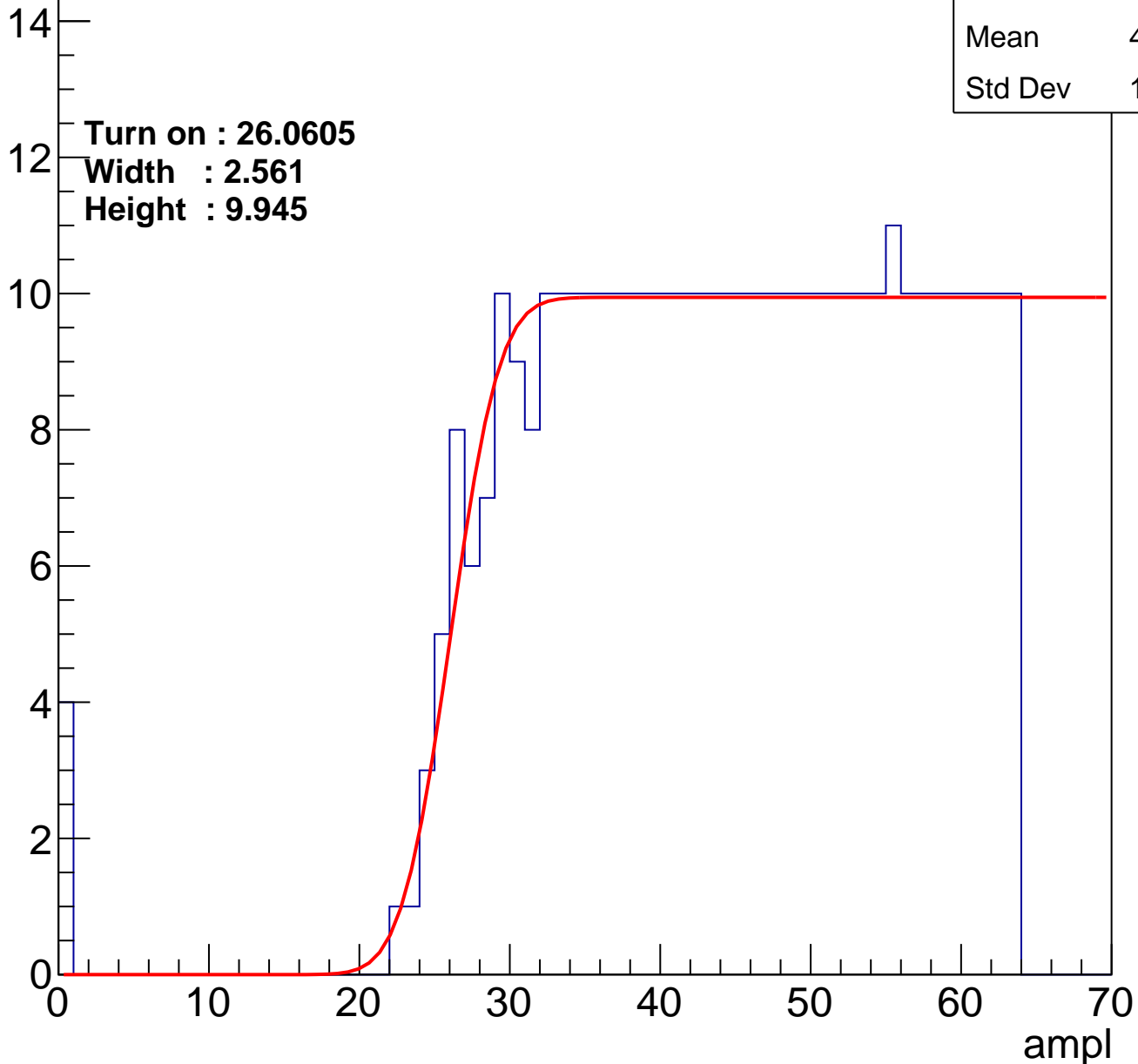
Entries	383
Mean	44.05
Std Dev	11.93

Turn on : 26.0605

Width : 2.561

Height : 9.945

Entry



B0L102S, U24-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.37
Std Dev	12.22

Turn on : 25.0359

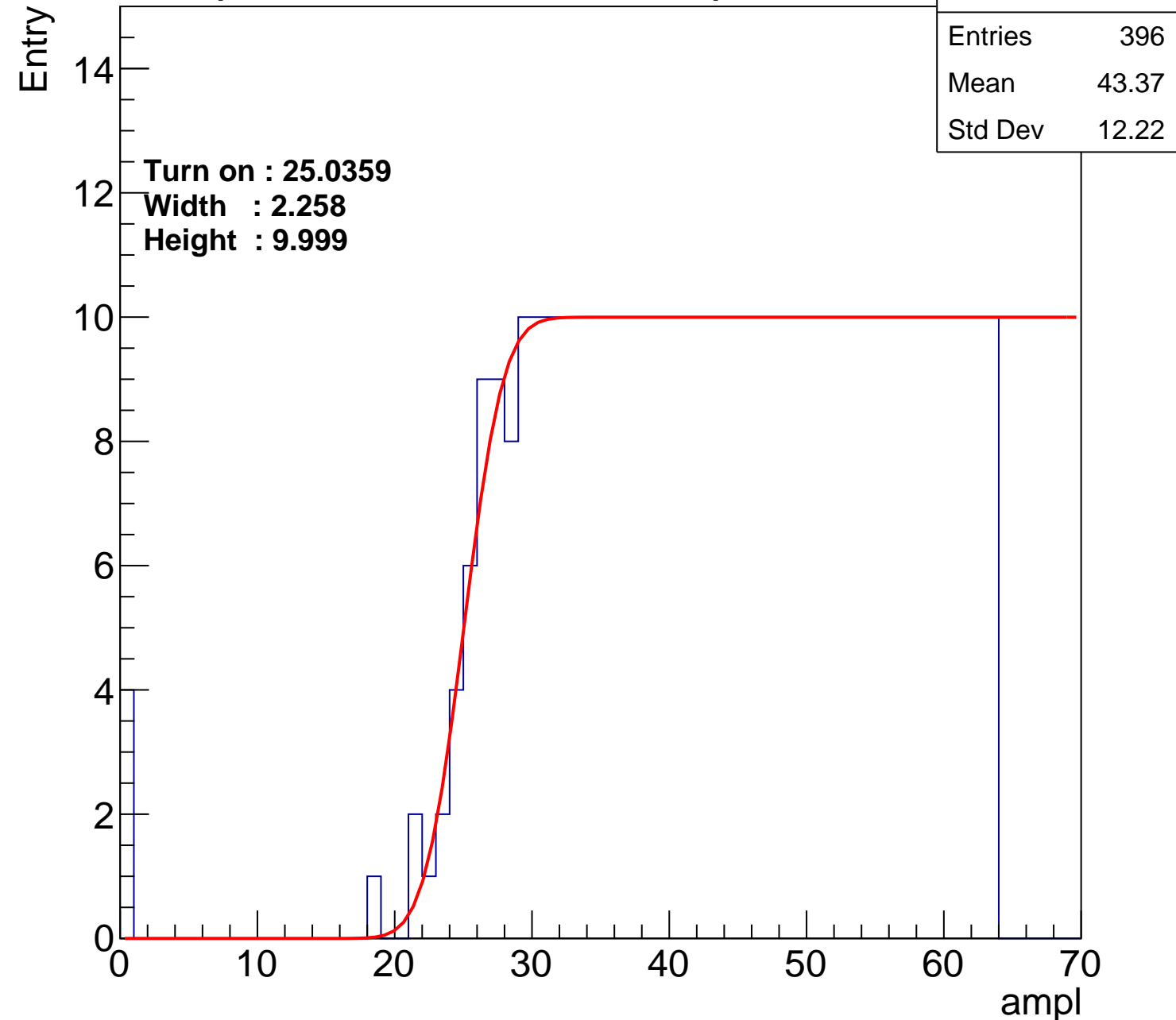
Width : 2.258

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.4022

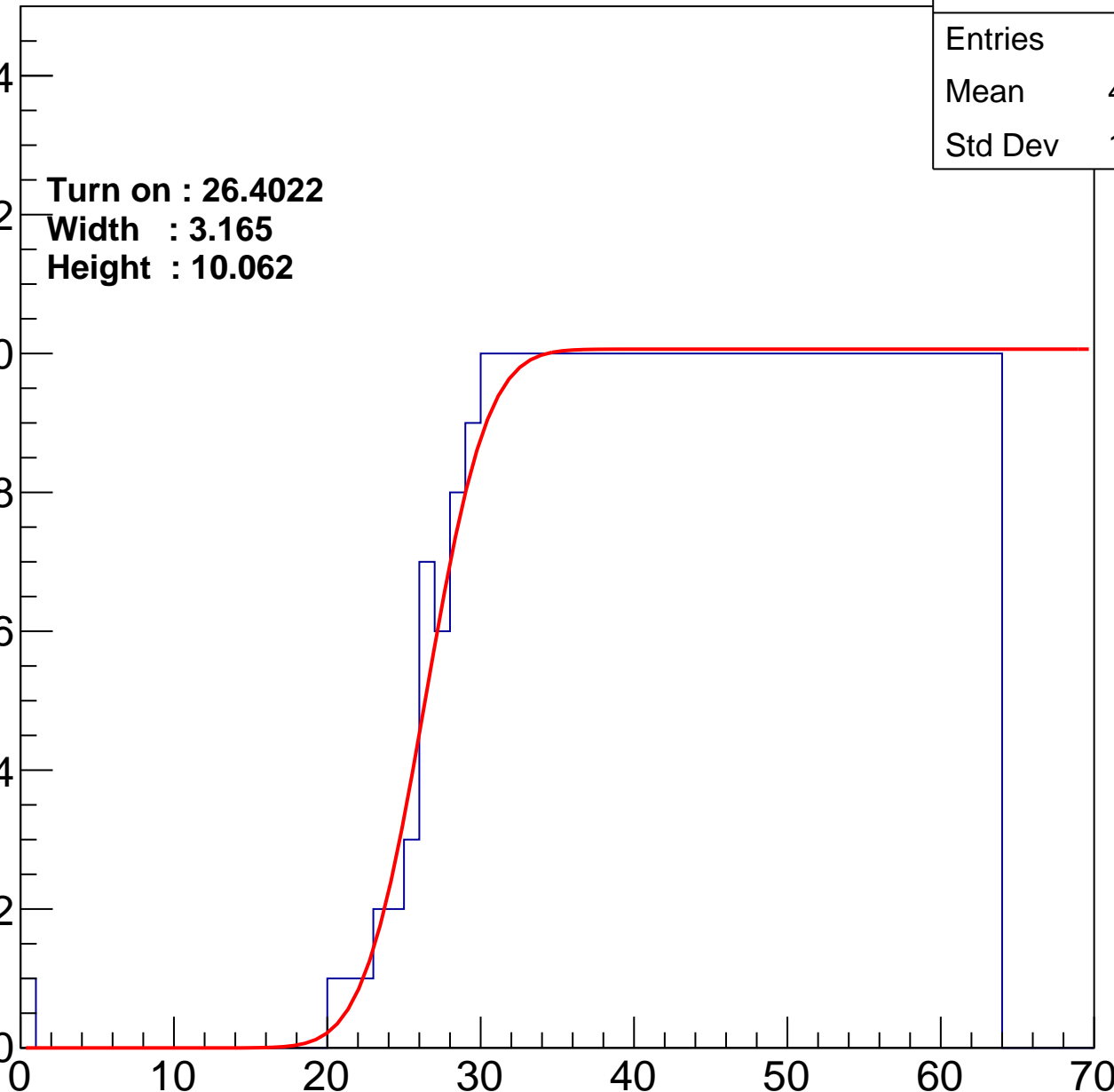
Width : 3.165

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.31
Std Dev	12.26

Turn on : 25.1584

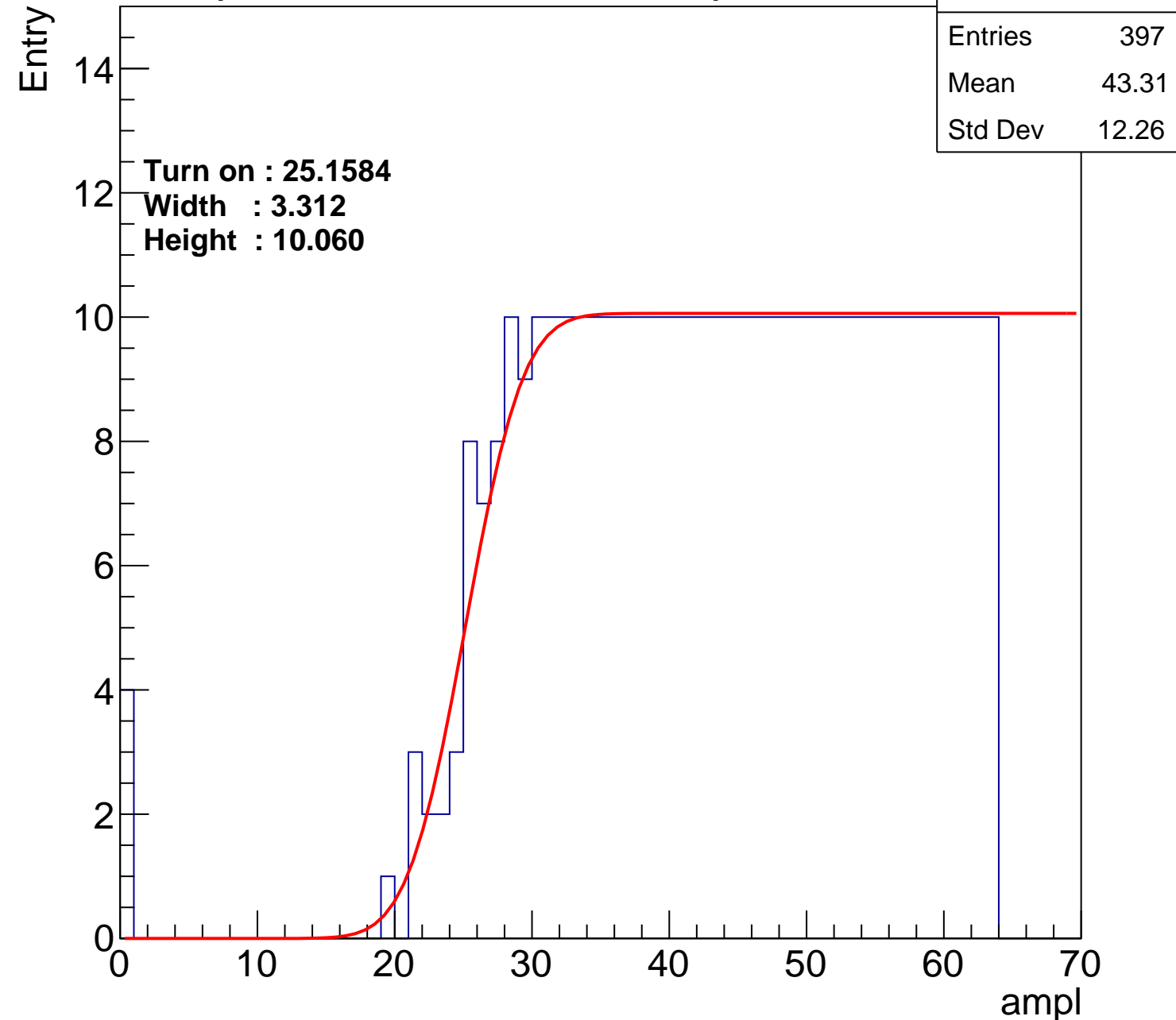
Width : 3.312

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch106

calib_packv5_042523_0143.root, FC#12, port B1

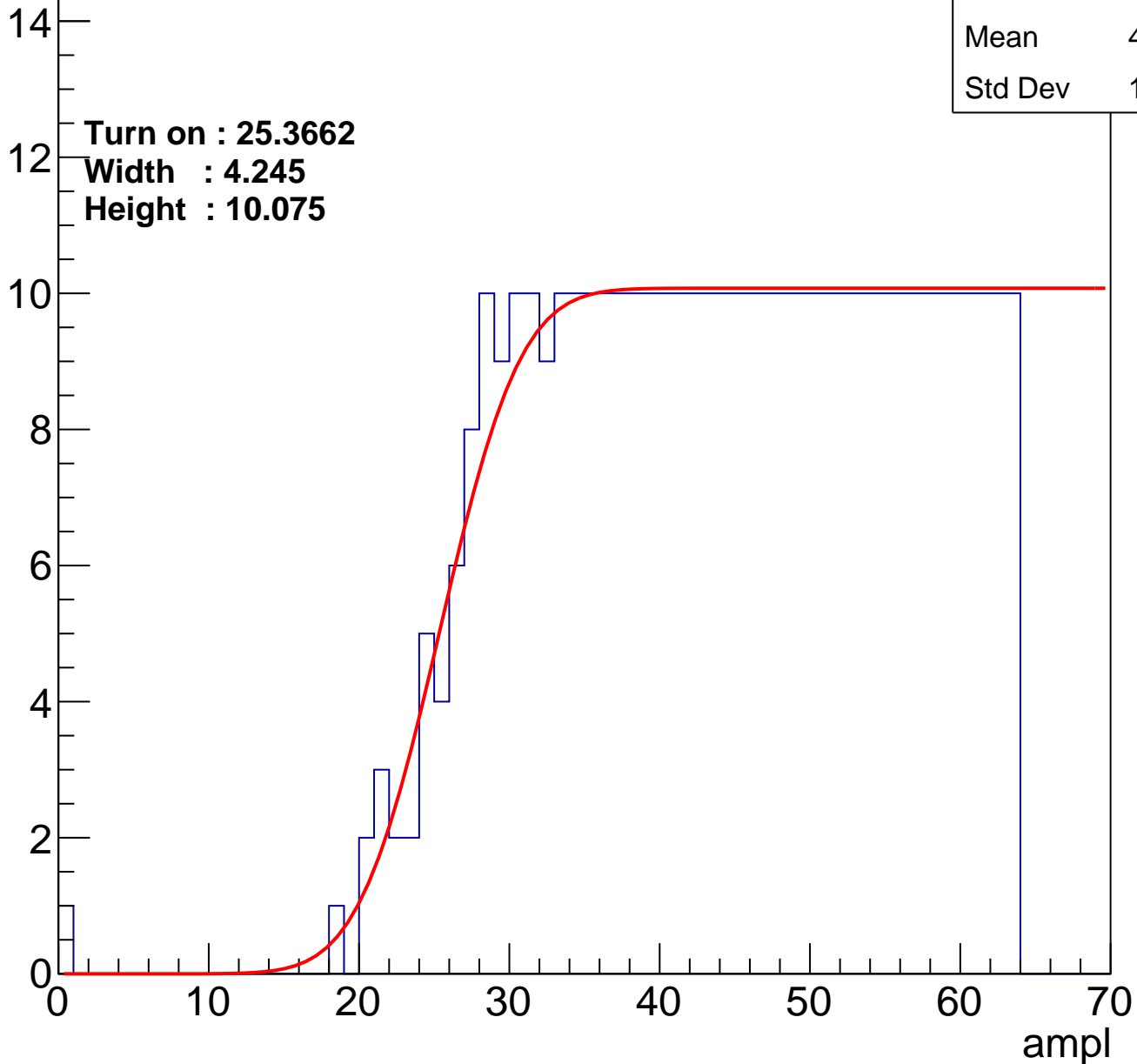
Entries	392
Mean	43.68
Std Dev	11.75

Turn on : 25.3662

Width : 4.245

Height : 10.075

Entry



B0L102S, U24-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.13
Std Dev	12.05

Turn on : 27.0156

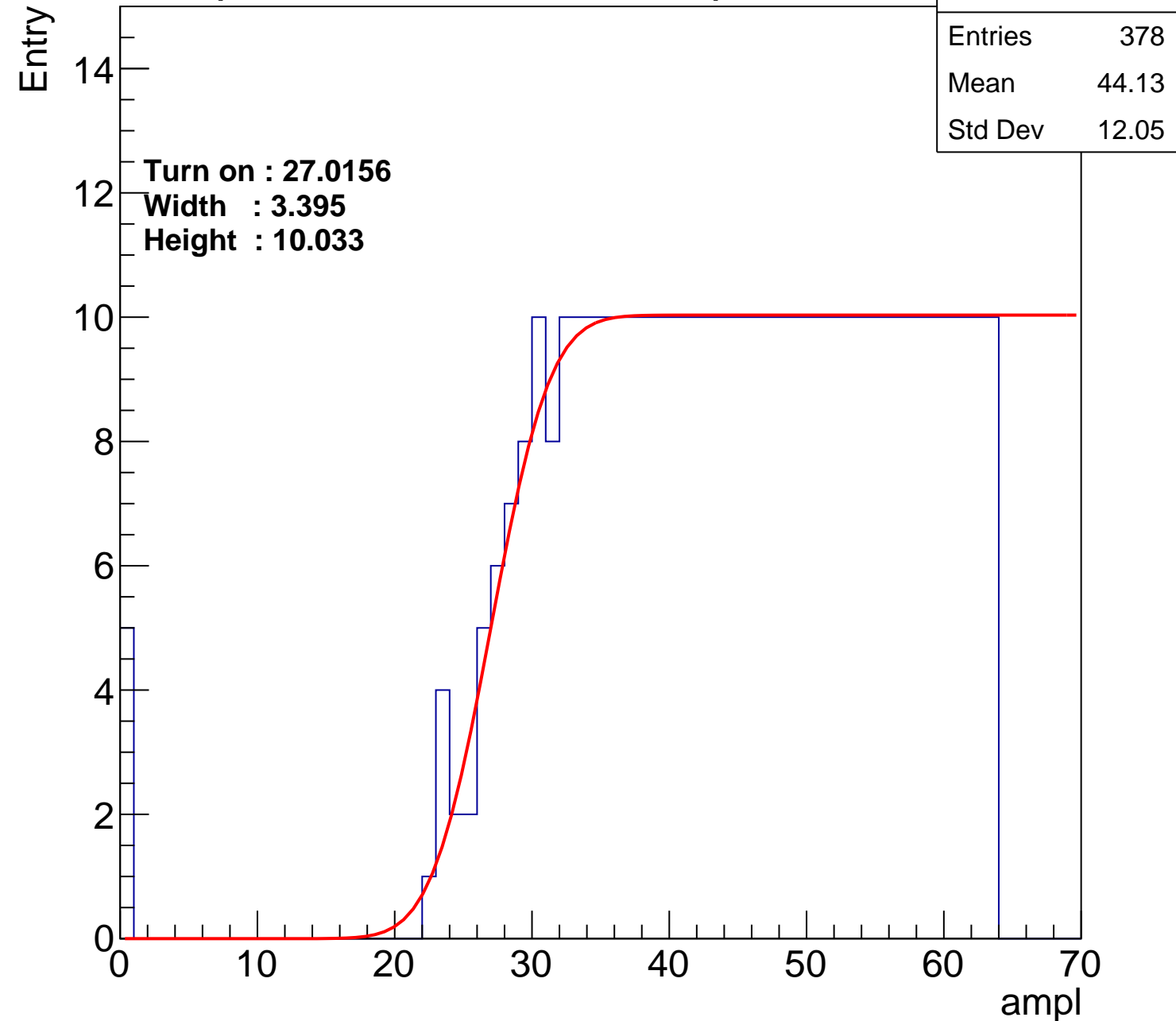
Width : 3.395

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.86
Std Dev	12.38

Turn on : 24.0490

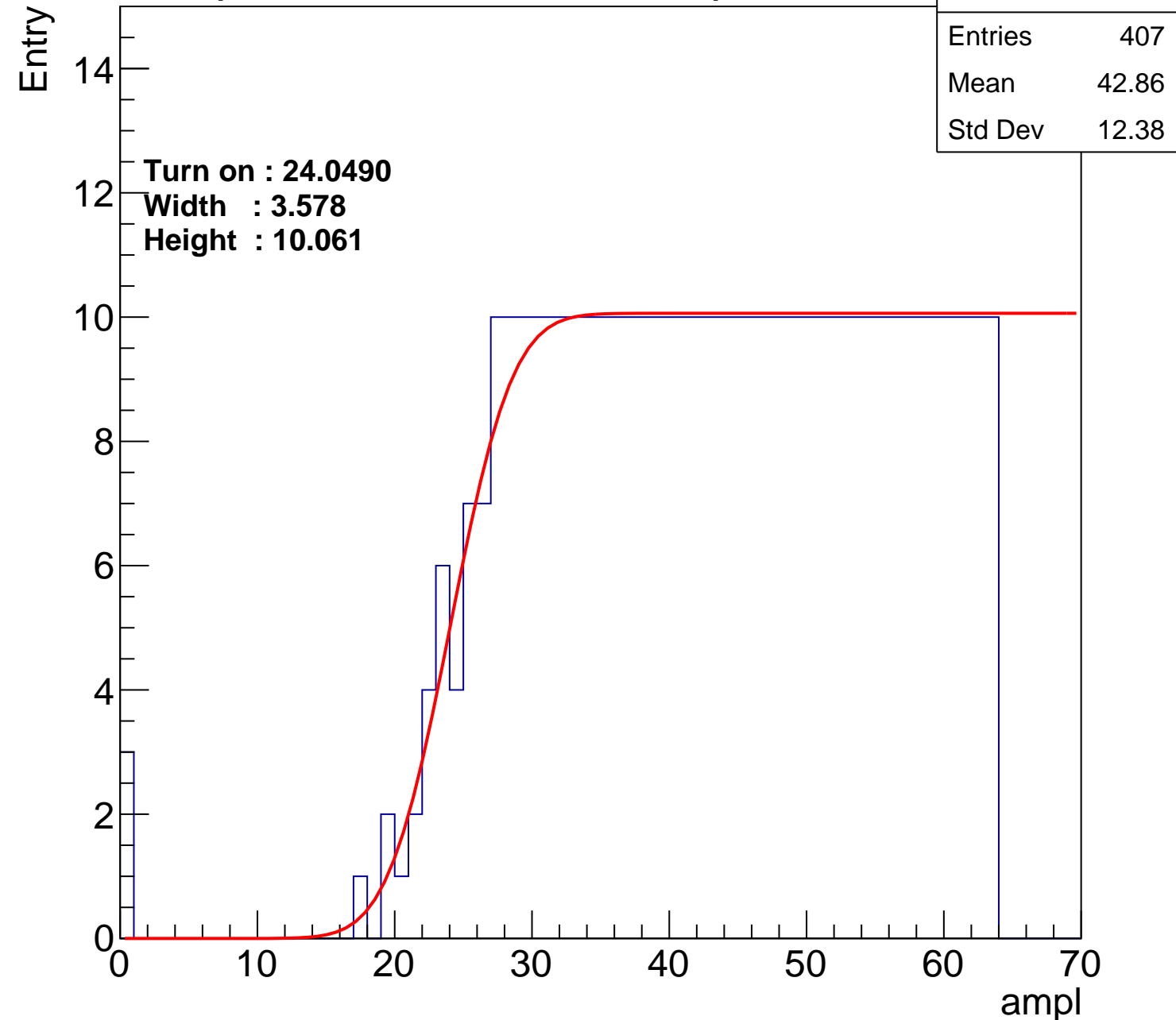
Width : 3.578

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.58
Std Dev	11.87

Turn on : 25.2546

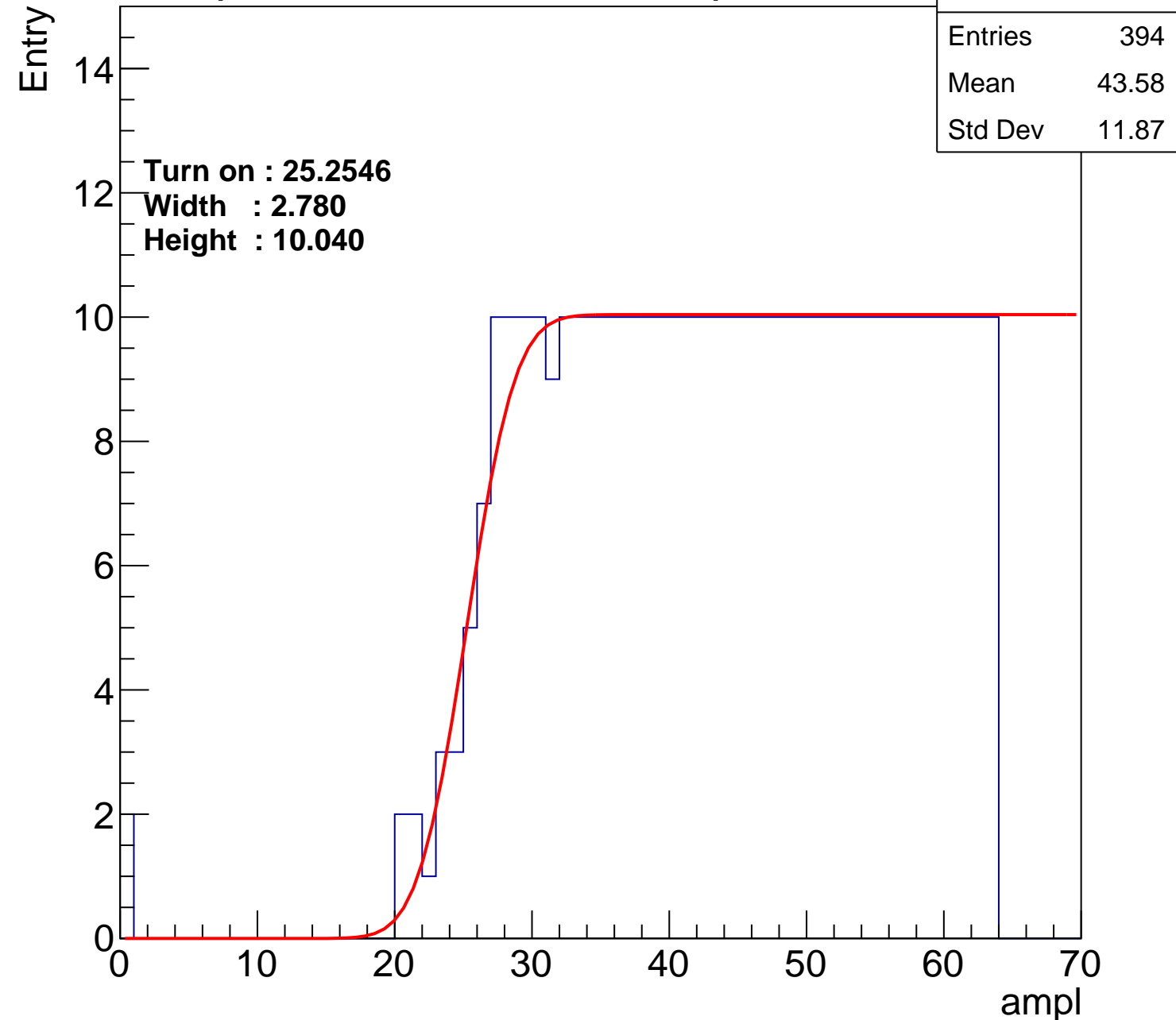
Width : 2.780

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch110

calib_packv5_042523_0143.root, FC#12, port B1

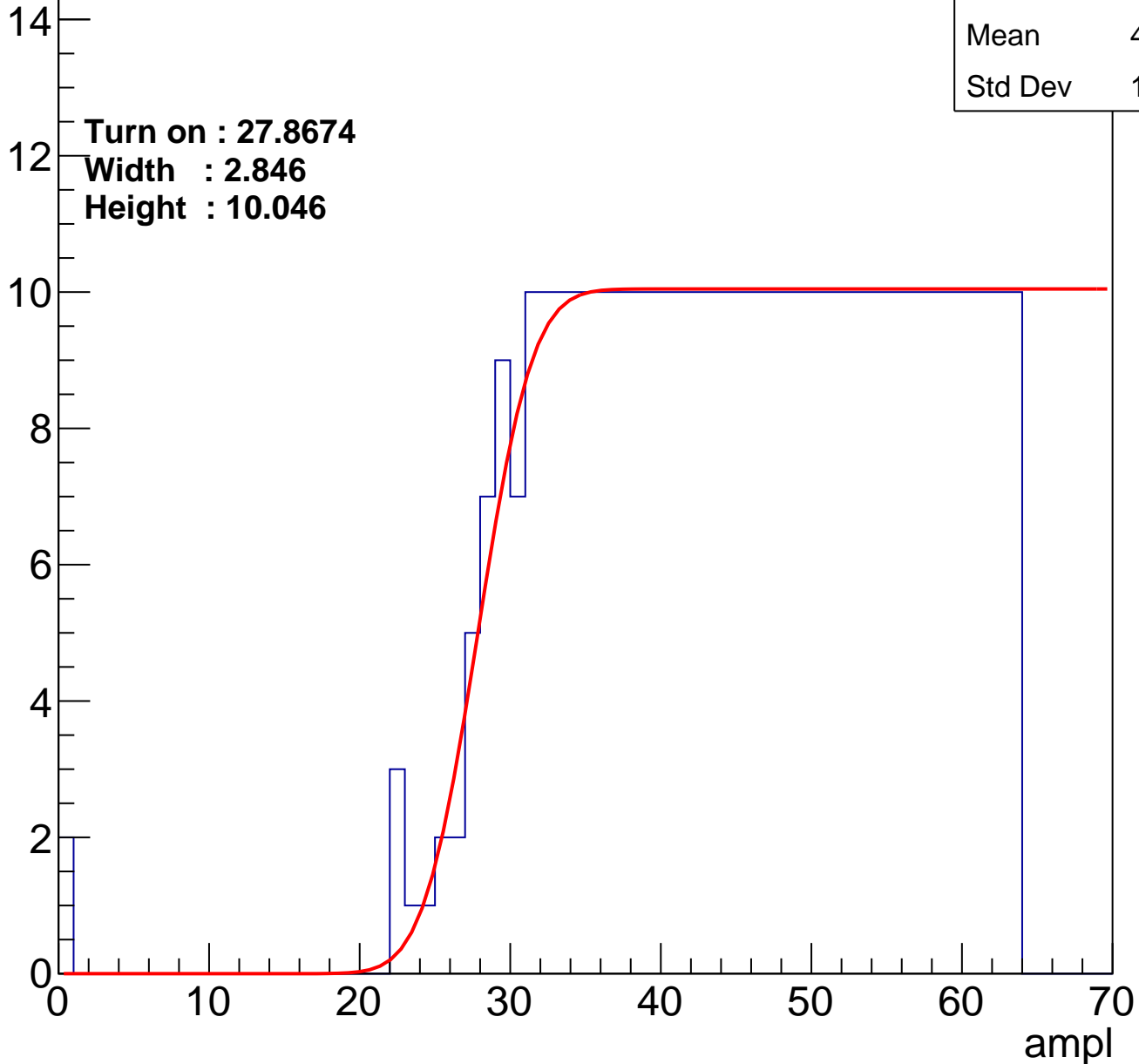
Entries	369
Mean	44.79
Std Dev	11.27

Turn on : 27.8674

Width : 2.846

Height : 10.046

Entry



B0L102S, U24-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.23
Std Dev	11.18

Turn on : 28.3700

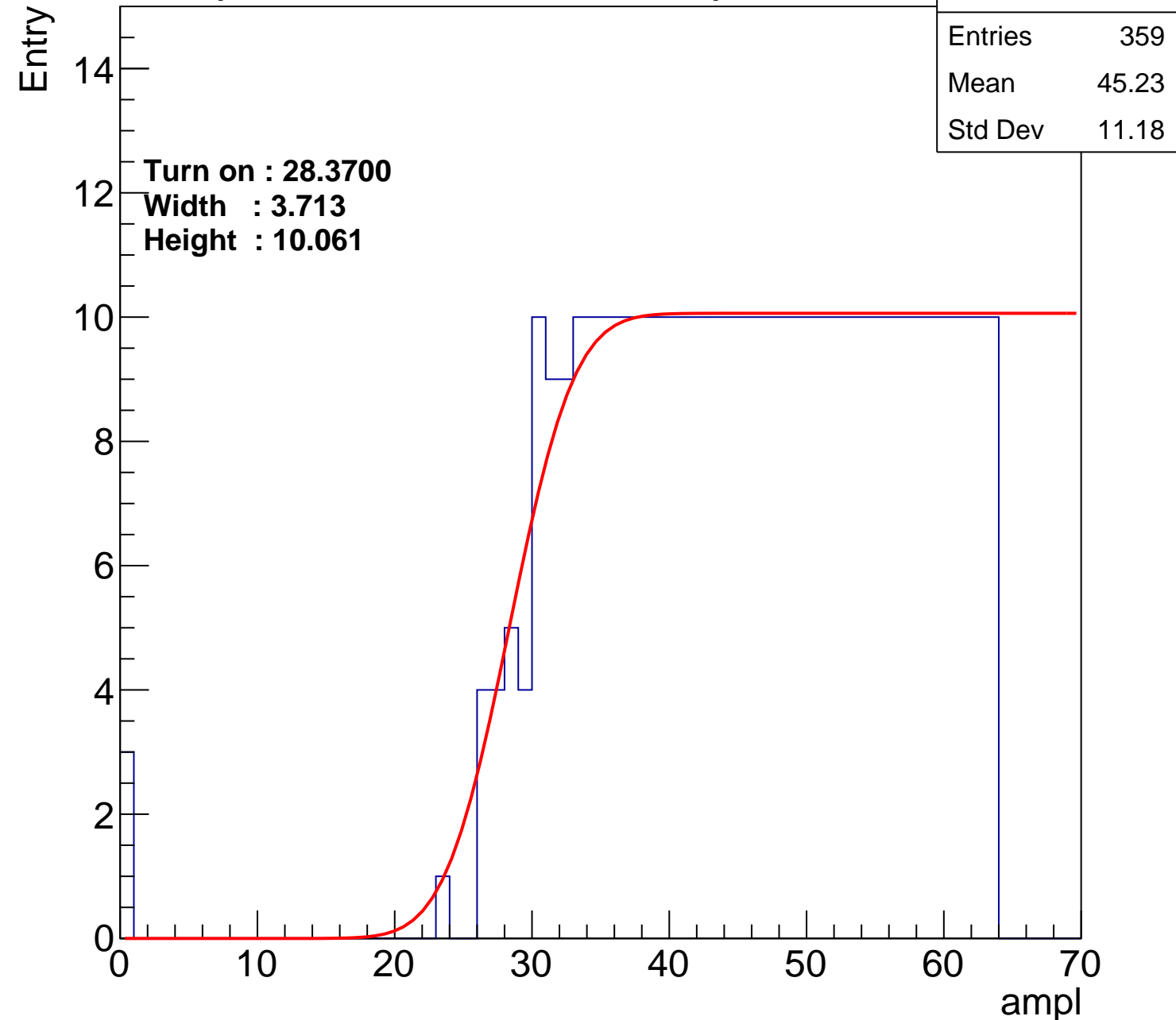
Width : 3.713

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.31
Std Dev	11.55

Turn on : 25.5187

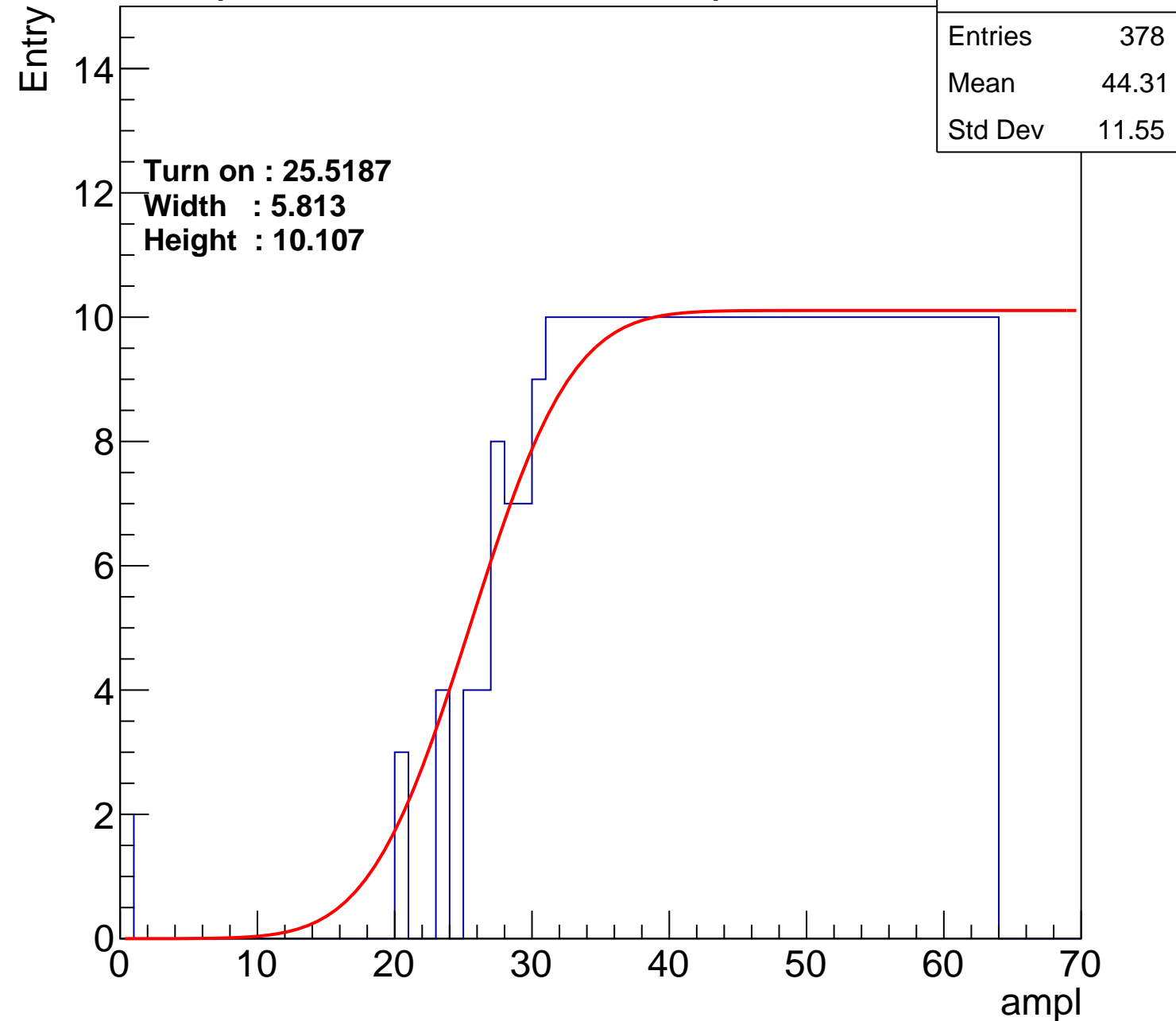
Width : 5.813

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.23
Std Dev	11.66

Turn on : 26.8800

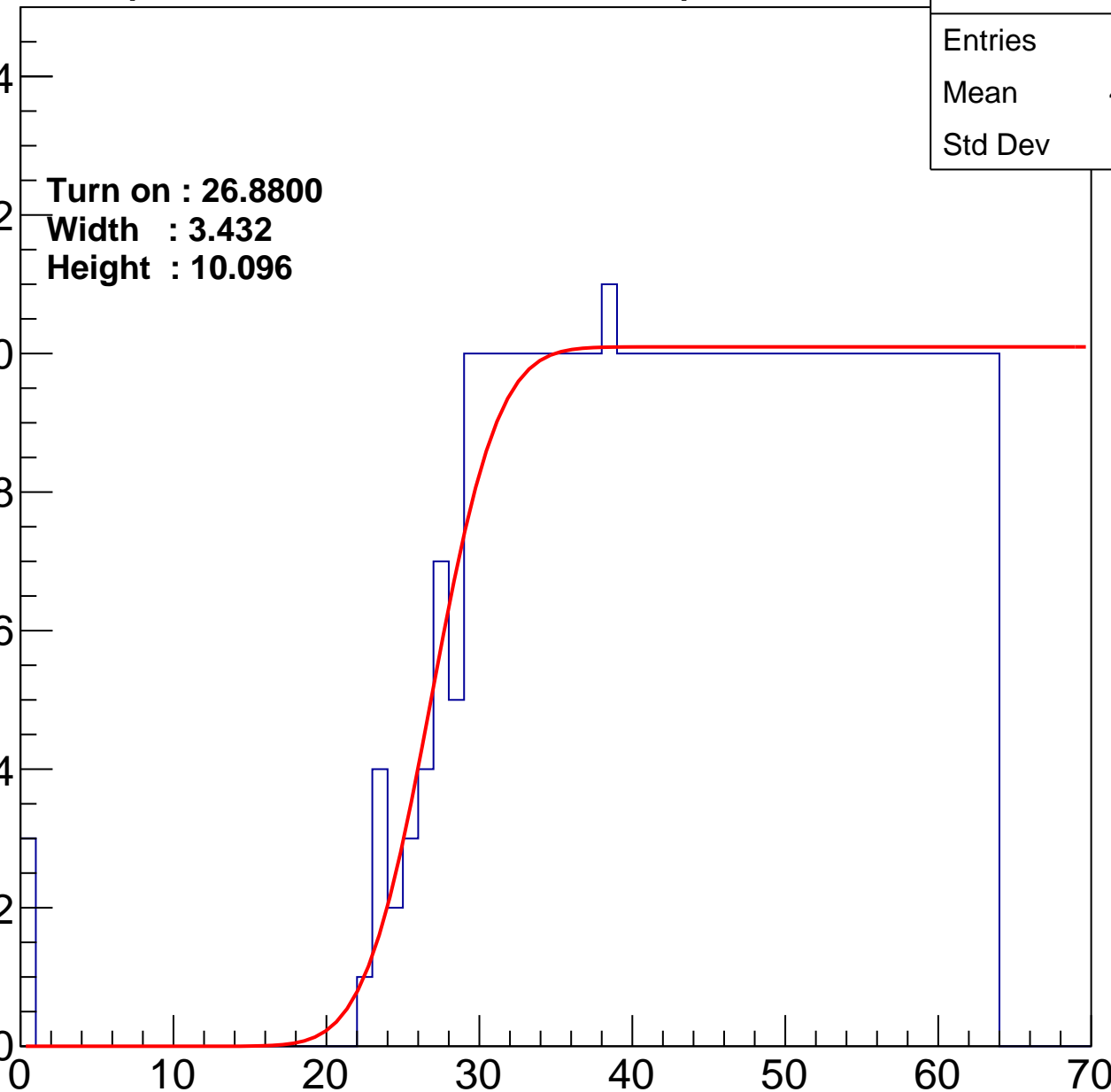
Width : 3.432

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.13
Std Dev	12.39

Turn on : 24.9196

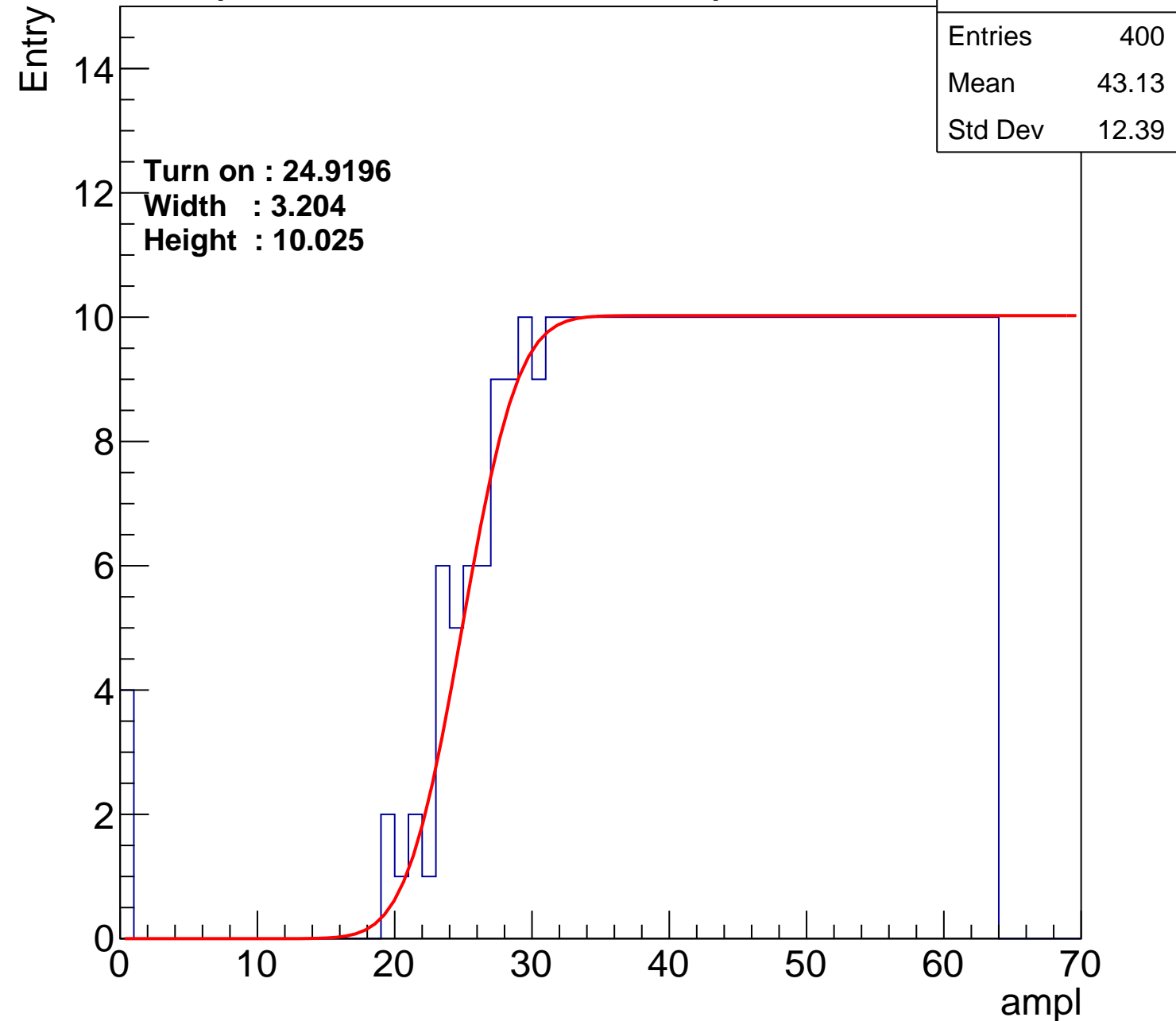
Width : 3.204

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 27.4215

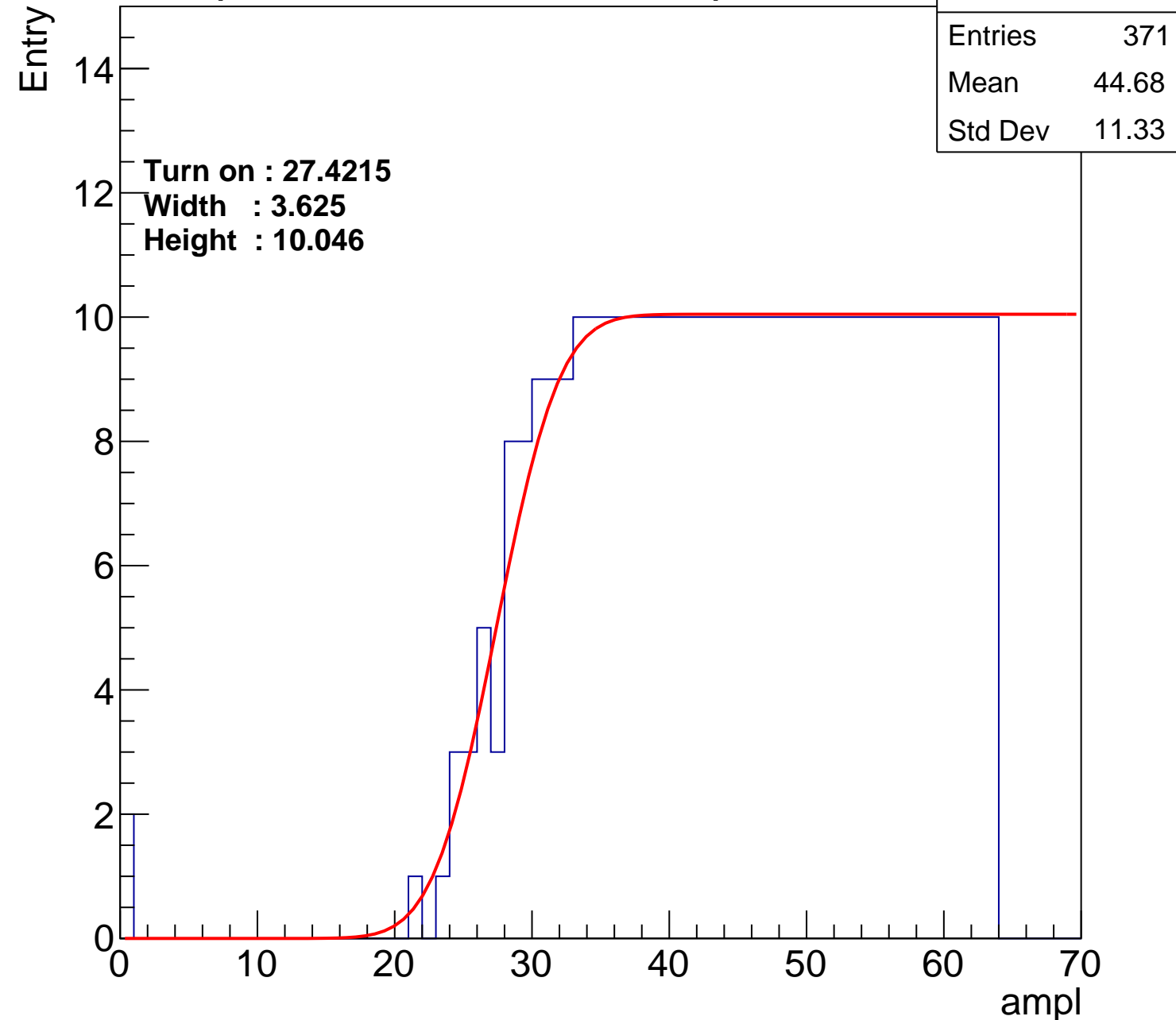
Width : 3.625

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.06
Std Dev	12.39

Turn on : 24.0845

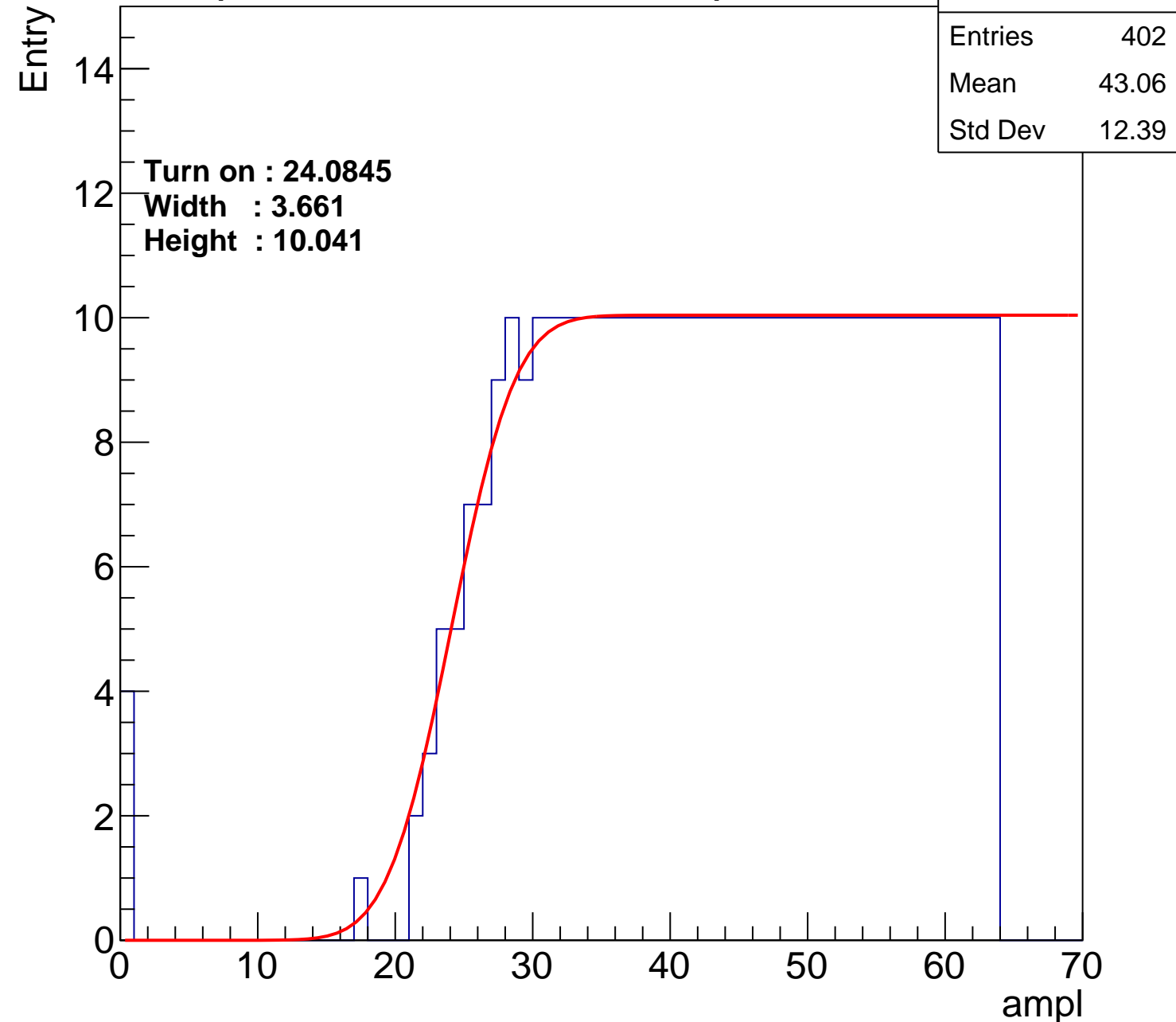
Width : 3.661

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.22
Std Dev	11.71

Turn on : 26.8422

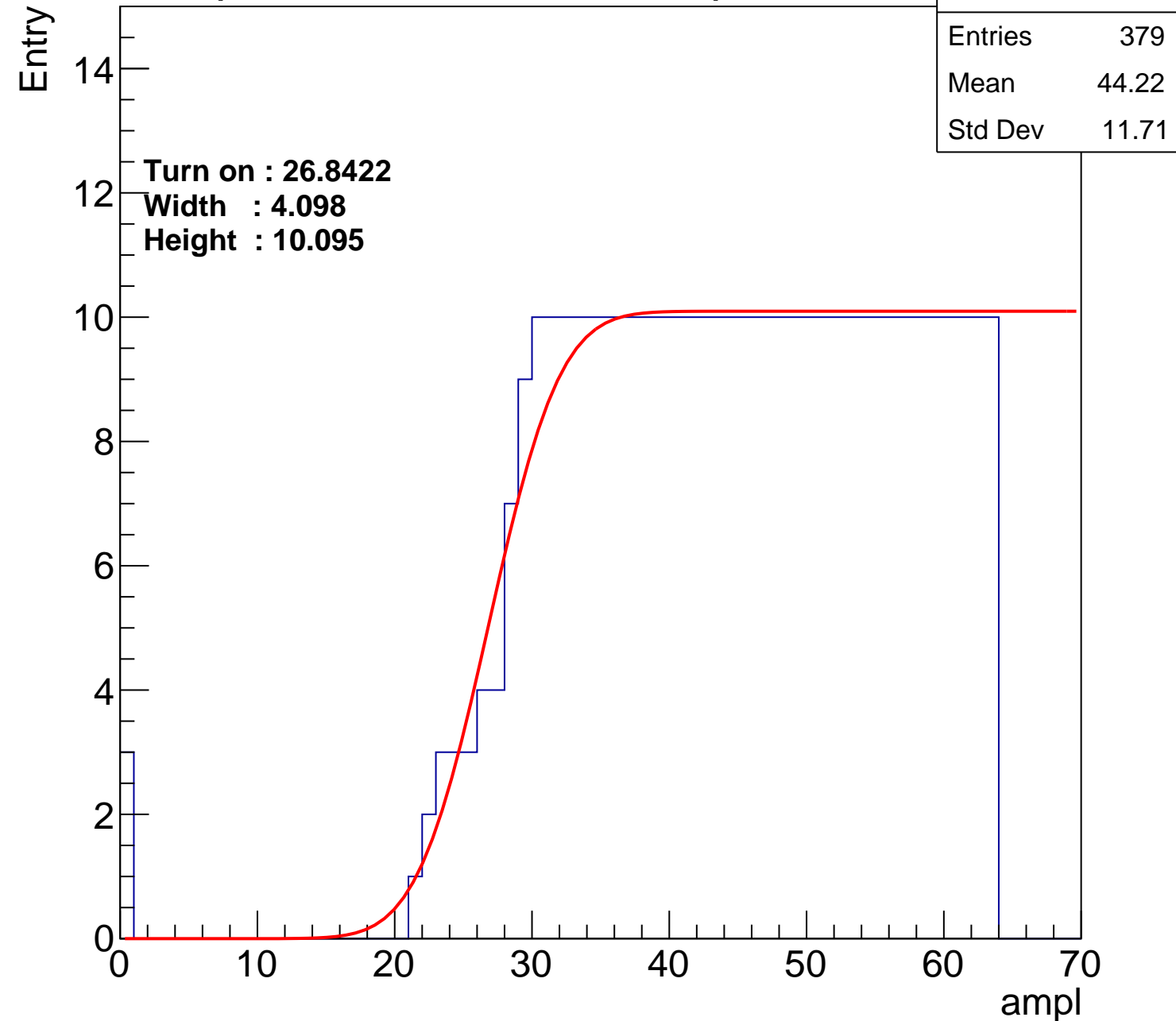
Width : 4.098

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.01
Std Dev	11.83

Turn on : 26.5488

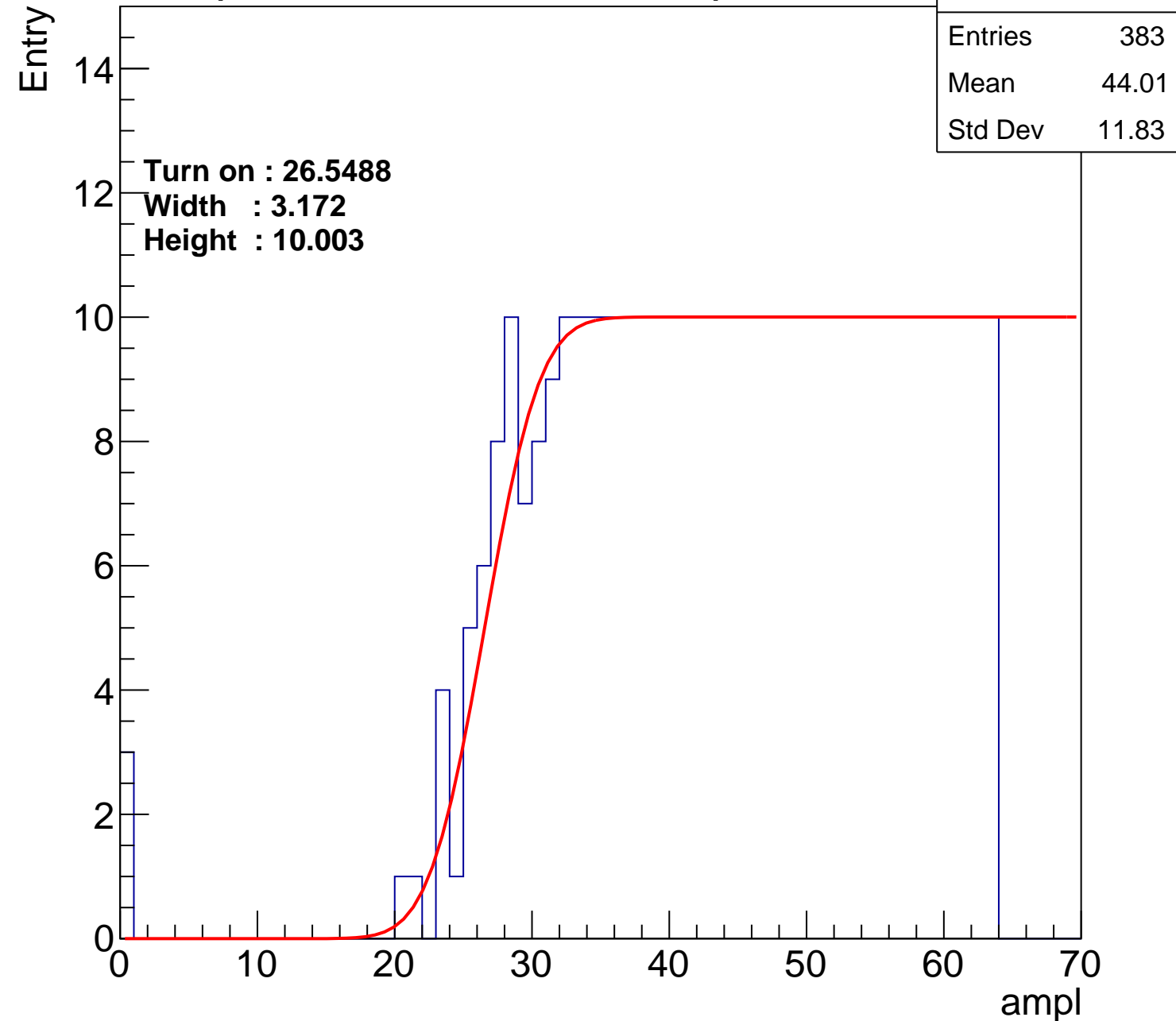
Width : 3.172

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.27
Std Dev	11.68

Turn on : 27.0797

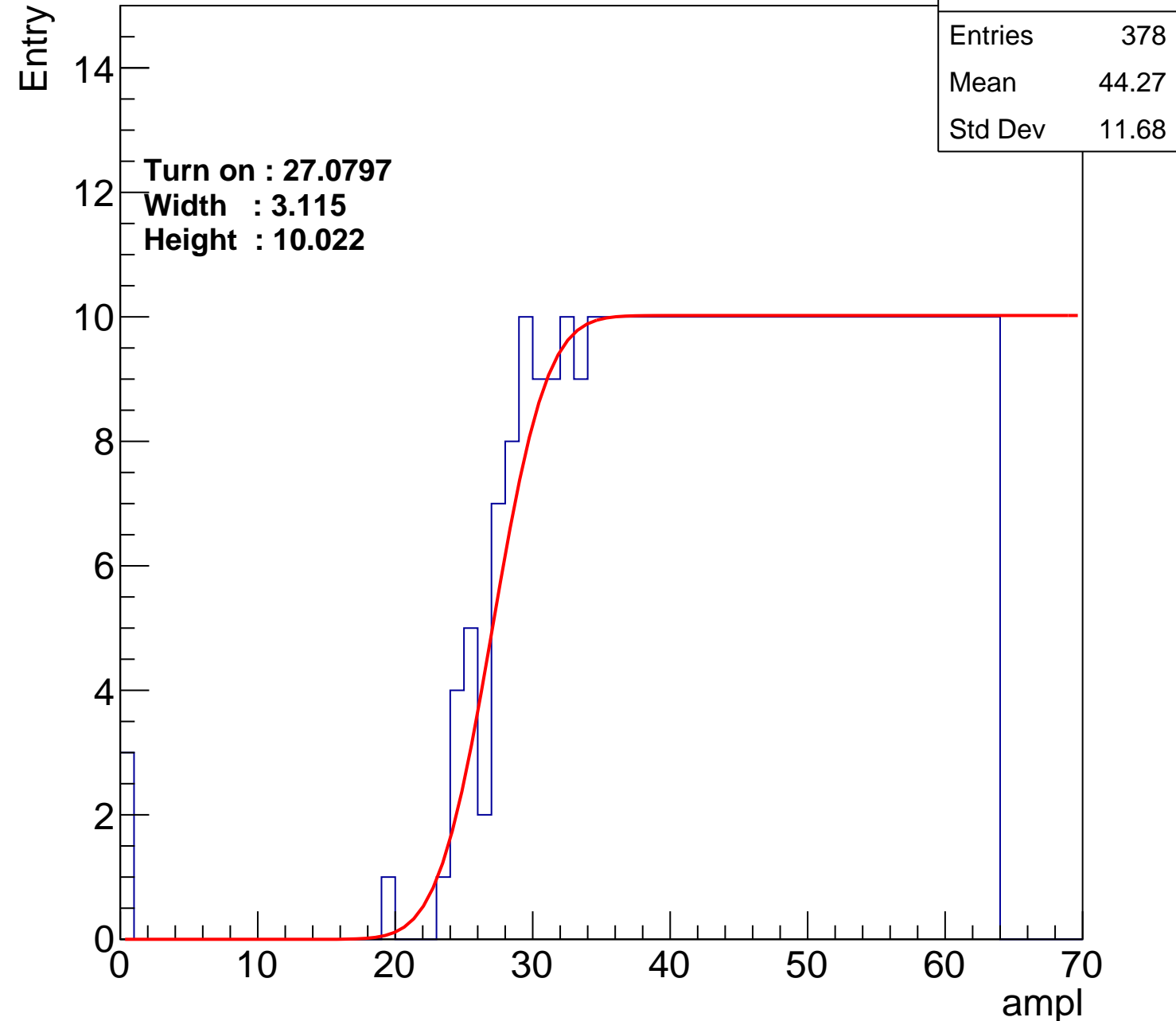
Width : 3.115

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch120

calib_packv5_042523_0143.root, FC#12, port B1

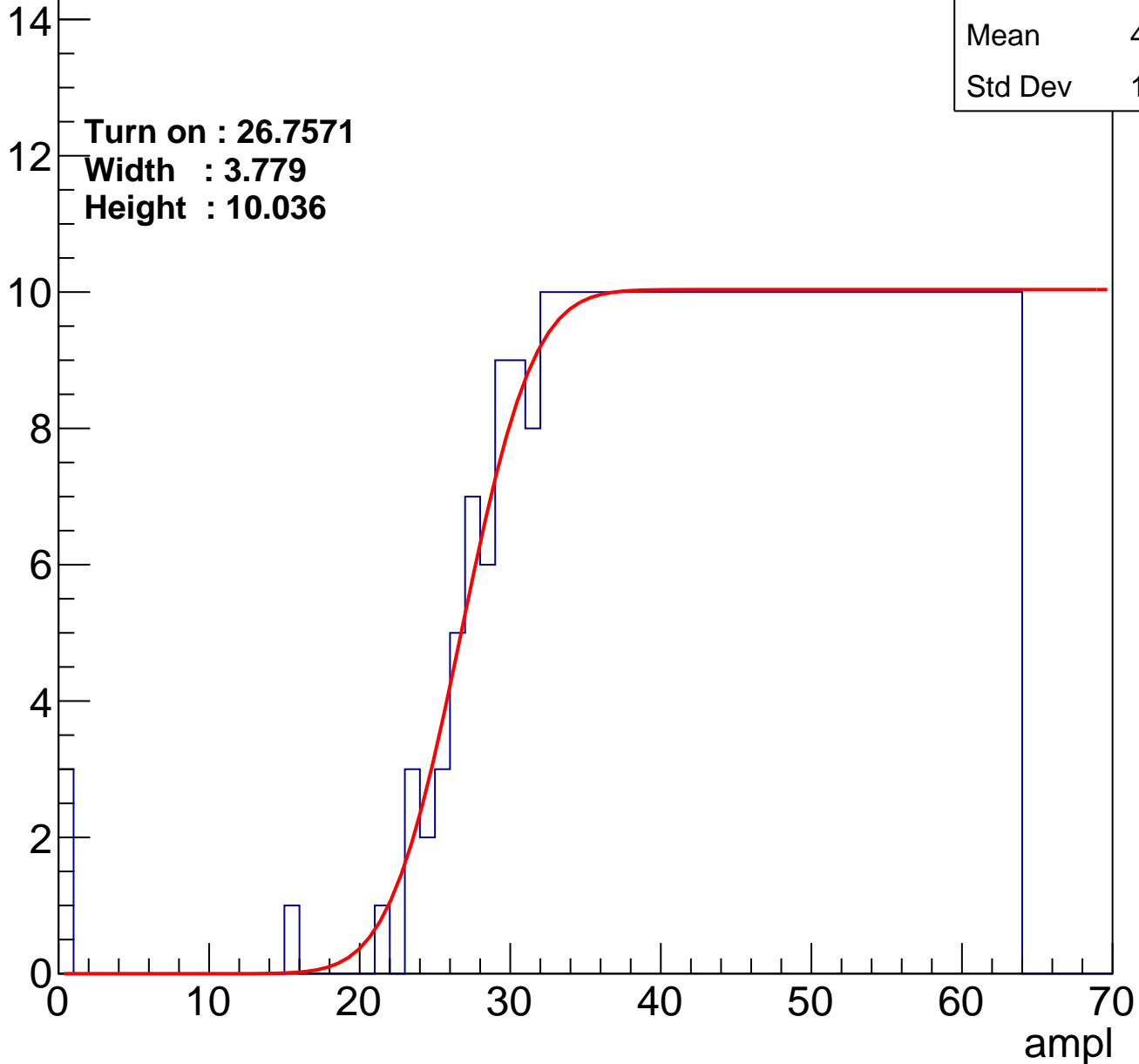
Entries	377
Mean	44.28
Std Dev	11.73

Turn on : 26.7571

Width : 3.779

Height : 10.036

Entry



B0L102S, U24-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.45
Std Dev	12.19

Turn on : 25.2739

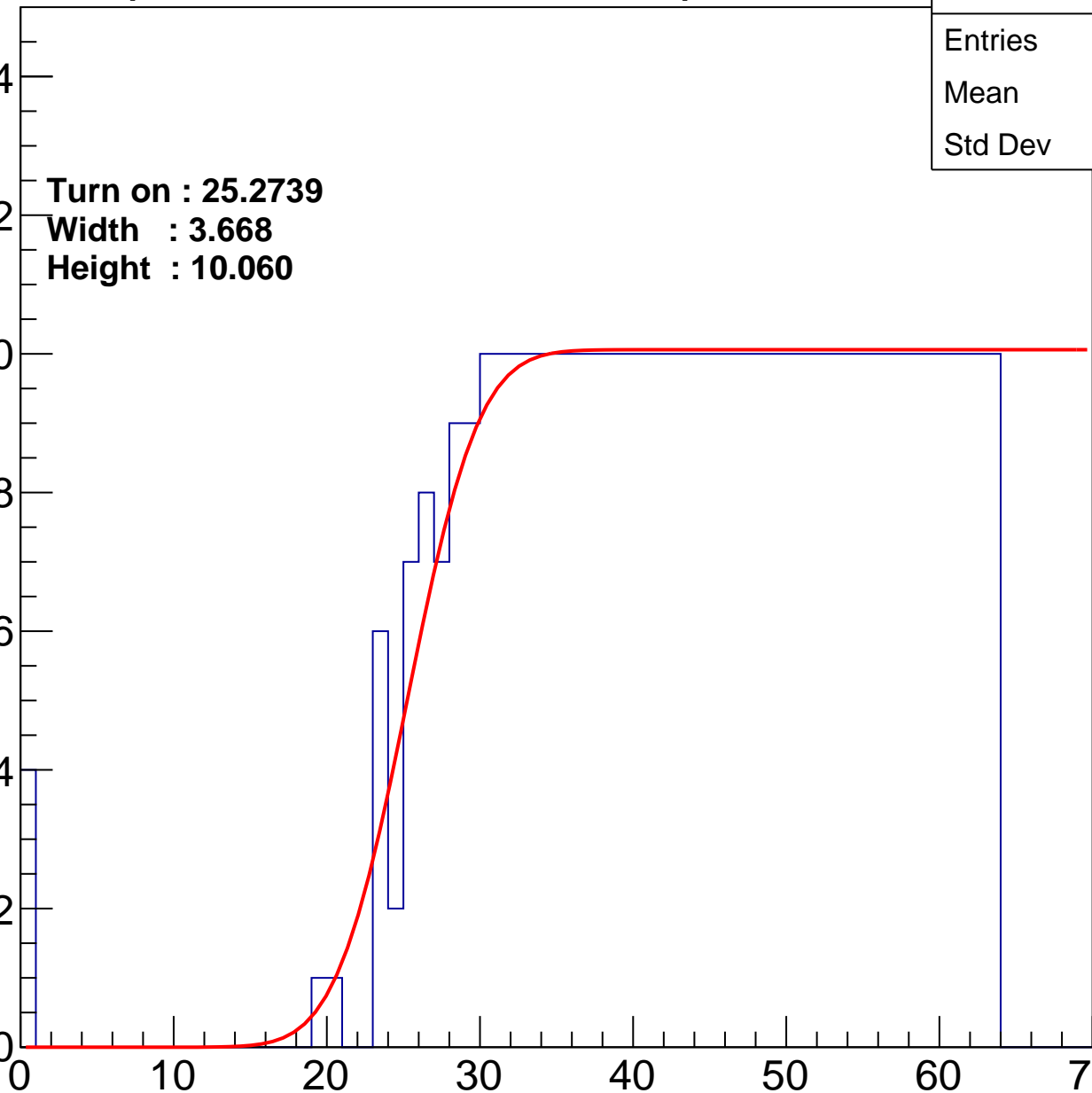
Width : 3.668

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.73
Std Dev	11.84

Turn on : 25.0314

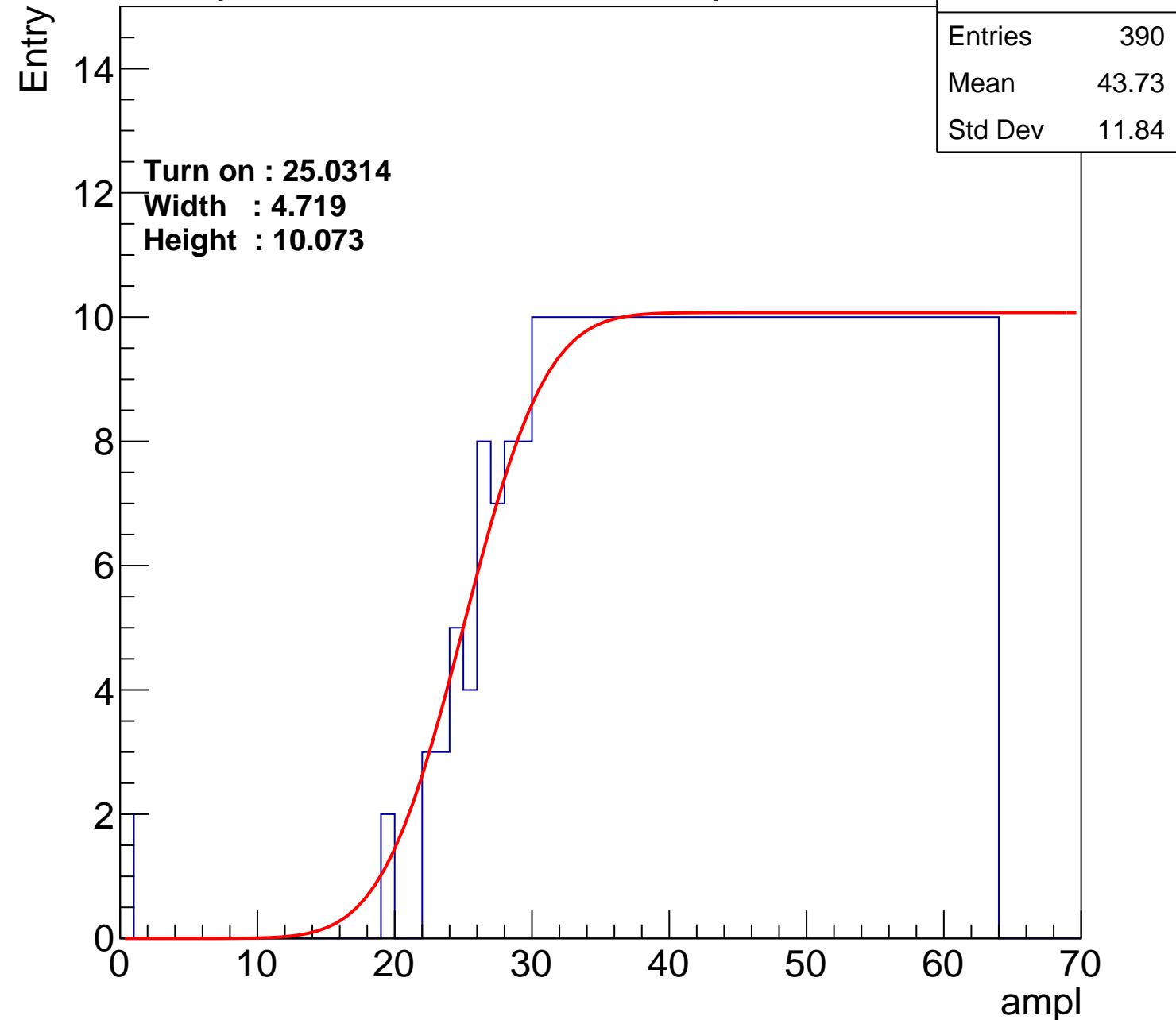
Width : 4.719

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.37
Std Dev	11.81

Turn on : 27.2647

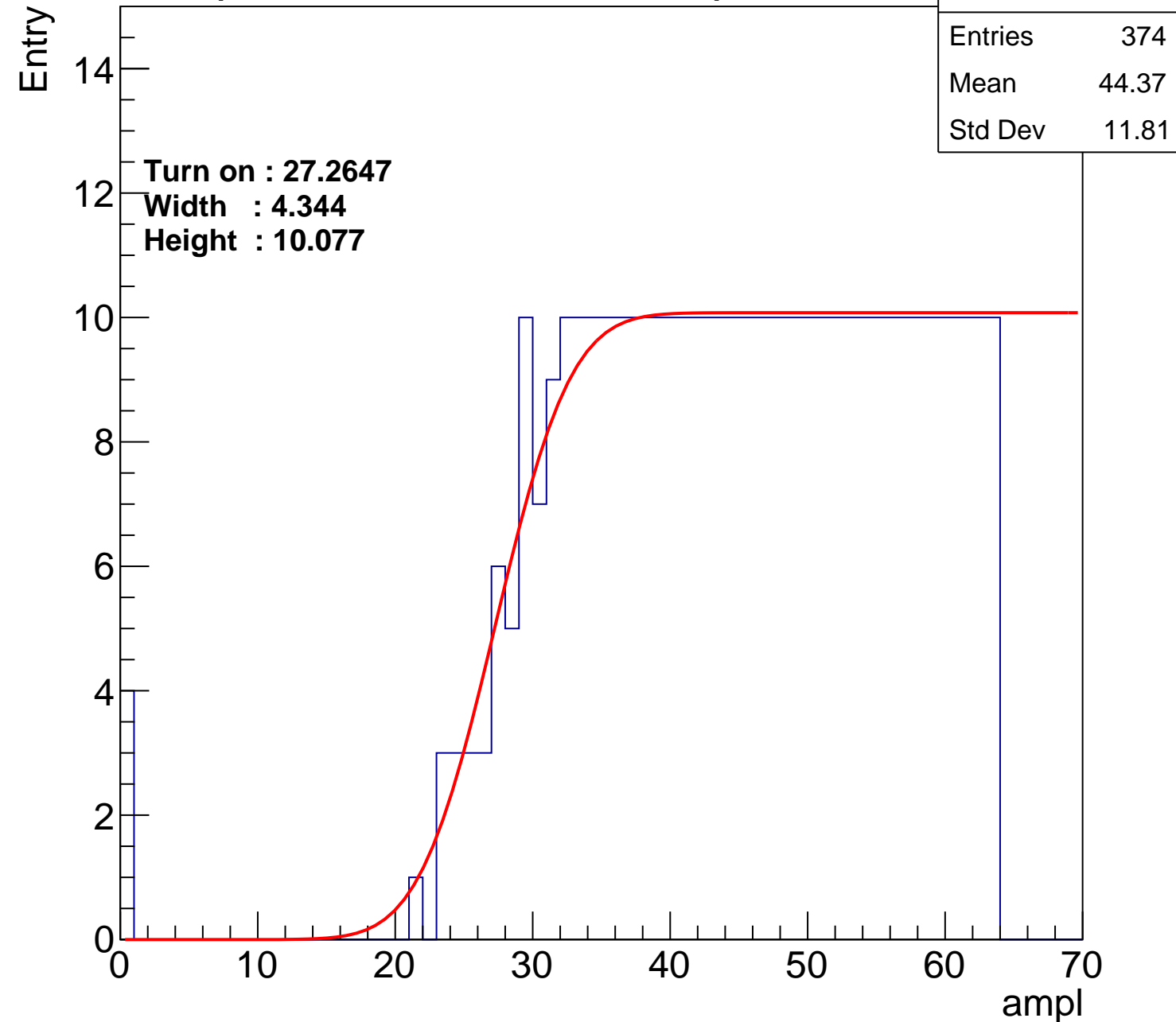
Width : 4.344

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.31
Std Dev	11.63

Turn on : 26.8328

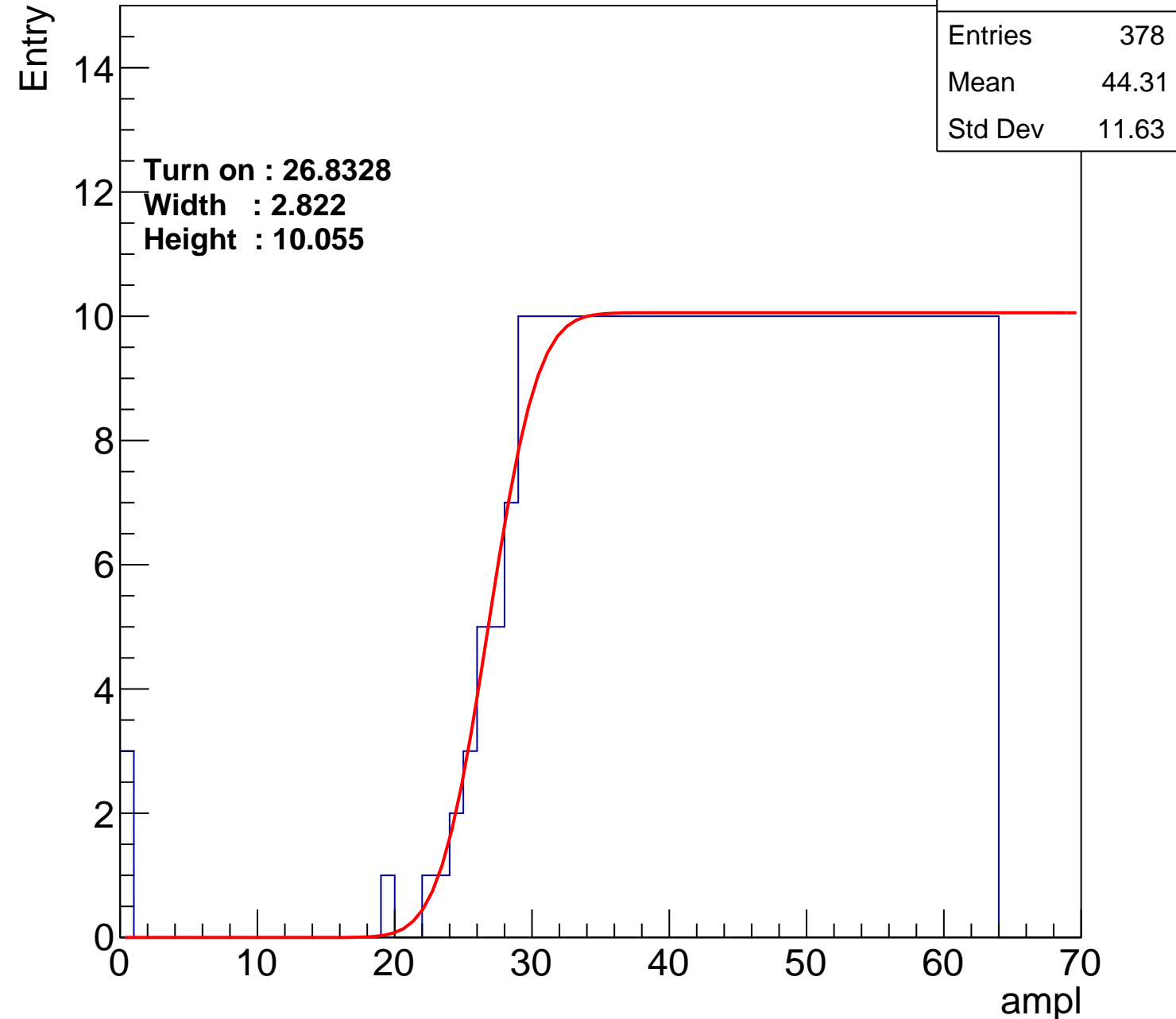
Width : 2.822

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.34
Std Dev	11.63

Turn on : 27.2230

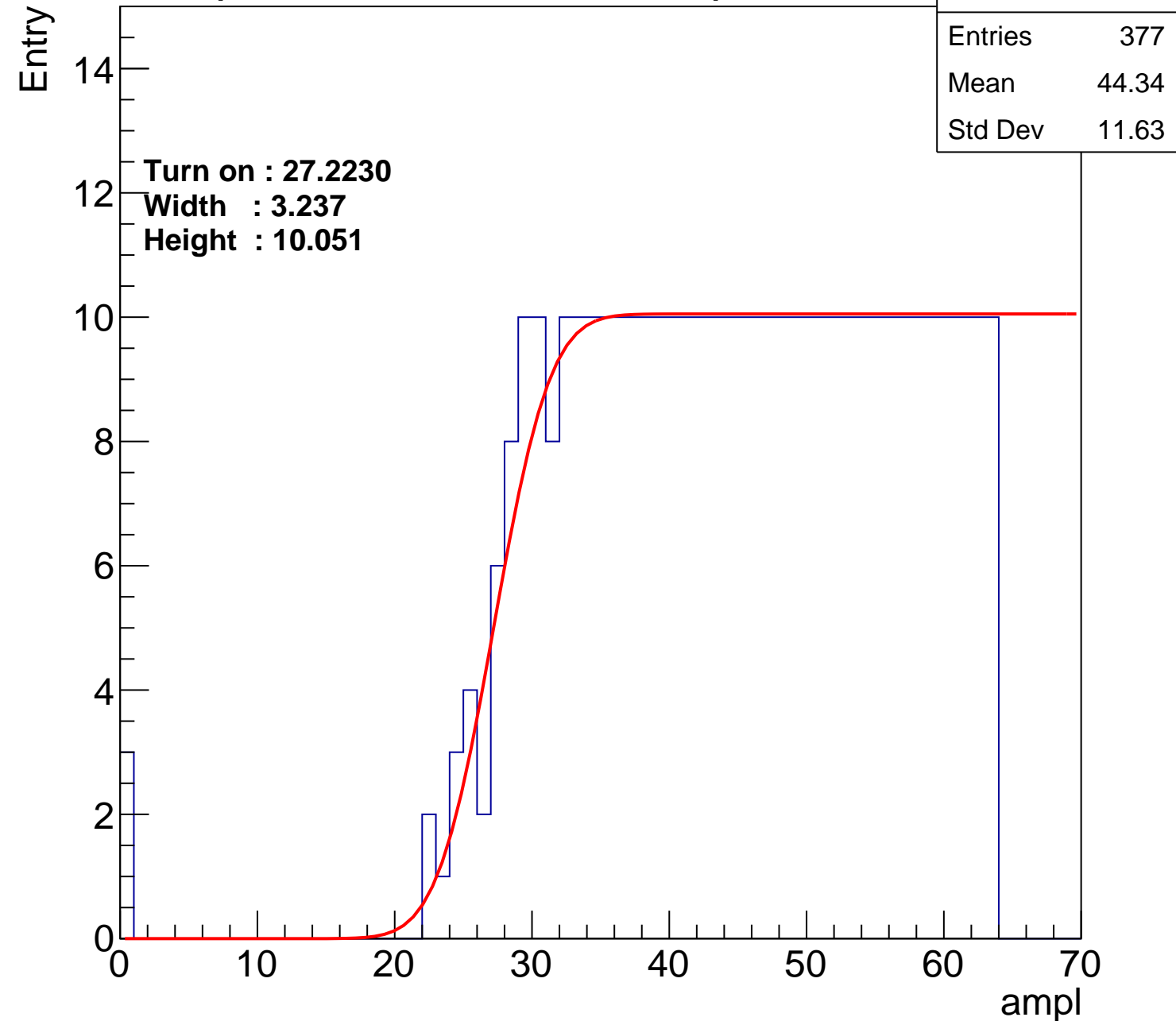
Width : 3.237

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch126

calib_packv5_042523_0143.root, FC#12, port B1

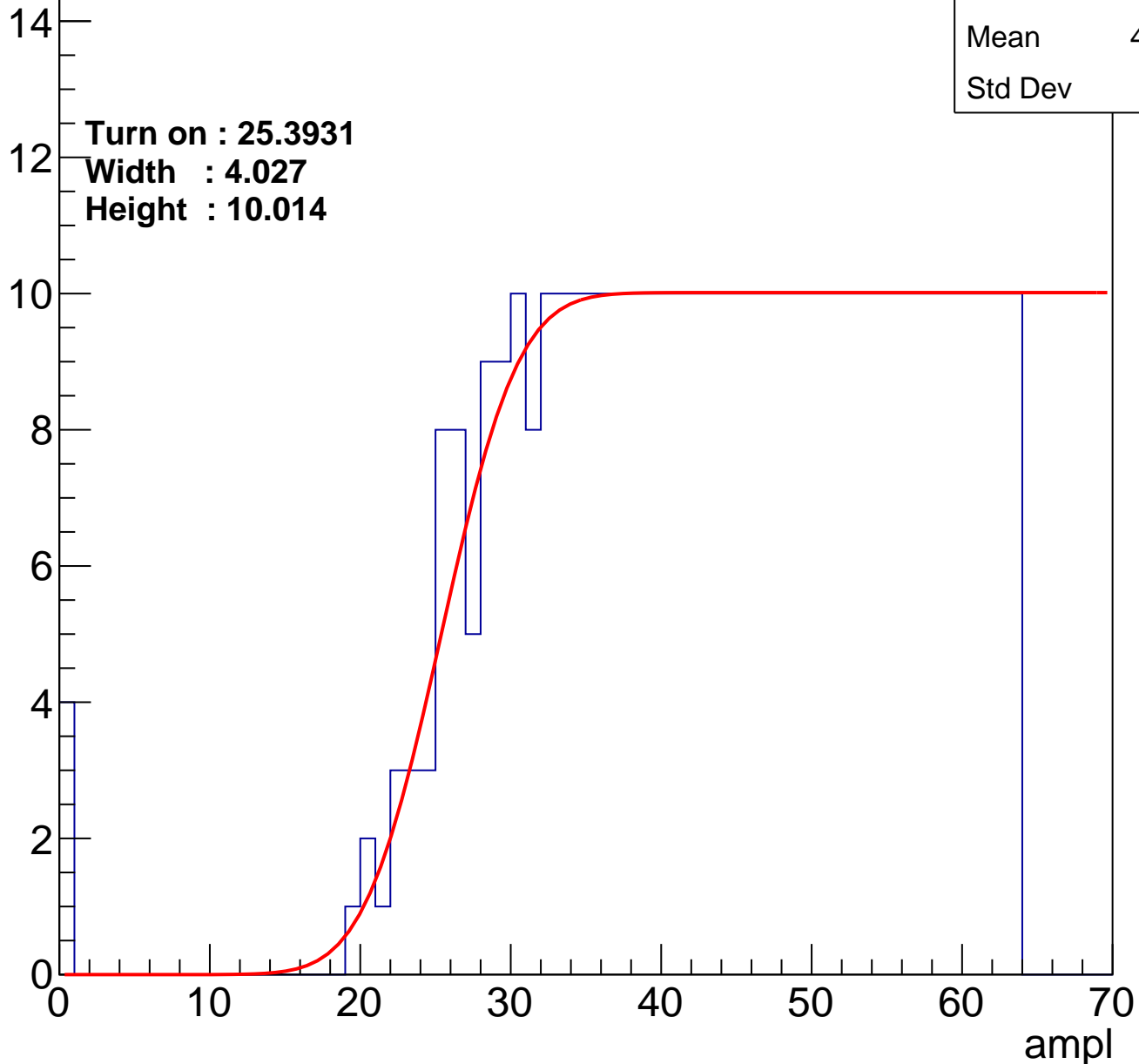
Entries	394
Mean	43.38
Std Dev	12.3

Turn on : 25.3931

Width : 4.027

Height : 10.014

Entry



B0L102S, U24-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.79
Std Dev	12.33

Turn on : 23.0540

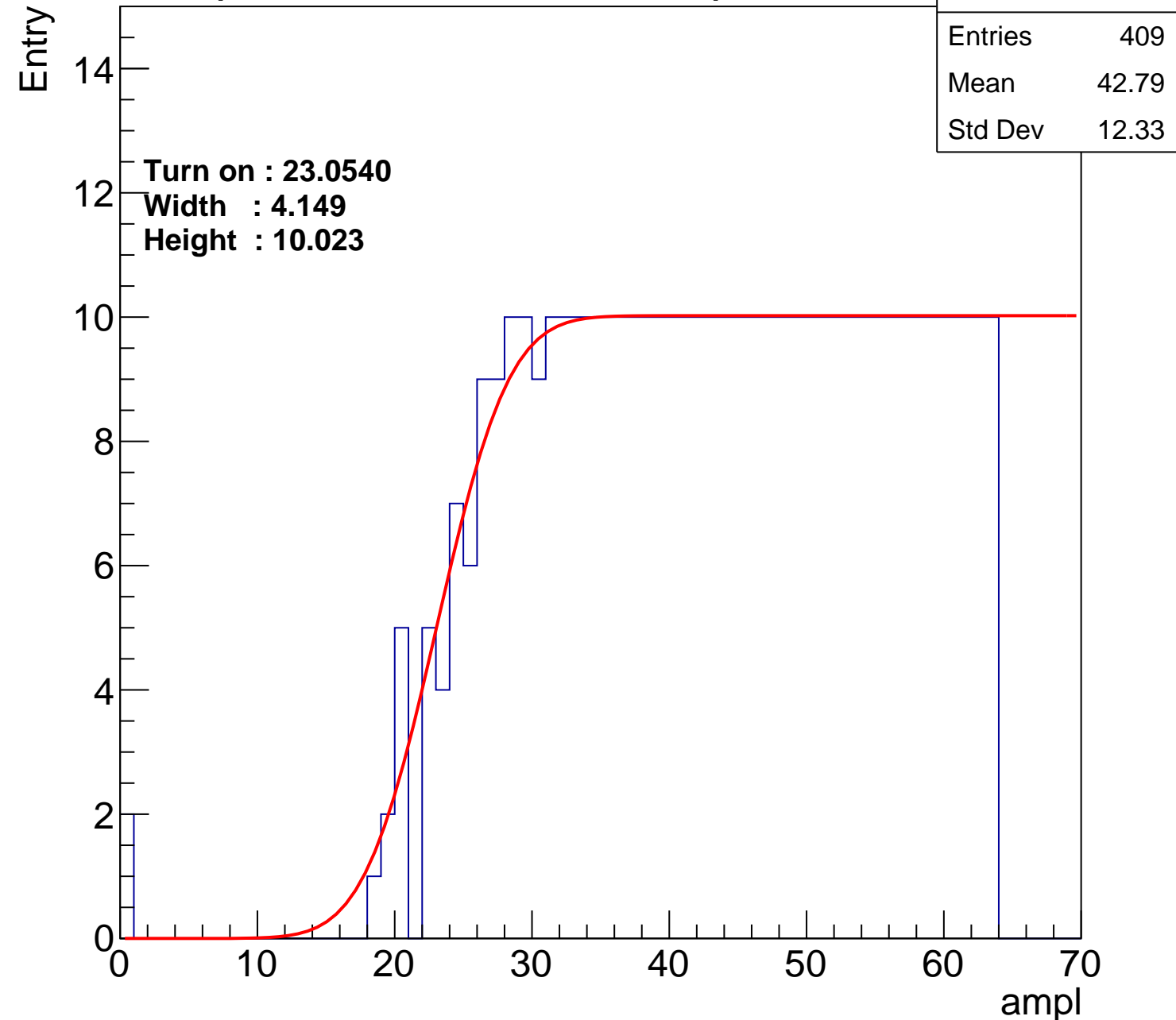
Width : 4.149

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U24-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.79
Std Dev	12.33

Turn on : 23.0540

Width : 4.149

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl

