

B1L100S, U21-ch0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.57
Std Dev	12.4

Turn on : 26.1724

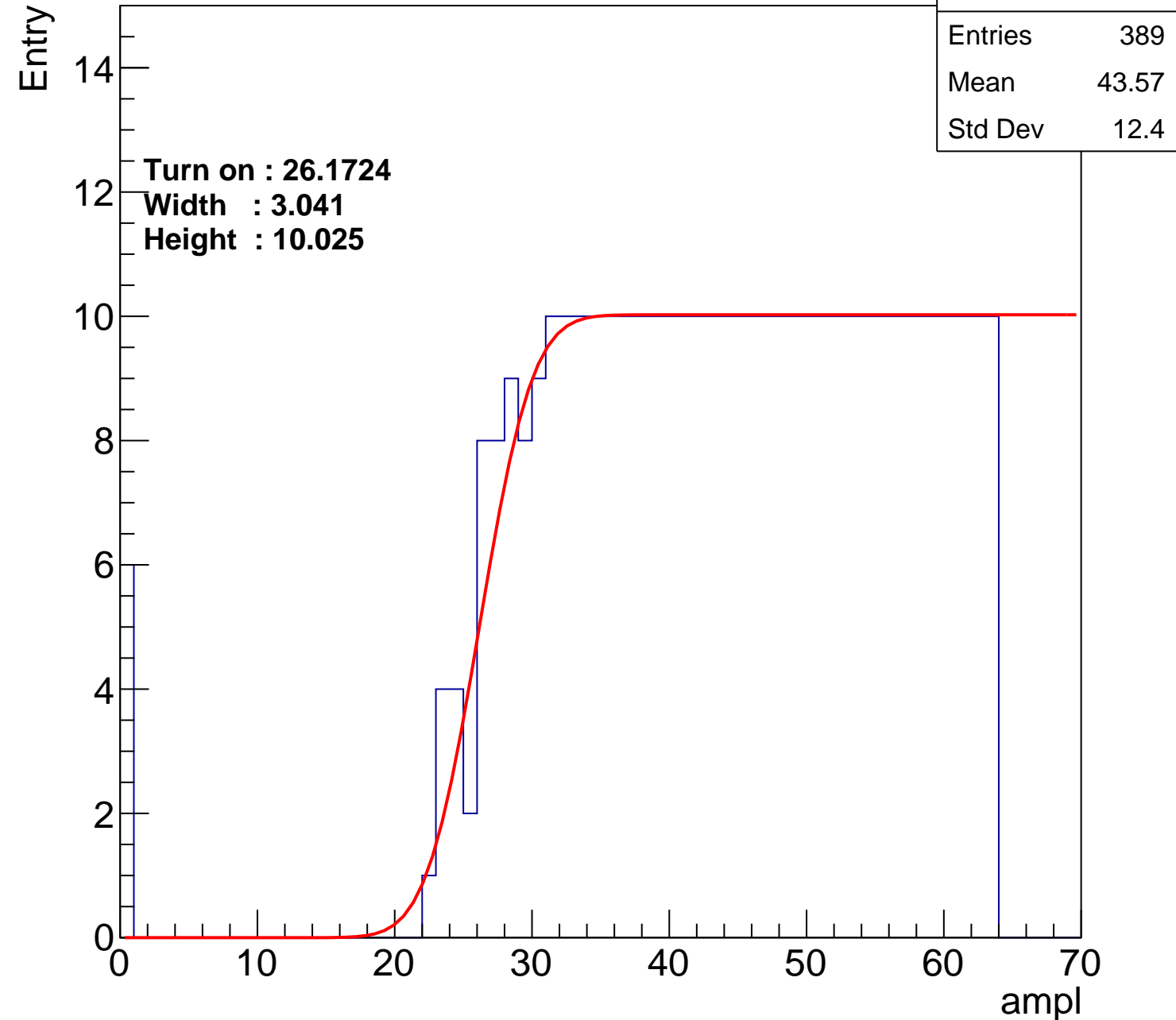
Width : 3.041

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.73
Std Dev	11.45

Turn on : 28.1814

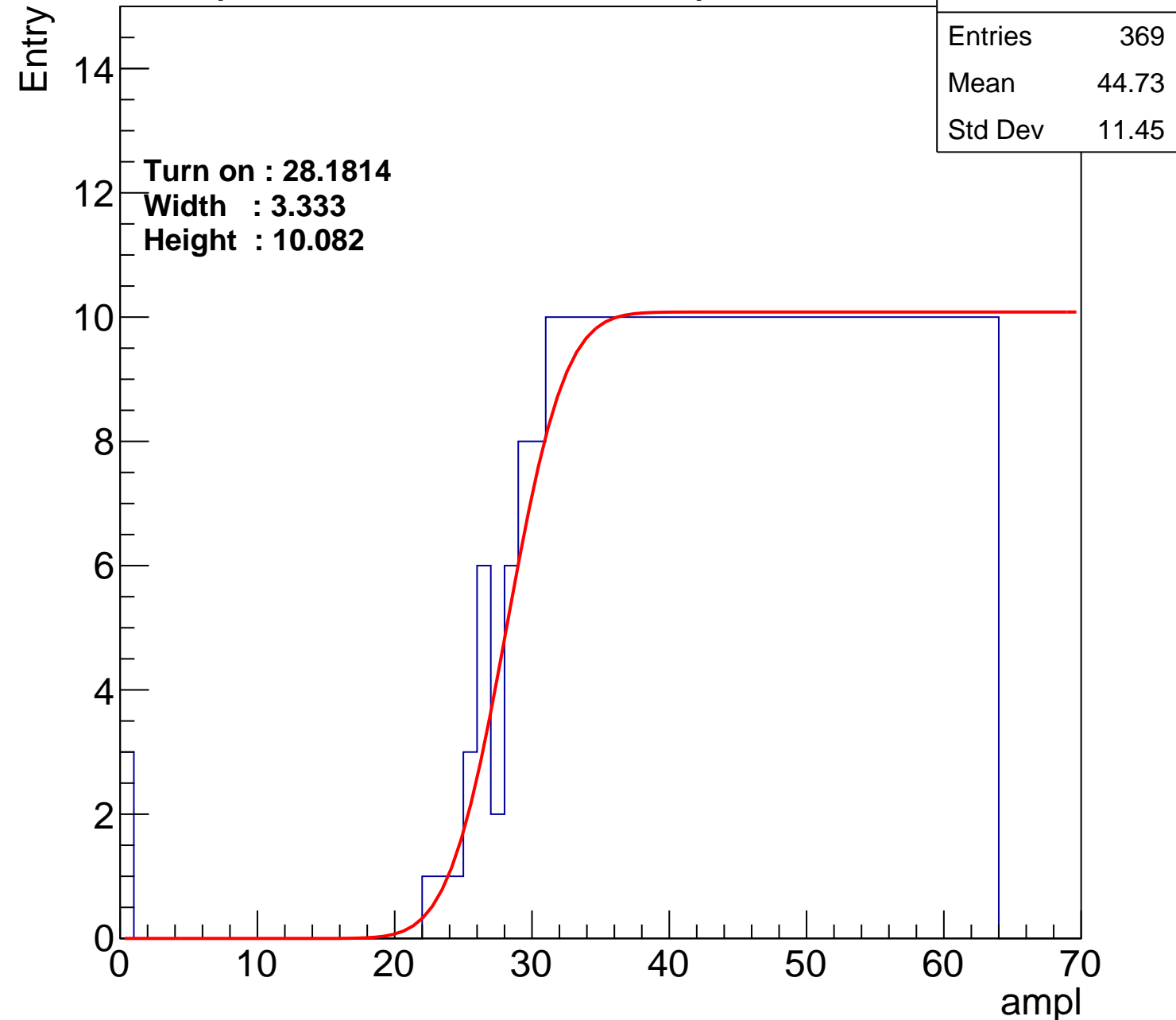
Width : 3.333

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.59
Std Dev	11.31

Turn on : 26.5108

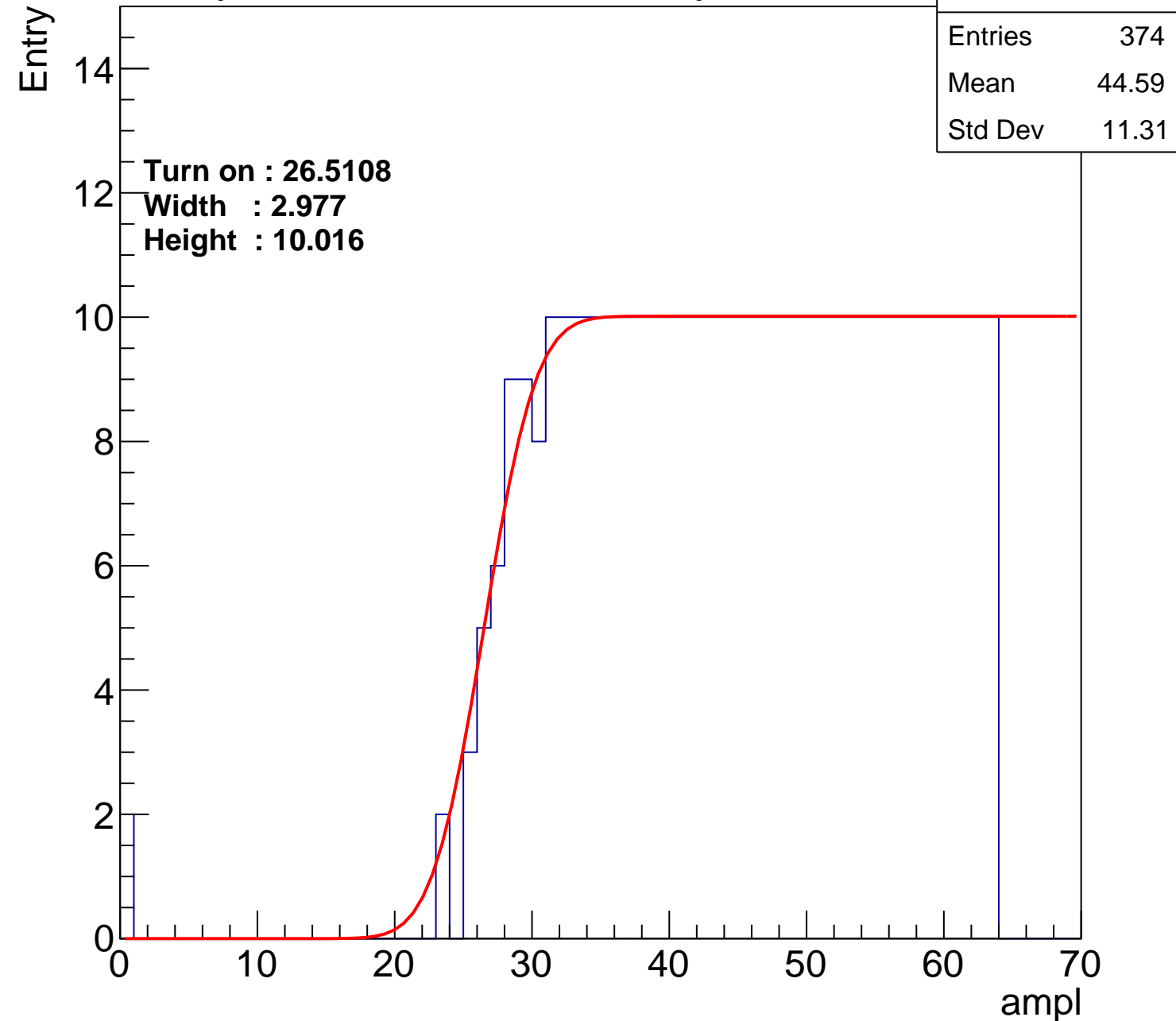
Width : 2.977

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	400
Mean	43.12
Std Dev	12.45

Turn on : 24.5090

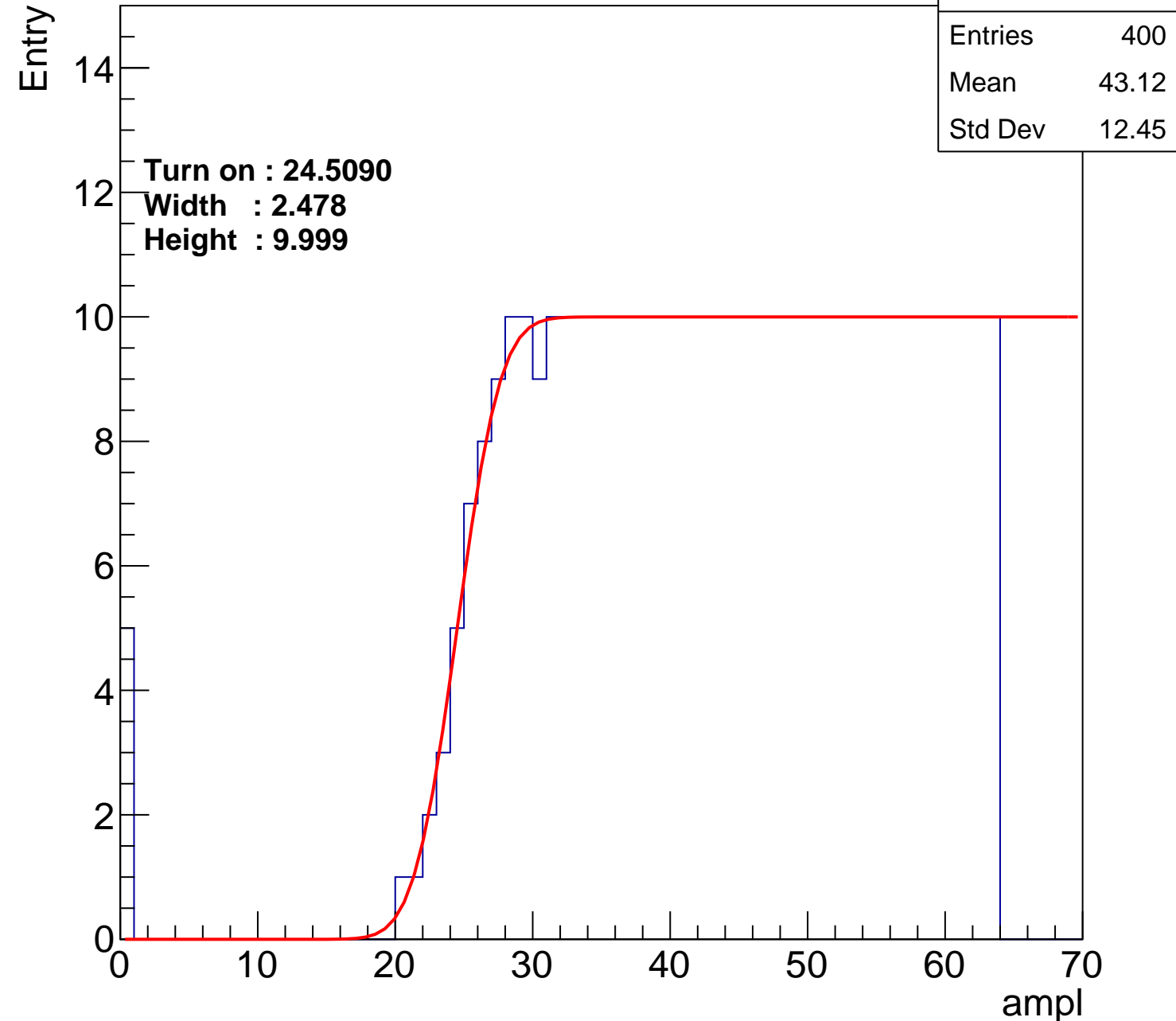
Width : 2.478

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.36
Std Dev	11.6

Turn on : 26.8157

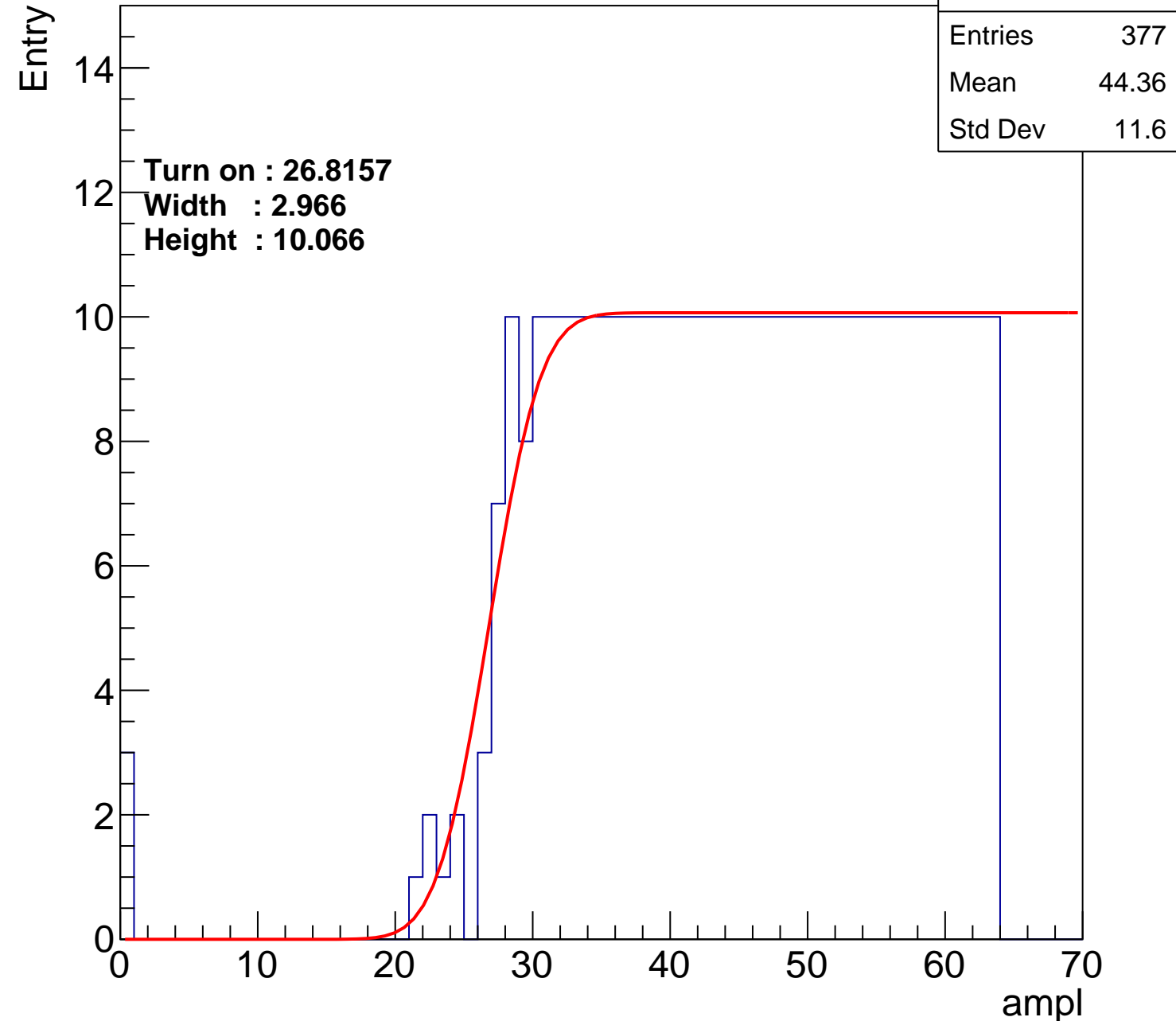
Width : 2.966

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.88
Std Dev	11.16

Turn on : 27.6579

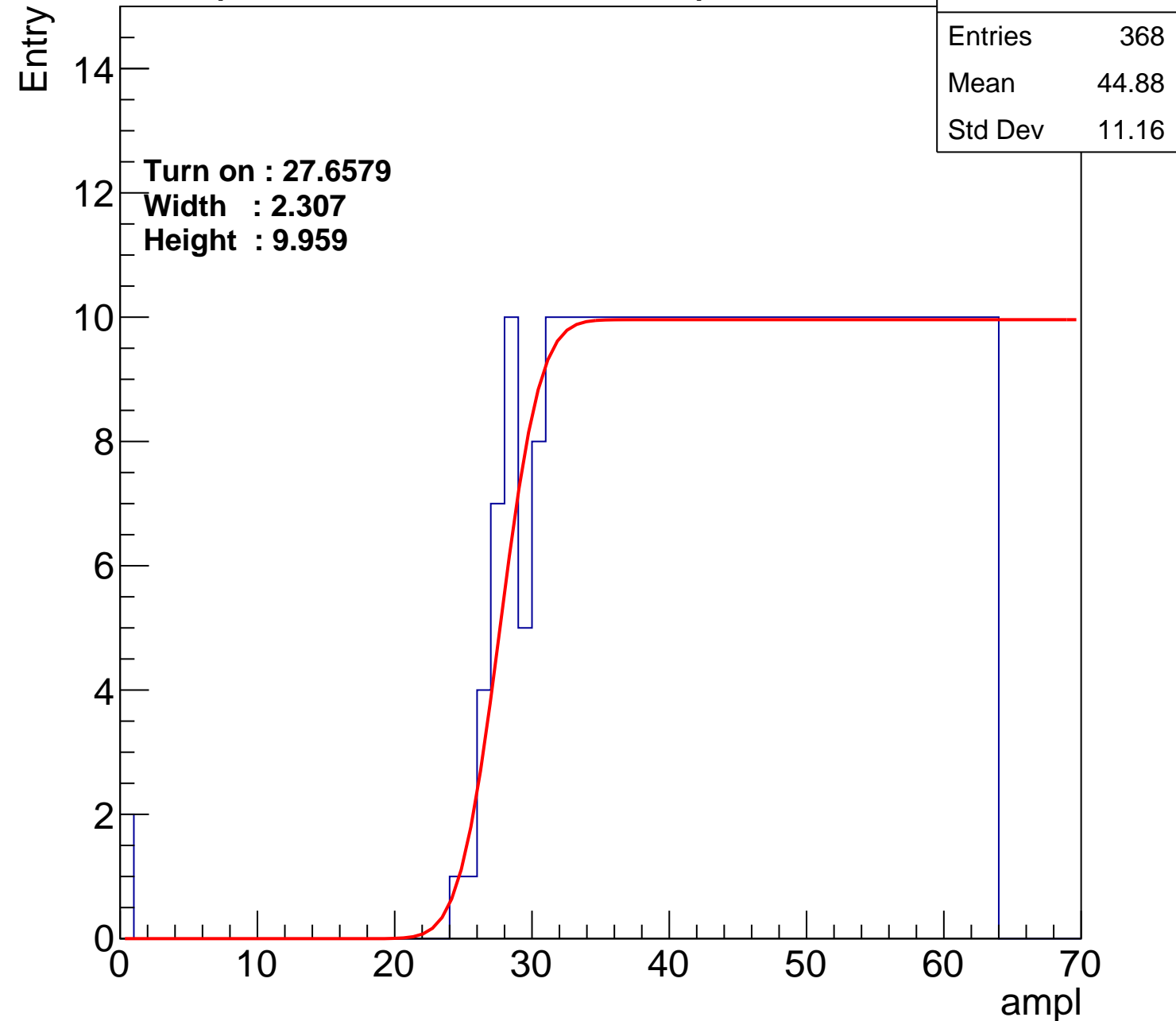
Width : 2.307

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch6

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.45
Std Dev	11.62

Turn on : 27.2865

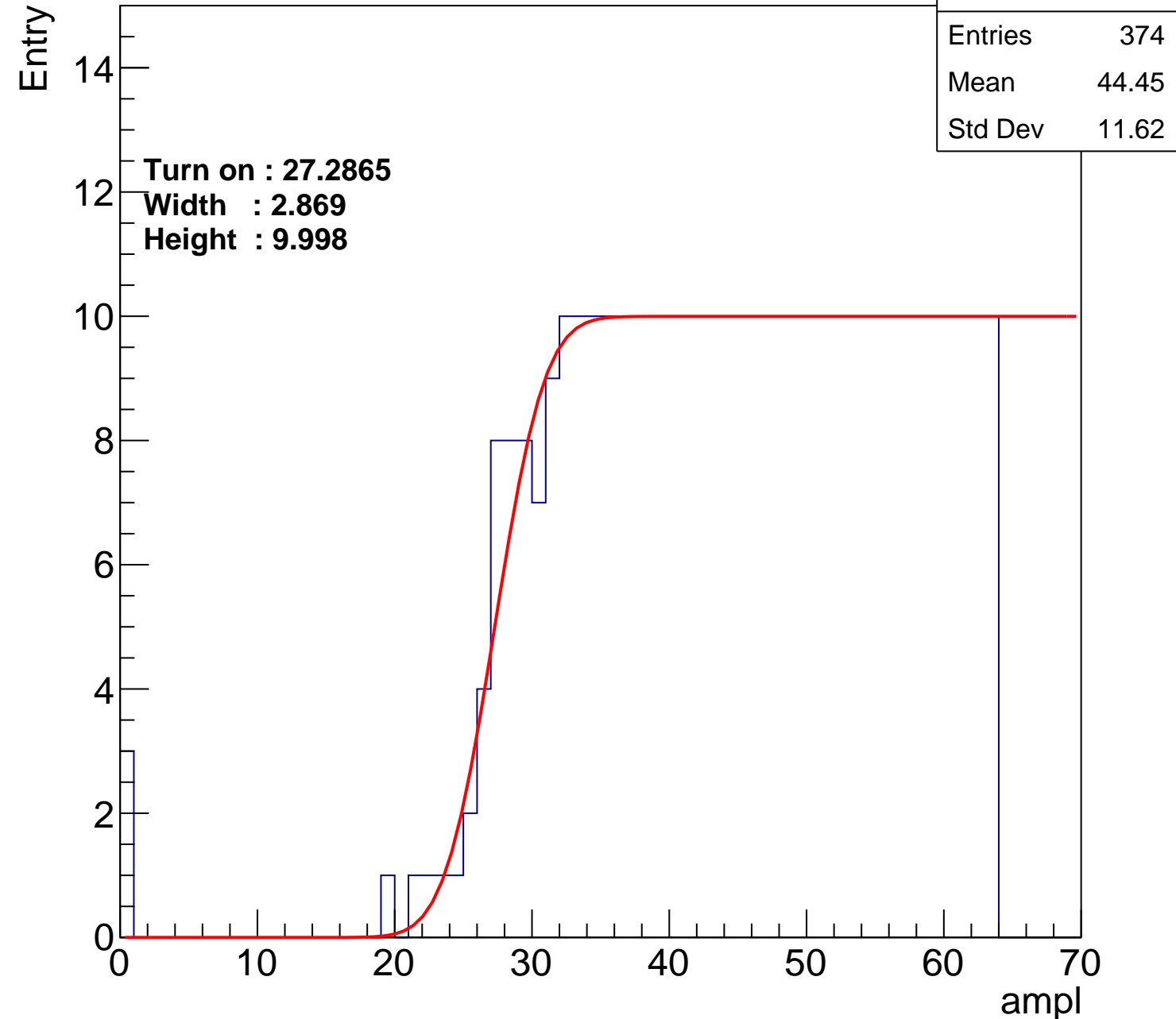
Width : 2.869

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch7

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.81
Std Dev	11.02

Turn on : 27.2454

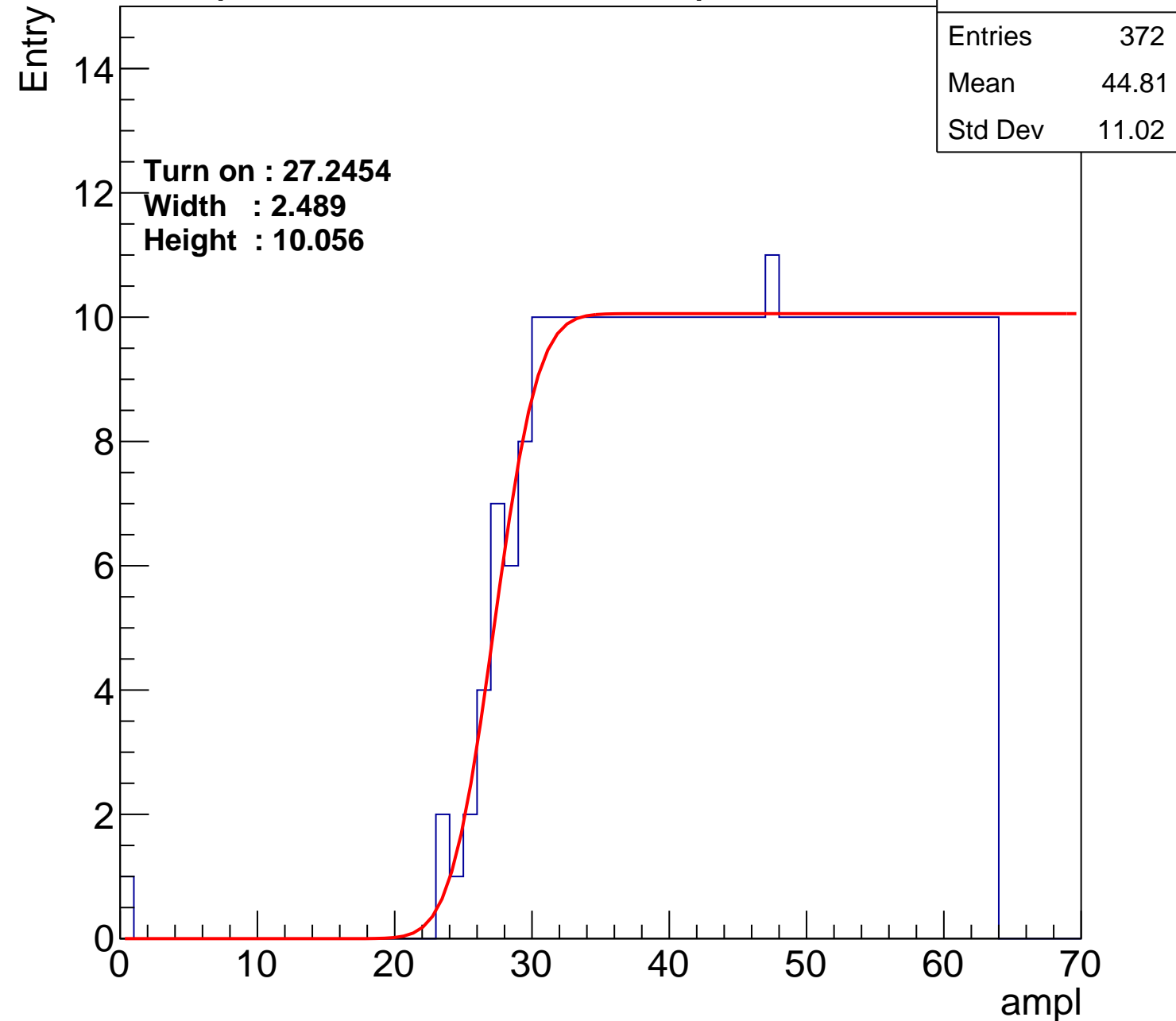
Width : 2.489

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch8

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.58
Std Dev	11.81

Turn on : 24.7442

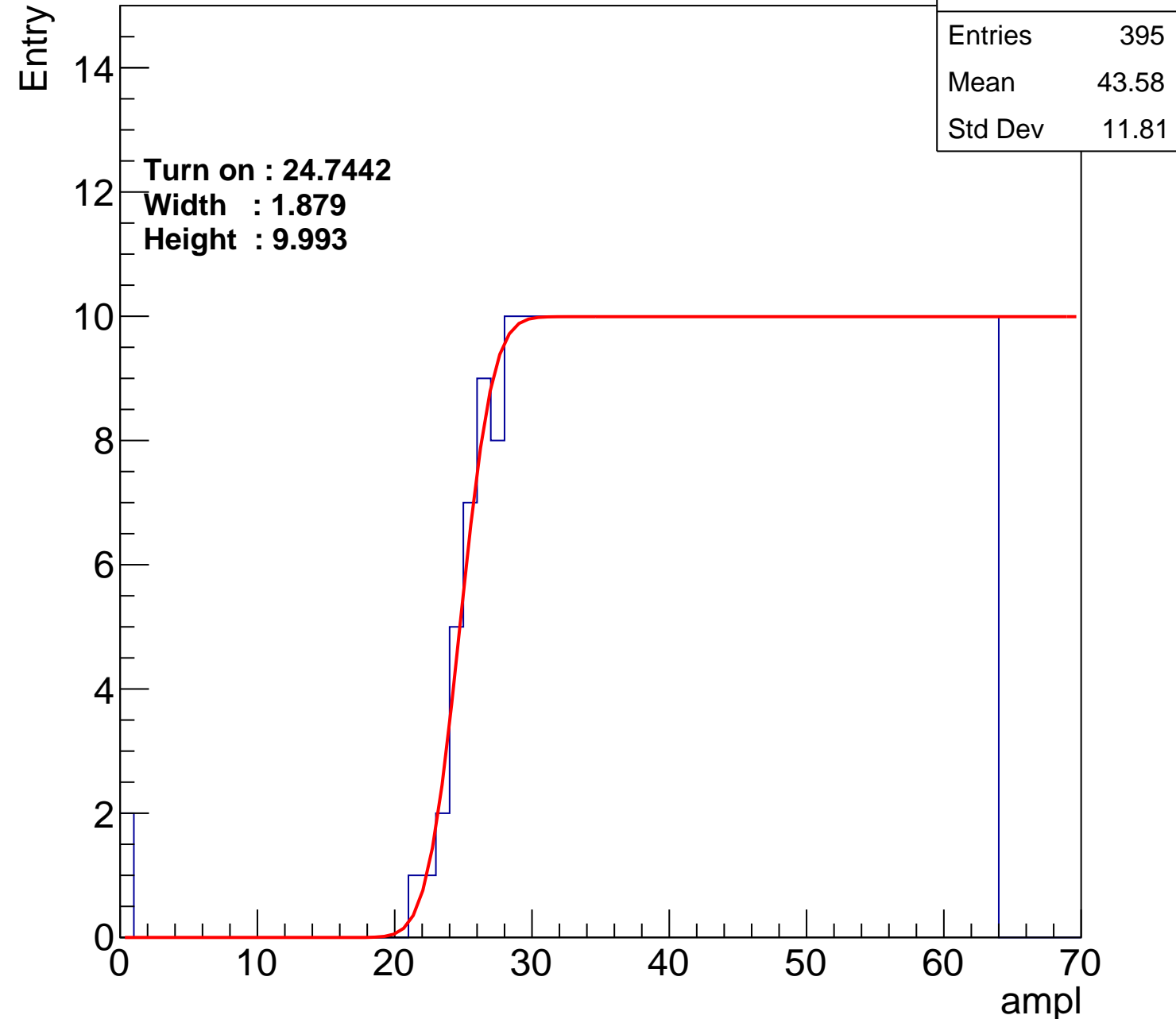
Width : 1.879

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch9

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.41
Std Dev	11.75

Turn on : 27.4482

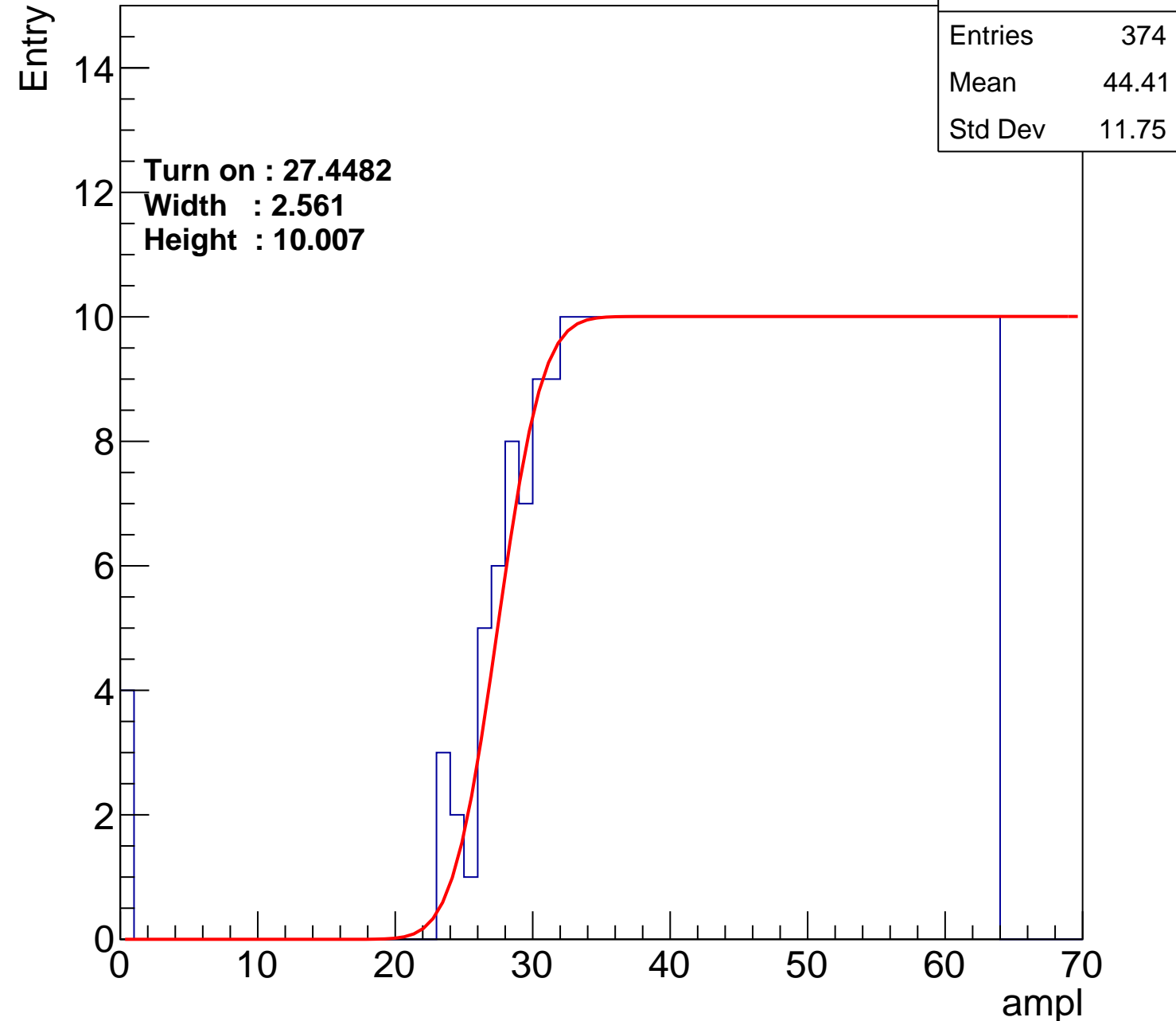
Width : 2.561

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch10

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.89
Std Dev	11.14

Turn on : 27.6803

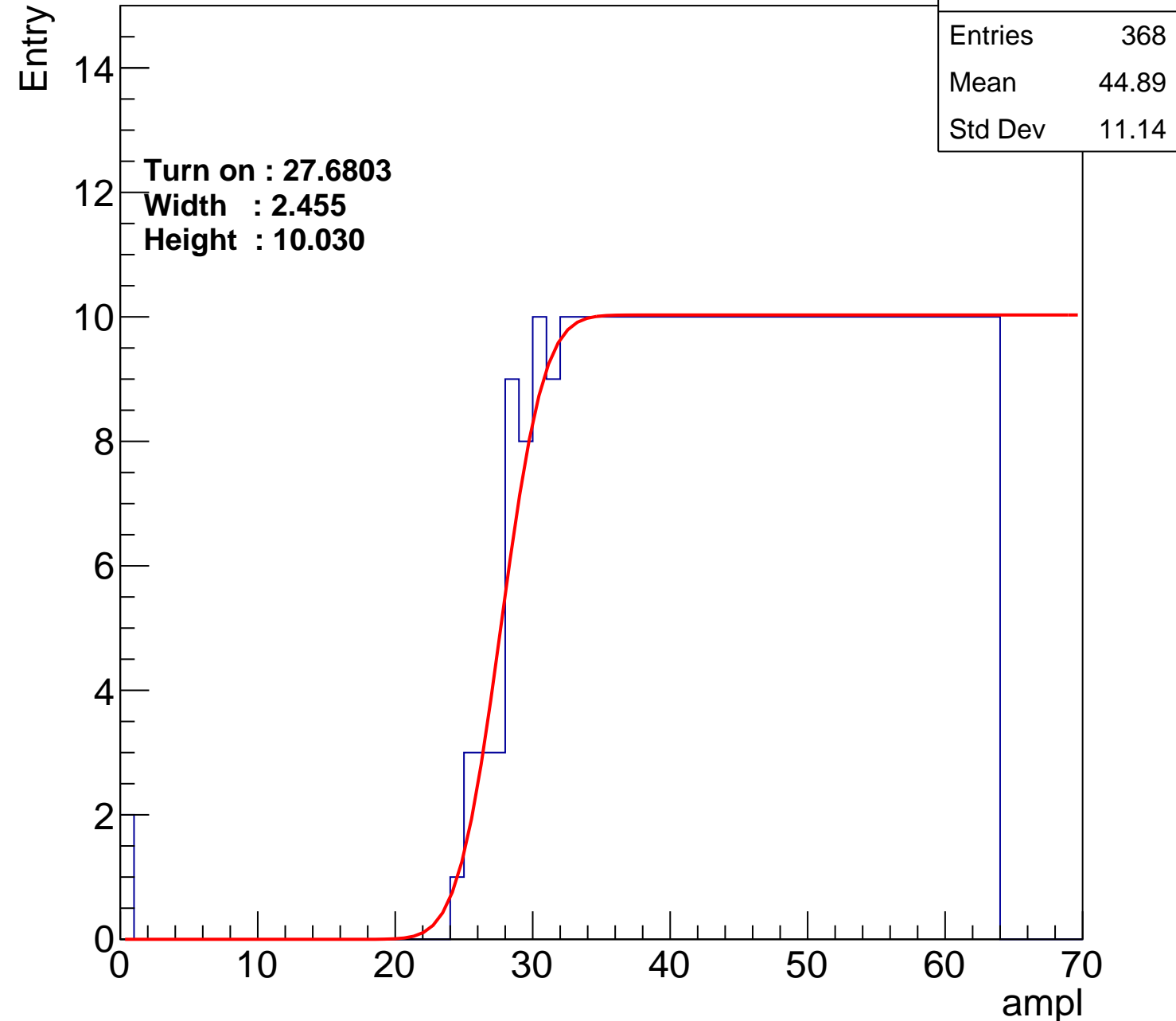
Width : 2.455

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch11

calib_packv5_042523_0143.root, FC#4, port A2

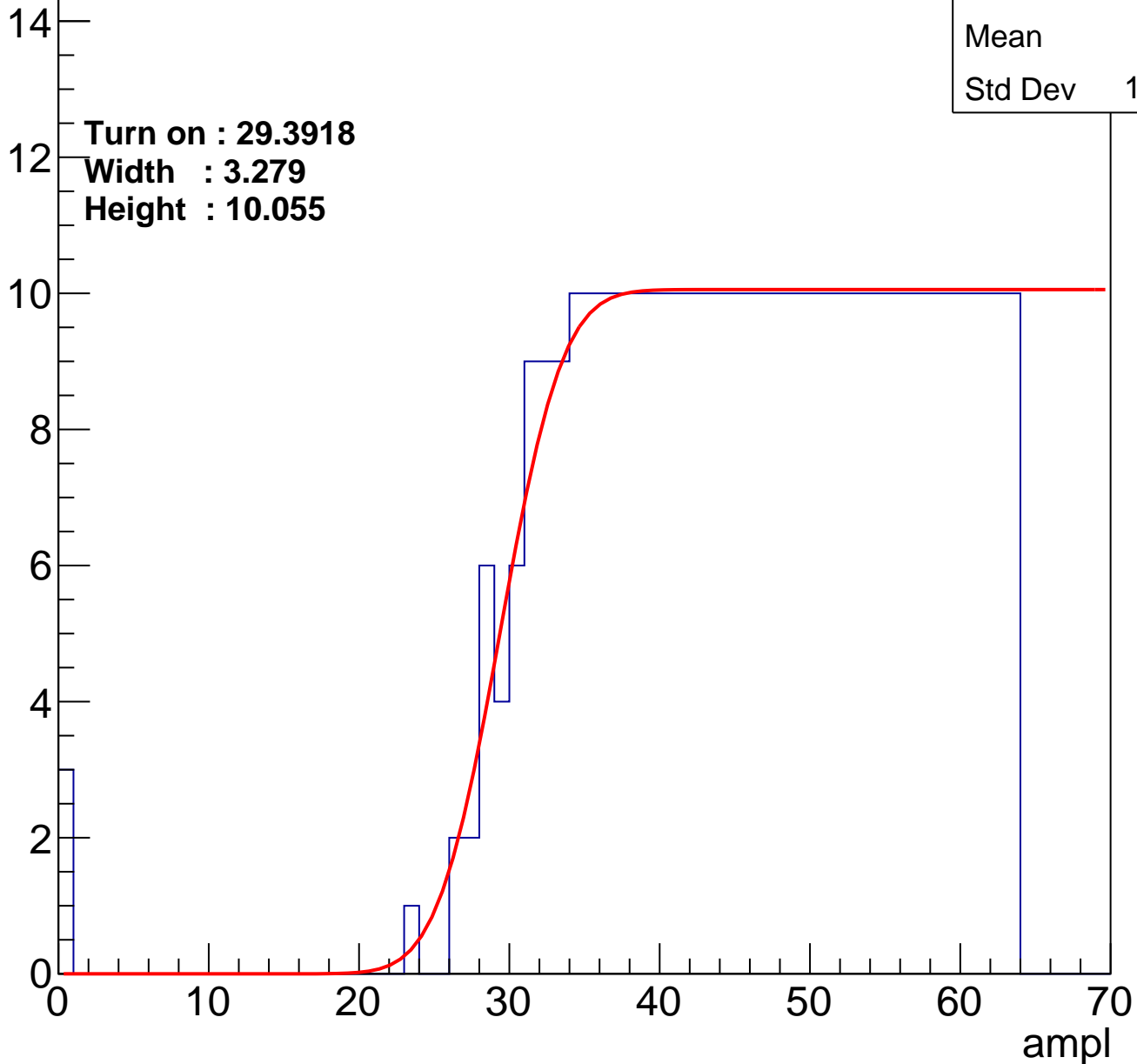
Entries	351
Mean	45.6
Std Dev	11.02

Turn on : 29.3918

Width : 3.279

Height : 10.055

Entry



B1L100S, U21-ch12

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	44.04
Std Dev	11.49

Turn on : 25.6423

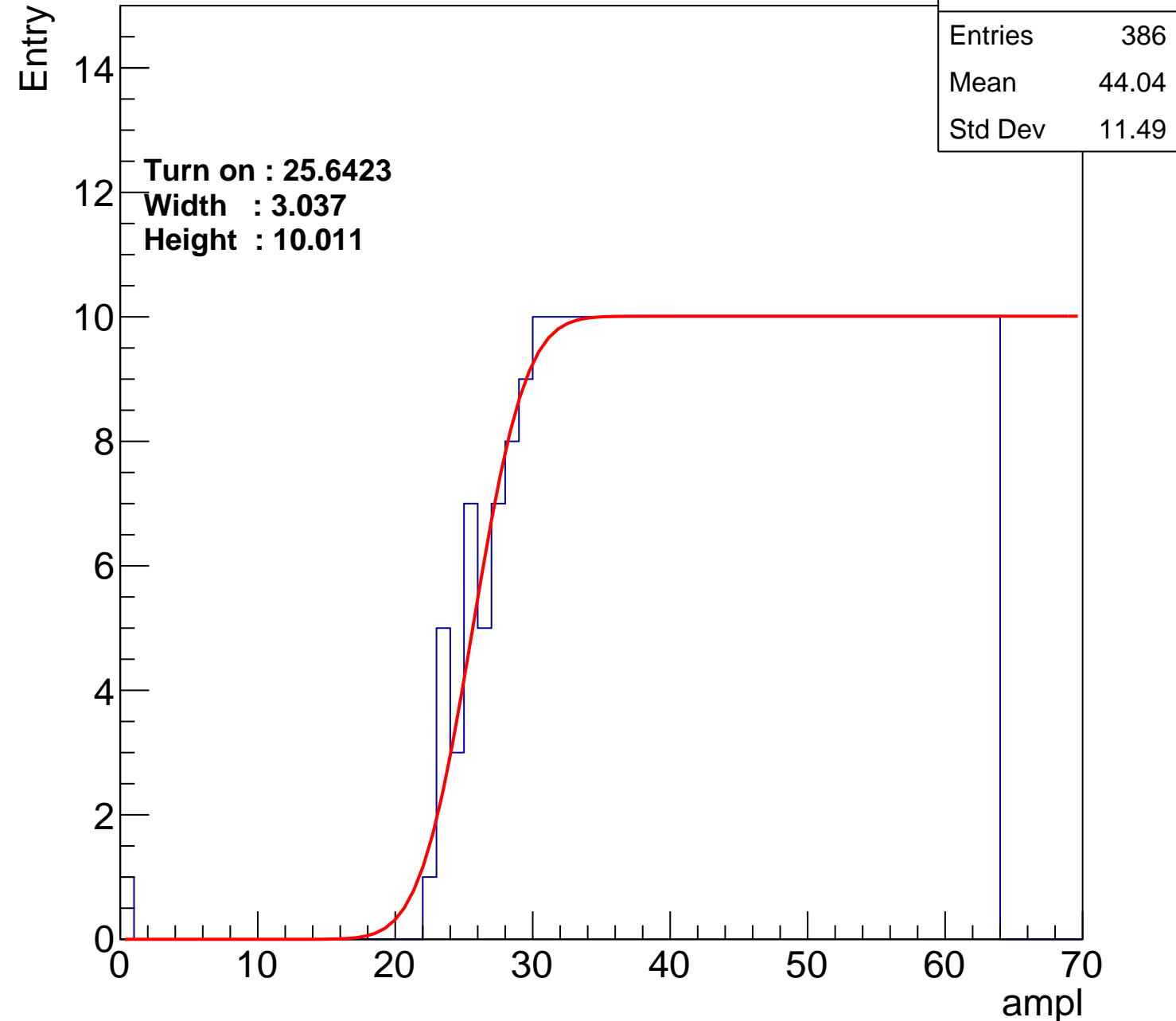
Width : 3.037

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch13

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.89
Std Dev	11.2

Turn on : 27.6963

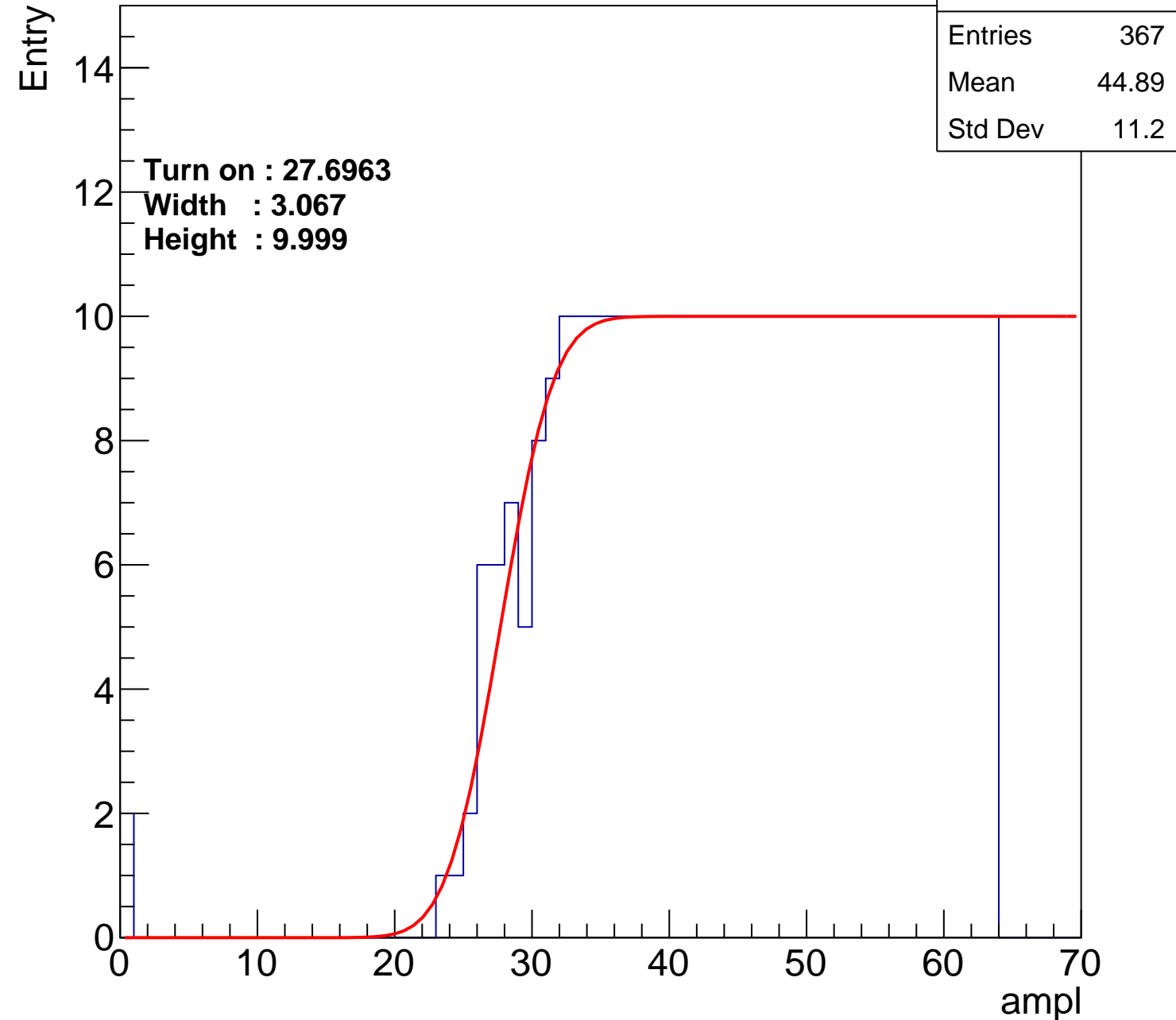
Width : 3.067

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch14

calib_packv5_042523_0143.root, FC#4, port A2

Entries	353
Mean	45.53
Std Dev	11.03

Turn on : 29.2839

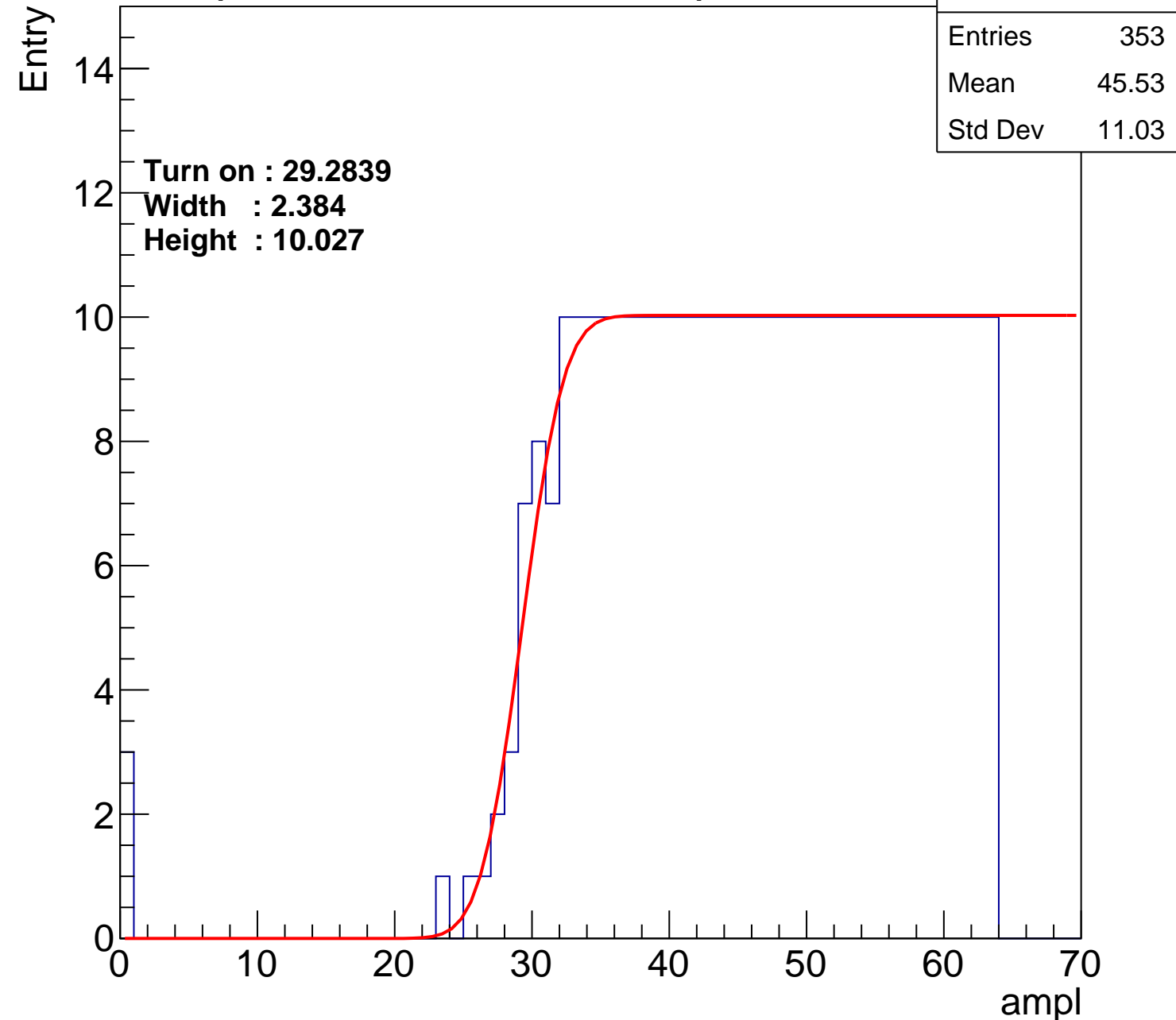
Width : 2.384

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch15

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.77
Std Dev	12.02

Turn on : 25.7925

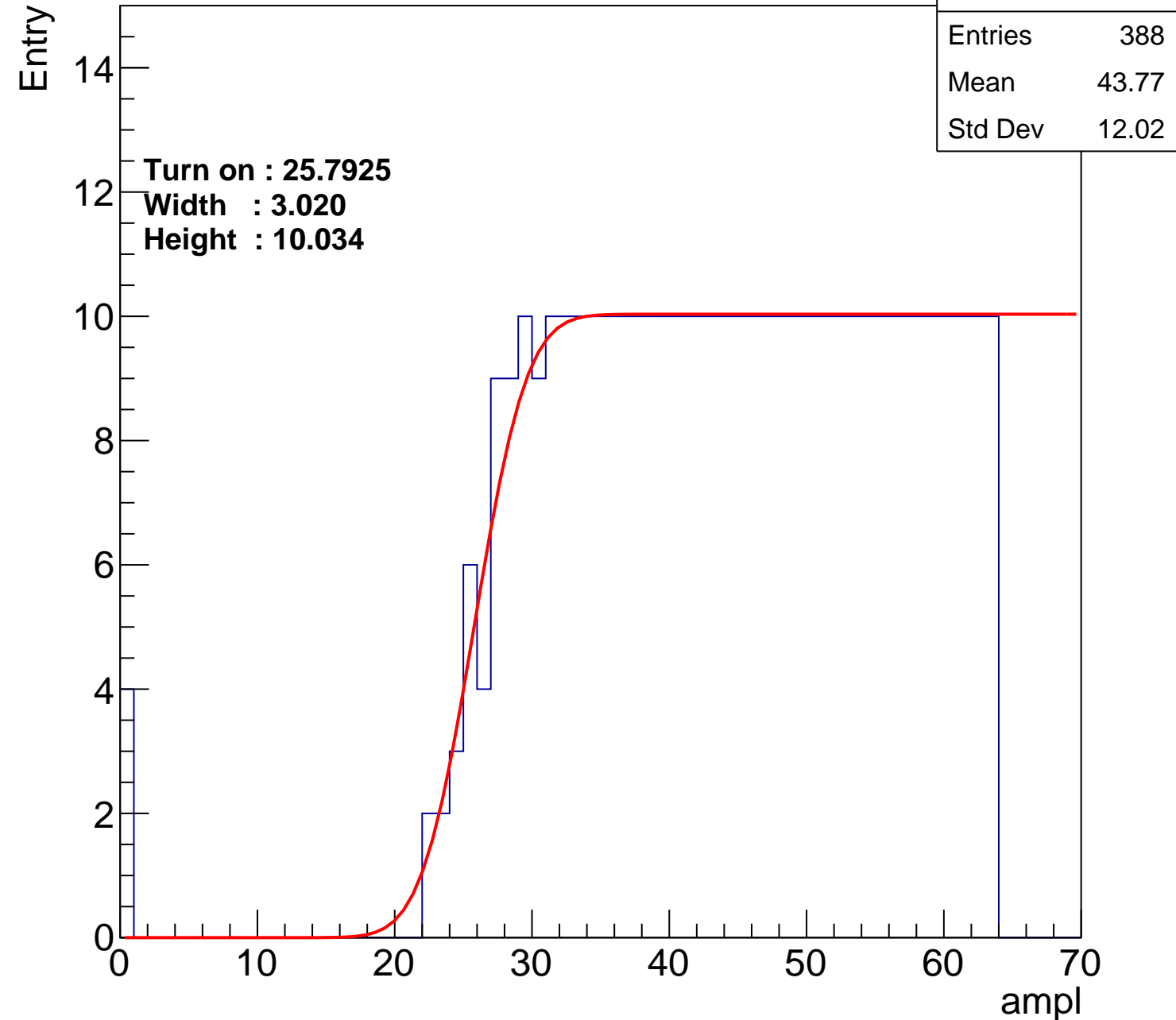
Width : 3.020

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch16

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.1193

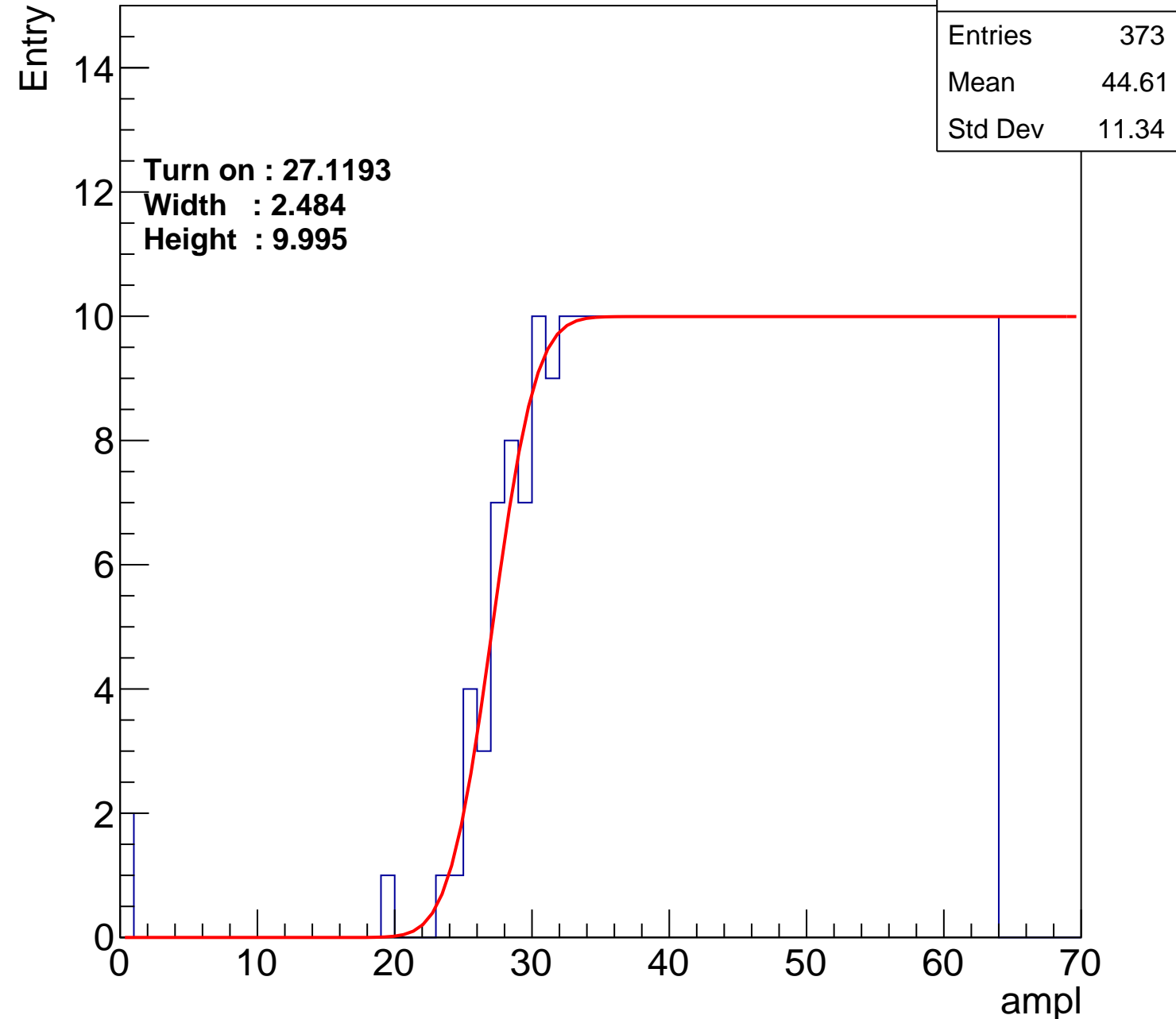
Width : 2.484

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch17

calib_packv5_042523_0143.root, FC#4, port A2

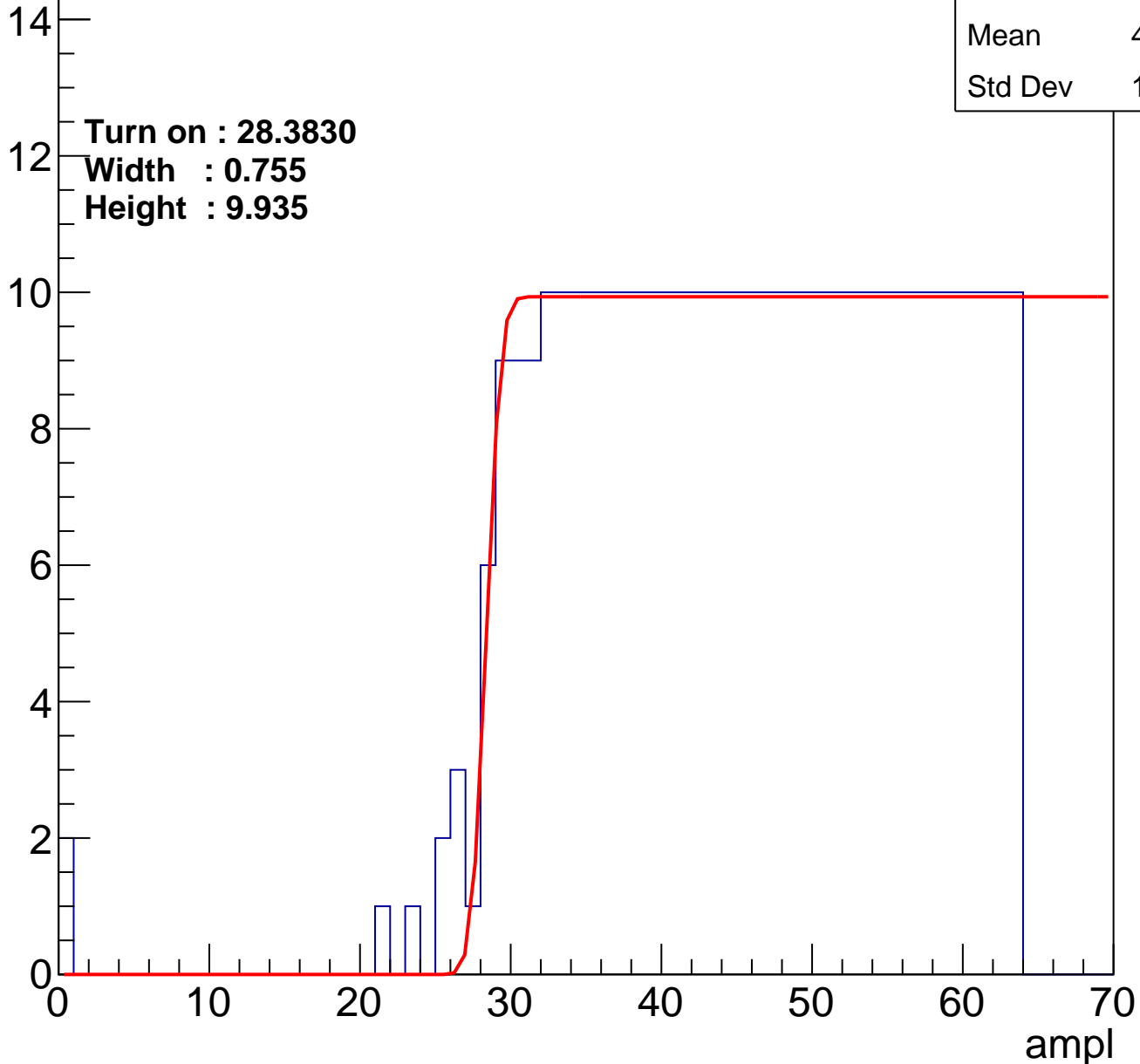
Entries	363
Mean	45.12
Std Dev	11.06

Turn on : 28.3830

Width : 0.755

Height : 9.935

Entry



B1L100S, U21-ch18

calib_packv5_042523_0143.root, FC#4, port A2

Entries	356
Mean	45.52
Std Dev	10.69

Turn on : 28.7698

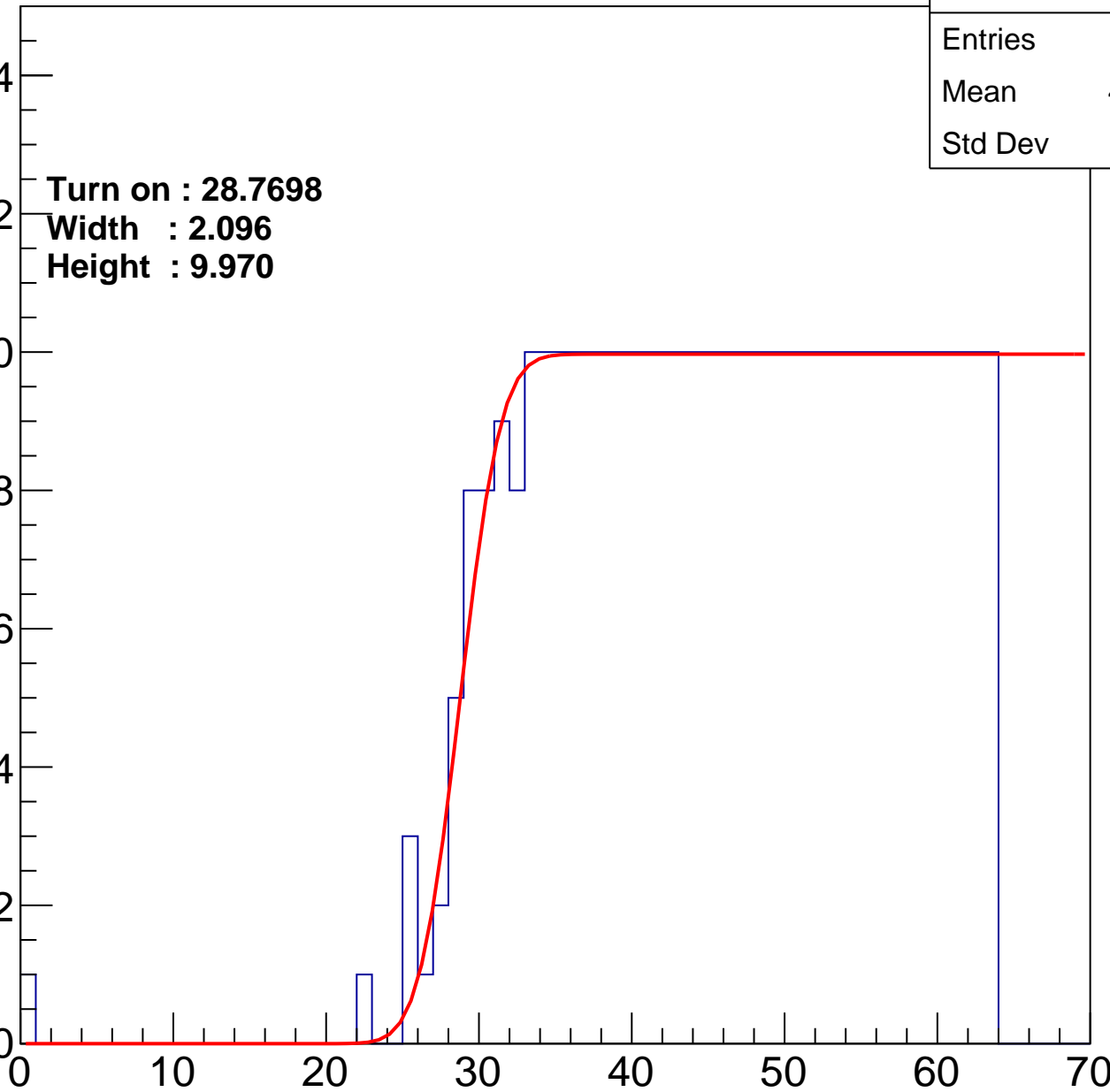
Width : 2.096

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch19

calib_packv5_042523_0143.root, FC#4, port A2

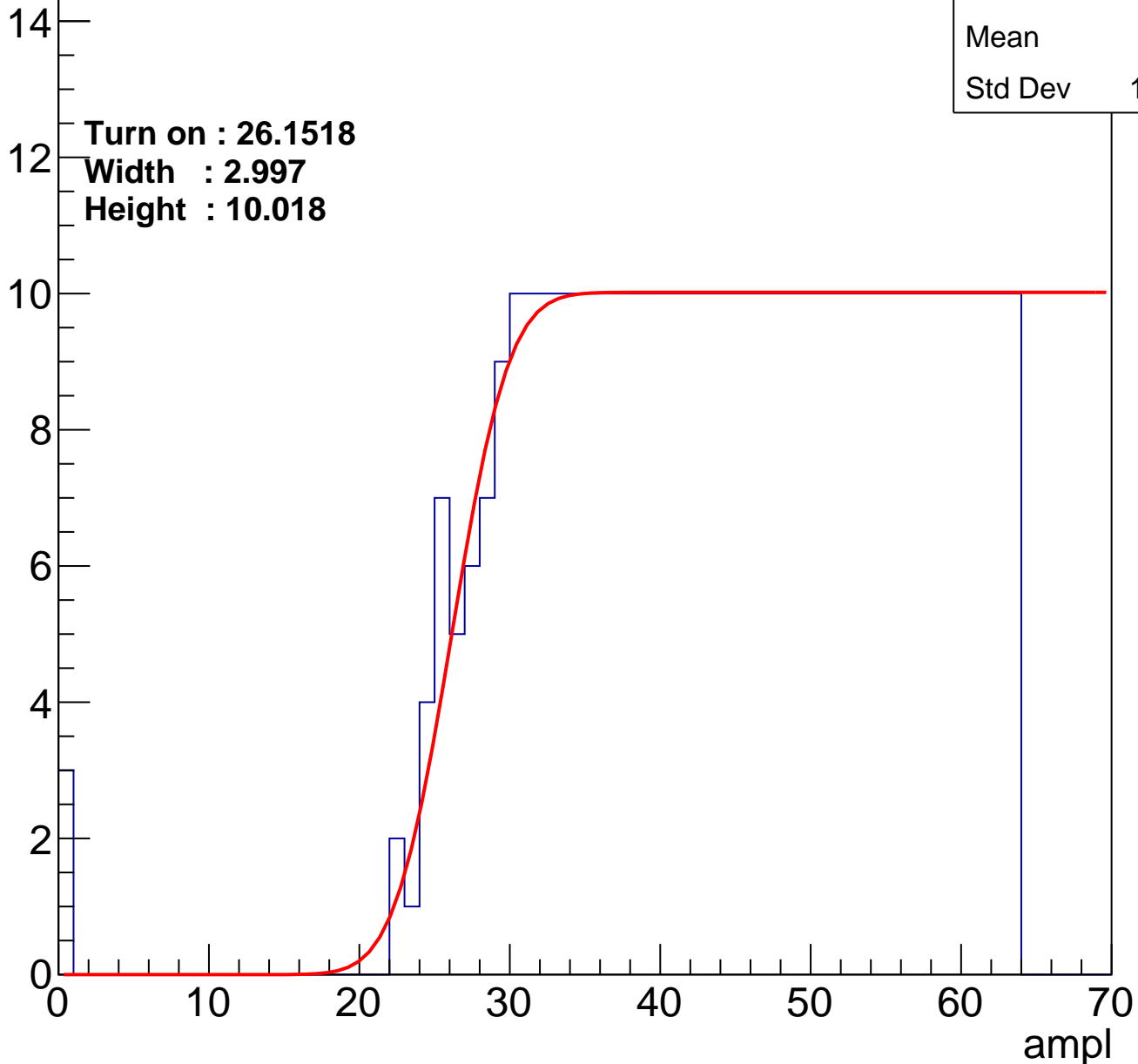
Entries	384
Mean	44
Std Dev	11.79

Turn on : 26.1518

Width : 2.997

Height : 10.018

Entry



B1L100S, U21-ch20

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.26
Std Dev	11.88

Turn on : 27.0768

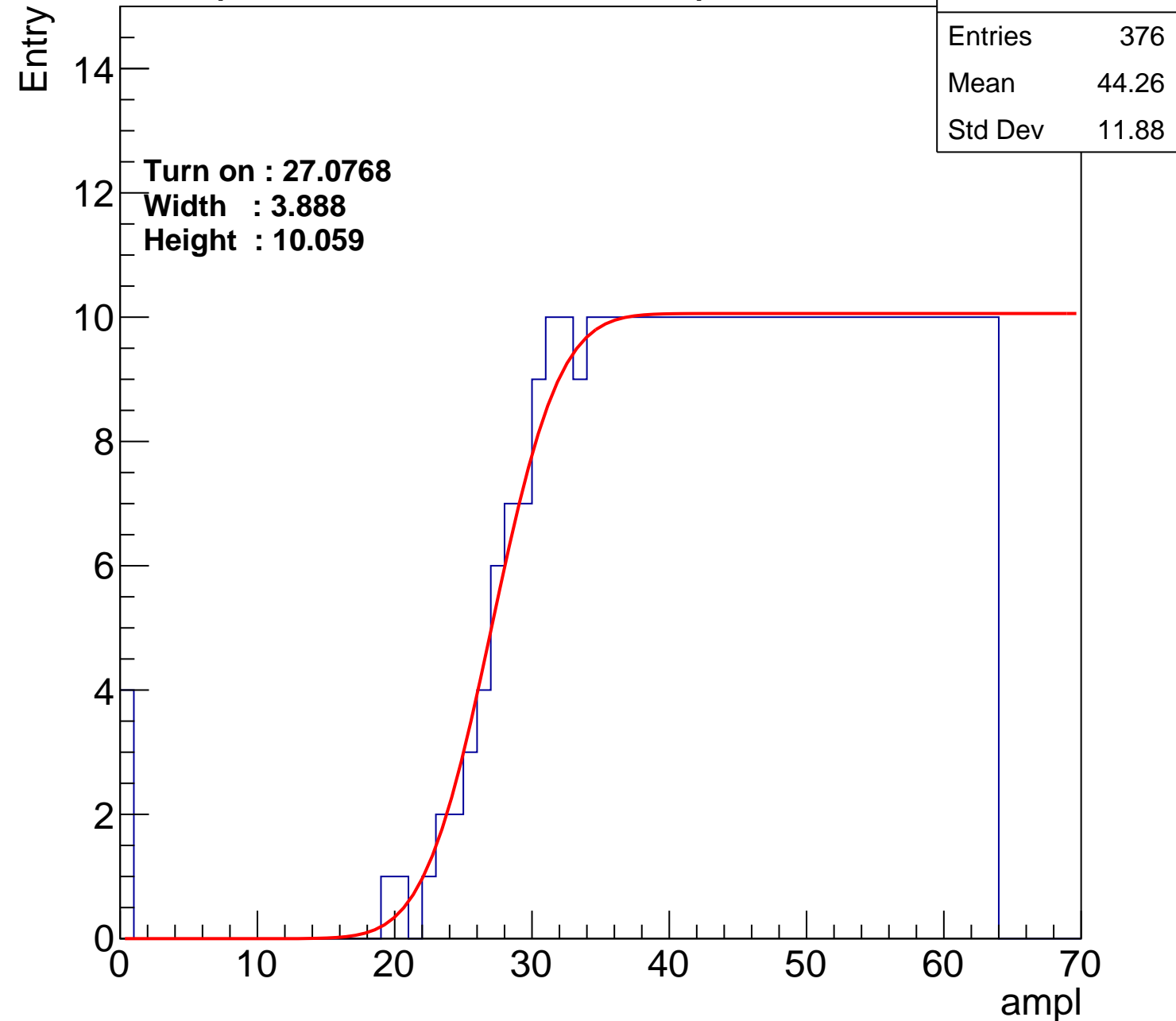
Width : 3.888

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch21

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.06
Std Dev	11.92

Turn on : 26.5776

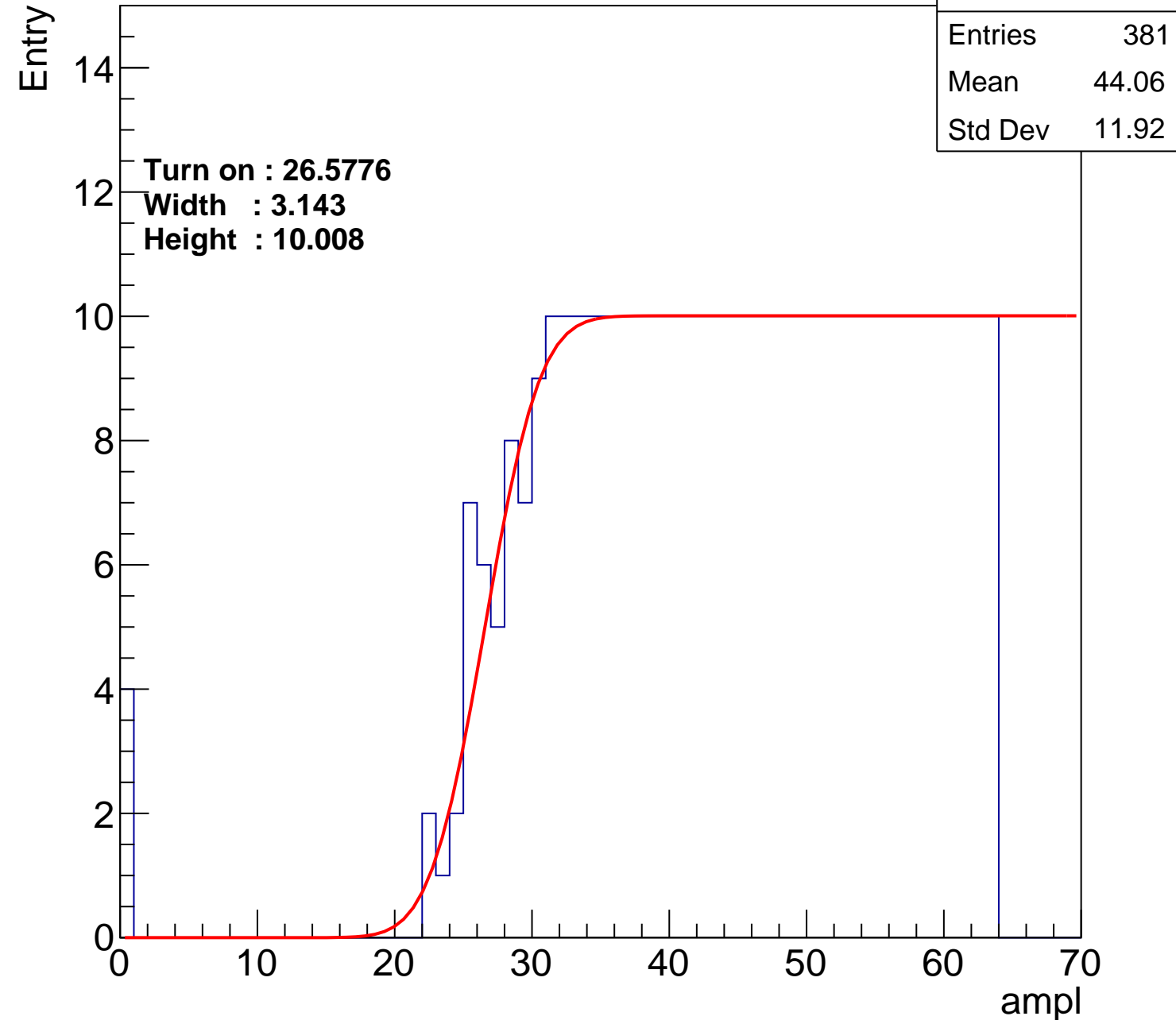
Width : 3.143

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch22

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.57
Std Dev	11.72

Turn on : 24.7886

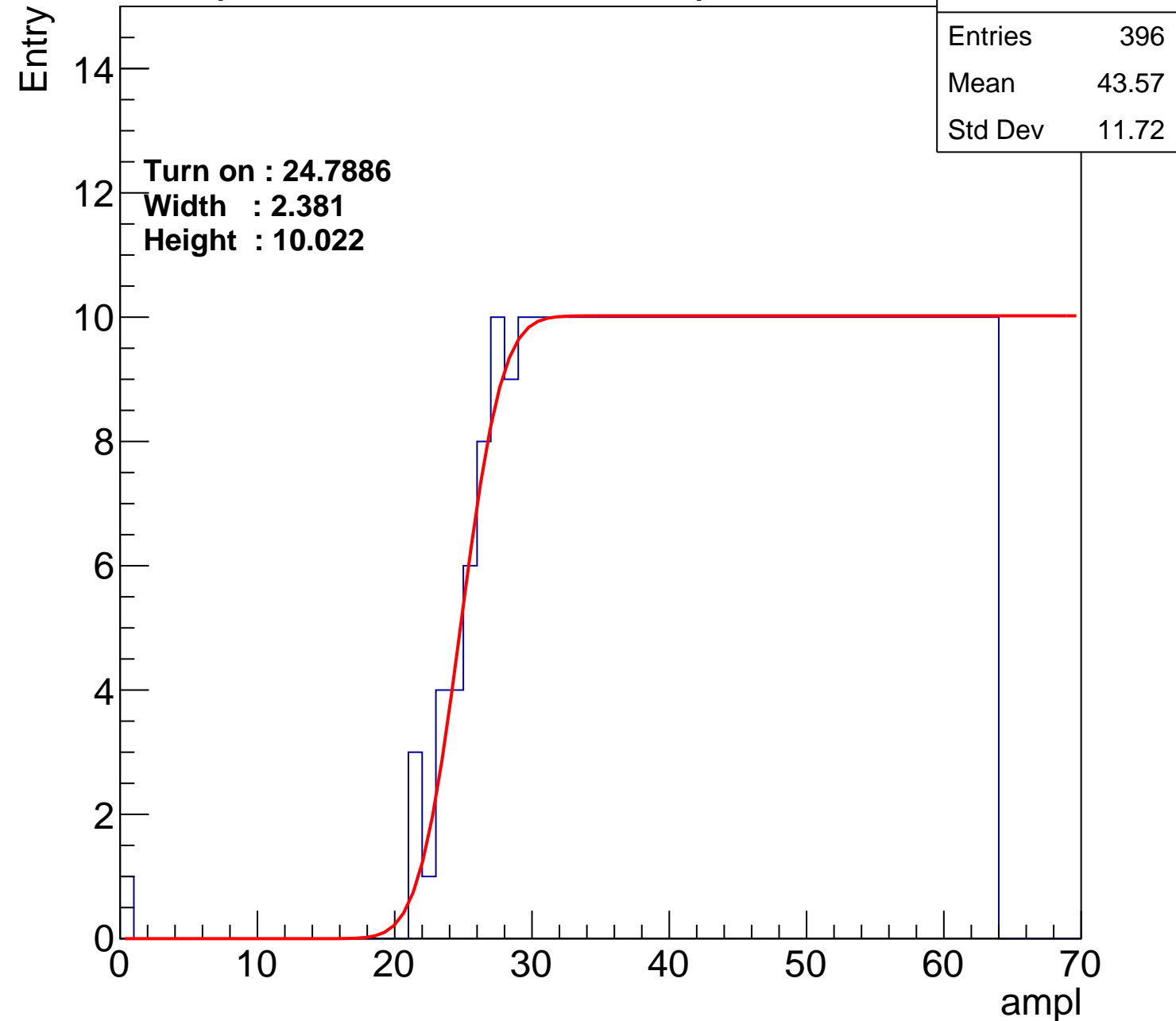
Width : 2.381

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch23

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.79
Std Dev	11.53

Turn on : 27.8522

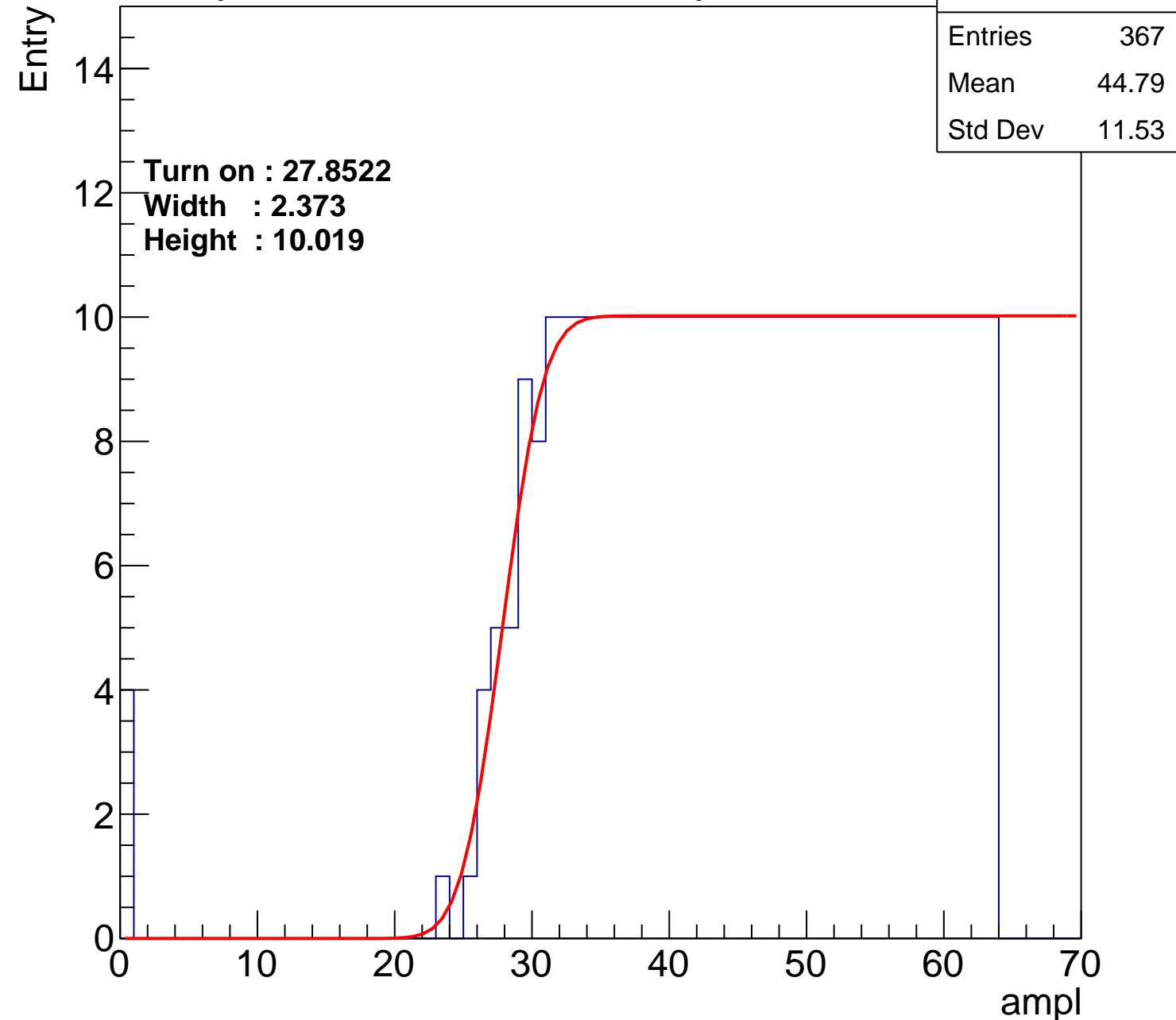
Width : 2.373

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch24

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.23
Std Dev	11.33

Turn on : 25.8582

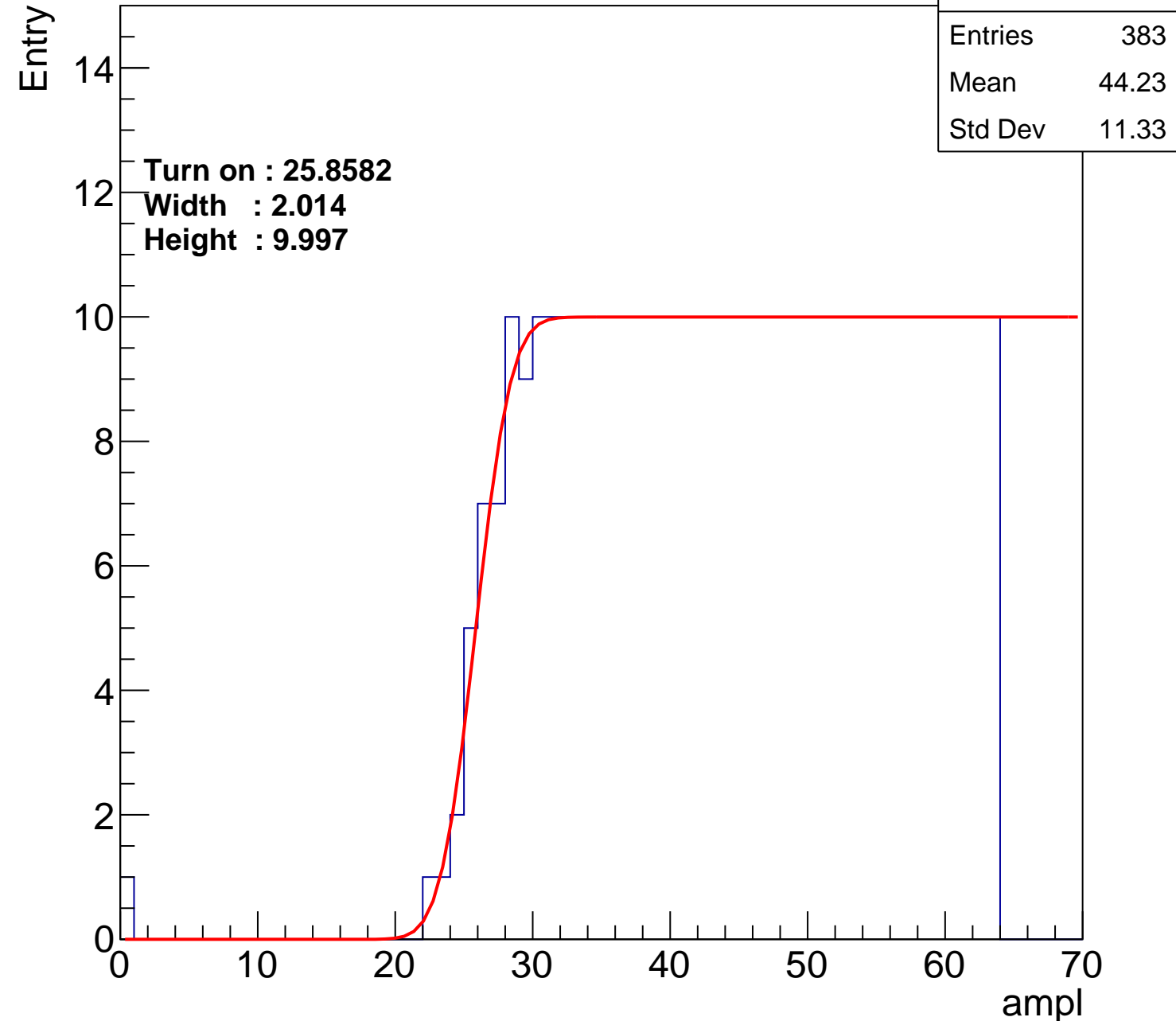
Width : 2.014

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch25

calib_packv5_042523_0143.root, FC#4, port A2

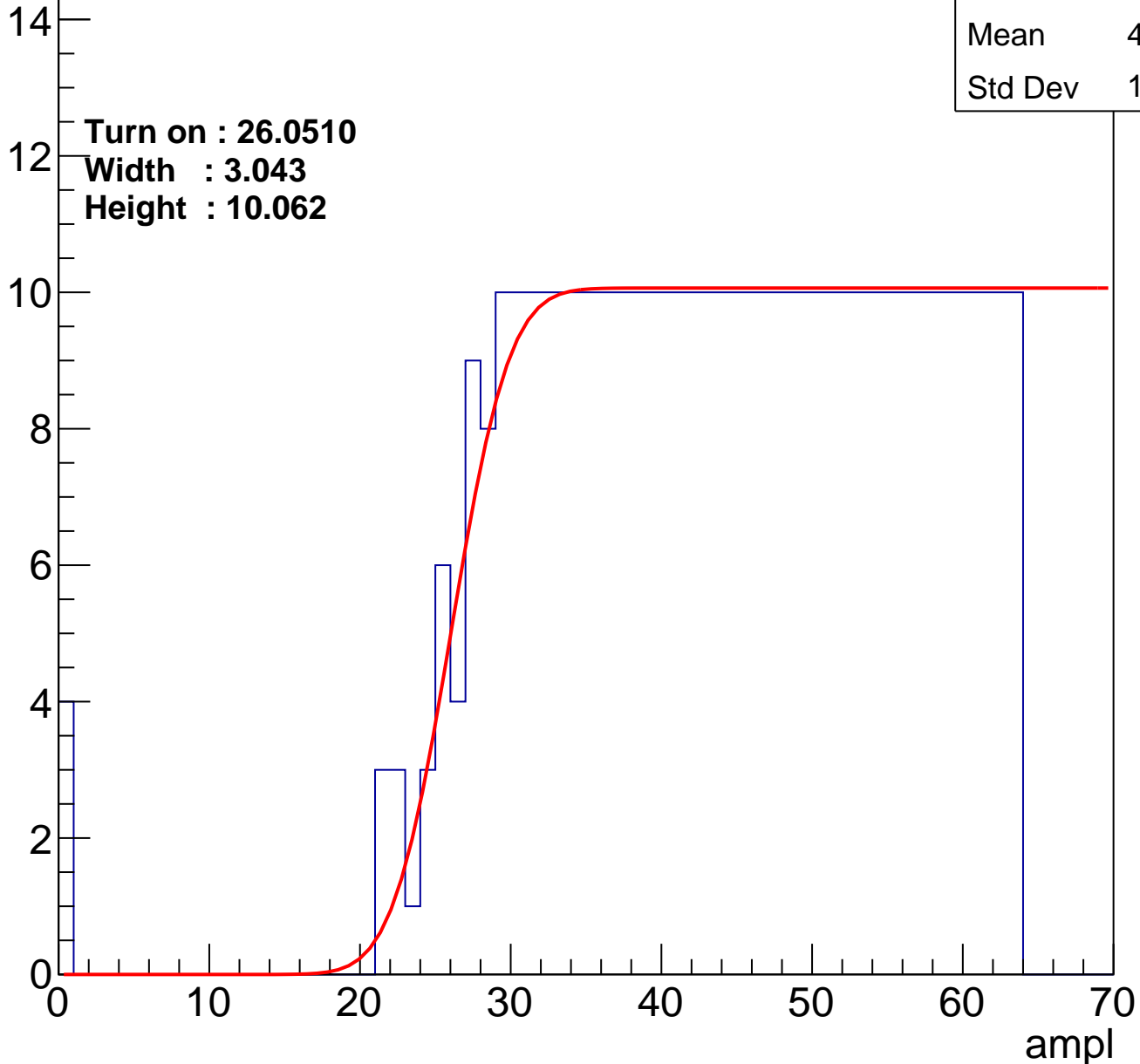
Entries	391
Mean	43.59
Std Dev	12.13

Turn on : 26.0510

Width : 3.043

Height : 10.062

Entry



B1L100S, U21-ch26

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.59
Std Dev	11.83

Turn on : 27.7968

Width : 3.125

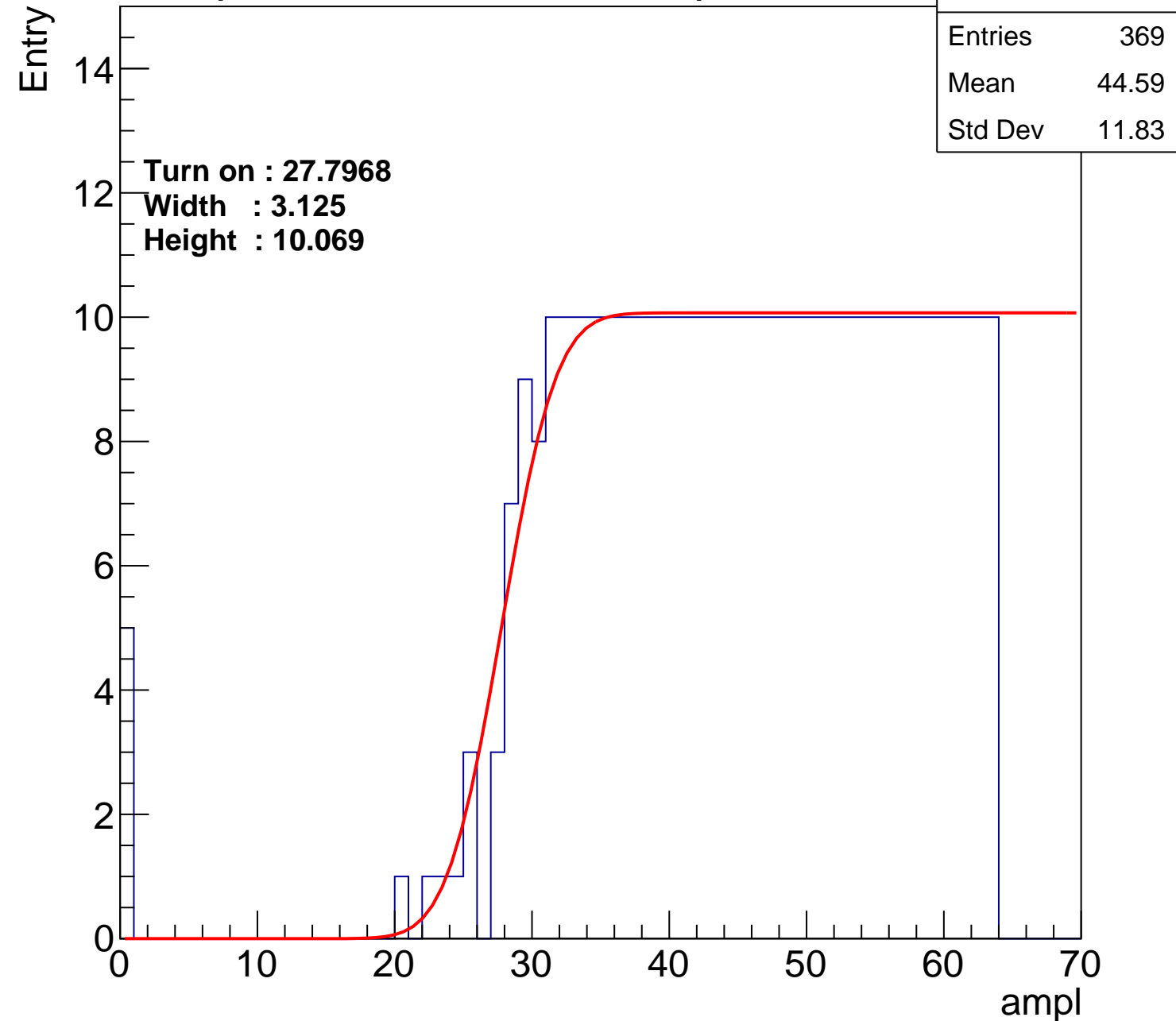
Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U21-ch27

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.31
Std Dev	11.37

Turn on : 29.1184

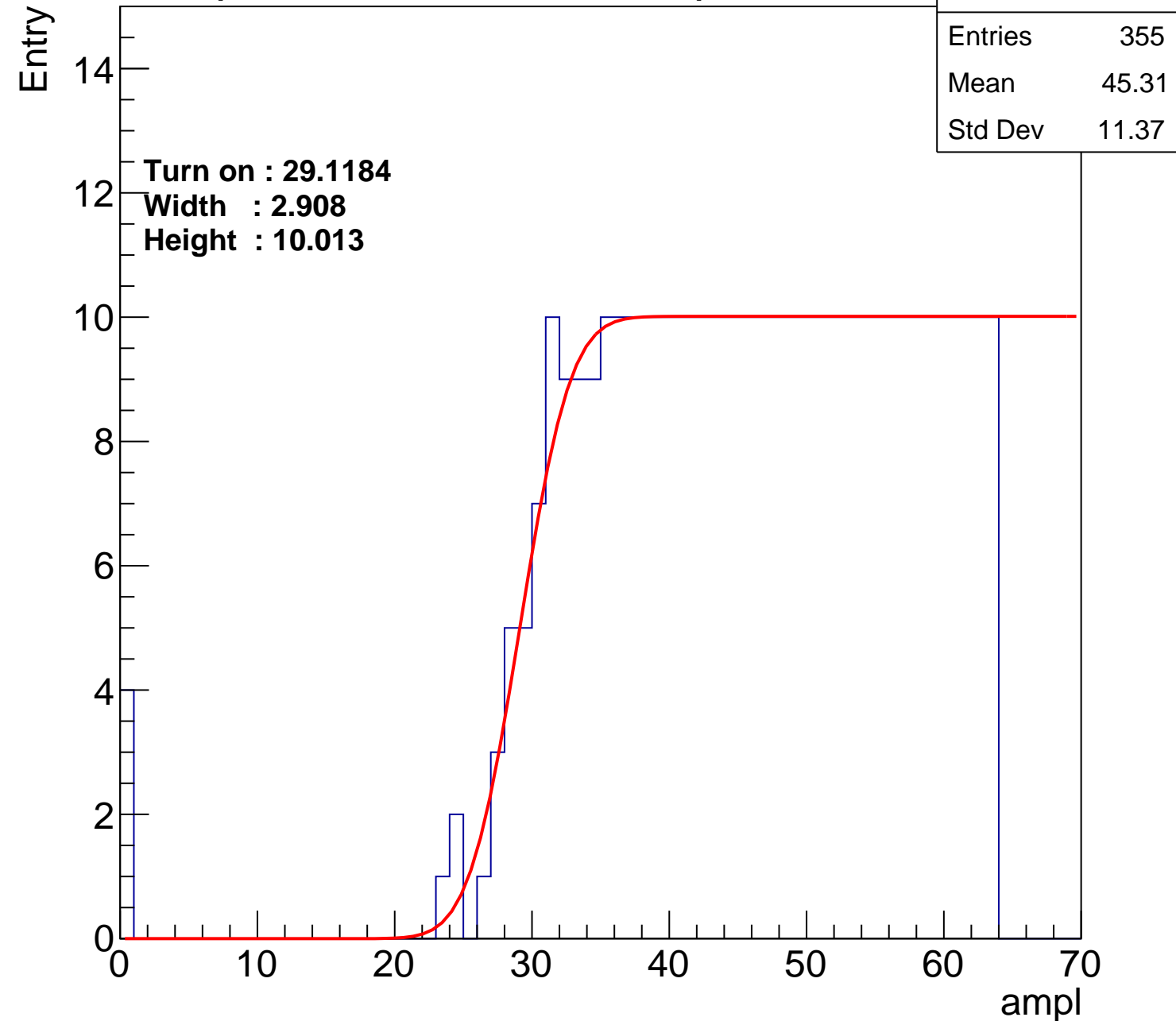
Width : 2.908

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch28

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.03
Std Dev	11.13

Turn on : 27.4960

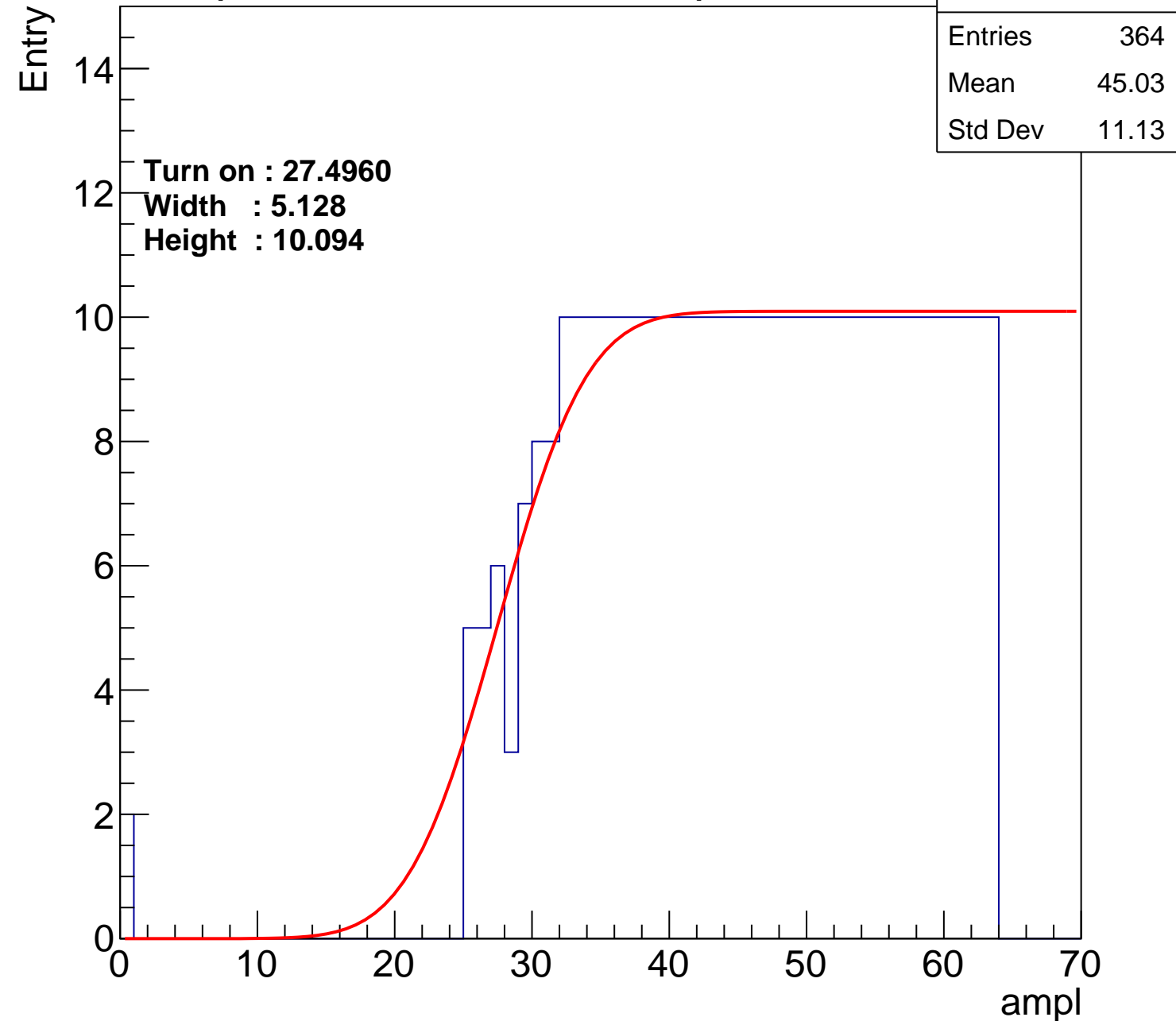
Width : 5.128

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch29

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.6
Std Dev	12.52

Turn on : 26.1961

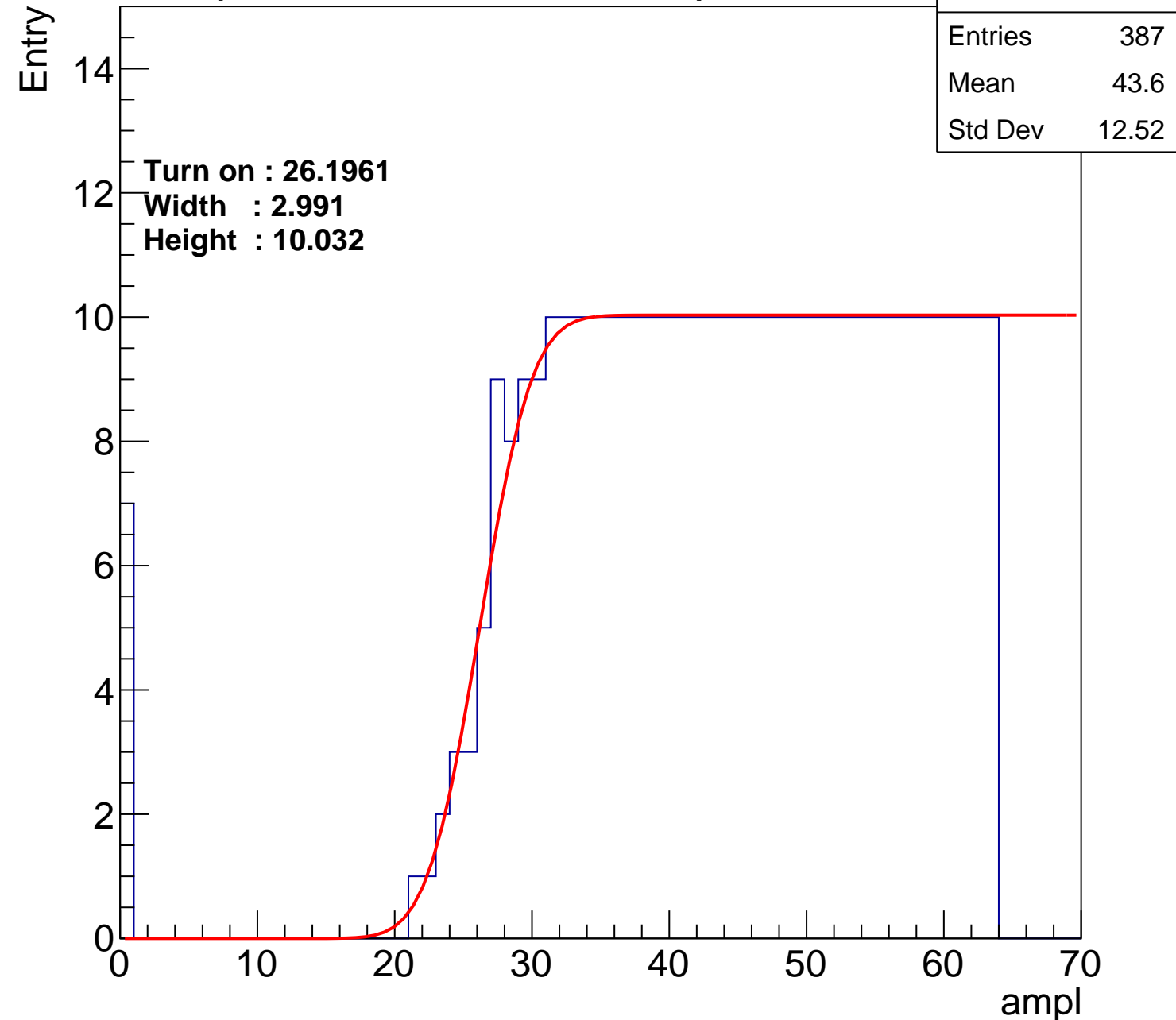
Width : 2.991

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch30

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.7537

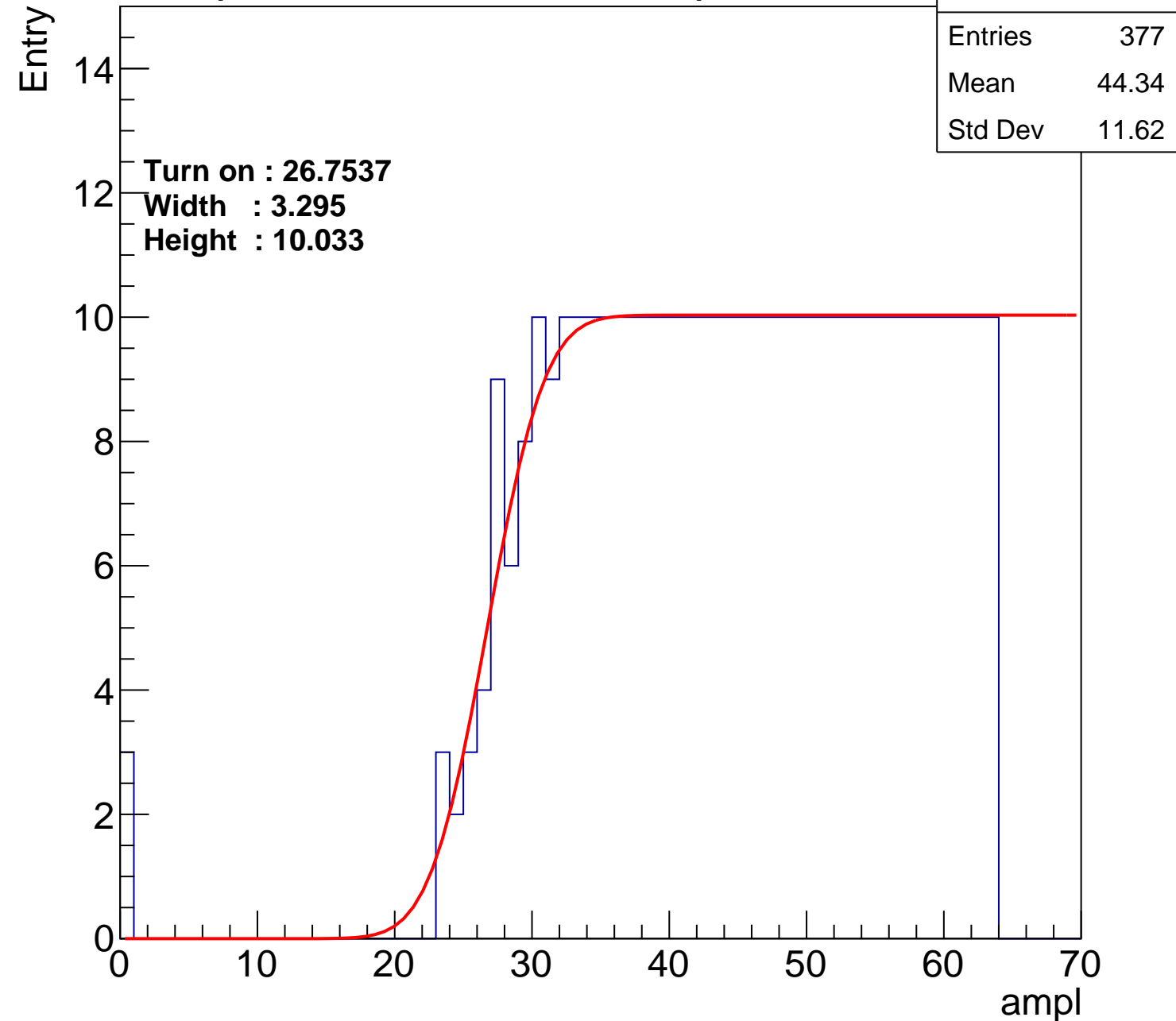
Width : 3.295

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch31

calib_packv5_042523_0143.root, FC#4, port A2

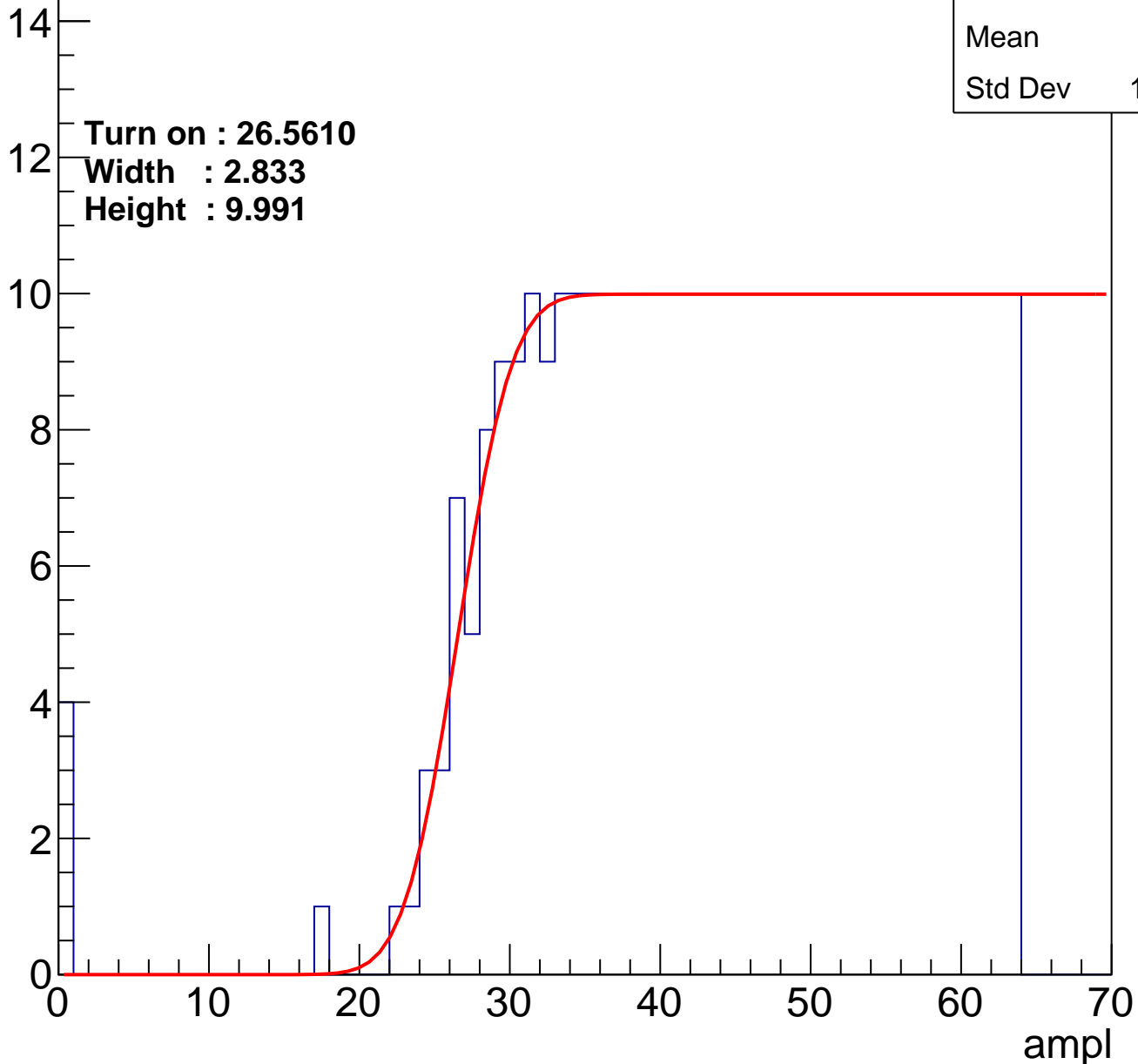
Entries	380
Mean	44.1
Std Dev	11.92

Turn on : 26.5610

Width : 2.833

Height : 9.991

Entry



B1L100S, U21-ch32

calib_packv5_042523_0143.root, FC#4, port A2

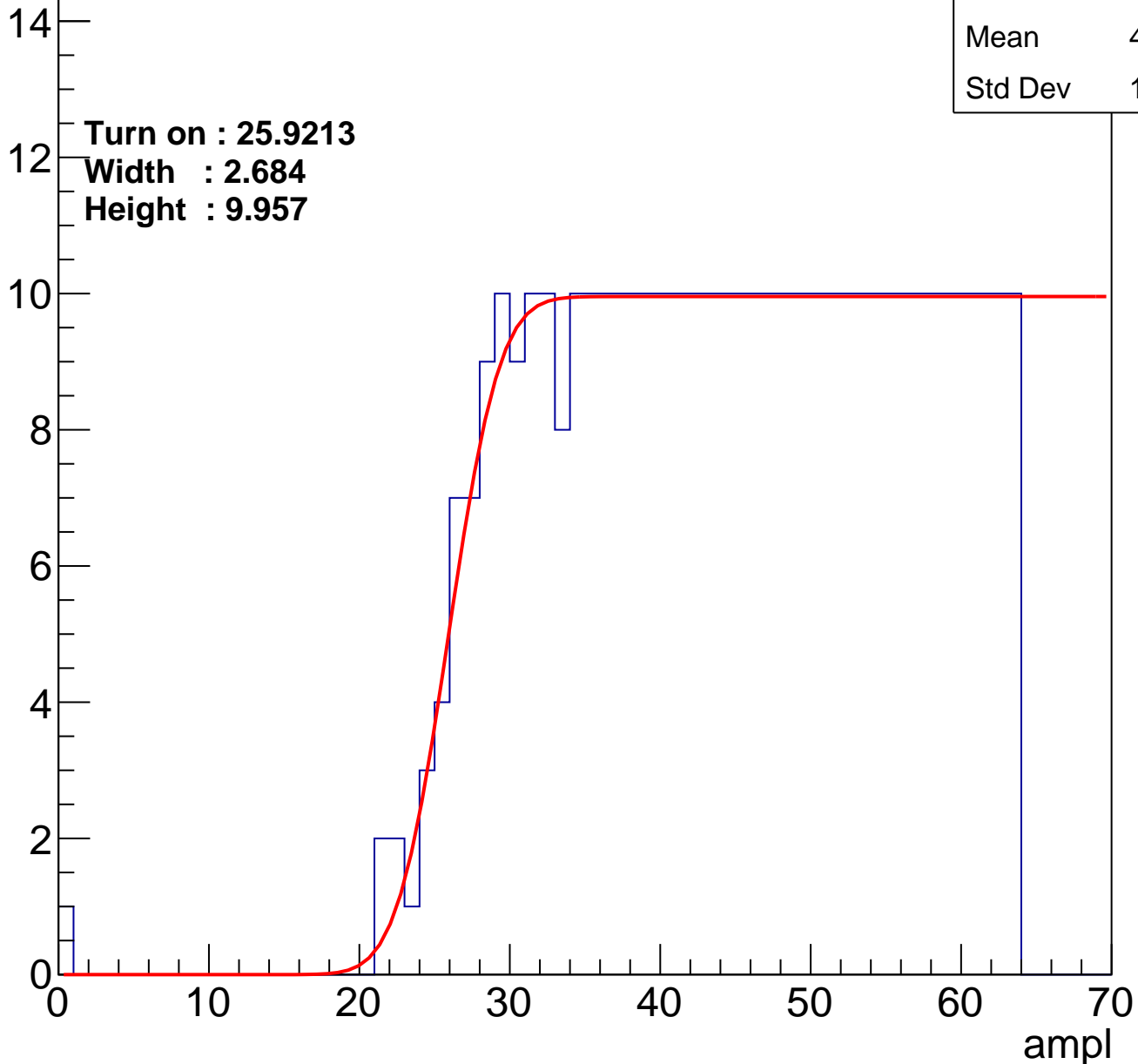
Entries	383
Mean	44.15
Std Dev	11.46

Turn on : 25.9213

Width : 2.684

Height : 9.957

Entry



B1L100S, U21-ch33

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.19
Std Dev	10.9

Turn on : 27.8935

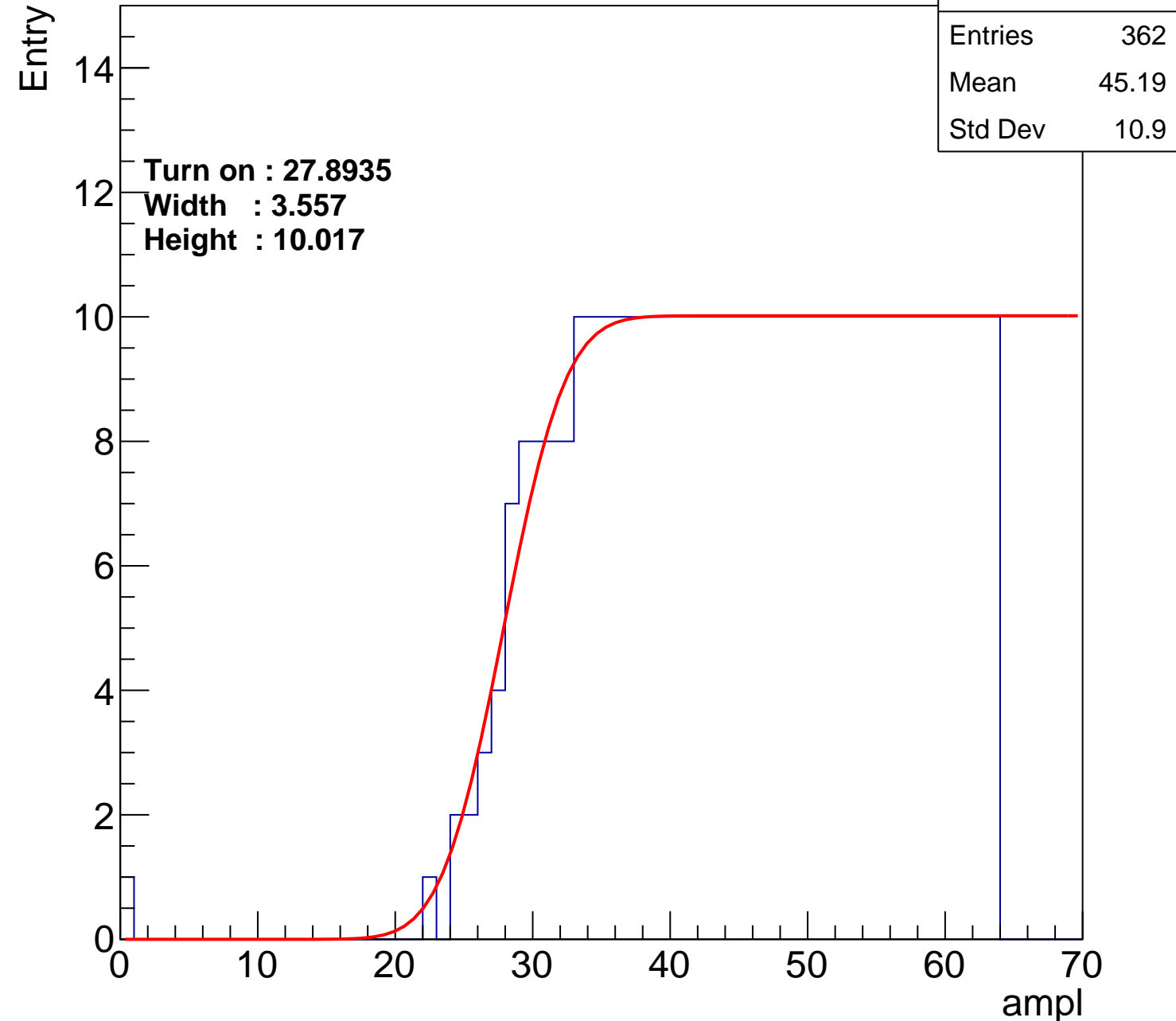
Width : 3.557

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch34

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.66
Std Dev	11.35

Turn on : 27.2951

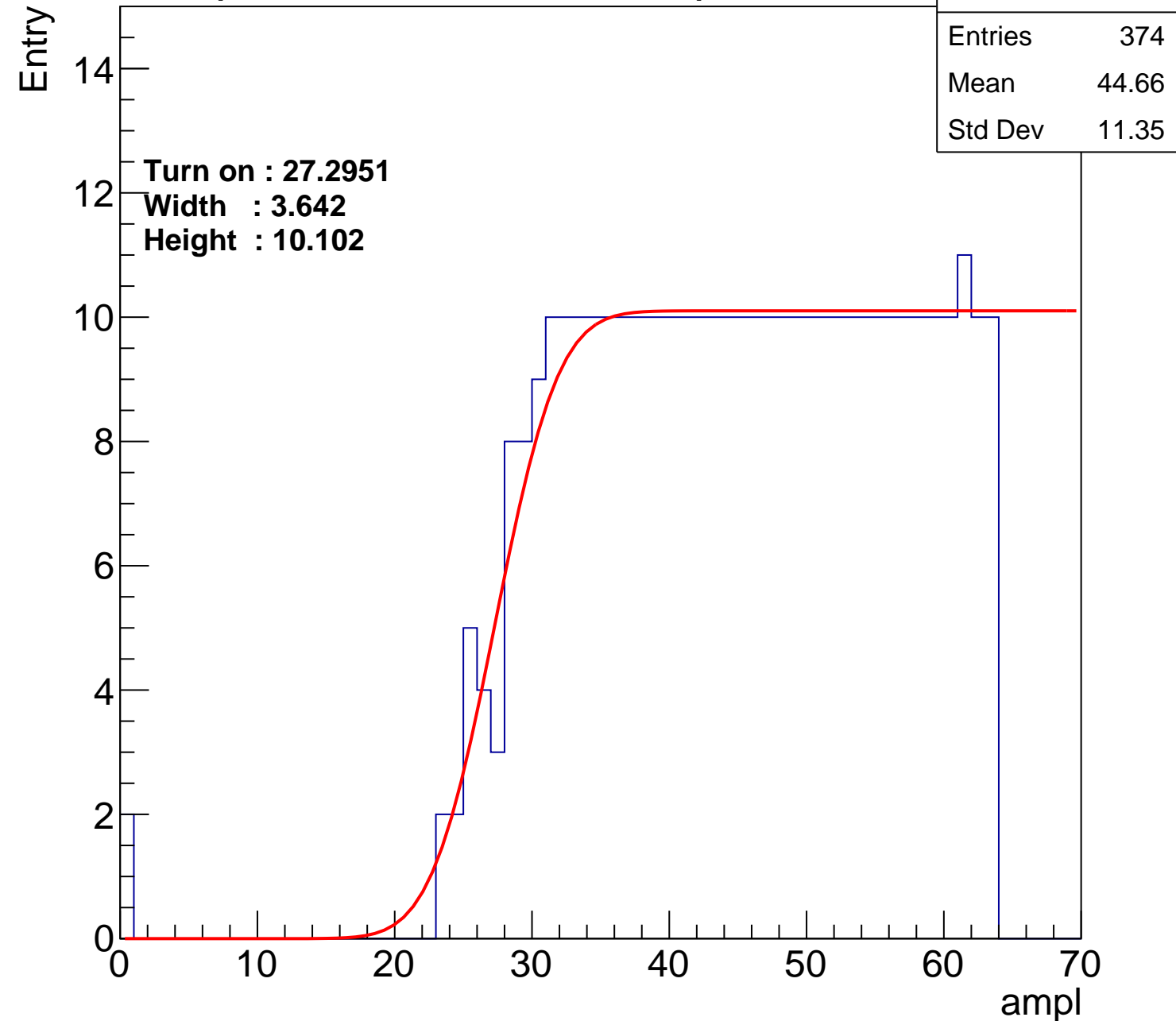
Width : 3.642

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch35

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.13
Std Dev	10.89

Turn on : 27.3665

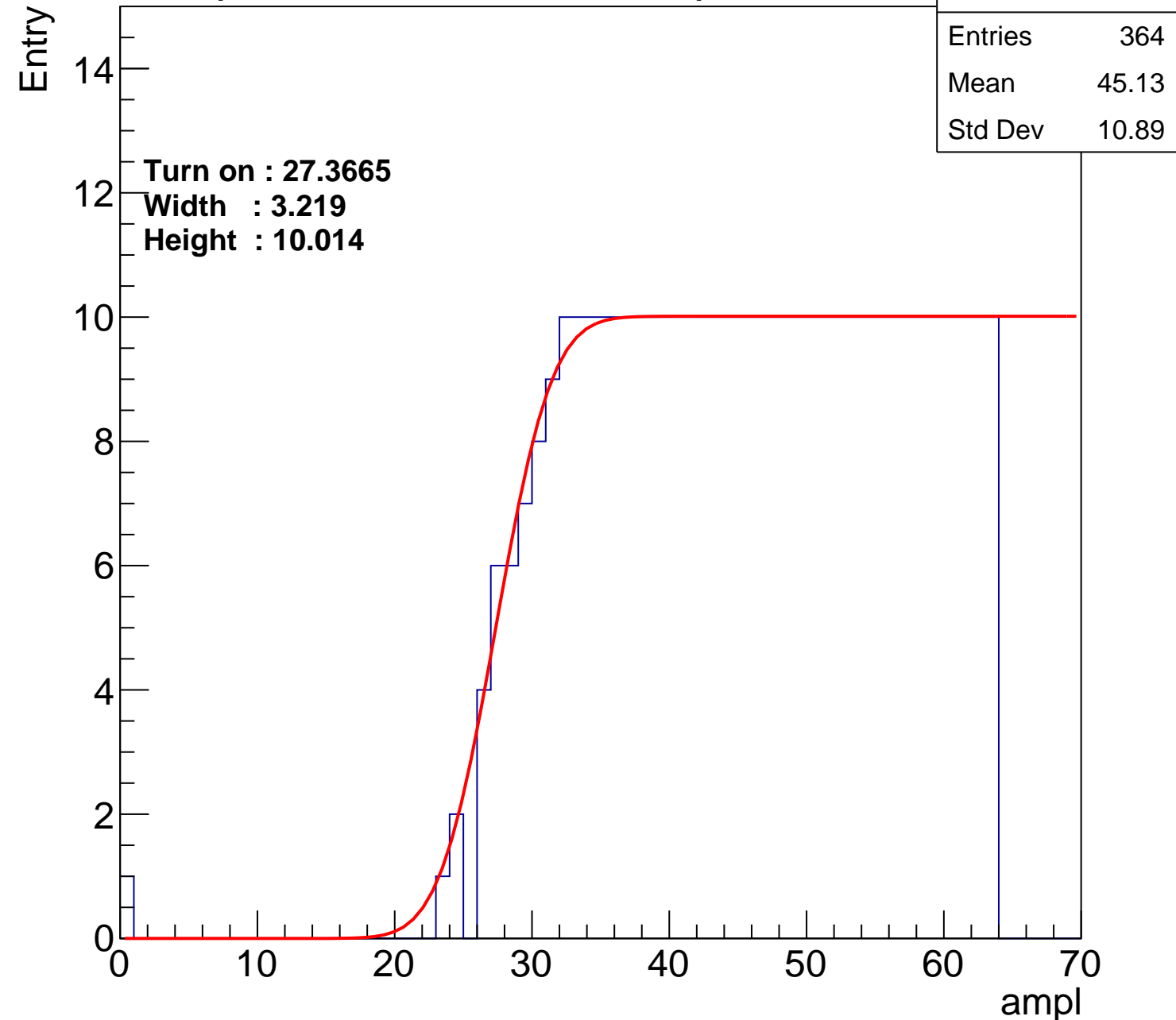
Width : 3.219

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch36

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.52
Std Dev	11.38

Turn on : 26.9329

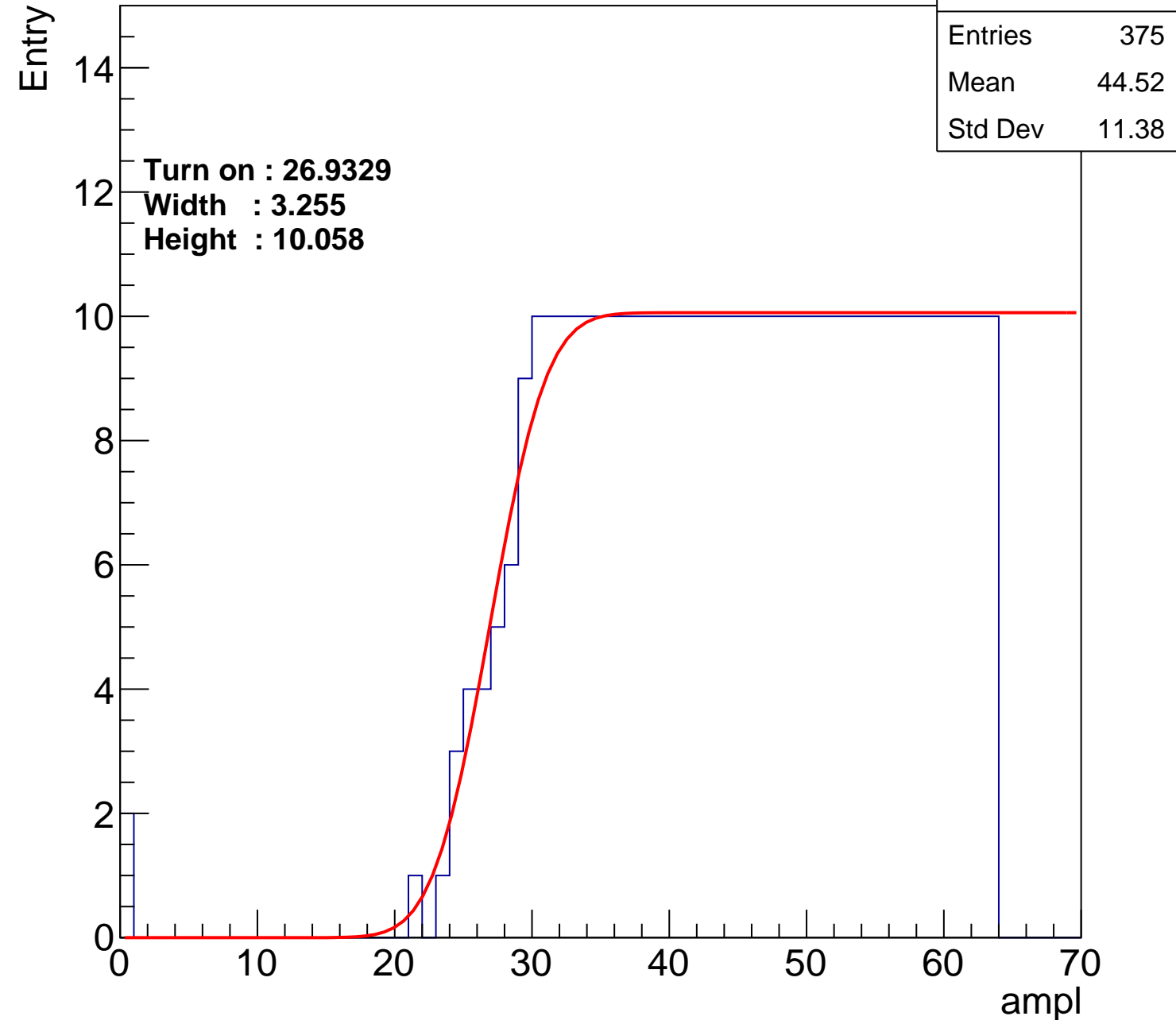
Width : 3.255

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch37

calib_packv5_042523_0143.root, FC#4, port A2

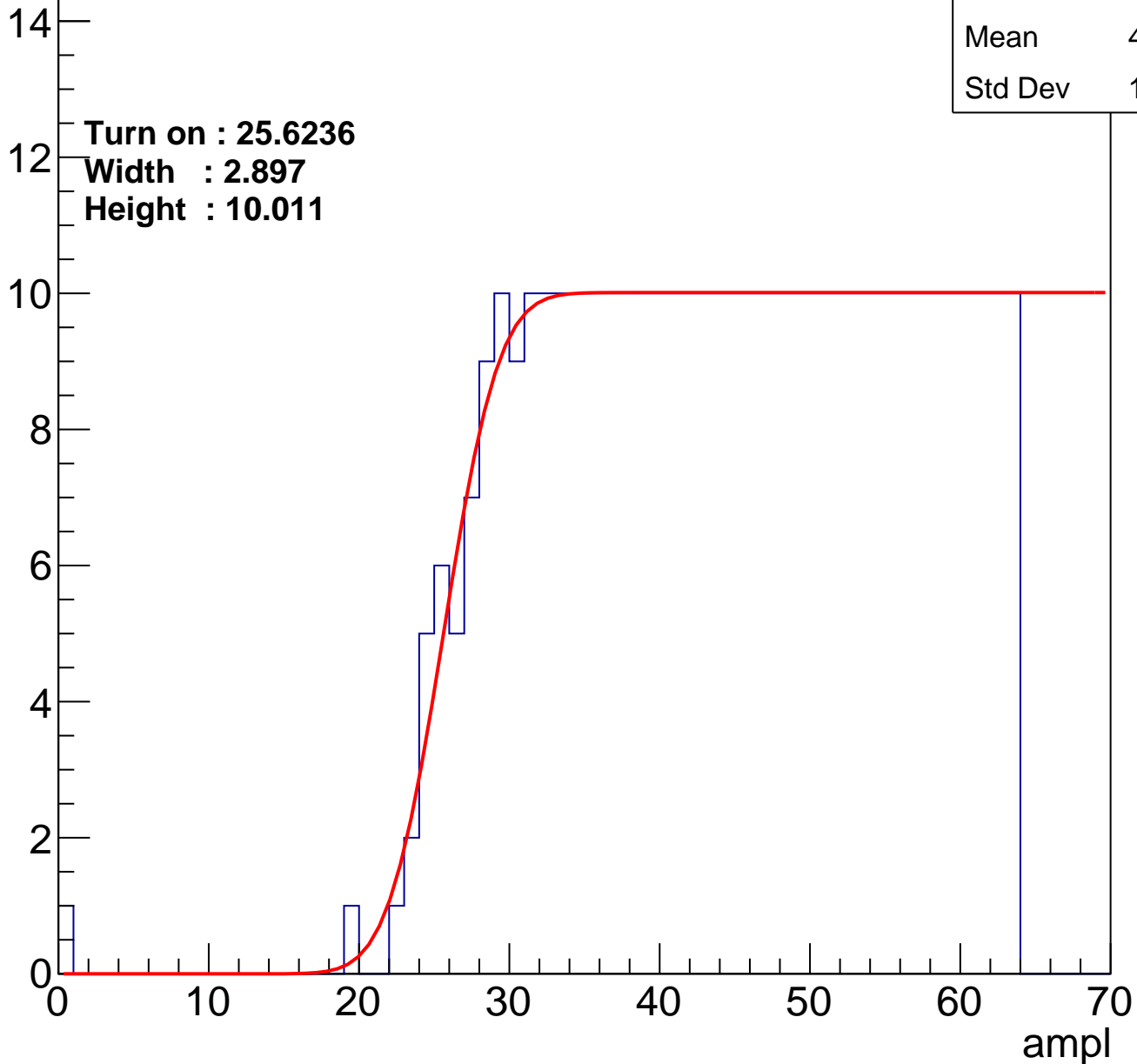
Entries	386
Mean	44.04
Std Dev	11.49

Turn on : 25.6236

Width : 2.897

Height : 10.011

Entry



B1L100S, U21-ch38

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 26.0900

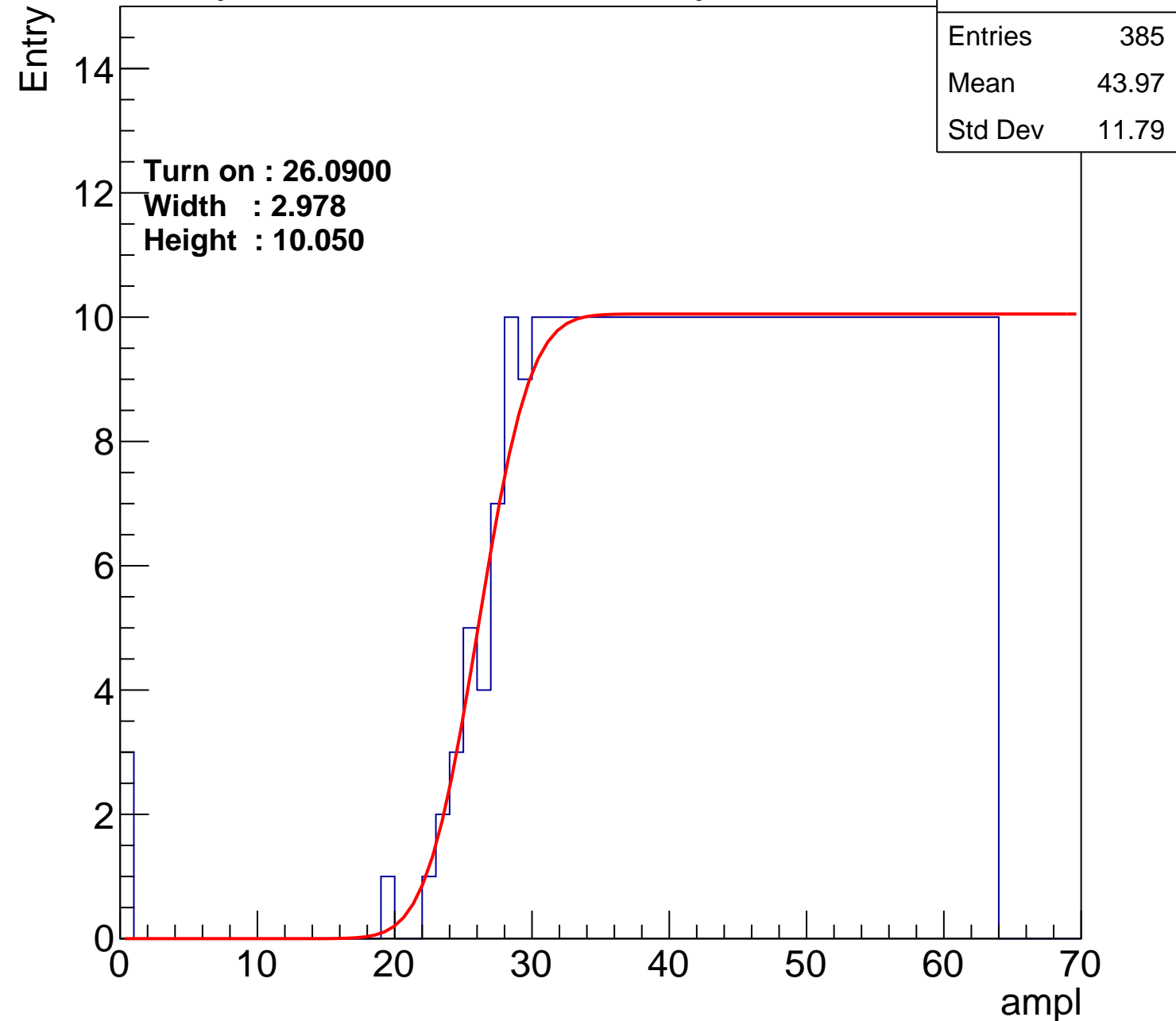
Width : 2.978

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch39

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.89
Std Dev	11.34

Turn on : 27.8123

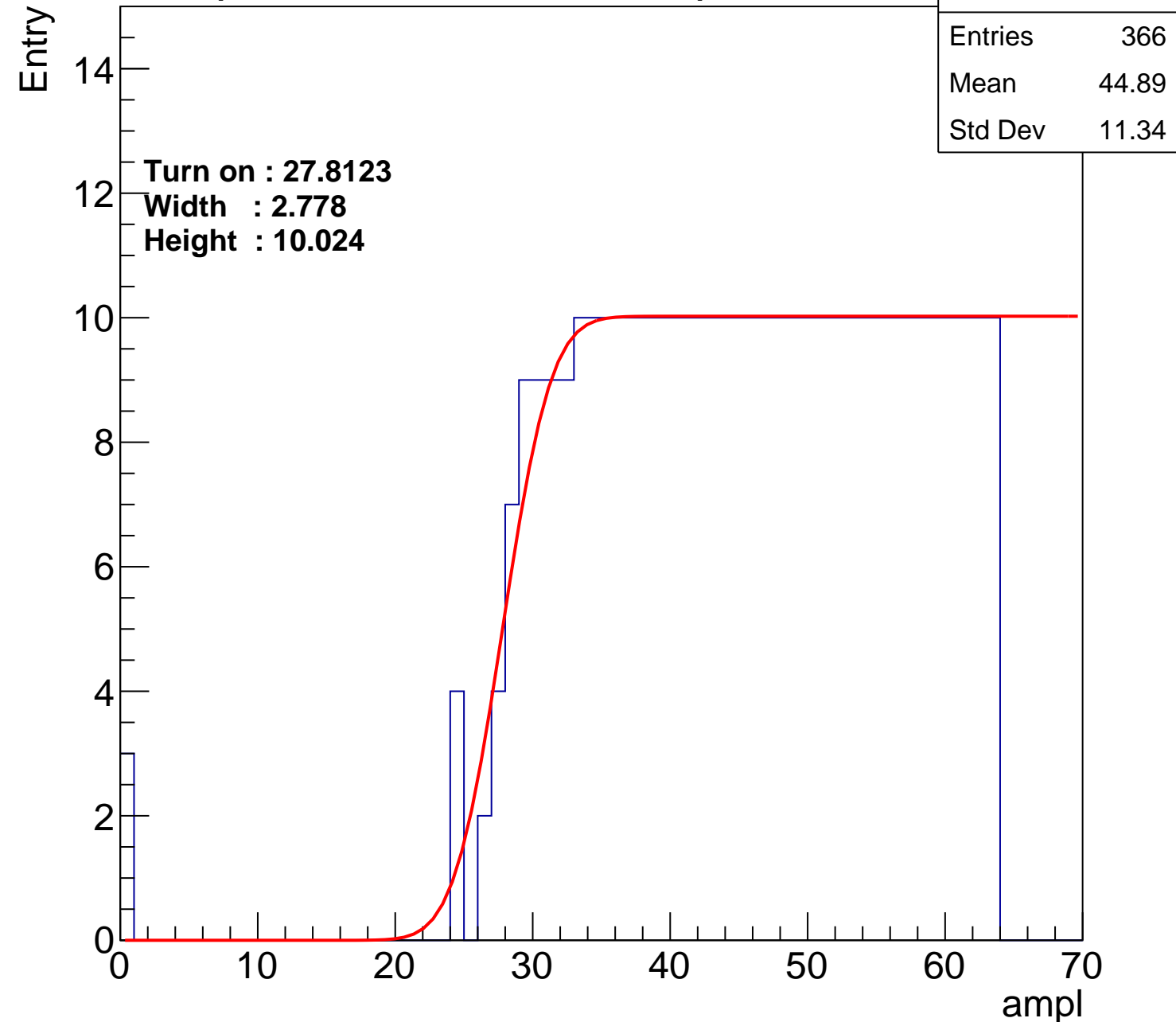
Width : 2.778

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch40

calib_packv5_042523_0143.root, FC#4, port A2

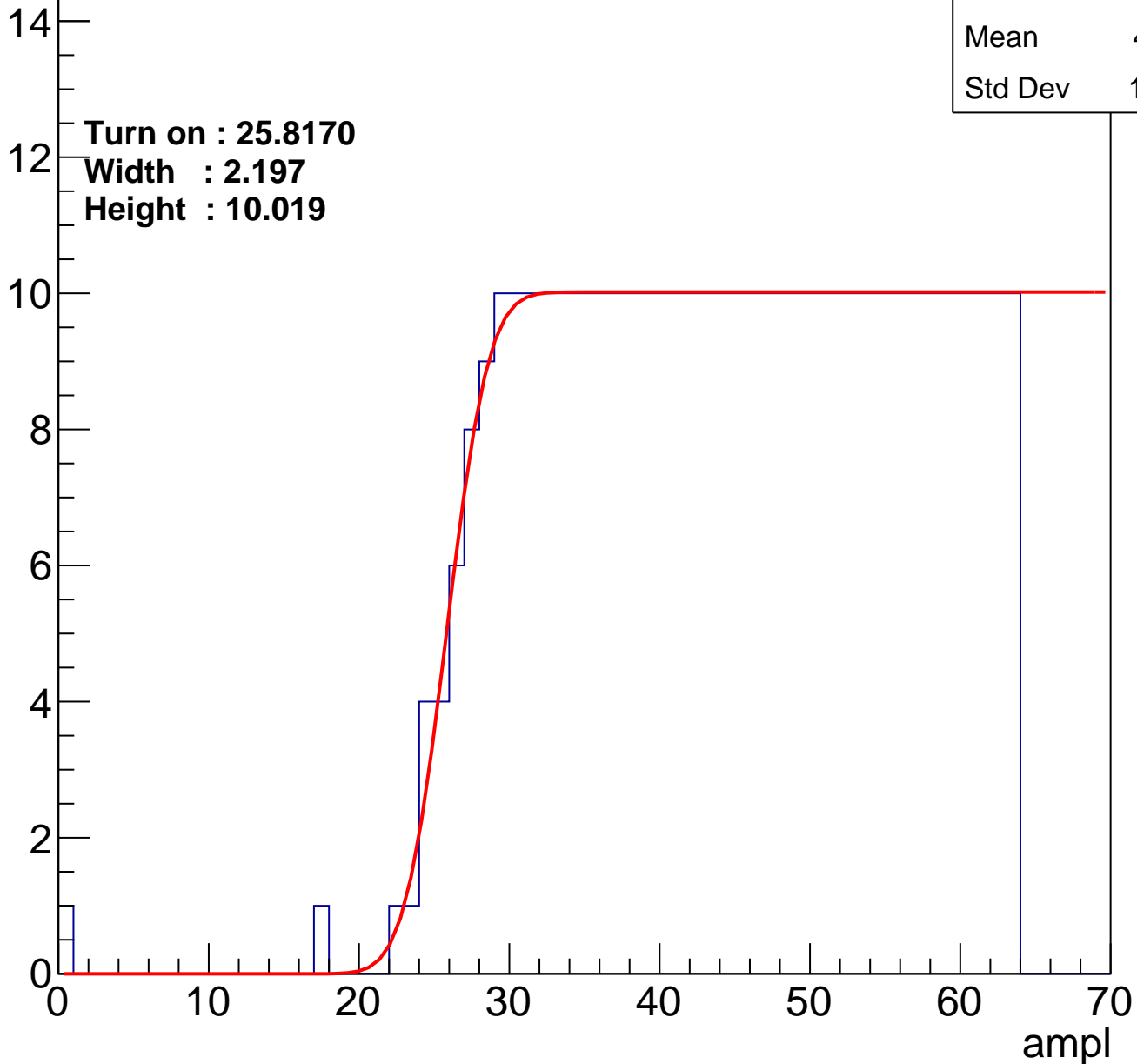
Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 25.8170

Width : 2.197

Height : 10.019

Entry



B1L100S, U21-ch41

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.7
Std Dev	11.1

Turn on : 26.9931

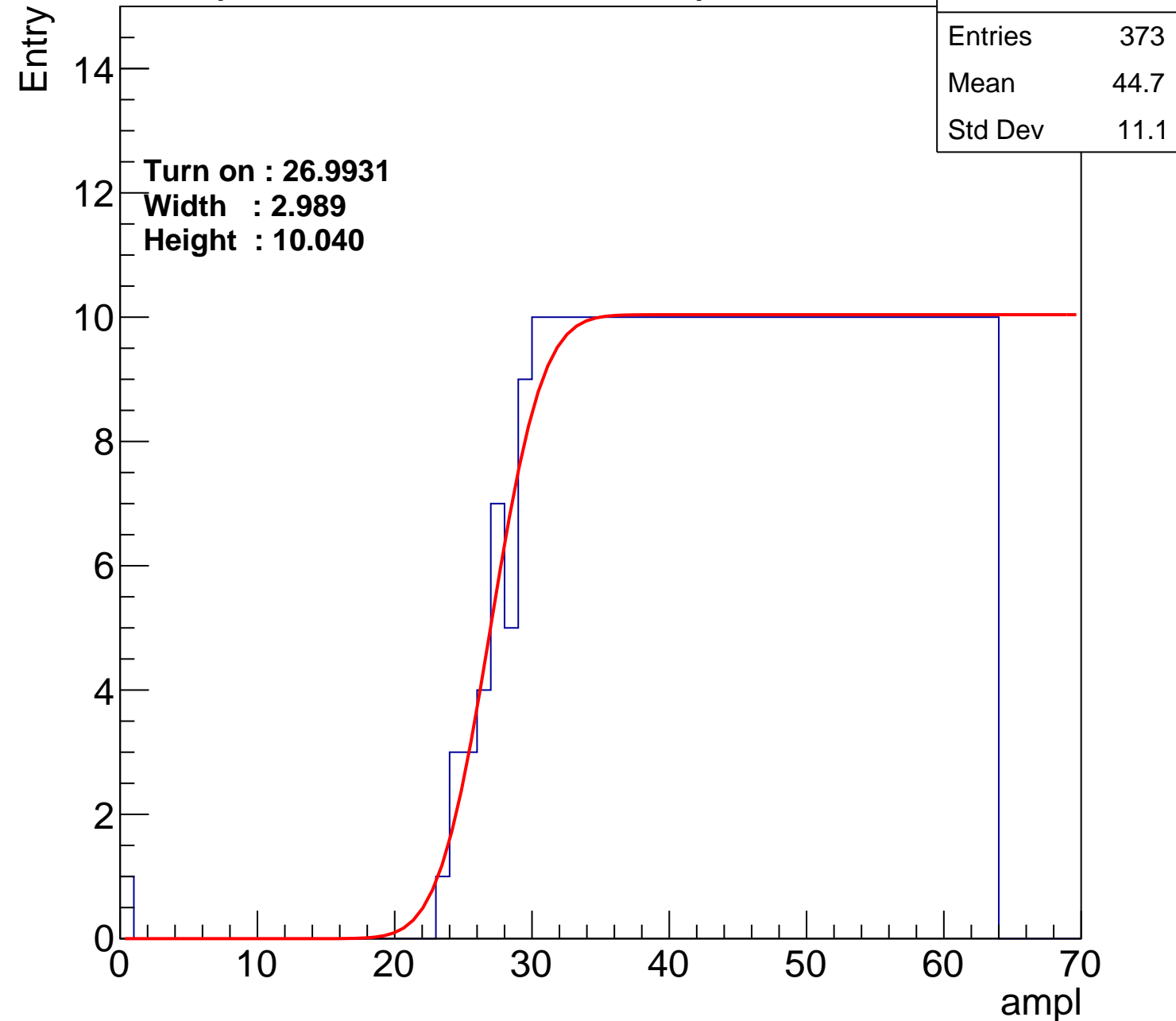
Width : 2.989

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch42

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.32
Std Dev	11.66

Turn on : 26.3587

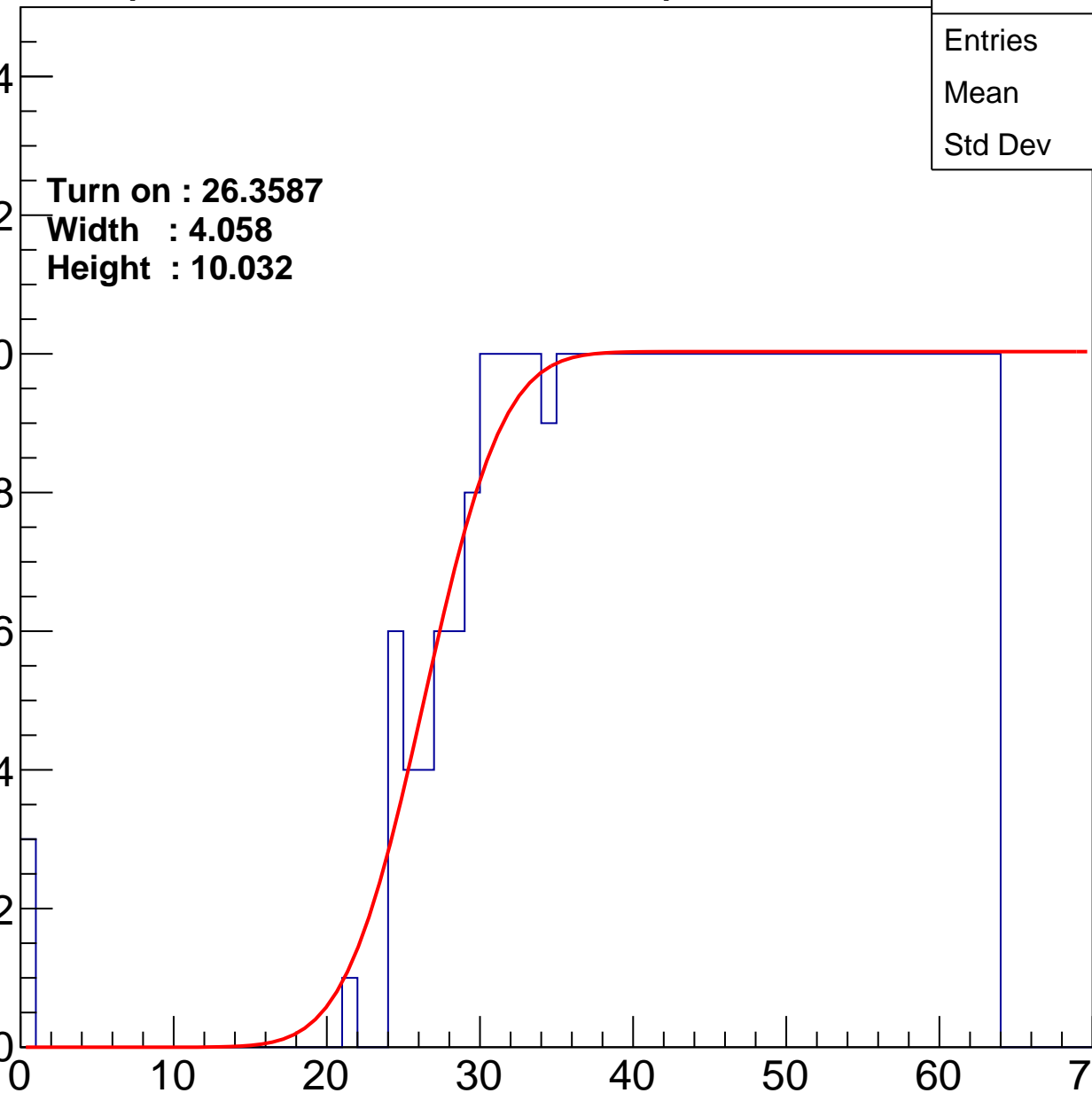
Width : 4.058

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch43

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.7215

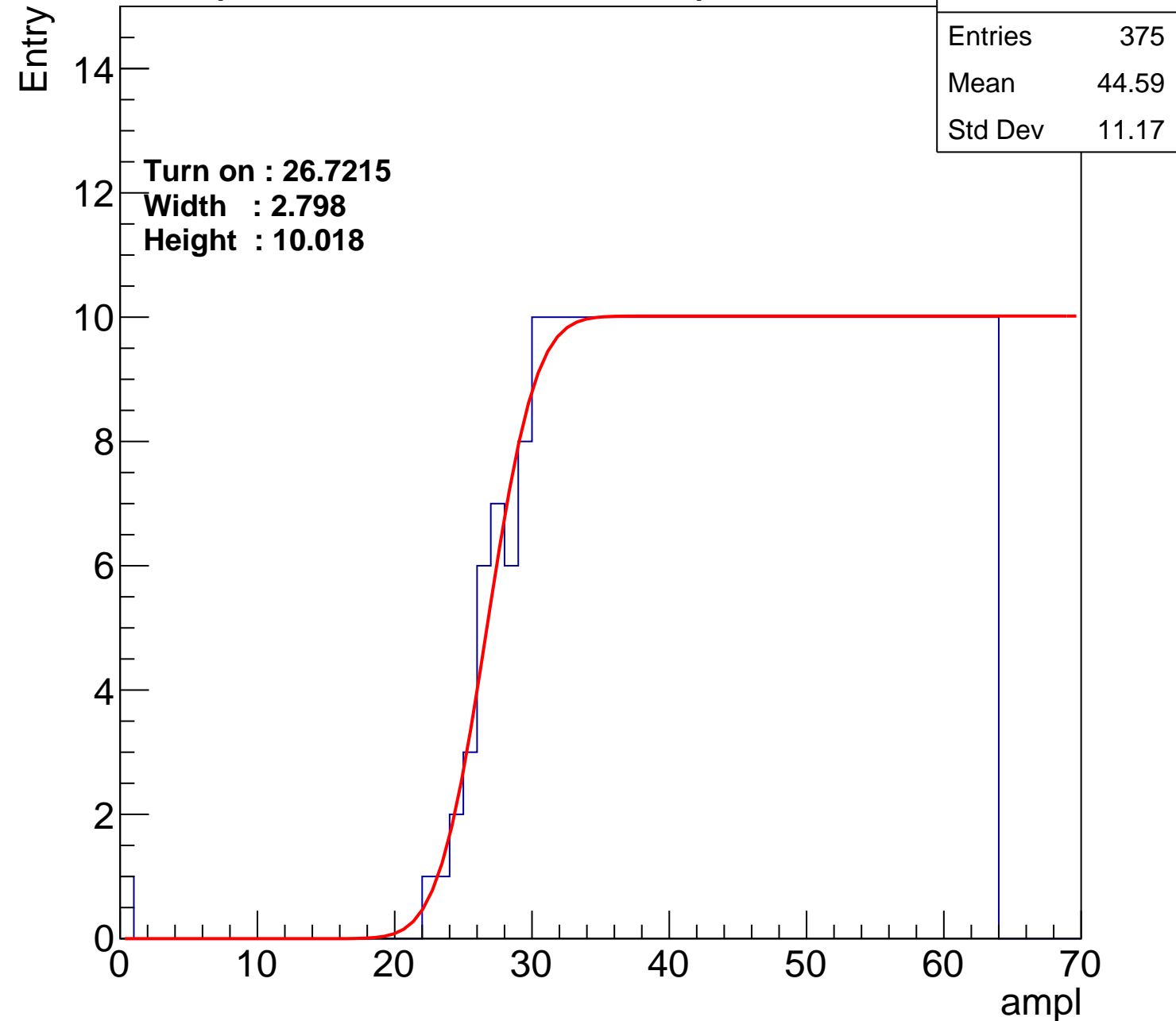
Width : 2.798

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch44

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 26.3535

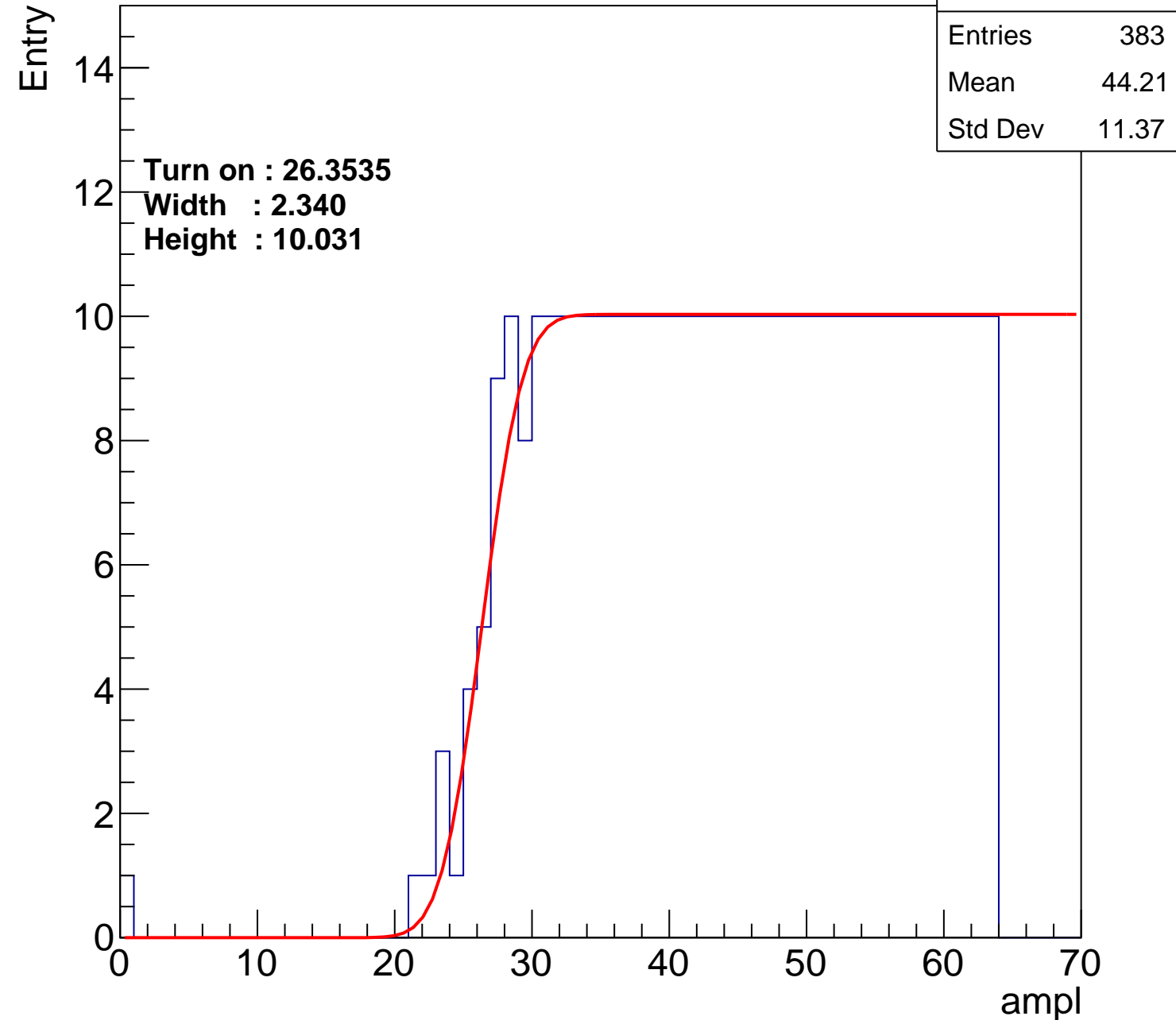
Width : 2.340

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#4, port A2

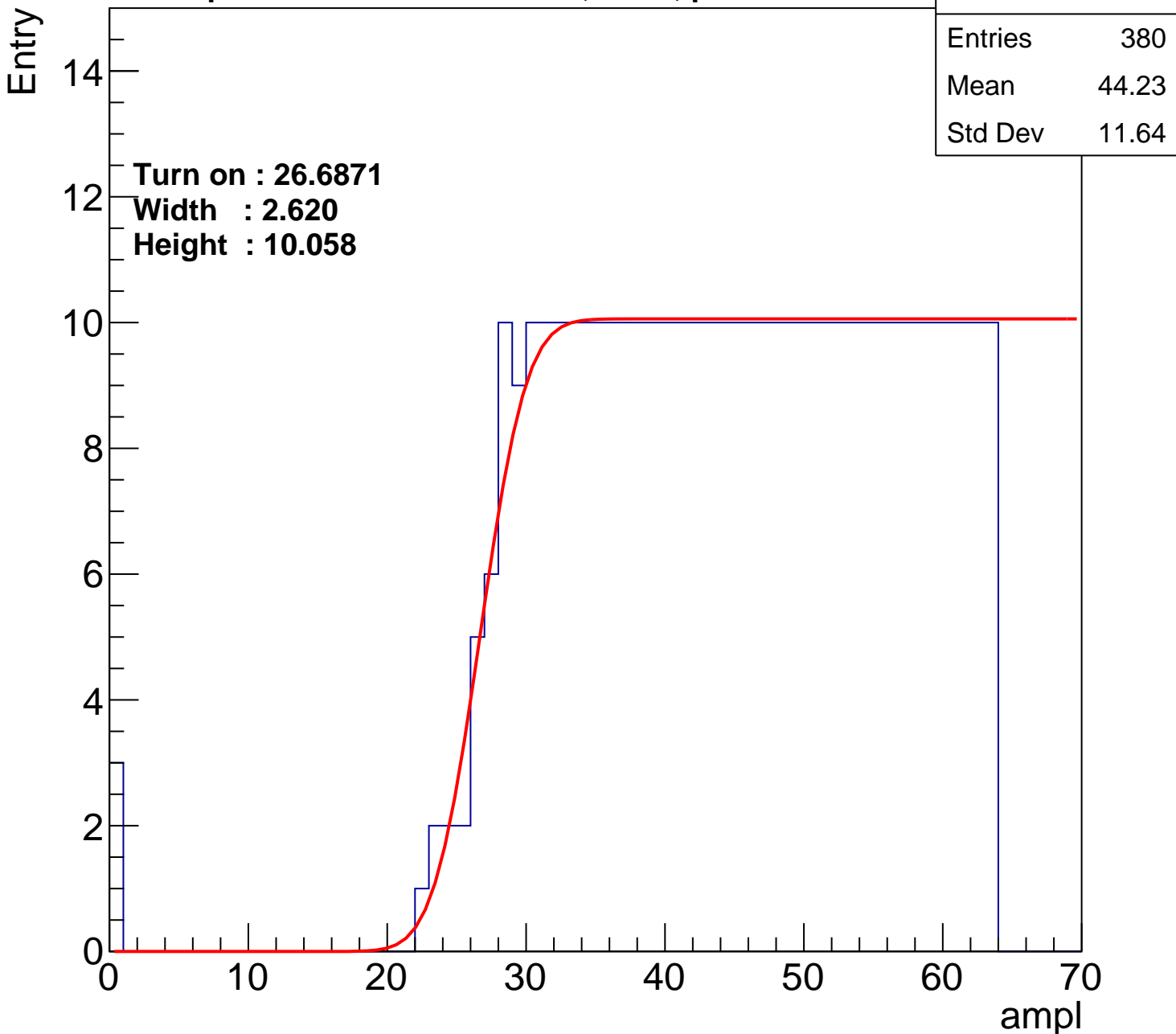
Entries	380
Mean	44.23
Std Dev	11.64

Mean	44.23
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Std Dev	11.64
---------	-------

Width : 2.620

Height : 10.058



B1L100S, U21-ch46

calib_packv5_042523_0143.root, FC#4, port A2

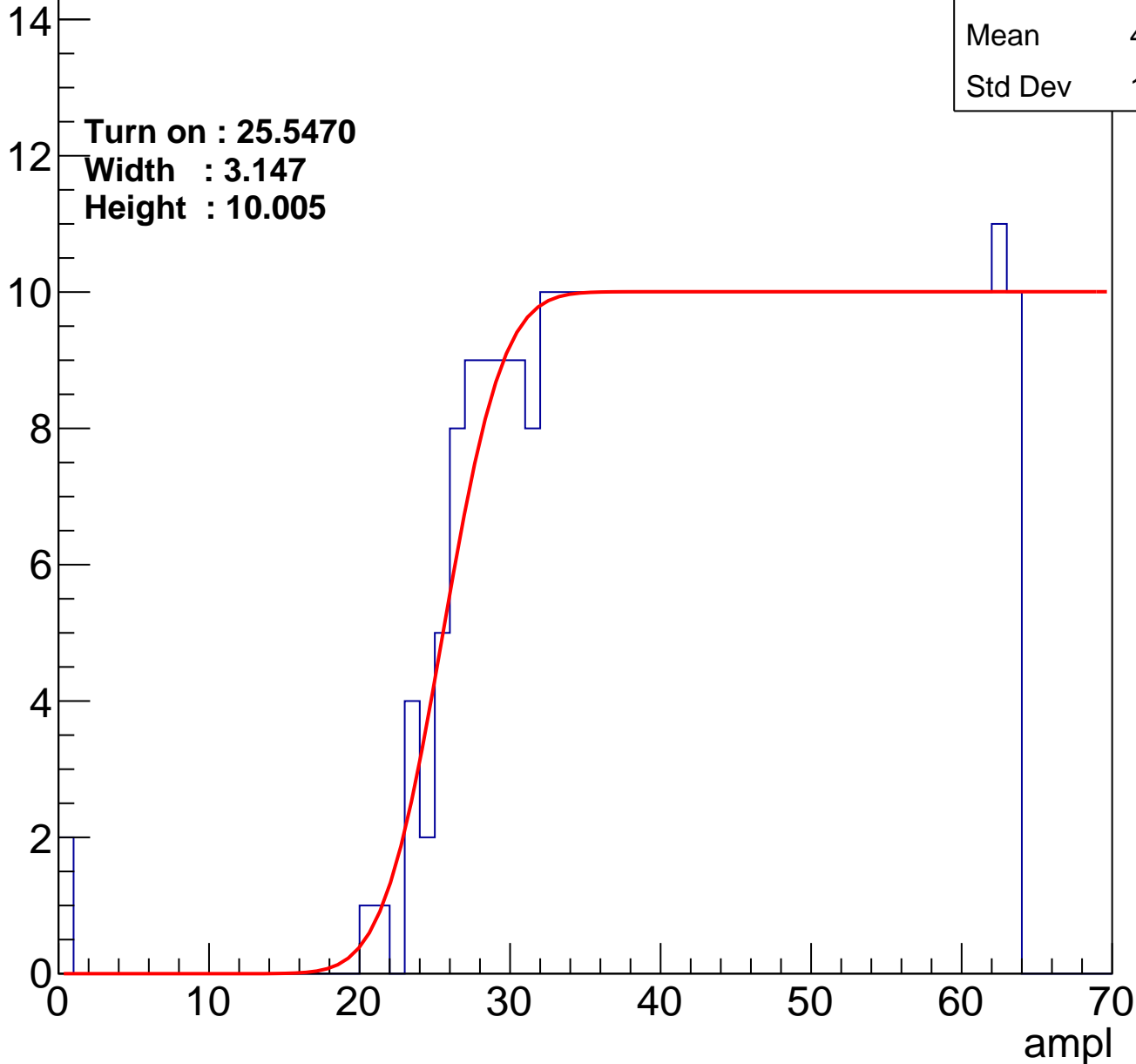
Entries	388
Mean	43.94
Std Dev	11.74

Turn on : 25.5470

Width : 3.147

Height : 10.005

Entry



B1L100S, U21-ch47

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.79
Std Dev	11.05

Turn on : 27.0729

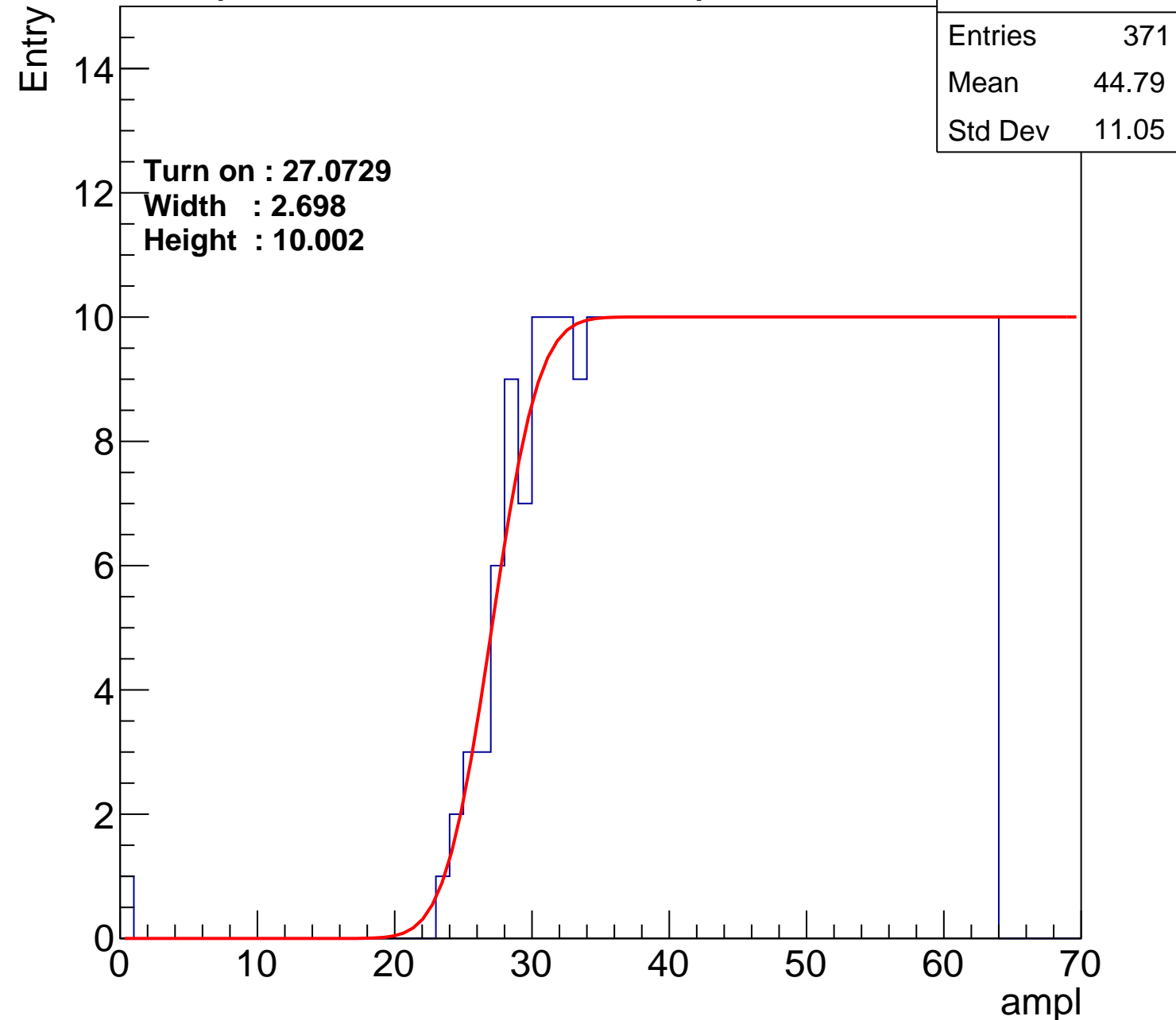
Width : 2.698

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch48

calib_packv5_042523_0143.root, FC#4, port A2

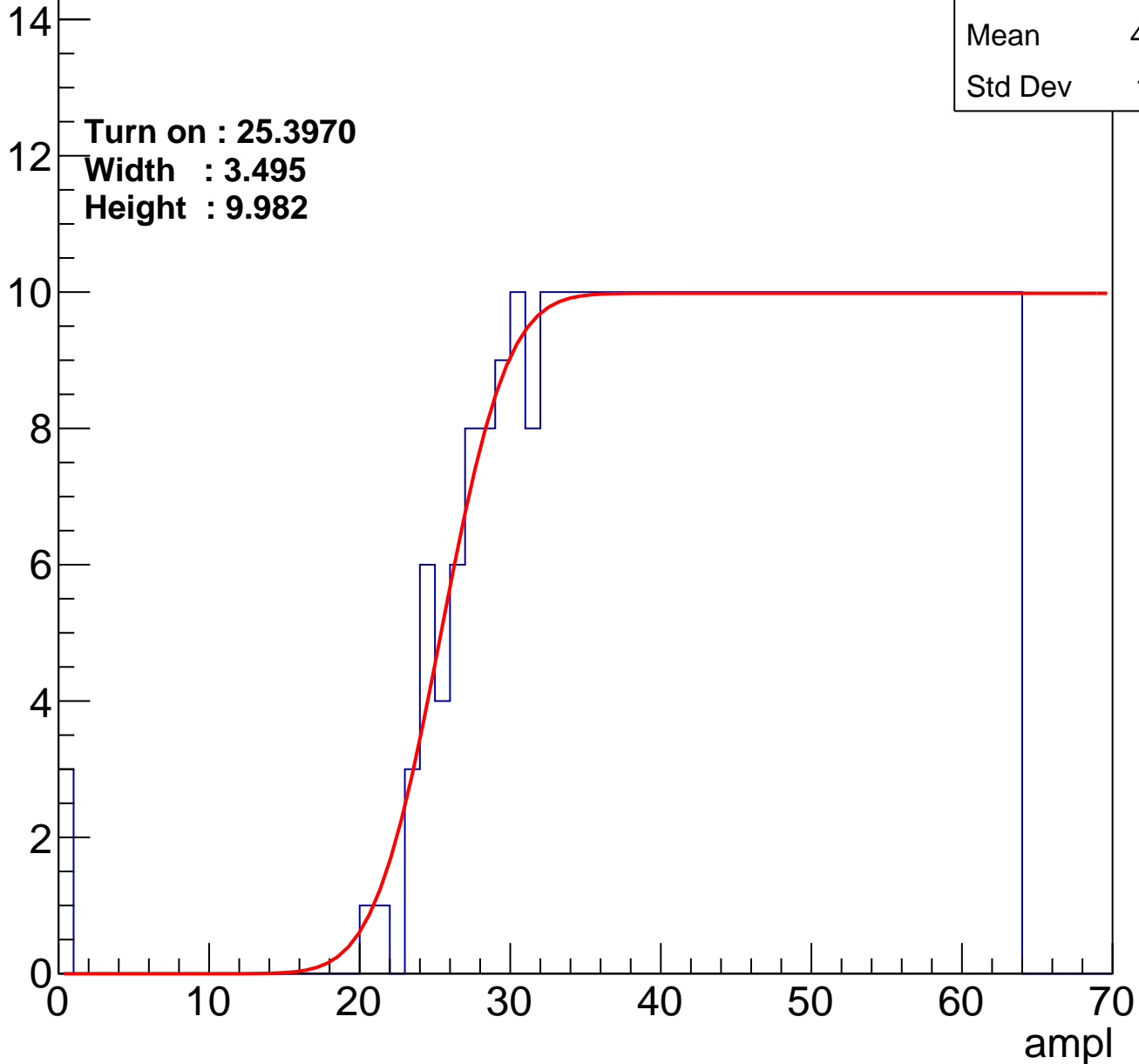
Entries	387
Mean	43.82
Std Dev	11.91

Turn on : 25.3970

Width : 3.495

Height : 9.982

Entry



B1L100S, U21-ch49

calib_packv5_042523_0143.root, FC#4, port A2

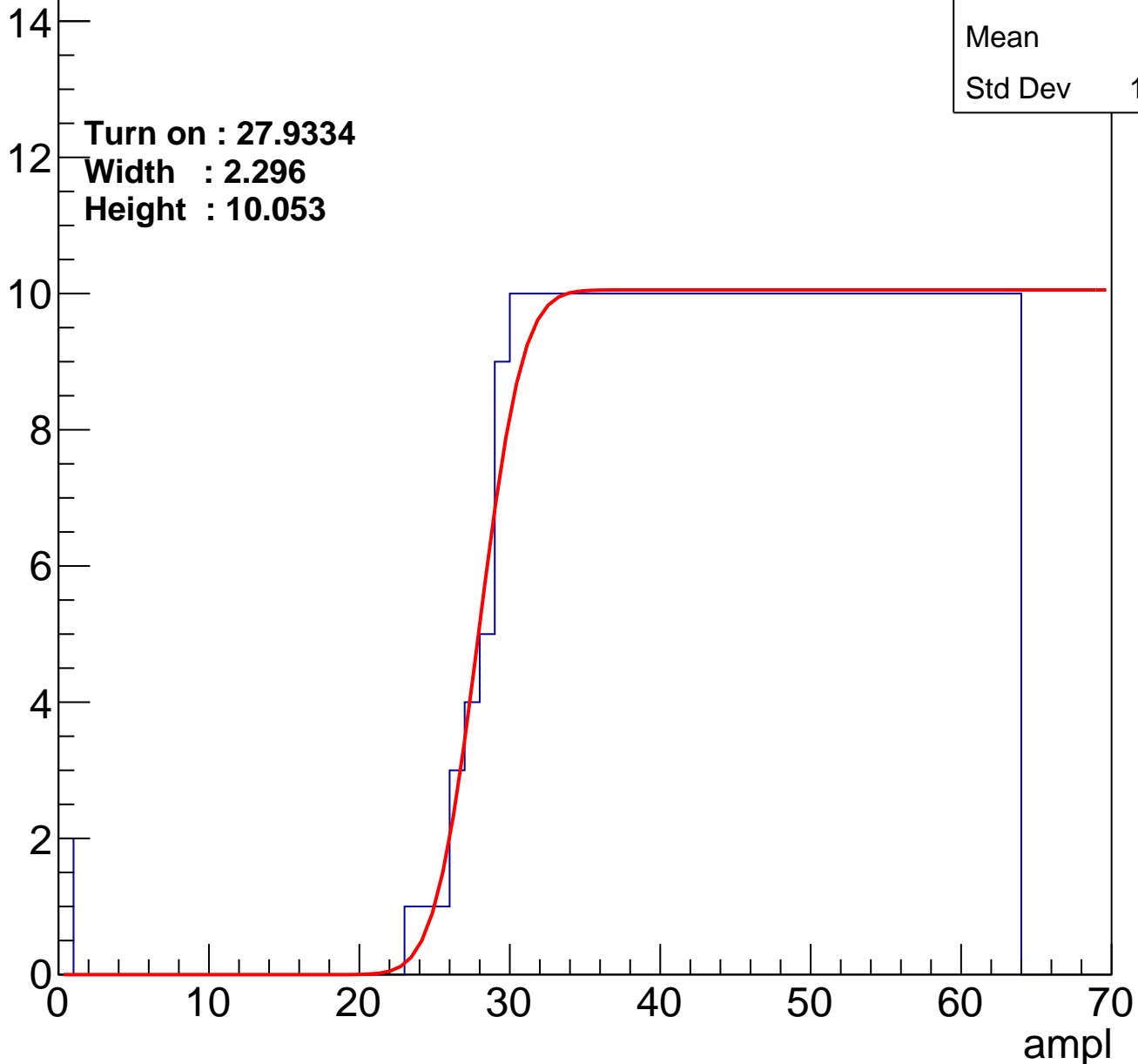
Entries	366
Mean	45
Std Dev	11.09

Turn on : 27.9334

Width : 2.296

Height : 10.053

Entry



B1L100S, U21-ch50

calib_packv5_042523_0143.root, FC#4, port A2

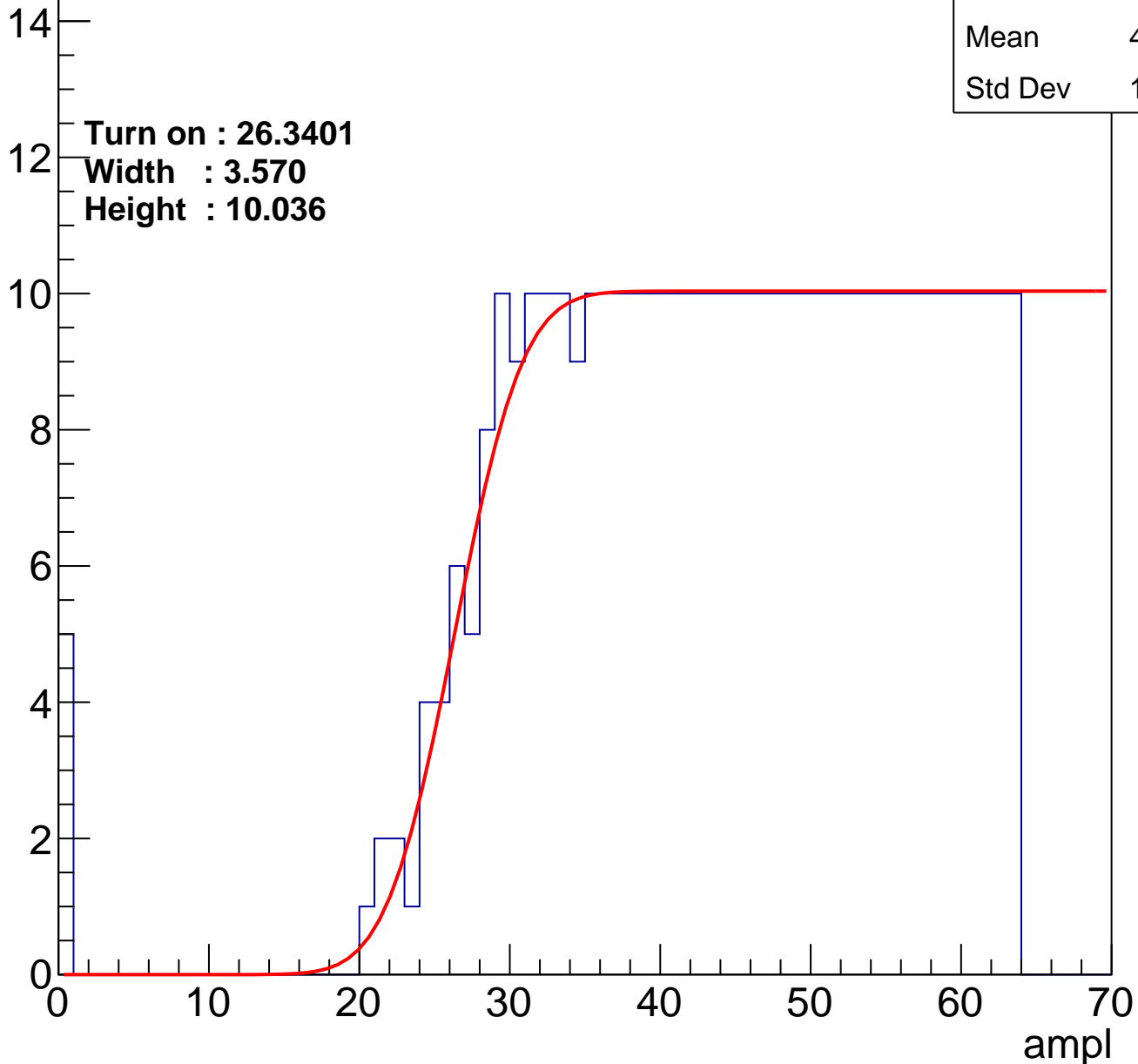
Entries	386
Mean	43.72
Std Dev	12.26

Turn on : 26.3401

Width : 3.570

Height : 10.036

Entry



B1L100S, U21-ch51

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.86
Std Dev	11.73

Turn on : 24.8366

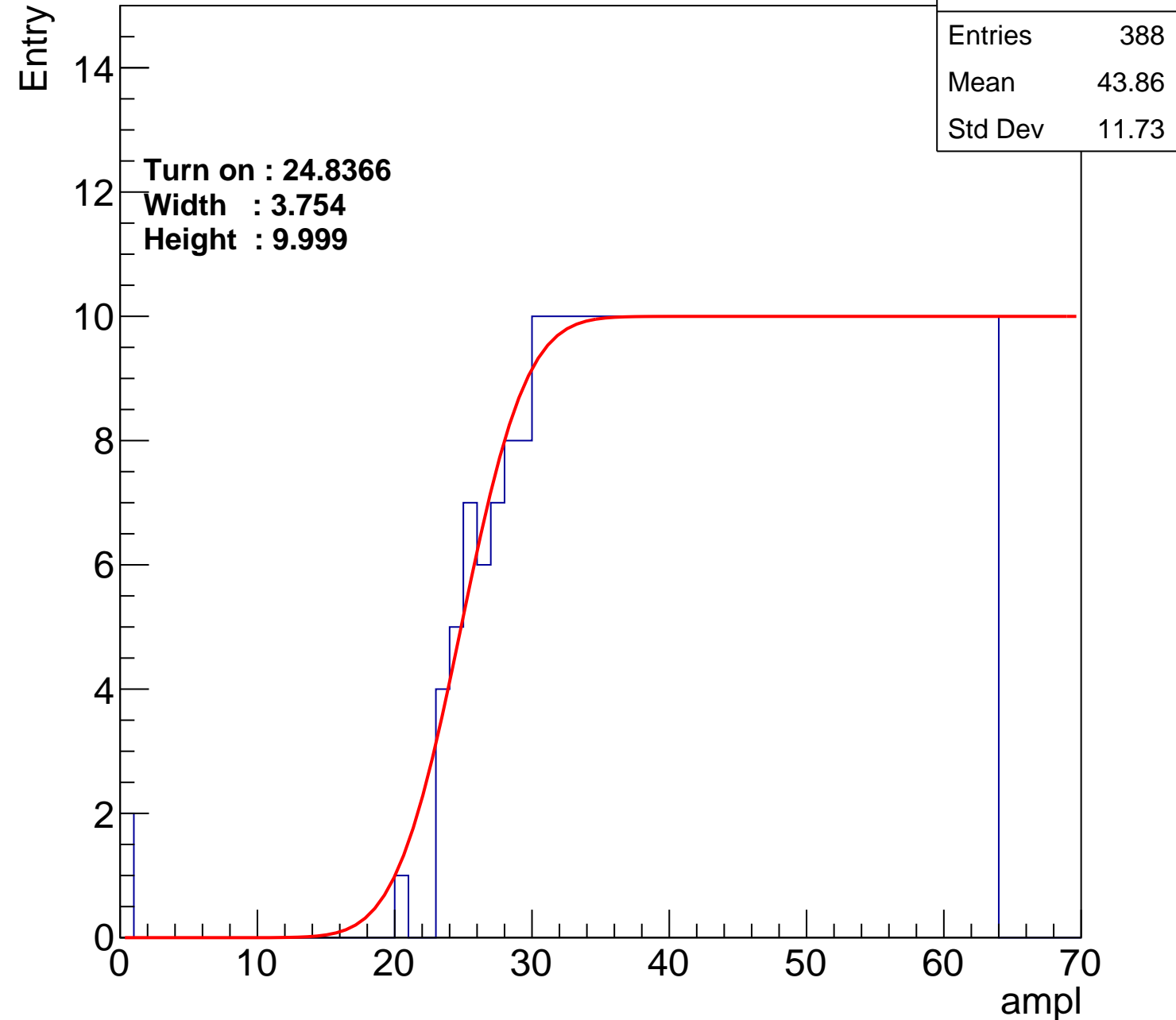
Width : 3.754

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch52

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.46
Std Dev	11.29

Turn on : 26.6152

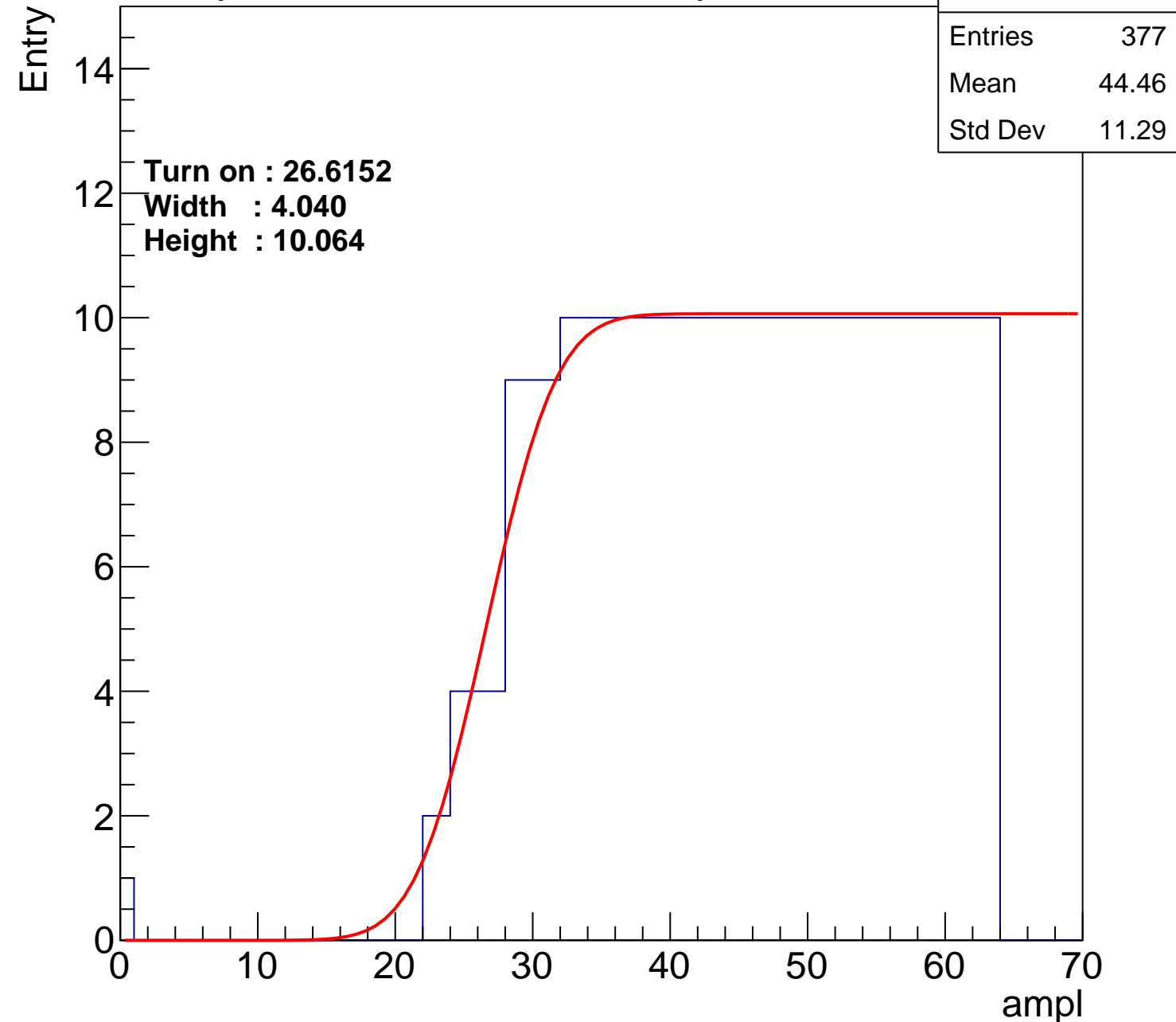
Width : 4.040

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch53

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.85
Std Dev	11.08

Turn on : 27.6754

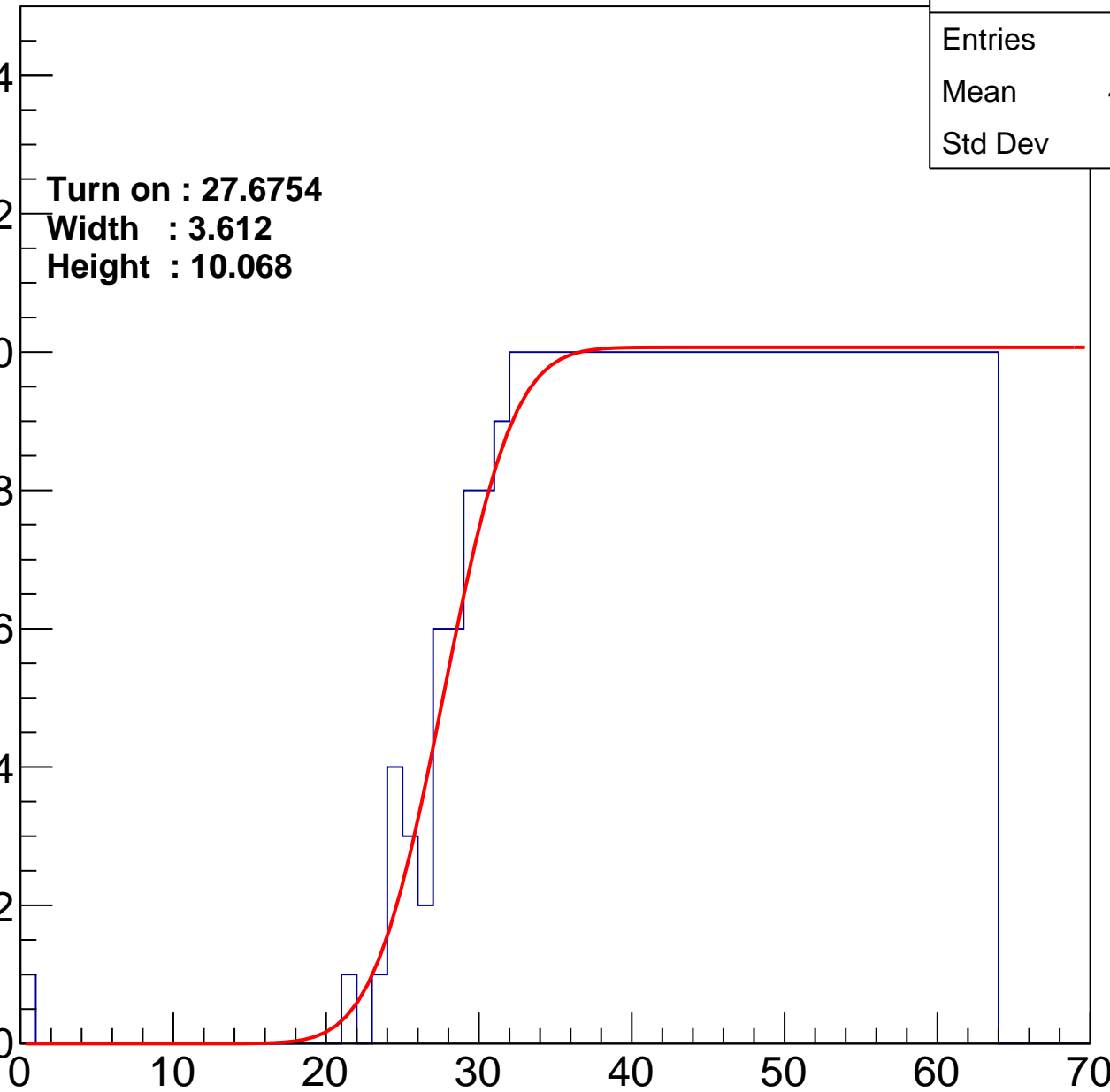
Width : 3.612

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch54

calib_packv5_042523_0143.root, FC#4, port A2

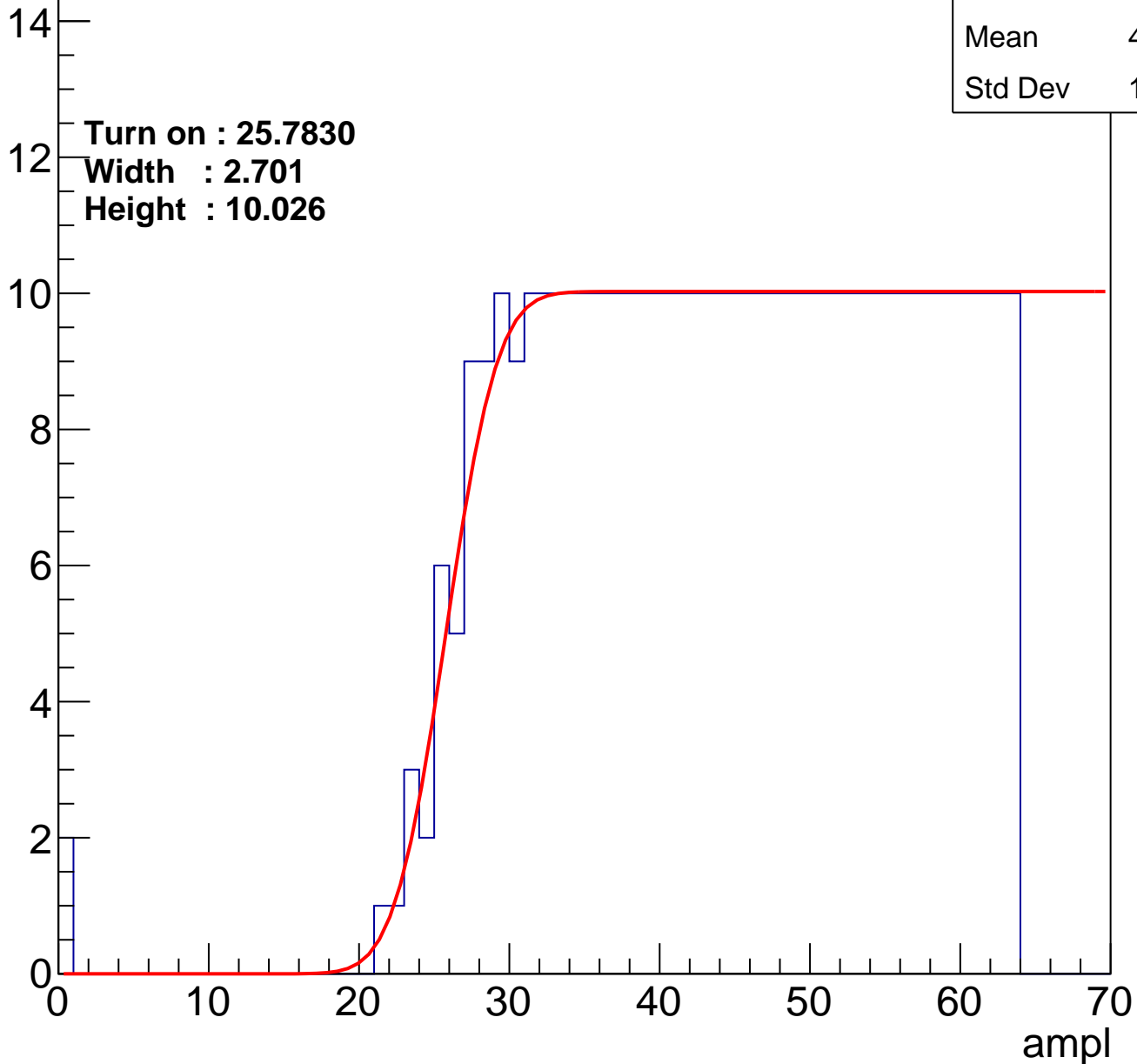
Entries	387
Mean	43.94
Std Dev	11.66

Turn on : 25.7830

Width : 2.701

Height : 10.026

Entry



B1L100S, U21-ch55

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.56
Std Dev	11.83

Turn on : 24.5940

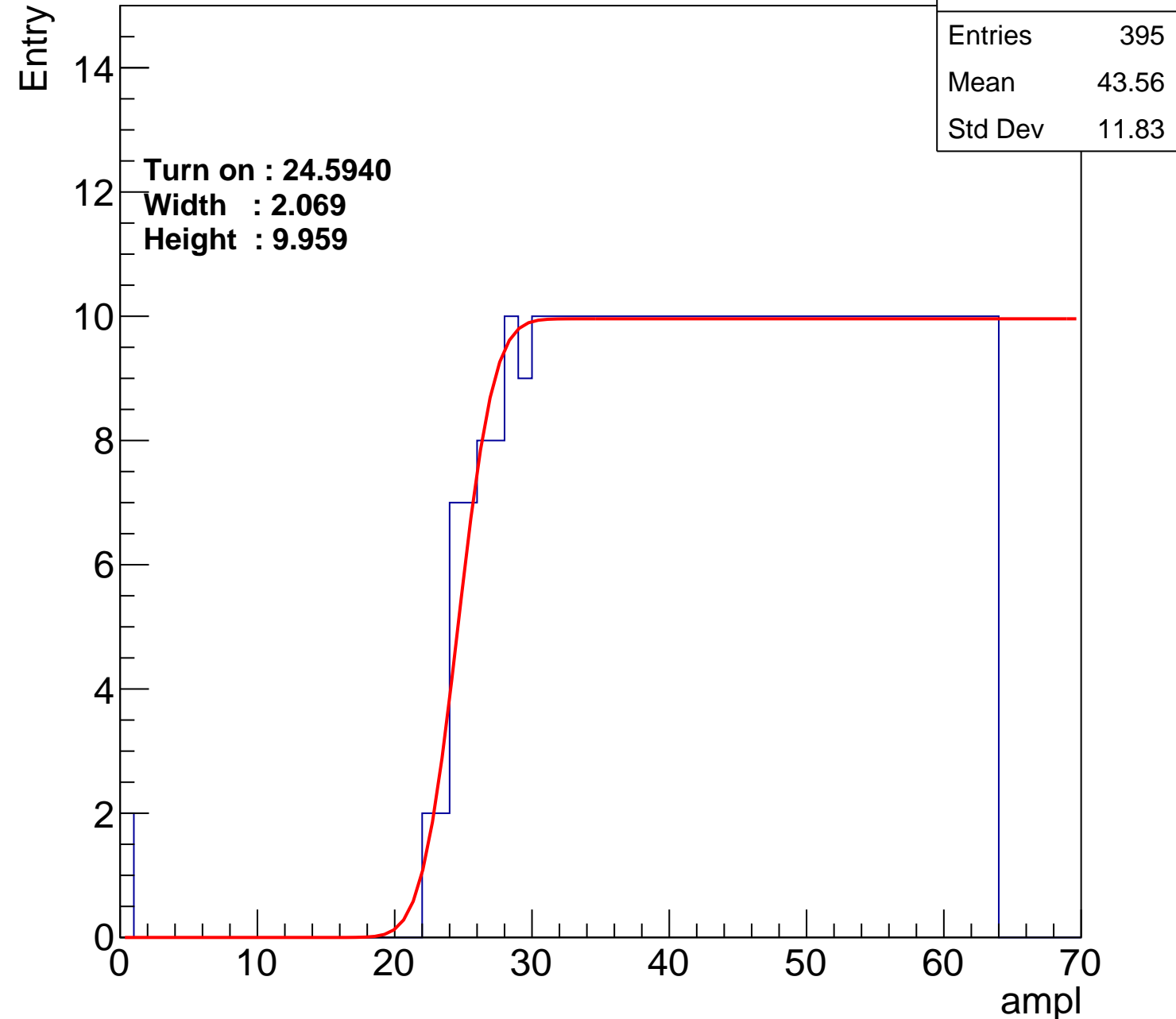
Width : 2.069

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch56

calib_packv5_042523_0143.root, FC#4, port A2

Entries	394
Mean	43.64
Std Dev	11.72

Turn on : 24.9169

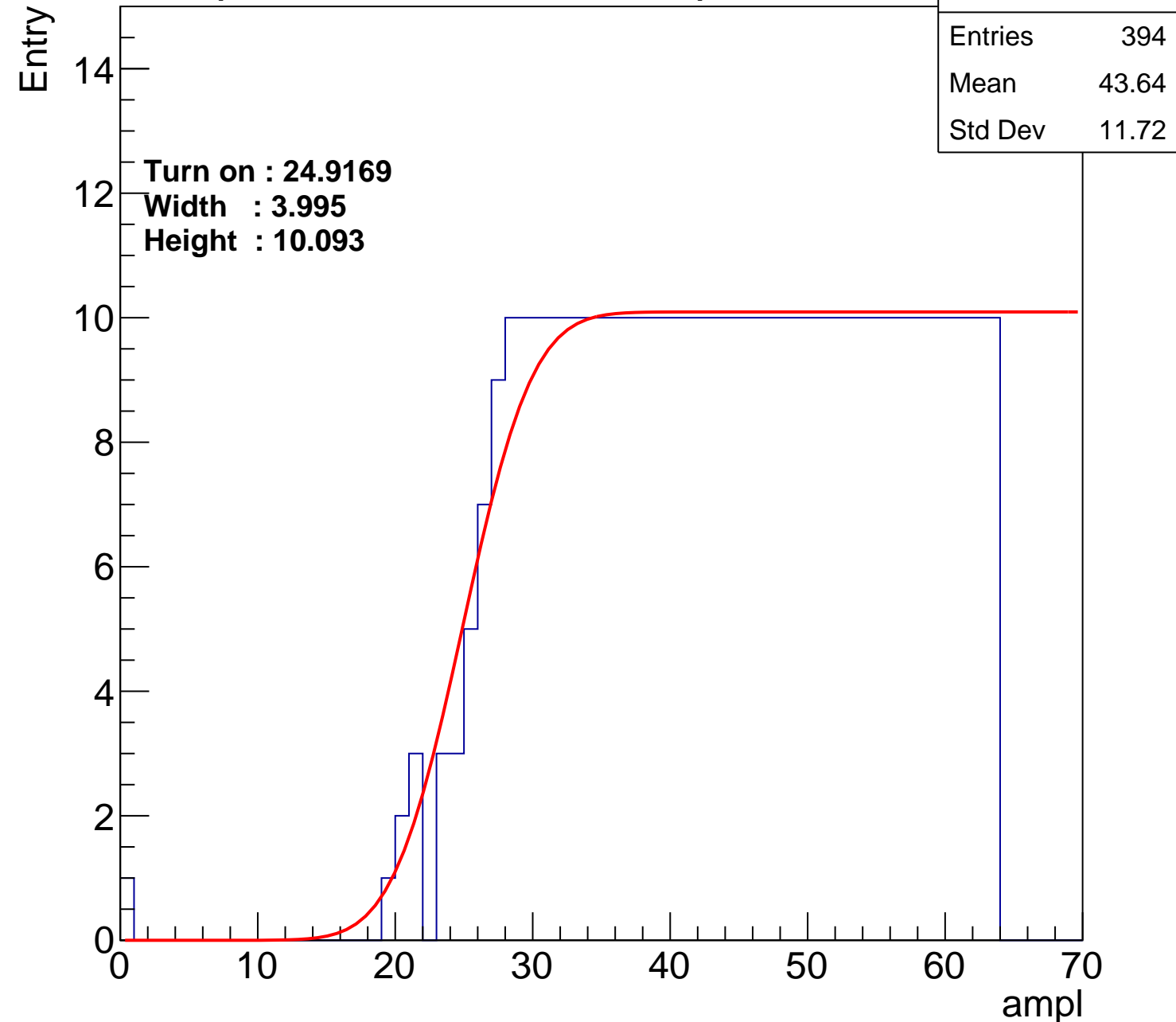
Width : 3.995

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch57

calib_packv5_042523_0143.root, FC#4, port A2

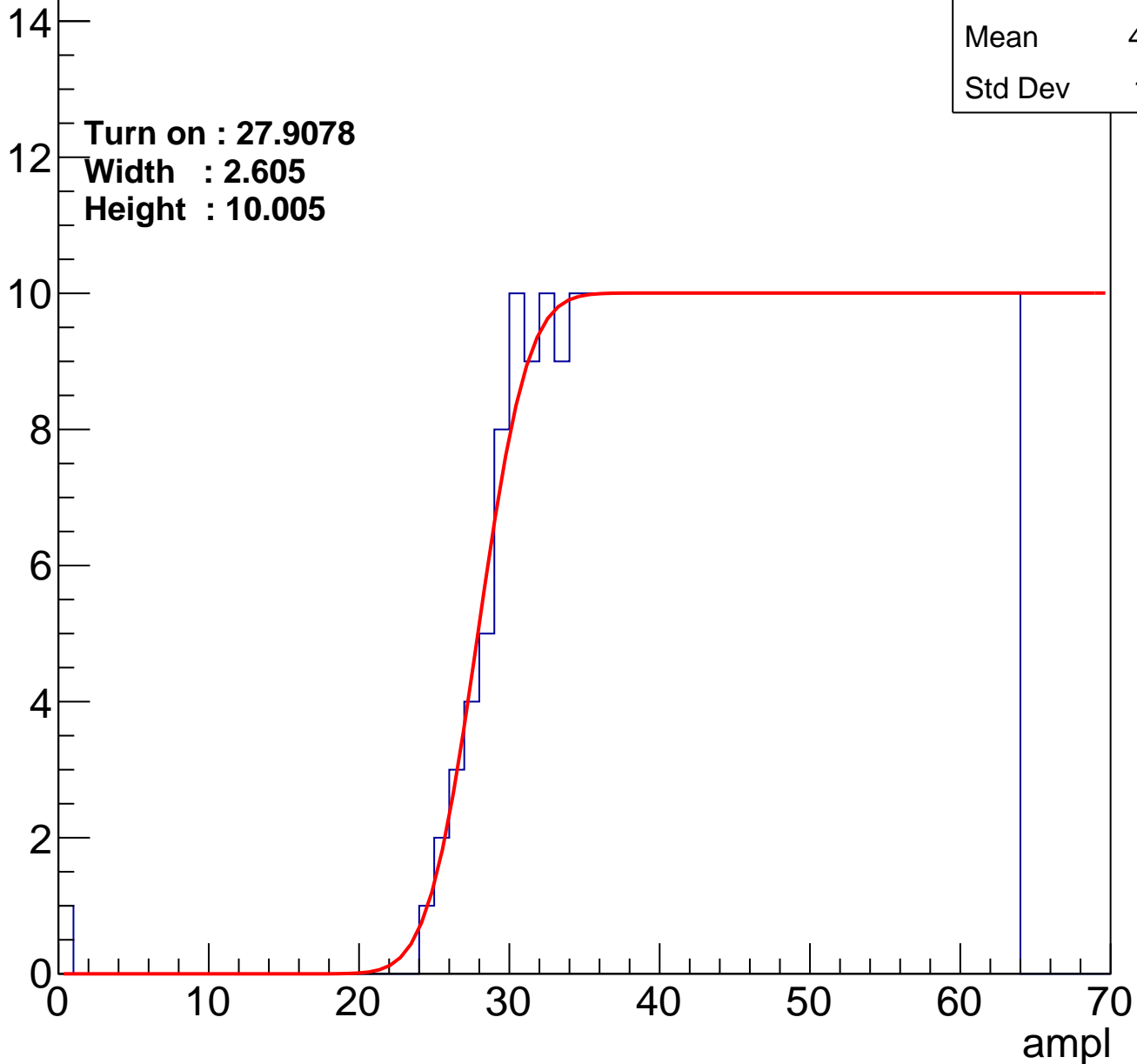
Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 27.9078

Width : 2.605

Height : 10.005

Entry



B1L100S, U21-ch58

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.81
Std Dev	11.09

Turn on : 27.2223

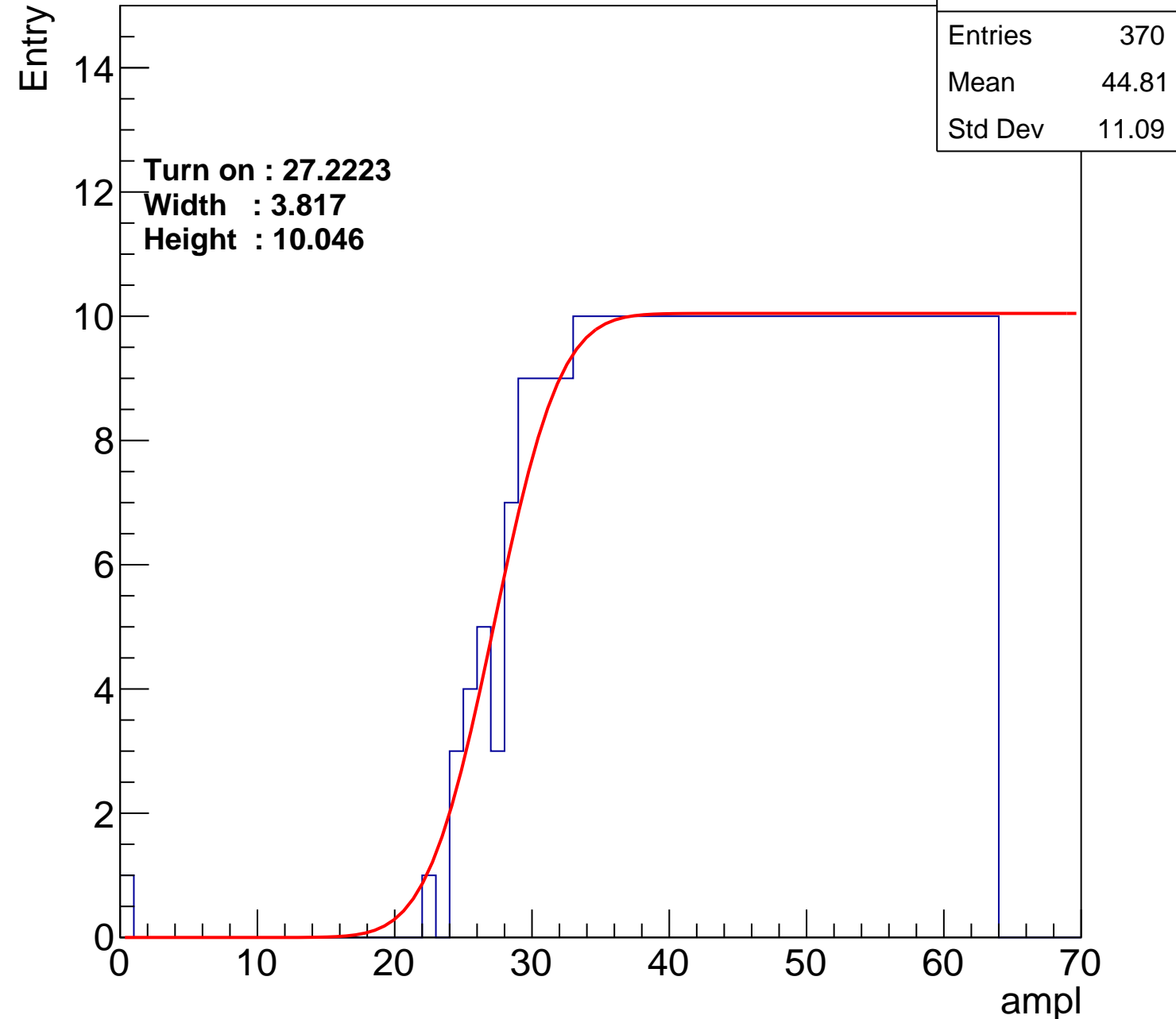
Width : 3.817

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch59

calib_packv5_042523_0143.root, FC#4, port A2

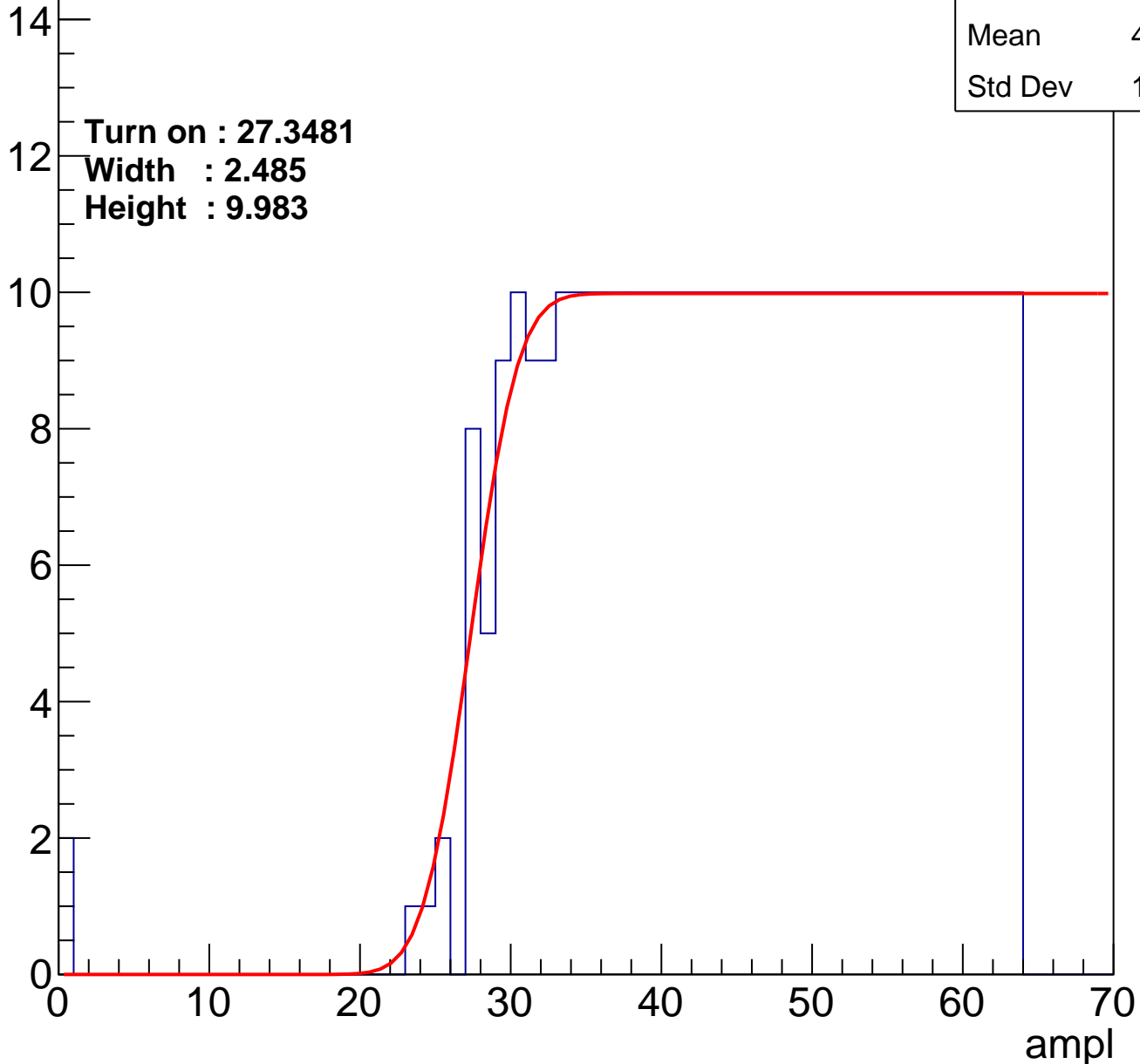
Entries	366
Mean	44.98
Std Dev	11.12

Turn on : 27.3481

Width : 2.485

Height : 9.983

Entry



B1L100S, U21-ch60

calib_packv5_042523_0143.root, FC#4, port A2

Entries	394
Mean	43.46
Std Dev	12.18

Turn on : 25.3729

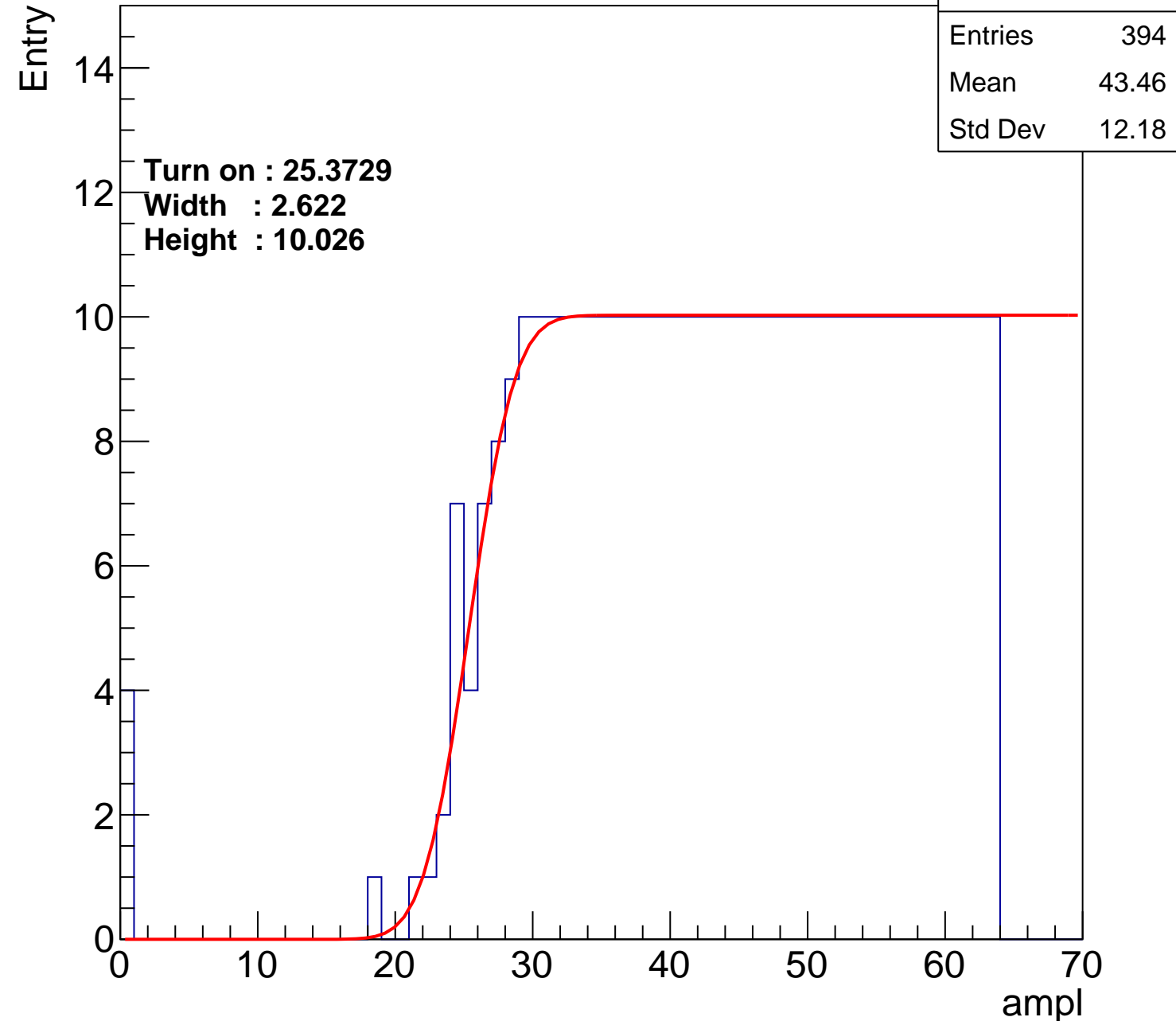
Width : 2.622

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch61

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.49
Std Dev	10.88

Turn on : 28.7676

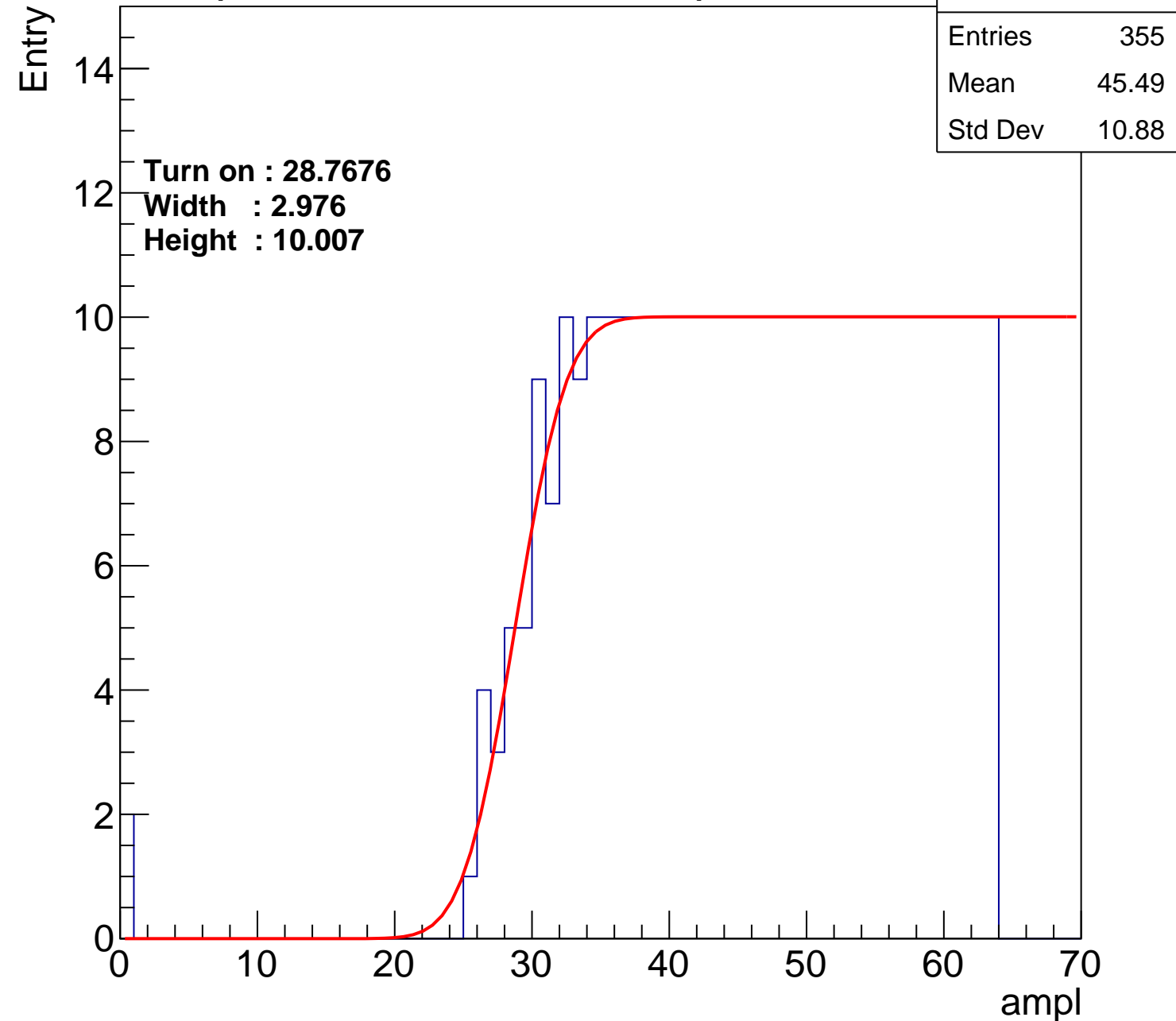
Width : 2.976

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch62

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 26.2052

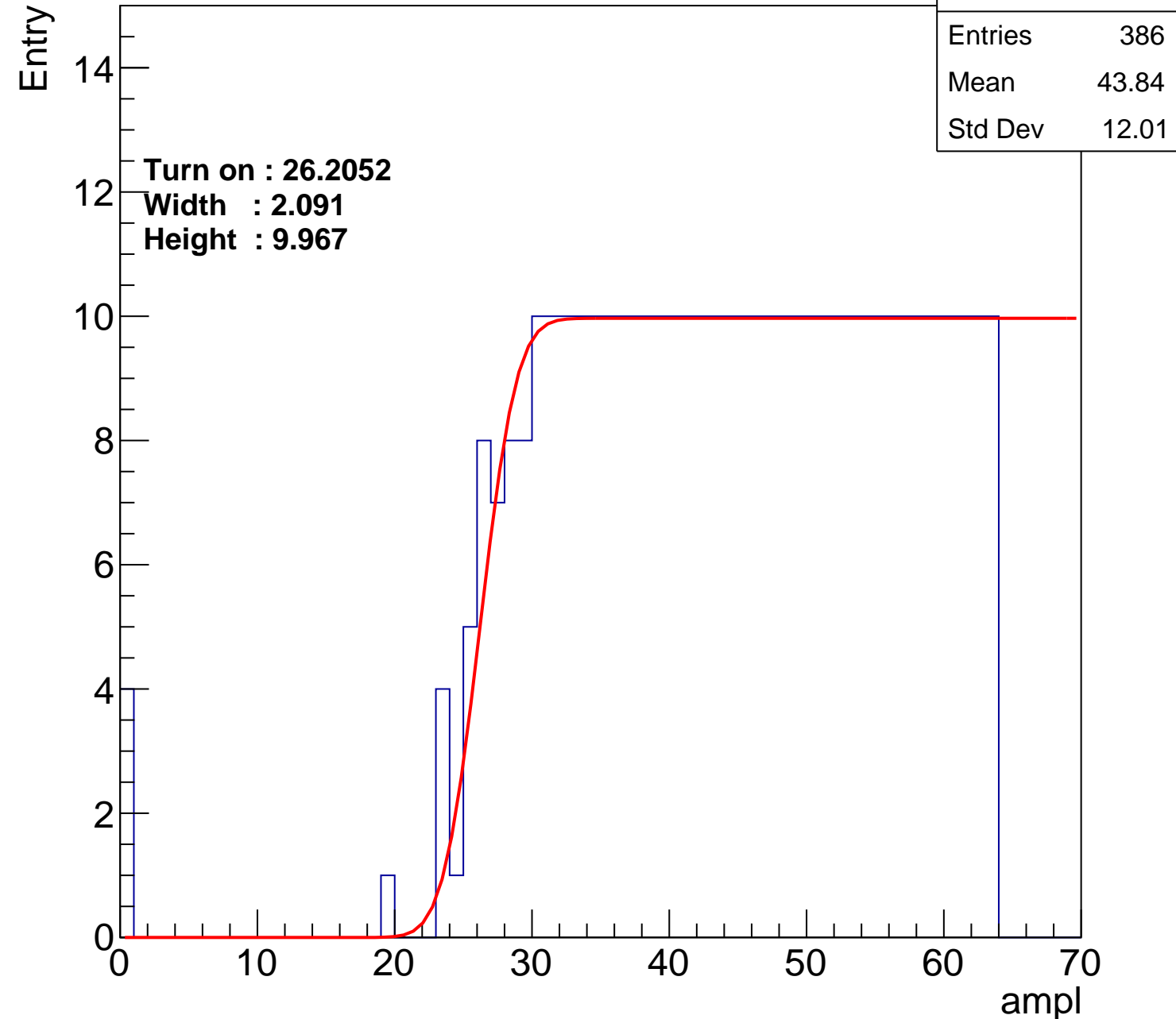
Width : 2.091

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch63

calib_packv5_042523_0143.root, FC#4, port A2

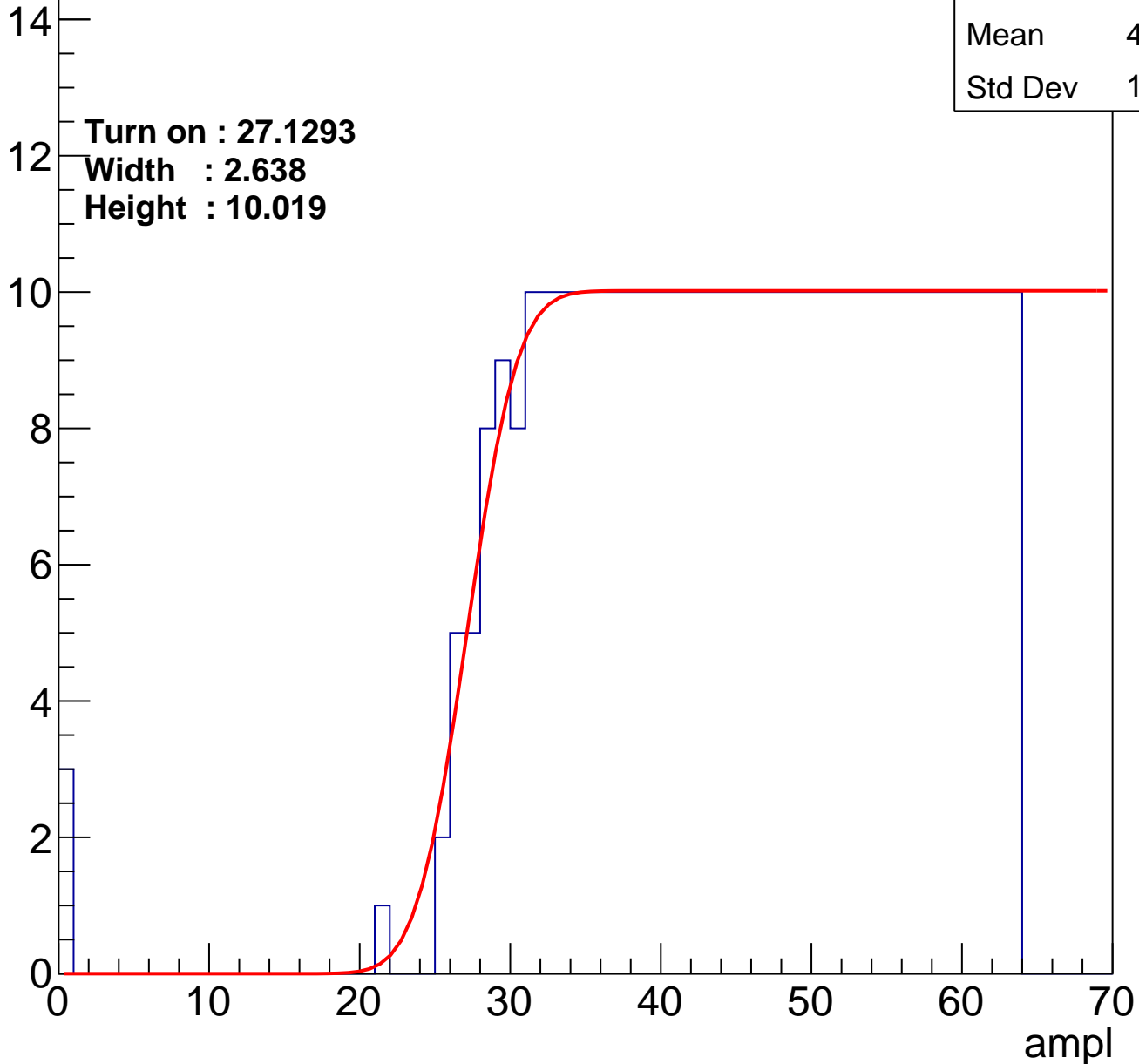
Entries	371
Mean	44.67
Std Dev	11.43

Turn on : 27.1293

Width : 2.638

Height : 10.019

Entry



B1L100S, U21-ch64

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.46
Std Dev	11.95

Turn on : 24.6438

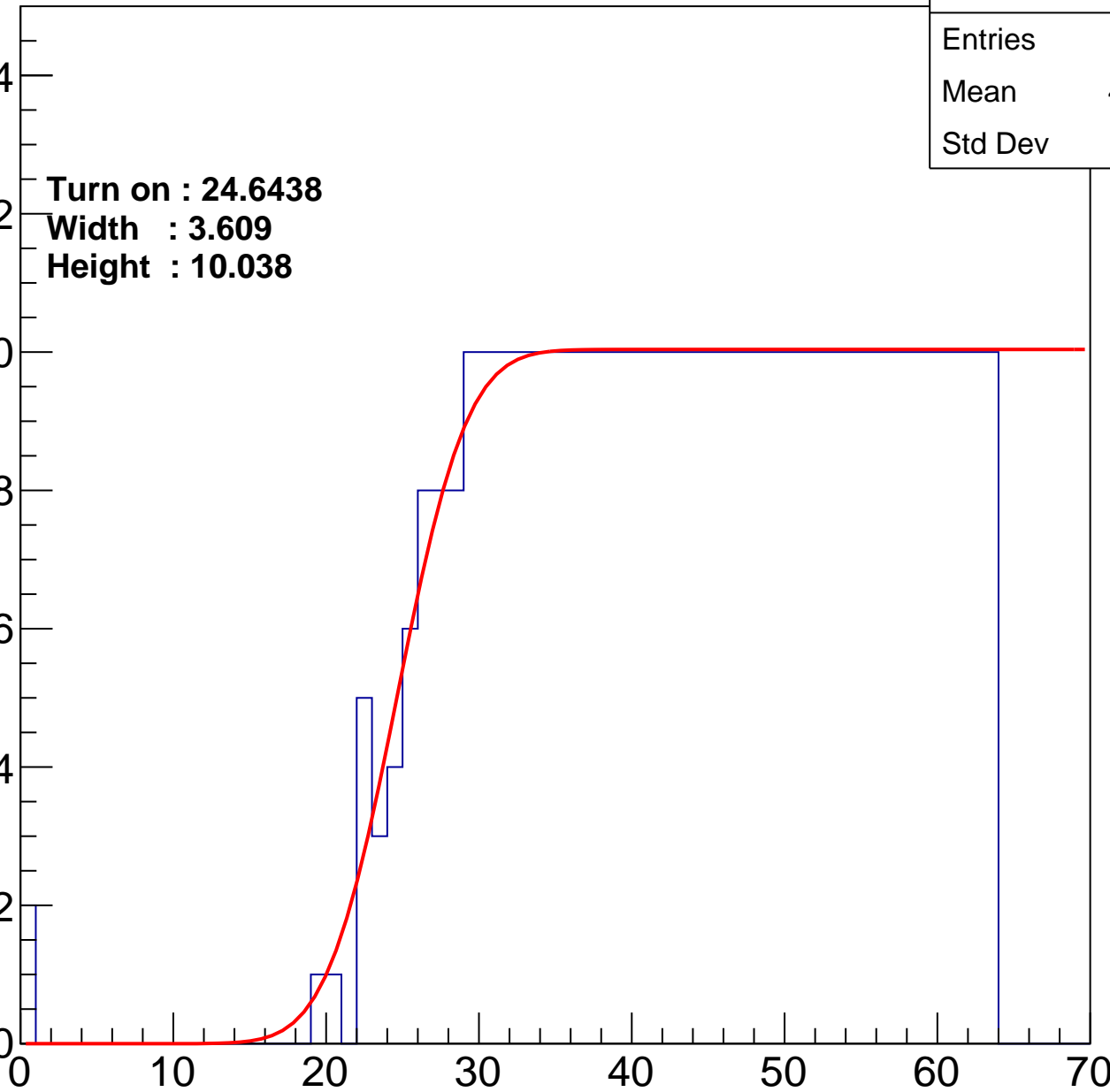
Width : 3.609

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch65

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.8
Std Dev	11.77

Turn on : 25.4968

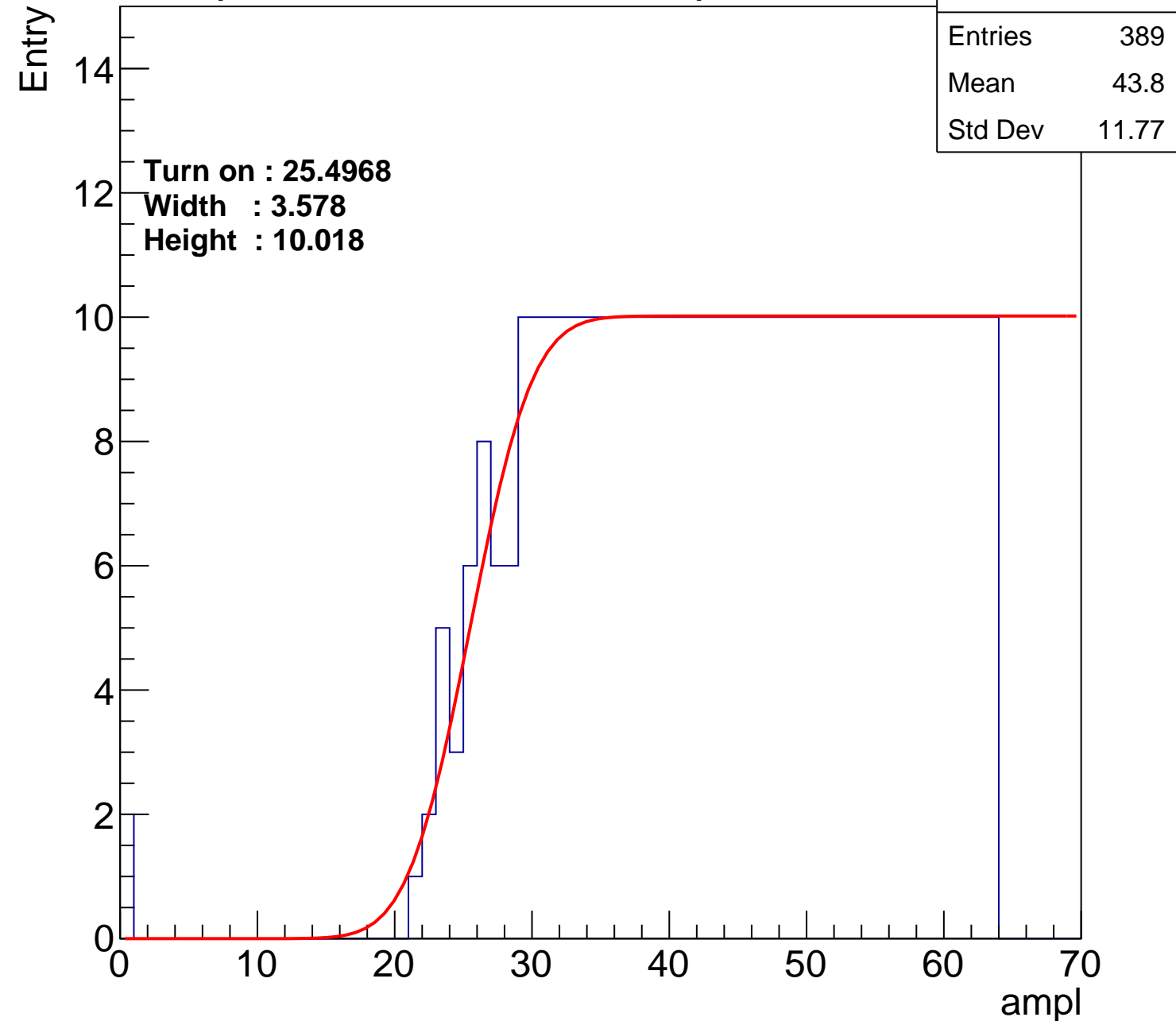
Width : 3.578

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch66

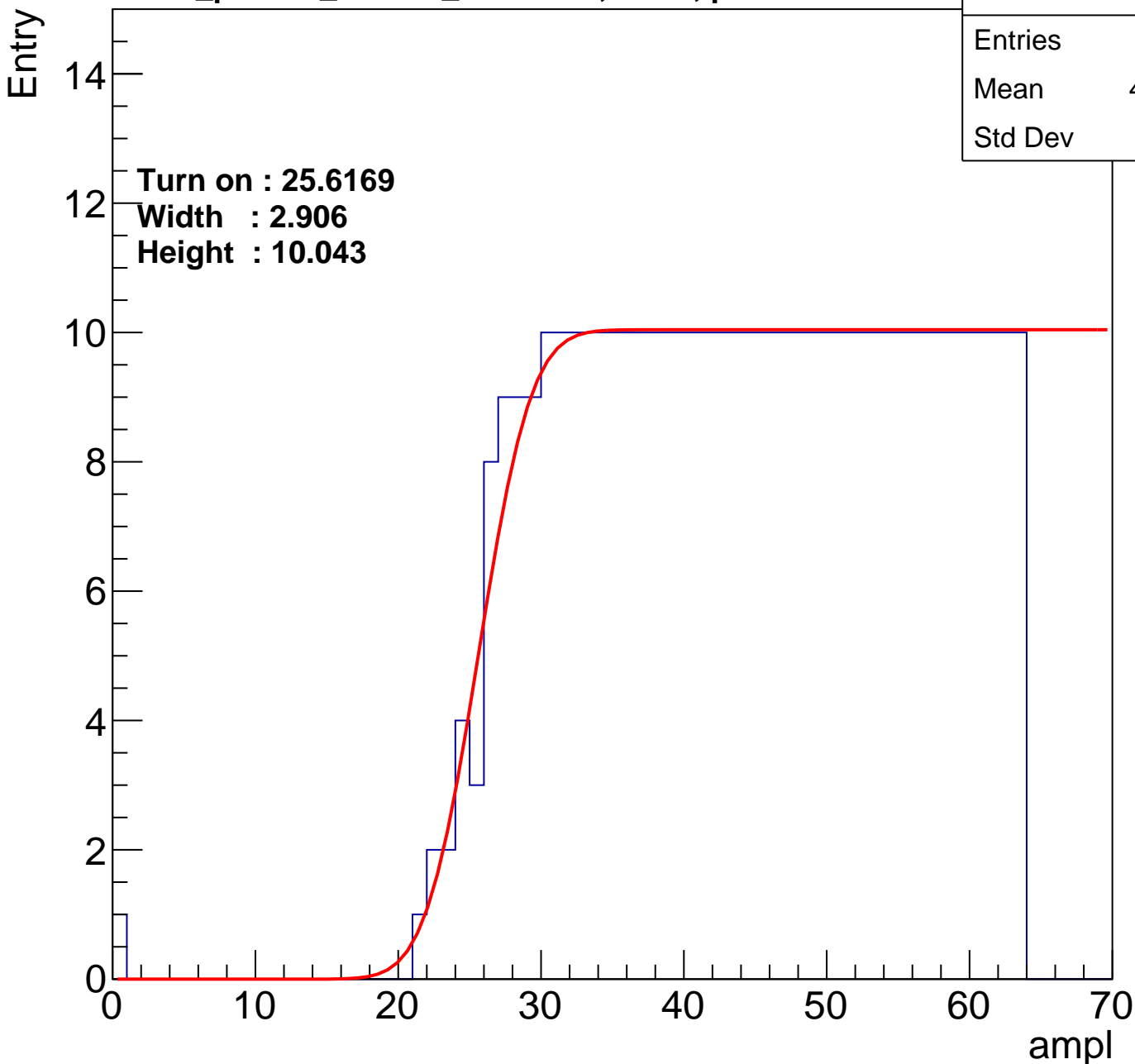
calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 25.6169

Width : 2.906

Height : 10.043



B1L100S, U21-ch67

calib_packv5_042523_0143.root, FC#4, port A2

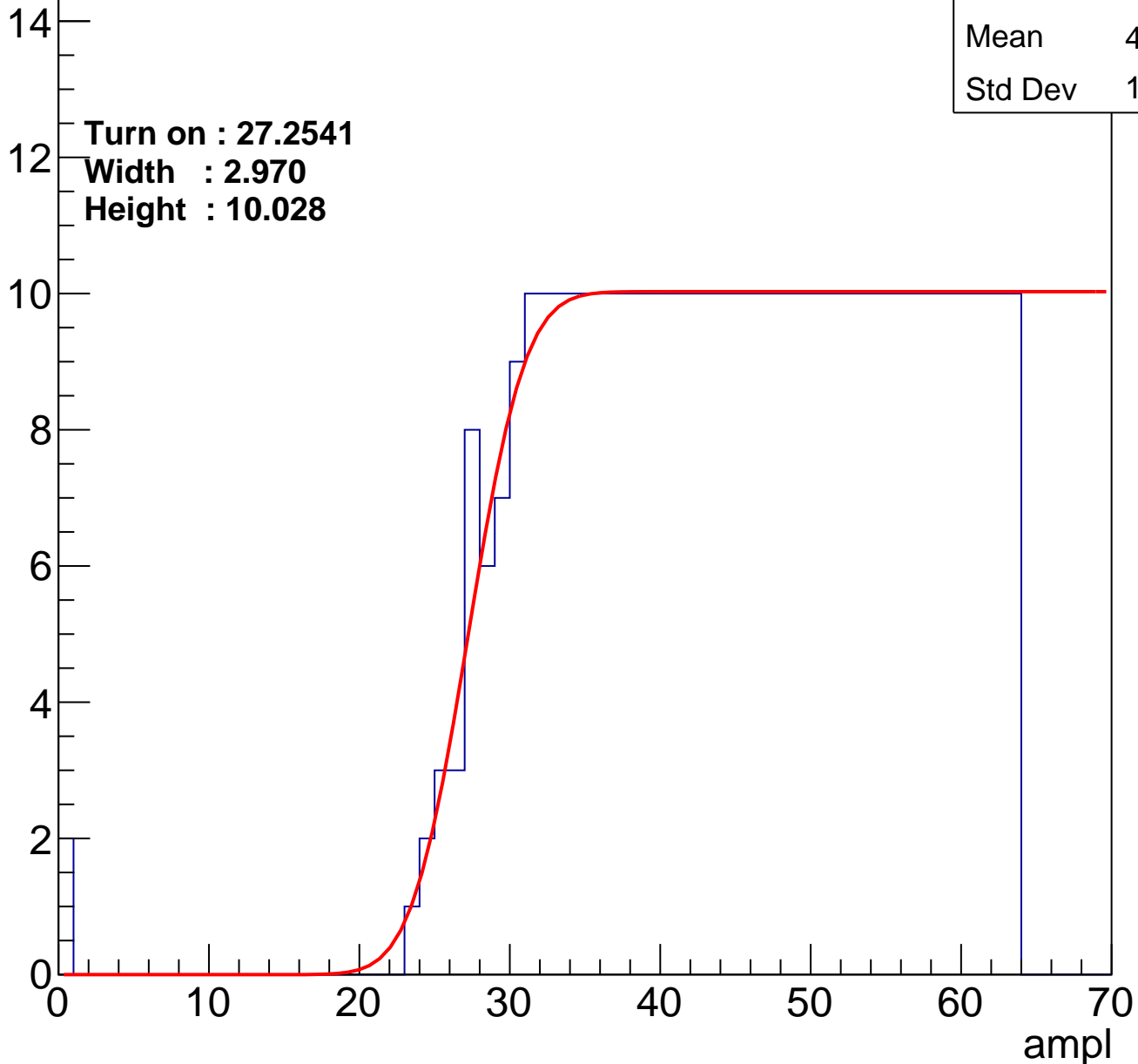
Entries	371
Mean	44.72
Std Dev	11.26

Turn on : 27.2541

Width : 2.970

Height : 10.028

Entry



B1L100S, U21-ch68

calib_packv5_042523_0143.root, FC#4, port A2

Entries	406
Mean	43.04
Std Dev	12.04

Turn on : 23.9222

Width : 3.078

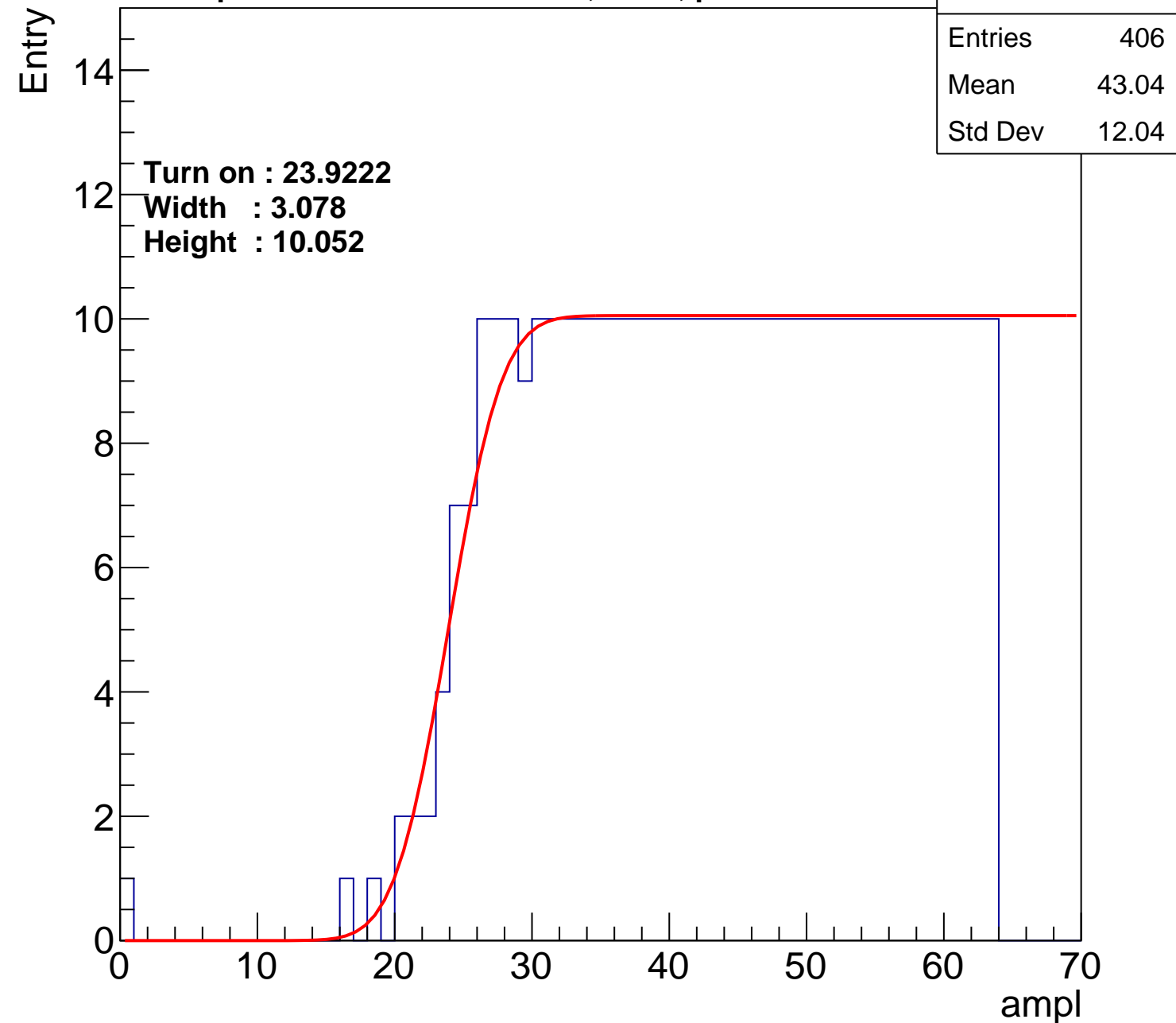
Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U21-ch69

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.12
Std Dev	11.9

Turn on : 27.1358

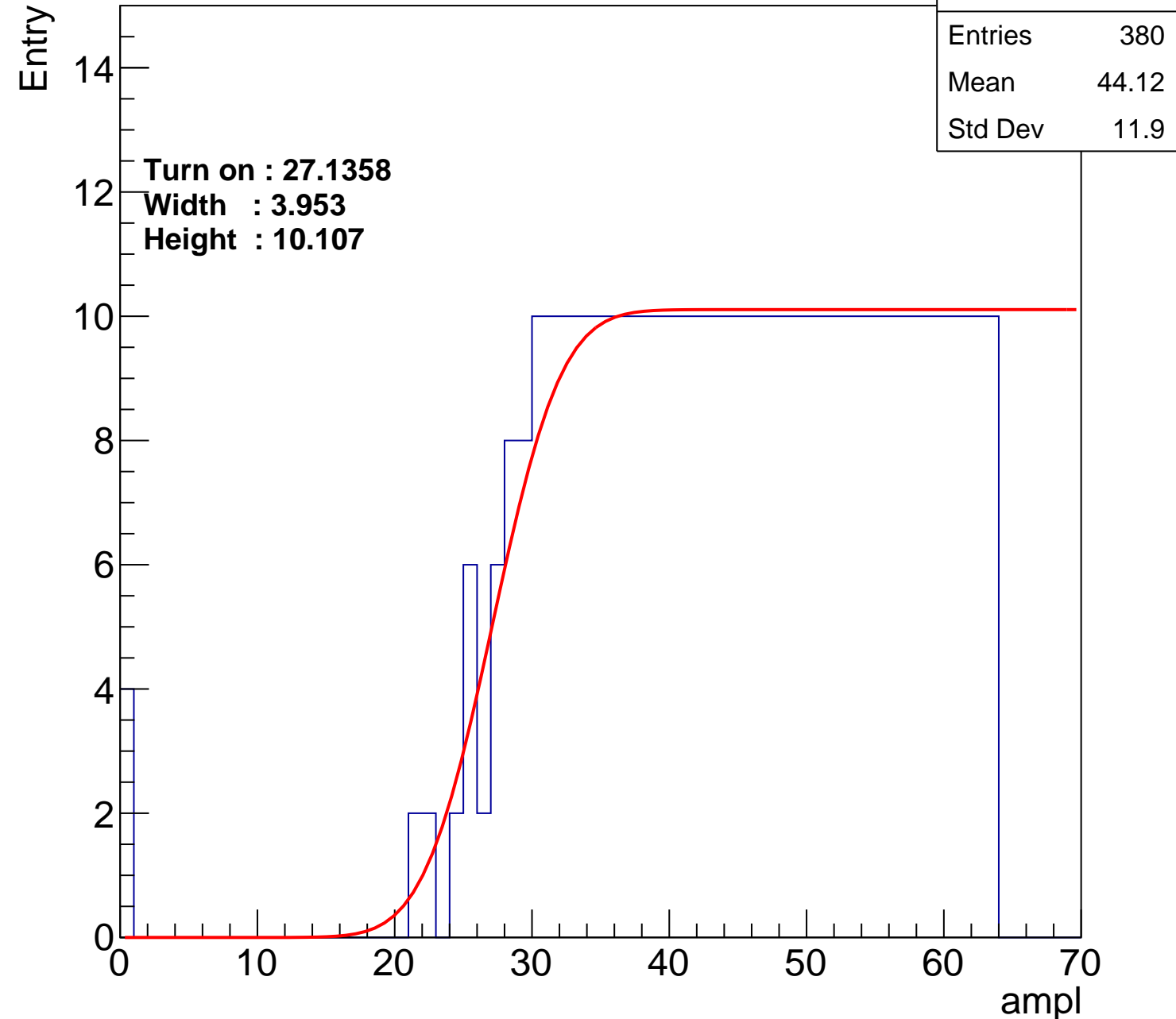
Width : 3.953

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch70

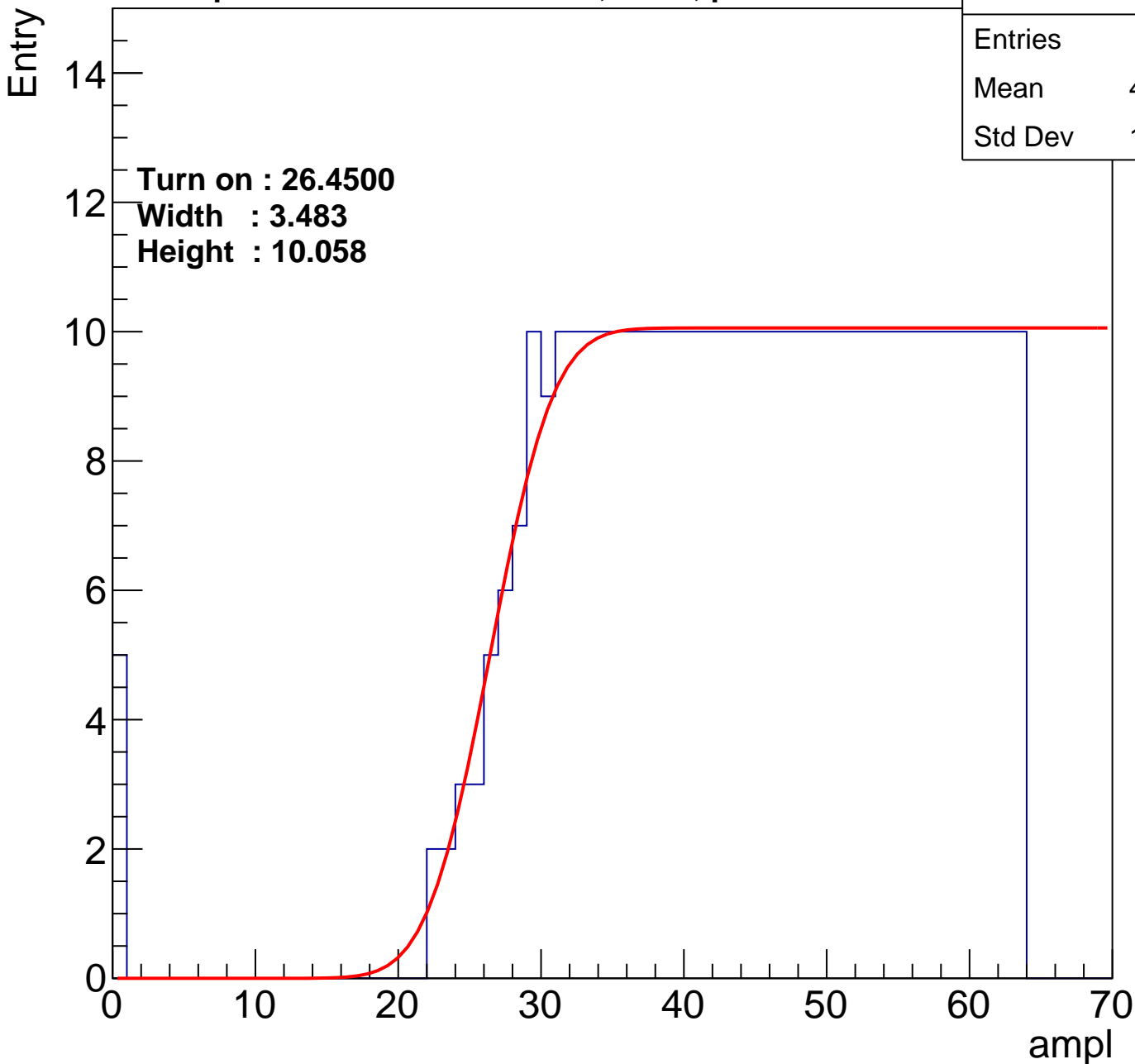
calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	43.97
Std Dev	12.09

Turn on : 26.4500

Width : 3.483

Height : 10.058



B1L100S, U21-ch71

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 25.9850

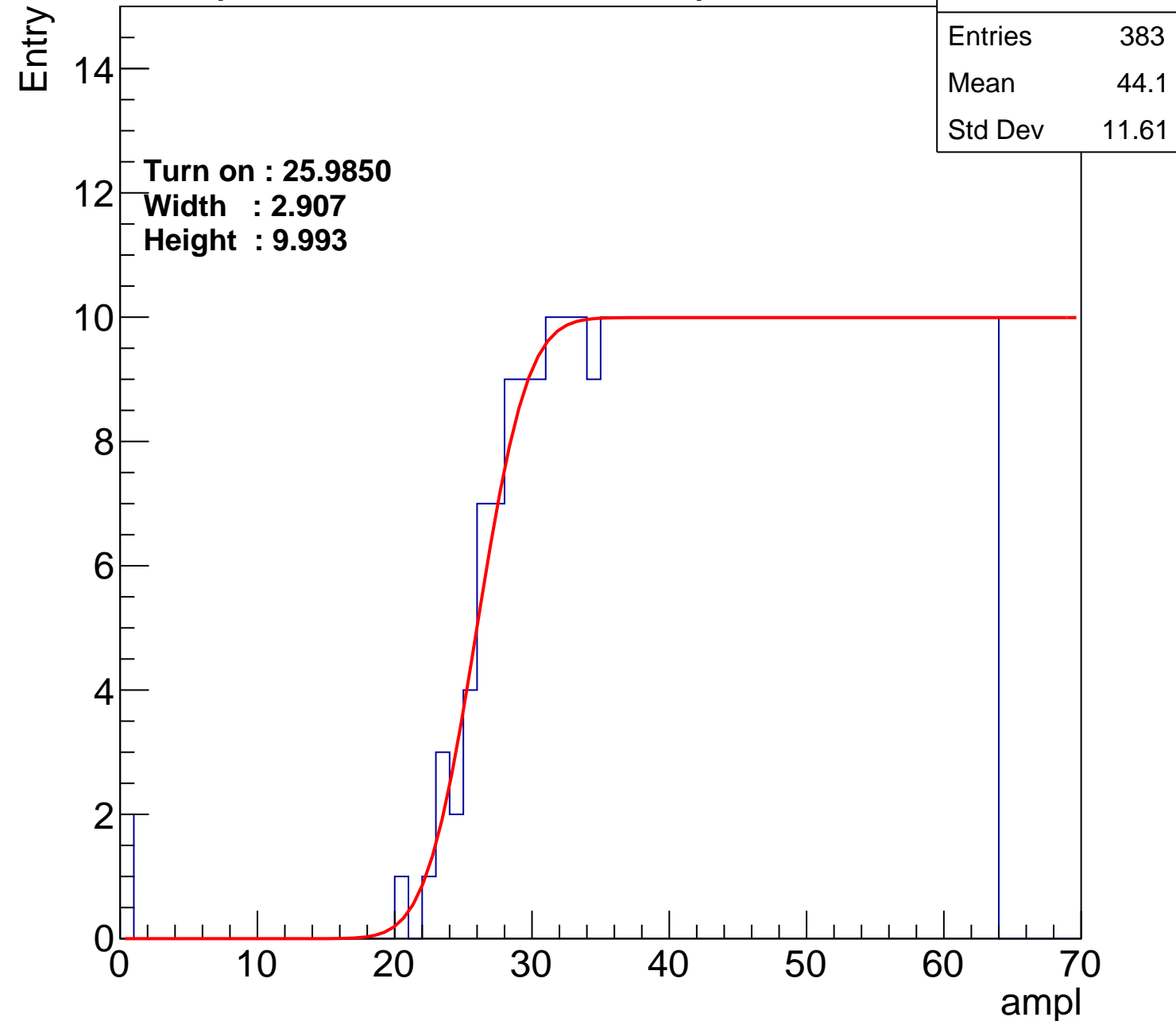
Width : 2.907

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 26.8118
Width : 2.961
Height : 10.010



B1L100S, U21-ch73

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	44.07
Std Dev	11.48

Turn on : 26.1026

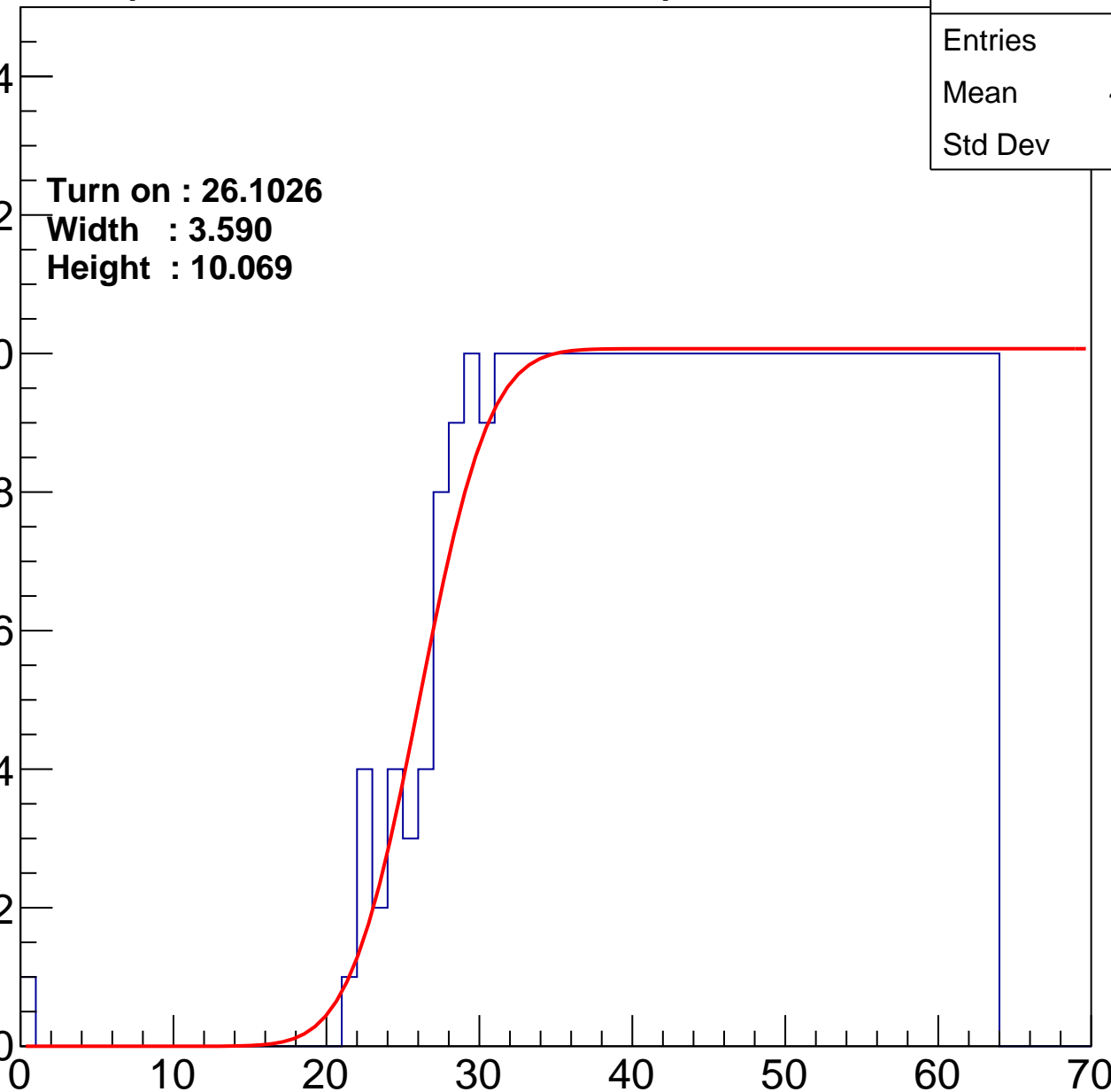
Width : 3.590

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch74

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	44.06
Std Dev	11.74

Turn on : 26.4204

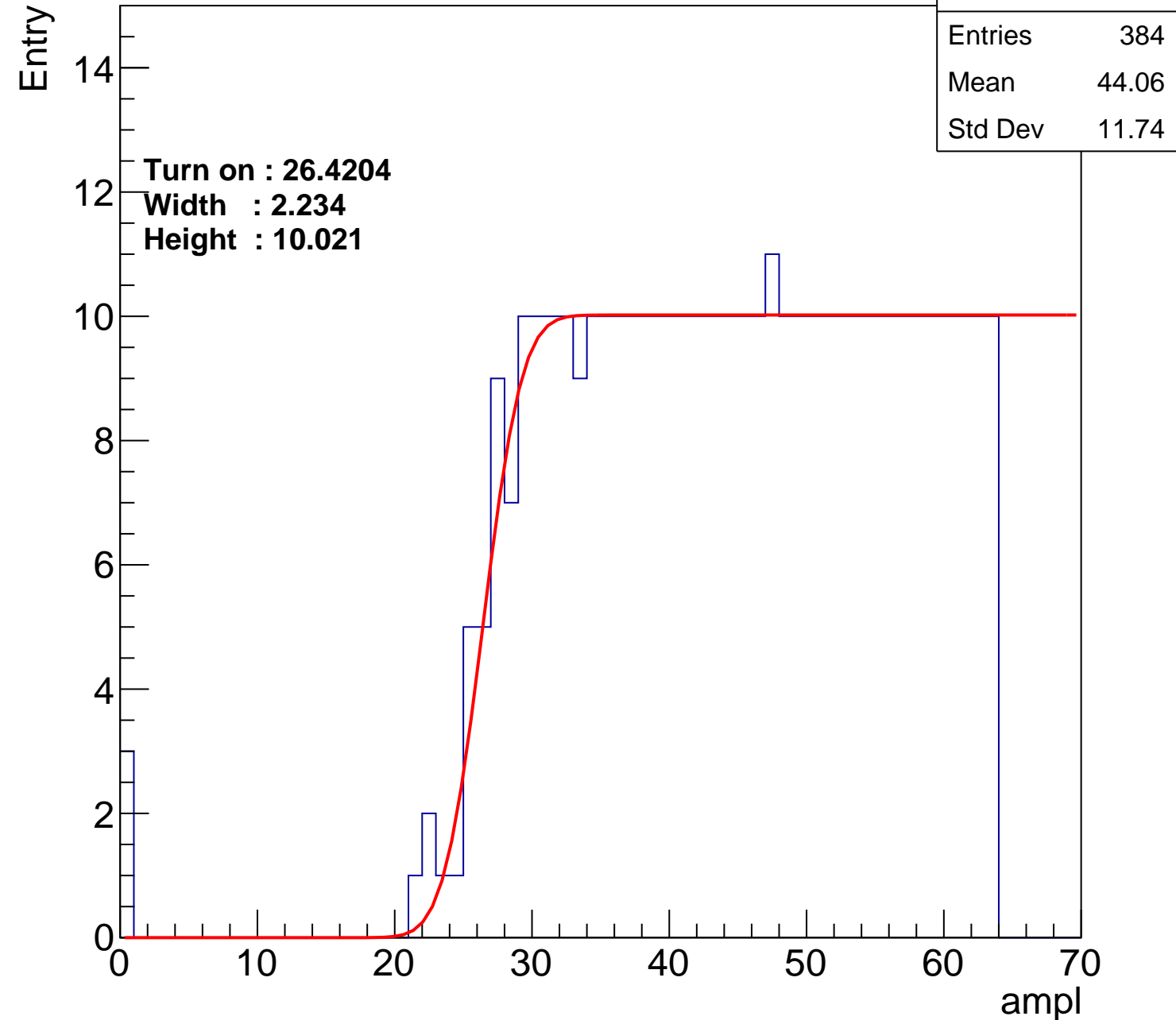
Width : 2.234

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch75

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.6
Std Dev	11.41

Turn on : 27.5469

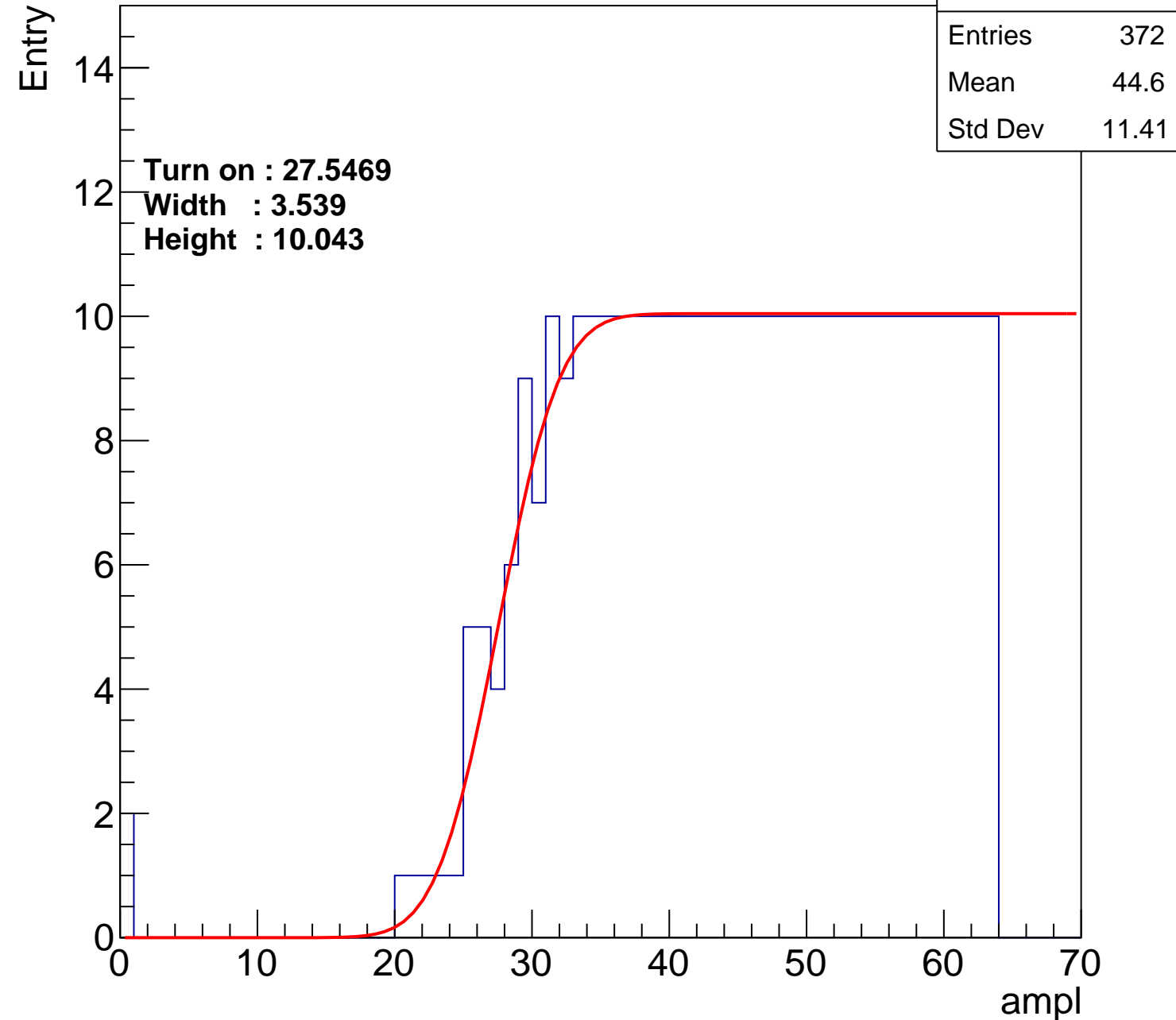
Width : 3.539

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch76

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.69
Std Dev	11.45

Turn on : 27.8567

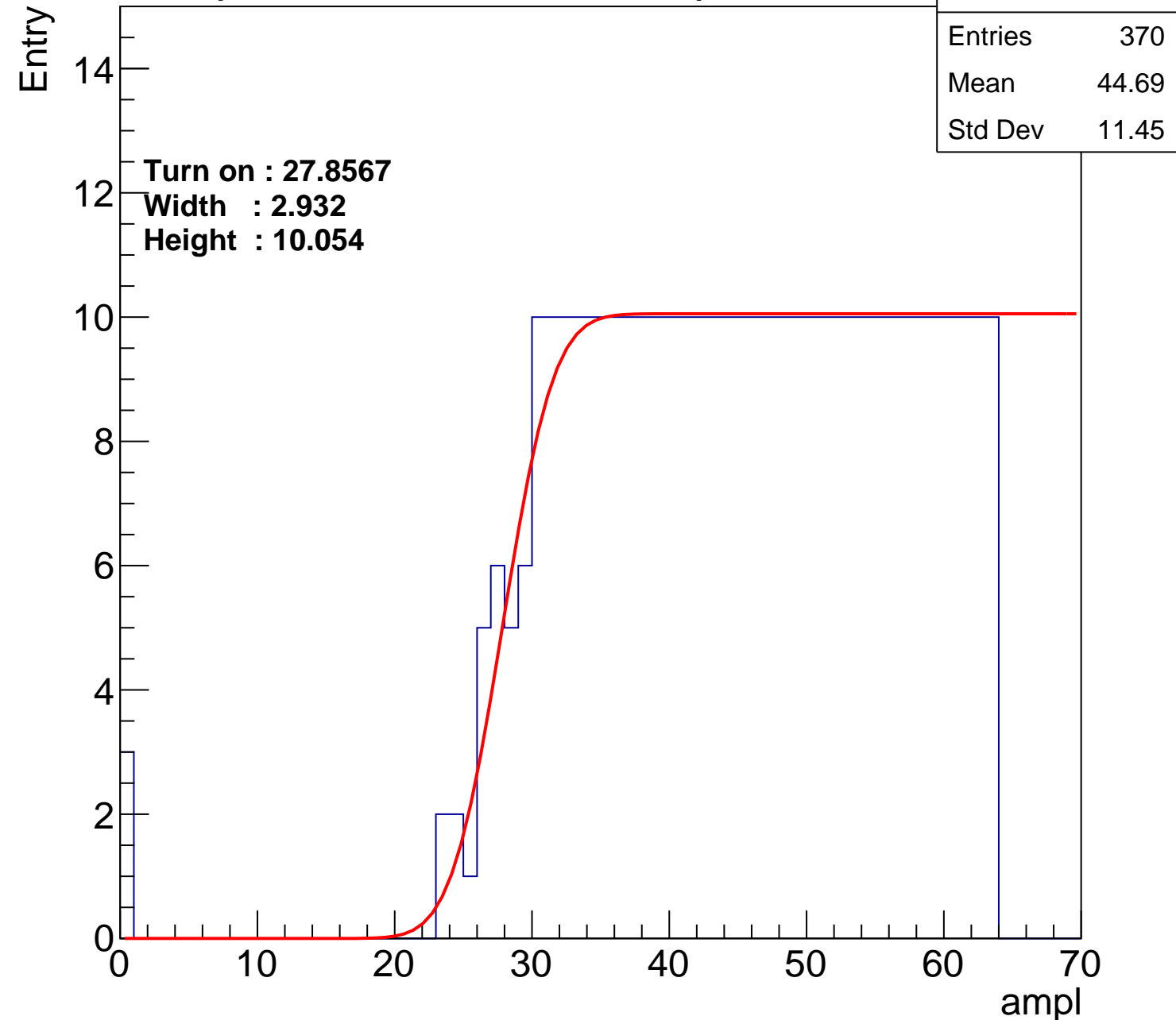
Width : 2.932

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch77

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.16
Std Dev	11.88

Turn on : 26.5081

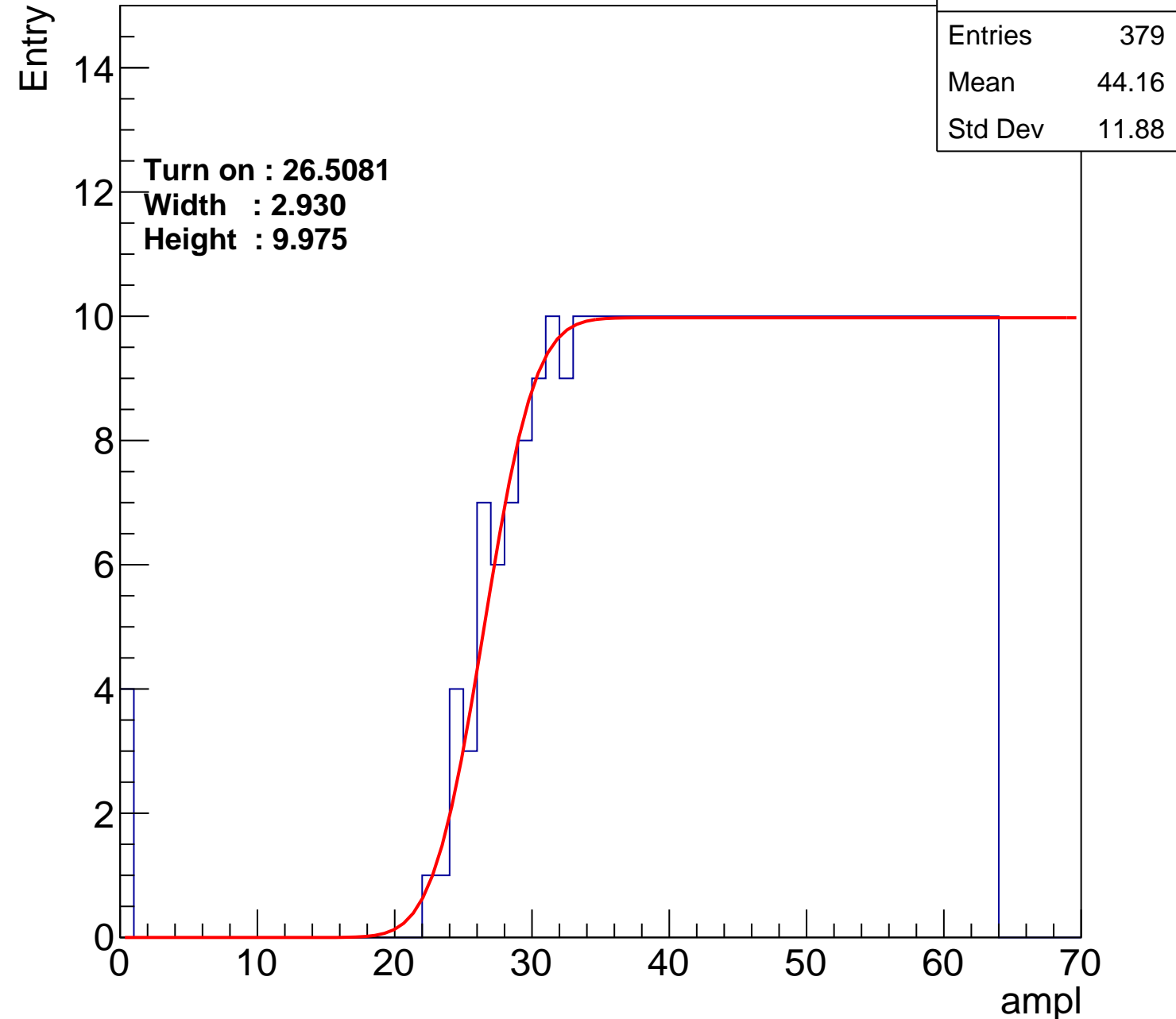
Width : 2.930

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch78

calib_packv5_042523_0143.root, FC#4, port A2

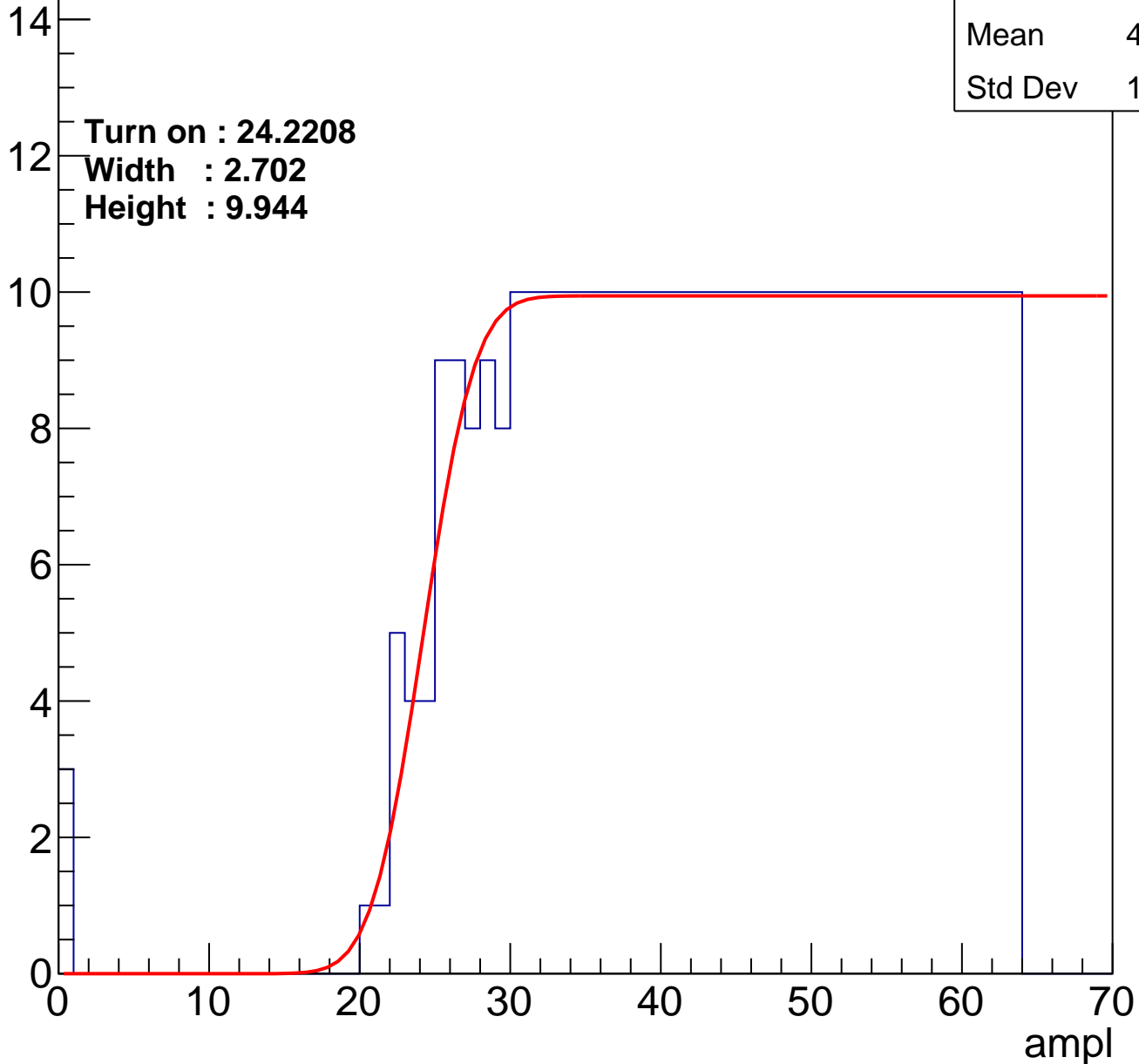
Entries	401
Mean	43.16
Std Dev	12.22

Turn on : 24.2208

Width : 2.702

Height : 9.944

Entry



B1L100S, U21-ch79

calib_packv5_042523_0143.root, FC#4, port A2

Entries	398
Mean	43.34
Std Dev	12.09

Turn on : 24.9501

Width : 1.876

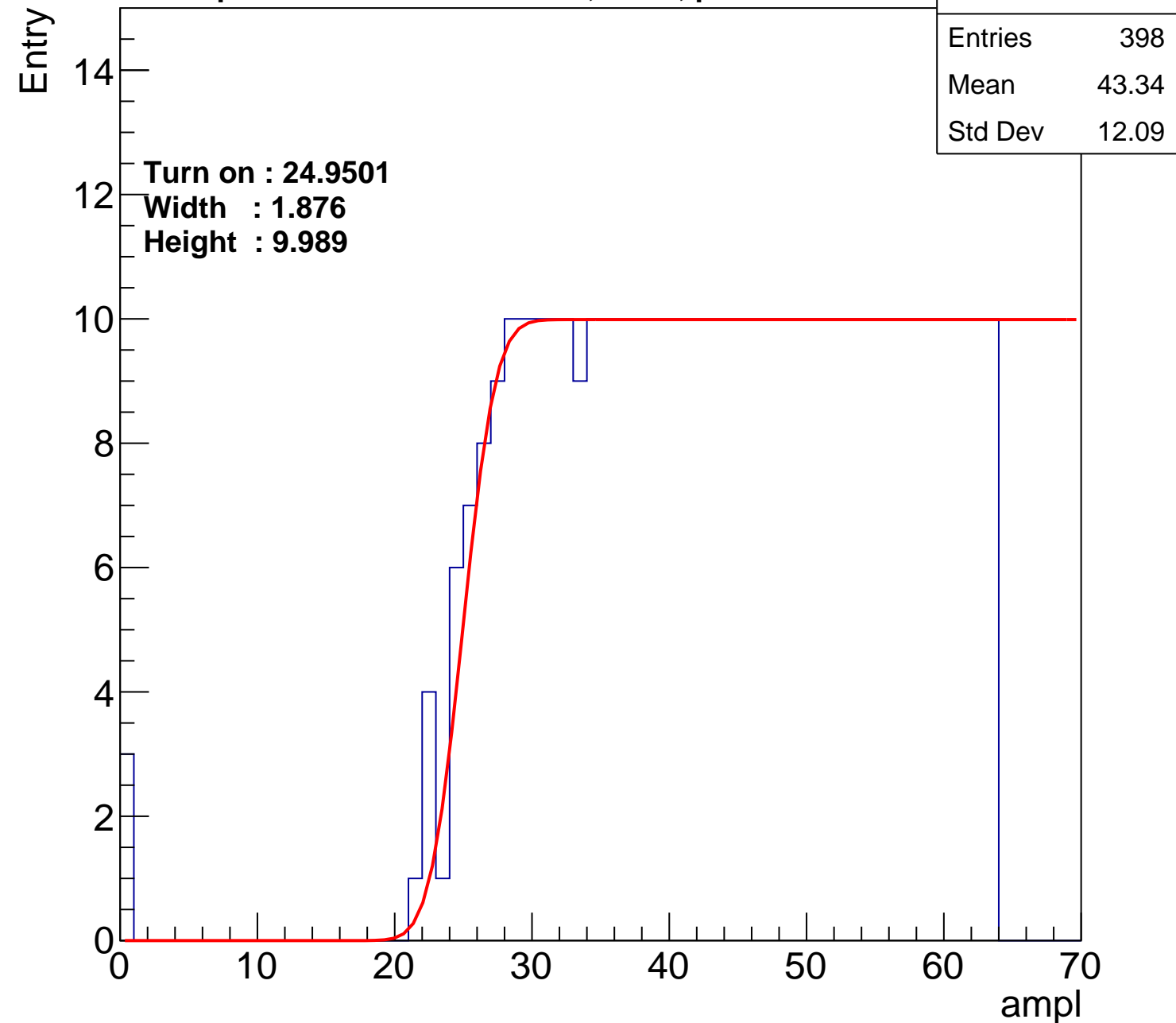
Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U21-ch80

calib_packv5_042523_0143.root, FC#4, port A2

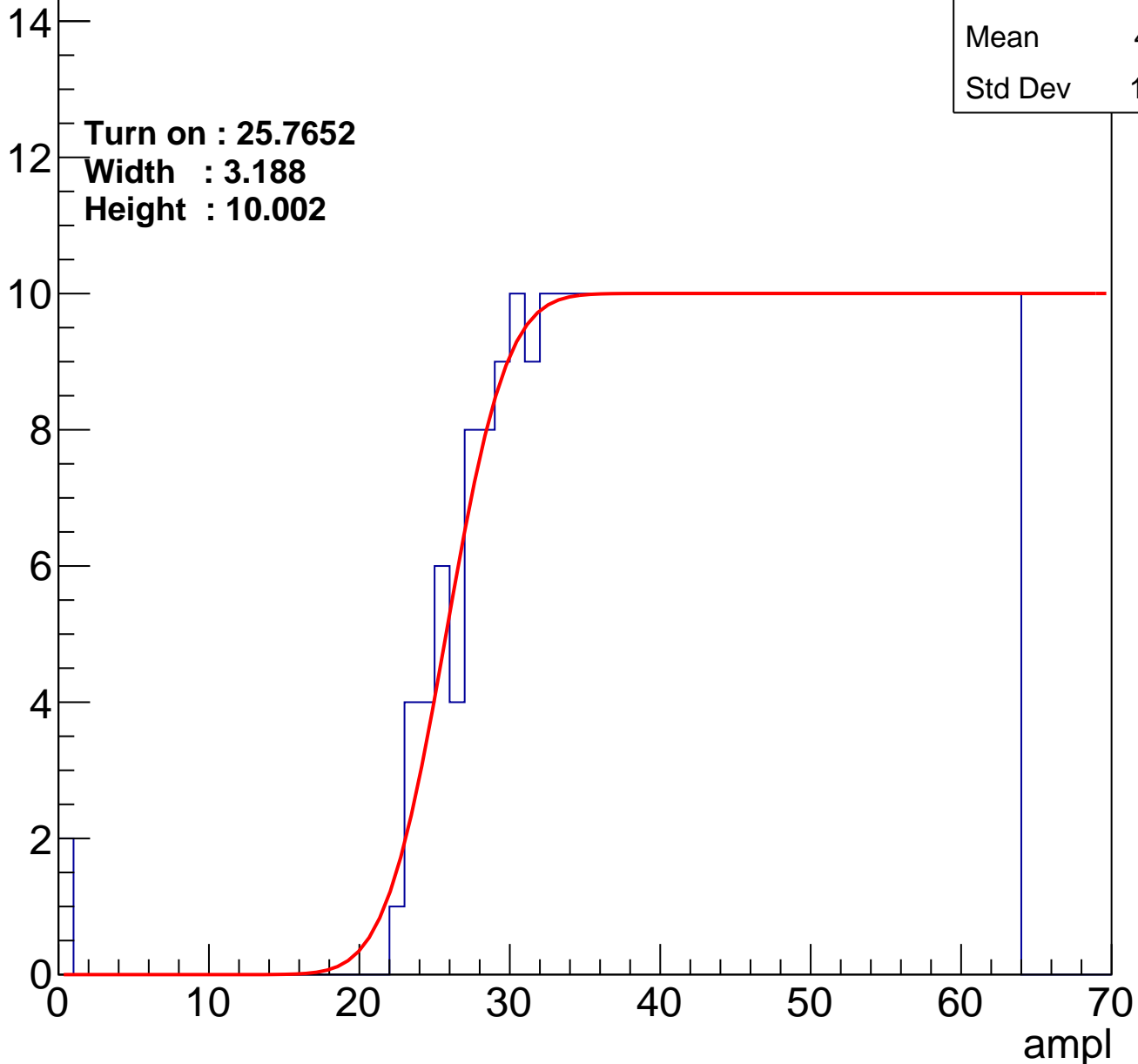
Entries	385
Mean	44.01
Std Dev	11.65

Turn on : 25.7652

Width : 3.188

Height : 10.002

Entry



B1L100S, U21-ch81

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 27.3353

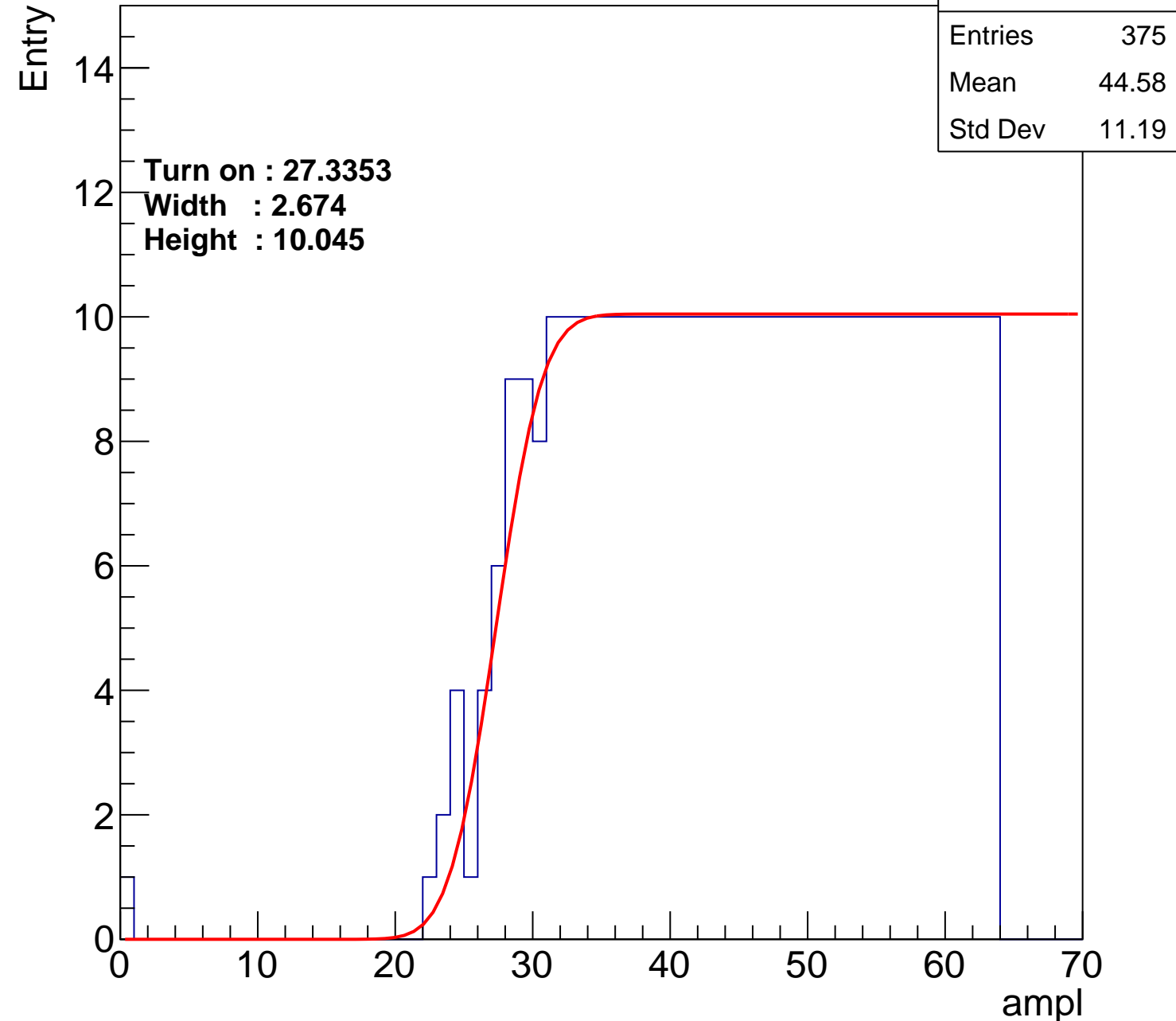
Width : 2.674

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch82

calib_packv5_042523_0143.root, FC#4, port A2

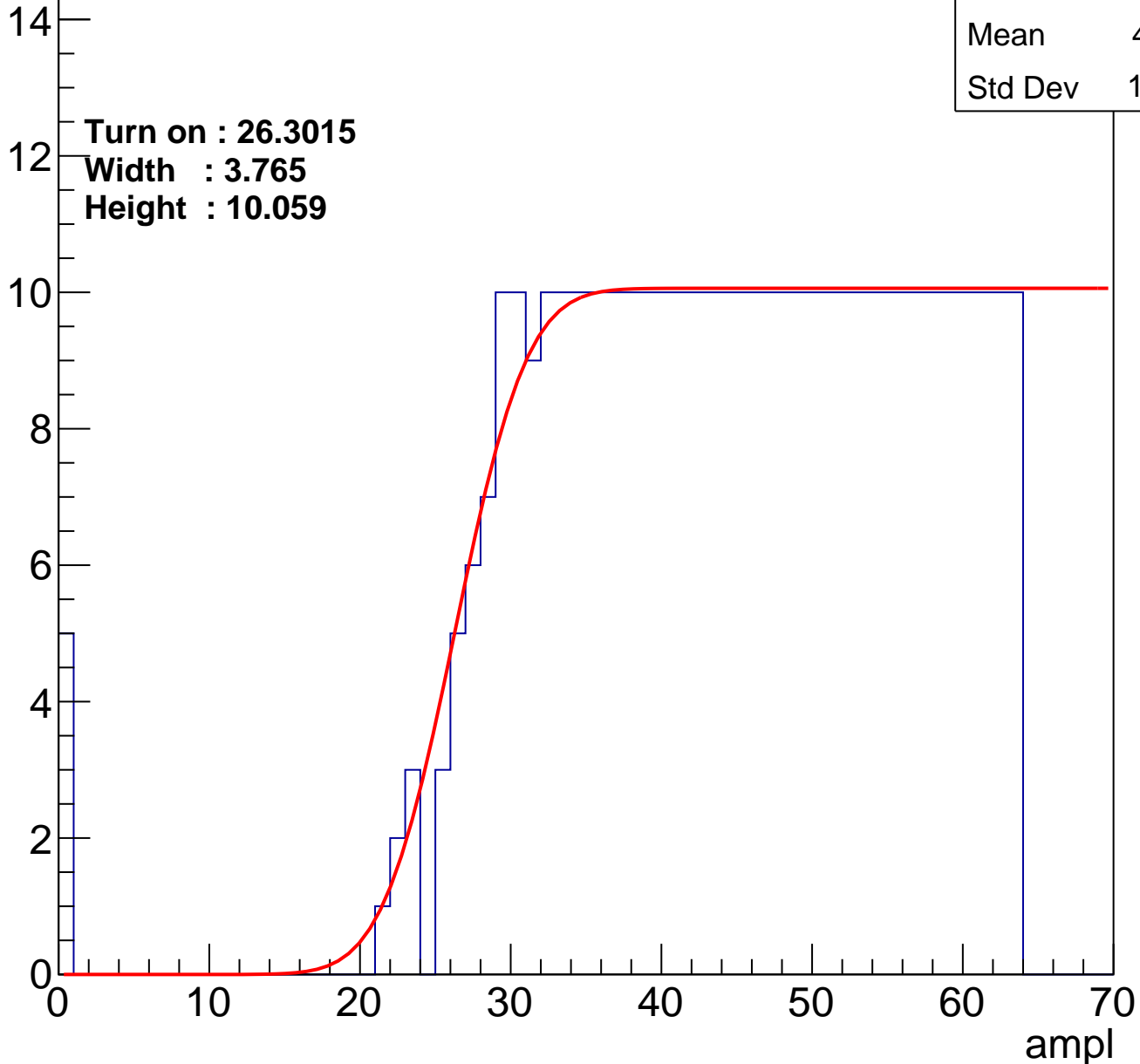
Entries	381
Mean	44.01
Std Dev	12.09

Turn on : 26.3015

Width : 3.765

Height : 10.059

Entry



B1L100S, U21-ch83

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.7
Std Dev	12.02

Turn on : 25.2091

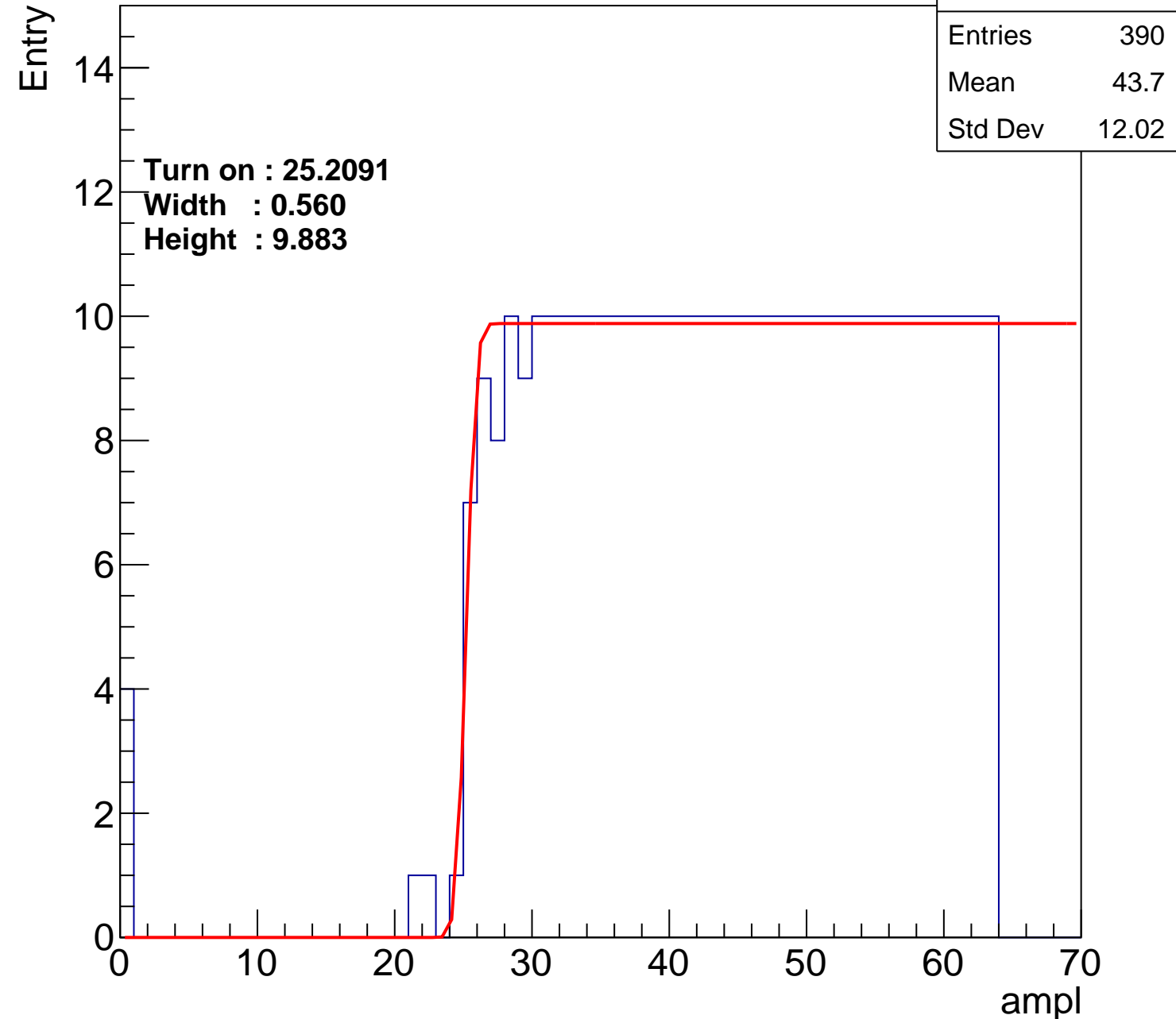
Width : 0.560

Height : 9.883

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch84

calib_packv5_042523_0143.root, FC#4, port A2

Entries	408
Mean	42.86
Std Dev	12.34

Turn on : 23.5374

Width : 2.109

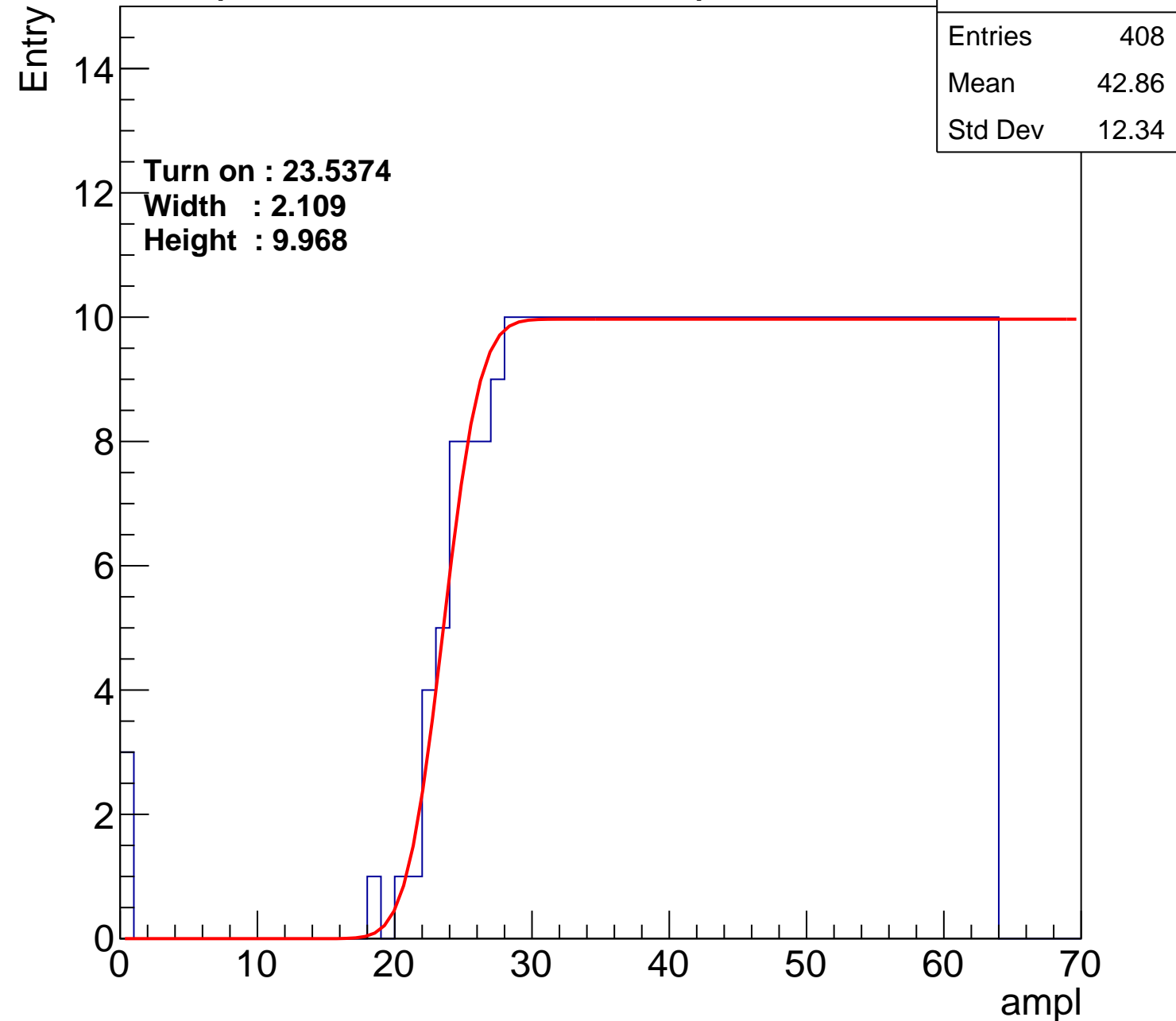
Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U21-ch85

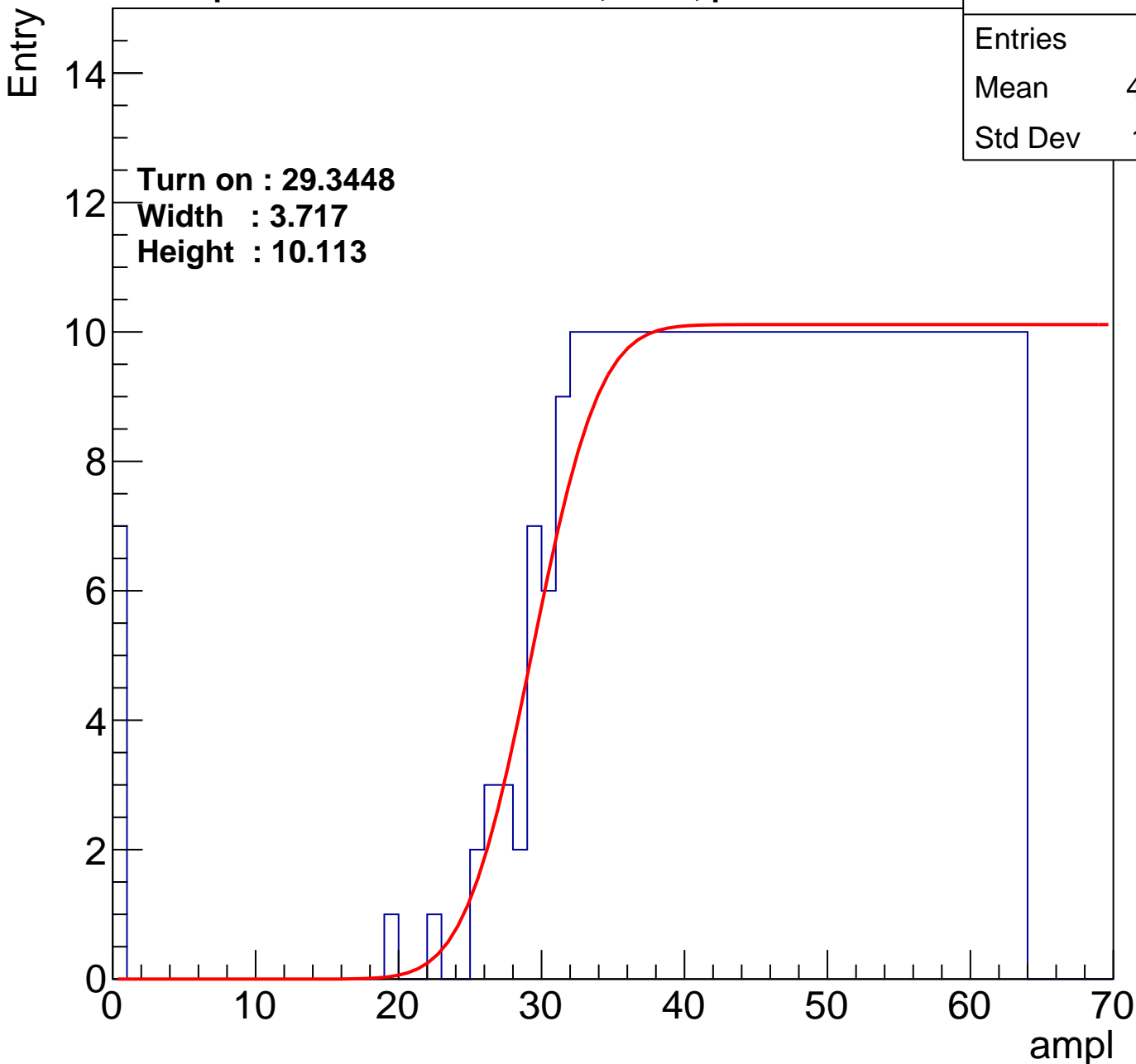
calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	44.79
Std Dev	12.11

Turn on : 29.3448

Width : 3.717

Height : 10.113



B1L100S, U21-ch86

calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	45.01
Std Dev	11.5

Turn on : 28.5456

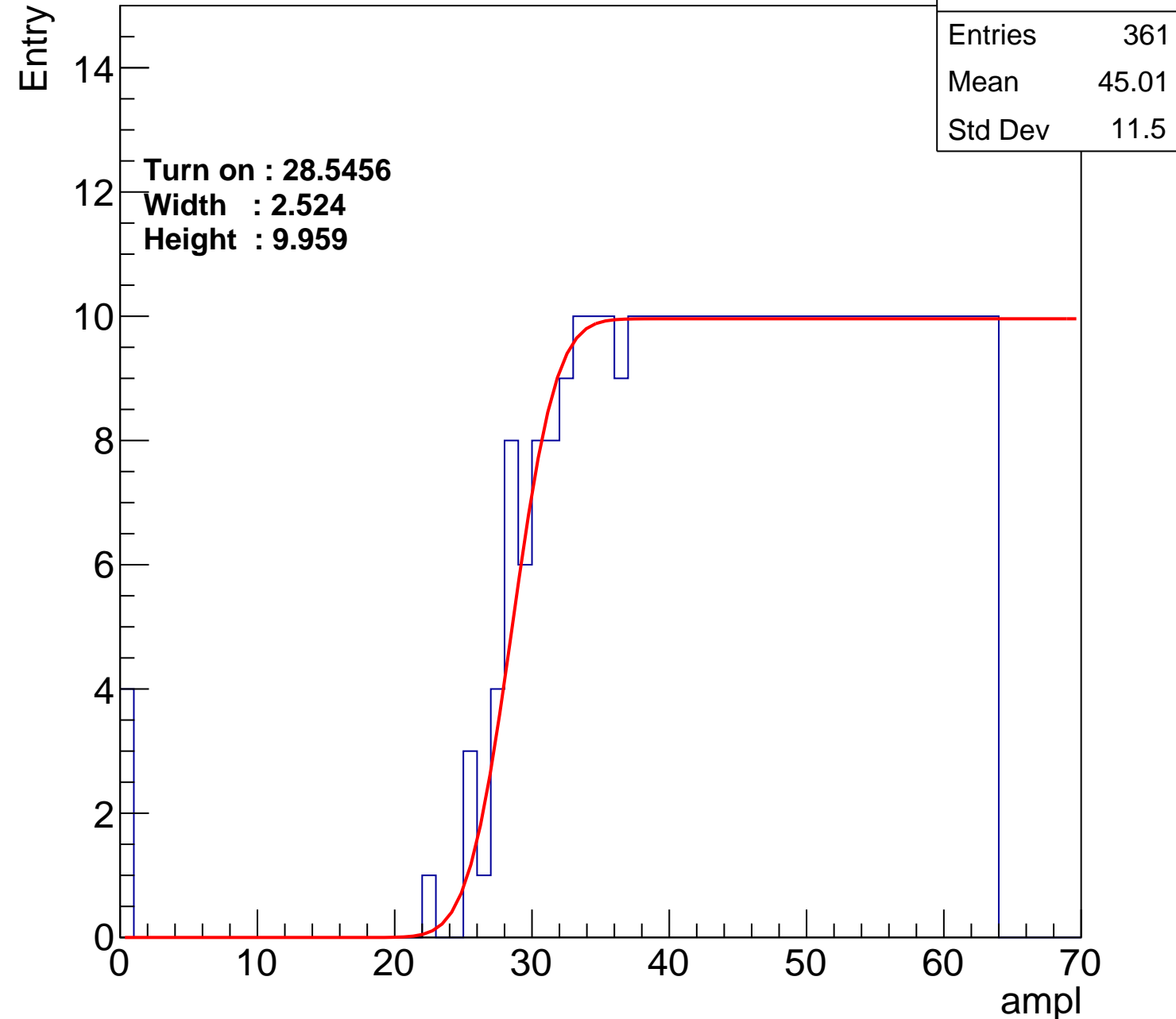
Width : 2.524

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch87

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.86
Std Dev	11.34

Turn on : 27.5724

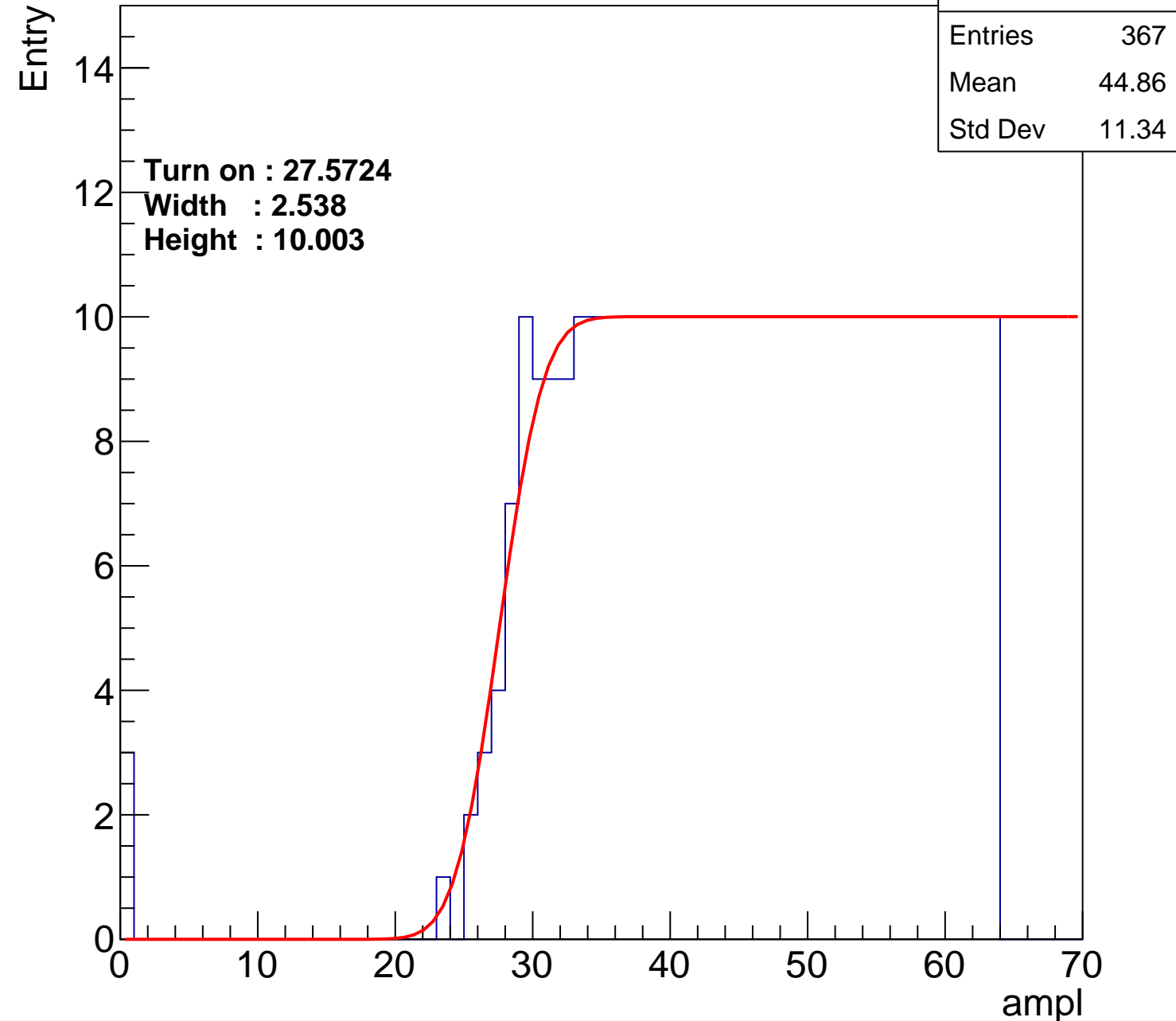
Width : 2.538

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch88

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.69
Std Dev	11.92

Turn on : 25.3208

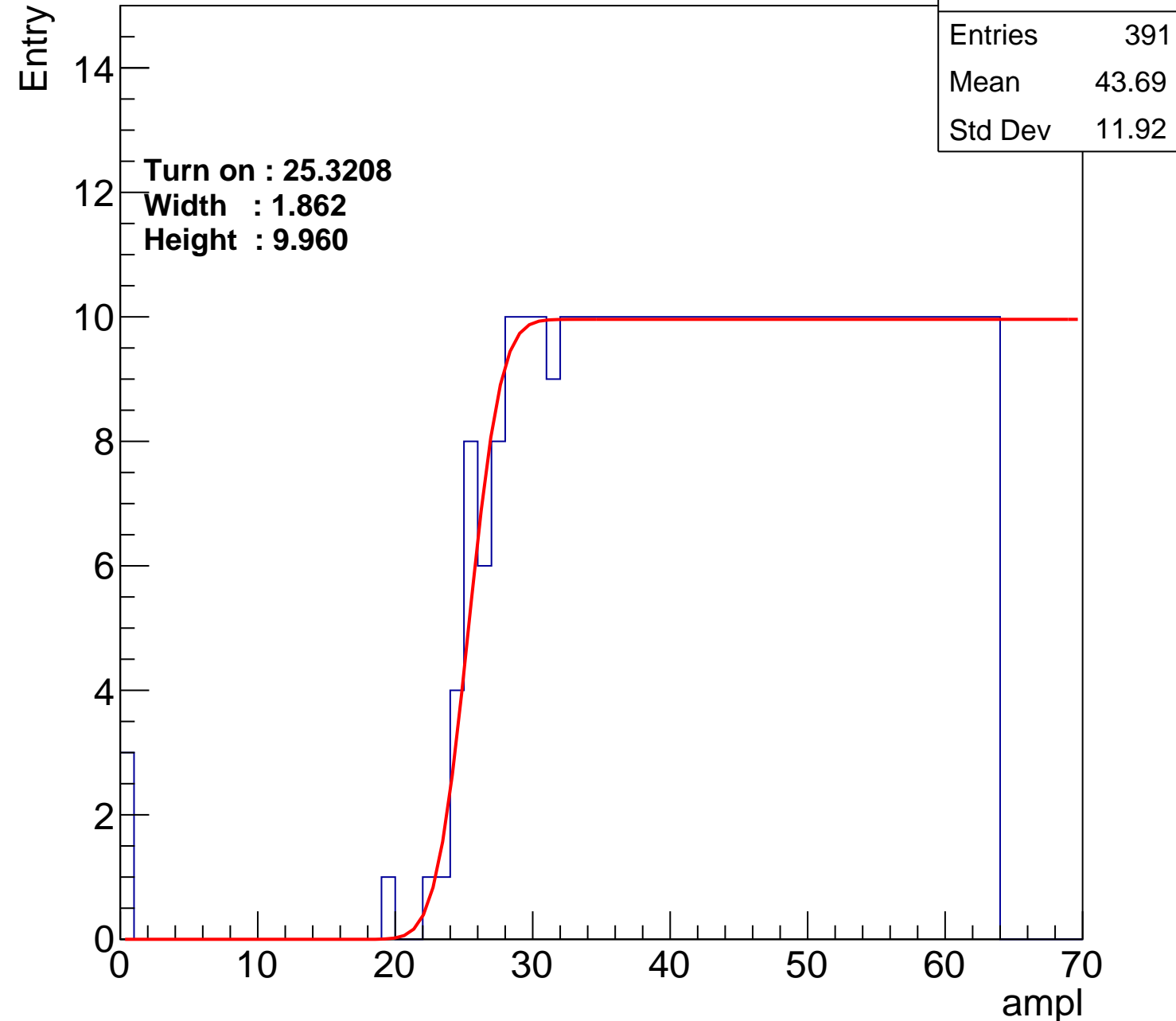
Width : 1.862

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch89

calib_packv5_042523_0143.root, FC#4, port A2

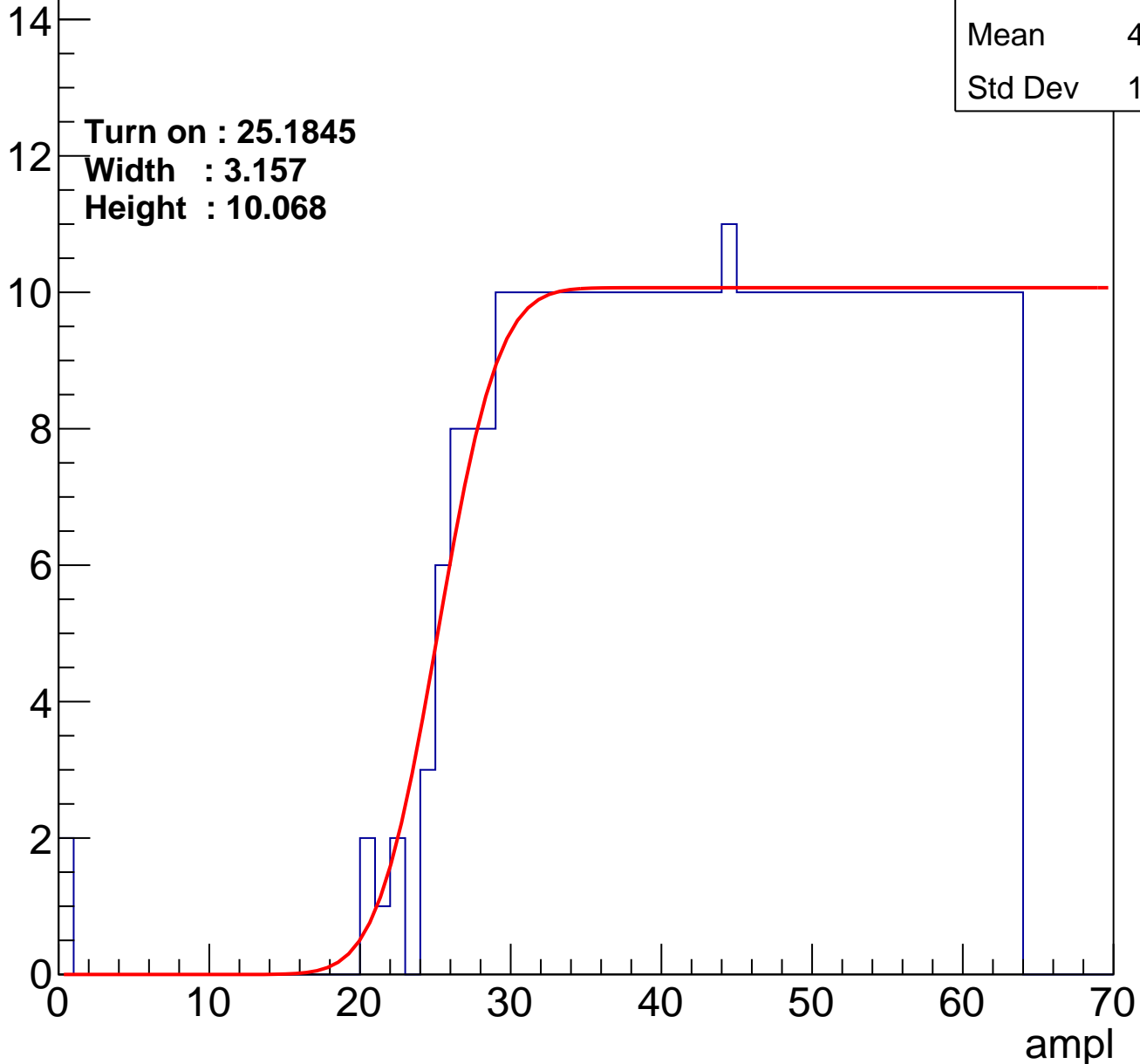
Entries	391
Mean	43.78
Std Dev	11.74

Turn on : 25.1845

Width : 3.157

Height : 10.068

Entry



B1L100S, U21-ch90

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.94
Std Dev	11.03

Turn on : 28.1377

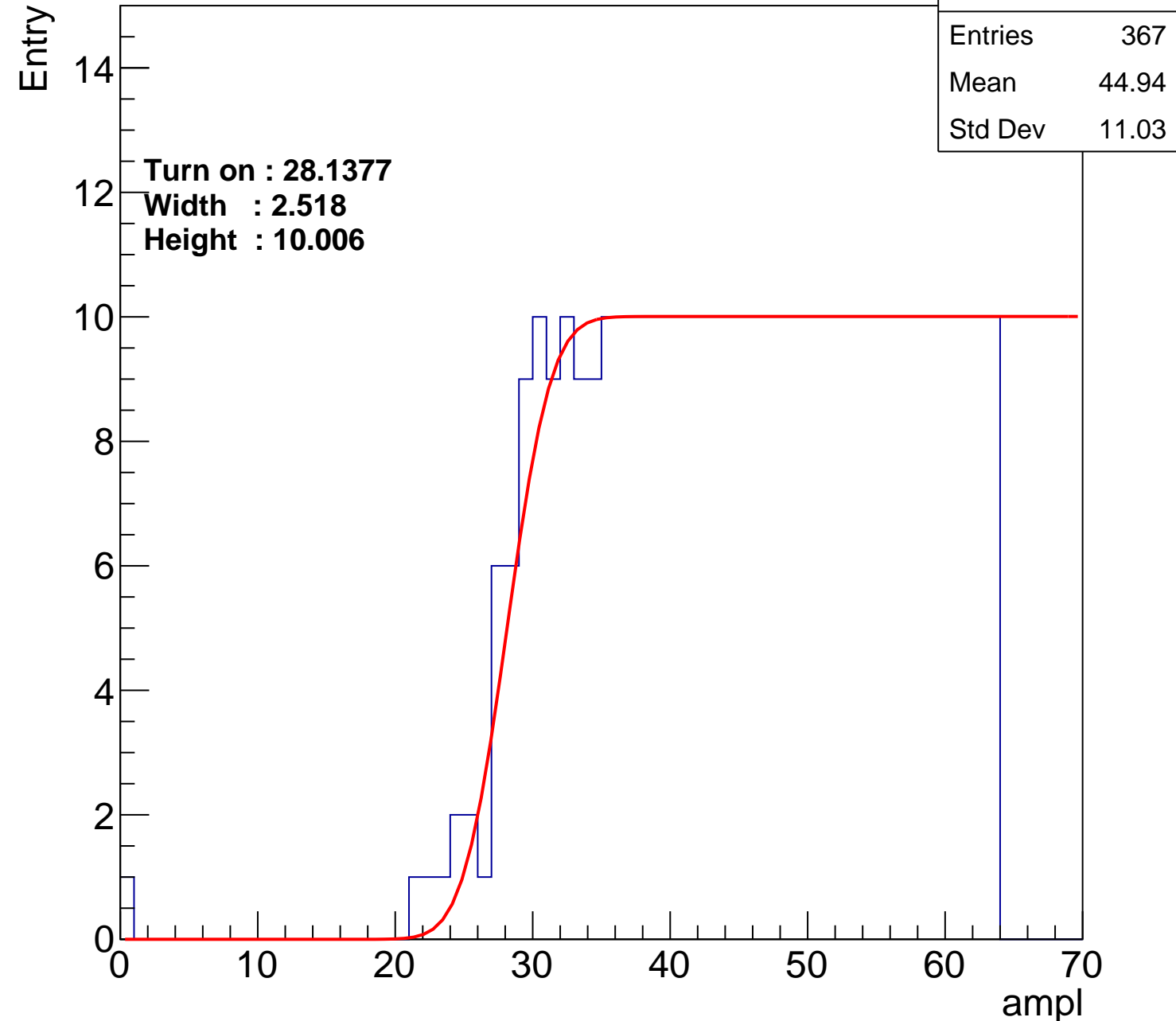
Width : 2.518

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch91

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.58
Std Dev	11.52

Turn on : 27.3032

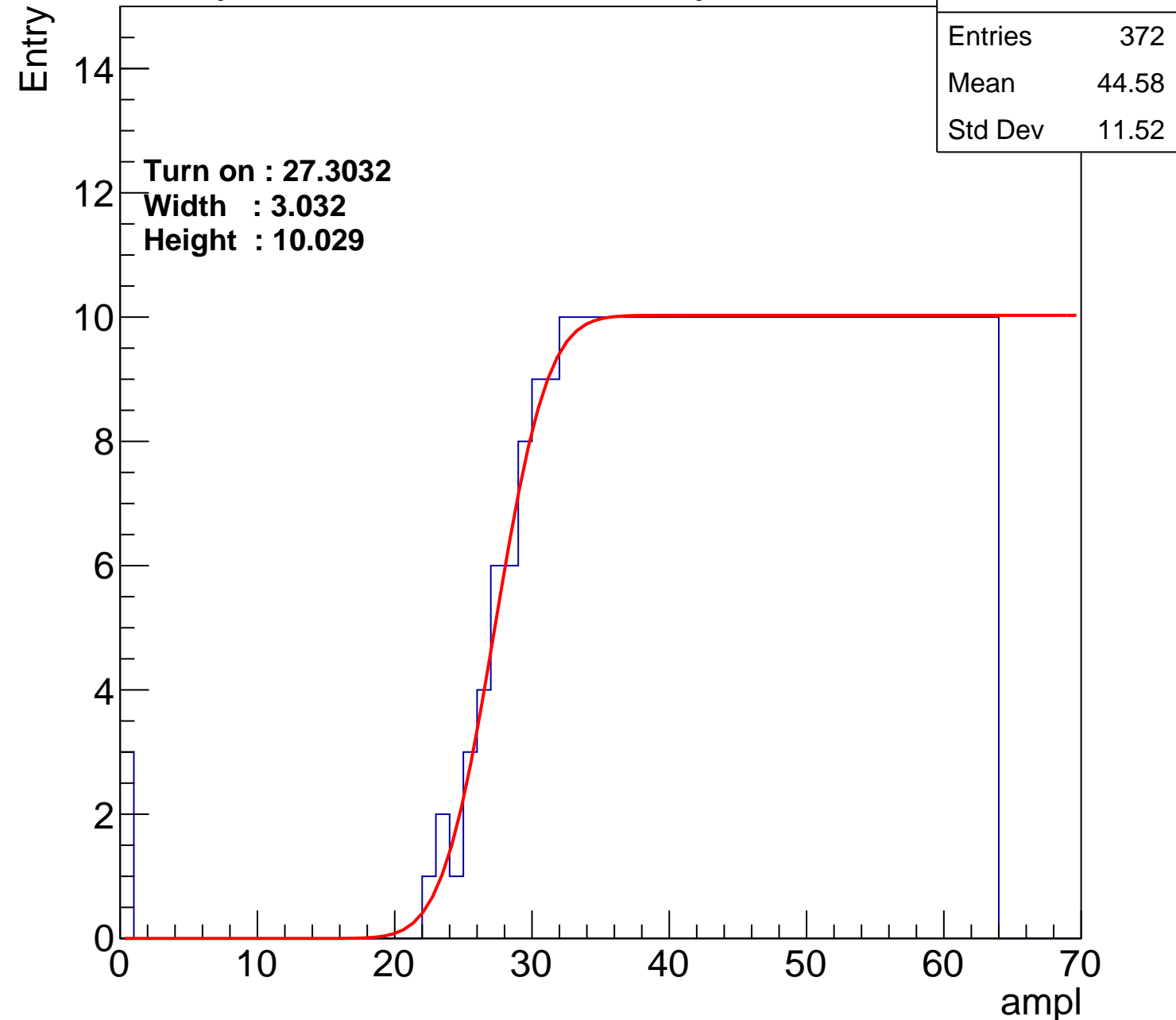
Width : 3.032

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch92

calib_packv5_042523_0143.root, FC#4, port A2

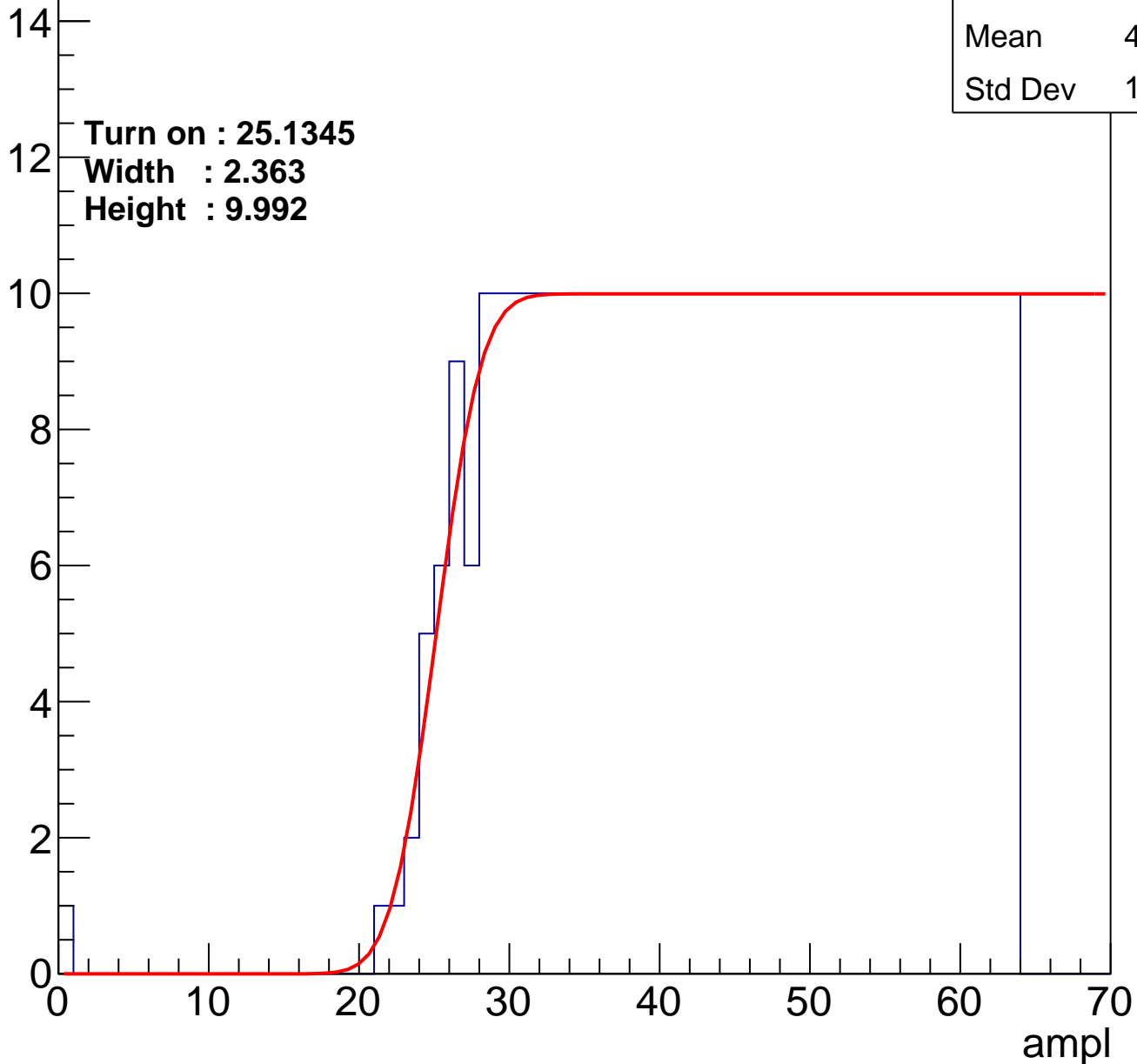
Entries	391
Mean	43.82
Std Dev	11.56

Turn on : 25.1345

Width : 2.363

Height : 9.992

Entry



B1L100S, U21-ch93

calib_packv5_042523_0143.root, FC#4, port A2

Entries	363
Mean	45.13
Std Dev	11.03

Turn on : 27.8819

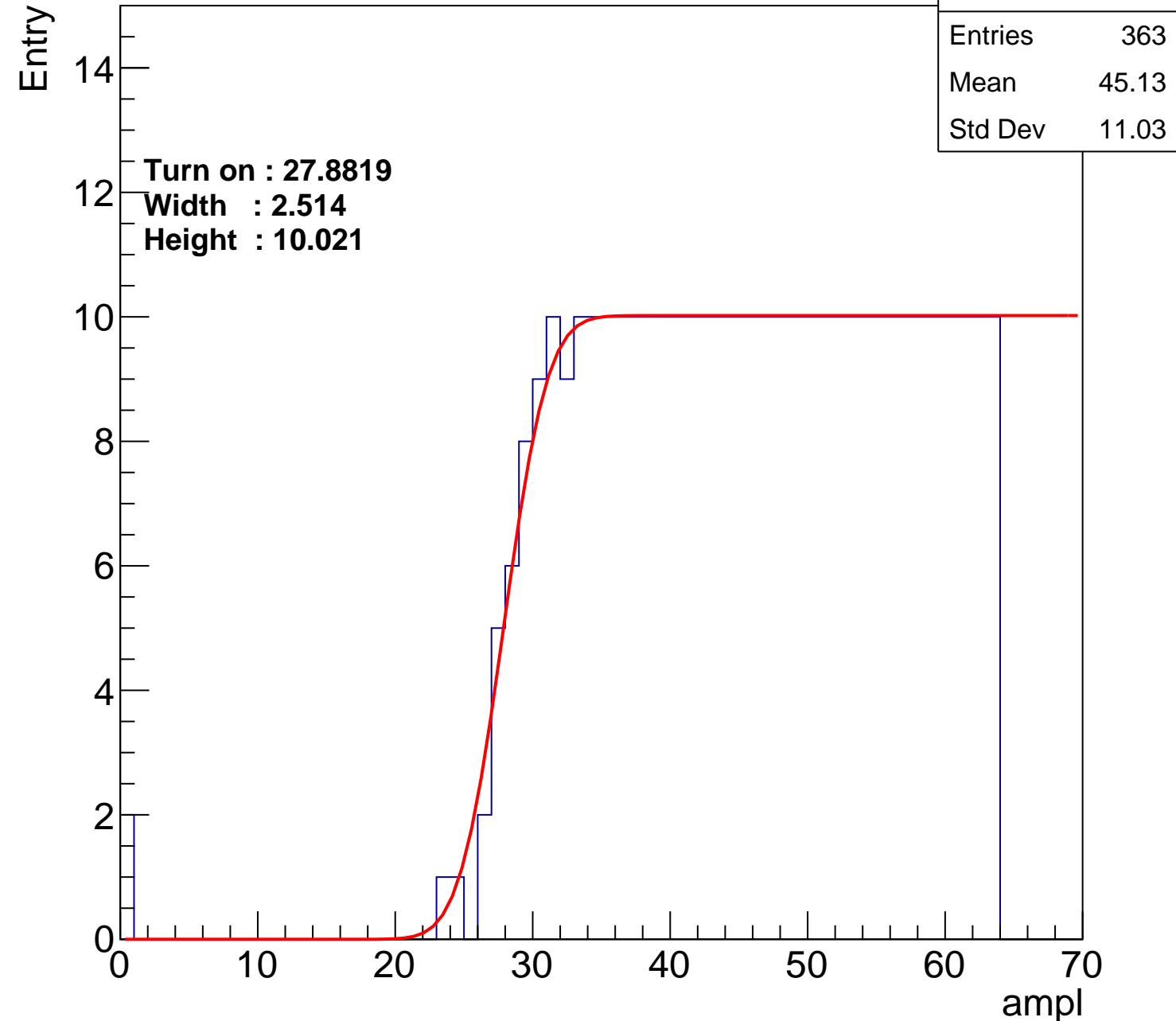
Width : 2.514

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch94

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.42
Std Dev	11.66

Turn on : 26.9198

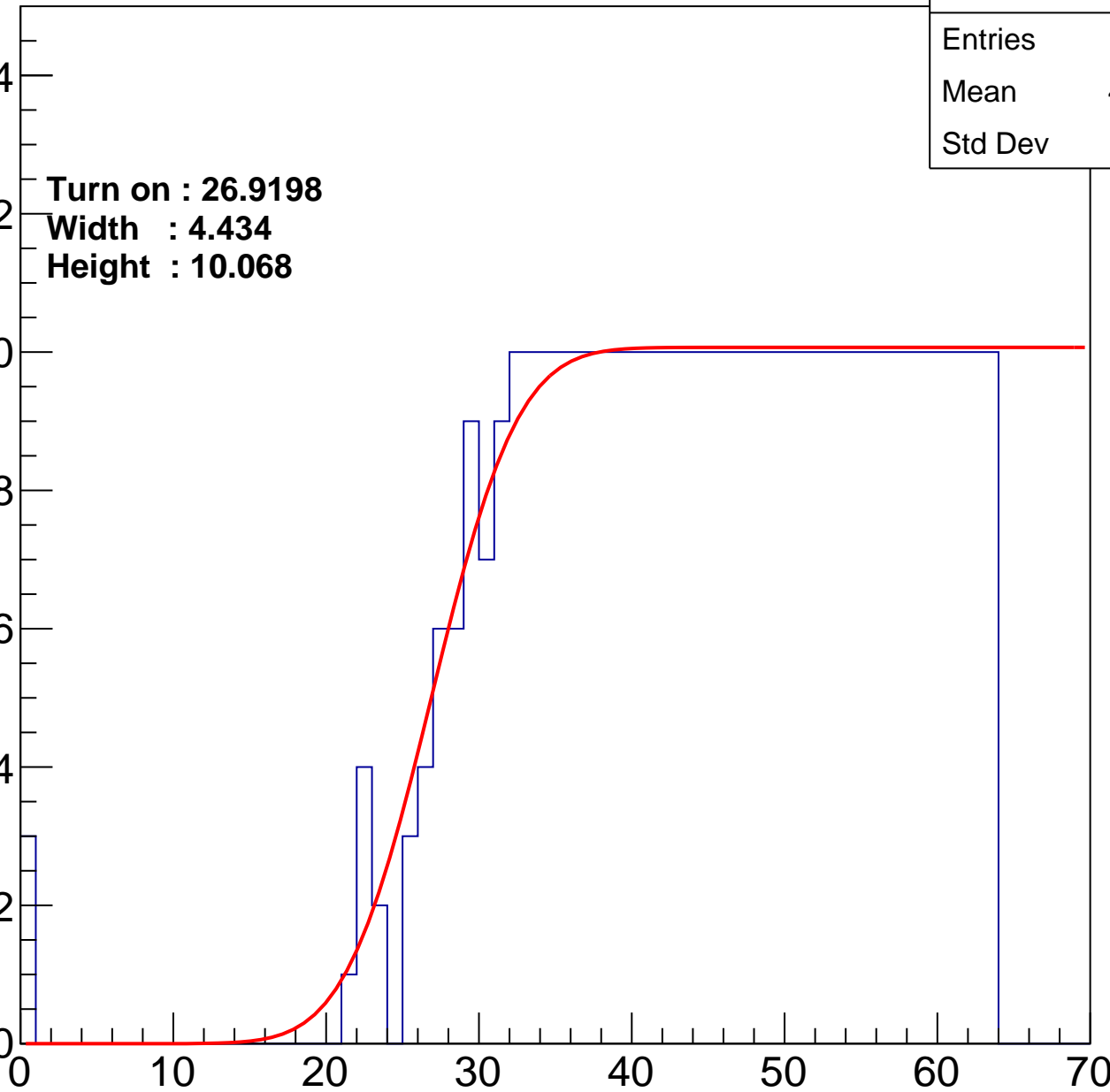
Width : 4.434

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch95

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.45
Std Dev	11.6

Turn on : 27.1900

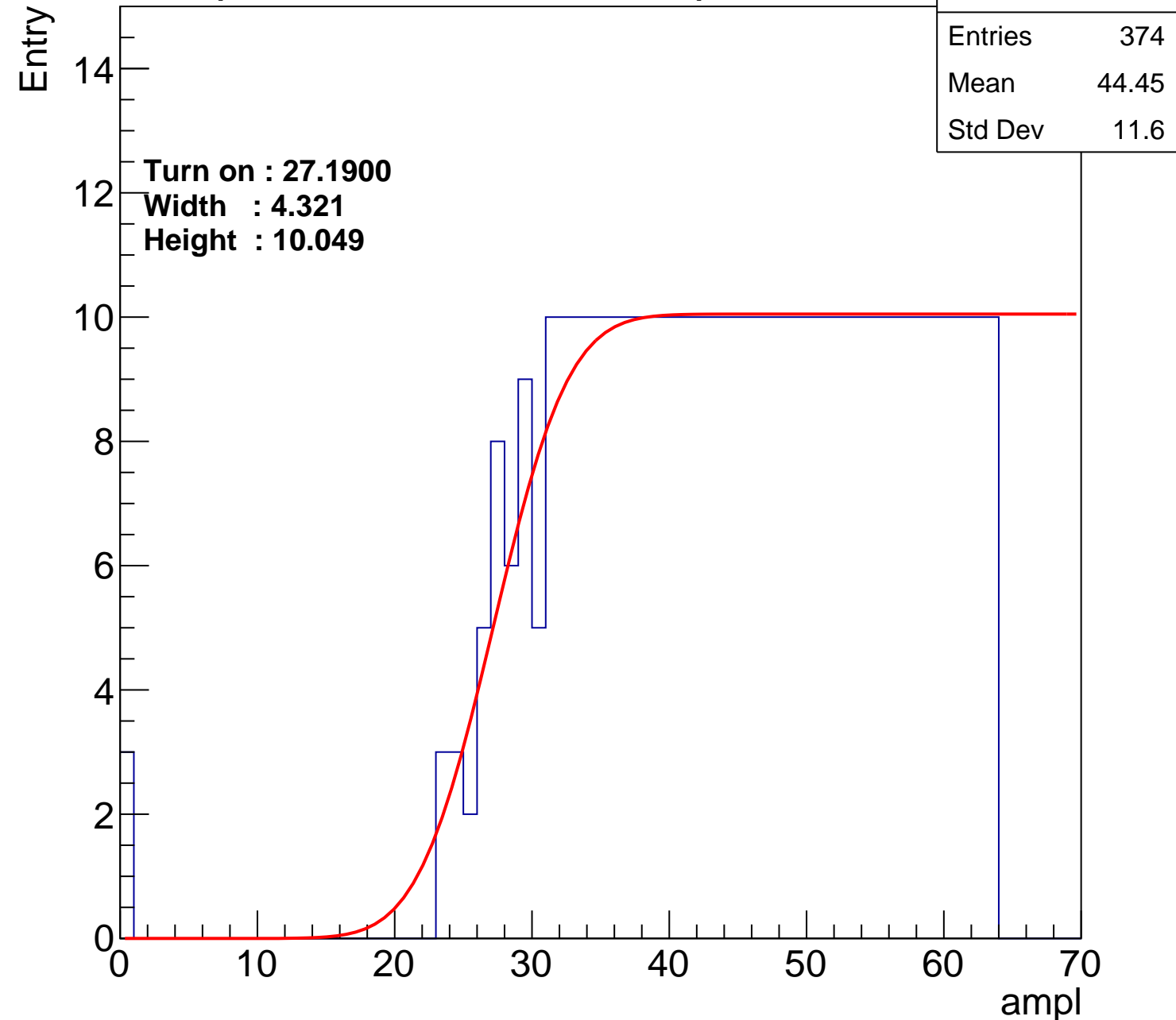
Width : 4.321

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch96

calib_packv5_042523_0143.root, FC#4, port A2

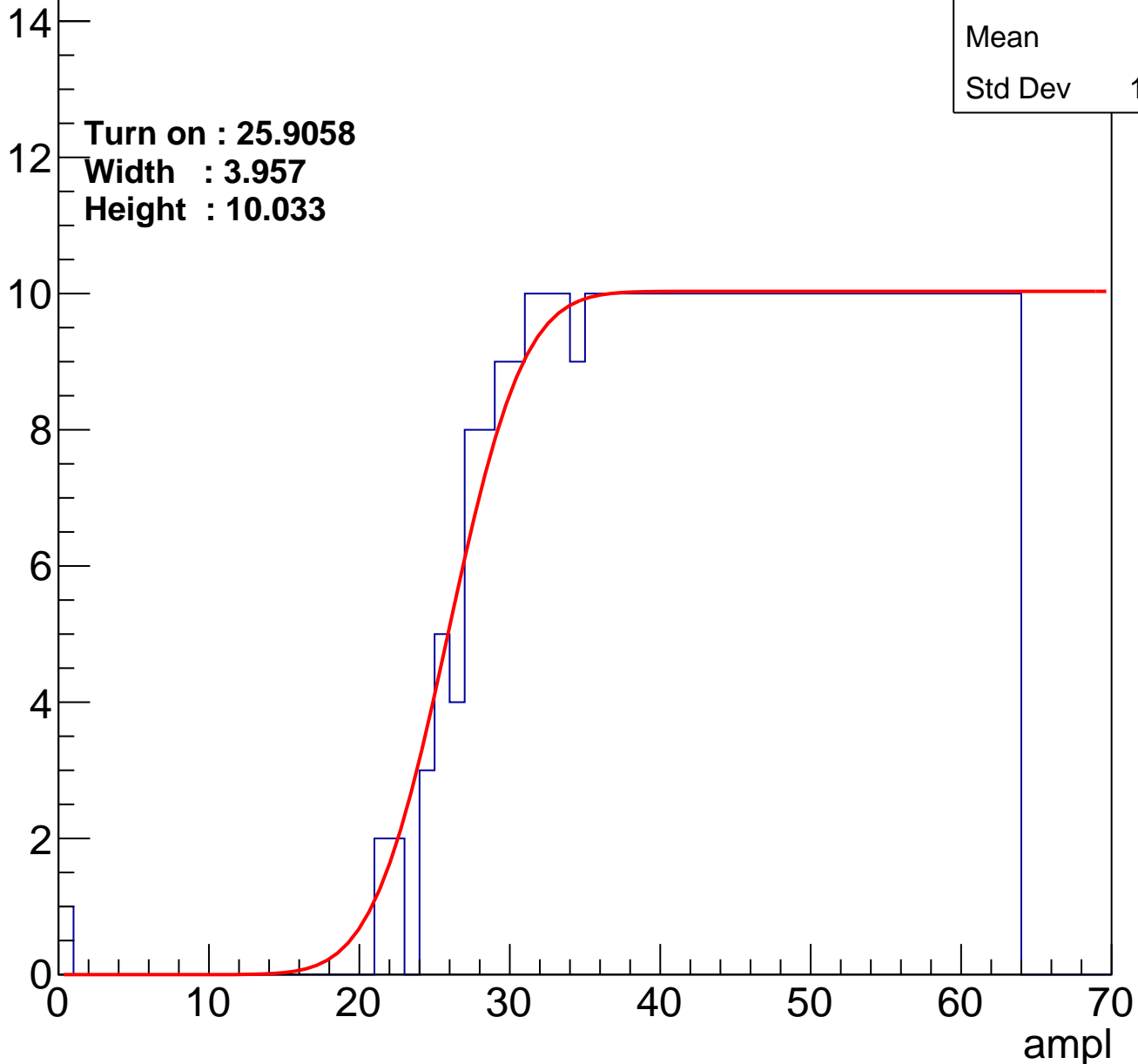
Entries	380
Mean	44.3
Std Dev	11.37

Turn on : 25.9058

Width : 3.957

Height : 10.033

Entry



calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.91
Std Dev	12.07

Mean	43.91
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Std Dev	12.07
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Width : 2.549

Height : 10.024



B1L100S, U21-ch98

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.8
Std Dev	11.92

Turn on : 25.8680

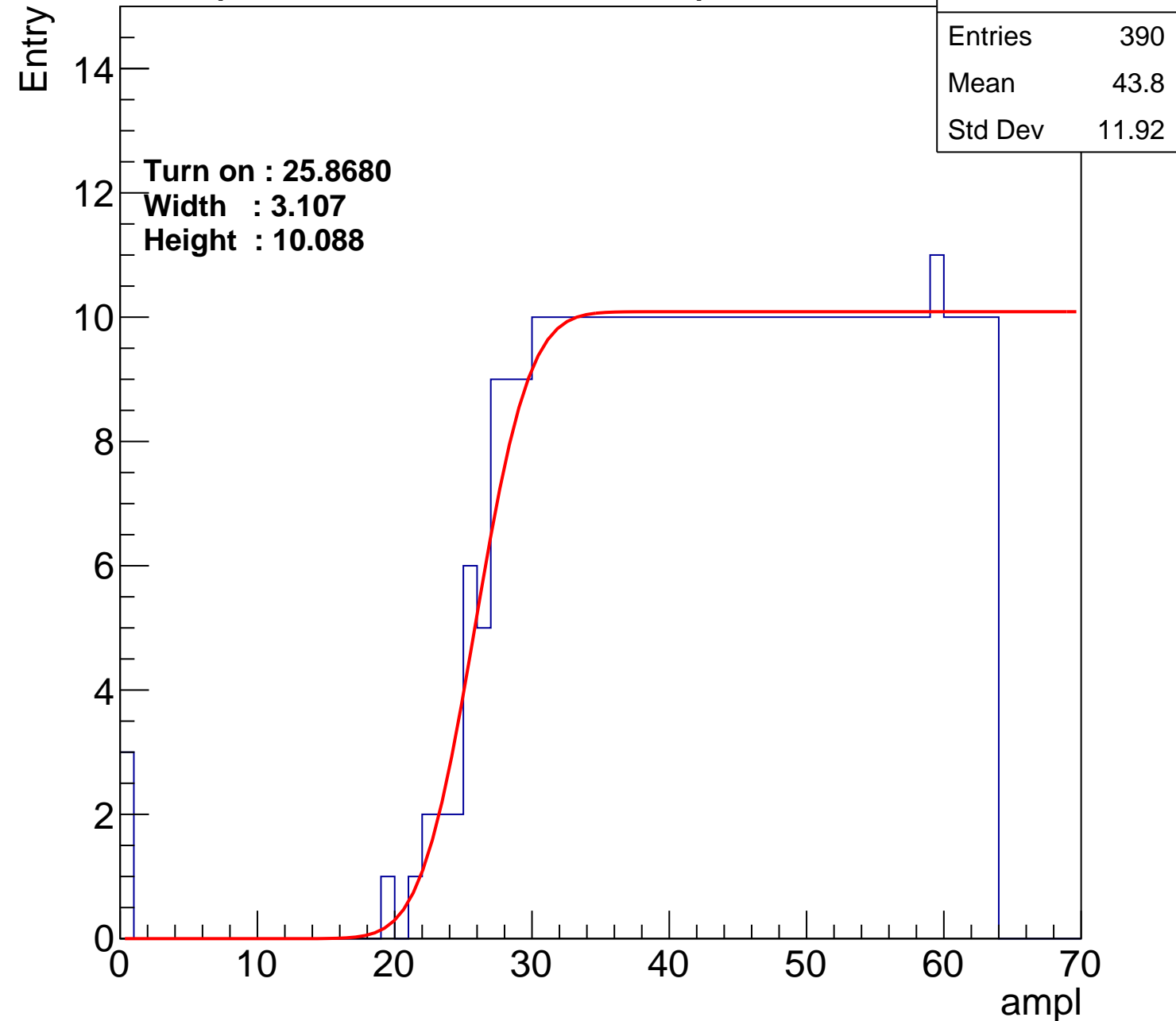
Width : 3.107

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch99

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.92
Std Dev	11.86

Turn on : 26.1240

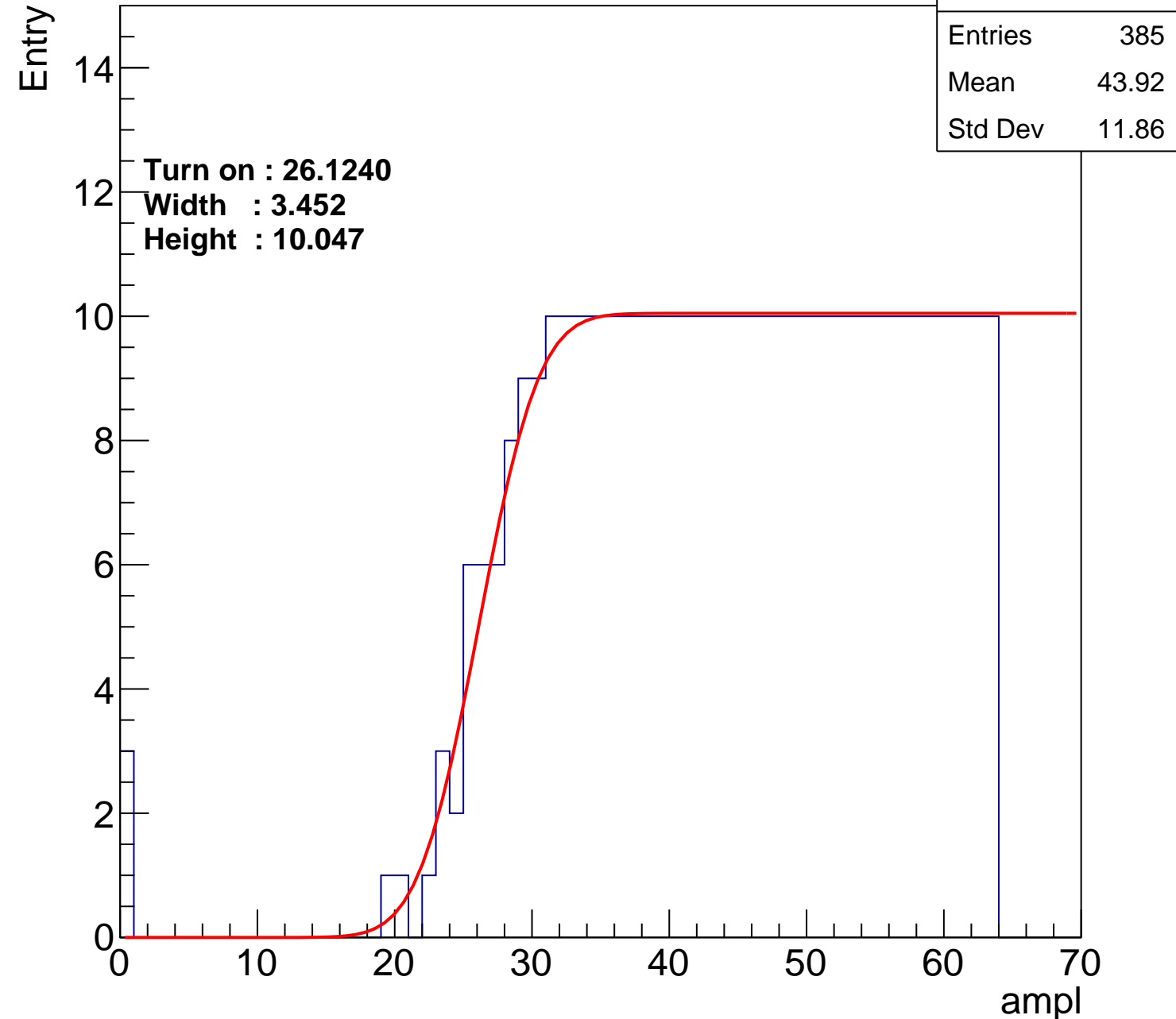
Width : 3.452

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch100

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.63
Std Dev	11.54

Turn on : 27.8310

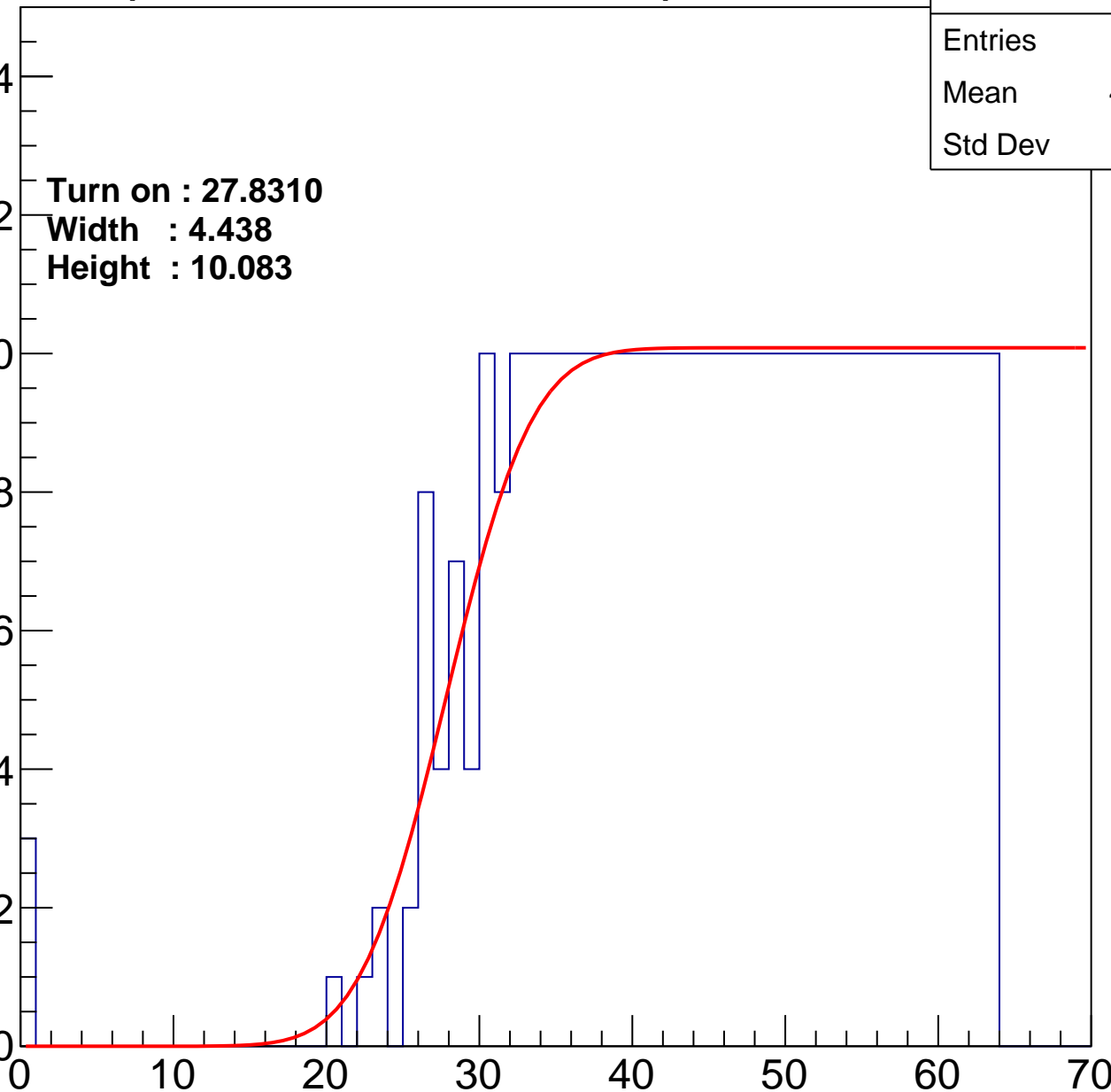
Width : 4.438

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch101

calib_packv5_042523_0143.root, FC#4, port A2

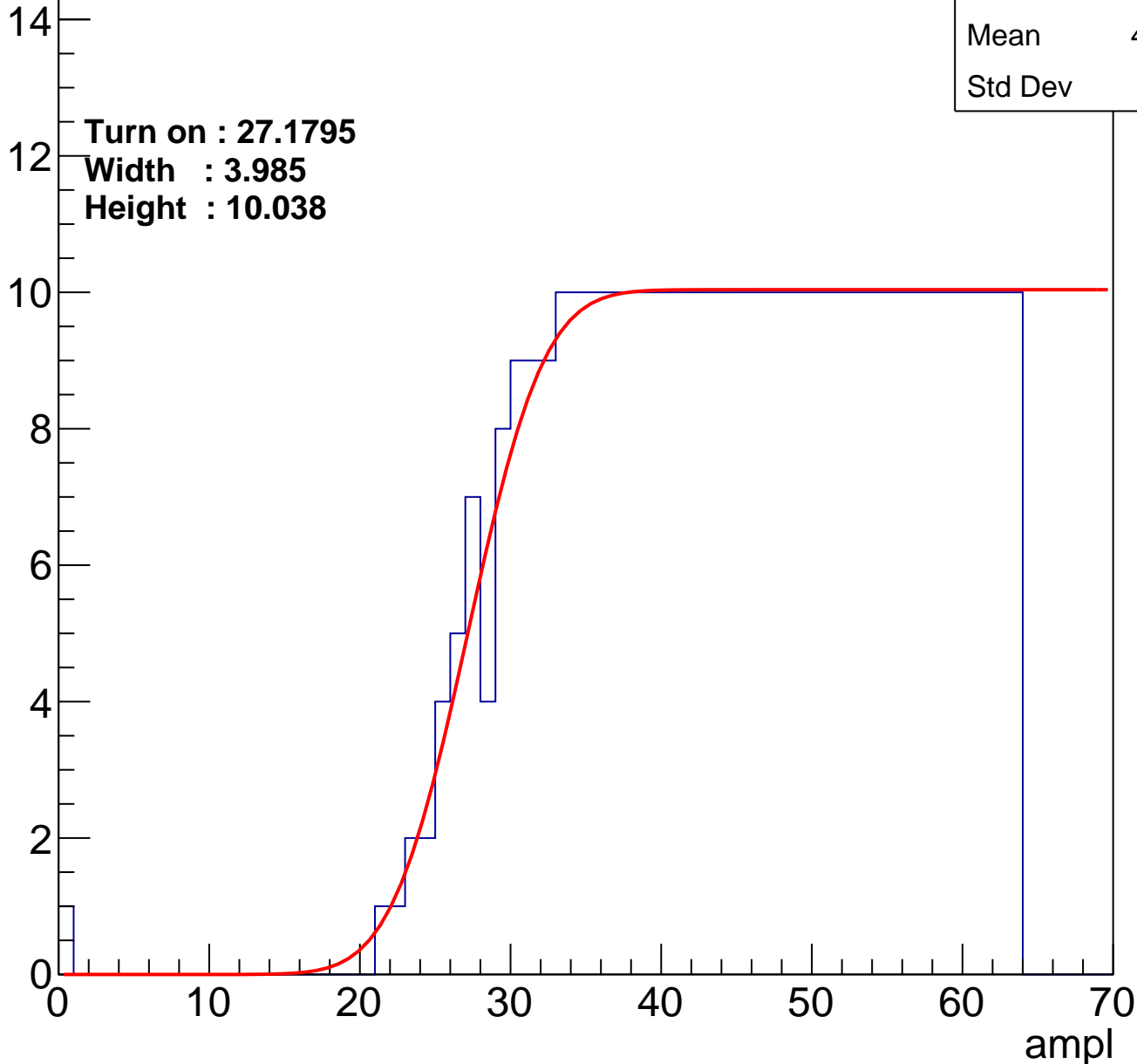
Entries	372
Mean	44.67
Std Dev	11.21

Turn on : 27.1795

Width : 3.985

Height : 10.038

Entry



B1L100S, U21-ch102

calib_packv5_042523_0143.root, FC#4, port A2

Entries	401
Mean	43.23
Std Dev	12.31

Turn on : 24.6613

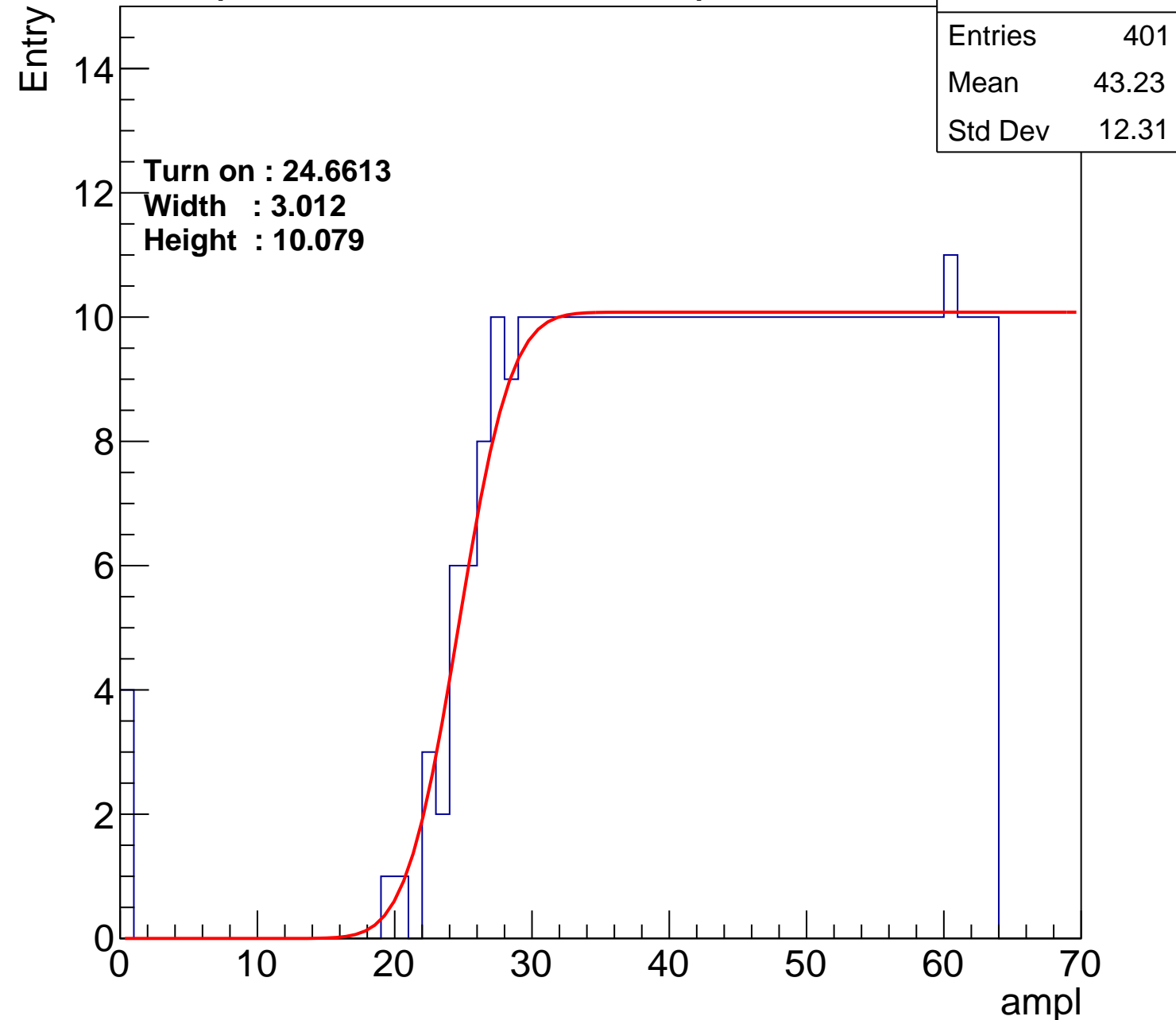
Width : 3.012

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch103

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.5341

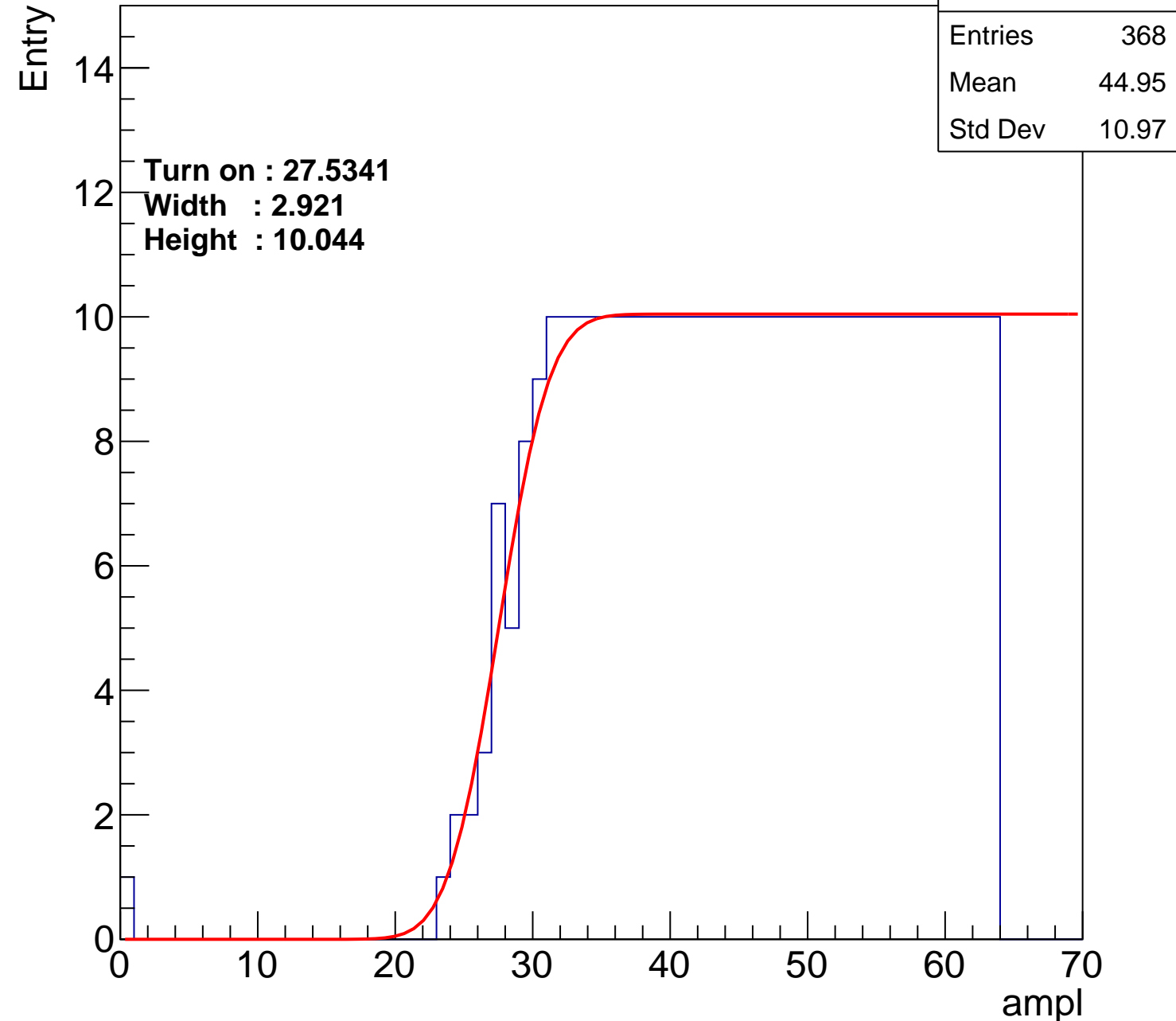
Width : 2.921

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch104

calib_packv5_042523_0143.root, FC#4, port A2

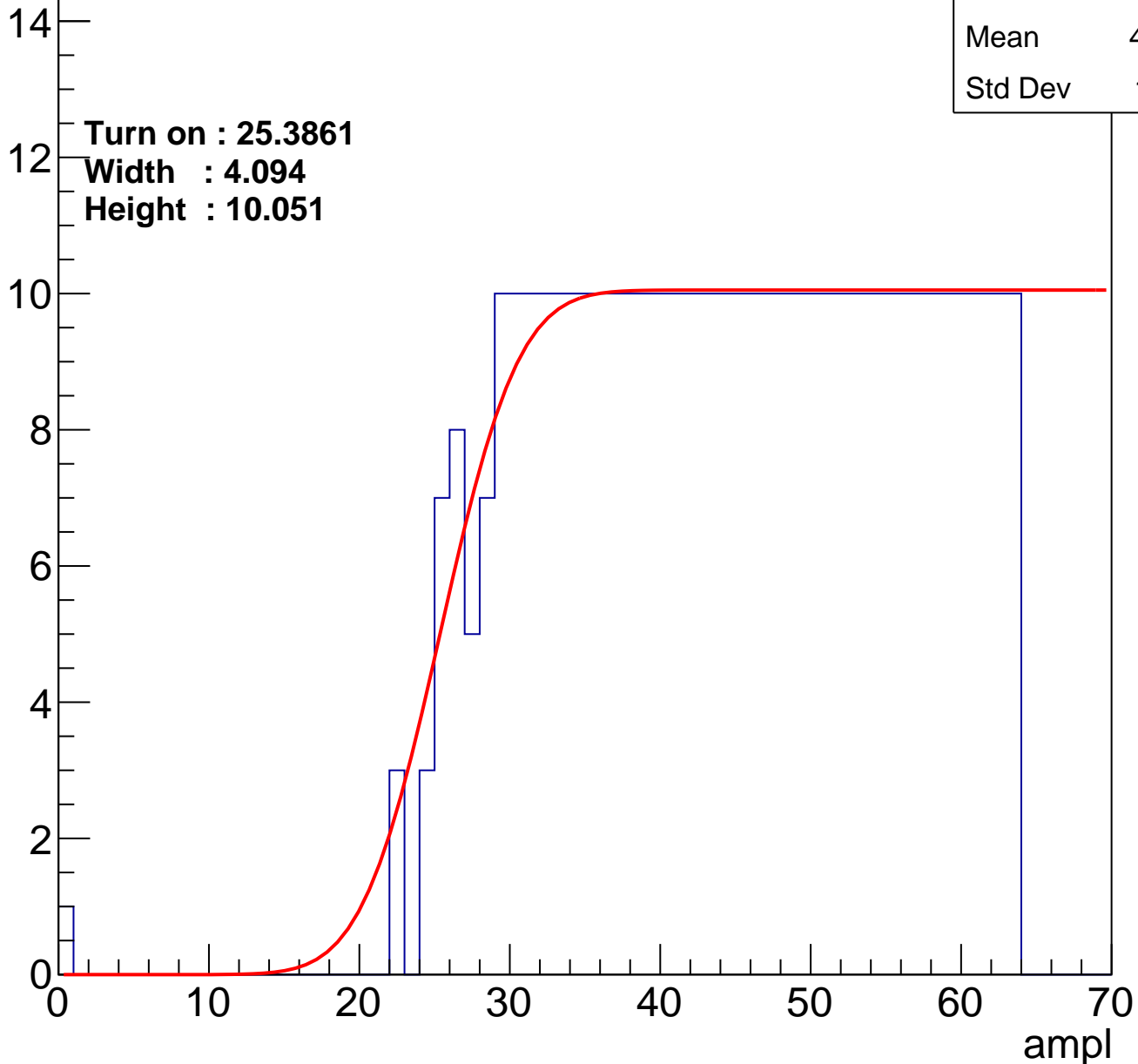
Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.3861

Width : 4.094

Height : 10.051

Entry



B1L100S, U21-ch105

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	43.99
Std Dev	11.93

Turn on : 26.0803

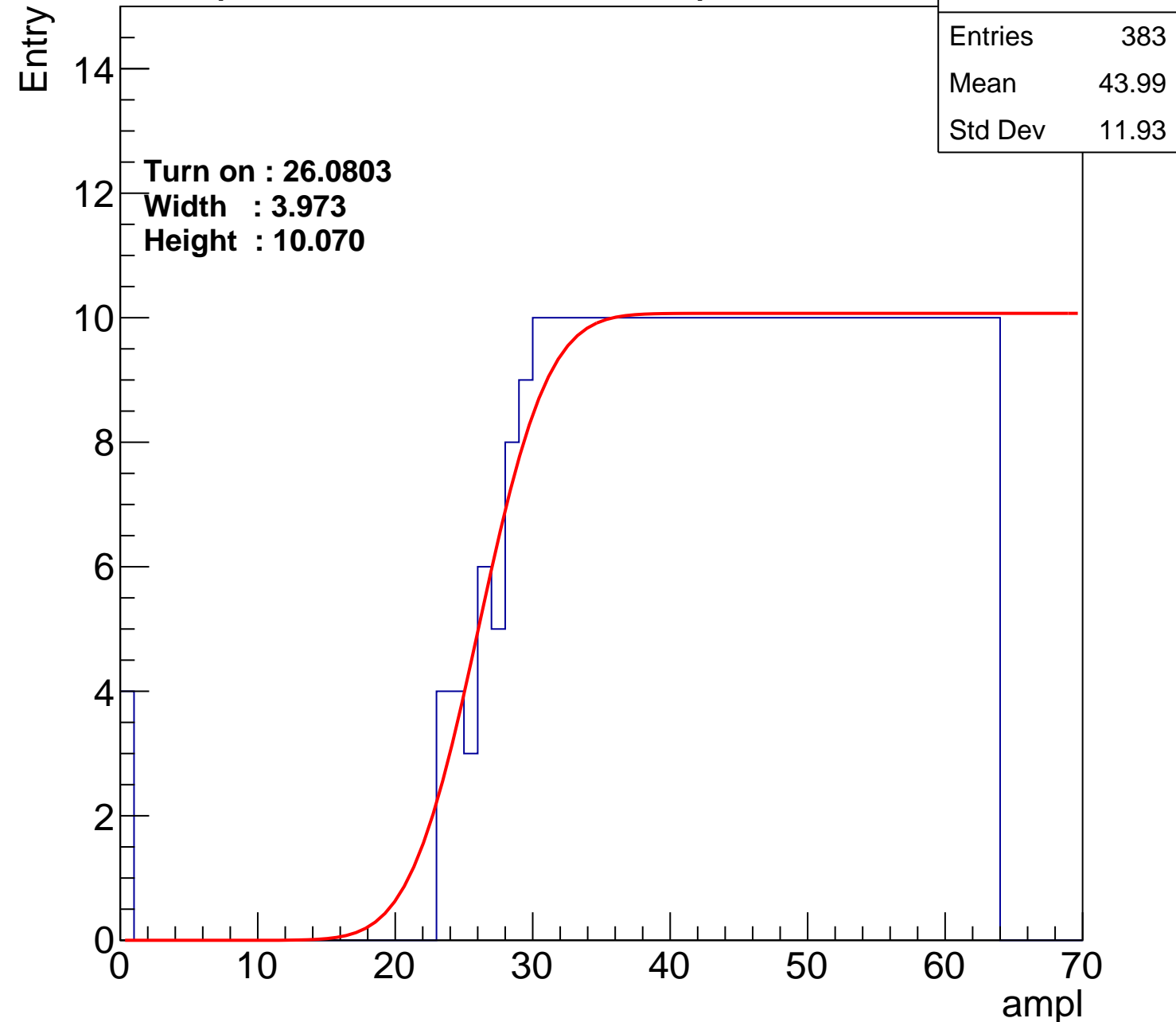
Width : 3.973

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch106

calib_packv5_042523_0143.root, FC#4, port A2

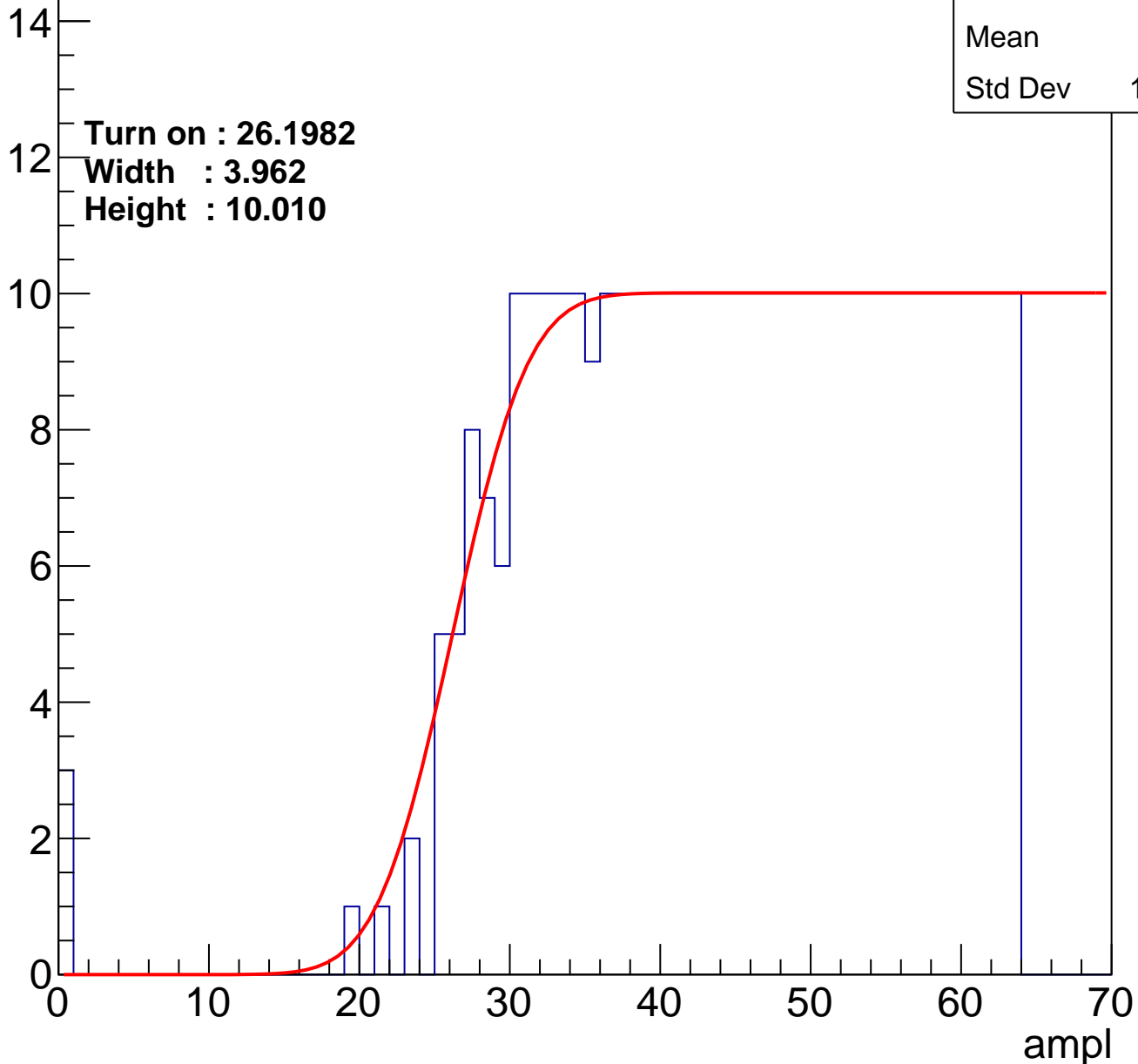
Entries	377
Mean	44.3
Std Dev	11.68

Turn on : 26.1982

Width : 3.962

Height : 10.010

Entry



B1L100S, U21-ch107

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 26.8056

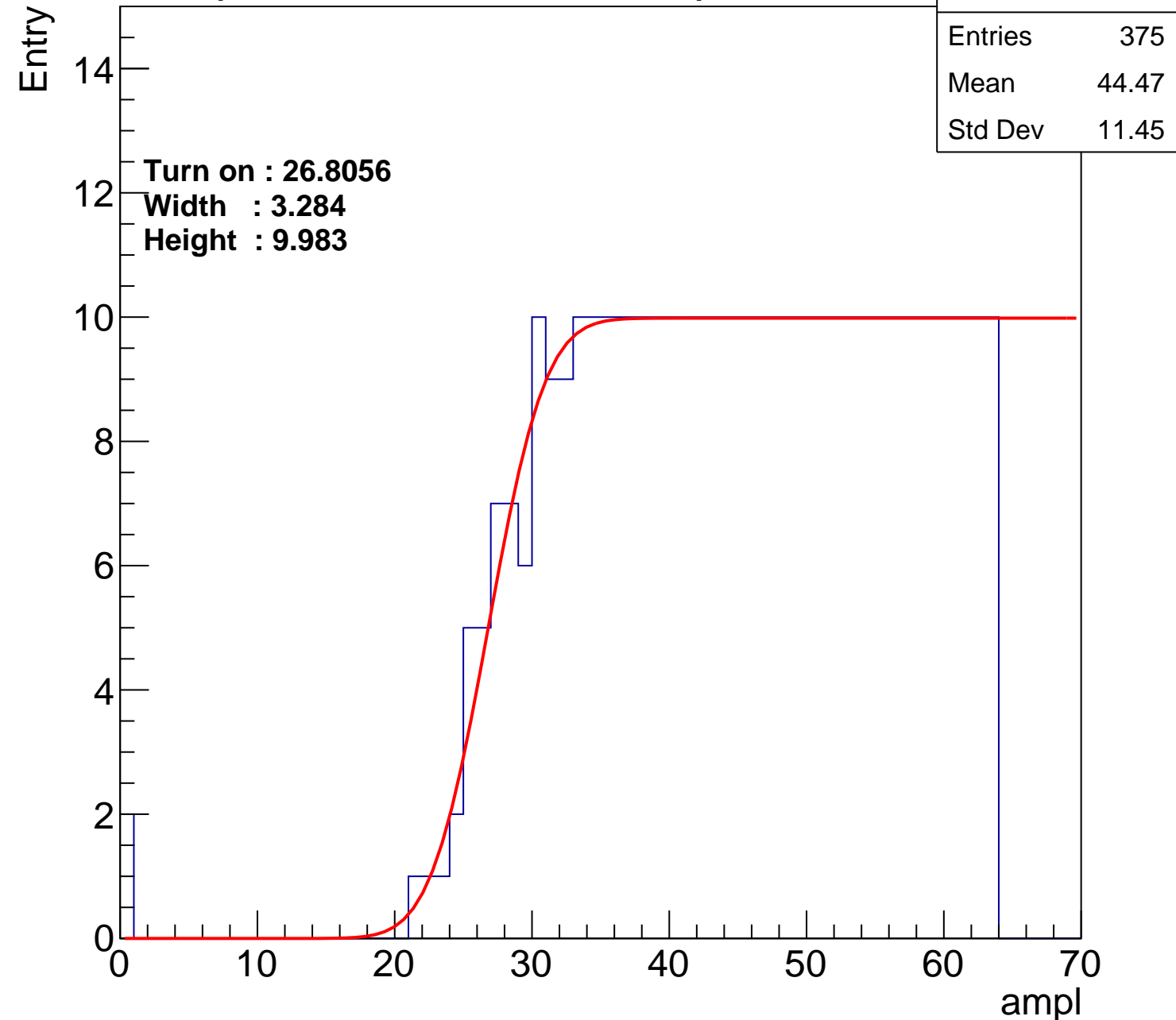
Width : 3.284

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch108

calib_packv5_042523_0143.root, FC#4, port A2

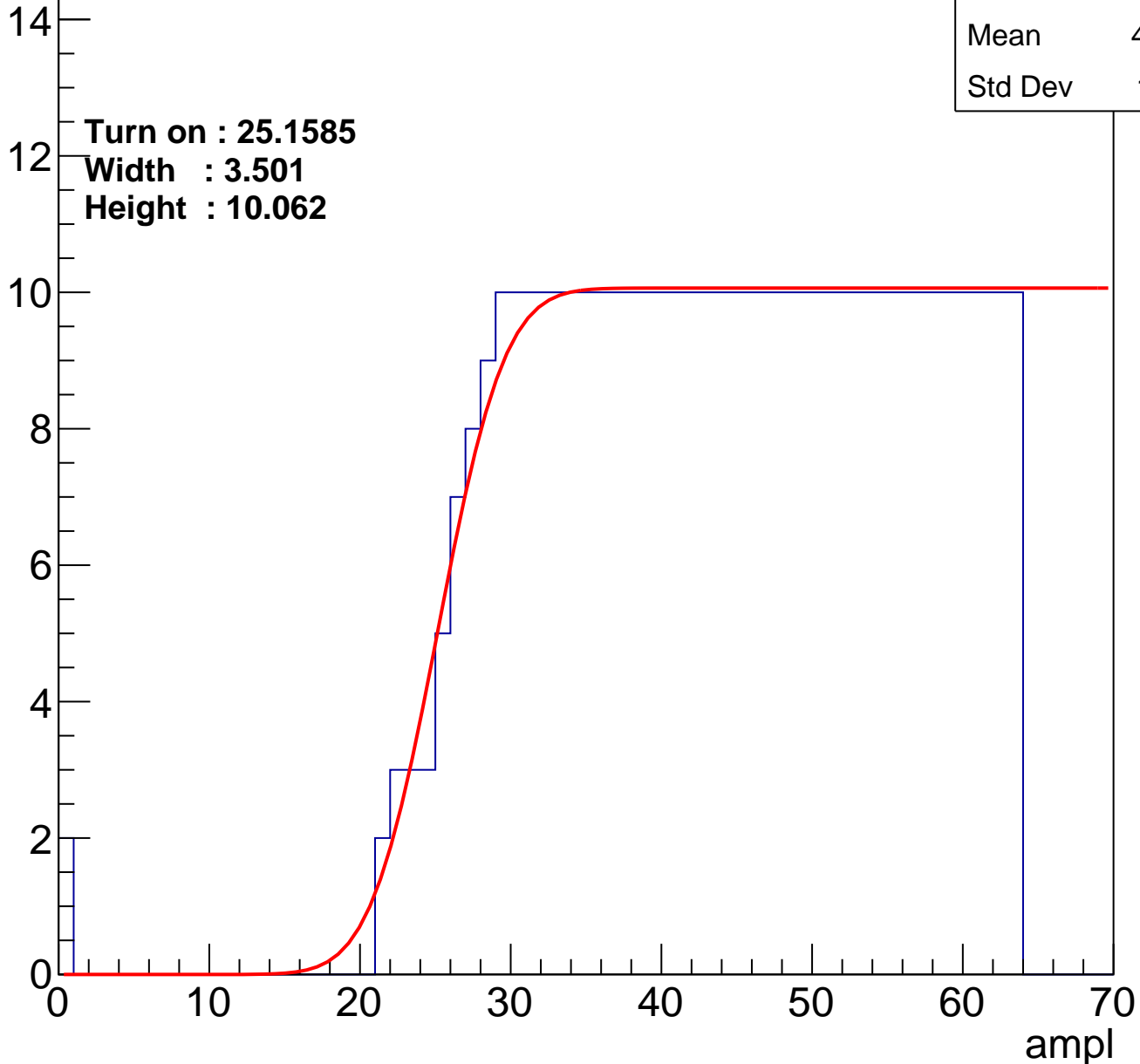
Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 25.1585

Width : 3.501

Height : 10.062

Entry



B1L100S, U21-ch109

calib_packv5_042523_0143.root, FC#4, port A2

Entries	398
Mean	43.38
Std Dev	11.98

Turn on : 25.0310

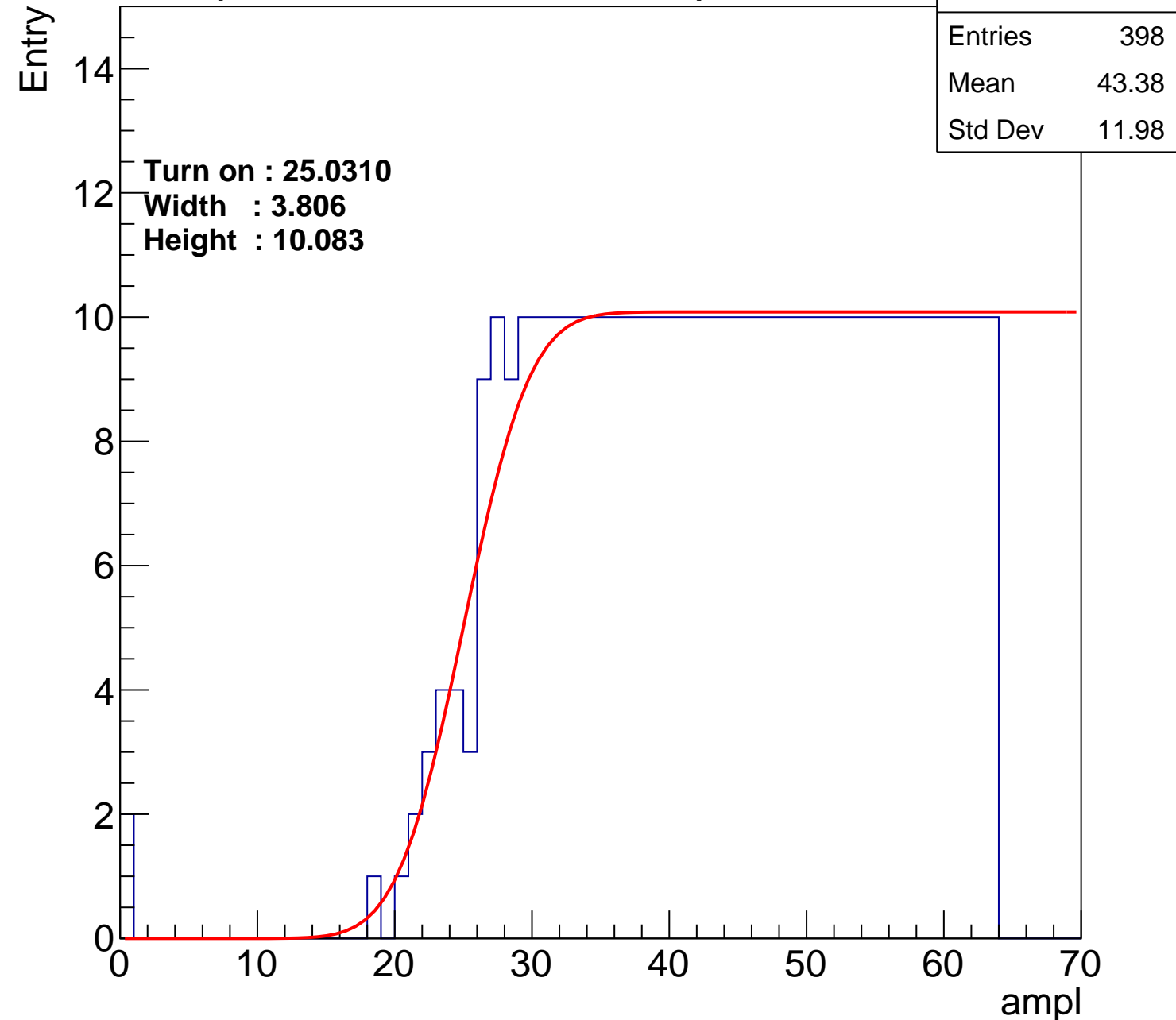
Width : 3.806

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch110

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.55
Std Dev	11.56

Turn on : 27.5542

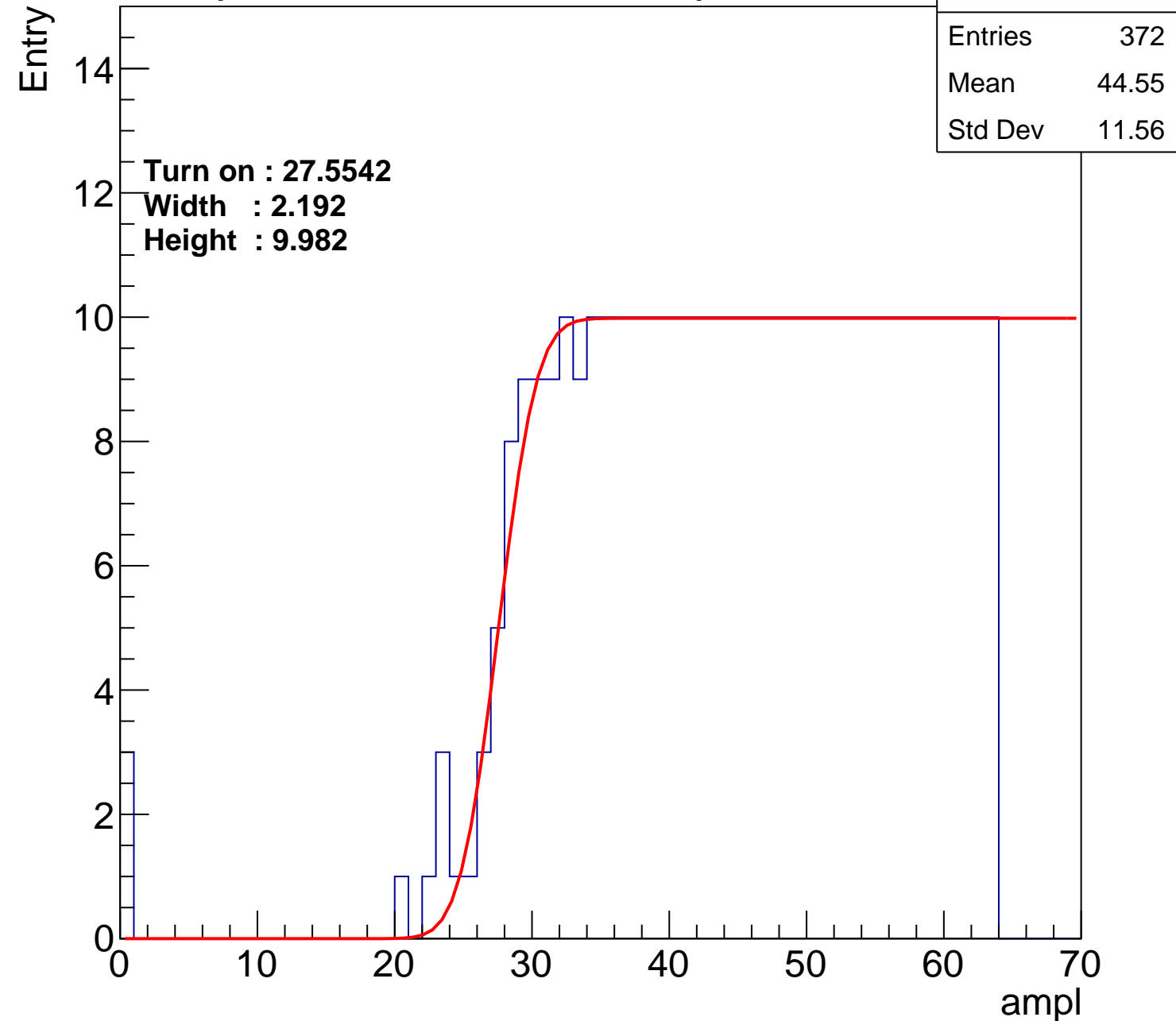
Width : 2.192

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch111

calib_packv5_042523_0143.root, FC#4, port A2

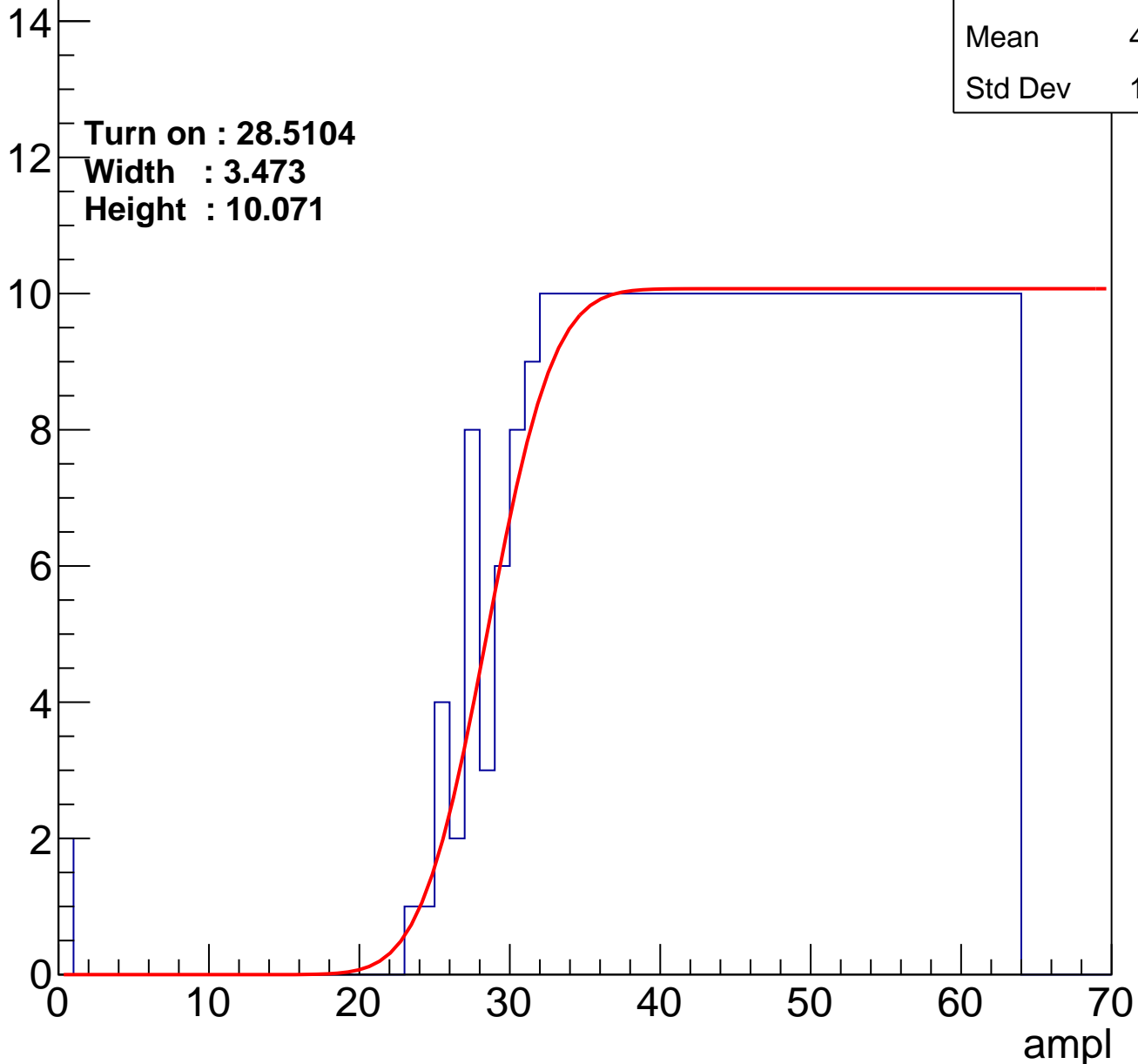
Entries	364
Mean	45.03
Std Dev	11.14

Turn on : 28.5104

Width : 3.473

Height : 10.071

Entry



B1L100S, U21-ch112

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.99
Std Dev	11.73

Turn on : 25.7464

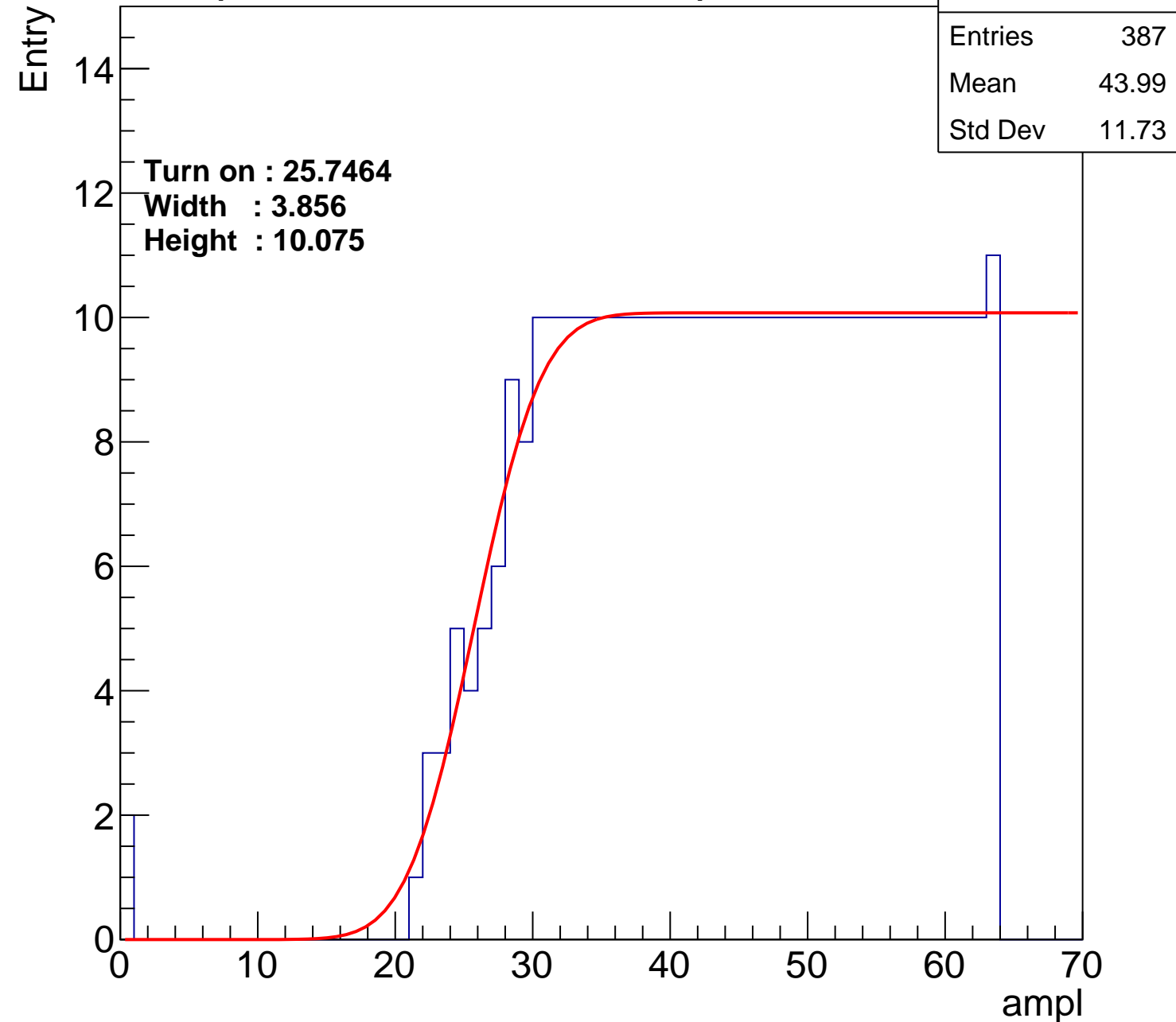
Width : 3.856

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch113

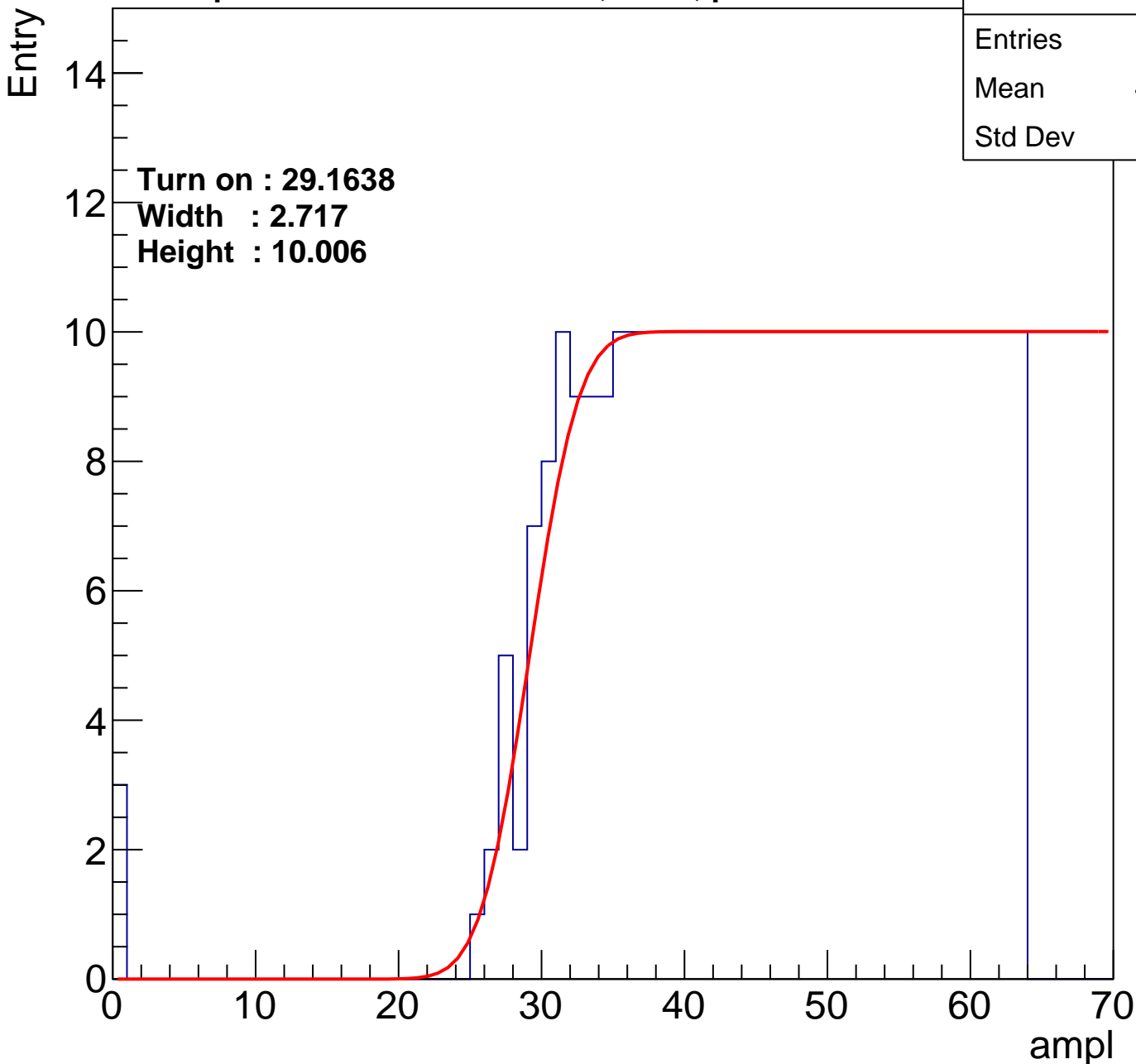
calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.41
Std Dev	11.1

Turn on : 29.1638

Width : 2.717

Height : 10.006



B1L100S, U21-ch114

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.37
Std Dev	11.5

Turn on : 27.0766

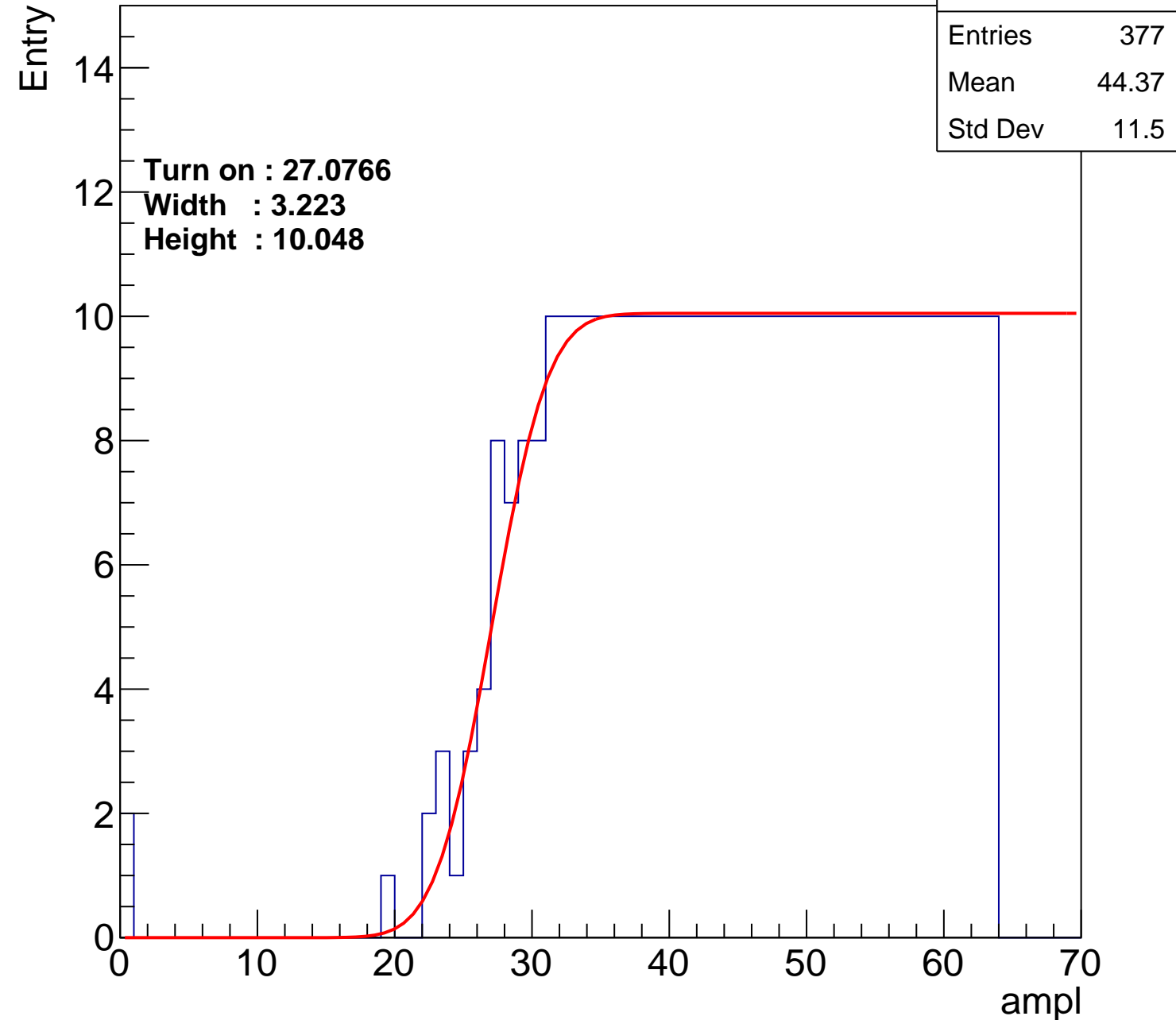
Width : 3.223

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch115

calib_packv5_042523_0143.root, FC#4, port A2

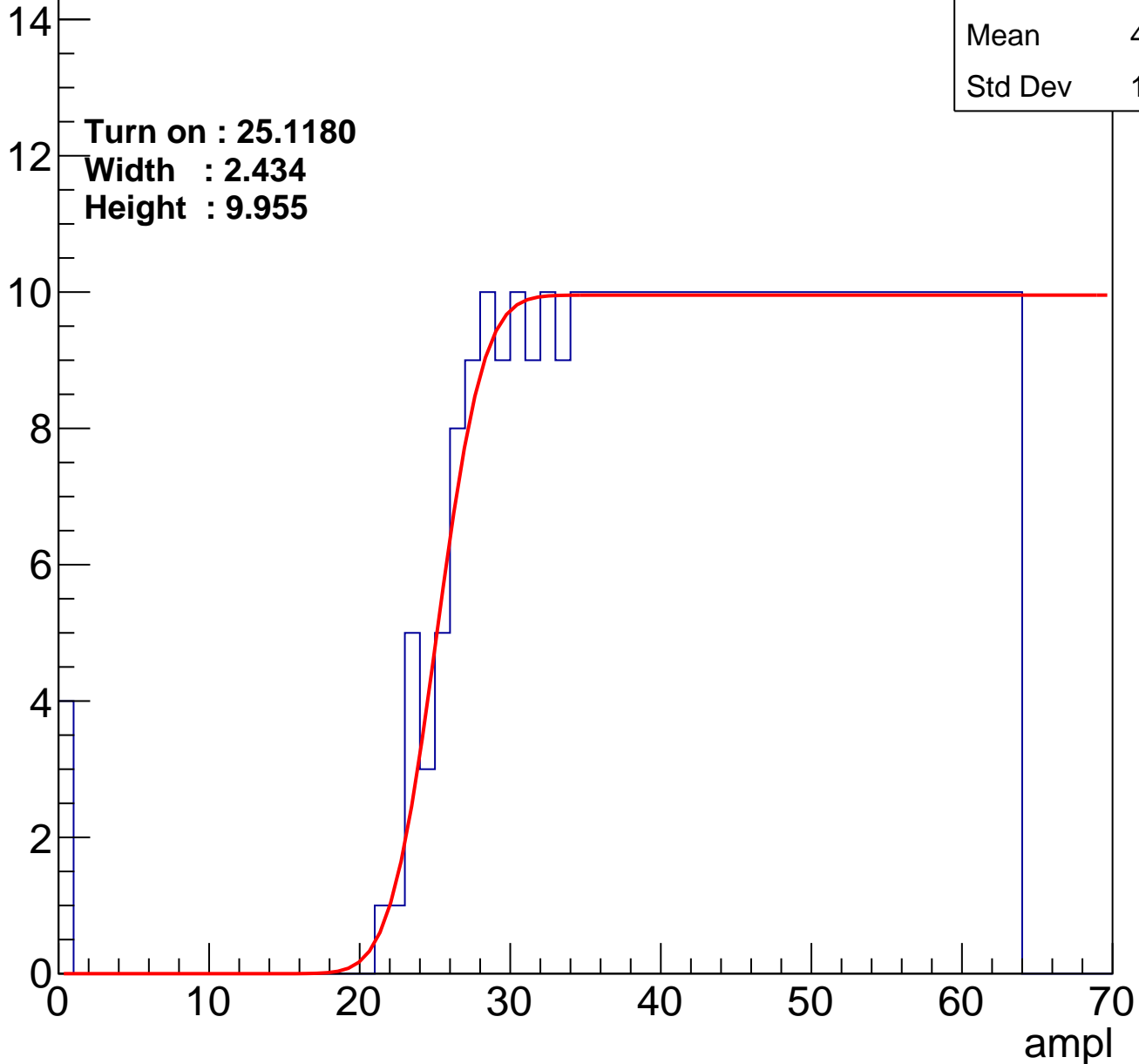
Entries	393
Mean	43.49
Std Dev	12.17

Turn on : 25.1180

Width : 2.434

Height : 9.955

Entry



B1L100S, U21-ch116

calib_packv5_042523_0143.root, FC#4, port A2

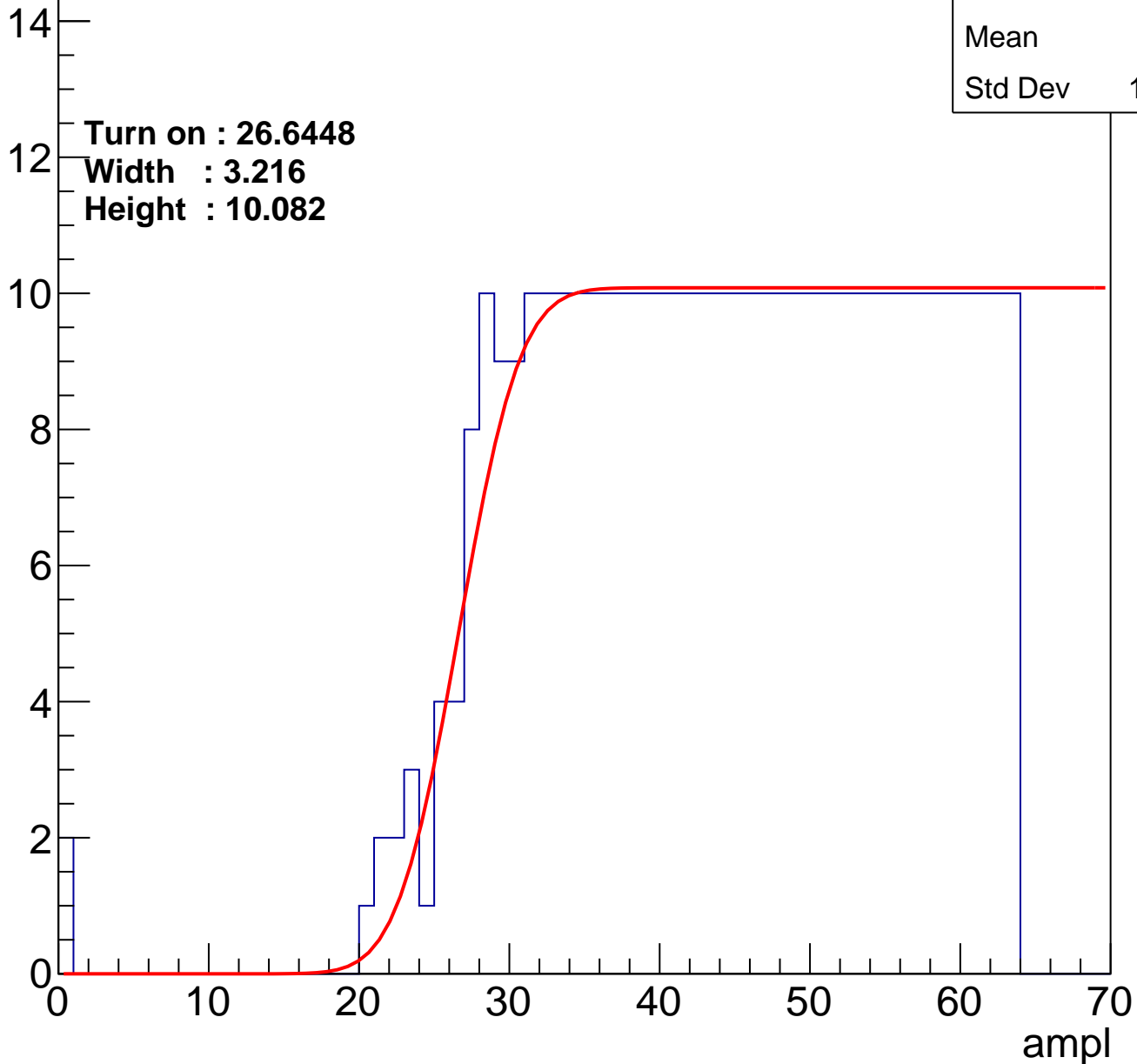
Entries	385
Mean	44
Std Dev	11.68

Turn on : 26.6448

Width : 3.216

Height : 10.082

Entry



B1L100S, U21-ch117

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.72
Std Dev	12.26

Turn on : 26.3346

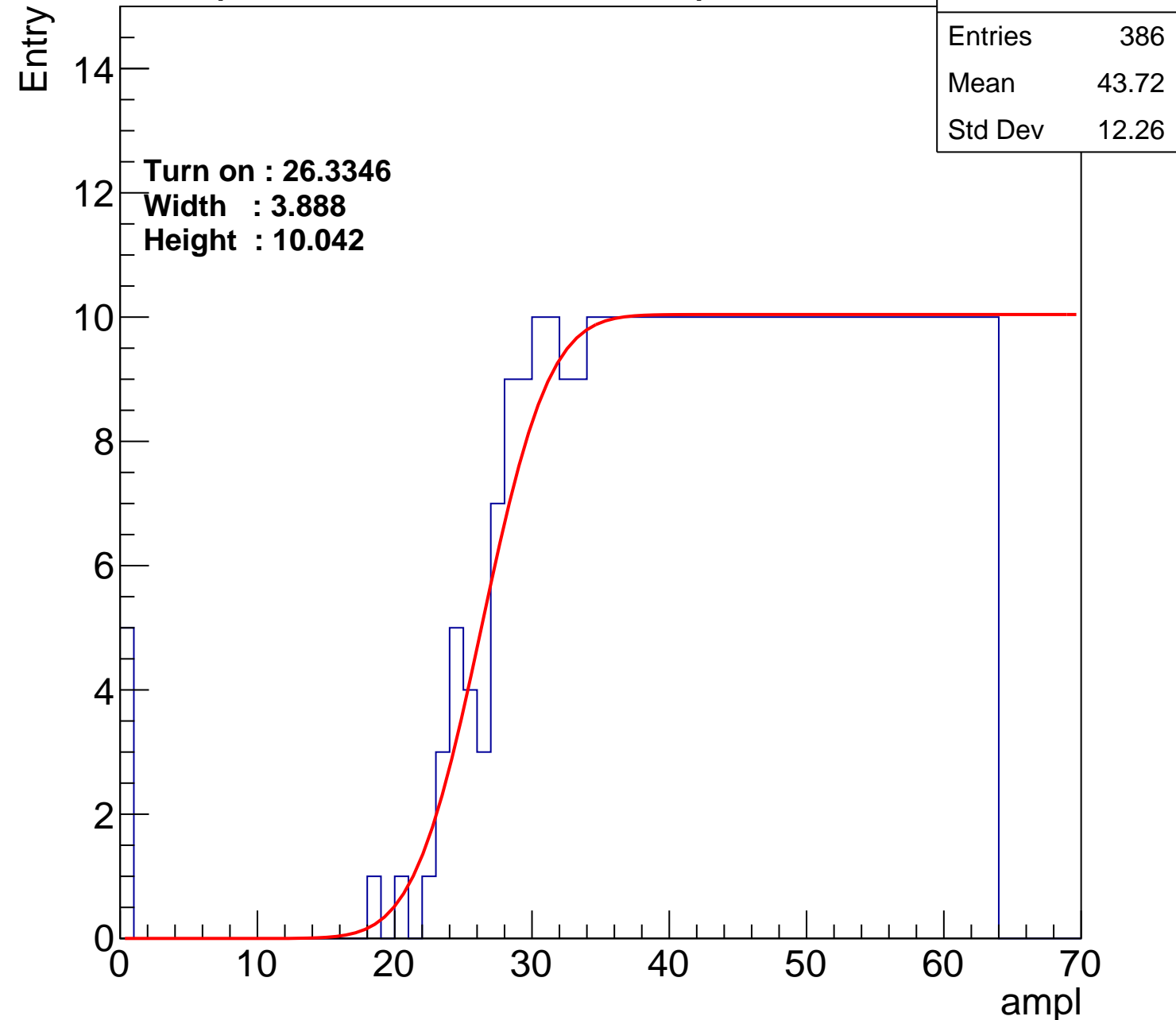
Width : 3.888

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch118

calib_packv5_042523_0143.root, FC#4, port A2

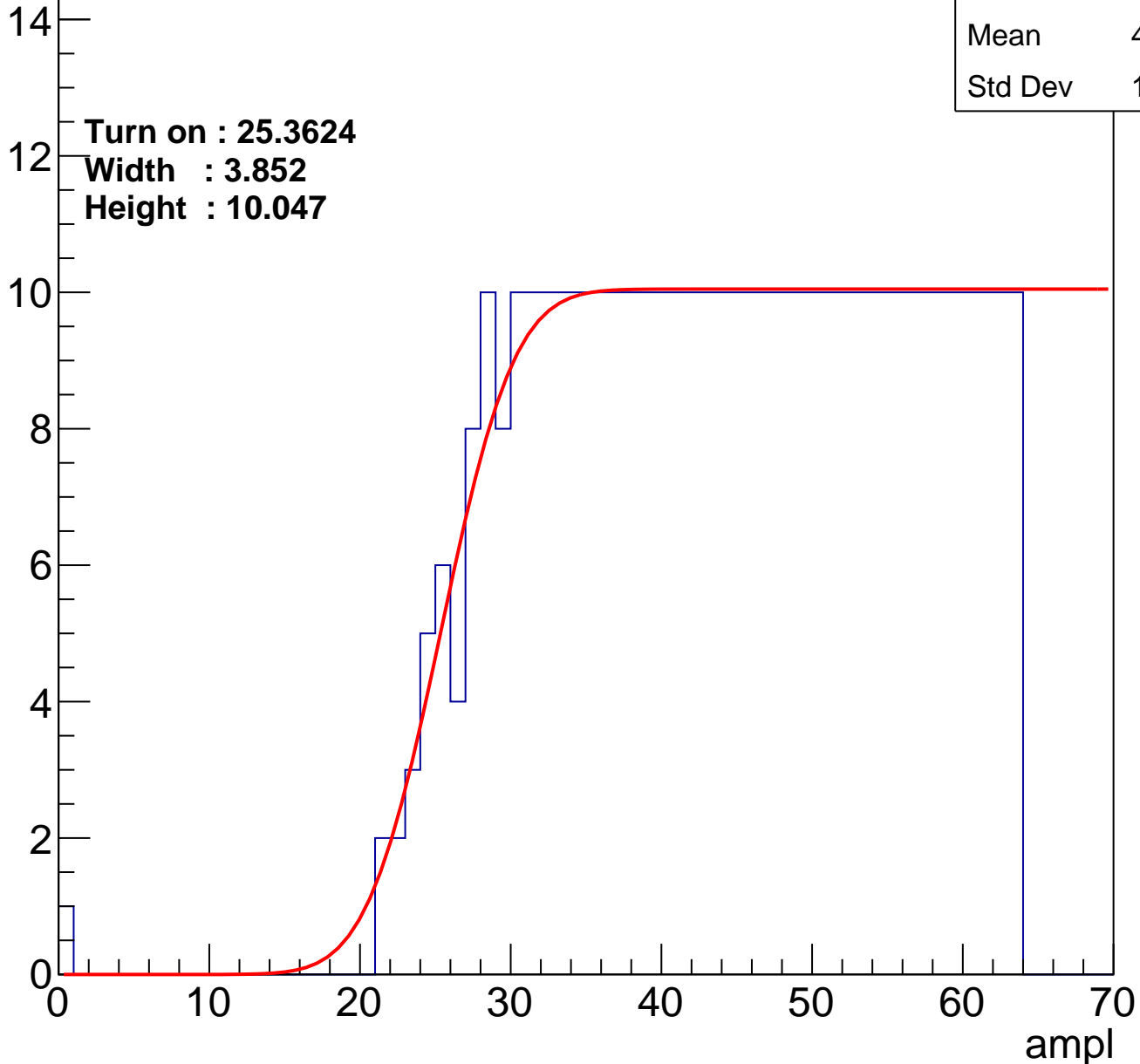
Entries	389
Mean	43.87
Std Dev	11.59

Turn on : 25.3624

Width : 3.852

Height : 10.047

Entry



B1L100S, U21-ch119

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.62
Std Dev	11.18

Turn on : 26.9598

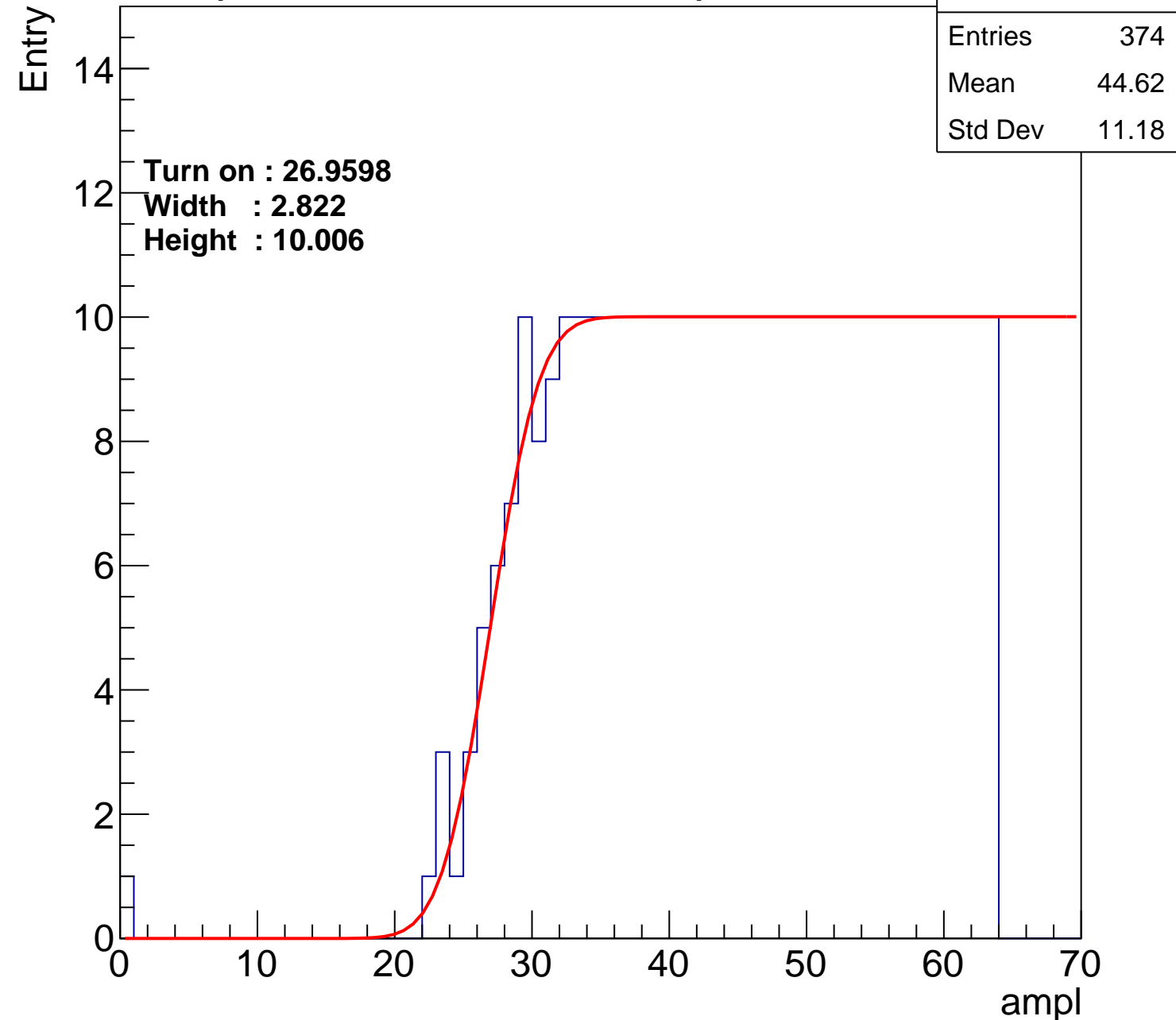
Width : 2.822

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch120

calib_packv5_042523_0143.root, FC#4, port A2

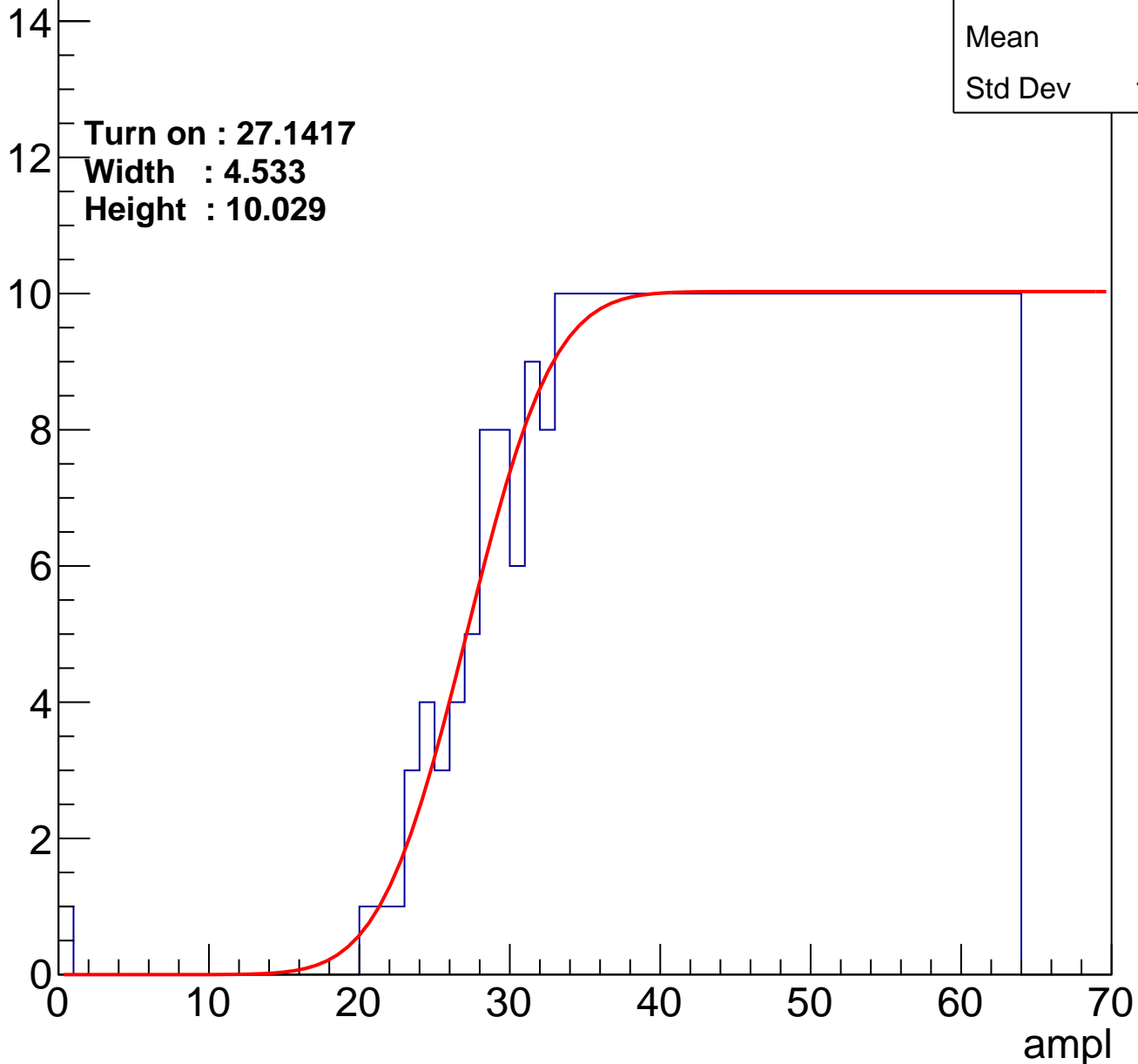
Entries	372
Mean	44.6
Std Dev	11.31

Turn on : 27.1417

Width : 4.533

Height : 10.029

Entry



B1L100S, U21-ch121

calib_packv5_042523_0143.root, FC#4, port A2

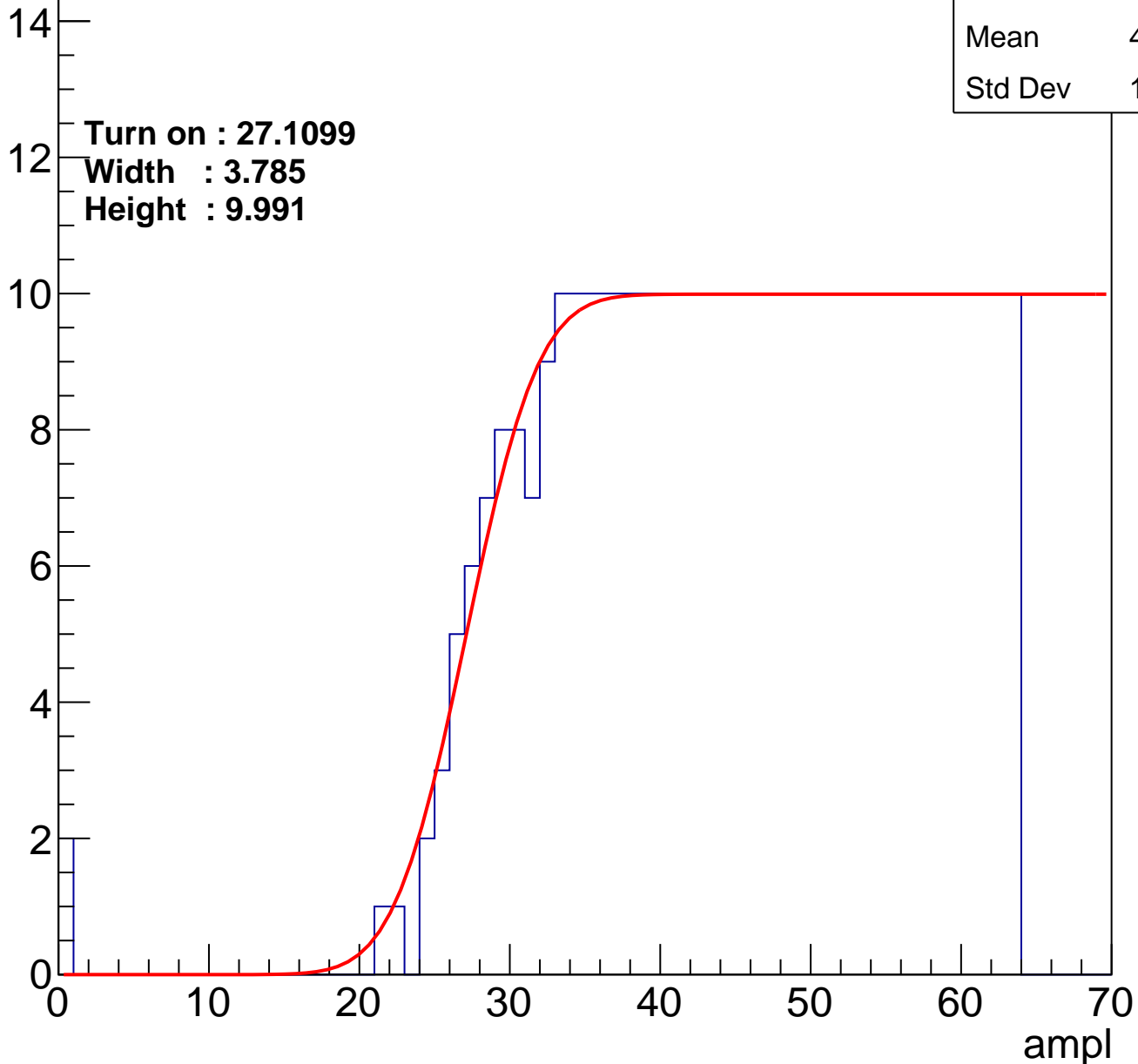
Entries	369
Mean	44.75
Std Dev	11.32

Turn on : 27.1099

Width : 3.785

Height : 9.991

Entry



B1L100S, U21-ch122

calib_packv5_042523_0143.root, FC#4, port A2

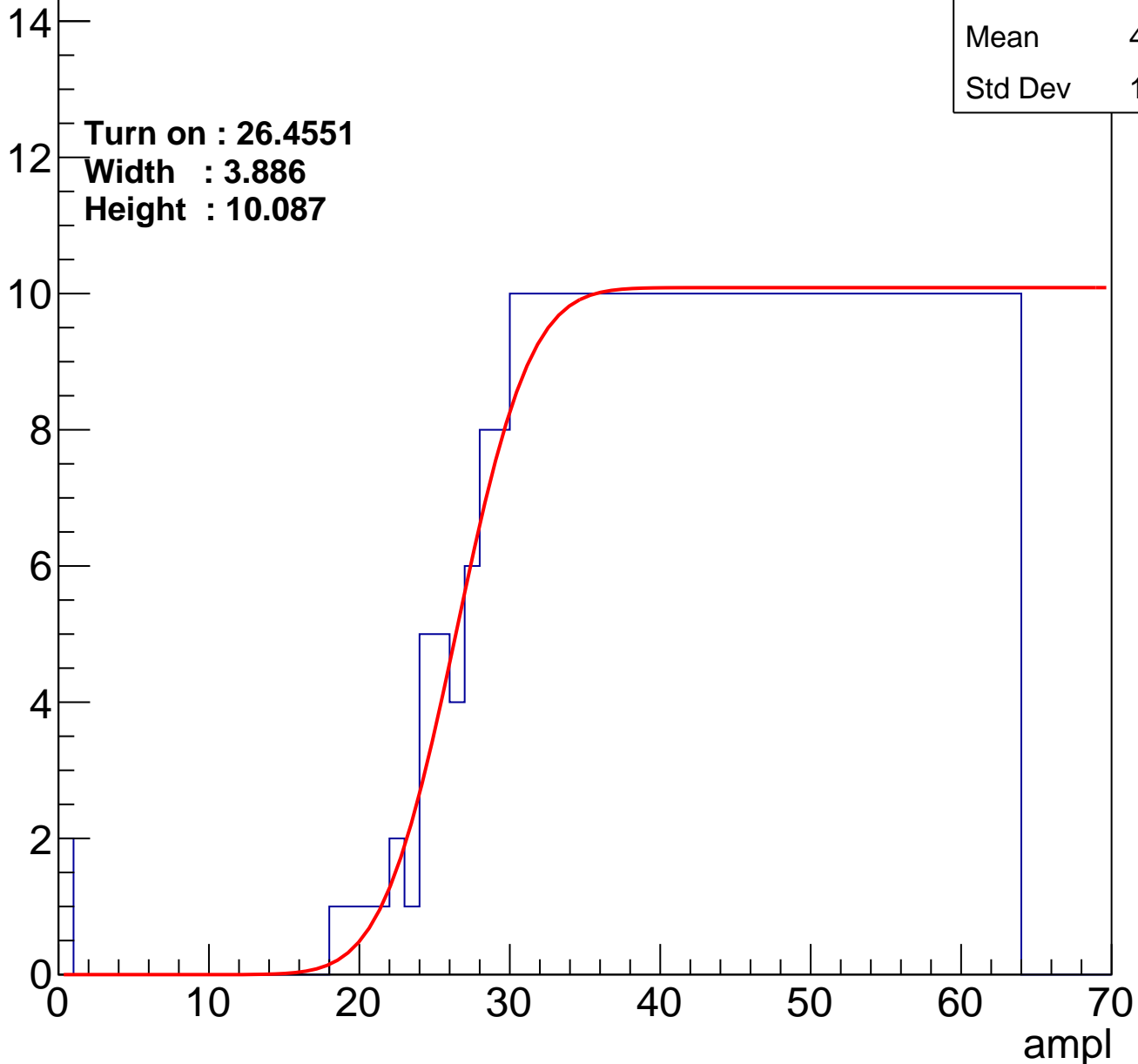
Entries	385
Mean	43.95
Std Dev	11.76

Turn on : 26.4551

Width : 3.886

Height : 10.087

Entry



B1L100S, U21-ch123

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.81
Std Dev	11.84

Turn on : 25.8185

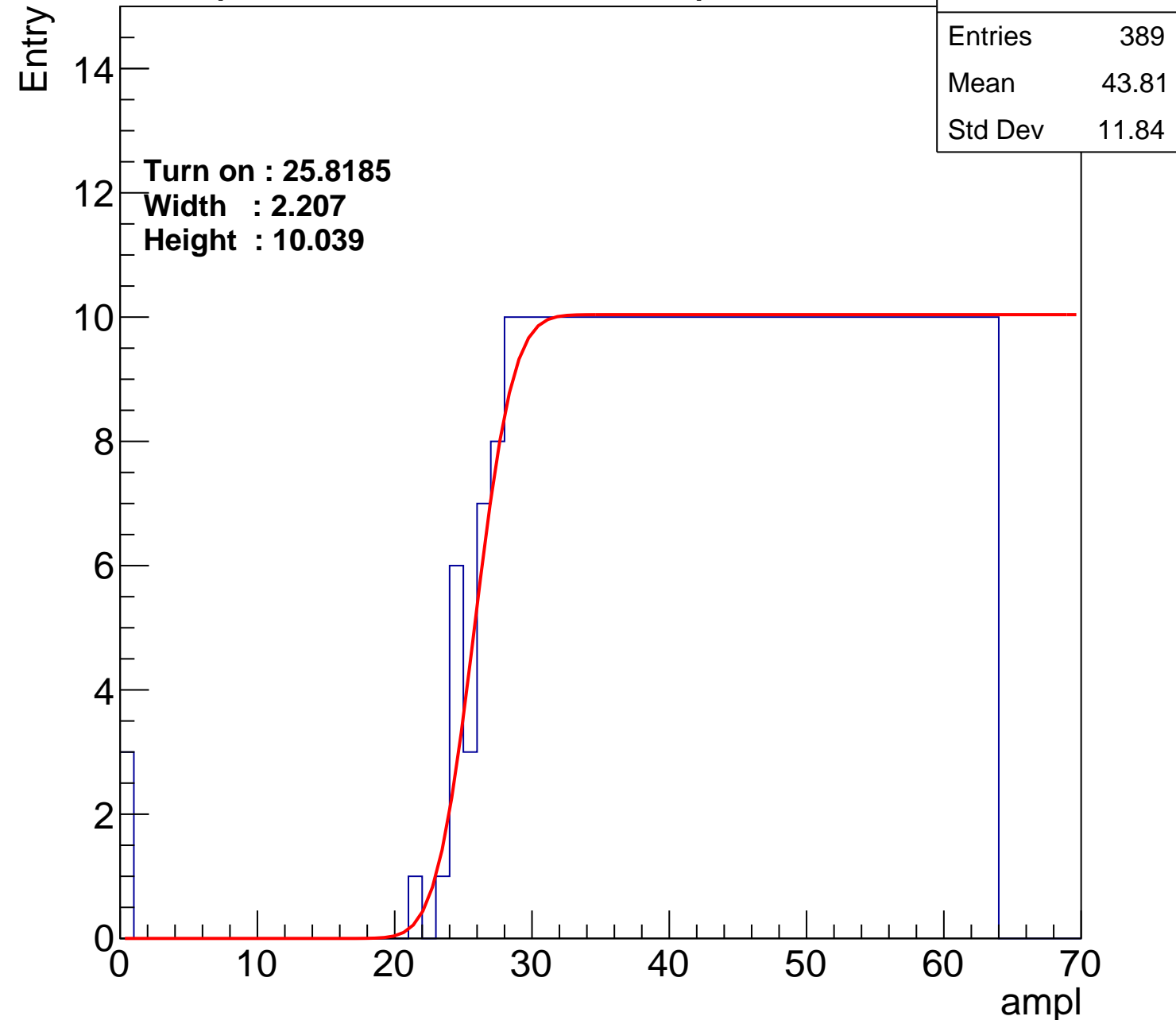
Width : 2.207

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch124

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.71
Std Dev	12.22

Turn on : 25.9725

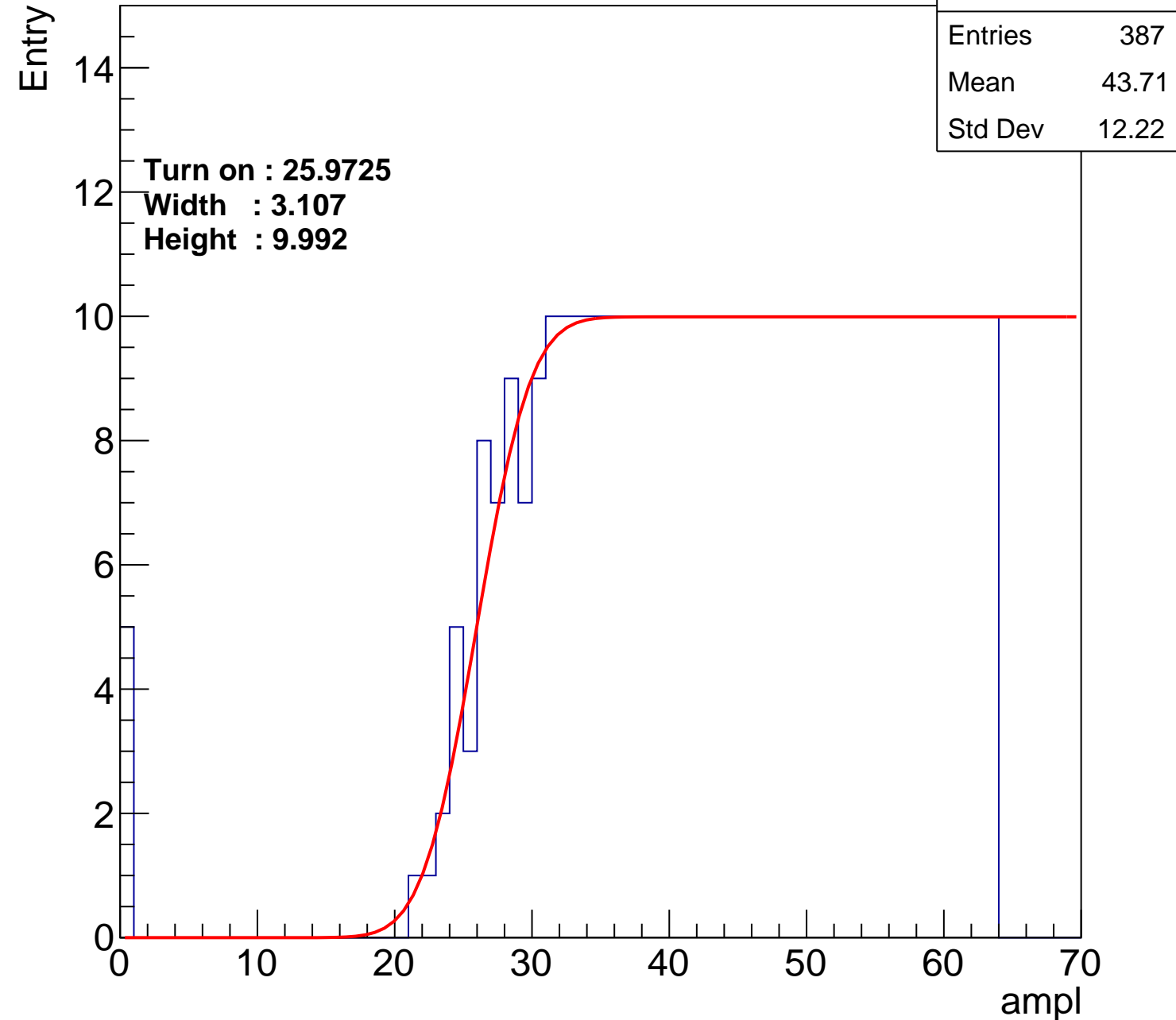
Width : 3.107

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch125

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.57
Std Dev	11.83

Turn on : 24.8557

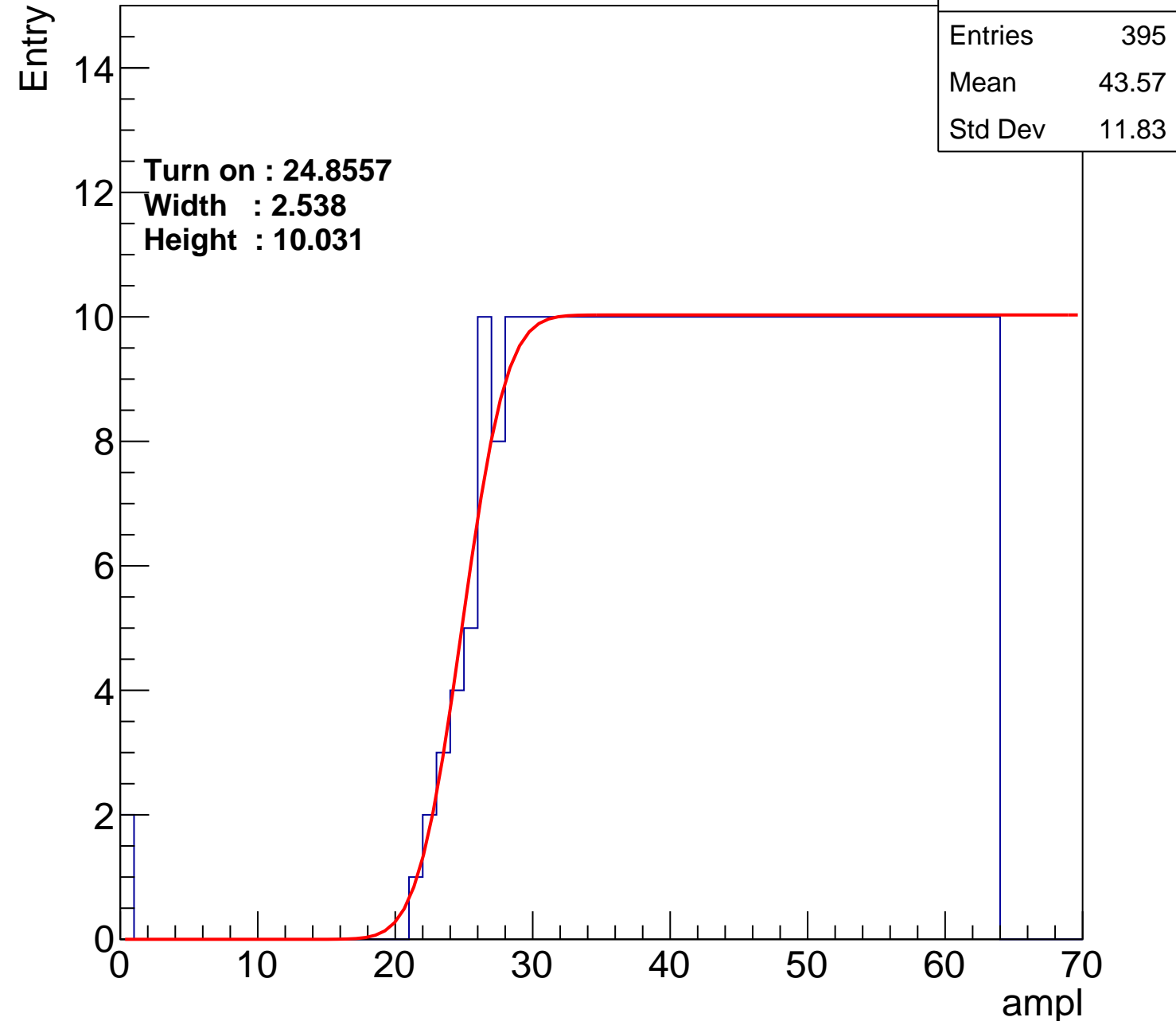
Width : 2.538

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch126

calib_packv5_042523_0143.root, FC#4, port A2

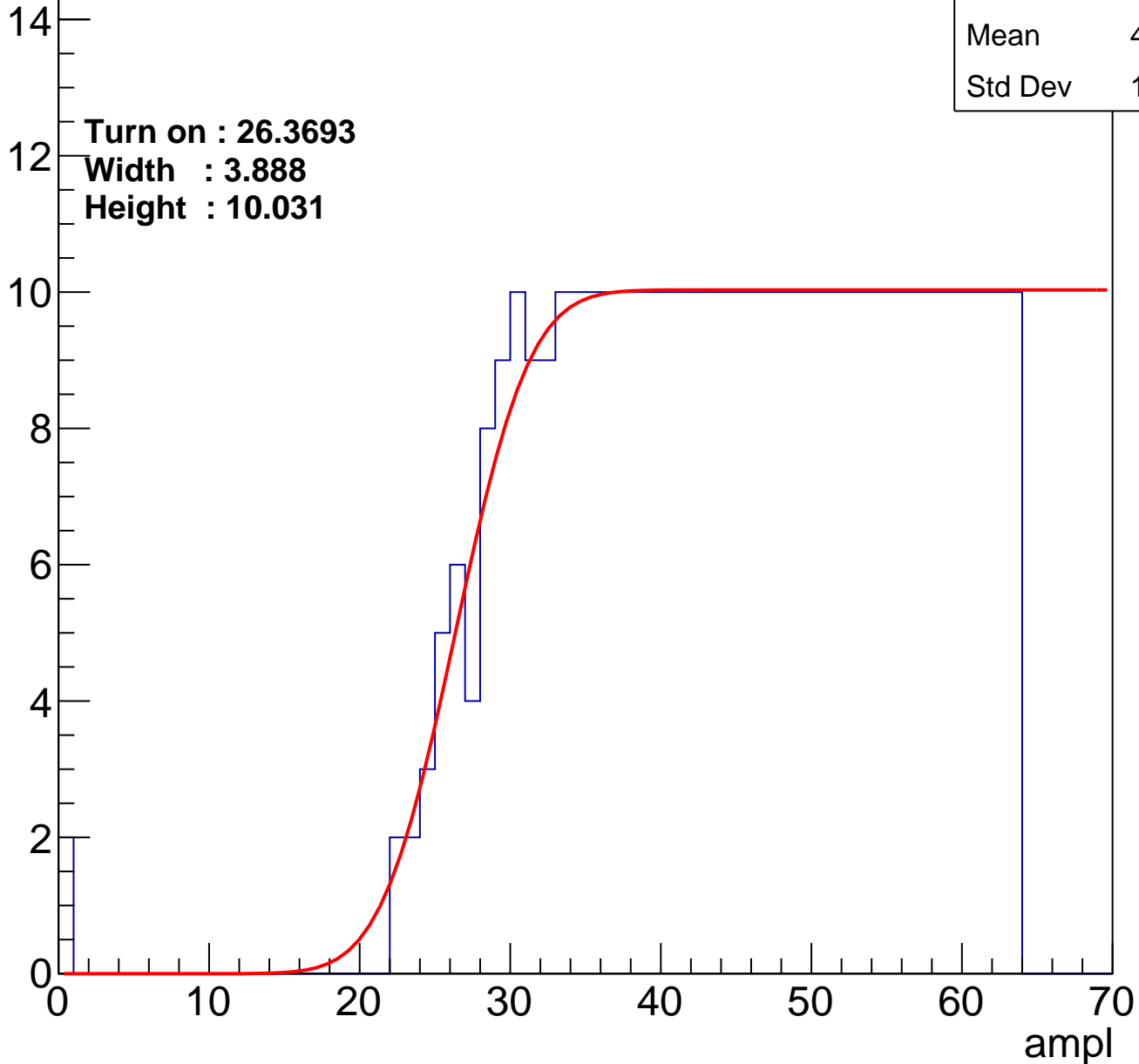
Entries	379
Mean	44.28
Std Dev	11.53

Turn on : 26.3693

Width : 3.888

Height : 10.031

Entry



B1L100S, U21-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.33
Std Dev	11.82

Turn on : 26.7911

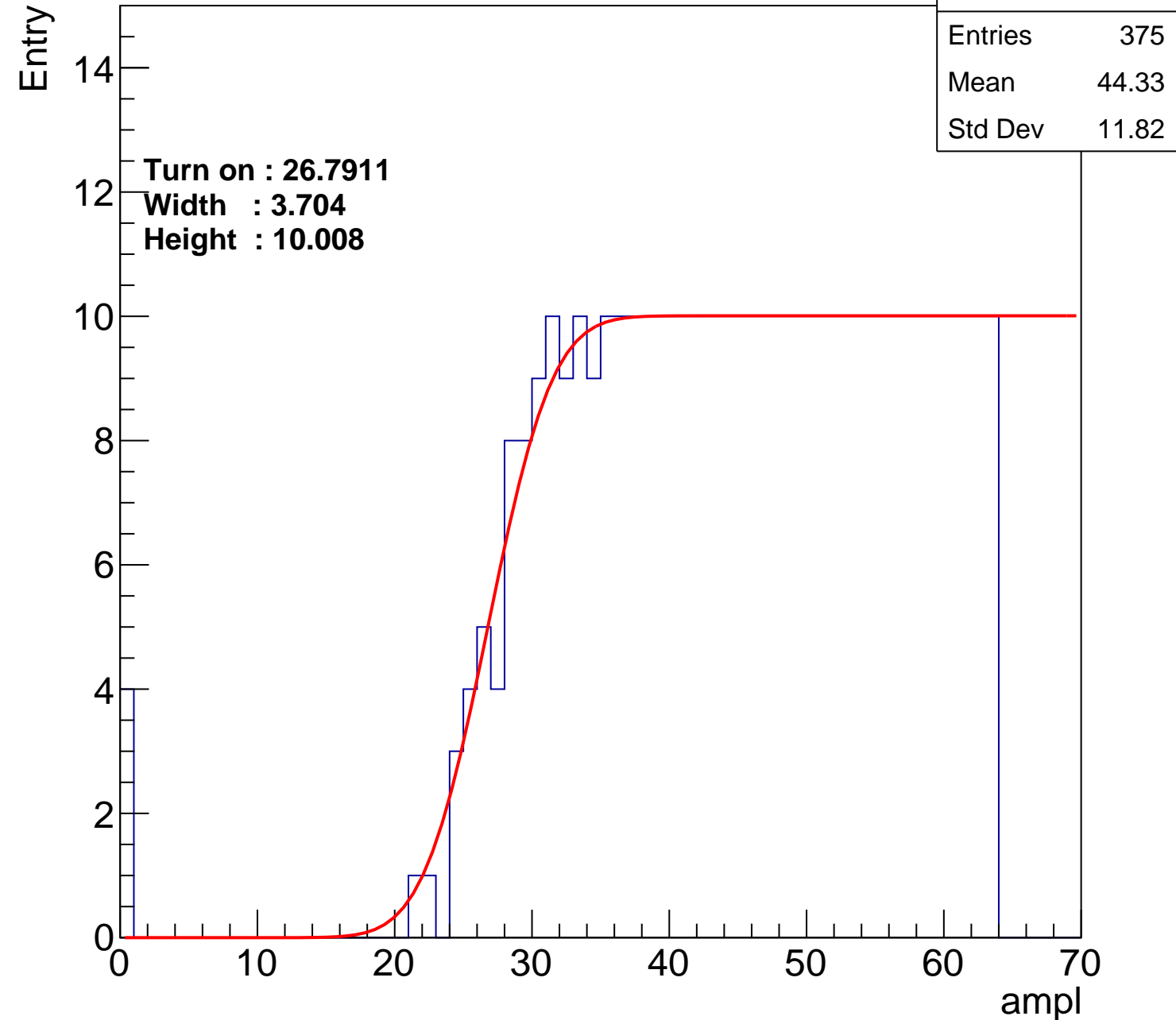
Width : 3.704

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U21-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.33
Std Dev	11.82

Turn on : 26.7911

Width : 3.704

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl

