



# B1L100S, U15-ch0

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	396
Mean	43.41
Std Dev	12.02

**Turn on : 24.7640**

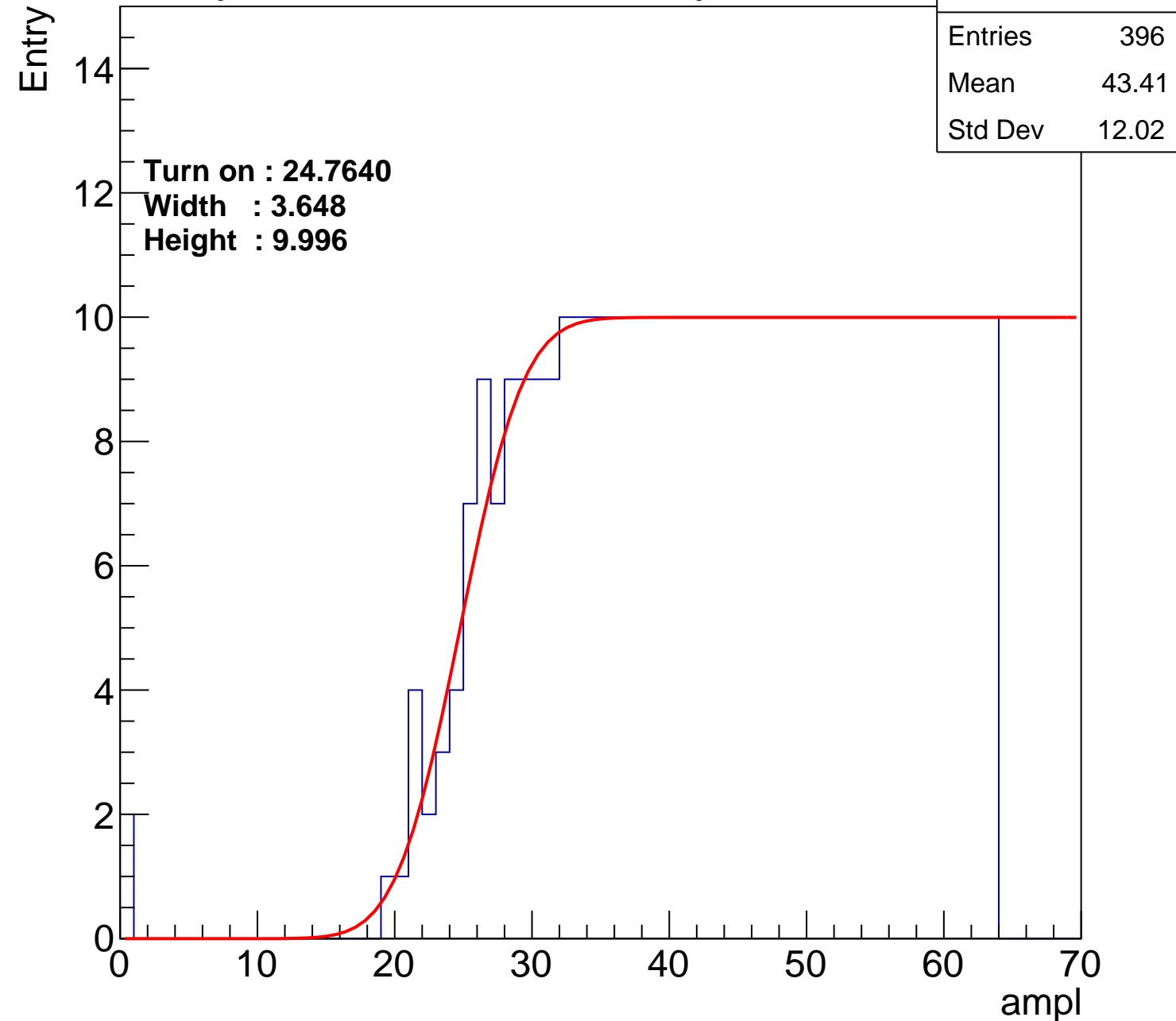
**Width : 3.648**

**Height : 9.996**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch1

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	390
Mean	43.68
Std Dev	11.98

Turn on : 25.1772

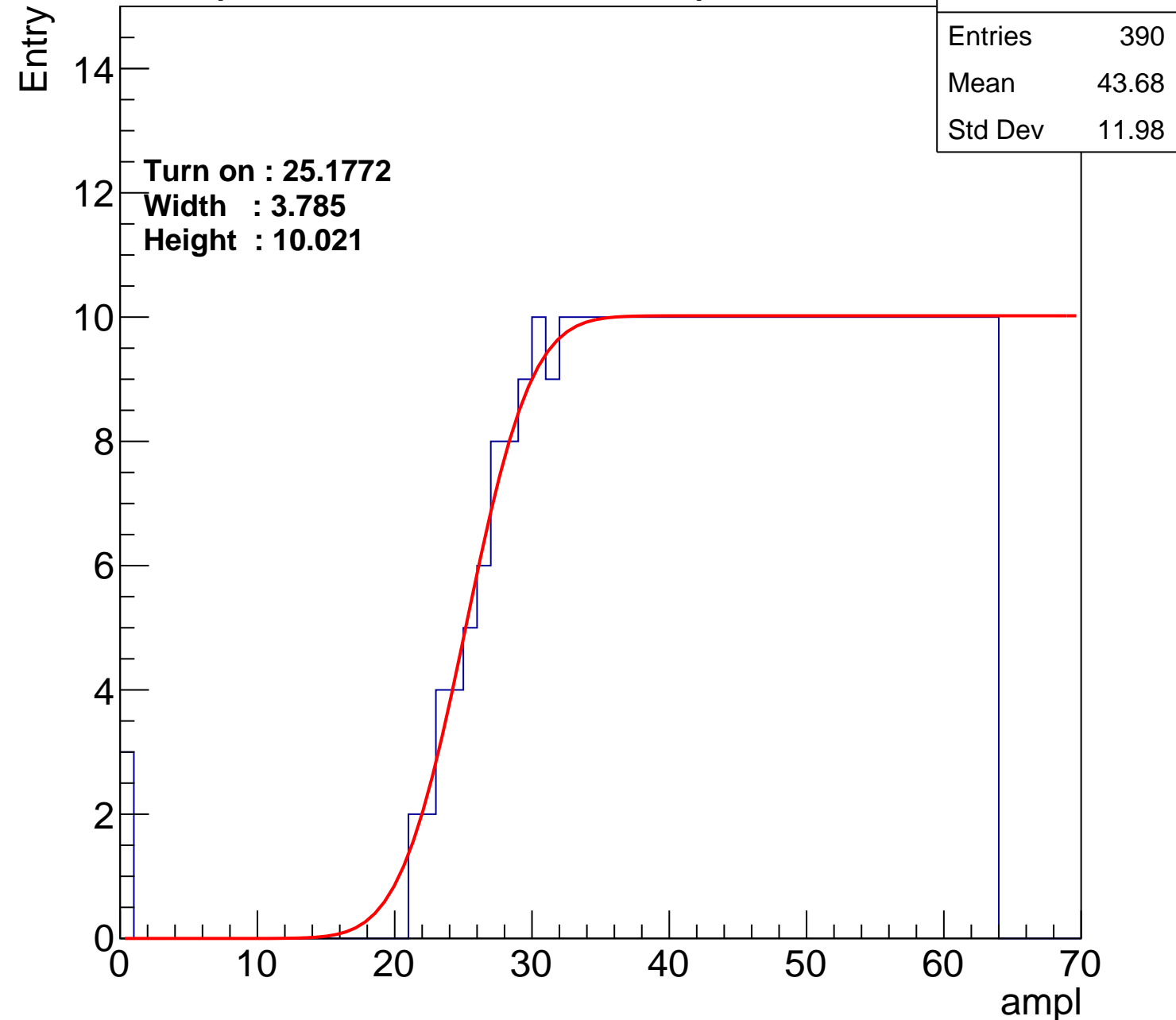
Width : 3.785

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch2

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	43.94
Std Dev	12.37

Turn on : 26.8921

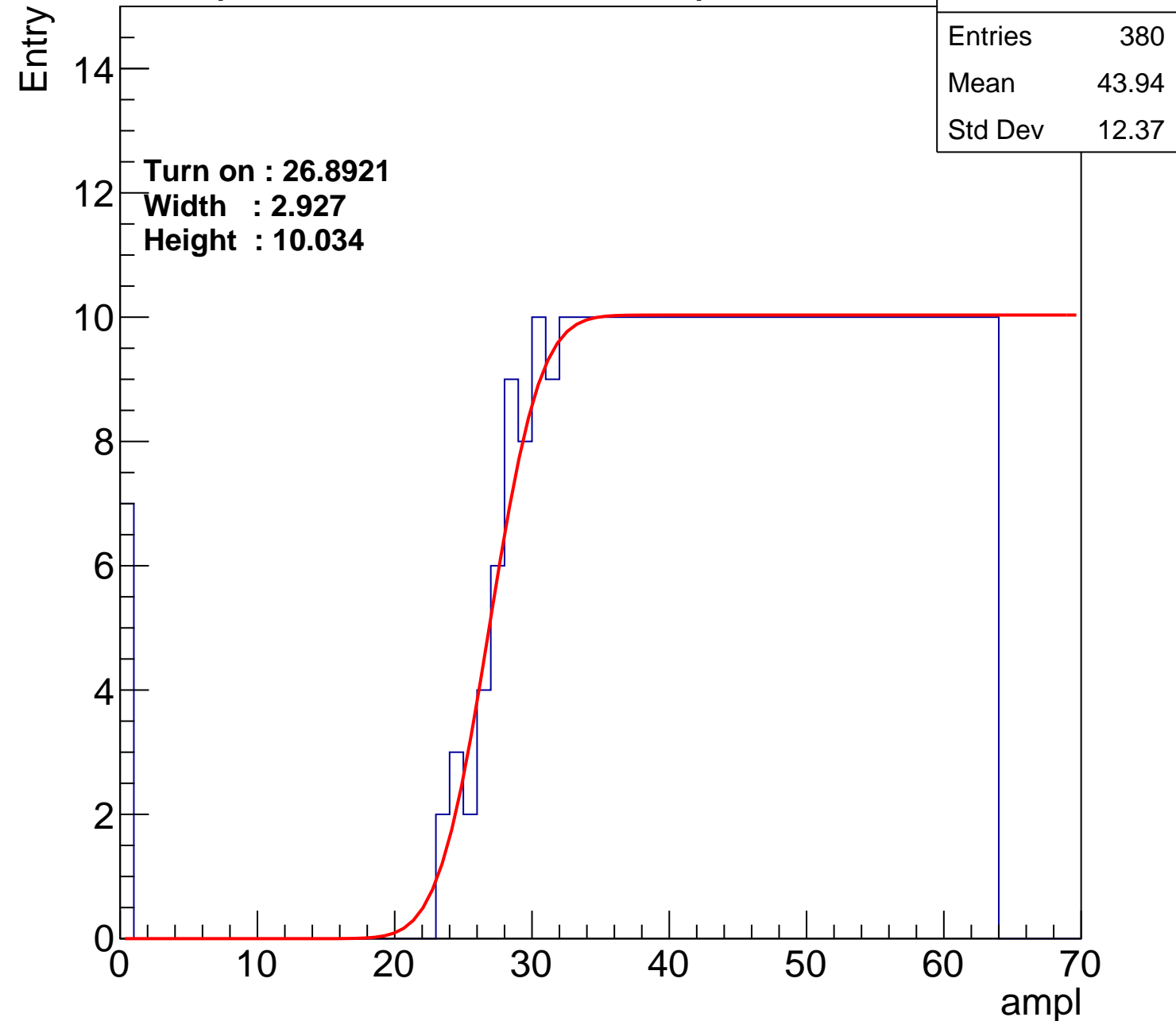
Width : 2.927

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch3

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	44.03
Std Dev	11.87

**Turn on : 26.1940**

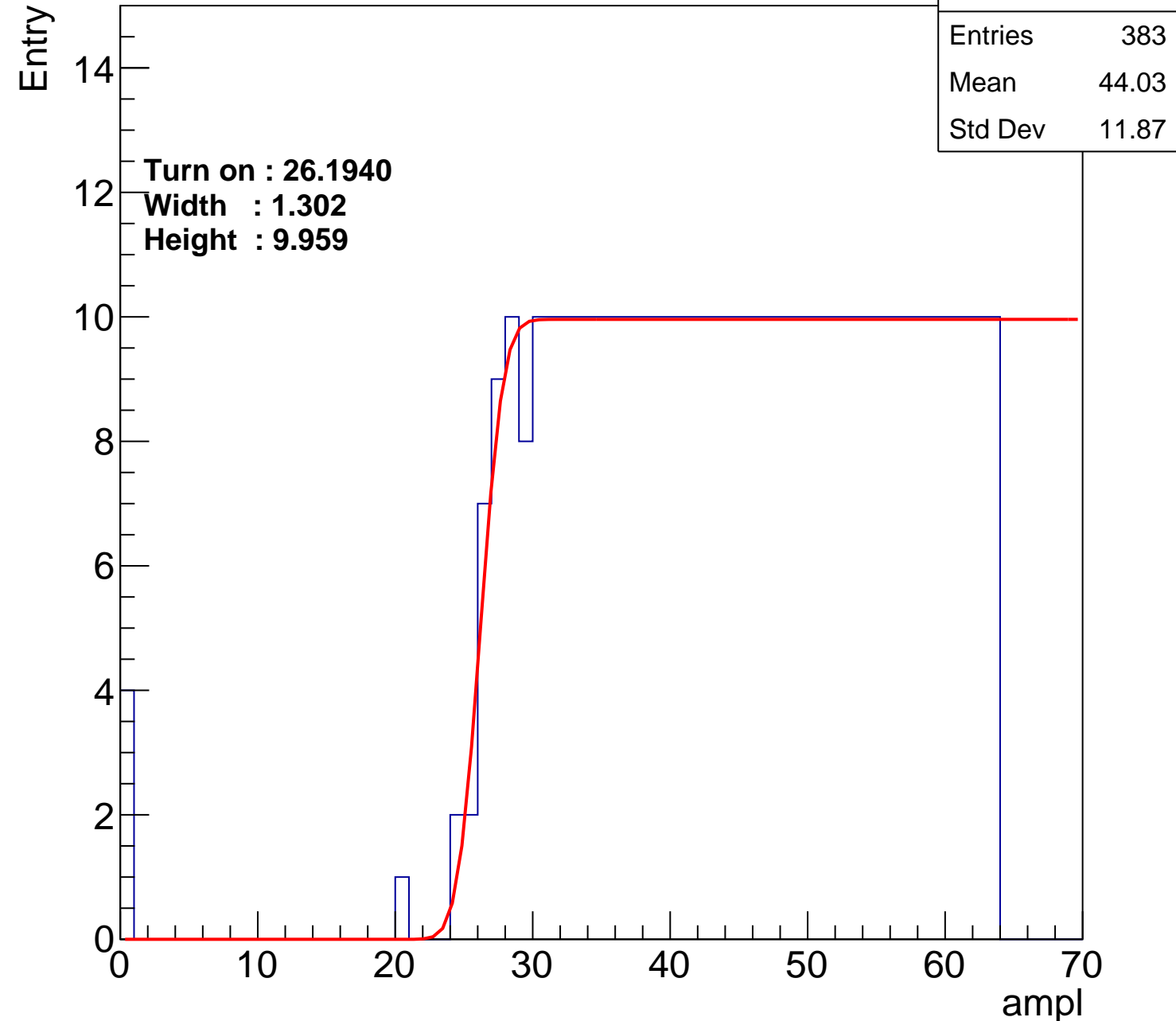
**Width : 1.302**

**Height : 9.959**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch4

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.36
Std Dev	11.29

Turn on : 26.1454

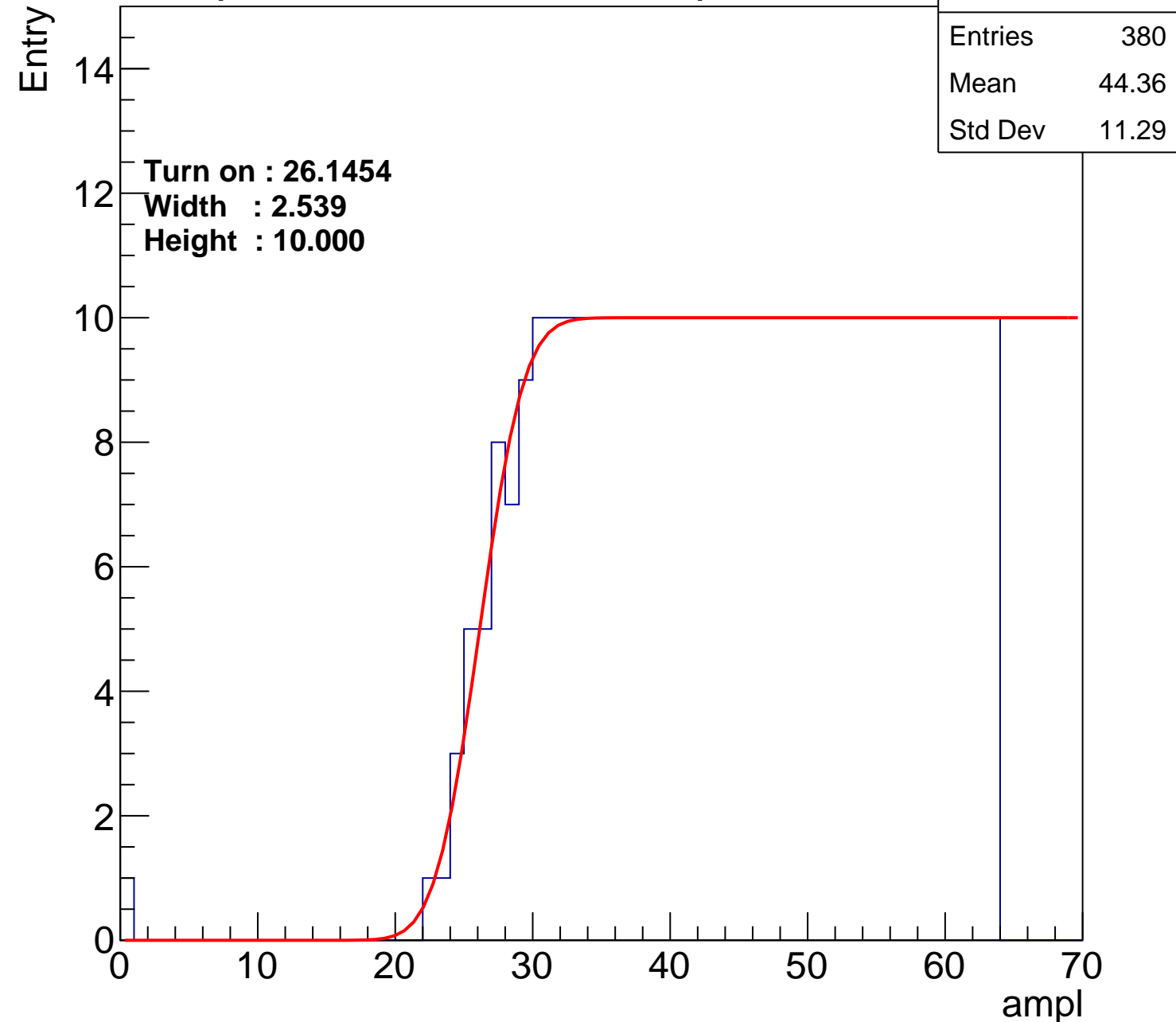
Width : 2.539

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch5

calib\_packv5\_042523\_0143.root, FC#4, port A2

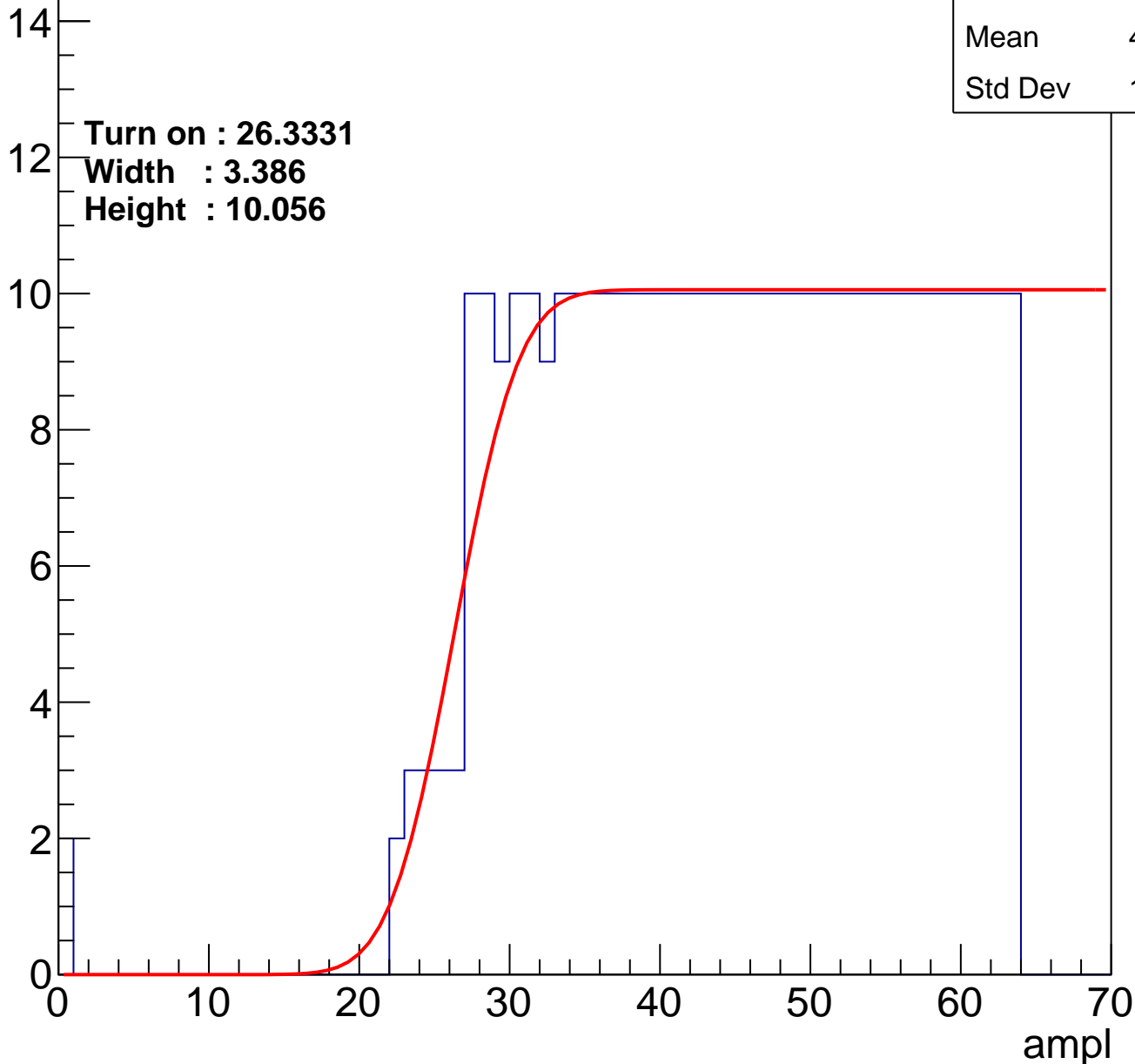
Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 26.3331

Width : 3.386

Height : 10.056

Entry



# B1L100S, U15-ch6

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	423
Mean	41.99
Std Dev	13.02

Turn on : 23.1070

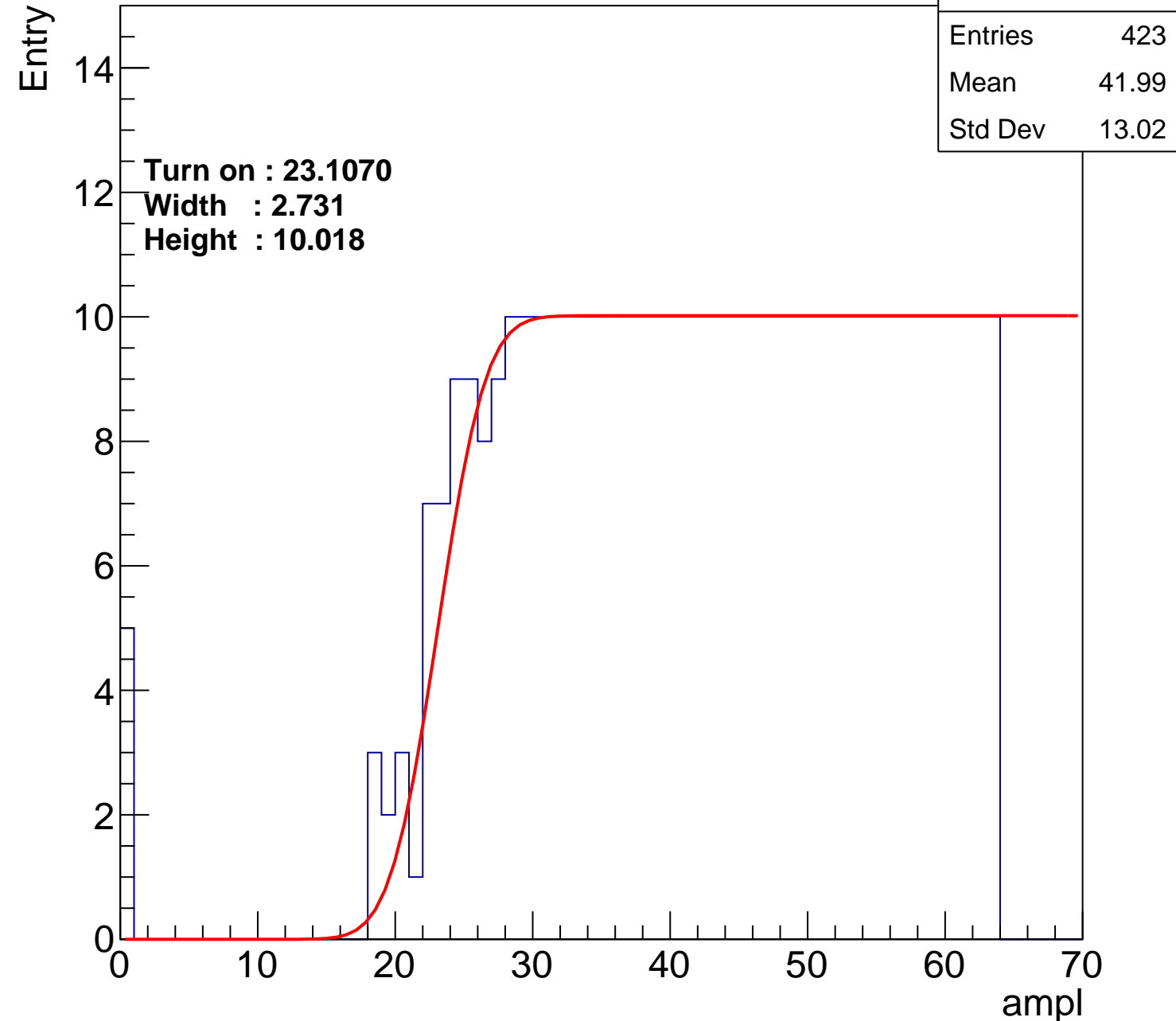
Width : 2.731

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch7

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.8612

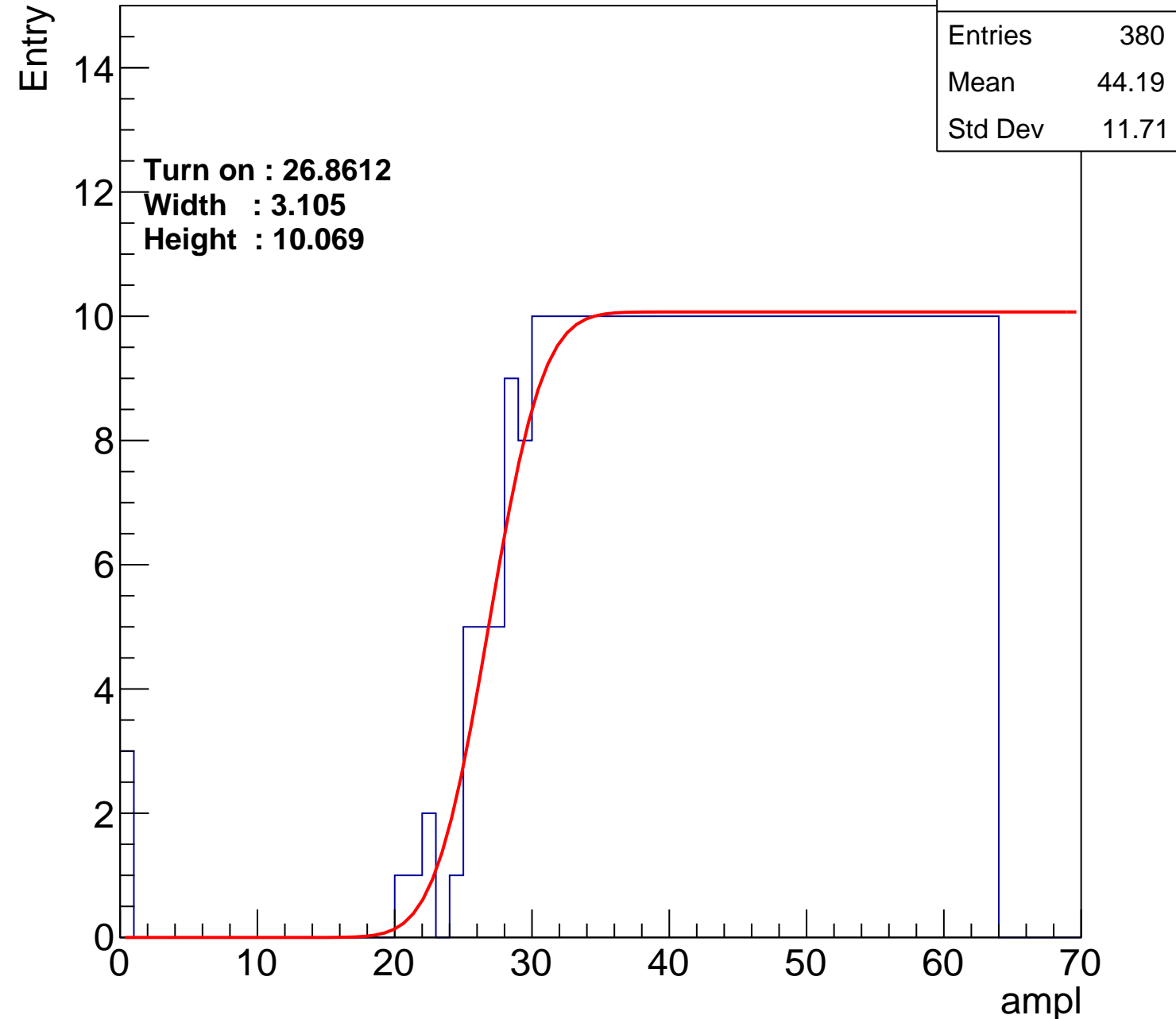
Width : 3.105

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch8

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	400
Mean	43.02
Std Dev	12.73

**Turn on : 24.8180**

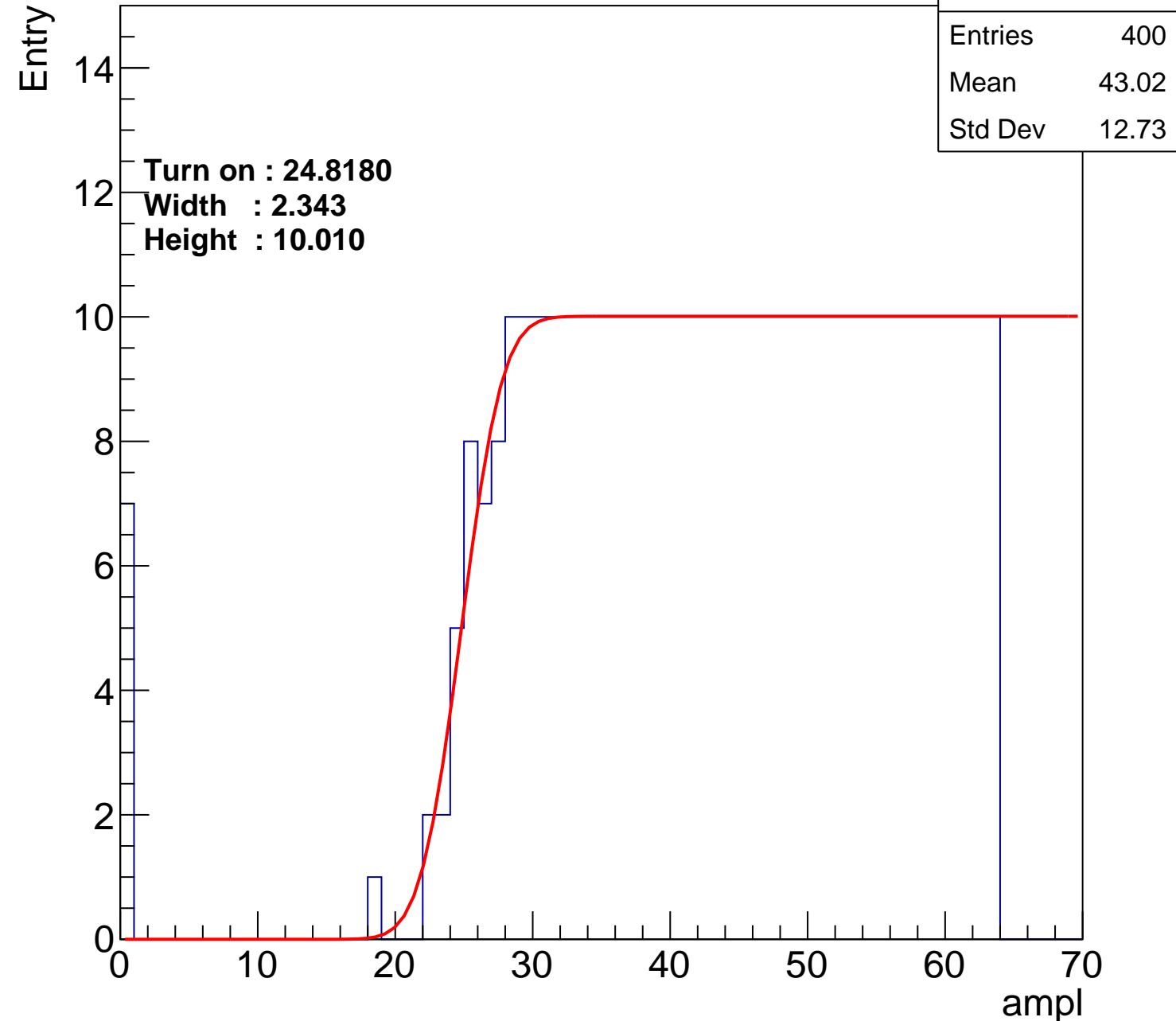
**Width : 2.343**

**Height : 10.010**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch9

calib\_packv5\_042523\_0143.root, FC#4, port A2

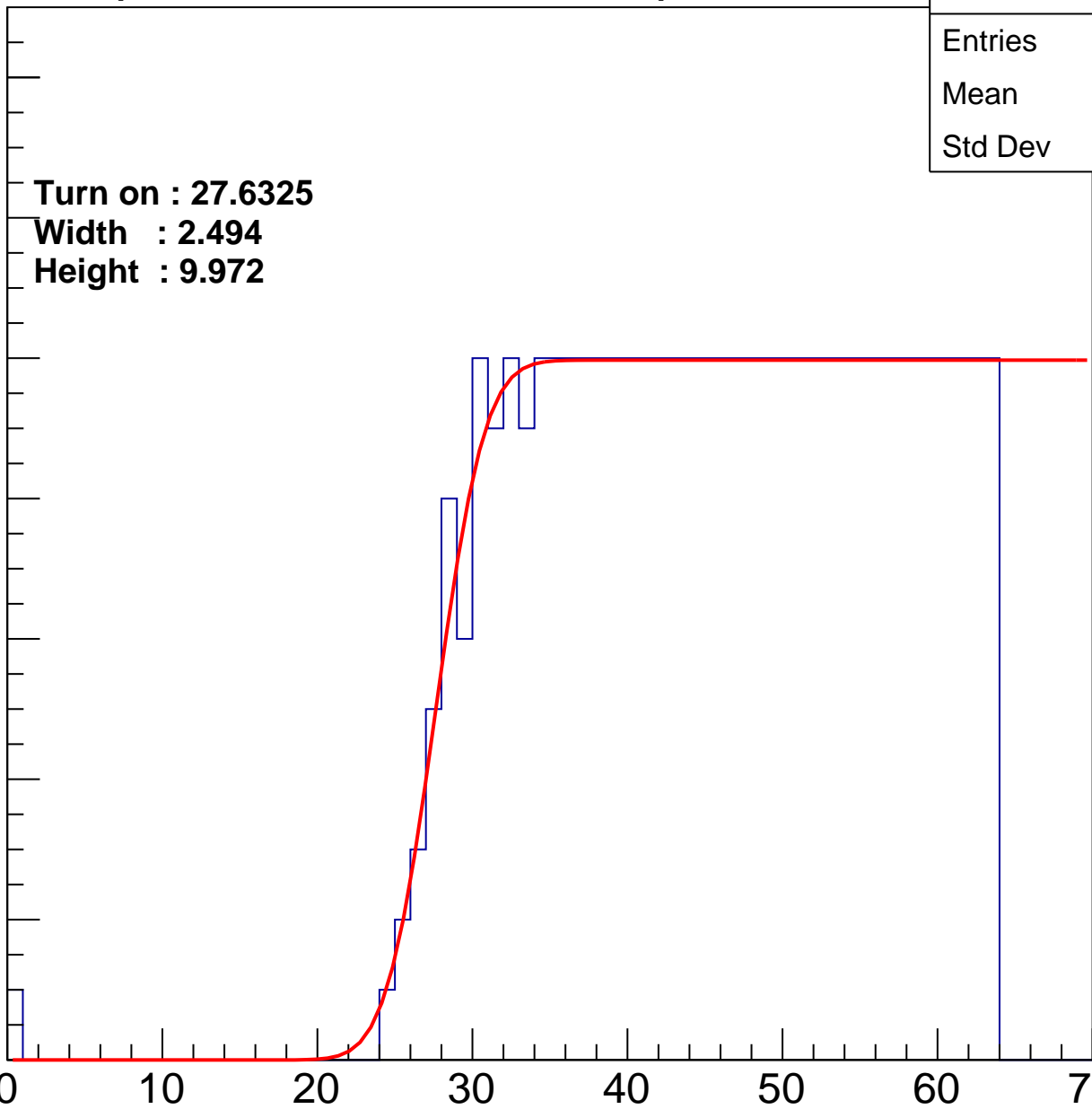
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.6325  
Width : 2.494  
Height : 9.972

Entries	364
Mean	45.14
Std Dev	10.86

ampl



# B1L100S, U15-ch10

calib\_packv5\_042523\_0143.root, FC#4, port A2

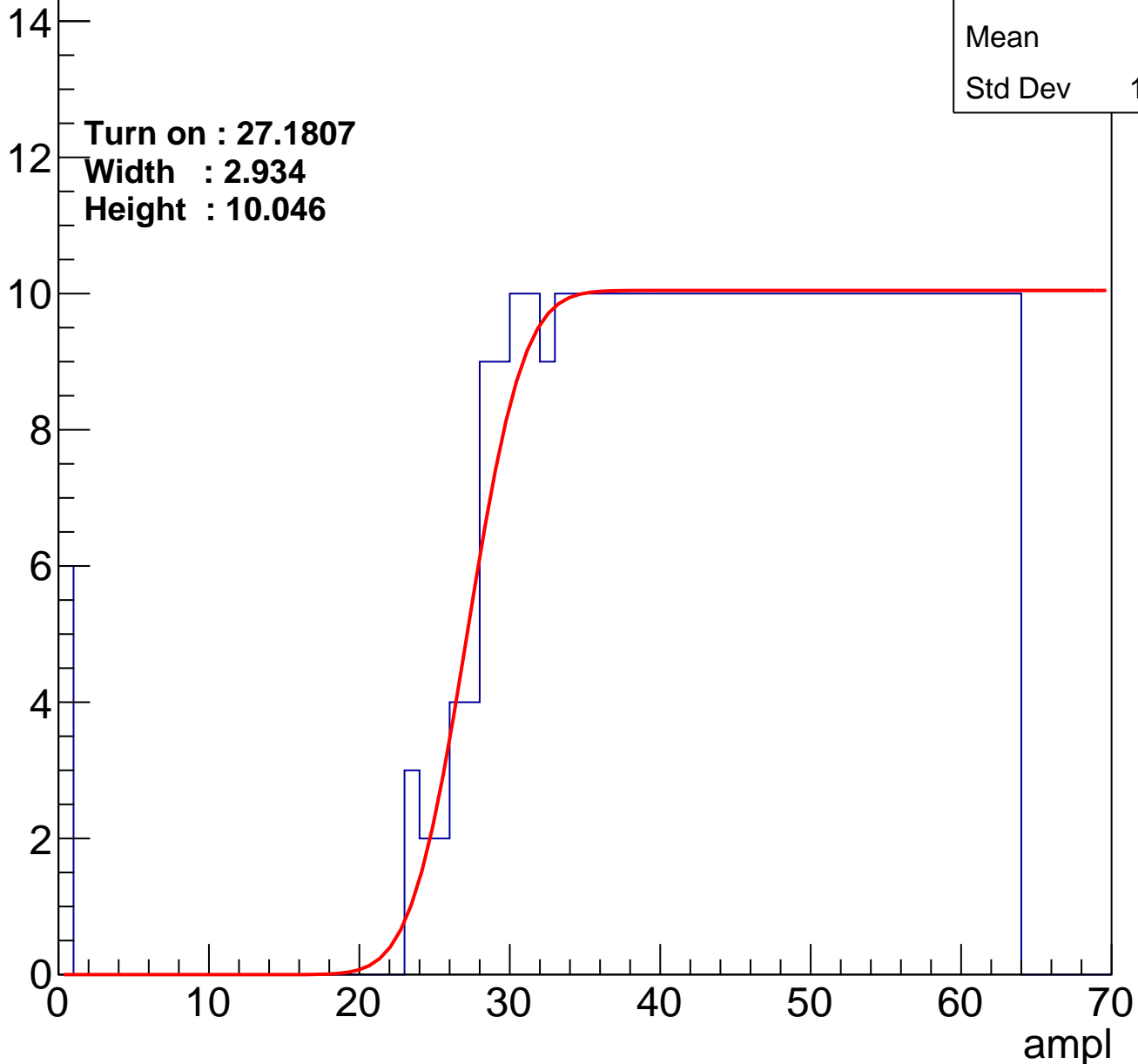
Entries	378
Mean	44.1
Std Dev	12.17

Turn on : 27.1807

Width : 2.934

Height : 10.046

Entry



# B1L100S, U15-ch11

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.12
Std Dev	11.89

Turn on : 27.2388

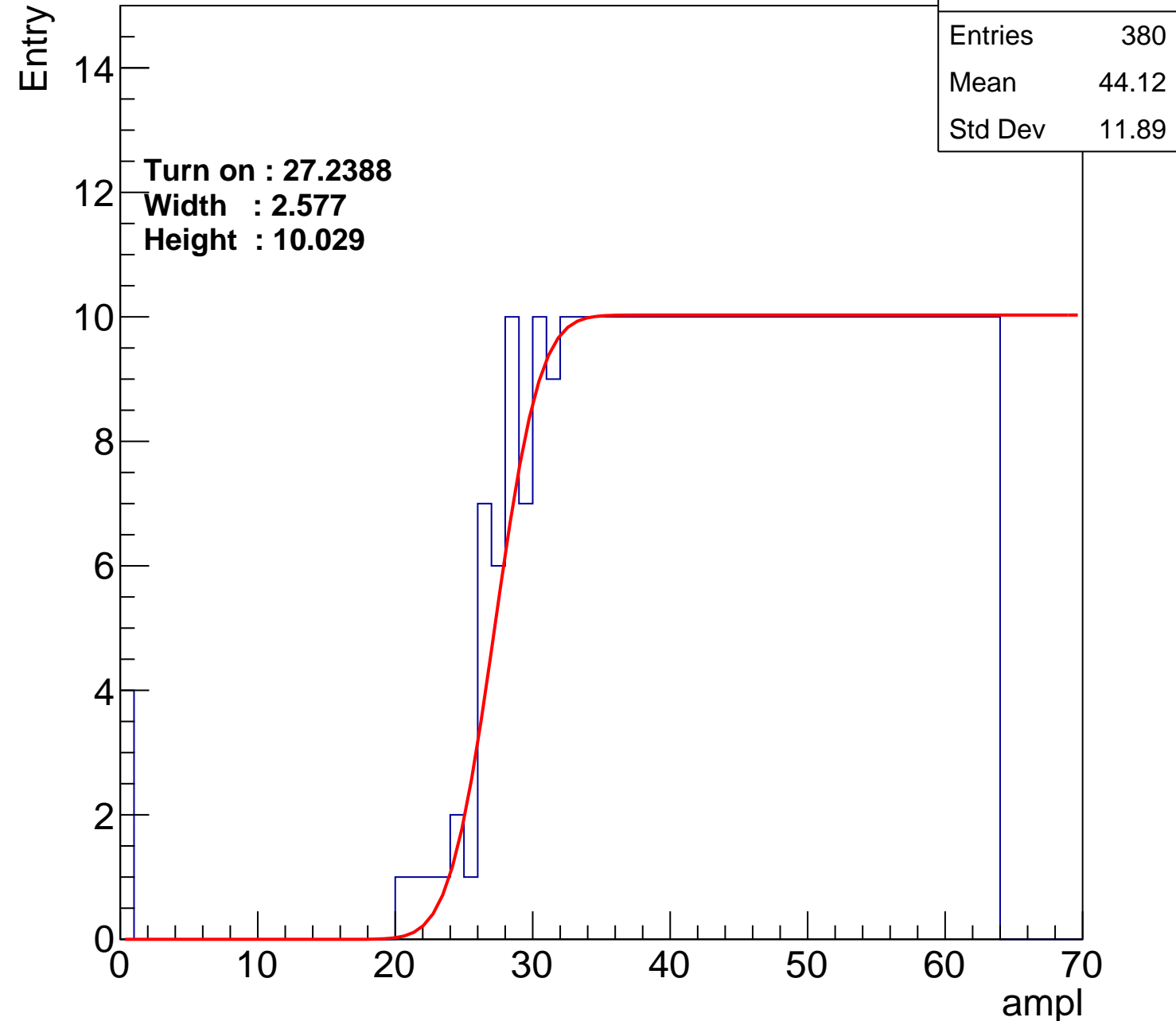
Width : 2.577

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch12

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	393
Mean	43.42
Std Dev	12.35

Turn on : 25.1332

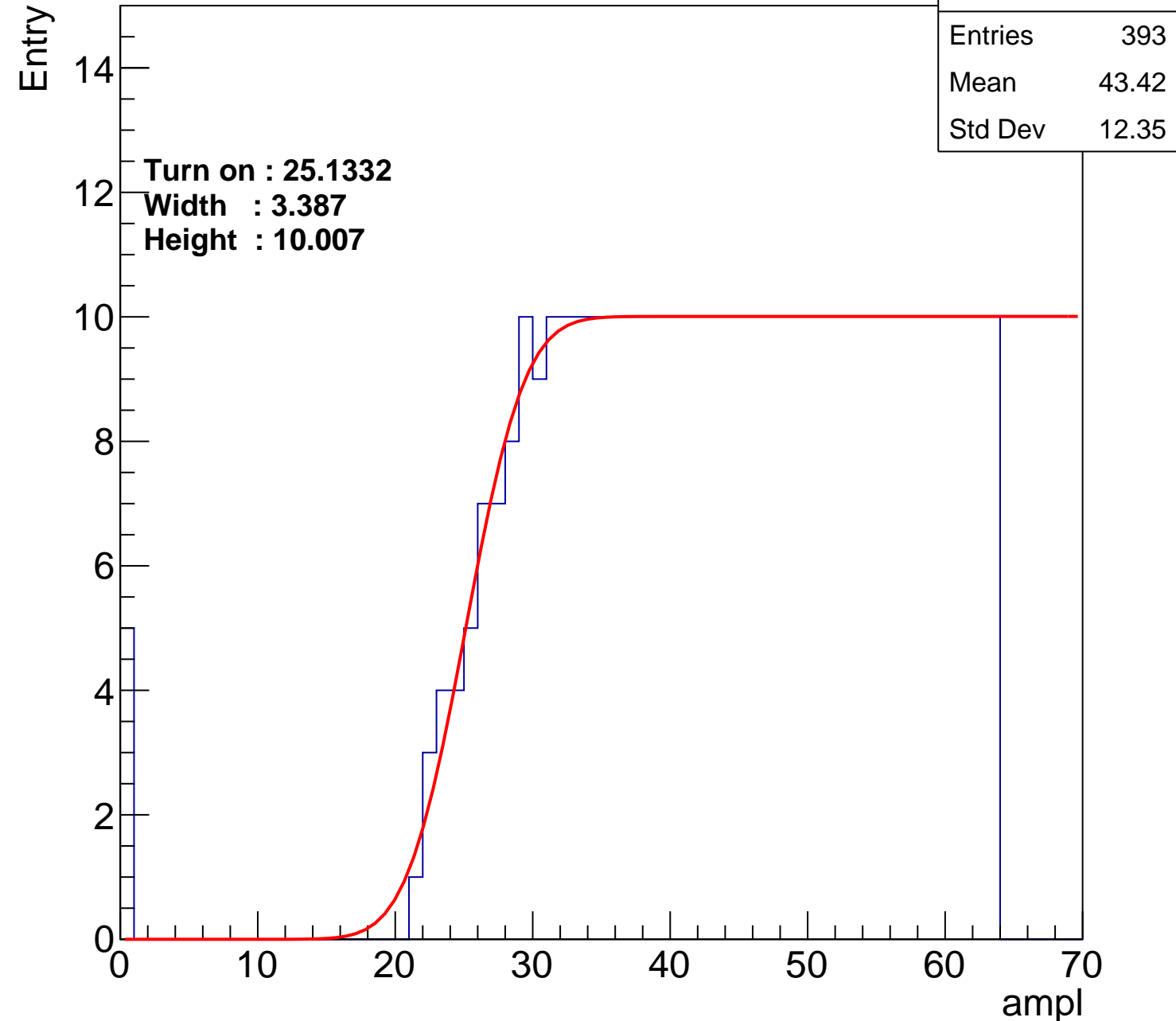
Width : 3.387

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch13

calib\_packv5\_042523\_0143.root, FC#4, port A2

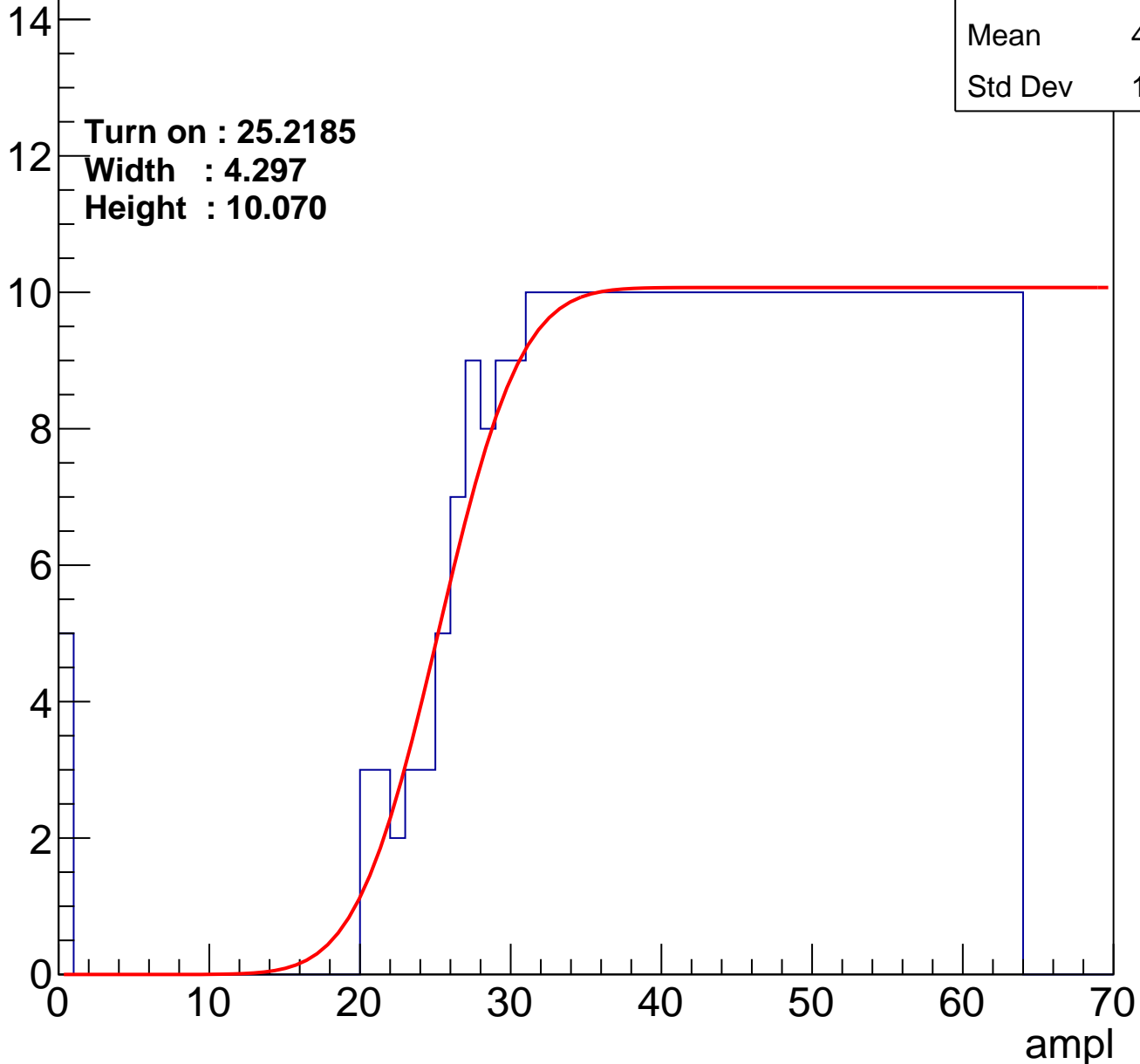
Entries	396
Mean	43.24
Std Dev	12.48

Turn on : 25.2185

Width : 4.297

Height : 10.070

Entry



# B1L100S, U15-ch14

calib\_packv5\_042523\_0143.root, FC#4, port A2

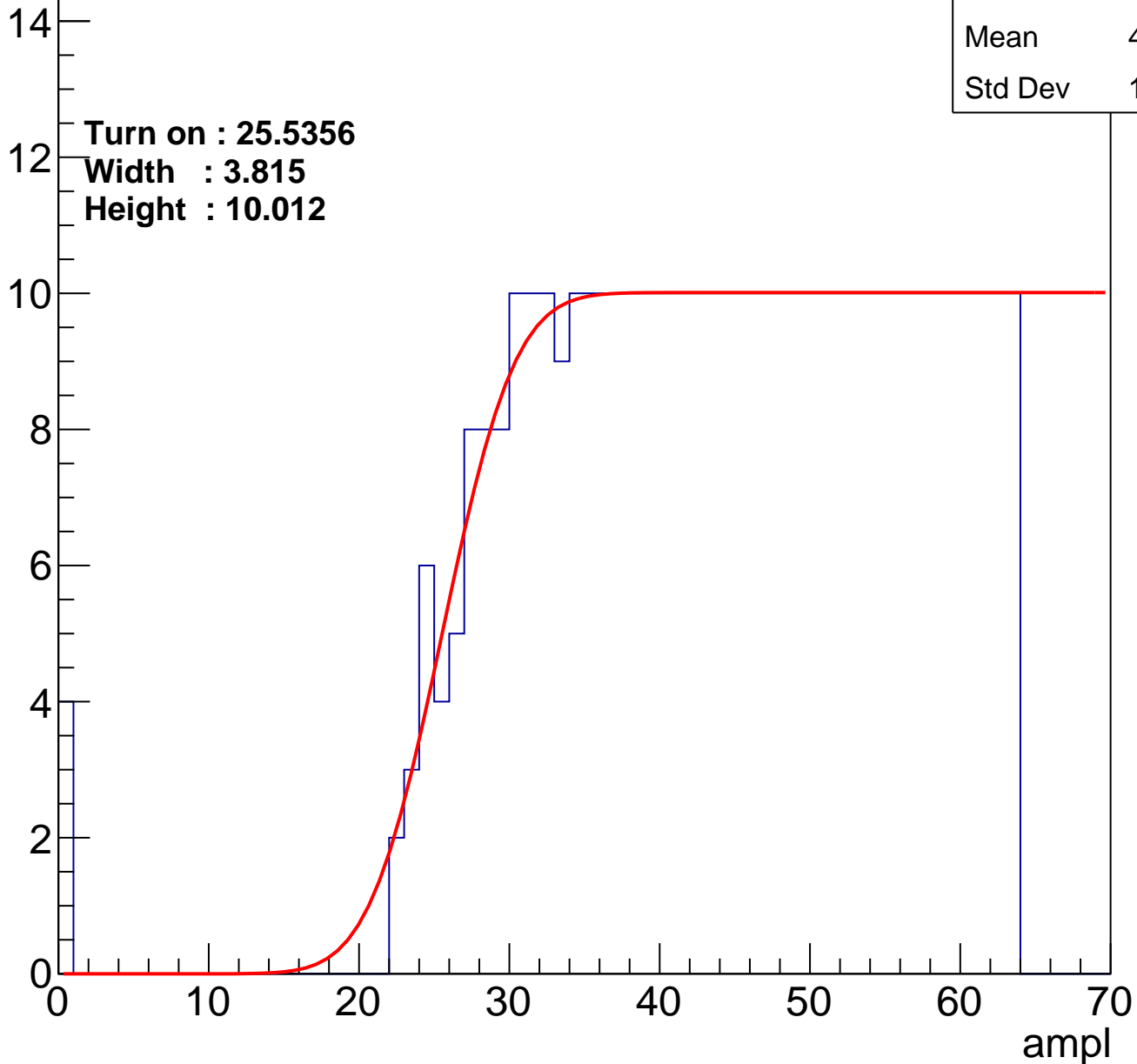
Entries	387
Mean	43.76
Std Dev	12.07

Turn on : 25.5356

Width : 3.815

Height : 10.012

Entry





# B1L100S, U15-ch15

calib\_packv5\_042523\_0143.root, FC#4, port A2

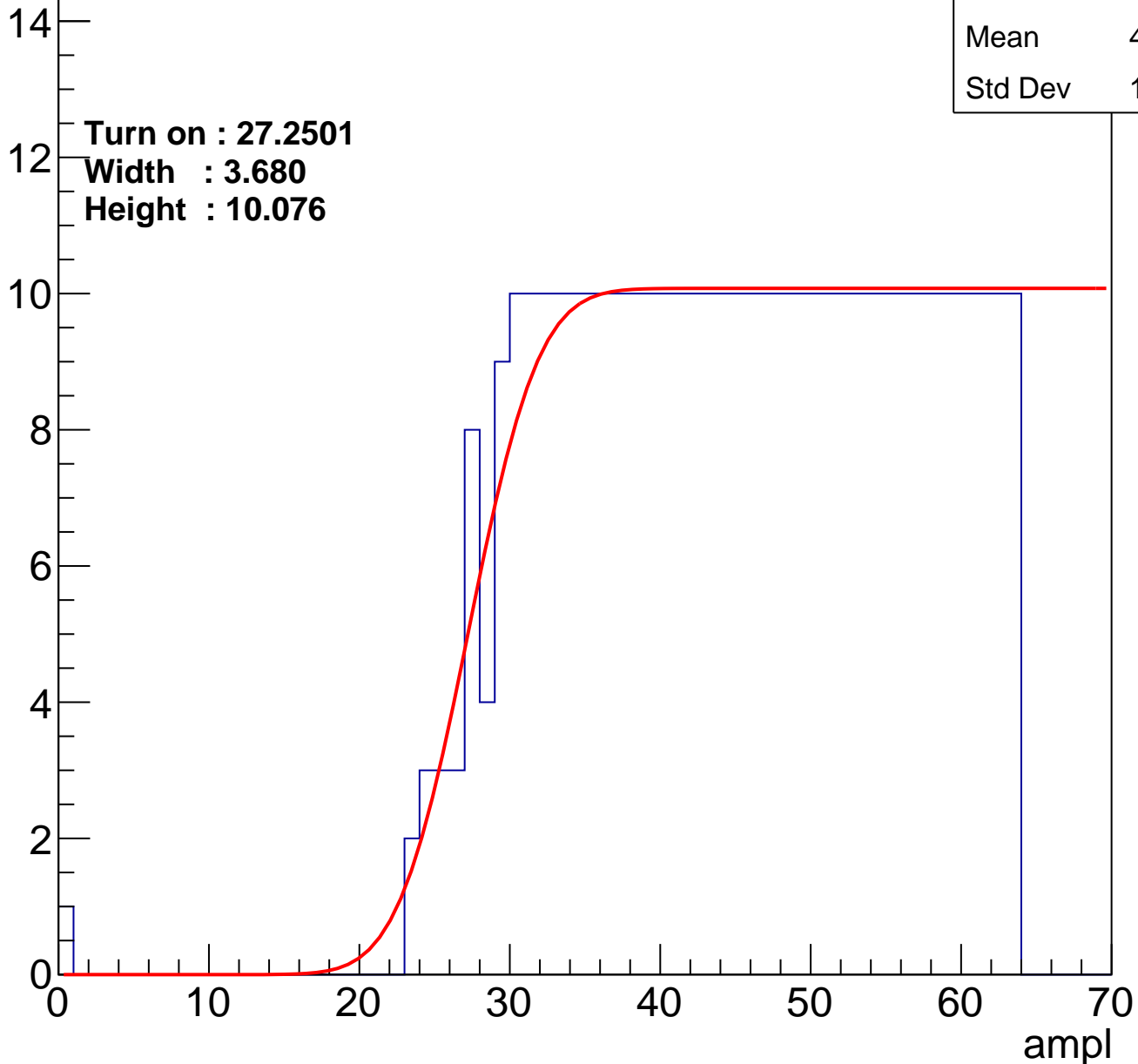
Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 27.2501

Width : 3.680

Height : 10.076

Entry



# B1L100S, U15-ch16

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 27.1510

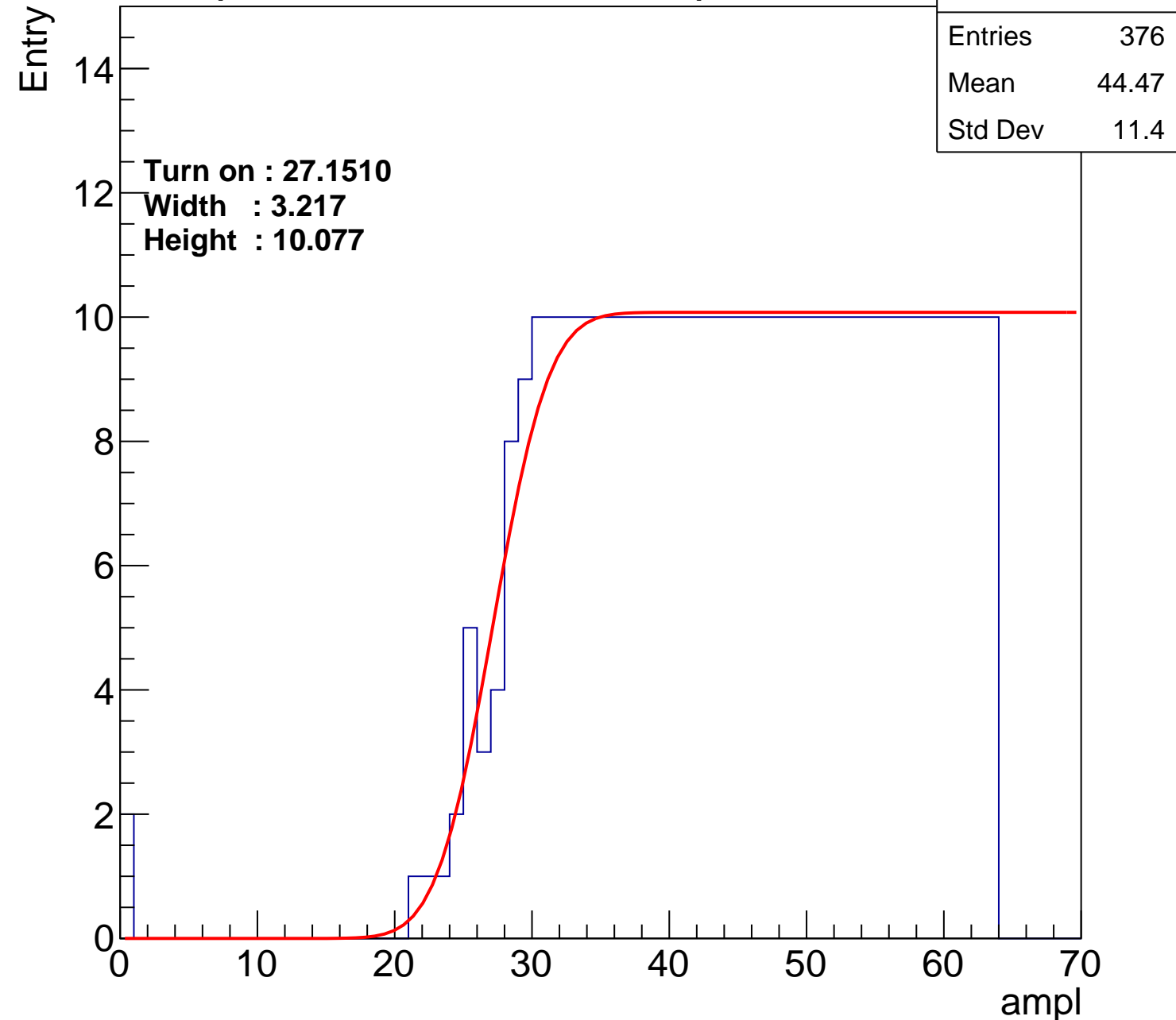
Width : 3.217

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch17

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	373
Mean	44.58
Std Dev	11.37

Turn on : 27.0292

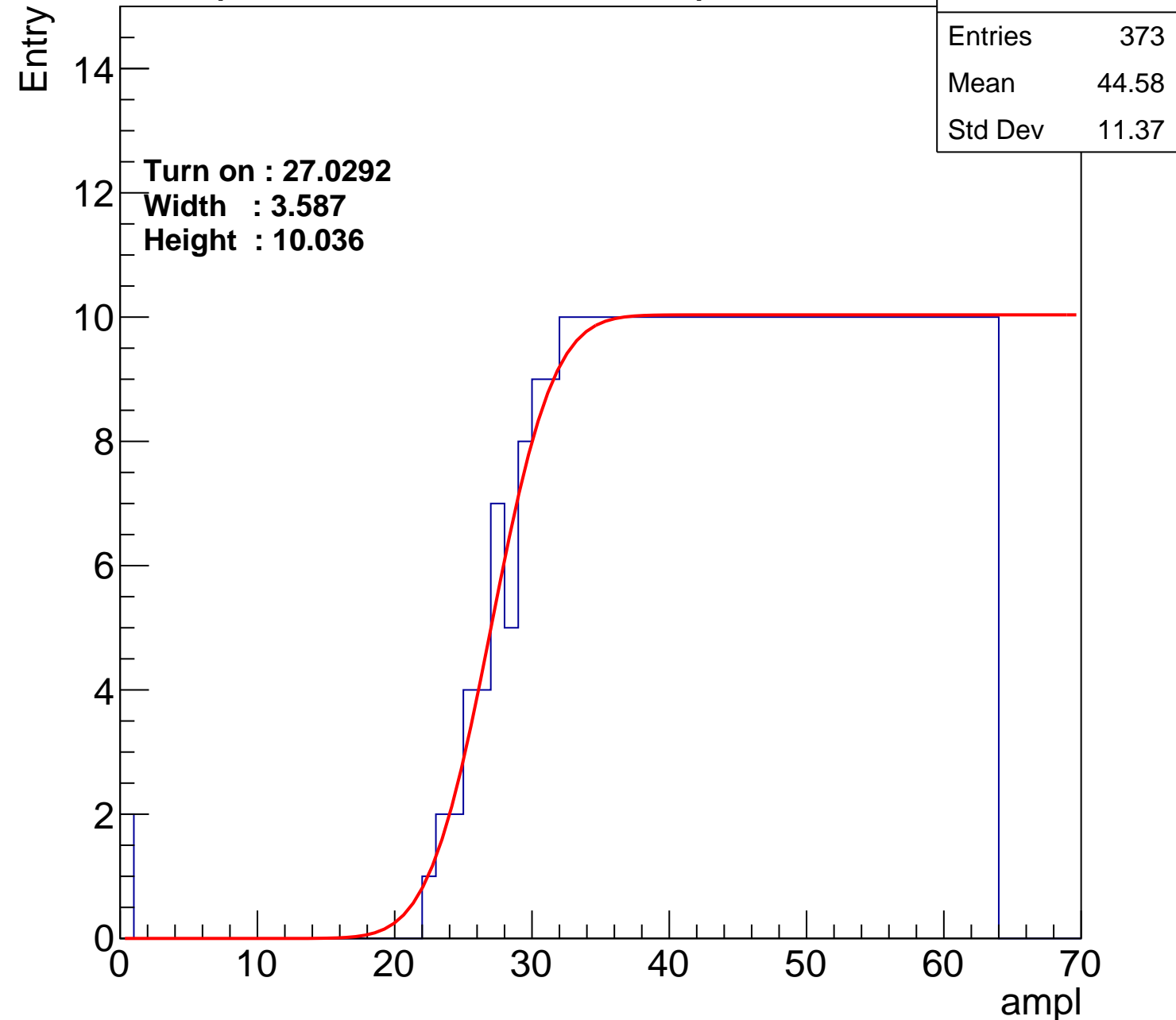
Width : 3.587

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch18

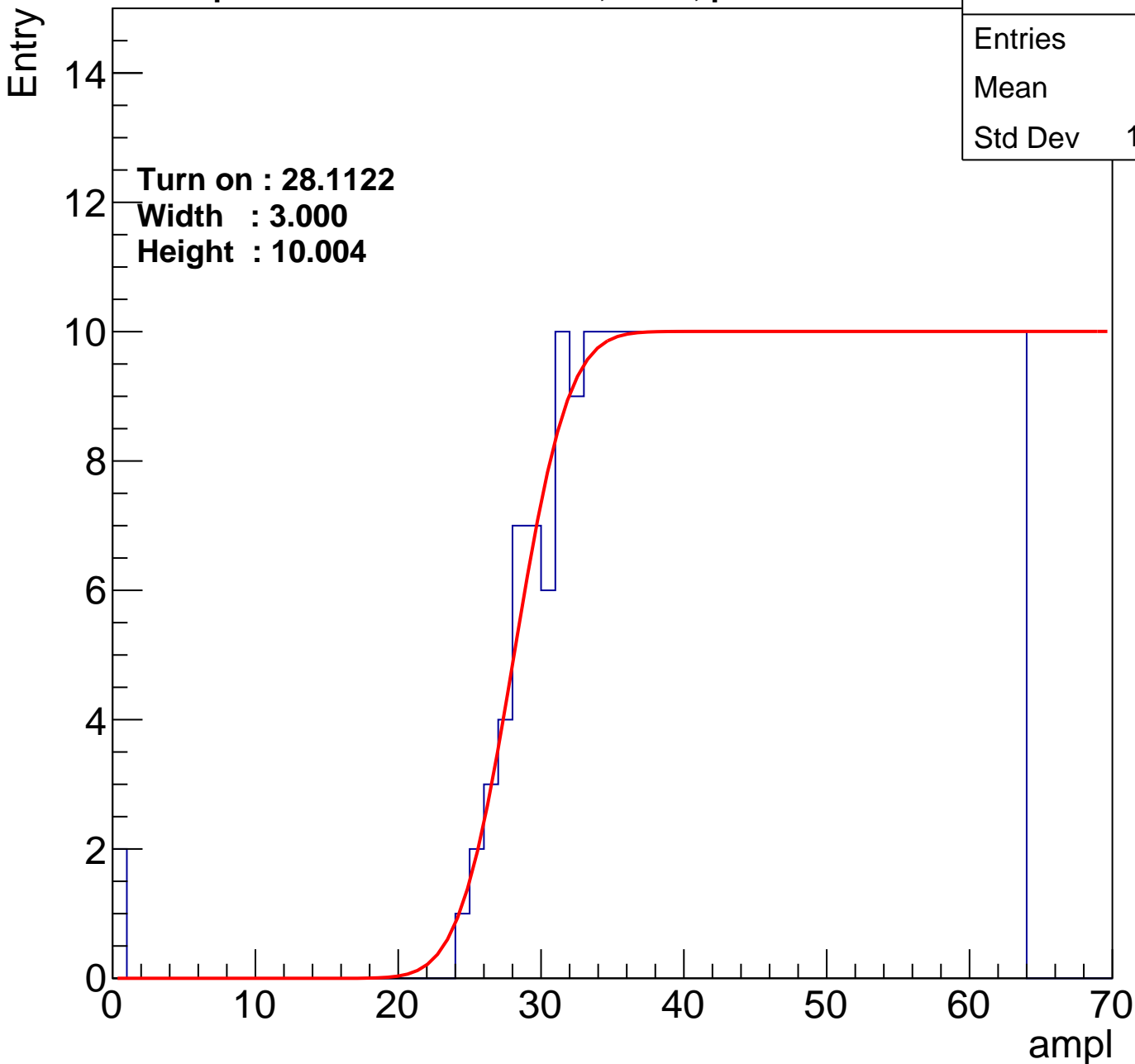
calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	361
Mean	45.2
Std Dev	11.03

Turn on : 28.1122

Width : 3.000

Height : 10.004



# B1L100S, U15-ch19

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	399
Mean	43.37
Std Dev	11.94

Turn on : 24.3554

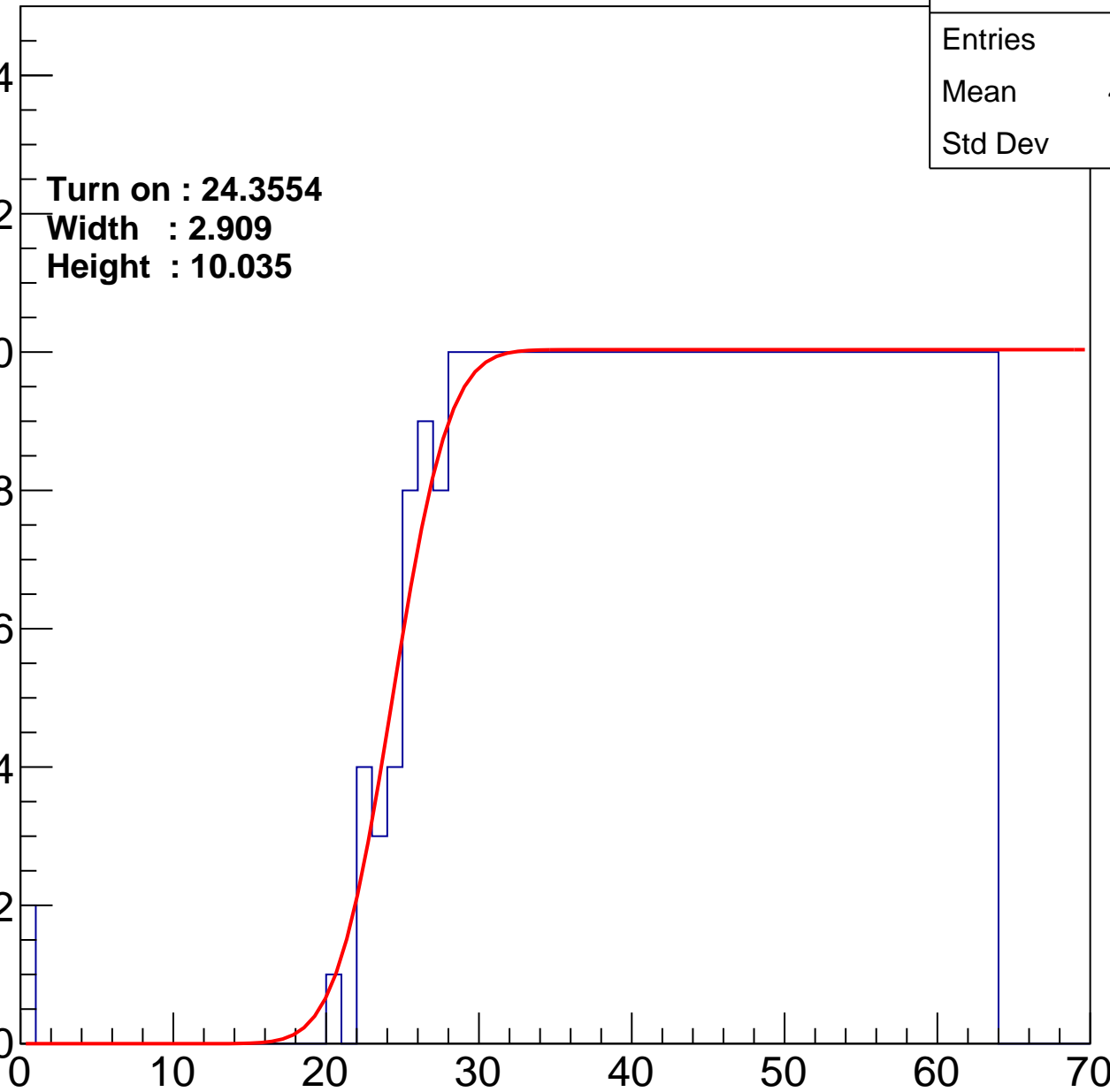
Width : 2.909

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch20

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	398
Mean	43.18
Std Dev	12.41

Turn on : 25.2730

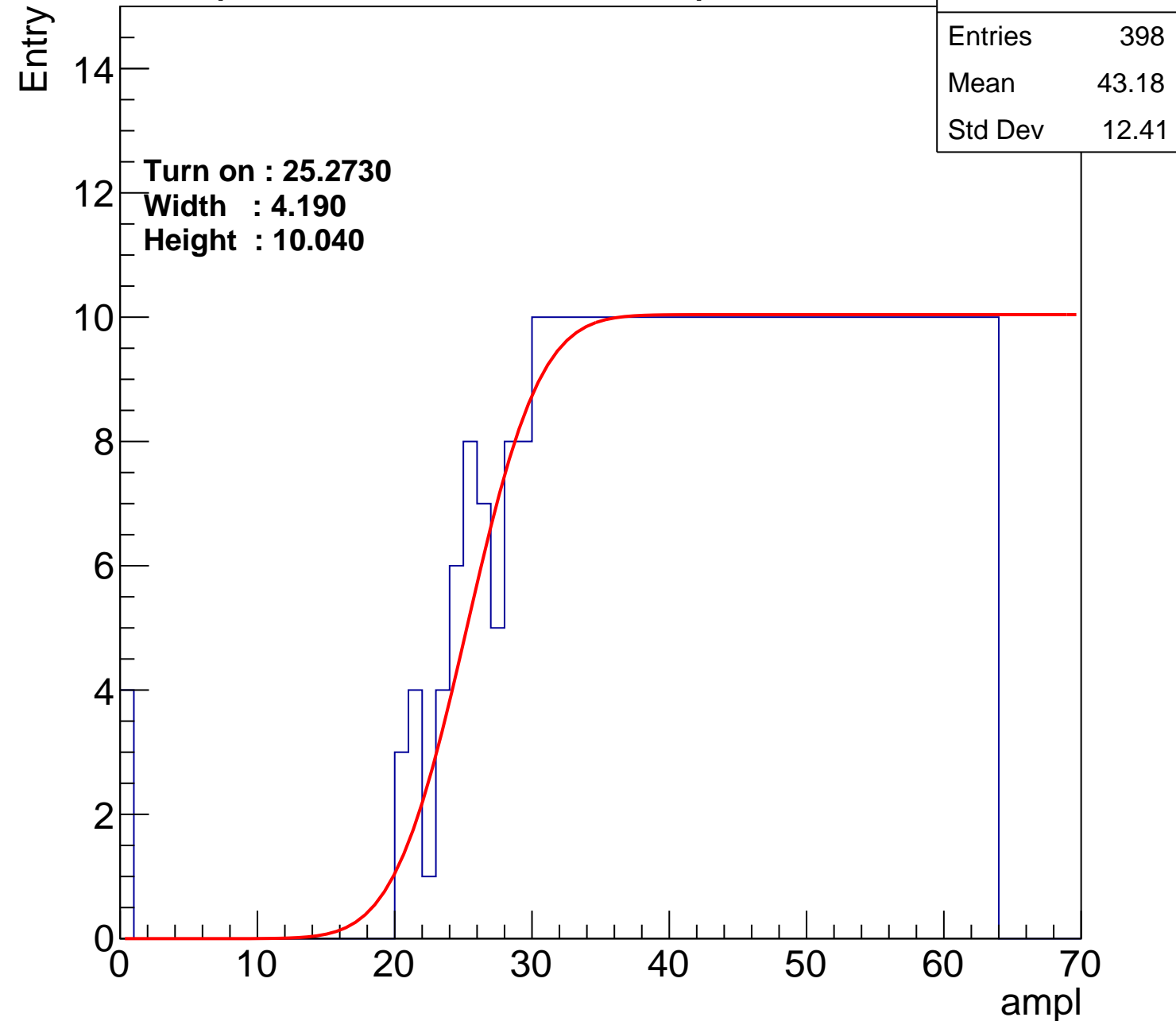
Width : 4.190

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch21

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	363
Mean	45.12
Std Dev	11.04

Turn on : 27.6385

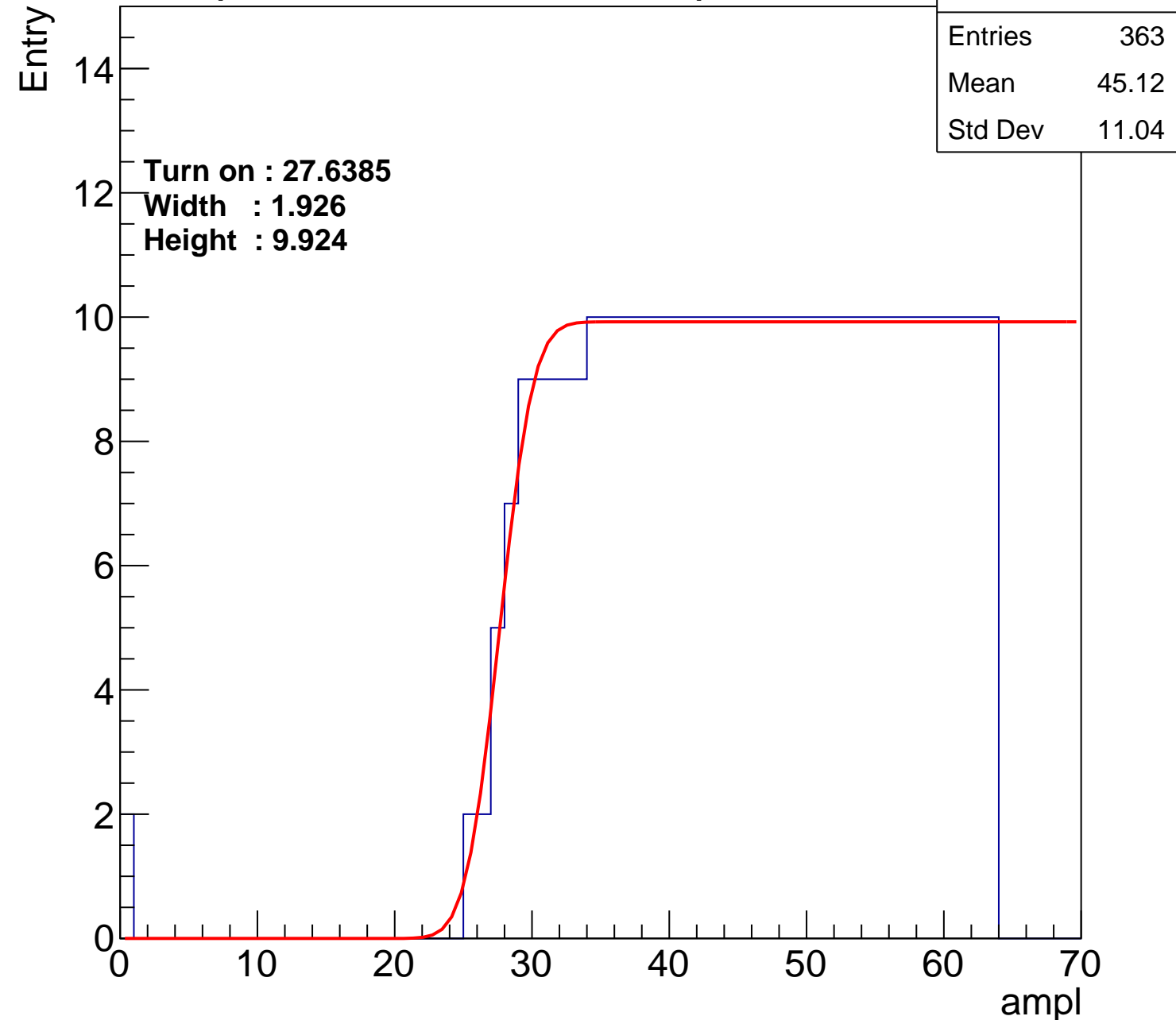
Width : 1.926

Height : 9.924

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch22

calib\_packv5\_042523\_0143.root, FC#4, port A2

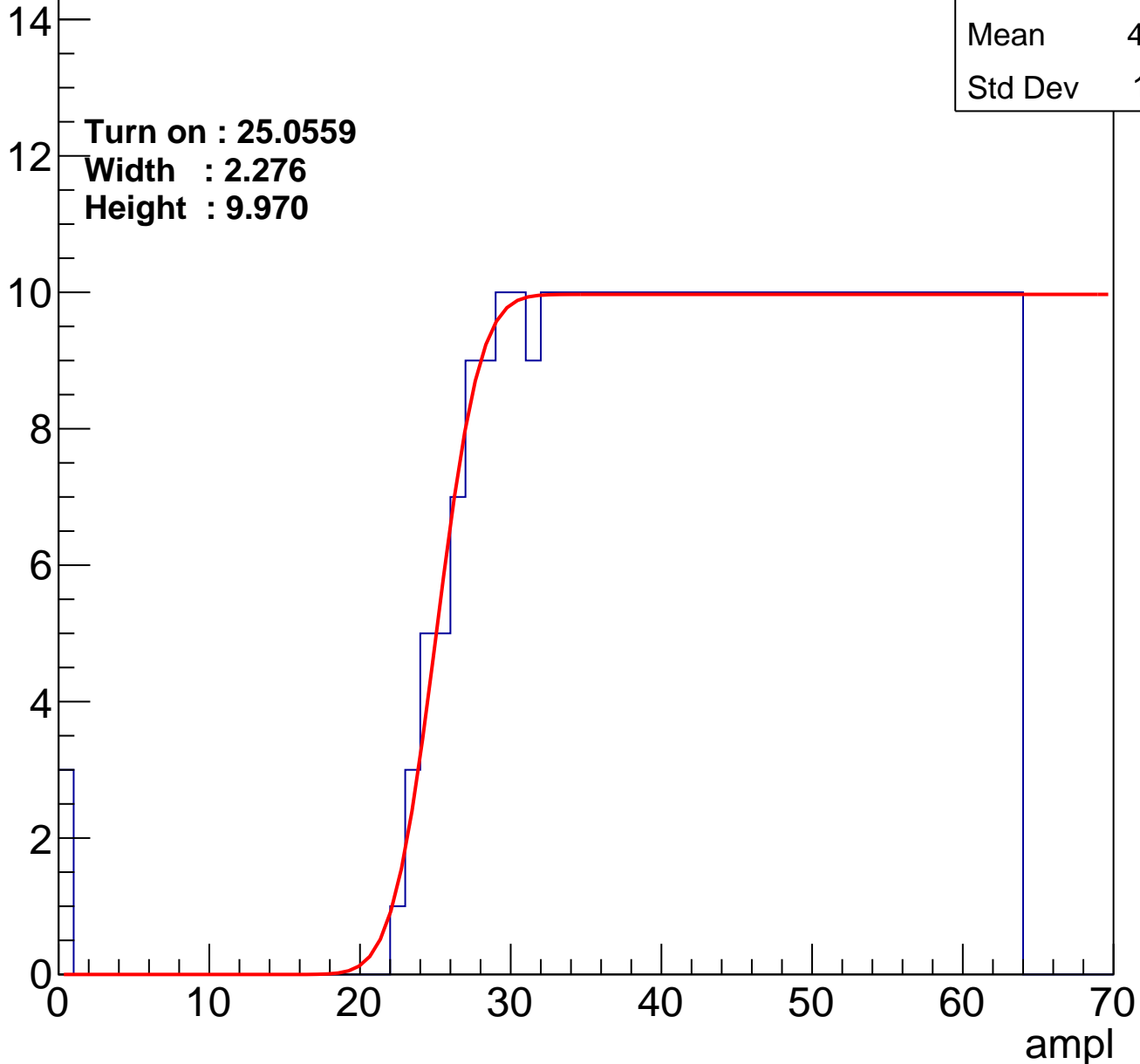
Entries	391
Mean	43.69
Std Dev	11.91

Turn on : 25.0559

Width : 2.276

Height : 9.970

Entry





# B1L100S, U15-ch23

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	388
Mean	43.86
Std Dev	11.74

Turn on : 26.2920

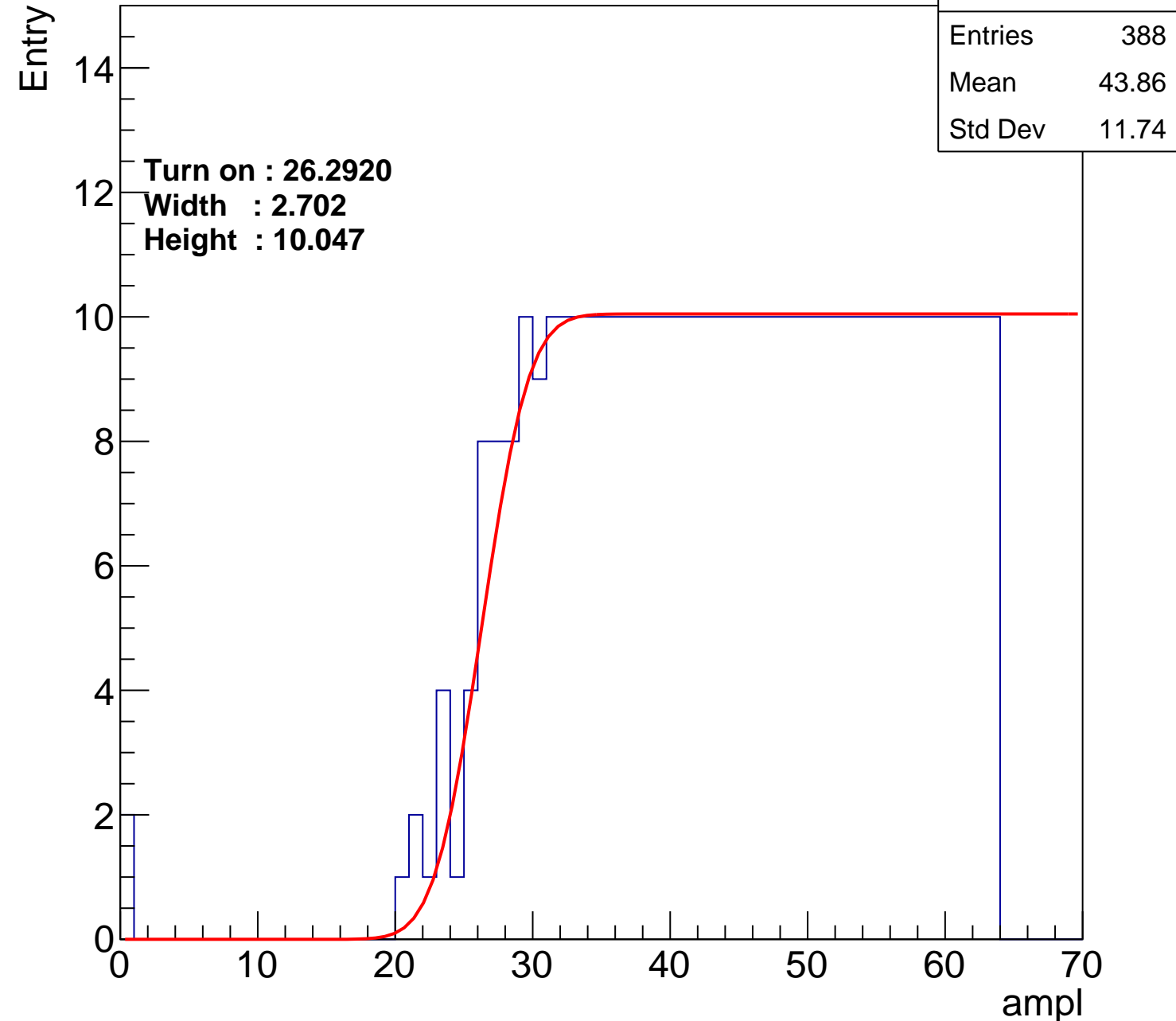
Width : 2.702

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch24

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	389
Mean	43.62
Std Dev	12.24

Turn on : 25.7213

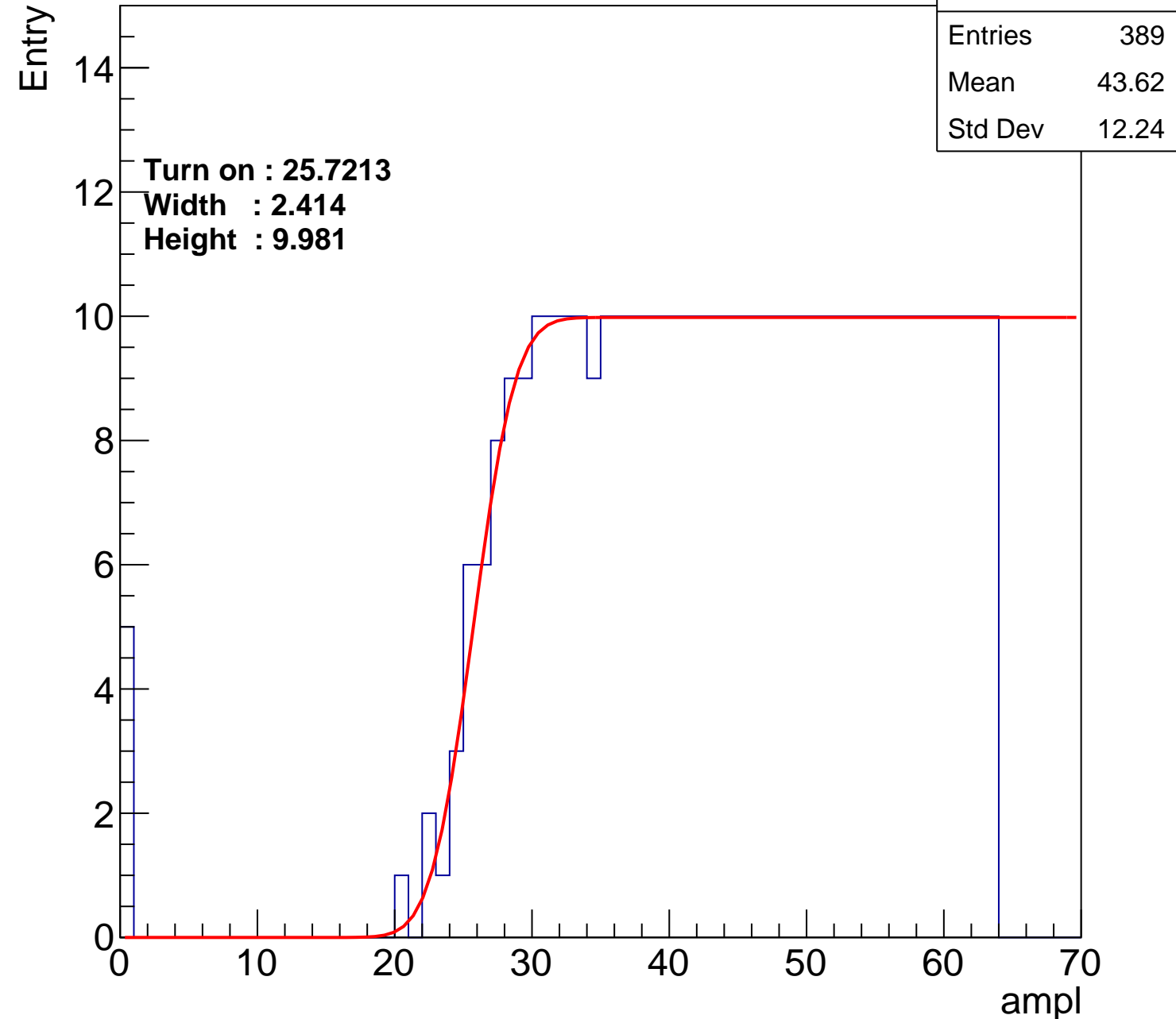
Width : 2.414

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch25

calib\_packv5\_042523\_0143.root, FC#4, port A2

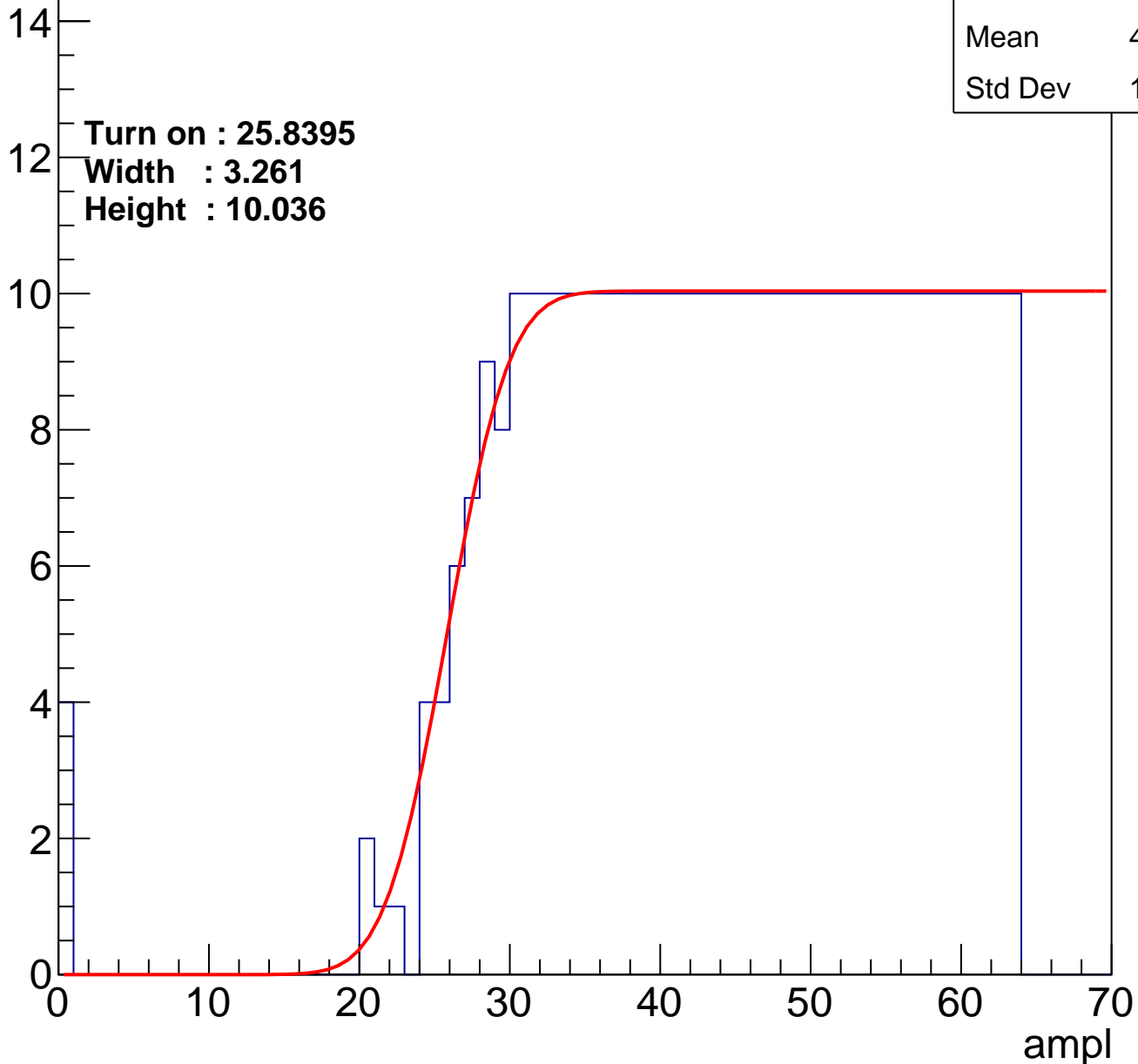
Entries	386
Mean	43.83
Std Dev	12.03

Turn on : 25.8395

Width : 3.261

Height : 10.036

Entry



# B1L100S, U15-ch26

calib\_packv5\_042523\_0143.root, FC#4, port A2

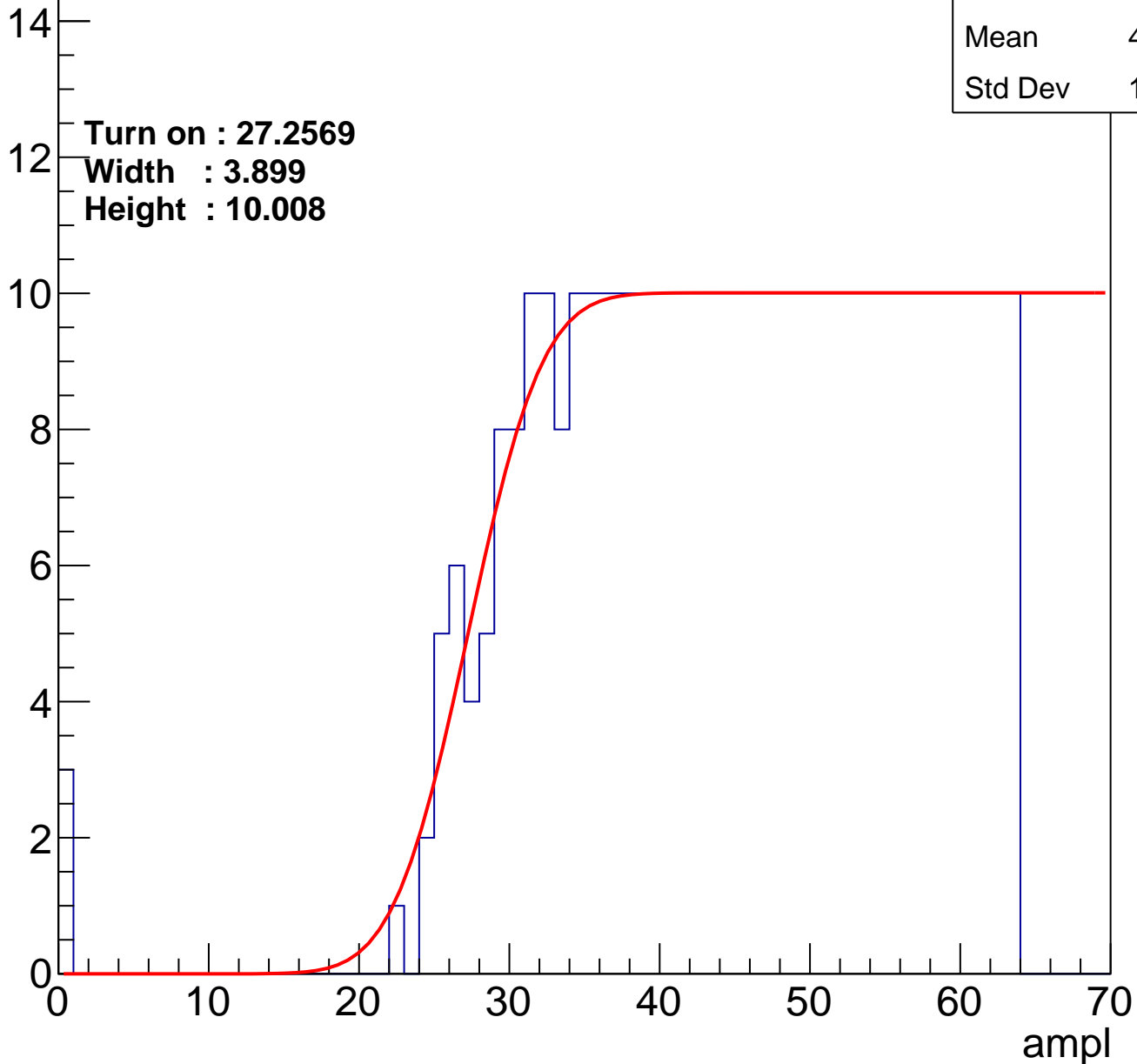
Entries	370
Mean	44.64
Std Dev	11.52

Turn on : 27.2569

Width : 3.899

Height : 10.008

Entry



# B1L100S, U15-ch27

calib\_packv5\_042523\_0143.root, FC#4, port A2

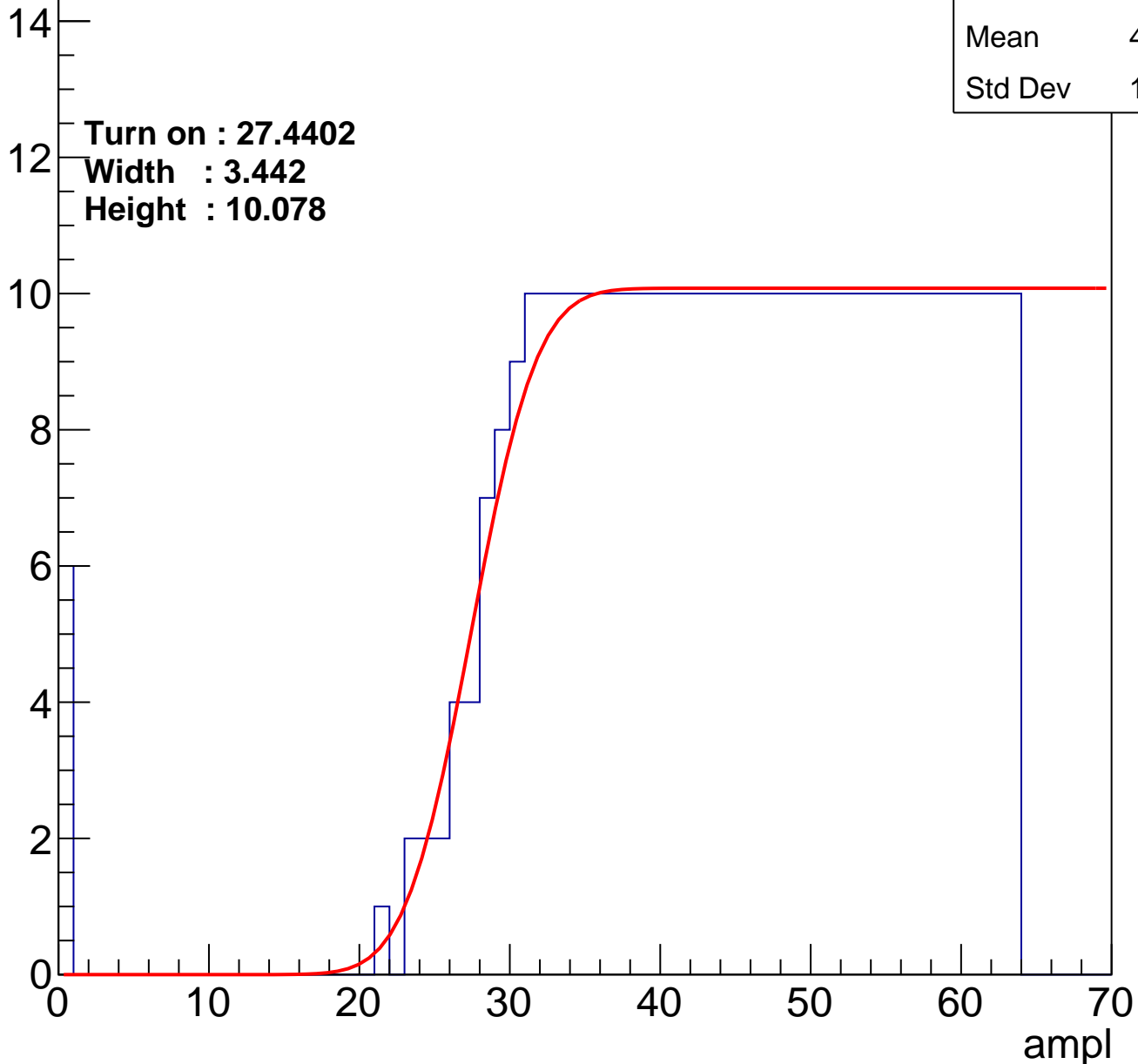
Entries	375
Mean	44.23
Std Dev	12.14

Turn on : 27.4402

Width : 3.442

Height : 10.078

Entry



# B1L100S, U15-ch28

calib\_packv5\_042523\_0143.root, FC#4, port A2

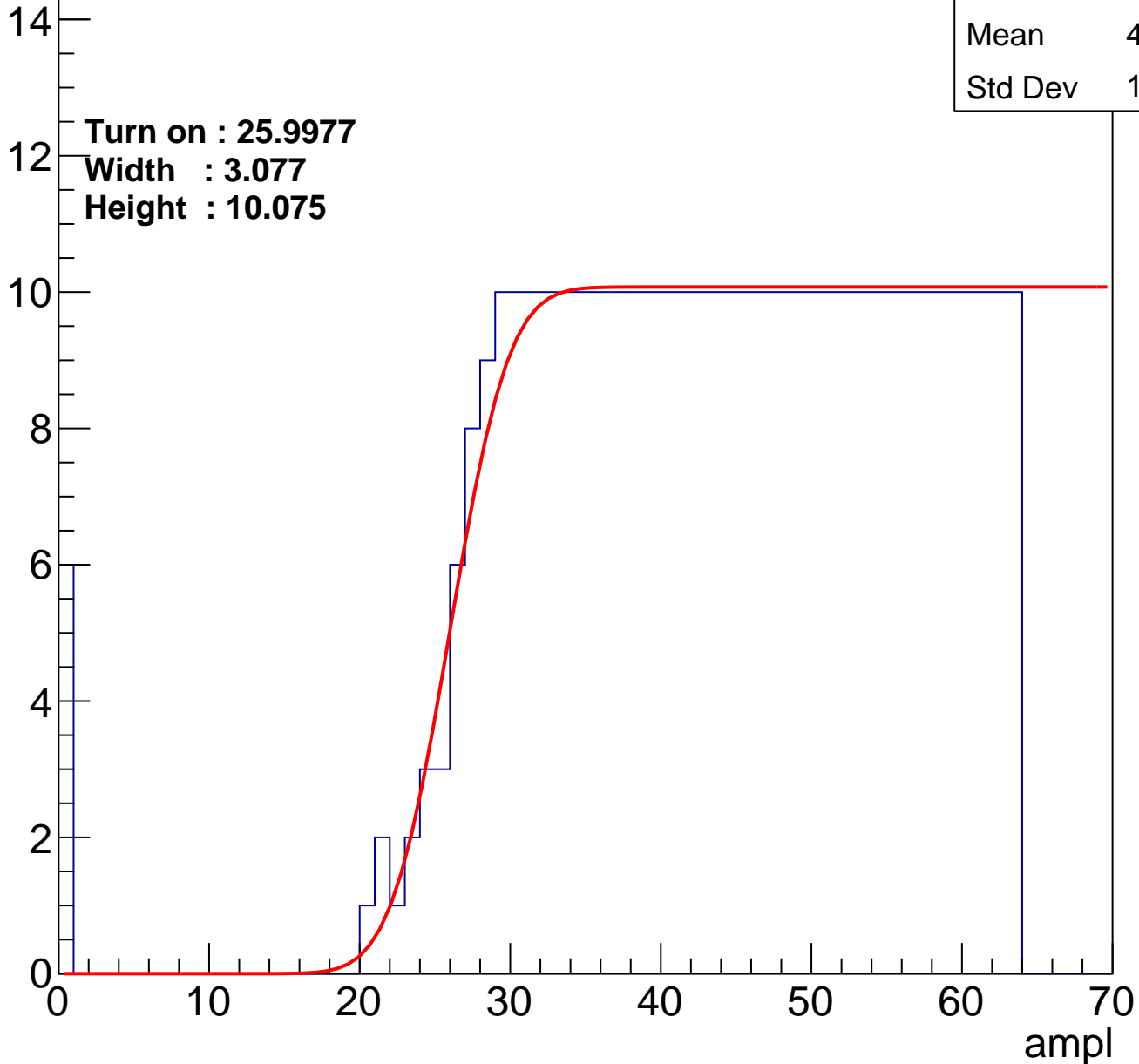
Entries	391
Mean	43.48
Std Dev	12.43

**Turn on : 25.9977**

**Width : 3.077**

**Height : 10.075**

Entry



# B1L100S, U15-ch29

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	377
Mean	44.42
Std Dev	11.42

**Turn on : 26.5434**

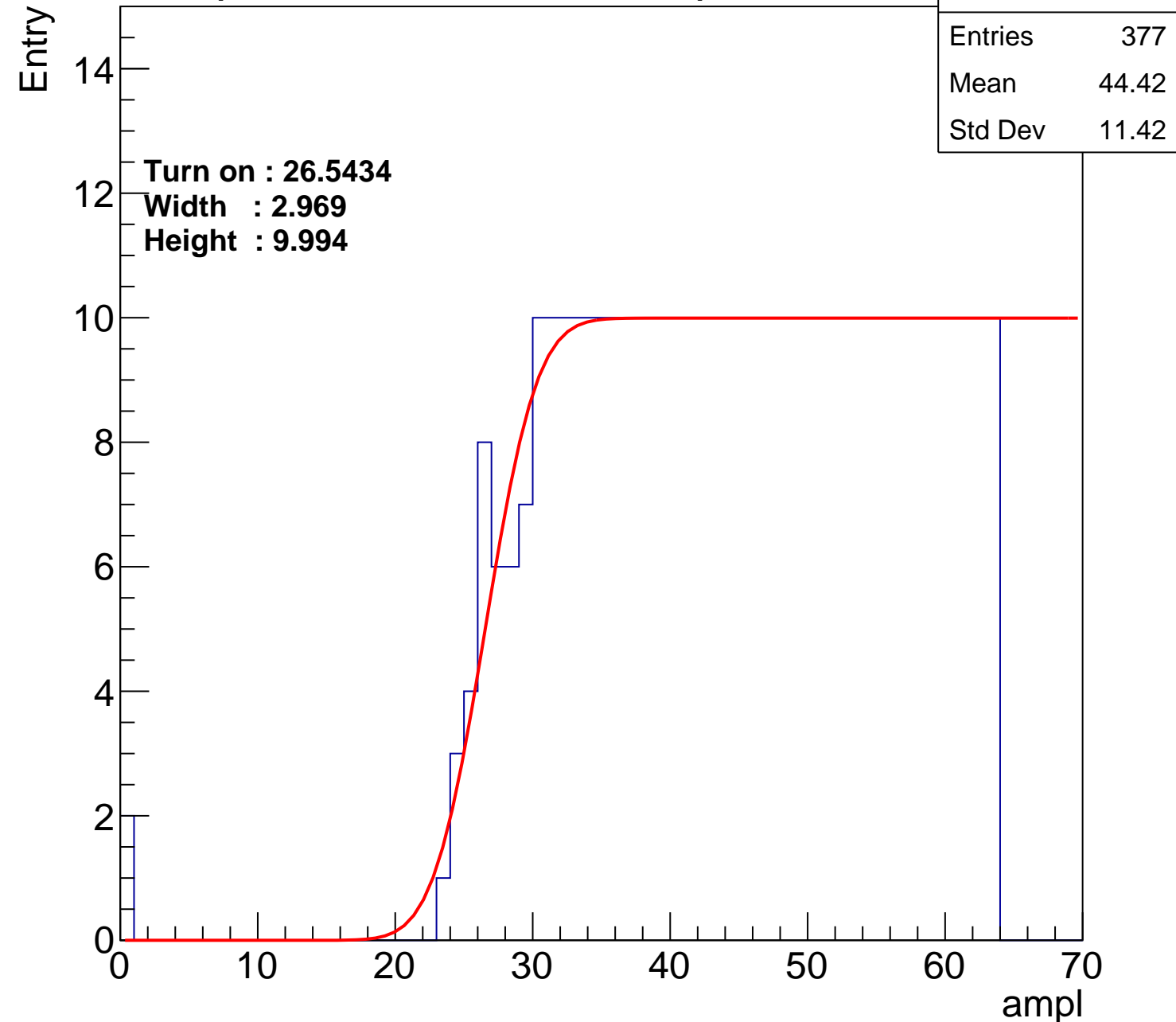
**Width : 2.969**

**Height : 9.994**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch30

calib\_packv5\_042523\_0143.root, FC#4, port A2

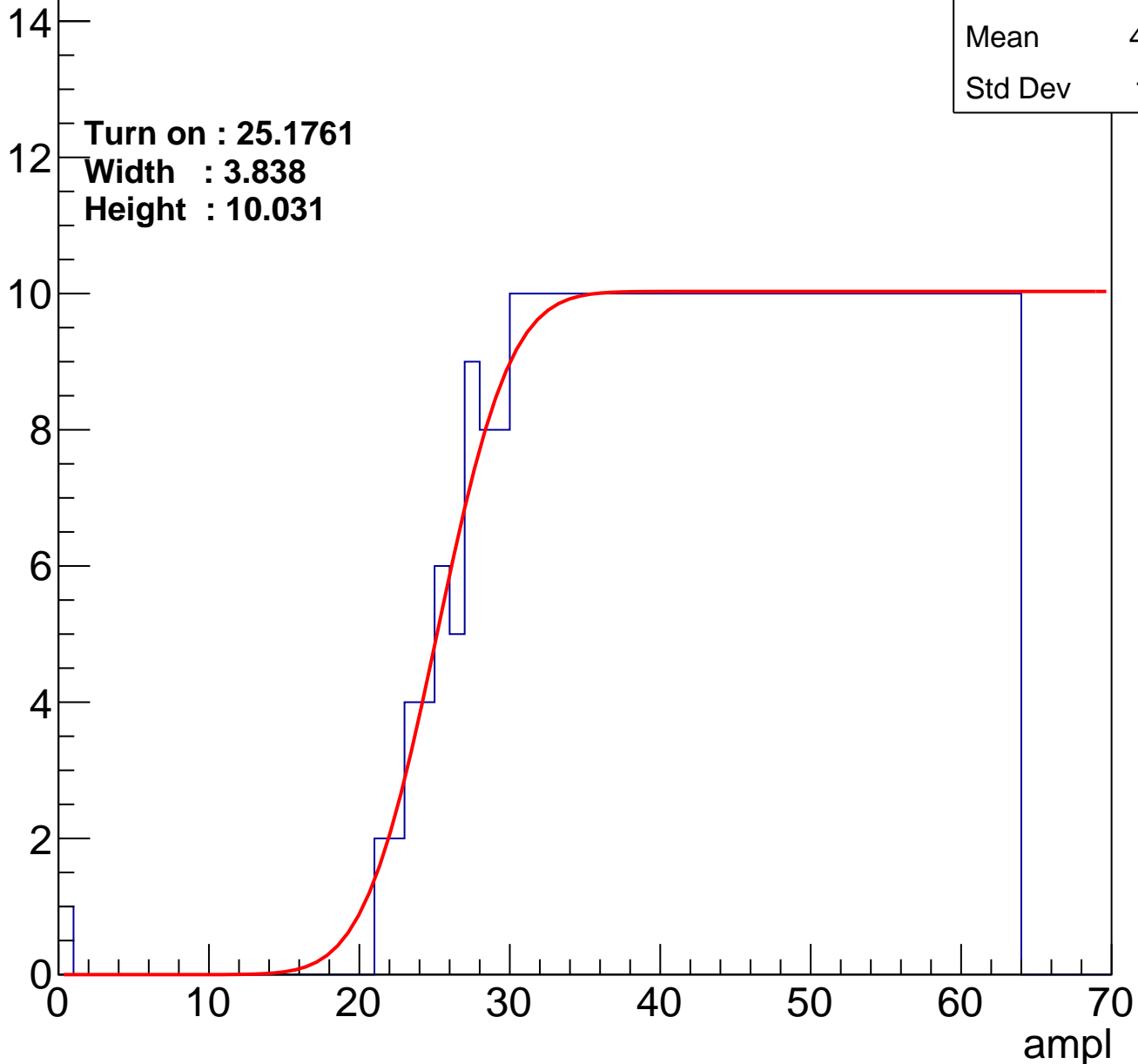
Entries	389
Mean	43.86
Std Dev	11.61

Turn on : 25.1761

Width : 3.838

Height : 10.031

Entry





# B1L100S, U15-ch31

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.5693

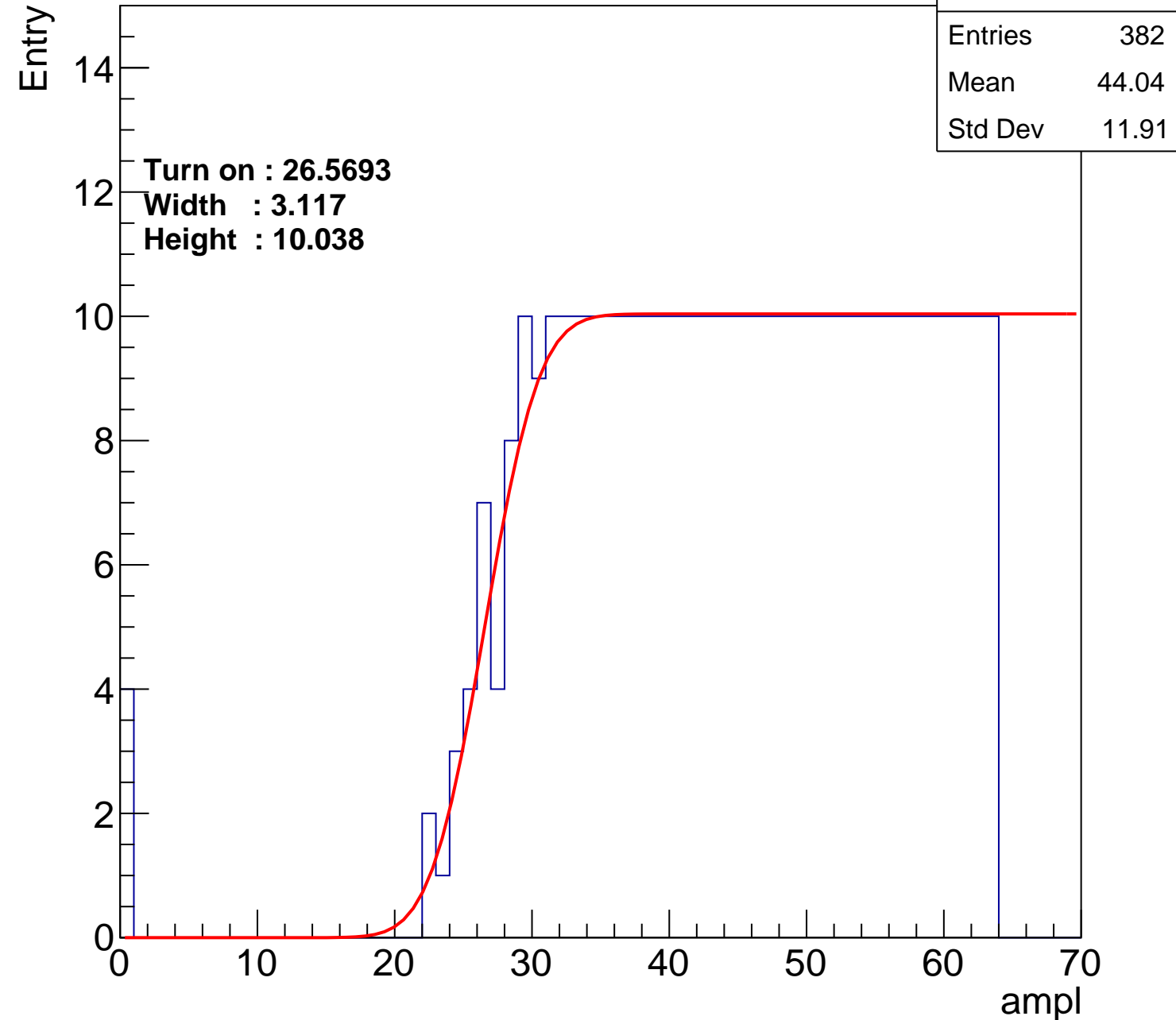
Width : 3.117

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch32

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.14
Std Dev	11.87

Turn on : 26.4942

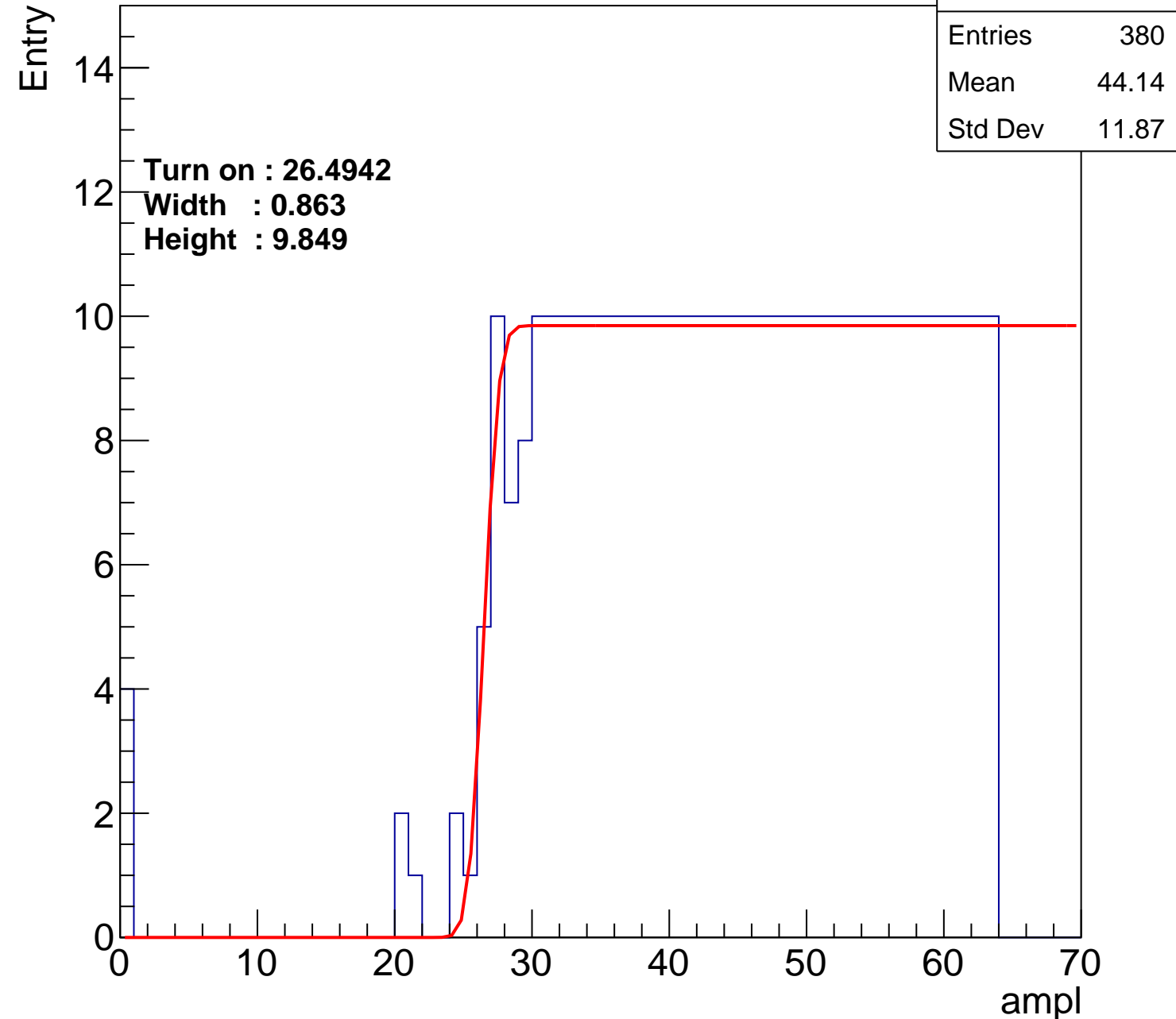
Width : 0.863

Height : 9.849

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch33

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	396
Mean	43.22
Std Dev	12.57

Turn on : 25.3849

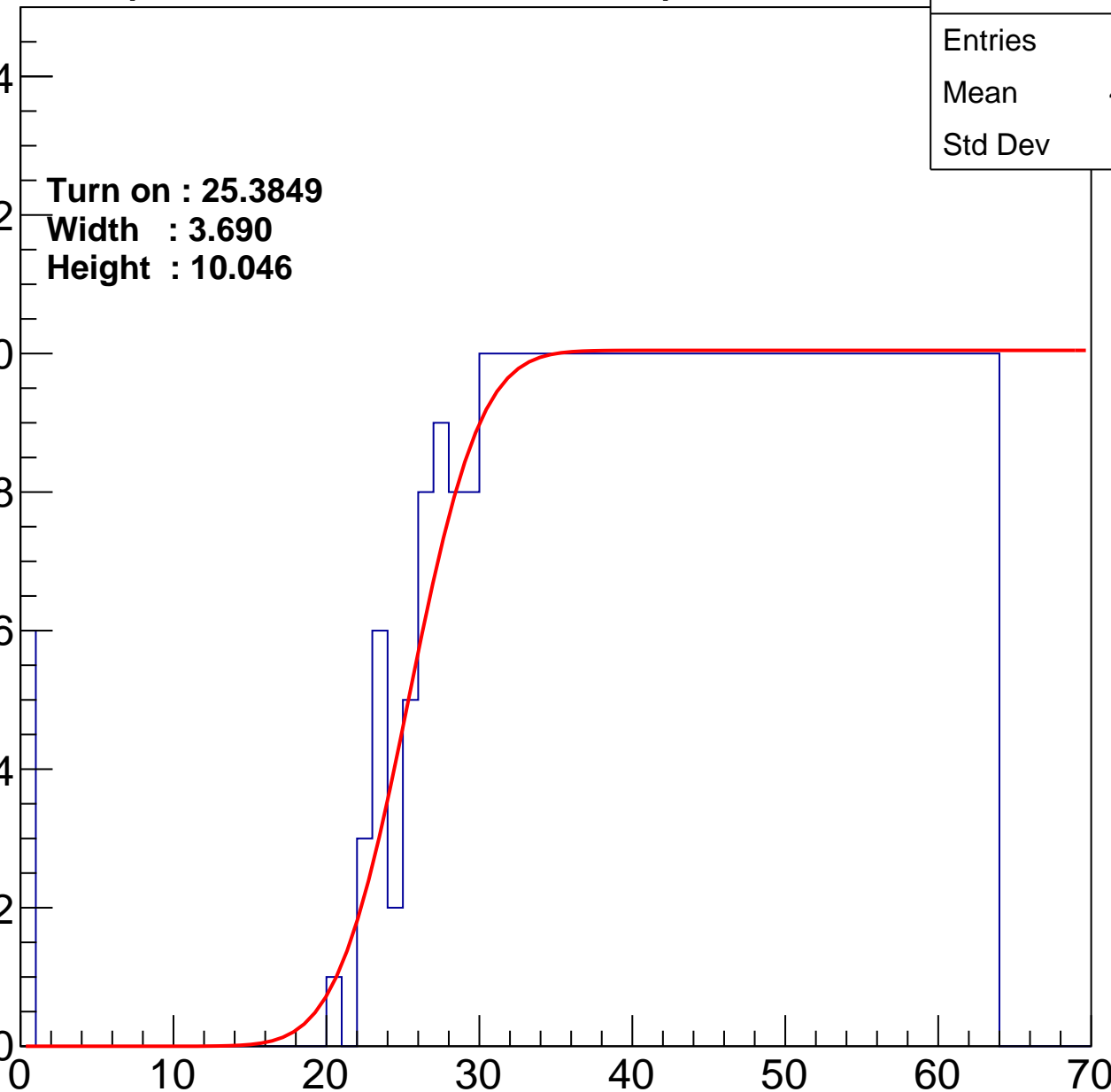
Width : 3.690

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

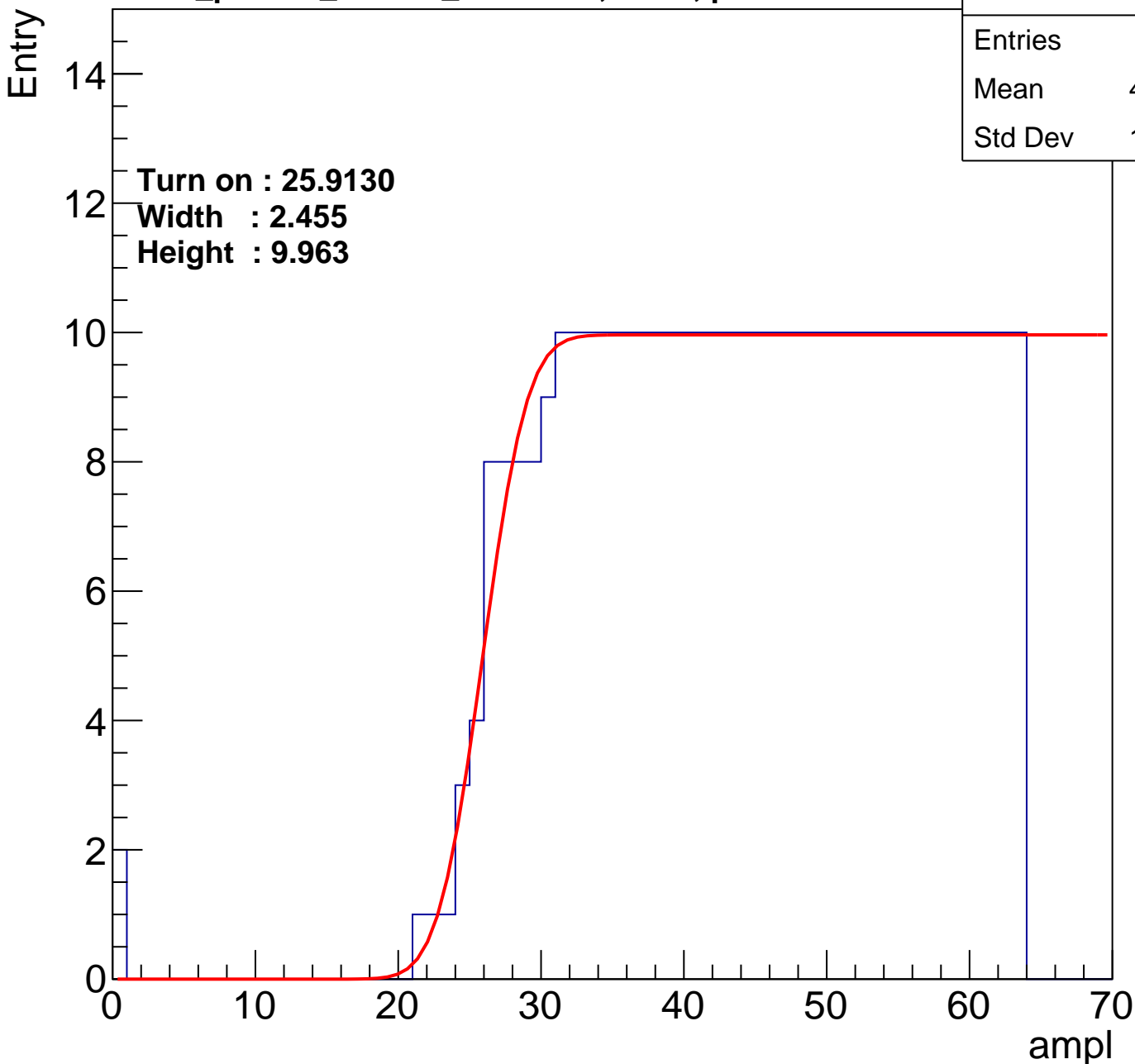
ampl



**calib\_packv5\_042523\_0143.root, FC#4, port A2**

**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Turn on : 25.9130  
Width : 2.455  
Height : 9.963



# B1L100S, U15-ch35

calib\_packv5\_042523\_0143.root, FC#4, port A2

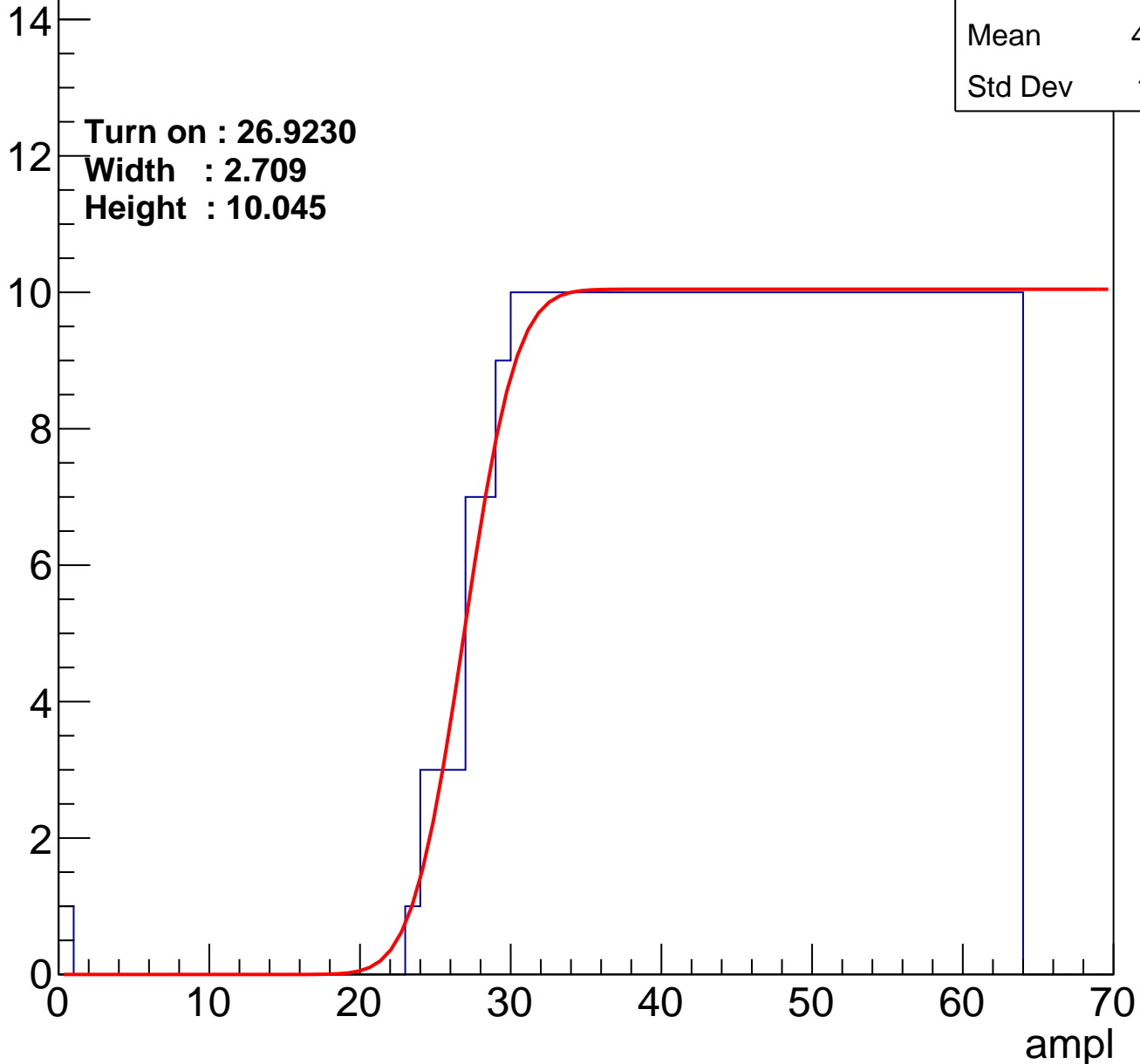
Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 26.9230

Width : 2.709

Height : 10.045

Entry



# B1L100S, U15-ch36

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	388
Mean	43.82
Std Dev	11.87

Turn on : 26.6971

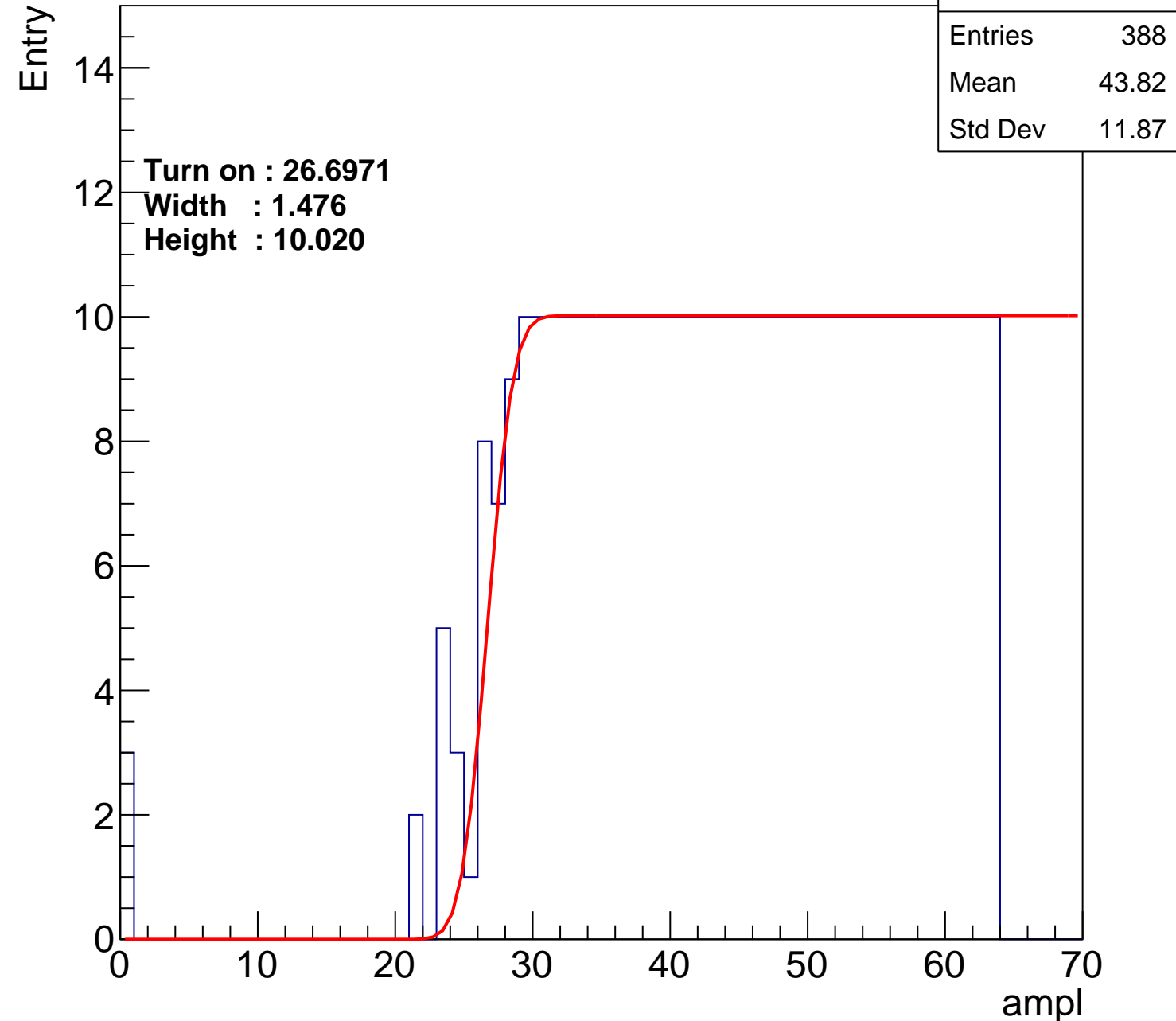
Width : 1.476

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch37

calib\_packv5\_042523\_0143.root, FC#4, port A2

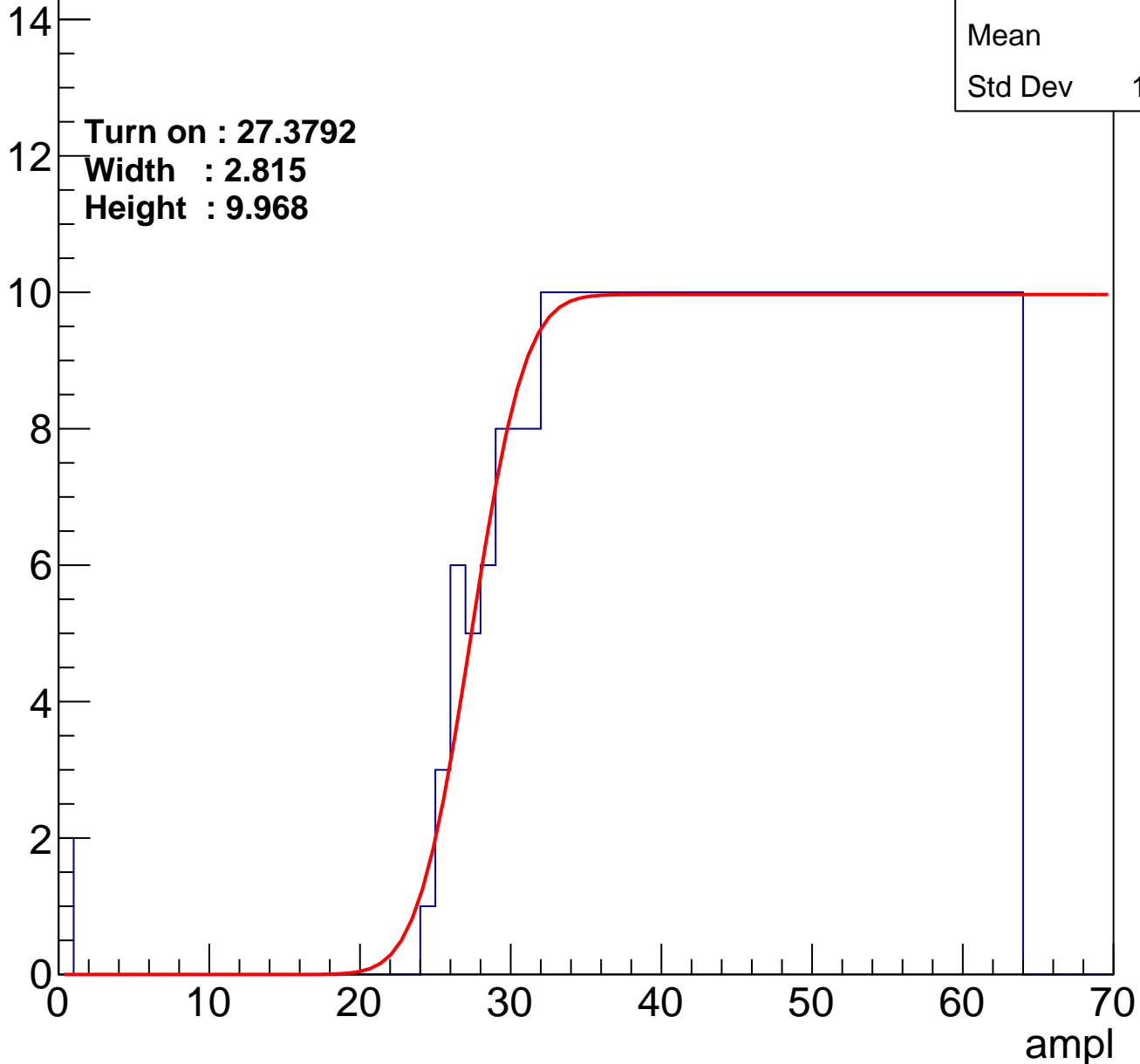
Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.3792

Width : 2.815

Height : 9.968

Entry



# B1L100S, U15-ch38

calib\_packv5\_042523\_0143.root, FC#4, port A2

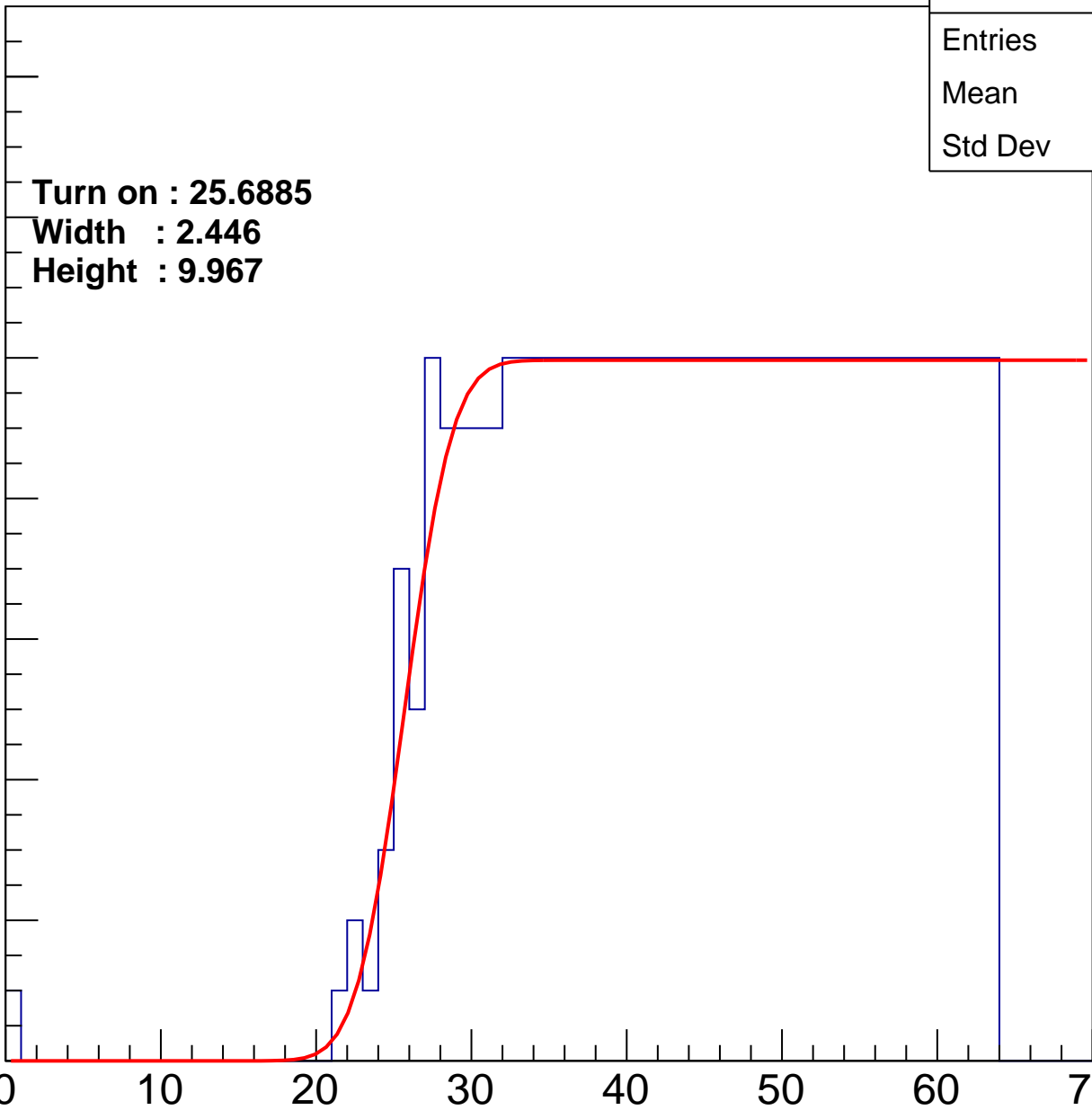
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.6885  
Width : 2.446  
Height : 9.967

Entries	386
Mean	44.03
Std Dev	11.49

ampl





# B1L100S, U15-ch39

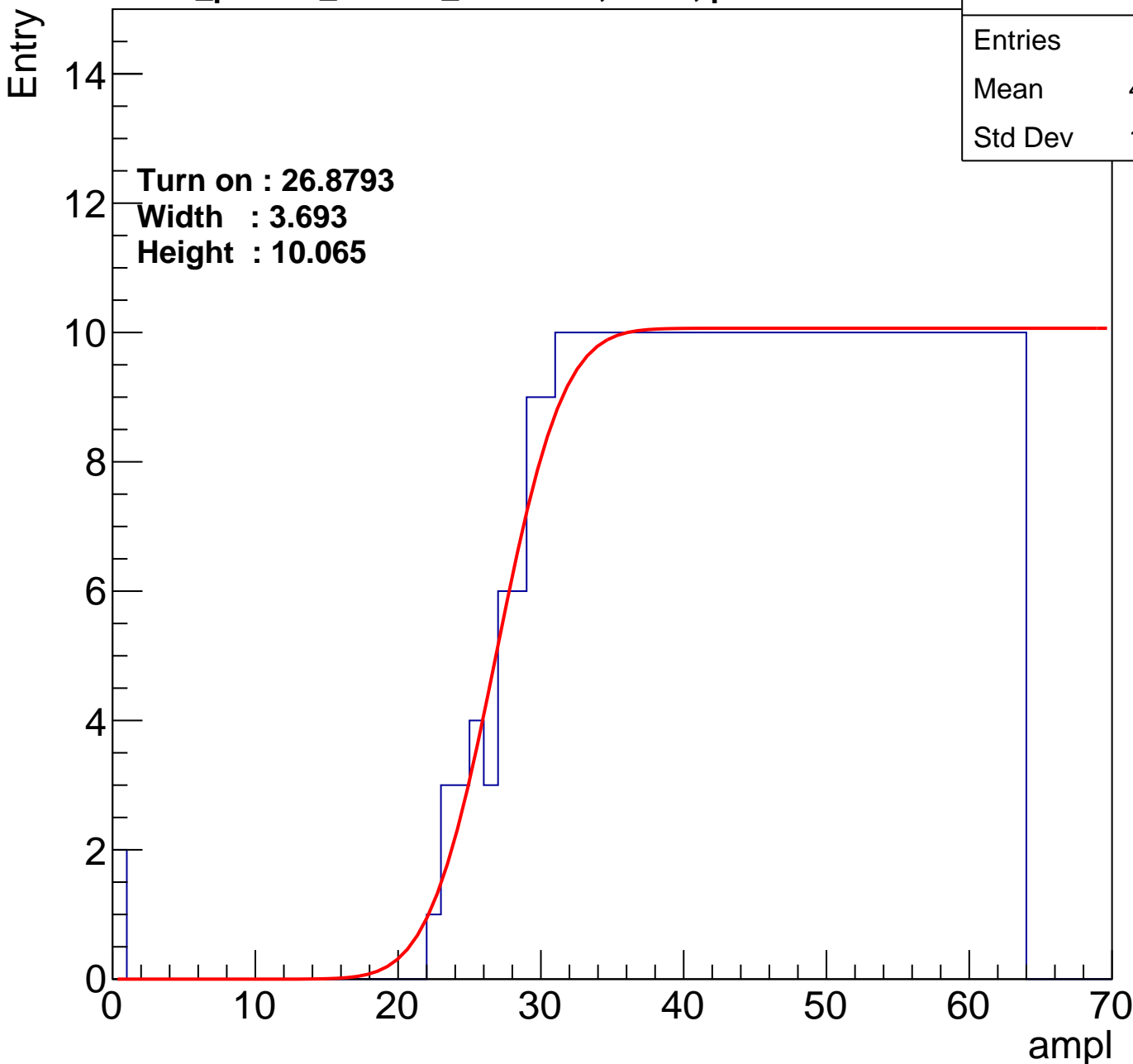
**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	376
Mean	44.45
Std Dev	11.44

**Turn on : 26.8793**

**Width : 3.693**

**Height : 10.065**



# B1L100S, U15-ch40

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	379
Mean	44.22
Std Dev	11.71

Turn on : 26.7008

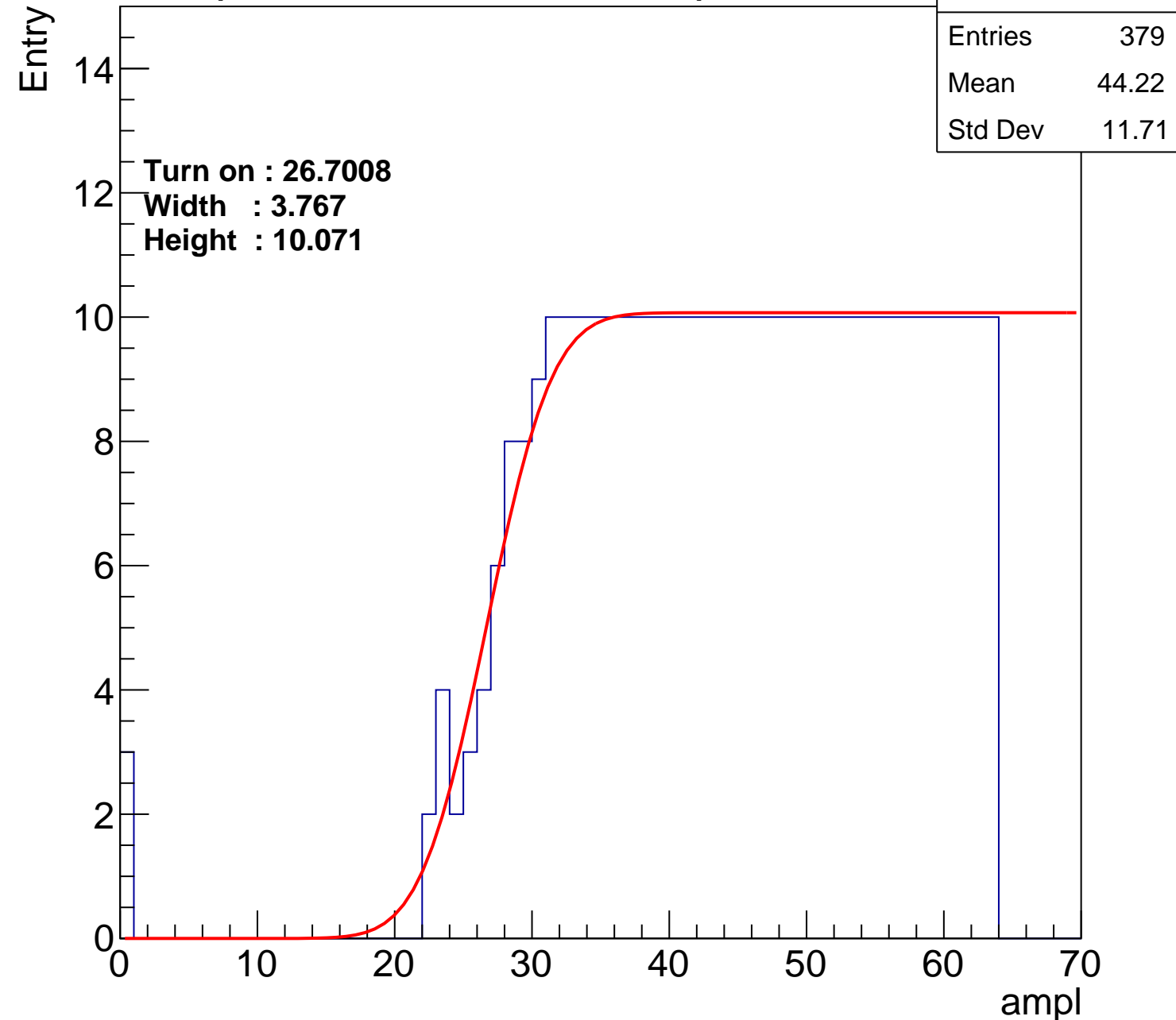
Width : 3.767

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch41

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.39
Std Dev	11.61

Turn on : 27.2442

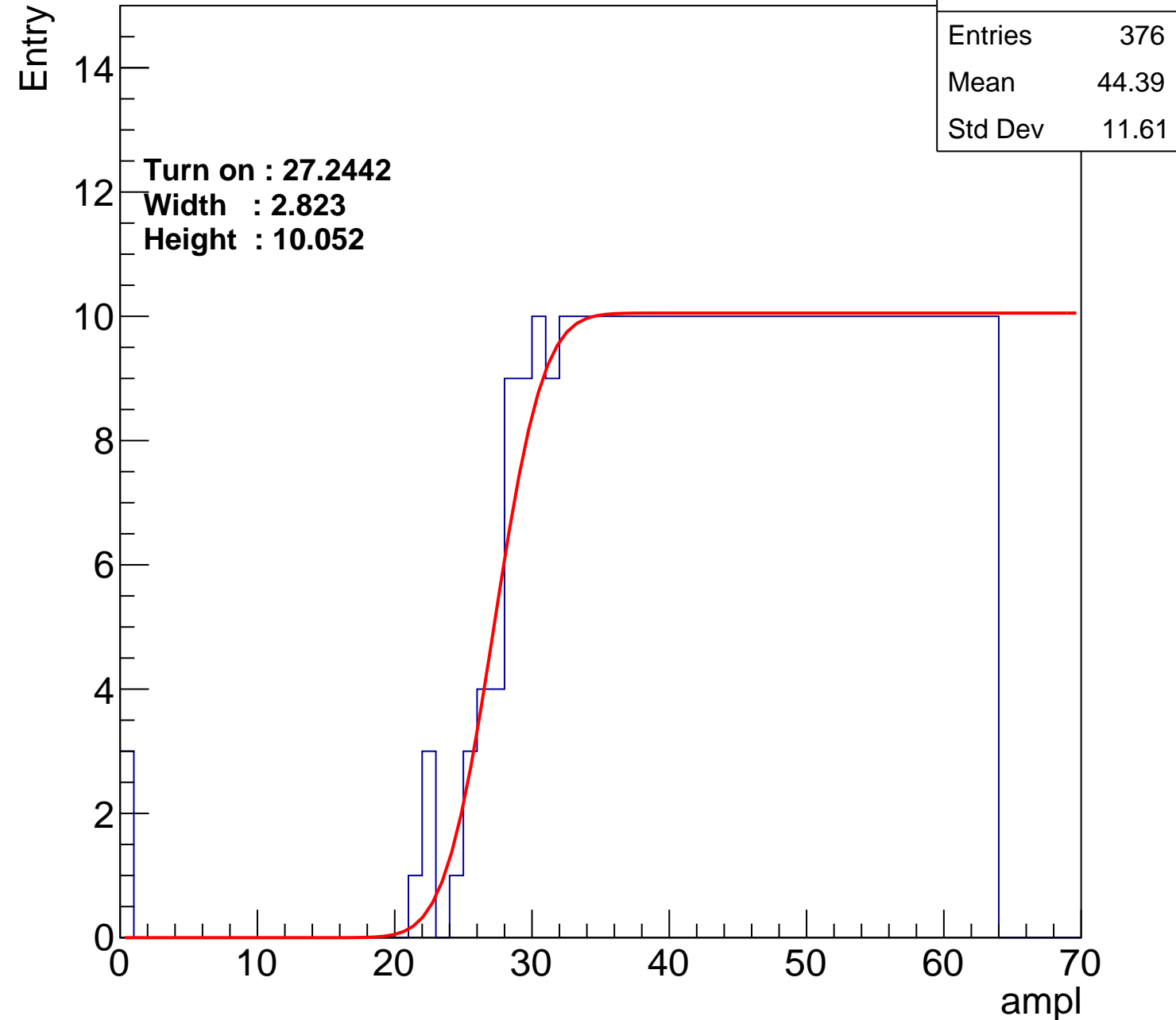
Width : 2.823

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch42

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	396
Mean	43.43
Std Dev	12.07

Turn on : 25.0779

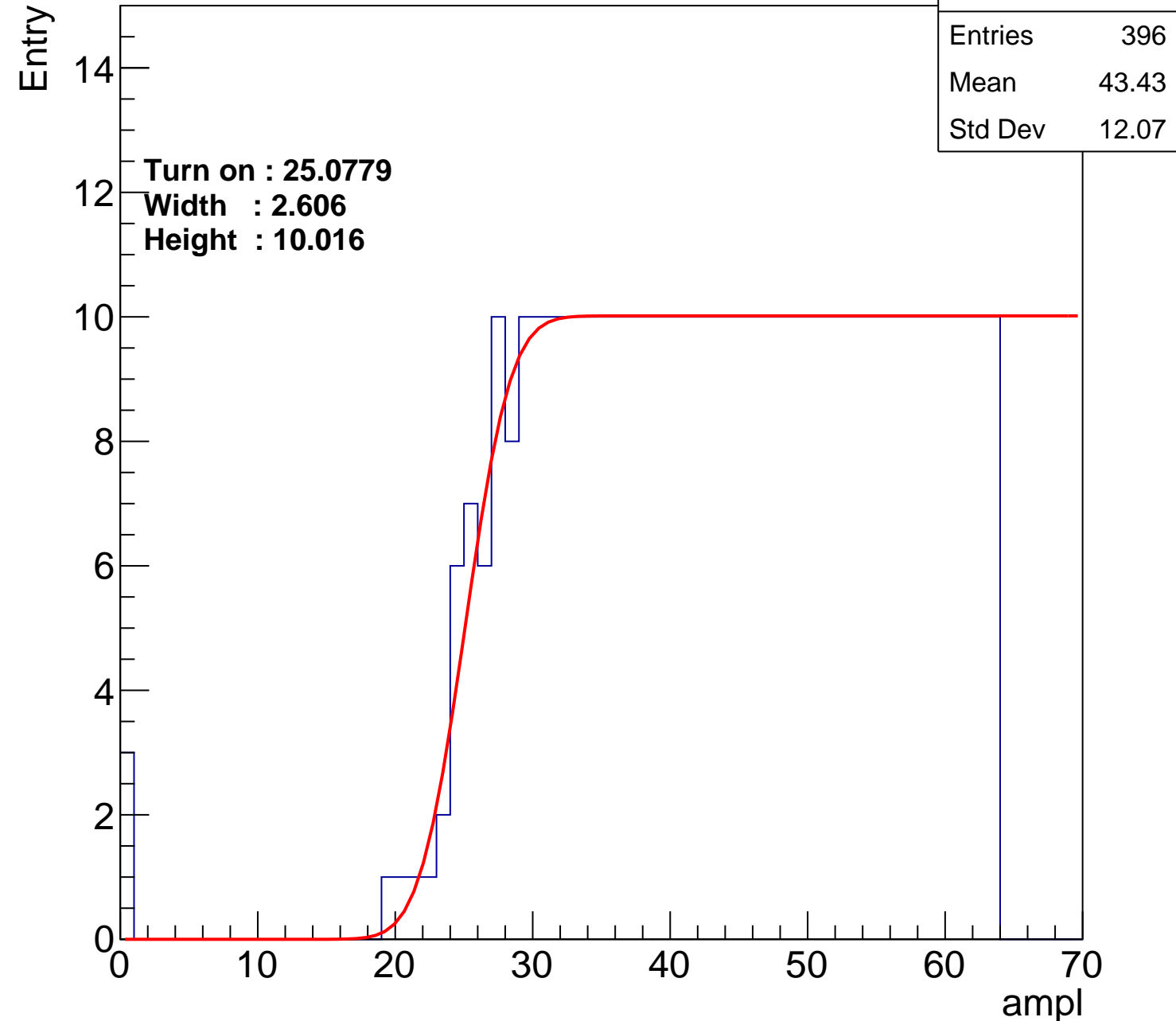
Width : 2.606

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch43

calib\_packv5\_042523\_0143.root, FC#4, port A2

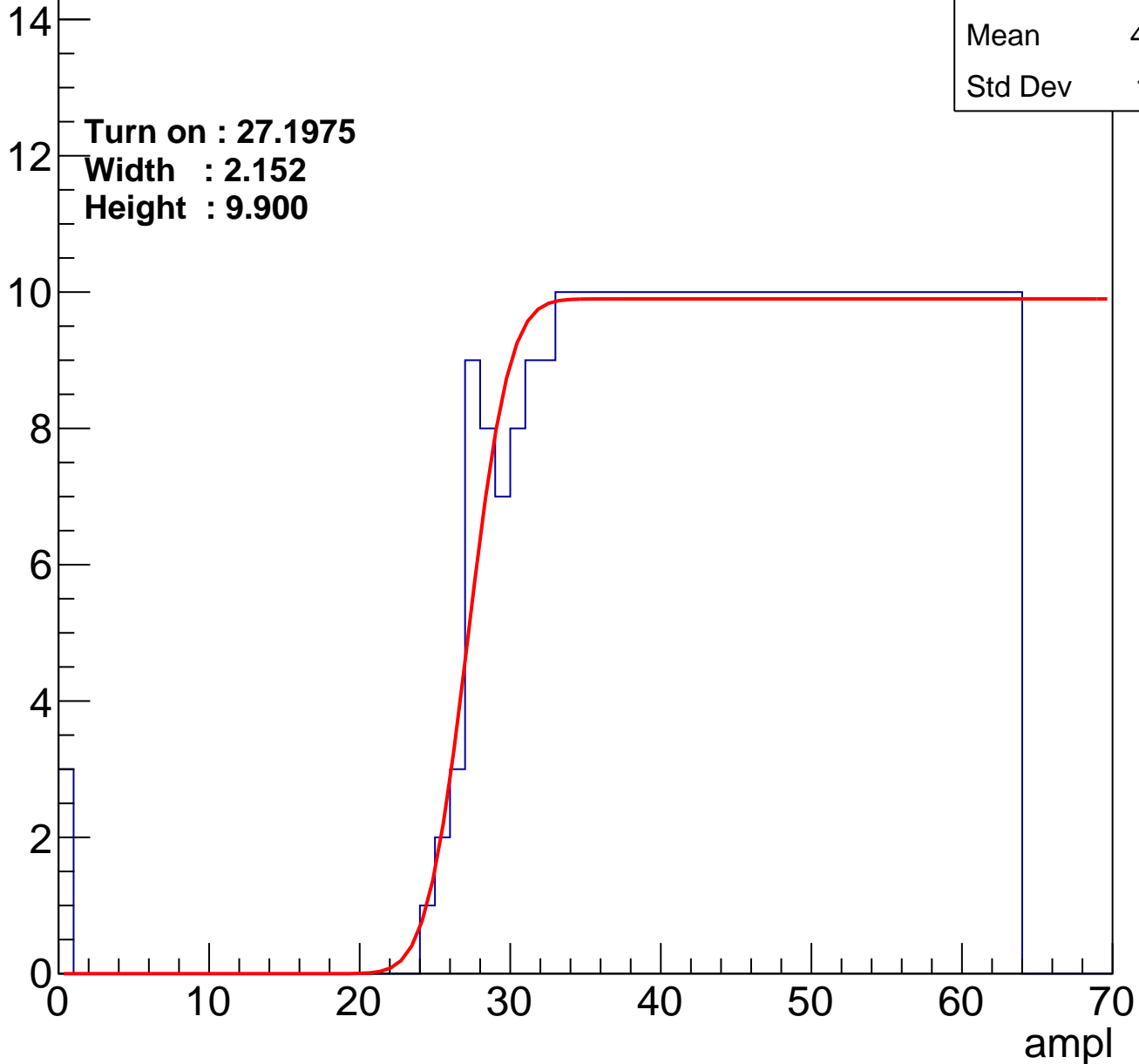
Entries	369
Mean	44.74
Std Dev	11.41

**Turn on : 27.1975**

**Width : 2.152**

**Height : 9.900**

Entry



# B1L100S, U15-ch44

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 27.0830

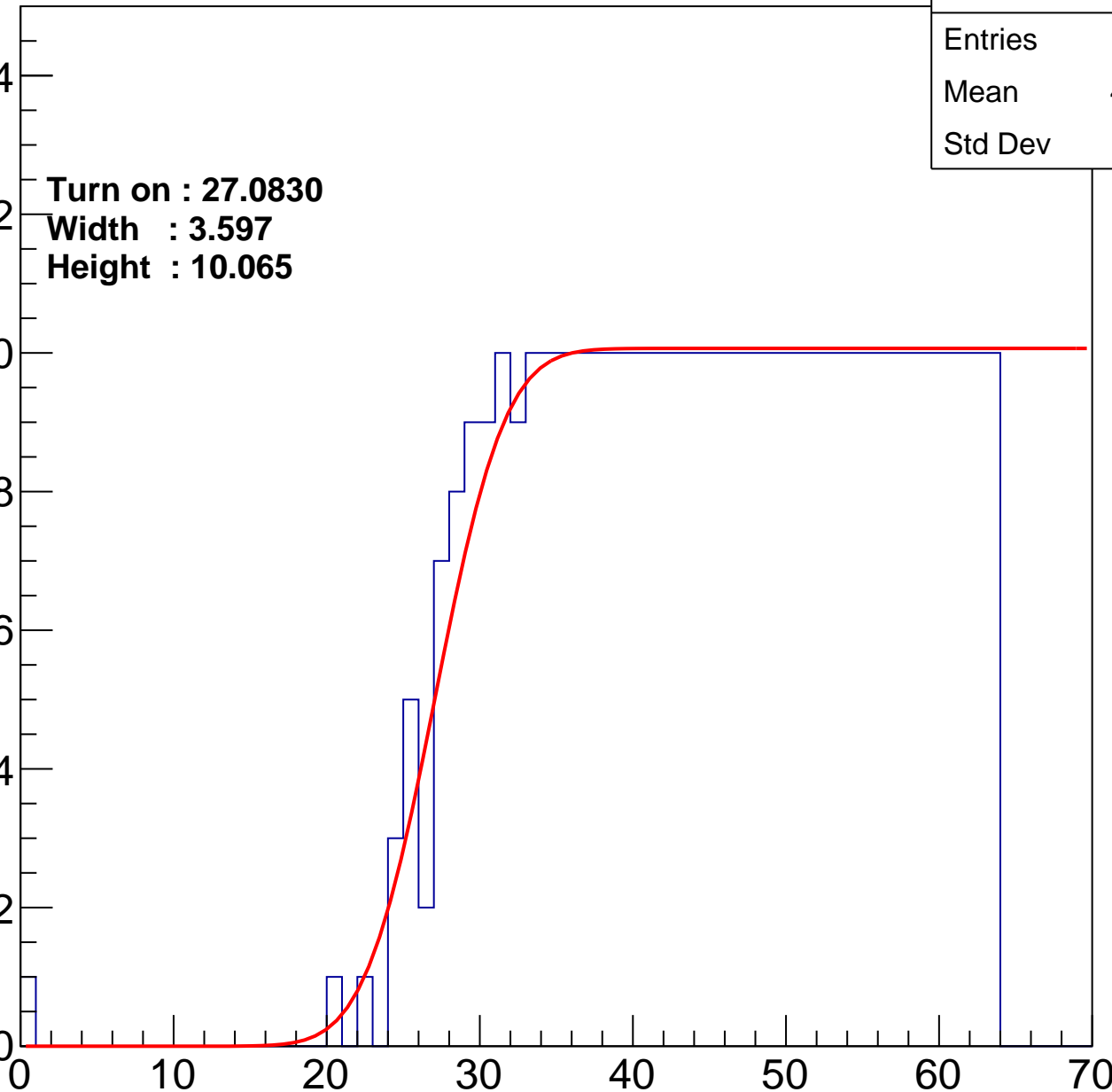
Width : 3.597

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch45

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.0746

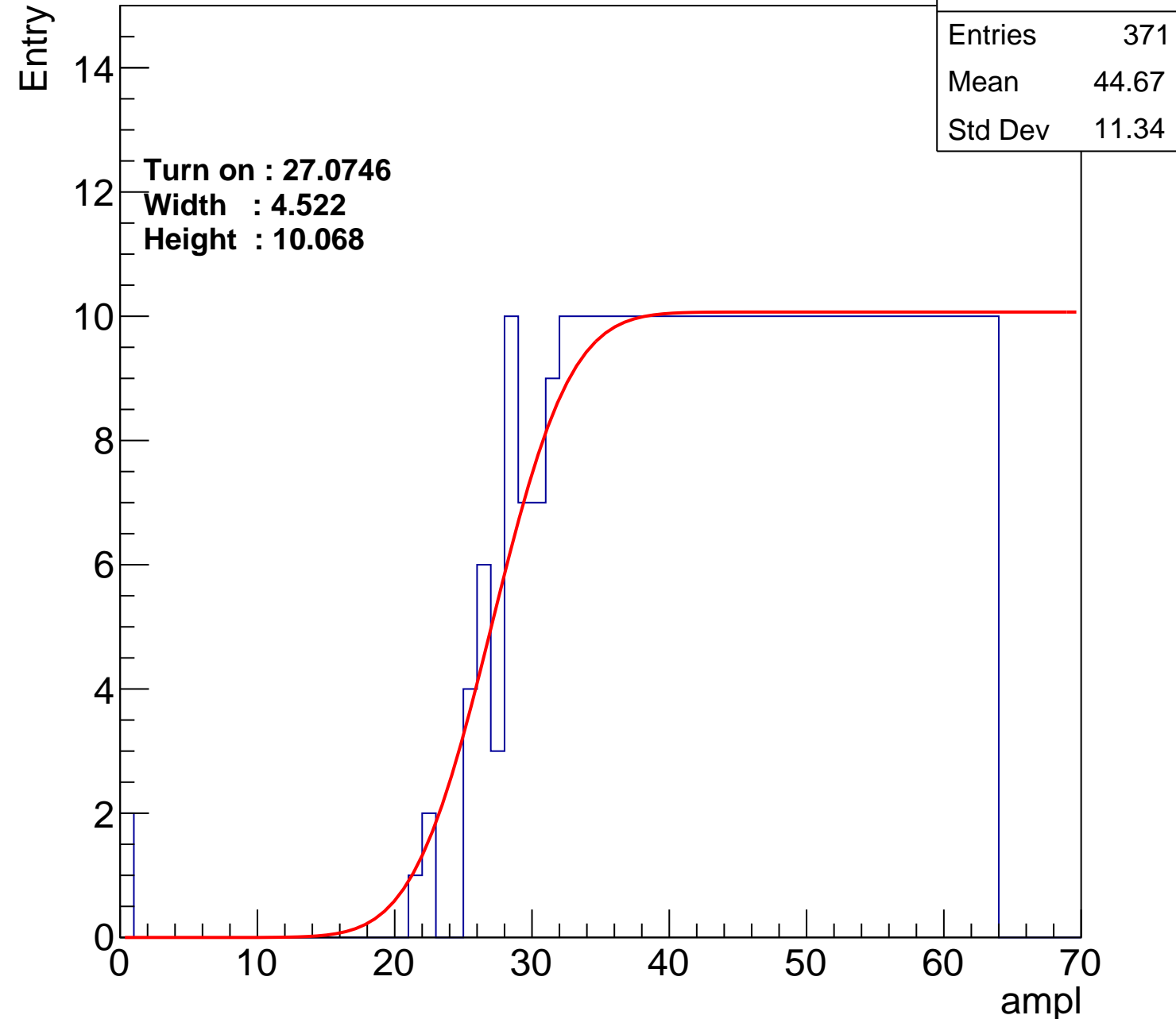
Width : 4.522

Height : 10.068

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch46

calib\_packv5\_042523\_0143.root, FC#4, port A2

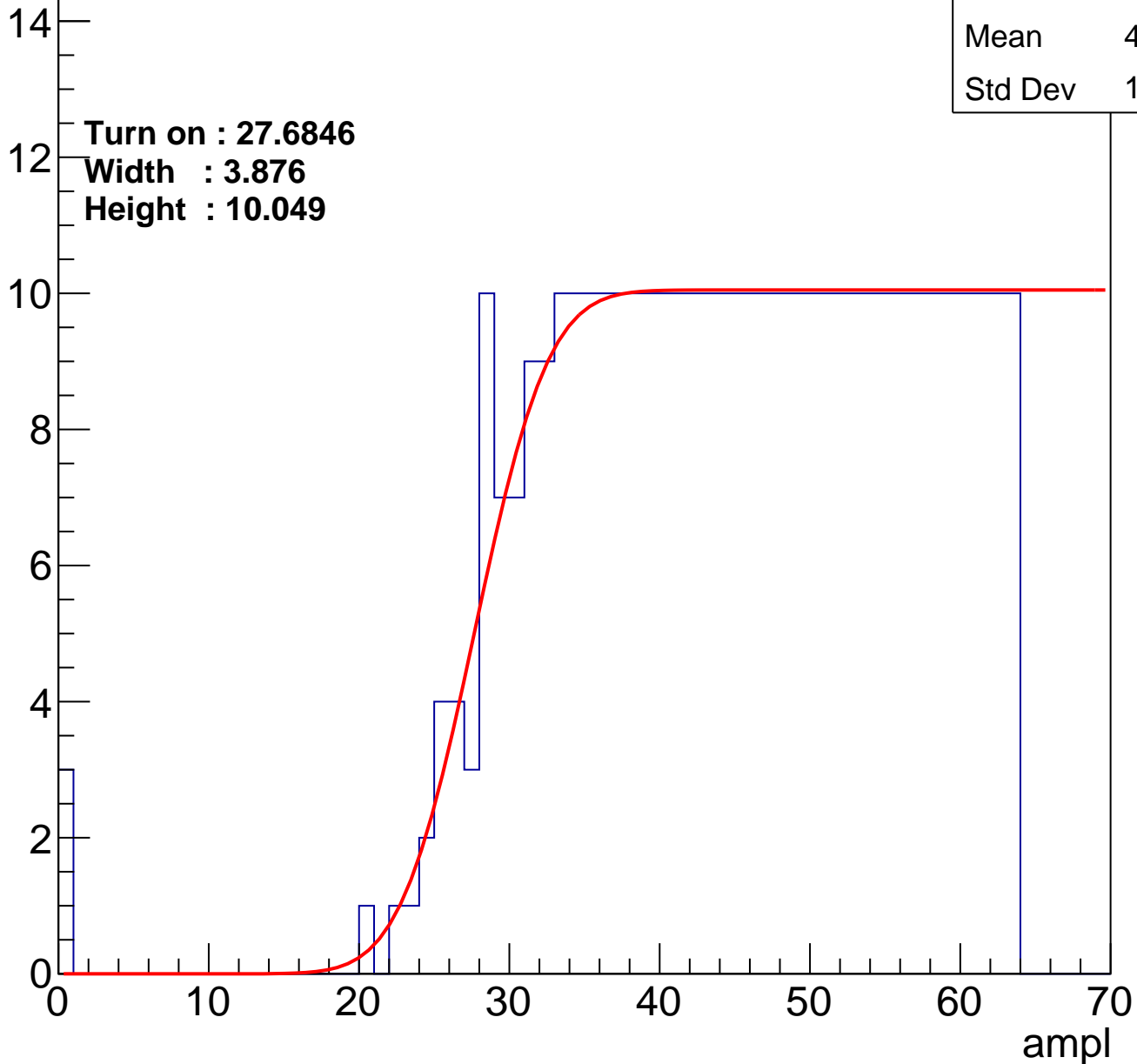
Entries	371
Mean	44.58
Std Dev	11.57

Turn on : 27.6846

Width : 3.876

Height : 10.049

Entry





# B1L100S, U15-ch47

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	371
Mean	44.5
Std Dev	11.85

Turn on : 27.2283

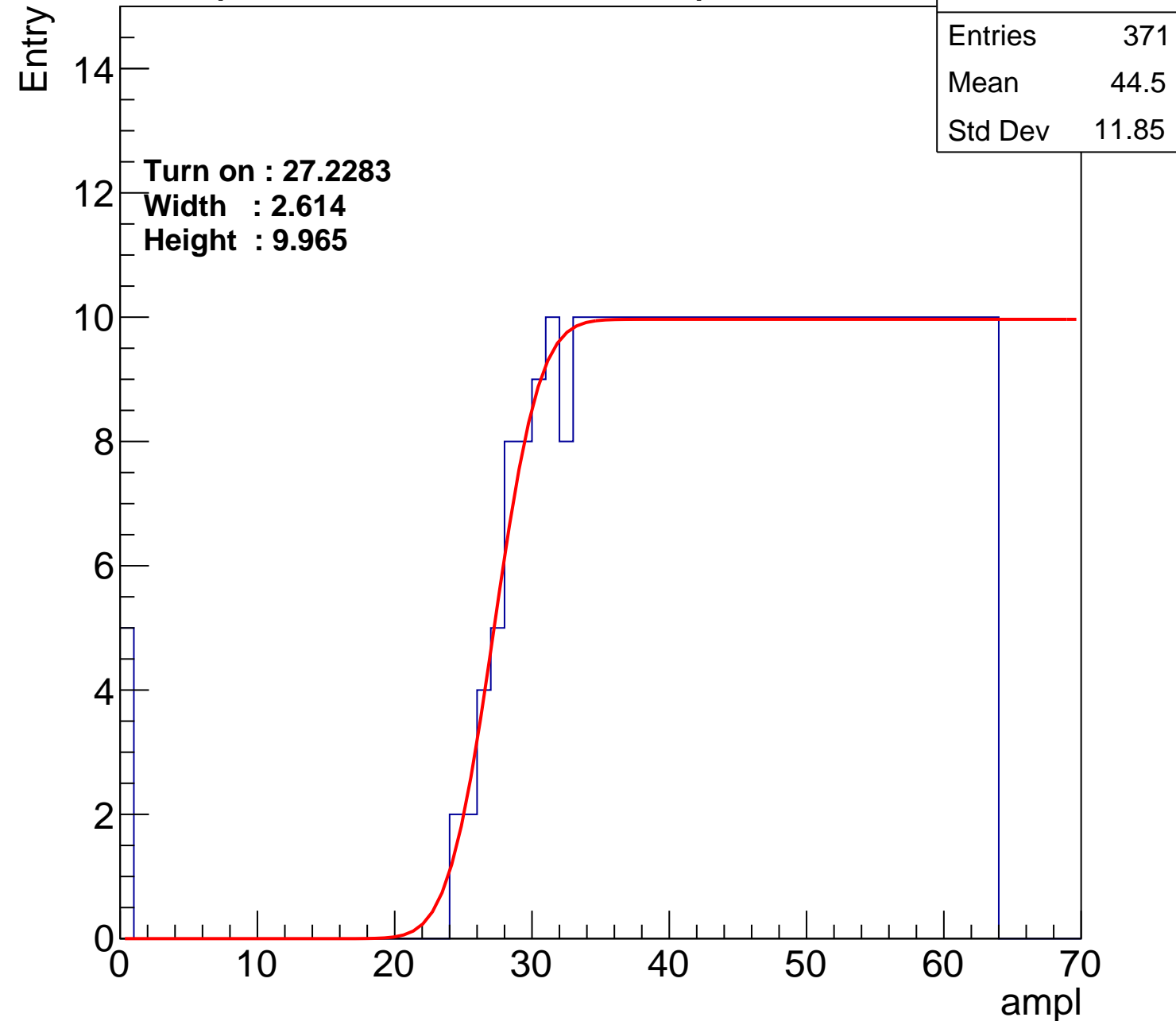
Width : 2.614

Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch48

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.73
Std Dev	11.43

Turn on : 27.6838

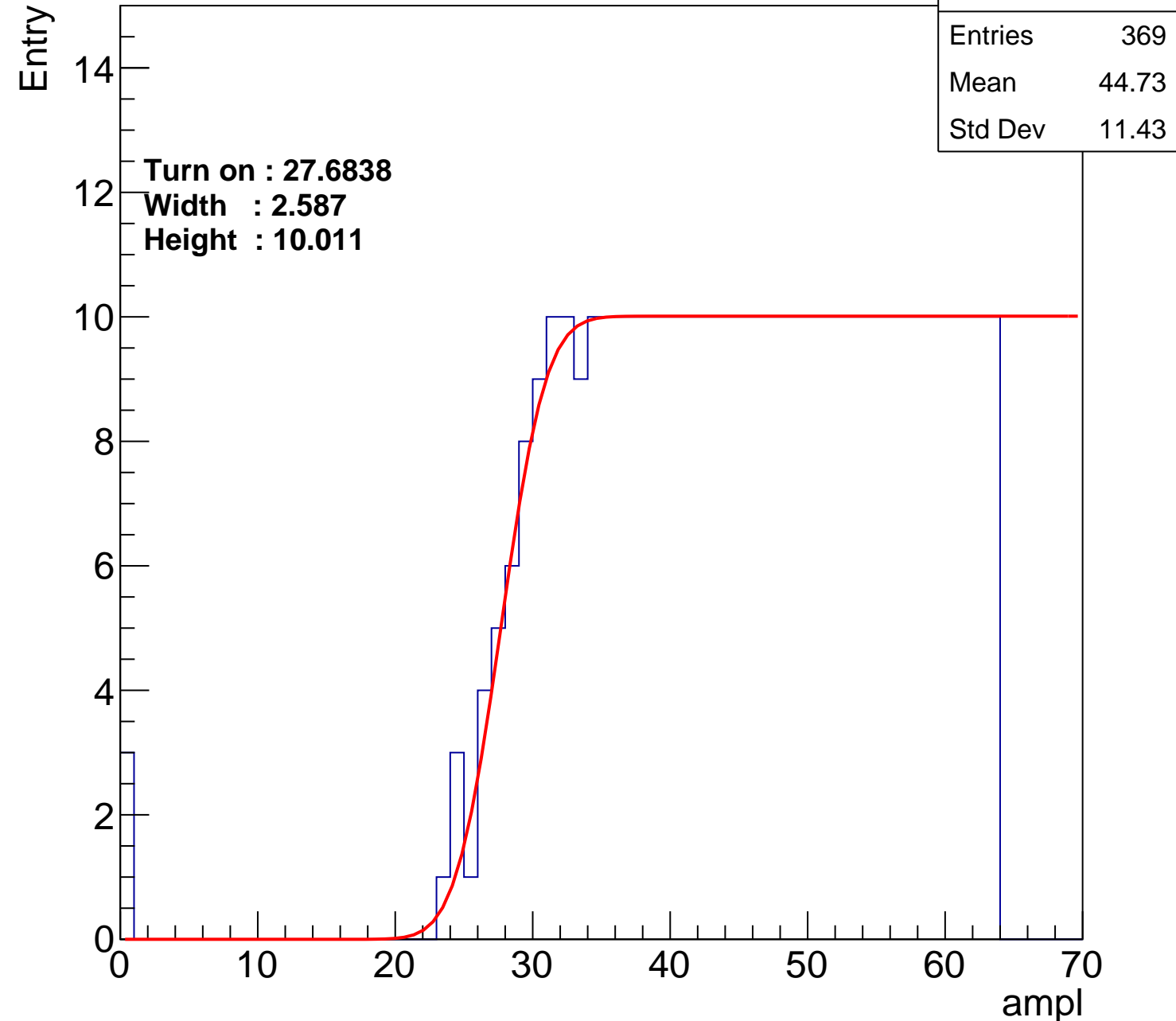
Width : 2.587

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch49

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	390
Mean	43.87
Std Dev	11.54

Turn on : 25.3895

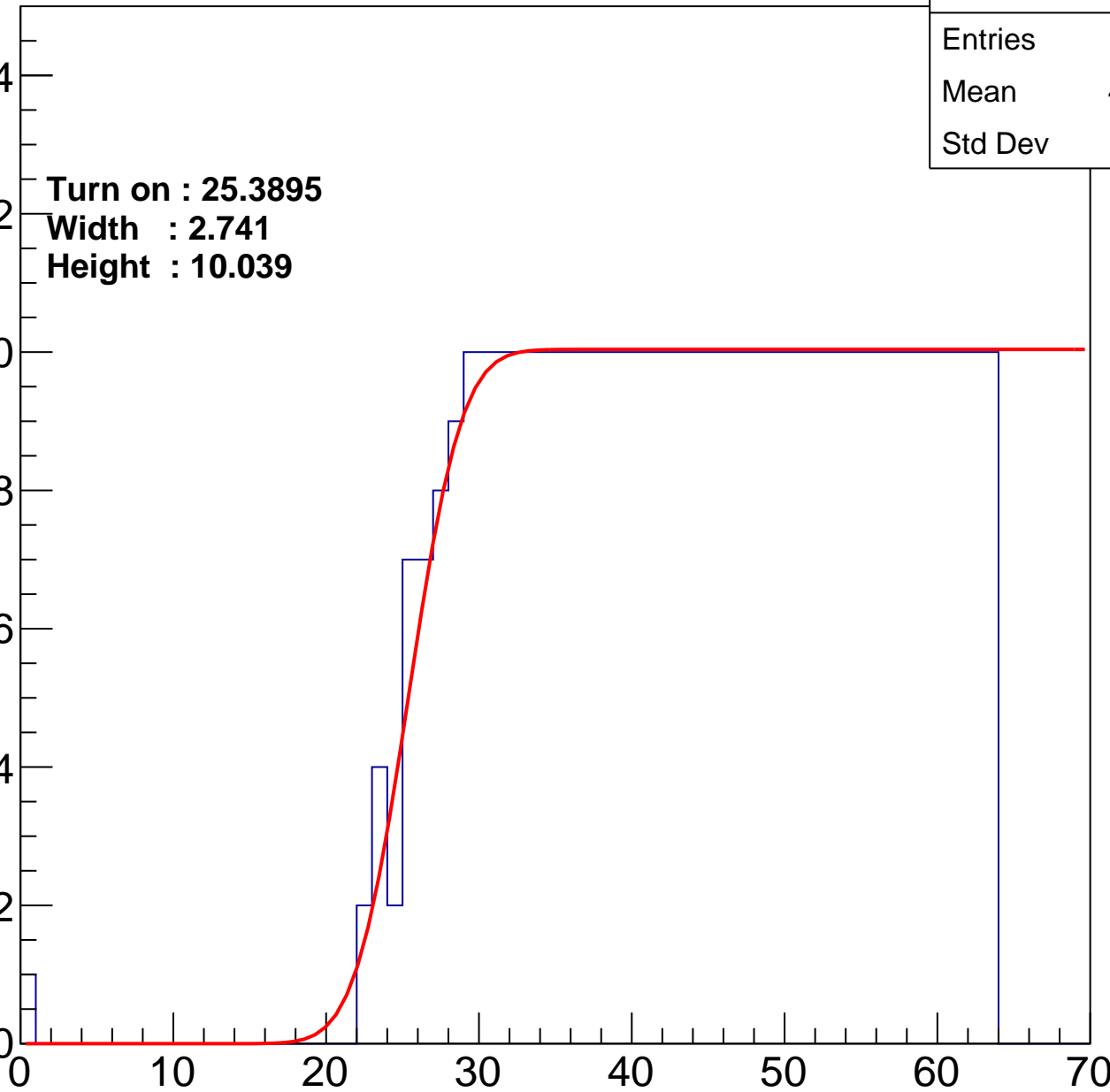
Width : 2.741

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch50

calib\_packv5\_042523\_0143.root, FC#4, port A2

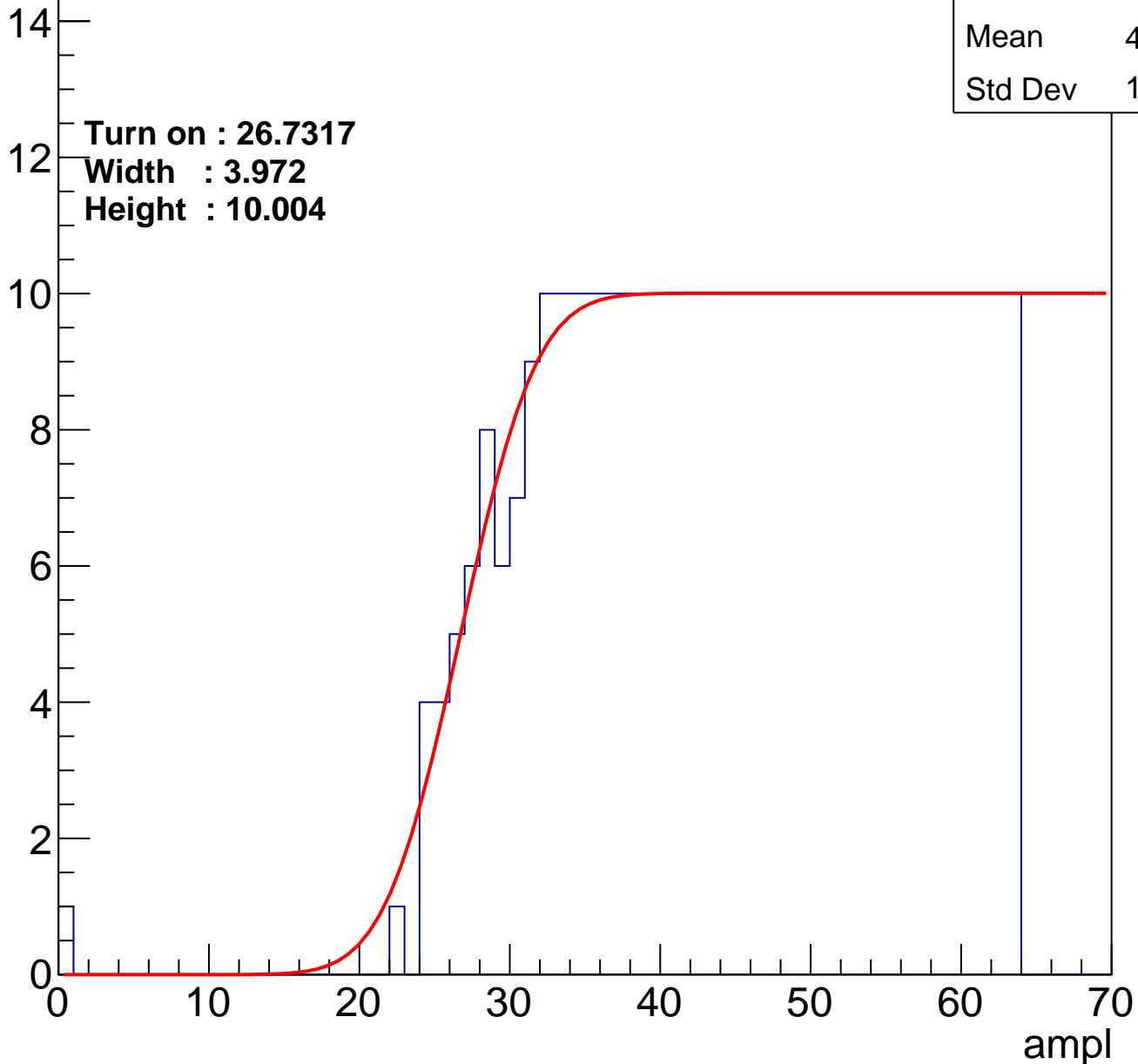
Entries	371
Mean	44.74
Std Dev	11.15

**Turn on : 26.7317**

**Width : 3.972**

**Height : 10.004**

Entry



# B1L100S, U15-ch51

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	393
Mean	43.47
Std Dev	12.22

Turn on : 25.5824

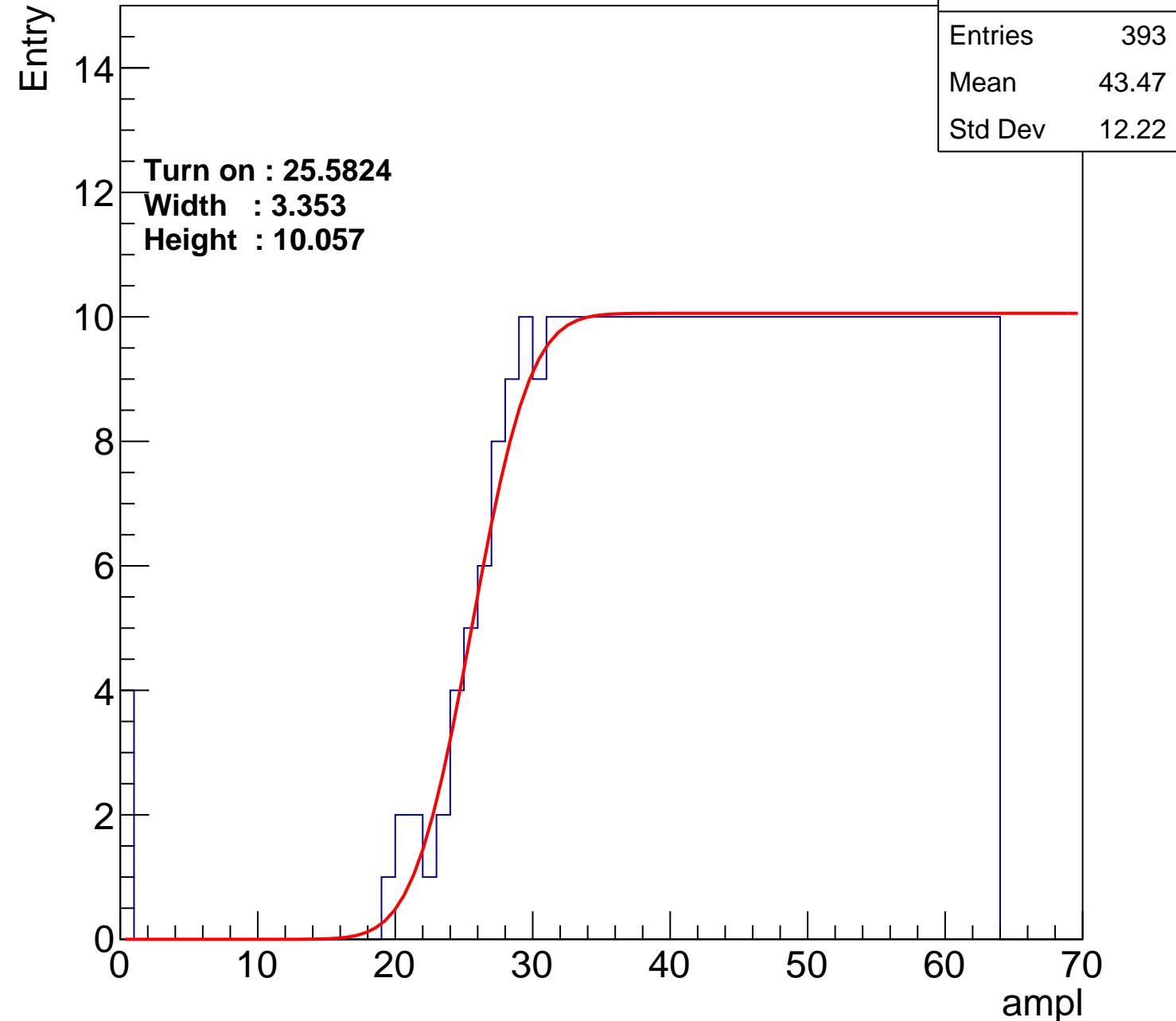
Width : 3.353

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch52

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	398
Mean	43.4
Std Dev	11.94

Turn on : 24.3644

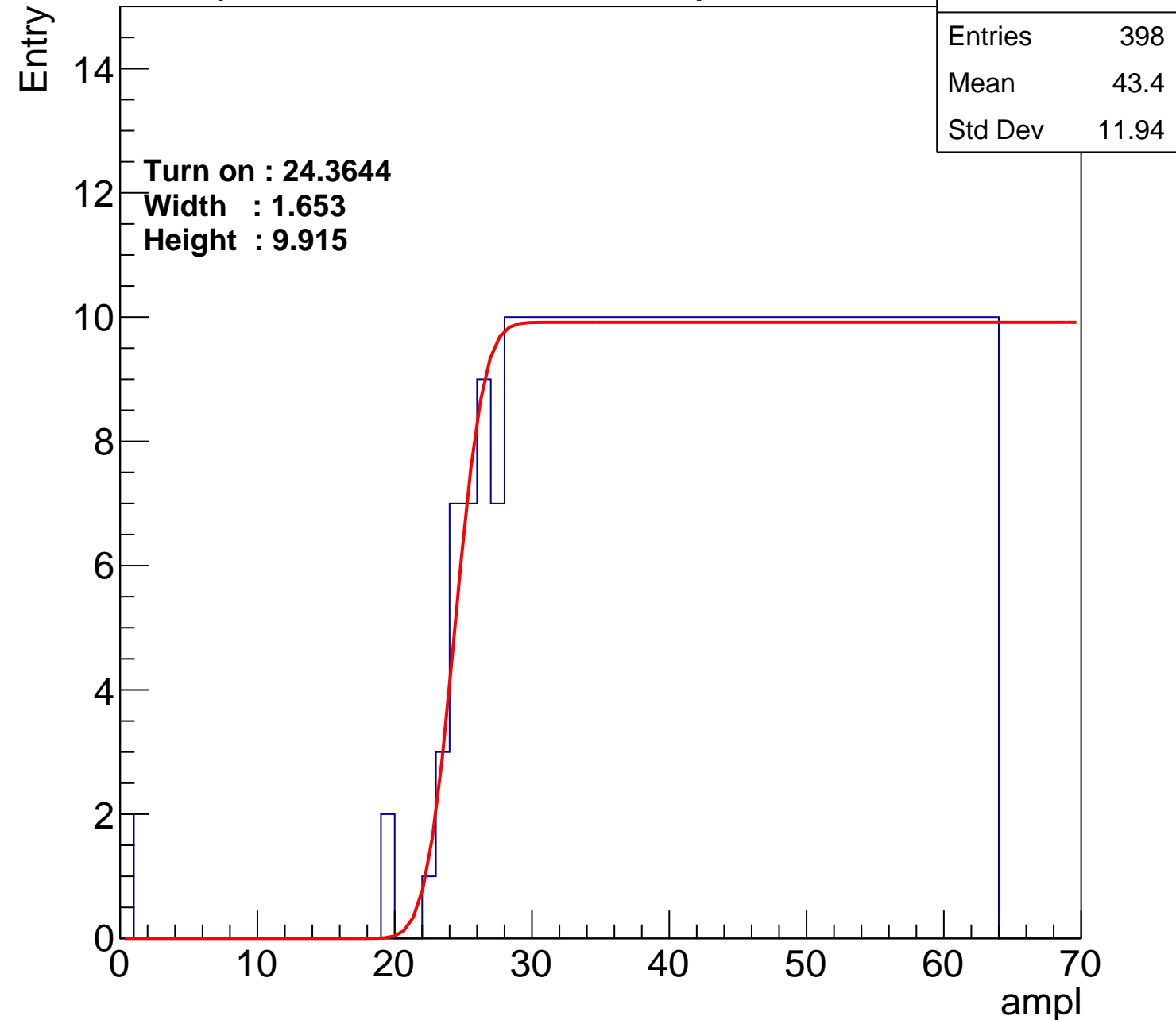
Width : 1.653

Height : 9.915

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch53

calib\_packv5\_042523\_0143.root, FC#4, port A2

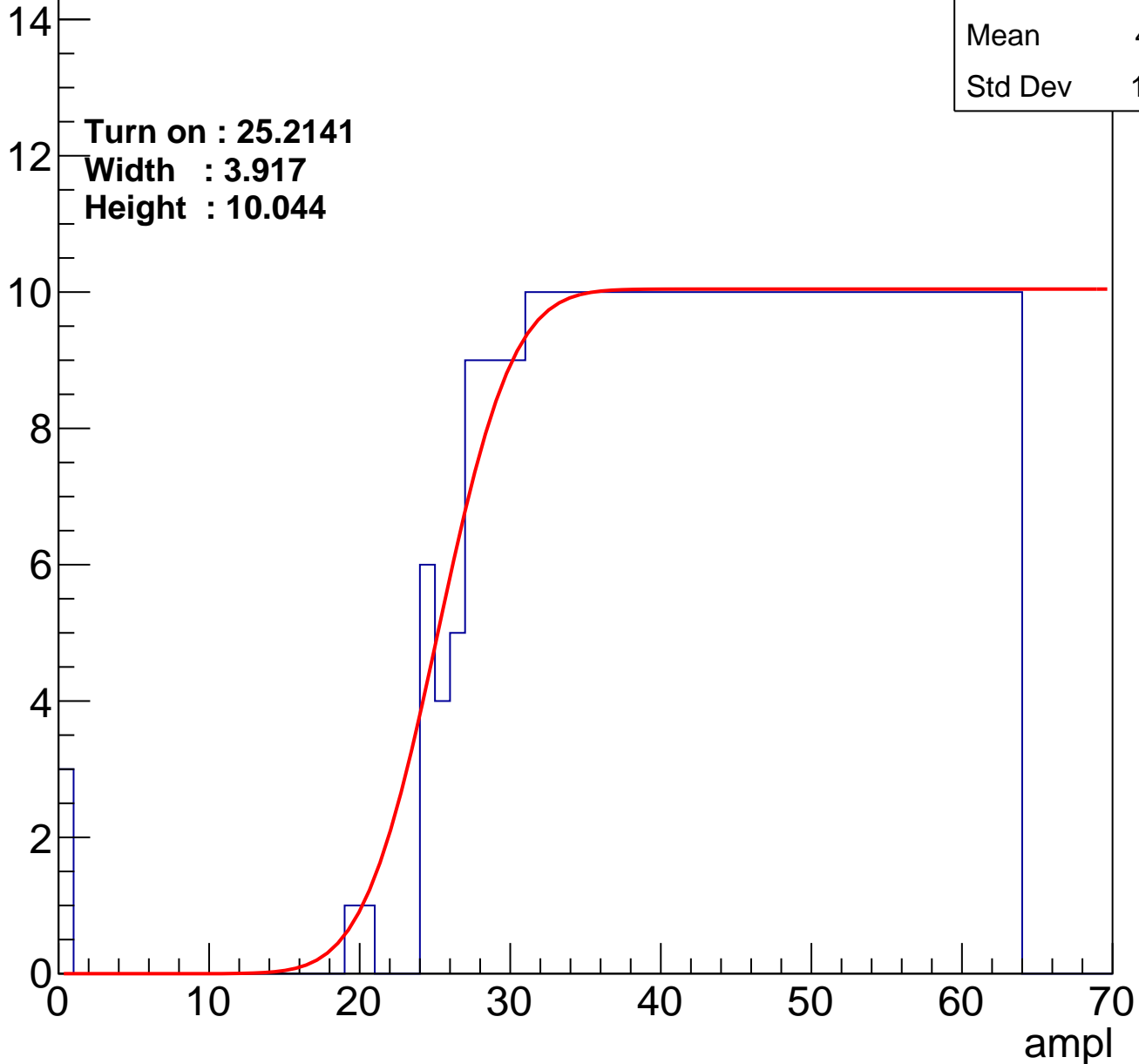
Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.2141

Width : 3.917

Height : 10.044

Entry



# B1L100S, U15-ch54

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	43.99
Std Dev	11.86

Turn on : 26.4663

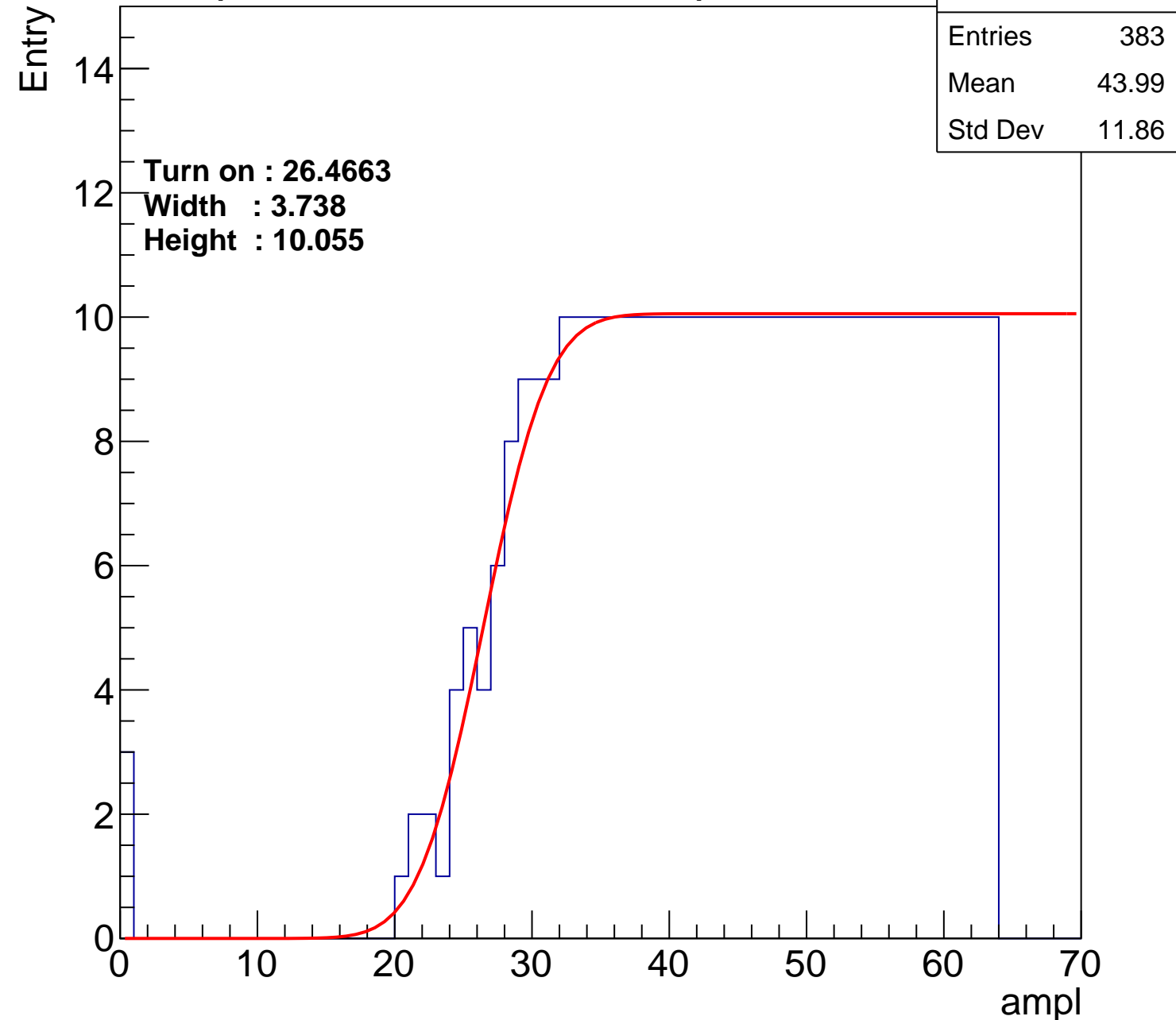
Width : 3.738

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch55

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	374
Mean	44.64
Std Dev	11.15

Turn on : 27.1863

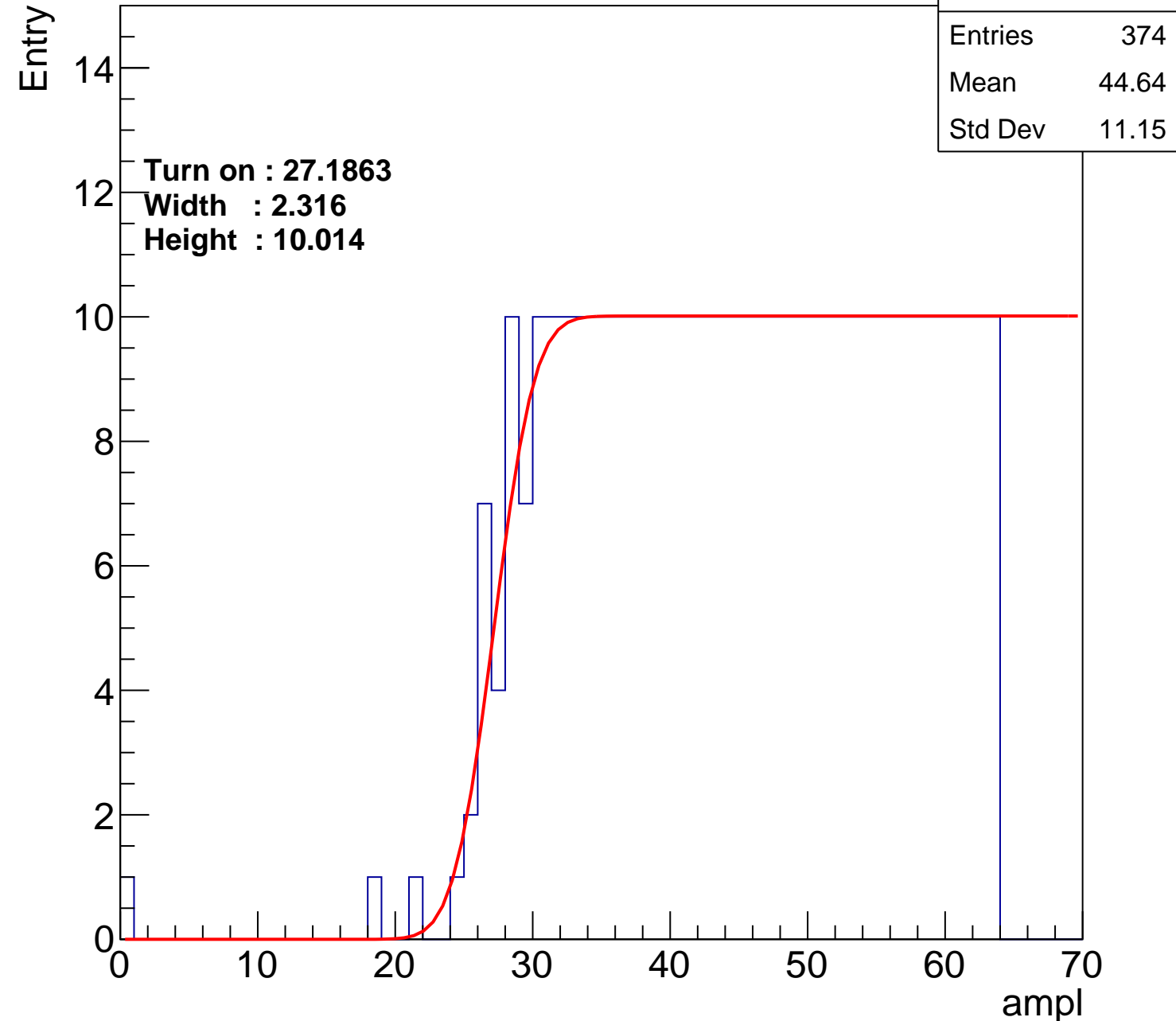
Width : 2.316

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch56

calib\_packv5\_042523\_0143.root, FC#4, port A2

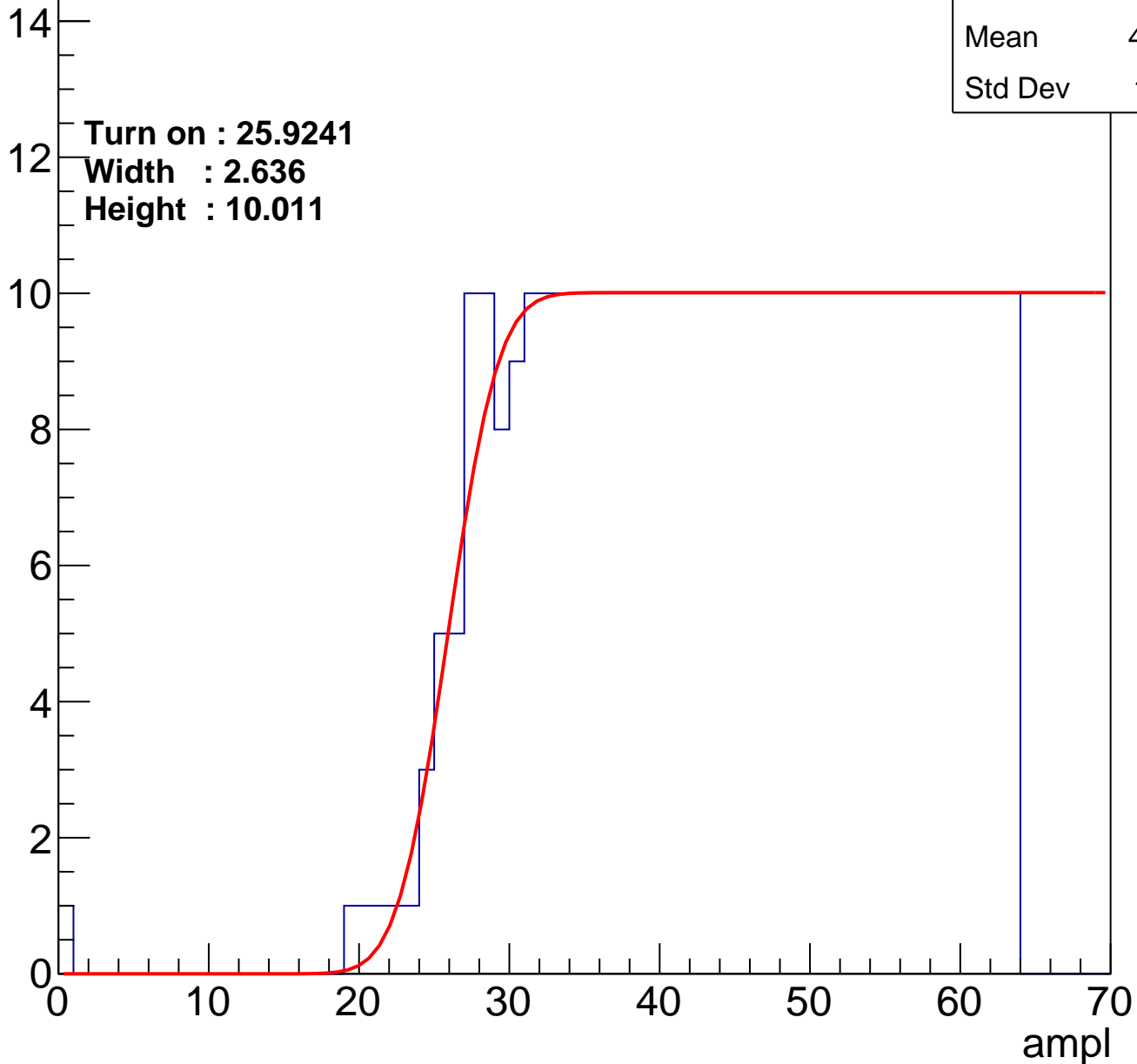
Entries	386
Mean	44.03
Std Dev	11.51

Turn on : 25.9241

Width : 2.636

Height : 10.011

Entry



# B1L100S, U15-ch57

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	372
Mean	44.7
Std Dev	11.15

Turn on : 27.5857

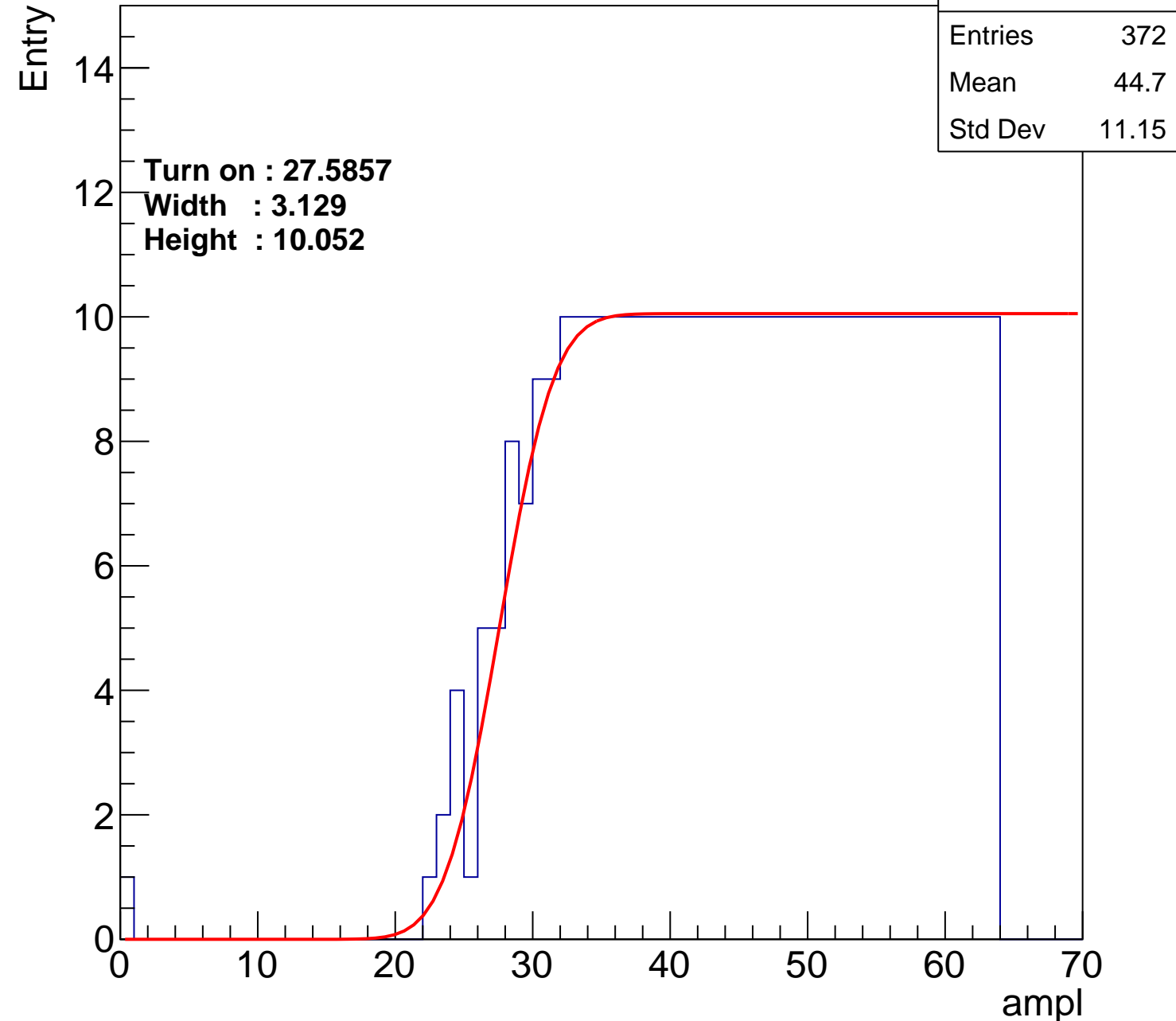
Width : 3.129

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch58

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	399
Mean	43.28
Std Dev	12.14

Turn on : 24.2988

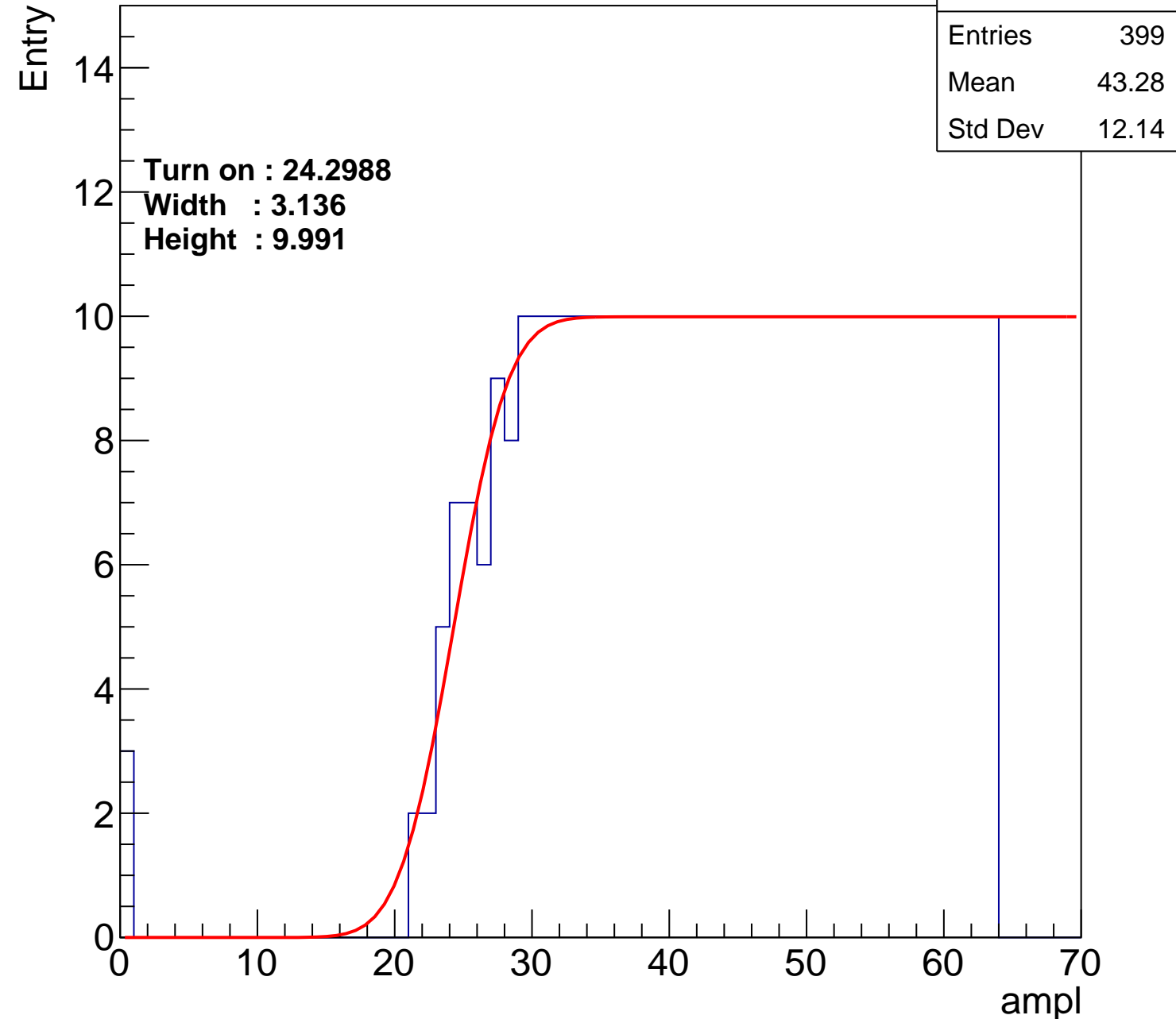
Width : 3.136

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch59

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.21
Std Dev	11.61

Turn on : 26.4299

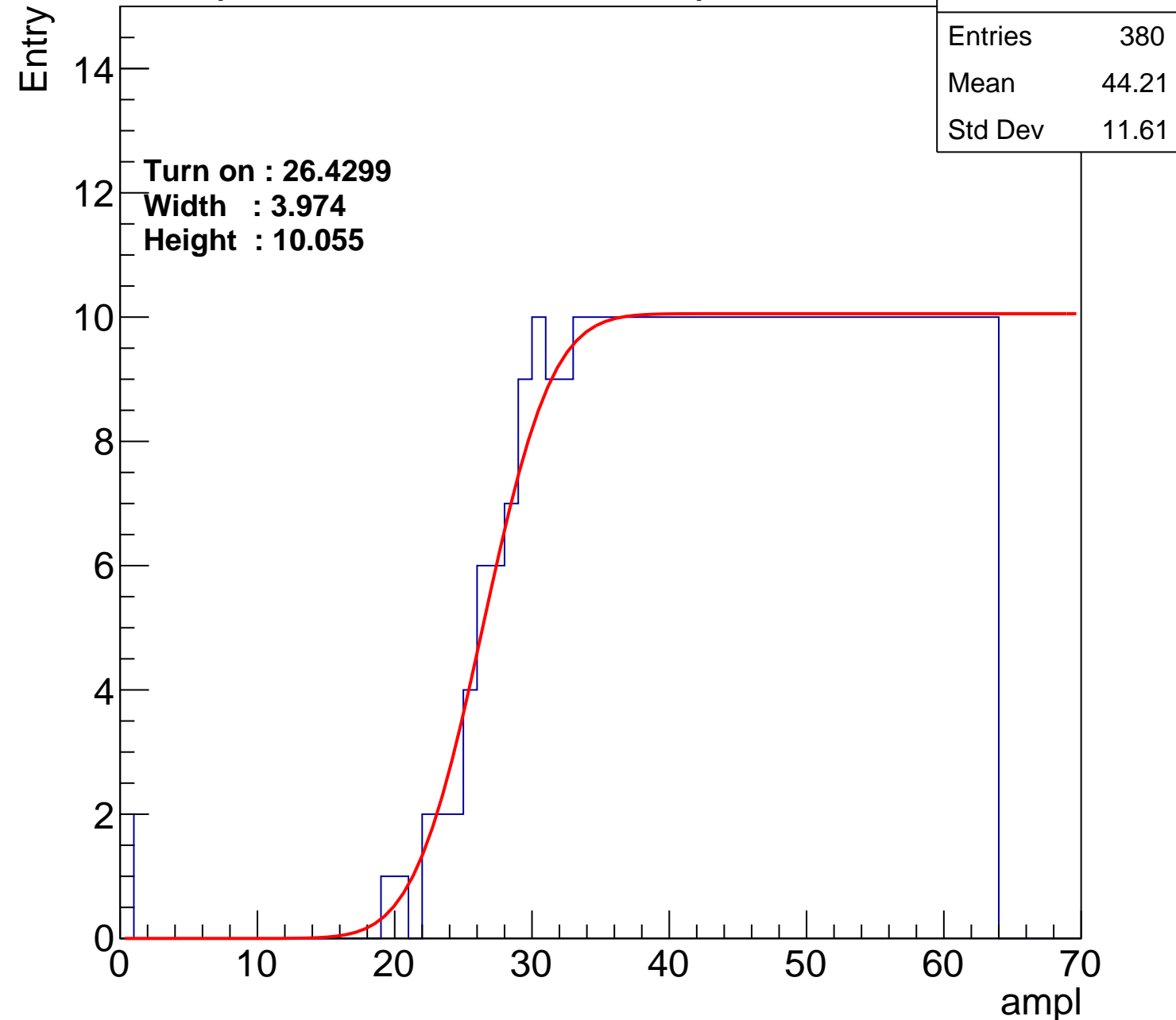
Width : 3.974

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch60

calib\_packv5\_042523\_0143.root, FC#4, port A2

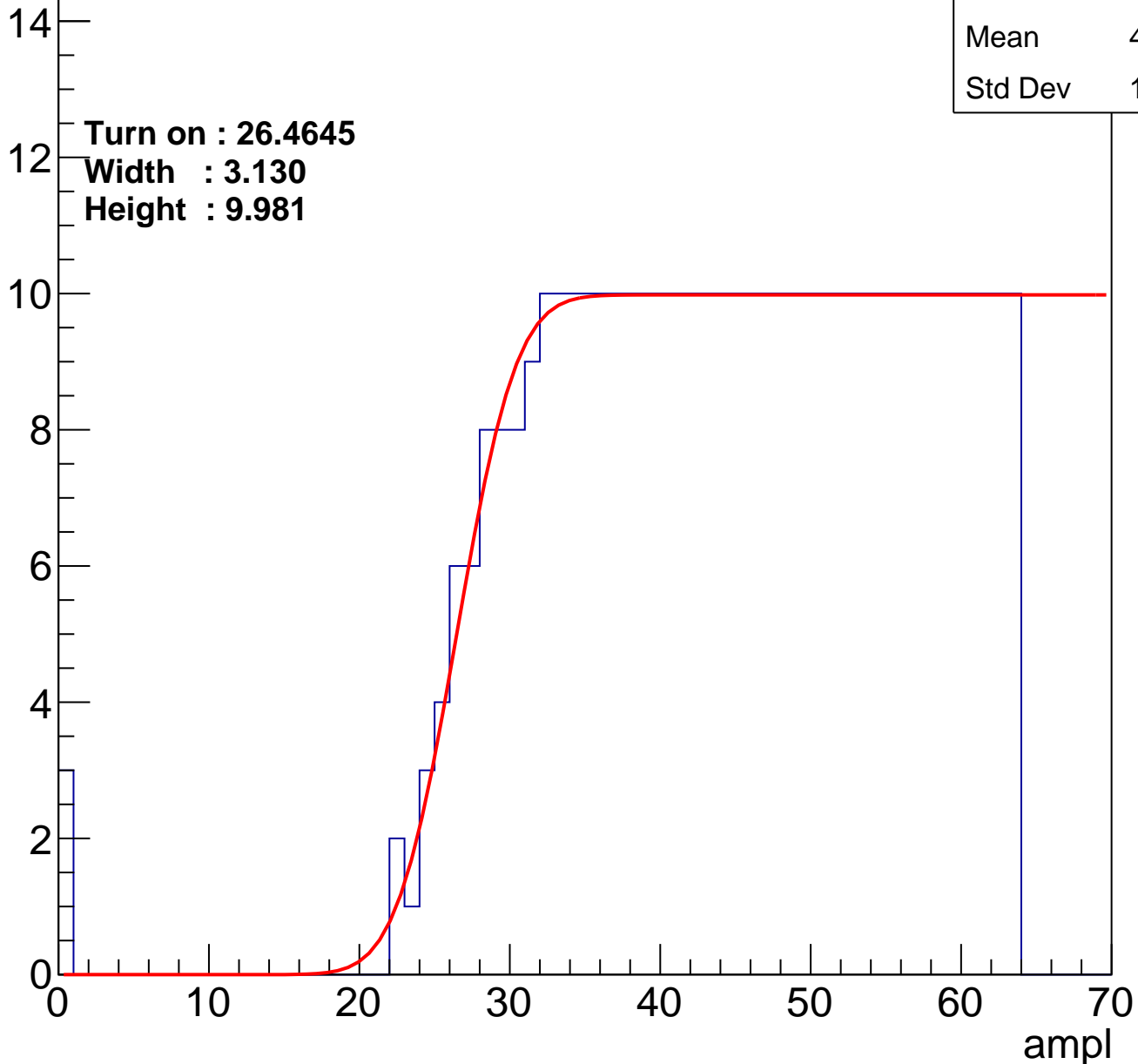
Entries	378
Mean	44.26
Std Dev	11.69

Turn on : 26.4645

Width : 3.130

Height : 9.981

Entry



# B1L100S, U15-ch61

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	374
Mean	44.51
Std Dev	11.44

Turn on : 27.0082

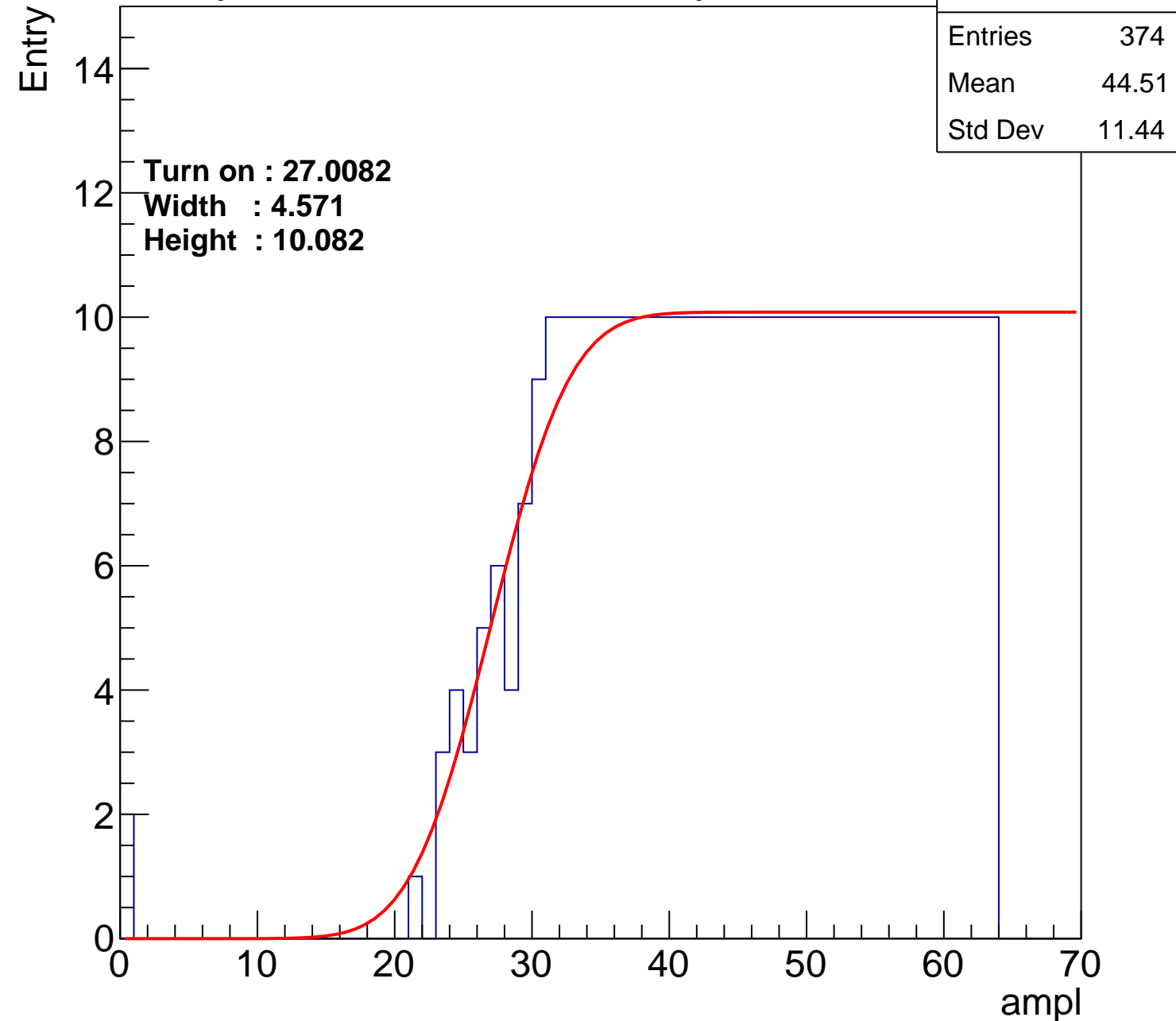
Width : 4.571

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch62

calib\_packv5\_042523\_0143.root, FC#4, port A2

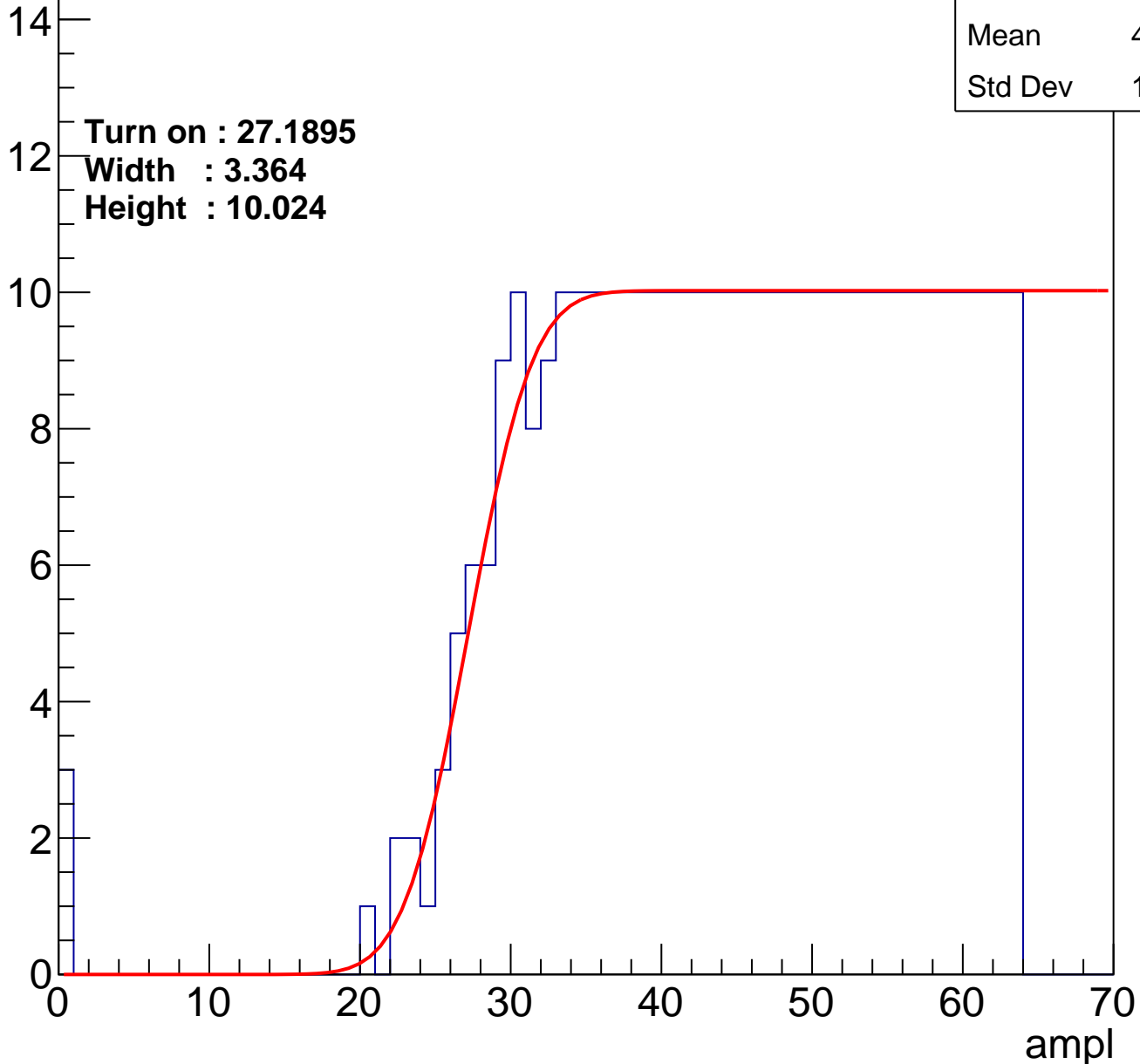
Entries	375
Mean	44.39
Std Dev	11.66

Turn on : 27.1895

Width : 3.364

Height : 10.024

Entry





# B1L100S, U15-ch63

calib\_packv5\_042523\_0143.root, FC#4, port A2

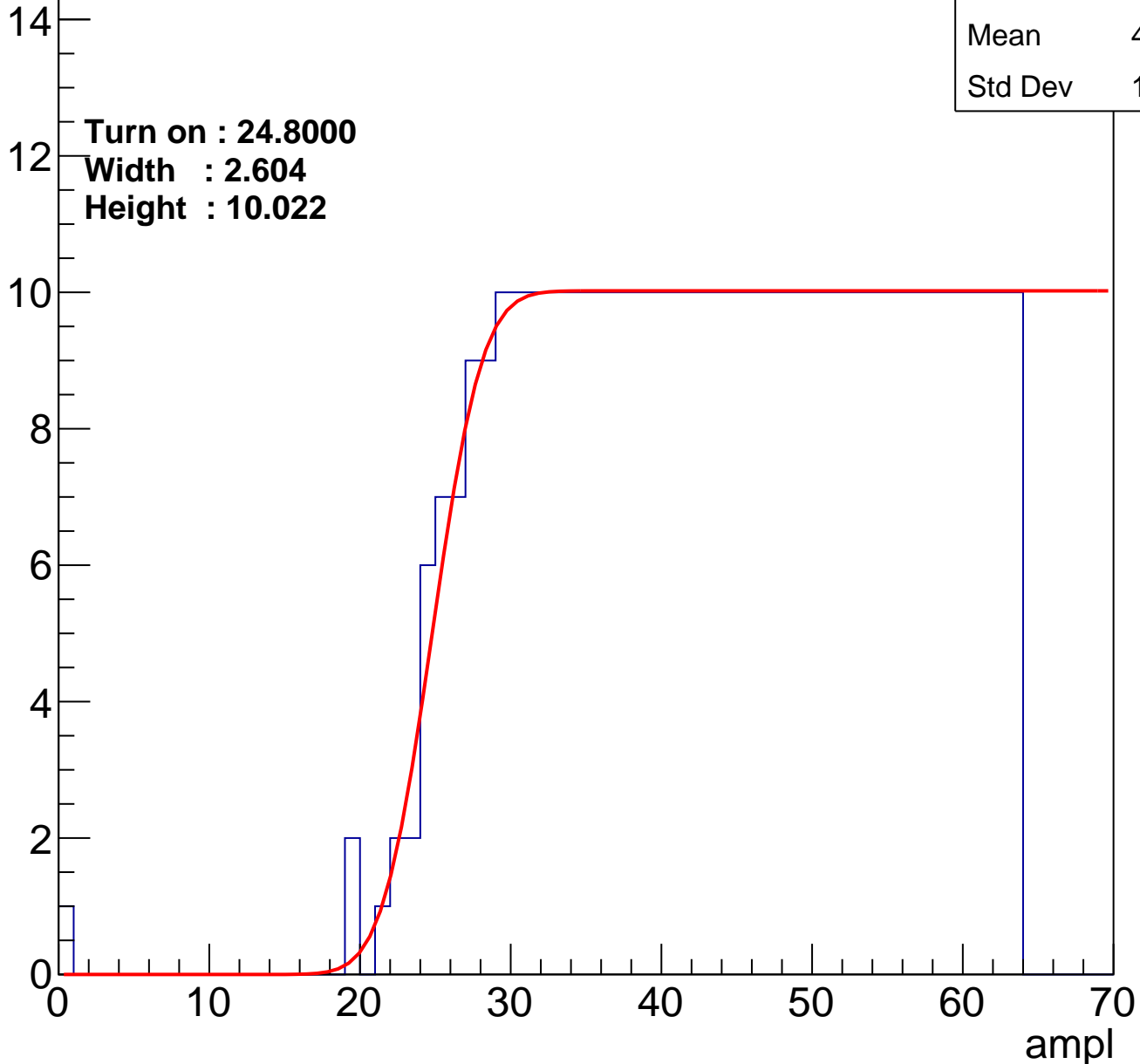
Entries	396
Mean	43.55
Std Dev	11.75

Turn on : 24.8000

Width : 2.604

Height : 10.022

Entry



# B1L100S, U15-ch64

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	386
Mean	43.99
Std Dev	11.56

Turn on : 26.0358

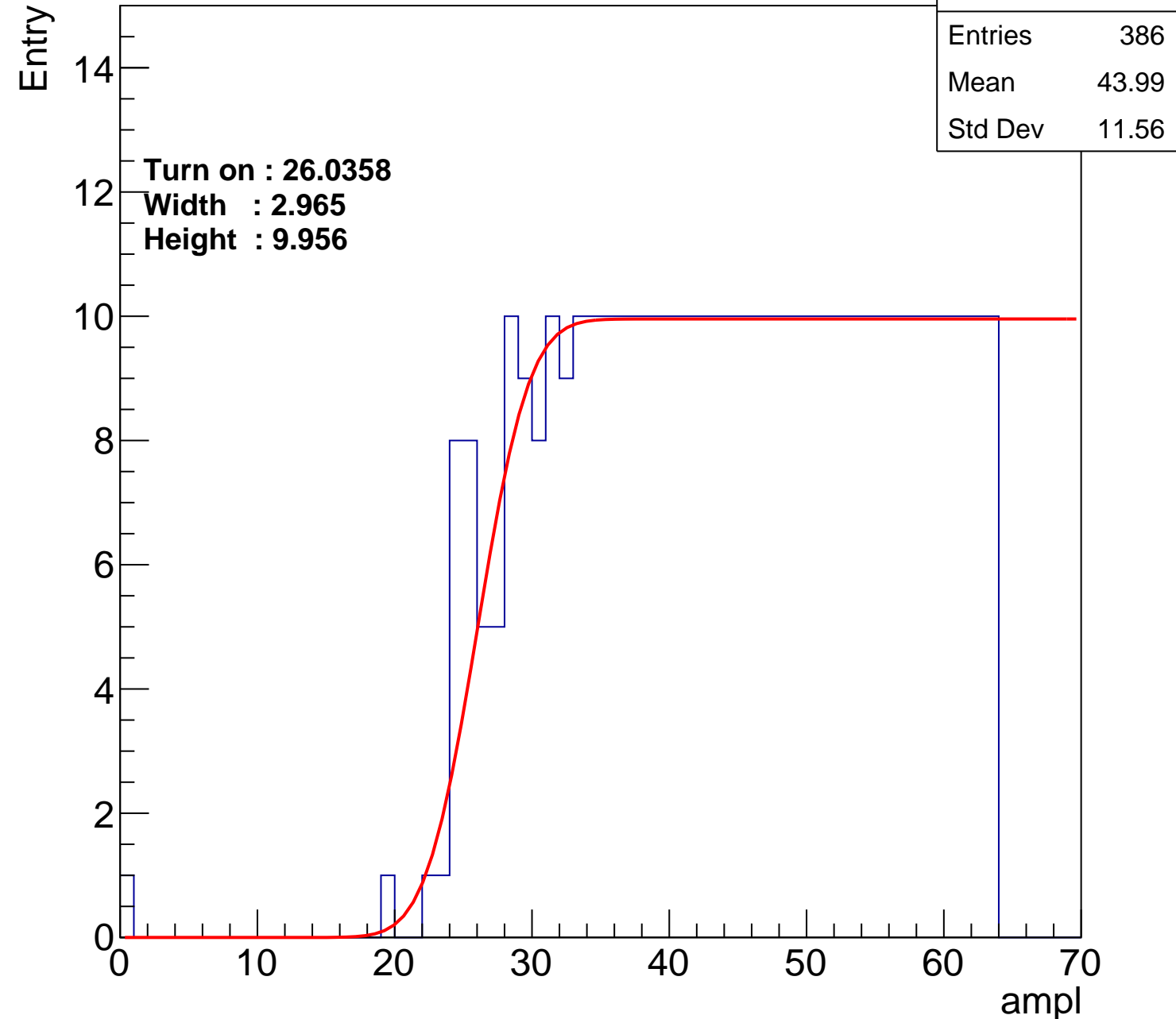
Width : 2.965

Height : 9.956

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch65

calib\_packv5\_042523\_0143.root, FC#4, port A2

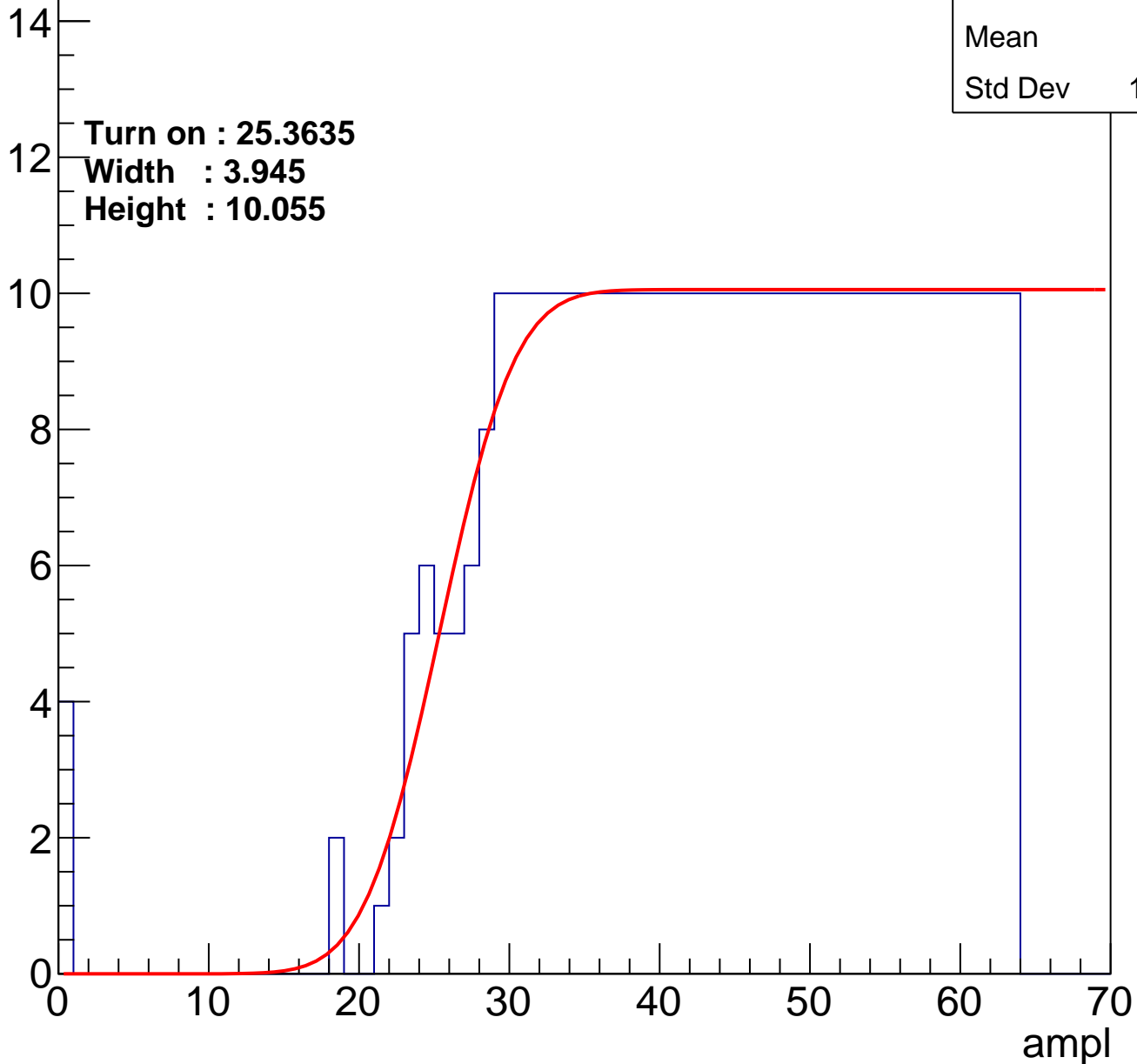
Entries	394
Mean	43.4
Std Dev	12.27

Turn on : 25.3635

Width : 3.945

Height : 10.055

Entry



# B1L100S, U15-ch66

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	397
Mean	43.43
Std Dev	11.95

Turn on : 25.4783

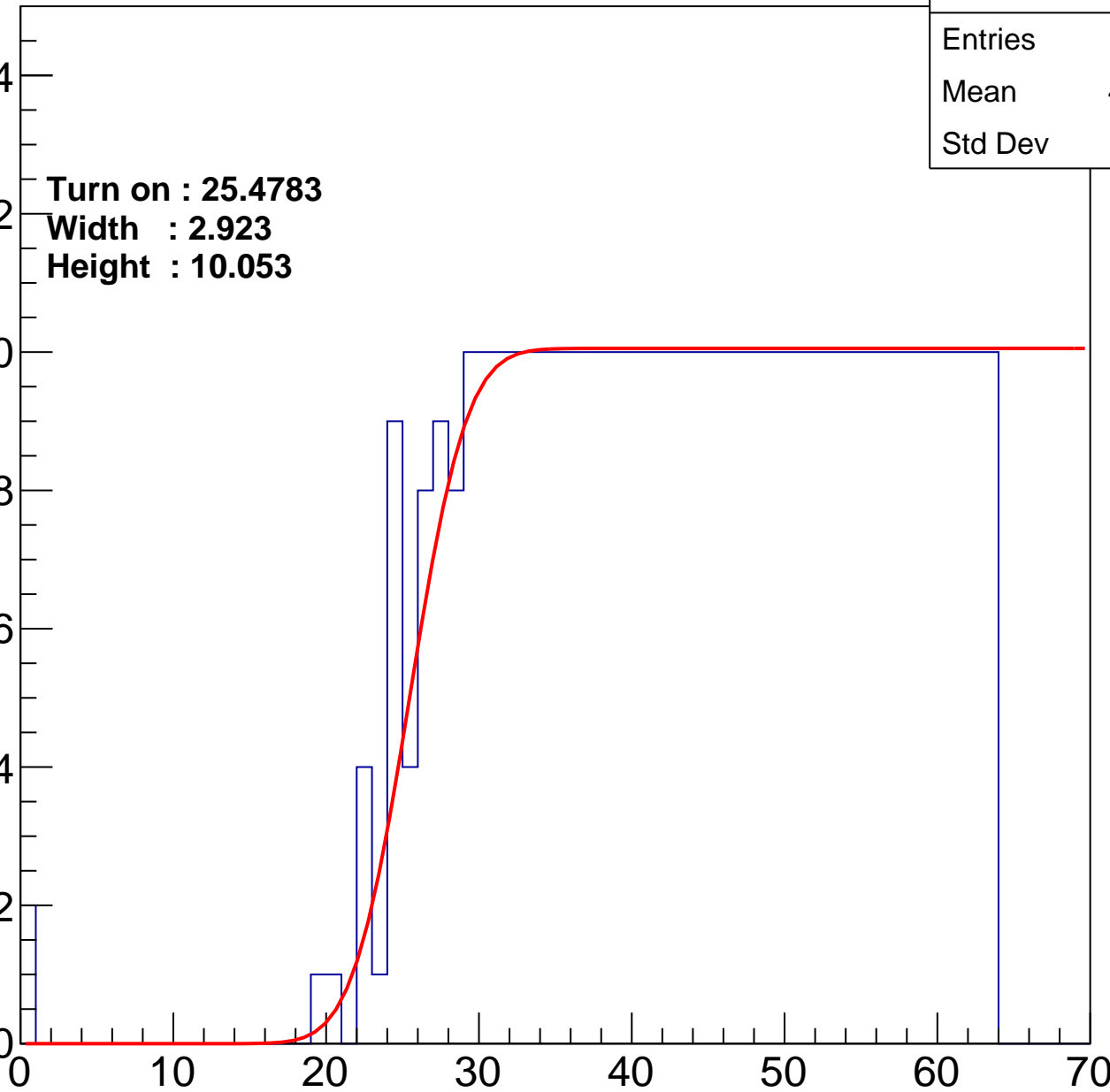
Width : 2.923

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch67

calib\_packv5\_042523\_0143.root, FC#4, port A2

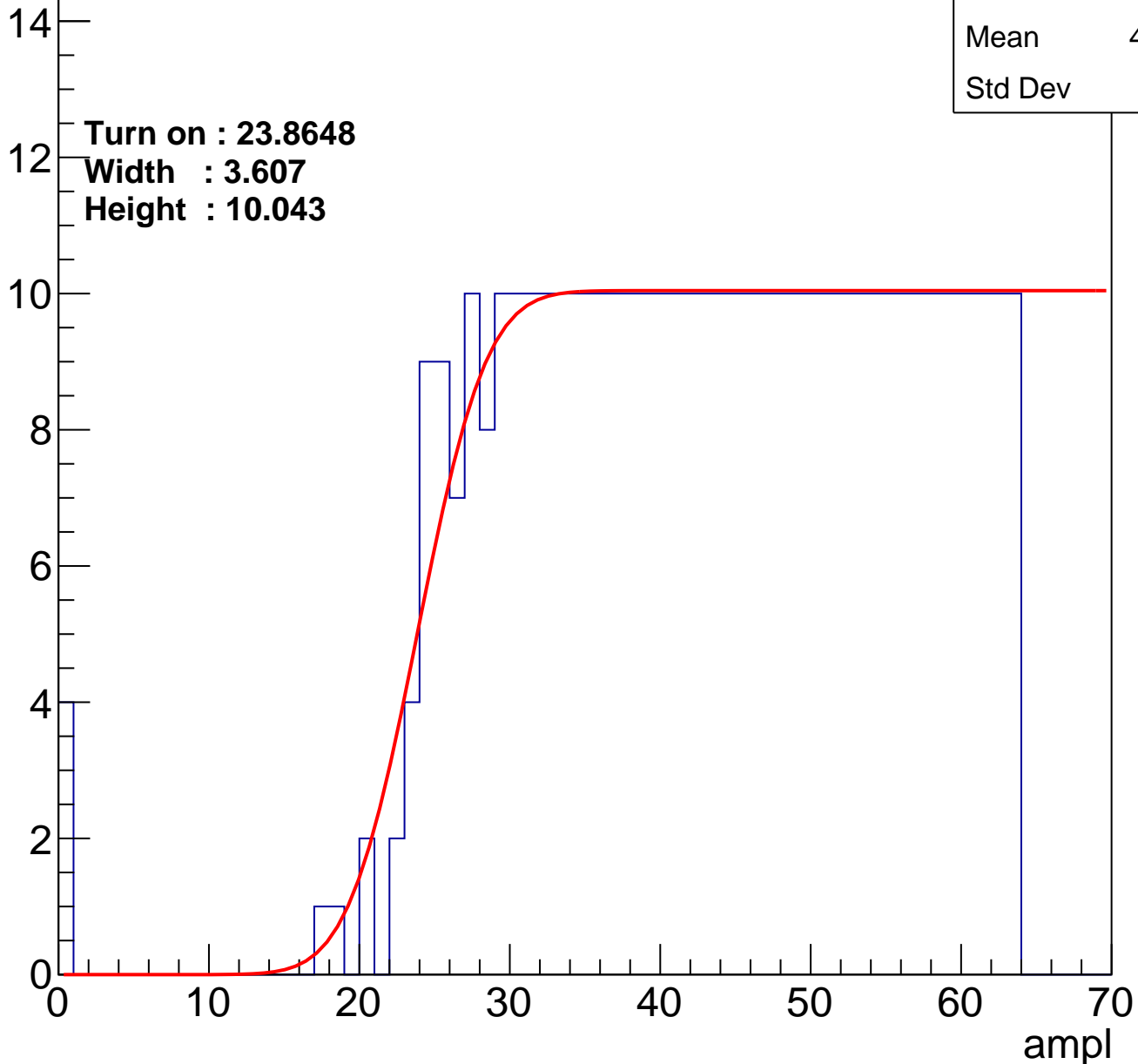
Entries	407
Mean	42.82
Std Dev	12.5

Turn on : 23.8648

Width : 3.607

Height : 10.043

Entry



# B1L100S, U15-ch68

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 26.1030

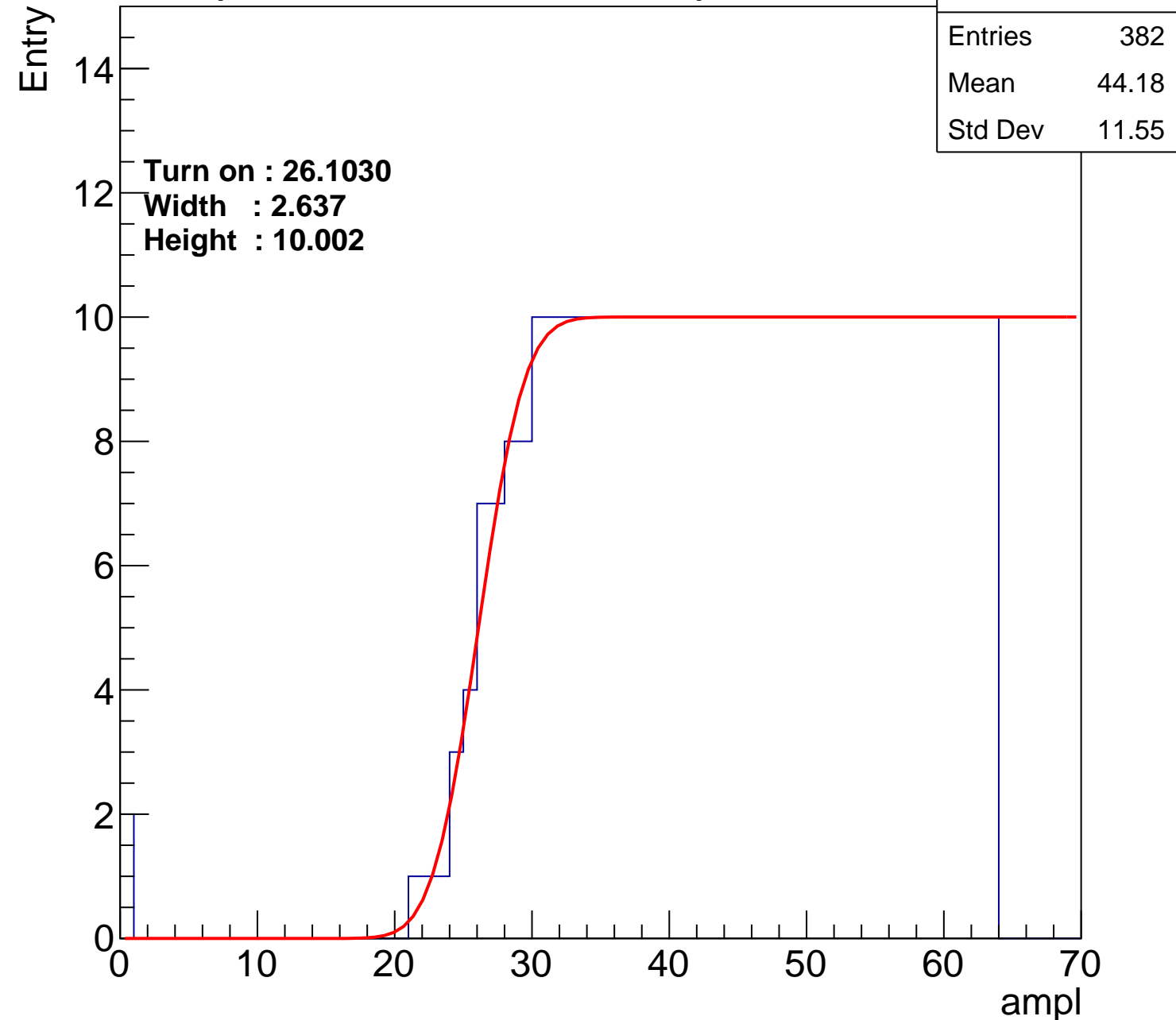
Width : 2.637

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch69

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 25.9883

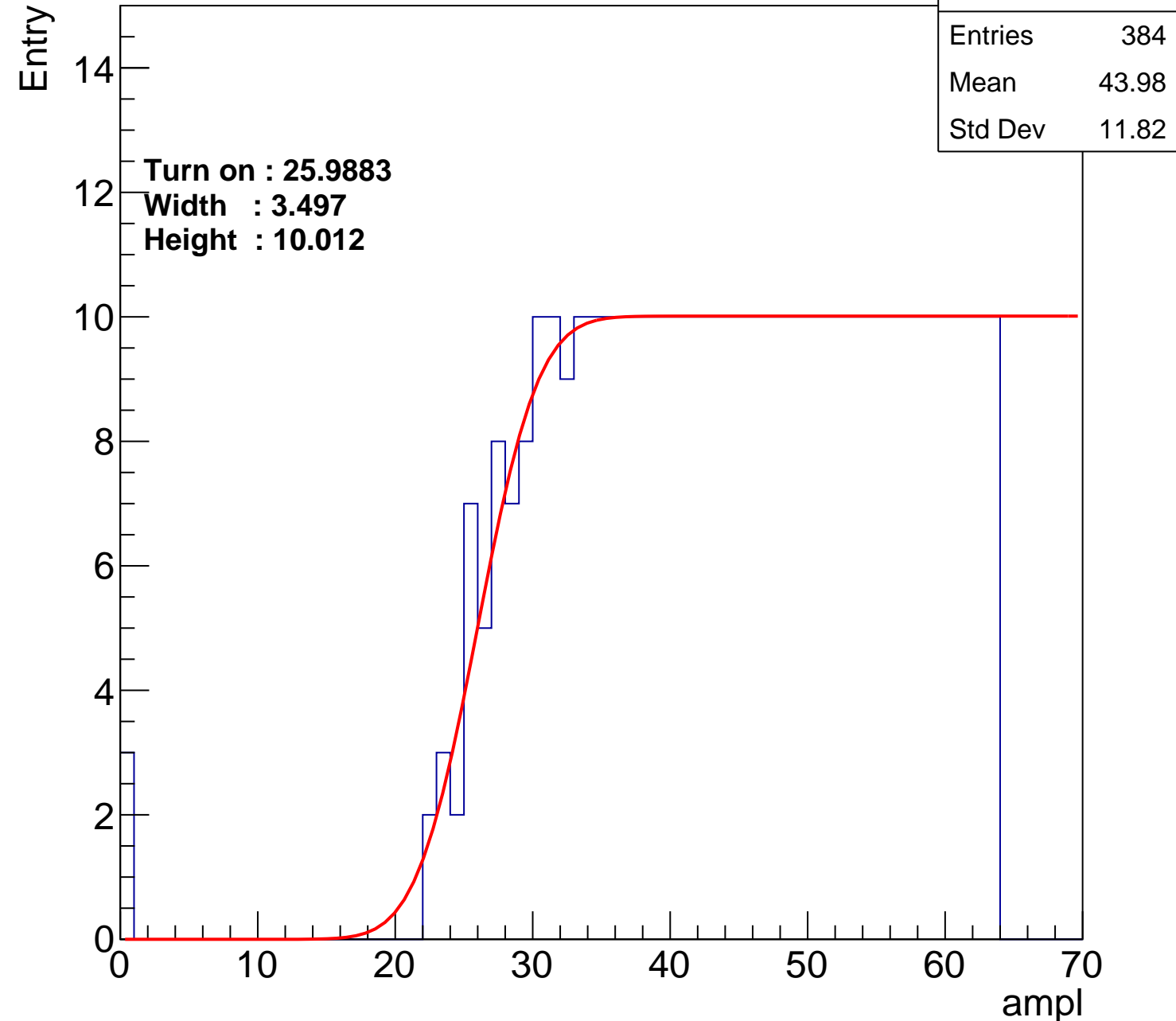
Width : 3.497

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch70

calib\_packv5\_042523\_0143.root, FC#4, port A2

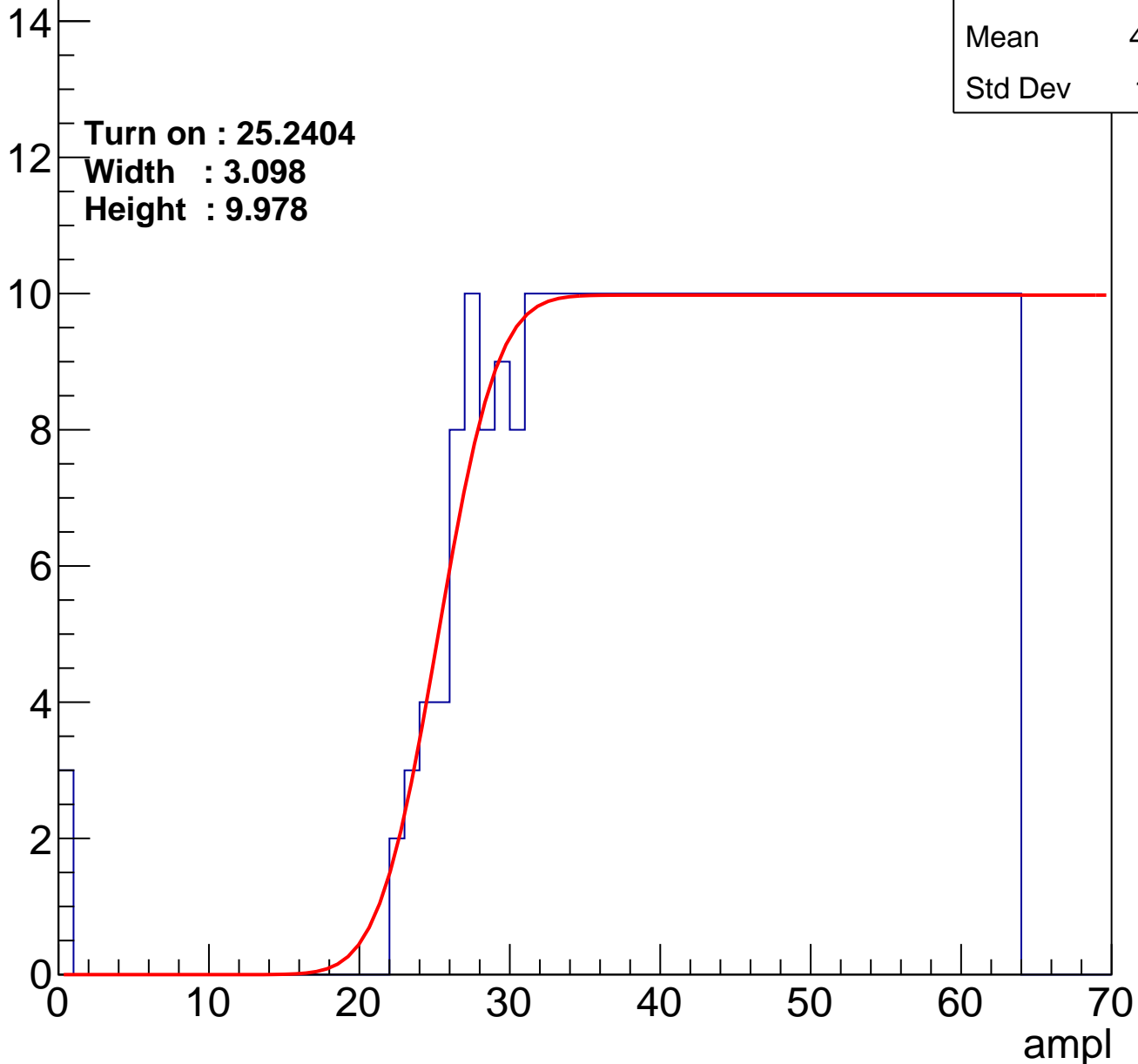
Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.2404

Width : 3.098

Height : 9.978

Entry





# B1L100S, U15-ch71

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	386
Mean	43.95
Std Dev	11.7

Turn on : 25.0504

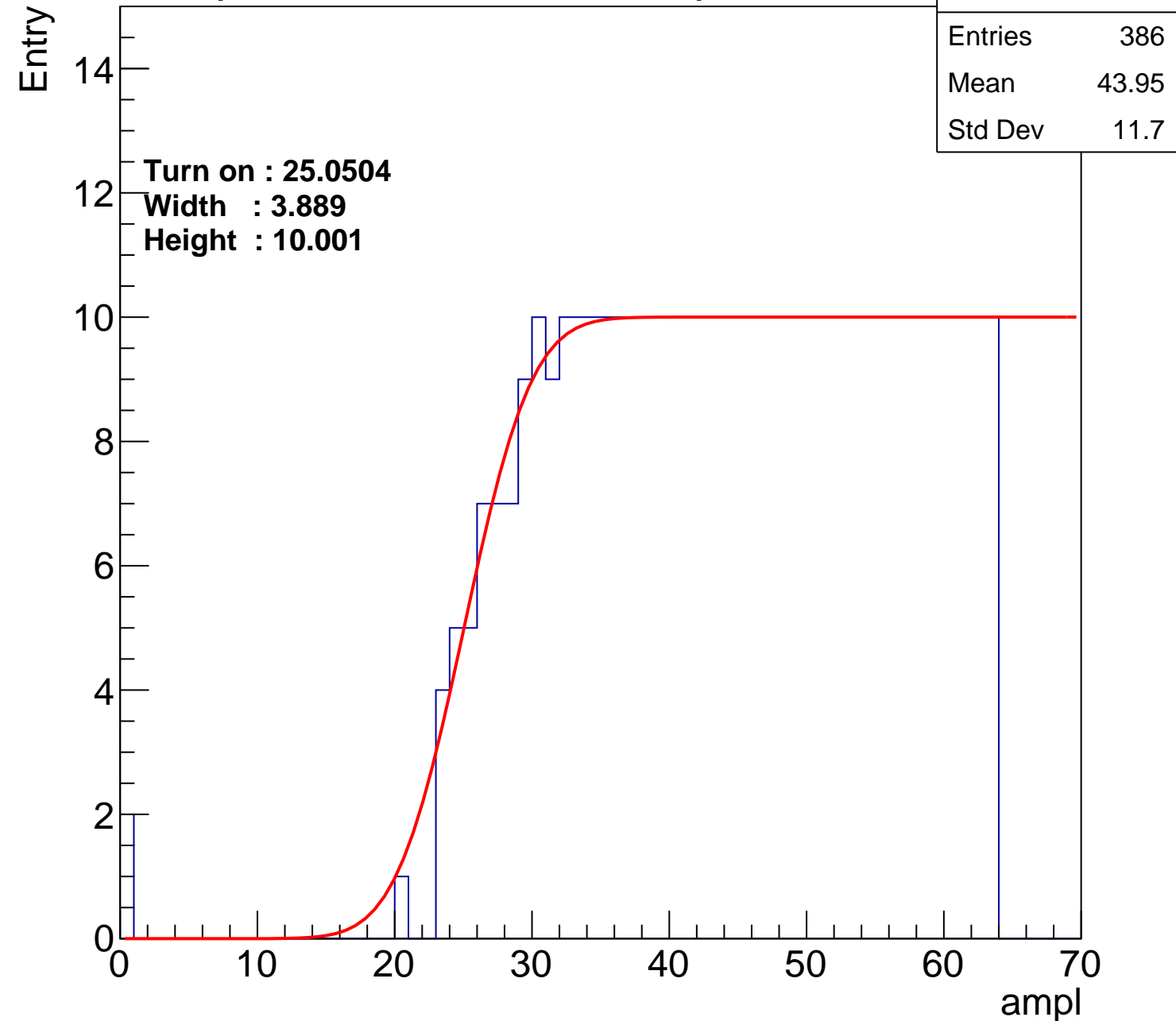
Width : 3.889

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch72

calib\_packv5\_042523\_0143.root, FC#4, port A2

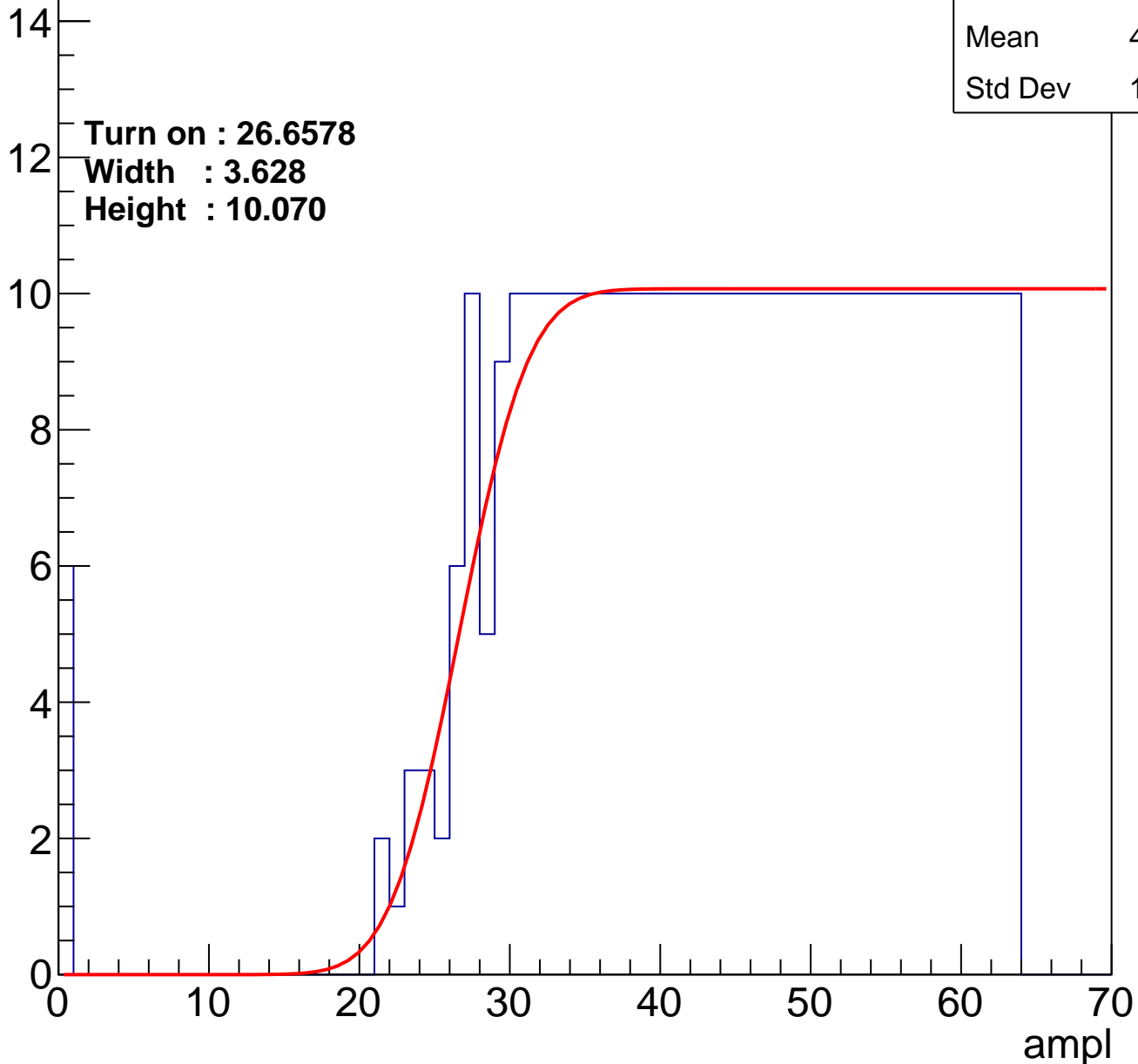
Entries	387
Mean	43.65
Std Dev	12.38

Turn on : 26.6578

Width : 3.628

Height : 10.070

Entry



# B1L100S, U15-ch73

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.28
Std Dev	11.5

Turn on : 26.3619

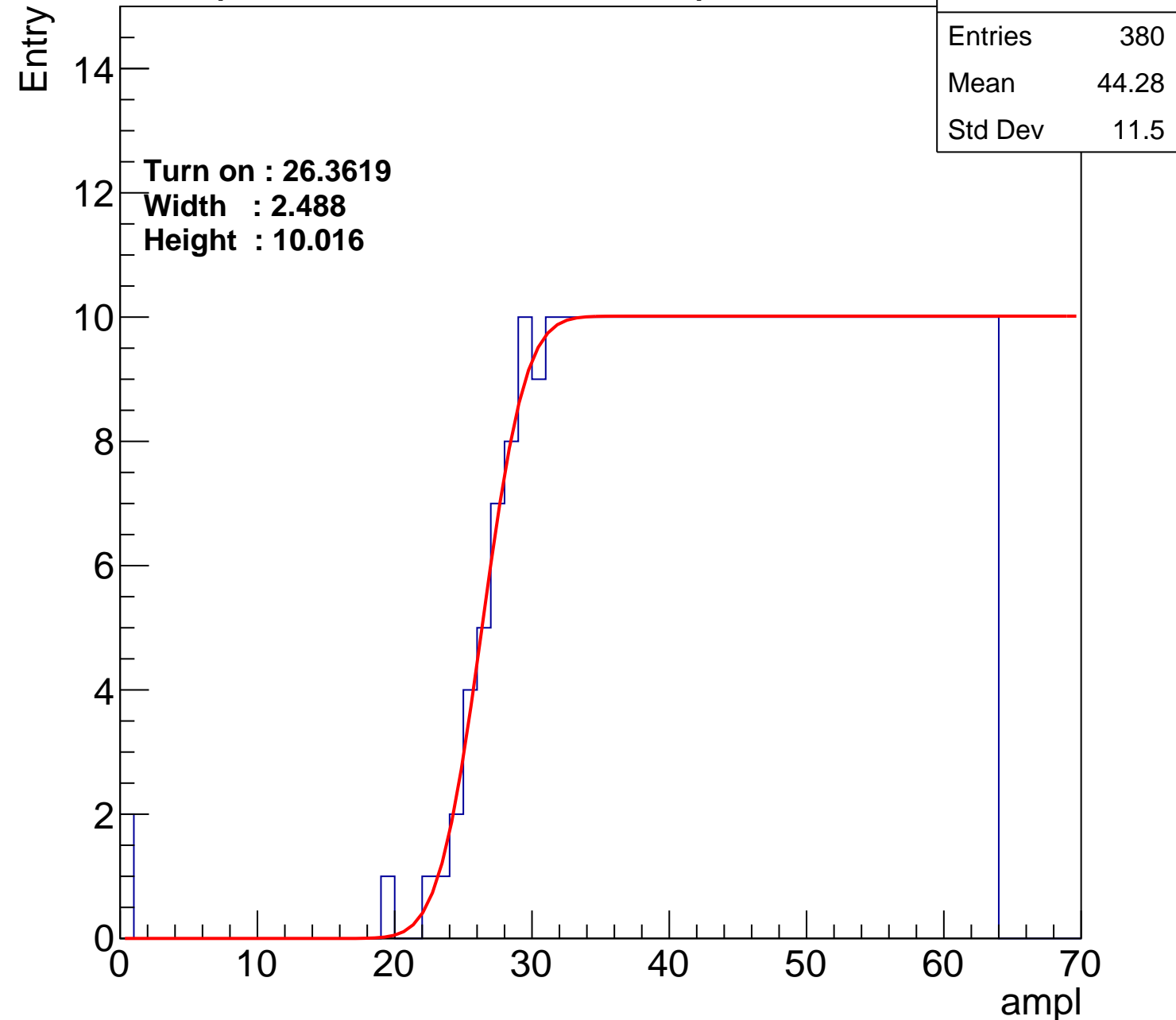
Width : 2.488

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch74

calib\_packv5\_042523\_0143.root, FC#4, port A2

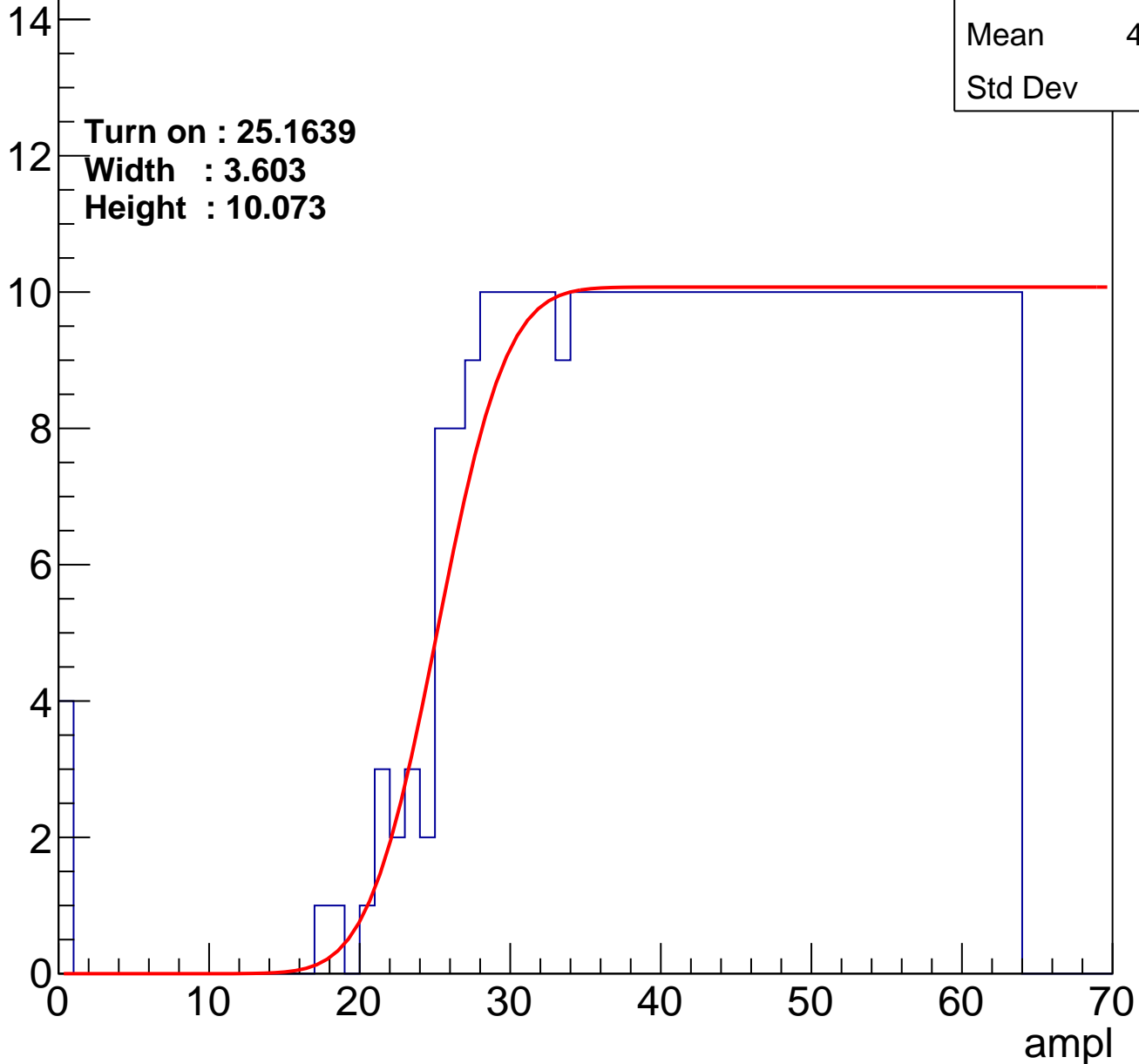
Entries	401
Mean	43.08
Std Dev	12.4

Turn on : 25.1639

Width : 3.603

Height : 10.073

Entry



# B1L100S, U15-ch75

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.43
Std Dev	11.58

Turn on : 26.8889

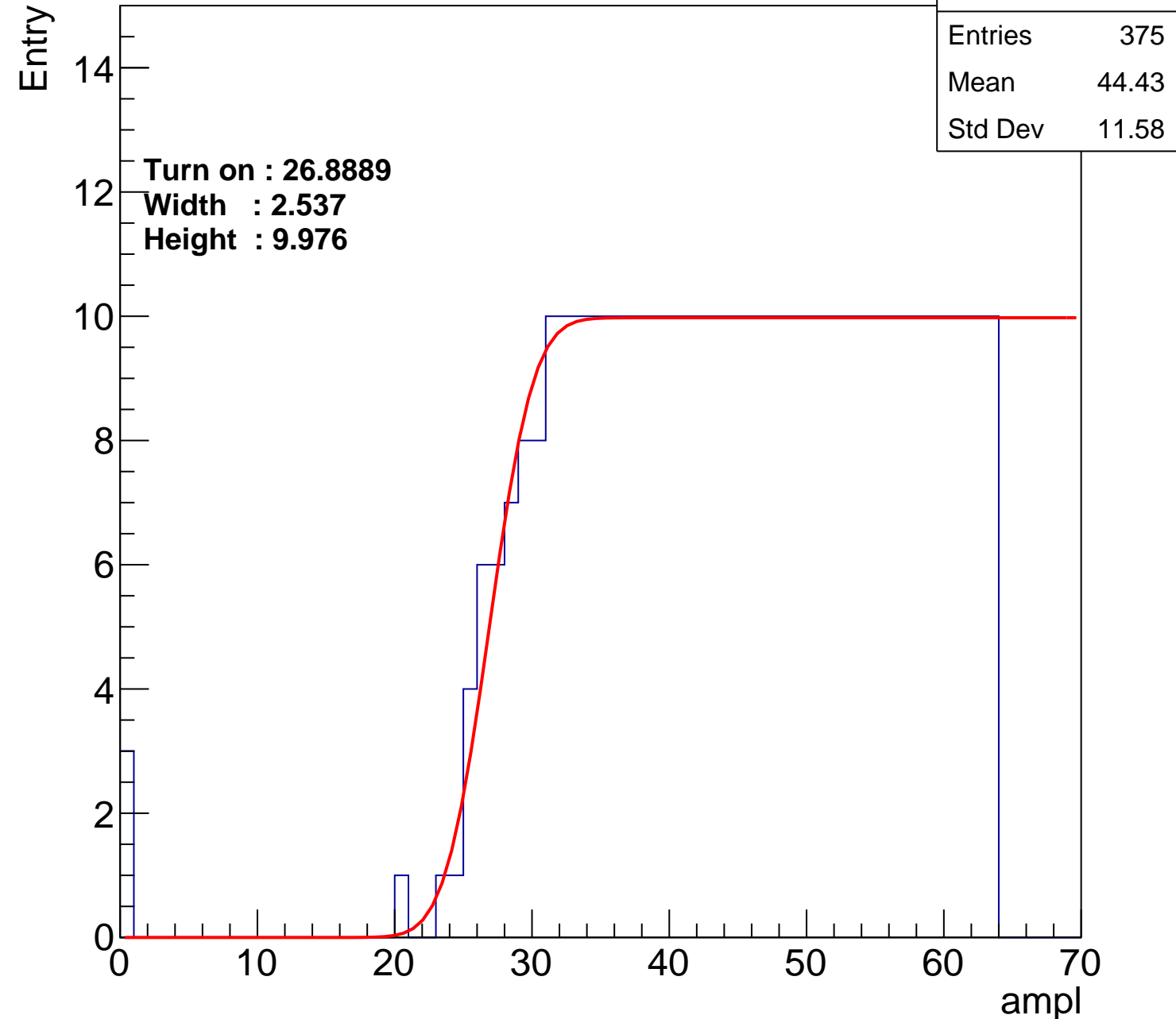
Width : 2.537

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch76

calib\_packv5\_042523\_0143.root, FC#4, port A2

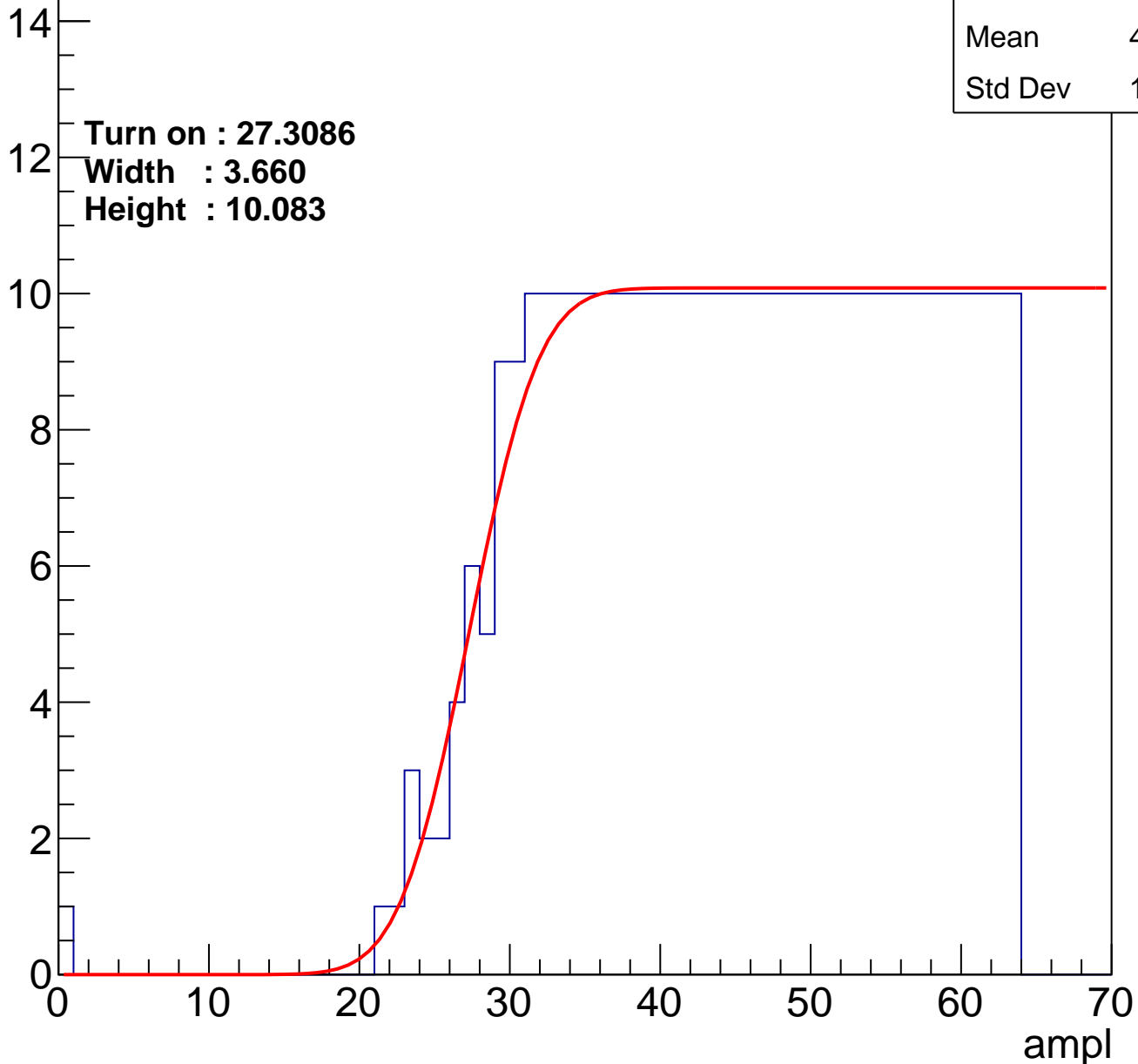
Entries	373
Mean	44.66
Std Dev	11.18

Turn on : 27.3086

Width : 3.660

Height : 10.083

Entry



# B1L100S, U15-ch77

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	366
Mean	45.01
Std Dev	10.96

Turn on : 27.0525

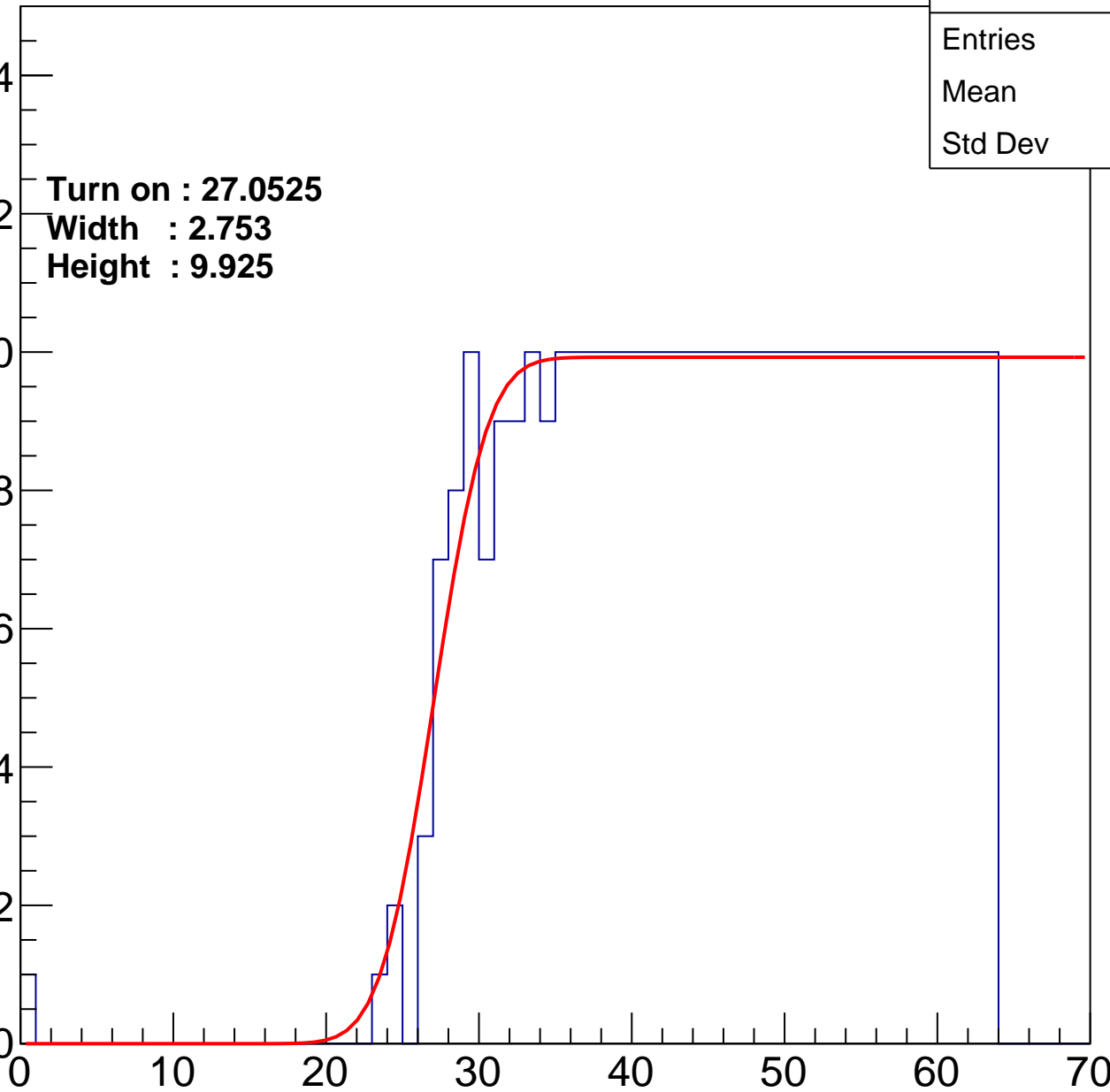
Width : 2.753

Height : 9.925

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch78

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	392
Mean	43.72
Std Dev	11.69

Turn on : 25.5784

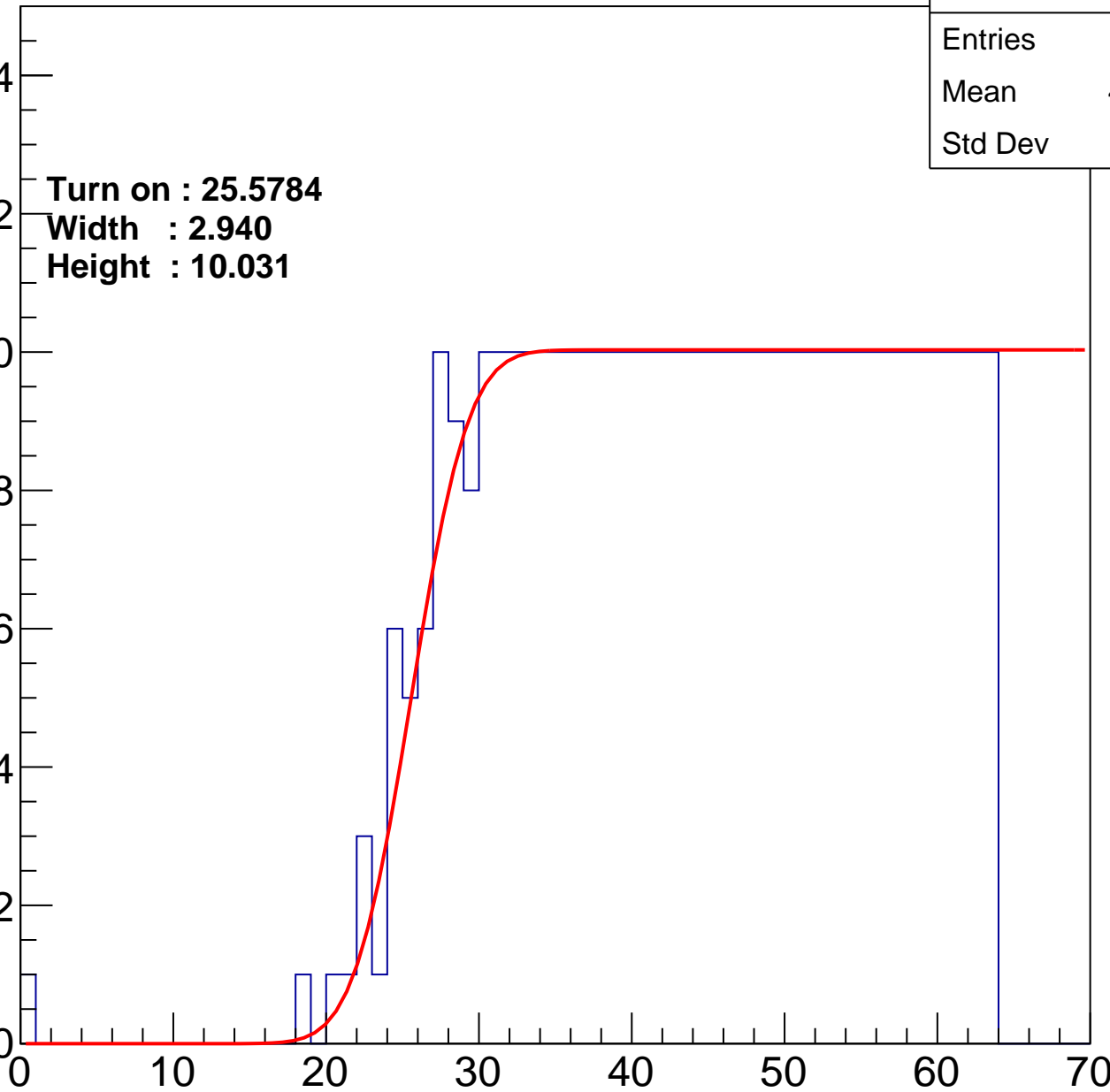
Width : 2.940

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch79

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.8605

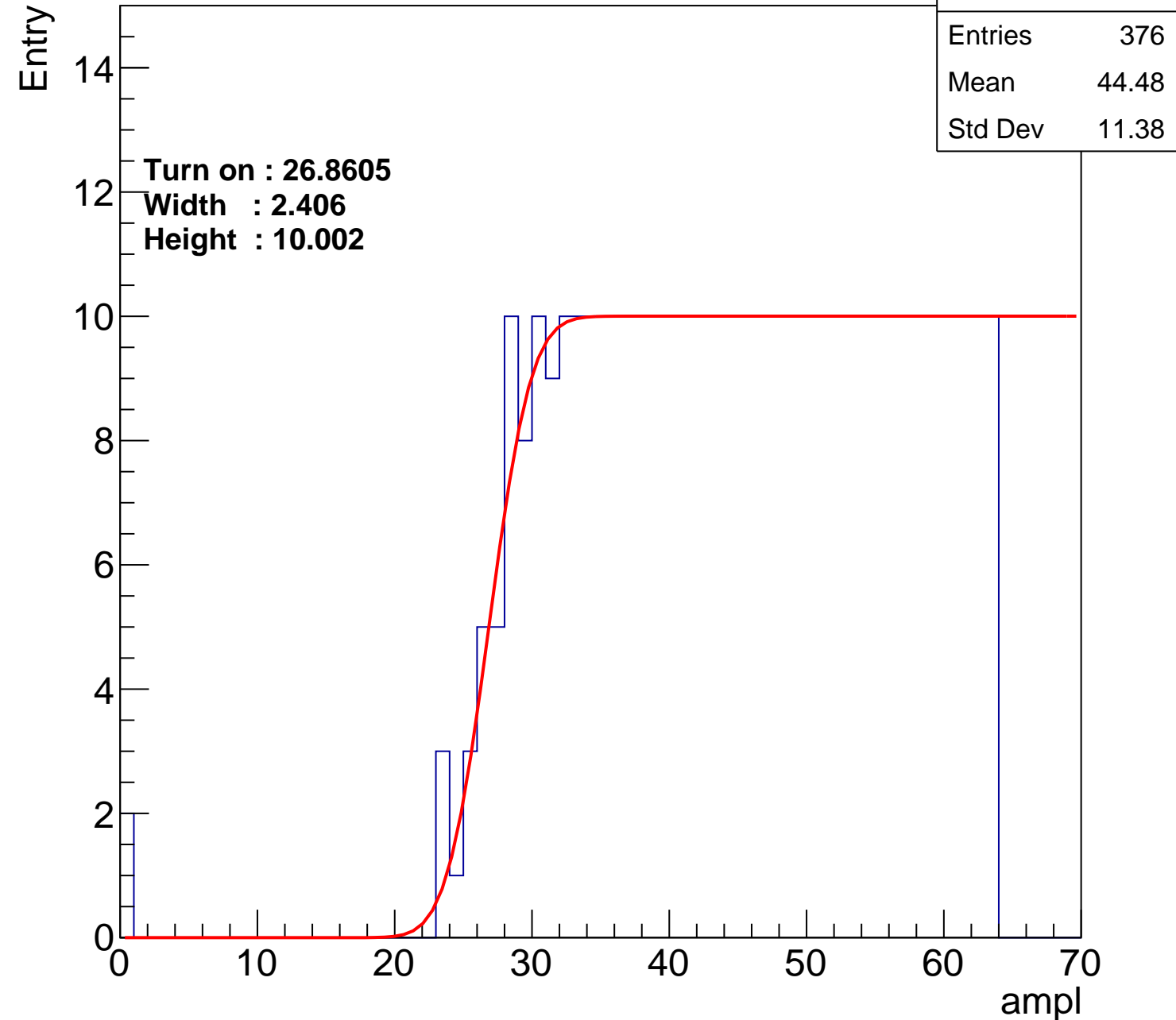
Width : 2.406

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch80

calib\_packv5\_042523\_0143.root, FC#4, port A2

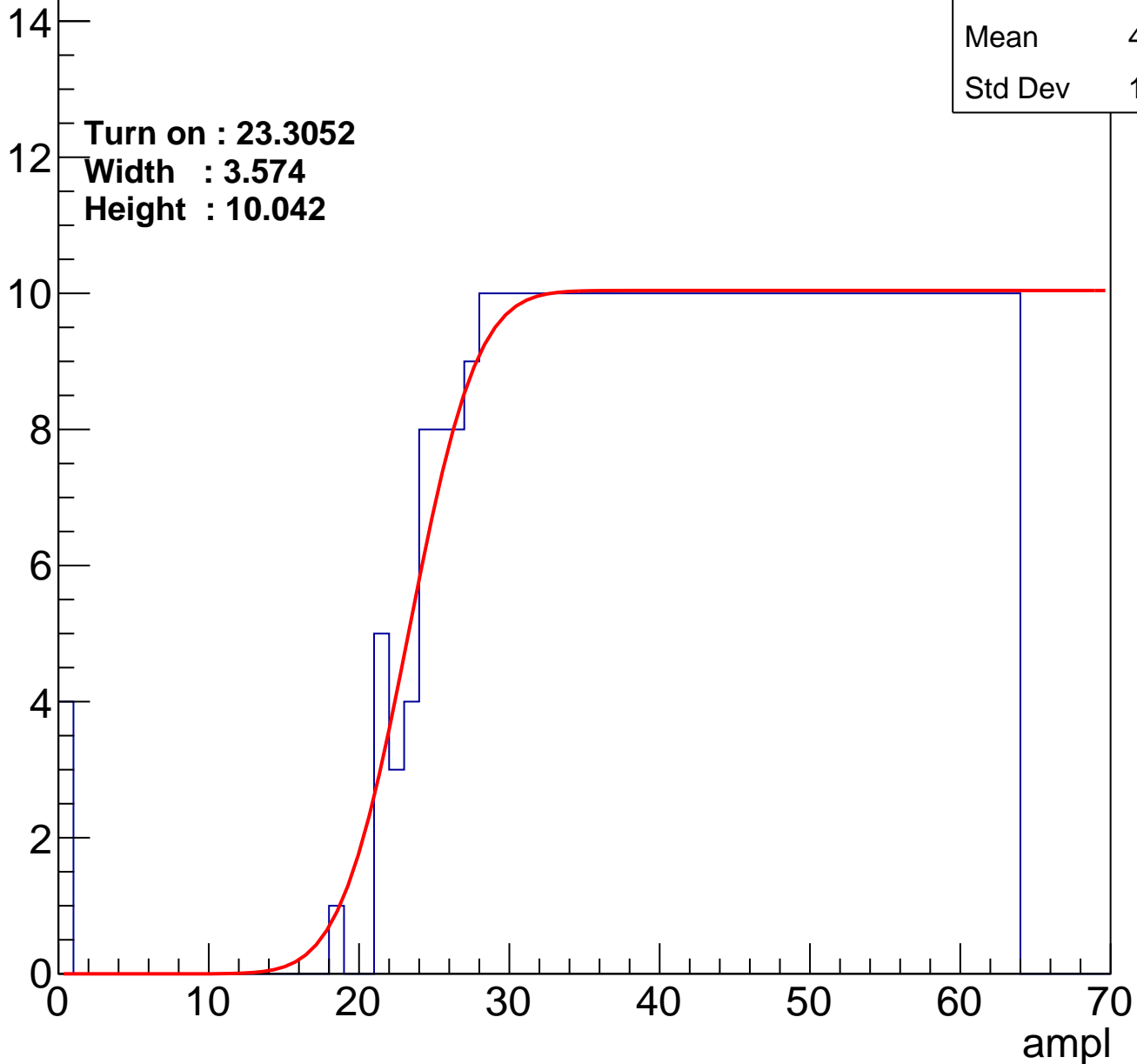
Entries	410
Mean	42.69
Std Dev	12.54

Turn on : 23.3052

Width : 3.574

Height : 10.042

Entry



# B1L100S, U15-ch81

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	390
Mean	43.72
Std Dev	11.93

Turn on : 25.6348

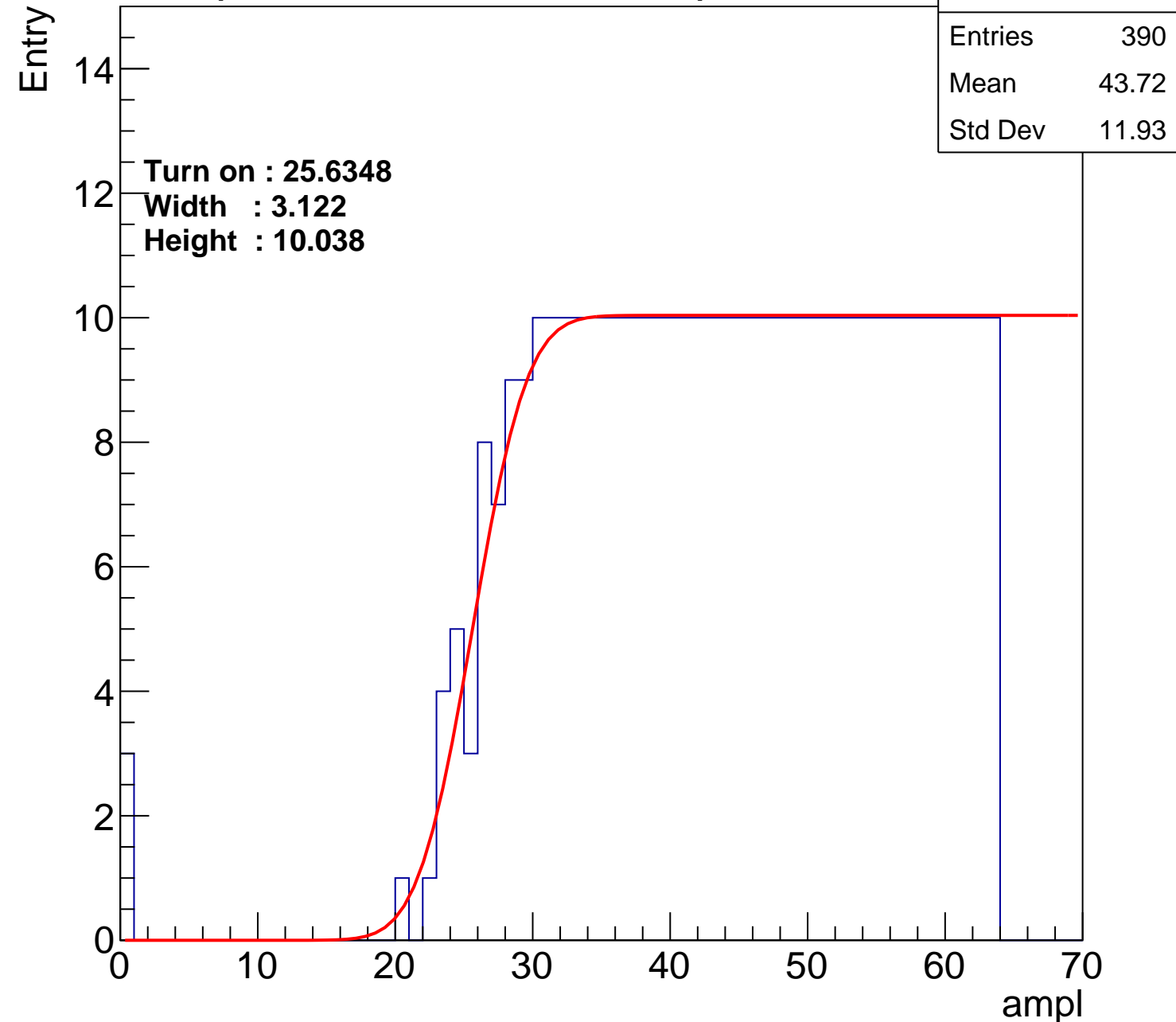
Width : 3.122

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch82

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	381
Mean	44.11
Std Dev	11.78

Turn on : 26.6344

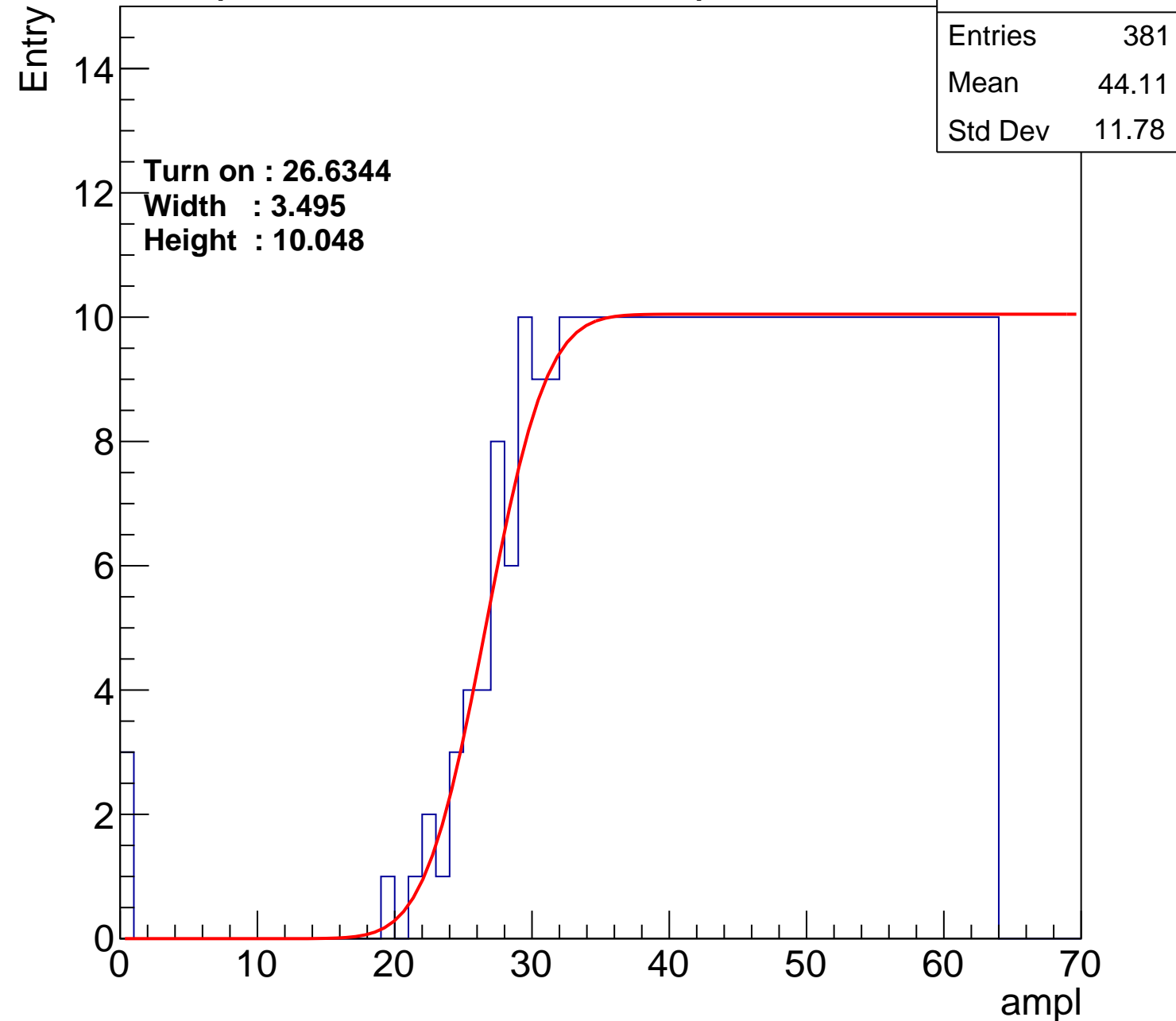
Width : 3.495

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch83

calib\_packv5\_042523\_0143.root, FC#4, port A2

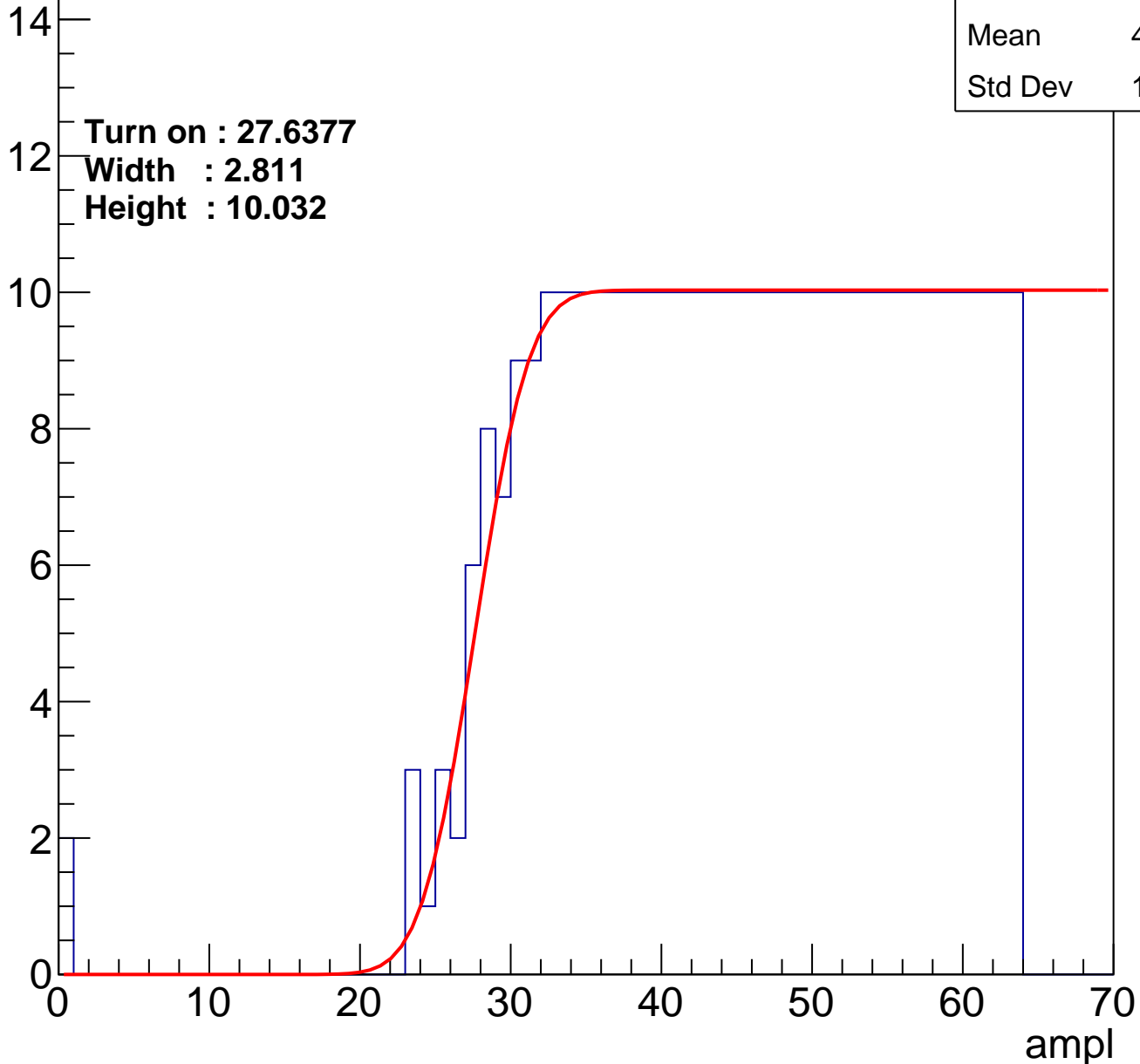
Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.6377

Width : 2.811

Height : 10.032

Entry



# B1L100S, U15-ch84

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	386
Mean	43.89
Std Dev	11.79

Turn on : 25.9352

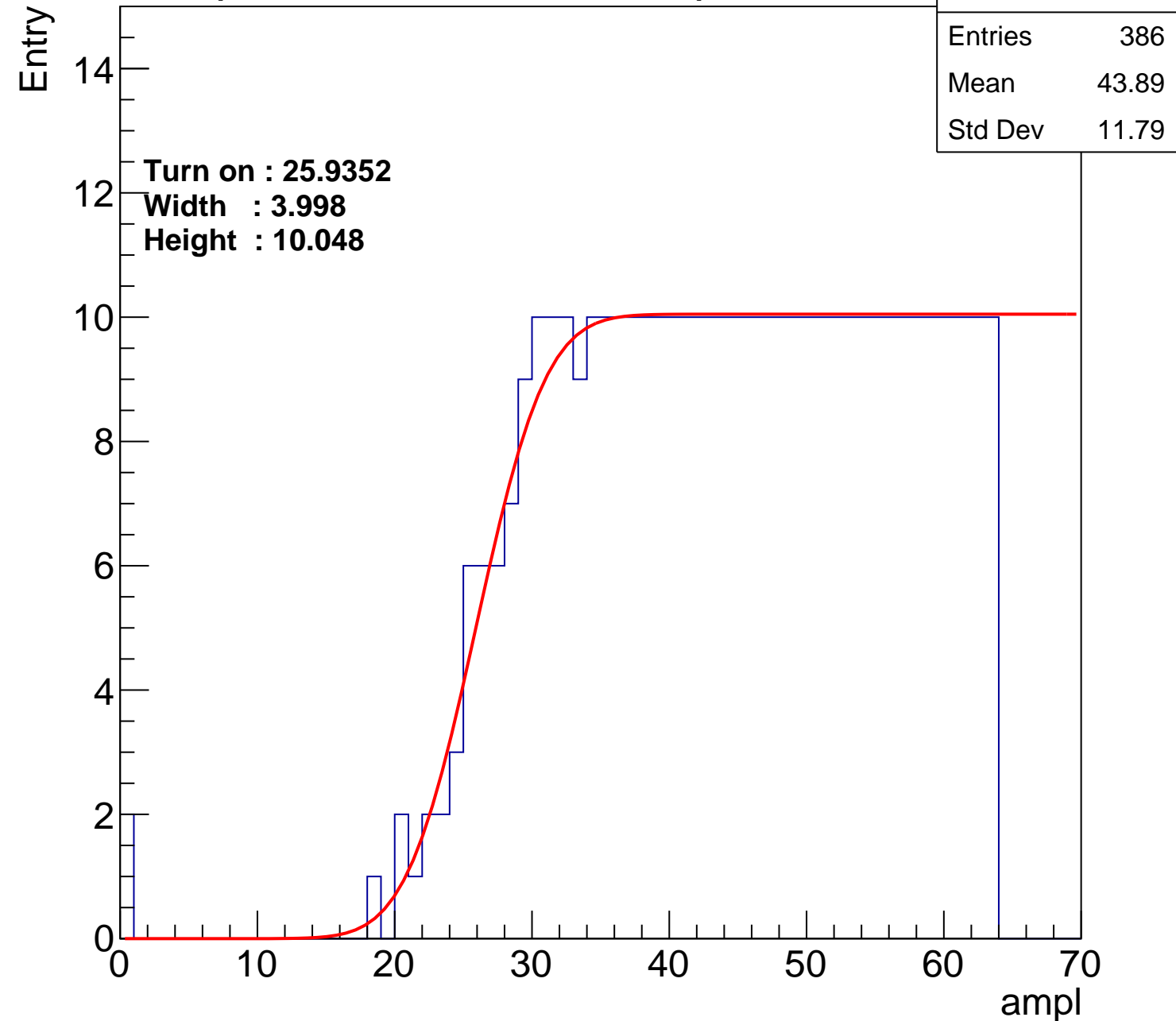
Width : 3.998

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch85

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.74
Std Dev	11.29

**Turn on : 27.5744**

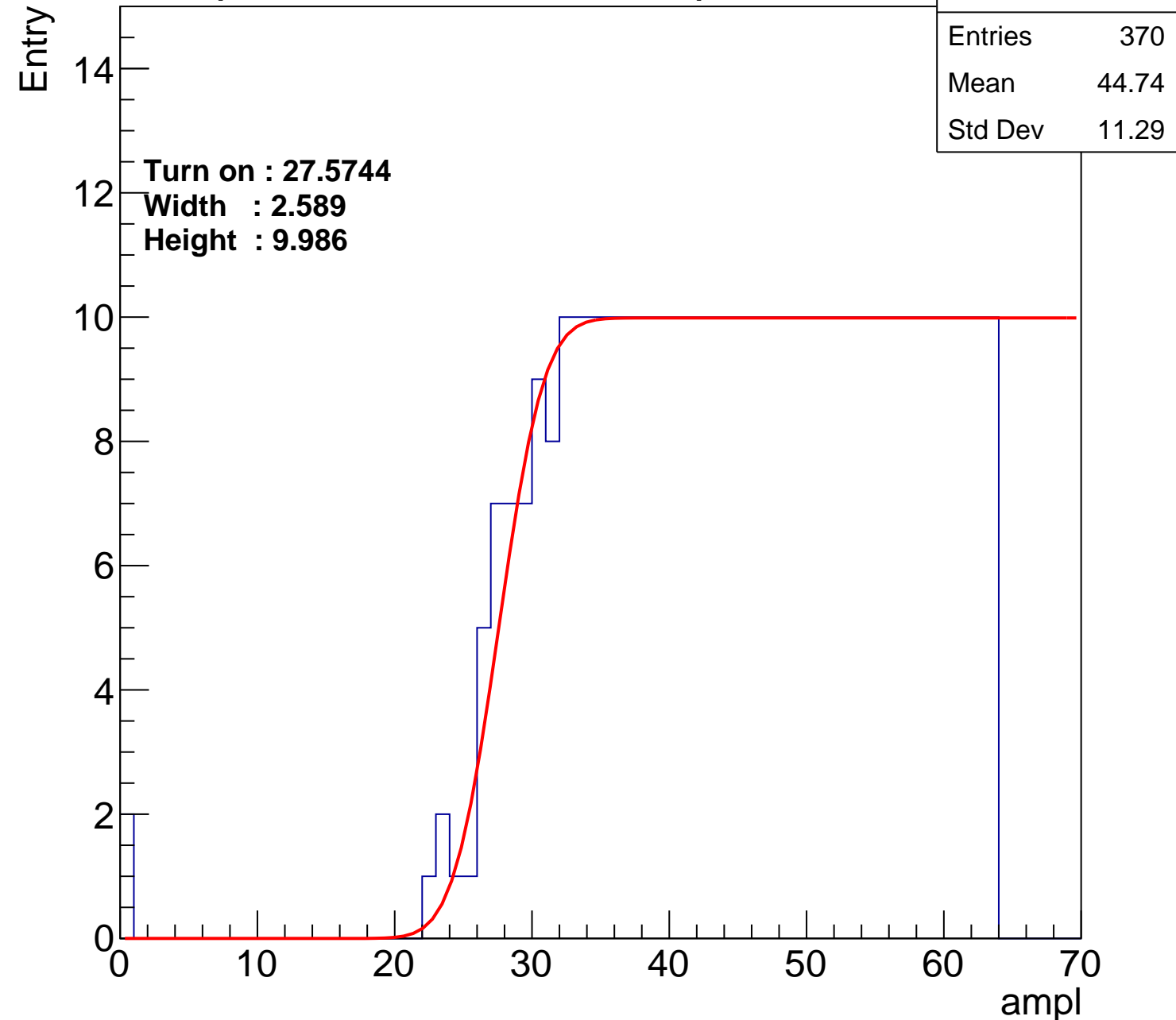
**Width : 2.589**

**Height : 9.986**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch86

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	392
Mean	43.69
Std Dev	11.79

Turn on : 25.5474

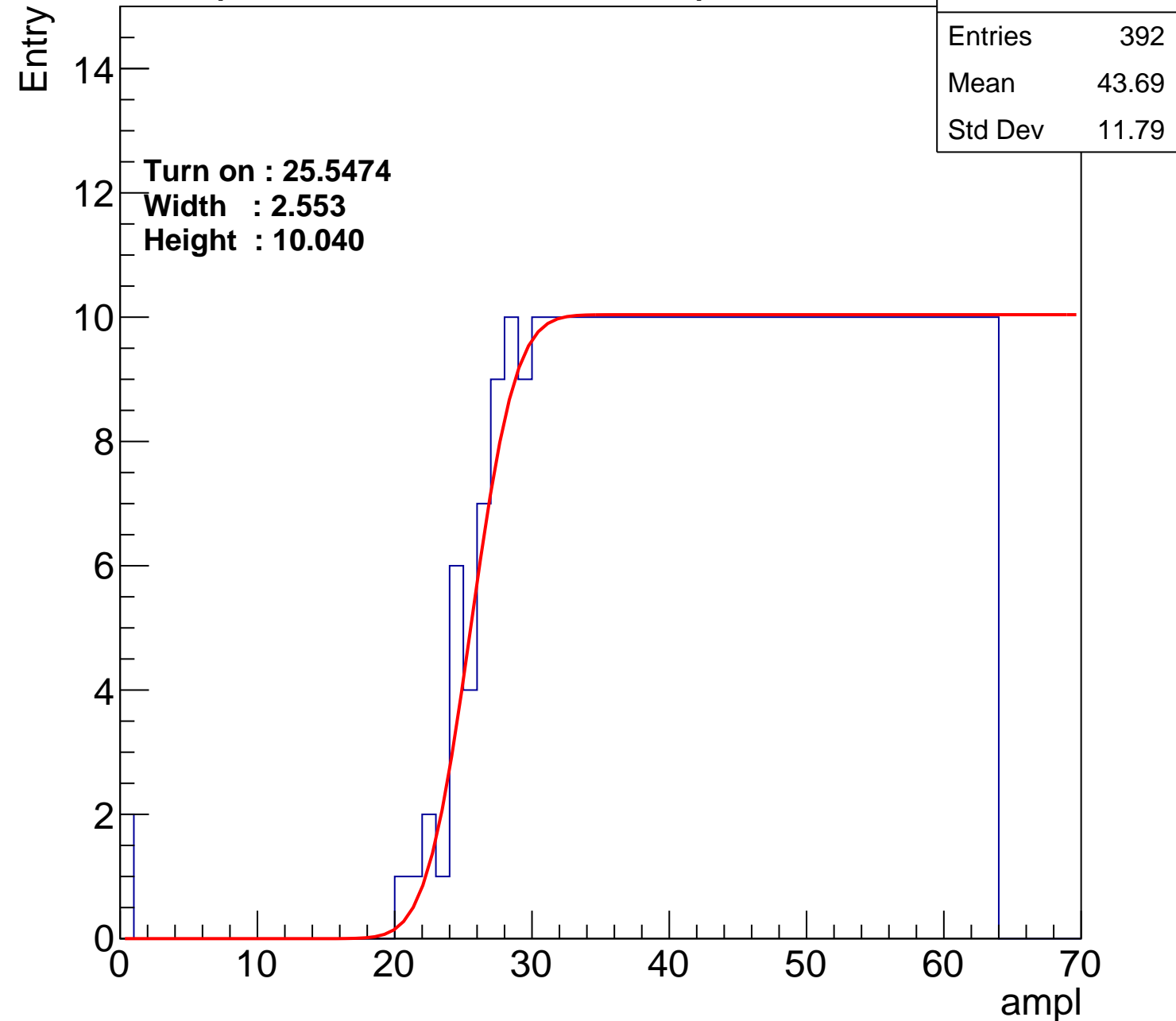
Width : 2.553

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch87

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	364
Mean	44.95
Std Dev	11.35

Turn on : 28.5273

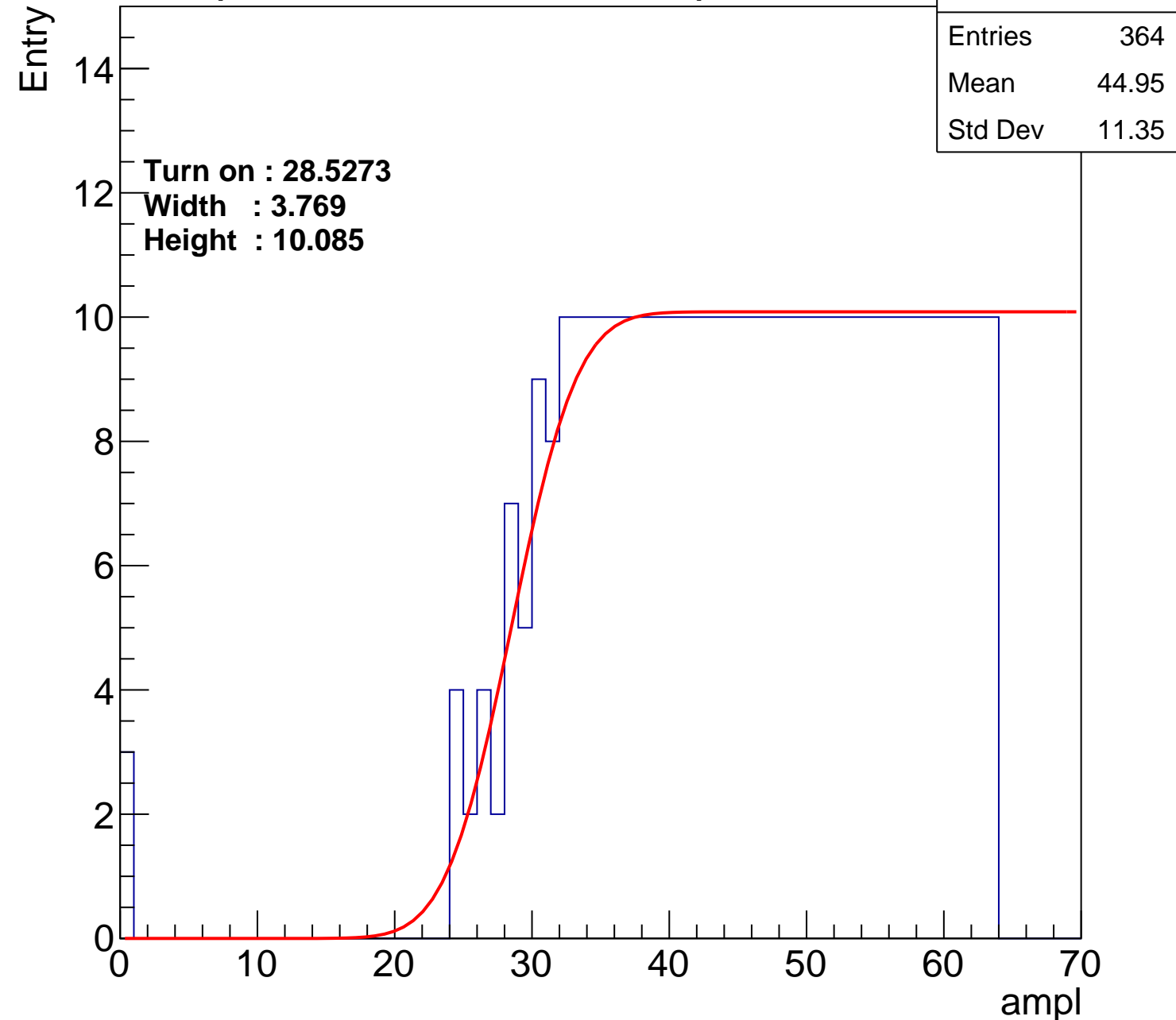
Width : 3.769

Height : 10.085

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch88

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	44.05
Std Dev	11.75

Turn on : 25.7908

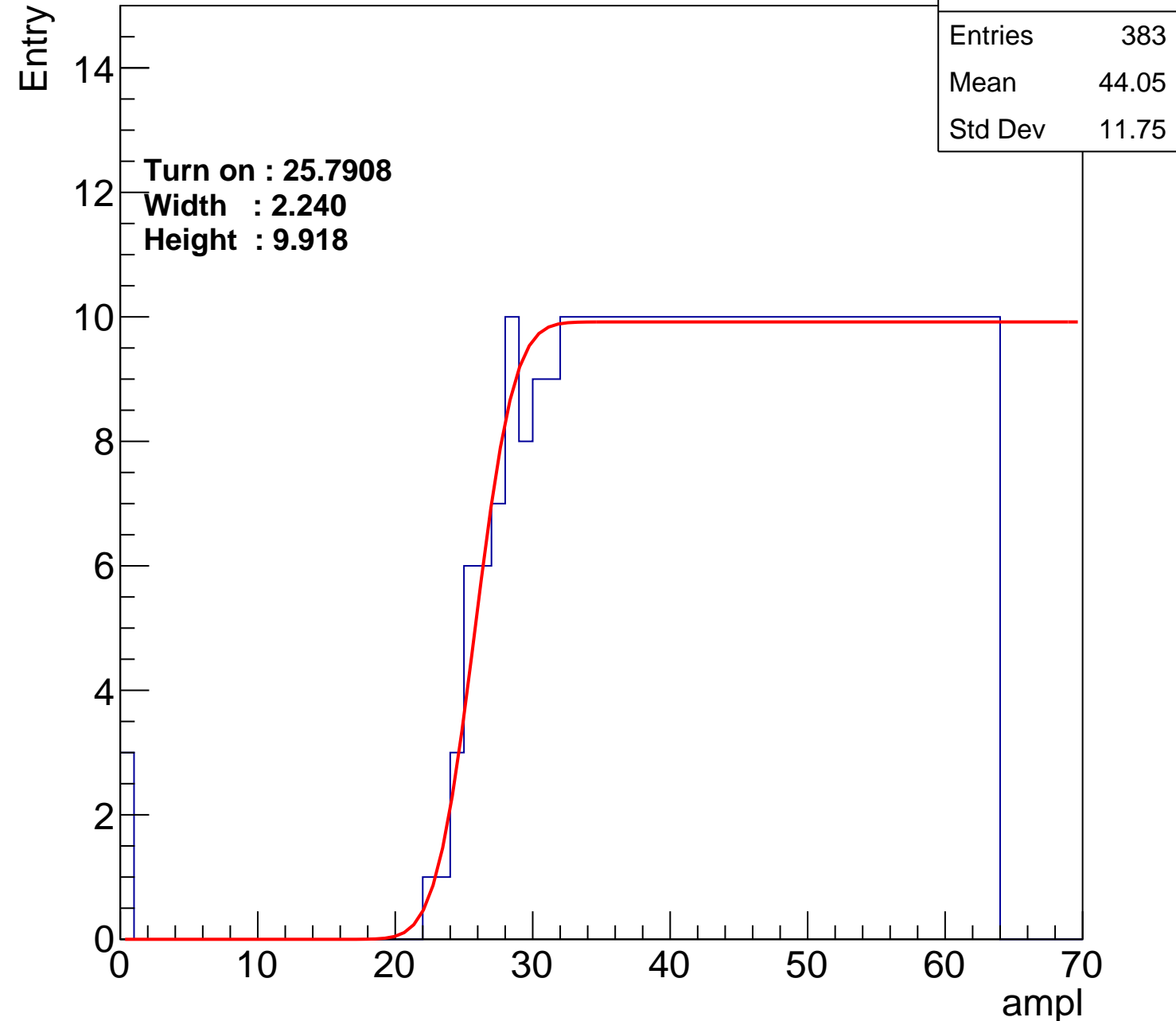
Width : 2.240

Height : 9.918

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch89

calib\_packv5\_042523\_0143.root, FC#4, port A2

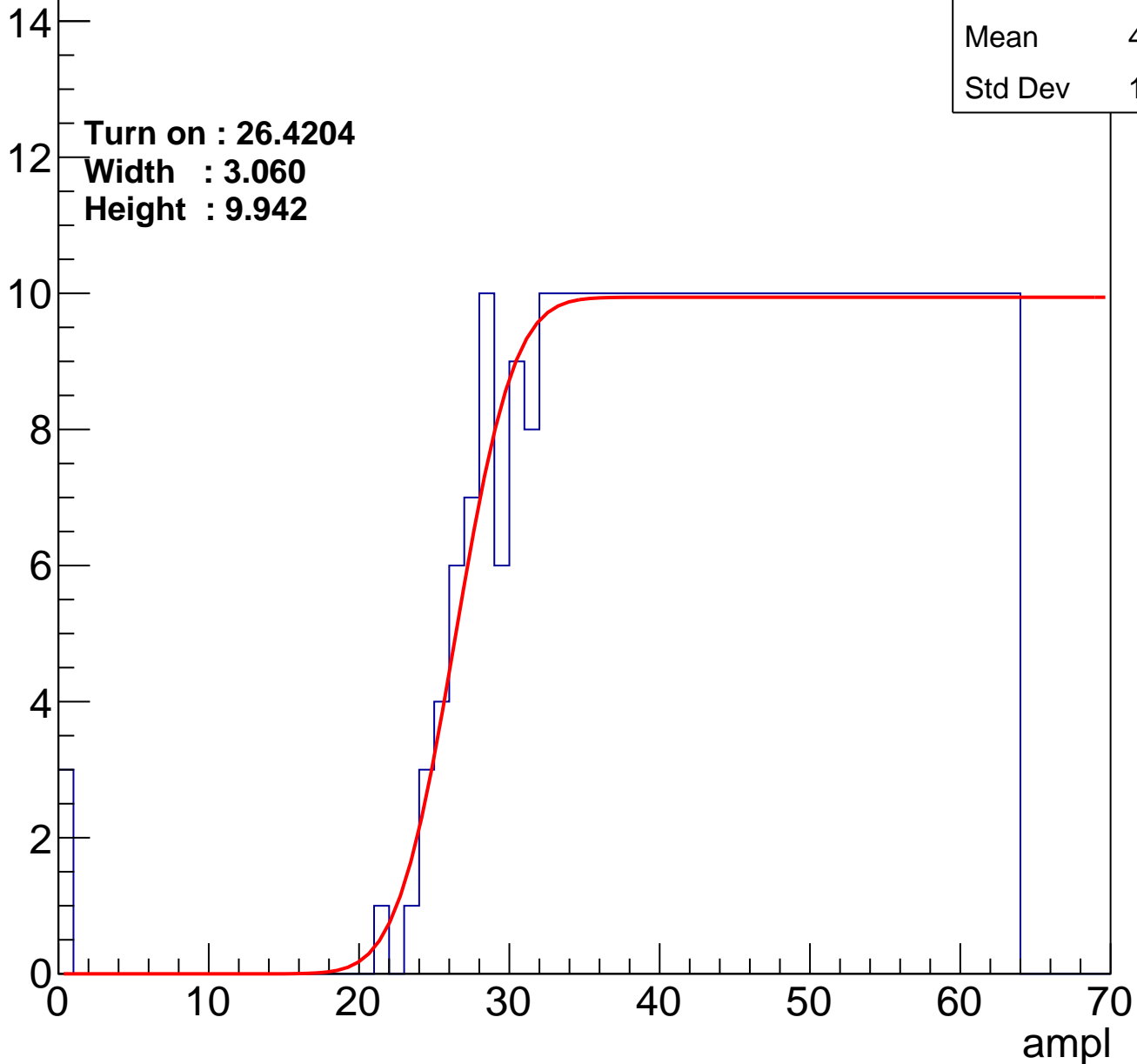
Entries	378
Mean	44.27
Std Dev	11.68

Turn on : 26.4204

Width : 3.060

Height : 9.942

Entry



# B1L100S, U15-ch90

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	399
Mean	43.24
Std Dev	12.2

Turn on : 24.5870

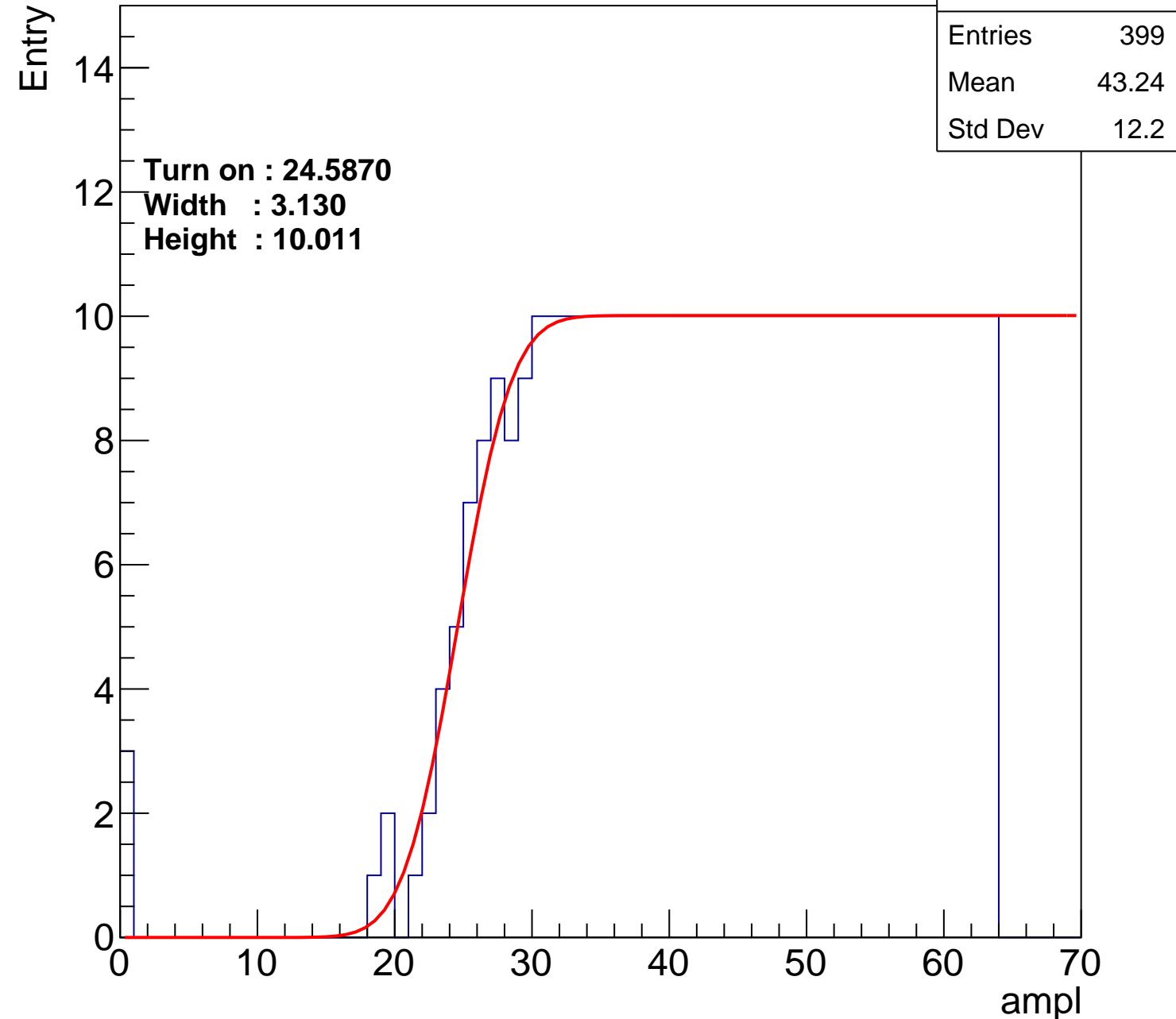
Width : 3.130

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch91

calib\_packv5\_042523\_0143.root, FC#4, port A2

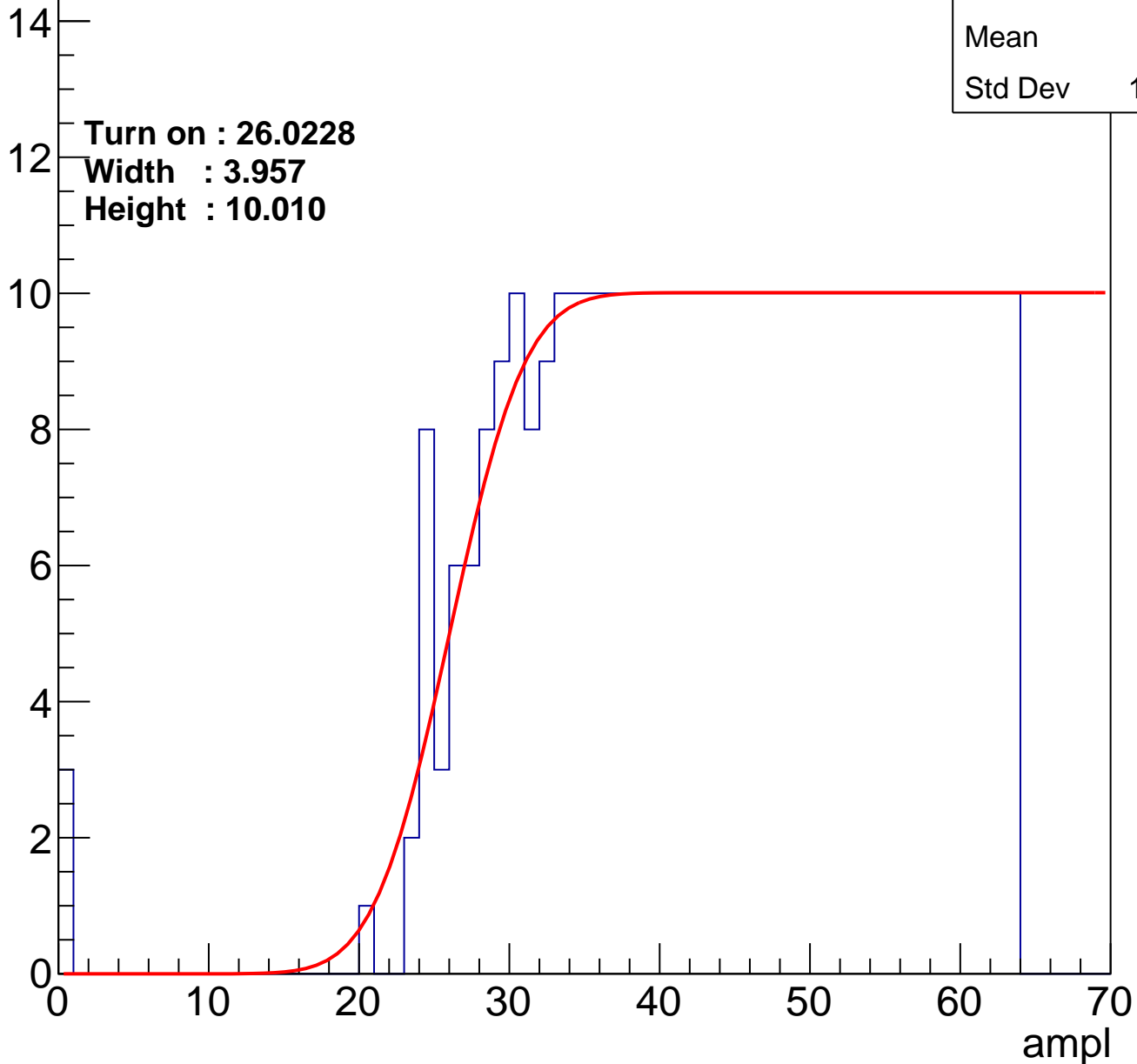
Entries	383
Mean	44
Std Dev	11.84

Turn on : 26.0228

Width : 3.957

Height : 10.010

Entry



# B1L100S, U15-ch92

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	397
Mean	43.21
Std Dev	12.48

Turn on : 25.4306

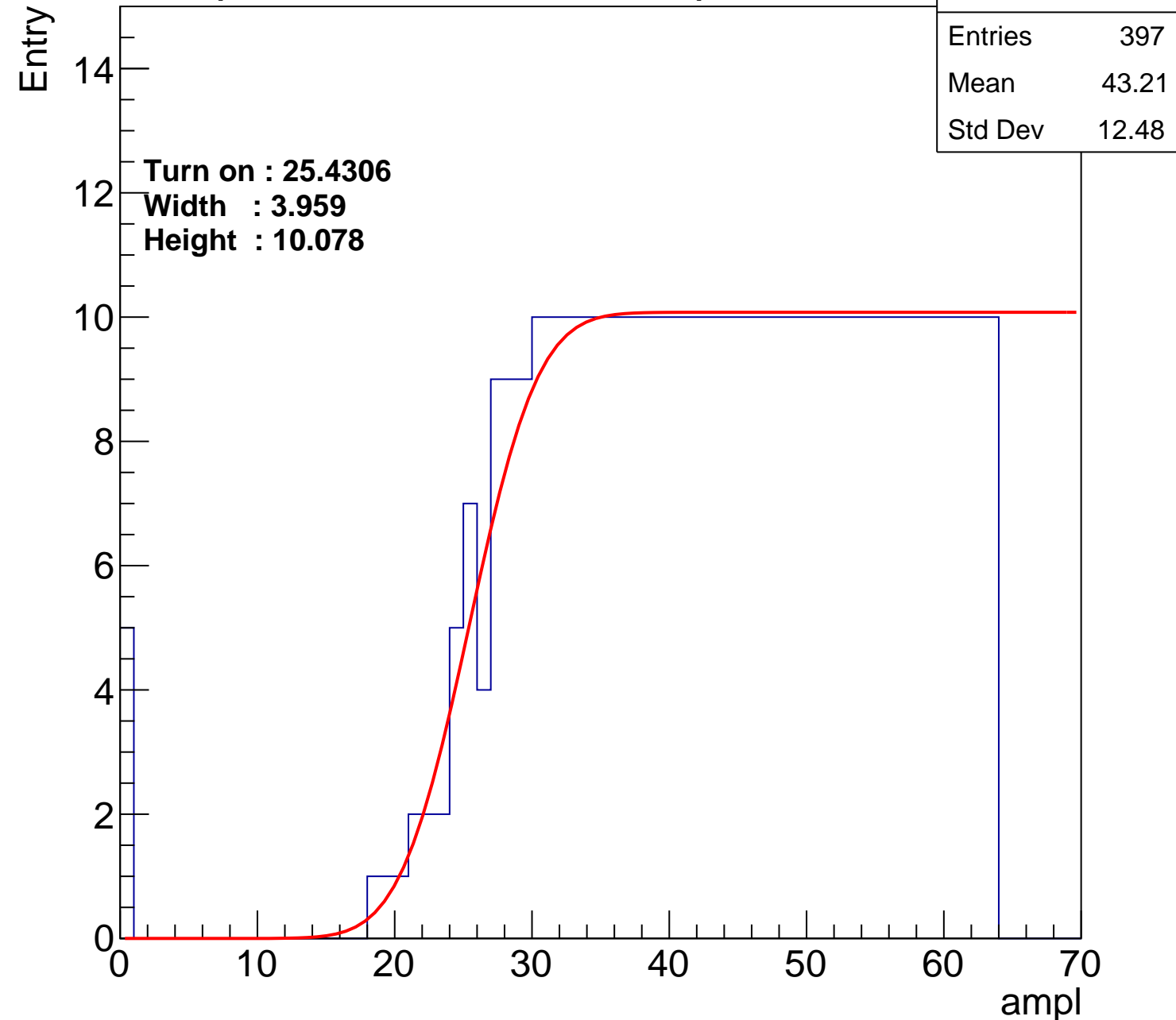
Width : 3.959

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch93

calib\_packv5\_042523\_0143.root, FC#4, port A2

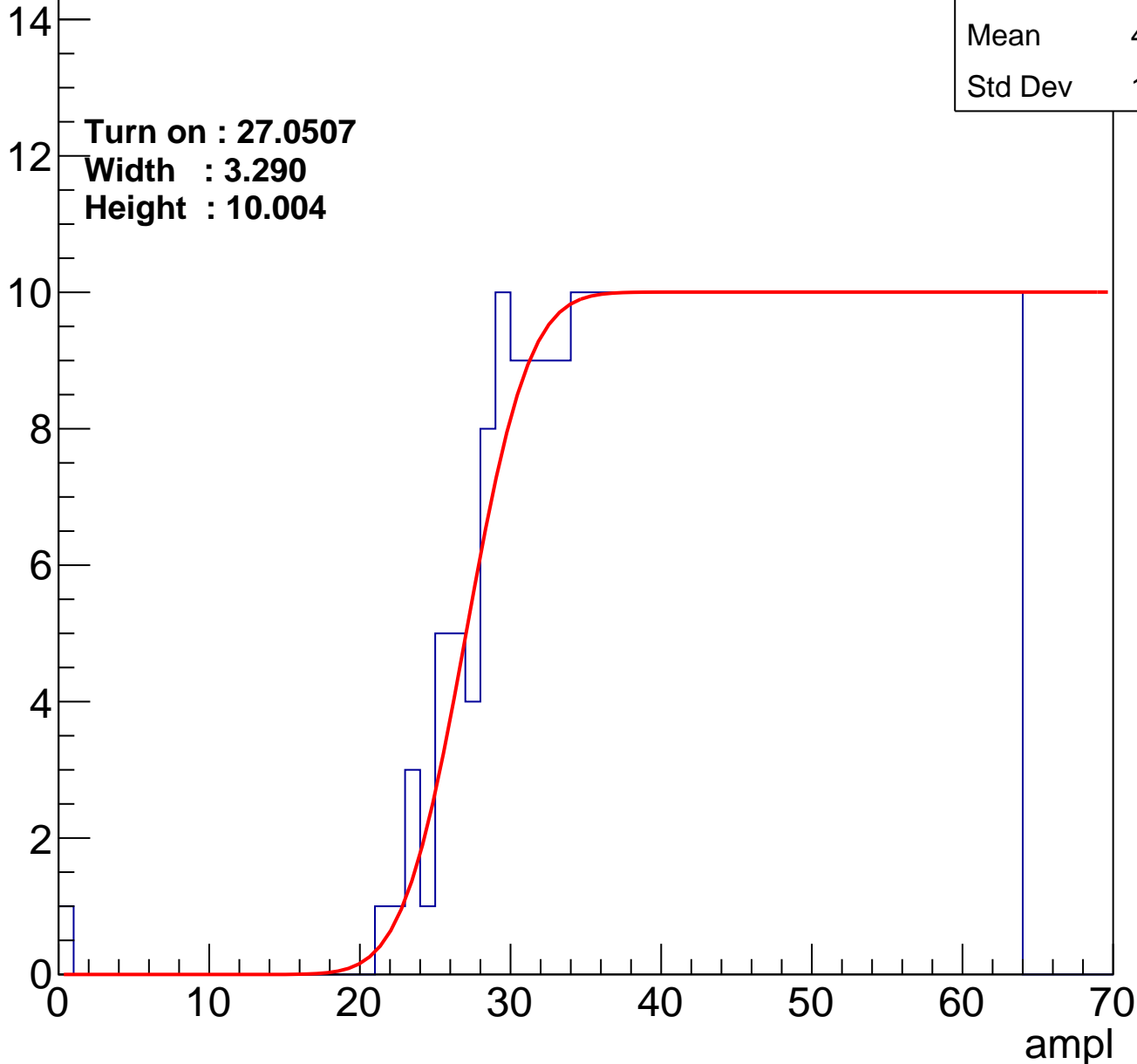
Entries	375
Mean	44.53
Std Dev	11.27

Turn on : 27.0507

Width : 3.290

Height : 10.004

Entry



# B1L100S, U15-ch94

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	390
Mean	43.67
Std Dev	12

Turn on : 25.7623

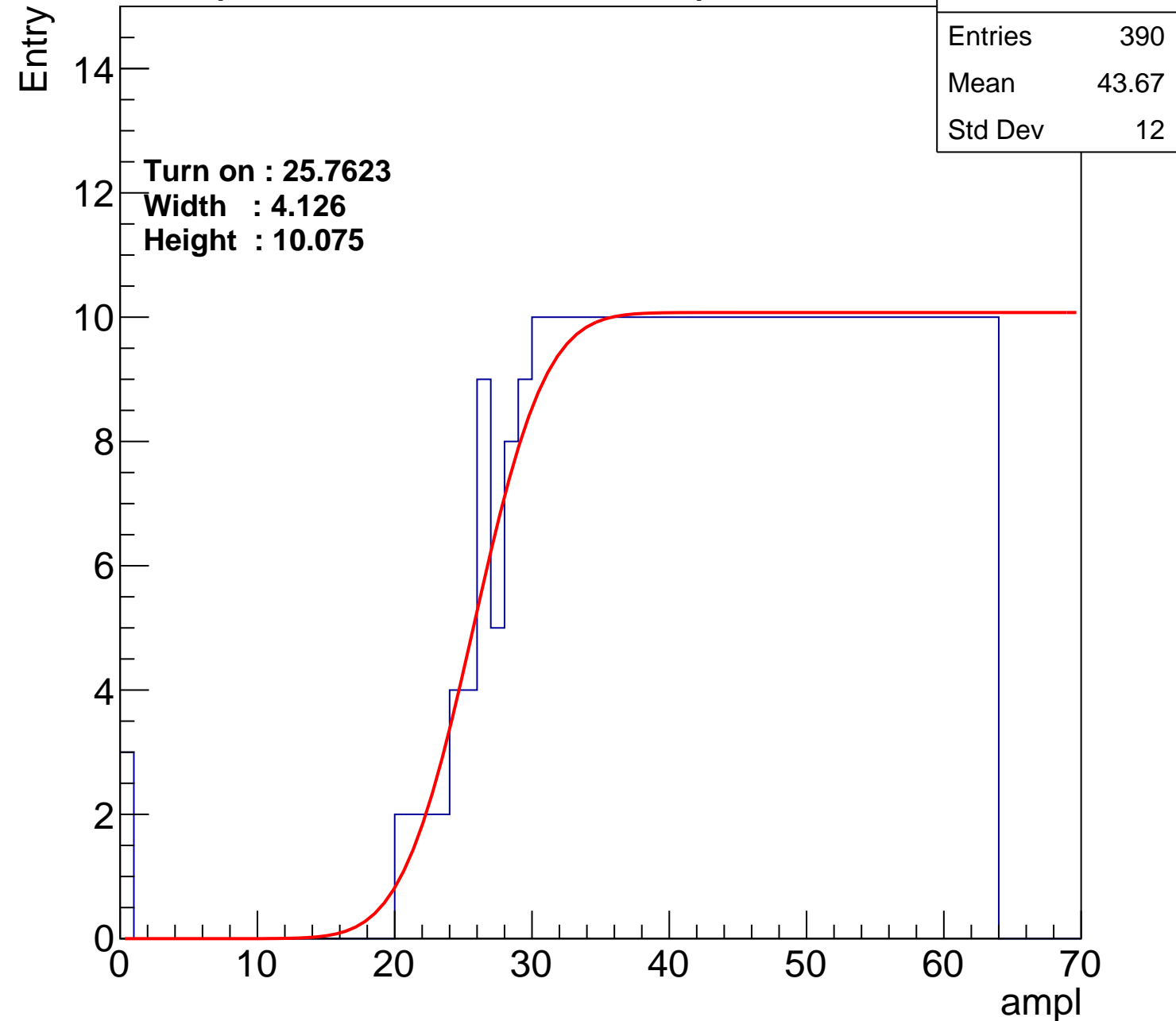
Width : 4.126

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch95

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	386
Mean	43.62
Std Dev	12.55

Turn on : 26.3547

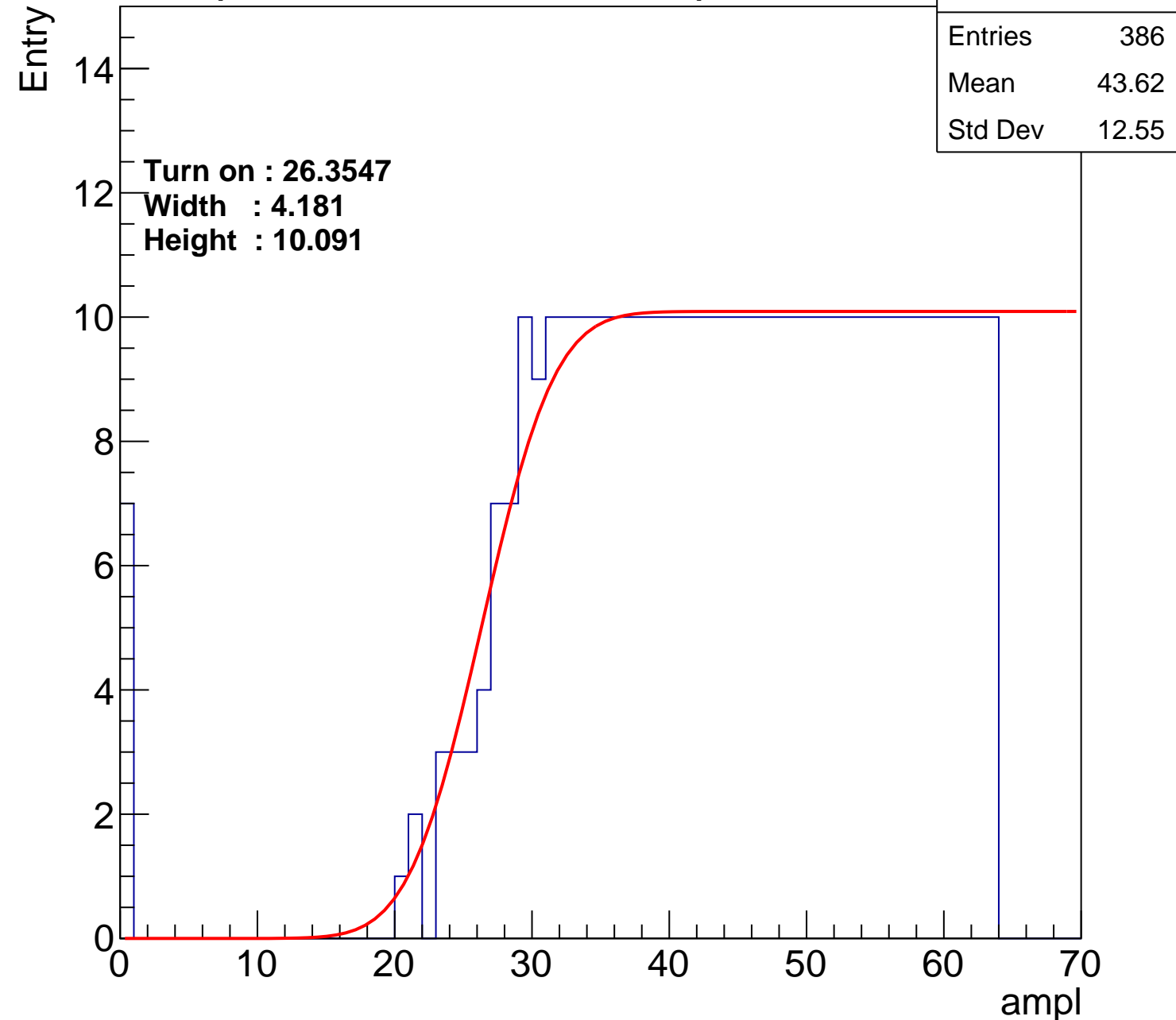
Width : 4.181

Height : 10.091

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch96

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	394
Mean	43.53
Std Dev	11.94

Turn on : 25.7555

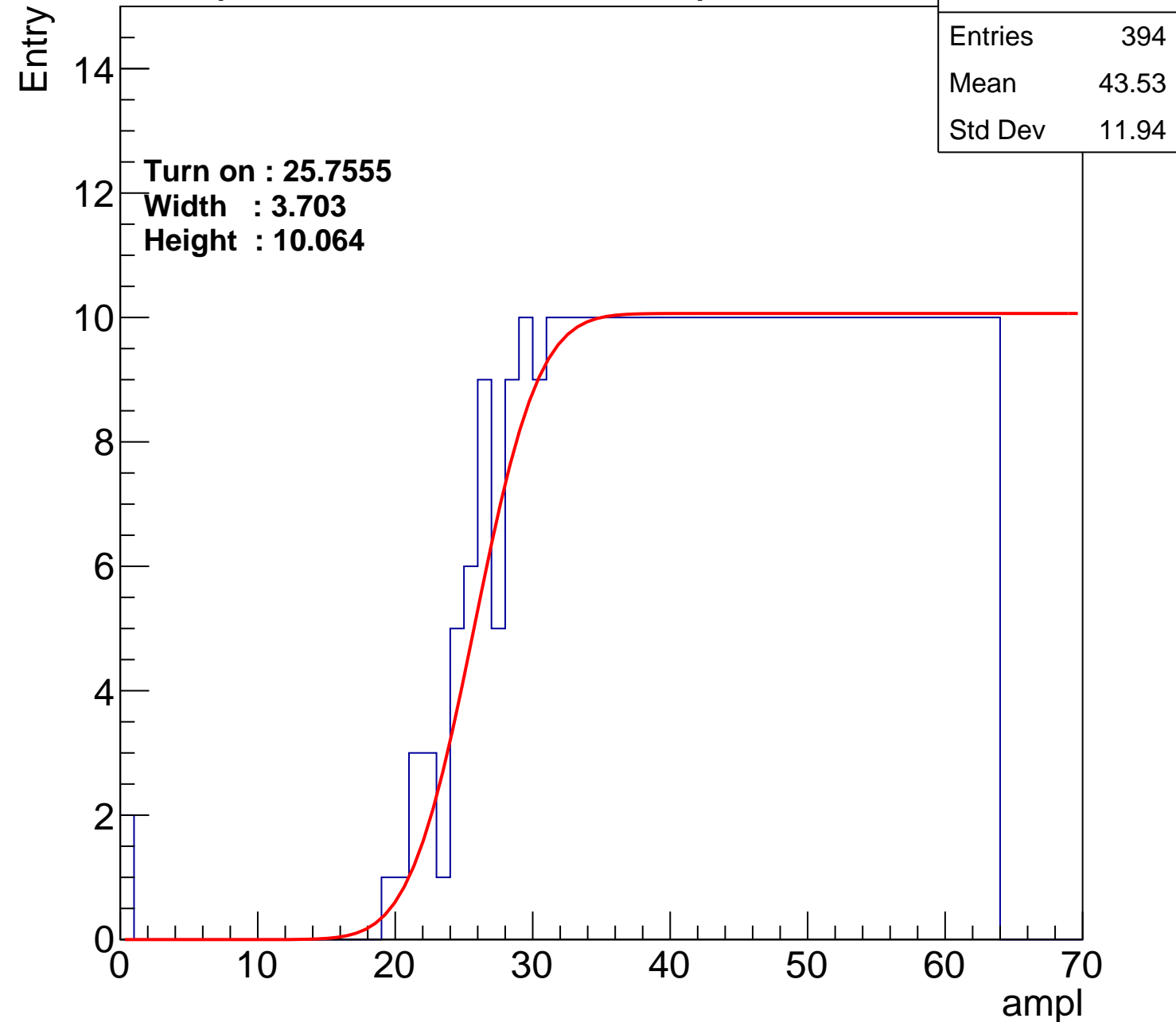
Width : 3.703

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch97

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	387
Mean	43.89
Std Dev	11.75

Turn on : 26.0856

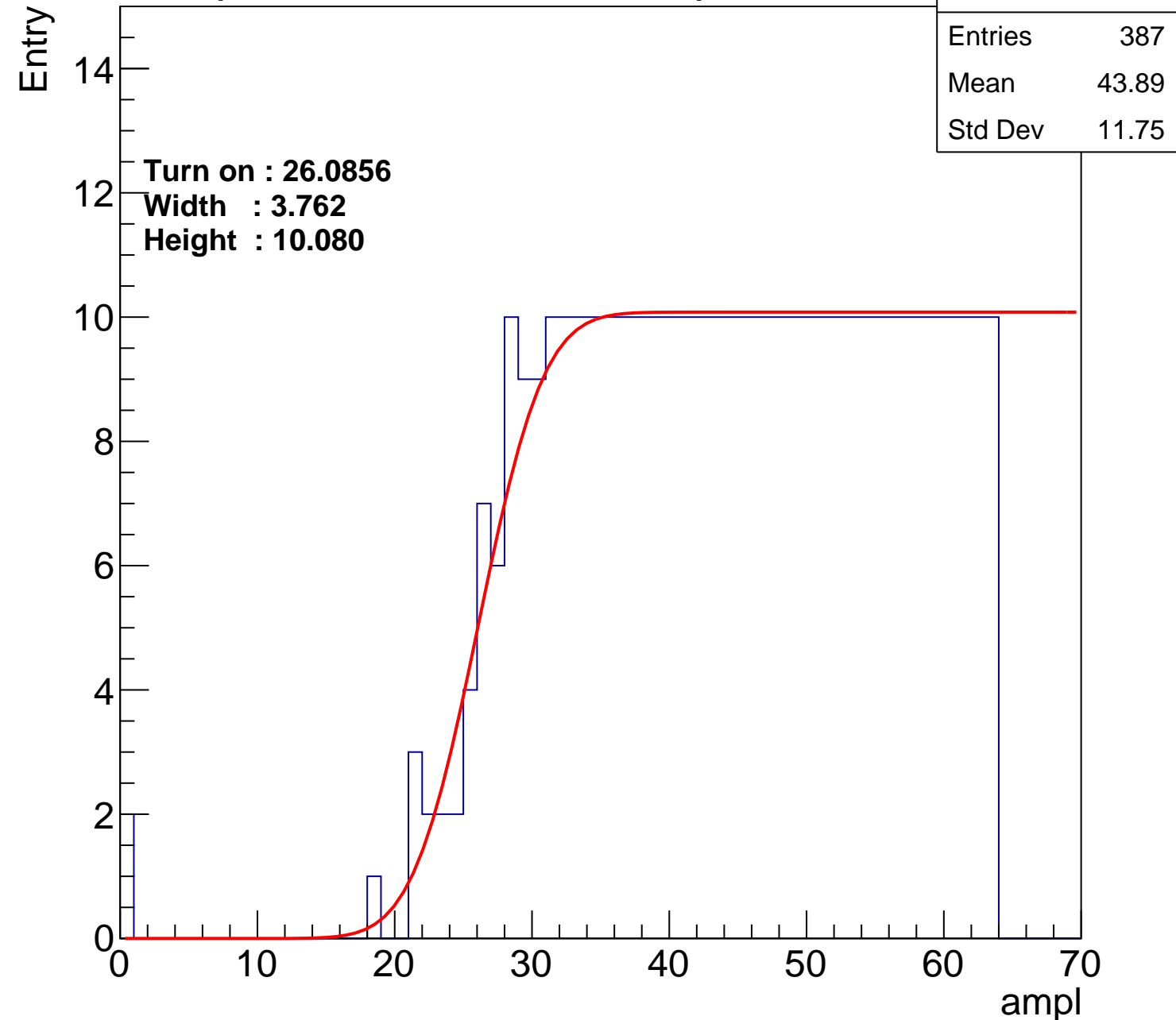
Width : 3.762

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch98

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.42
Std Dev	11.61

**Turn on : 26.8975**

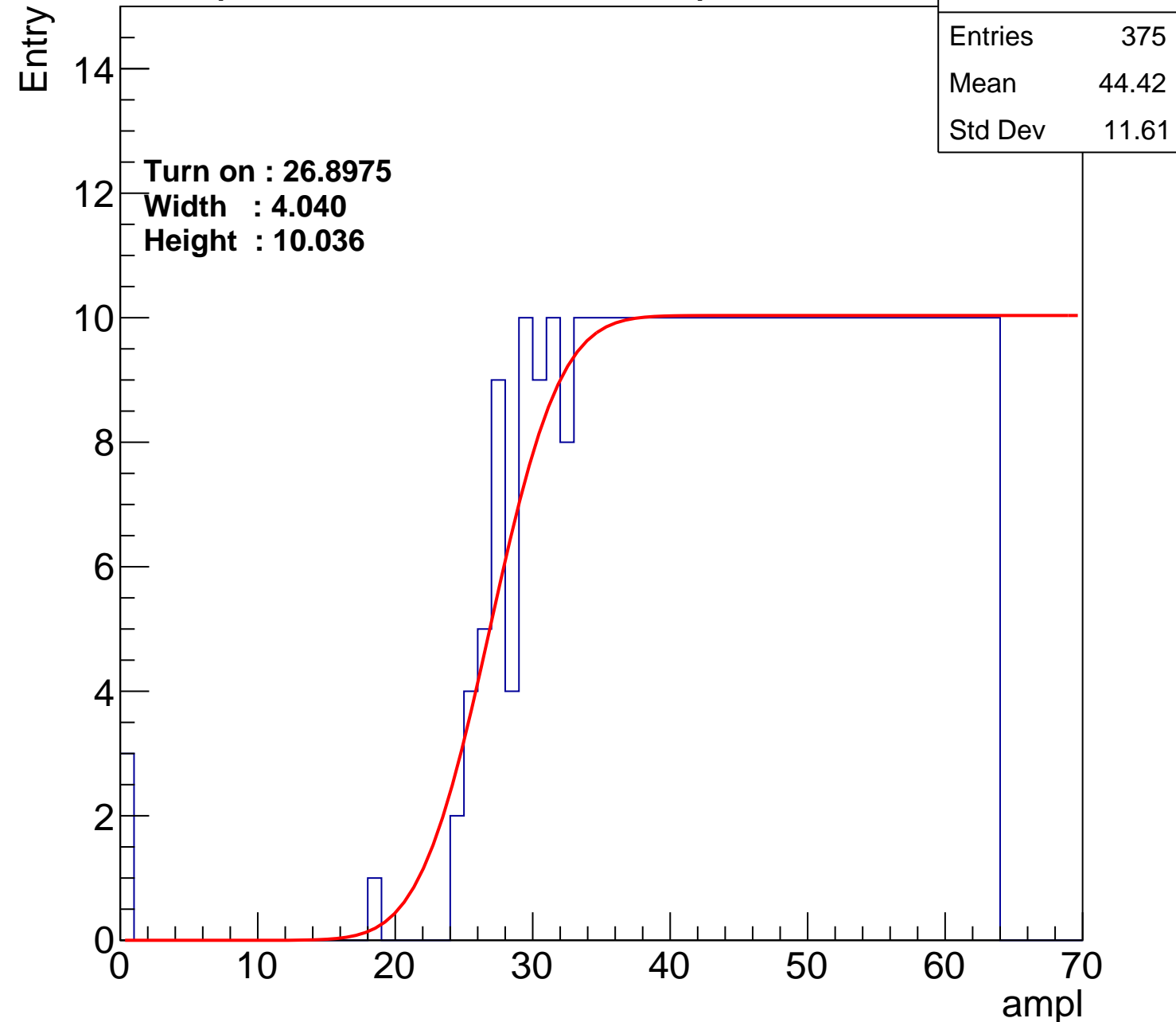
**Width : 4.040**

**Height : 10.036**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch99

calib\_packv5\_042523\_0143.root, FC#4, port A2

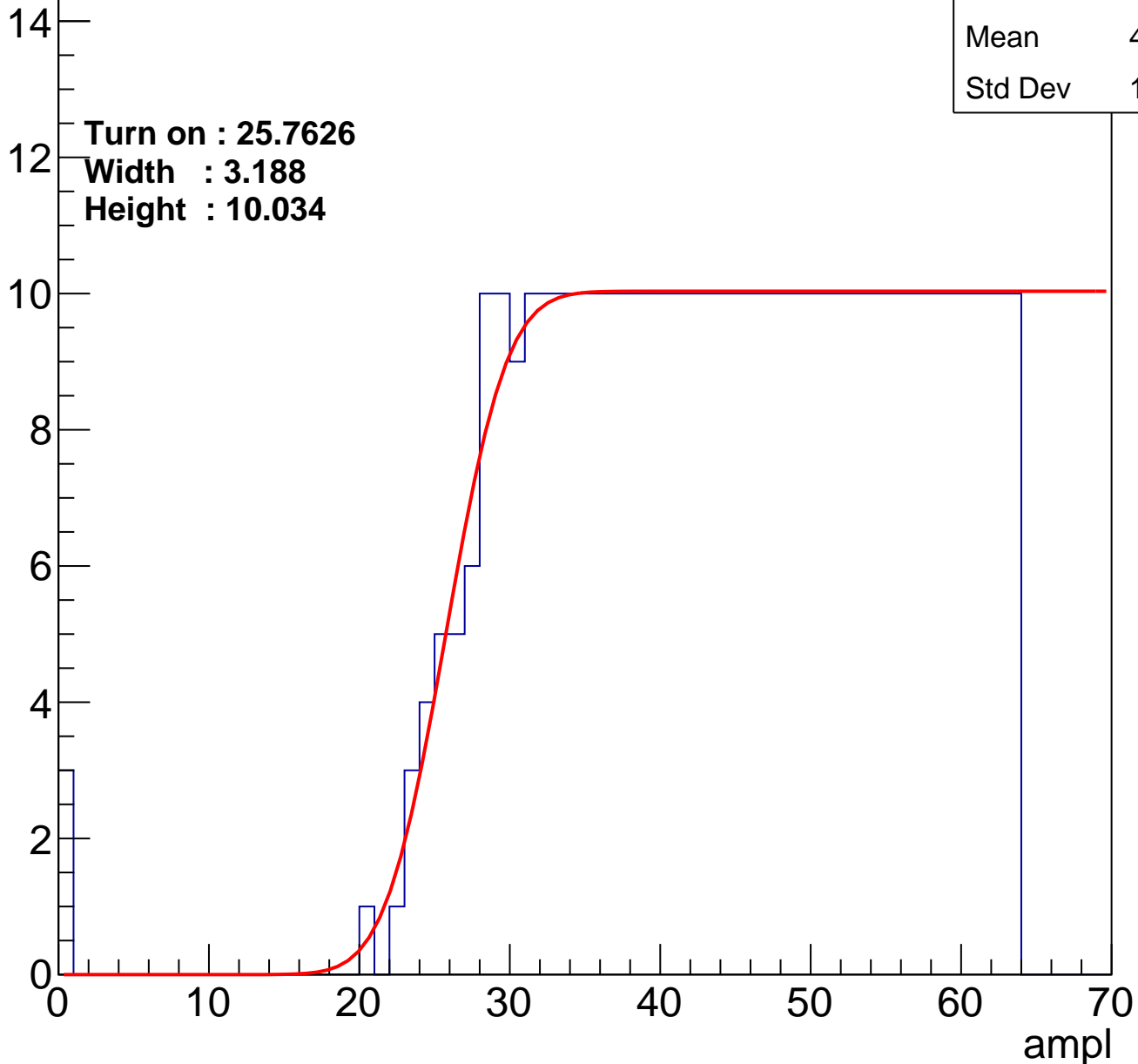
Entries	387
Mean	43.86
Std Dev	11.86

Turn on : 25.7626

Width : 3.188

Height : 10.034

Entry



# B1L100S, U15-ch100

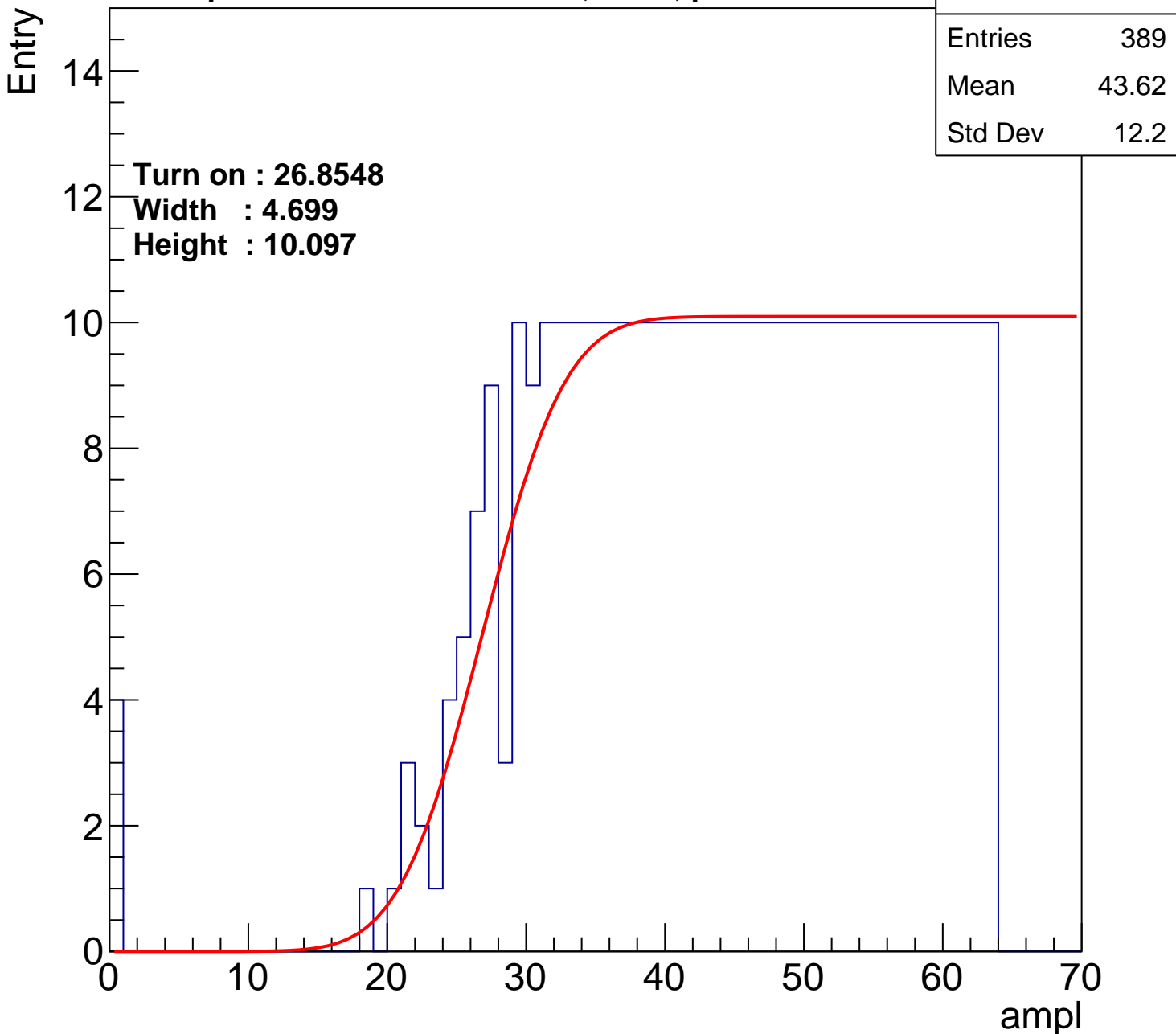
**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	389
Mean	43.62
Std Dev	12.2

**Turn on : 26.8548**

**Width : 4.699**

**Height : 10.097**



# B1L100S, U15-ch101

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	399
Mean	43.33
Std Dev	12

Turn on : 24.1326

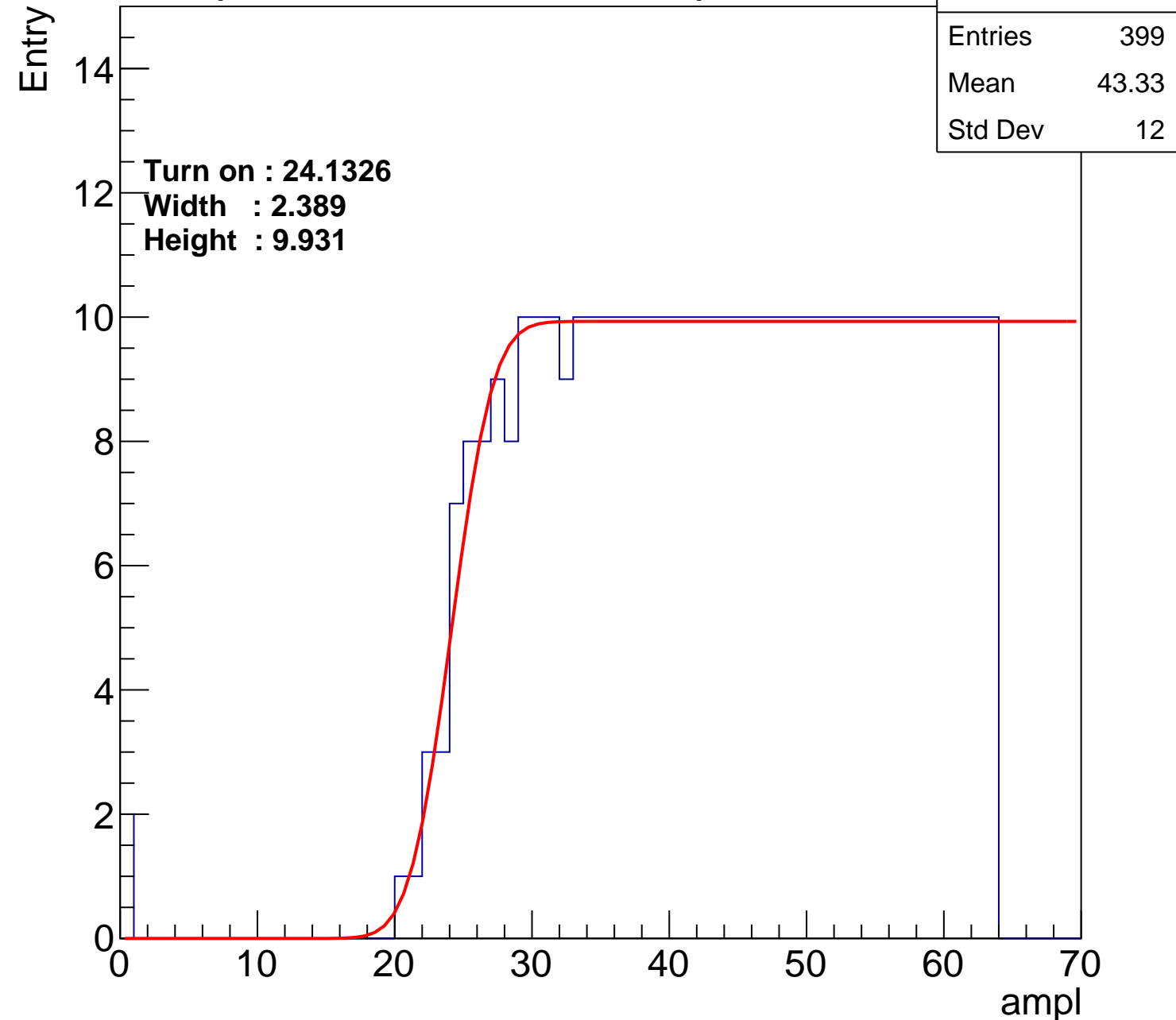
Width : 2.389

Height : 9.931

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch102

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.9534

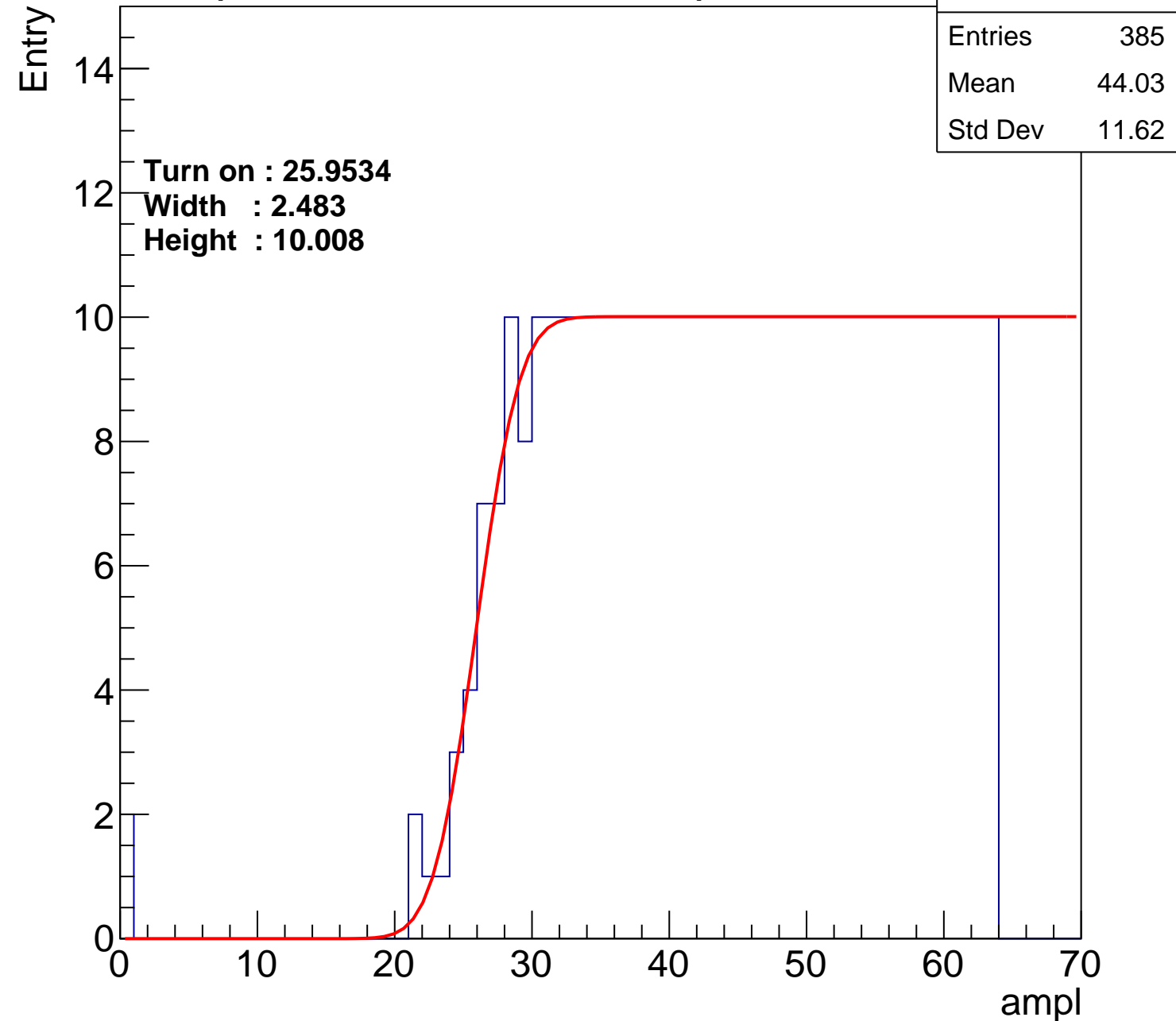
Width : 2.483

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch103

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.67
Std Dev	11.54

Turn on : 27.5914

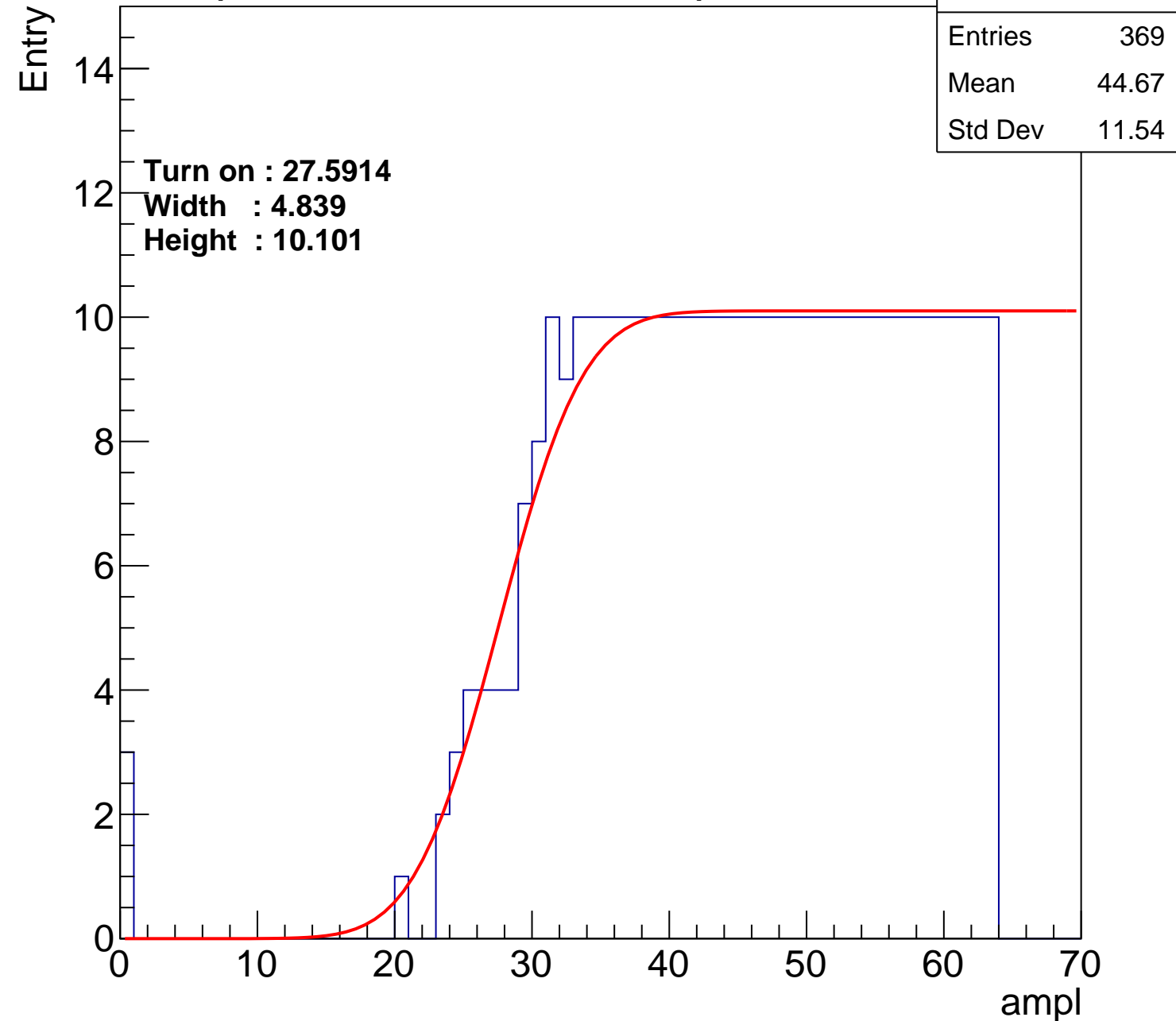
Width : 4.839

Height : 10.101

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch104

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	398
Mean	43.15
Std Dev	12.56

Turn on : 24.4610

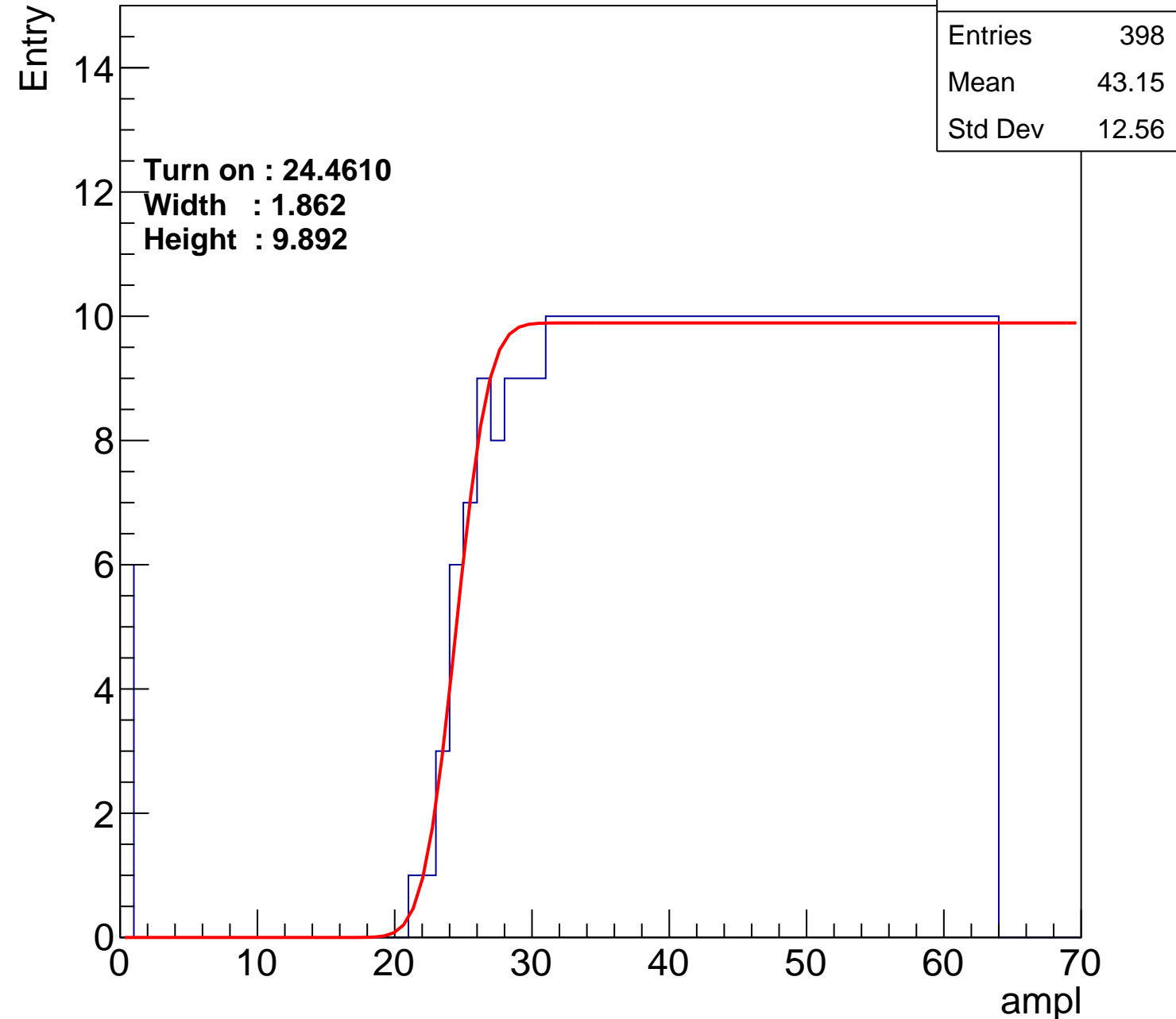
Width : 1.862

Height : 9.892

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch105

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	385
Mean	43.86
Std Dev	12.03

Turn on : 26.1111

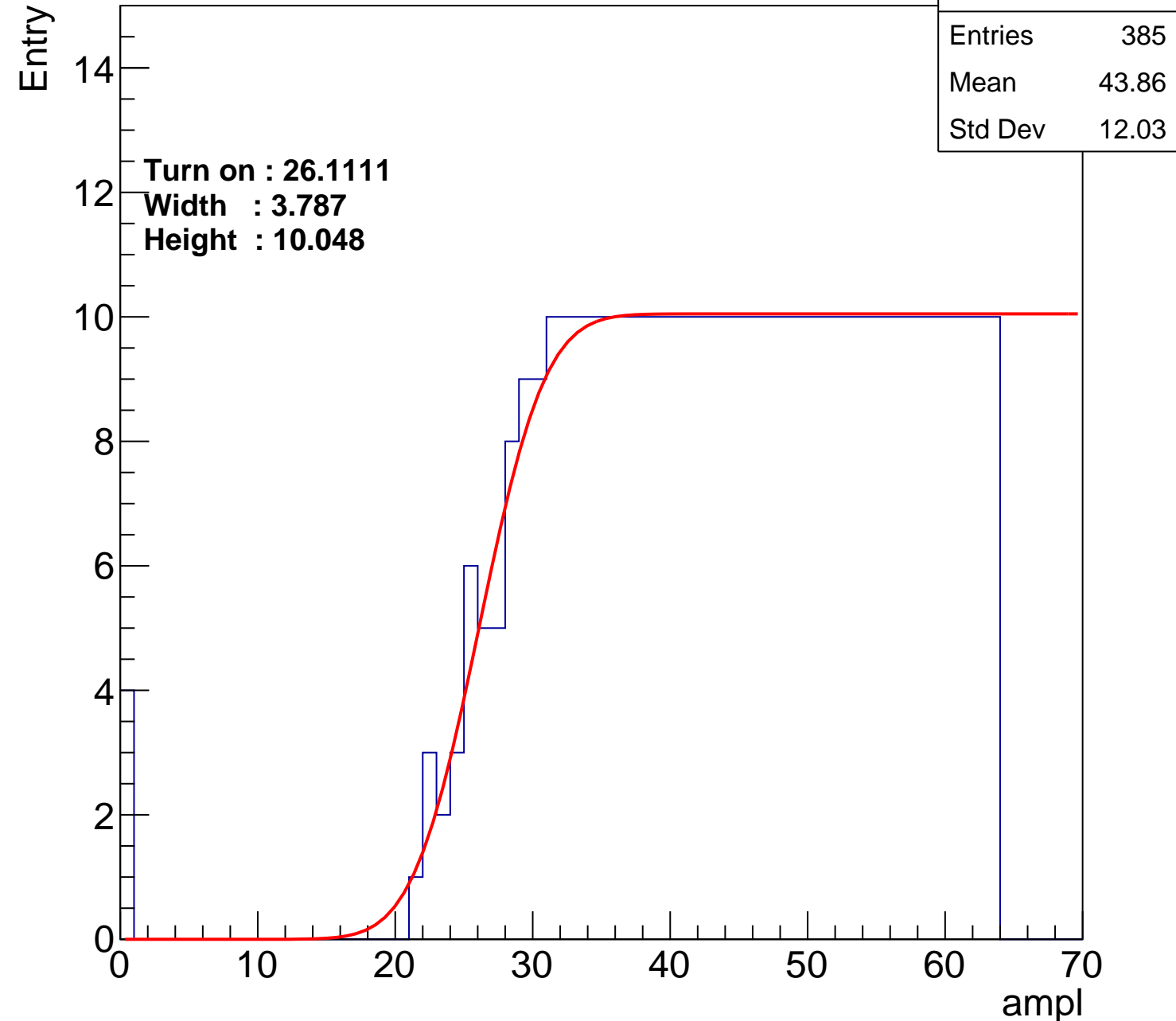
Width : 3.787

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch106

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	404
Mean	42.88
Std Dev	12.62

**Turn on : 24.5508**

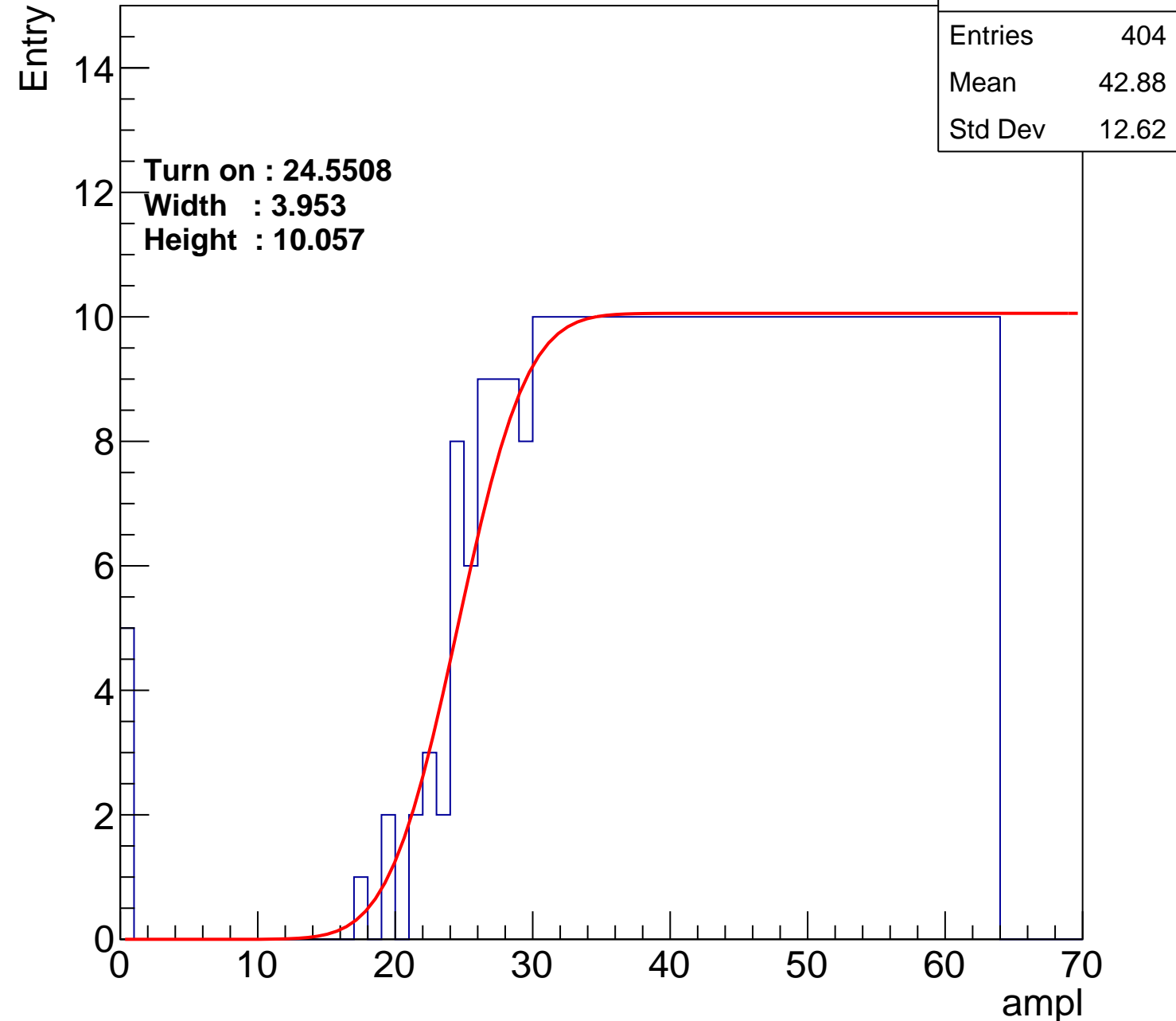
**Width : 3.953**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch107

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	350
Mean	45.75
Std Dev	10.63

Turn on : 30.0109

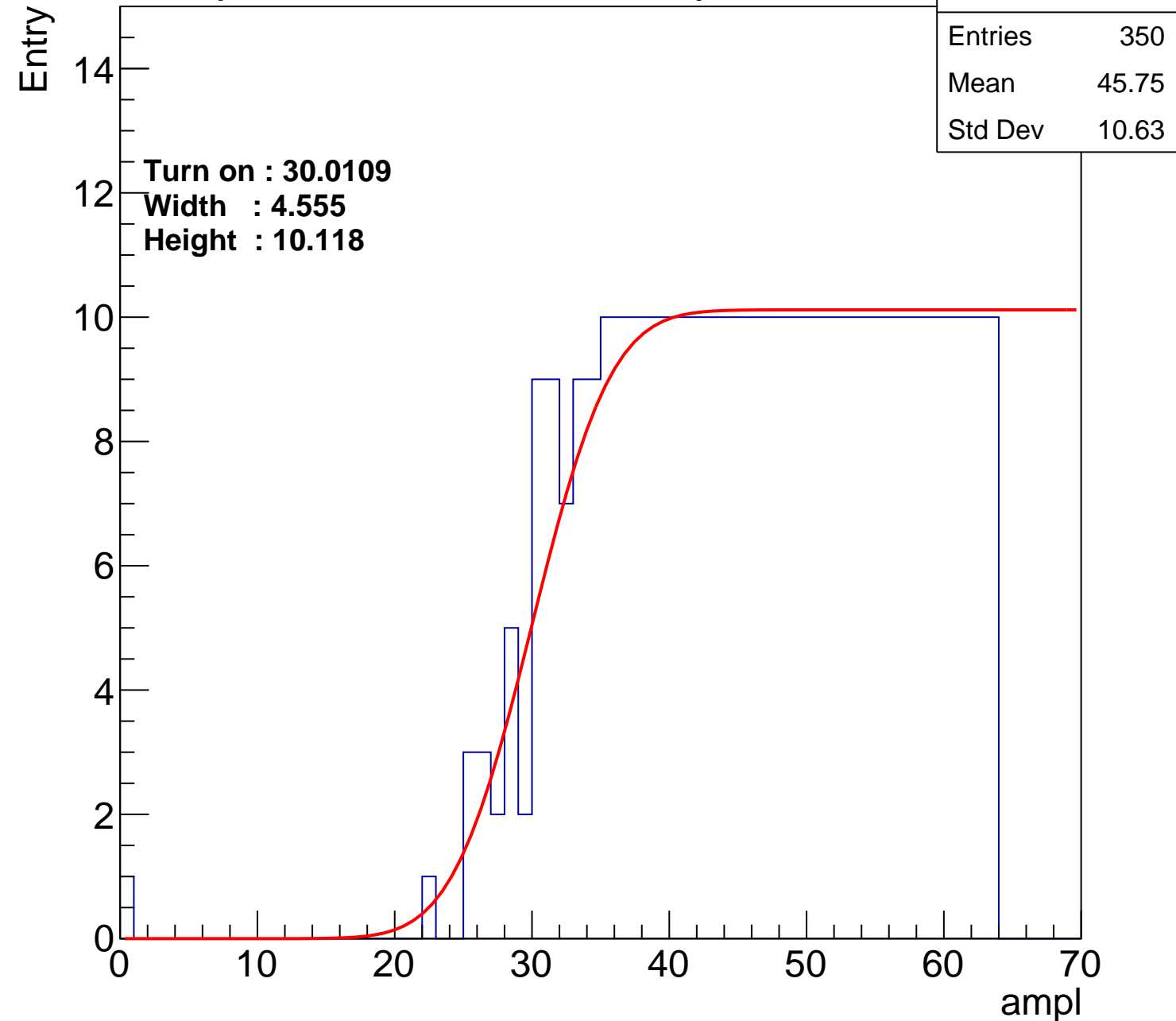
Width : 4.555

Height : 10.118

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch108

calib\_packv5\_042523\_0143.root, FC#4, port A2

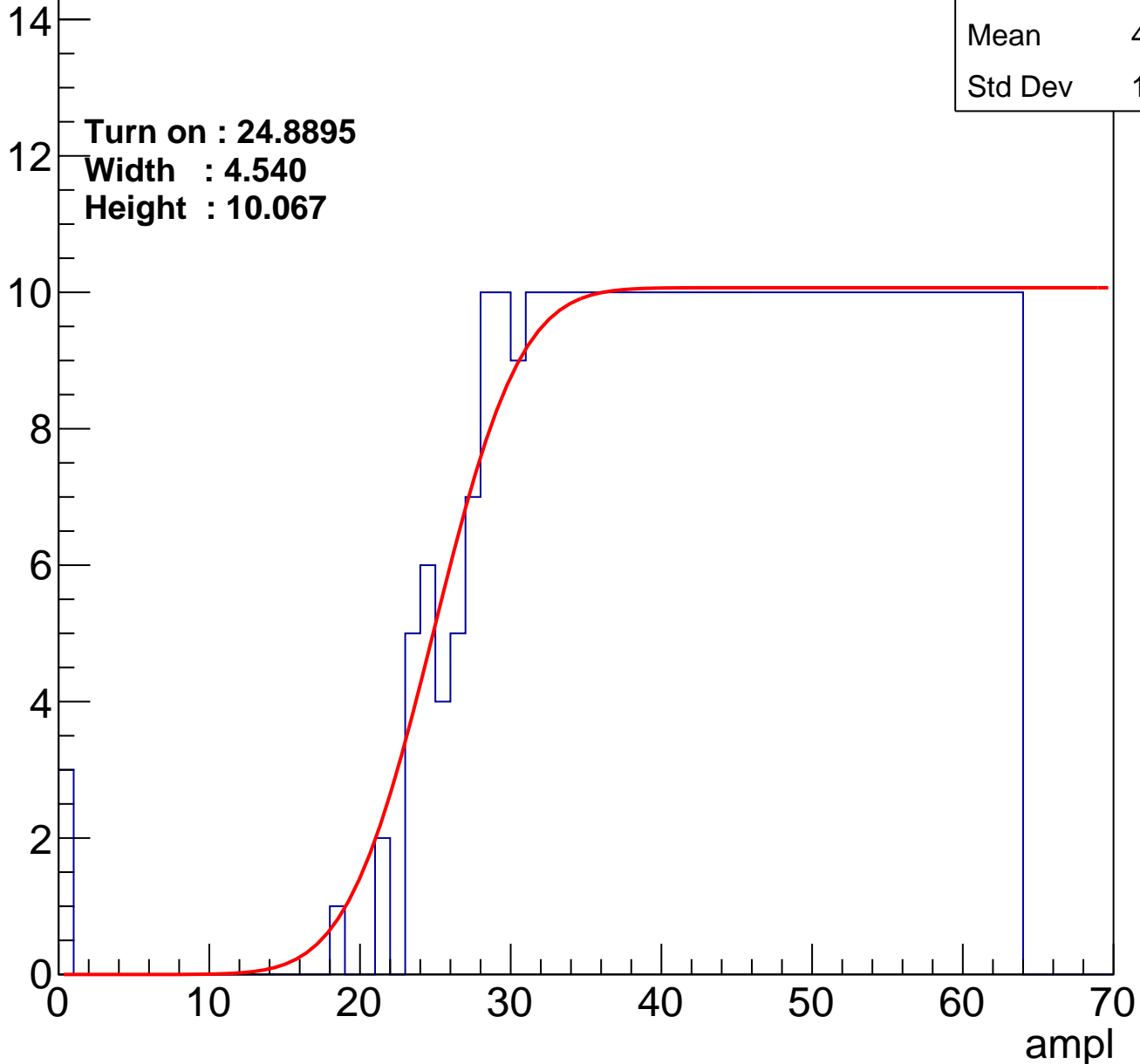
Entries	392
Mean	43.59
Std Dev	12.02

Turn on : 24.8895

Width : 4.540

Height : 10.067

Entry



# B1L100S, U15-ch109

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	373
Mean	44.4
Std Dev	11.9

Turn on : 27.0590

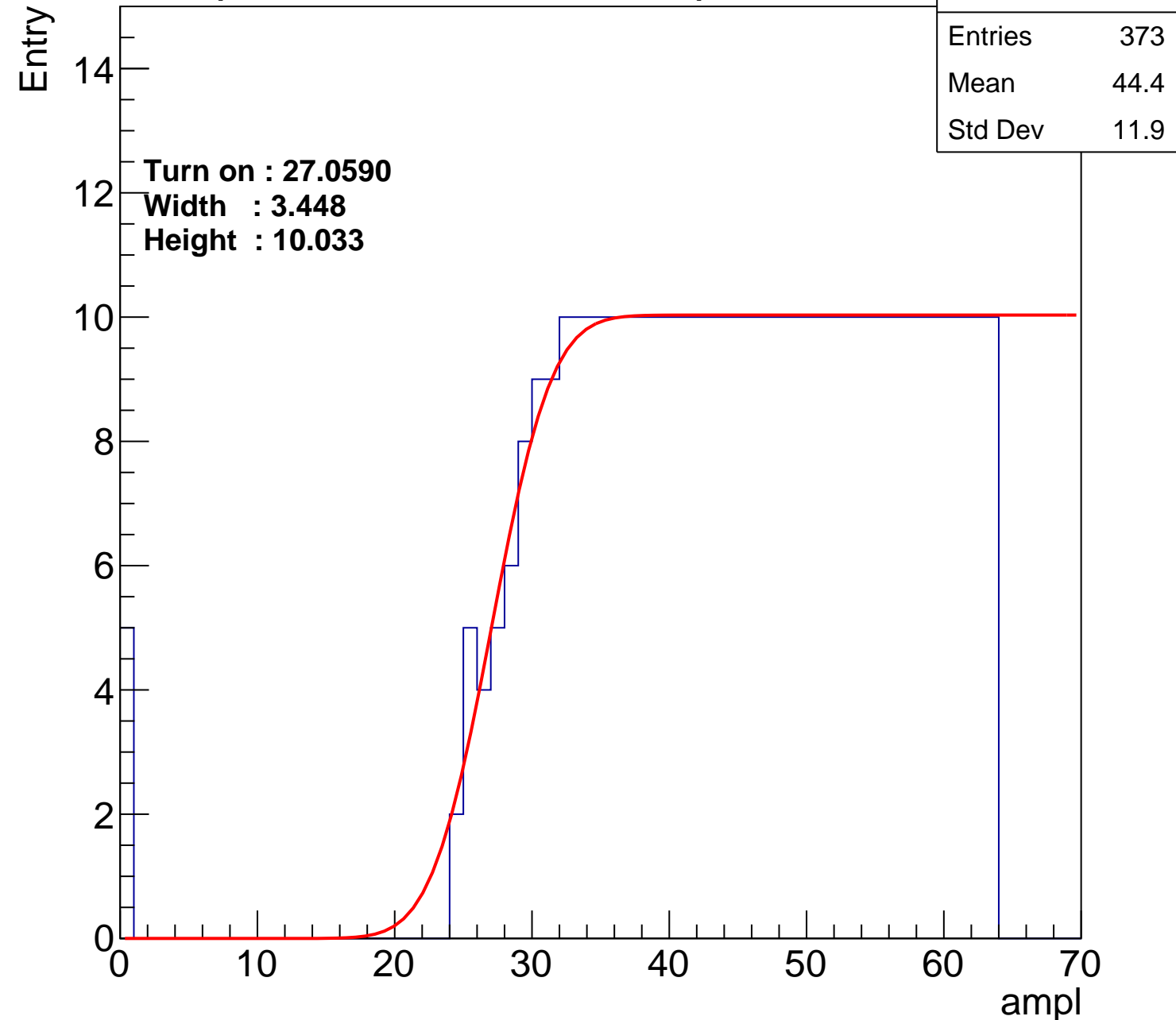
Width : 3.448

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch110

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	404
Mean	43.09
Std Dev	12.12

Turn on : 24.2367

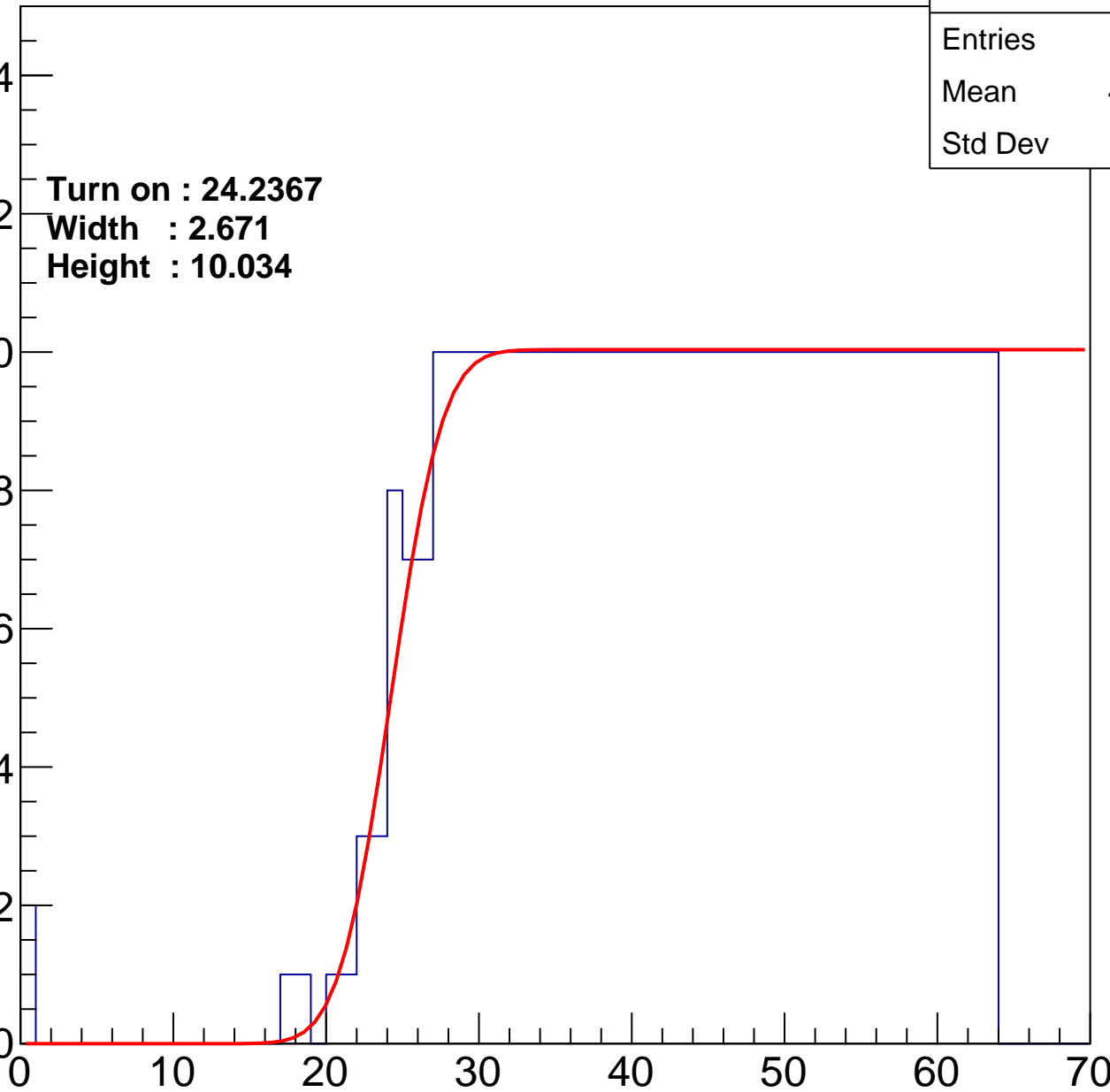
Width : 2.671

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch111

calib\_packv5\_042523\_0143.root, FC#4, port A2

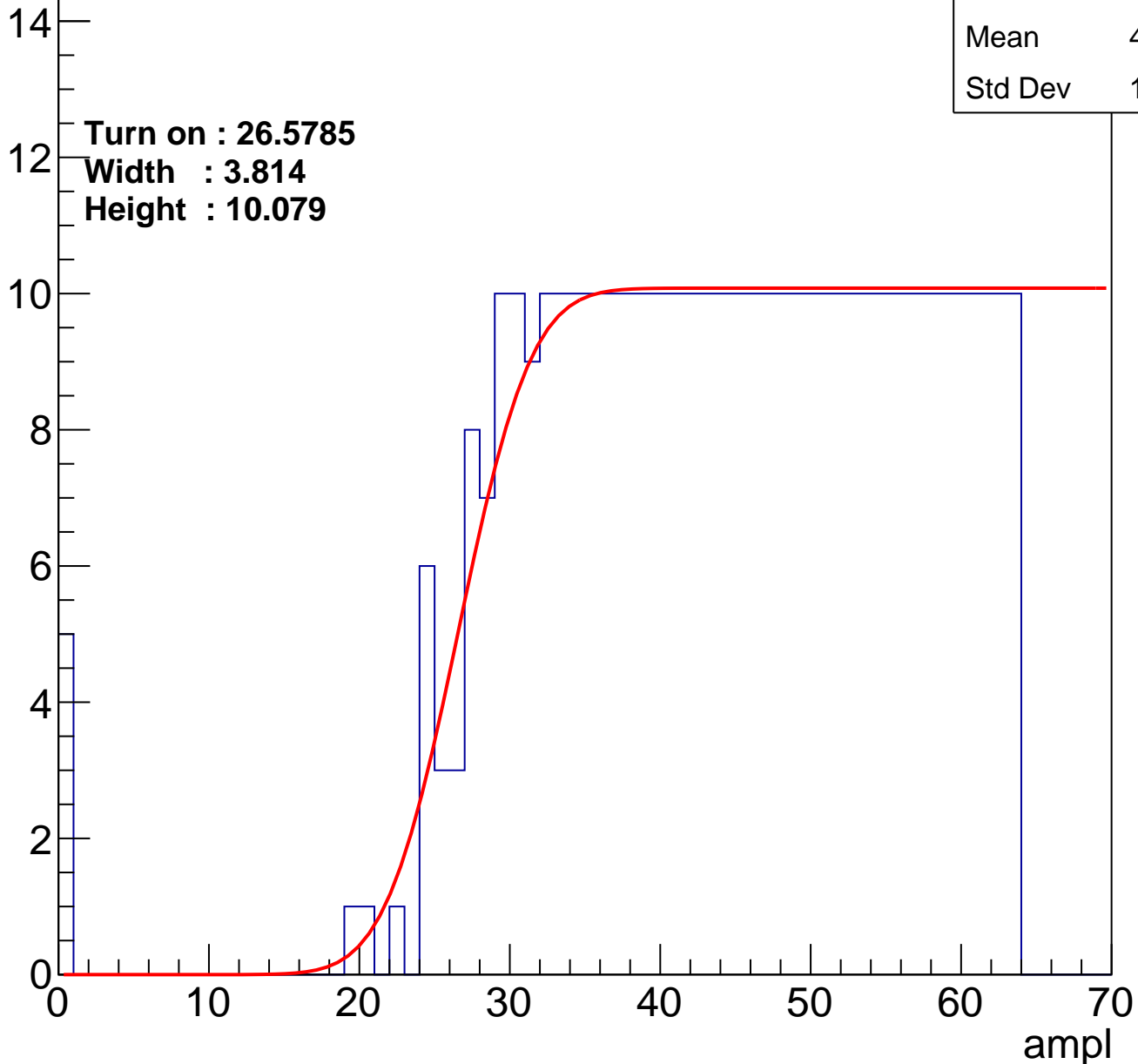
Entries	384
Mean	43.85
Std Dev	12.17

**Turn on : 26.5785**

**Width : 3.814**

**Height : 10.079**

Entry



# B1L100S, U15-ch112

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	397
Mean	43.35
Std Dev	12.13

Turn on : 24.8702

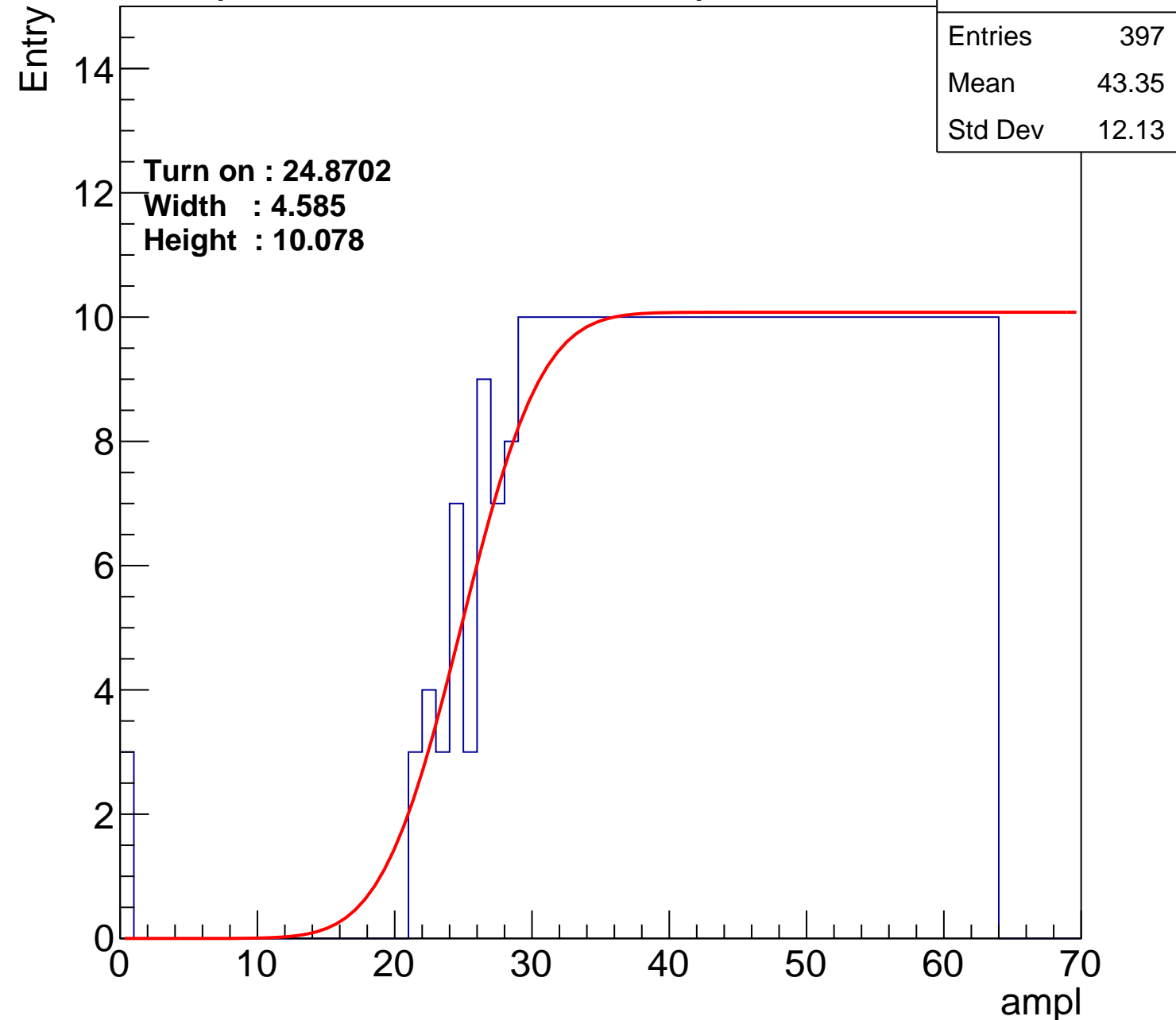
Width : 4.585

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch113

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	405
Mean	42.96
Std Dev	12.34

**Turn on : 24.2332**

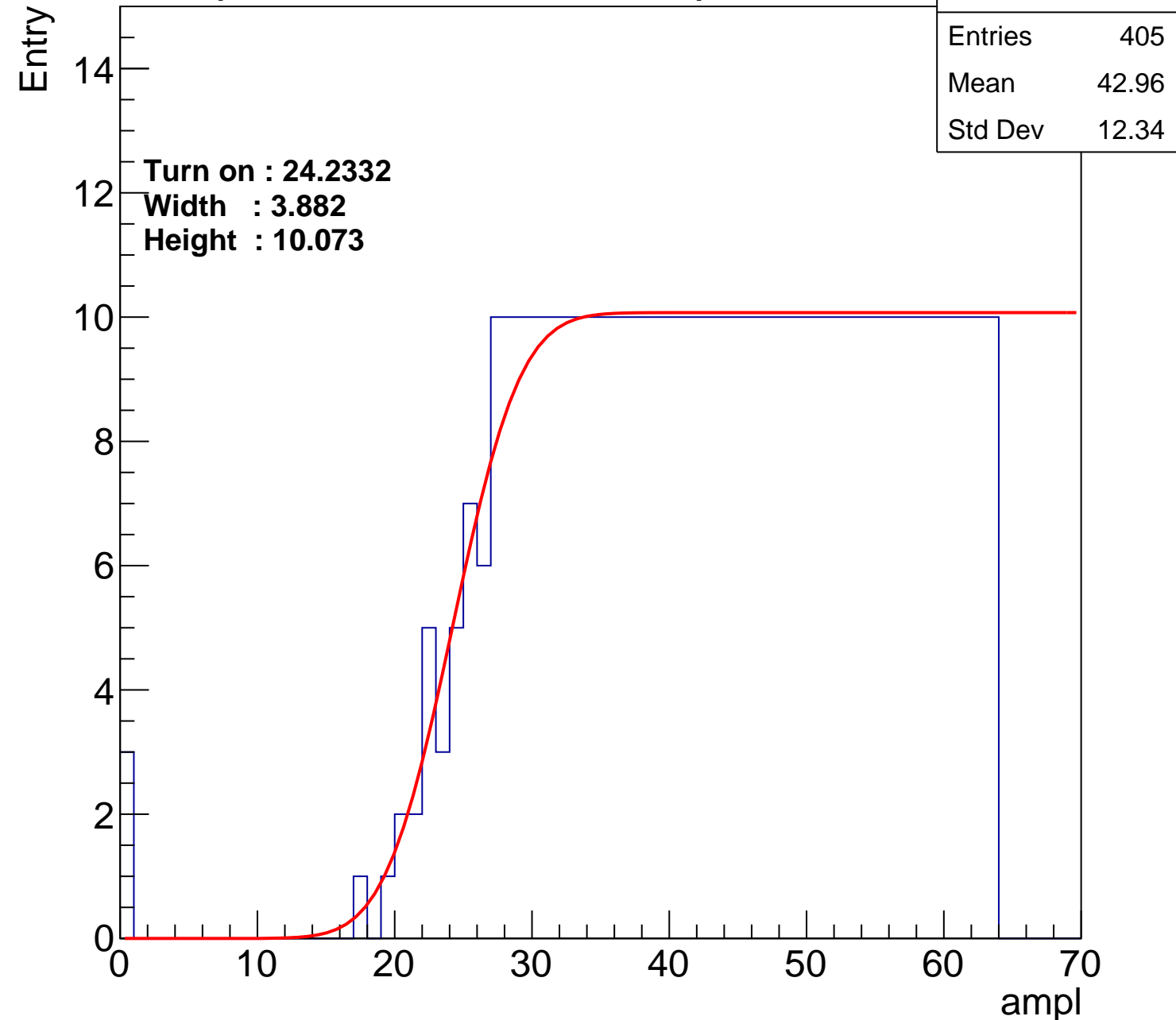
**Width : 3.882**

**Height : 10.073**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch114

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	388
Mean	43.67
Std Dev	12.23

**Turn on : 25.8804**

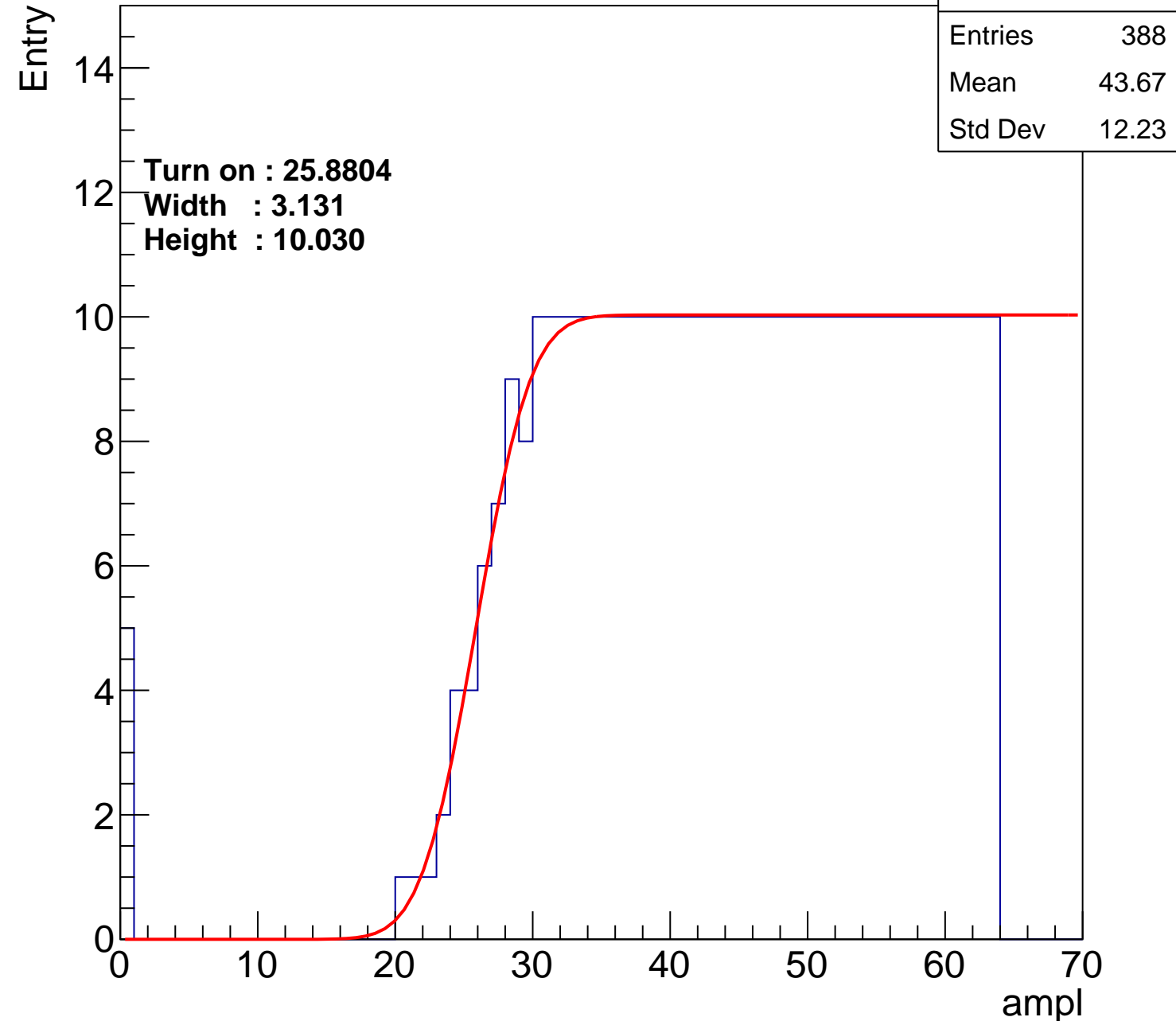
**Width : 3.131**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch115

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 26.7560

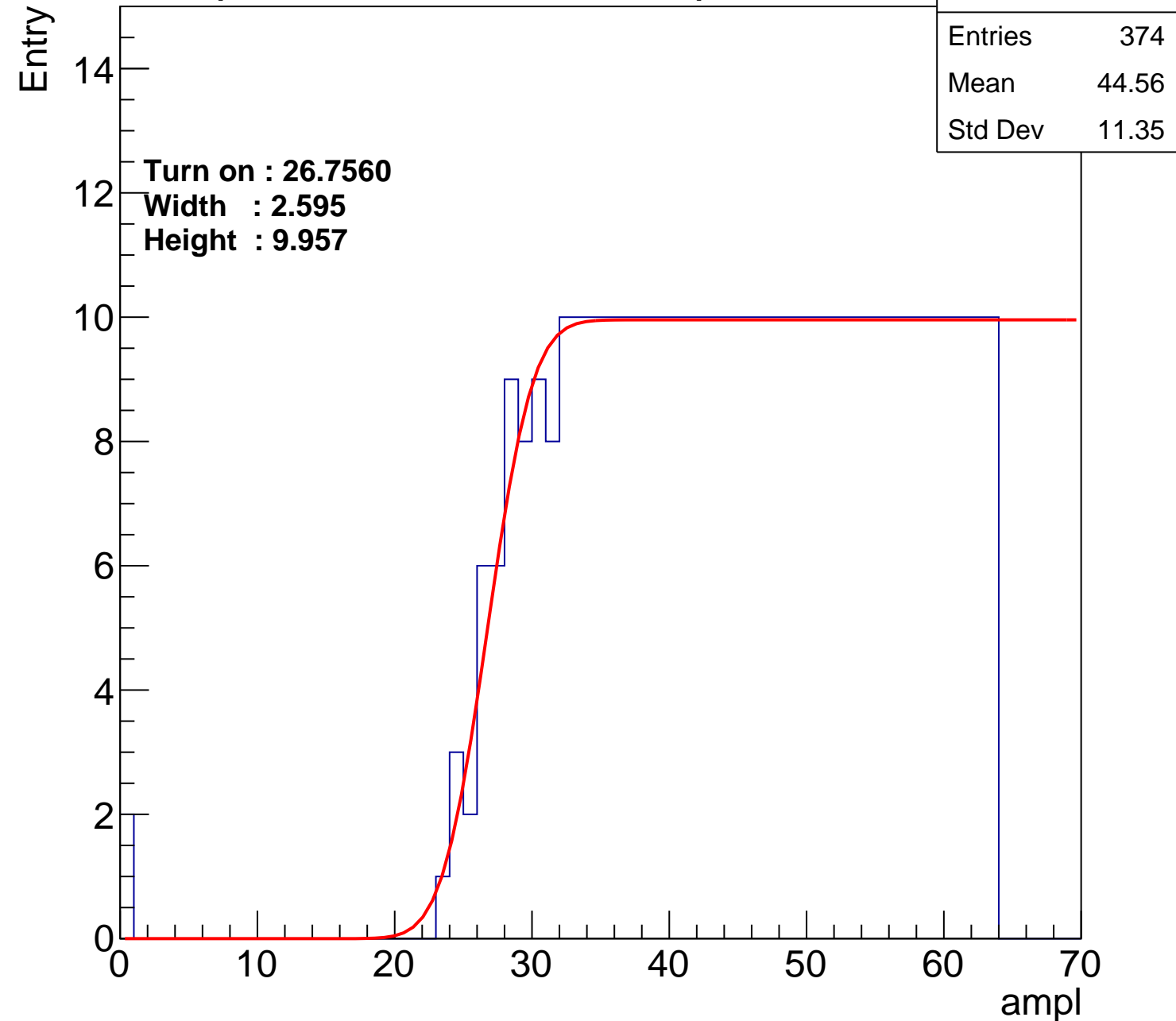
Width : 2.595

Height : 9.957

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch116

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	397
Mean	43.27
Std Dev	12.33

**Turn on : 25.1719**

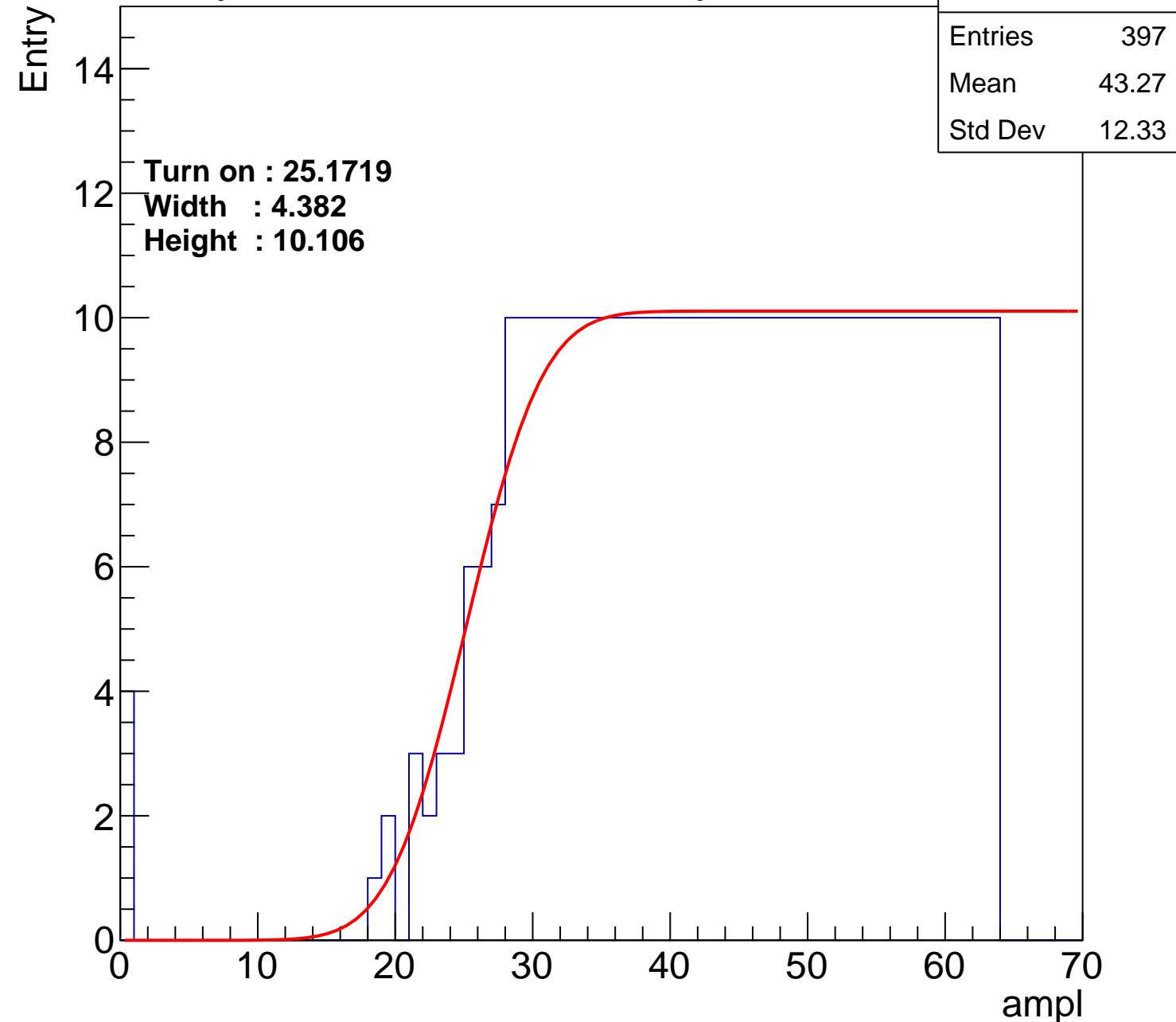
**Width : 4.382**

**Height : 10.106**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch117

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	385
Mean	44.03
Std Dev	11.61

**Turn on : 26.2185**

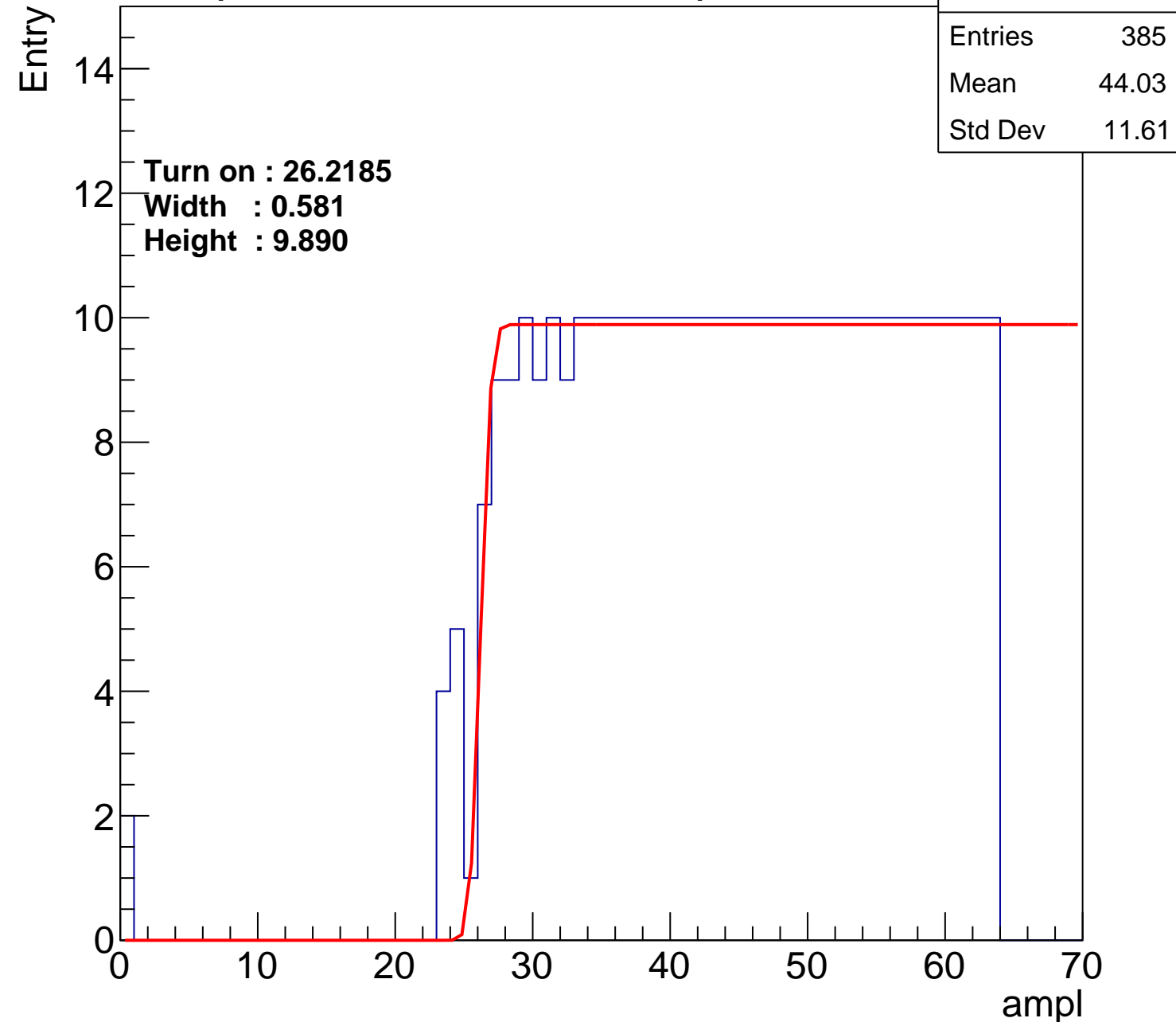
**Width : 0.581**

**Height : 9.890**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch118

calib\_packv5\_042523\_0143.root, FC#4, port A2

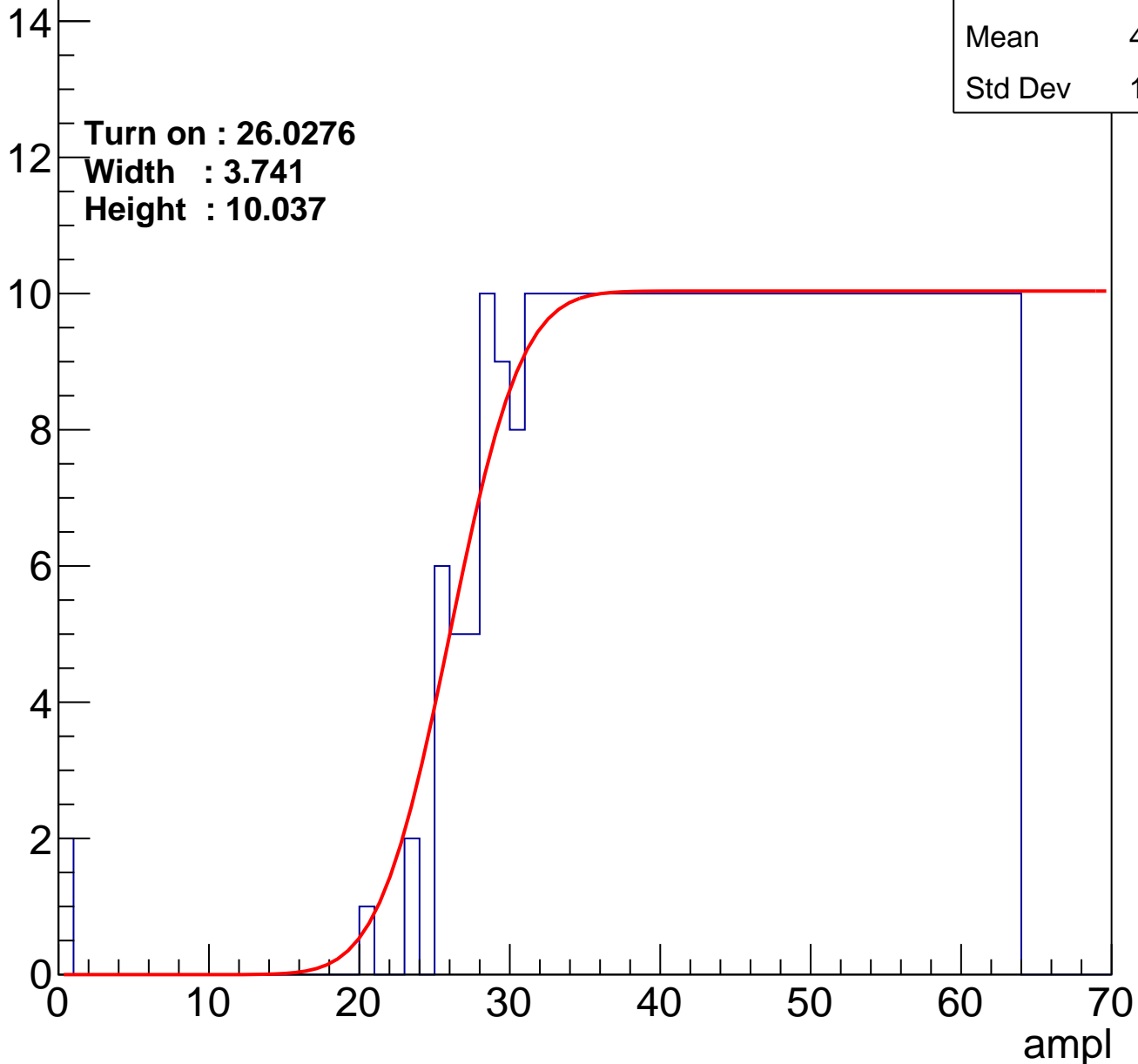
Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.0276

Width : 3.741

Height : 10.037

Entry





# B1L100S, U15-ch119

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	374
Mean	44.53
Std Dev	11.41

Turn on : 26.6773

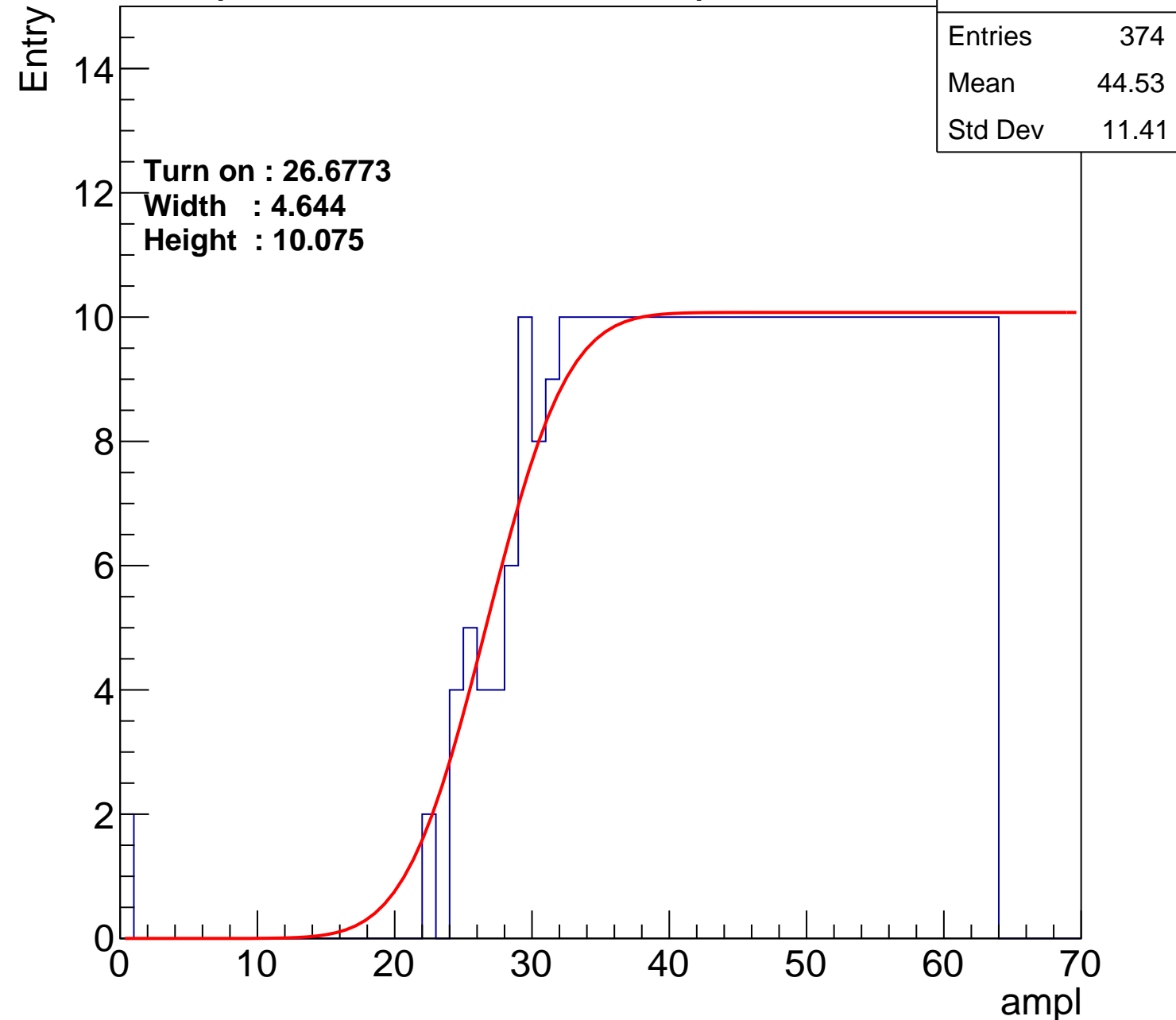
Width : 4.644

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch120

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	379
Mean	44.41
Std Dev	11.25

**Turn on : 26.7137**

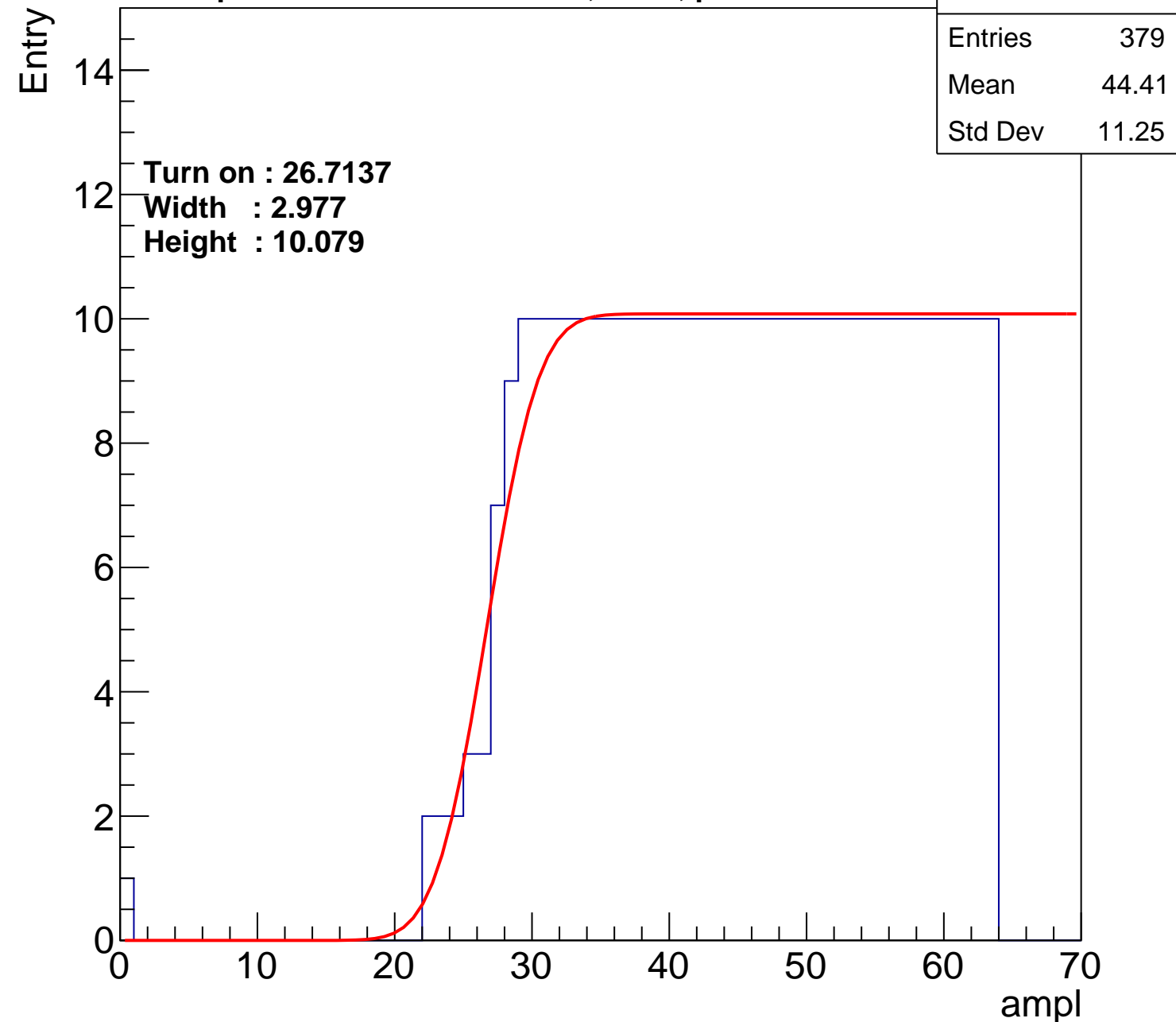
**Width : 2.977**

**Height : 10.079**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch121

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	43.94
Std Dev	12.01

Turn on : 26.1918

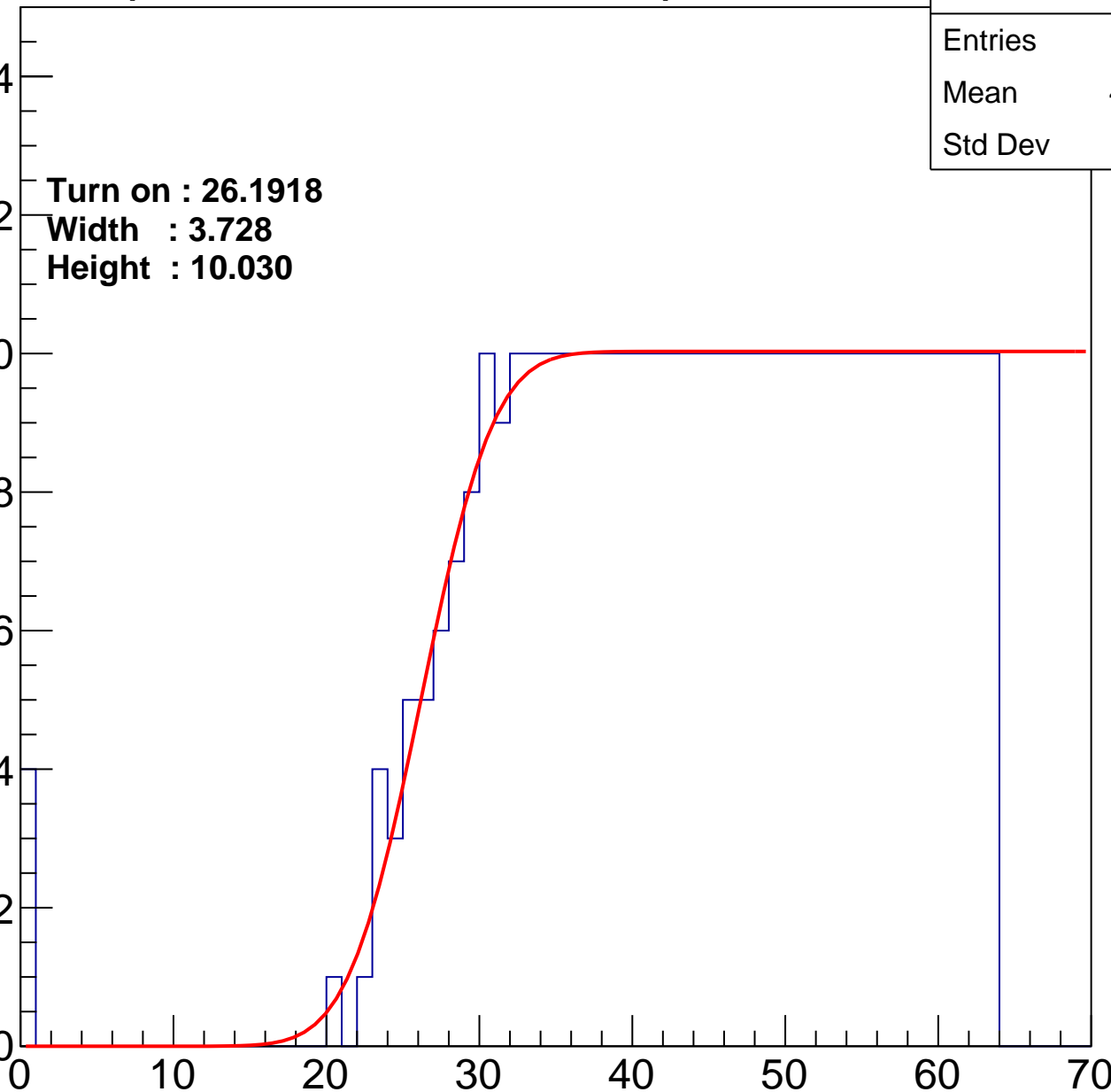
Width : 3.728

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch122

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	384
Mean	44.09
Std Dev	11.5

Turn on : 25.6205

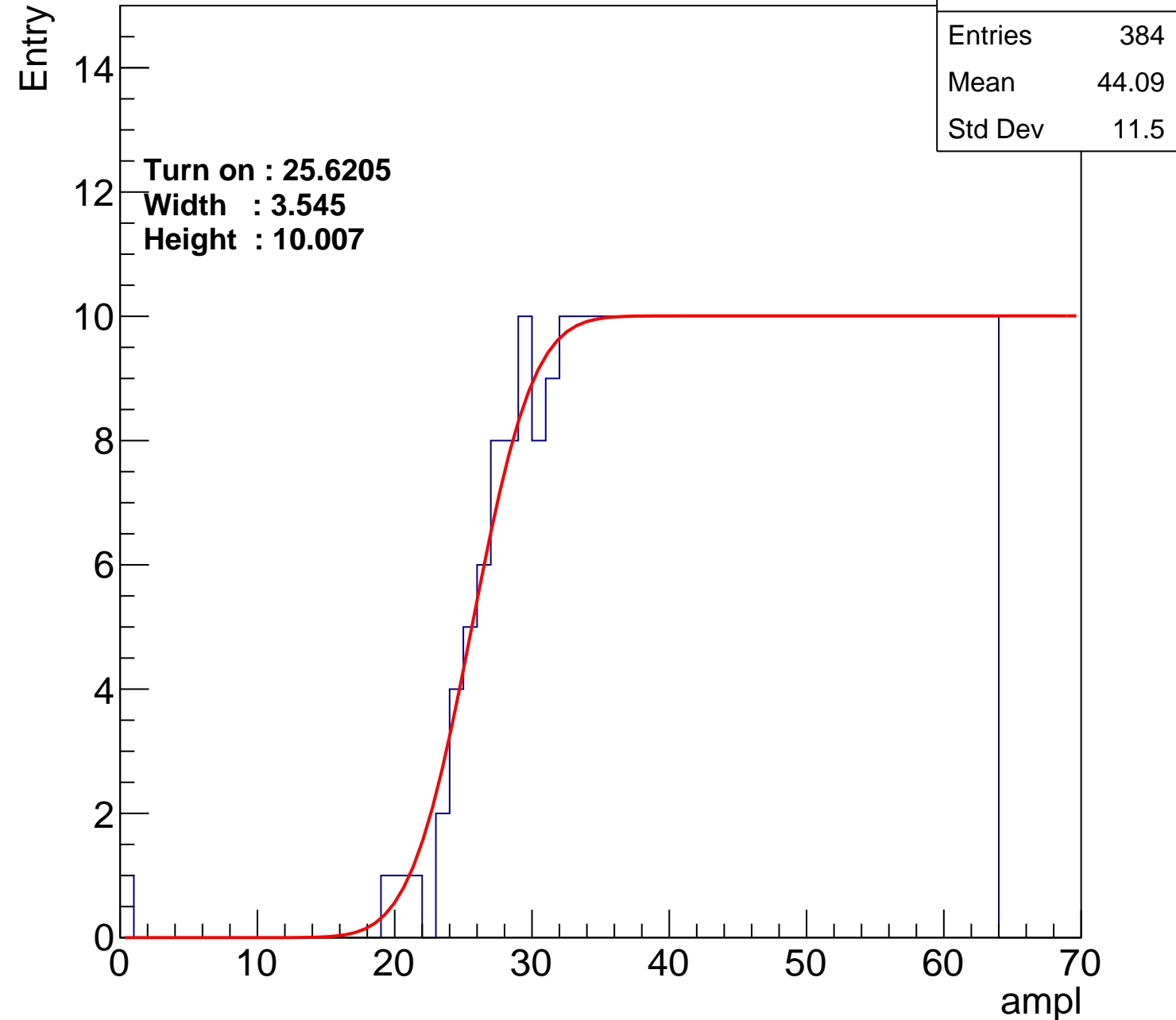
Width : 3.545

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch123

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	390
Mean	43.75
Std Dev	11.8

**Turn on : 25.1604**

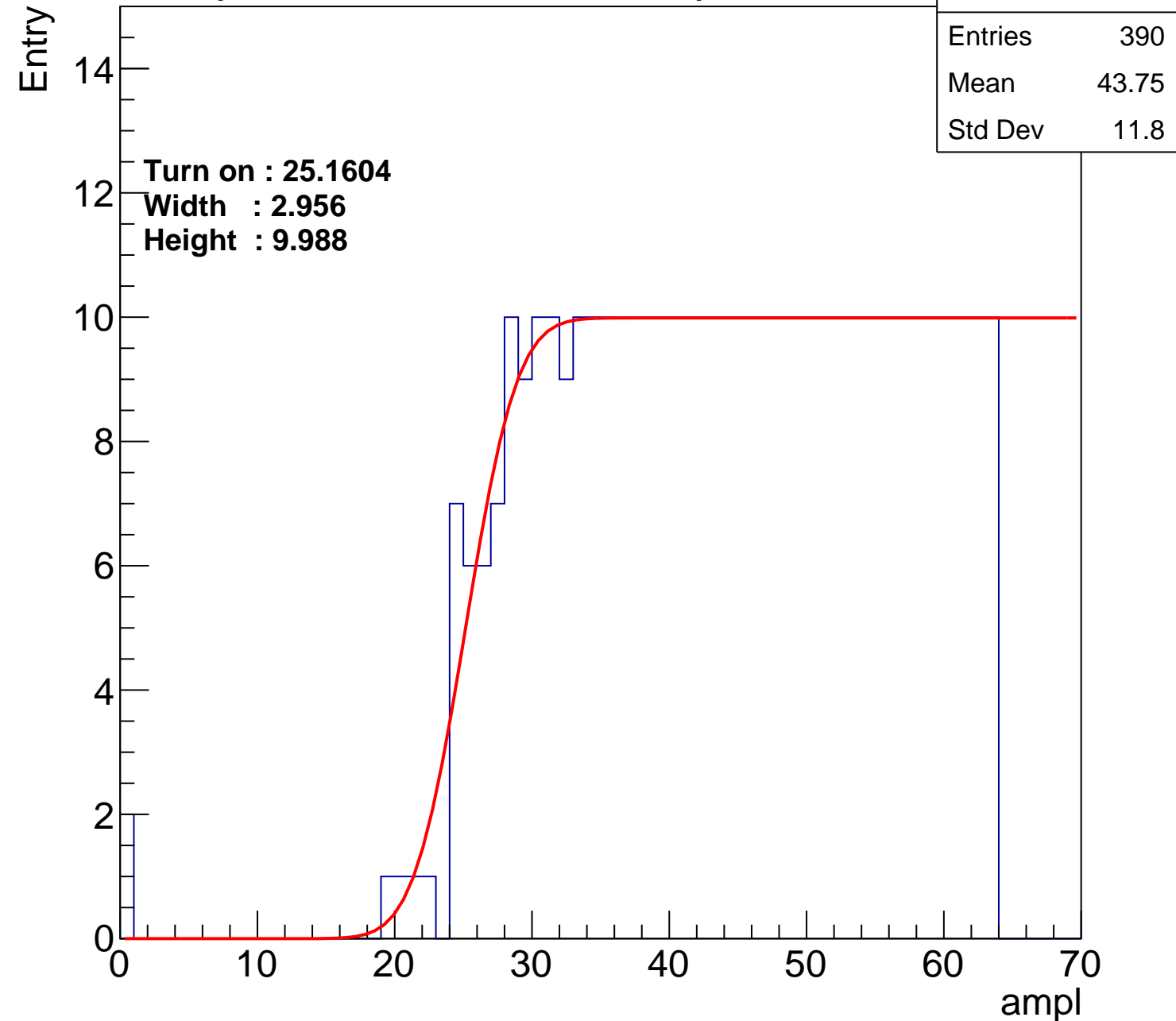
**Width : 2.956**

**Height : 9.988**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch124

calib\_packv5\_042523\_0143.root, FC#4, port A2

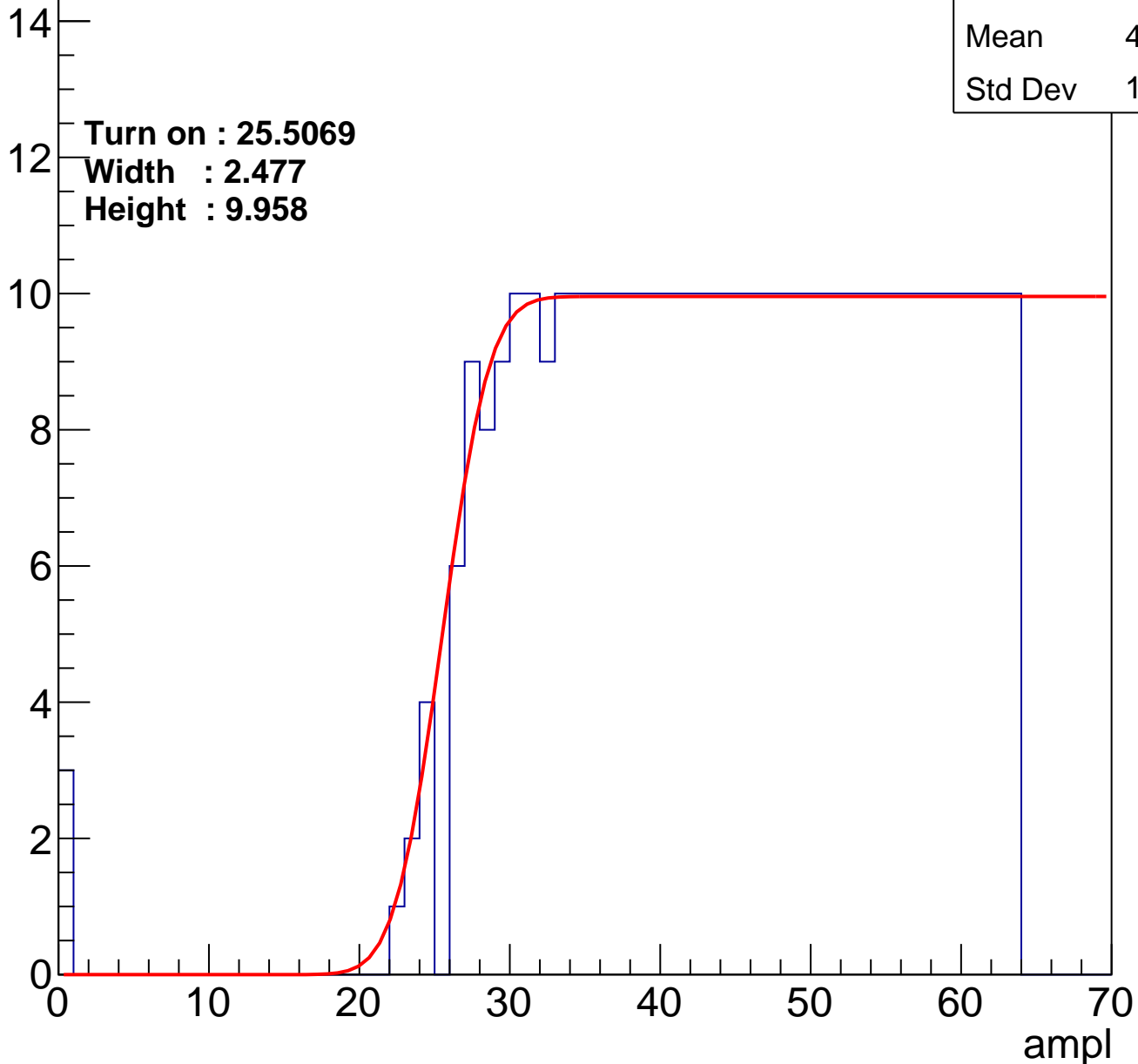
Entries	381
Mean	44.16
Std Dev	11.69

**Turn on : 25.5069**

**Width : 2.477**

**Height : 9.958**

Entry



# B1L100S, U15-ch125

calib\_packv5\_042523\_0143.root, FC#4, port A2

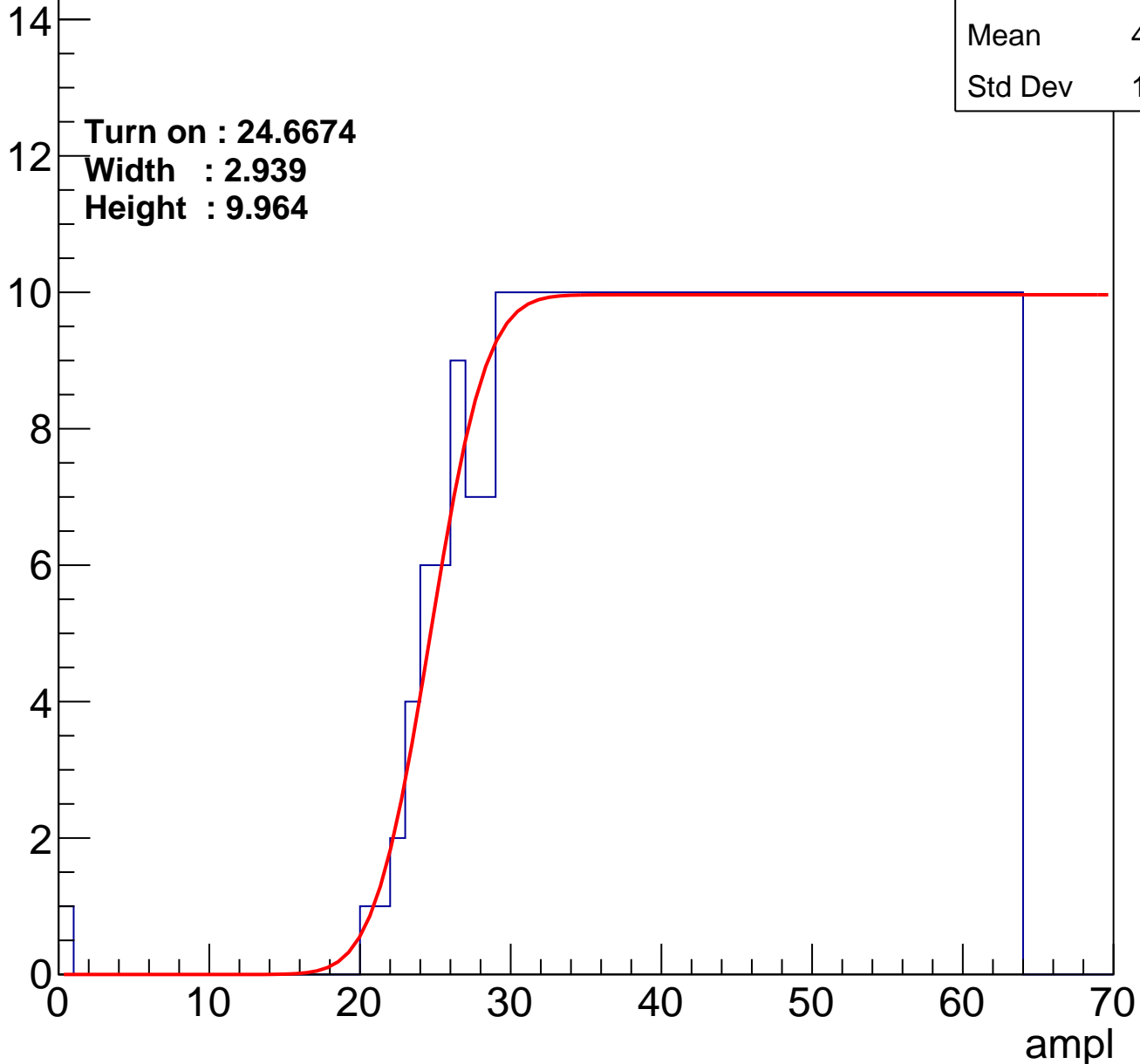
Entries	394
Mean	43.63
Std Dev	11.72

Turn on : 24.6674

Width : 2.939

Height : 9.964

Entry



# B1L100S, U15-ch126

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	398
Mean	43.21
Std Dev	12.36

**Turn on : 24.6906**

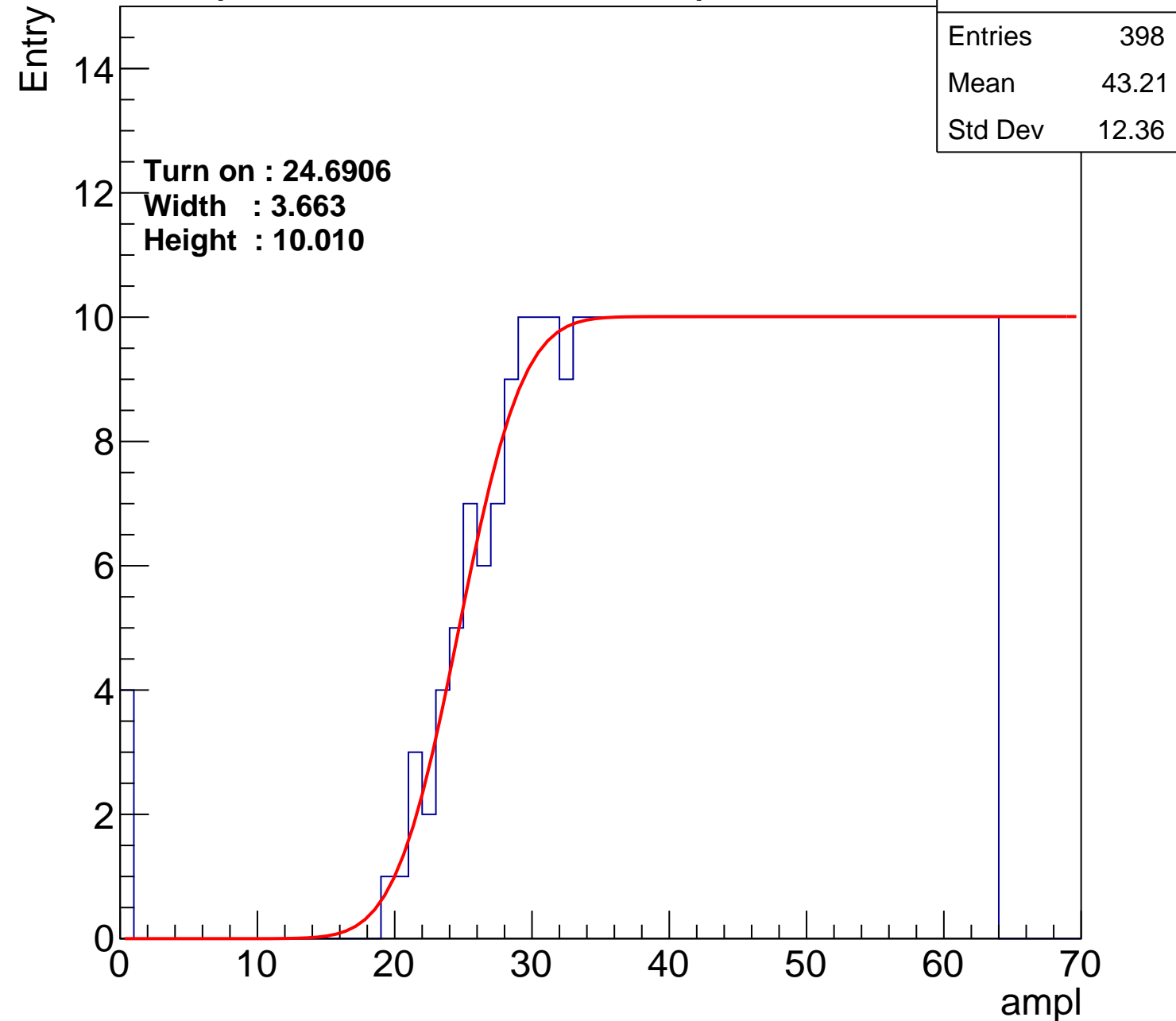
**Width : 3.663**

**Height : 10.010**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U15-ch127

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.8
Std Dev	11.24

**Turn on : 27.3692**

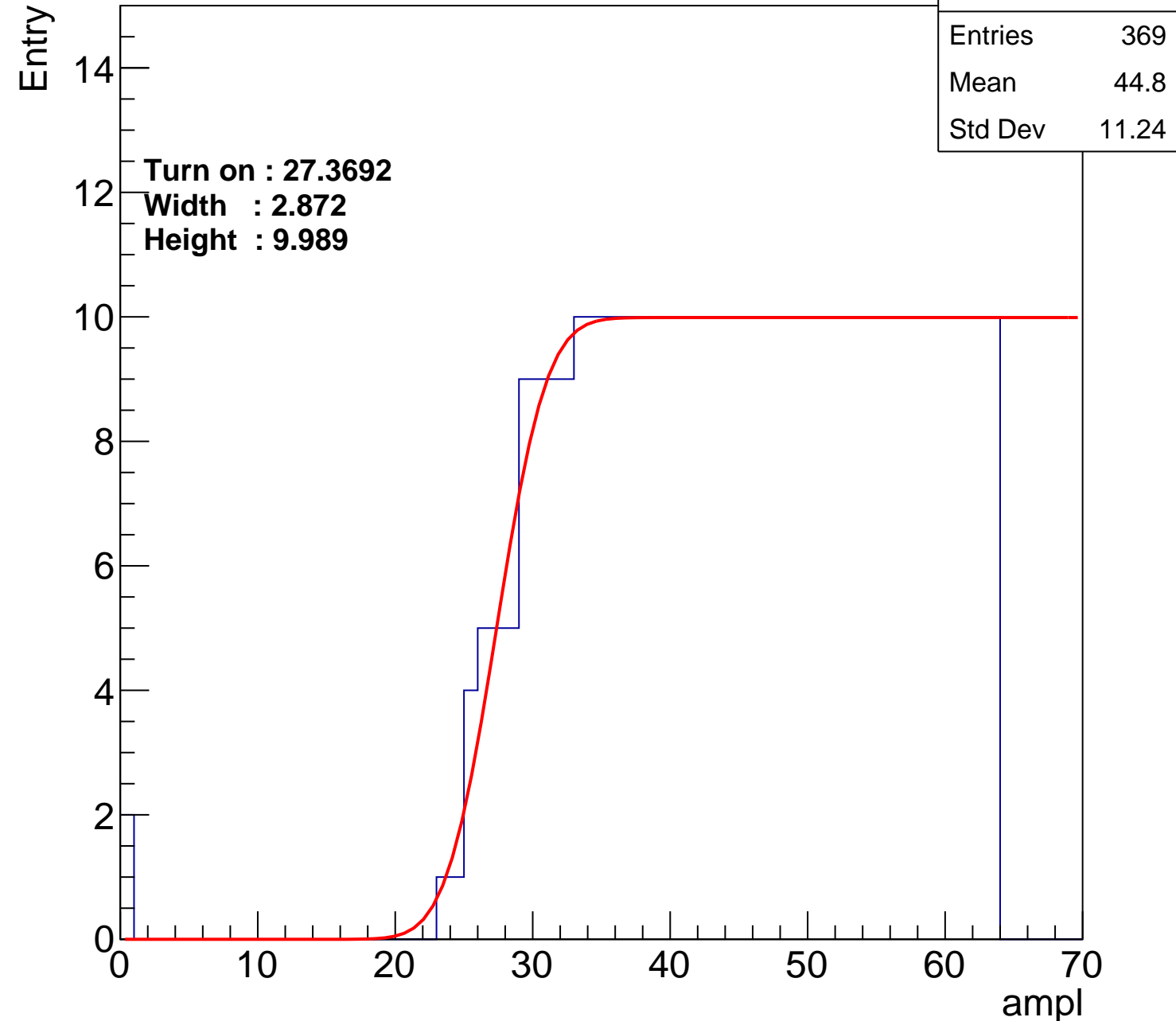
**Width : 2.872**

**Height : 9.989**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U15-ch127

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.8
Std Dev	11.24

**Turn on : 27.3692**

**Width : 2.872**

**Height : 9.989**

Entry

