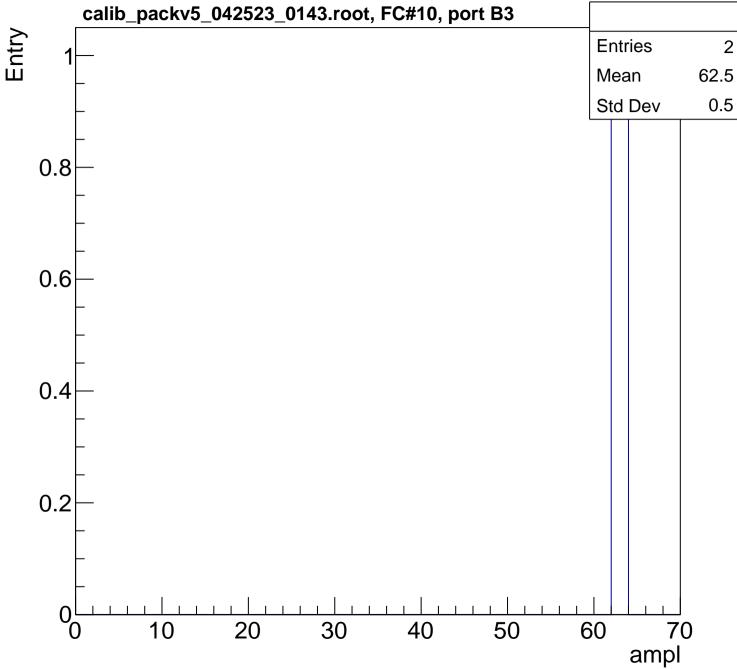
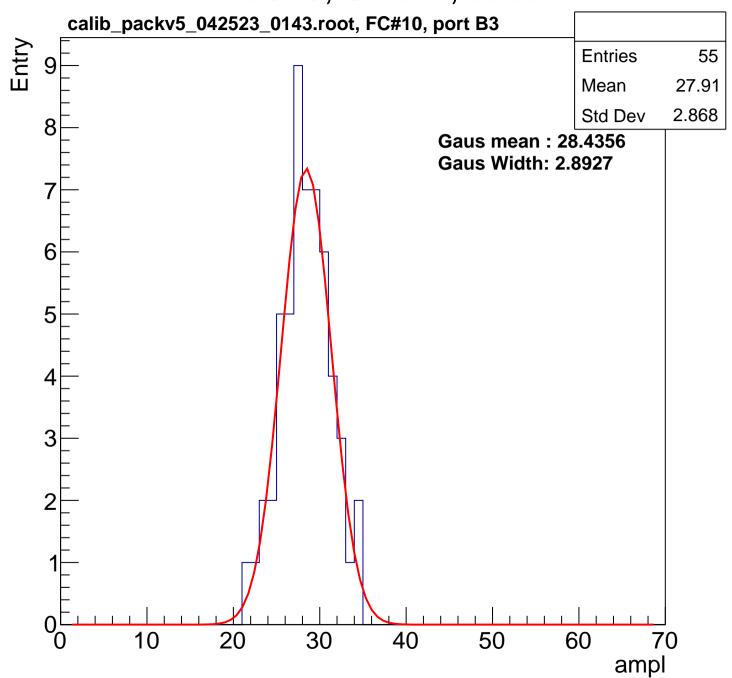
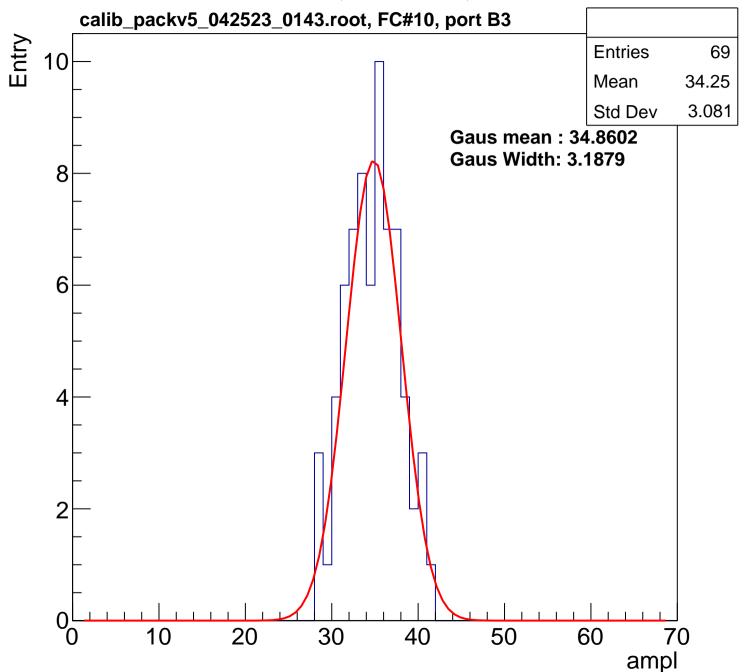
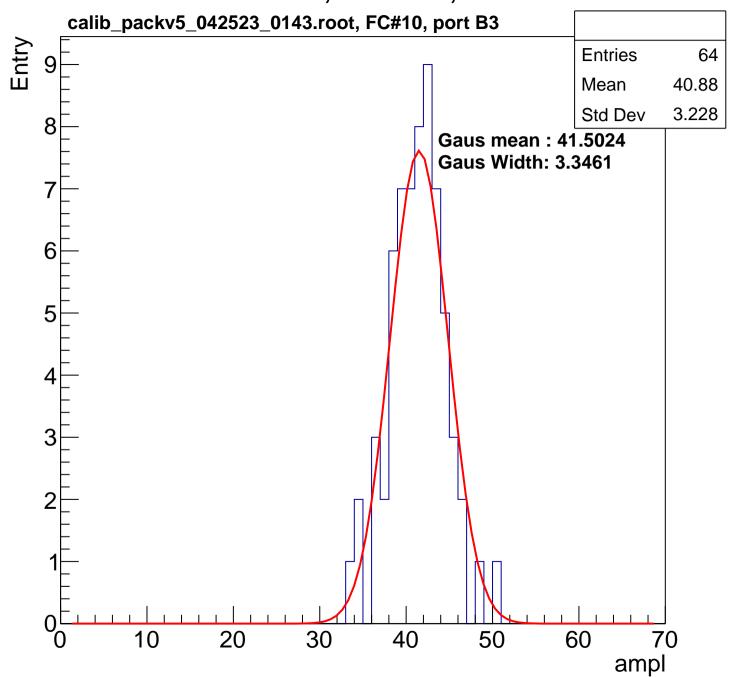


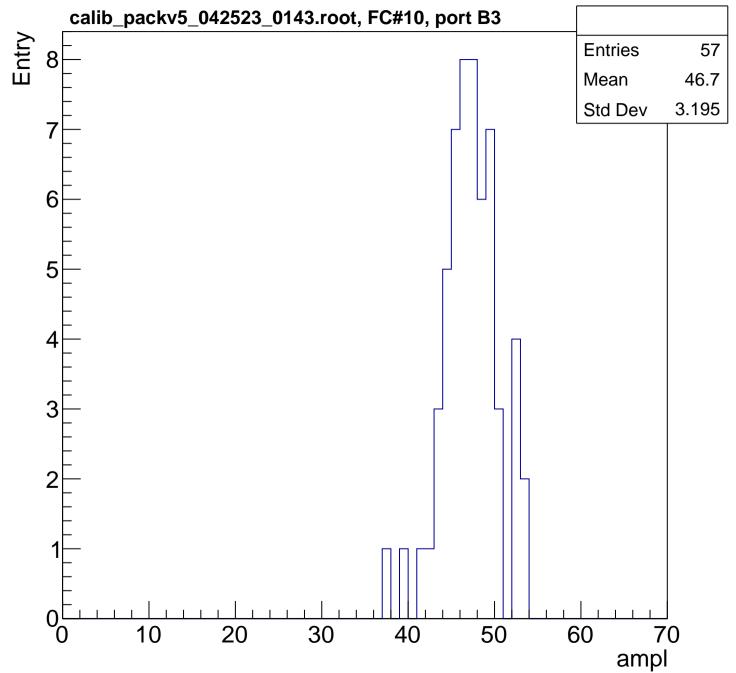
2

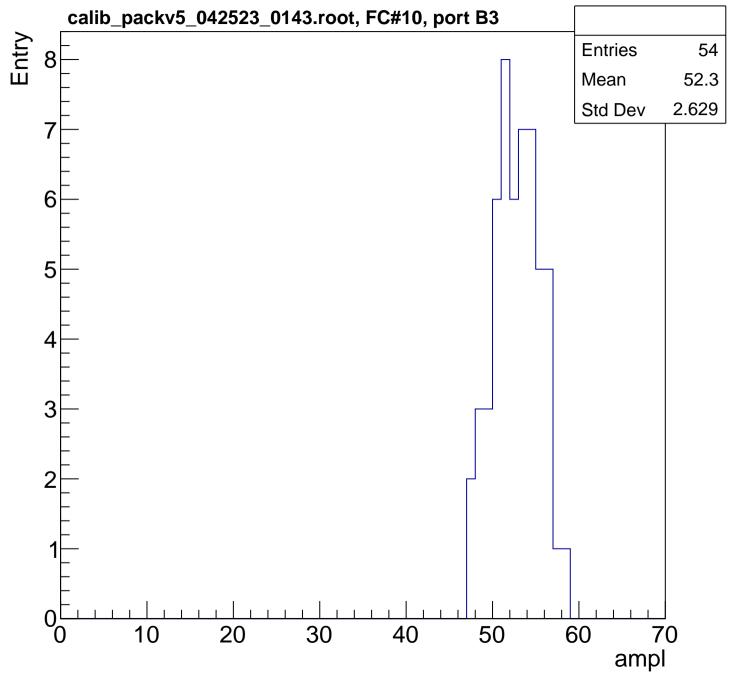


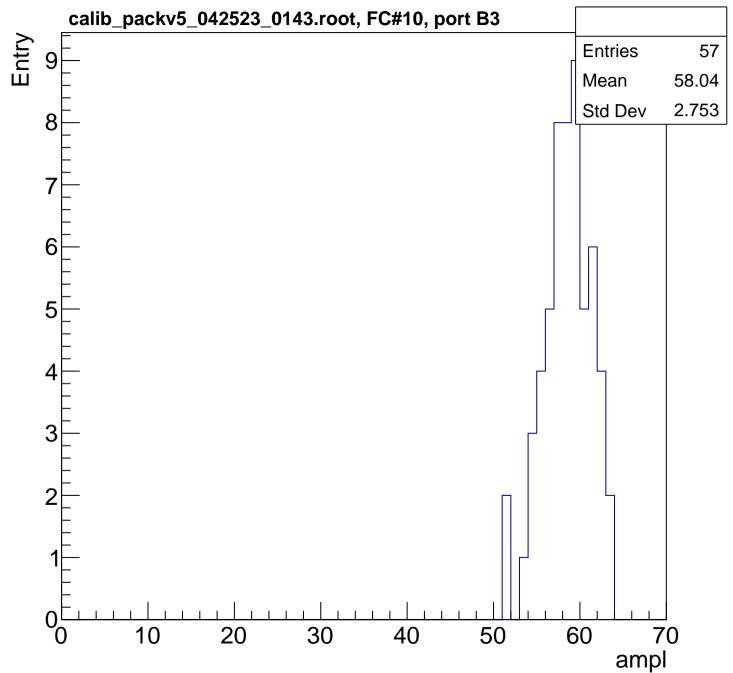


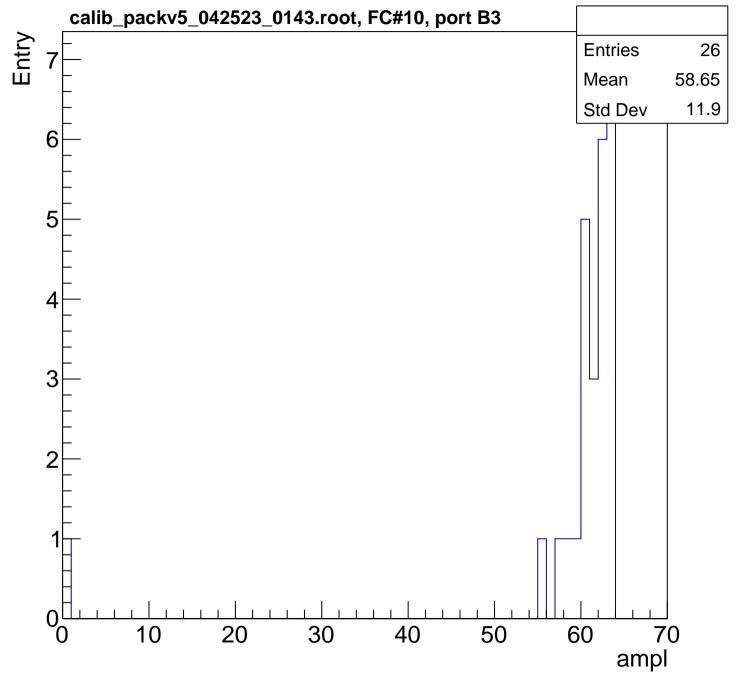


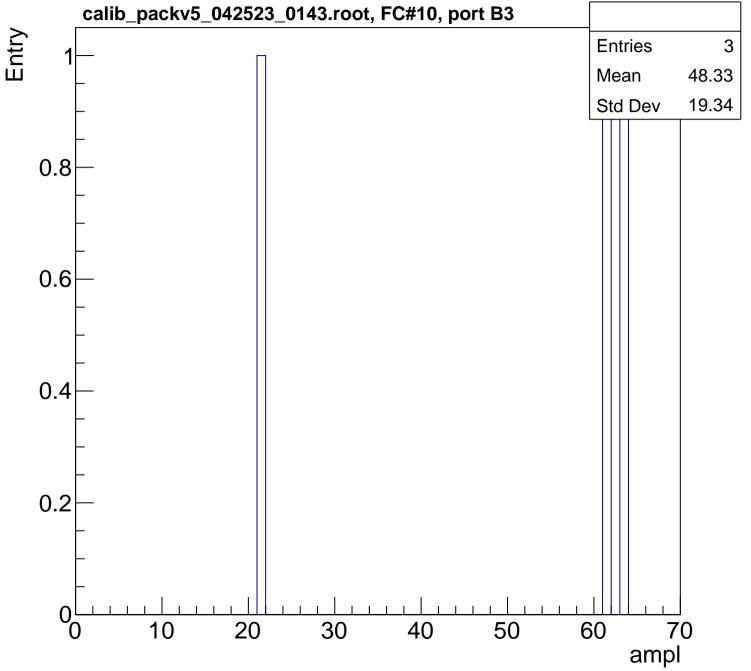


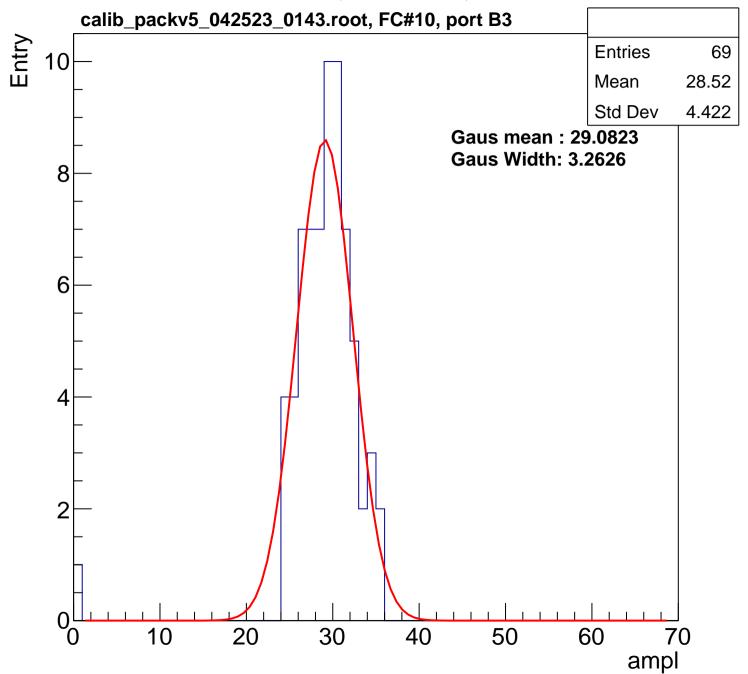


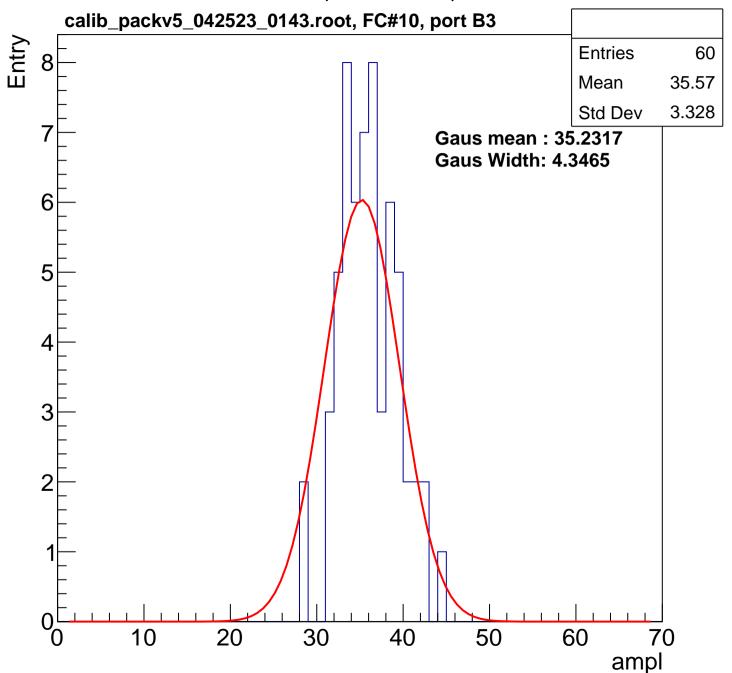


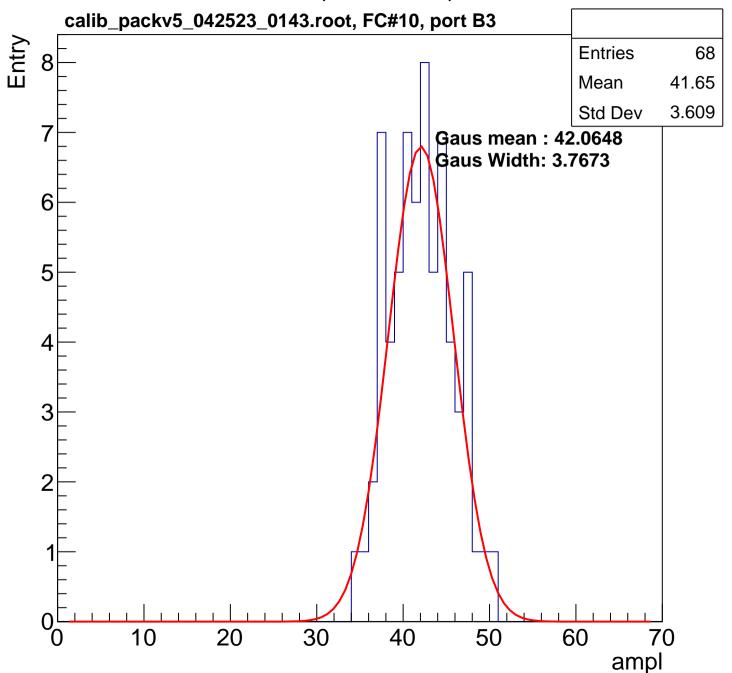


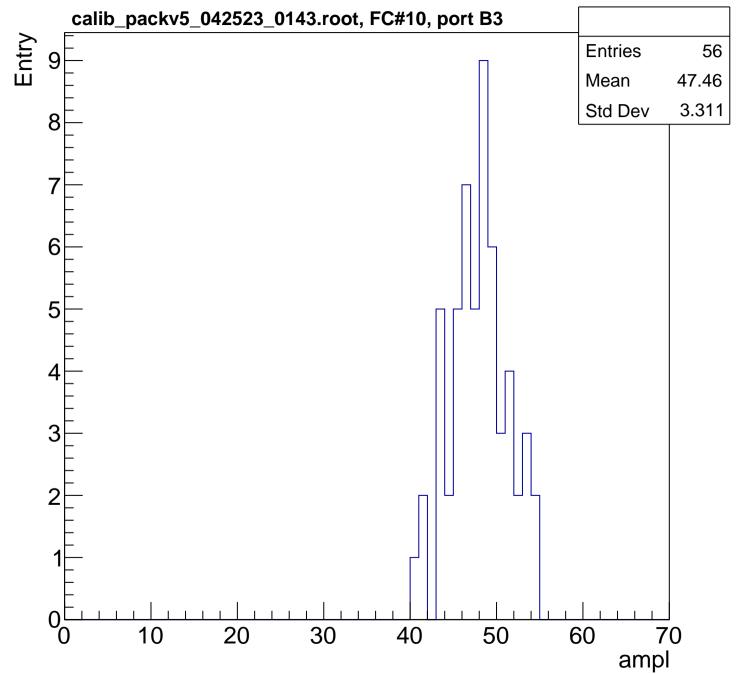


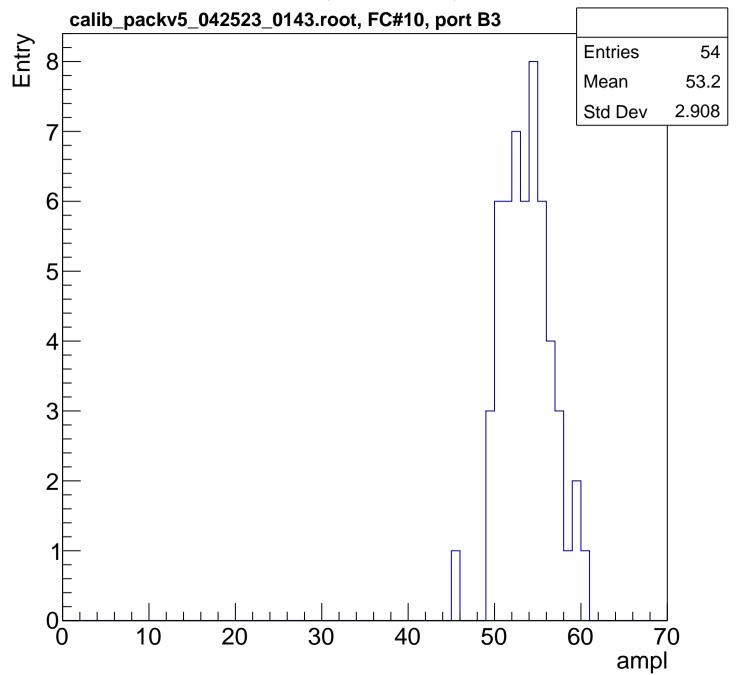


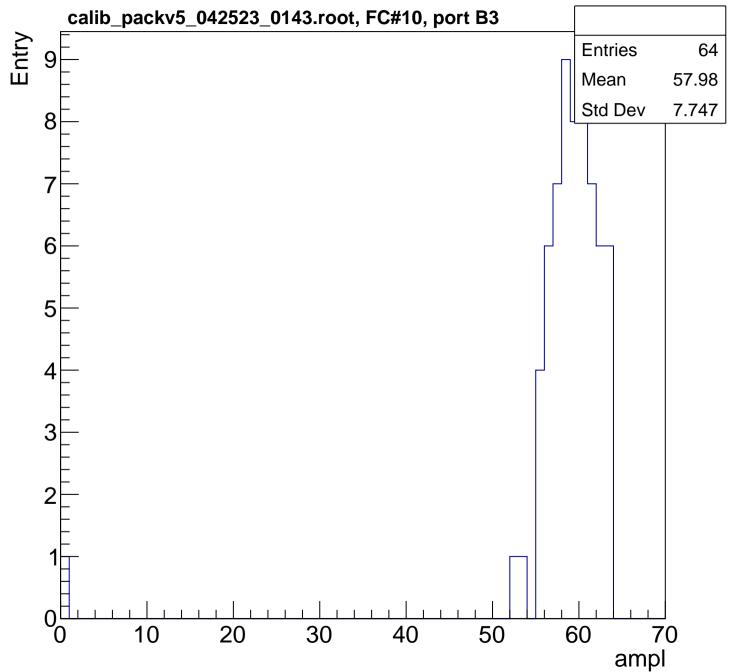


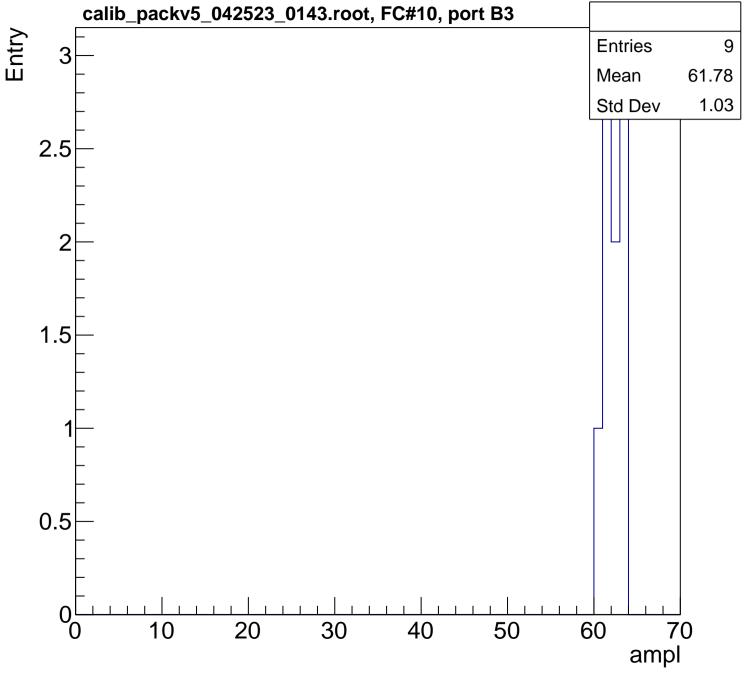


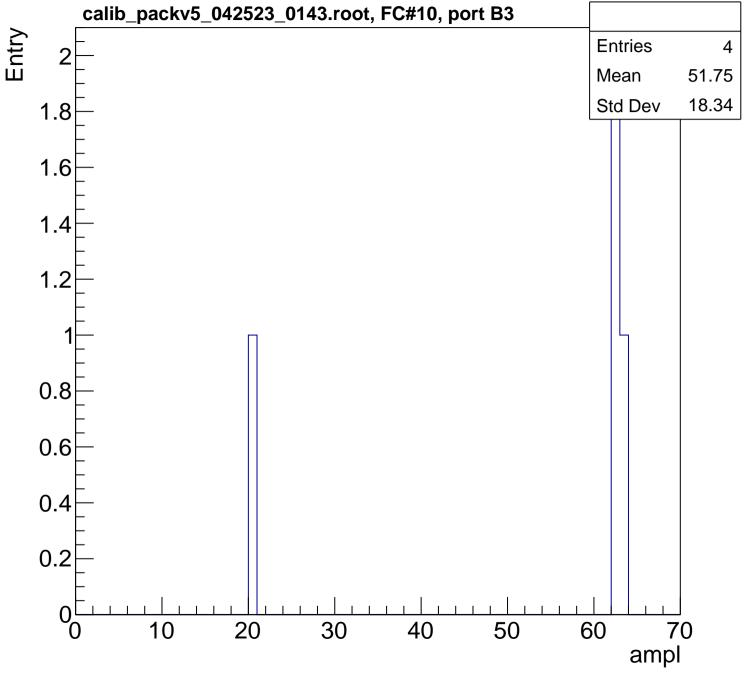


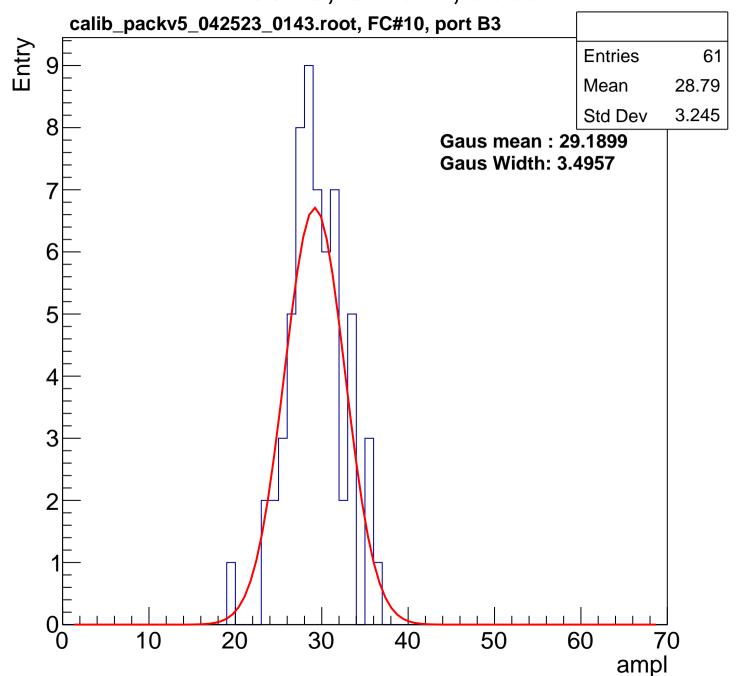


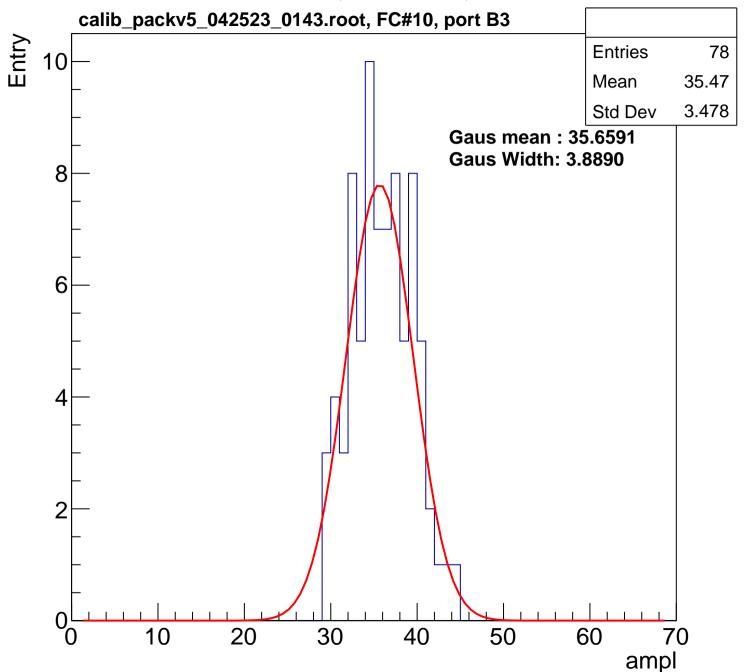


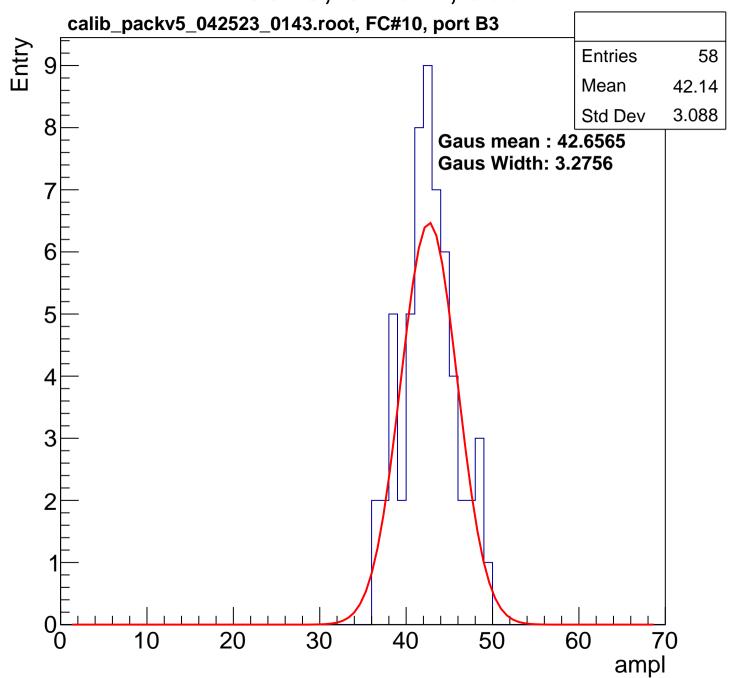


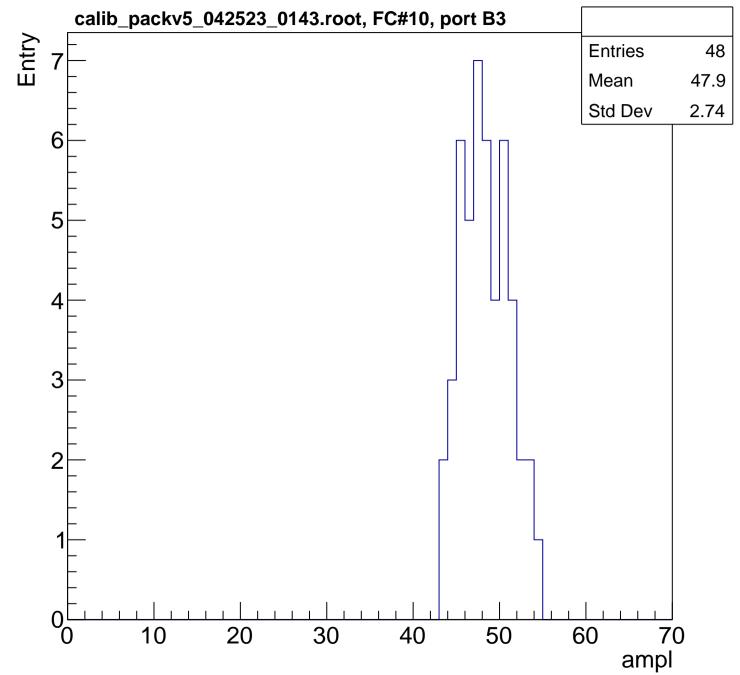


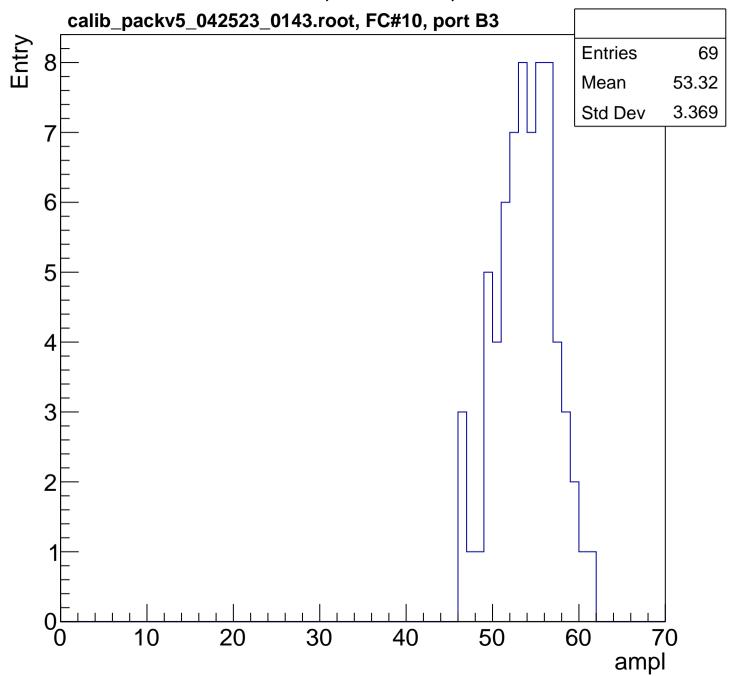


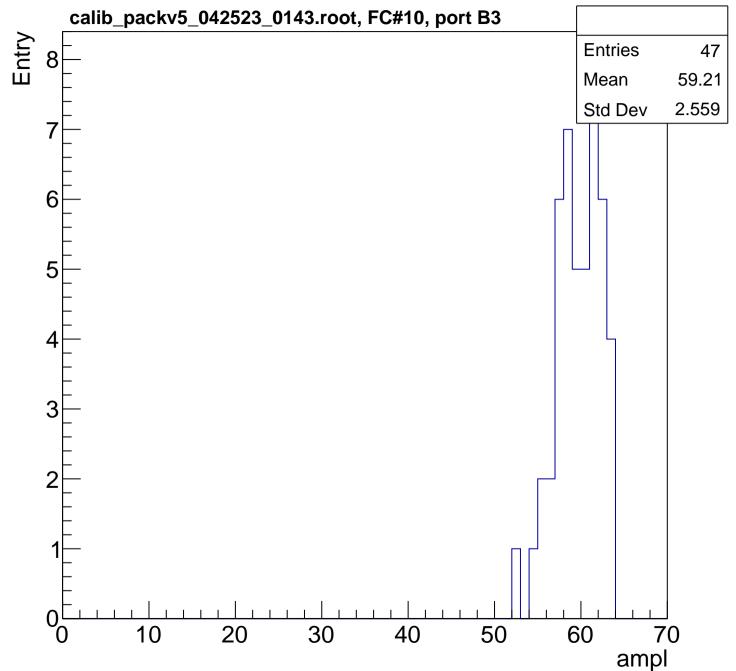


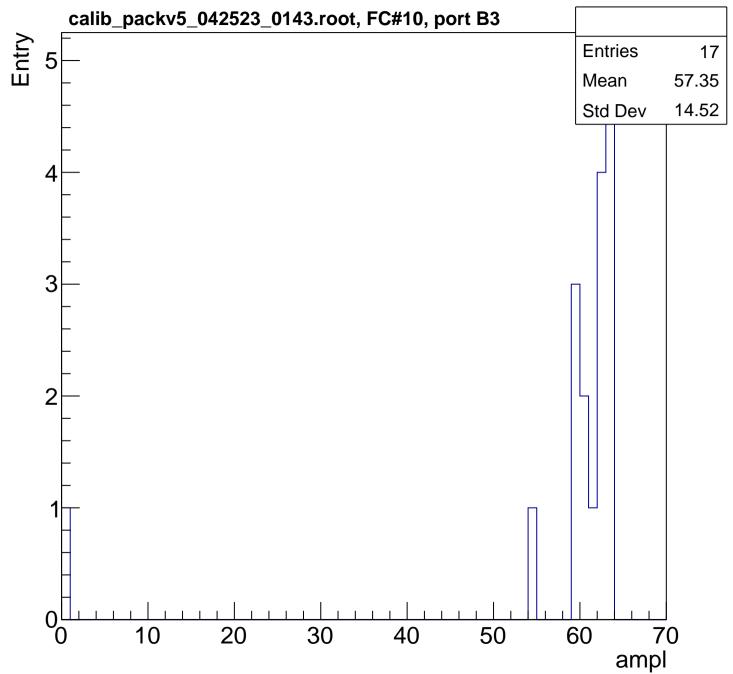


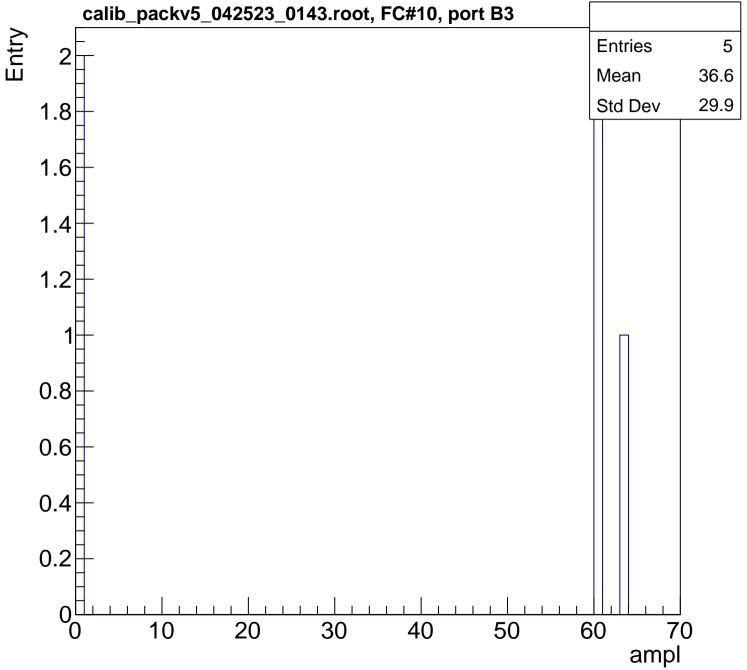


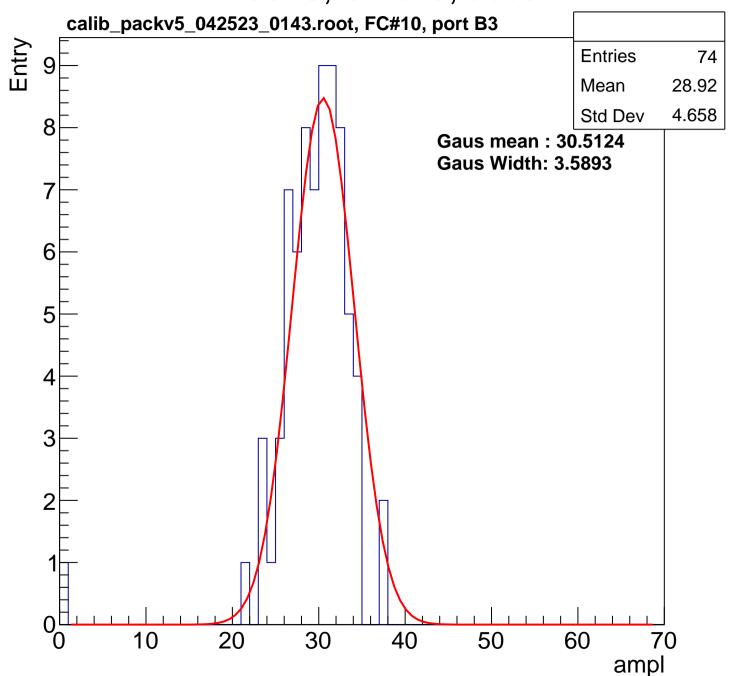


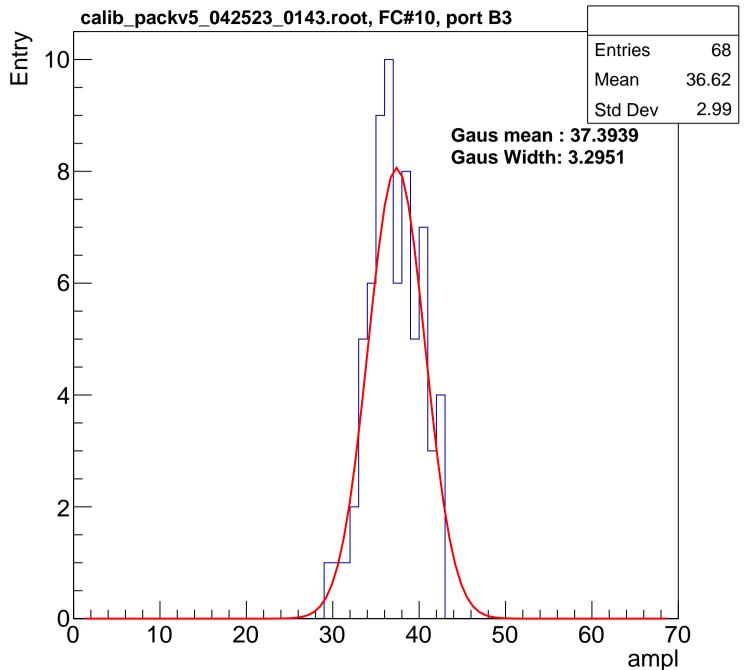


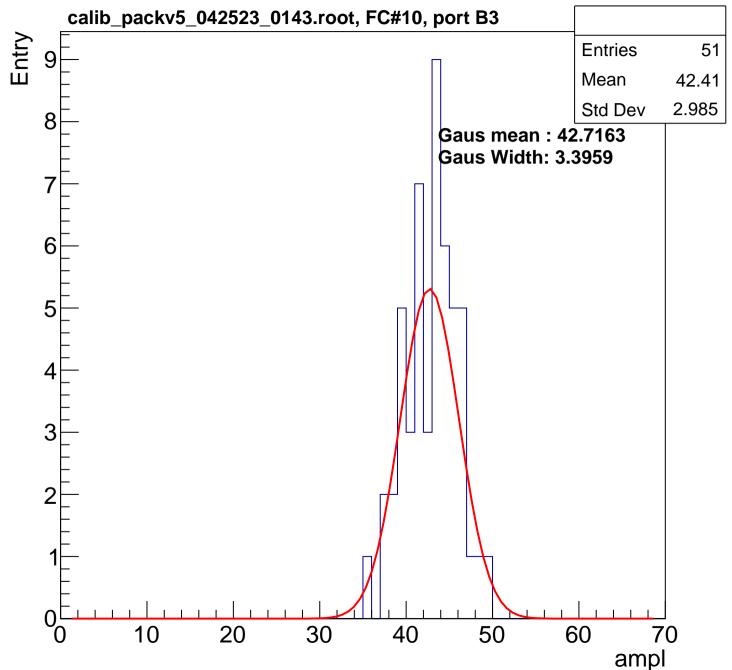


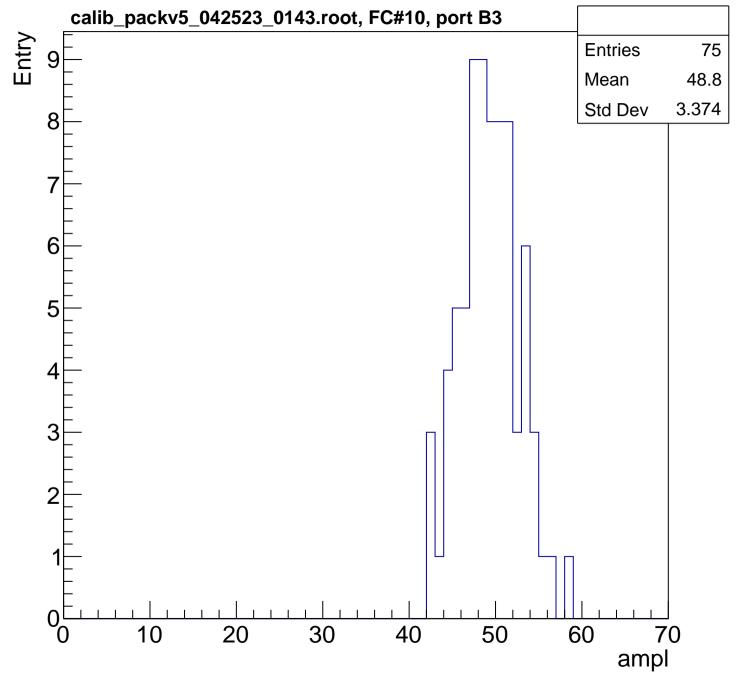


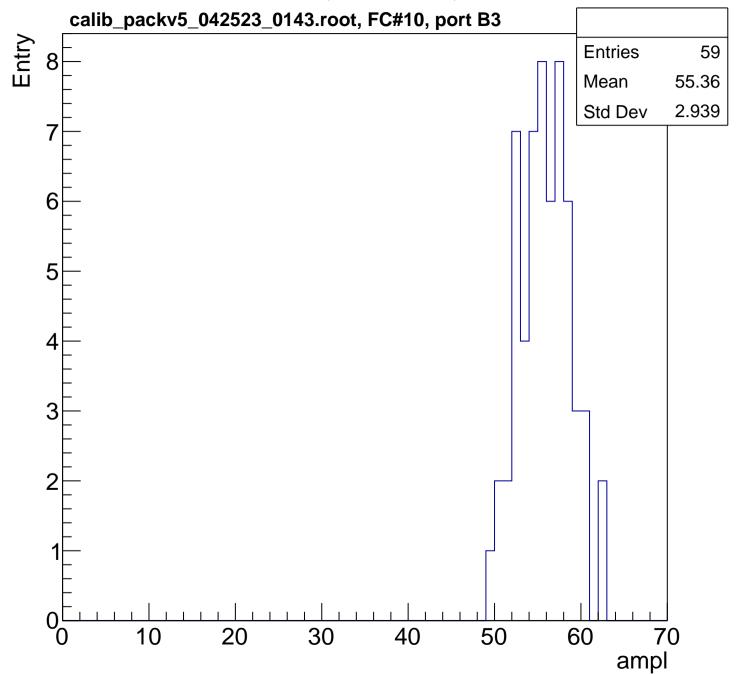


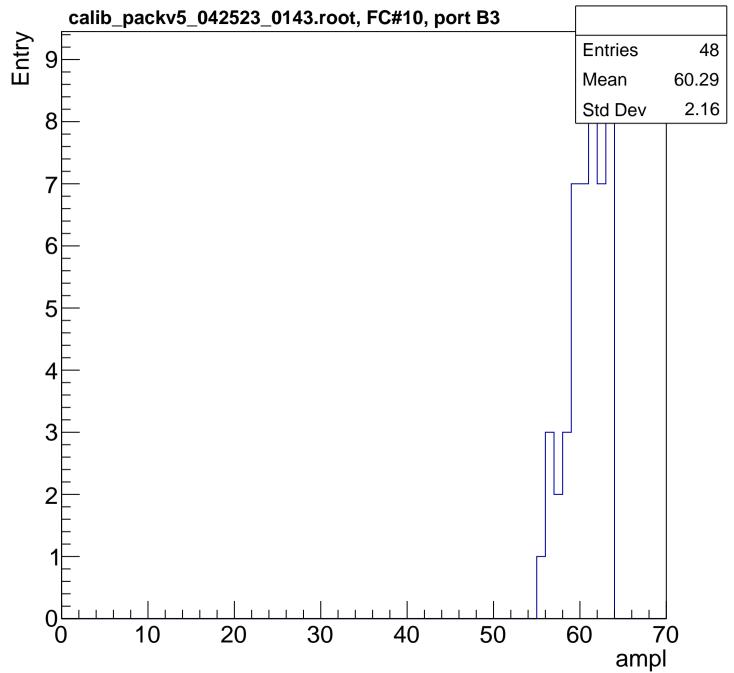


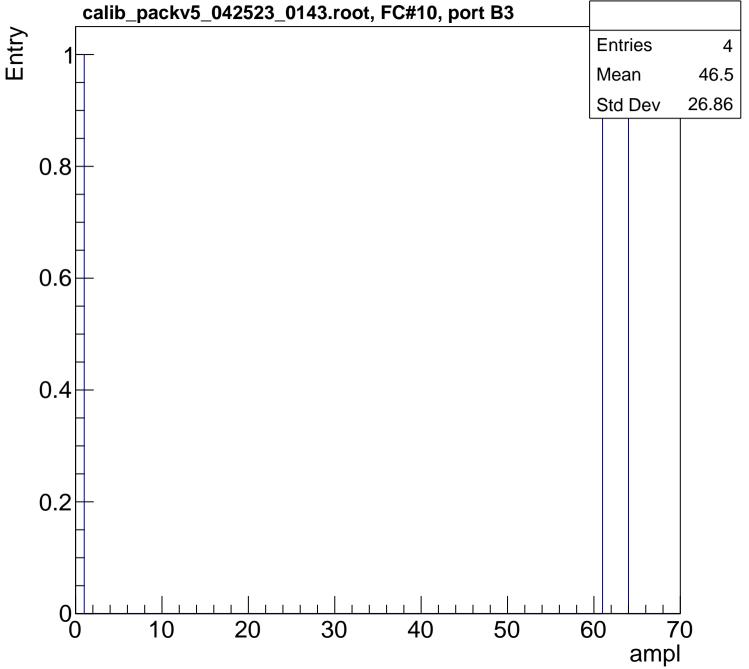




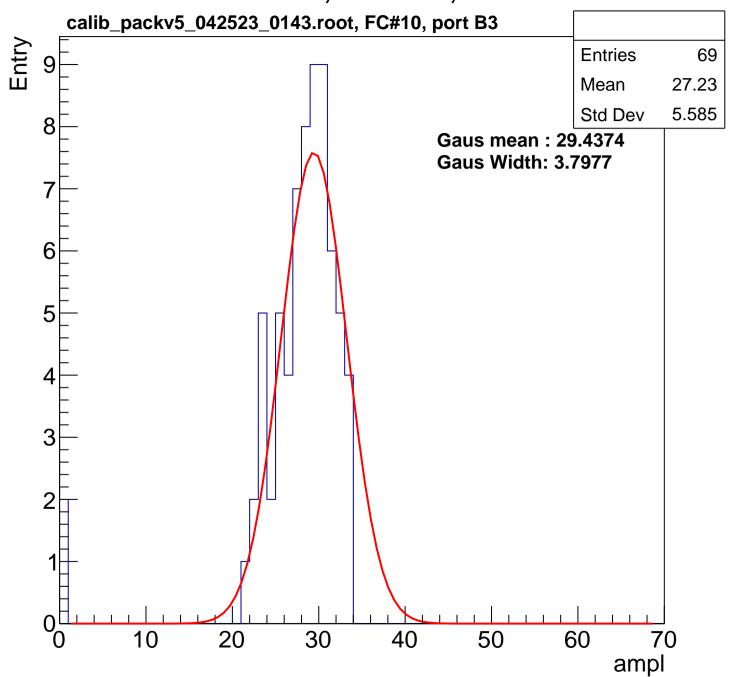


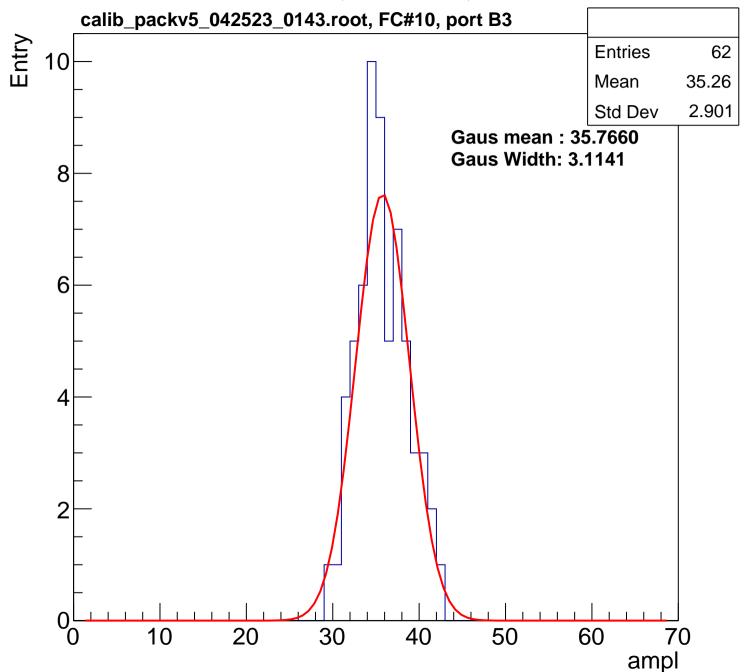


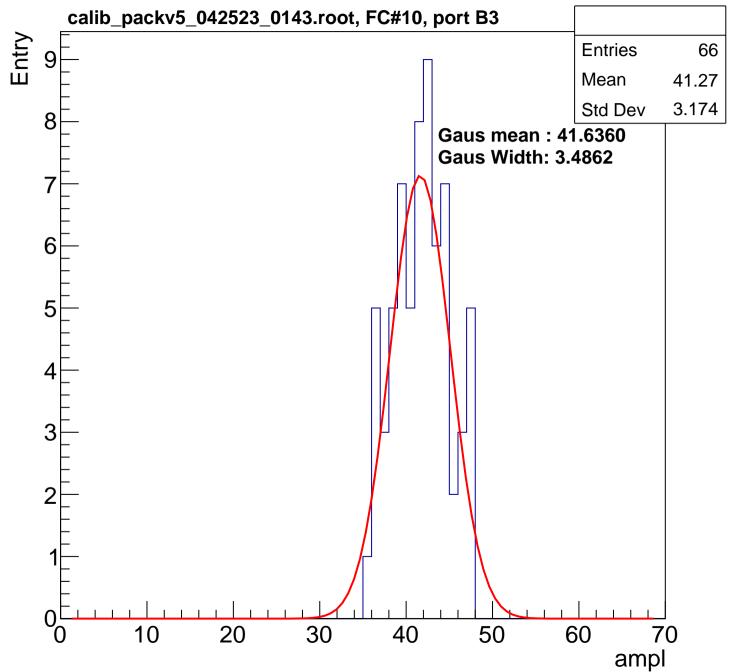


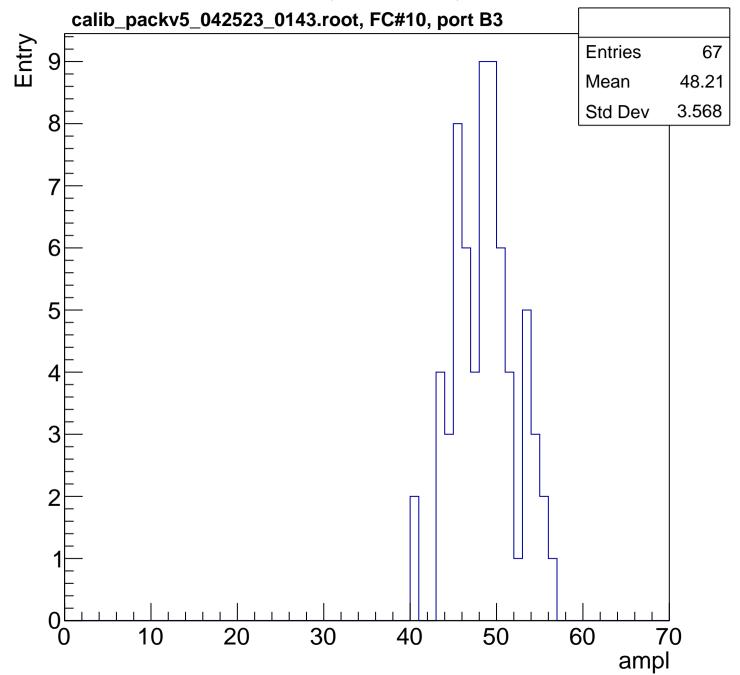


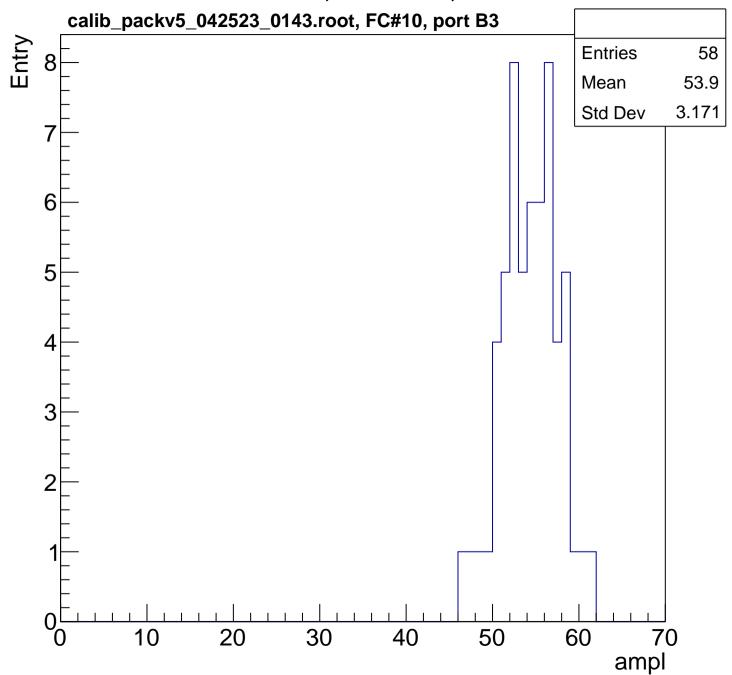
B1L002S, U4-ch5, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

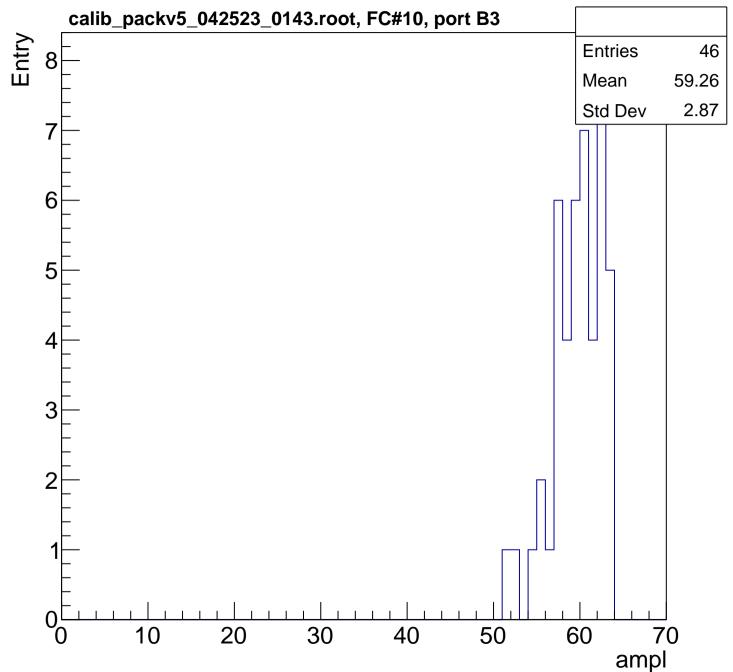


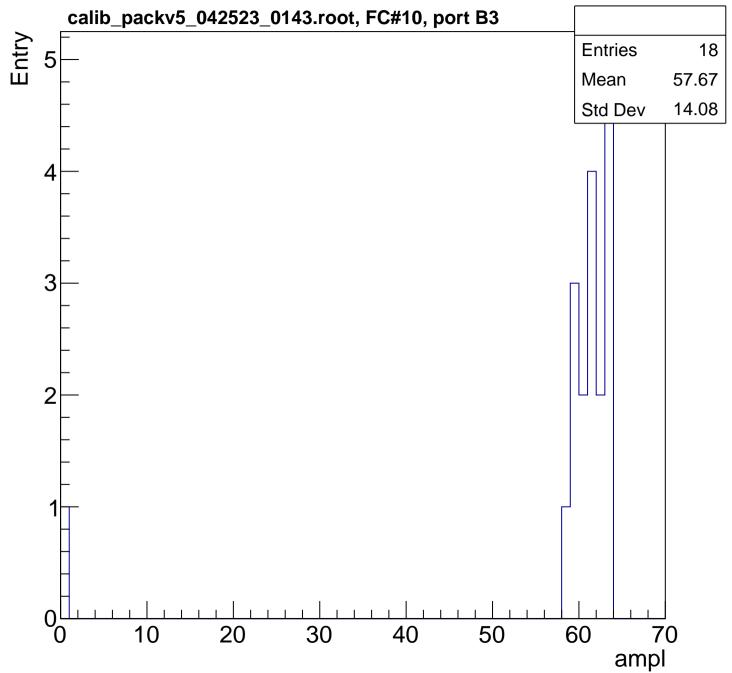


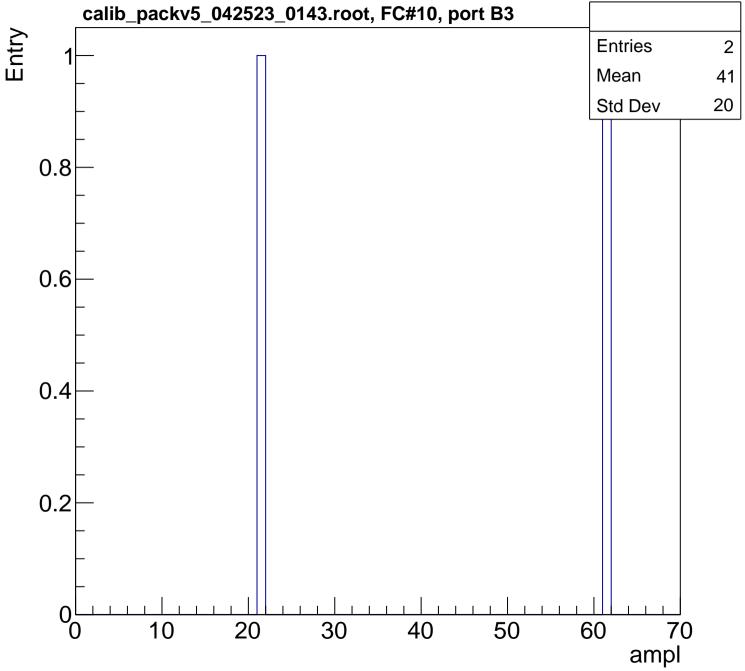


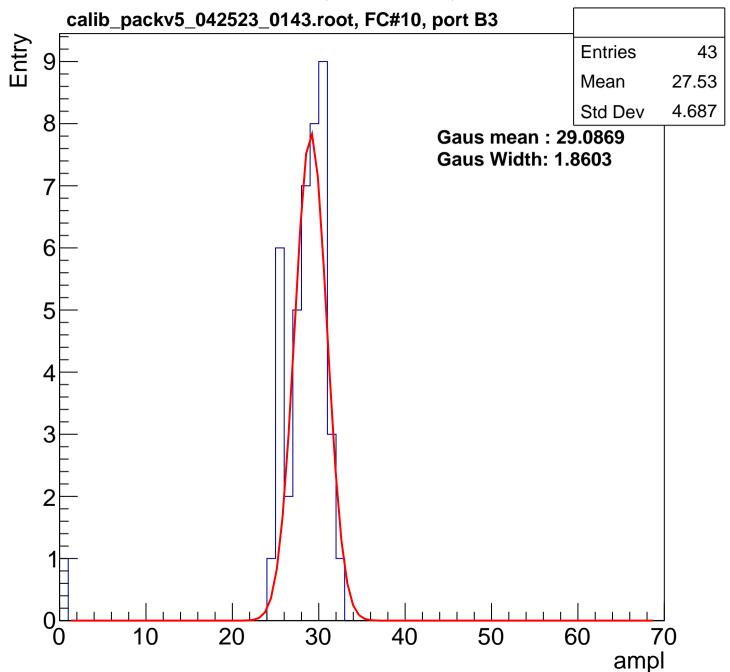


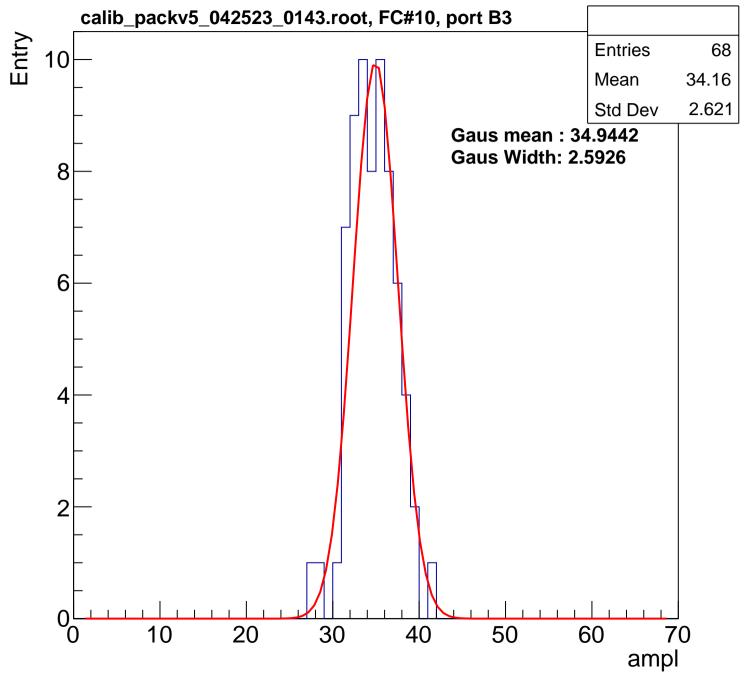


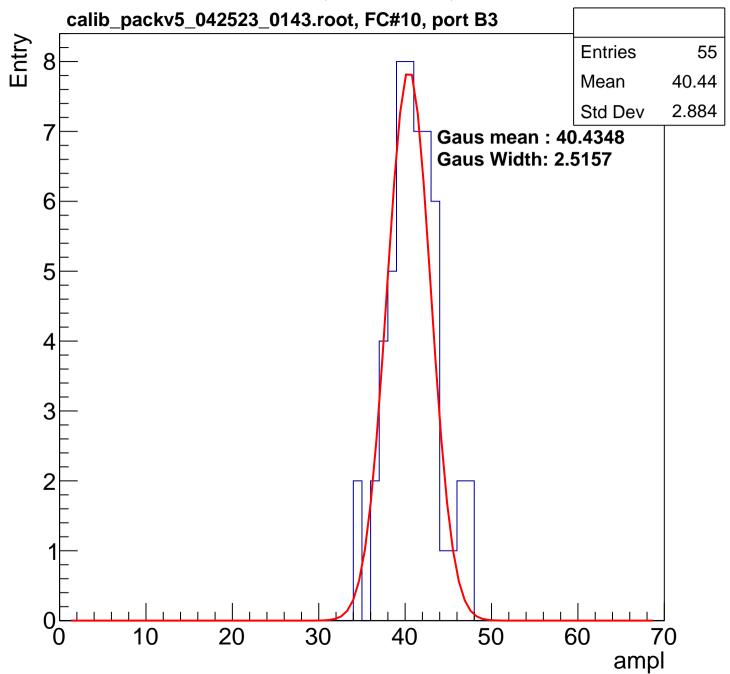


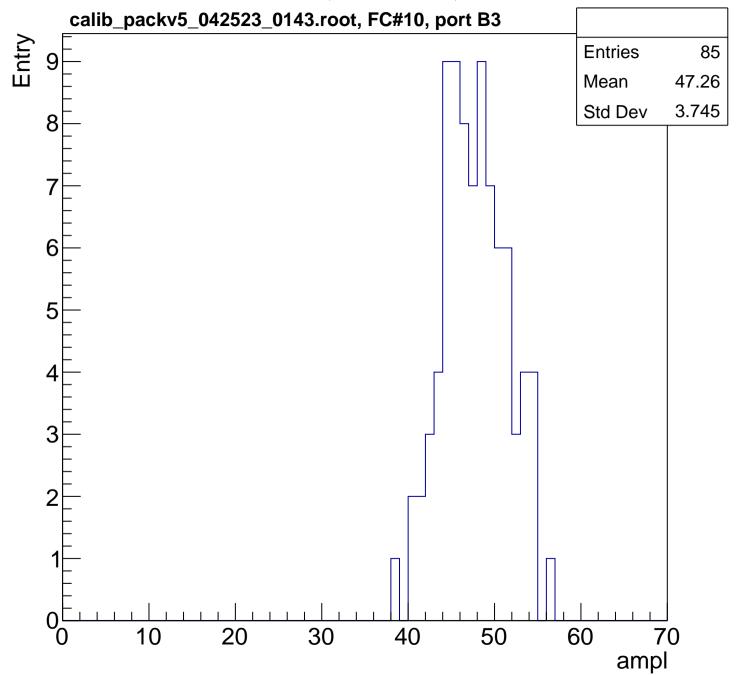


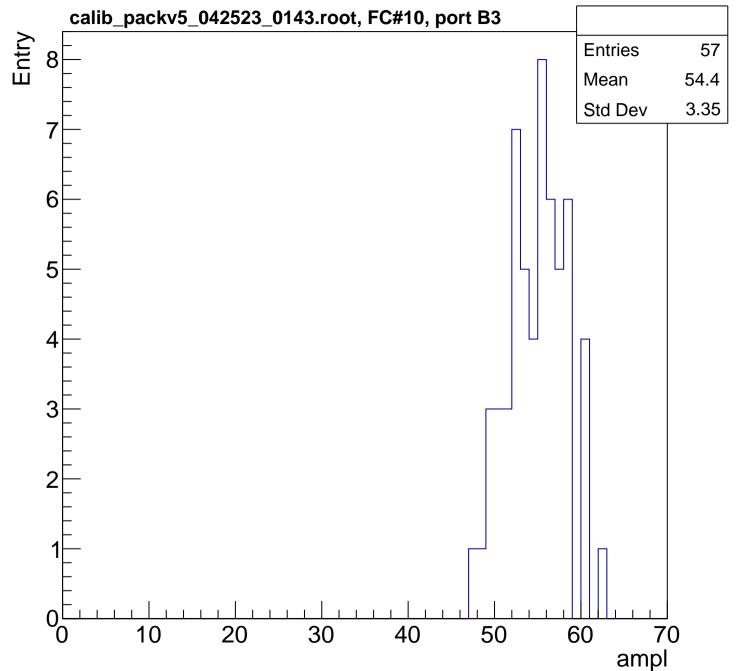


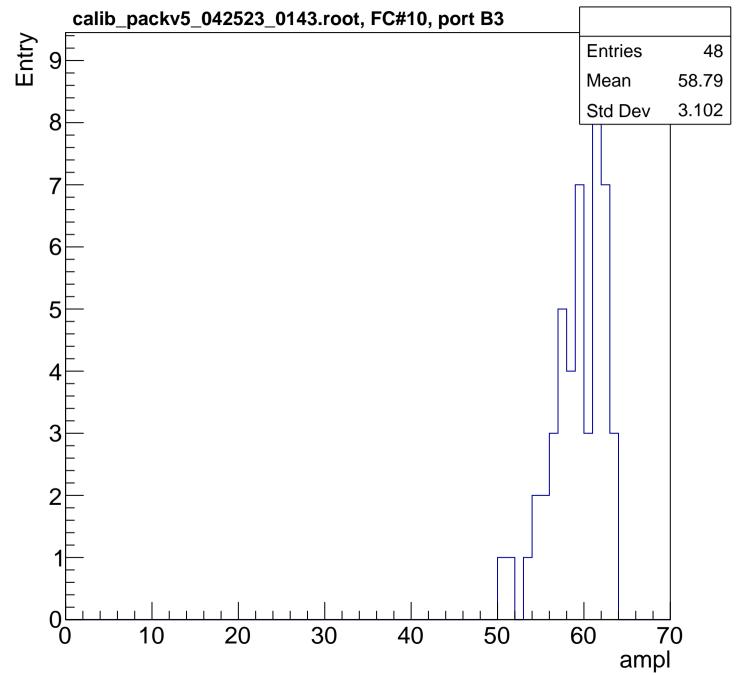


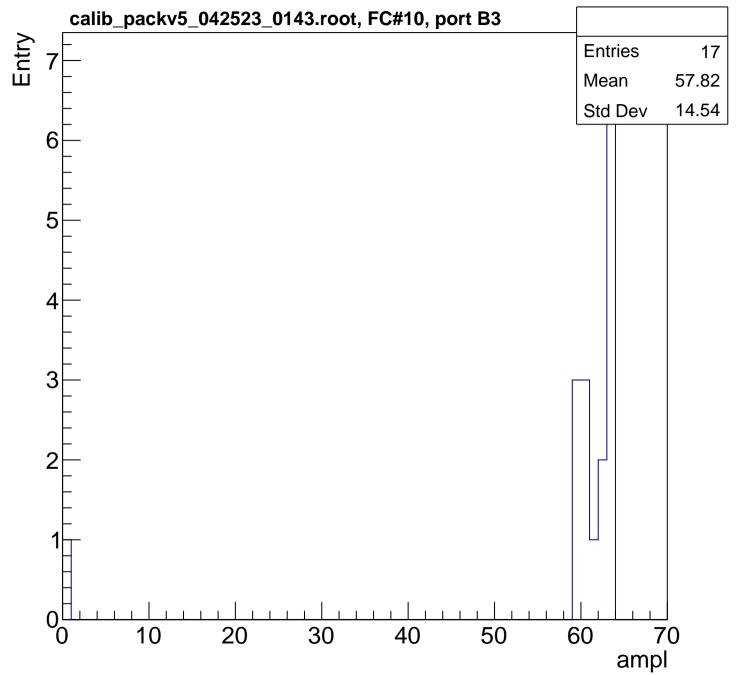




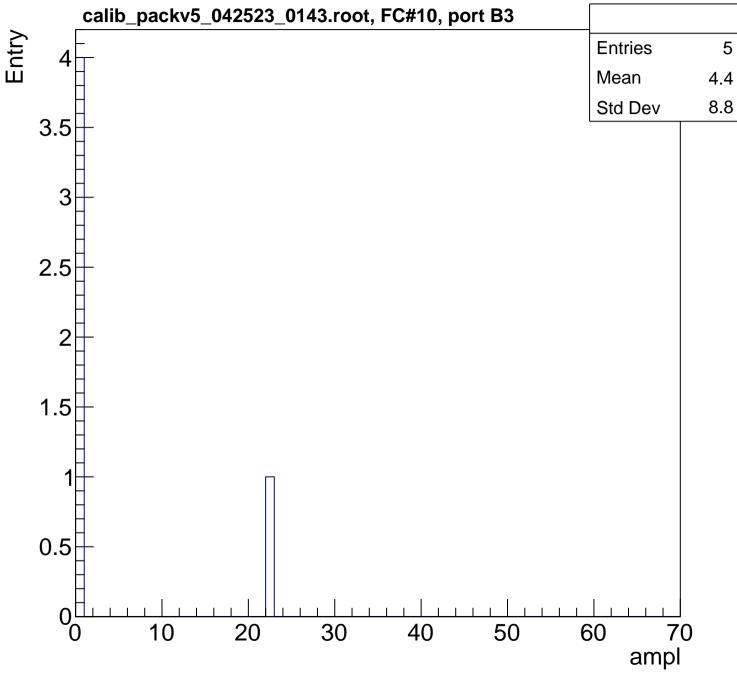


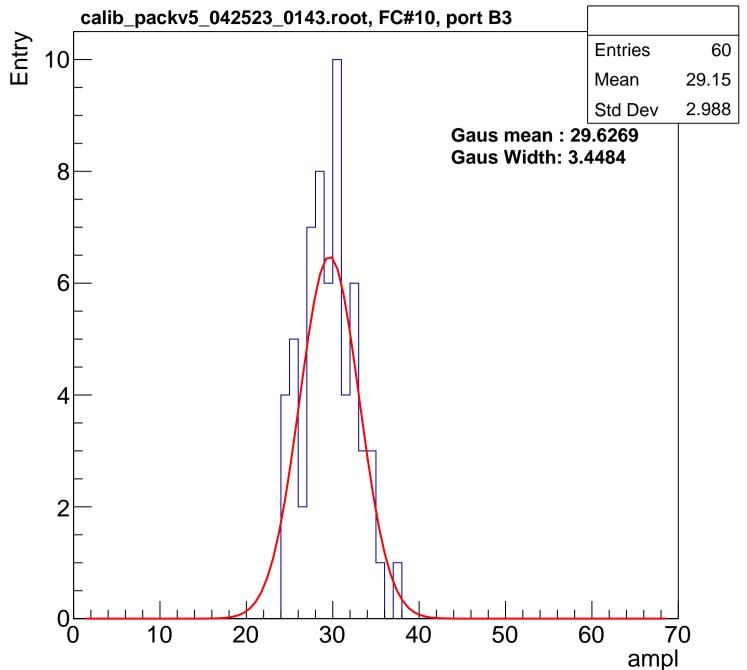


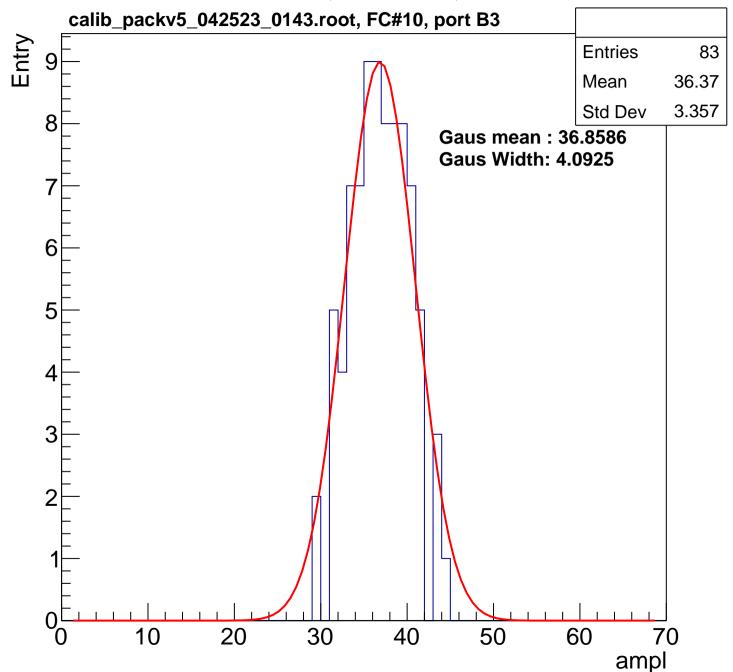


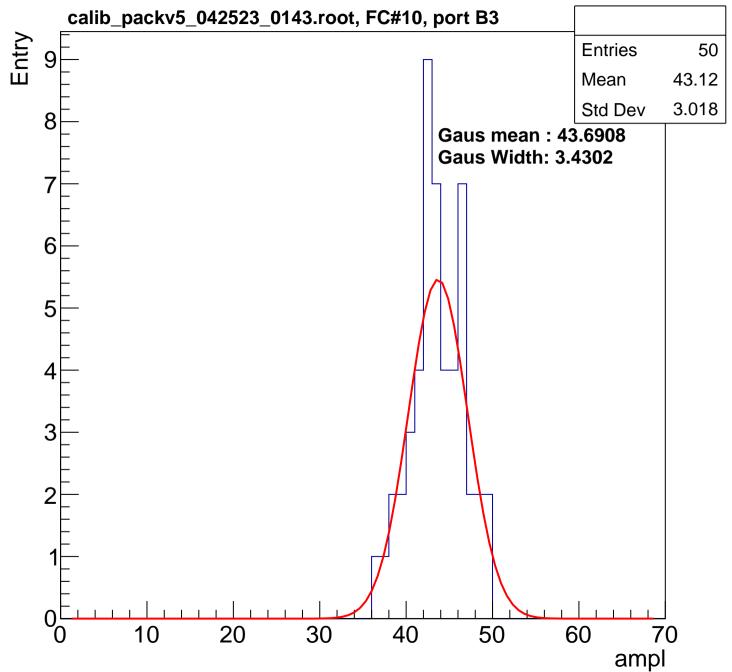


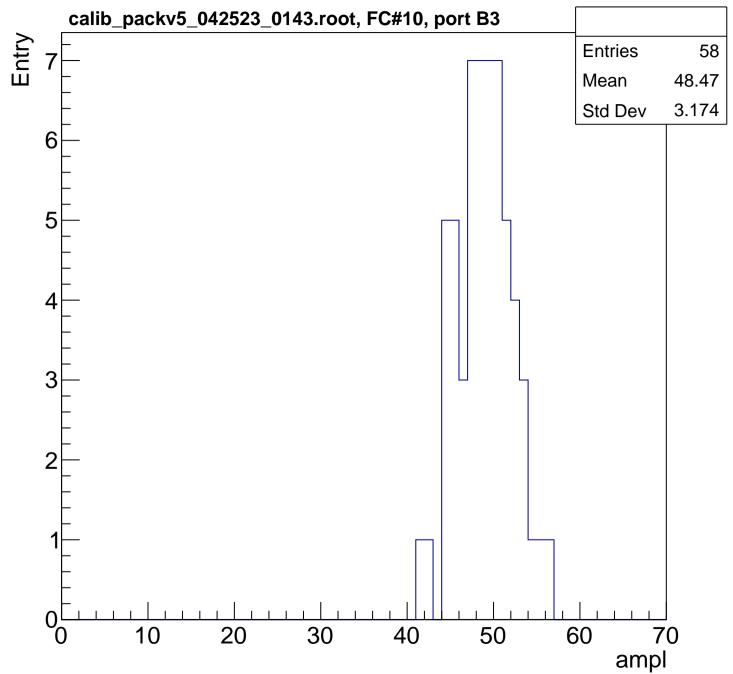
5

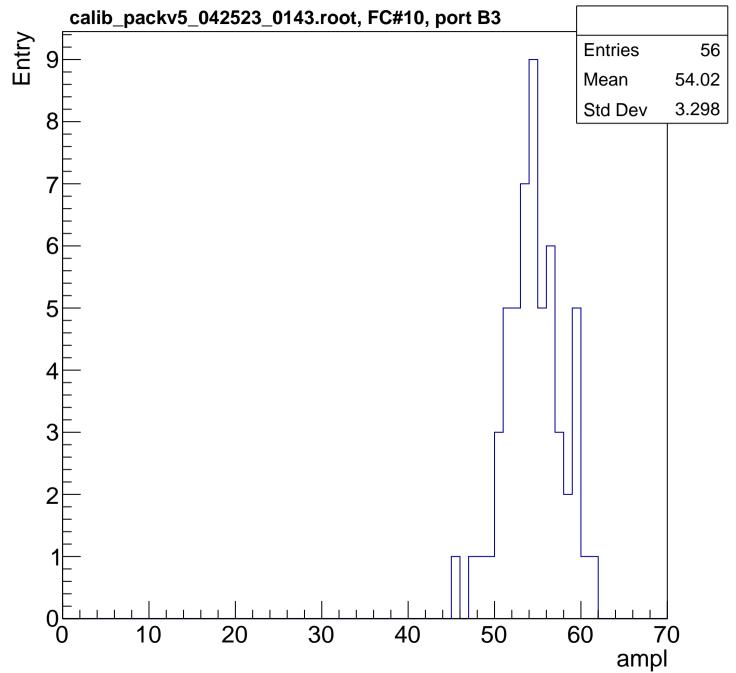


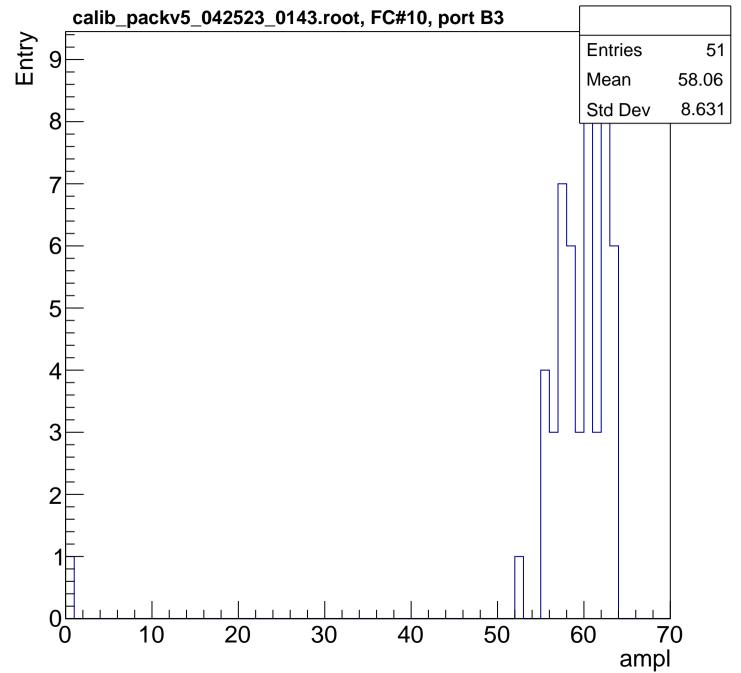


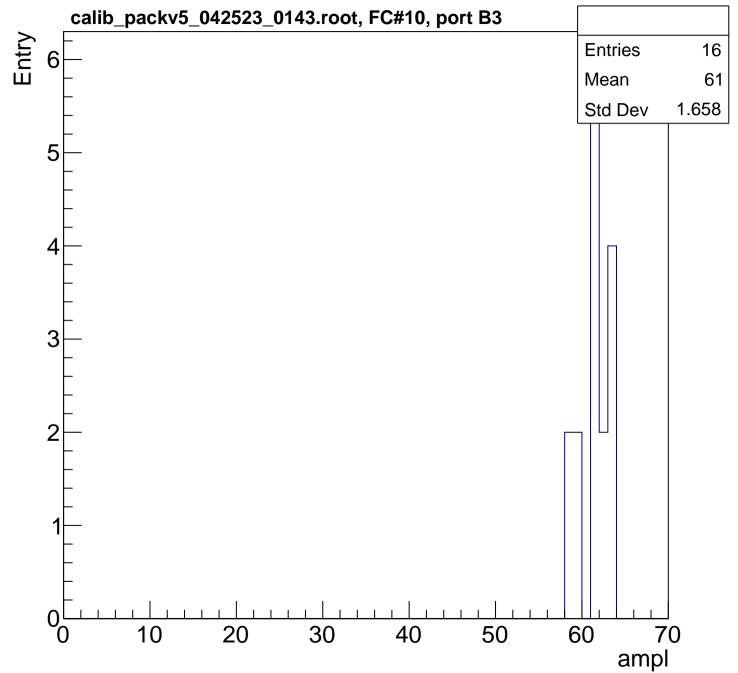




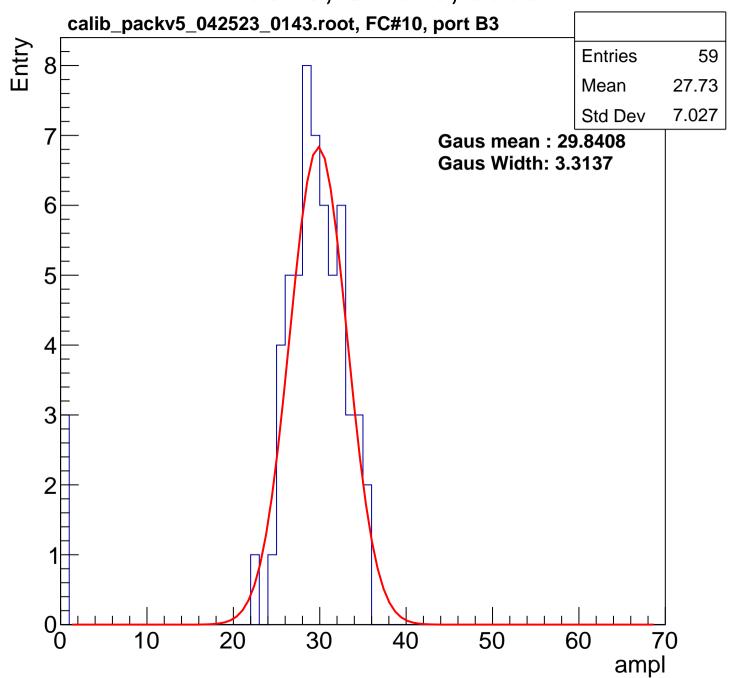


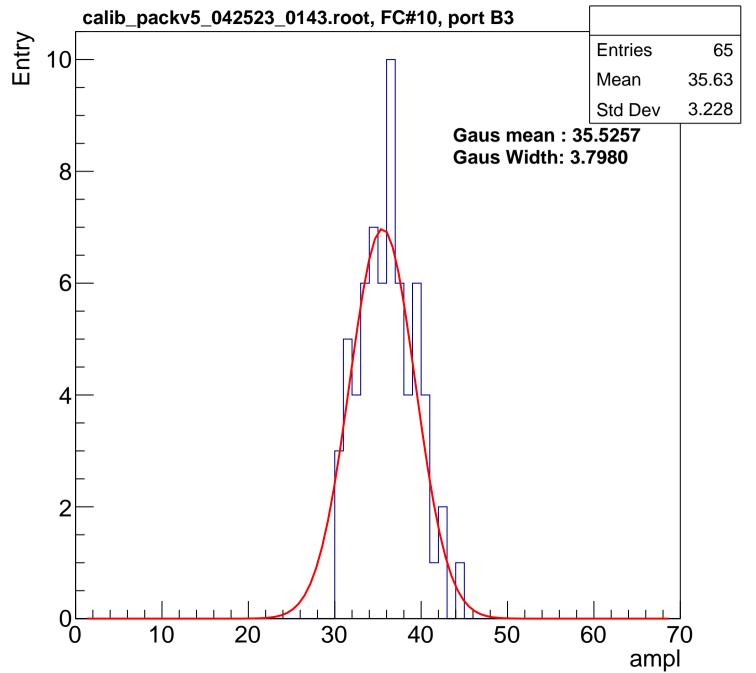


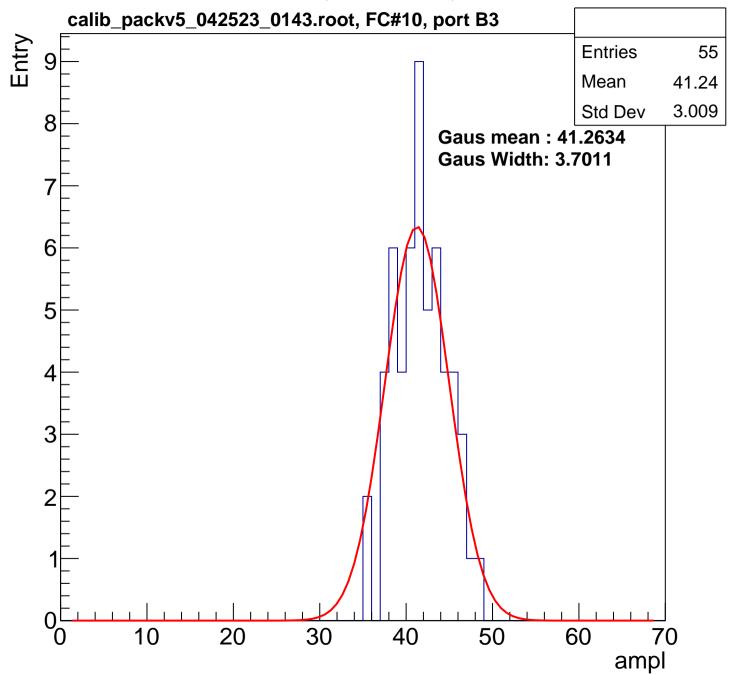


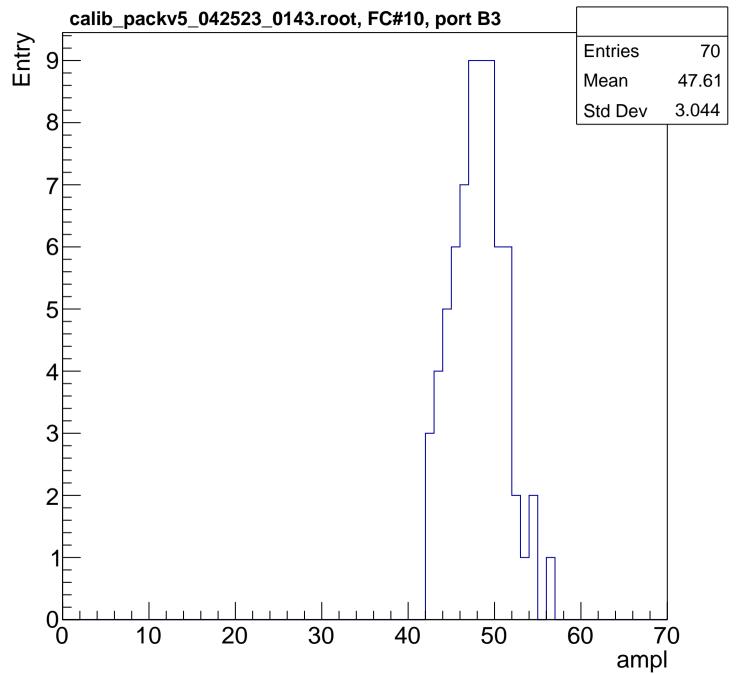


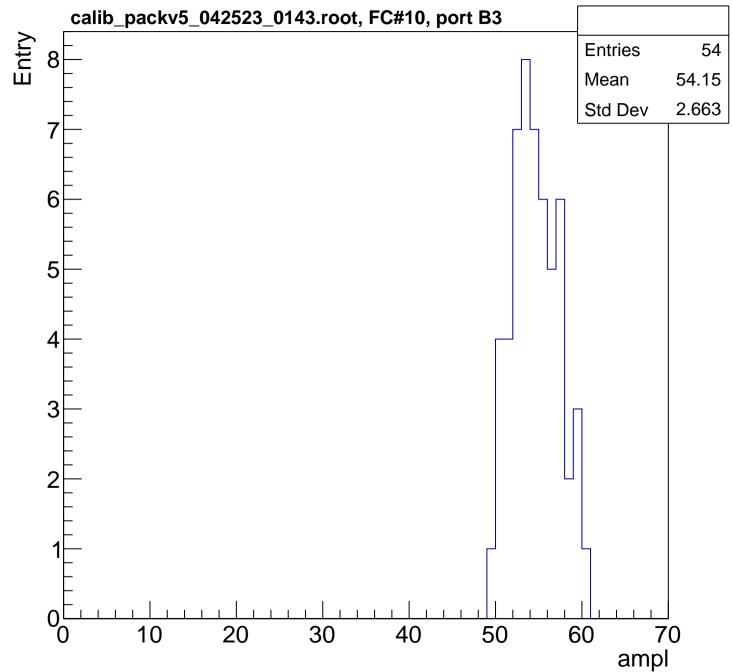
B1L002S, U4-ch8, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

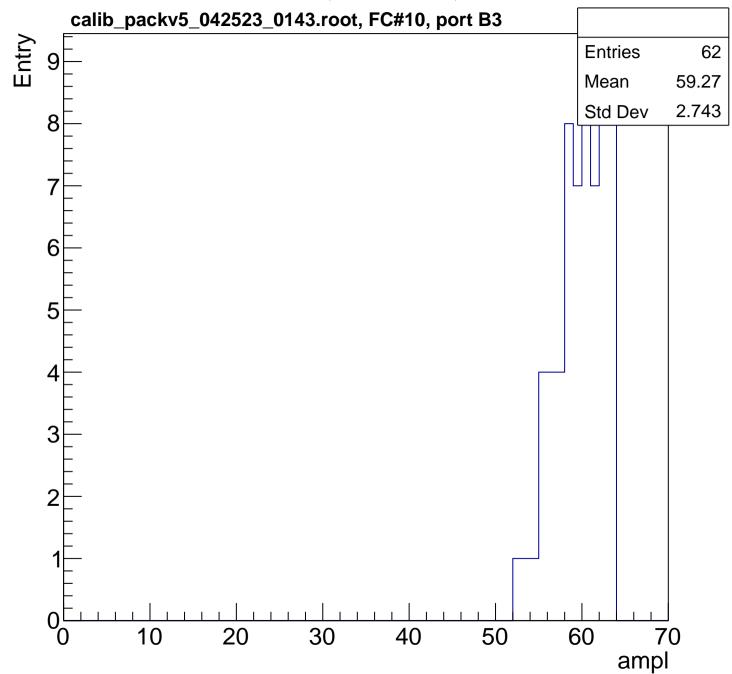


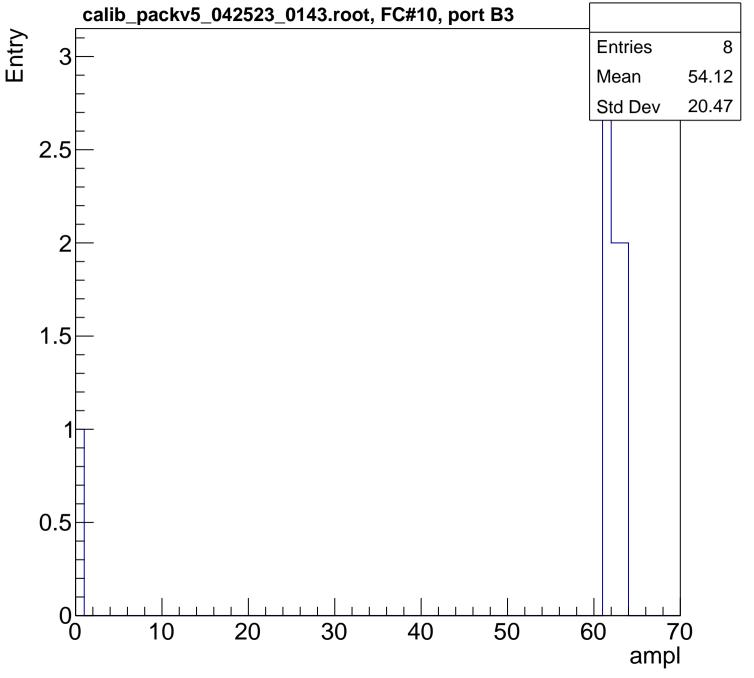


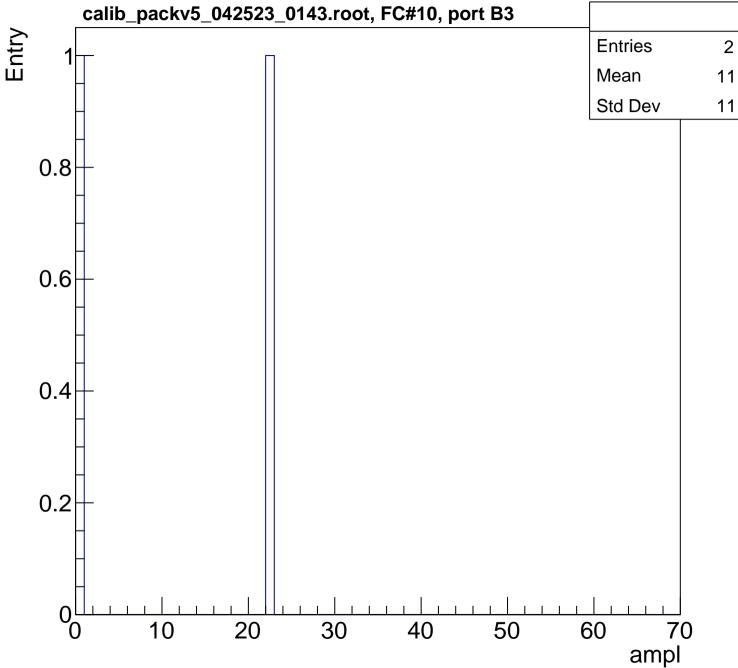


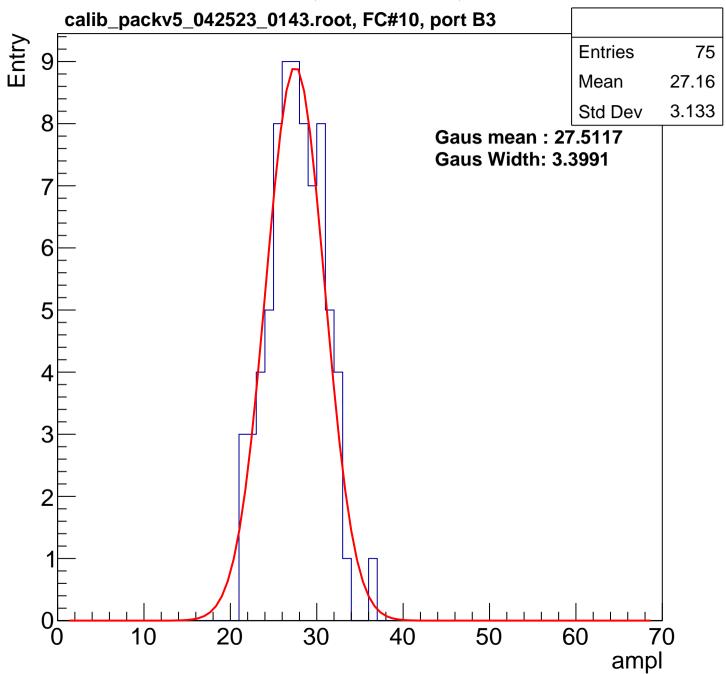


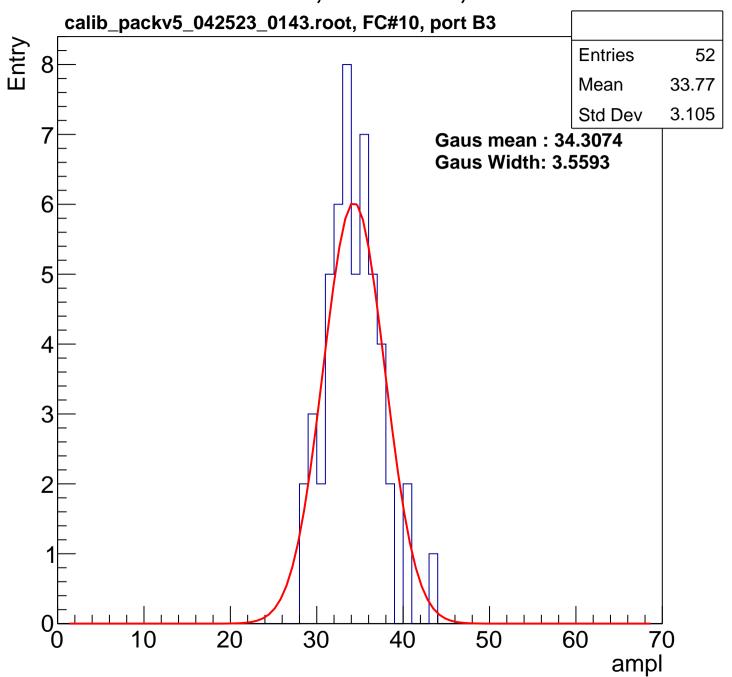


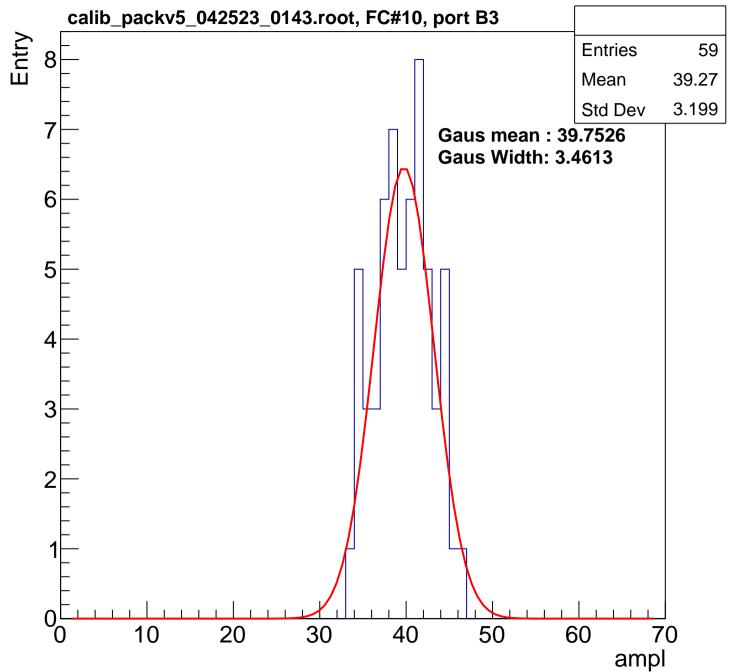


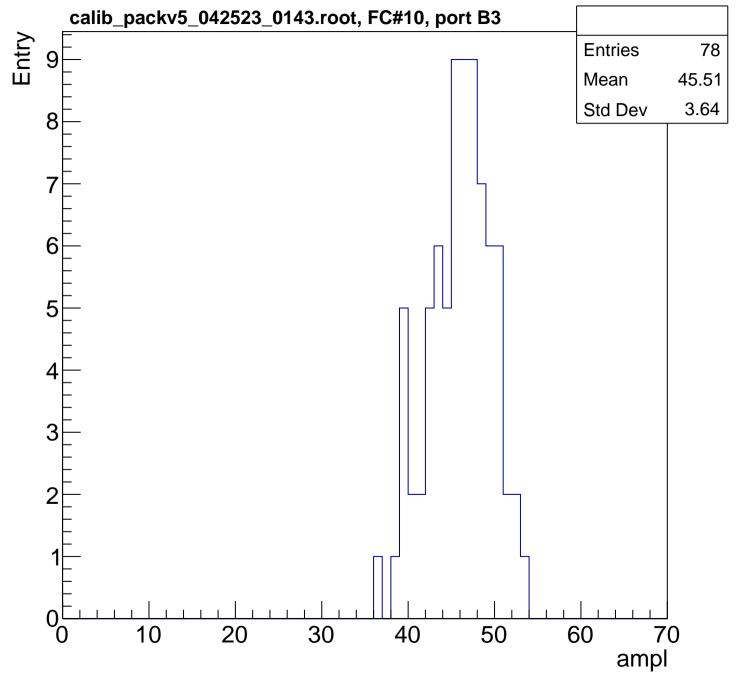


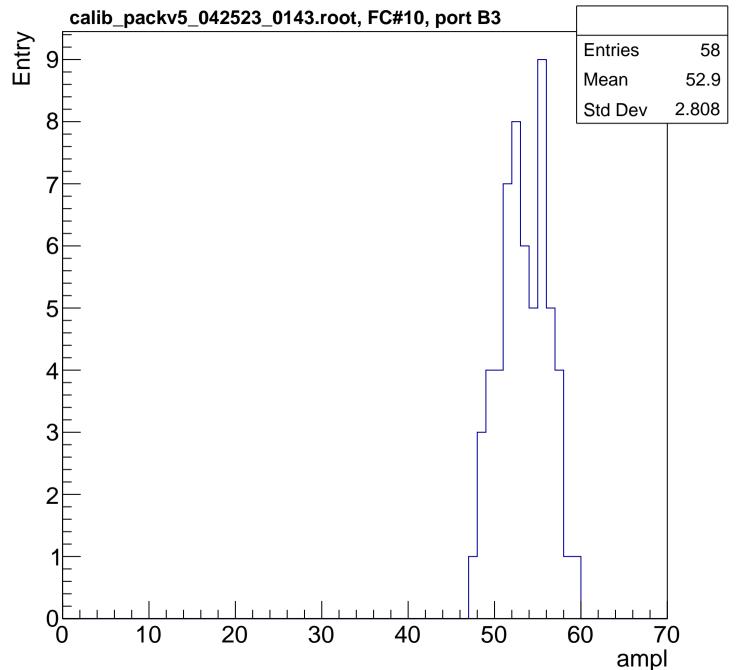


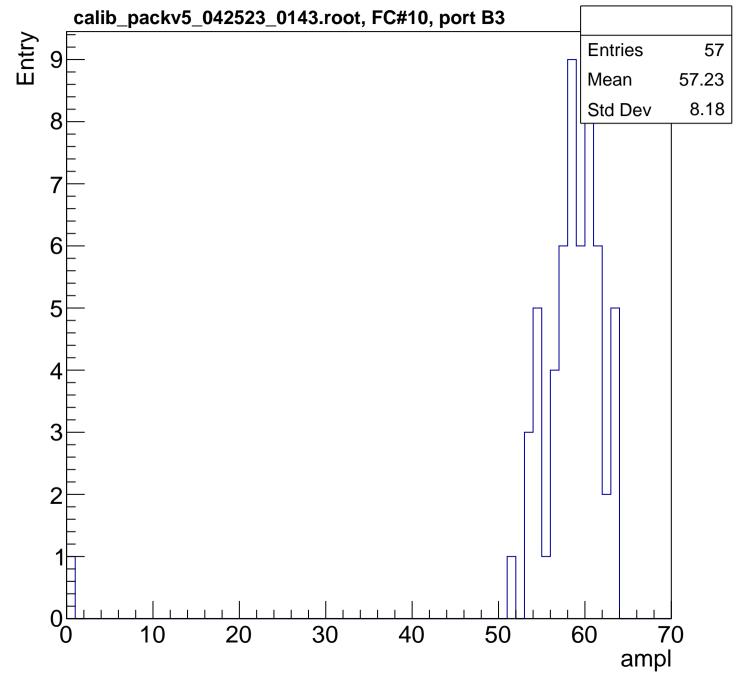


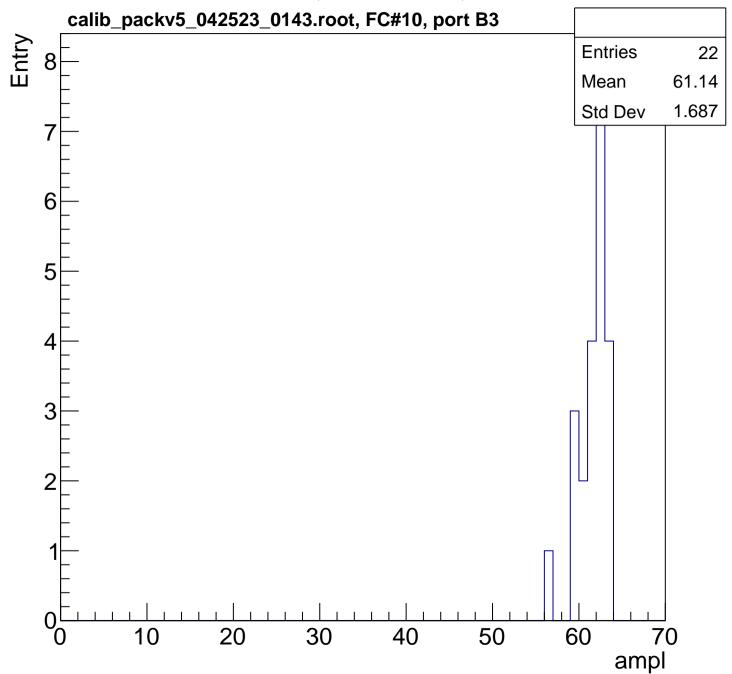


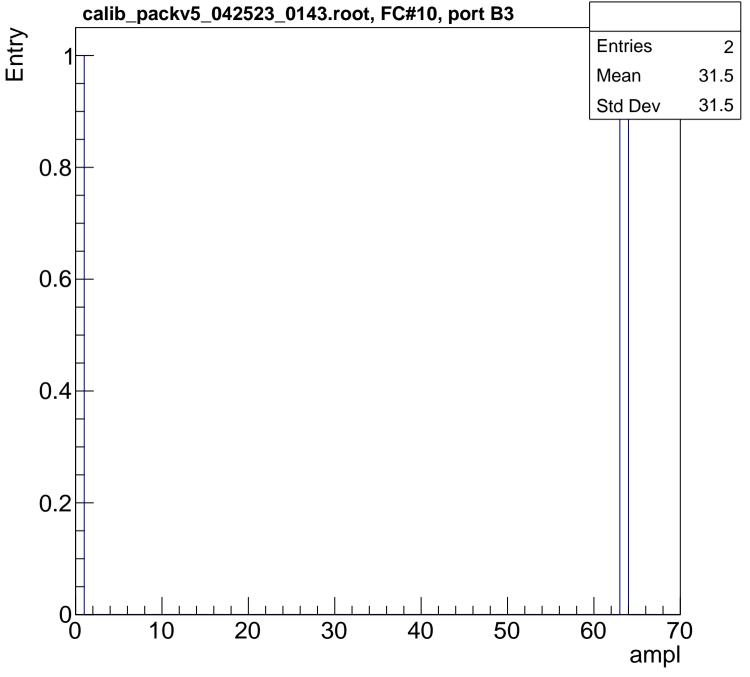


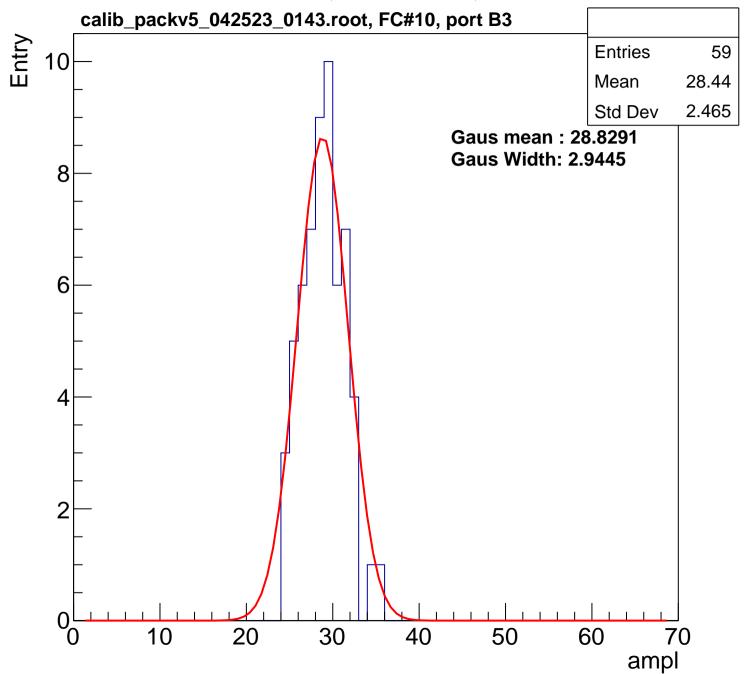


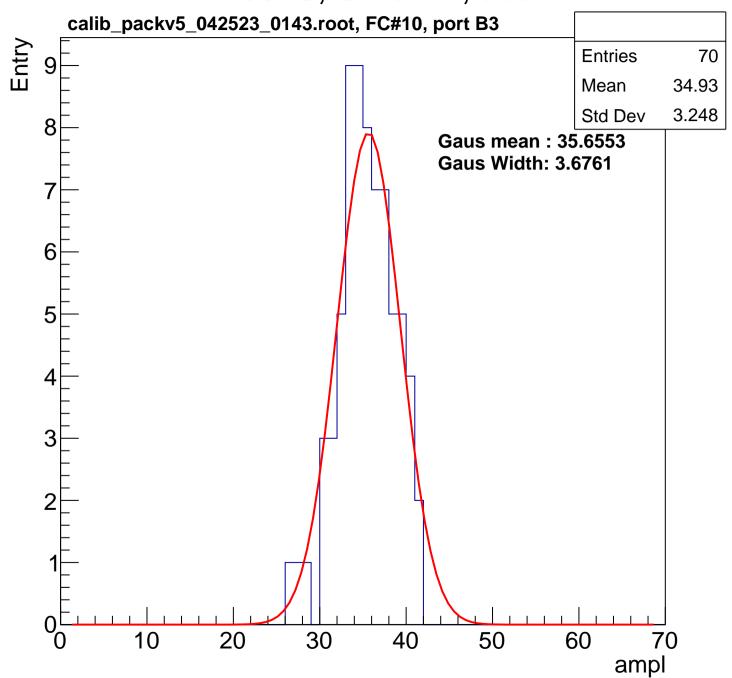


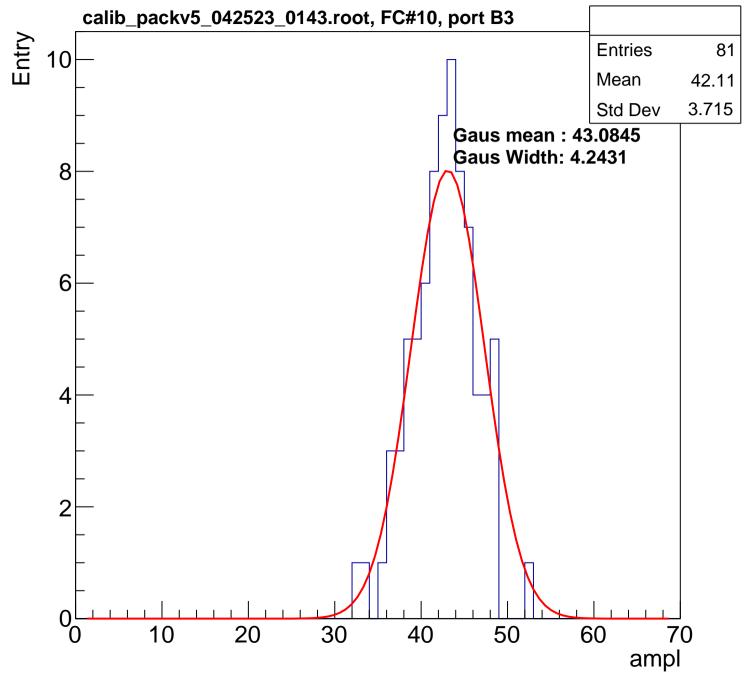


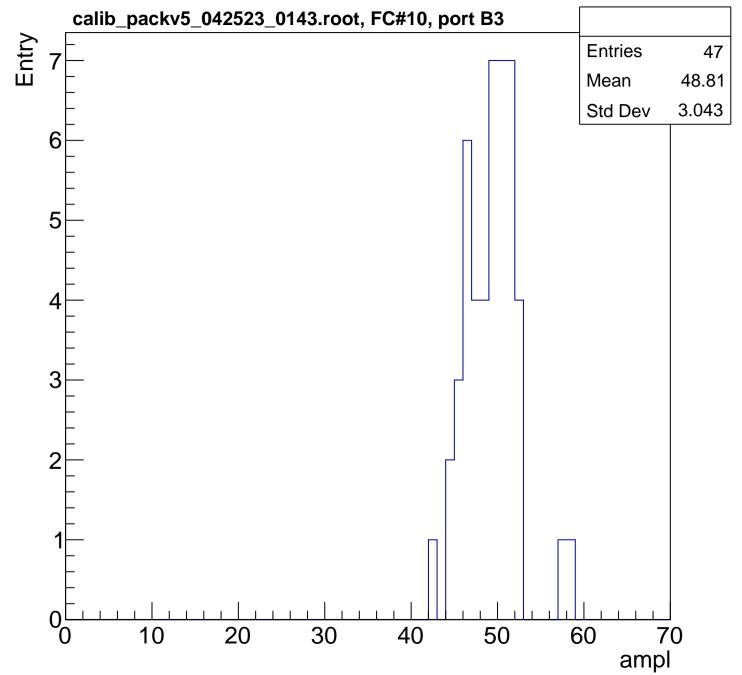


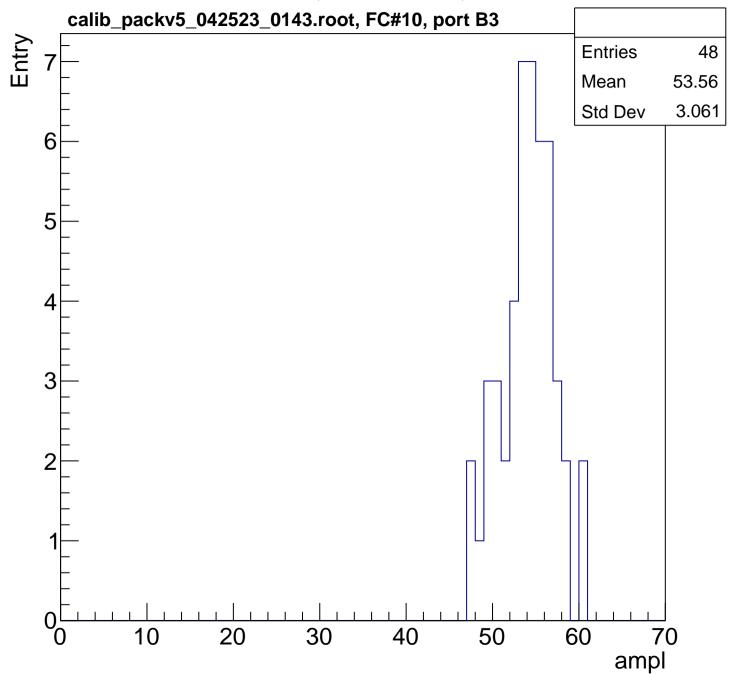


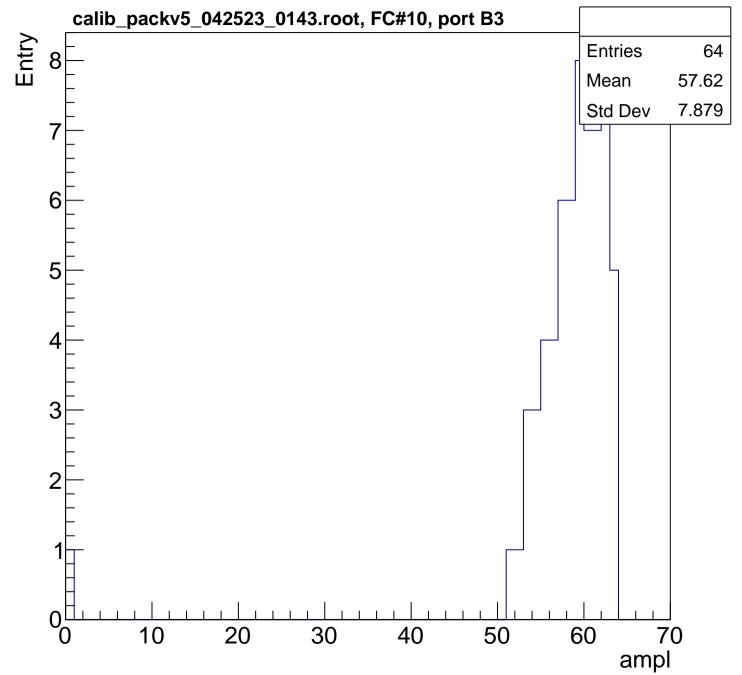


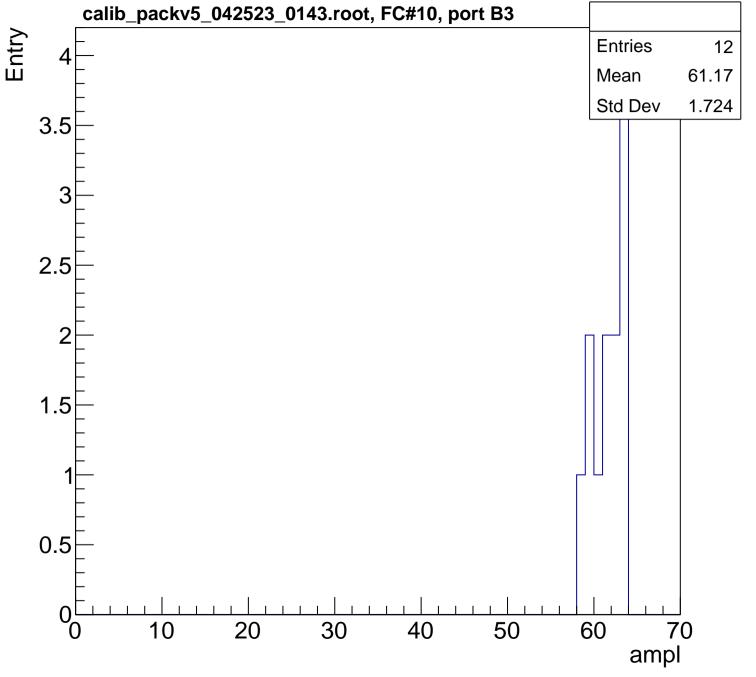


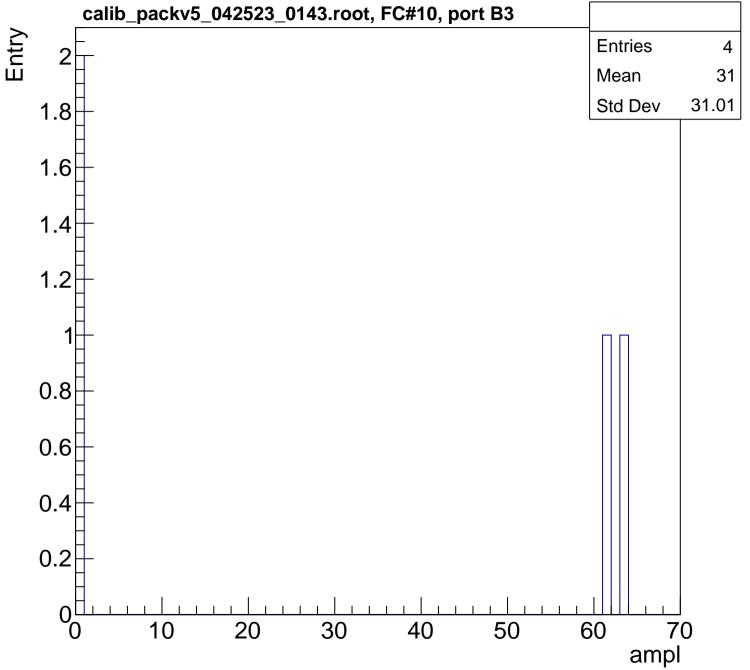


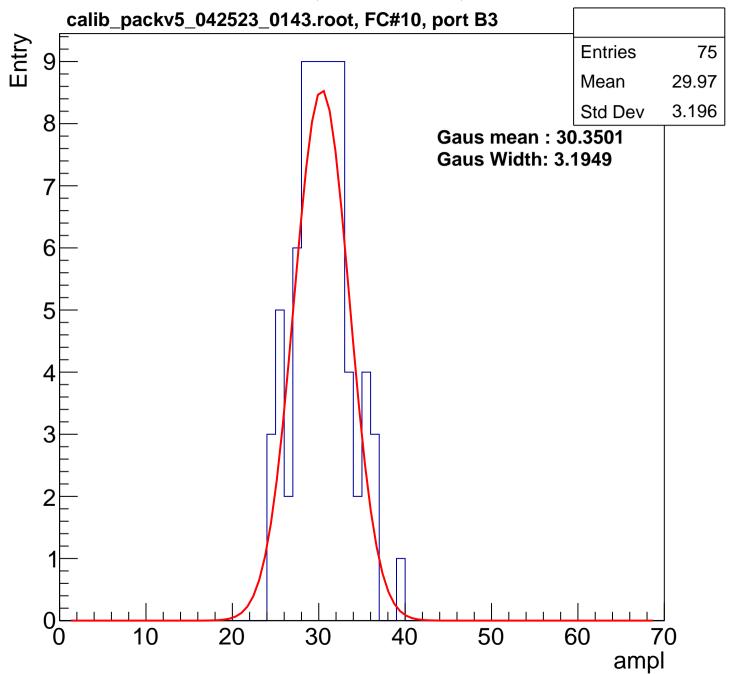


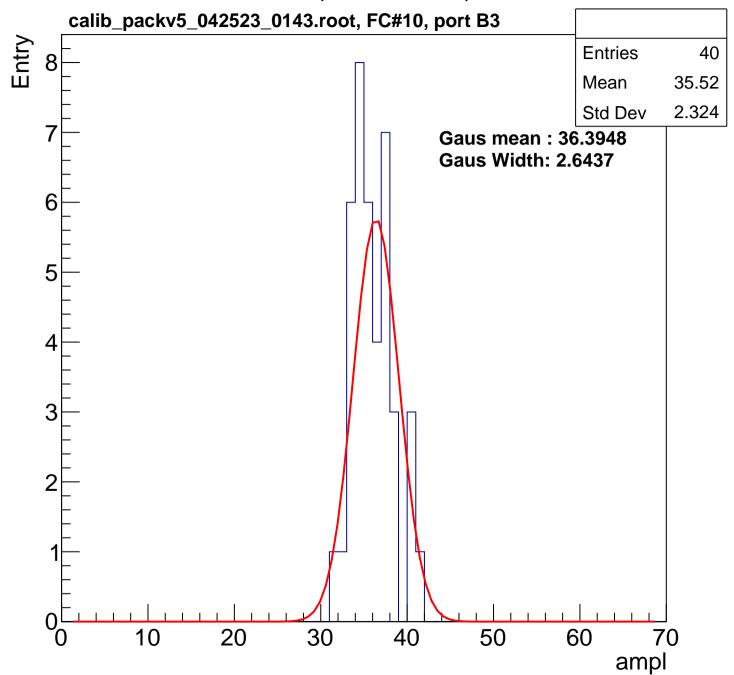


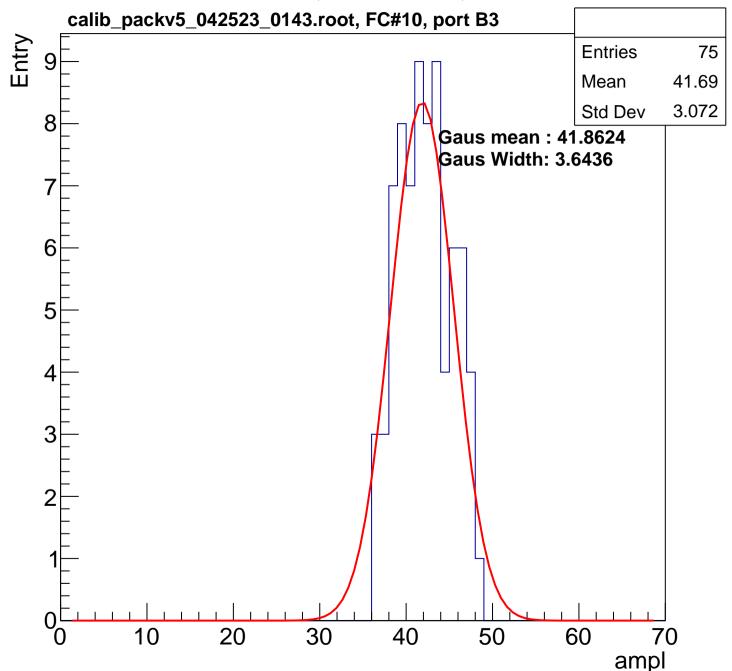


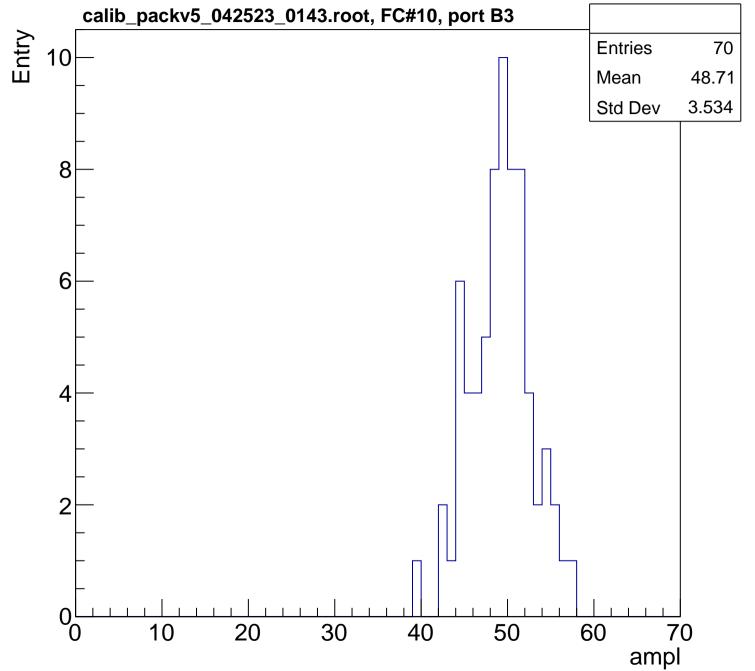


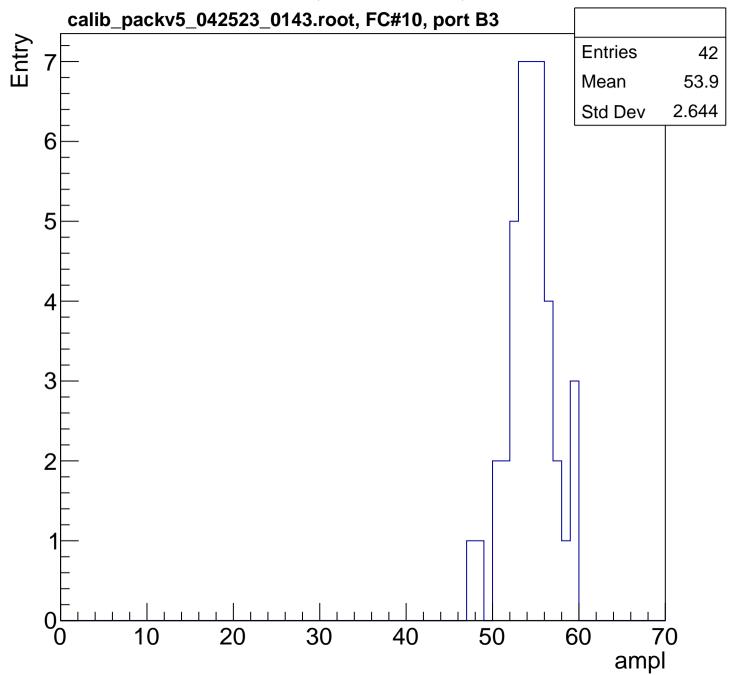


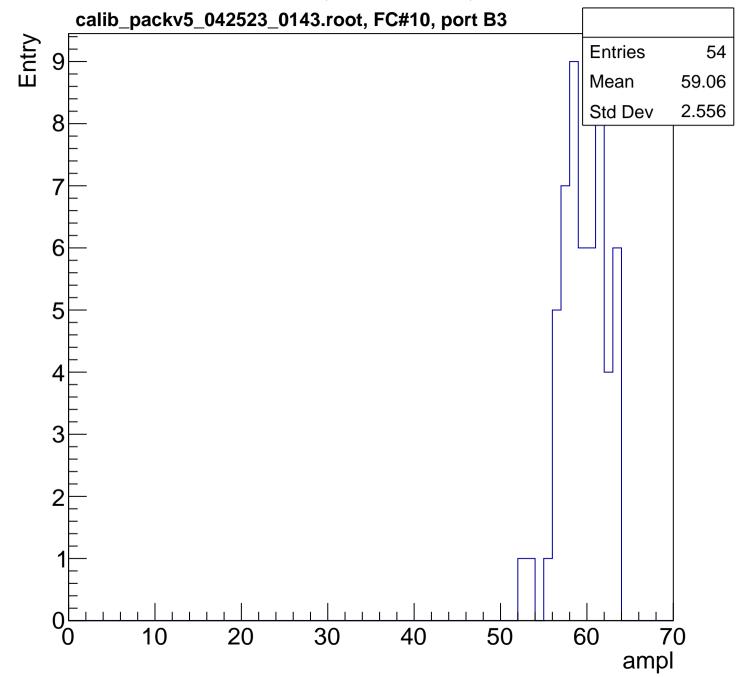


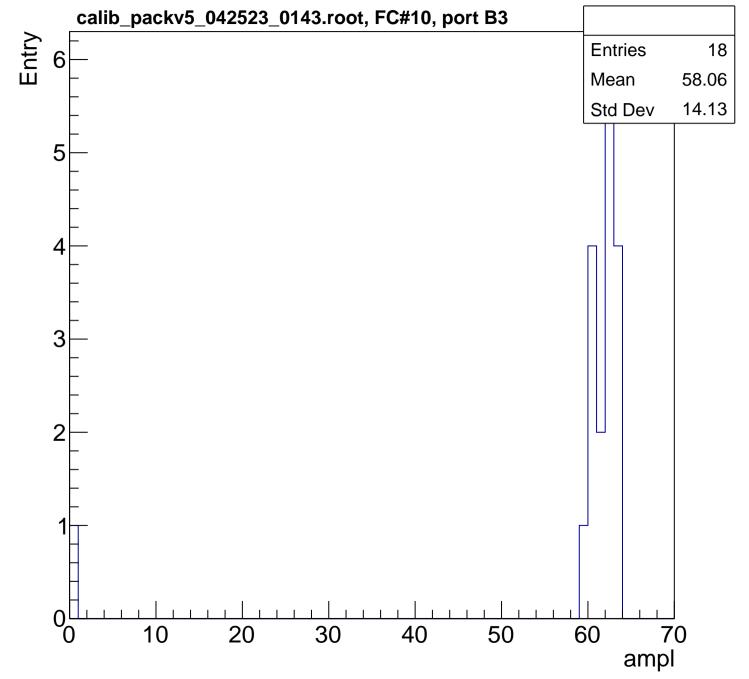


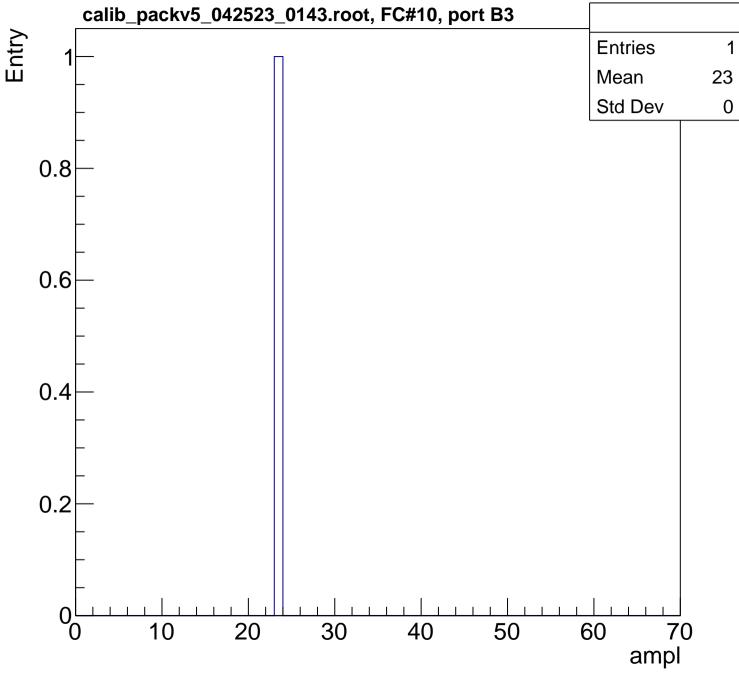


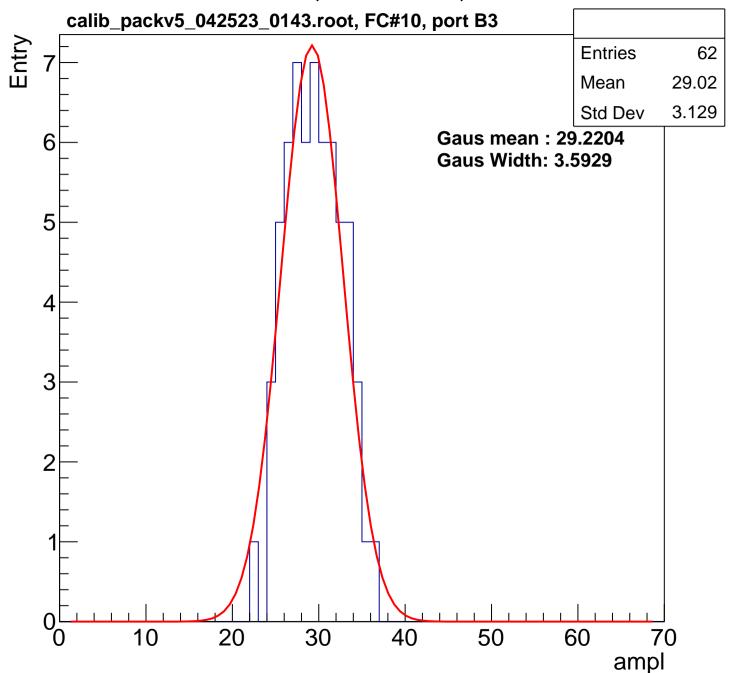


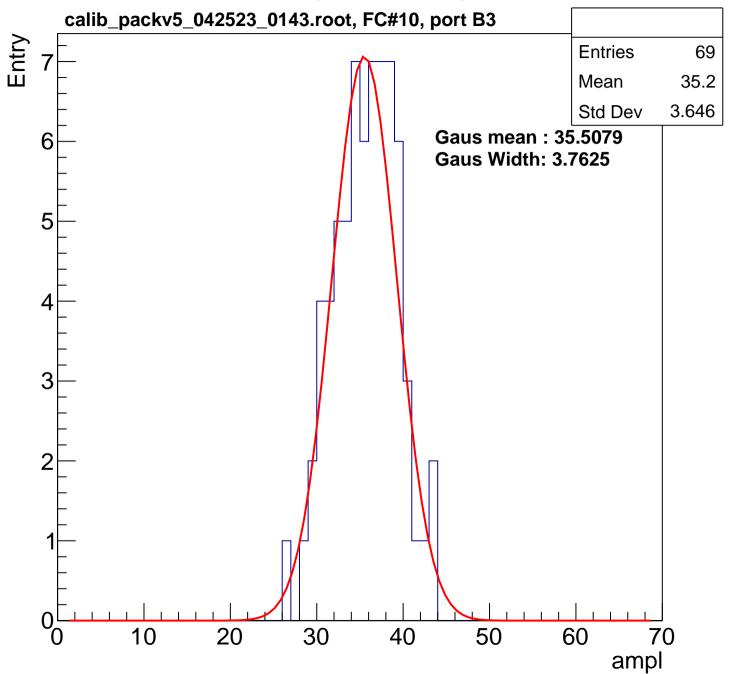


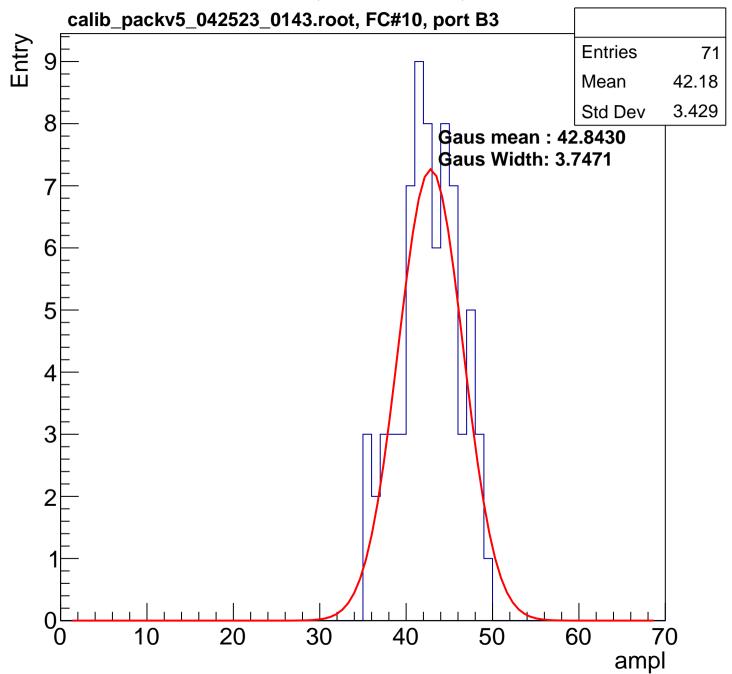


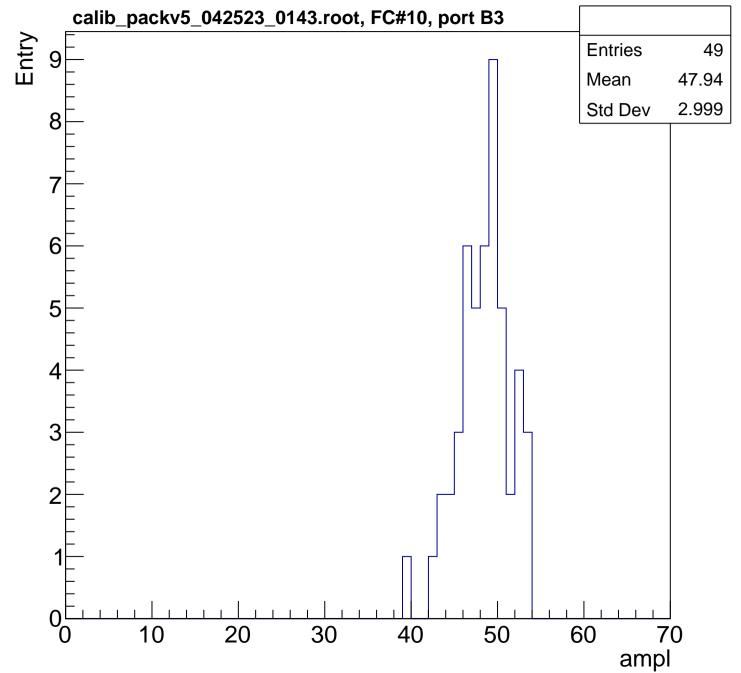


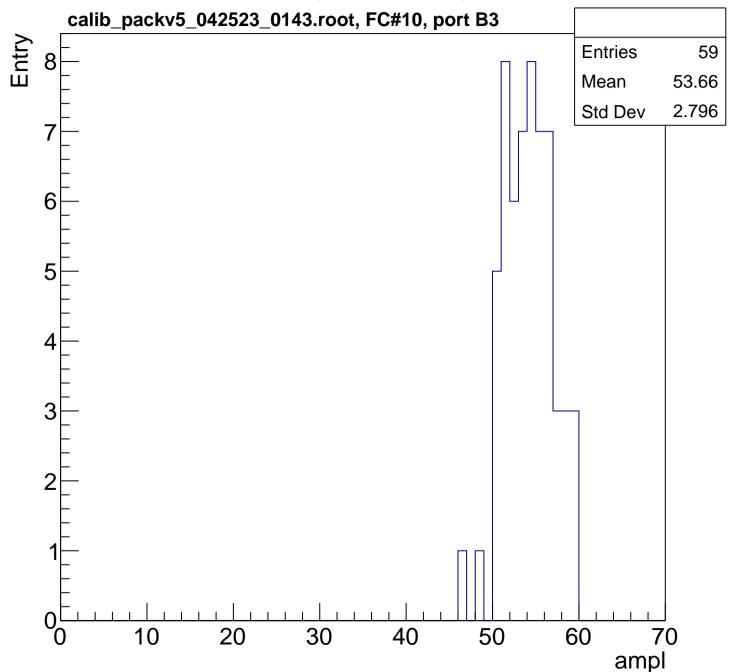


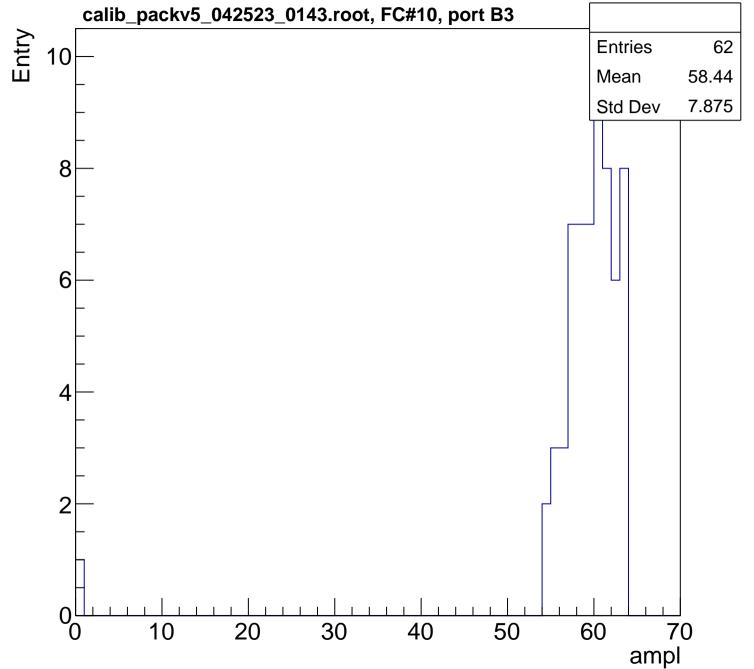


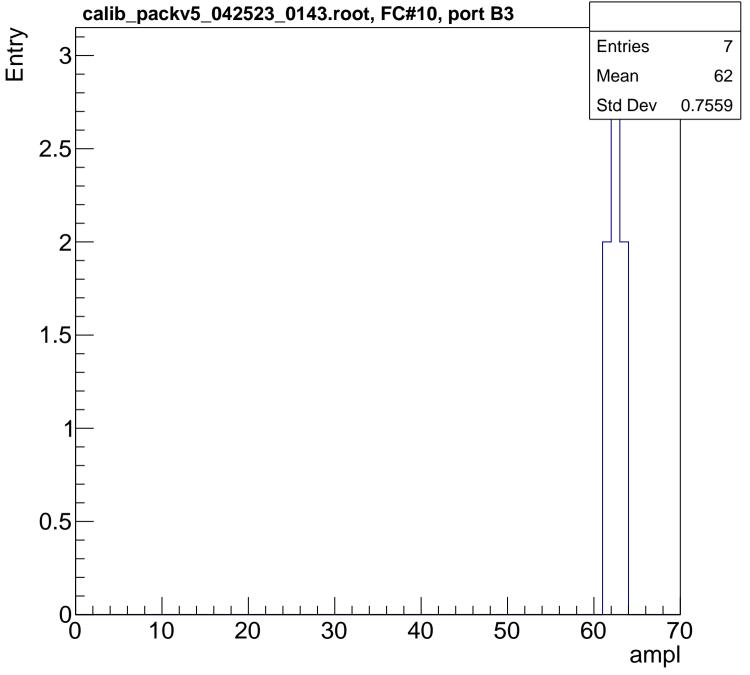


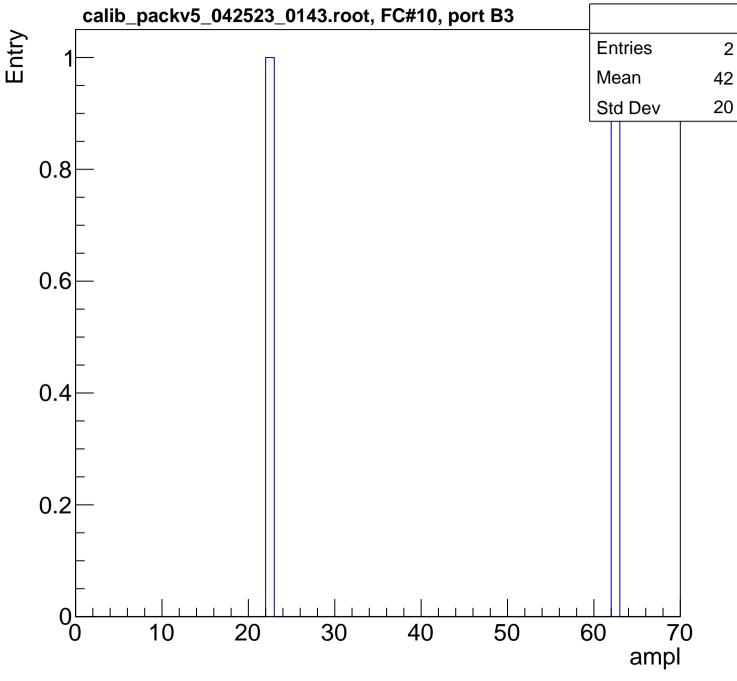


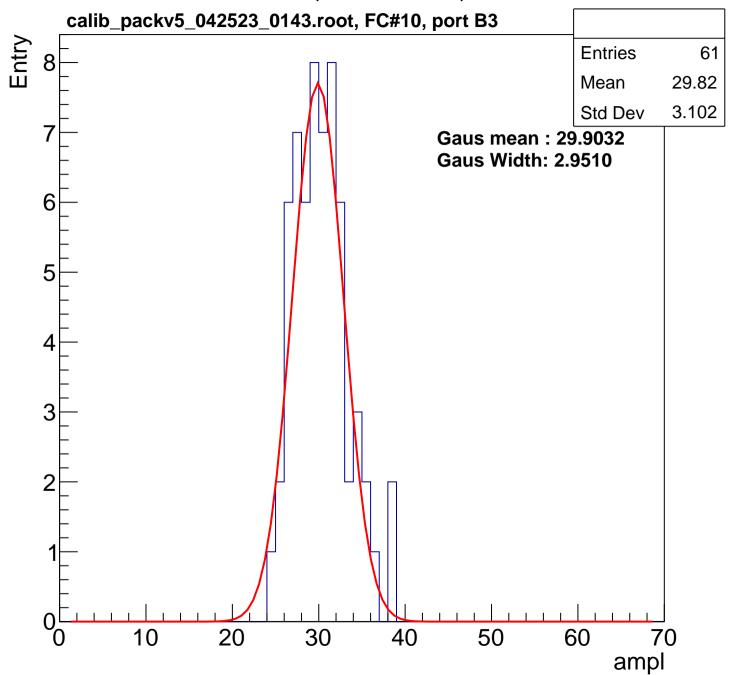


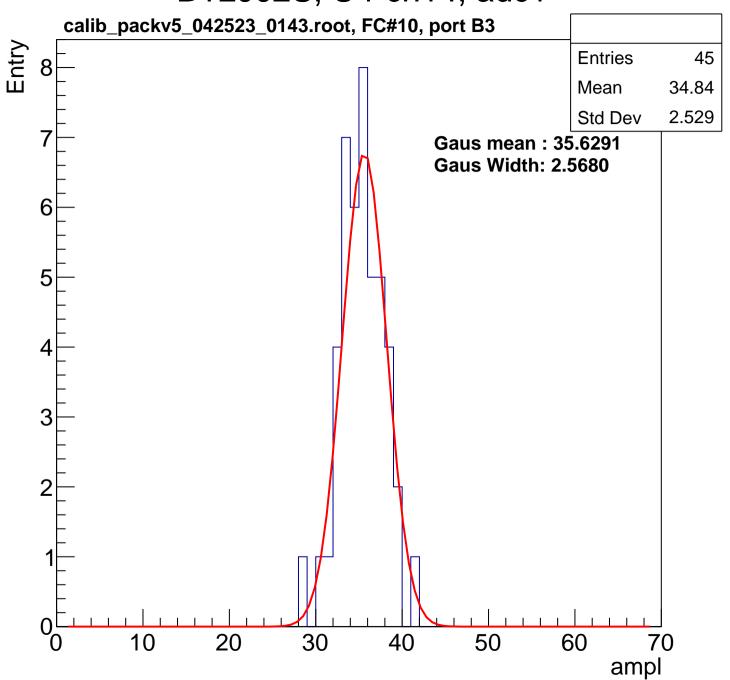


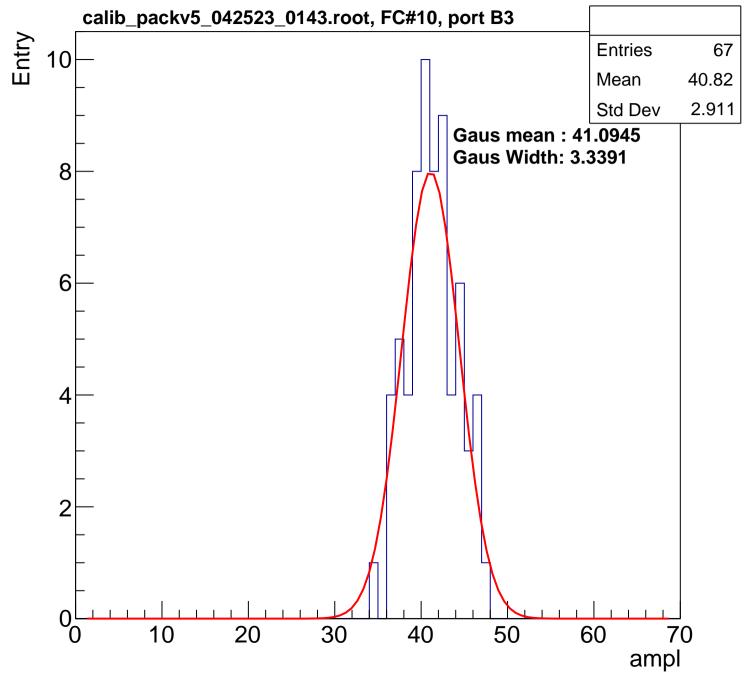


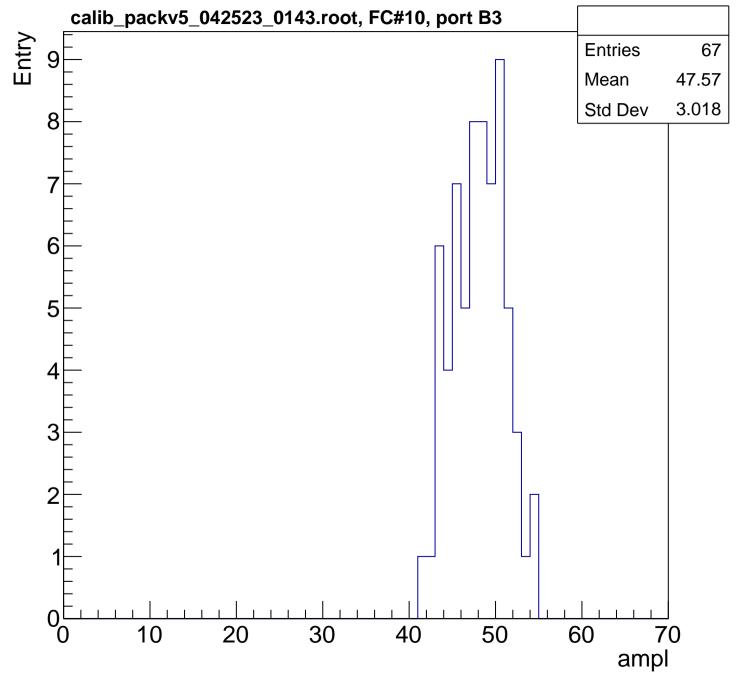


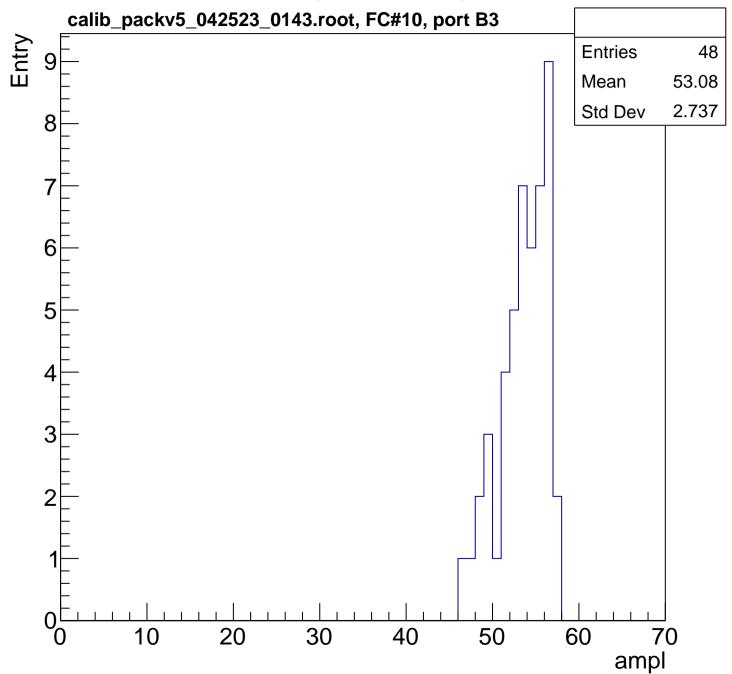


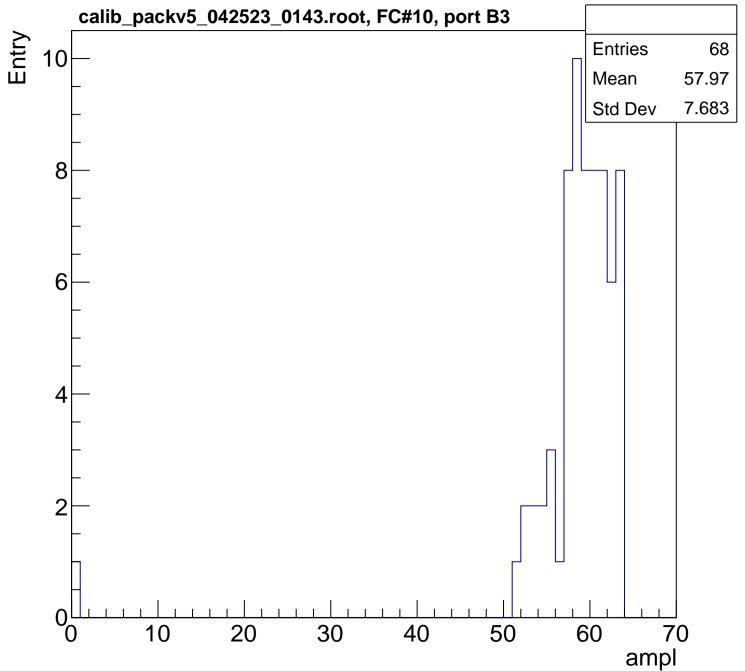


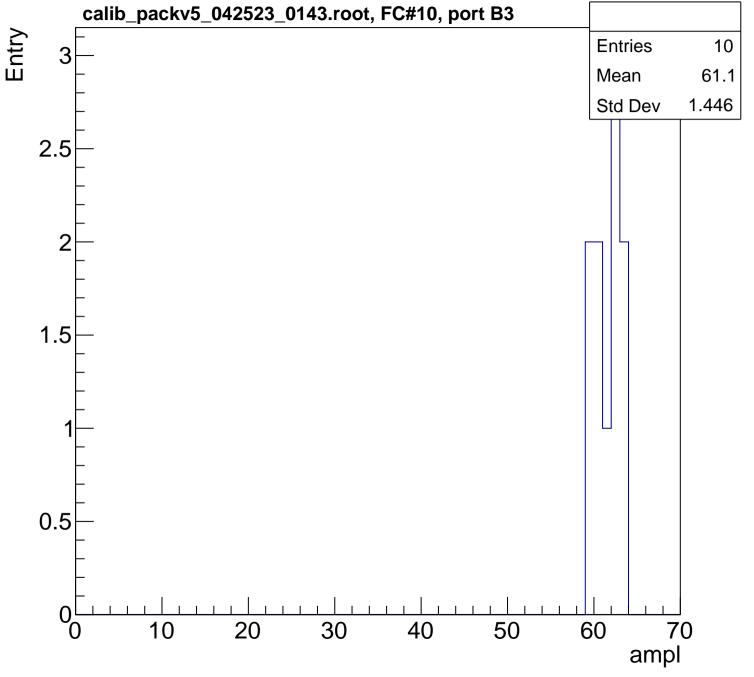


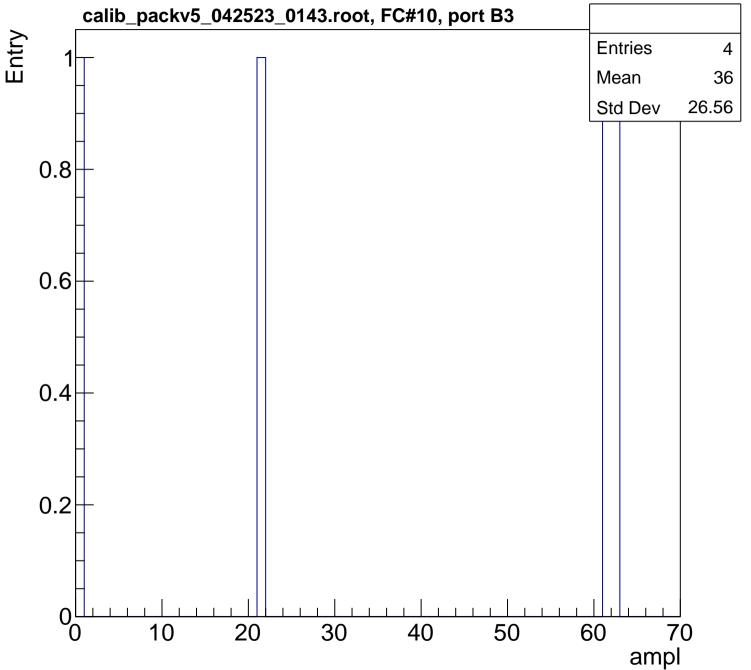


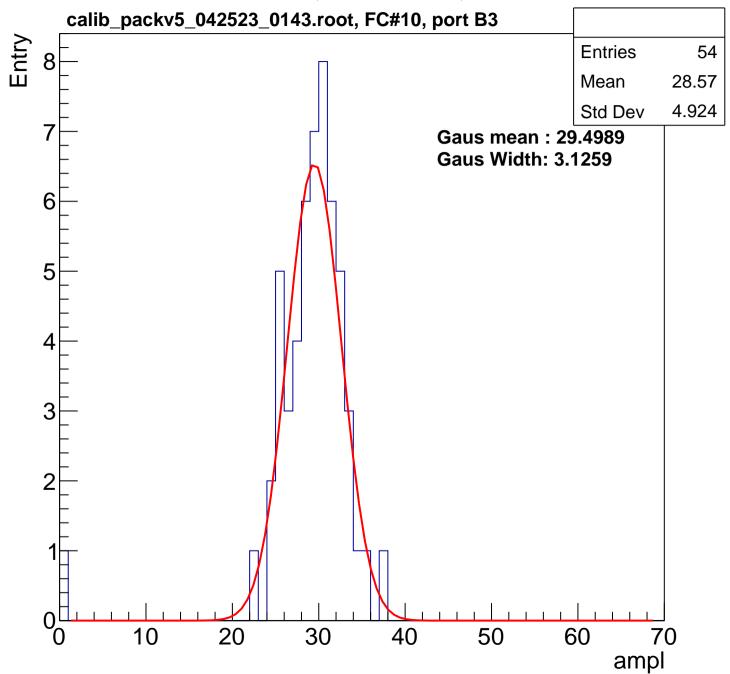


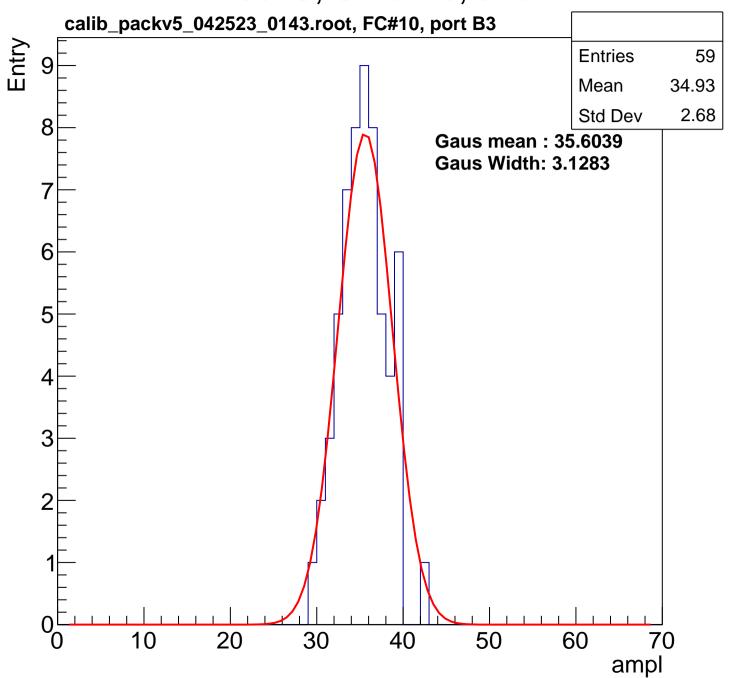


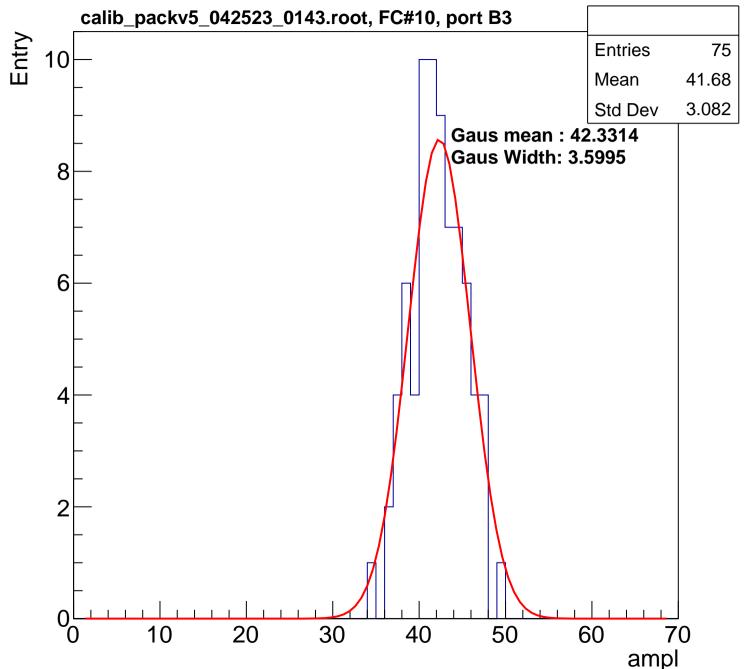


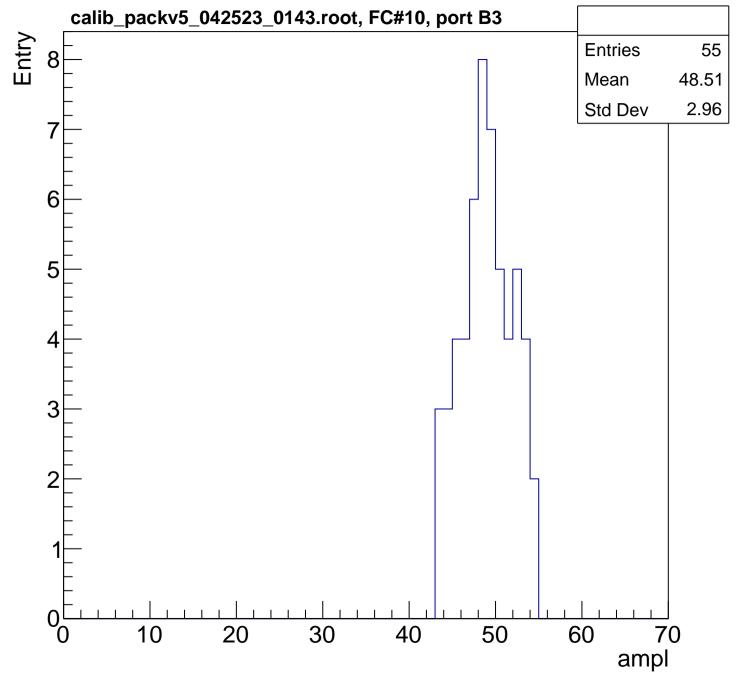


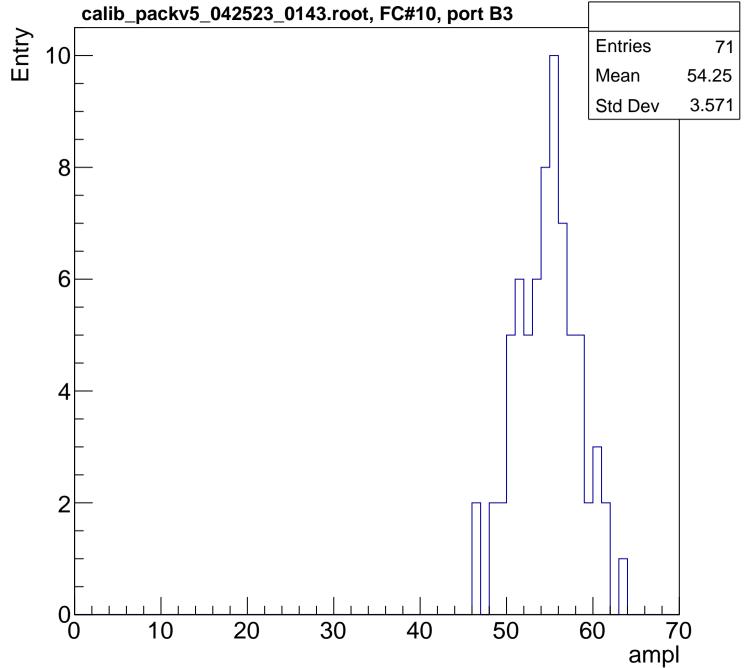


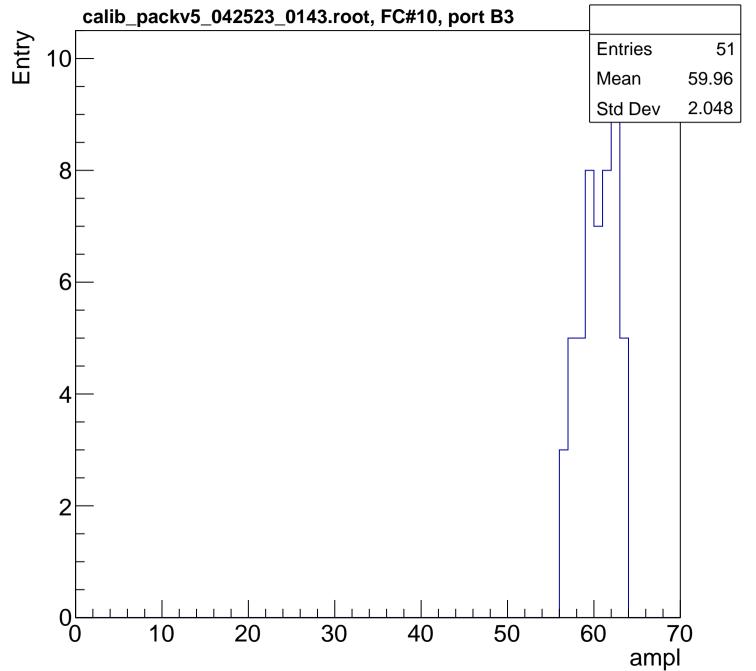


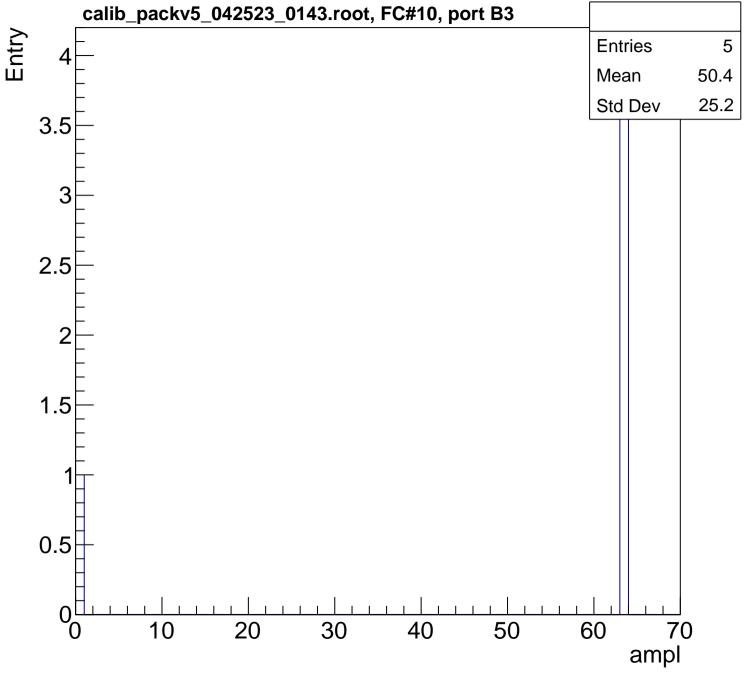










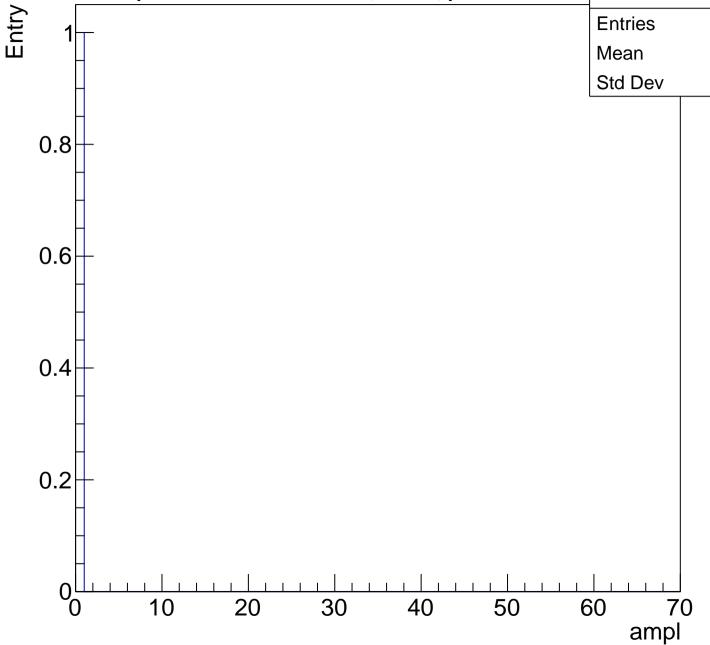


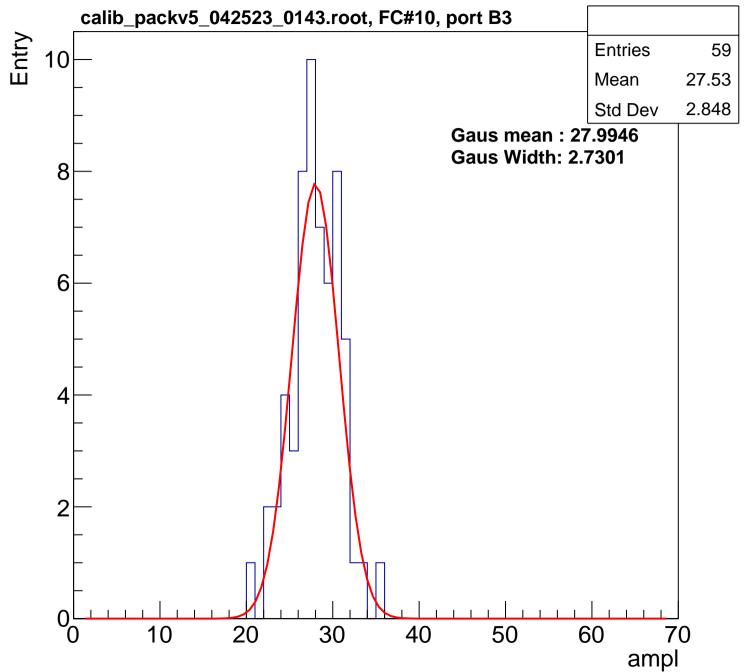
B1L002S, U4-ch15, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 **Entries** Mean

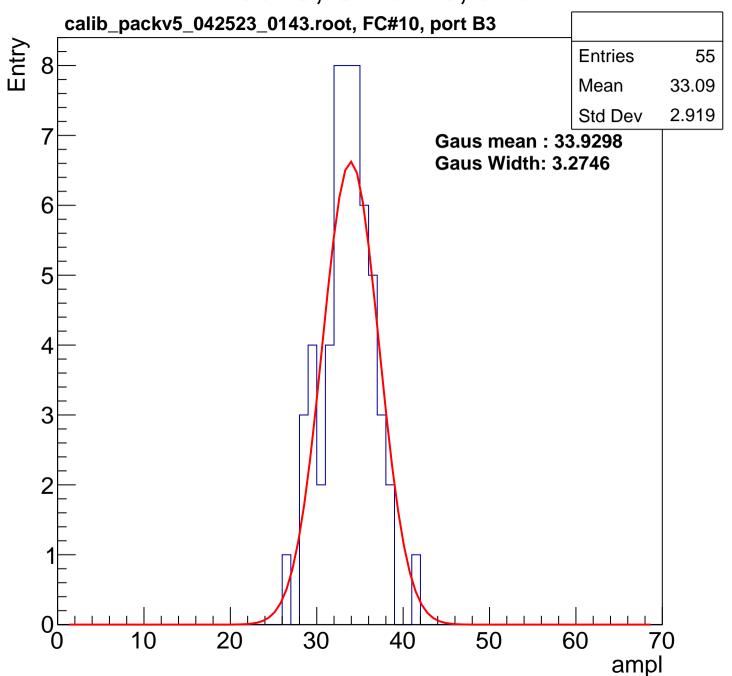
1

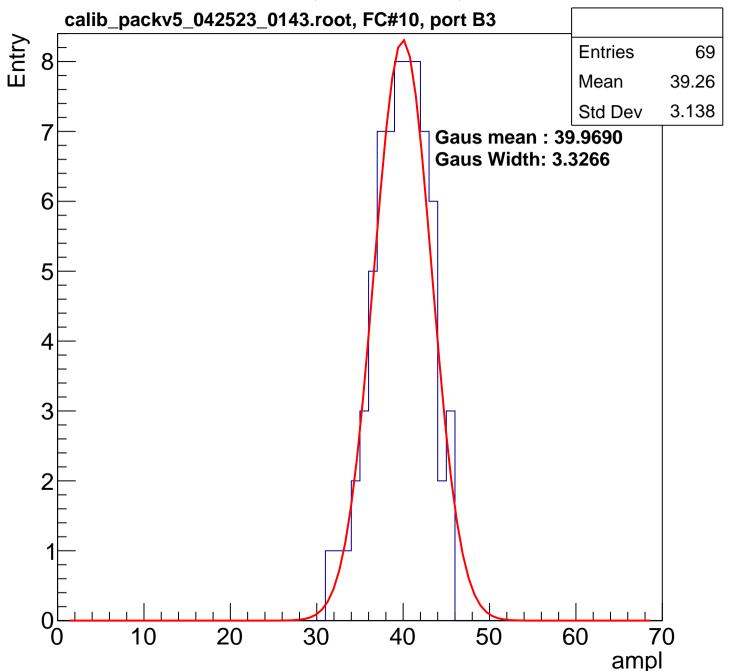
0

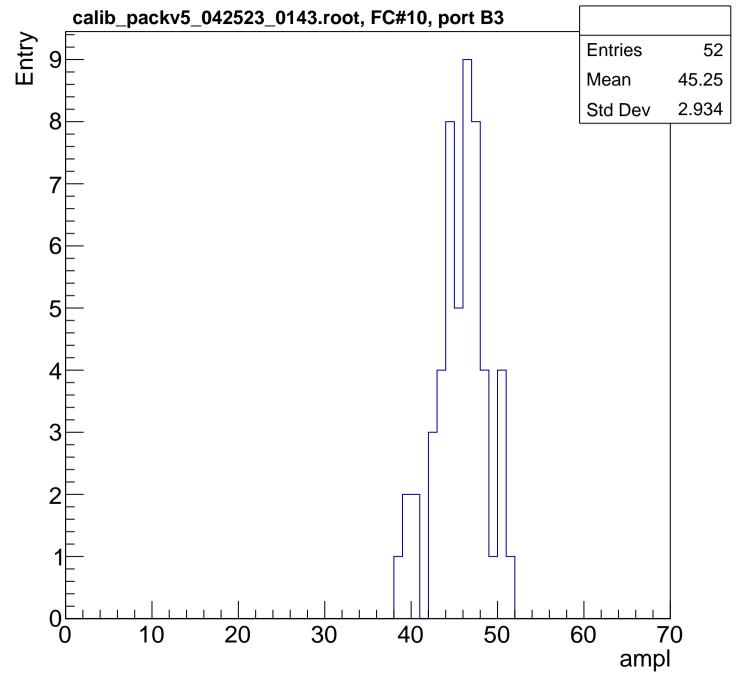
0

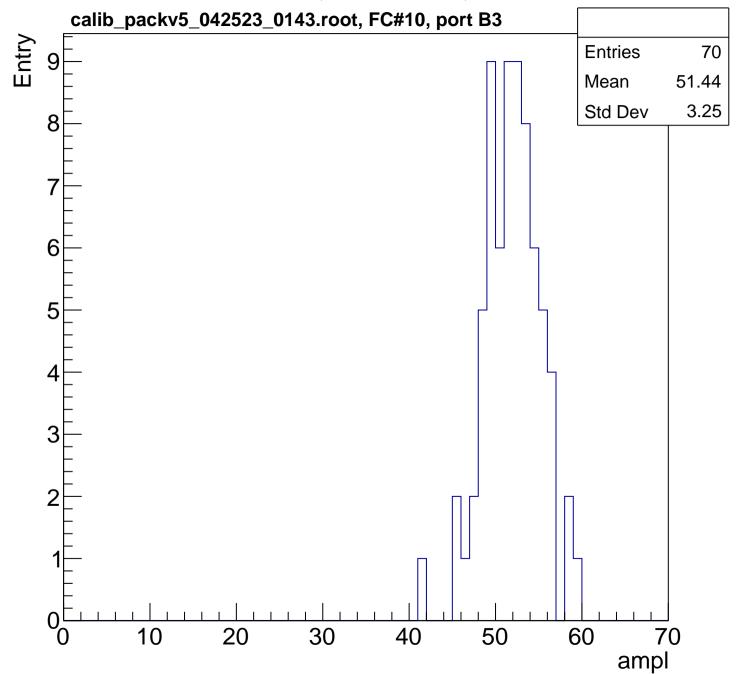


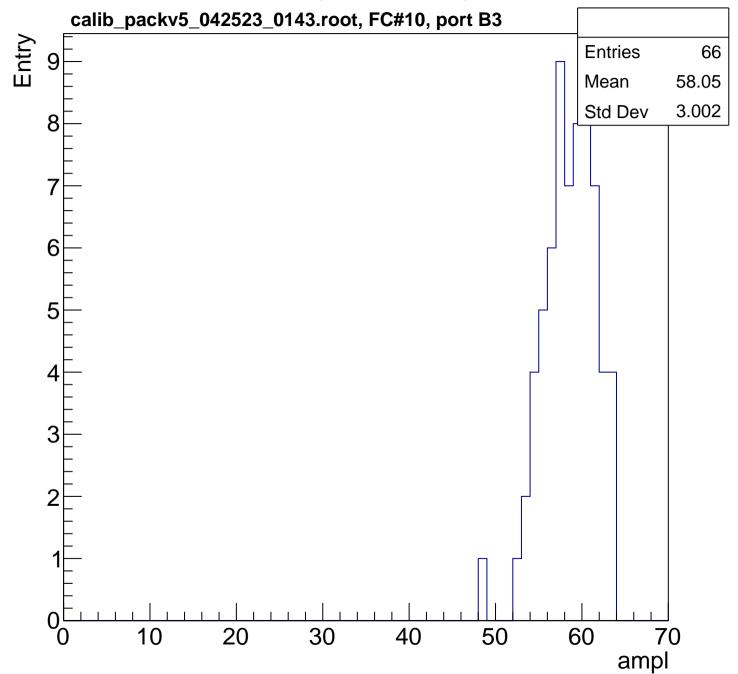


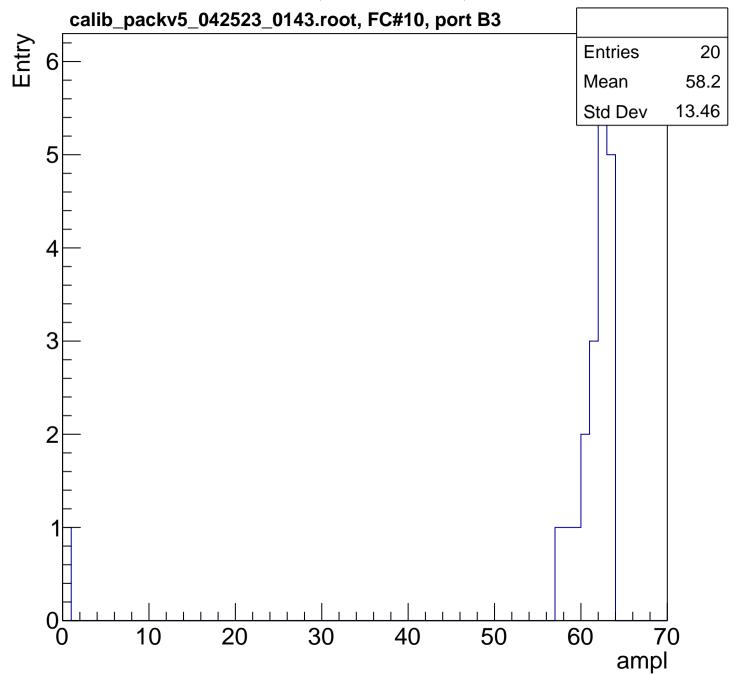




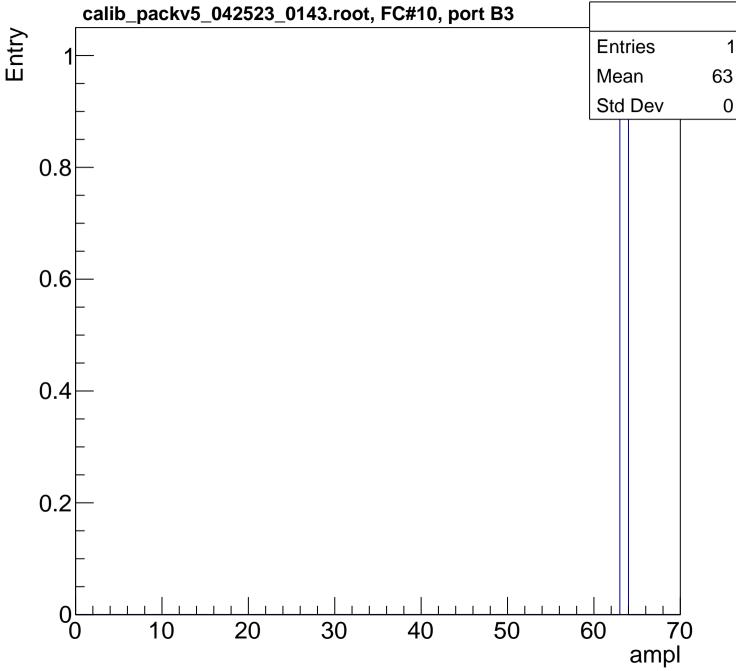


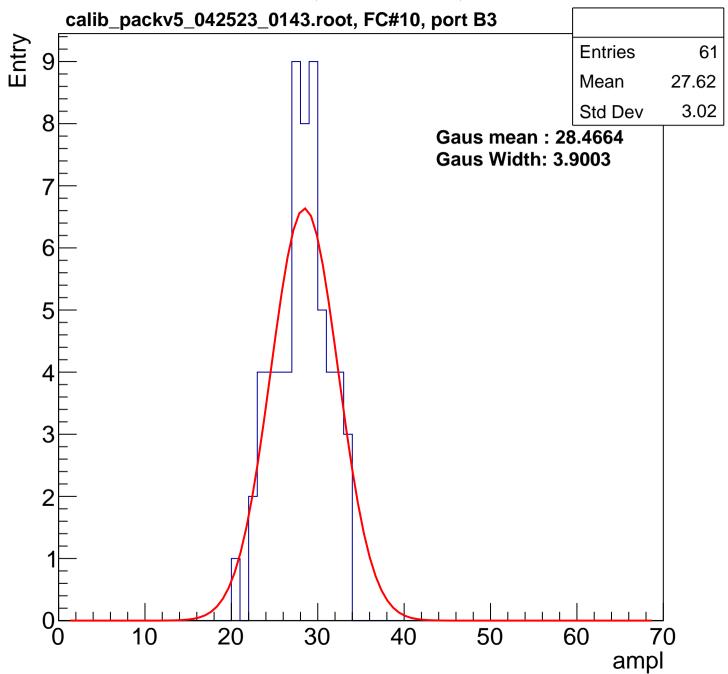


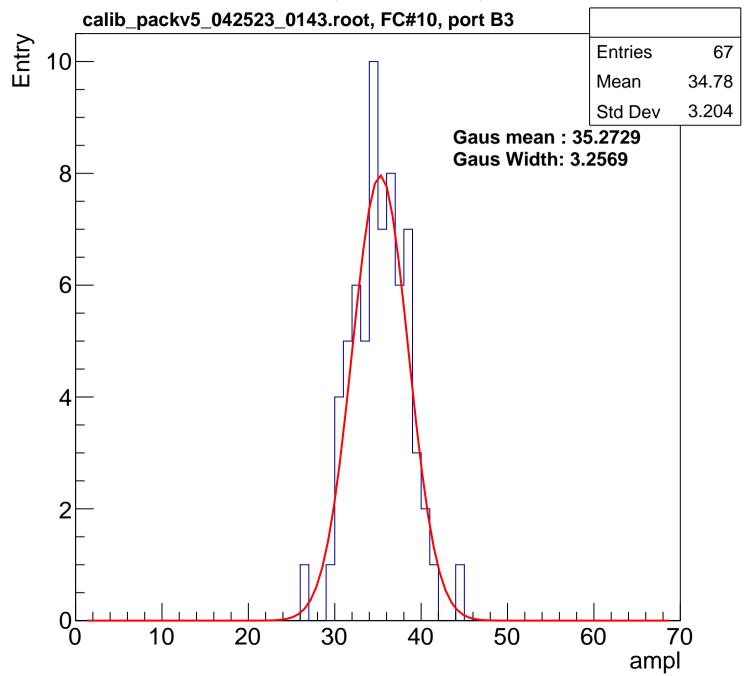


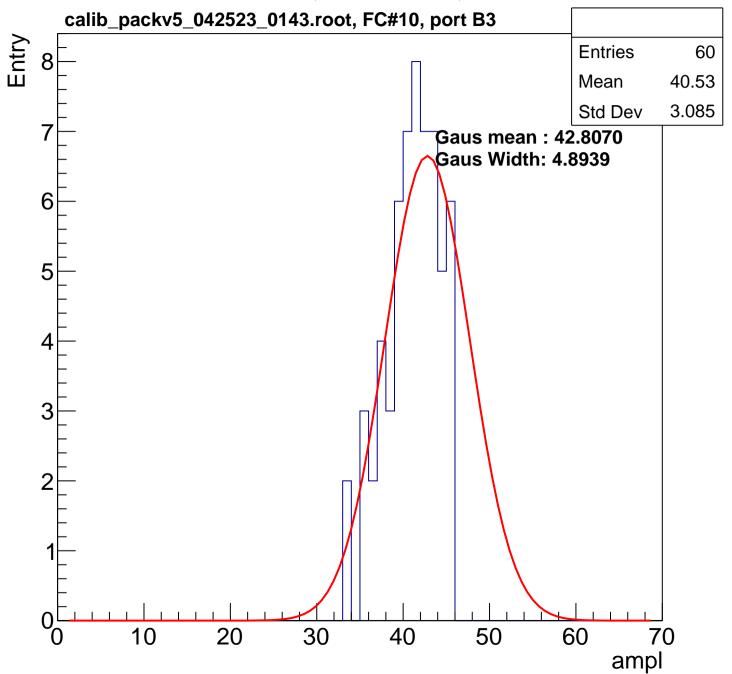


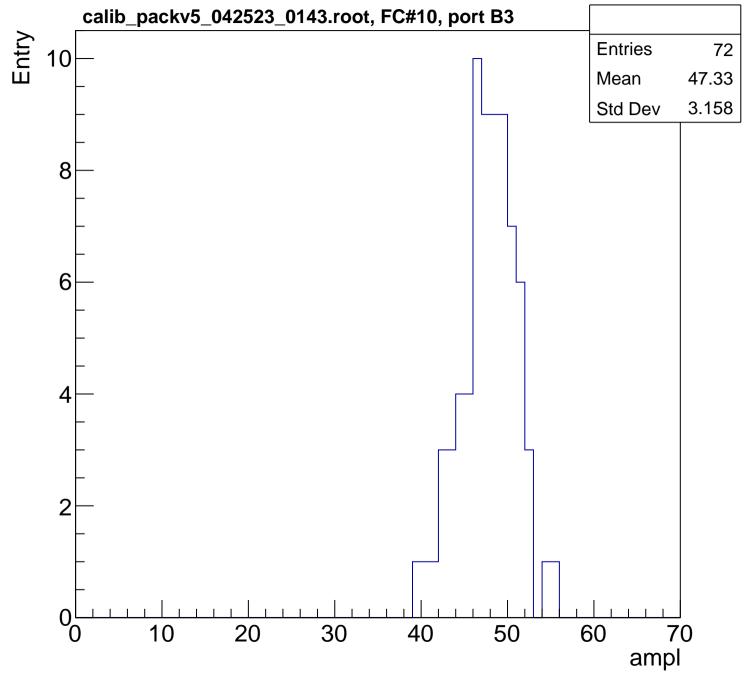
0

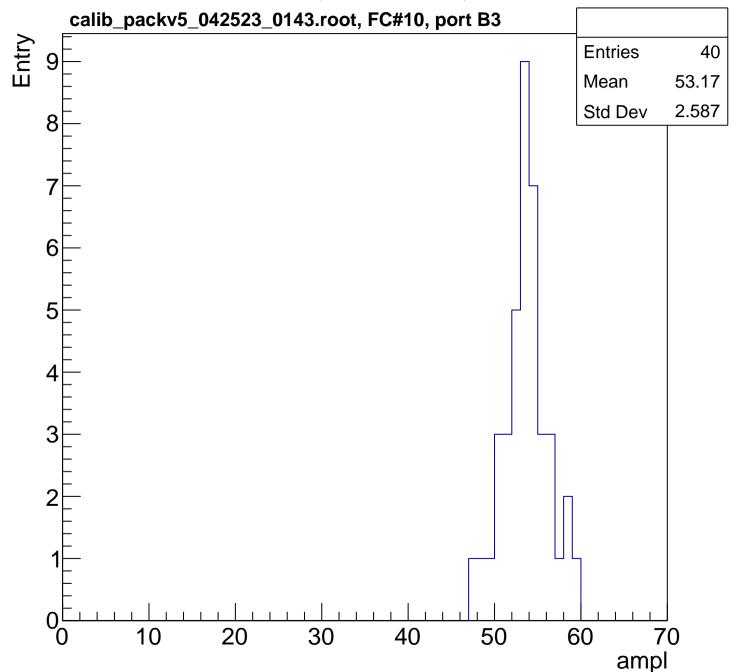


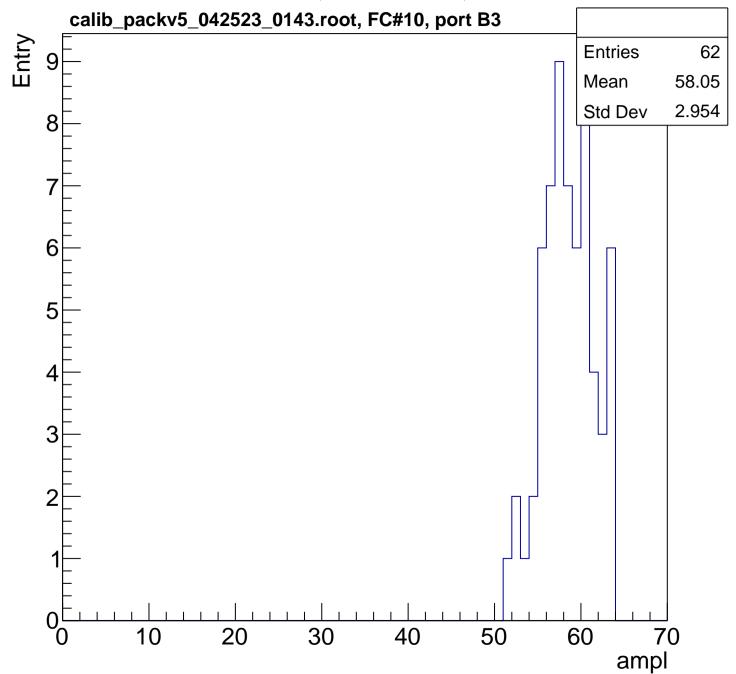


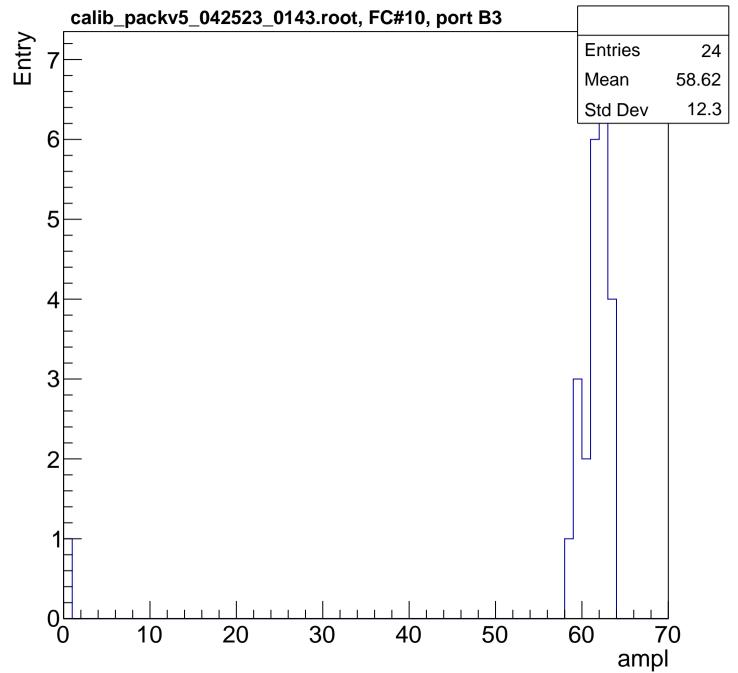




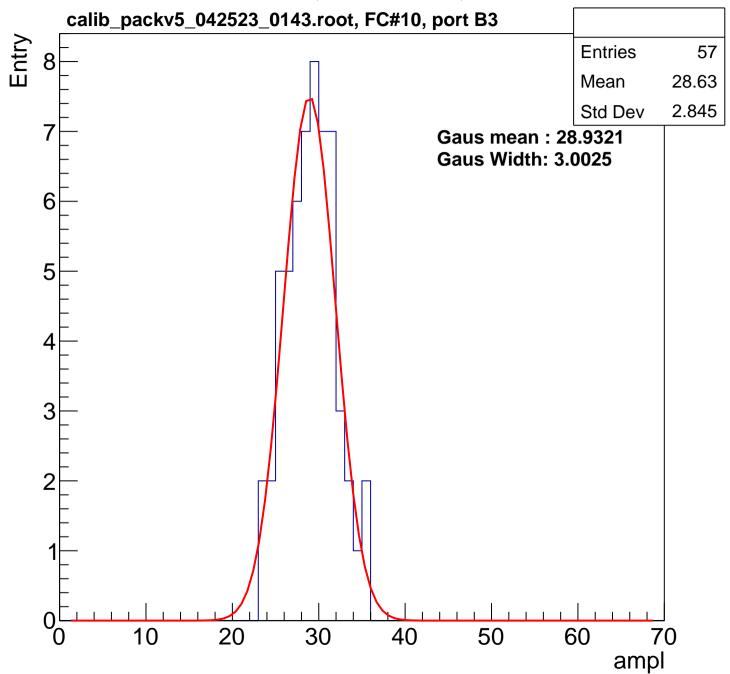


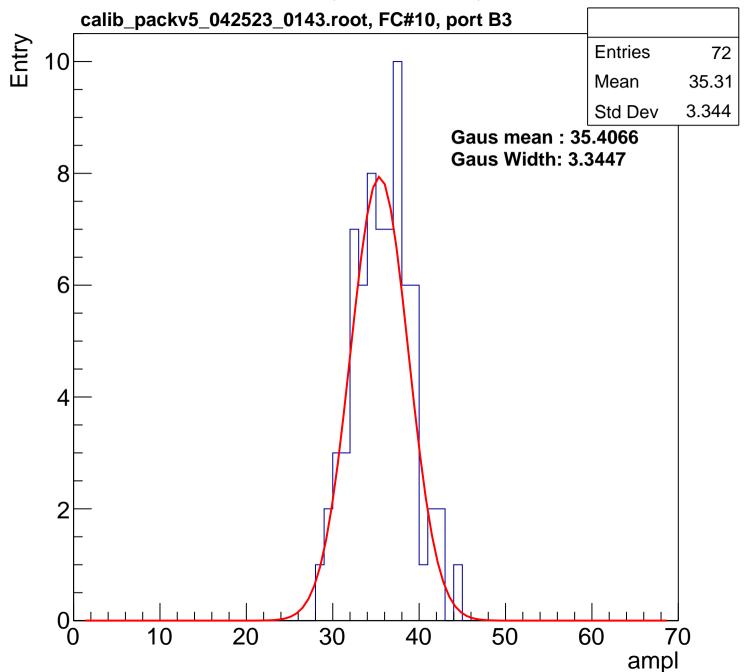


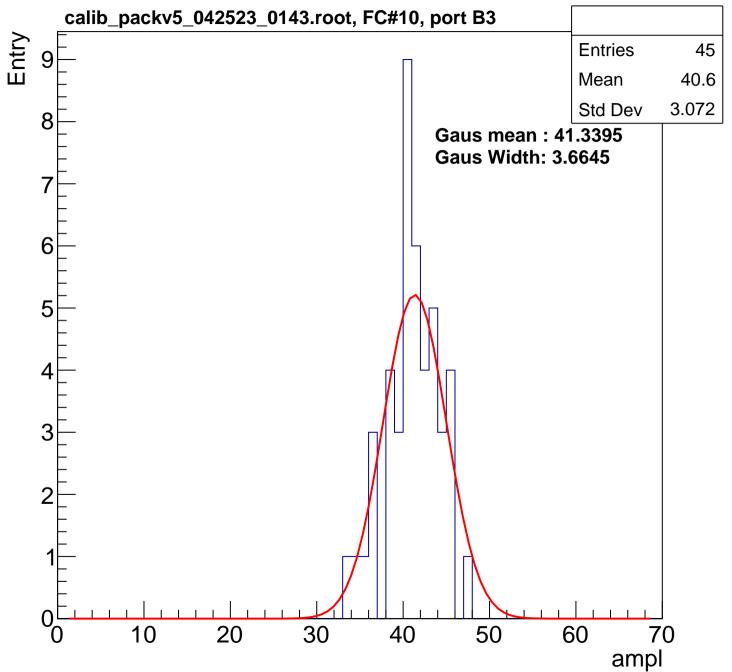


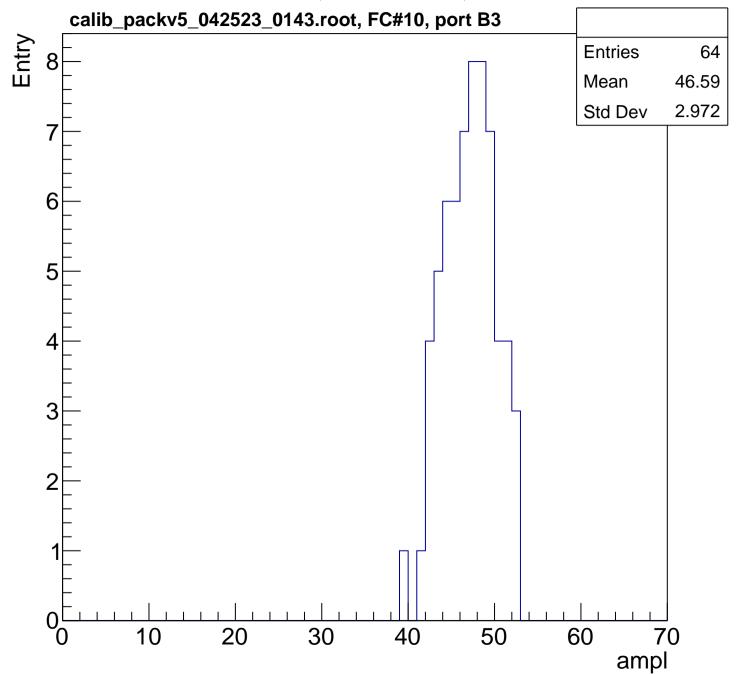


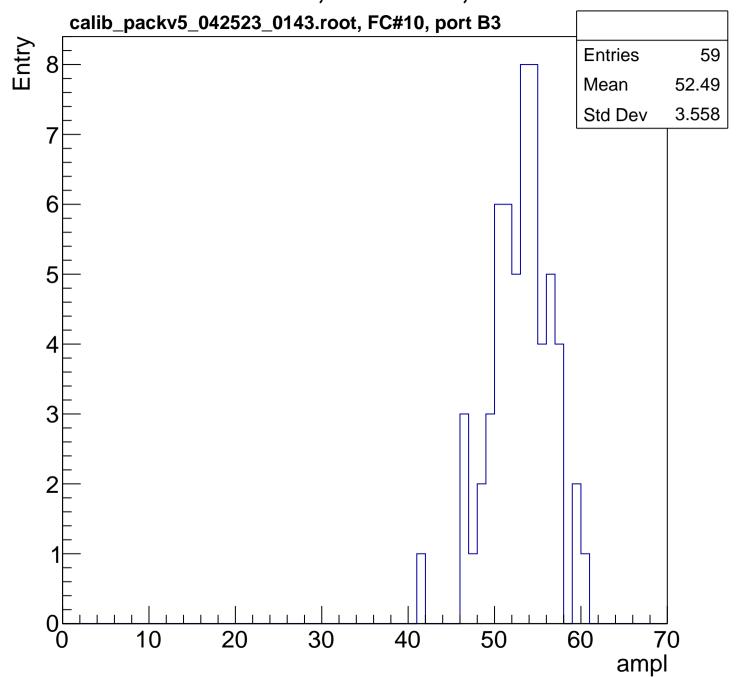
B1L002S, U4-ch17, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

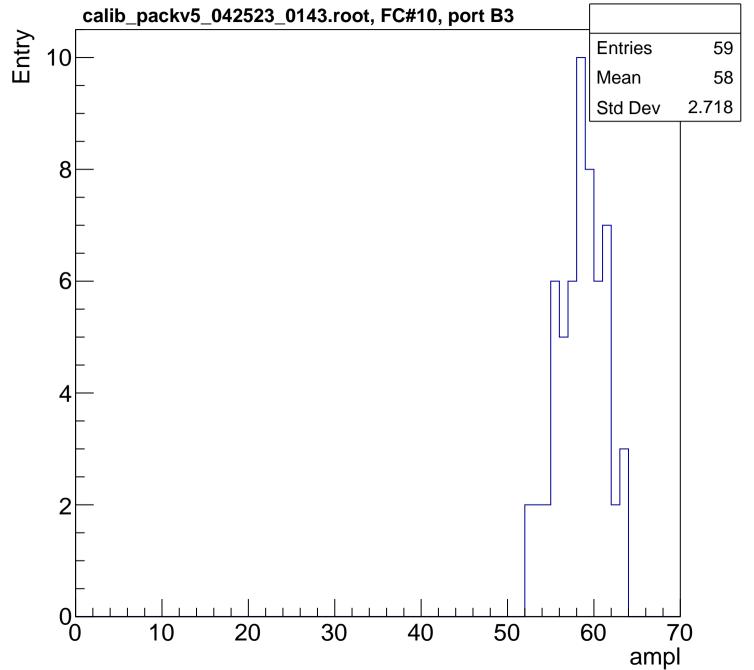


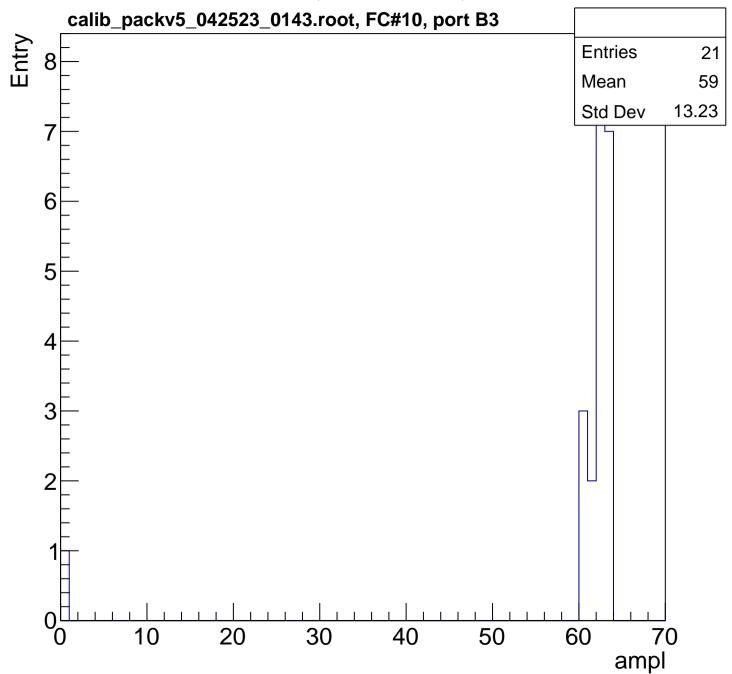


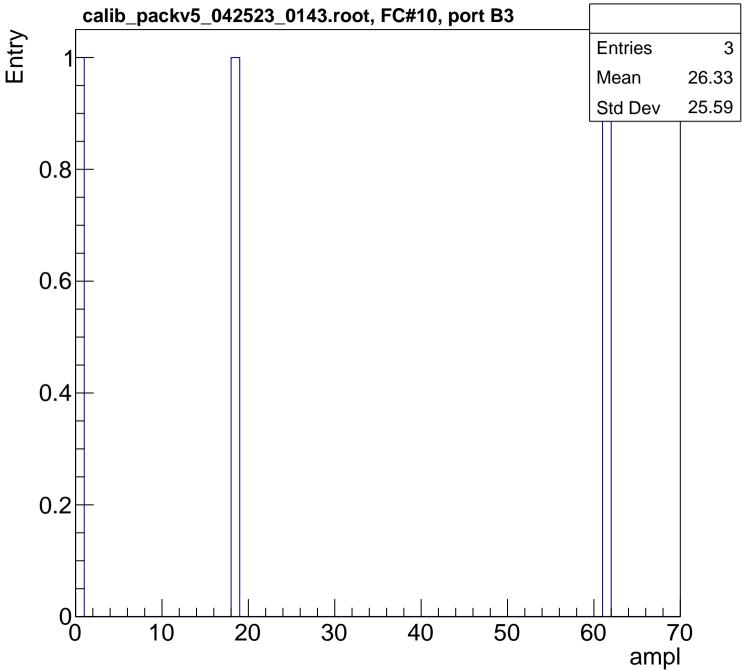


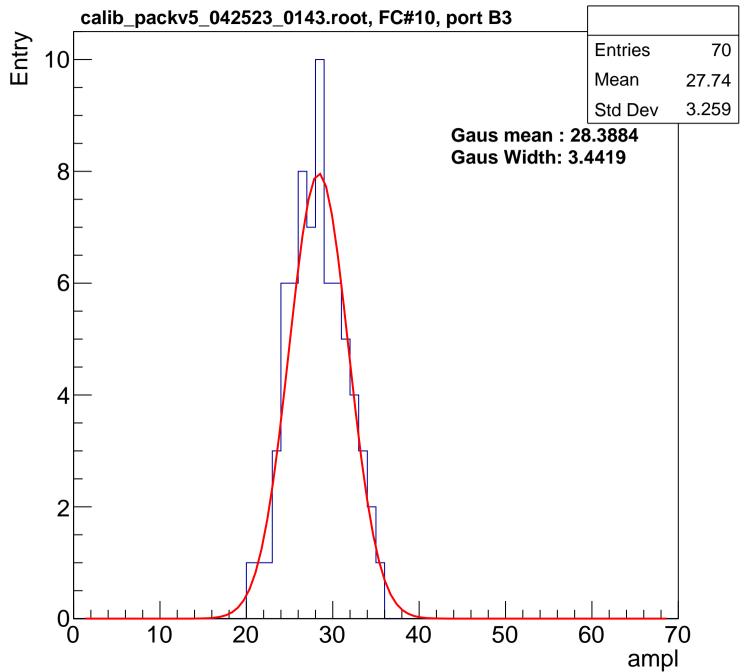


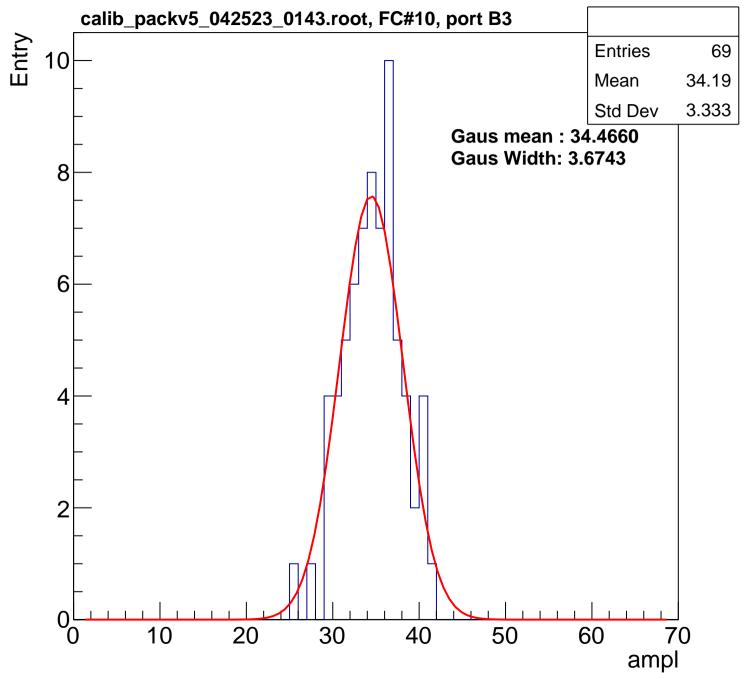


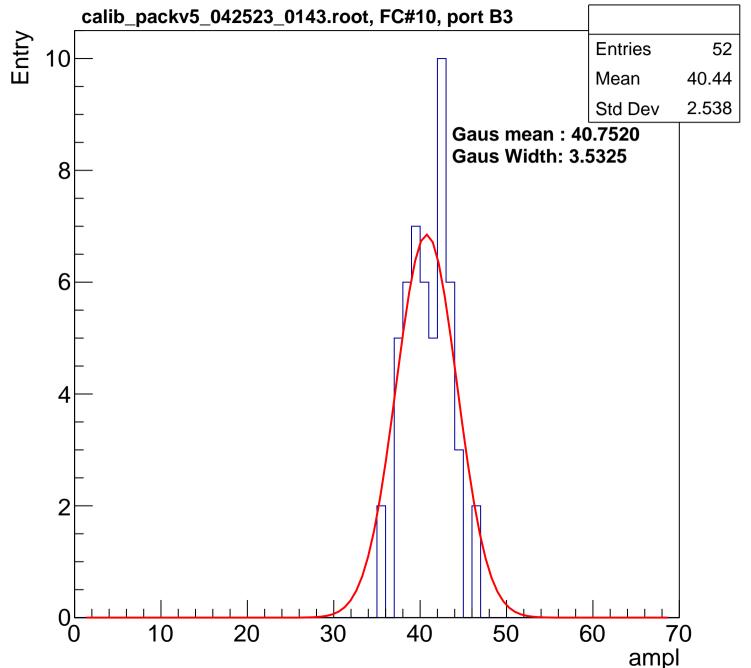


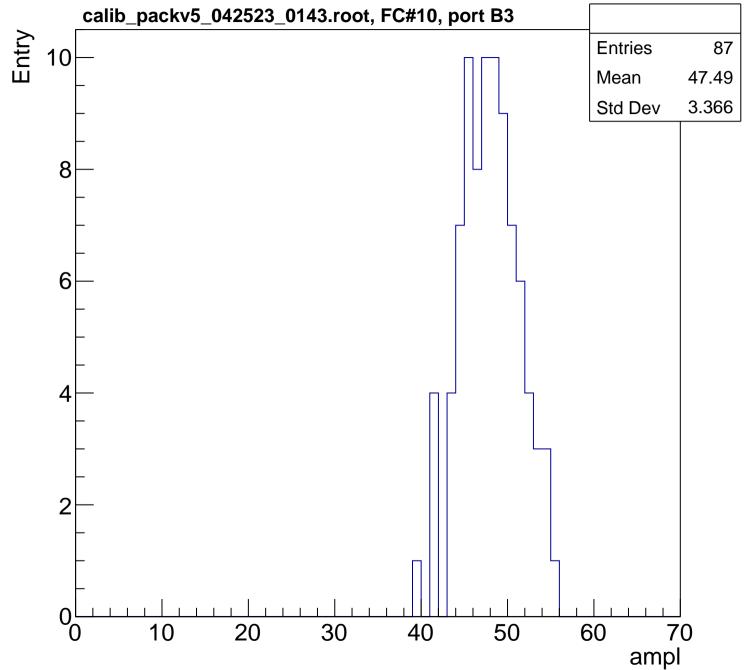


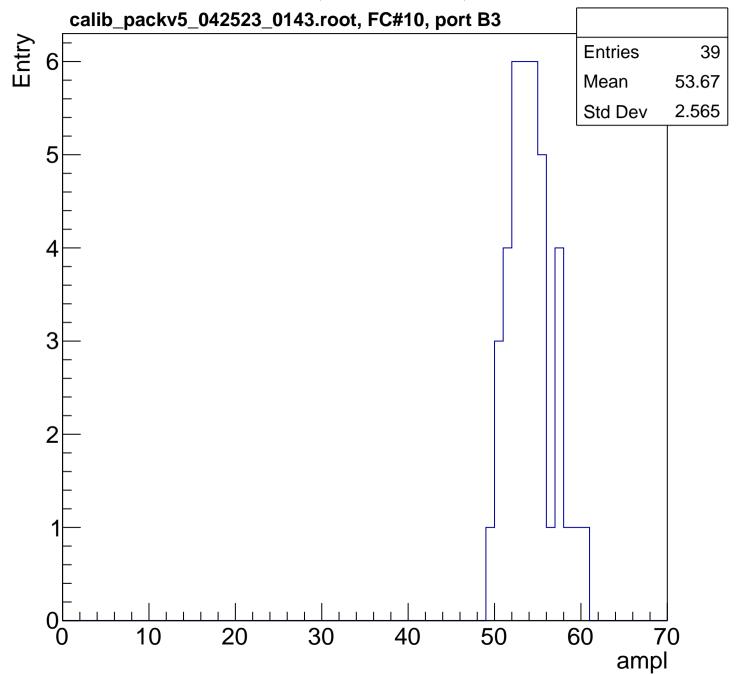


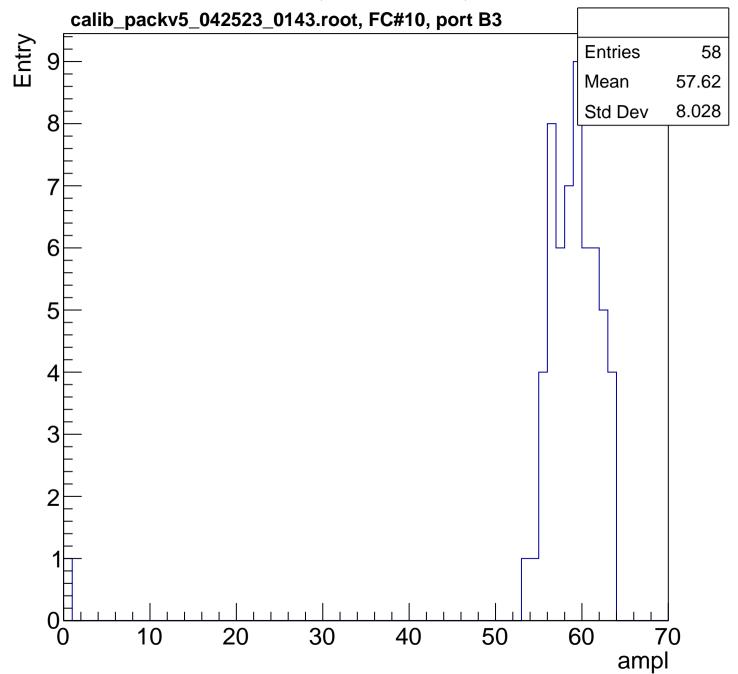


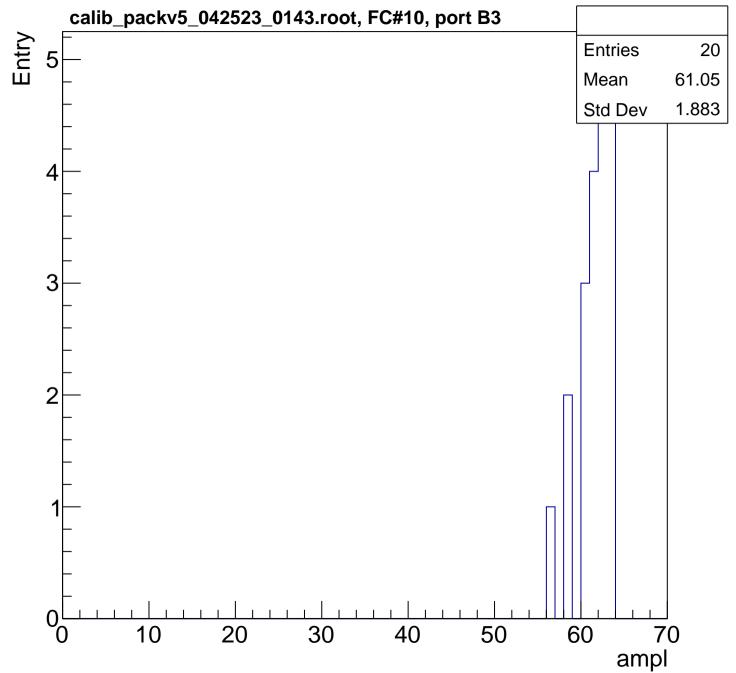


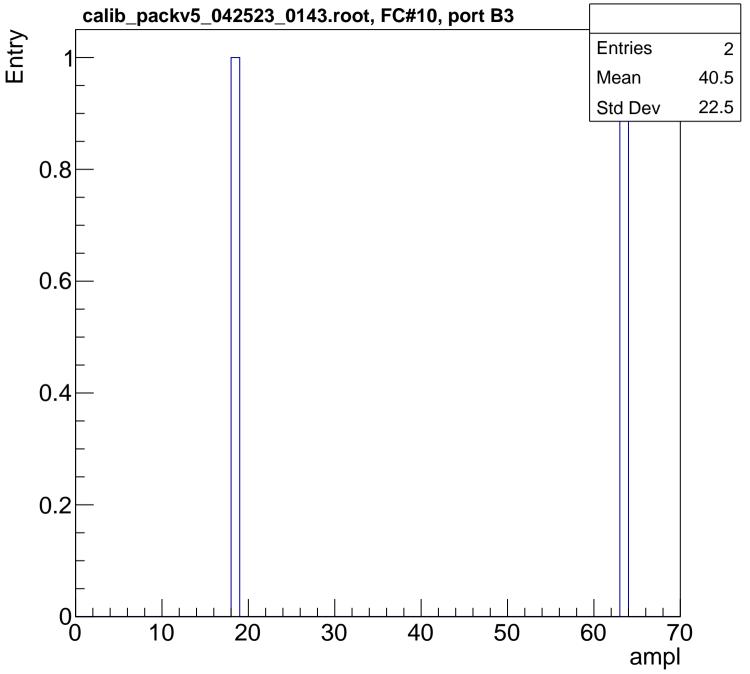


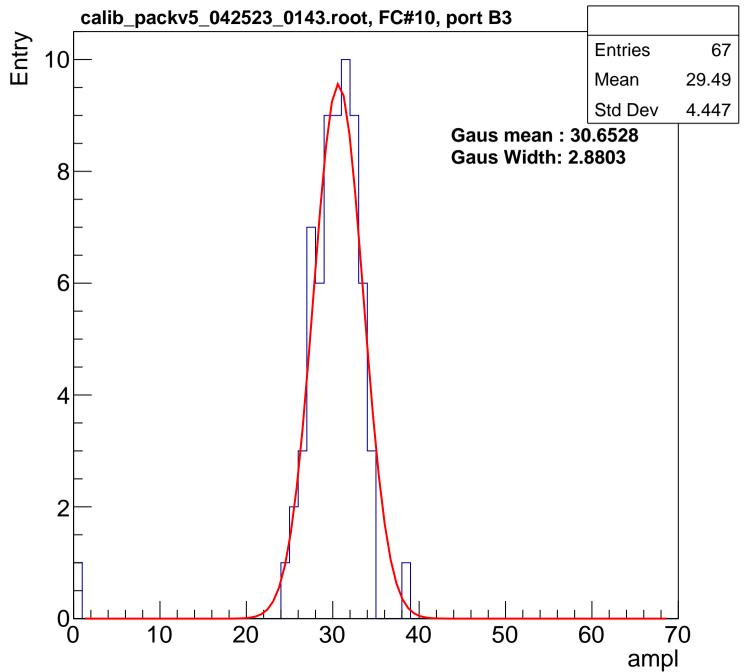


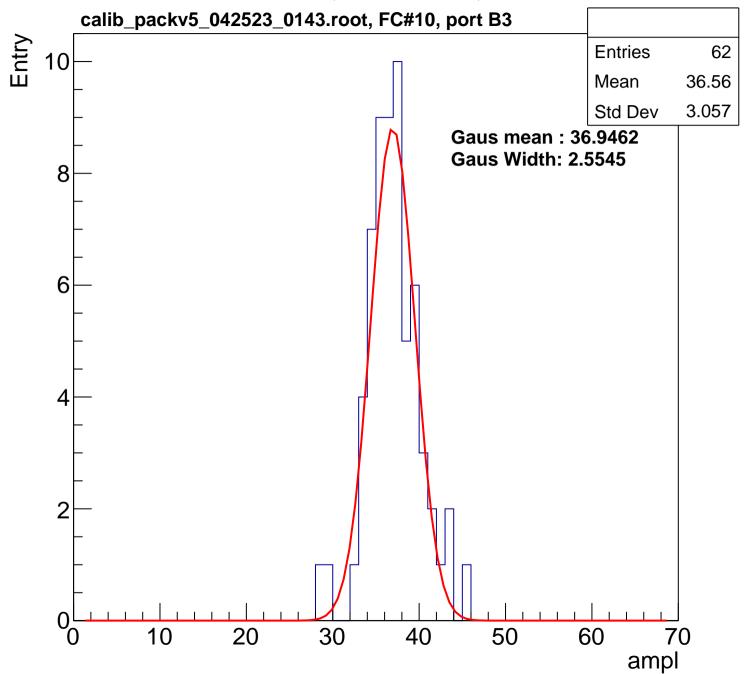


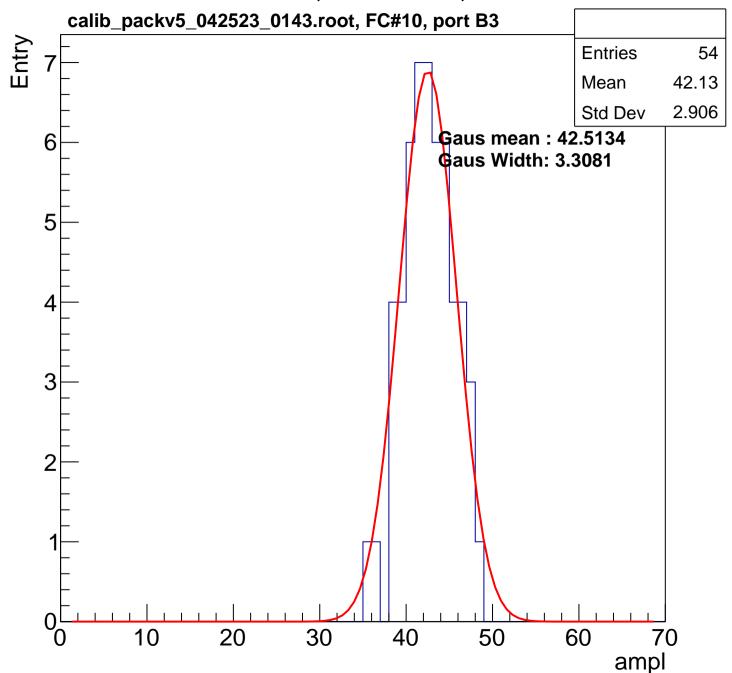


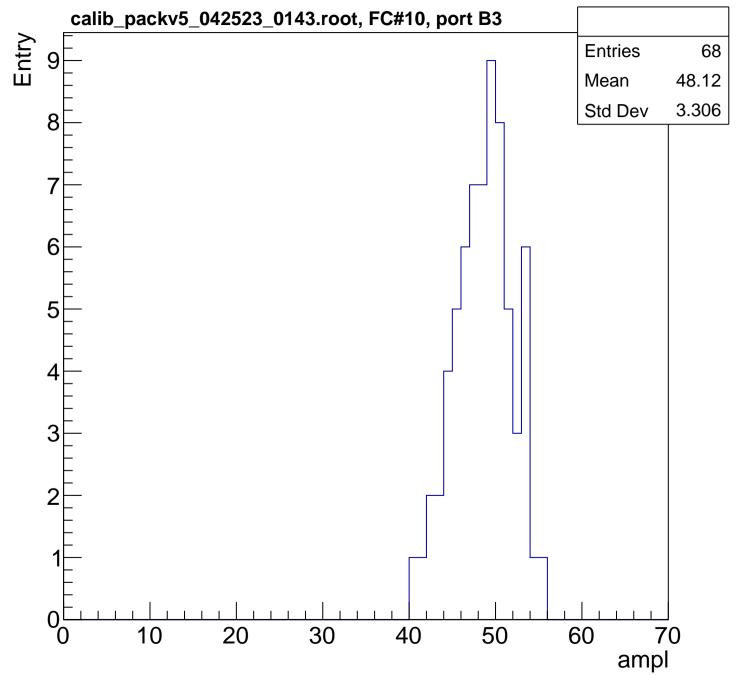


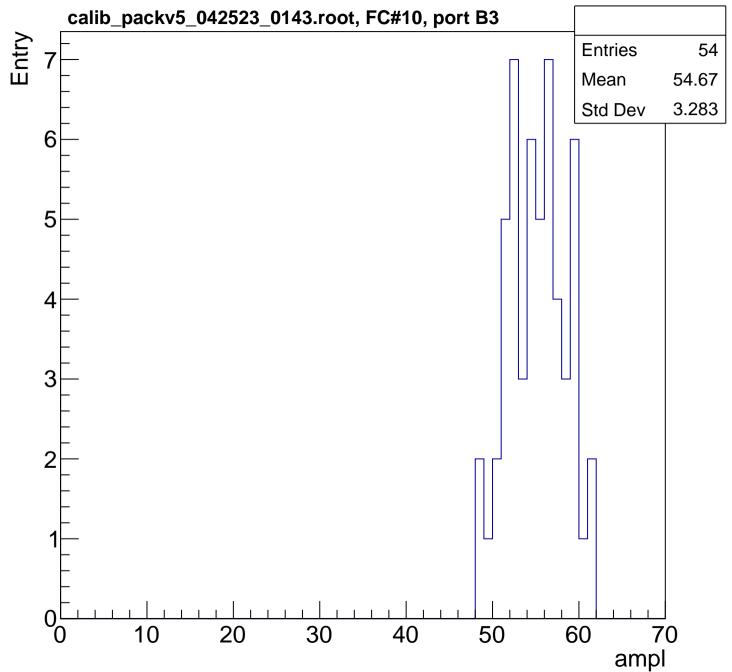


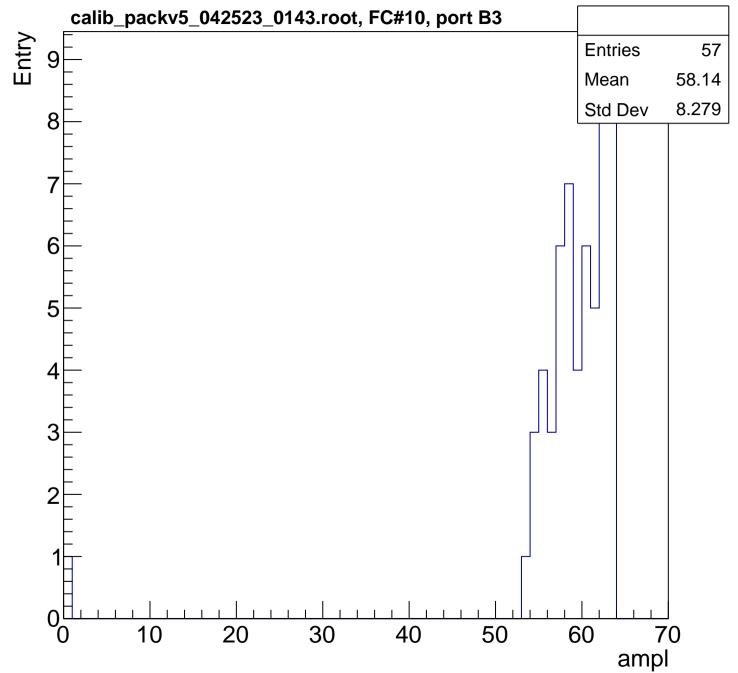


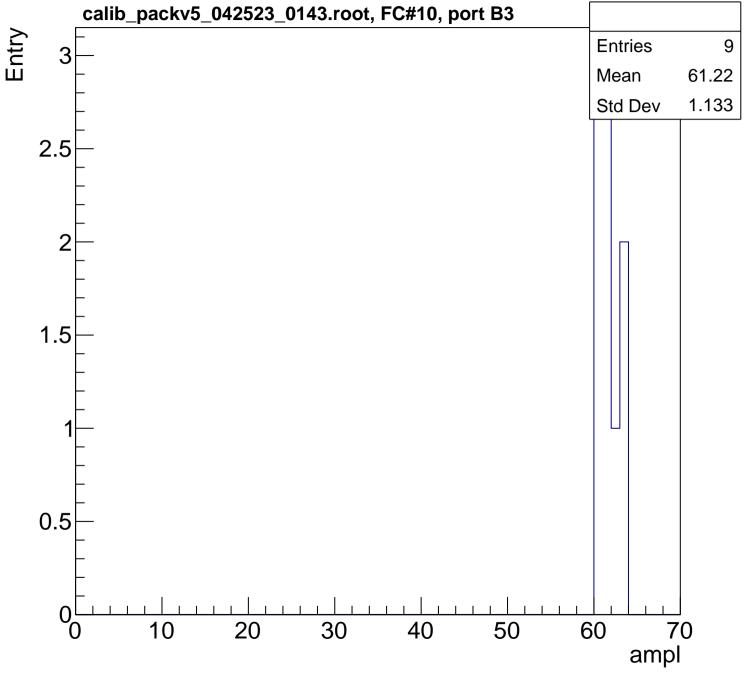




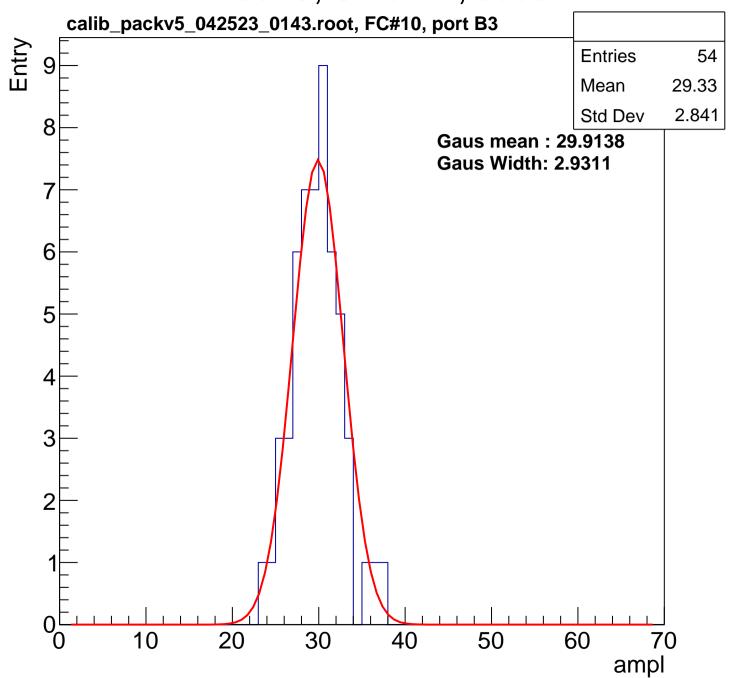


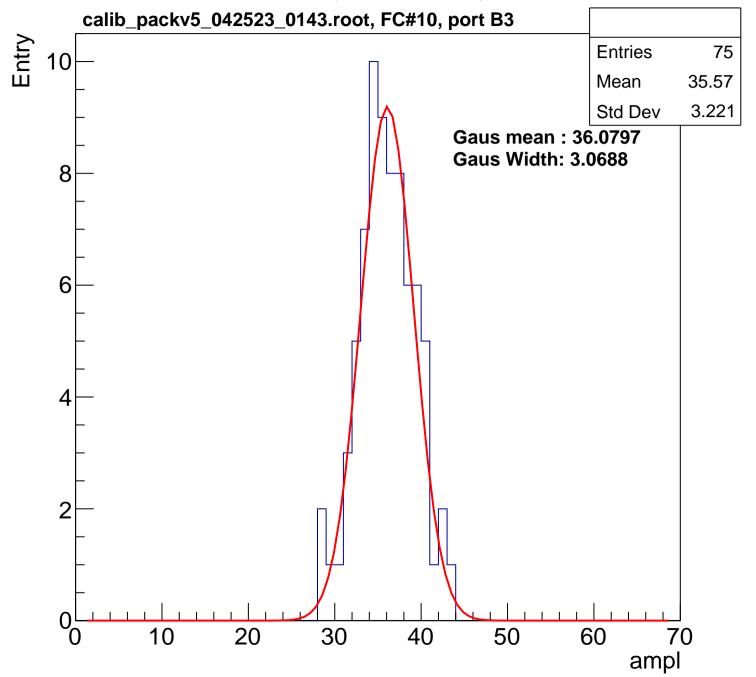


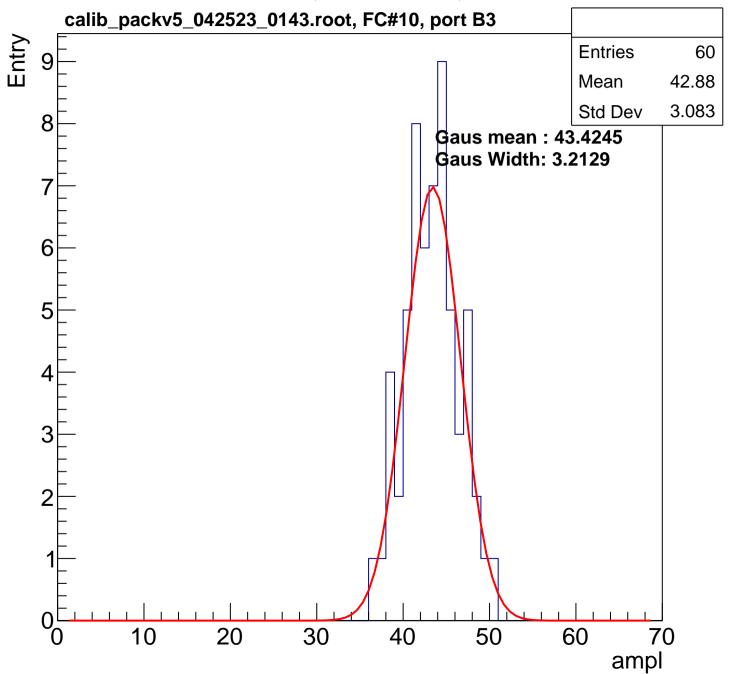


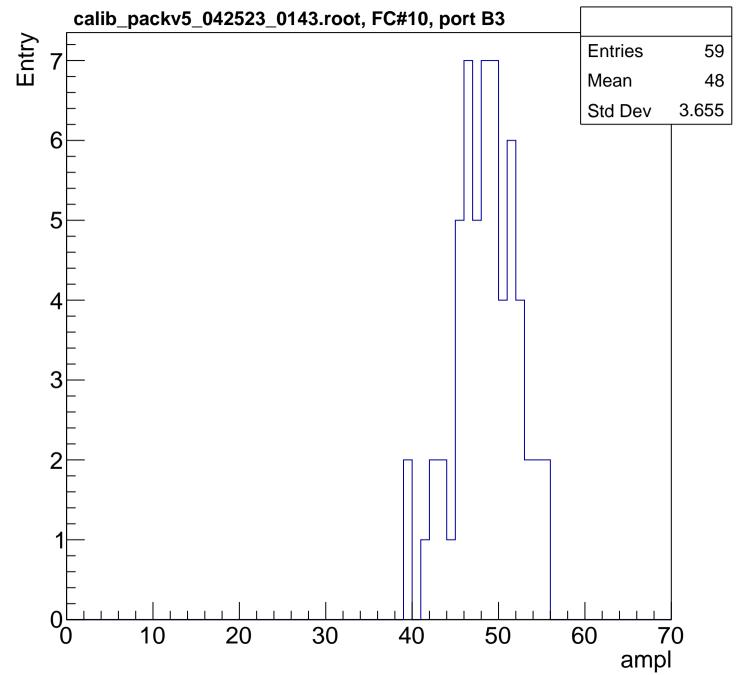


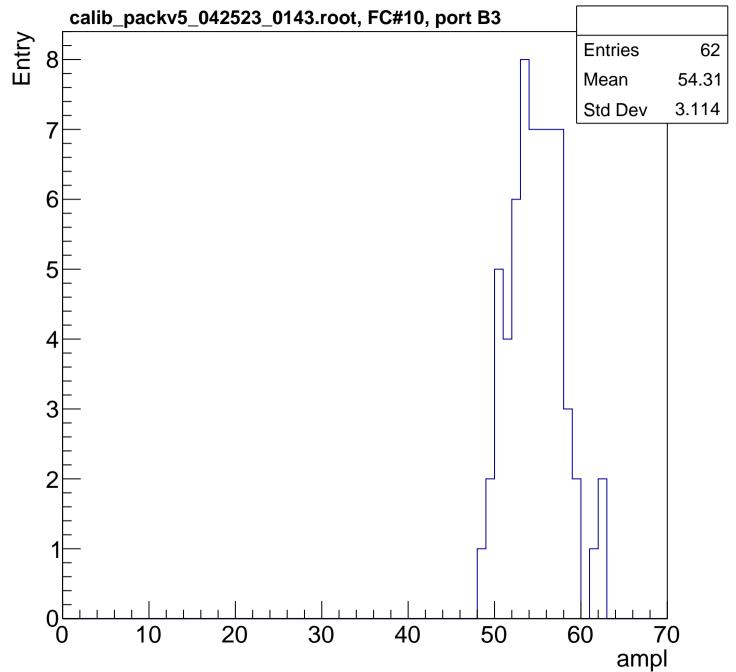
B1L002S, U4-ch20, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

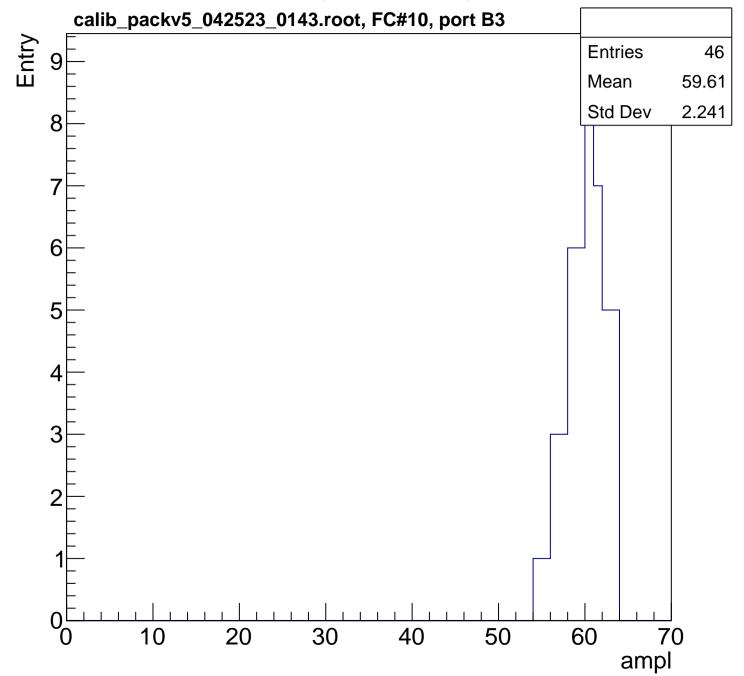


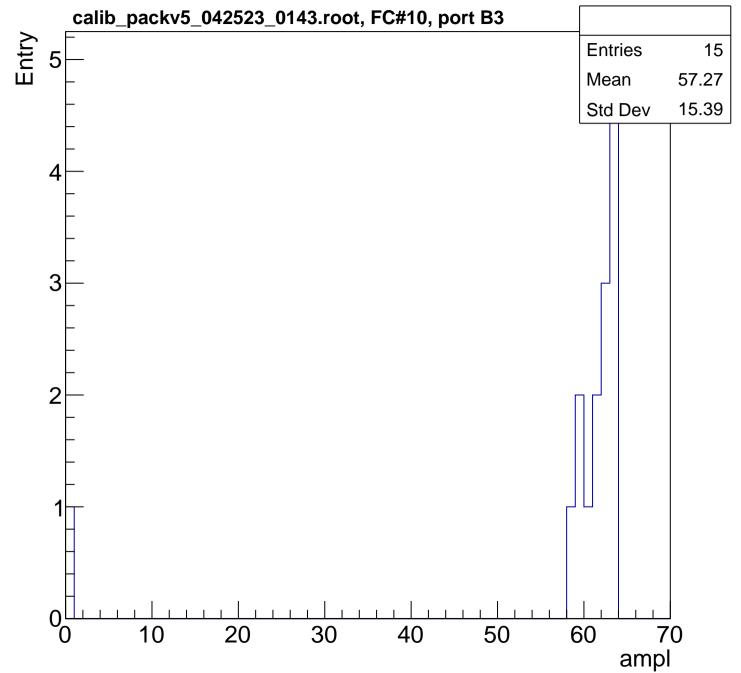




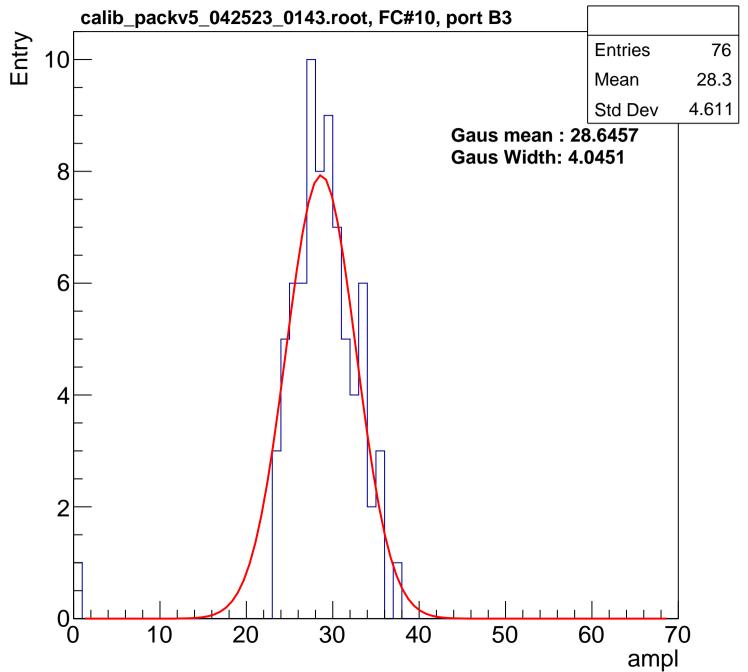


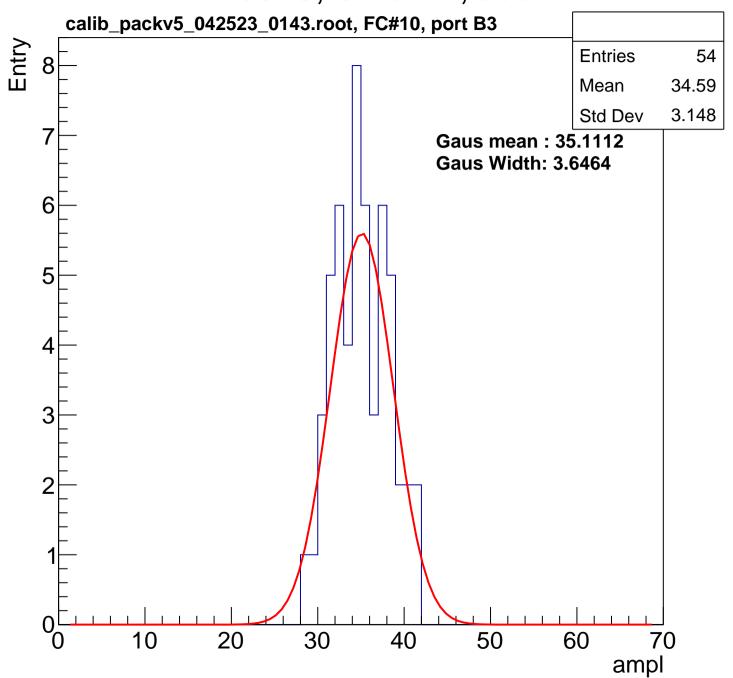


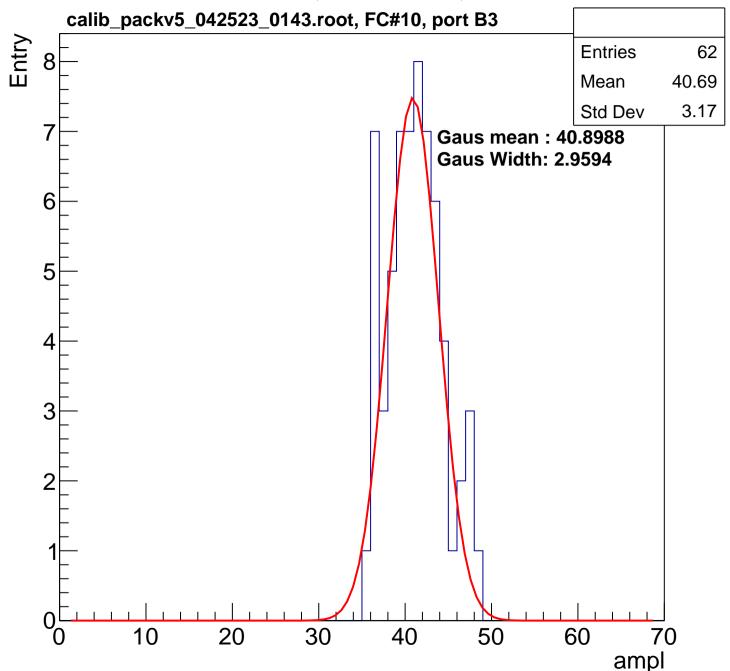


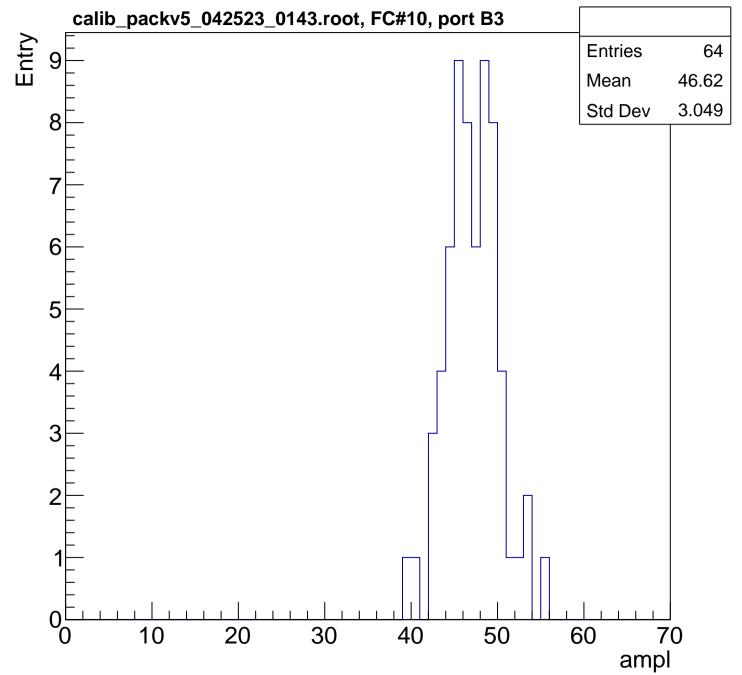


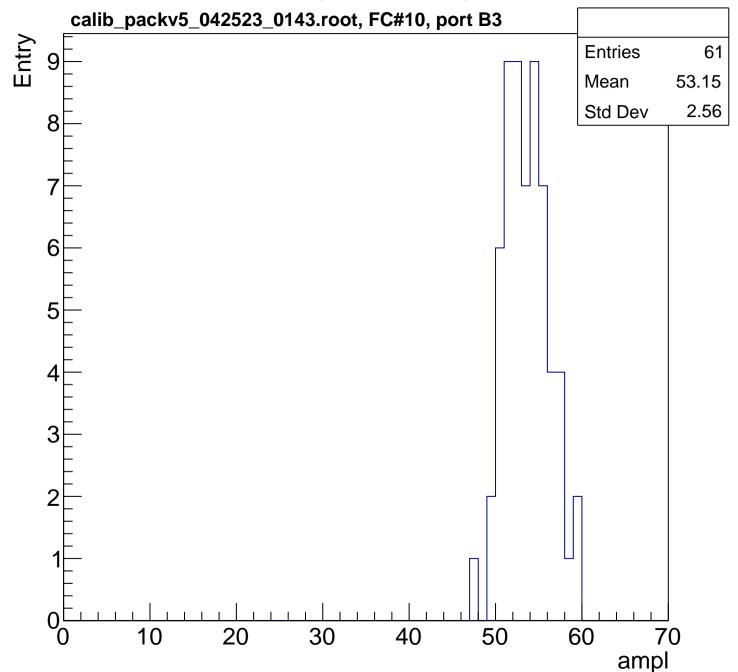
B1L002S, U4-ch21, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

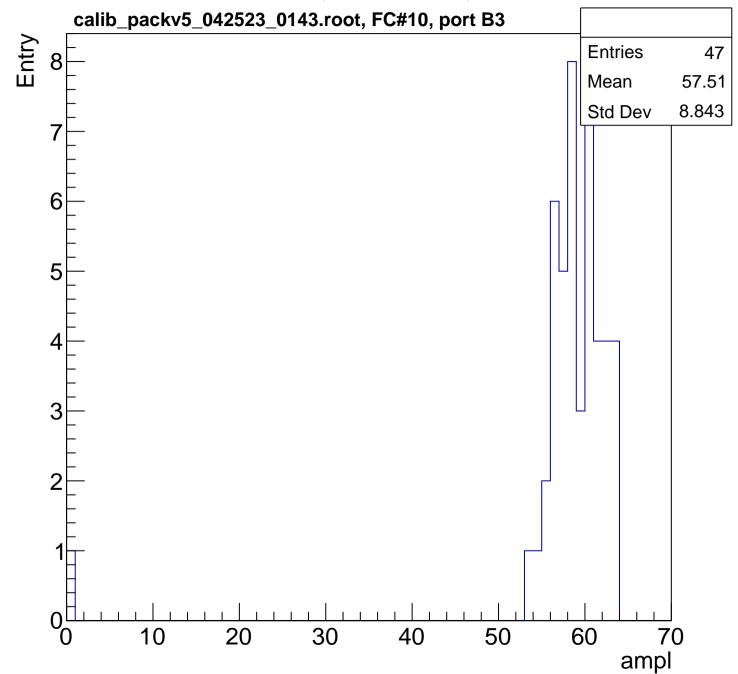


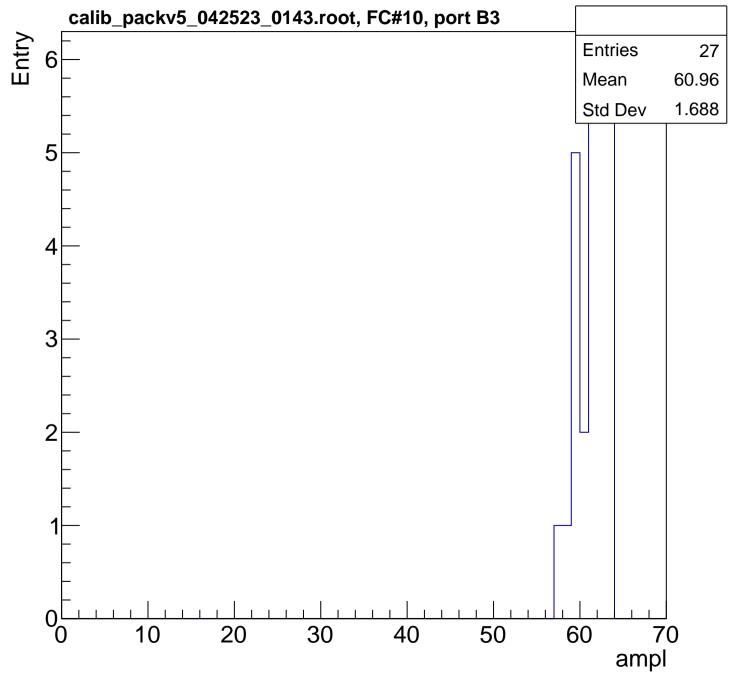




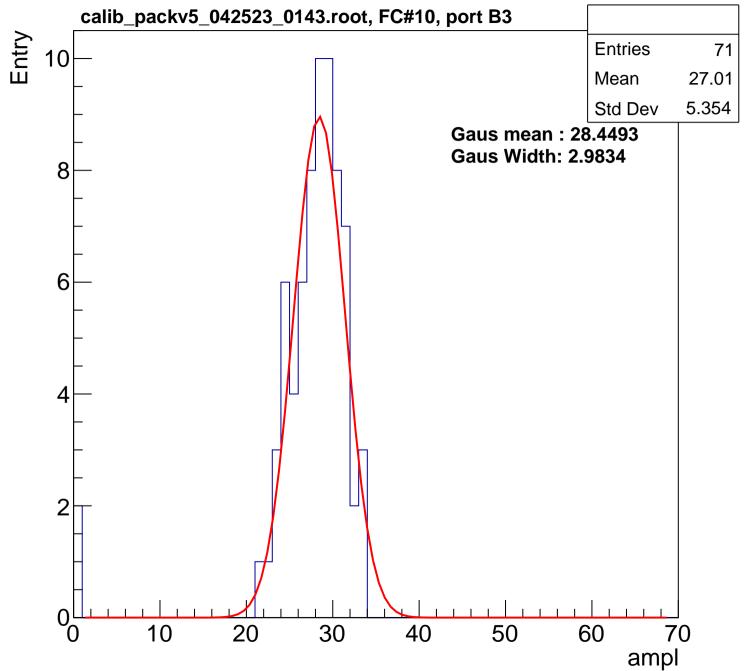


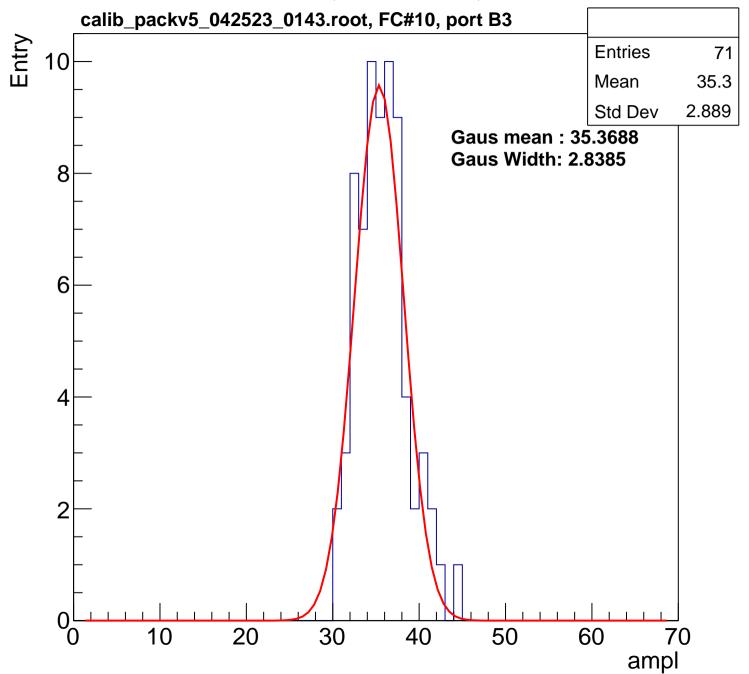


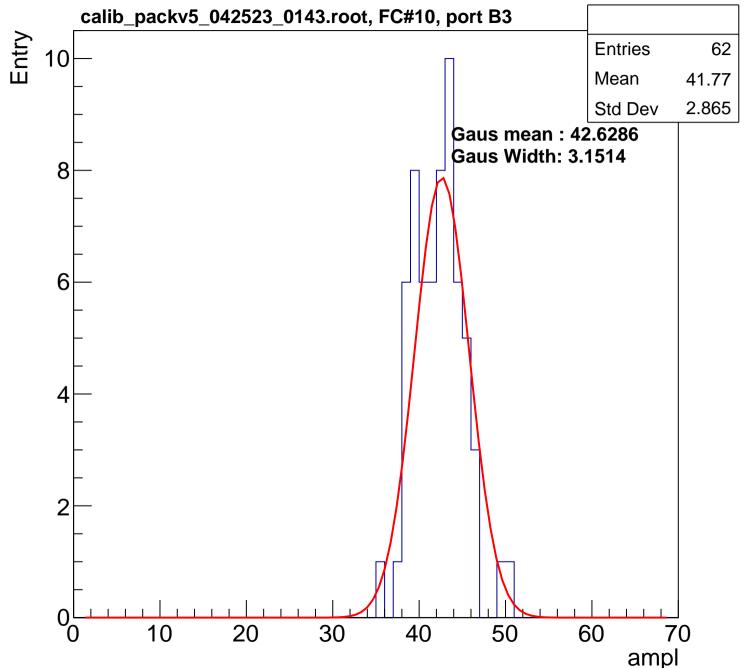


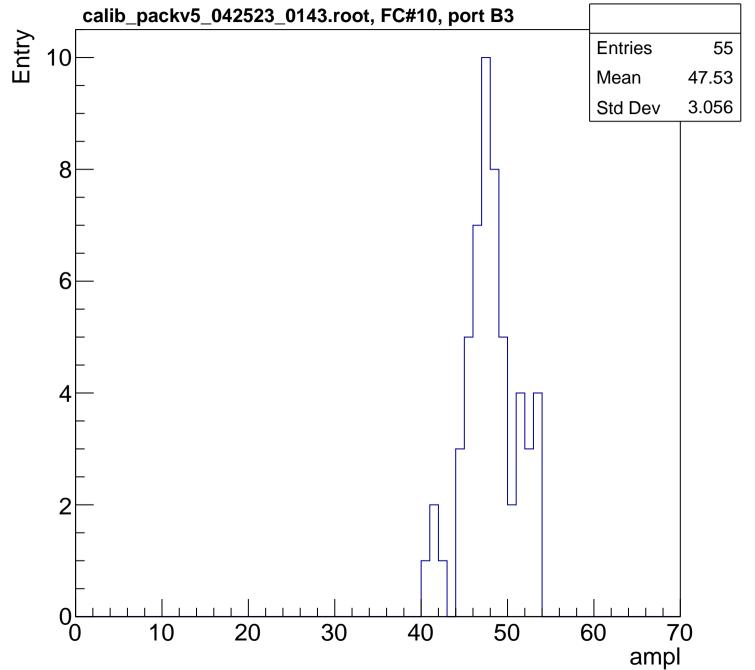


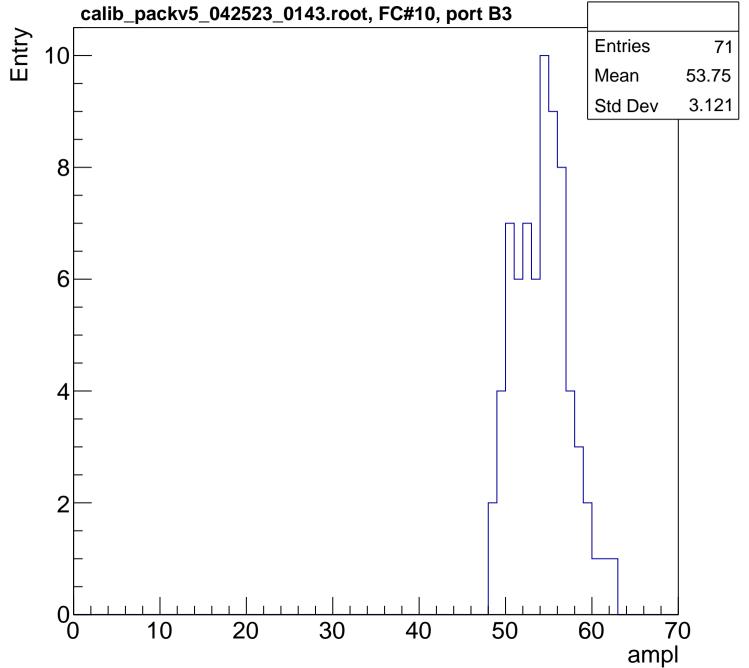
B1L002S, U4-ch22, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

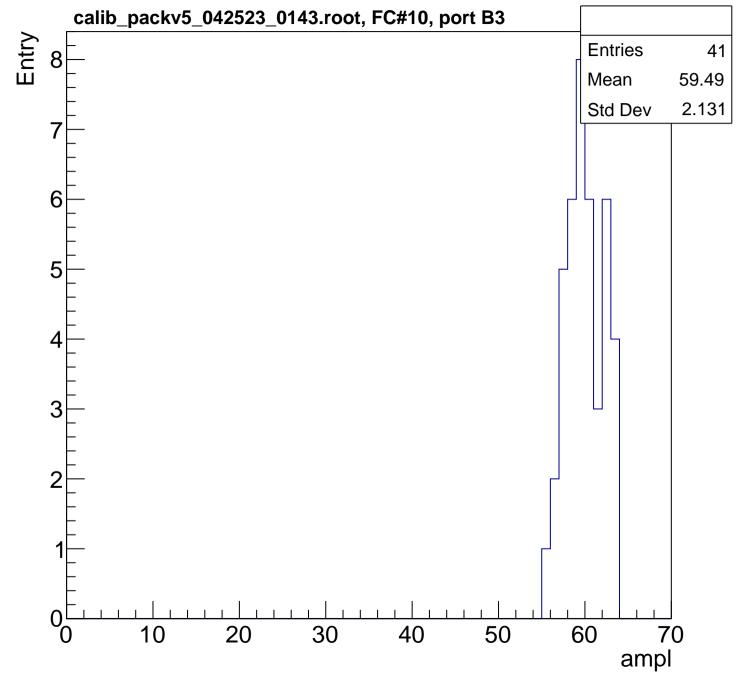


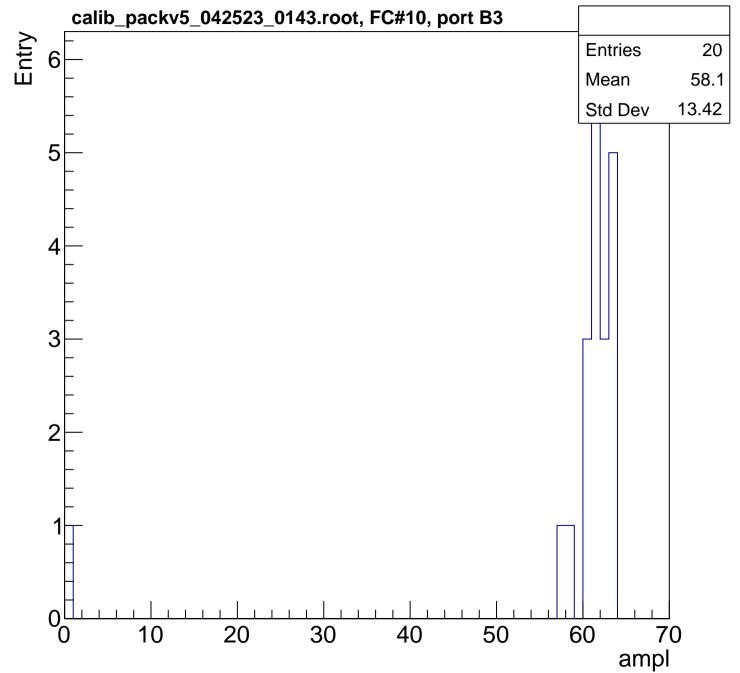


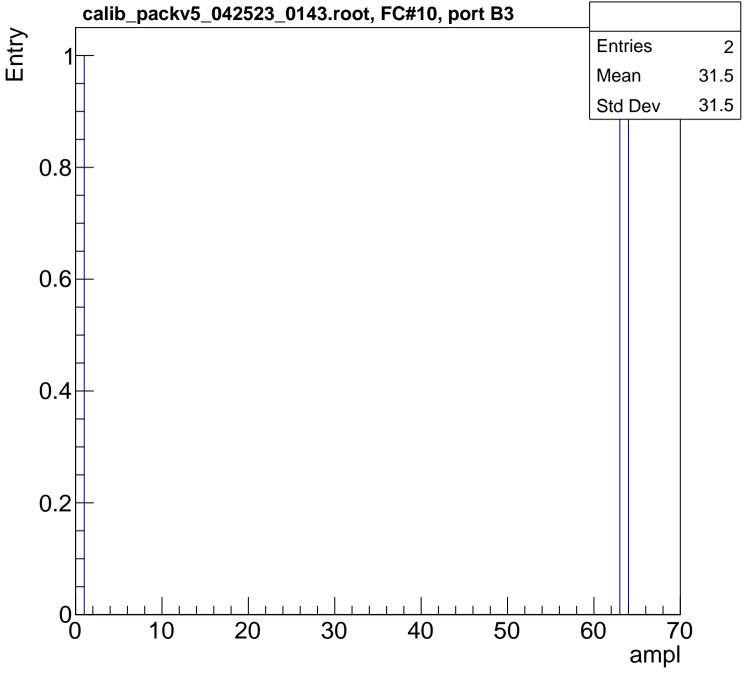


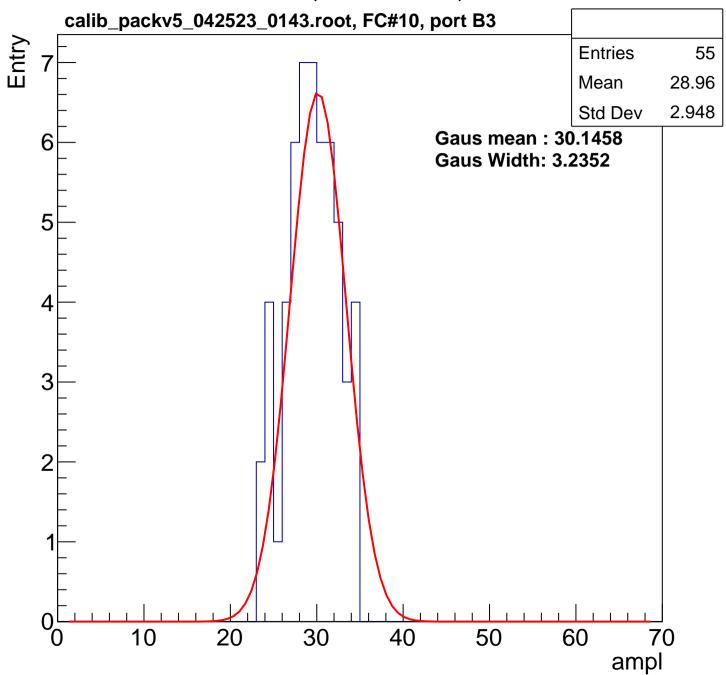


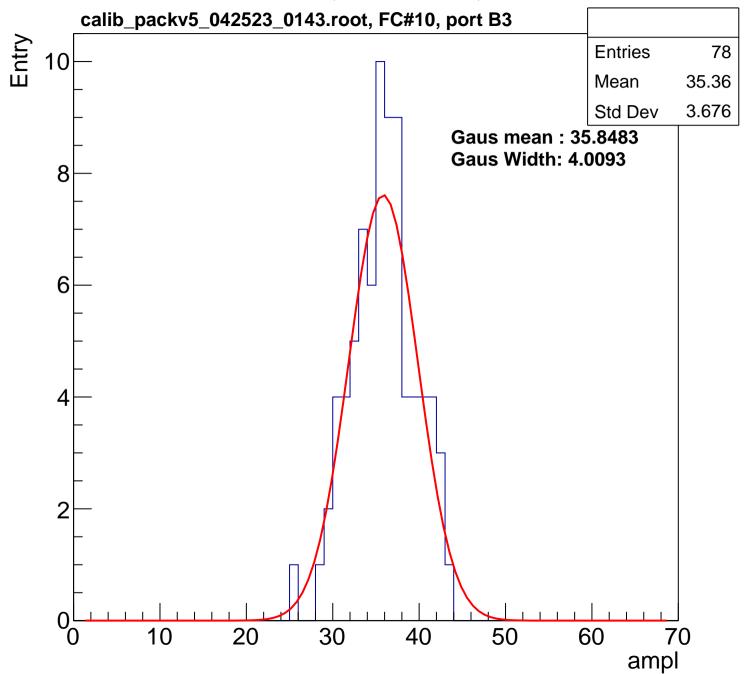


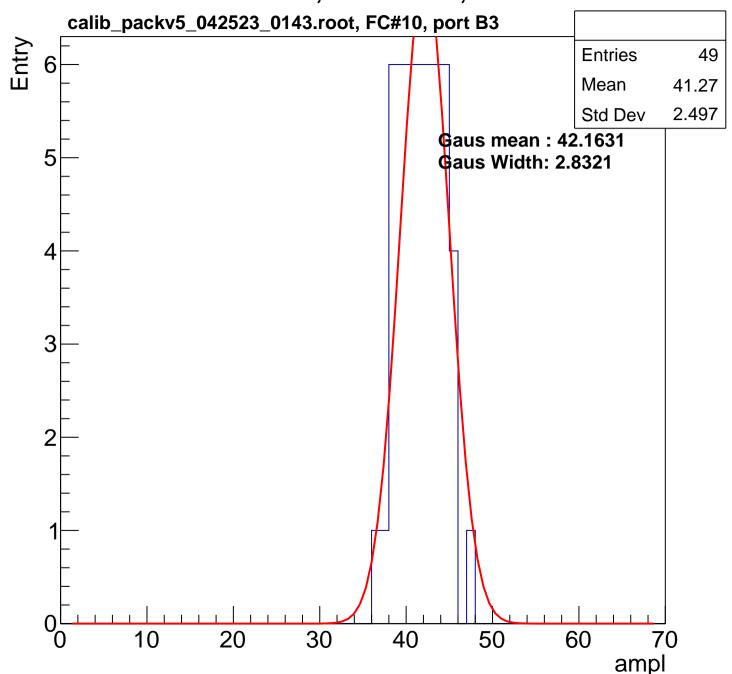


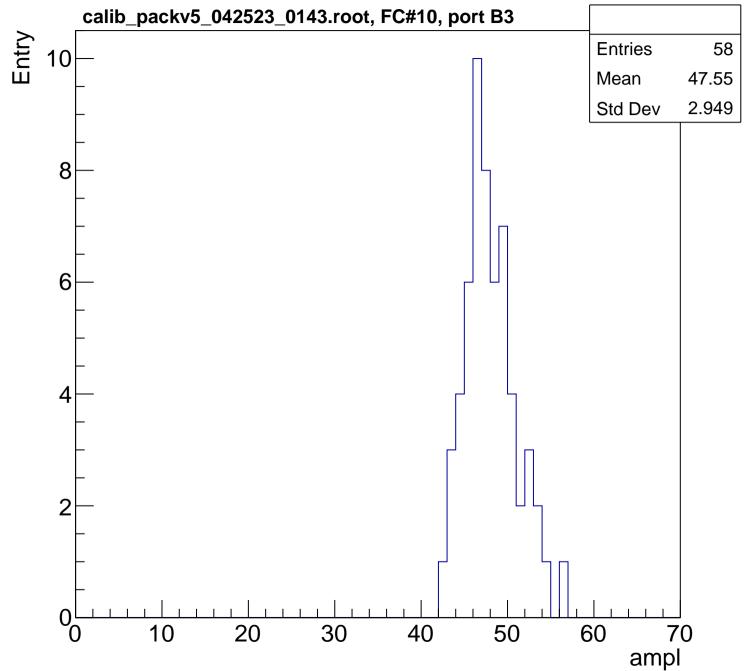


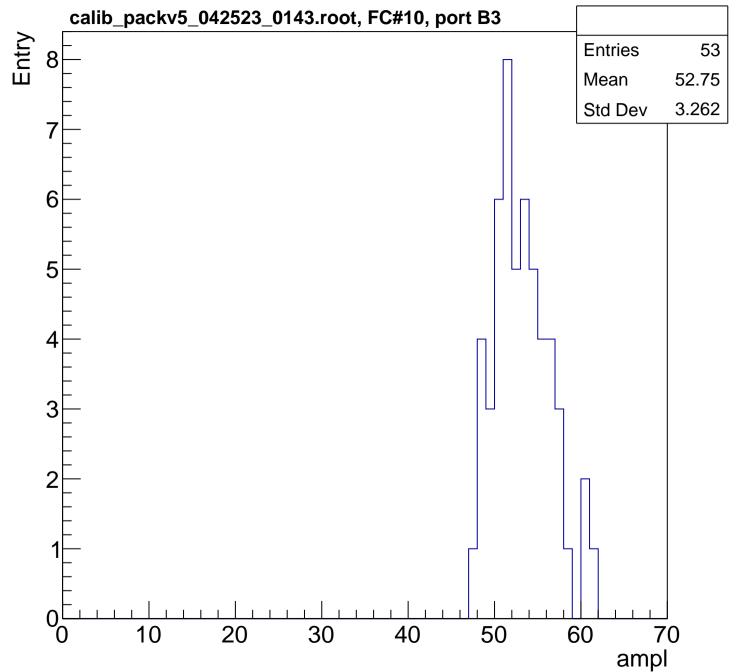


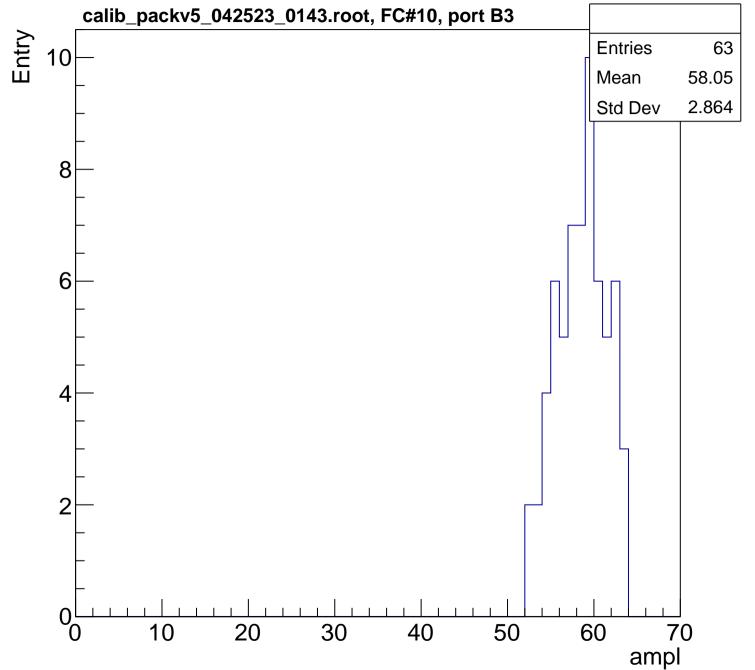


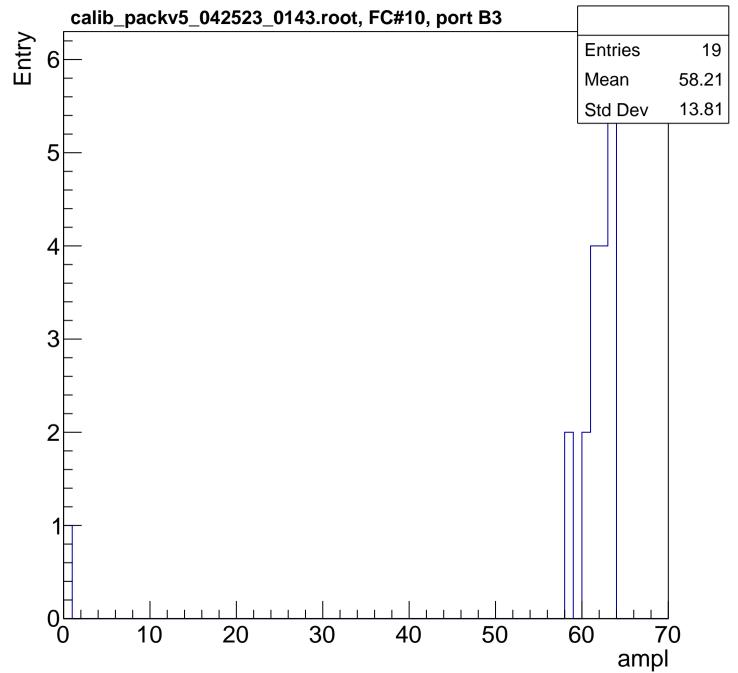


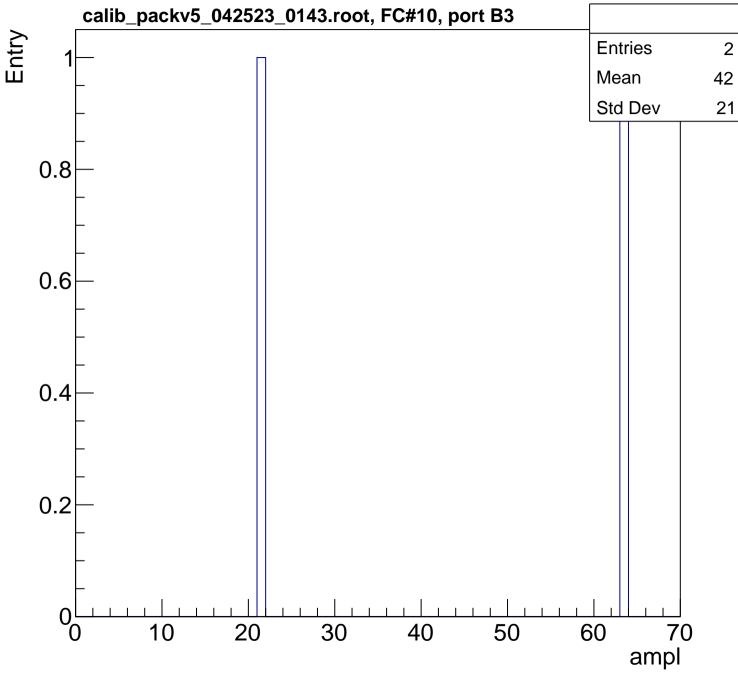


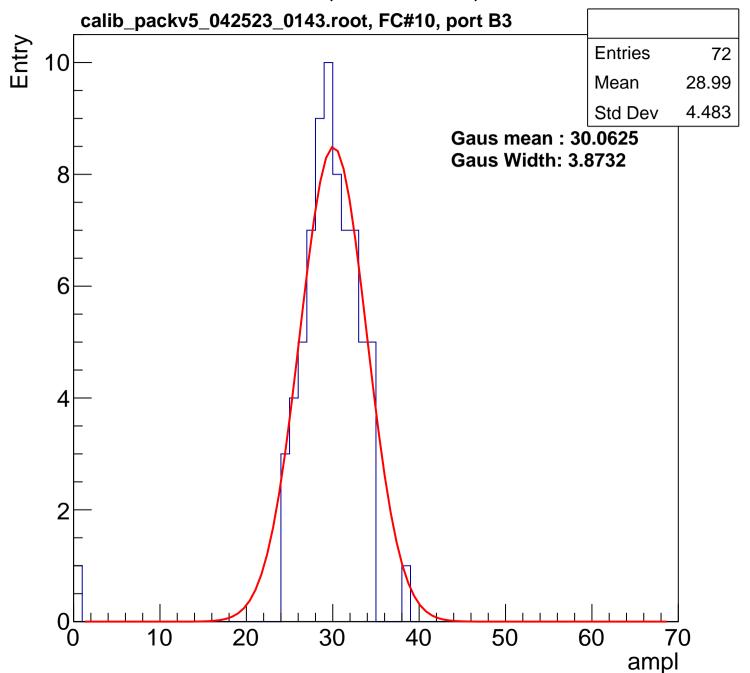


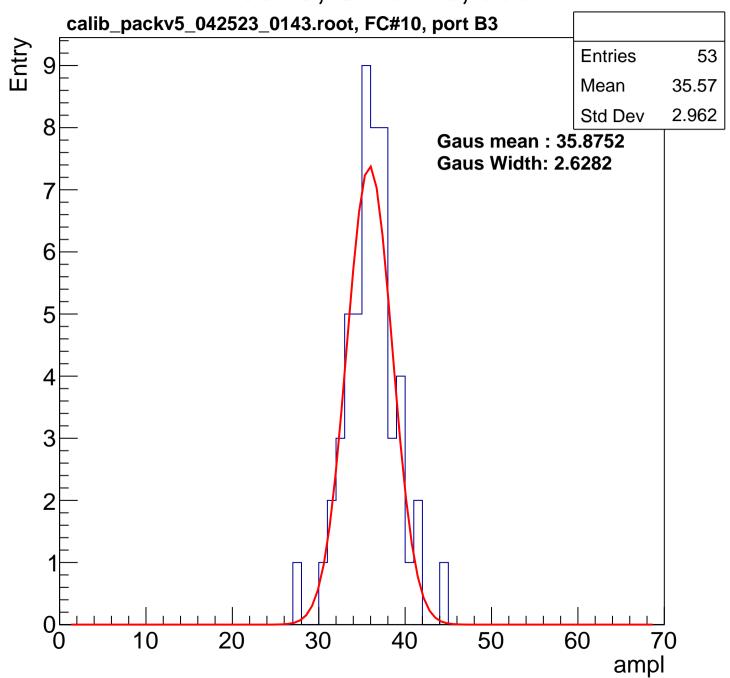


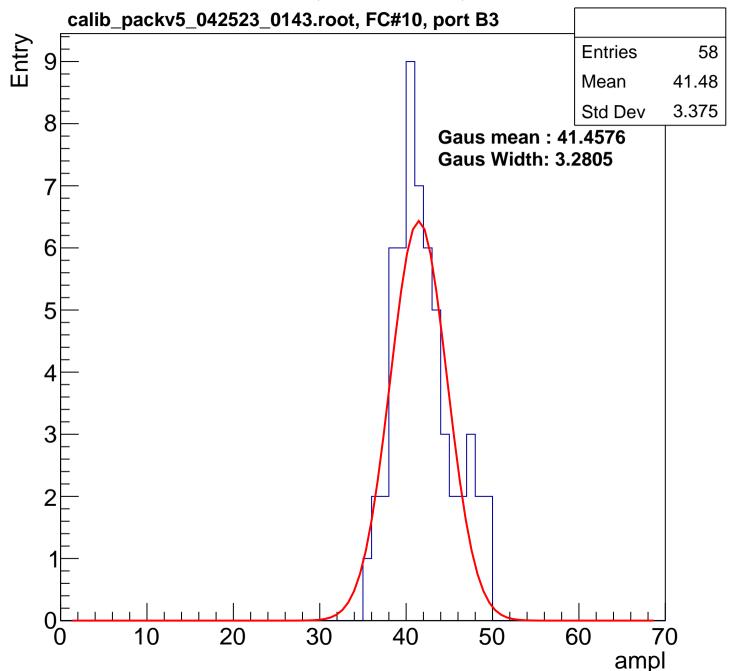


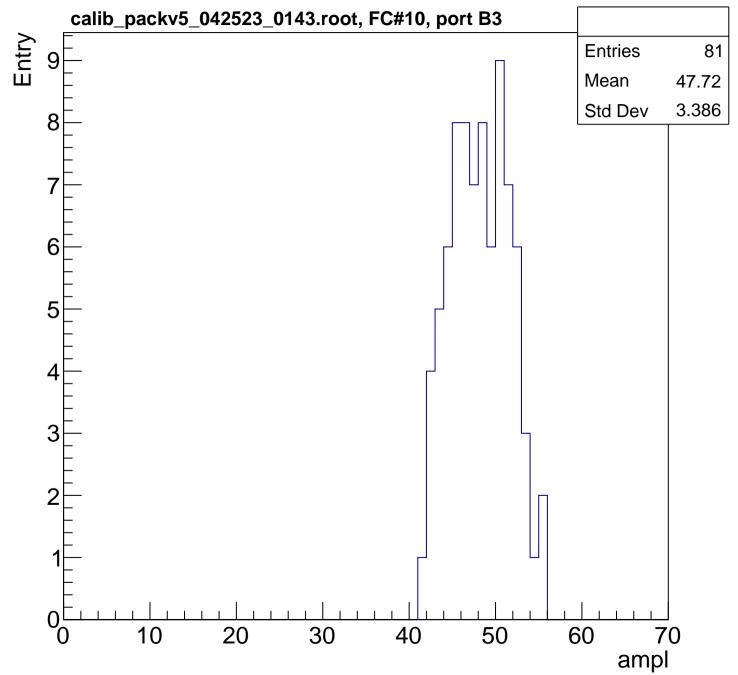


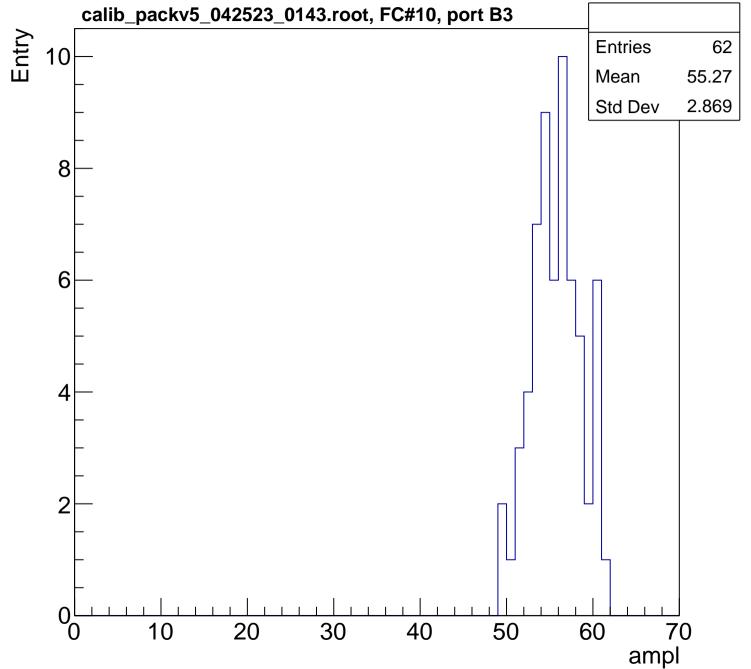


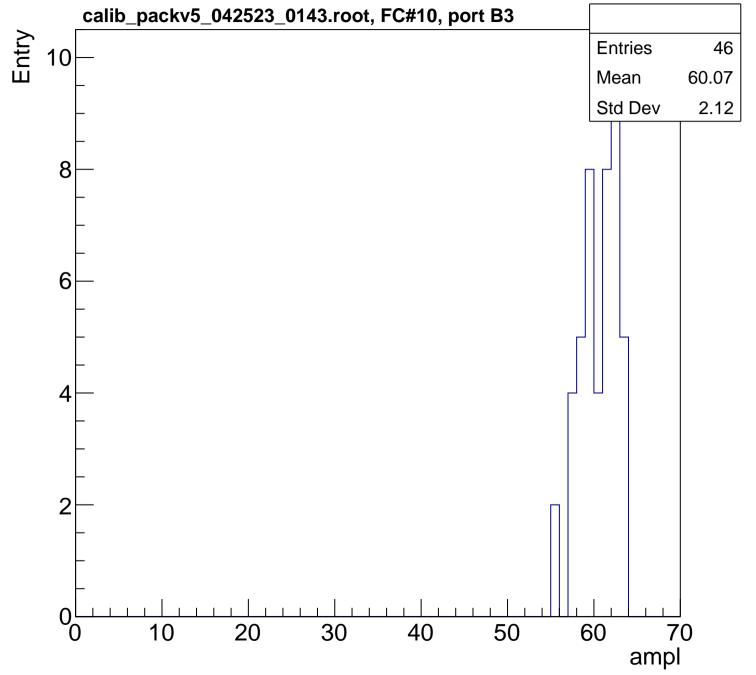


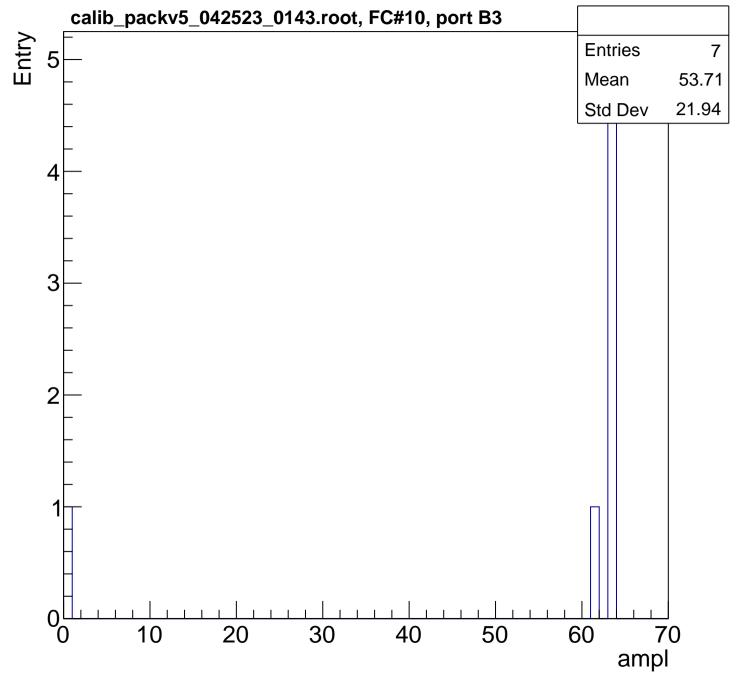




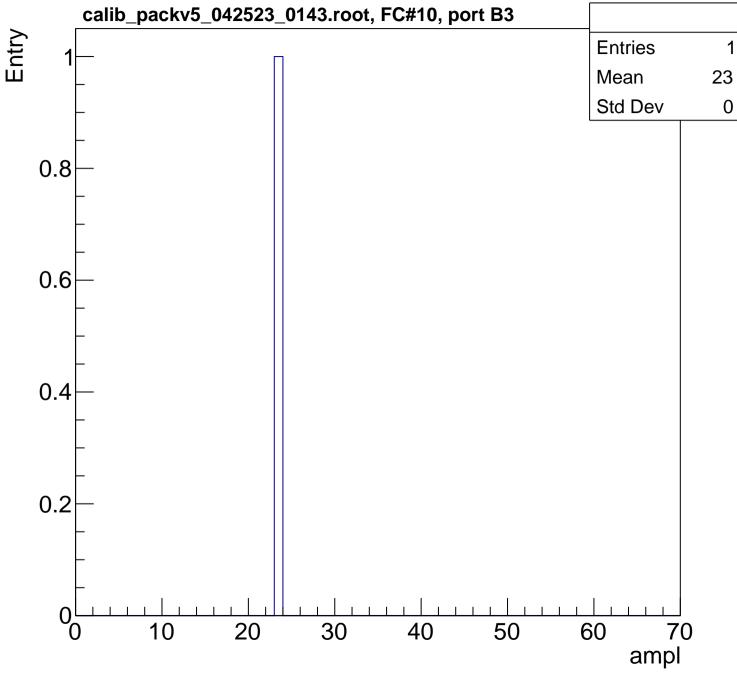


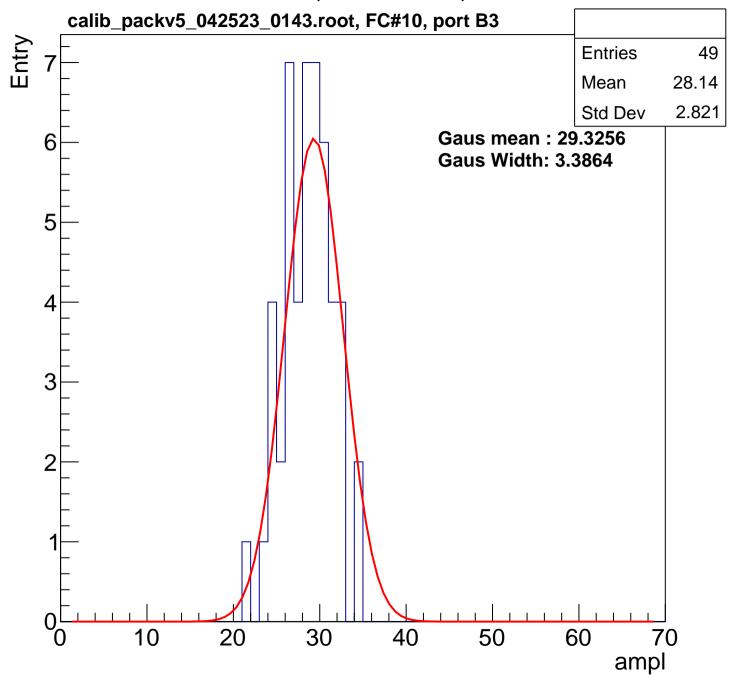


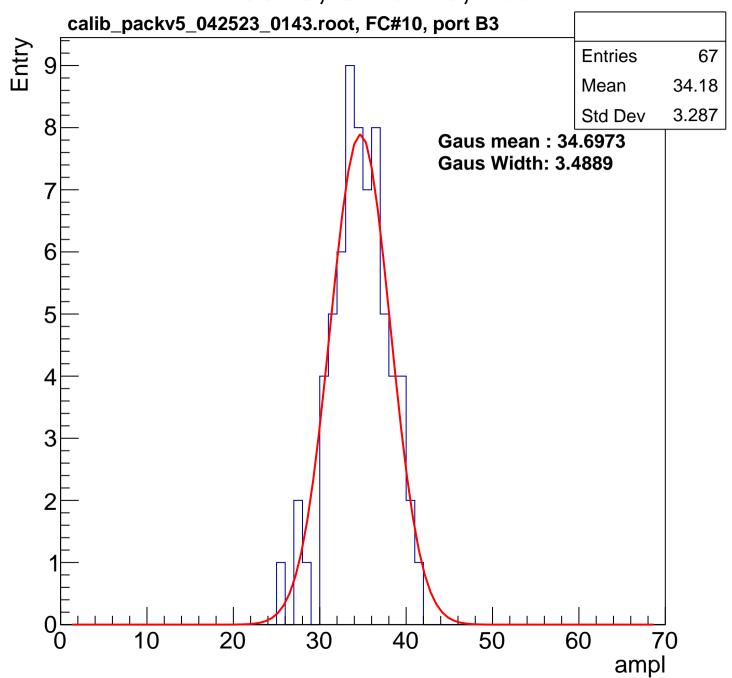


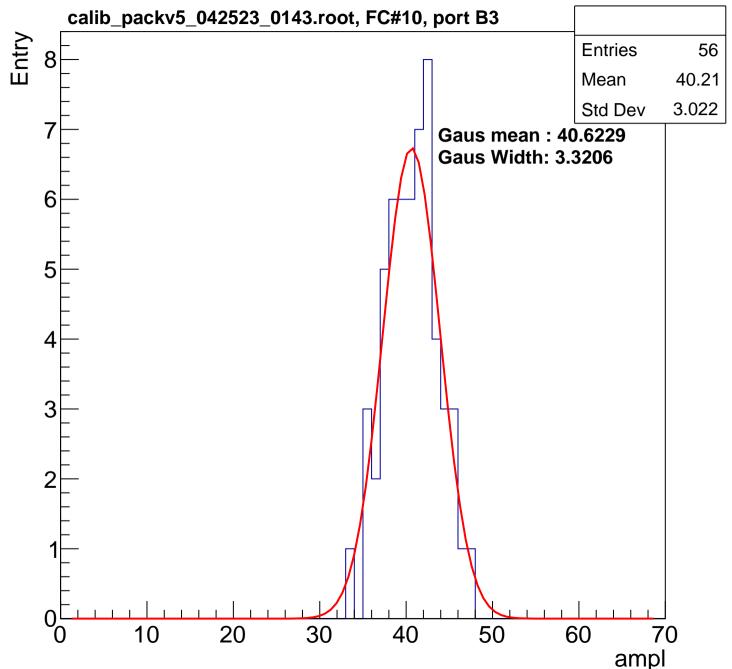


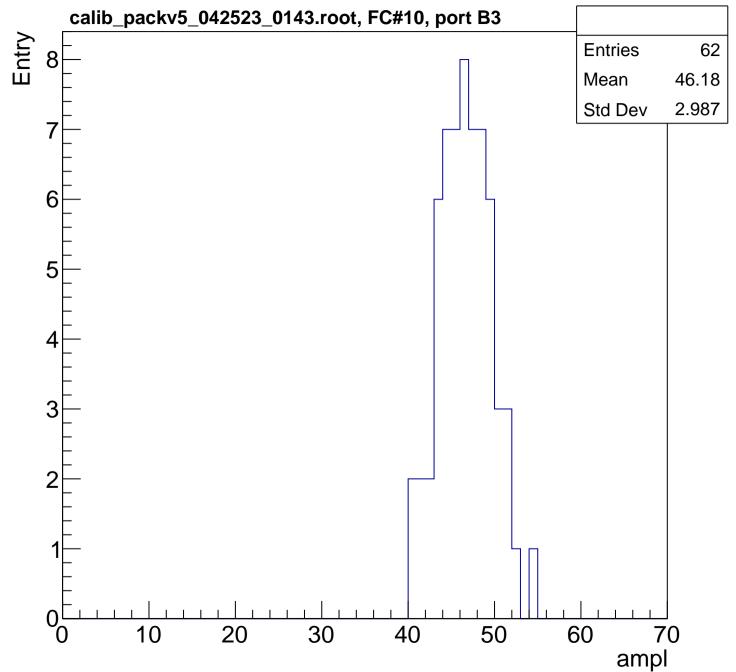
0

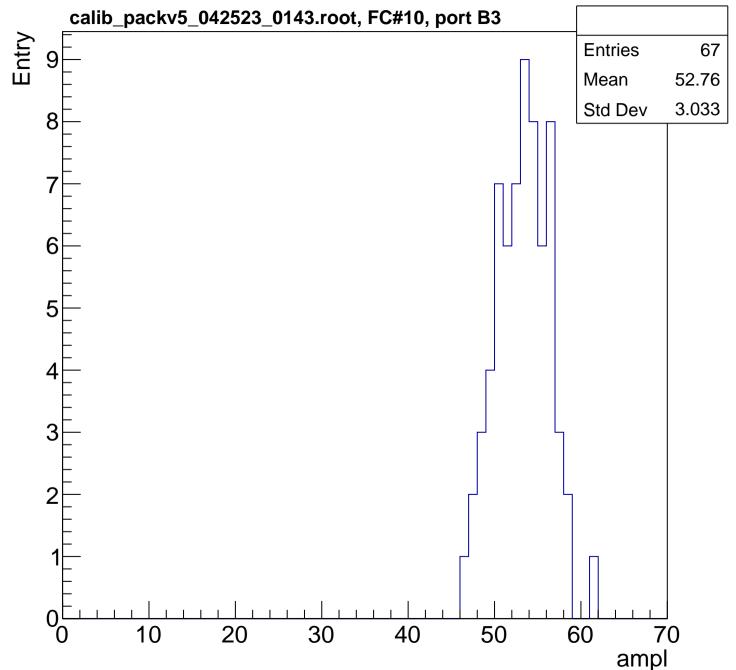


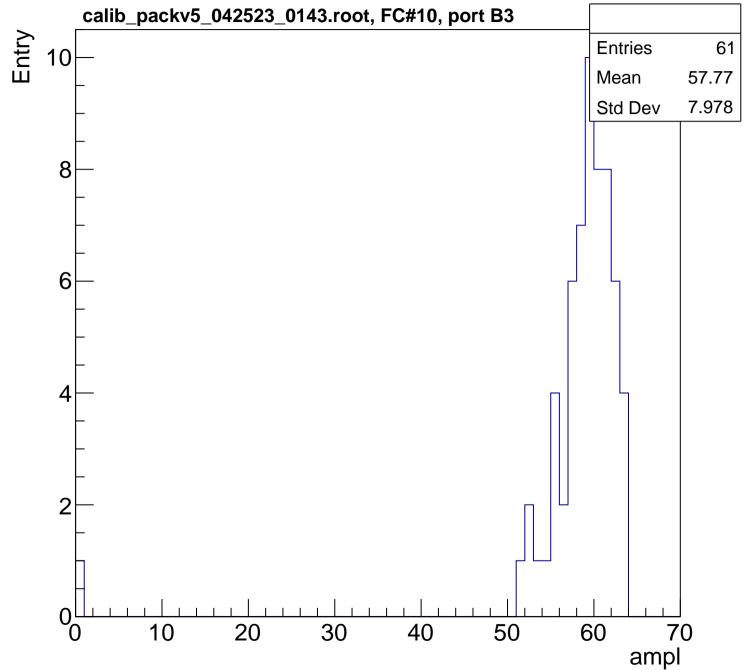


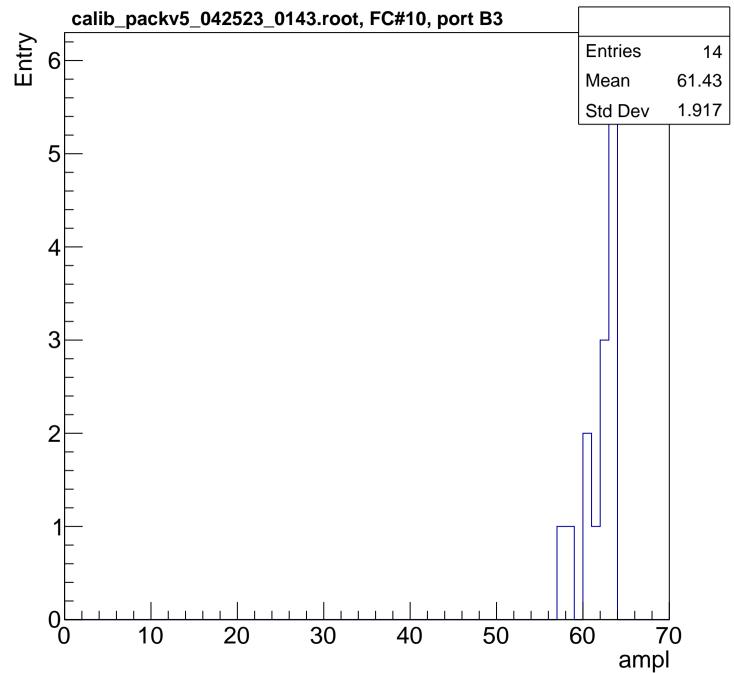


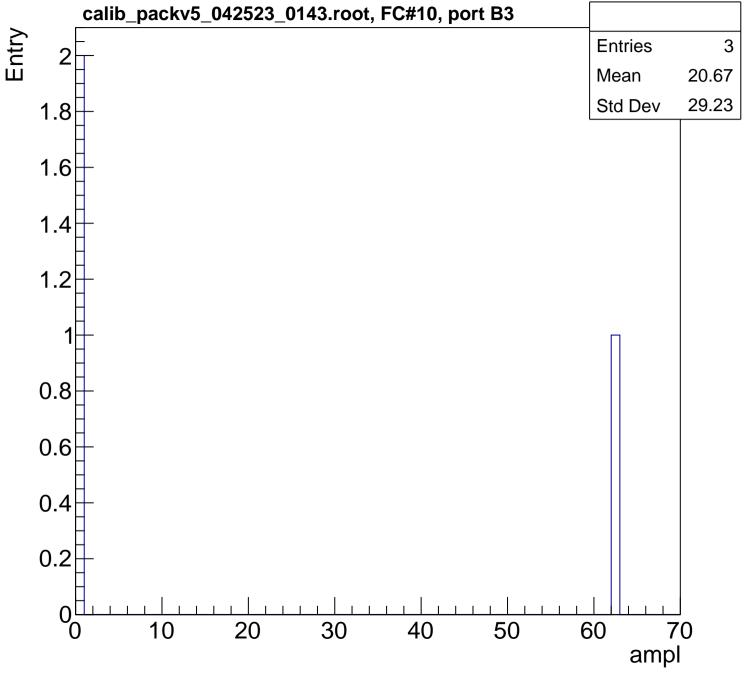


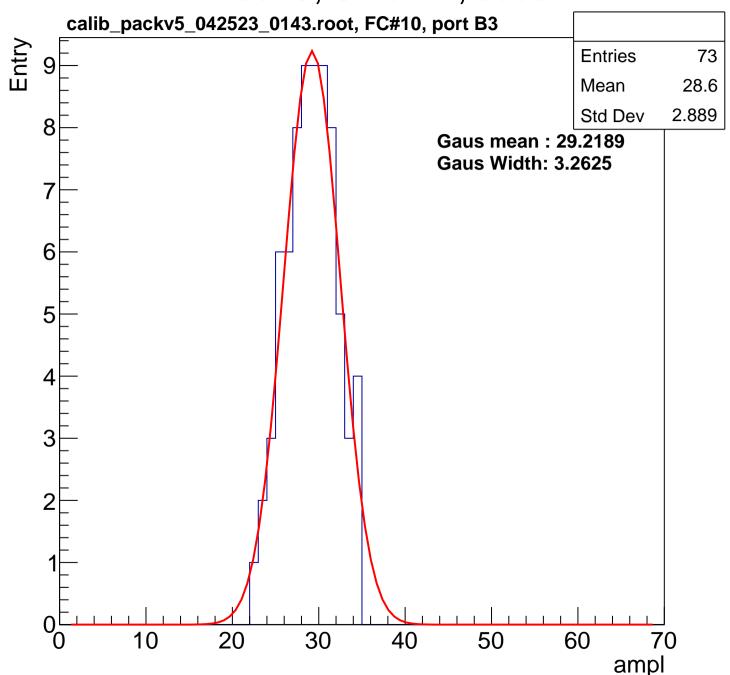


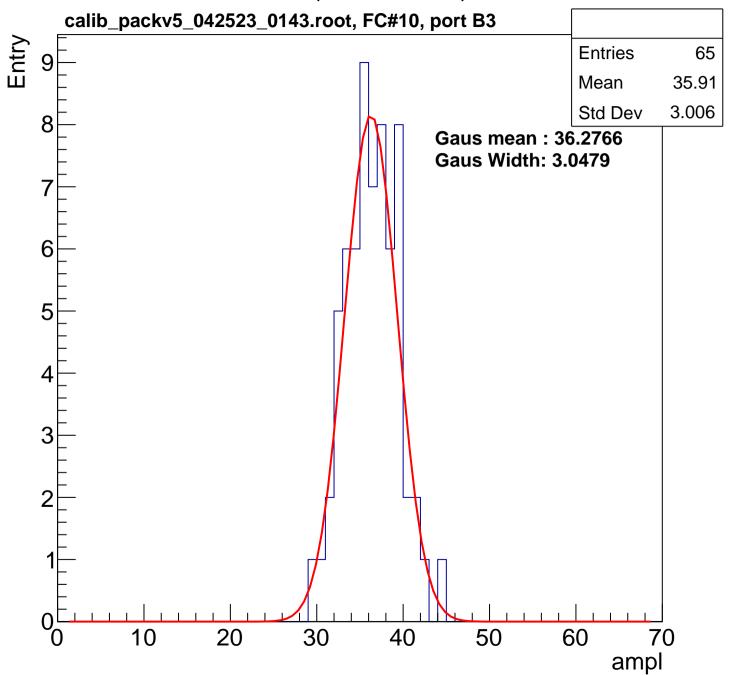


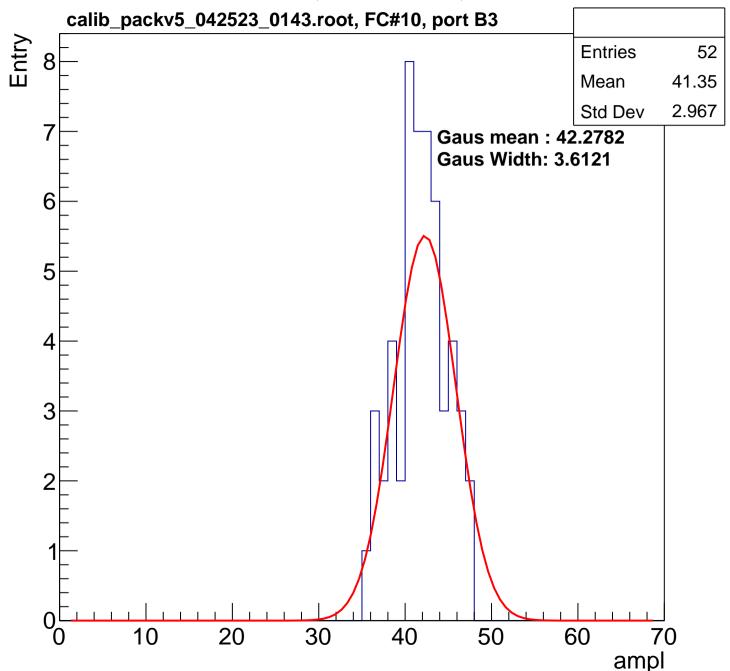


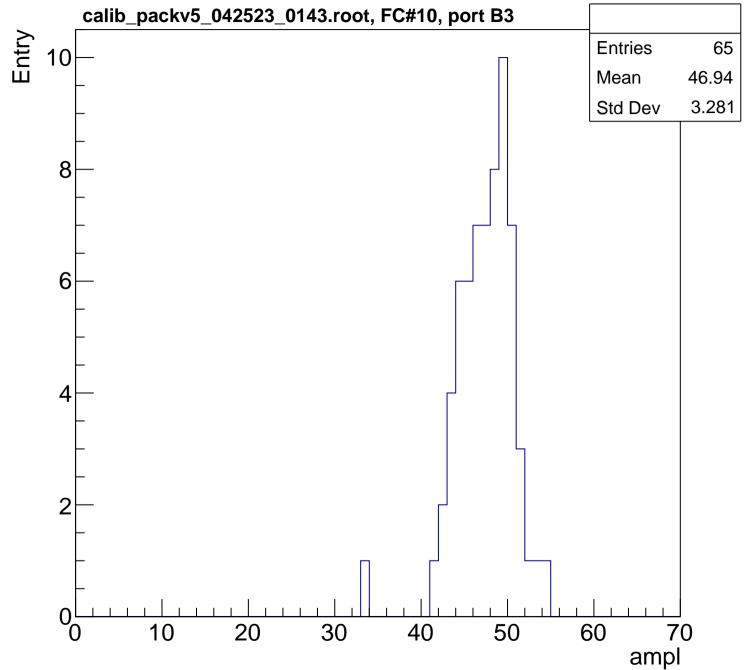


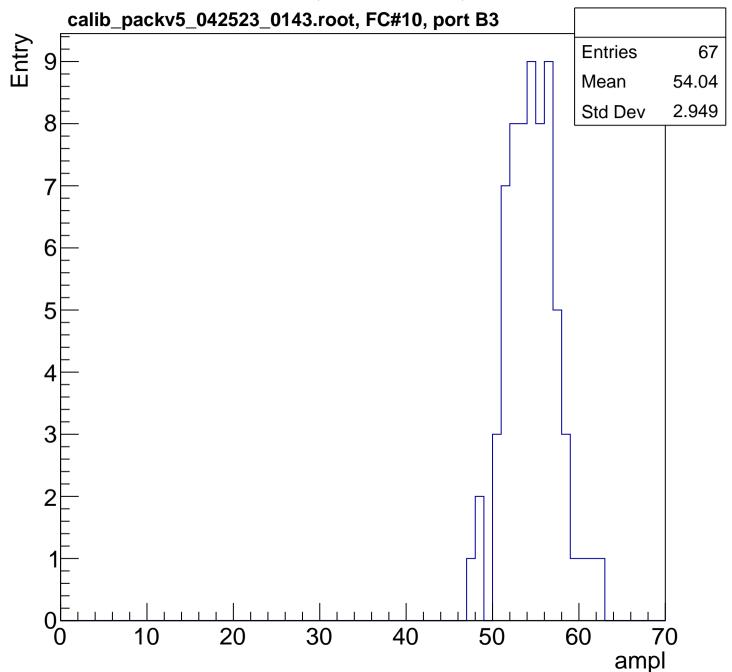


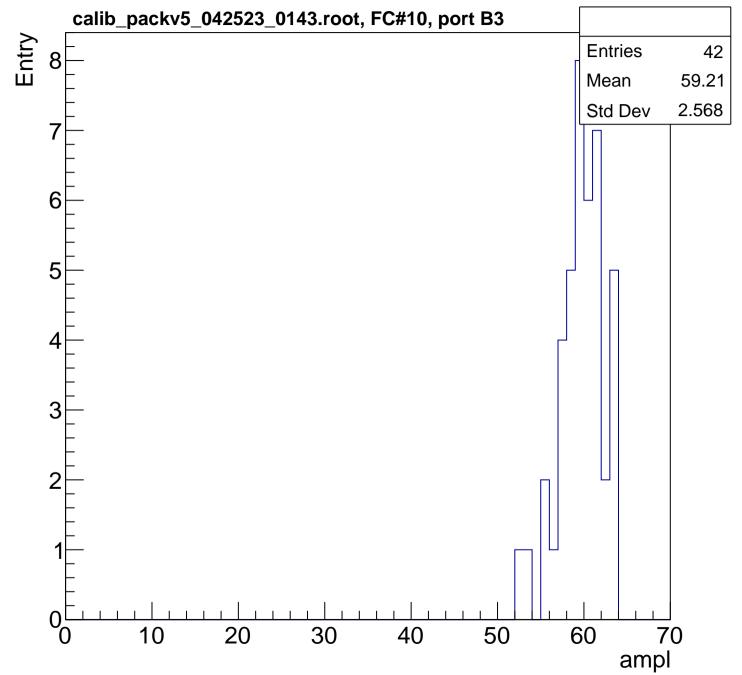


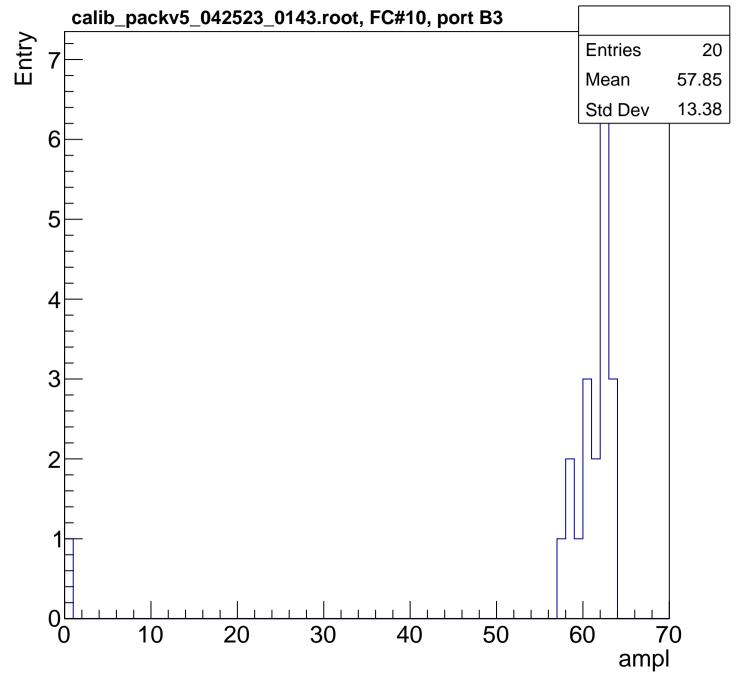


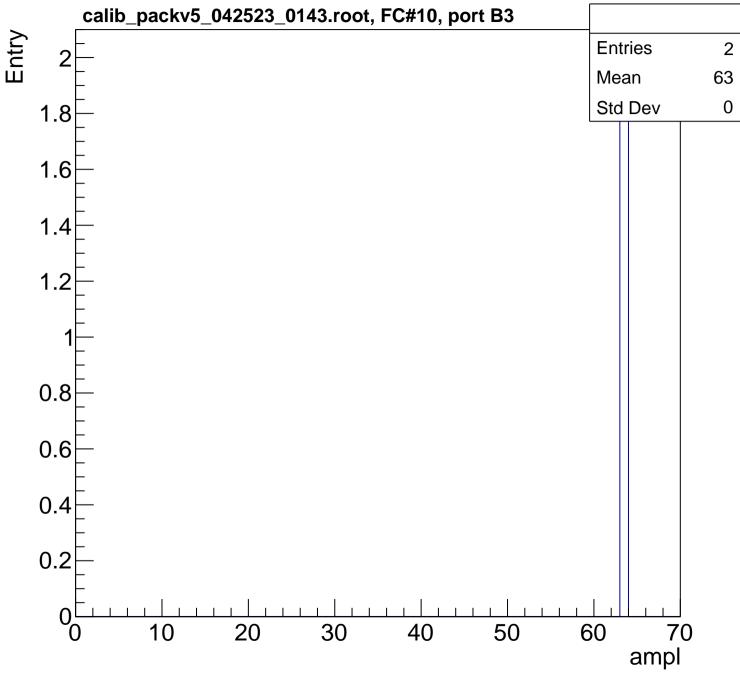


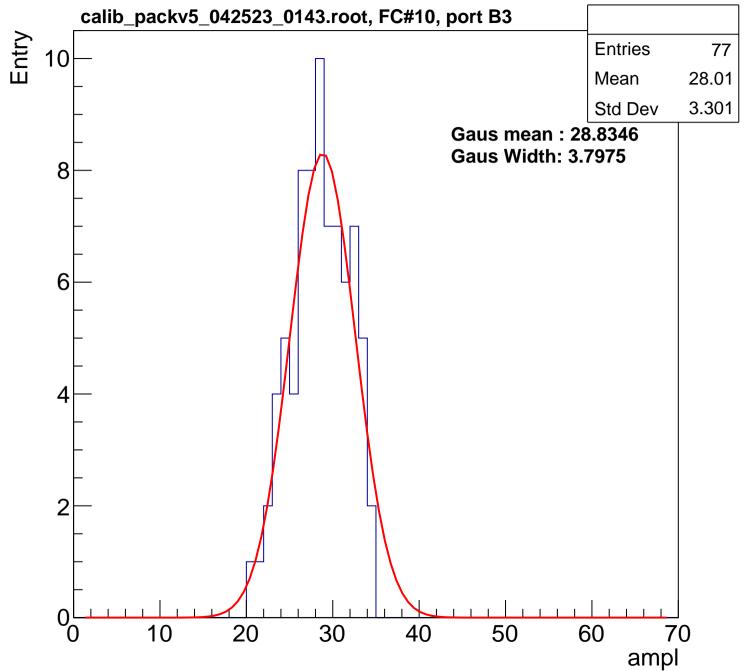


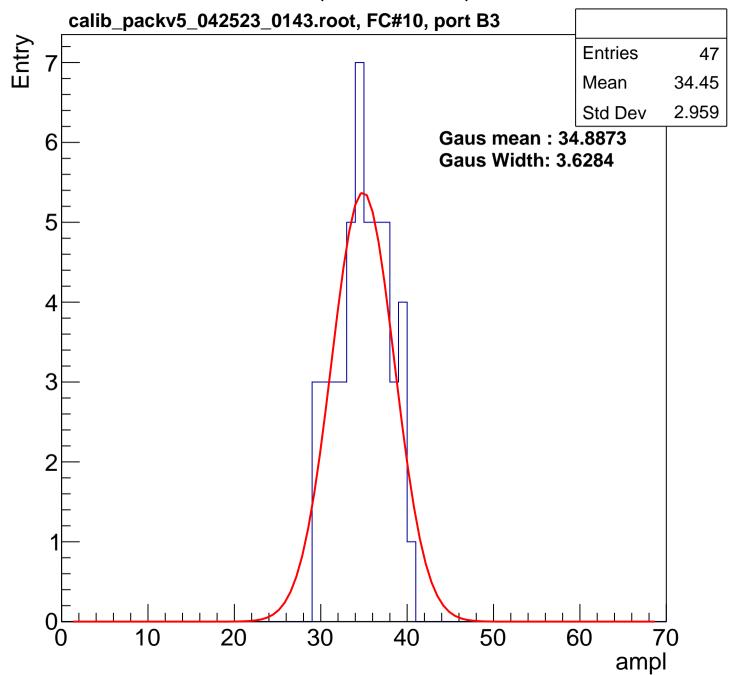


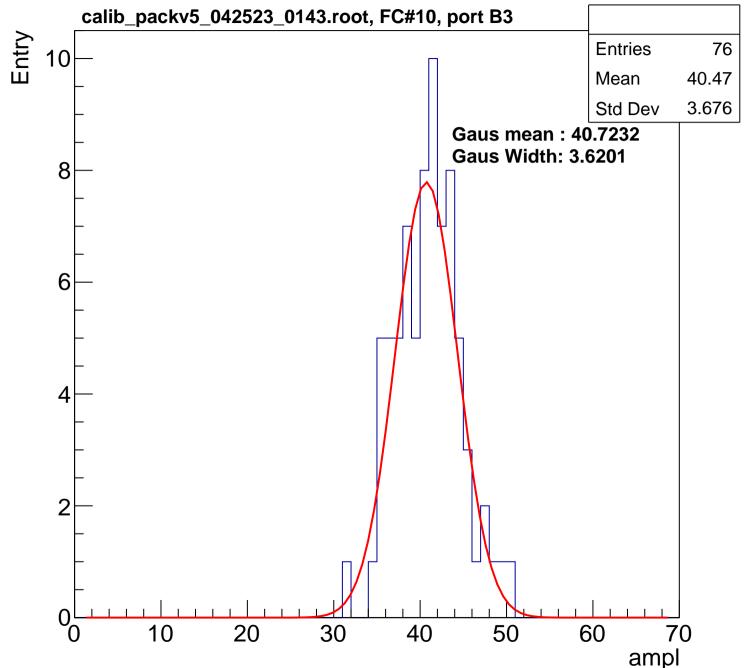


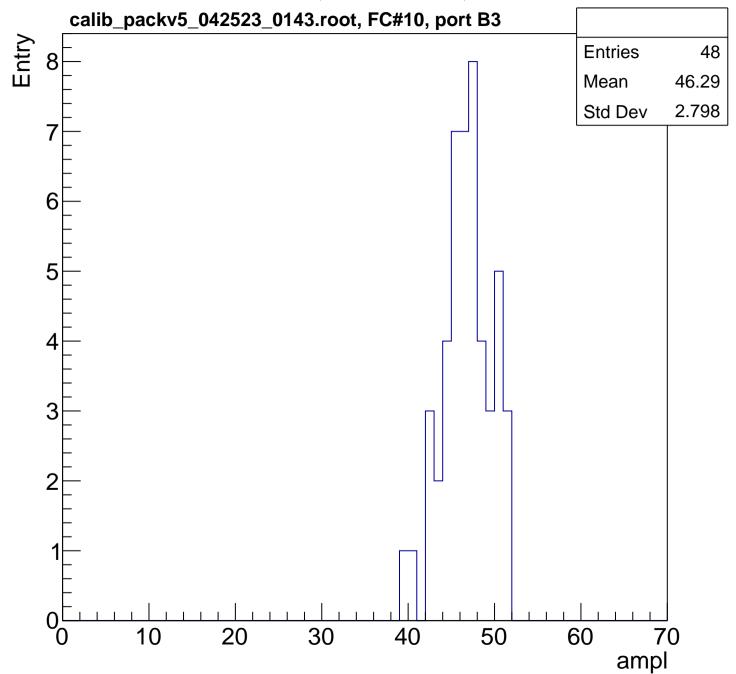


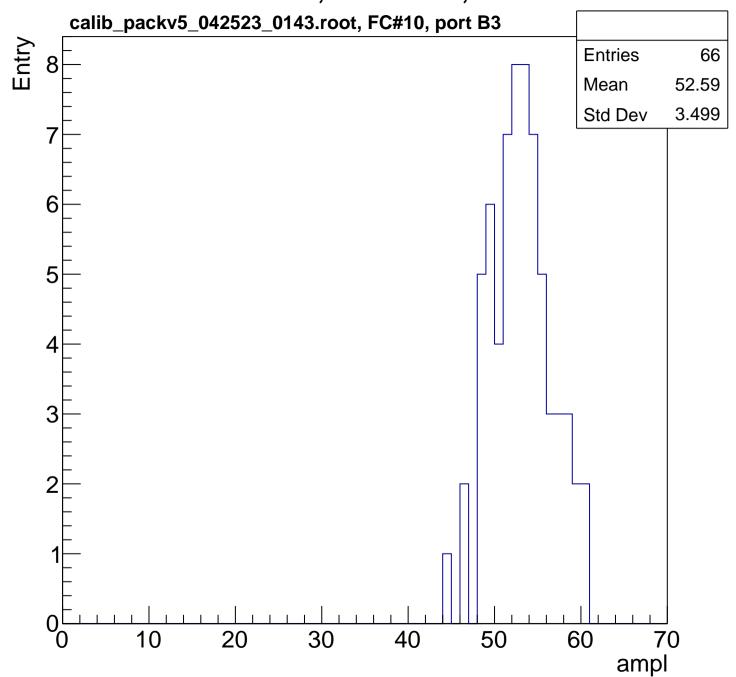


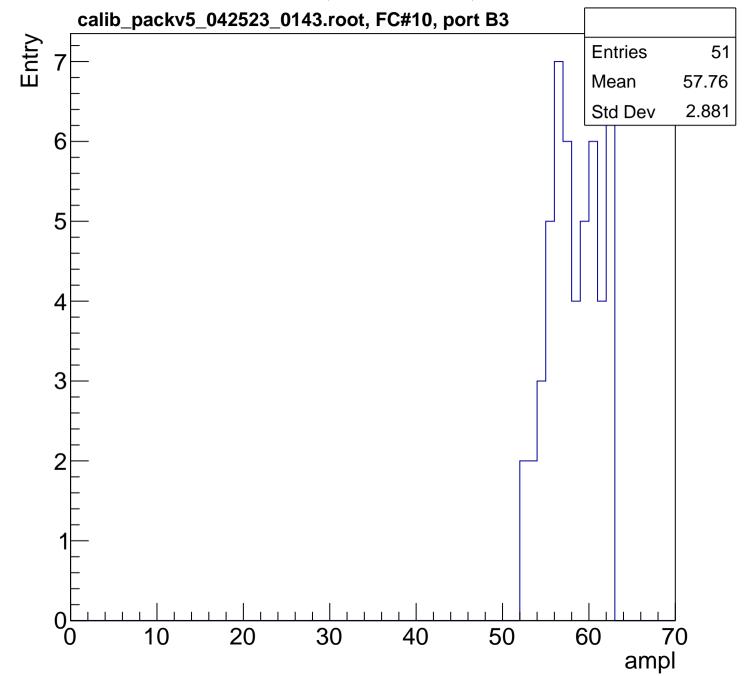


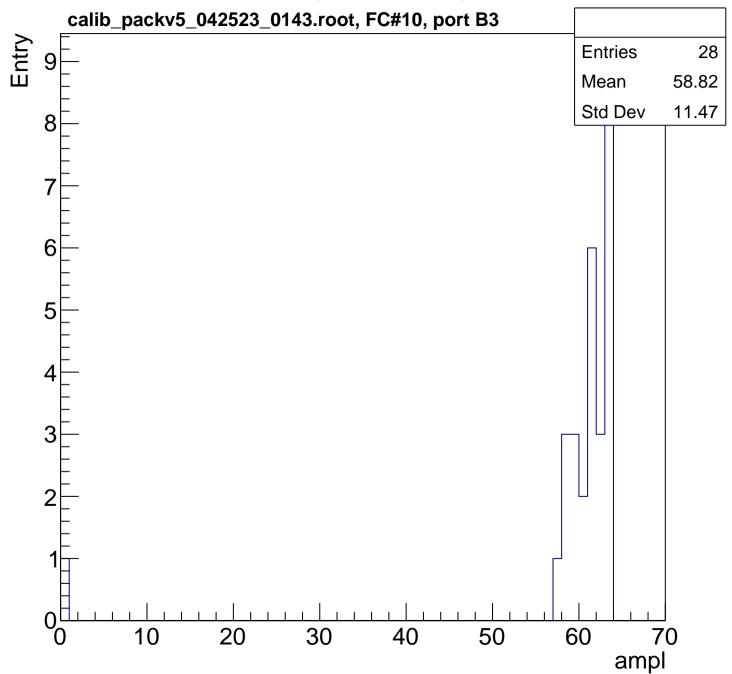




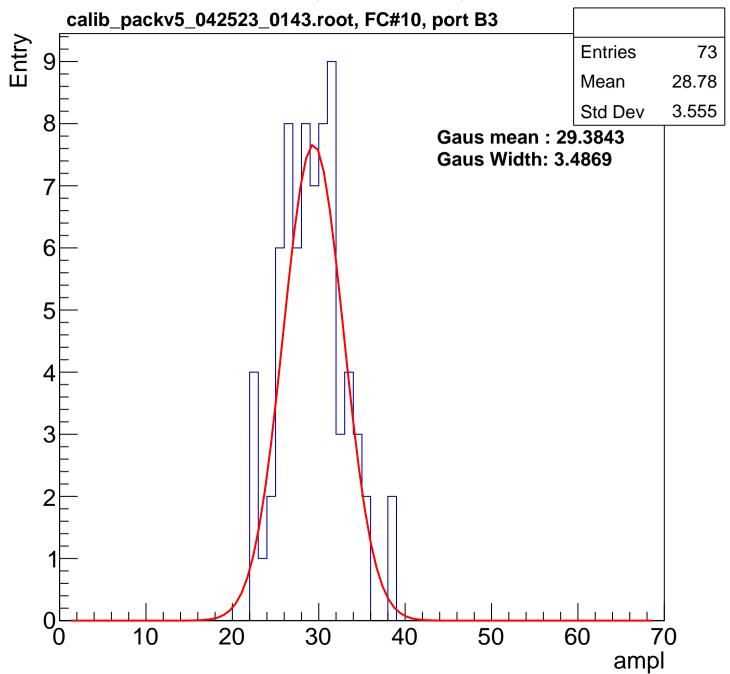


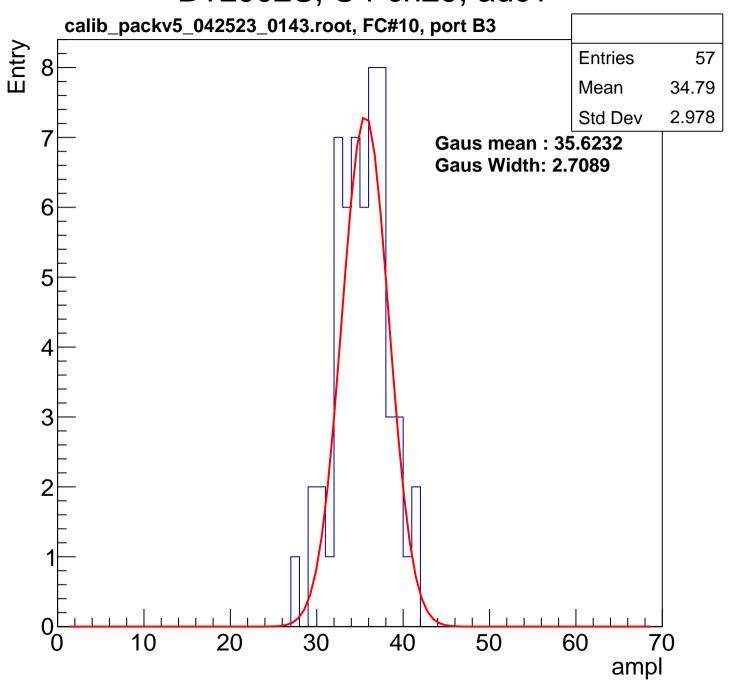


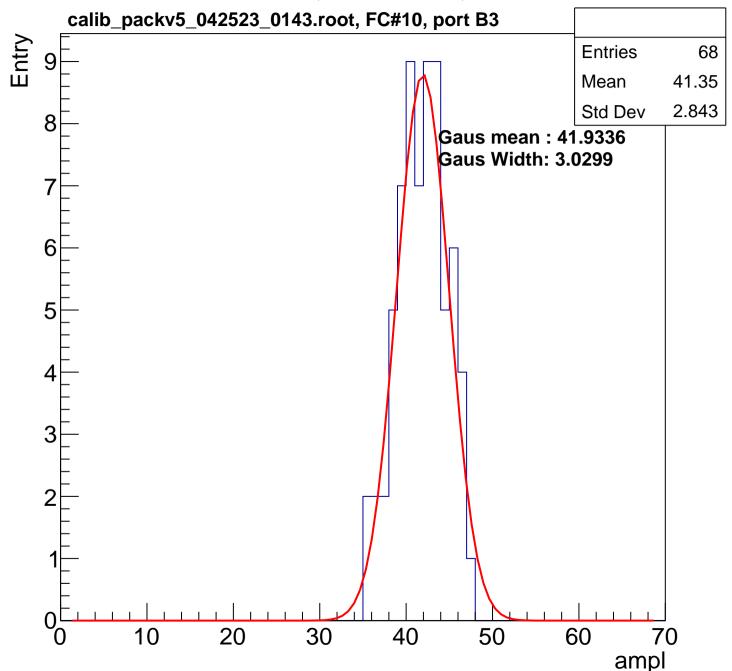


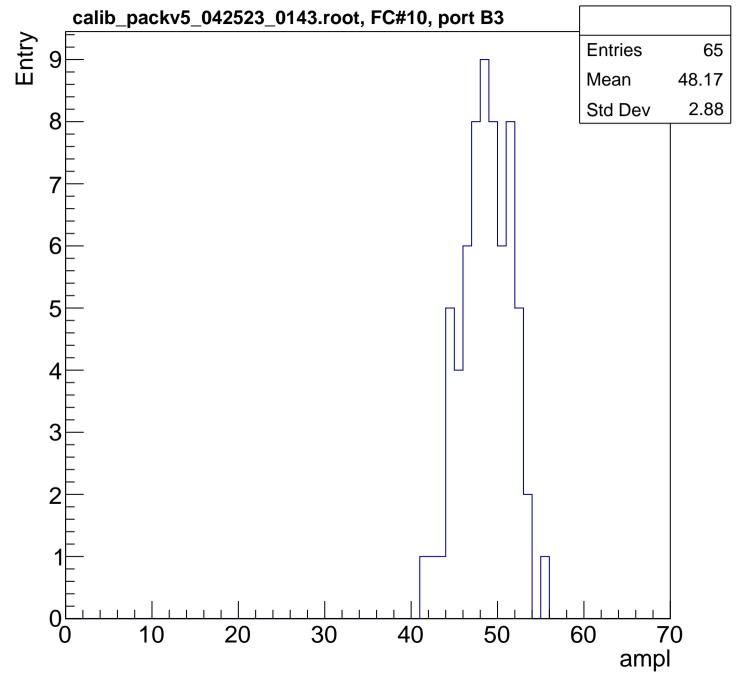


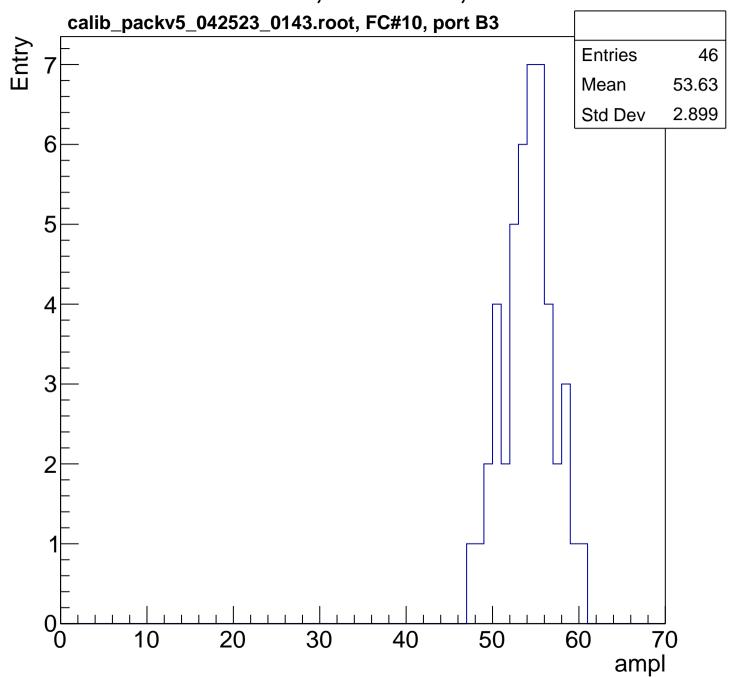


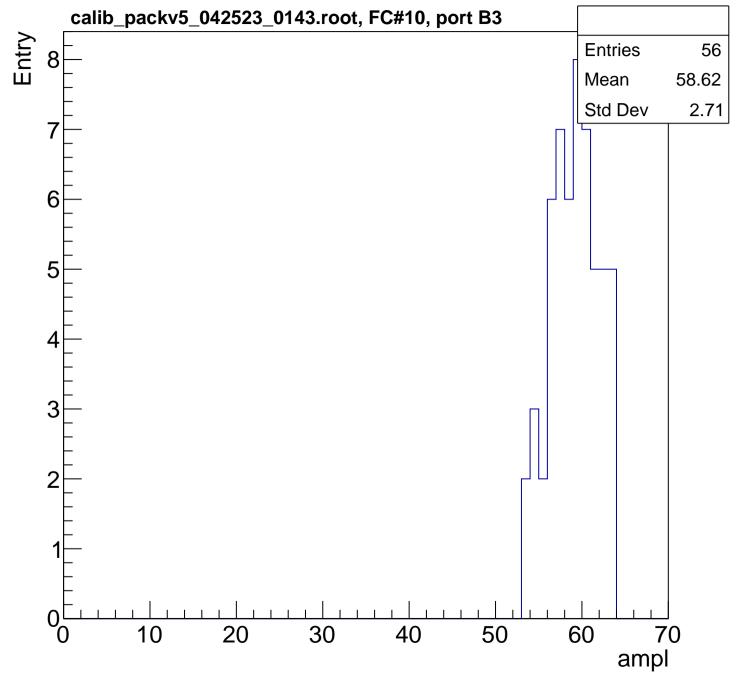


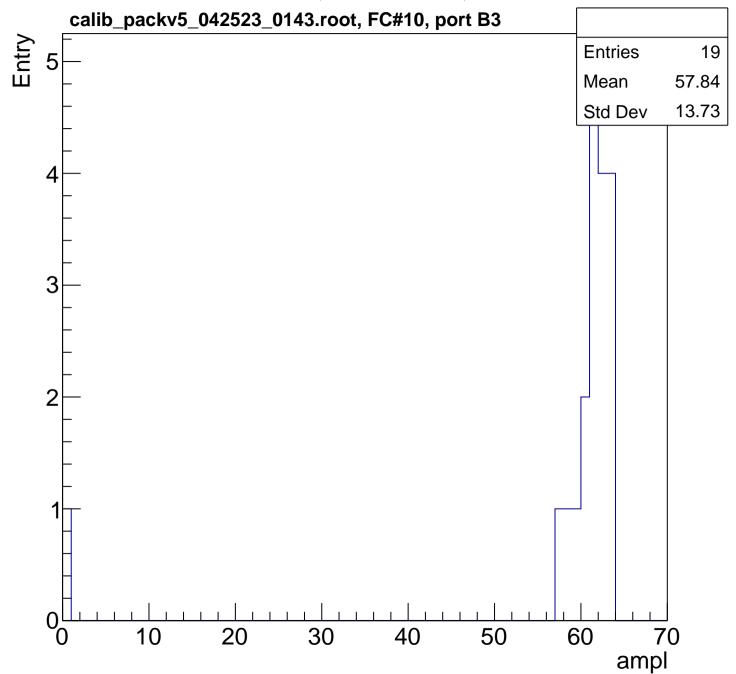


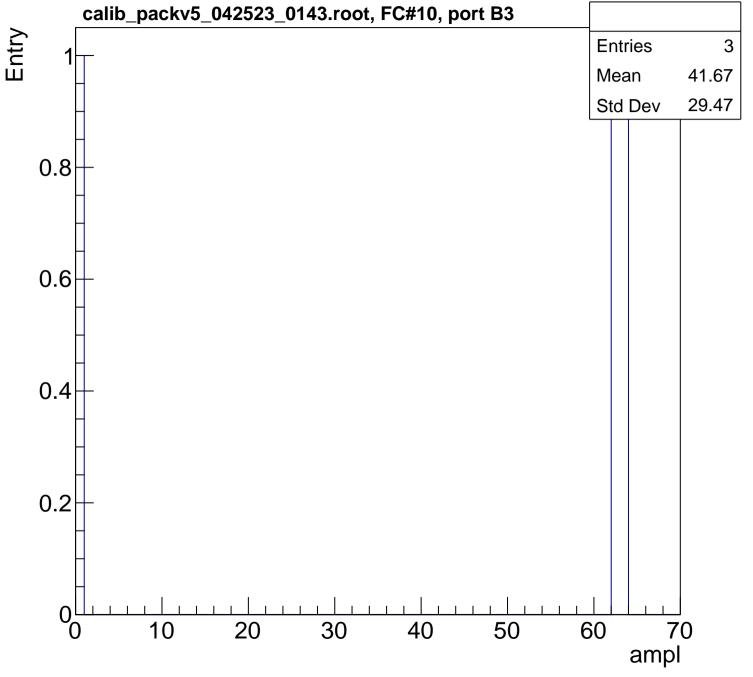


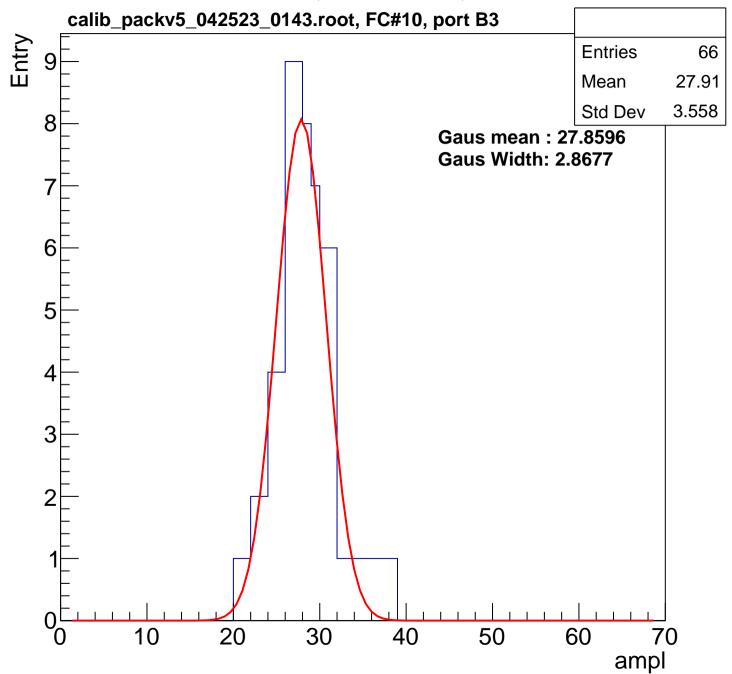


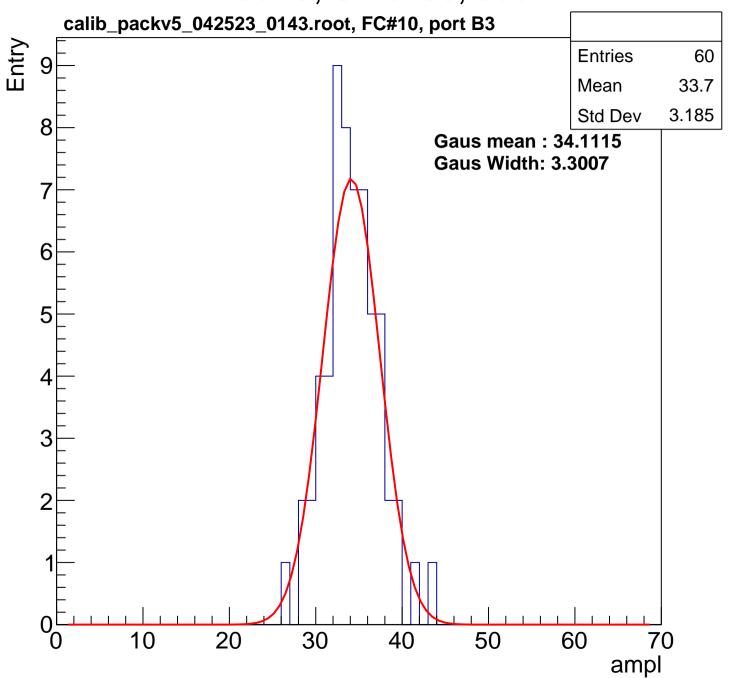


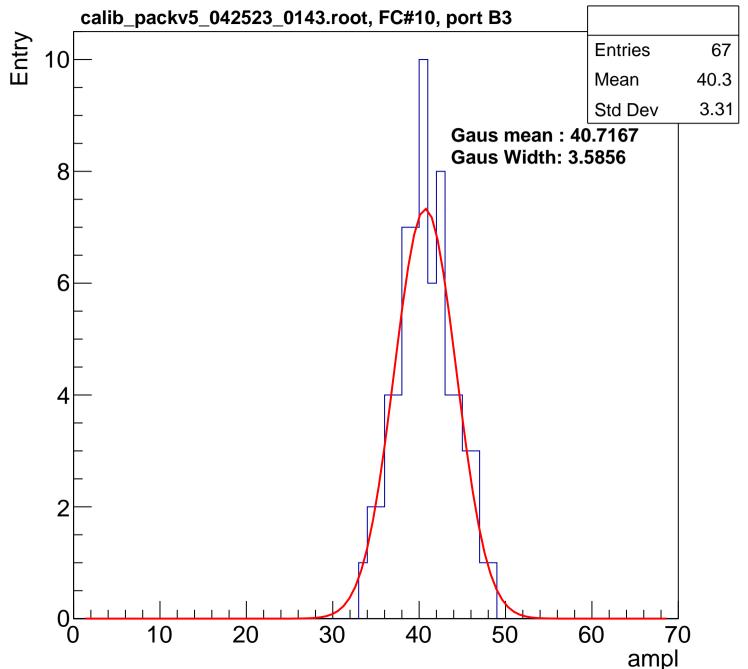


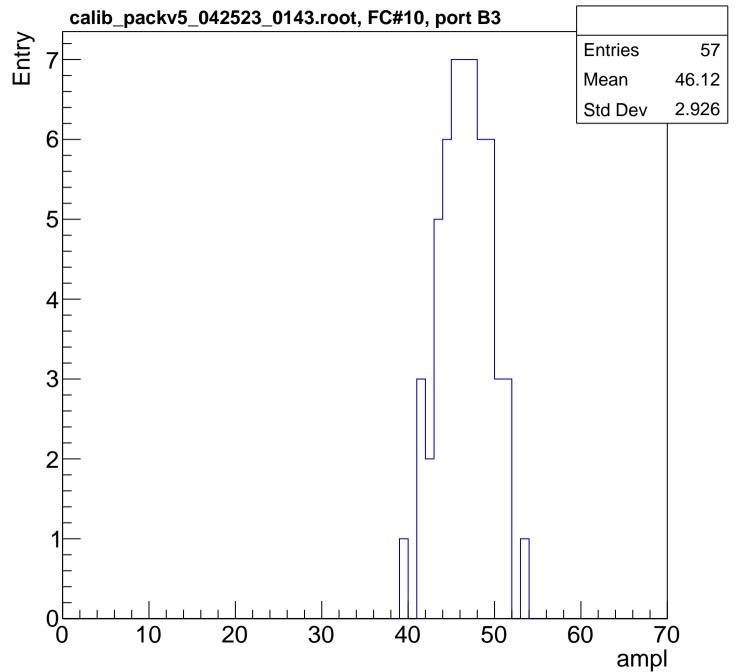


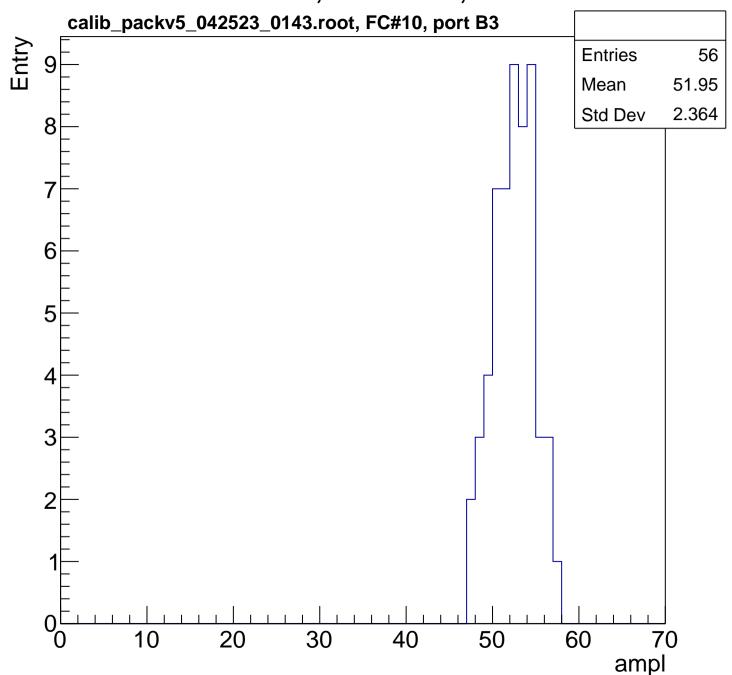


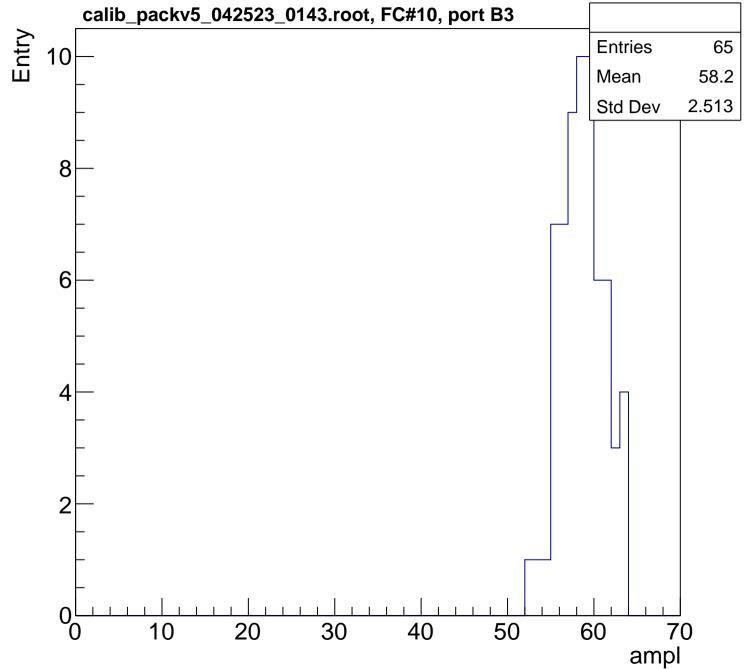


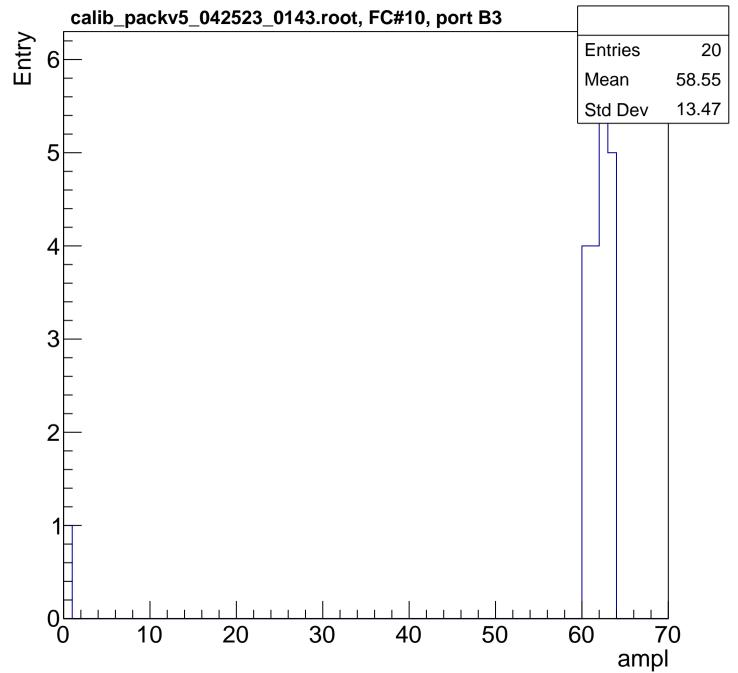


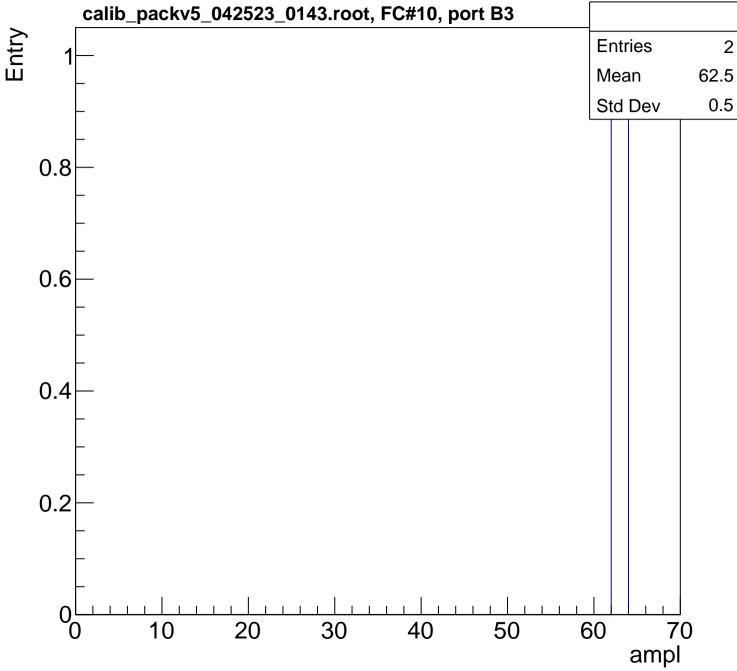


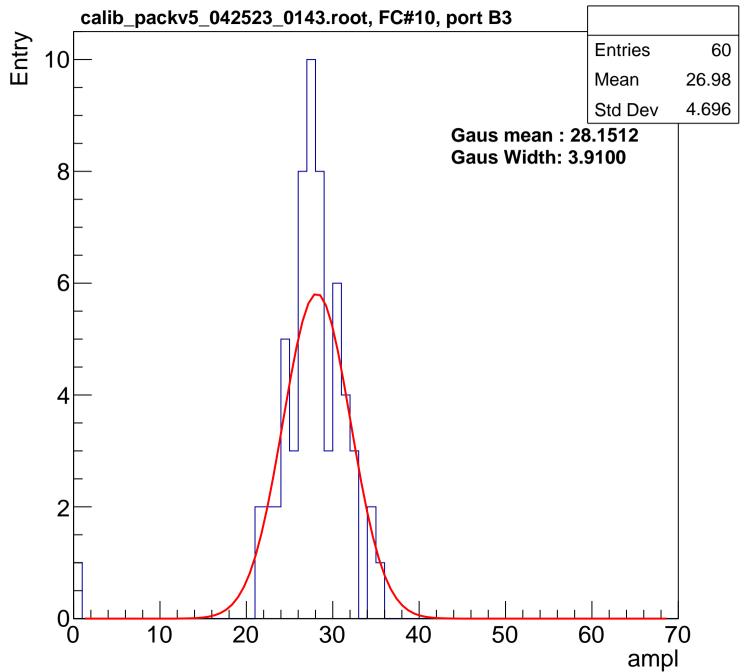


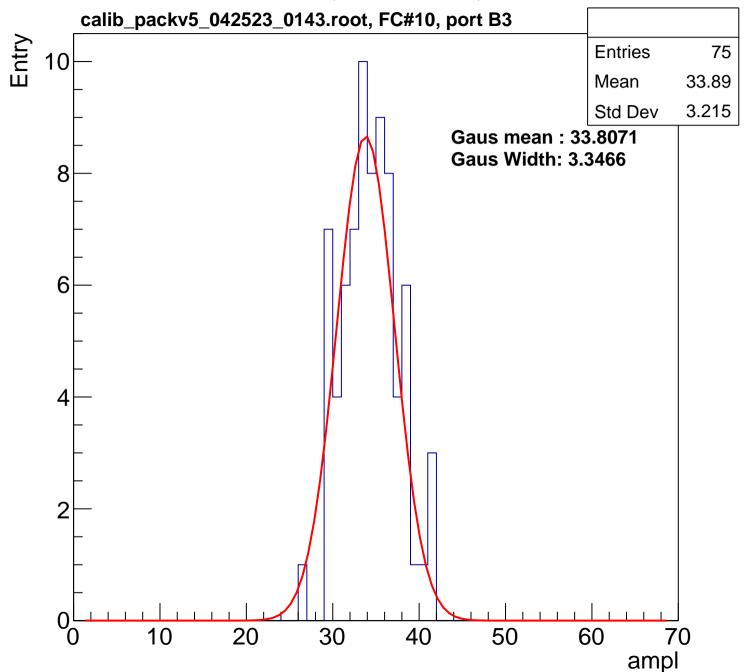


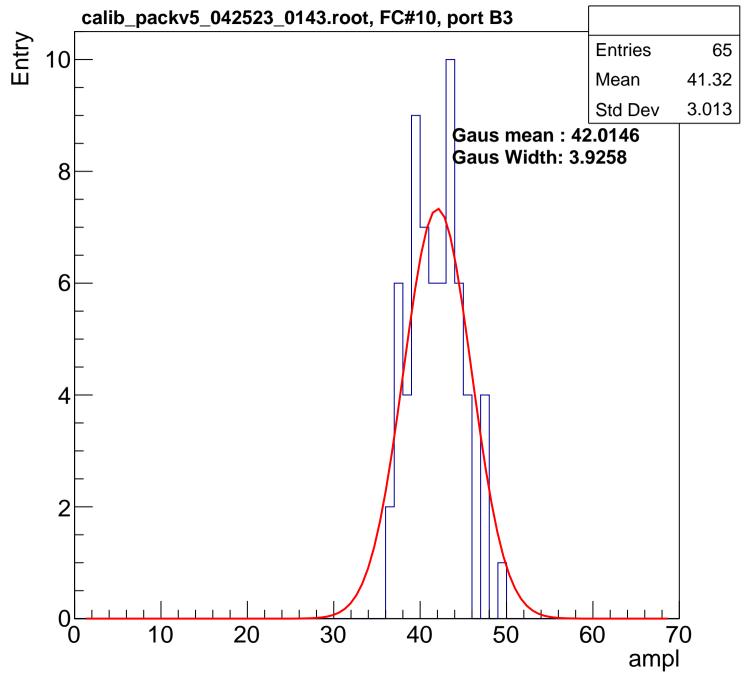


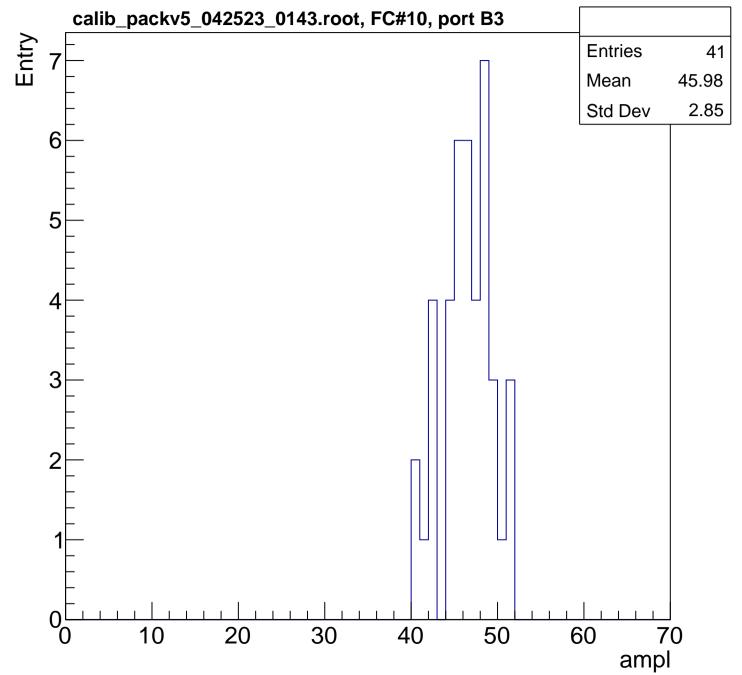


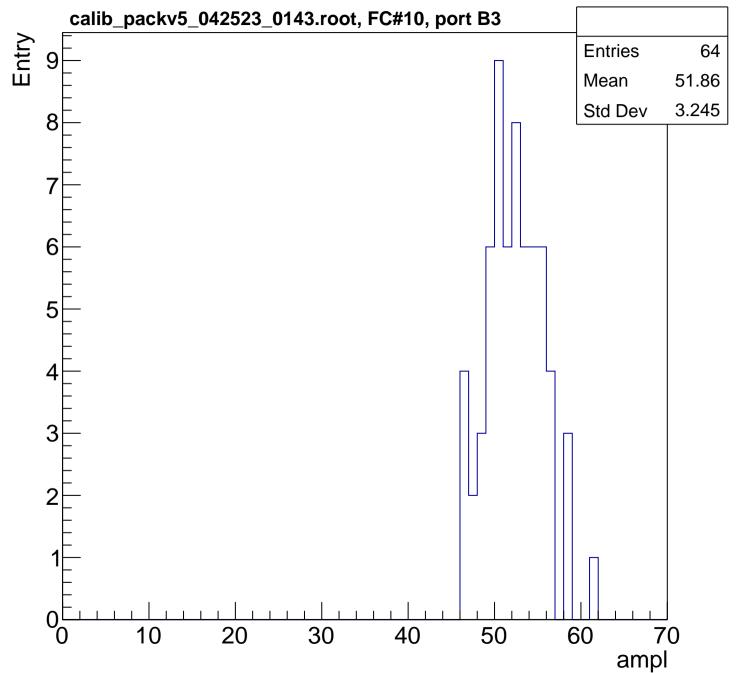


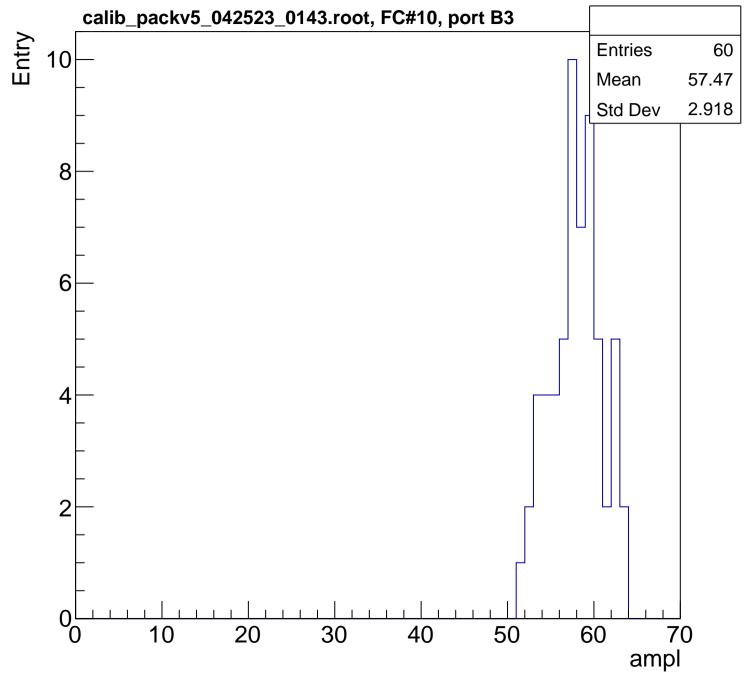


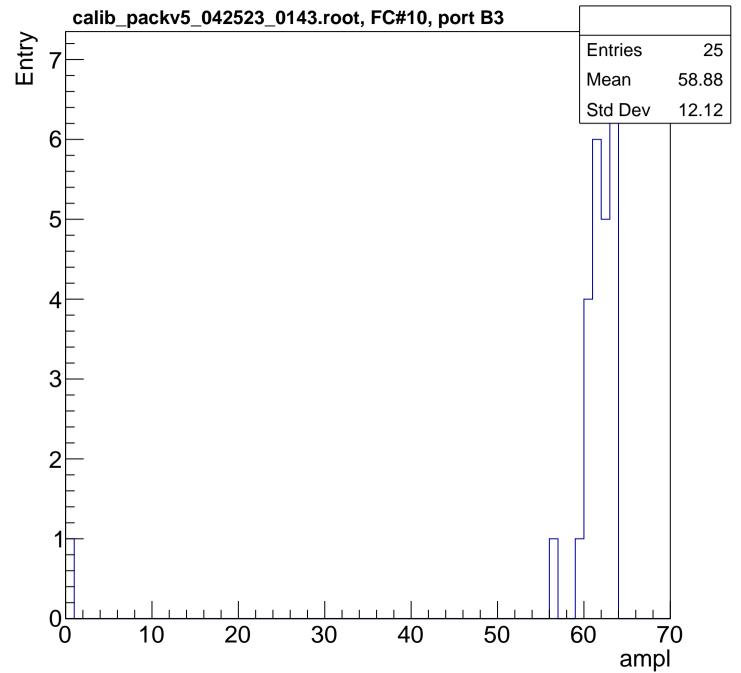


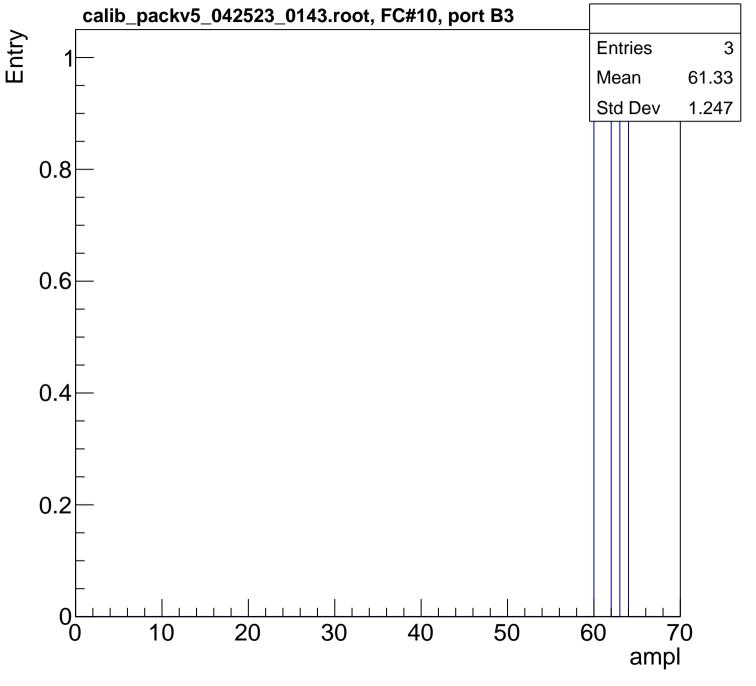


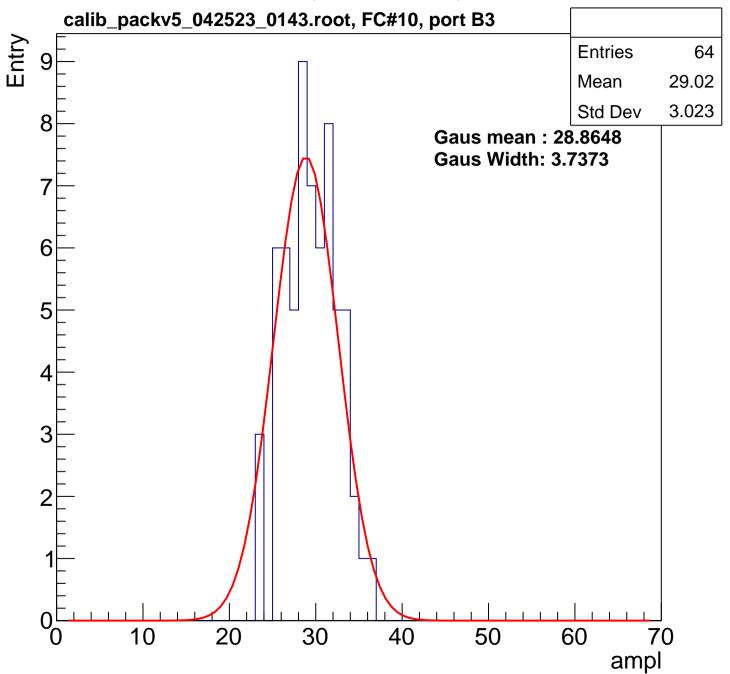


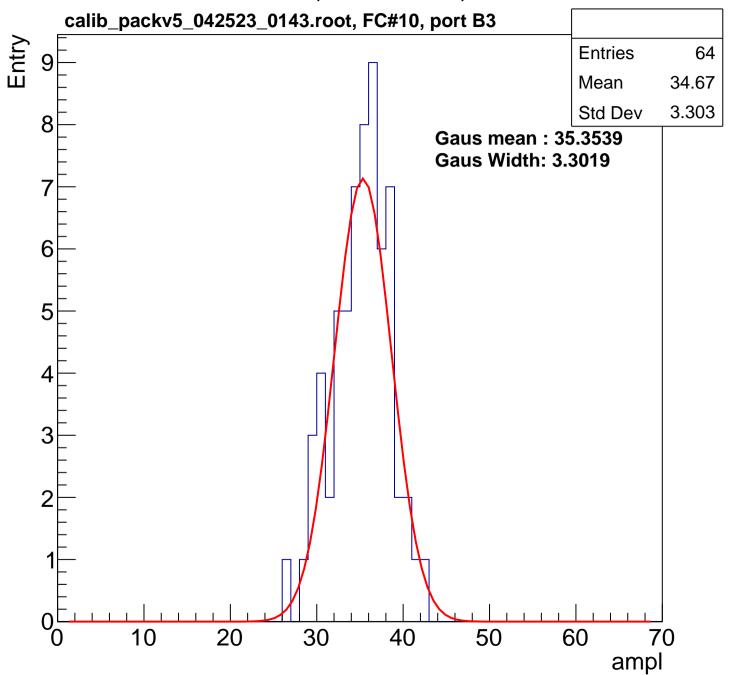


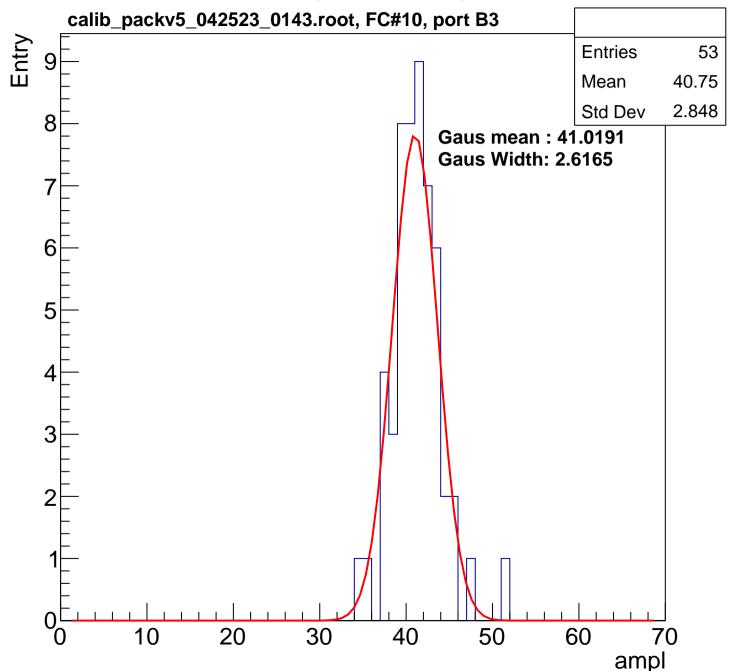


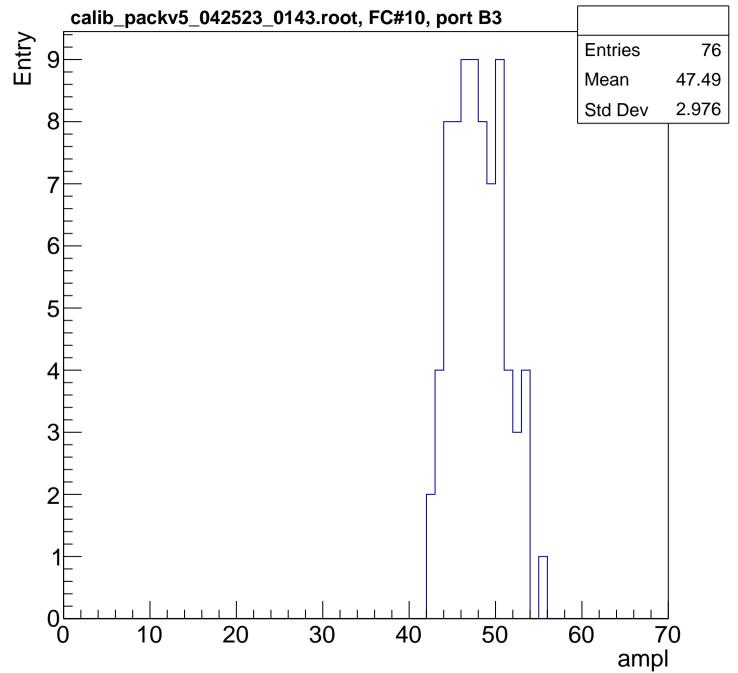


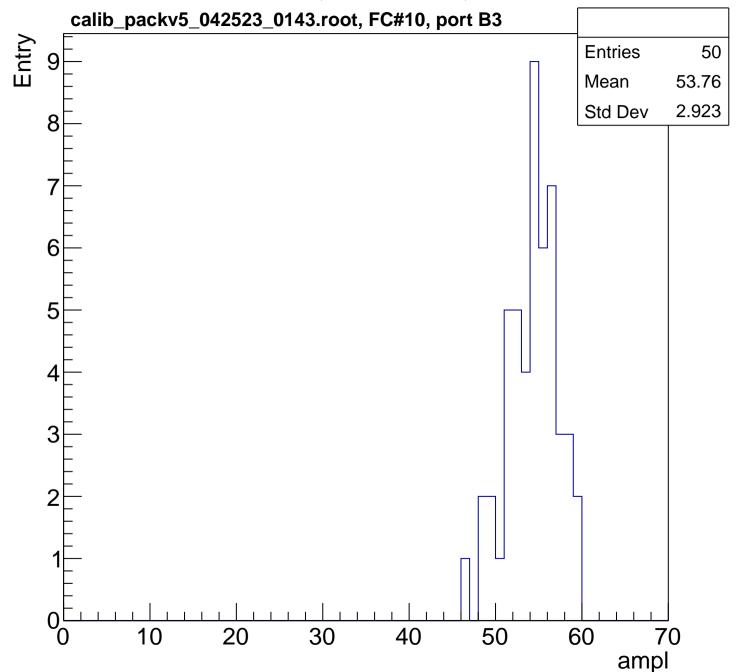


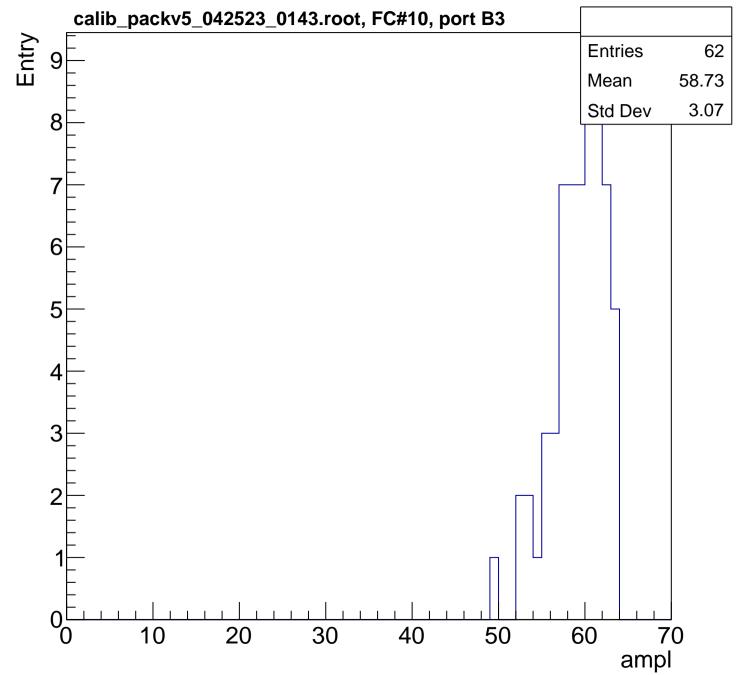


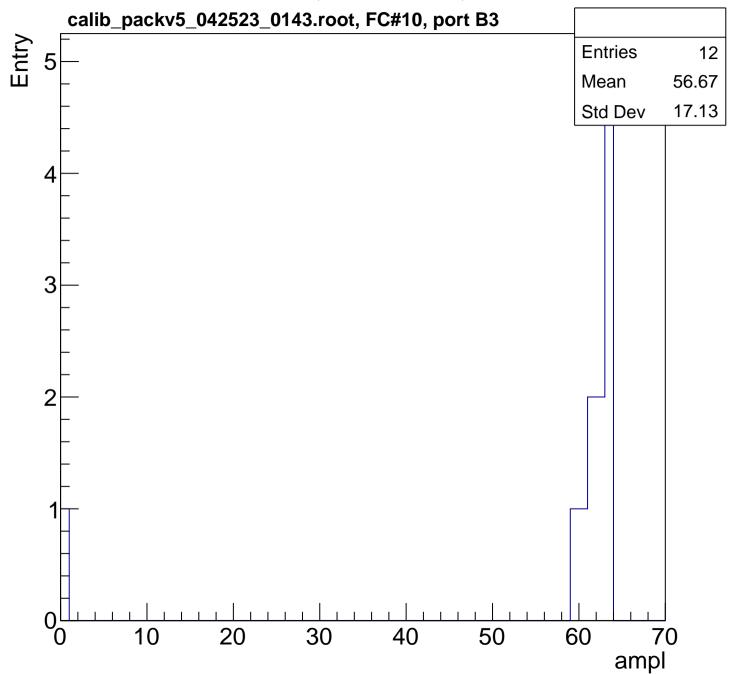


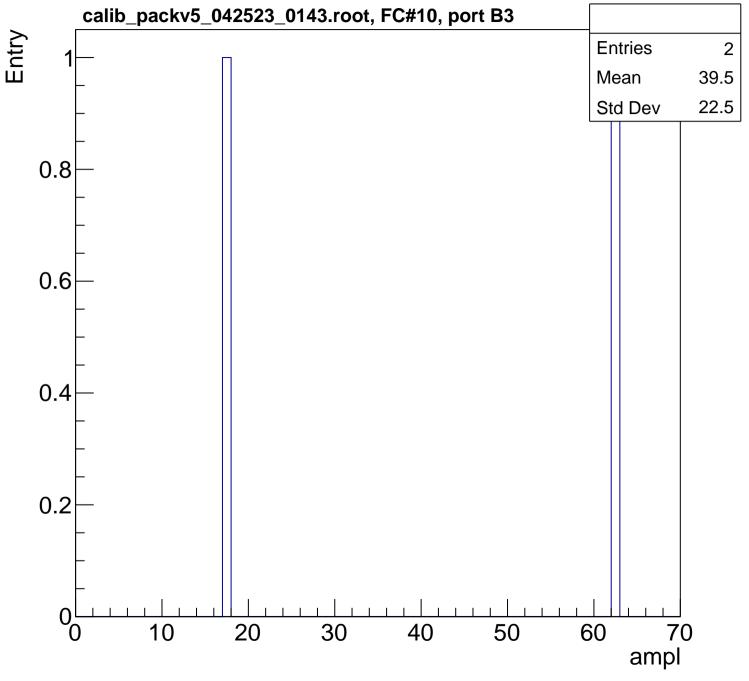


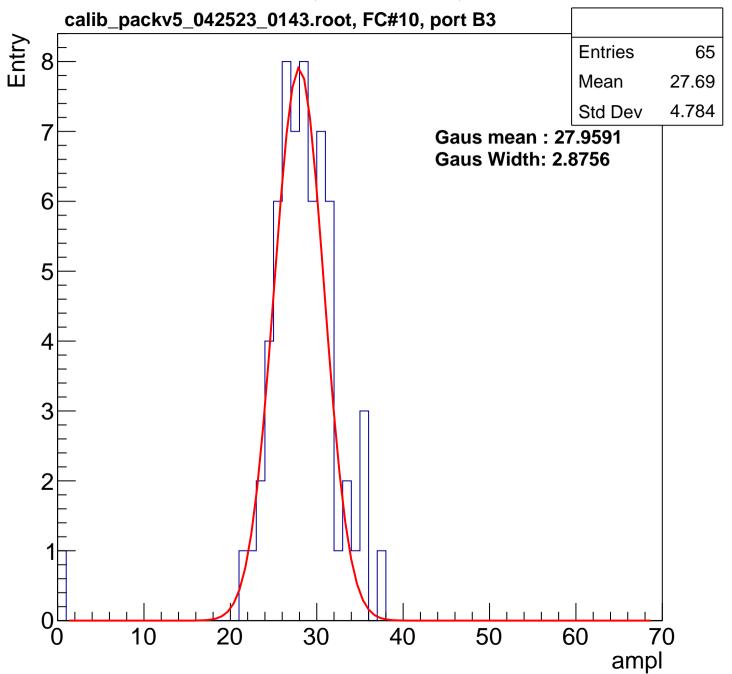


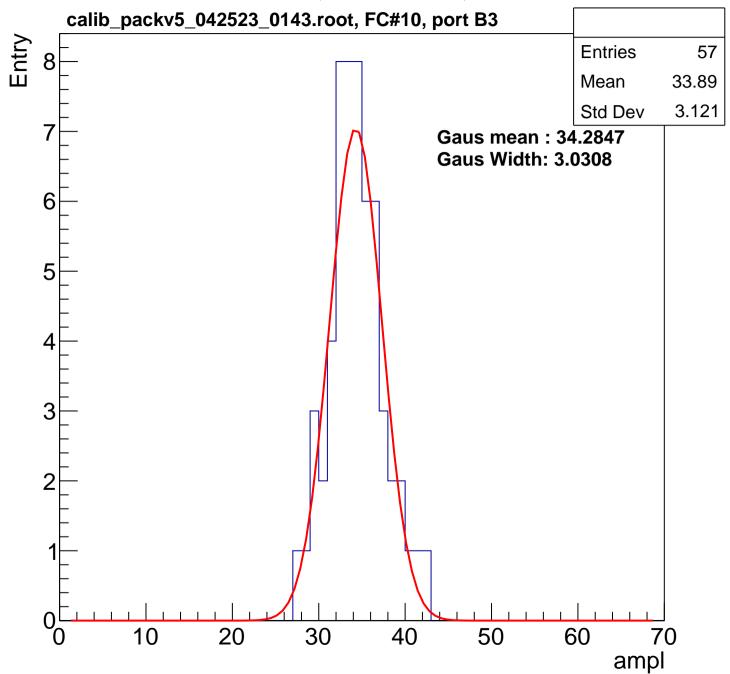


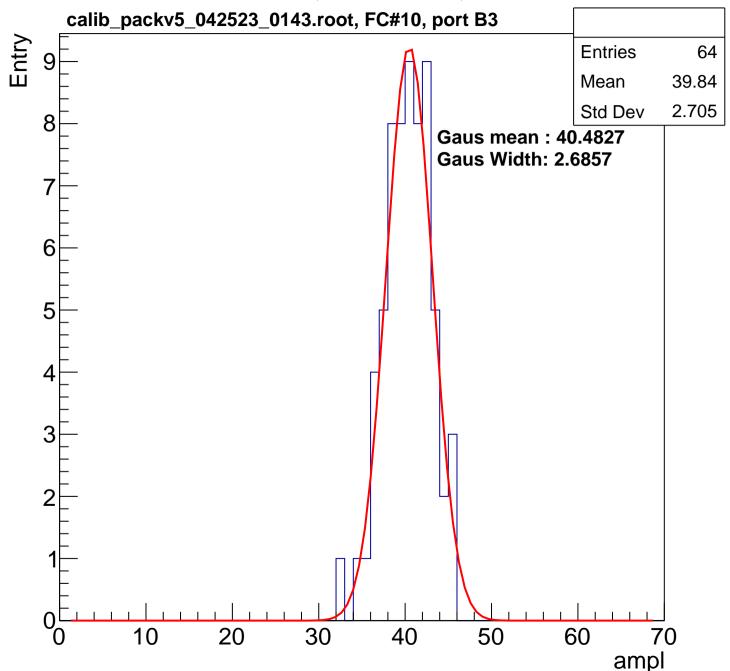


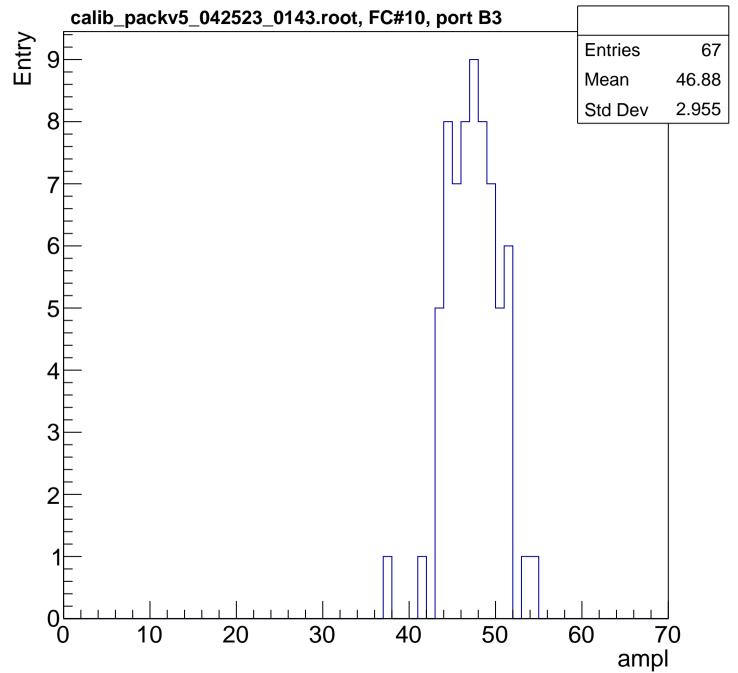


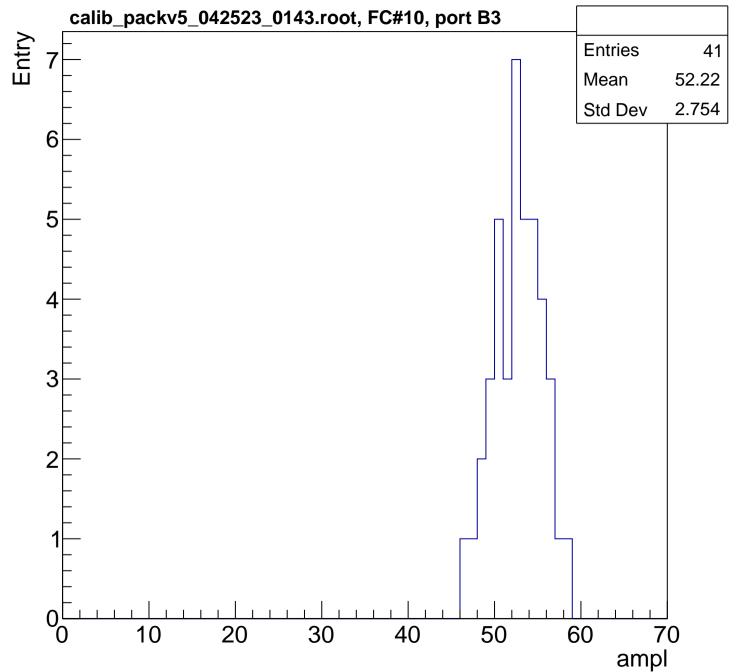


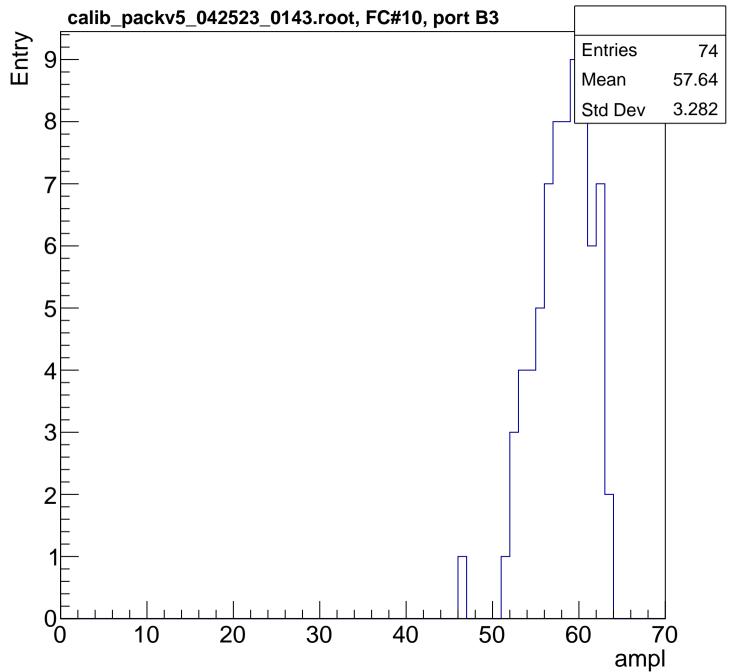


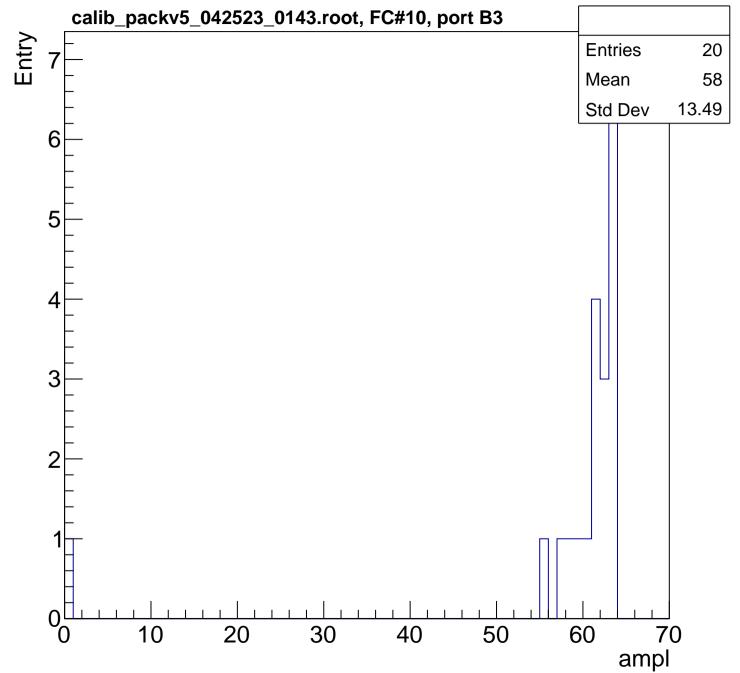


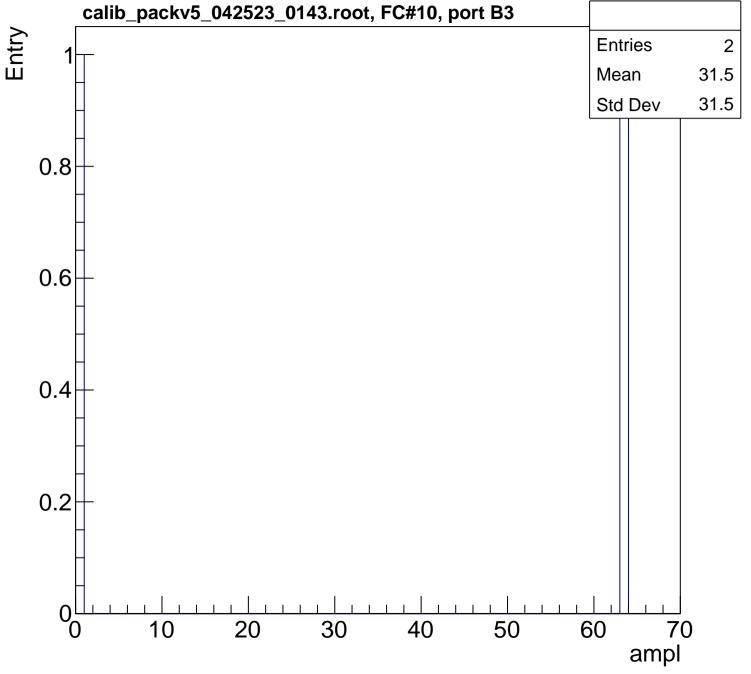


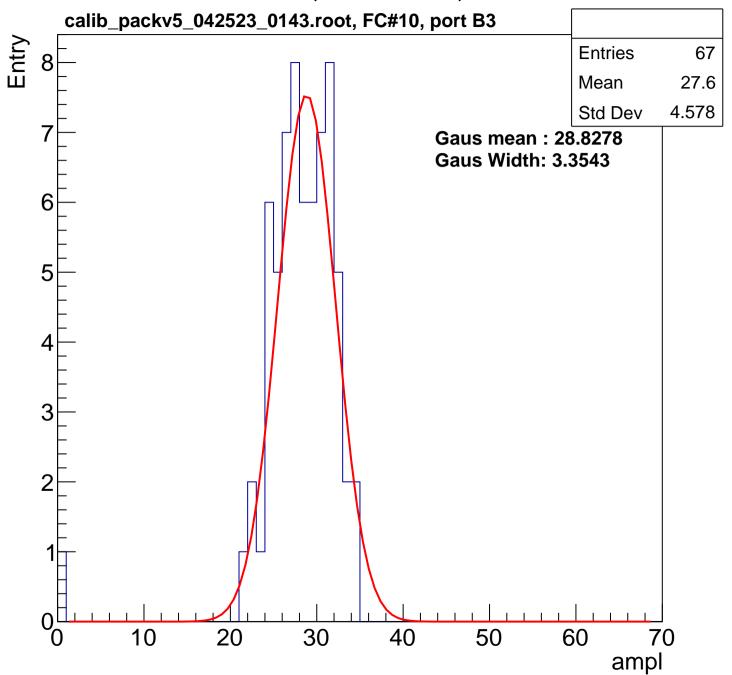


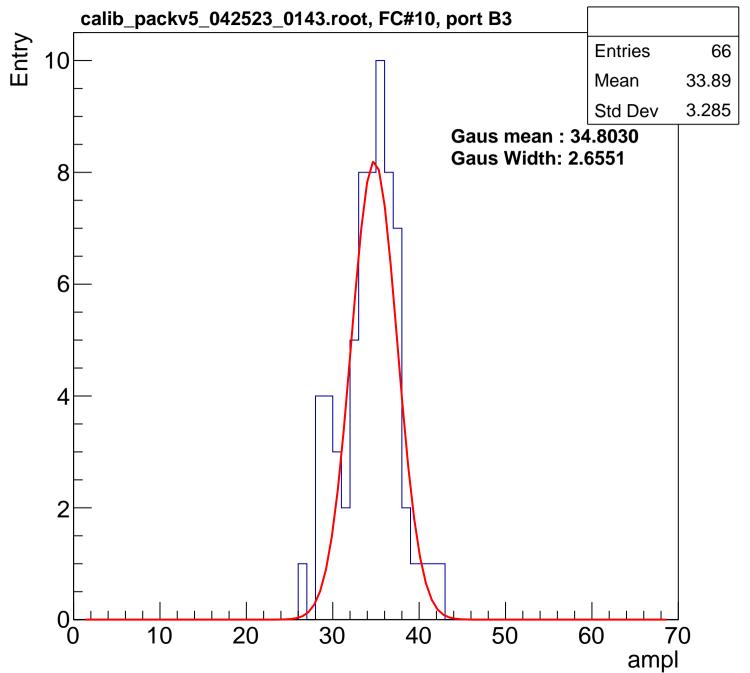


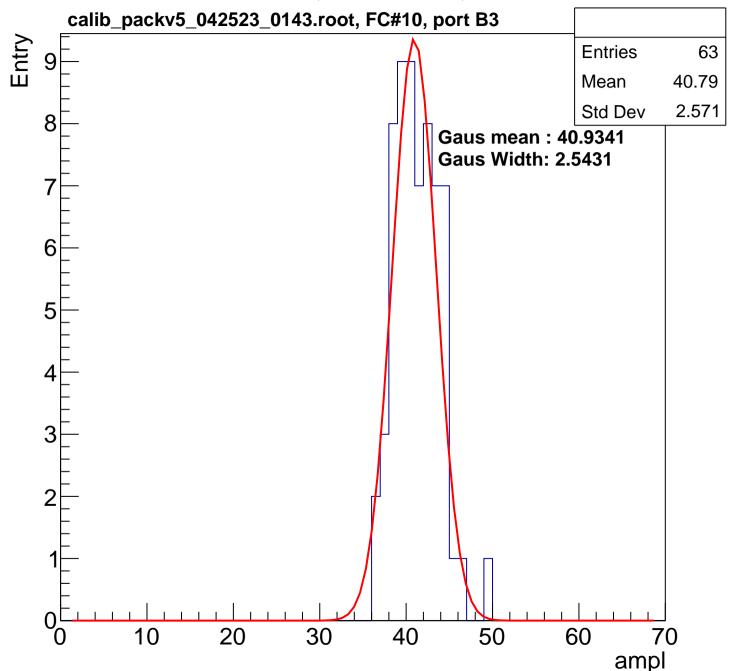


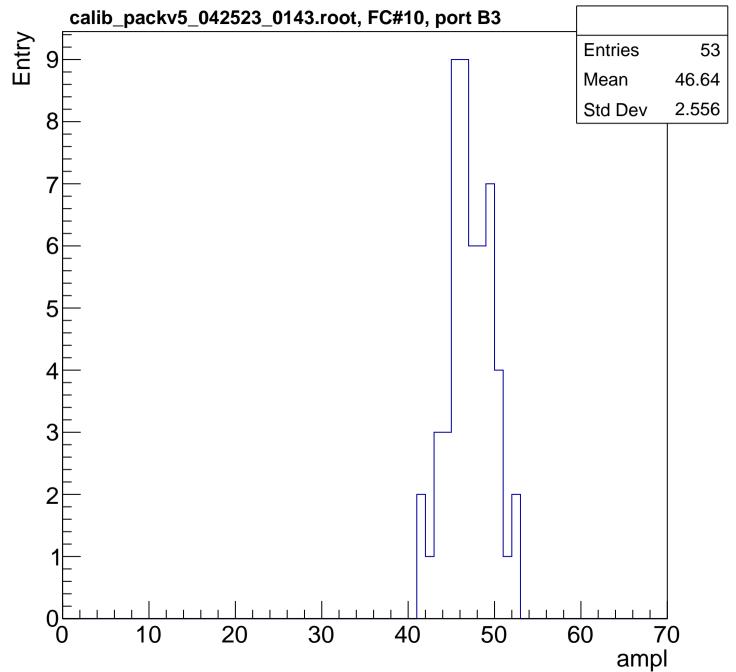


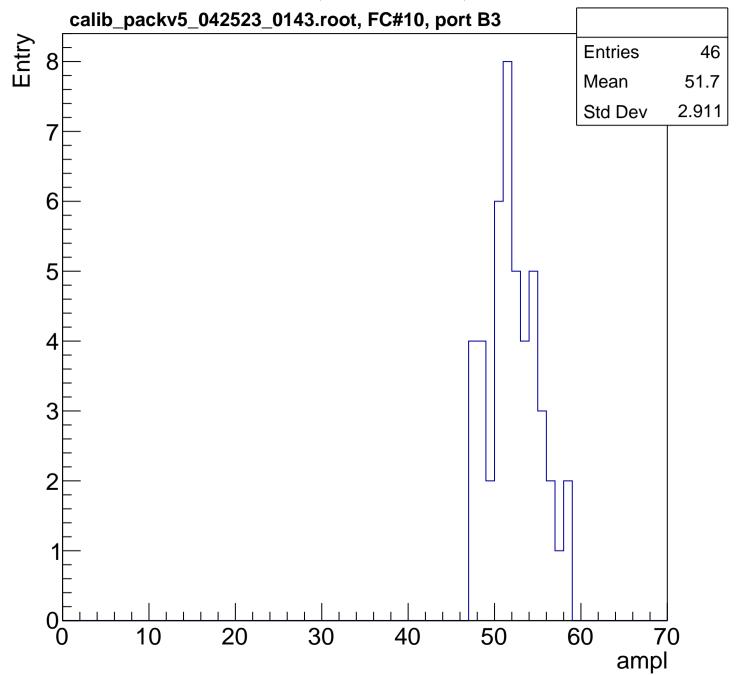


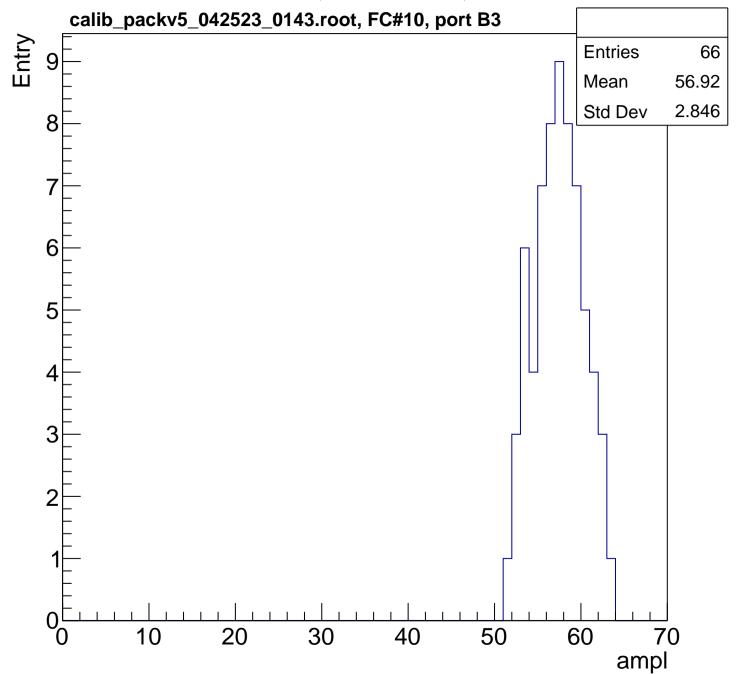


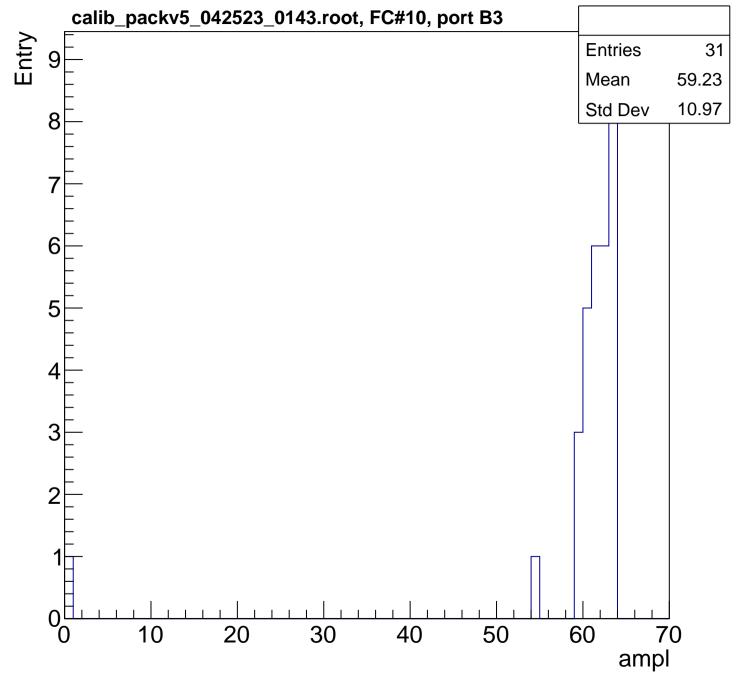


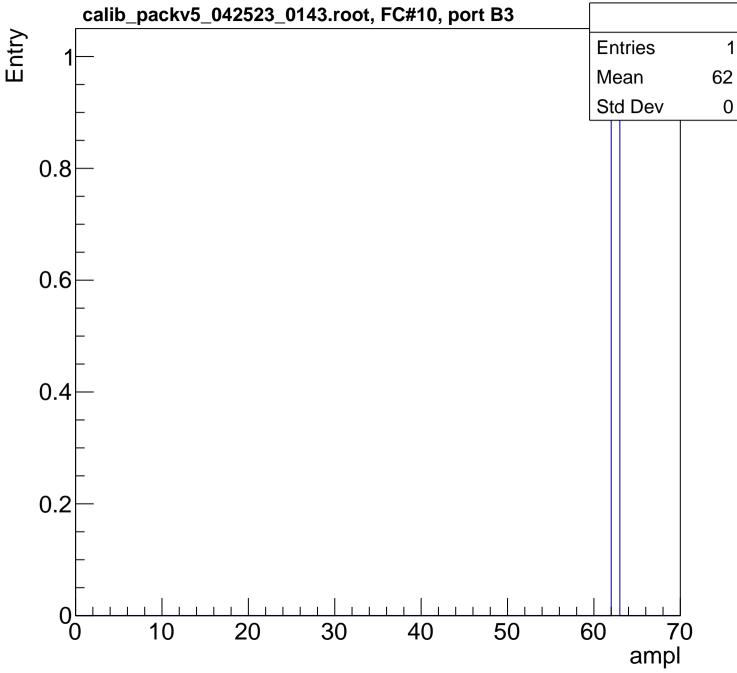


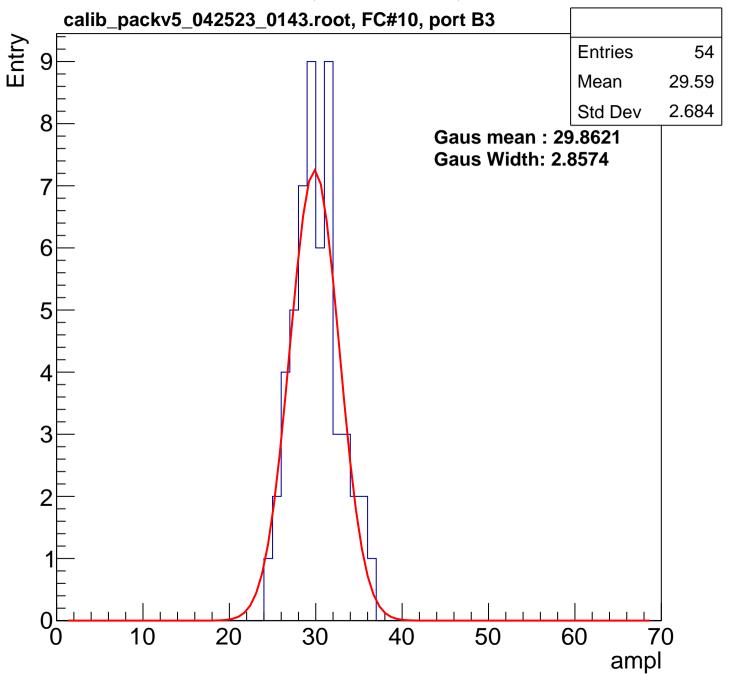


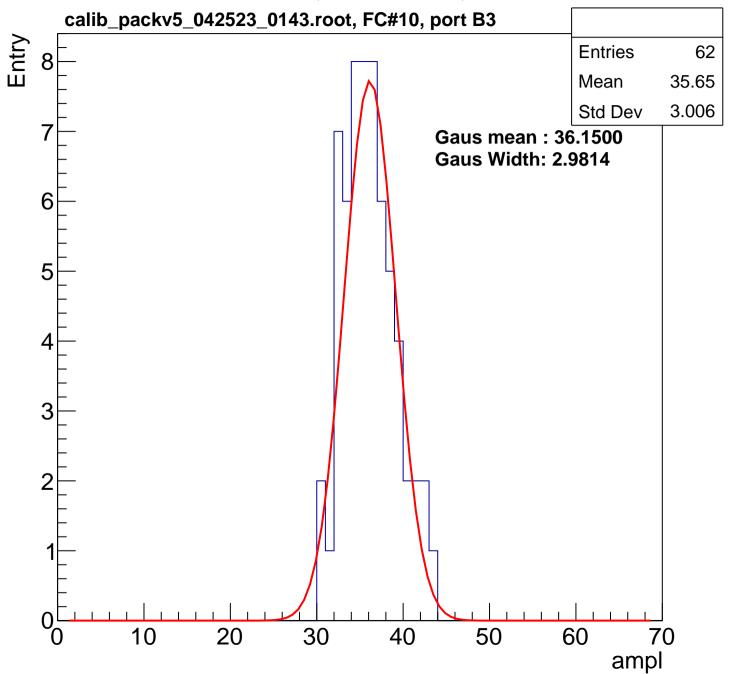


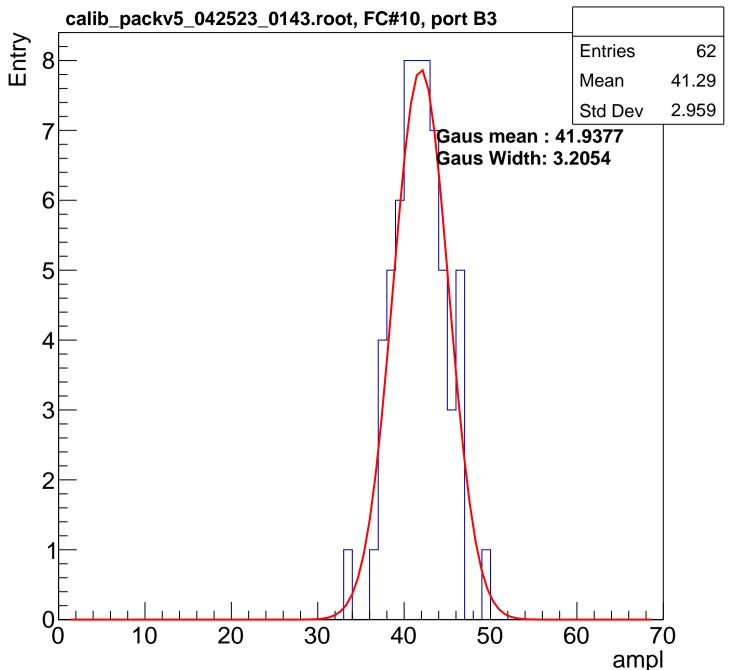


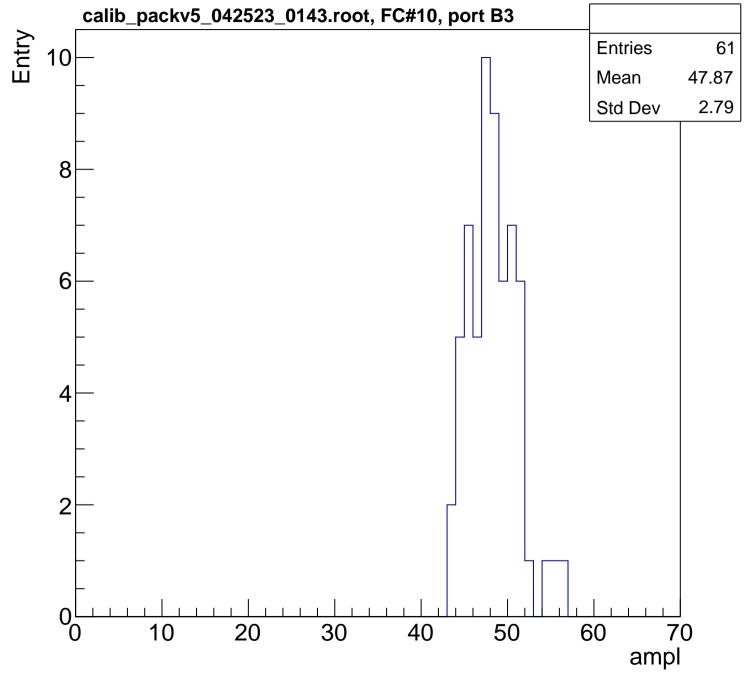


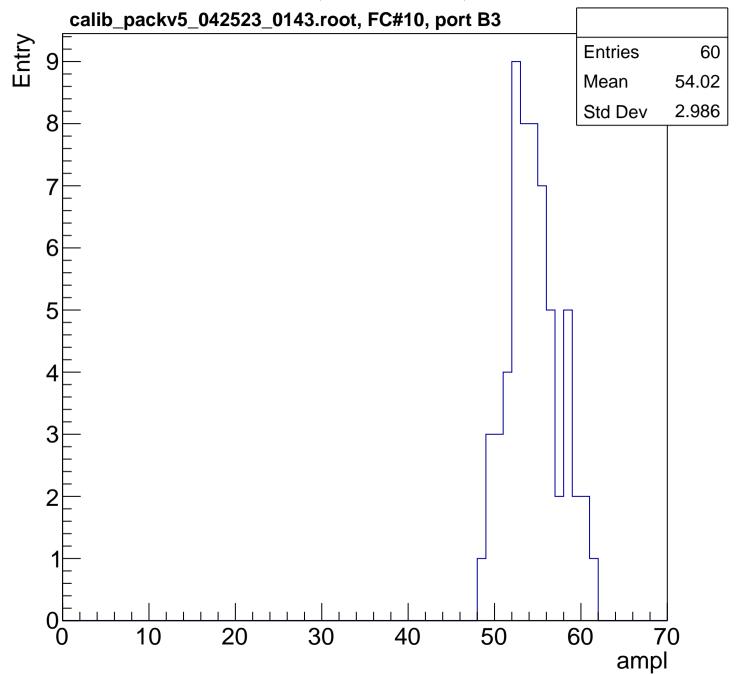


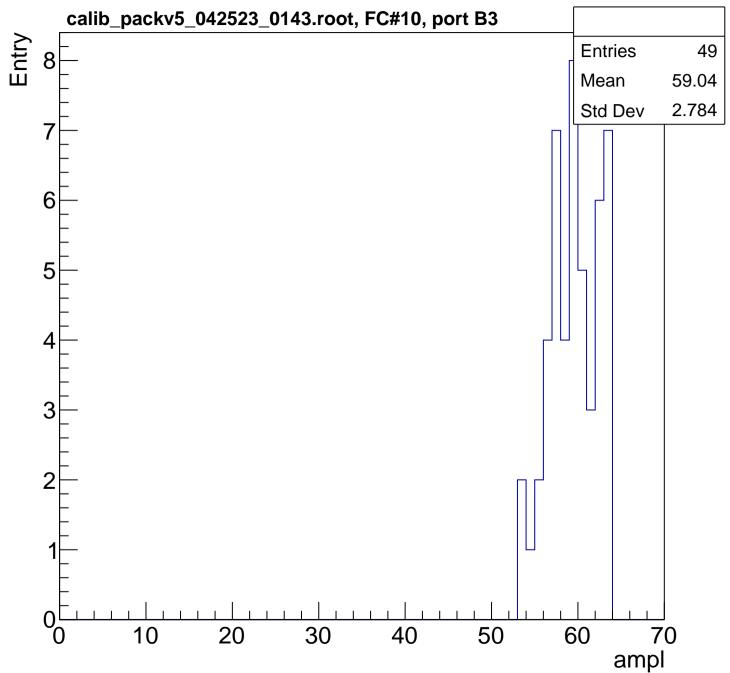


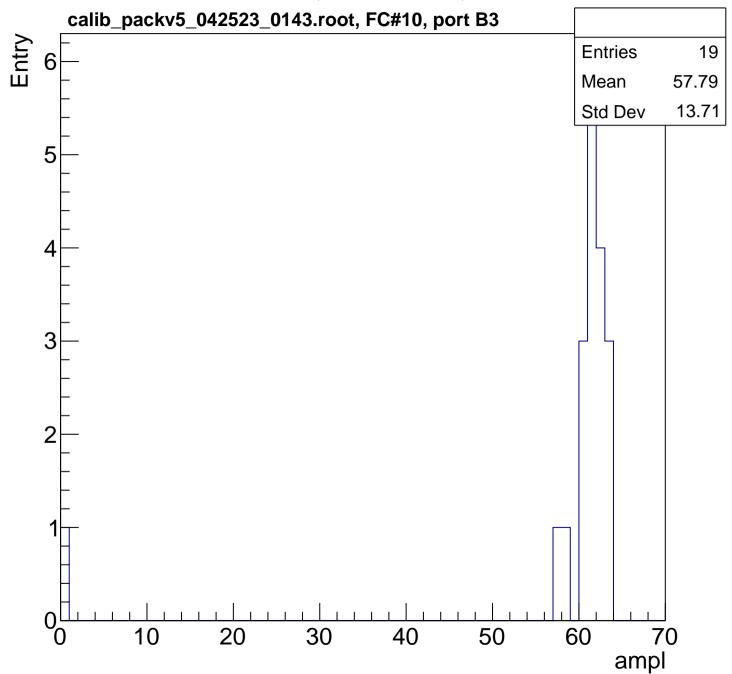


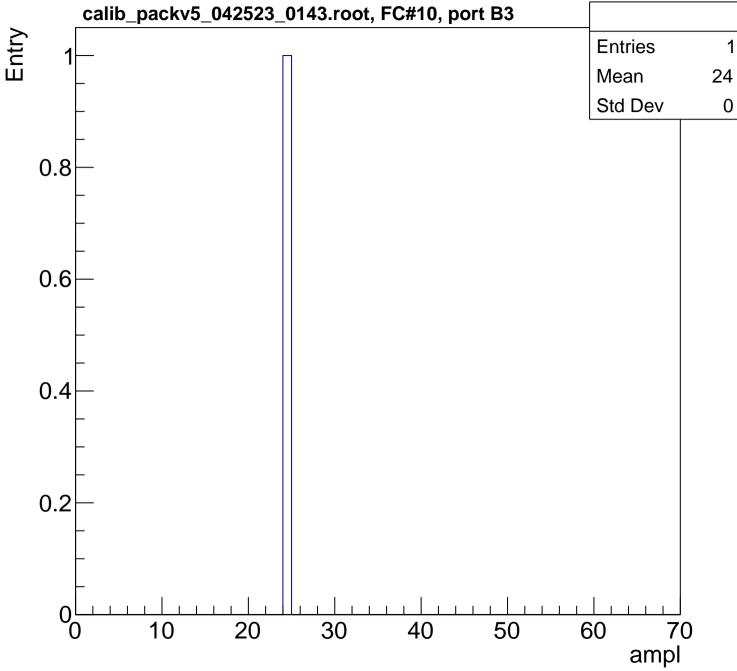


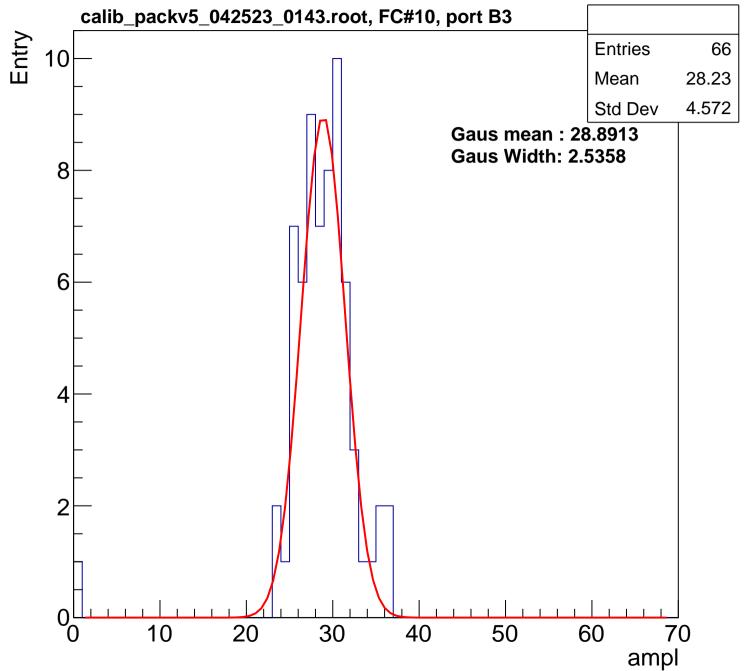


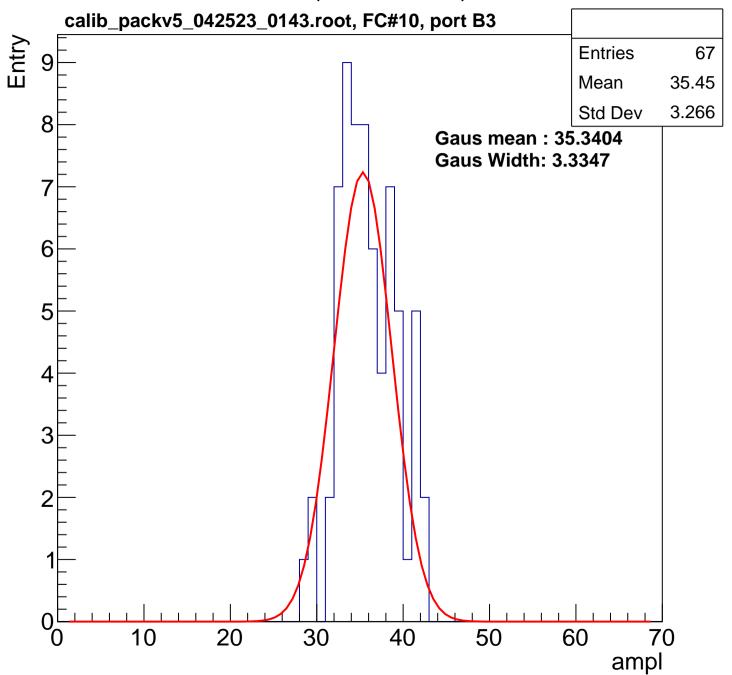


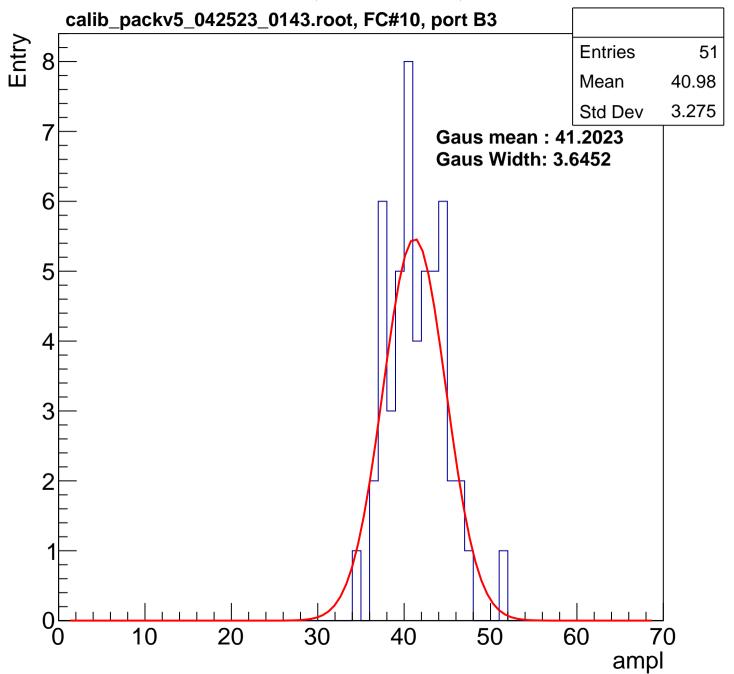


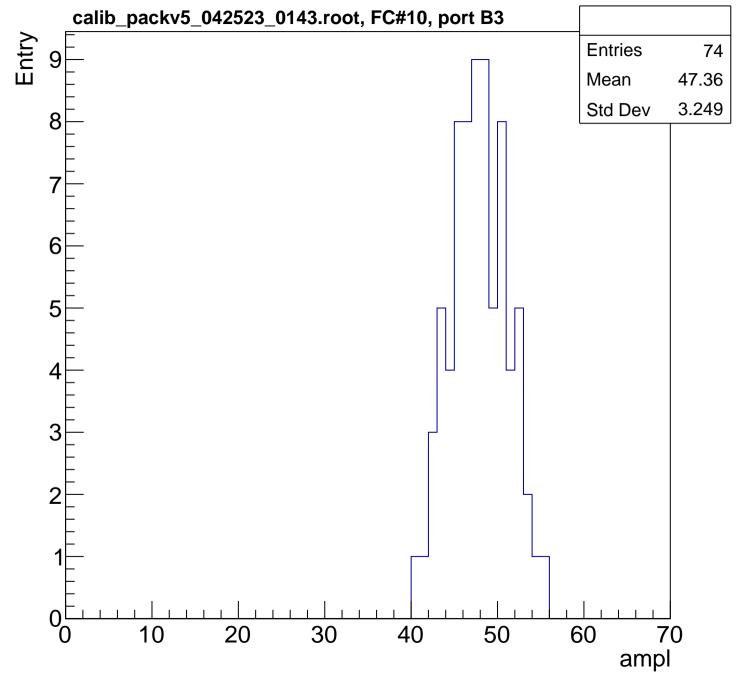


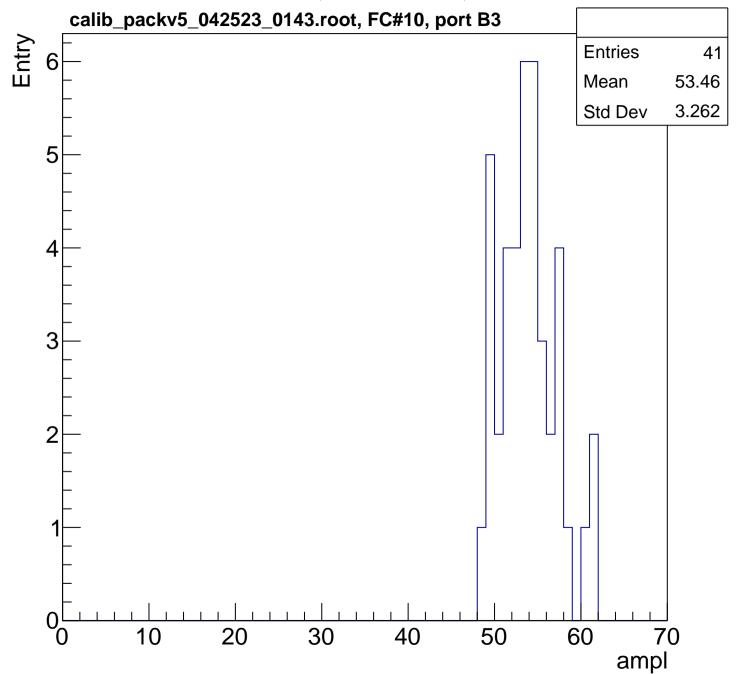


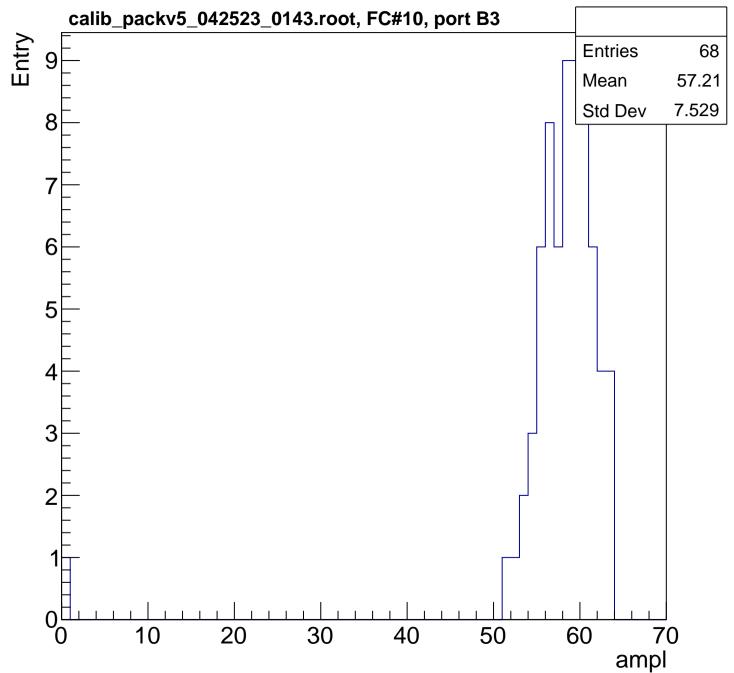


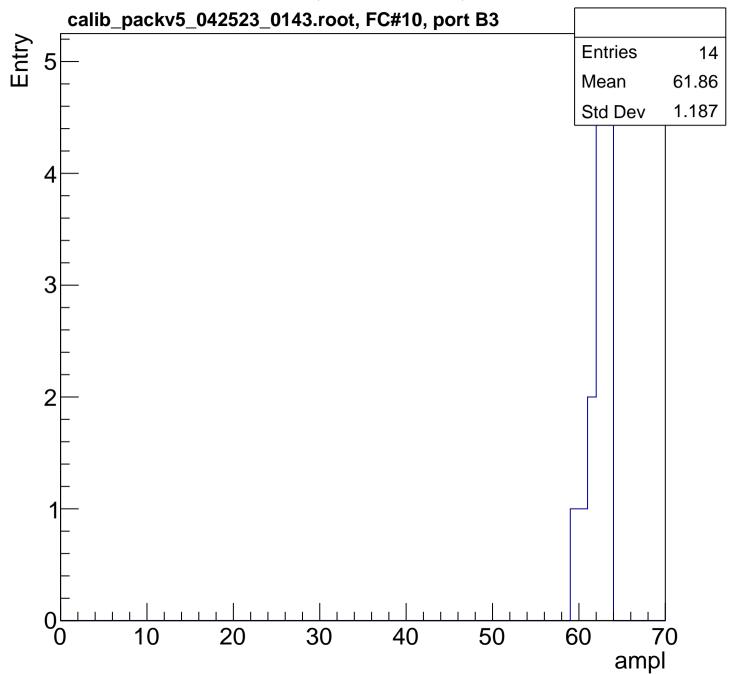


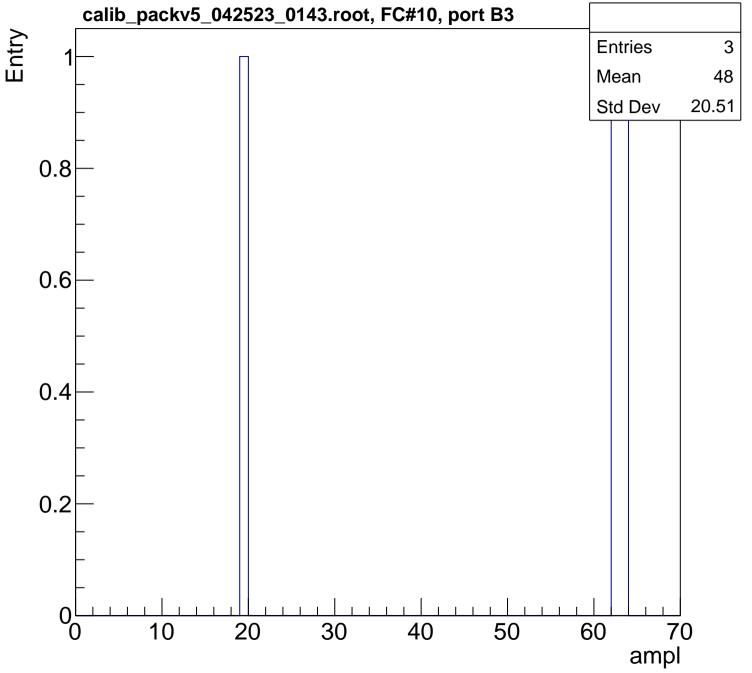


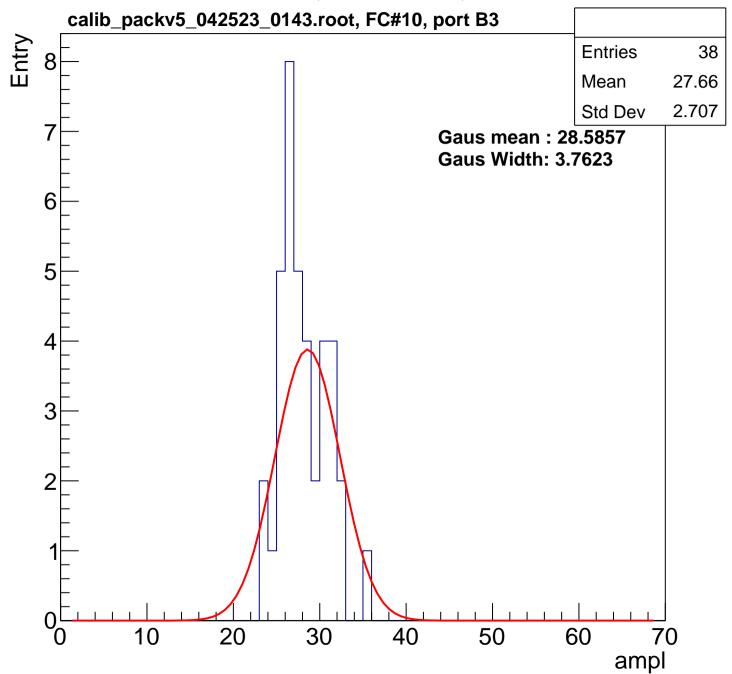


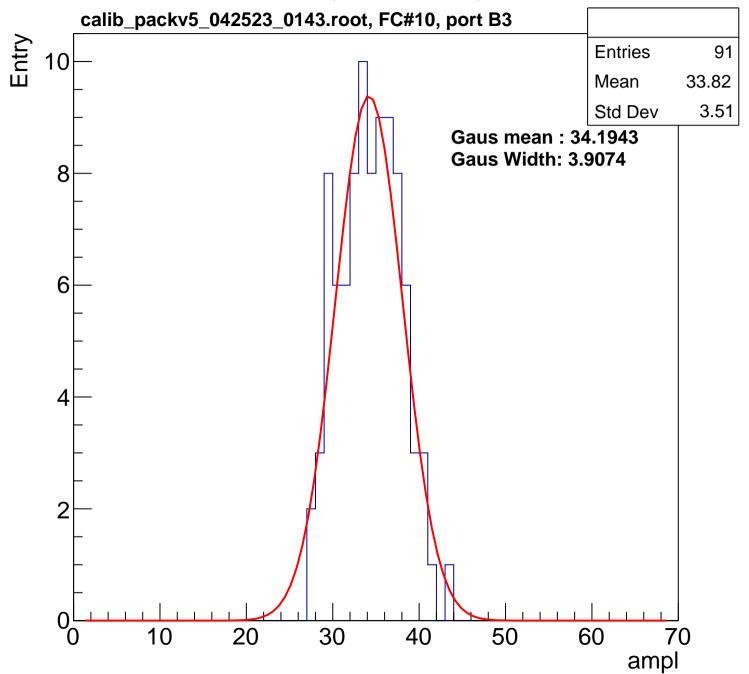


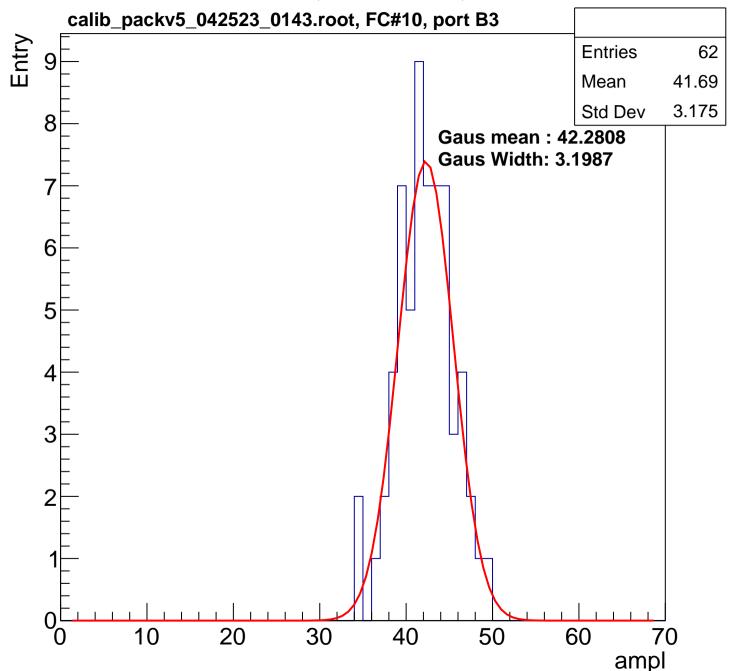


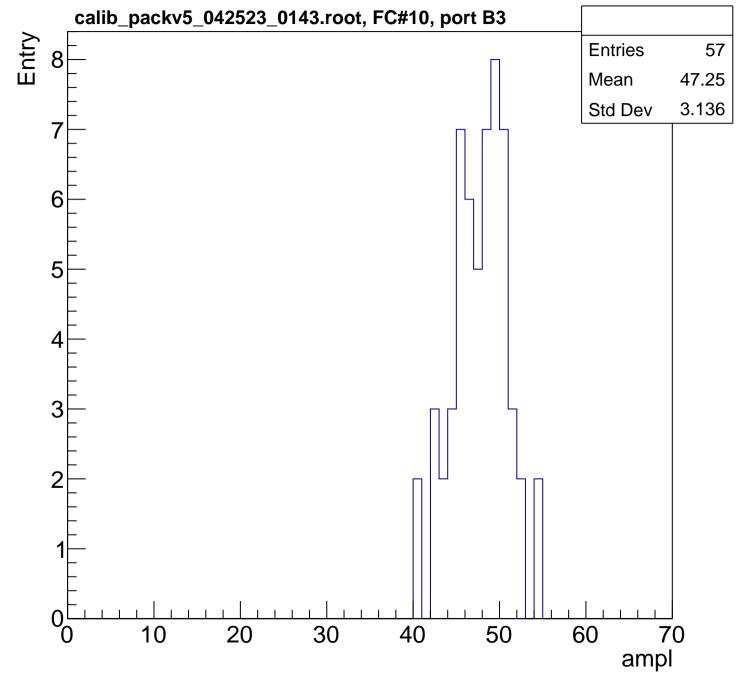


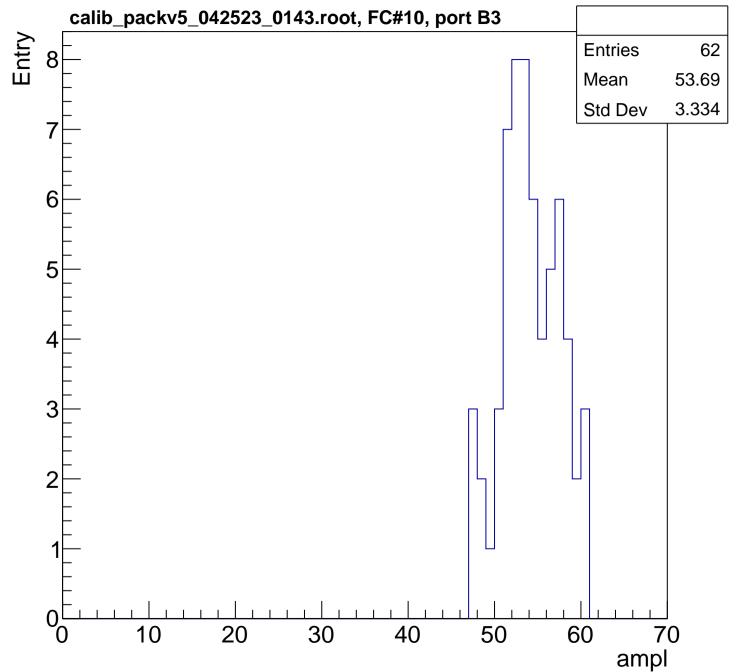


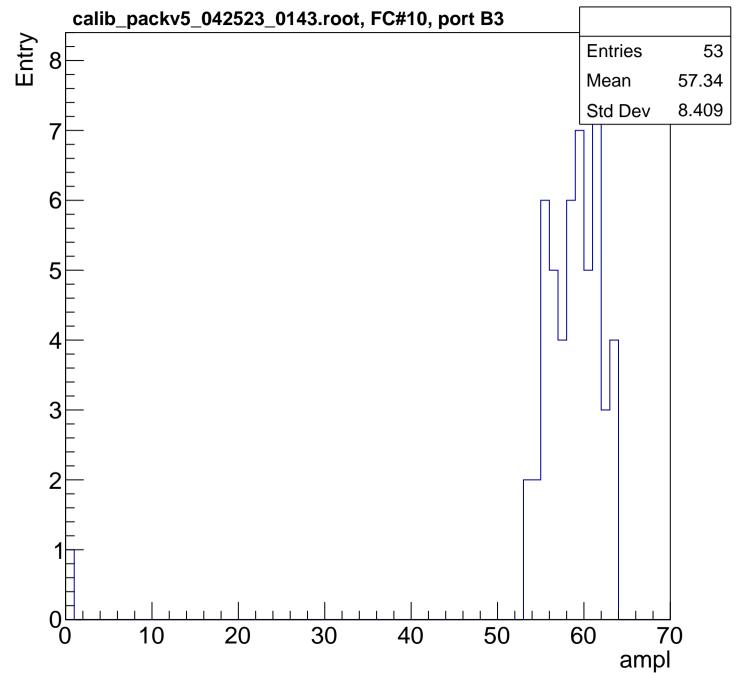


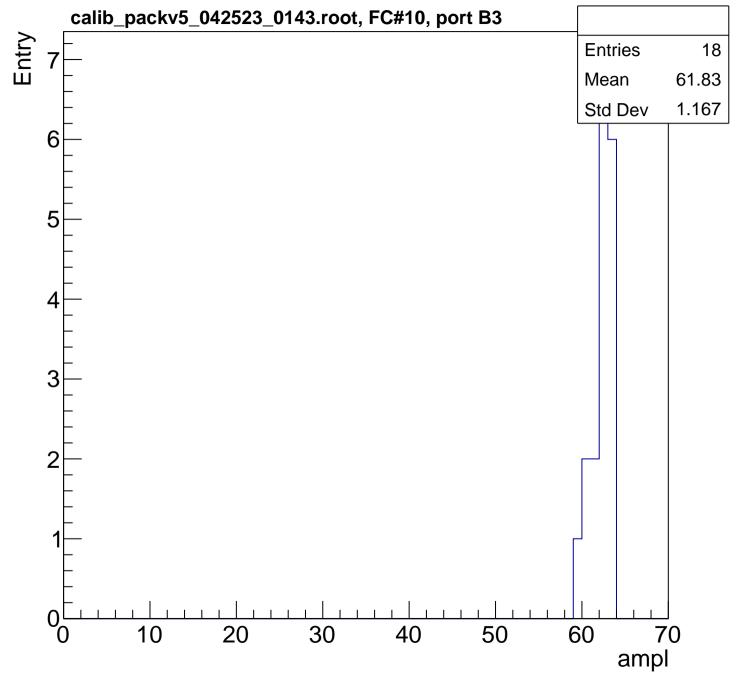




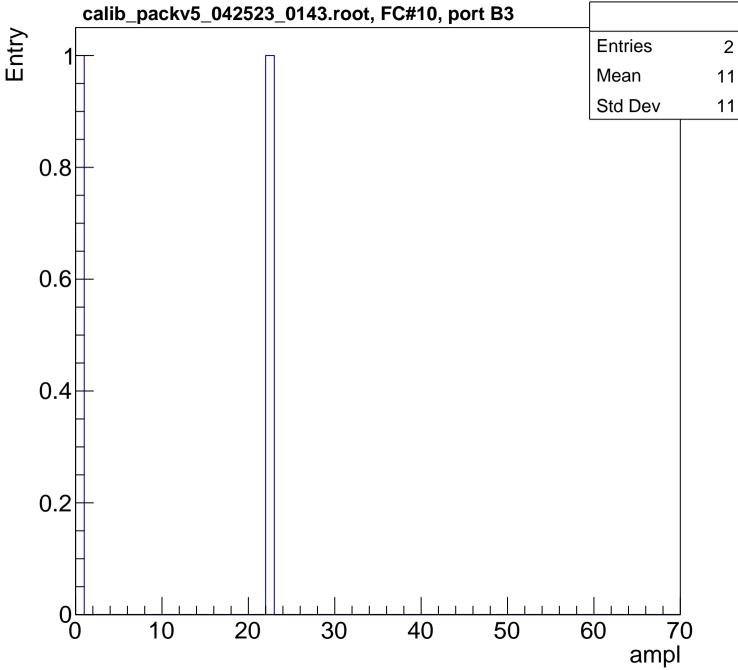


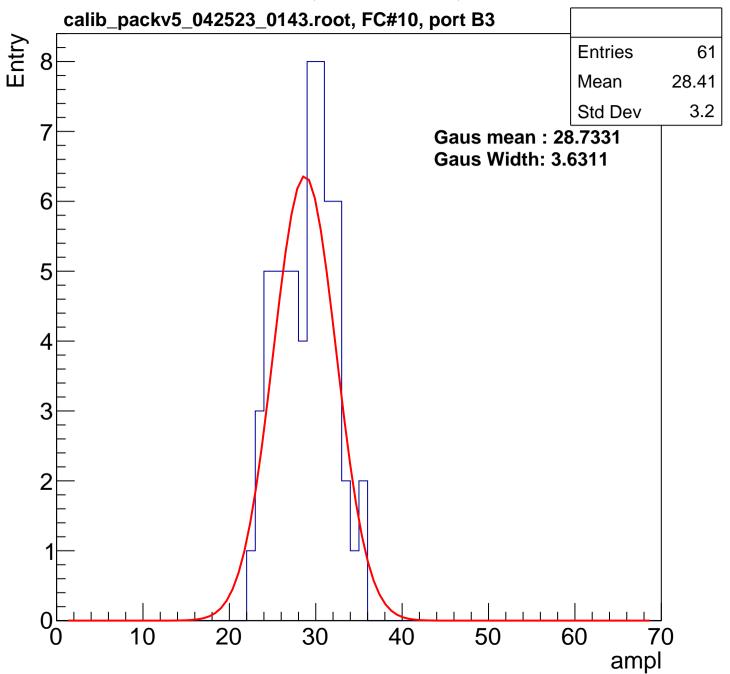


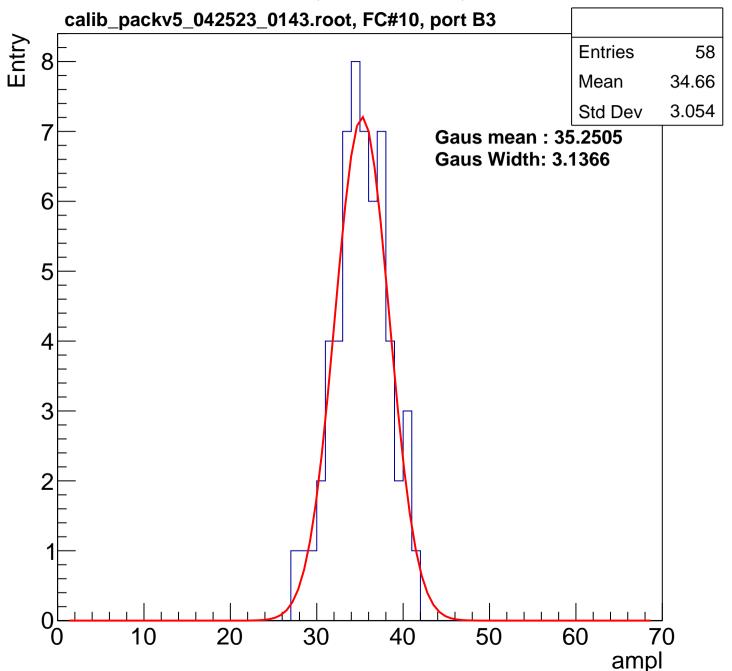


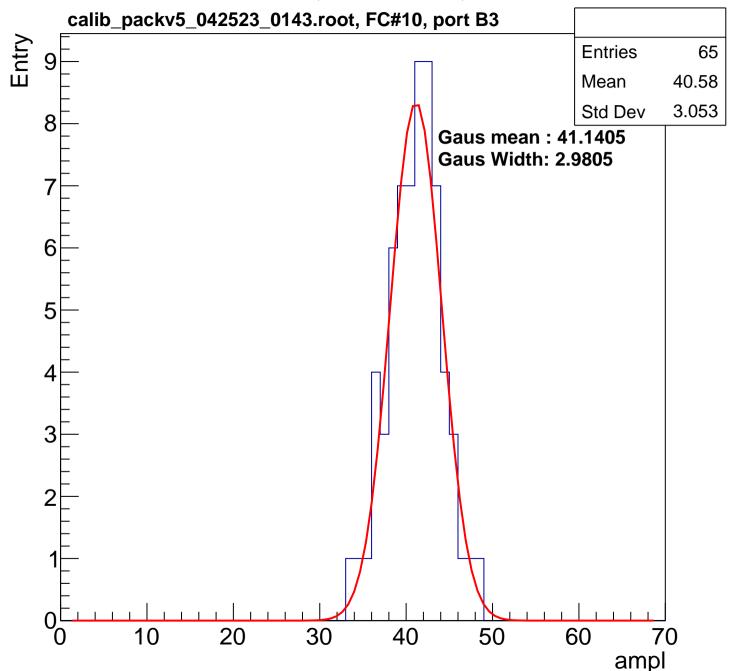


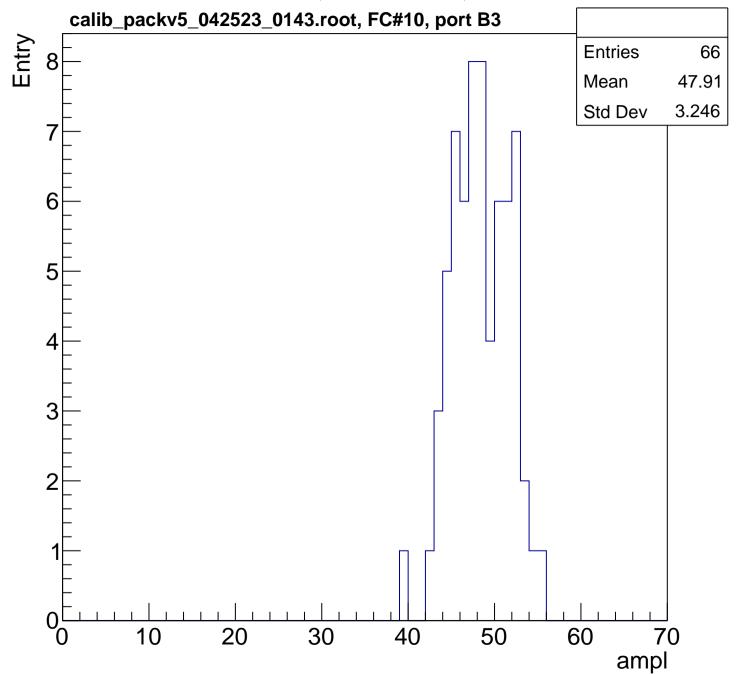
2

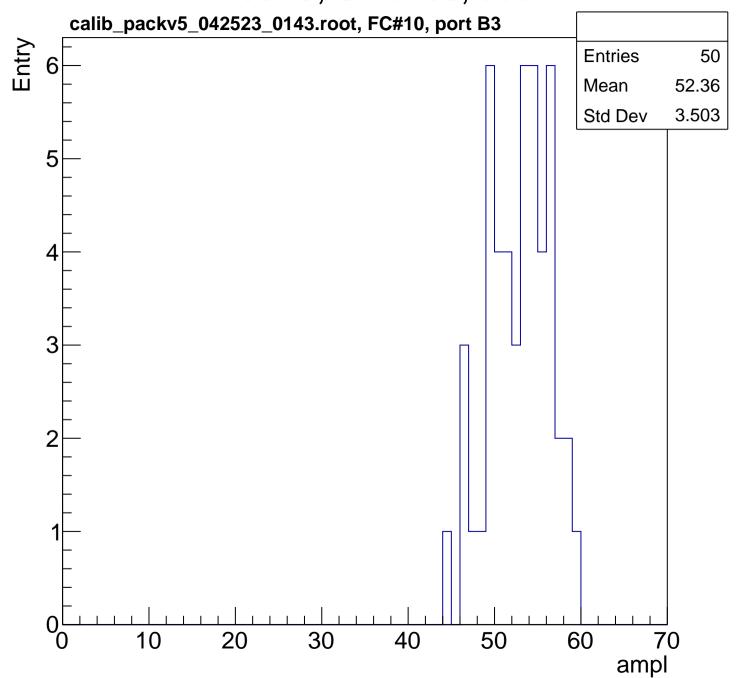


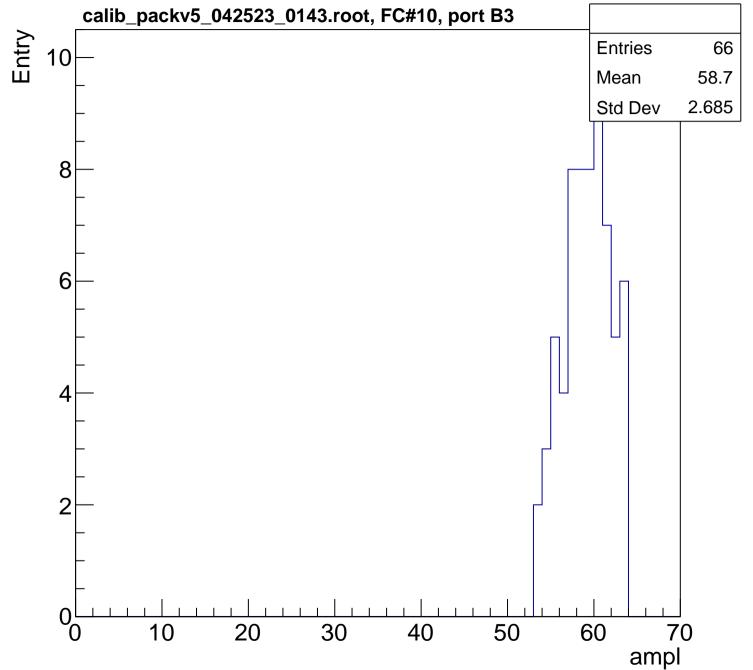


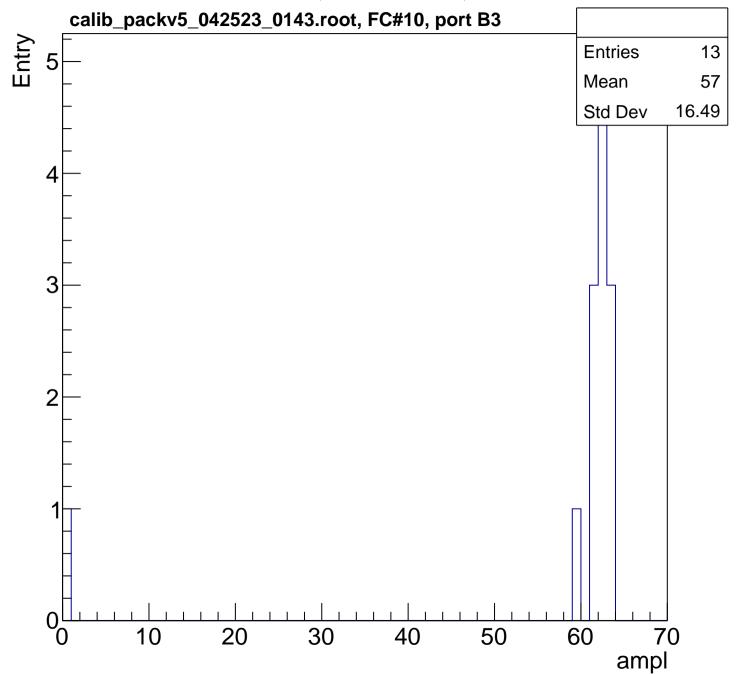




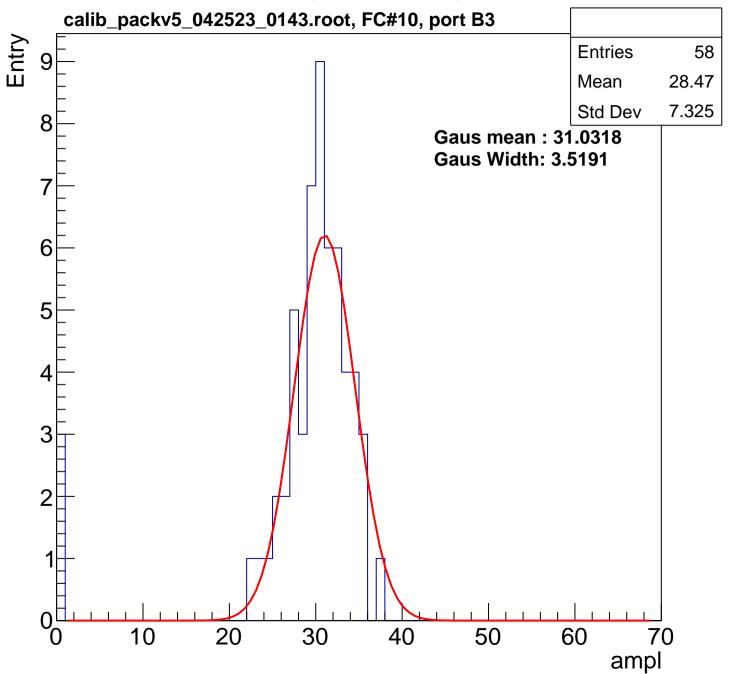


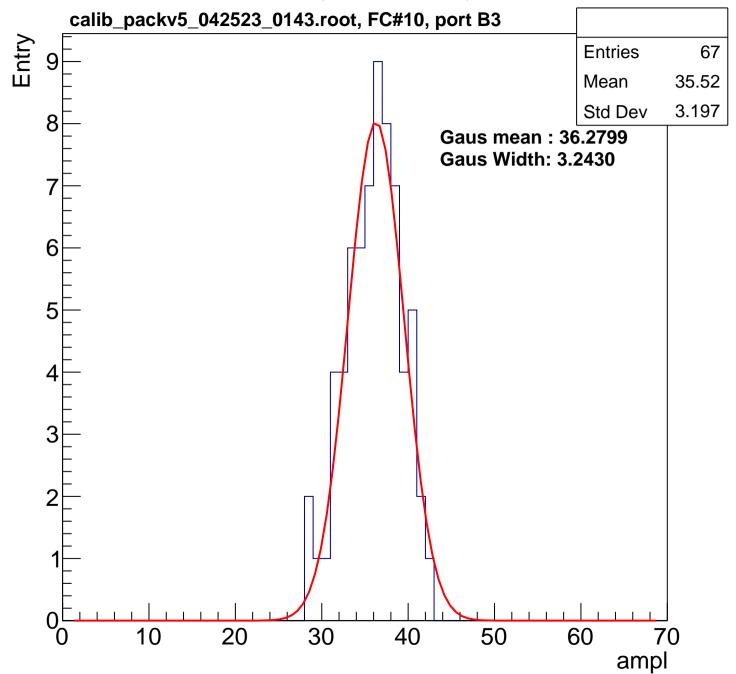


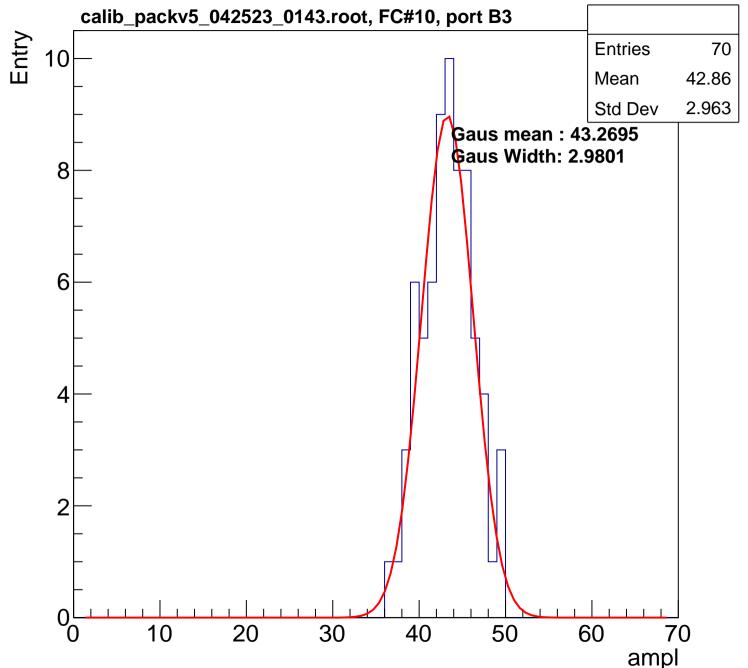


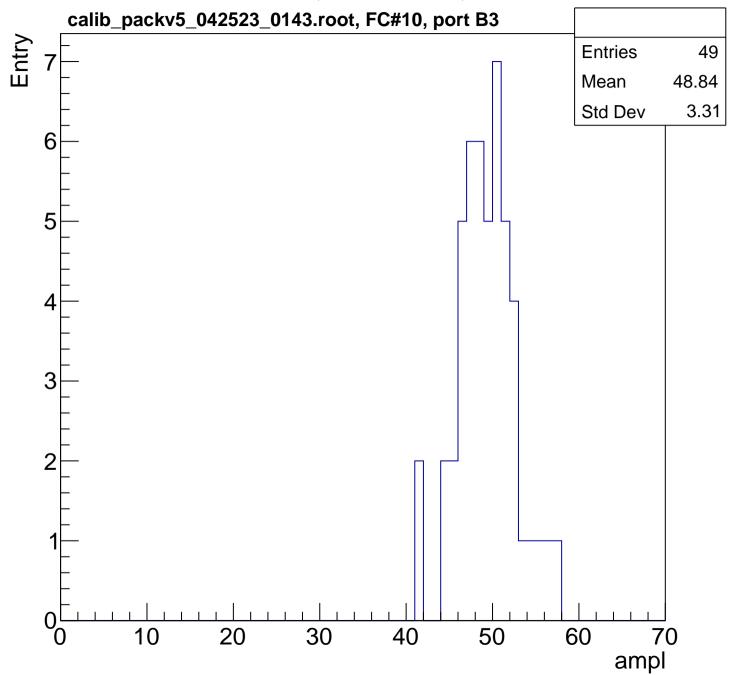


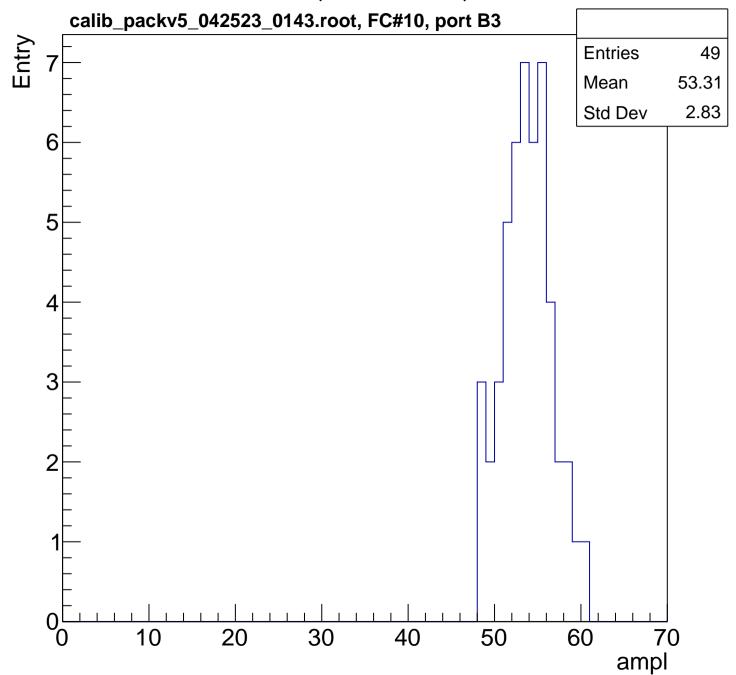


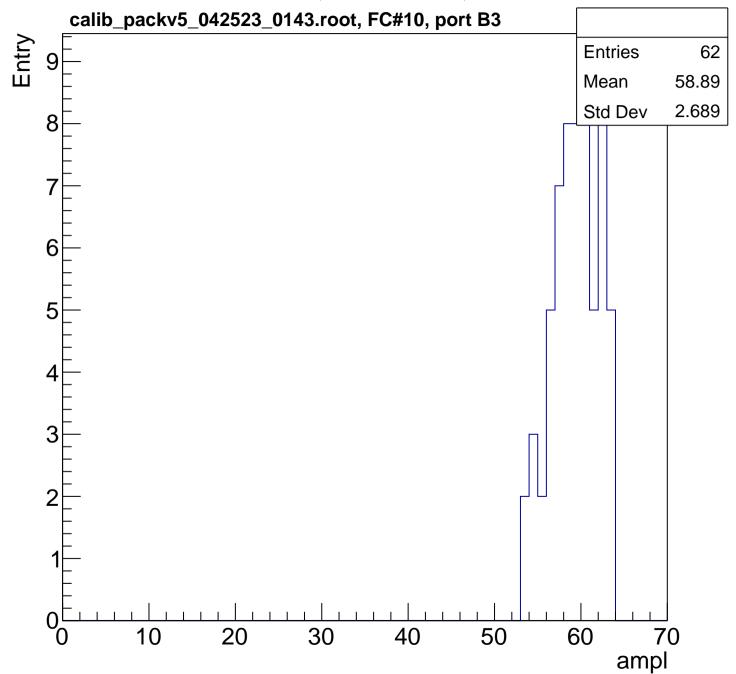


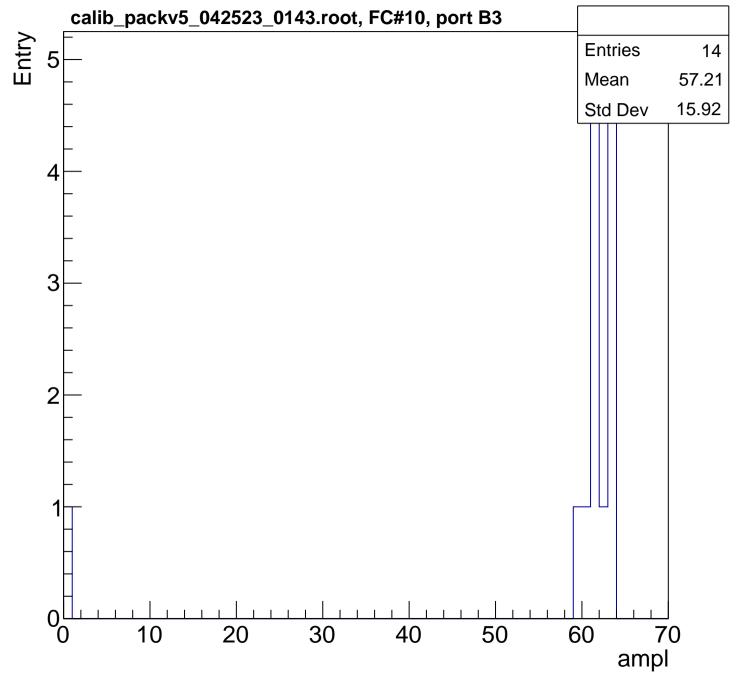


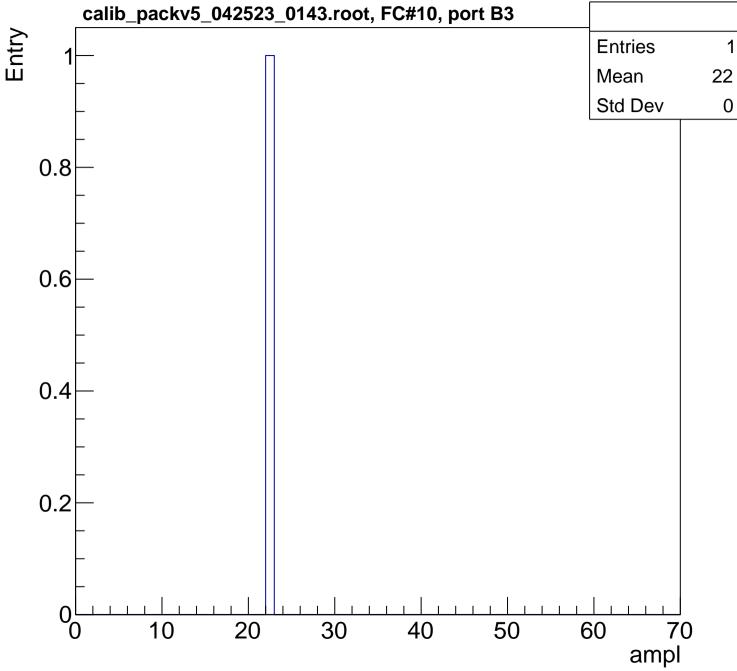


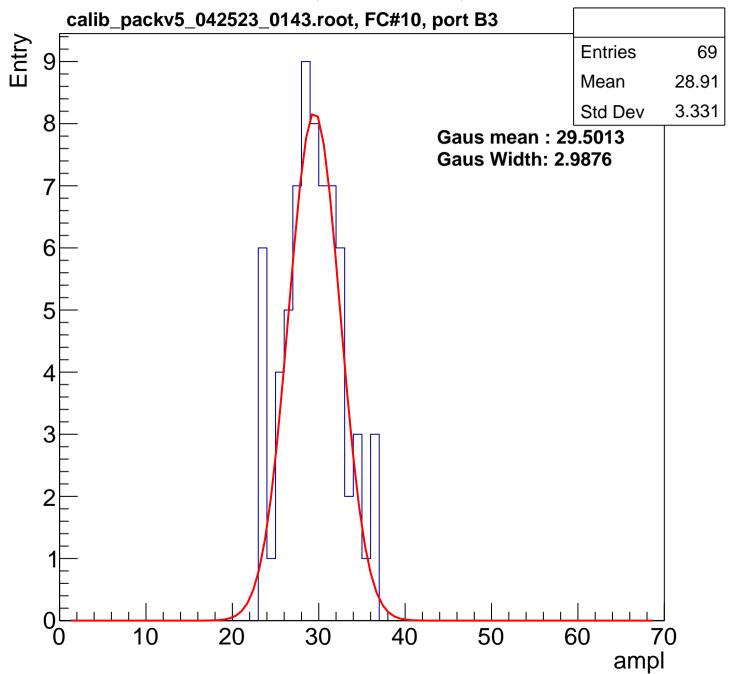


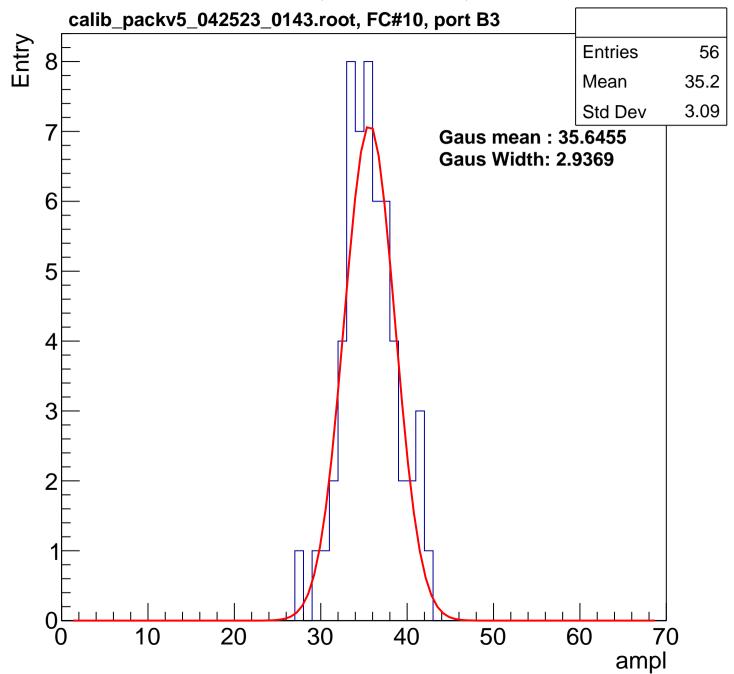


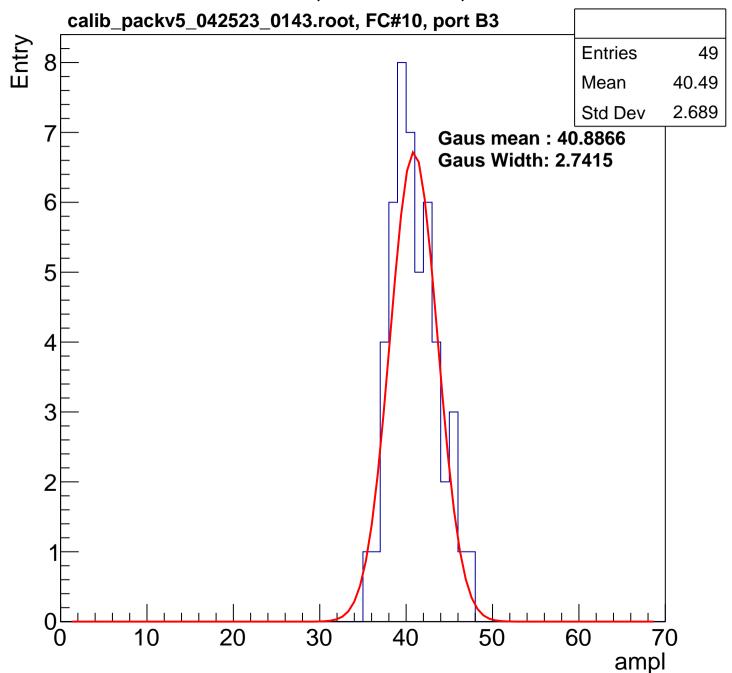


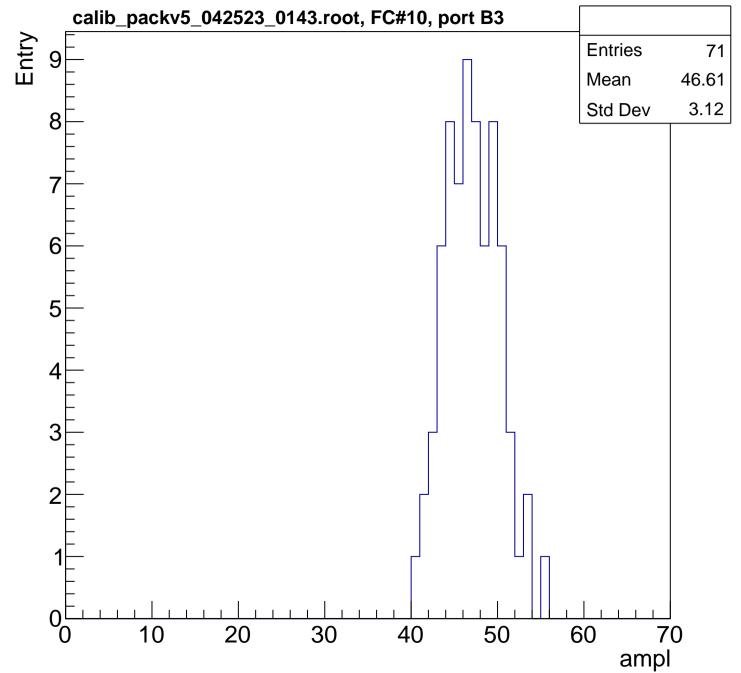


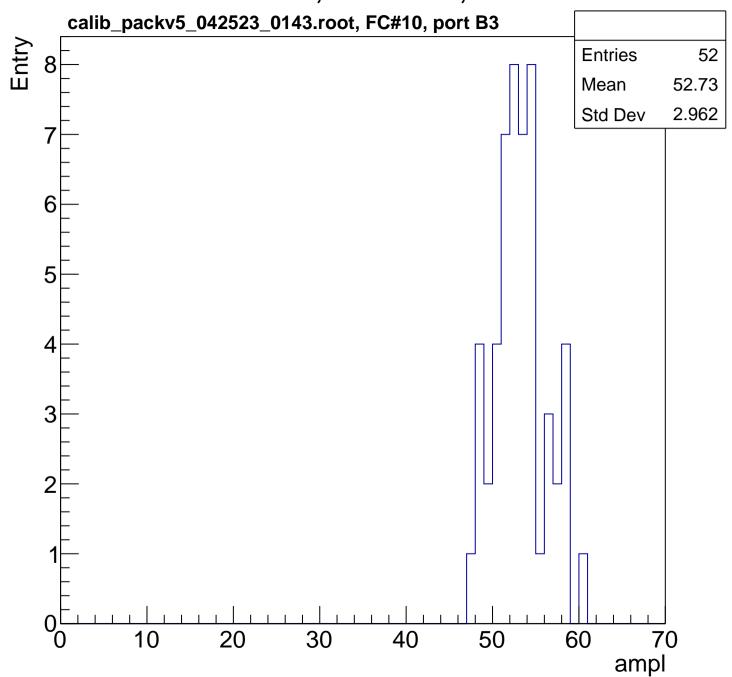


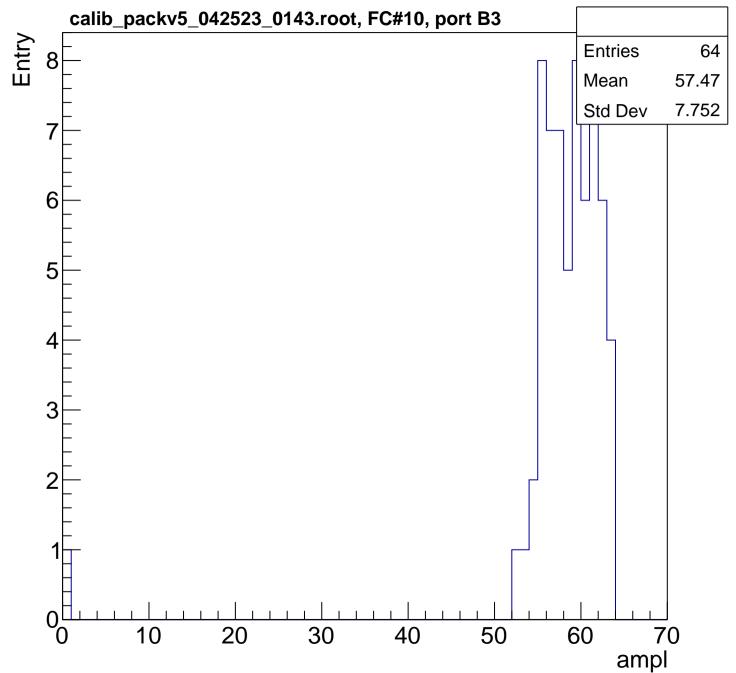


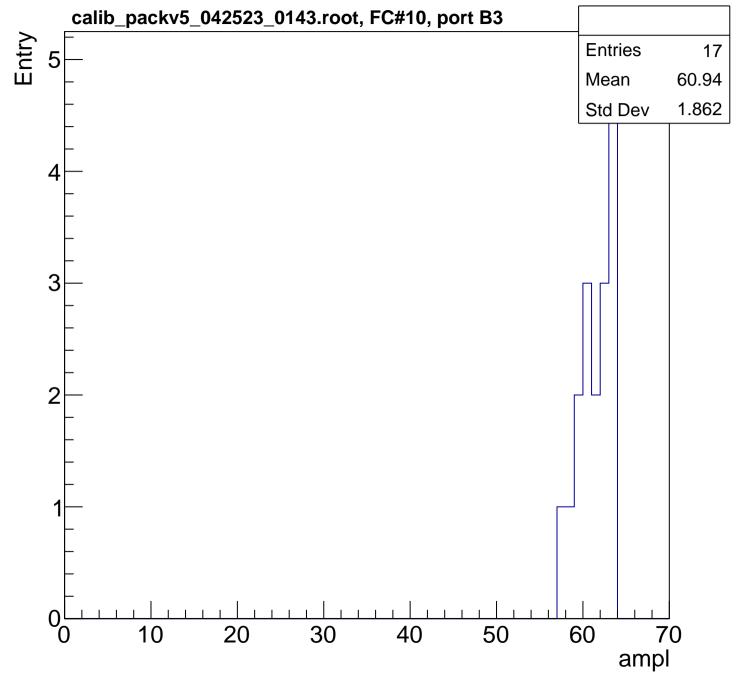


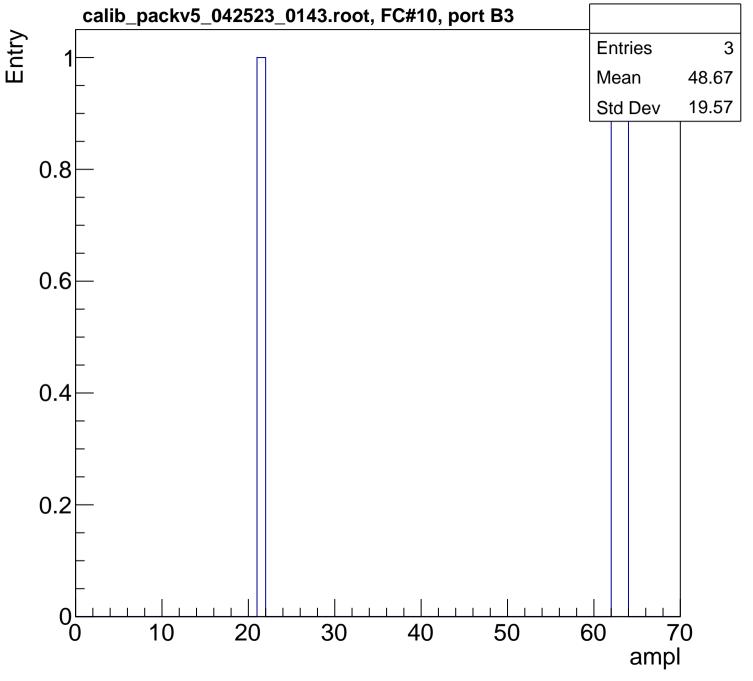


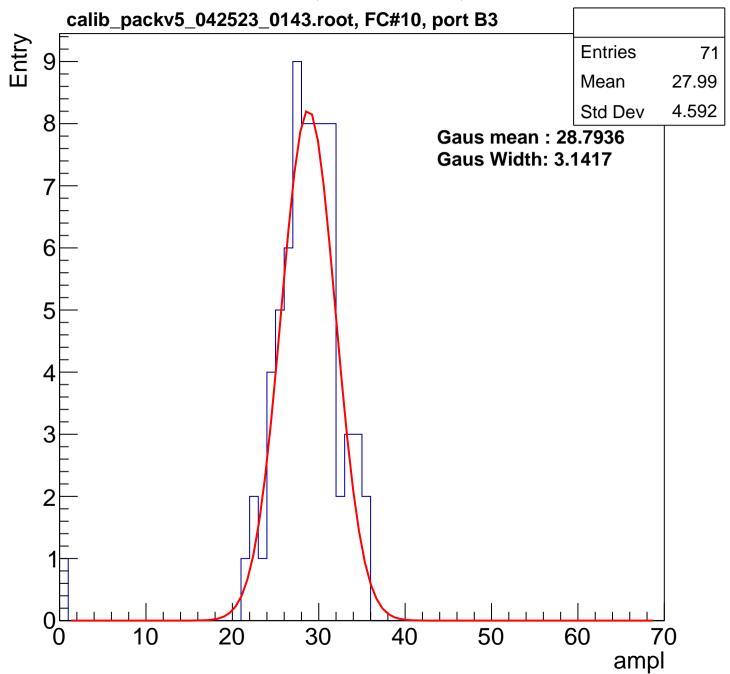


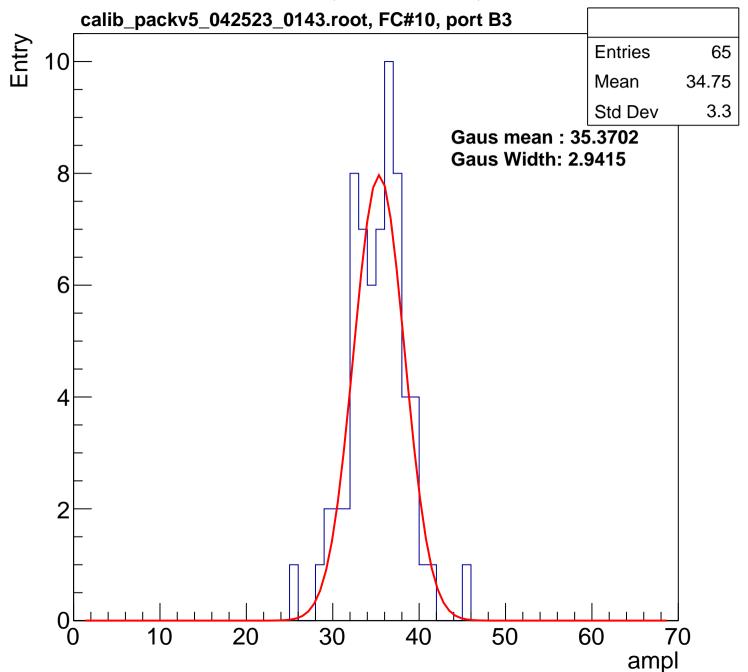


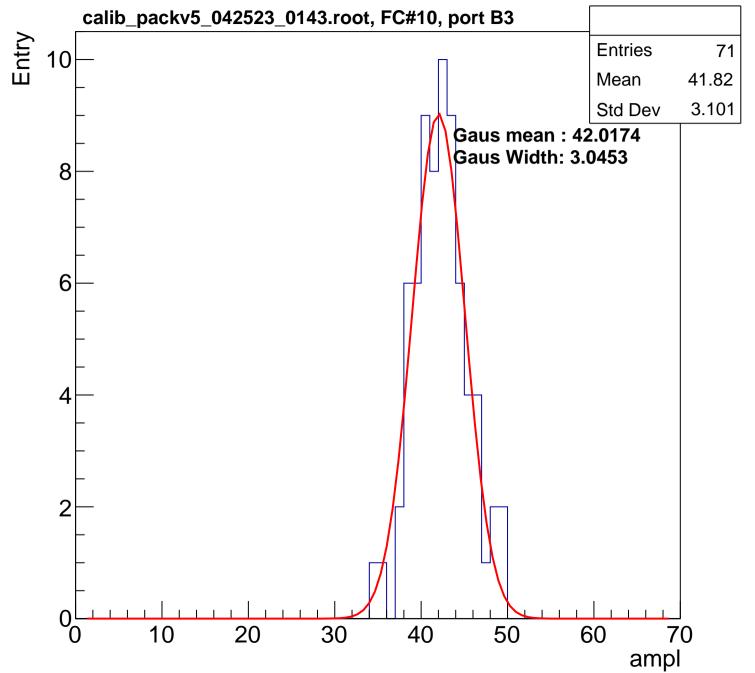


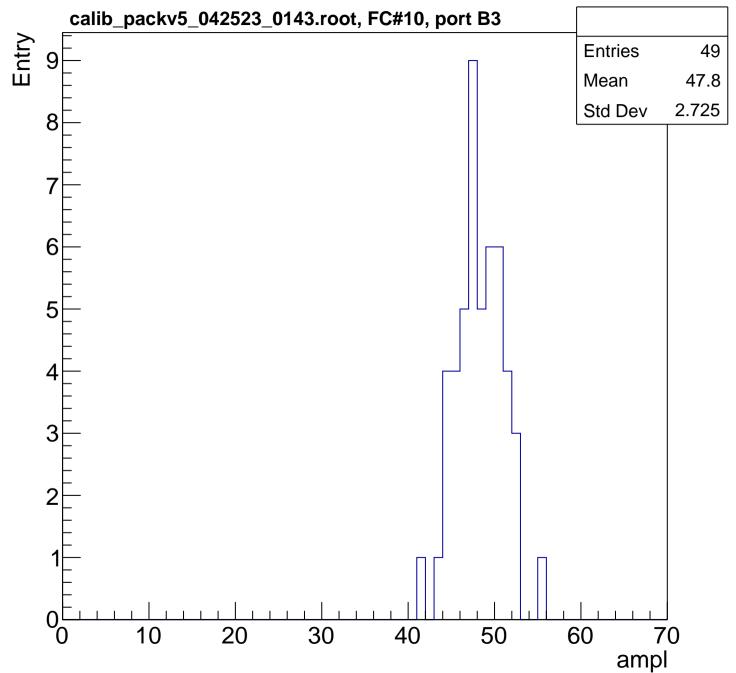


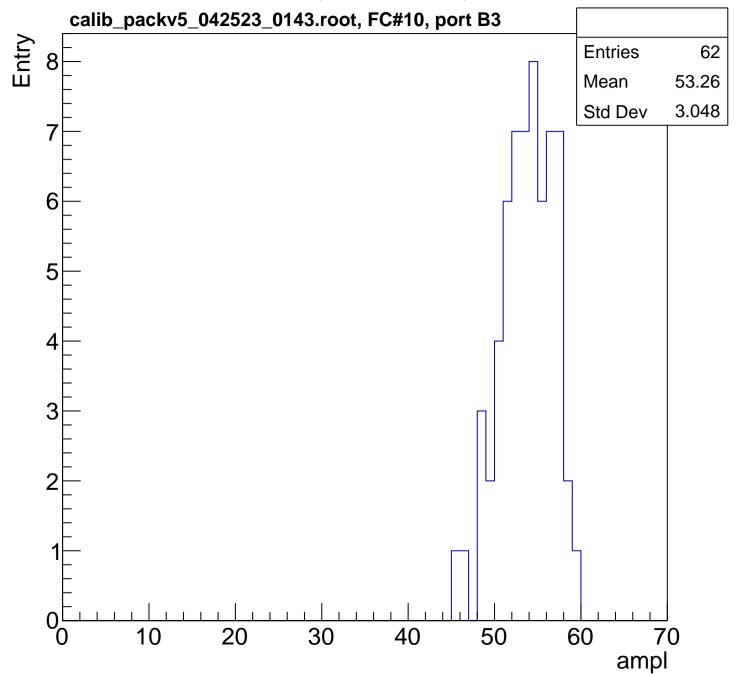


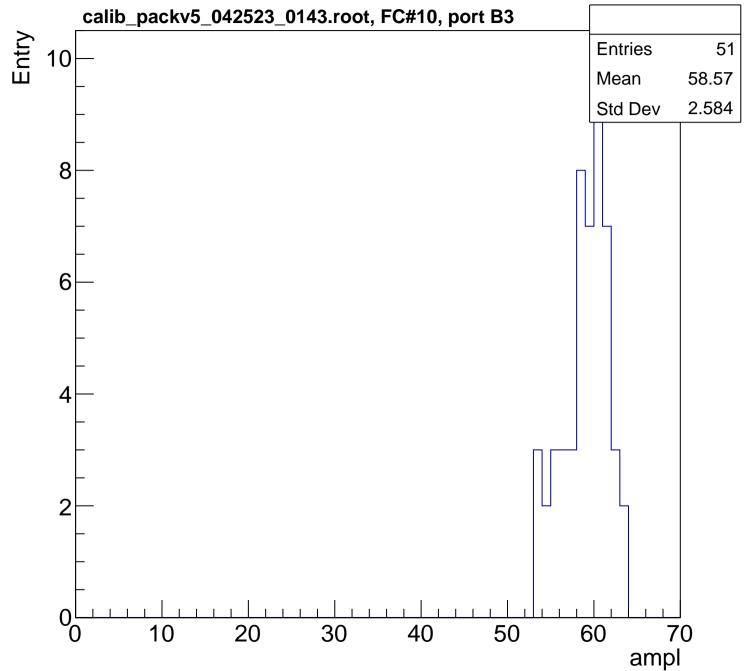


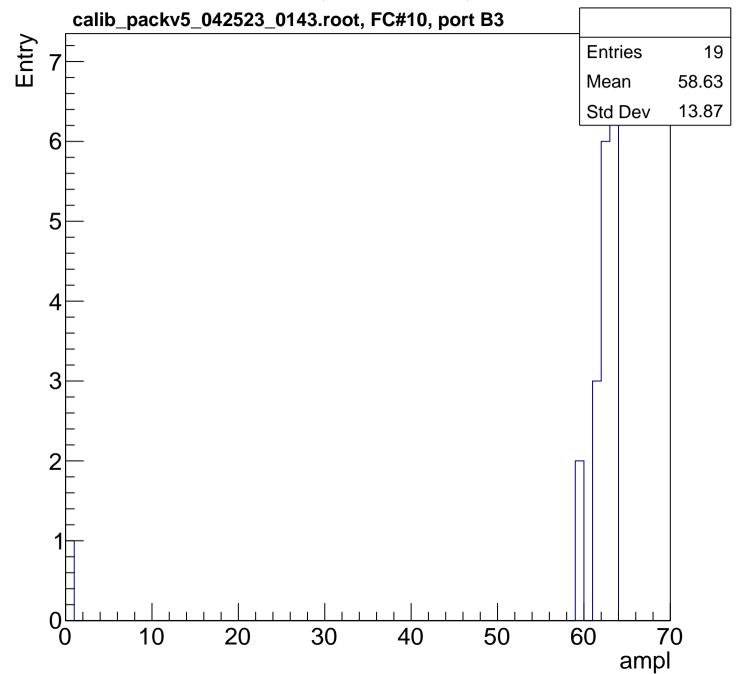


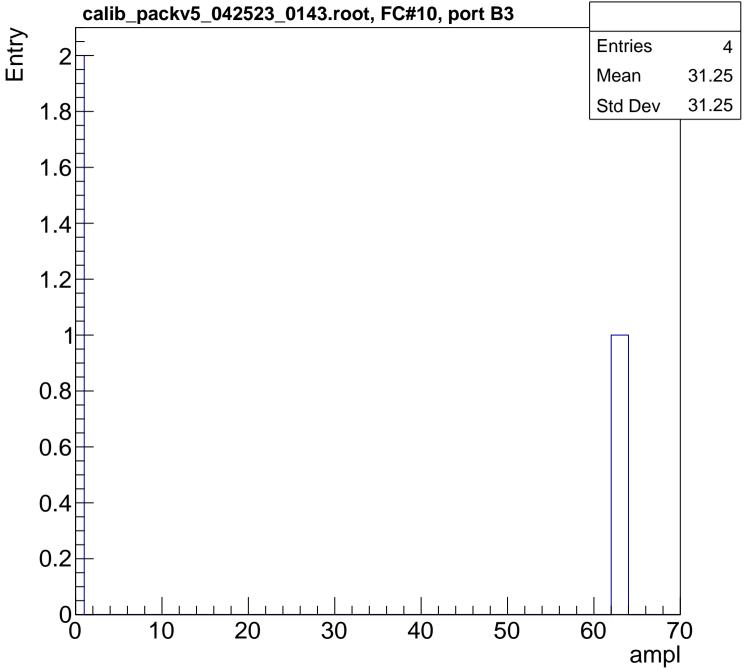


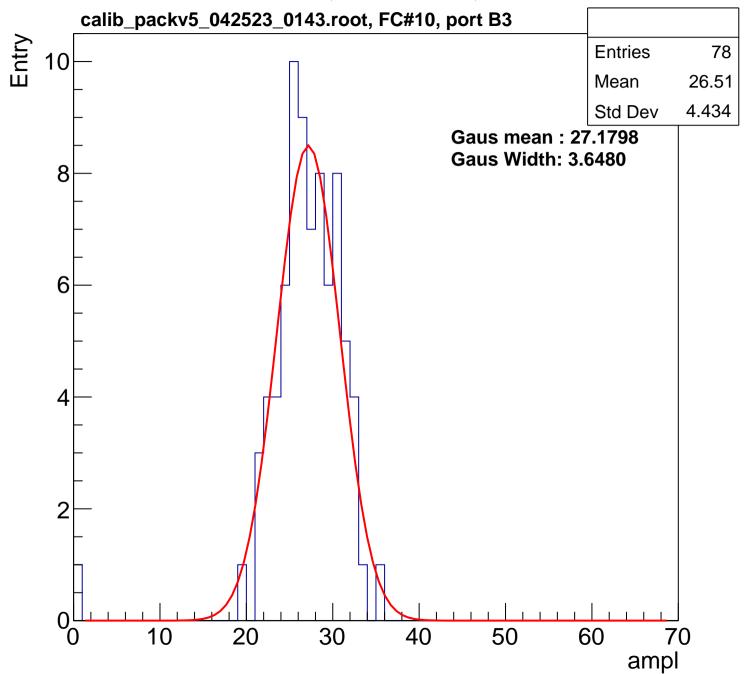


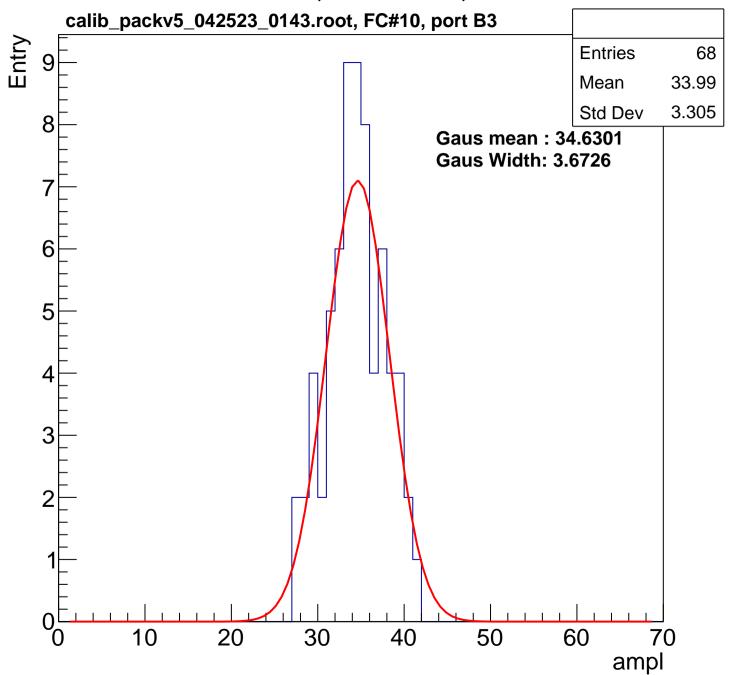


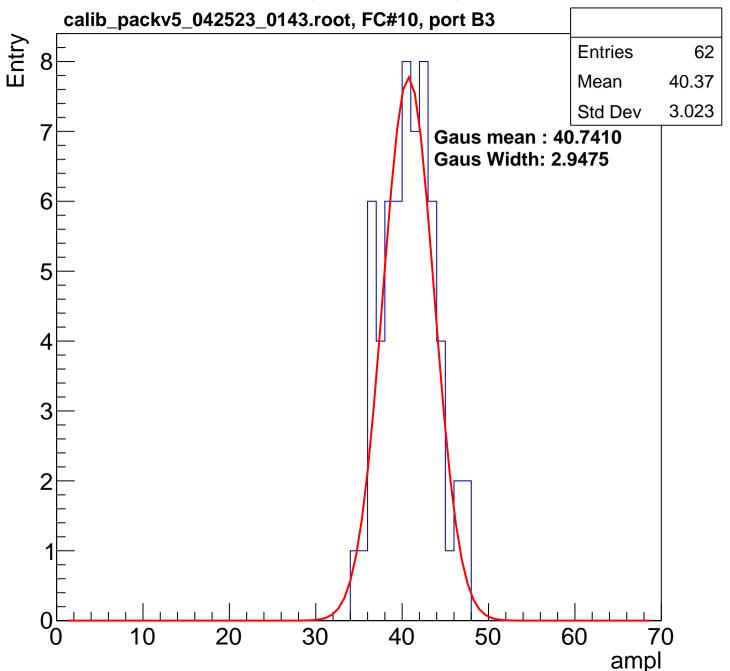


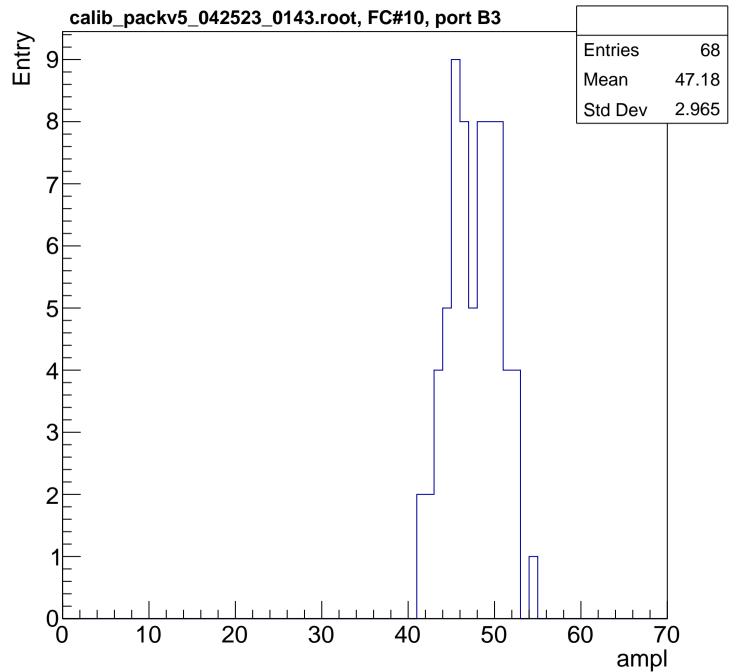


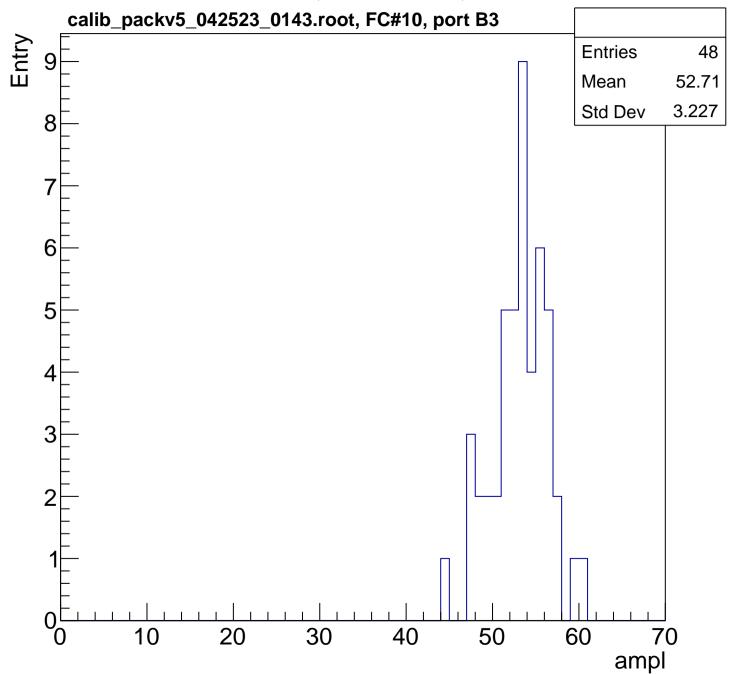


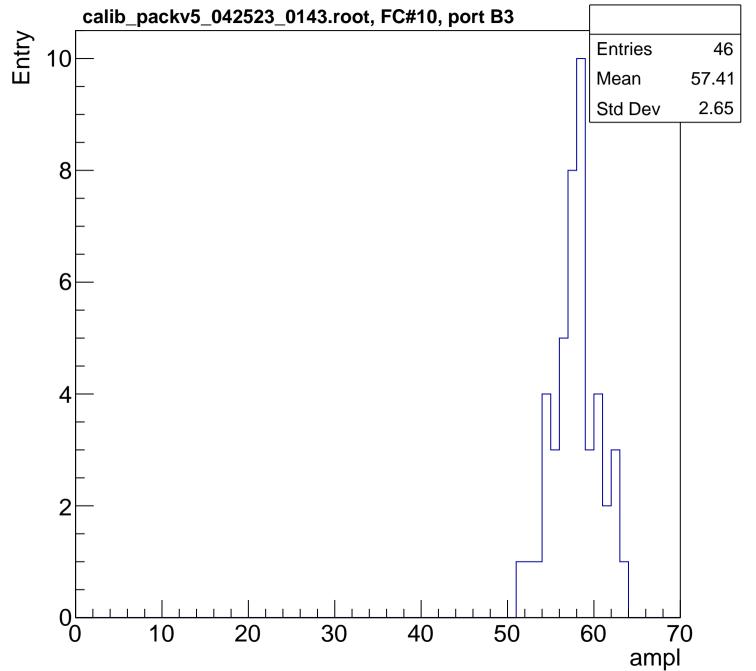


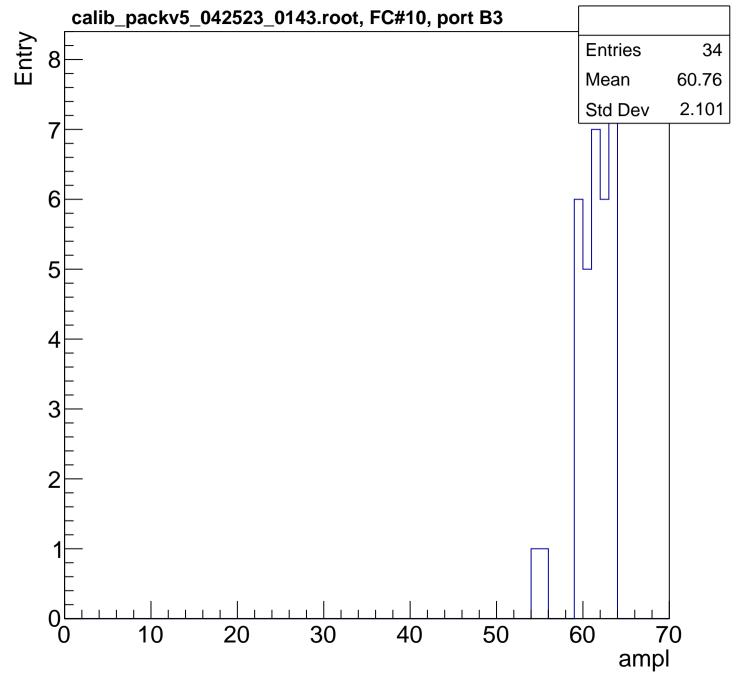


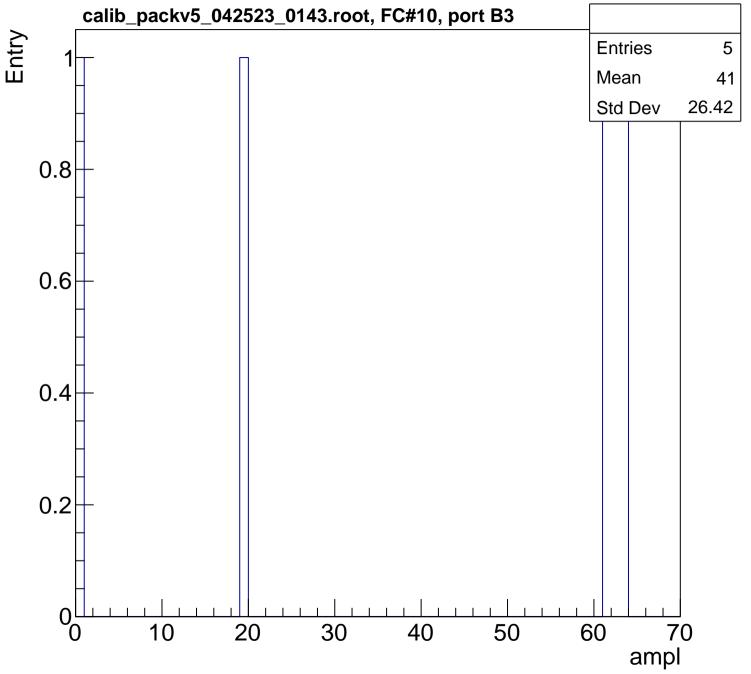


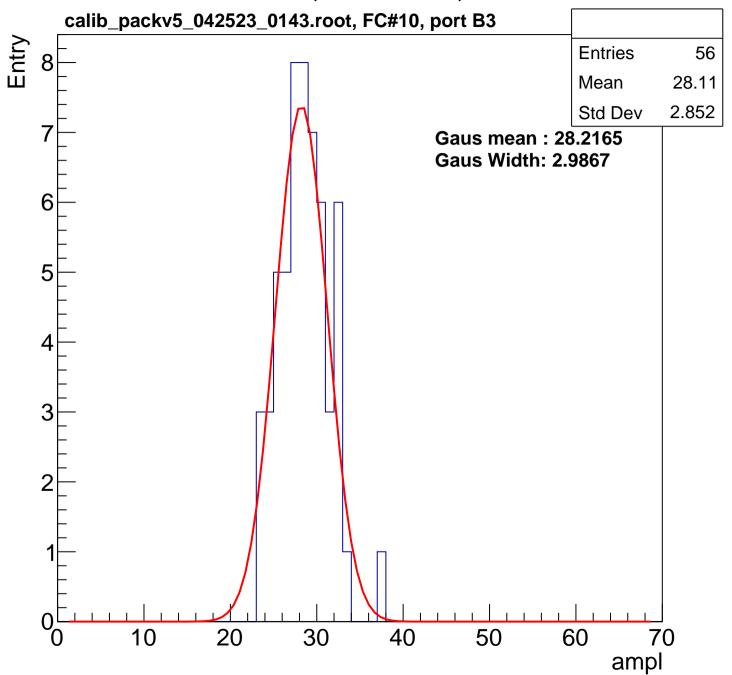


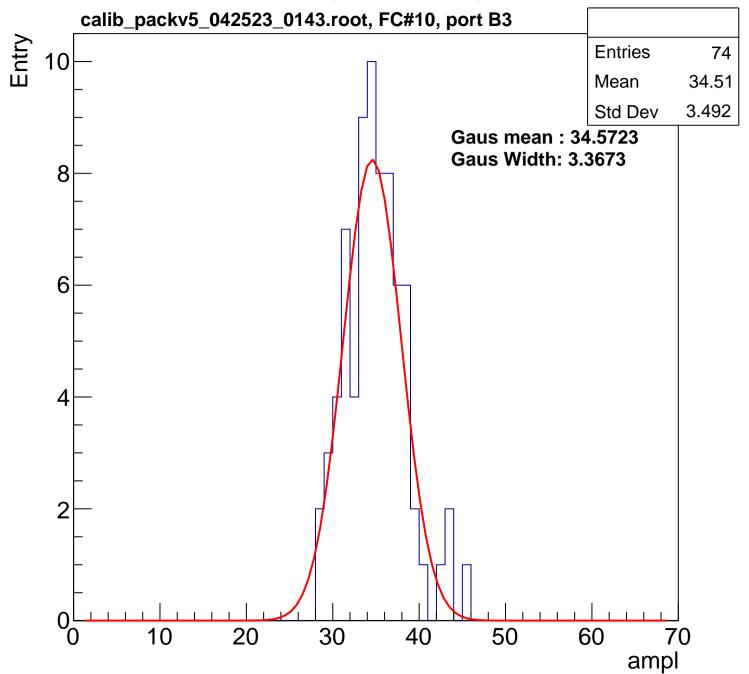


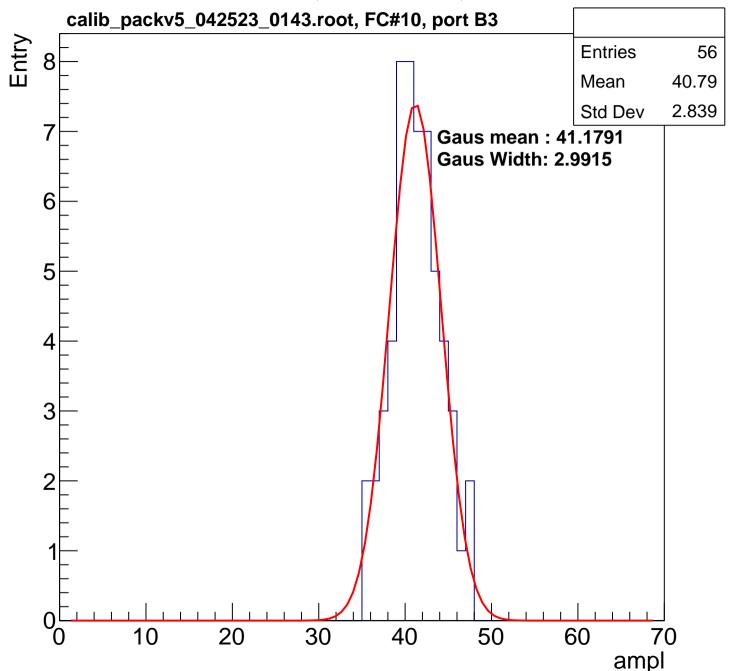


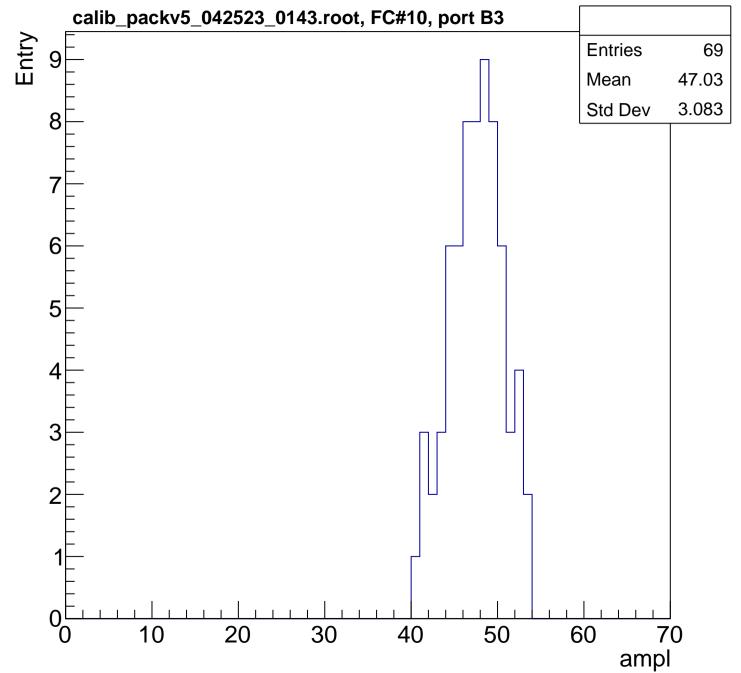


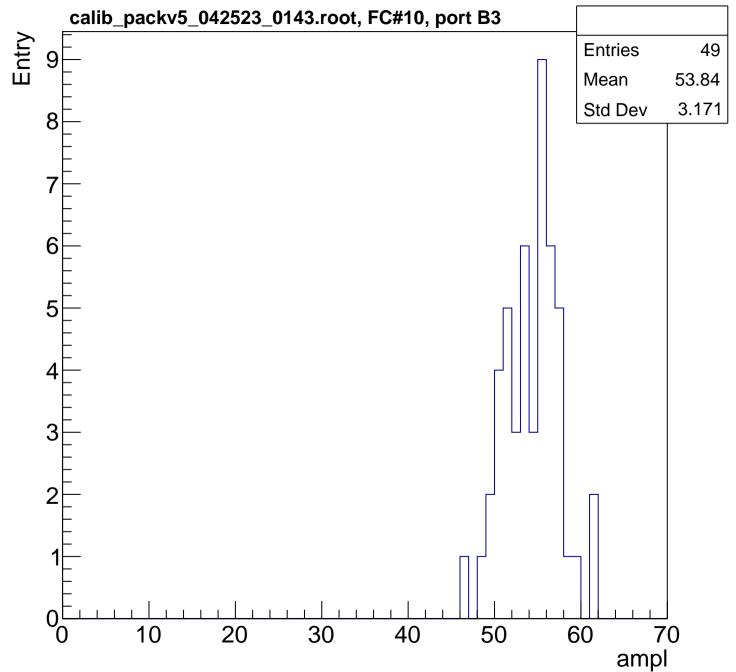


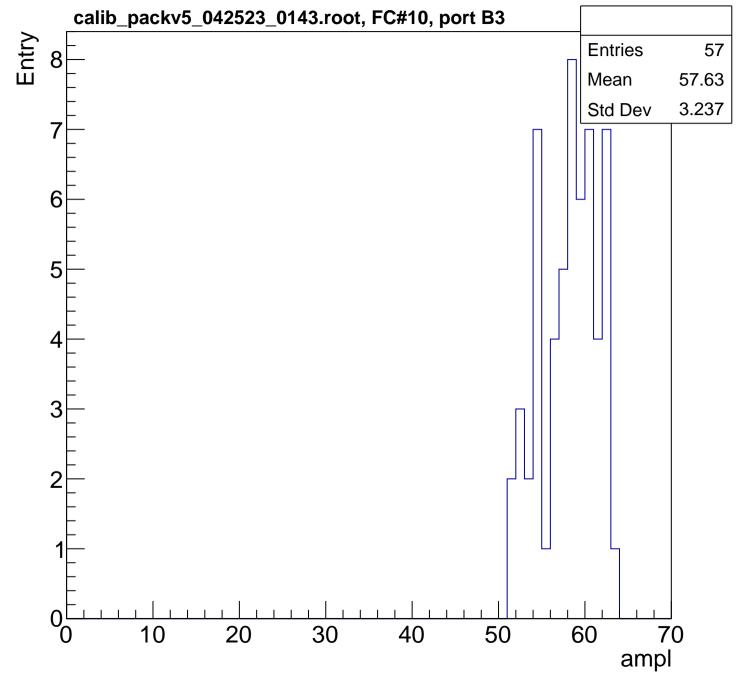


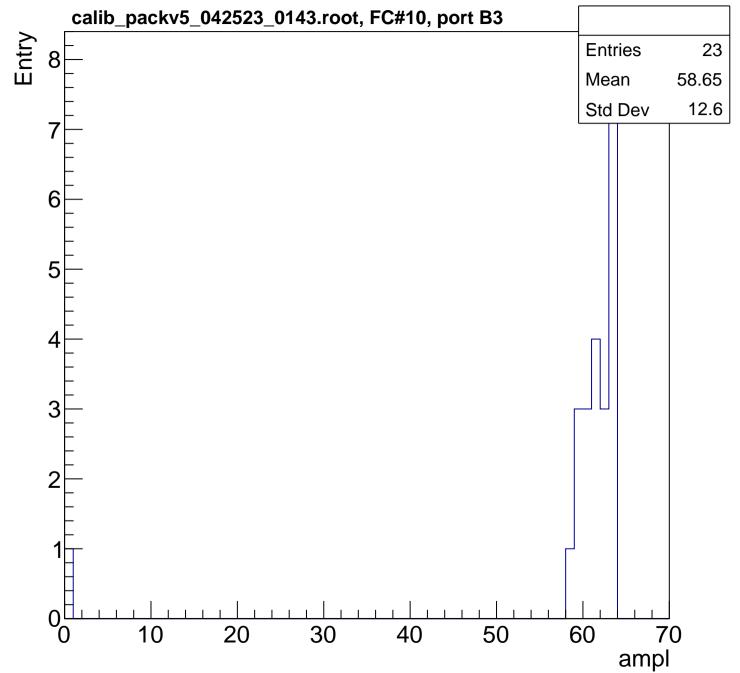


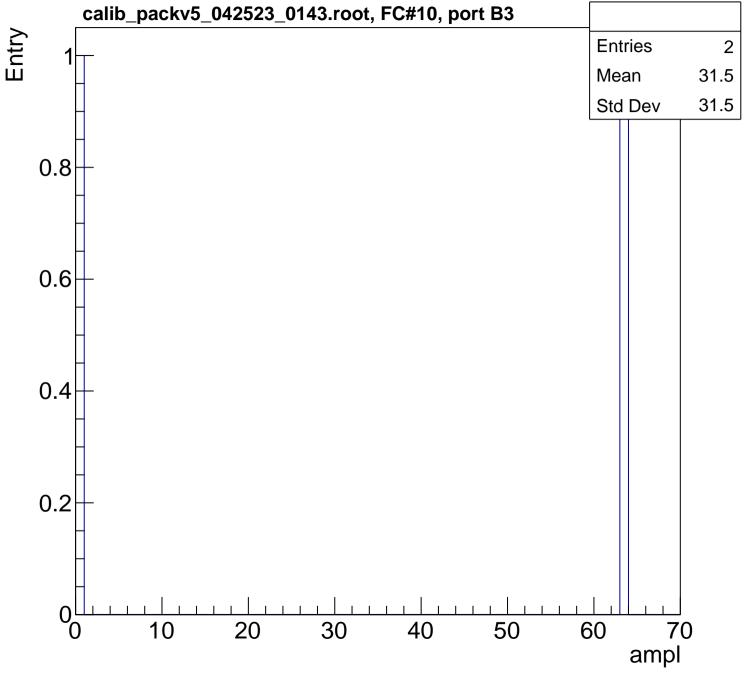


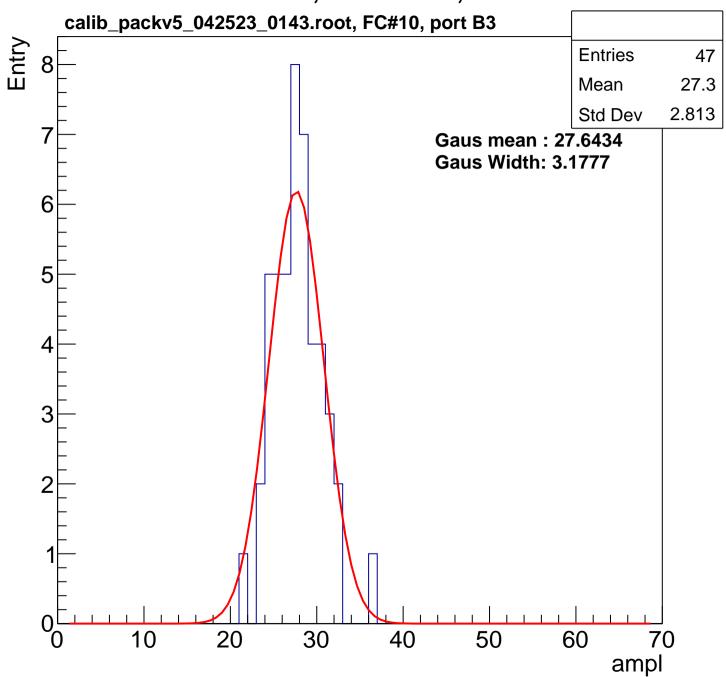


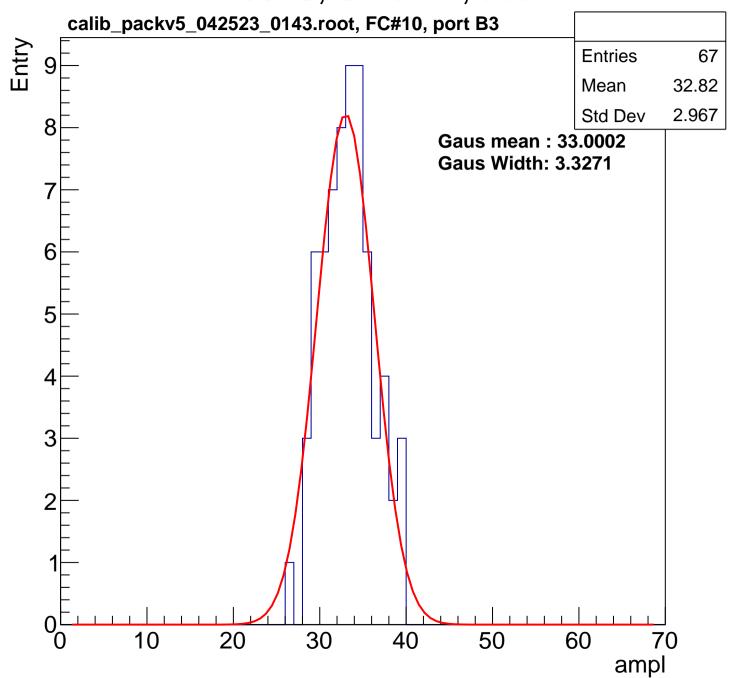


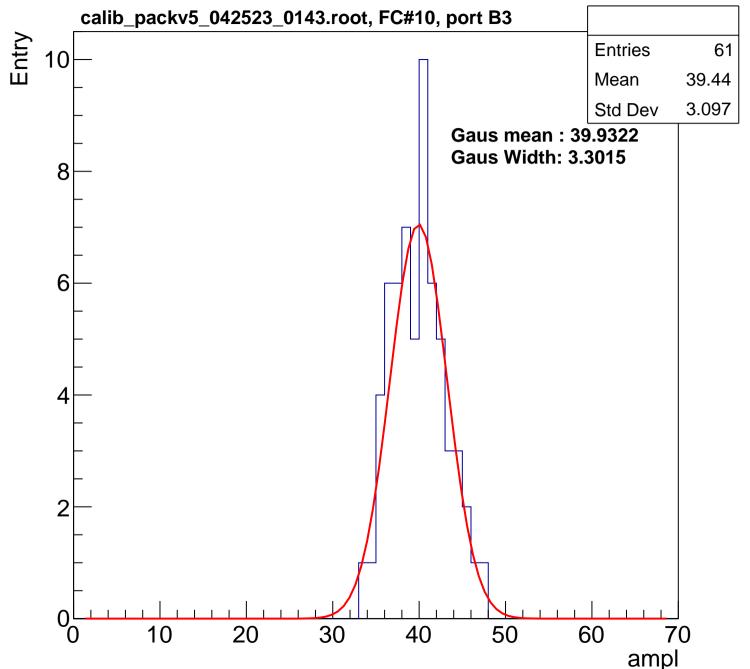


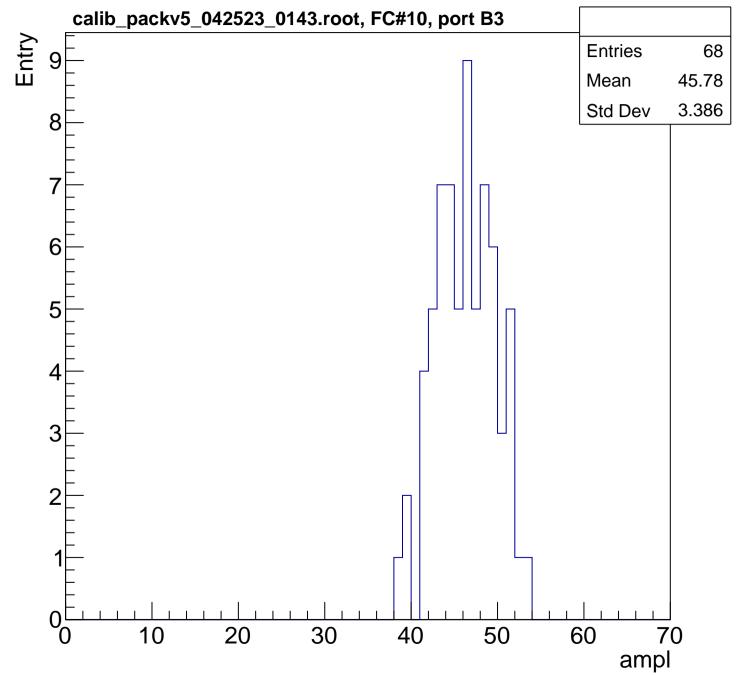


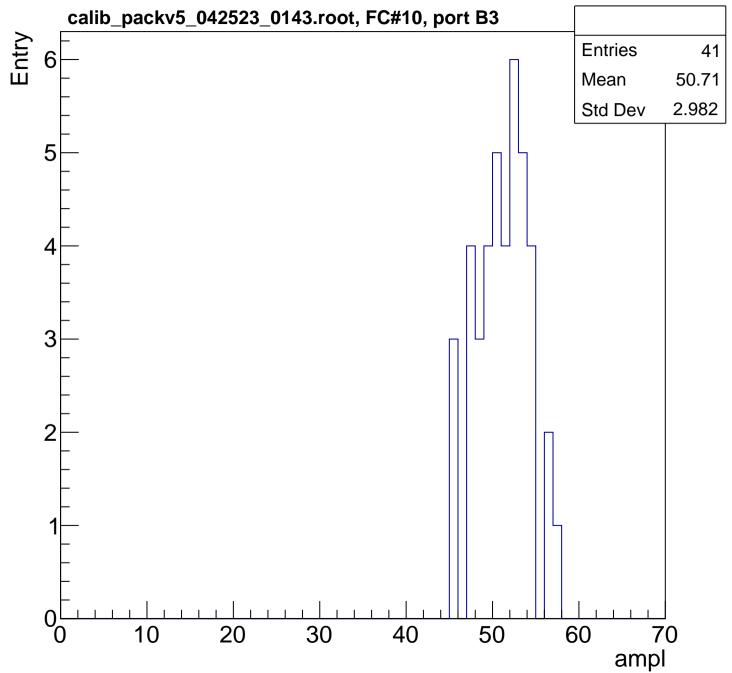


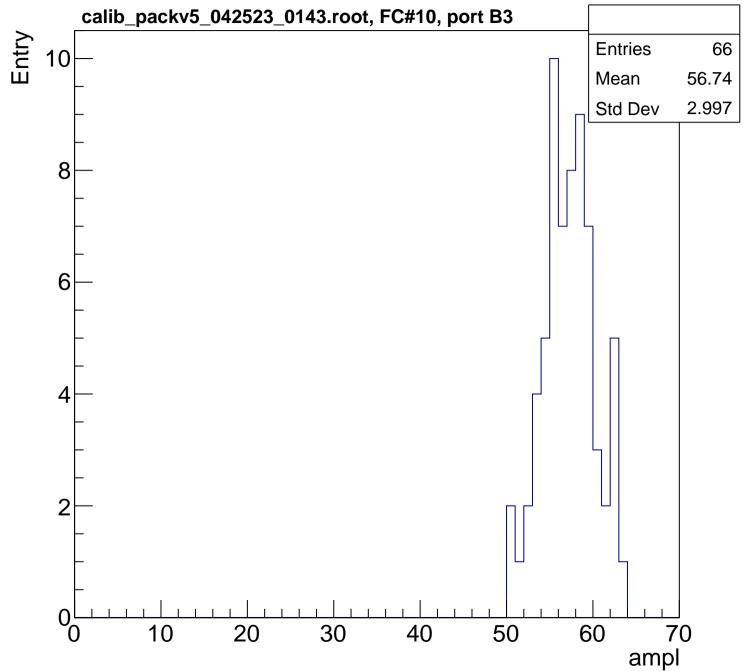


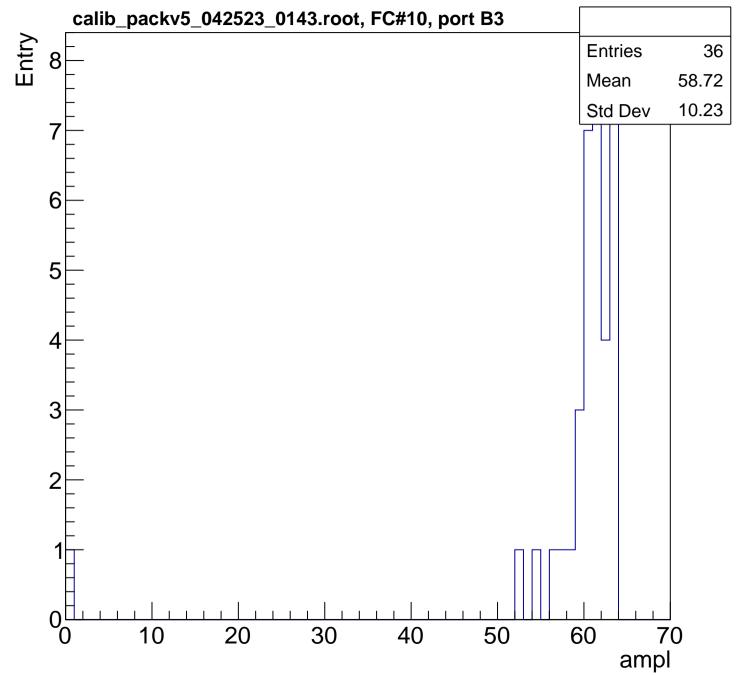


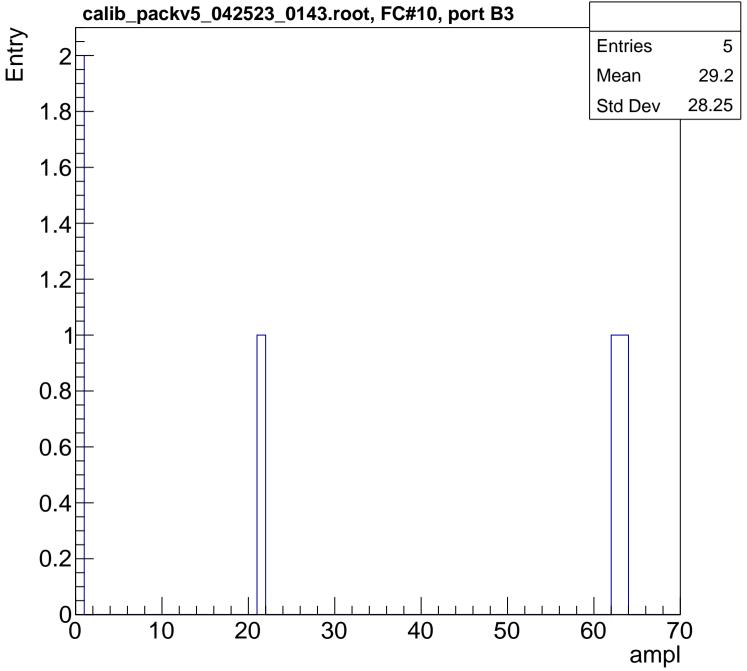


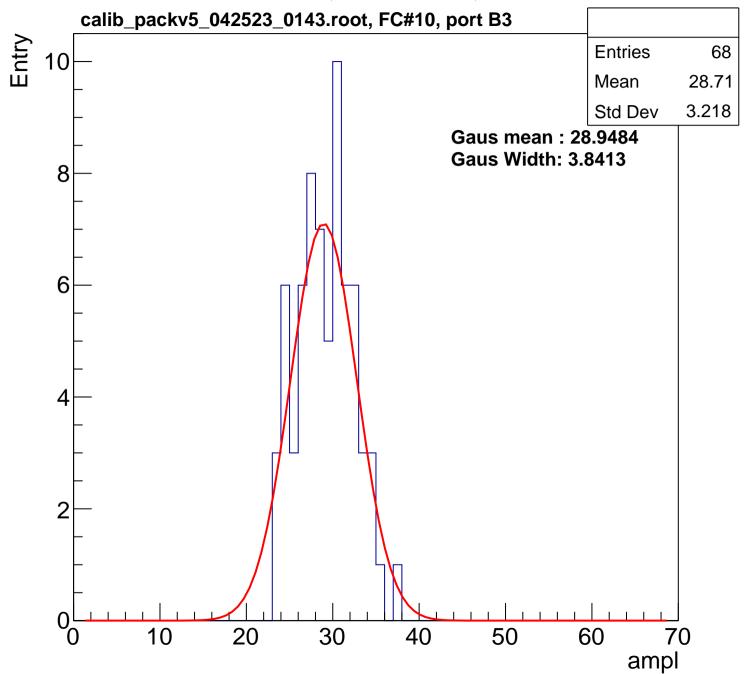


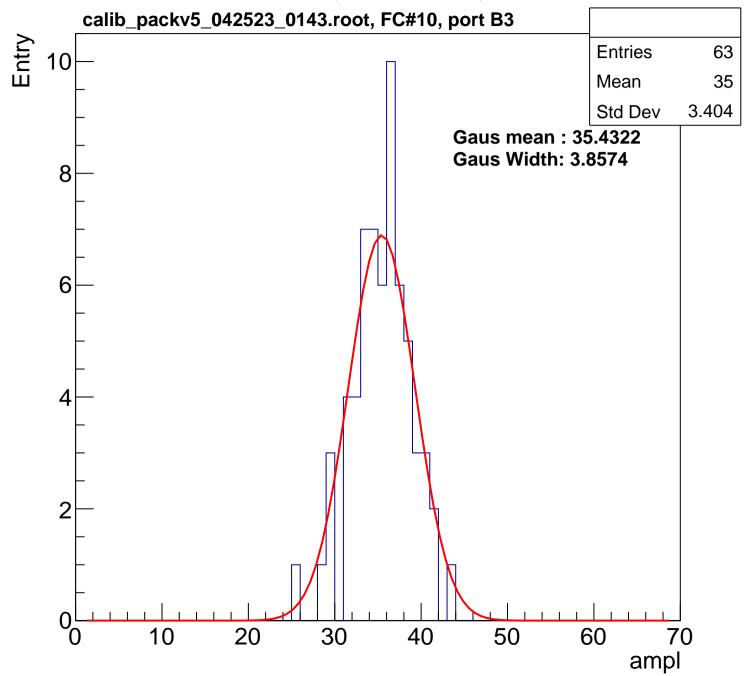


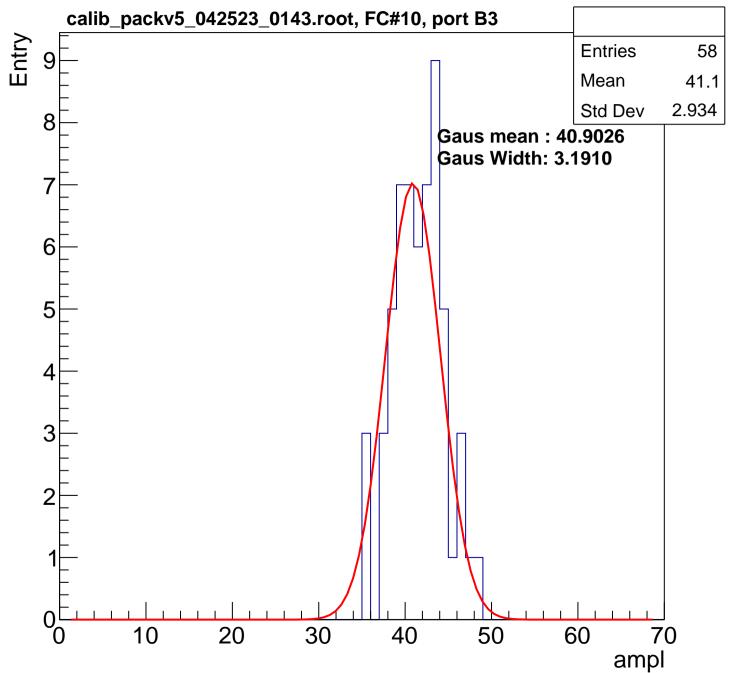


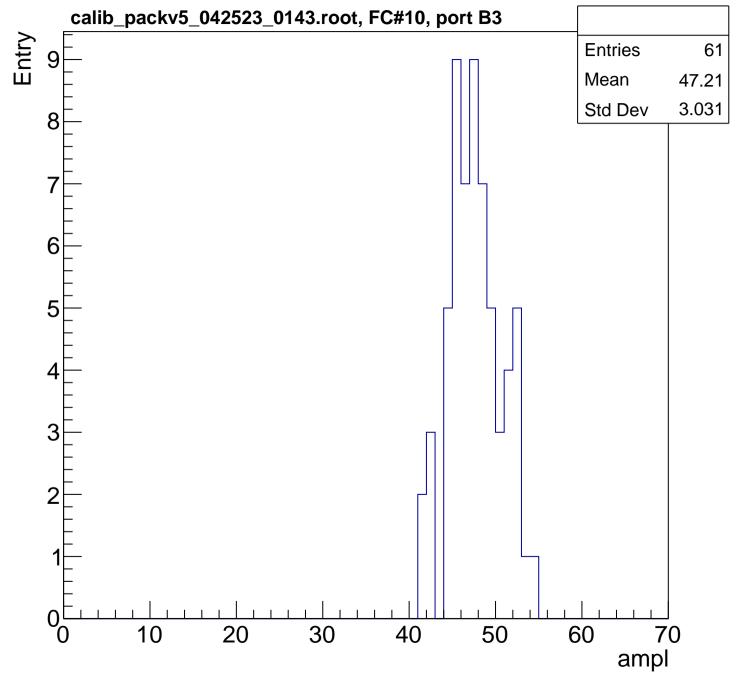


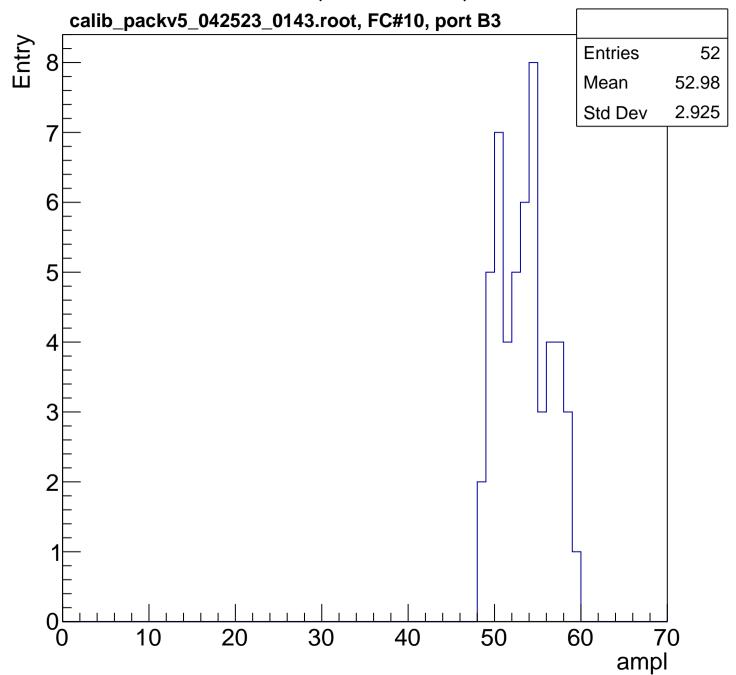


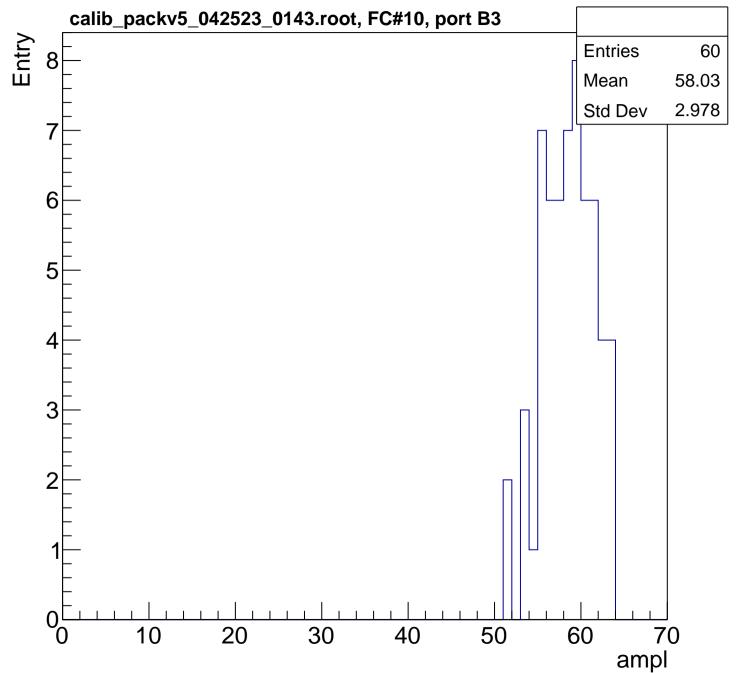


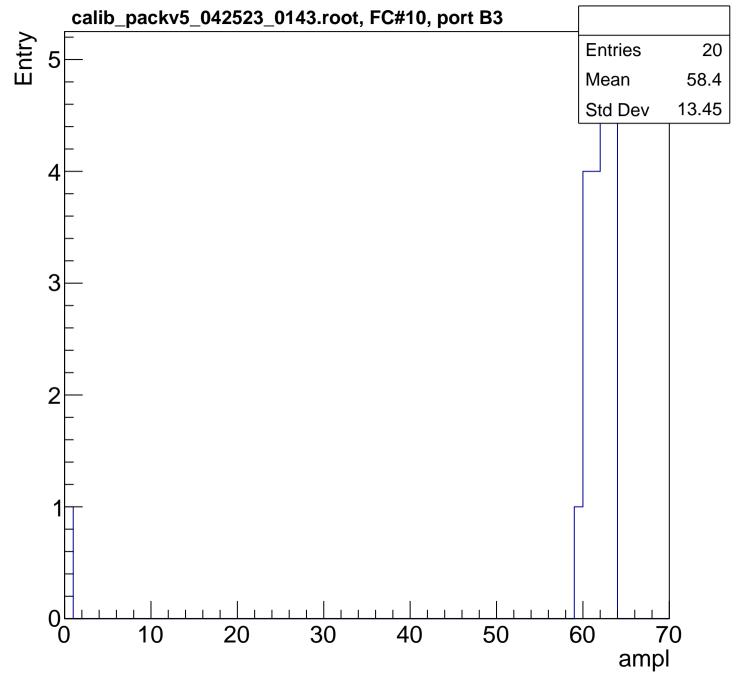


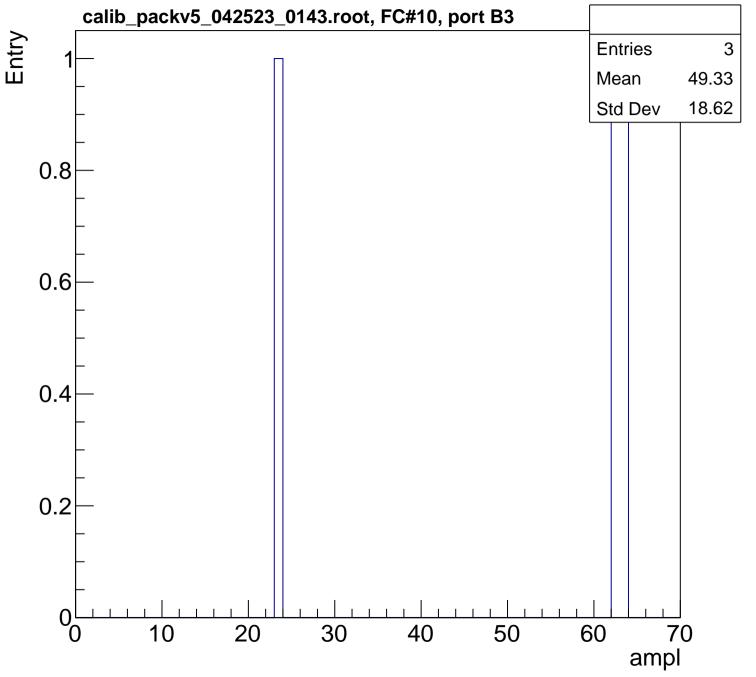


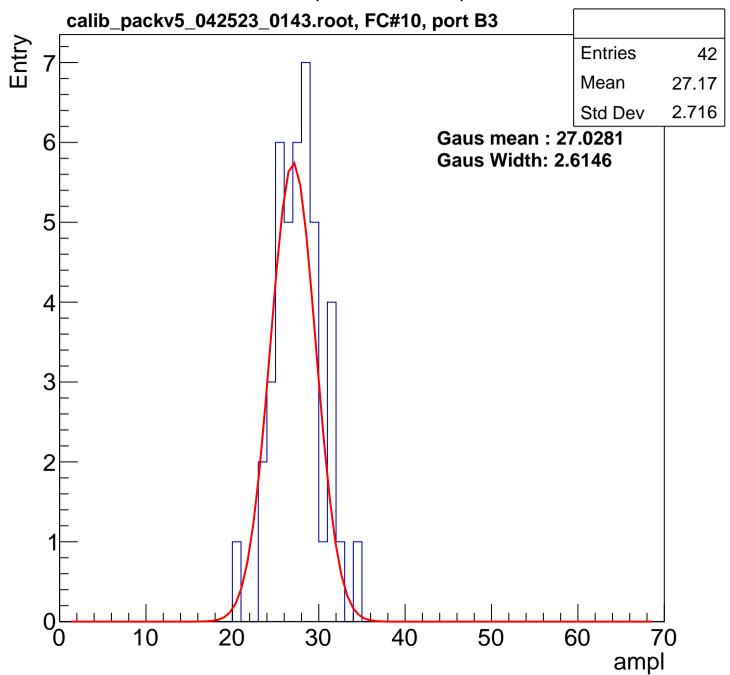


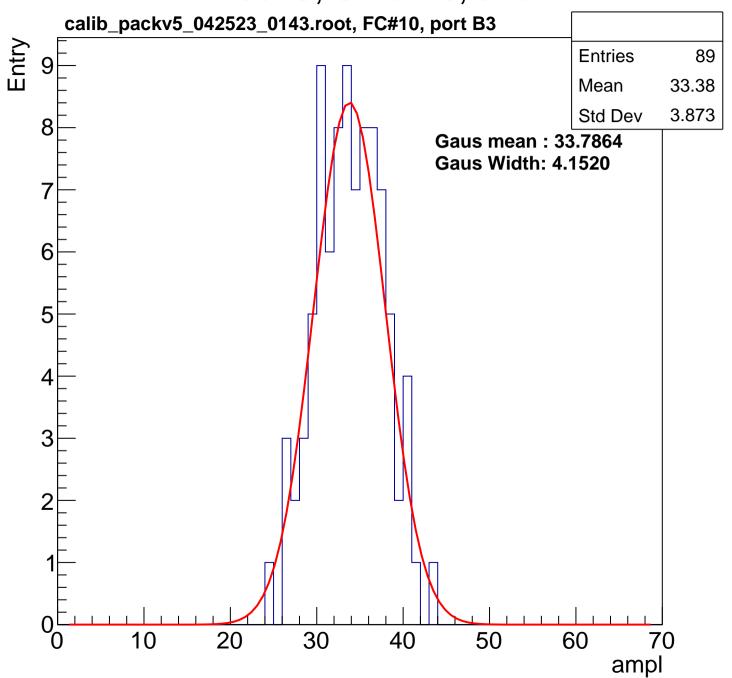


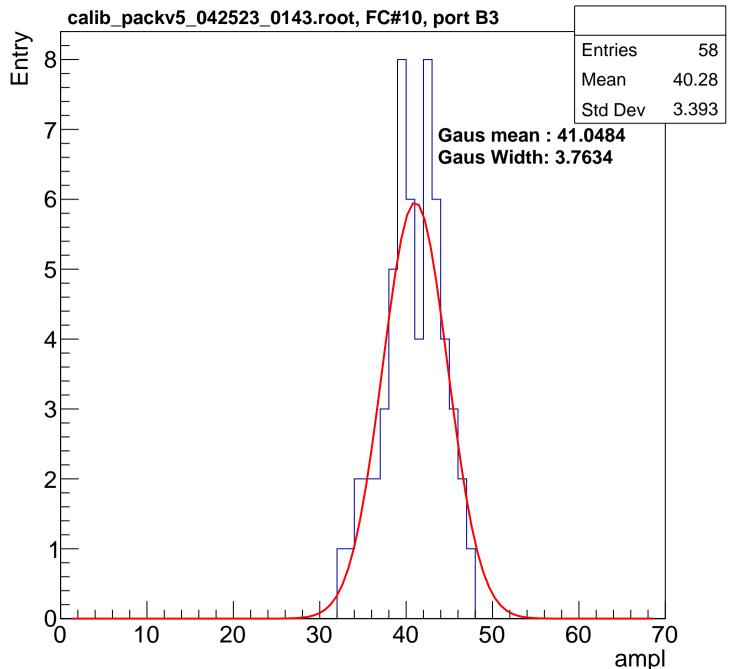


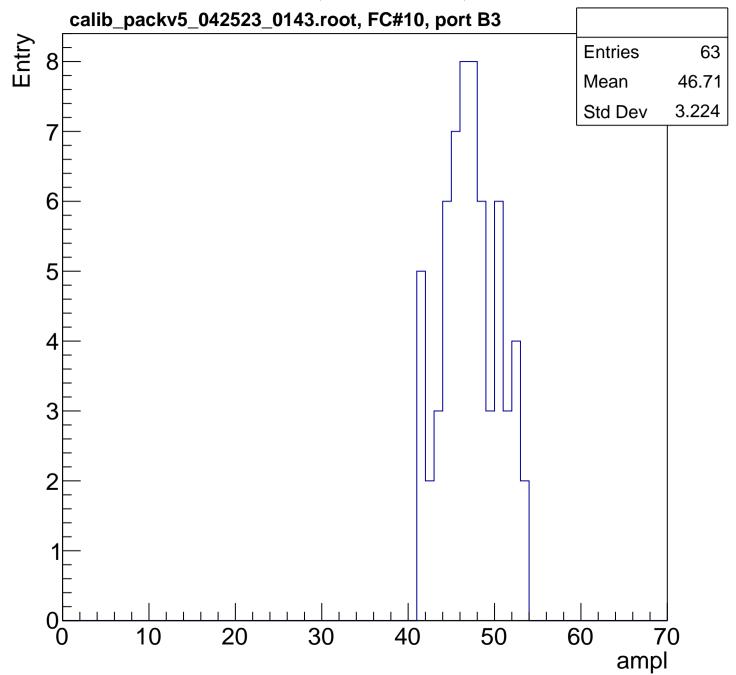


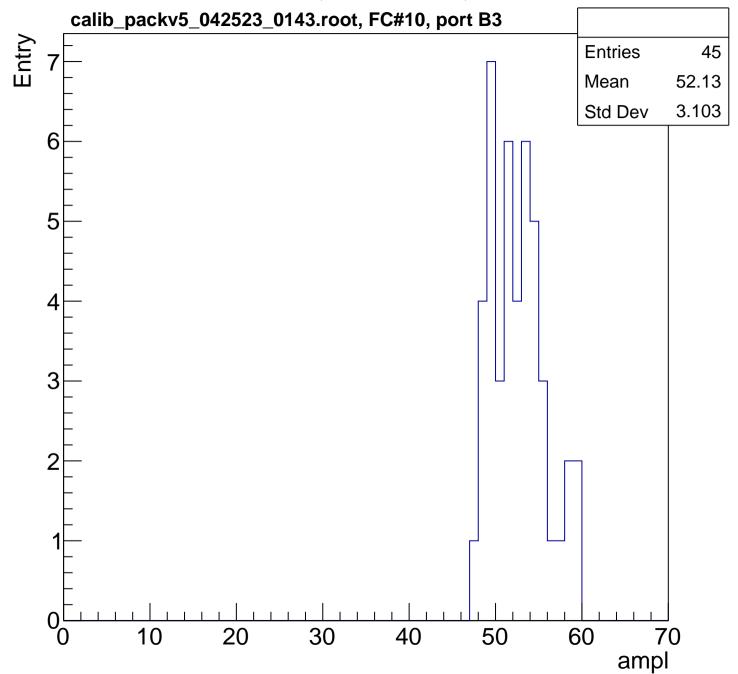


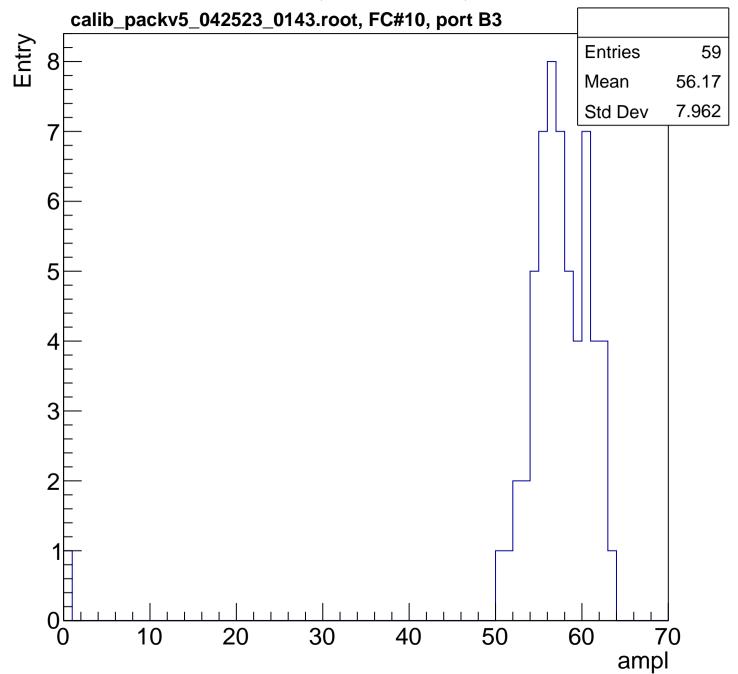


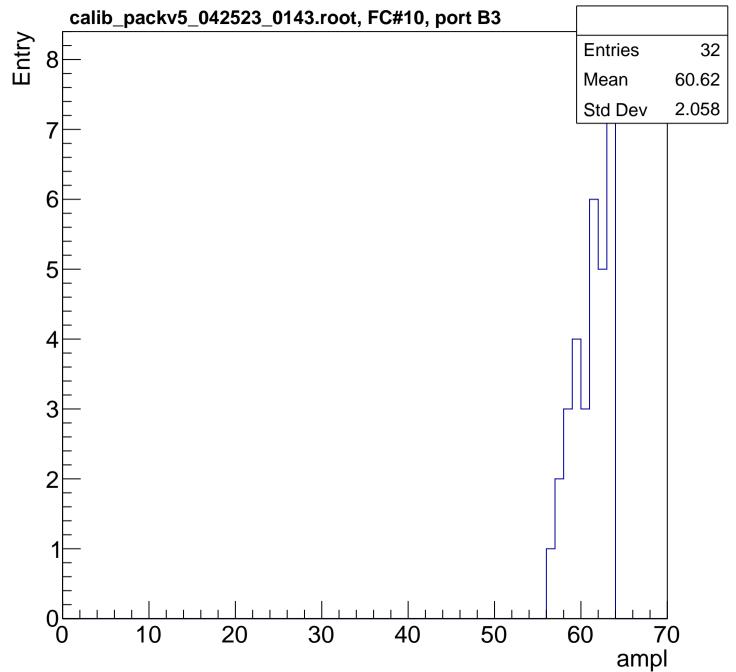


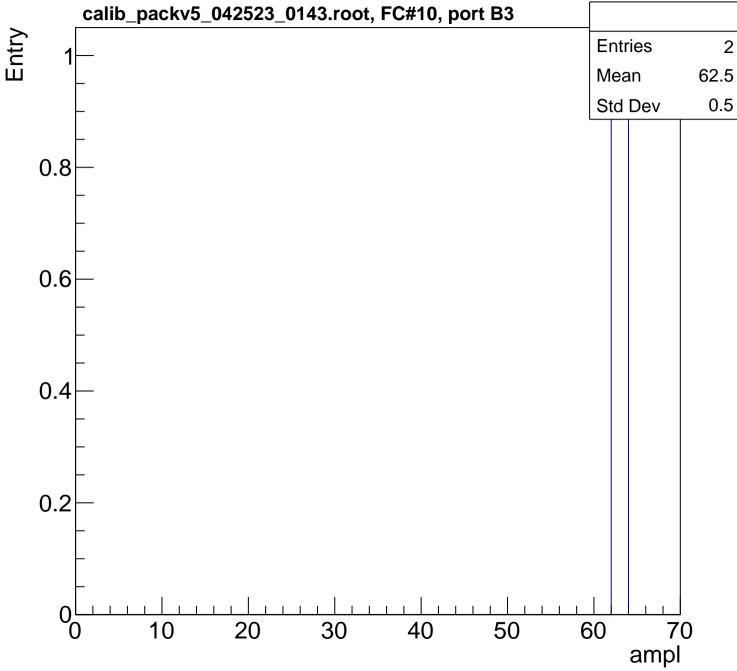


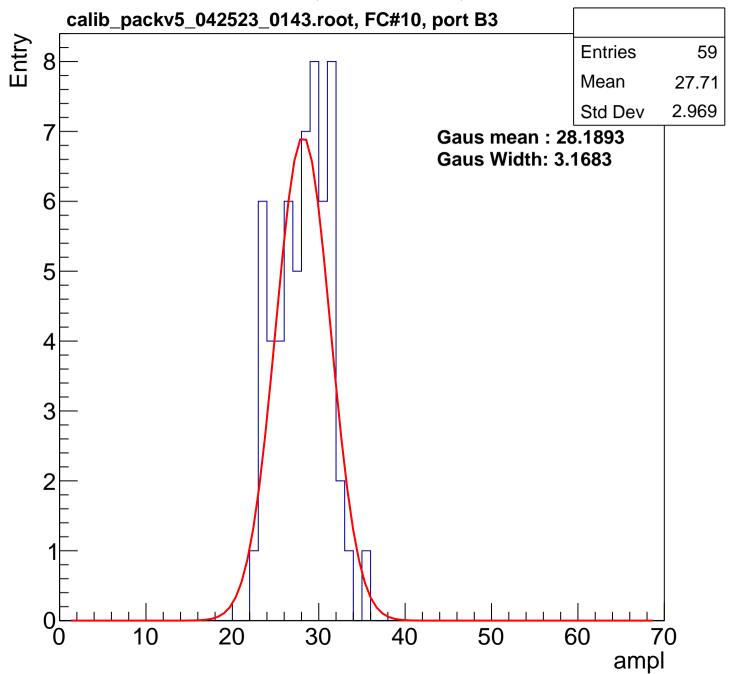


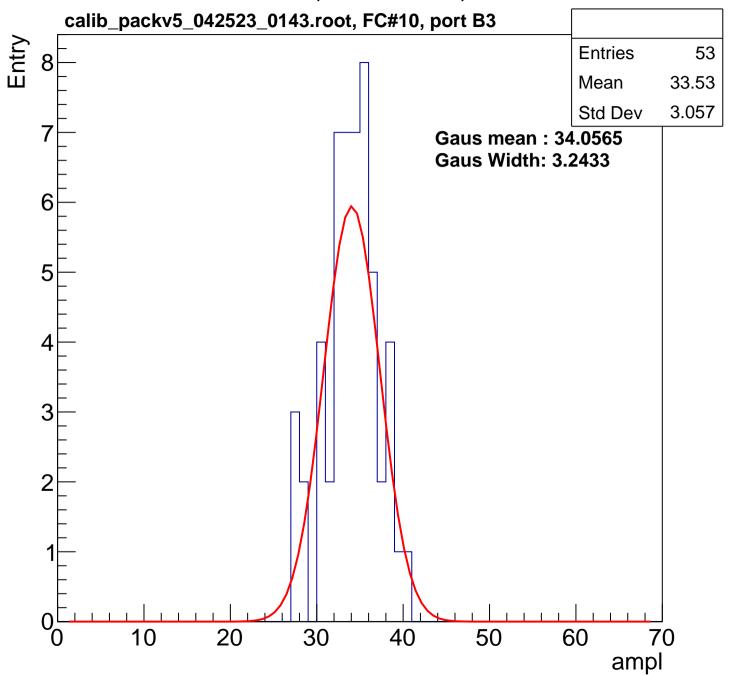


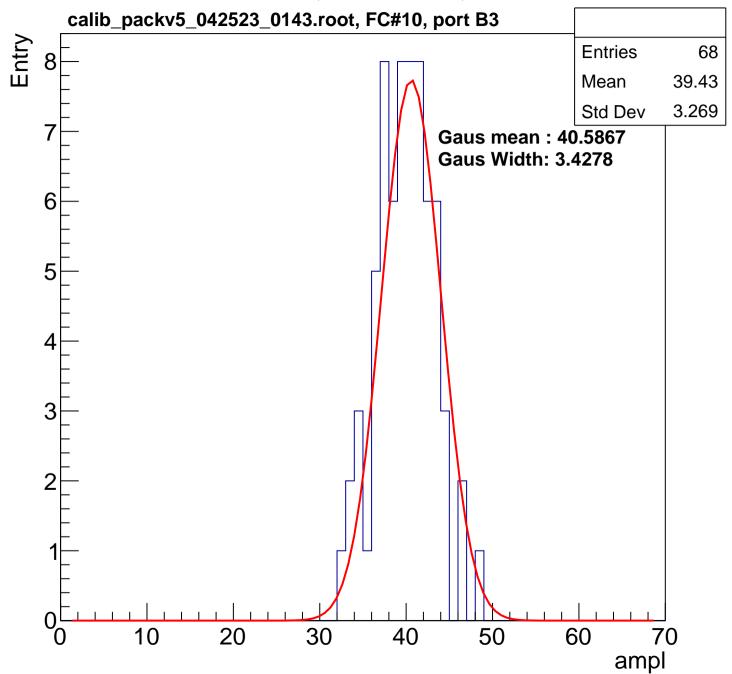


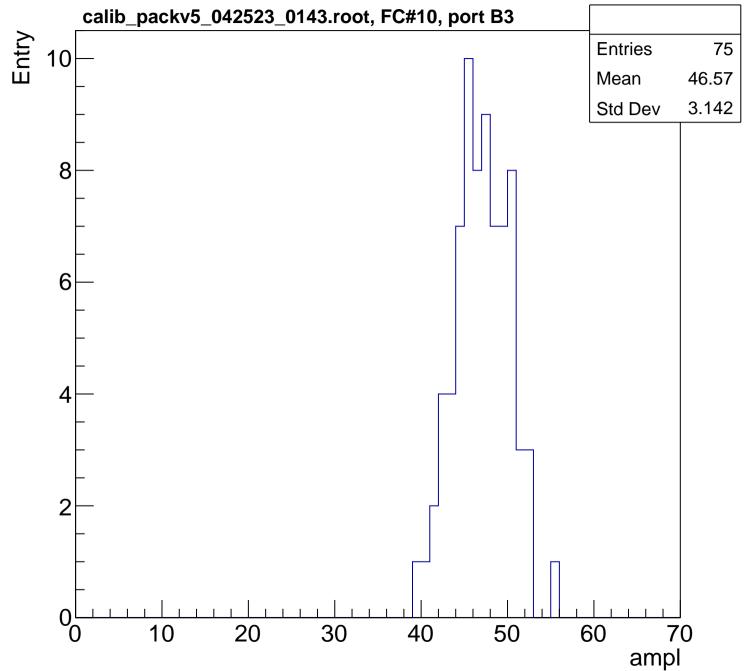


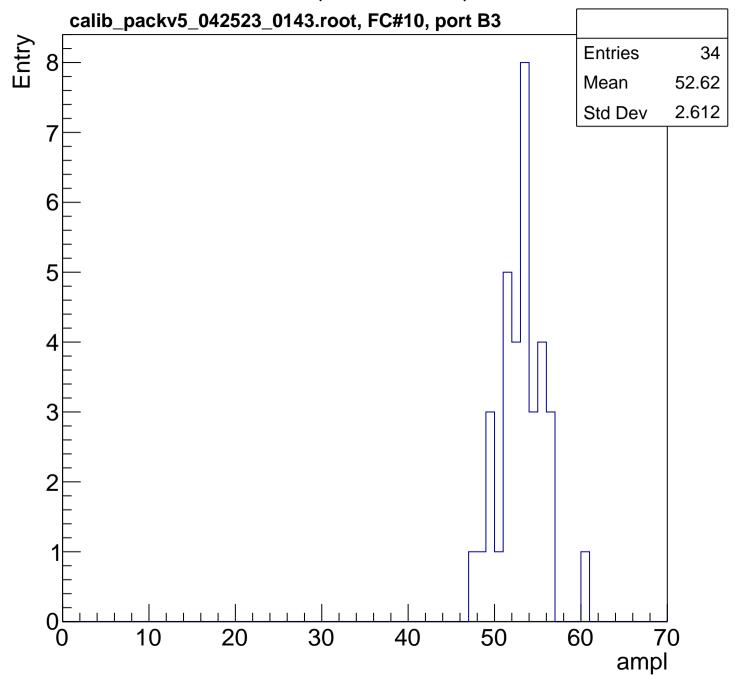


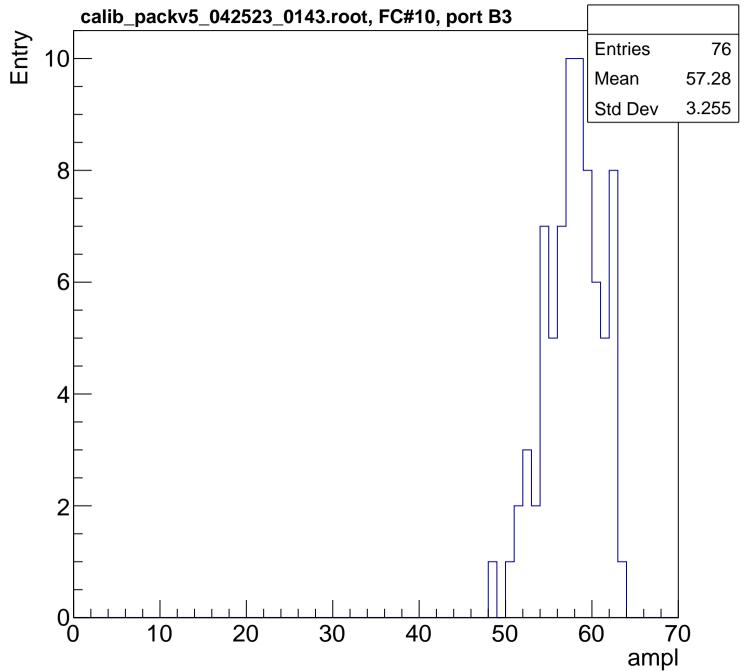


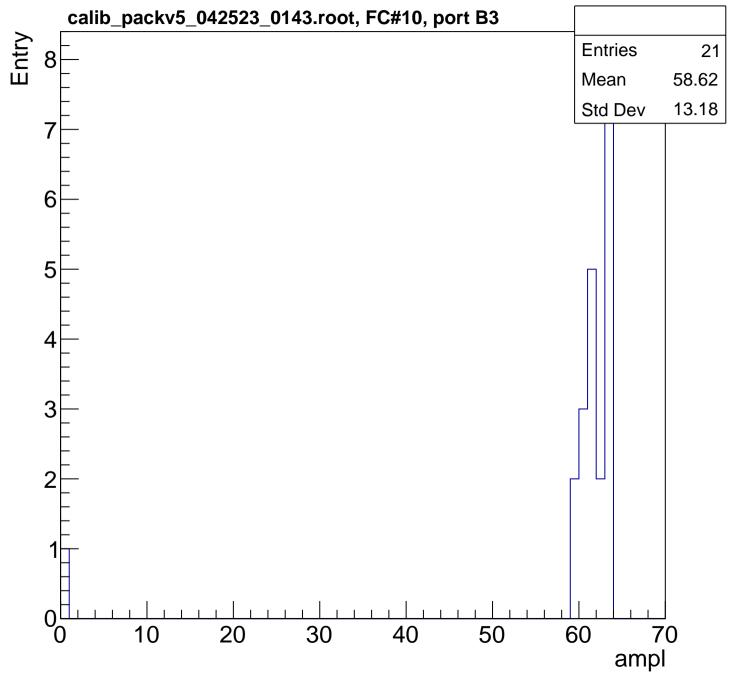


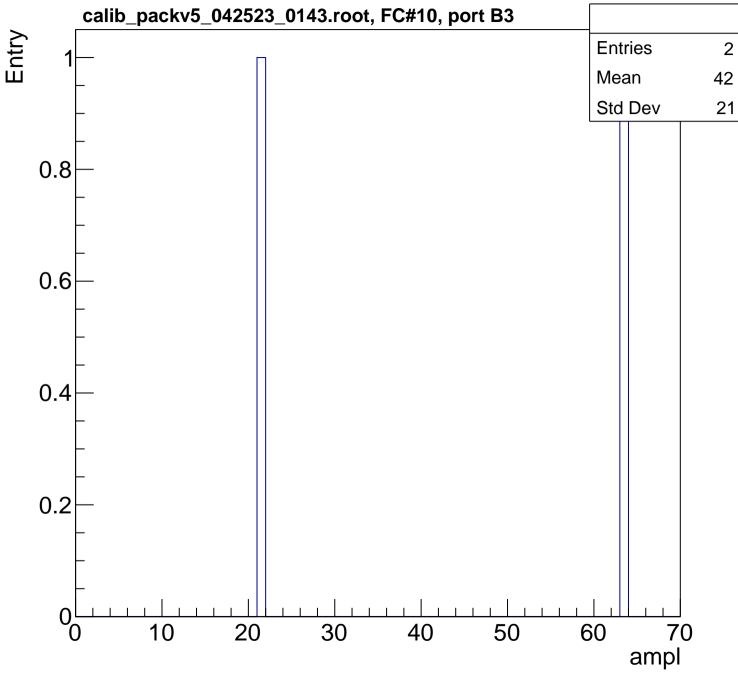


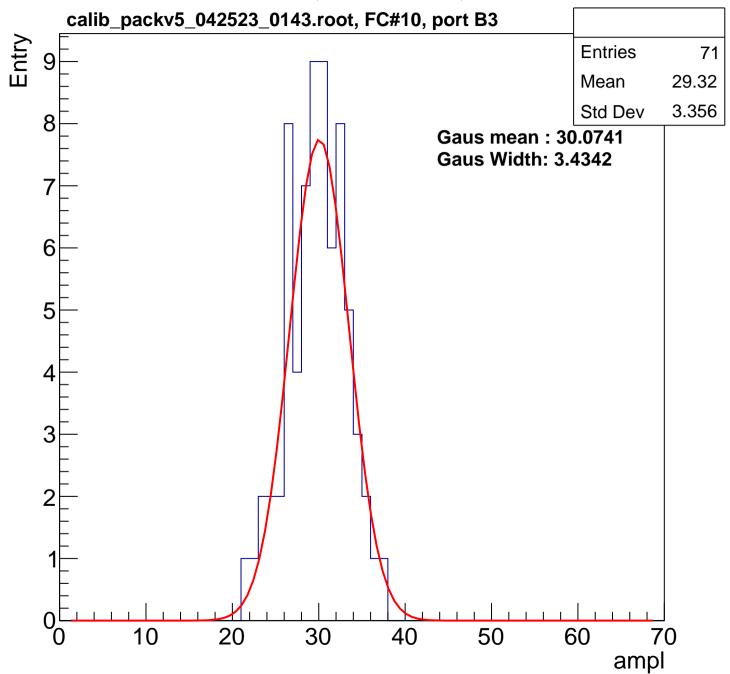


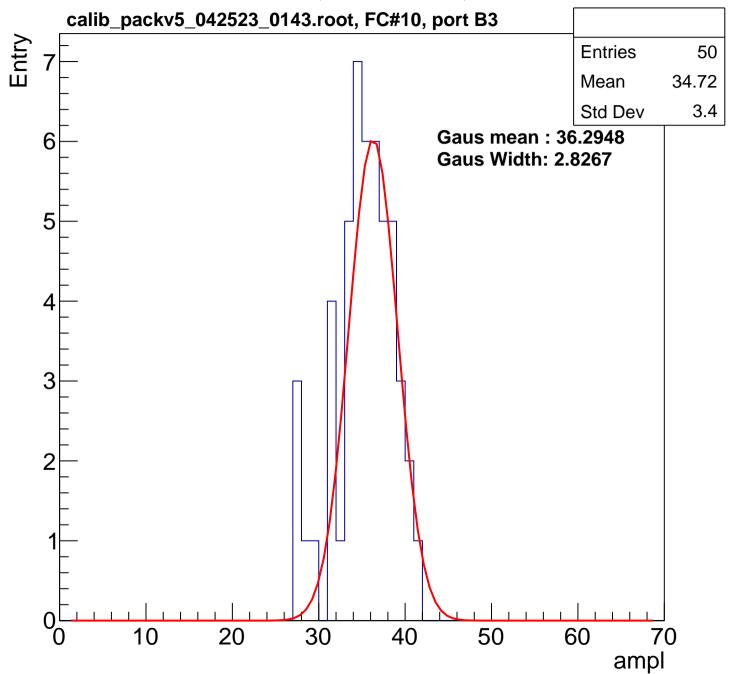


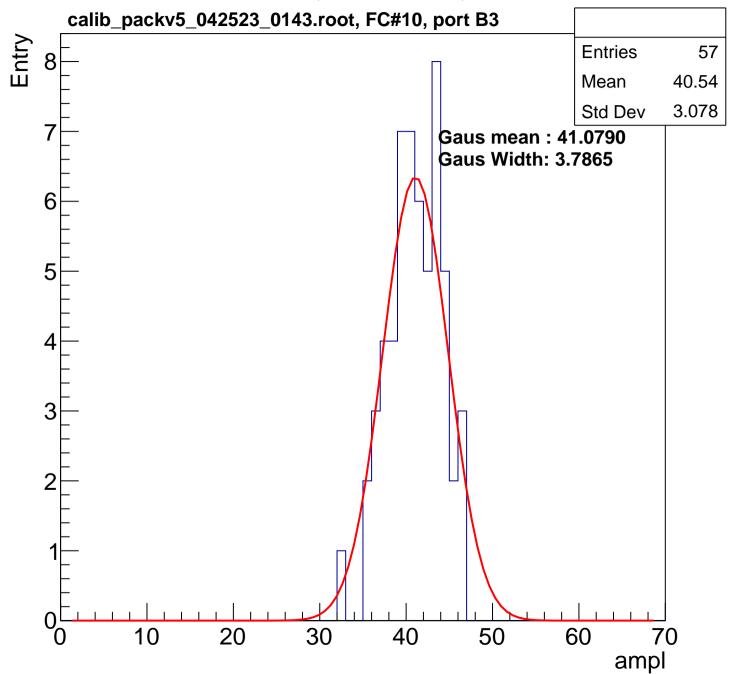


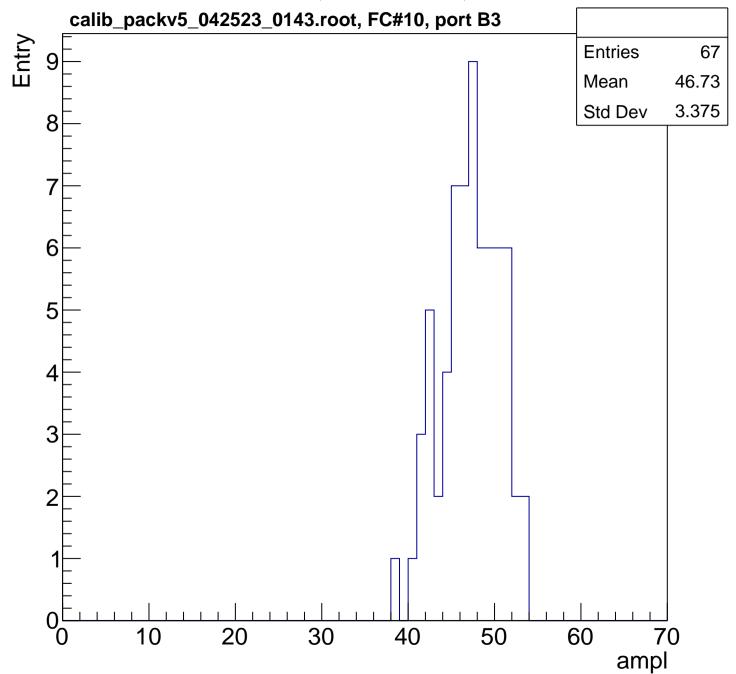


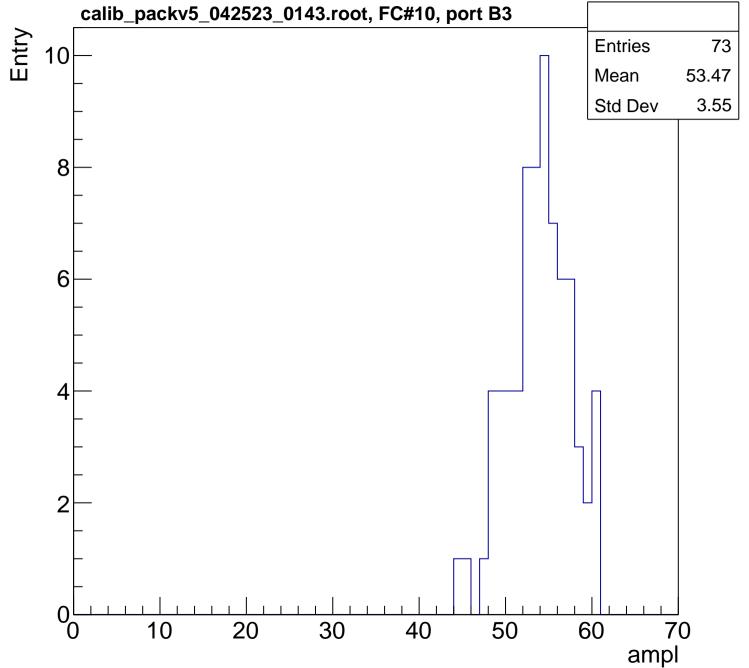


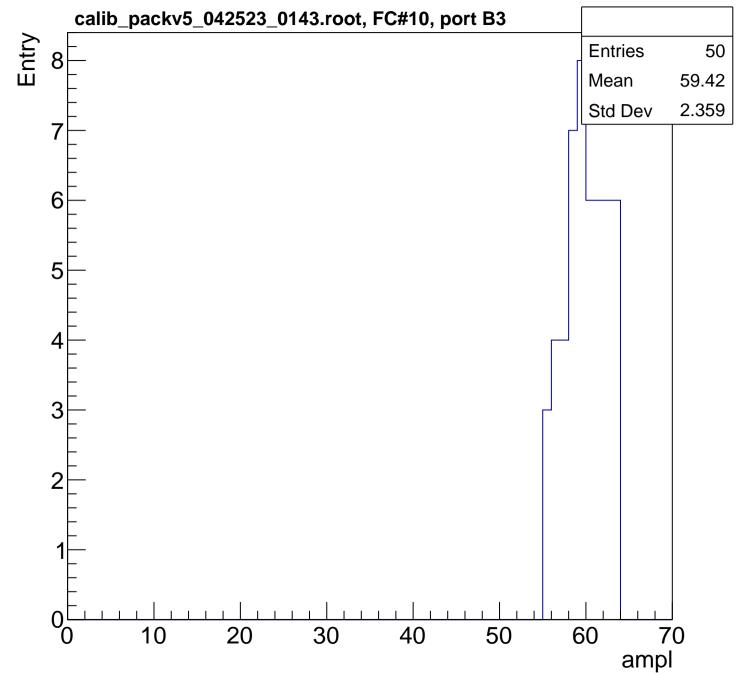


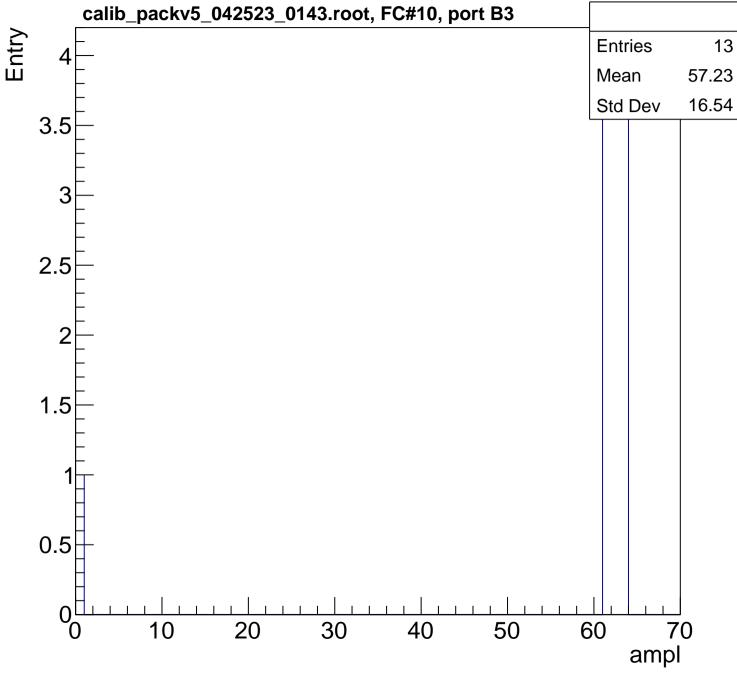




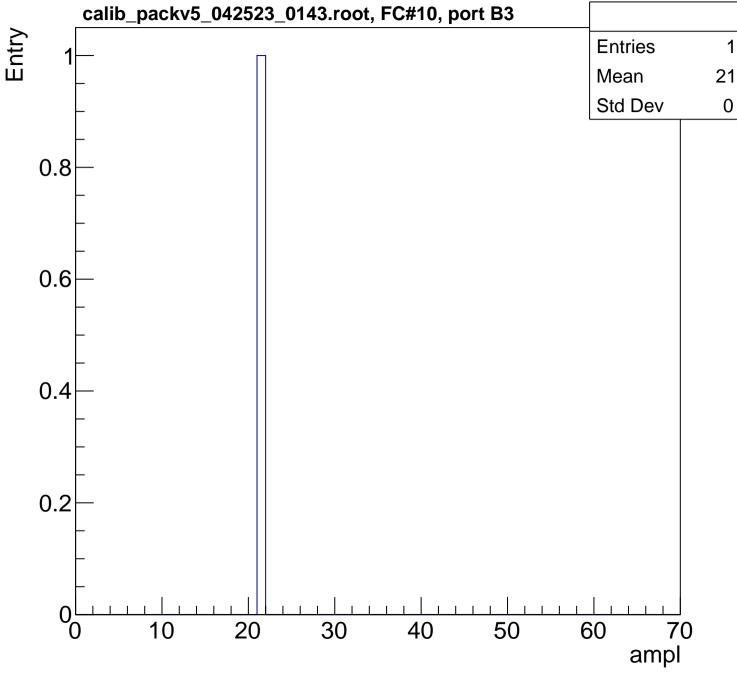


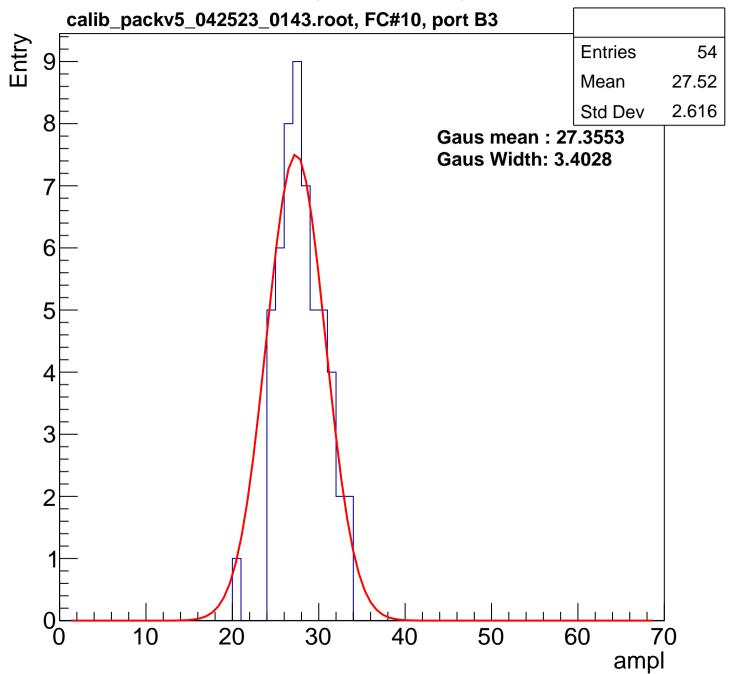


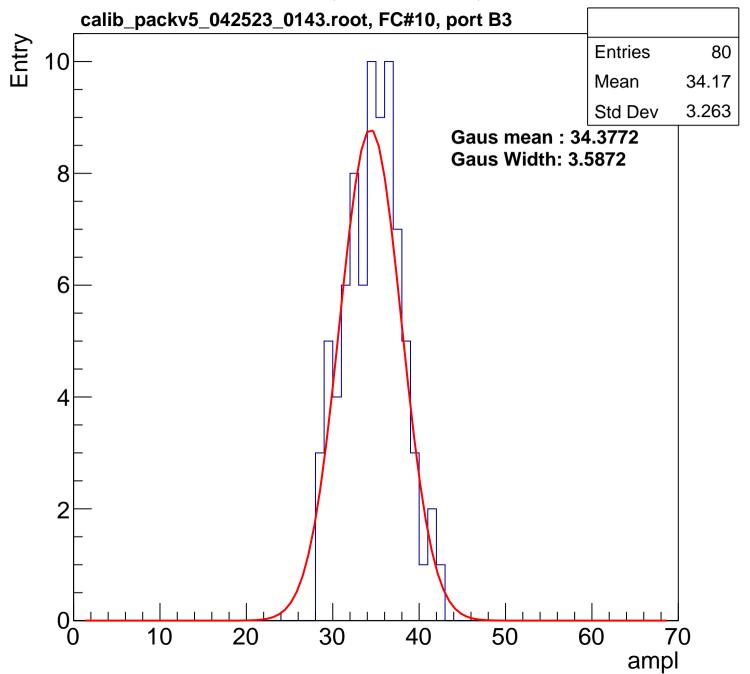


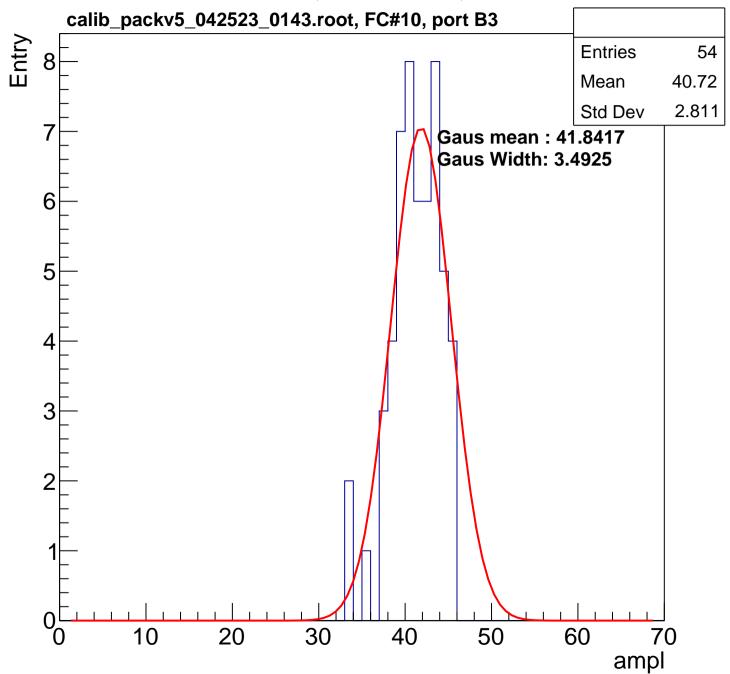


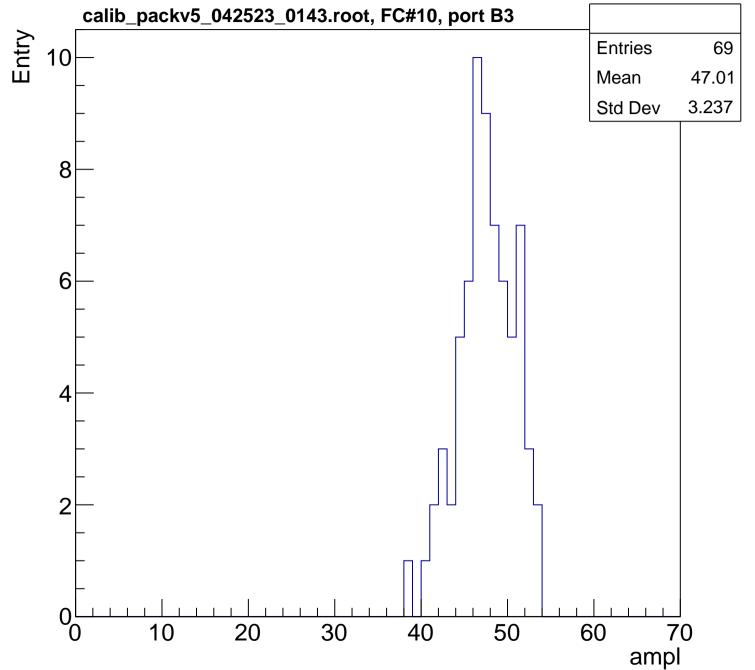
0

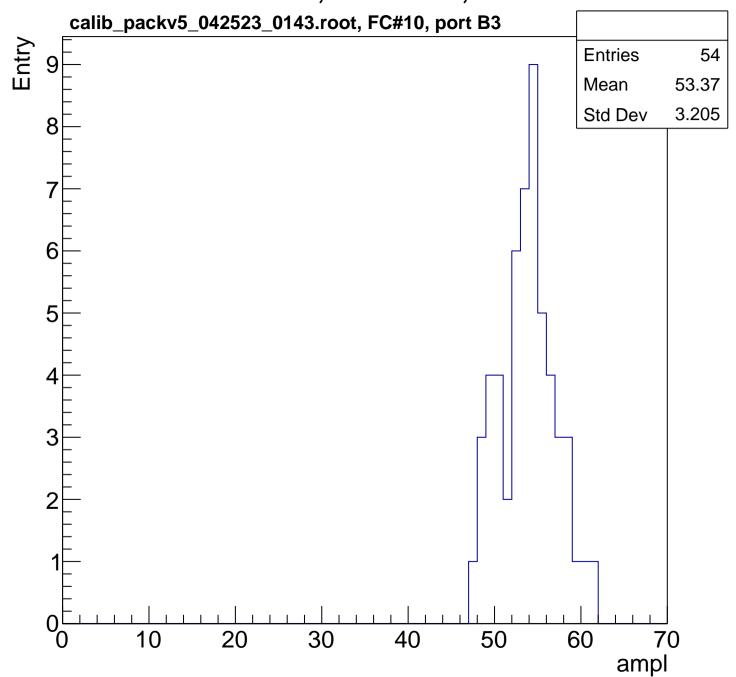


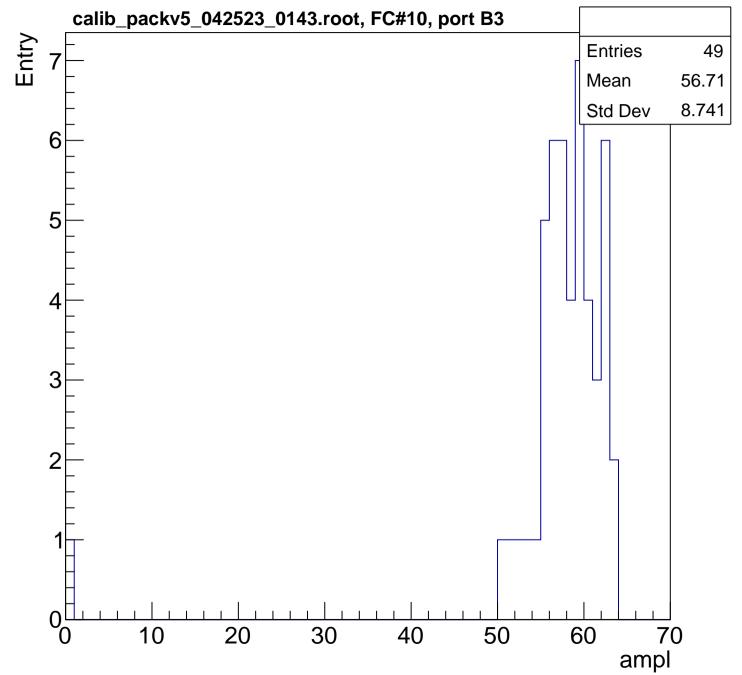


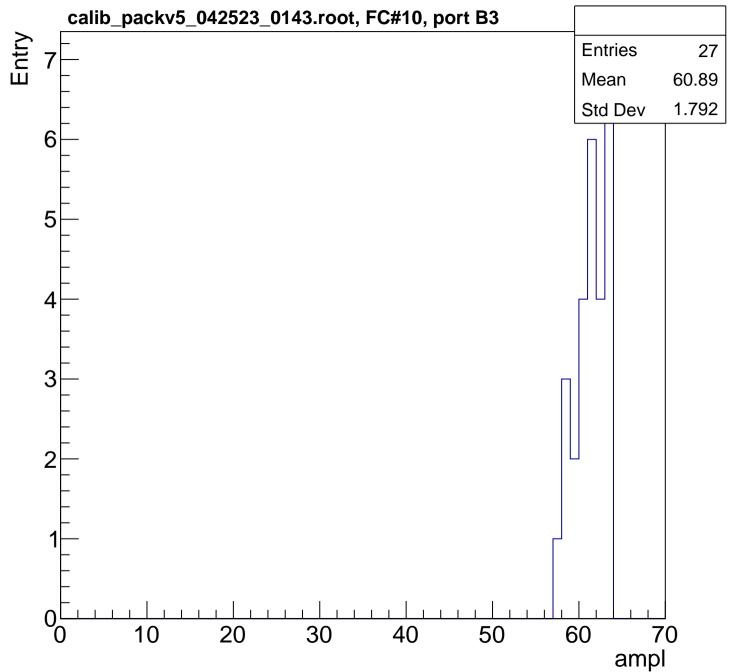


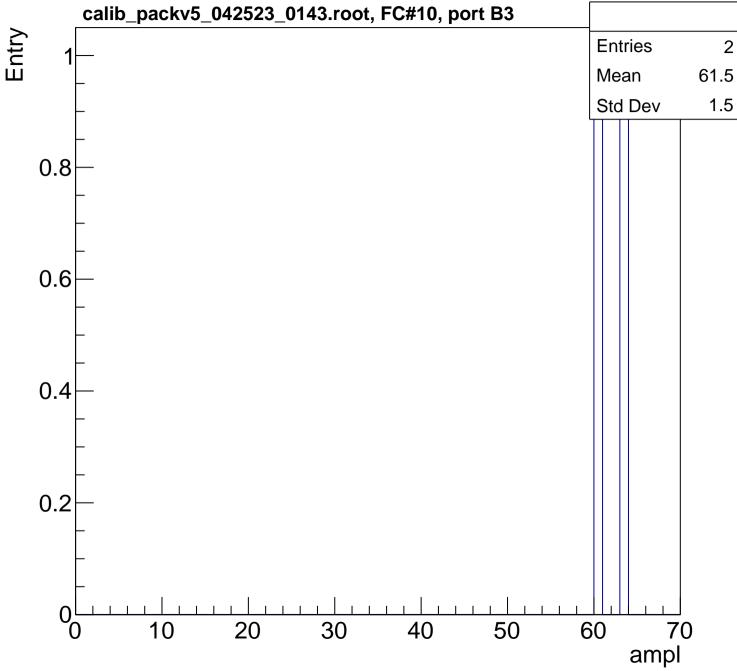


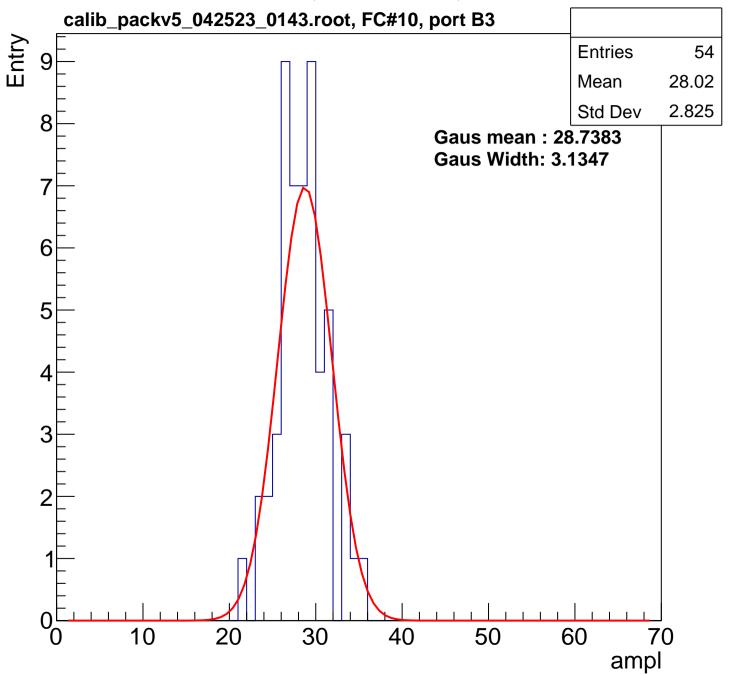


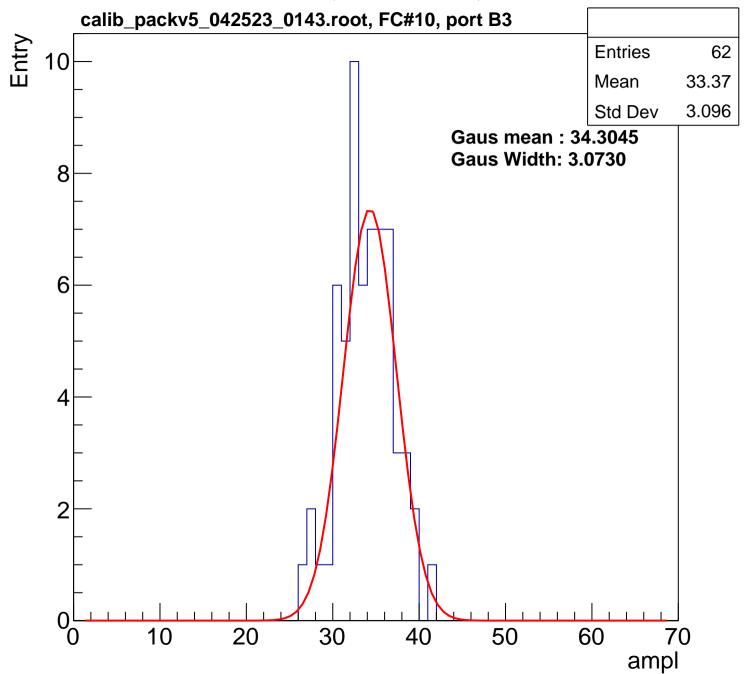


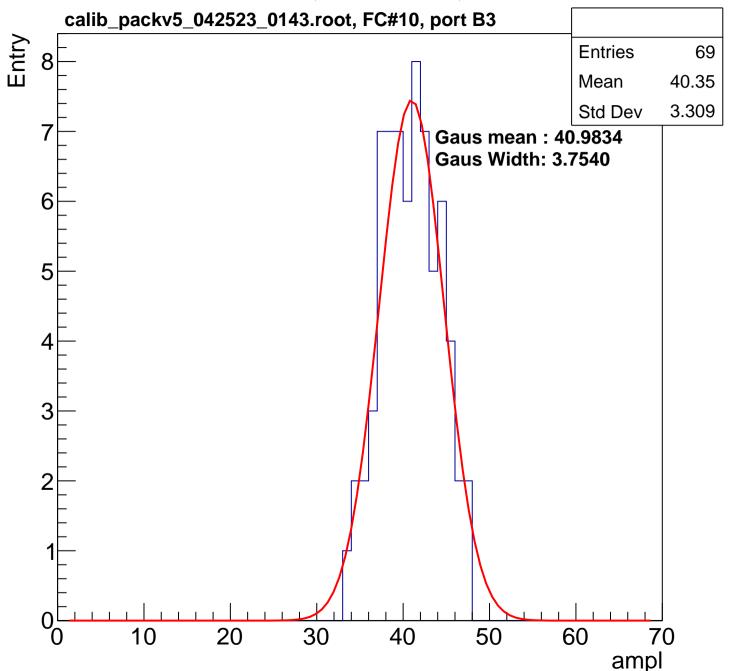


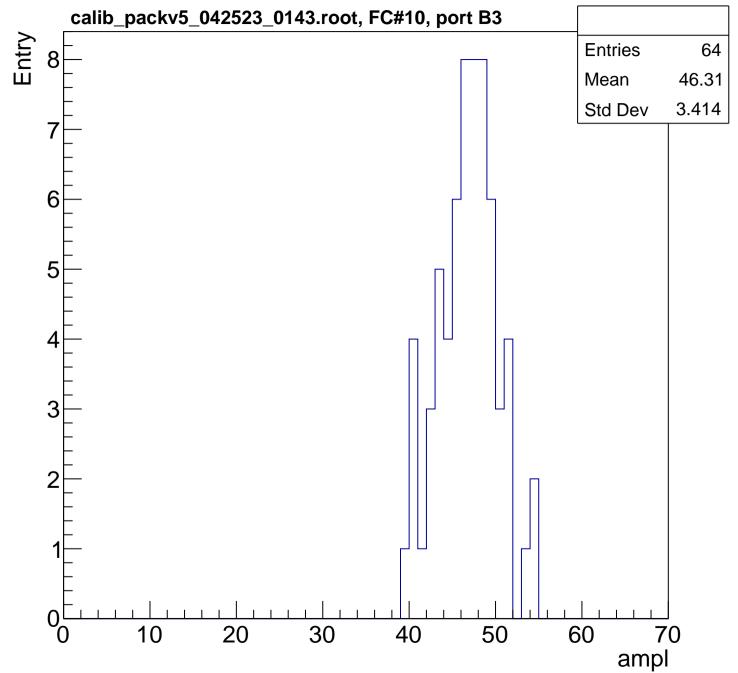


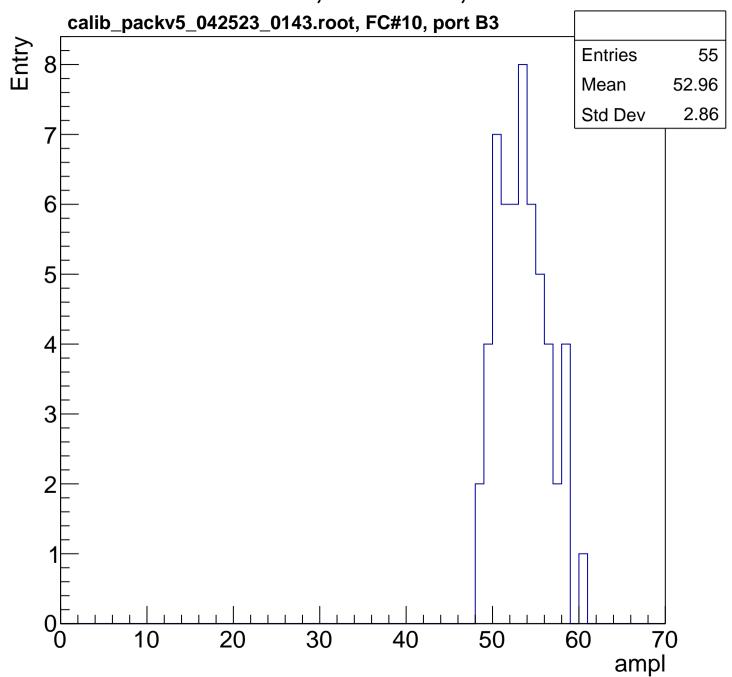


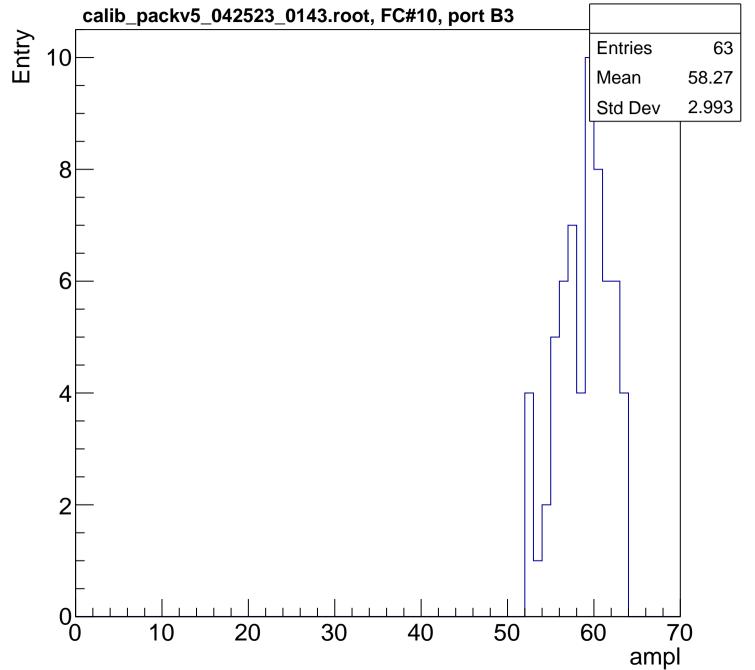


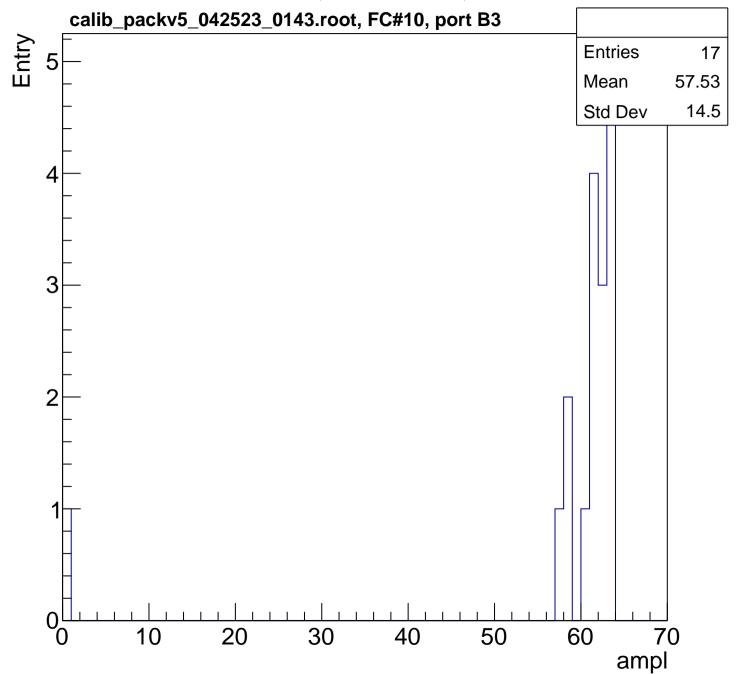


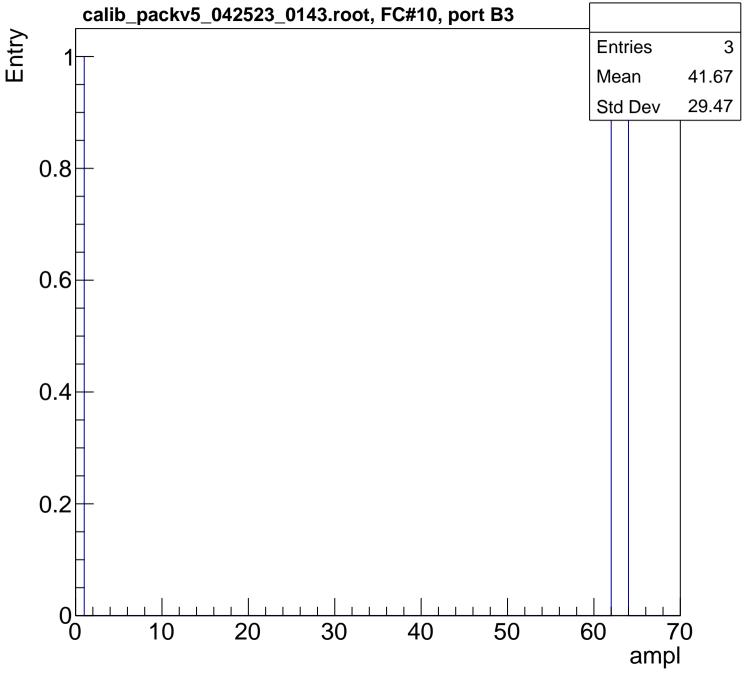


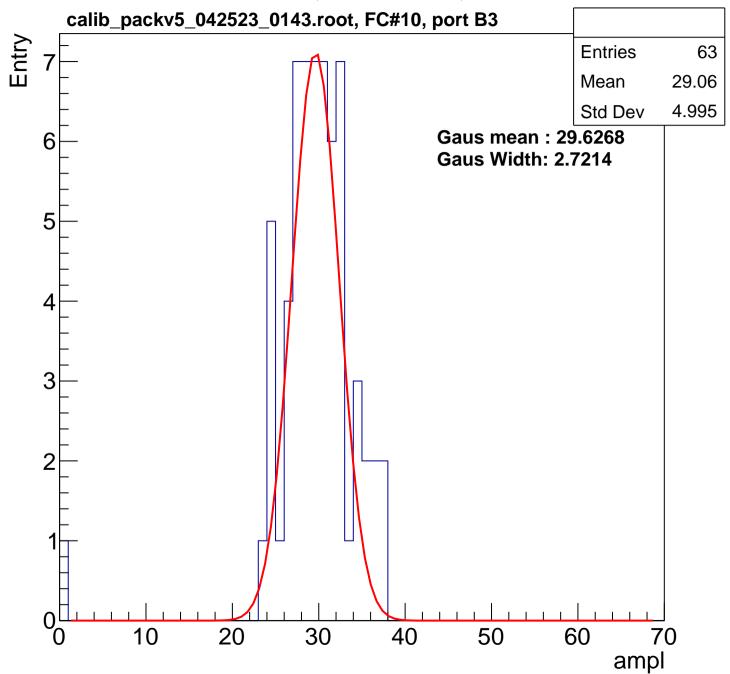


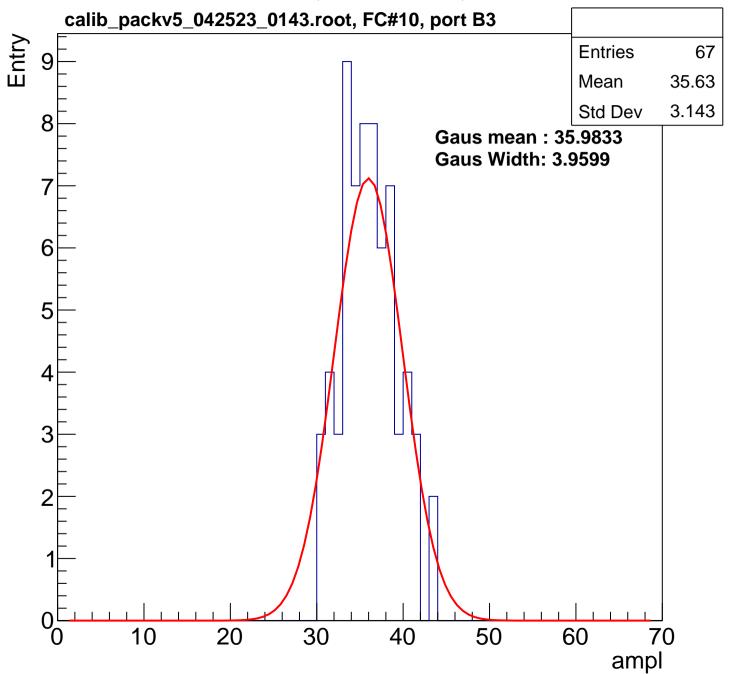


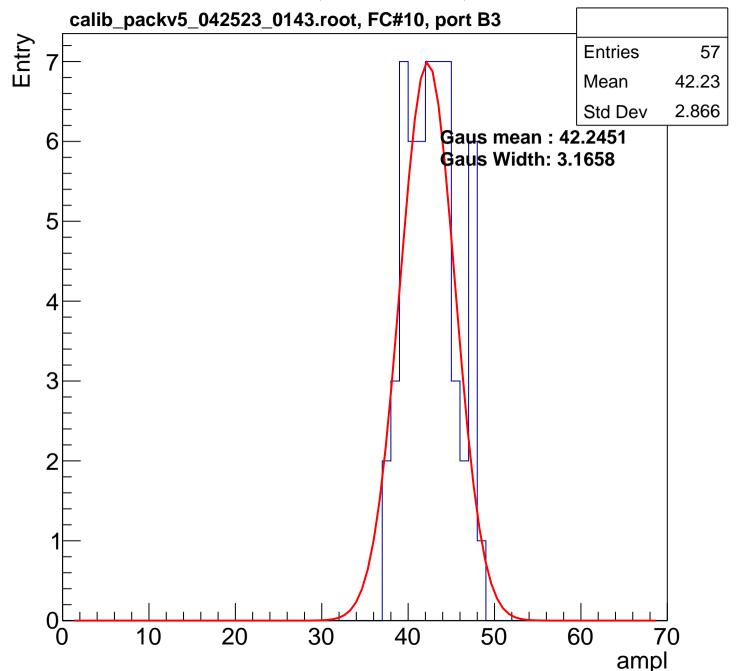


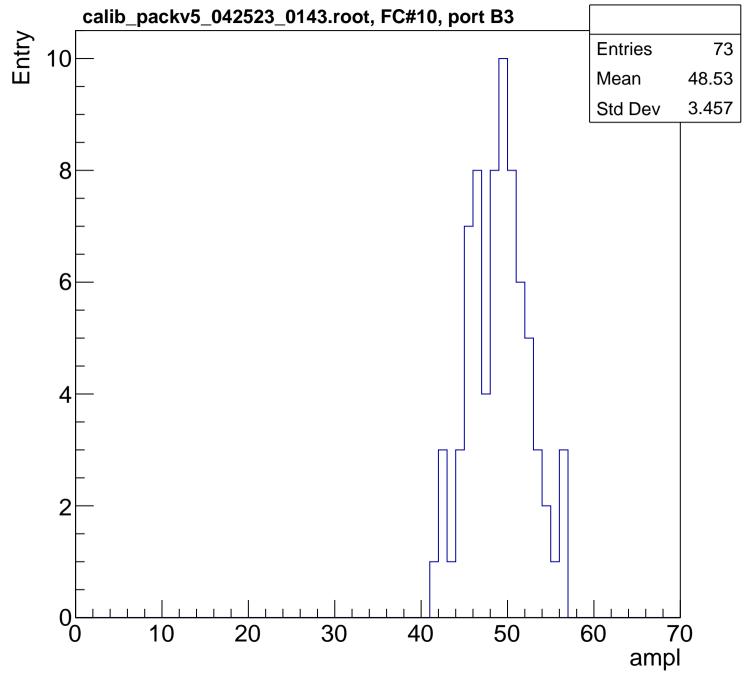


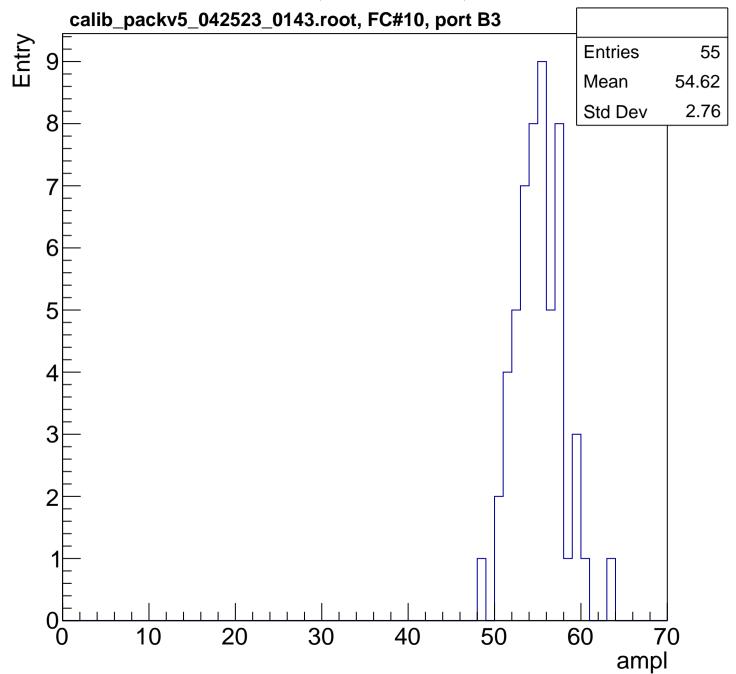


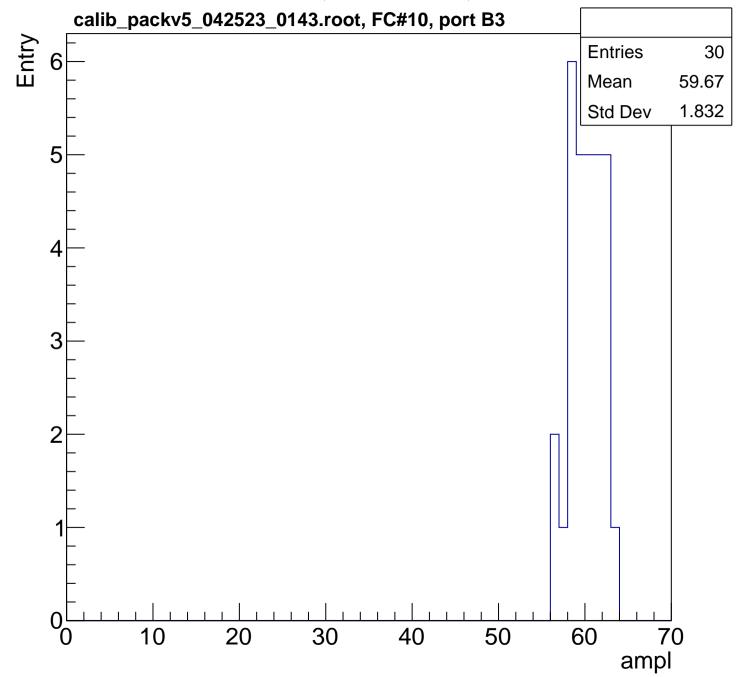


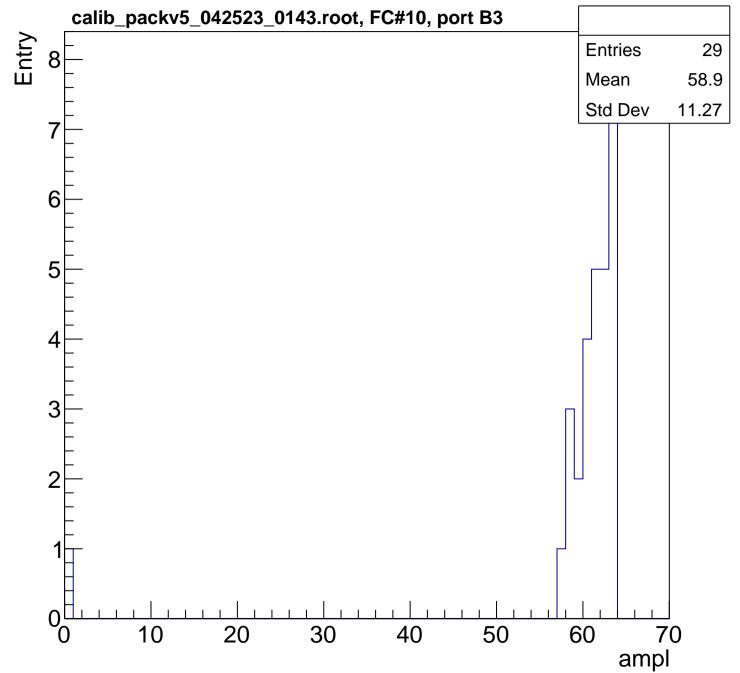


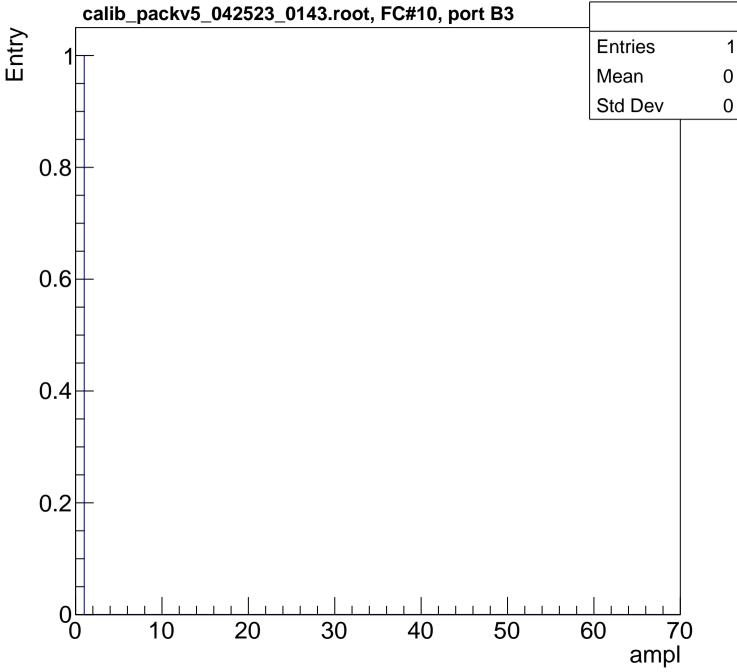


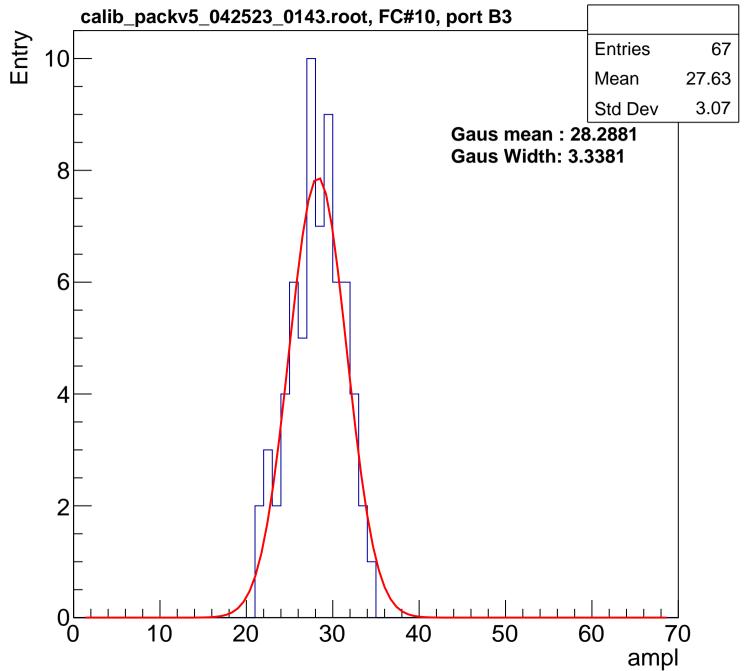


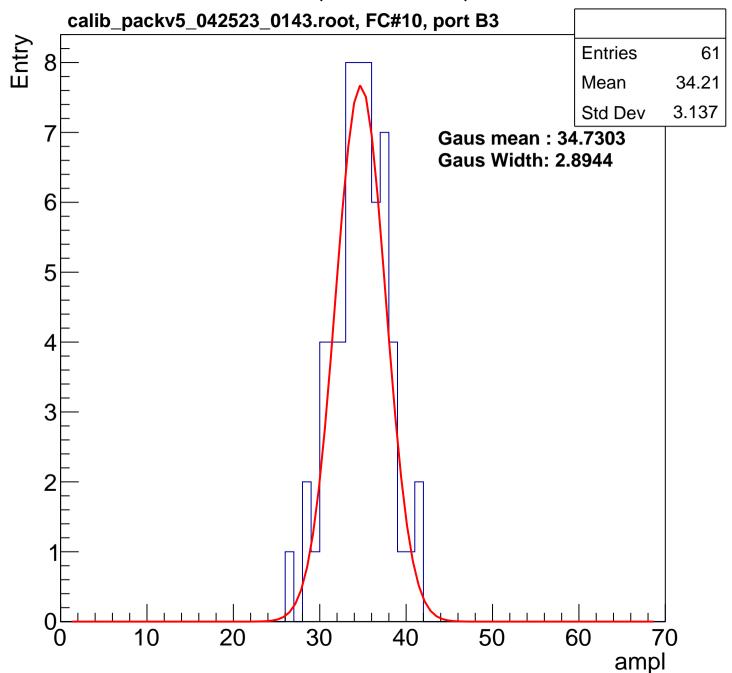


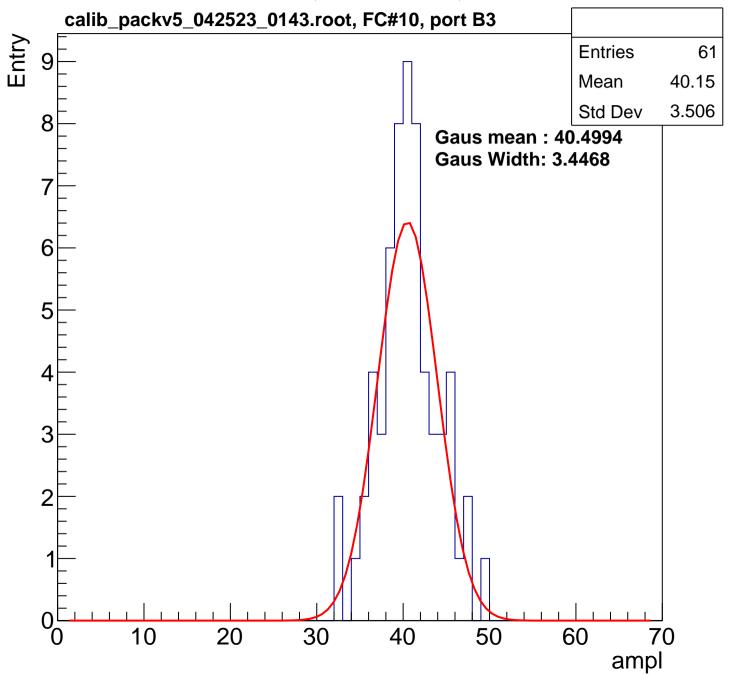


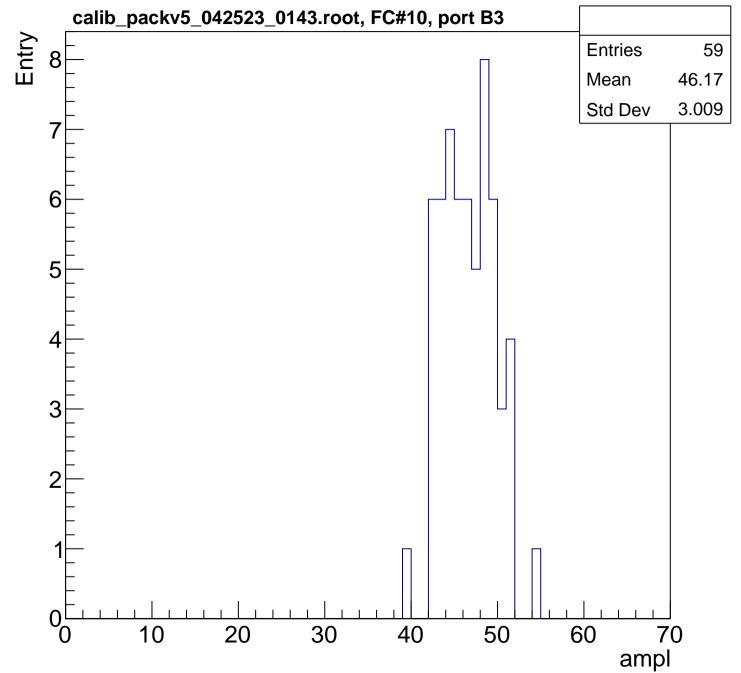


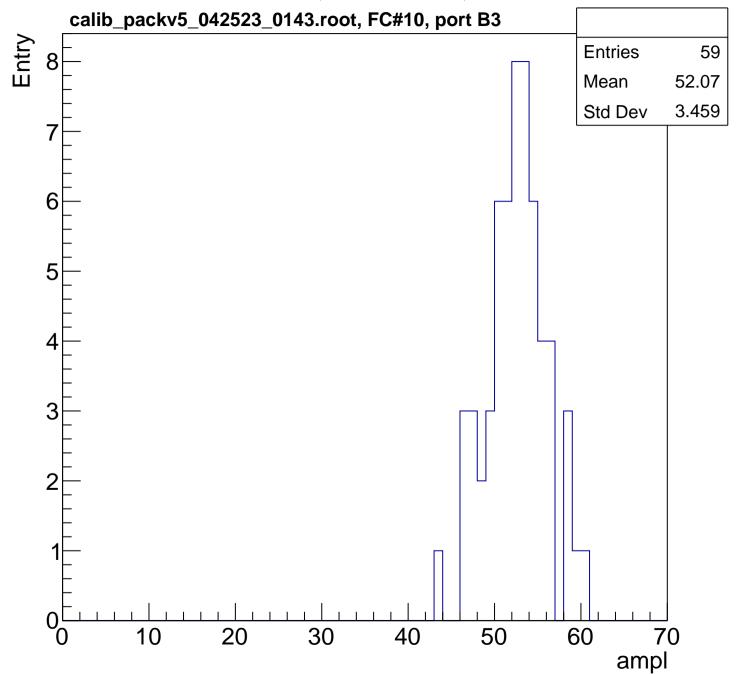


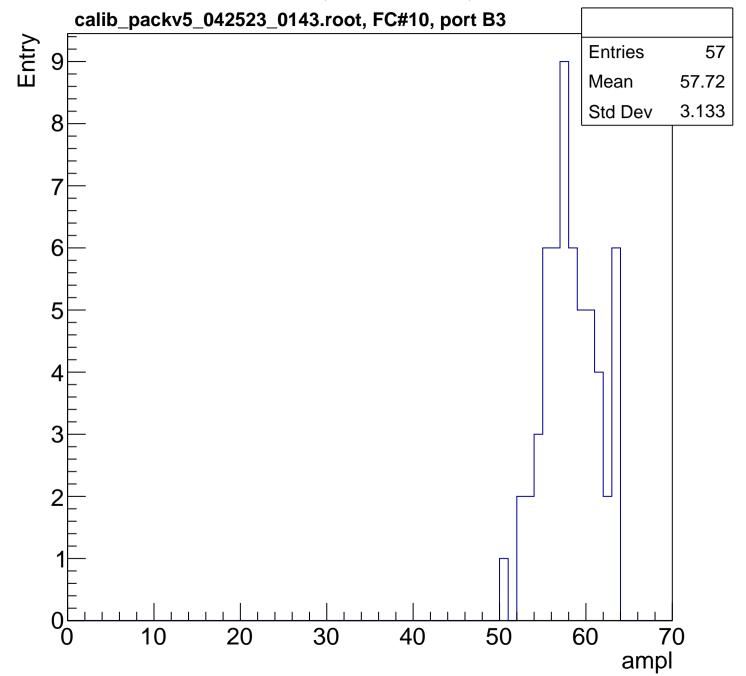


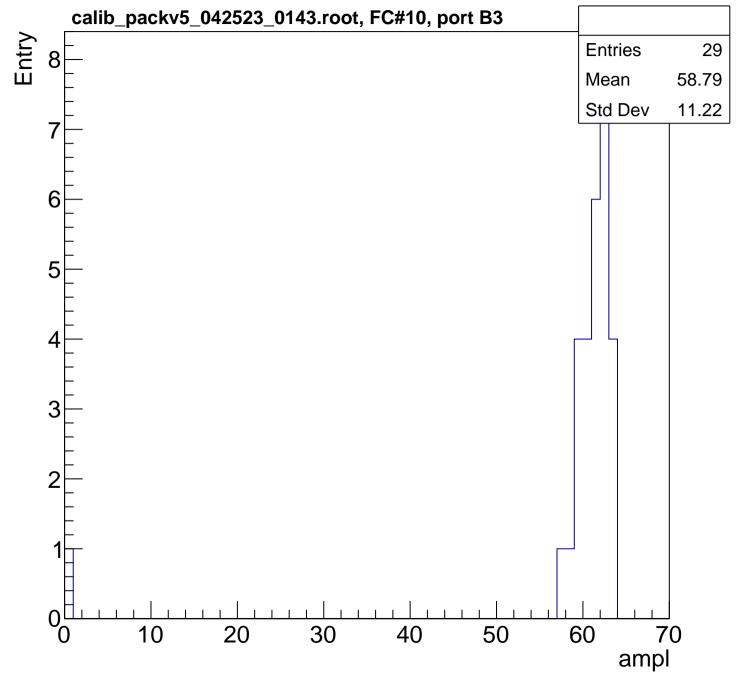


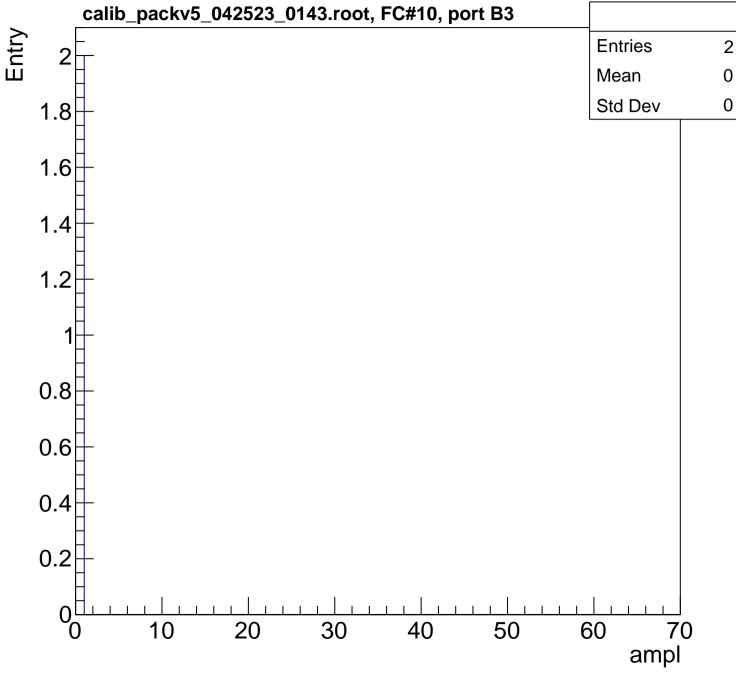


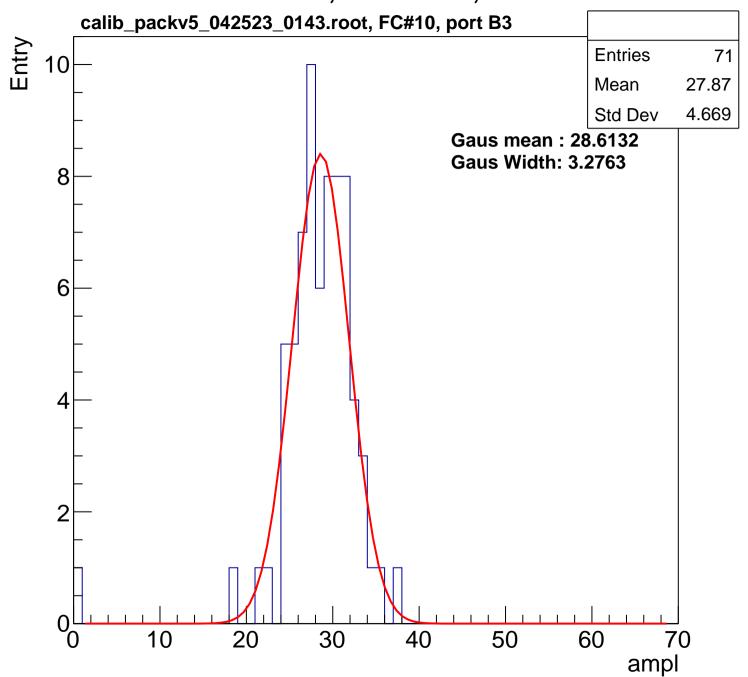


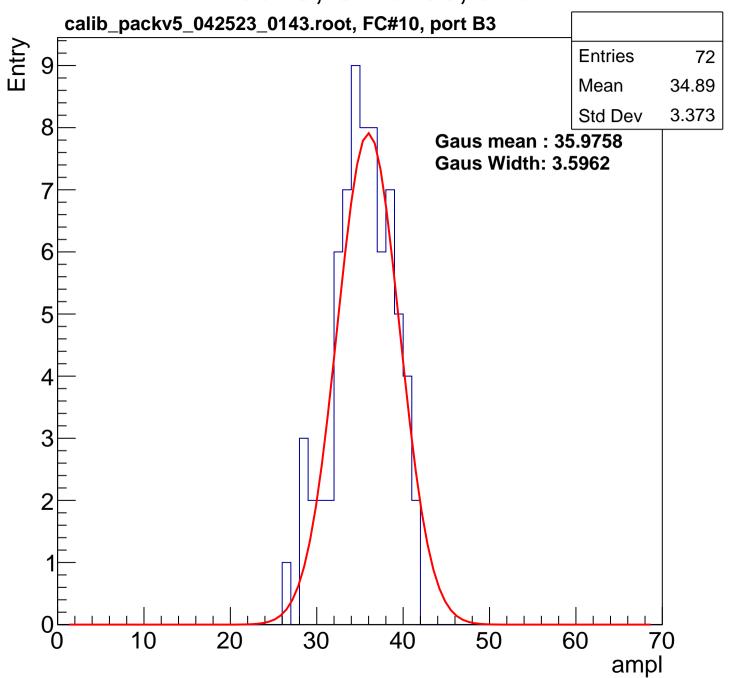


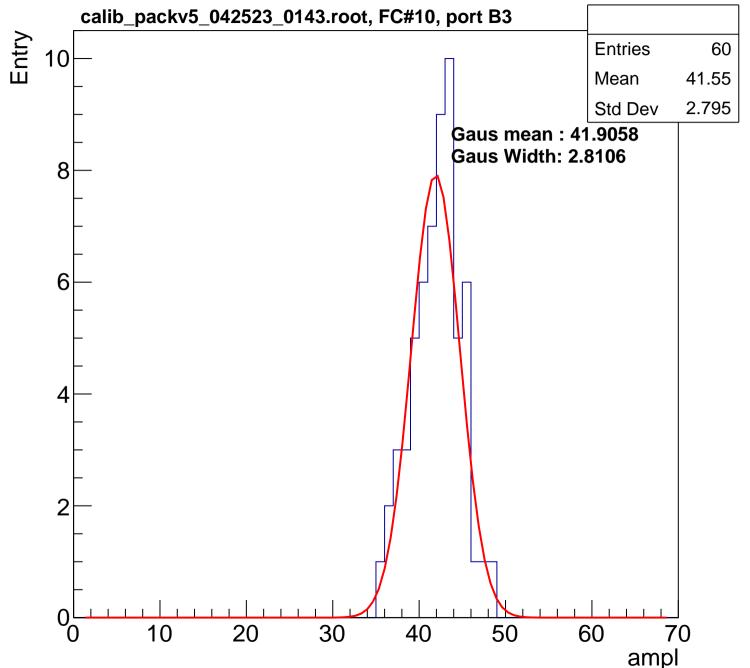


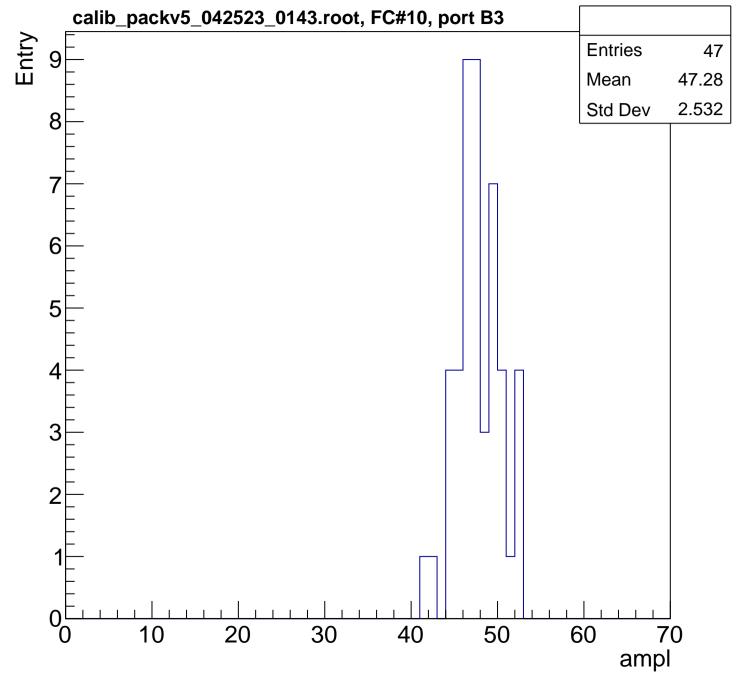


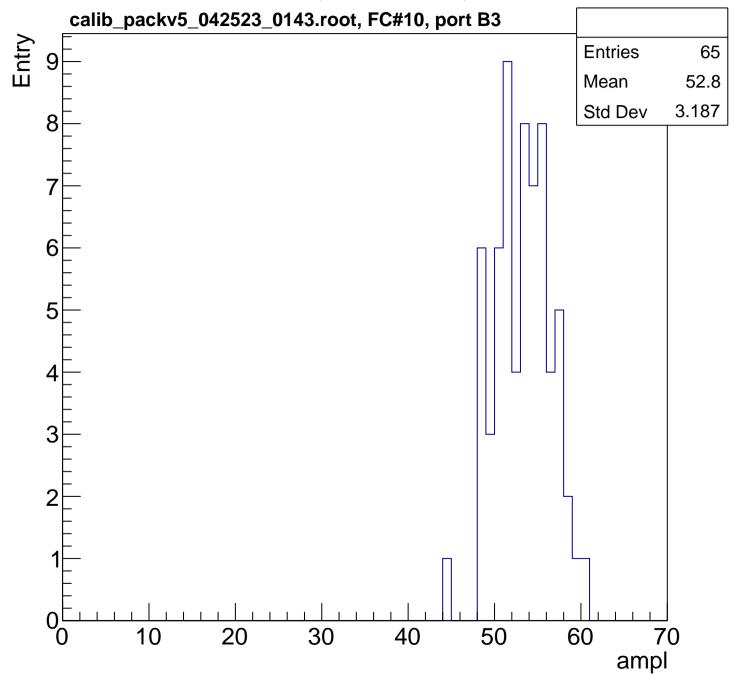


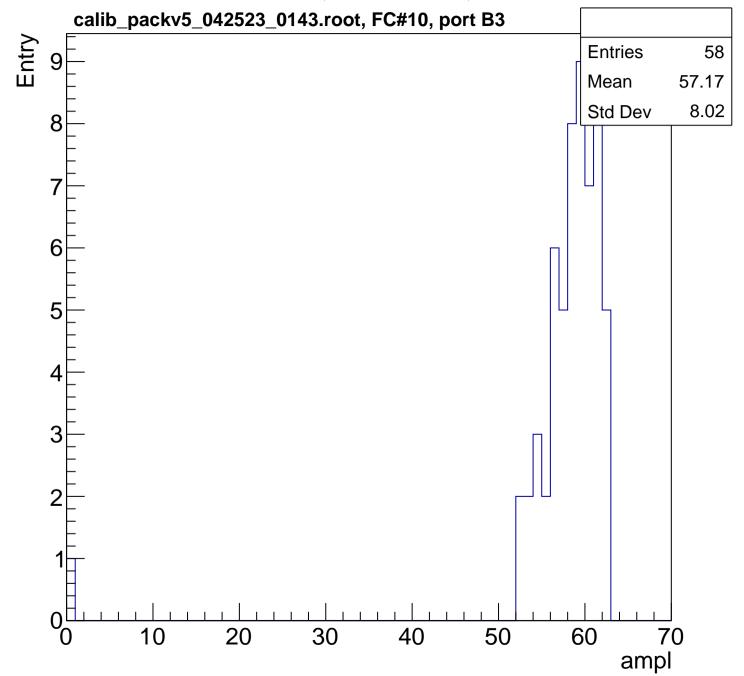


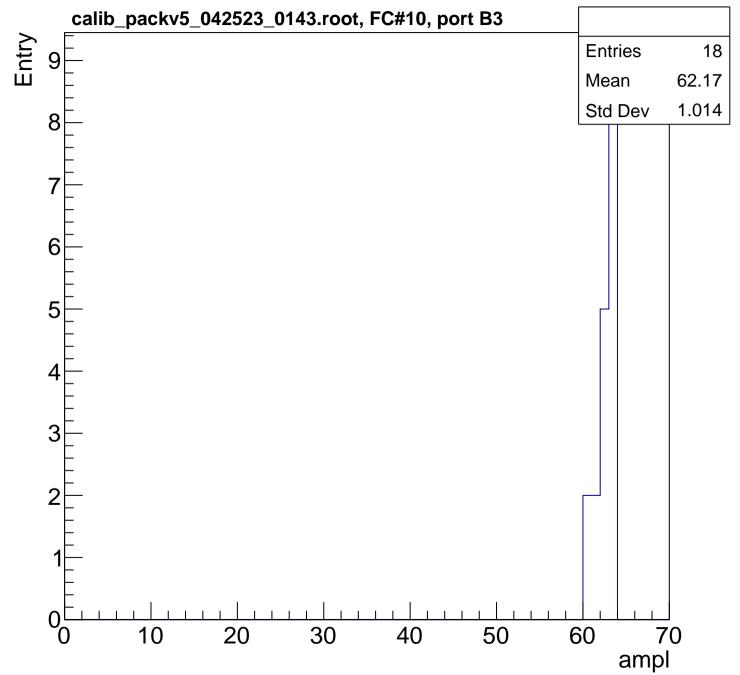




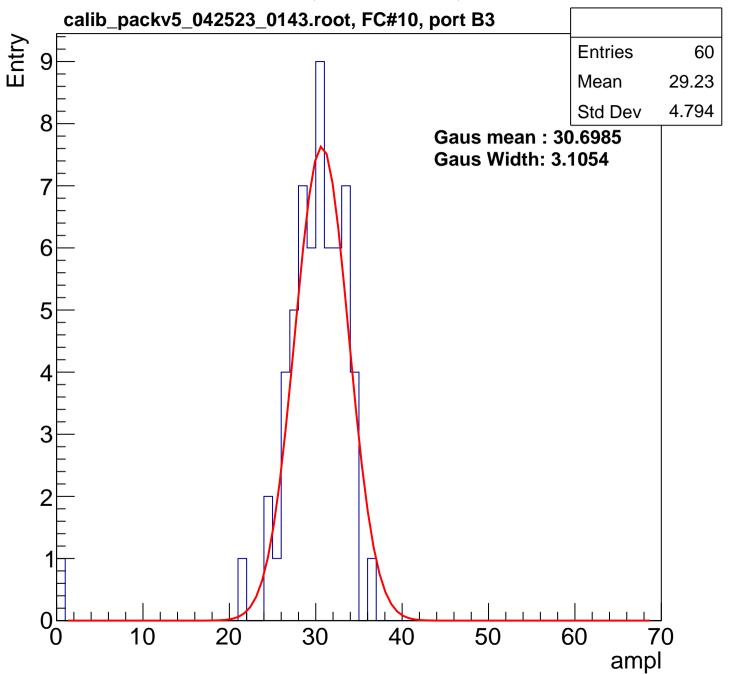


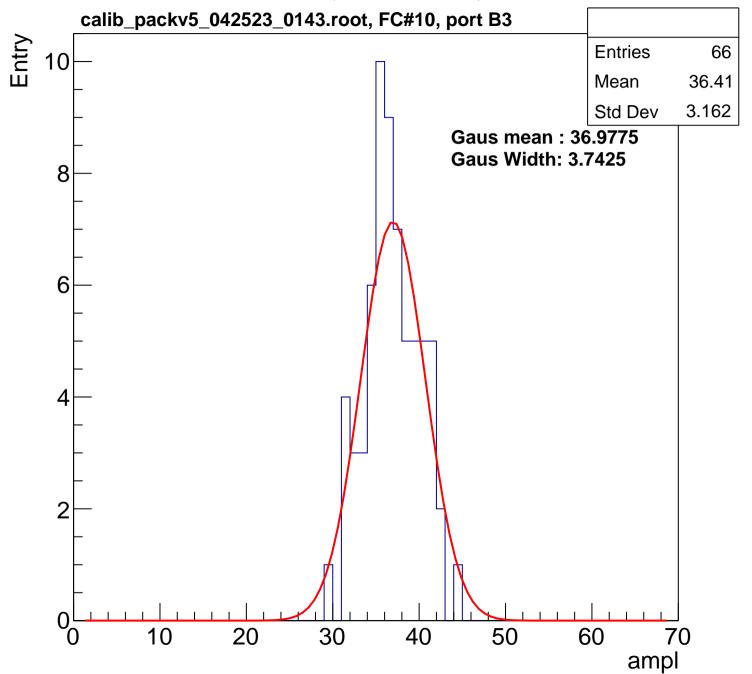


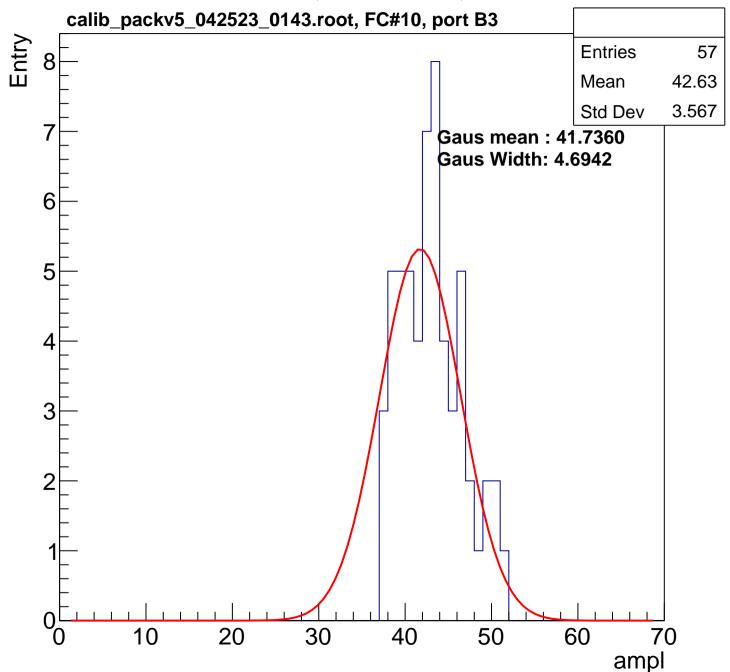


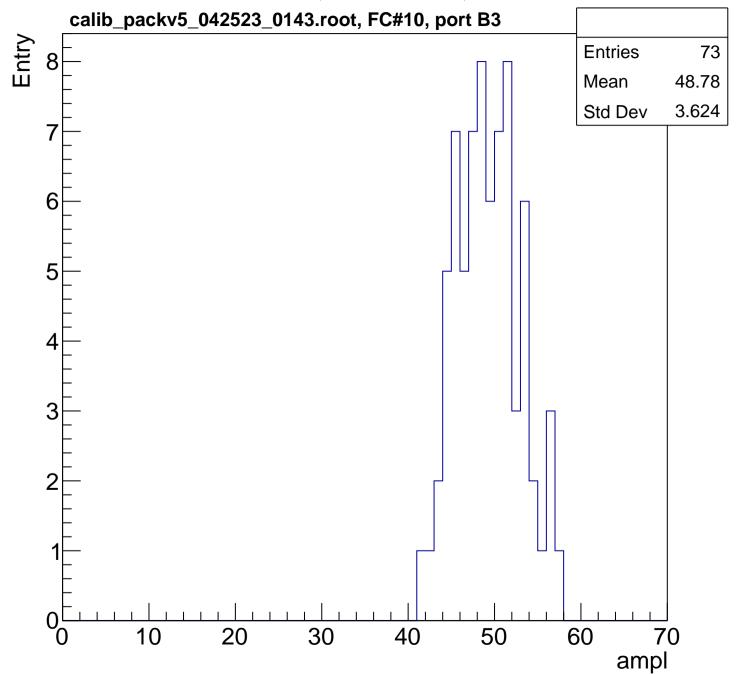


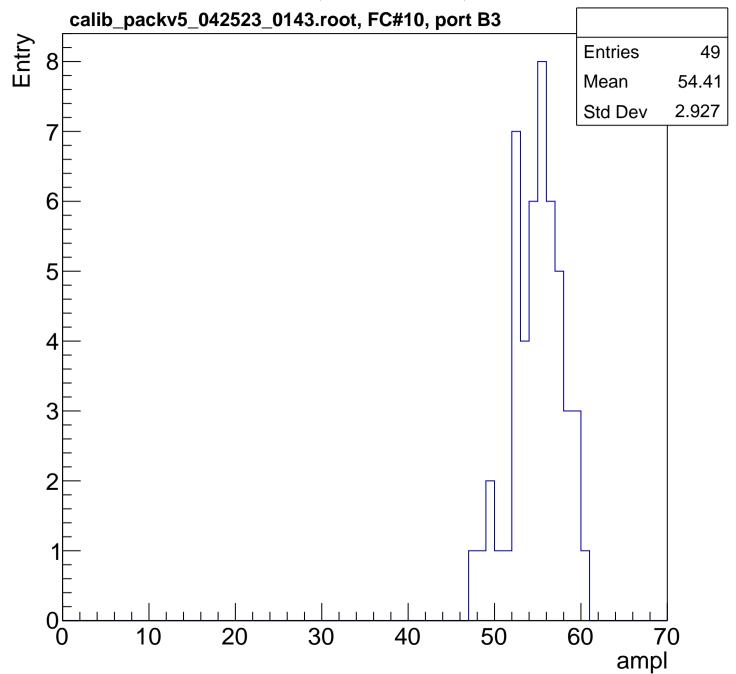


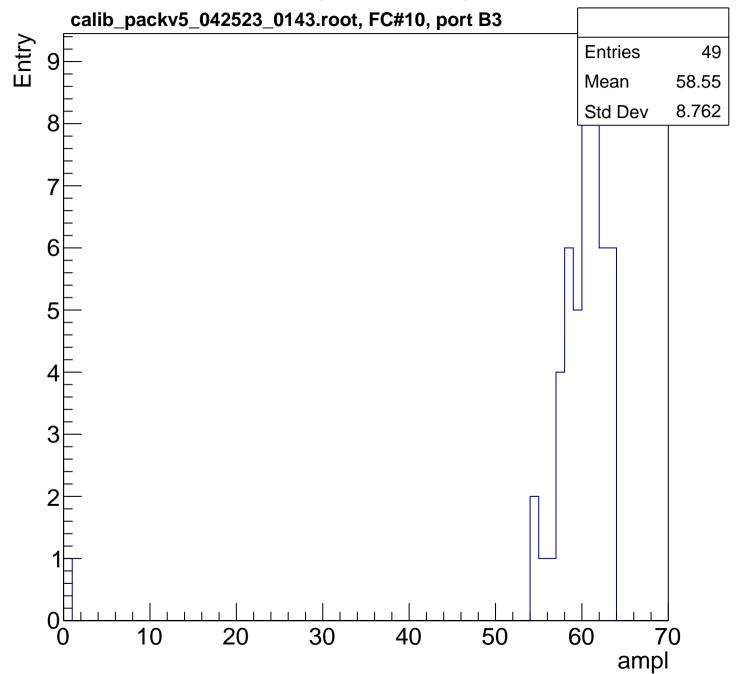


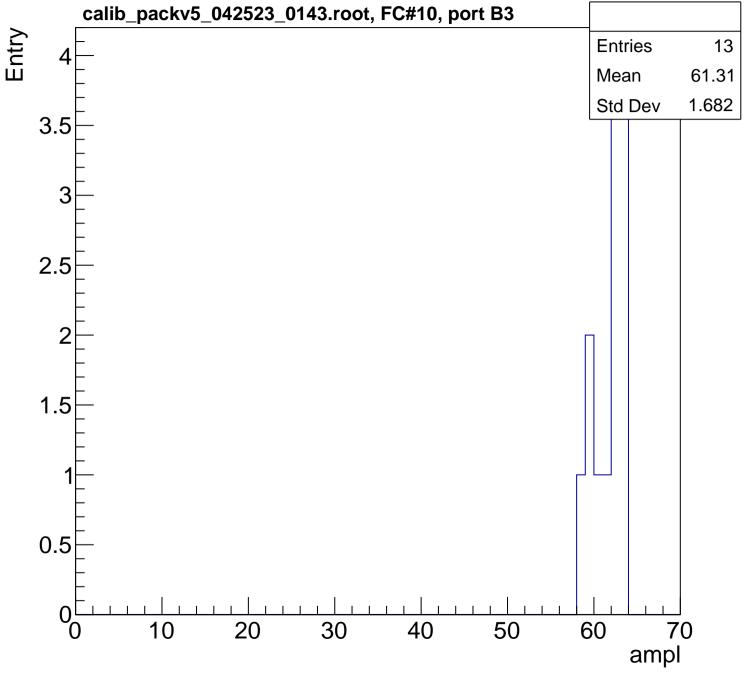


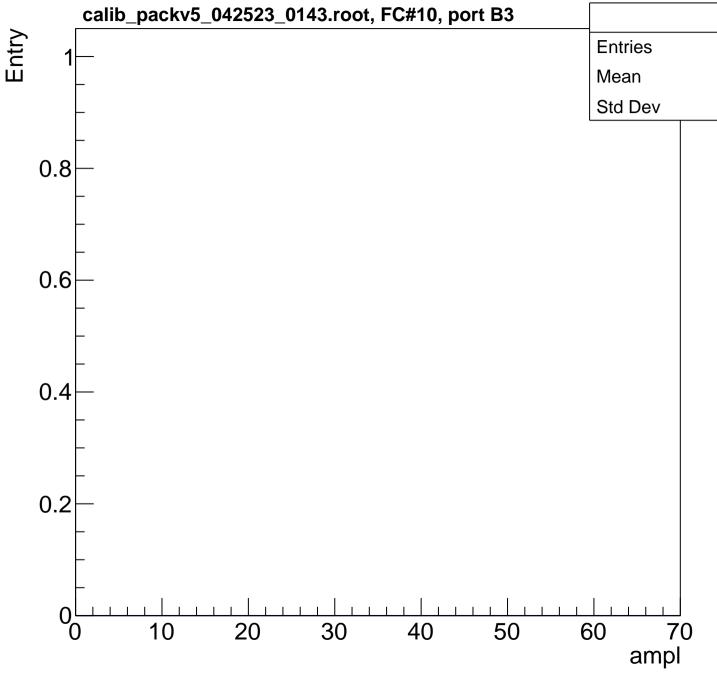


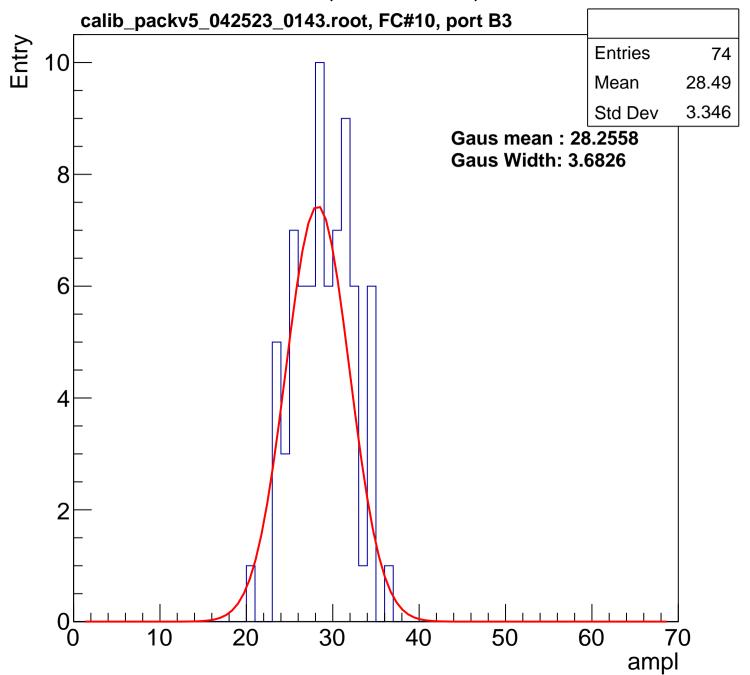


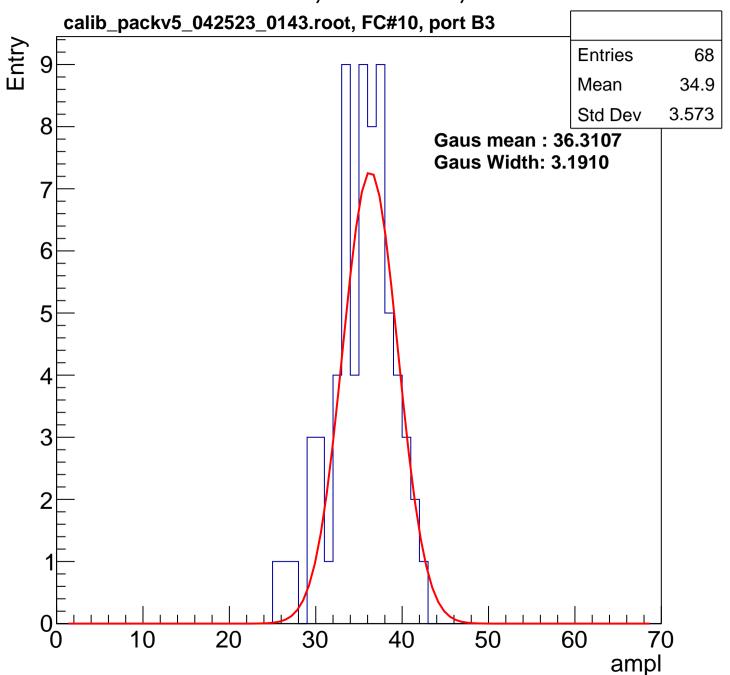


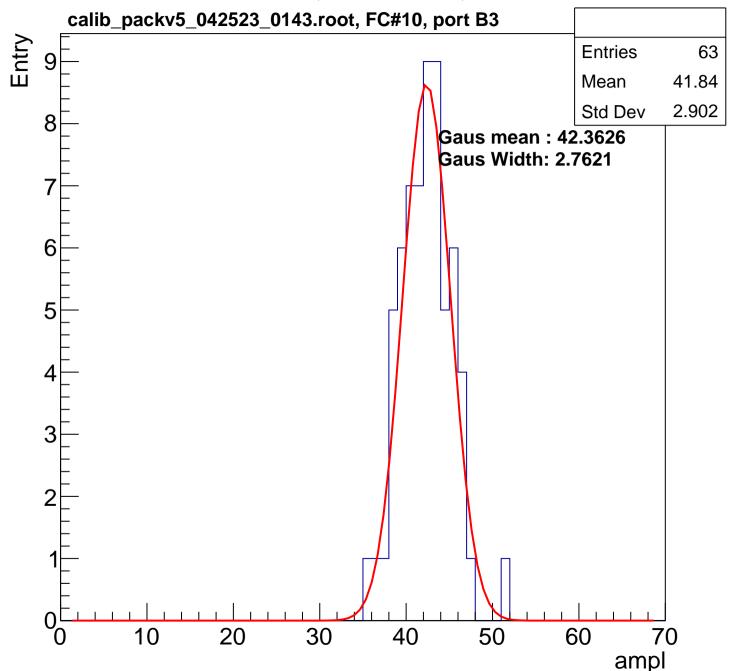


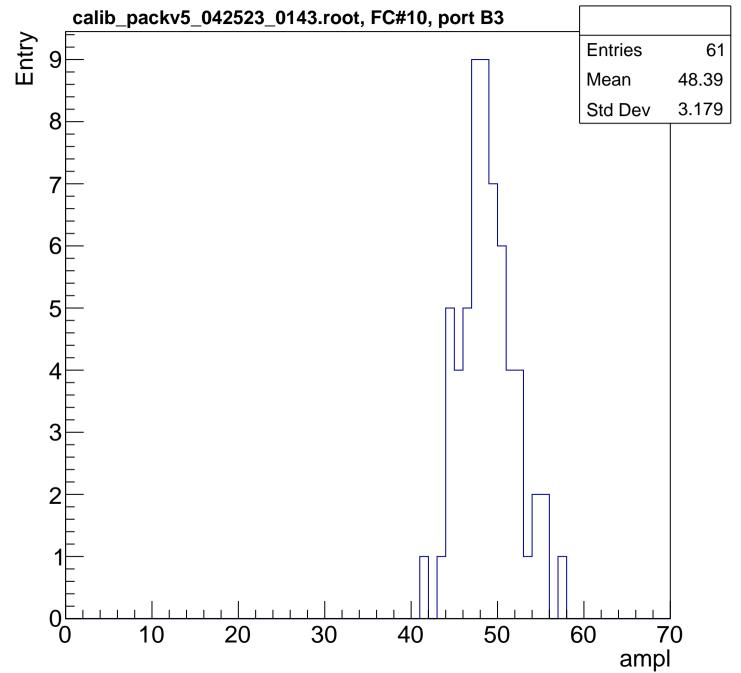


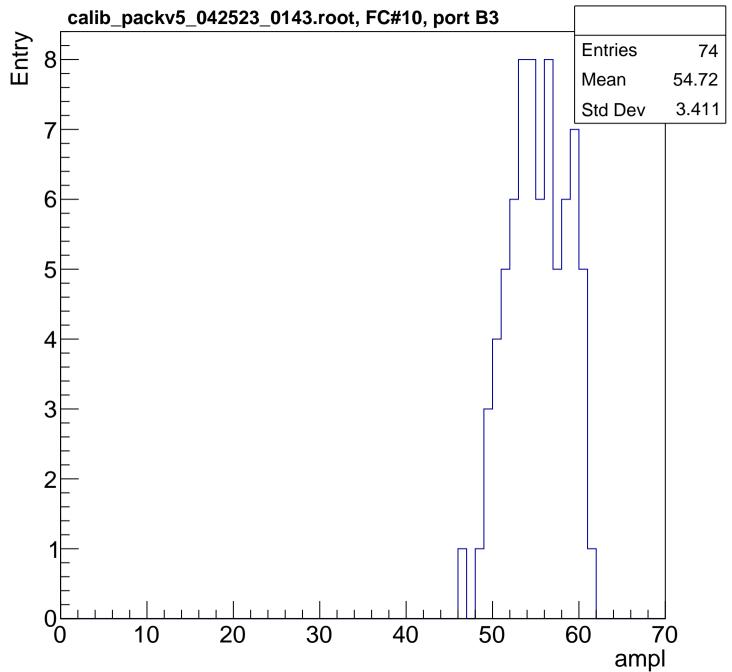


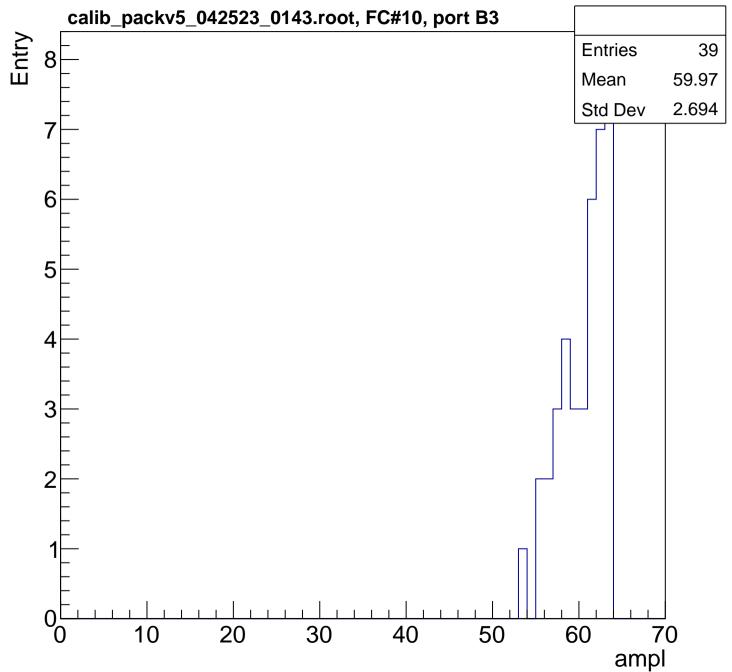


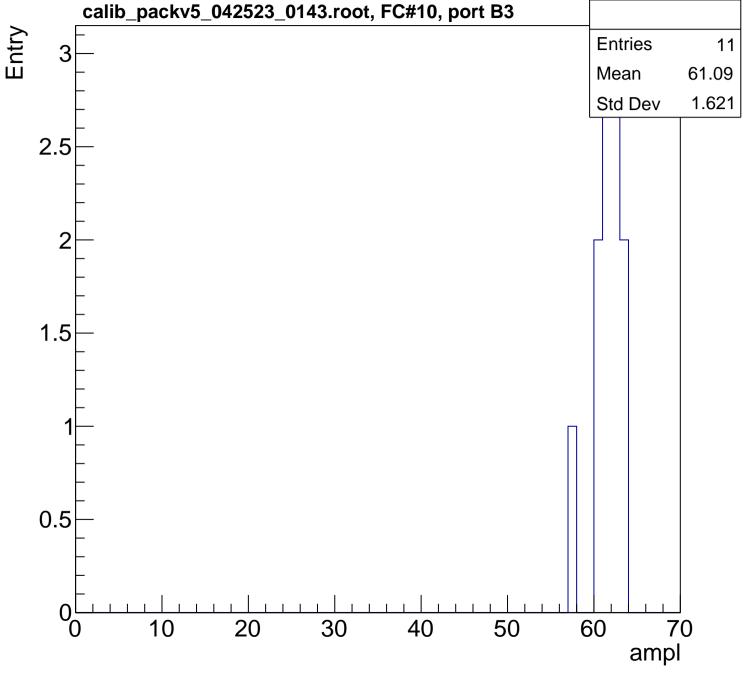


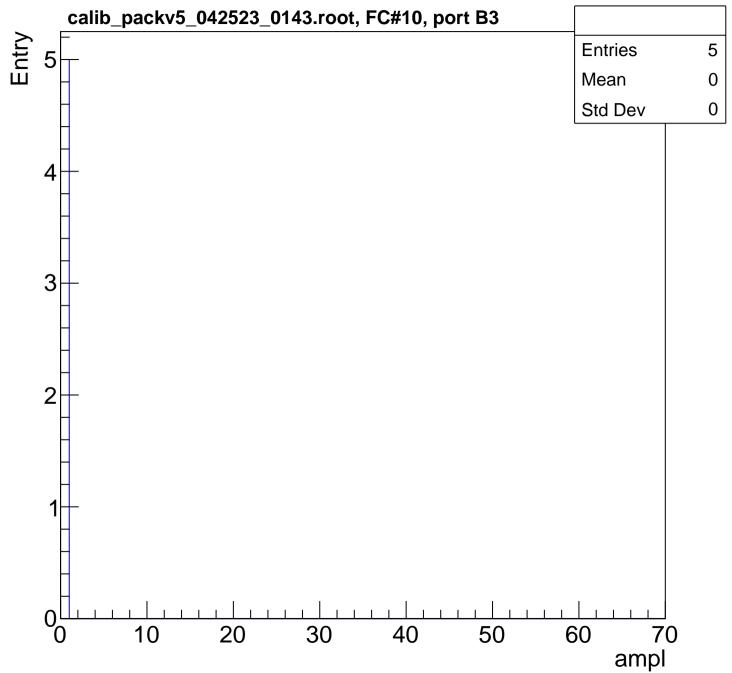


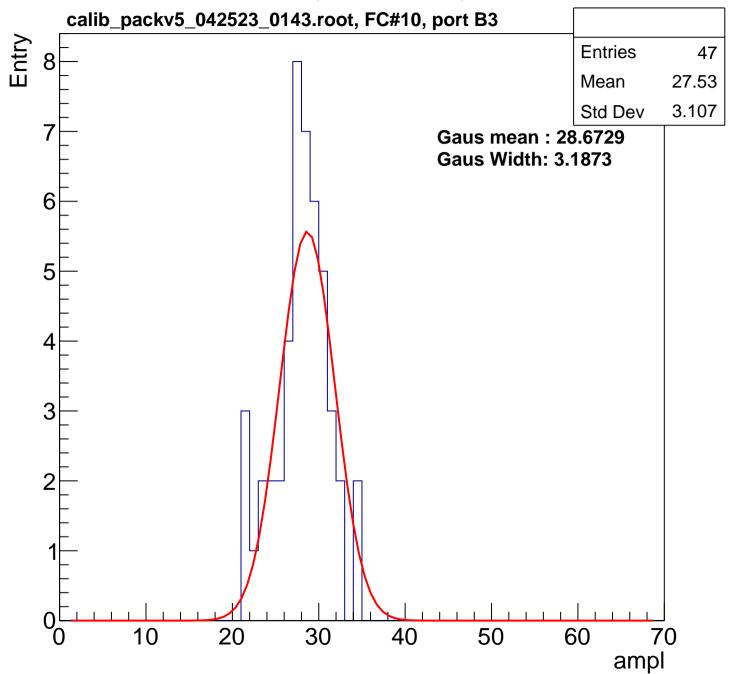


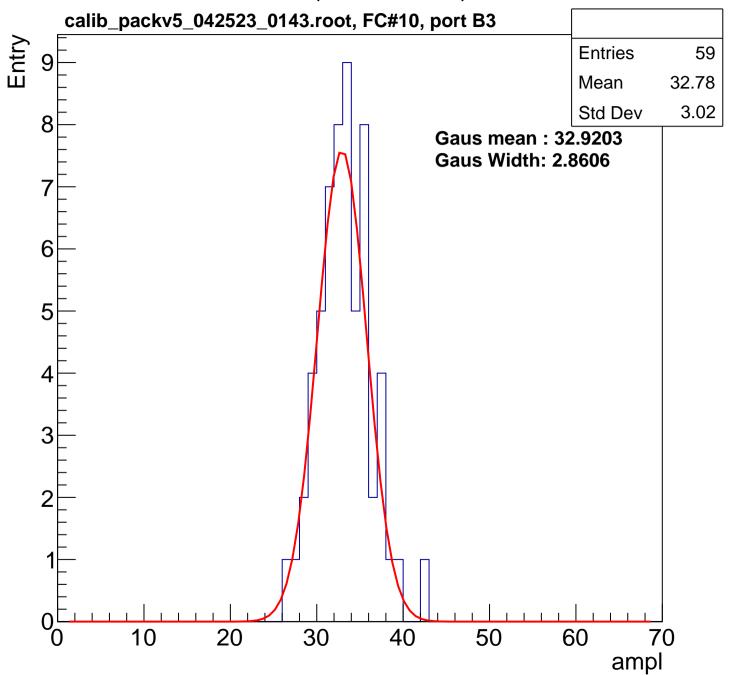


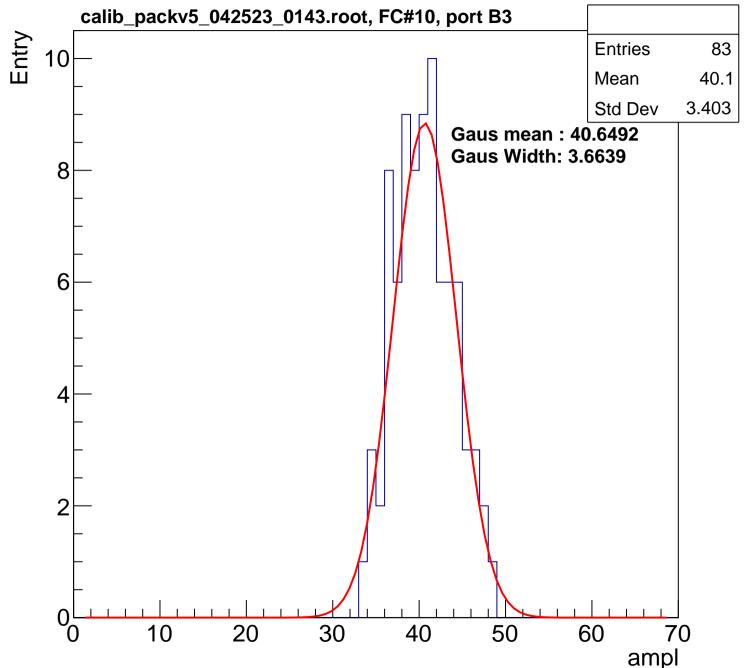


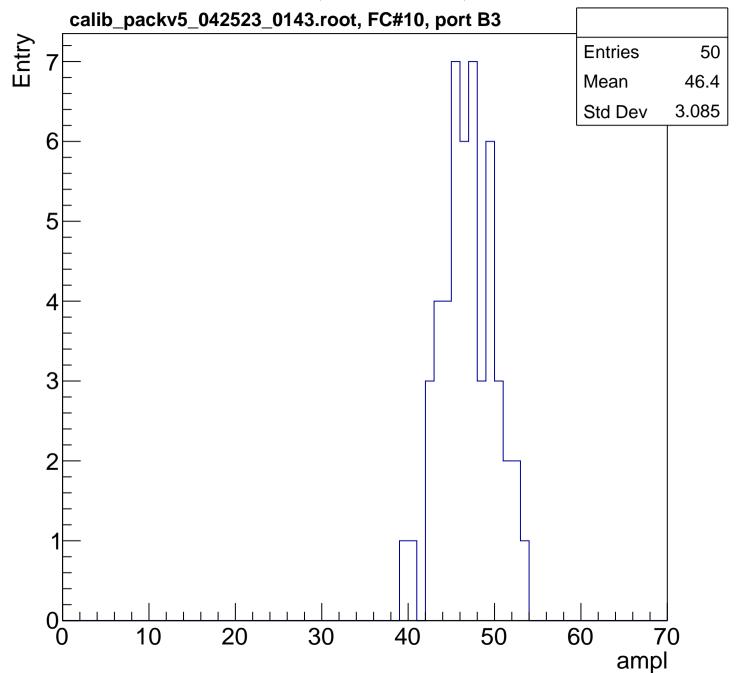


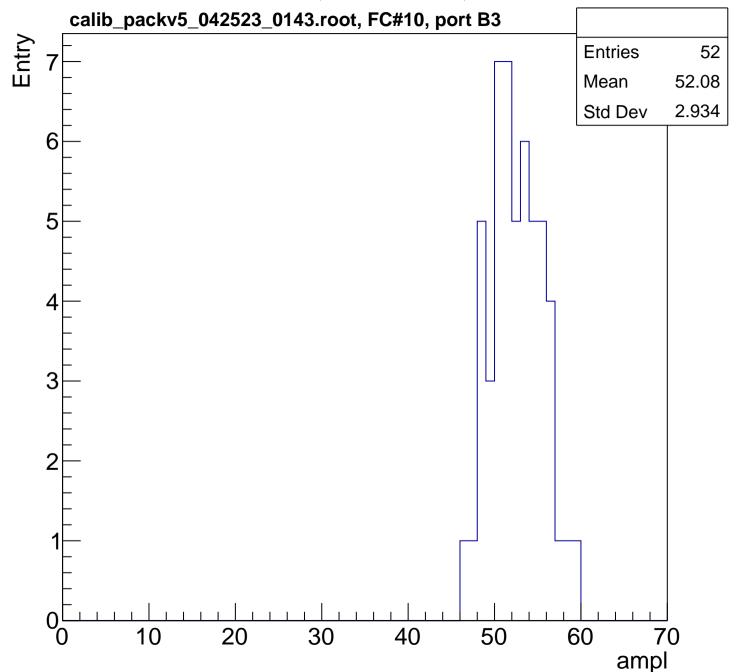


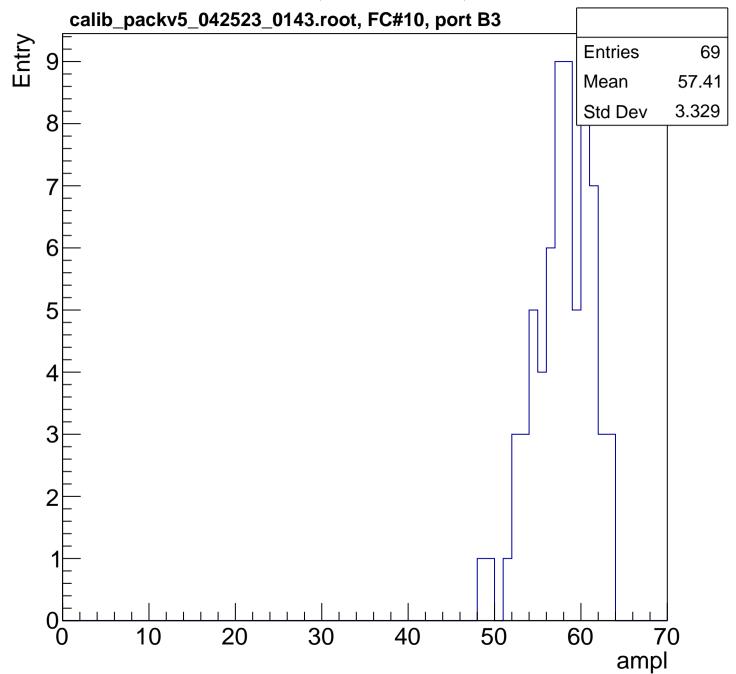


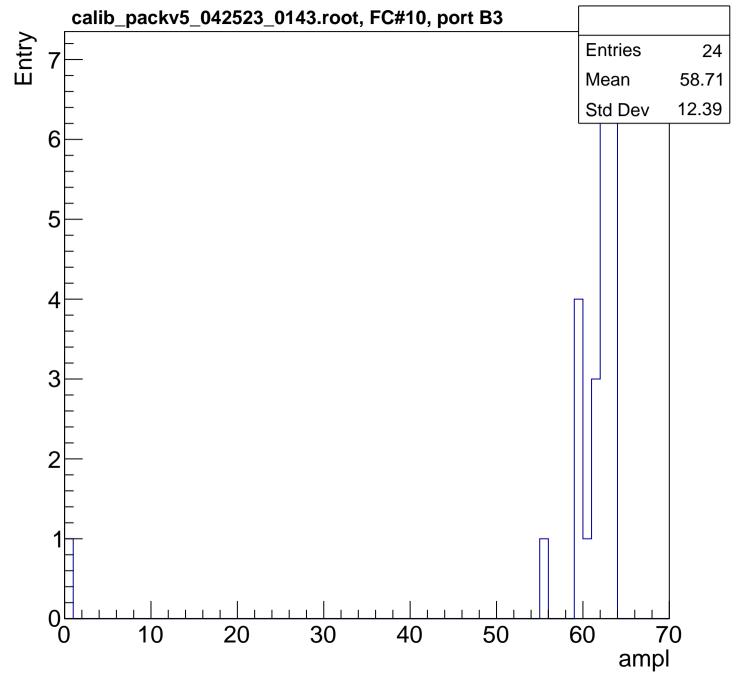




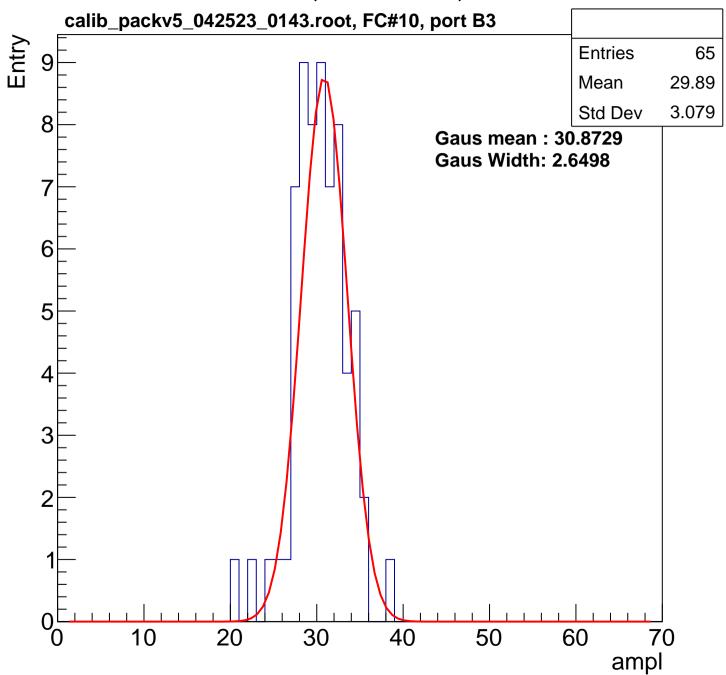


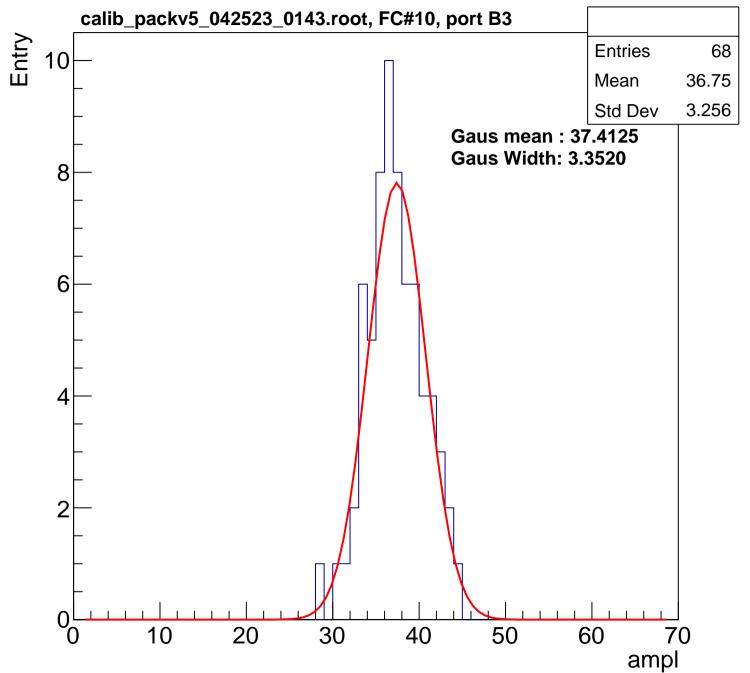


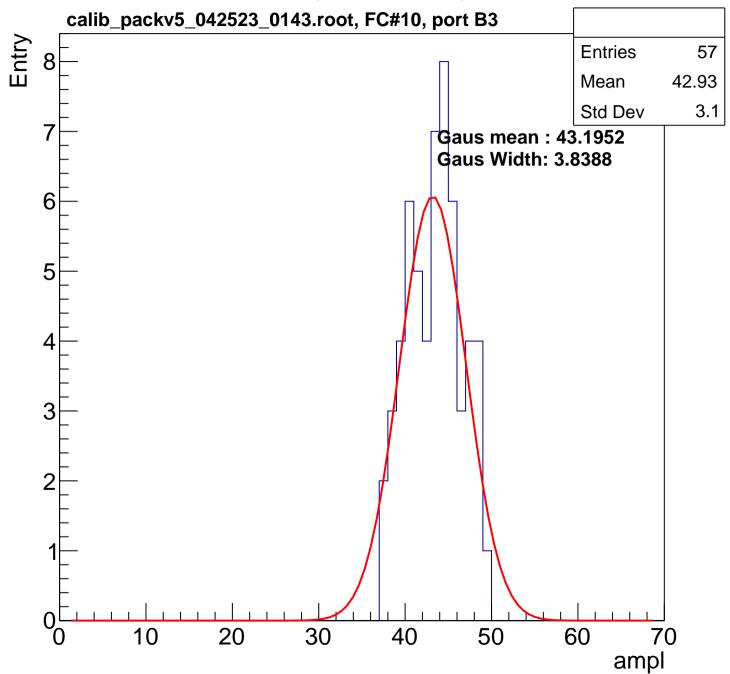


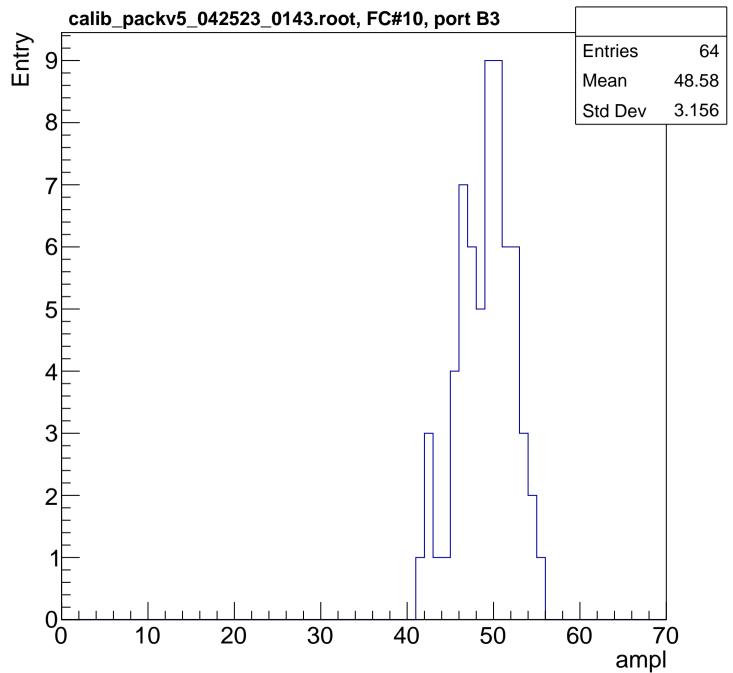


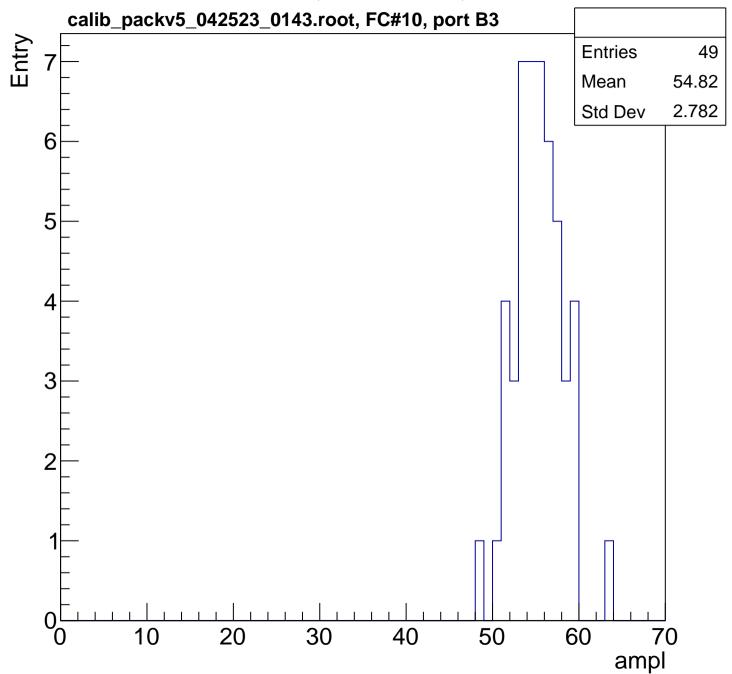
B1L002S, U4-ch56, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

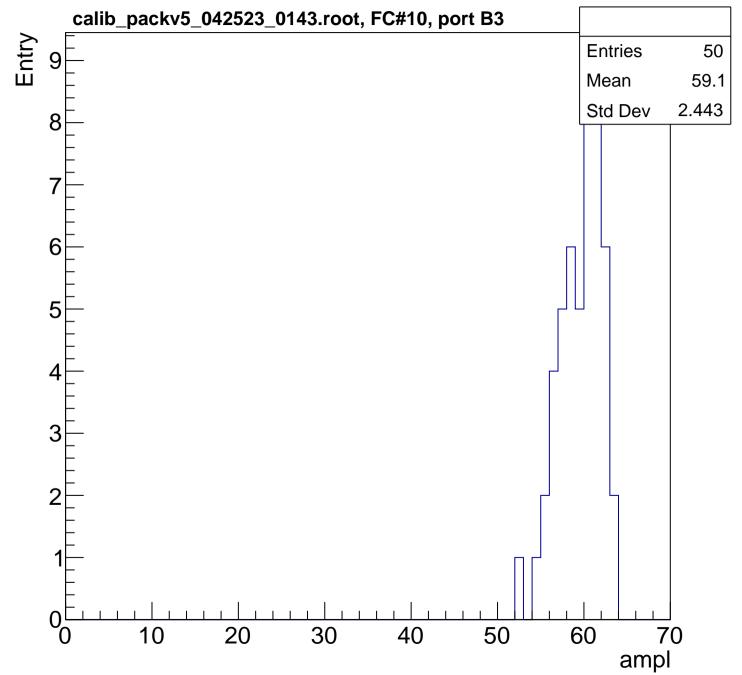


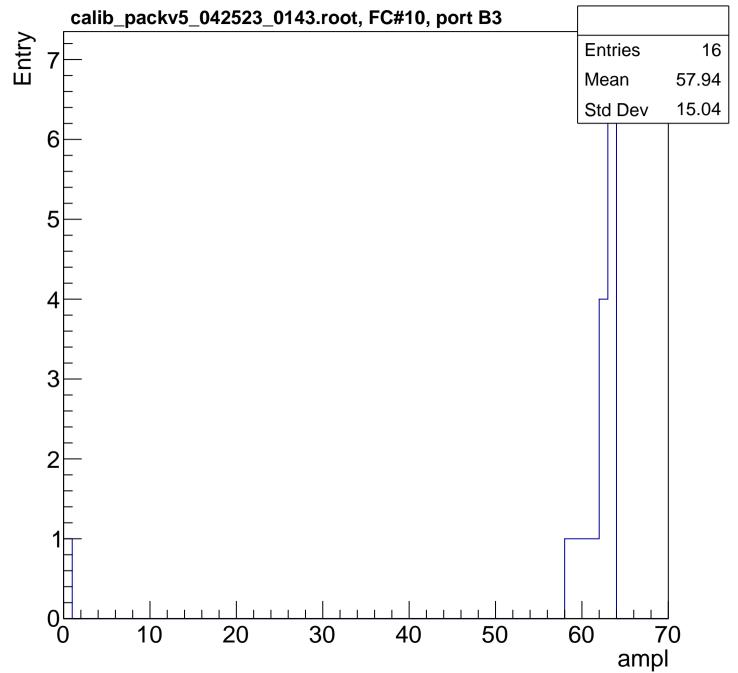




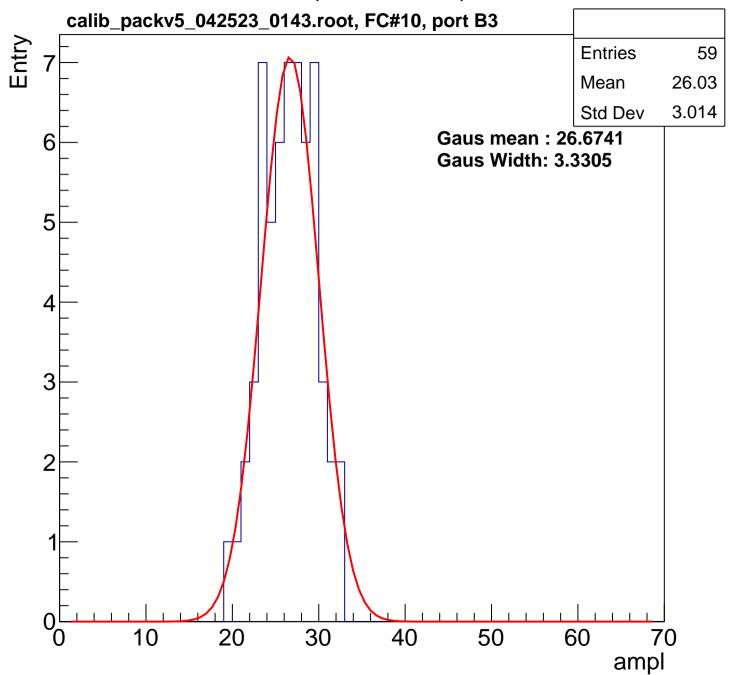


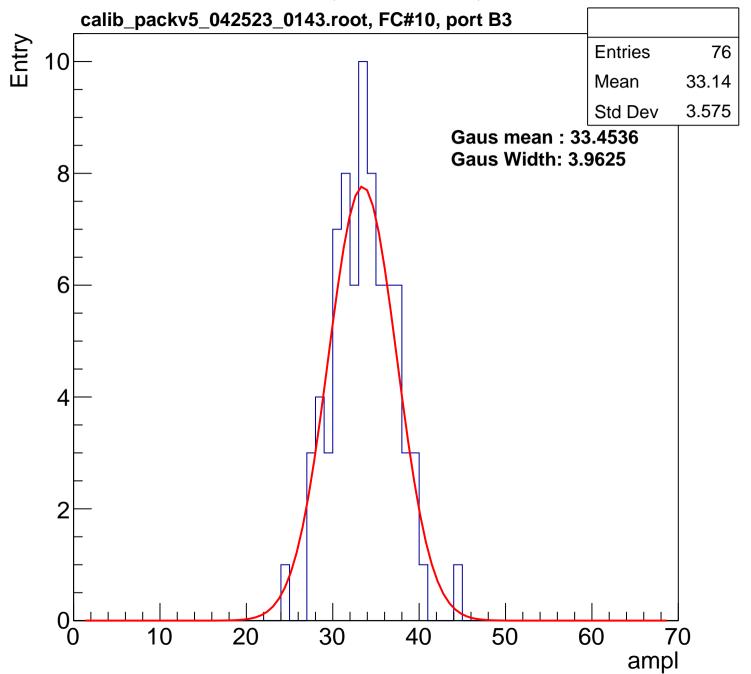


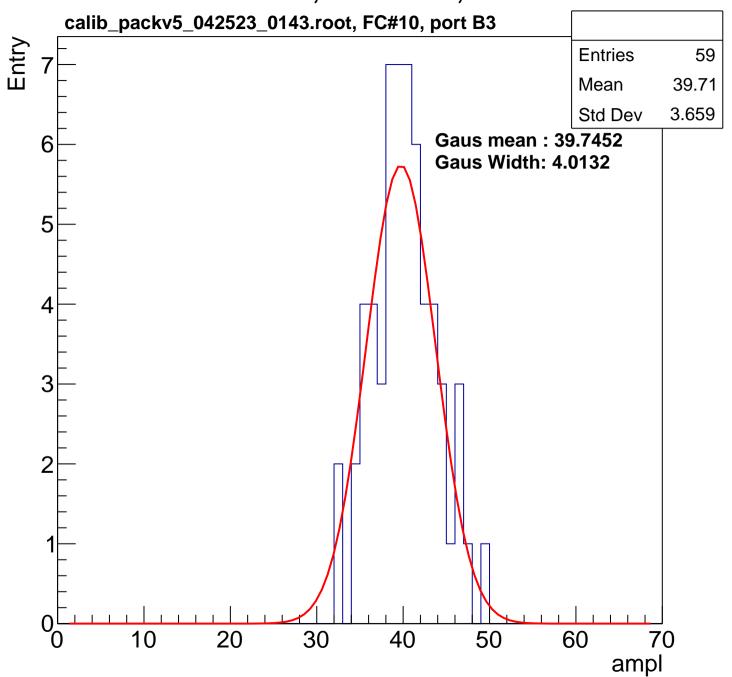


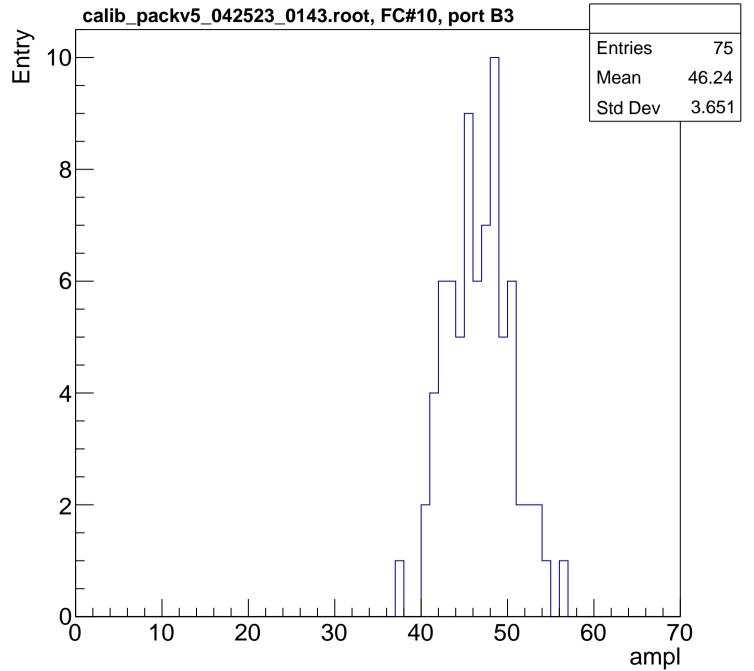


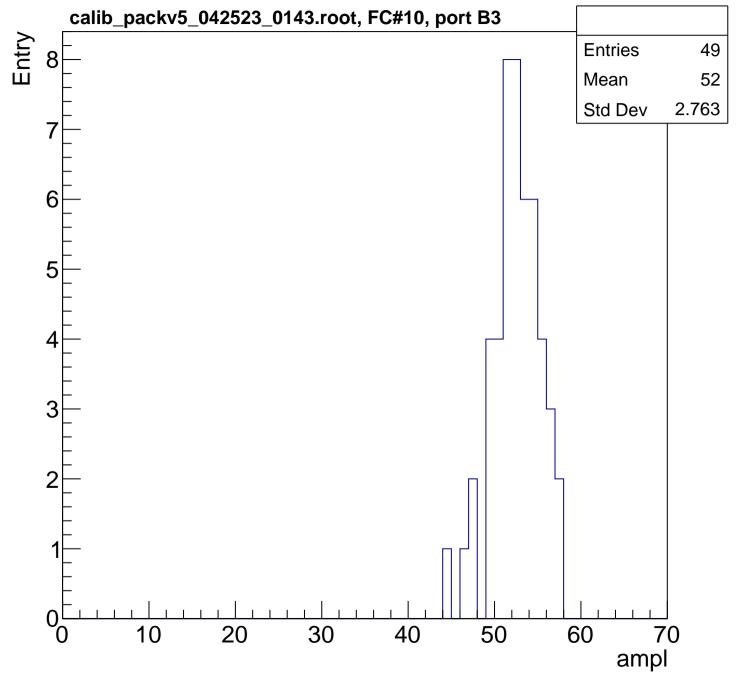


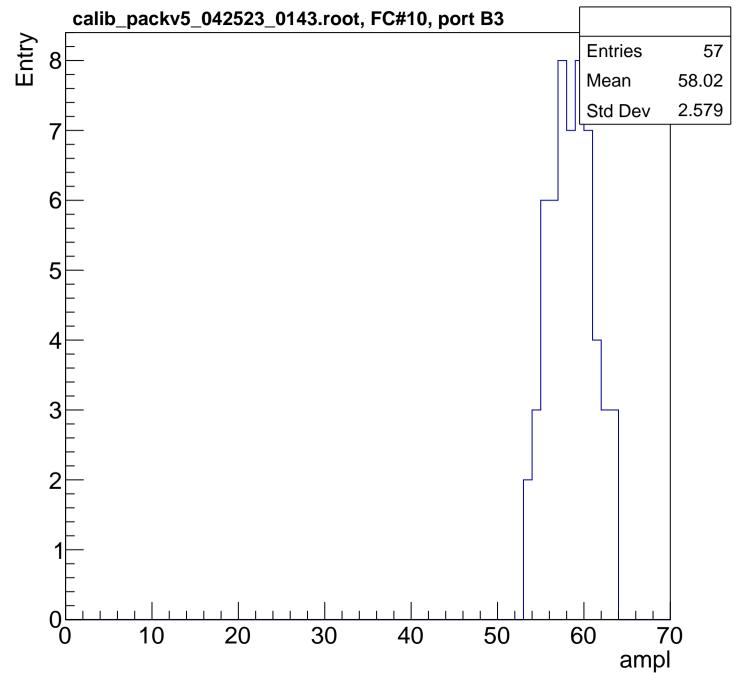


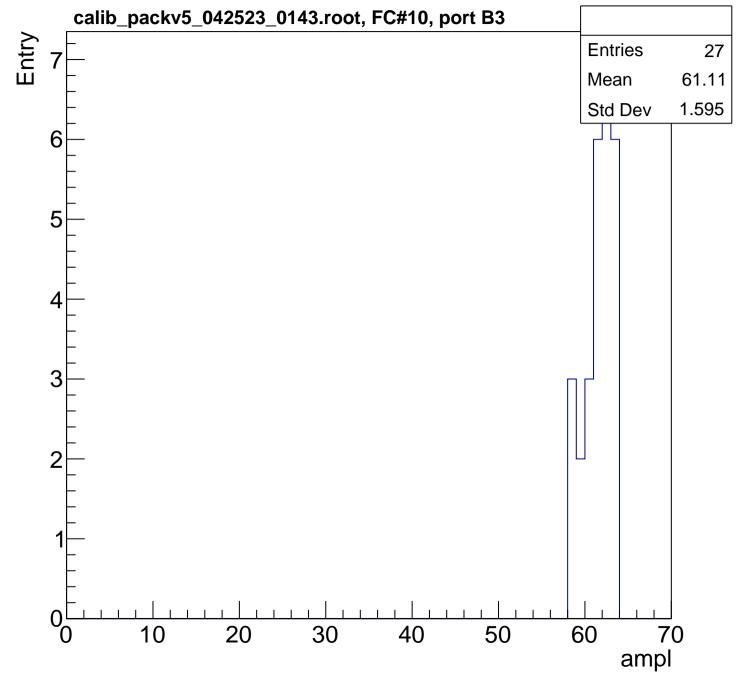


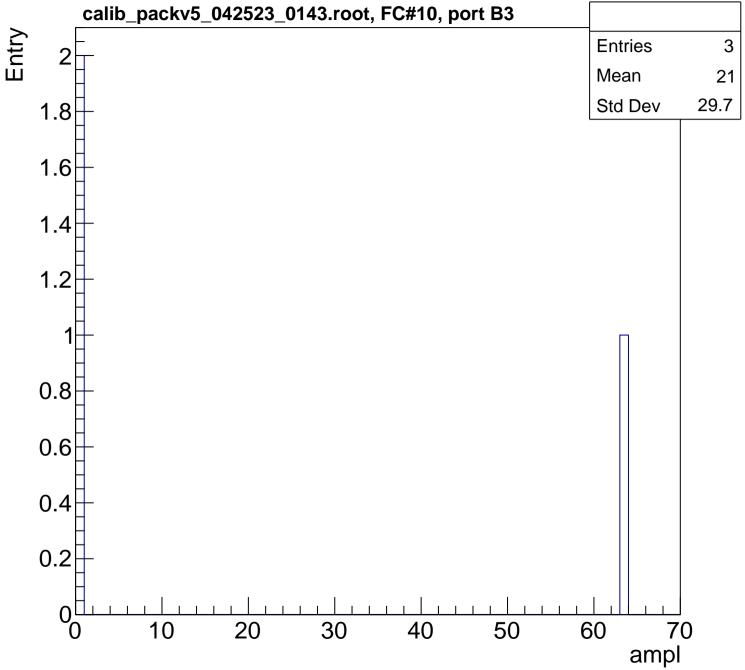


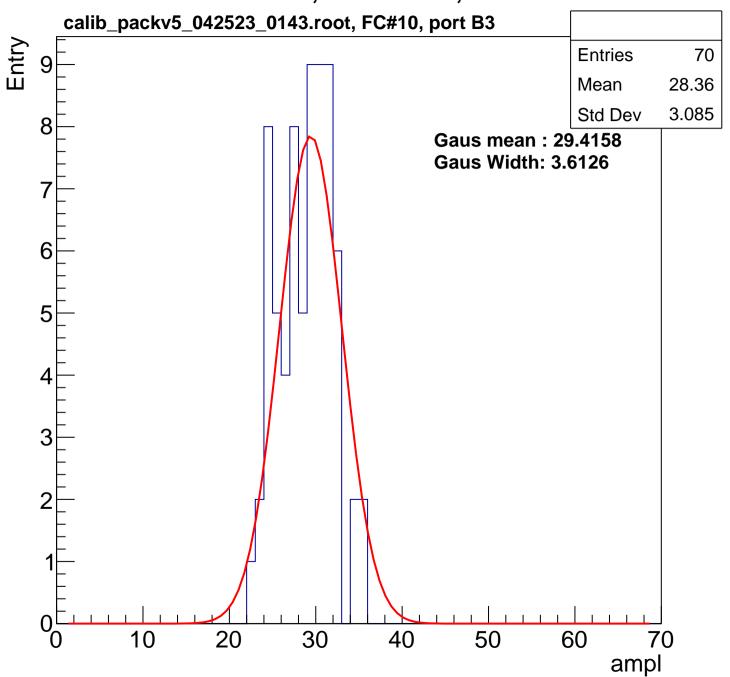


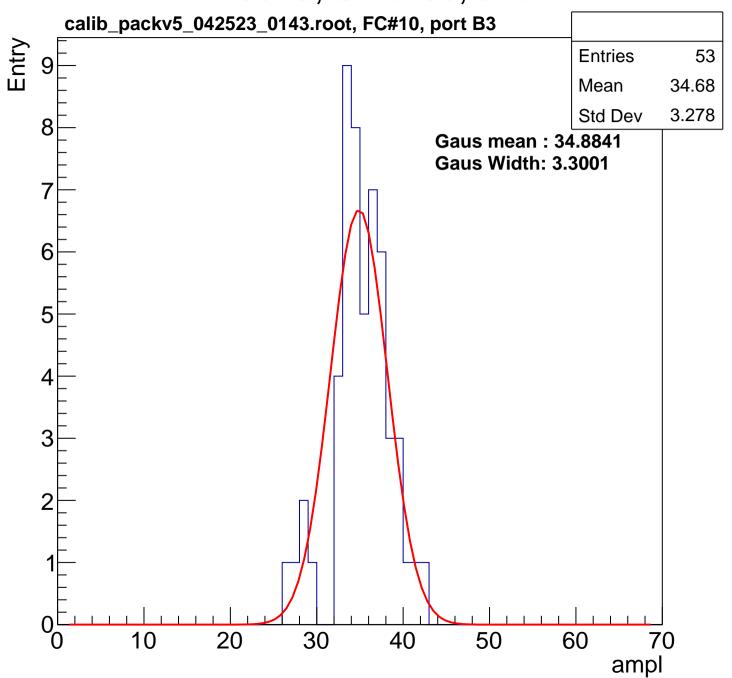


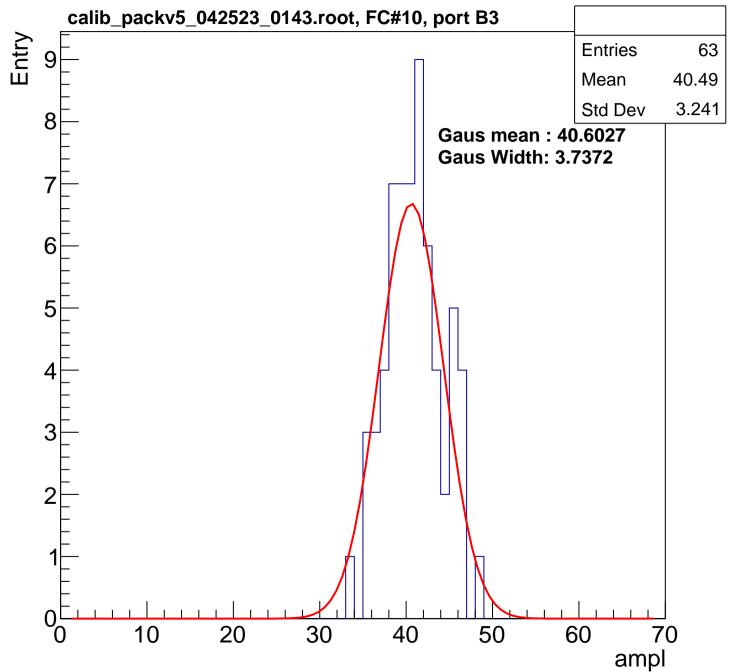


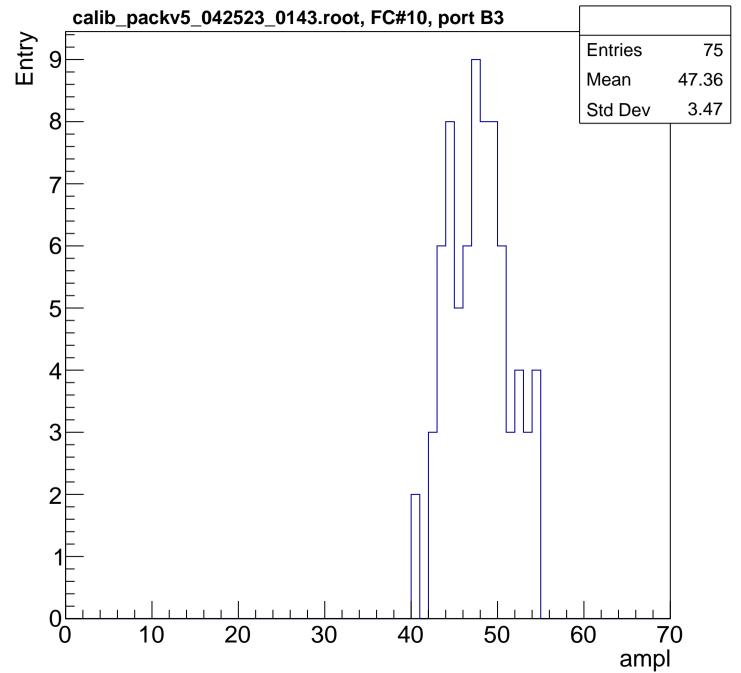


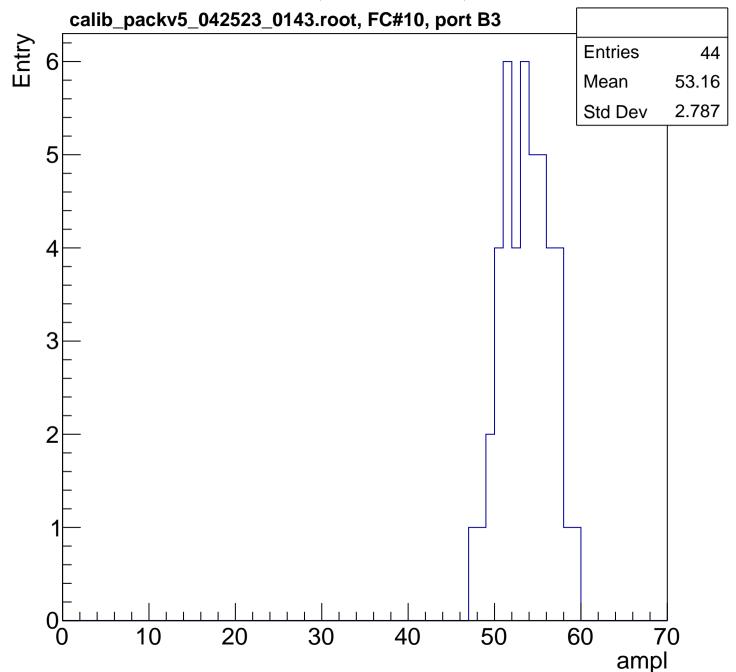


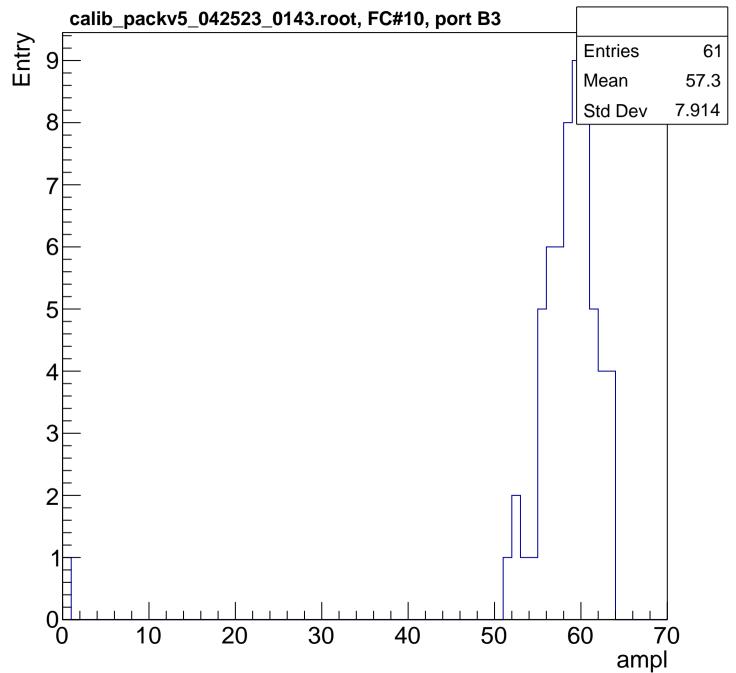


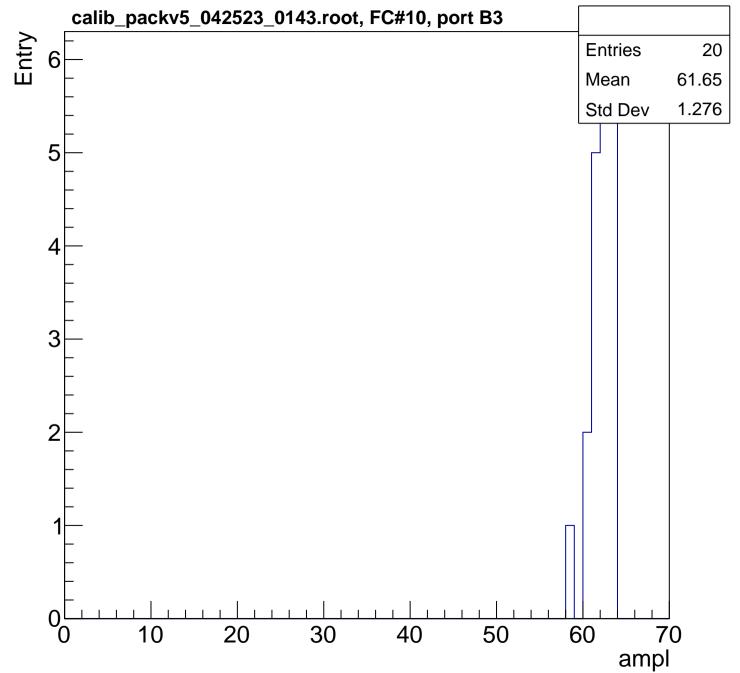




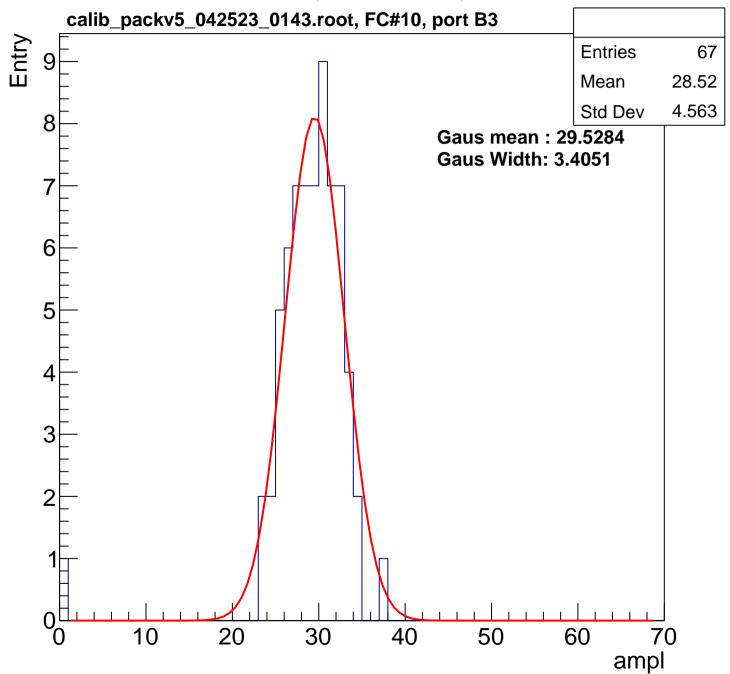


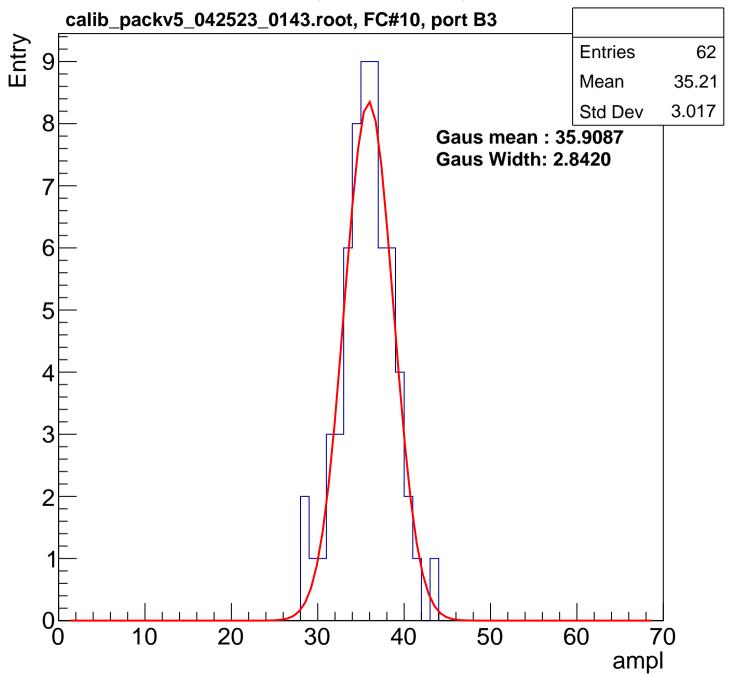


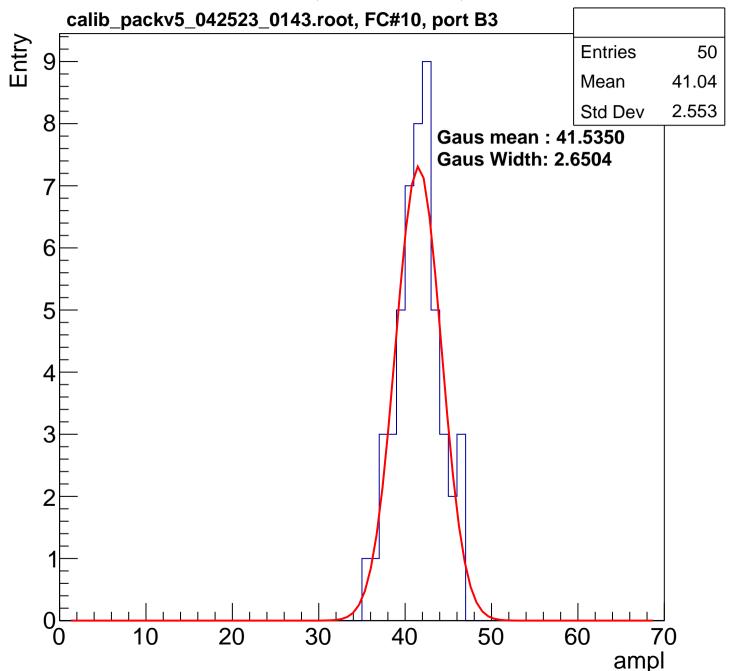


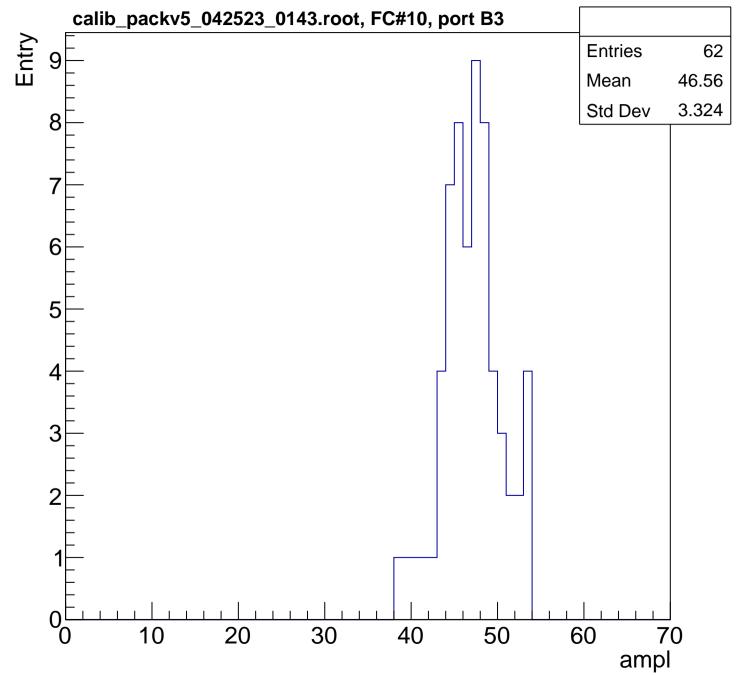


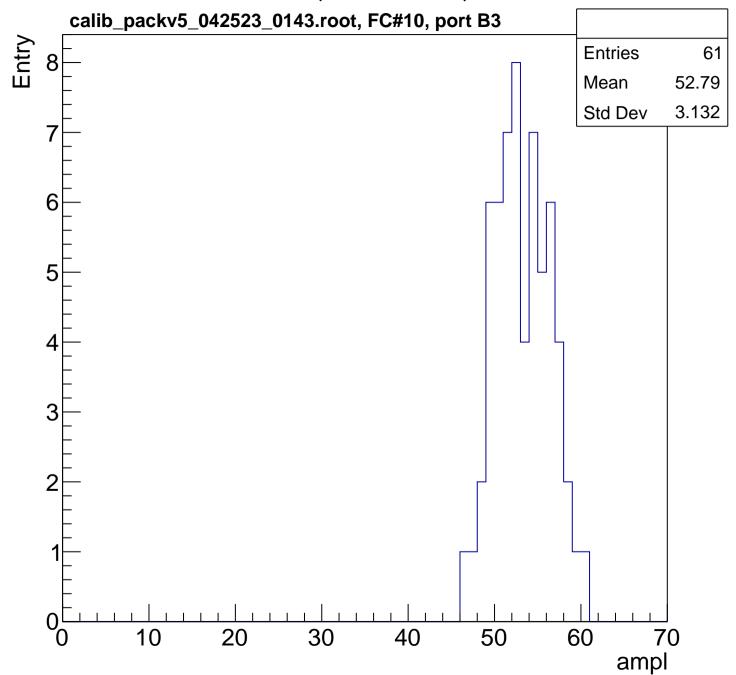


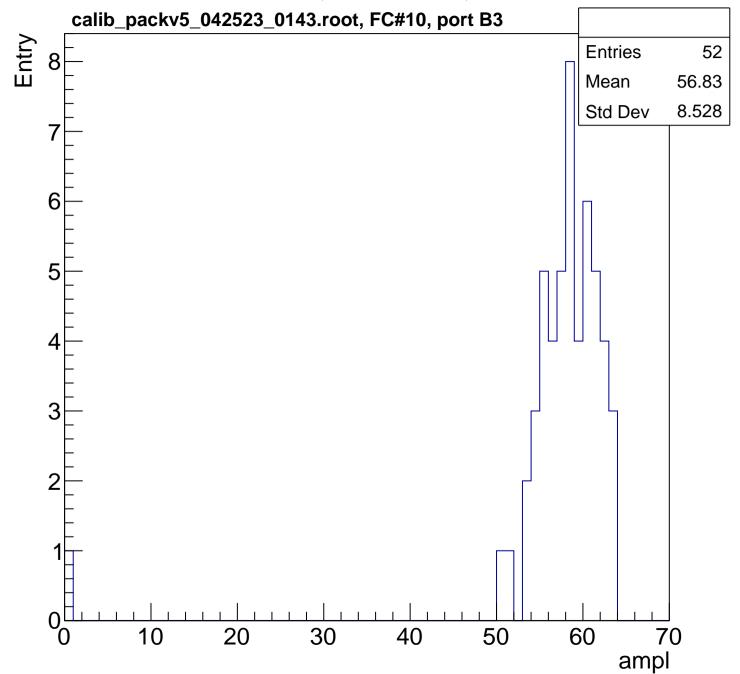


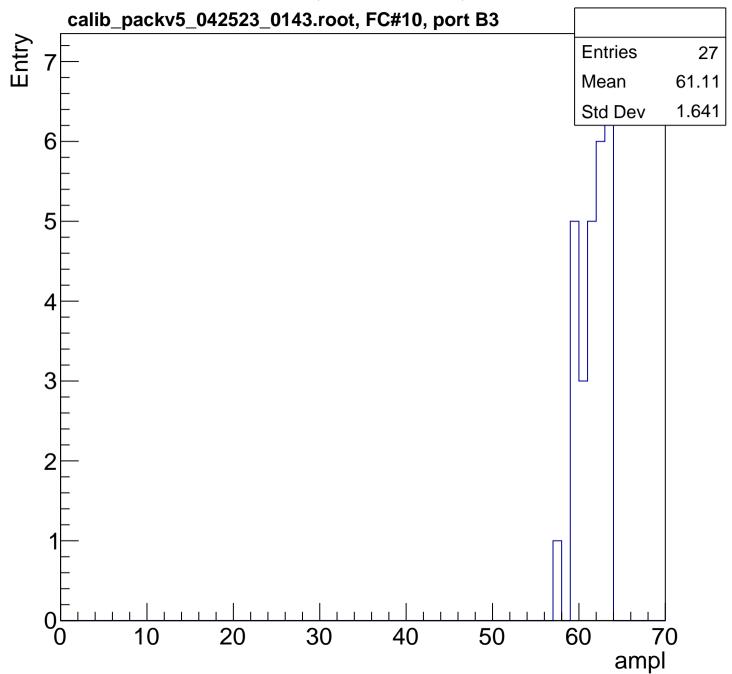


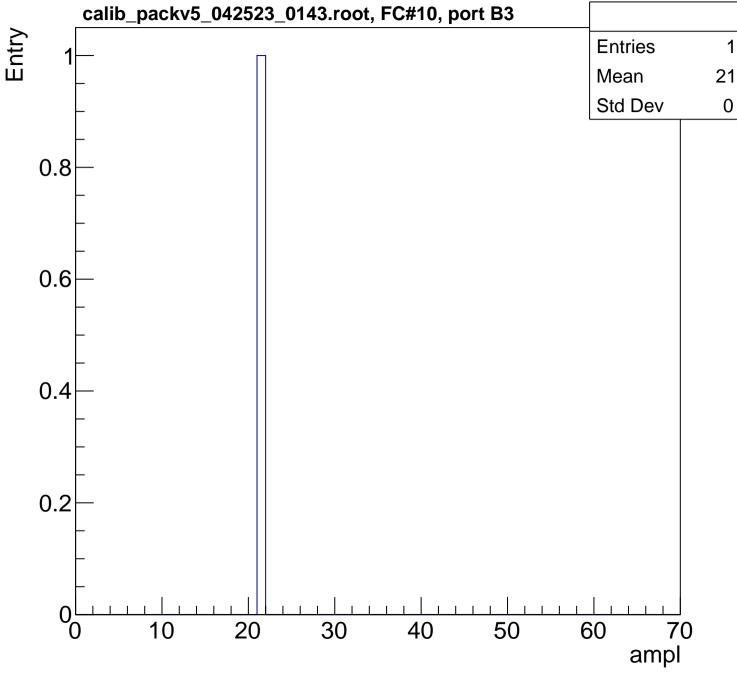


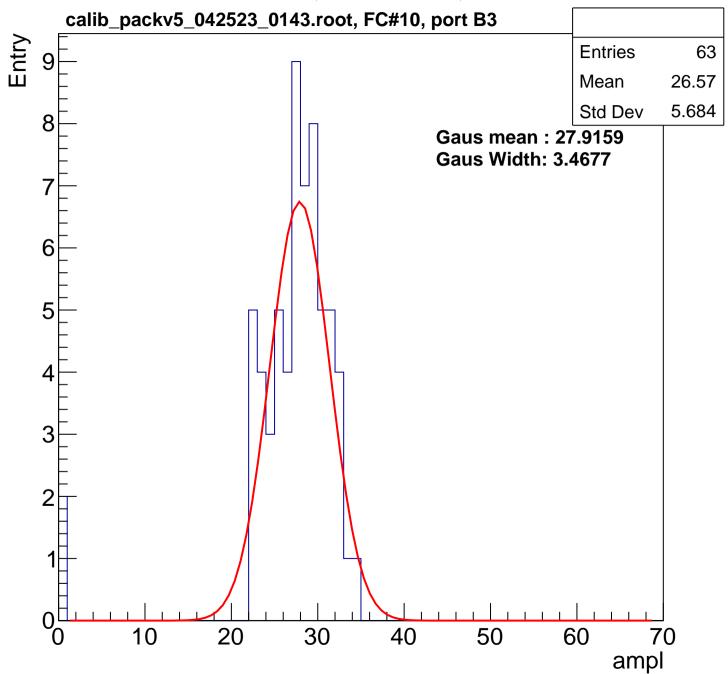


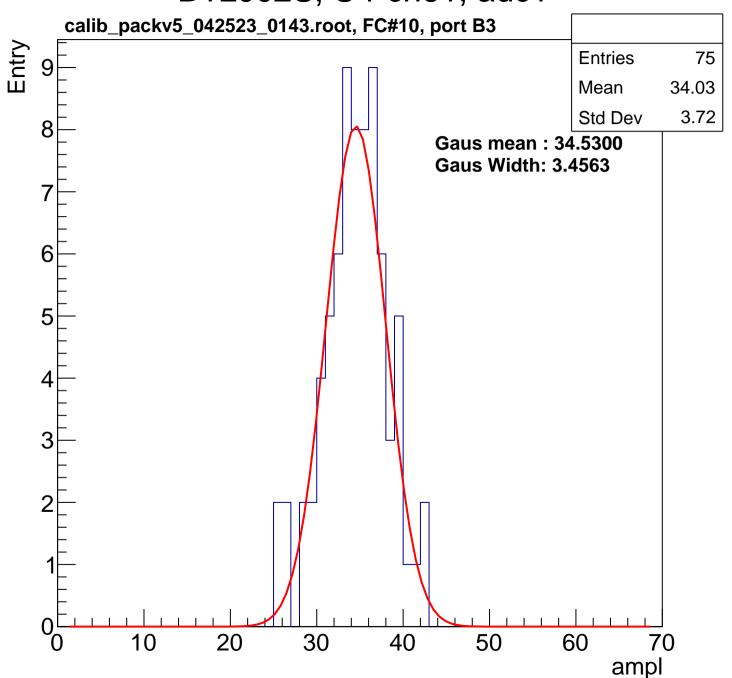


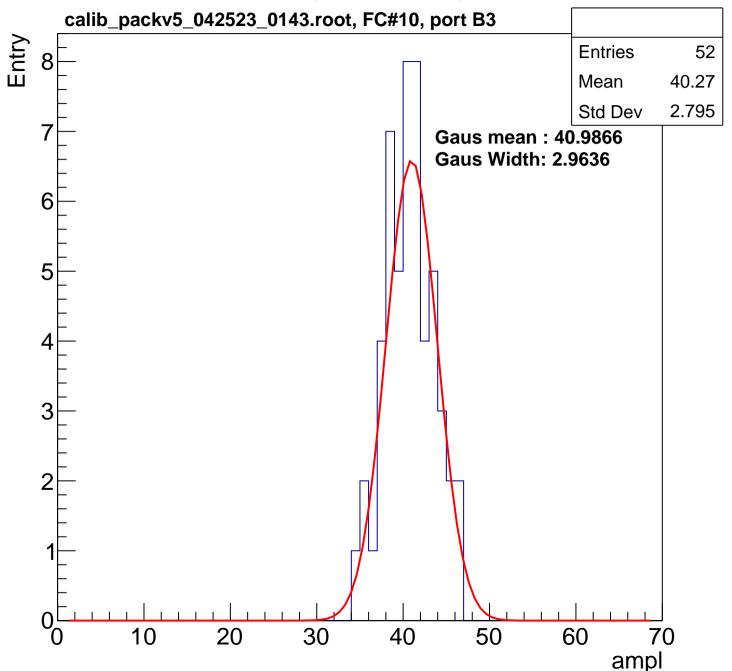


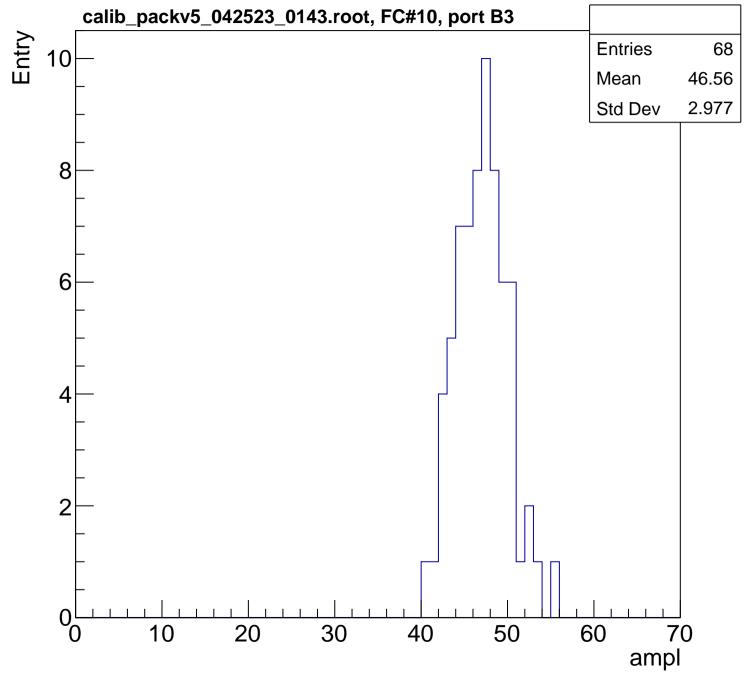


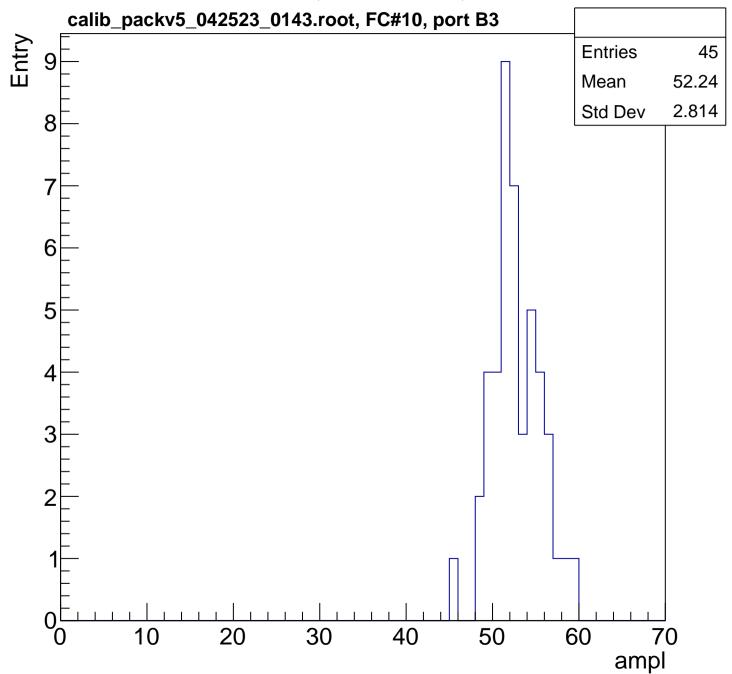


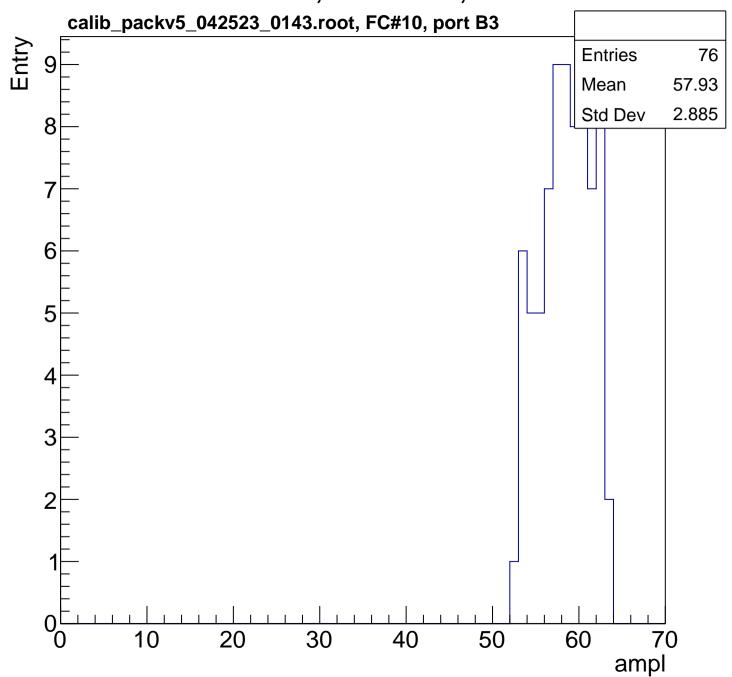


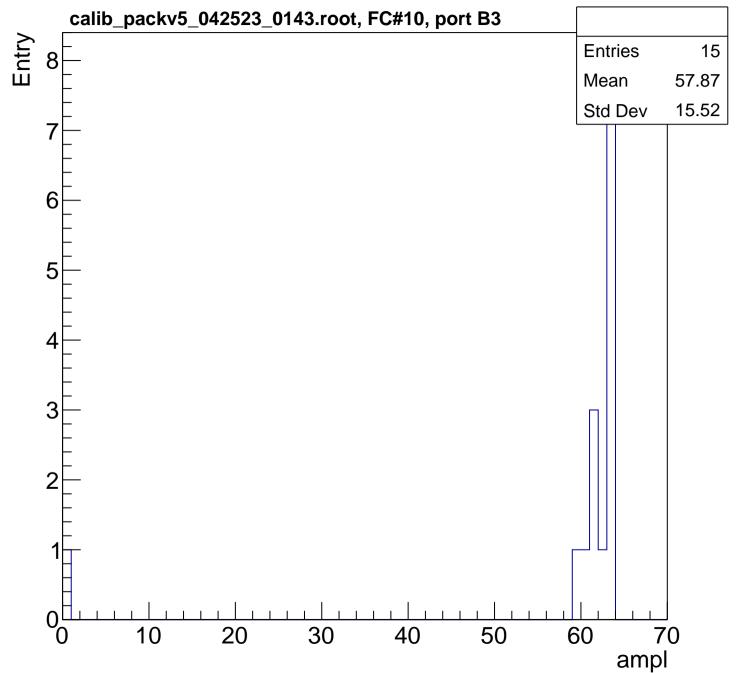


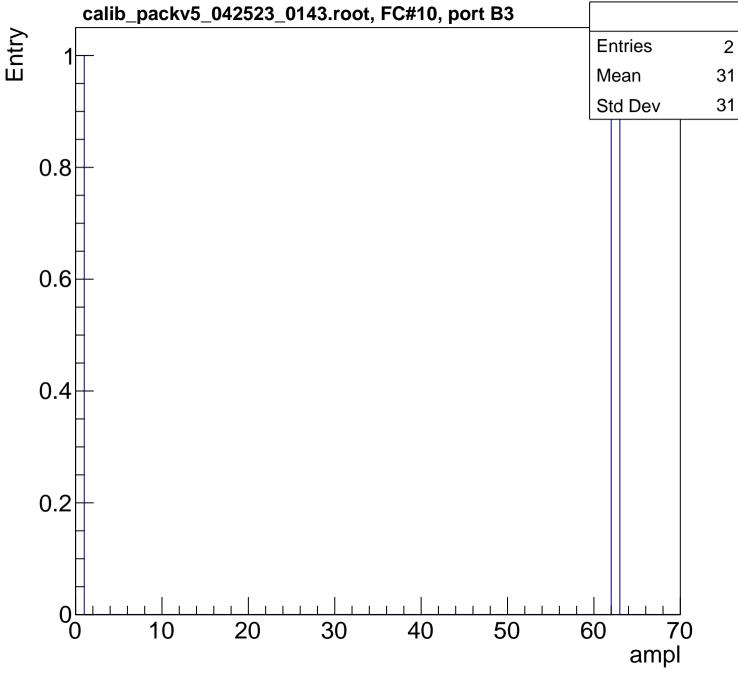


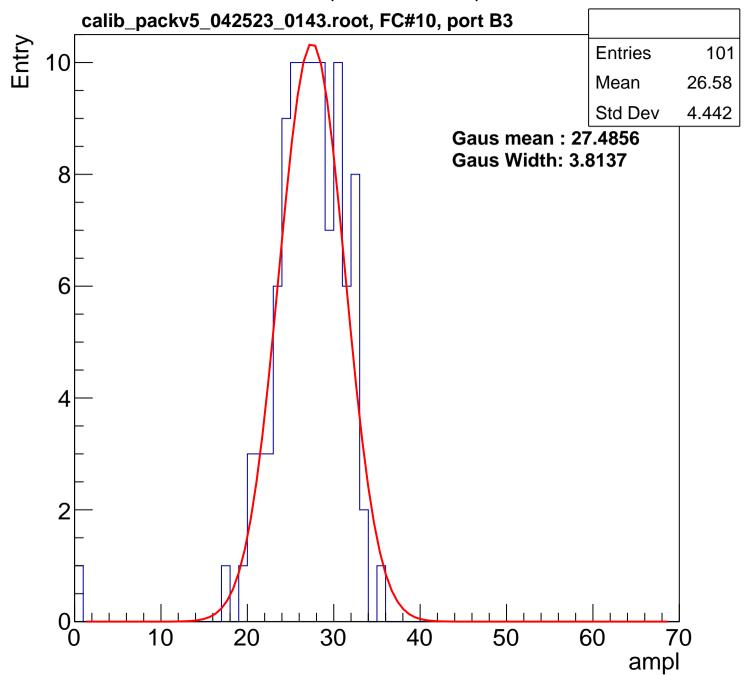


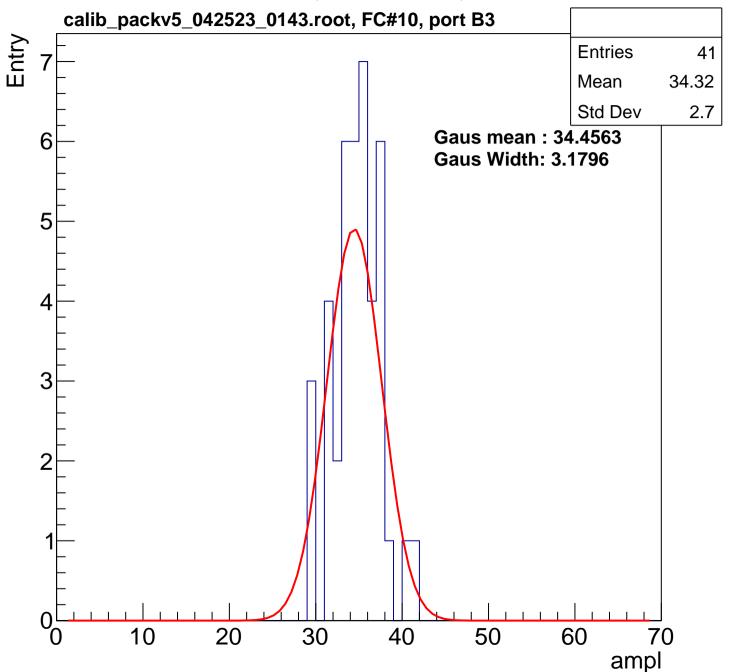


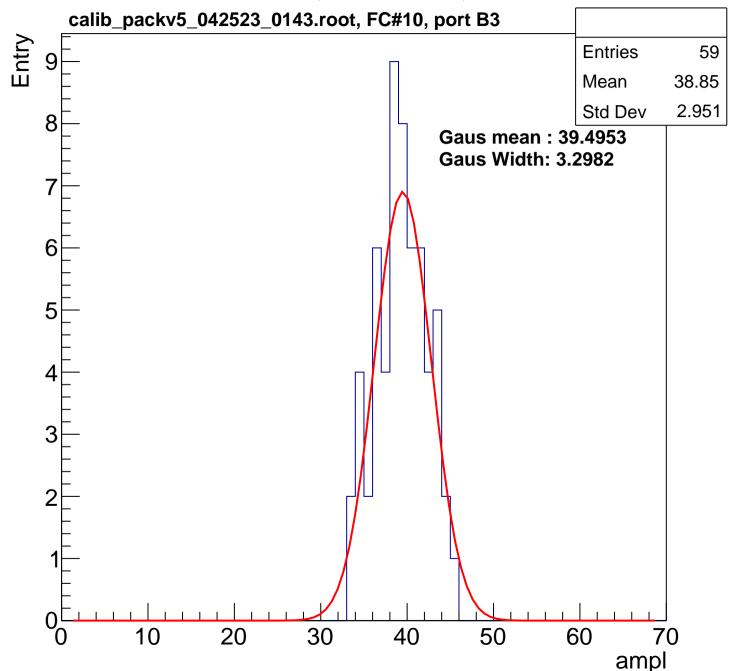


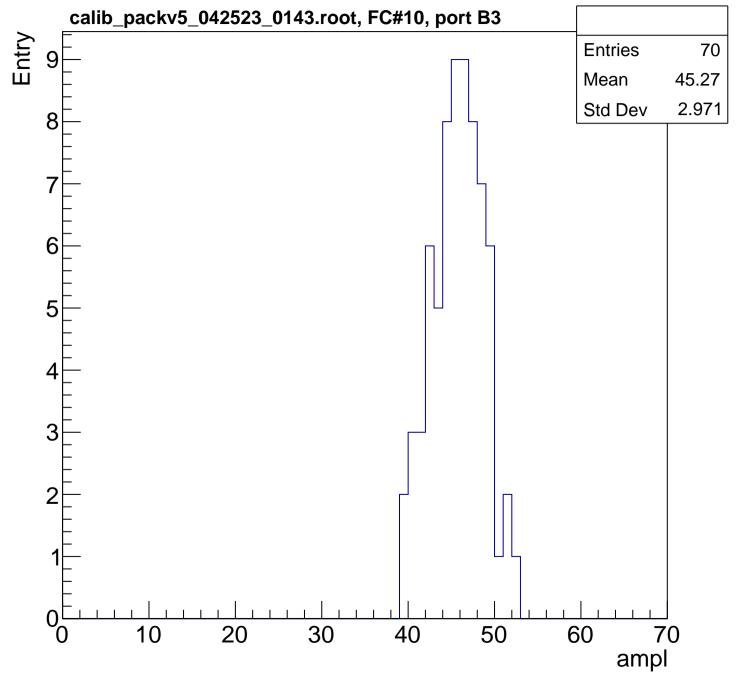


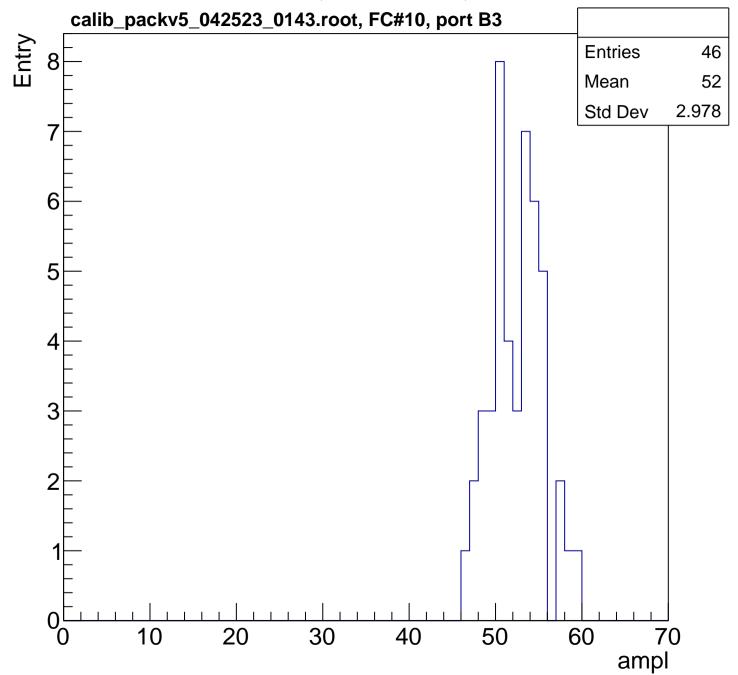


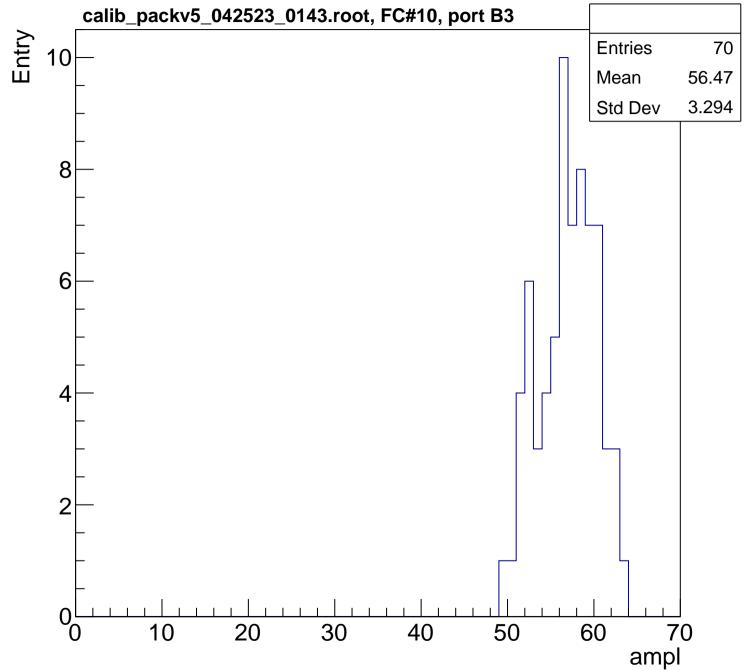


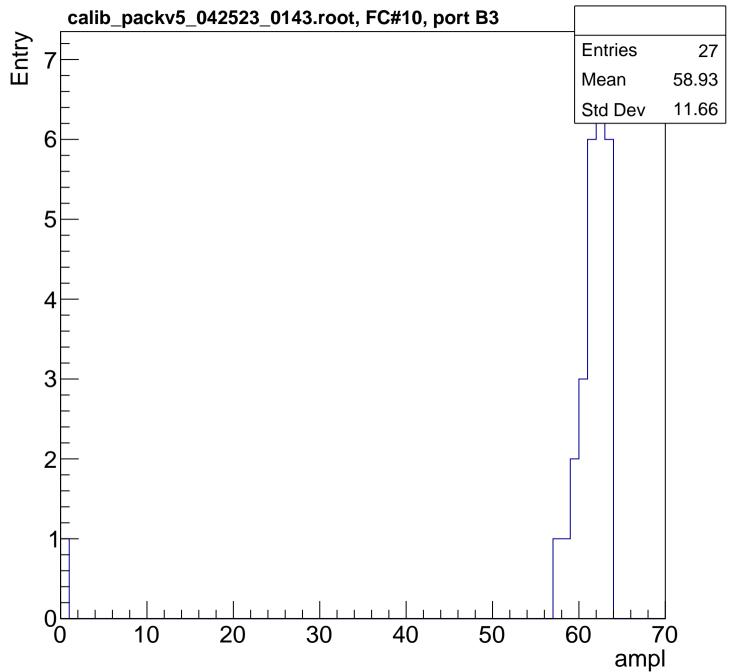


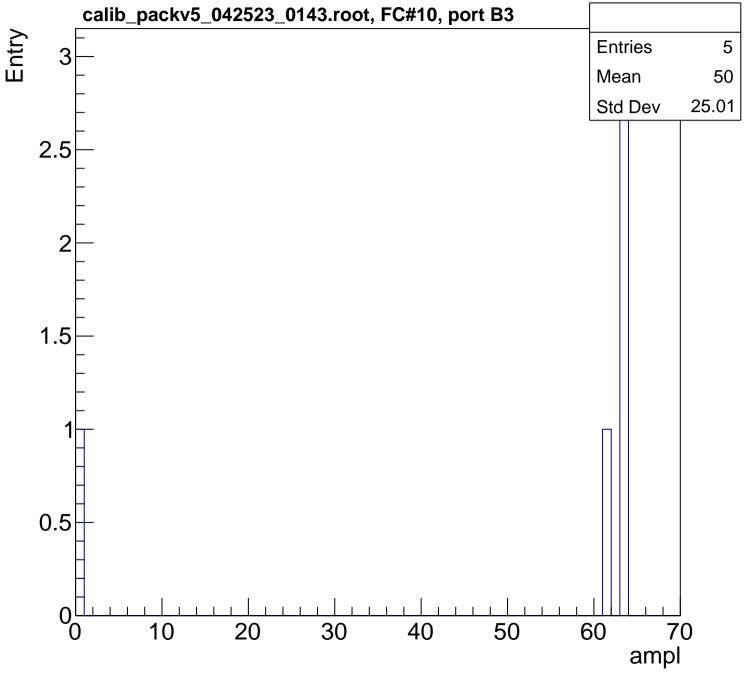


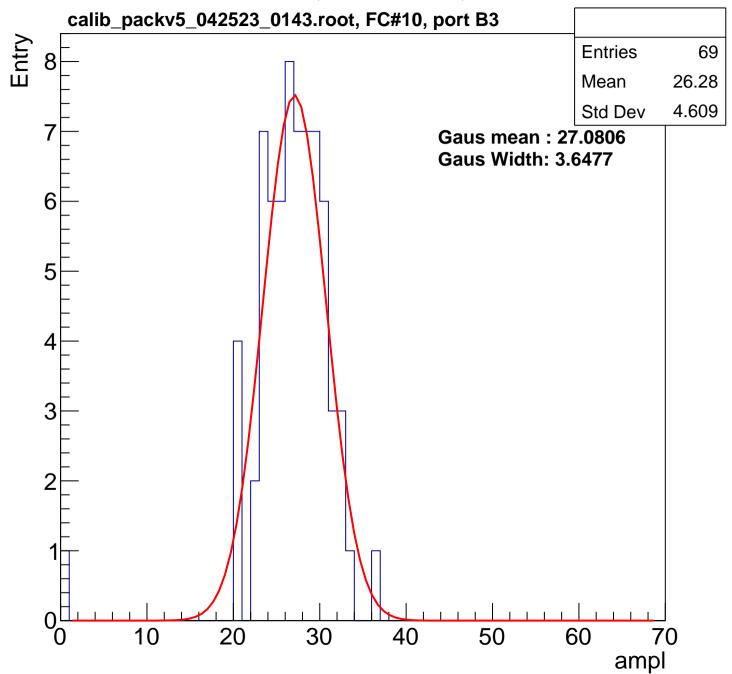


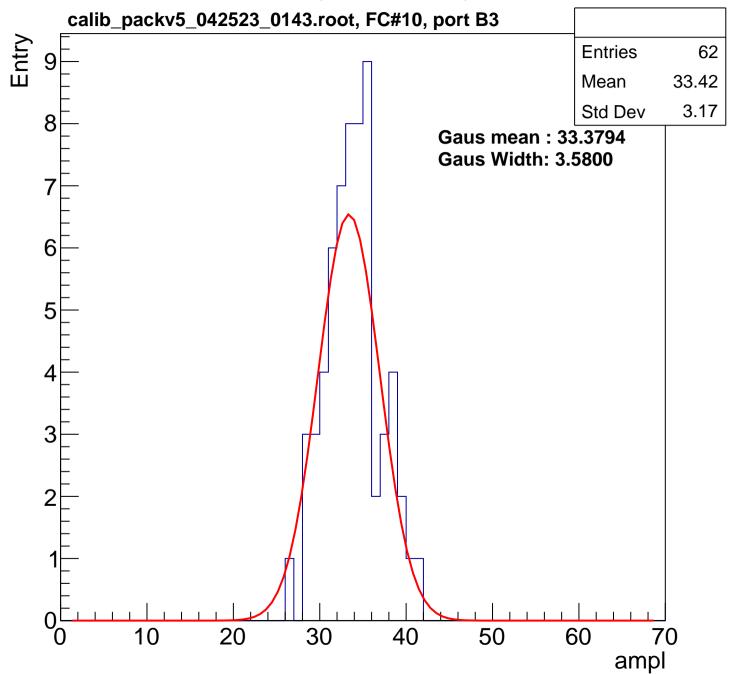


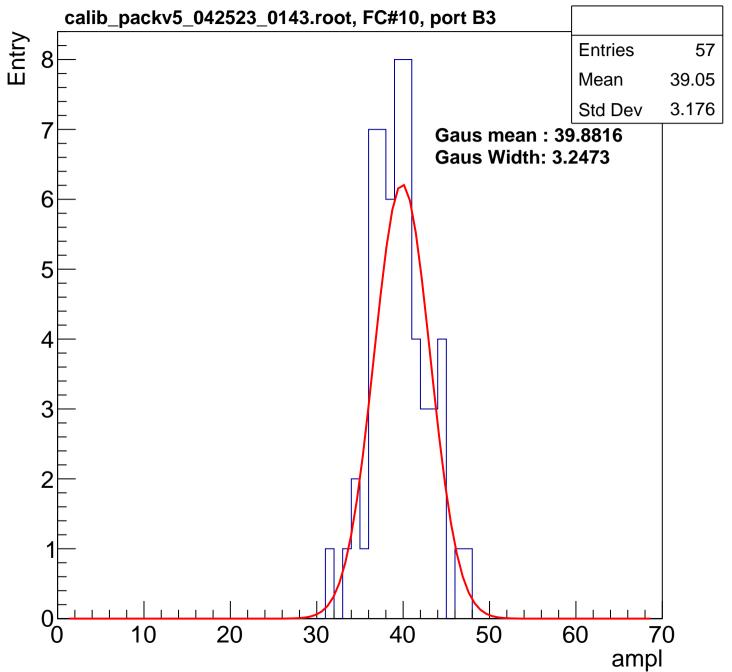


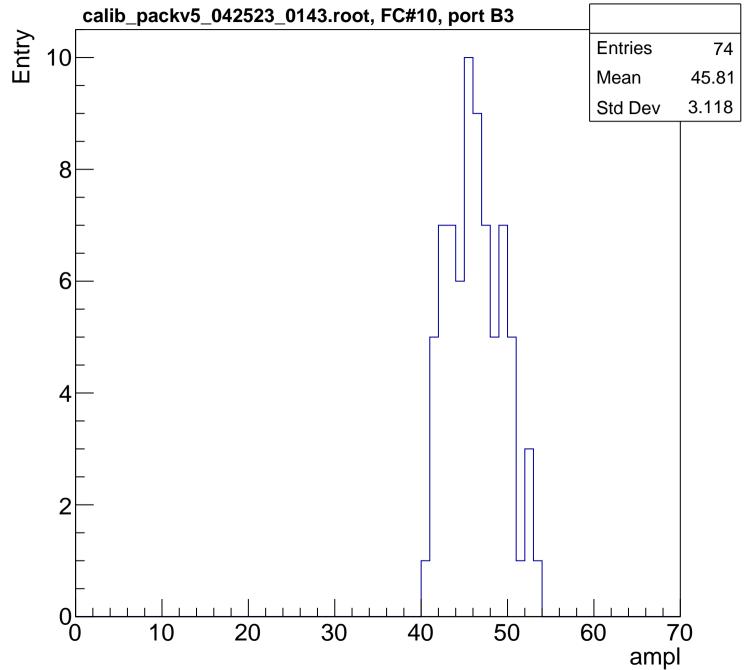


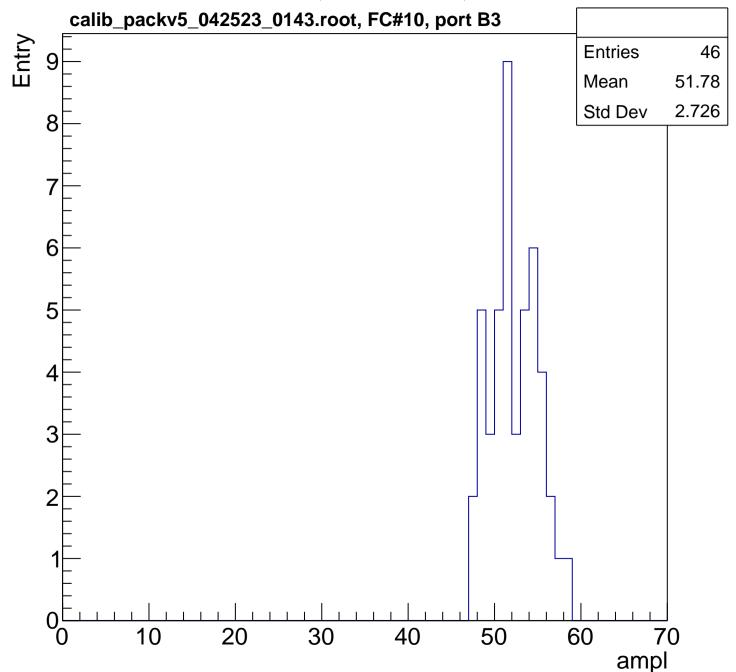


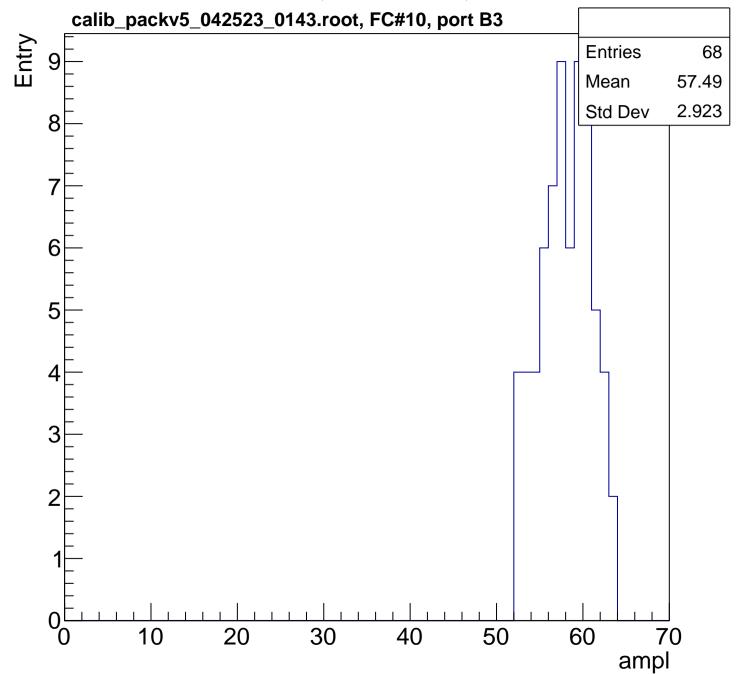


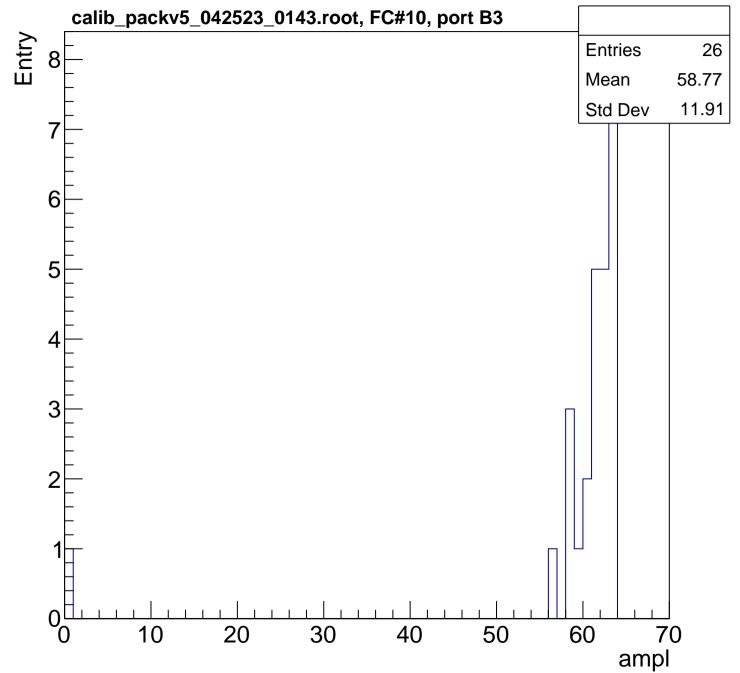




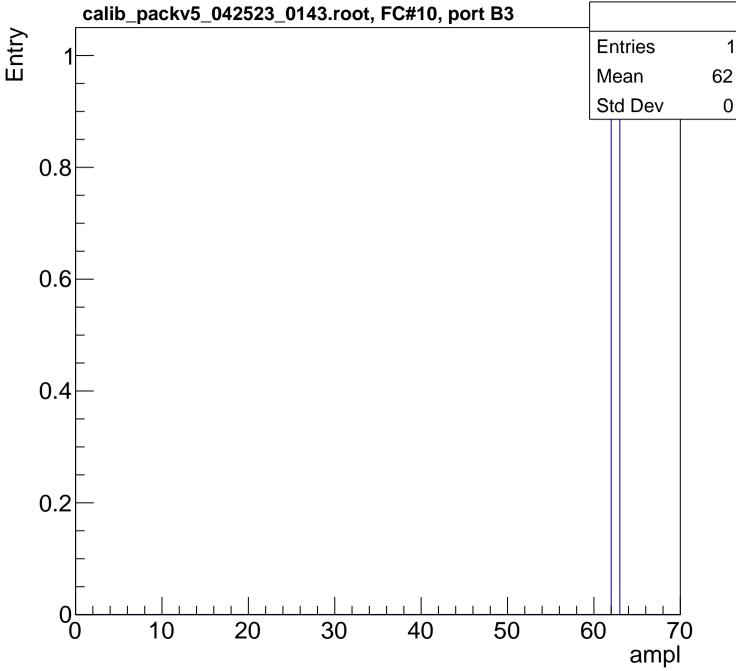


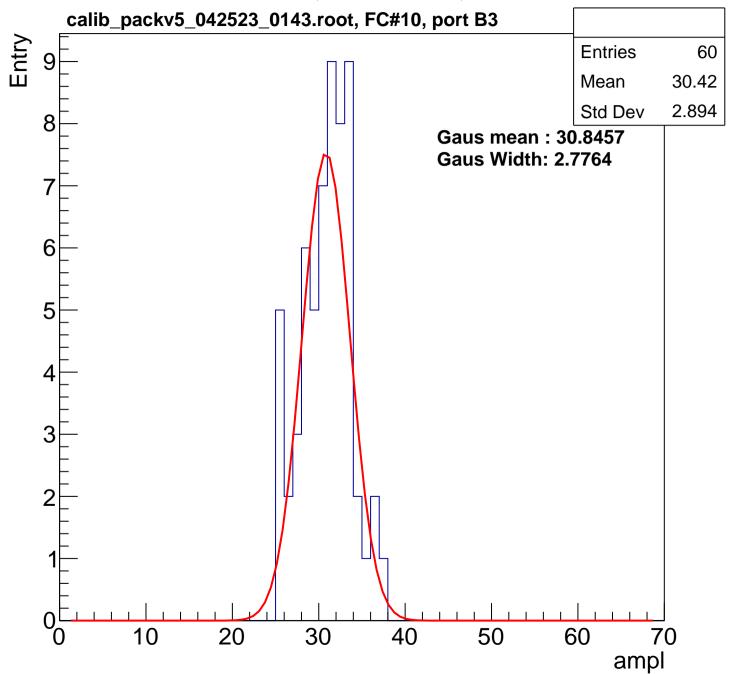


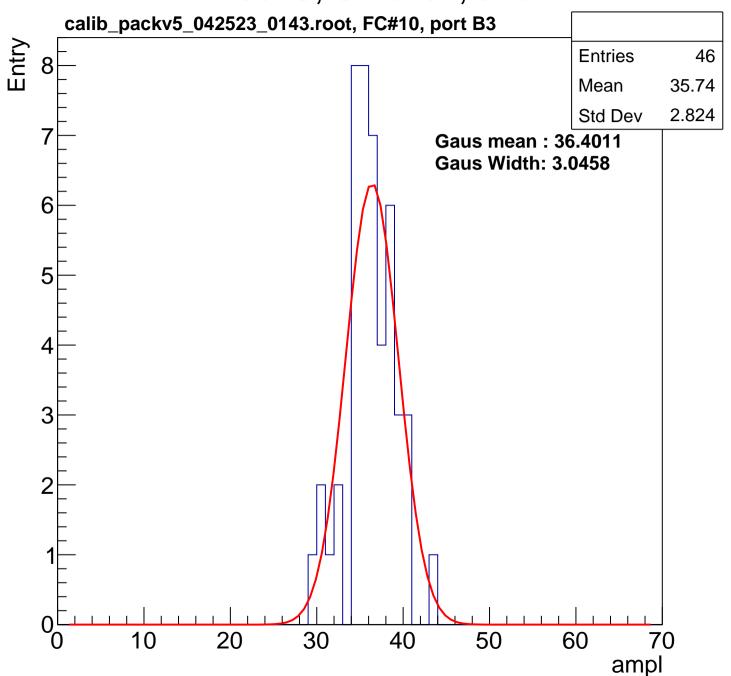


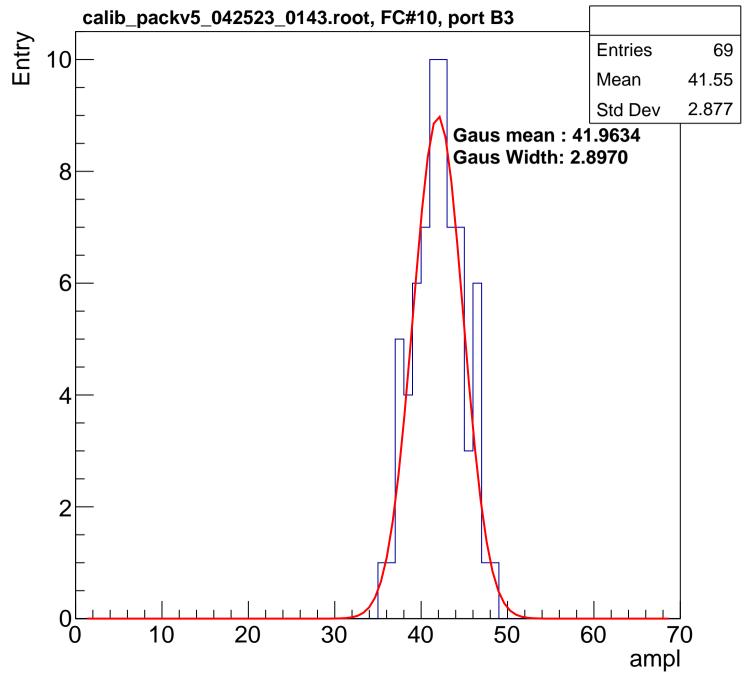


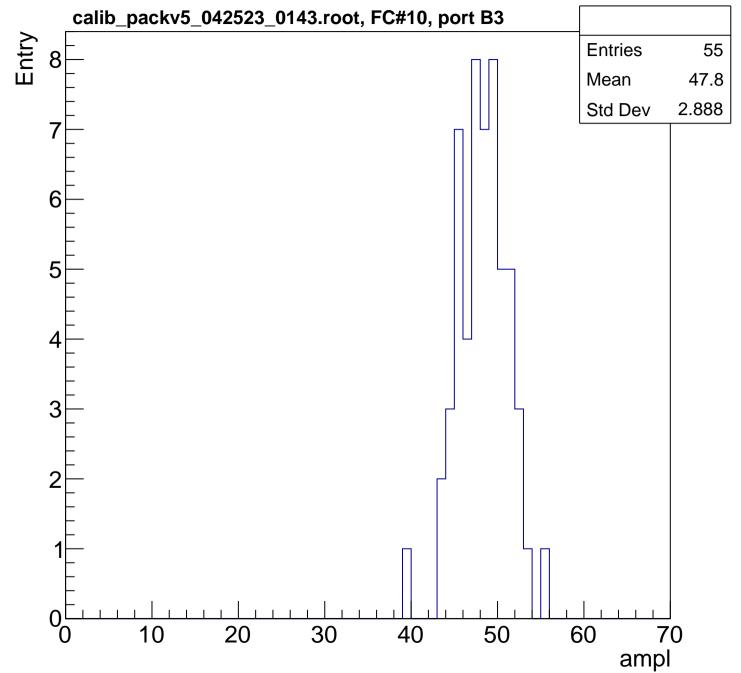
0

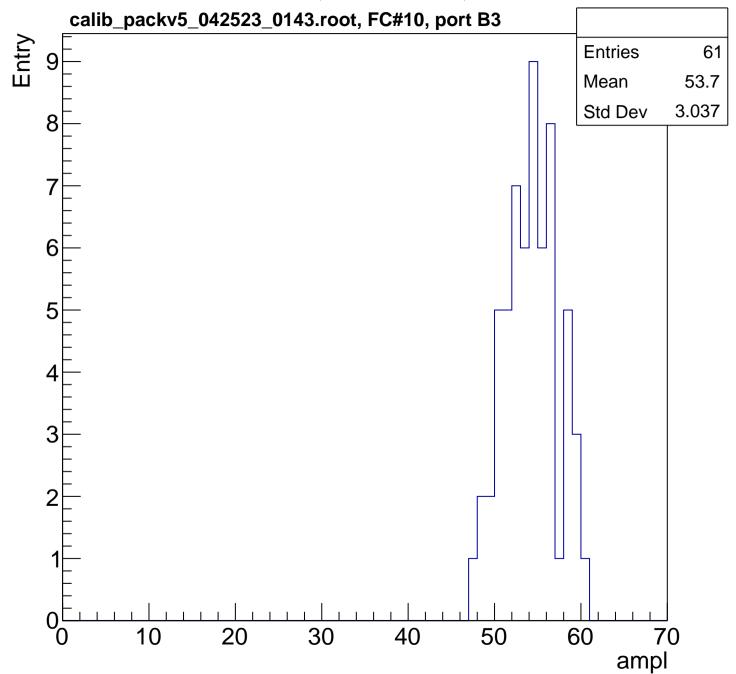


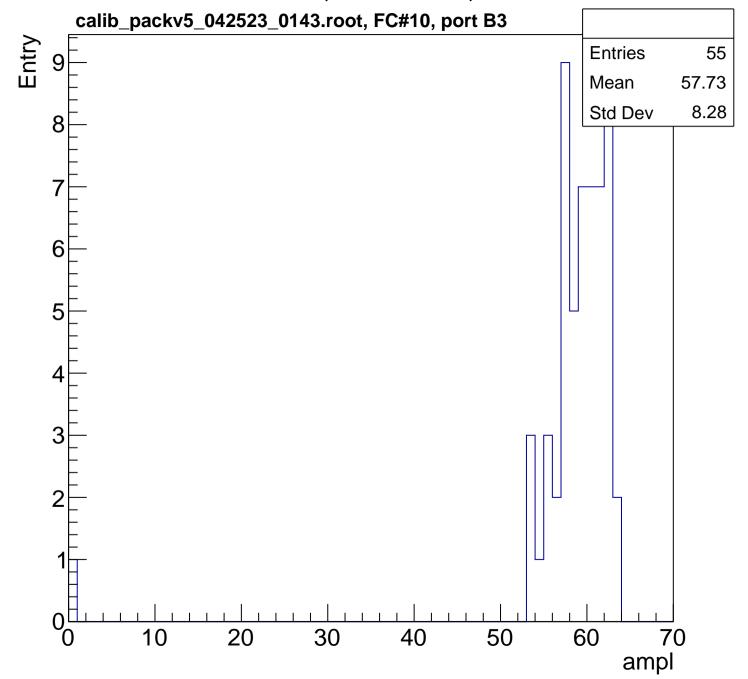


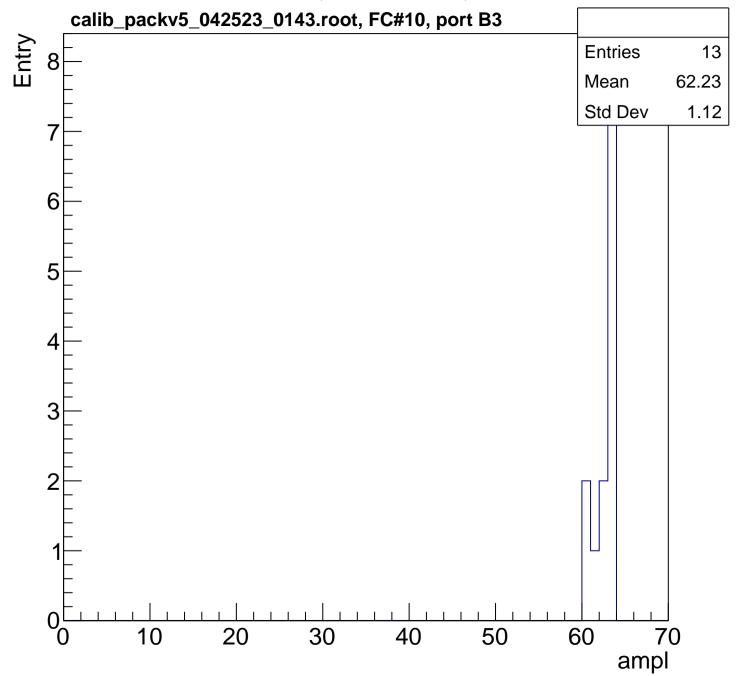


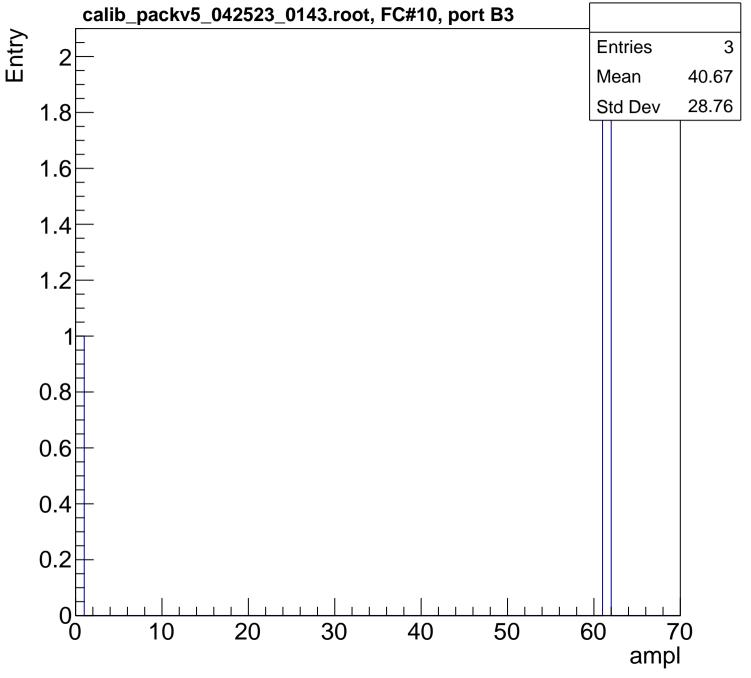


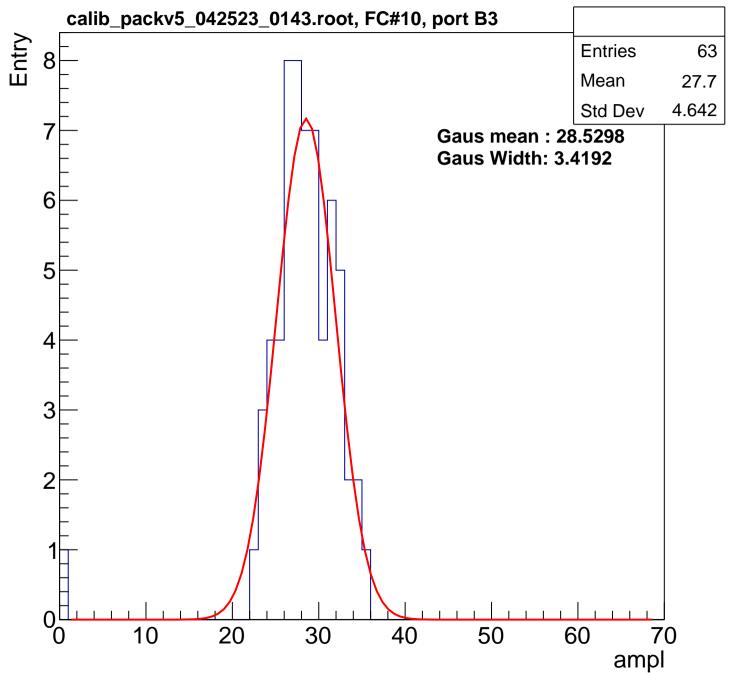


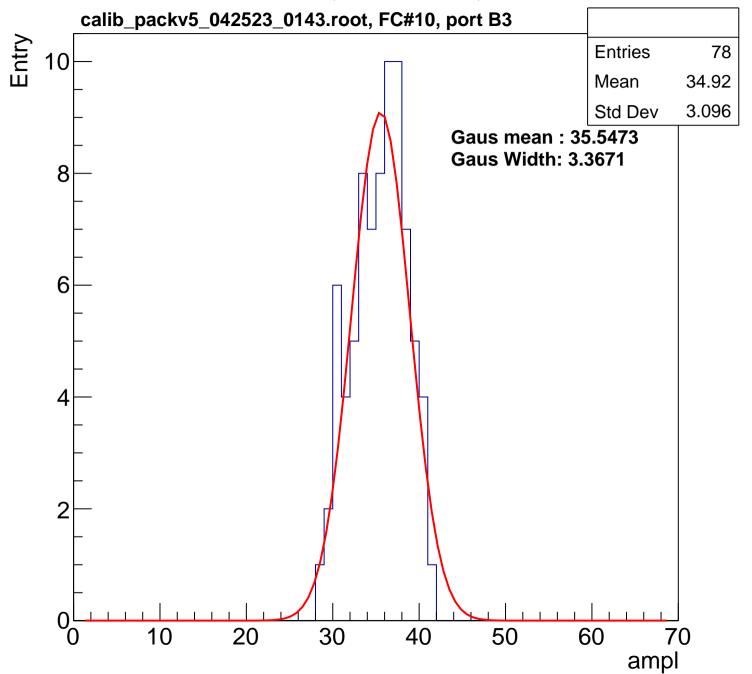


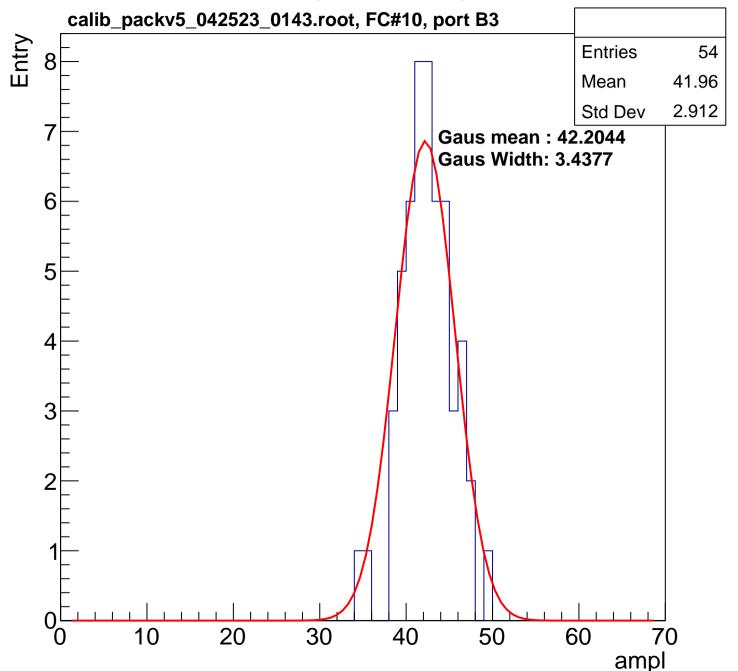


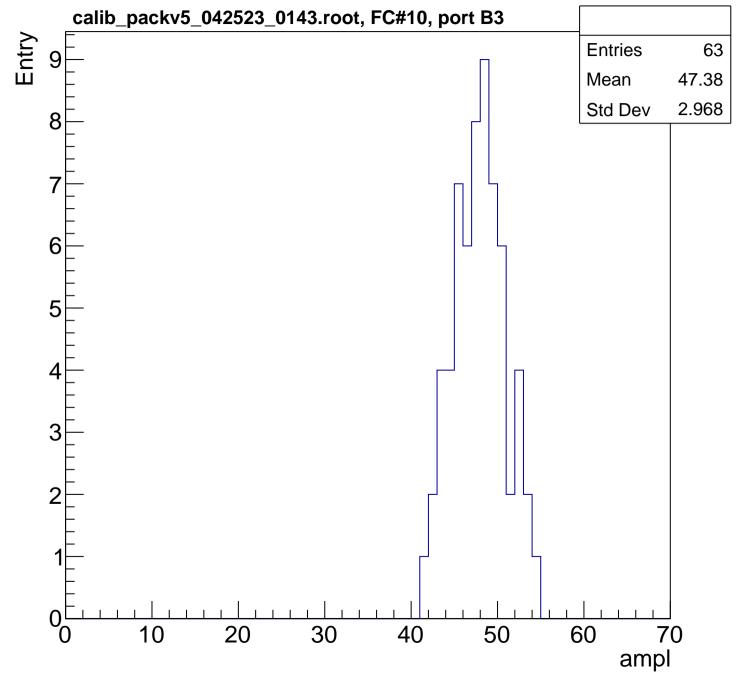


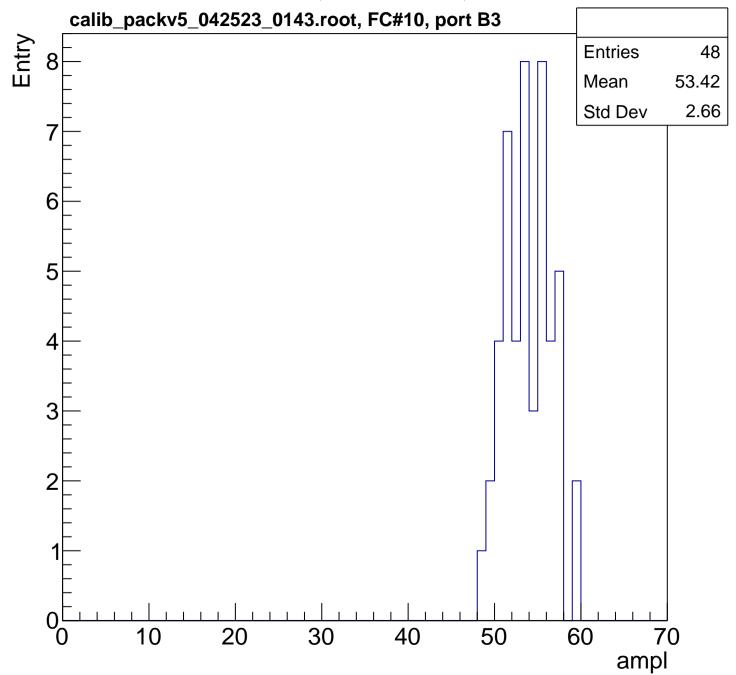


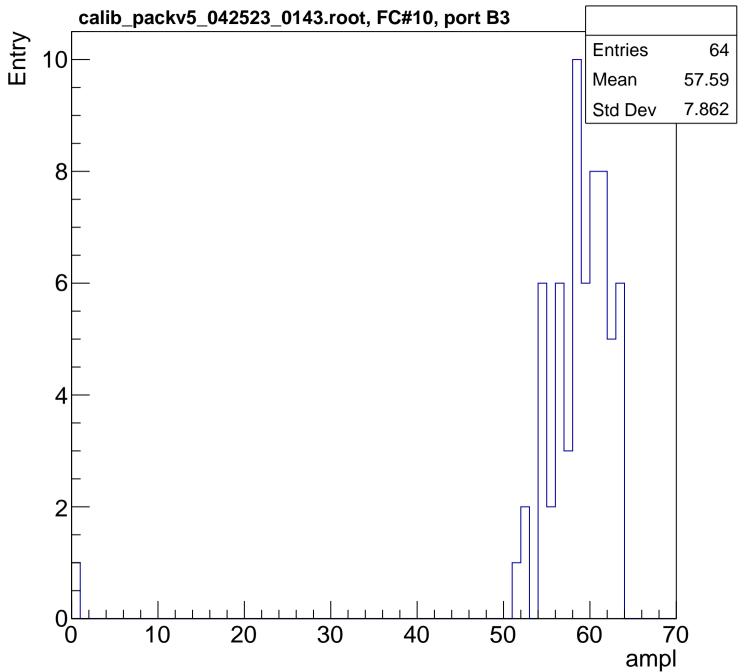


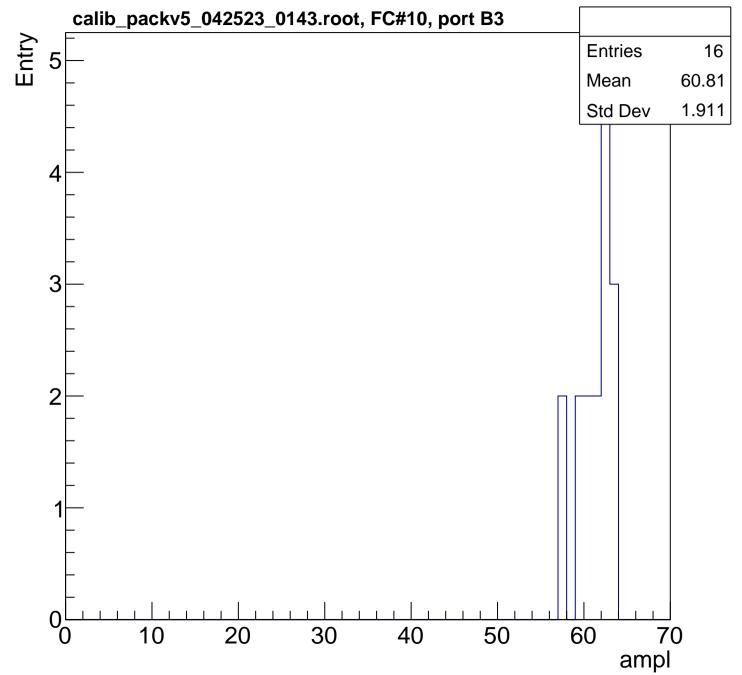




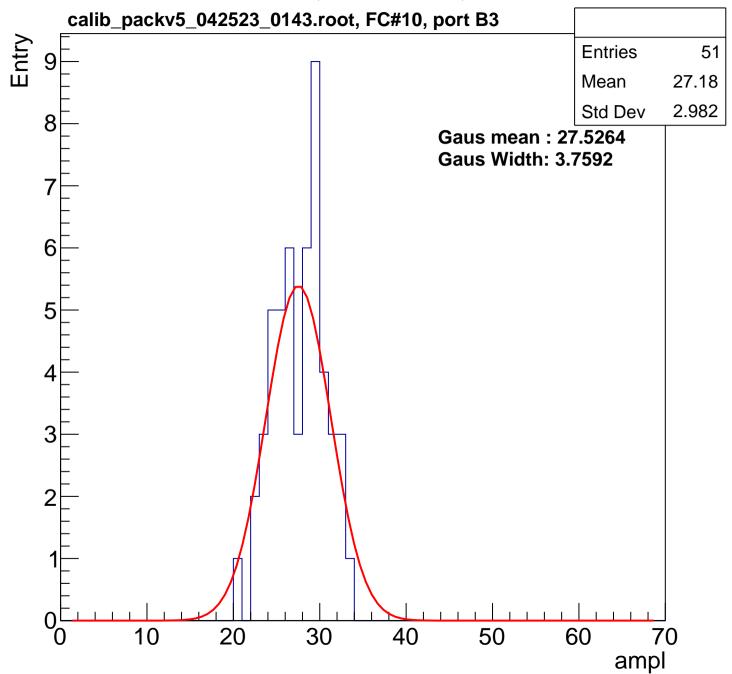


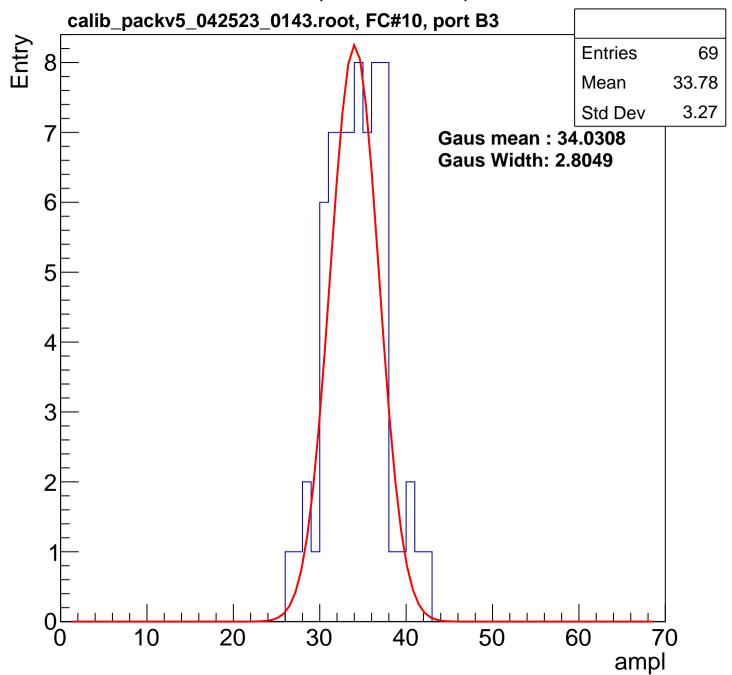


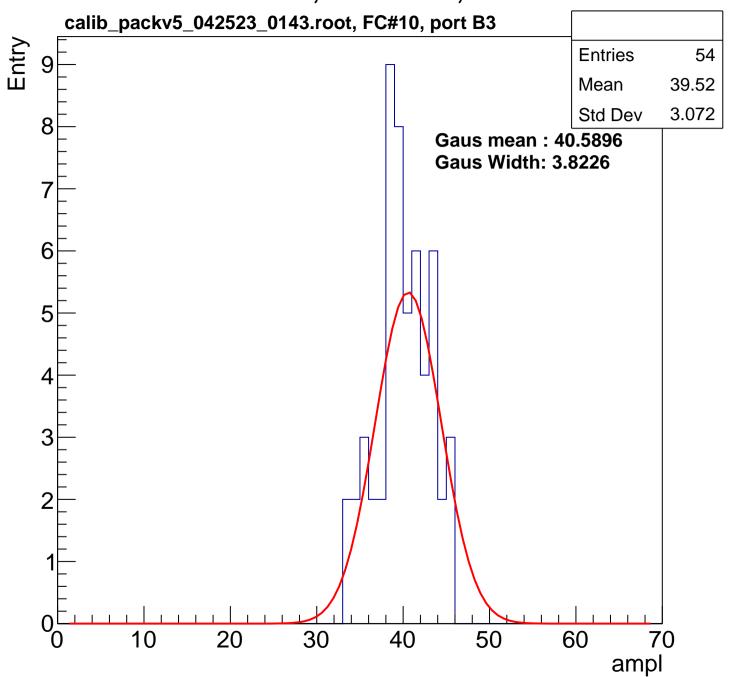


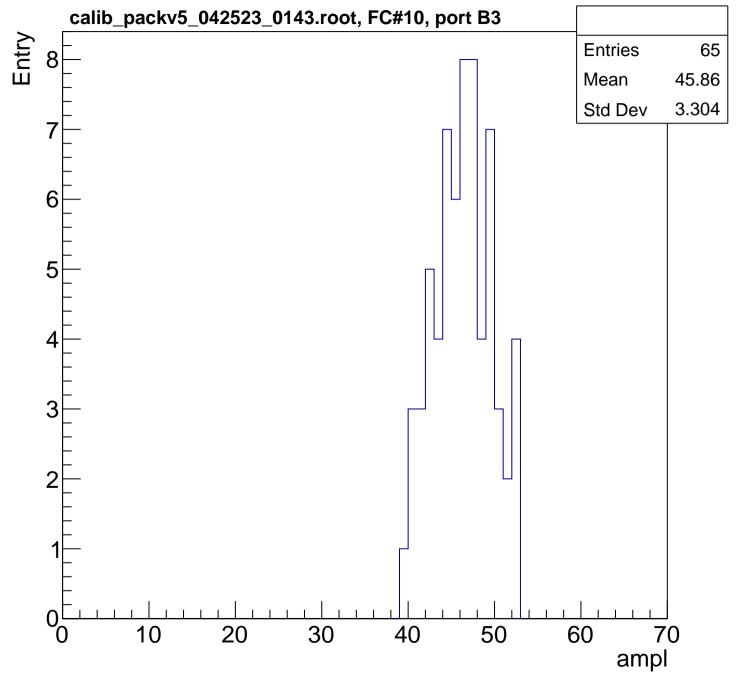


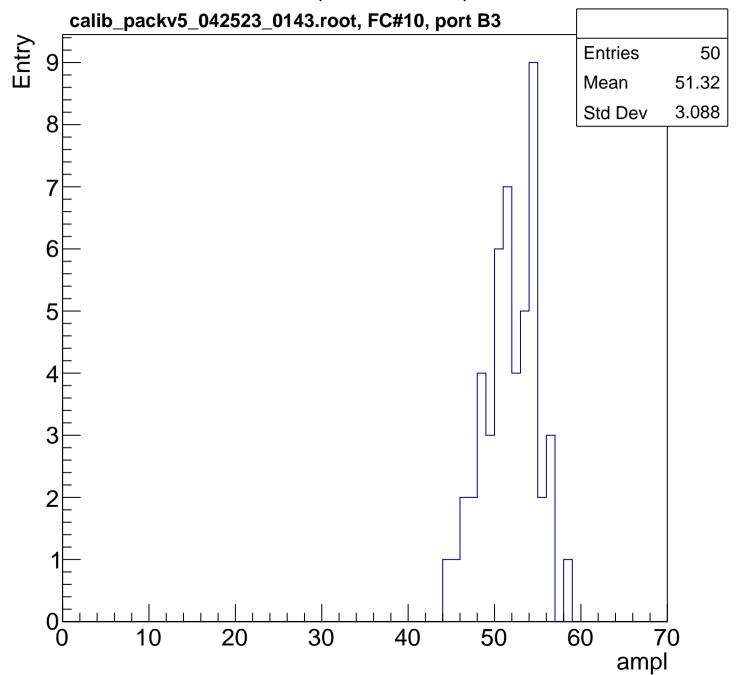


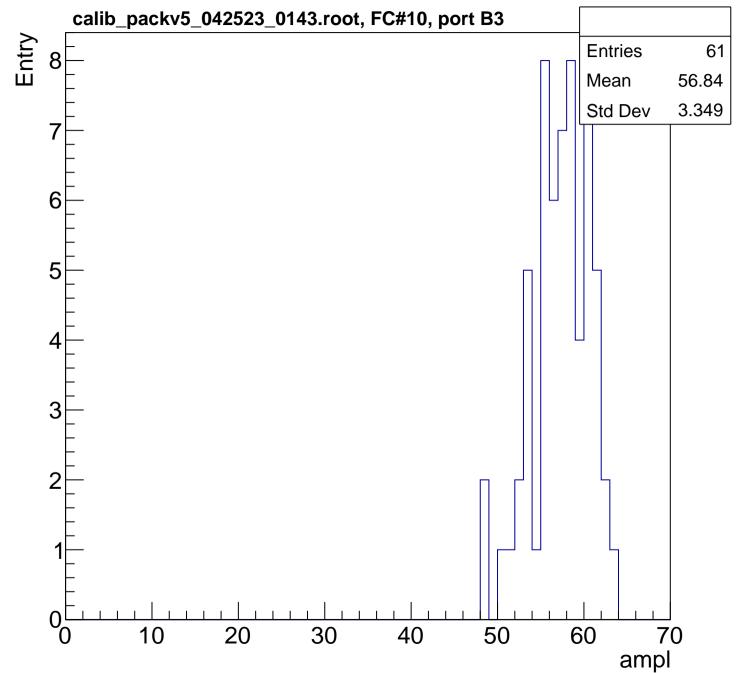


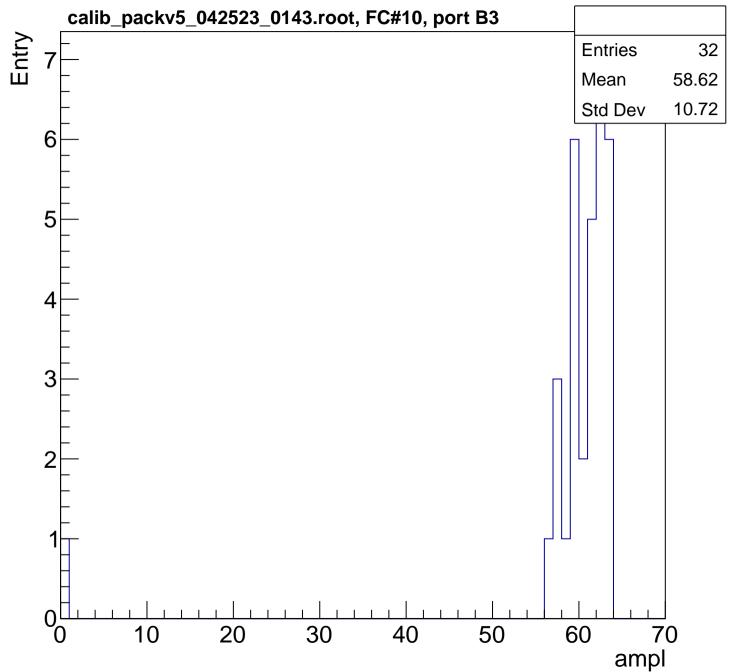


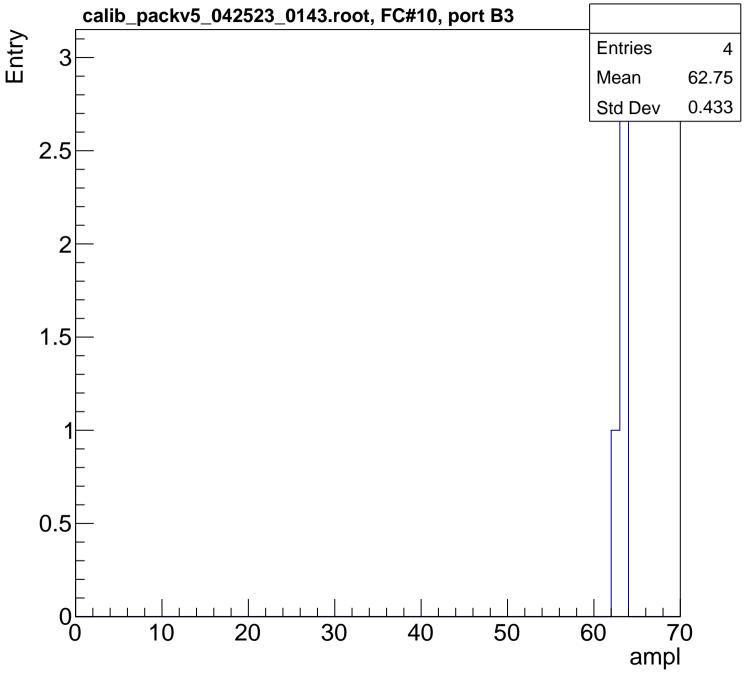


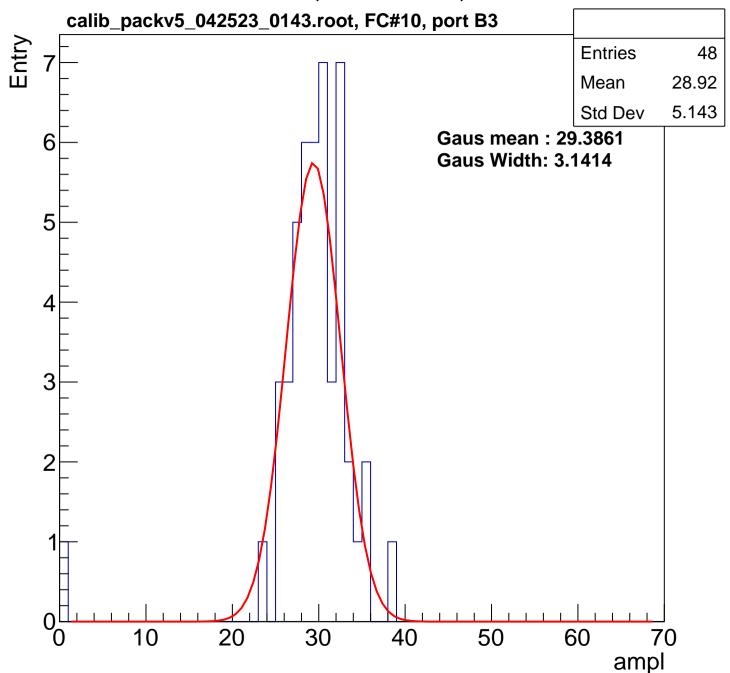


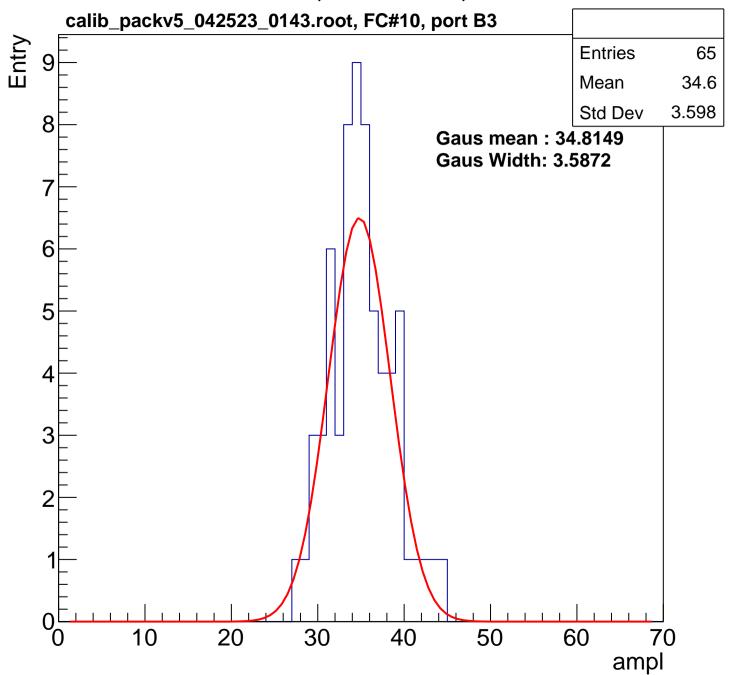


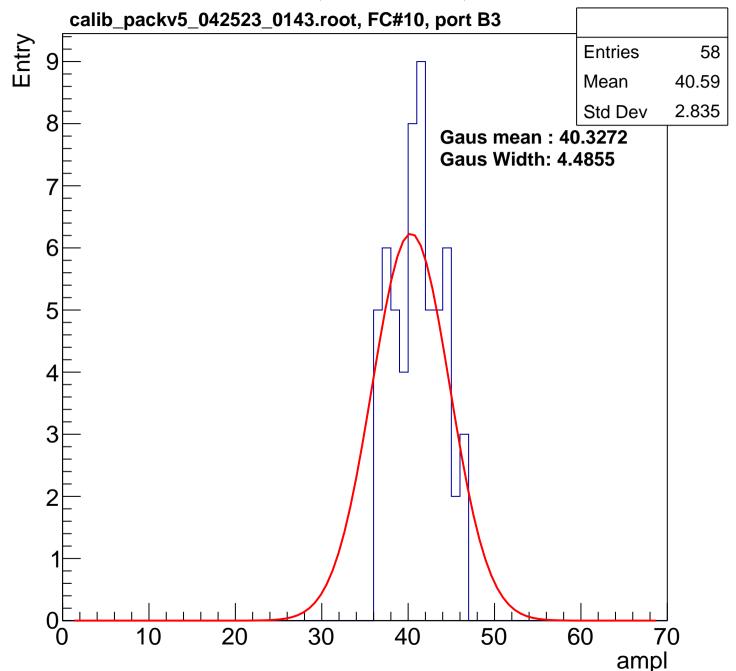


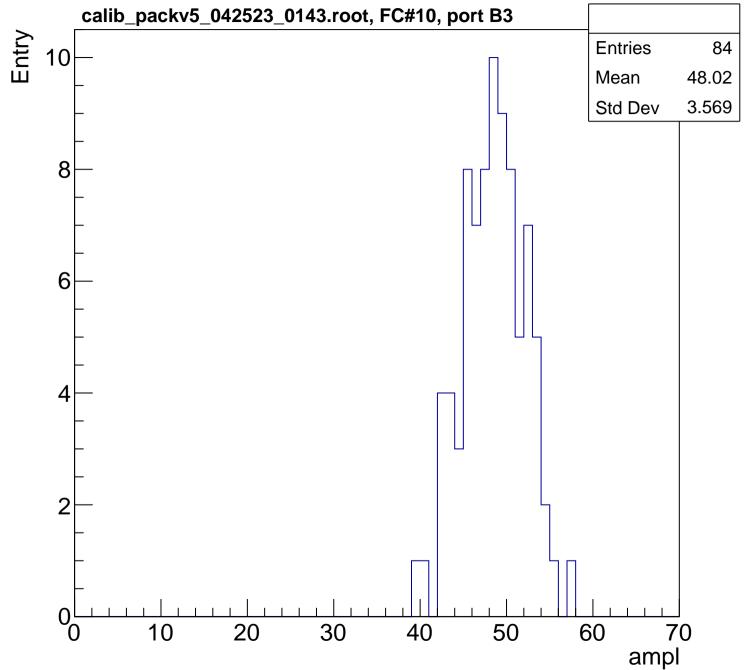


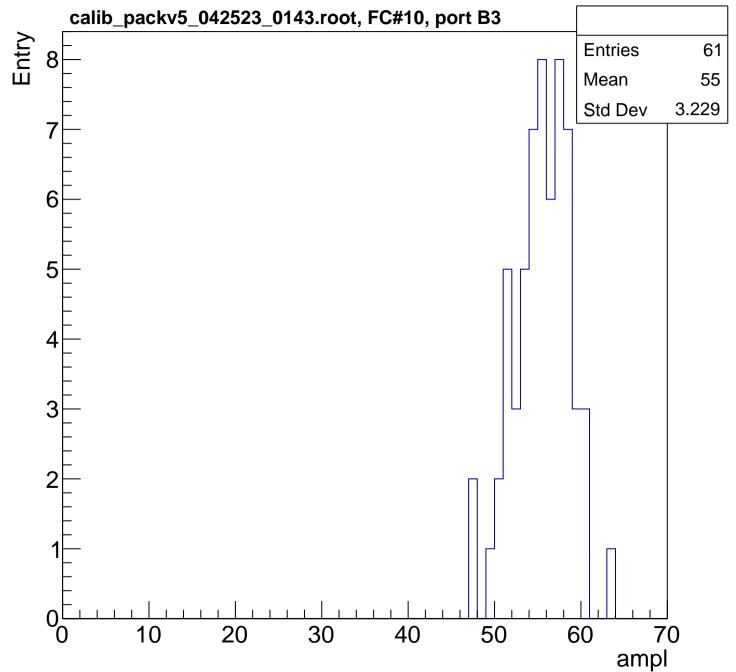


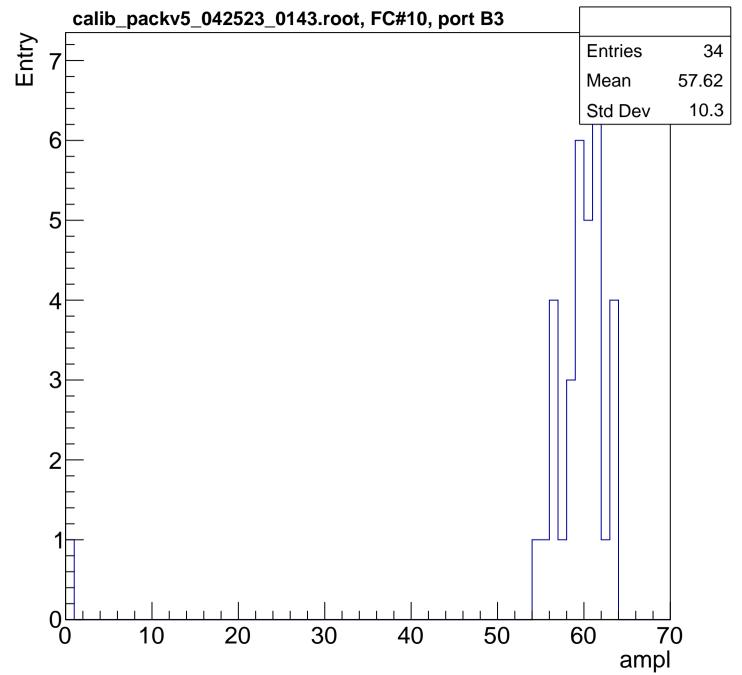


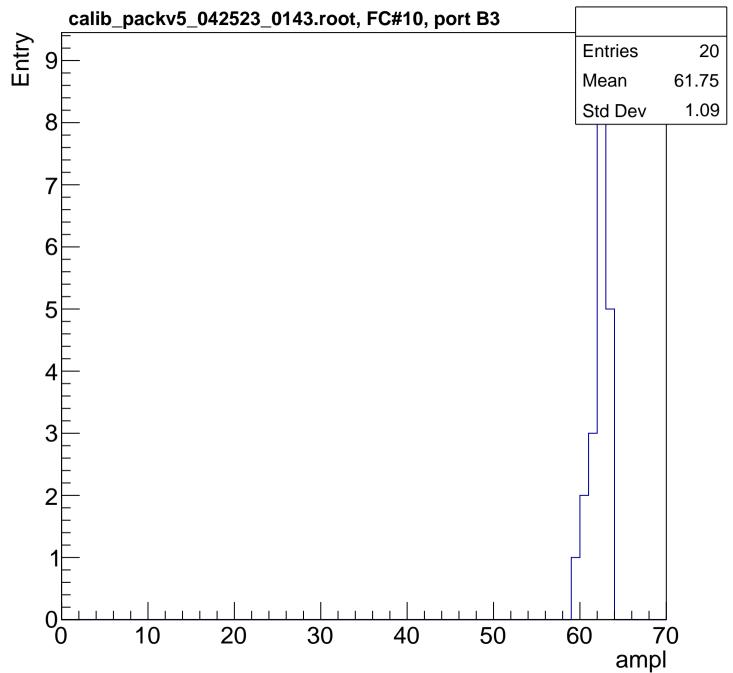




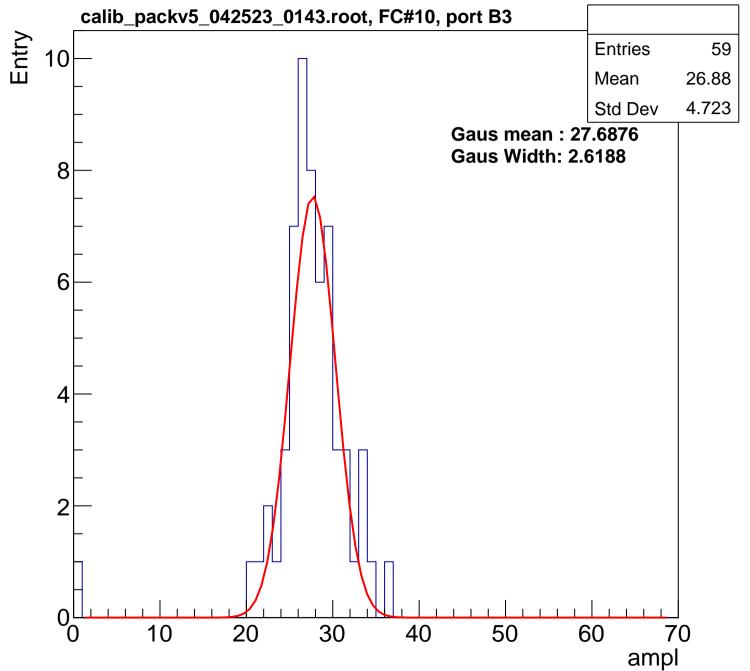


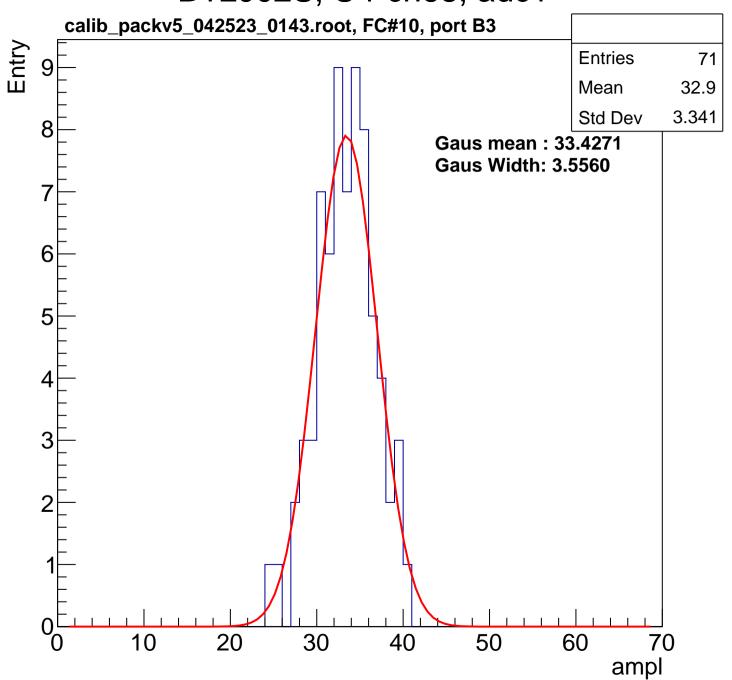


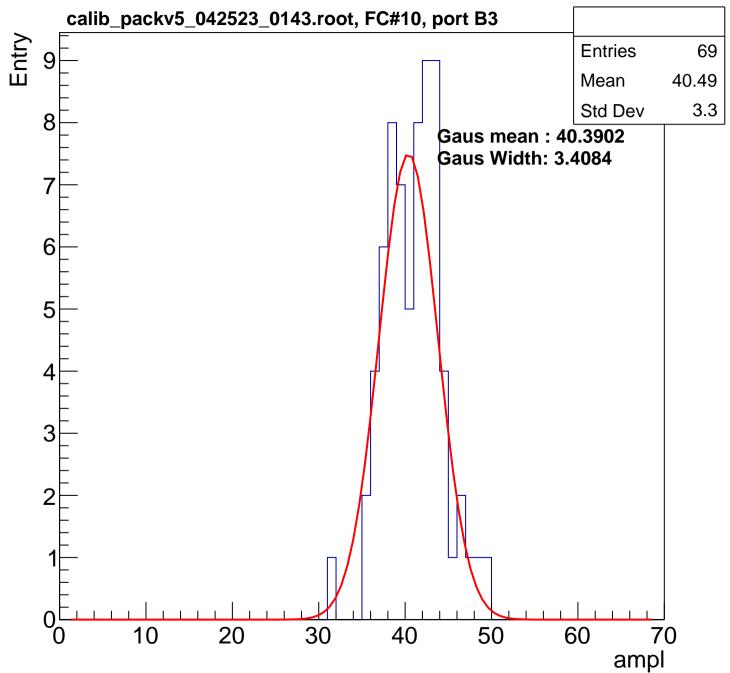


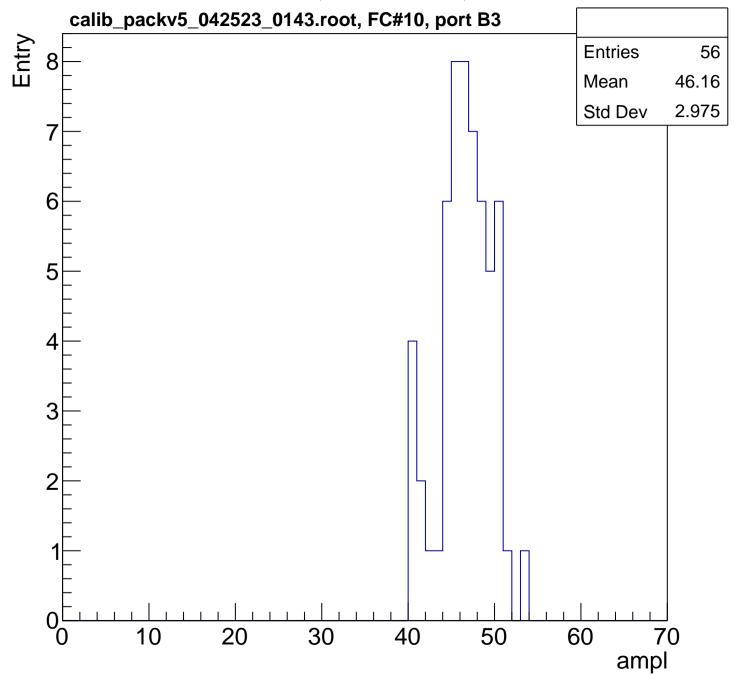


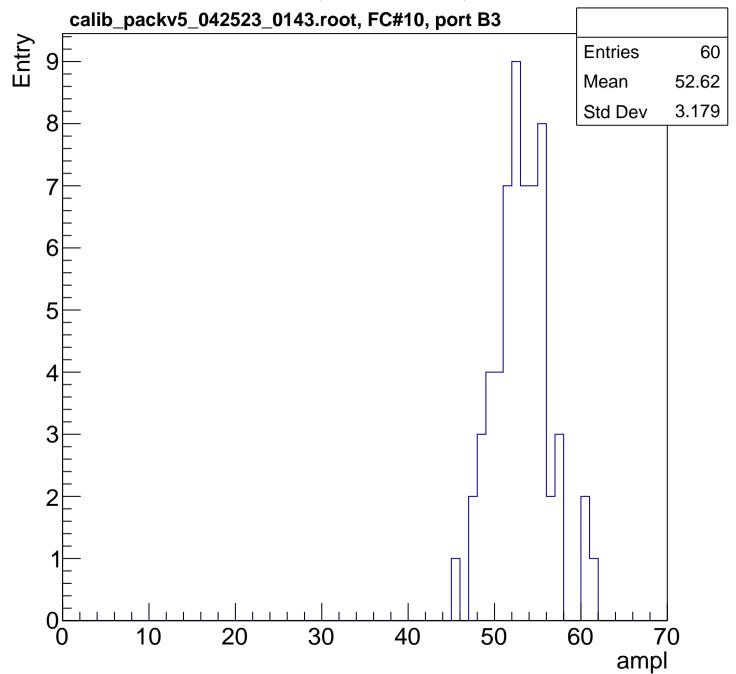


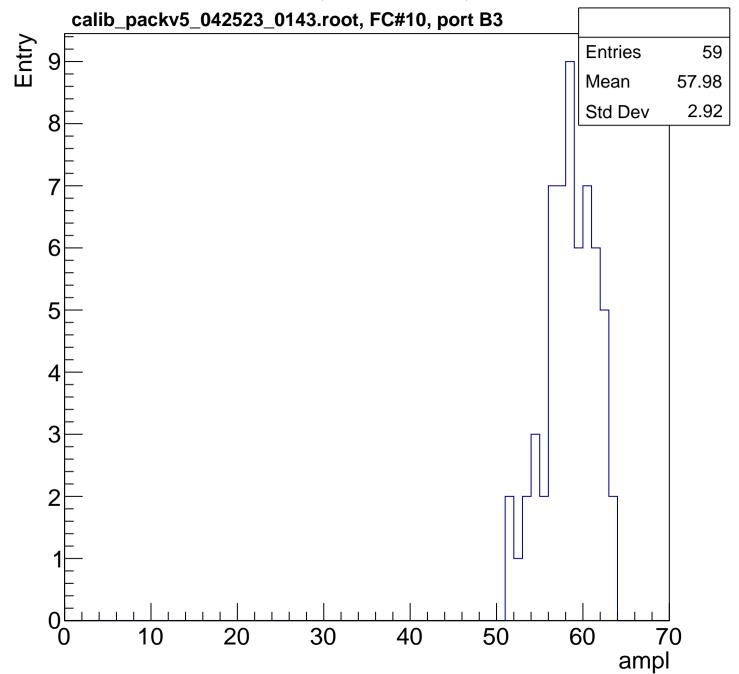


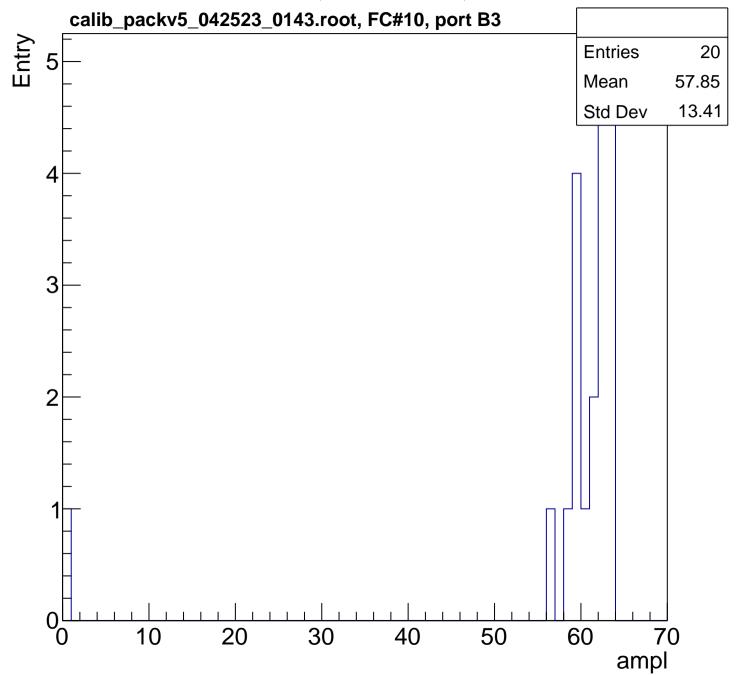


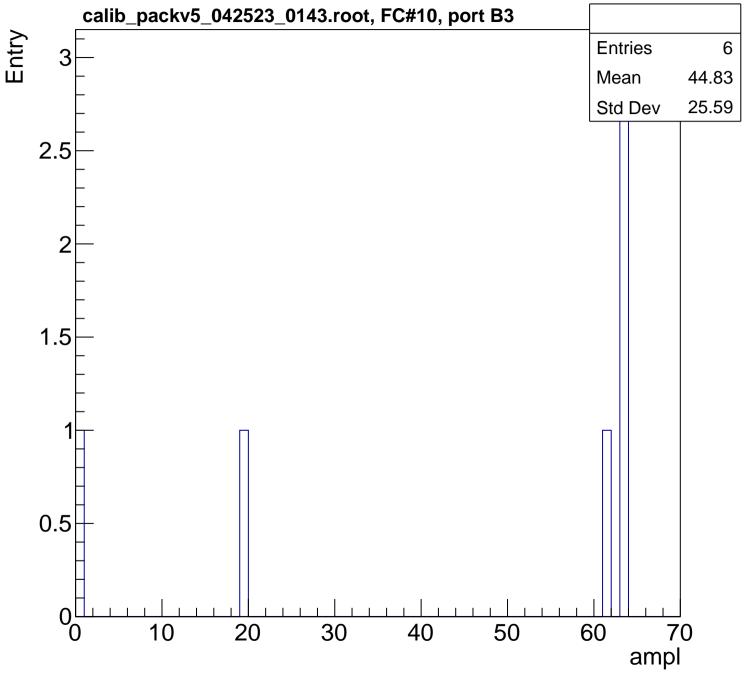


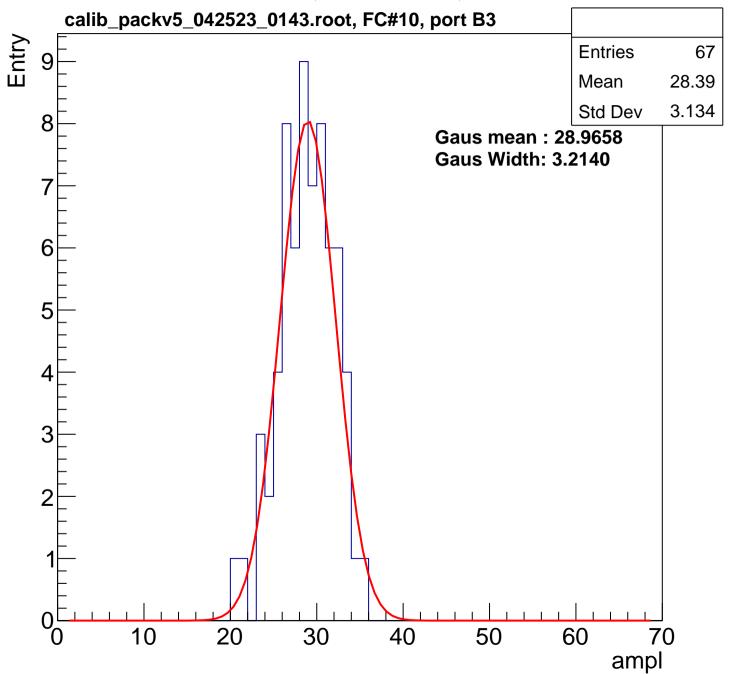


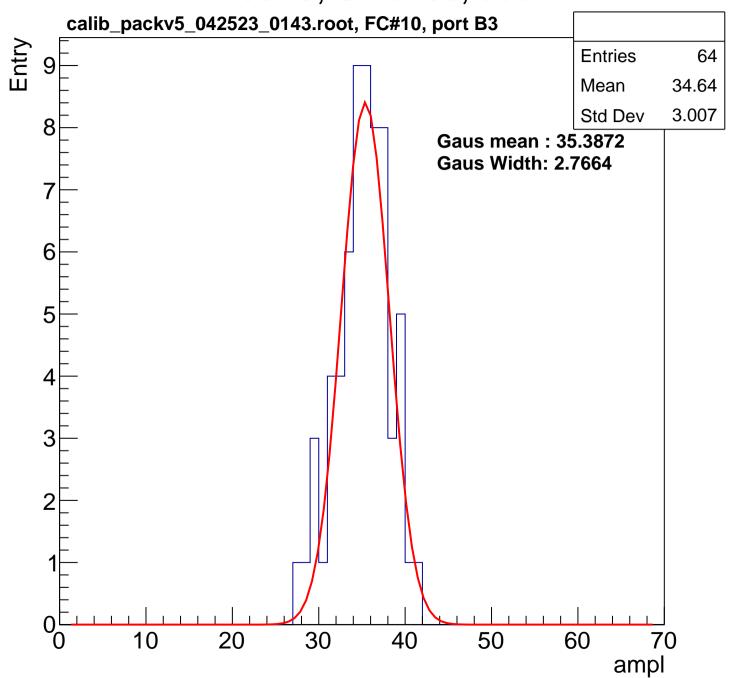


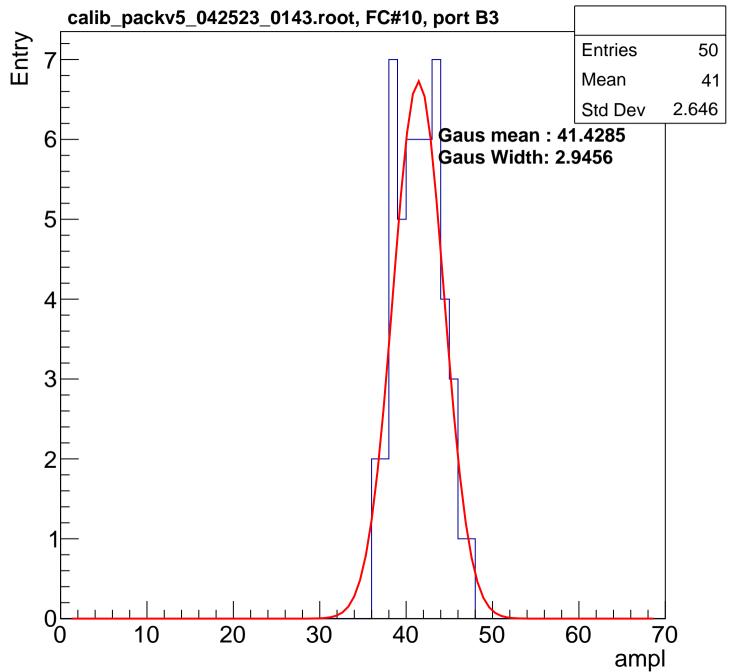


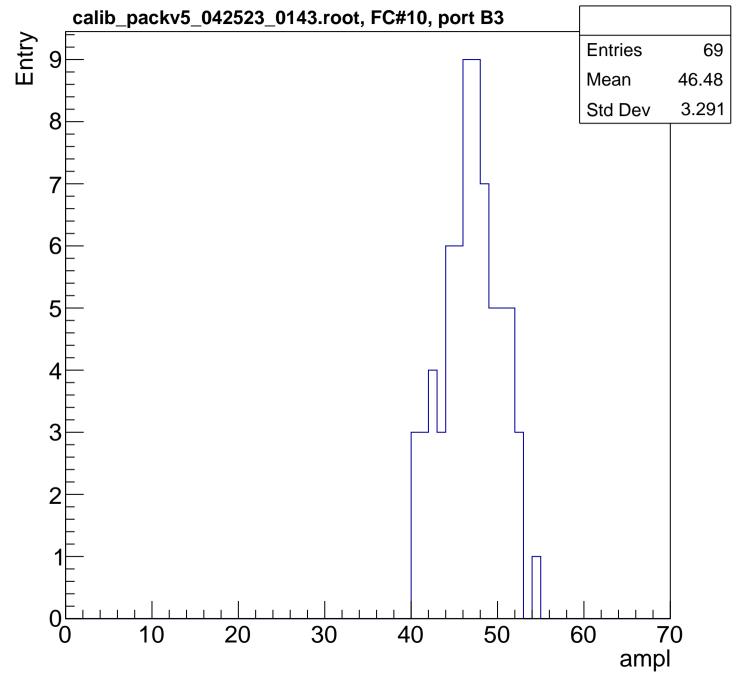


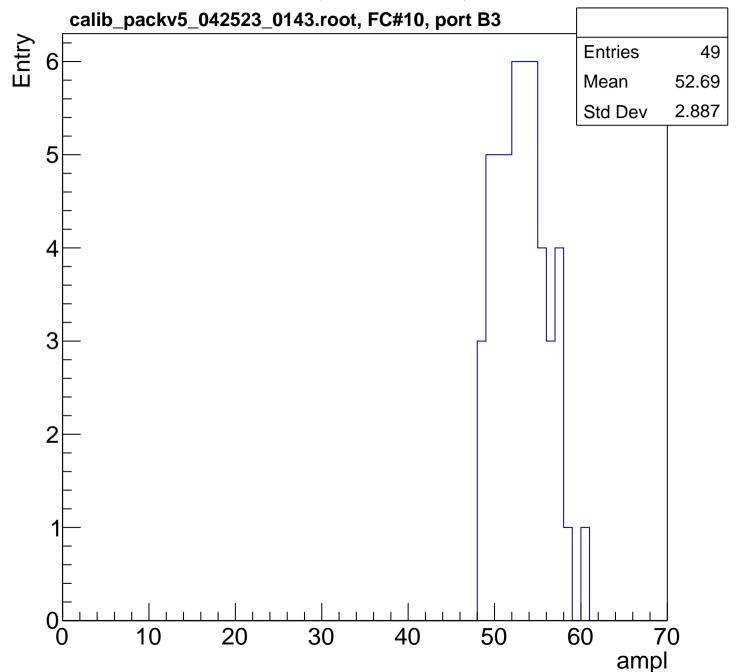


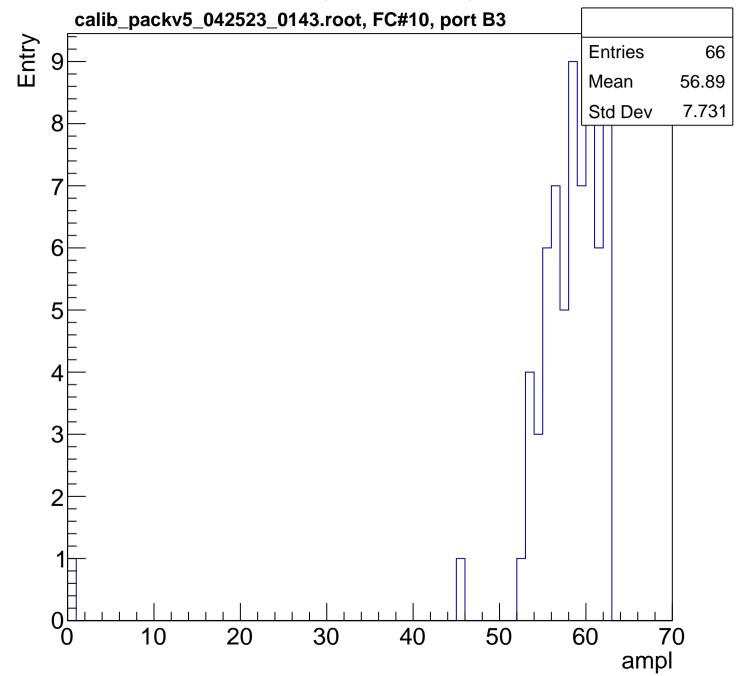


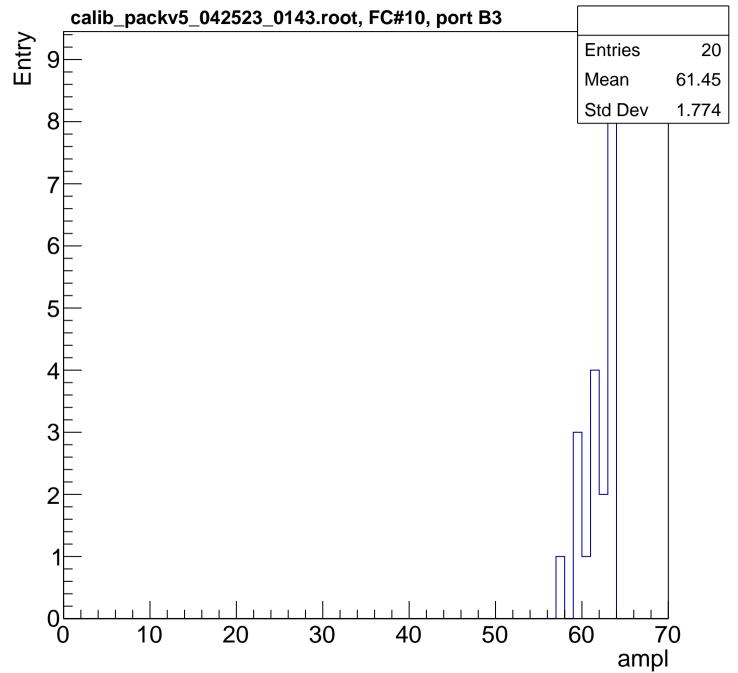


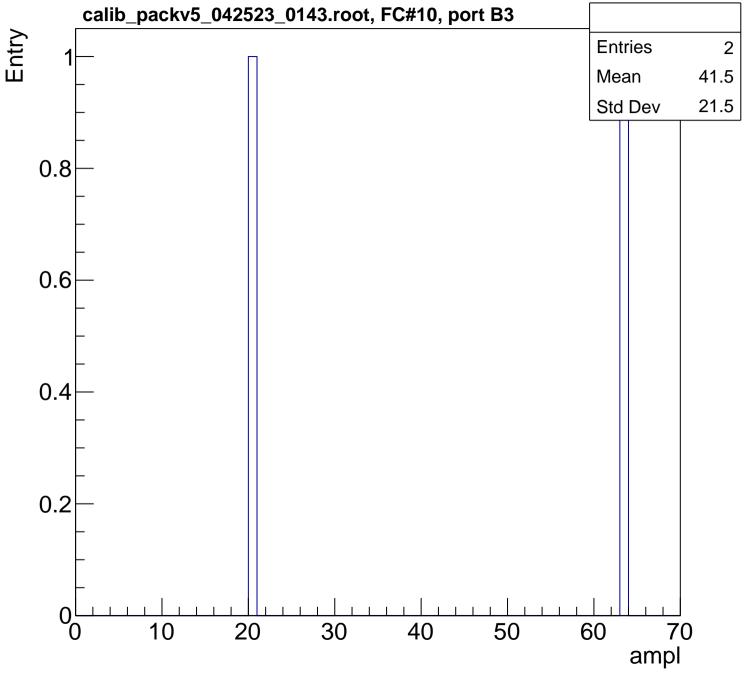


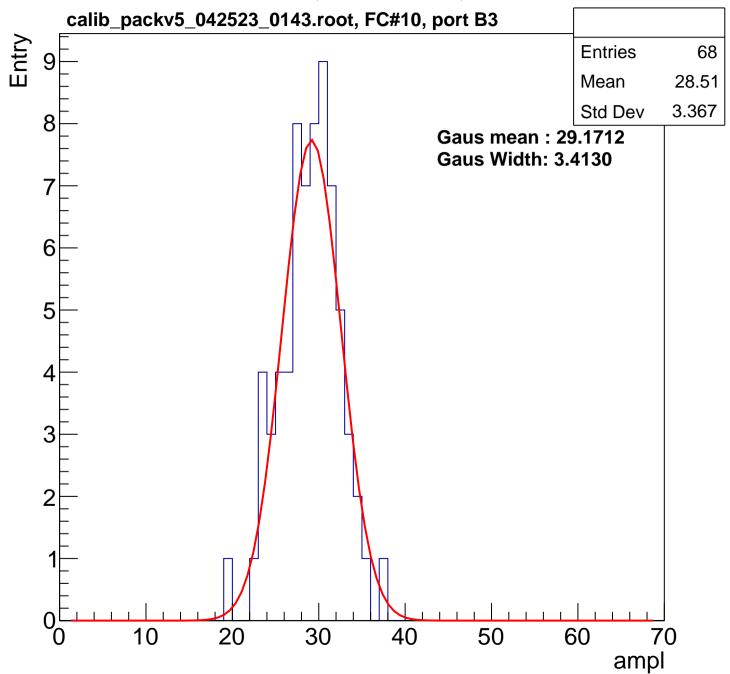


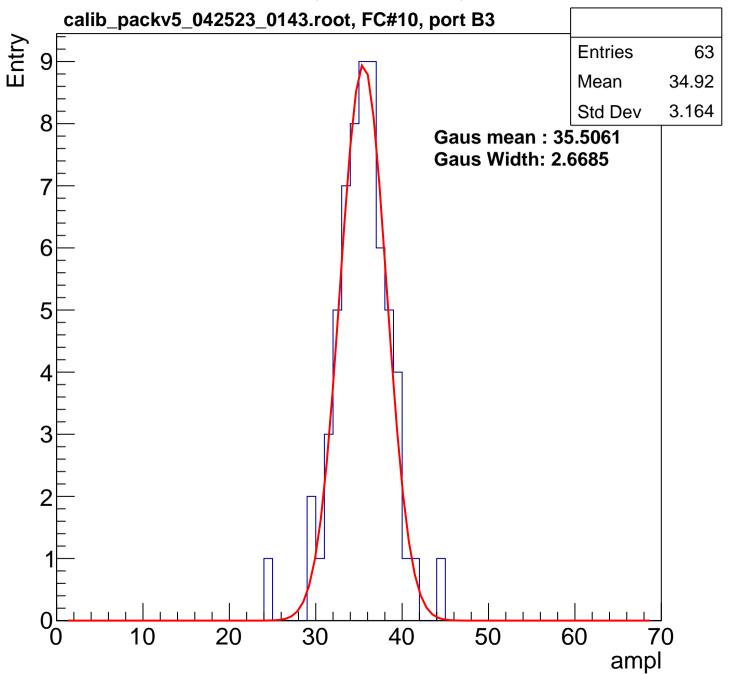


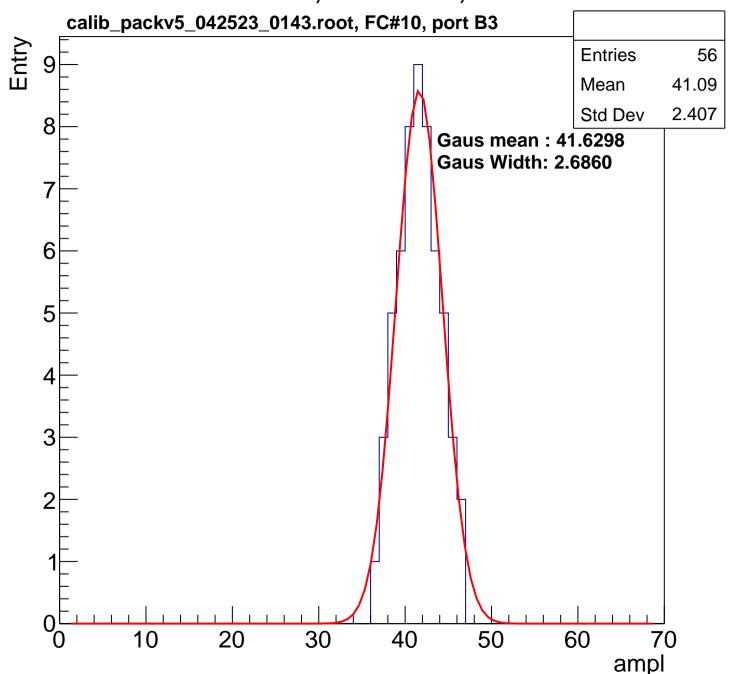


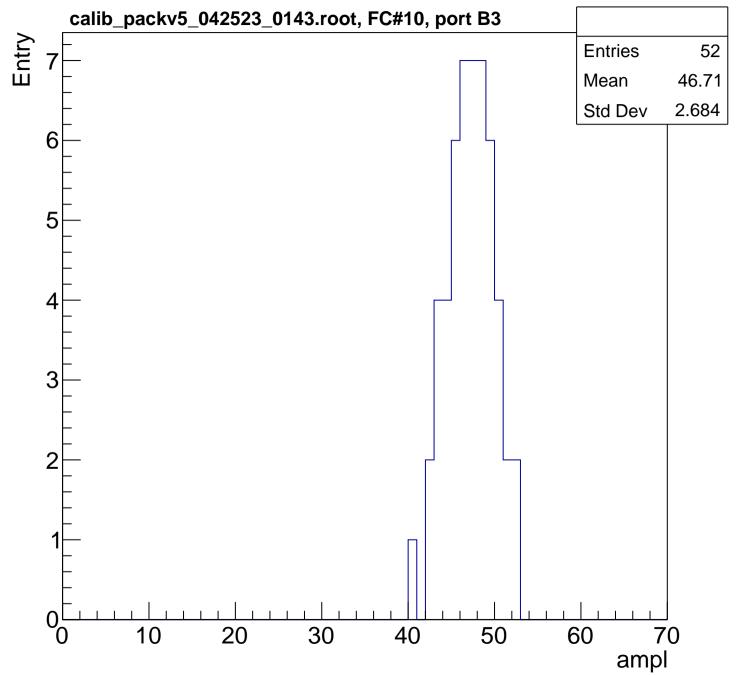


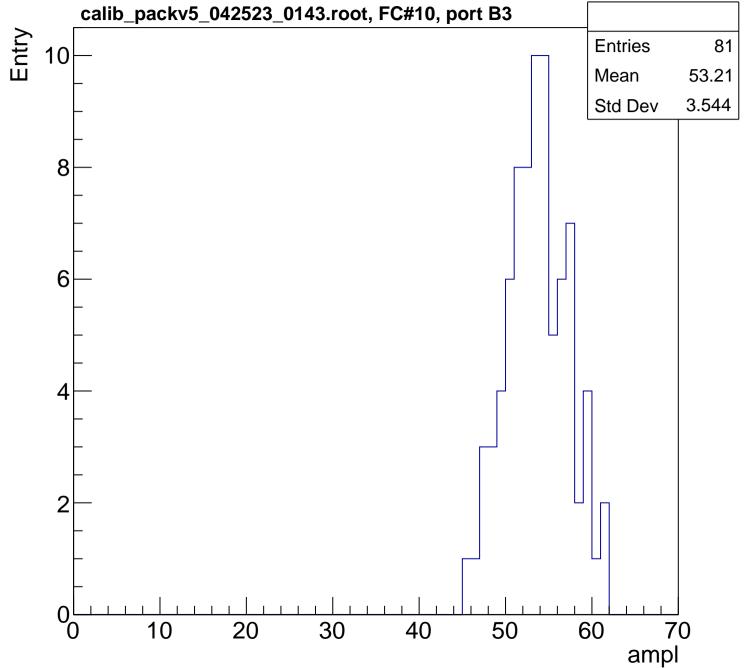


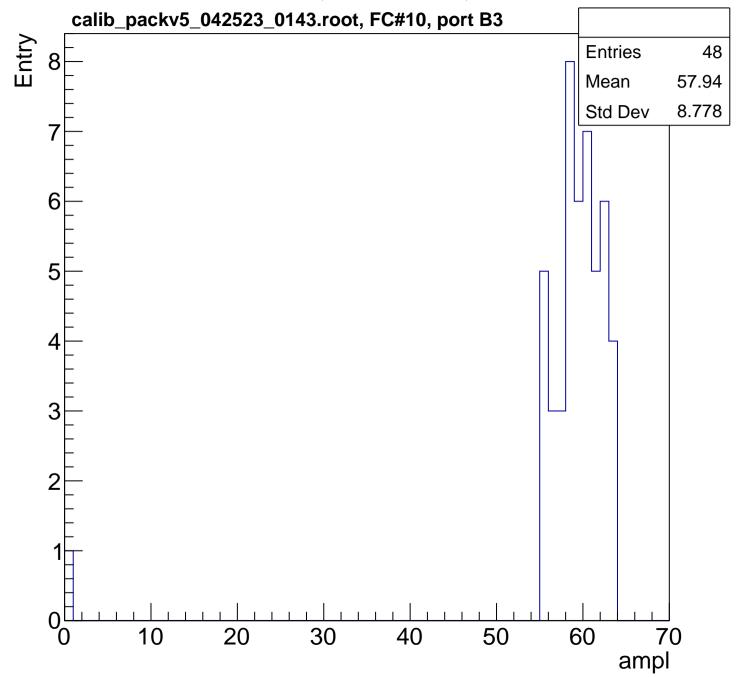


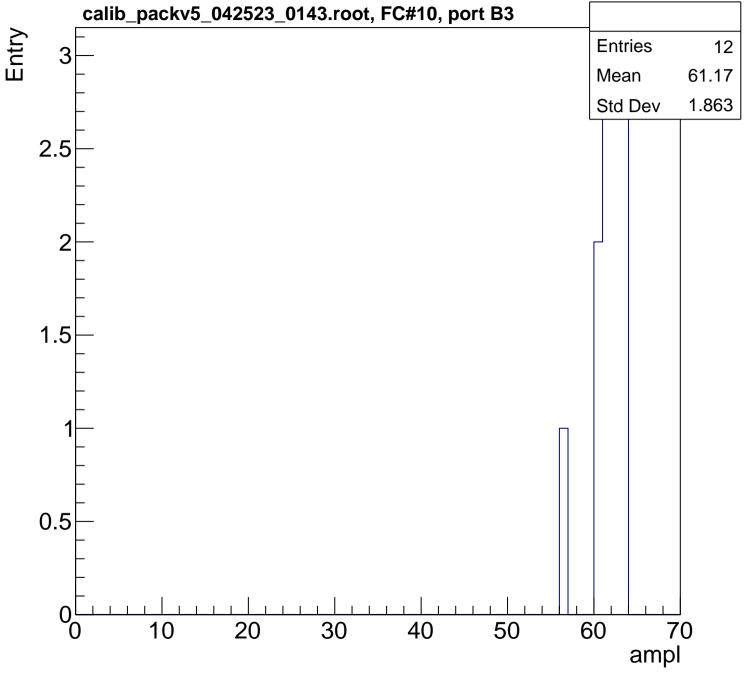


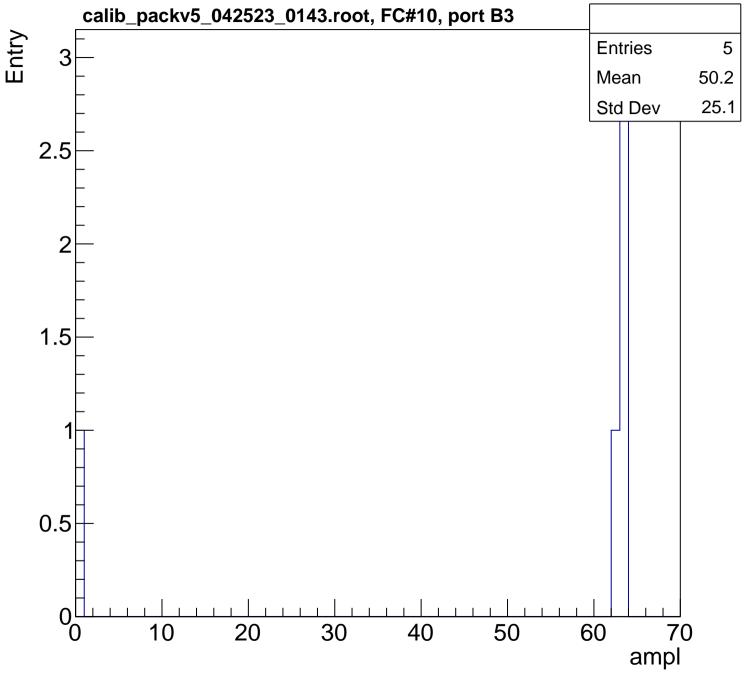


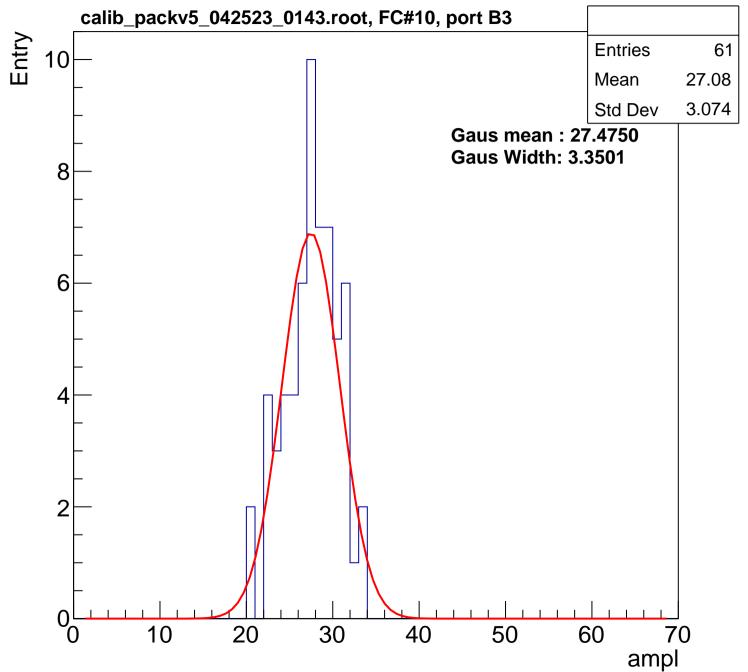


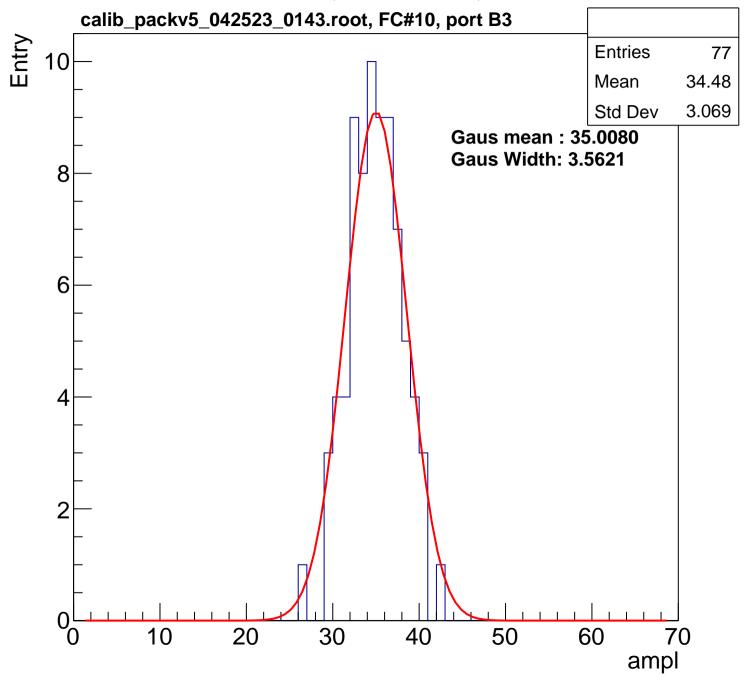


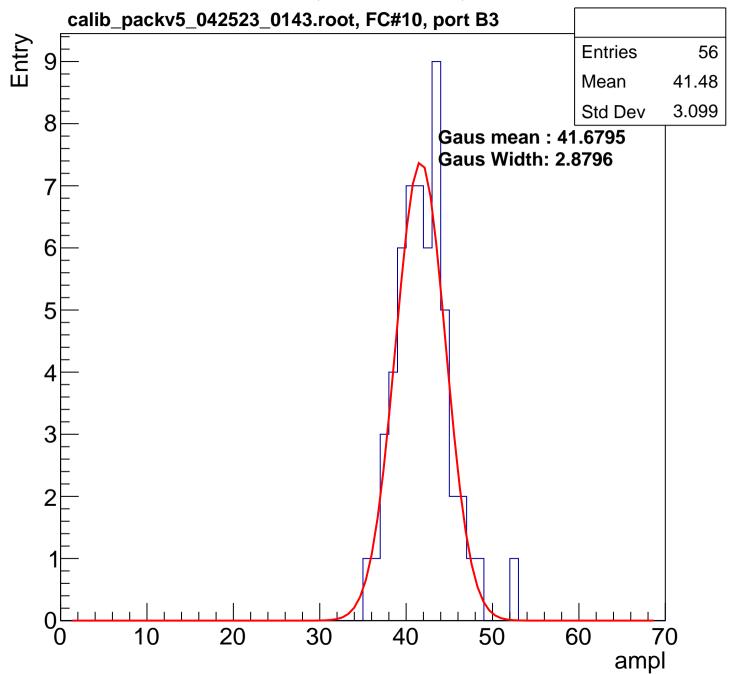


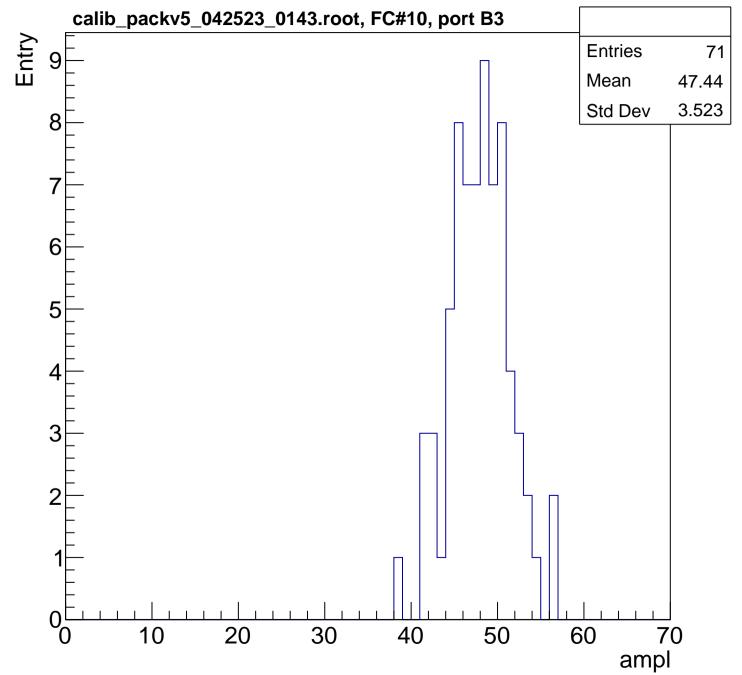


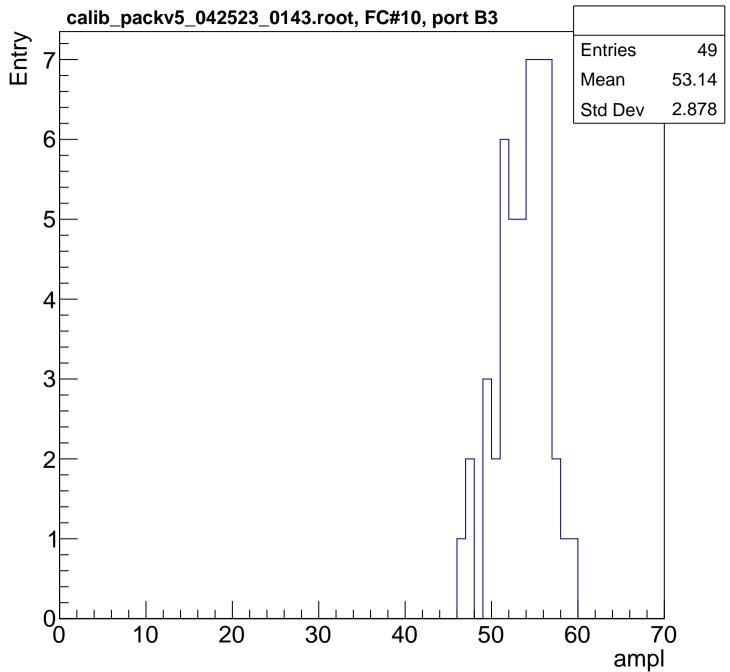


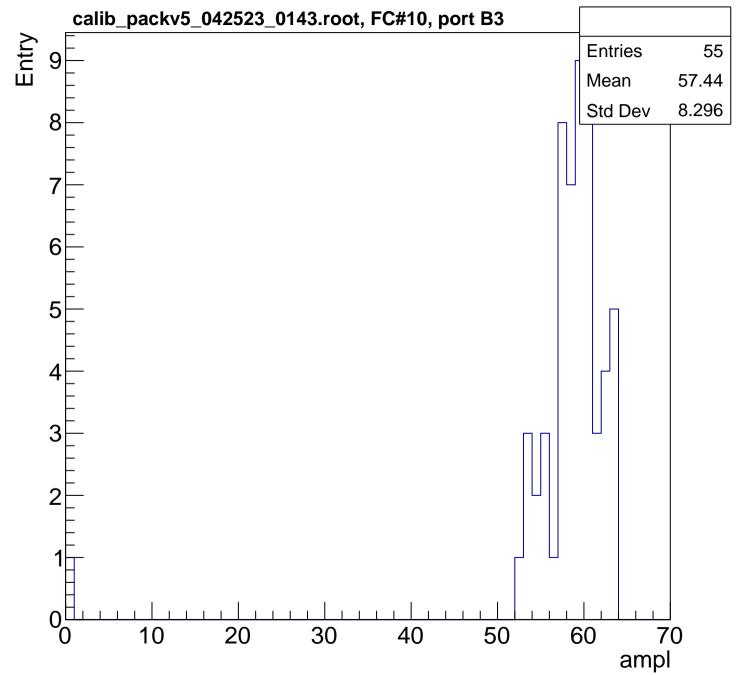


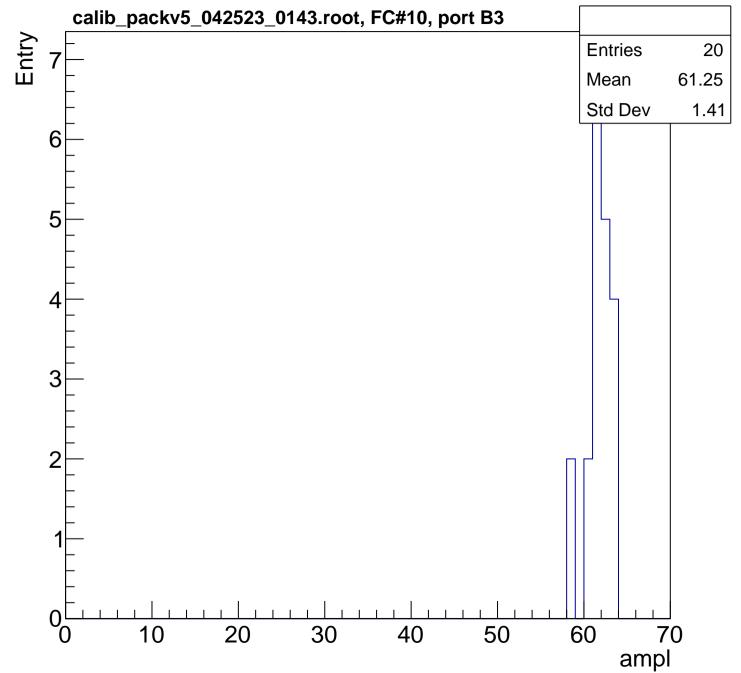


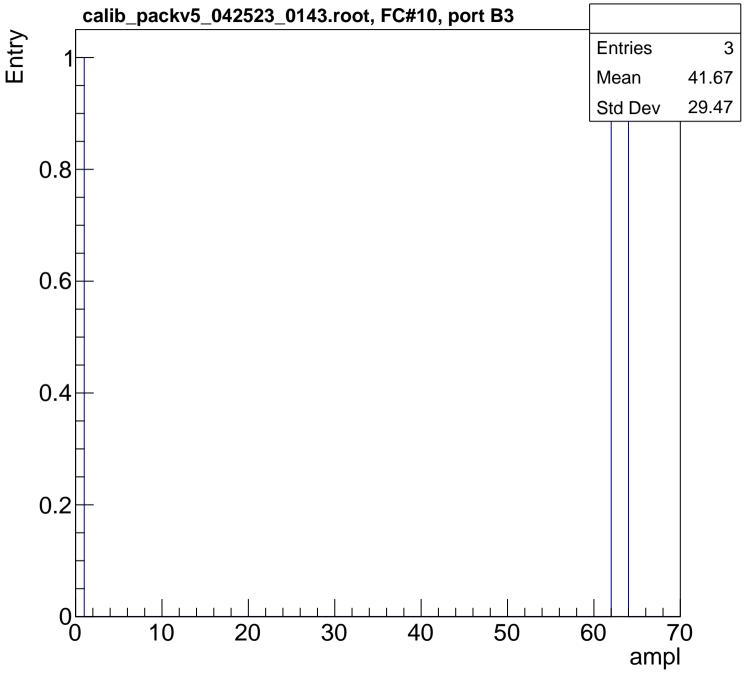


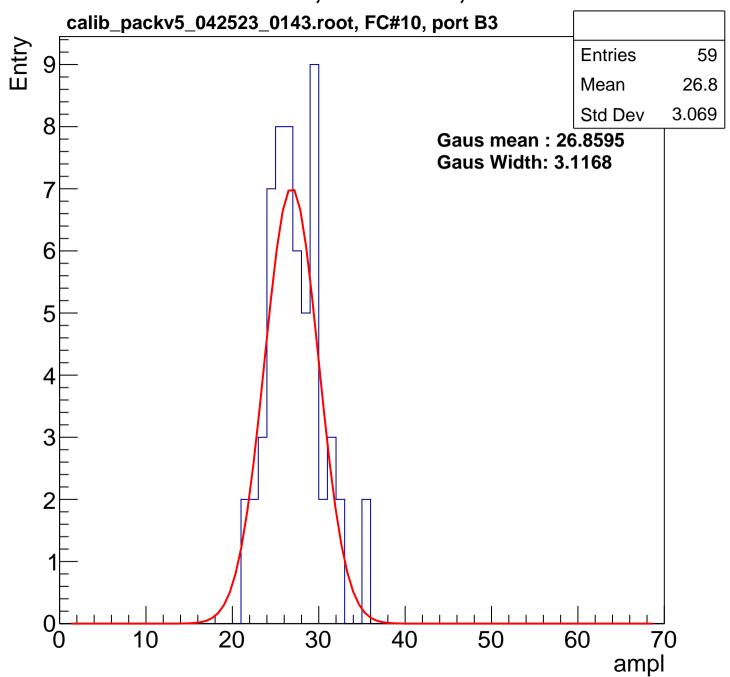


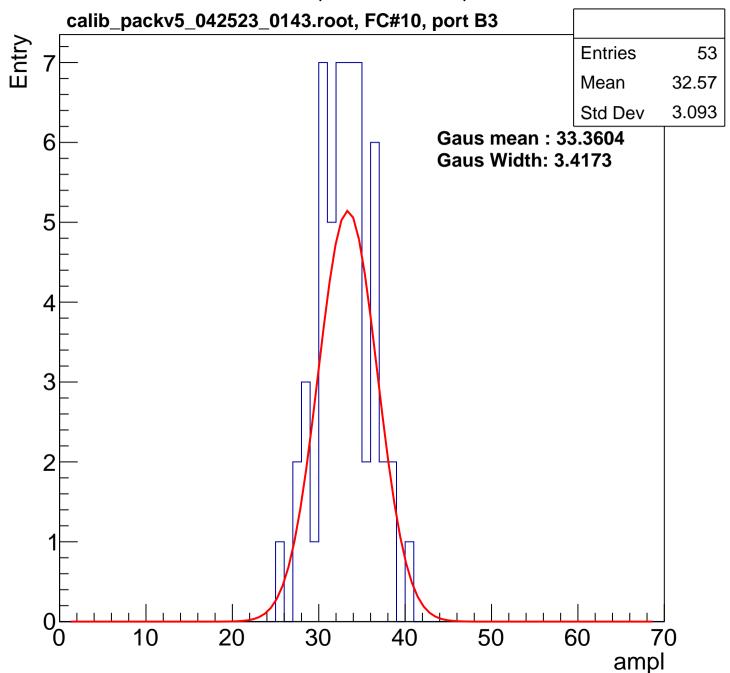


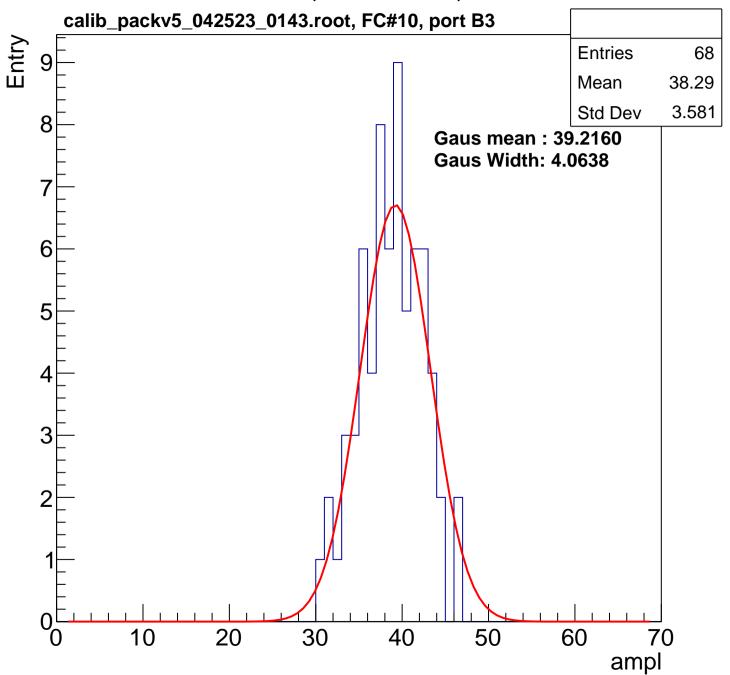


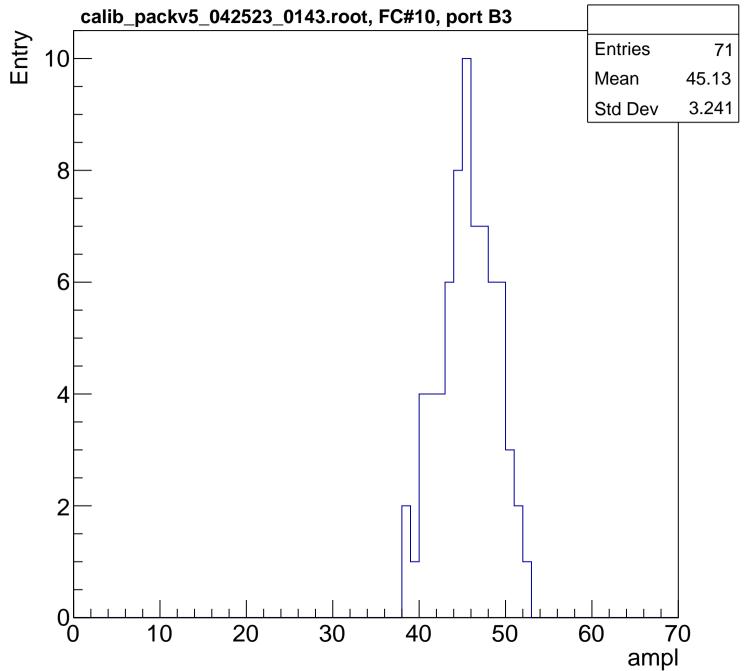


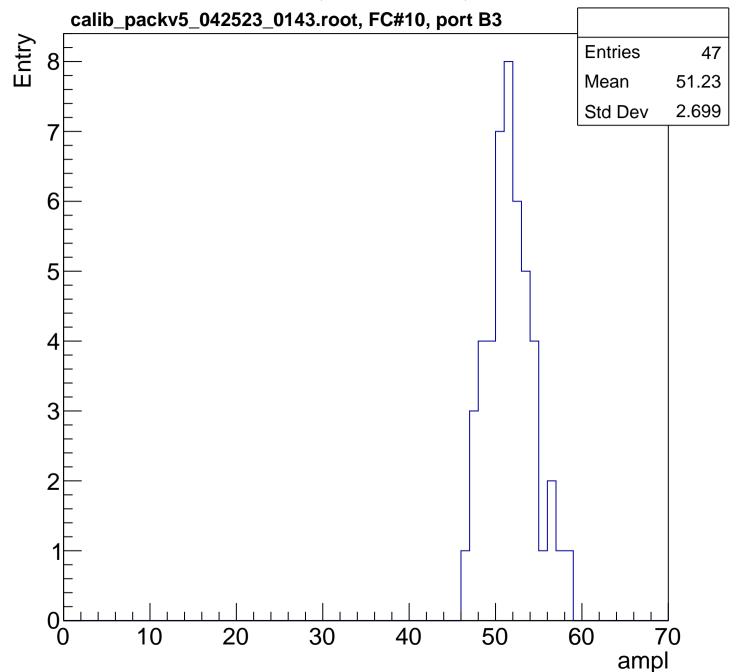


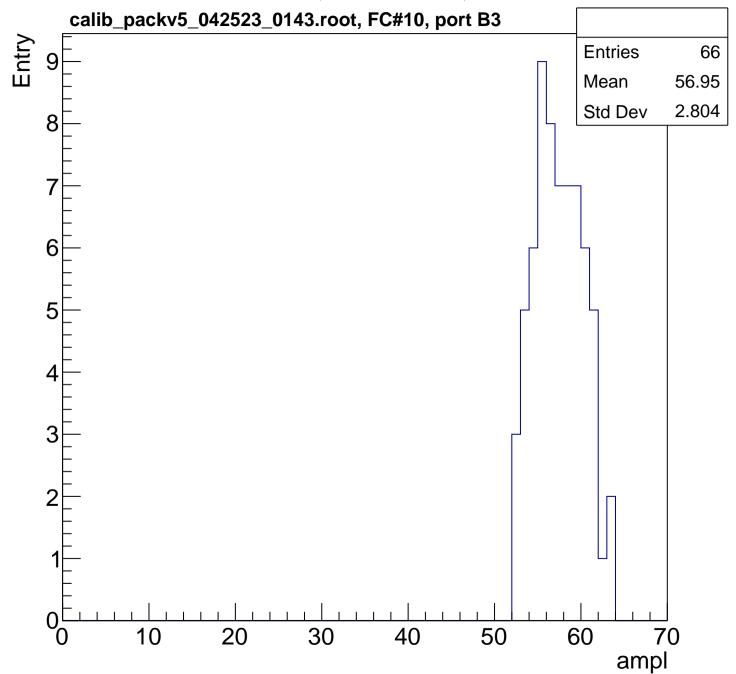


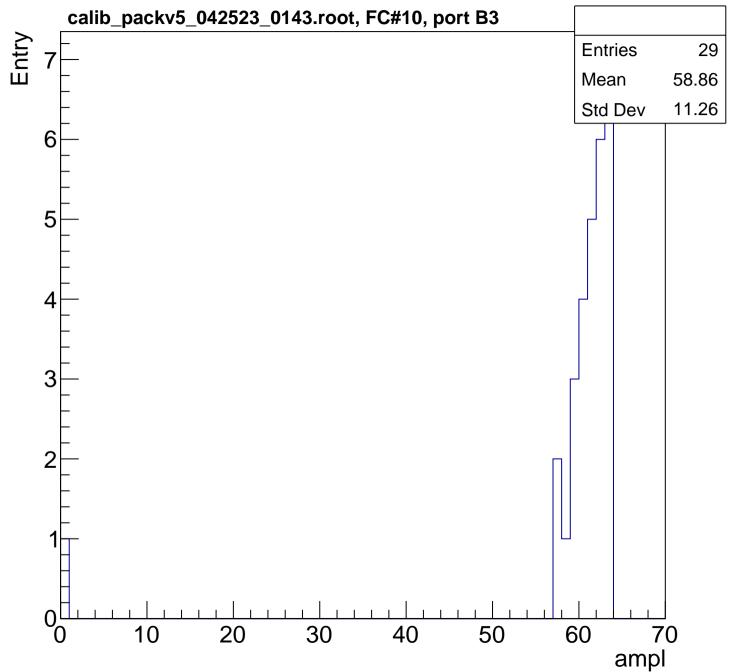


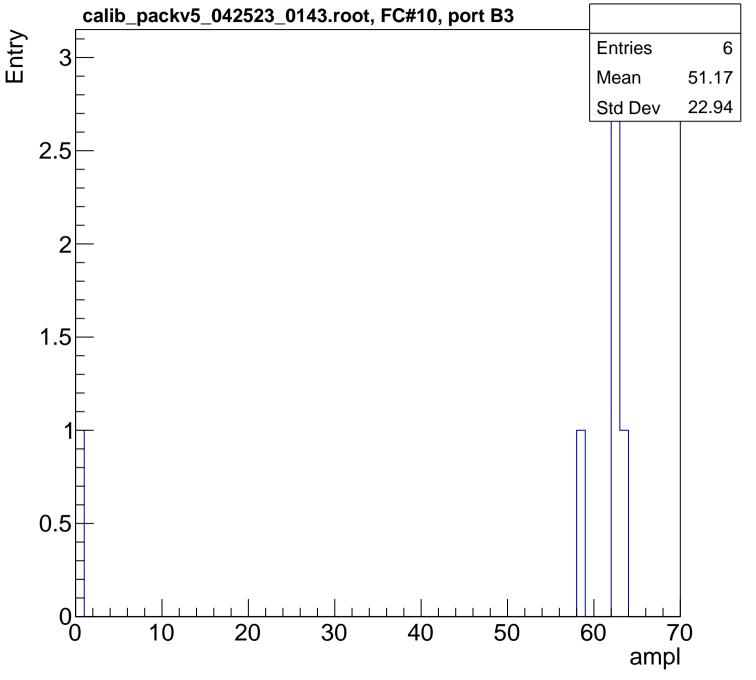


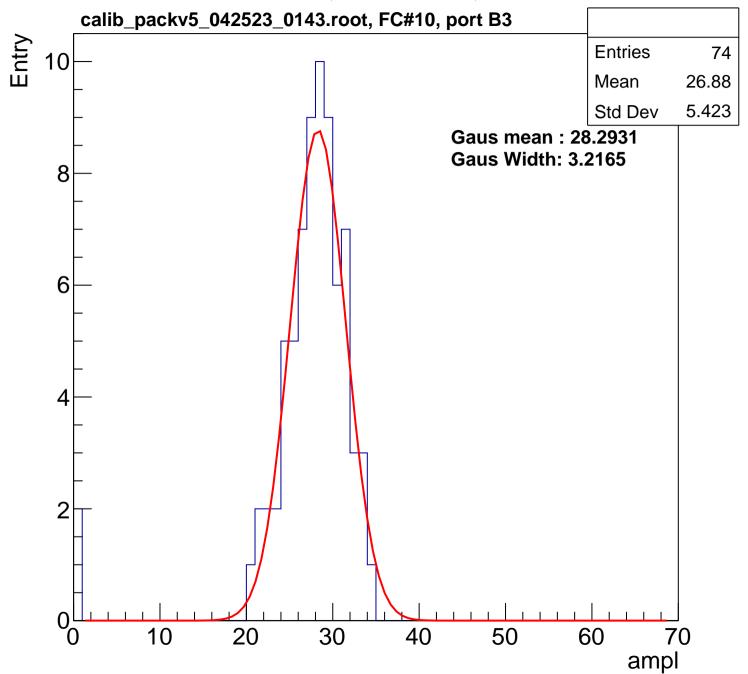


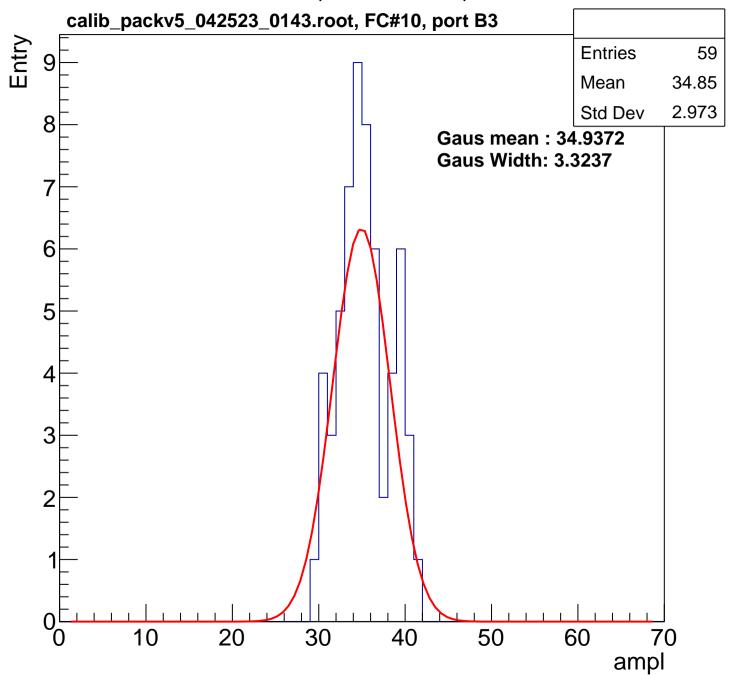


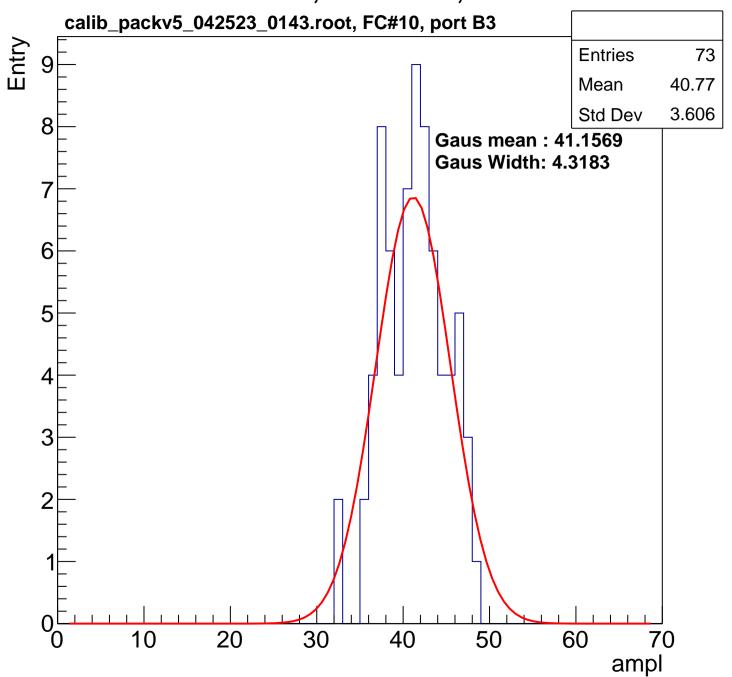


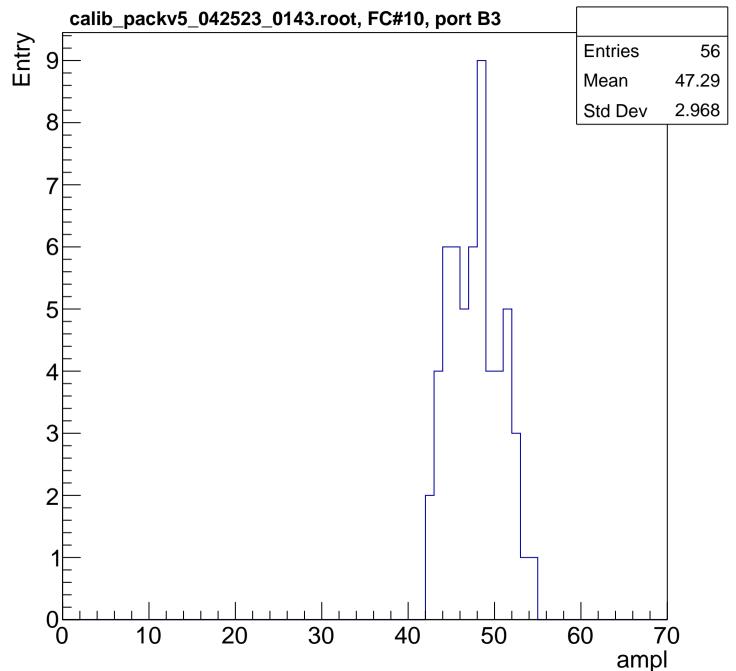


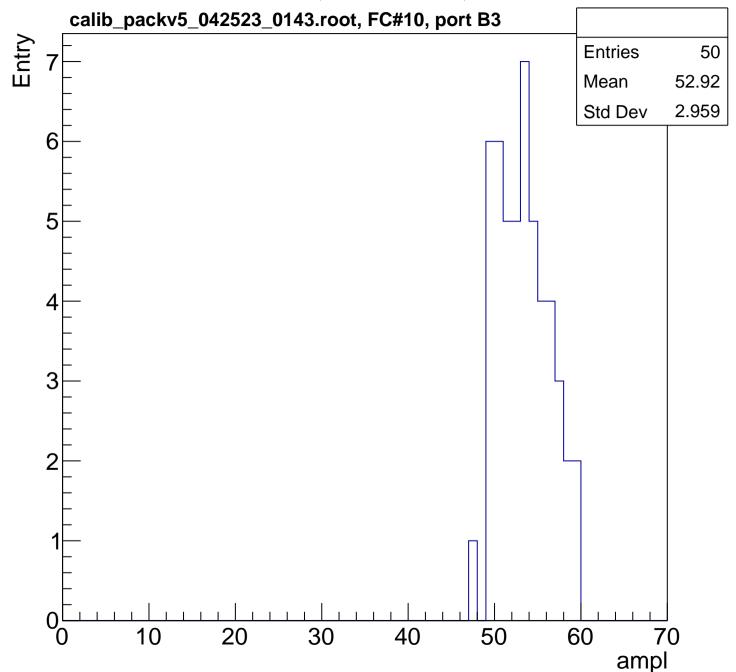


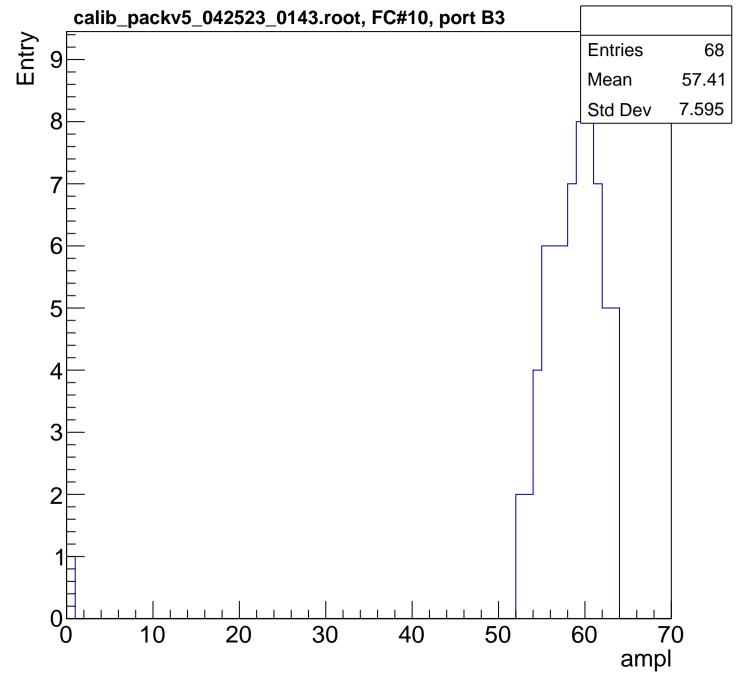


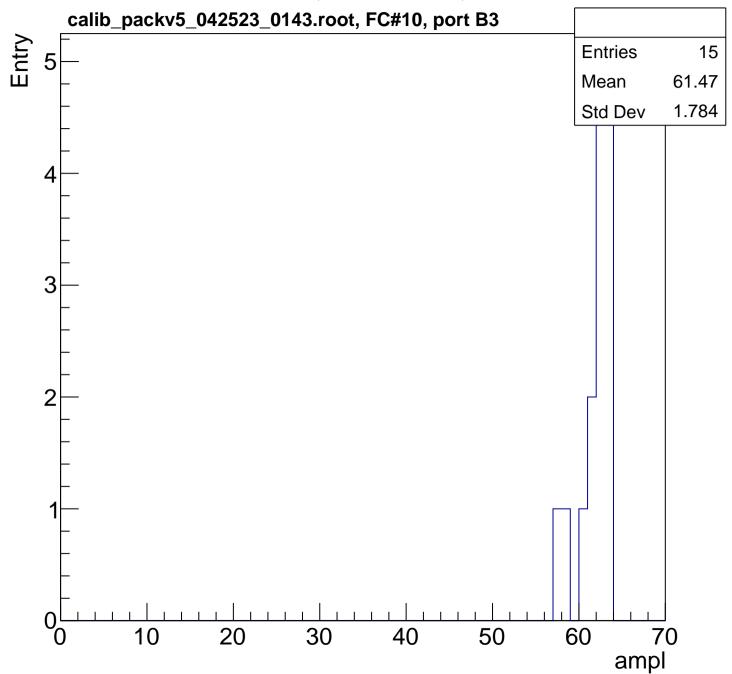


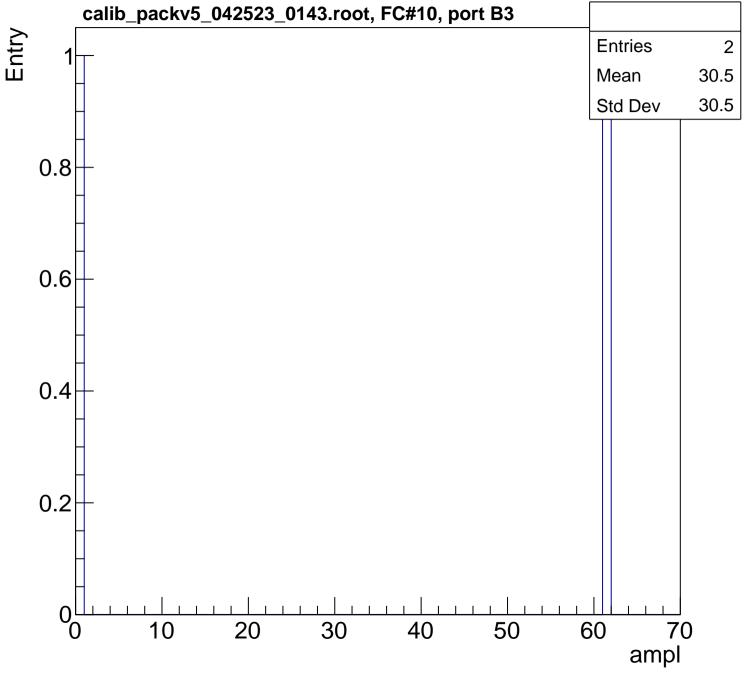


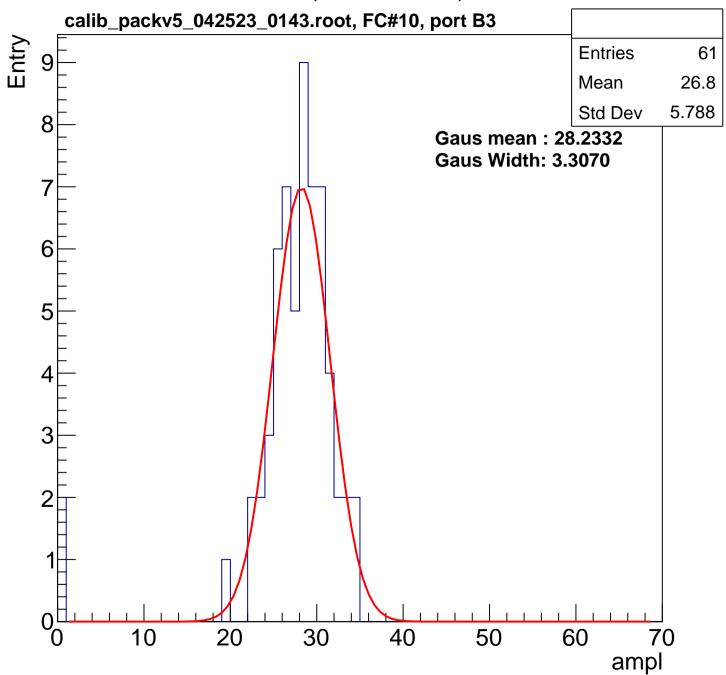


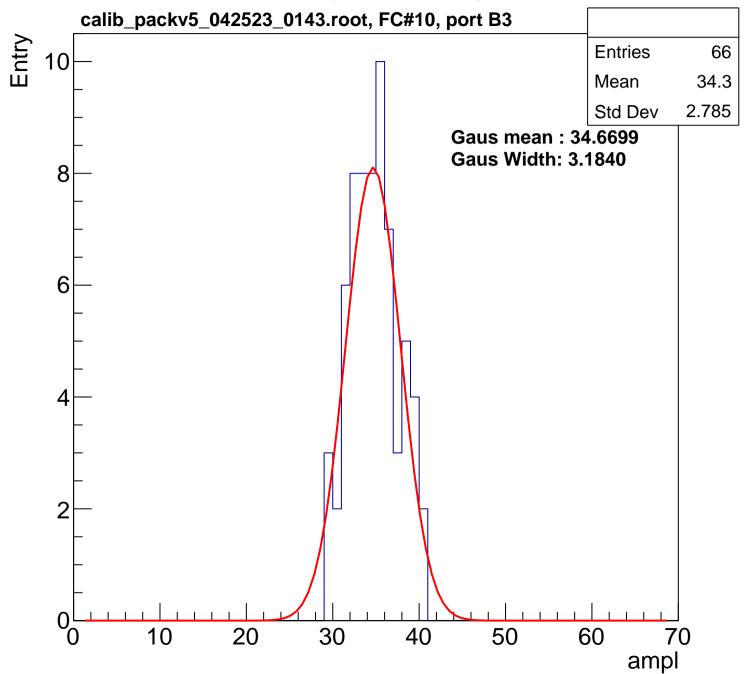


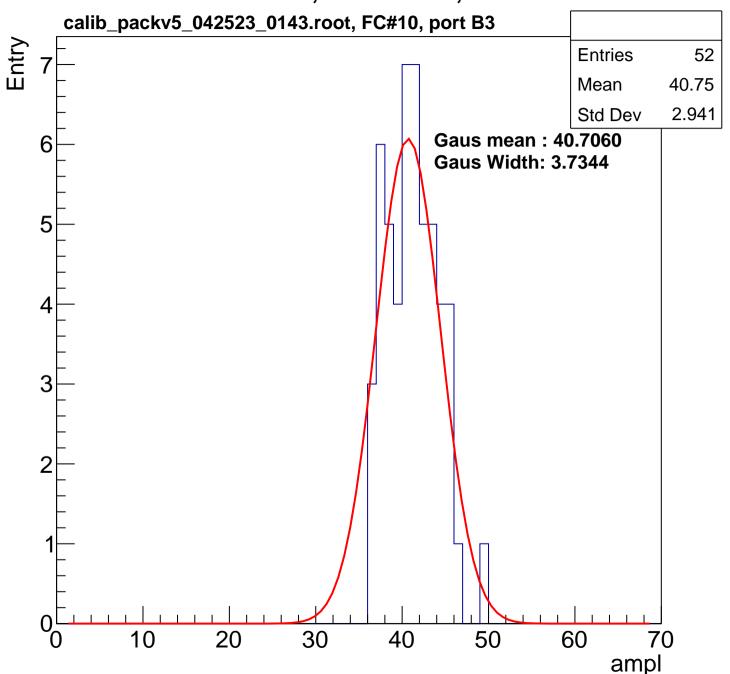


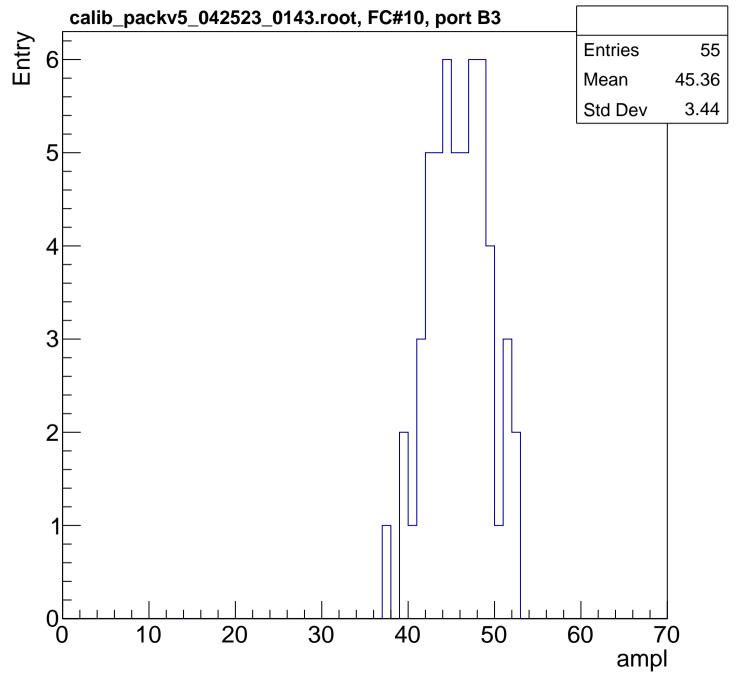


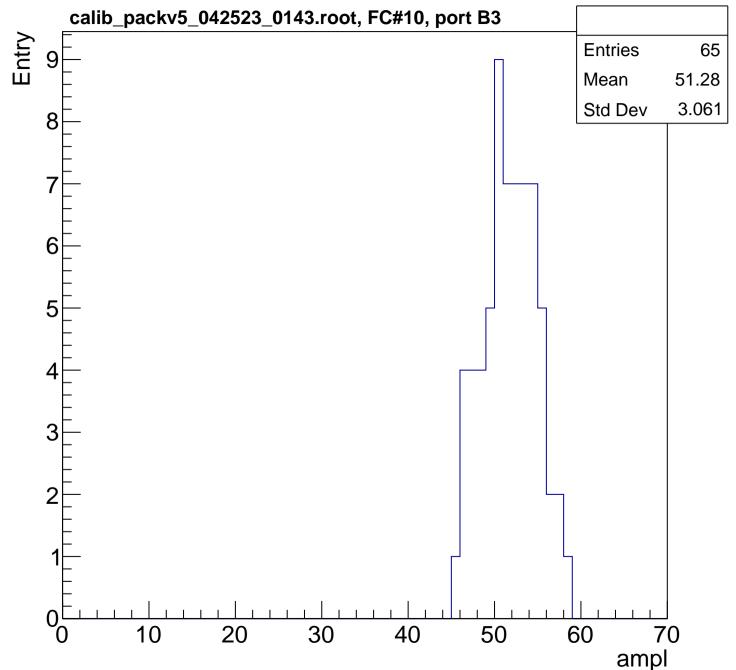


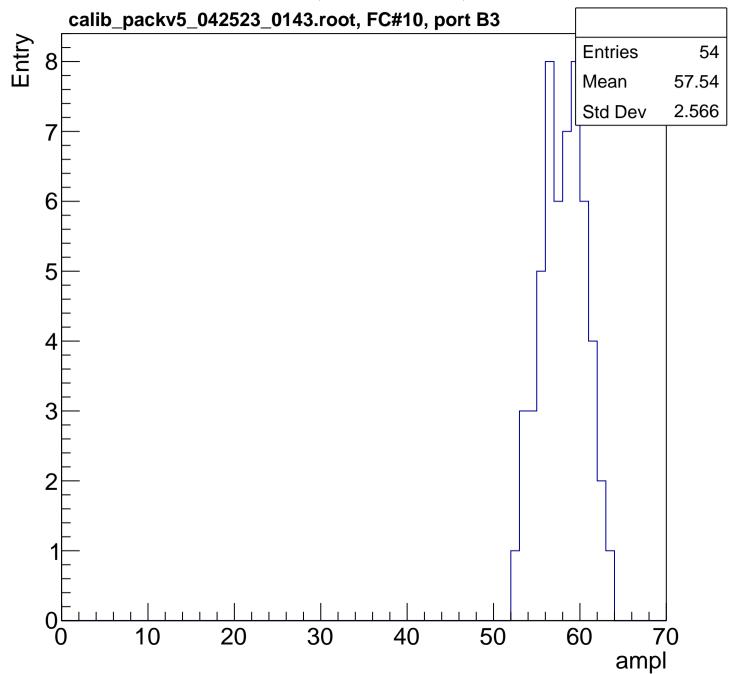


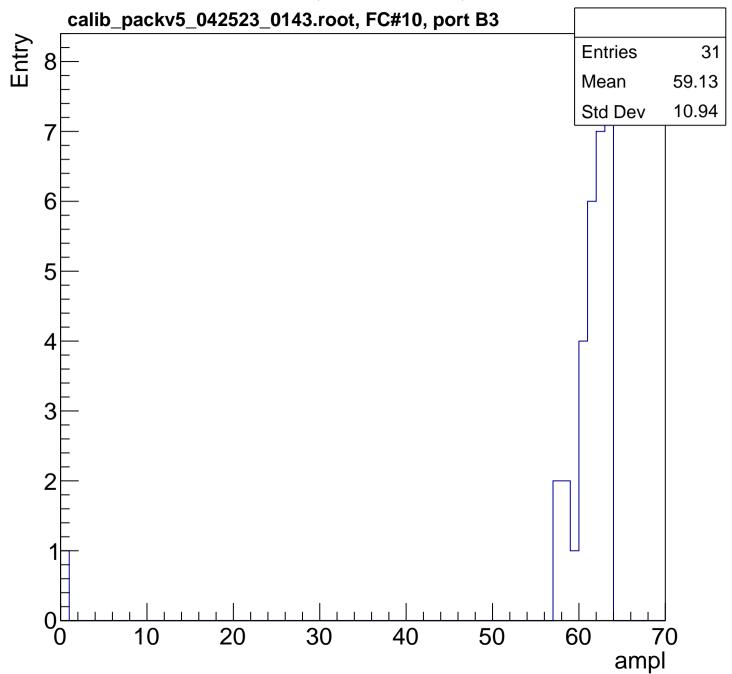


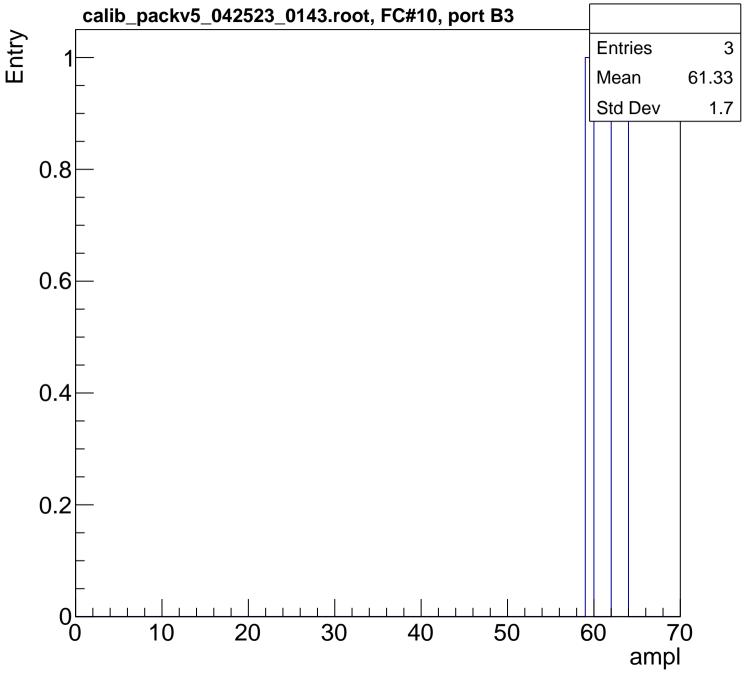


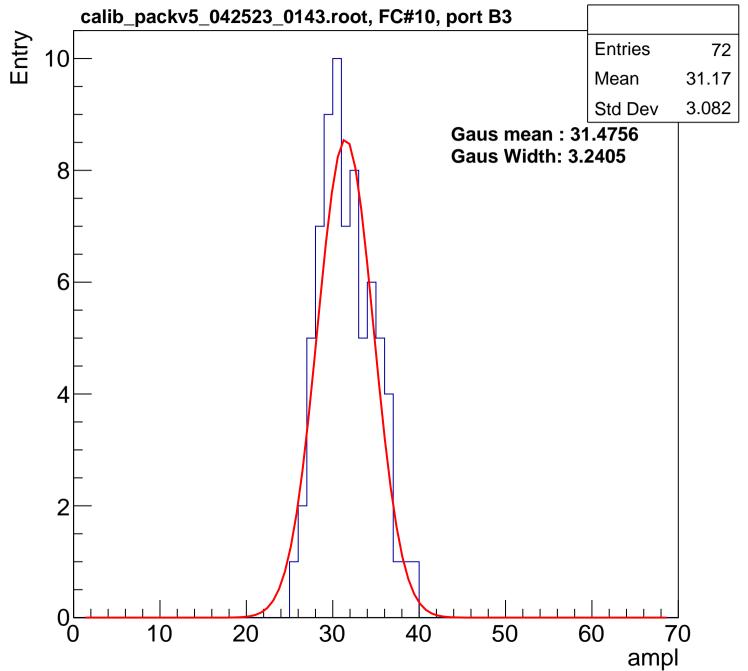


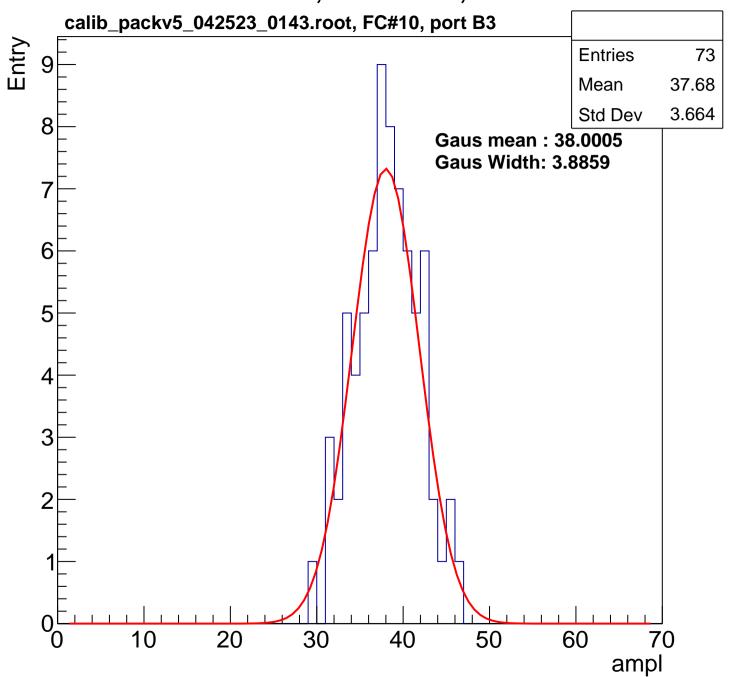


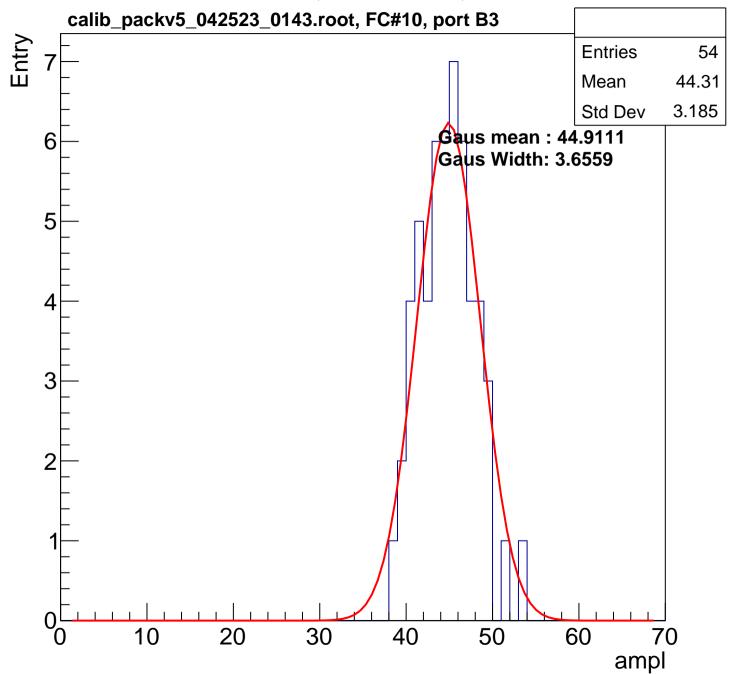


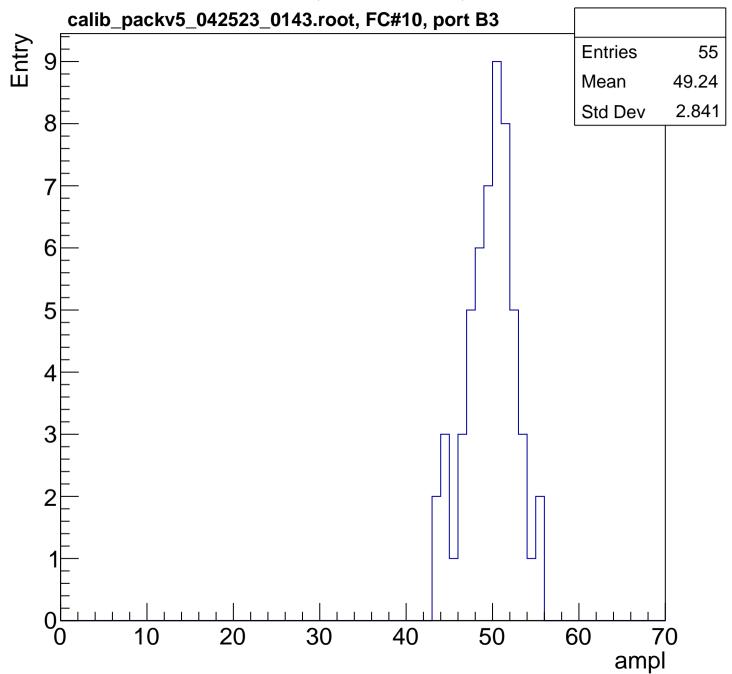


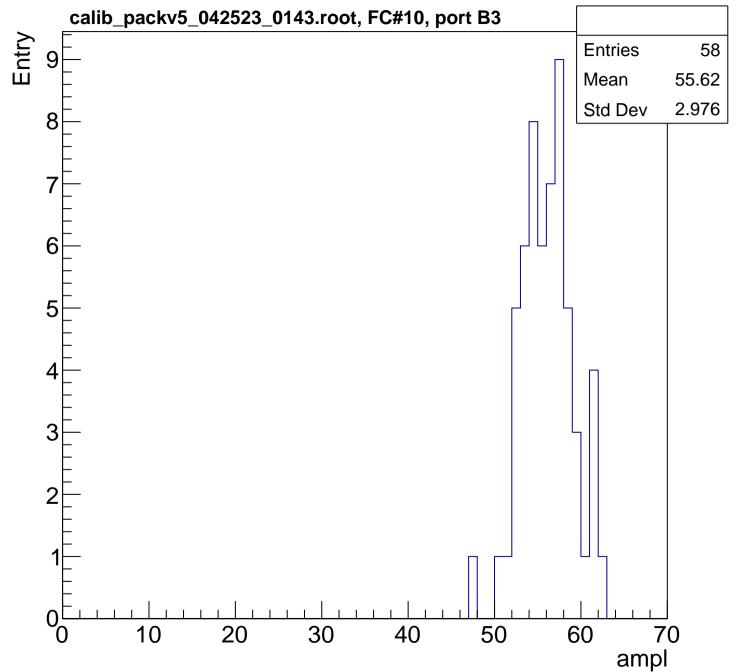


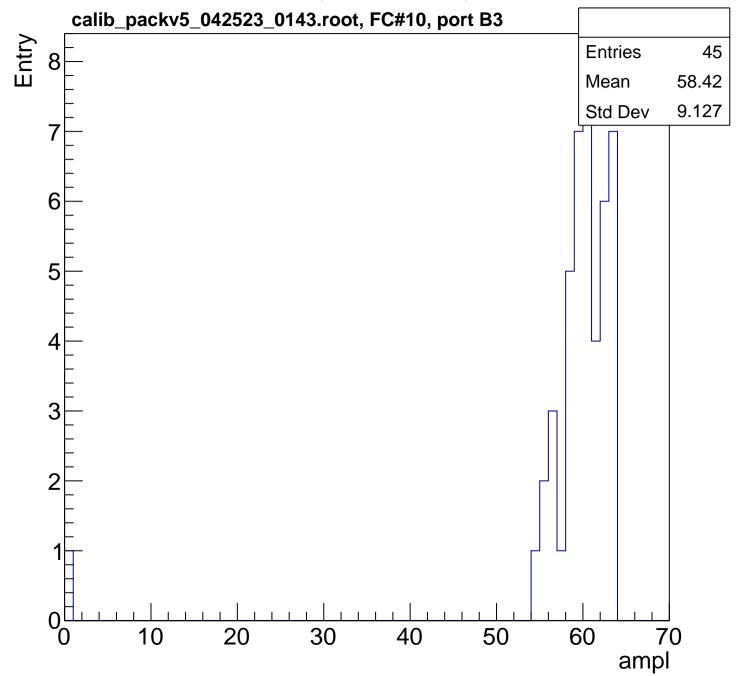


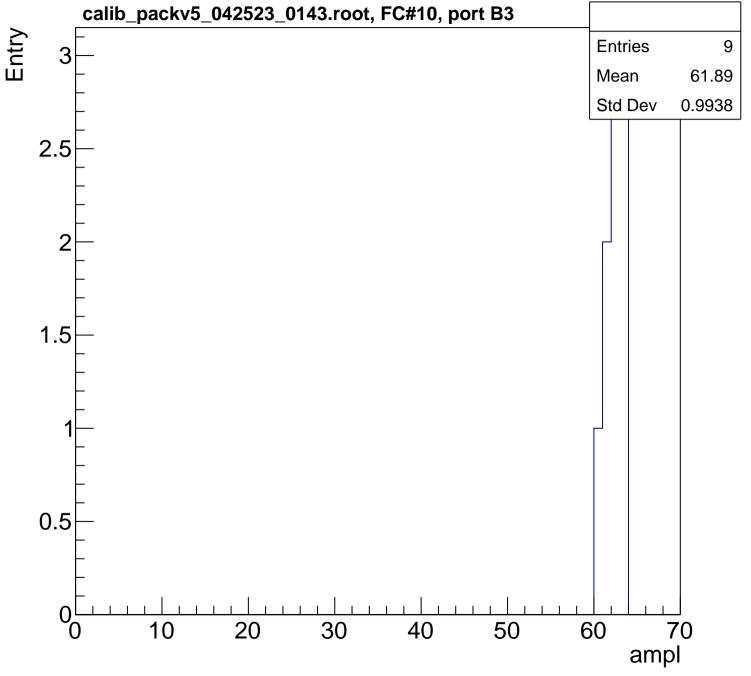


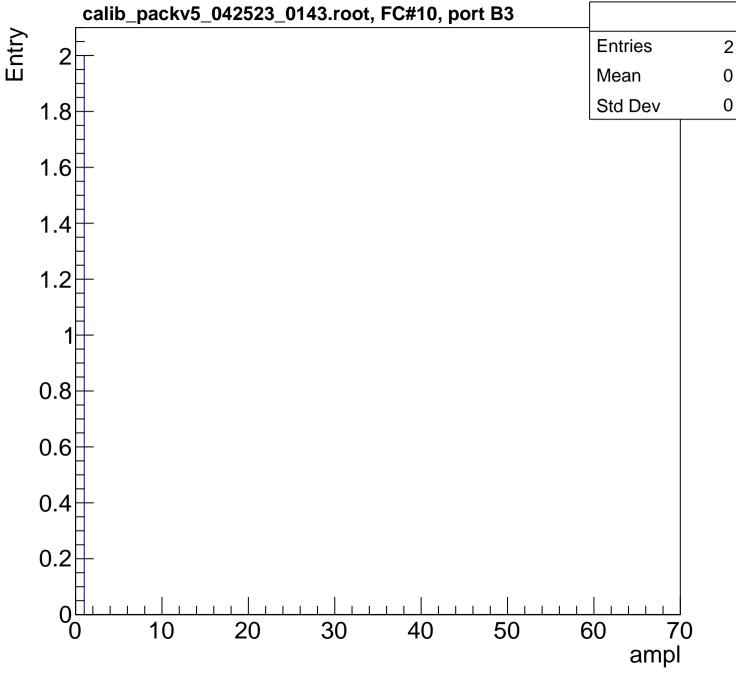


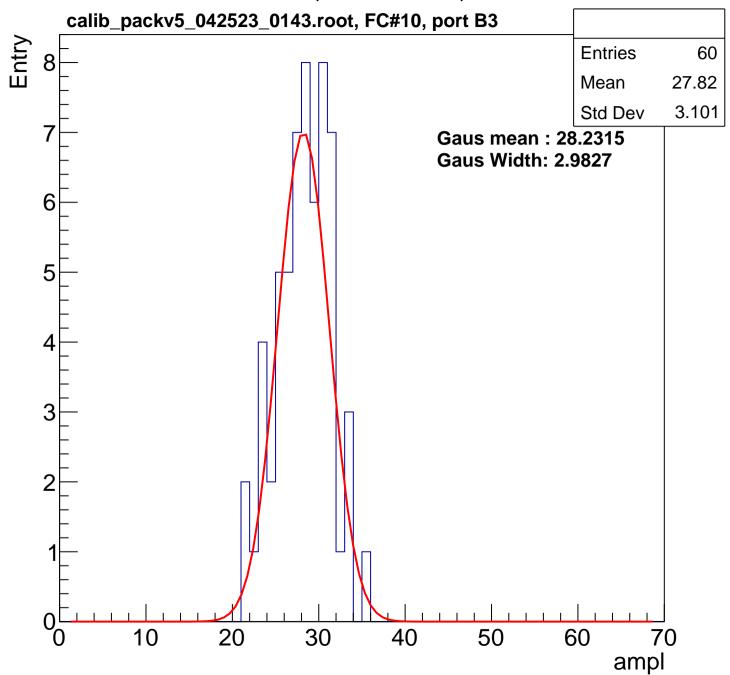


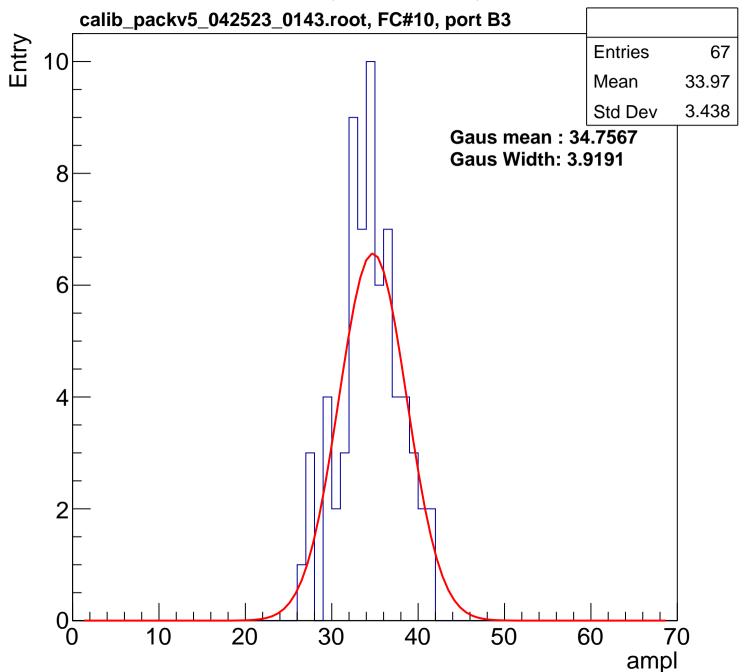


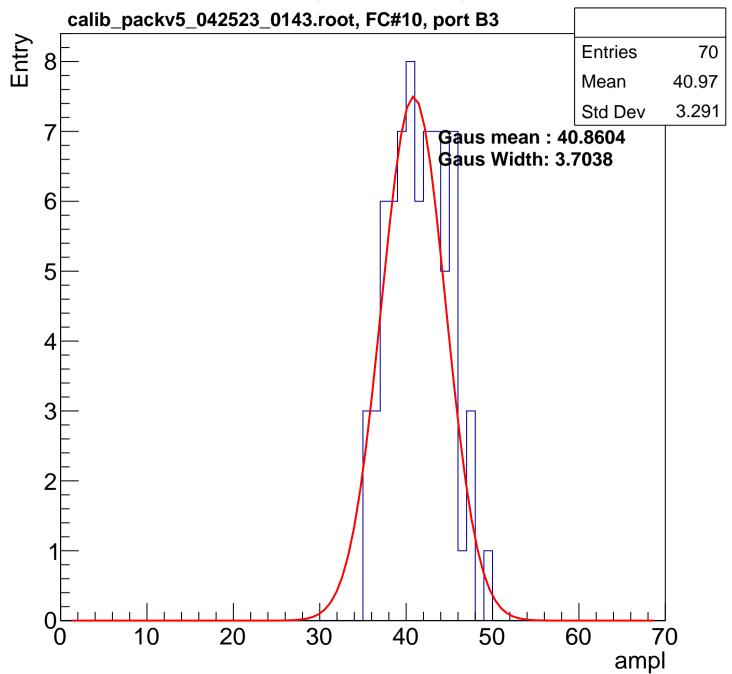


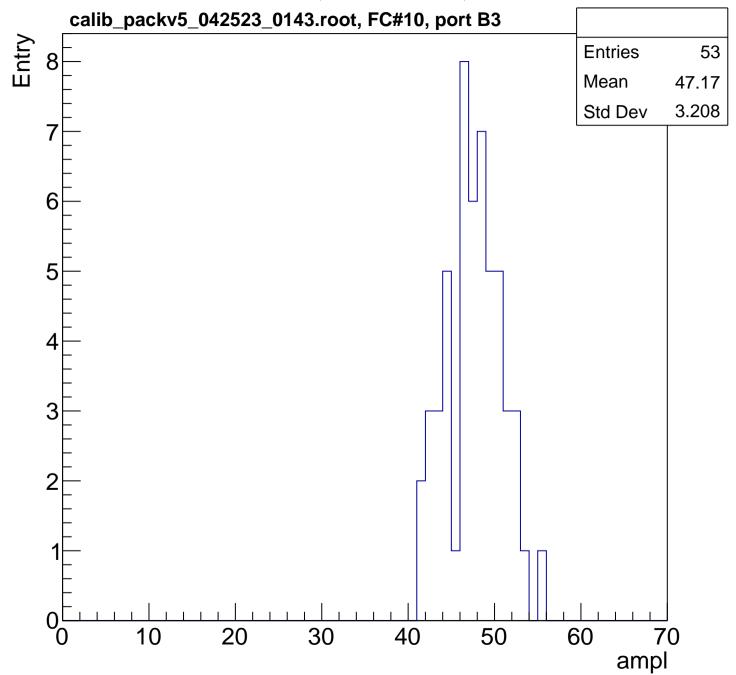


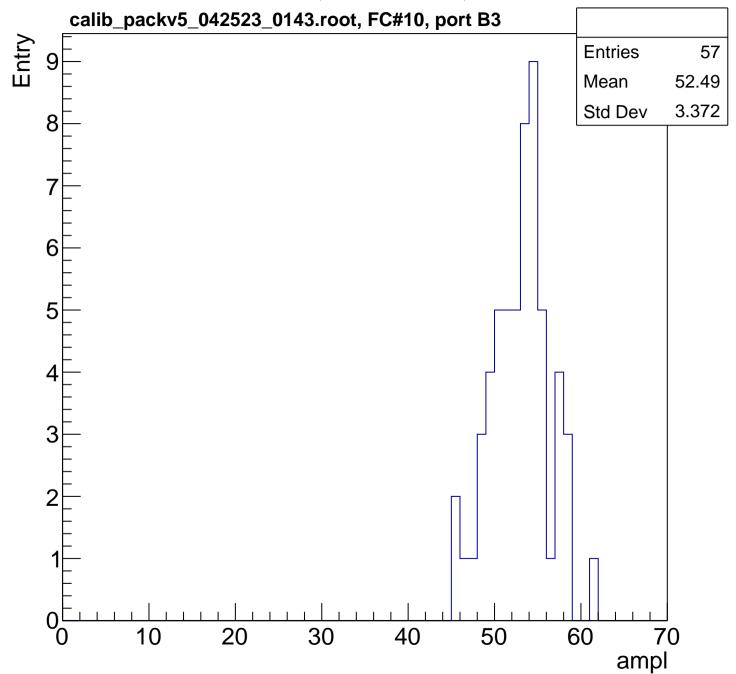


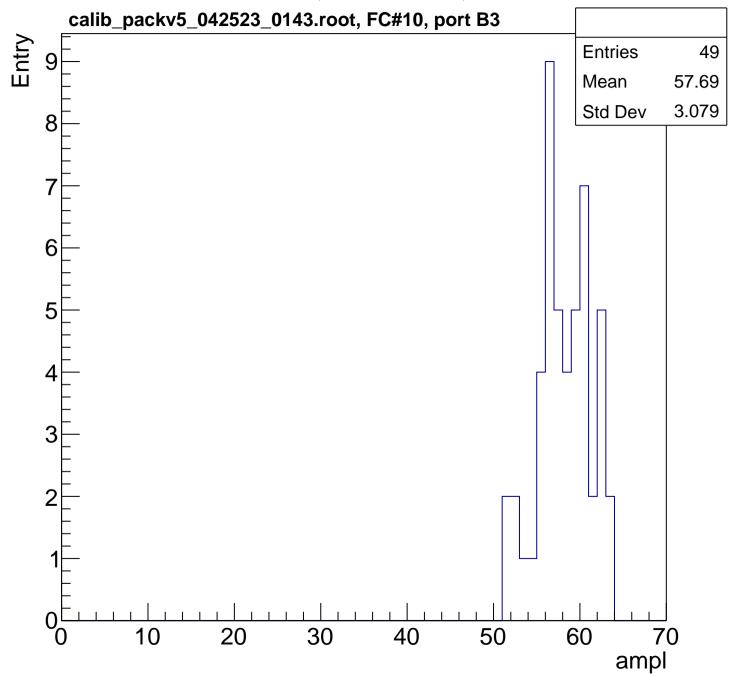


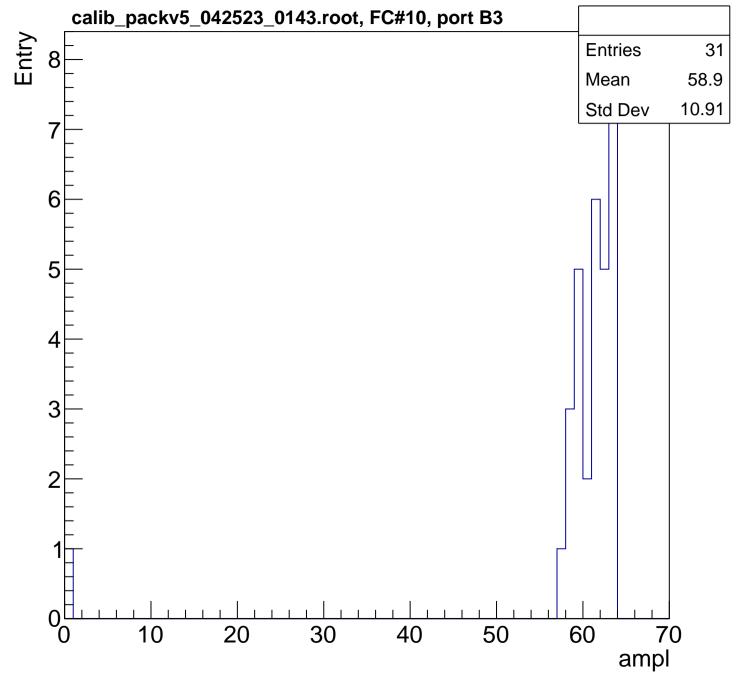


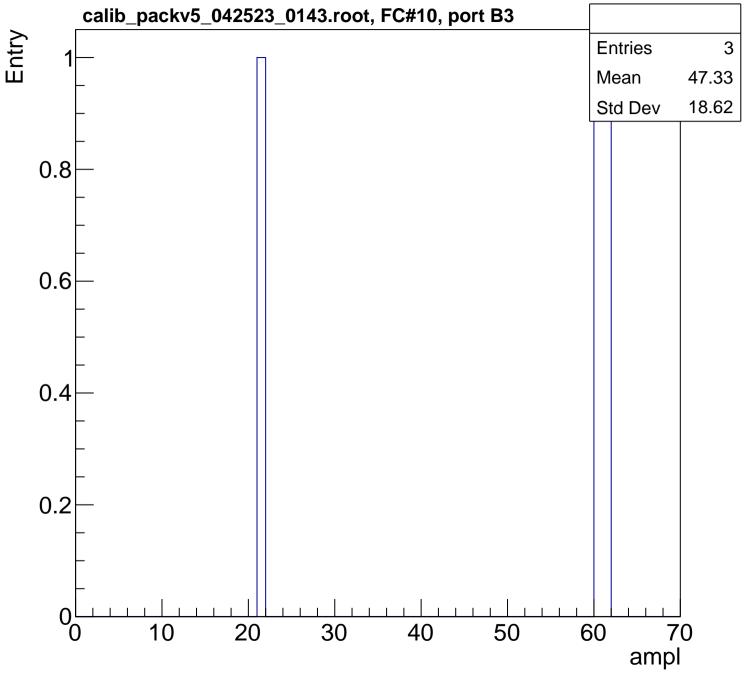


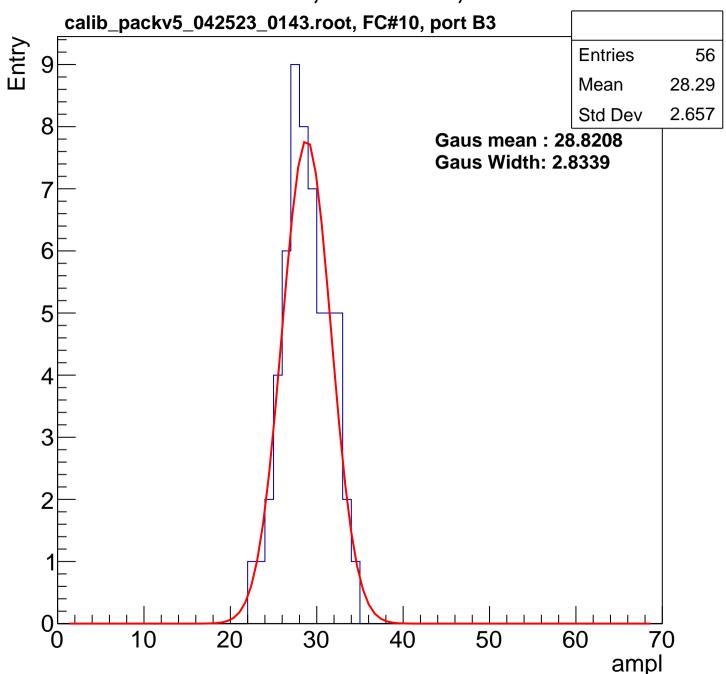


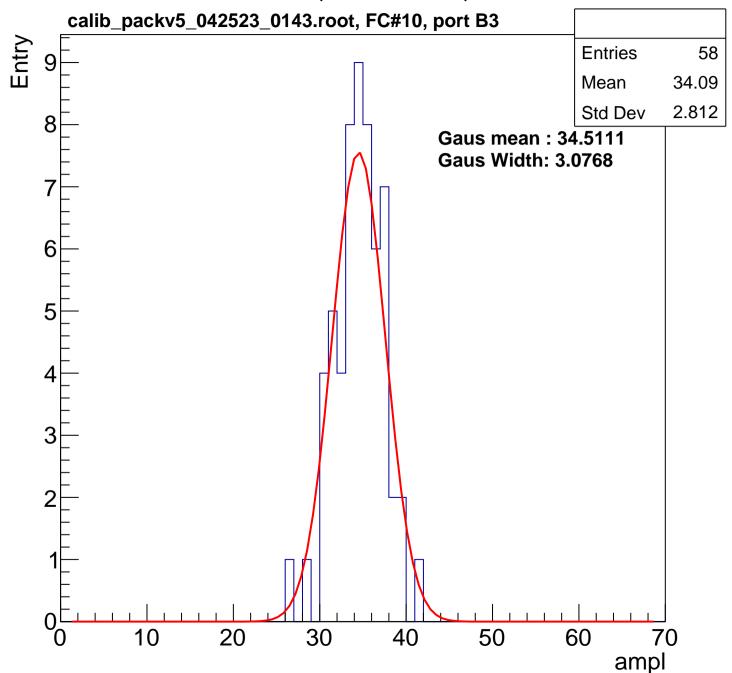


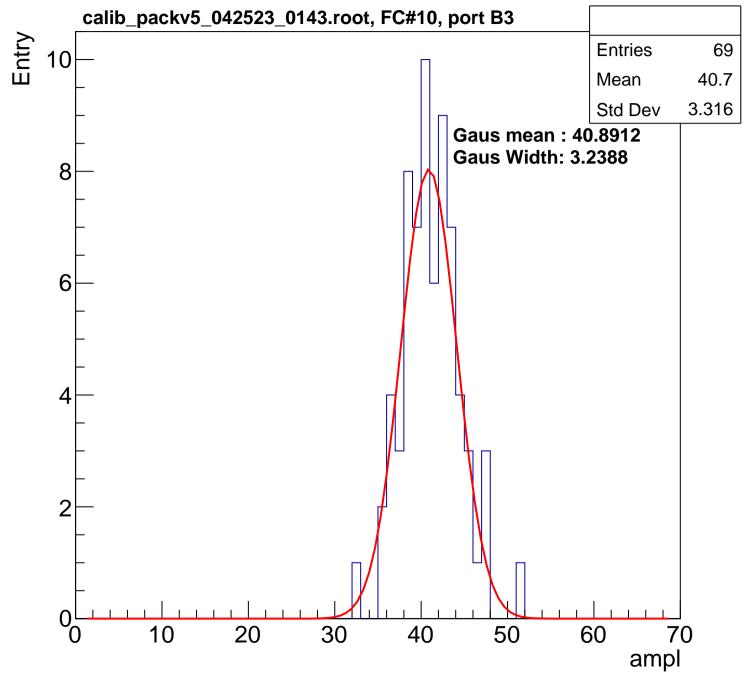


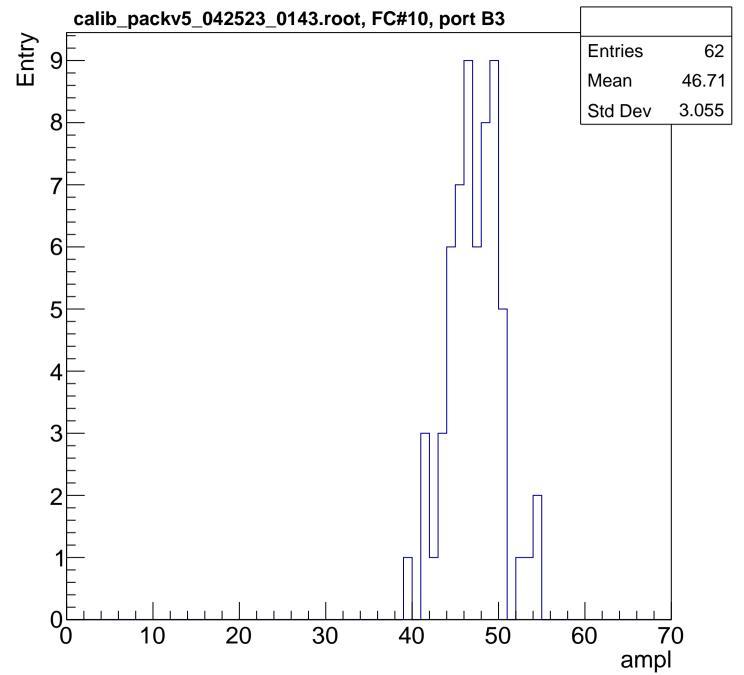


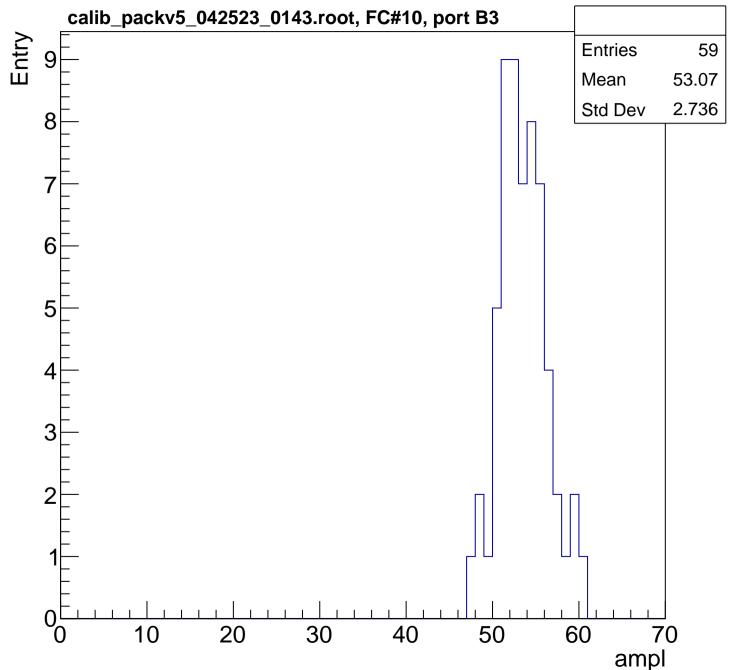


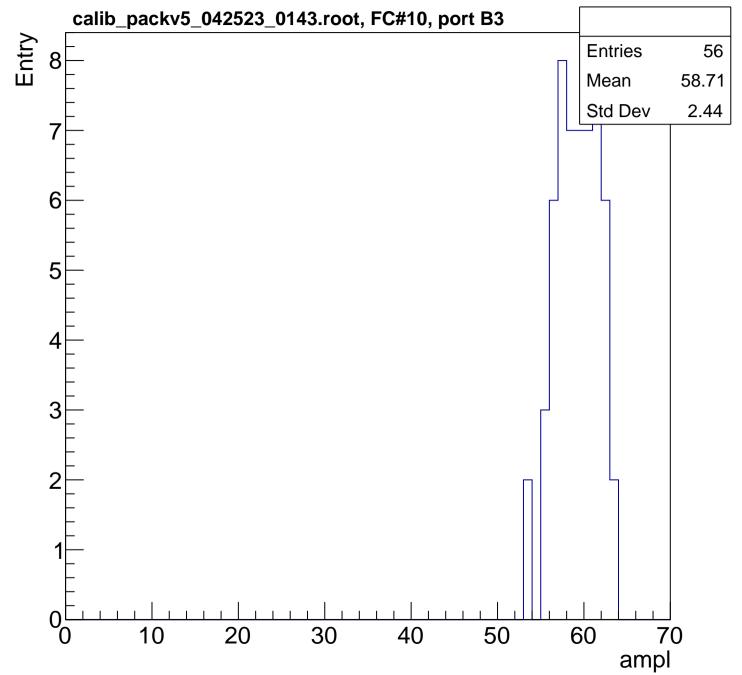


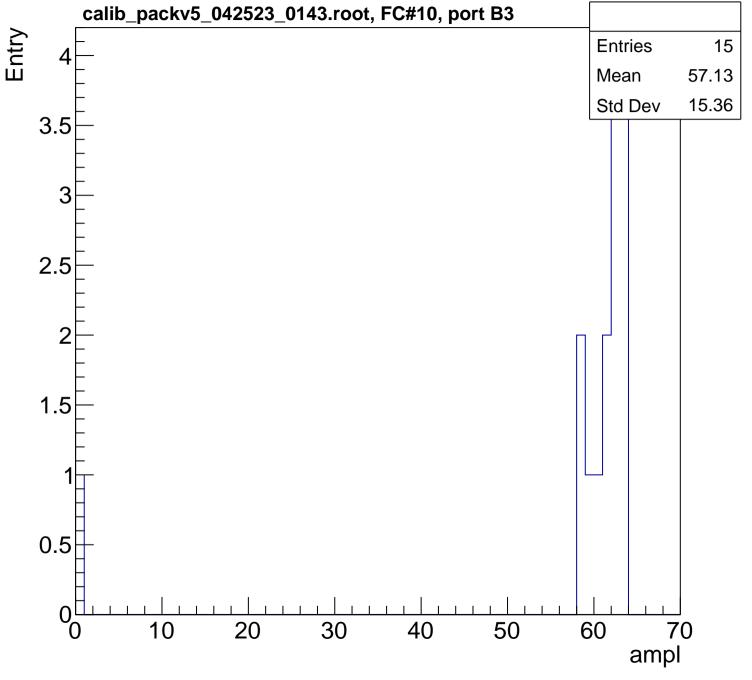


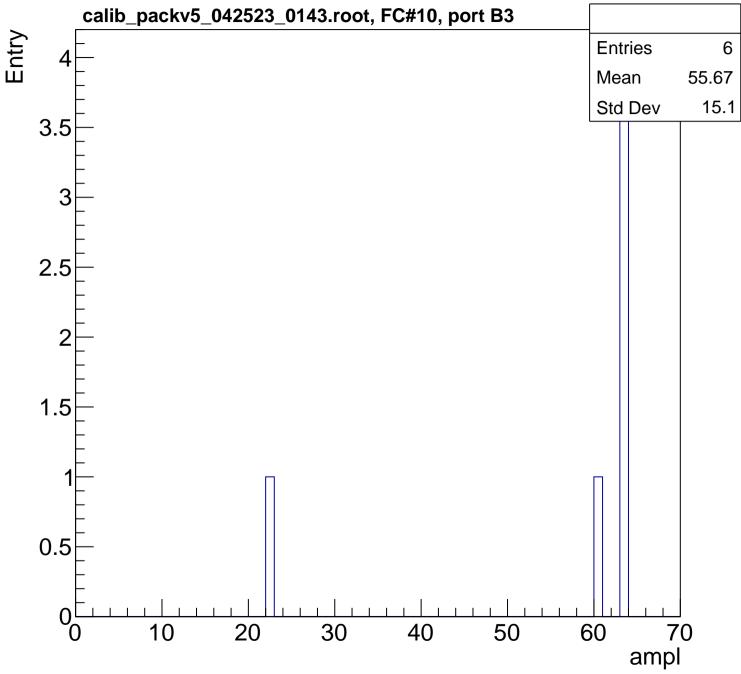


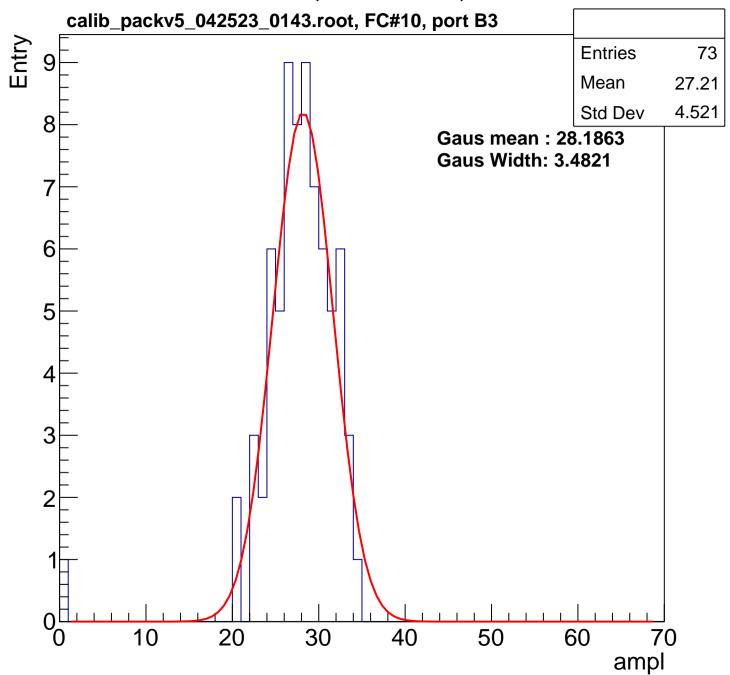


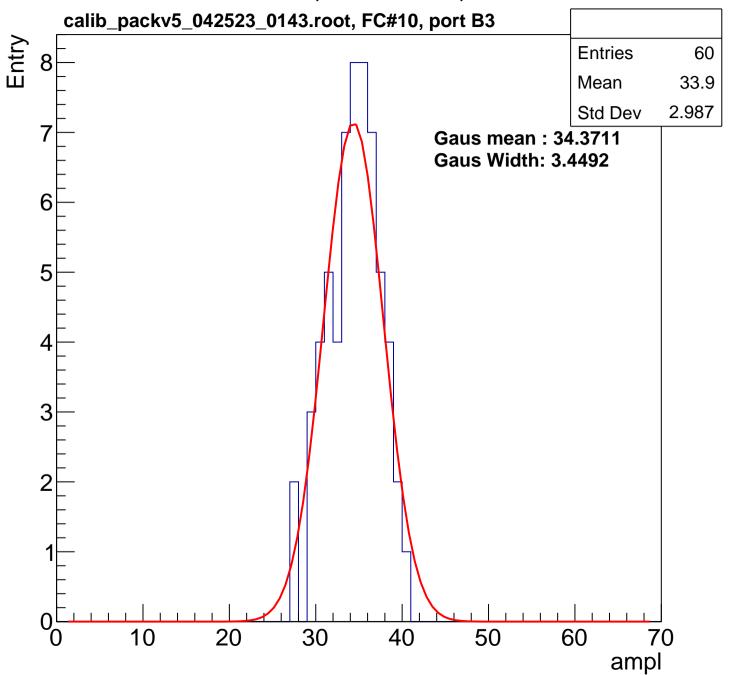


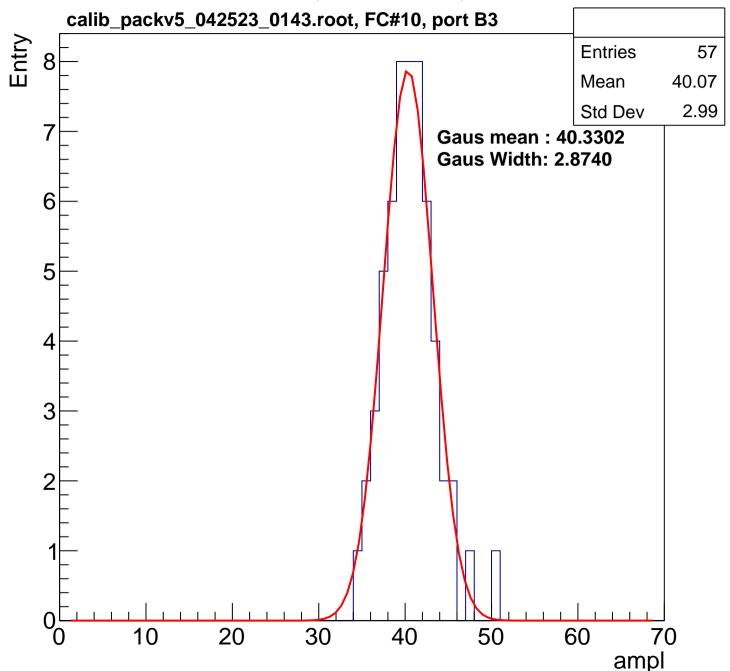


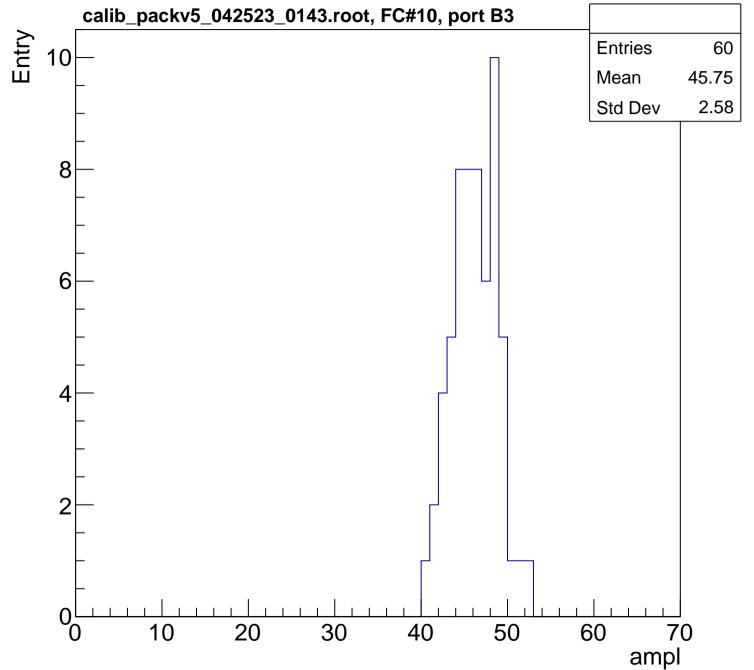


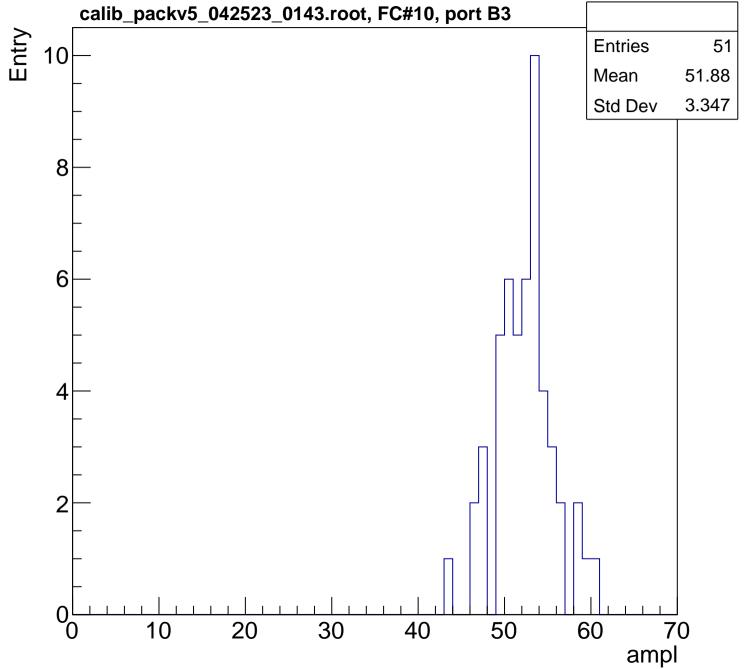


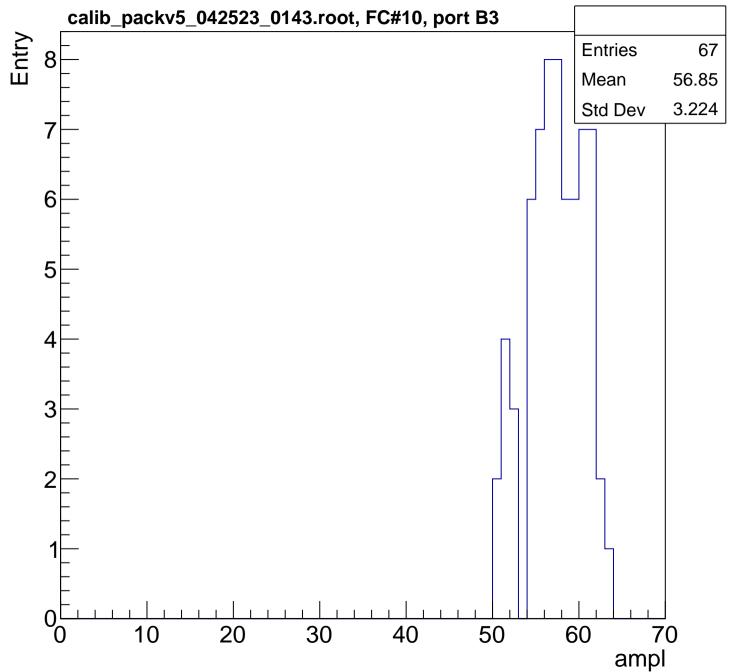


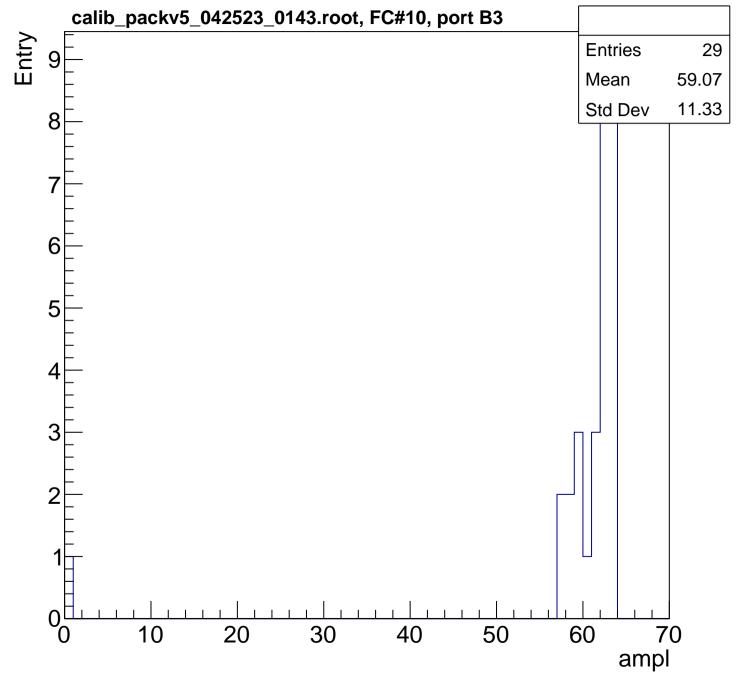


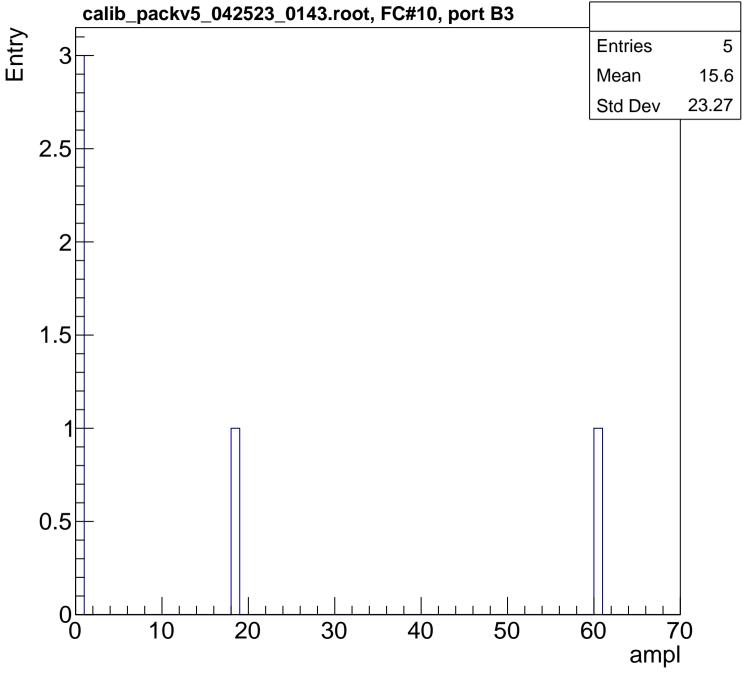


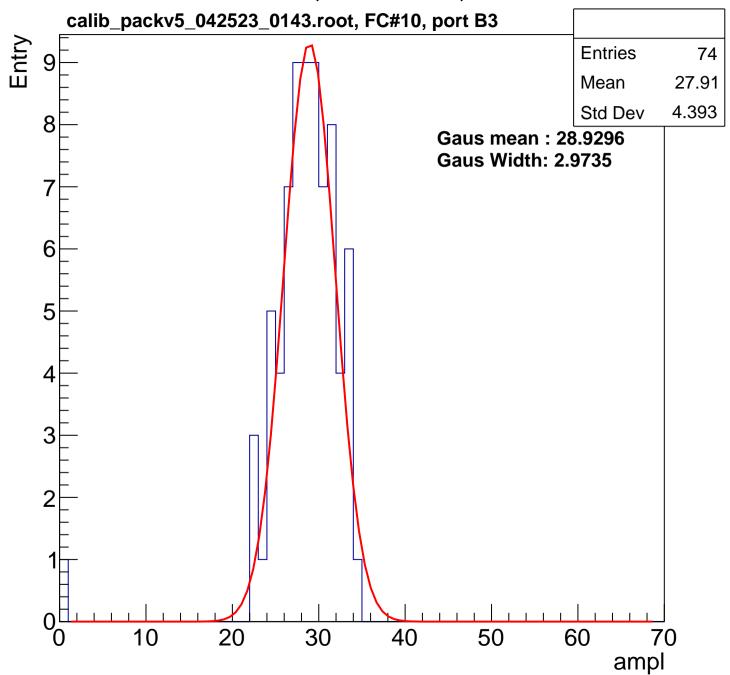


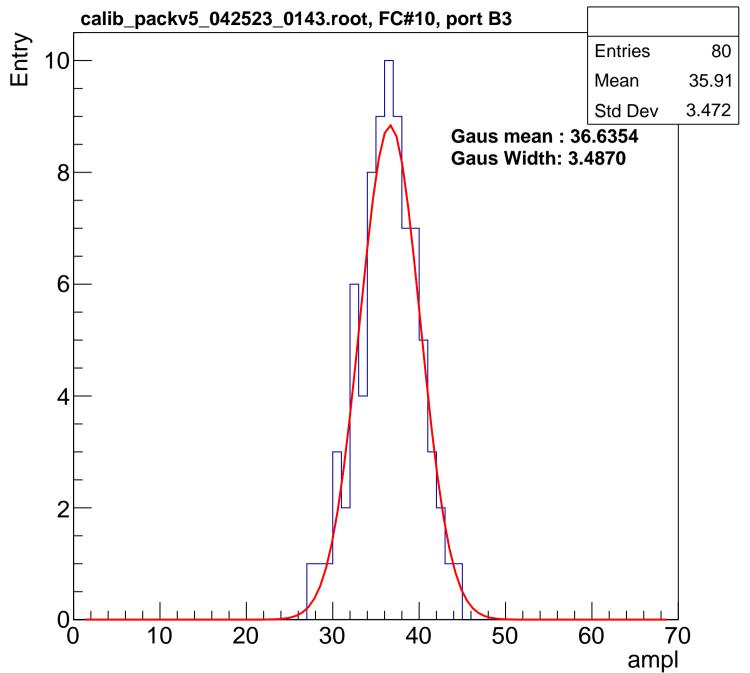


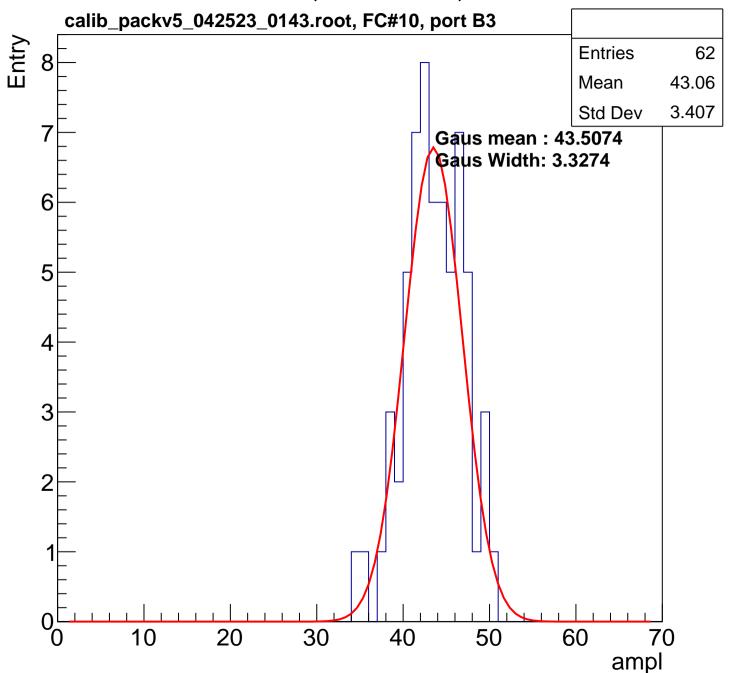


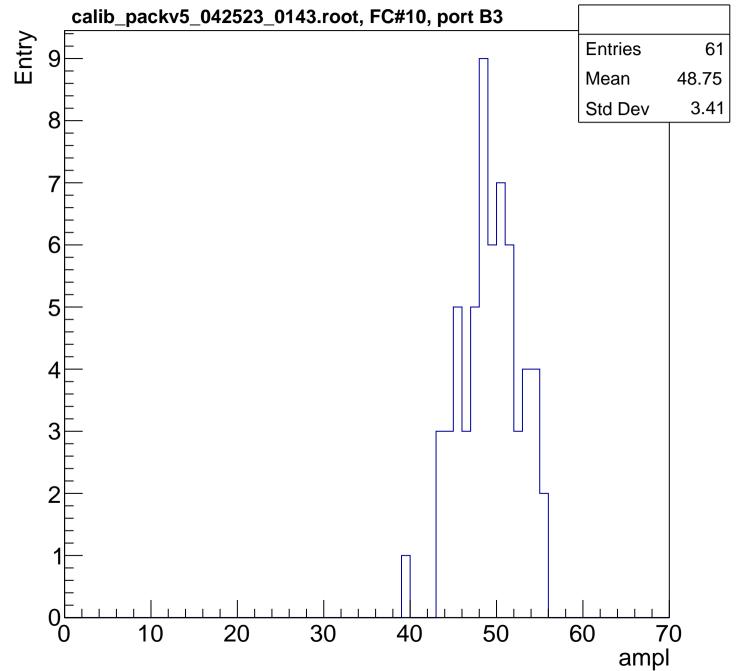


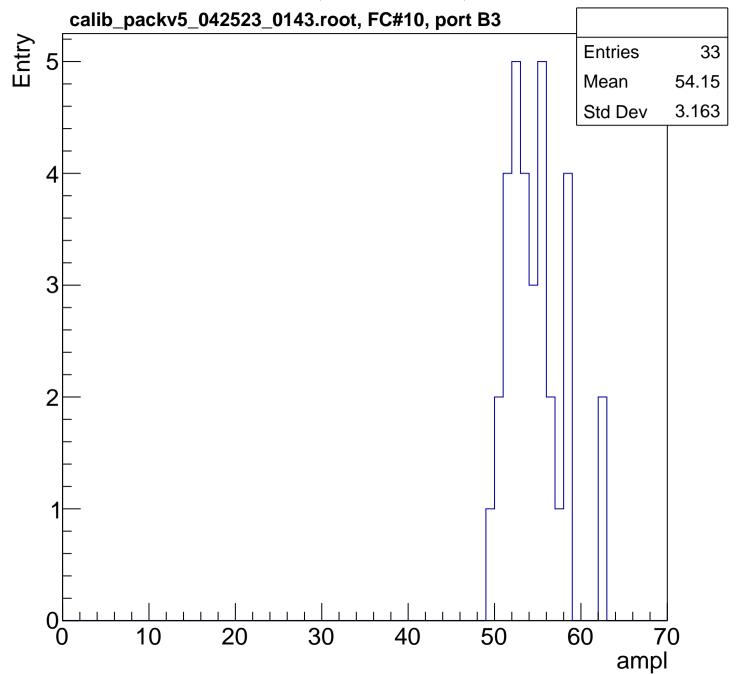


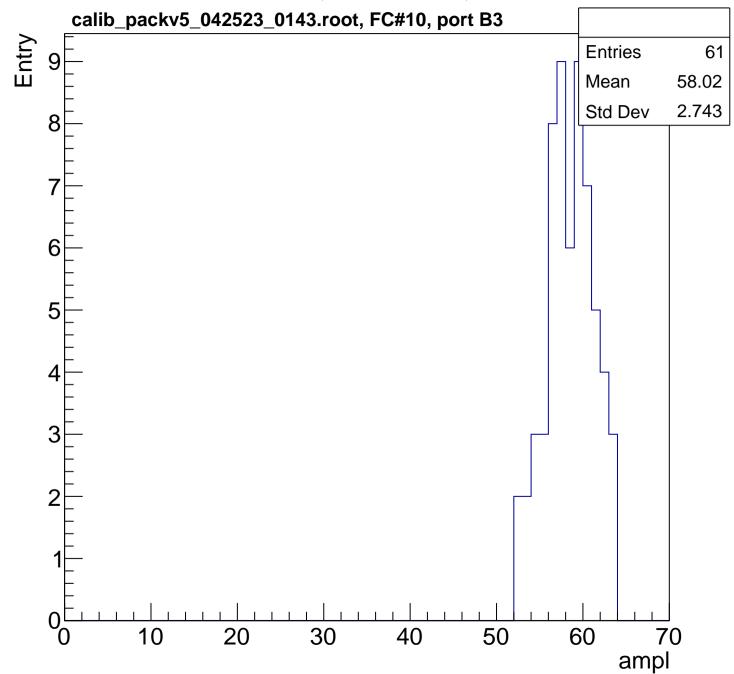


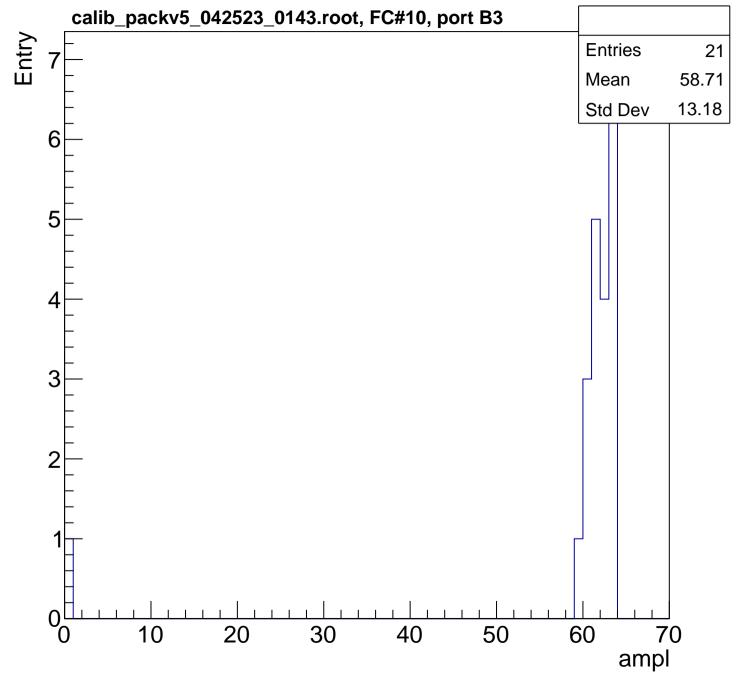


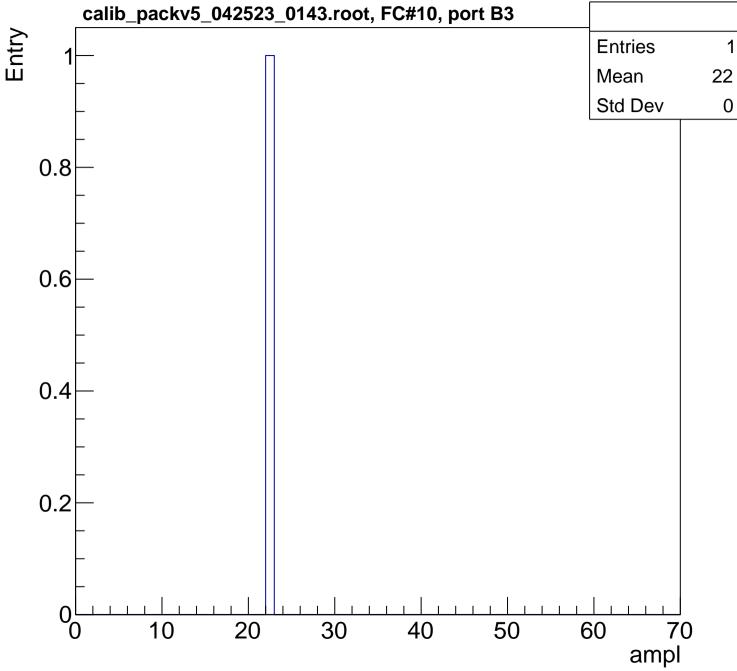


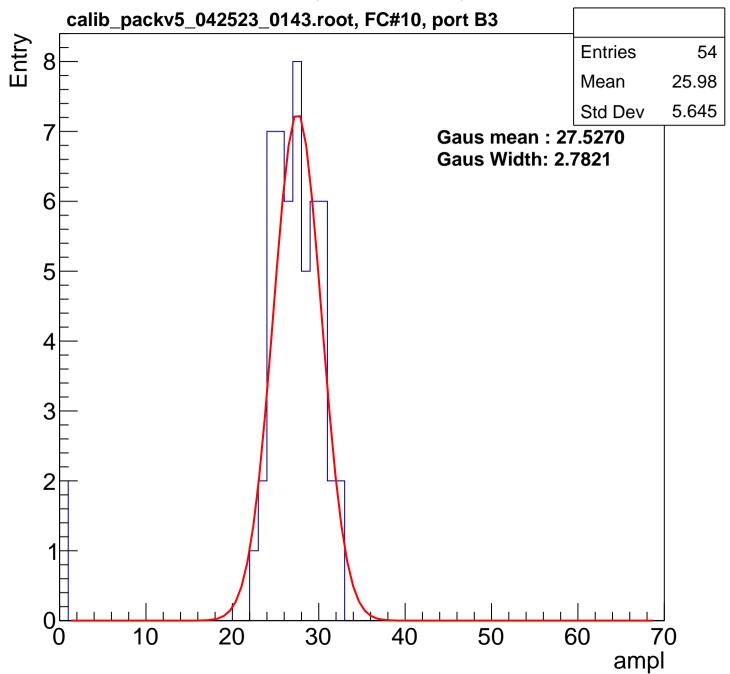


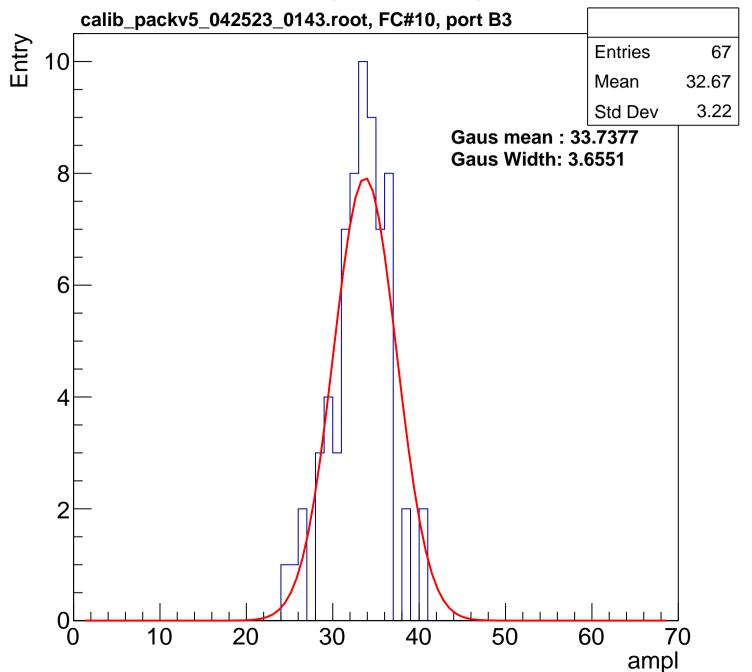


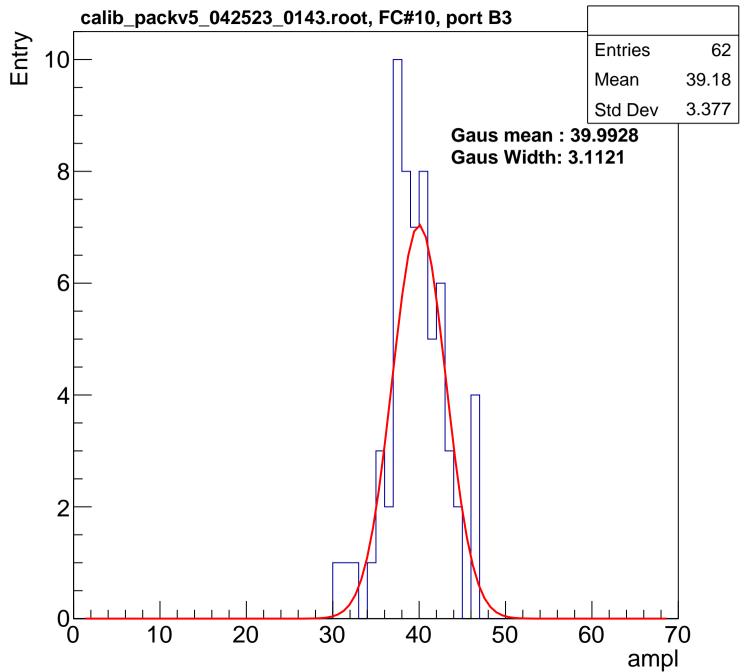


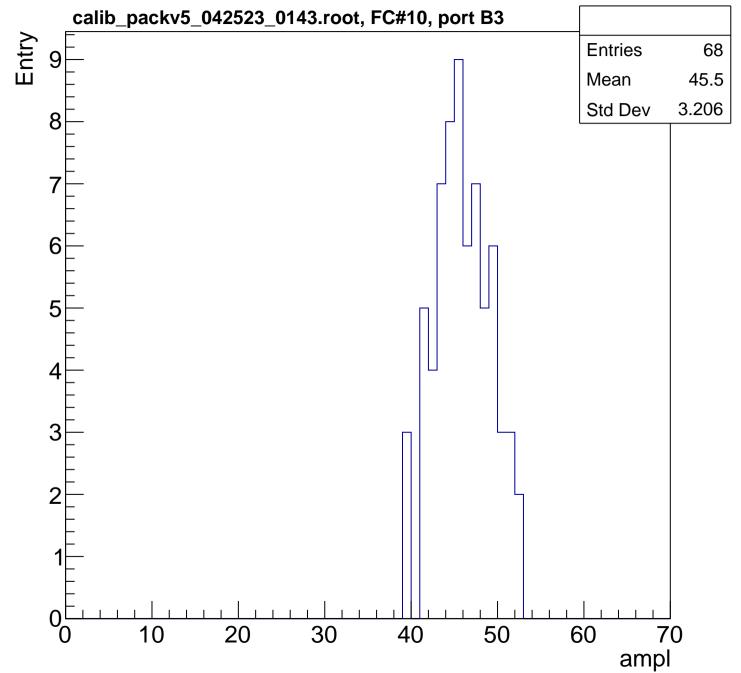


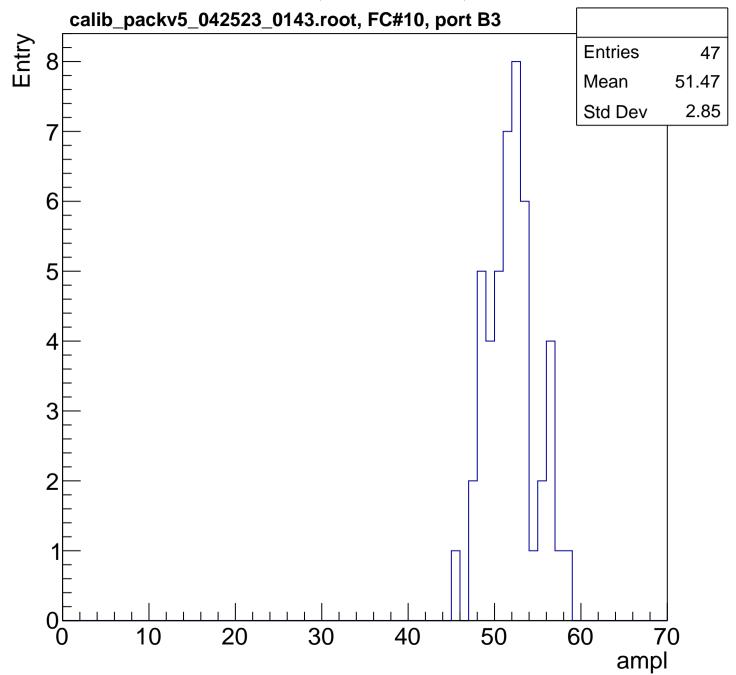


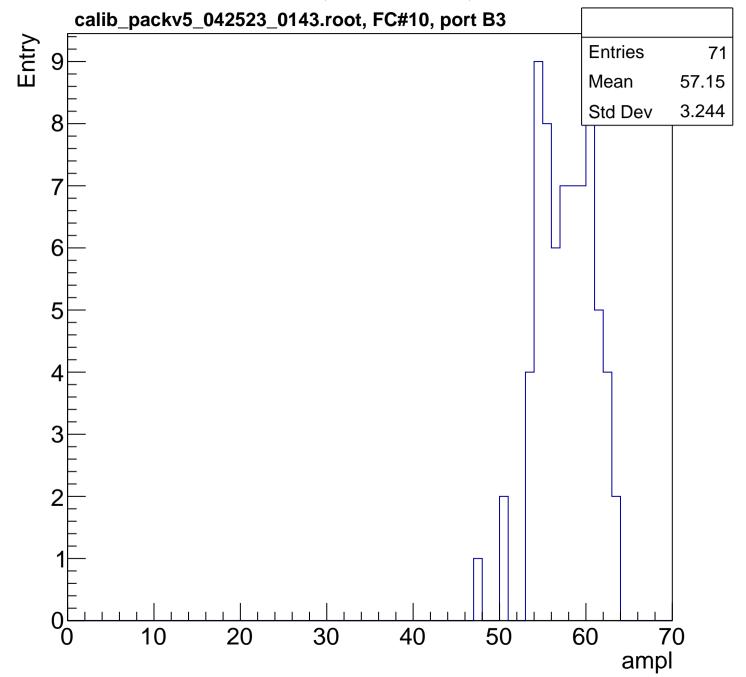


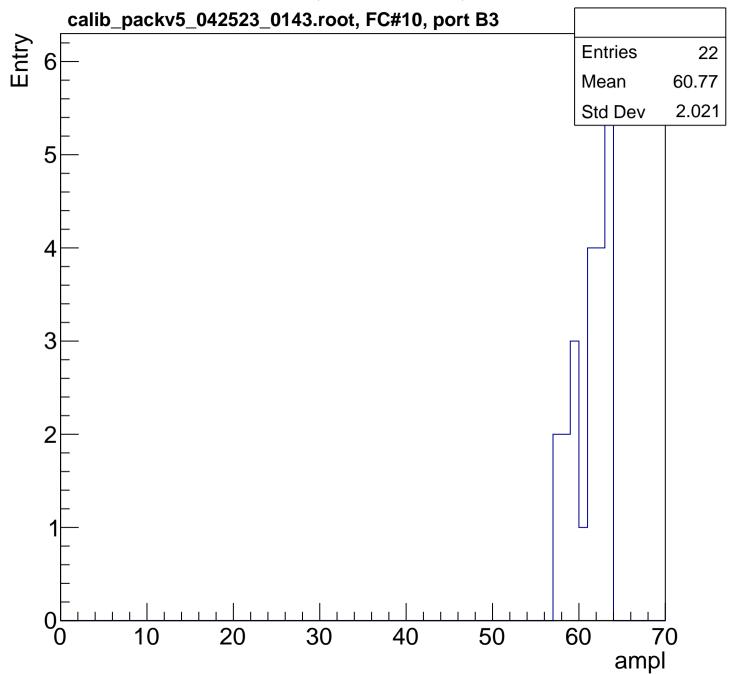


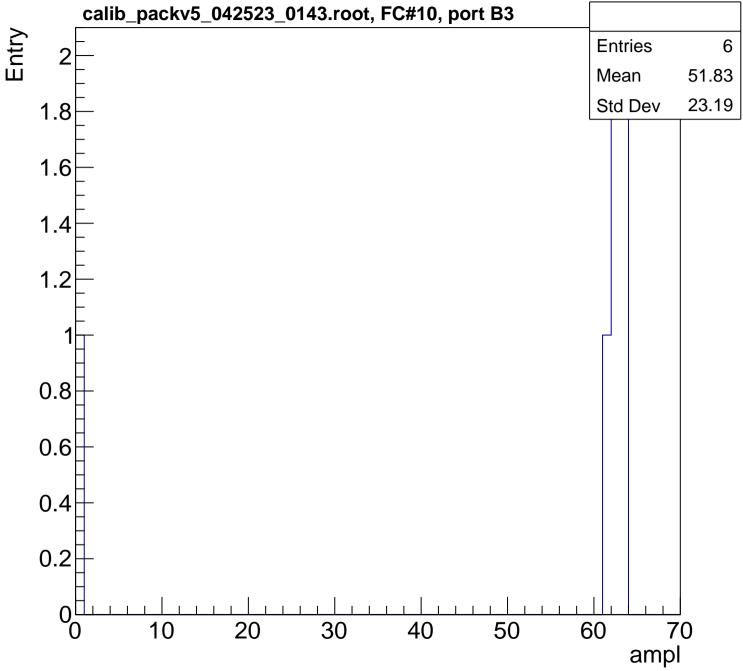


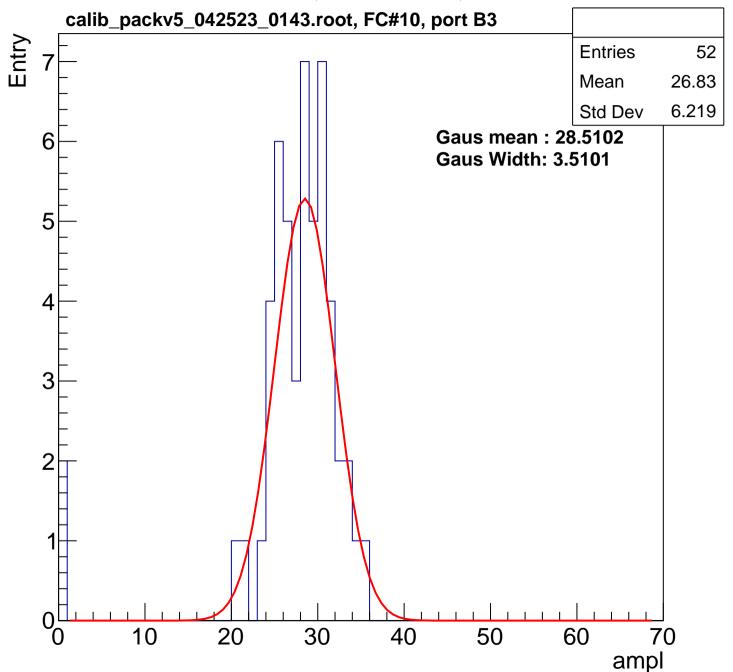


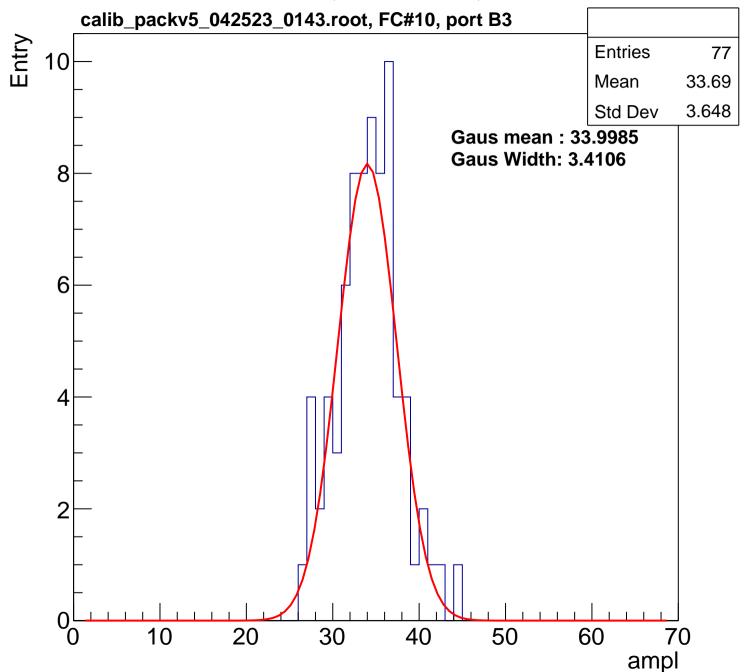


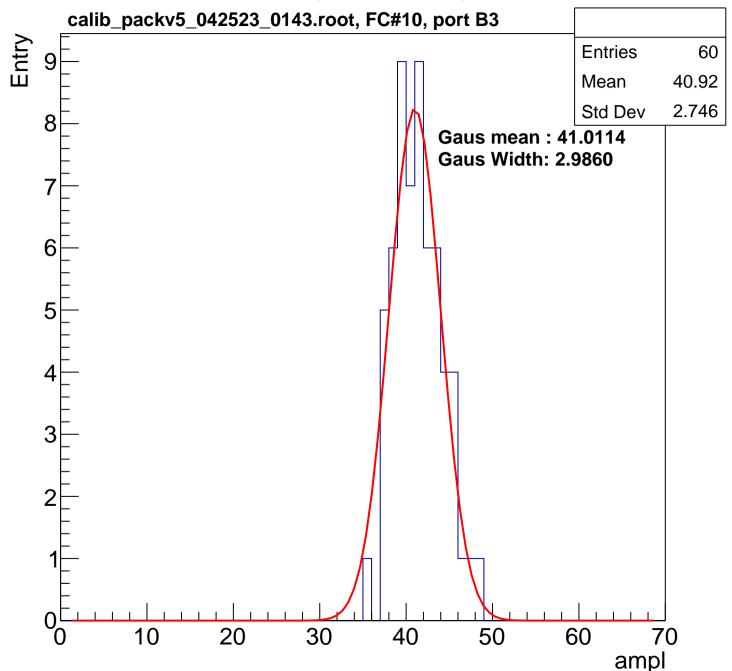


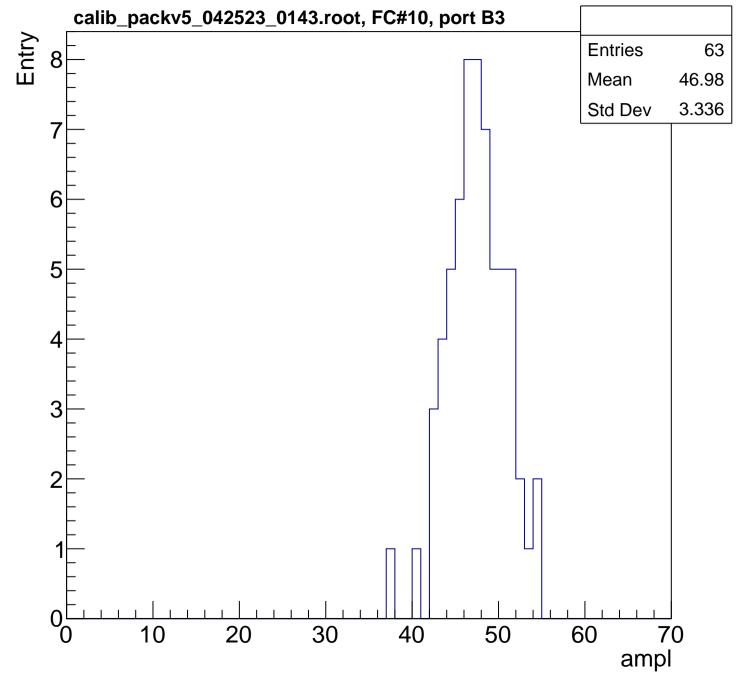


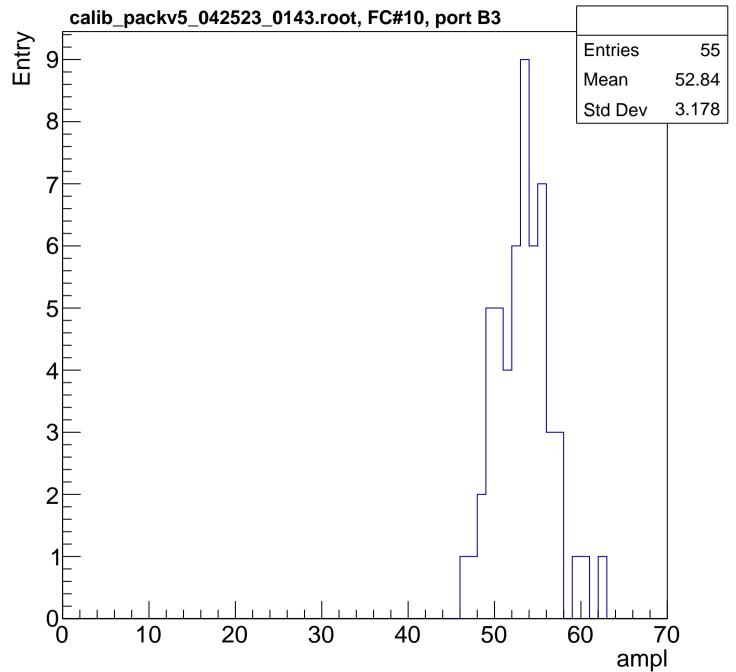


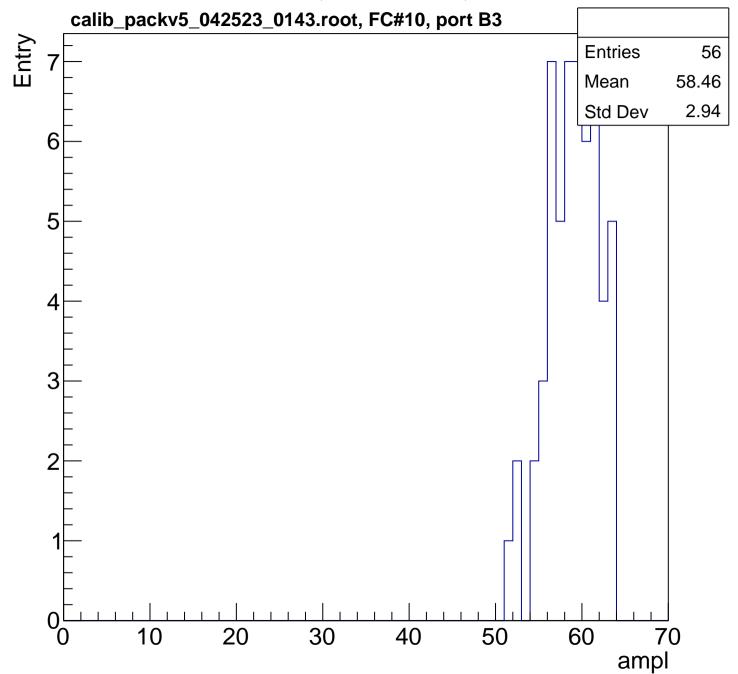


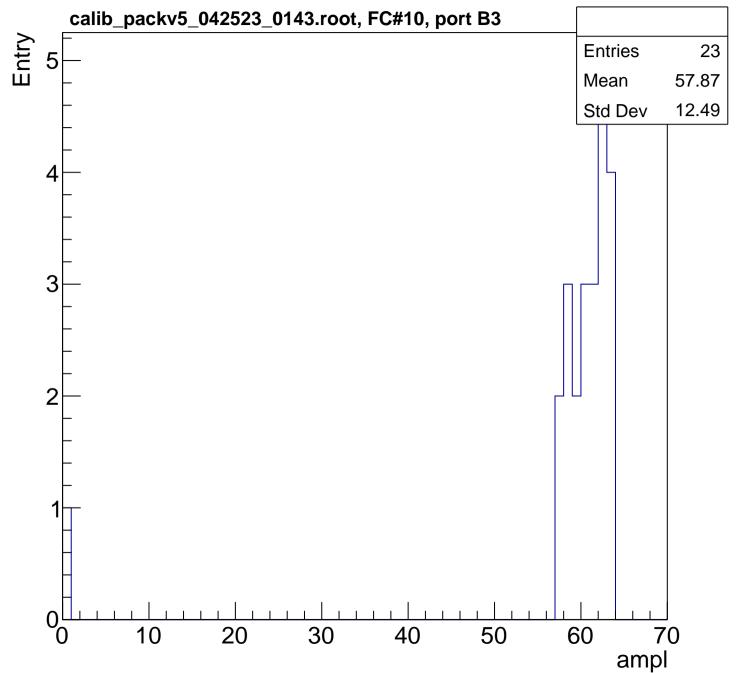


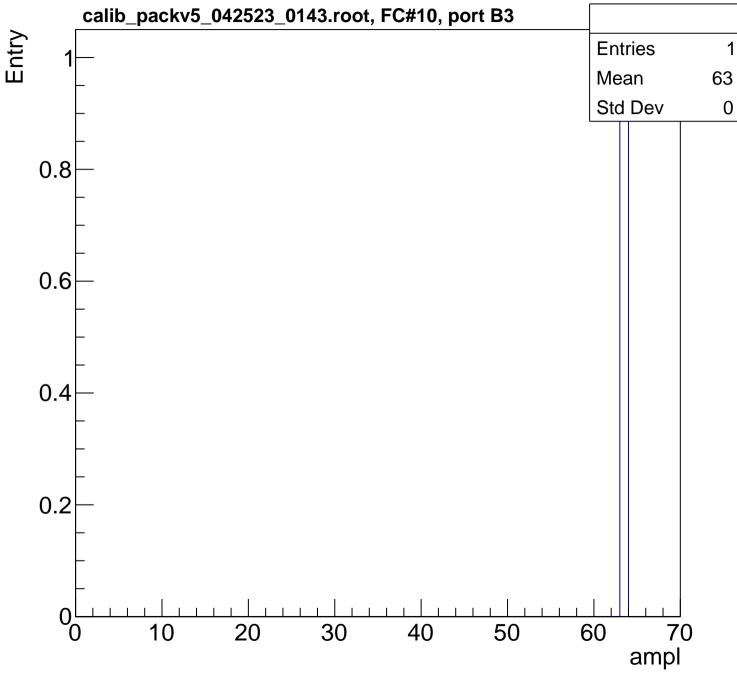


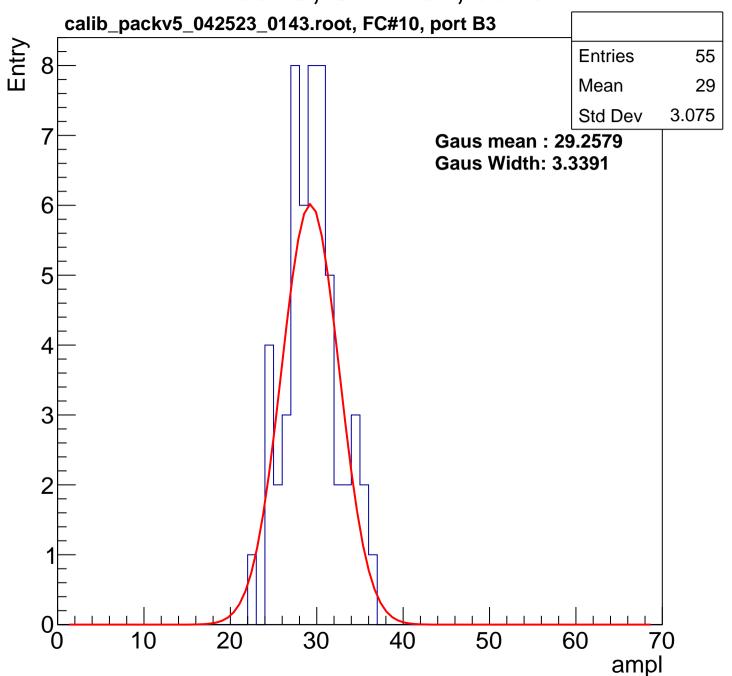


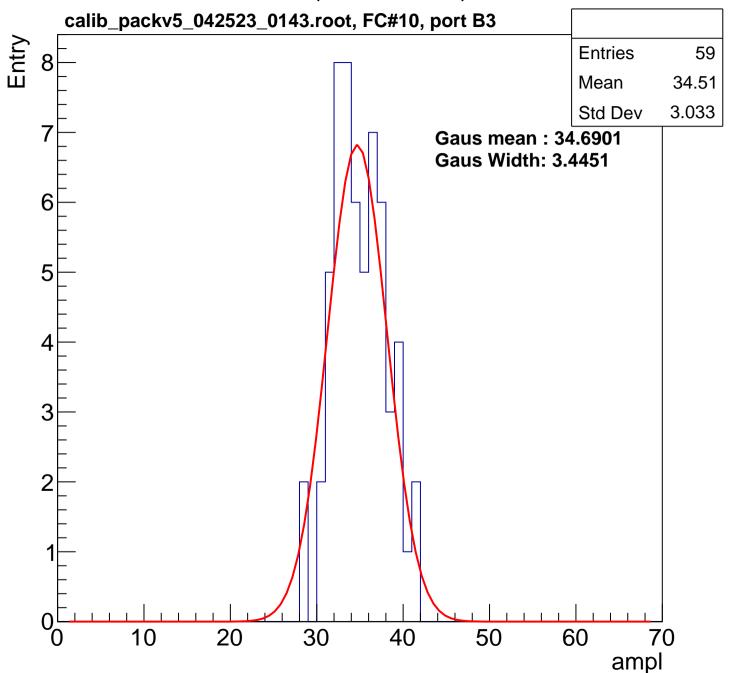


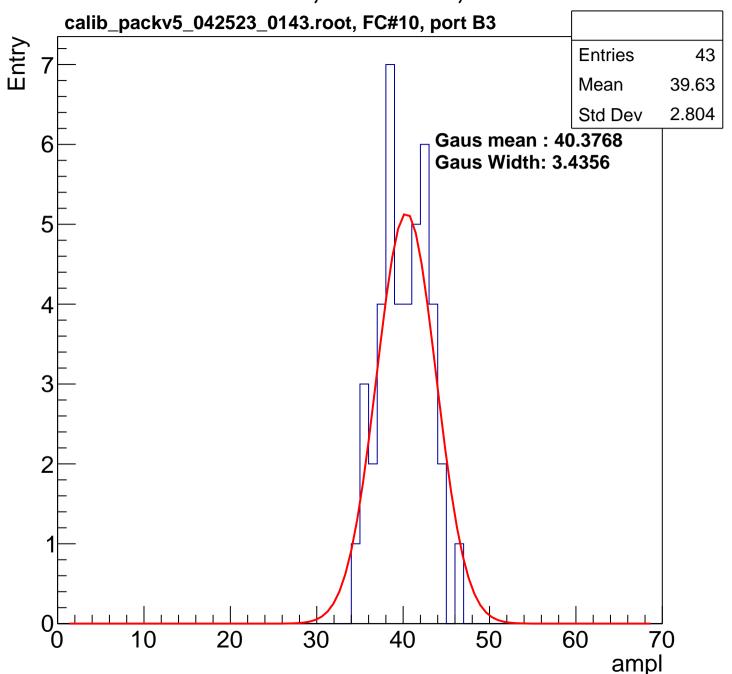


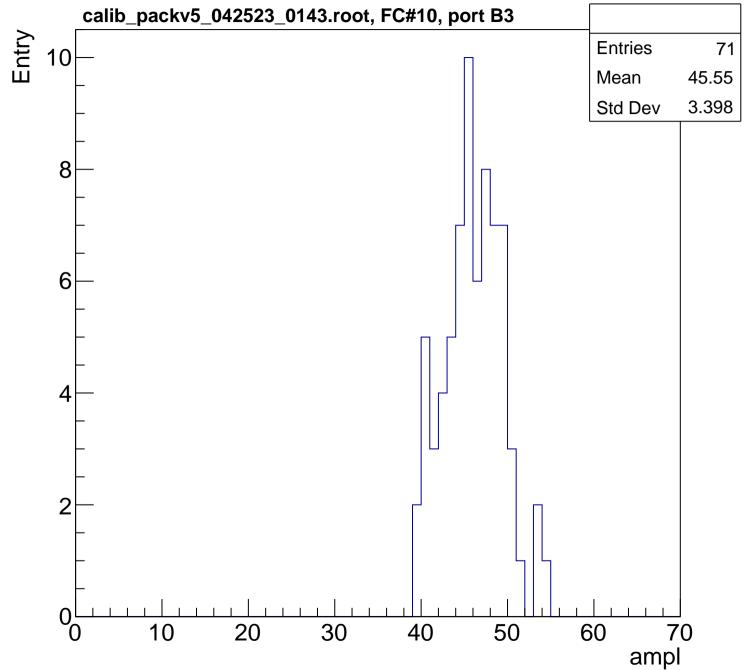


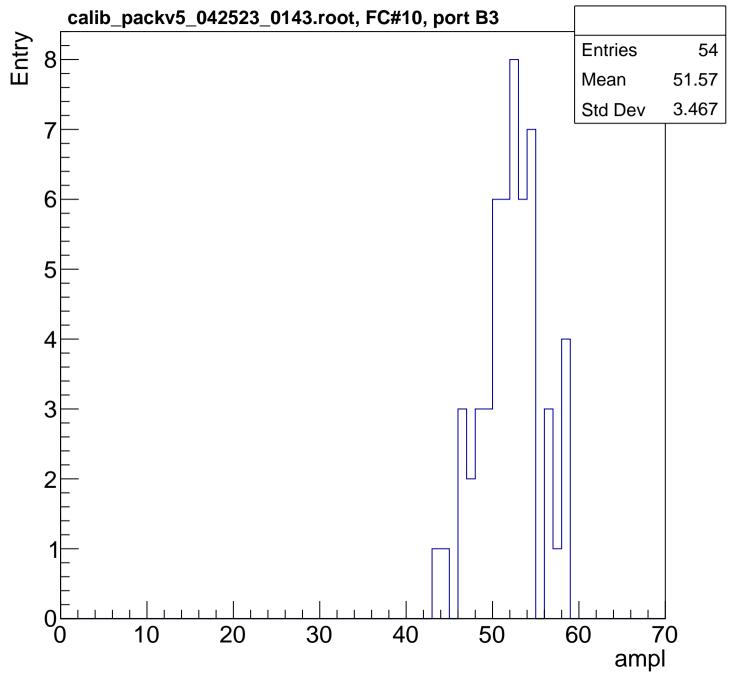


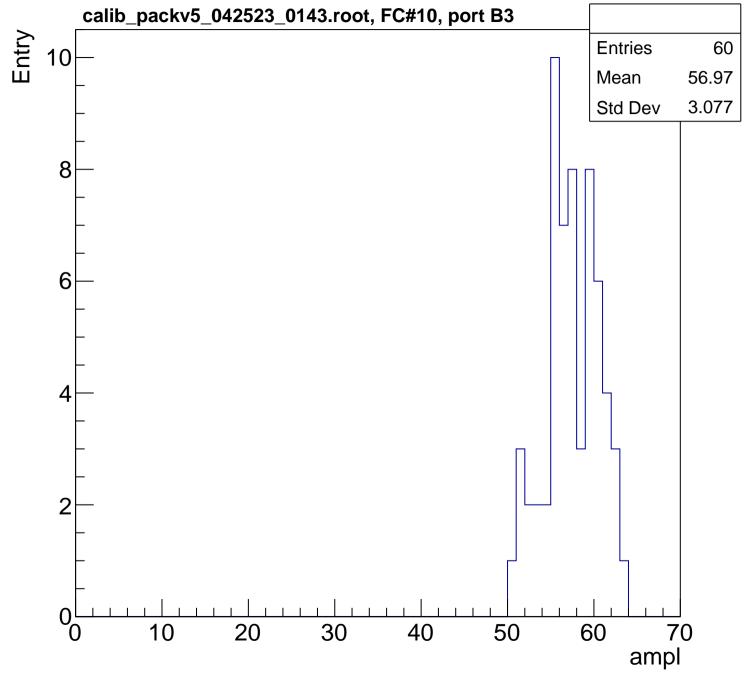


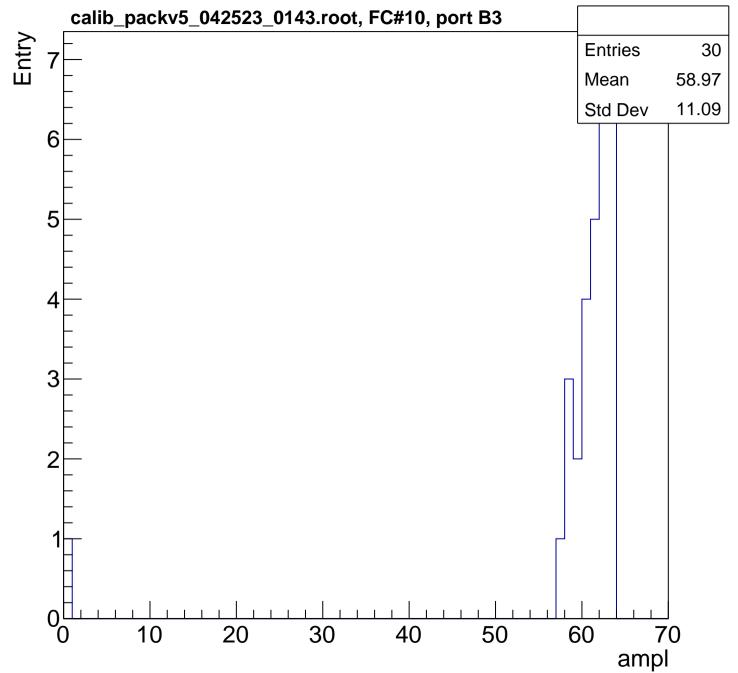


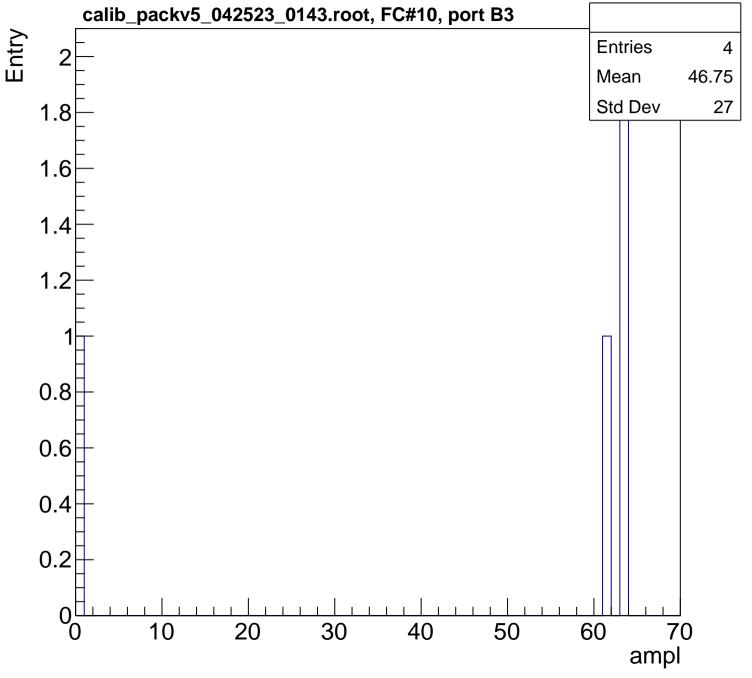


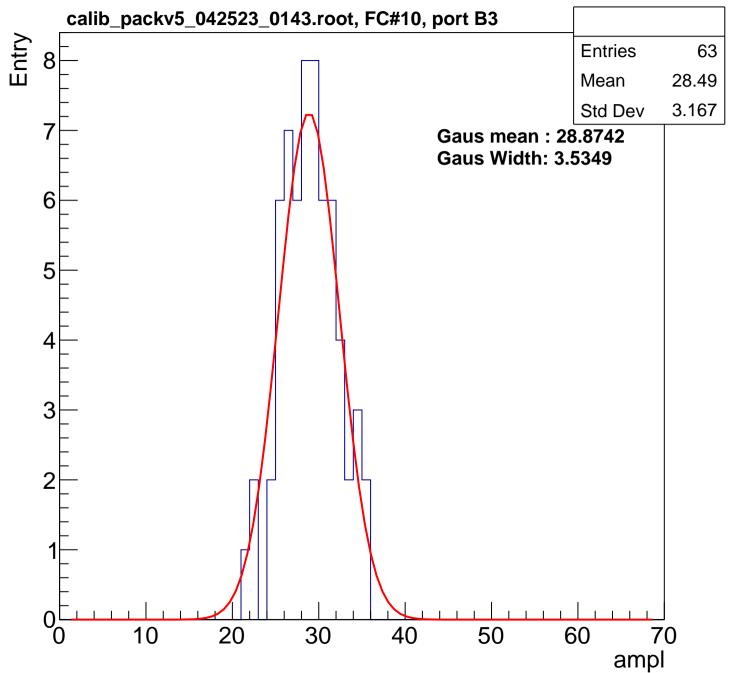


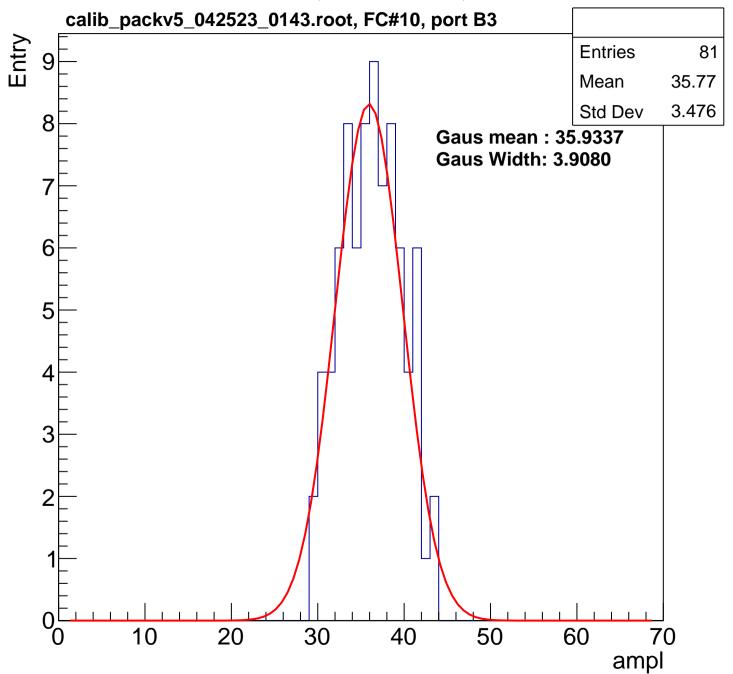


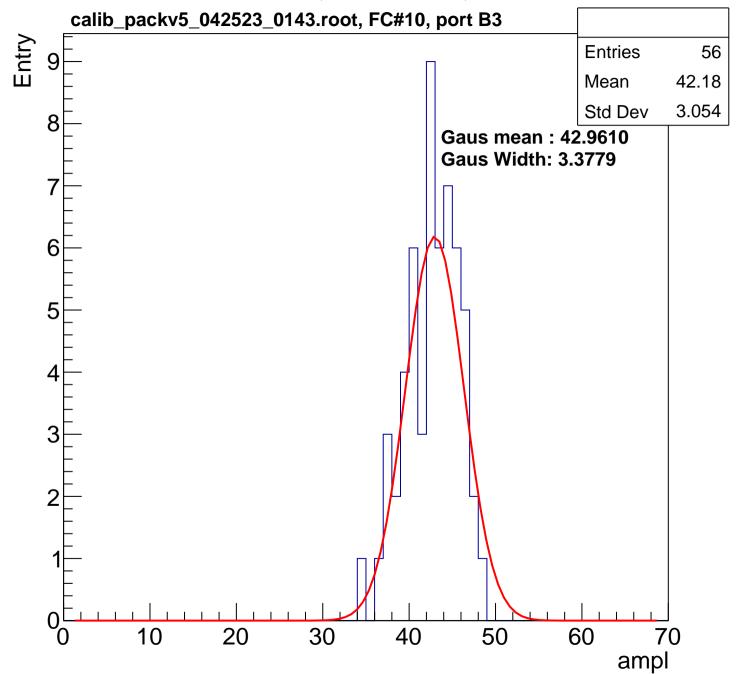


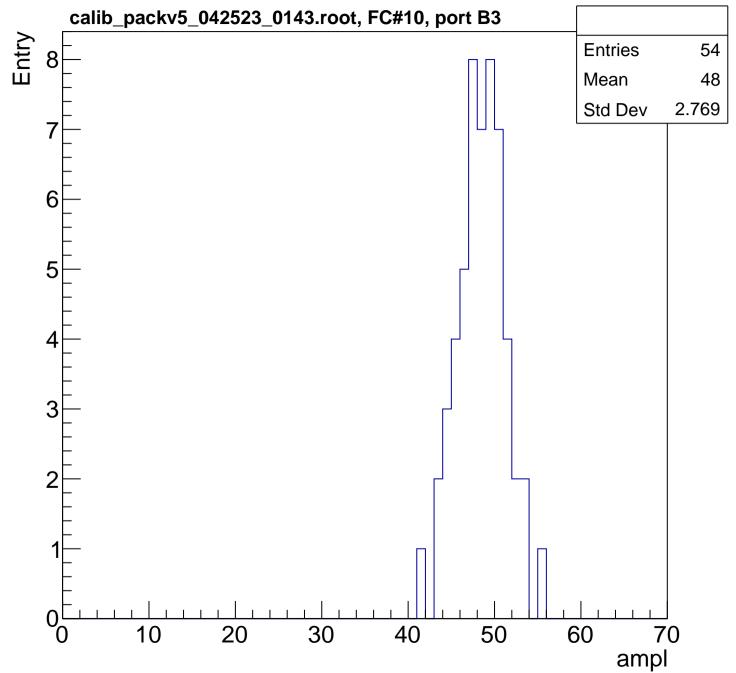


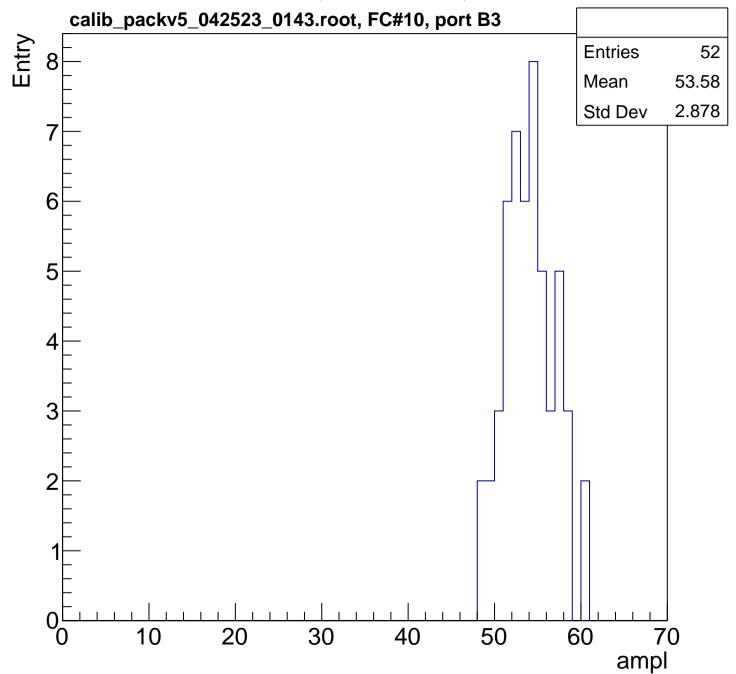


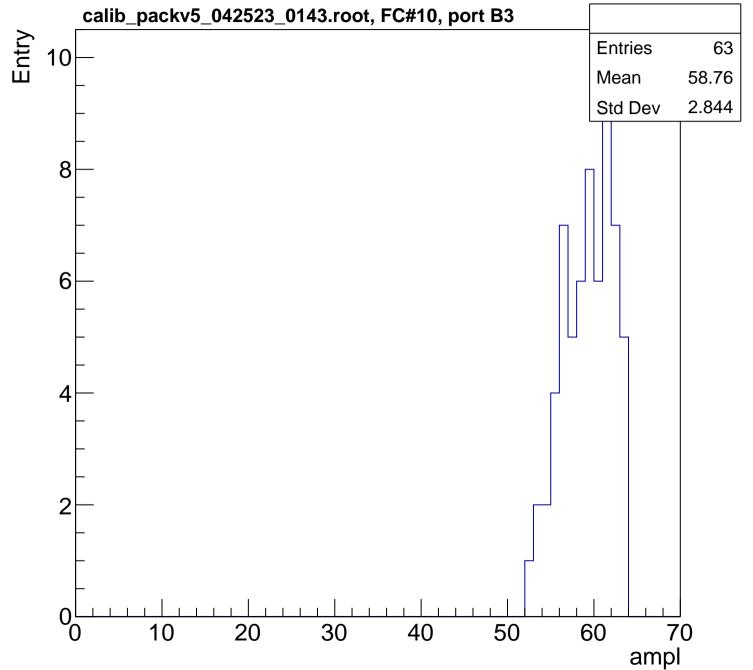


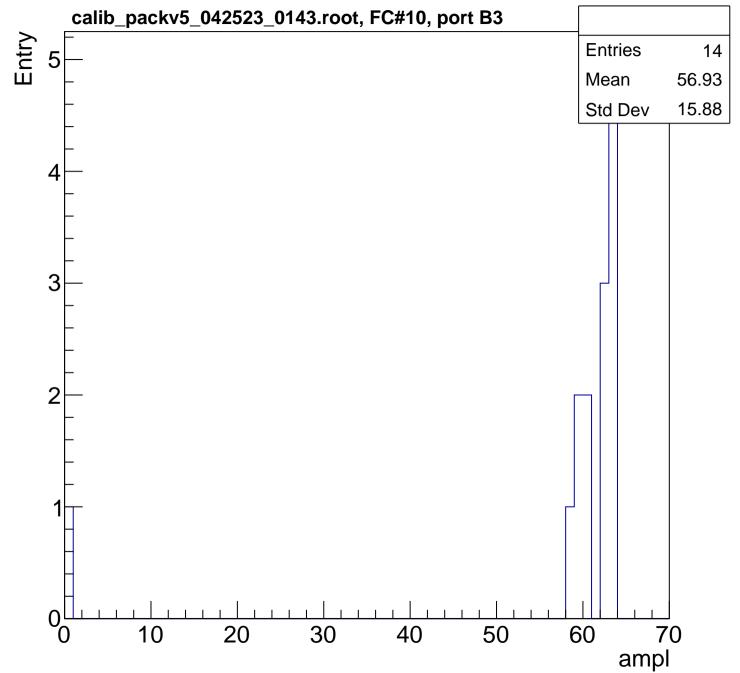


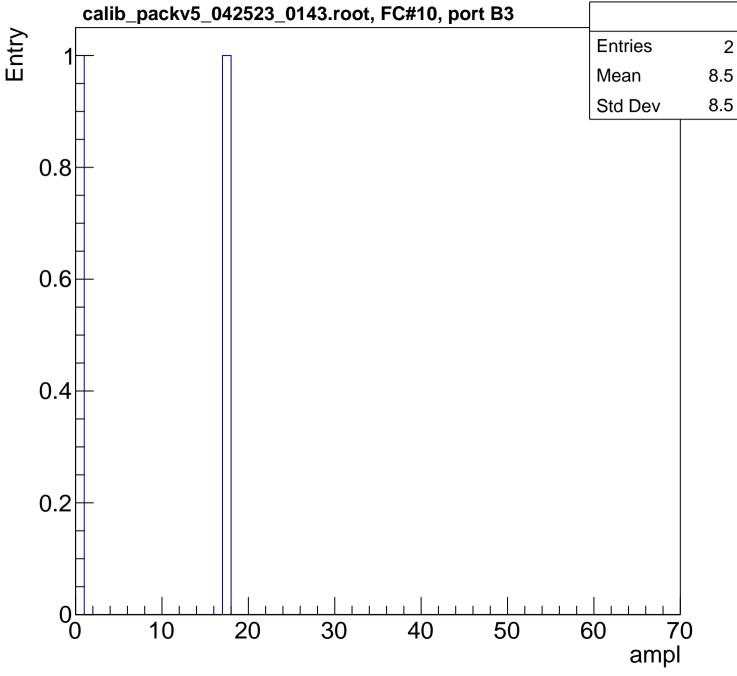


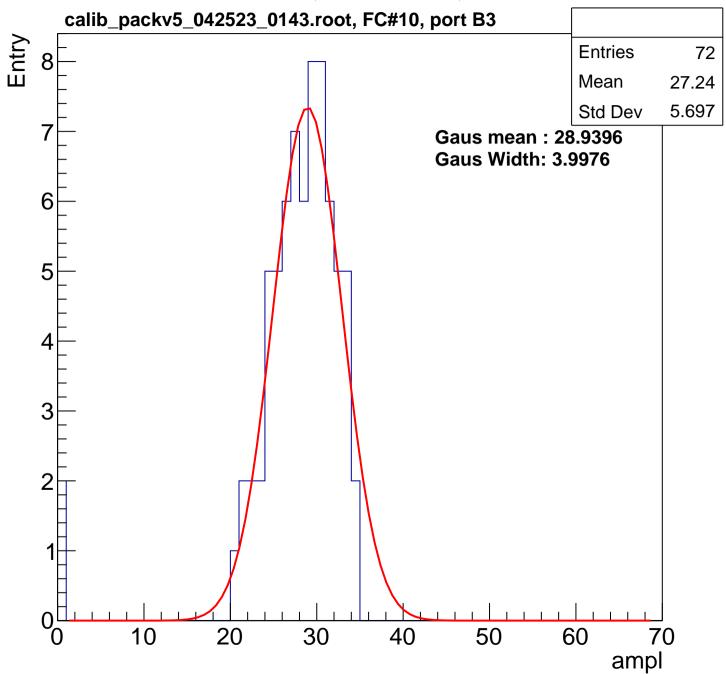


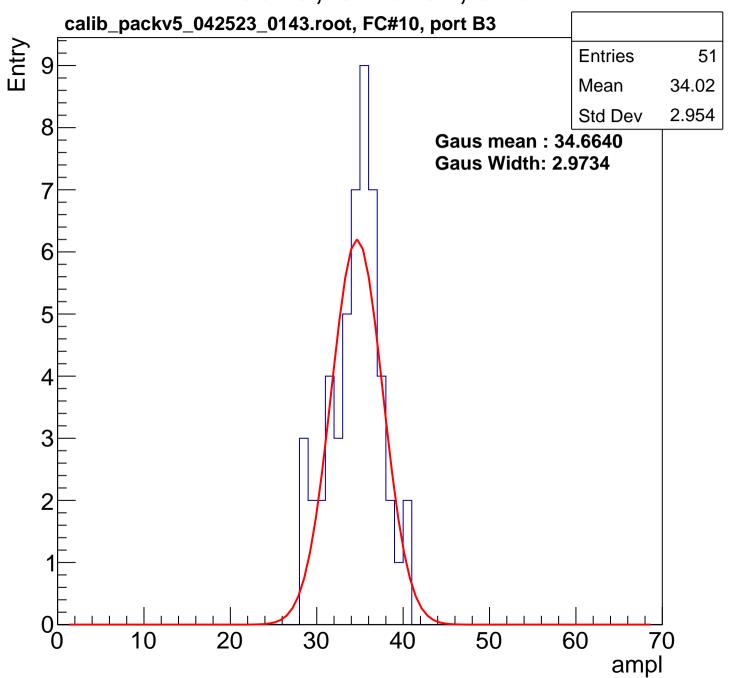


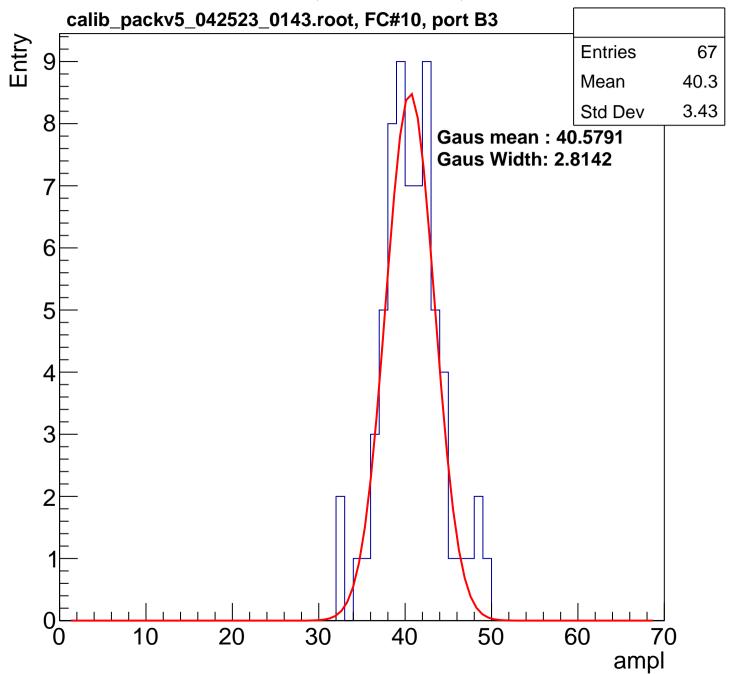


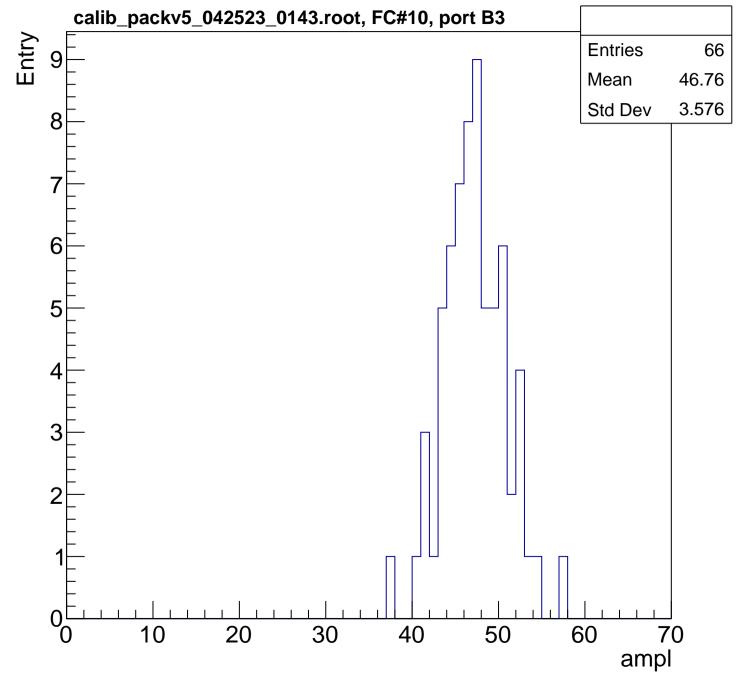


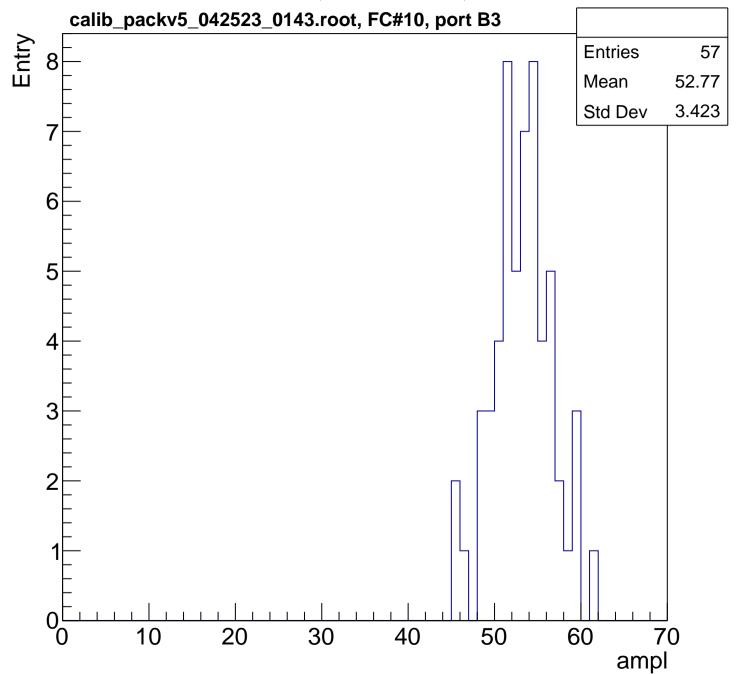


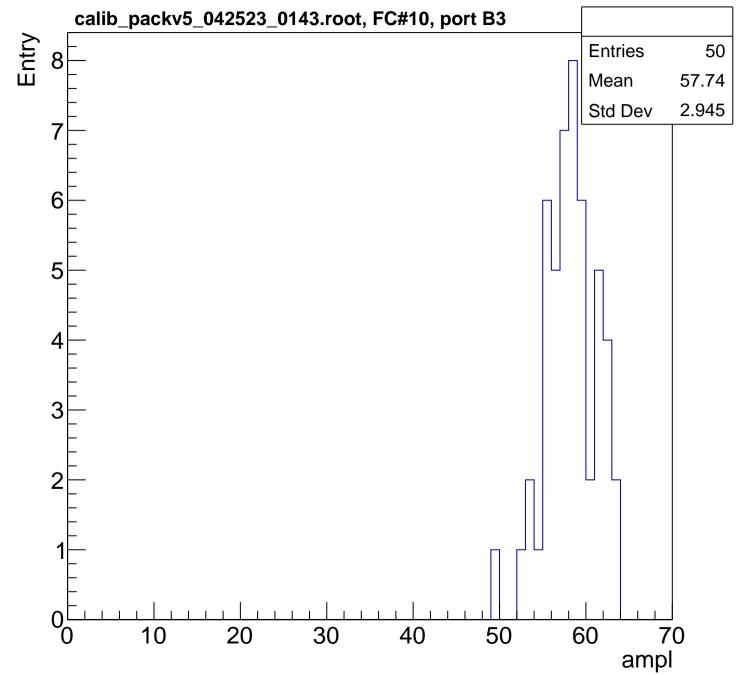


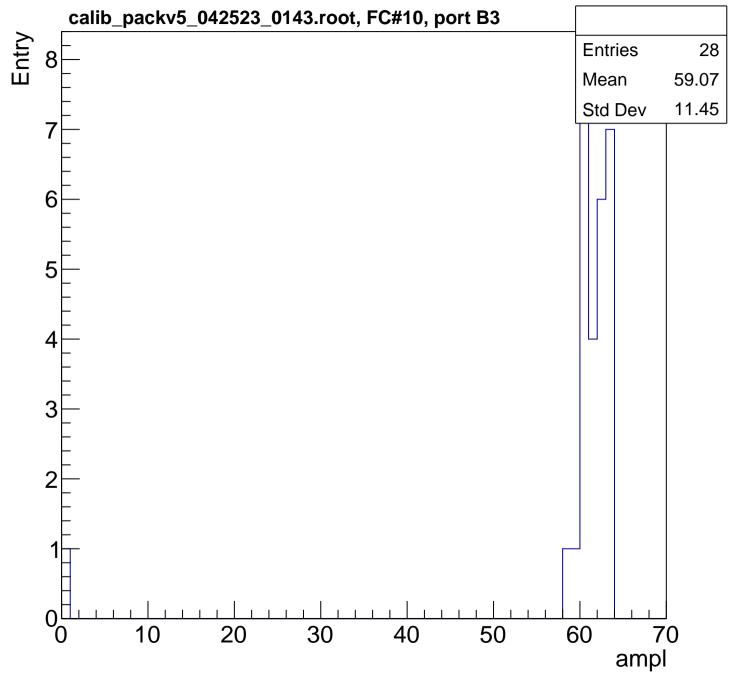




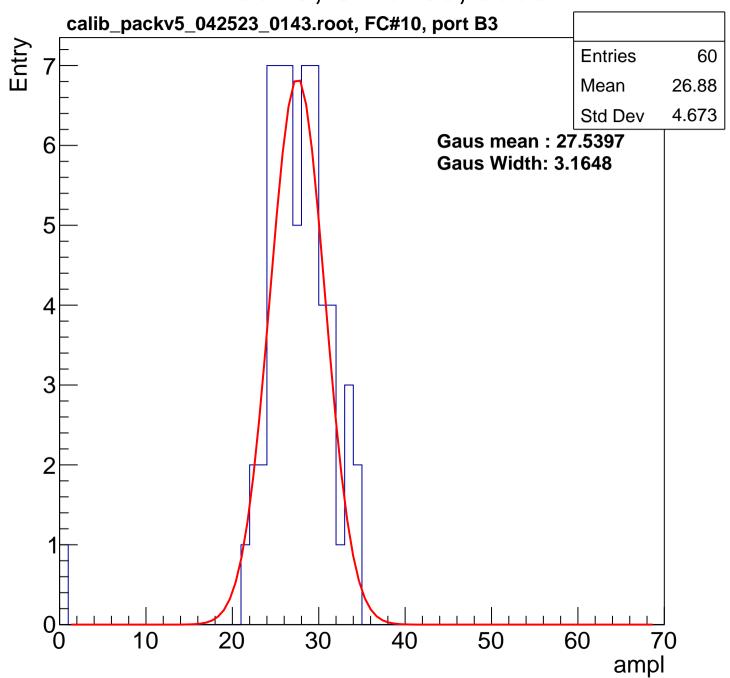


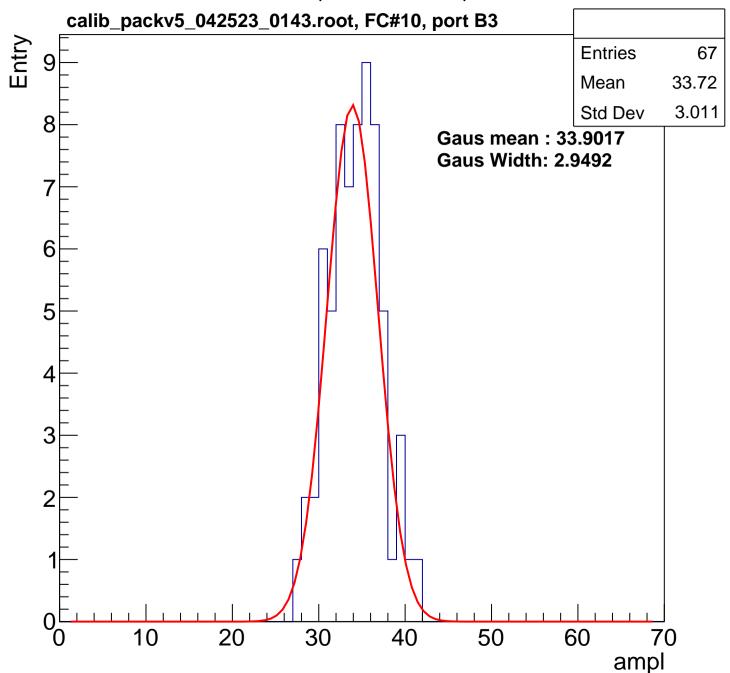


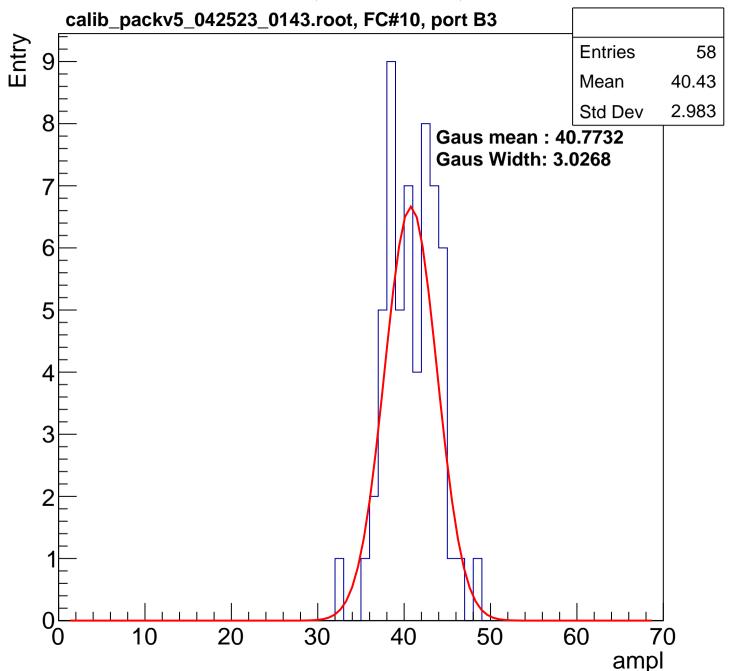


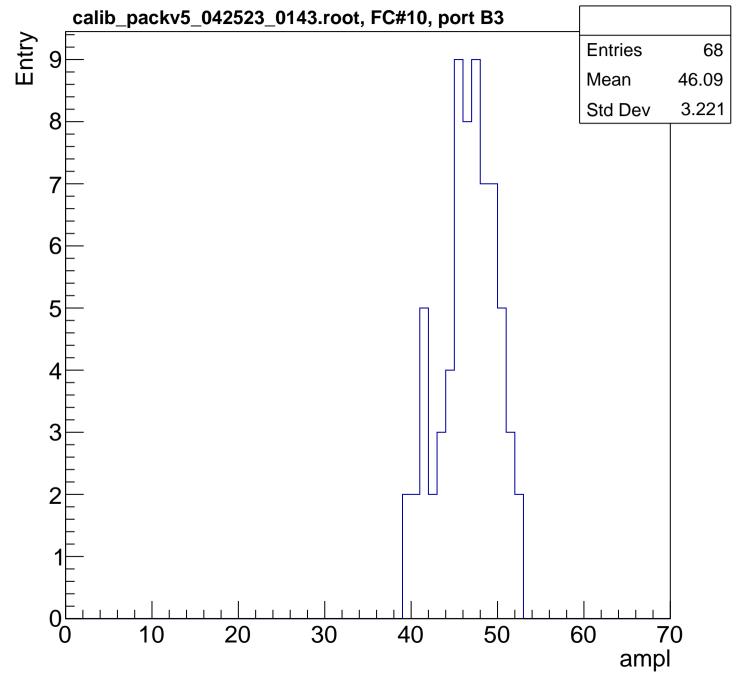


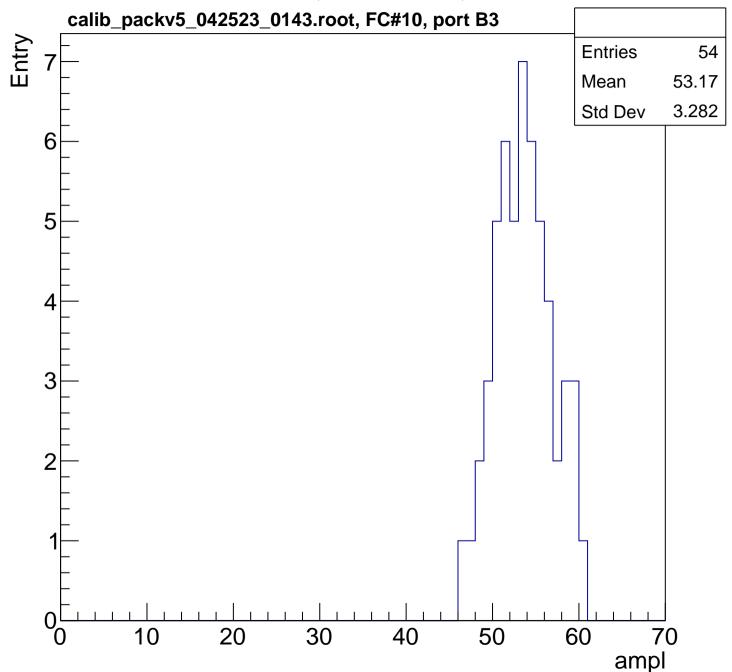


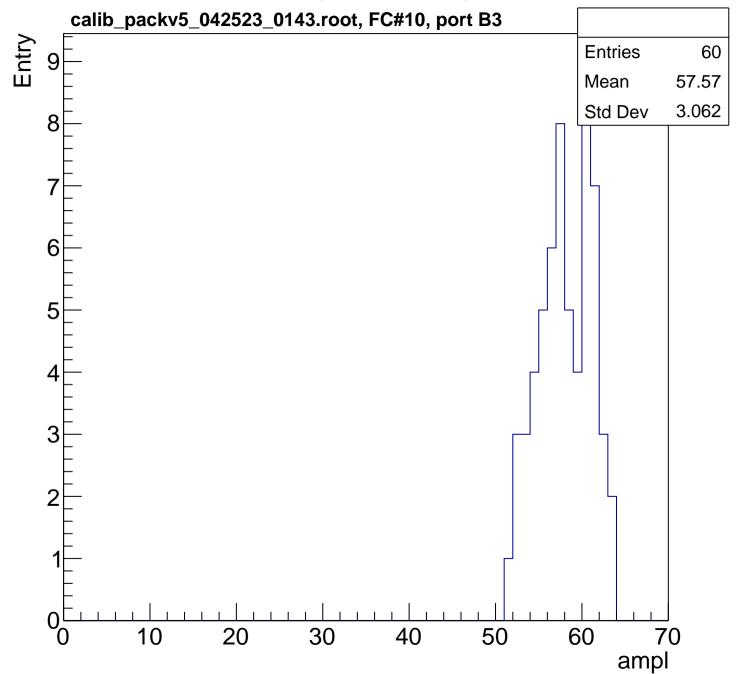


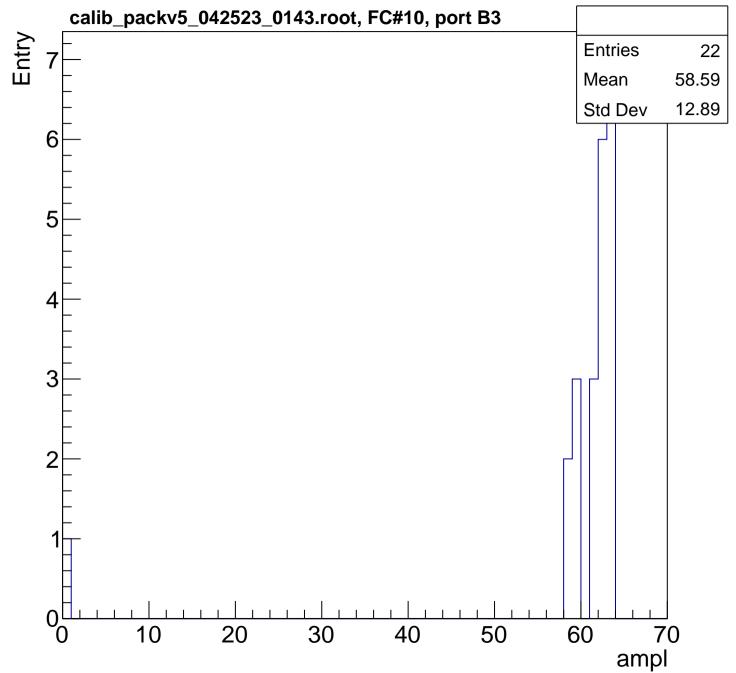


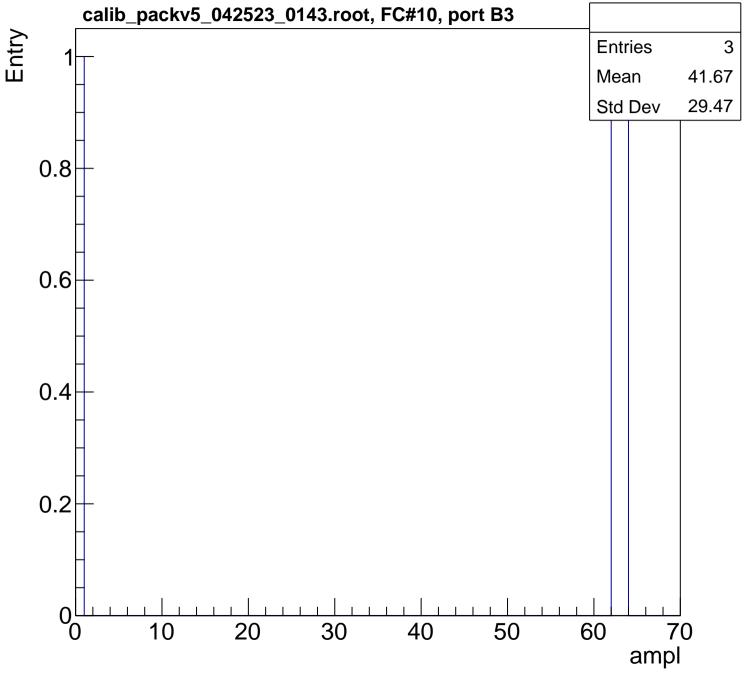


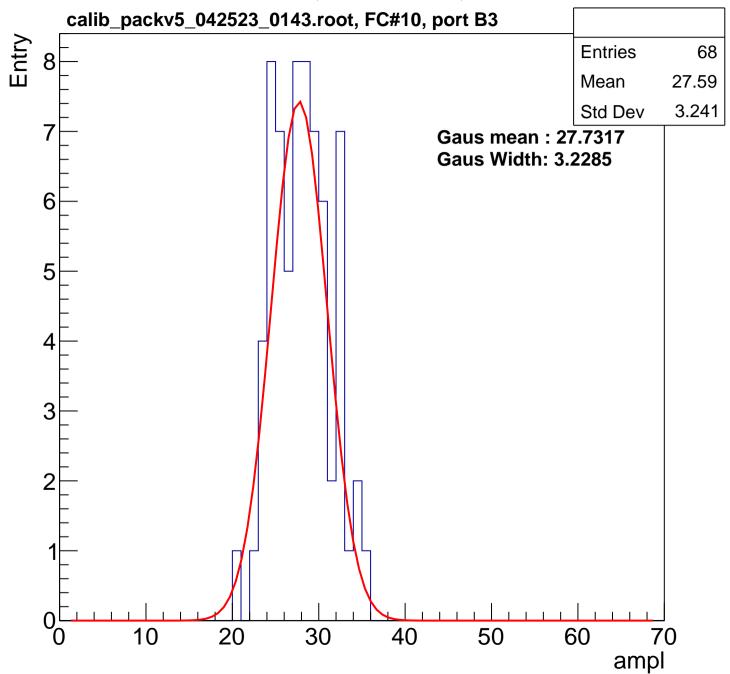


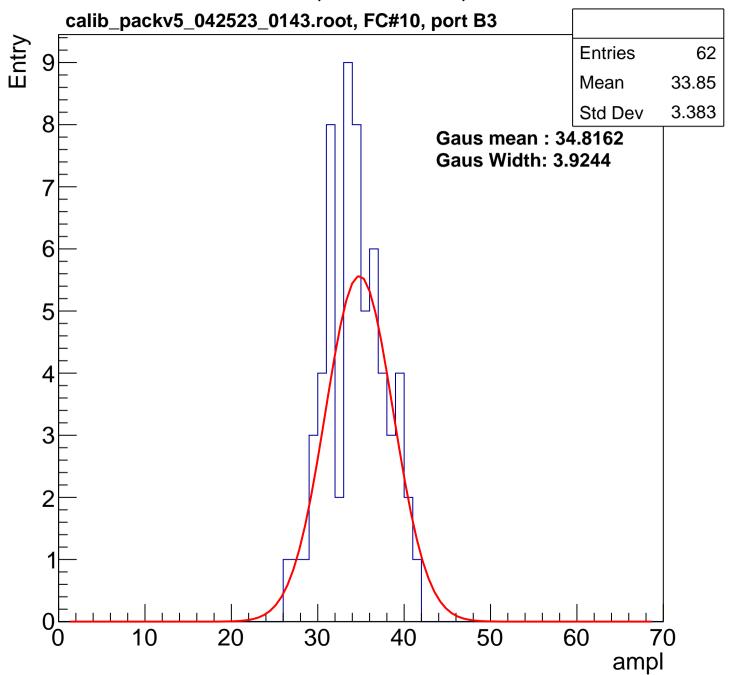


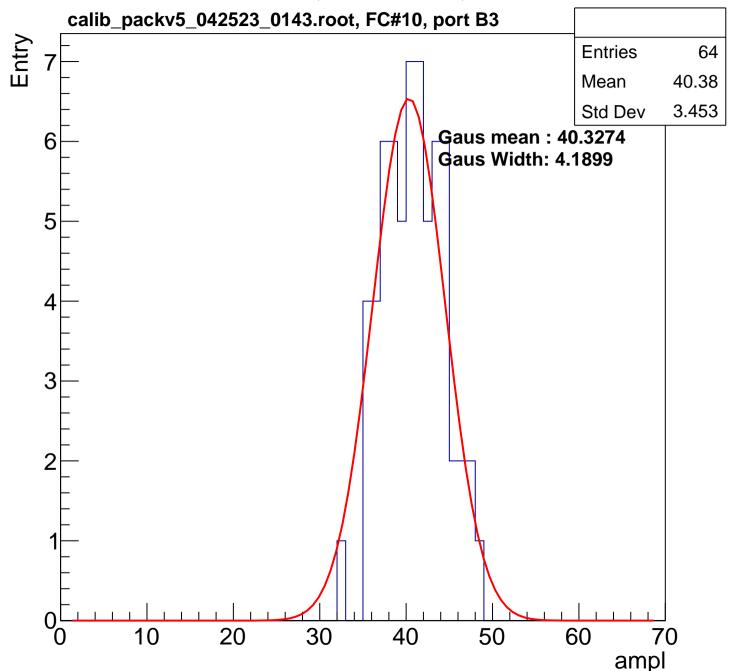


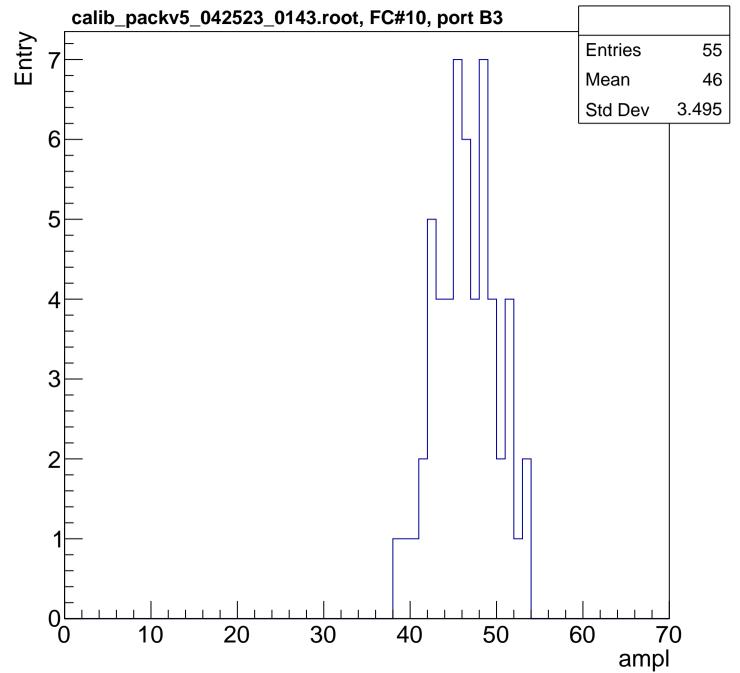


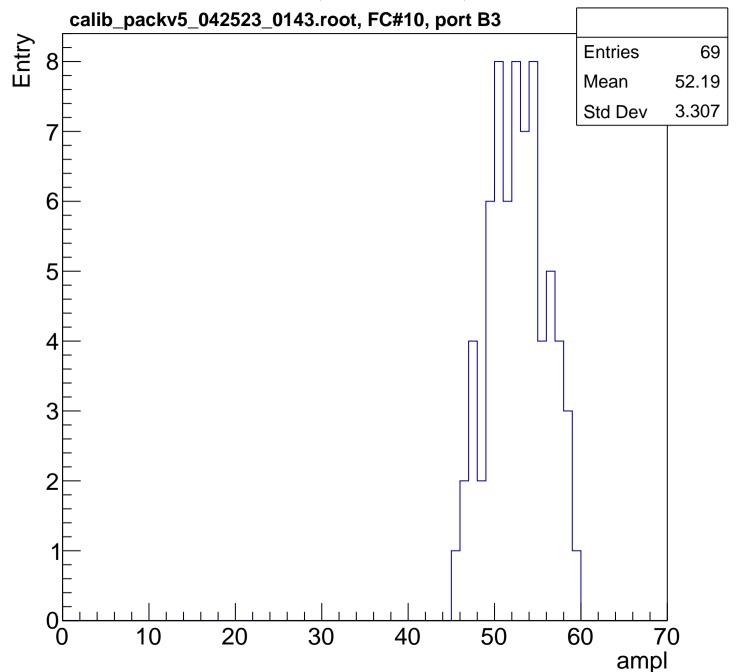


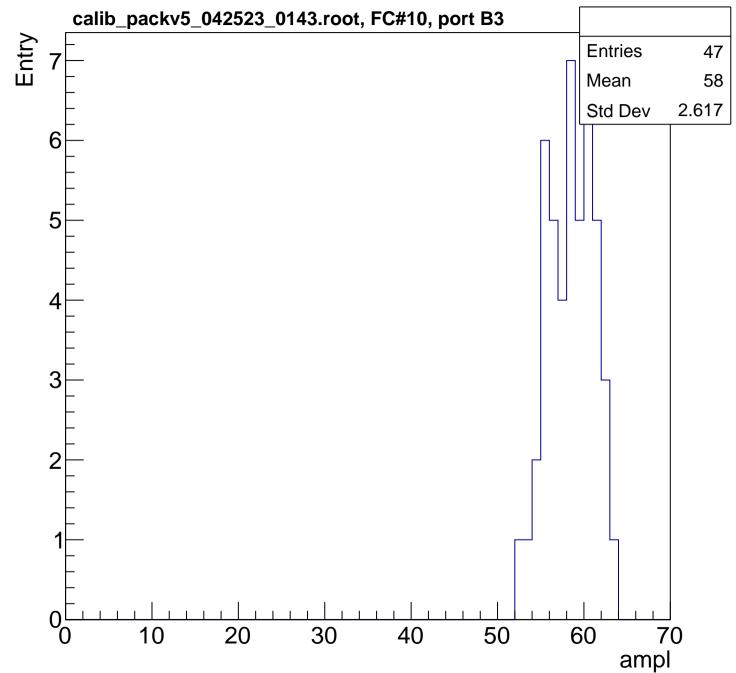


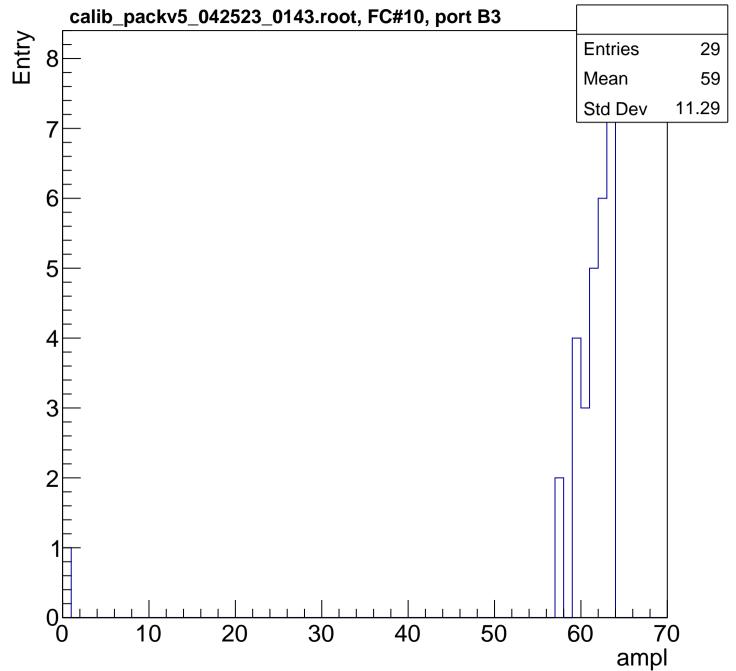


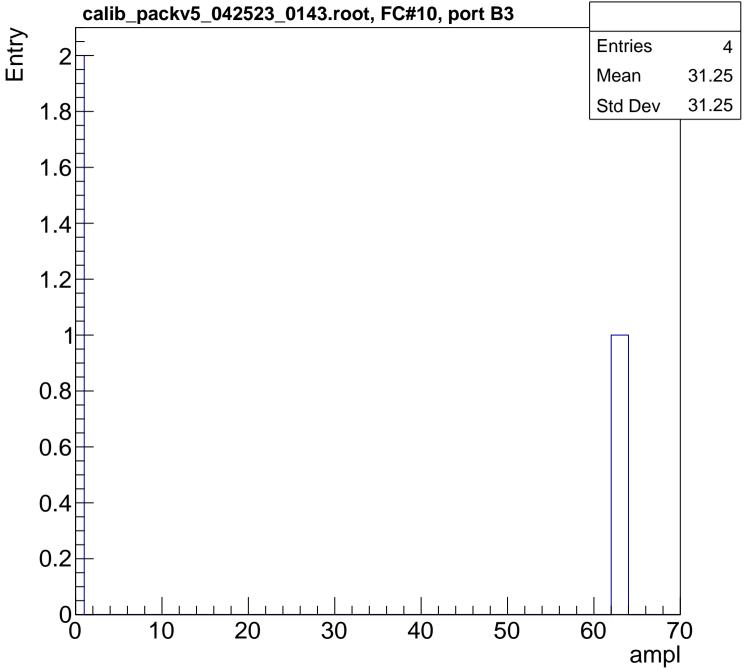


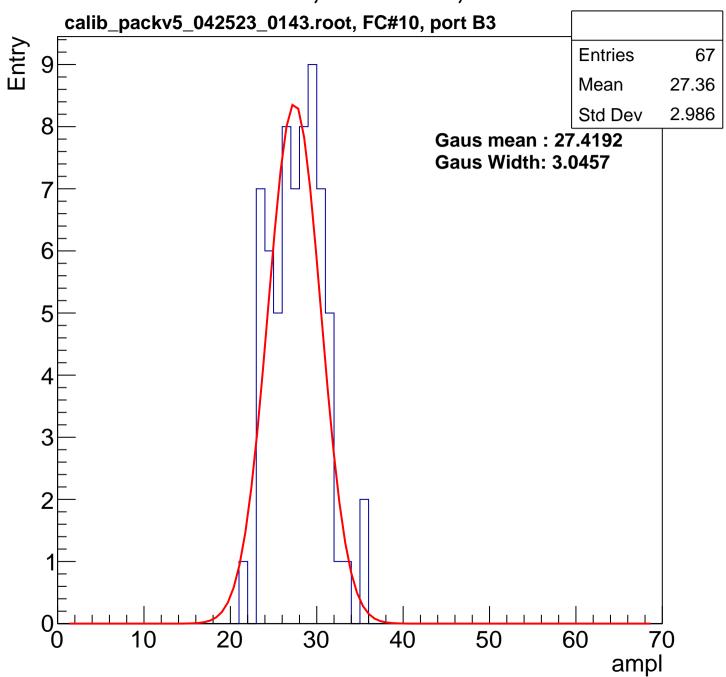


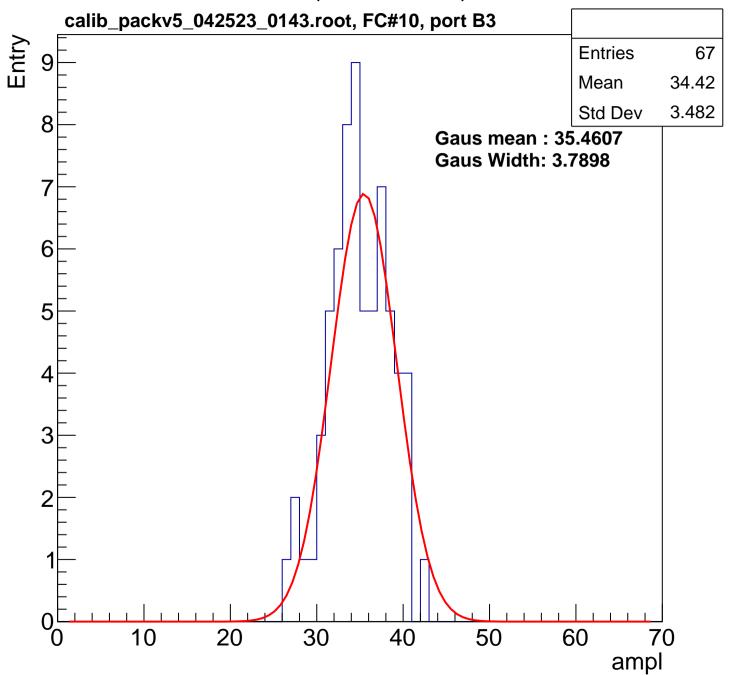


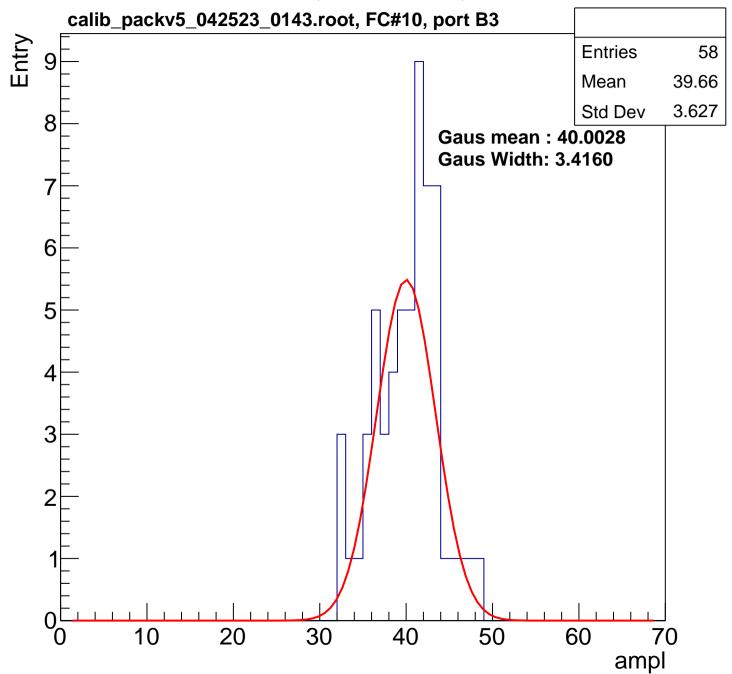


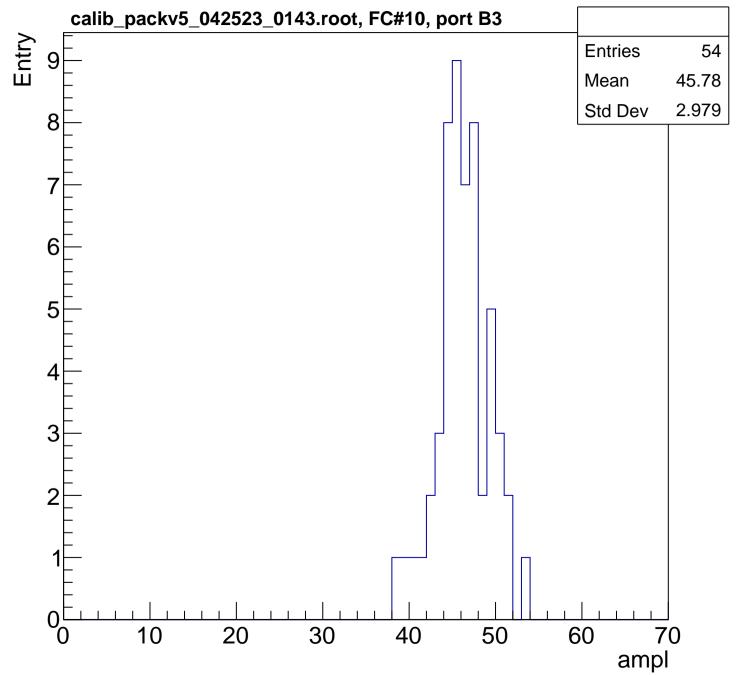


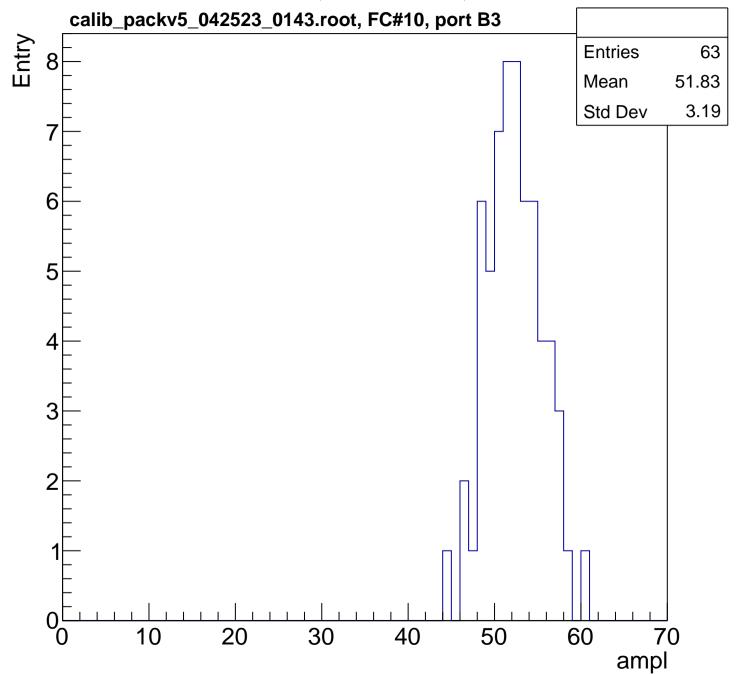


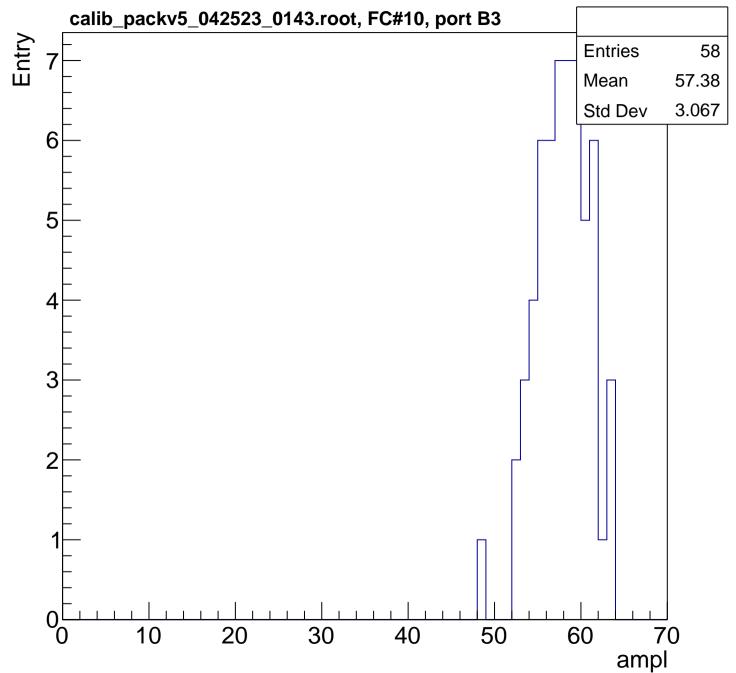


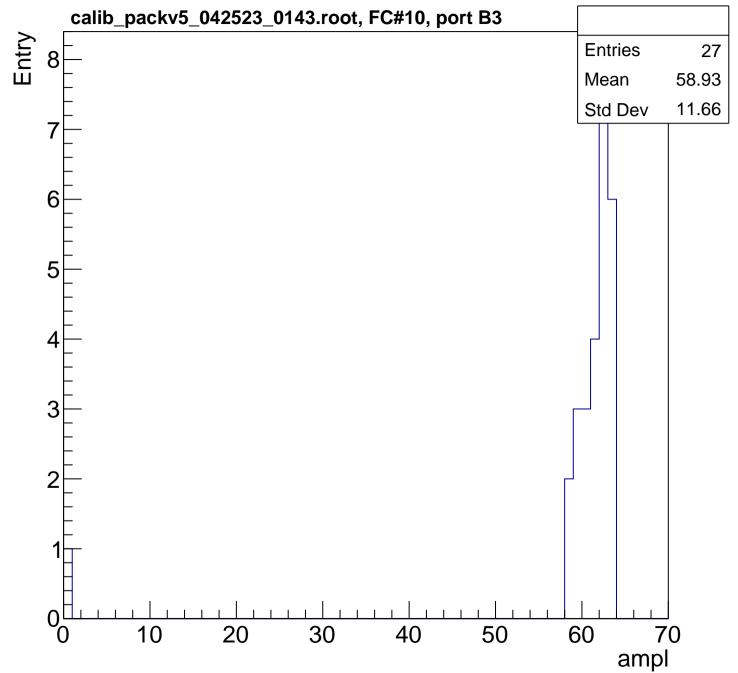


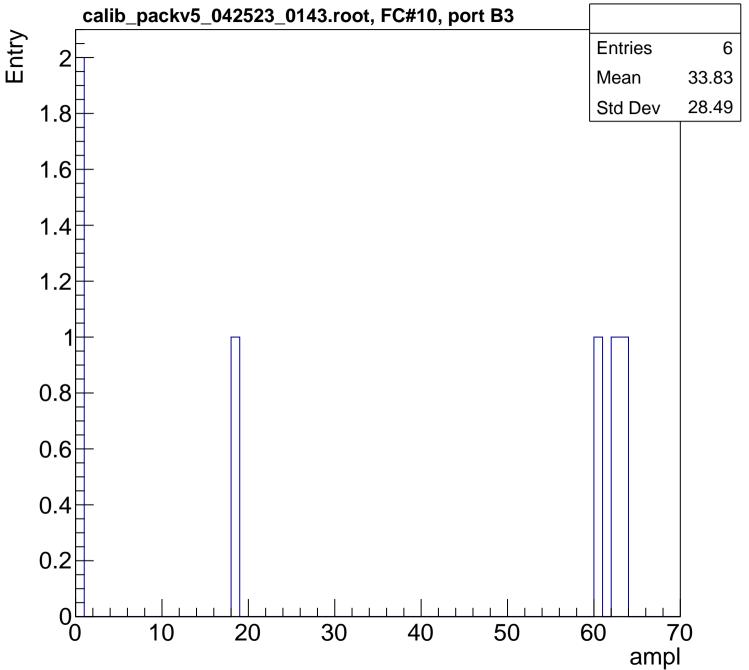


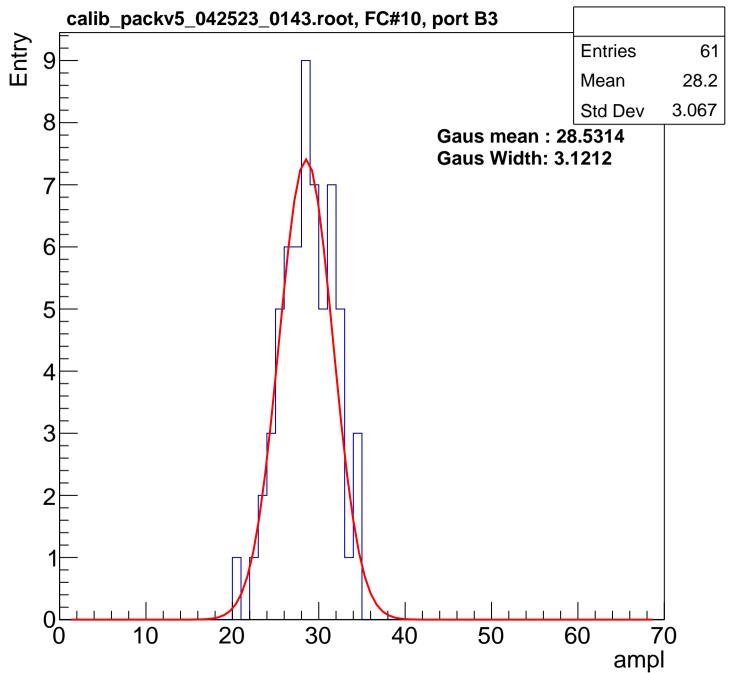


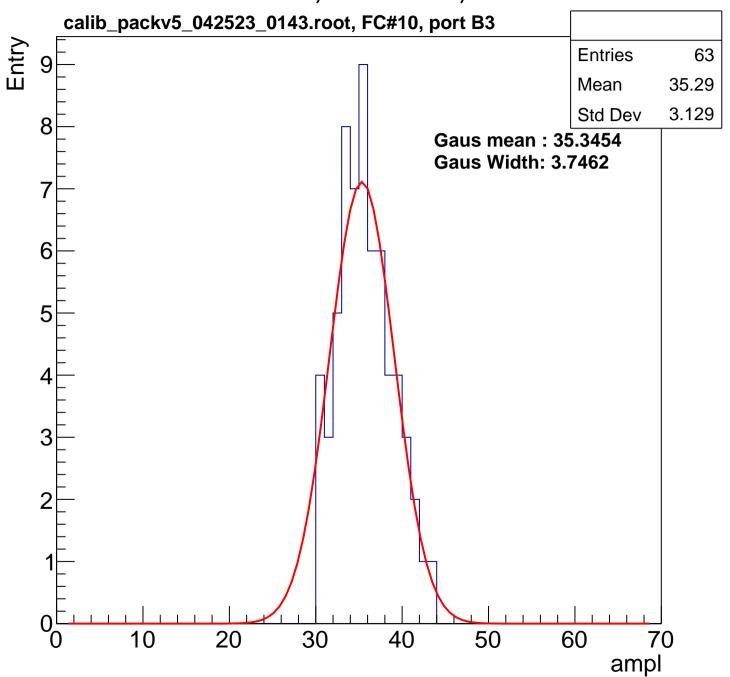


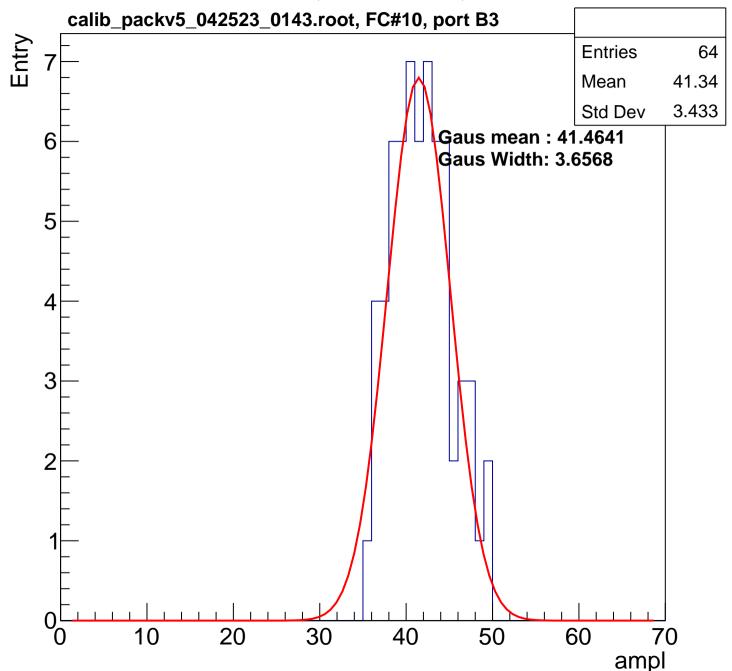


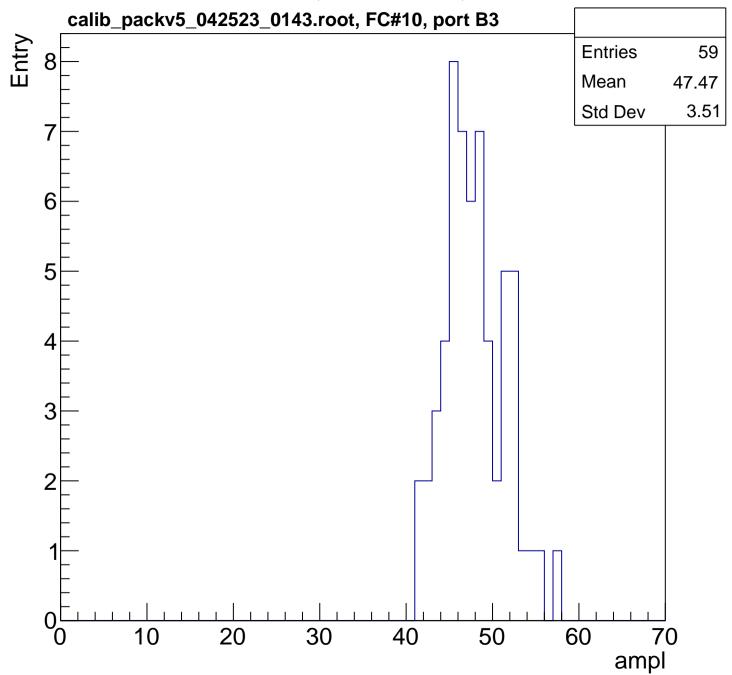


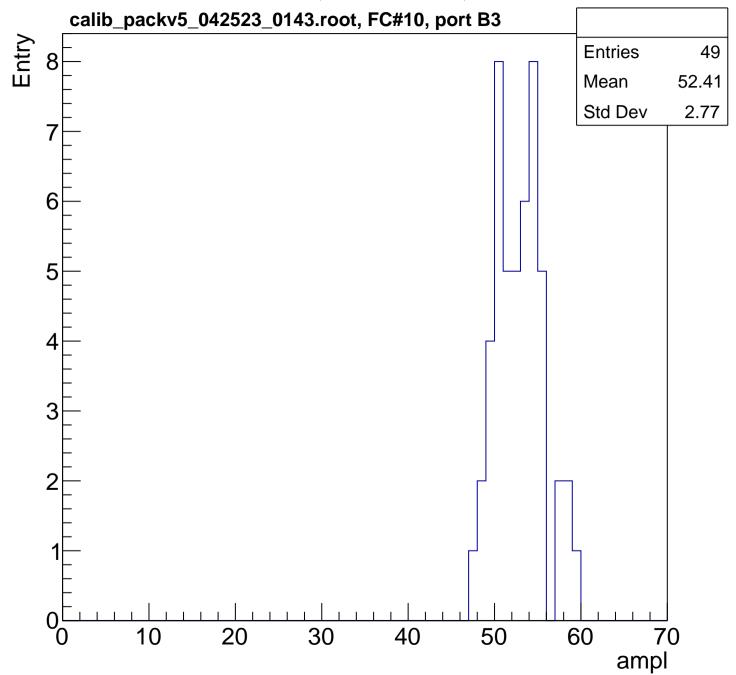


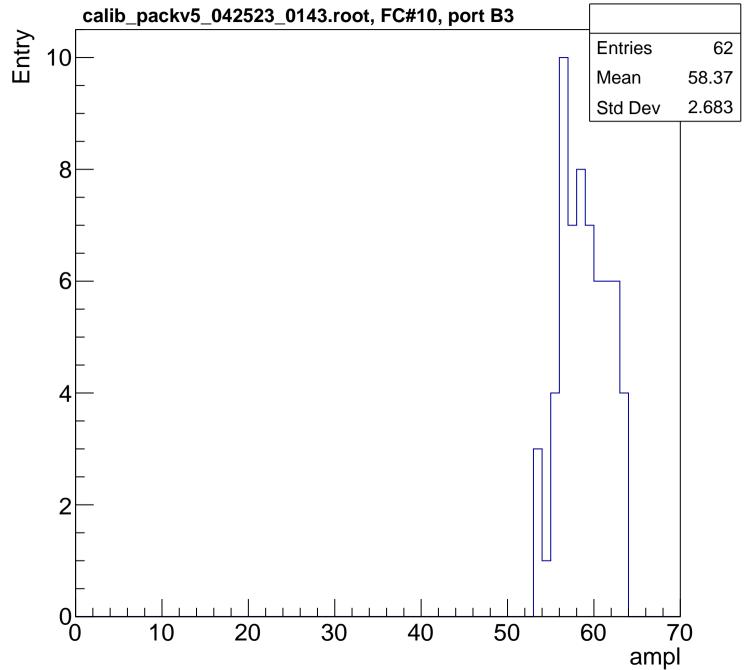


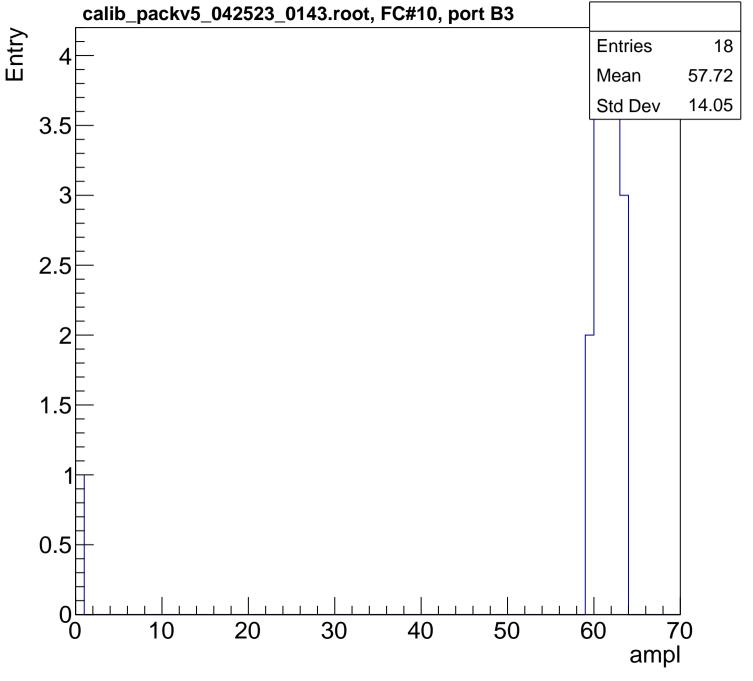


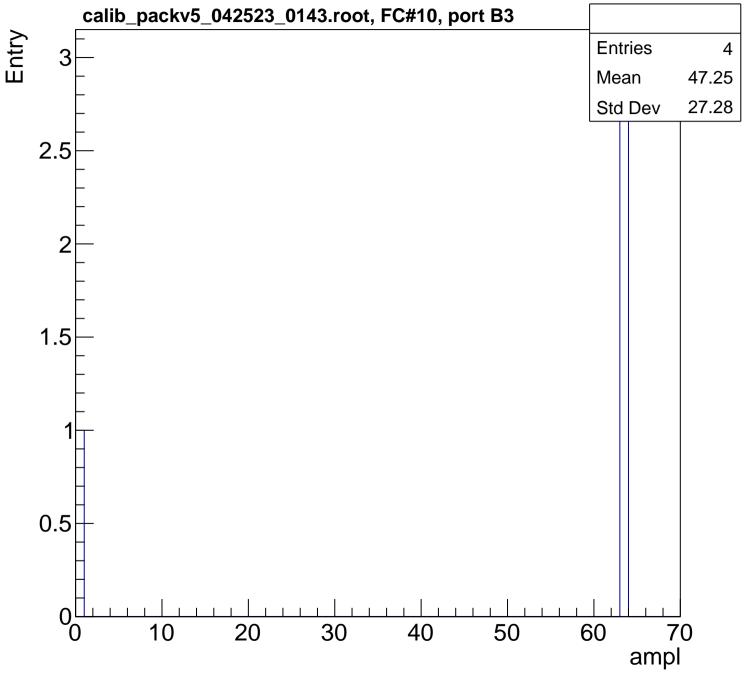


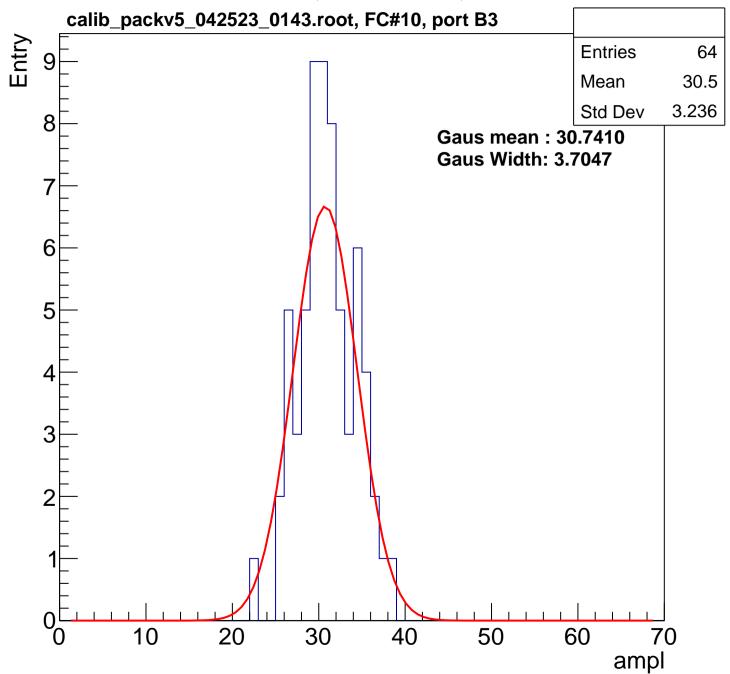


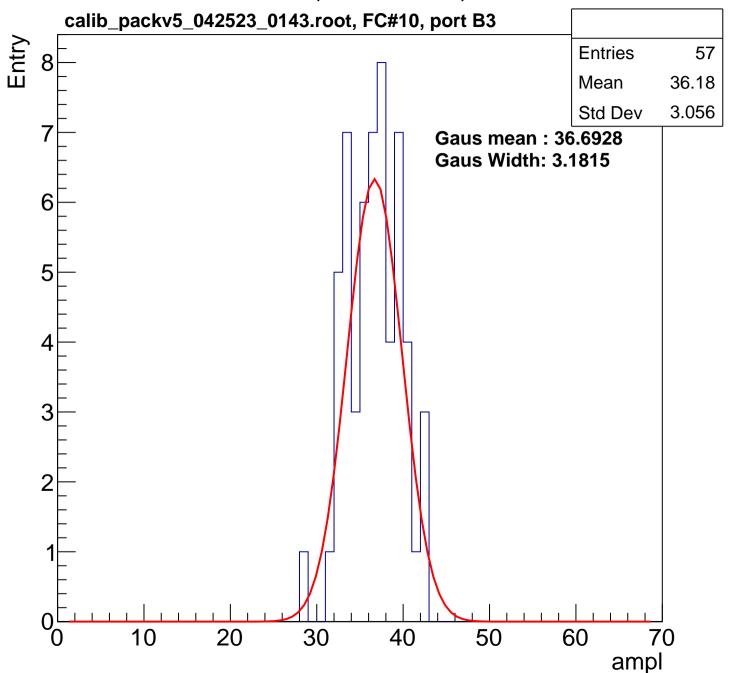


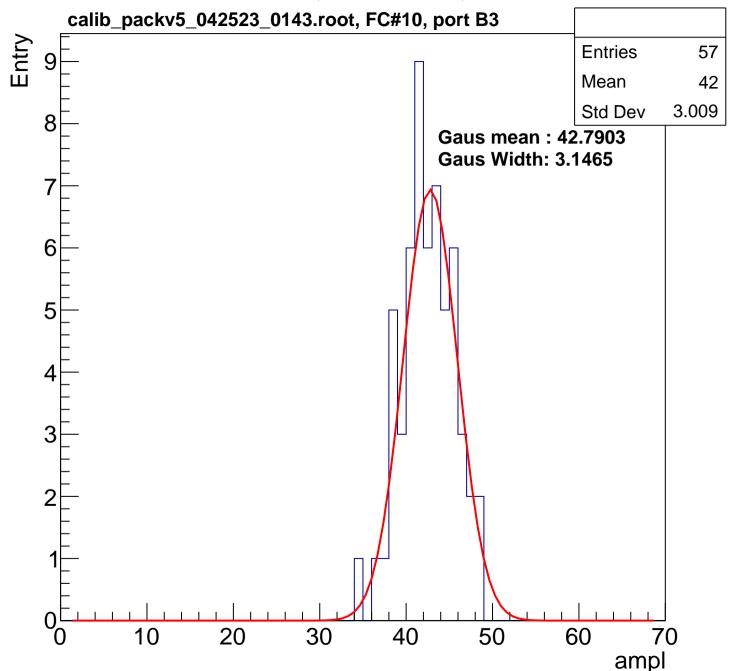


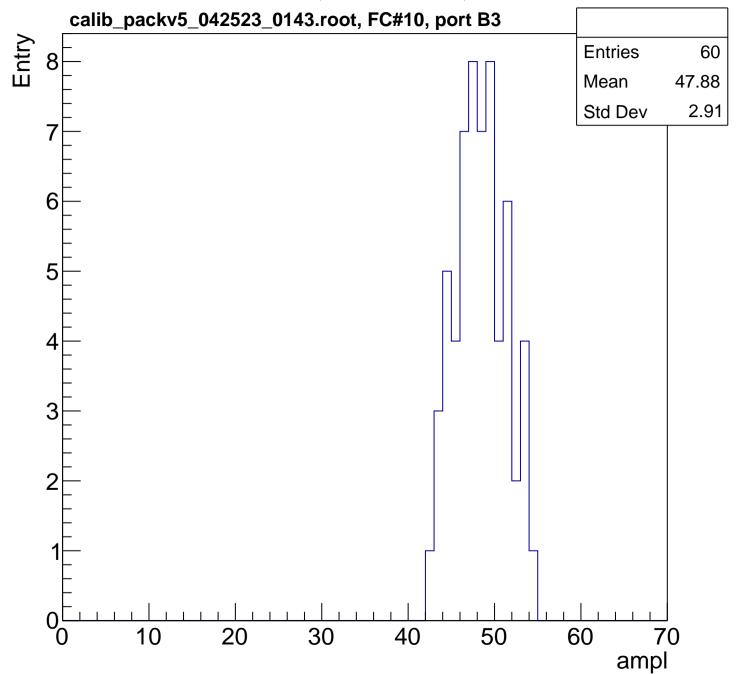


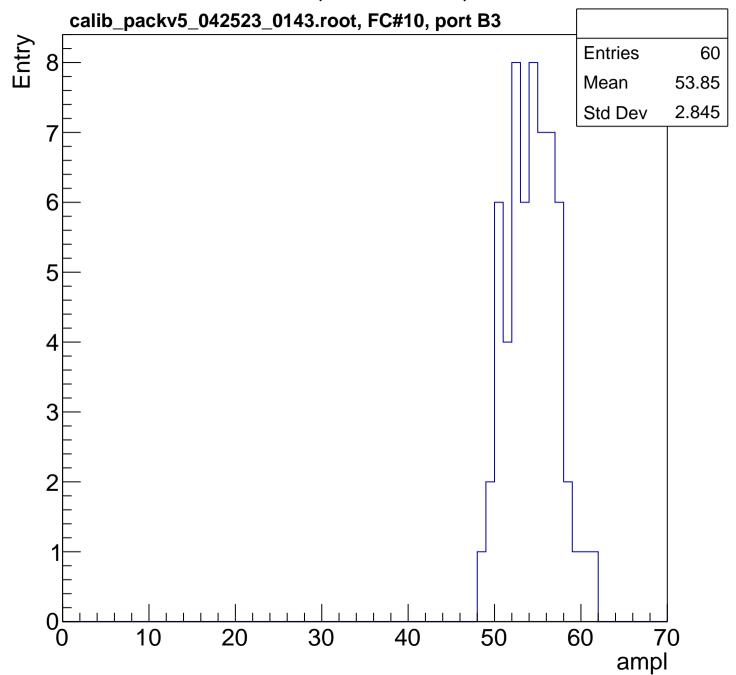


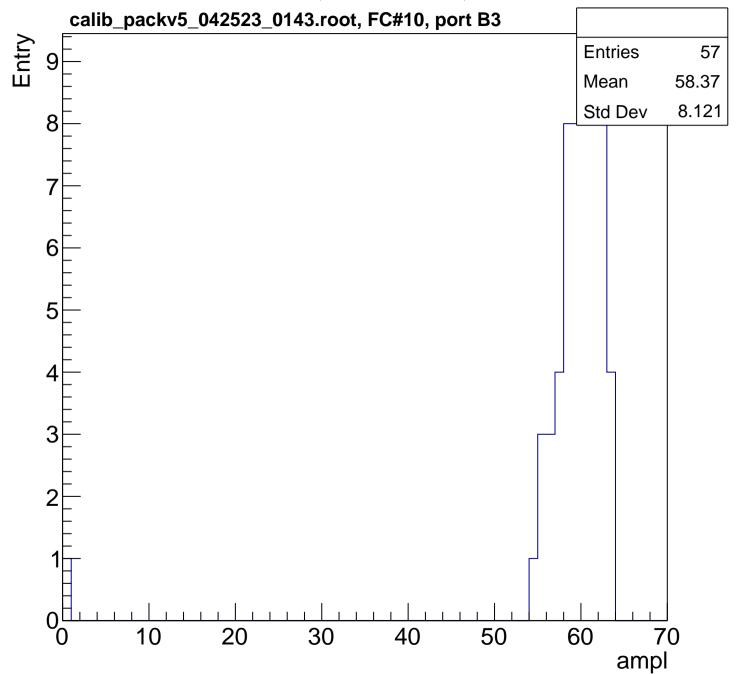


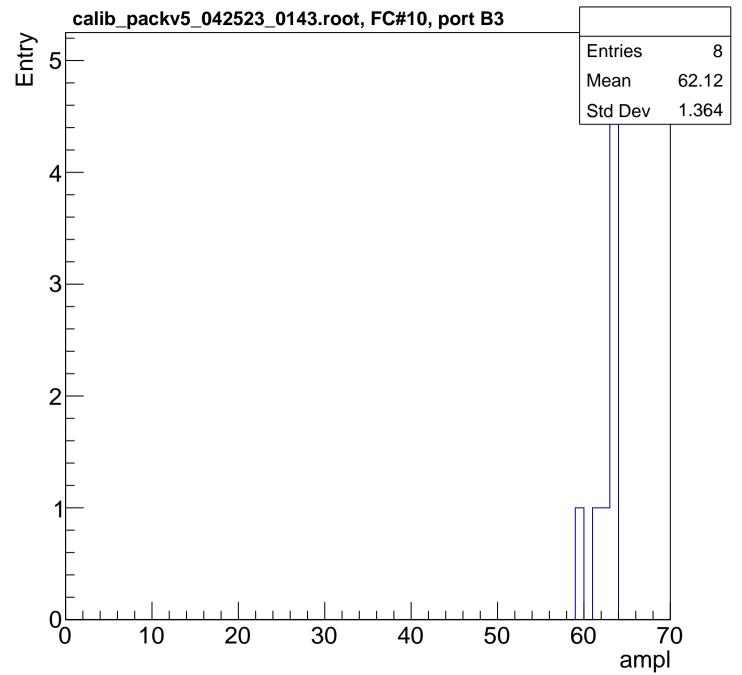


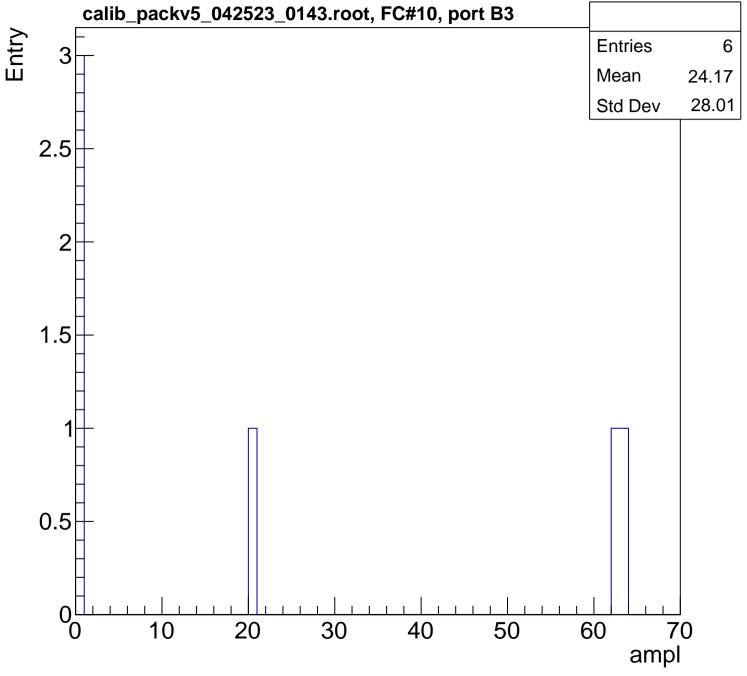


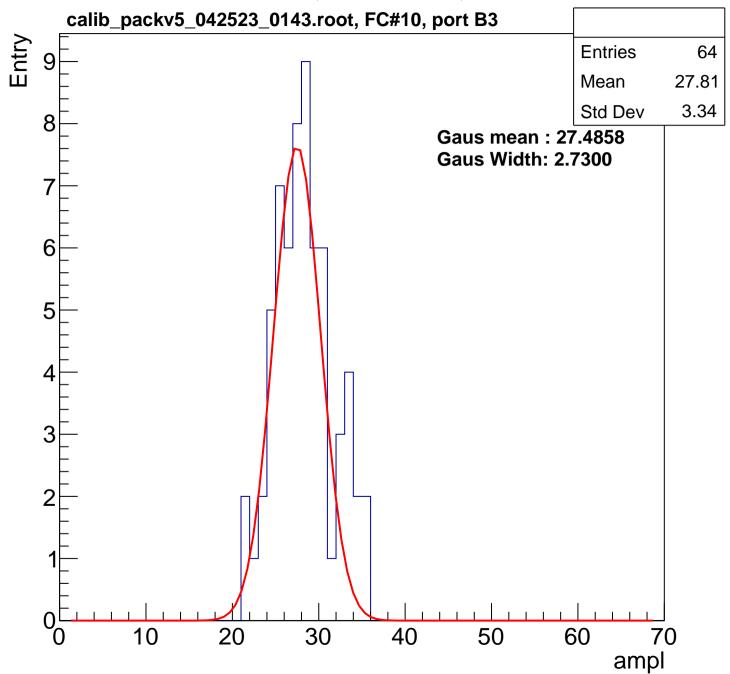


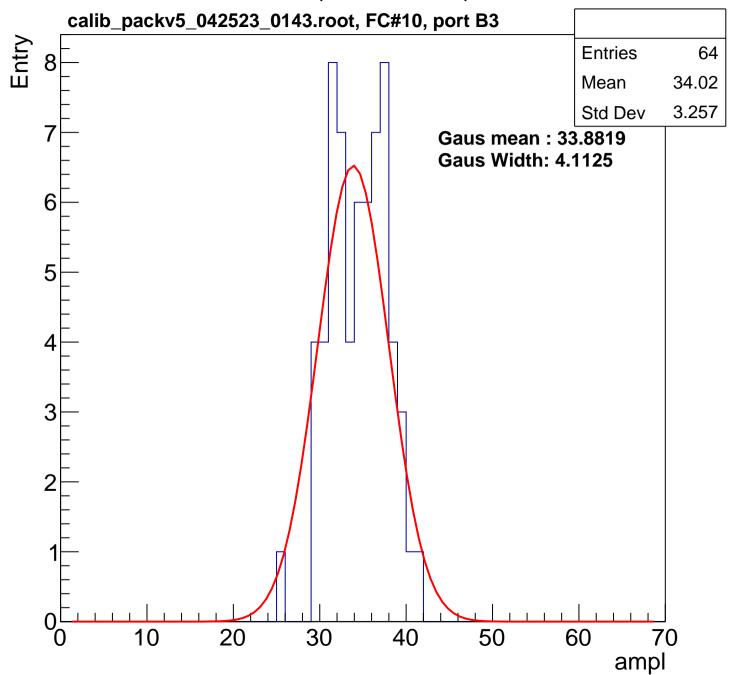


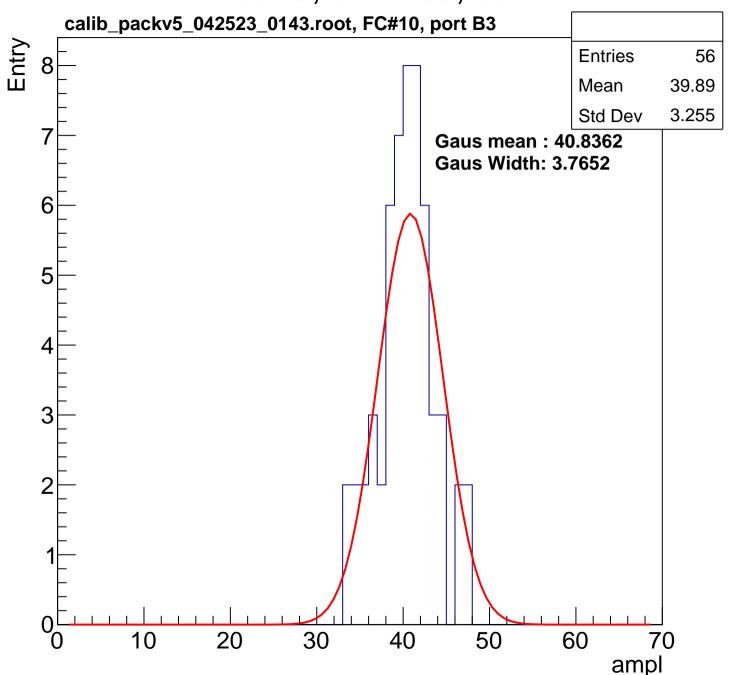


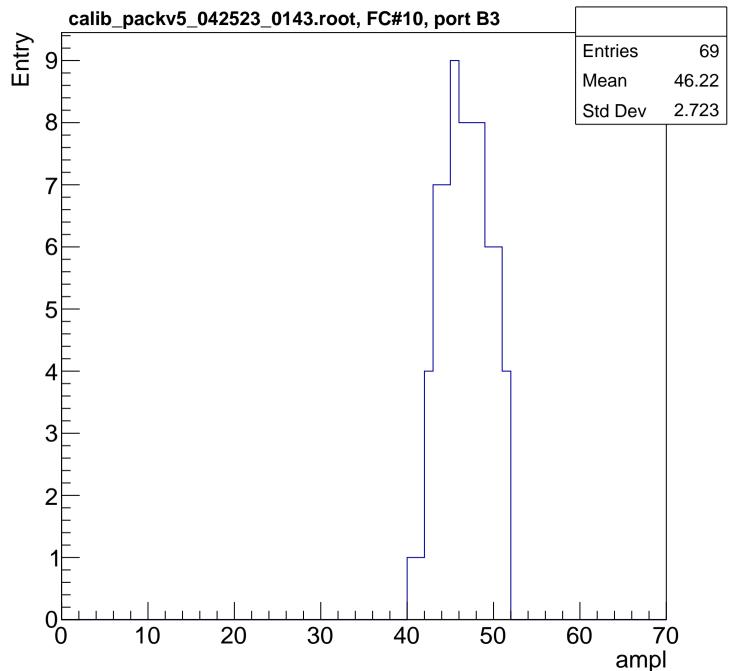


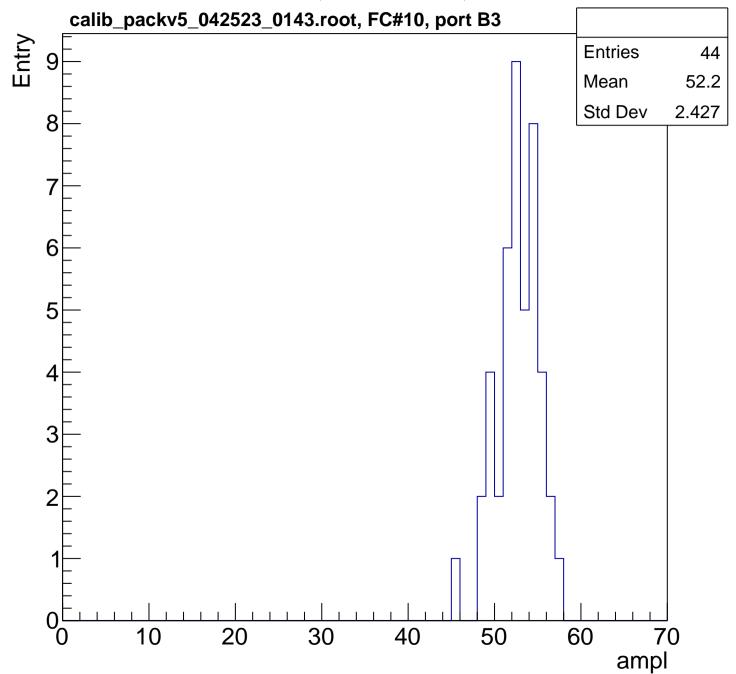


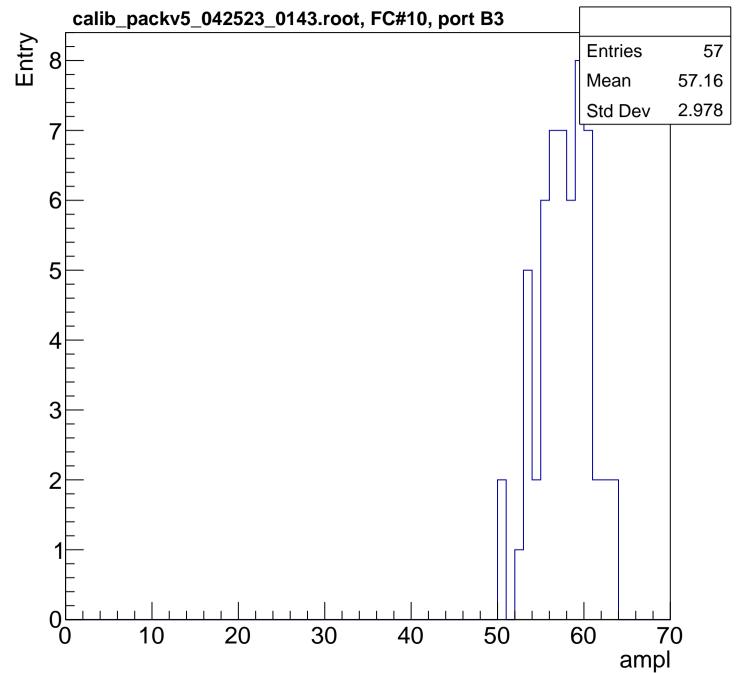


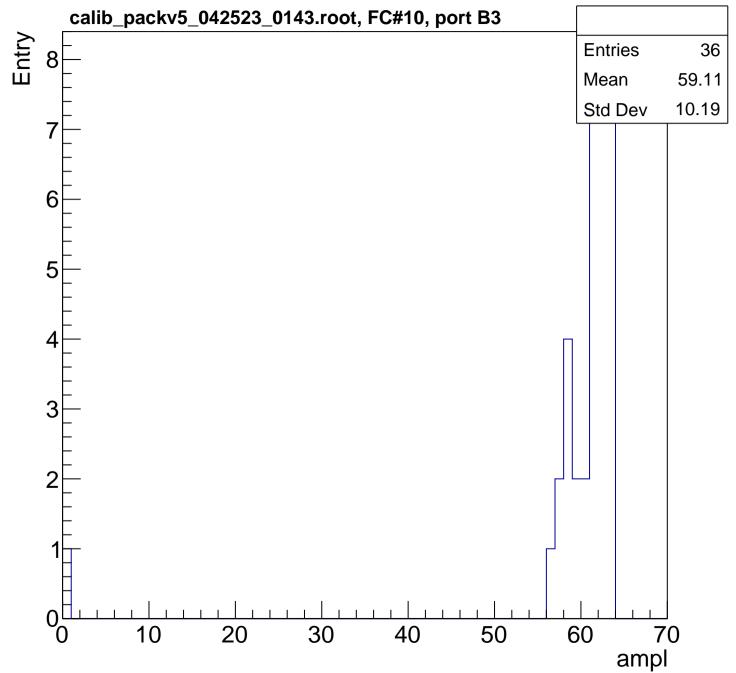


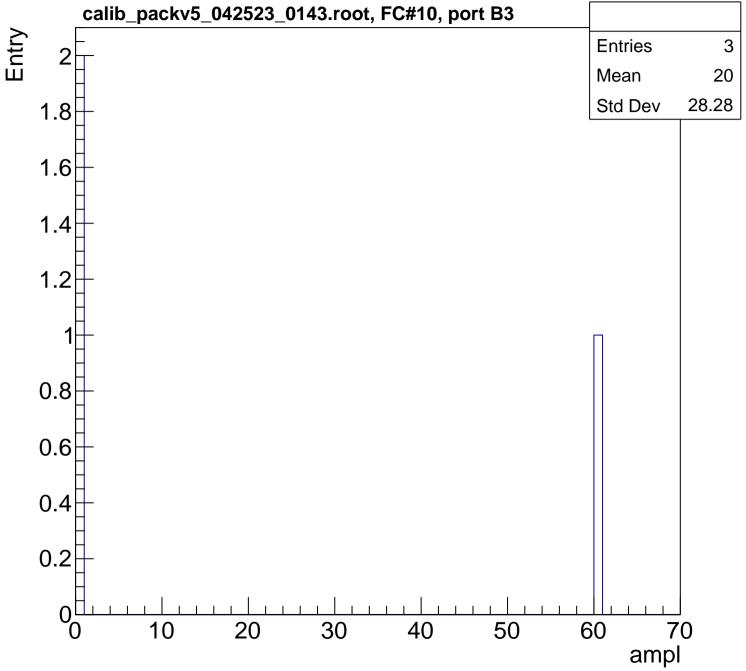


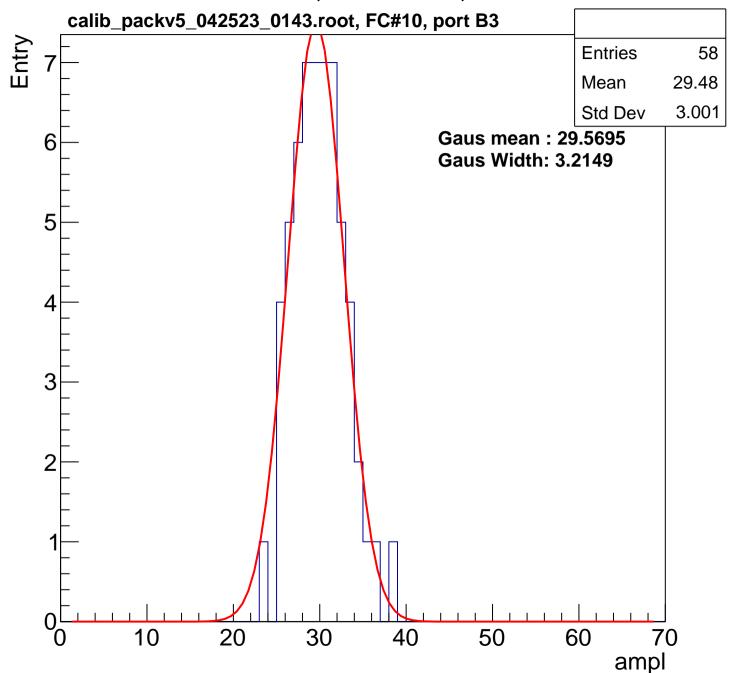


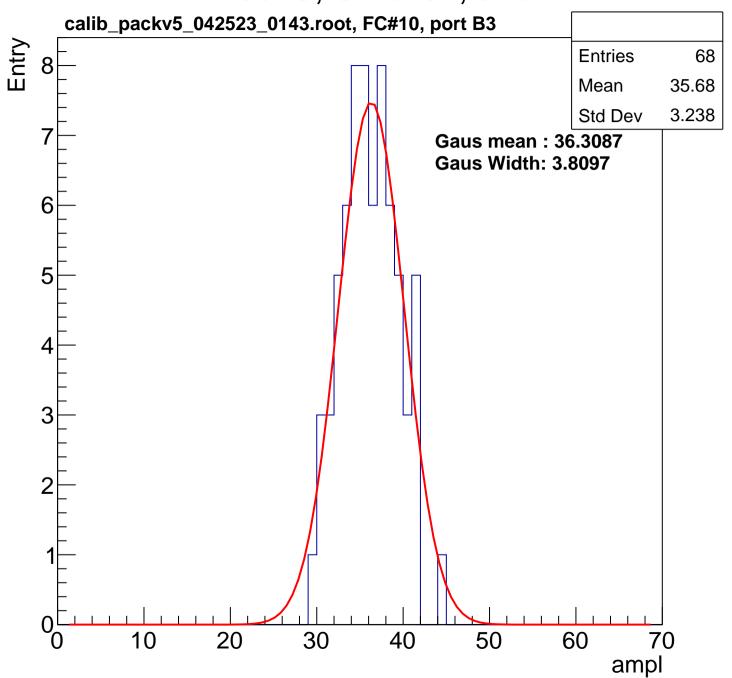


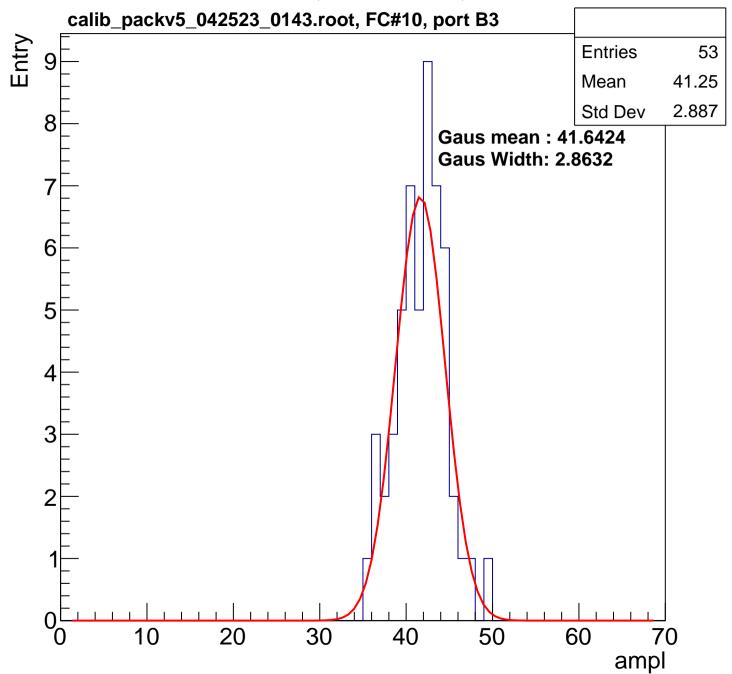


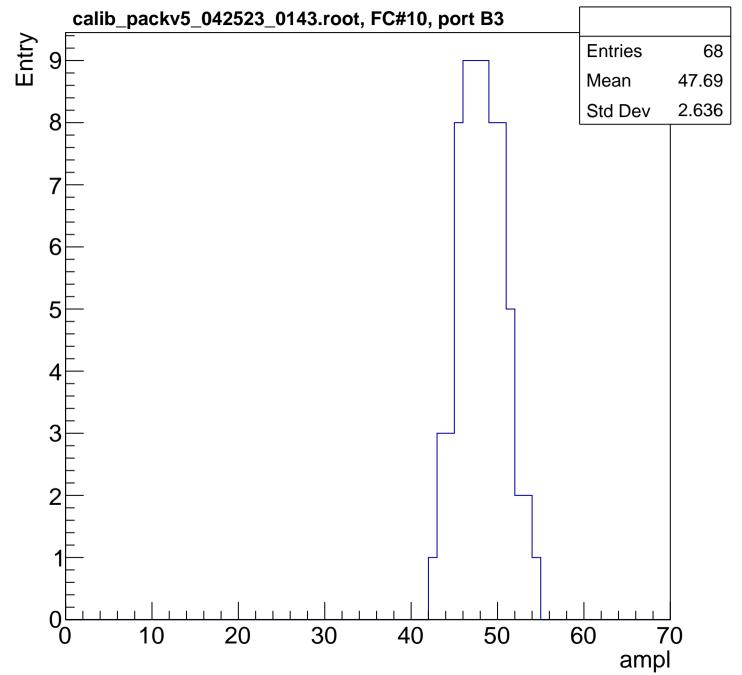


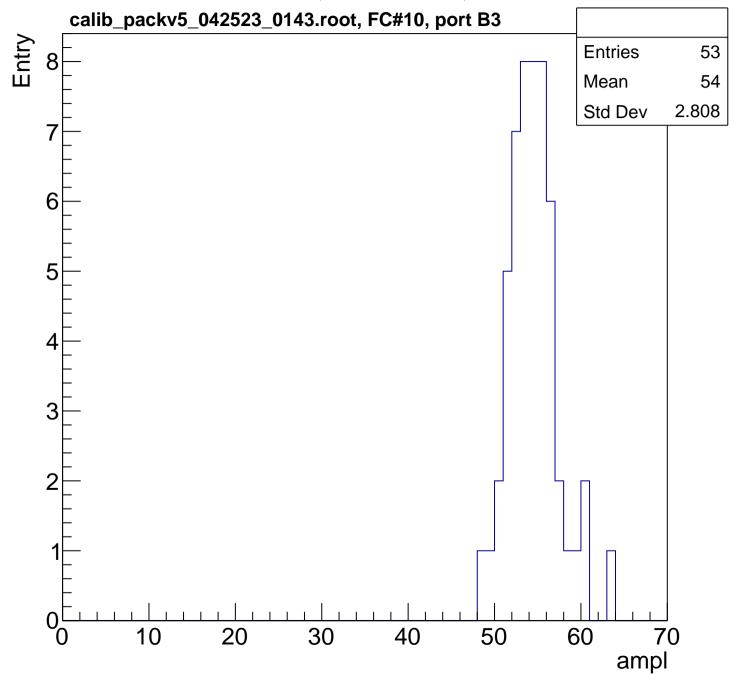


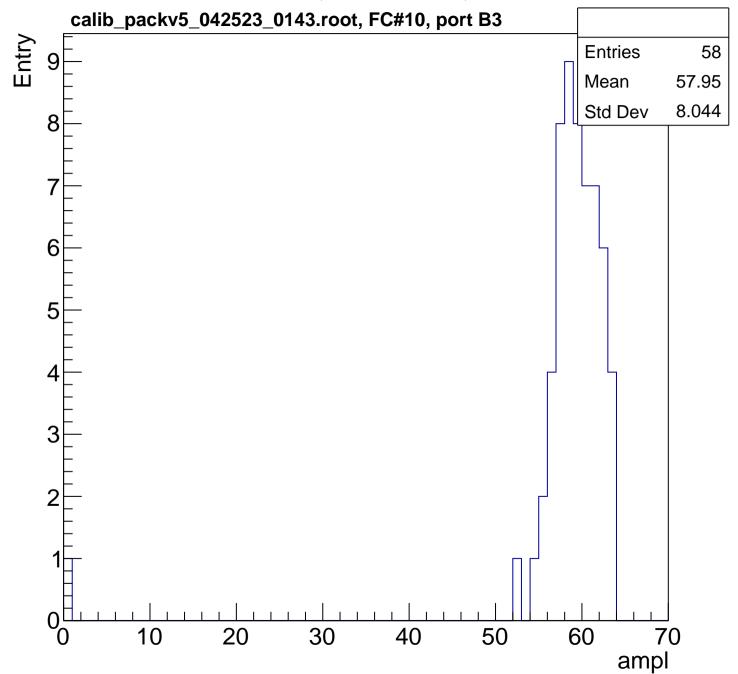


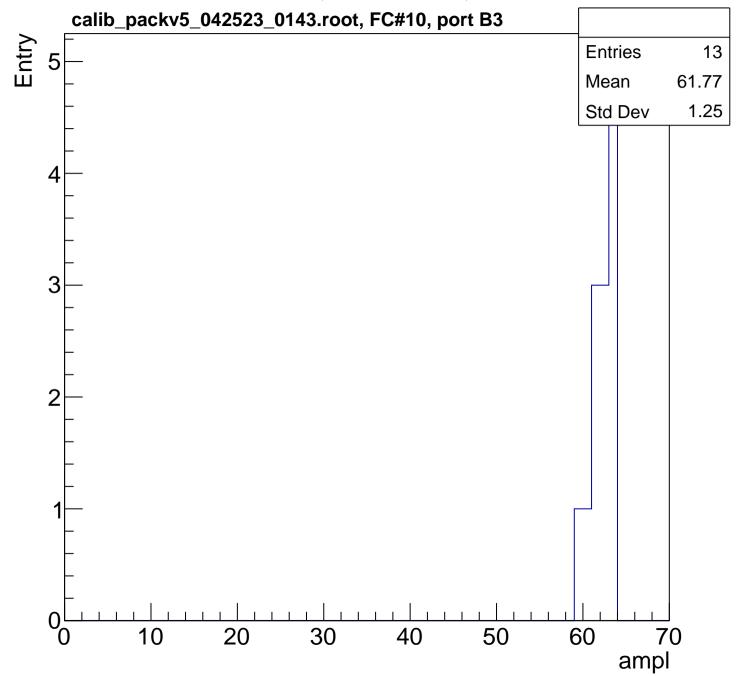


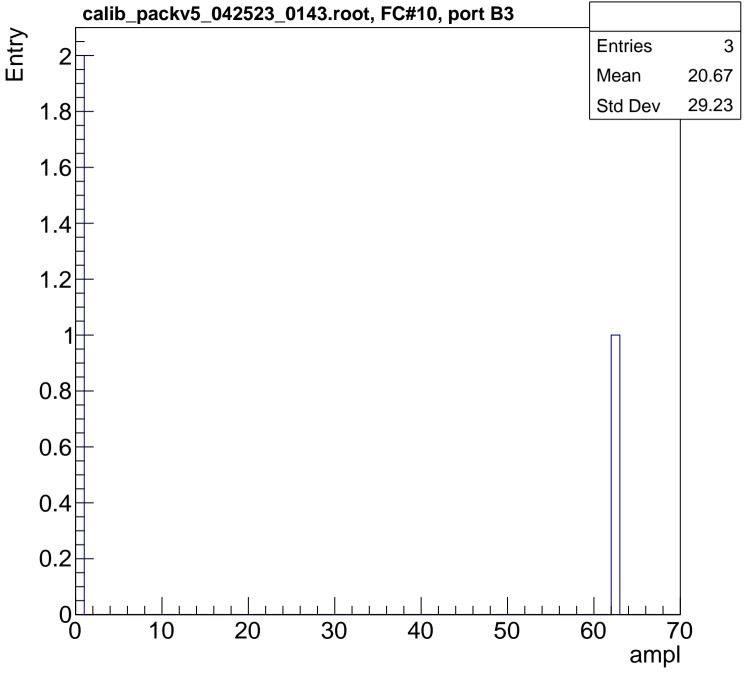


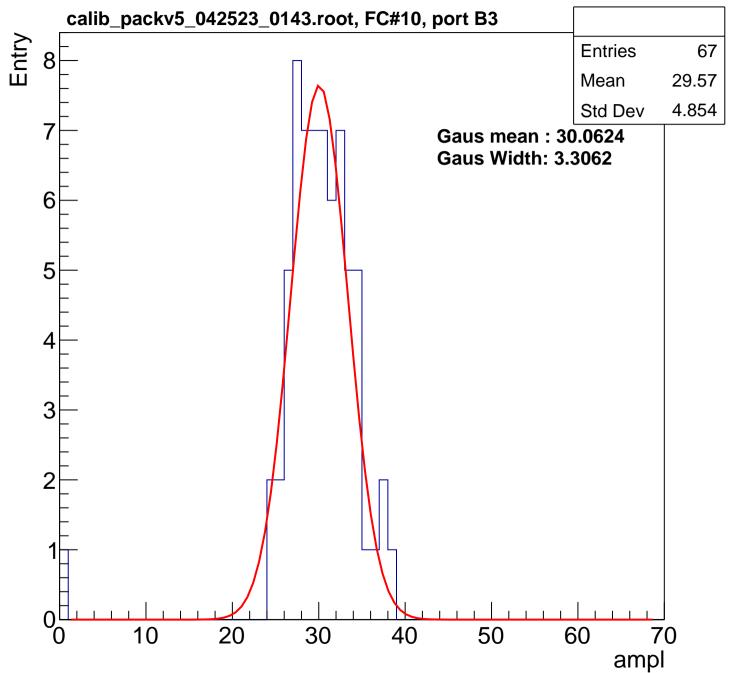


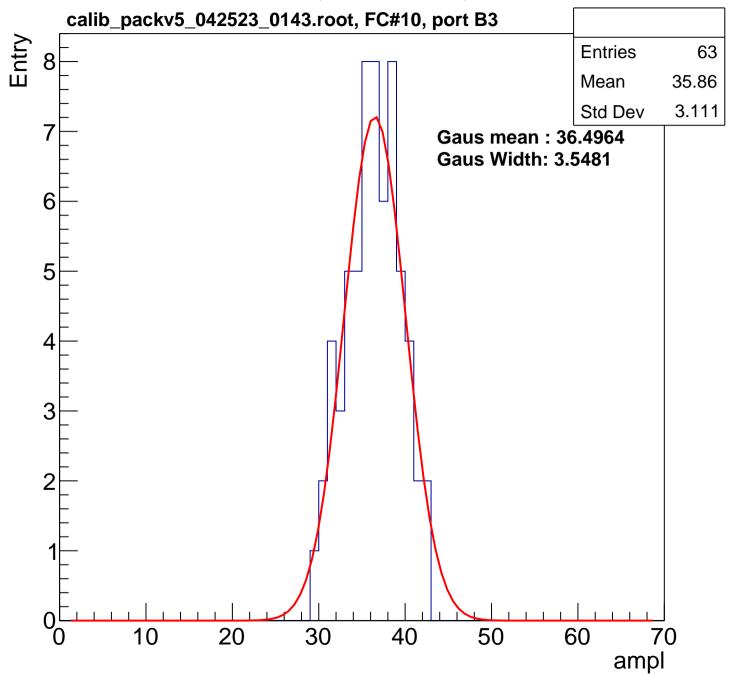


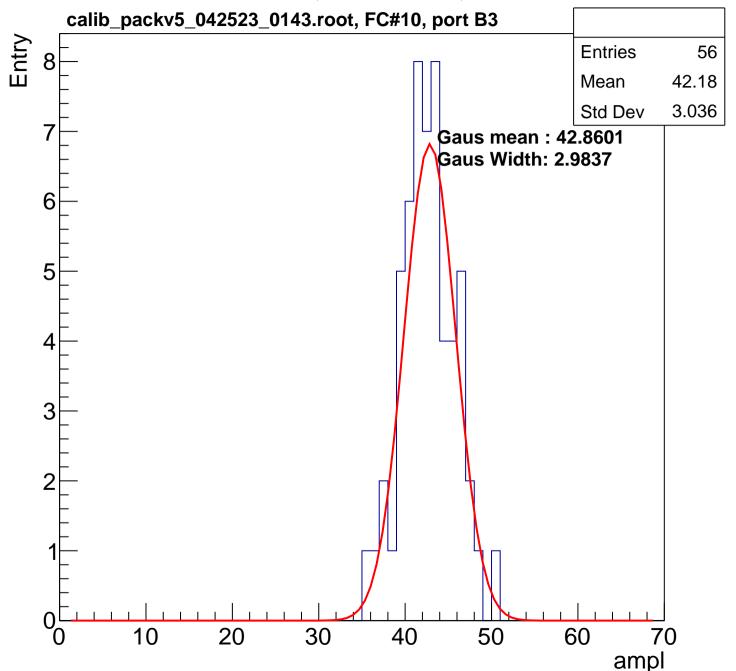


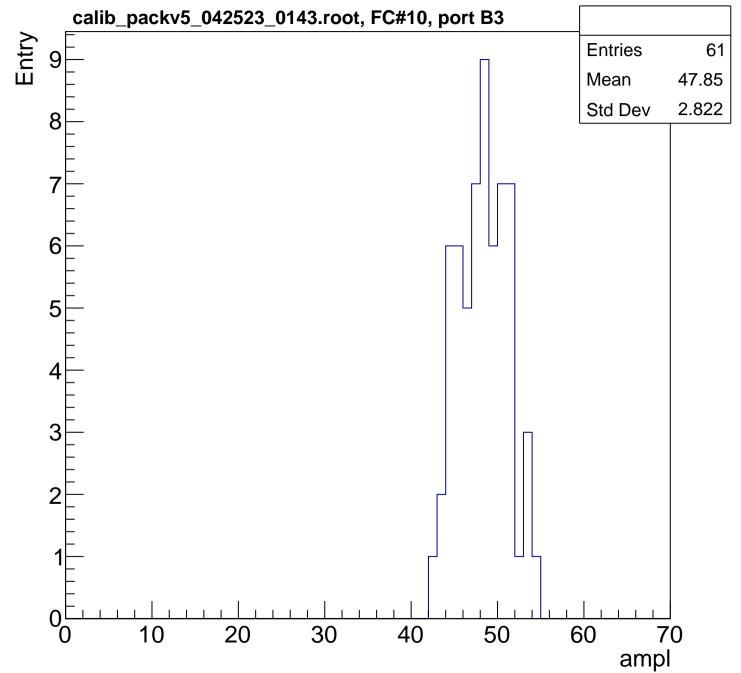


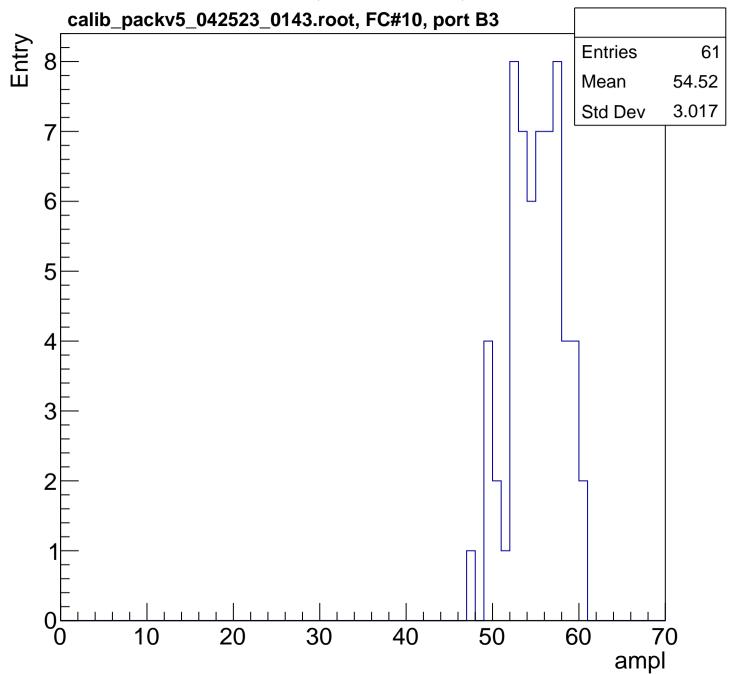


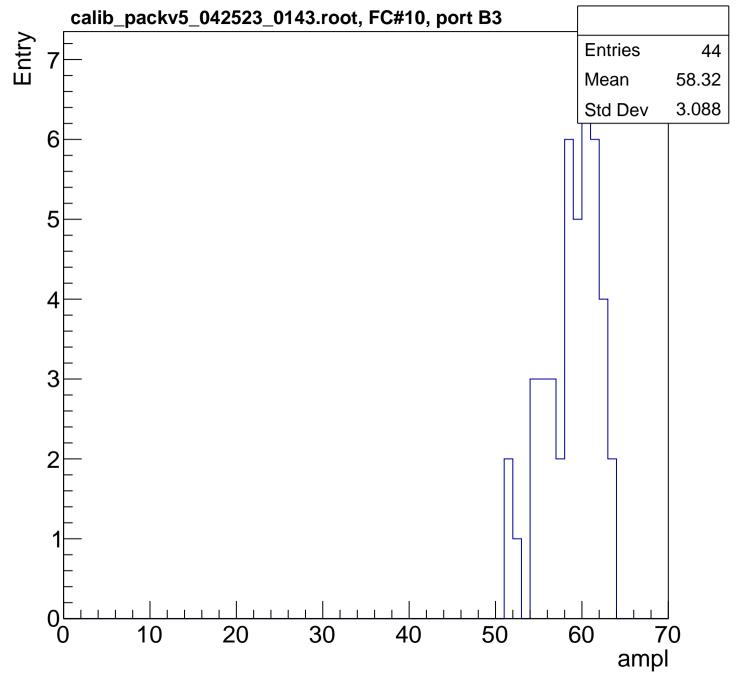


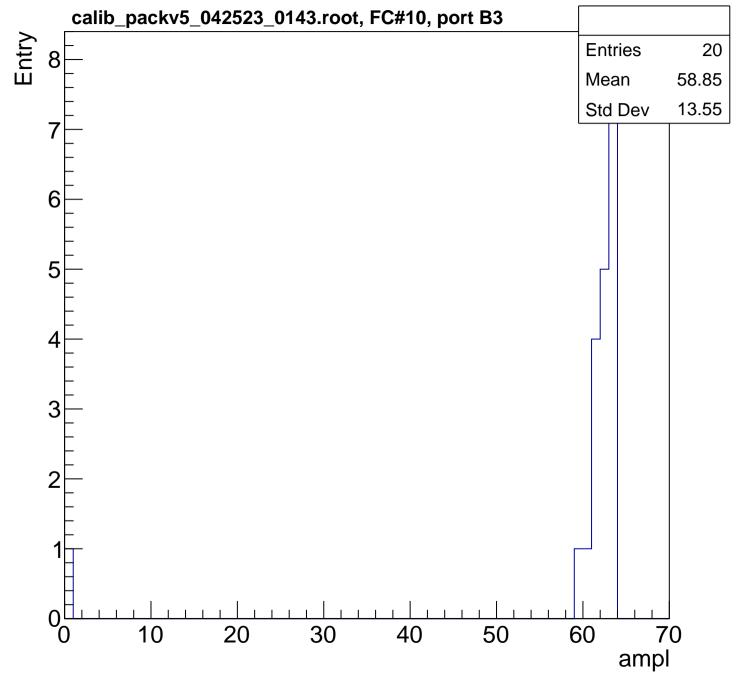


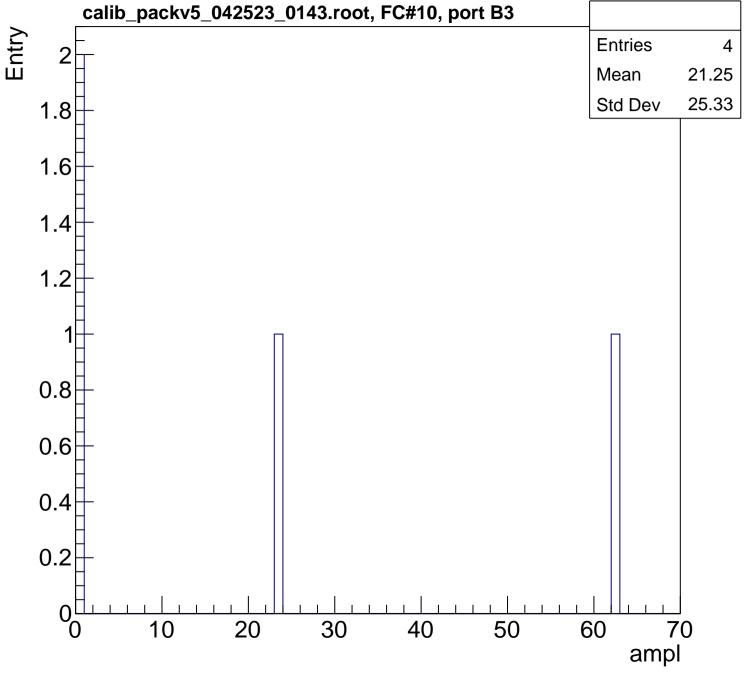


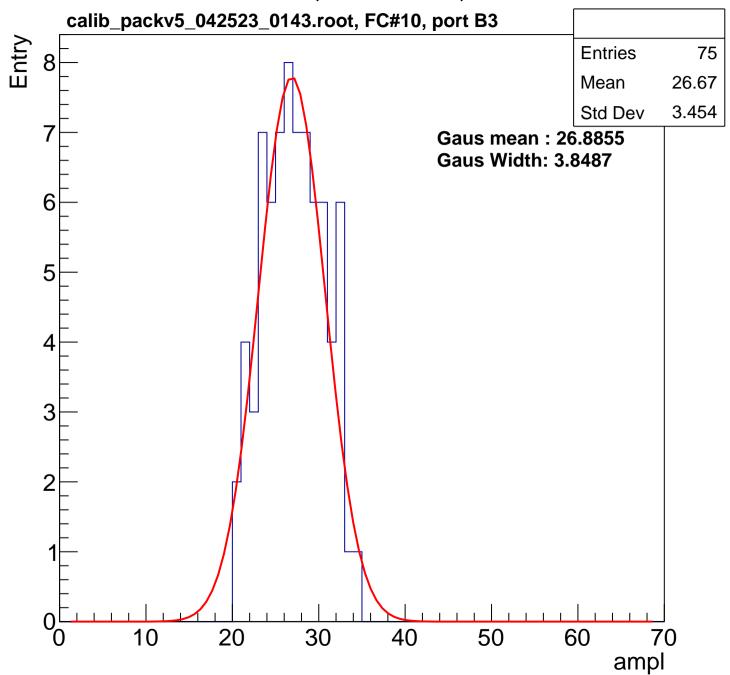


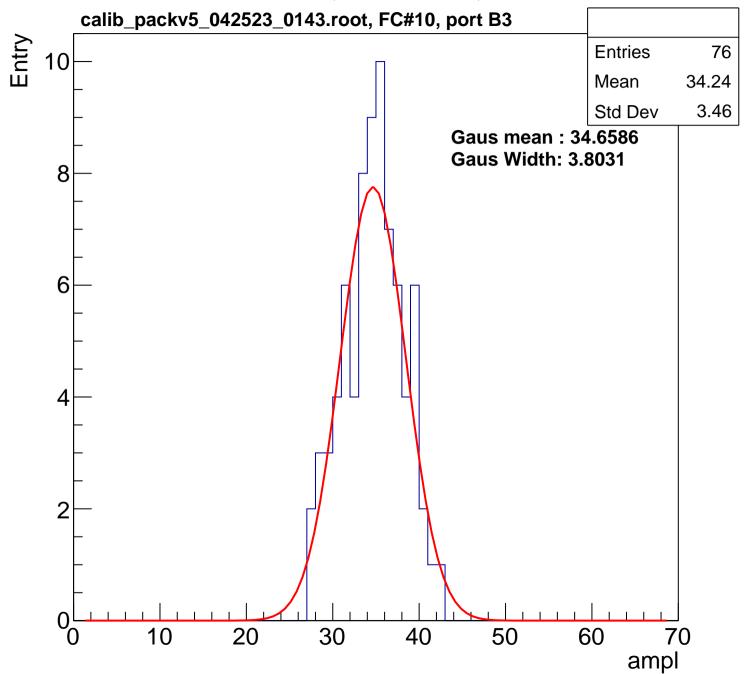


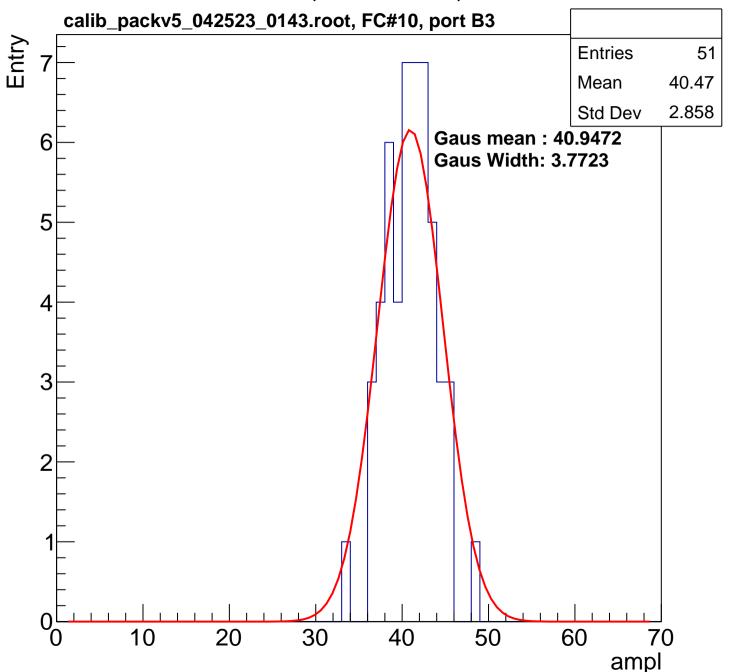


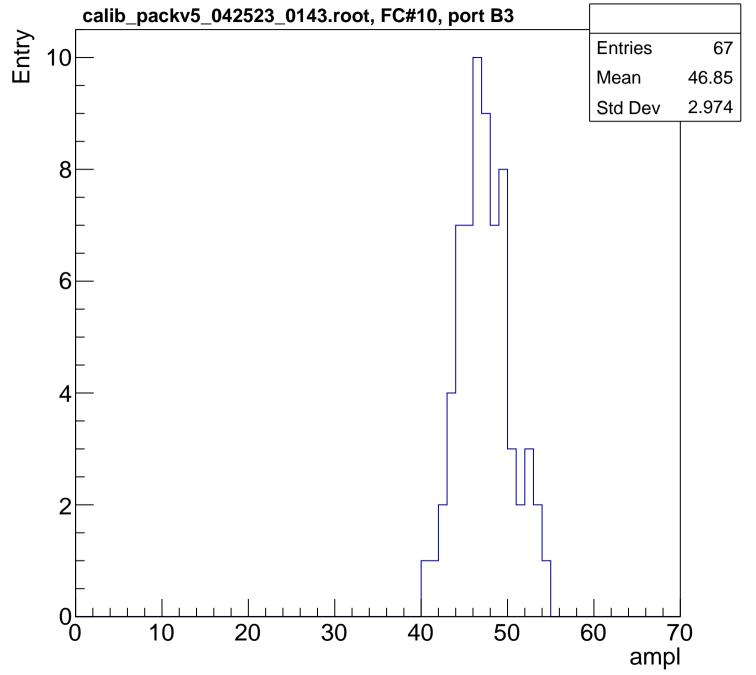


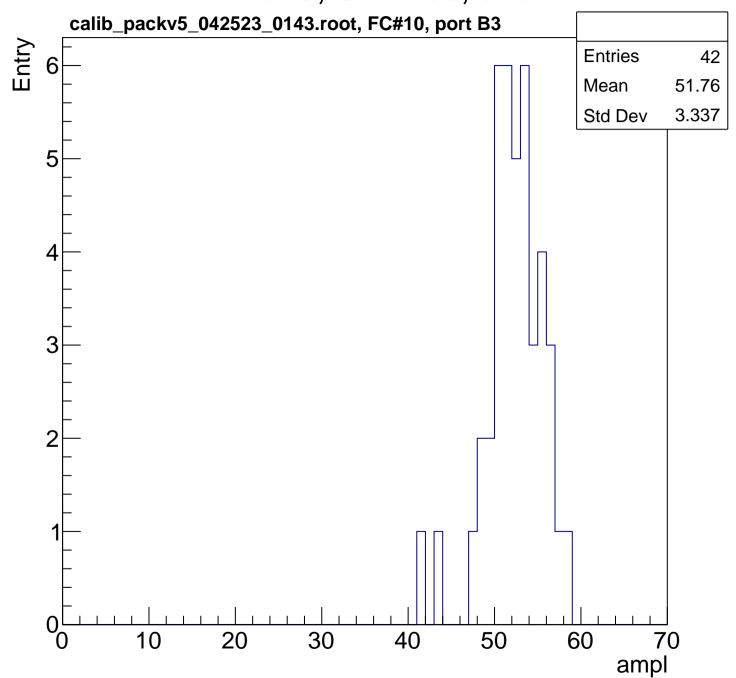


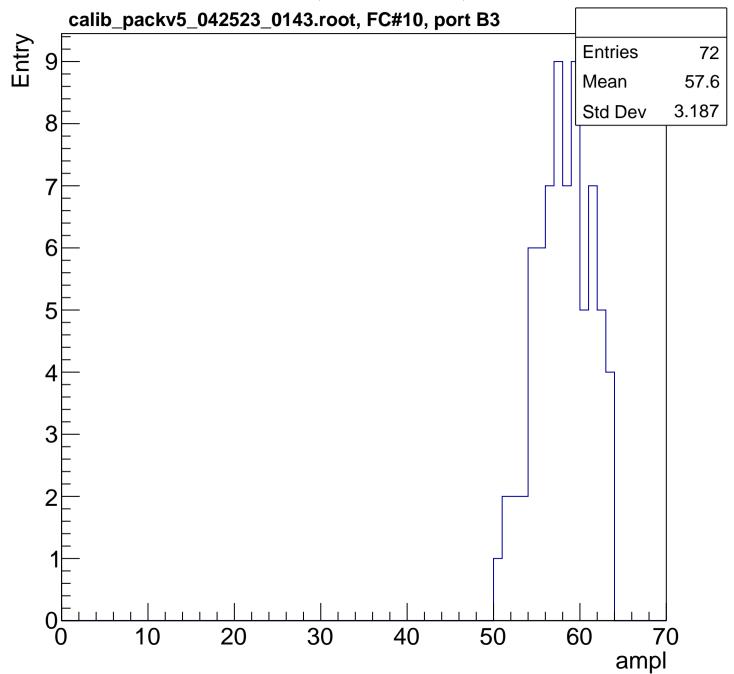


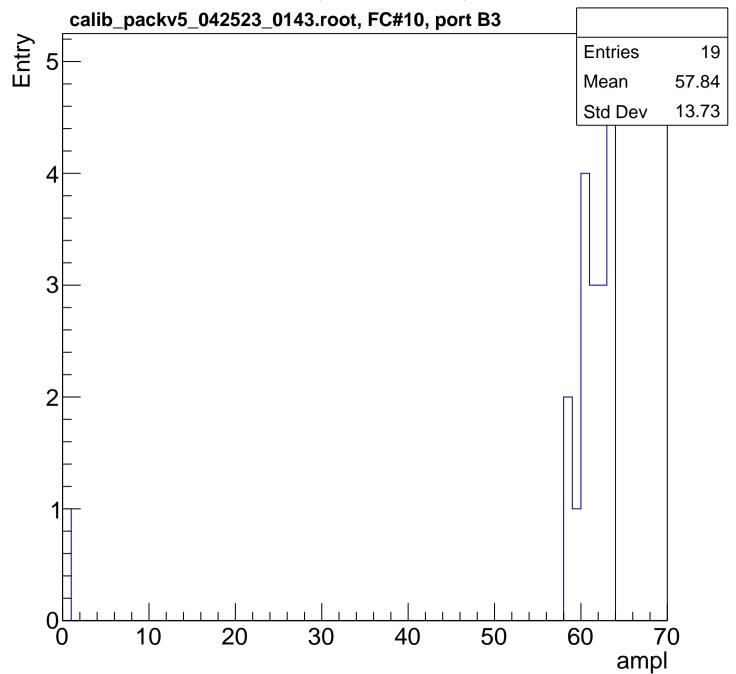


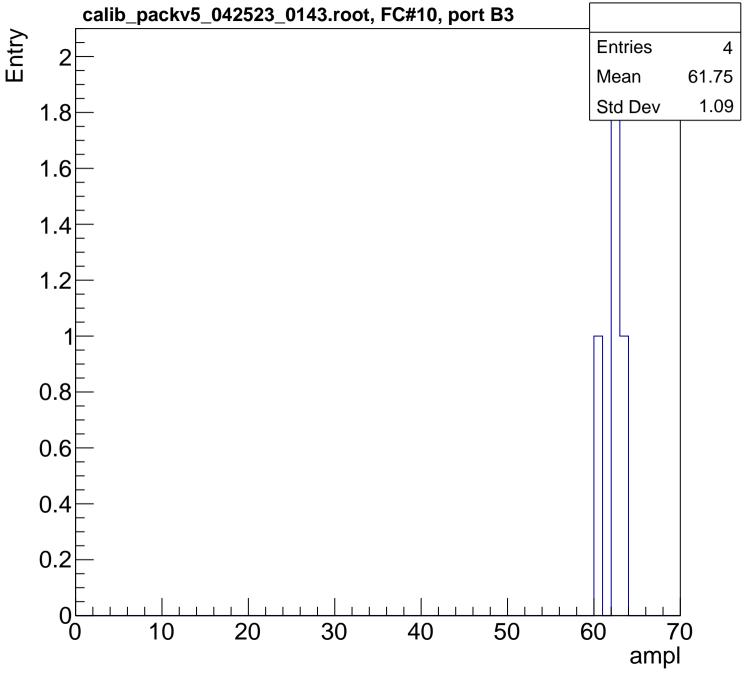


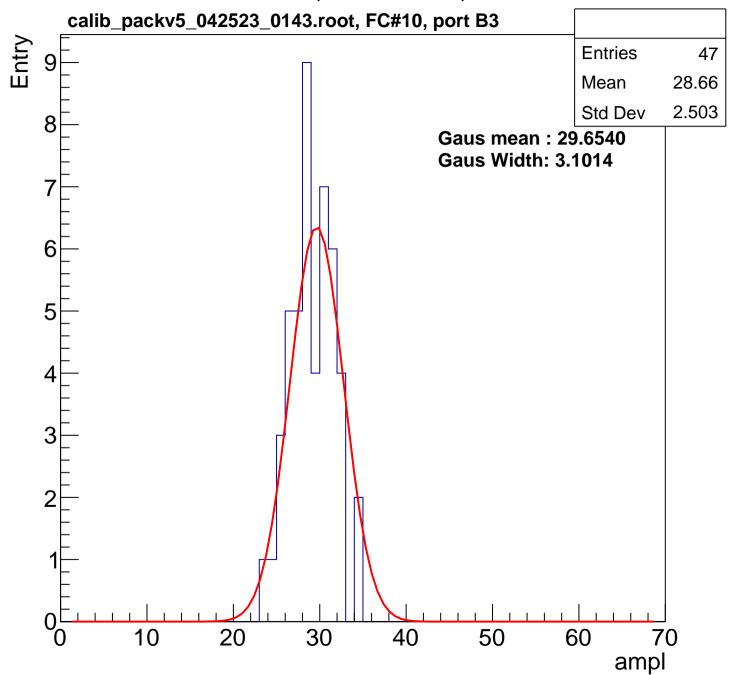


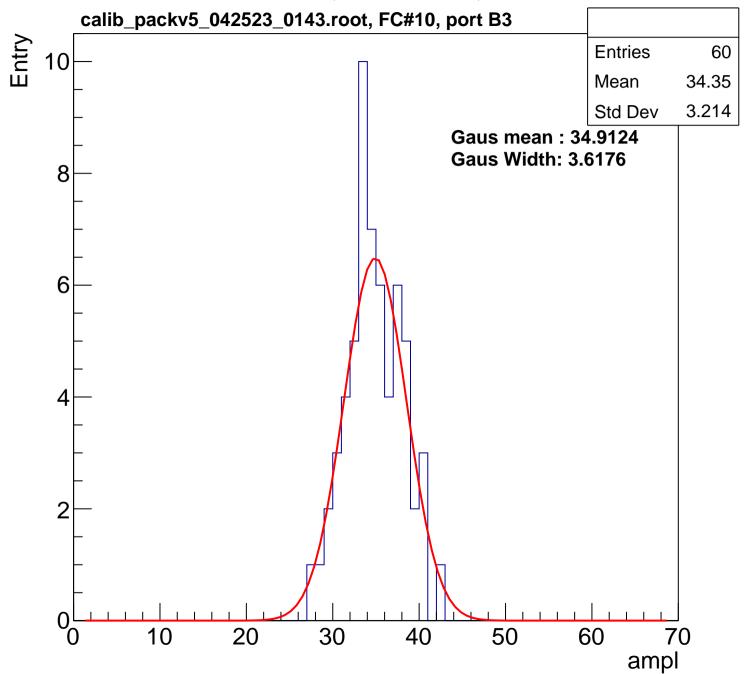


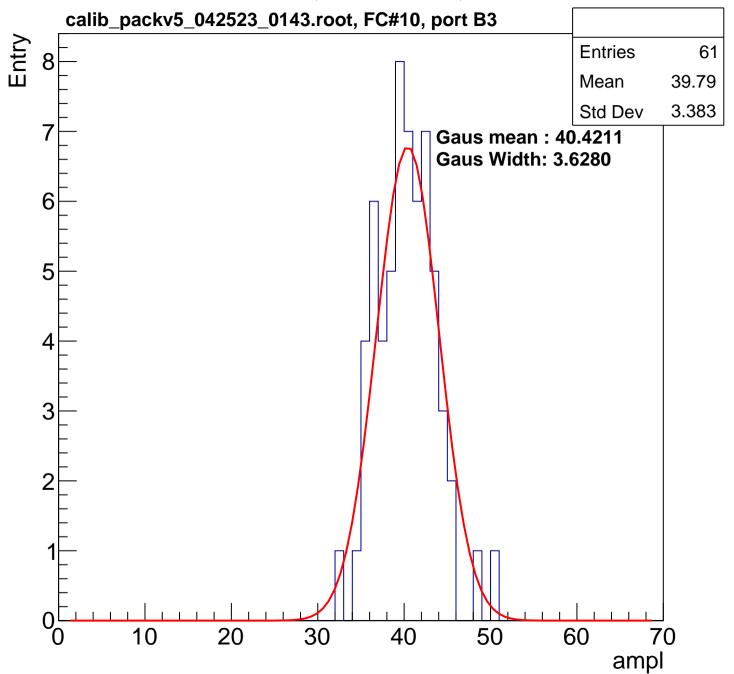


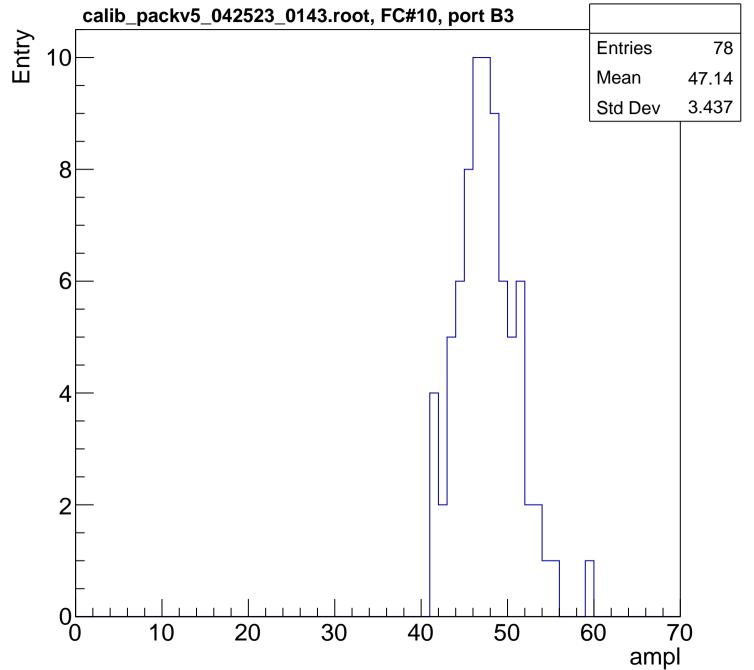


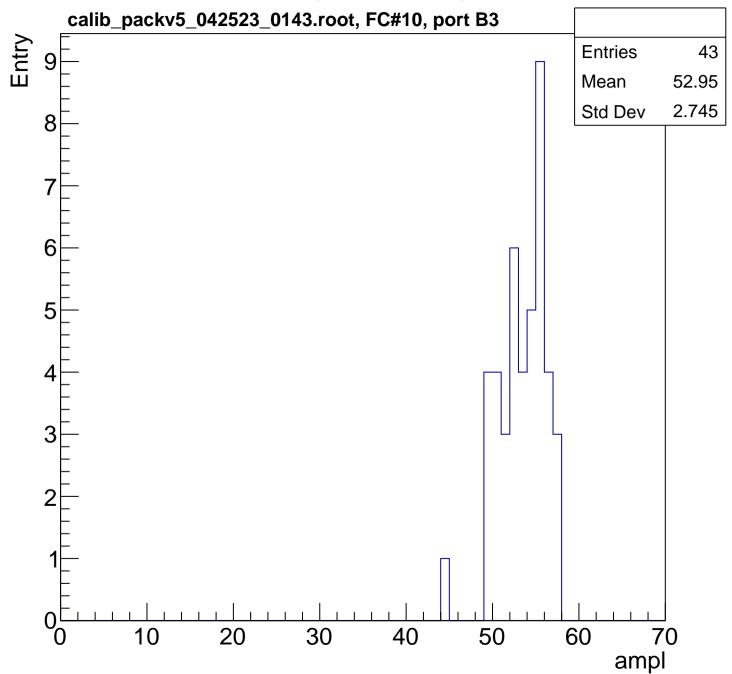


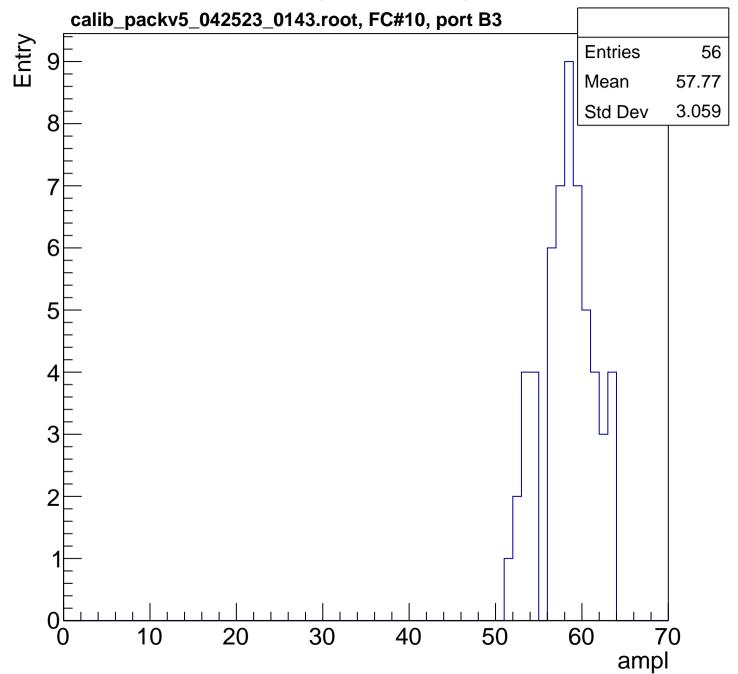


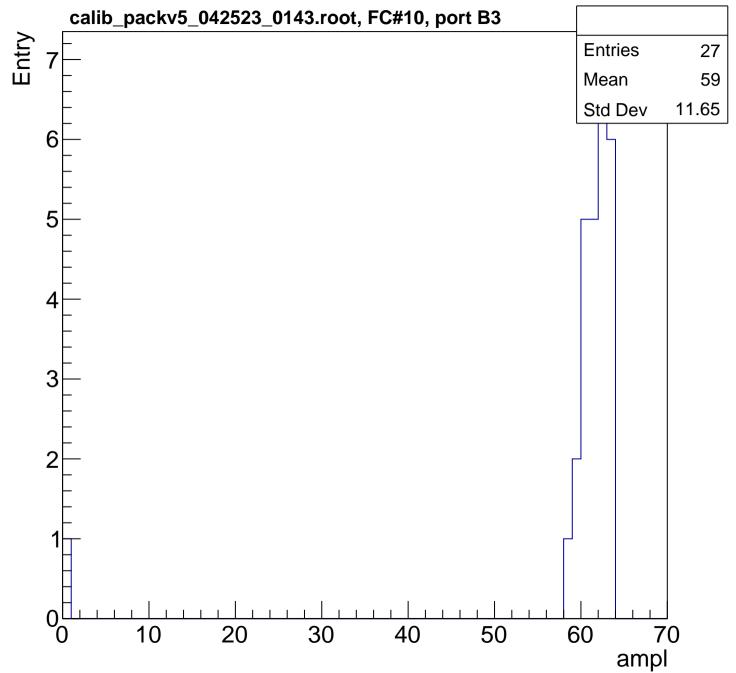


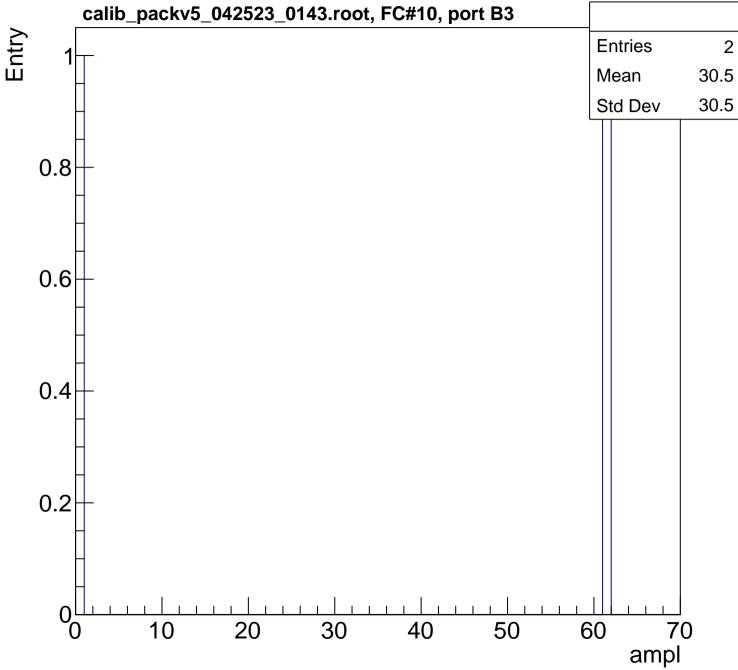


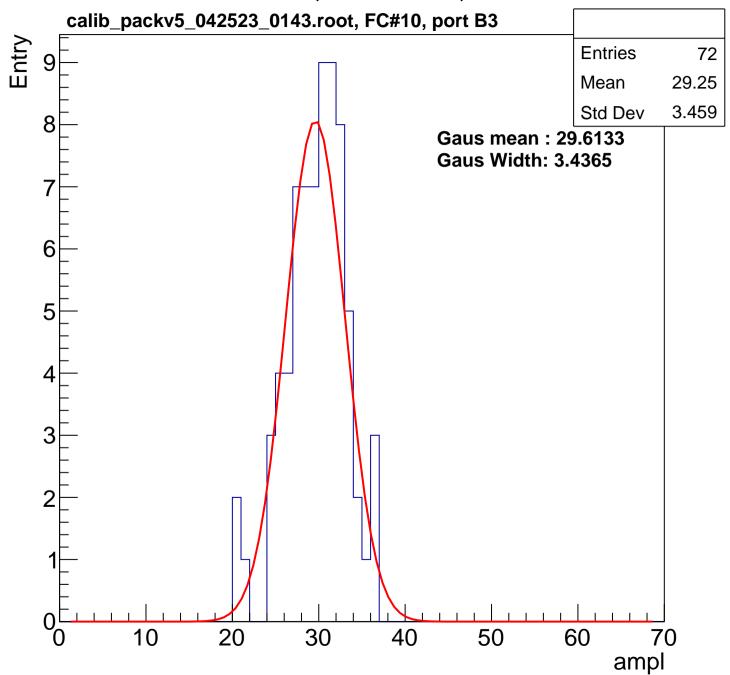


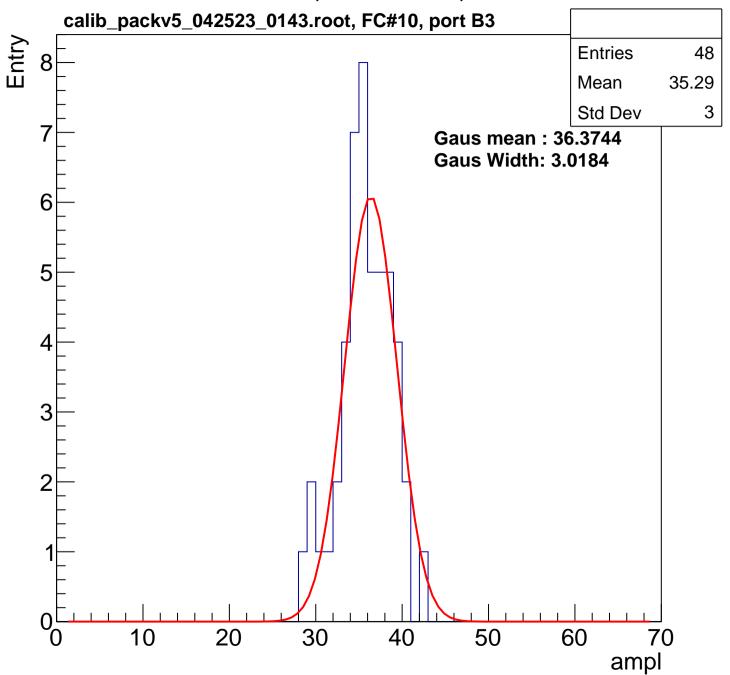


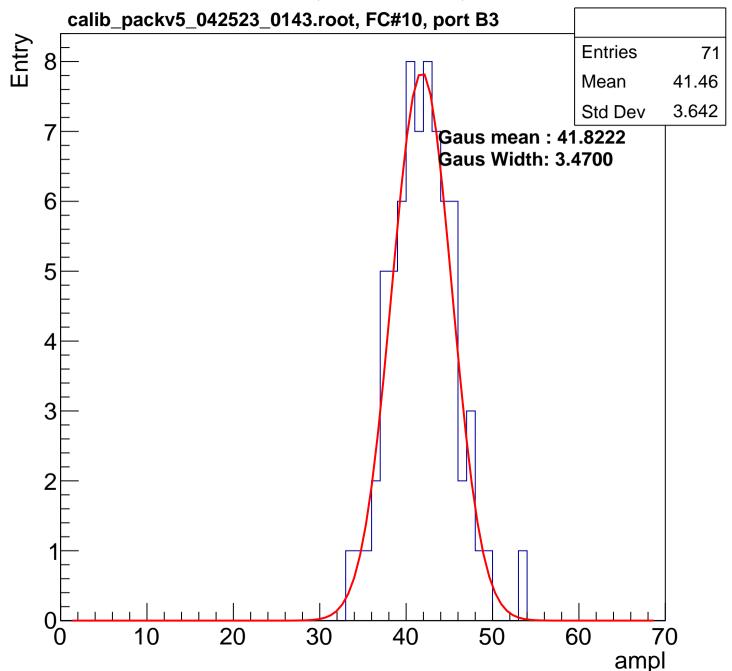


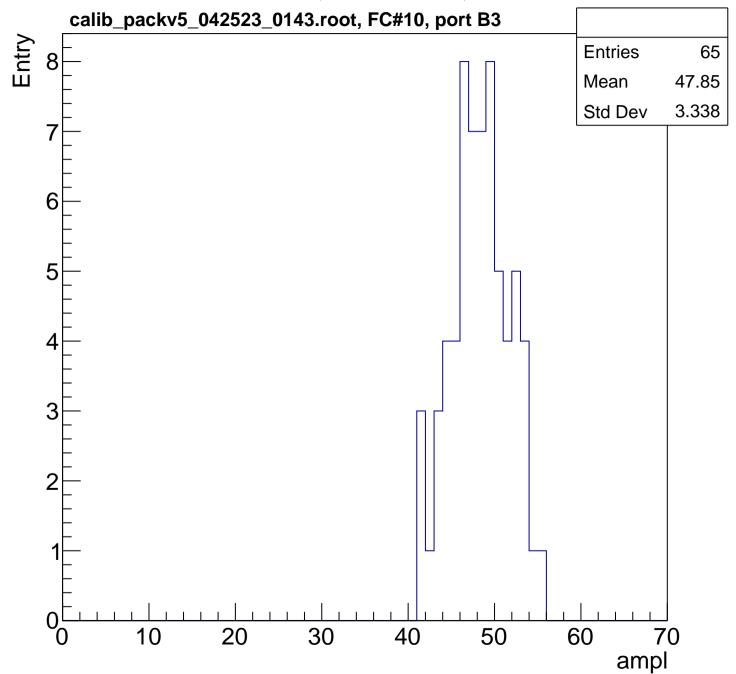


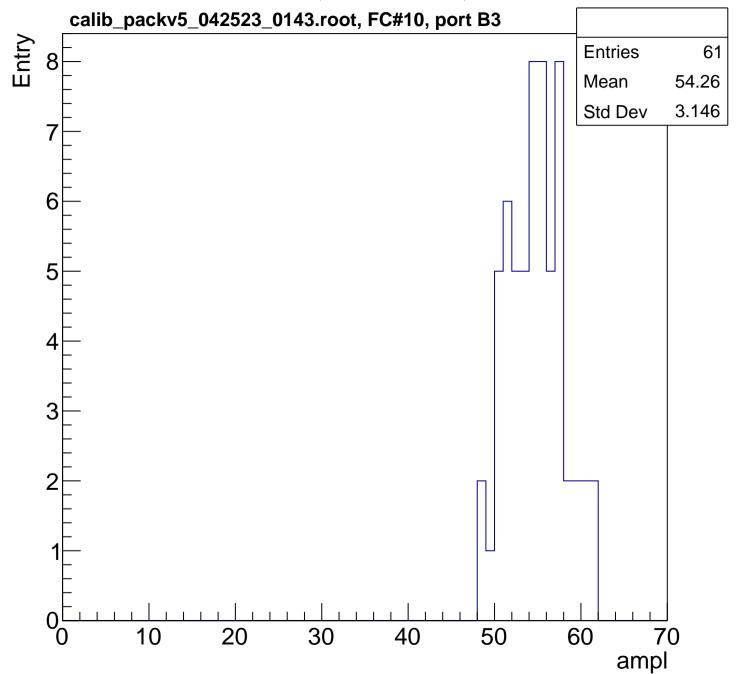


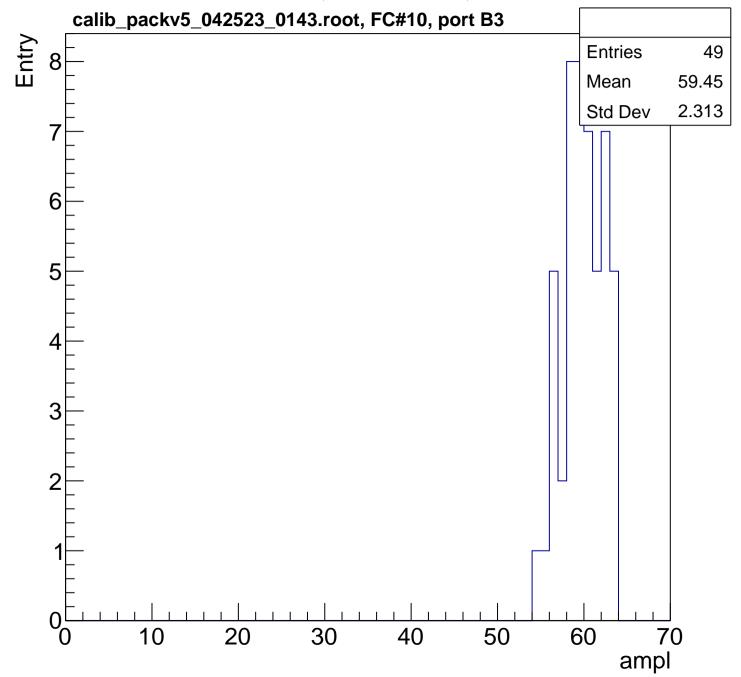


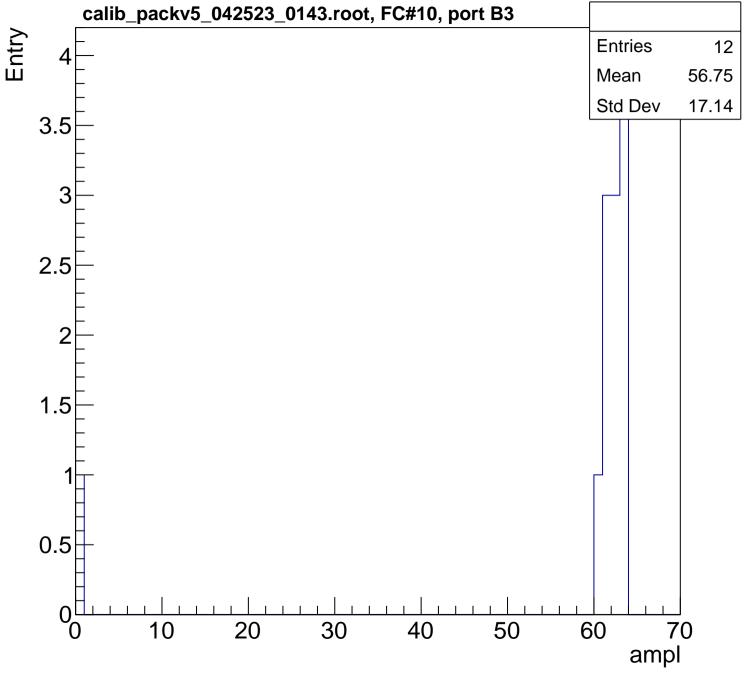




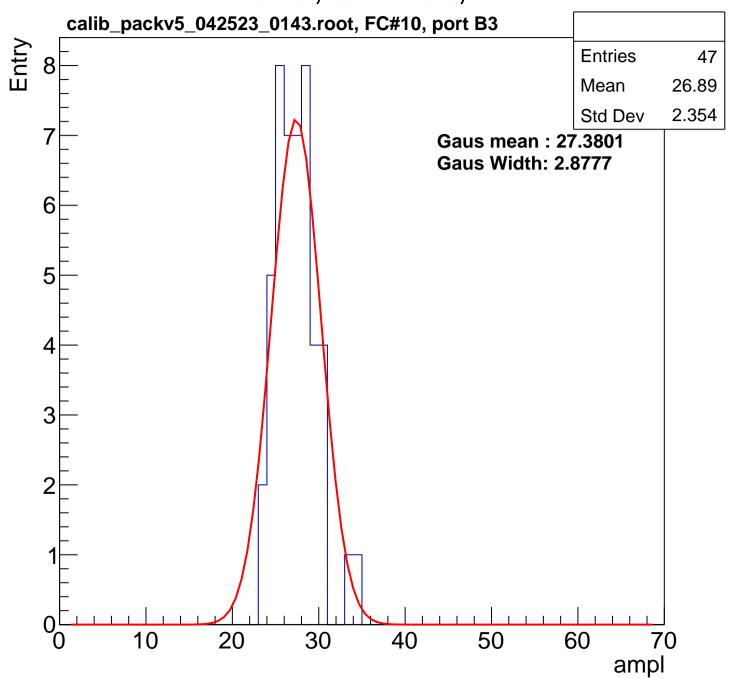


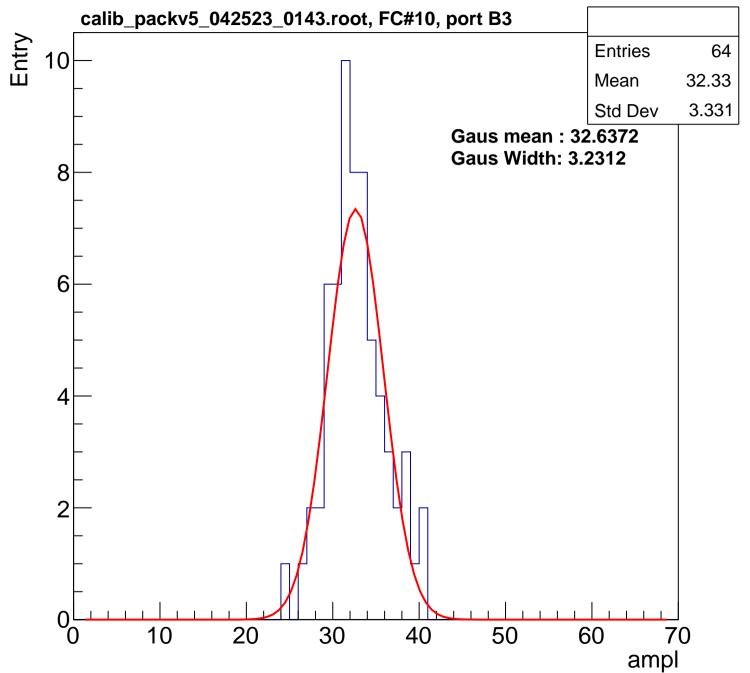


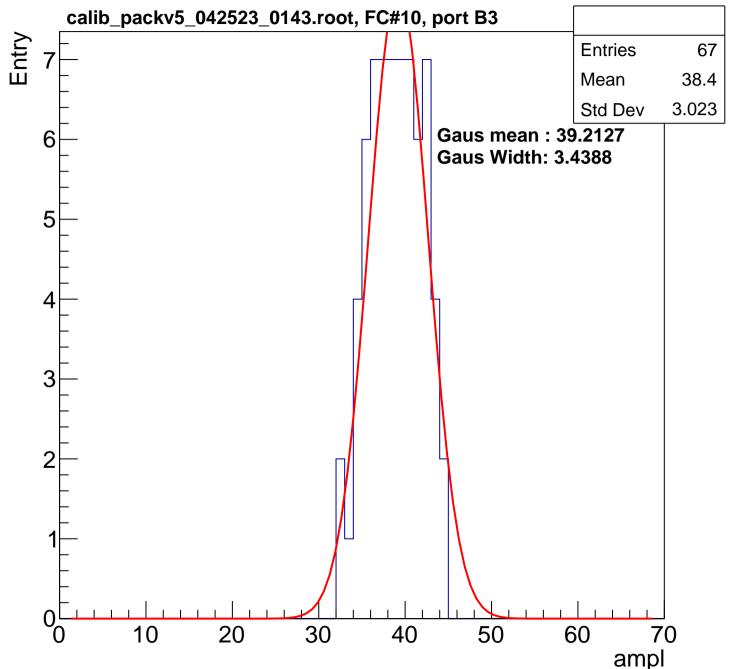


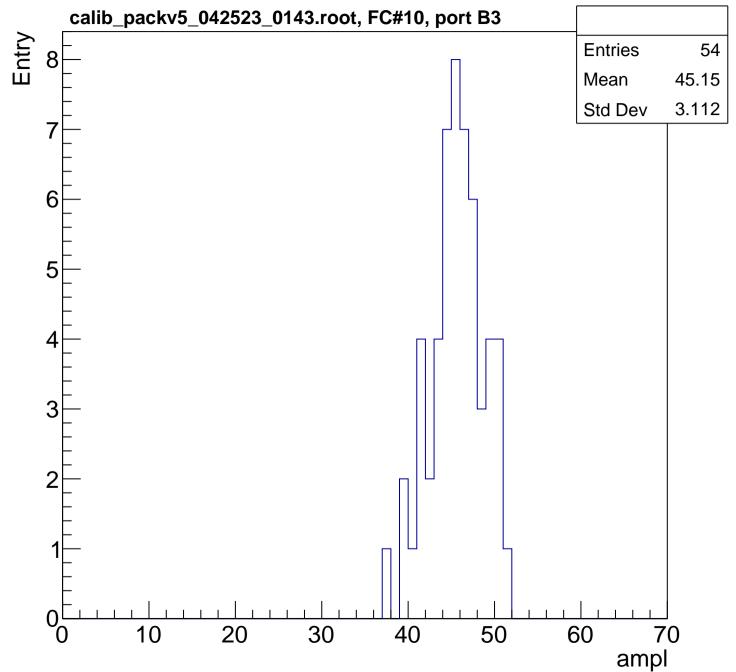


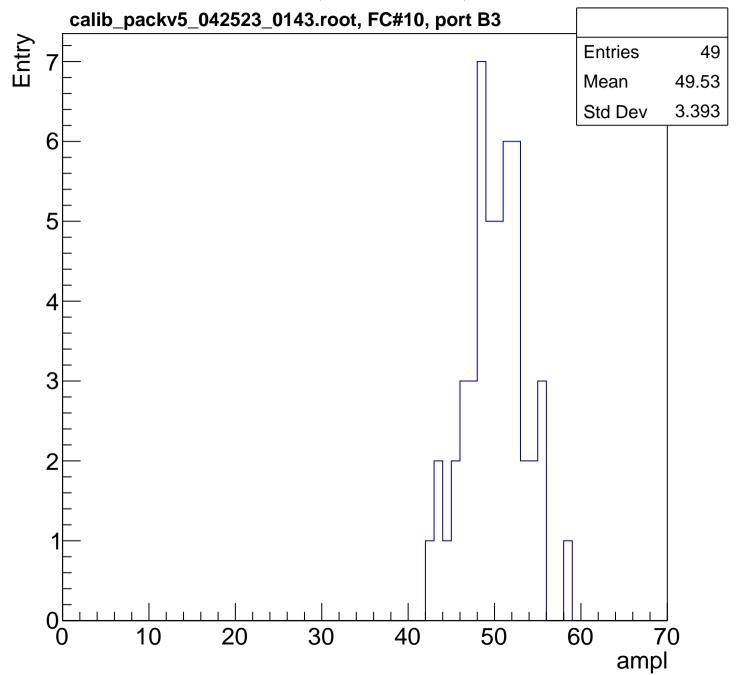


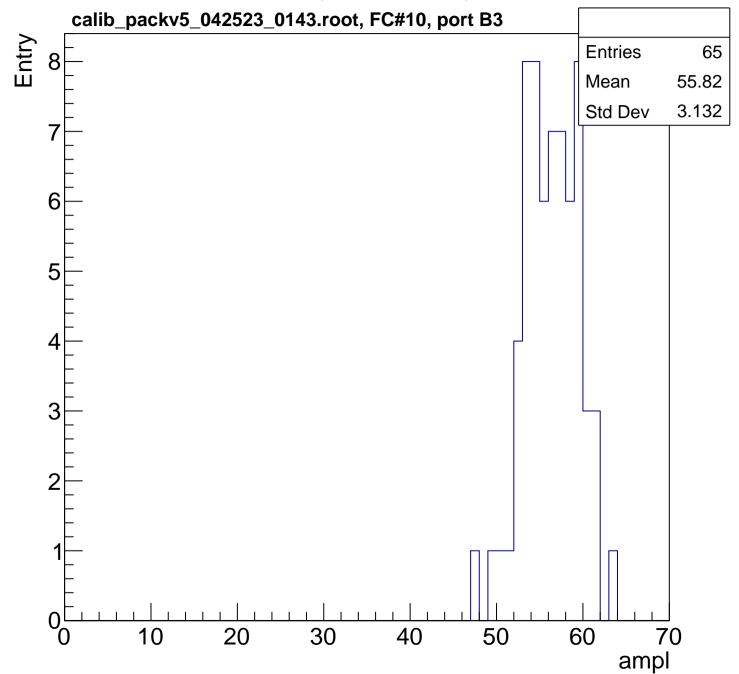


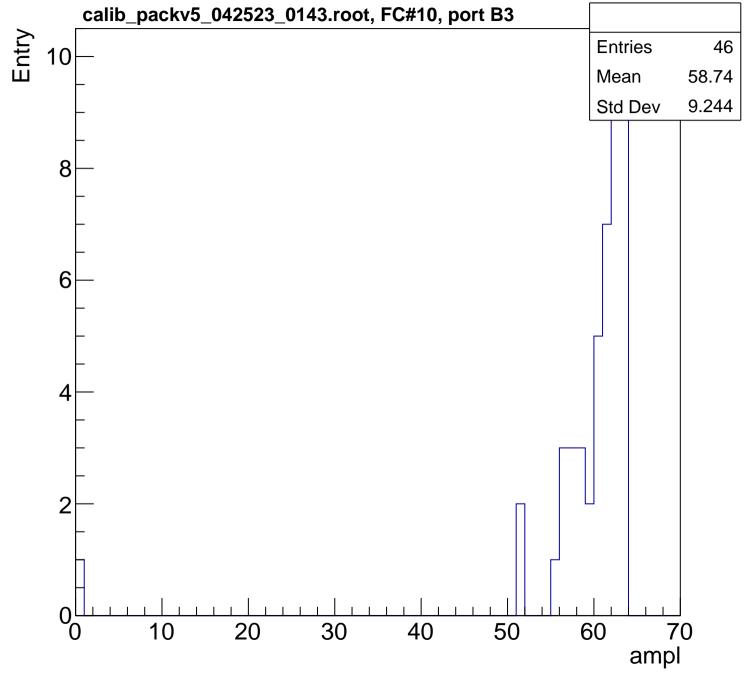


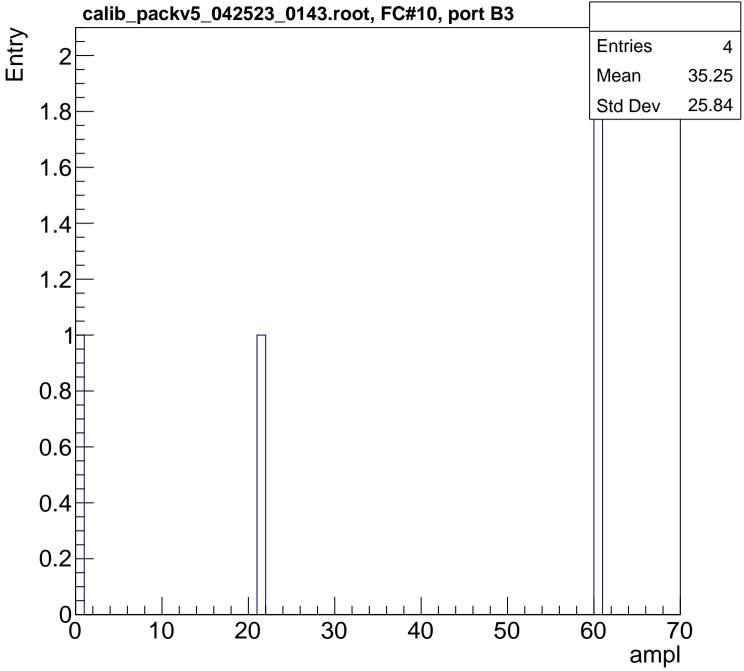


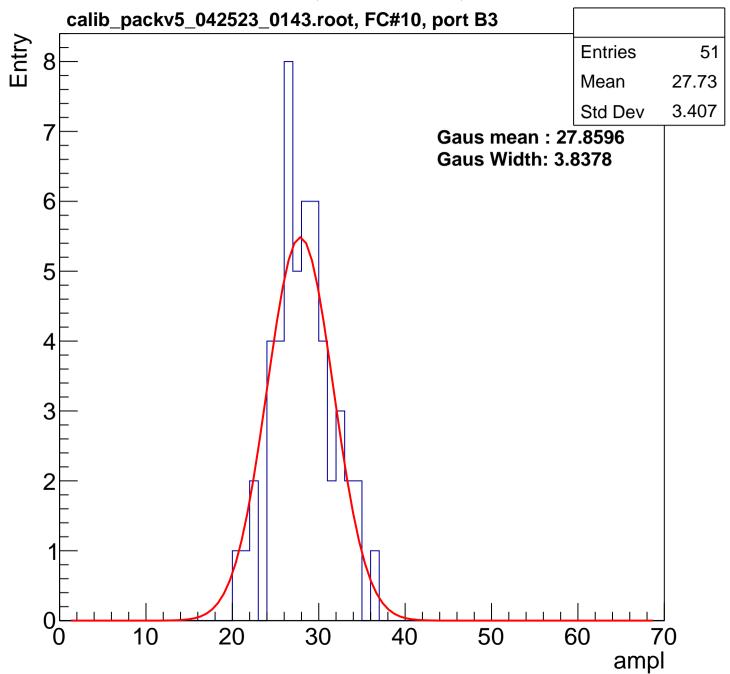


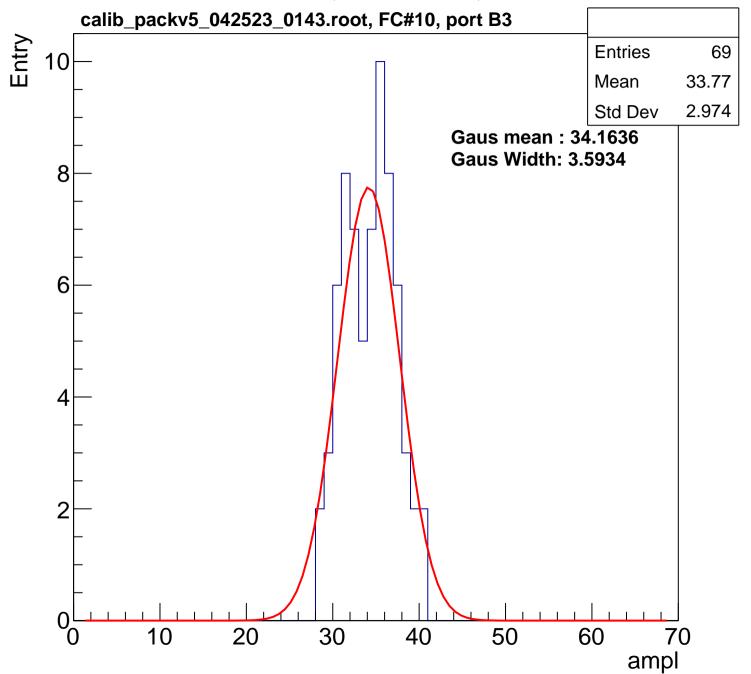


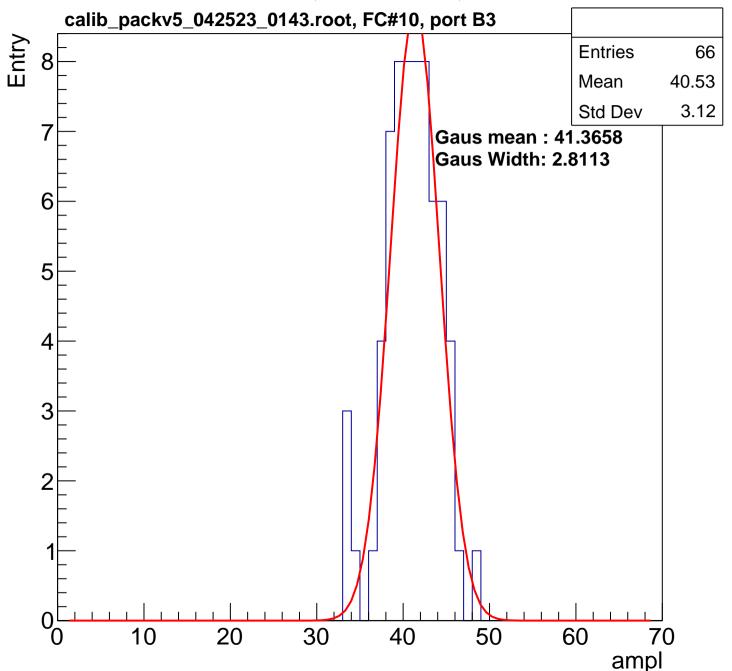


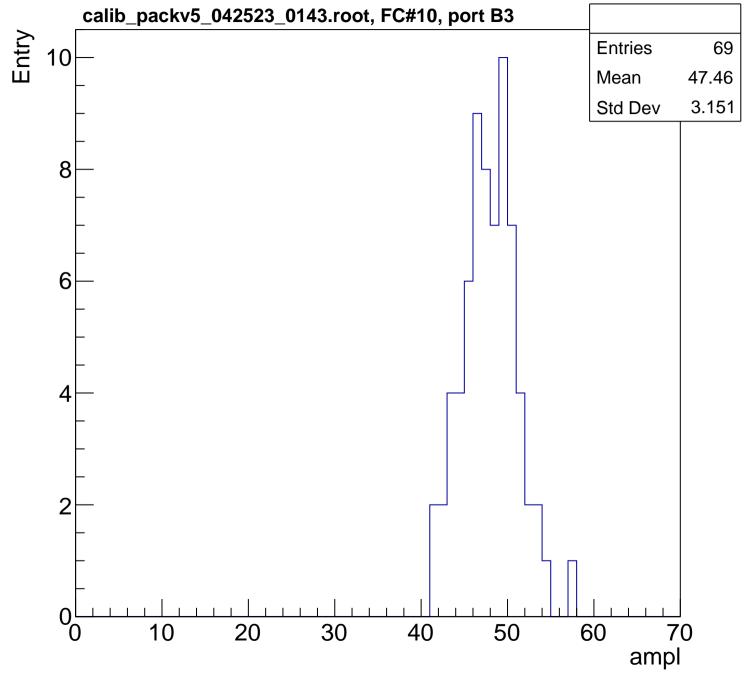


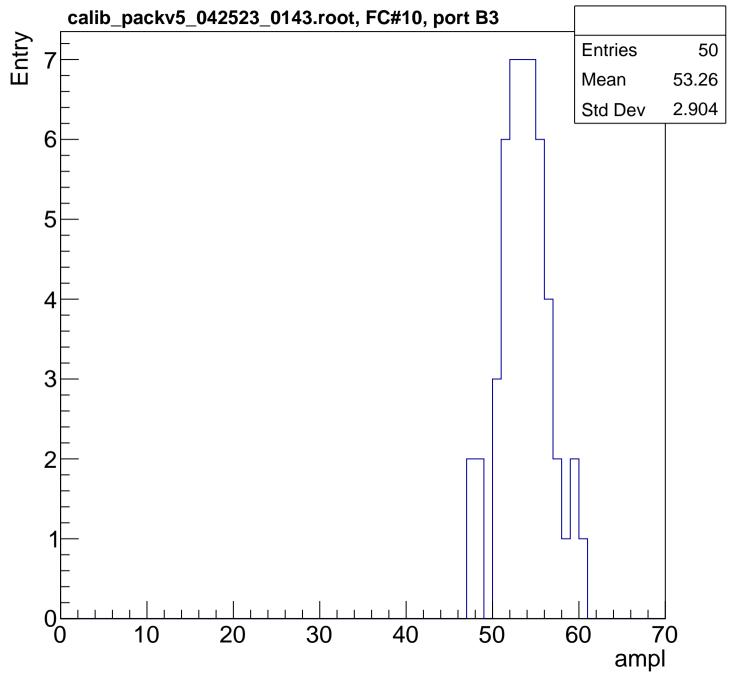


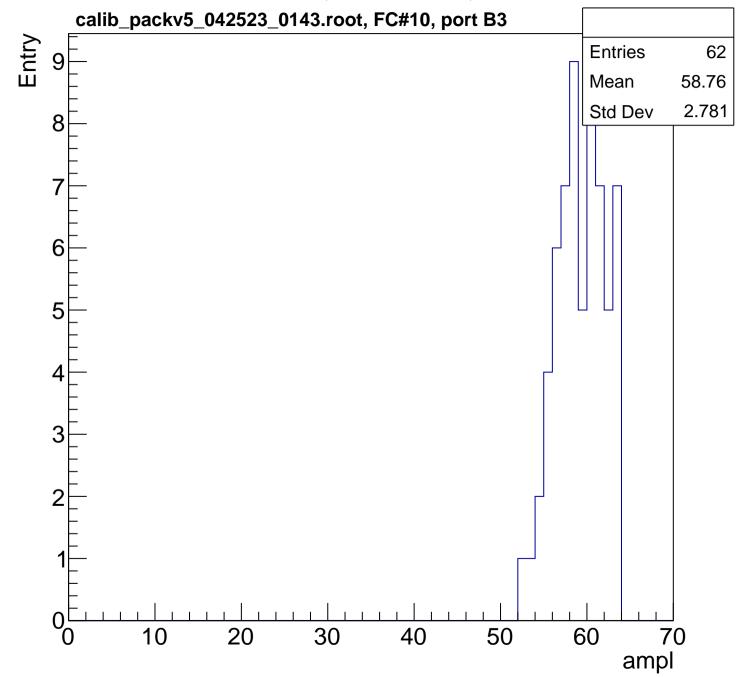


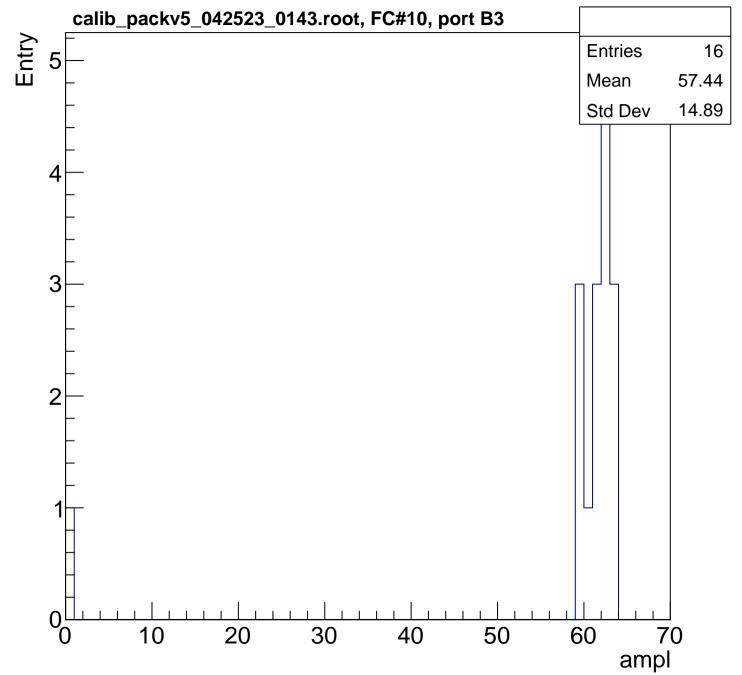




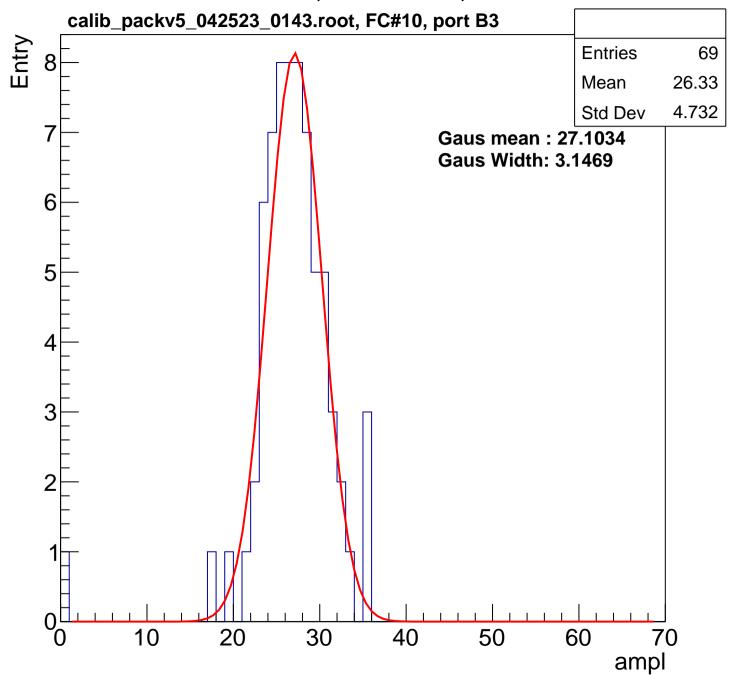


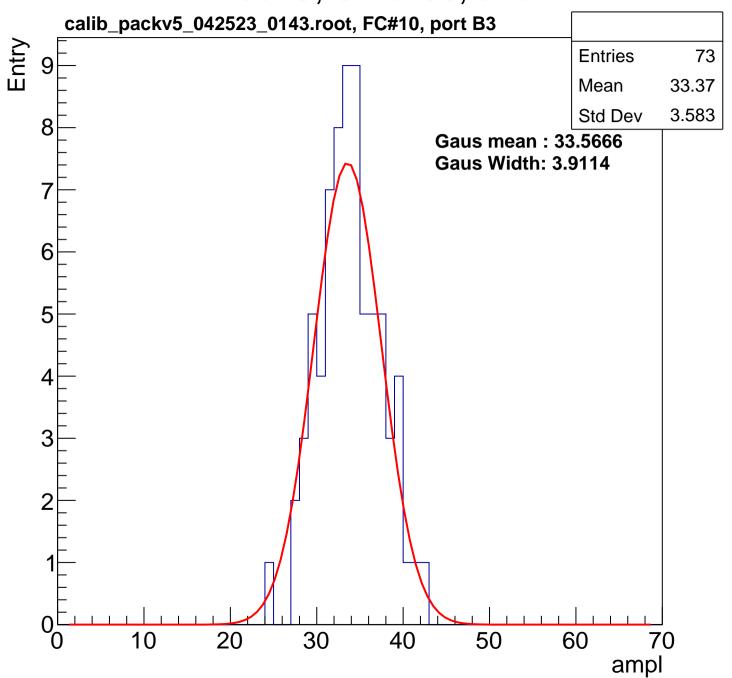


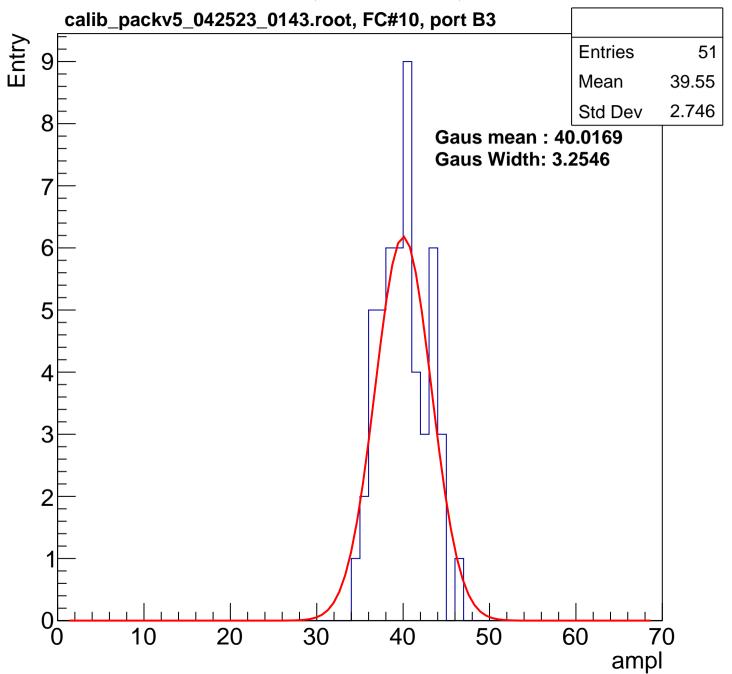


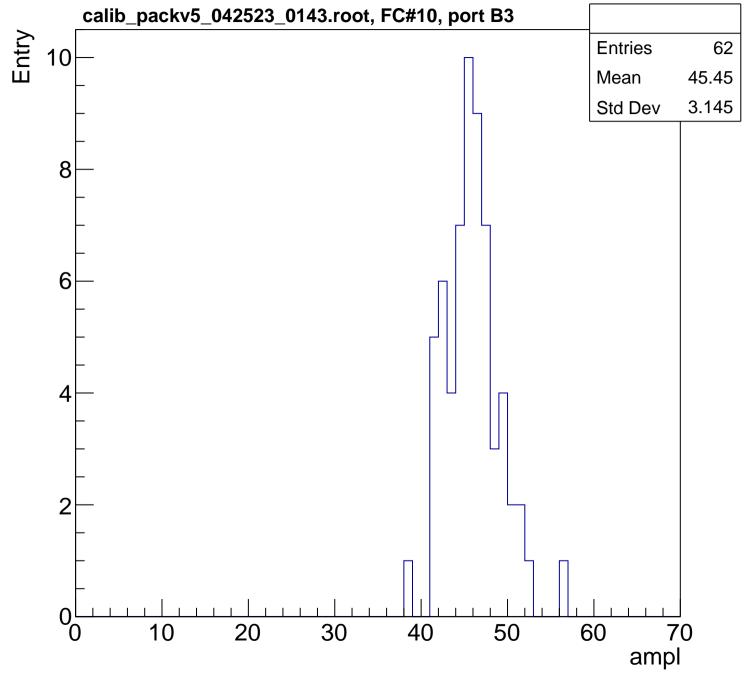


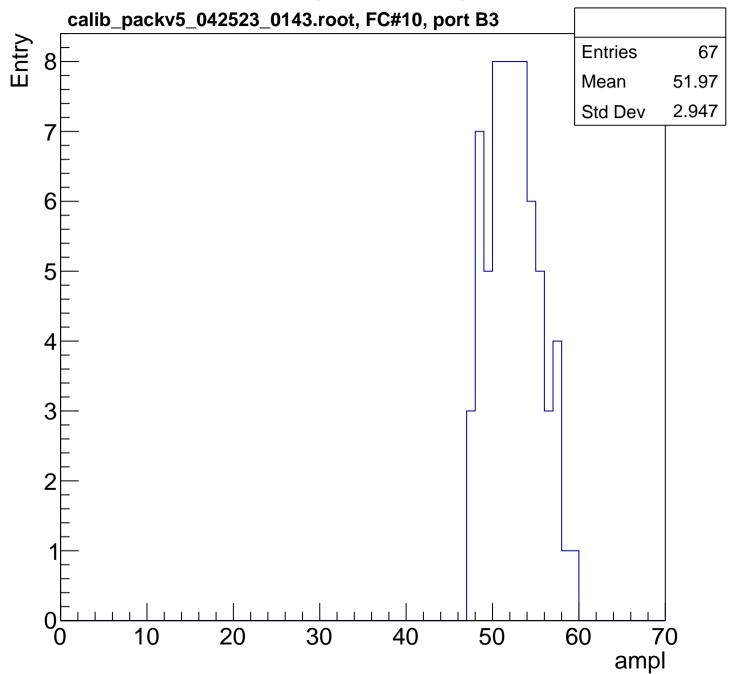
B1L002S, U4-ch97, adc7 calib\_packv5\_042523\_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

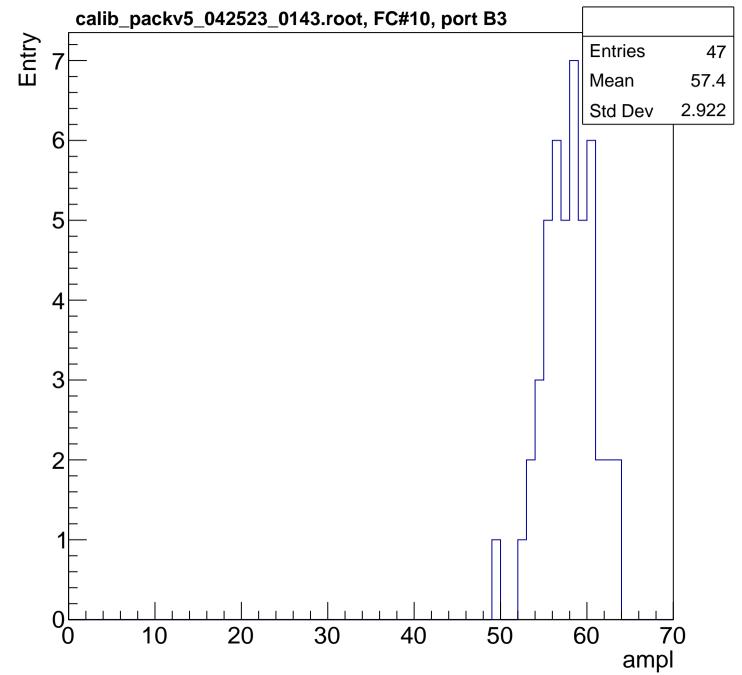


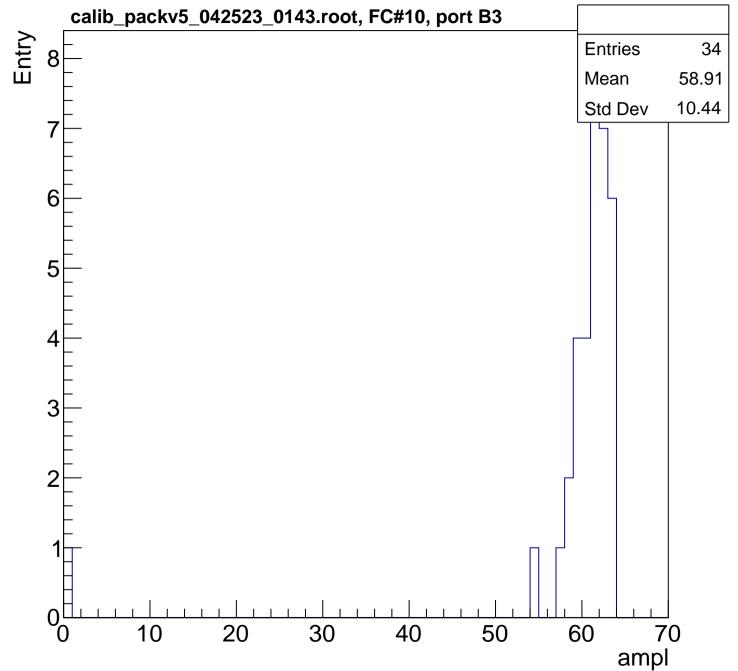


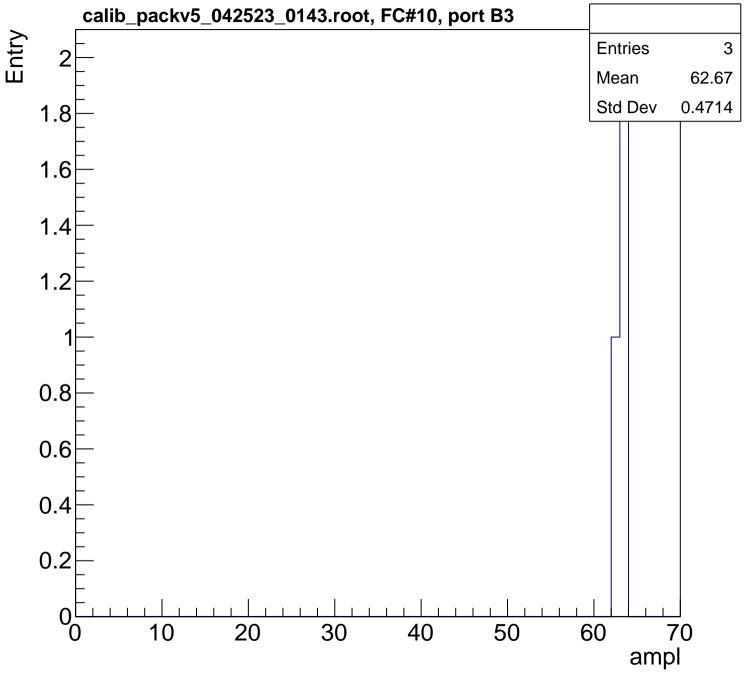


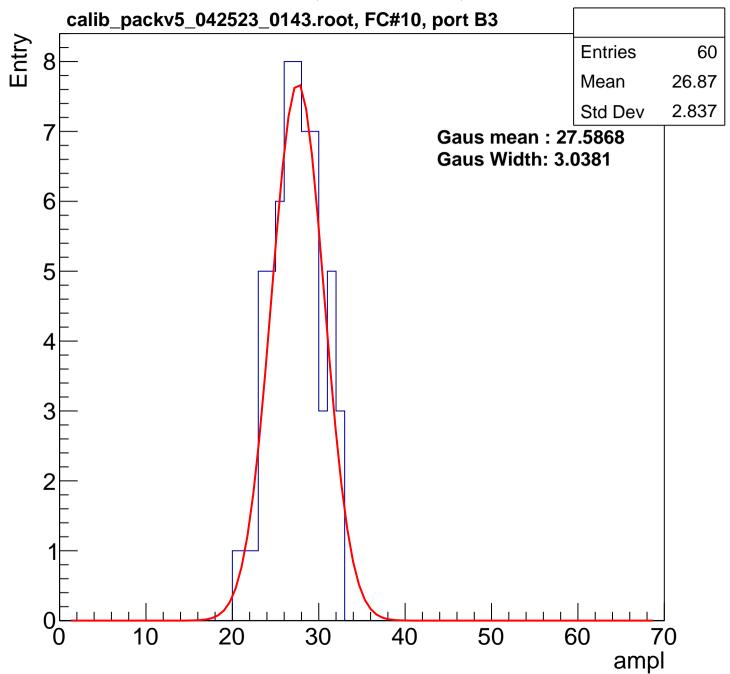


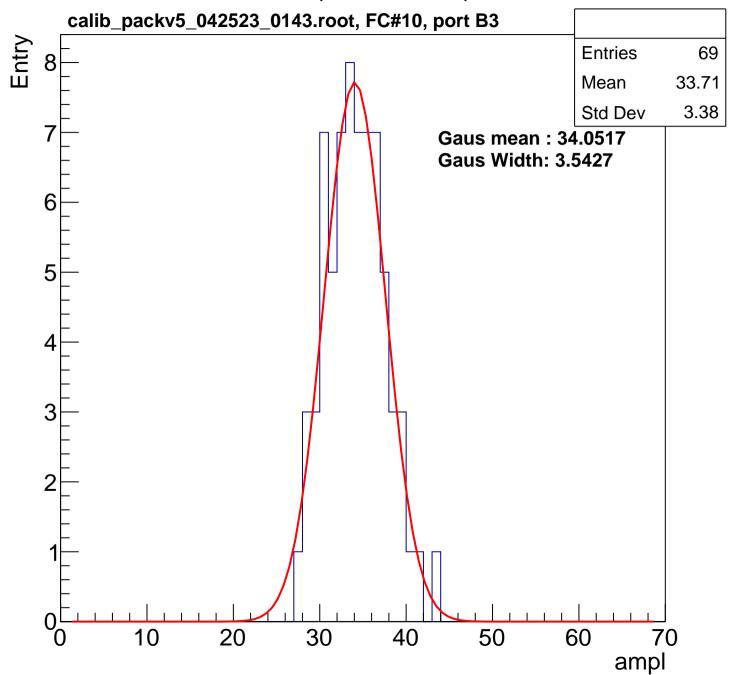


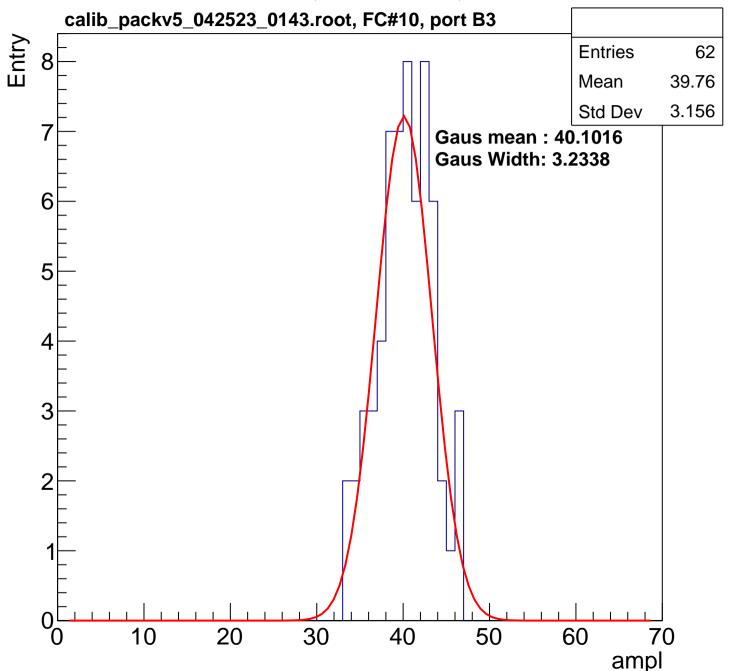


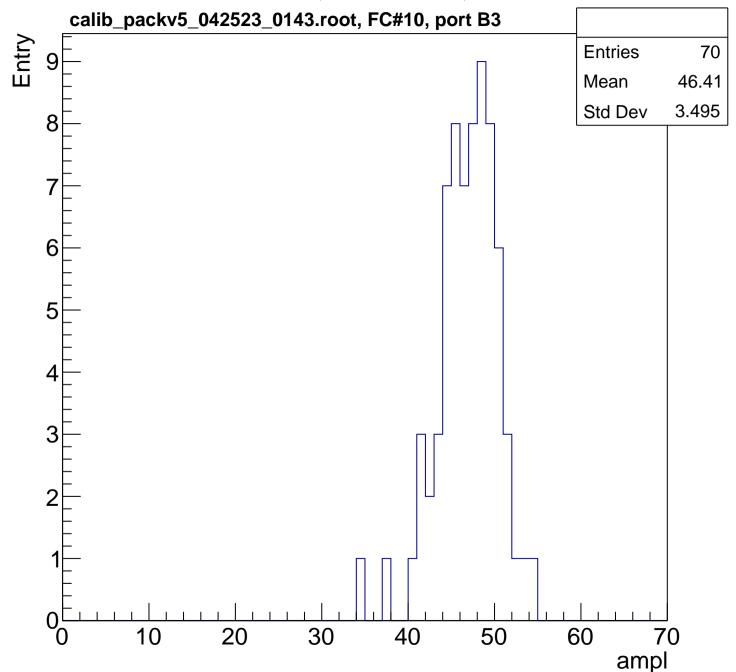


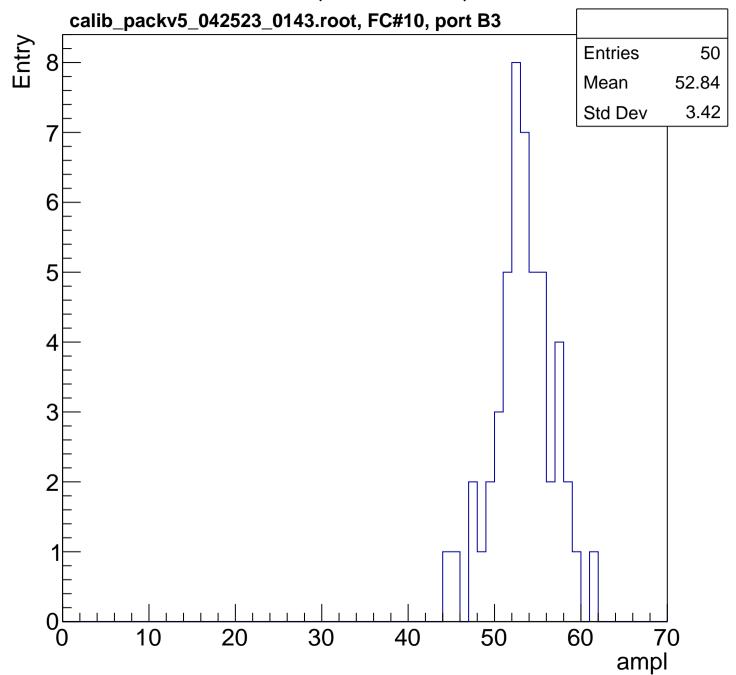


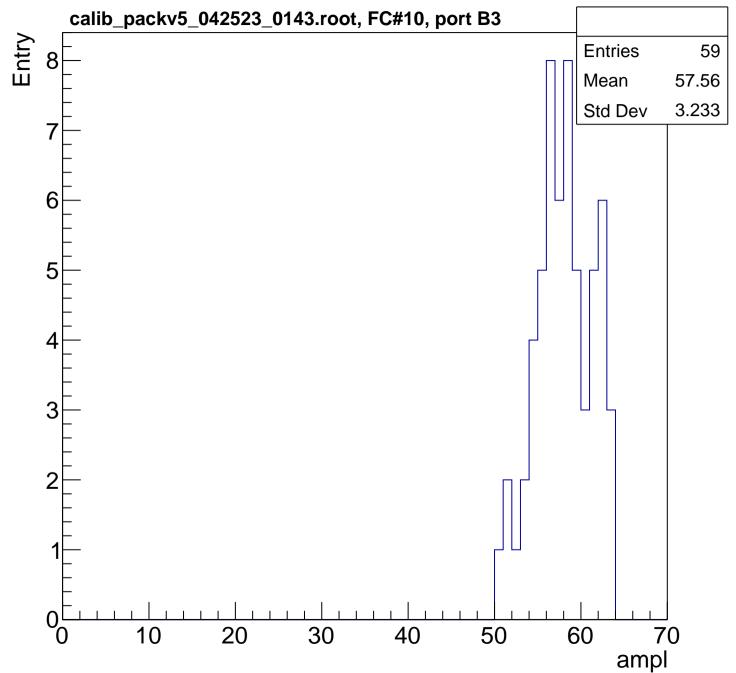


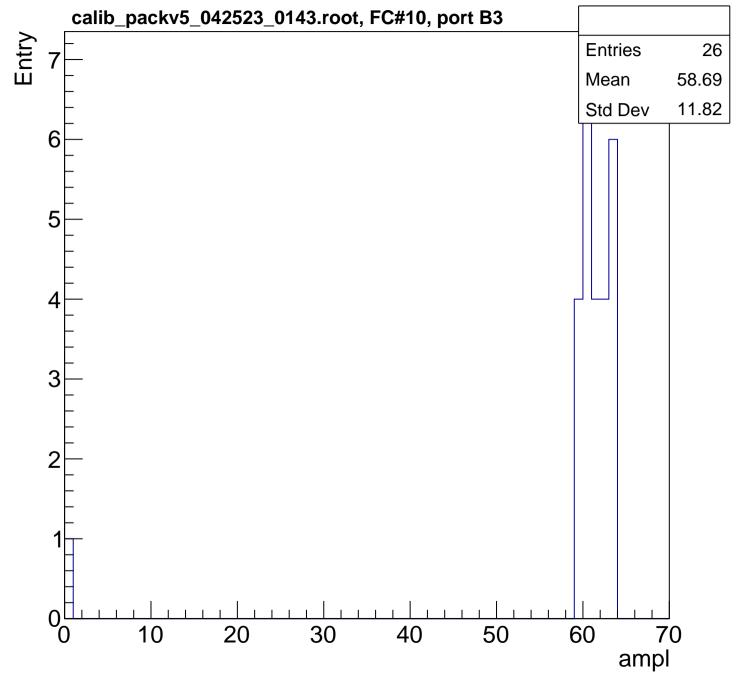












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