



# B0L000S, U26-ch0, adc0

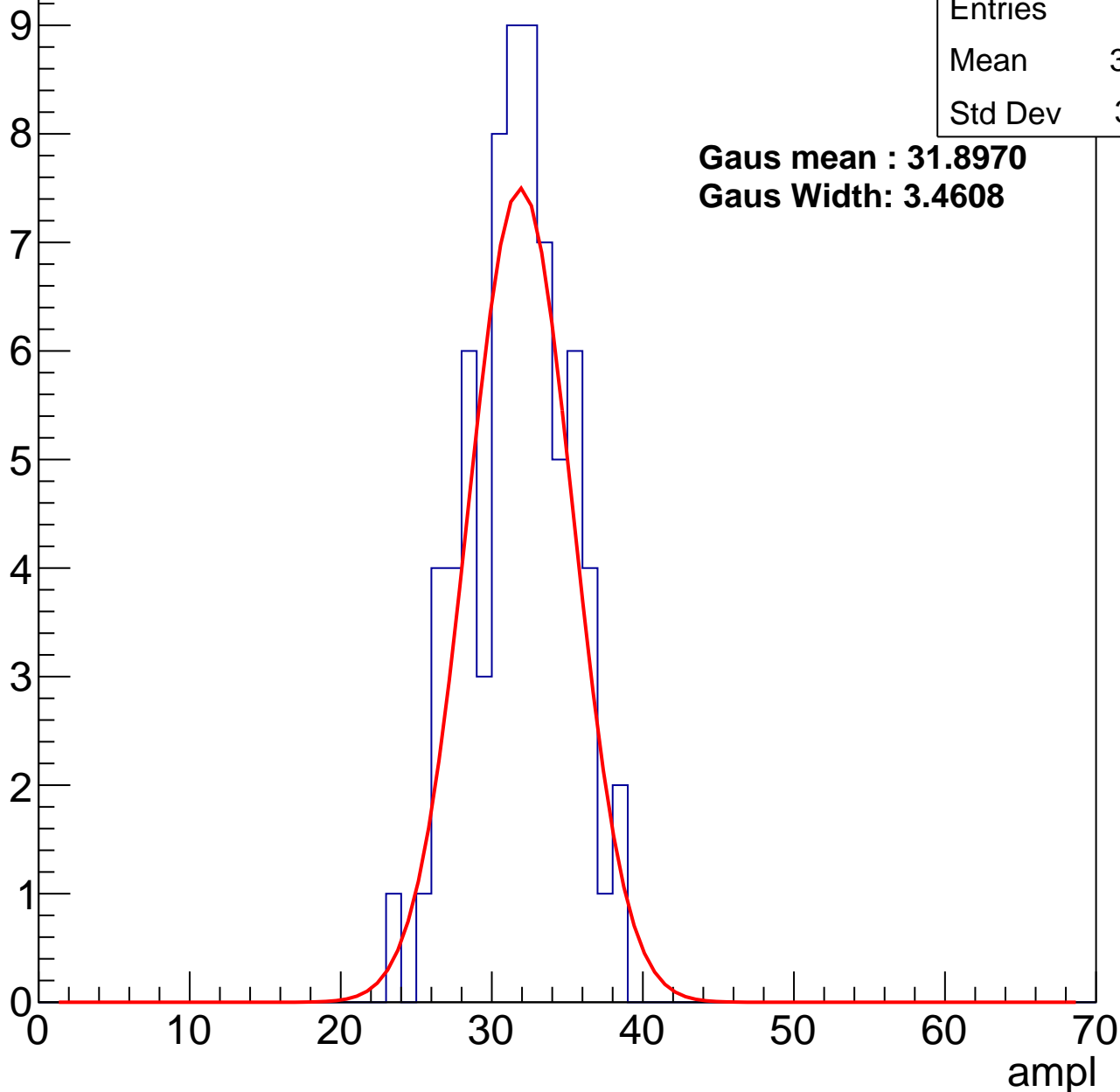
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	31.29
Std Dev	3.261

**Gaus mean : 31.8970**

**Gaus Width: 3.4608**



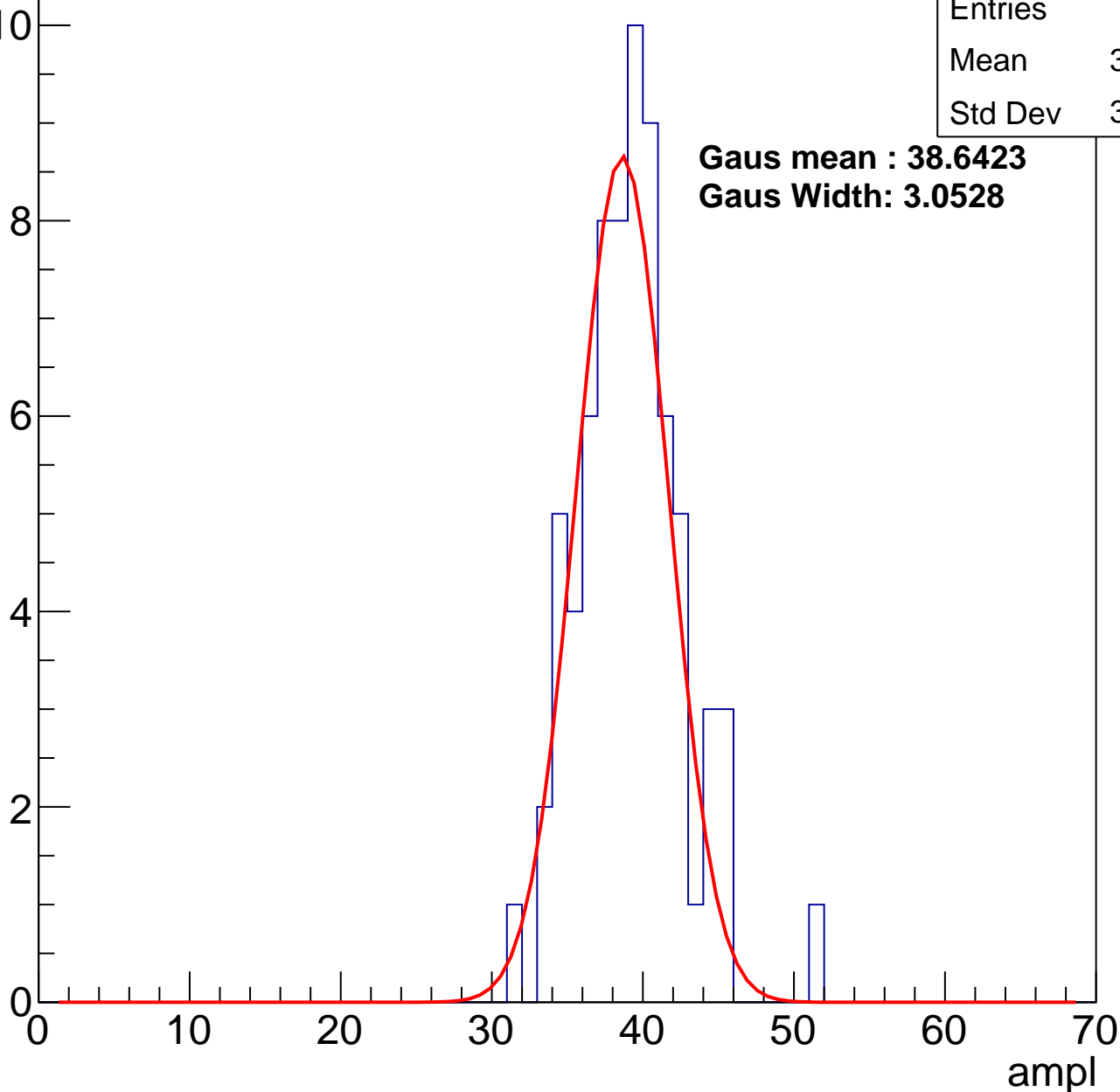
# B0L000S, U26-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	38.75
Std Dev	3.406

**Gaus mean : 38.6423**  
**Gaus Width: 3.0528**



# B0L000S, U26-ch0, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	44.68
Std Dev	2.603

**Gaus mean : 44.7877**

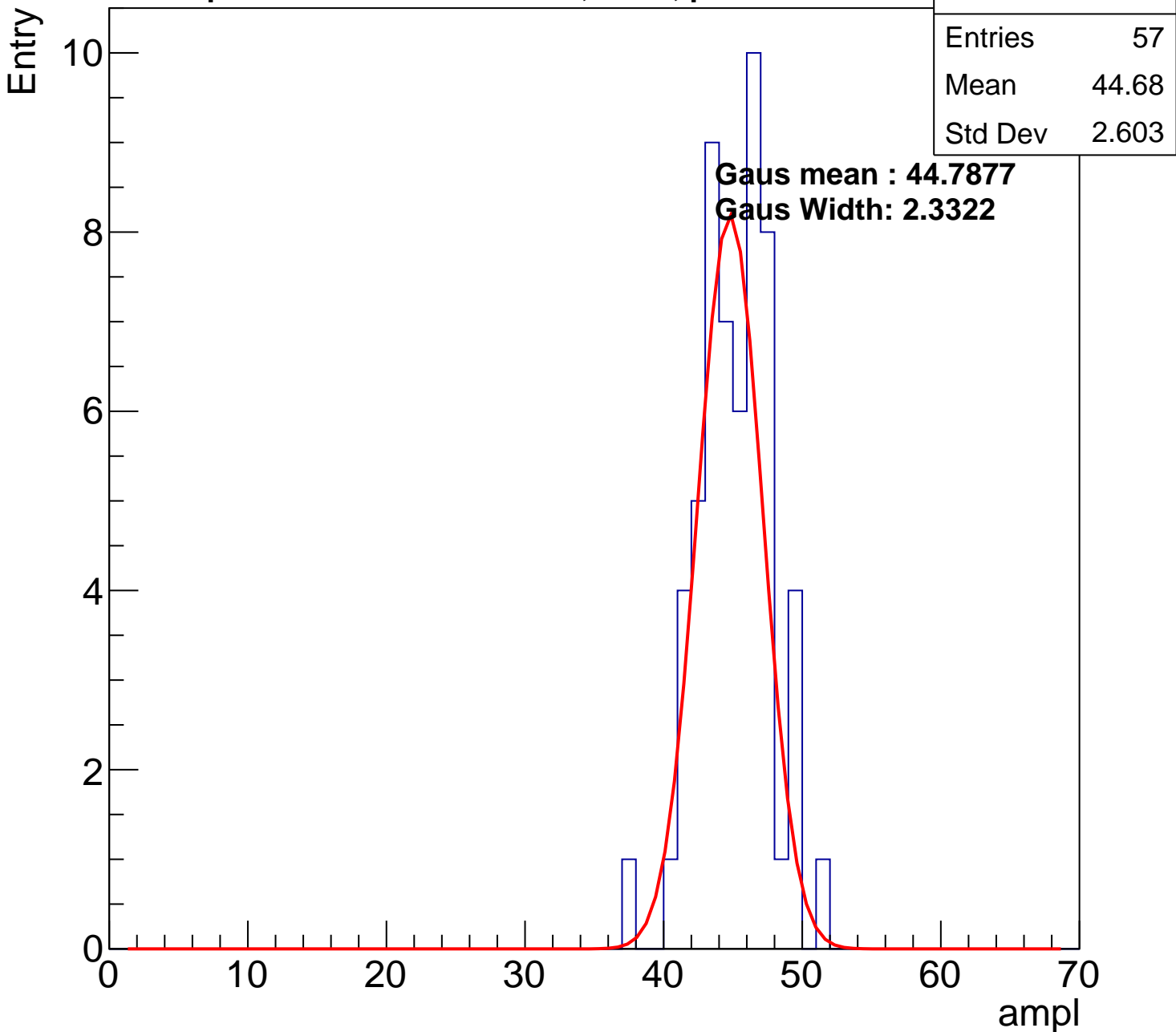
**Gaus Width: 2.3322**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

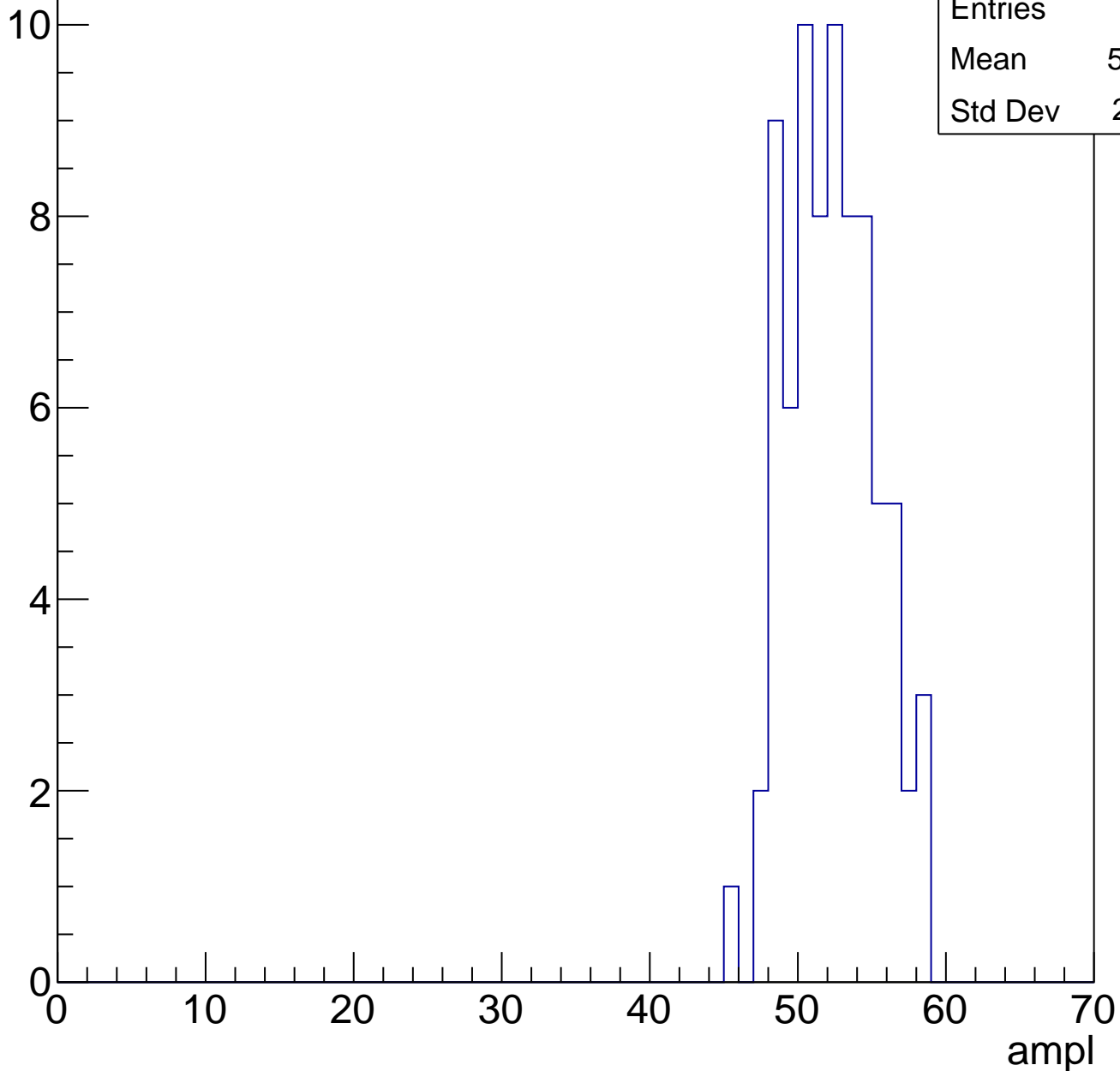


# B0L000S, U26-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	51.84
Std Dev	2.941

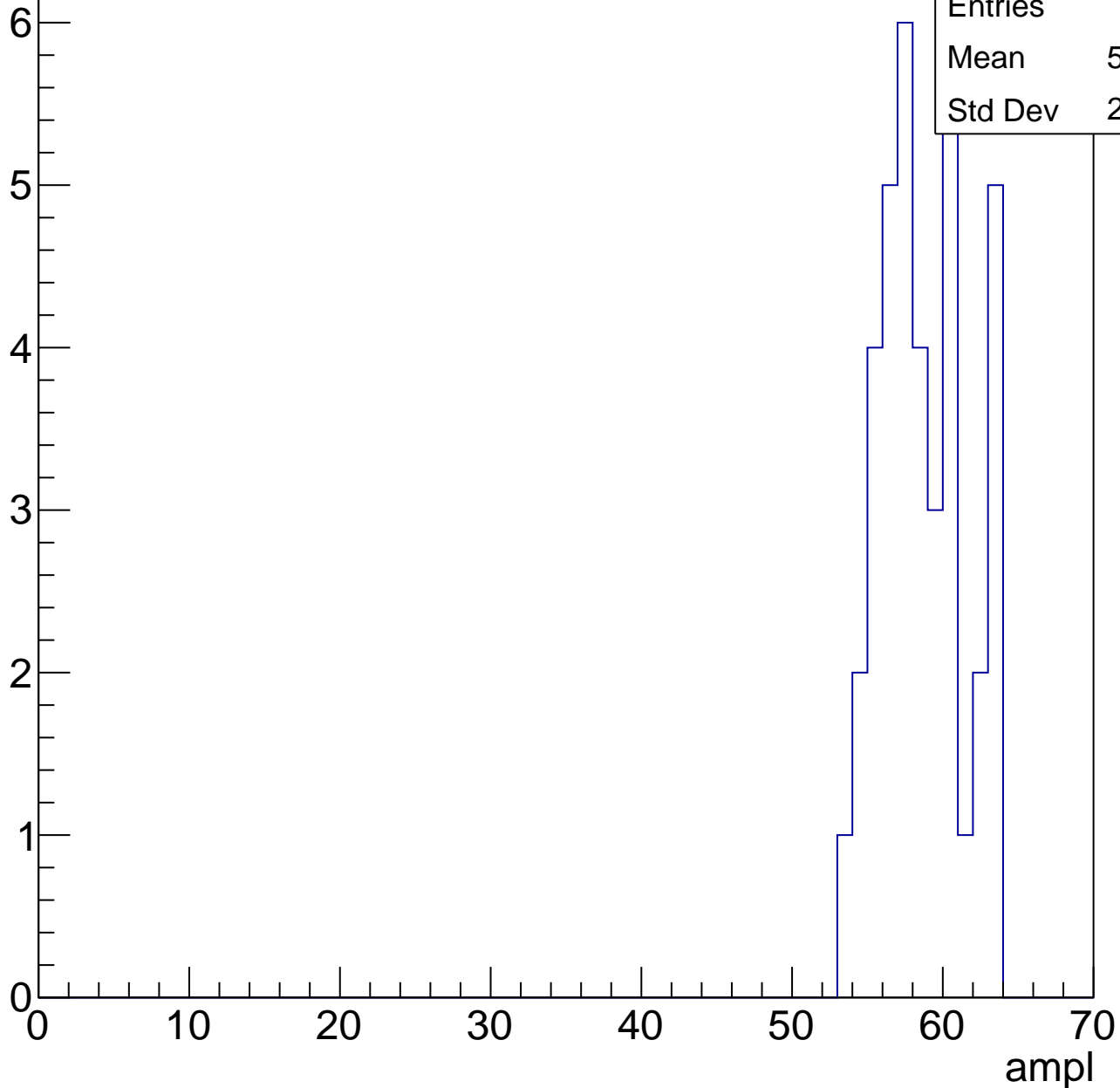
Entry



# B0L000S, U26-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

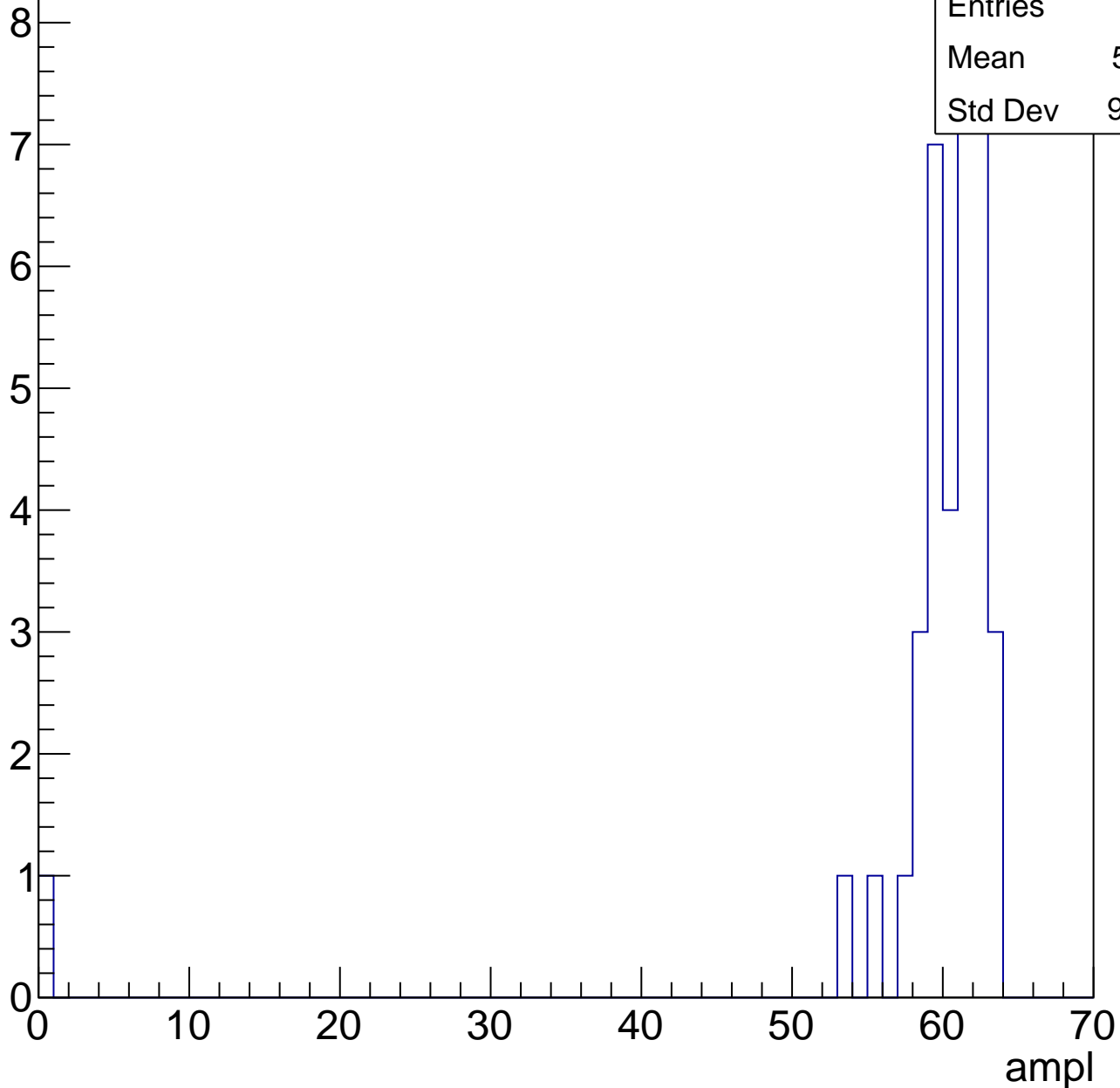
Entry



# B0L000S, U26-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

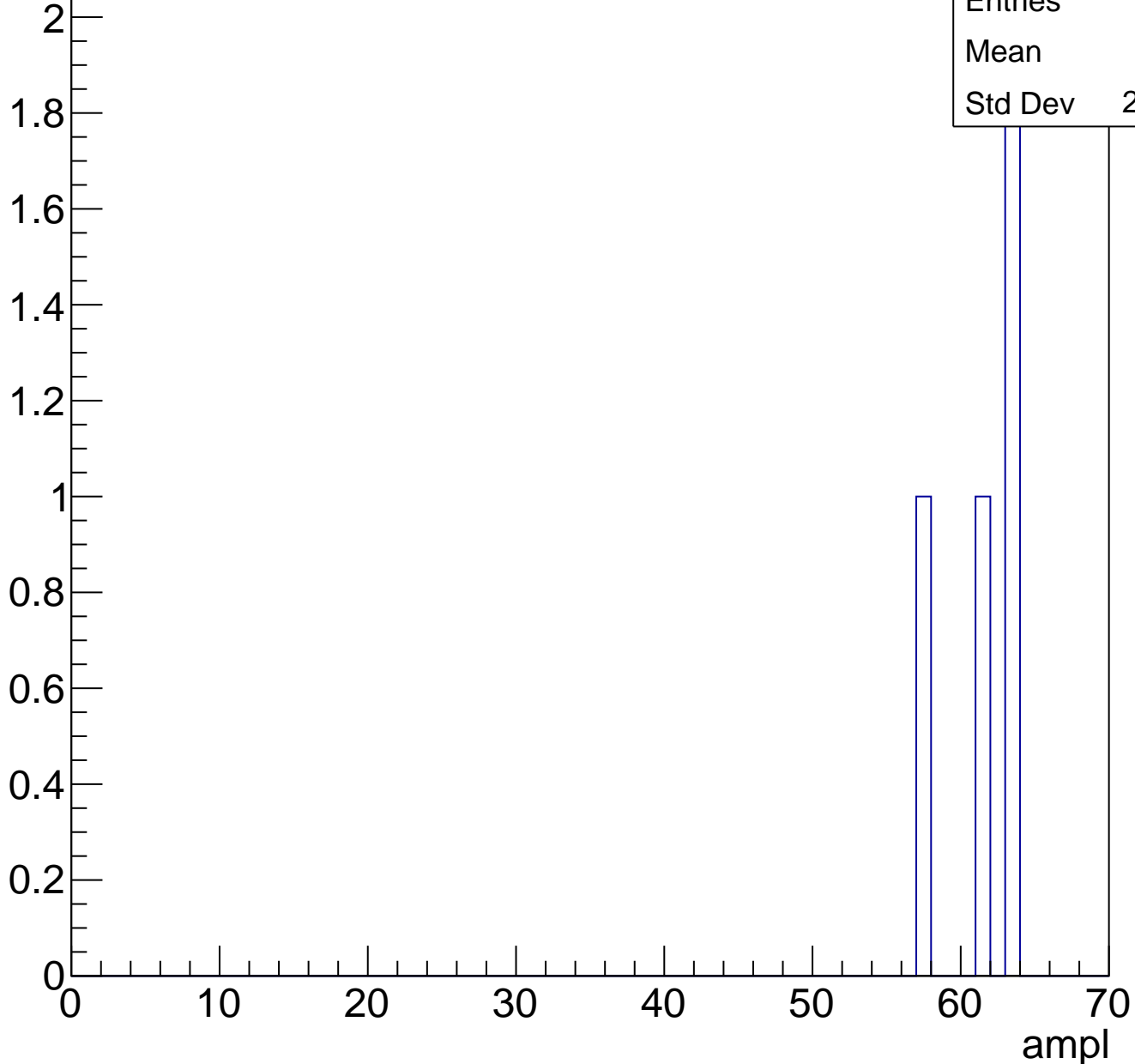
Entry



# B0L000S, U26-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch1, adc0

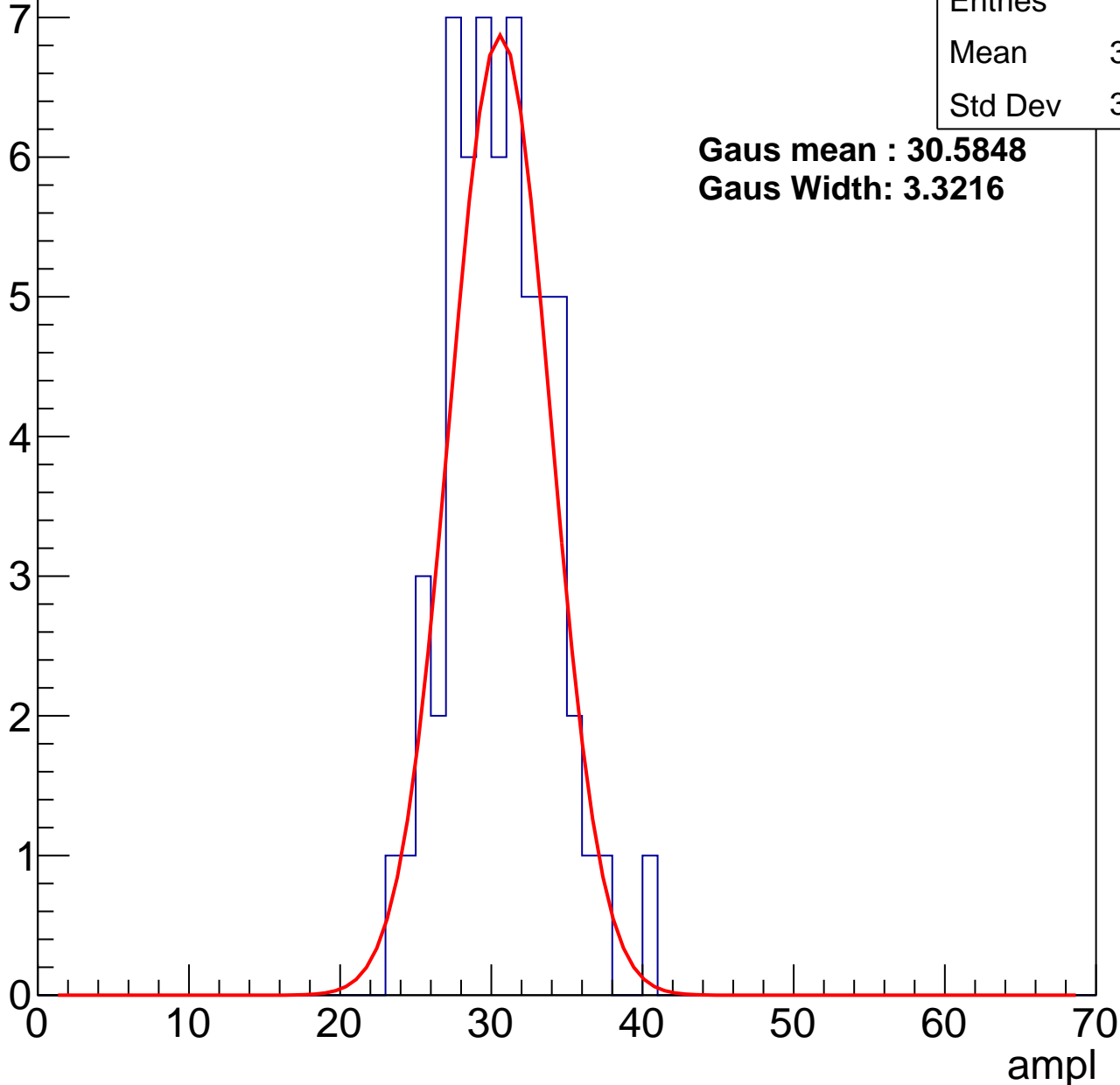
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.15
Std Dev	3.346

**Gaus mean : 30.5848**

**Gaus Width: 3.3216**



# B0L000S, U26-ch1, adc1

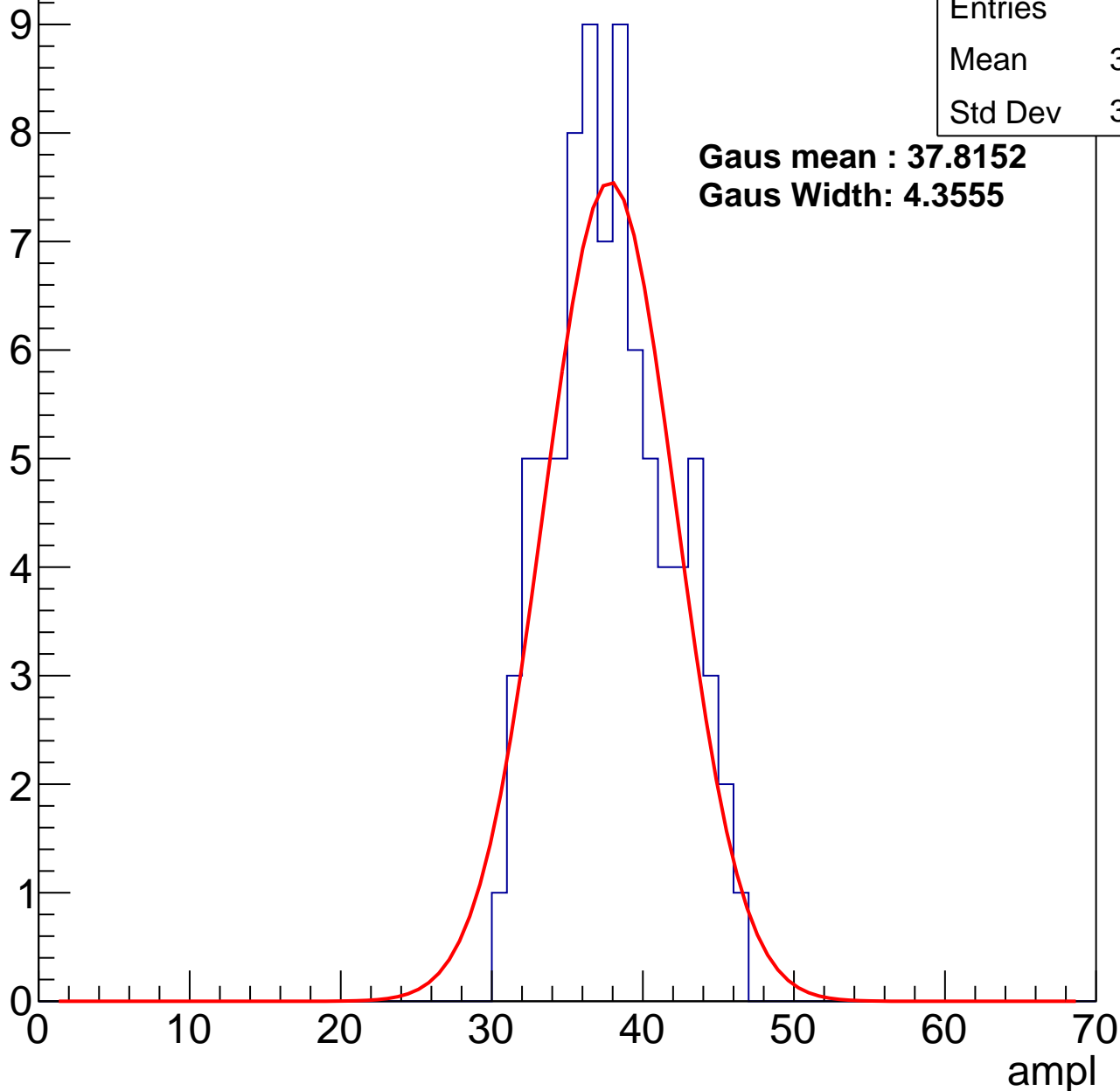
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	37.46
Std Dev	3.839

**Gaus mean : 37.8152**

**Gaus Width: 4.3555**

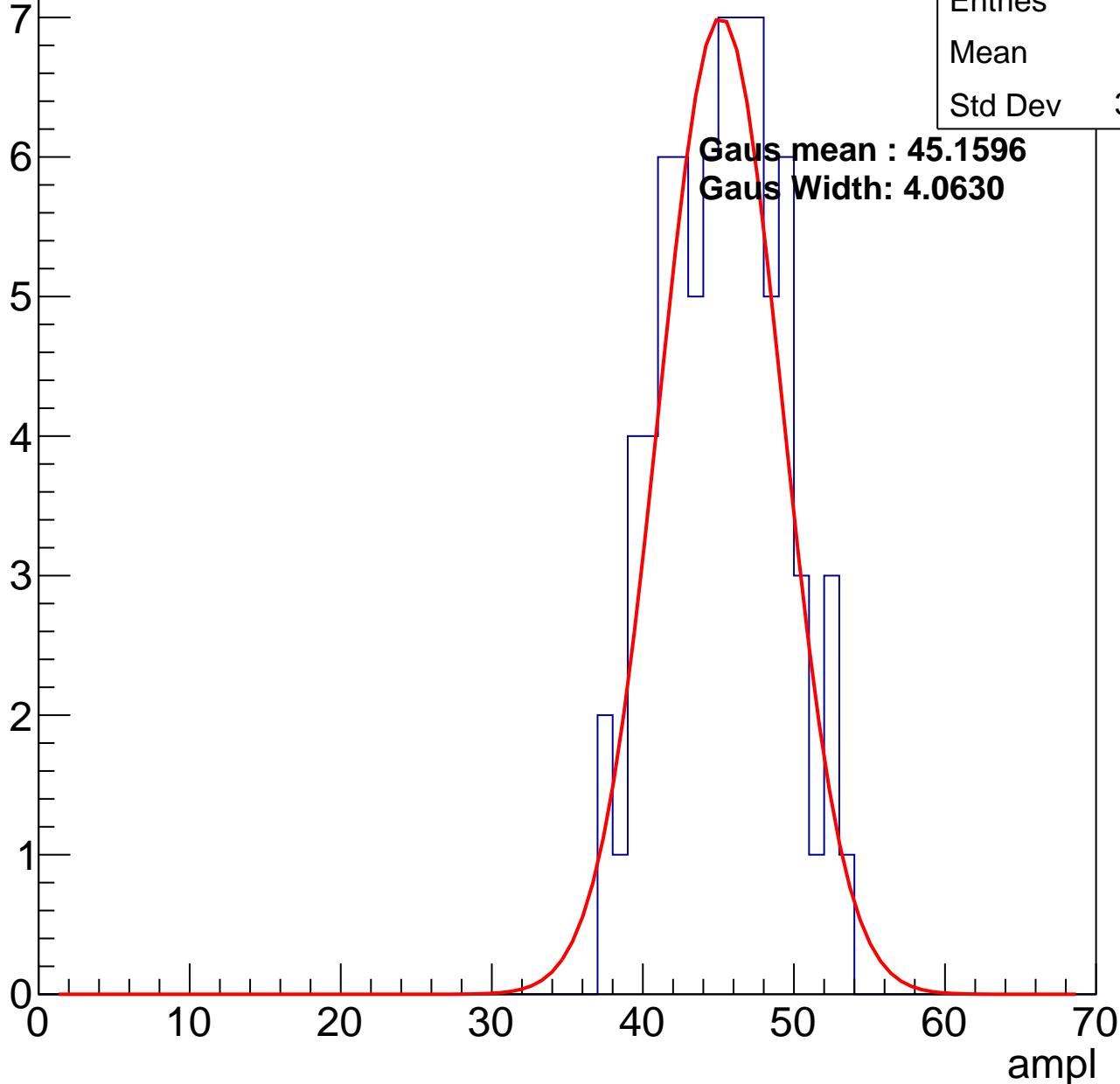


# B0L000S, U26-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	44.8
Std Dev	3.841

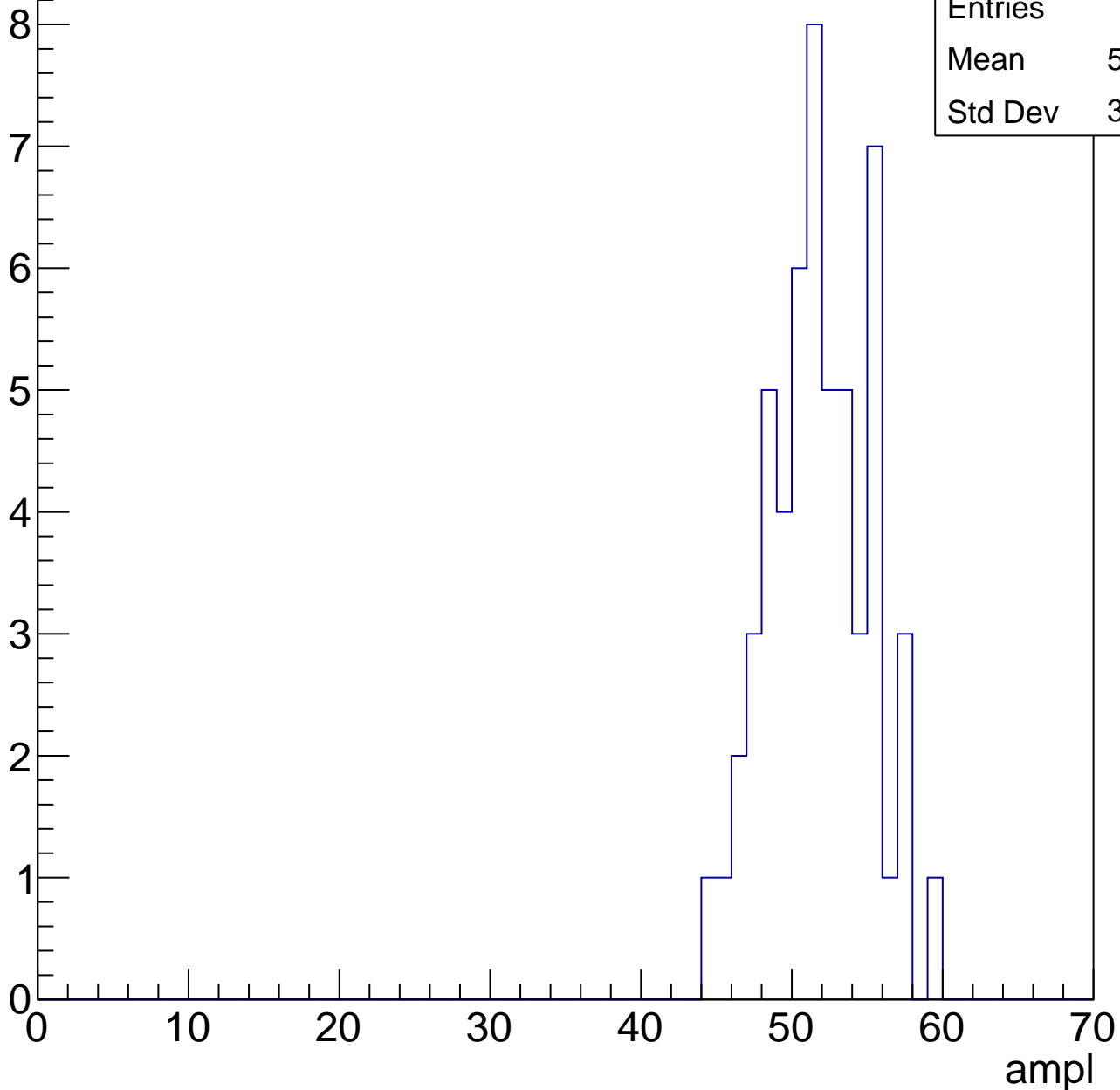


# B0L000S, U26-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	51.35
Std Dev	3.315

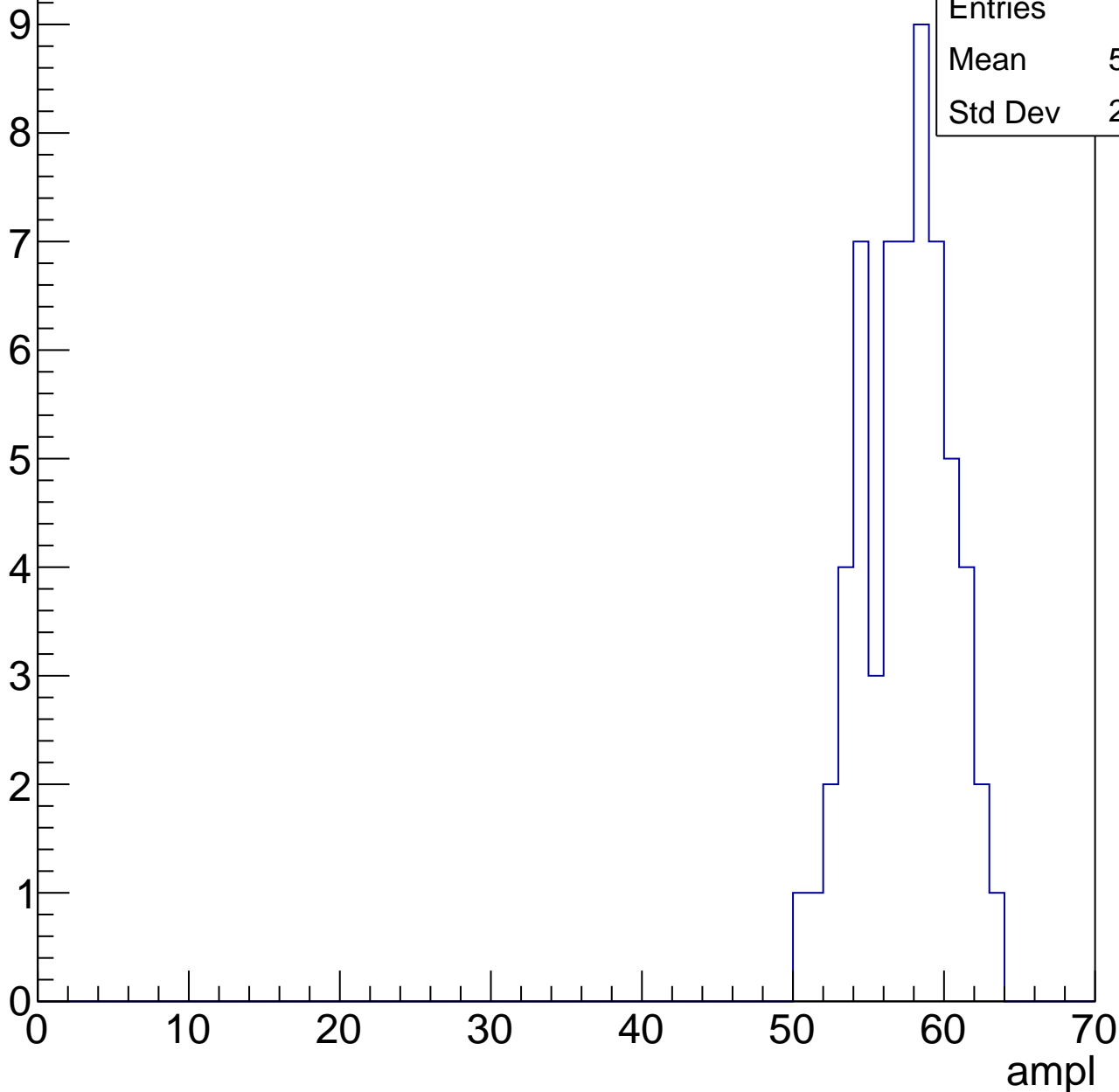


# B0L000S, U26-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	56.95
Std Dev	2.924

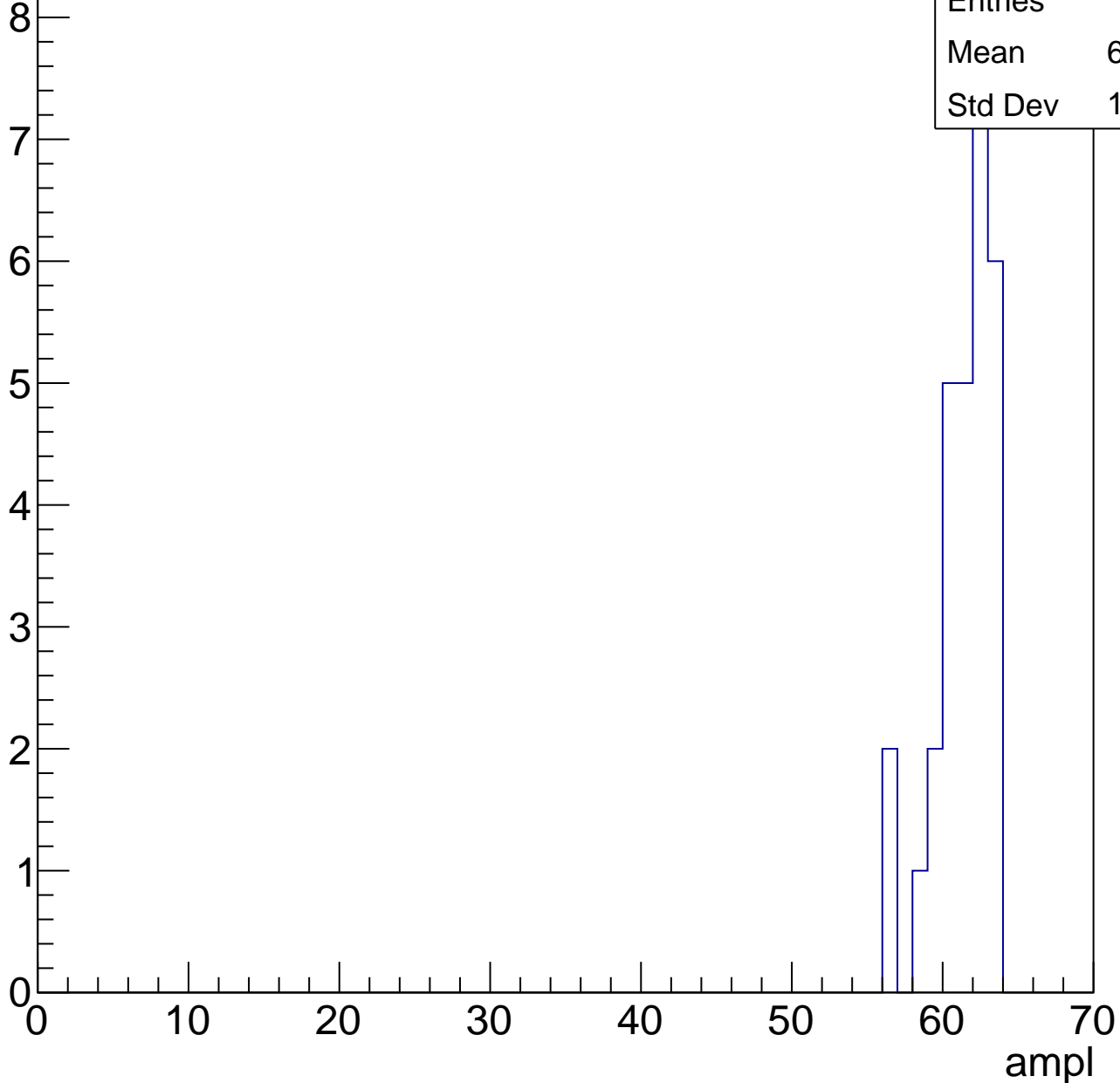


# B0L000S, U26-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

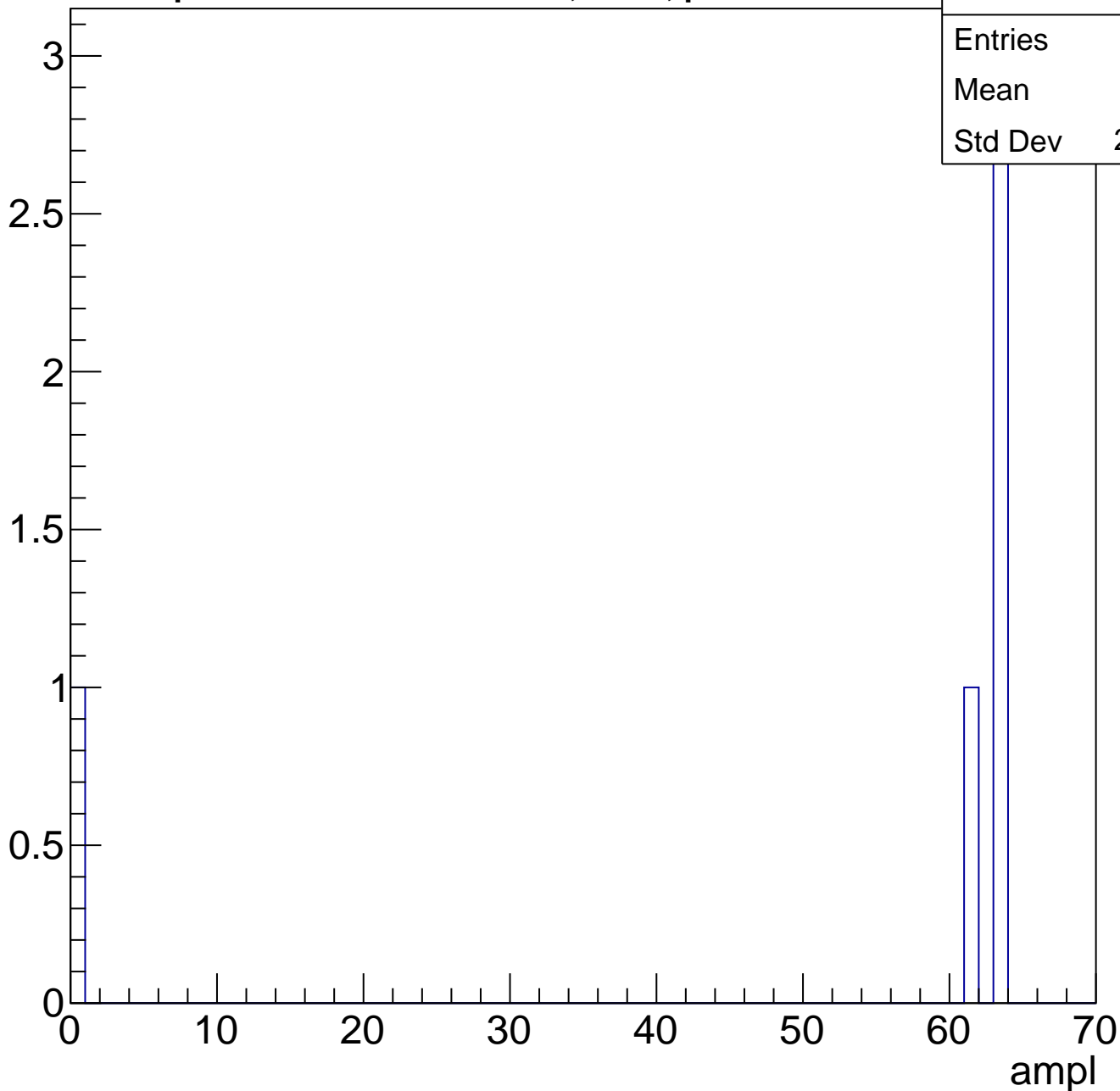
Entries	29
Mean	60.93
Std Dev	1.892



# B0L000S, U26-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U26-ch2, adc0

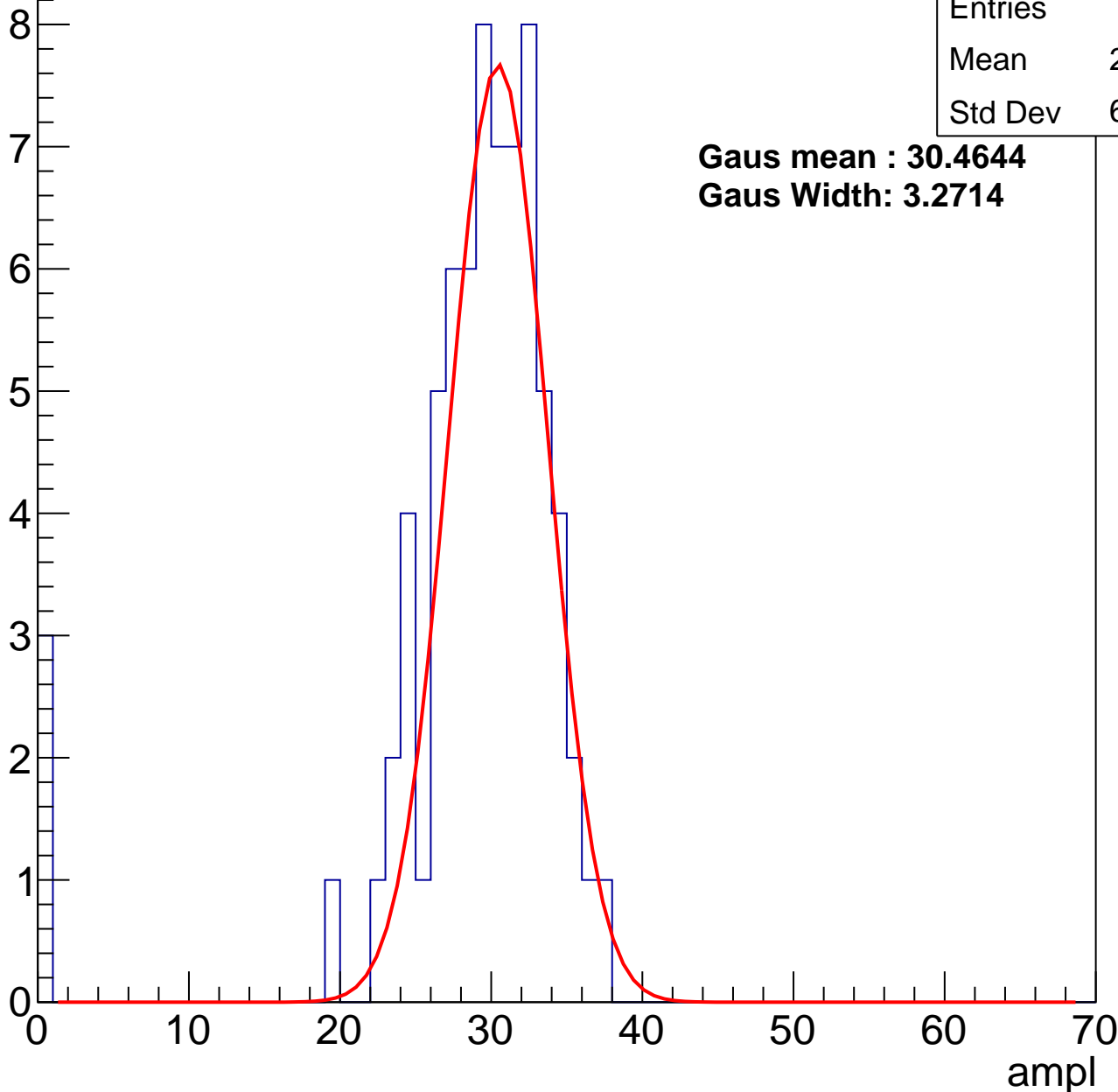
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	28.15
Std Dev	6.827

**Gaus mean : 30.4644**

**Gaus Width: 3.2714**



# B0L000S, U26-ch2, adc1

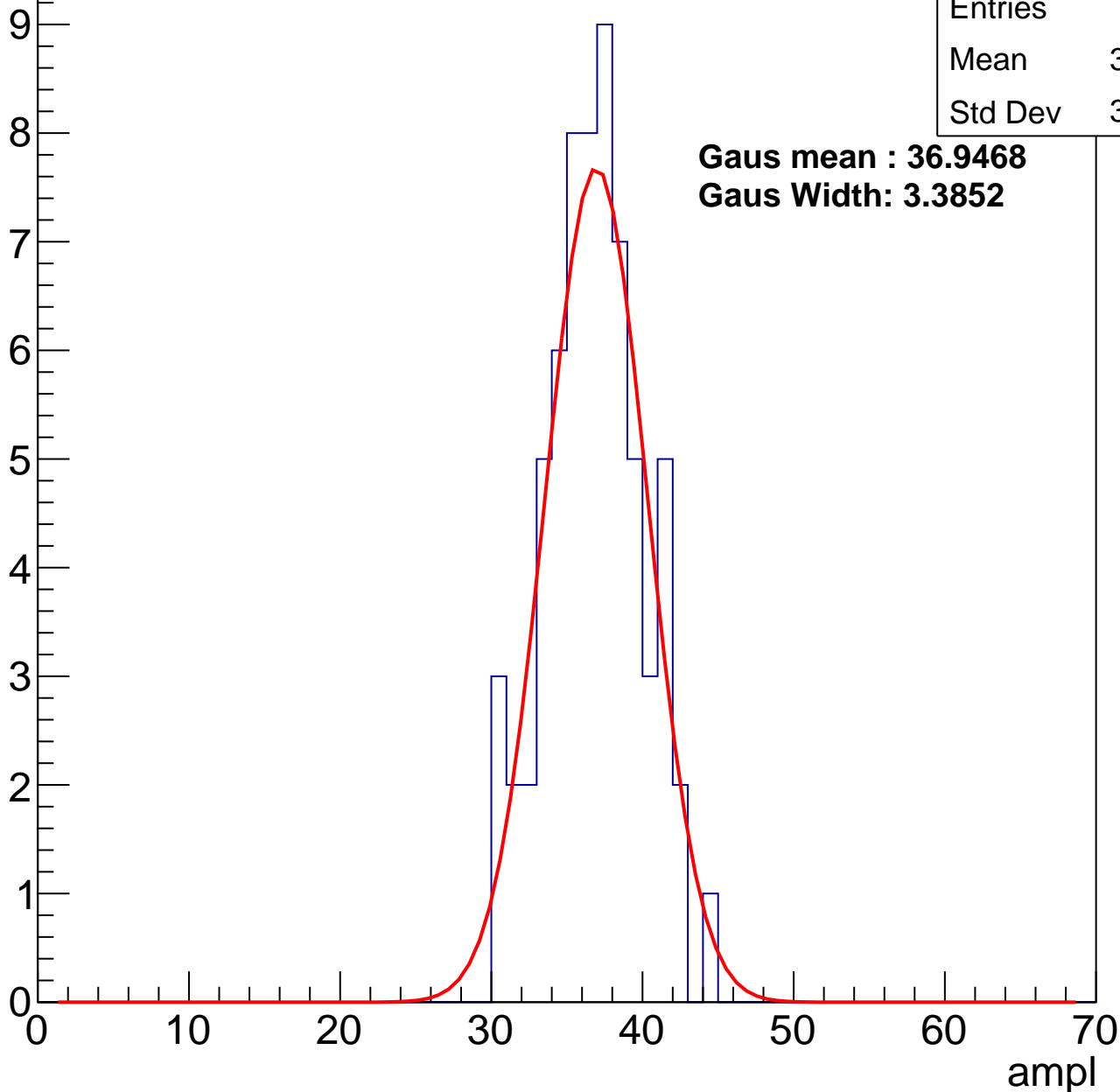
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.36
Std Dev	3.136

**Gaus mean : 36.9468**

**Gaus Width: 3.3852**



# B0L000S, U26-ch2, adc2

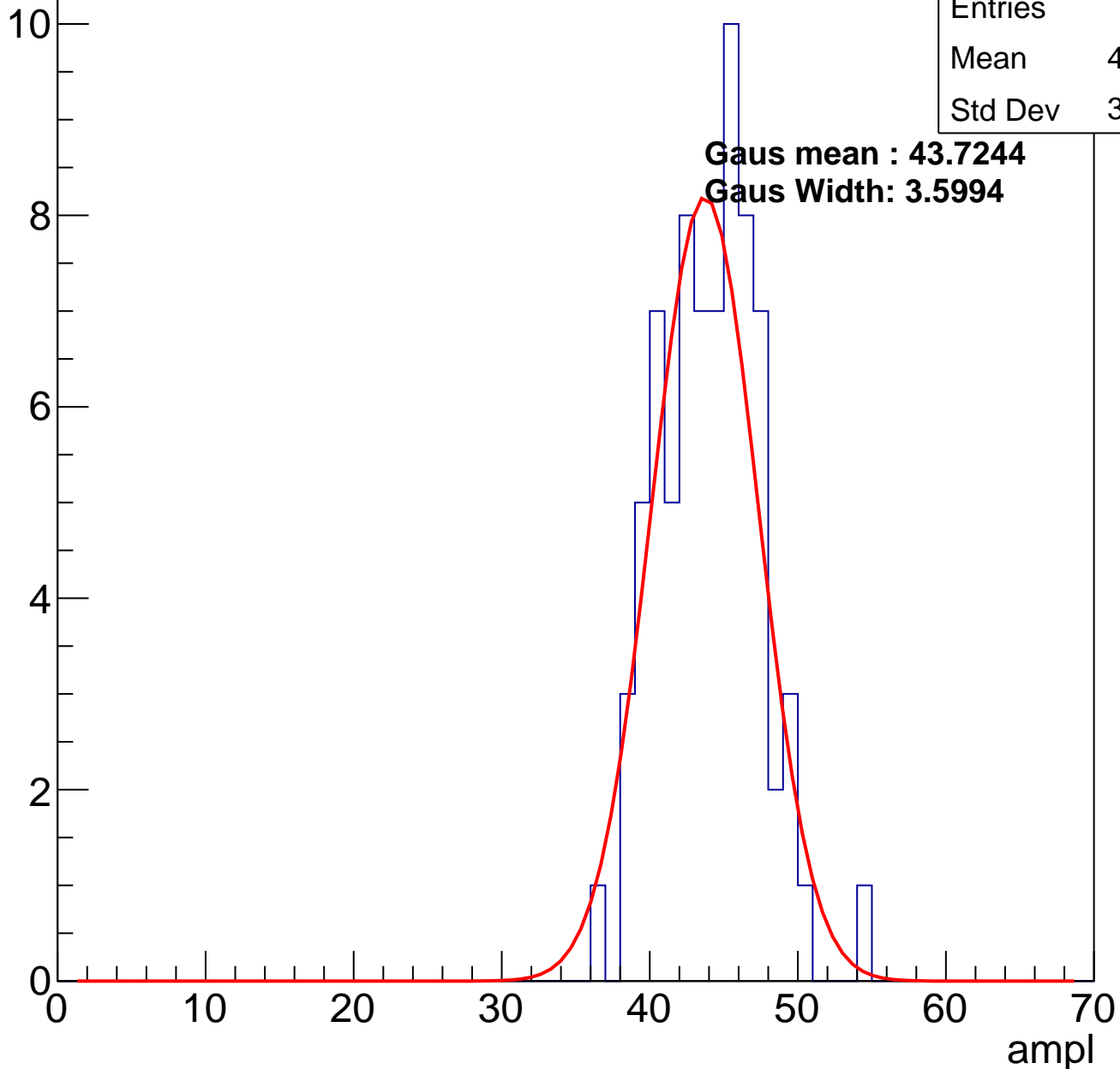
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	43.59
Std Dev	3.323

**Gaus mean : 43.7244**

**Gaus Width: 3.5994**

Entry

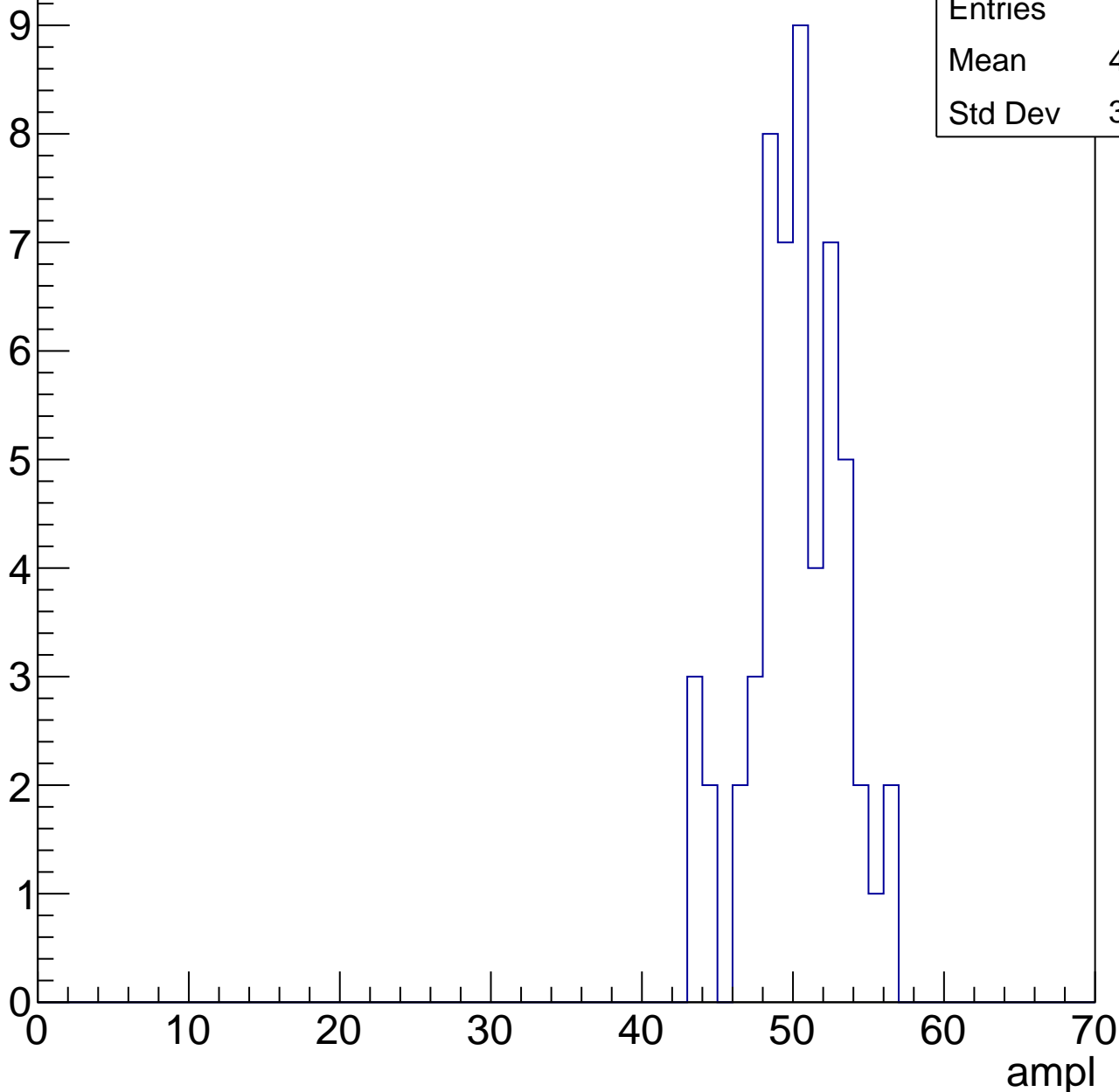


# B0L000S, U26-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

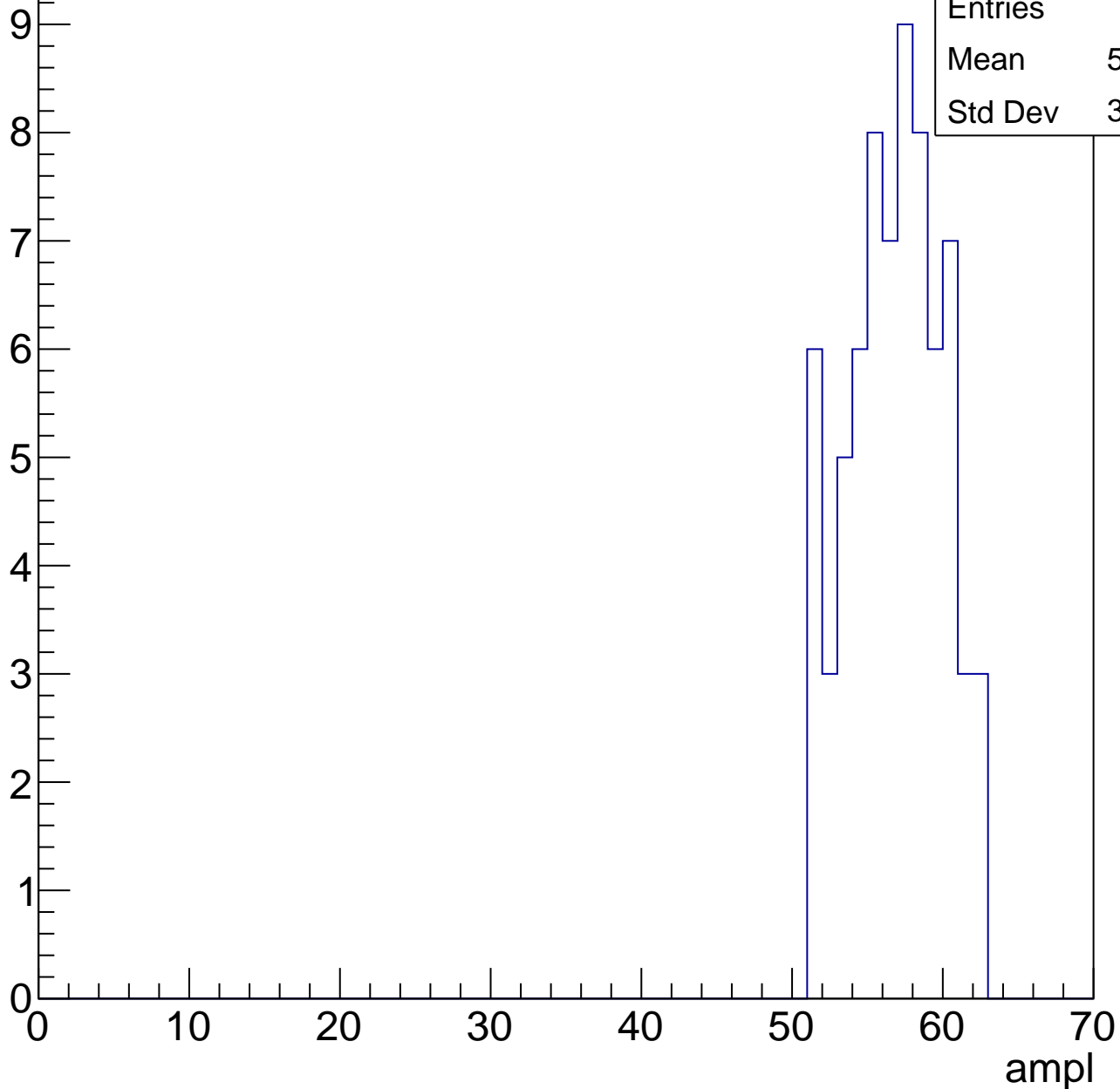
Entries	55
Mean	49.73
Std Dev	3.072



# B0L000S, U26-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



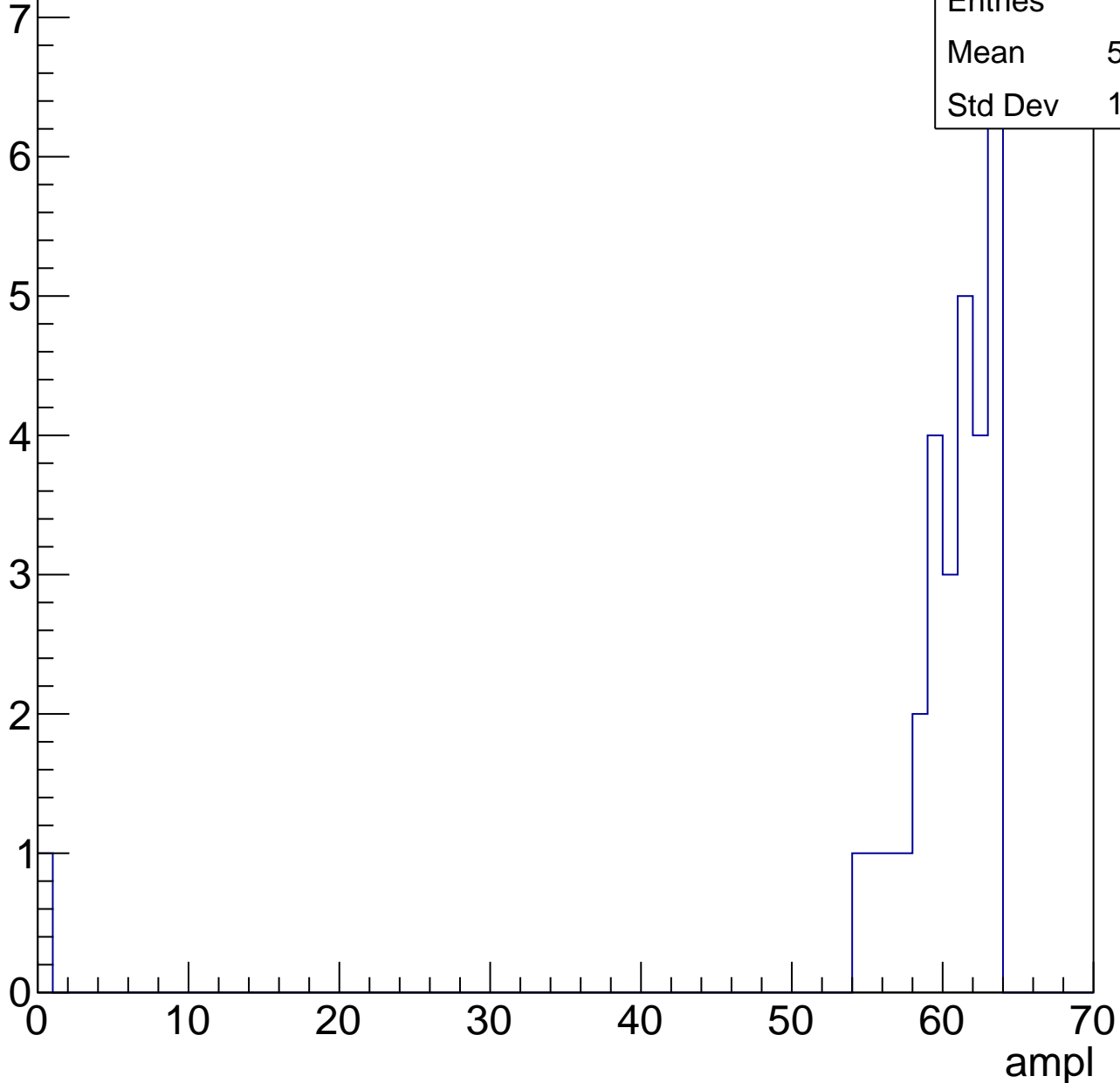
Entries	71
Mean	56.38
Std Dev	3.037

# B0L000S, U26-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

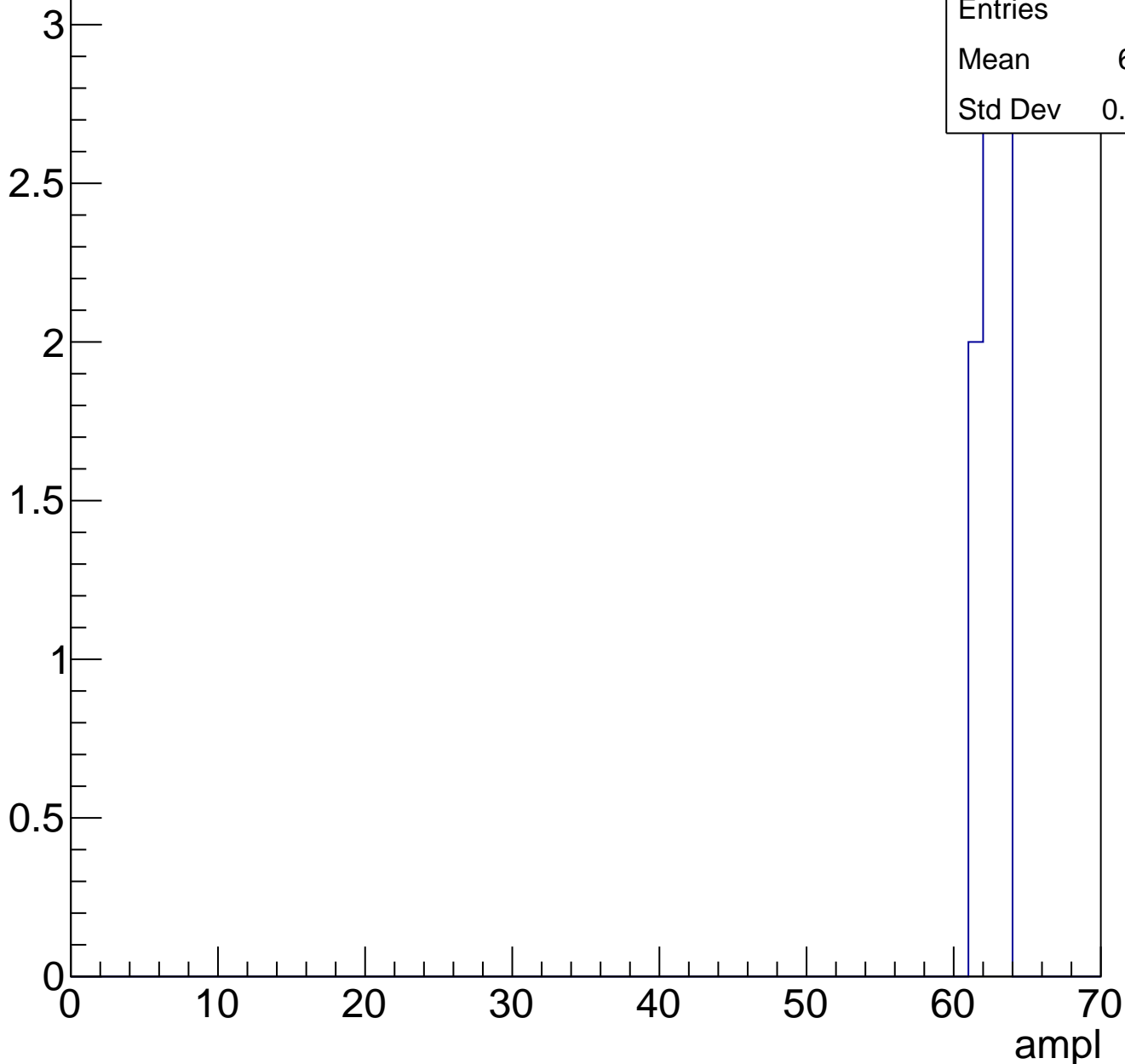
Entries	30
Mean	58.27
Std Dev	11.09



# B0L000S, U26-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch3, adc0

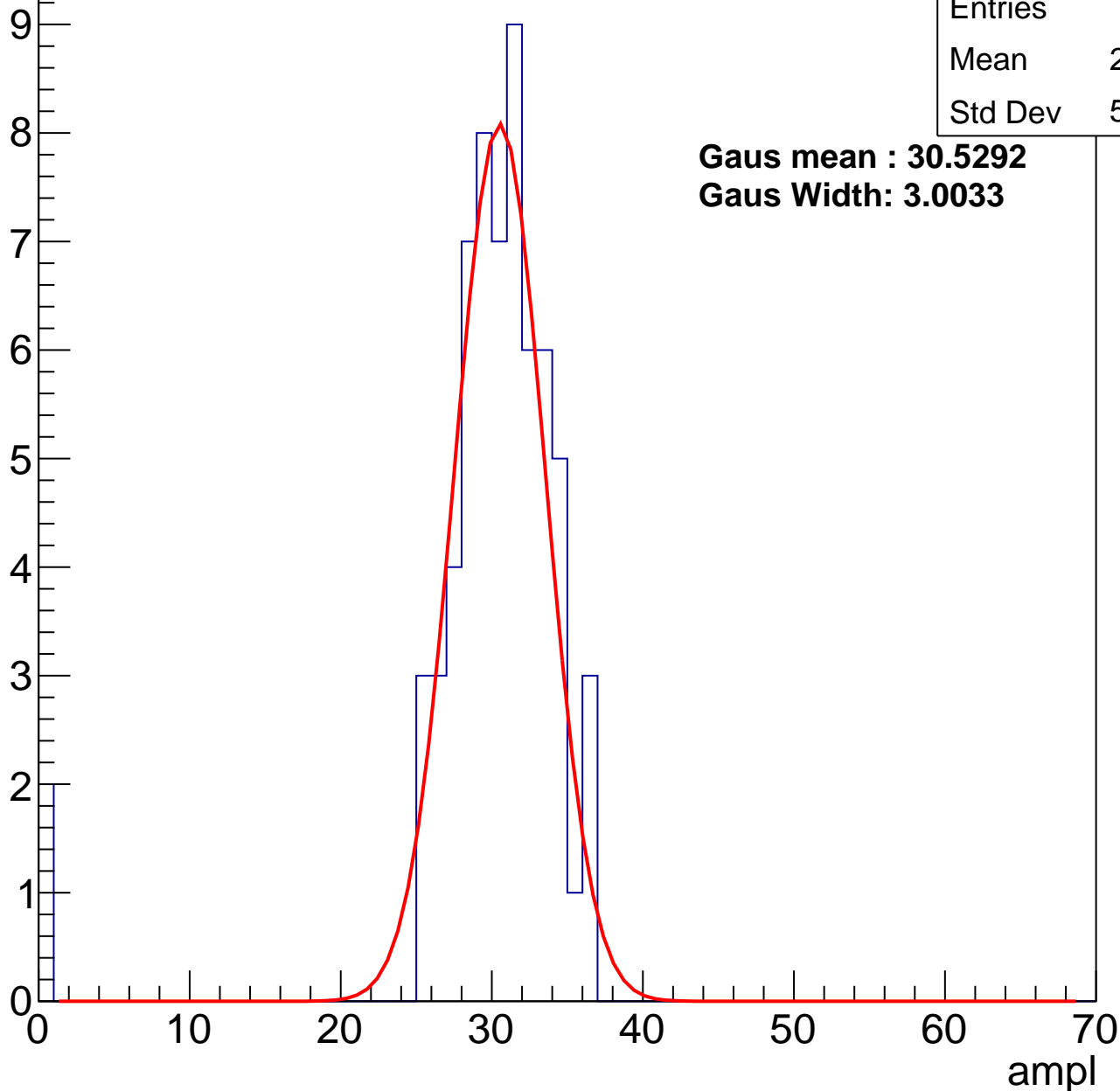
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.39
Std Dev	5.957

**Gaus mean : 30.5292**

**Gaus Width: 3.0033**



# B0L000S, U26-ch3, adc1

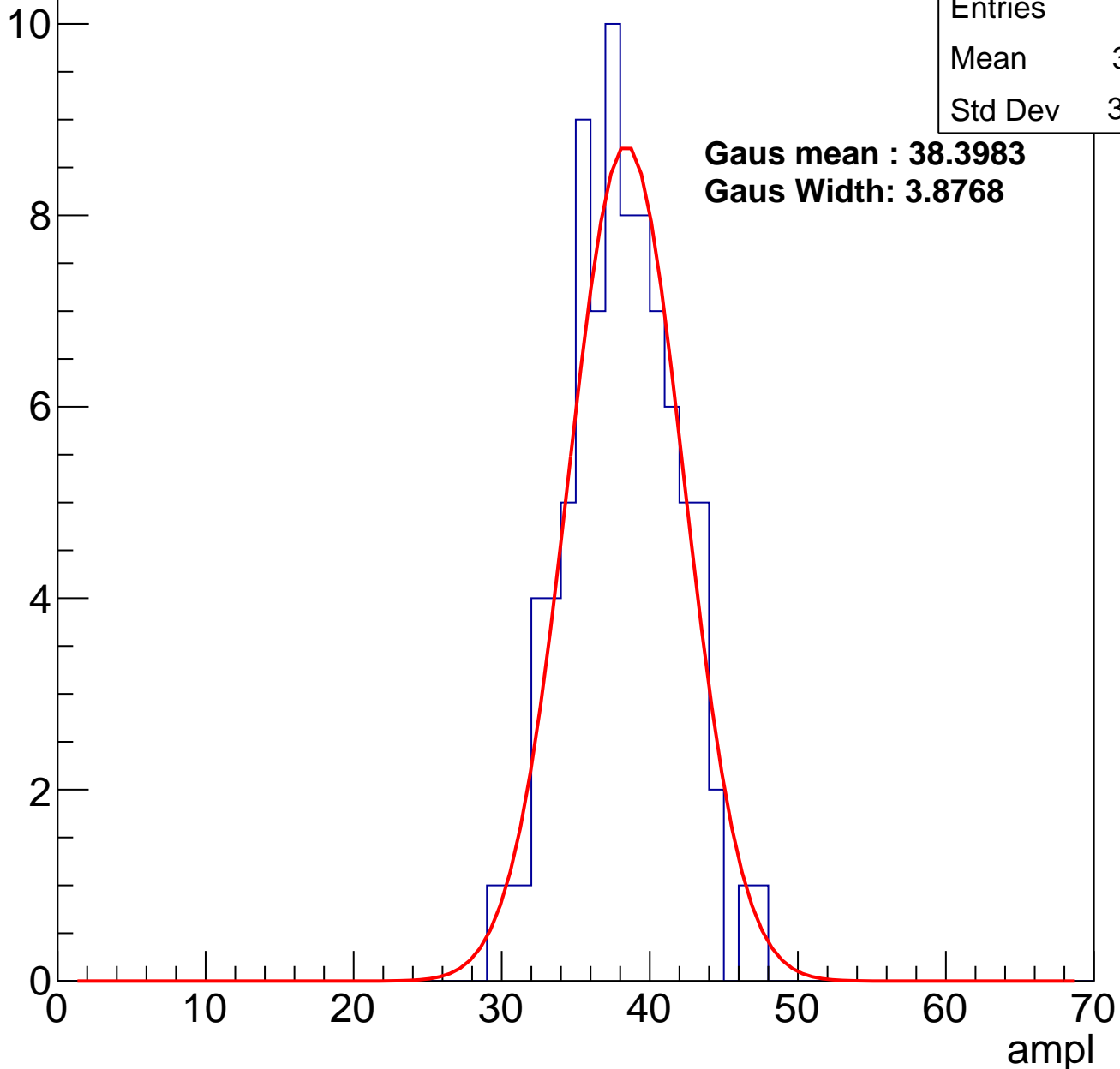
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	37.71
Std Dev	3.665

**Gaus mean : 38.3983**

**Gaus Width: 3.8768**

Entry



# B0L000S, U26-ch3, adc2

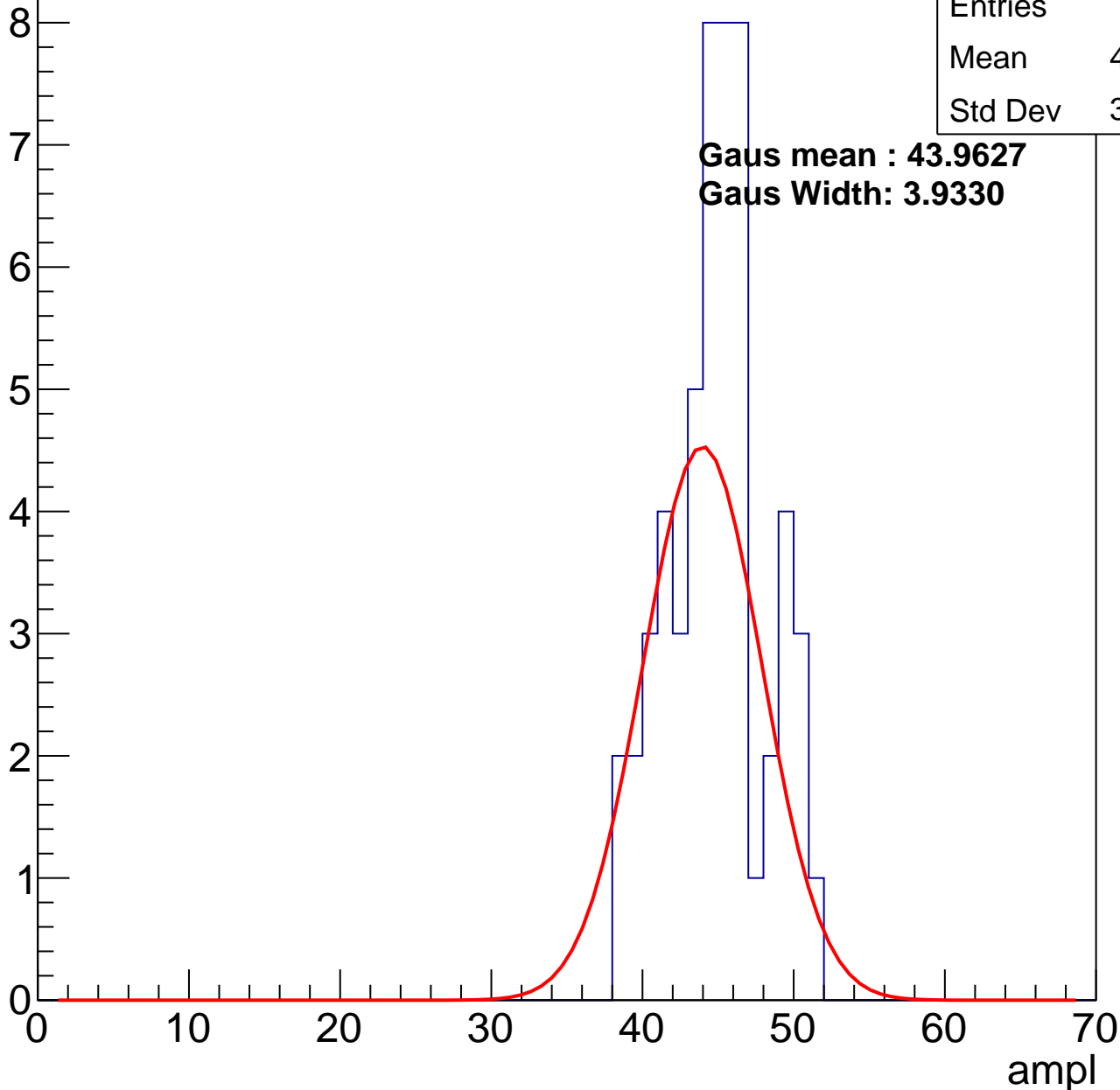
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	44.43
Std Dev	3.195

**Gaus mean : 43.9627**

**Gaus Width: 3.9330**

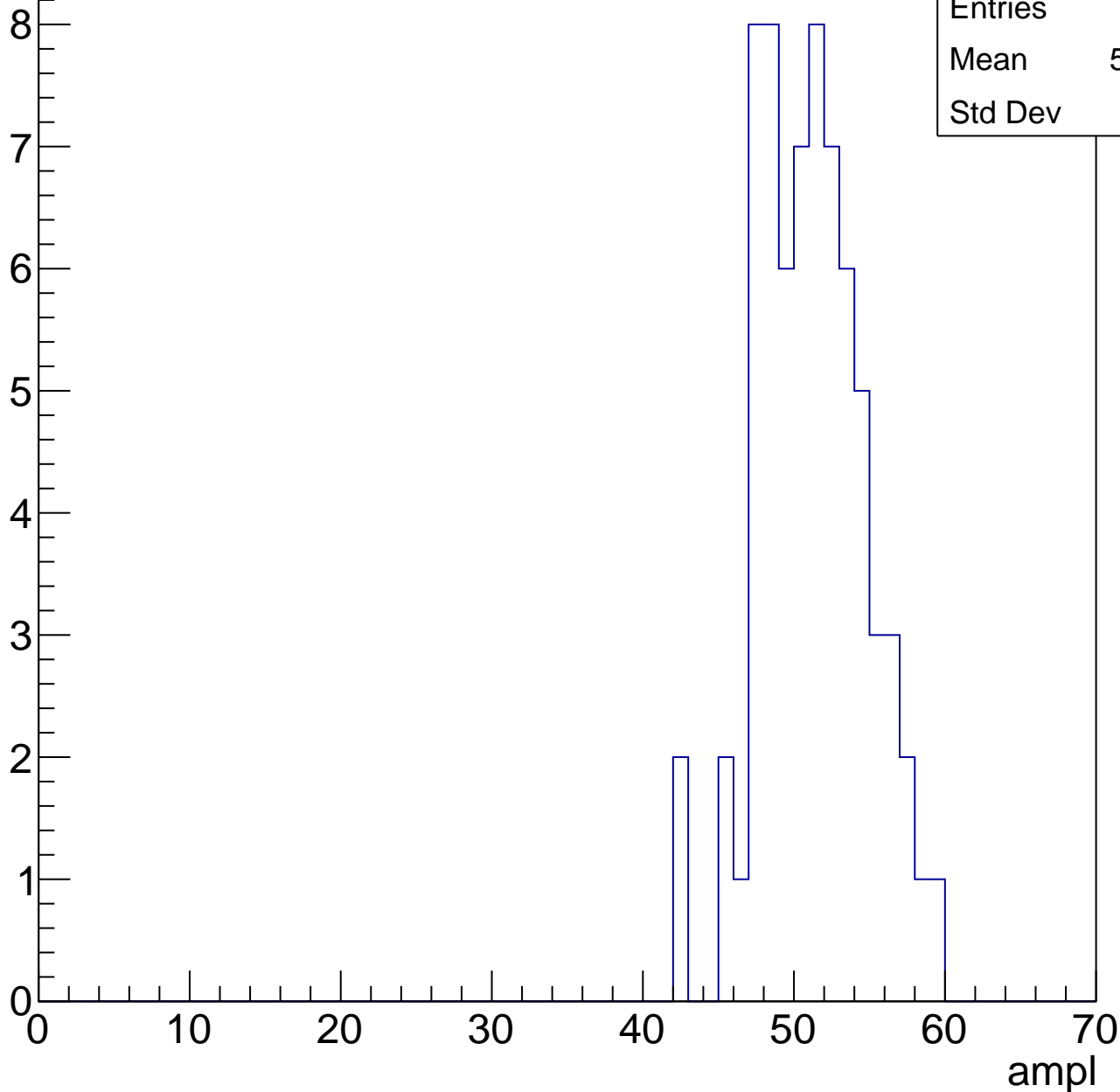


# B0L000S, U26-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	50.69
Std Dev	3.52

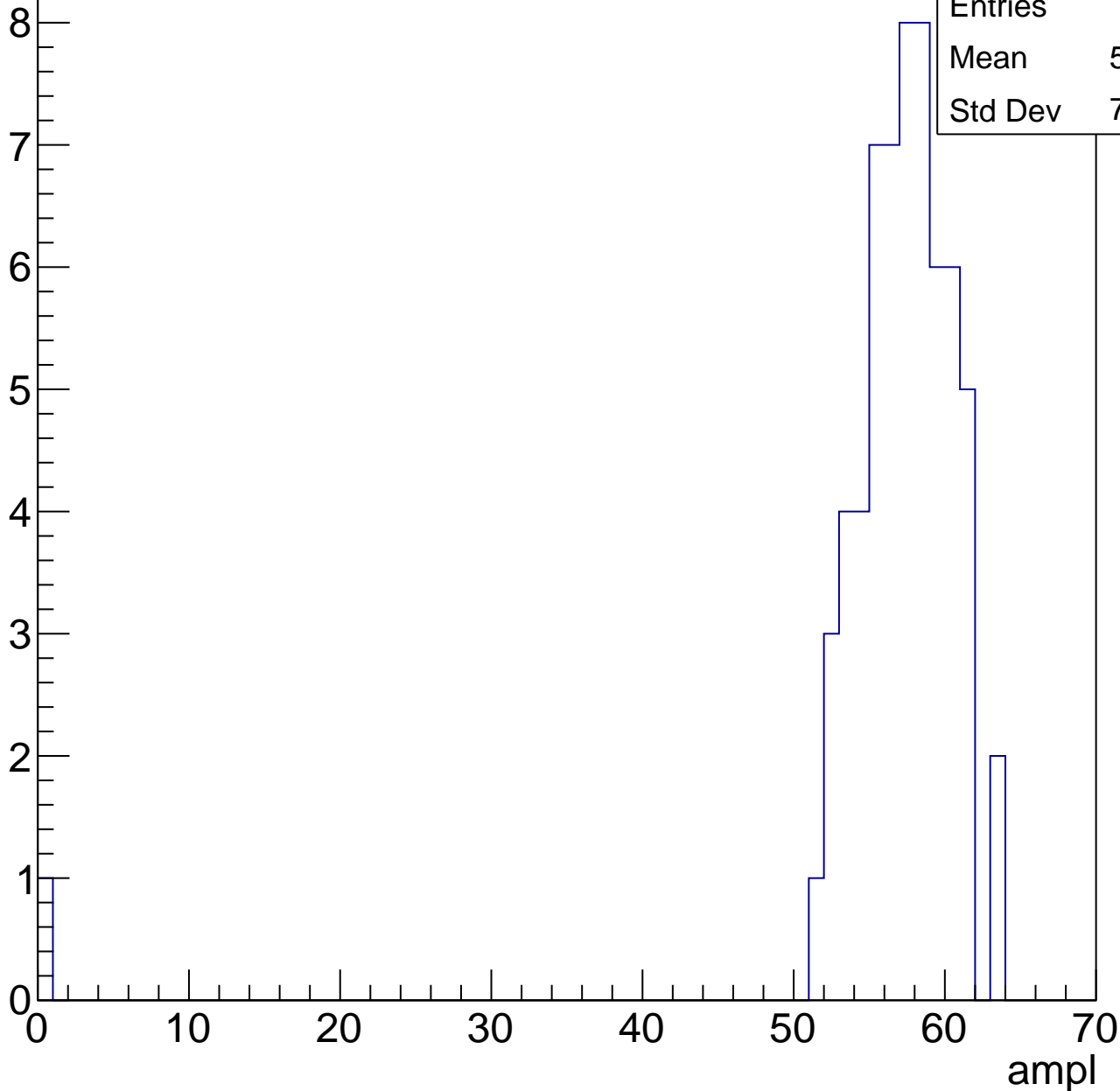


# B0L000S, U26-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	56.08
Std Dev	7.705

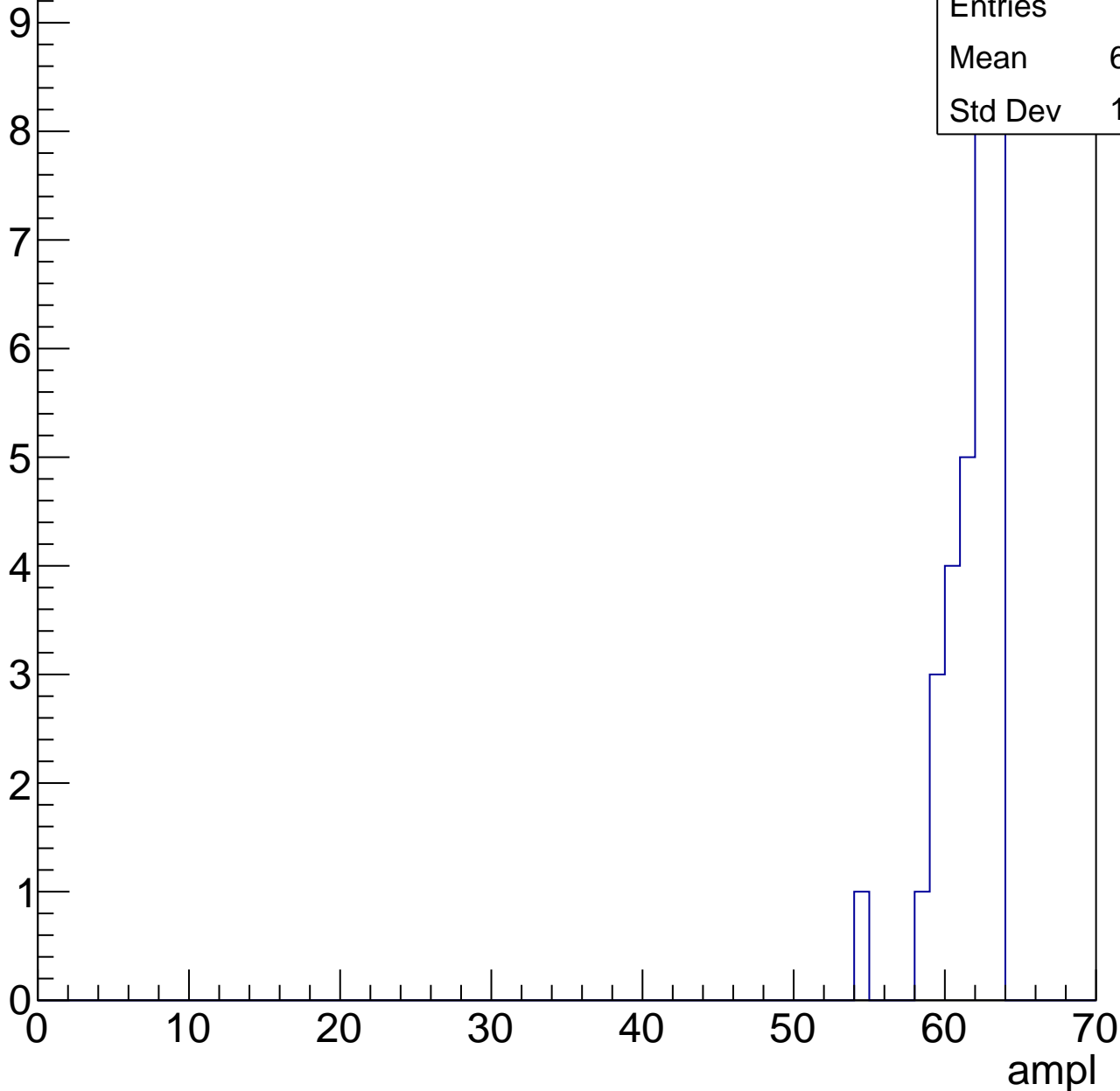


# B0L000S, U26-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	61.16
Std Dev	1.919



# B0L000S, U26-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch4, adc0

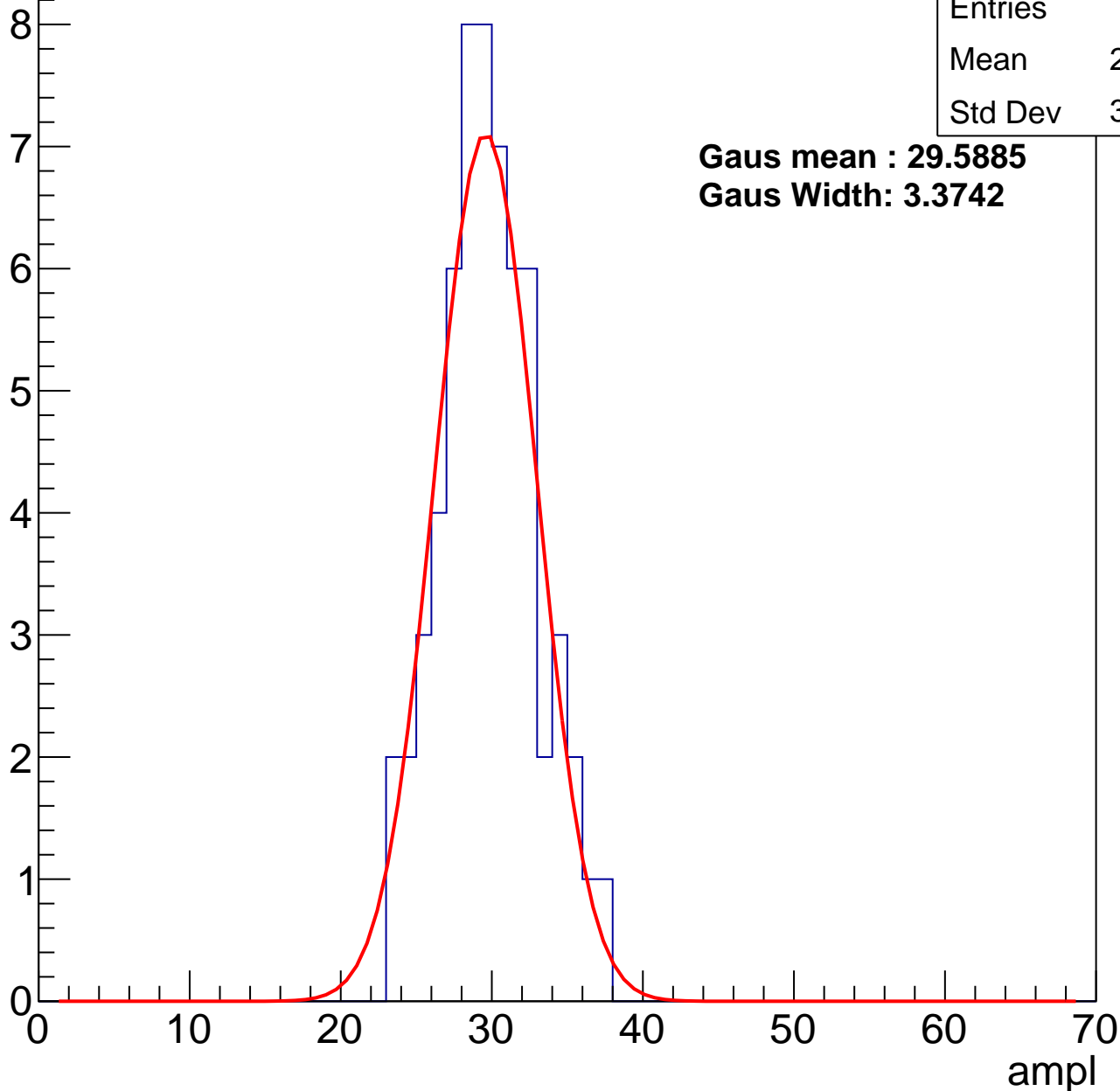
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.34
Std Dev	3.156

**Gaus mean : 29.5885**

**Gaus Width: 3.3742**



# B0L000S, U26-ch4, adc1

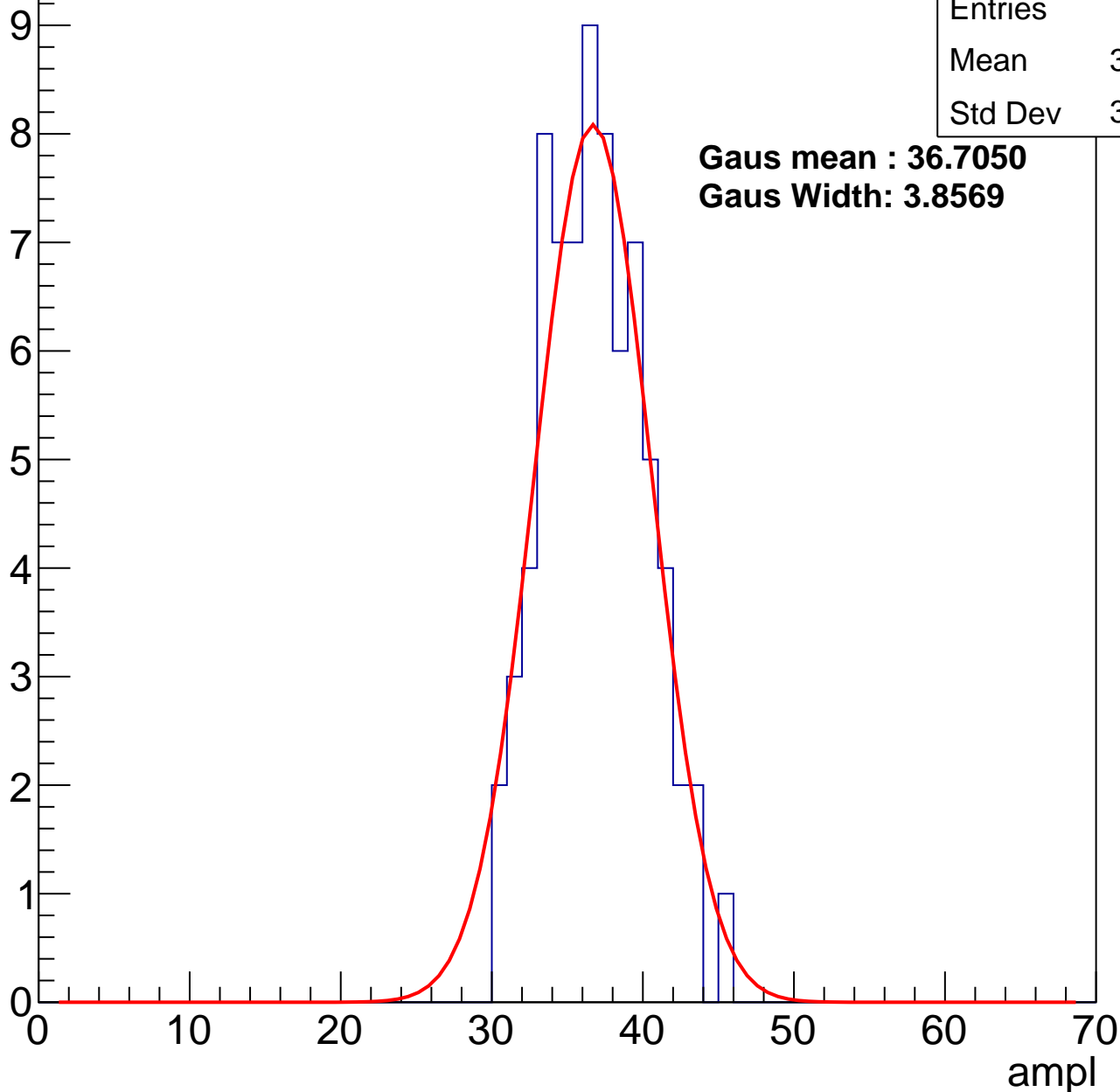
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	36.37
Std Dev	3.334

**Gaus mean : 36.7050**

**Gaus Width: 3.8569**



# B0L000S, U26-ch4, adc2

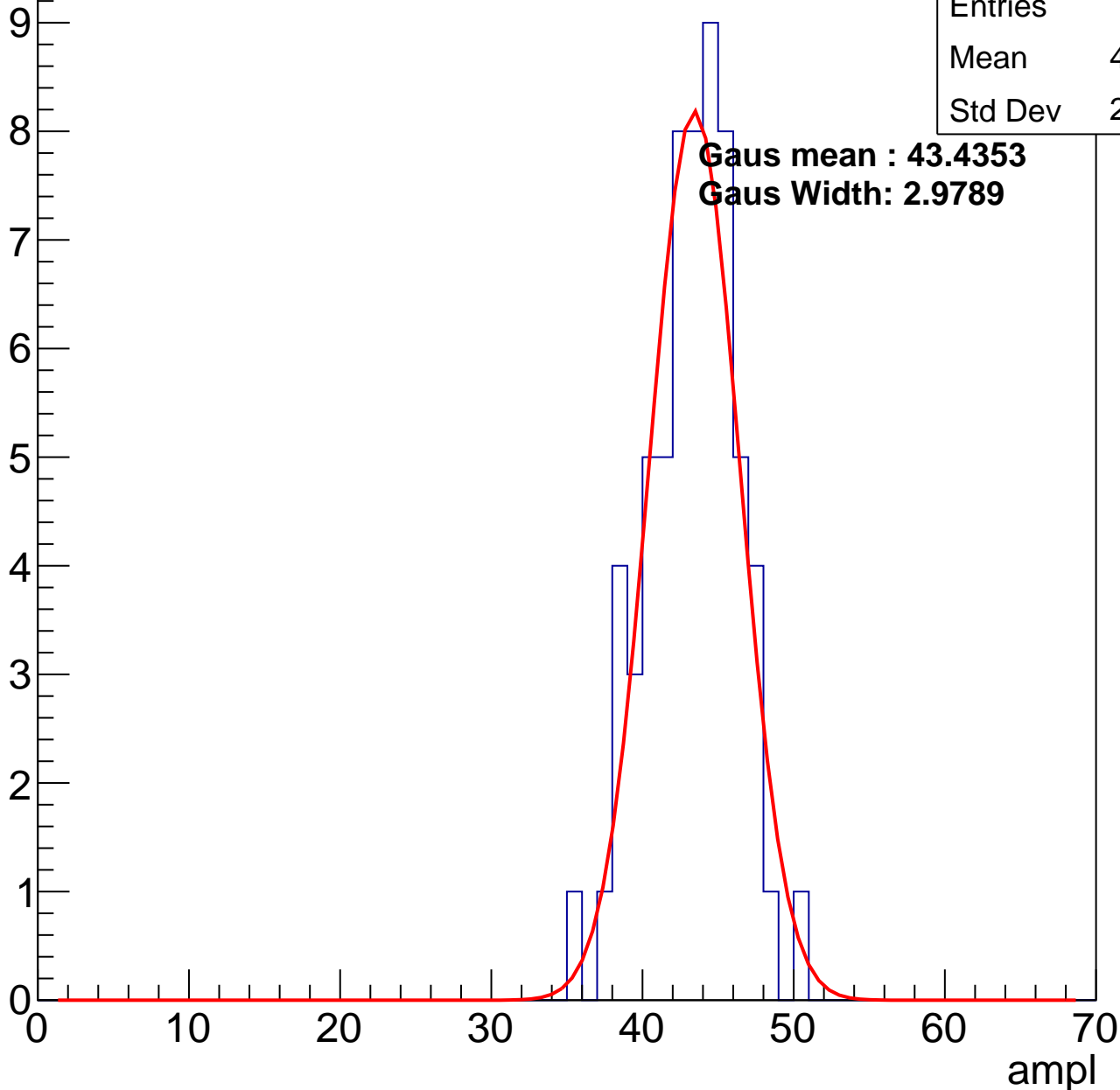
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.83
Std Dev	2.936

**Gaus mean : 43.4353**

**Gaus Width: 2.9789**

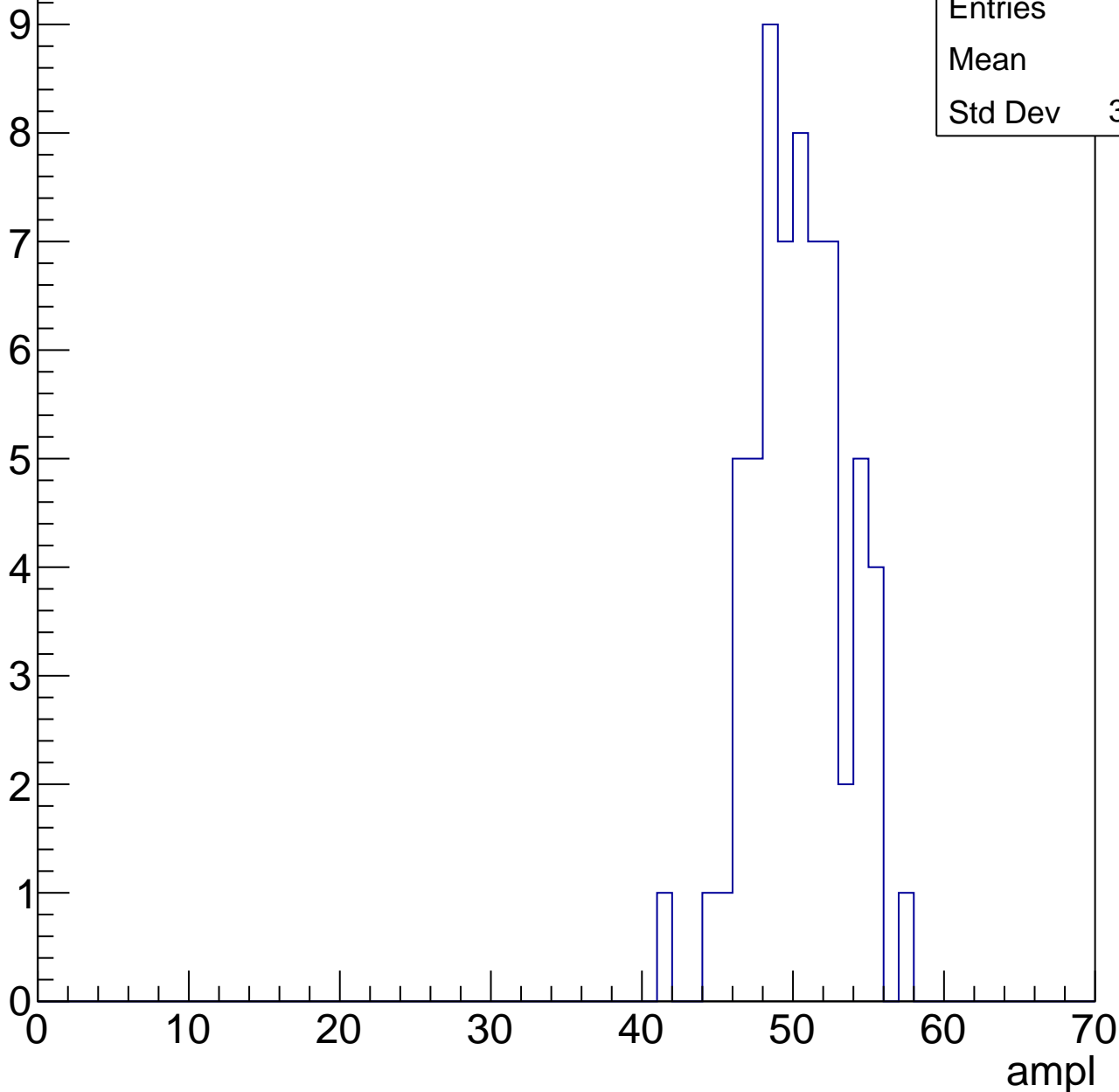


# B0L000S, U26-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	49.9
Std Dev	3.064

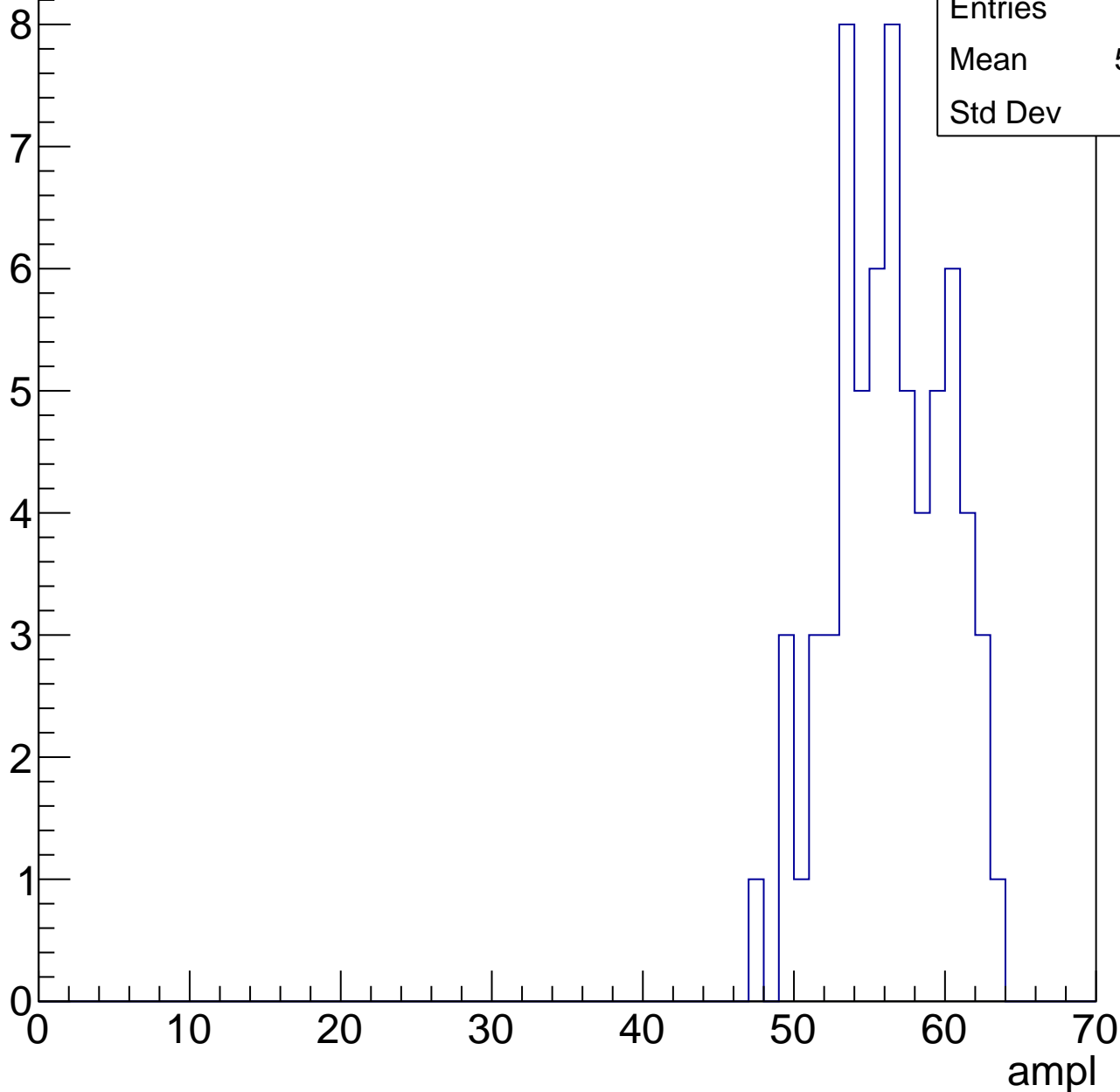


# B0L000S, U26-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	55.91
Std Dev	3.7

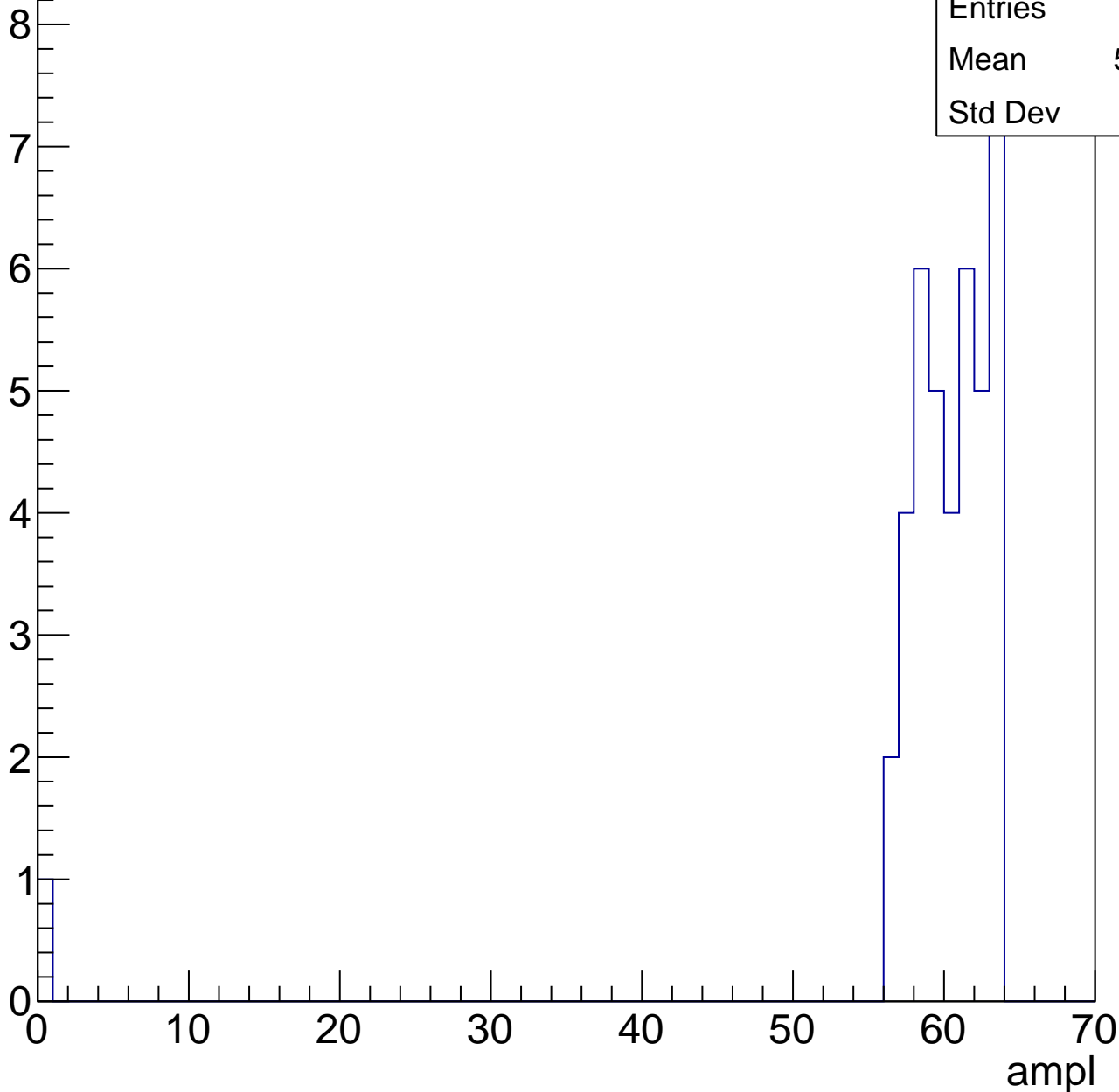


# B0L000S, U26-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.61
Std Dev	9.52



# B0L000S, U26-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch5, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	32.17
Std Dev	5.124

**Gaus mean : 33.5873**

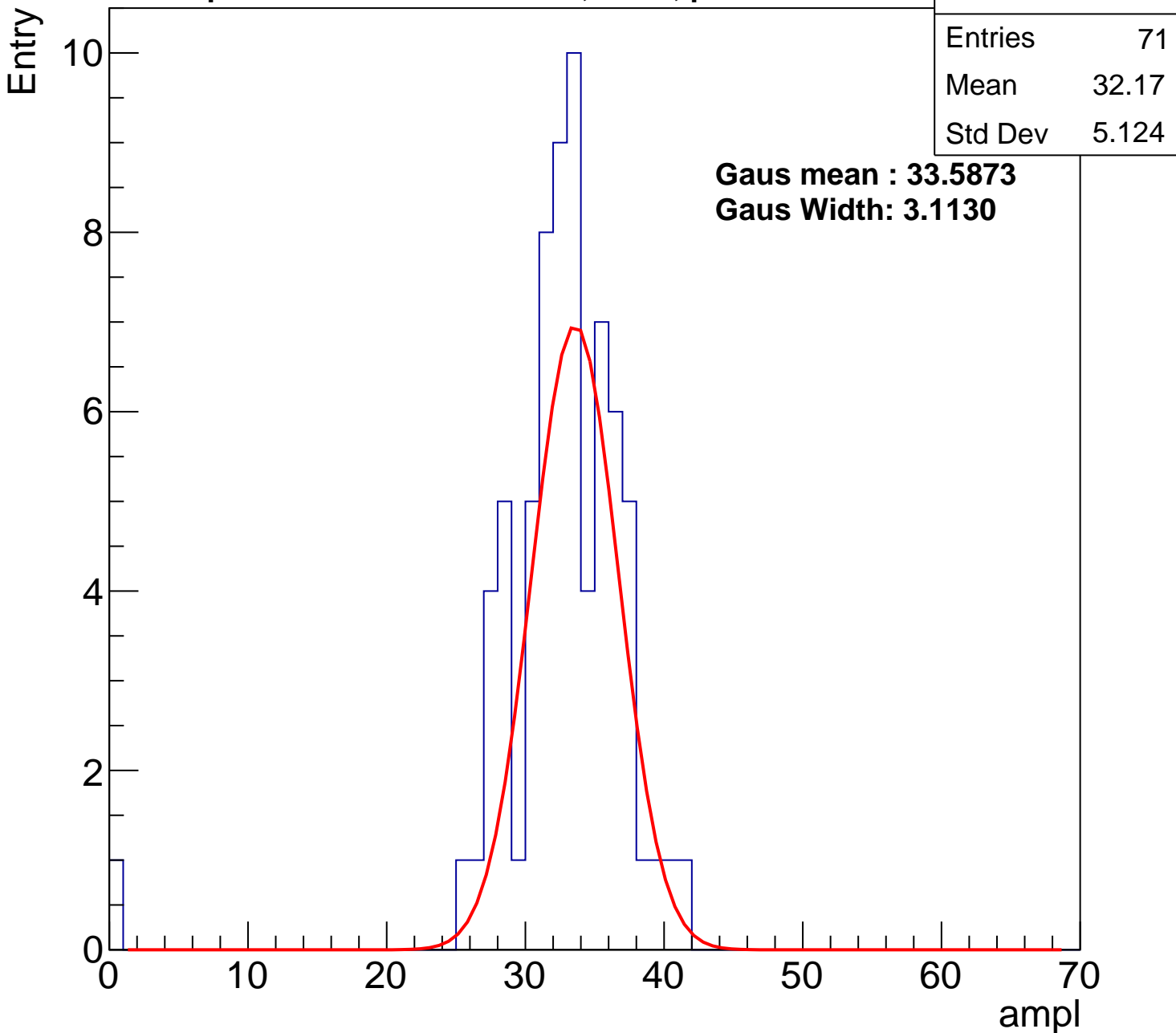
**Gaus Width: 3.1130**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



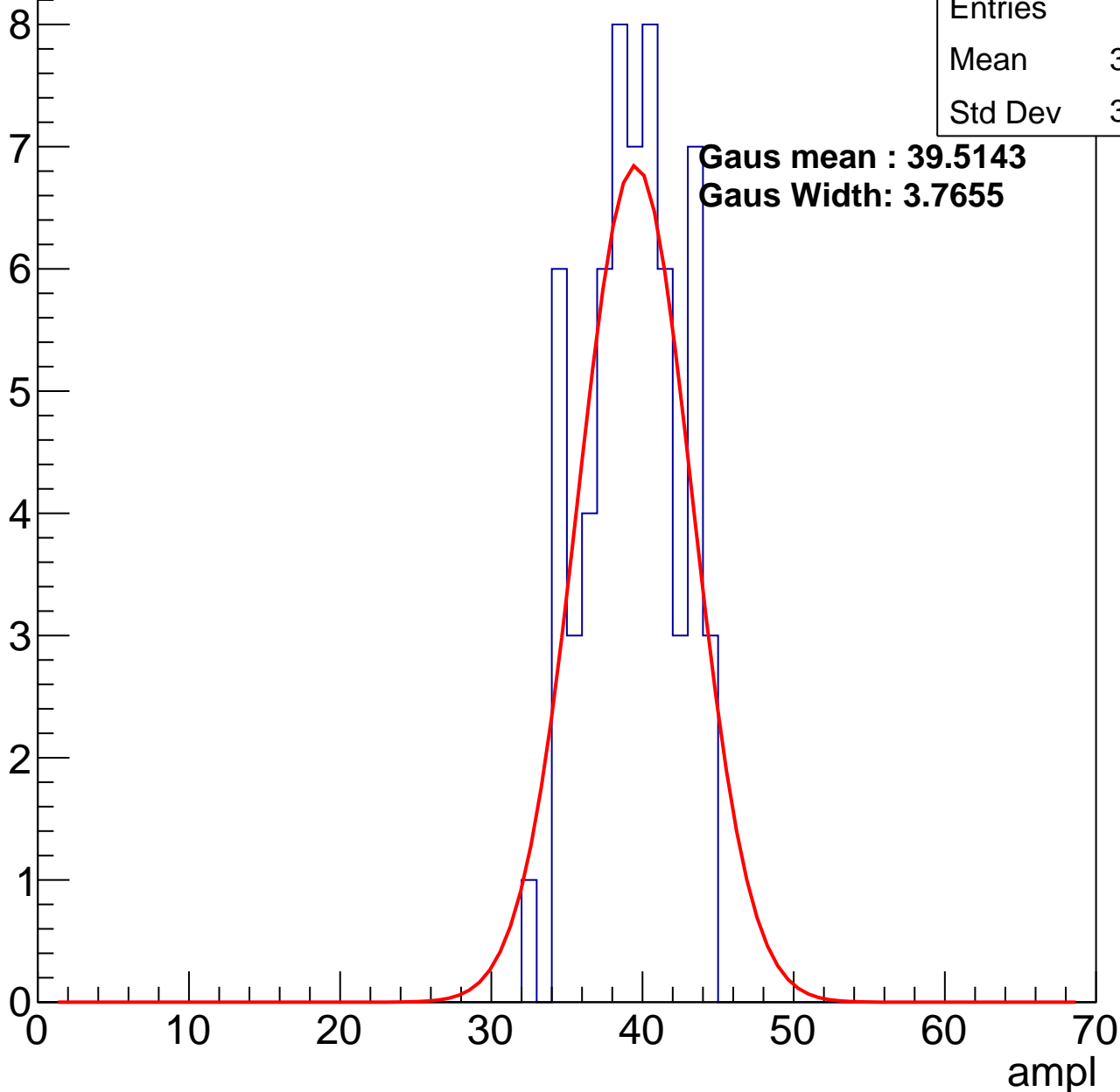
# B0L000S, U26-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.85
Std Dev	3.005

**Gaus mean : 39.5143**  
**Gaus Width: 3.7655**



# B0L000S, U26-ch5, adc2

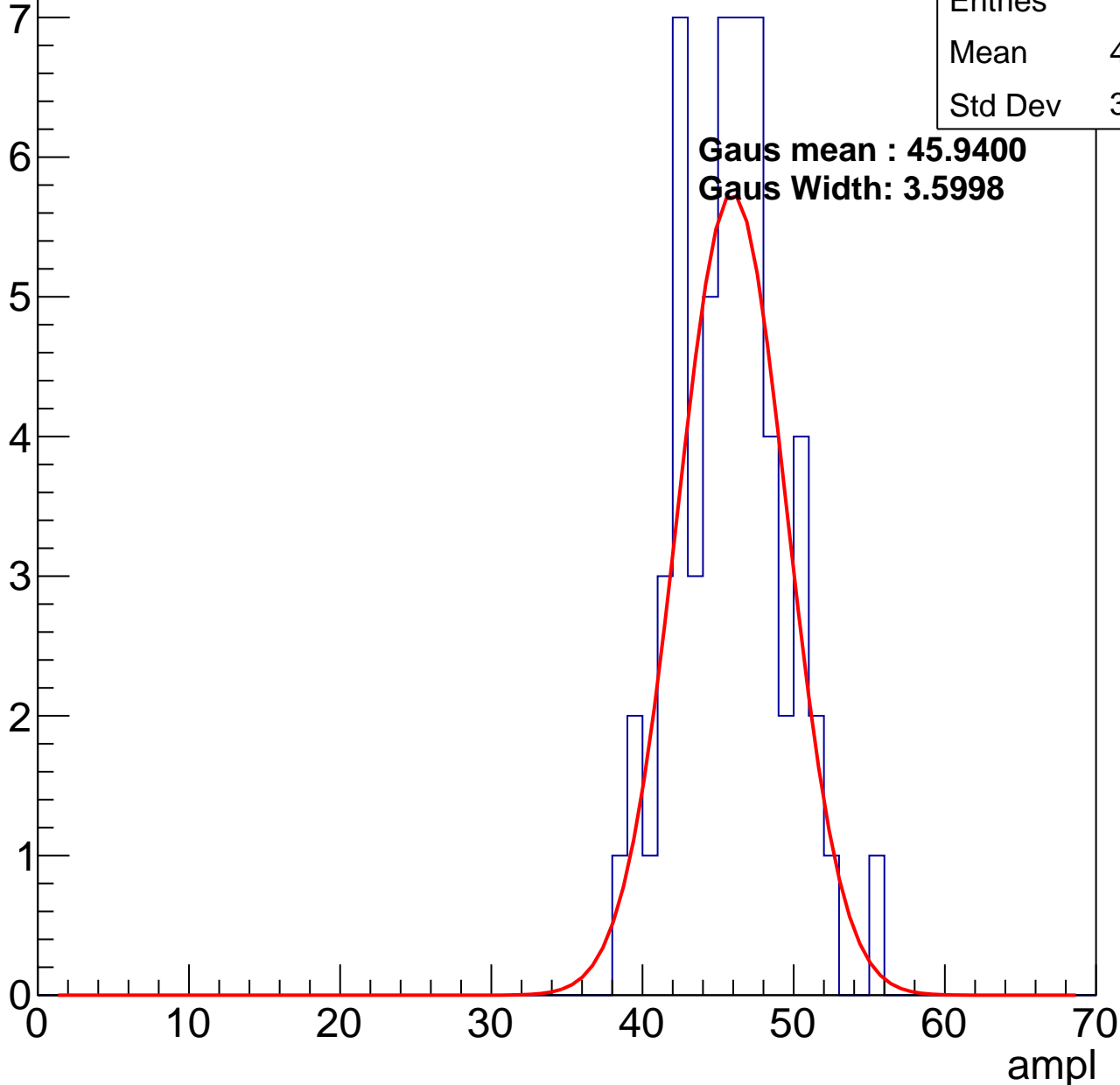
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	45.39
Std Dev	3.478

**Gaus mean : 45.9400**

**Gaus Width: 3.5998**

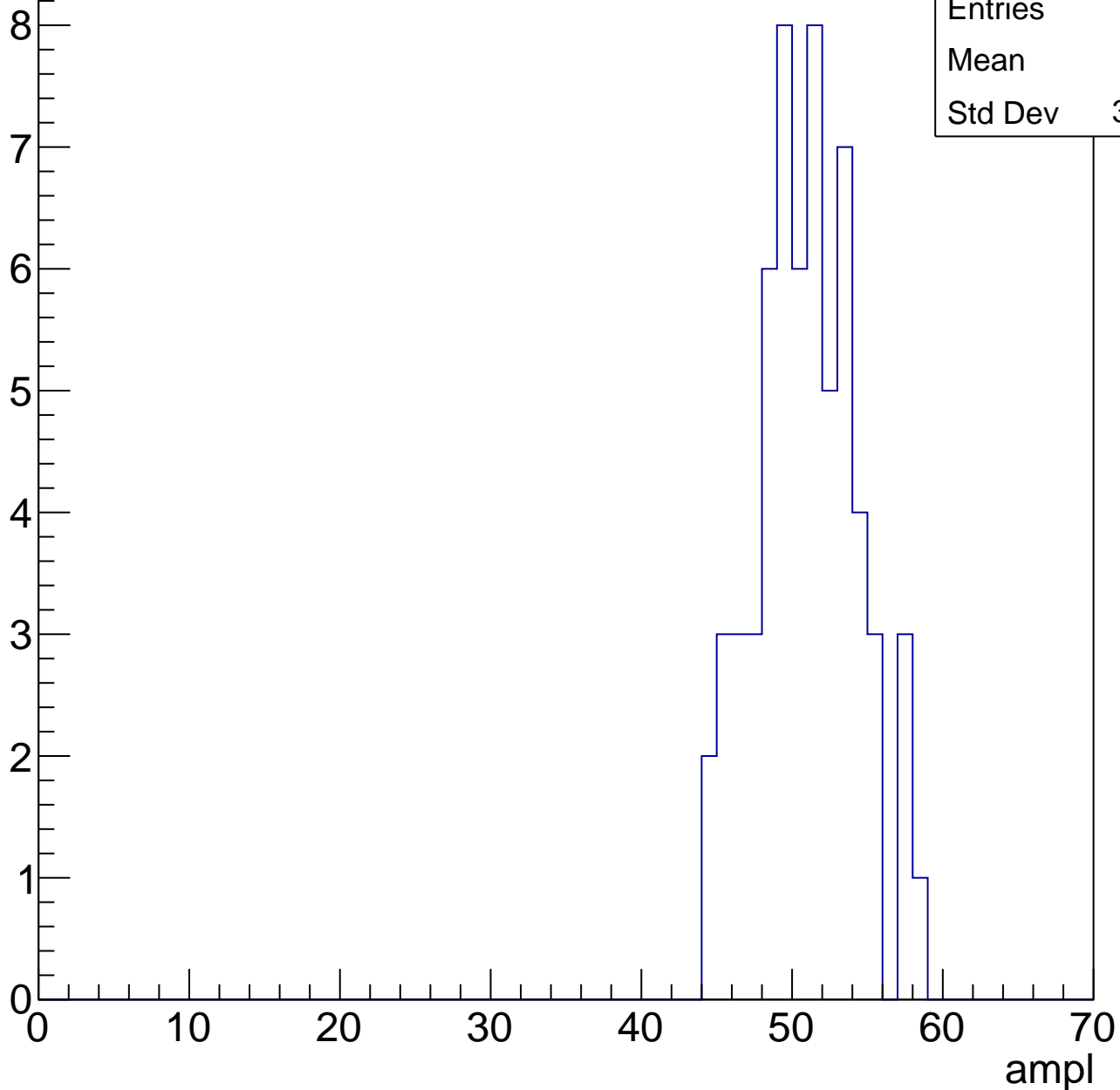


# B0L000S, U26-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

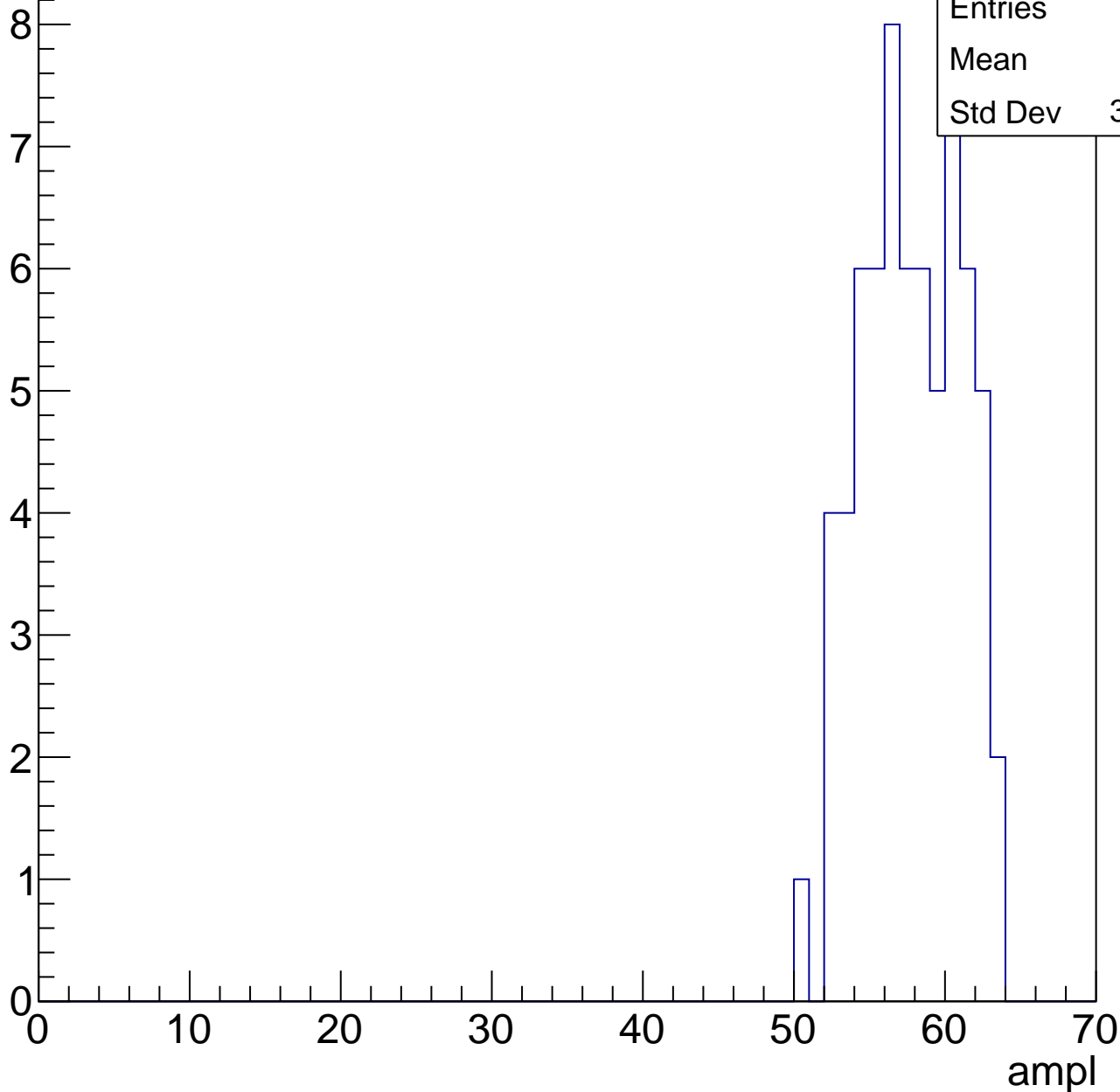
Entries	62
Mean	50.5
Std Dev	3.311



# B0L000S, U26-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



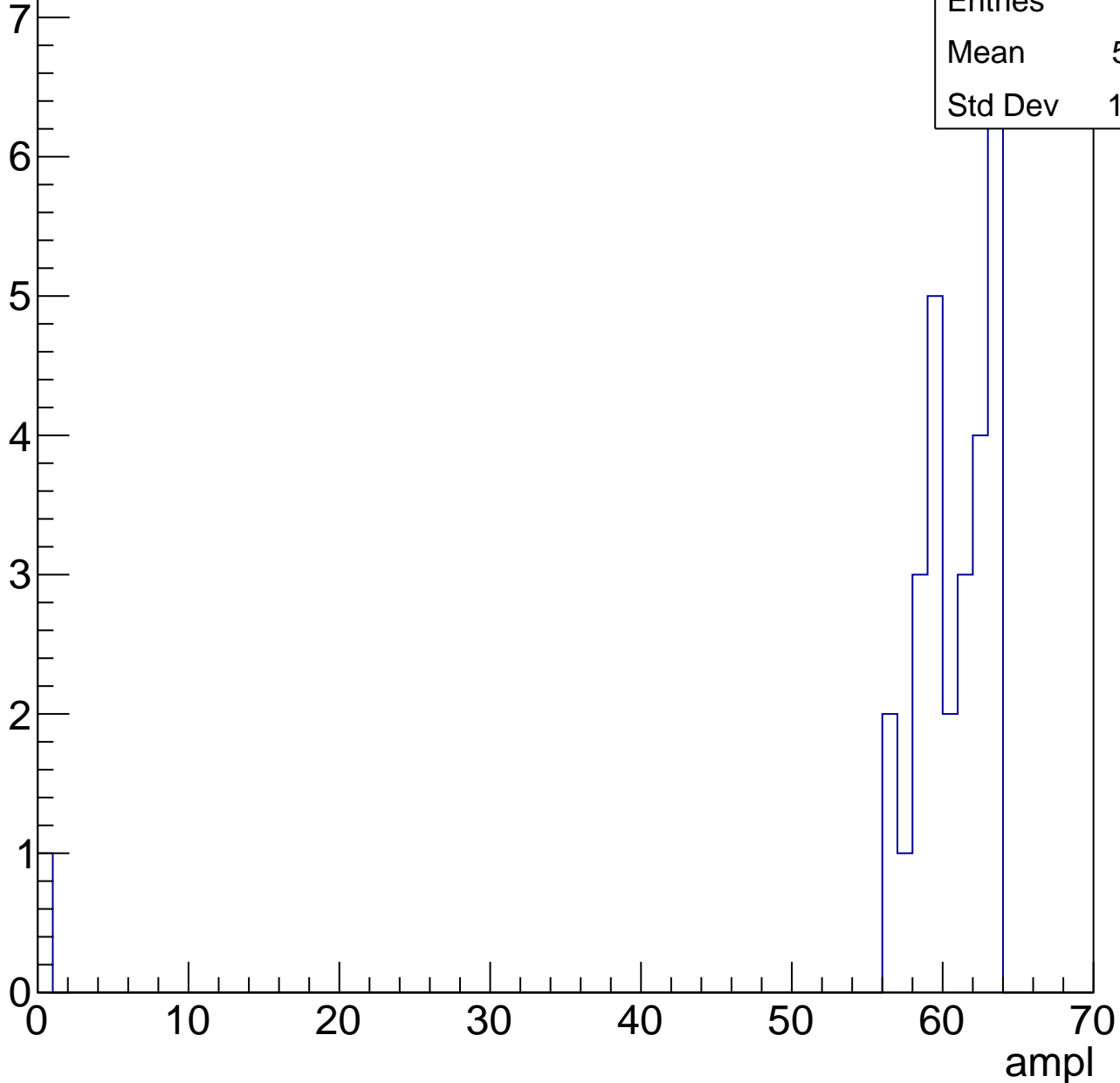
Entries	67
Mean	57.3
Std Dev	3.195

# B0L000S, U26-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.21
Std Dev	11.42



# B0L000S, U26-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L000S, U26-ch6, adc0

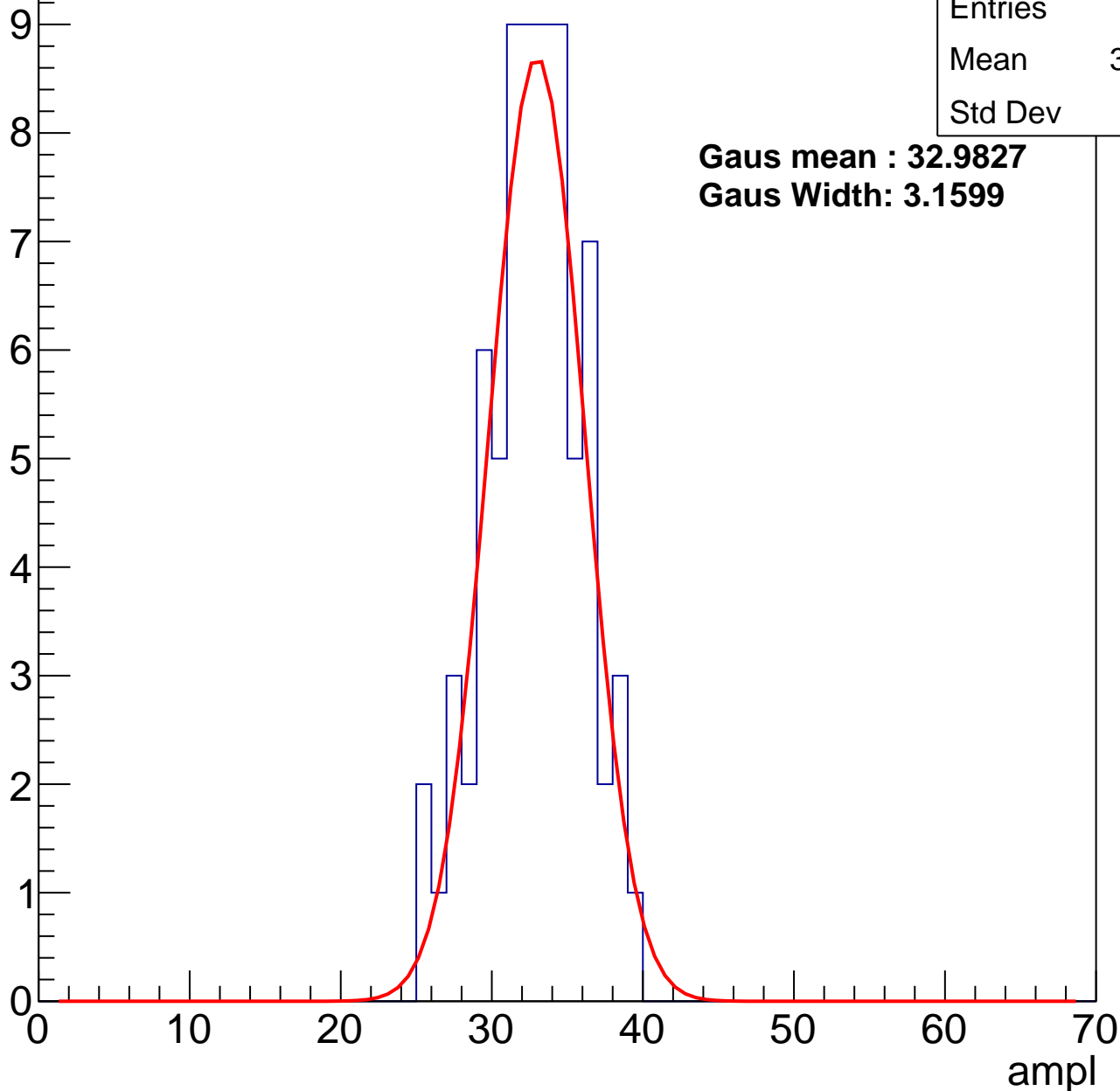
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	32.34
Std Dev	3.15

**Gaus mean : 32.9827**

**Gaus Width: 3.1599**



# B0L000S, U26-ch6, adc1

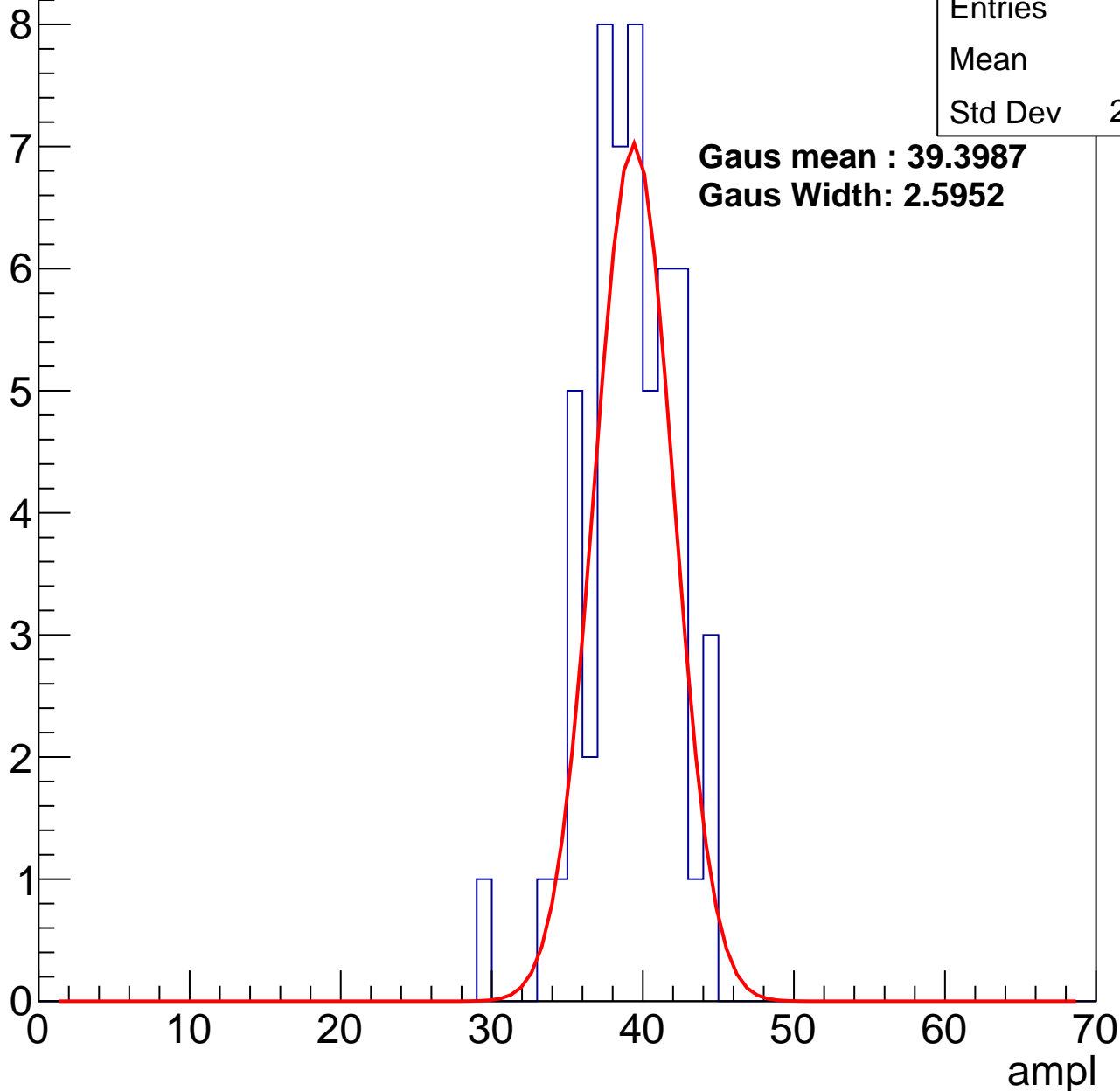
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	38.7
Std Dev	2.942

**Gaus mean : 39.3987**

**Gaus Width: 2.5952**



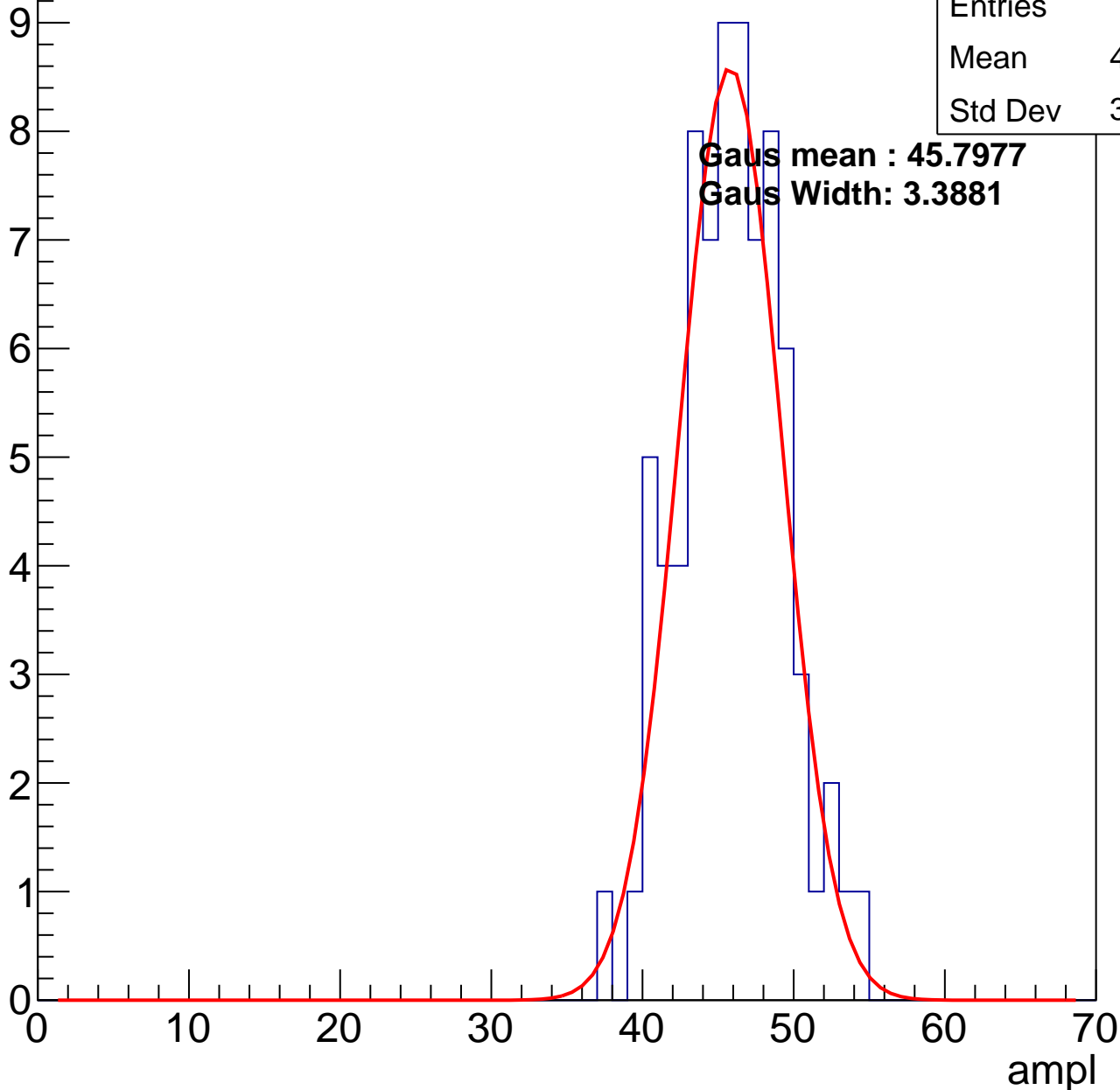
# B0L000S, U26-ch6, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	45.43
Std Dev	3.436

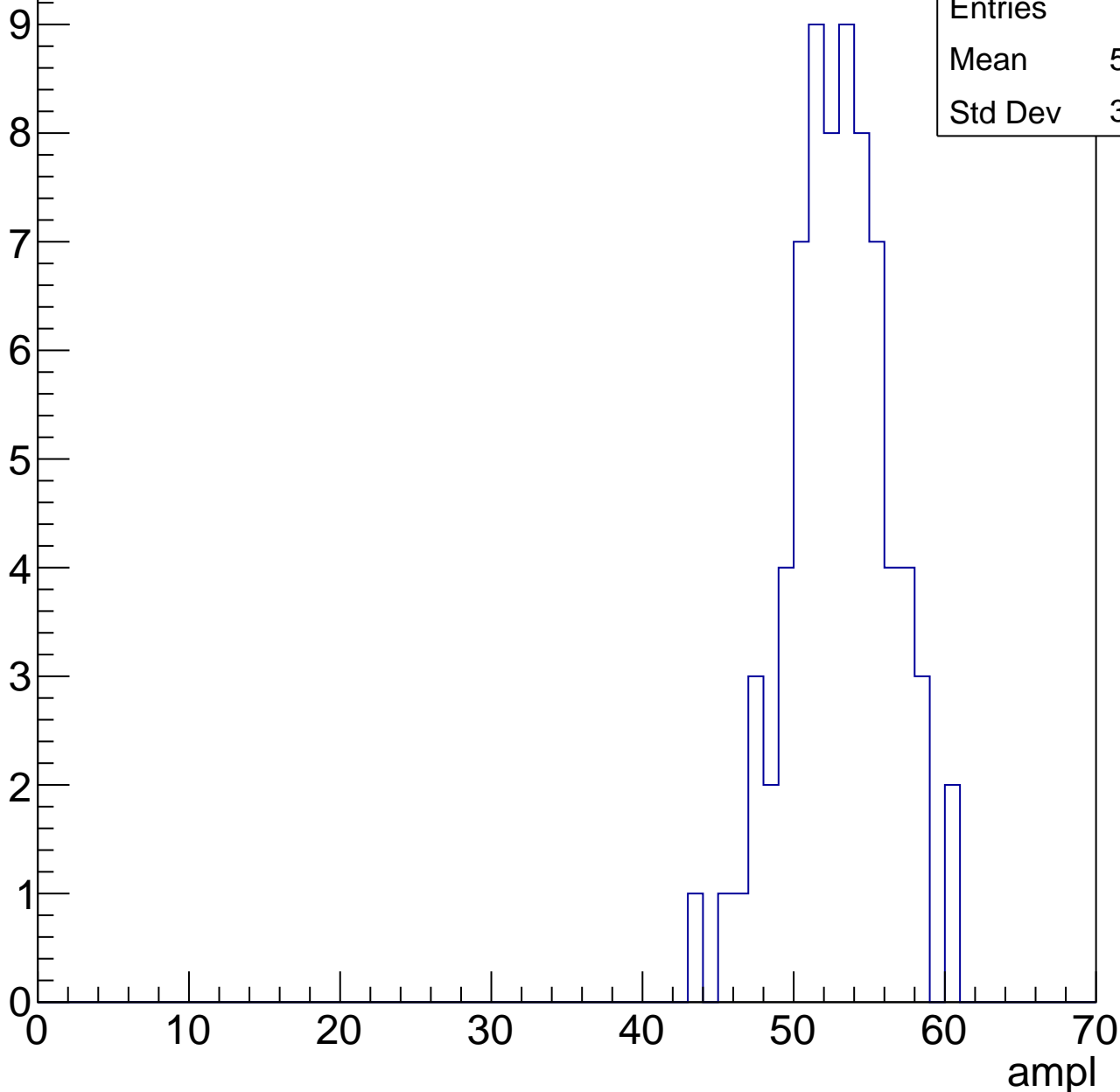
**Gaus mean : 45.7977**  
**Gaus Width: 3.3881**



# B0L000S, U26-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

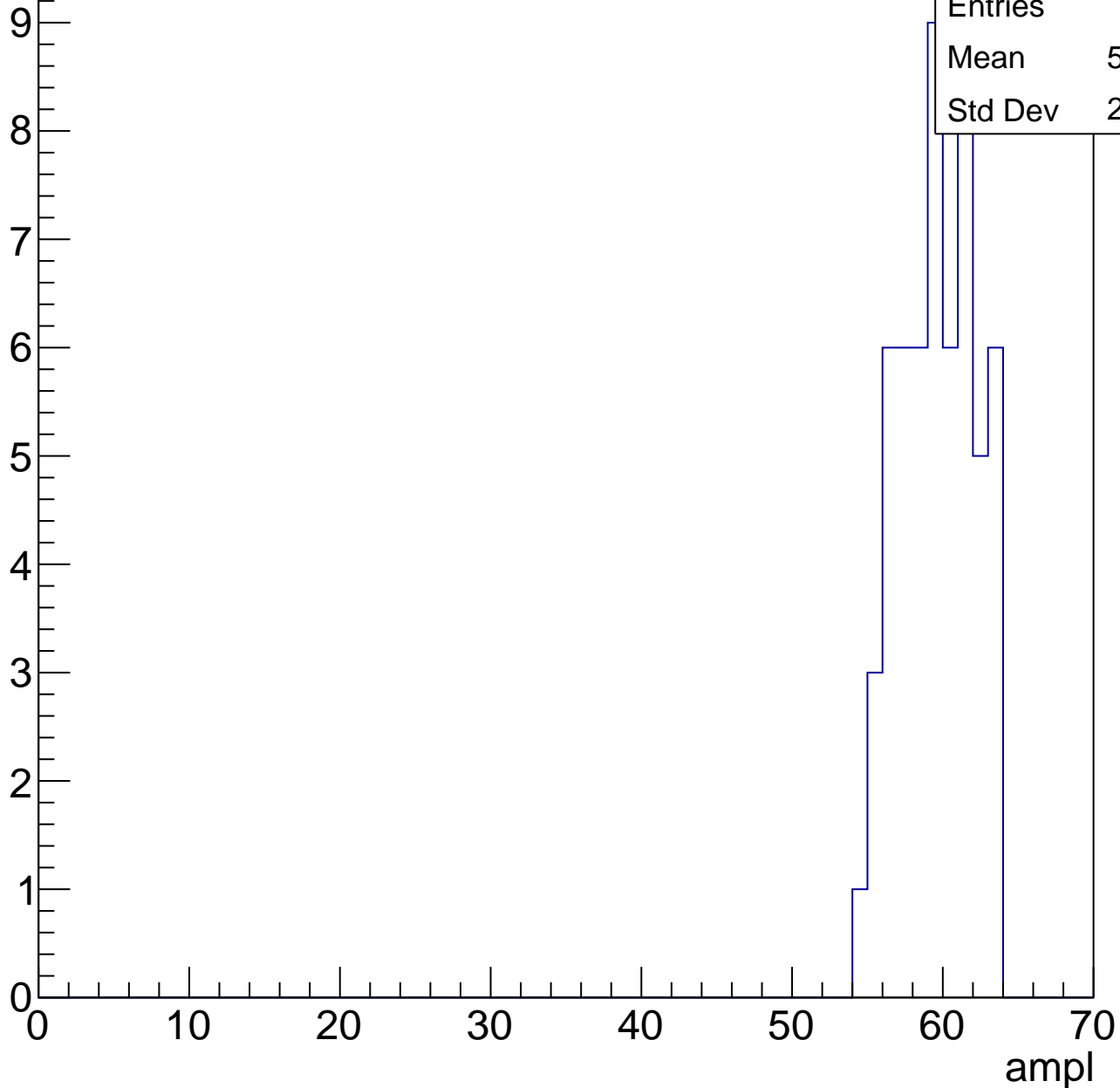


Entries	73
Mean	52.49
Std Dev	3.376

# B0L000S, U26-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

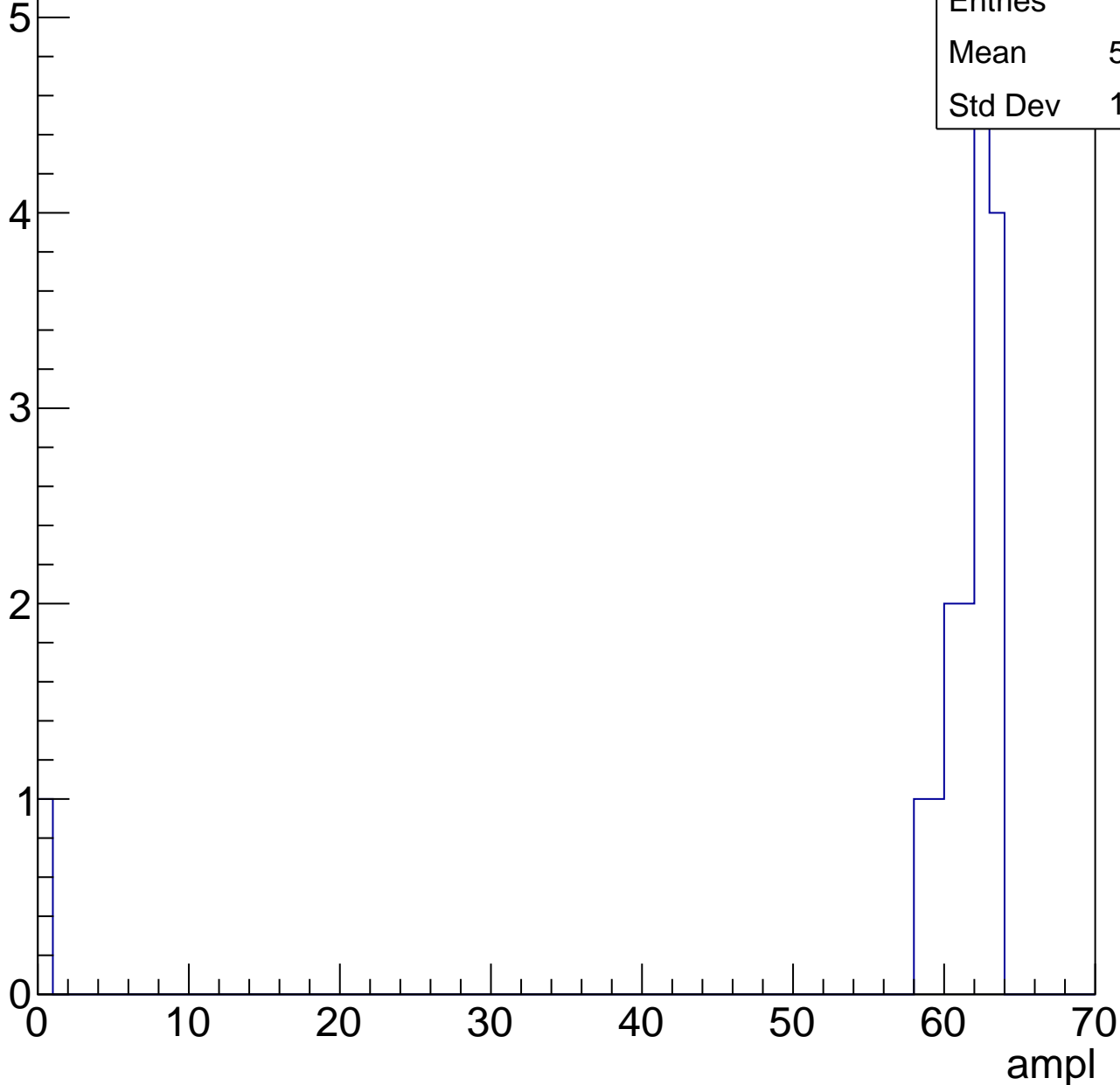


# B0L000S, U26-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.56
Std Dev	14.93



# B0L000S, U26-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U26-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch7, adc0

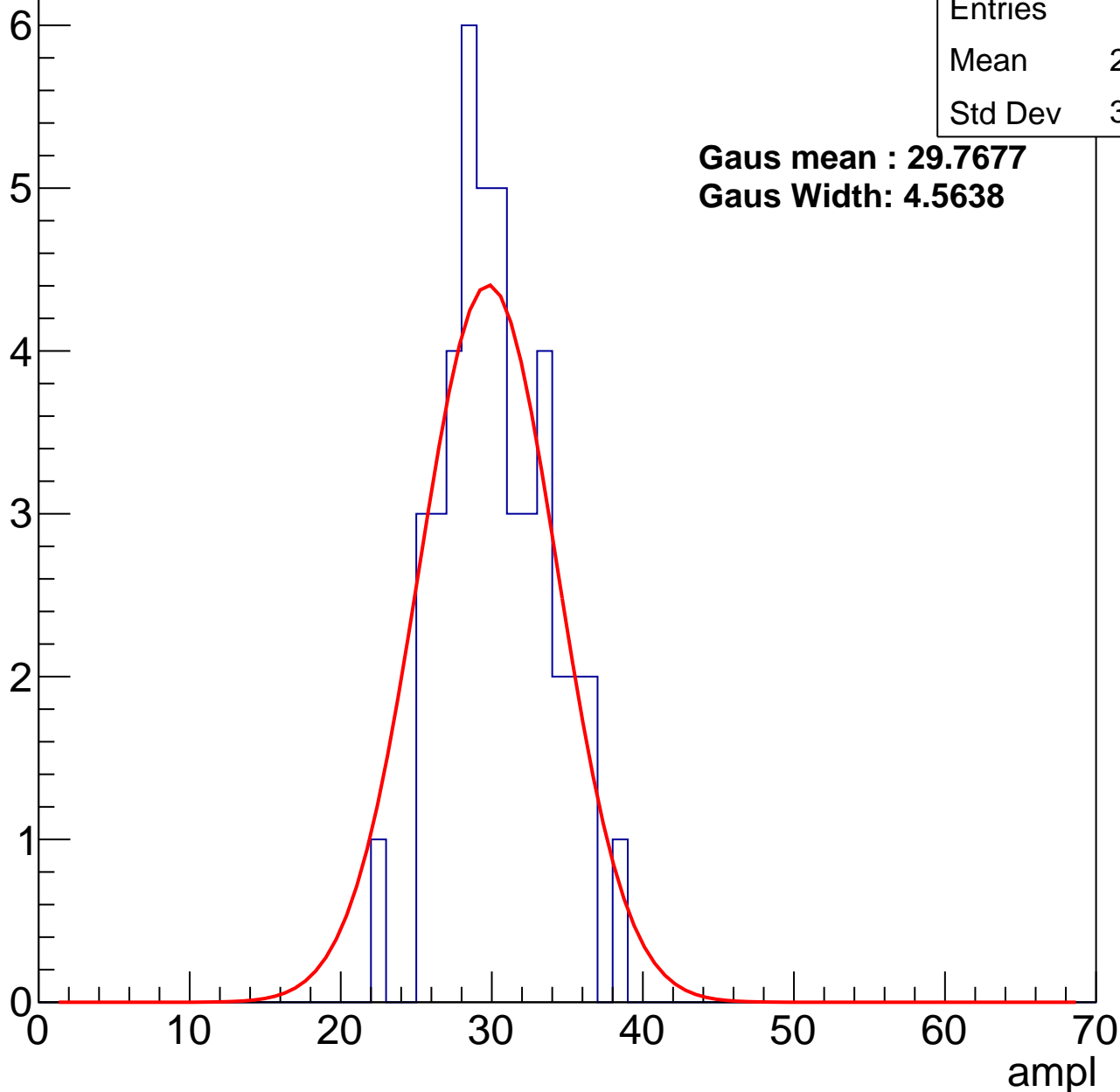
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	29.89
Std Dev	3.439

**Gaus mean : 29.7677**

**Gaus Width: 4.5638**



# B0L000S, U26-ch7, adc1

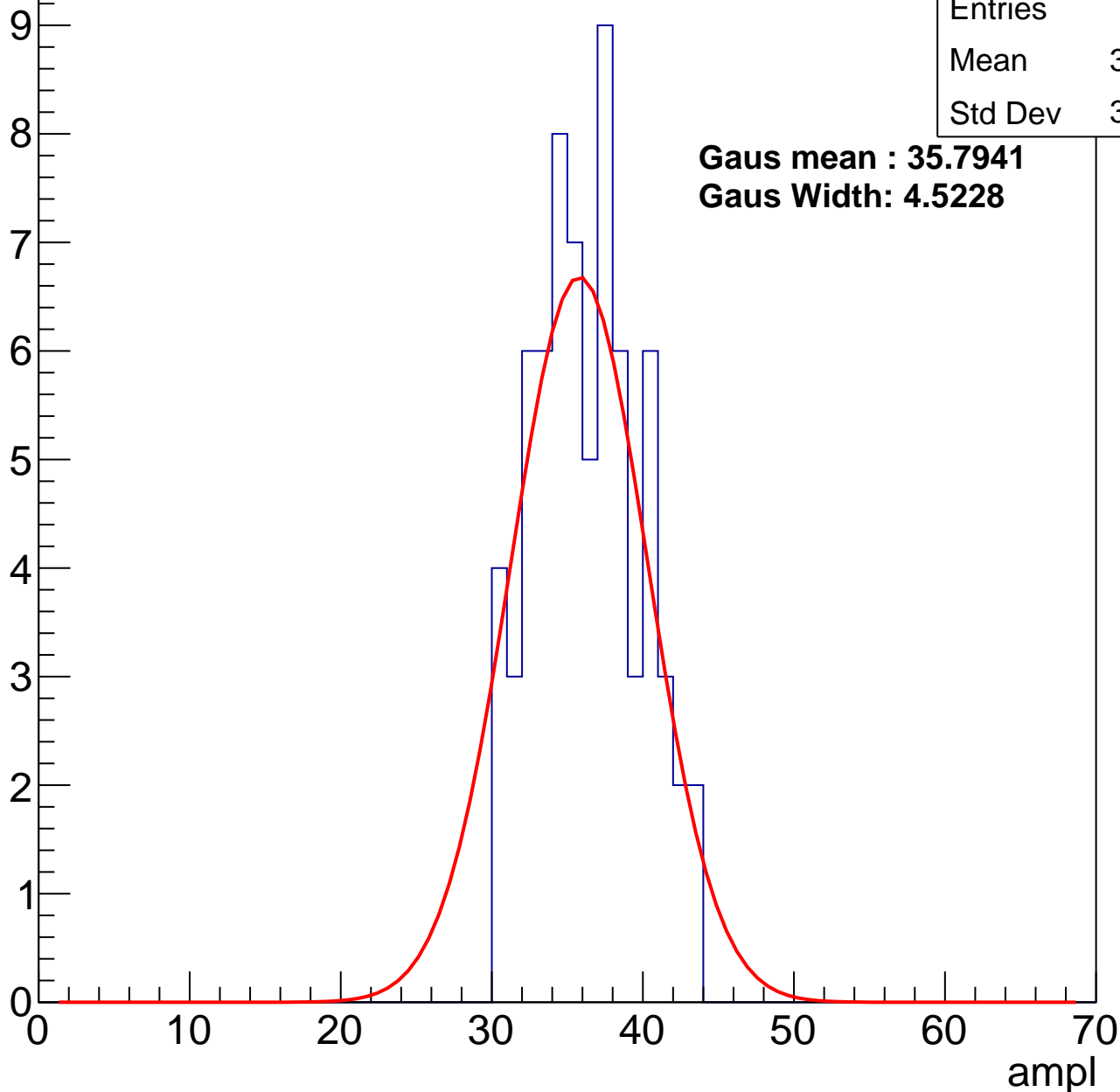
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.87
Std Dev	3.397

**Gaus mean : 35.7941**

**Gaus Width: 4.5228**



# B0L000S, U26-ch7, adc2

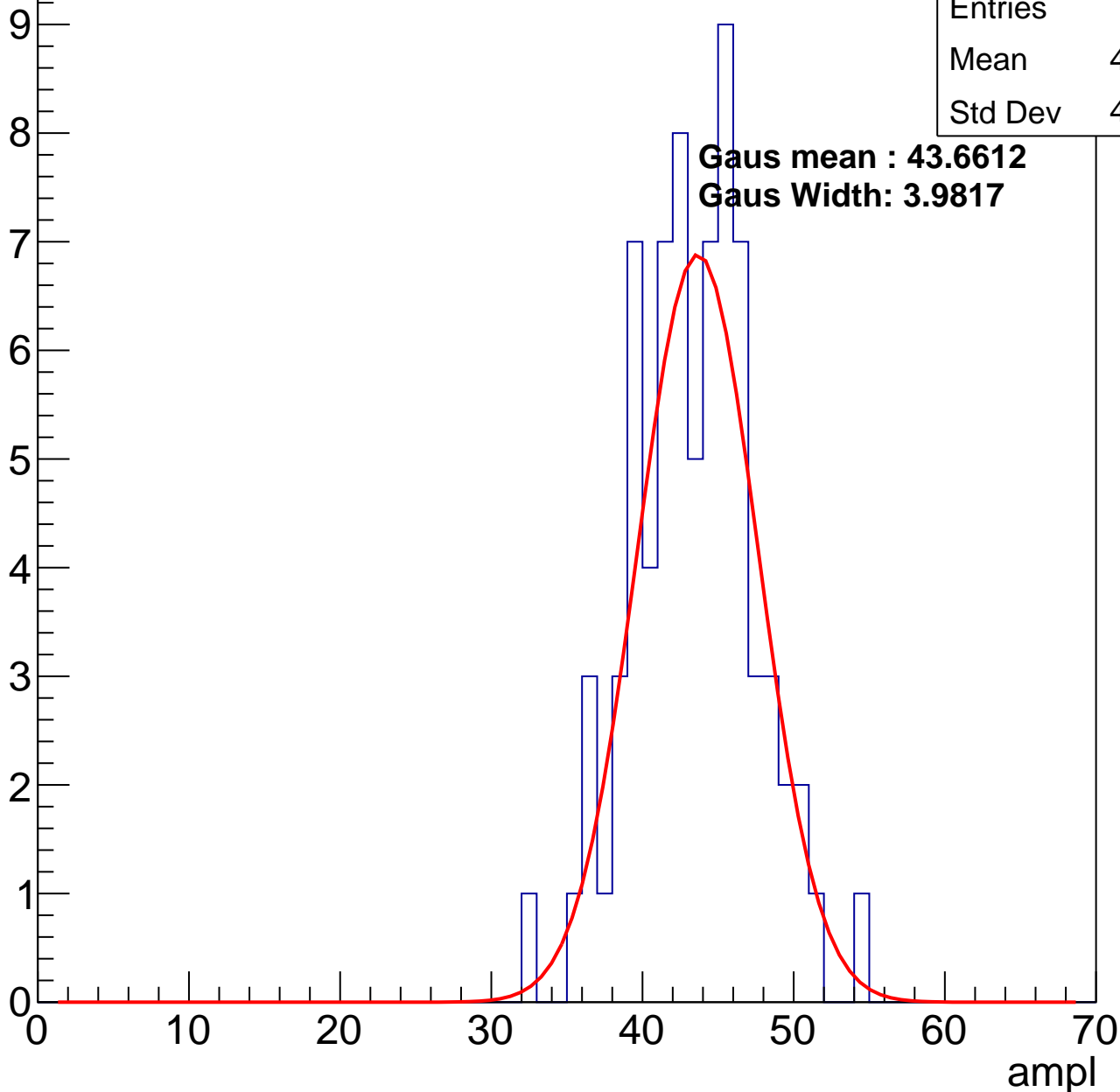
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	42.93
Std Dev	4.018

**Gaus mean : 43.6612**

**Gaus Width: 3.9817**

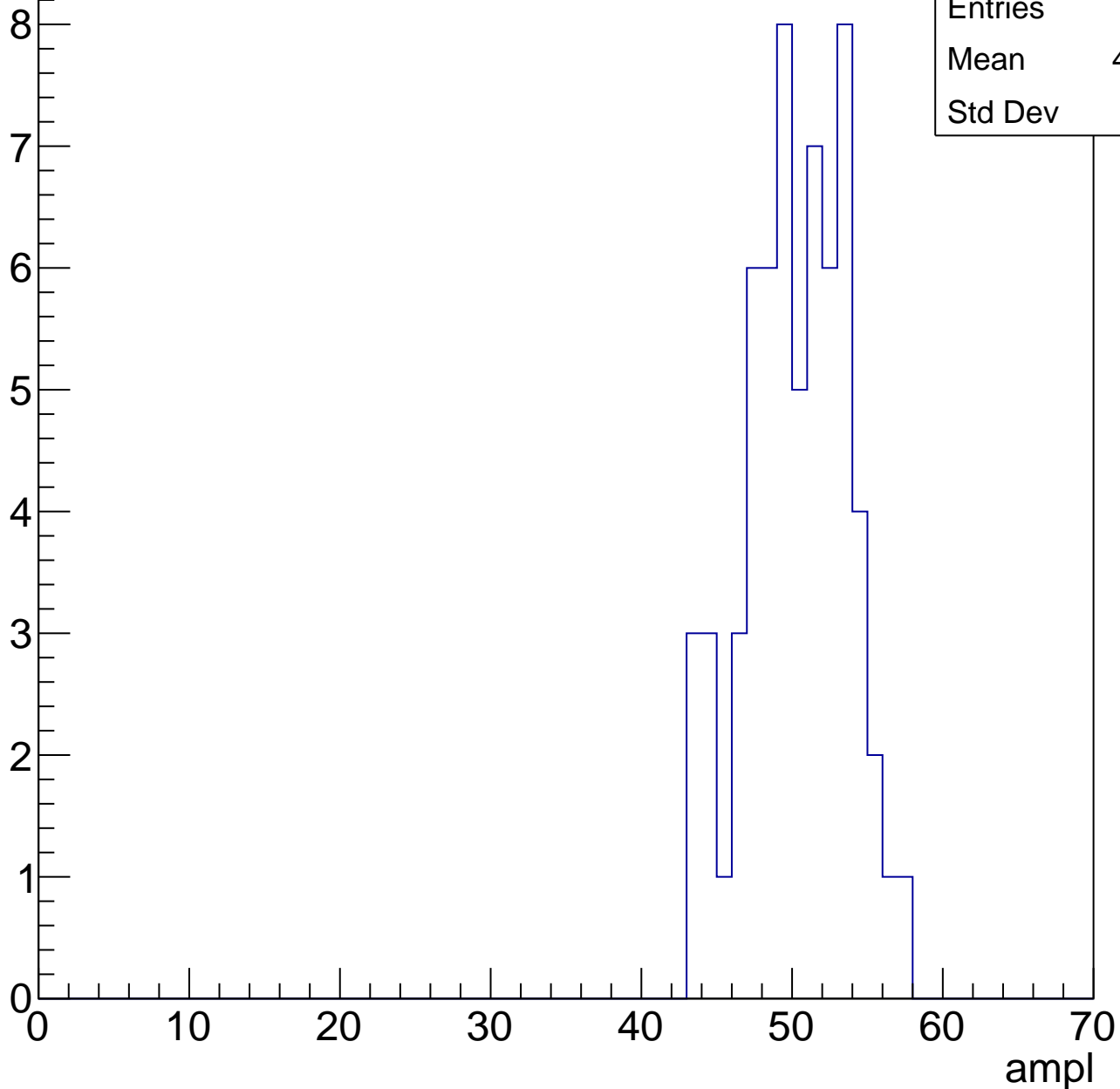


# B0L000S, U26-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.81
Std Dev	3.34

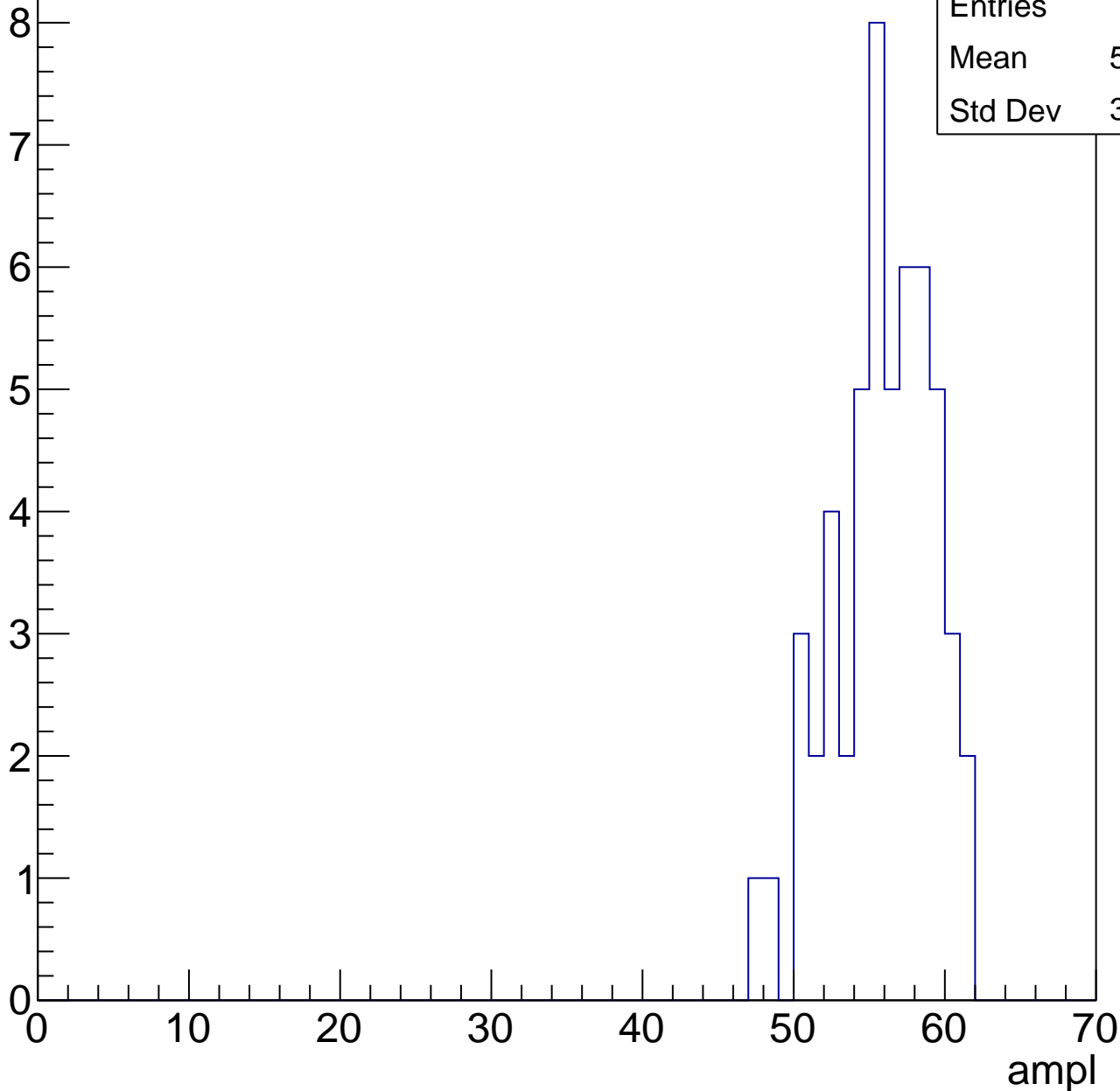


# B0L000S, U26-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	55.43
Std Dev	3.277

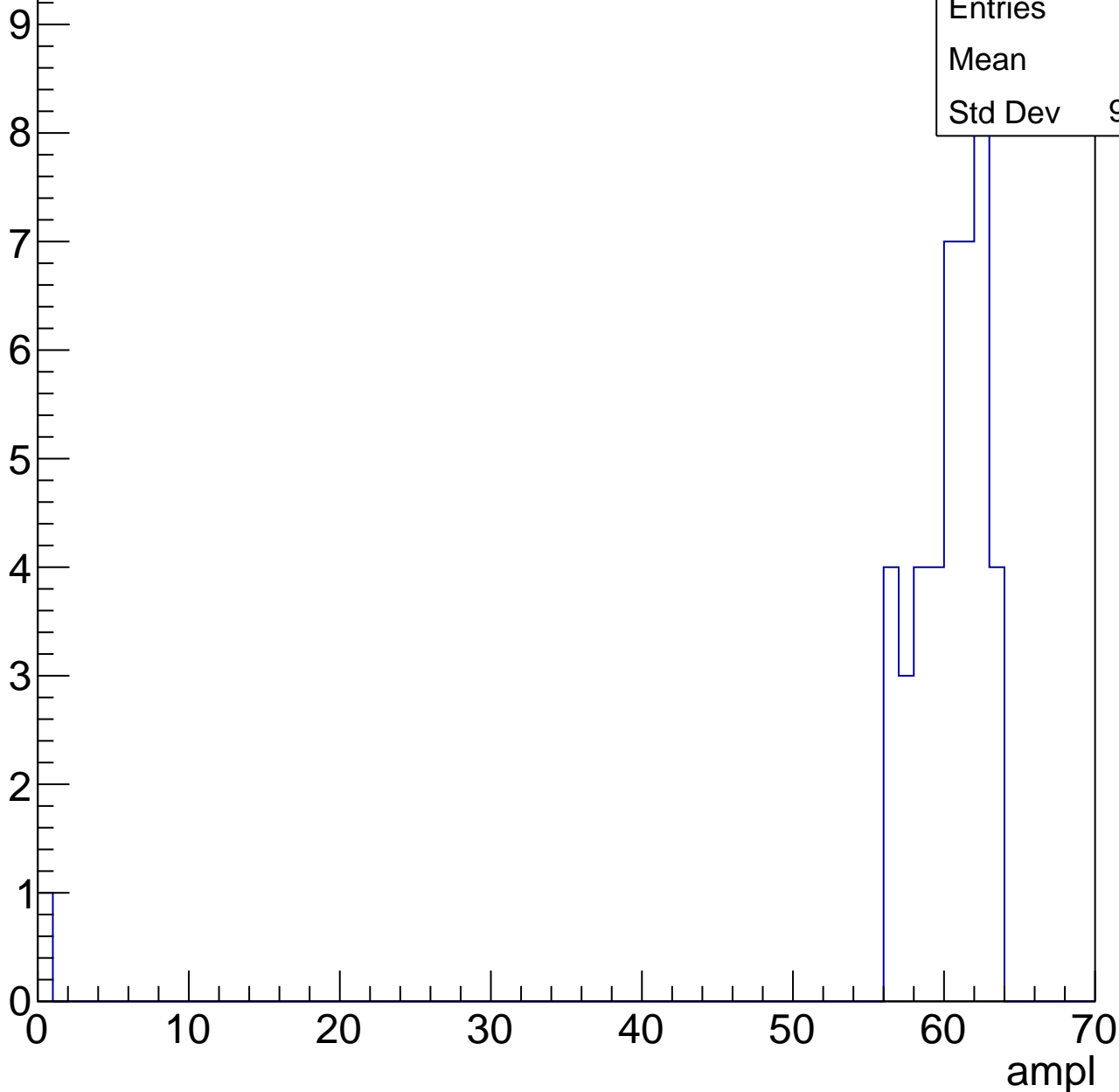


# B0L000S, U26-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

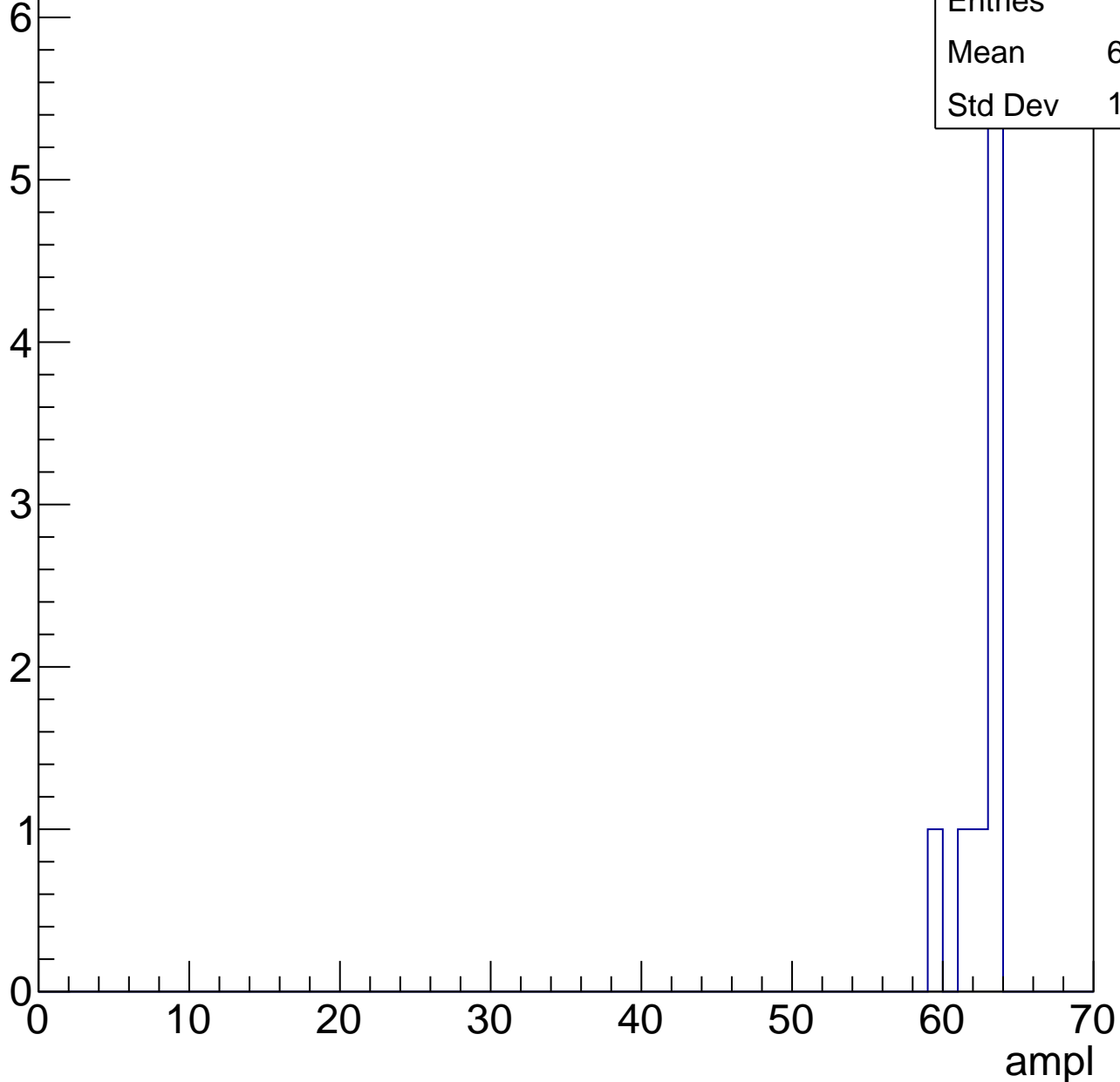
Entries	43
Mean	58.6
Std Dev	9.284



# B0L000S, U26-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	9
Mean	62.22
Std Dev	1.315



# B0L000S, U26-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch8, adc0

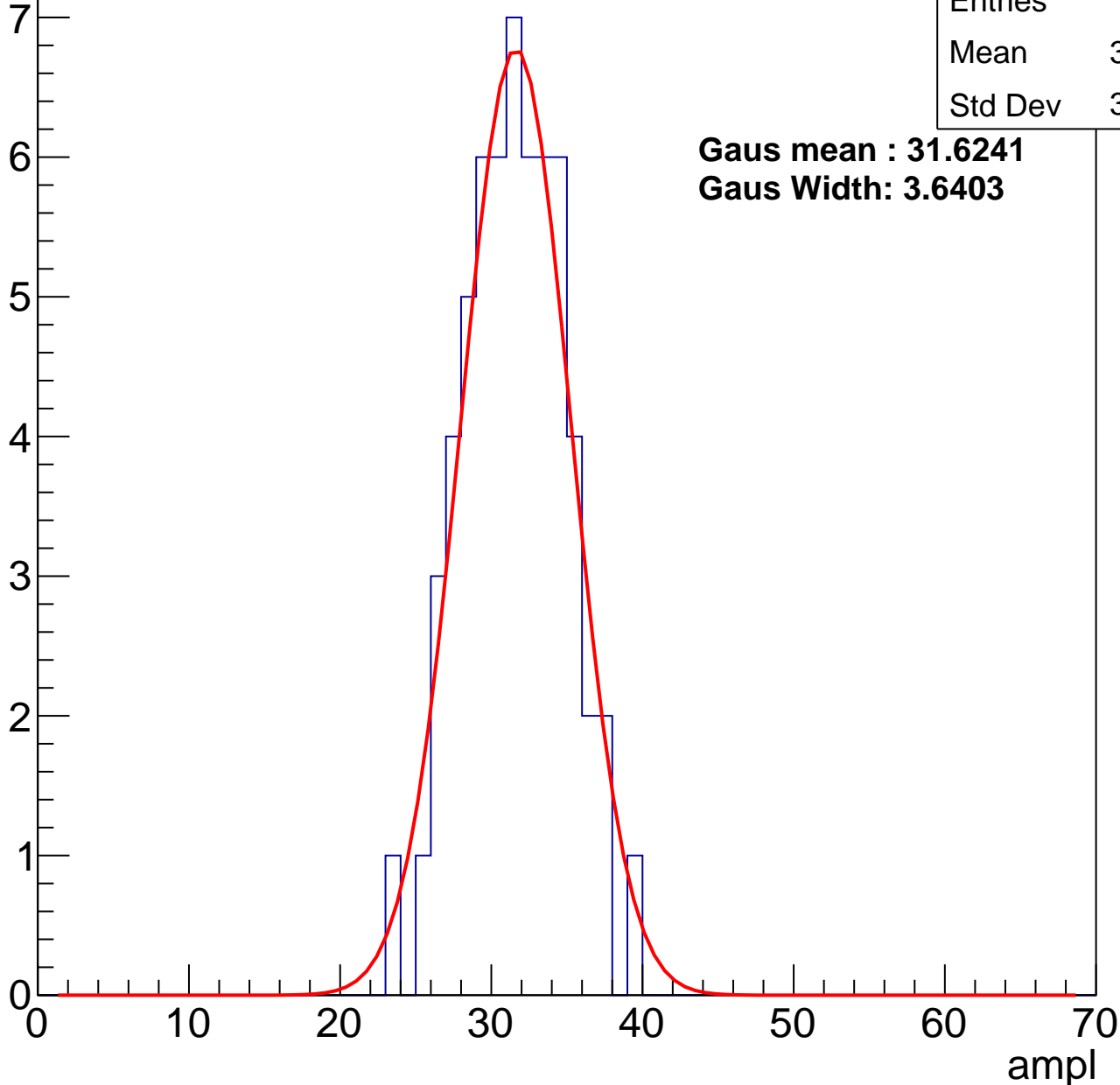
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	31.07
Std Dev	3.286

**Gaus mean : 31.6241**

**Gaus Width: 3.6403**



# B0L000S, U26-ch8, adc1

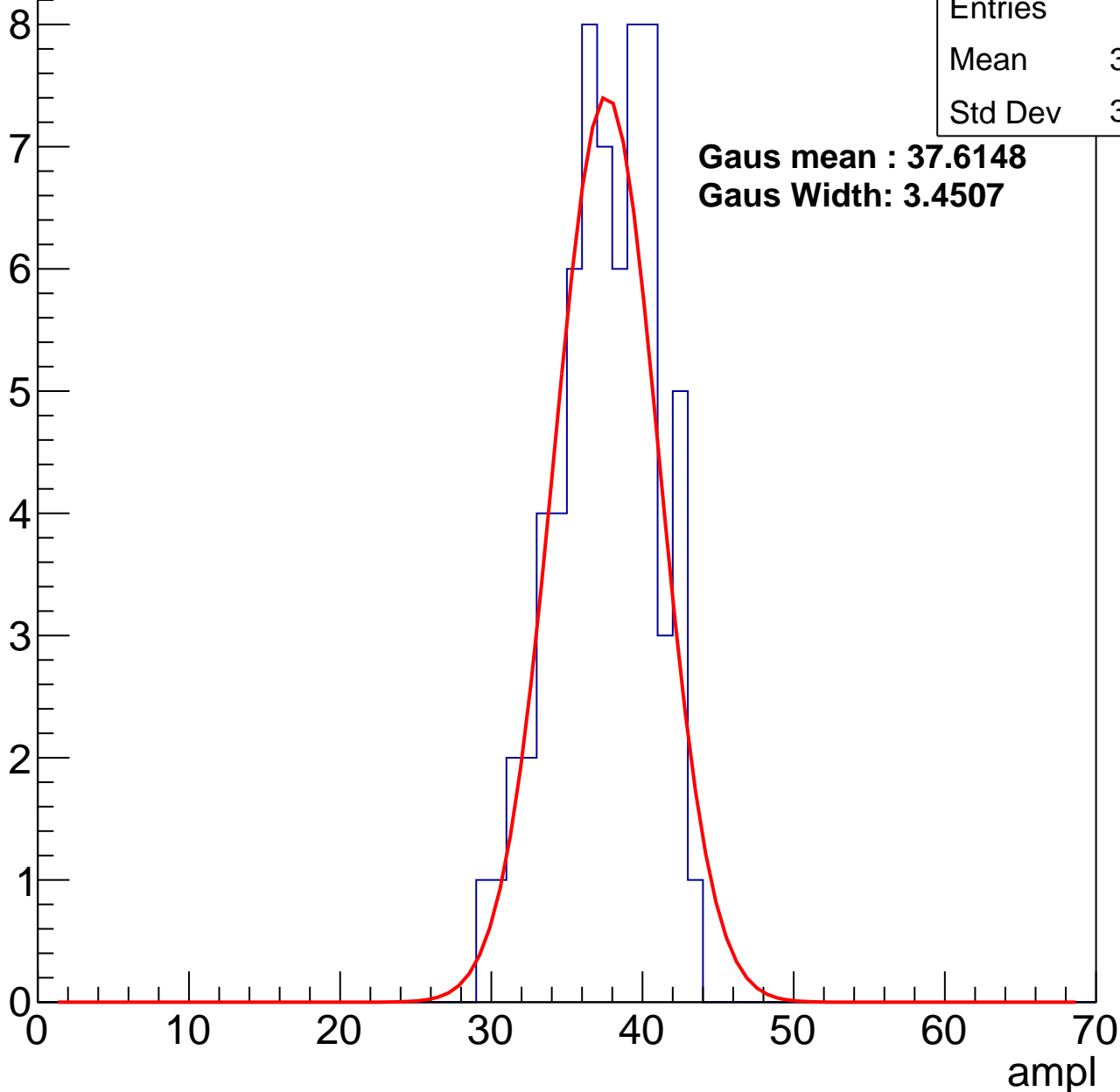
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.06
Std Dev	3.223

**Gaus mean : 37.6148**

**Gaus Width: 3.4507**



# B0L000S, U26-ch8, adc2

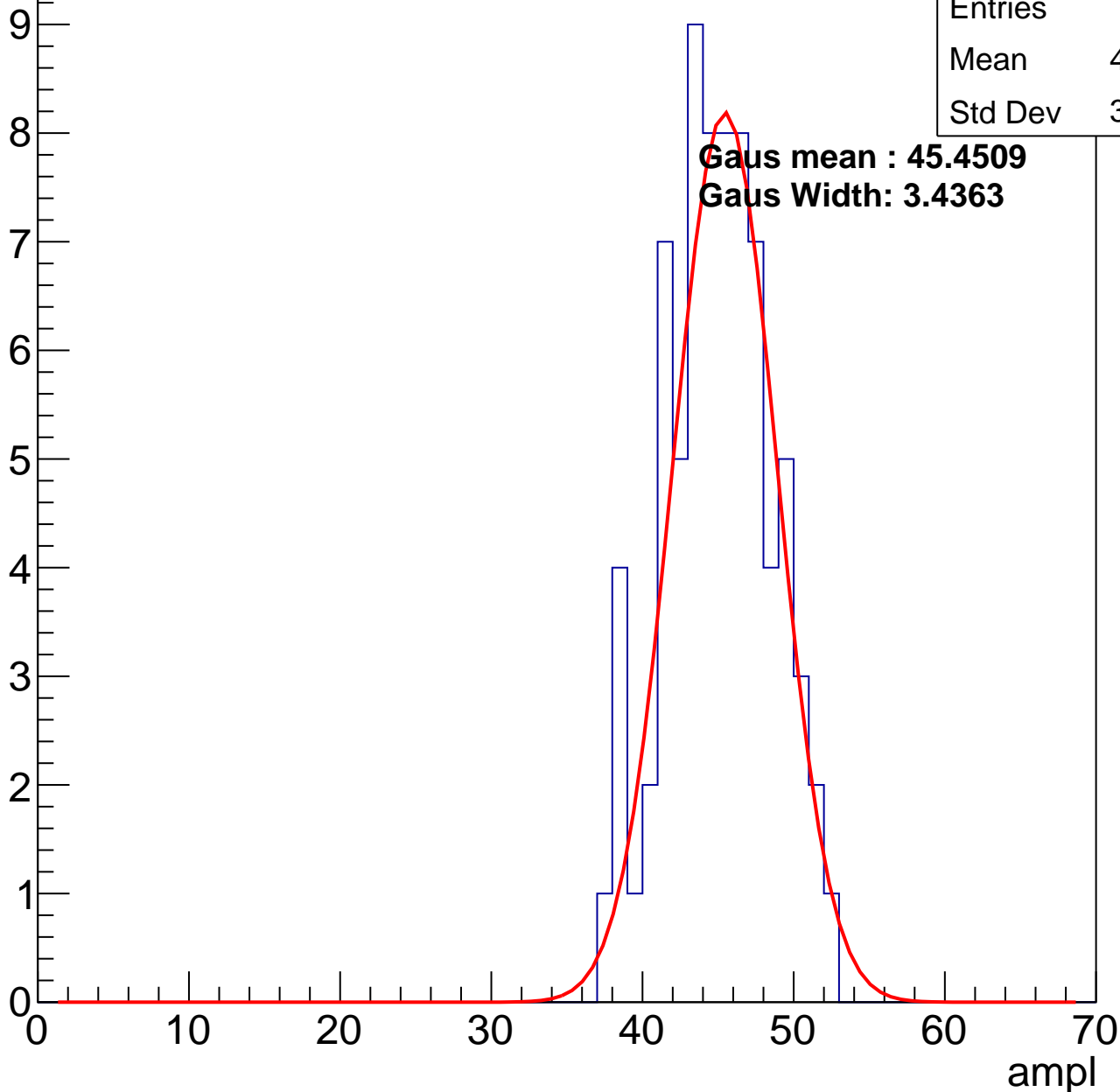
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.56
Std Dev	3.426

**Gaus mean : 45.4509**

**Gaus Width: 3.4363**

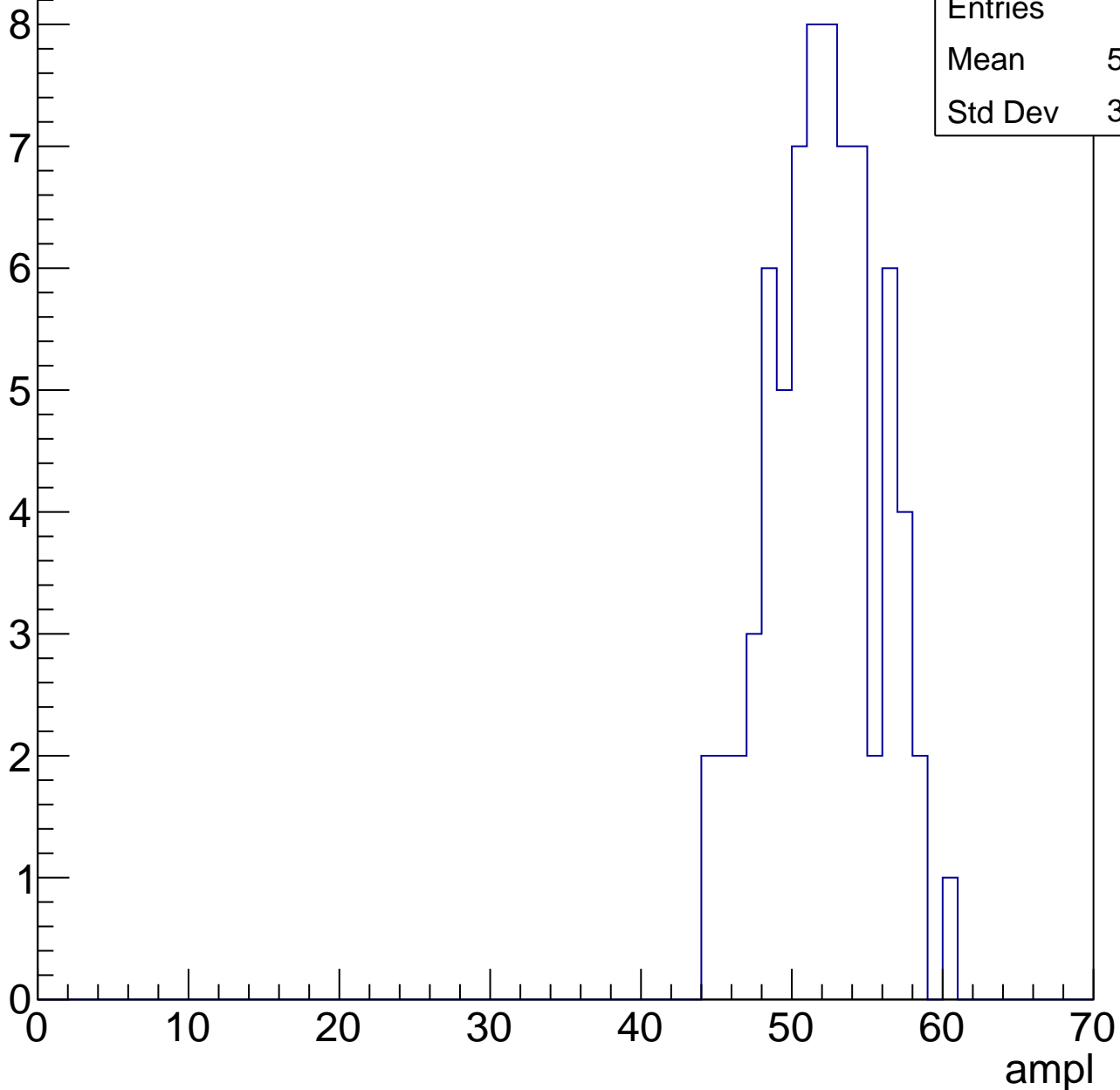


# B0L000S, U26-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

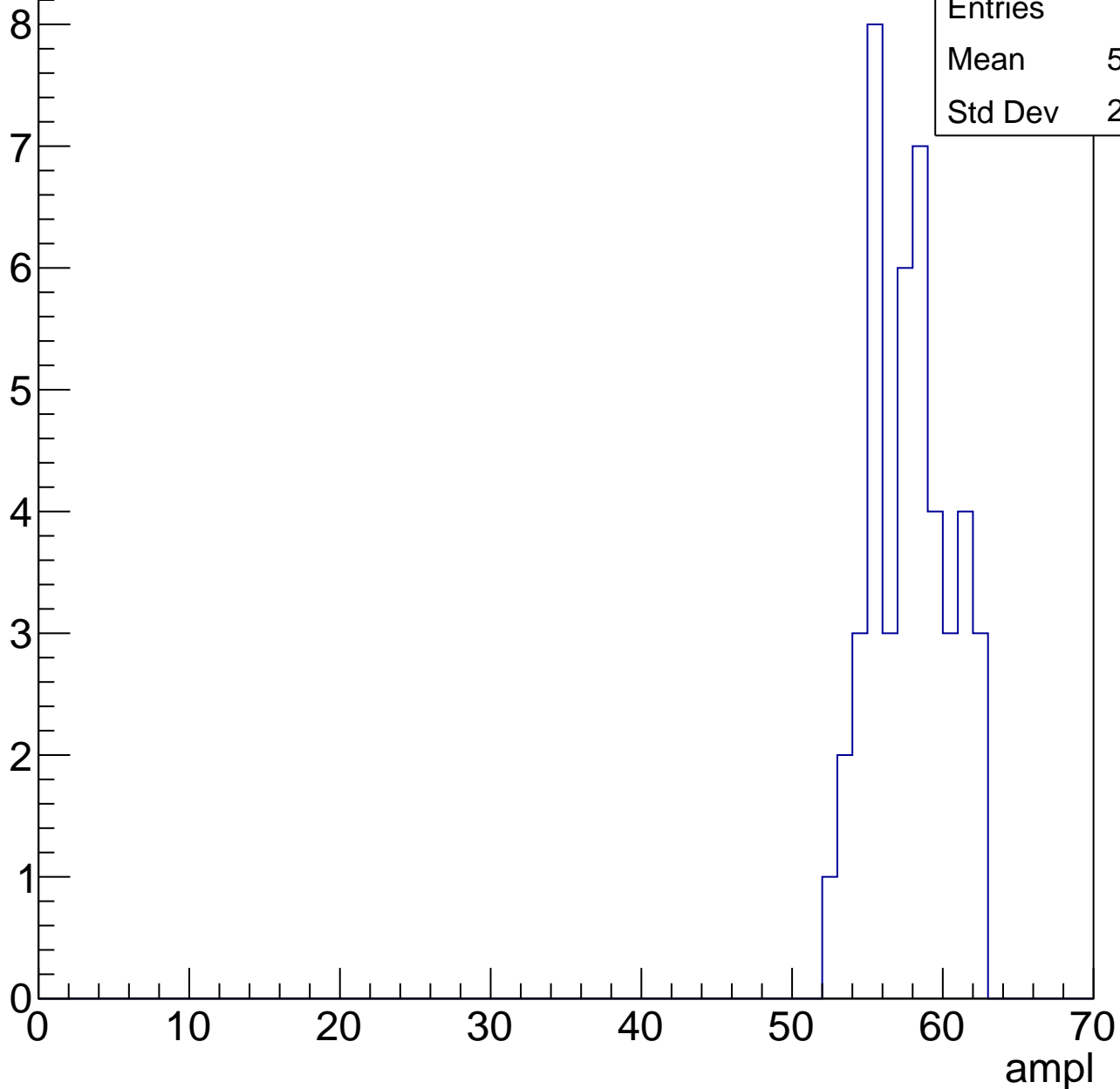
Entries	72
Mean	51.62
Std Dev	3.584



# B0L000S, U26-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



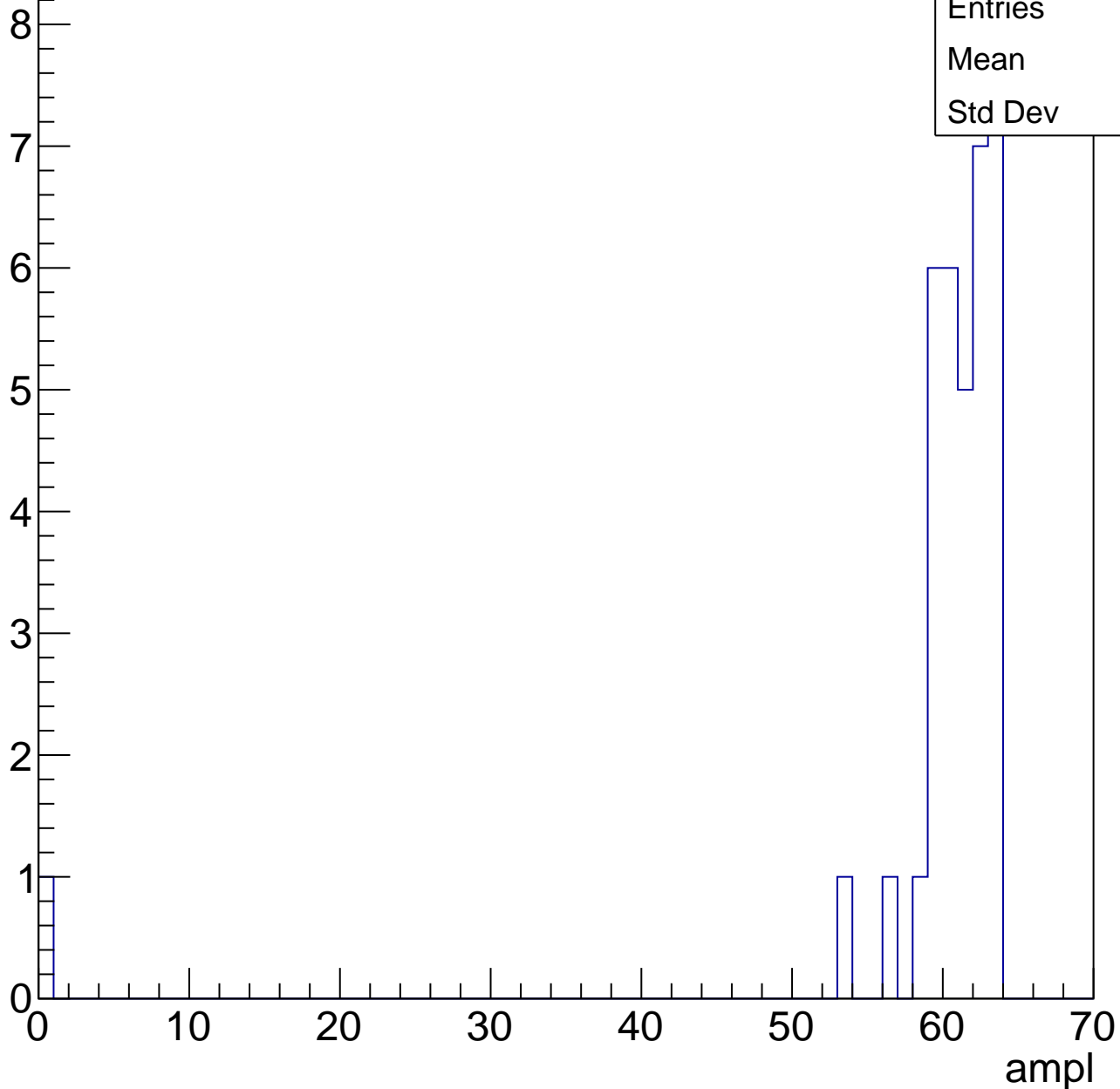
Entries	44
Mean	57.32
Std Dev	2.627

# B0L000S, U26-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	59
Std Dev	10.2



# B0L000S, U26-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch9, adc0

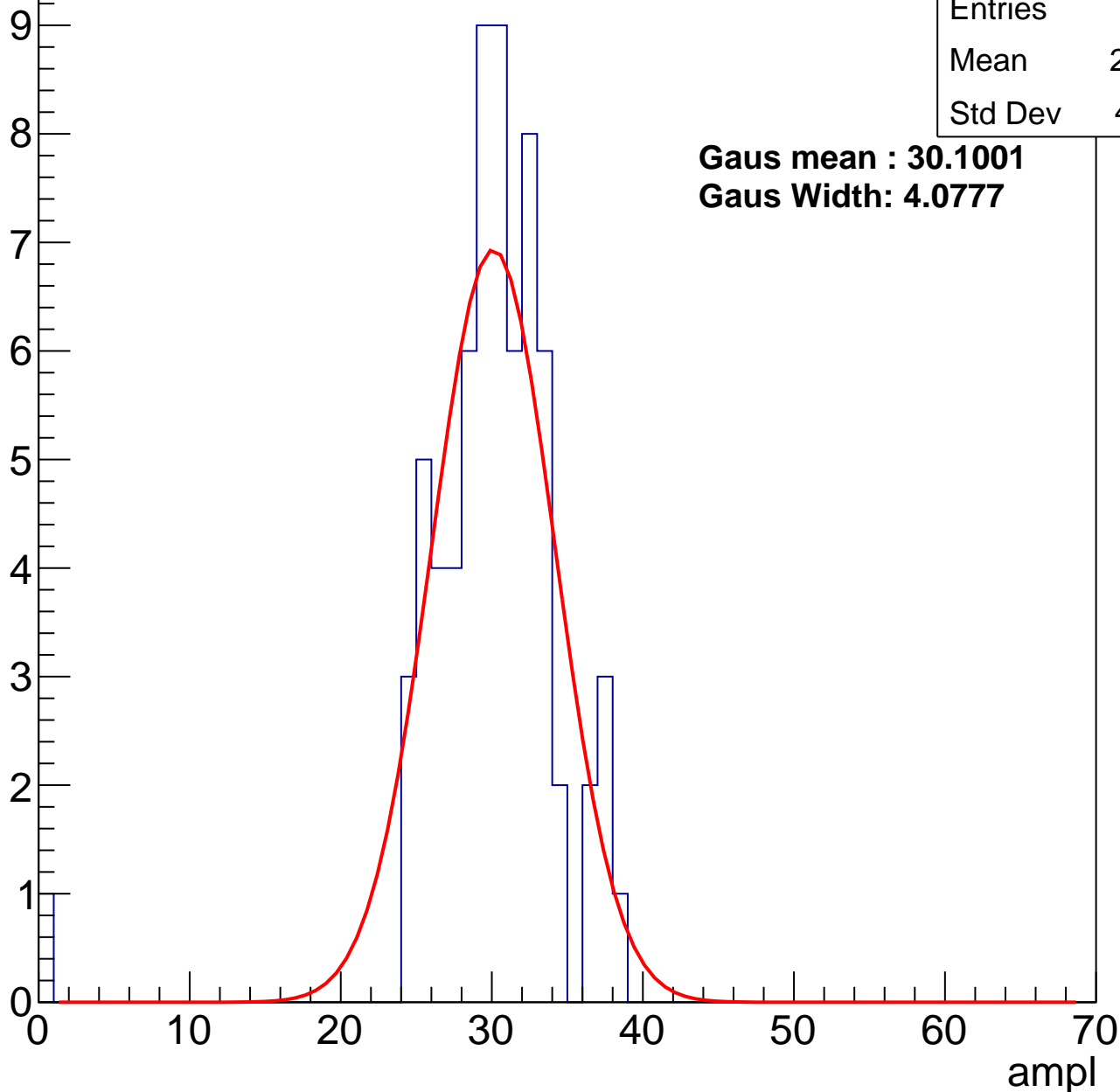
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.52
Std Dev	4.901

**Gaus mean : 30.1001**

**Gaus Width: 4.0777**



# B0L000S, U26-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	36.46
Std Dev	3.333

**Gaus mean : 37.1019**

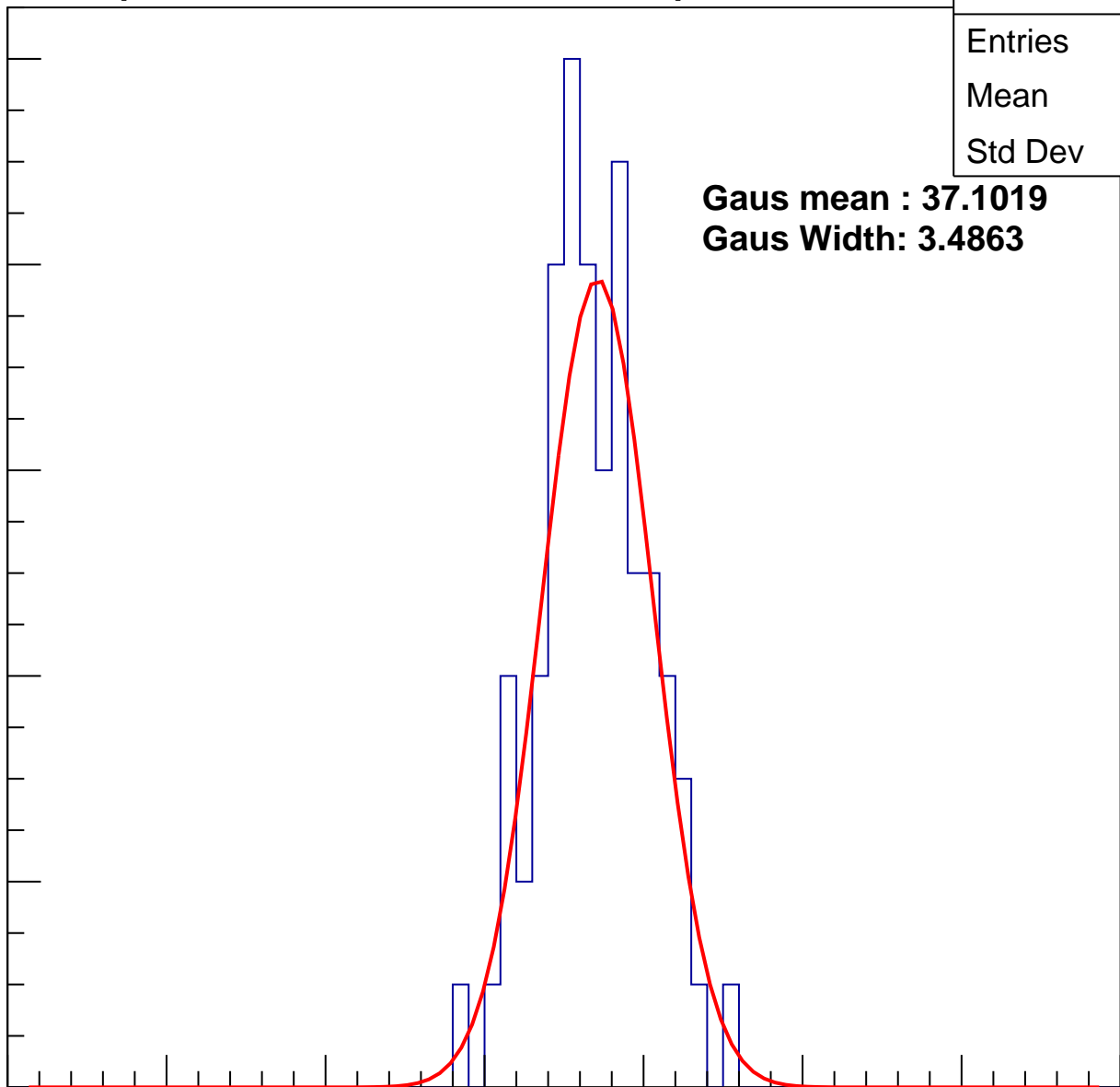
**Gaus Width: 3.4863**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch9, adc2

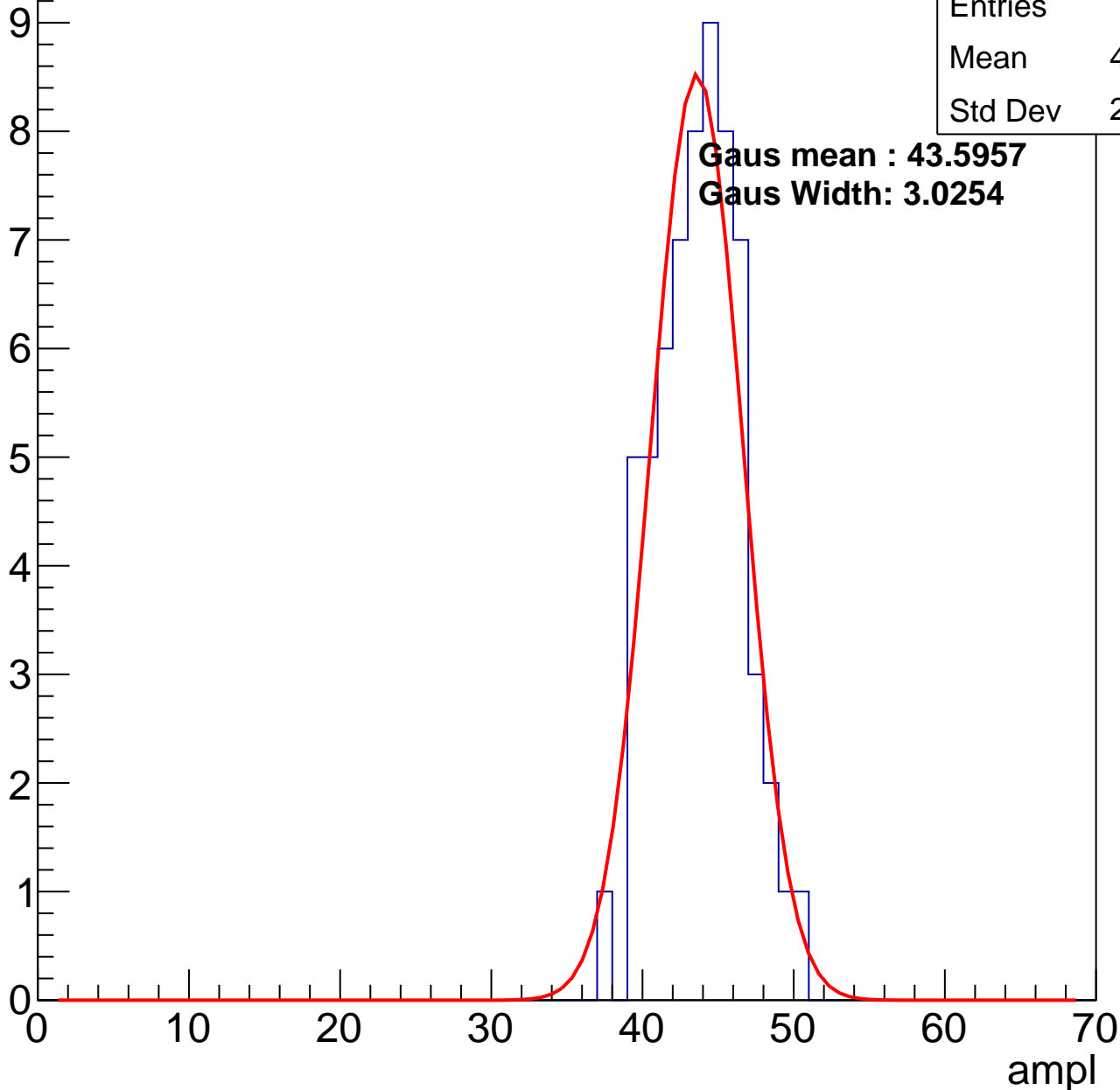
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	43.33
Std Dev	2.737

**Gaus mean : 43.5957**

**Gaus Width: 3.0254**

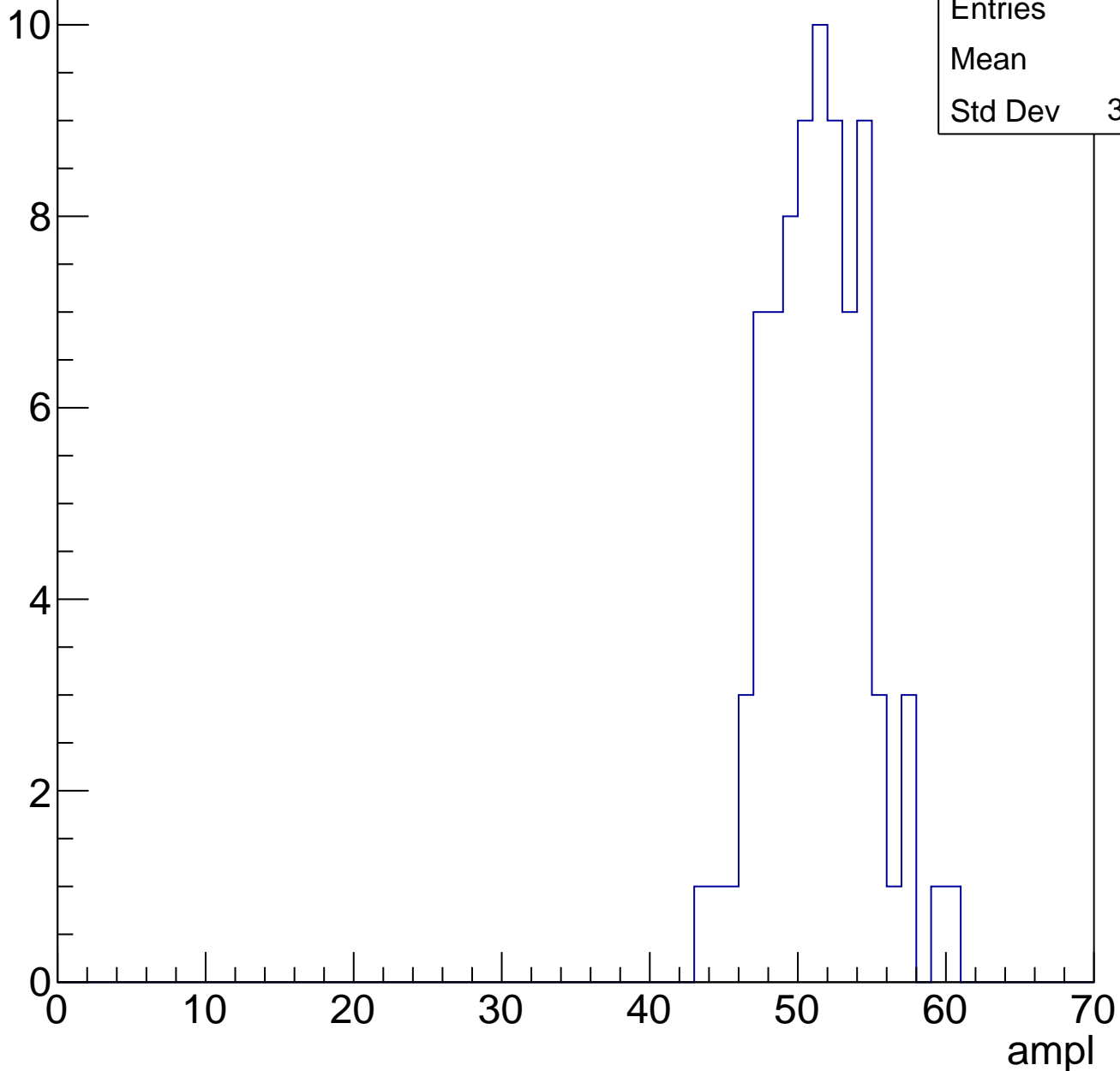


# B0L000S, U26-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	50.9
Std Dev	3.302

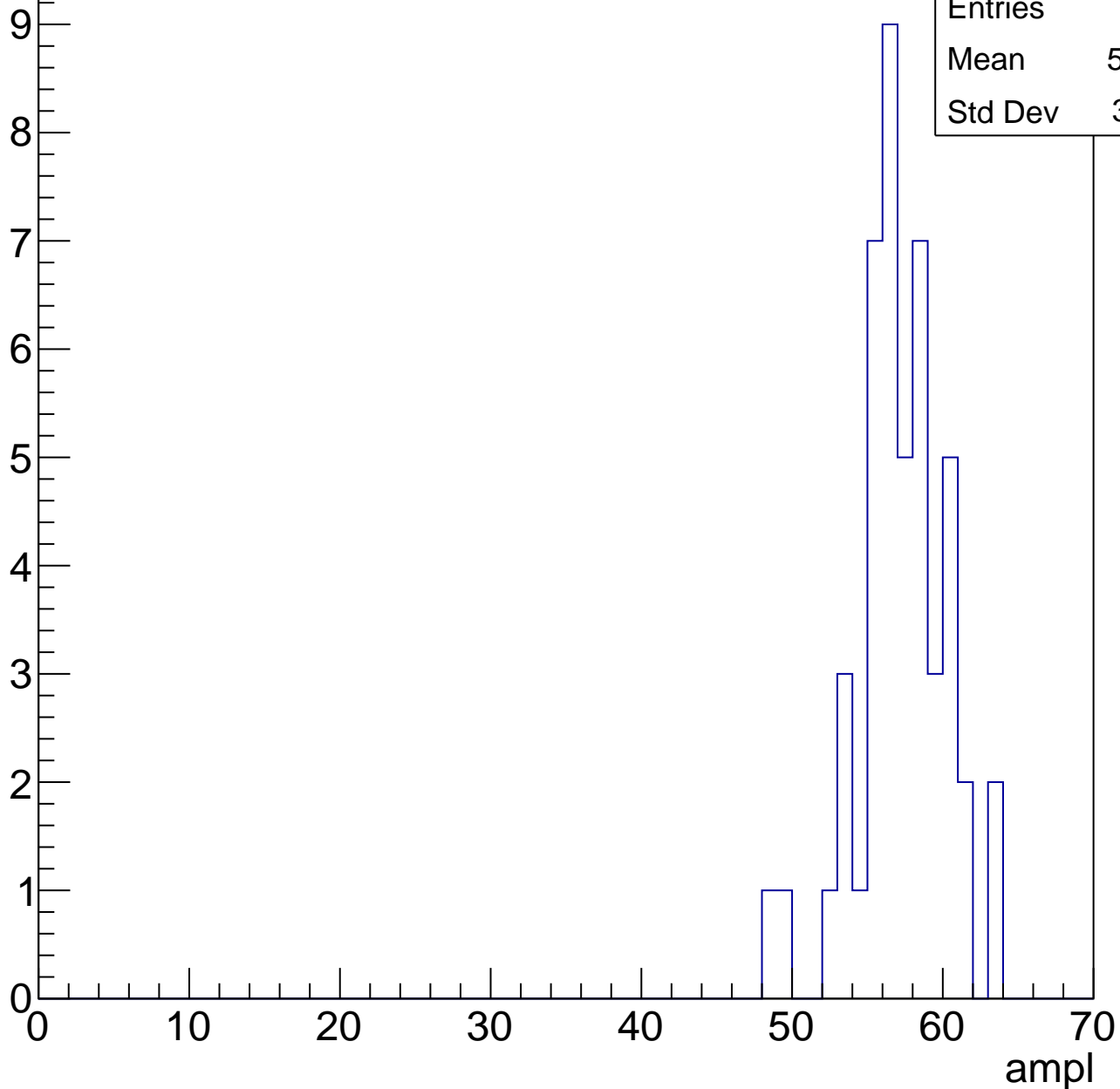
Entry



# B0L000S, U26-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

Entries 40

Mean 59.25

Std Dev 9.648

8

6

4

2

0

0

10

20

30

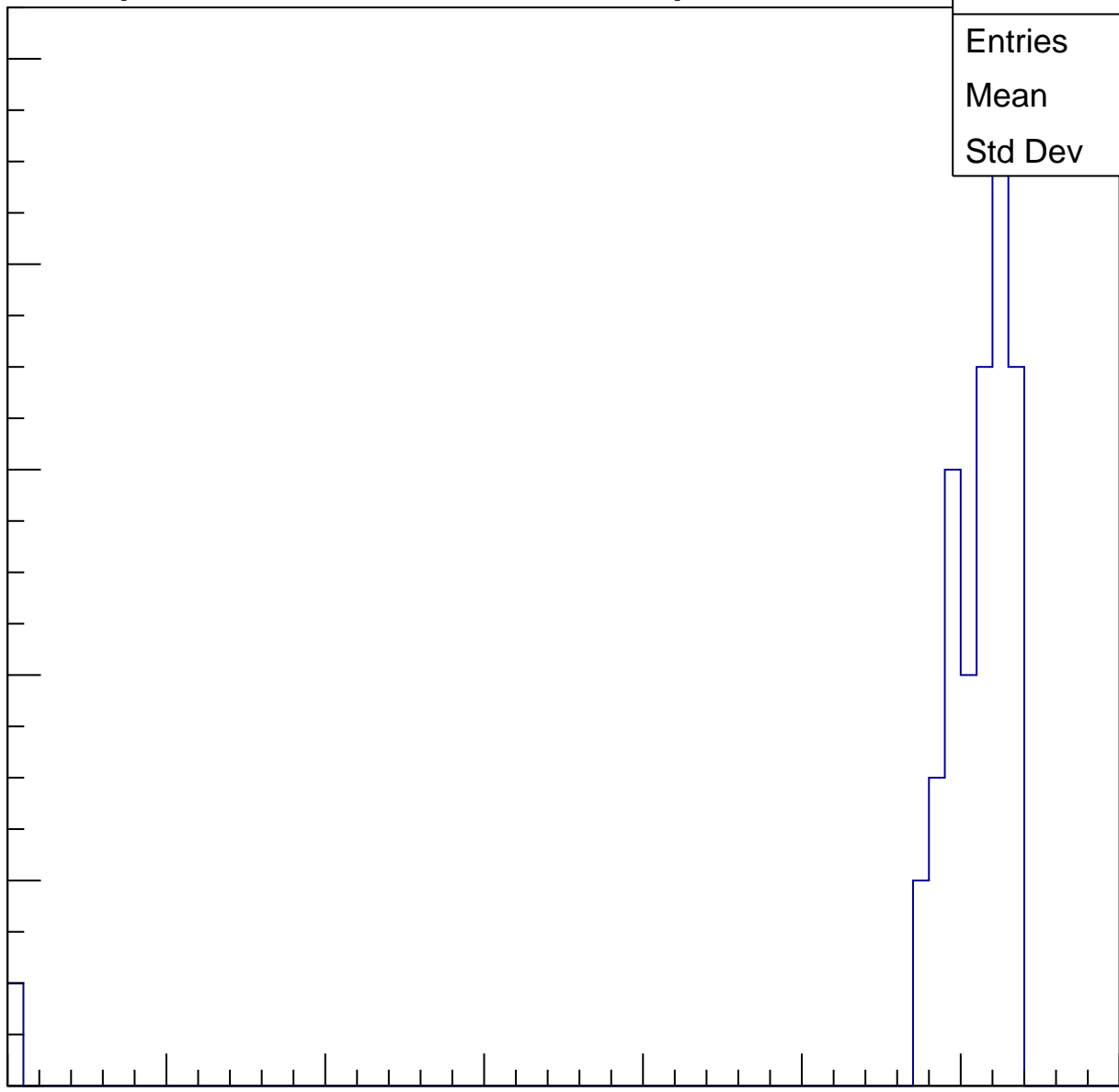
40

50

60

70

ampl



# B0L000S, U26-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L000S, U26-ch10, adc0

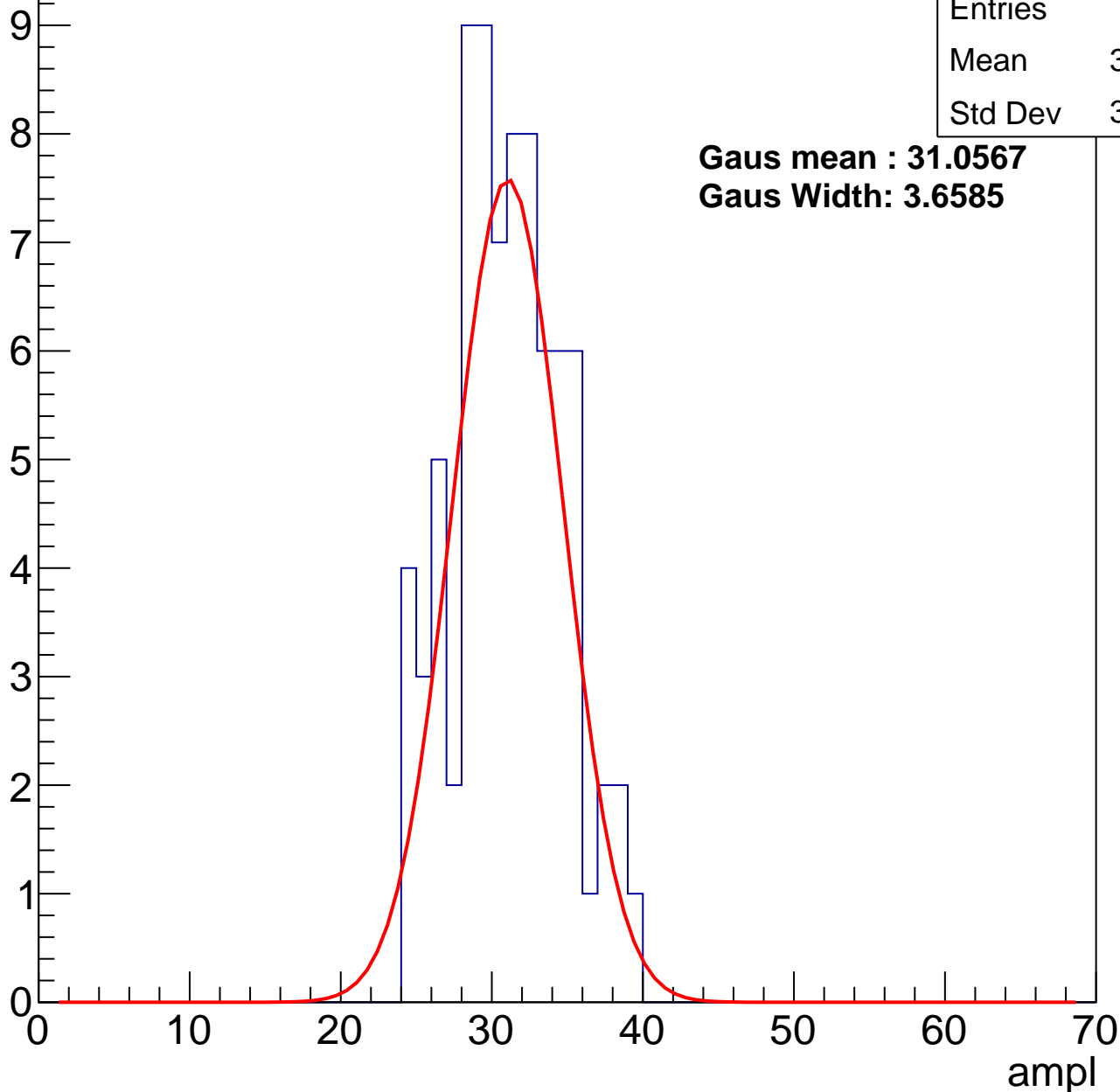
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	30.62
Std Dev	3.584

**Gaus mean : 31.0567**

**Gaus Width: 3.6585**



# B0L000S, U26-ch10, adc1

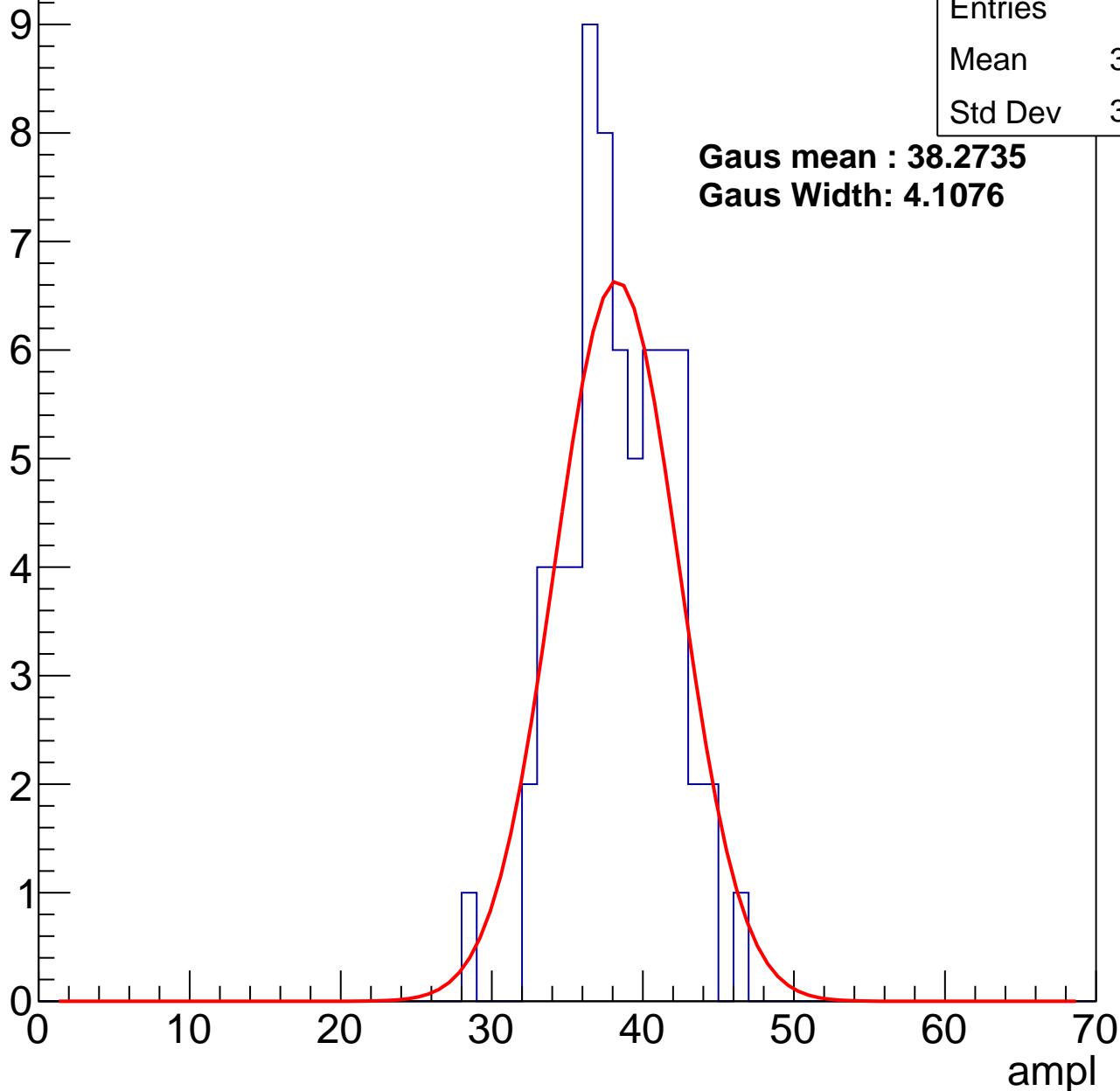
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.89
Std Dev	3.438

**Gaus mean : 38.2735**

**Gaus Width: 4.1076**



# B0L000S, U26-ch10, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

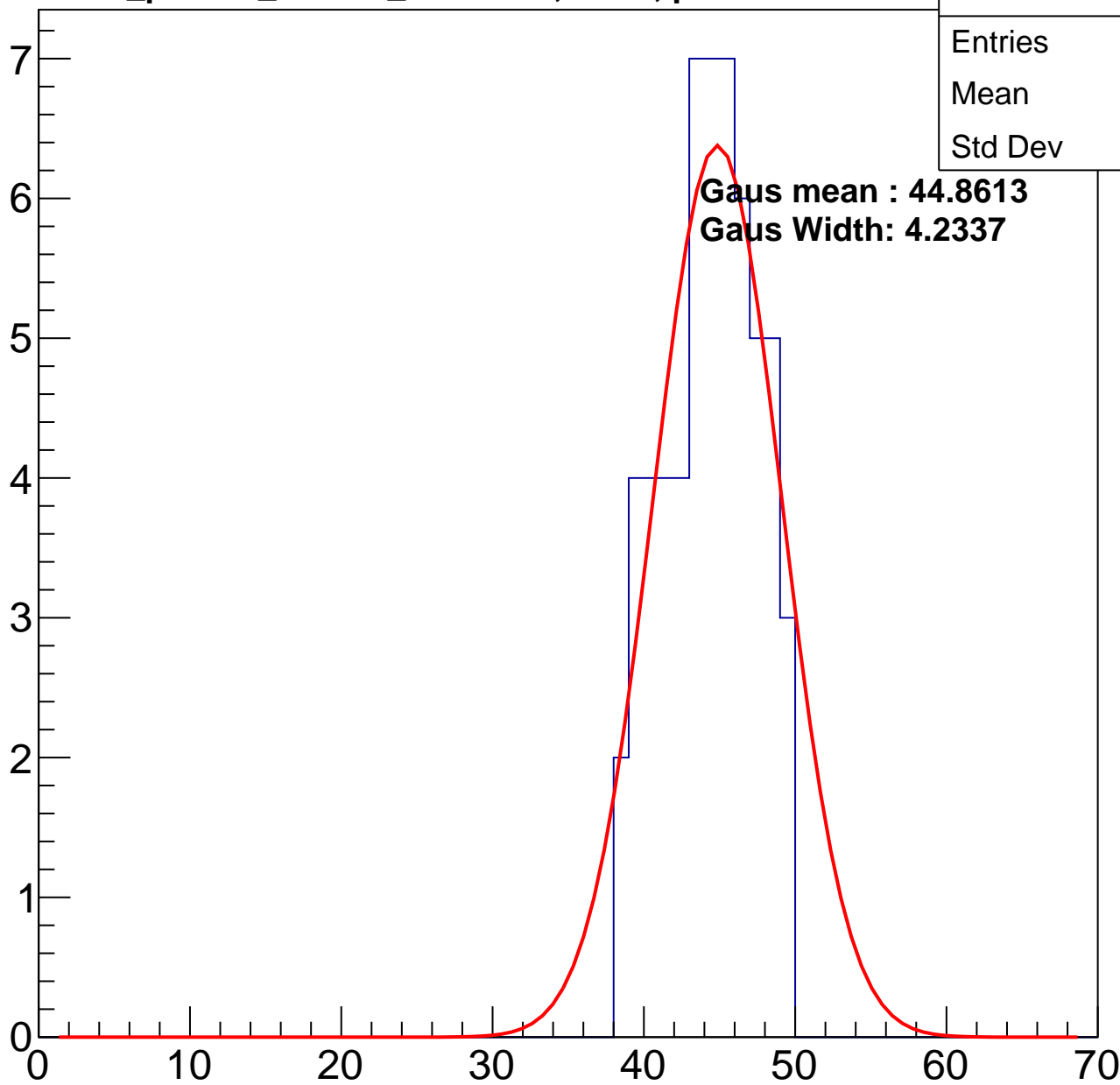
Entries	58
Mean	43.9
Std Dev	3.01

**Gaus mean : 44.8613**

**Gaus Width: 4.2337**

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

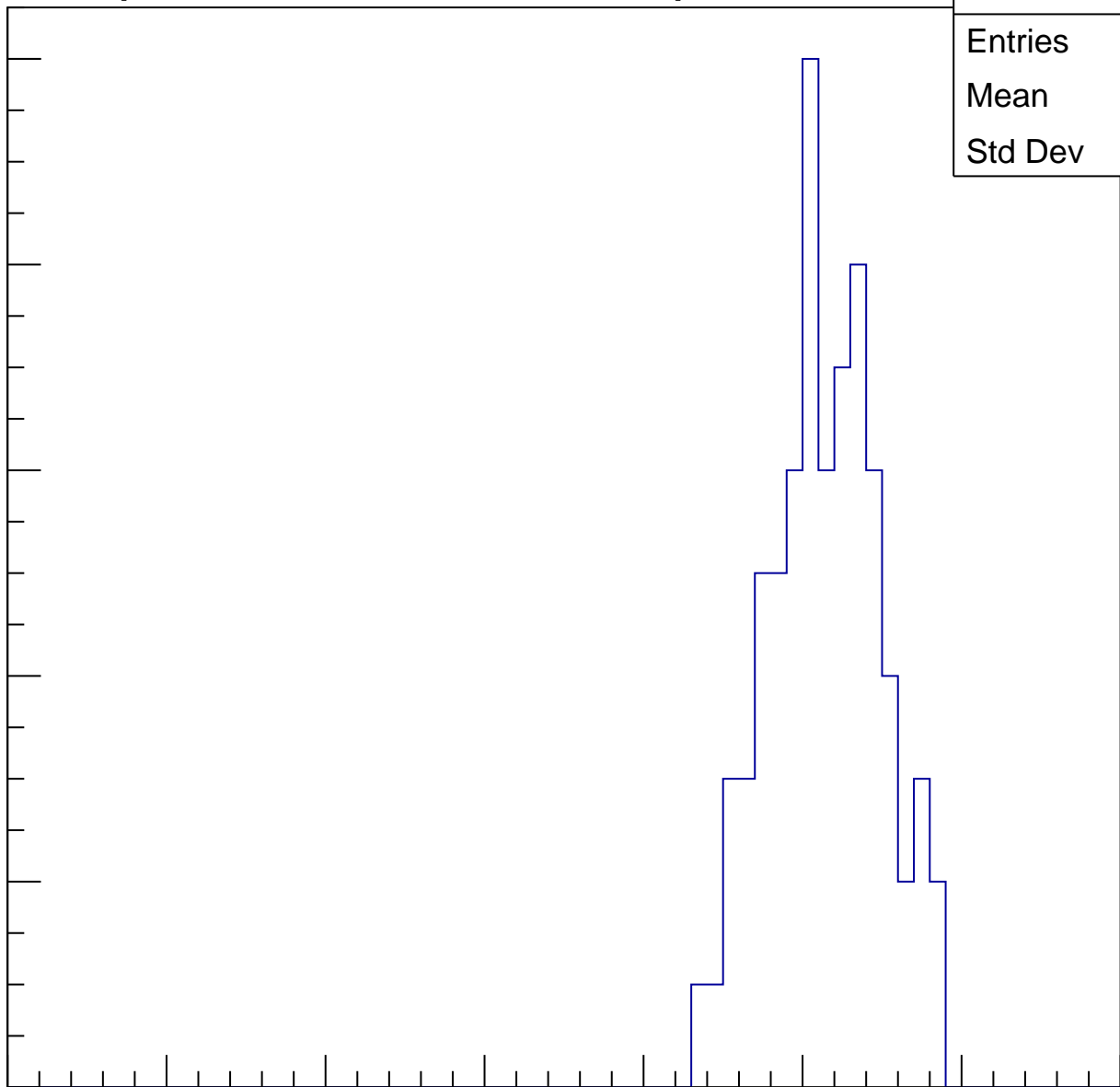
Entries	72
Mean	50.92
Std Dev	3.471

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

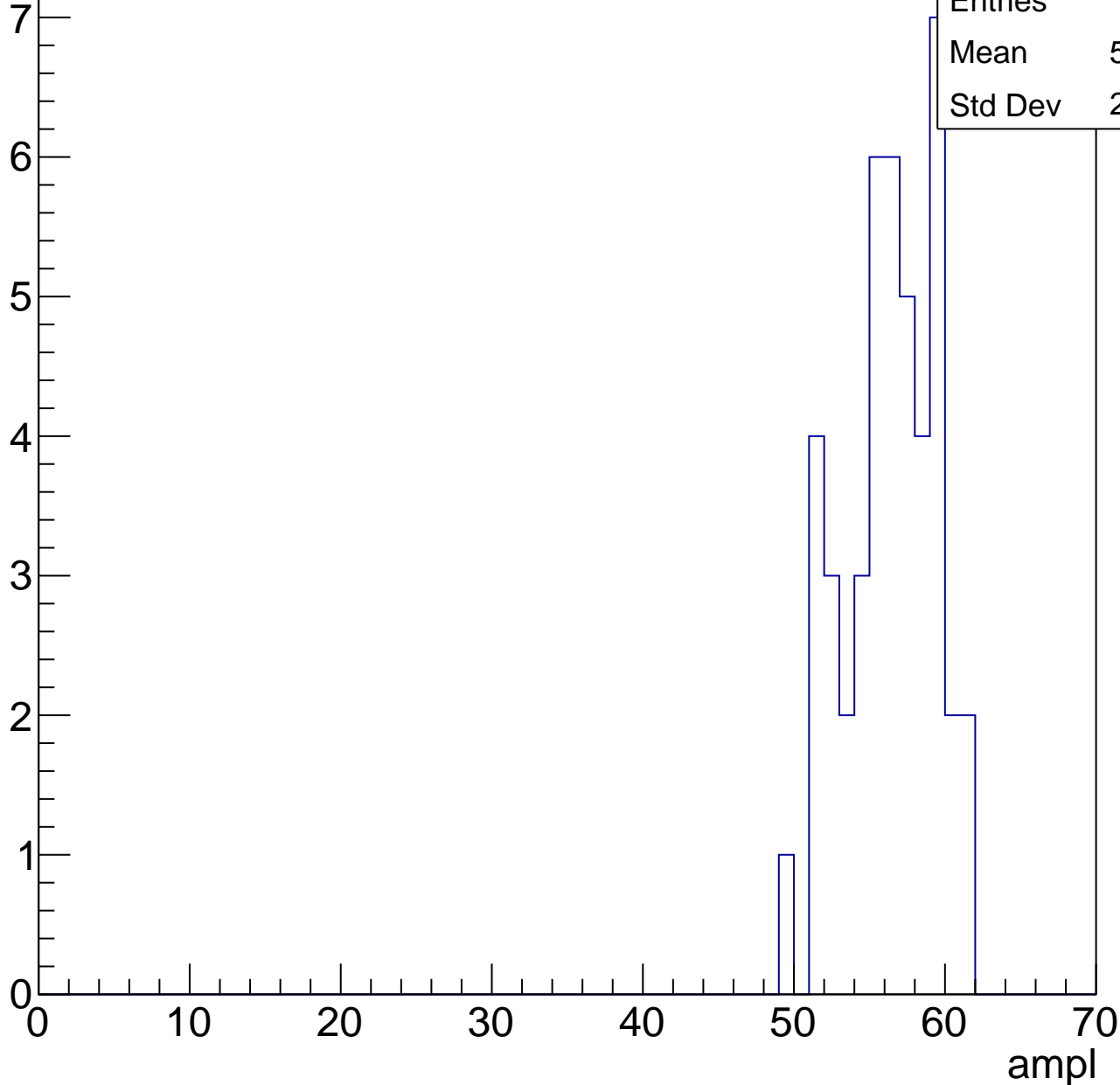


# B0L000S, U26-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	55.89
Std Dev	2.976

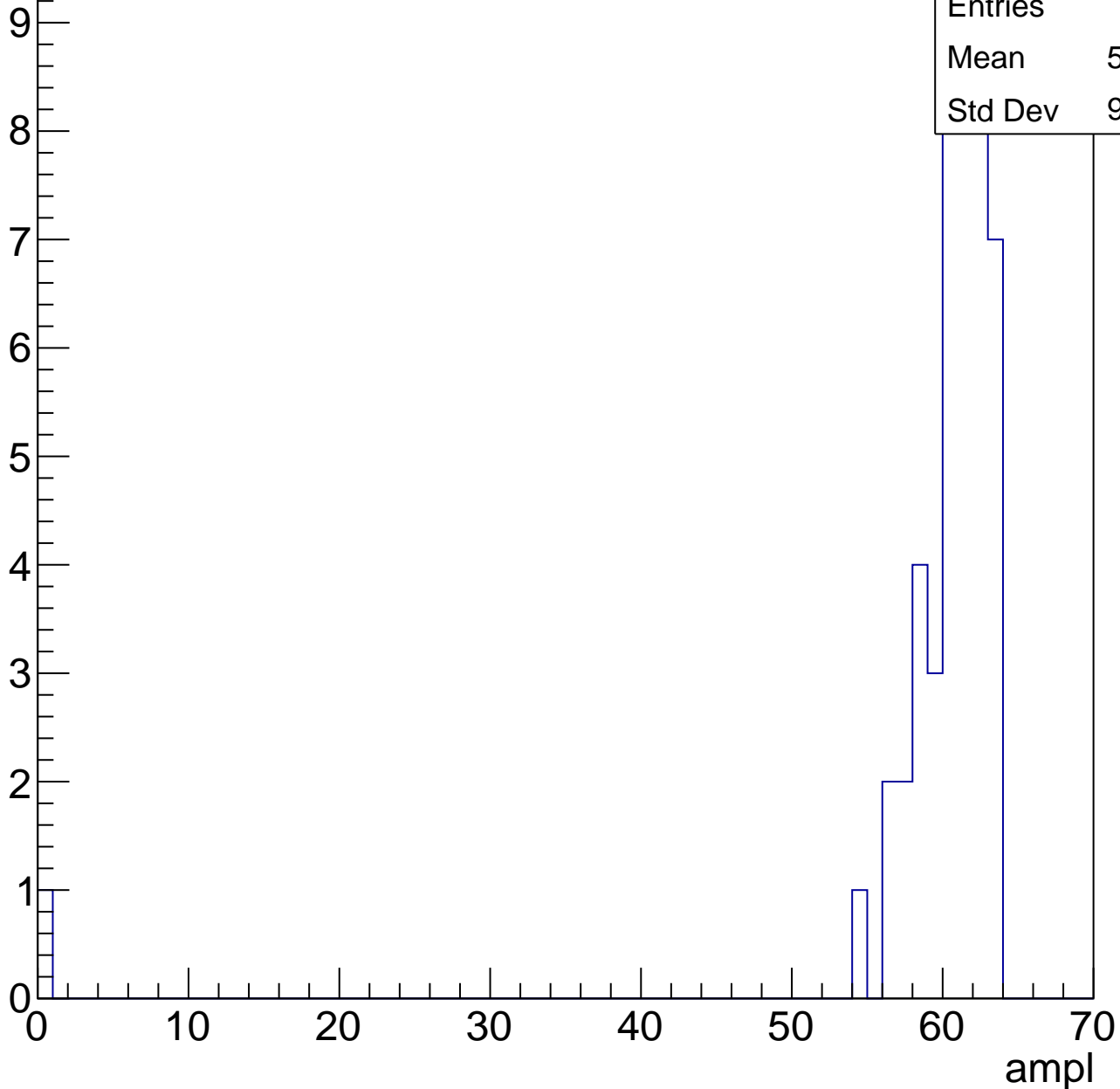


# B0L000S, U26-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	59.02
Std Dev	9.152



# B0L000S, U26-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch11, adc0

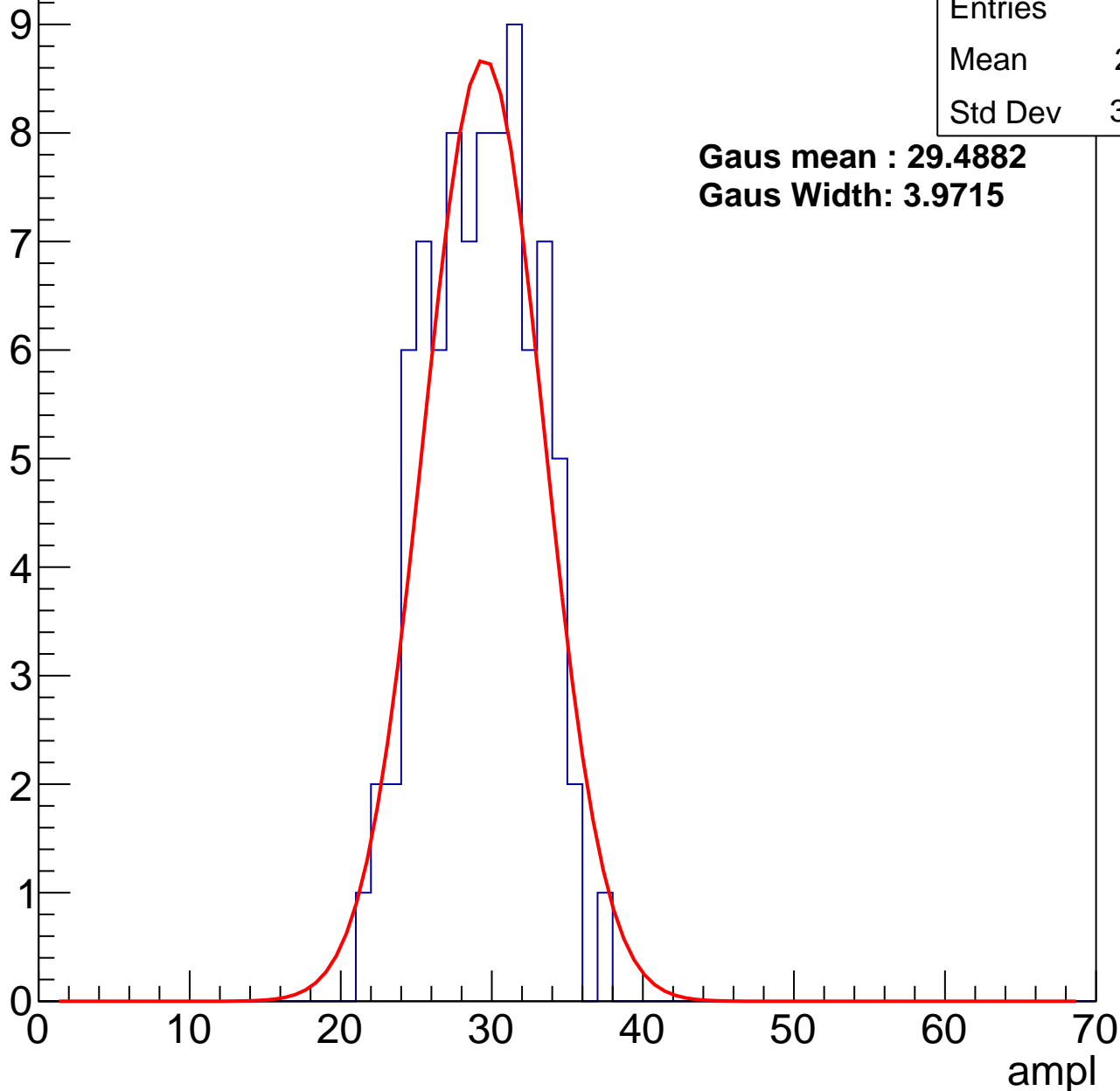
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	28.81
Std Dev	3.526

**Gaus mean : 29.4882**

**Gaus Width: 3.9715**



# B0L000S, U26-ch11, adc1

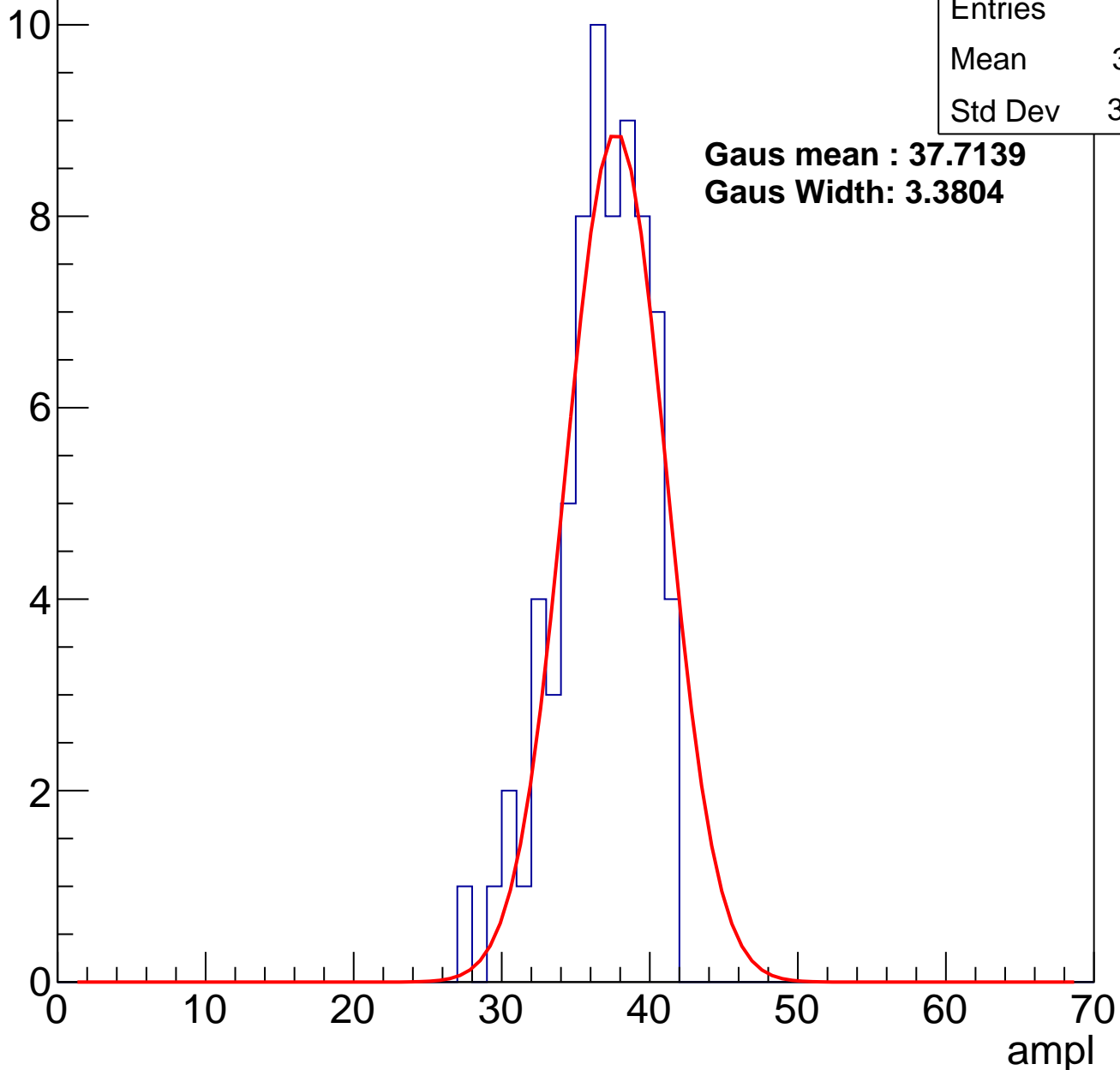
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	36.31
Std Dev	3.065

**Gaus mean : 37.7139**

**Gaus Width: 3.3804**

Entry



# B0L000S, U26-ch11, adc2

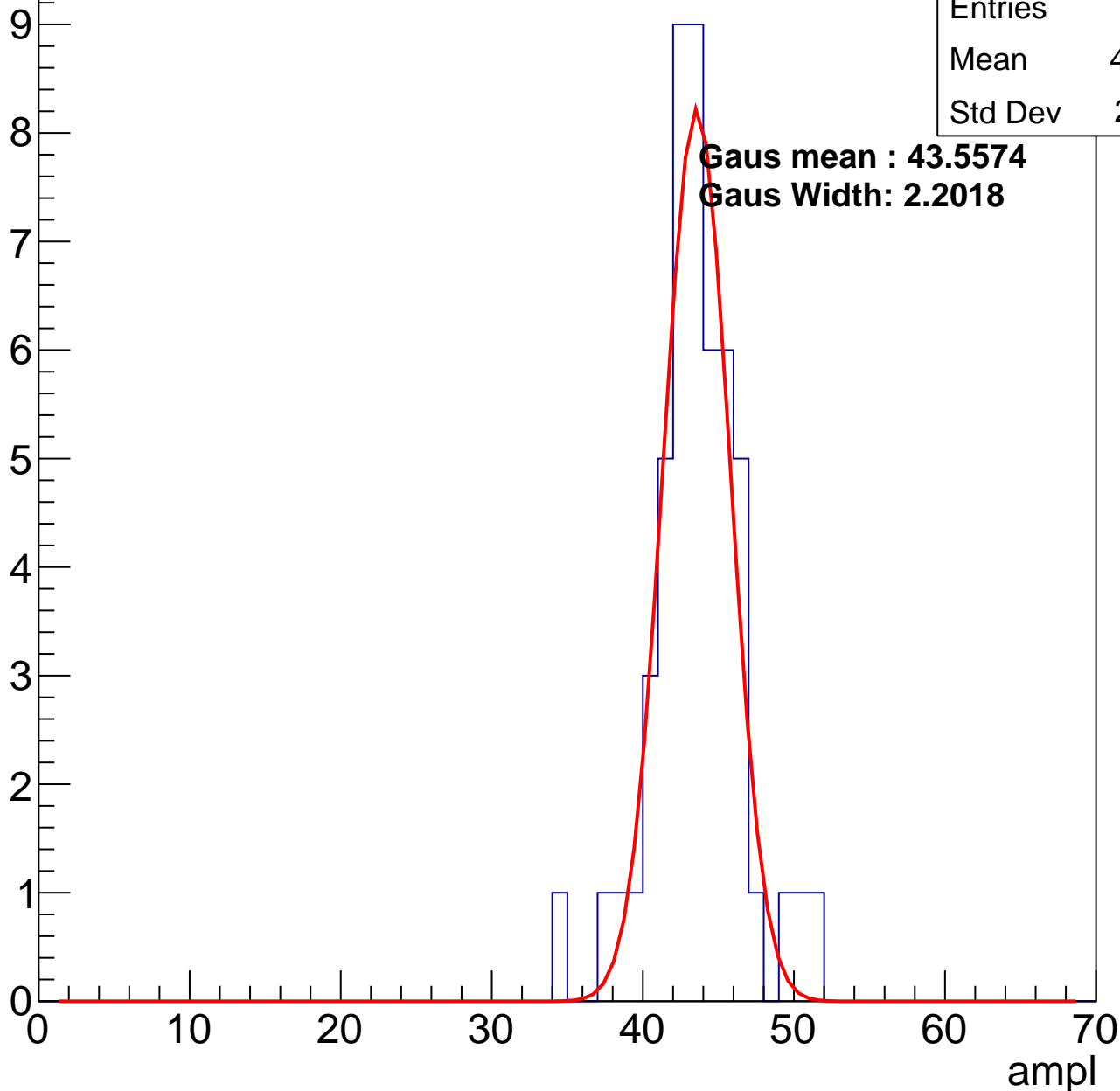
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	43.12
Std Dev	2.981

**Gaus mean : 43.5574**

**Gaus Width: 2.2018**

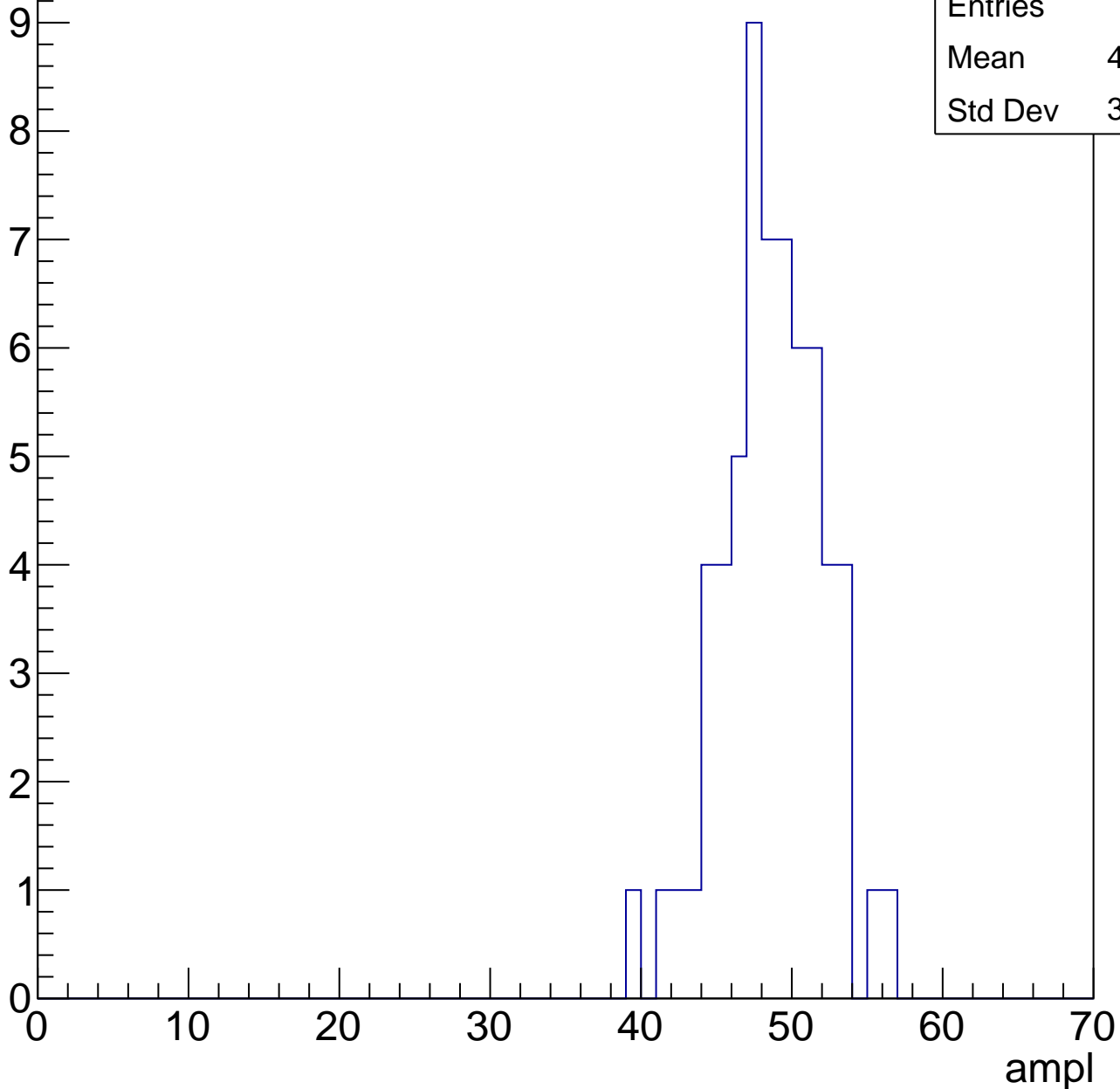


# B0L000S, U26-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	48.23
Std Dev	3.309

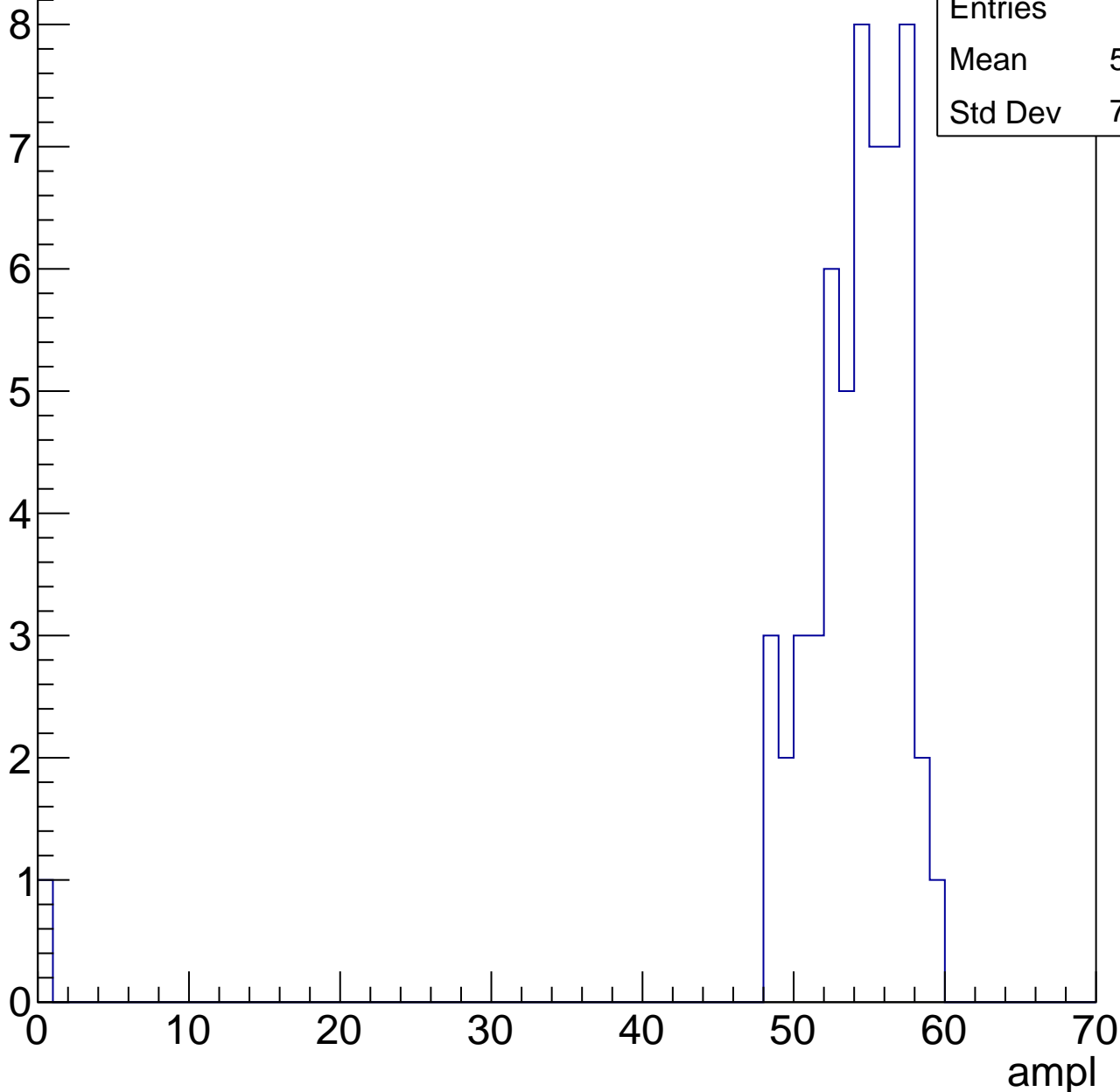


# B0L000S, U26-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	52.89
Std Dev	7.645



# B0L000S, U26-ch11, adc5

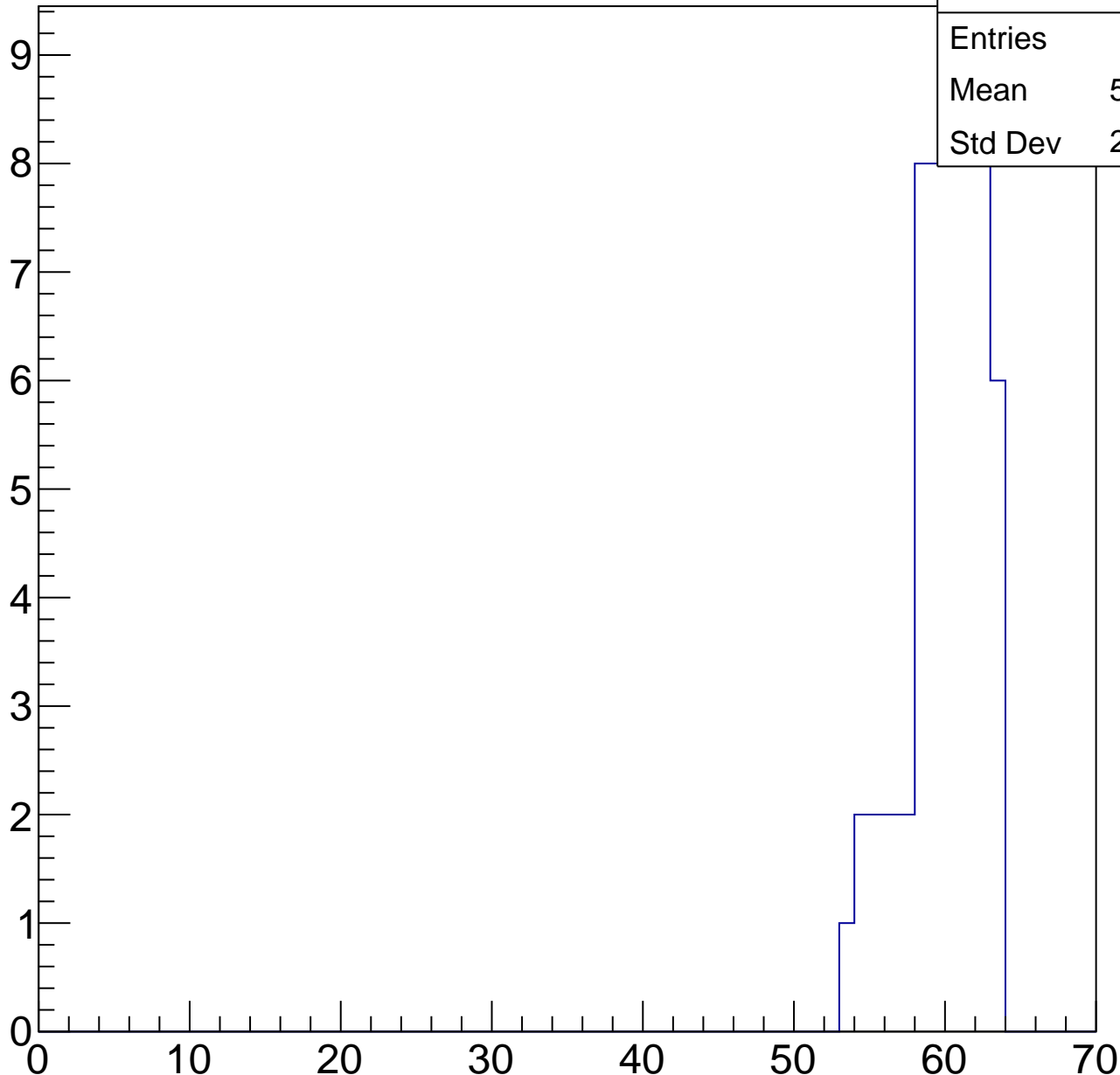
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	59.62
Std Dev	2.462

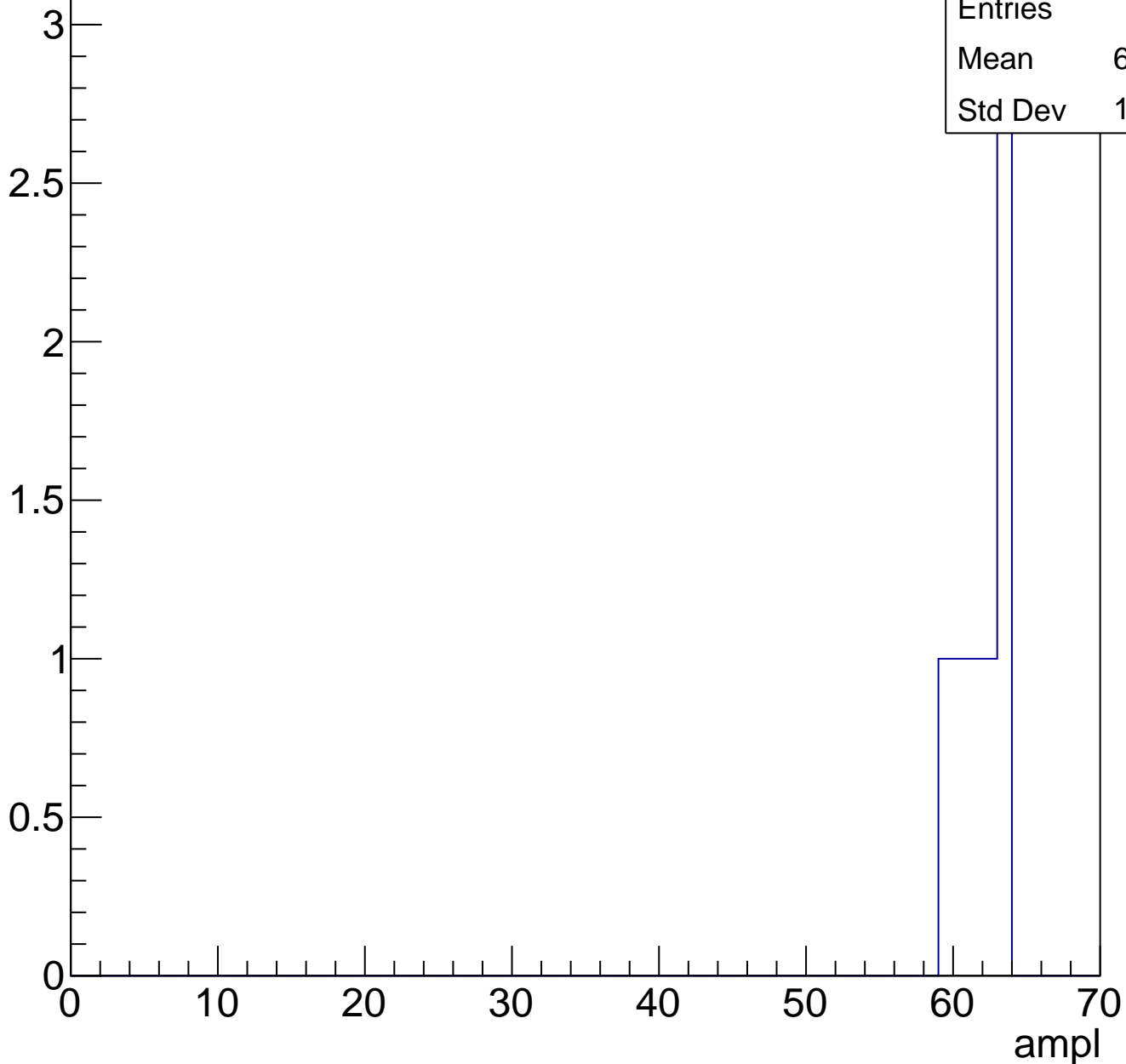
ampl



# B0L000S, U26-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

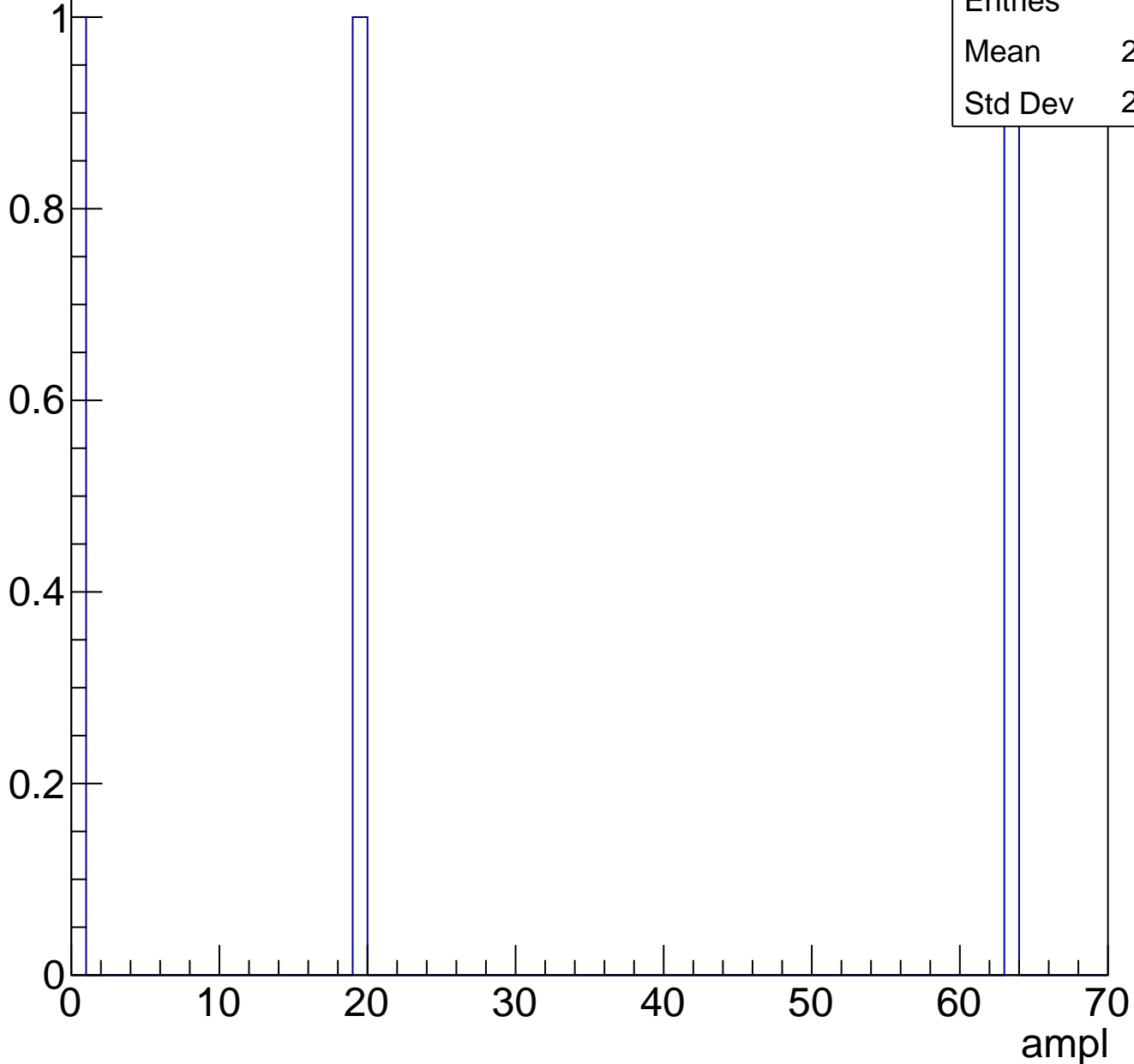




# B0L000S, U26-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	27.33
Std Dev	26.39

# B0L000S, U26-ch12, adc0

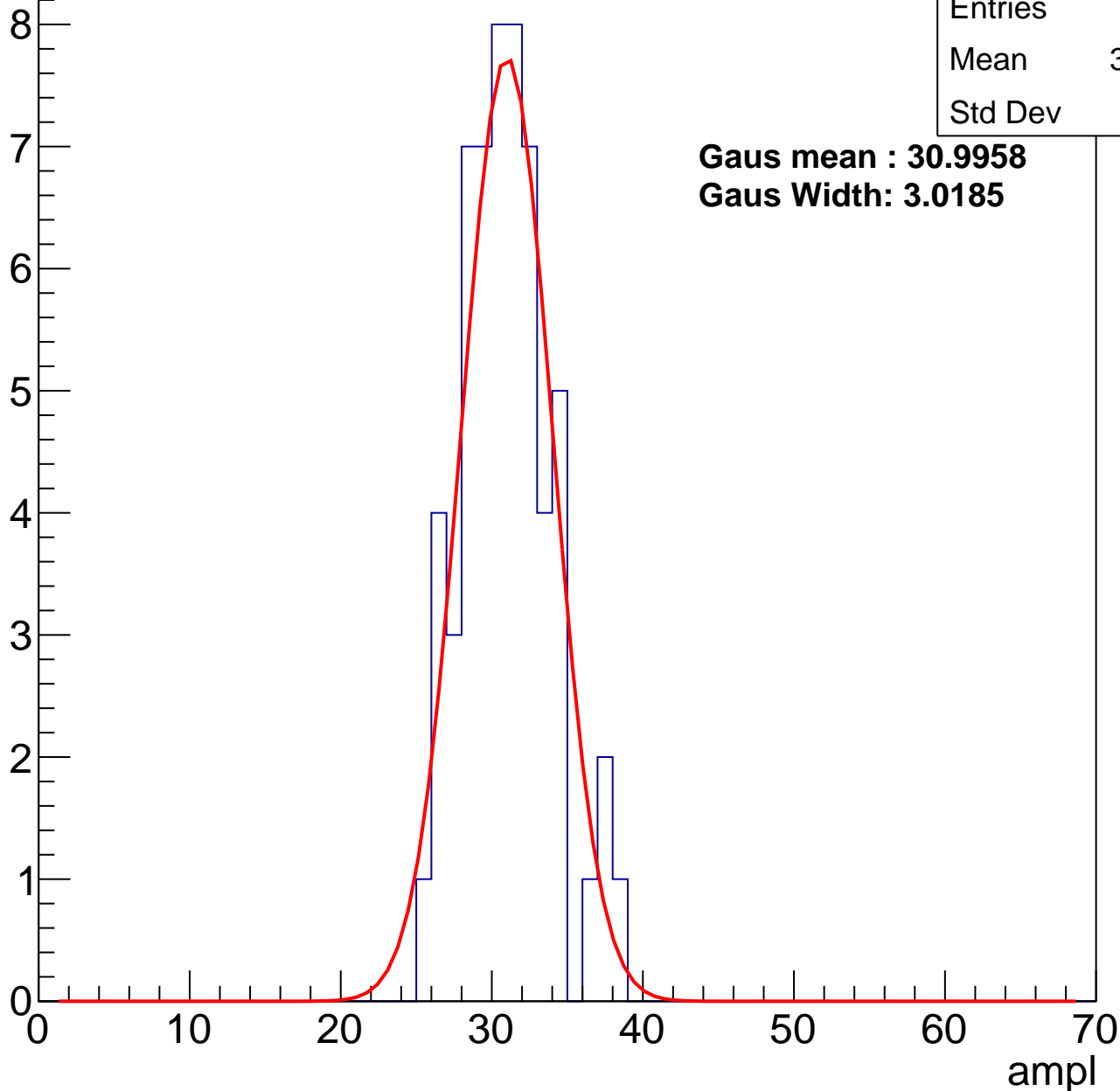
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	30.53
Std Dev	2.89

**Gaus mean : 30.9958**

**Gaus Width: 3.0185**



# B0L000S, U26-ch12, adc1

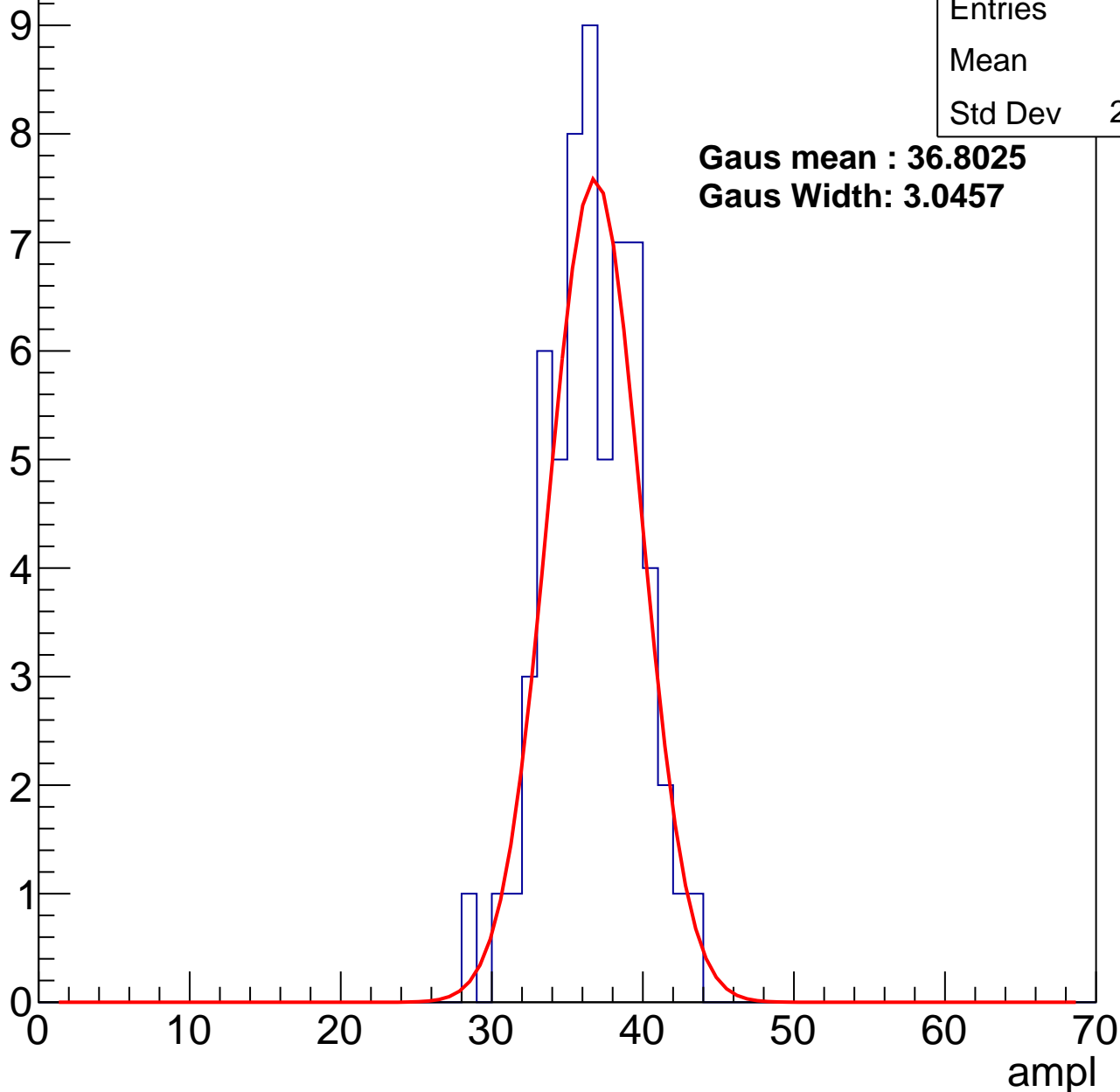
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.2
Std Dev	2.996

**Gaus mean : 36.8025**

**Gaus Width: 3.0457**



# B0L000S, U26-ch12, adc2

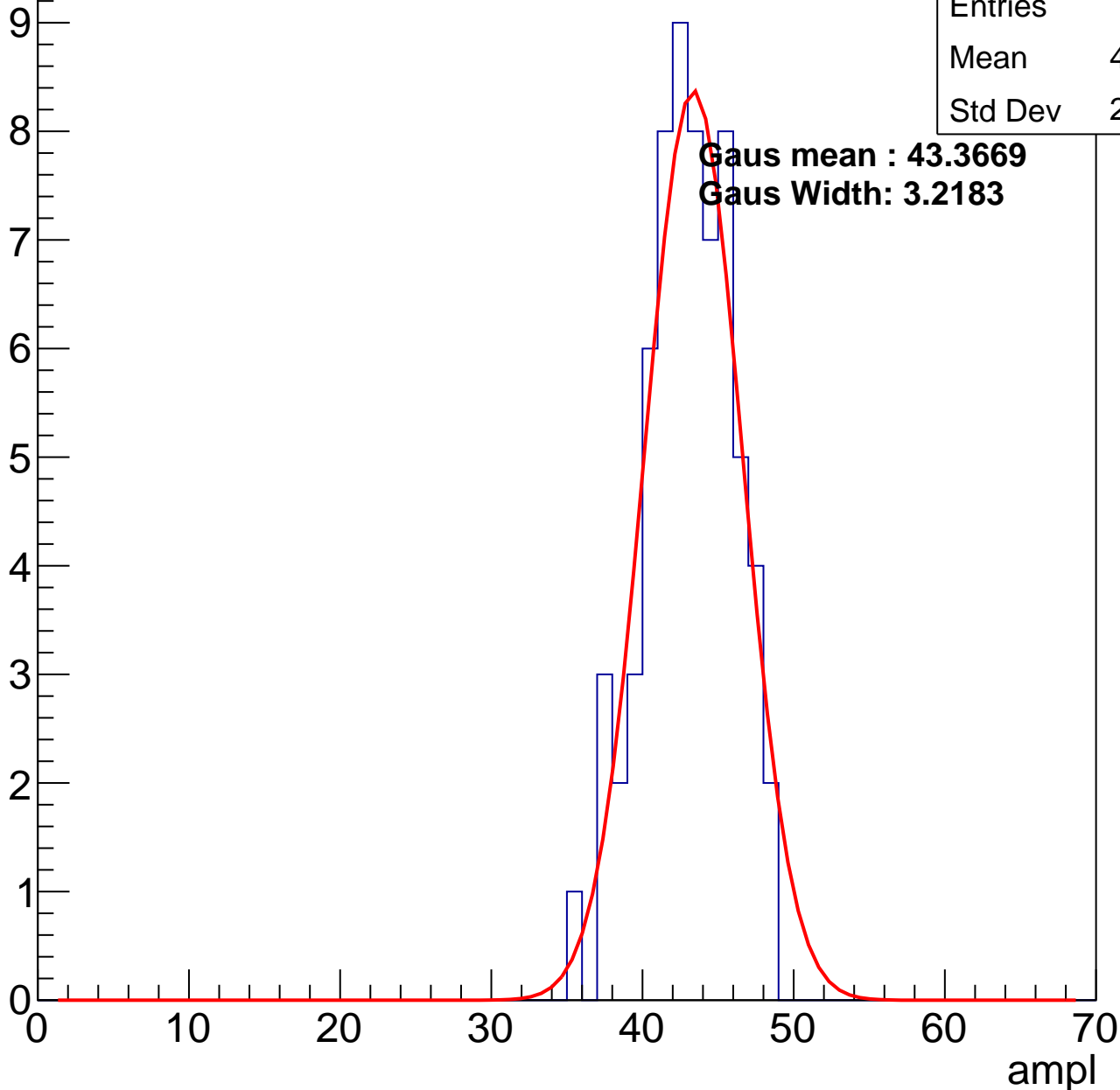
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	42.59
Std Dev	2.892

**Gaus mean : 43.3669**

**Gaus Width: 3.2183**

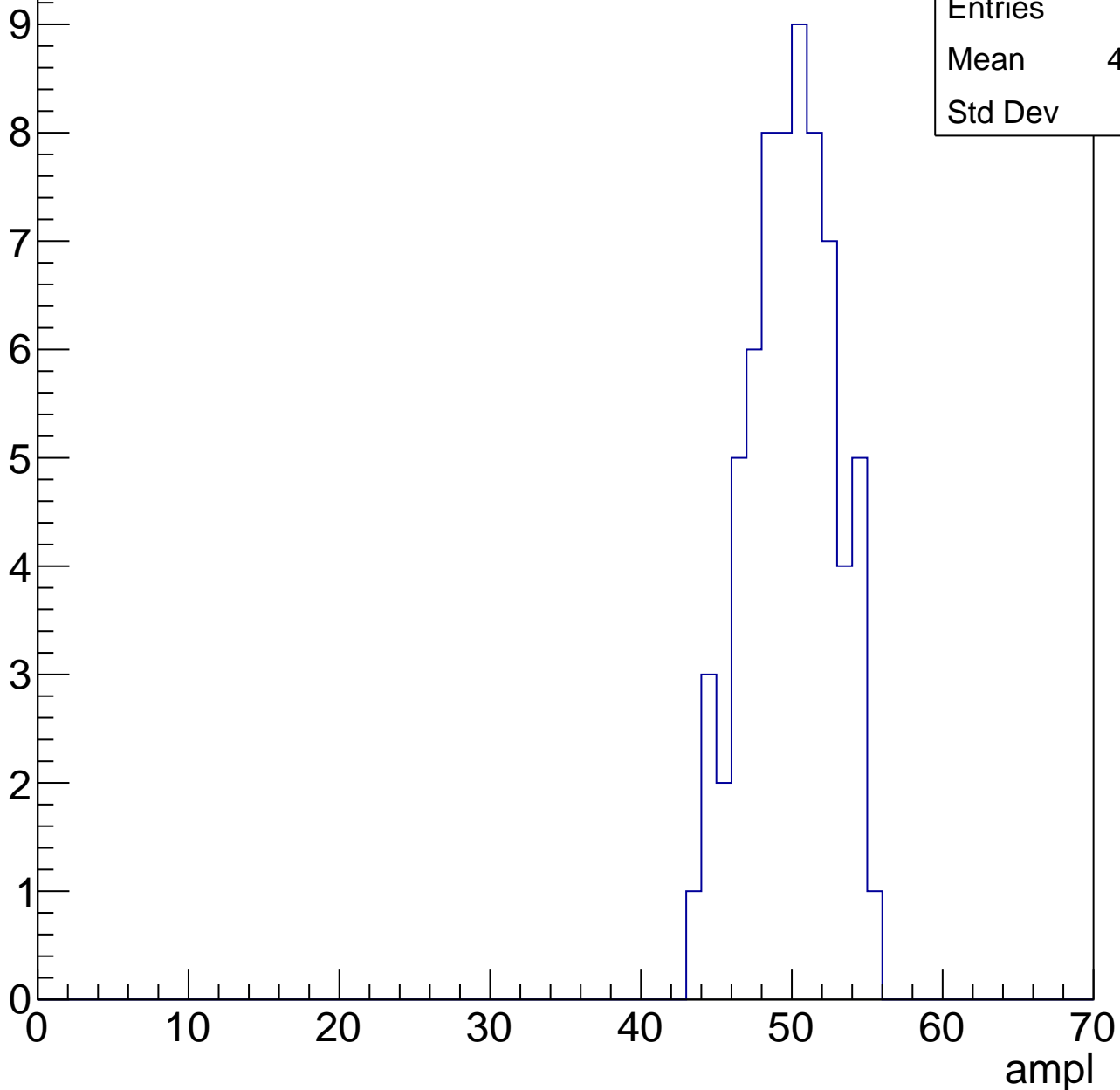


# B0L000S, U26-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

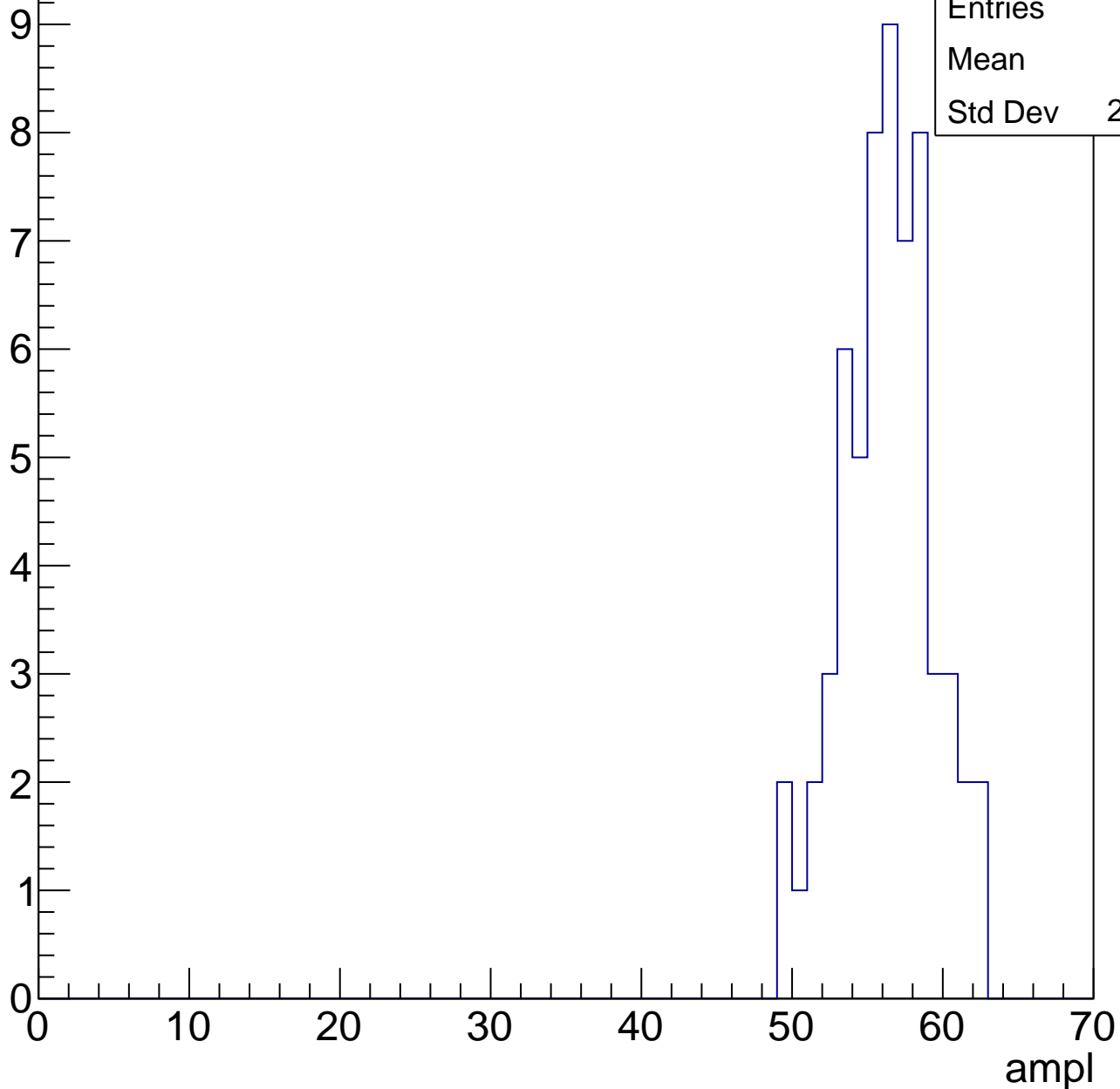
Entries	67
Mean	49.43
Std Dev	2.83



# B0L000S, U26-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

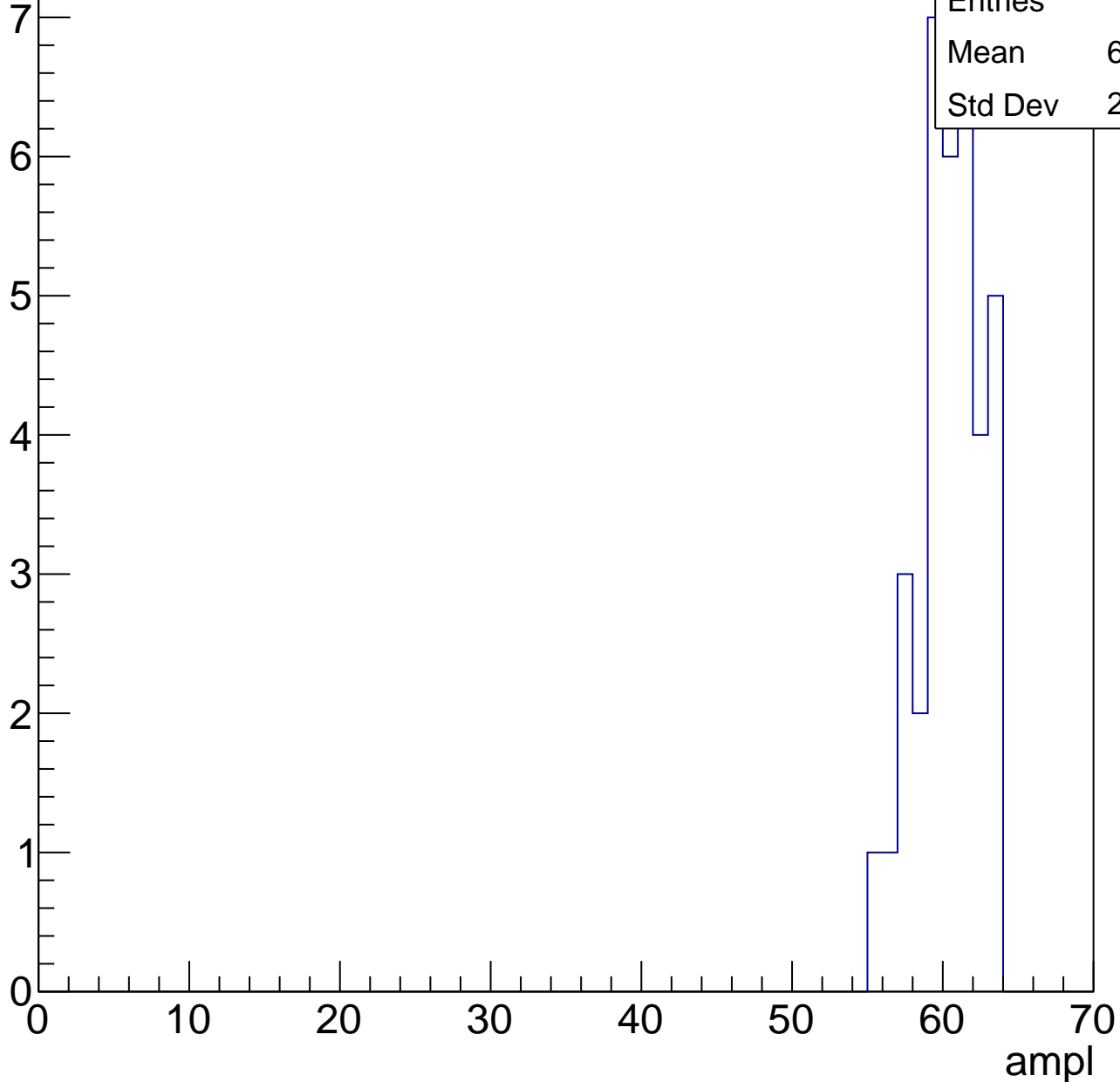
Entry



# B0L000S, U26-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

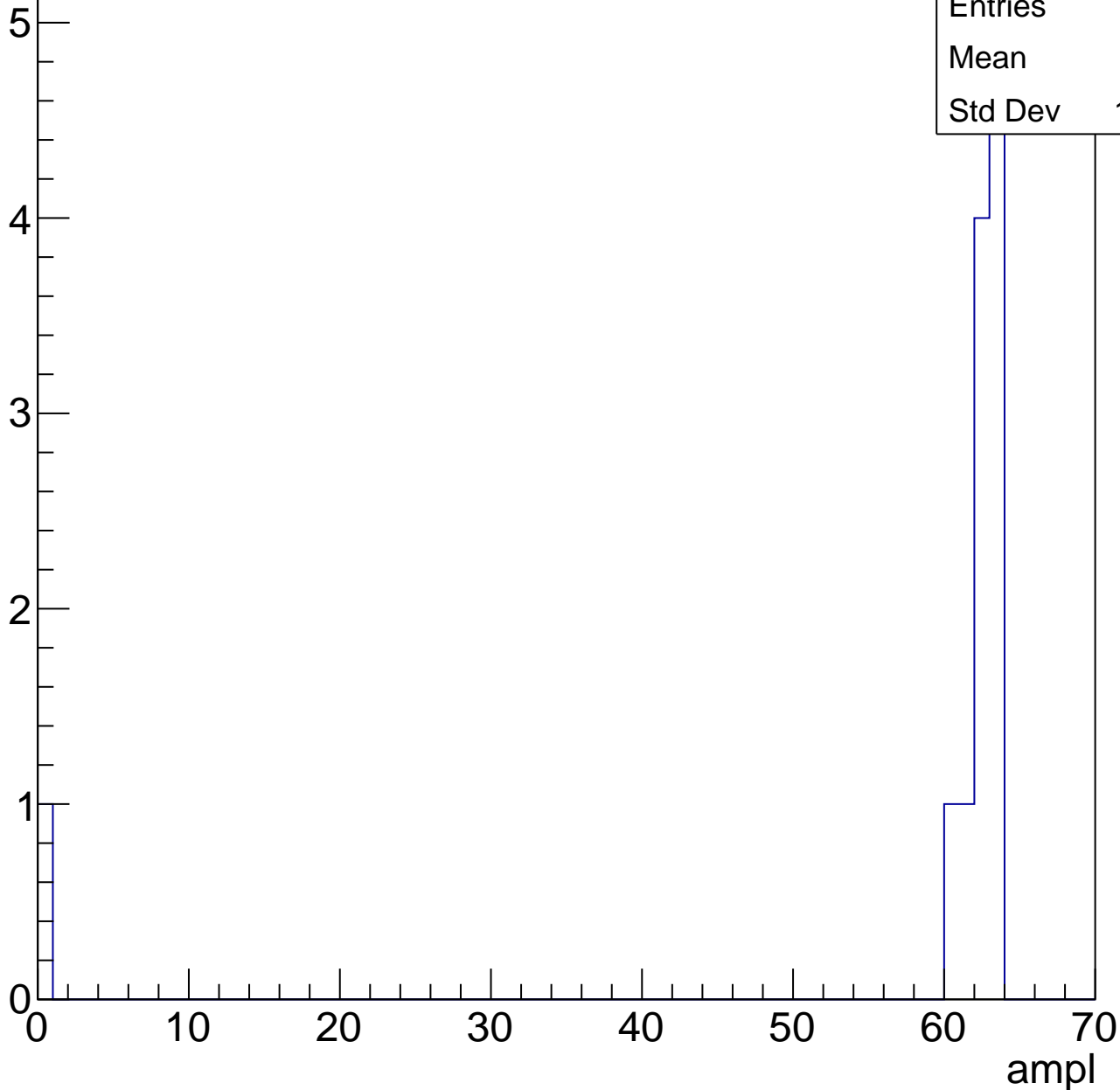


# B0L000S, U26-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	57
Std Dev	17.21





# B0L000S, U26-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch13, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	28.44
Std Dev	3.601

**Gaus mean : 29.1998**

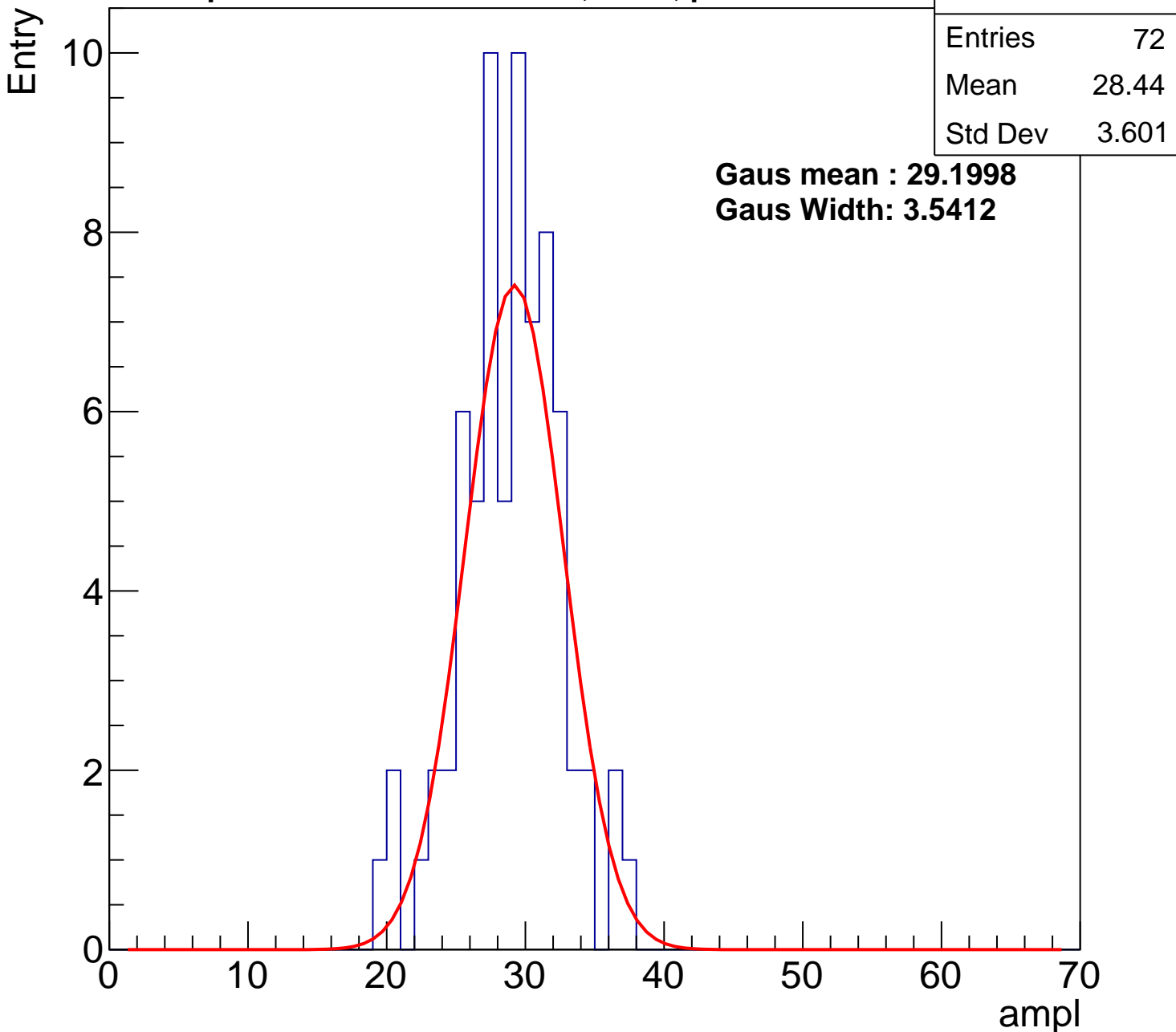
**Gaus Width: 3.5412**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch13, adc1

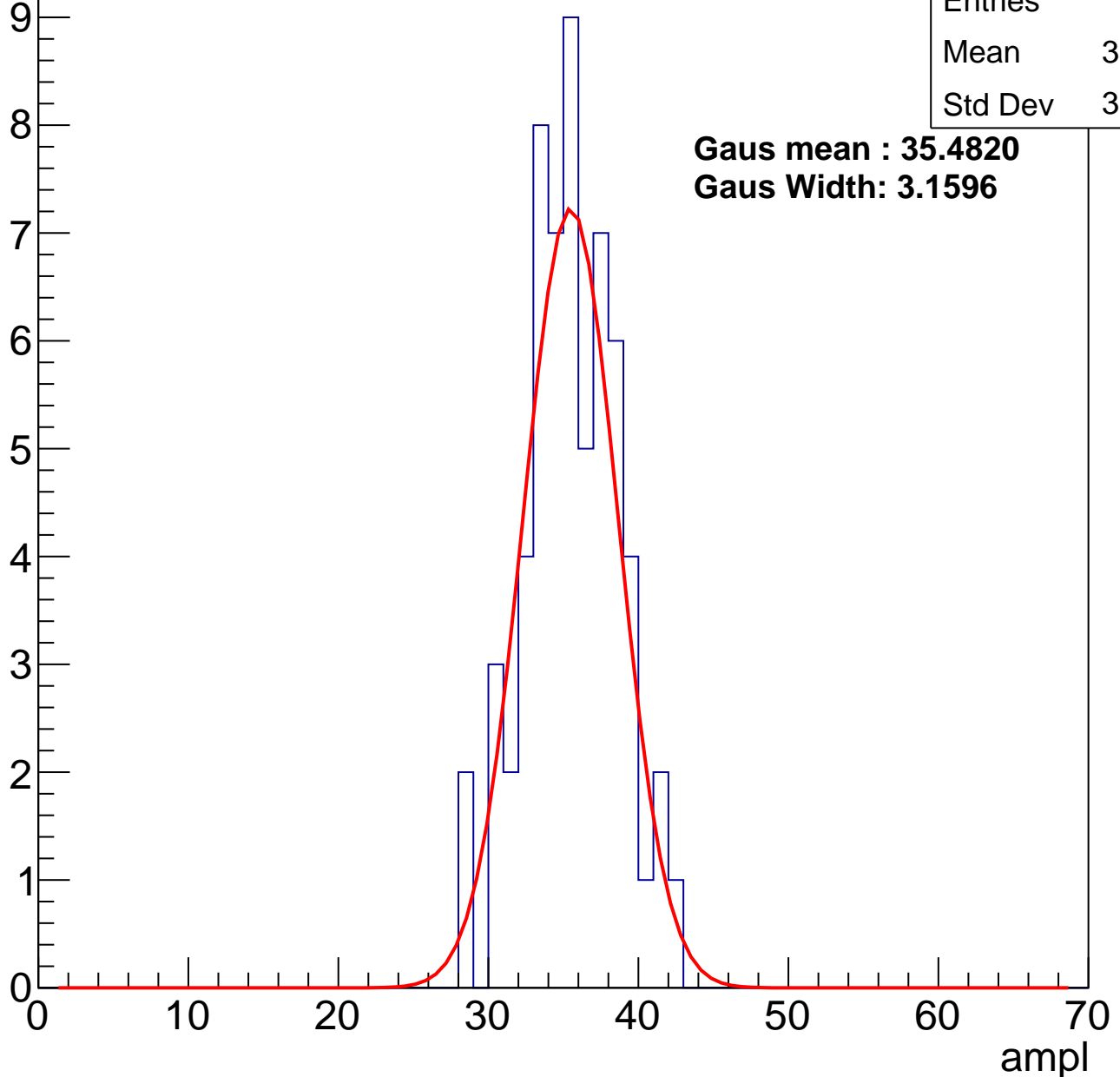
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.08
Std Dev	3.074

**Gaus mean : 35.4820**

**Gaus Width: 3.1596**



# B0L000S, U26-ch13, adc2

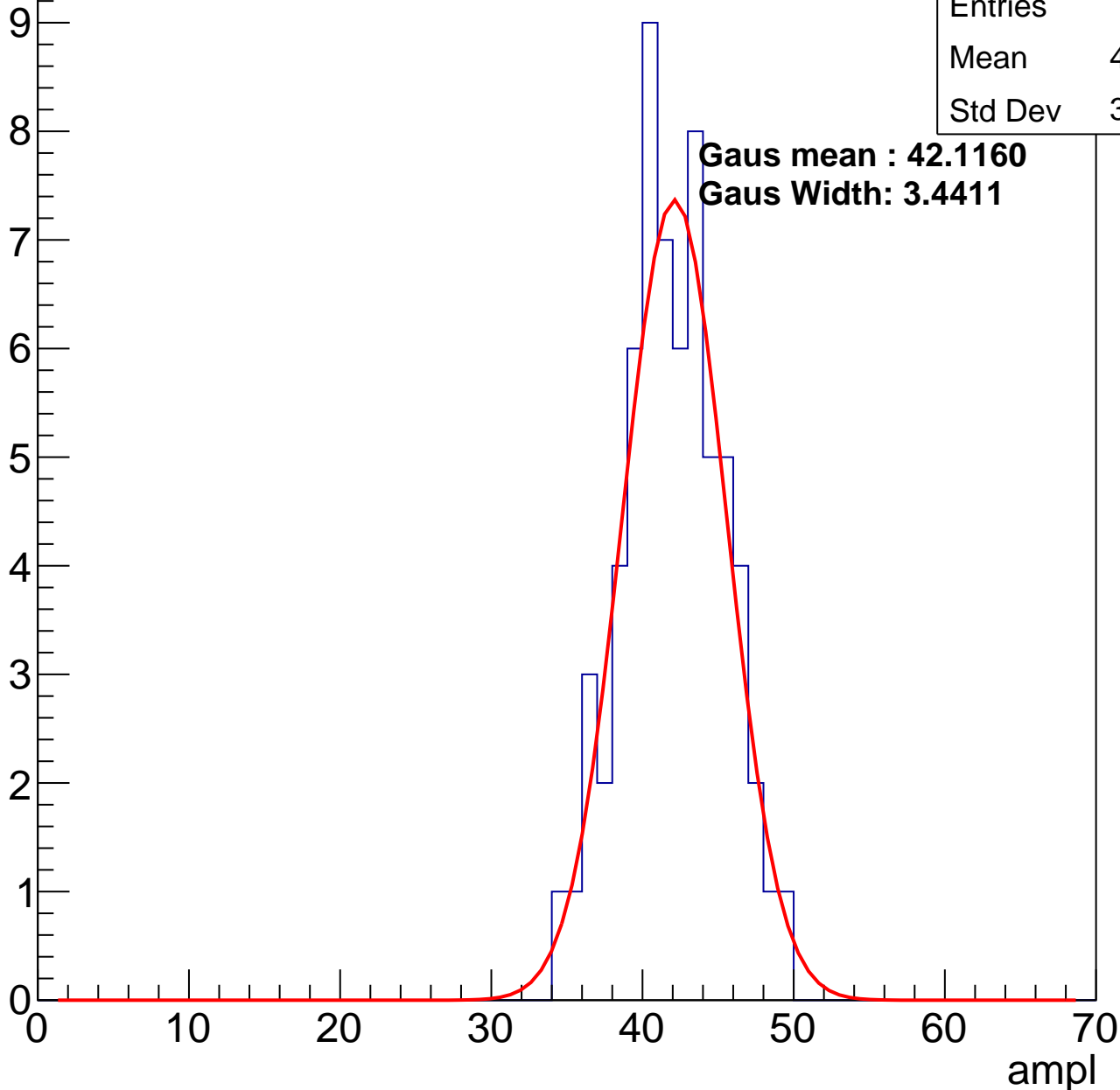
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	41.54
Std Dev	3.258

**Gaus mean : 42.1160**

**Gaus Width: 3.4411**

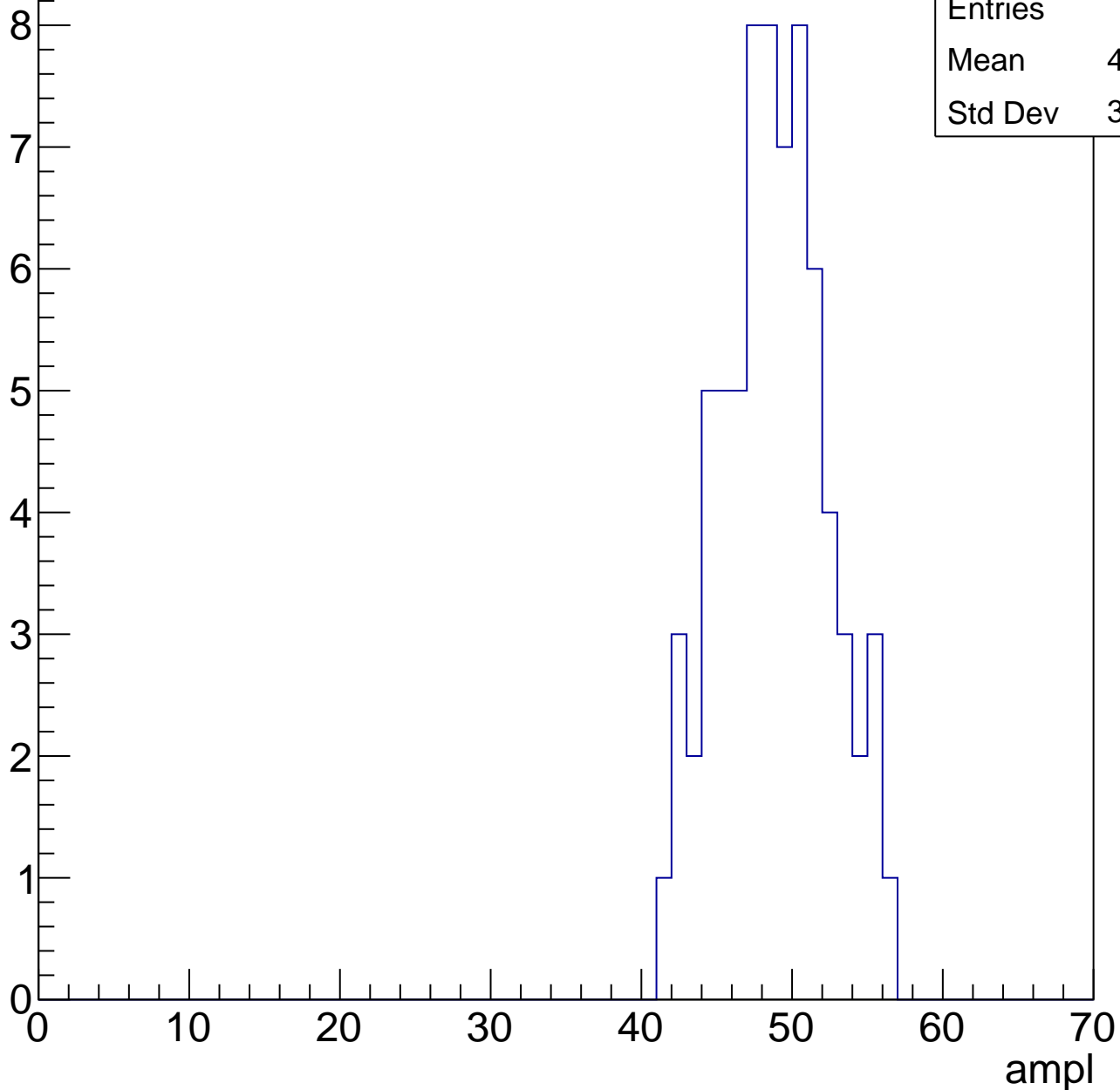


# B0L000S, U26-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	48.35
Std Dev	3.493

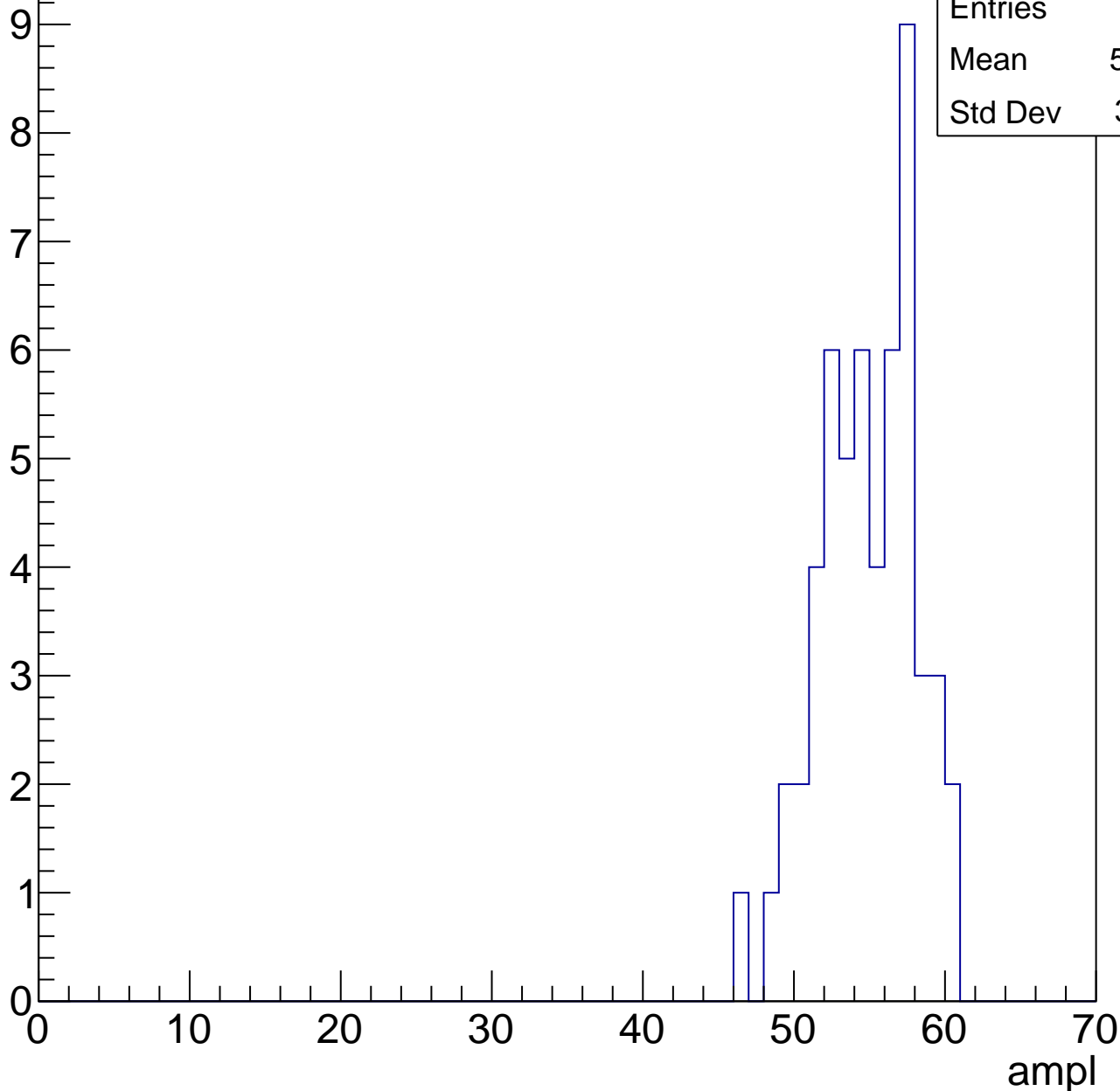


# B0L000S, U26-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

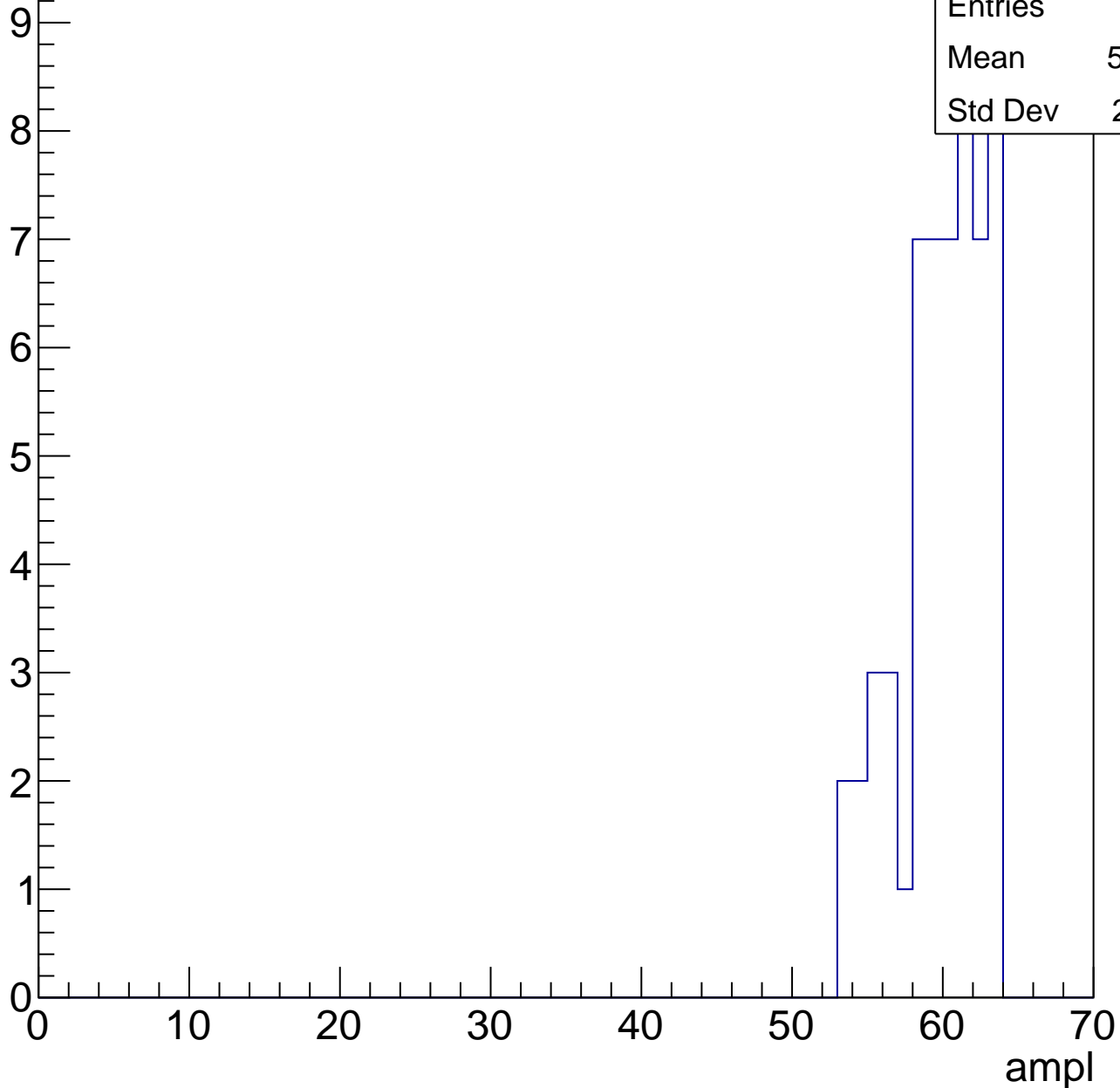
Entries	54
Mean	54.39
Std Dev	3.171



# B0L000S, U26-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

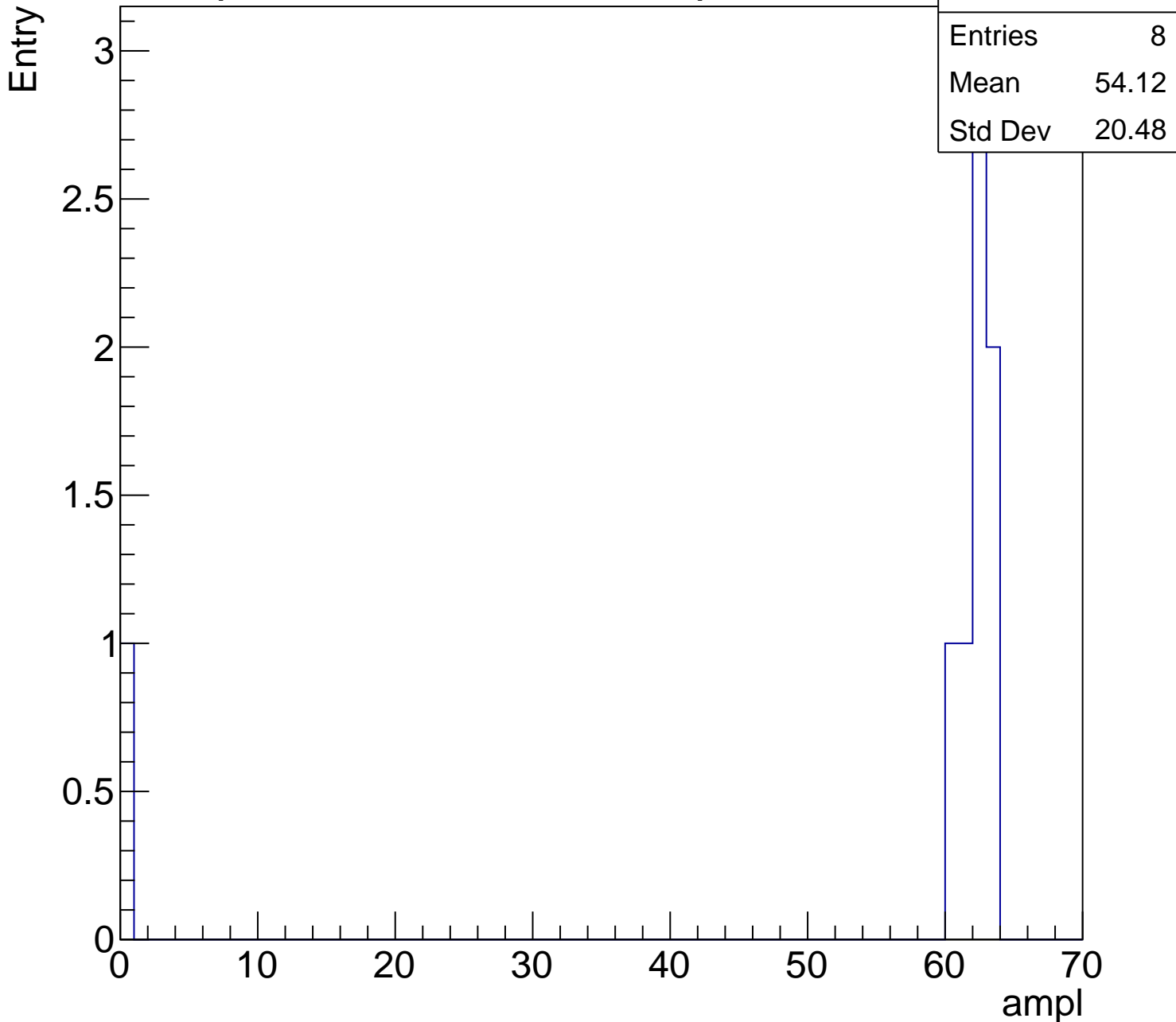
8

Mean

54.12

Std Dev

20.48





# B0L000S, U26-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch14, adc0

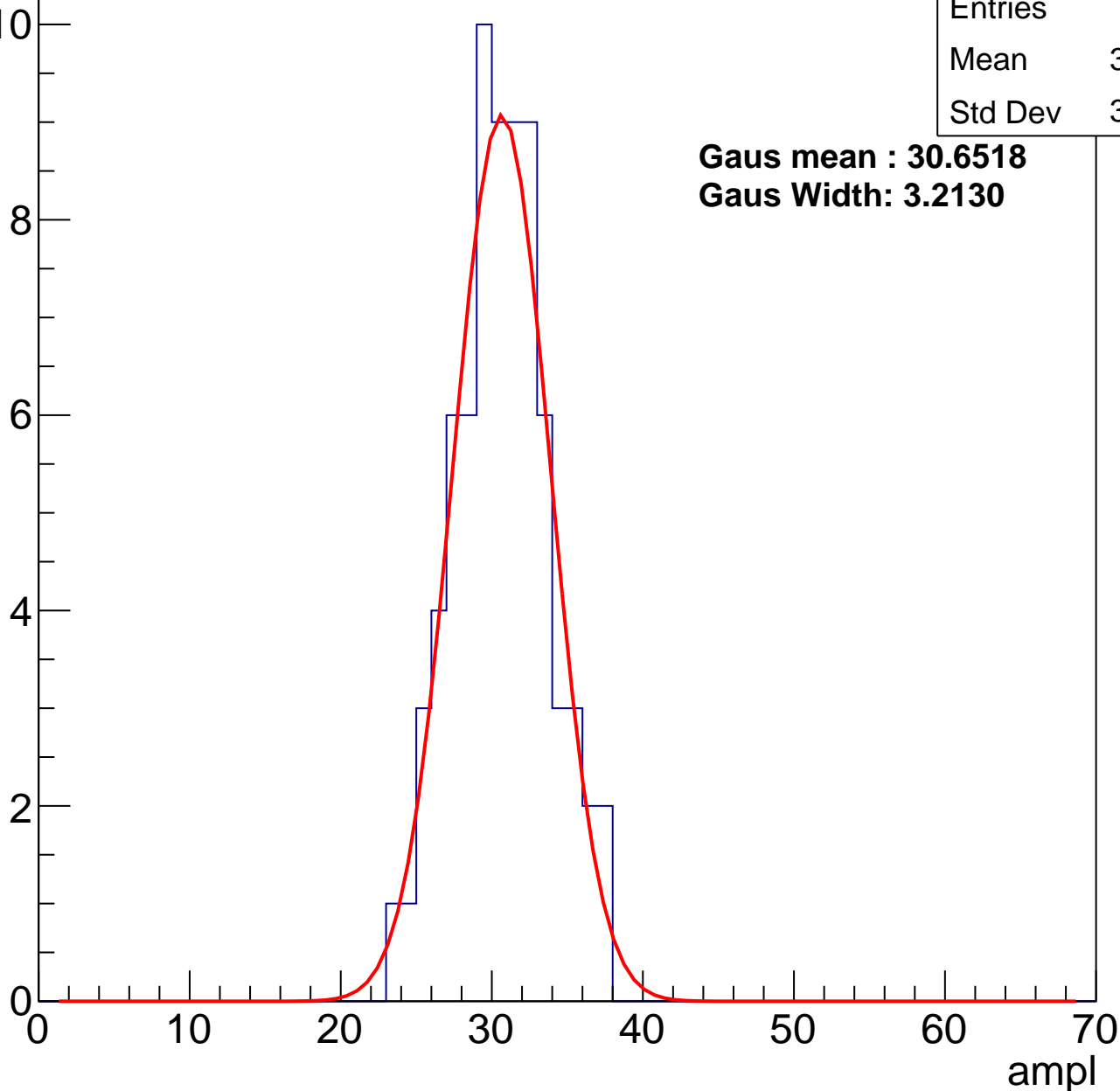
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	30.19
Std Dev	3.079

**Gaus mean : 30.6518**

**Gaus Width: 3.2130**



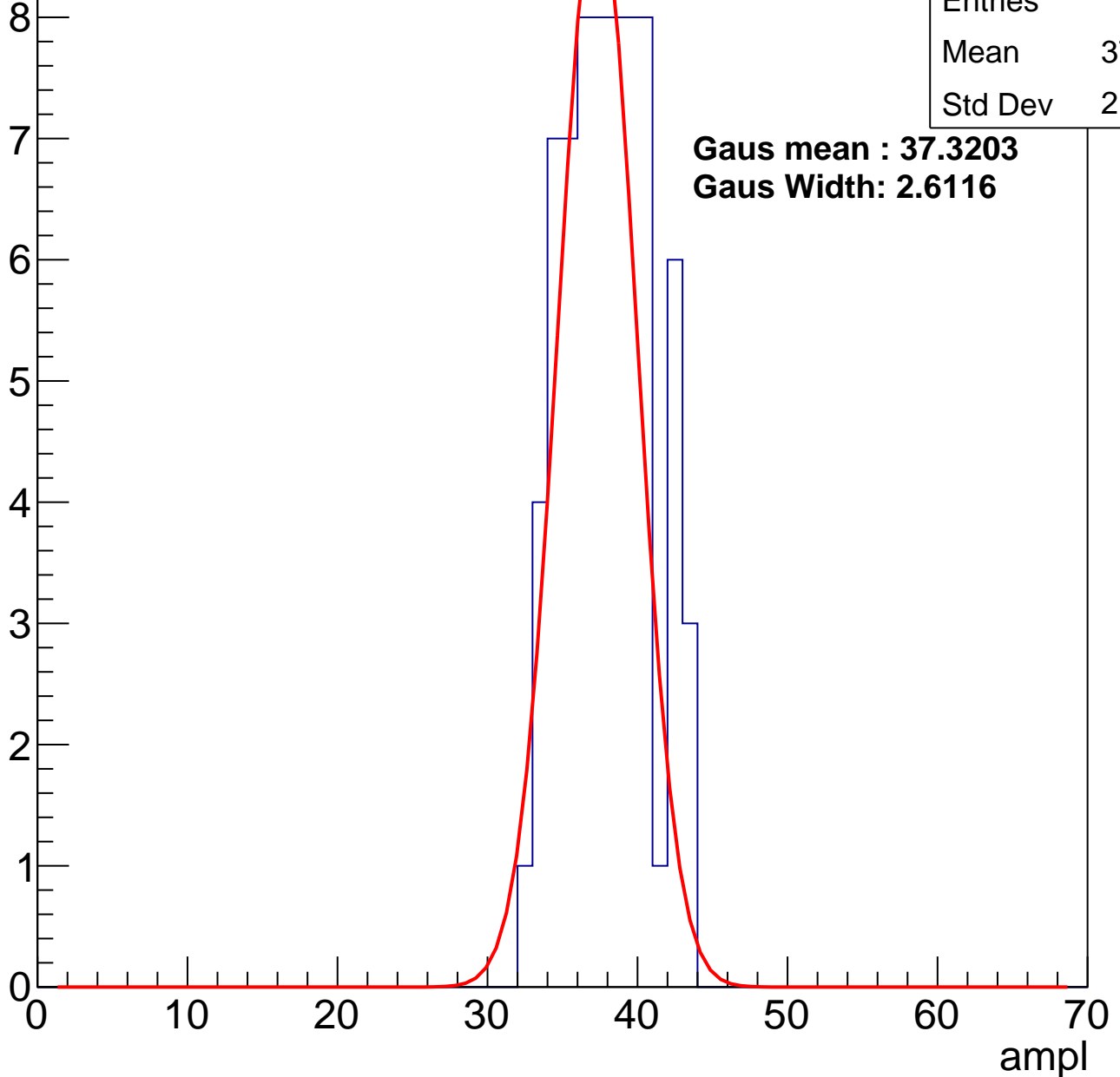
# B0L000S, U26-ch14, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	37.52
Std Dev	2.837

**Gaus mean : 37.3203**  
**Gaus Width: 2.6116**



# B0L000S, U26-ch14, adc2

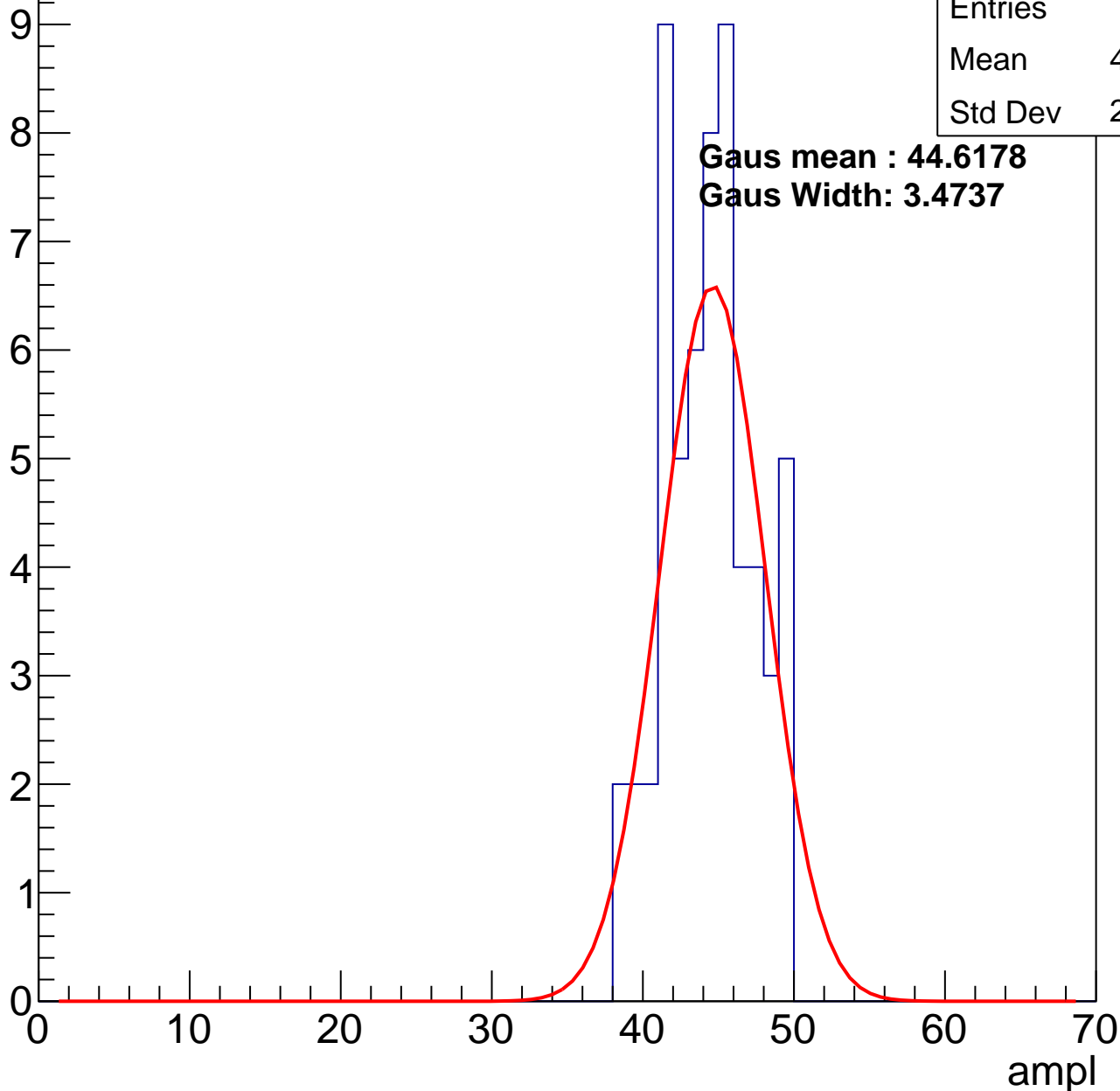
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	43.88
Std Dev	2.894

**Gaus mean : 44.6178**

**Gaus Width: 3.4737**



# B0L000S, U26-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

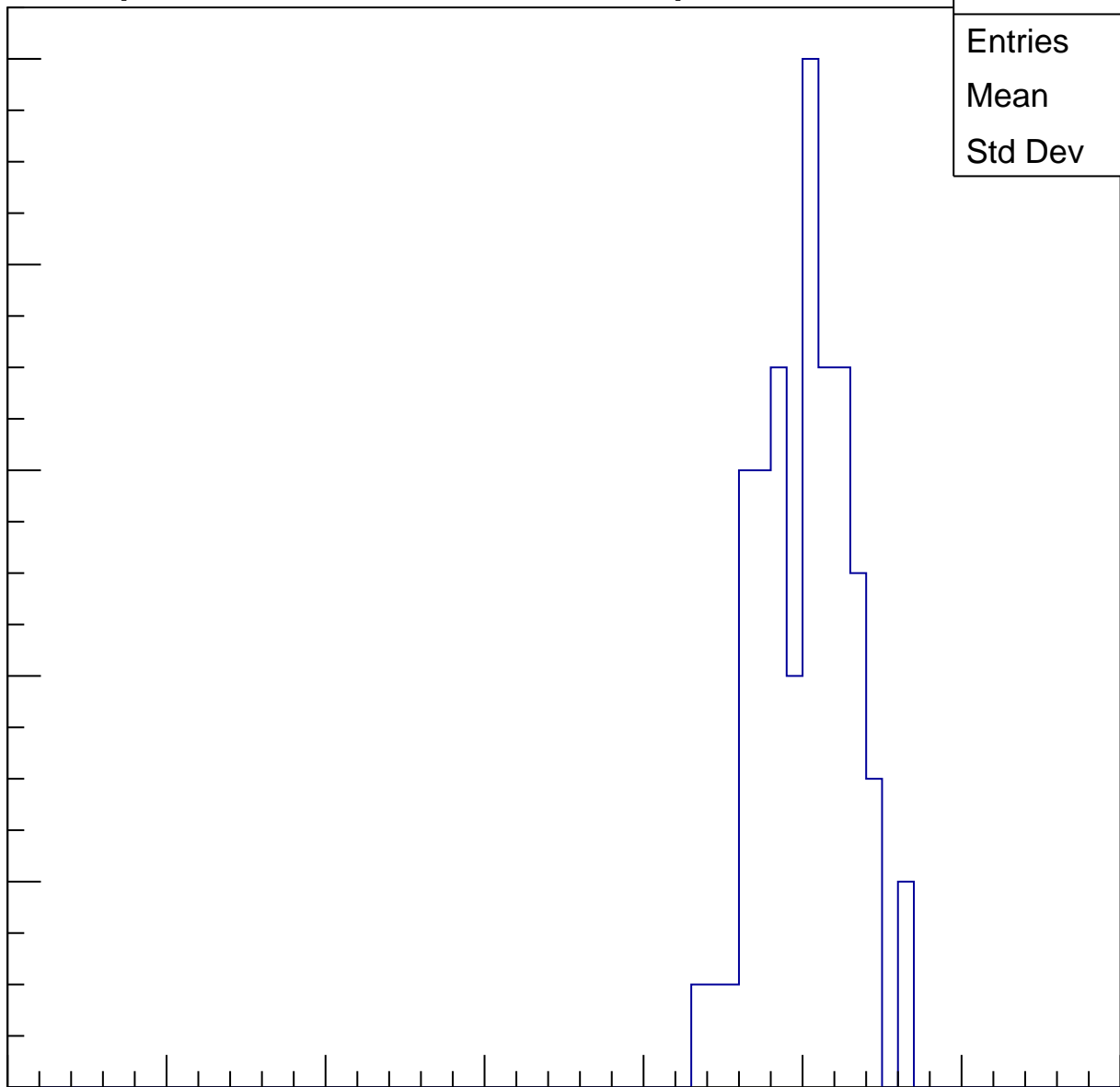
Entries	60
Mean	49.7
Std Dev	2.848

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

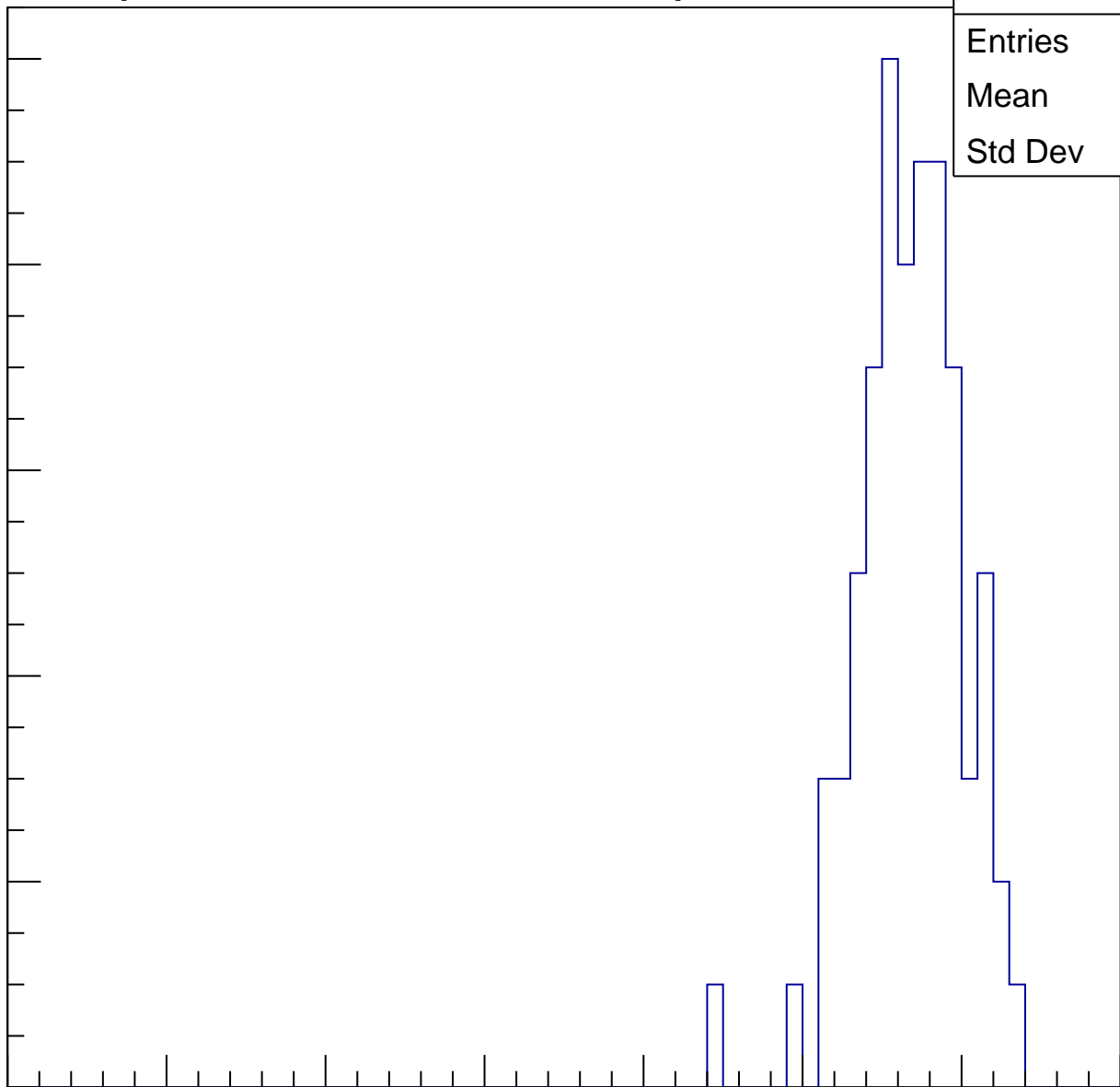
Entries	74
Mean	56.26
Std Dev	3.28

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

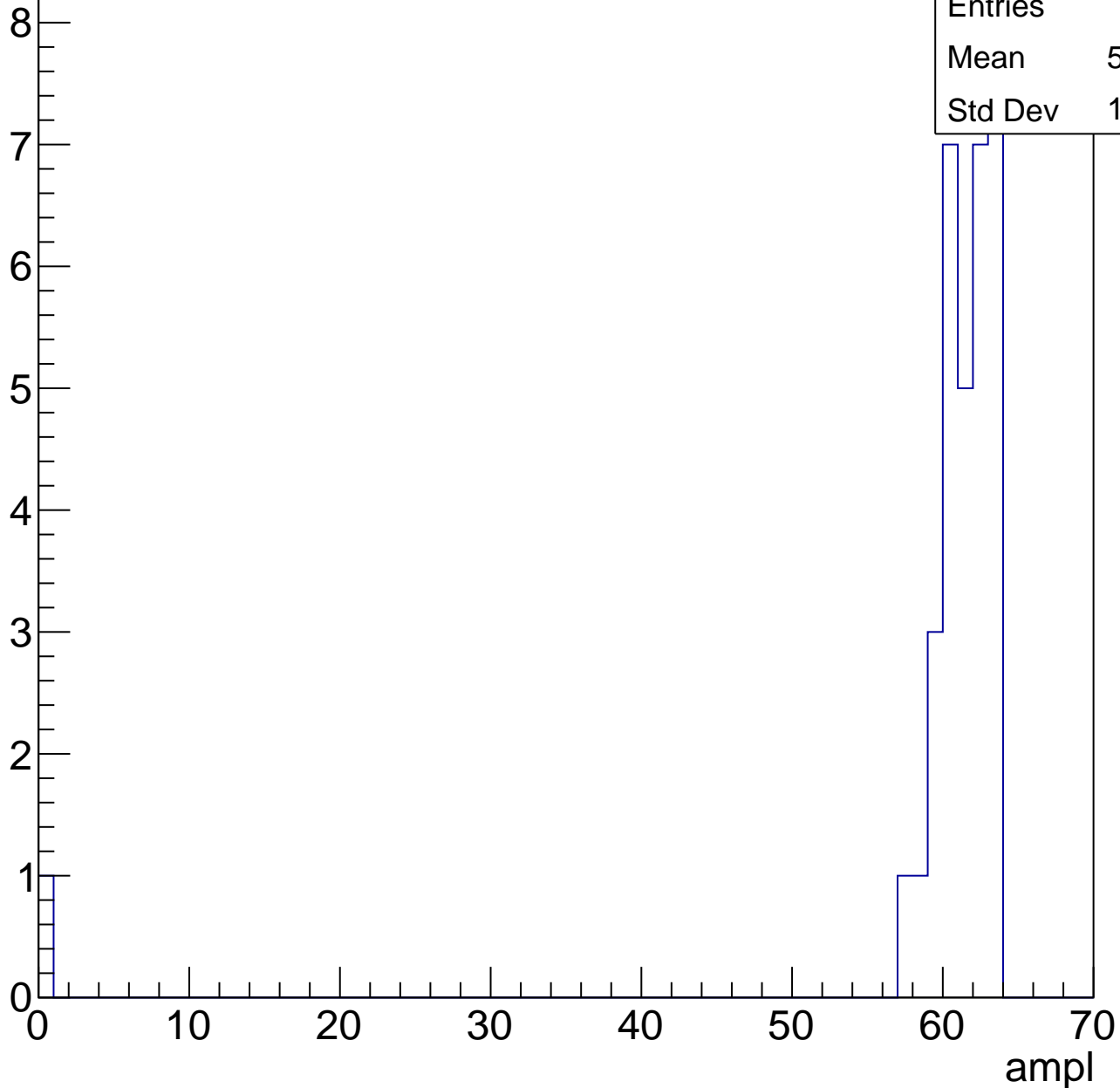
ampl



# B0L000S, U26-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U26-ch15, adc0

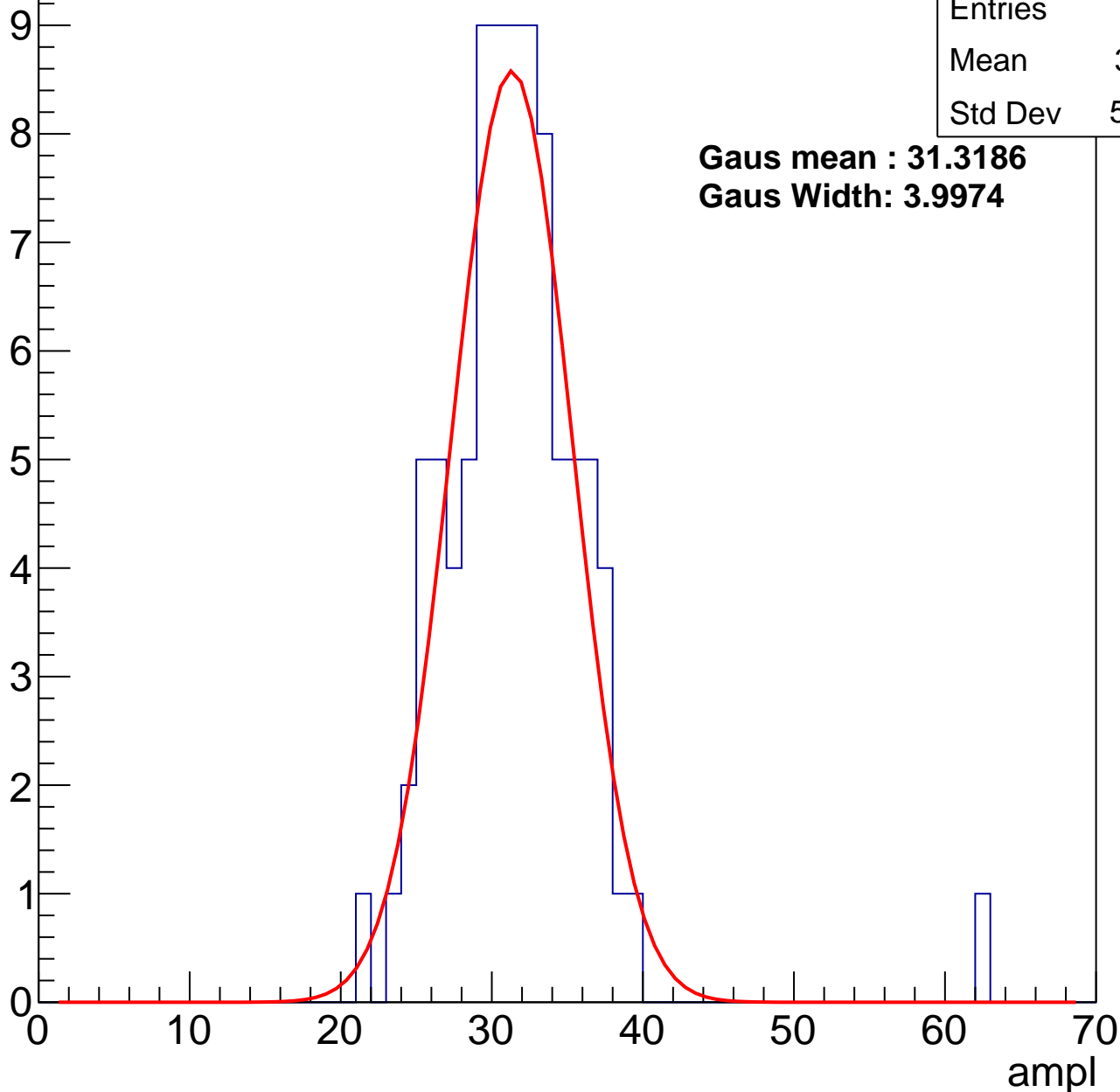
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	89
Mean	31.11
Std Dev	5.004

**Gaus mean : 31.3186**

**Gaus Width: 3.9974**



# B0L000S, U26-ch15, adc1

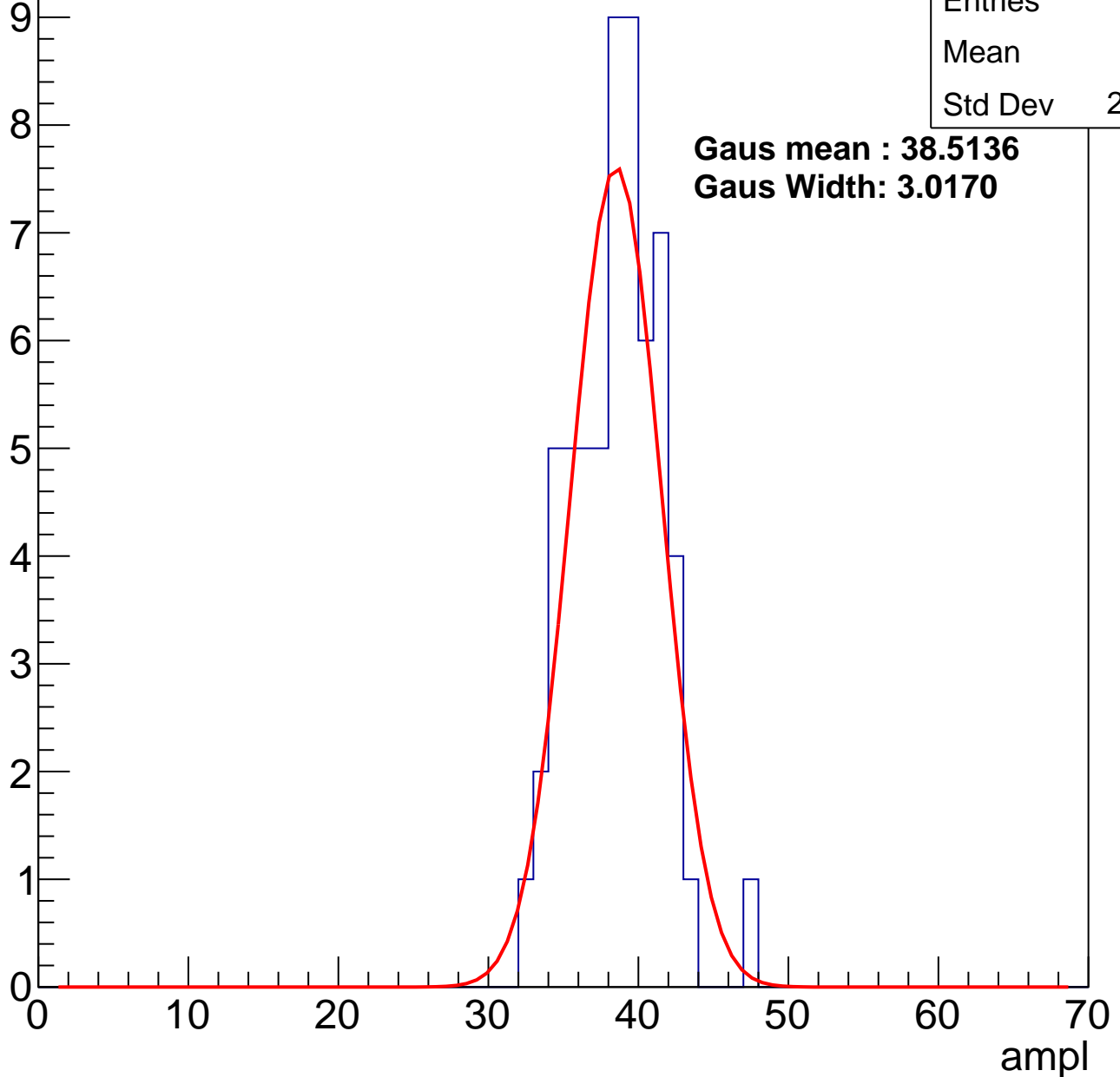
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	38.1
Std Dev	2.891

**Gaus mean : 38.5136**

**Gaus Width: 3.0170**



# B0L000S, U26-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	56
Mean	44.25
Std Dev	2.654

**Gaus mean : 44.4823**

**Gaus Width: 2.8295**

Entry

10

8

6

4

2

0

0

10

20

30

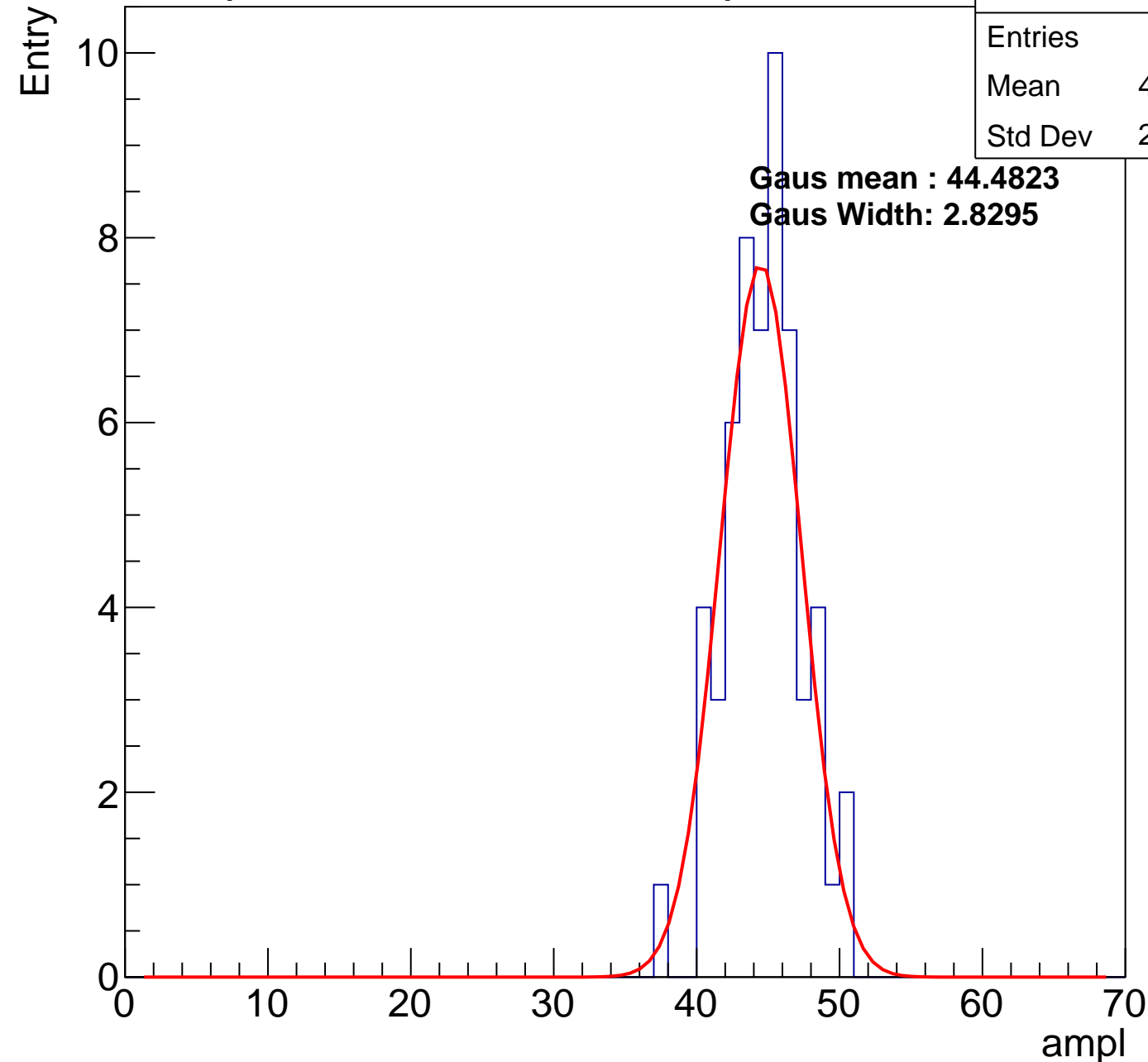
40

50

60

70

ampl

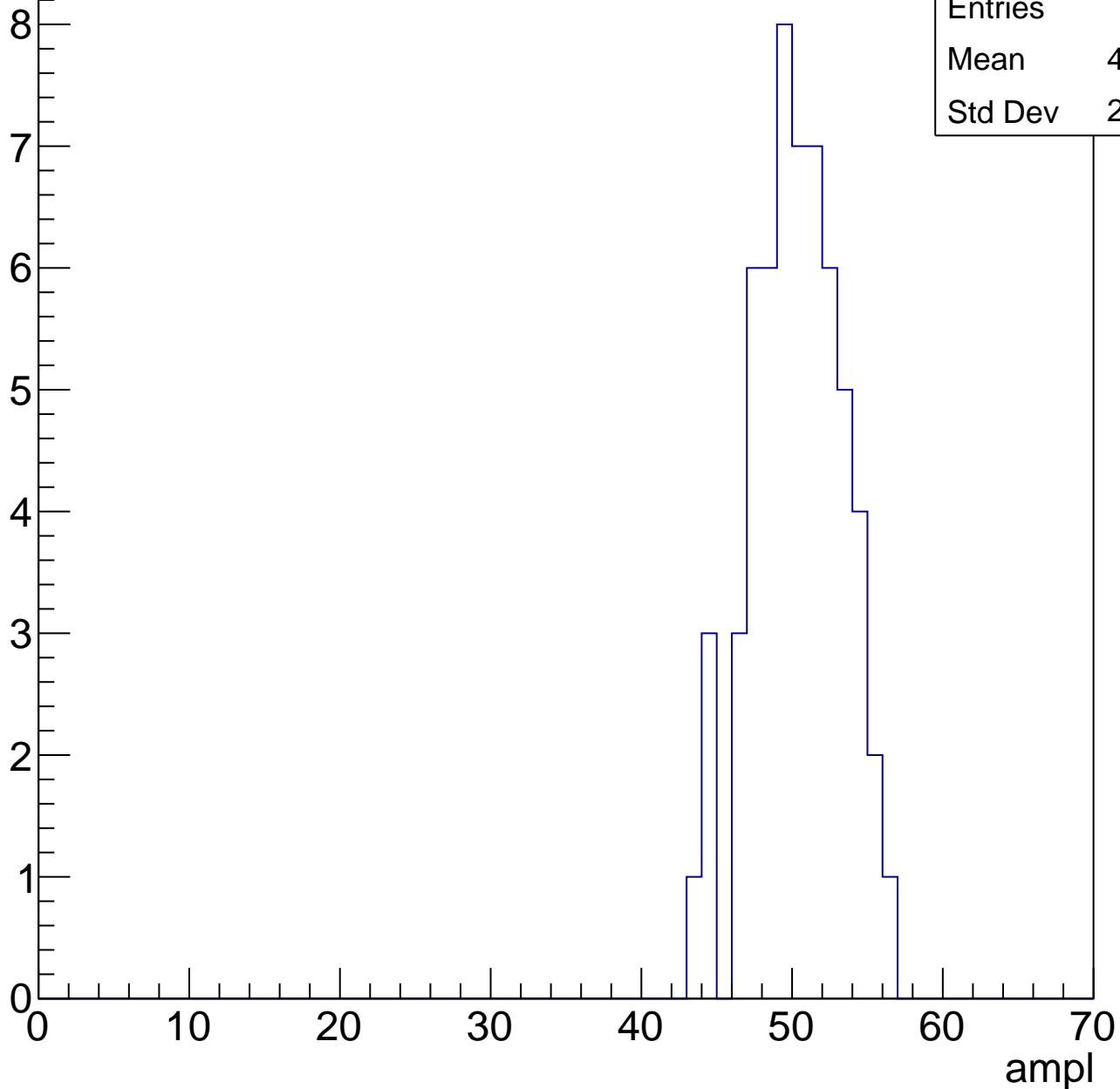


# B0L000S, U26-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

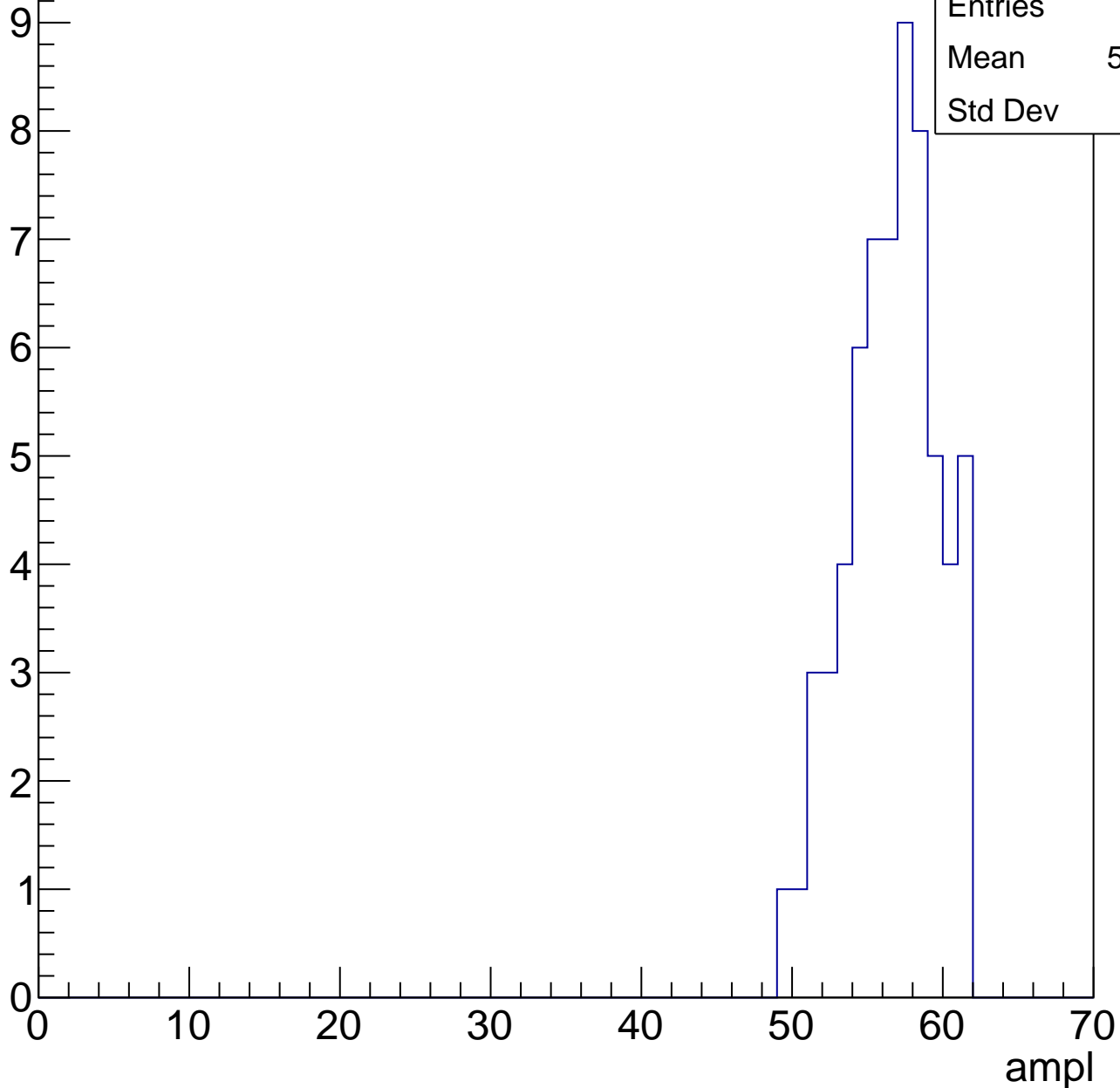
Entries	59
Mean	49.85
Std Dev	2.956



# B0L000S, U26-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



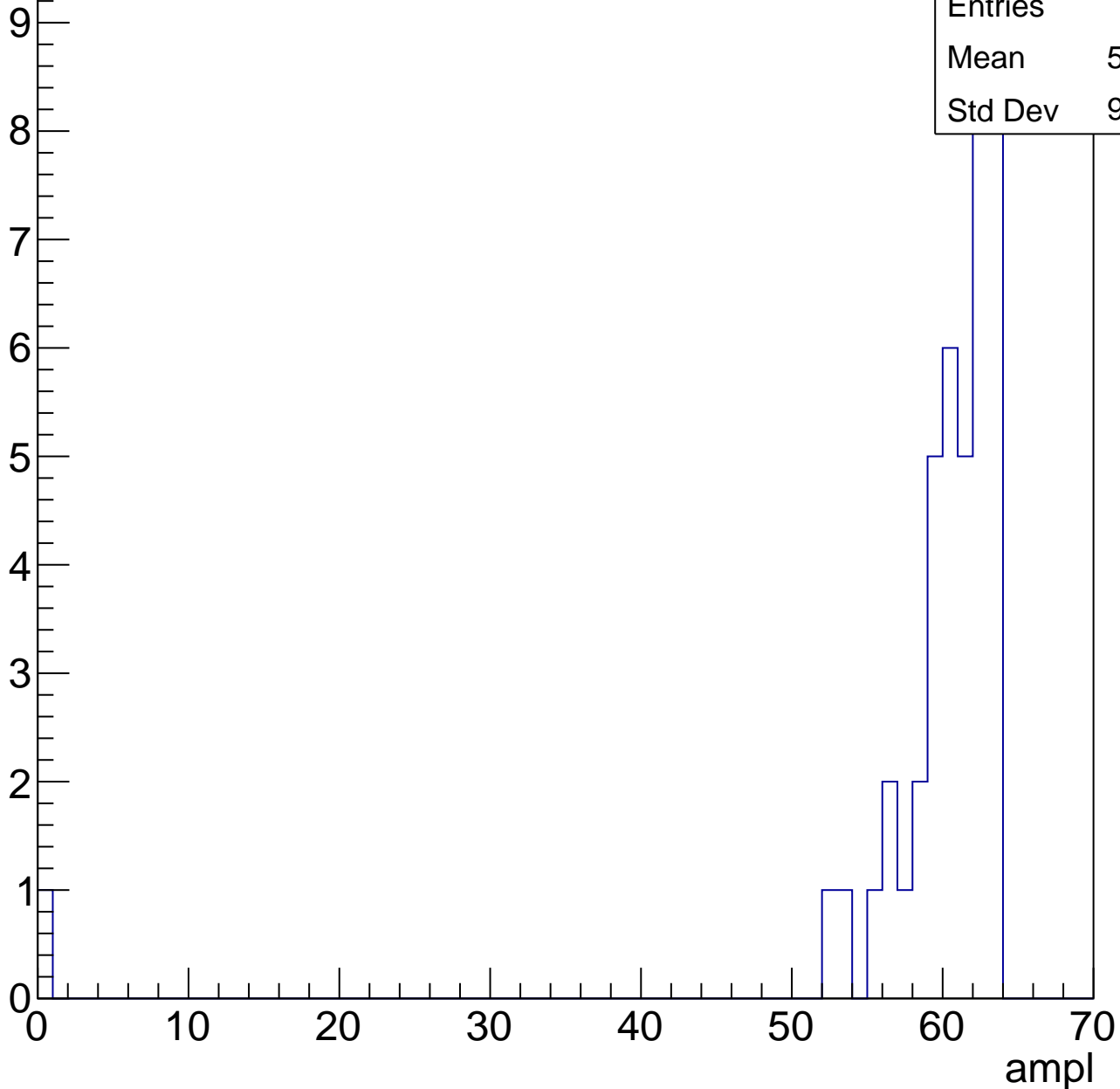
Entries	63
Mean	56.16
Std Dev	2.95

# B0L000S, U26-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.84
Std Dev	9.469



# B0L000S, U26-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch16, adc0

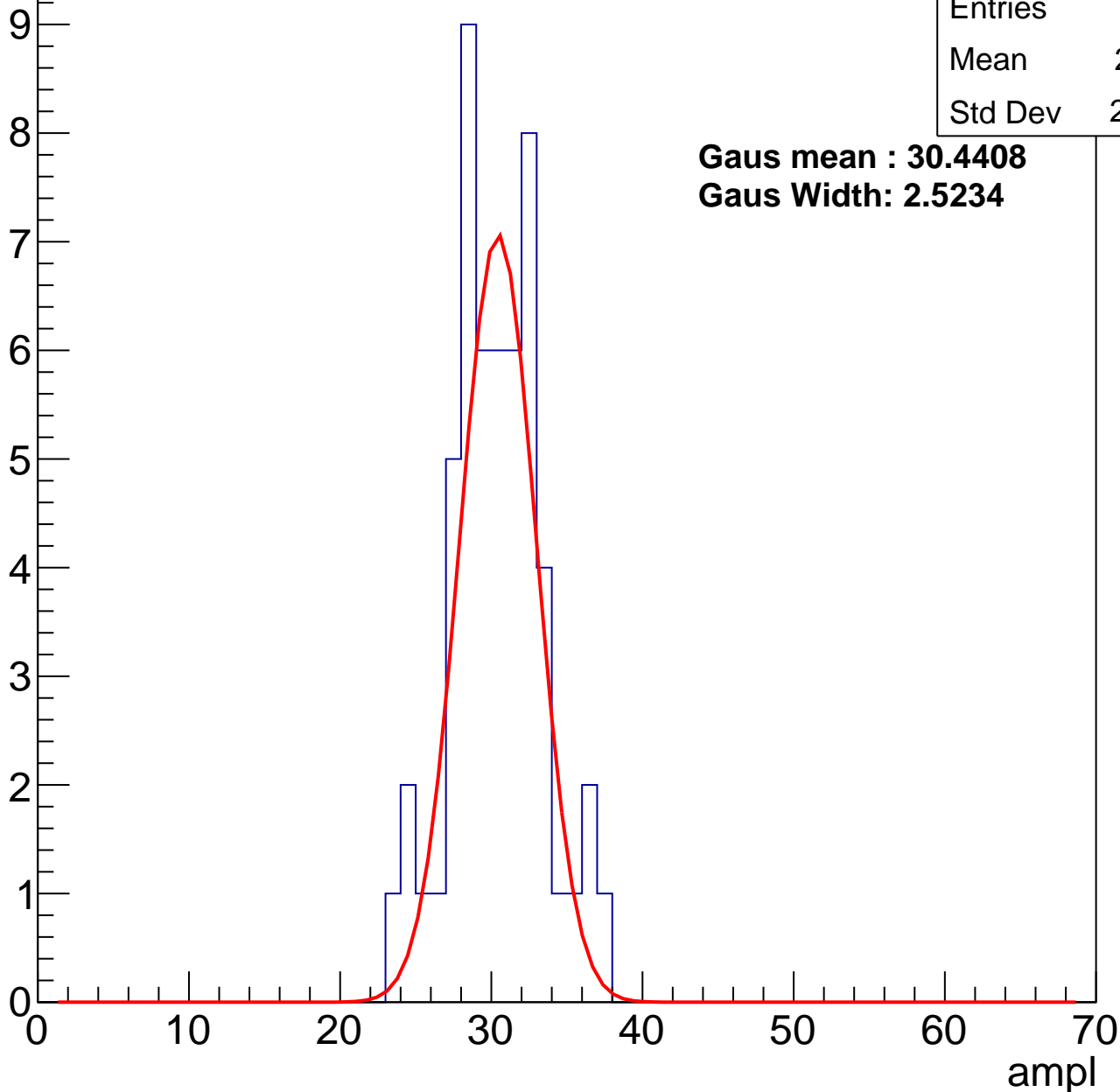
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	29.91
Std Dev	2.995

**Gaus mean : 30.4408**

**Gaus Width: 2.5234**



# B0L000S, U26-ch16, adc1

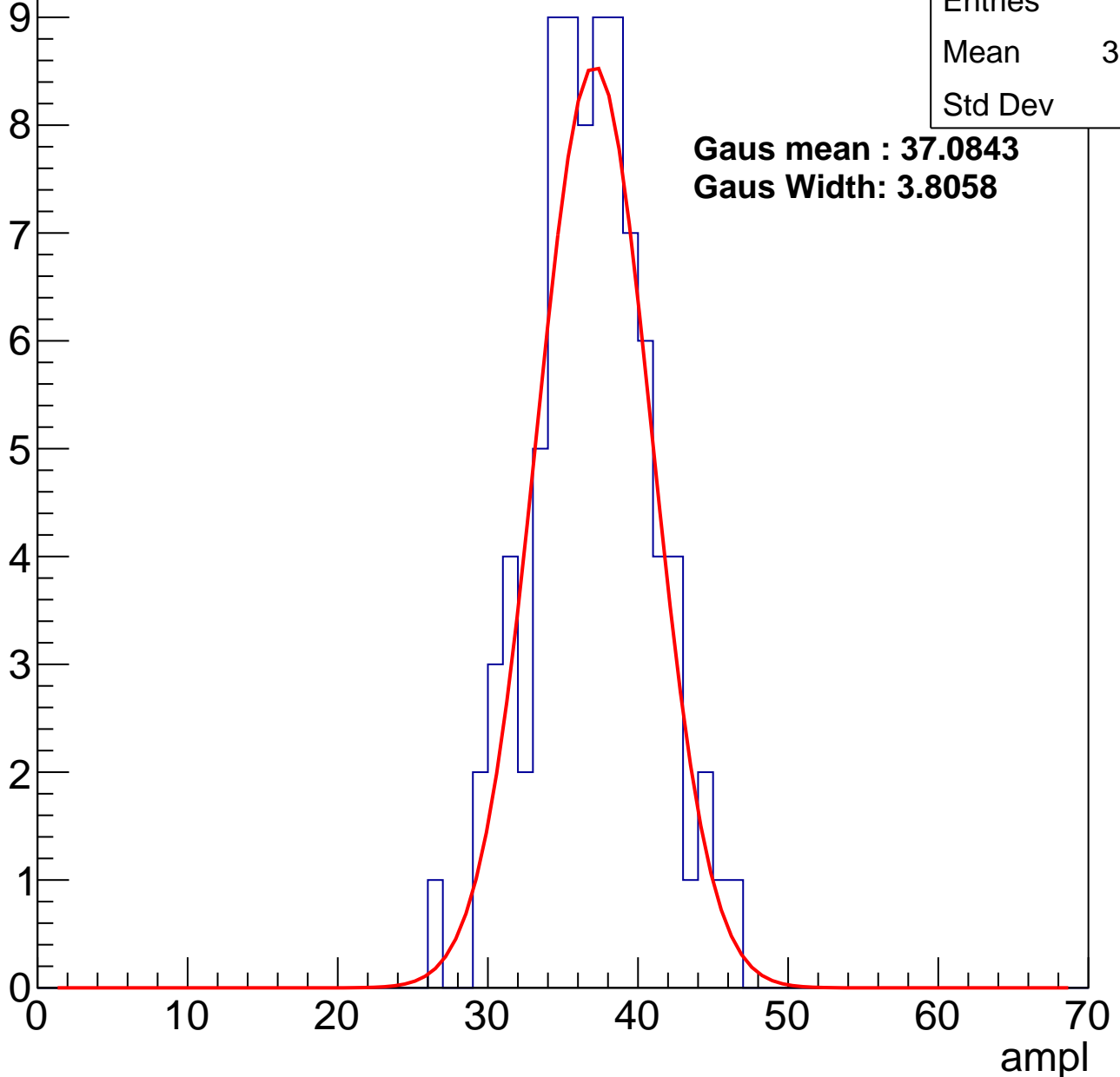
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	87
Mean	36.53
Std Dev	3.88

**Gaus mean : 37.0843**

**Gaus Width: 3.8058**



# B0L000S, U26-ch16, adc2

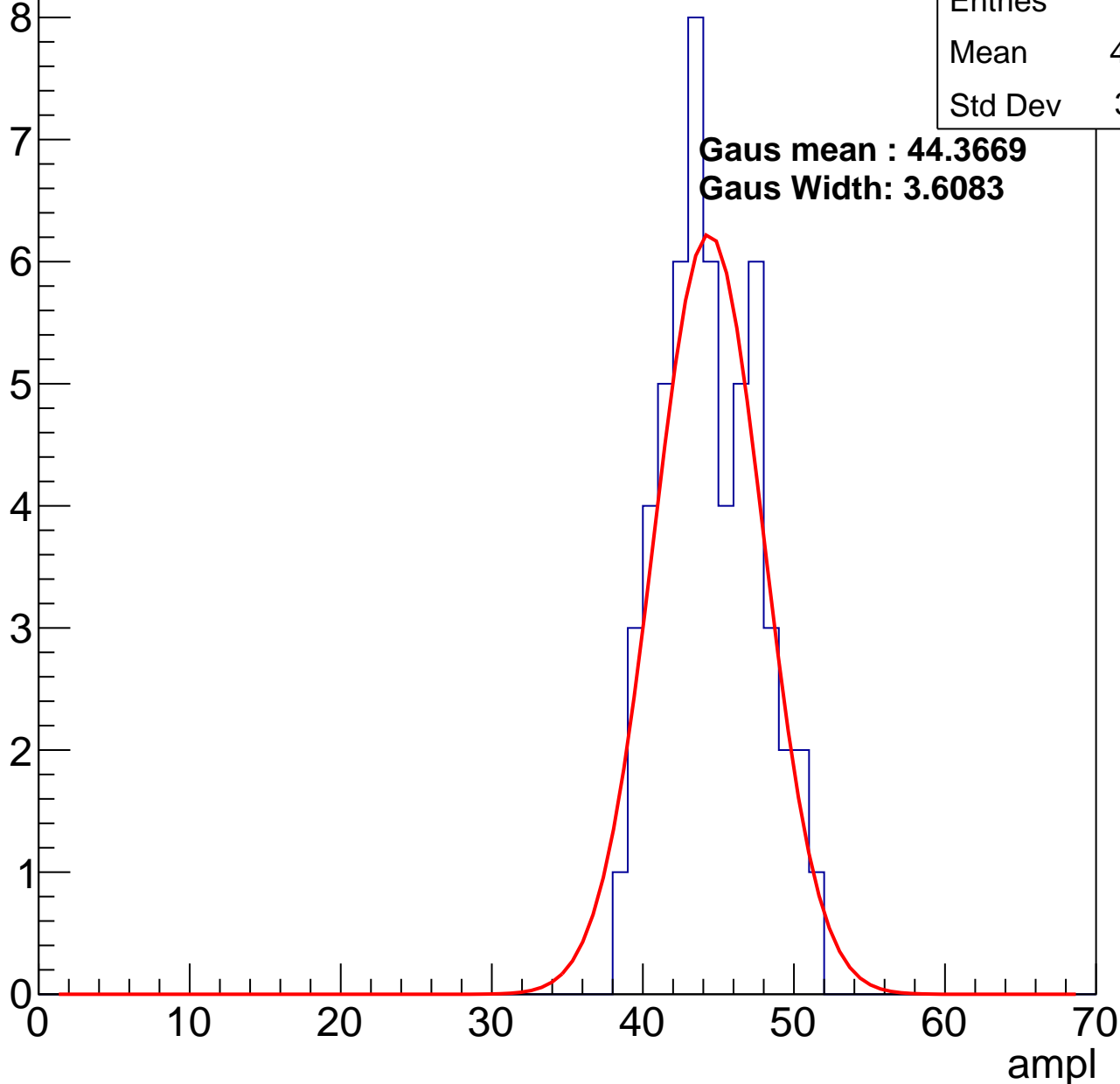
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	44.02
Std Dev	3.131

**Gaus mean : 44.3669**

**Gaus Width: 3.6083**

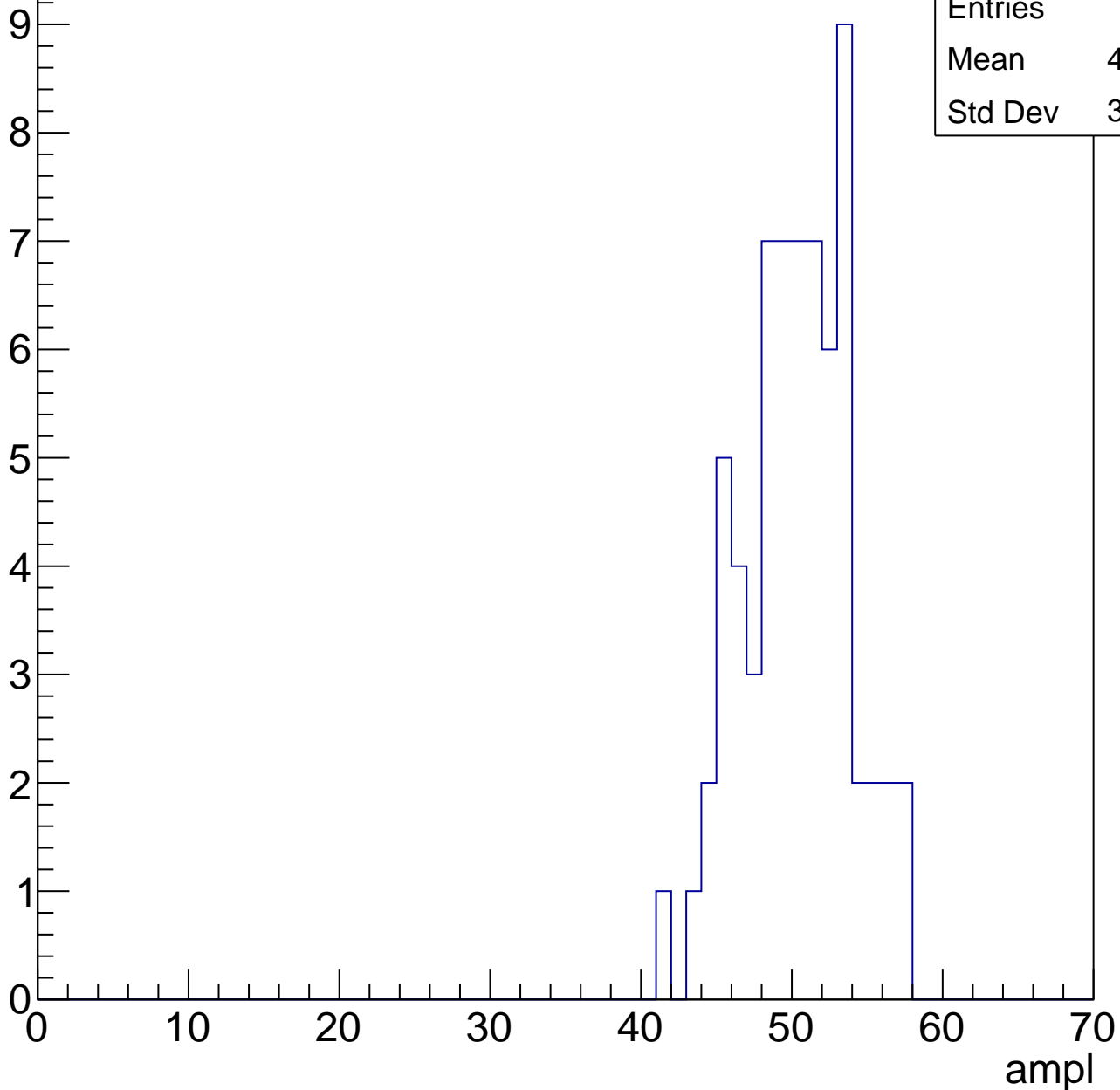


# B0L000S, U26-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.87
Std Dev	3.489

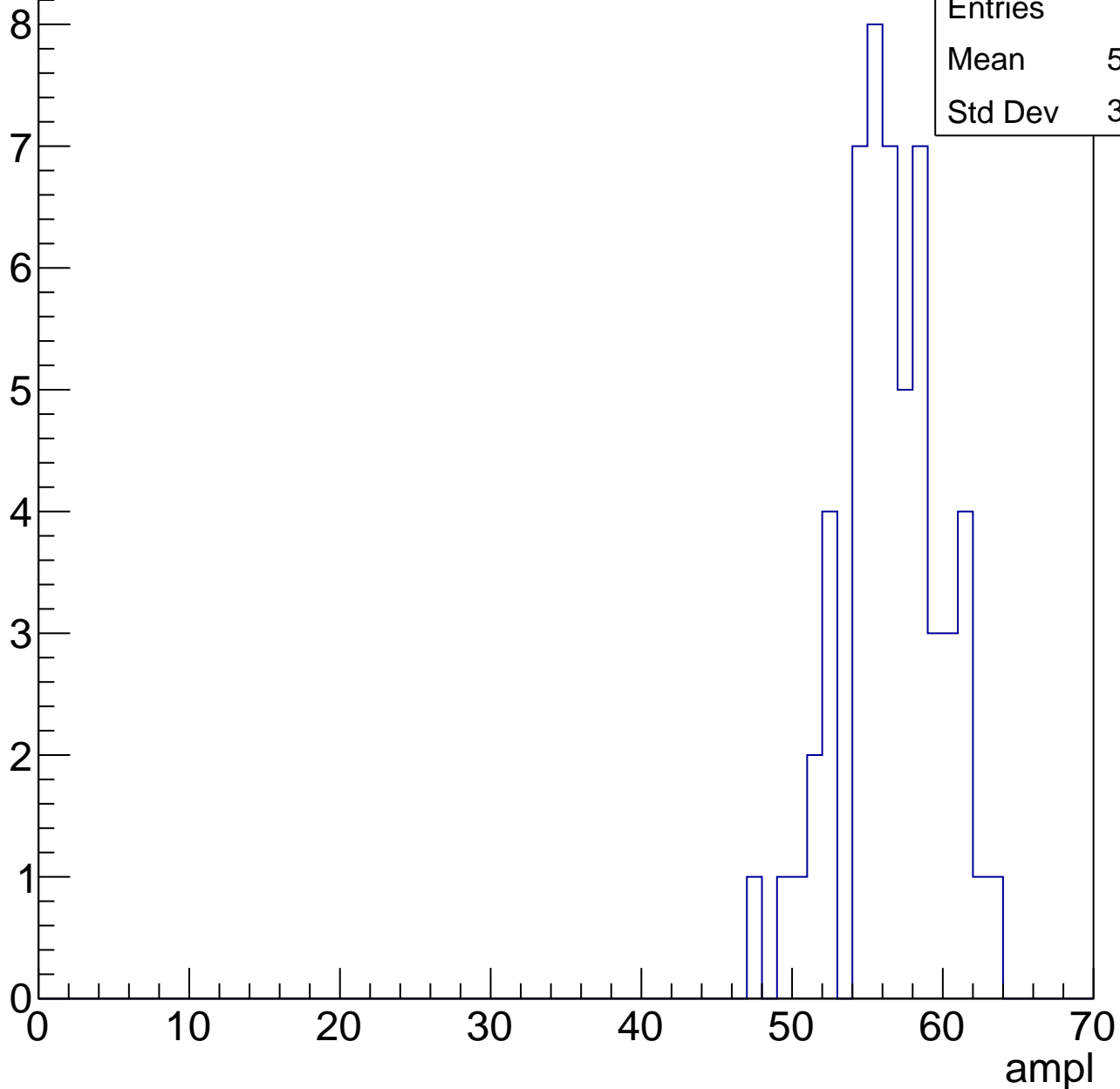


# B0L000S, U26-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.05
Std Dev	3.327

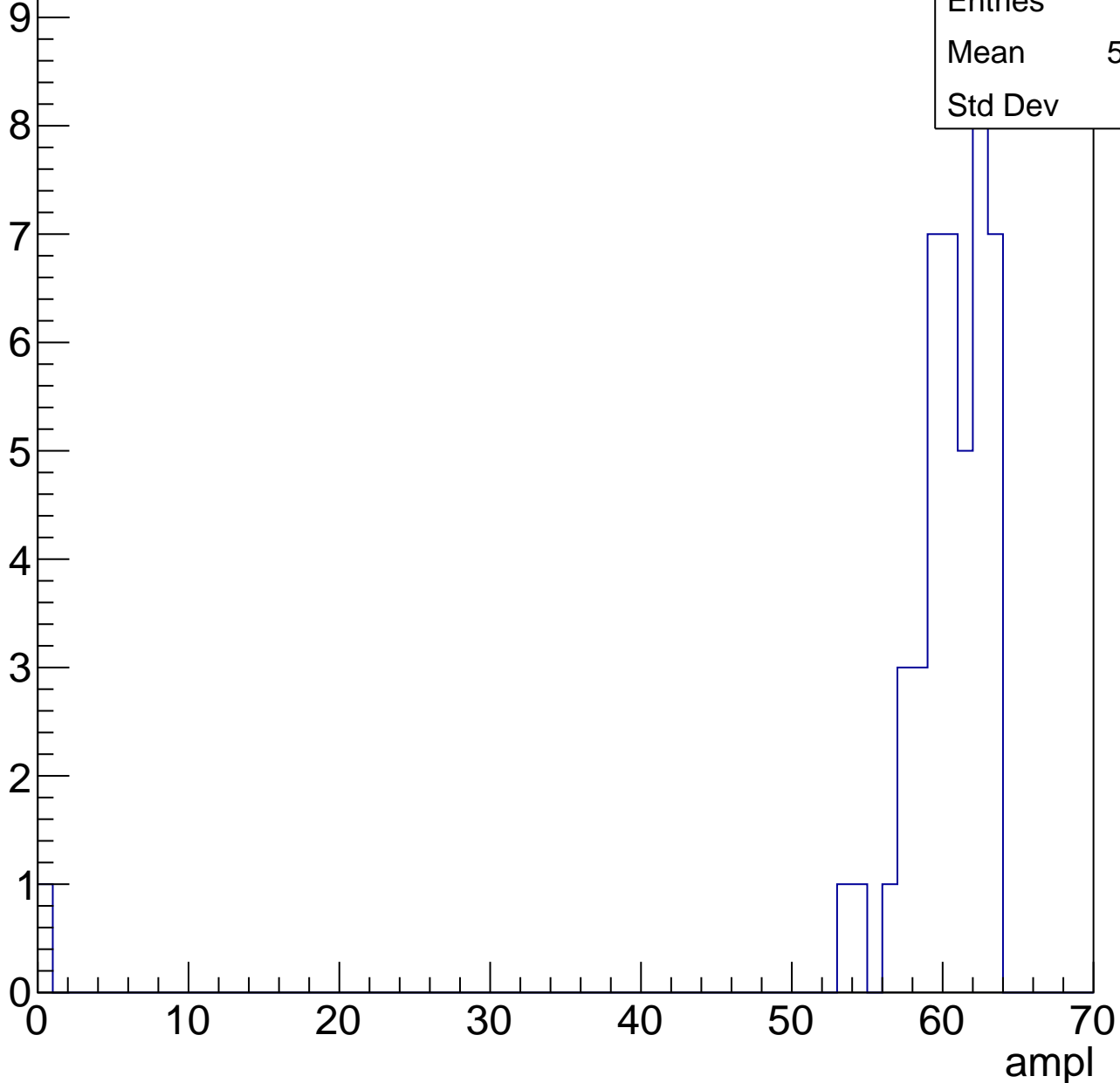


# B0L000S, U26-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.78
Std Dev	9.17



# B0L000S, U26-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl





# B0L000S, U26-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch17, adc0

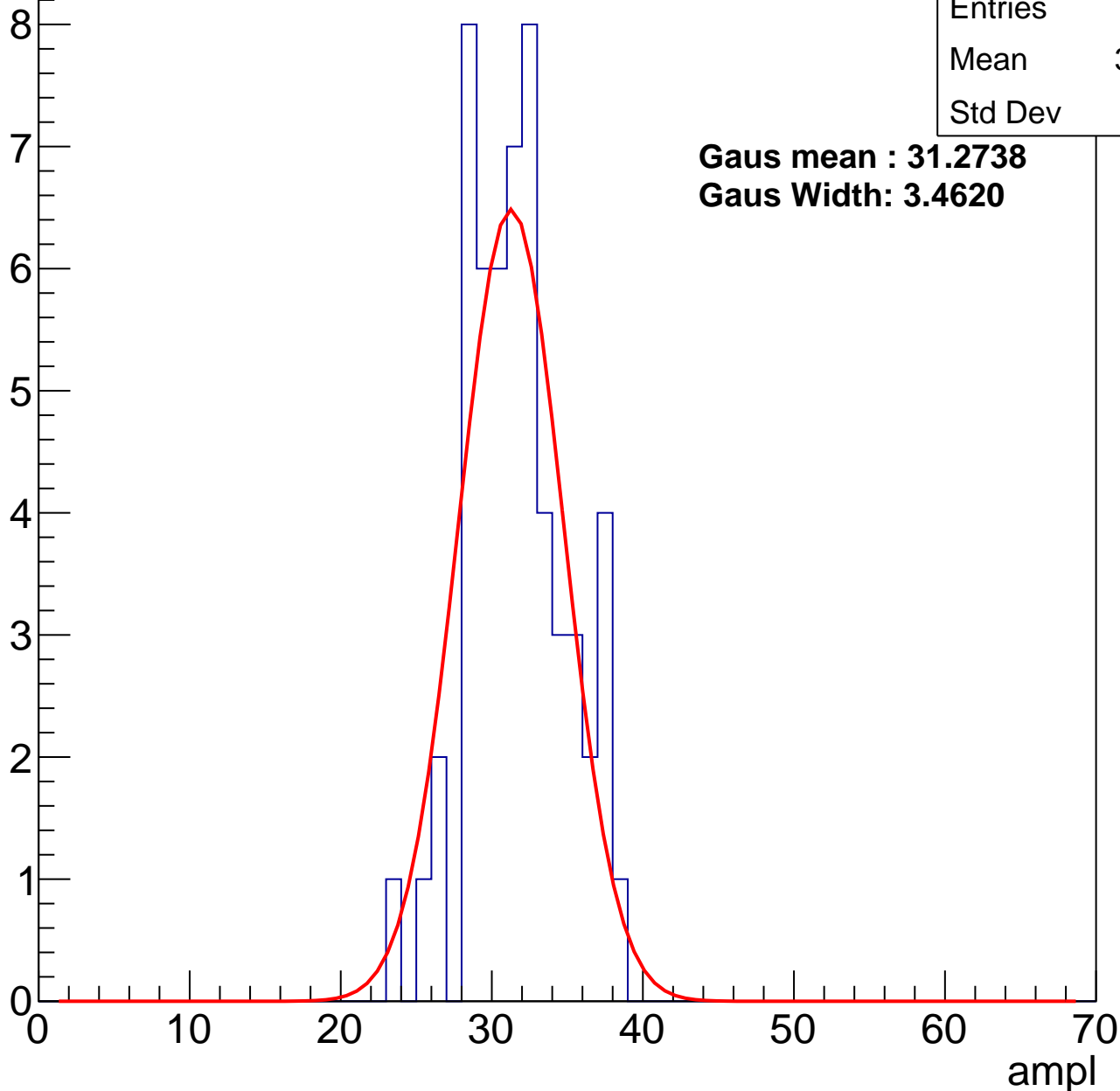
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	31.21
Std Dev	3.25

**Gaus mean : 31.2738**

**Gaus Width: 3.4620**



# B0L000S, U26-ch17, adc1

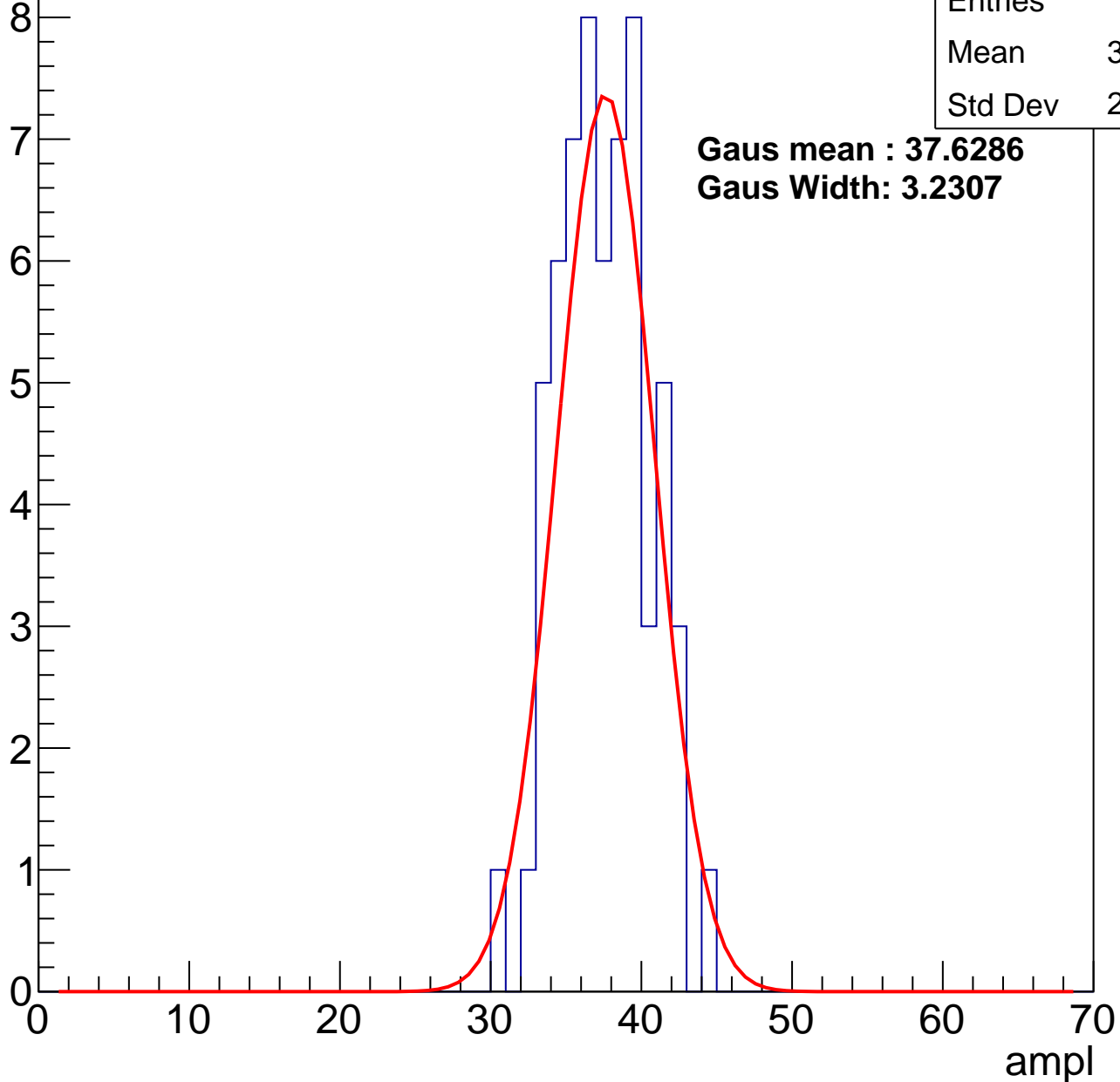
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.03
Std Dev	2.903

**Gaus mean : 37.6286**

**Gaus Width: 3.2307**



# B0L000S, U26-ch17, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	60
Mean	43.32
Std Dev	2.754

**Gaus mean : 43.6718**

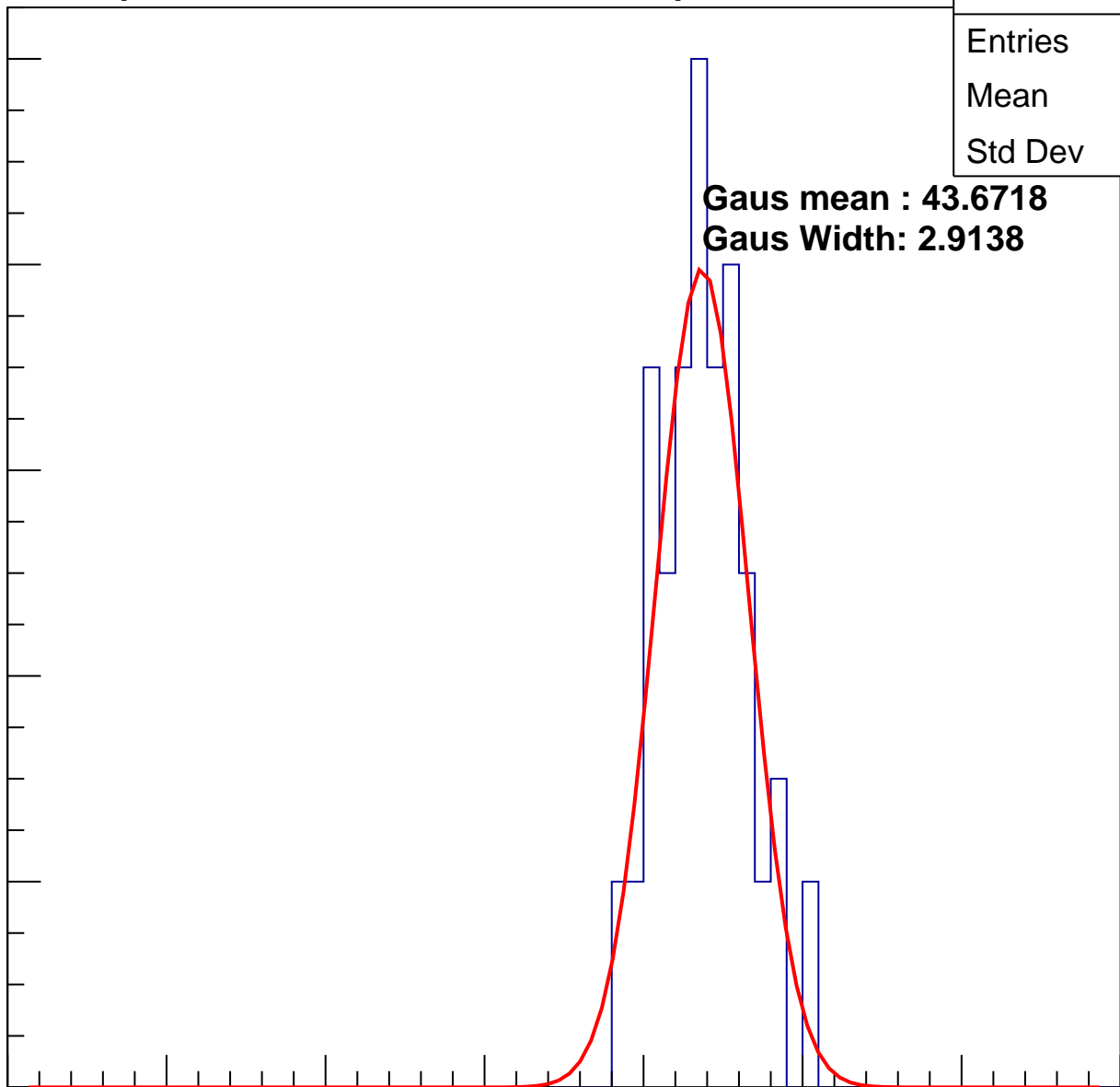
**Gaus Width: 2.9138**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

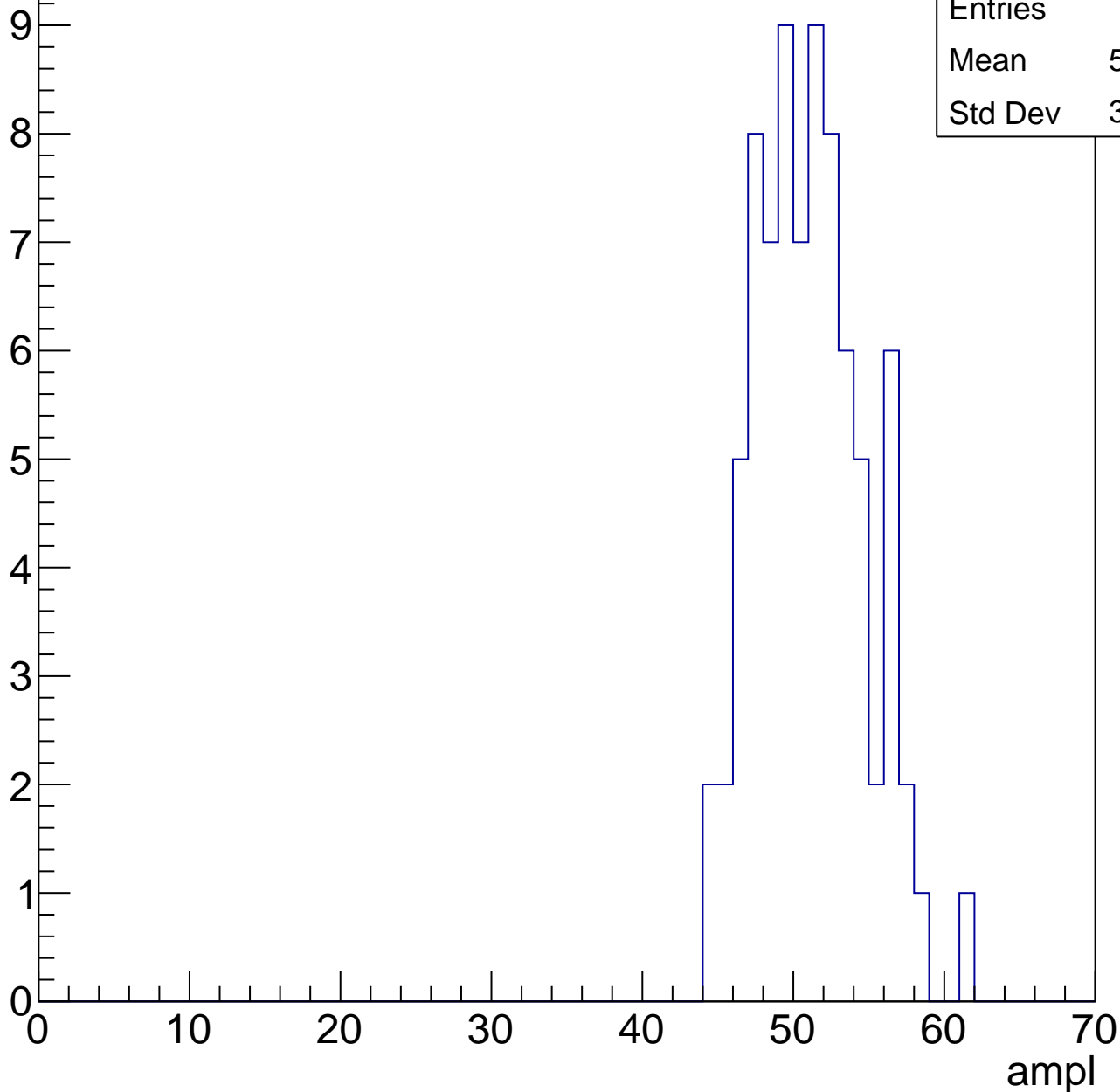


# B0L000S, U26-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

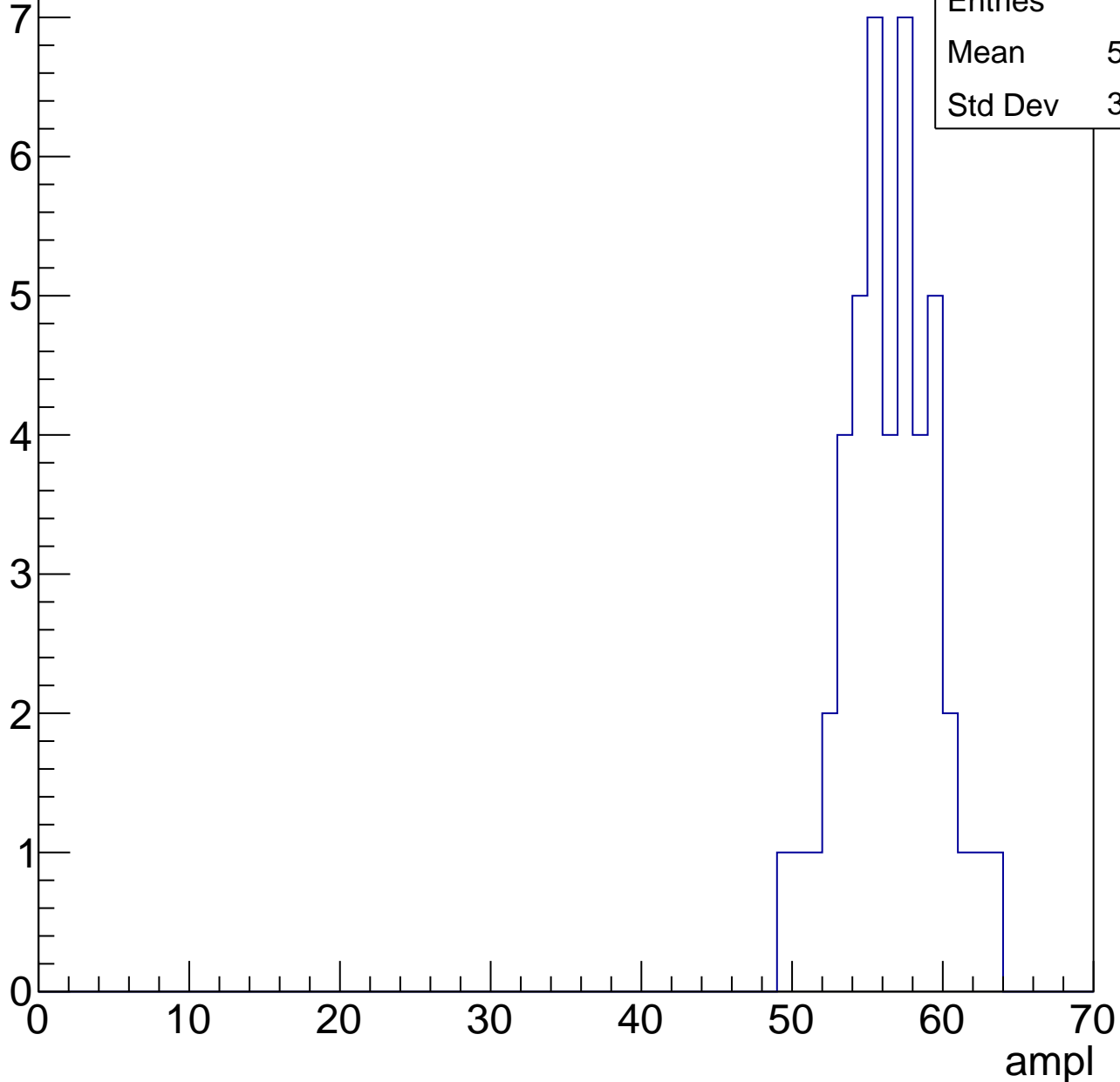
Entries	80
Mean	50.66
Std Dev	3.535



# B0L000S, U26-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

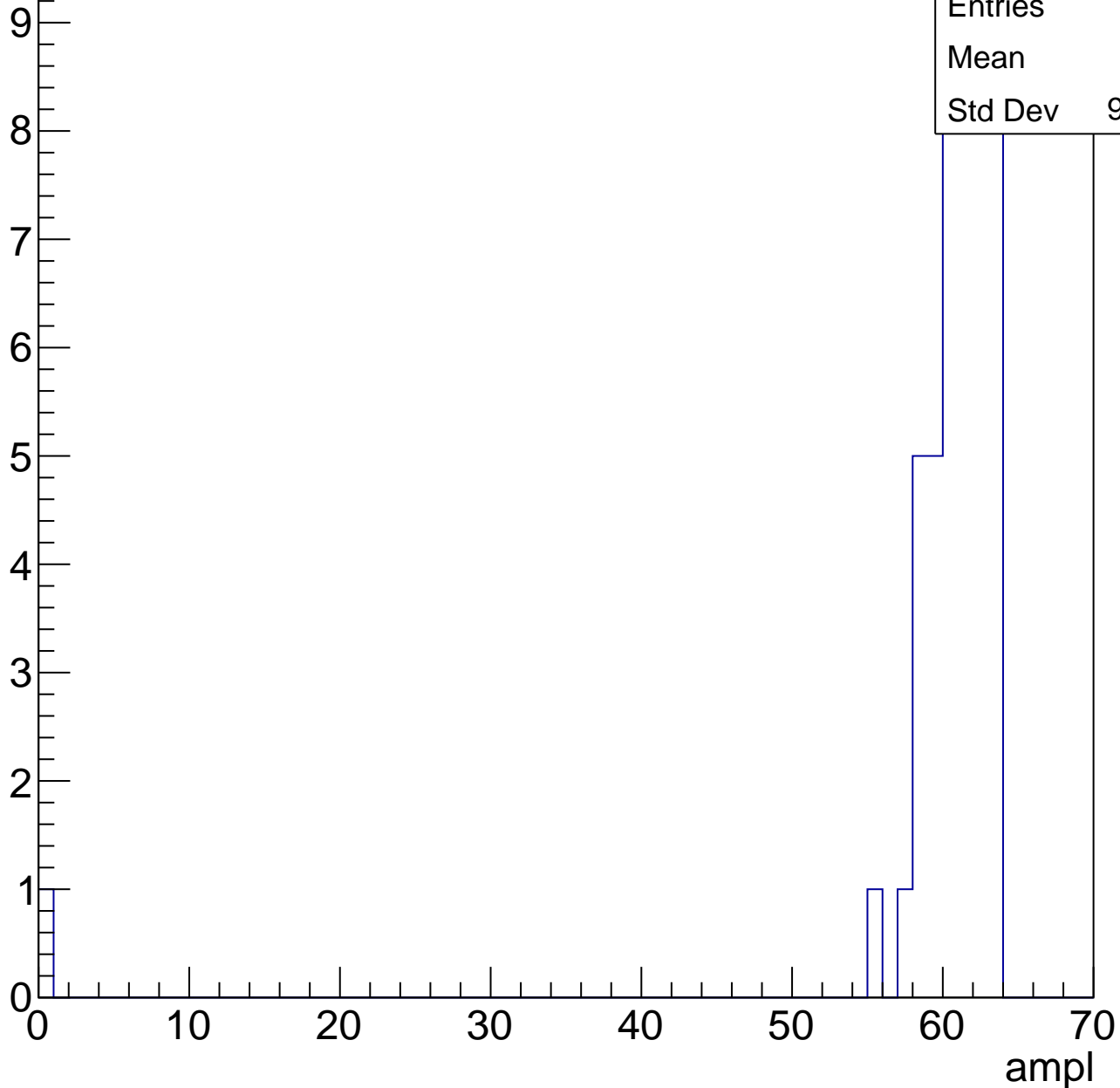
Entry



# B0L000S, U26-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch18, adc0

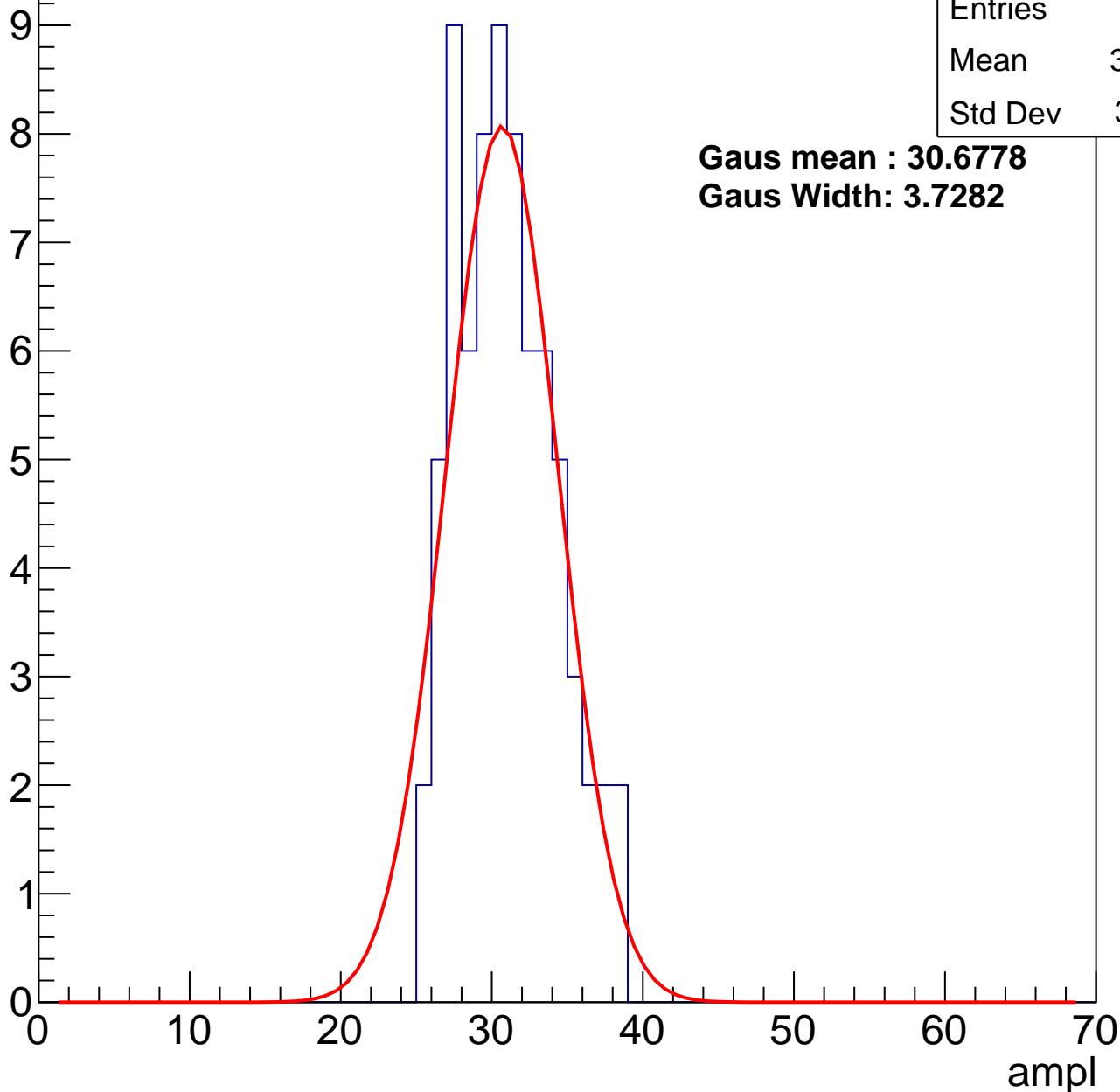
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	30.52
Std Dev	3.231

**Gaus mean : 30.6778**

**Gaus Width: 3.7282**



# B0L000S, U26-ch18, adc1

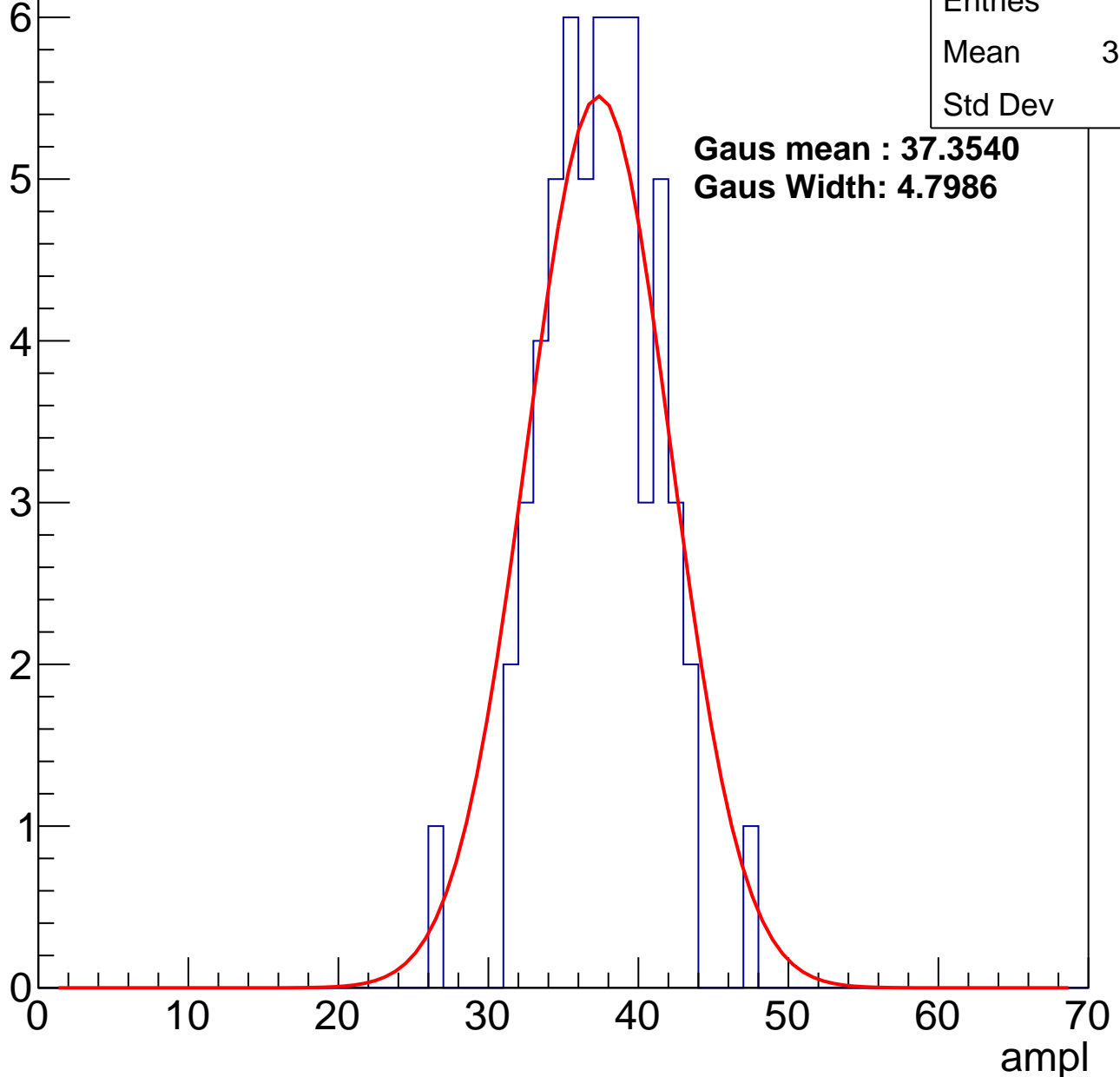
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	36.97
Std Dev	3.69

**Gaus mean : 37.3540**

**Gaus Width: 4.7986**



# B0L000S, U26-ch18, adc2

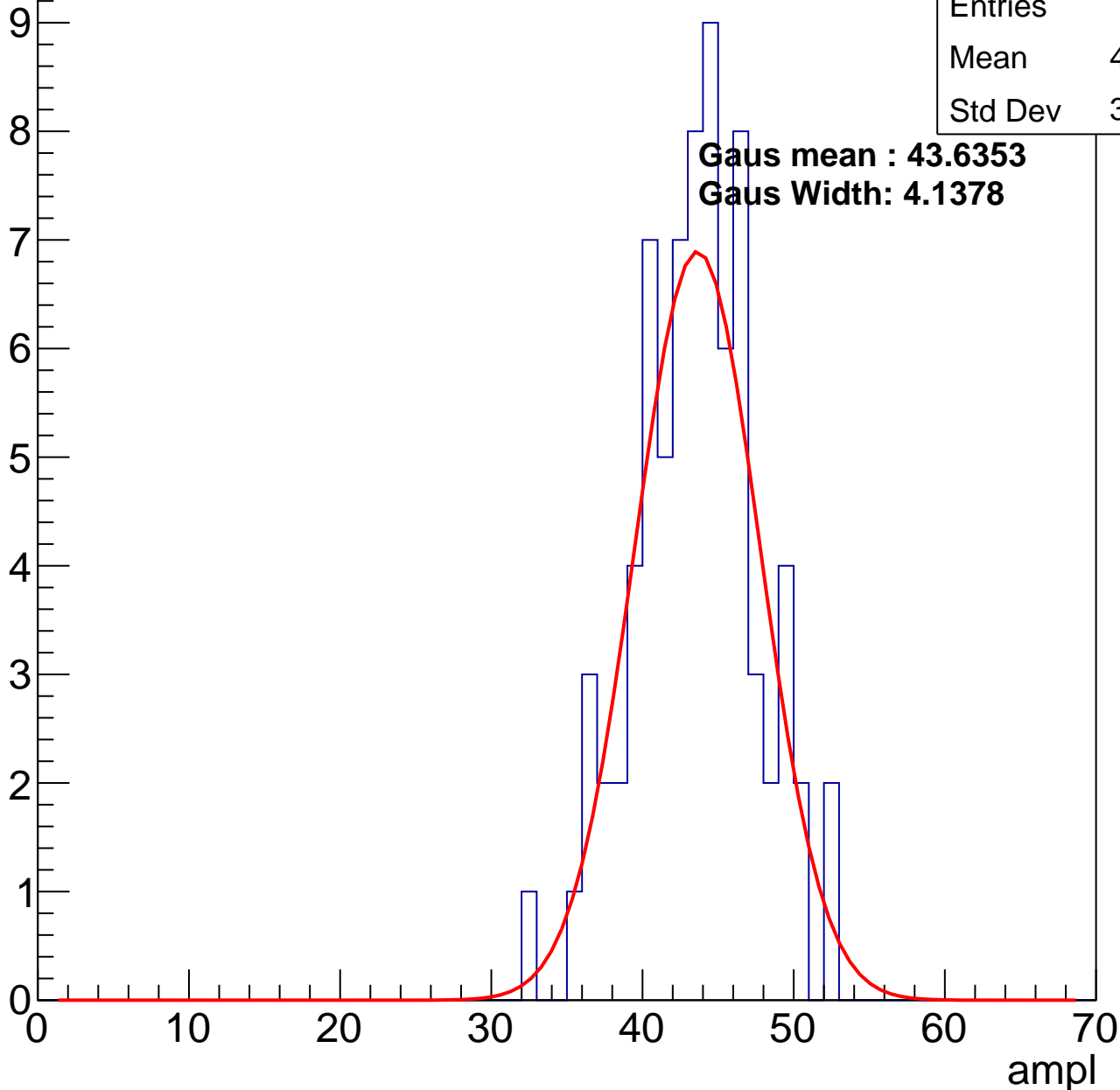
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	43.09
Std Dev	3.997

**Gaus mean : 43.6353**

**Gaus Width: 4.1378**

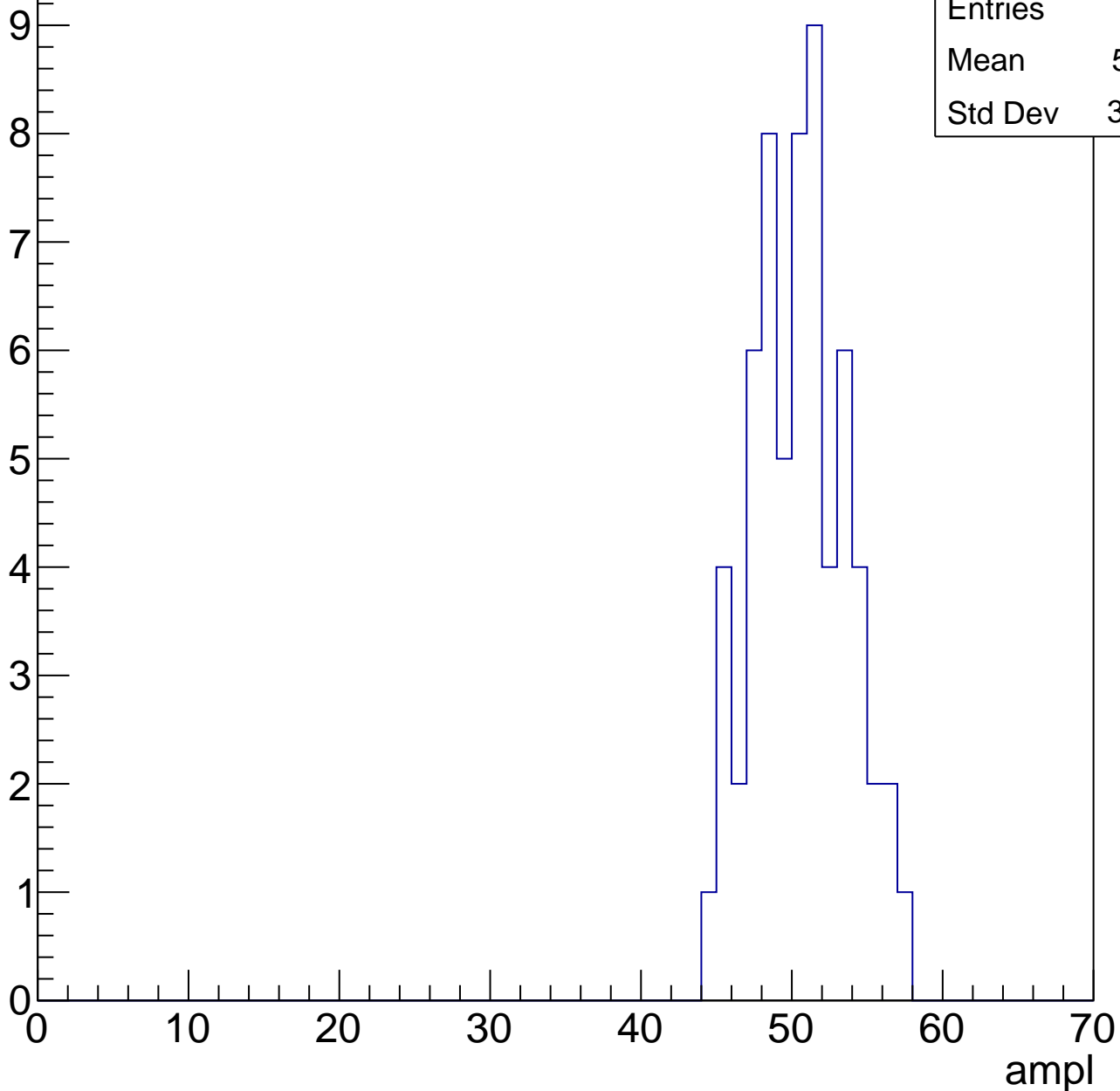


# B0L000S, U26-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.11
Std Dev	3.038

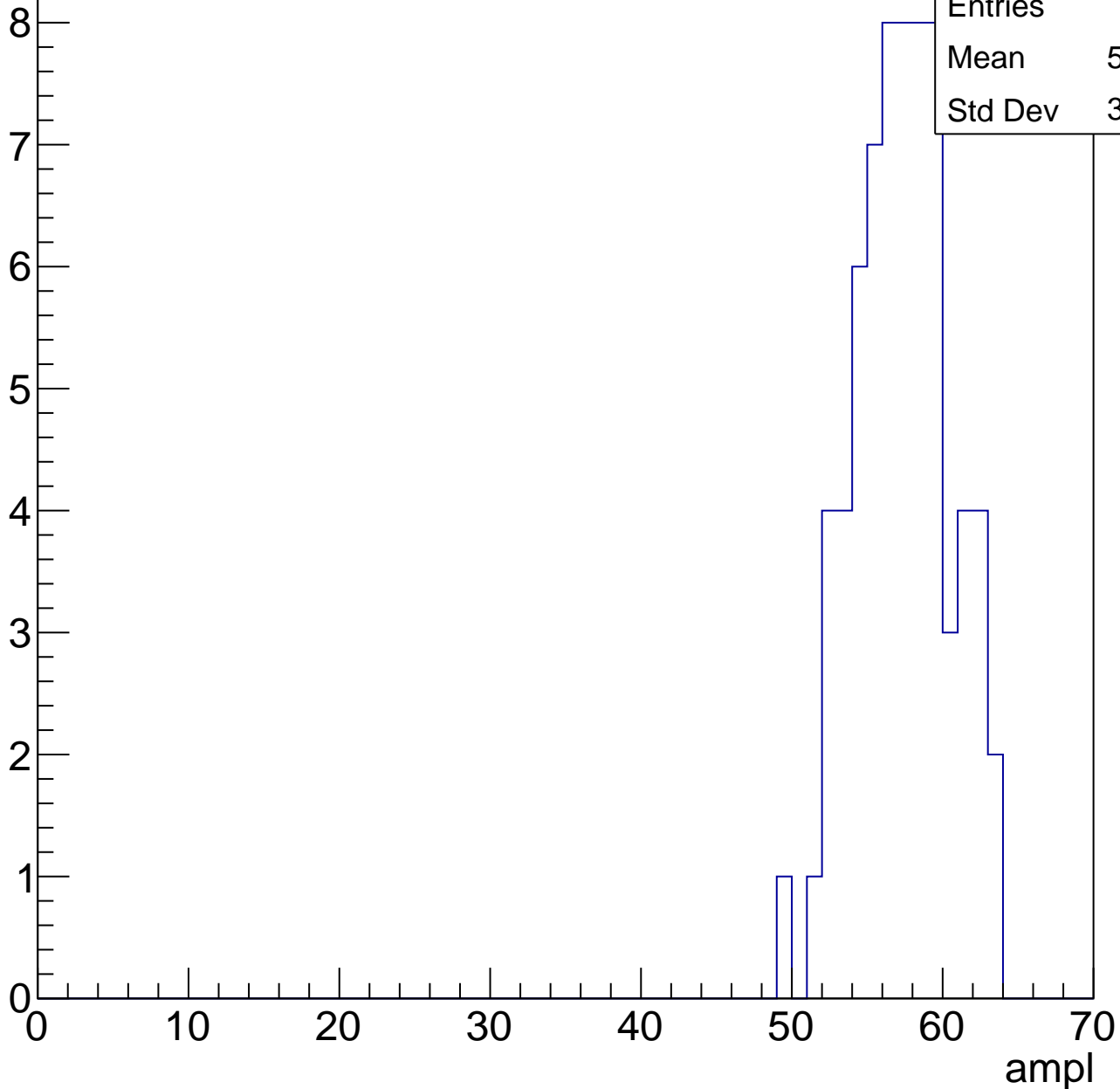


# B0L000S, U26-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

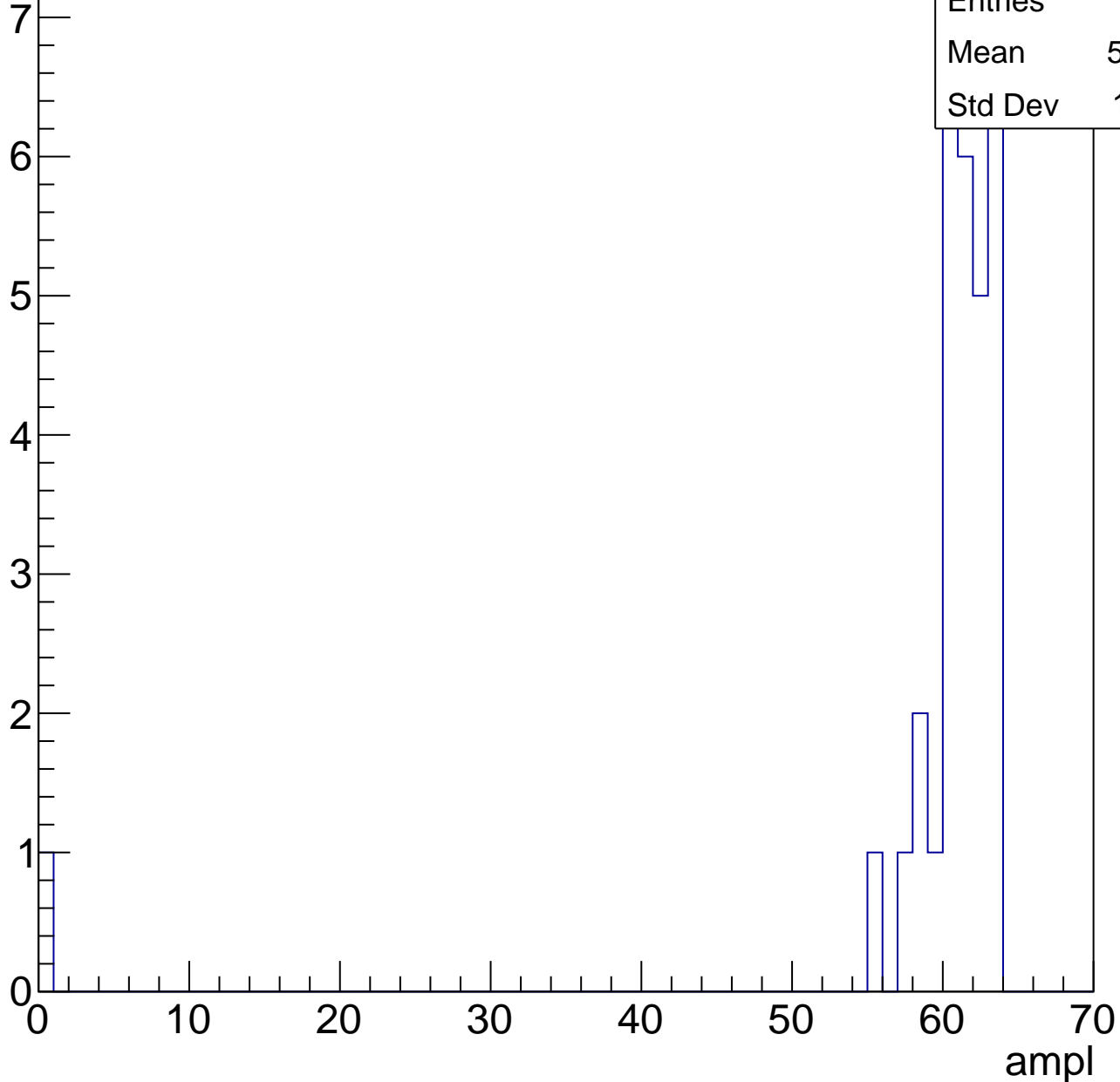
Entries	68
Mean	56.87
Std Dev	3.106



# B0L000S, U26-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	28.63
Std Dev	5.607

**Gaus mean : 29.6989**

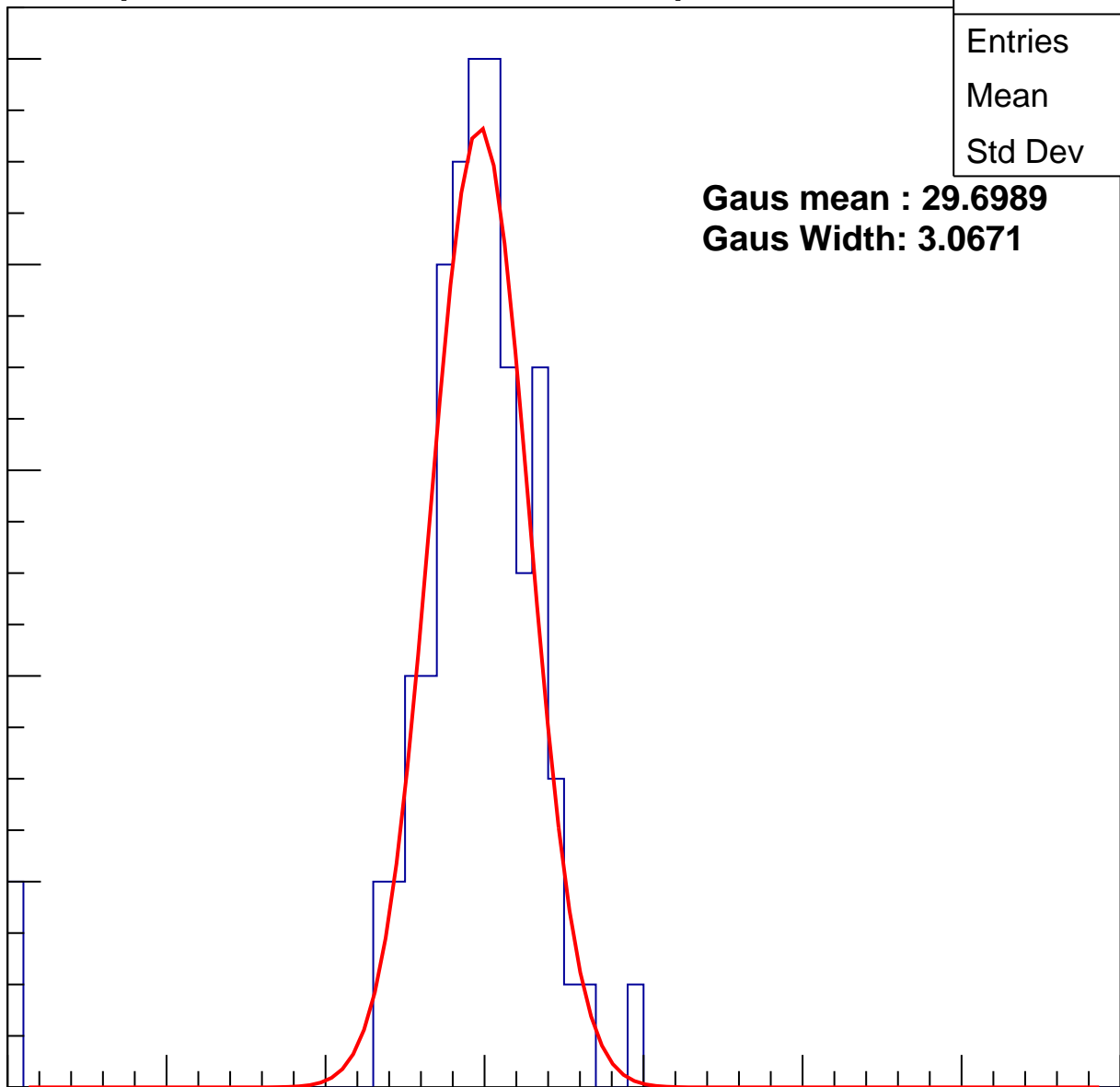
**Gaus Width: 3.0671**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch19, adc1

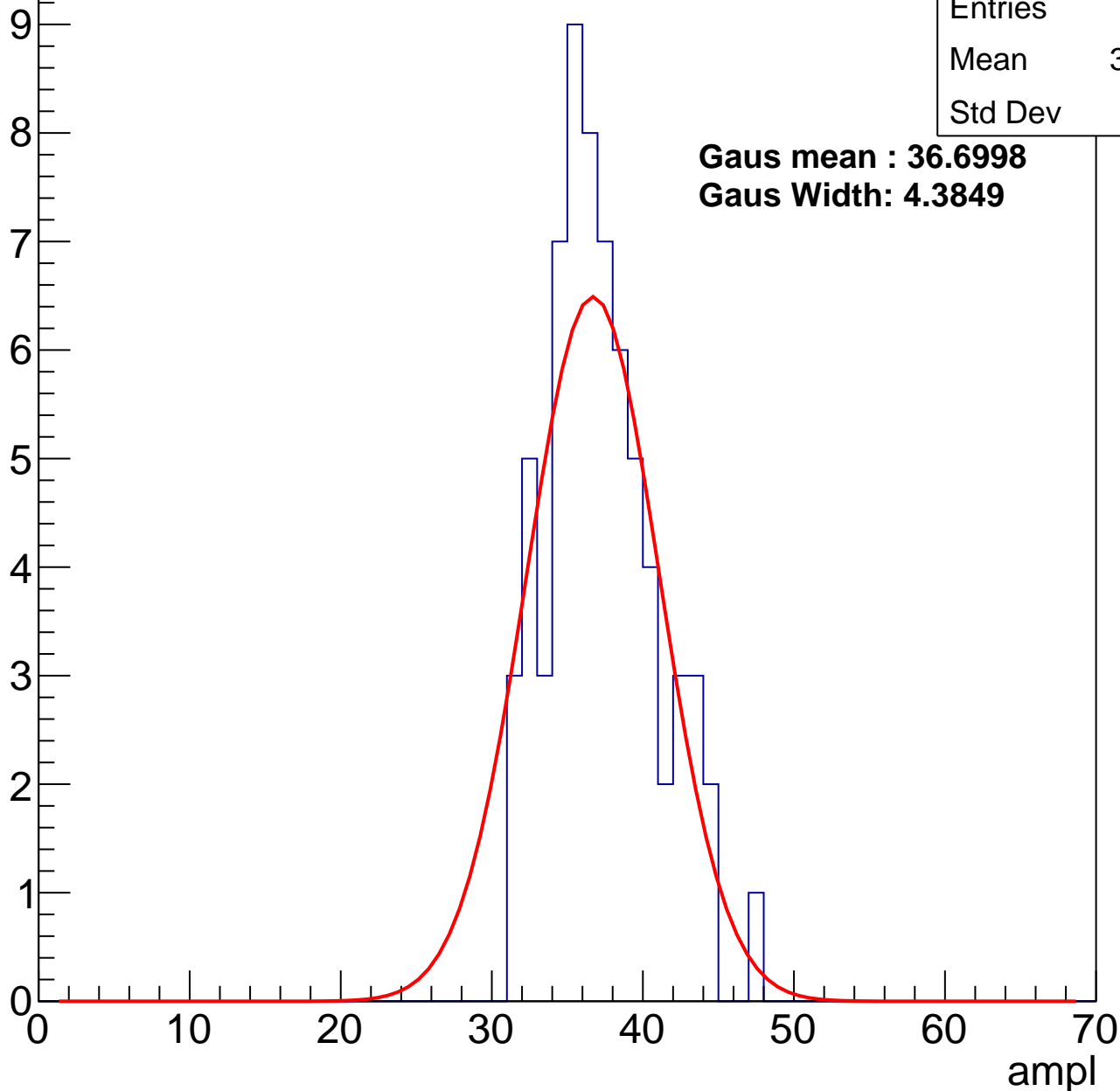
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.87
Std Dev	3.56

**Gaus mean : 36.6998**

**Gaus Width: 4.3849**



# B0L000S, U26-ch19, adc2

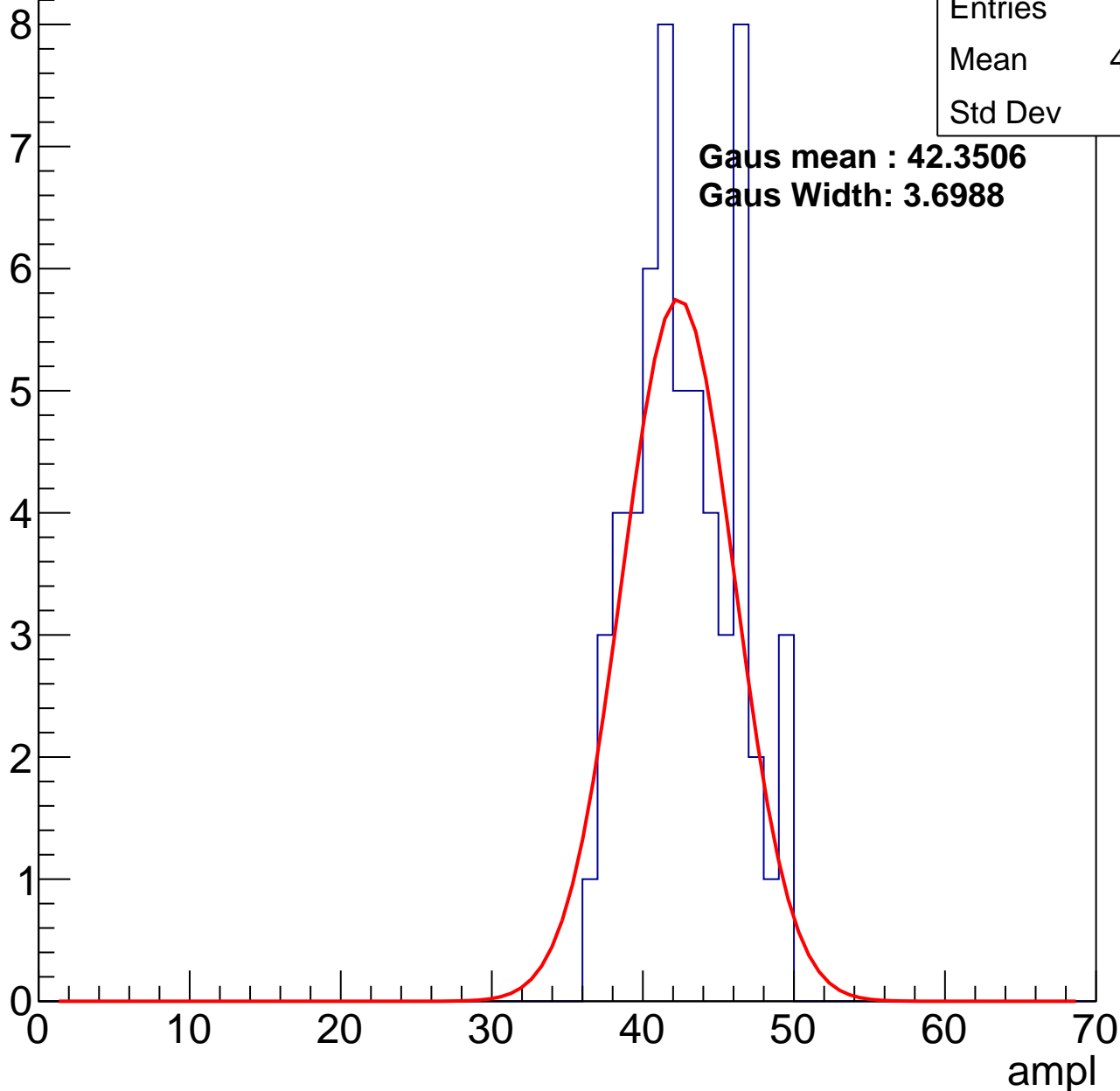
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.39
Std Dev	3.36

**Gaus mean : 42.3506**

**Gaus Width: 3.6988**

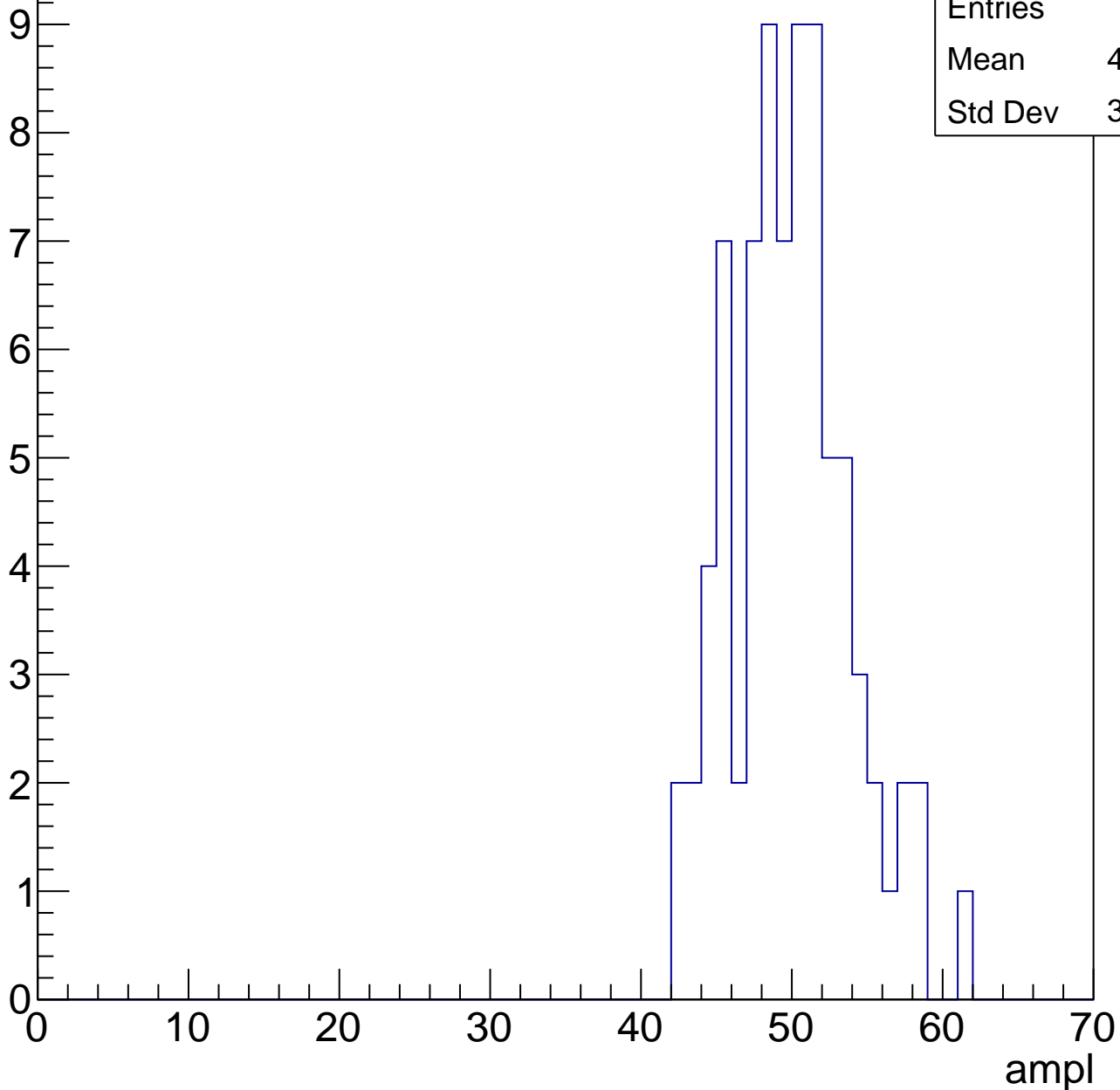


# B0L000S, U26-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	49.49
Std Dev	3.926



# B0L000S, U26-ch19, adc4

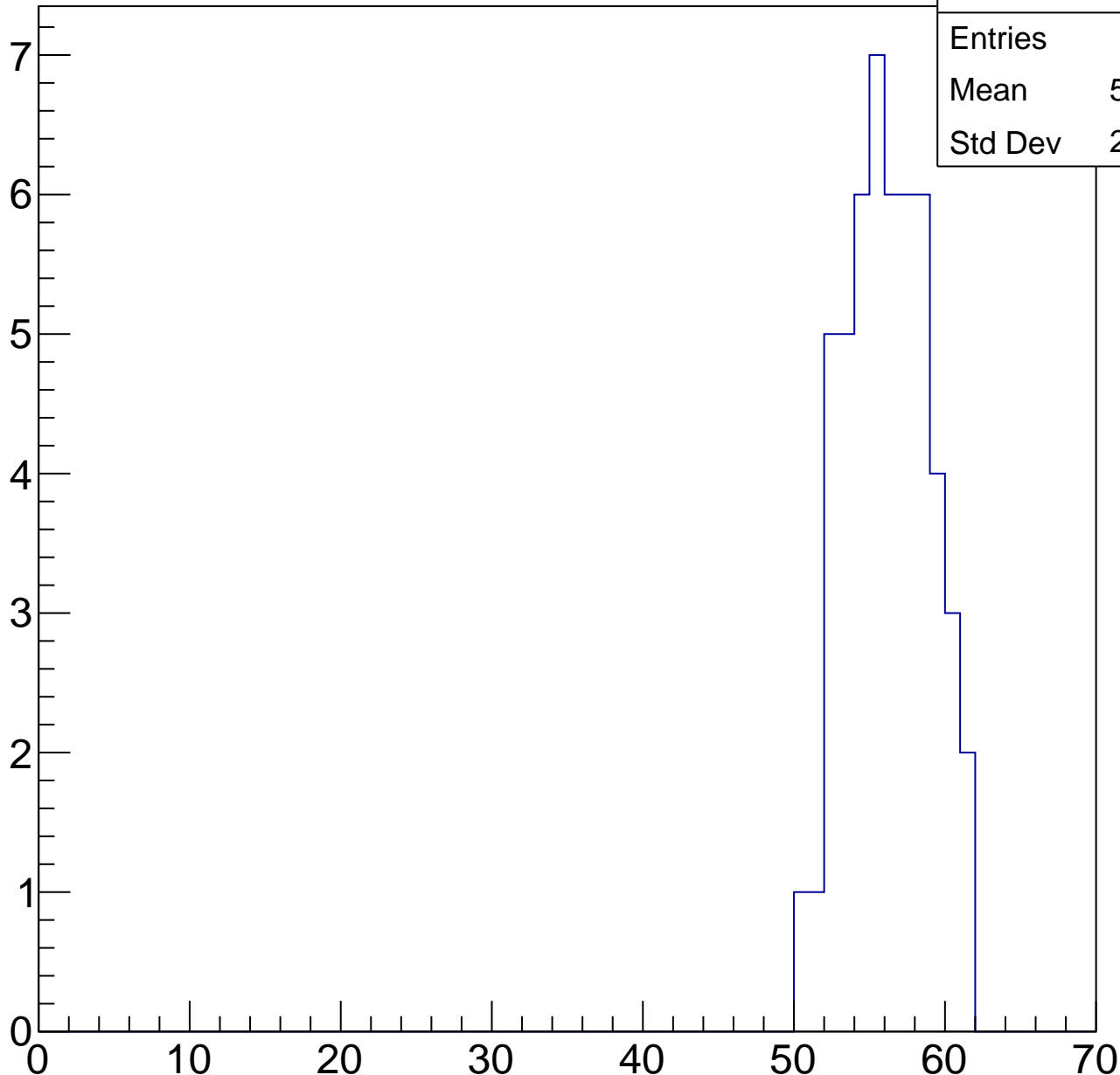
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	55.75
Std Dev	2.695

ampl

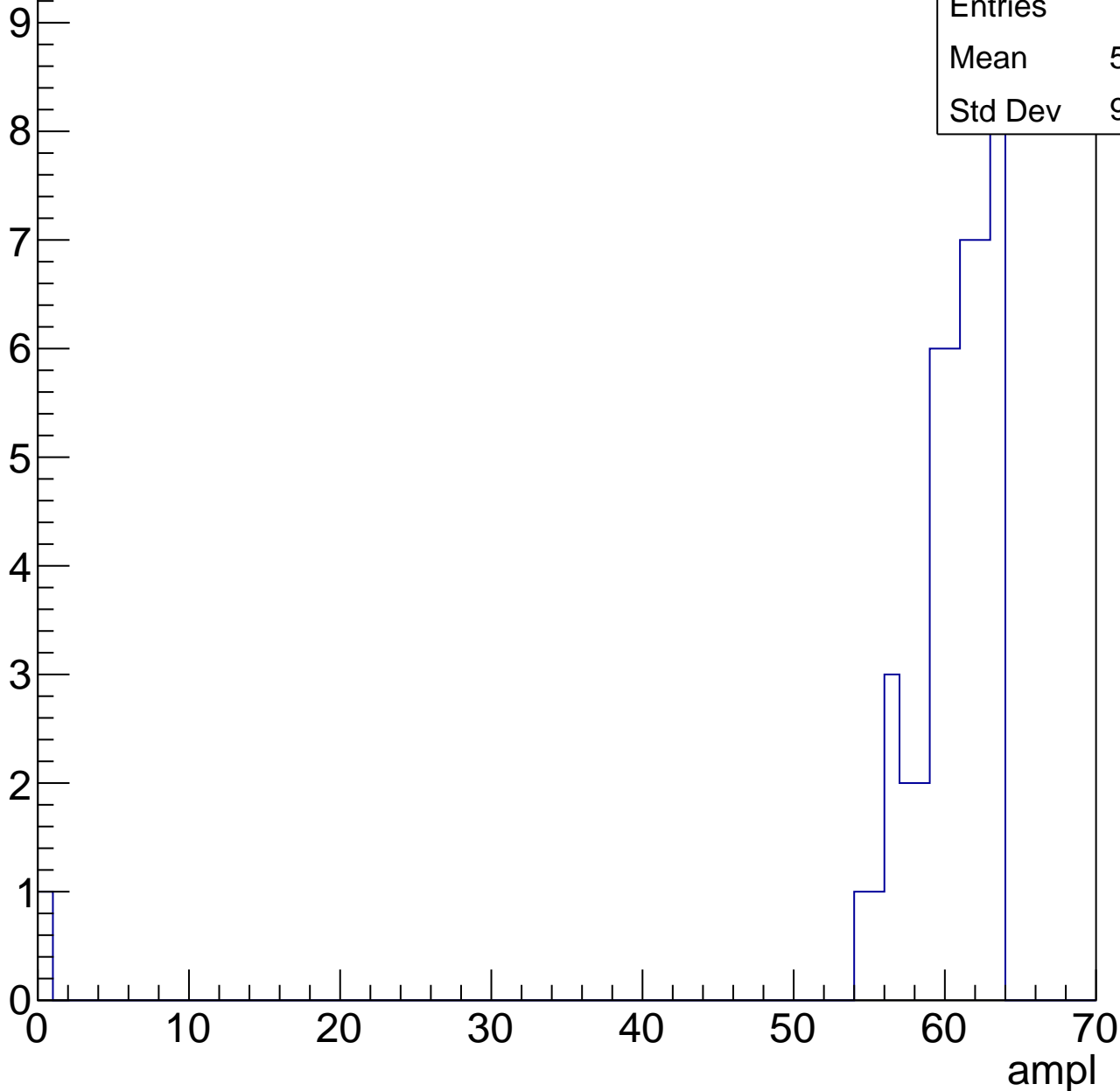


# B0L000S, U26-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

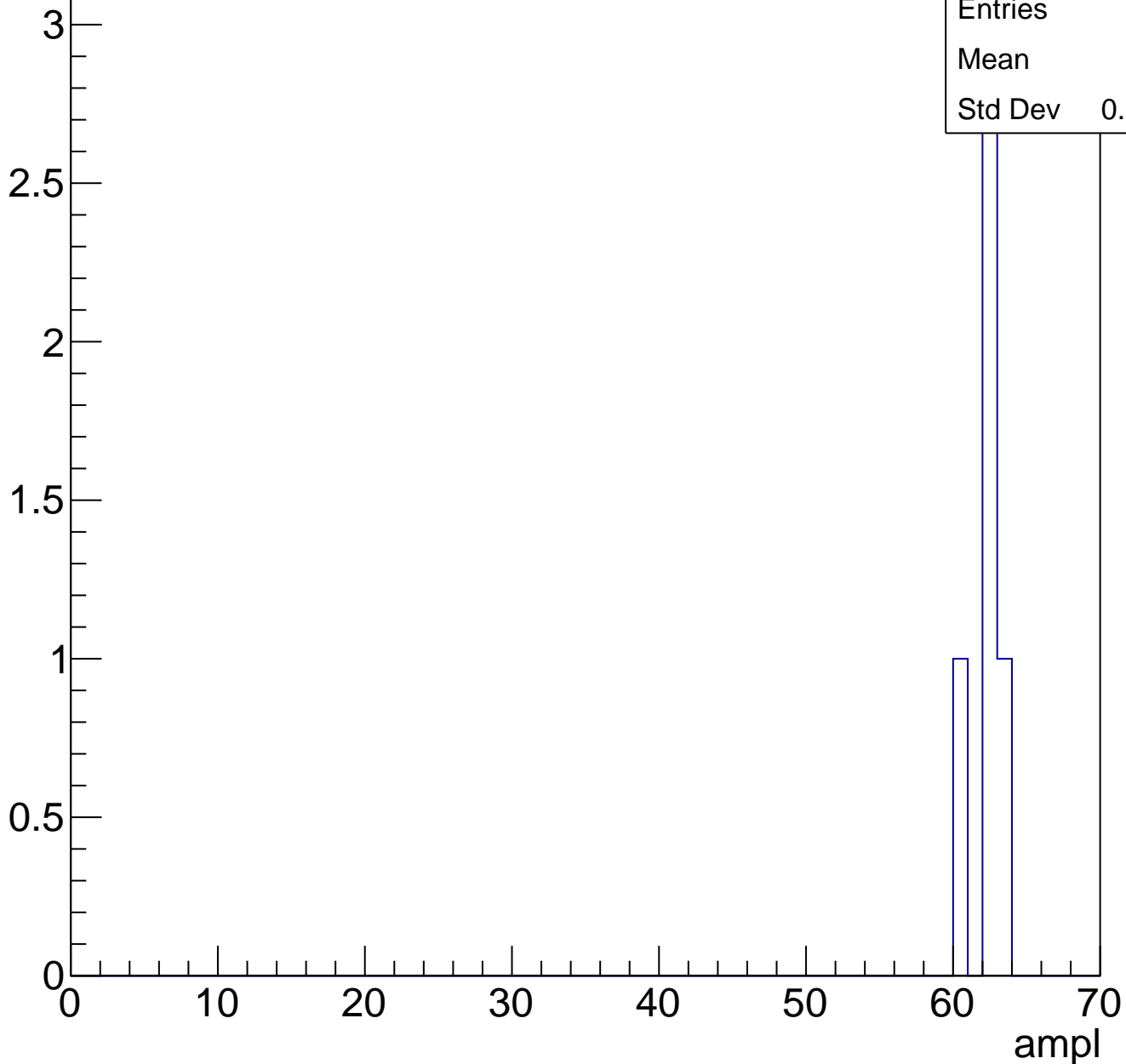
Entries	45
Mean	58.87
Std Dev	9.188



# B0L000S, U26-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch20, adc0

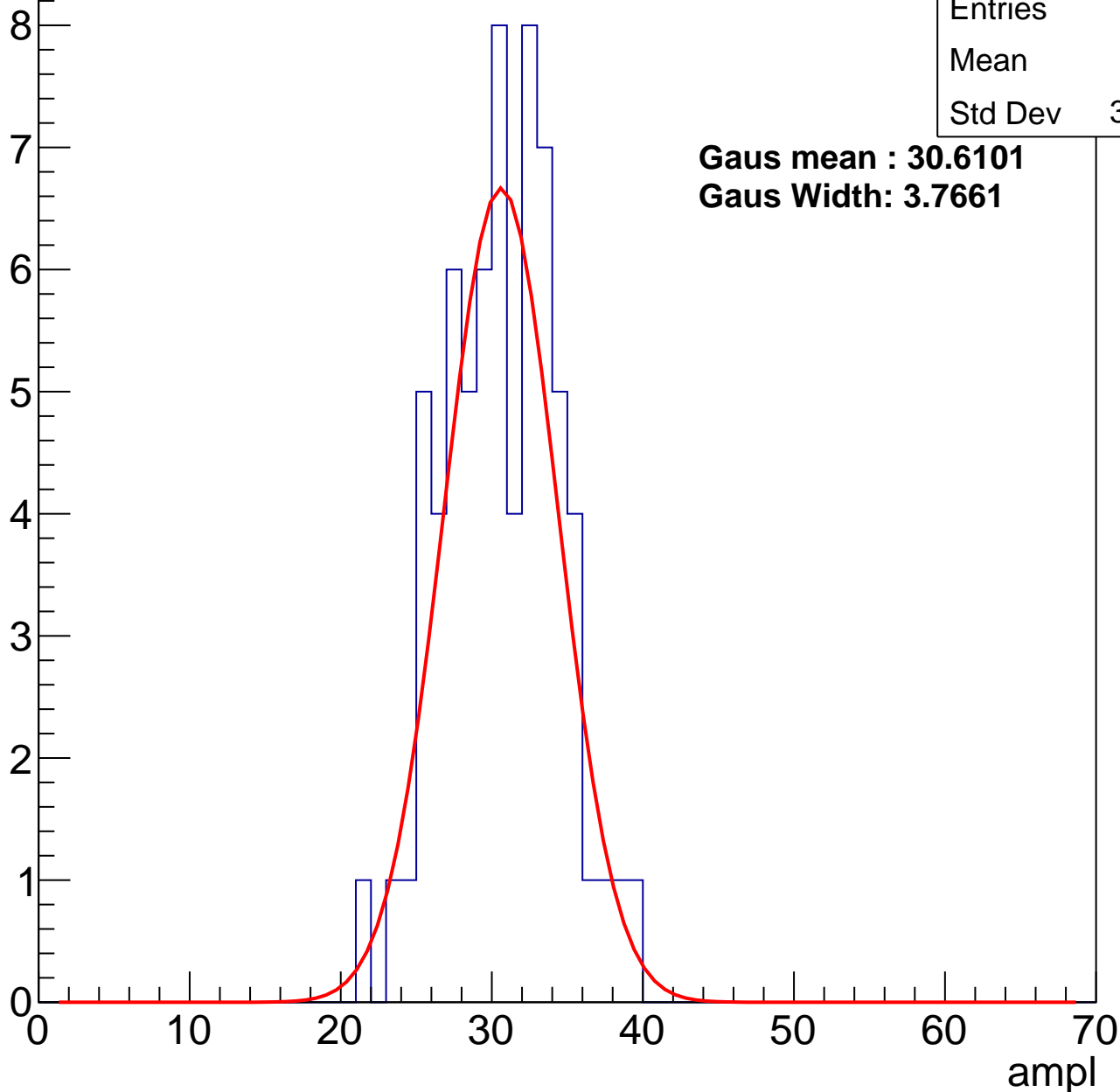
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.2
Std Dev	3.693

**Gaus mean : 30.6101**

**Gaus Width: 3.7661**



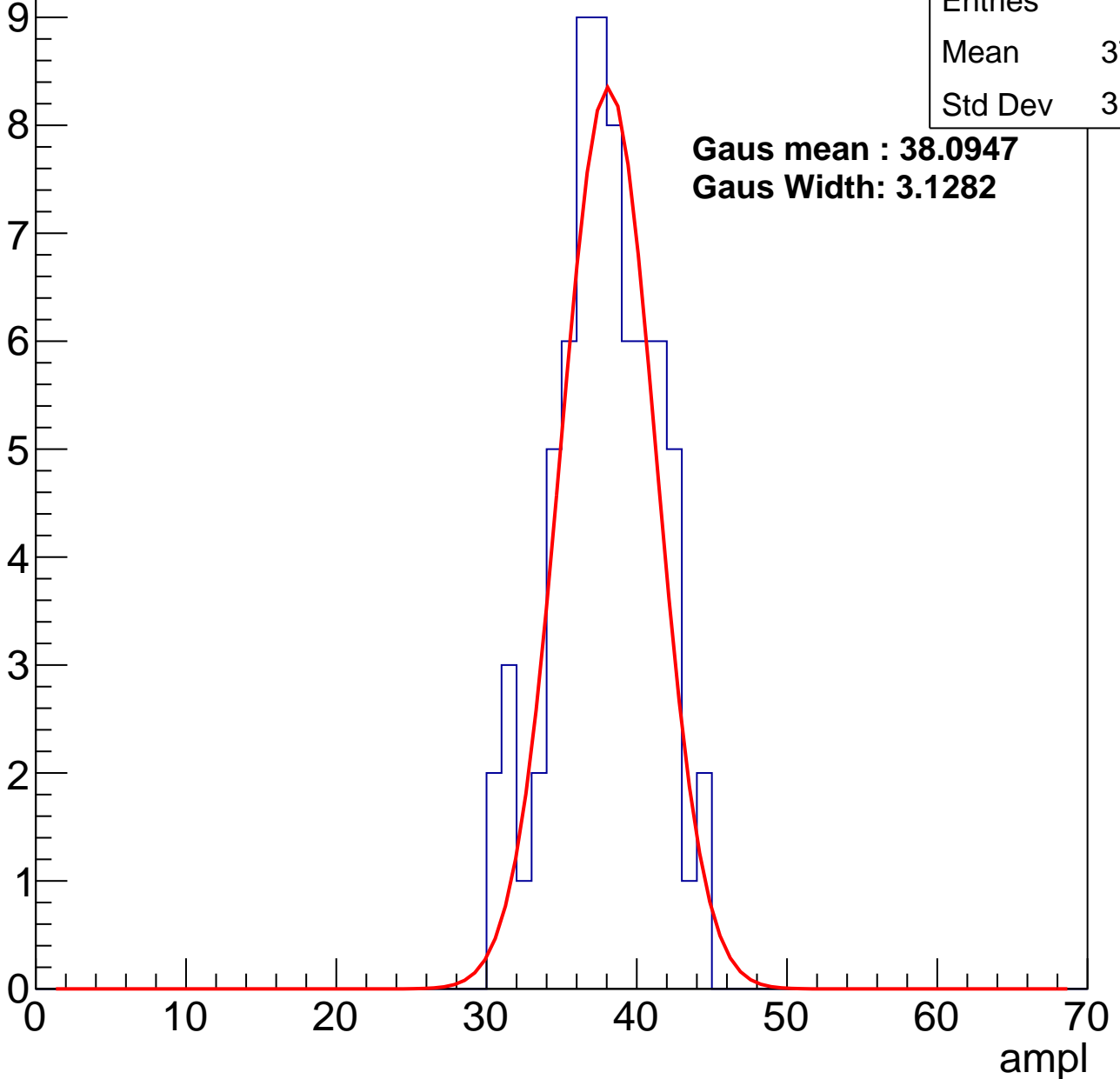
# B0L000S, U26-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.37
Std Dev	3.298

**Gaus mean : 38.0947**  
**Gaus Width: 3.1282**



# B0L000S, U26-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	44.32
Std Dev	2.988

**Gaus mean : 45.0072**

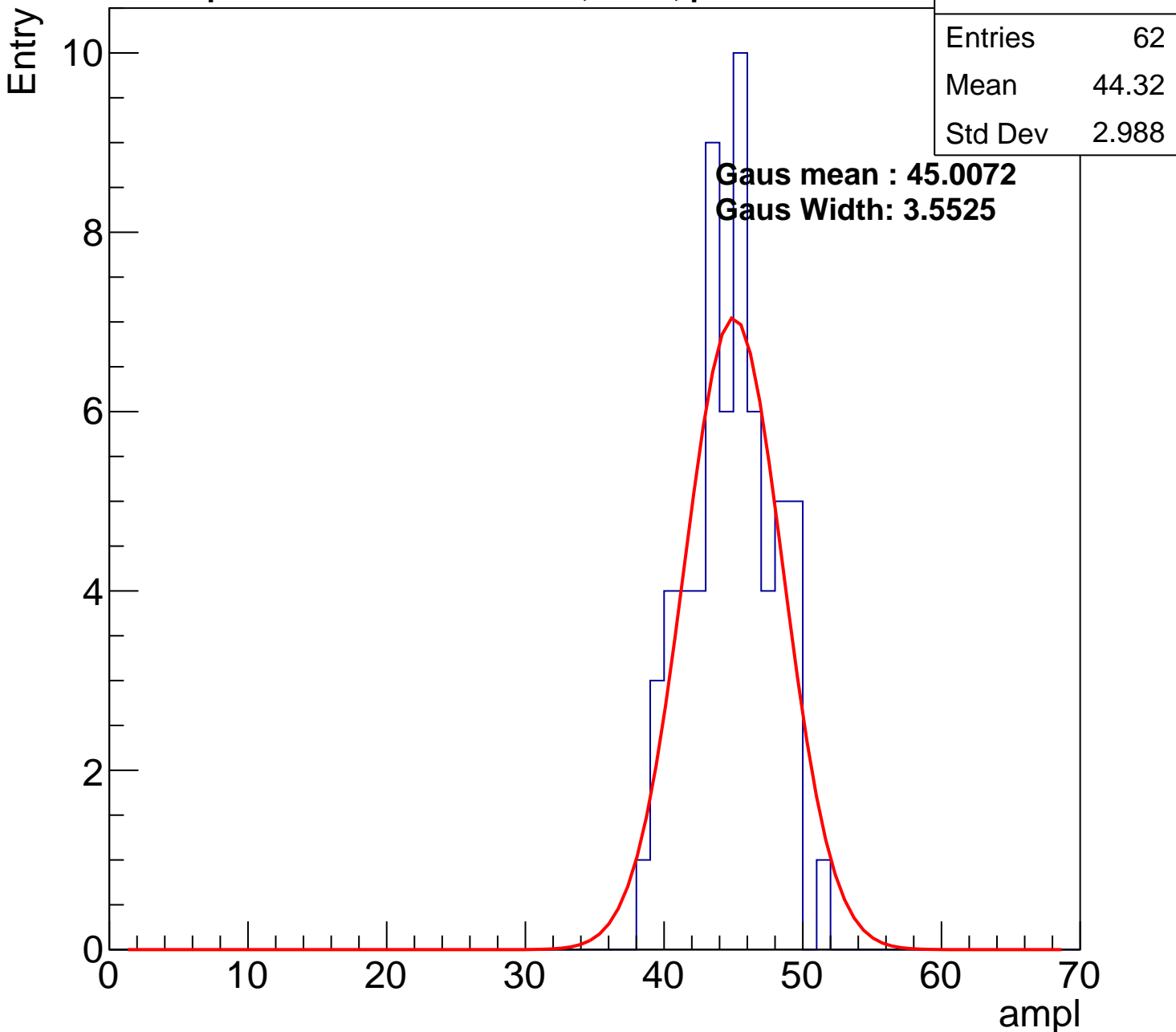
**Gaus Width: 3.5525**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

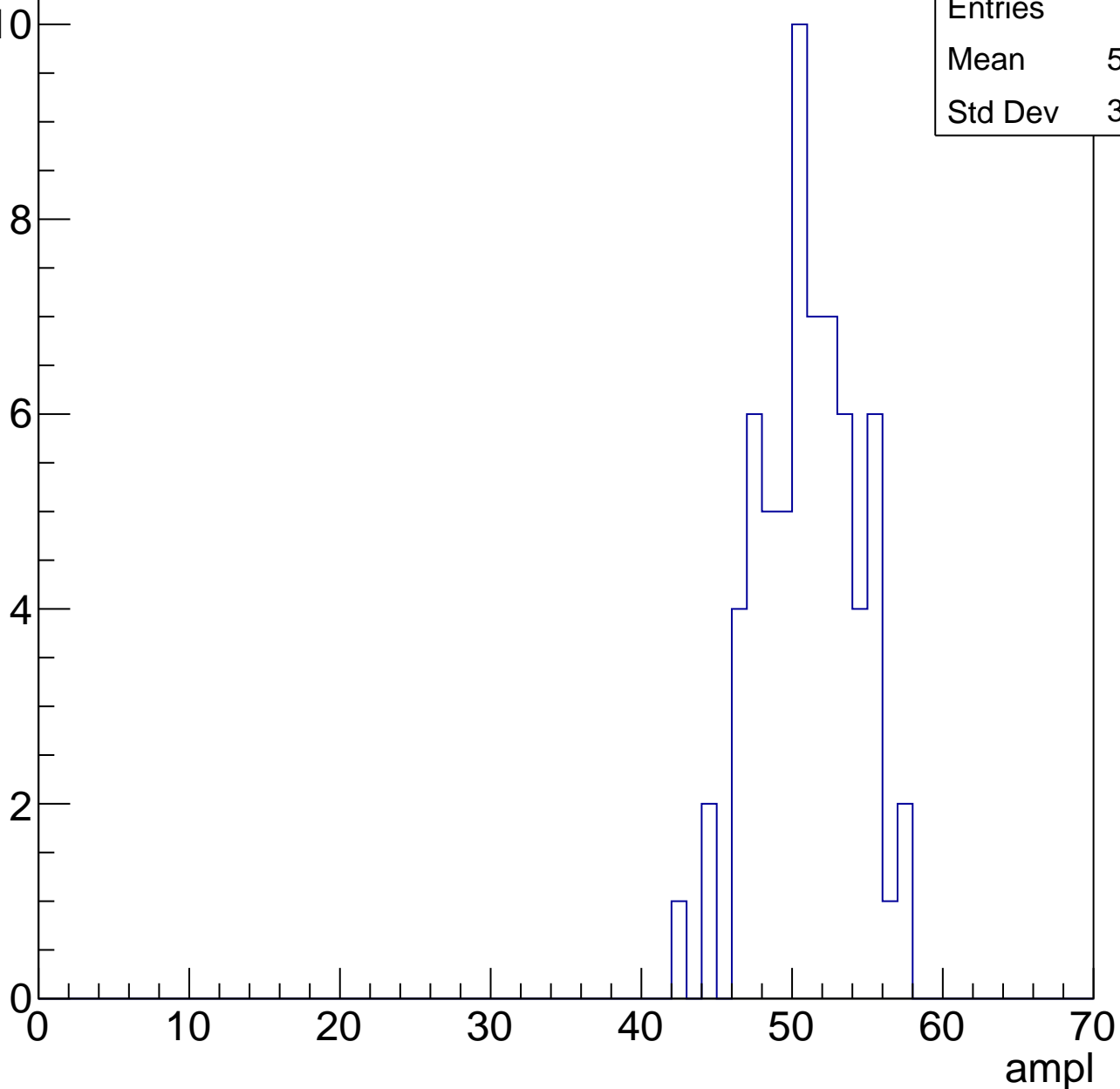


# B0L000S, U26-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.55
Std Dev	3.248

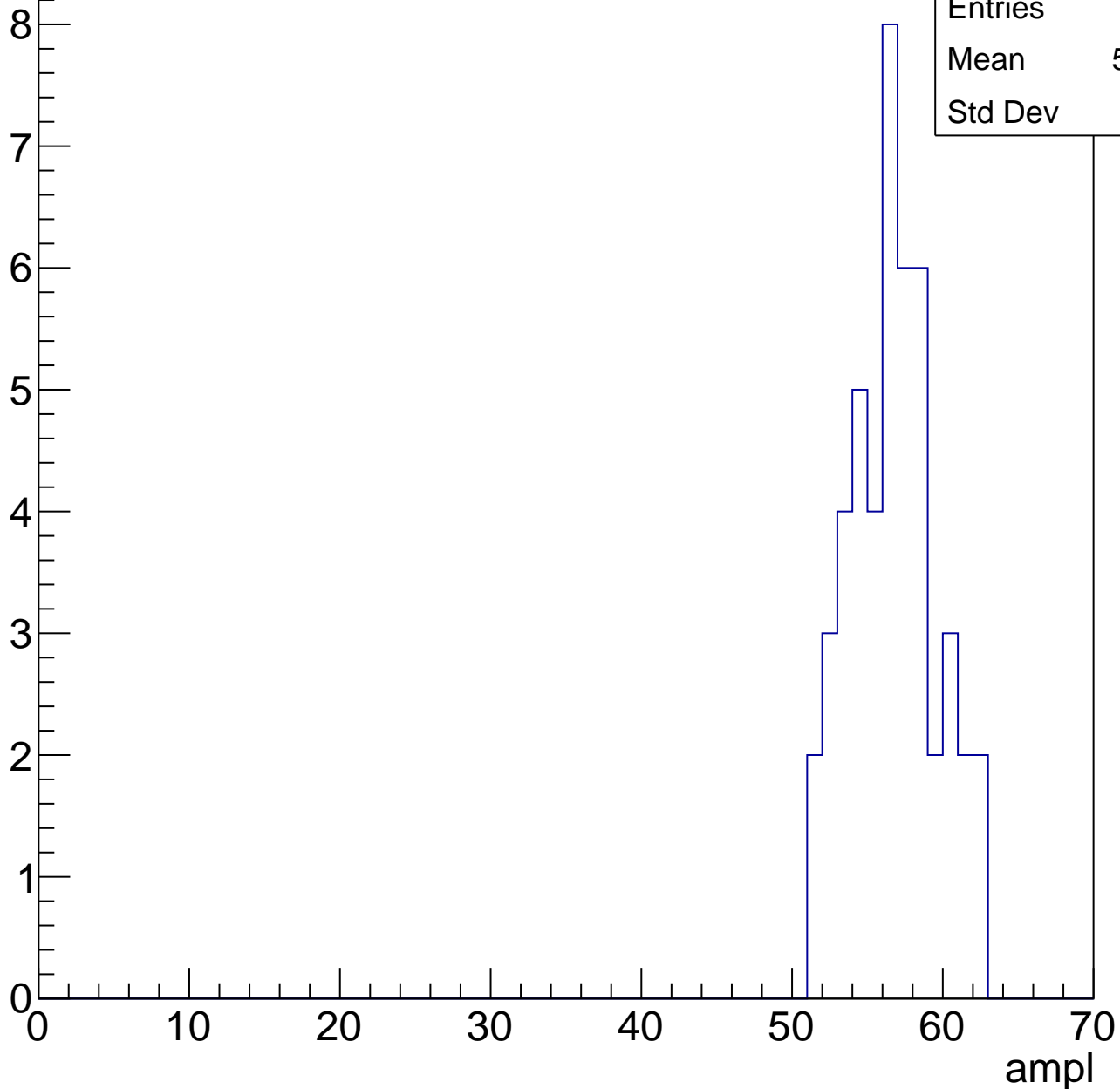


# B0L000S, U26-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

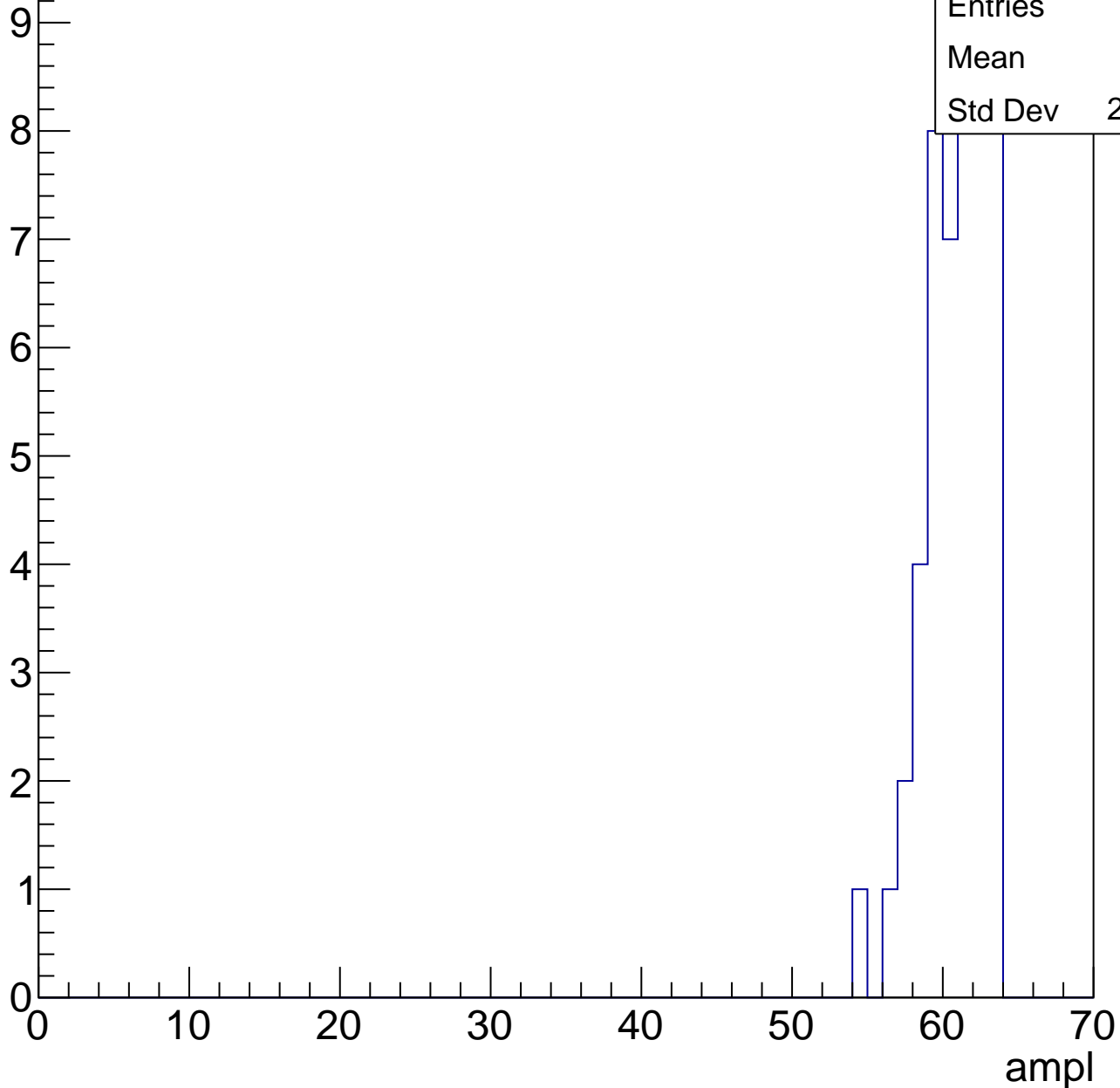
Entries	47
Mean	56.21
Std Dev	2.82



# B0L000S, U26-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch21, adc0

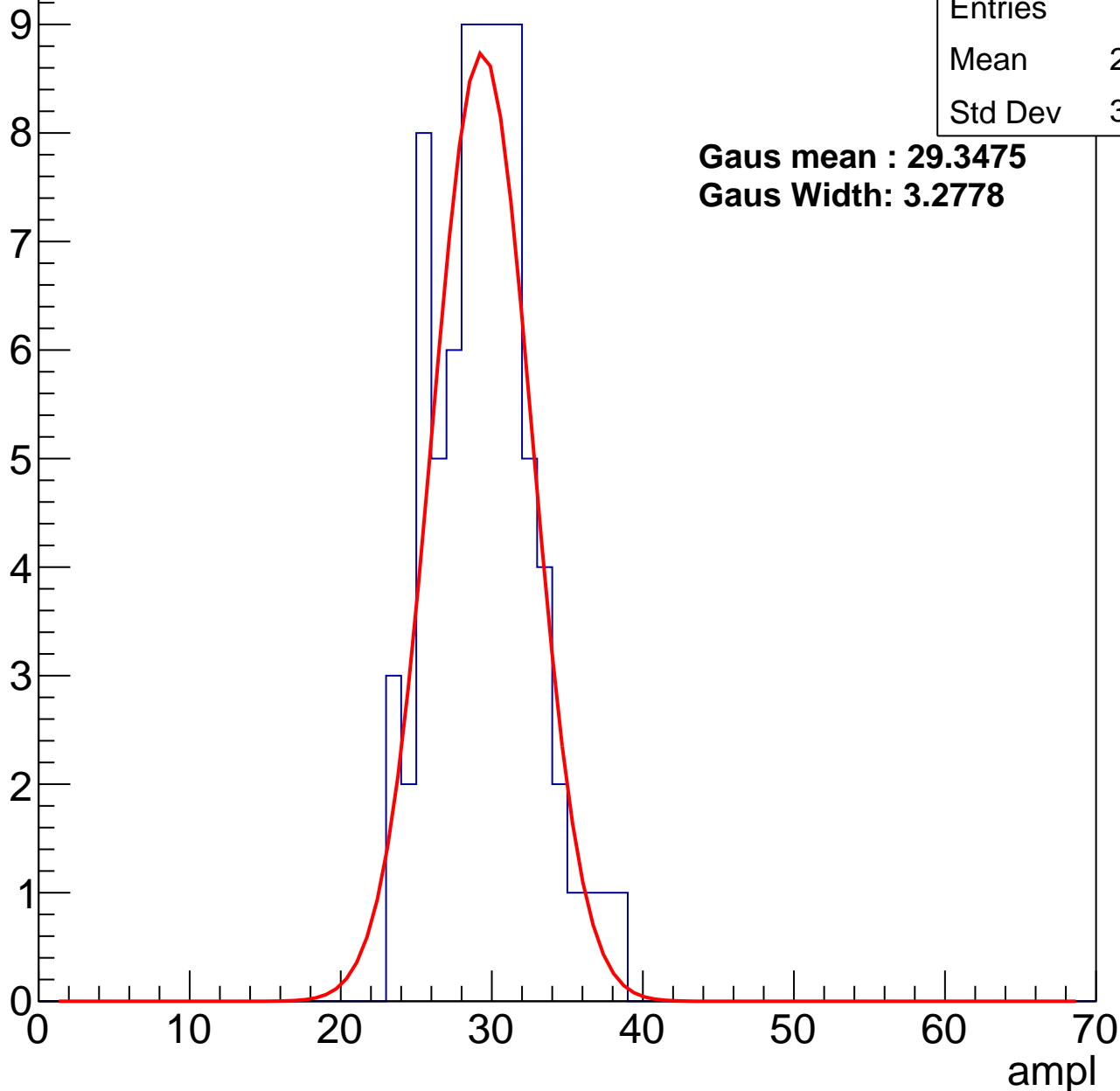
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.03
Std Dev	3.262

**Gaus mean : 29.3475**

**Gaus Width: 3.2778**



# B0L000S, U26-ch21, adc1

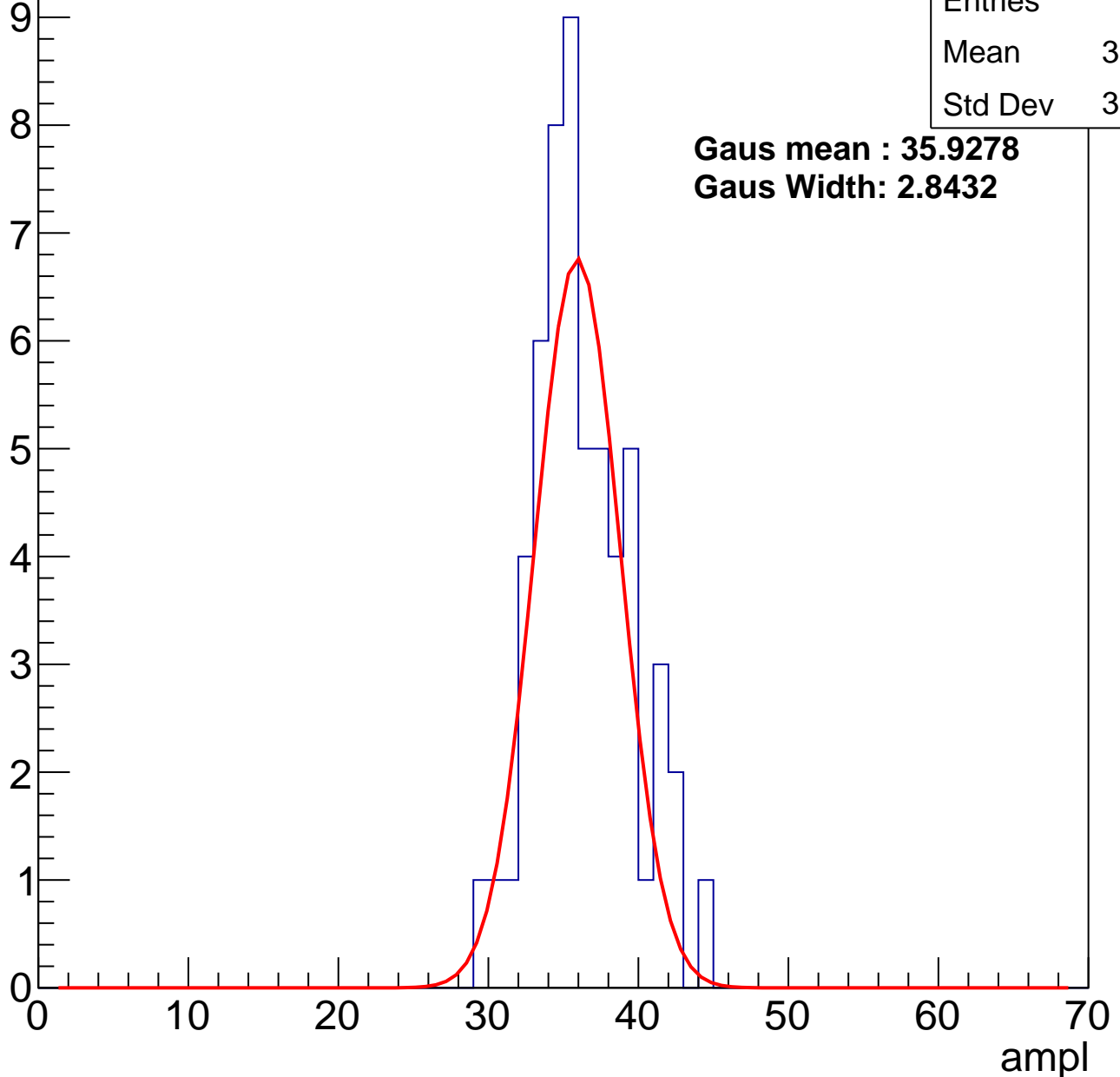
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	35.82
Std Dev	3.163

**Gaus mean : 35.9278**

**Gaus Width: 2.8432**



# B0L000S, U26-ch21, adc2

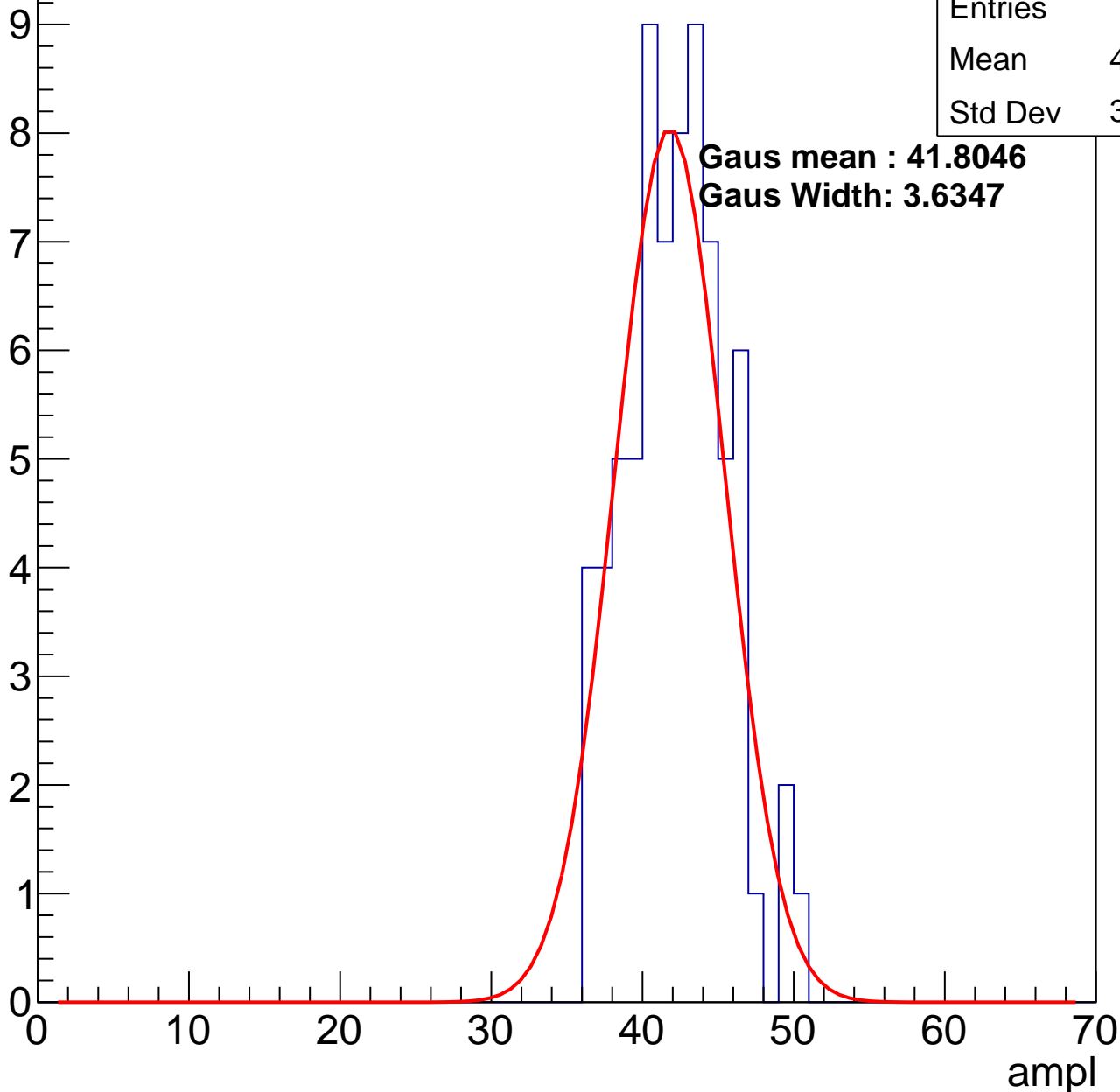
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	41.79
Std Dev	3.256

**Gaus mean : 41.8046**

**Gaus Width: 3.6347**



# B0L000S, U26-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

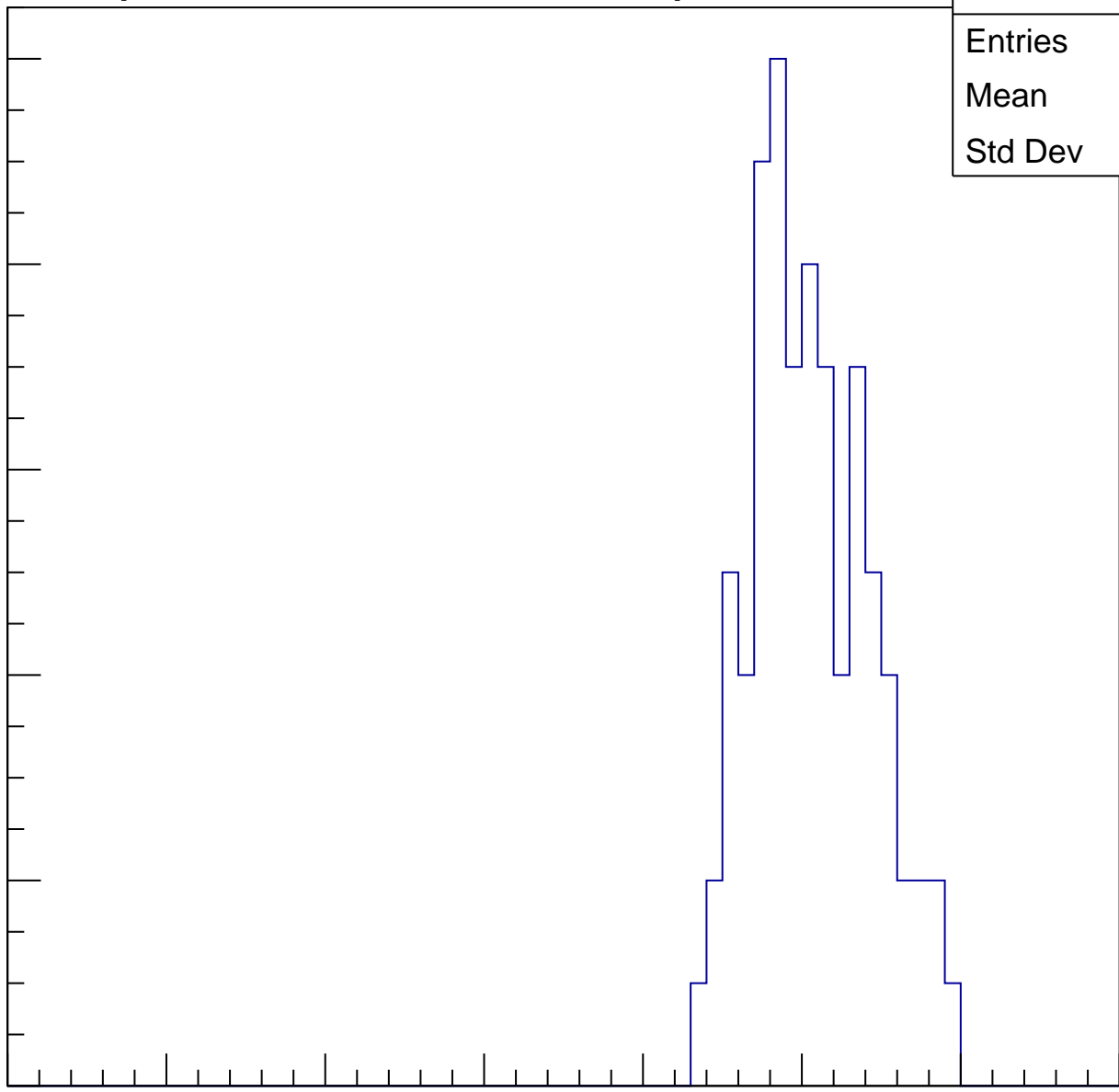
Entries	80
Mean	50.16
Std Dev	3.676

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

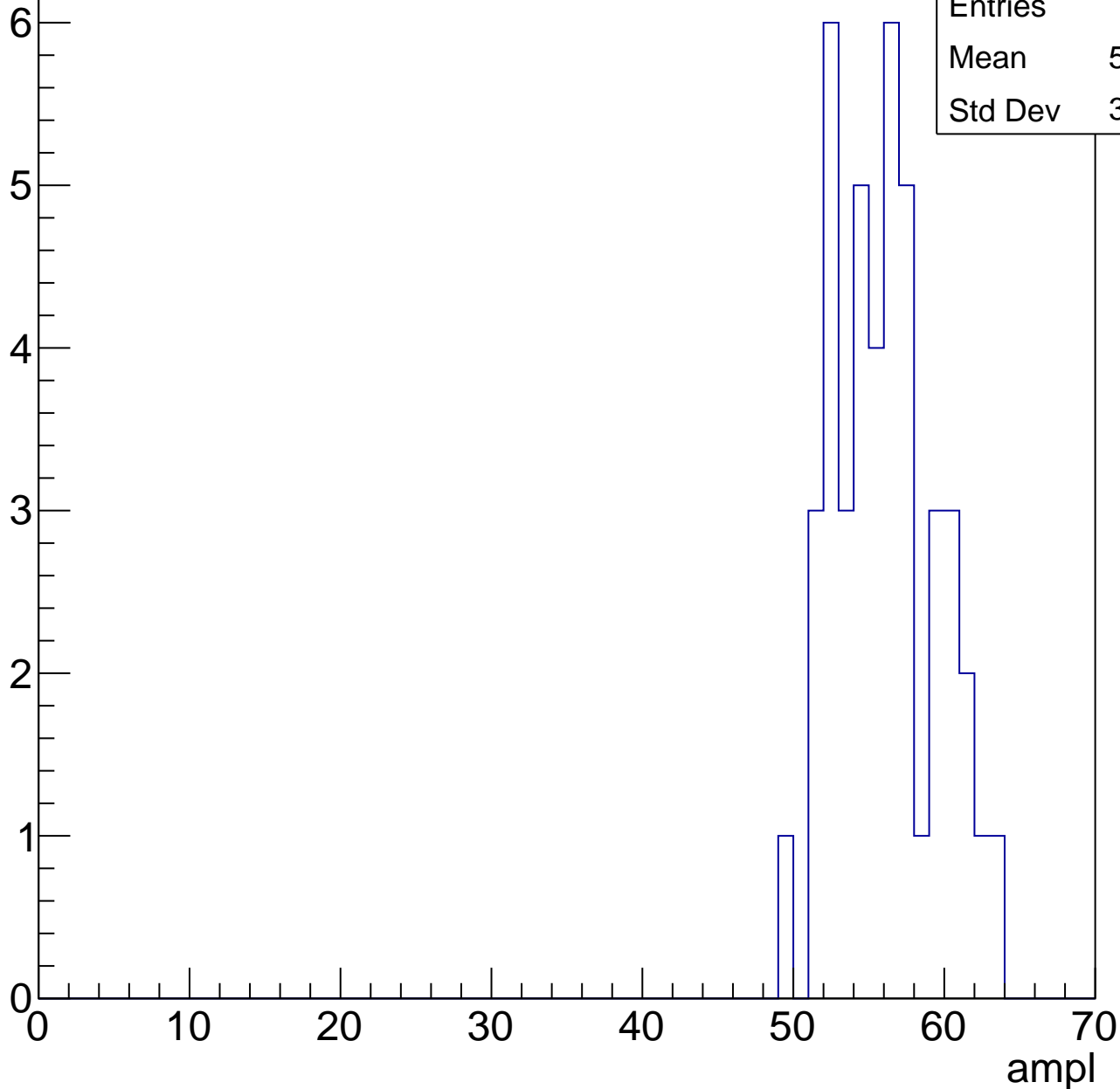


# B0L000S, U26-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	55.59
Std Dev	3.312

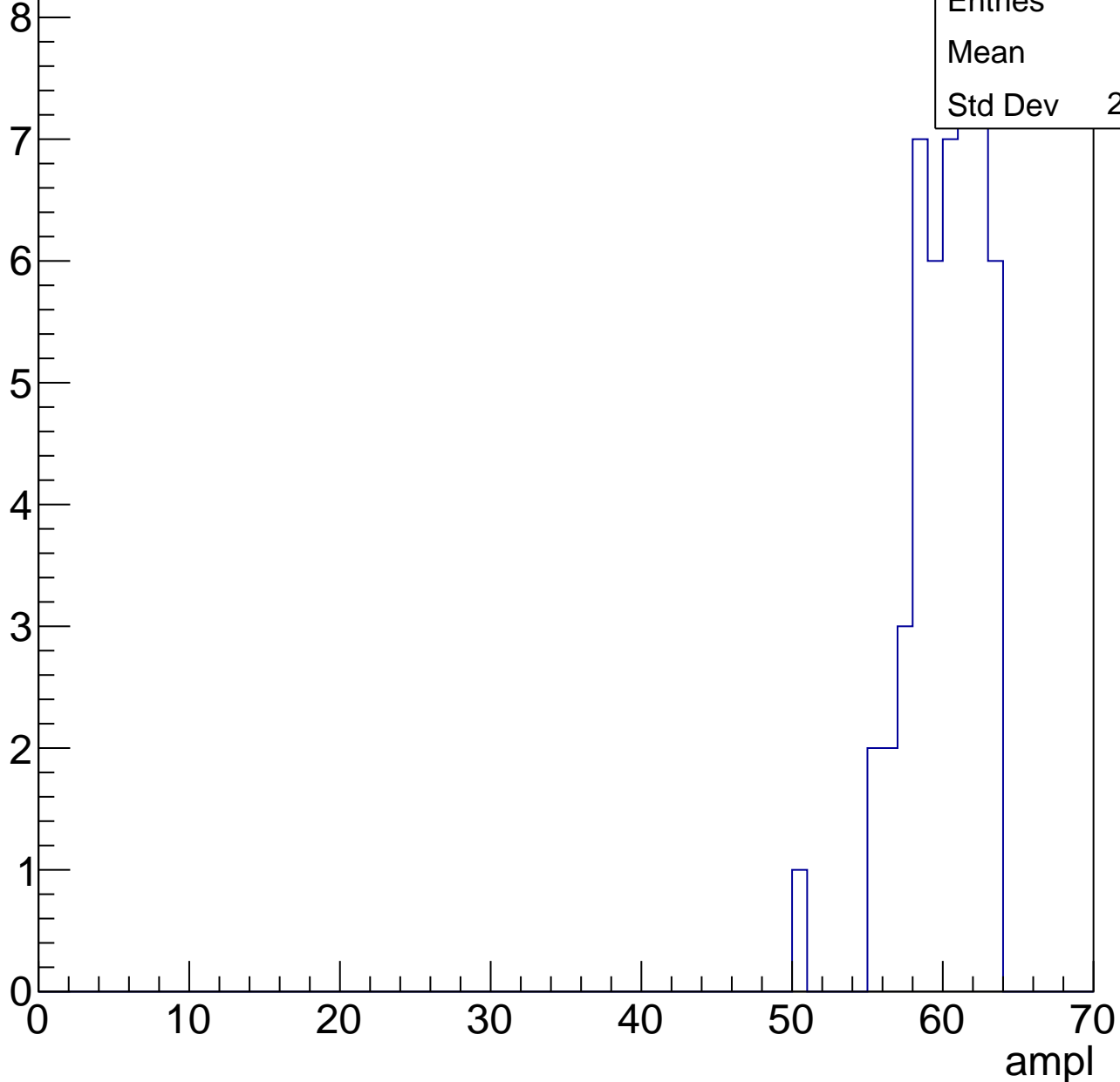


# B0L000S, U26-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	59.7
Std Dev	2.579



# B0L000S, U26-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U26-ch22, adc0

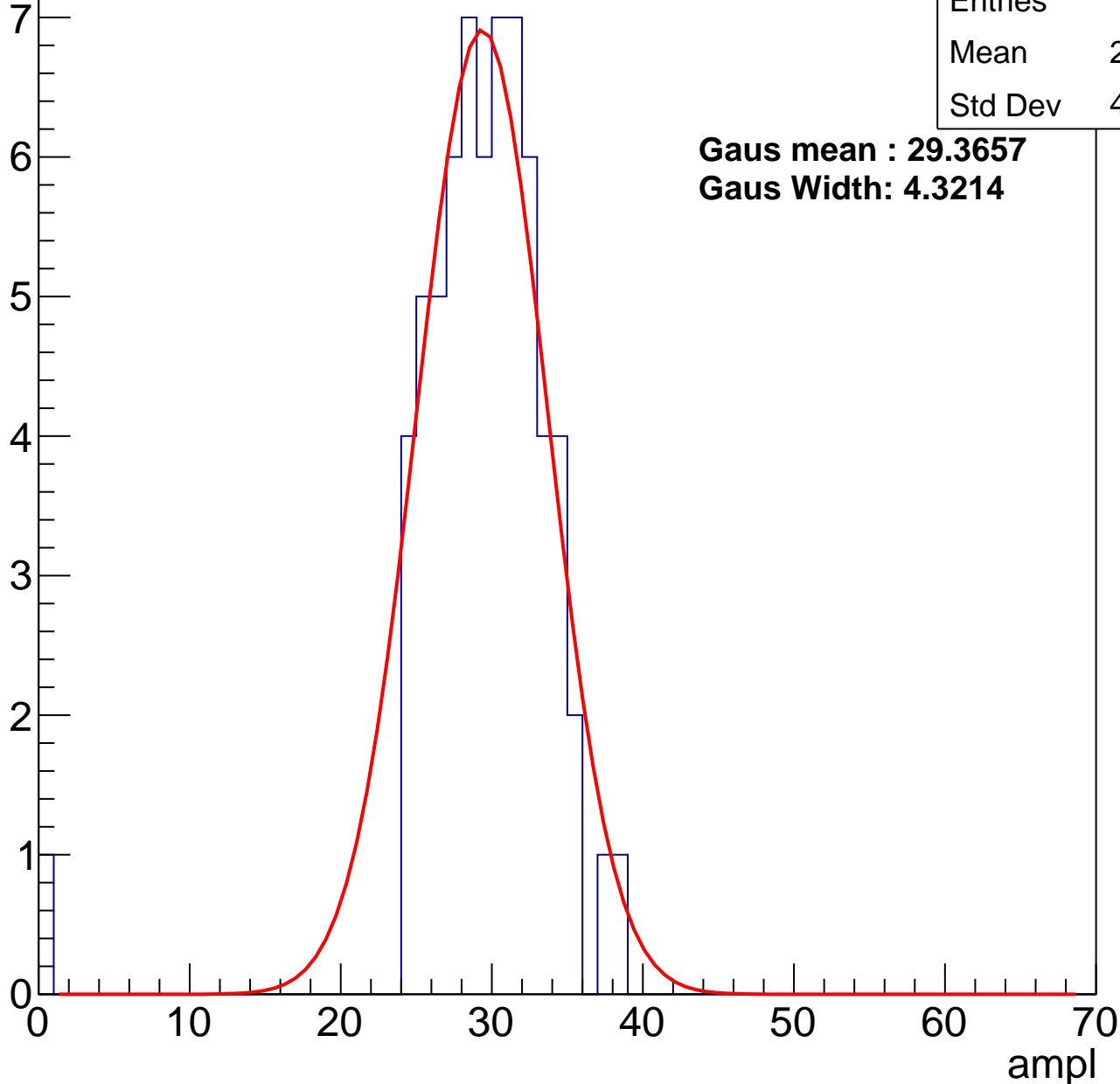
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.02
Std Dev	4.873

**Gaus mean : 29.3657**

**Gaus Width: 4.3214**



# B0L000S, U26-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	35.9
Std Dev	3.322

**Gaus mean : 36.3464**

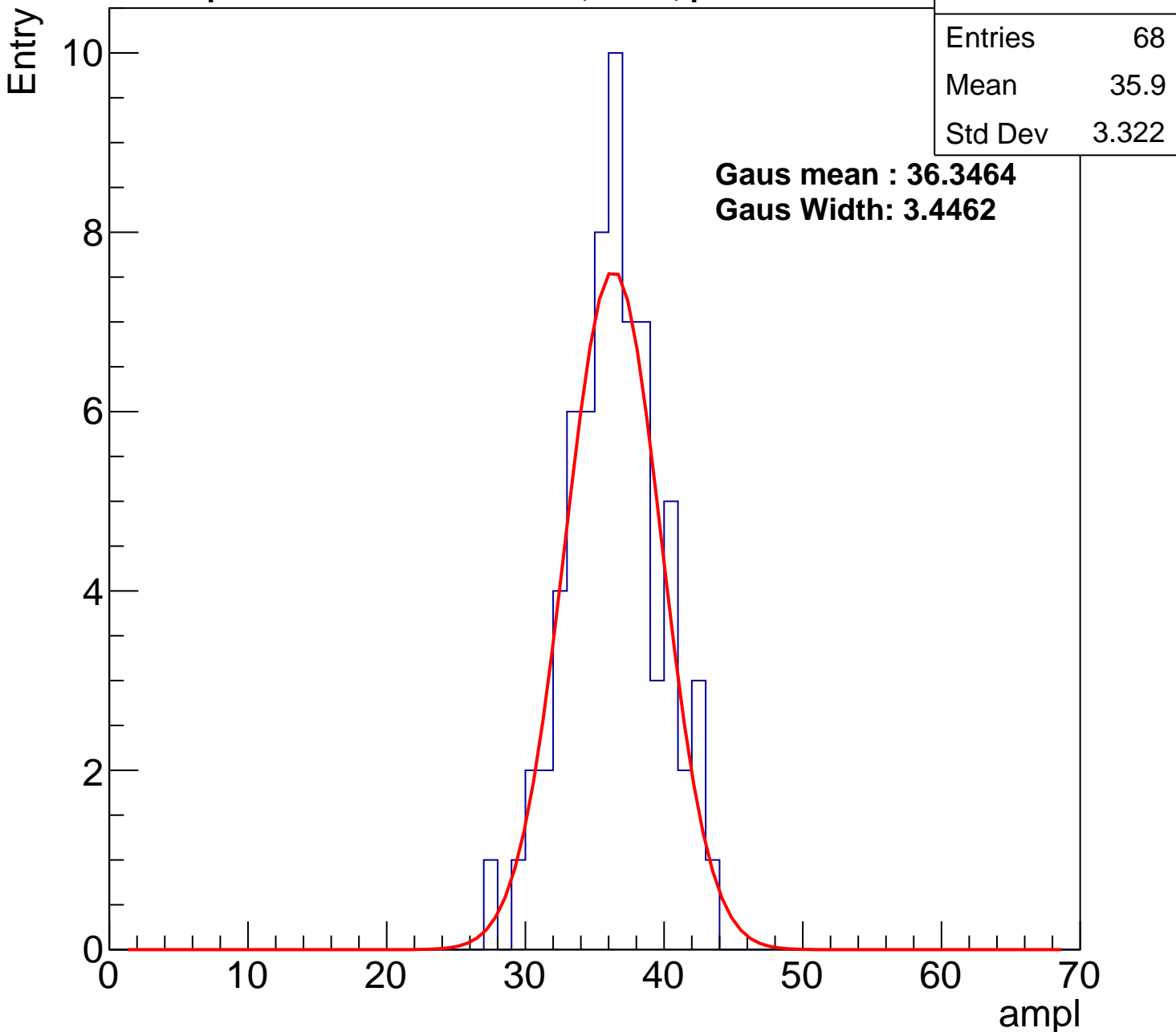
**Gaus Width: 3.4462**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch22, adc2

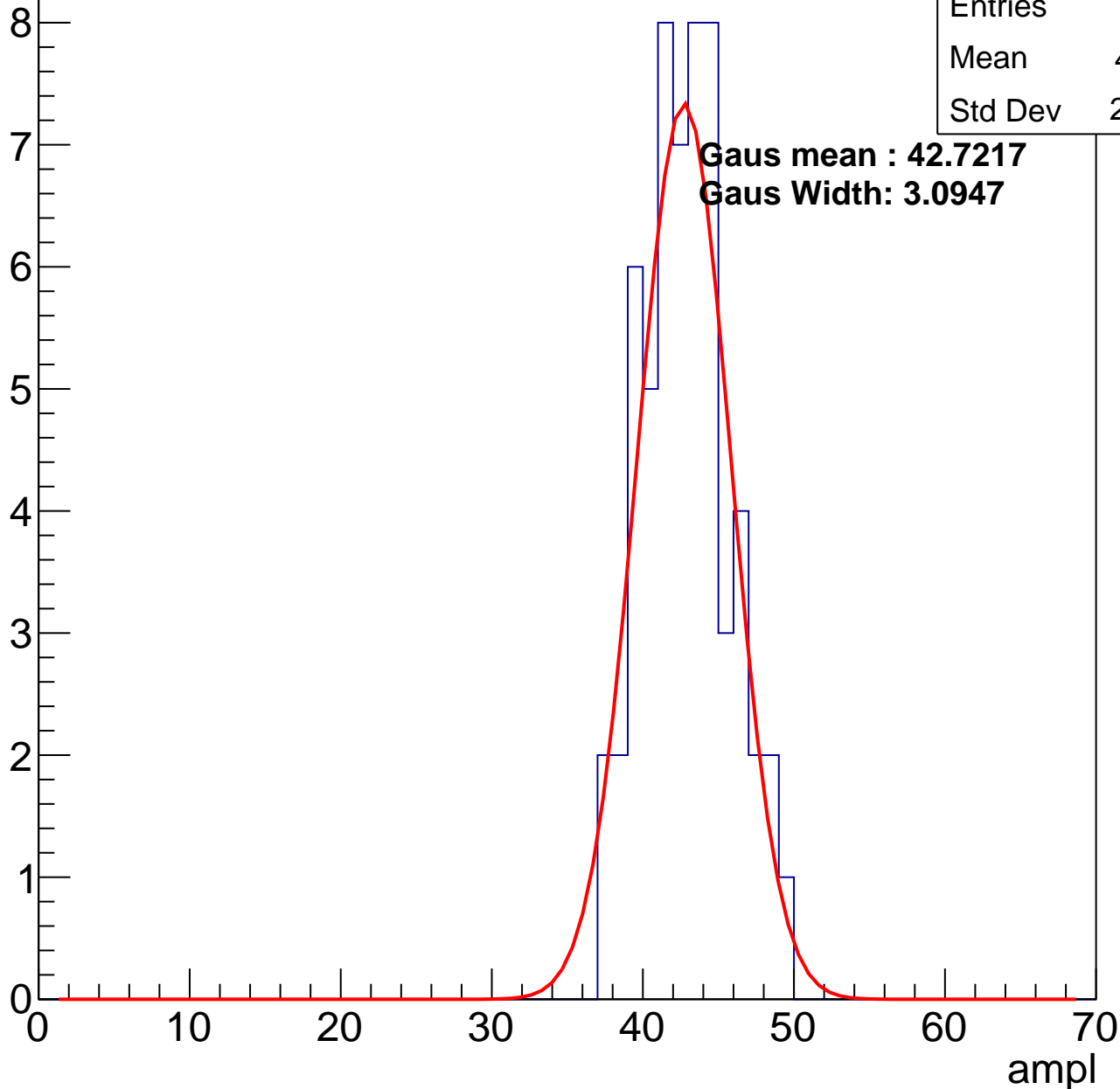
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.41
Std Dev	2.804

**Gaus mean : 42.7217**

**Gaus Width: 3.0947**

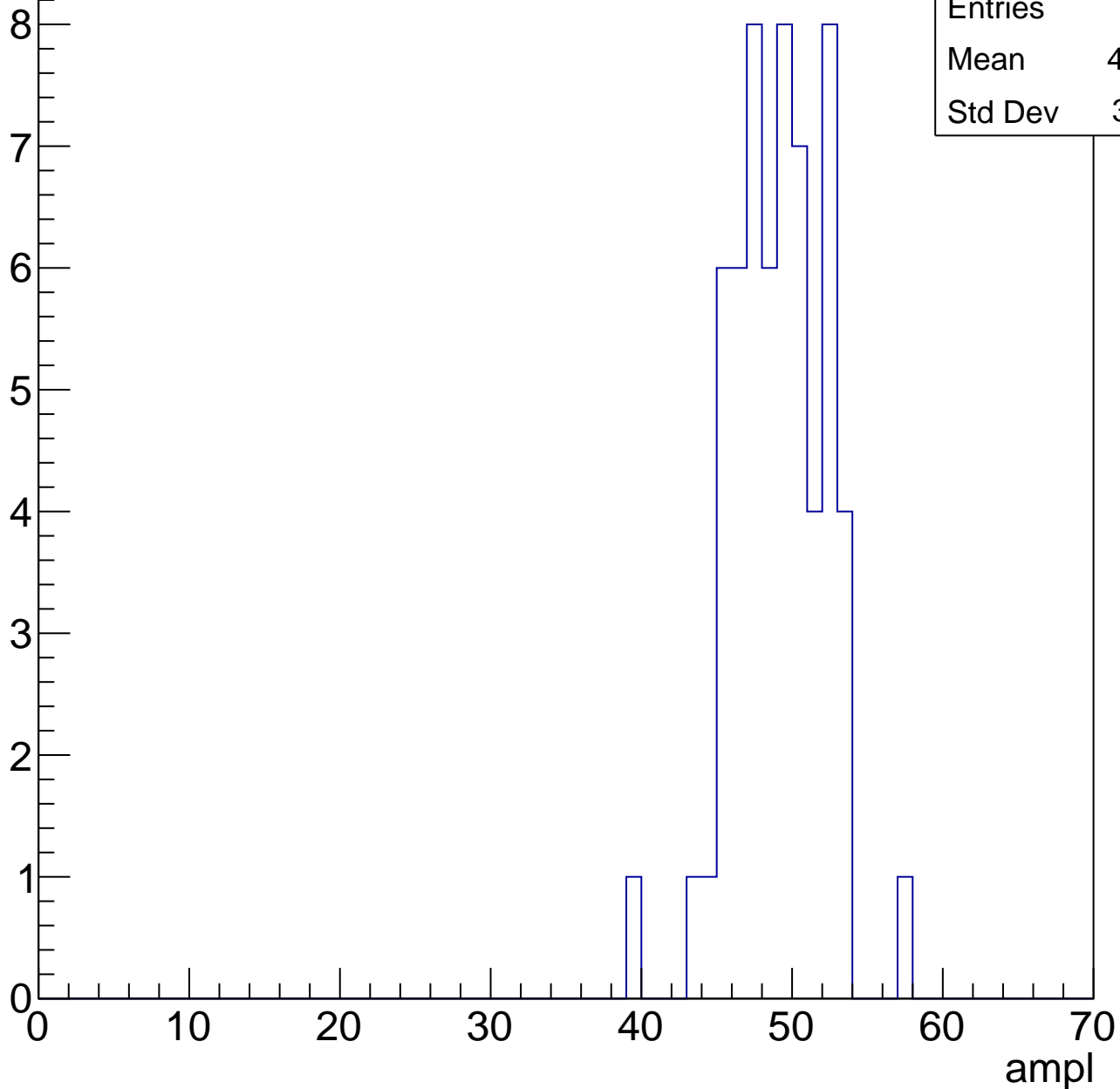


# B0L000S, U26-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	48.64
Std Dev	3.041



# B0L000S, U26-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	53.98
Std Dev	3.348

Entry

10

8

6

4

2

0

0

10

20

30

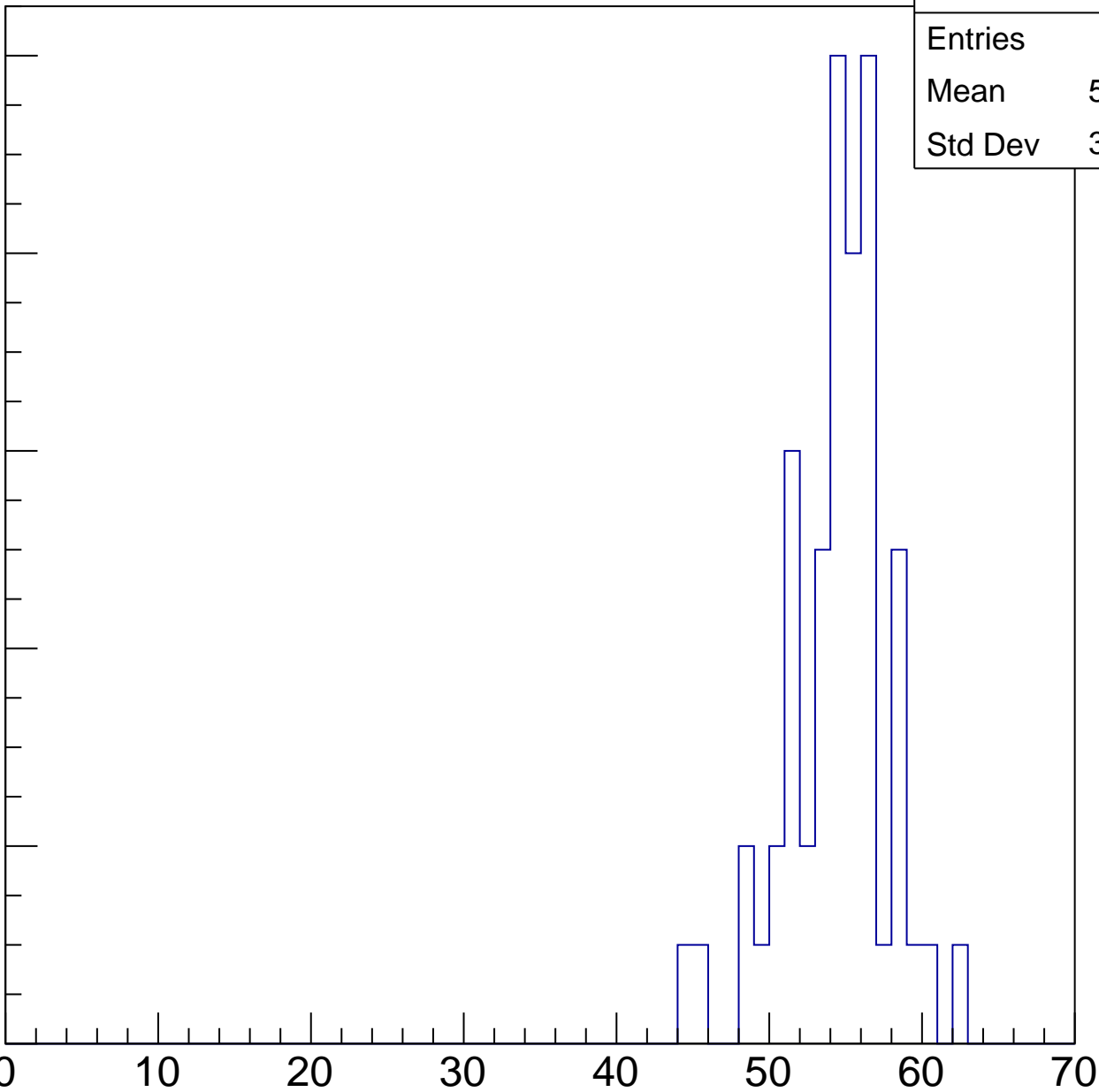
40

50

60

ampl

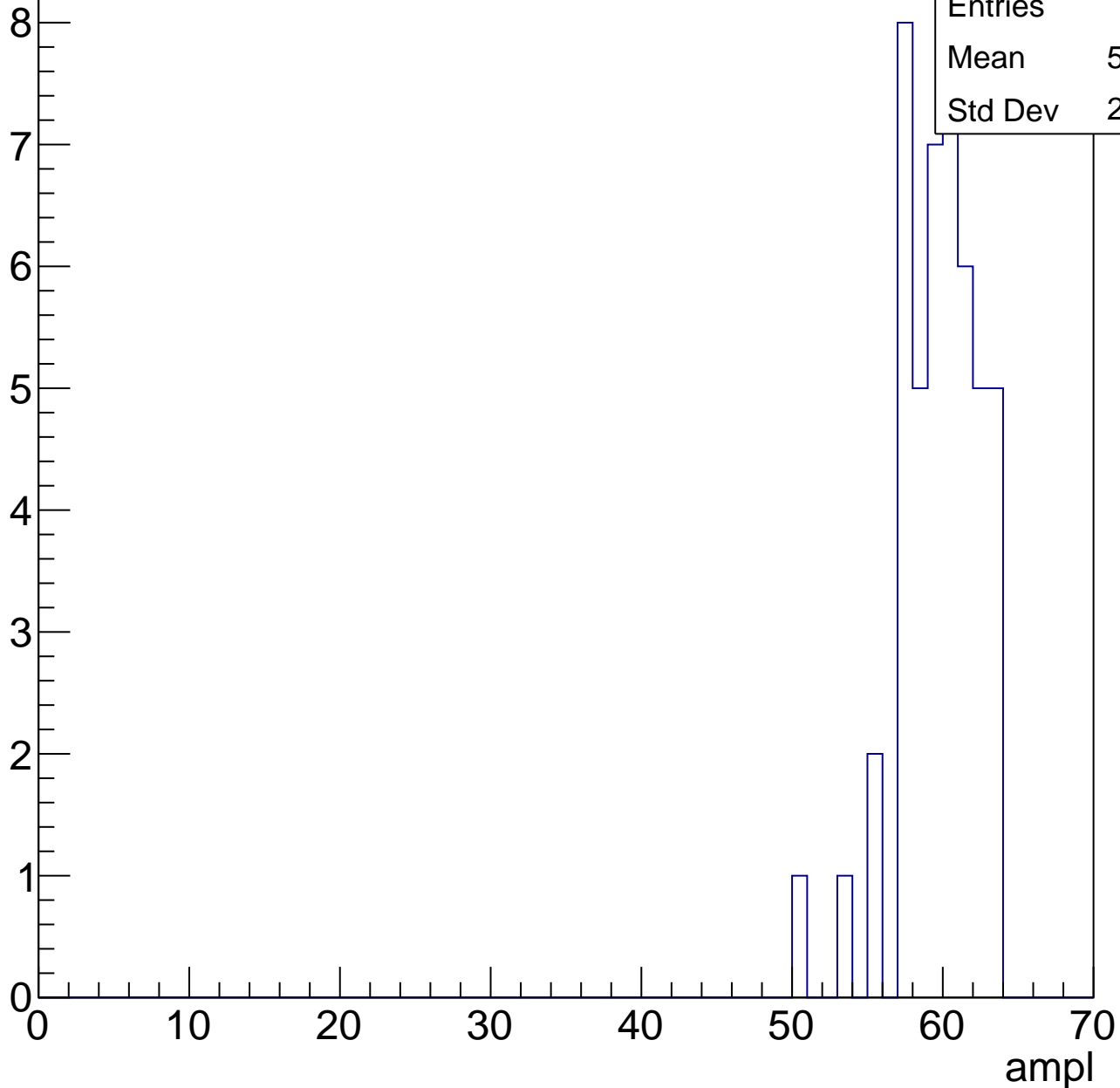
70



# B0L000S, U26-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

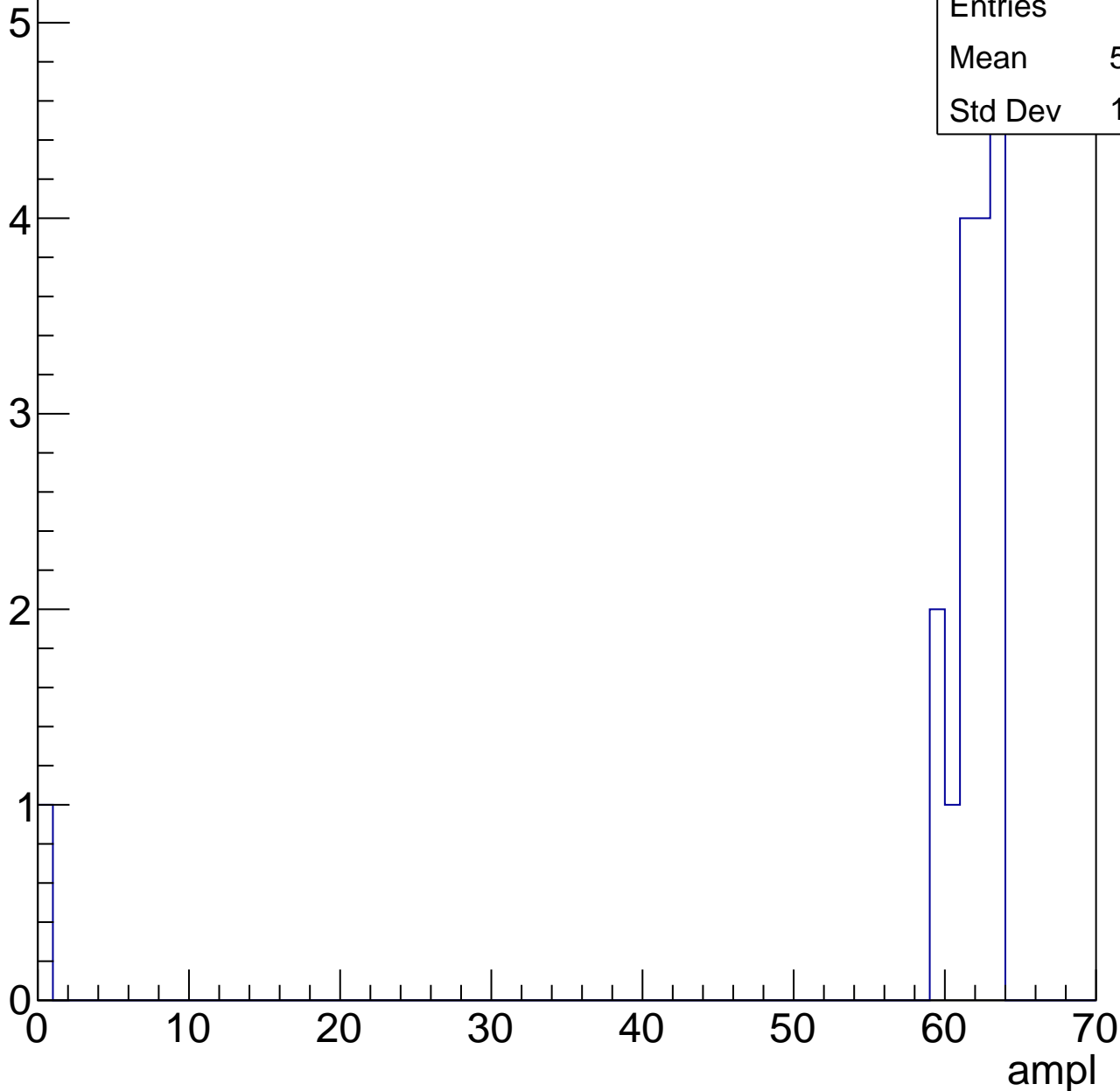


# B0L000S, U26-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	57.94
Std Dev	14.54





# B0L000S, U26-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	12
Std Dev	12

ampl

0 10 20 30 40 50 60 70

# B0L000S, U26-ch23, adc0

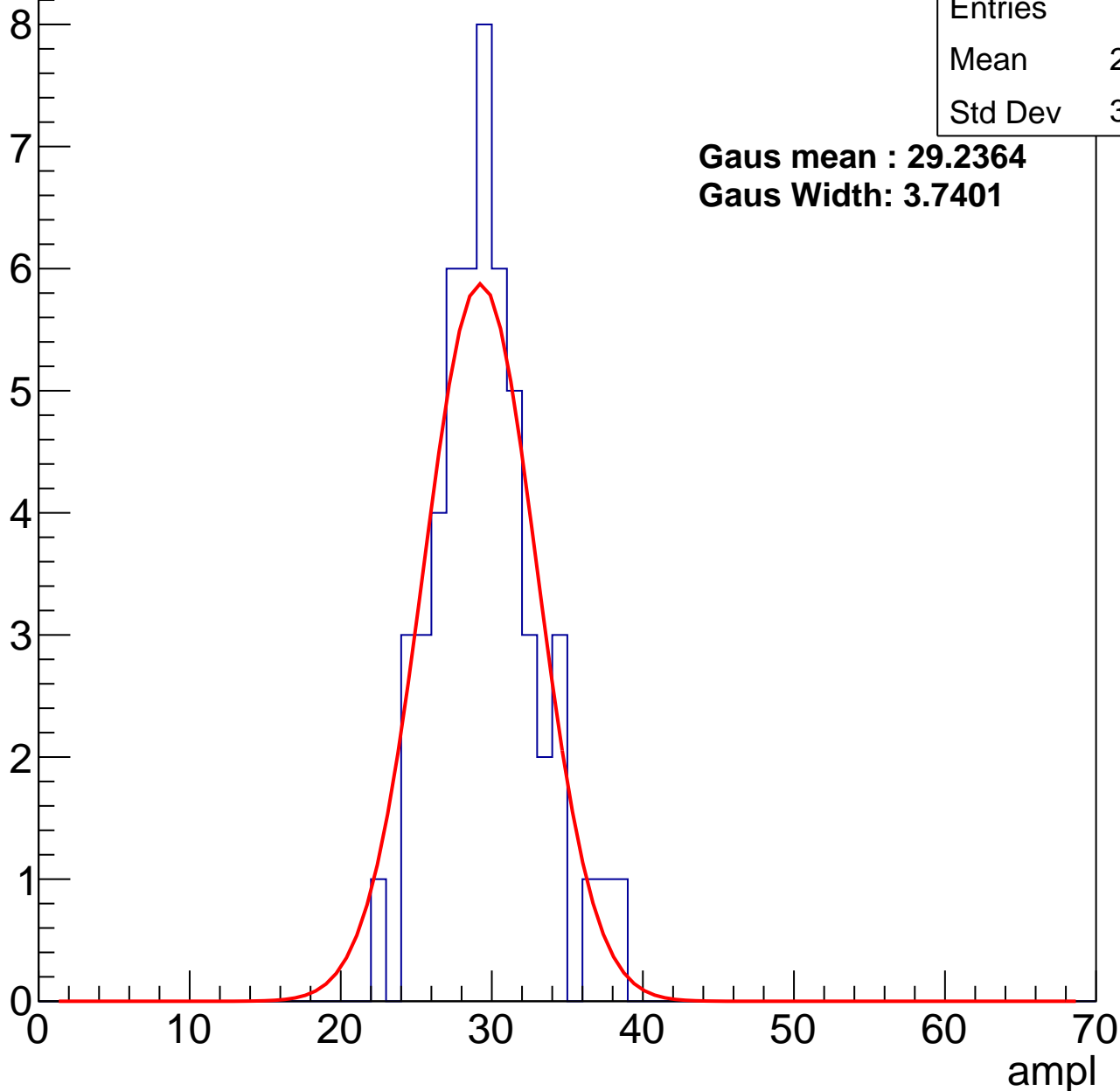
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	29.15
Std Dev	3.339

**Gaus mean : 29.2364**

**Gaus Width: 3.7401**



# B0L000S, U26-ch23, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	35.65
Std Dev	3.456

**Gaus mean : 35.3983**

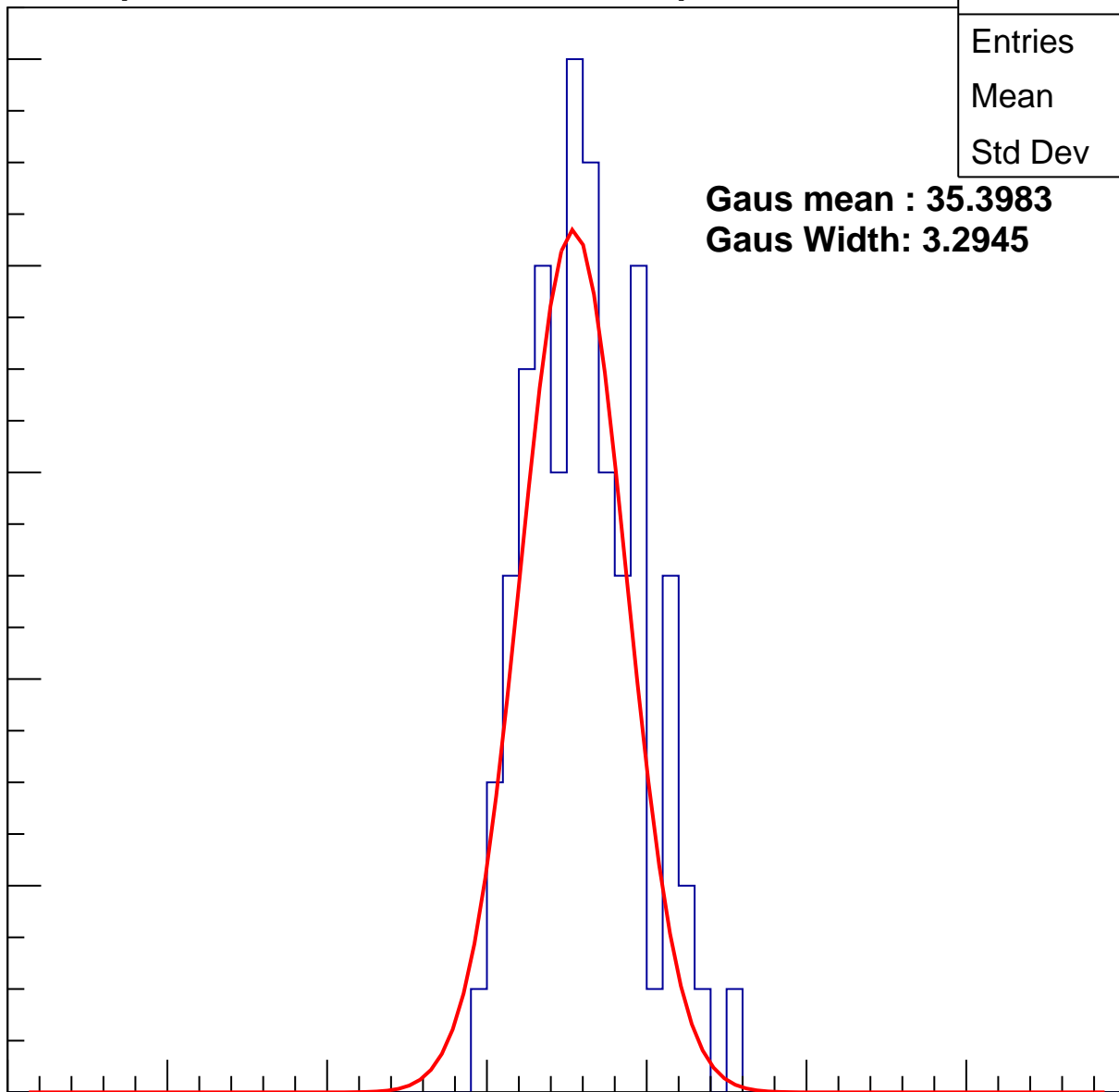
**Gaus Width: 3.2945**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch23, adc2

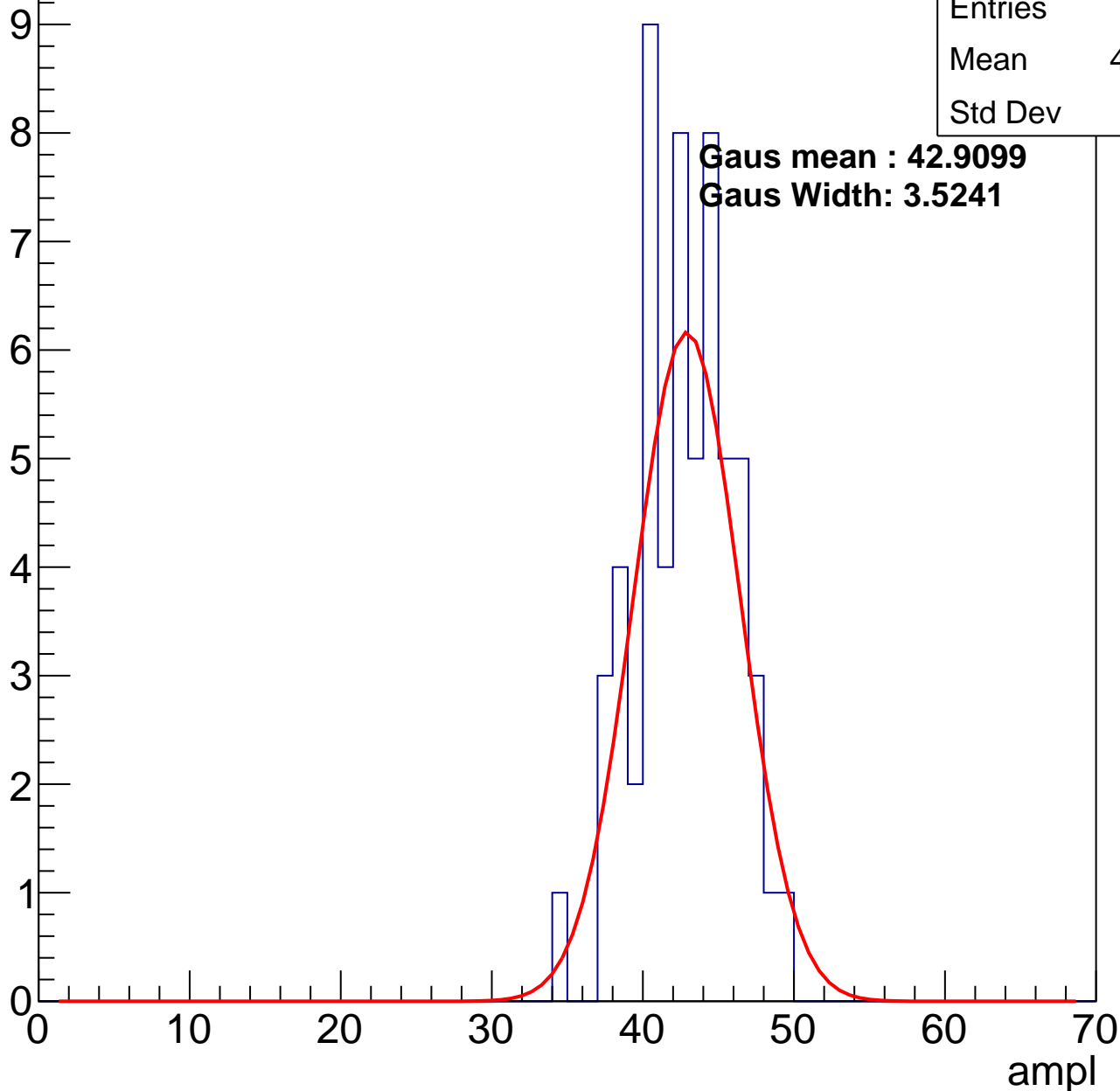
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.29
Std Dev	3.13

**Gaus mean : 42.9099**

**Gaus Width: 3.5241**

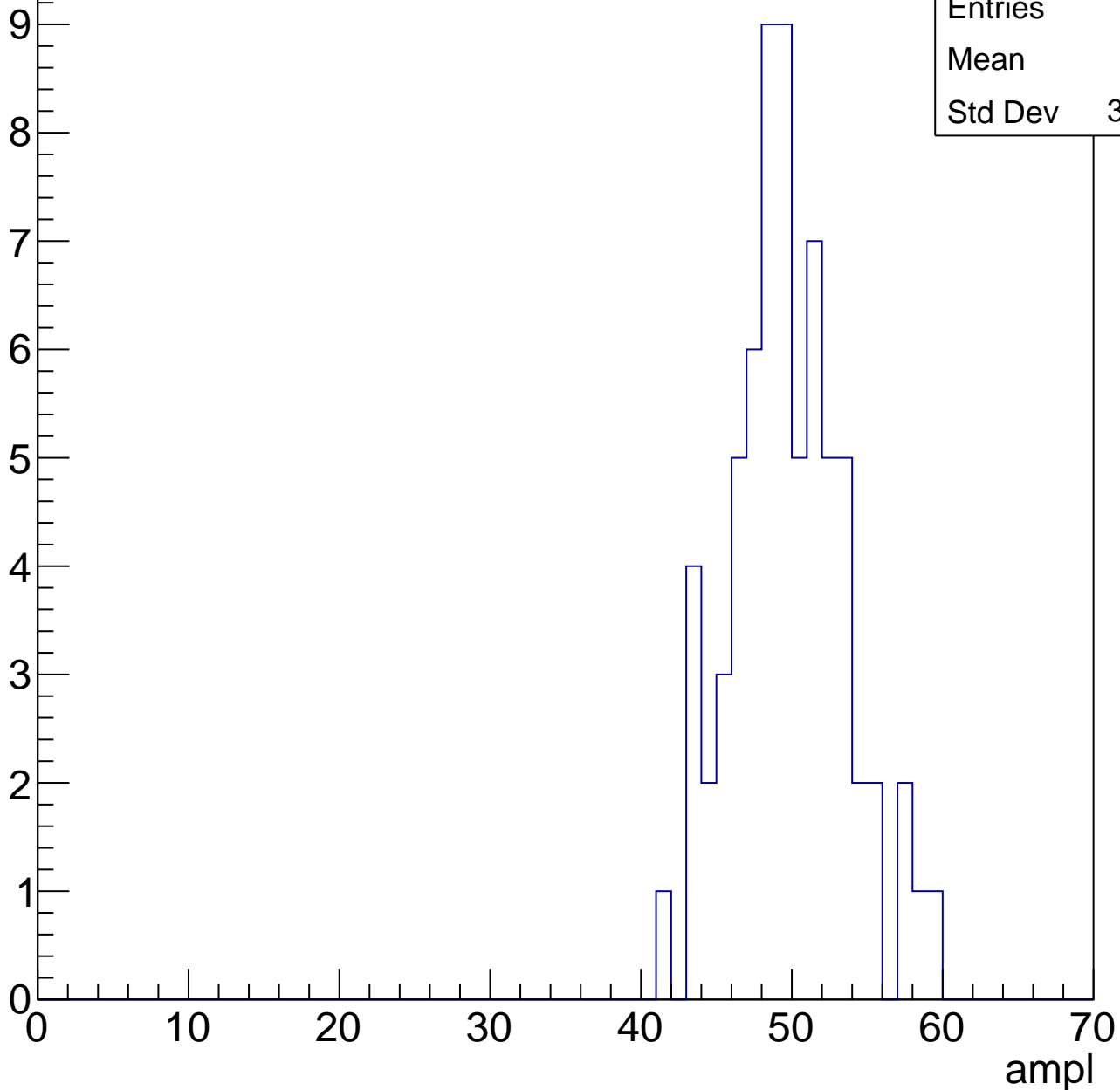


# B0L000S, U26-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.3
Std Dev	3.743

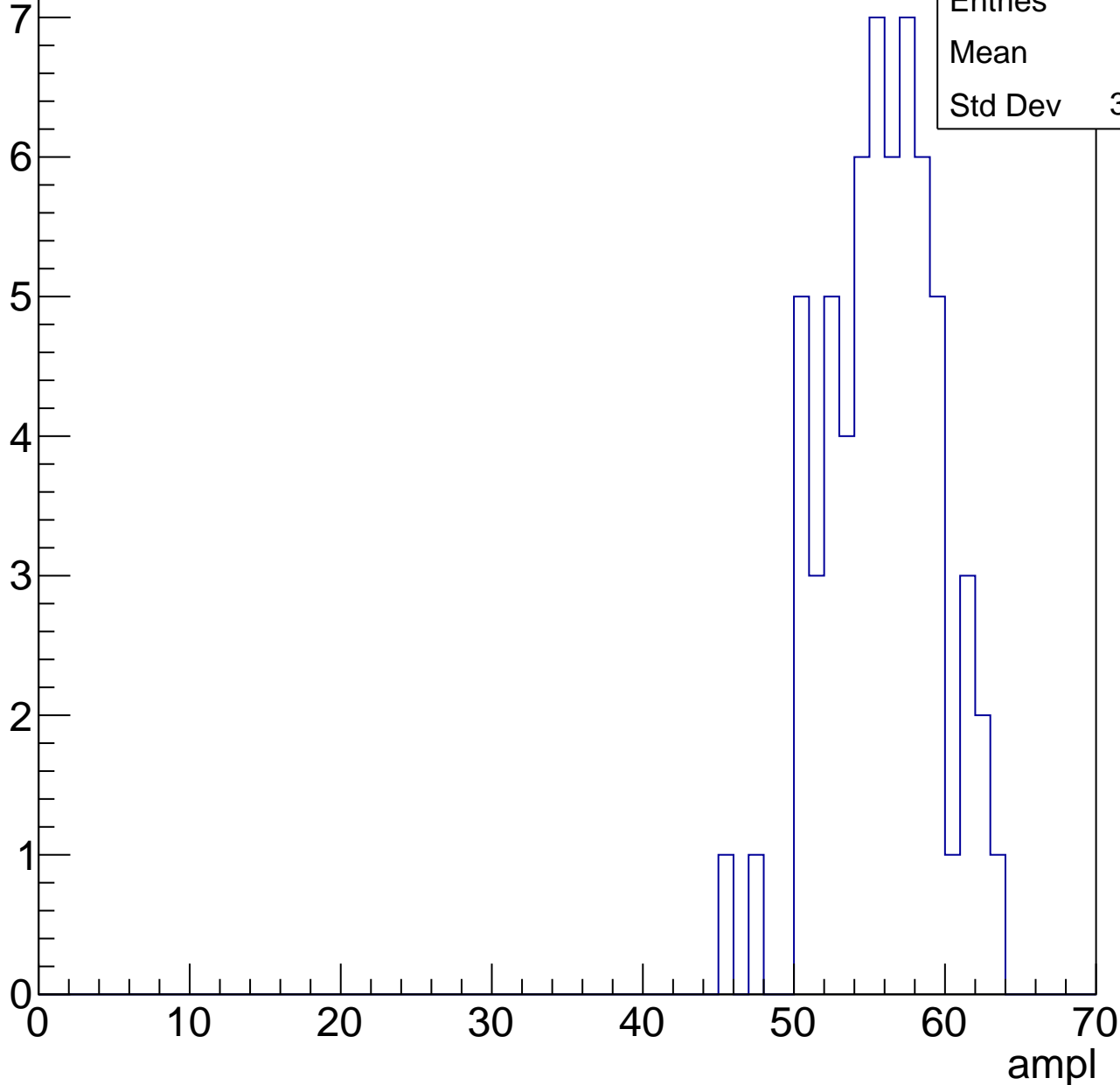


# B0L000S, U26-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

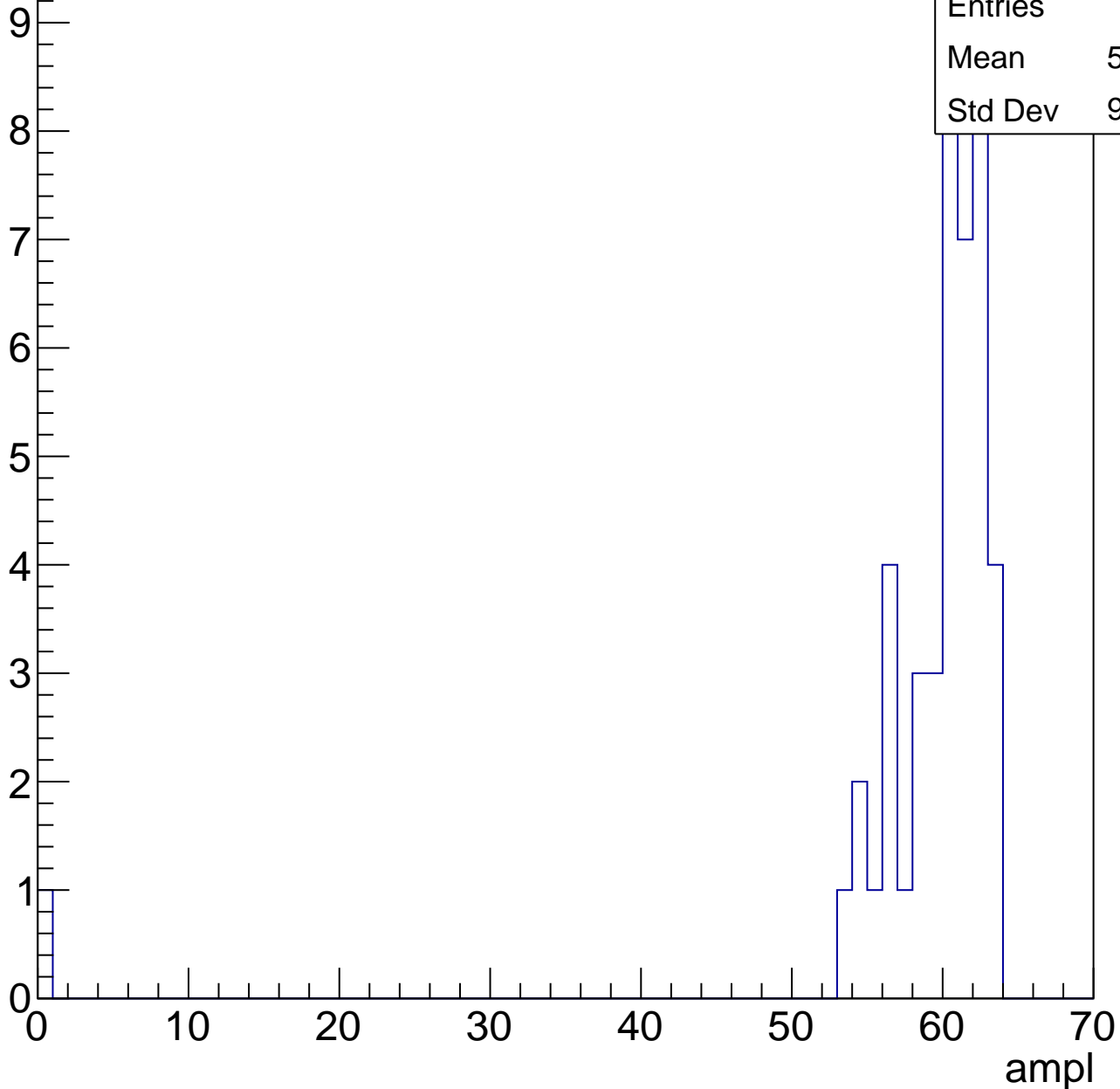
Entries	63
Mean	55.3
Std Dev	3.697



# B0L000S, U26-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

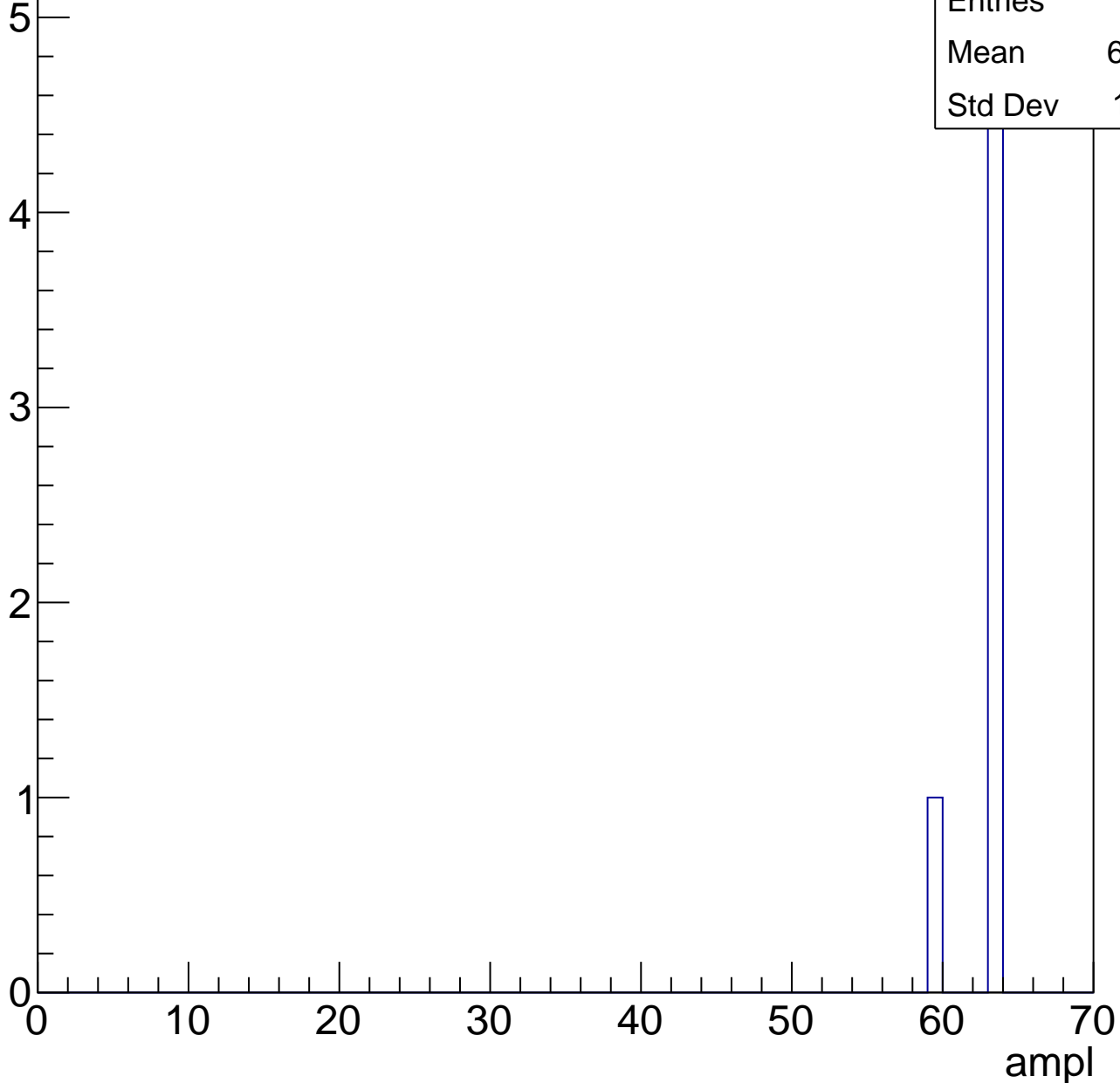


# B0L000S, U26-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	6
Mean	62.33
Std Dev	1.491





# B0L000S, U26-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch24, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	28.95
Std Dev	4.77

**Gaus mean : 29.6272**

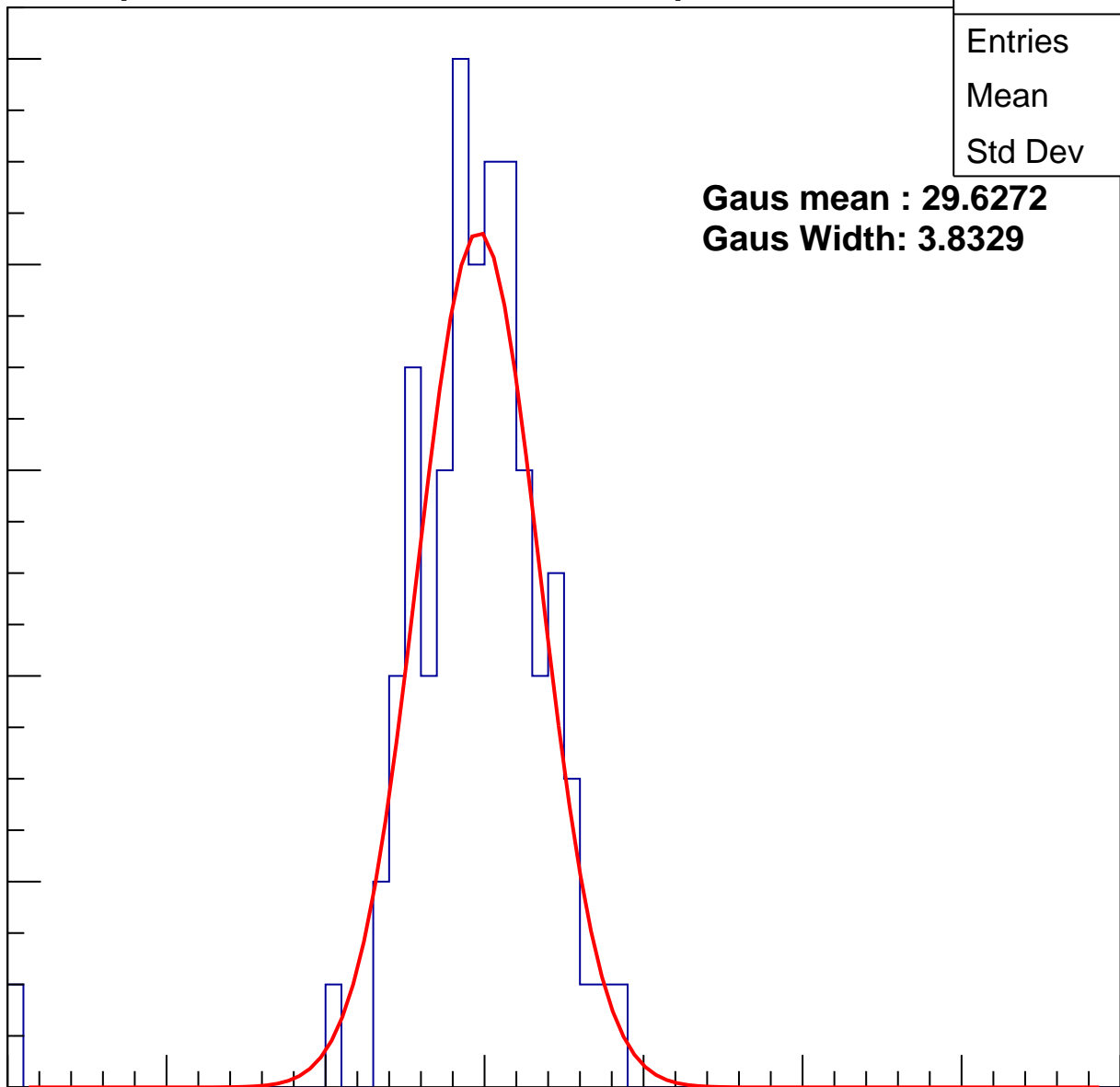
**Gaus Width: 3.8329**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch24, adc1

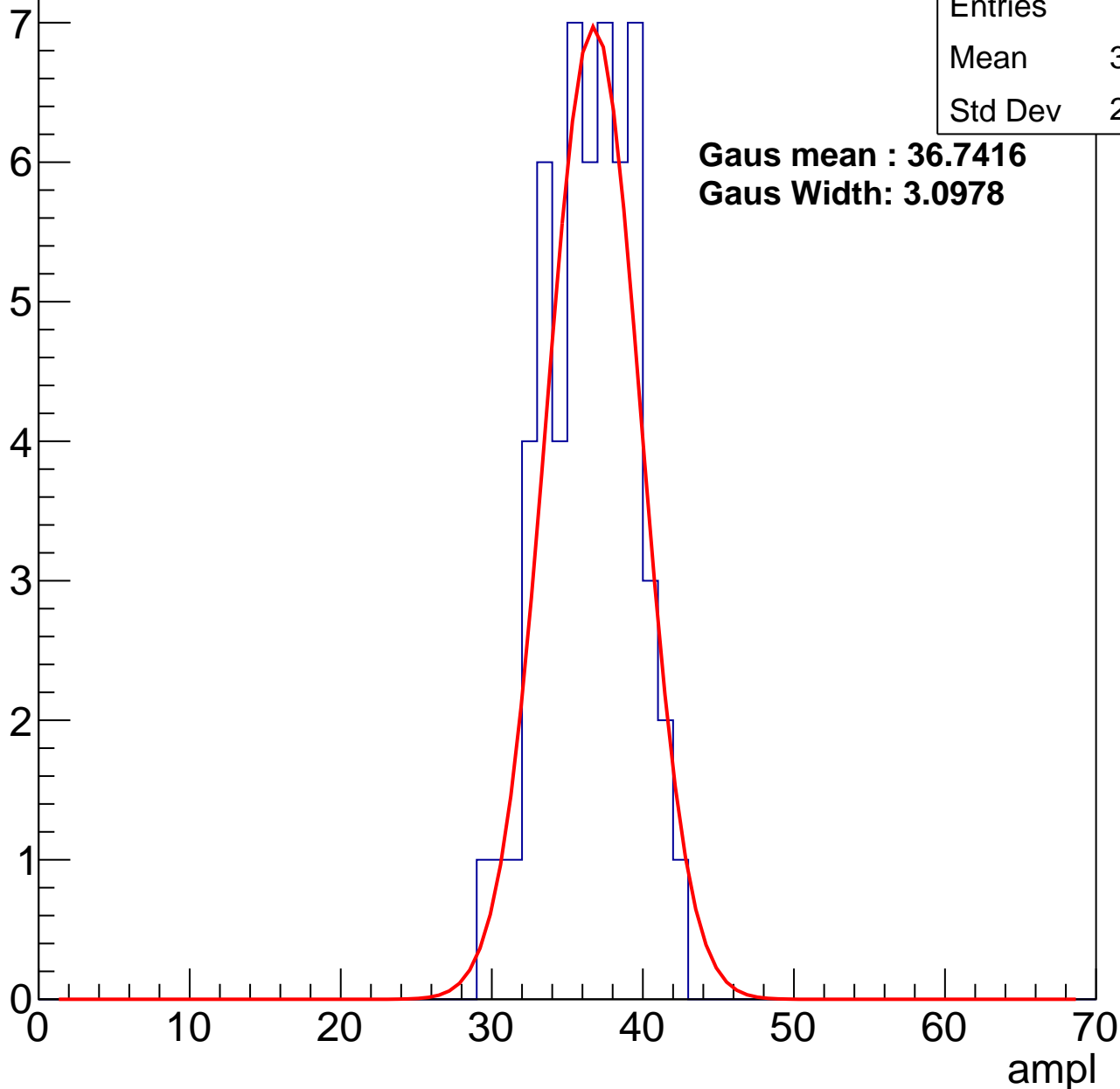
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.02
Std Dev	2.925

**Gaus mean : 36.7416**

**Gaus Width: 3.0978**



# B0L000S, U26-ch24, adc2

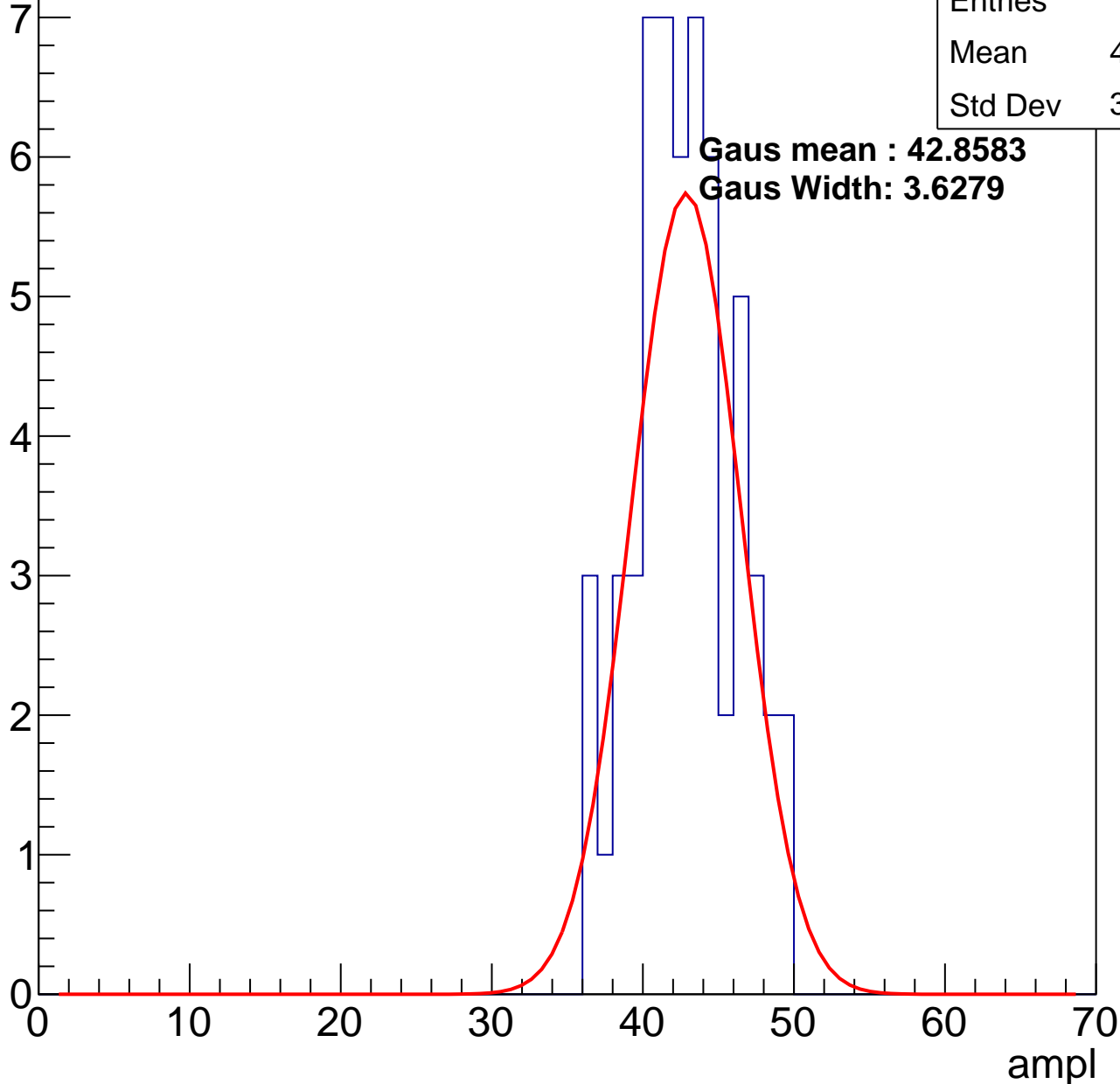
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.37
Std Dev	3.269

**Gaus mean : 42.8583**

**Gaus Width: 3.6279**

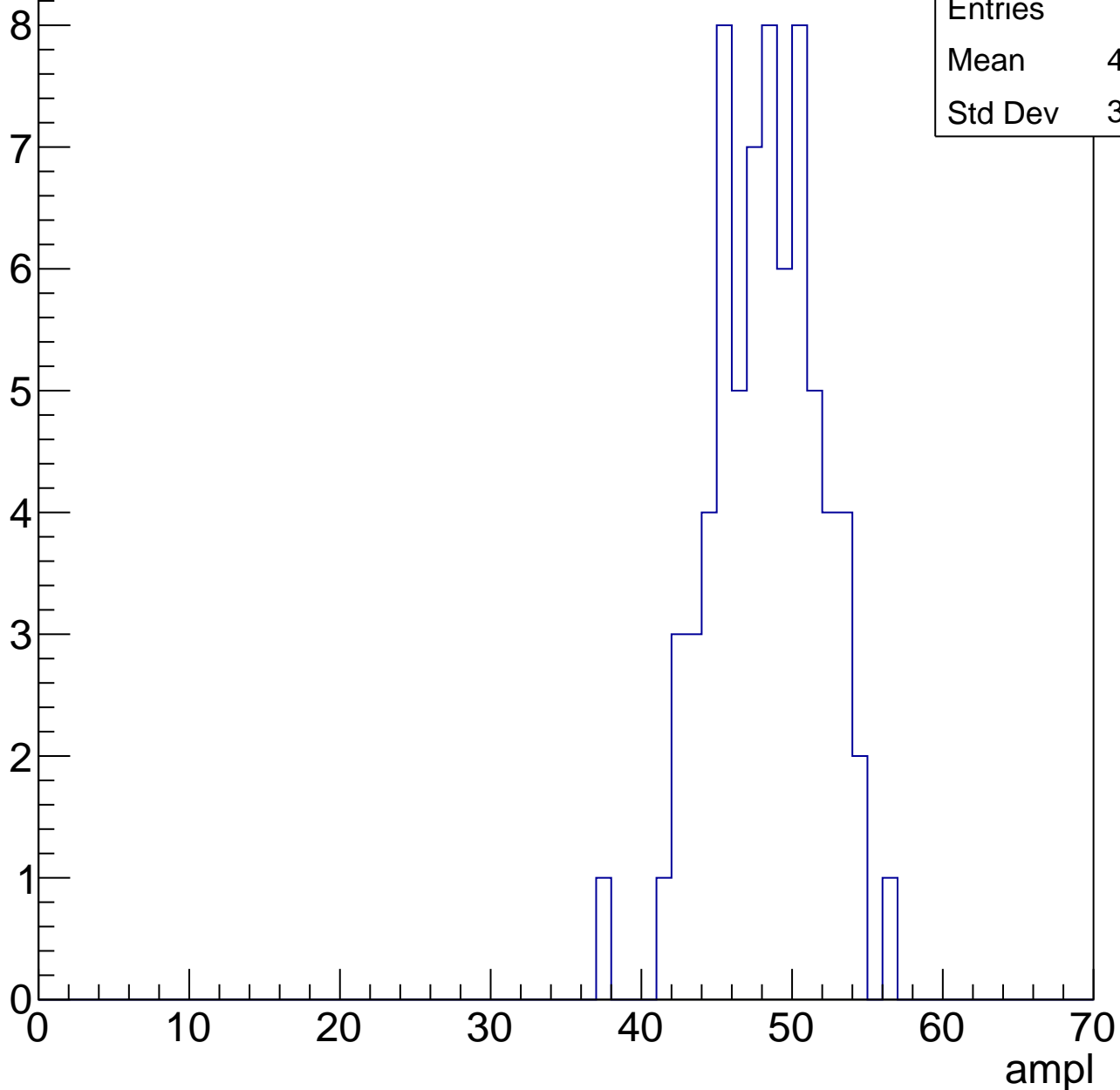


# B0L000S, U26-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

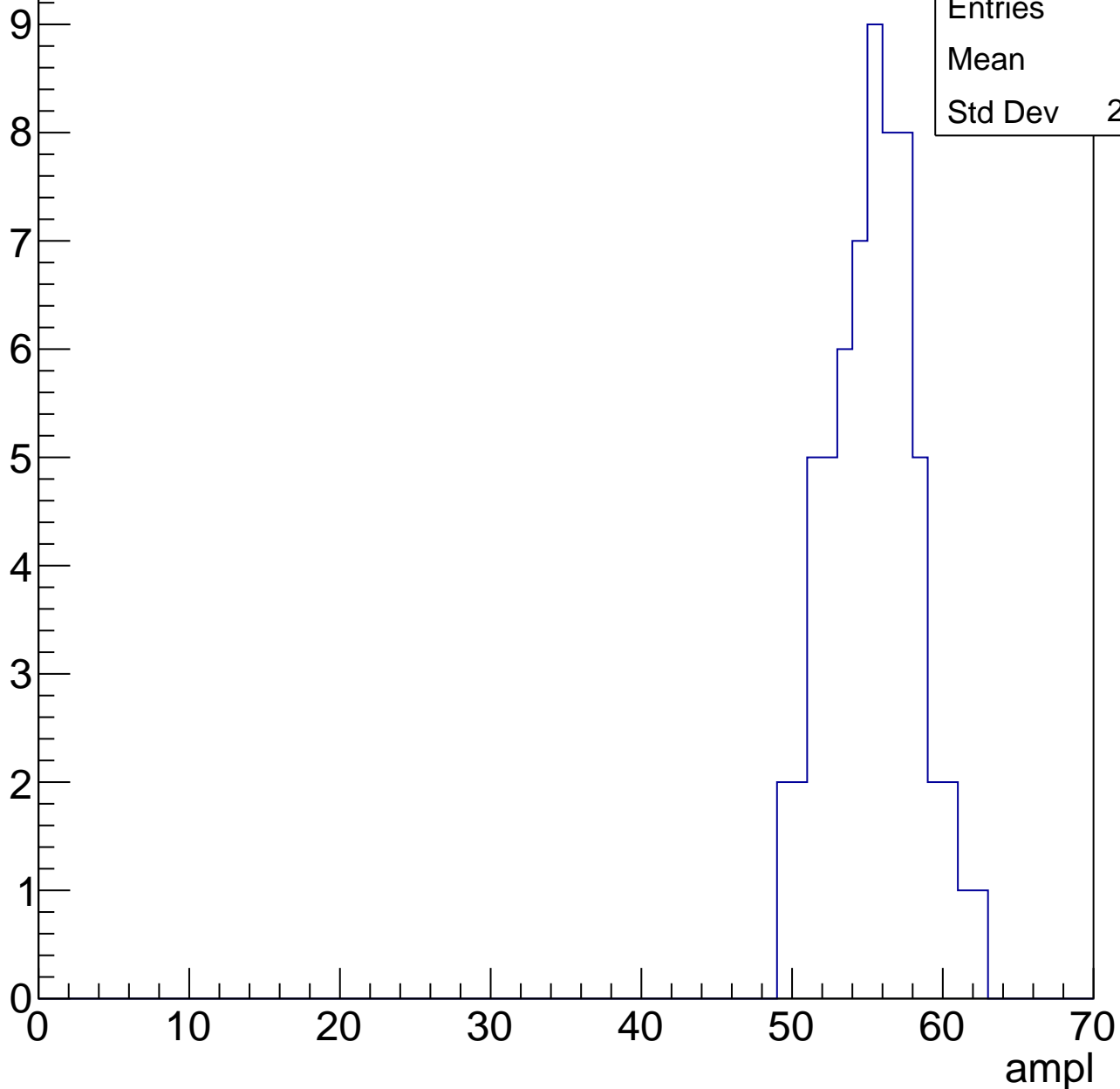
Entries	70
Mean	47.79
Std Dev	3.577



# B0L000S, U26-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

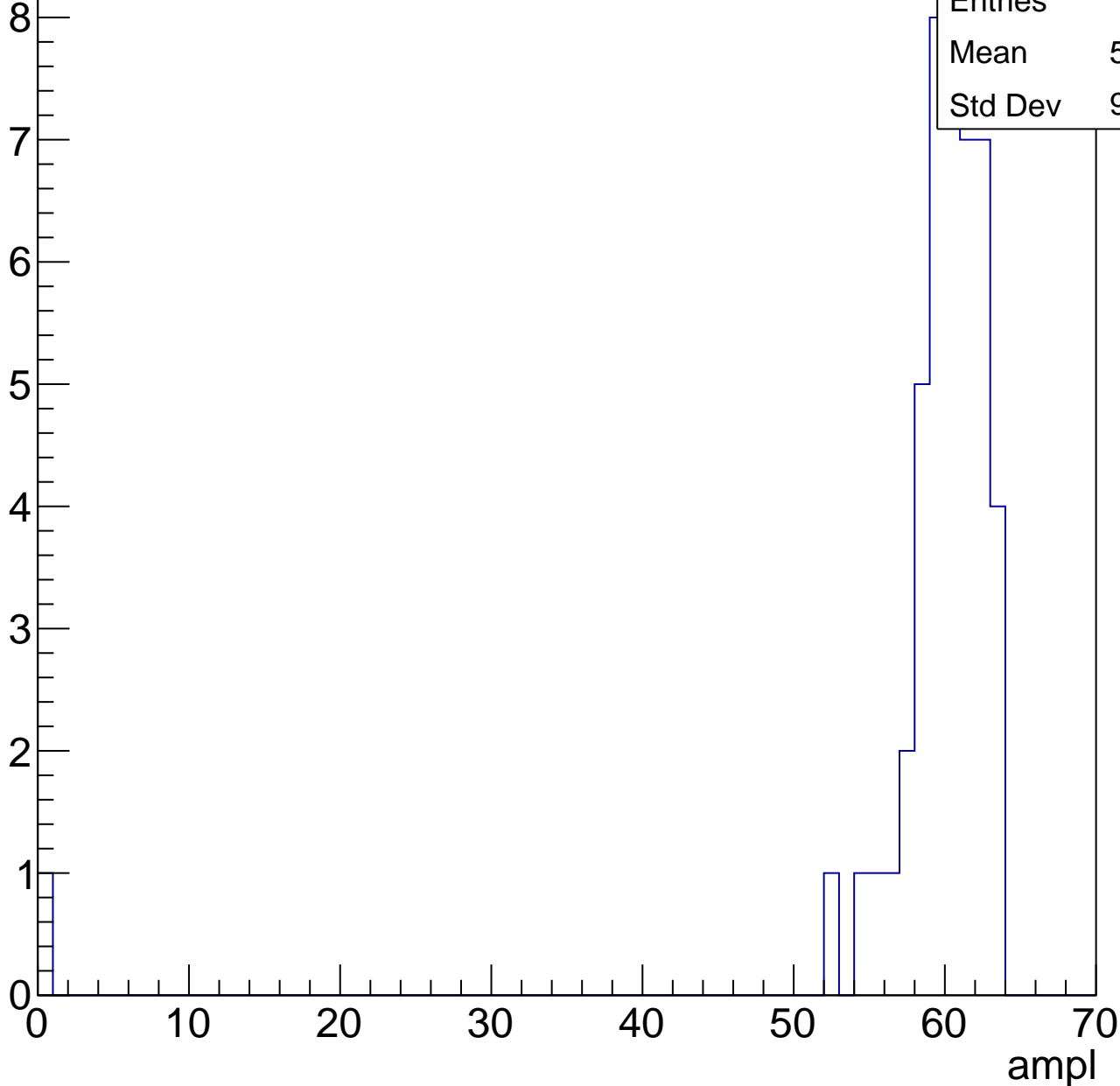


# B0L000S, U26-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.39
Std Dev	9.016

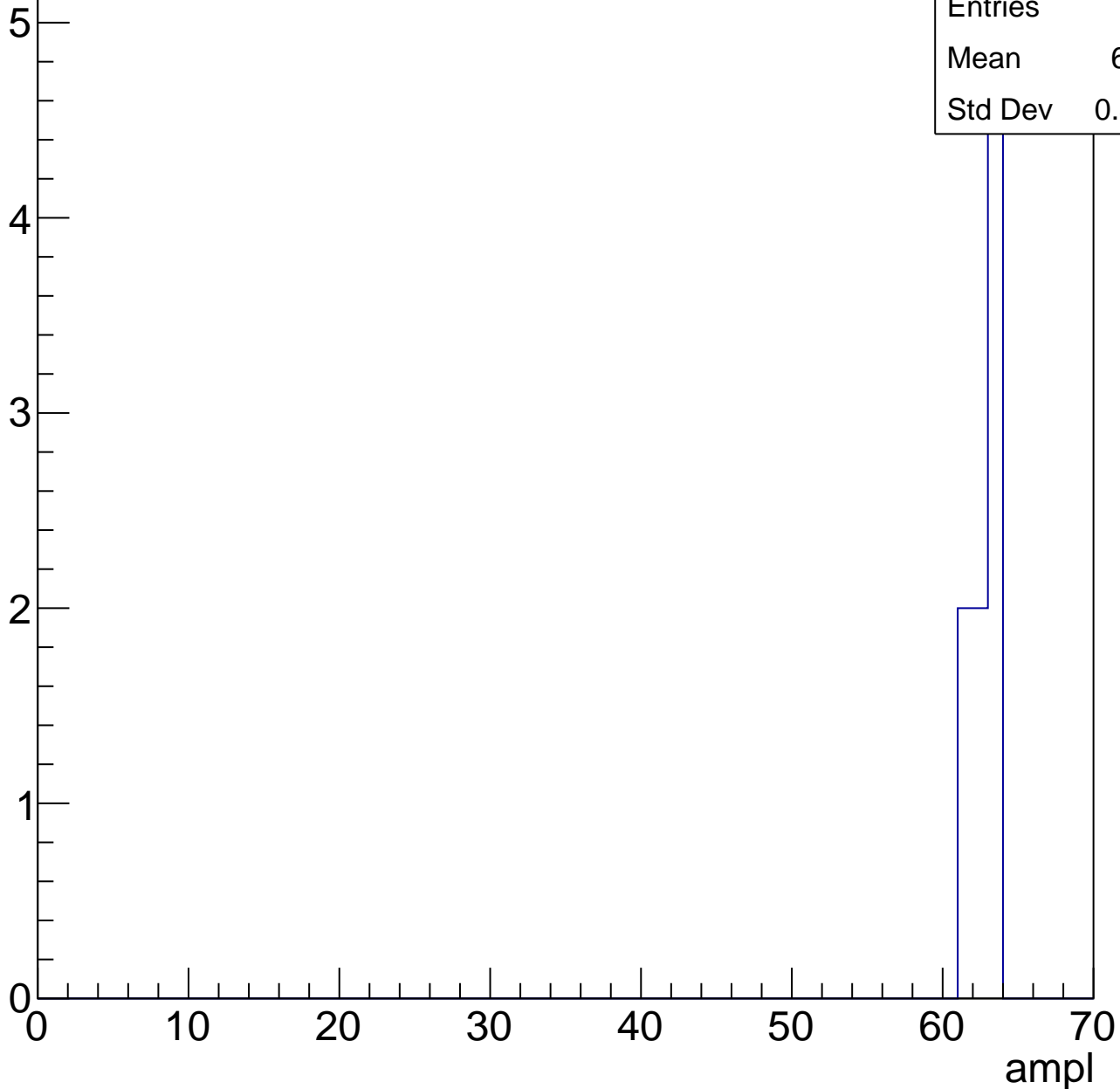


# B0L000S, U26-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.33
Std Dev	0.8165





# B0L000S, U26-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch25, adc0

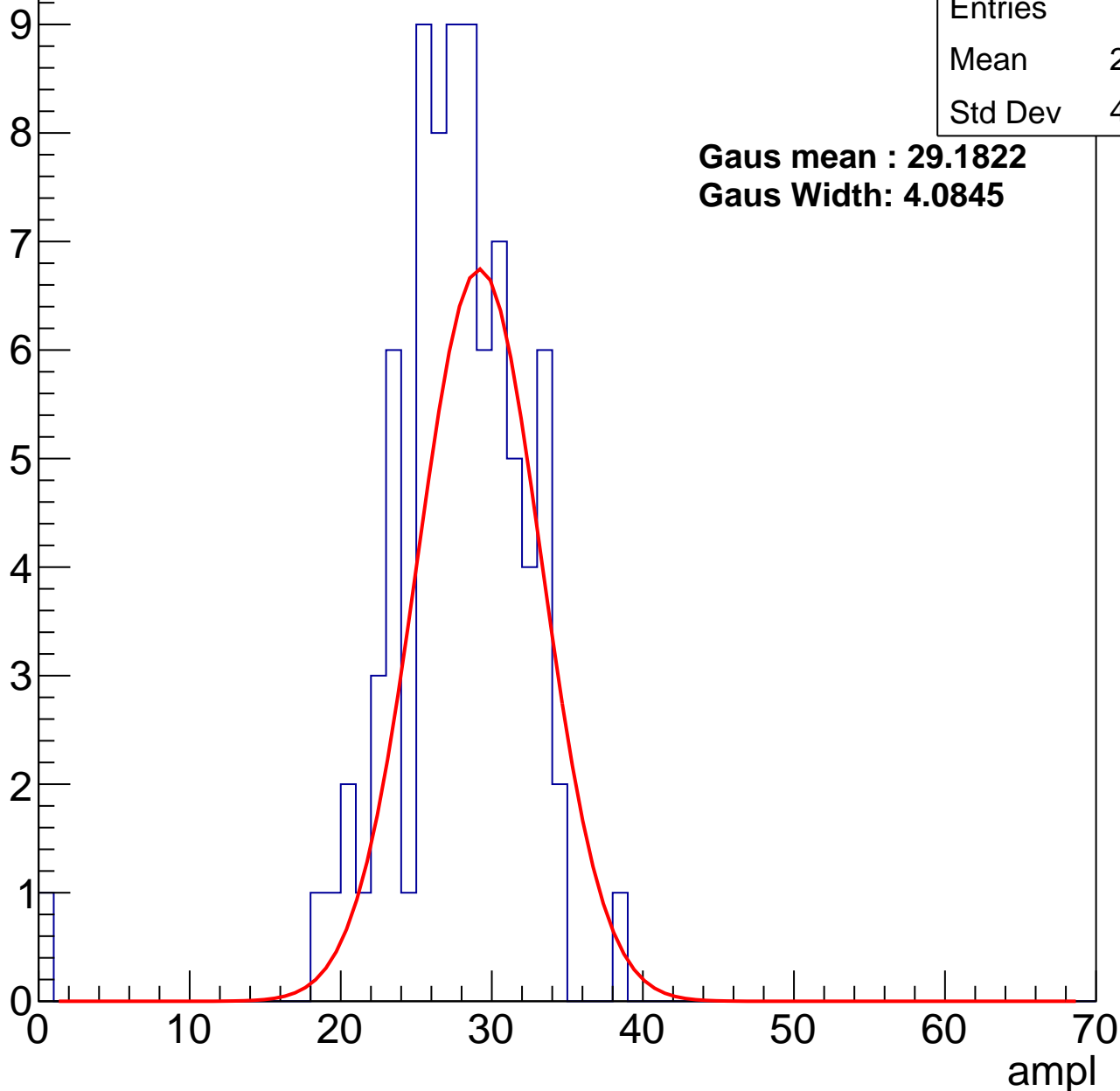
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	27.13
Std Dev	4.866

**Gaus mean : 29.1822**

**Gaus Width: 4.0845**



# B0L000S, U26-ch25, adc1

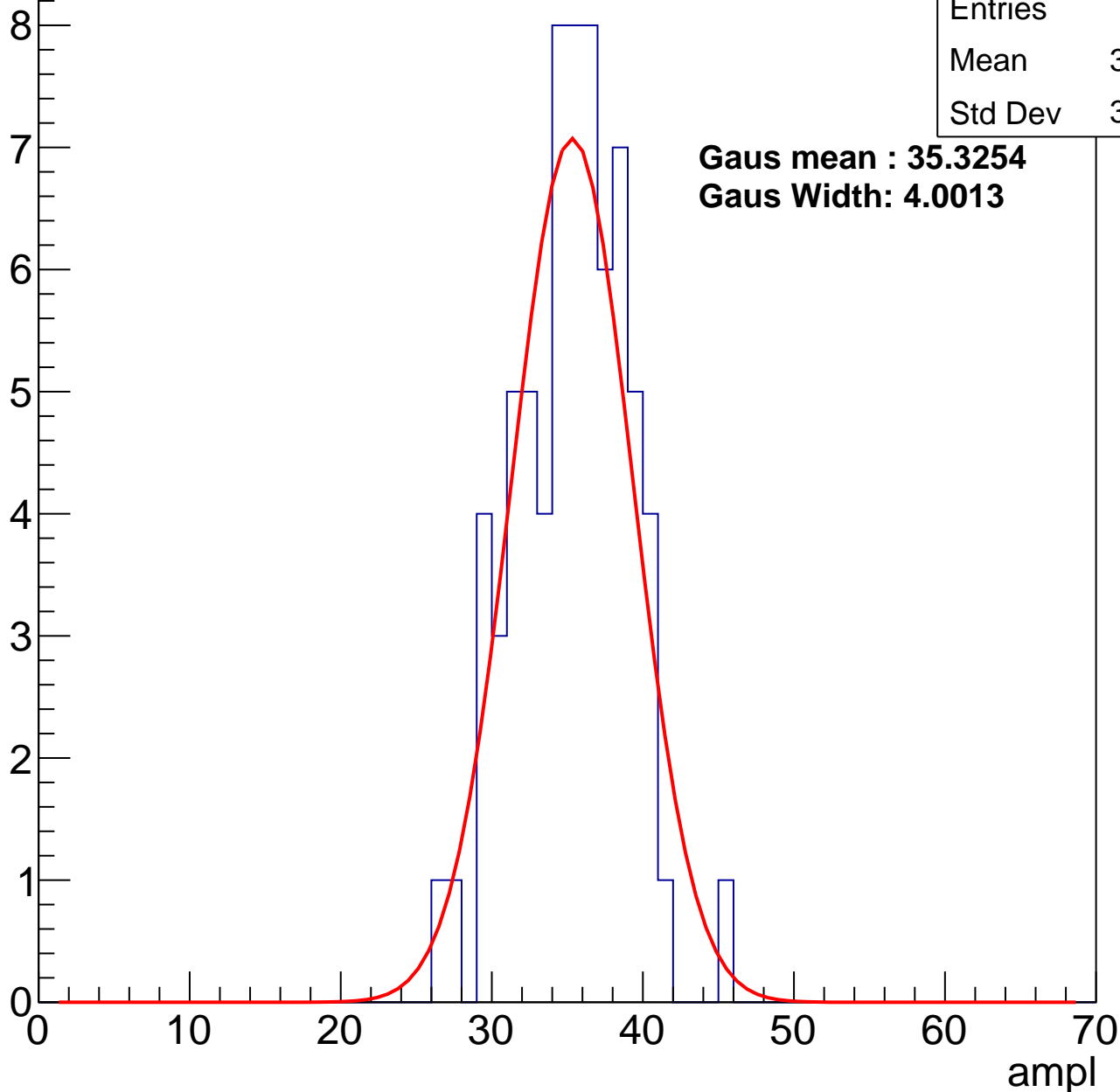
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	34.86
Std Dev	3.605

**Gaus mean : 35.3254**

**Gaus Width: 4.0013**



# B0L000S, U26-ch25, adc2

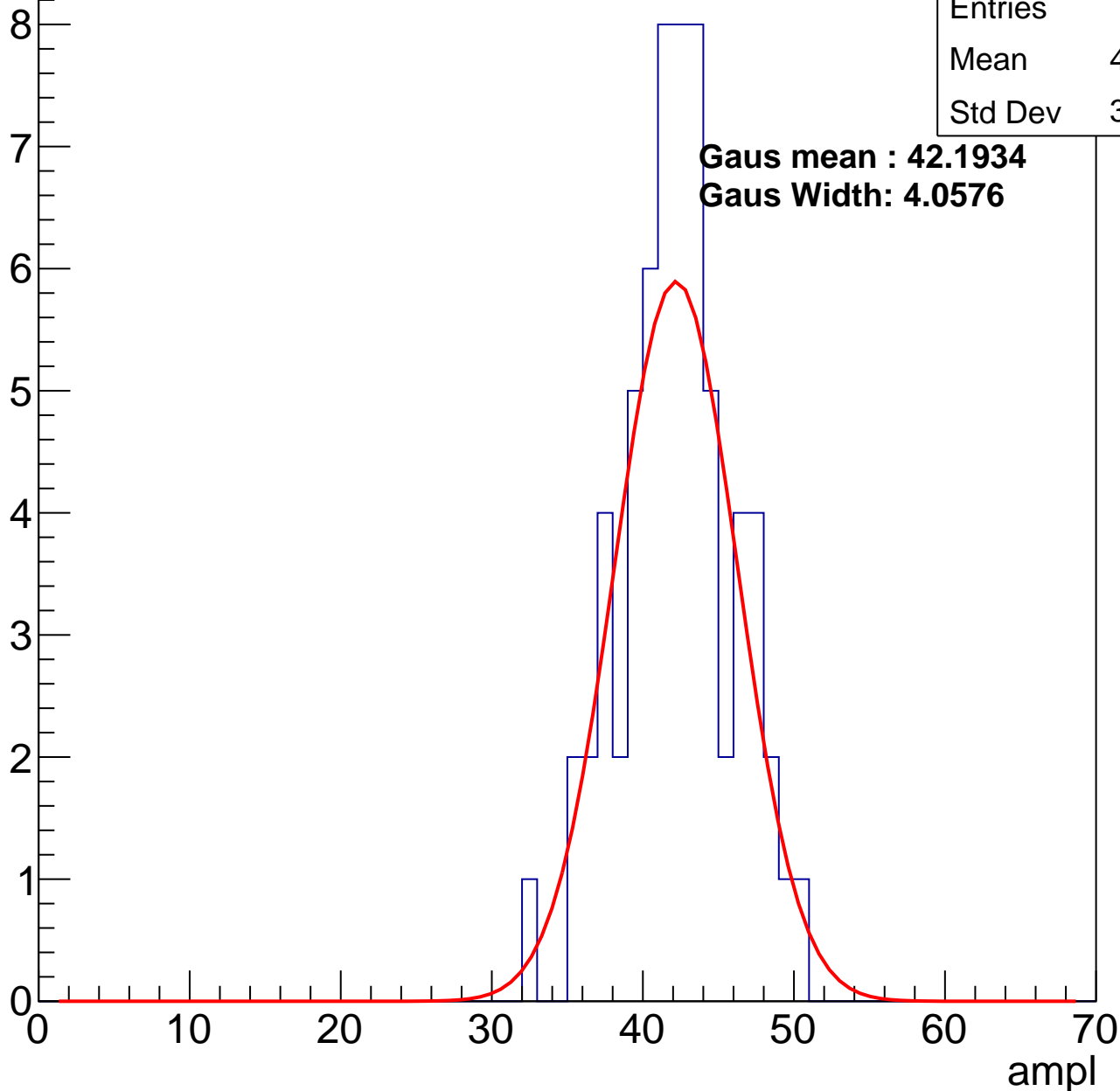
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	41.82
Std Dev	3.662

**Gaus mean : 42.1934**

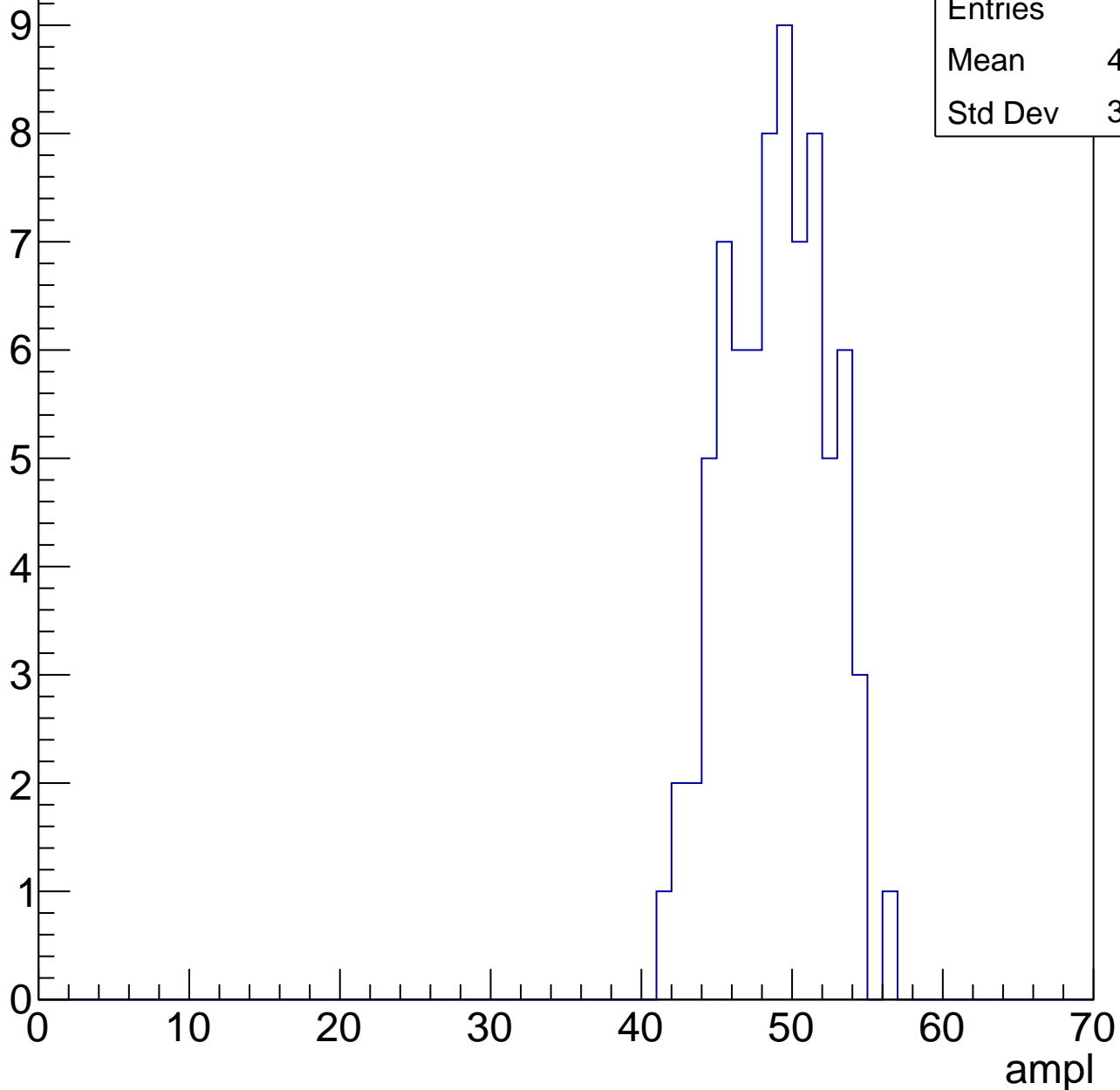
**Gaus Width: 4.0576**



# B0L000S, U26-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

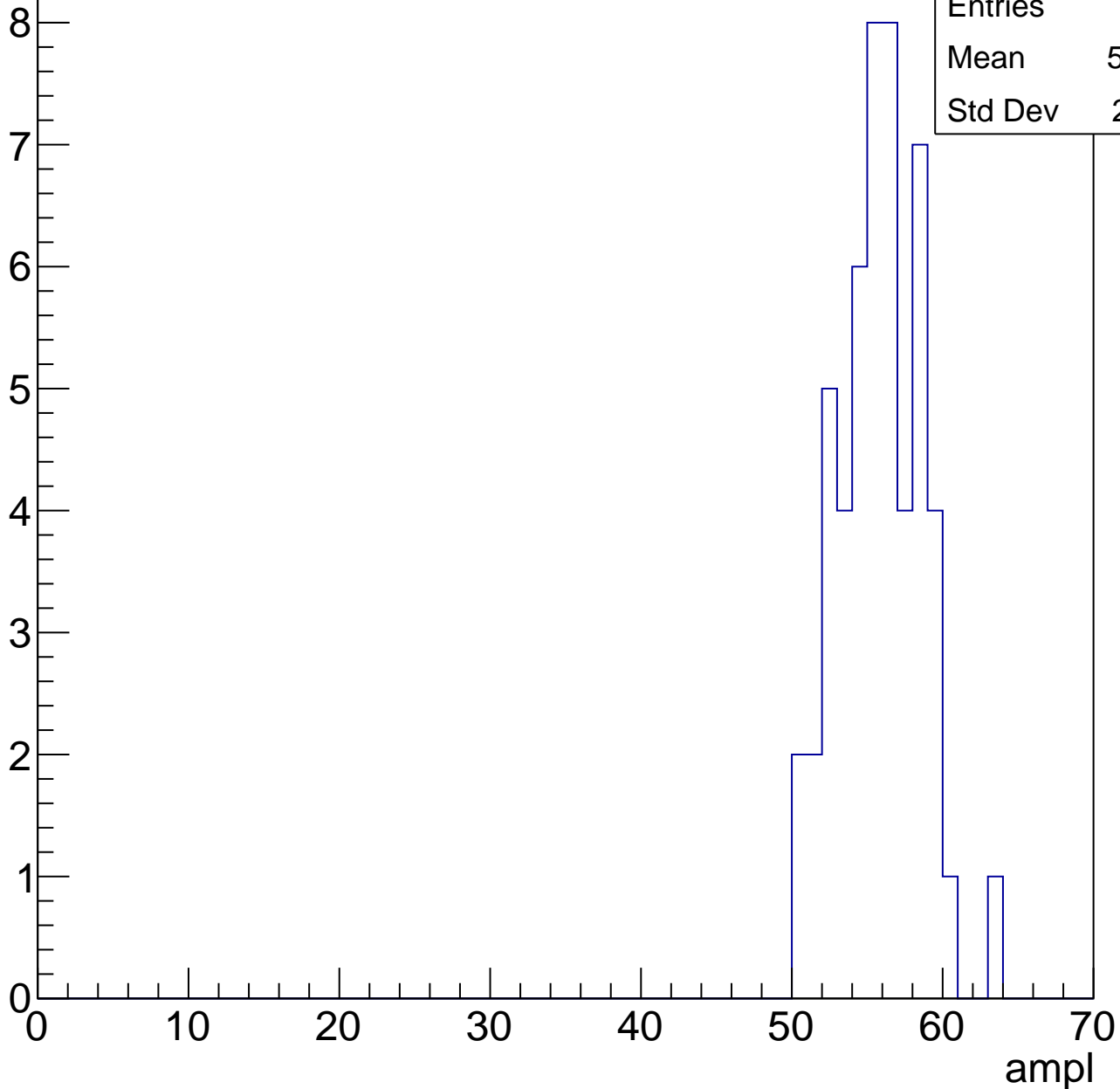


# B0L000S, U26-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

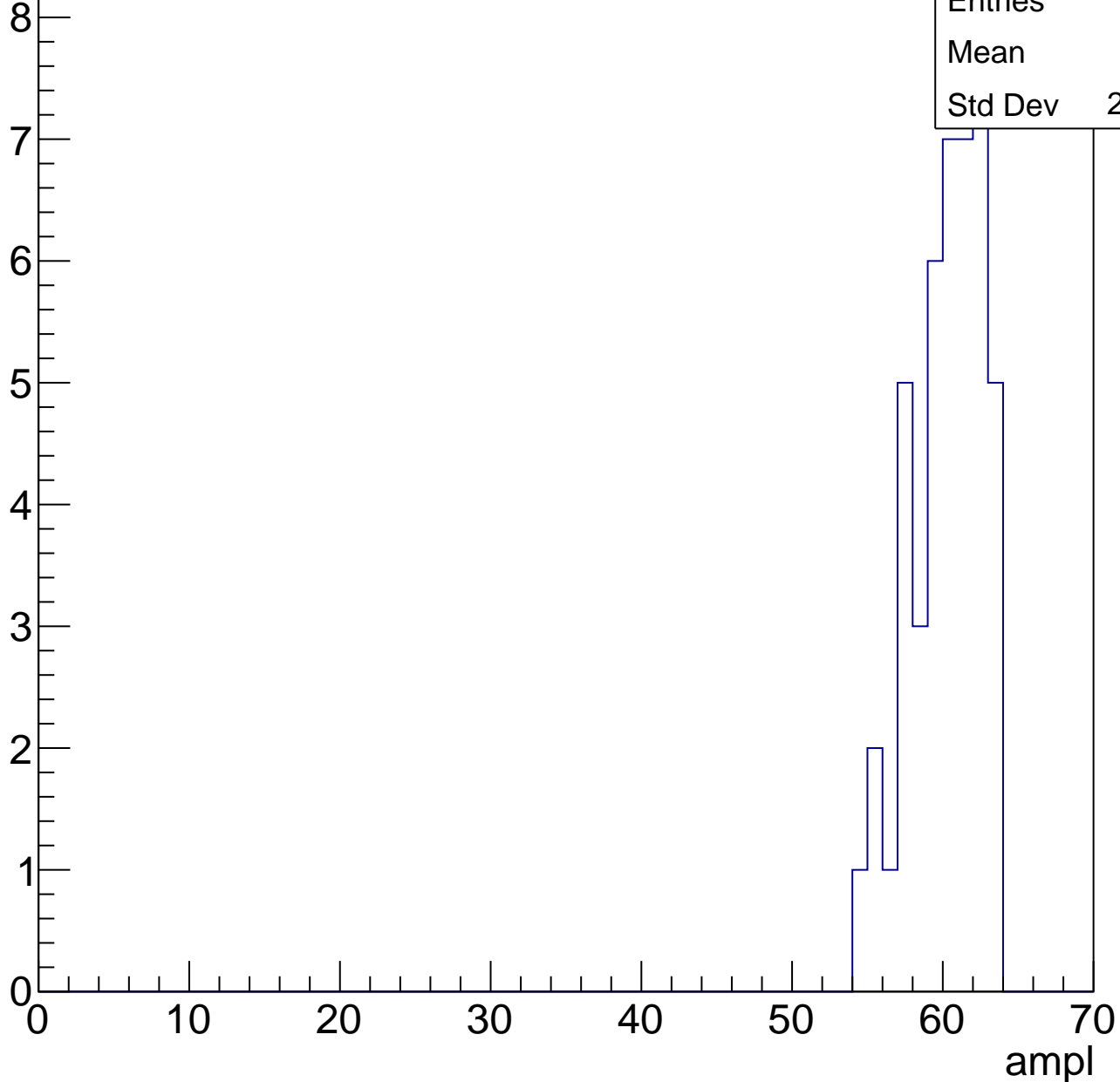
Entries	52
Mean	55.37
Std Dev	2.711



# B0L000S, U26-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

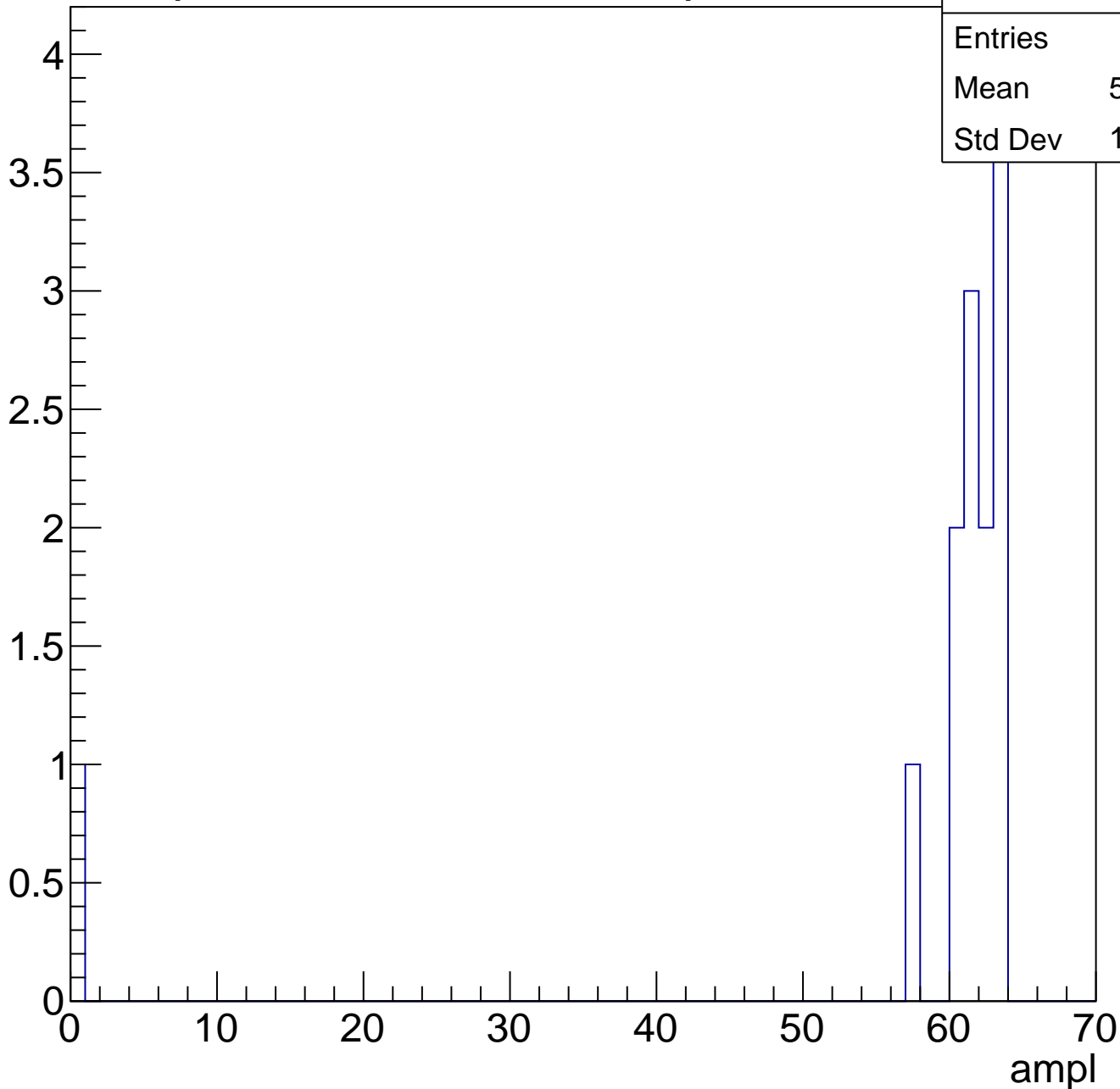
Entry



# B0L000S, U26-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch26, adc0

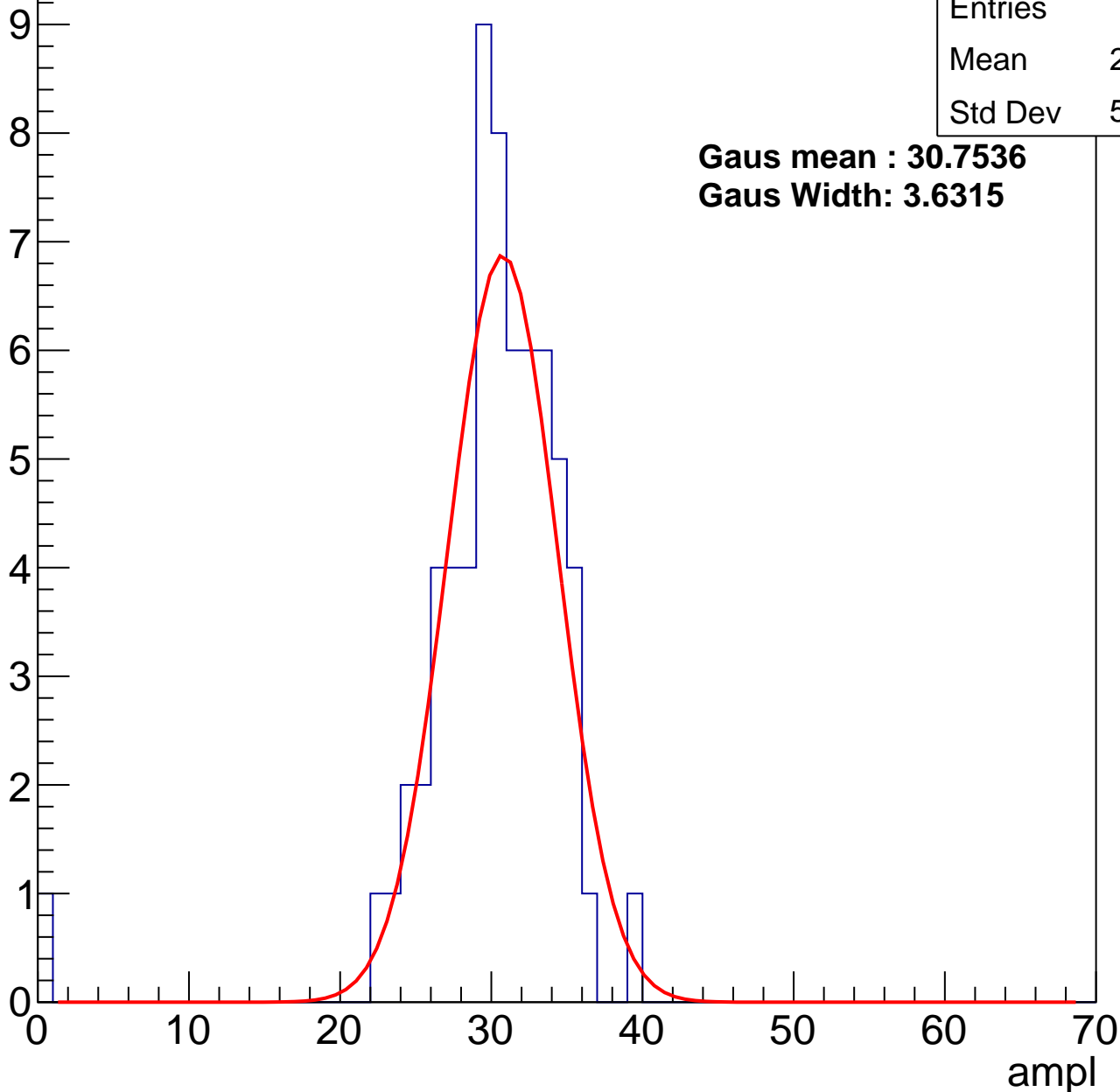
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.68
Std Dev	5.014

**Gaus mean : 30.7536**

**Gaus Width: 3.6315**



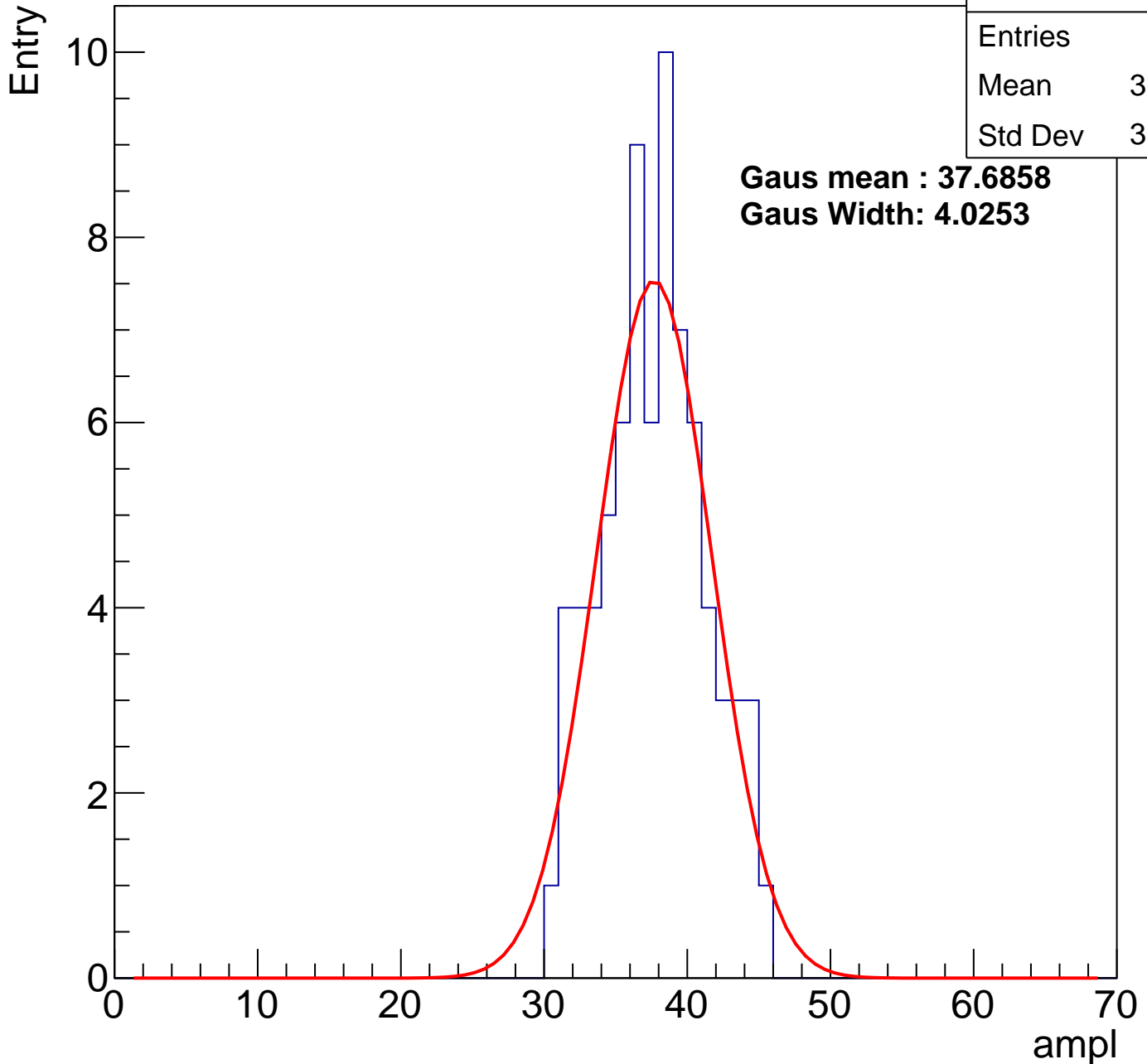
# B0L000S, U26-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	37.22
Std Dev	3.593

**Gaus mean : 37.6858**

**Gaus Width: 4.0253**



# B0L000S, U26-ch26, adc2

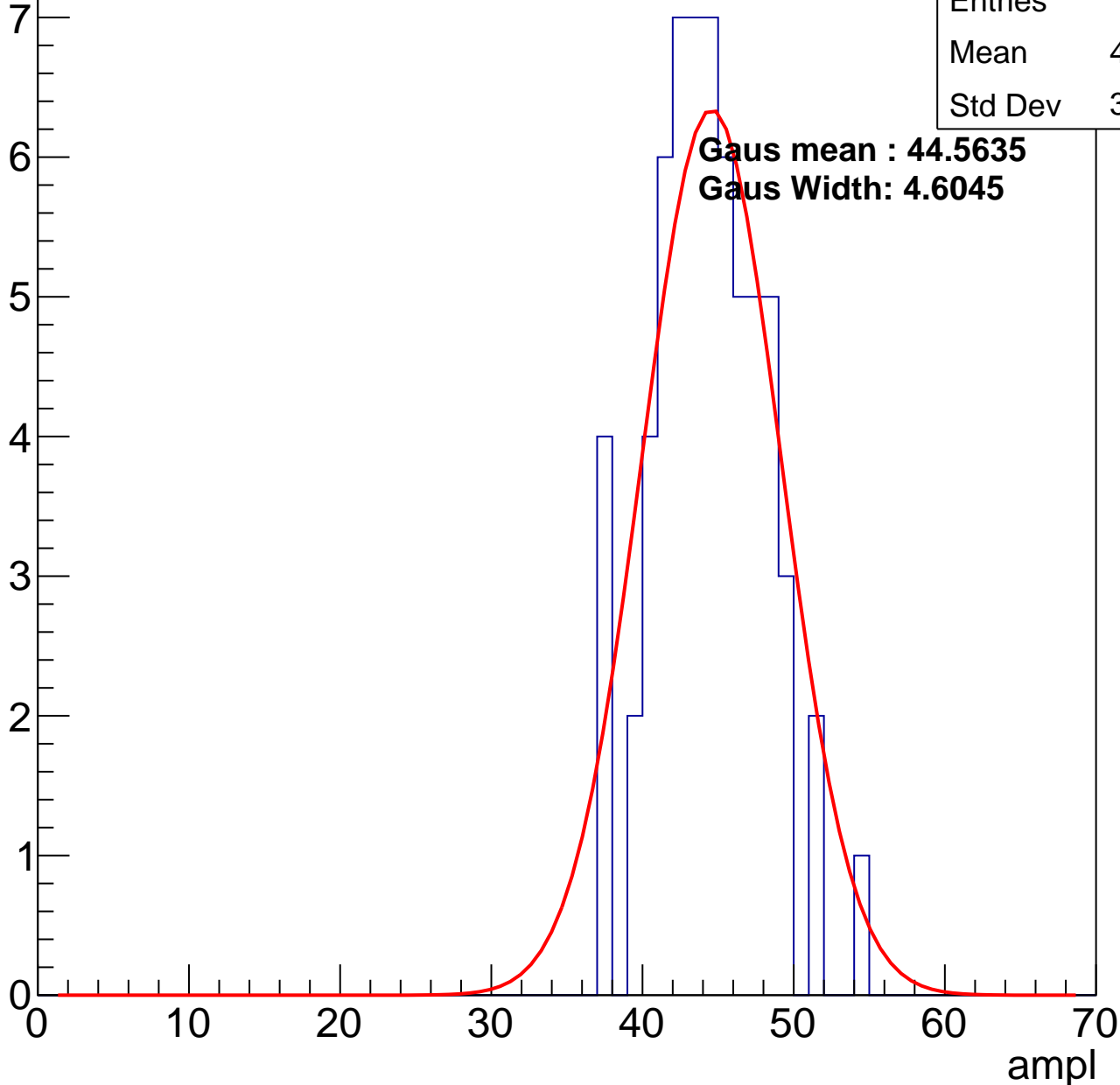
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.95
Std Dev	3.586

**Gaus mean : 44.5635**

**Gaus Width: 4.6045**



# B0L000S, U26-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

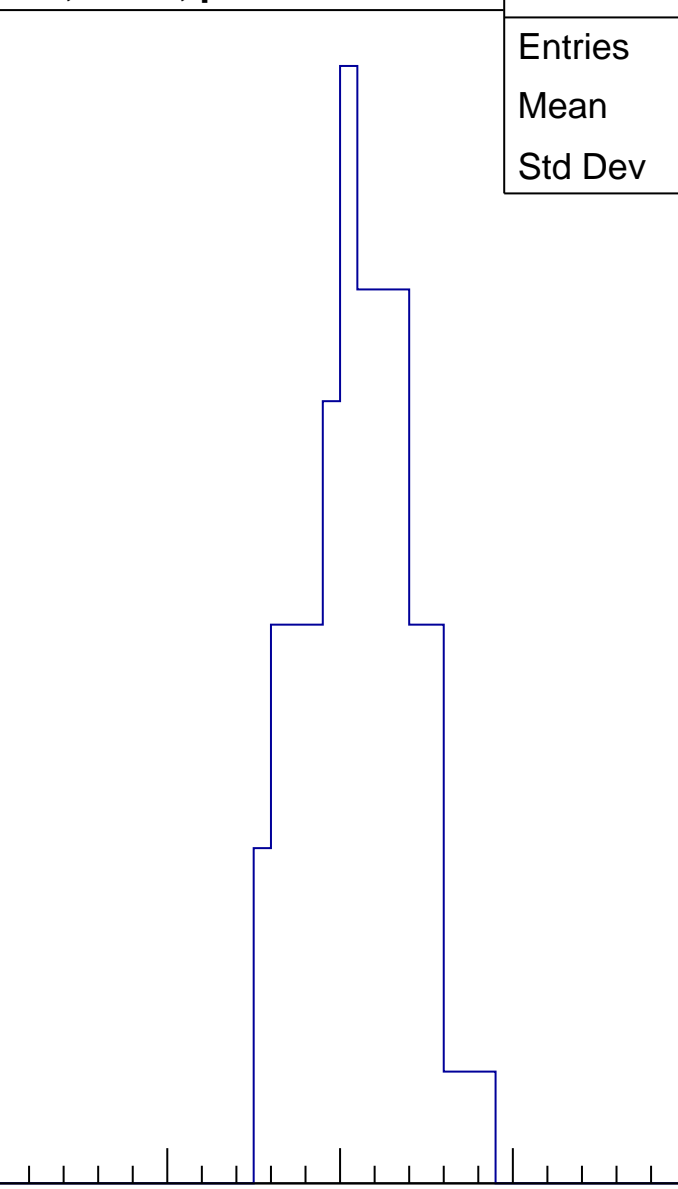
Entries	72
Mean	50.65
Std Dev	3.033

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	57.11
Std Dev	2.363

ampl

10

20

30

40

50

60

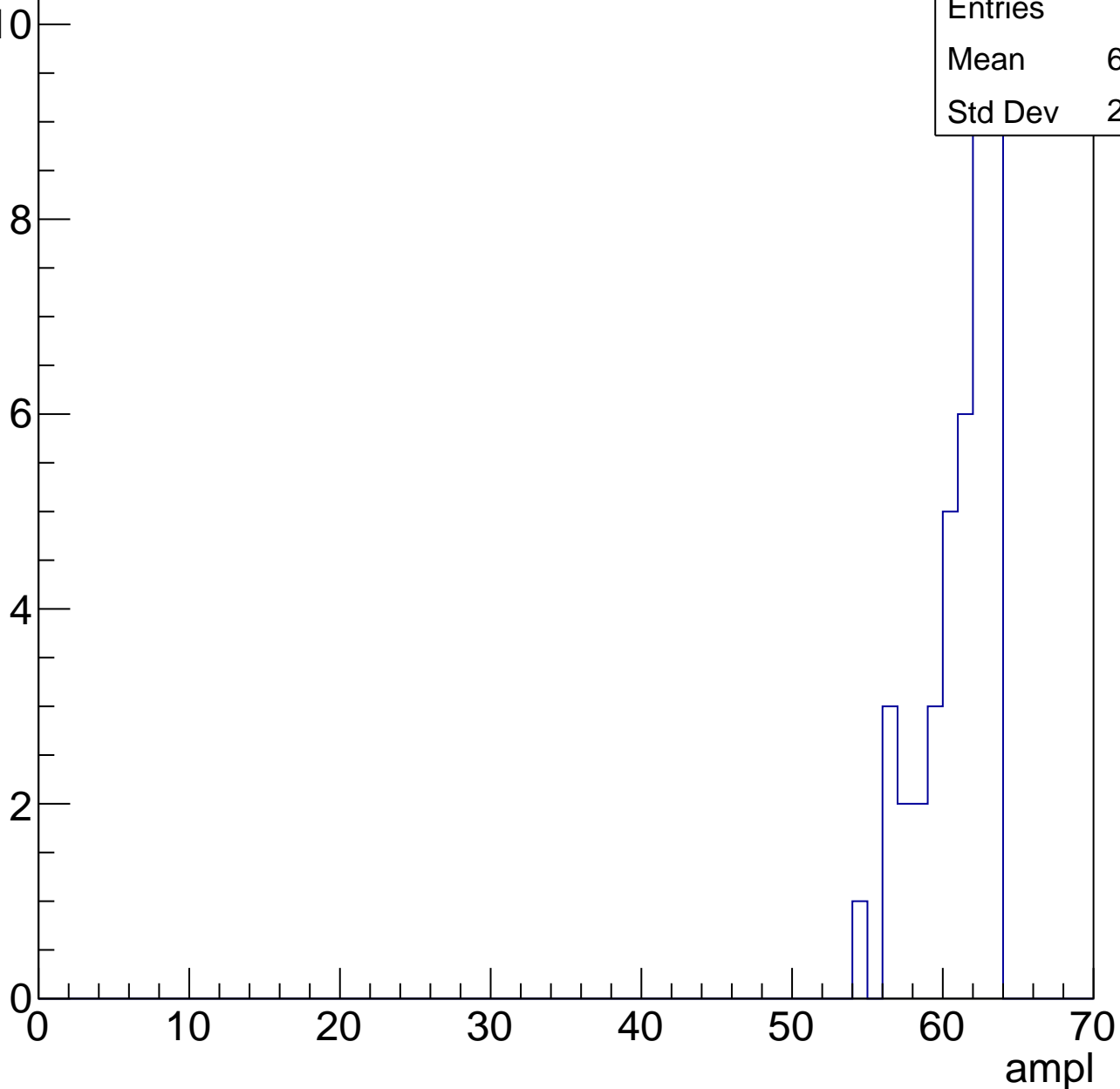
70

# B0L000S, U26-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	60.56
Std Dev	2.379



# B0L000S, U26-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch27, adc0

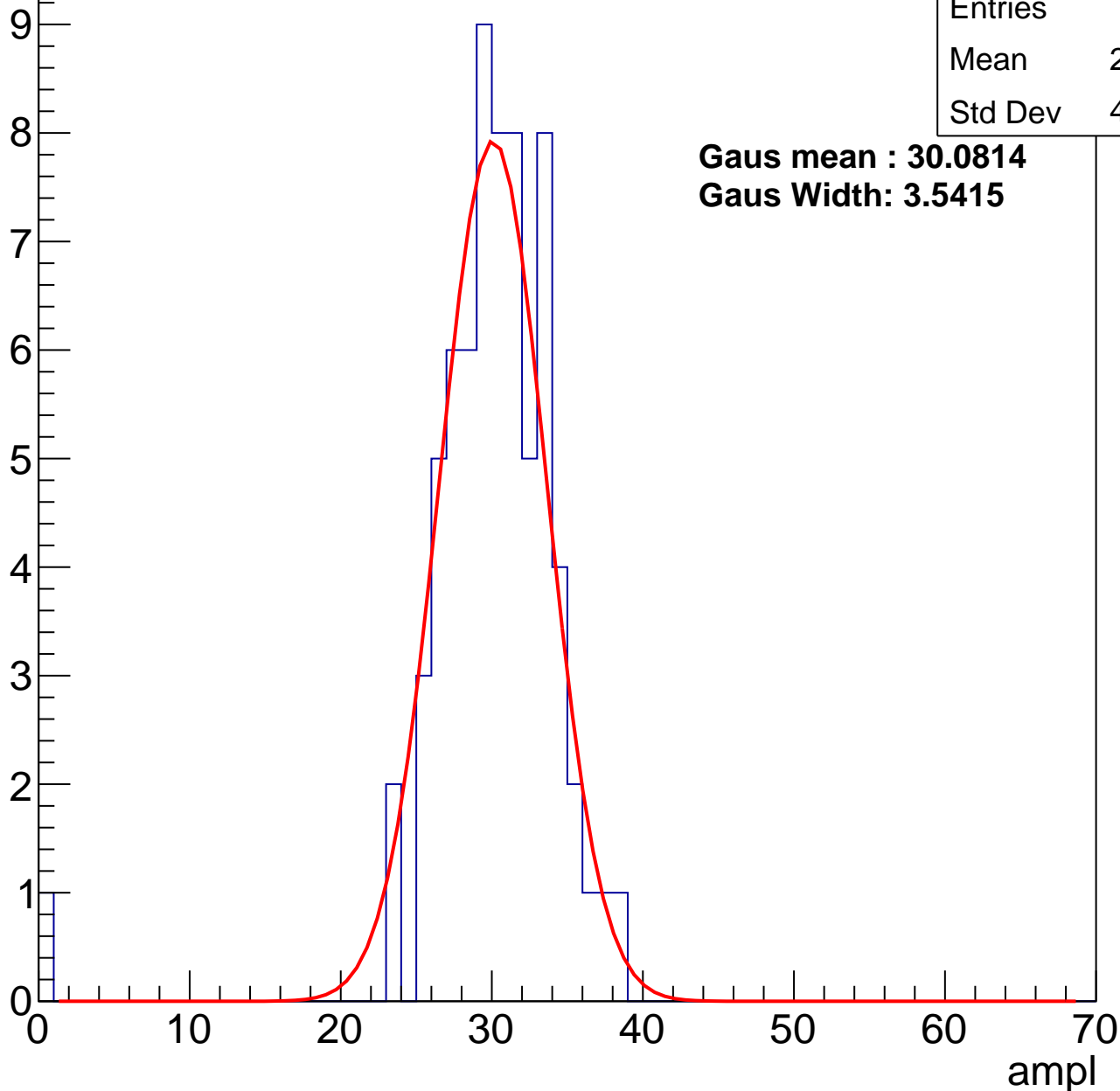
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.59
Std Dev	4.767

**Gaus mean : 30.0814**

**Gaus Width: 3.5415**



# B0L000S, U26-ch27, adc1

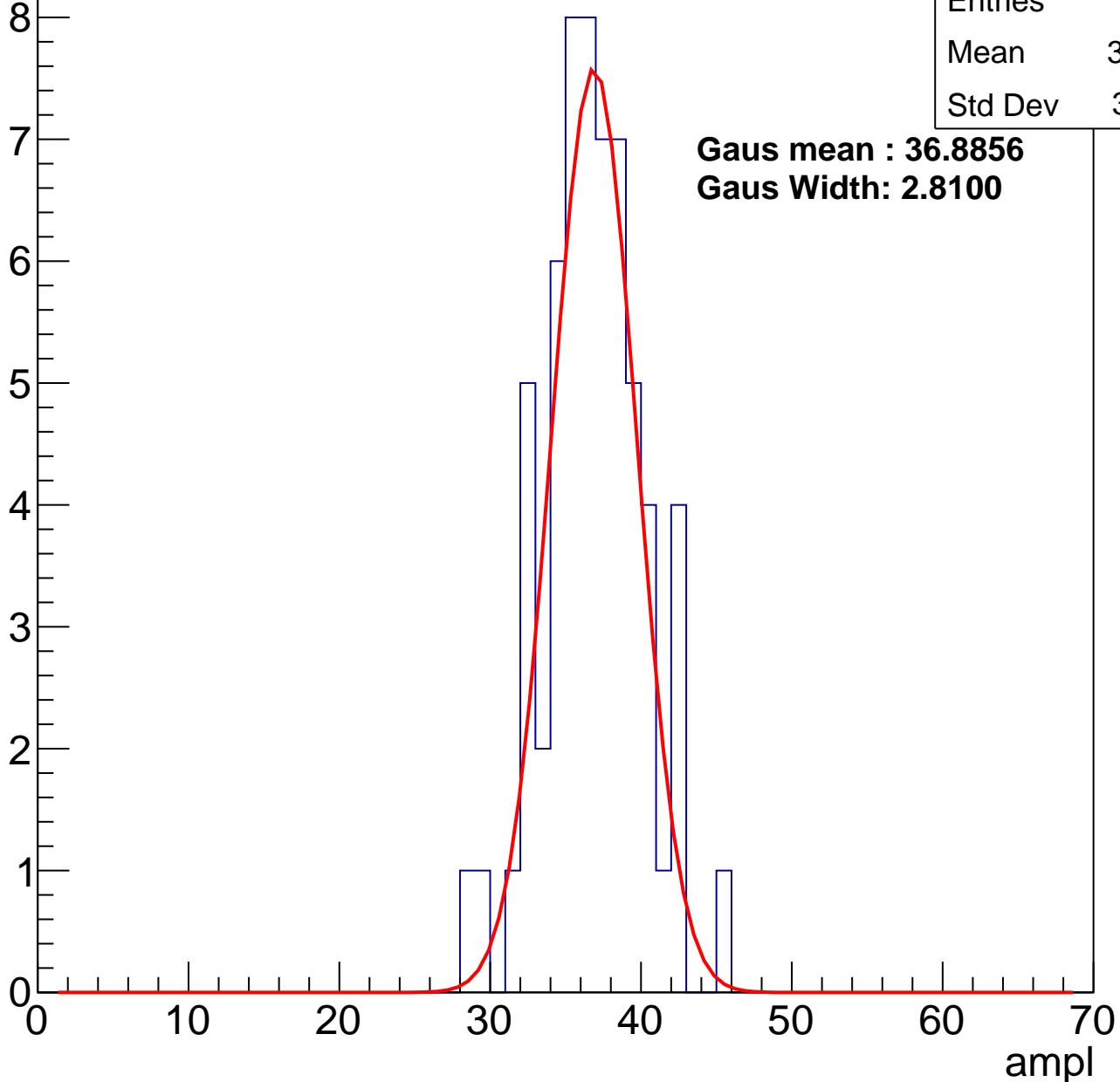
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.39
Std Dev	3.271

**Gaus mean : 36.8856**

**Gaus Width: 2.8100**



# B0L000S, U26-ch27, adc2

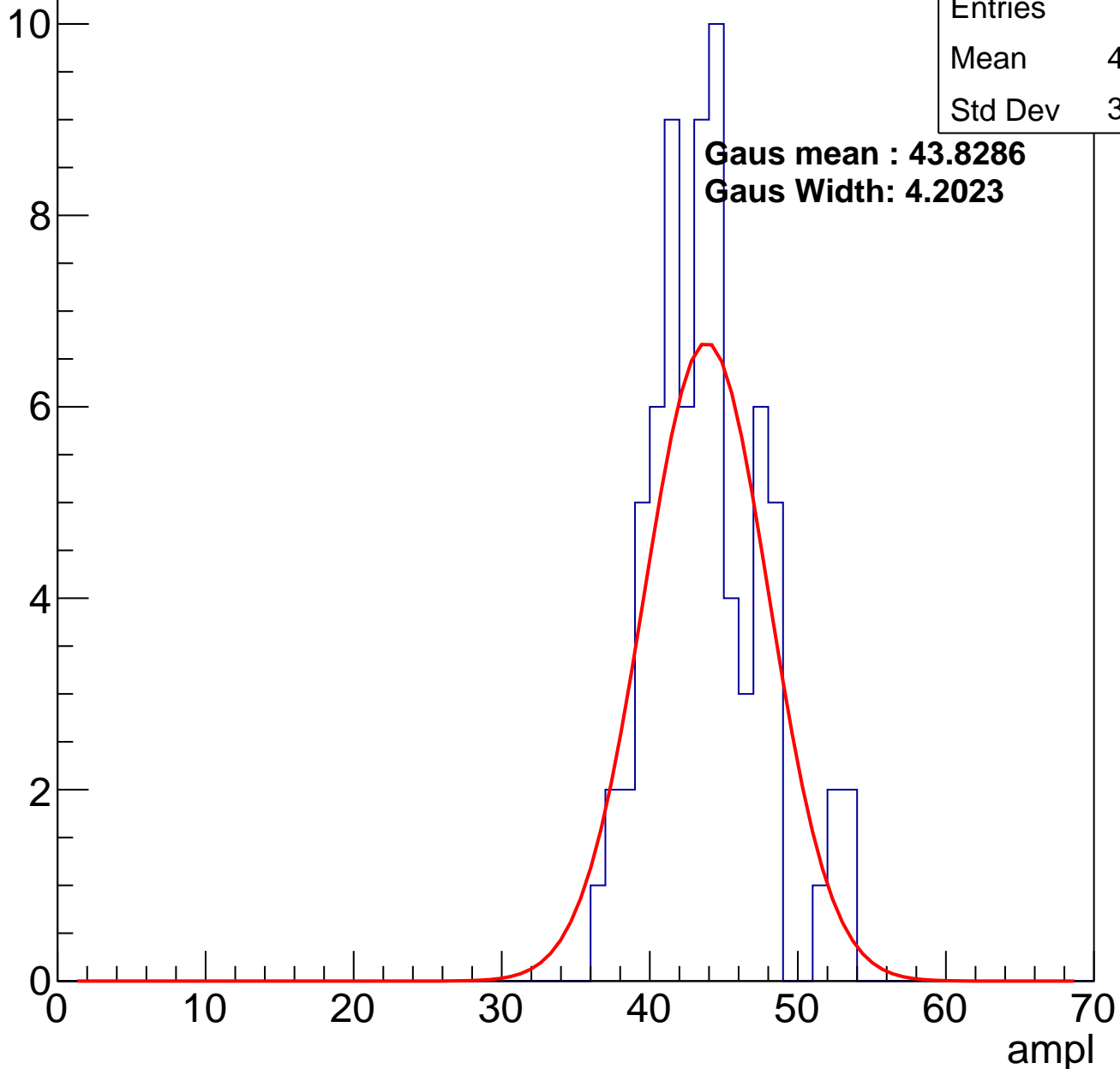
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	43.42
Std Dev	3.763

**Gaus mean : 43.8286**

**Gaus Width: 4.2023**

Entry

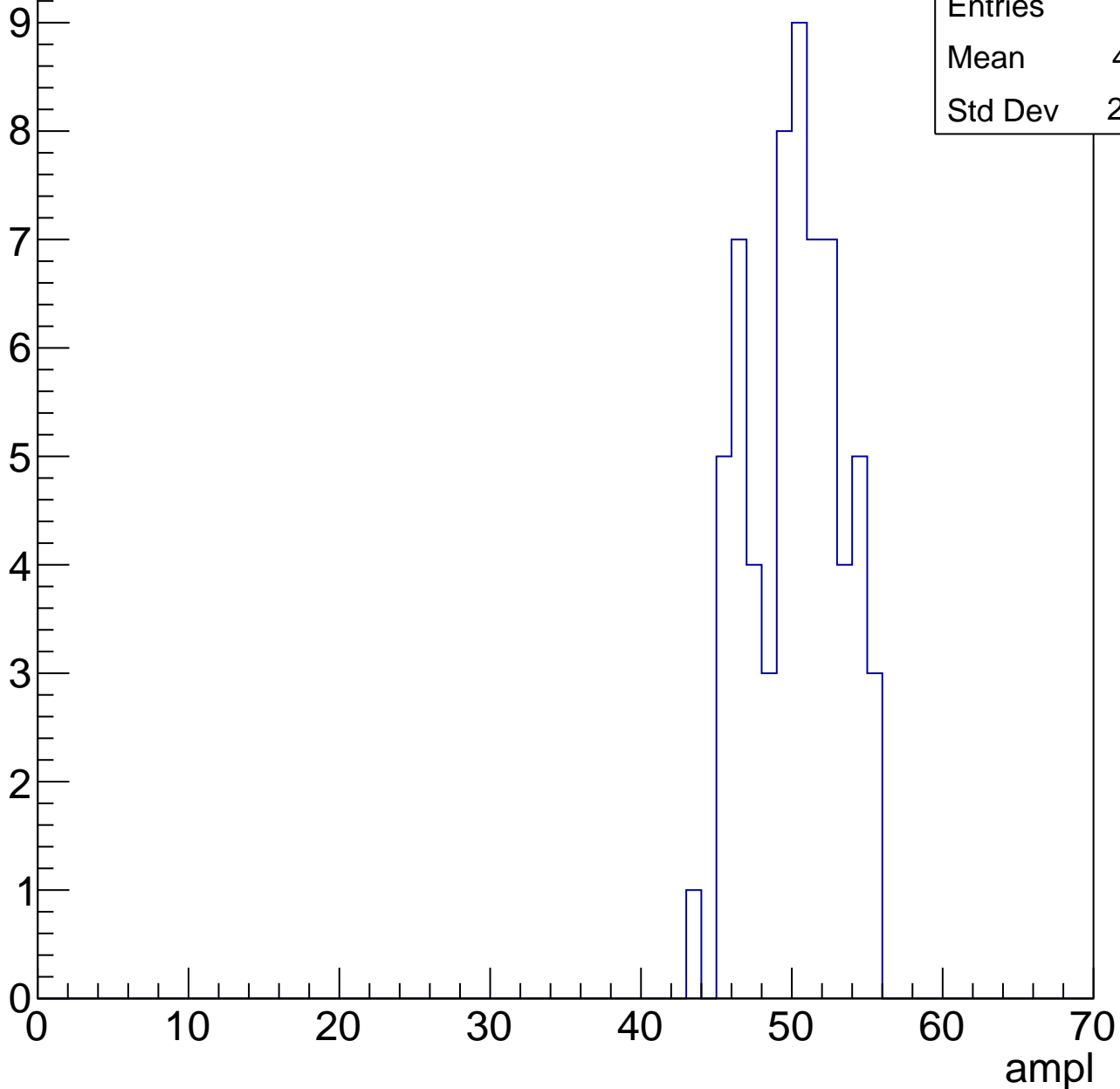


# B0L000S, U26-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	49.71
Std Dev	2.989

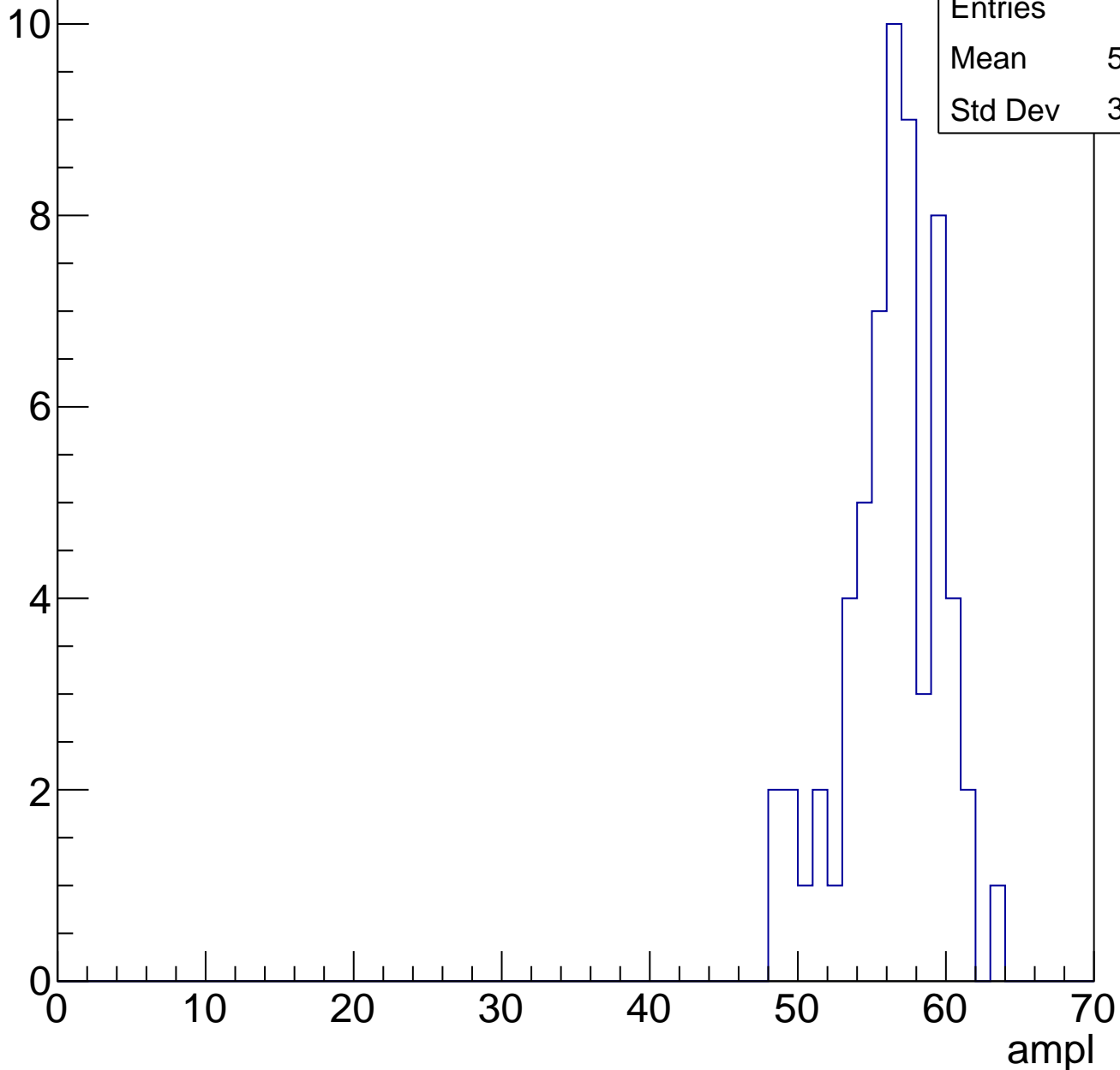


# B0L000S, U26-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	61
Mean	55.89
Std Dev	3.255

Entry

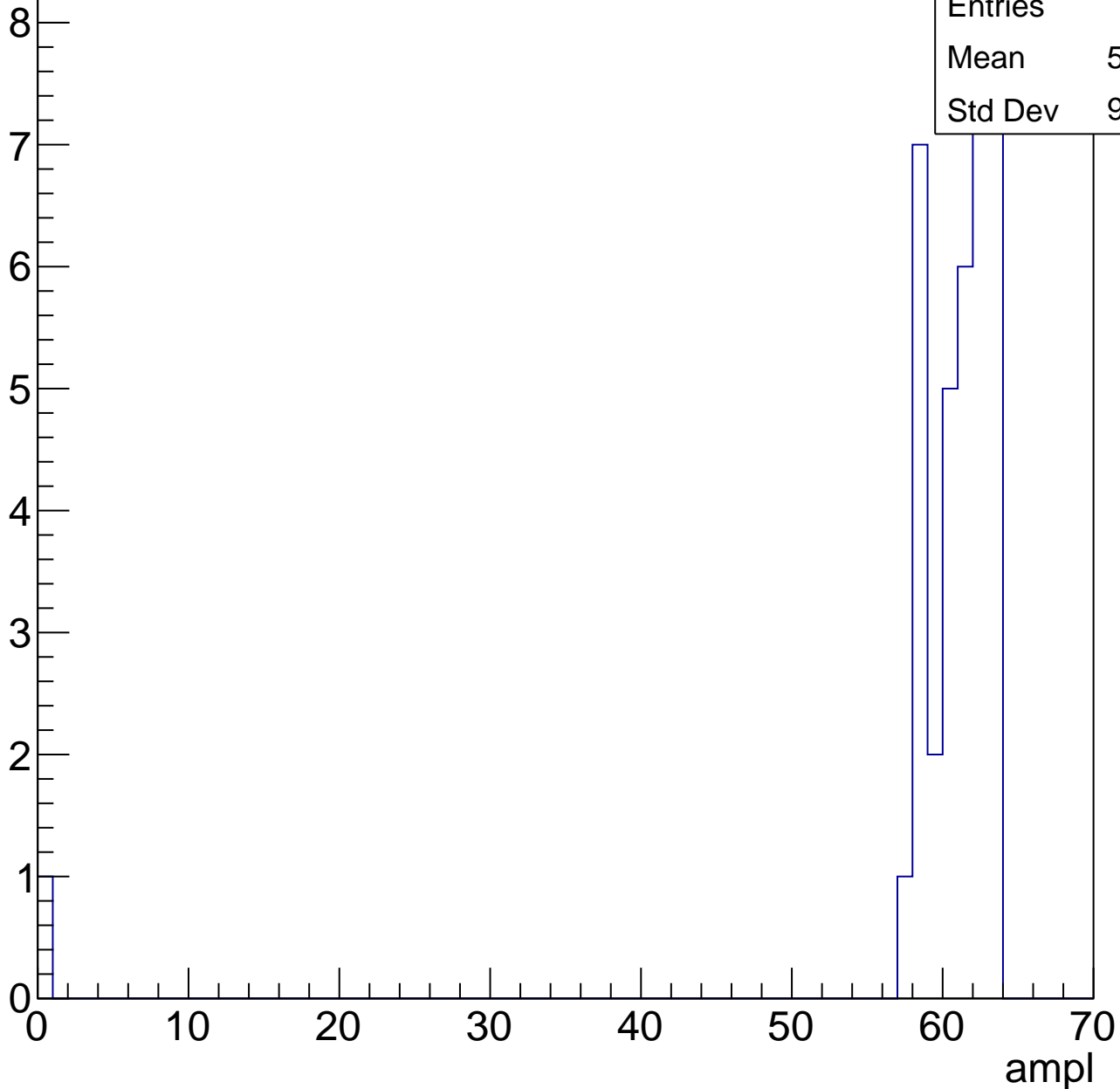


# B0L000S, U26-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	59.13
Std Dev	9.895



# B0L000S, U26-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

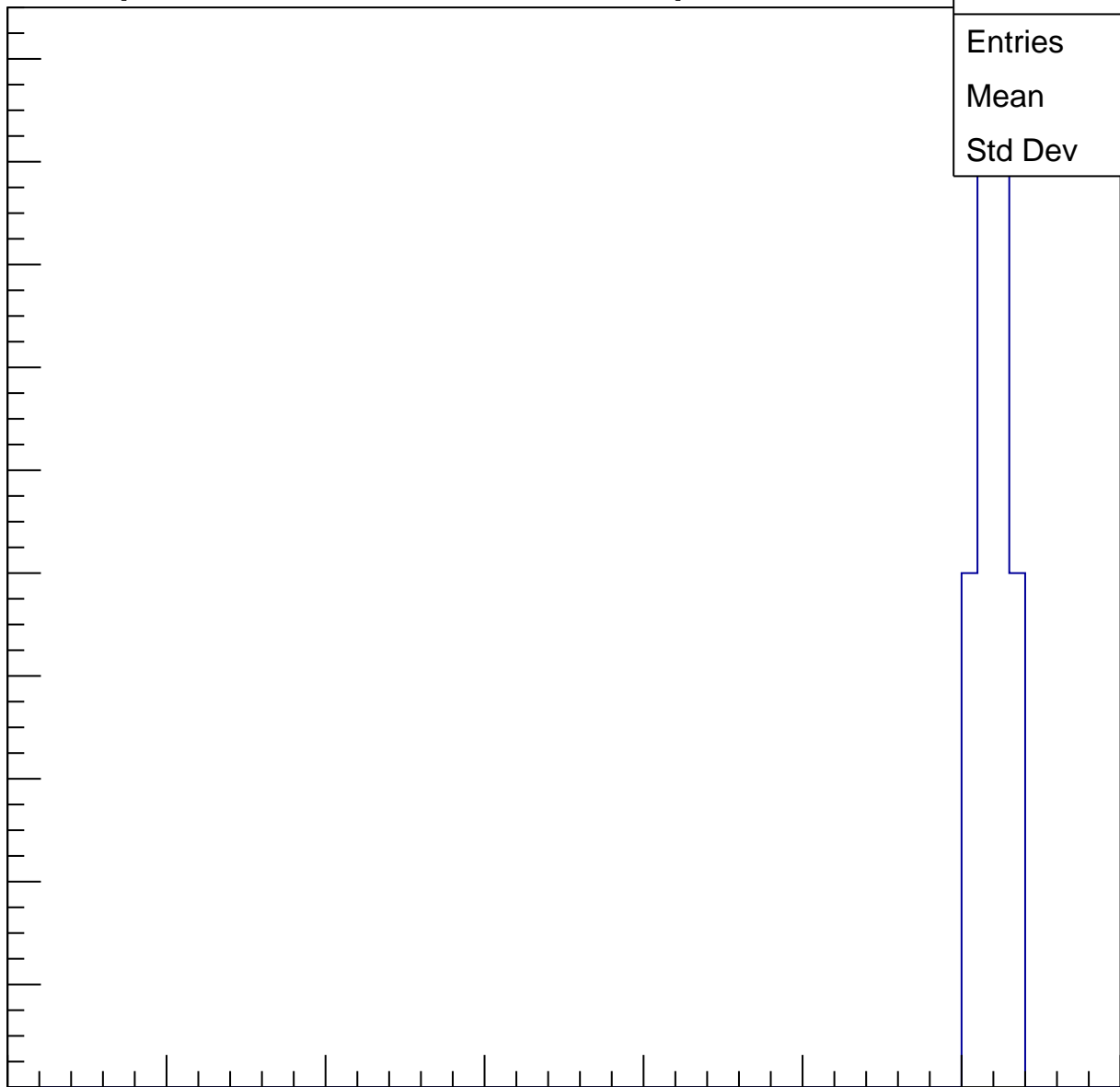
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	0.9574

0 10 20 30 40 50 60 70

ampl





# B0L000S, U26-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch28, adc0

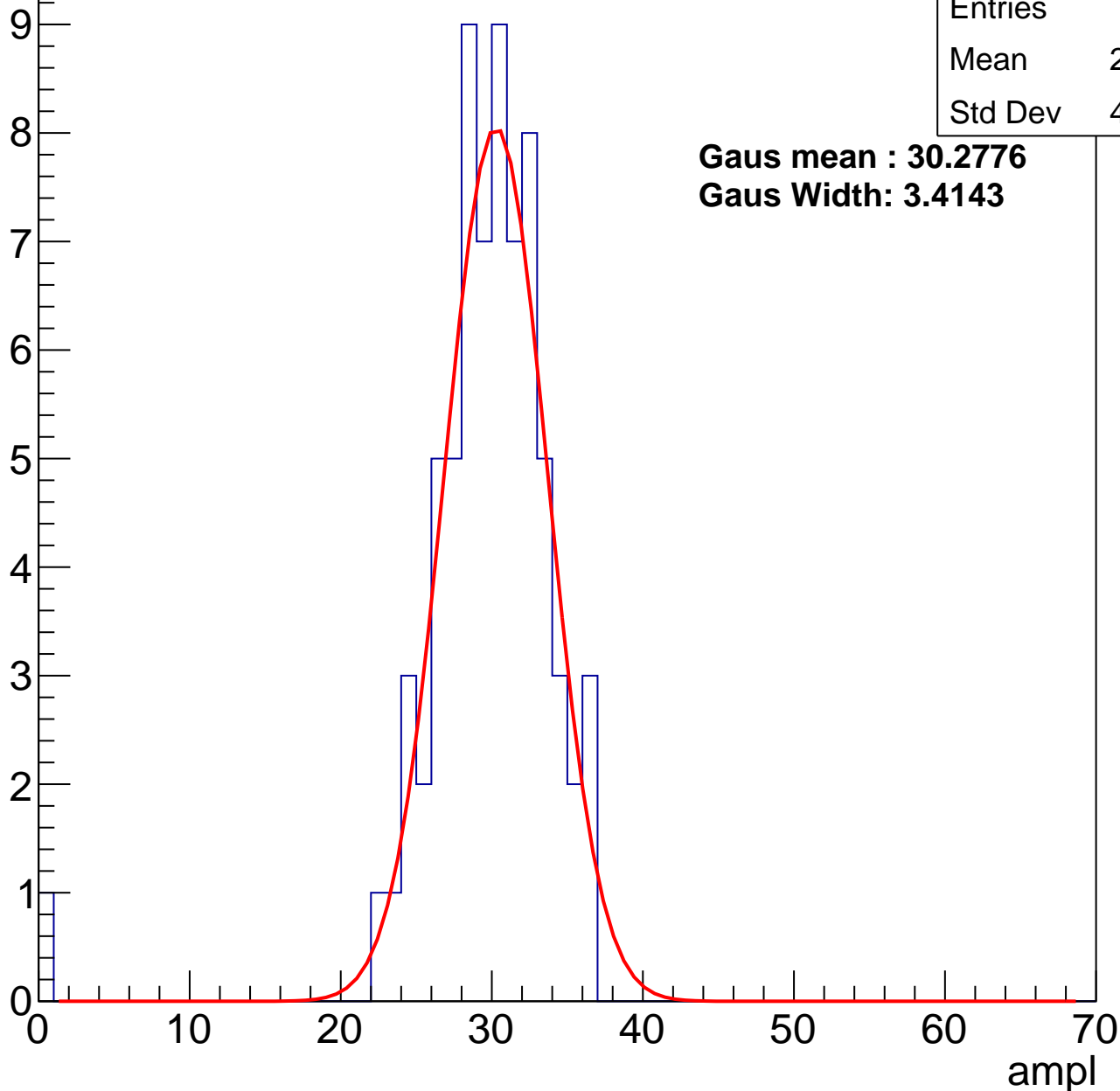
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	29.23
Std Dev	4.727

**Gaus mean : 30.2776**

**Gaus Width: 3.4143**



# B0L000S, U26-ch28, adc1

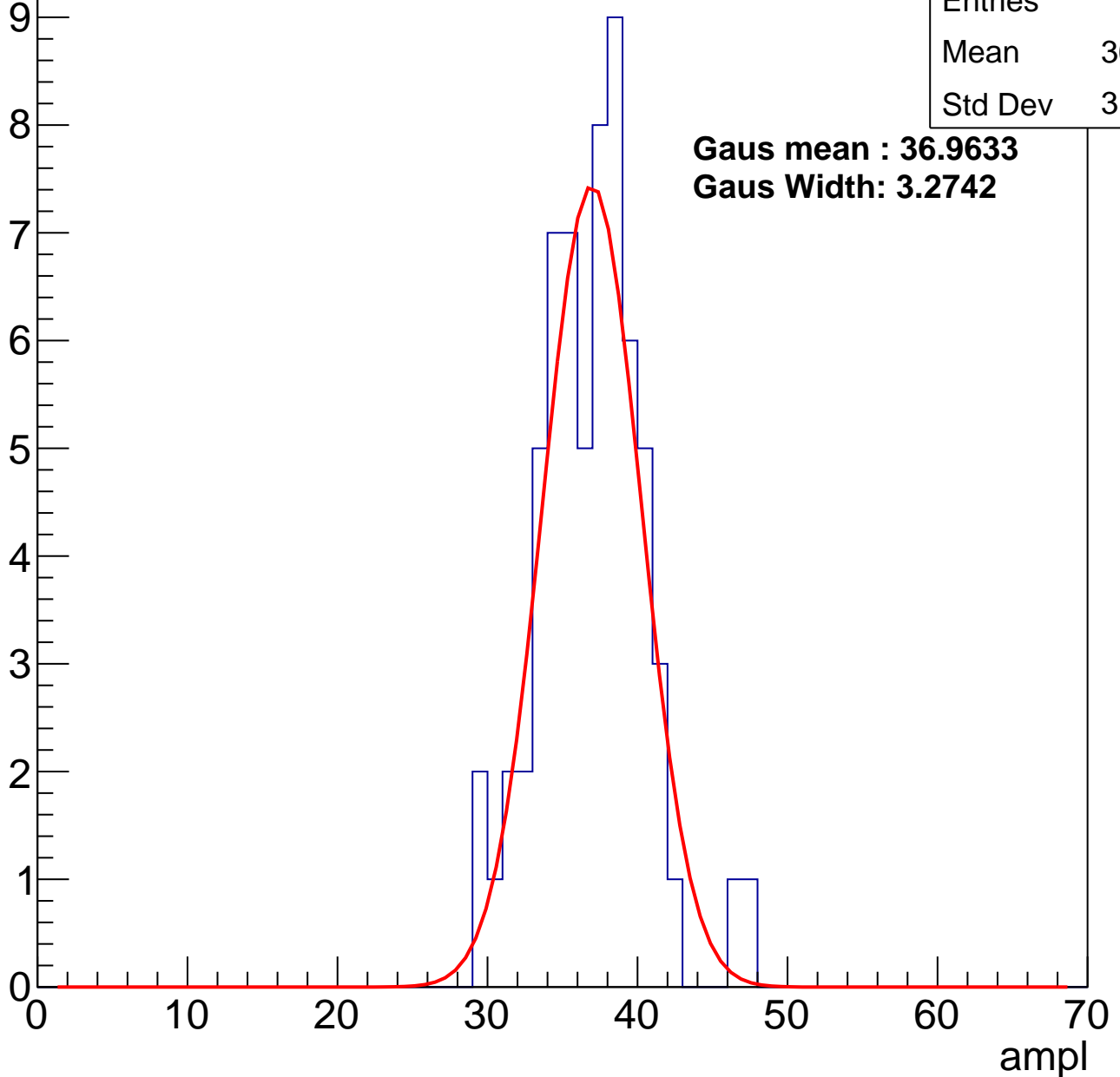
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.49
Std Dev	3.487

**Gaus mean : 36.9633**

**Gaus Width: 3.2742**



# B0L000S, U26-ch28, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	60
Mean	42.55
Std Dev	3.18

**Gaus mean : 43.2292**

**Gaus Width: 2.8208**

Entry

10

8

6

4

2

0

0

10

20

30

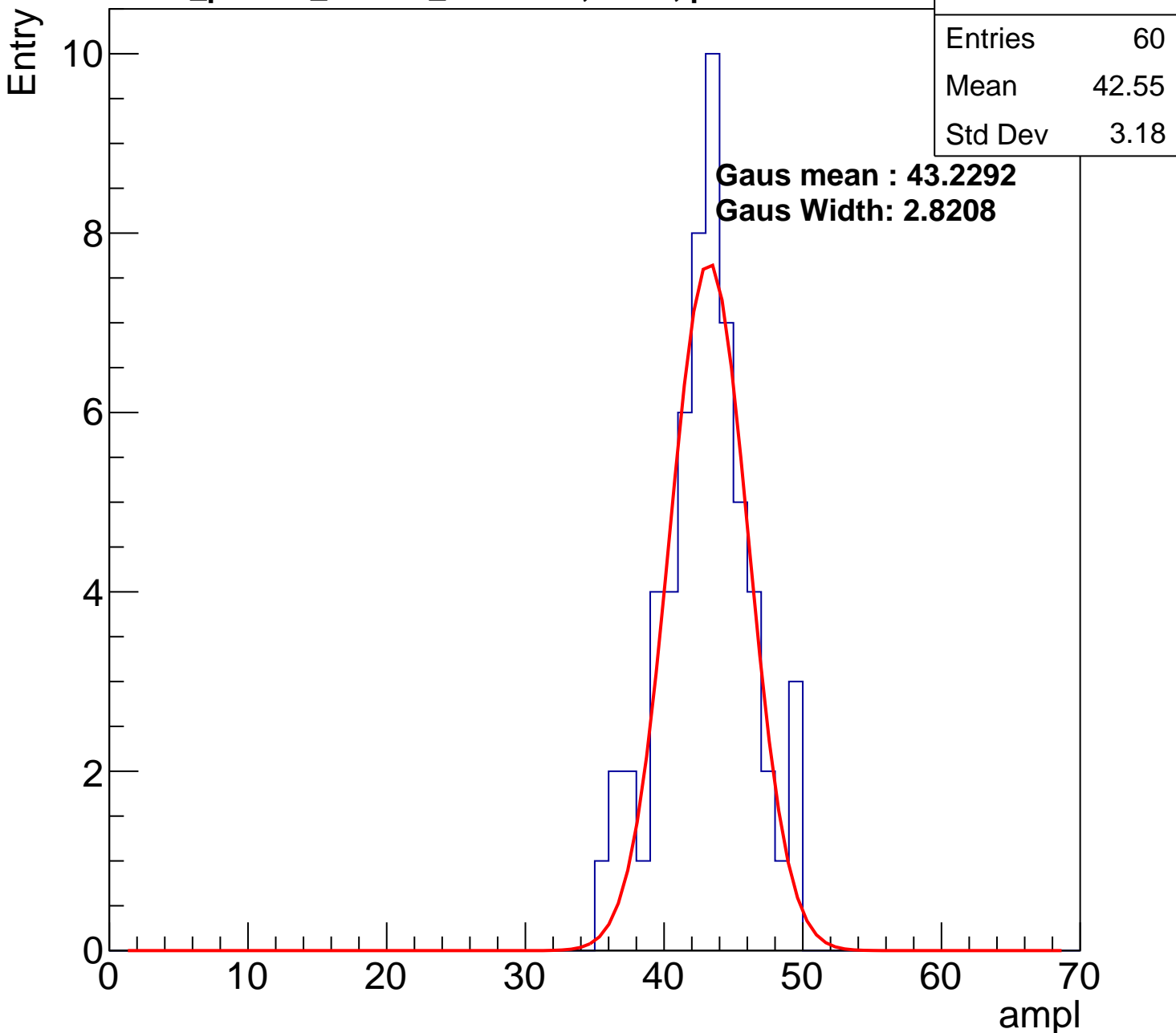
40

50

60

70

ampl

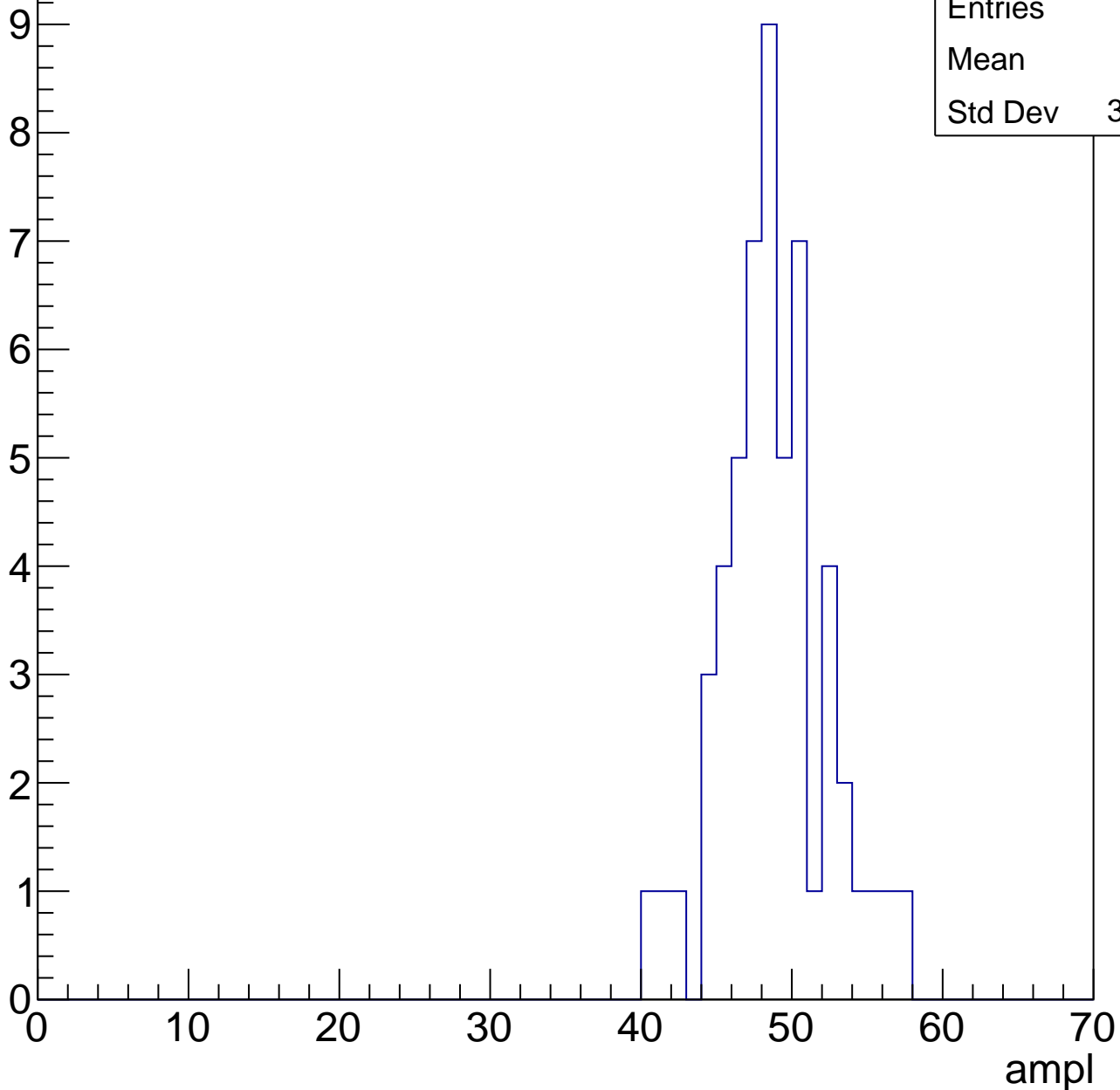


# B0L000S, U26-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	48.3
Std Dev	3.446

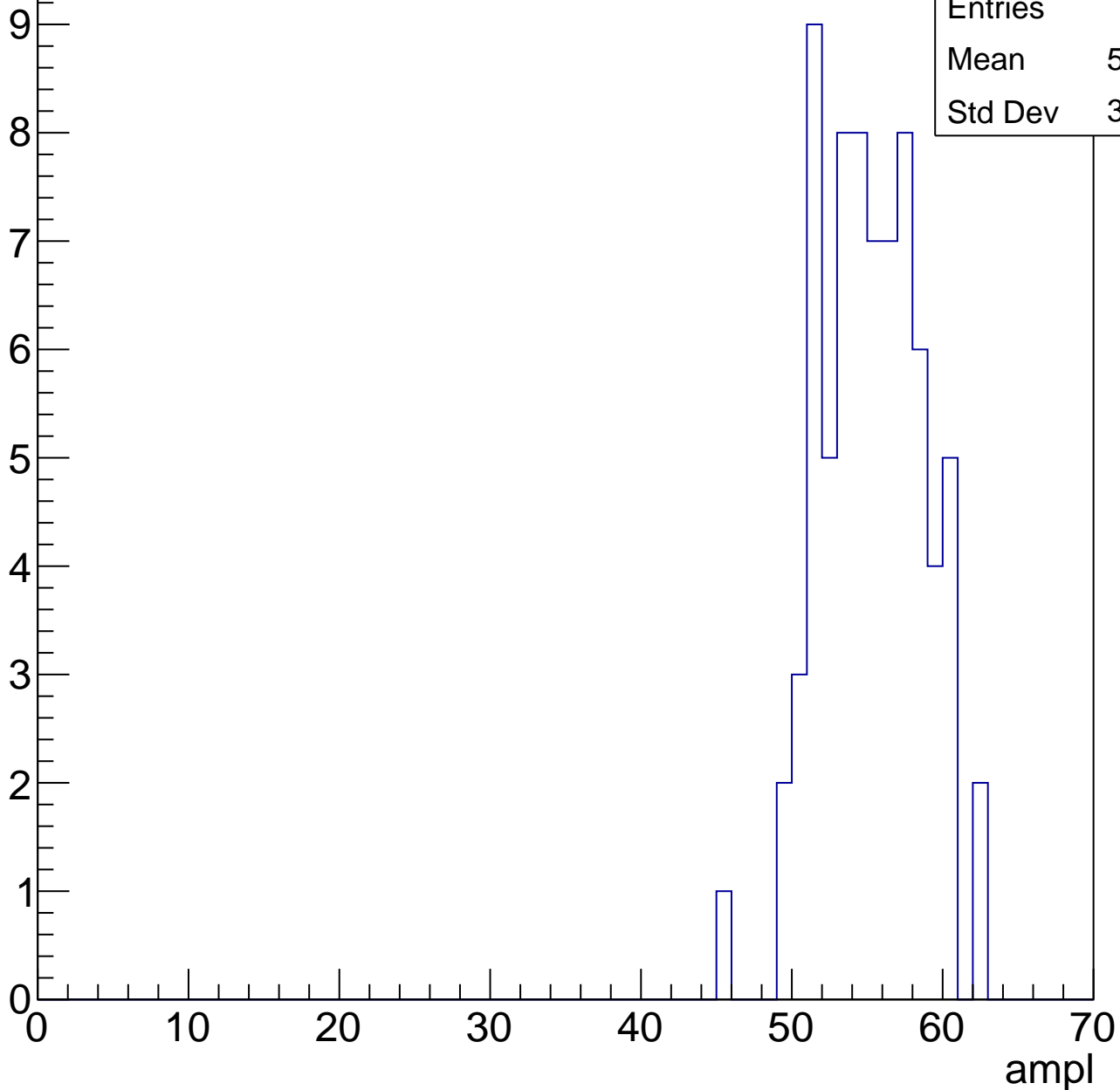


# B0L000S, U26-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	54.79
Std Dev	3.372

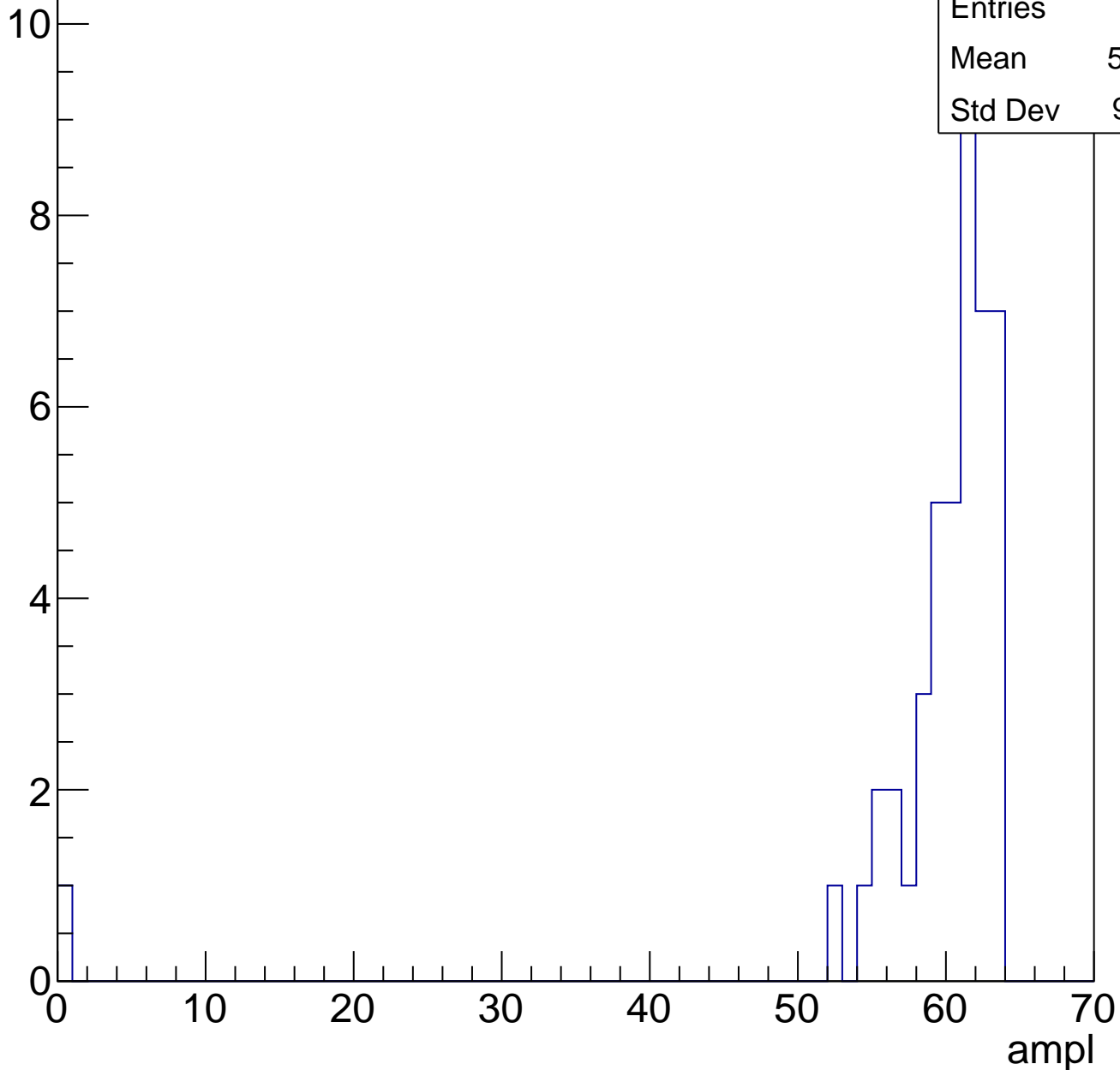


# B0L000S, U26-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	45
Mean	58.64
Std Dev	9.221

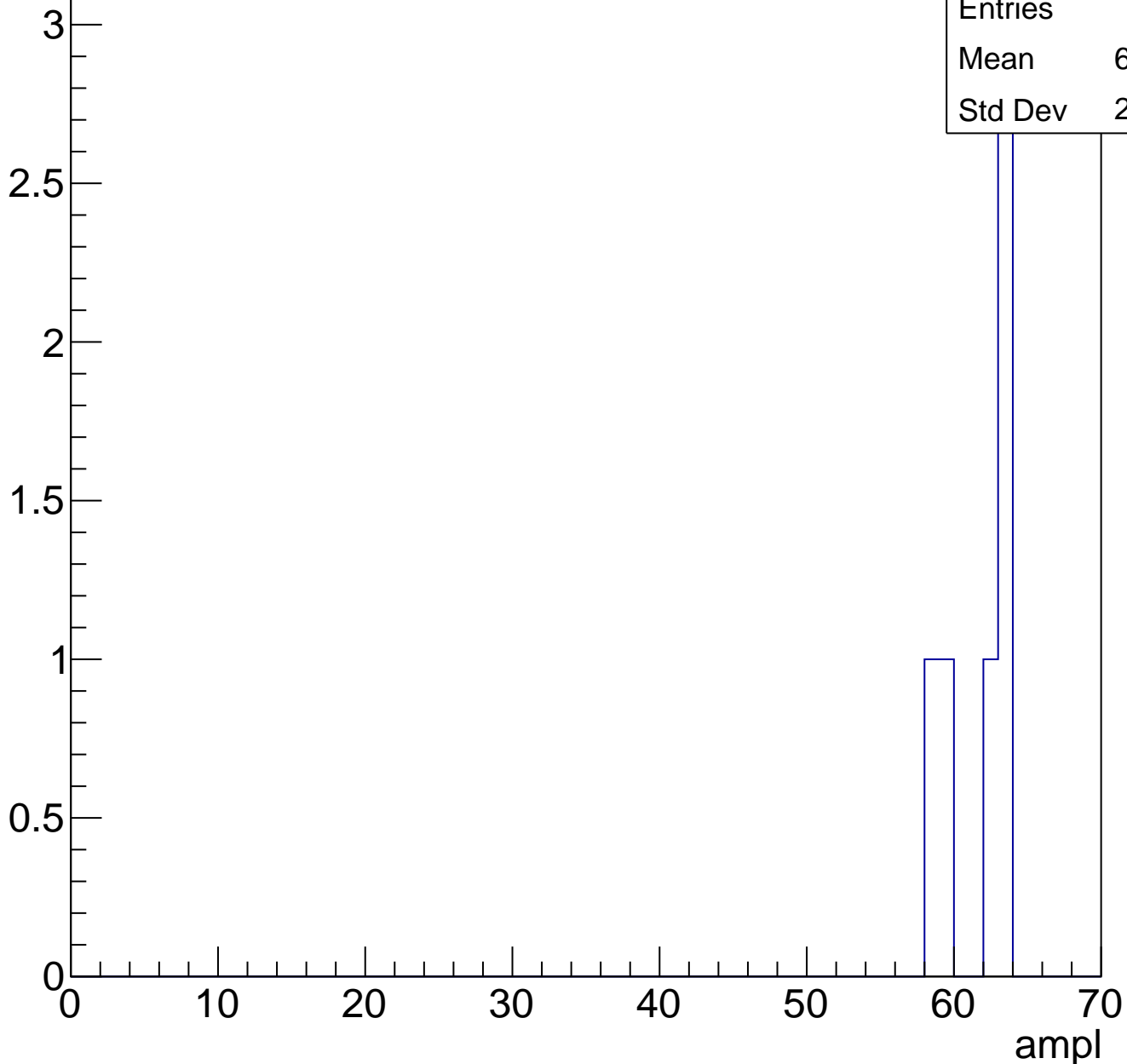
Entry



# B0L000S, U26-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch29, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	95
Mean	31.36
Std Dev	4.95

**Gaus mean : 32.1067**

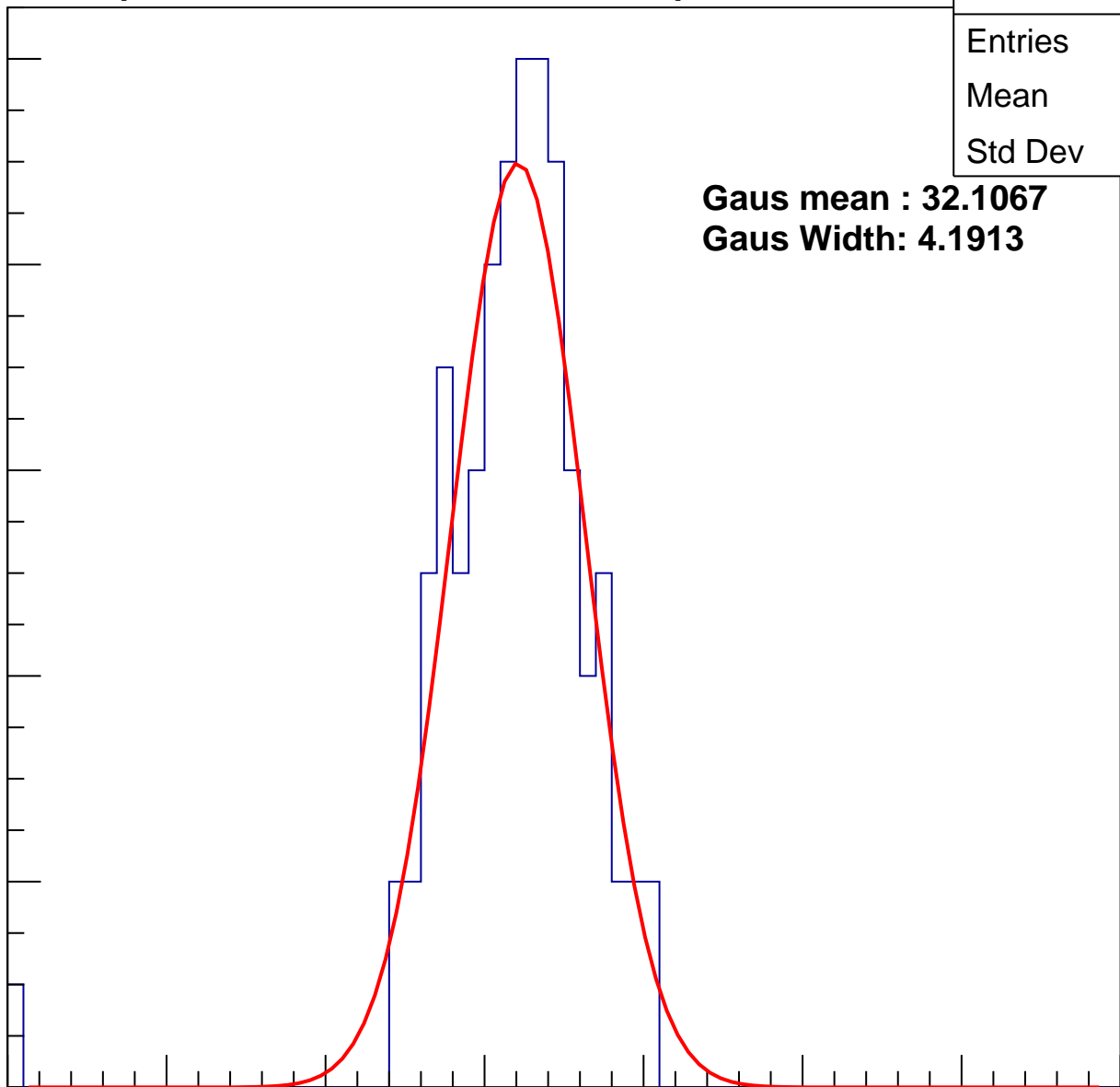
**Gaus Width: 4.1913**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch29, adc1

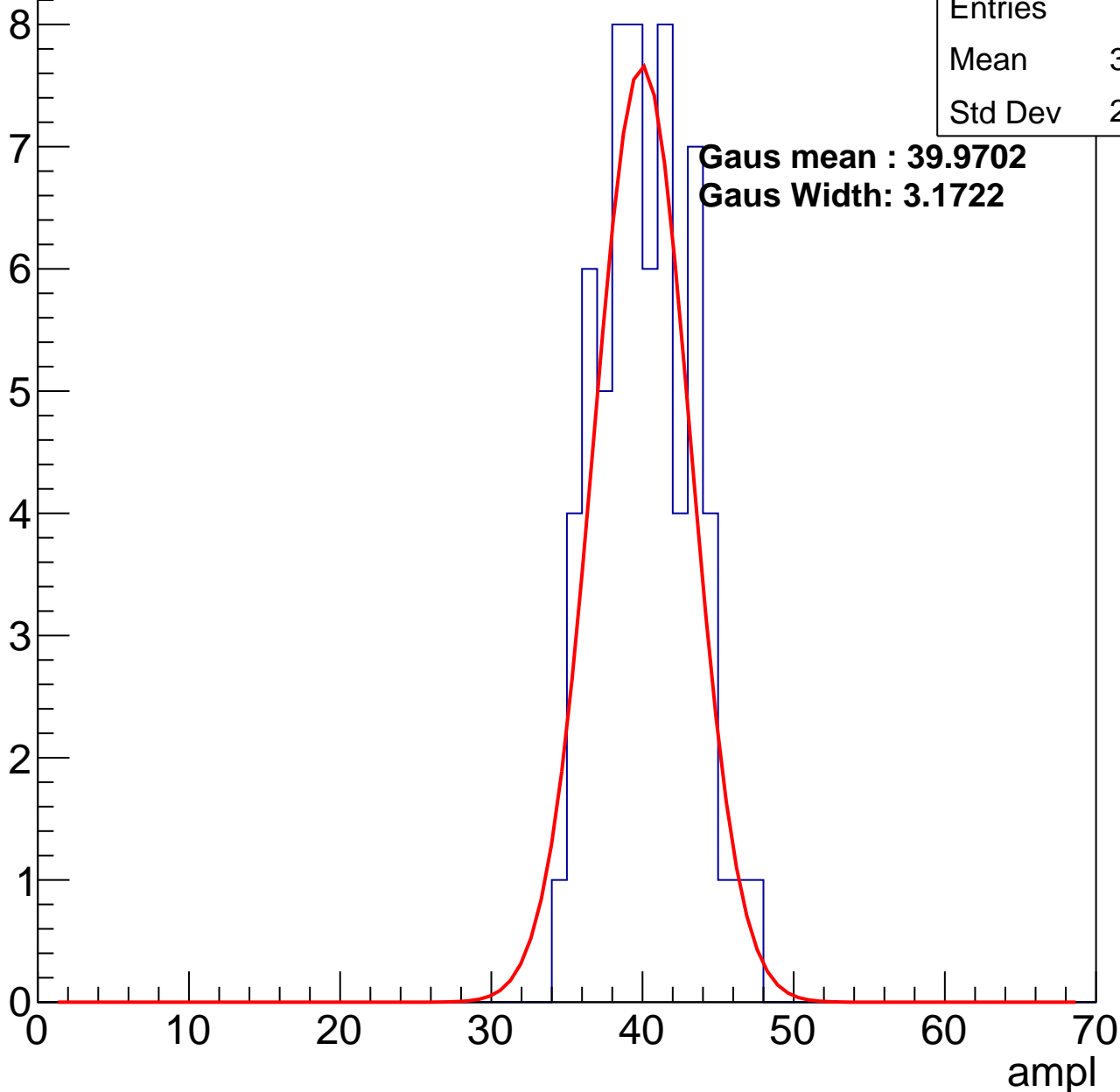
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	39.72
Std Dev	2.992

**Gaus mean : 39.9702**

**Gaus Width: 3.1722**



# B0L000S, U26-ch29, adc2

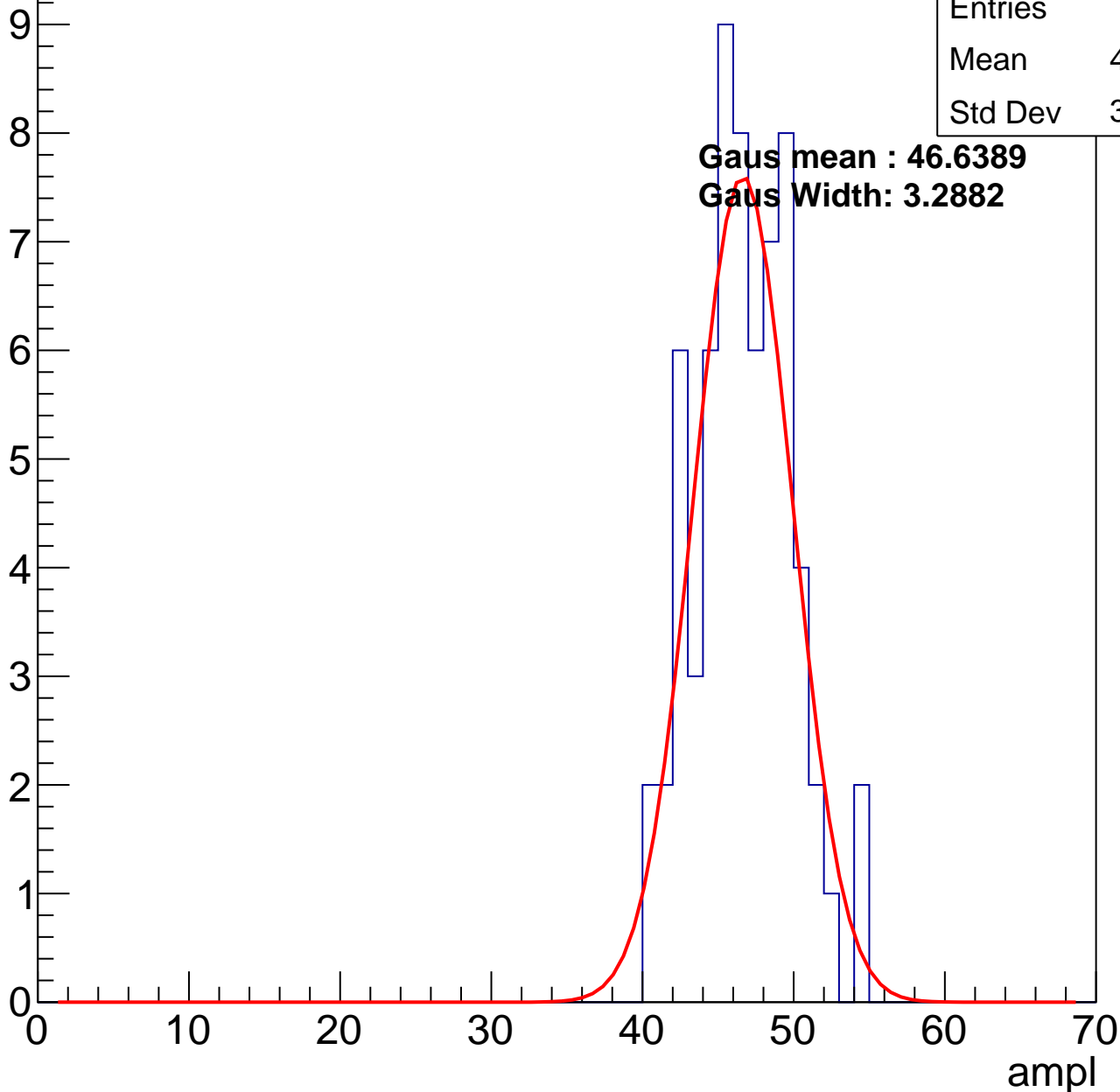
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	46.24
Std Dev	3.158

Gaus mean : 46.6389

Gaus Width: 3.2882

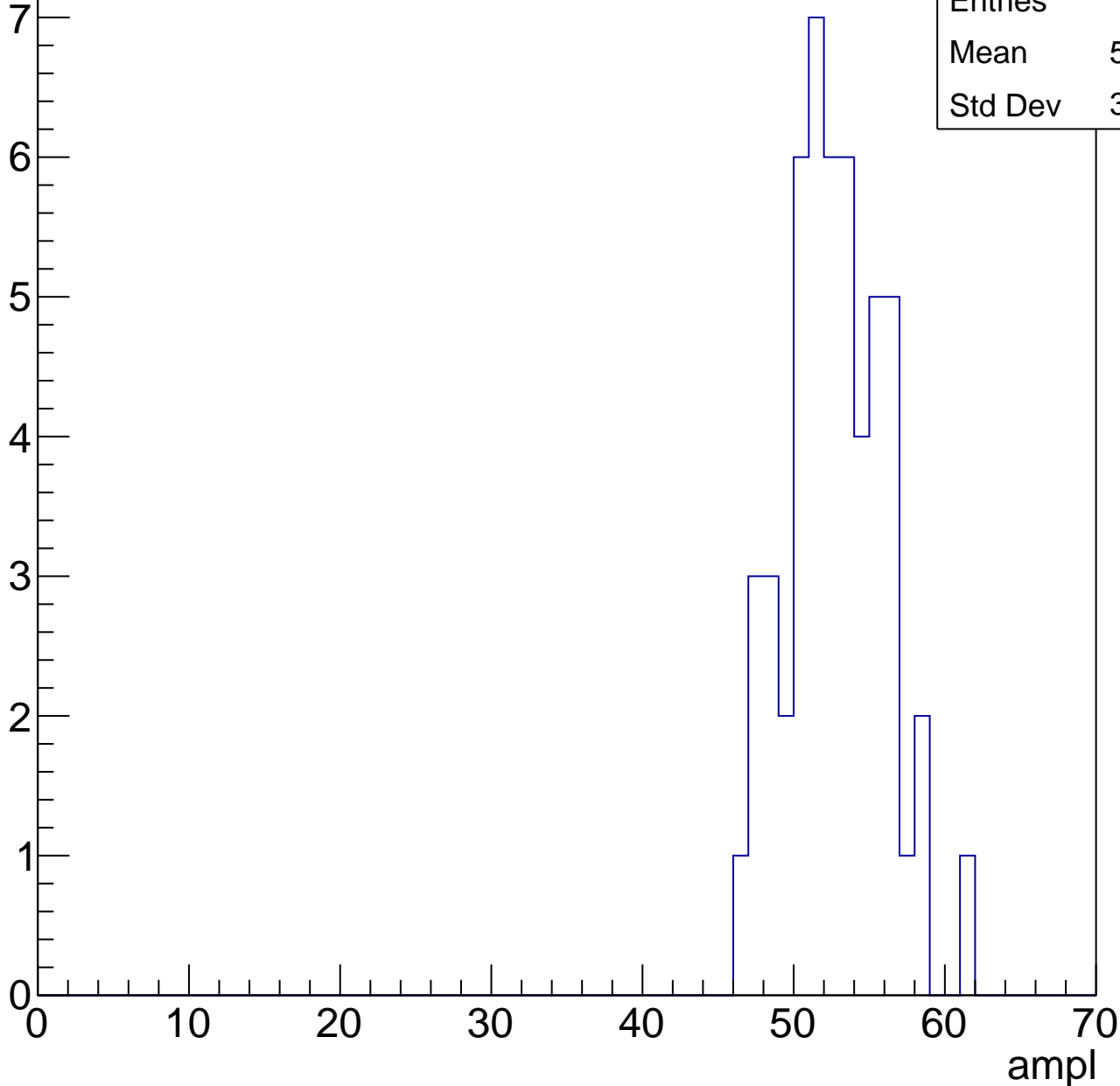


# B0L000S, U26-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

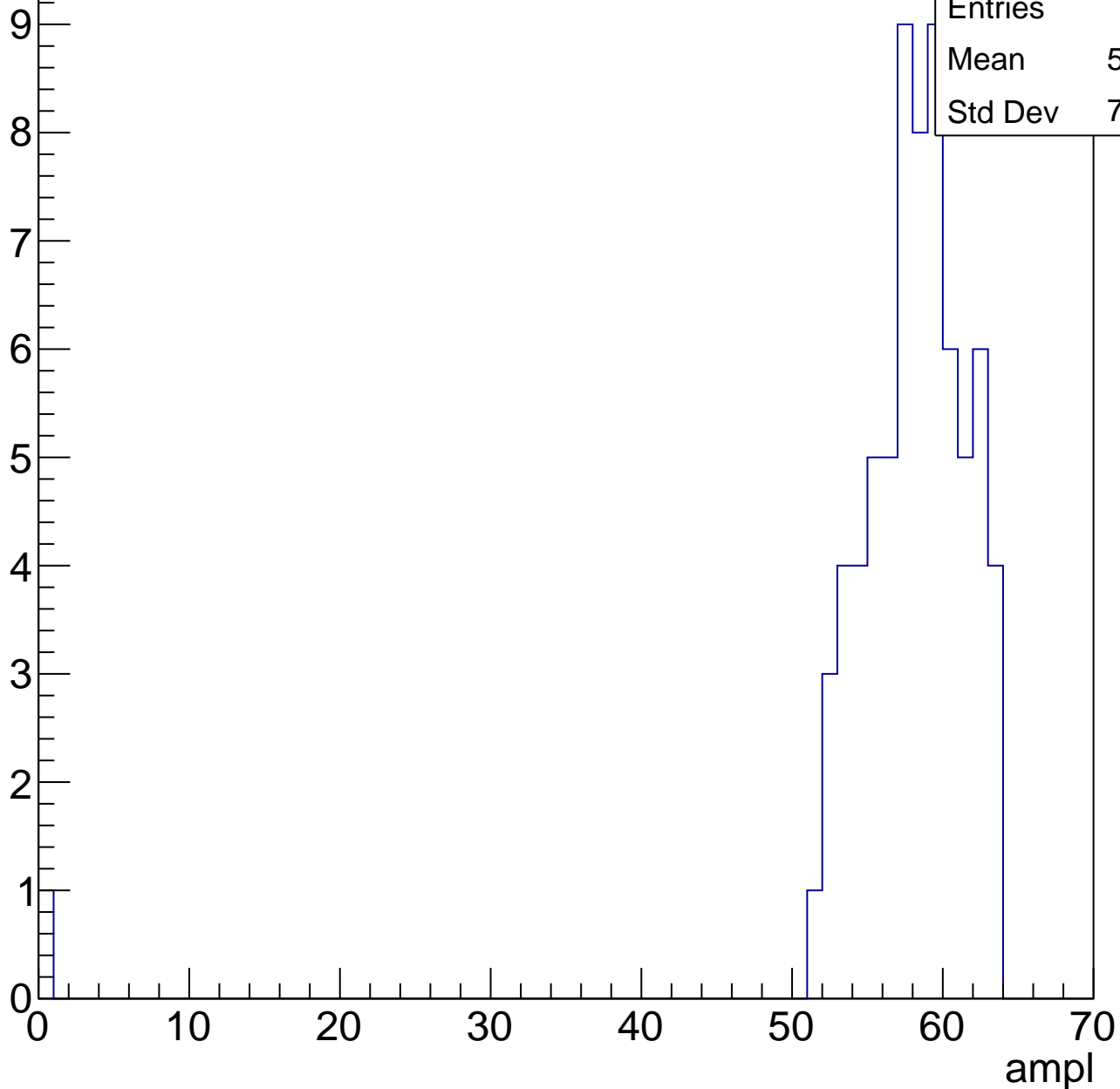
Entries	52
Mean	52.33
Std Dev	3.185



# B0L000S, U26-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

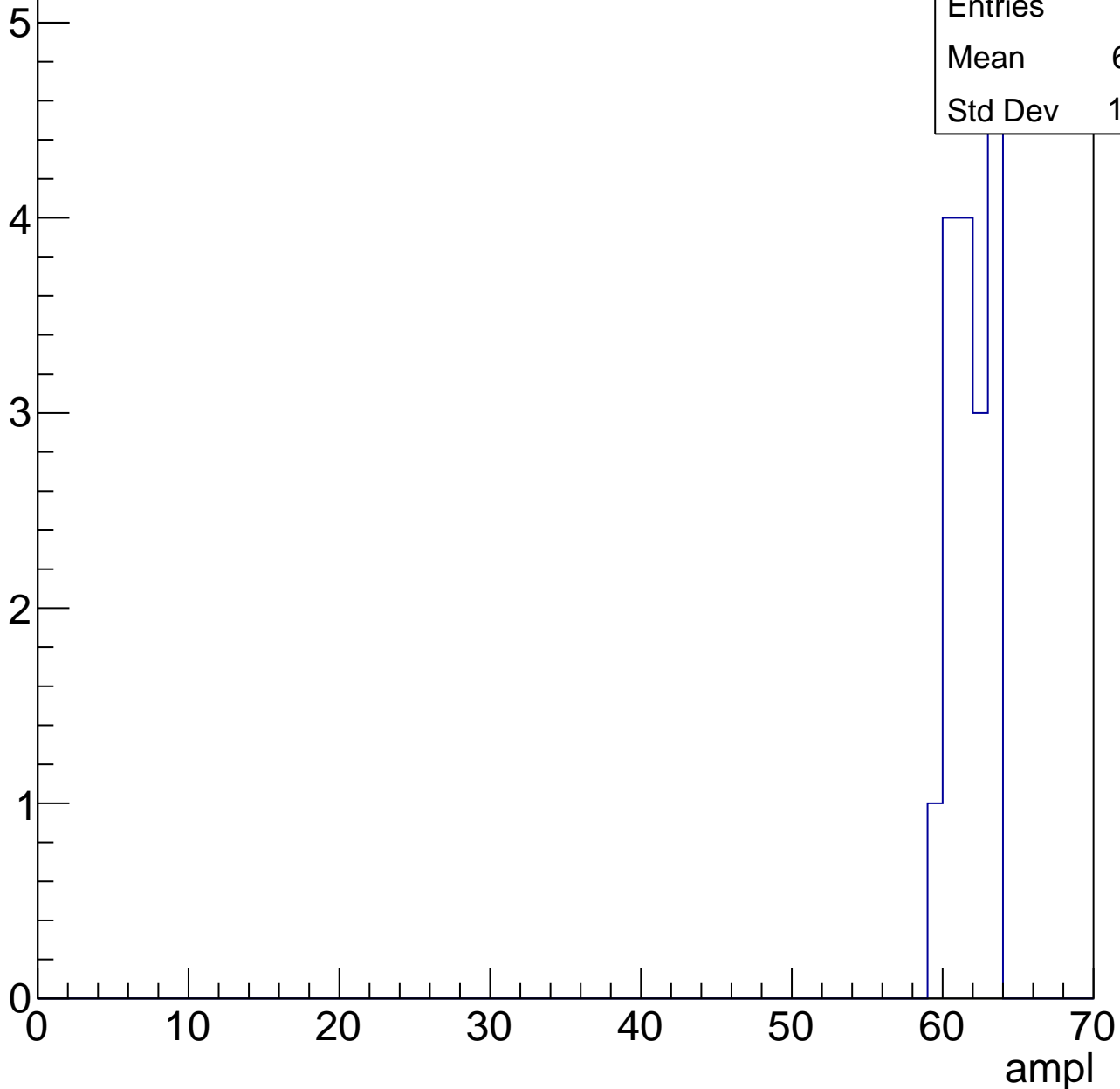


# B0L000S, U26-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61.41
Std Dev	1.286



# B0L000S, U26-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch30, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	30.68
Std Dev	3.443

**Gaus mean : 31.0974**

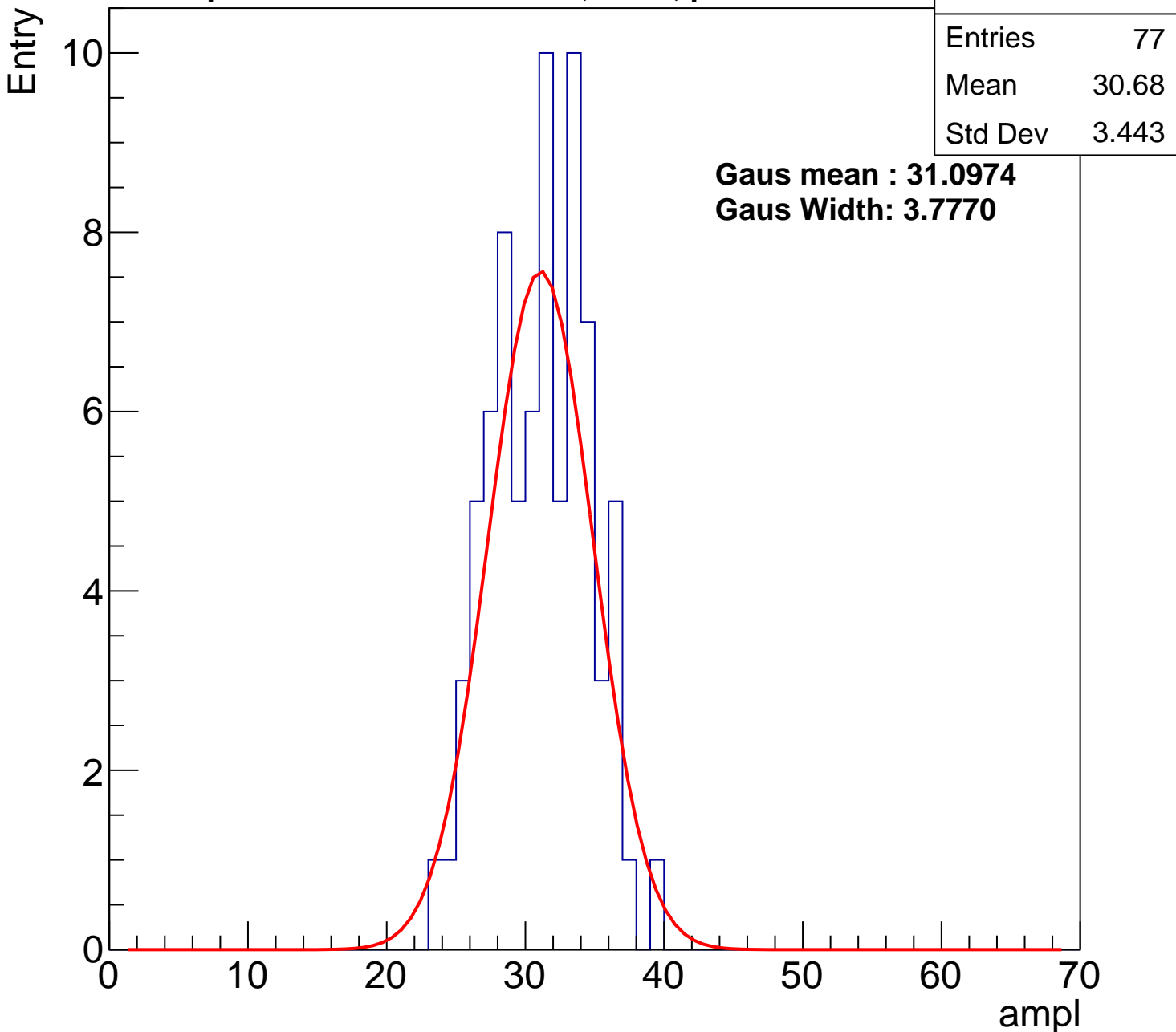
**Gaus Width: 3.7770**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch30, adc1

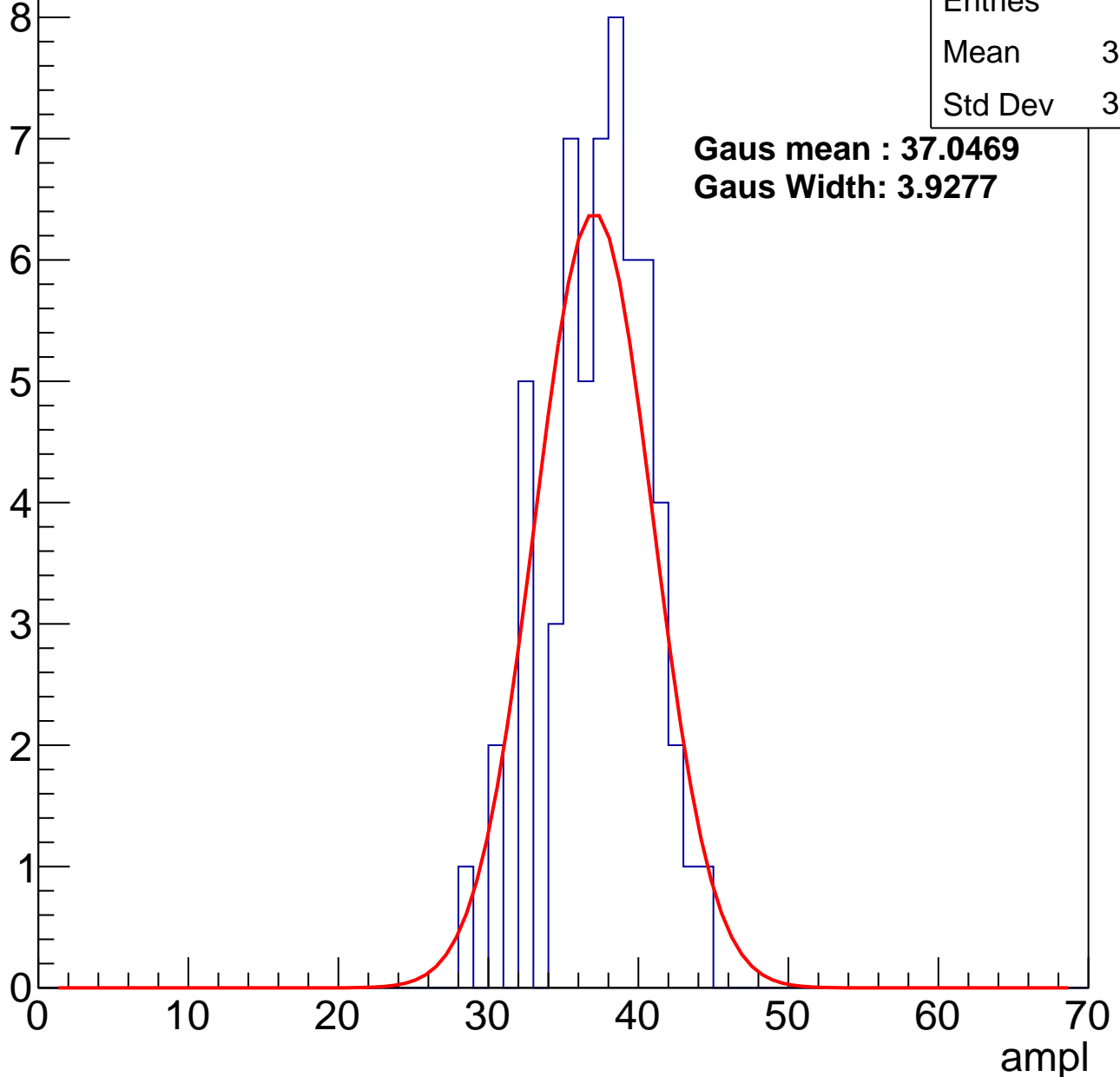
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	37.02
Std Dev	3.345

**Gaus mean : 37.0469**

**Gaus Width: 3.9277**



# B0L000S, U26-ch30, adc2

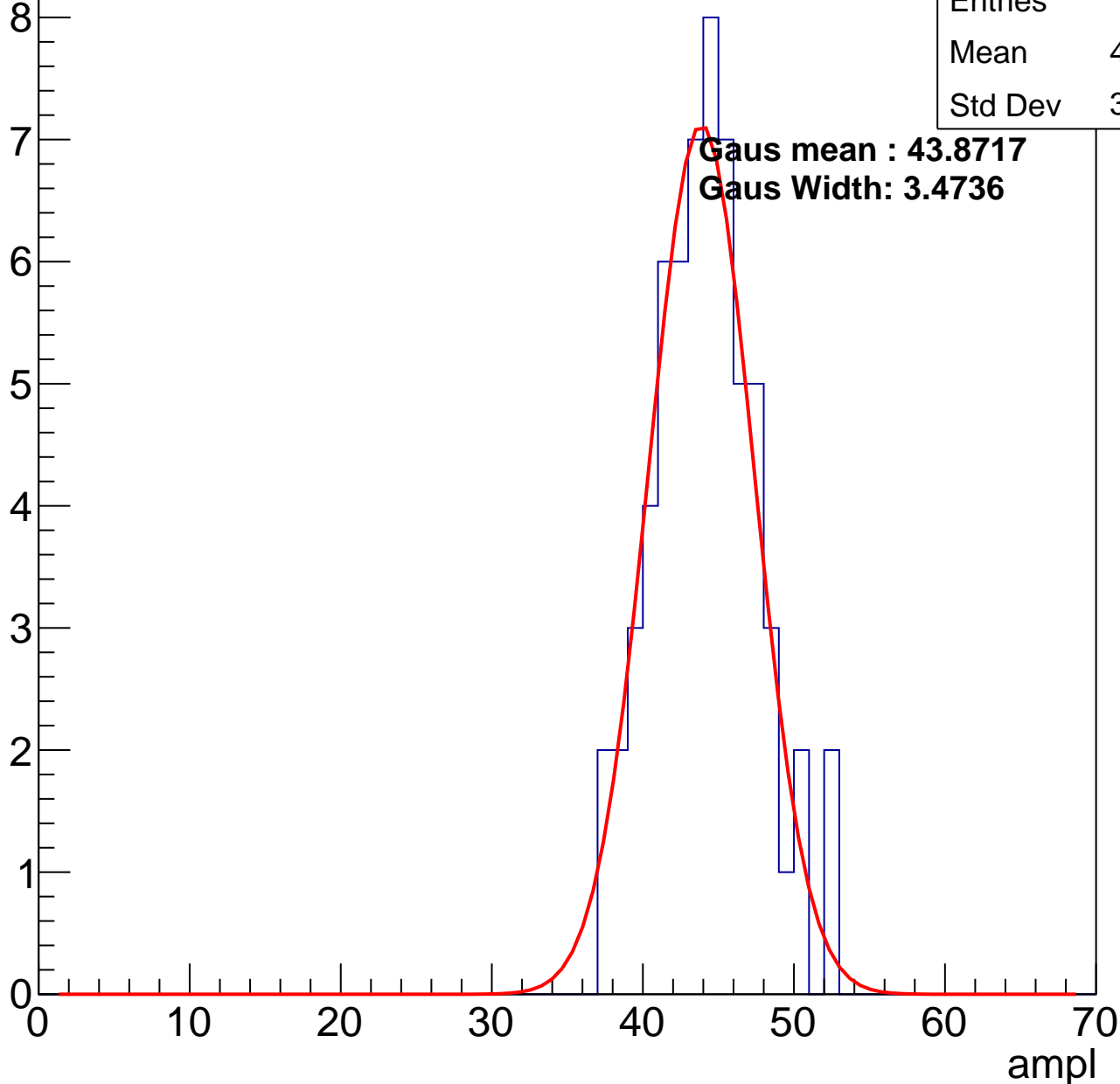
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	43.73
Std Dev	3.414

**Gaus mean : 43.8717**

**Gaus Width: 3.4736**

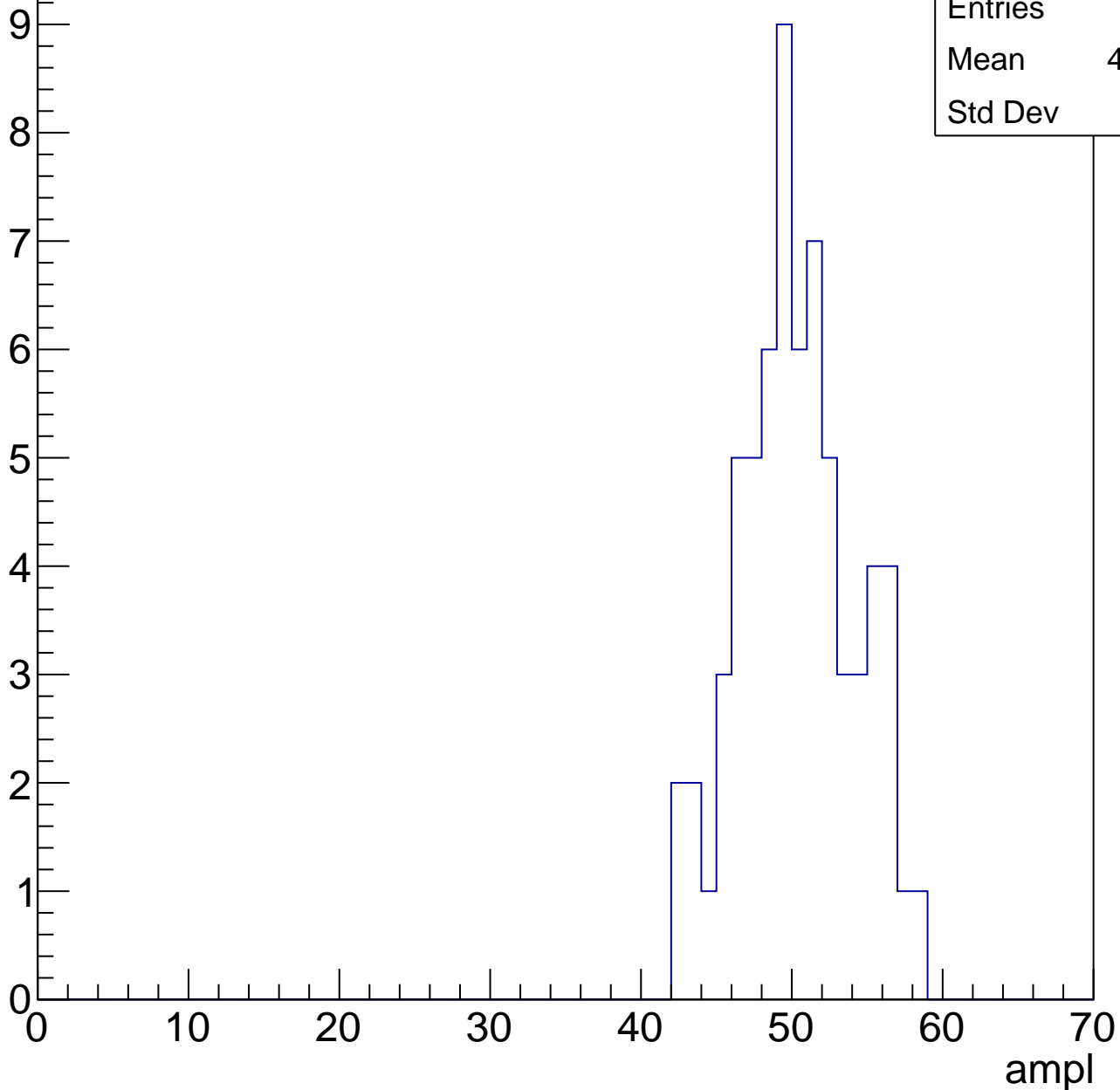


# B0L000S, U26-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.85
Std Dev	3.77

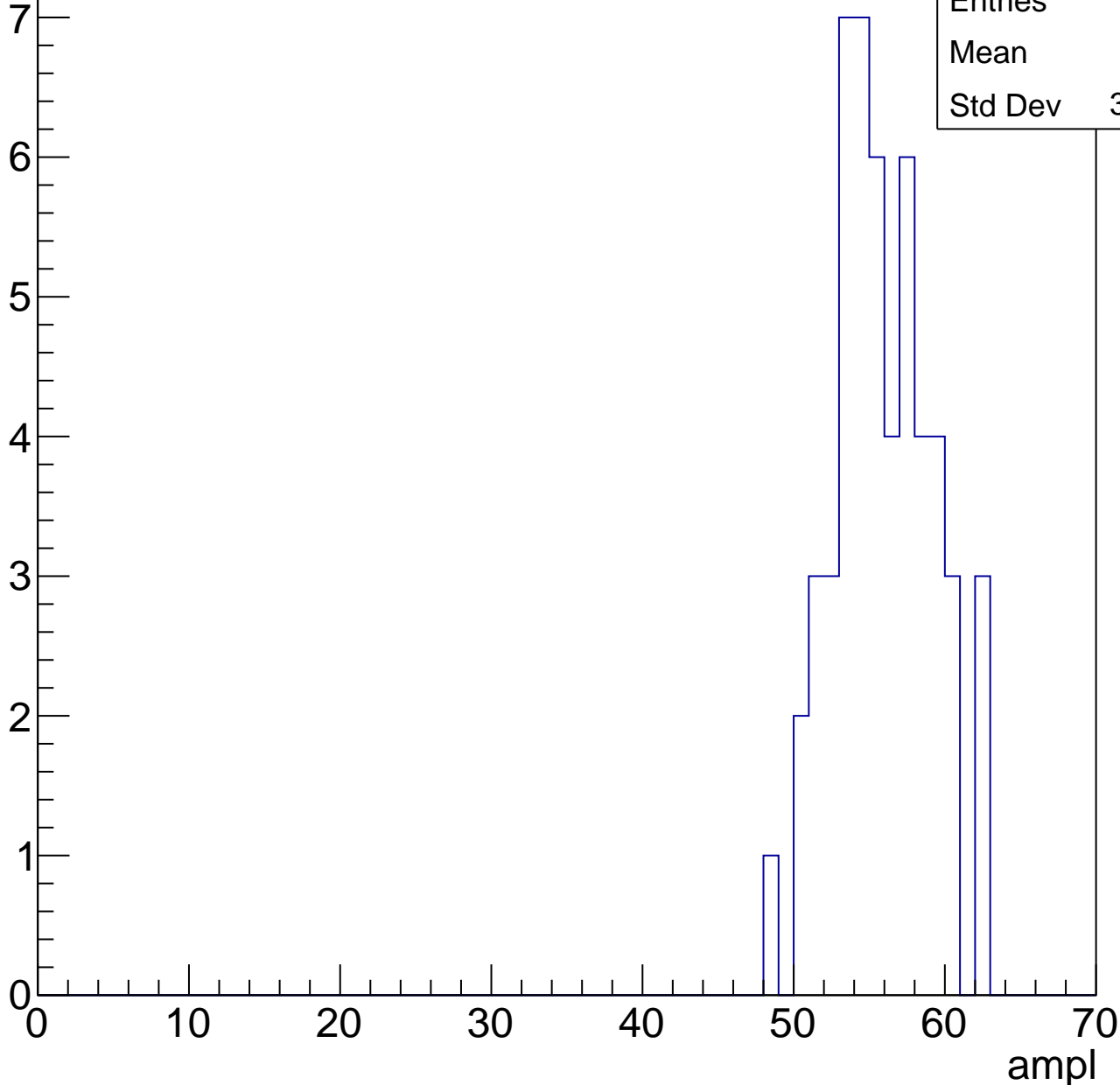


# B0L000S, U26-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	55.4
Std Dev	3.223

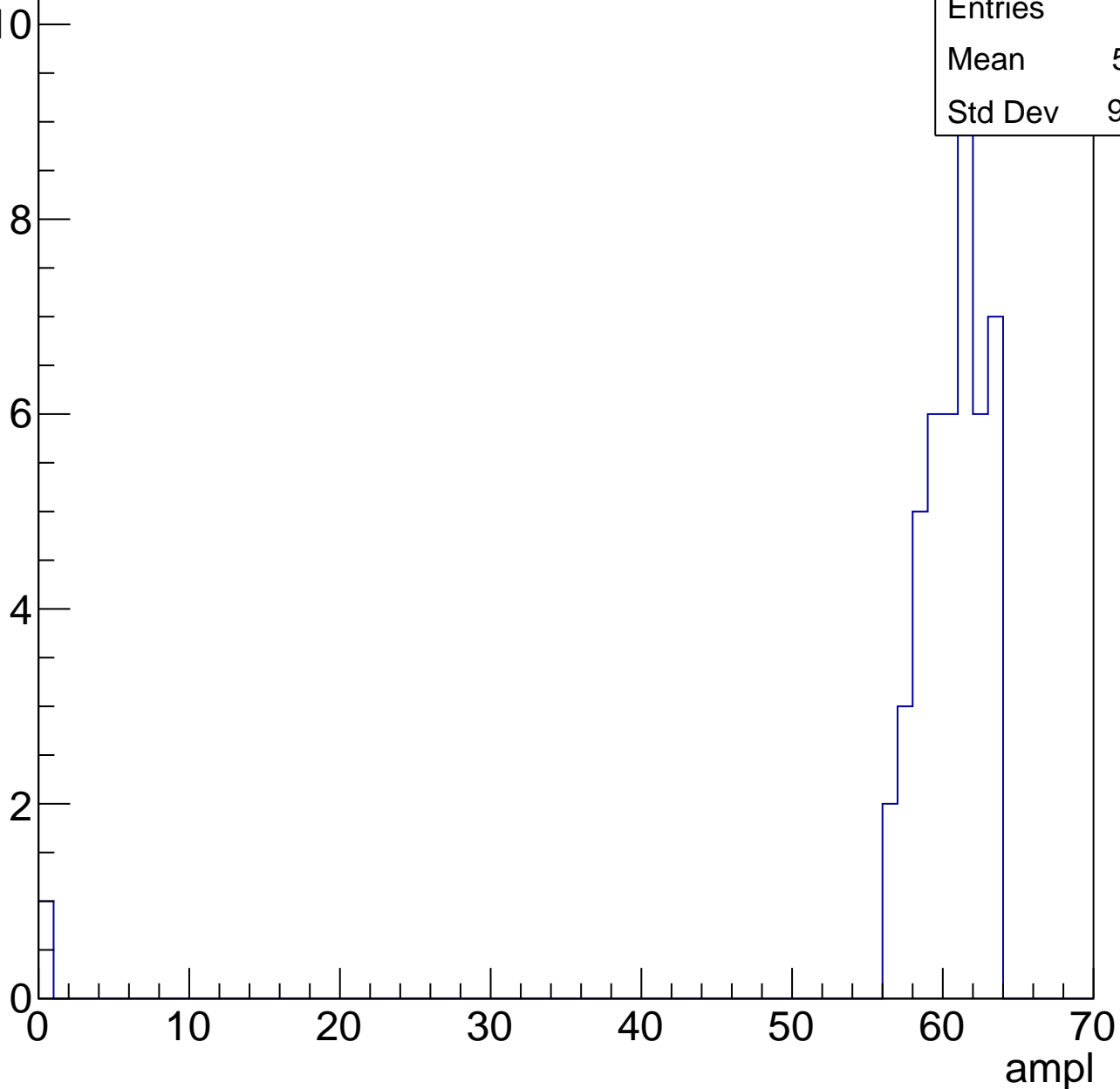


# B0L000S, U26-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.91
Std Dev	9.002



# B0L000S, U26-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



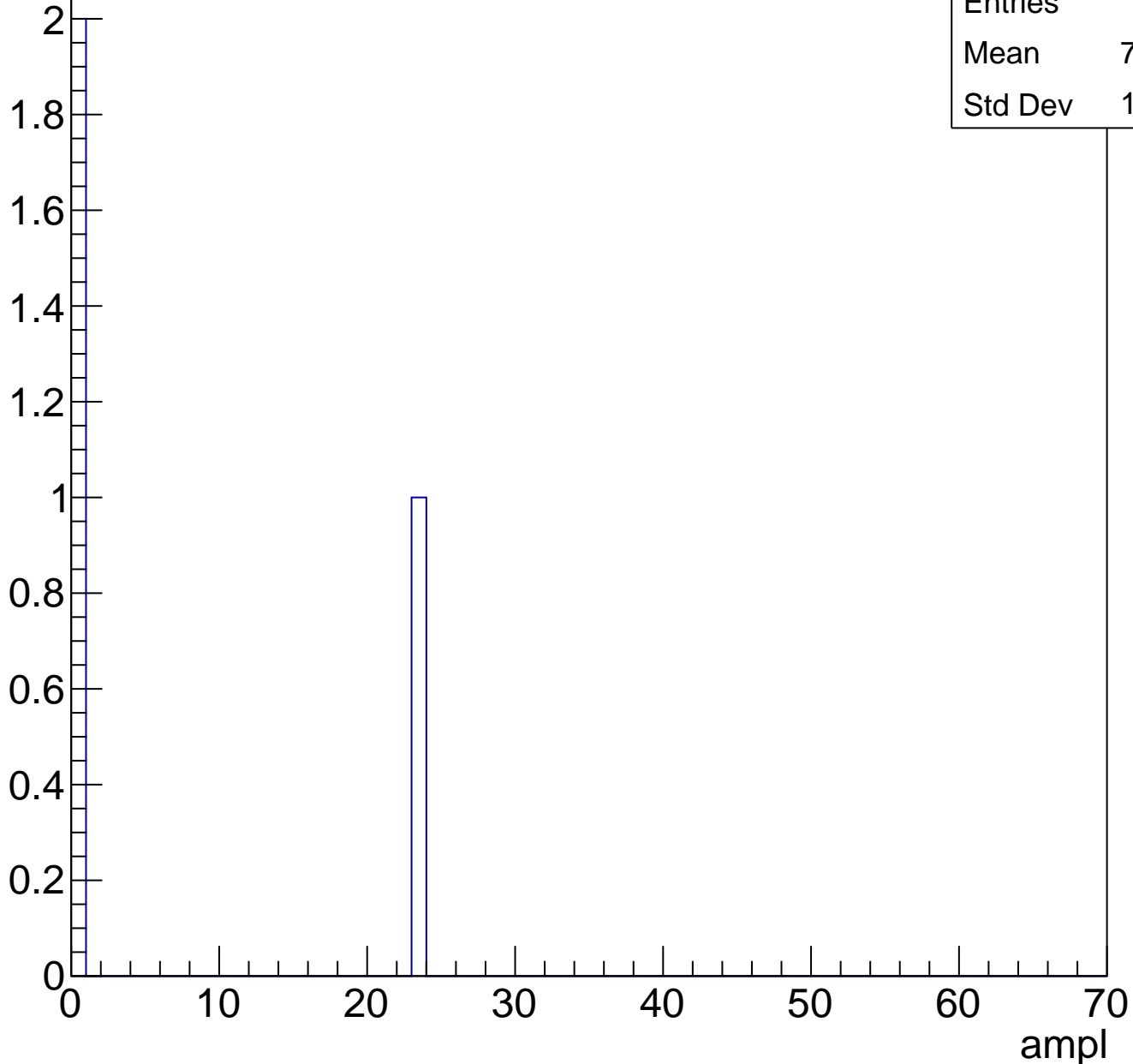
Entries	5
Mean	62.2
Std Dev	1.166



# B0L000S, U26-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U26-ch31, adc0

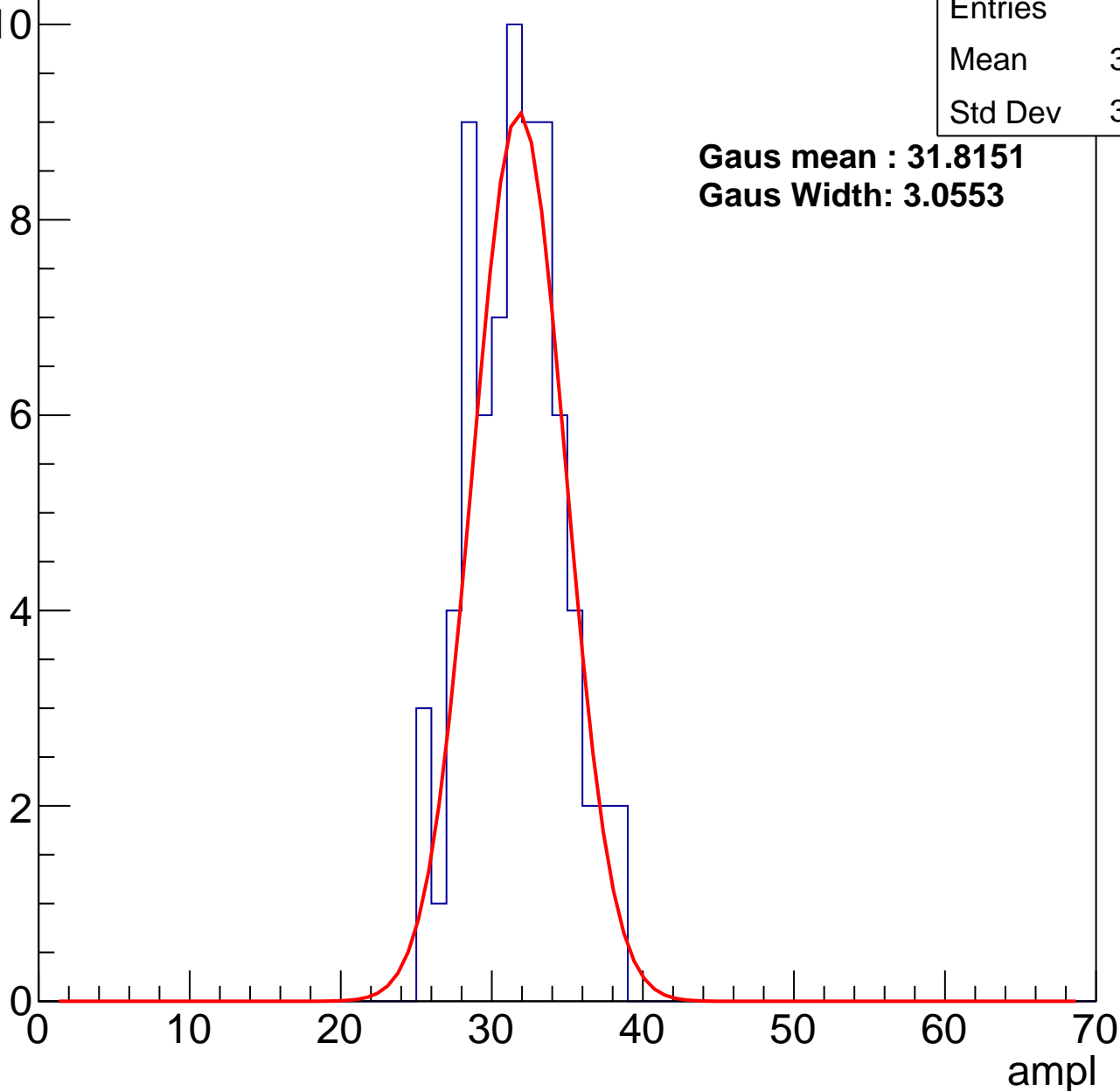
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	31.16
Std Dev	3.054

**Gaus mean : 31.8151**

**Gaus Width: 3.0553**



# B0L000S, U26-ch31, adc1

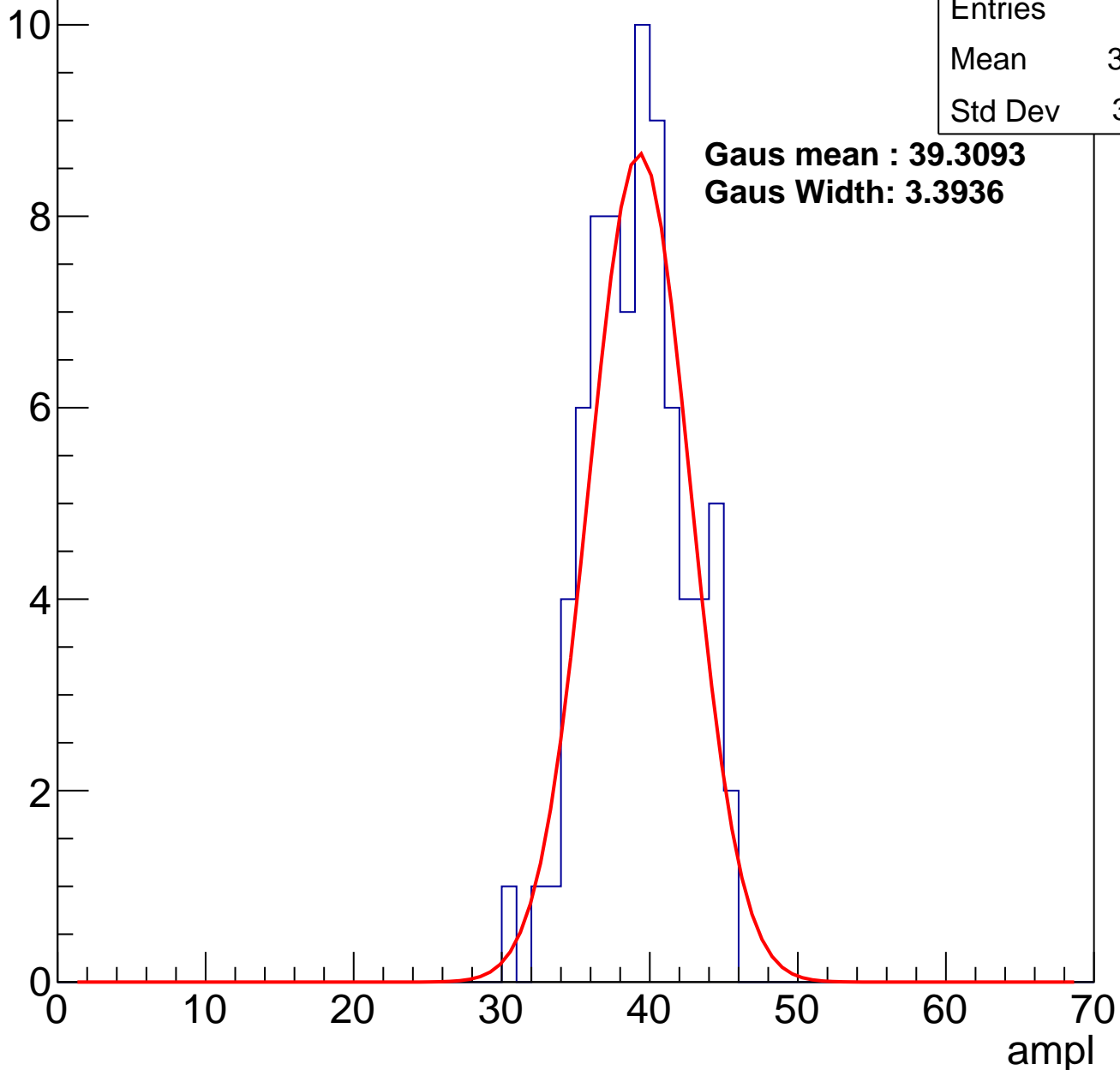
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	38.64
Std Dev	3.231

**Gaus mean : 39.3093**

**Gaus Width: 3.3936**

Entry



# B0L000S, U26-ch31, adc2

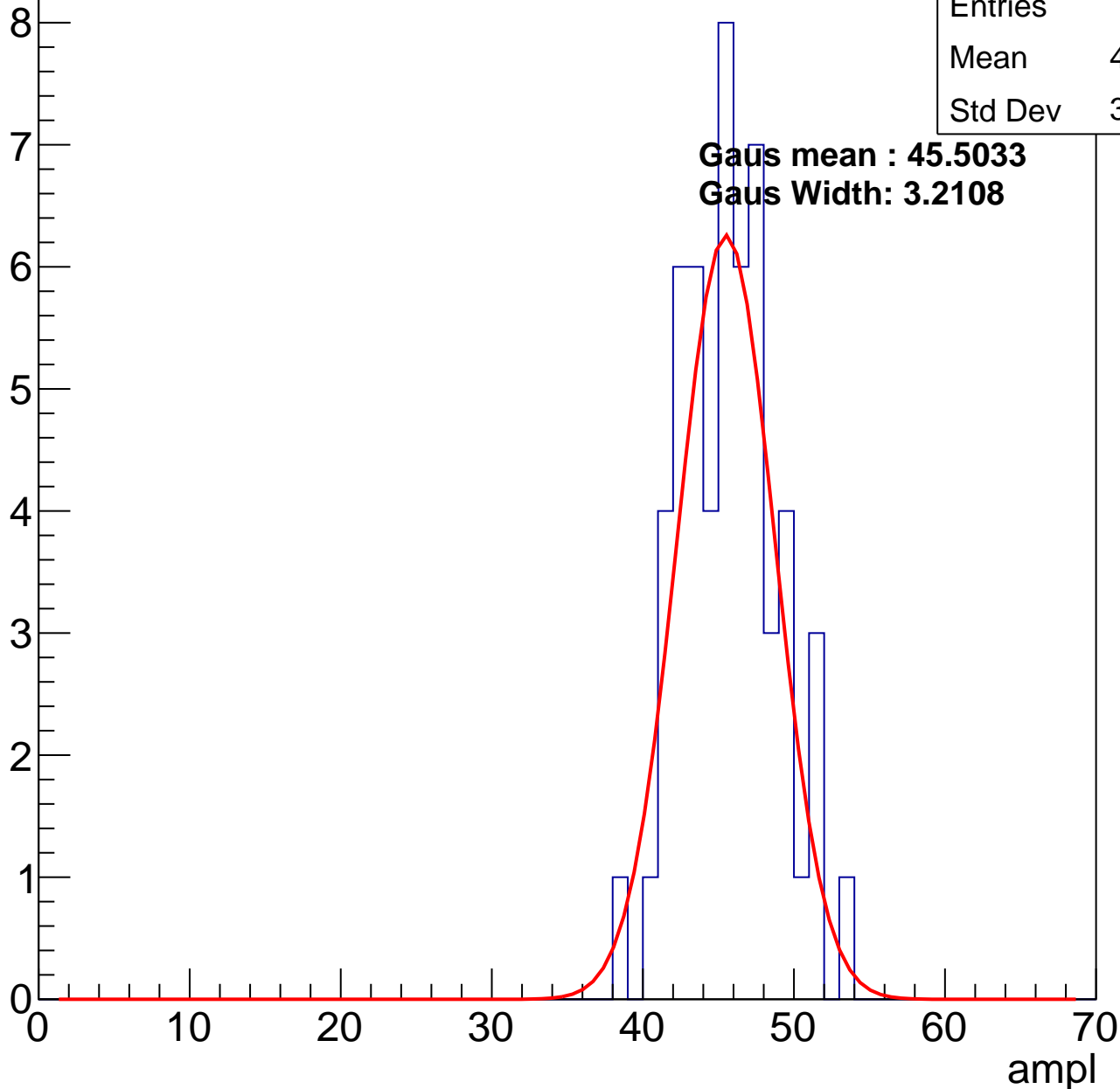
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	45.25
Std Dev	3.129

**Gaus mean : 45.5033**

**Gaus Width: 3.2108**

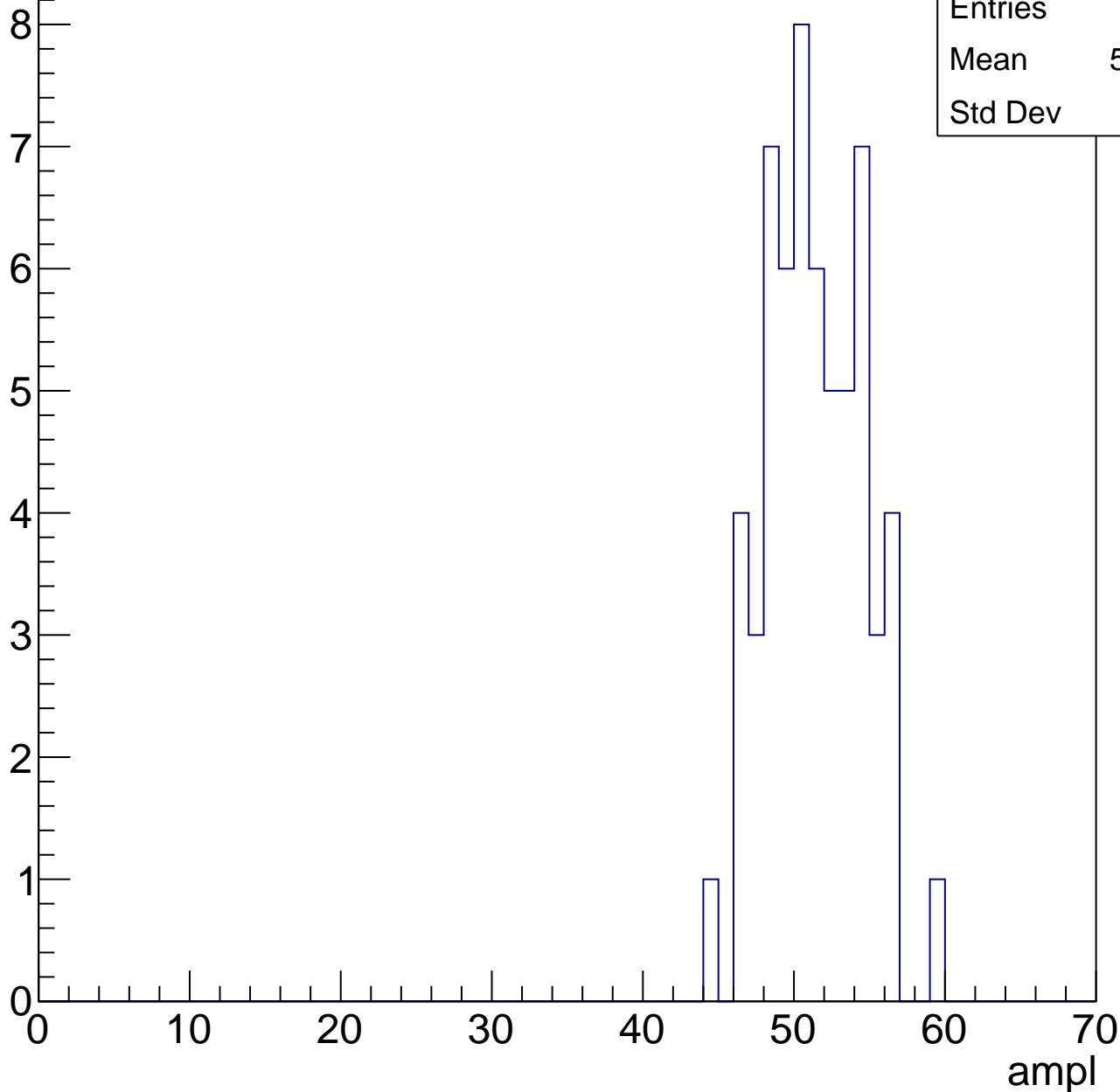


# B0L000S, U26-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.93
Std Dev	3.14

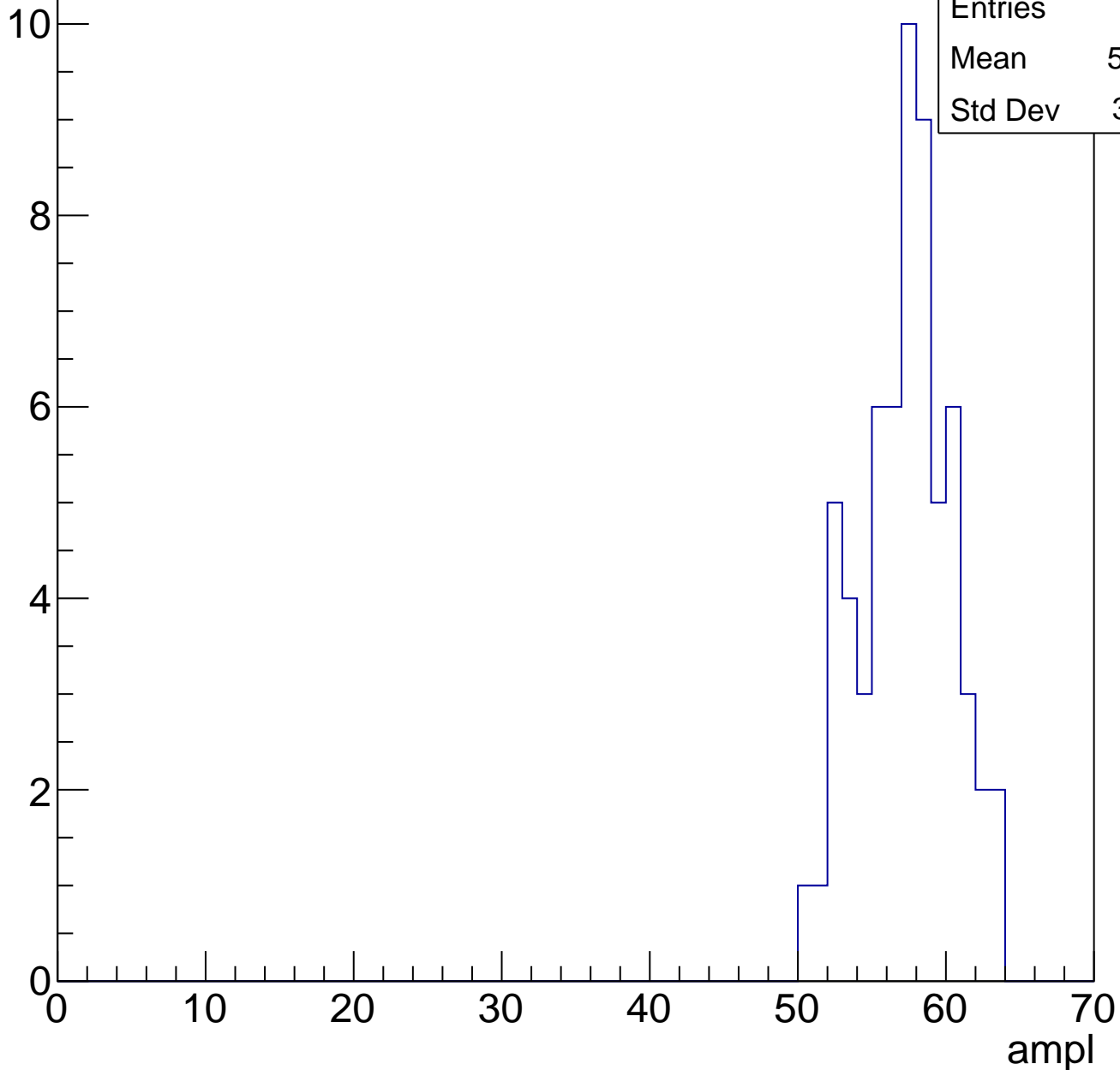


# B0L000S, U26-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	56.84
Std Dev	3.041

Entry

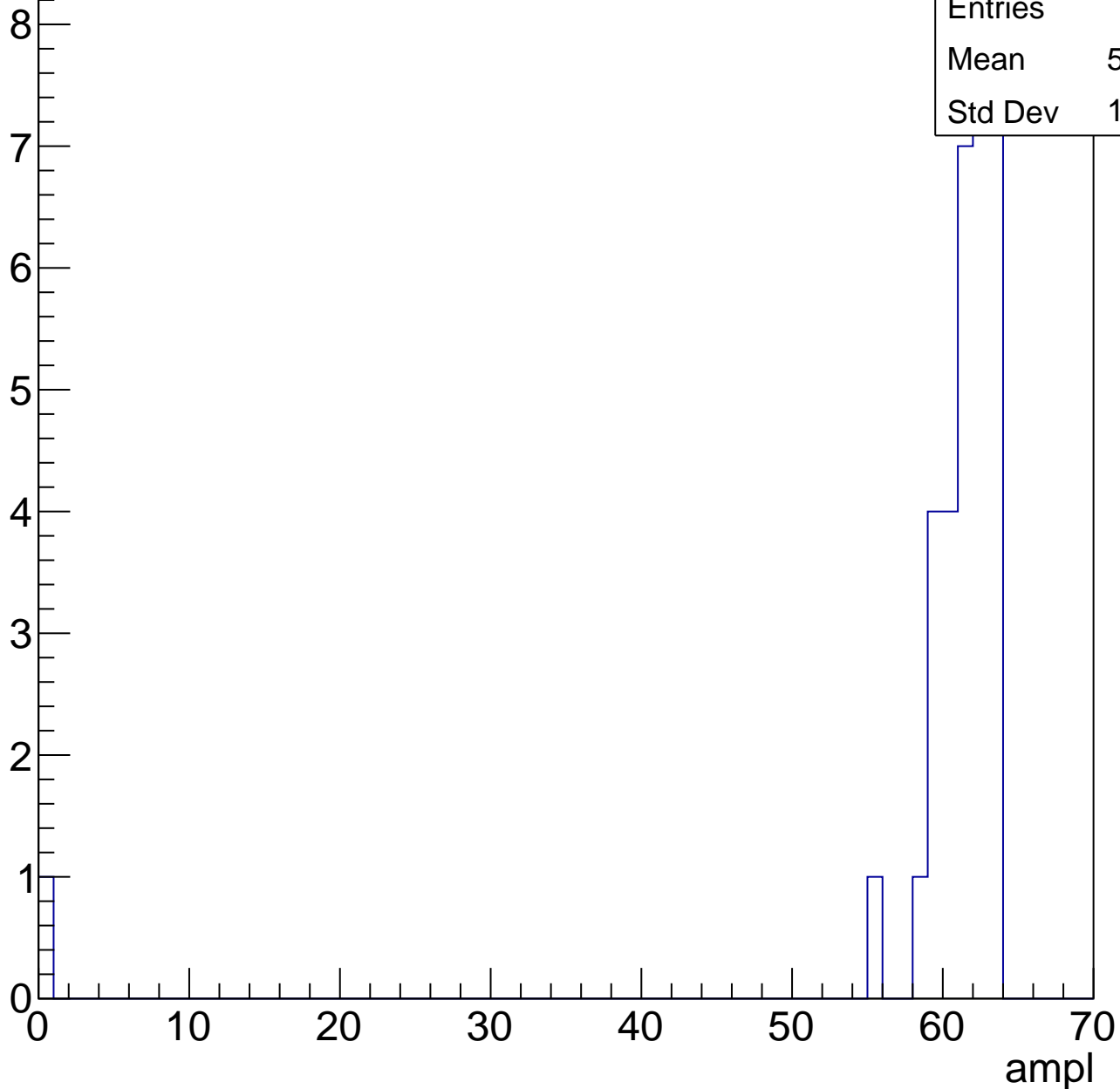


# B0L000S, U26-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	59.29
Std Dev	10.47



# B0L000S, U26-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0



# B0L000S, U26-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl



# B0L000S, U26-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	30.09
Std Dev	4.889

**Gaus mean : 31.1263**

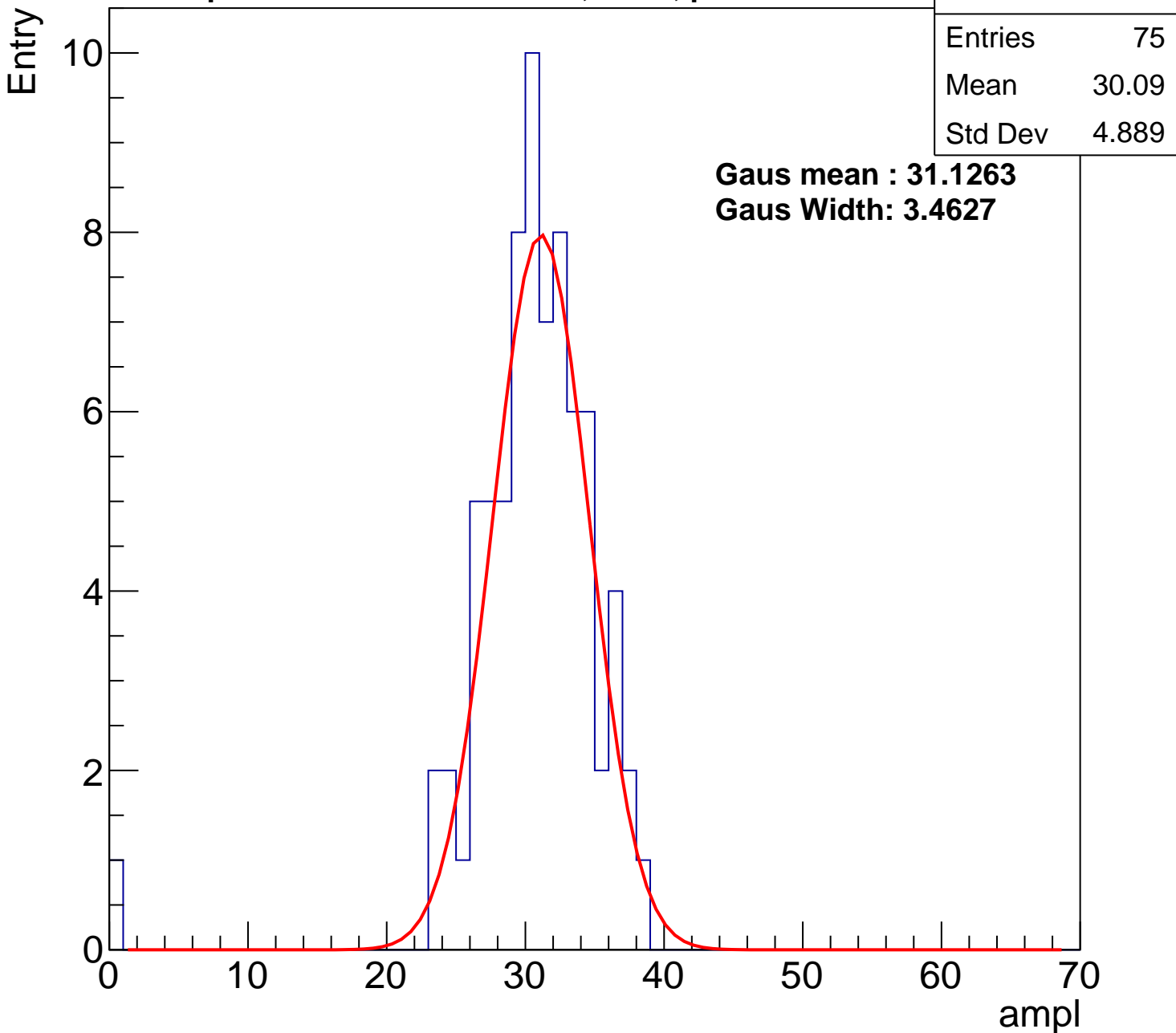
**Gaus Width: 3.4627**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



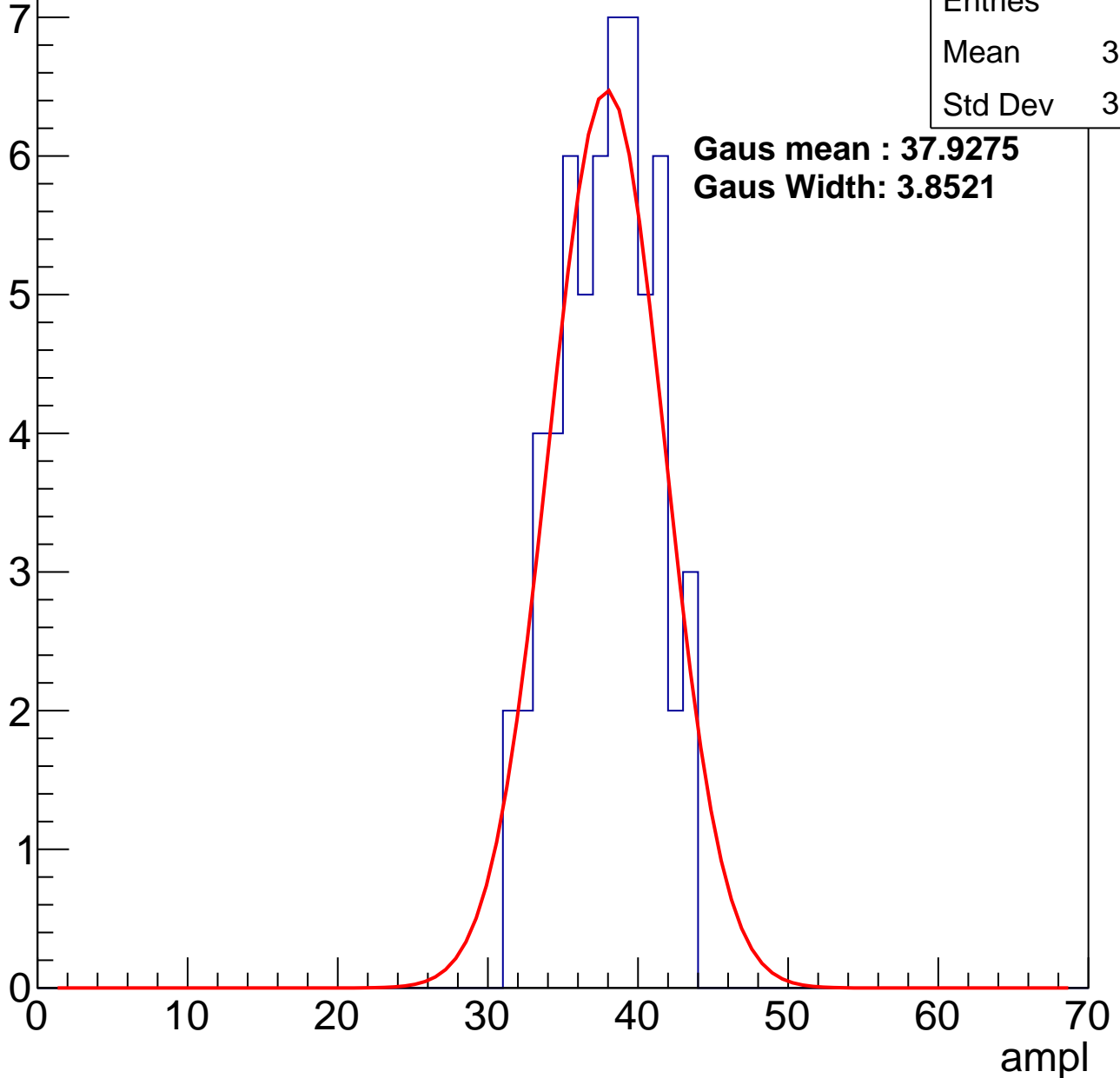
# B0L000S, U26-ch32, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.36
Std Dev	3.129

**Gaus mean : 37.9275**  
**Gaus Width: 3.8521**



# B0L000S, U26-ch32, adc2

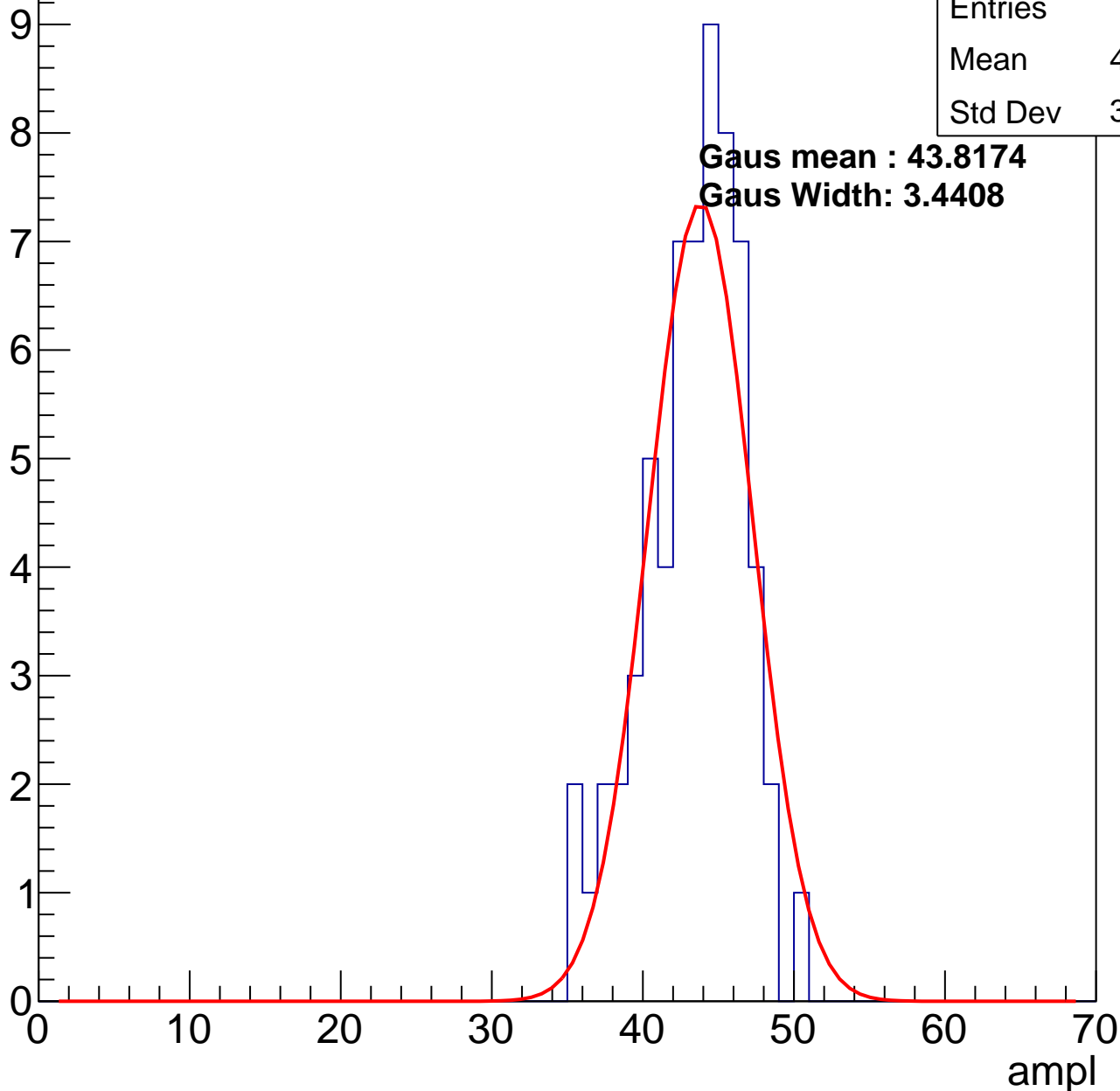
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	42.88
Std Dev	3.262

**Gaus mean : 43.8174**

**Gaus Width: 3.4408**

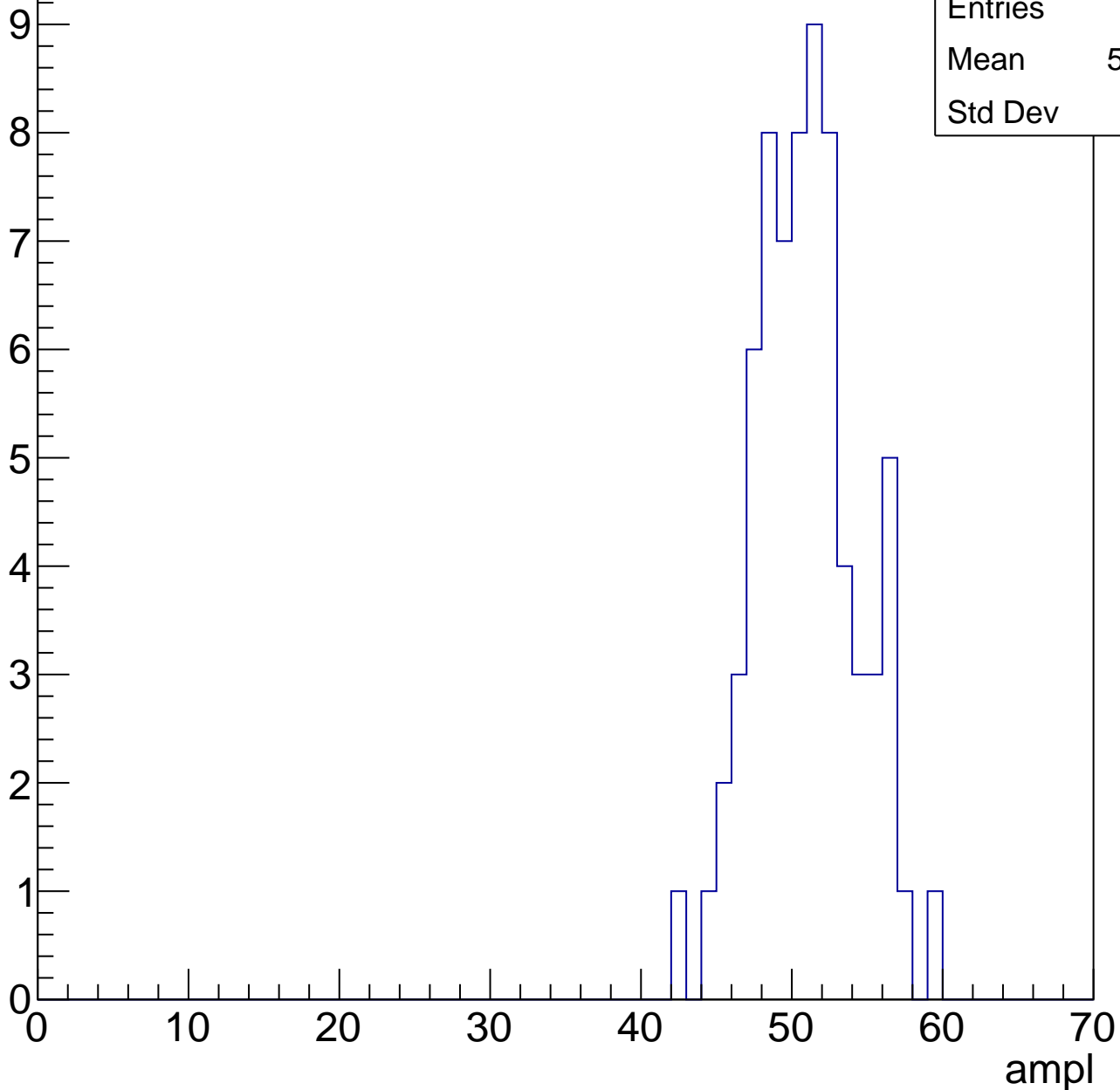


# B0L000S, U26-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

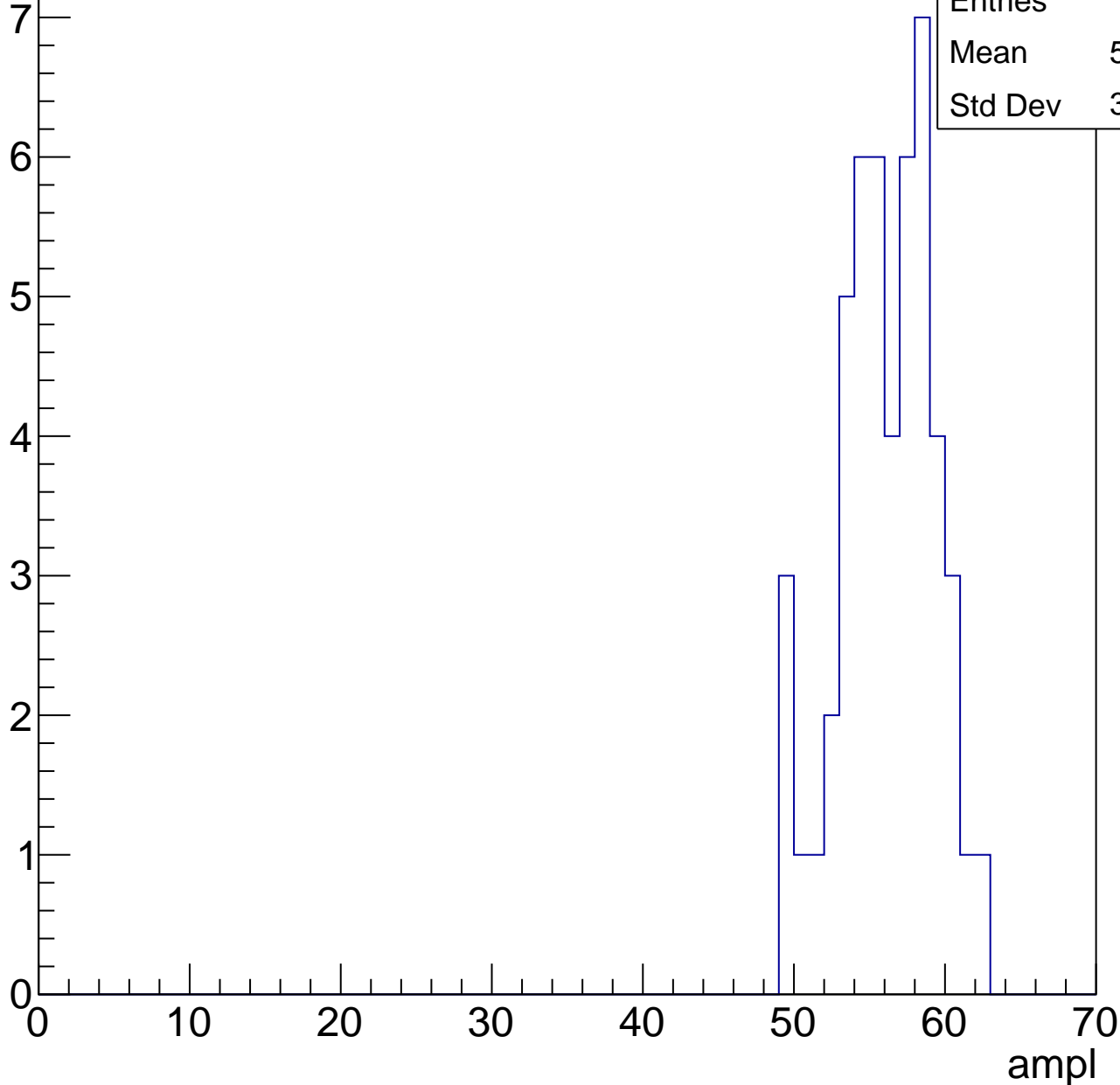
Entries	70
Mean	50.47
Std Dev	3.35



# B0L000S, U26-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

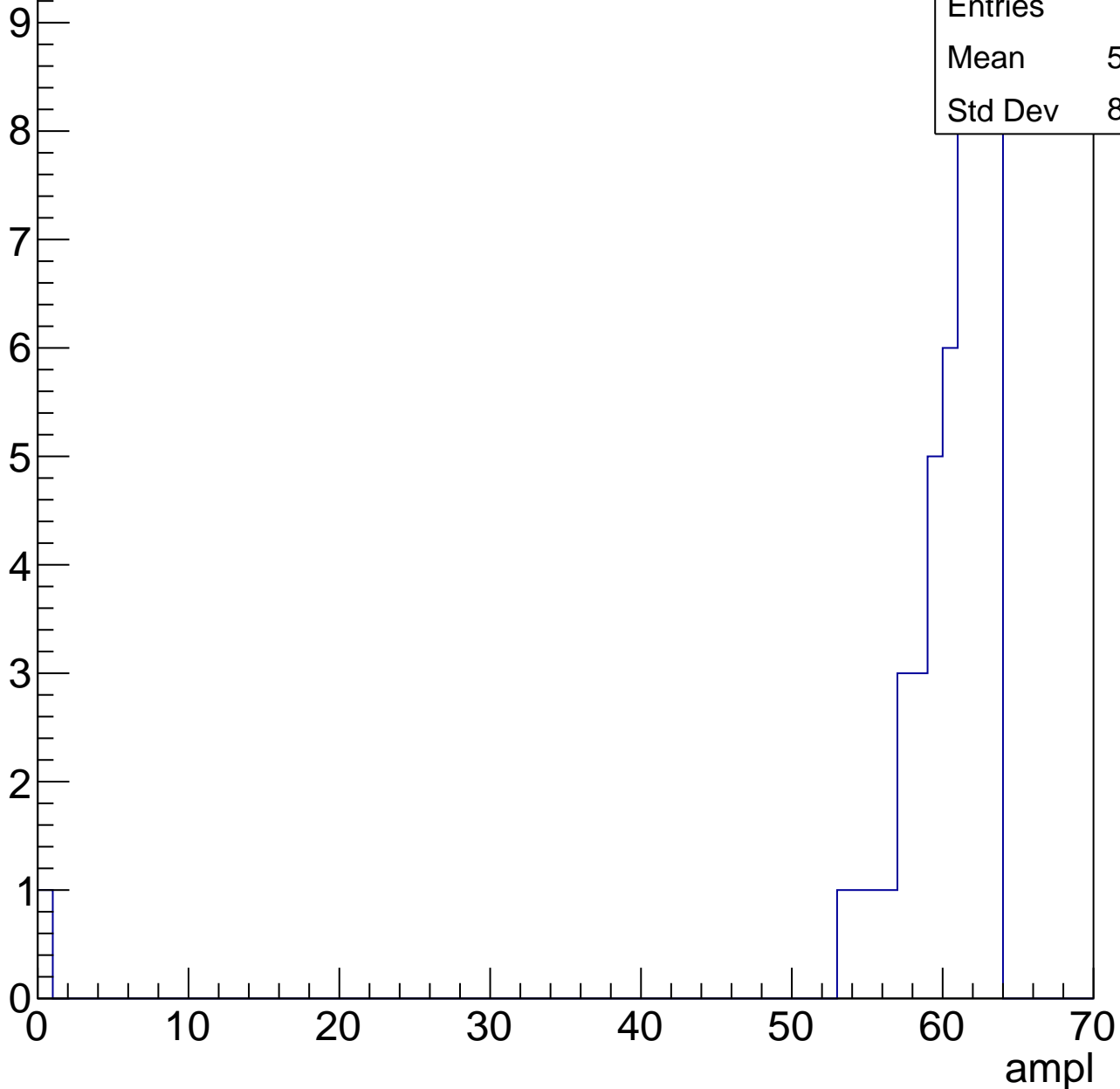


# B0L000S, U26-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

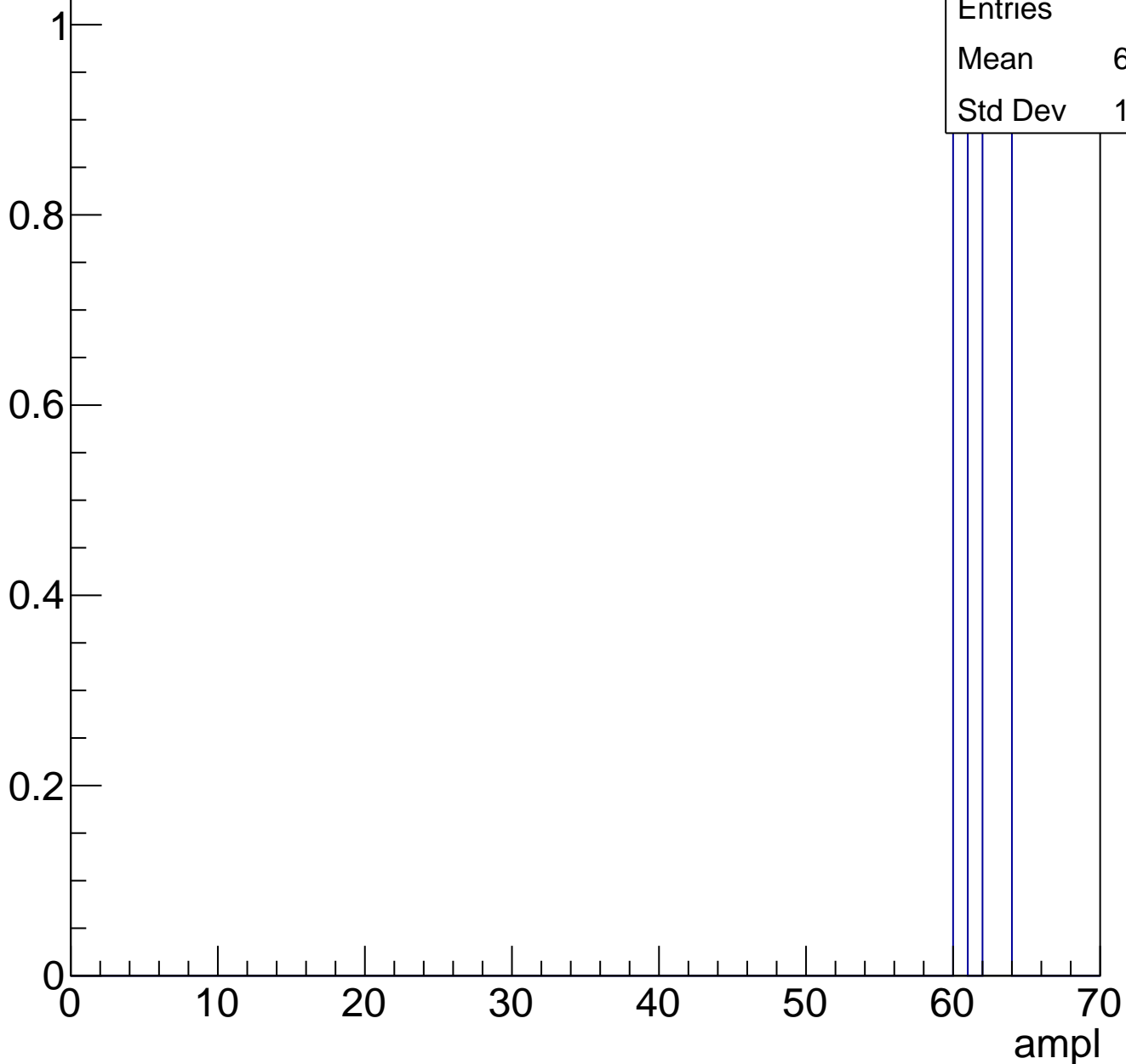
Entries	48
Mean	58.96
Std Dev	8.944



# B0L000S, U26-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U26-ch33, adc0

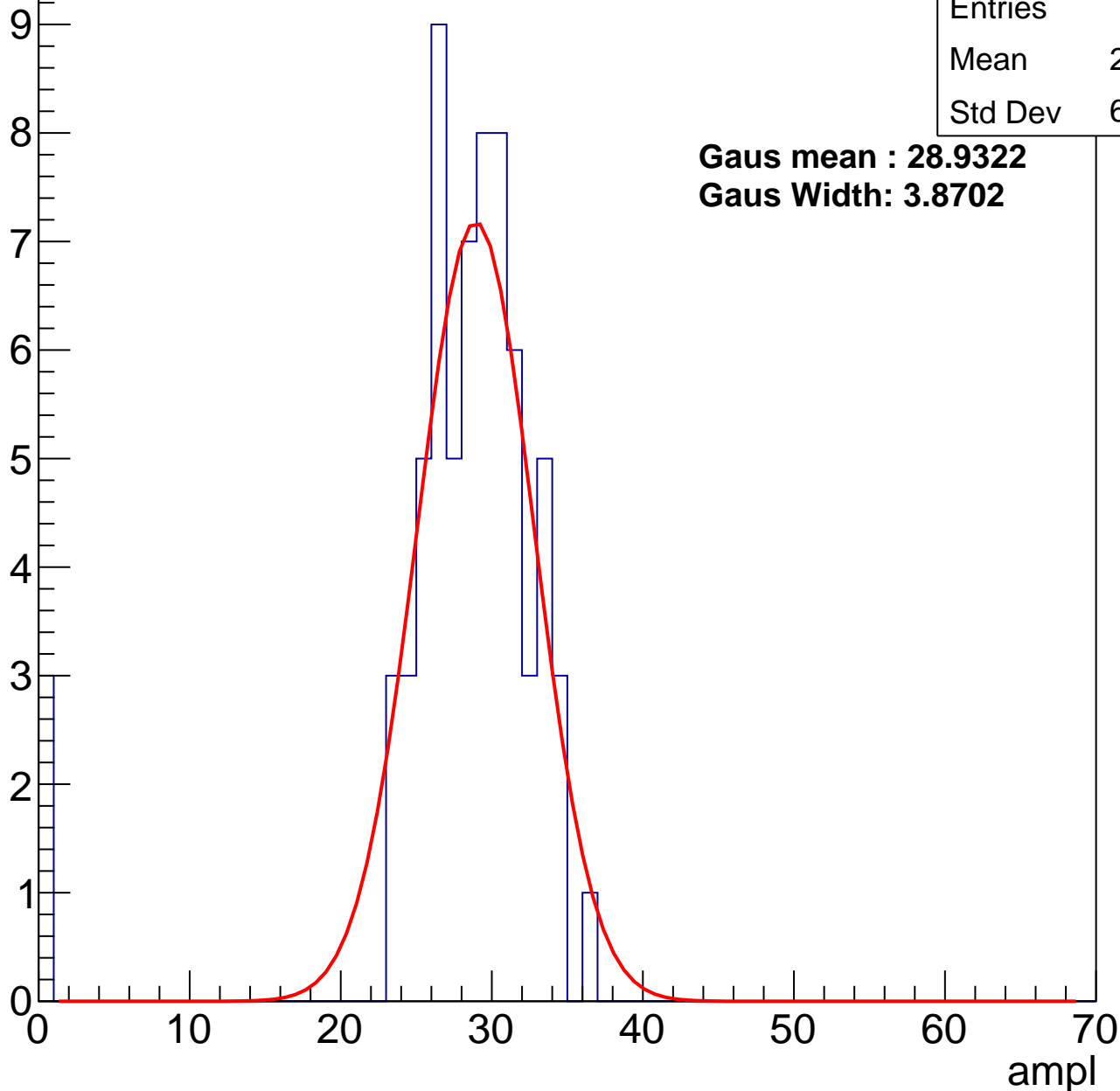
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	27.36
Std Dev	6.563

**Gaus mean : 28.9322**

**Gaus Width: 3.8702**



# B0L000S, U26-ch33, adc1

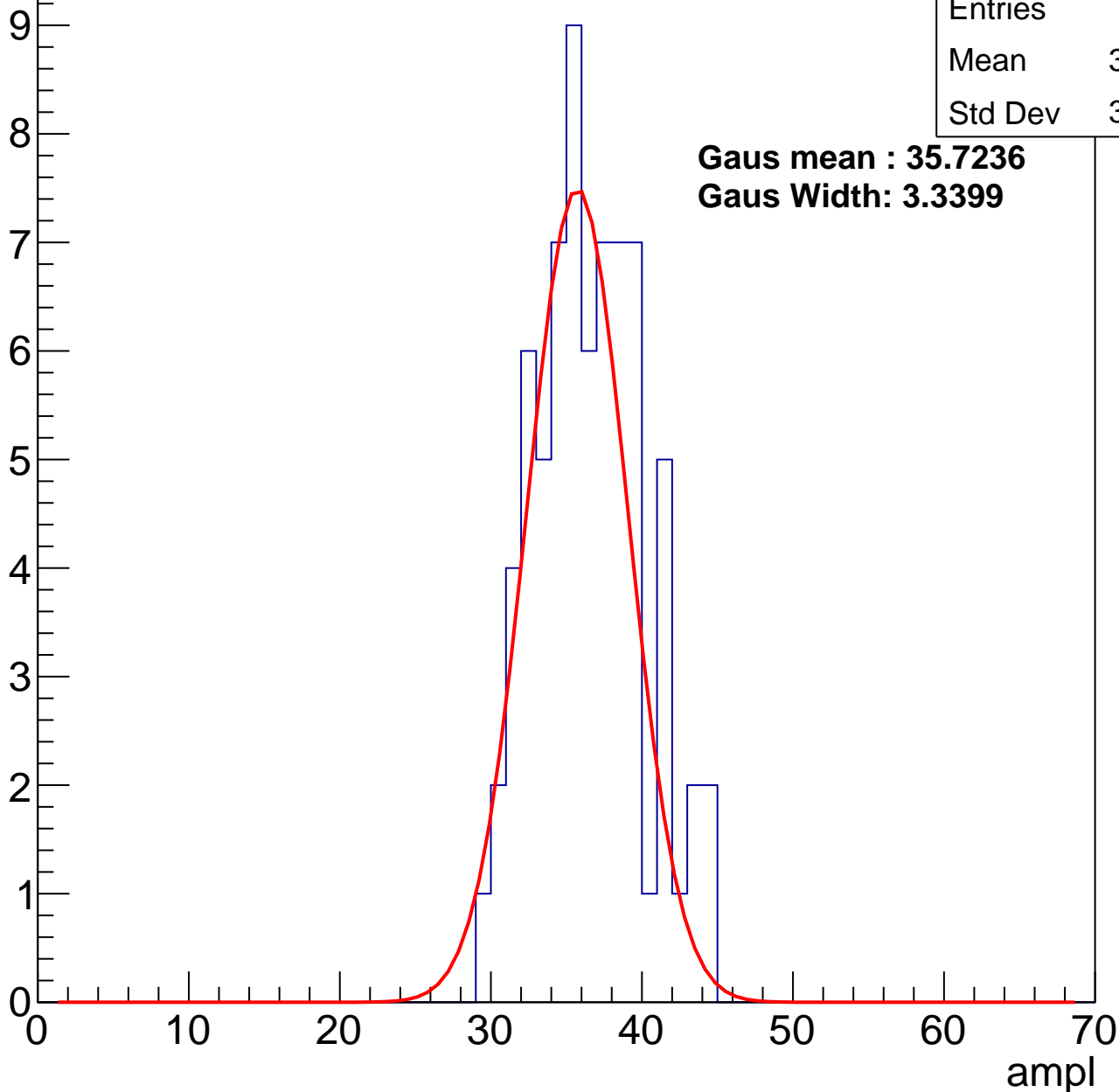
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.08
Std Dev	3.535

**Gaus mean : 35.7236**

**Gaus Width: 3.3399**



# B0L000S, U26-ch33, adc2

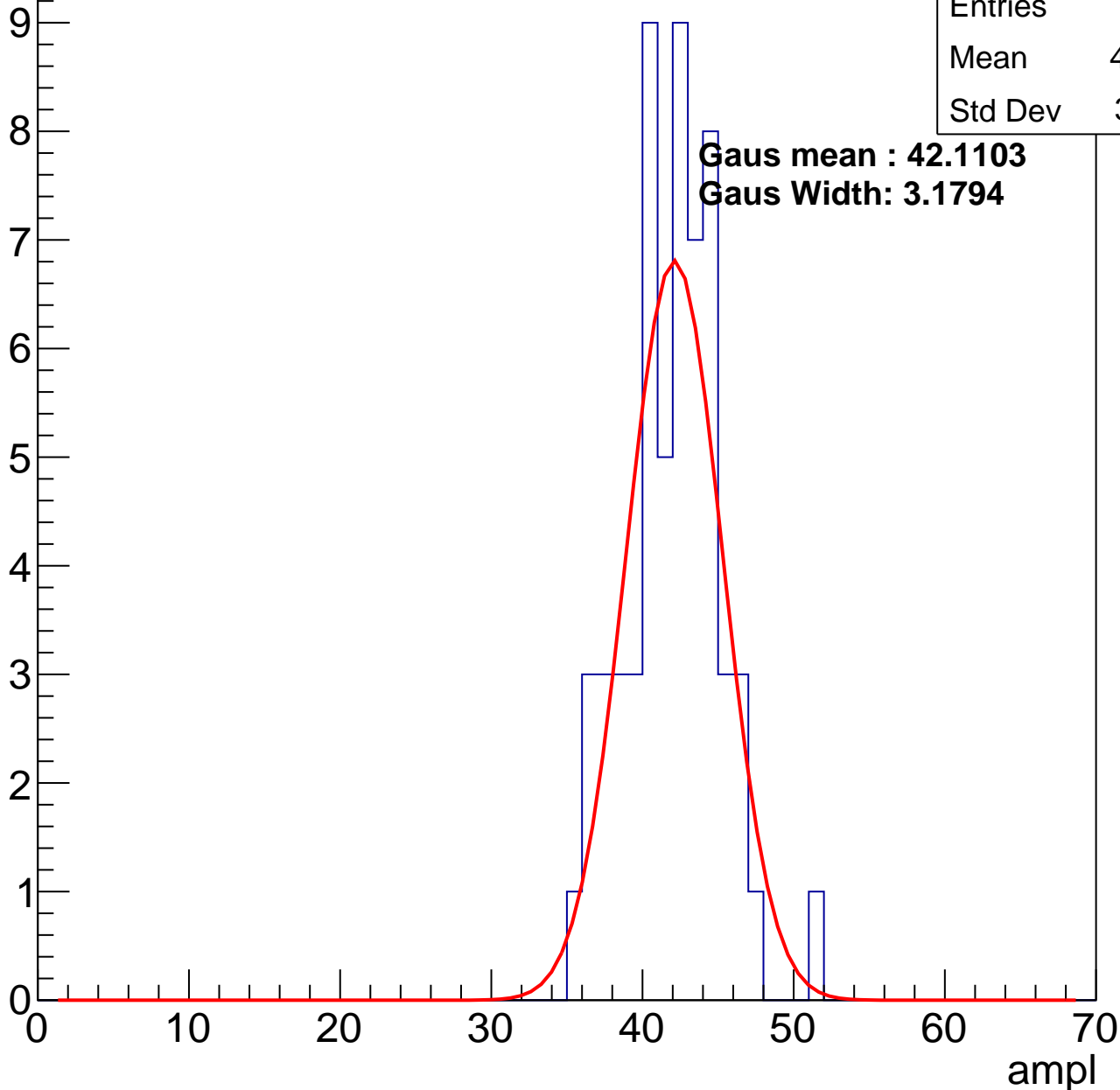
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	41.56
Std Dev	3.071

**Gaus mean : 42.1103**

**Gaus Width: 3.1794**

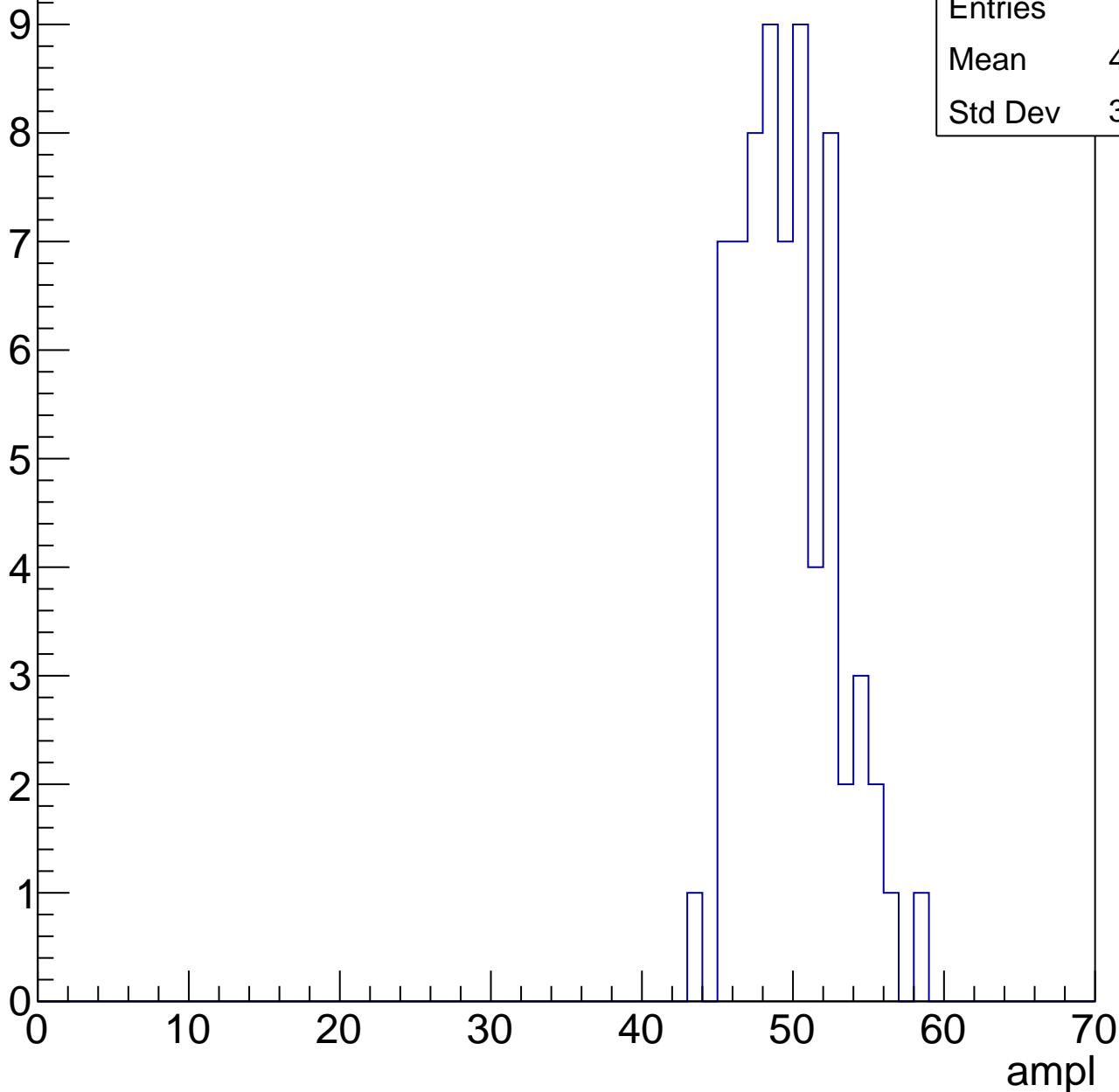


# B0L000S, U26-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.17
Std Dev	3.083



# B0L000S, U26-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	55.12
Std Dev	3.574

Entry

10

8

6

4

2

0

0

10

20

30

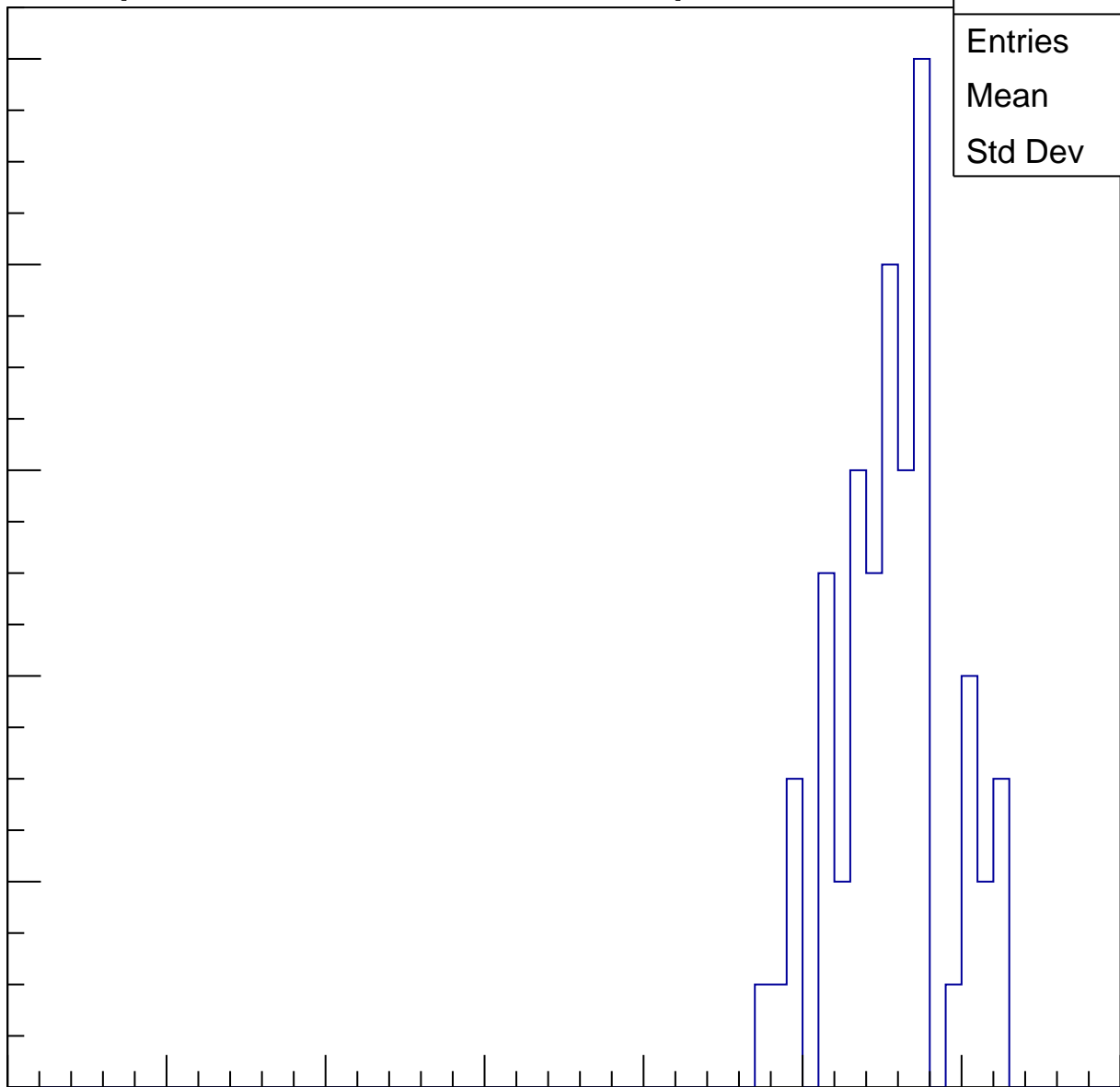
40

50

60

70

ampl

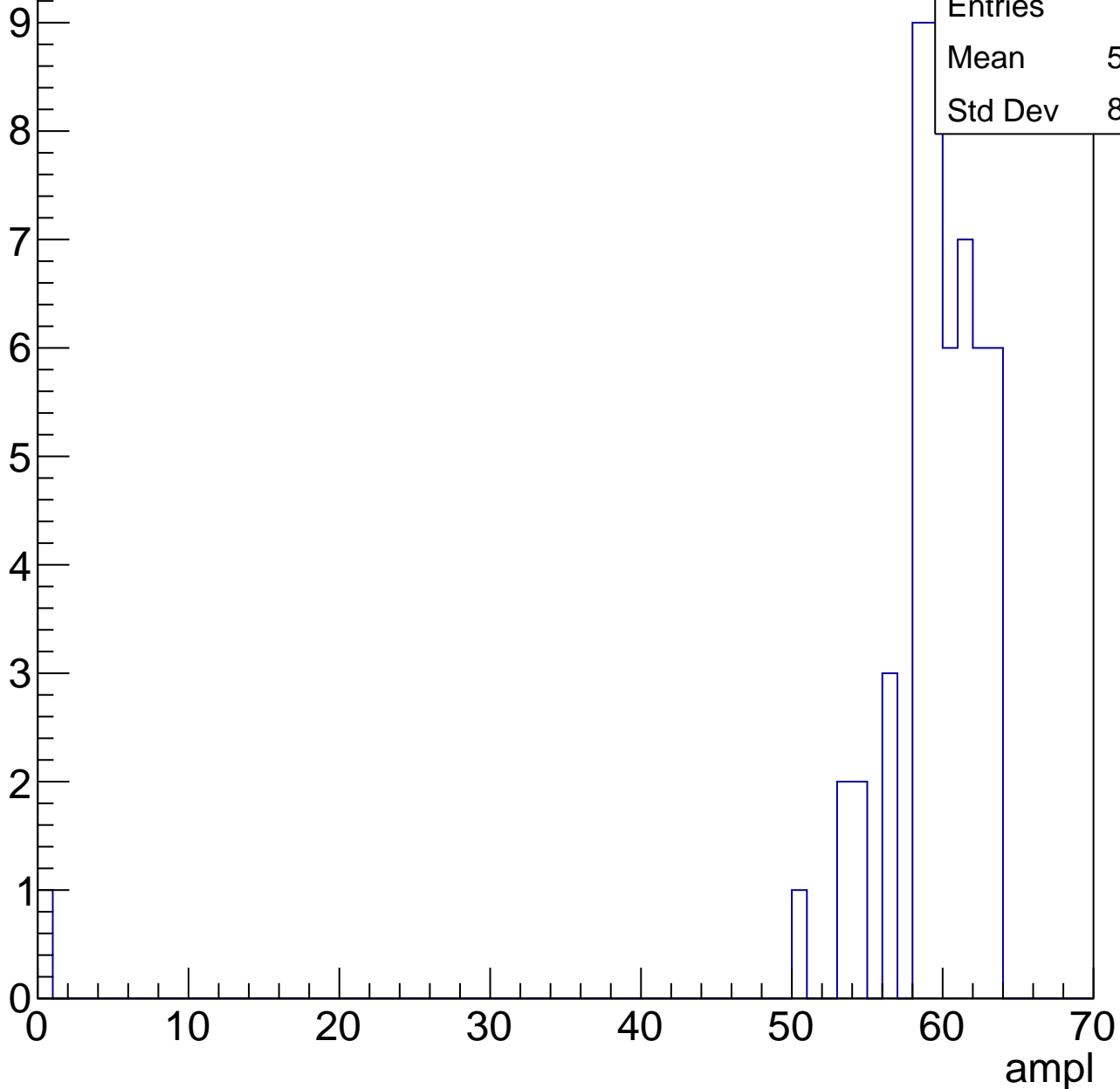


# B0L000S, U26-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.12
Std Dev	8.619



# B0L000S, U26-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch34, adc0

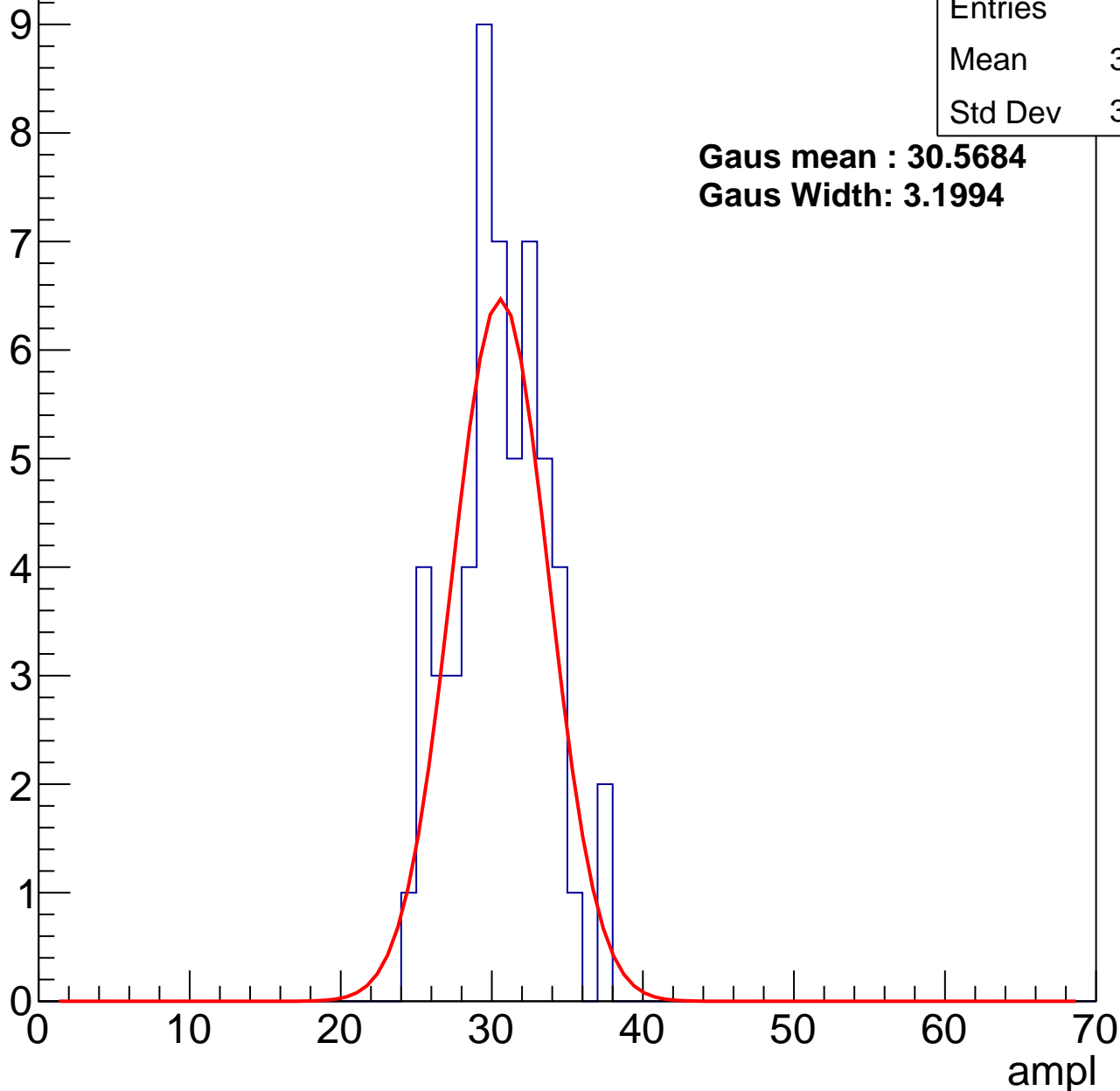
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	30.09
Std Dev	3.017

**Gaus mean : 30.5684**

**Gaus Width: 3.1994**



# B0L000S, U26-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	35.8
Std Dev	3.866

**Gaus mean : 36.1690**

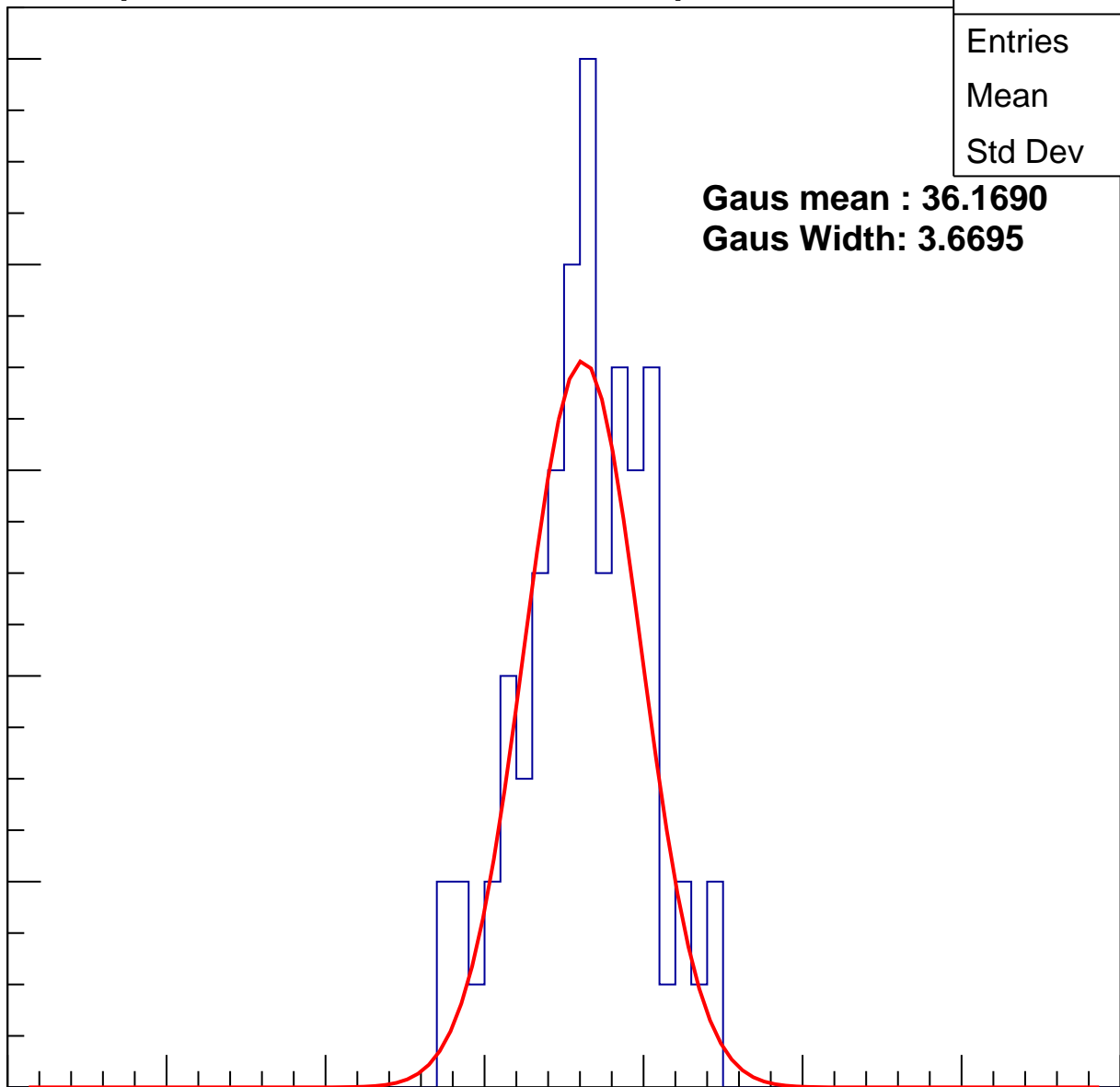
**Gaus Width: 3.6695**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch34, adc2

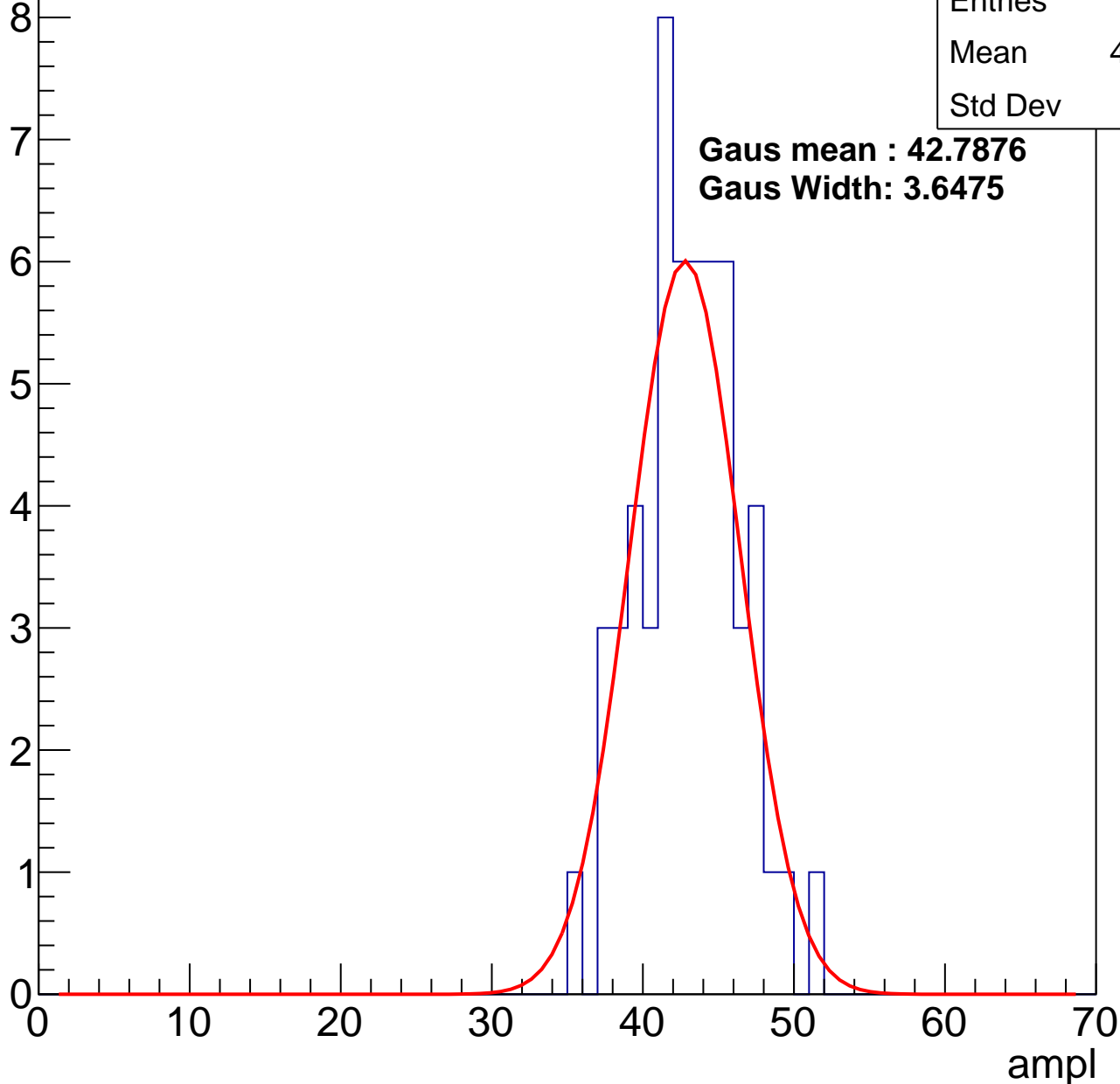
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.54
Std Dev	3.3

**Gaus mean : 42.7876**

**Gaus Width: 3.6475**

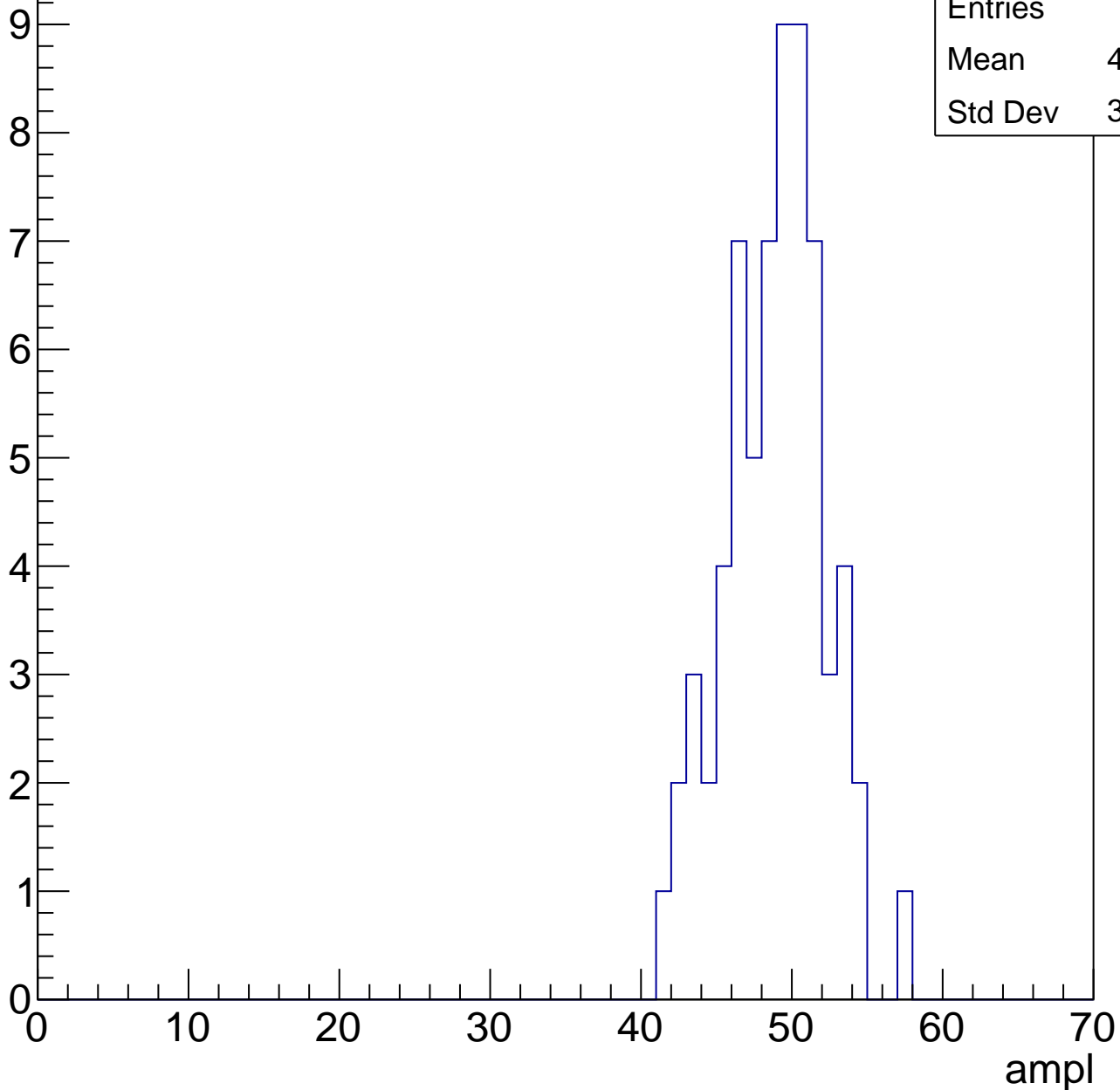


# B0L000S, U26-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	48.42
Std Dev	3.243

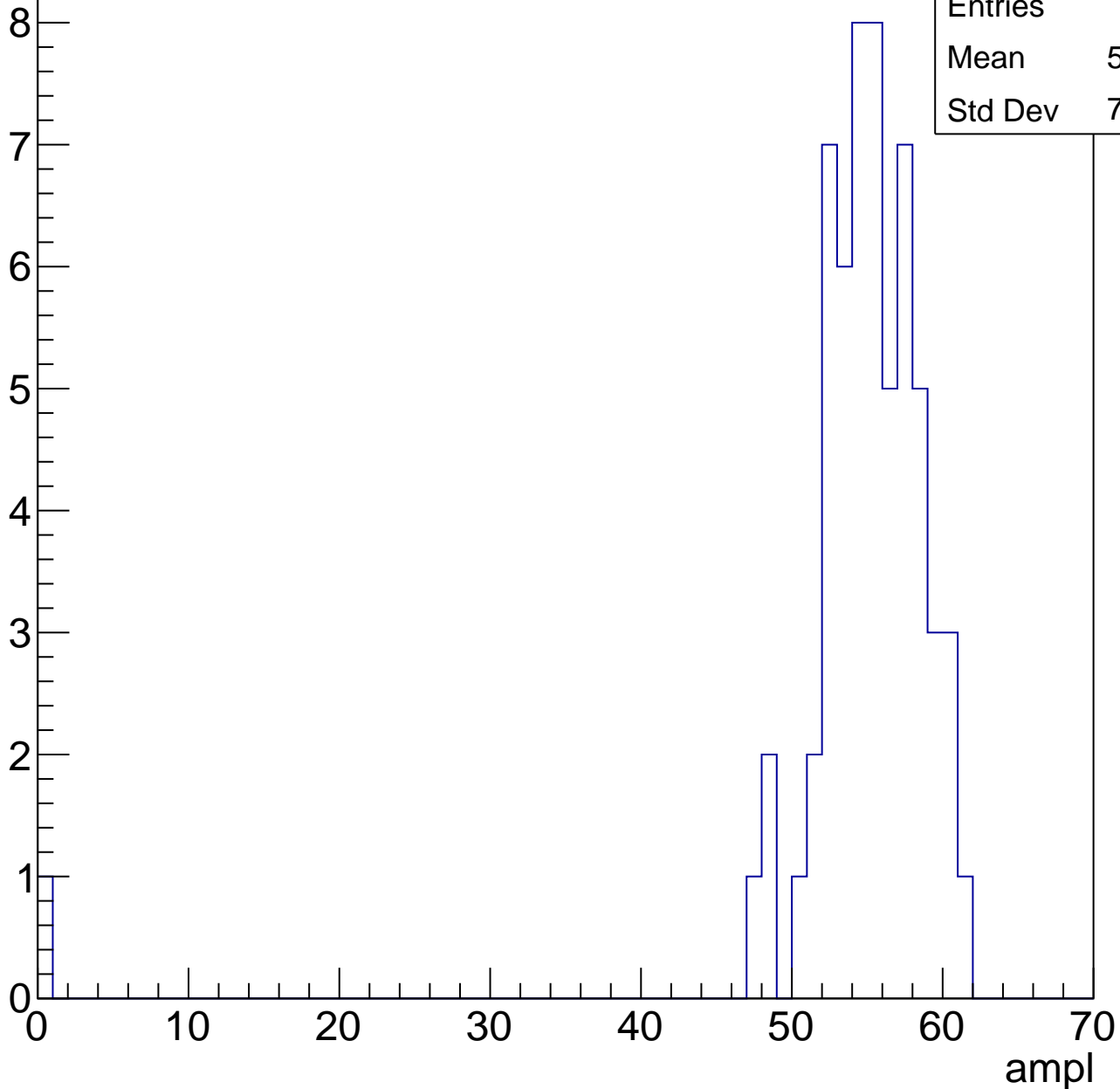


# B0L000S, U26-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	53.93
Std Dev	7.646

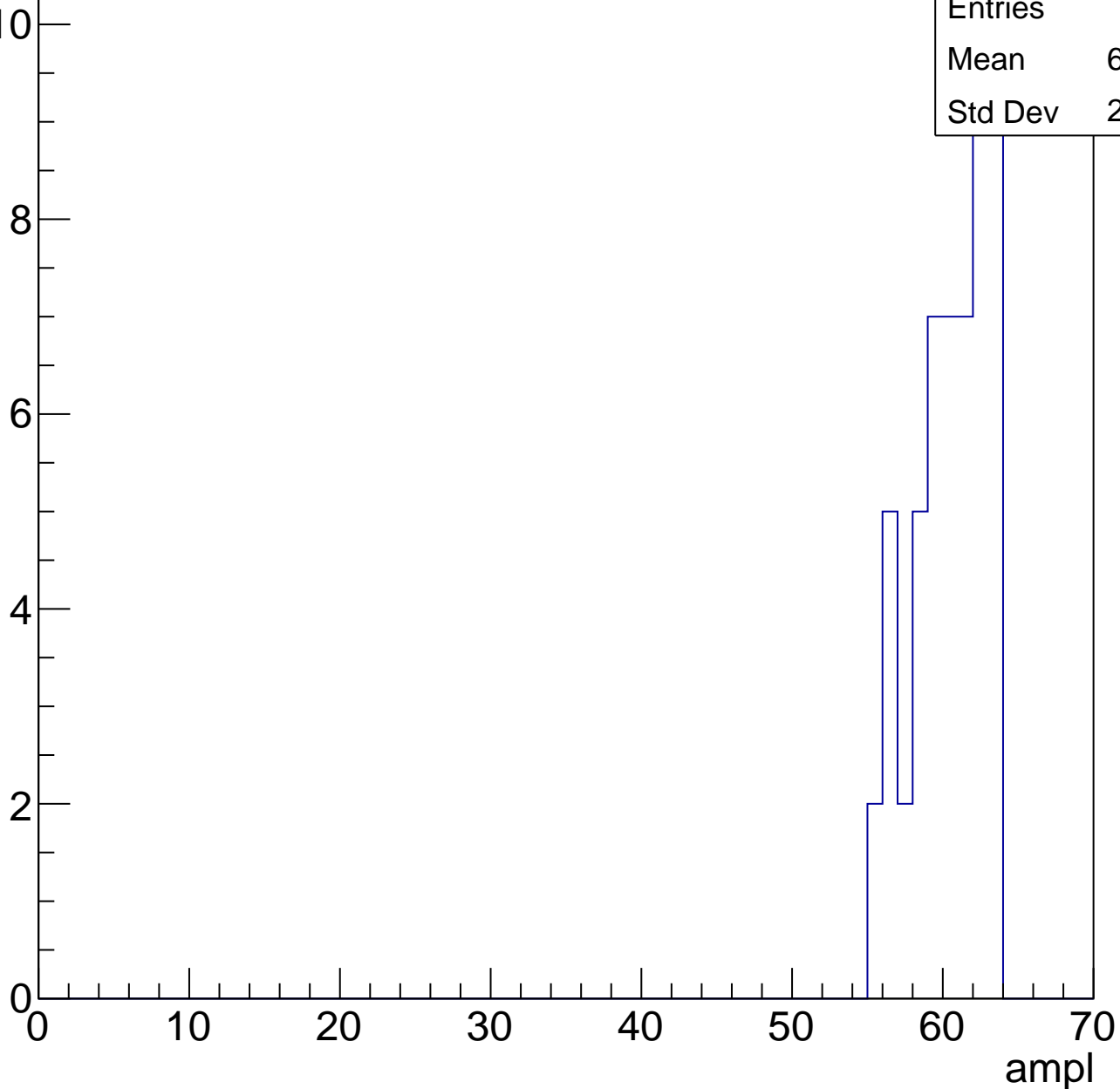


# B0L000S, U26-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

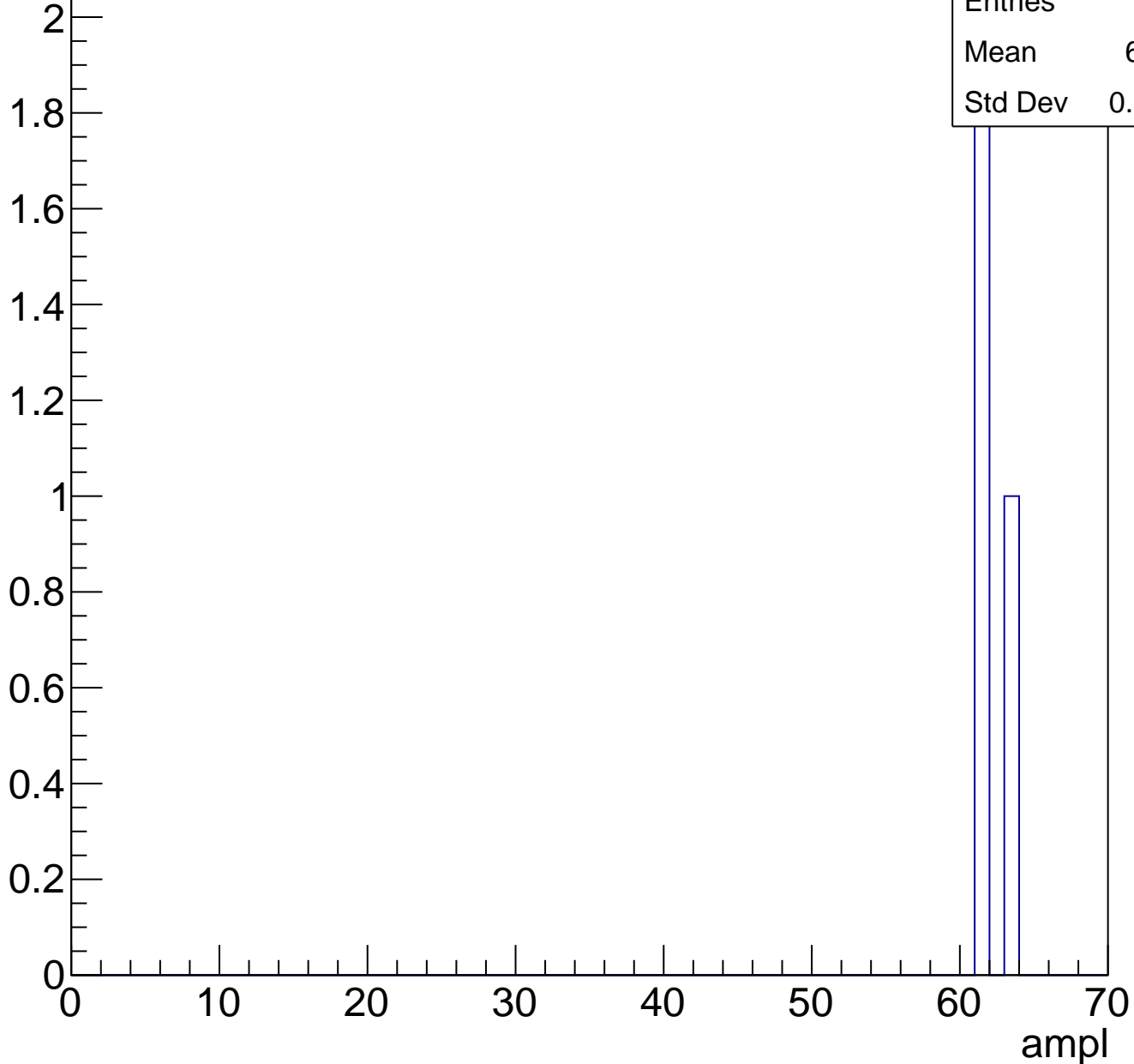
Entries	54
Mean	60.02
Std Dev	2.369



# B0L000S, U26-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch35, adc0

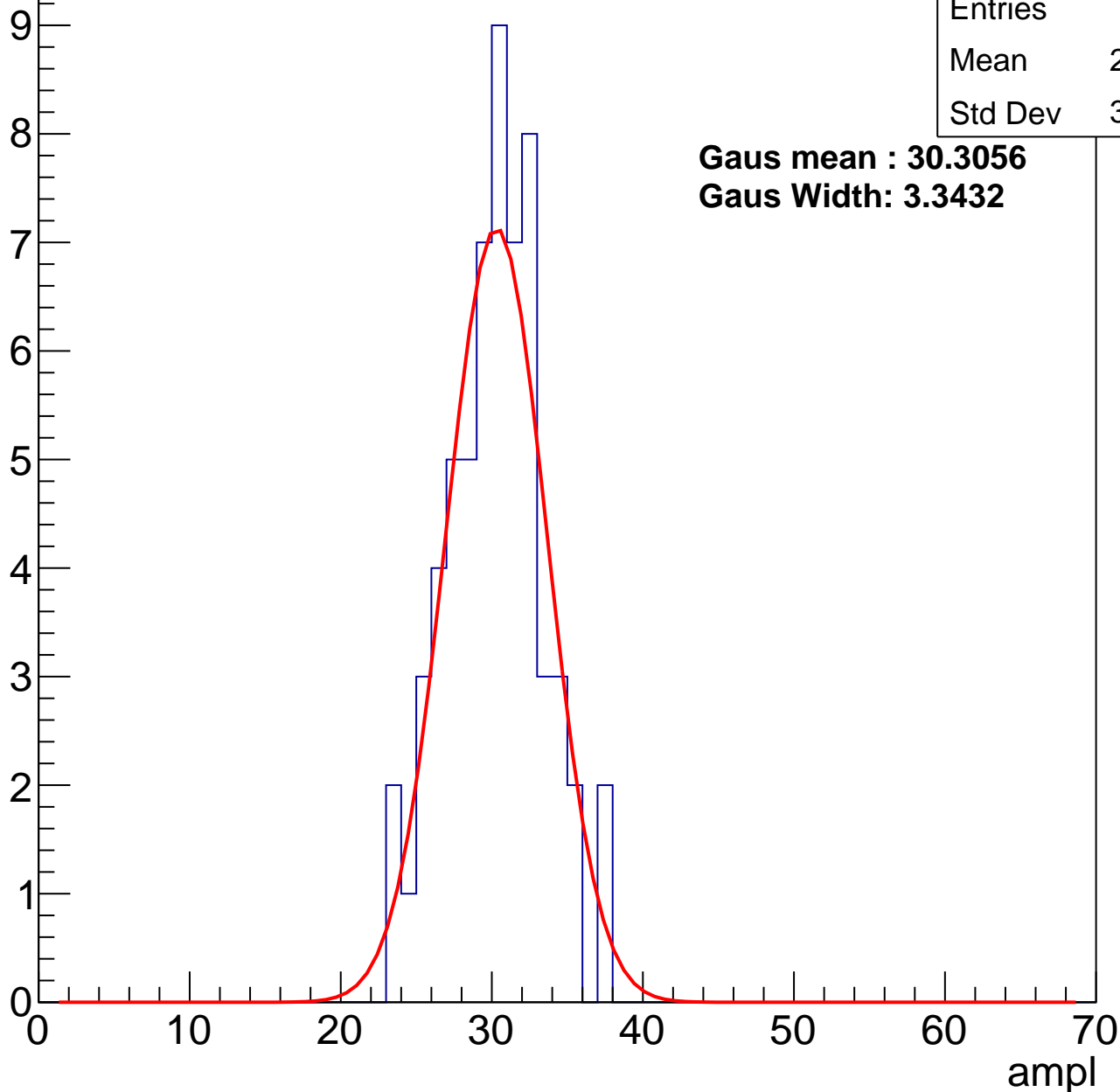
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.75
Std Dev	3.145

**Gaus mean : 30.3056**

**Gaus Width: 3.3432**



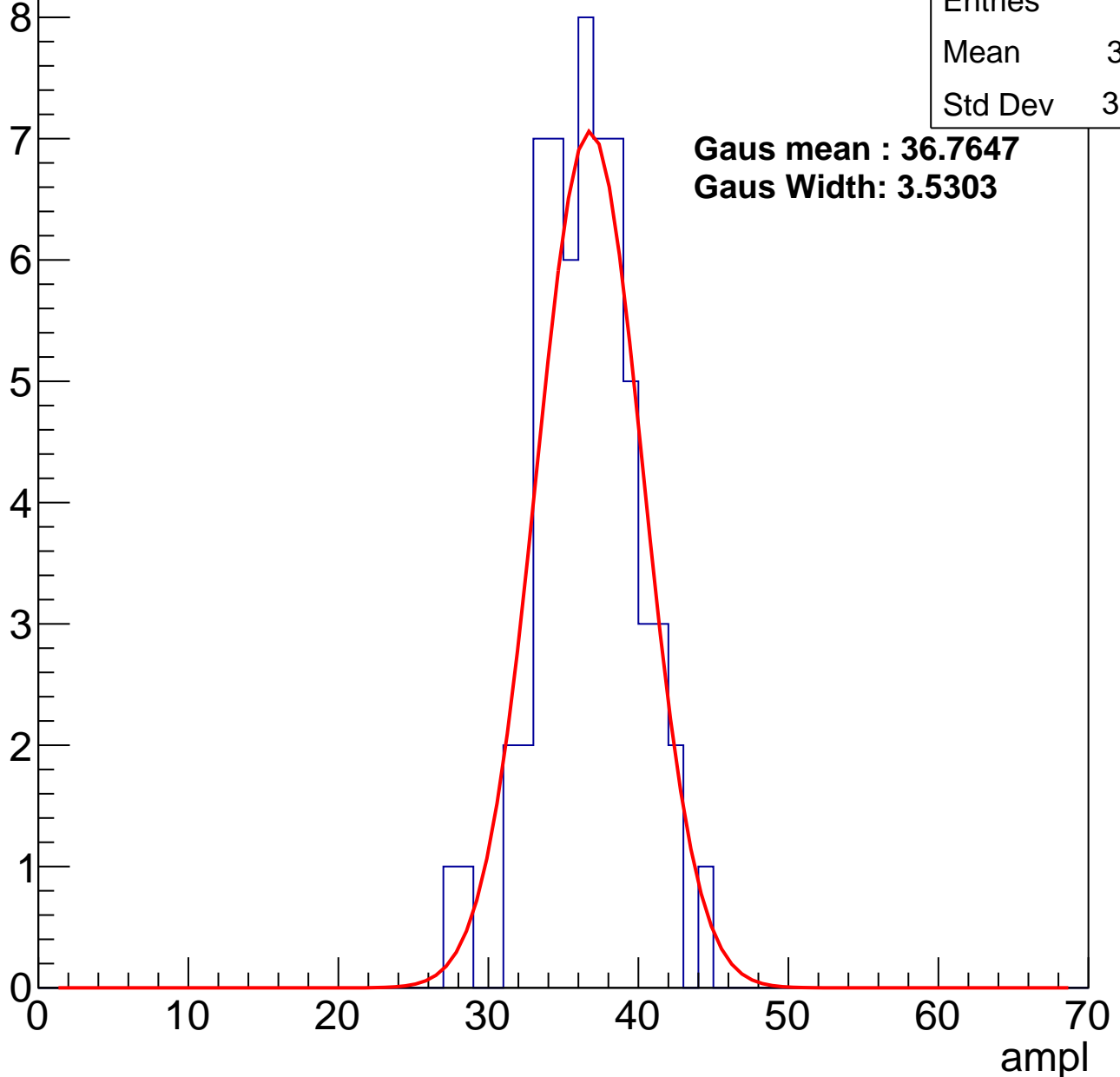
# B0L000S, U26-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.11
Std Dev	3.268

**Gaus mean : 36.7647**  
**Gaus Width: 3.5303**



# B0L000S, U26-ch35, adc2

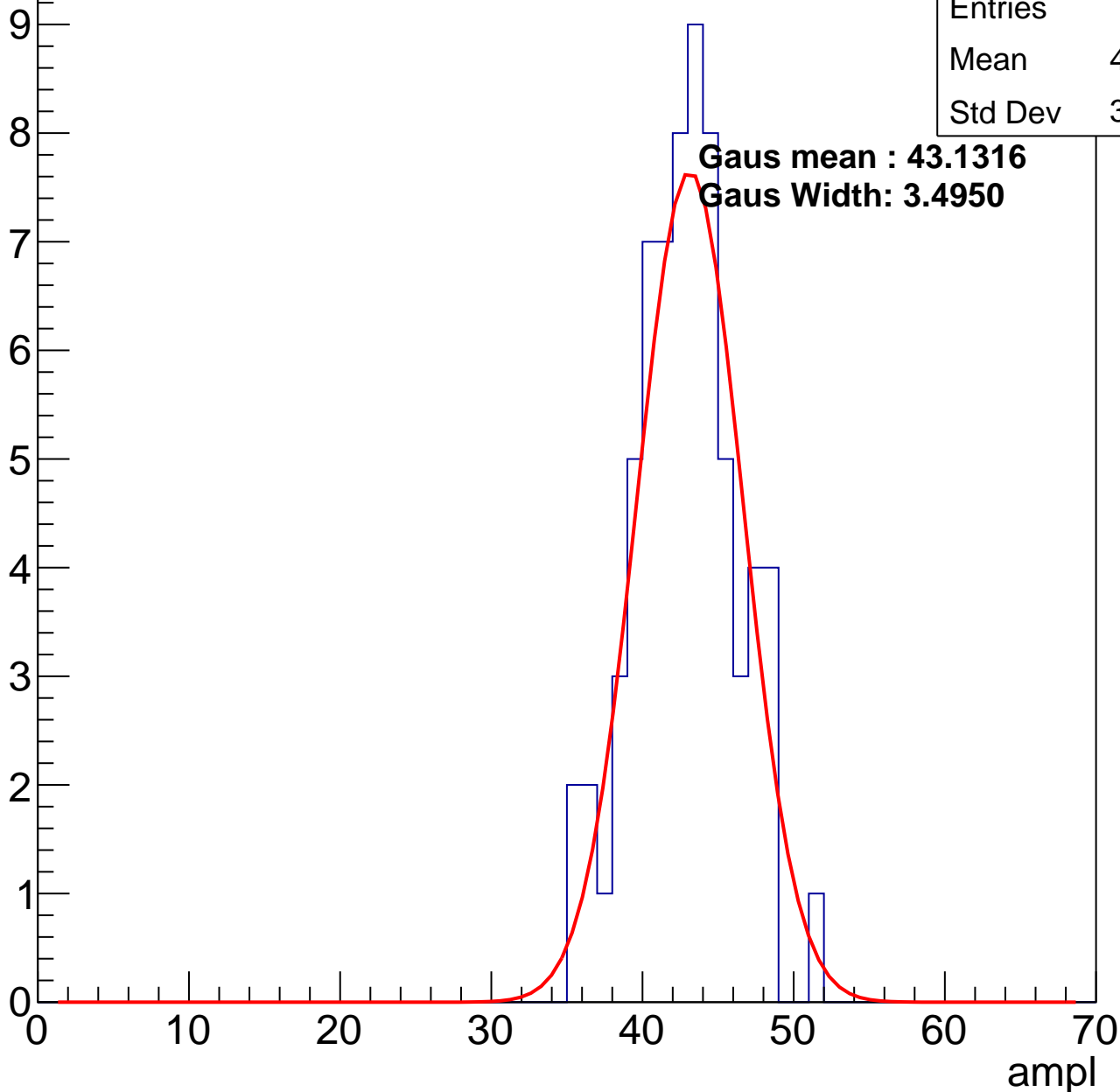
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.38
Std Dev	3.345

**Gaus mean : 43.1316**

**Gaus Width: 3.4950**

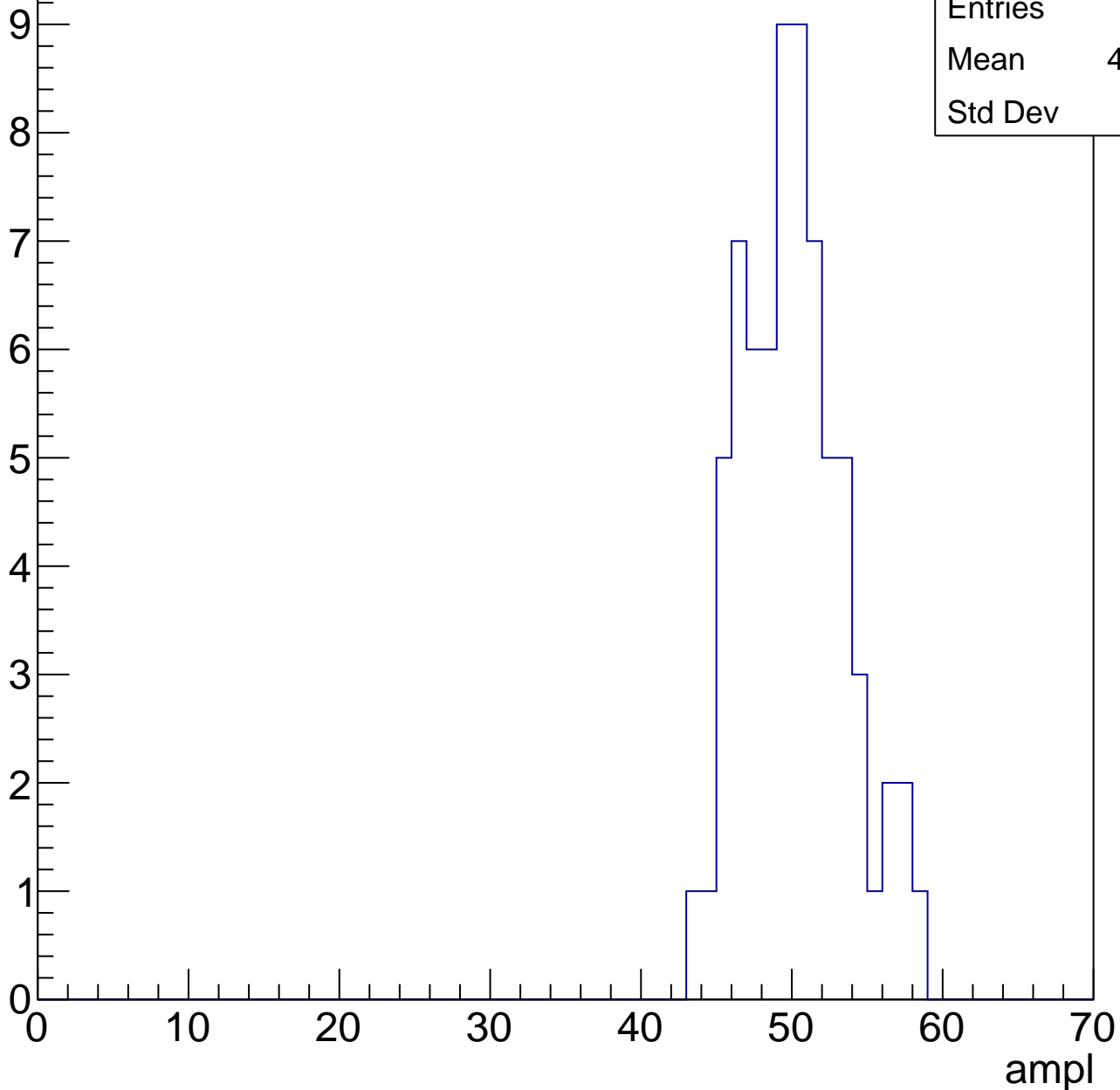


# B0L000S, U26-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

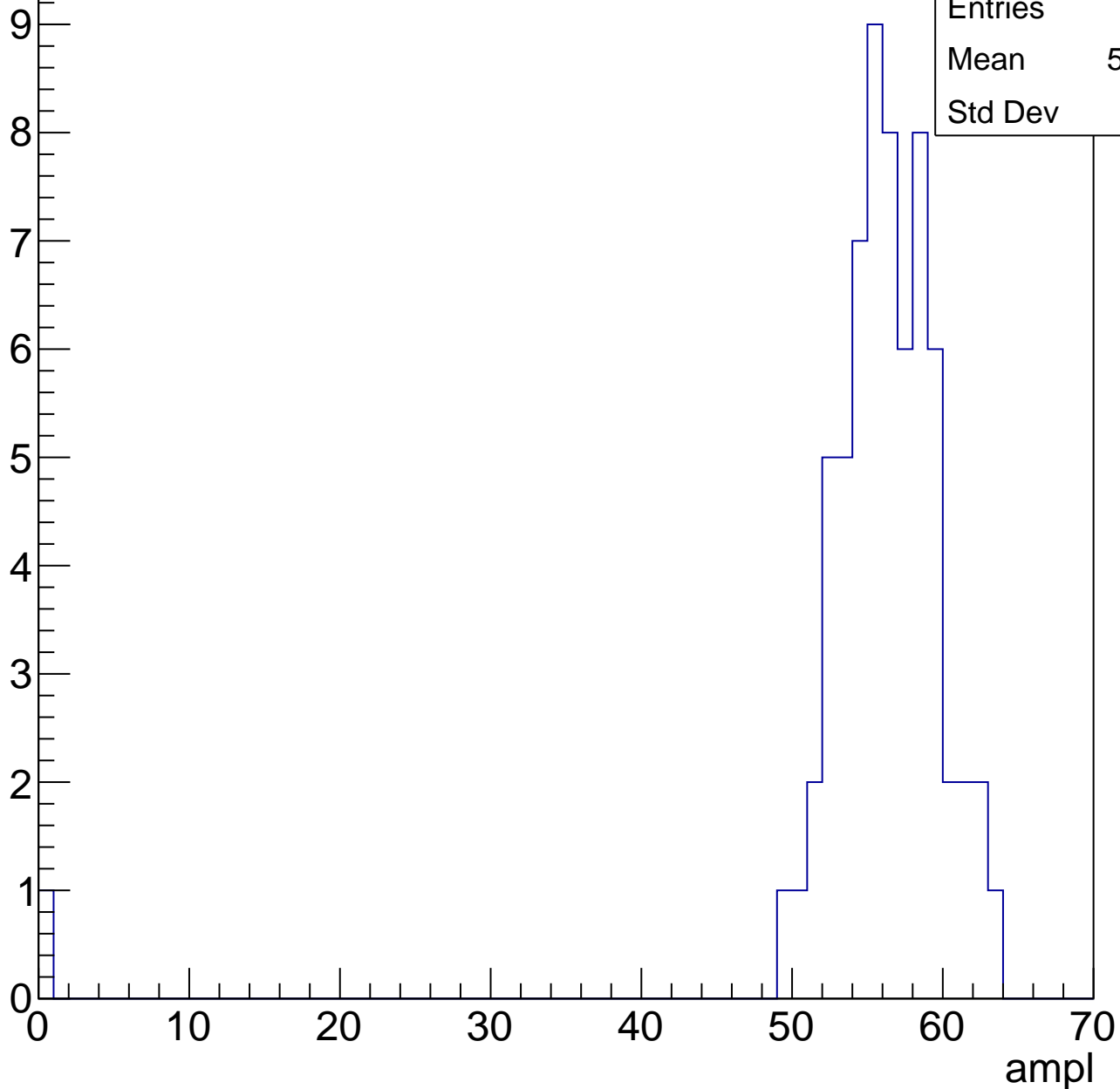
Entries	70
Mean	49.69
Std Dev	3.34



# B0L000S, U26-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

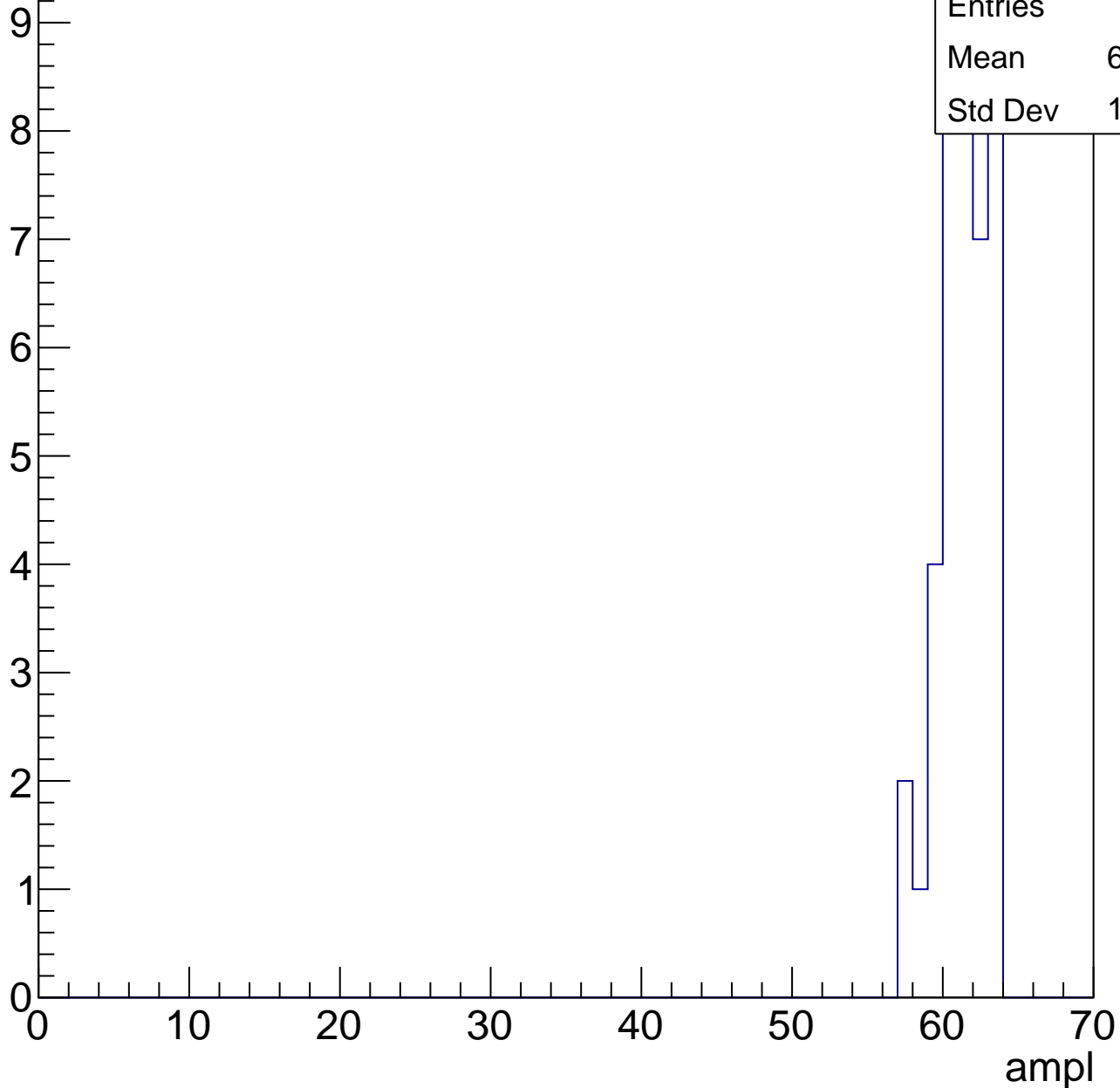
Entry



# B0L000S, U26-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	62
Std Dev	0



# B0L000S, U26-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch36, adc0

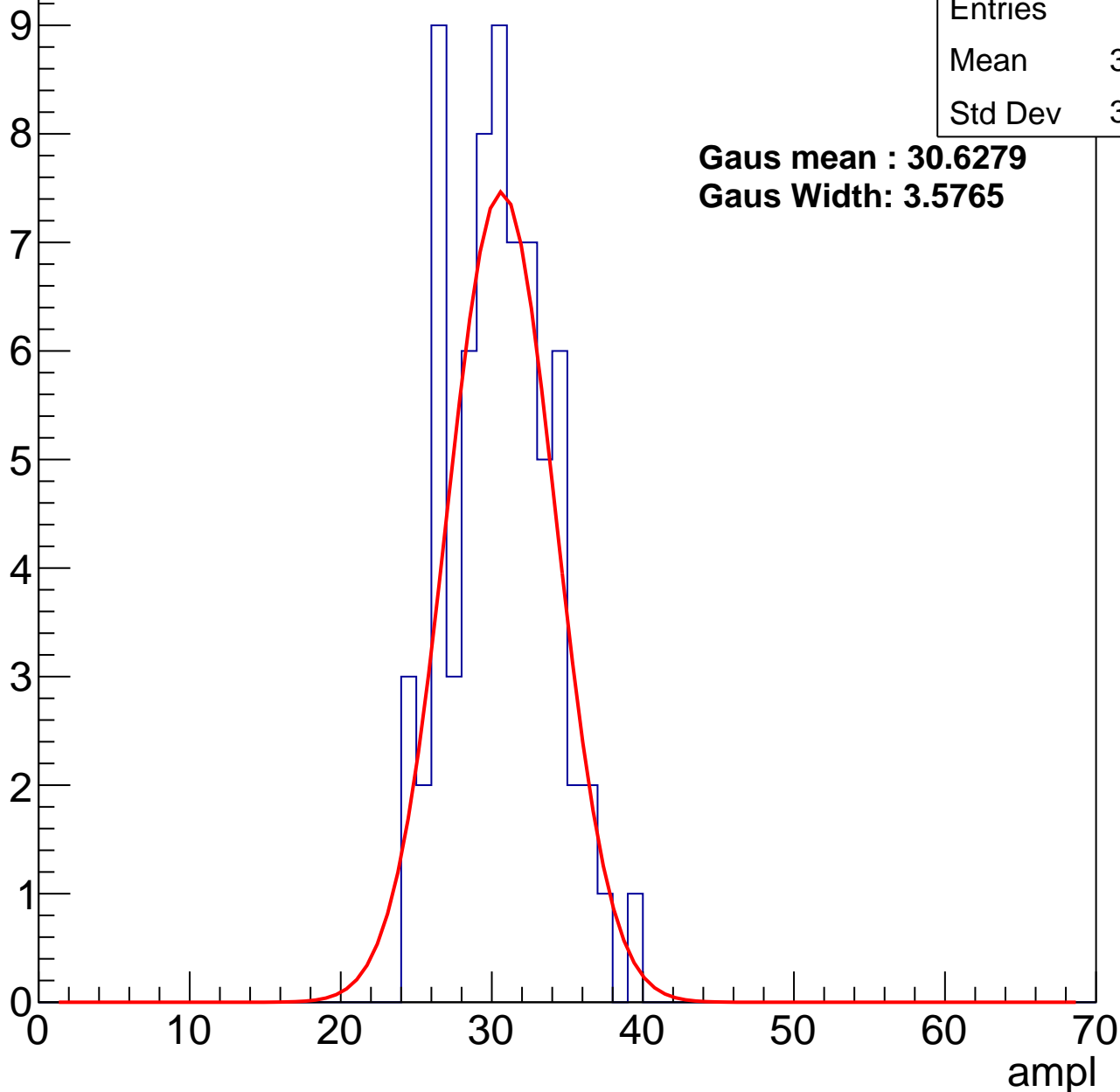
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.07
Std Dev	3.333

**Gaus mean : 30.6279**

**Gaus Width: 3.5765**



# B0L000S, U26-ch36, adc1

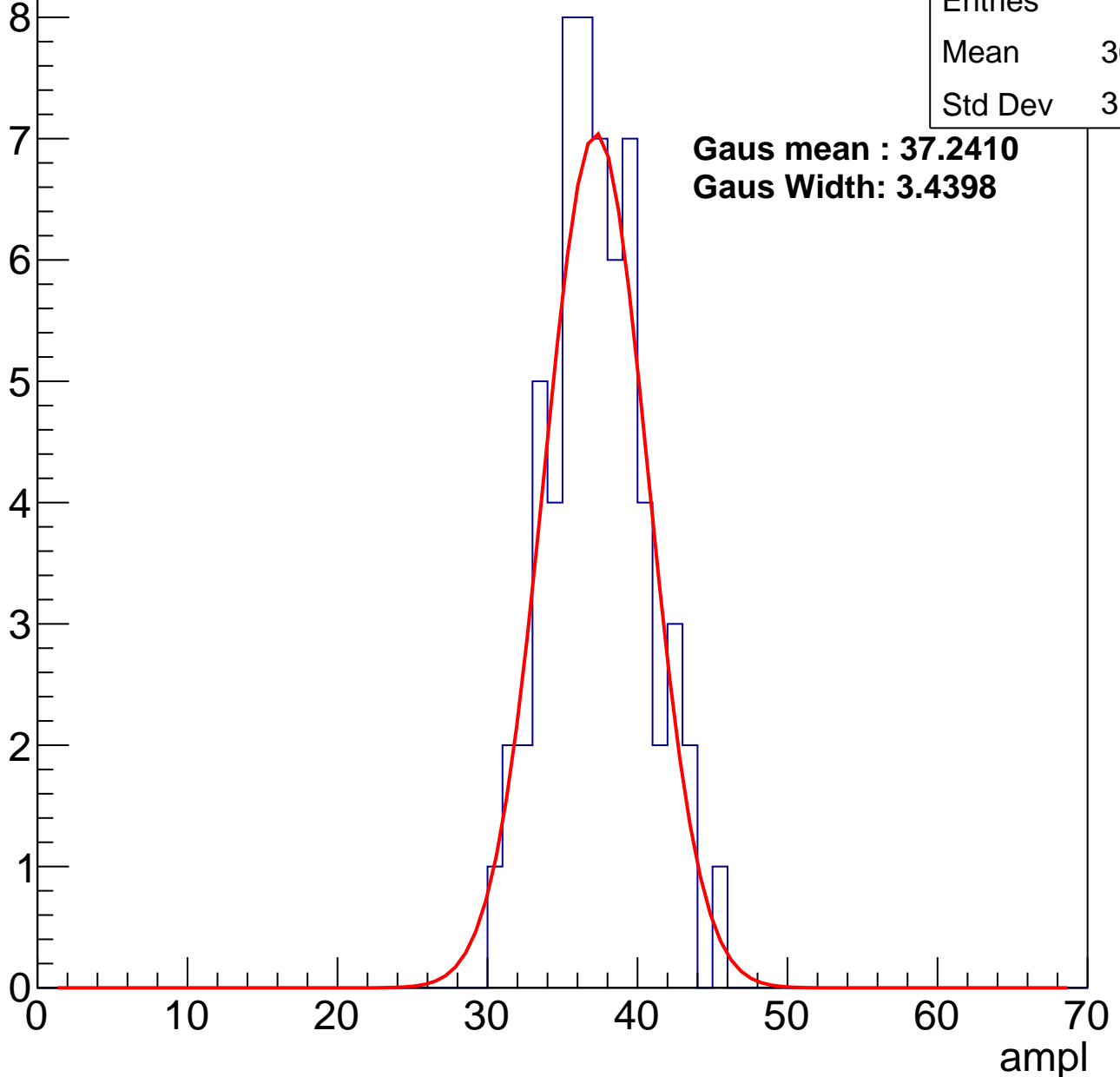
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.84
Std Dev	3.209

**Gaus mean : 37.2410**

**Gaus Width: 3.4398**



# B0L000S, U26-ch36, adc2

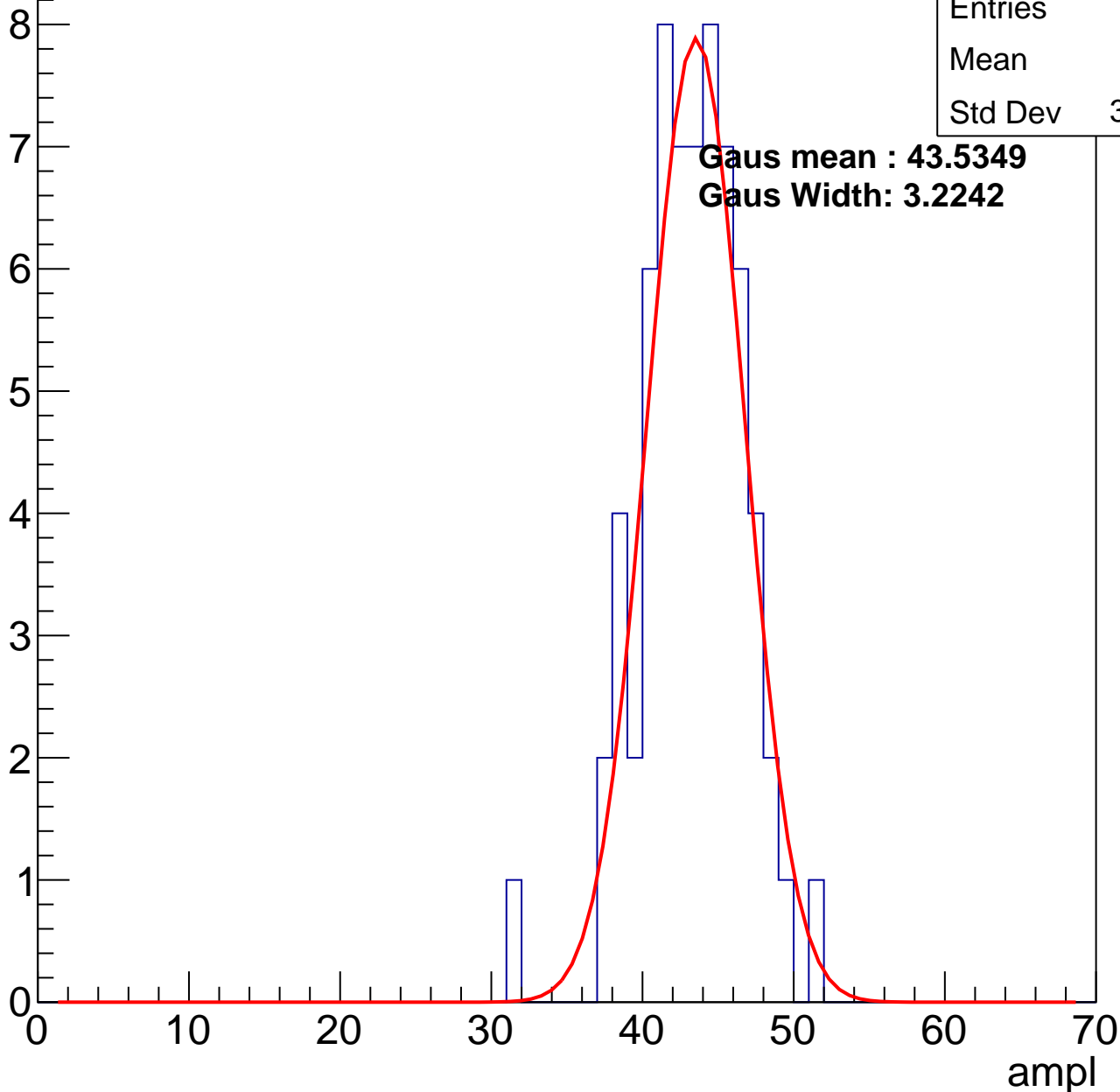
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	42.8
Std Dev	3.354

**Gaus mean : 43.5349**

**Gaus Width: 3.2242**

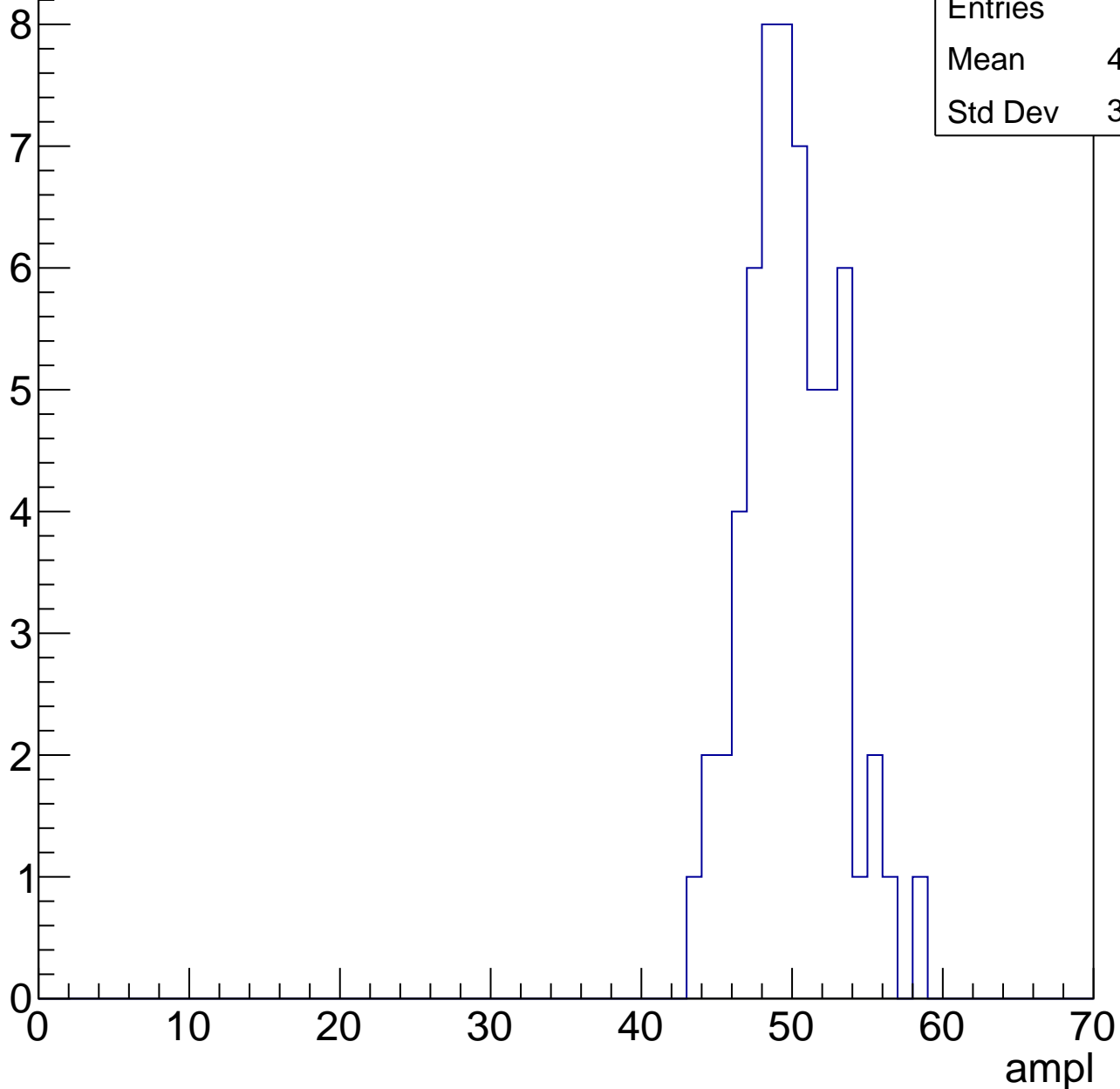


# B0L000S, U26-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.56
Std Dev	3.088

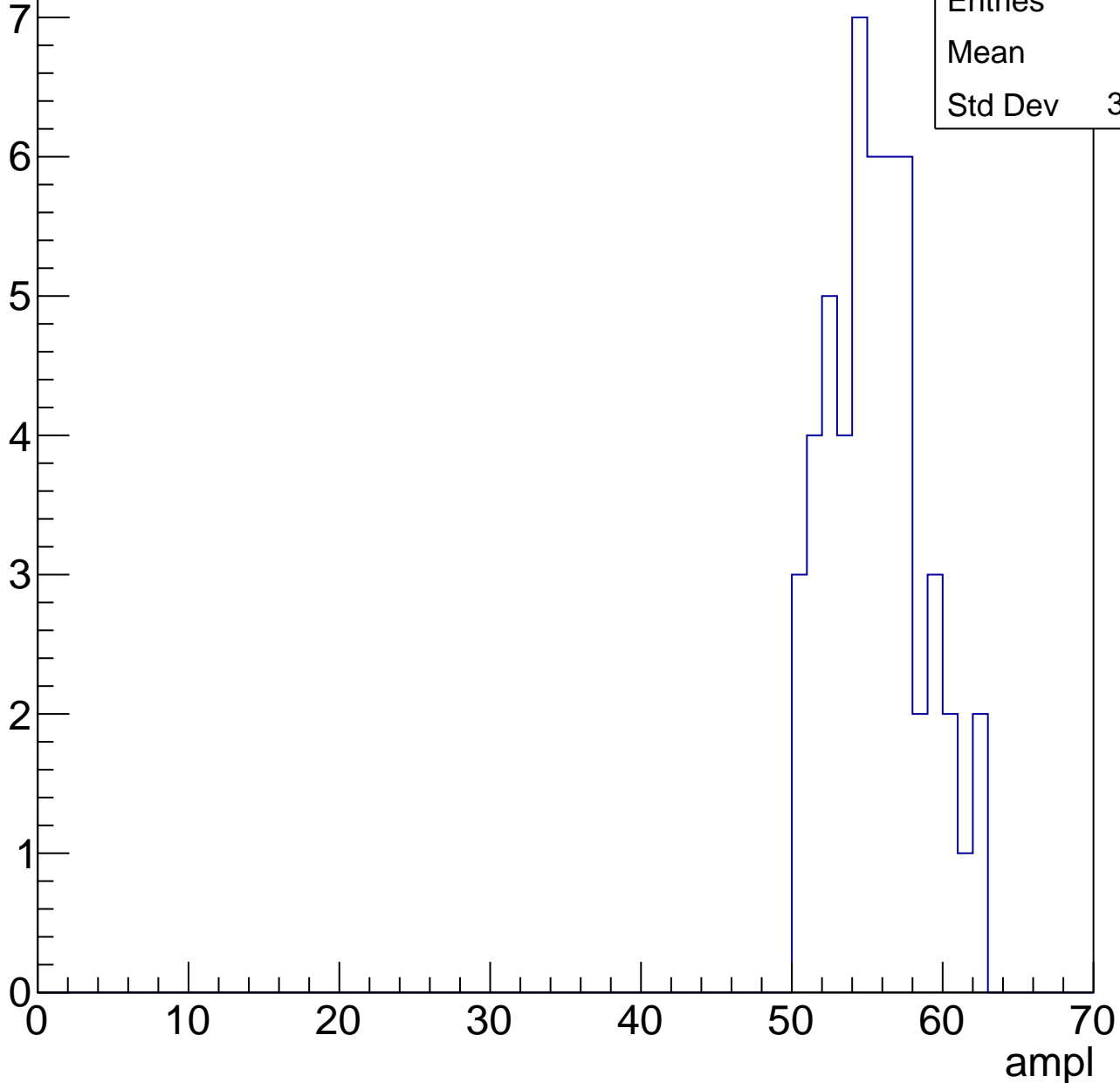


# B0L000S, U26-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	55.1
Std Dev	3.089

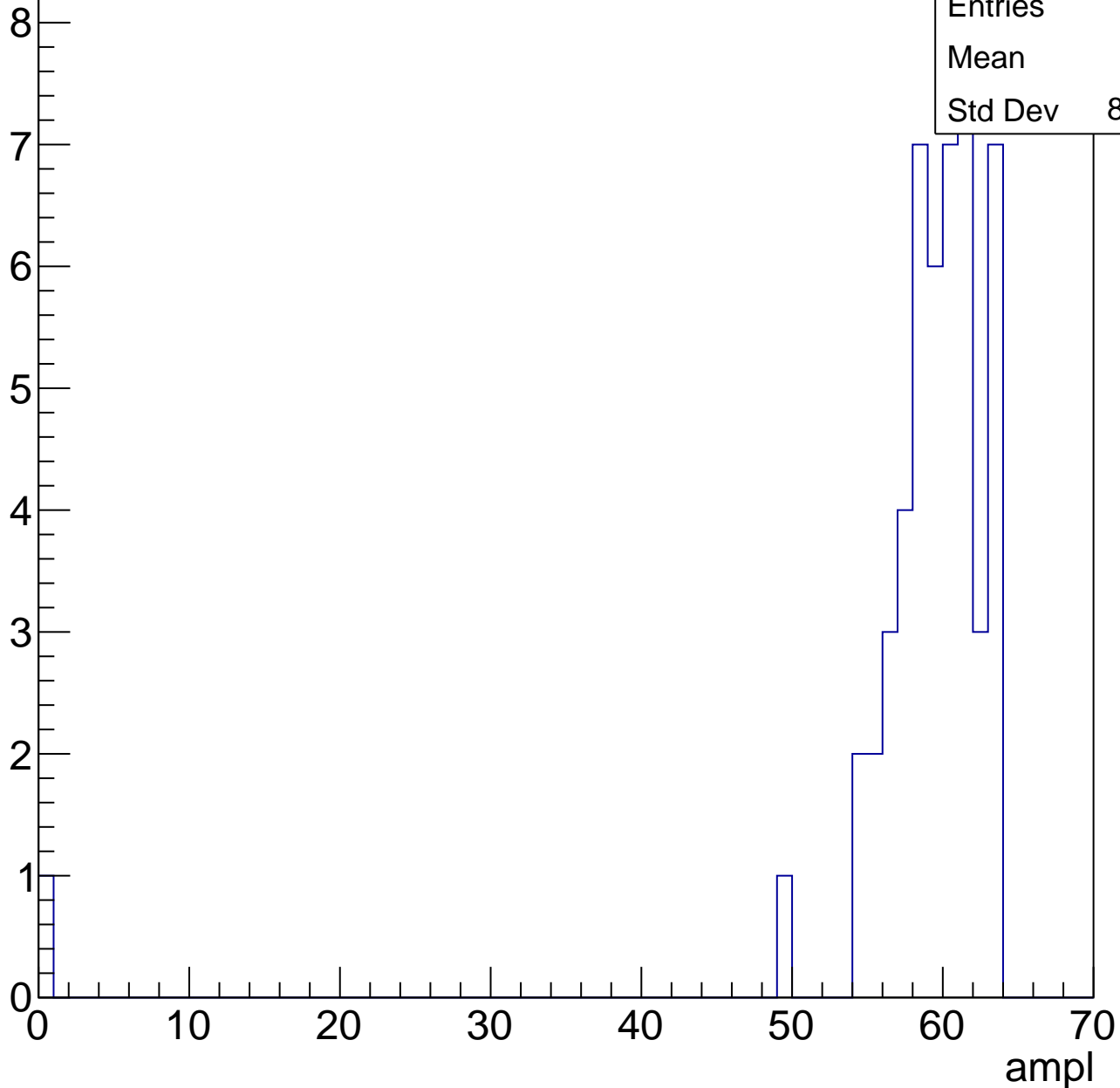


# B0L000S, U26-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	58
Std Dev	8.677

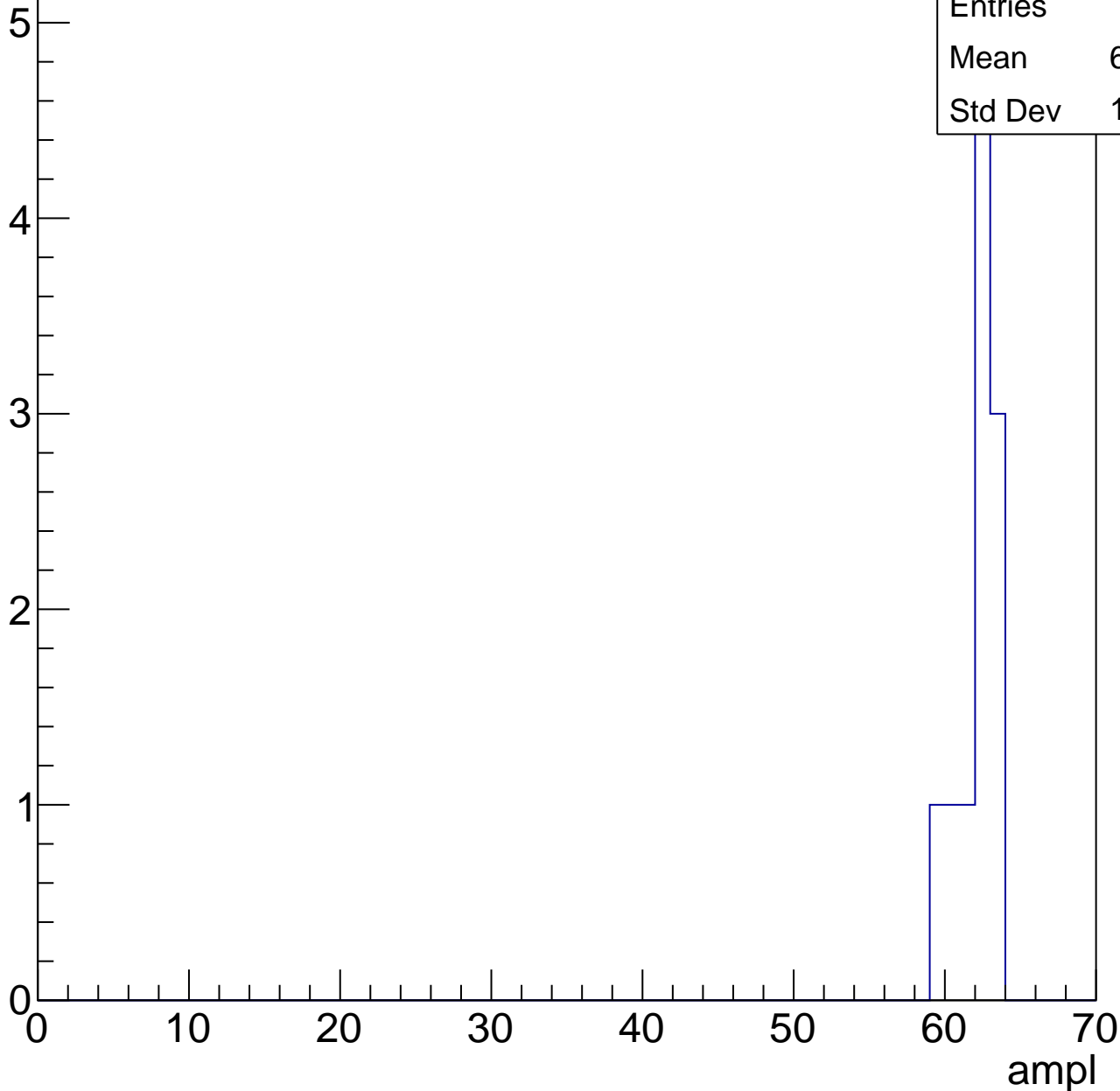


# B0L000S, U26-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	61.73
Std Dev	1.213





# B0L000S, U26-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch37, adc0

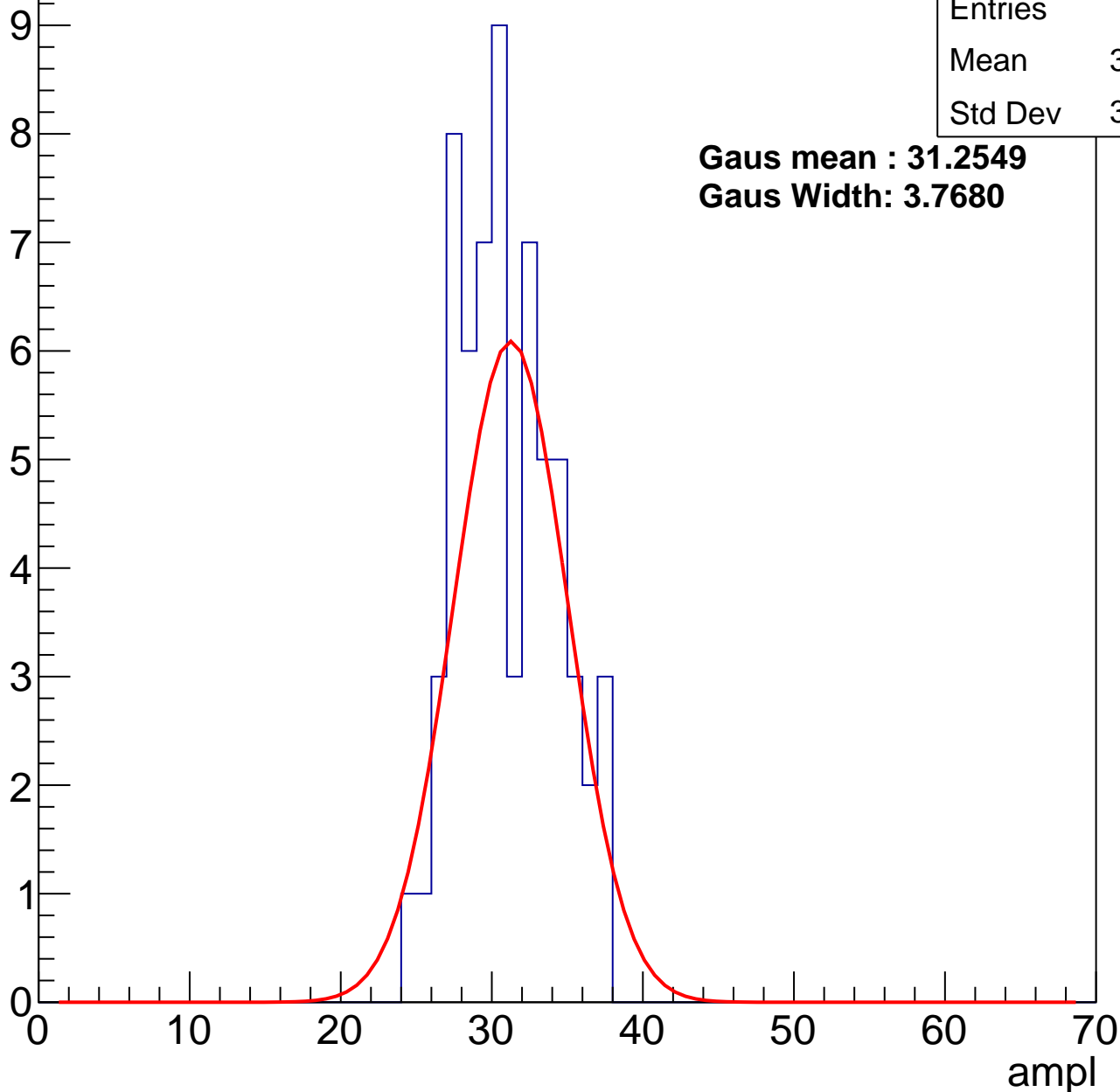
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.54
Std Dev	3.196

**Gaus mean : 31.2549**

**Gaus Width: 3.7680**



# B0L000S, U26-ch37, adc1

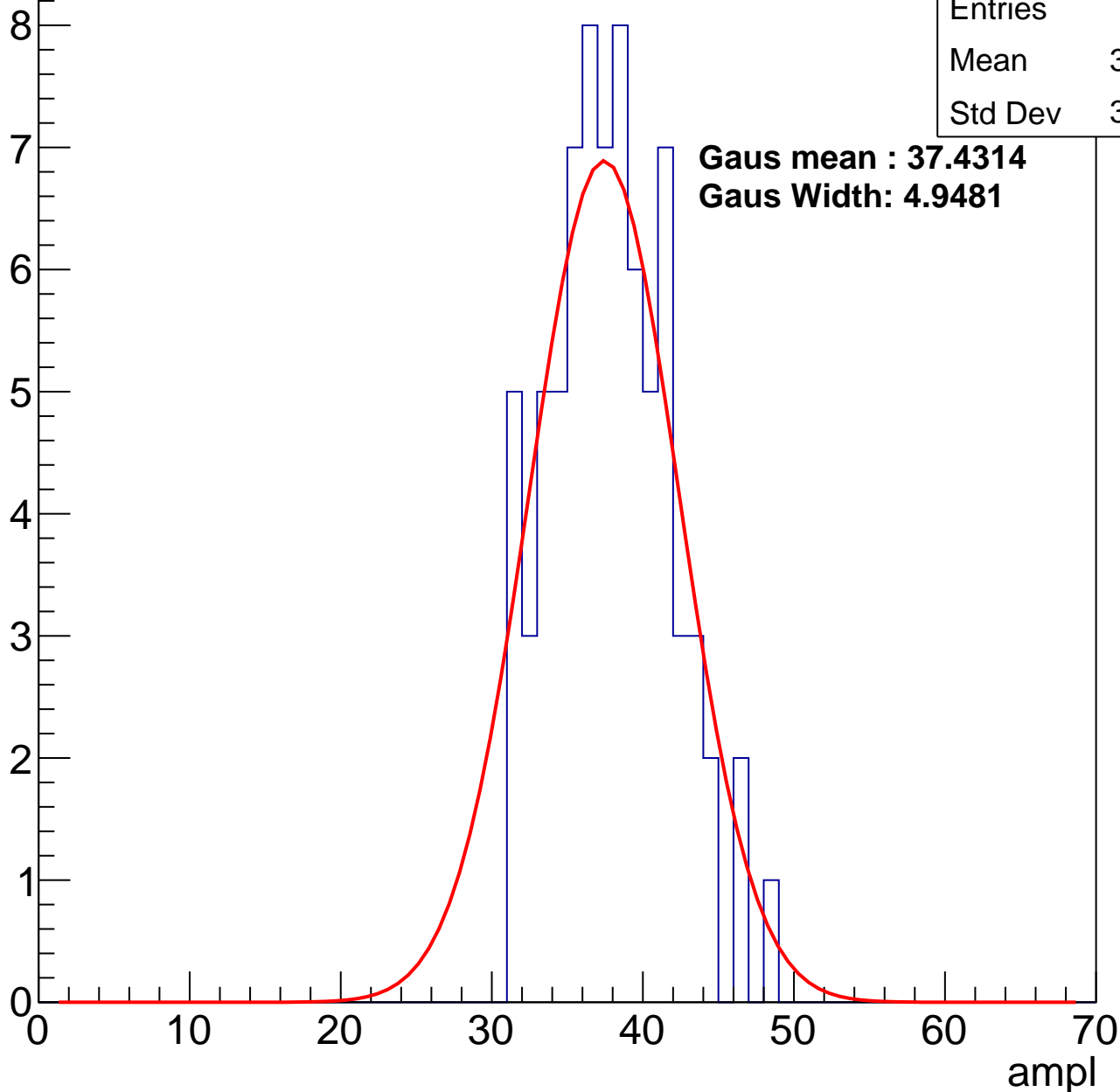
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	37.48
Std Dev	3.867

**Gaus mean : 37.4314**

**Gaus Width: 4.9481**



# B0L000S, U26-ch37, adc2

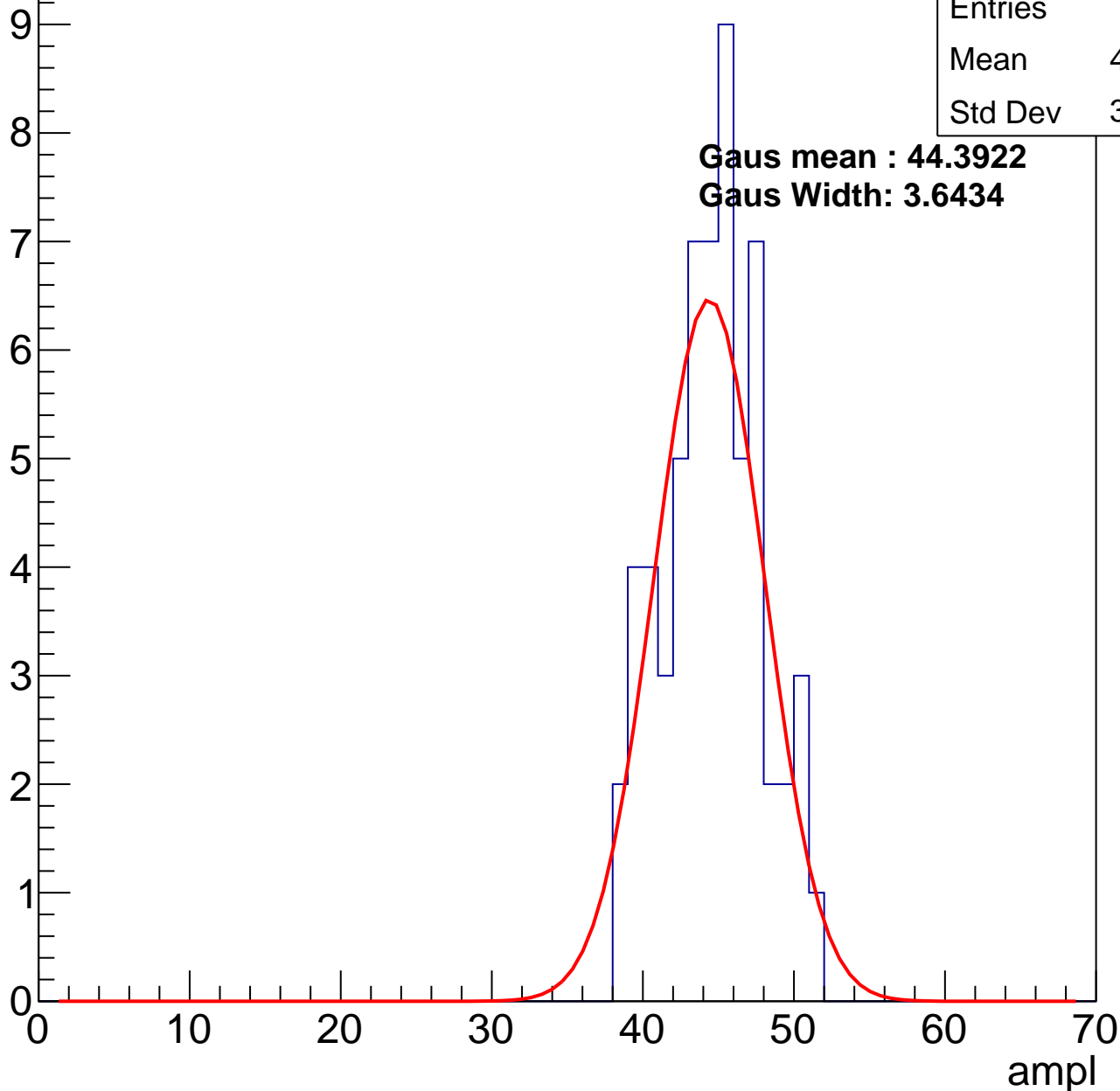
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.15
Std Dev	3.187

**Gaus mean : 44.3922**

**Gaus Width: 3.6434**

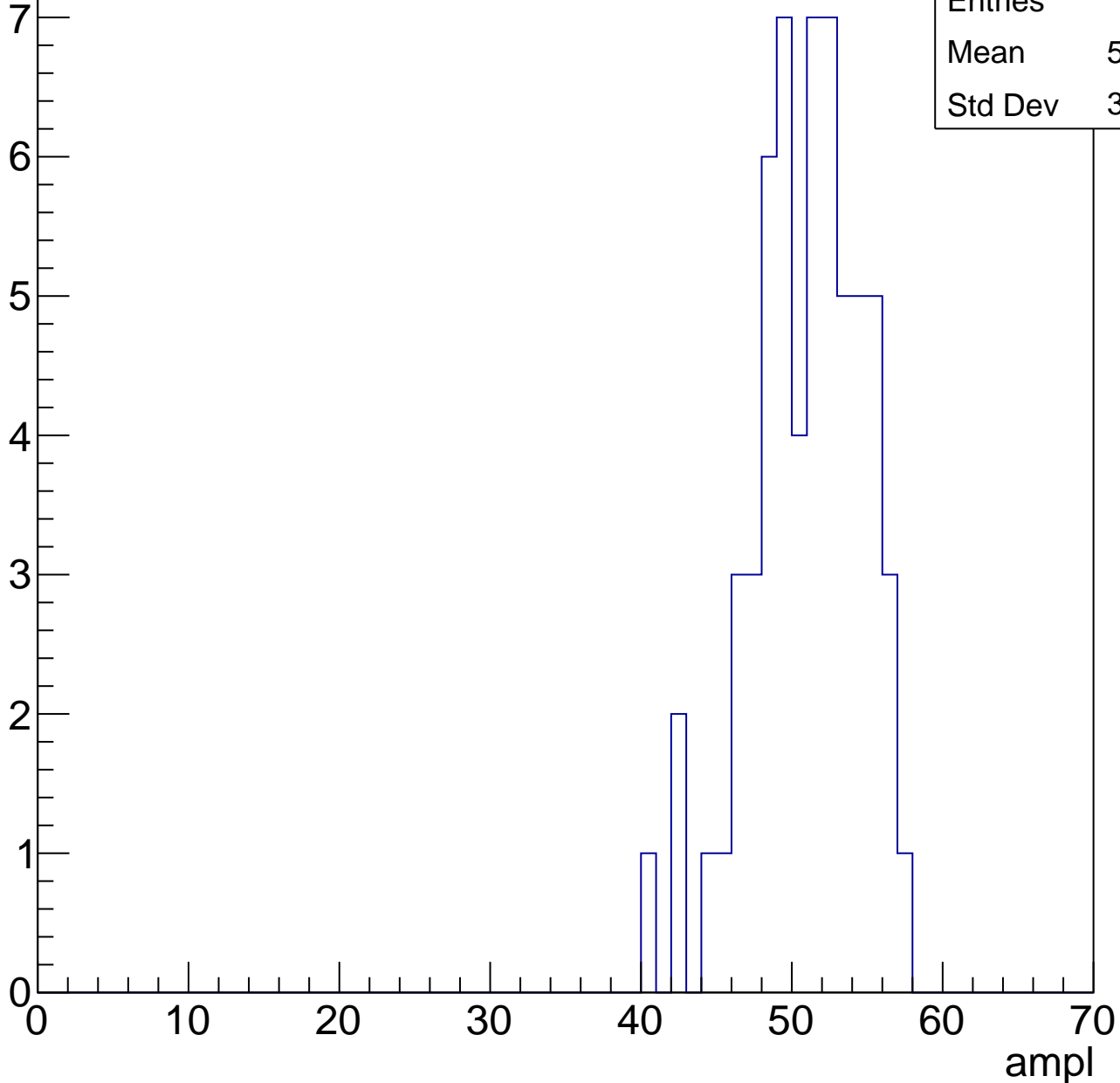


# B0L000S, U26-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	50.48
Std Dev	3.674

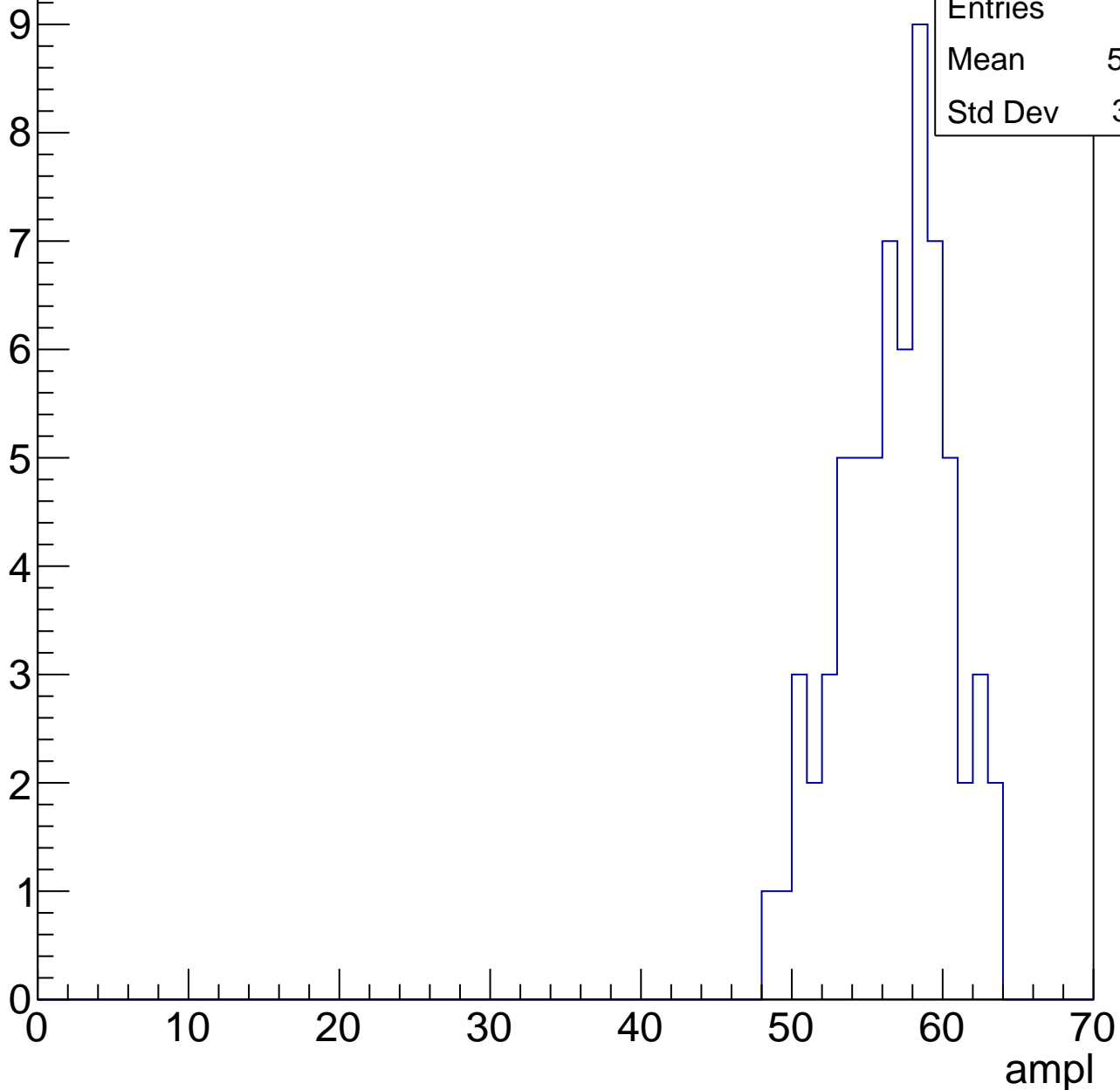


# B0L000S, U26-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	56.33
Std Dev	3.531

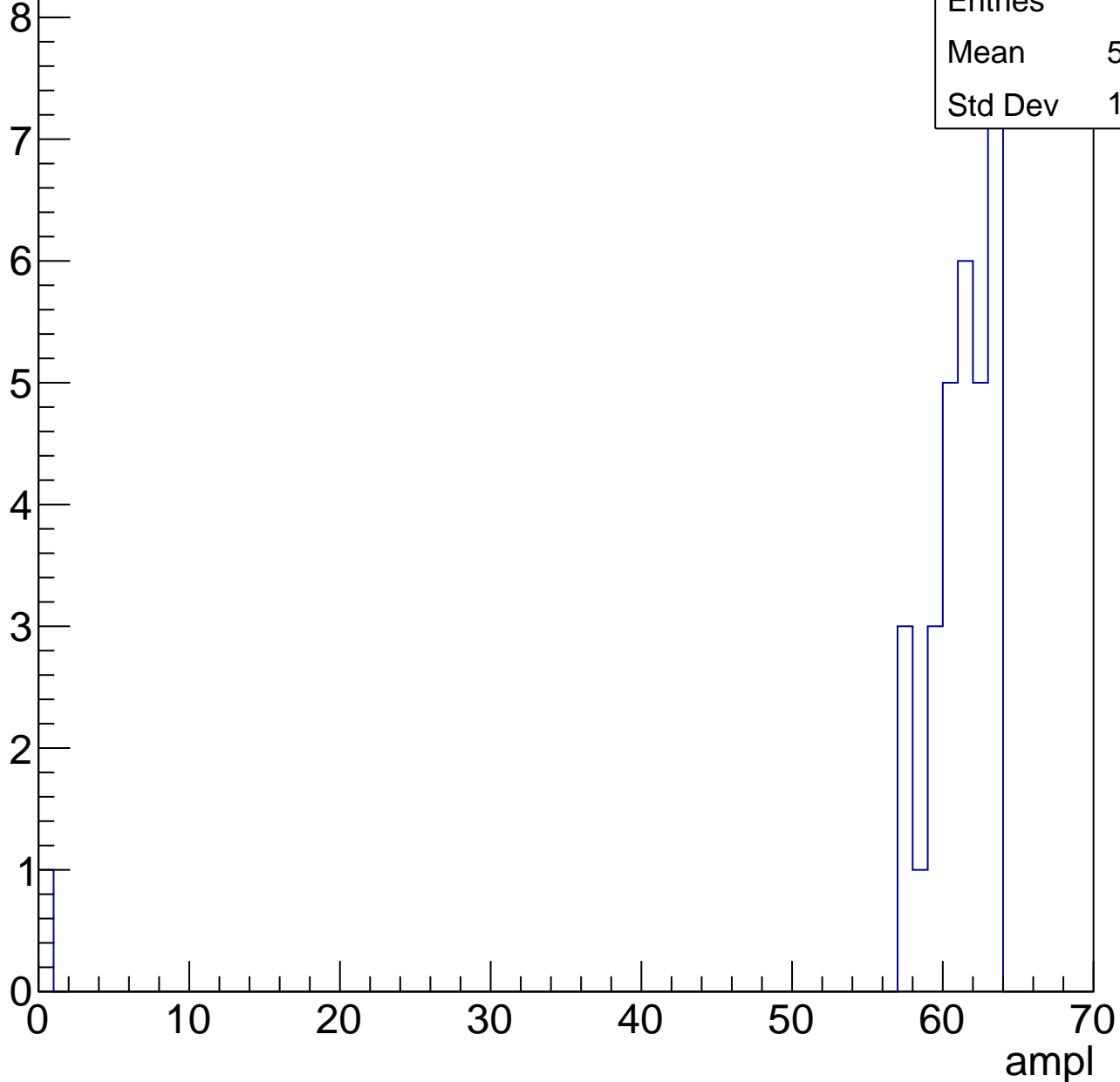


# B0L000S, U26-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

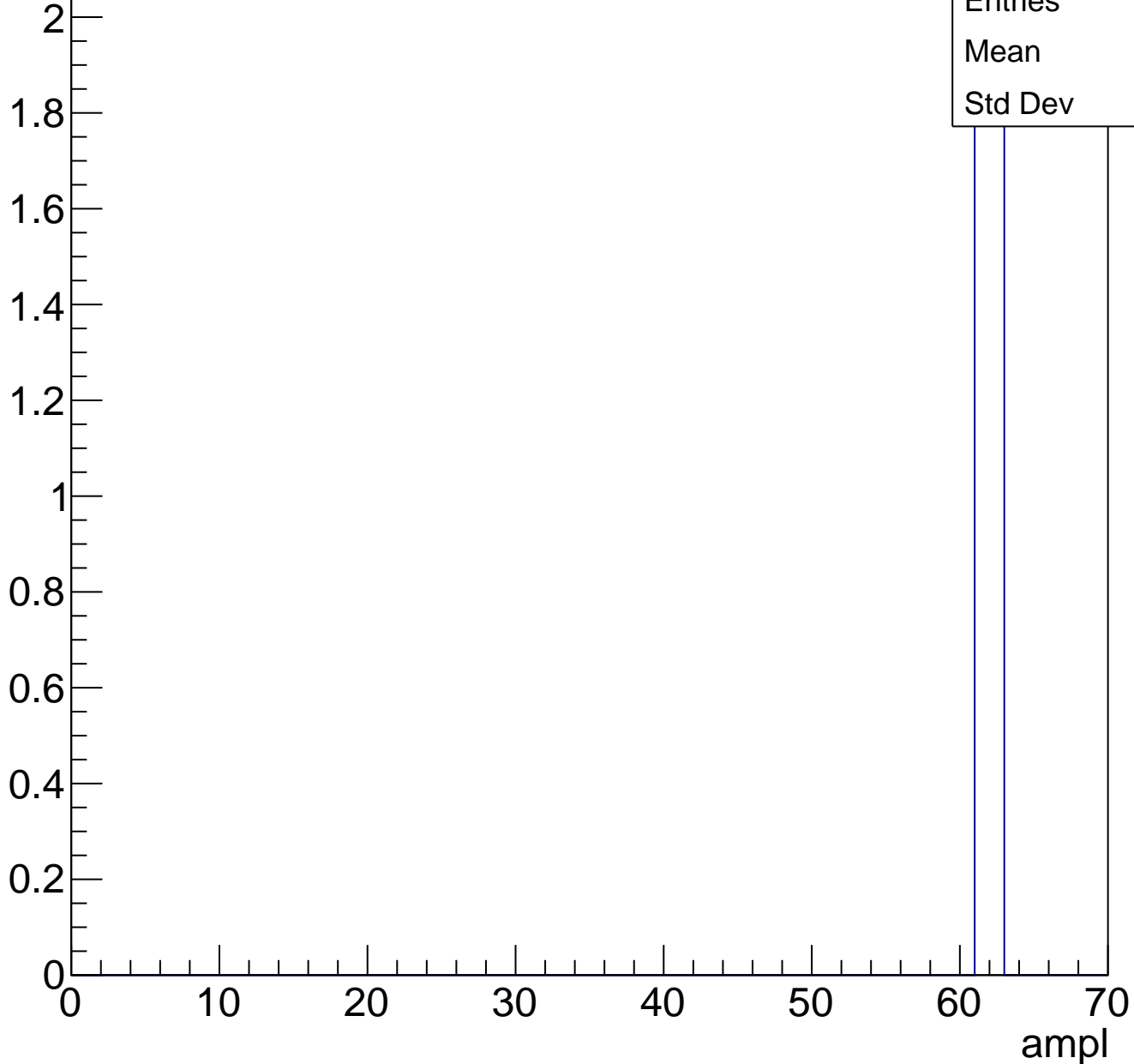
Entries	32
Mean	58.94
Std Dev	10.75



# B0L000S, U26-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B0L000S, U26-ch38, adc0

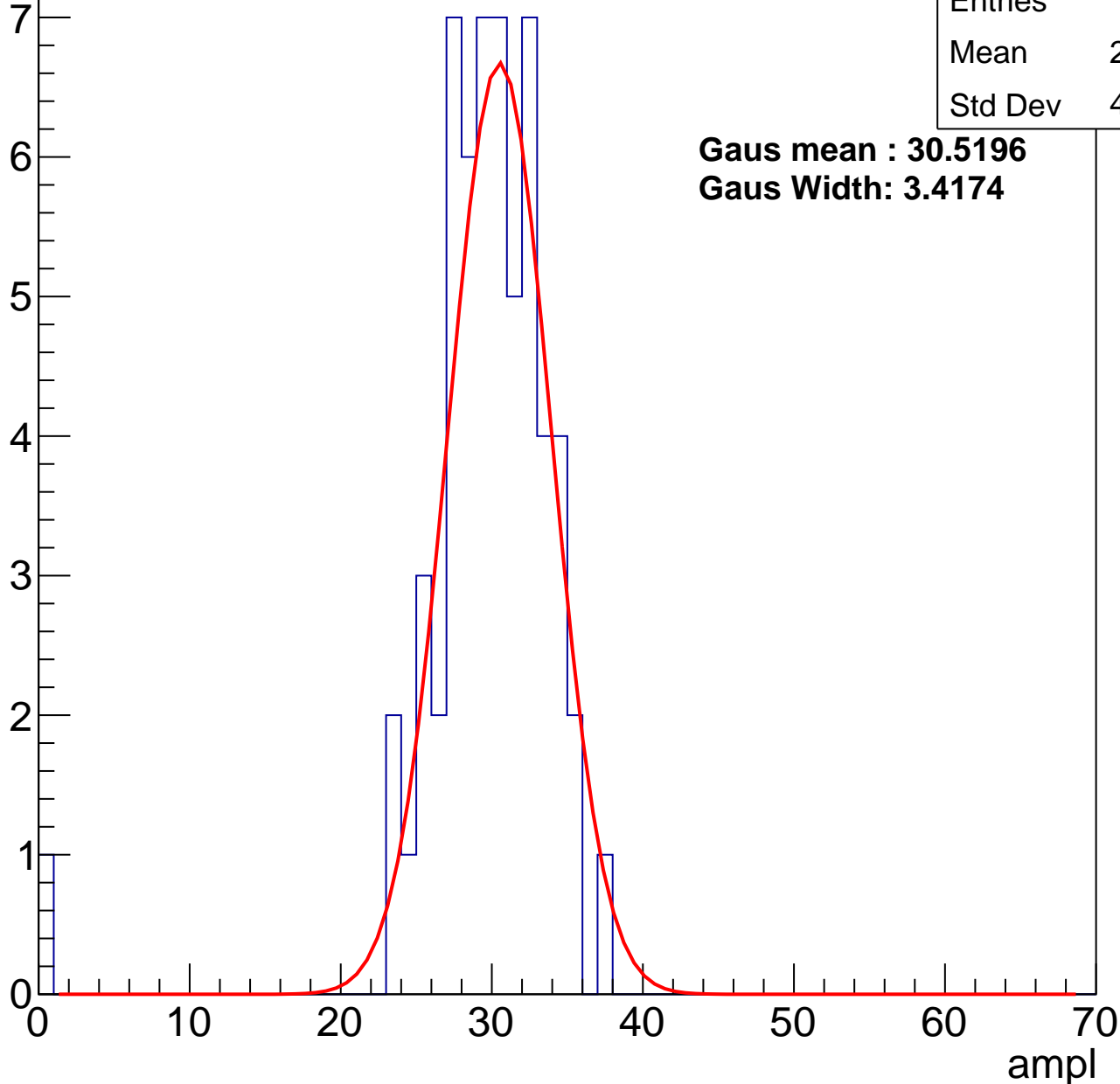
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.17
Std Dev	4.917

**Gaus mean : 30.5196**

**Gaus Width: 3.4174**



# B0L000S, U26-ch38, adc1

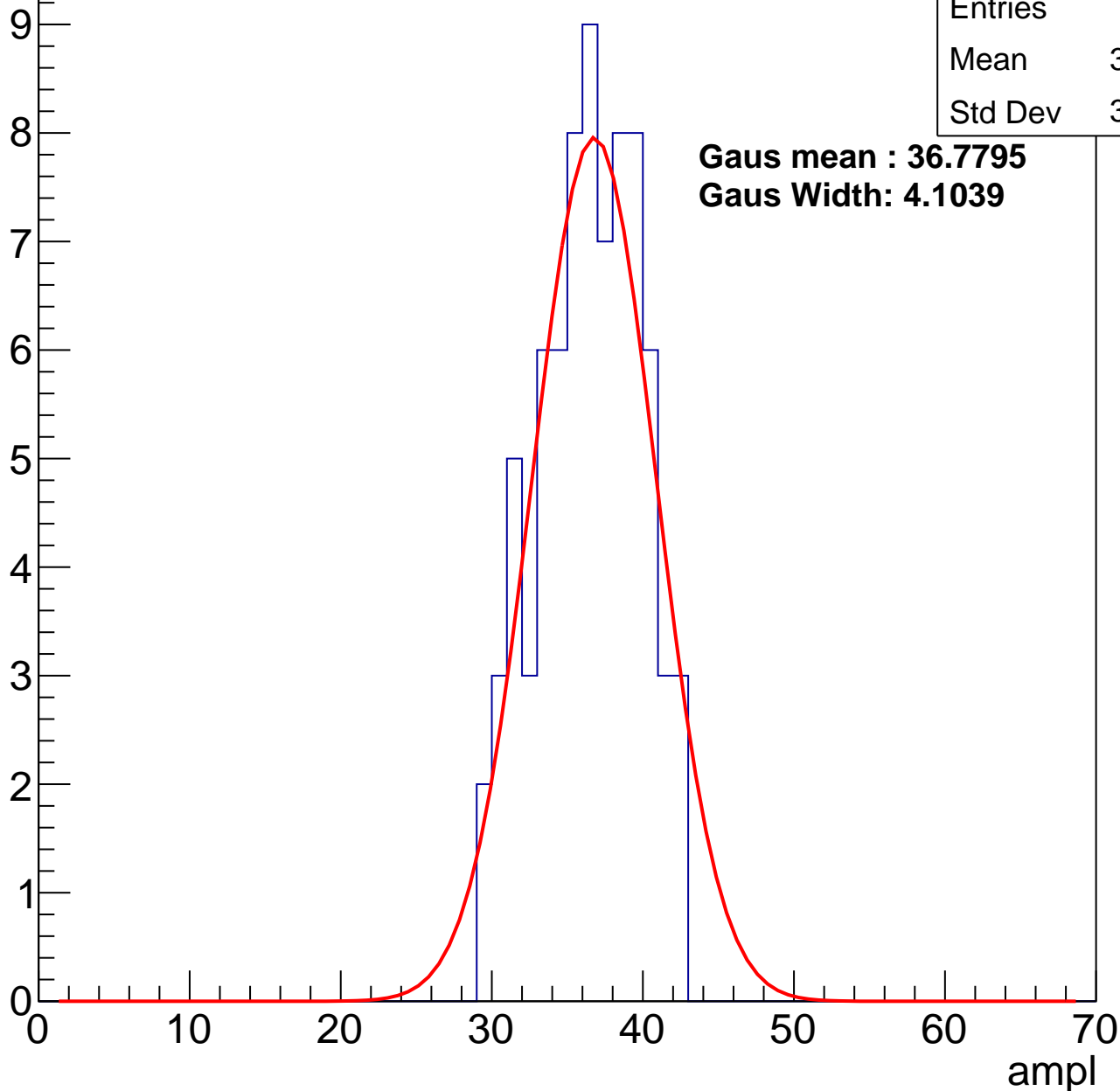
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	35.96
Std Dev	3.332

**Gaus mean : 36.7795**

**Gaus Width: 4.1039**



# B0L000S, U26-ch38, adc2

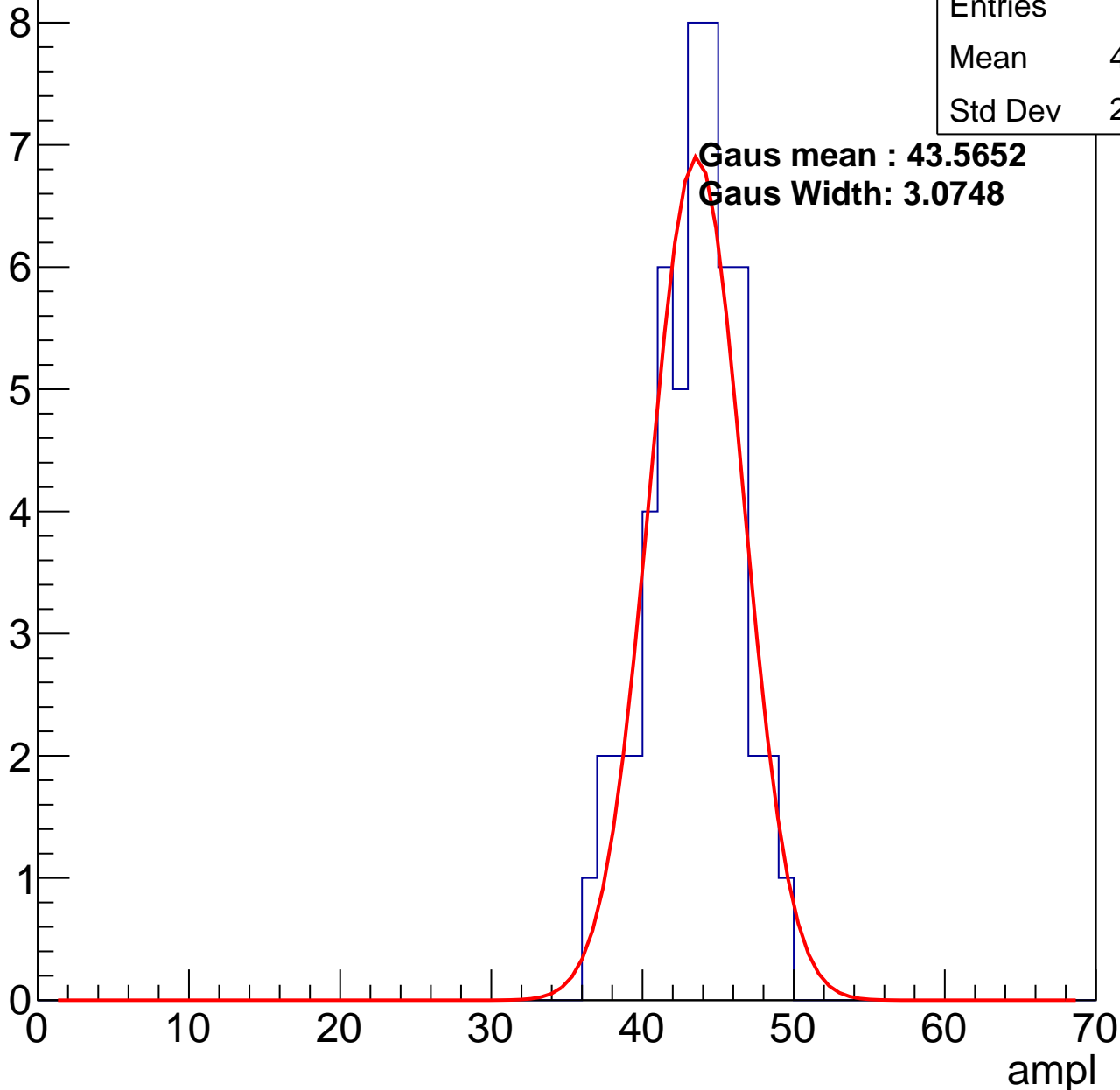
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	42.93
Std Dev	2.929

**Gaus mean : 43.5652**

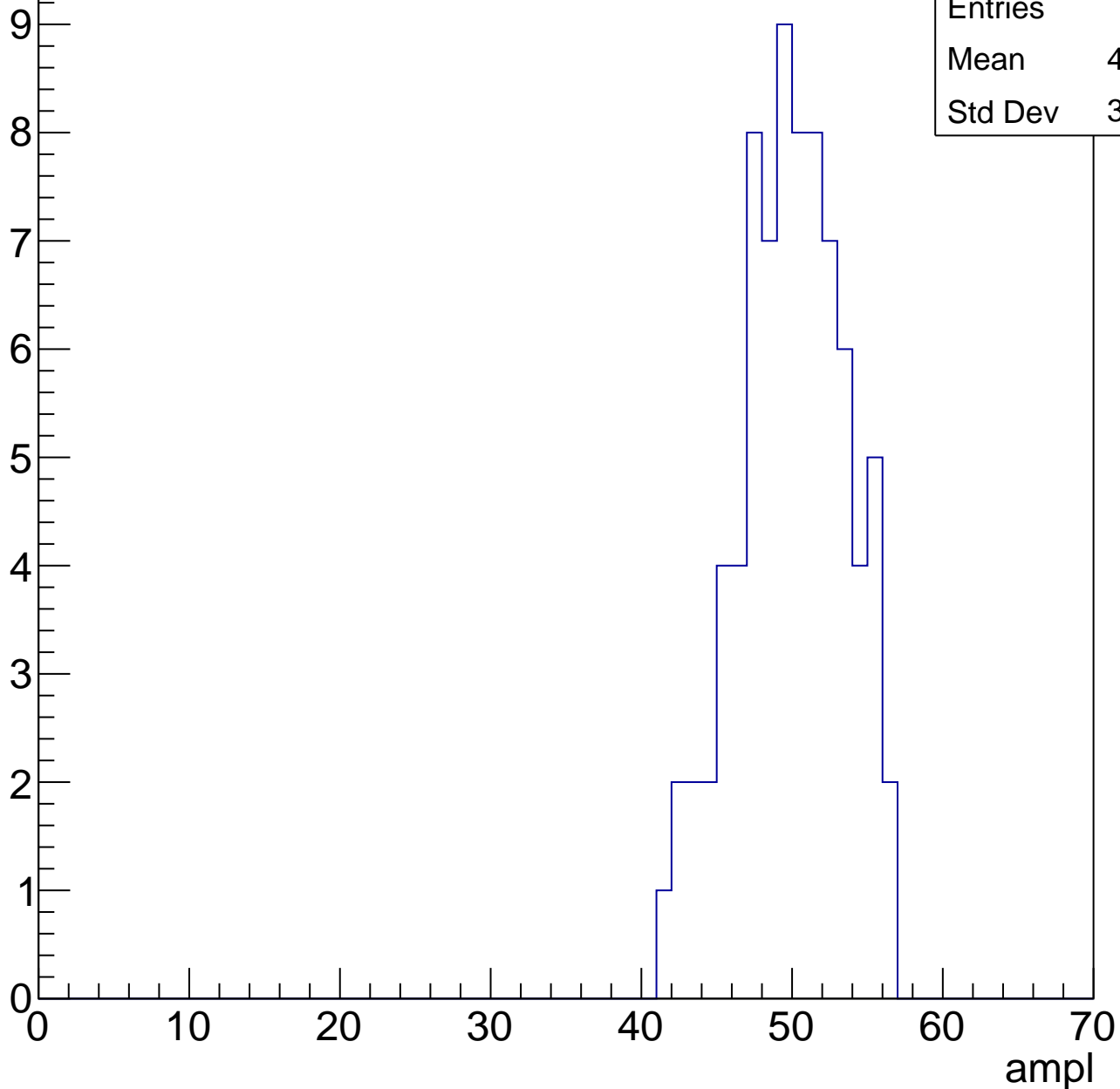
**Gaus Width: 3.0748**



# B0L000S, U26-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



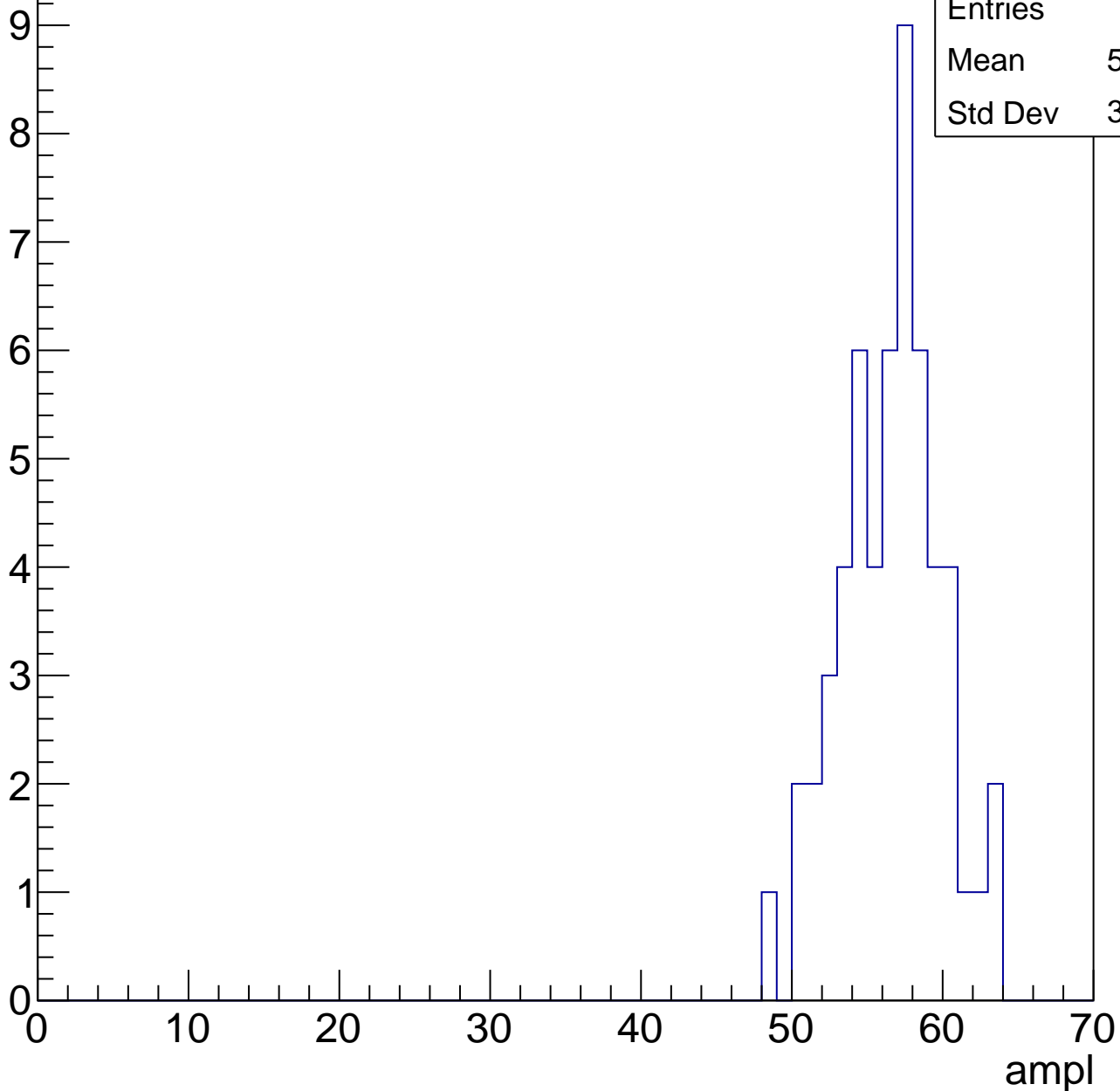
Entries	79
Mean	49.48
Std Dev	3.525

# B0L000S, U26-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.07
Std Dev	3.274

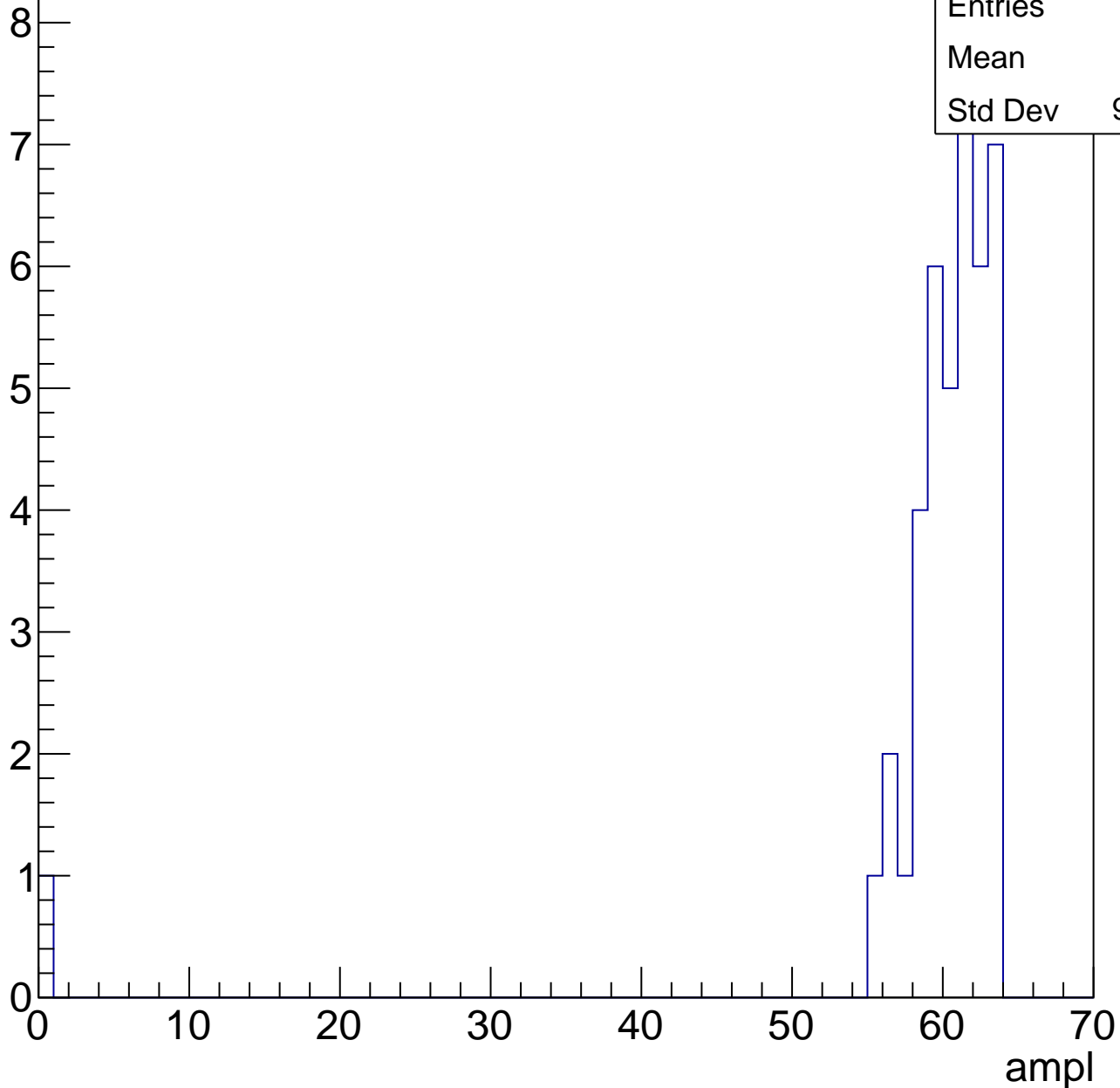


# B0L000S, U26-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.8
Std Dev	9.531



# B0L000S, U26-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B0L000S, U26-ch39, adc0

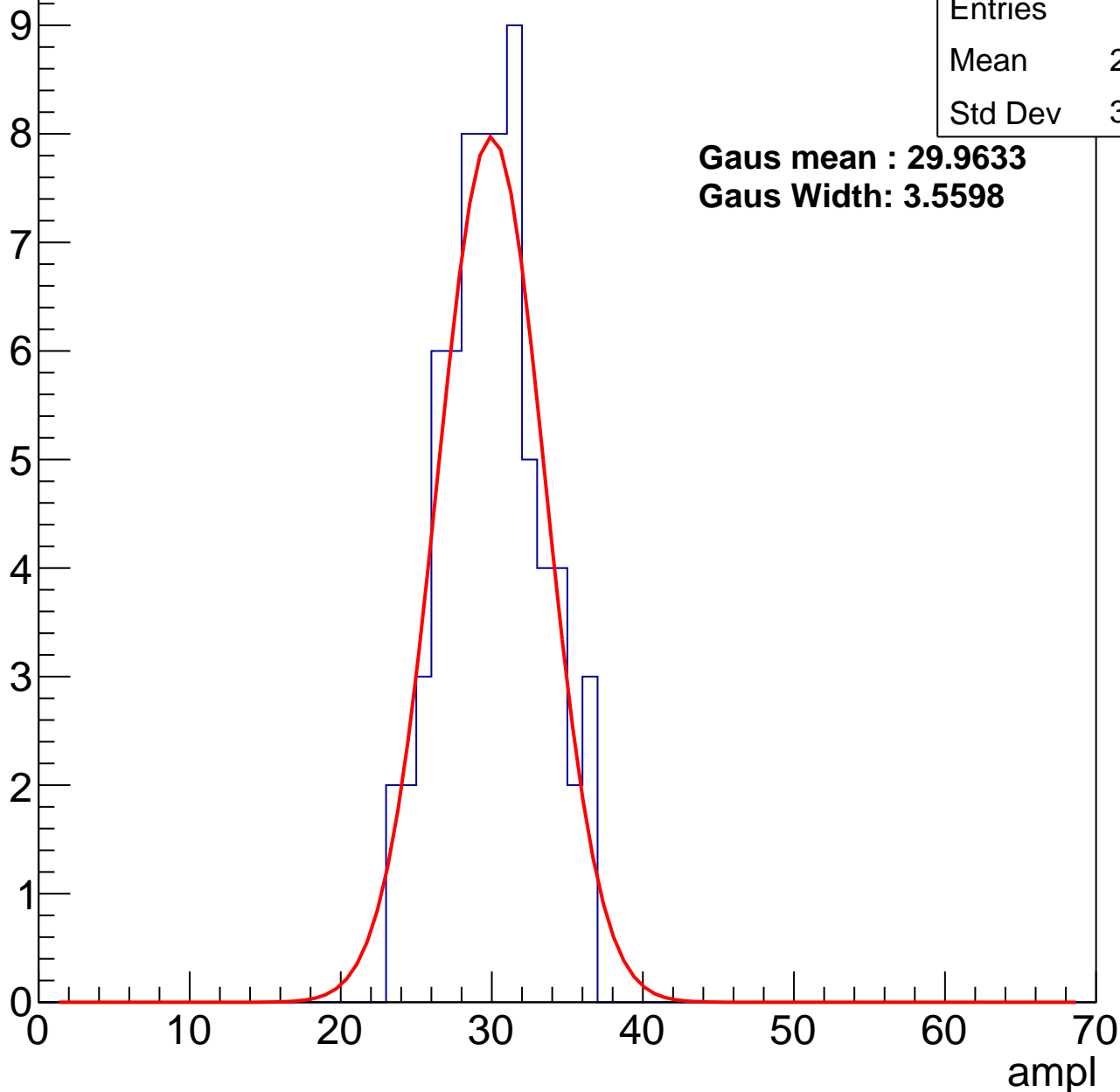
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.54
Std Dev	3.179

**Gaus mean : 29.9633**

**Gaus Width: 3.5598**



# B0L000S, U26-ch39, adc1

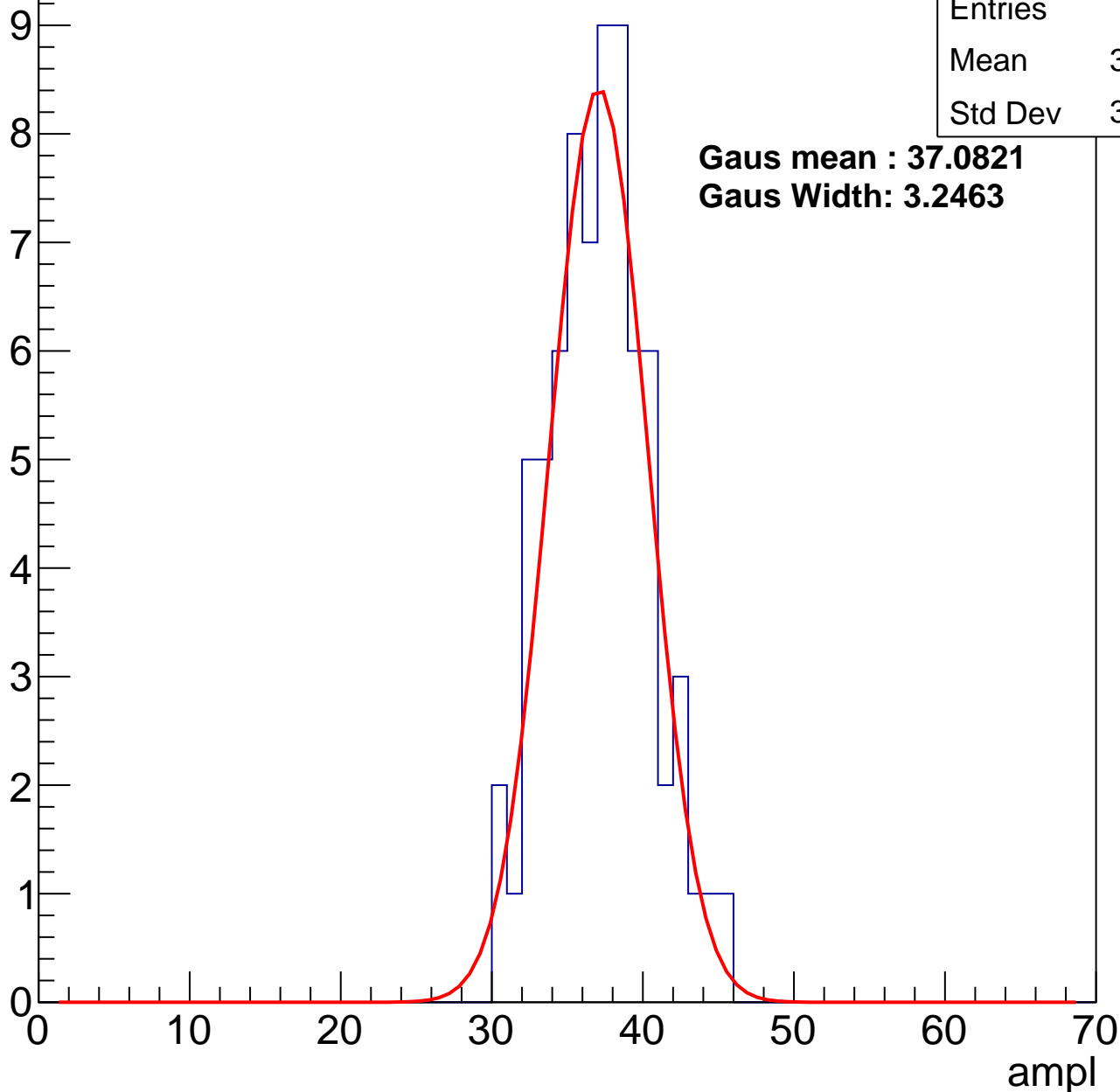
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.68
Std Dev	3.265

**Gaus mean : 37.0821**

**Gaus Width: 3.2463**



# B0L000S, U26-ch39, adc2

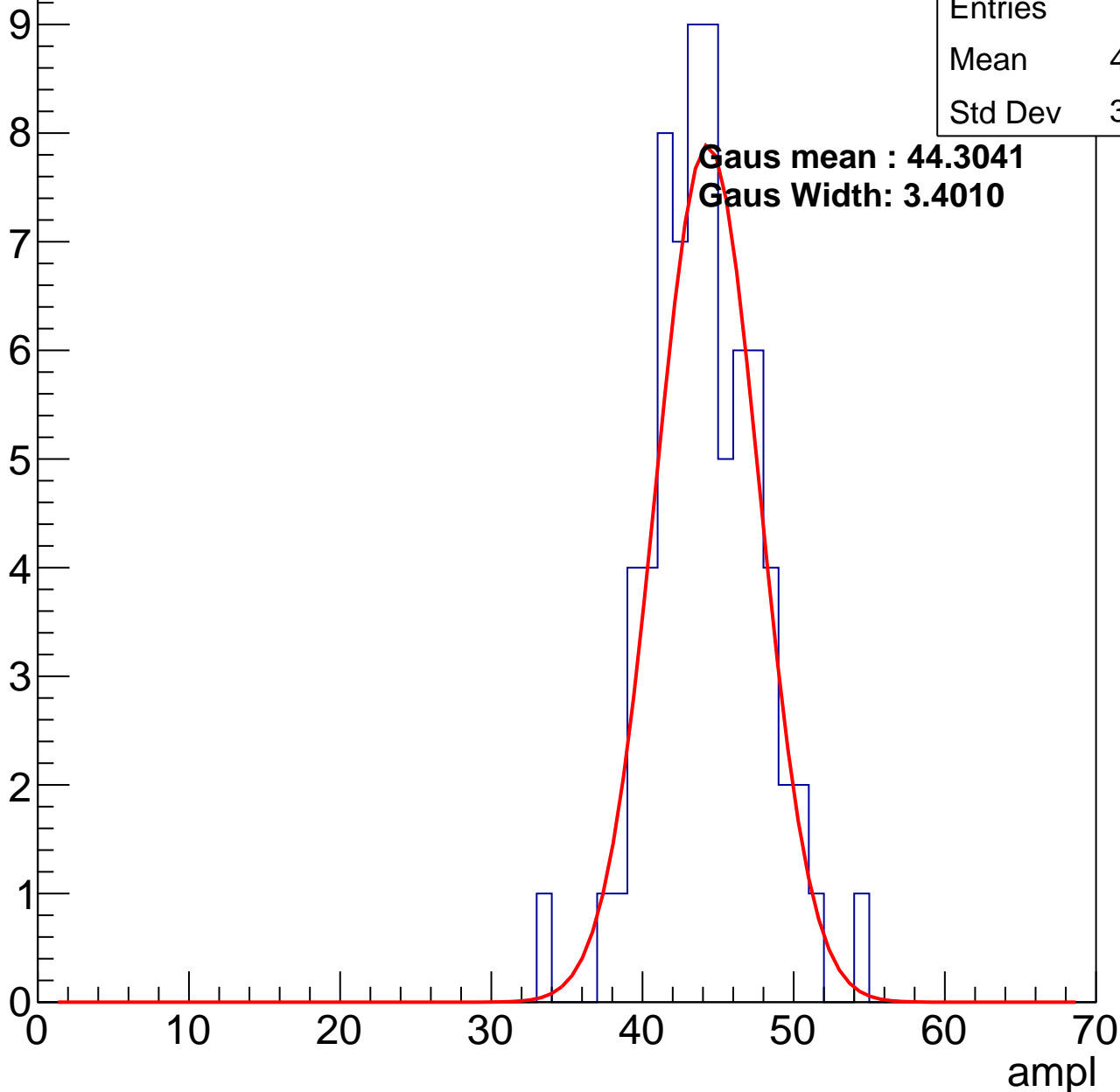
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	43.76
Std Dev	3.546

**Gaus mean : 44.3041**

**Gaus Width: 3.4010**

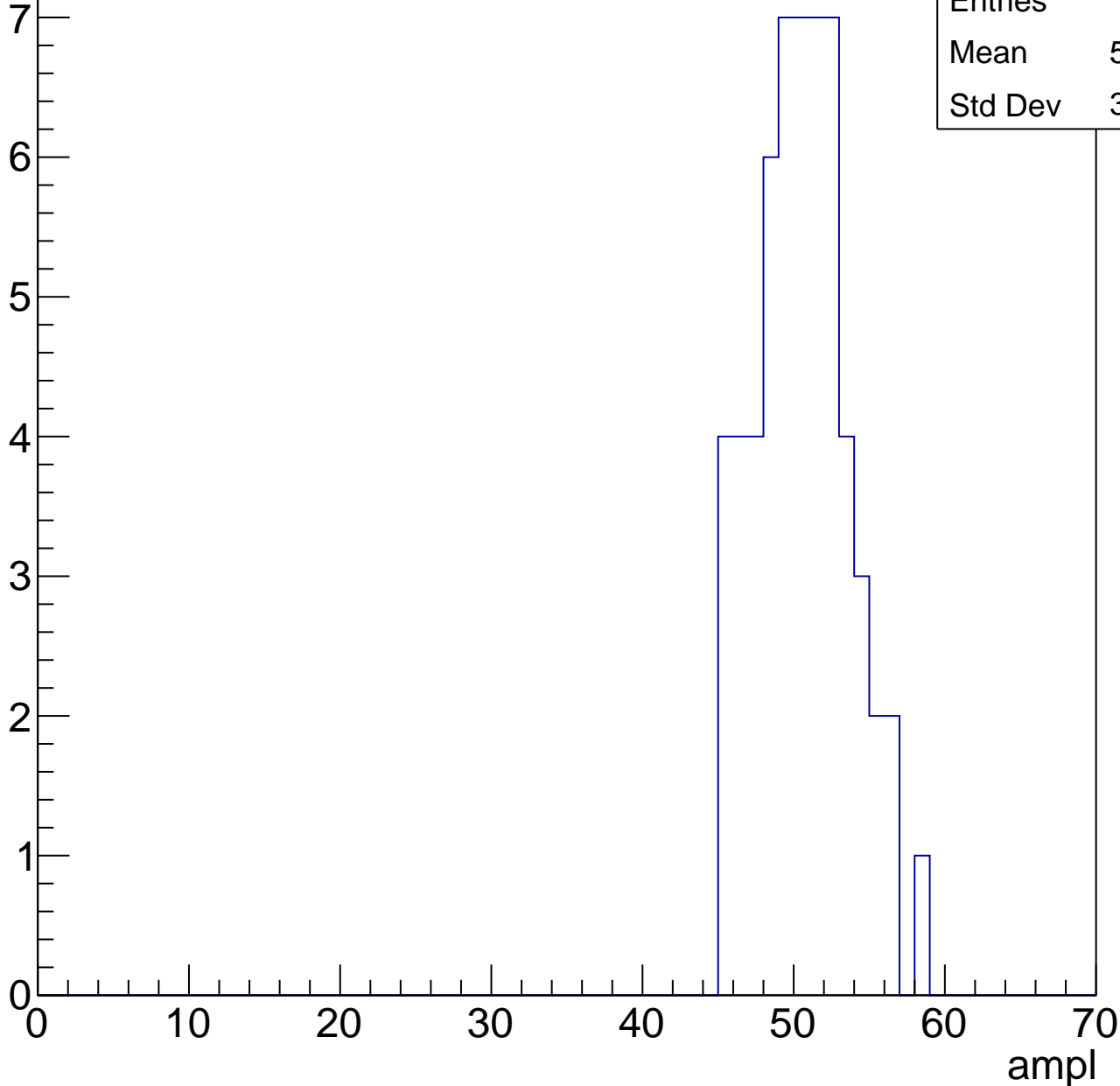


# B0L000S, U26-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	50.14
Std Dev	3.037



# B0L000S, U26-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	56.28
Std Dev	2.977

Entry

10

8

6

4

2

0

0

10

20

30

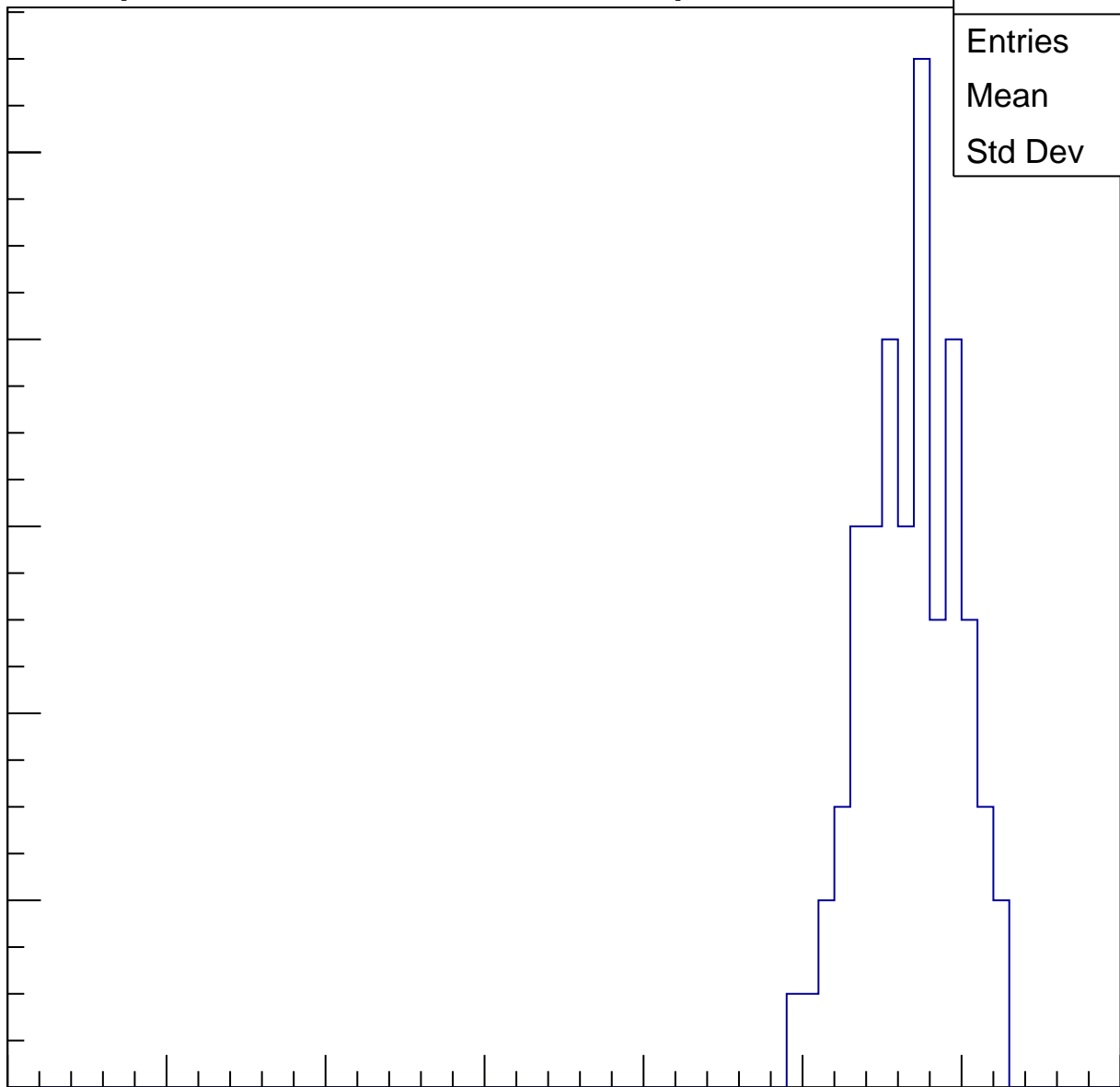
40

50

60

70

ampl

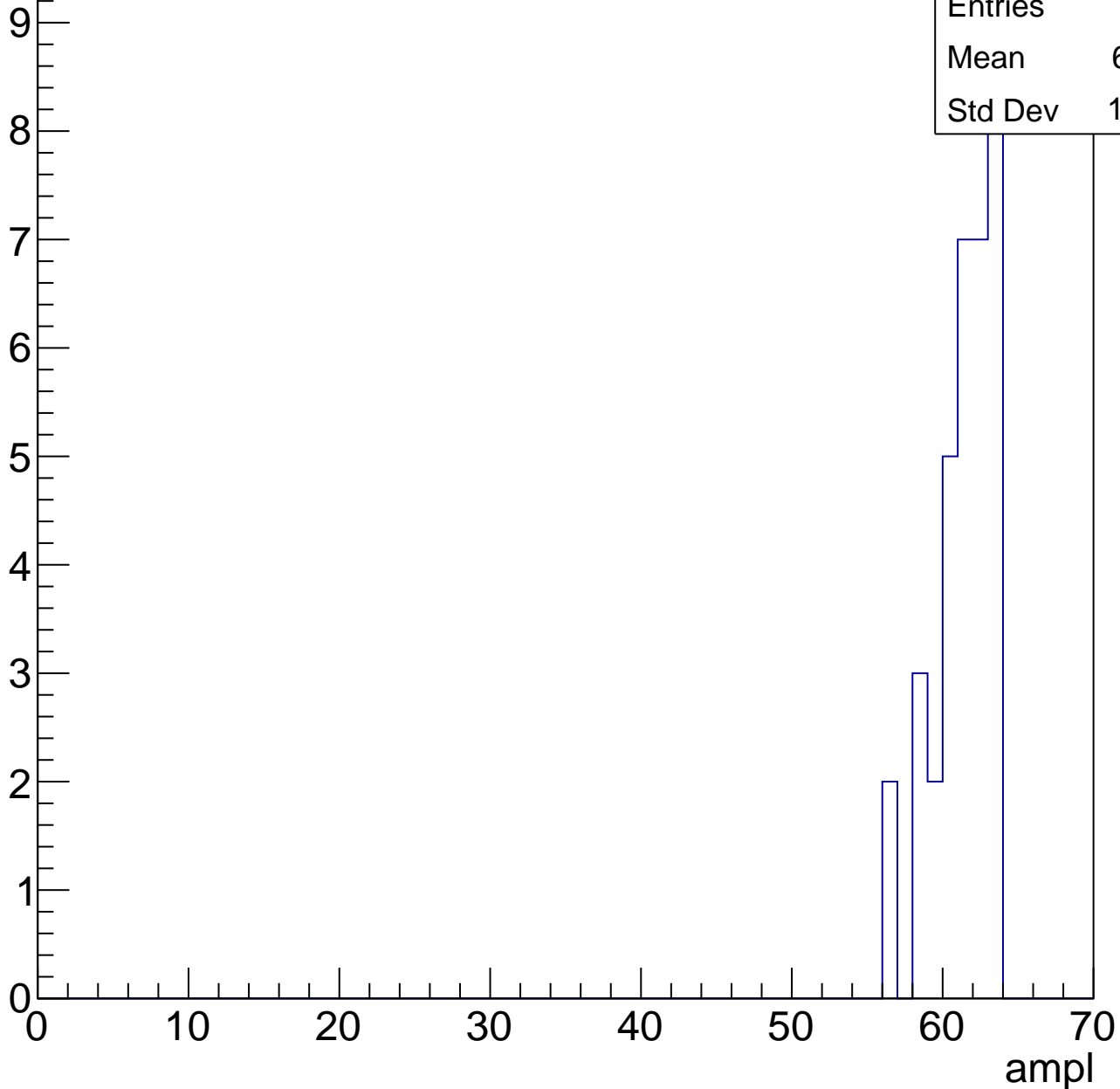


# B0L000S, U26-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

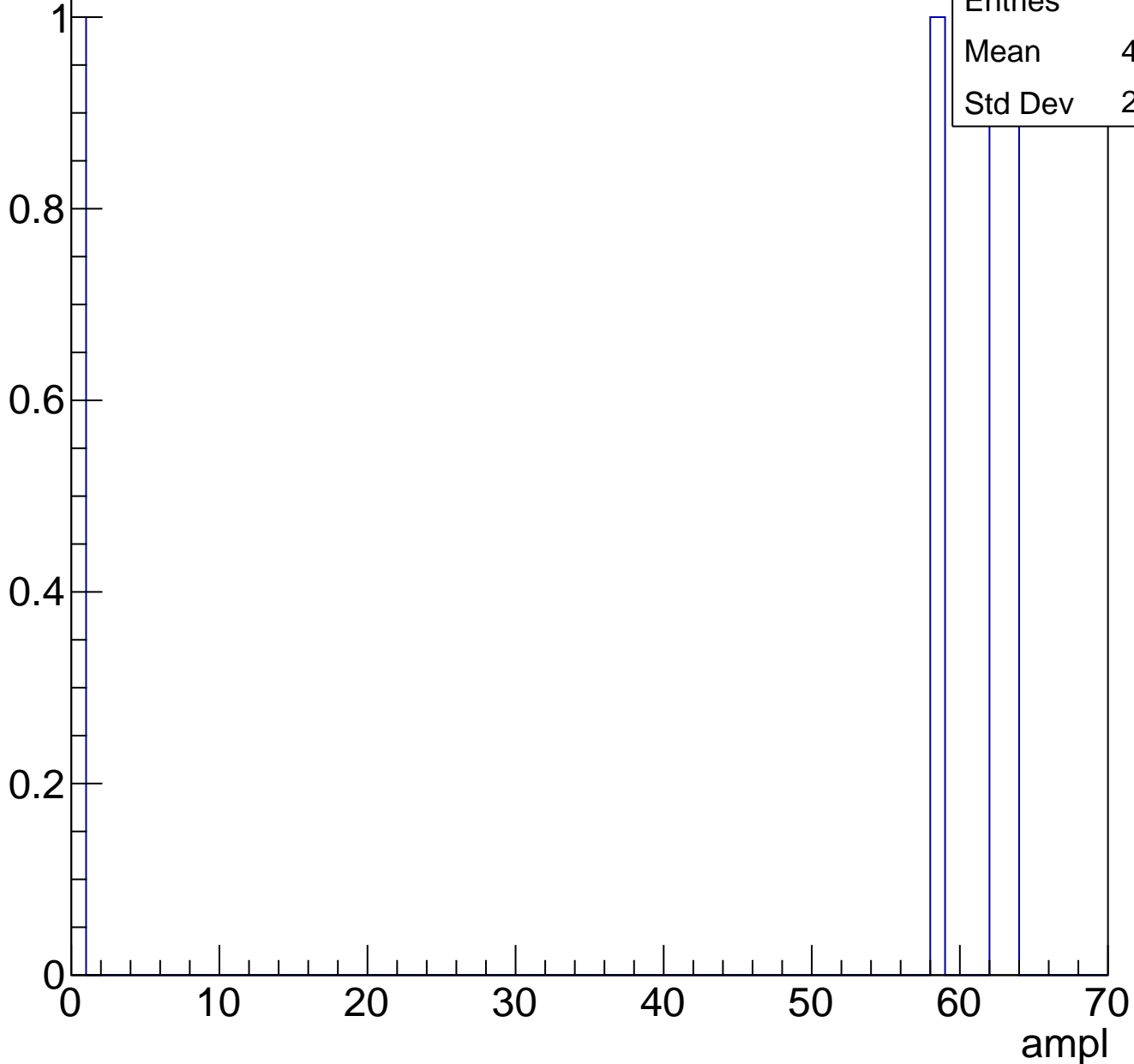
Entries	35
Mean	60.91
Std Dev	1.947



# B0L000S, U26-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B0L000S, U26-ch40, adc0

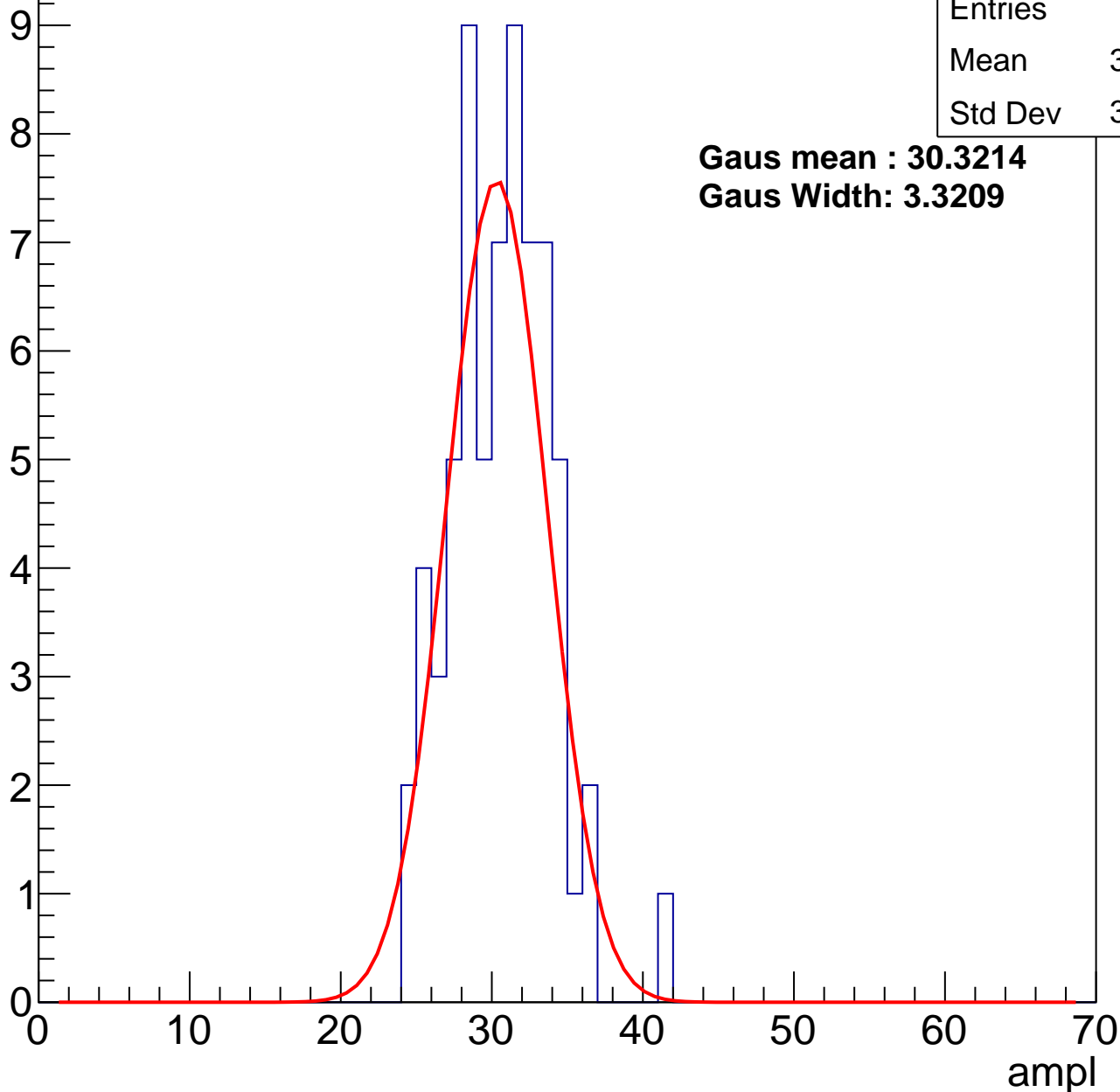
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.15
Std Dev	3.238

**Gaus mean : 30.3214**

**Gaus Width: 3.3209**



# B0L000S, U26-ch40, adc1

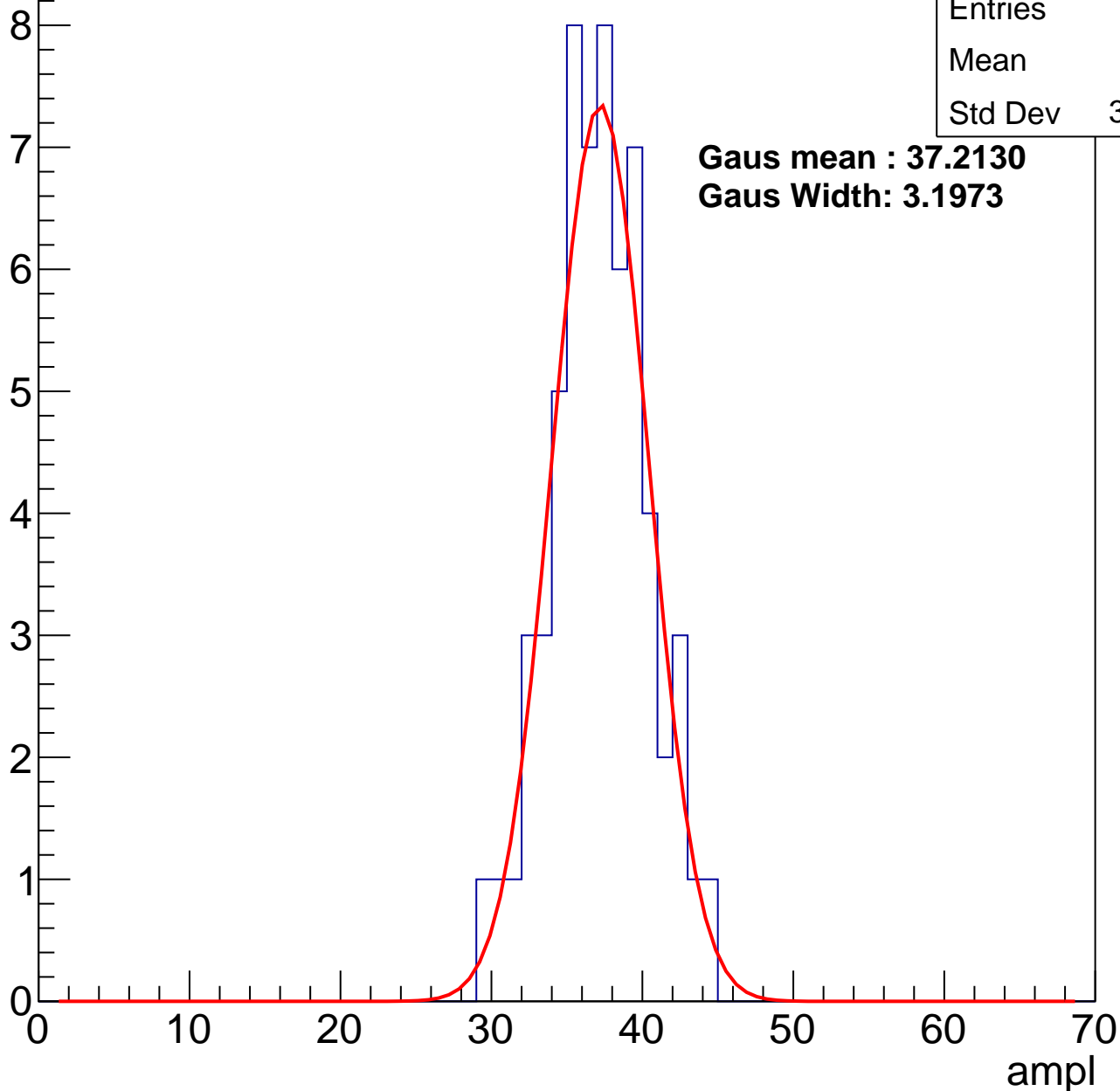
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.7
Std Dev	3.169

**Gaus mean : 37.2130**

**Gaus Width: 3.1973**



# B0L000S, U26-ch40, adc2

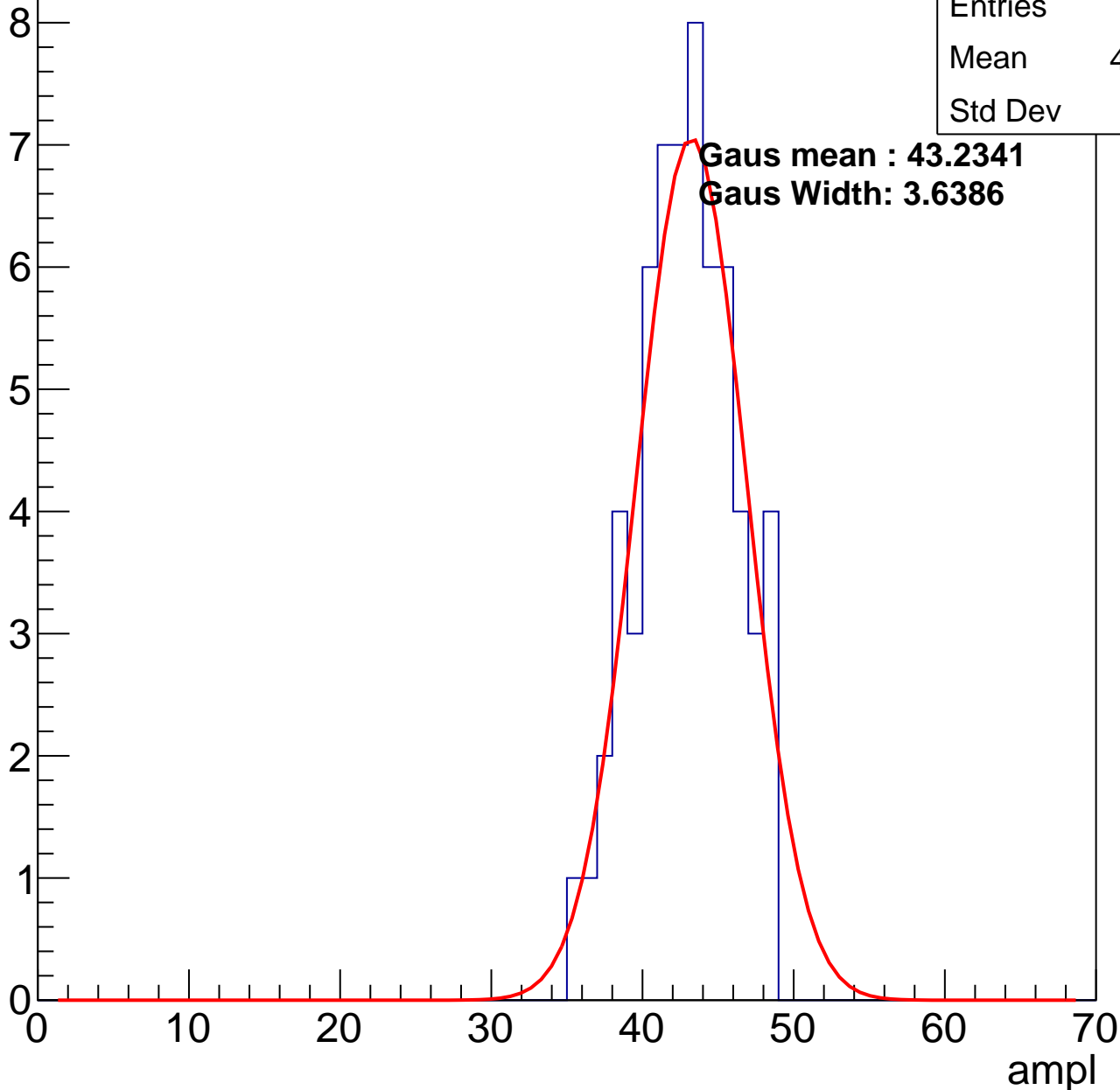
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	42.42
Std Dev	3.15

**Gaus mean : 43.2341**

**Gaus Width: 3.6386**



# B0L000S, U26-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

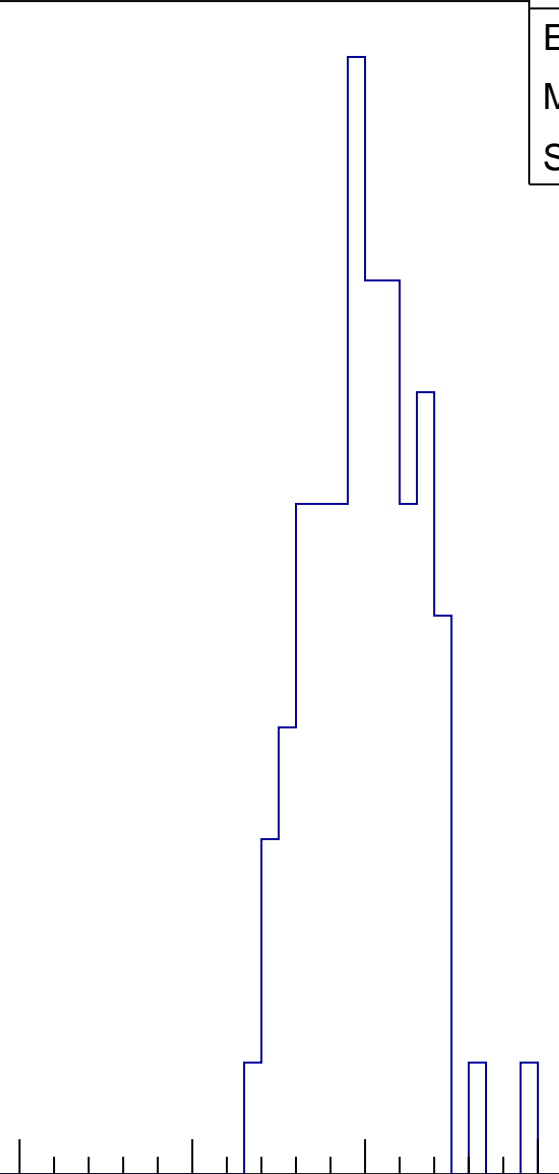
Entries	72
Mean	49.54
Std Dev	3.158

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

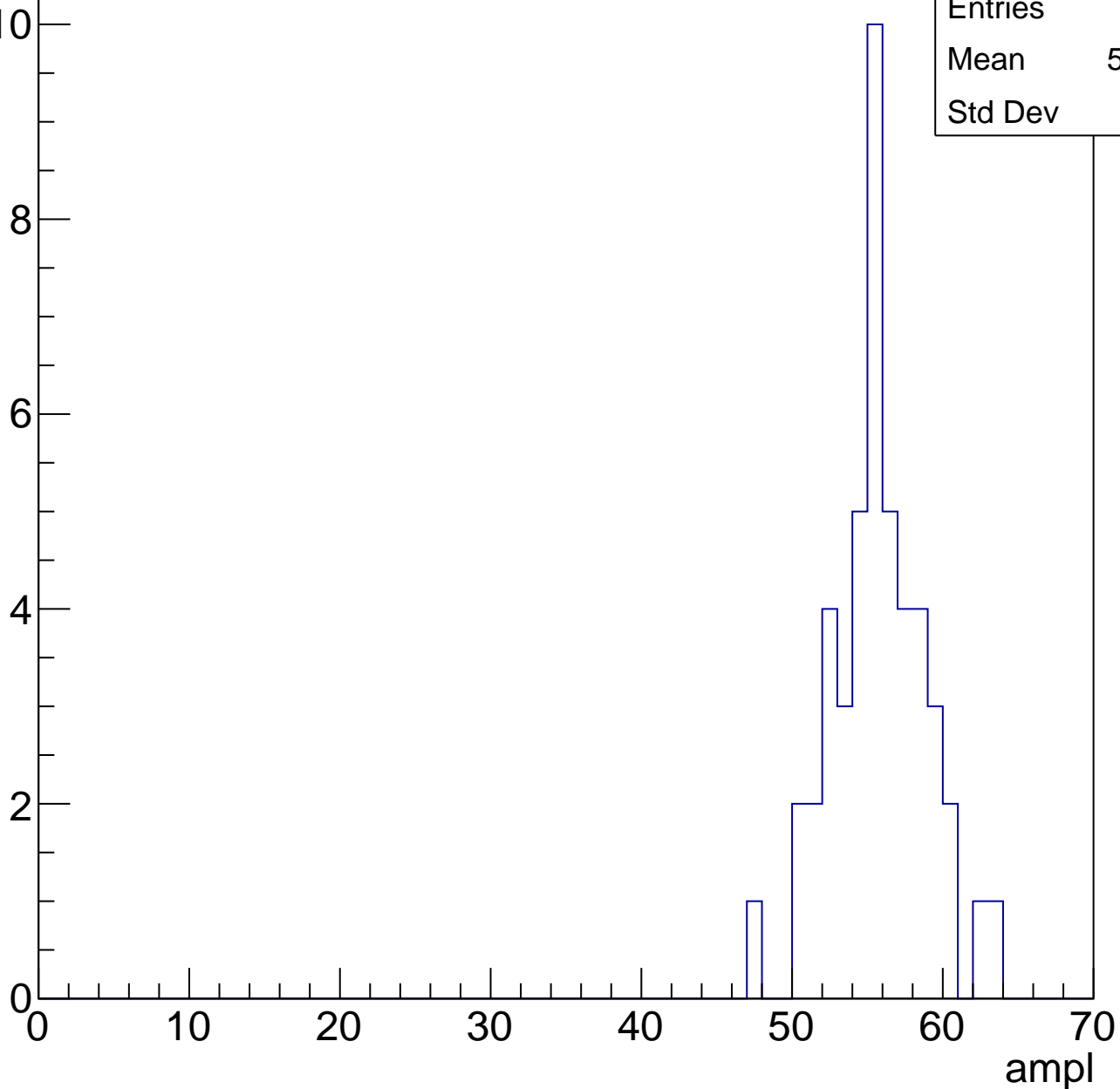


# B0L000S, U26-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

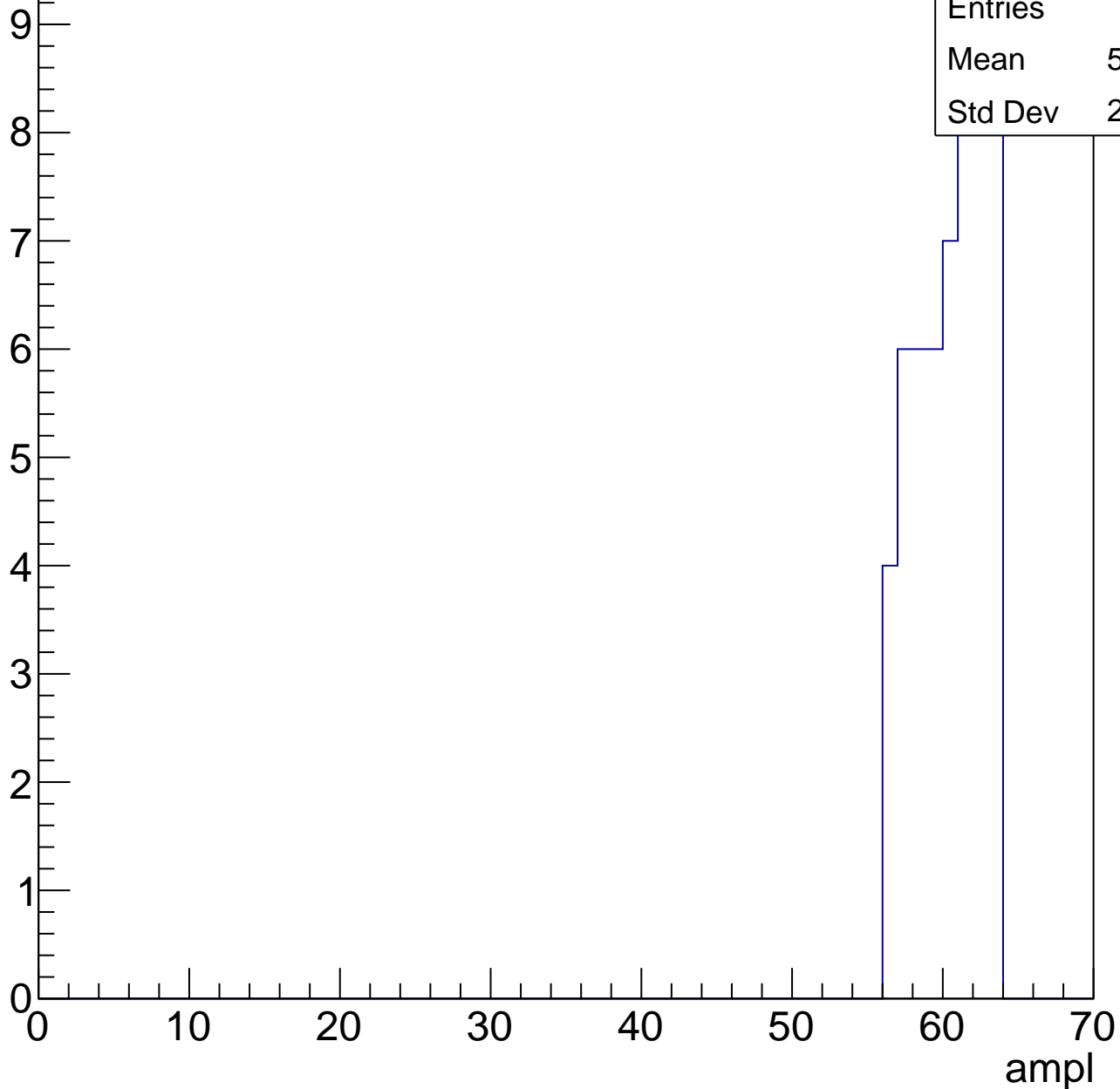
Entries	47
Mean	55.28
Std Dev	3.14



# B0L000S, U26-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

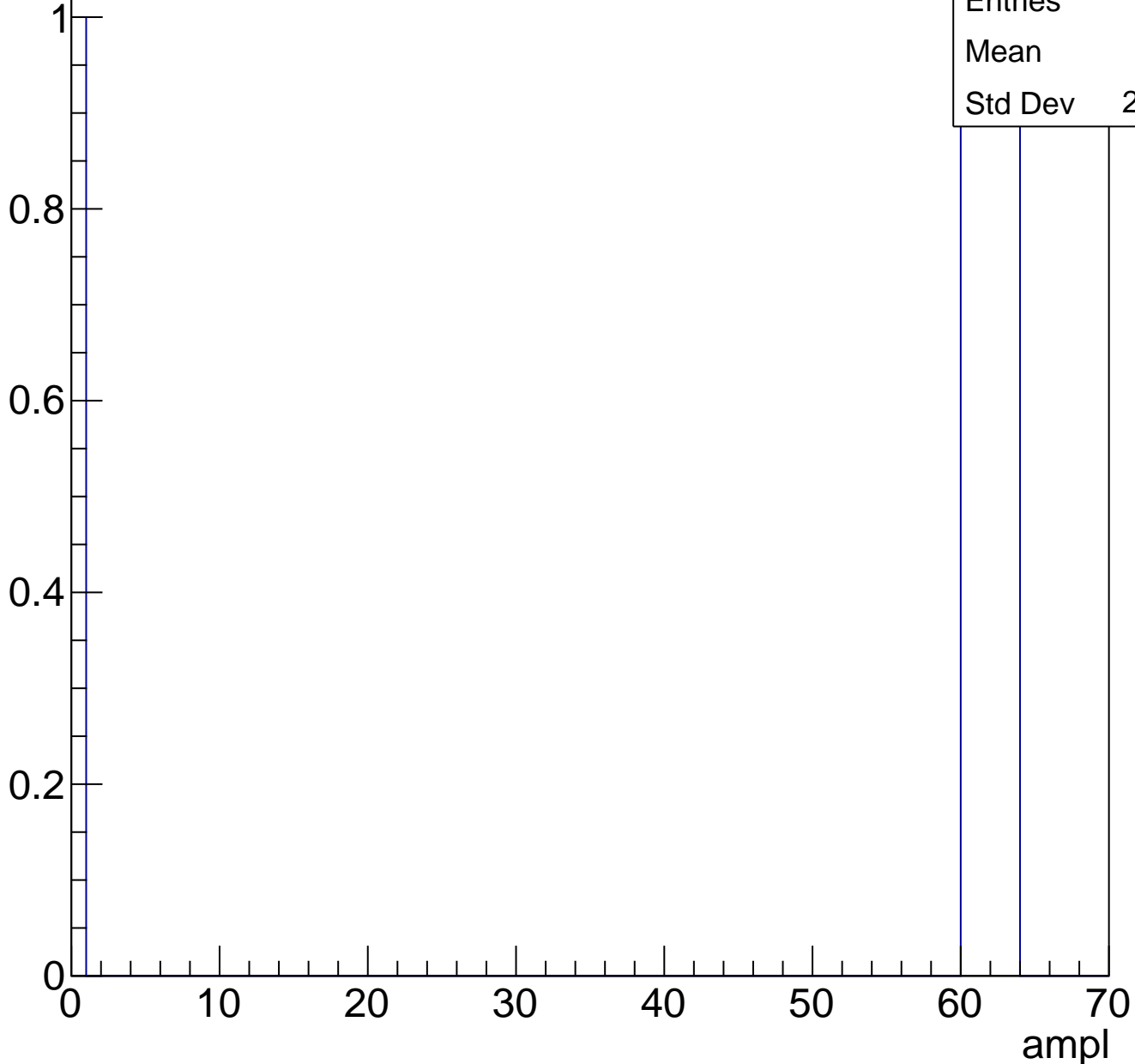
Entry



# B0L000S, U26-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

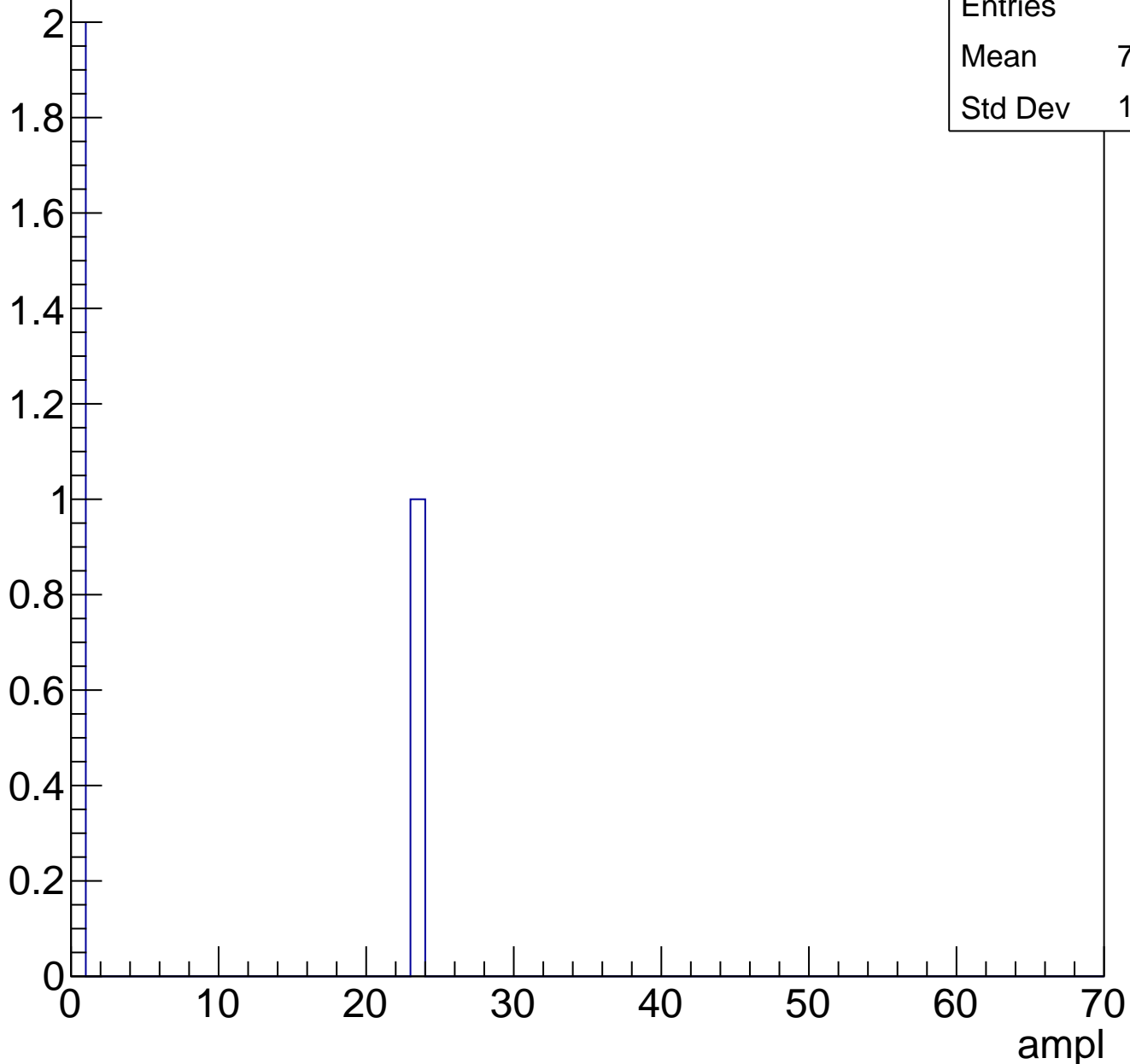




# B0L000S, U26-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U26-ch41, adc0

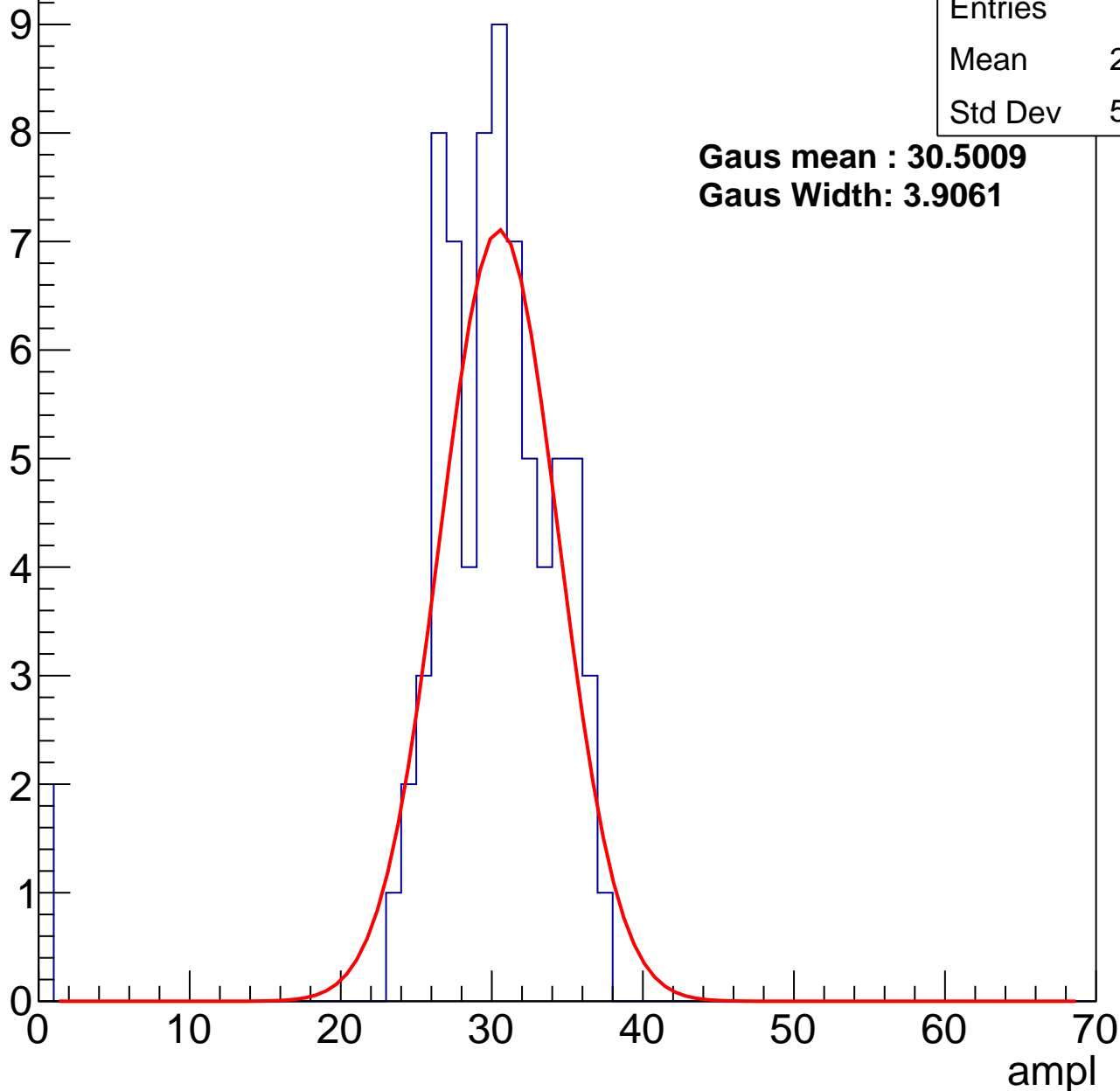
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	29.14
Std Dev	5.905

**Gaus mean : 30.5009**

**Gaus Width: 3.9061**



# B0L000S, U26-ch41, adc1

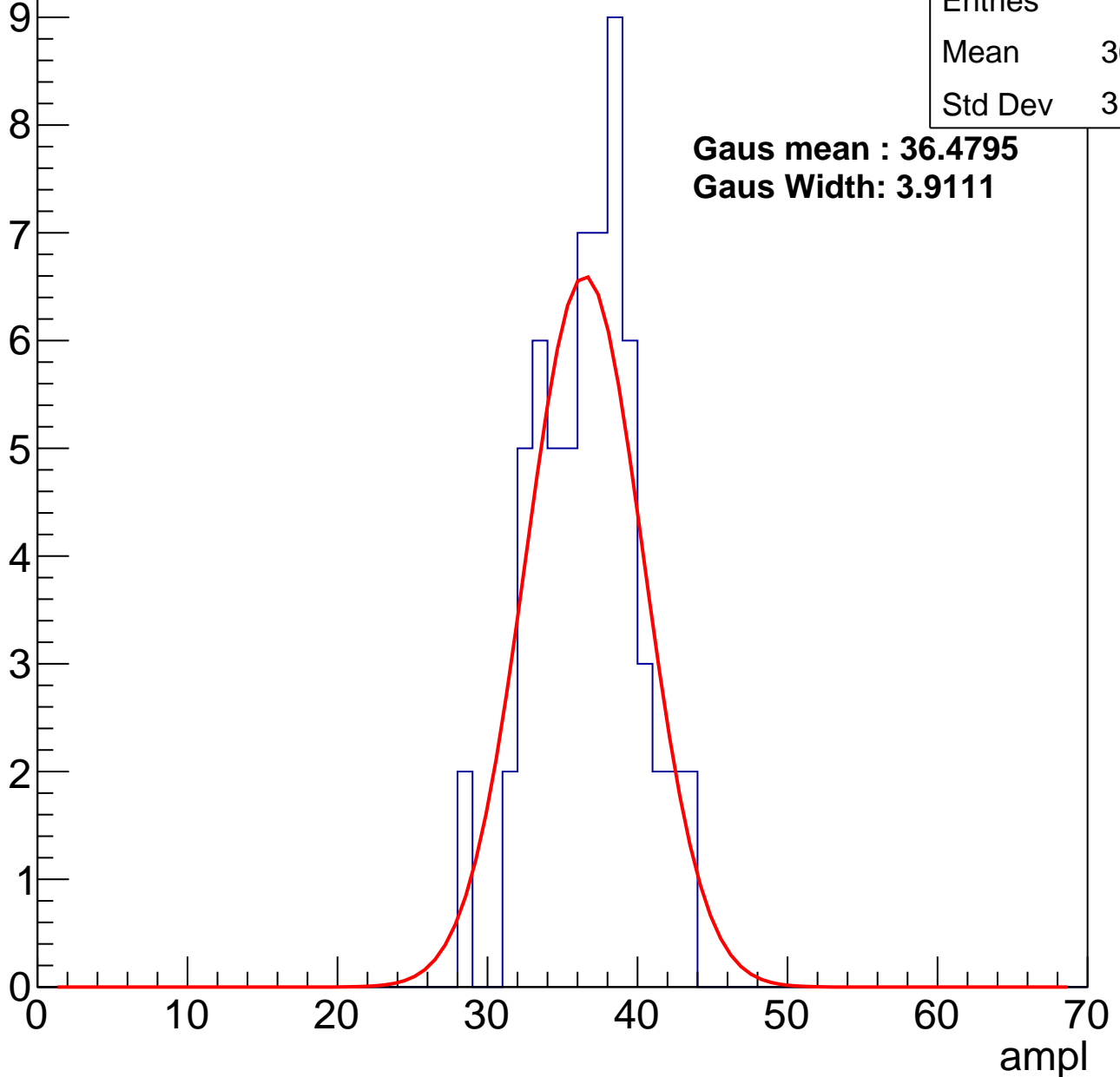
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.19
Std Dev	3.333

**Gaus mean : 36.4795**

**Gaus Width: 3.9111**



# B0L000S, U26-ch41, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	43.44
Std Dev	3.061

**Gaus mean : 44.4261**

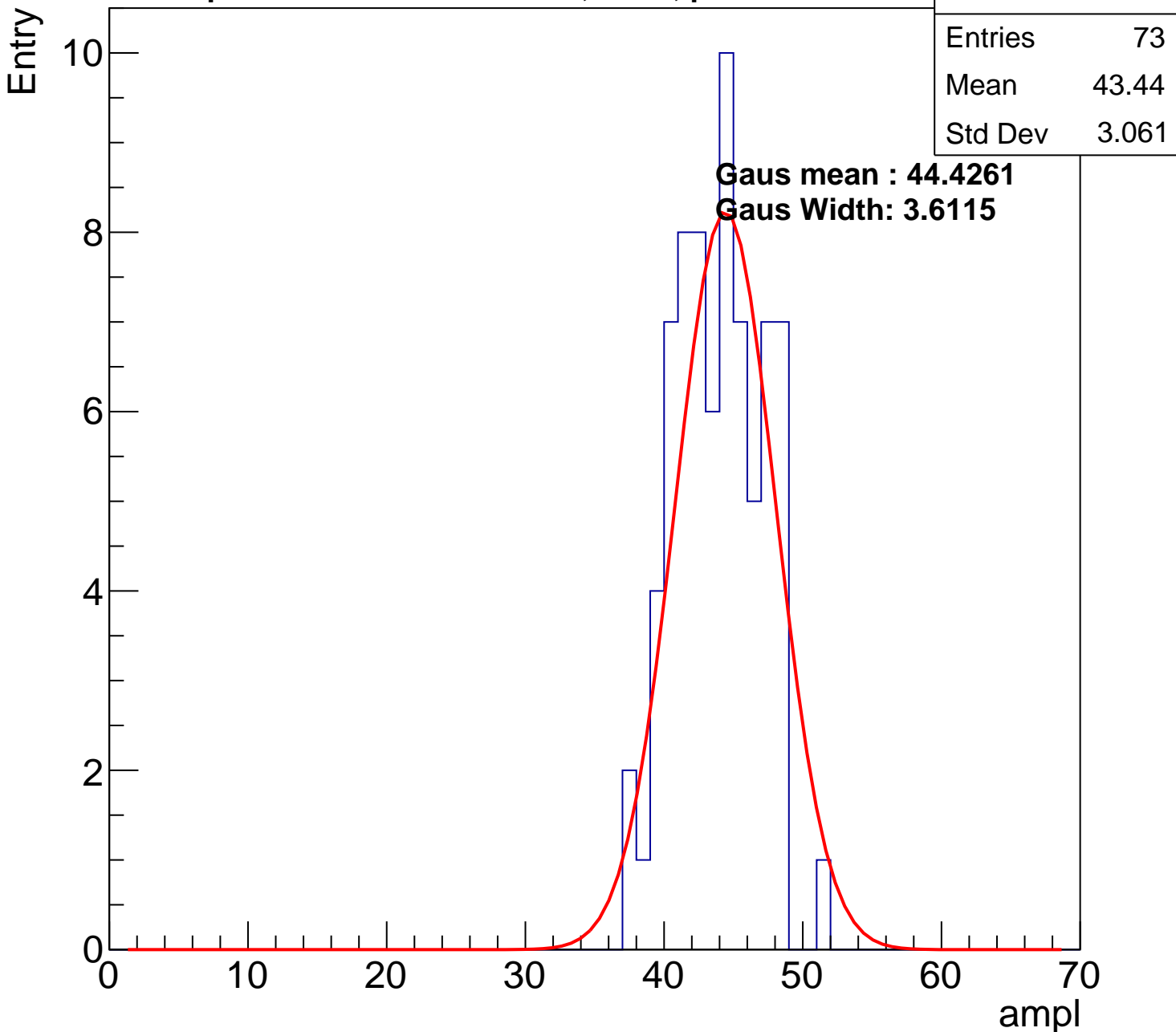
**Gaus Width: 3.6115**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

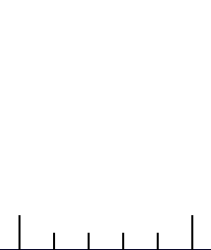
Entries	65
Mean	50.32
Std Dev	3.168

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

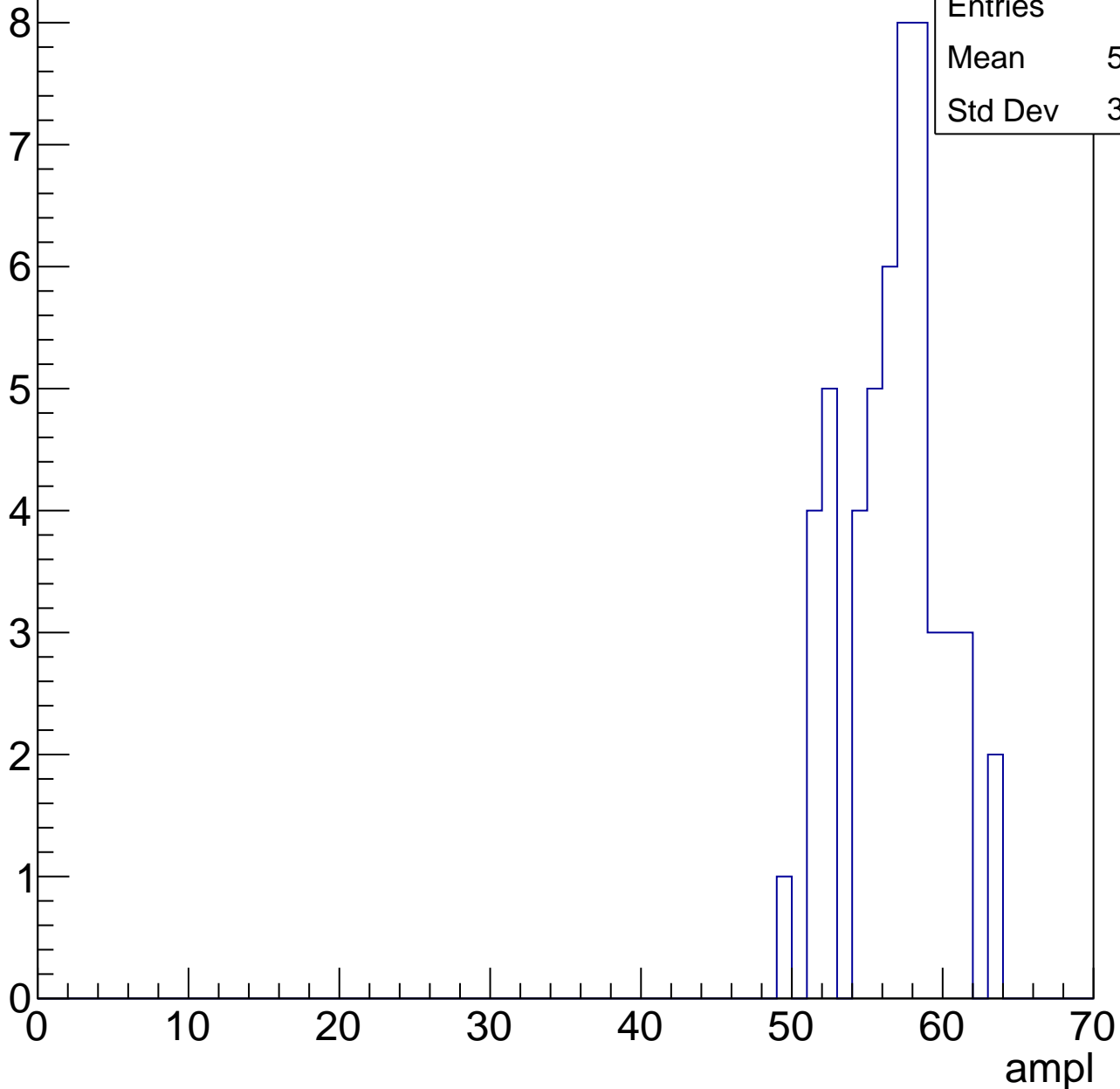


# B0L000S, U26-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	56.27
Std Dev	3.205

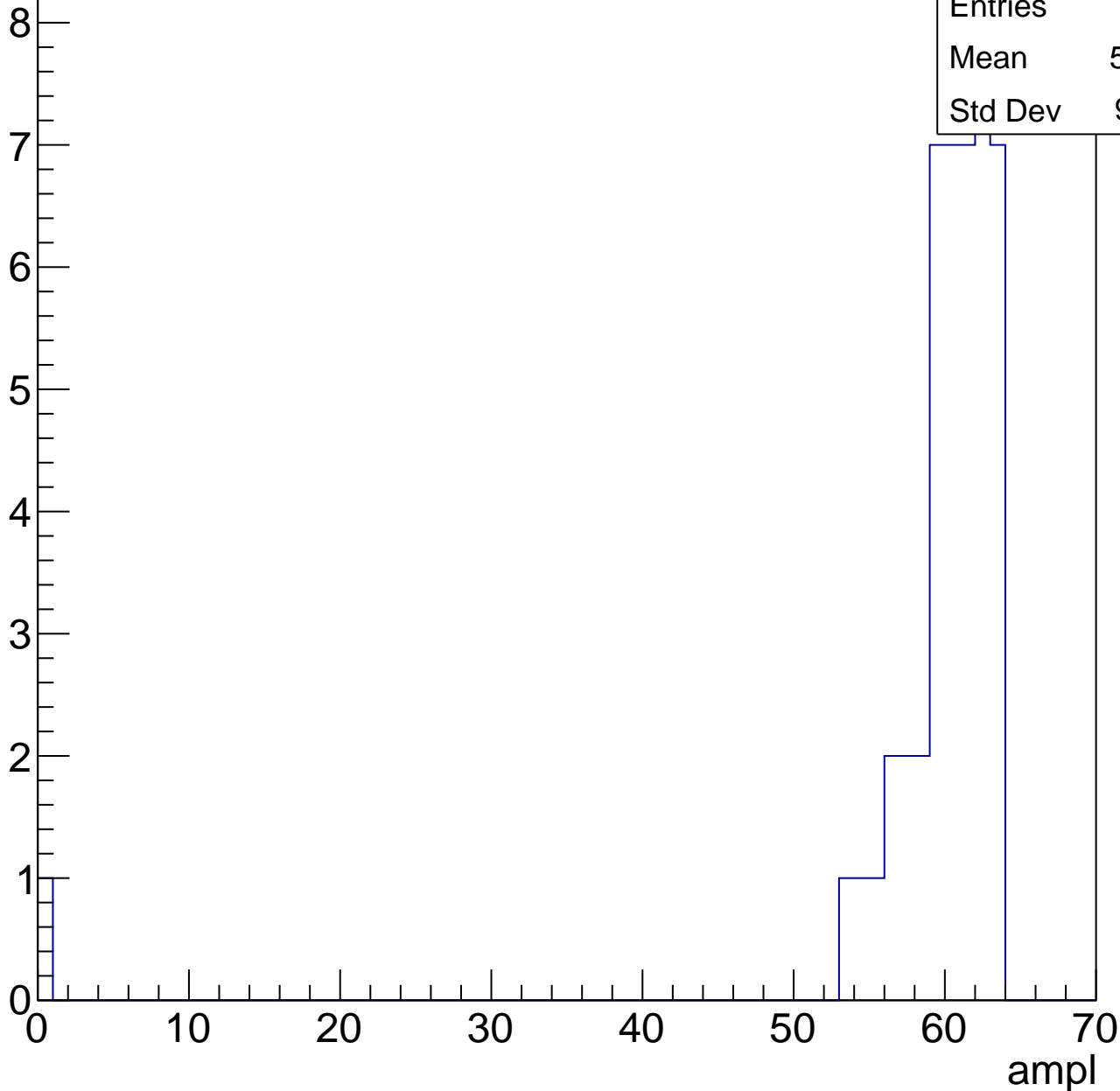


# B0L000S, U26-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.72
Std Dev	9.091



# B0L000S, U26-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch42, adc0

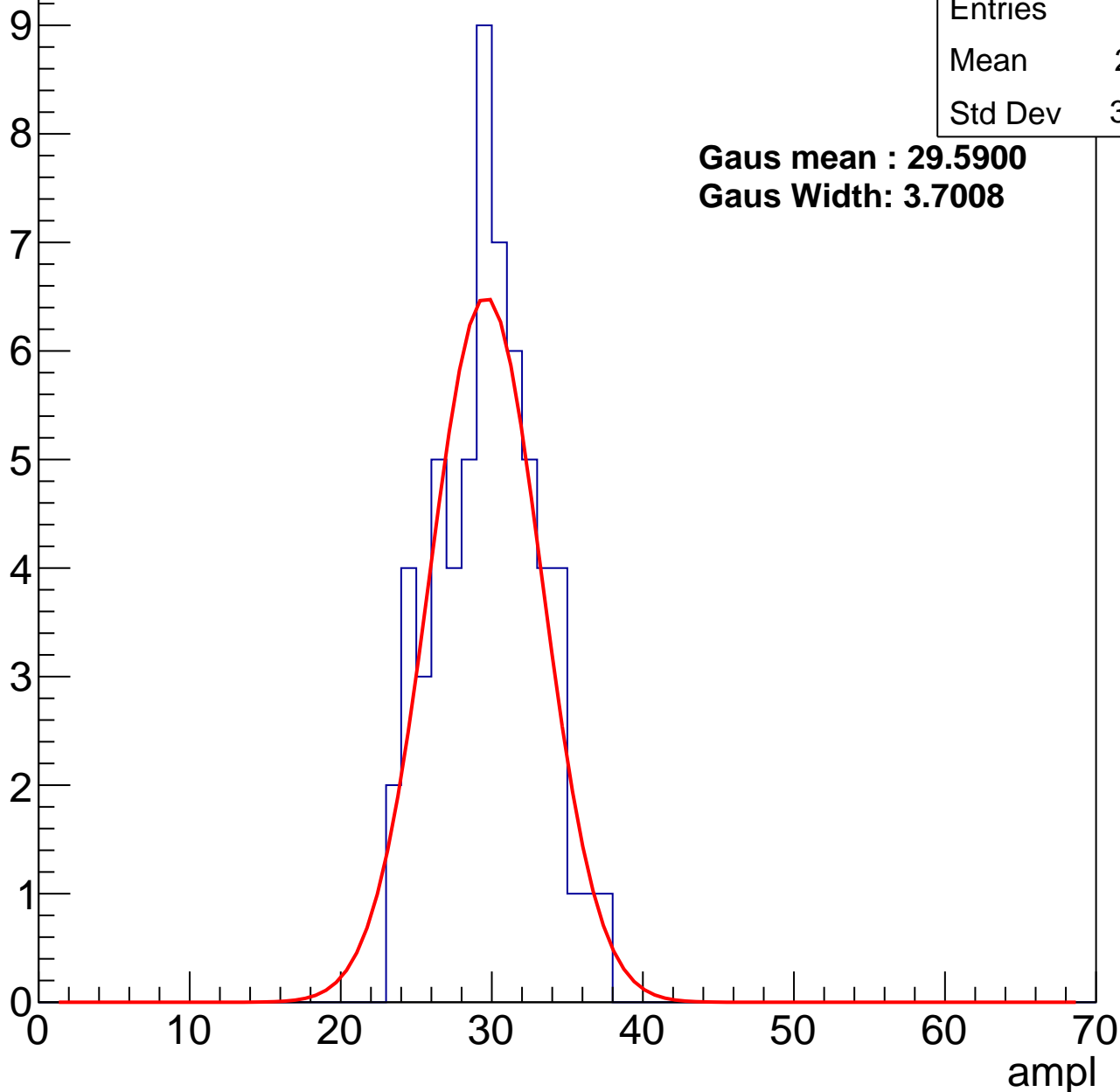
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.31
Std Dev	3.312

**Gaus mean : 29.5900**

**Gaus Width: 3.7008**



# B0L000S, U26-ch42, adc1

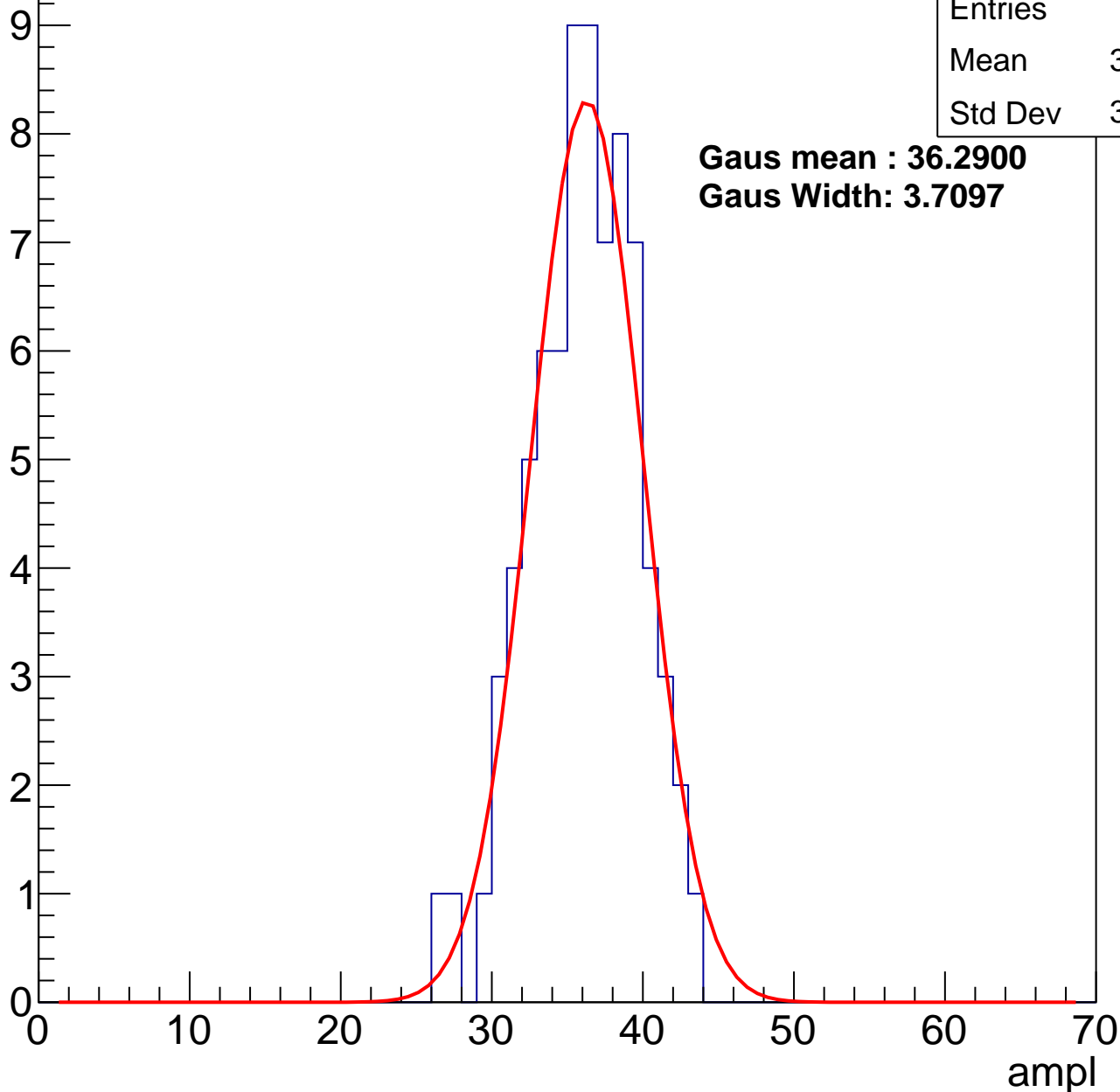
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	35.62
Std Dev	3.524

**Gaus mean : 36.2900**

**Gaus Width: 3.7097**

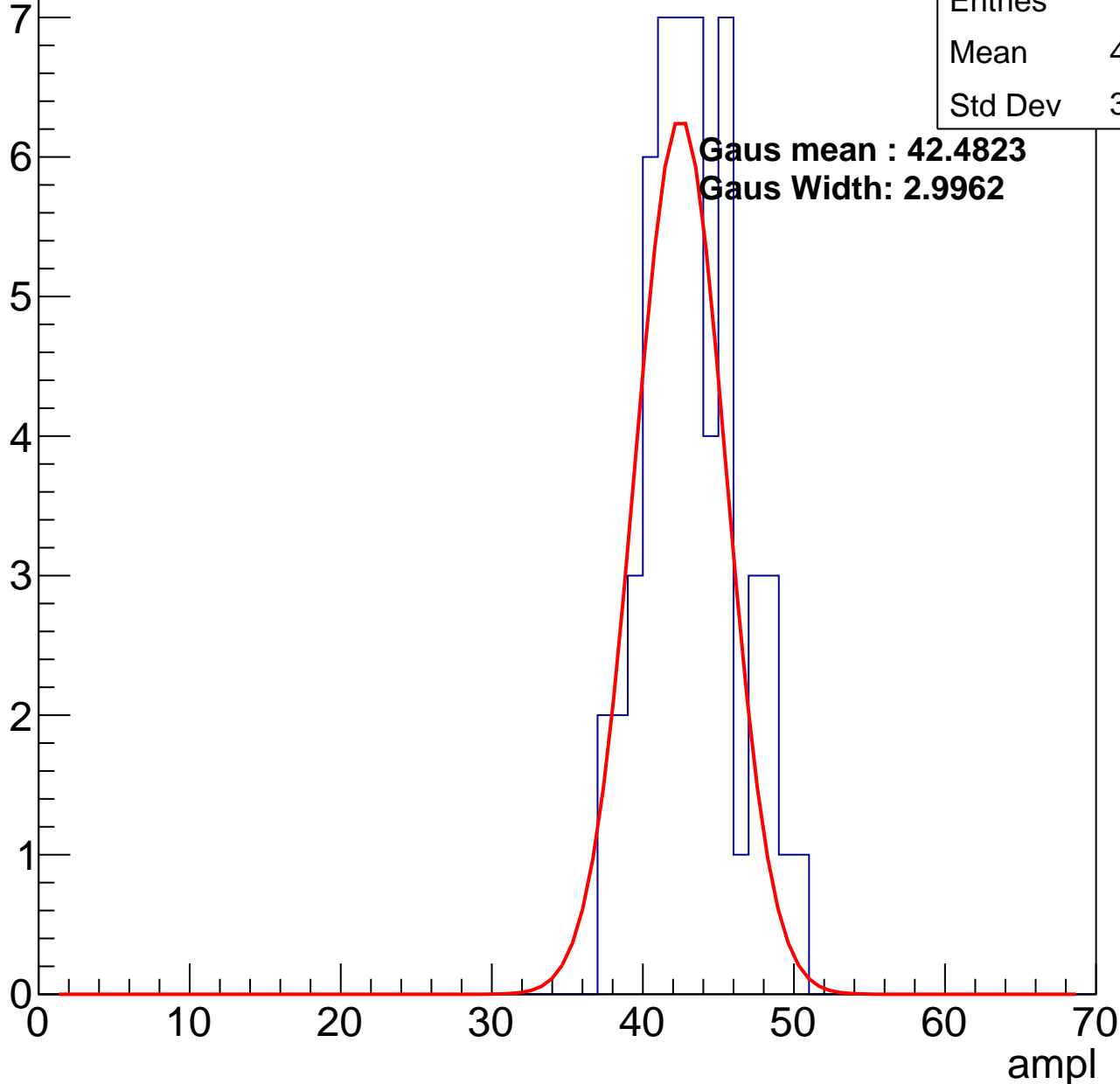


# B0L000S, U26-ch42, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	42.78
Std Dev	3.059

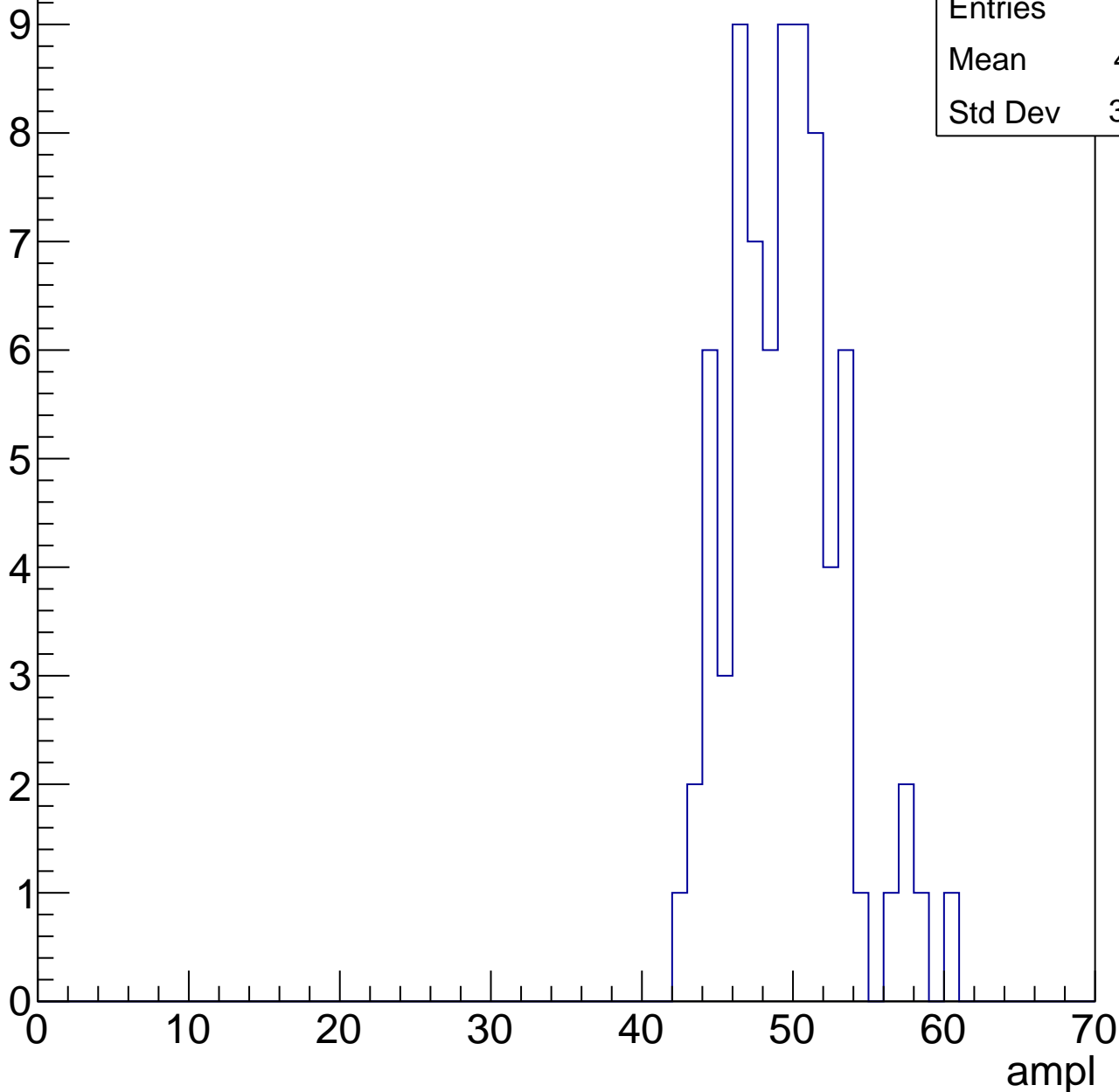


# B0L000S, U26-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	49.01
Std Dev	3.647

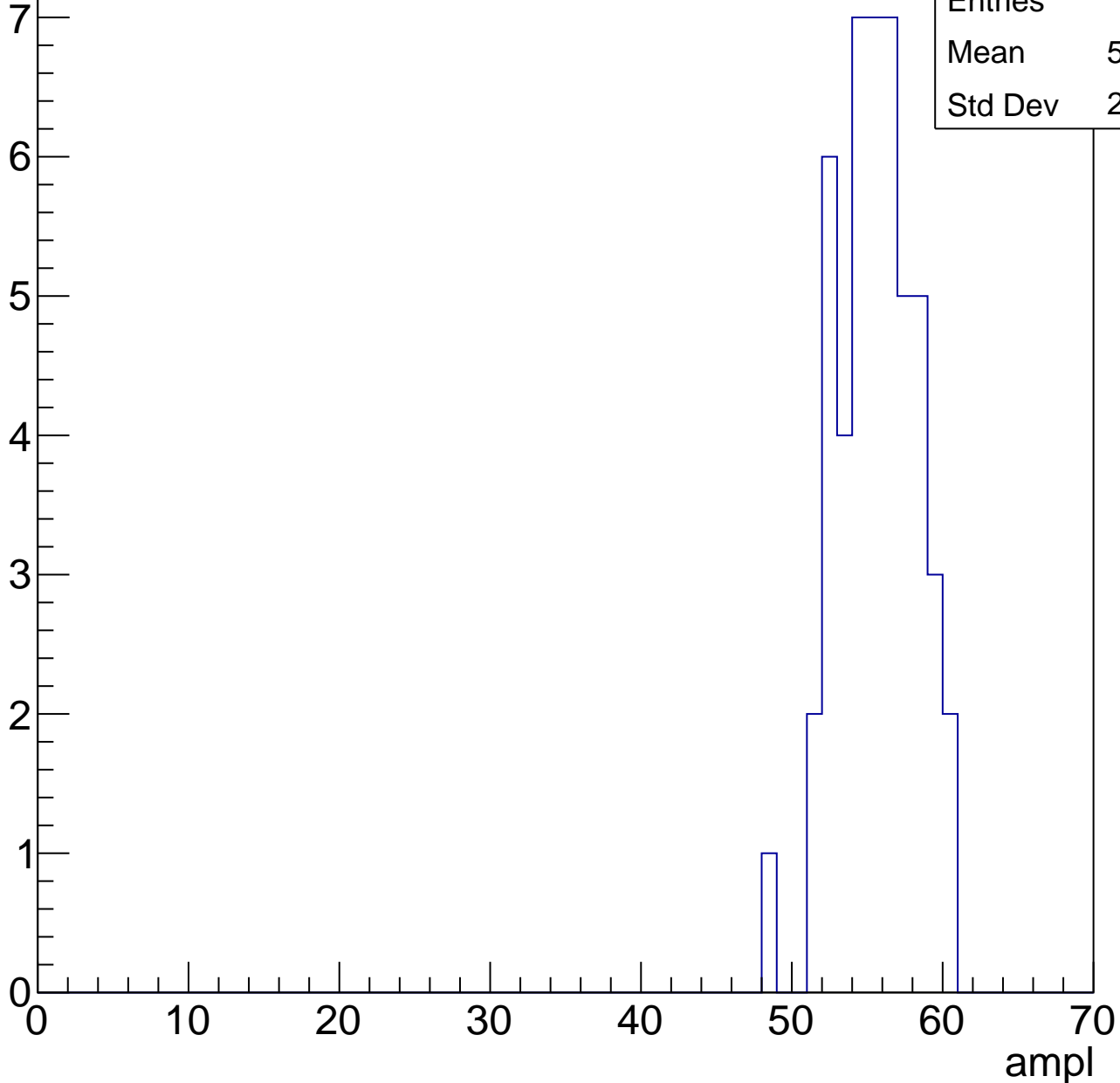


# B0L000S, U26-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.12
Std Dev	2.584



# B0L000S, U26-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	52
Mean	58.52
Std Dev	8.556

Entry

10

8

6

4

2

0

0

10

20

30

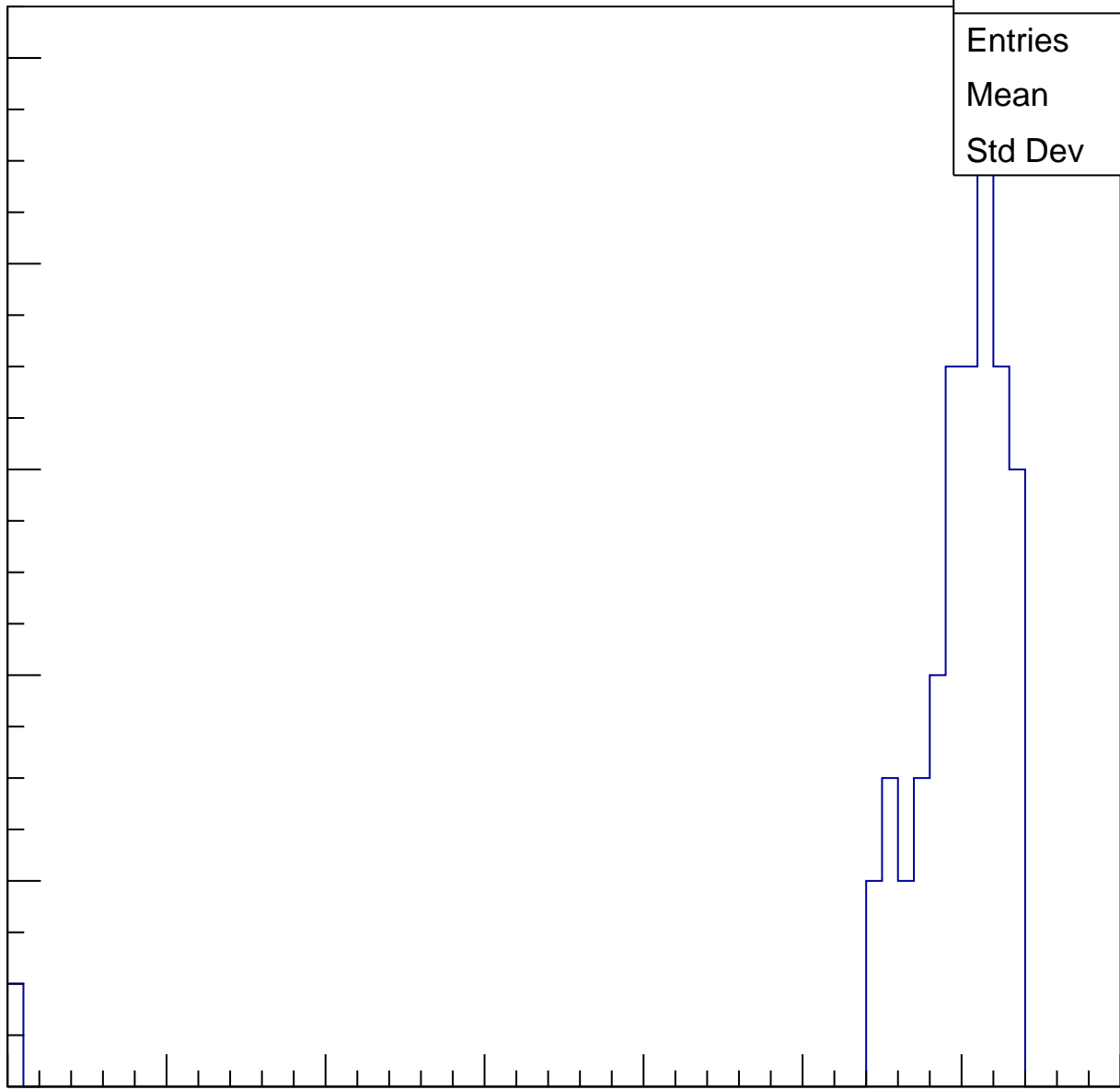
40

50

60

70

ampl



# B0L000S, U26-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch43, adc0

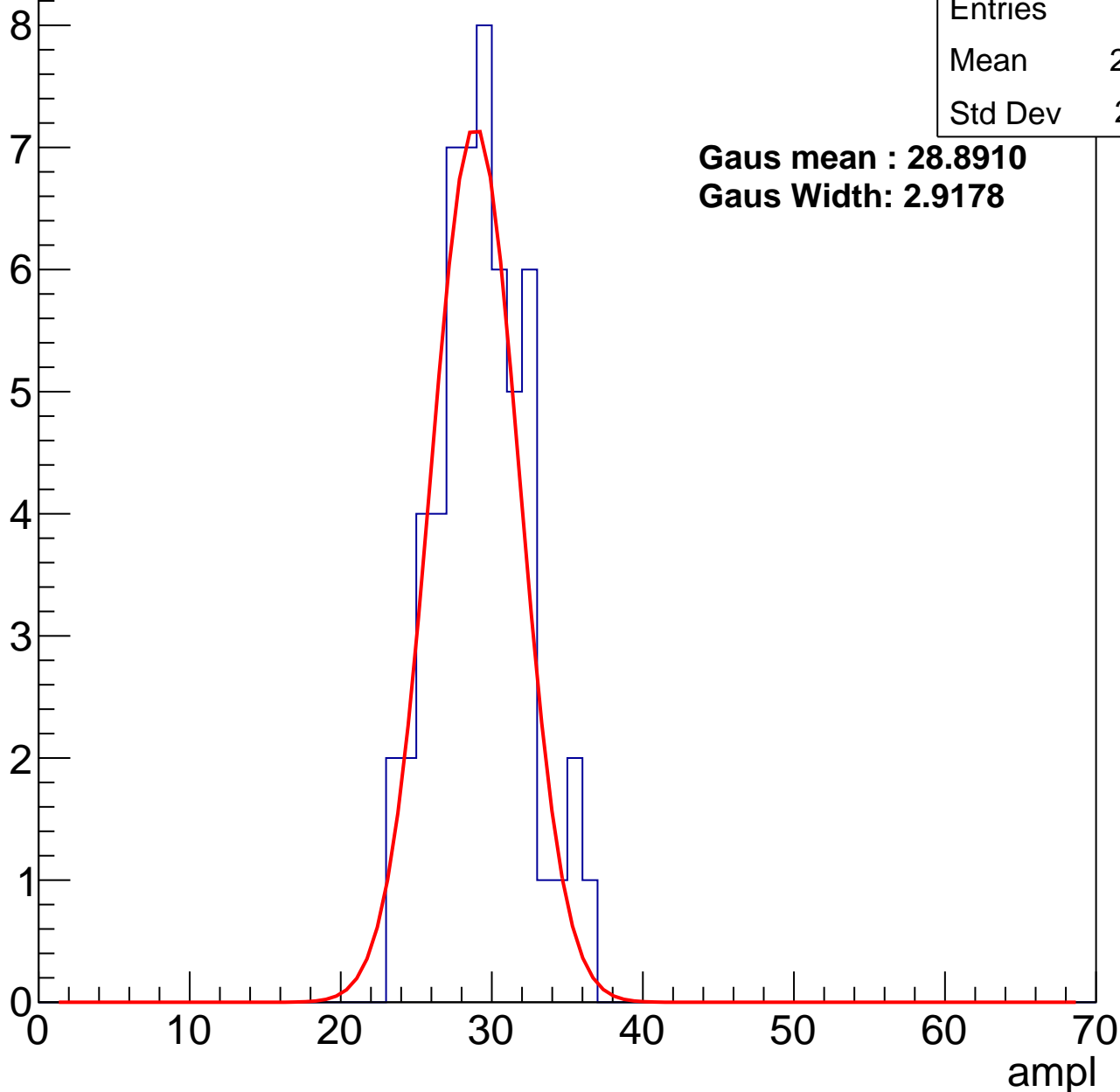
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	28.84
Std Dev	2.981

**Gaus mean : 28.8910**

**Gaus Width: 2.9178**



# B0L000S, U26-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	91
Mean	36.19
Std Dev	3.673

**Gaus mean : 36.8672**

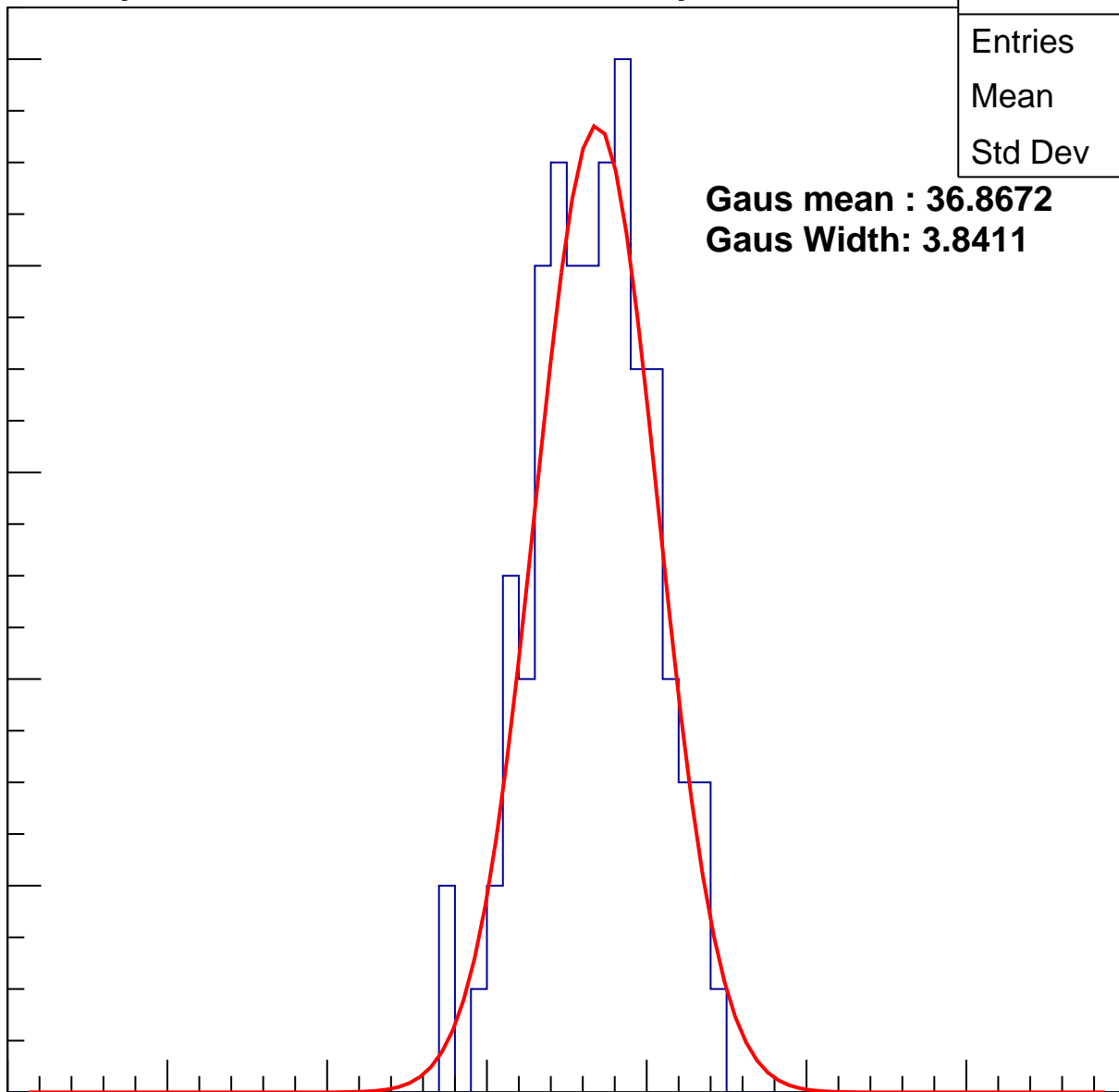
**Gaus Width: 3.8411**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

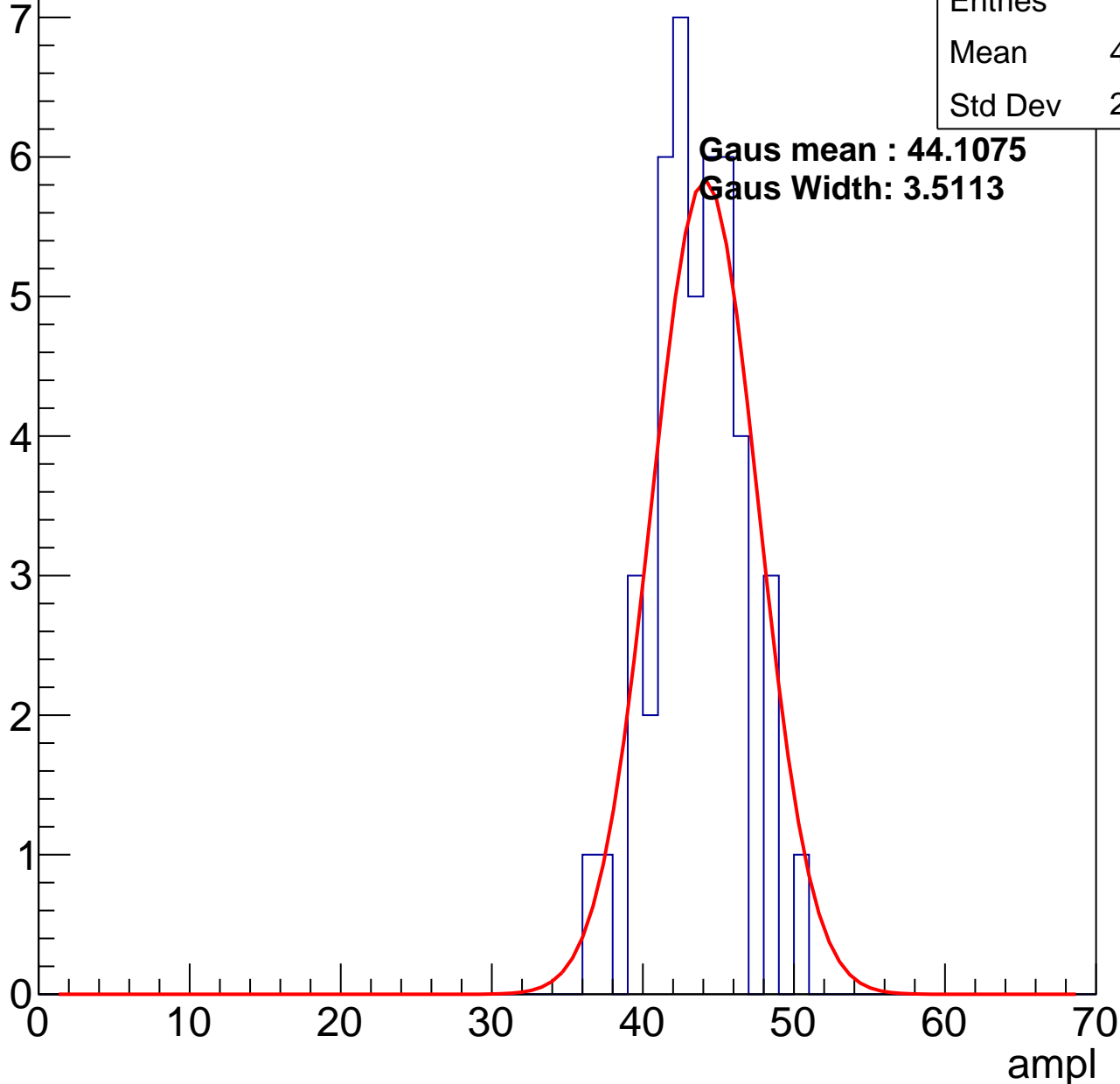


# B0L000S, U26-ch43, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	43.04
Std Dev	2.875

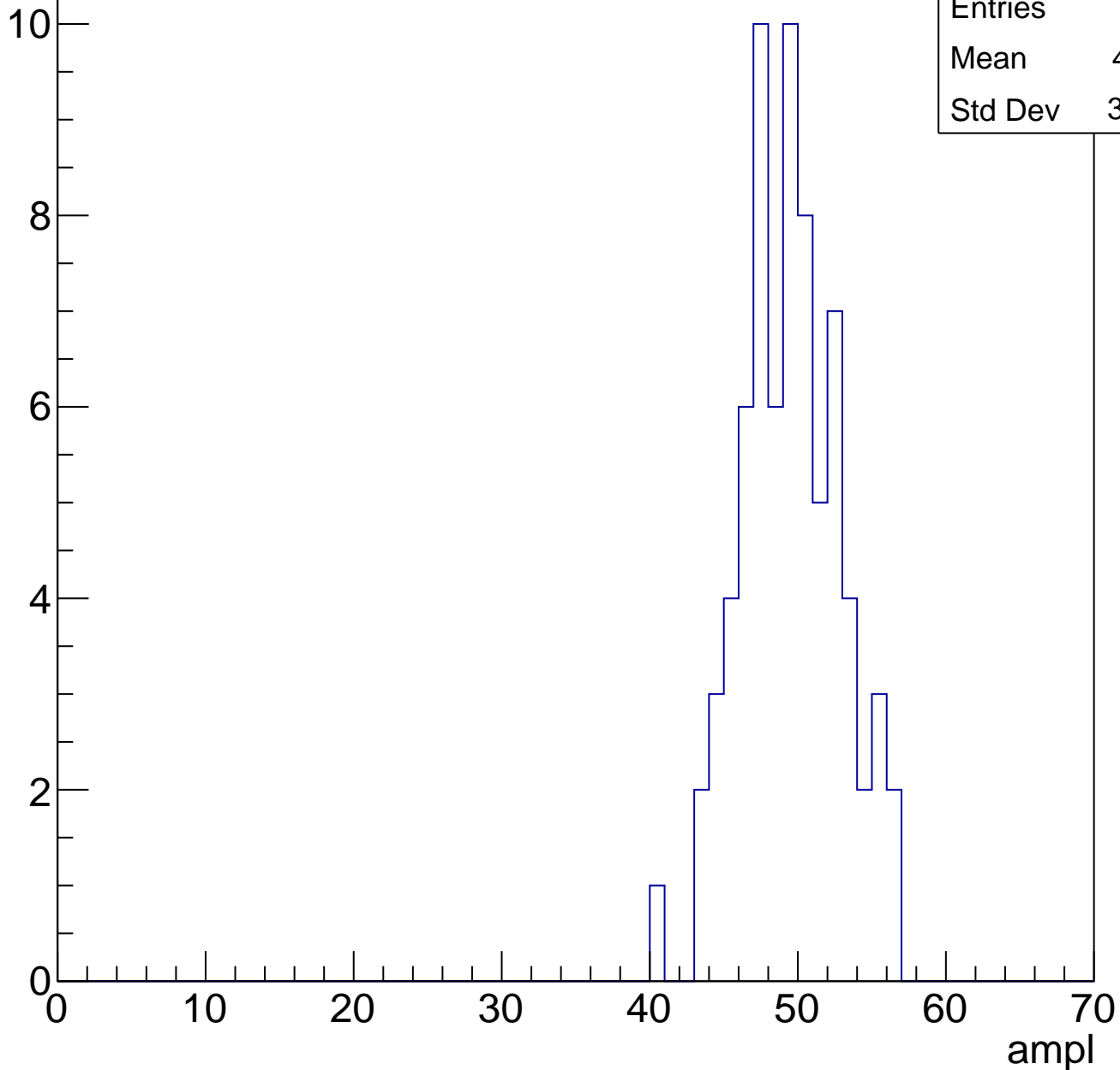


# B0L000S, U26-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	49.01
Std Dev	3.317

Entry

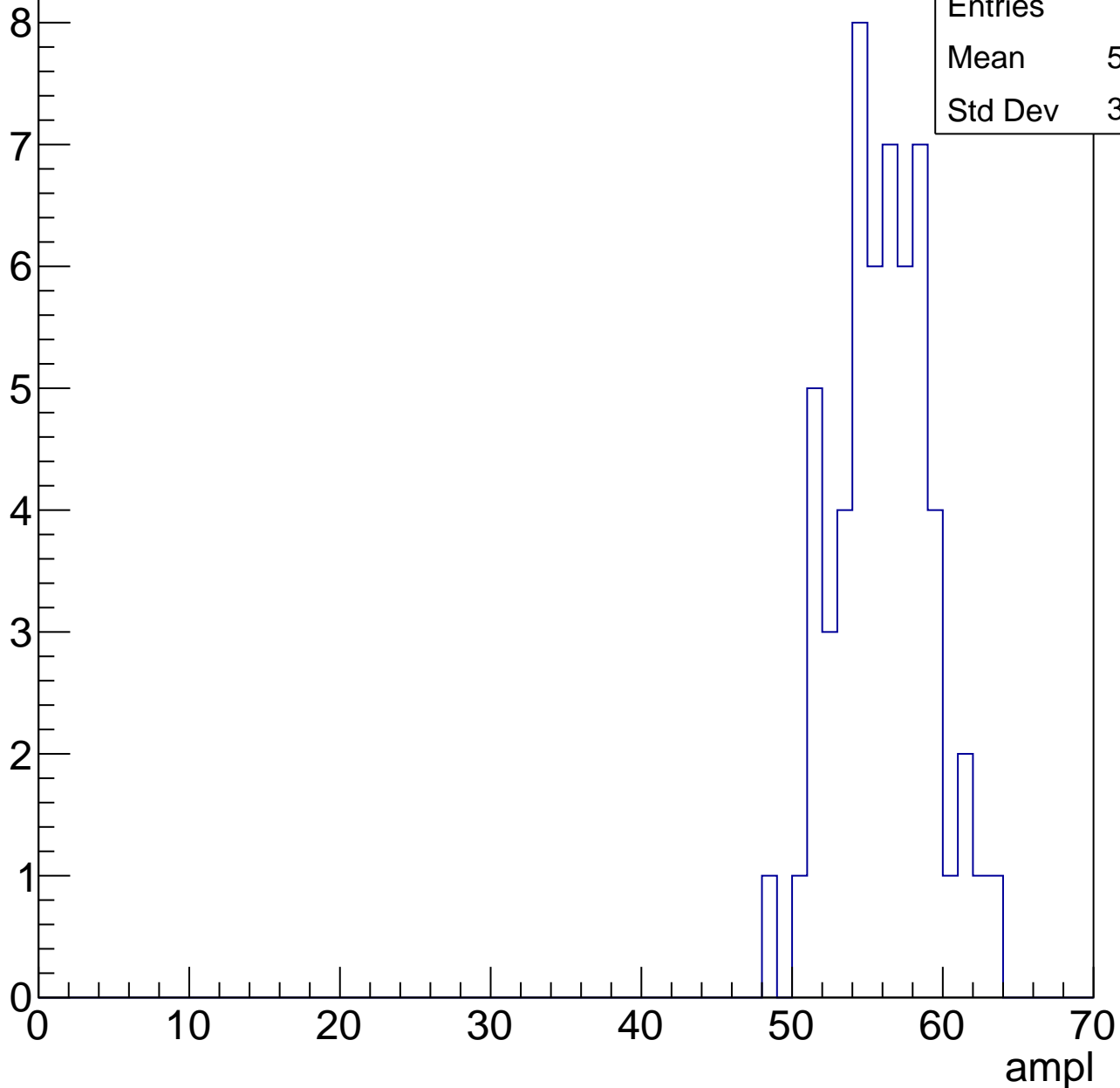


# B0L000S, U26-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

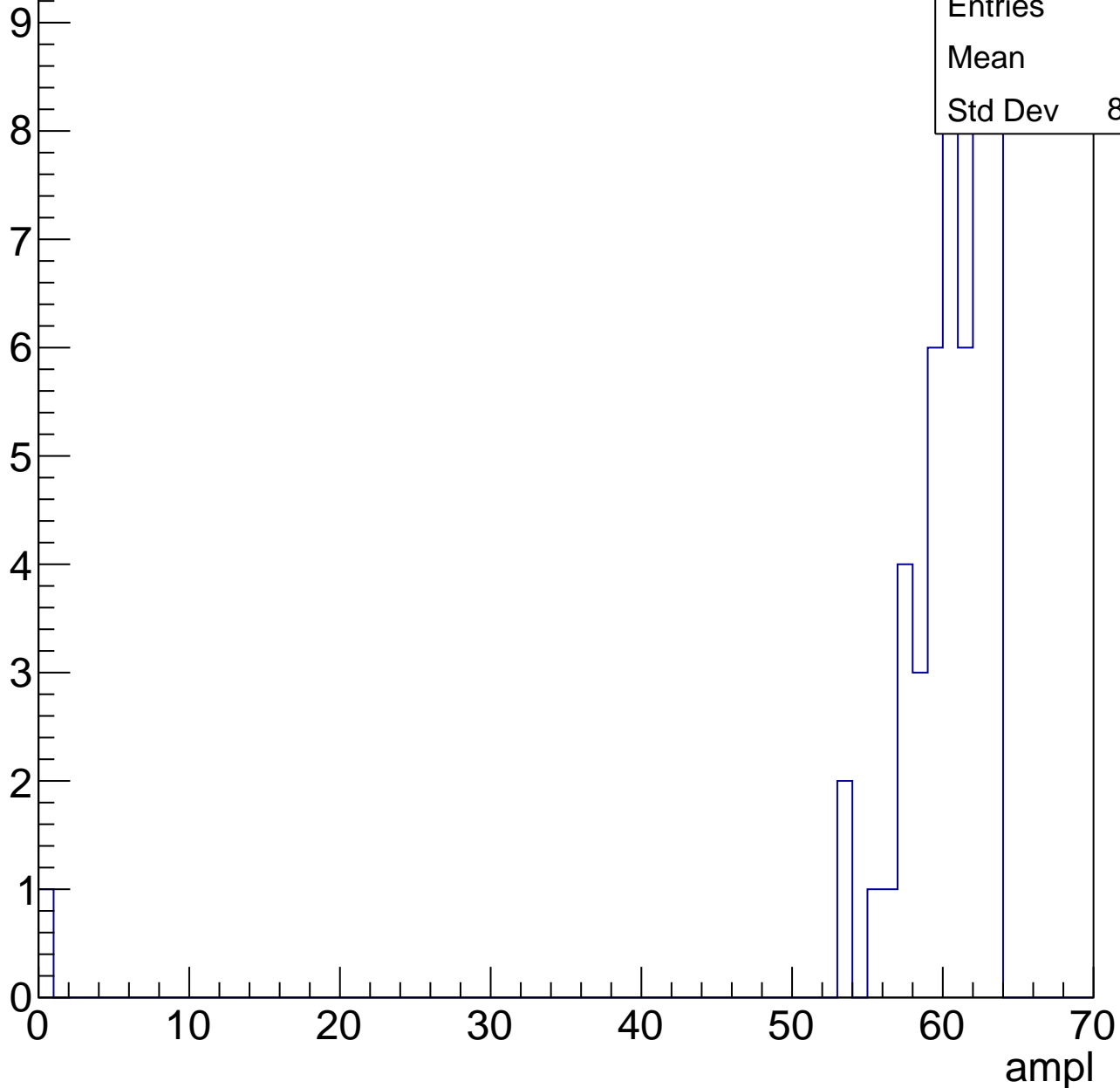
Entries	57
Mean	55.54
Std Dev	3.124



# B0L000S, U26-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

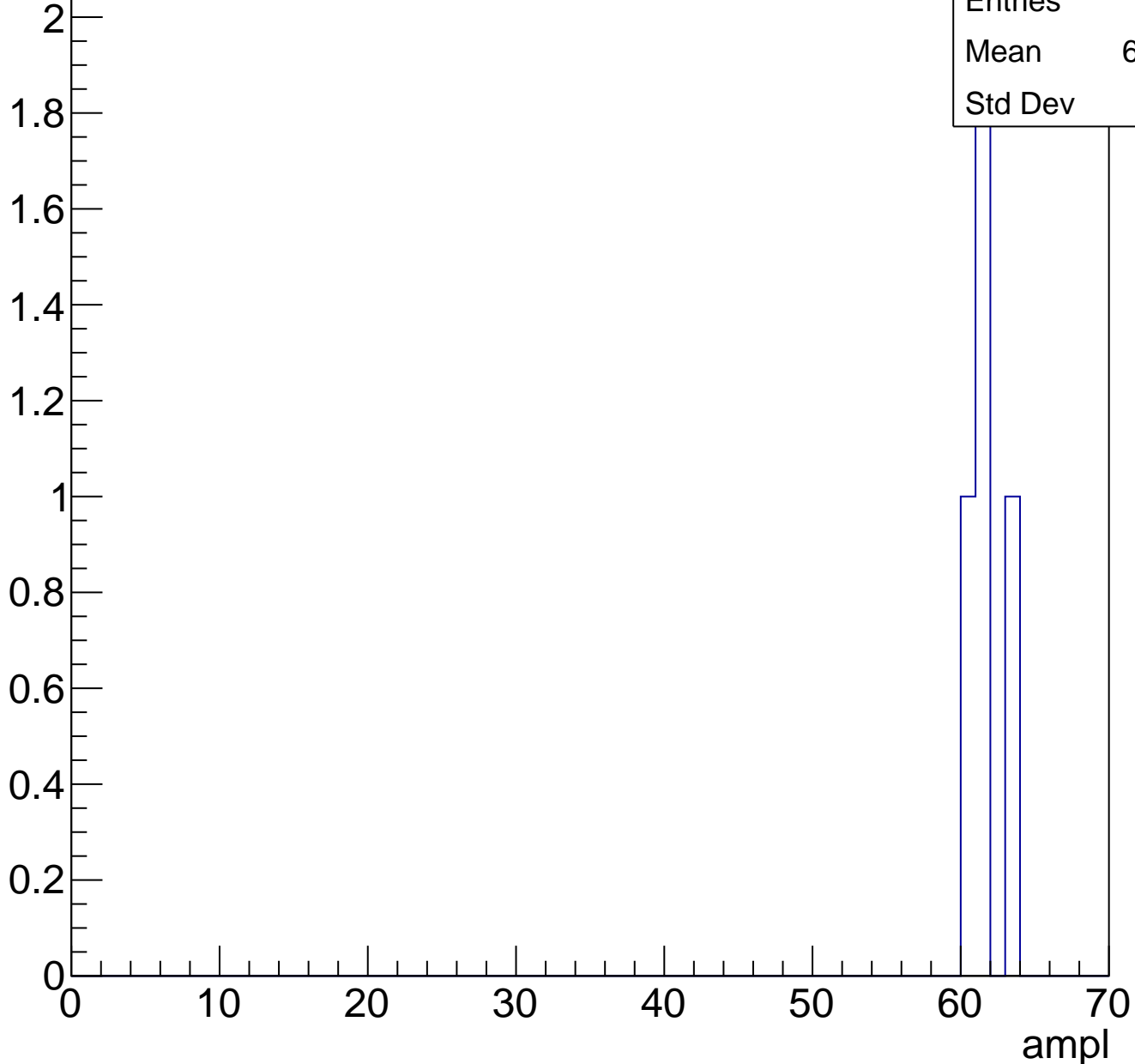
Entry



# B0L000S, U26-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B0L000S, U26-ch44, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	29.52
Std Dev	3.557

**Gaus mean : 29.3229**

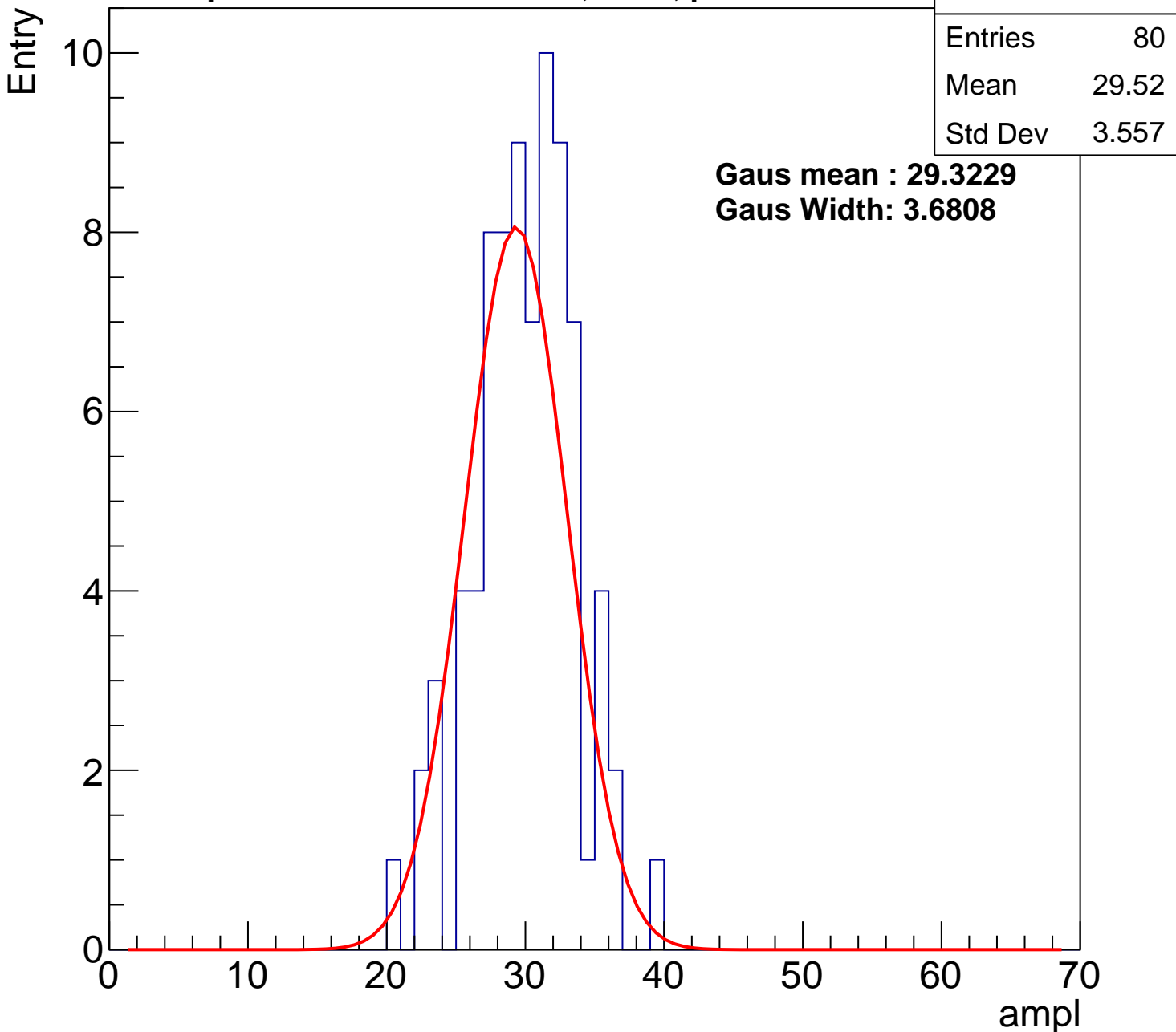
**Gaus Width: 3.6808**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch44, adc1

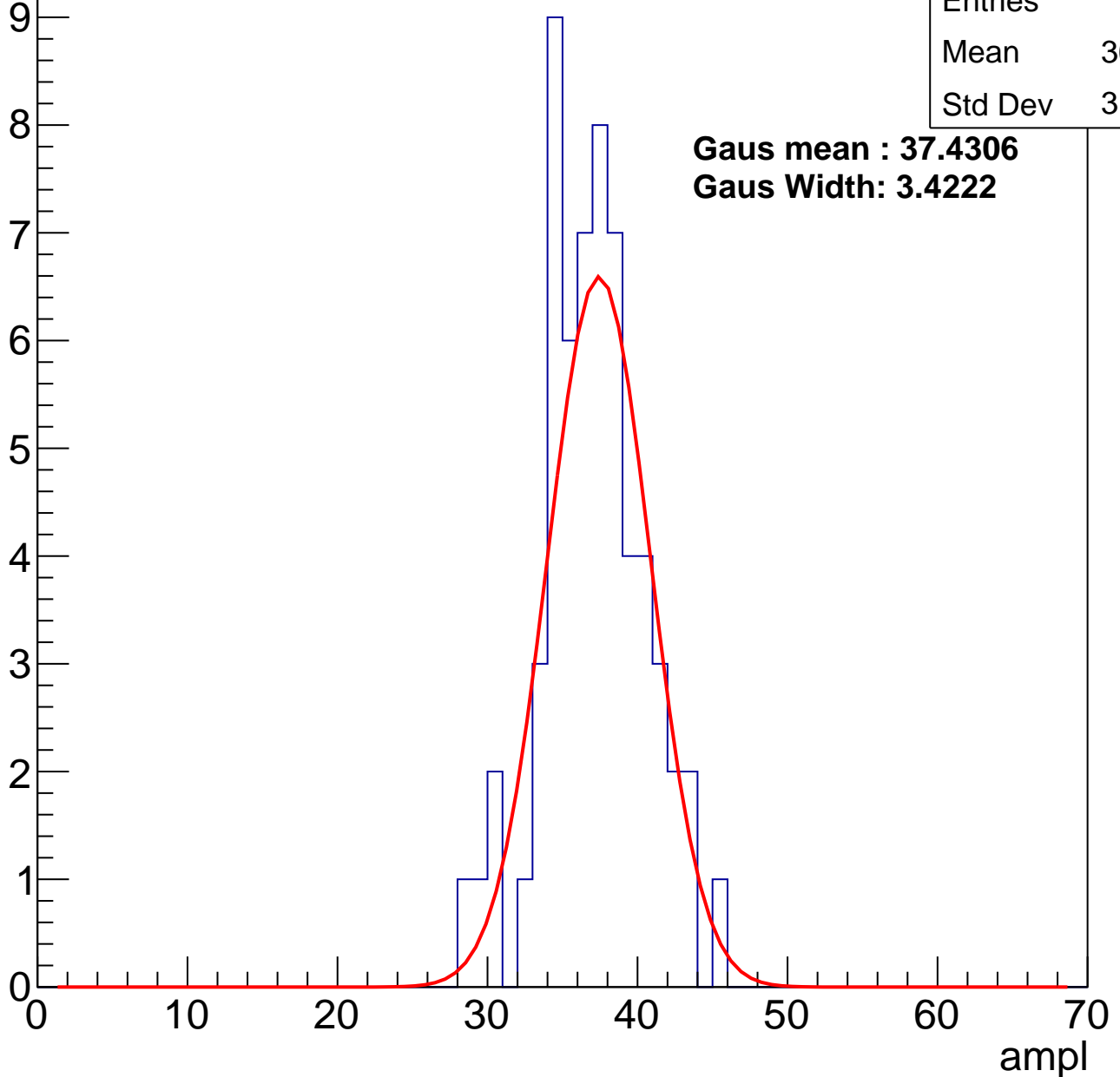
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.59
Std Dev	3.413

**Gaus mean : 37.4306**

**Gaus Width: 3.4222**



# B0L000S, U26-ch44, adc2

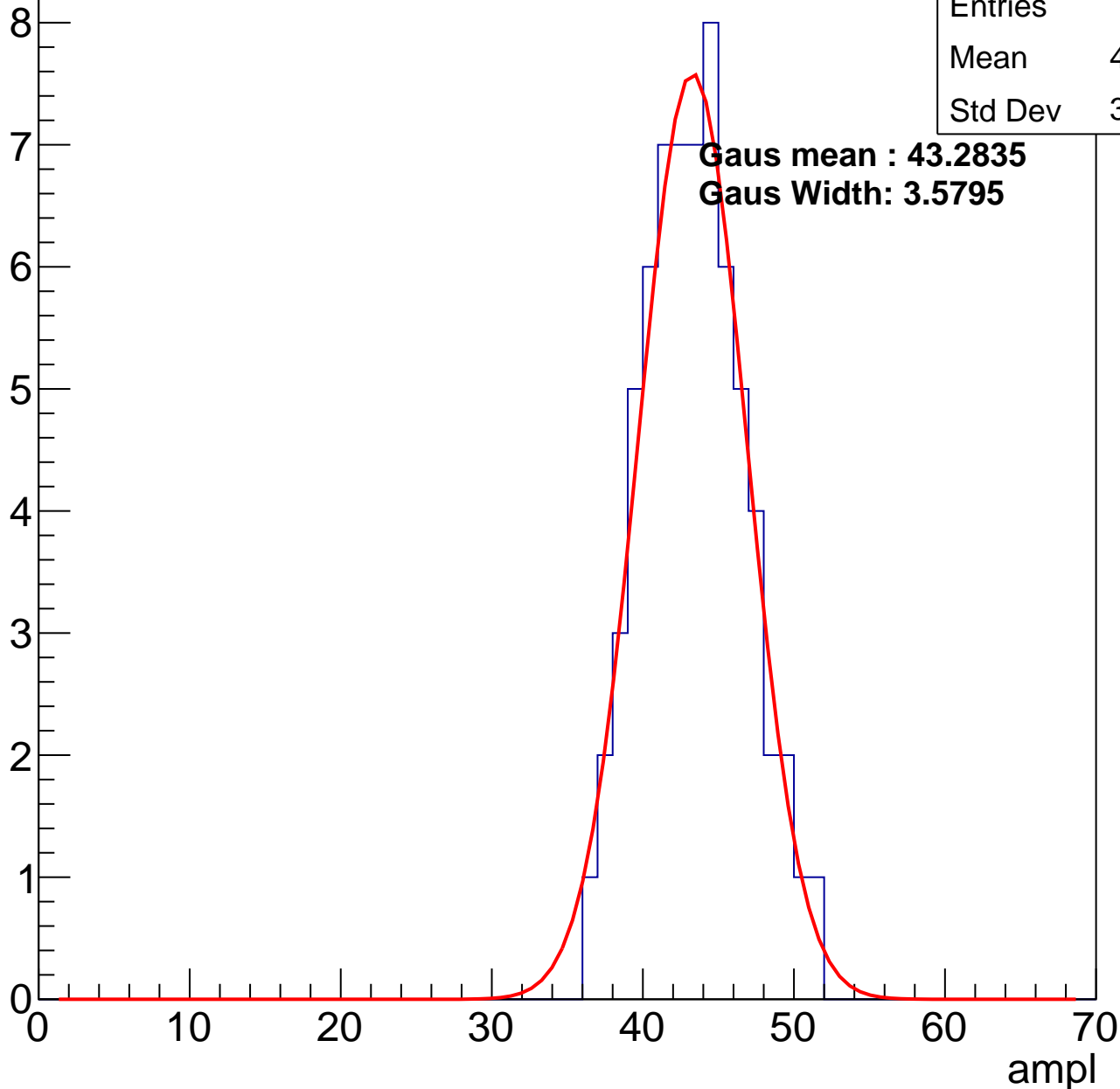
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	42.93
Std Dev	3.325

**Gaus mean : 43.2835**

**Gaus Width: 3.5795**

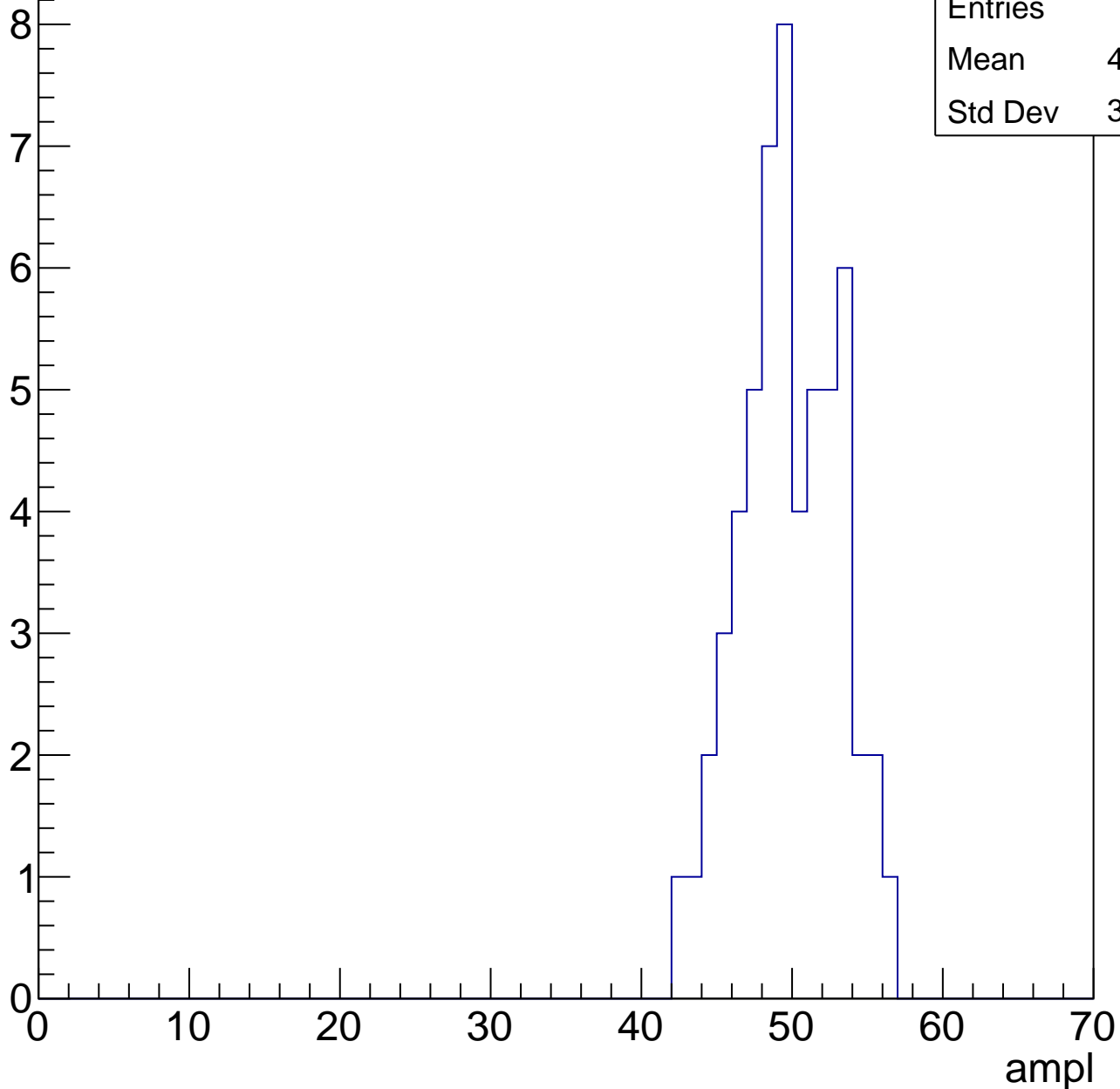


# B0L000S, U26-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	49.32
Std Dev	3.208

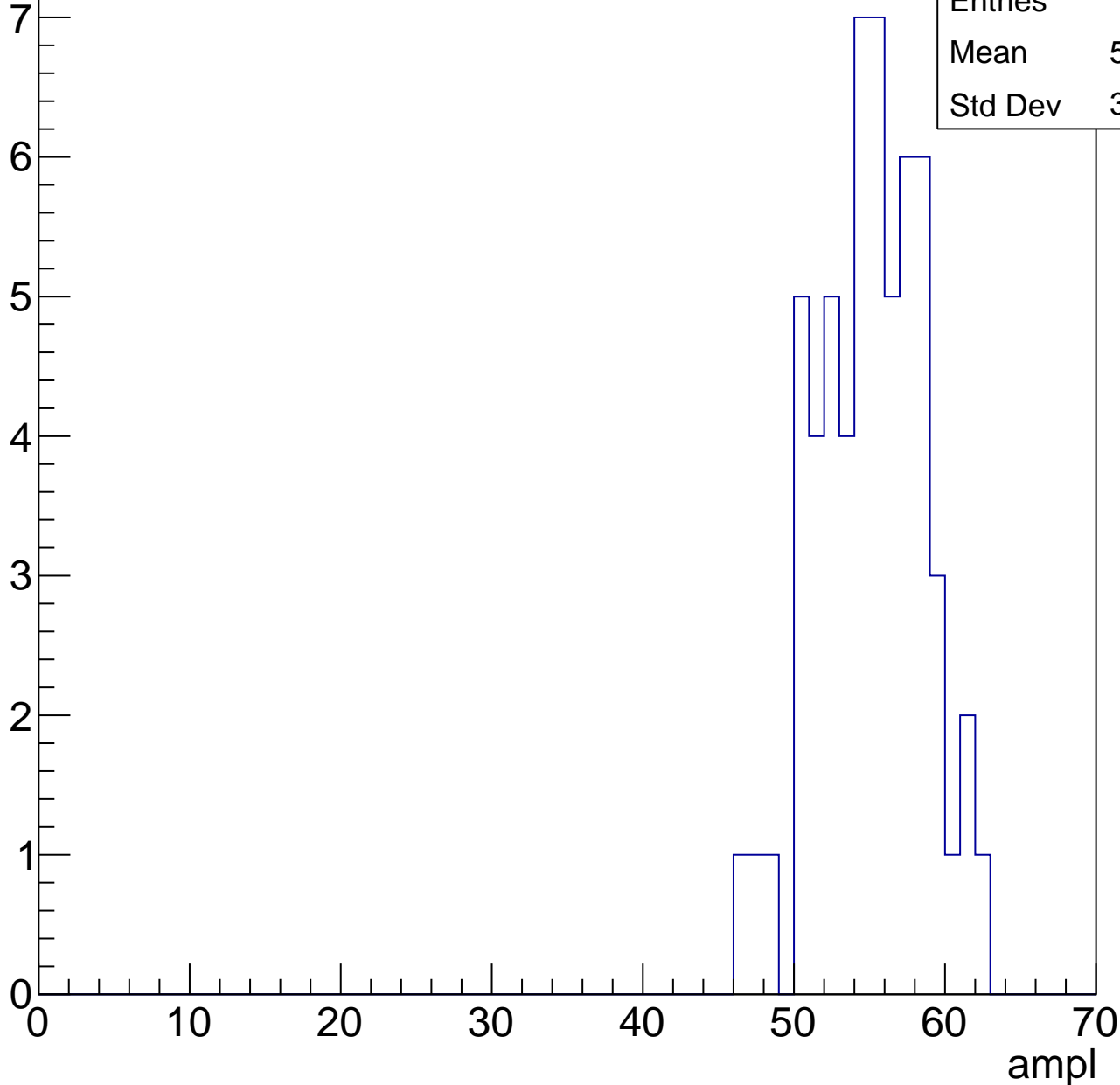


# B0L000S, U26-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

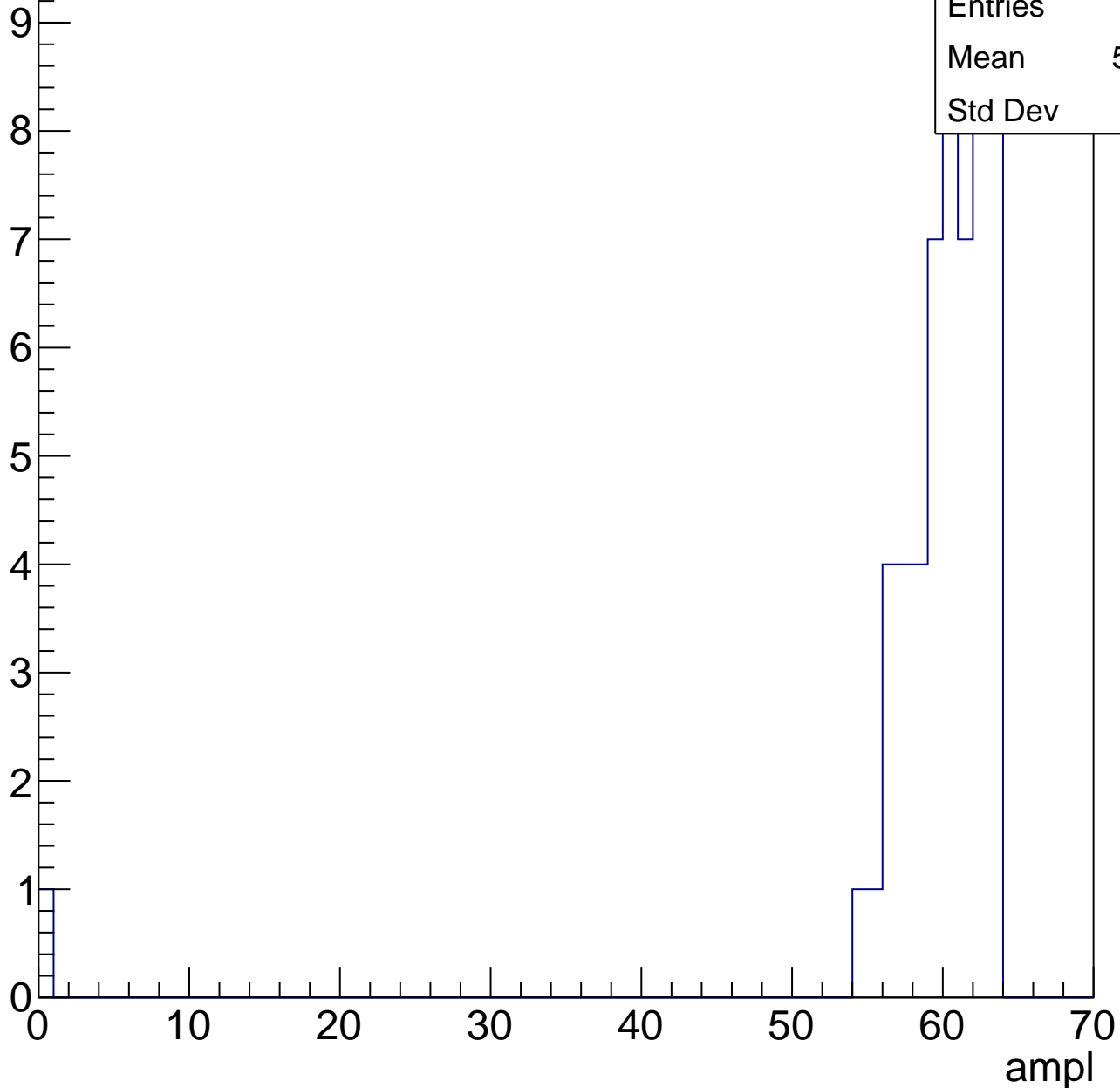
Entries	59
Mean	54.59
Std Dev	3.489



# B0L000S, U26-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U26-ch45, adc0

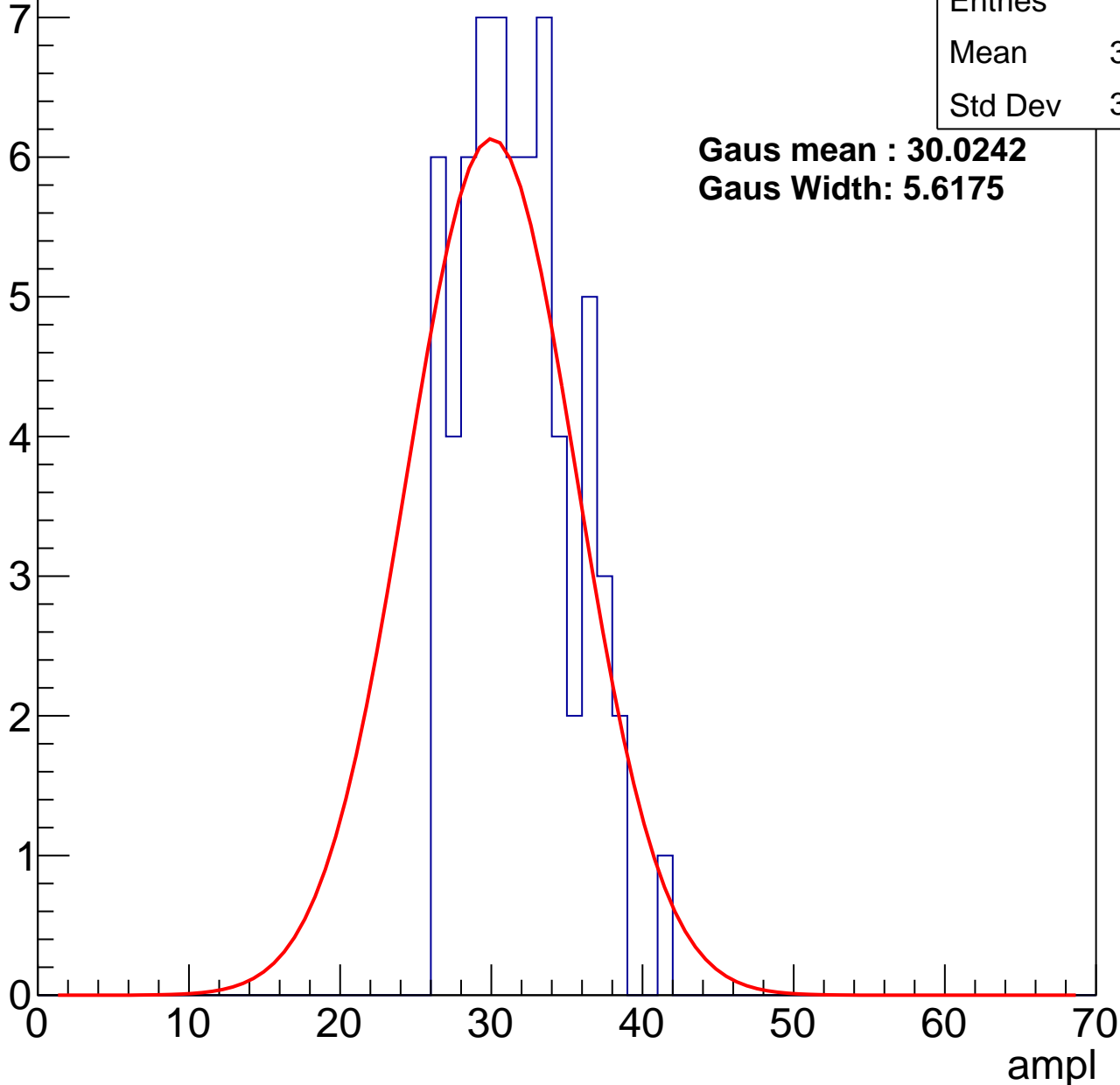
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	31.33
Std Dev	3.543

**Gaus mean : 30.0242**

**Gaus Width: 5.6175**



# B0L000S, U26-ch45, adc1

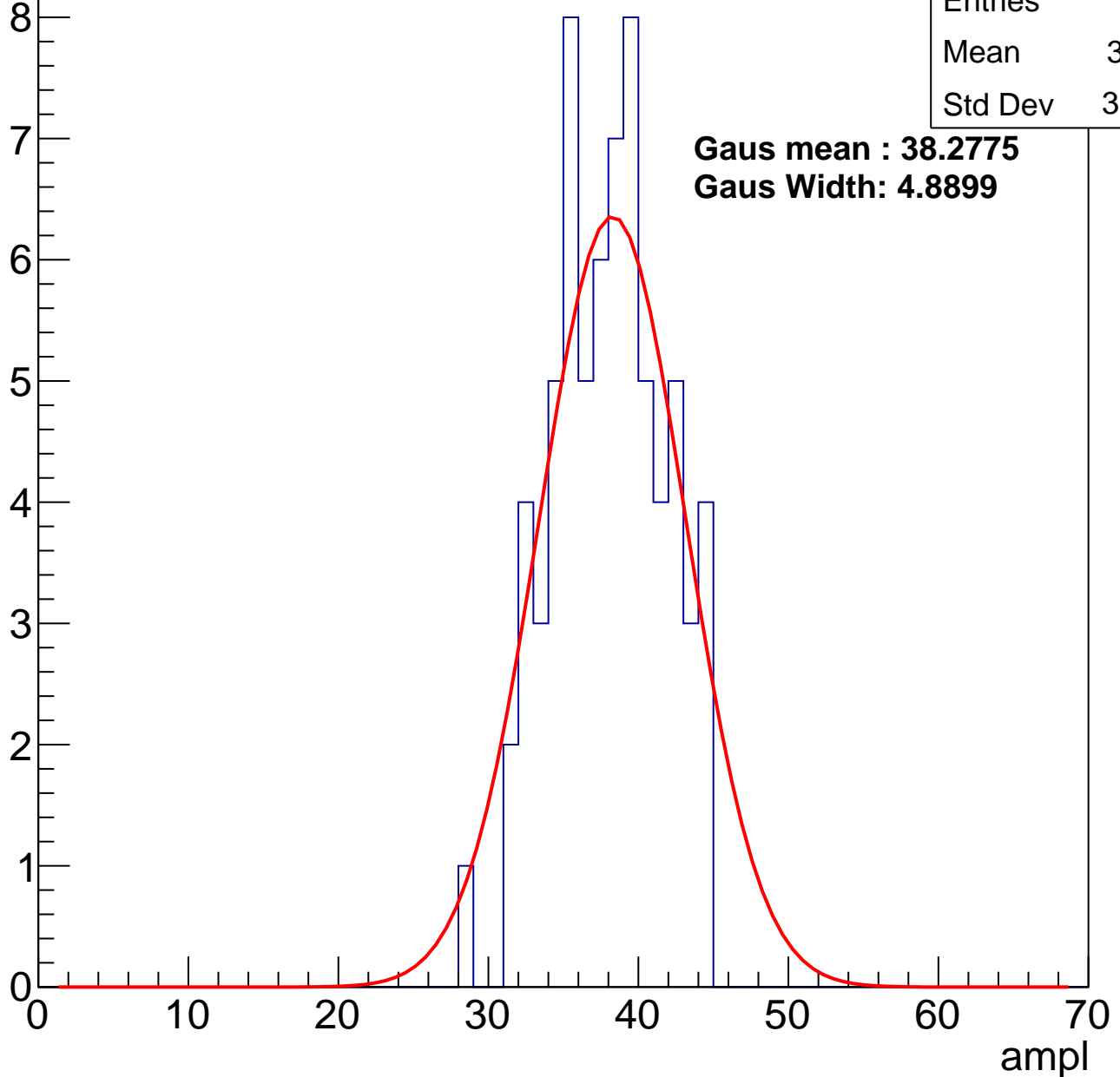
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.51
Std Dev	3.667

**Gaus mean : 38.2775**

**Gaus Width: 4.8899**



# B0L000S, U26-ch45, adc2

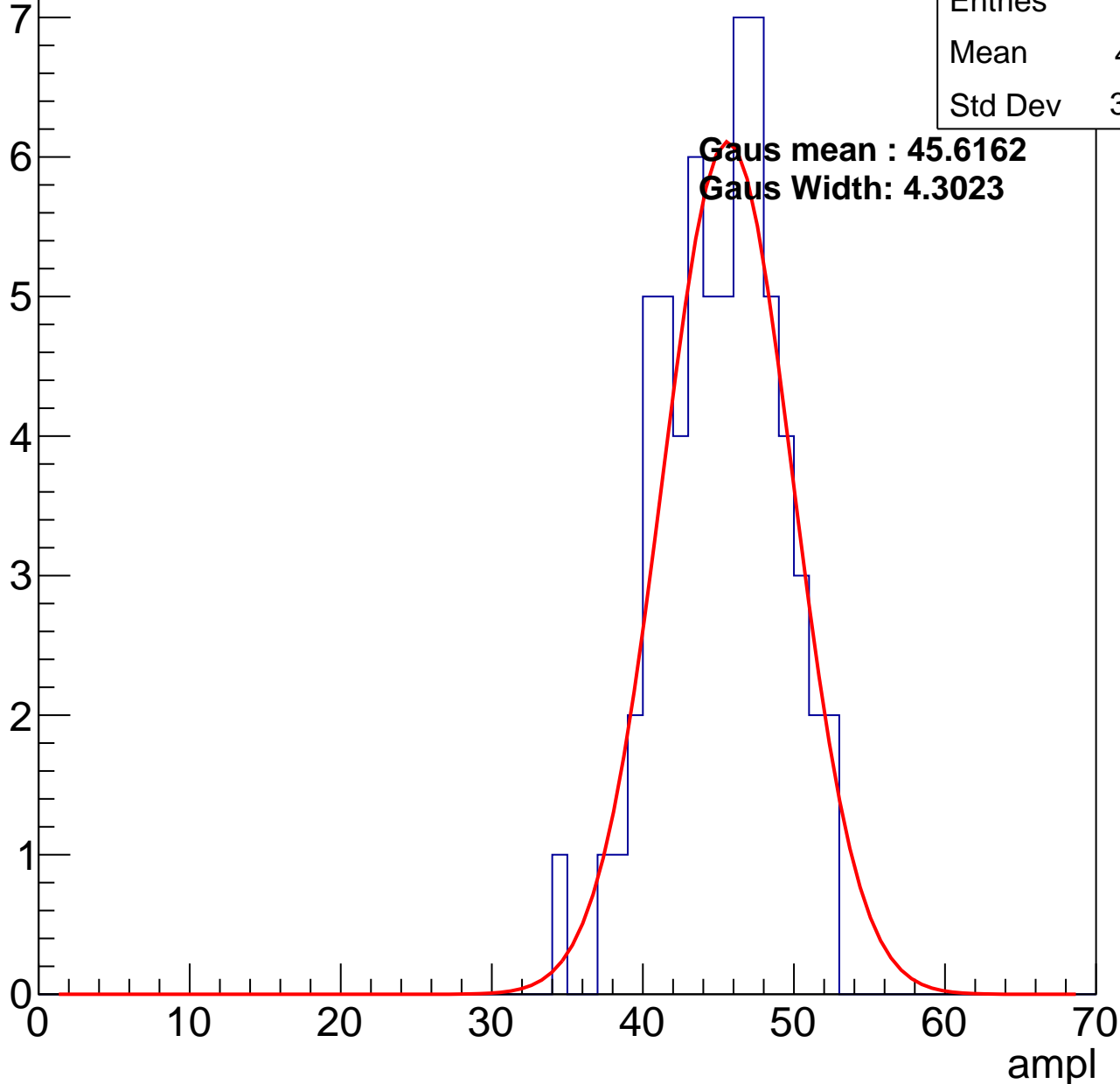
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.71
Std Dev	3.842

**Gaus mean : 45.6162**

**Gaus Width: 4.3023**

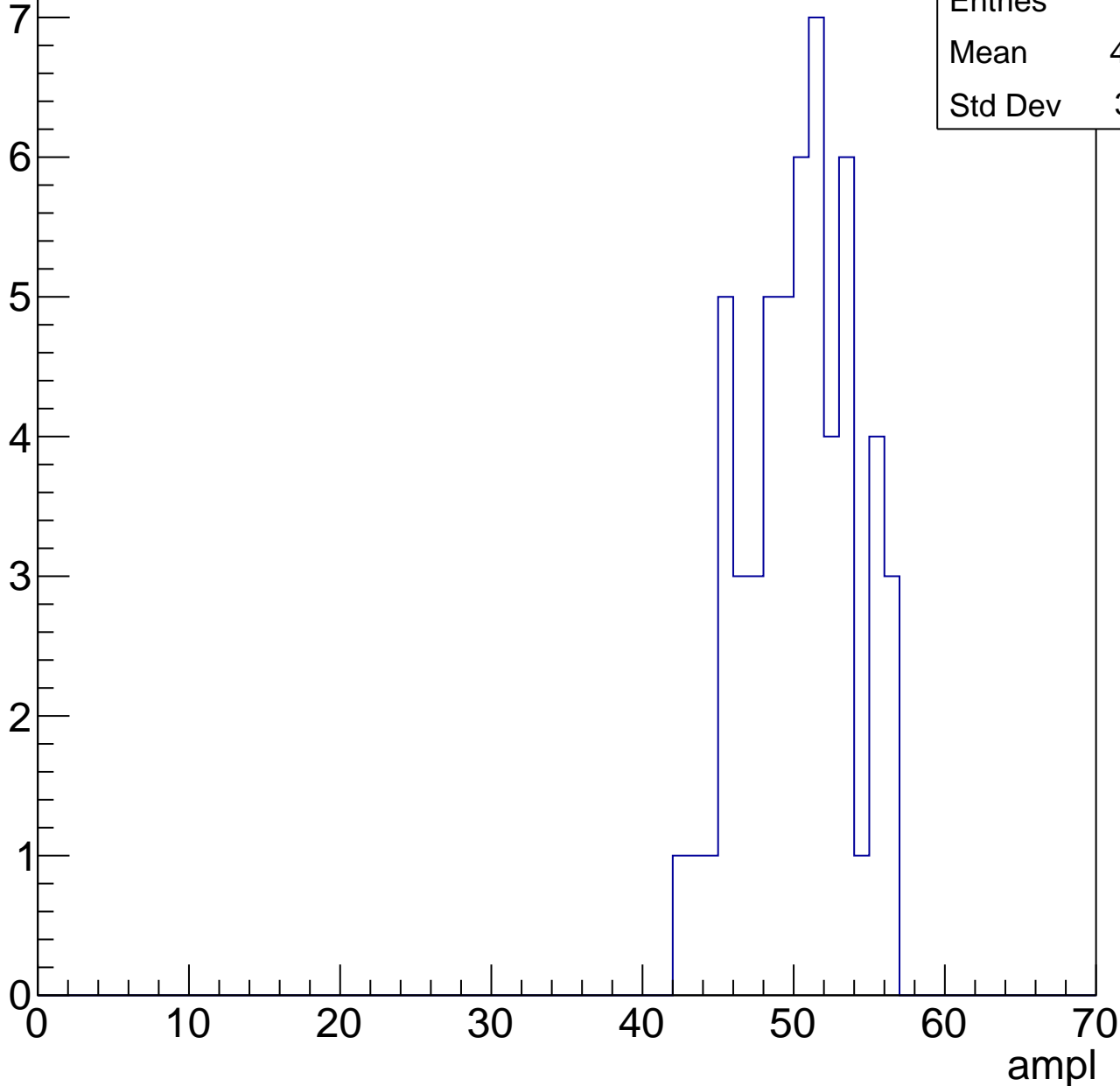


# B0L000S, U26-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.87
Std Dev	3.501

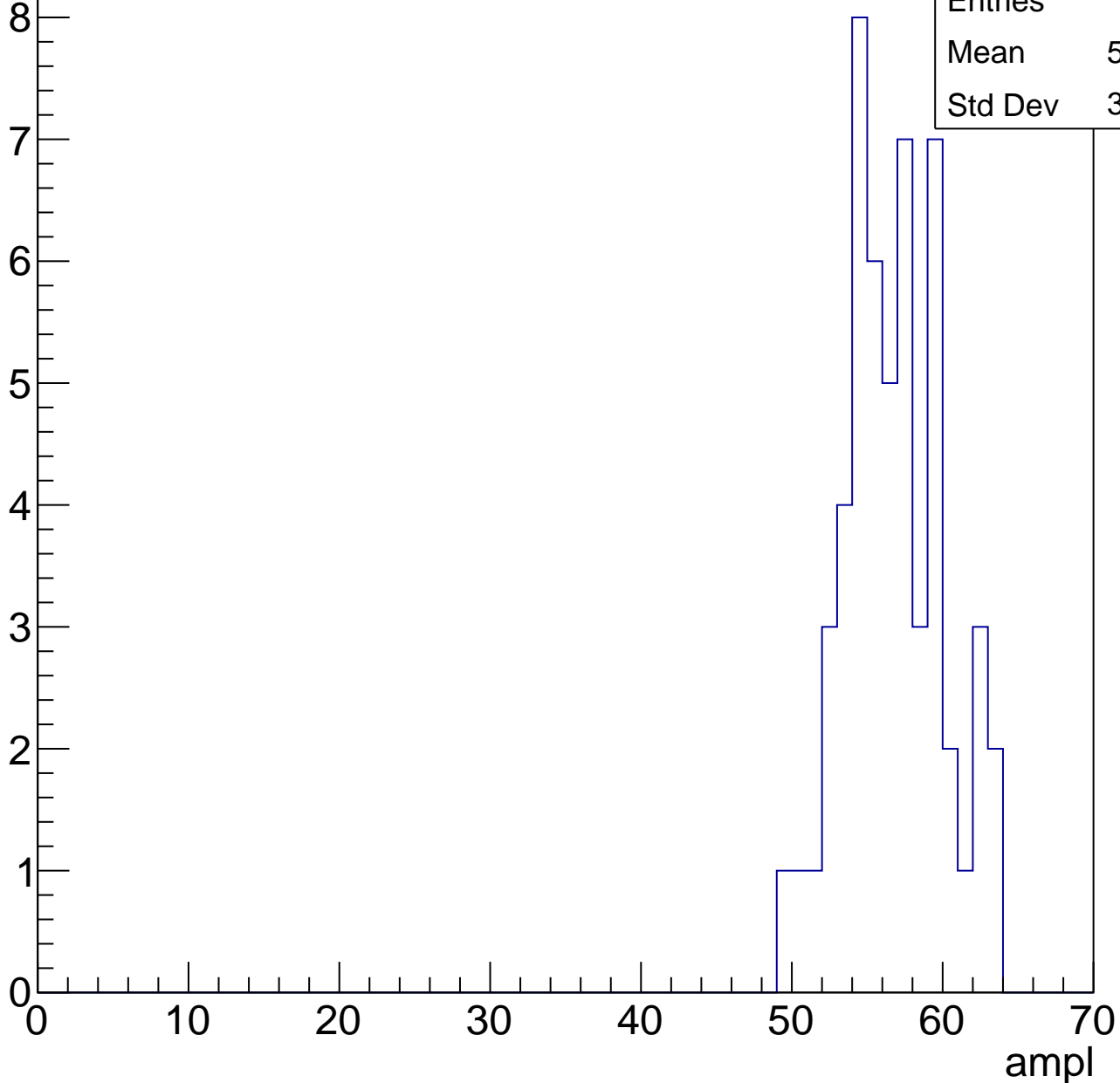


# B0L000S, U26-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	56.28
Std Dev	3.257

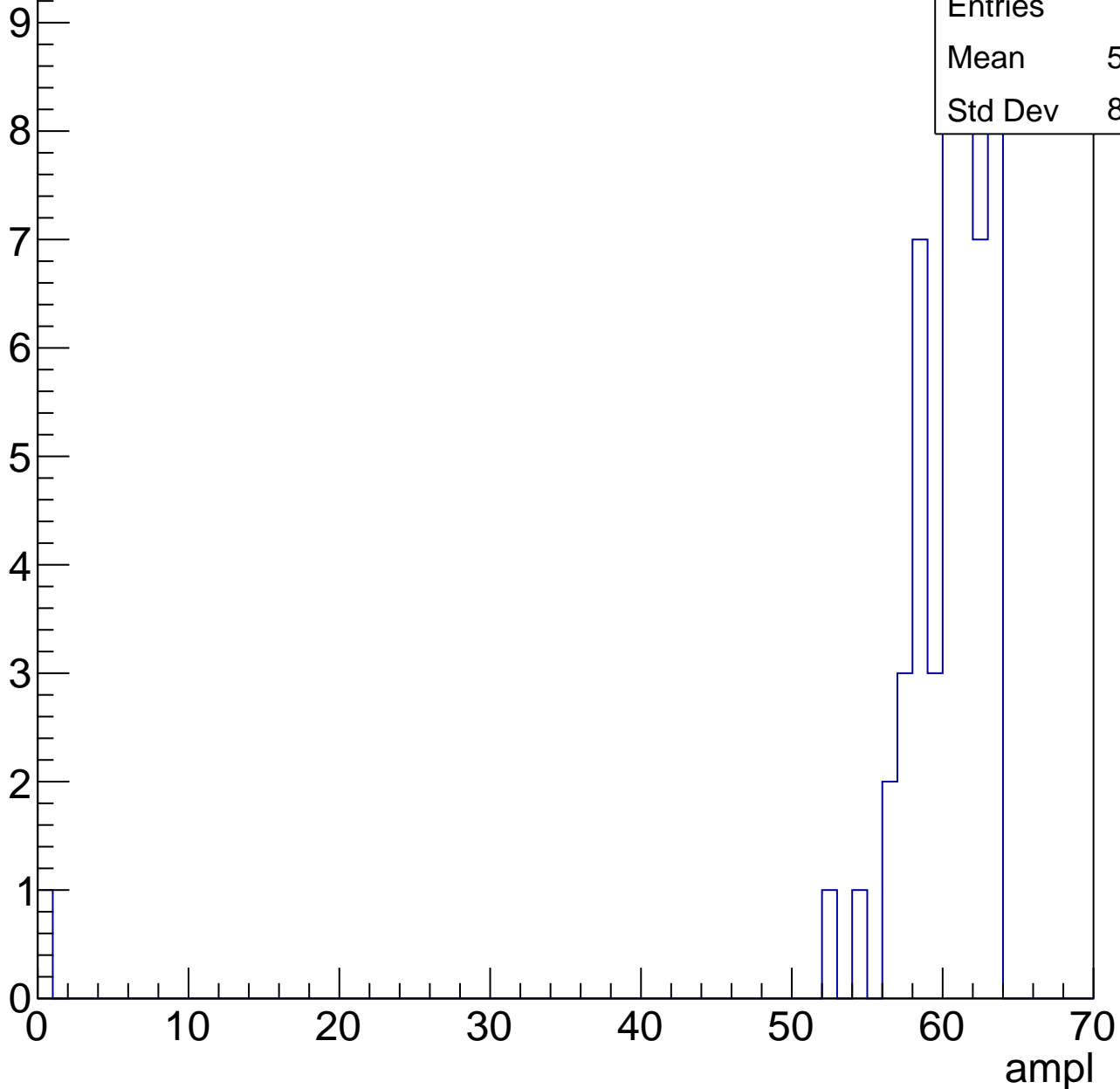


# B0L000S, U26-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	58.78
Std Dev	8.746



# B0L000S, U26-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U26-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch46, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	29.23
Std Dev	3.134

**Gaus mean : 30.0148**

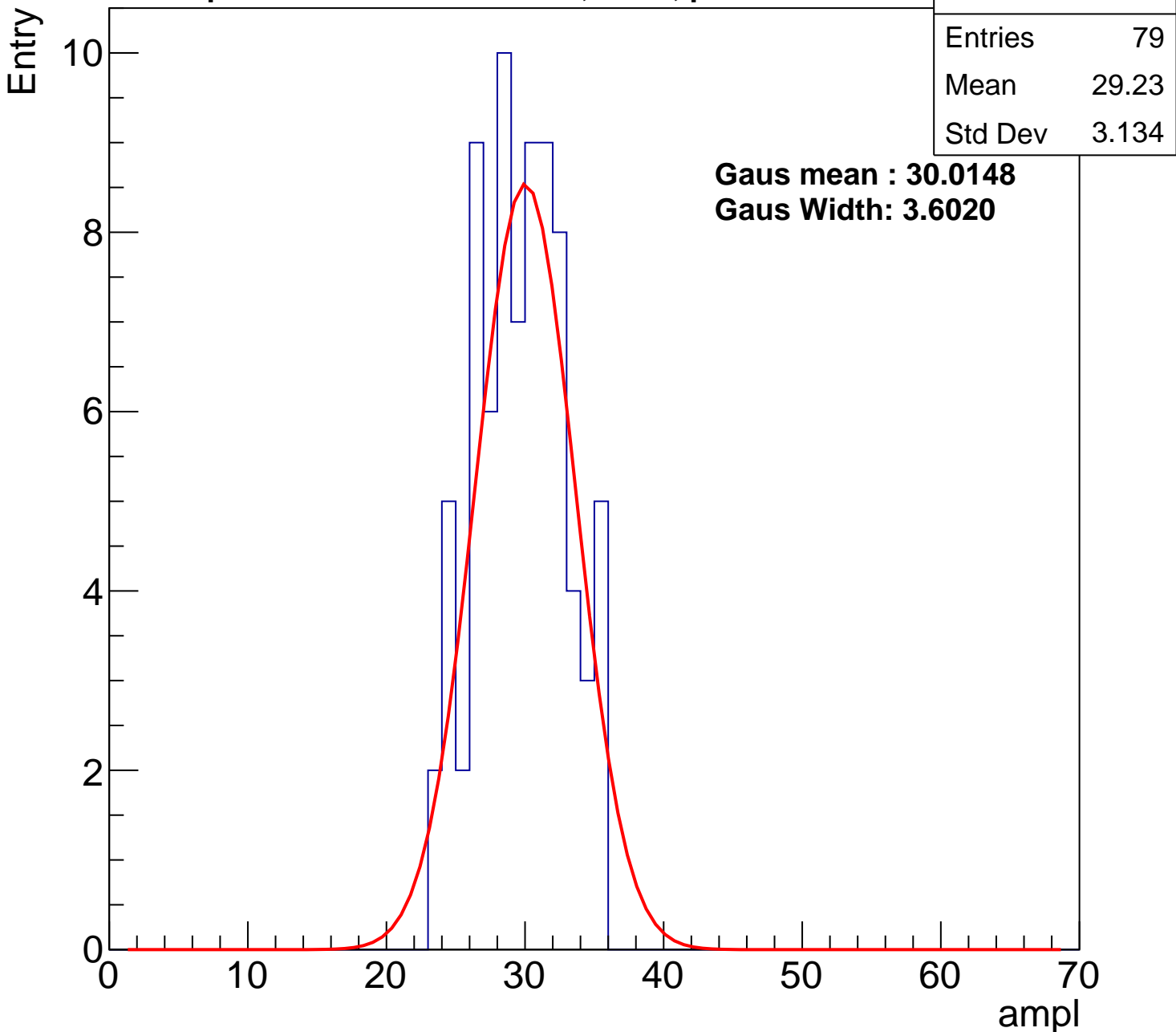
**Gaus Width: 3.6020**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

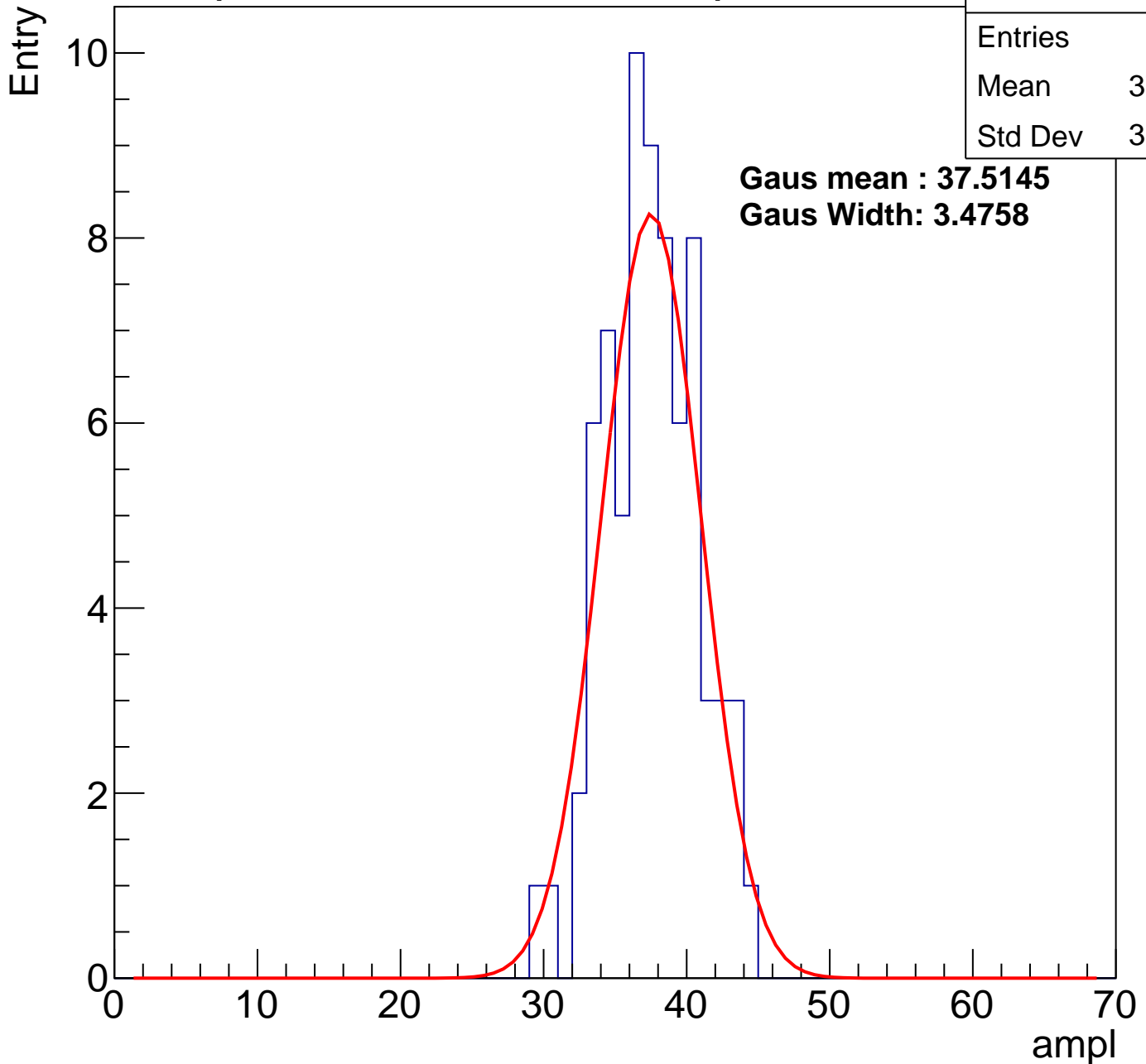


# B0L000S, U26-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	37.08
Std Dev	3.174

**Gaus mean : 37.5145**  
**Gaus Width: 3.4758**



# B0L000S, U26-ch46, adc2

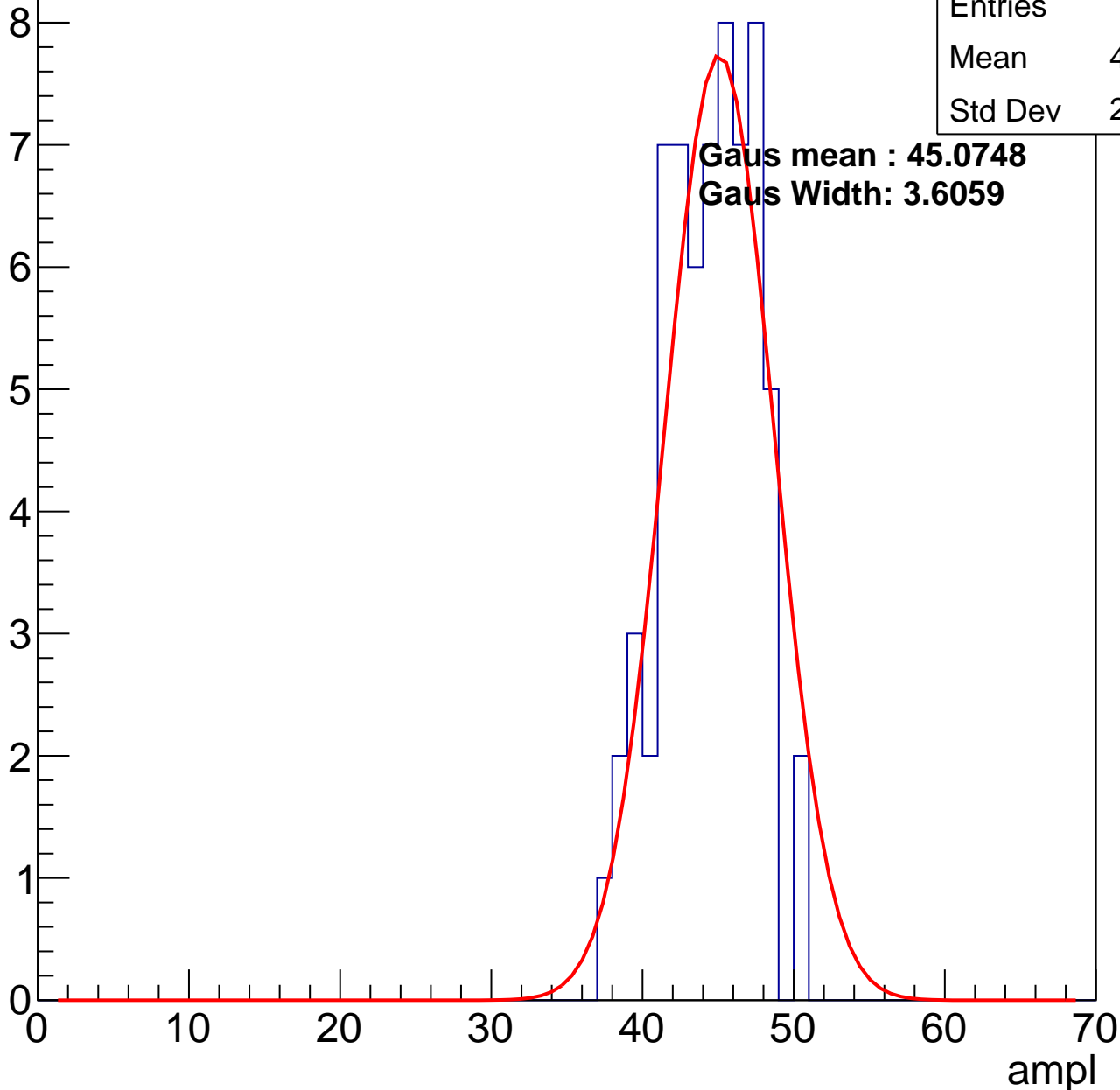
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.92
Std Dev	2.999

**Gaus mean : 45.0748**

**Gaus Width: 3.6059**

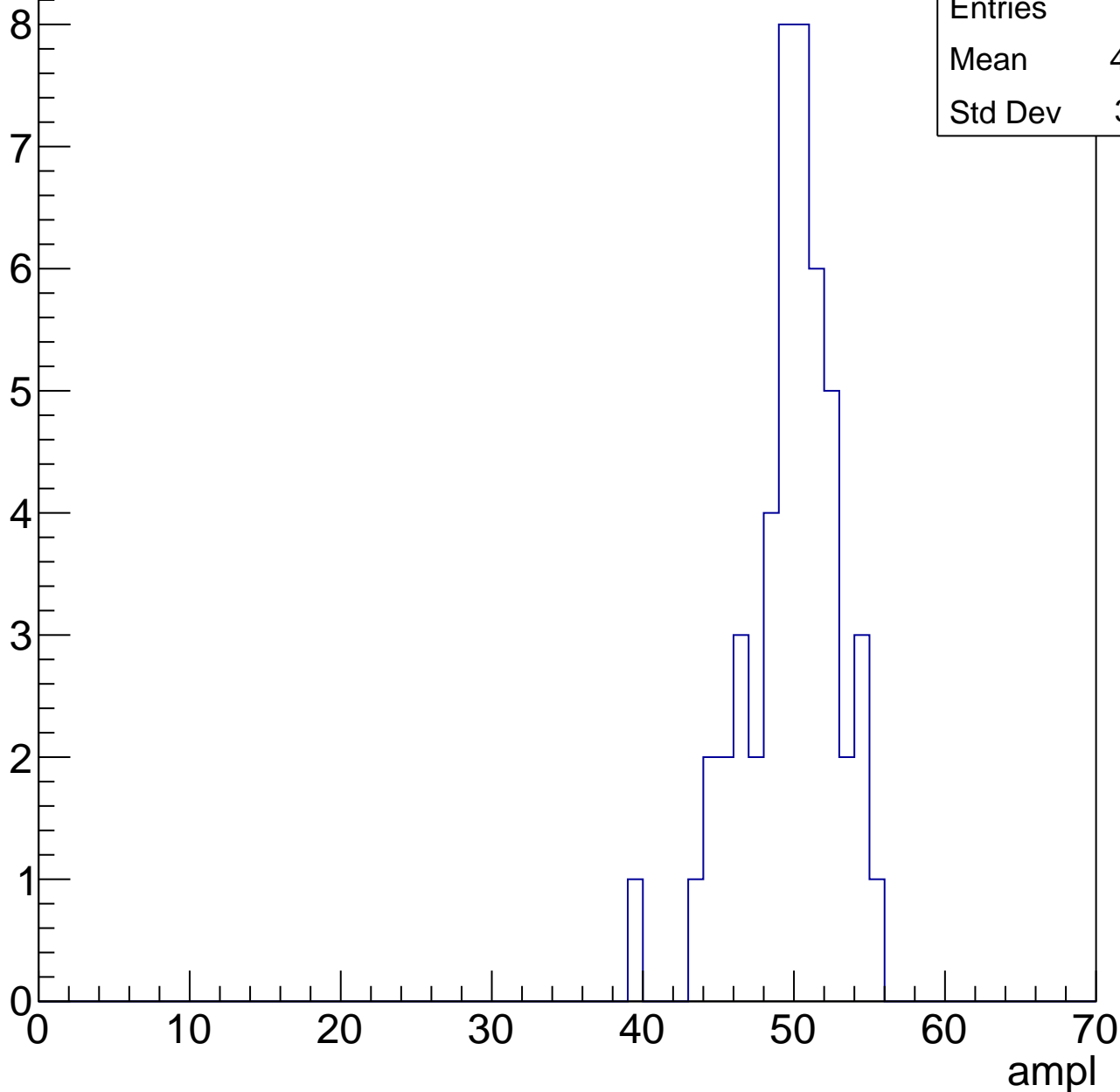


# B0L000S, U26-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	49.27
Std Dev	3.141



# B0L000S, U26-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	55.53
Std Dev	3.115

Entry

10

8

6

4

2

0

0

10

20

30

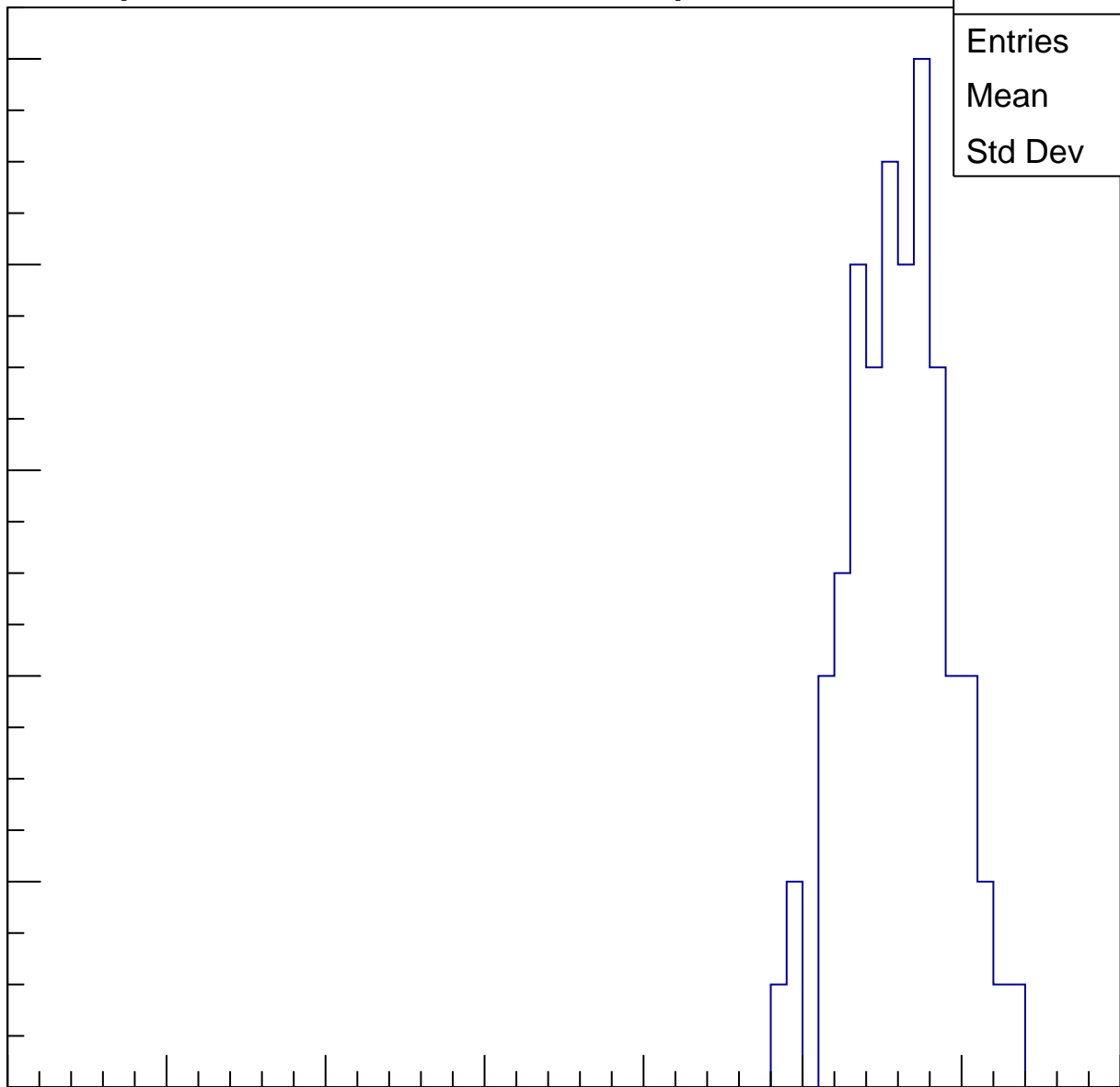
40

50

60

70

ampl

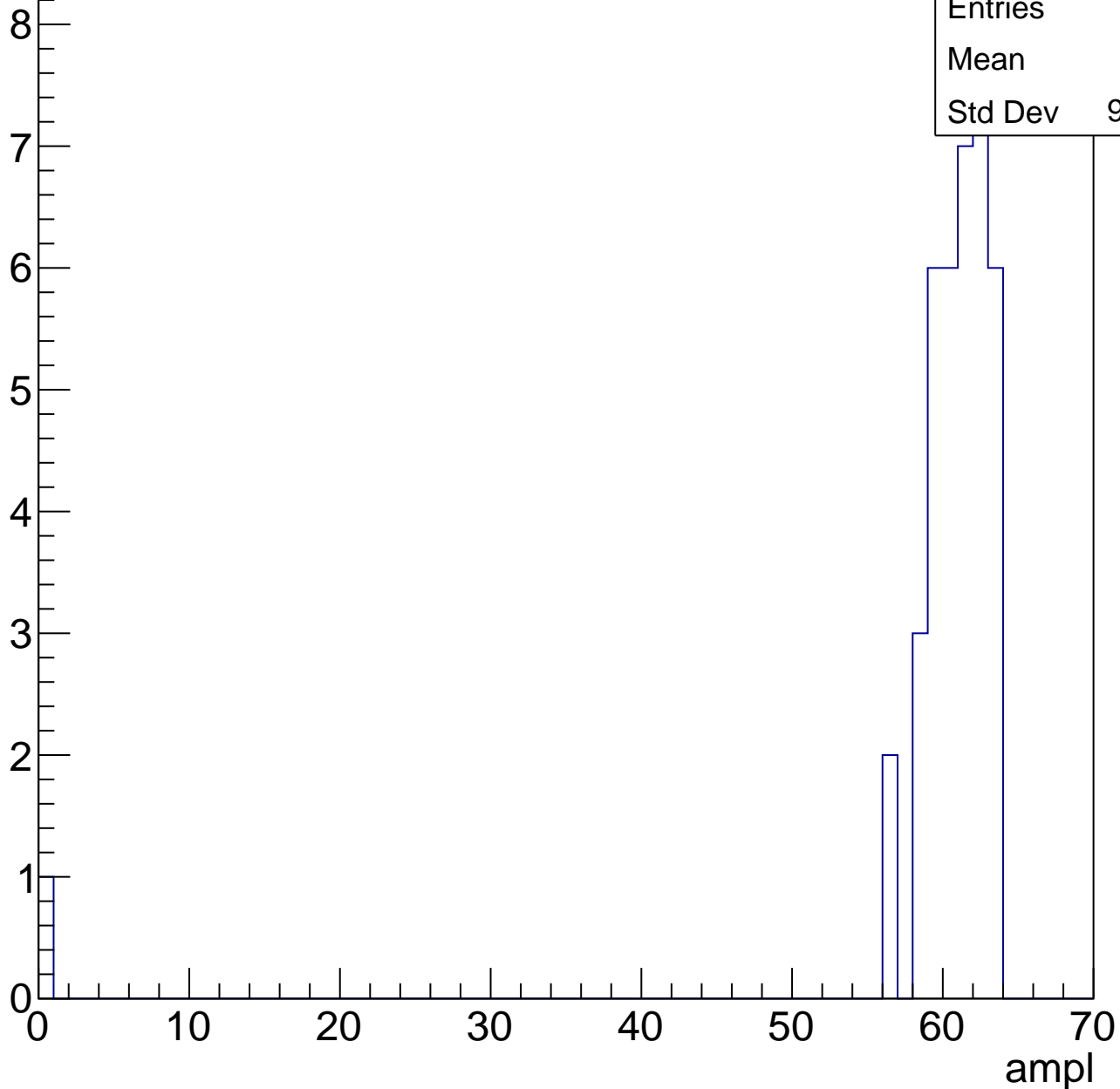


# B0L000S, U26-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	59
Std Dev	9.745



# B0L000S, U26-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch47, adc0

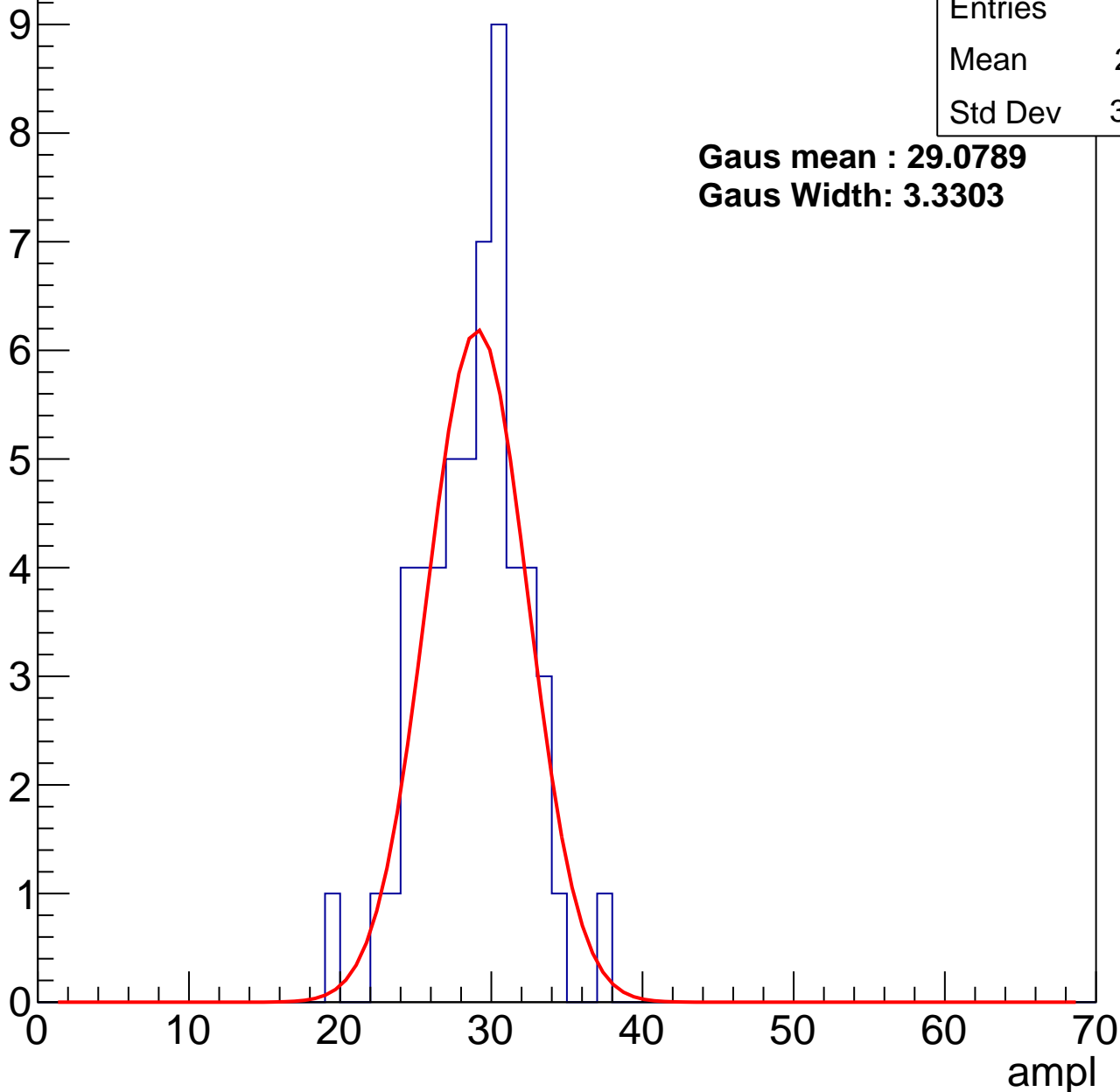
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	28.41
Std Dev	3.303

**Gaus mean : 29.0789**

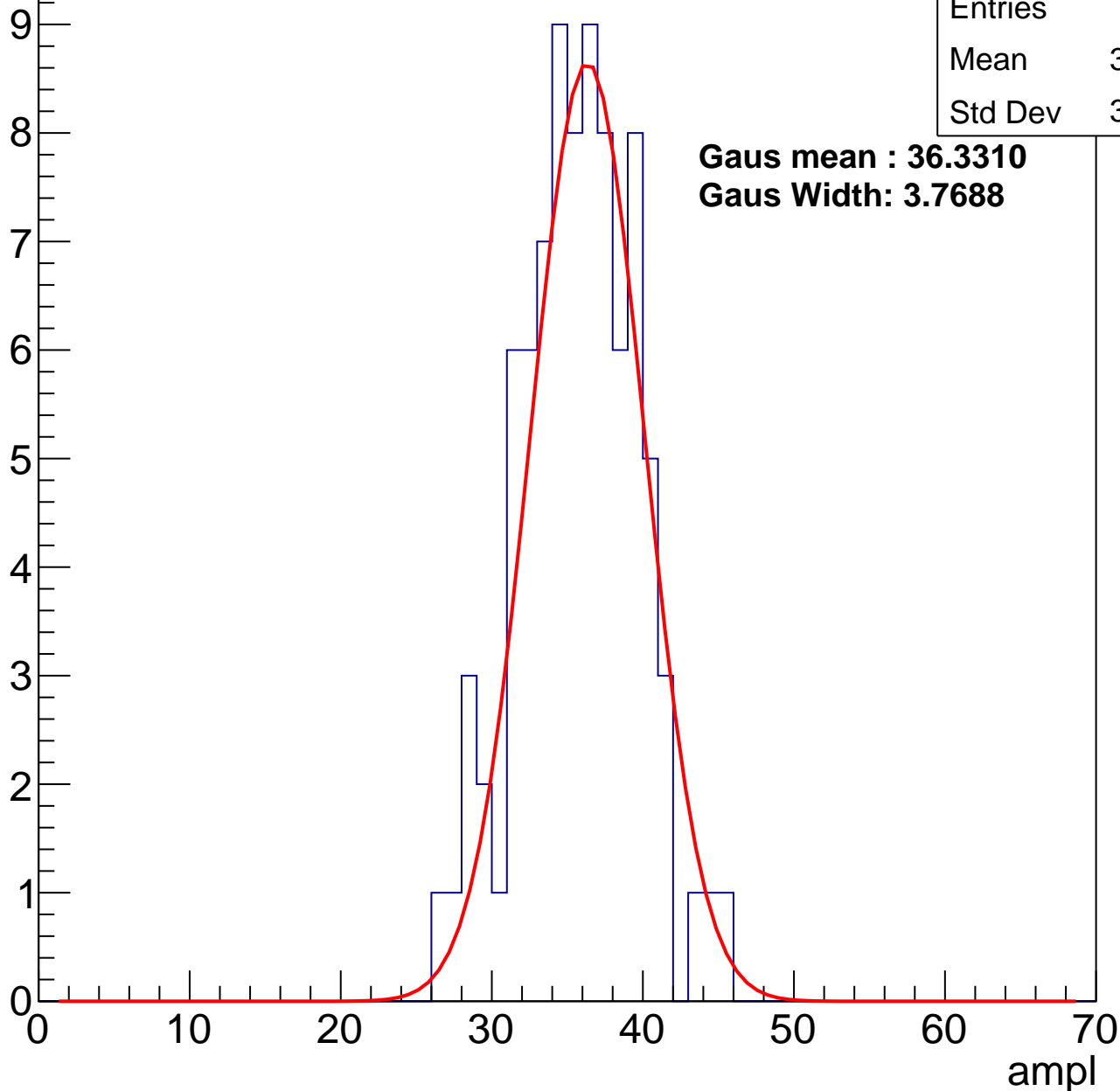
**Gaus Width: 3.3303**



# B0L000S, U26-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch47, adc2

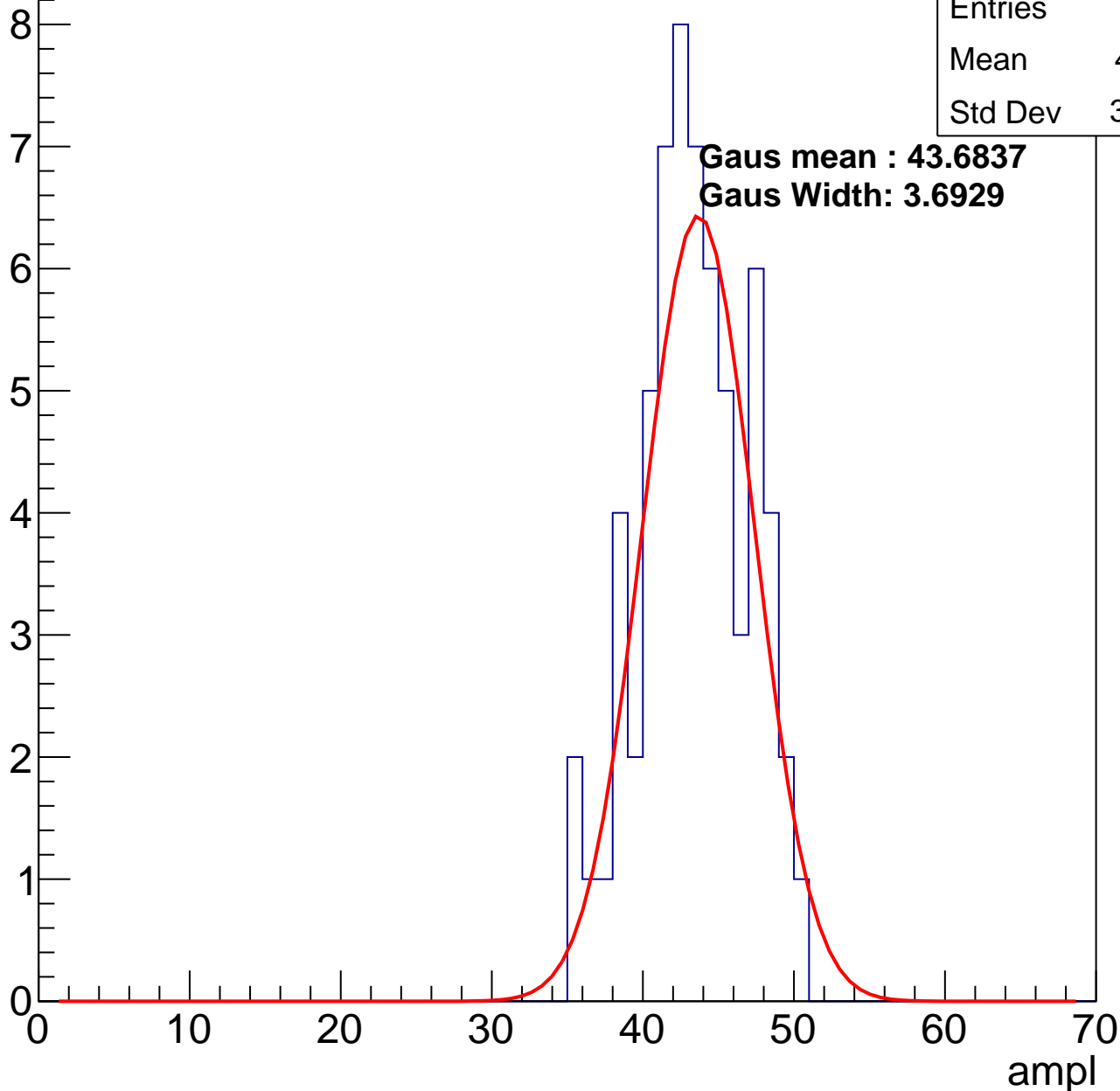
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	42.91
Std Dev	3.525

**Gaus mean : 43.6837**

**Gaus Width: 3.6929**

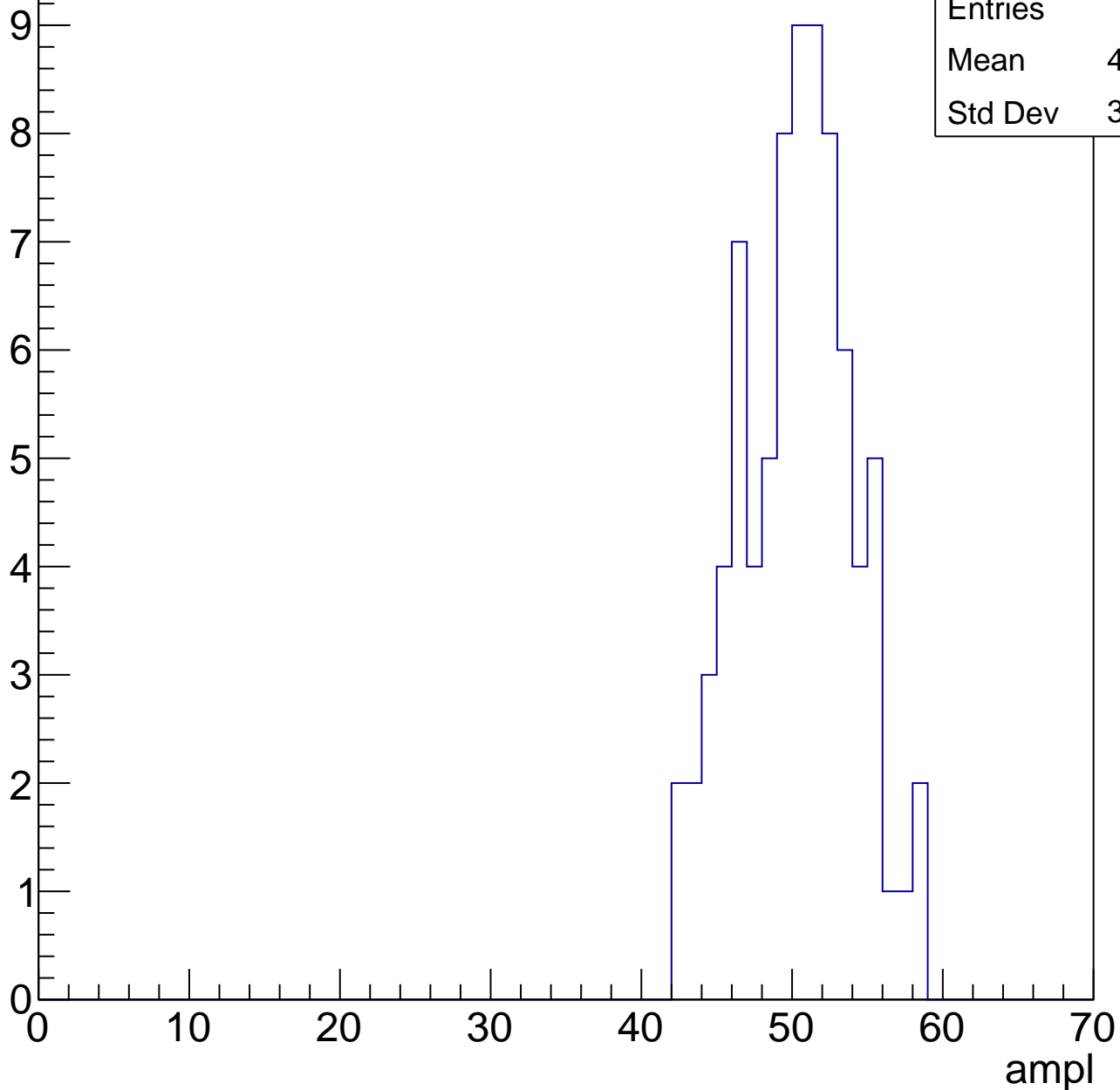


# B0L000S, U26-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	49.84
Std Dev	3.716

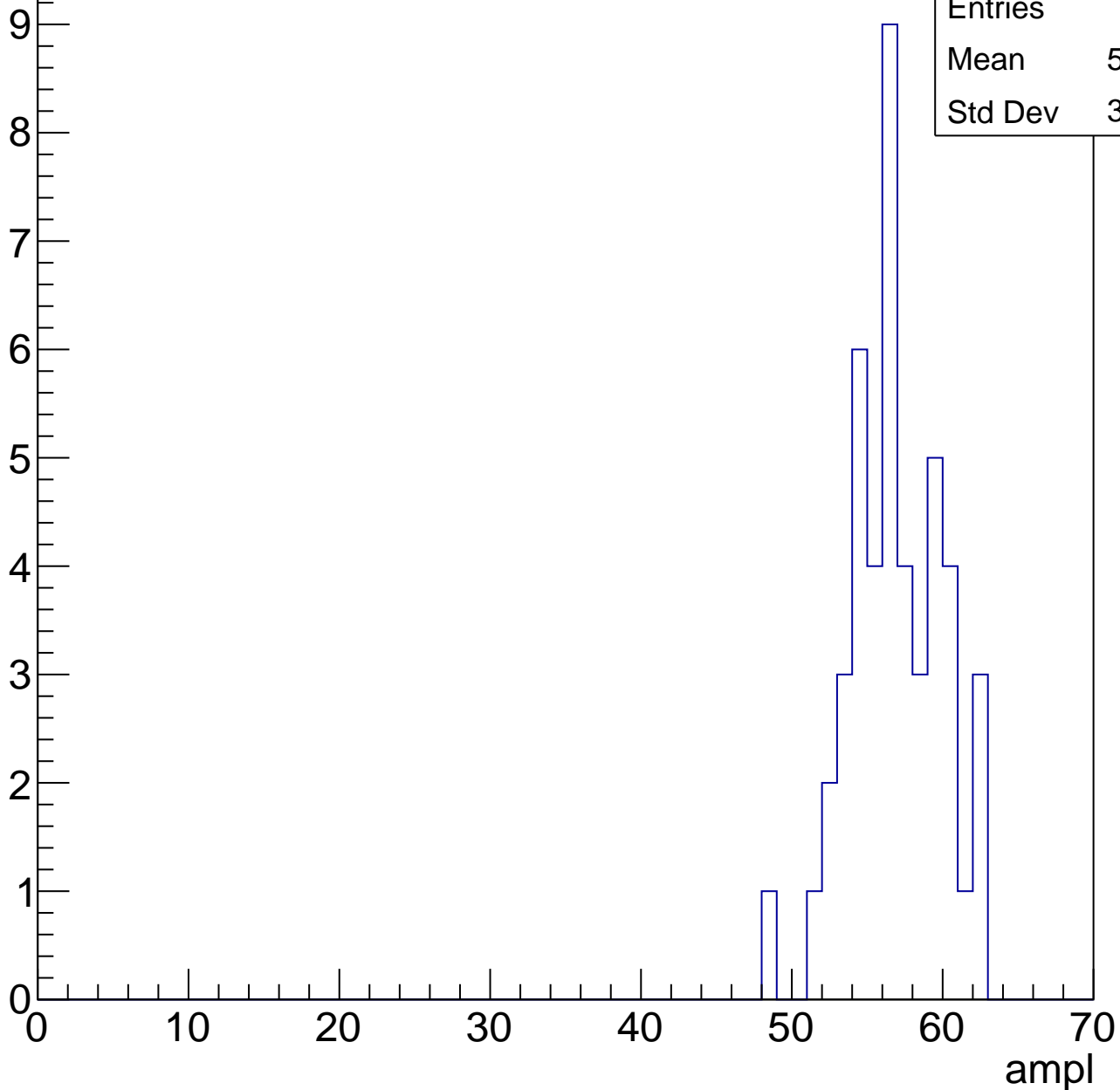


# B0L000S, U26-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	56.39
Std Dev	3.047

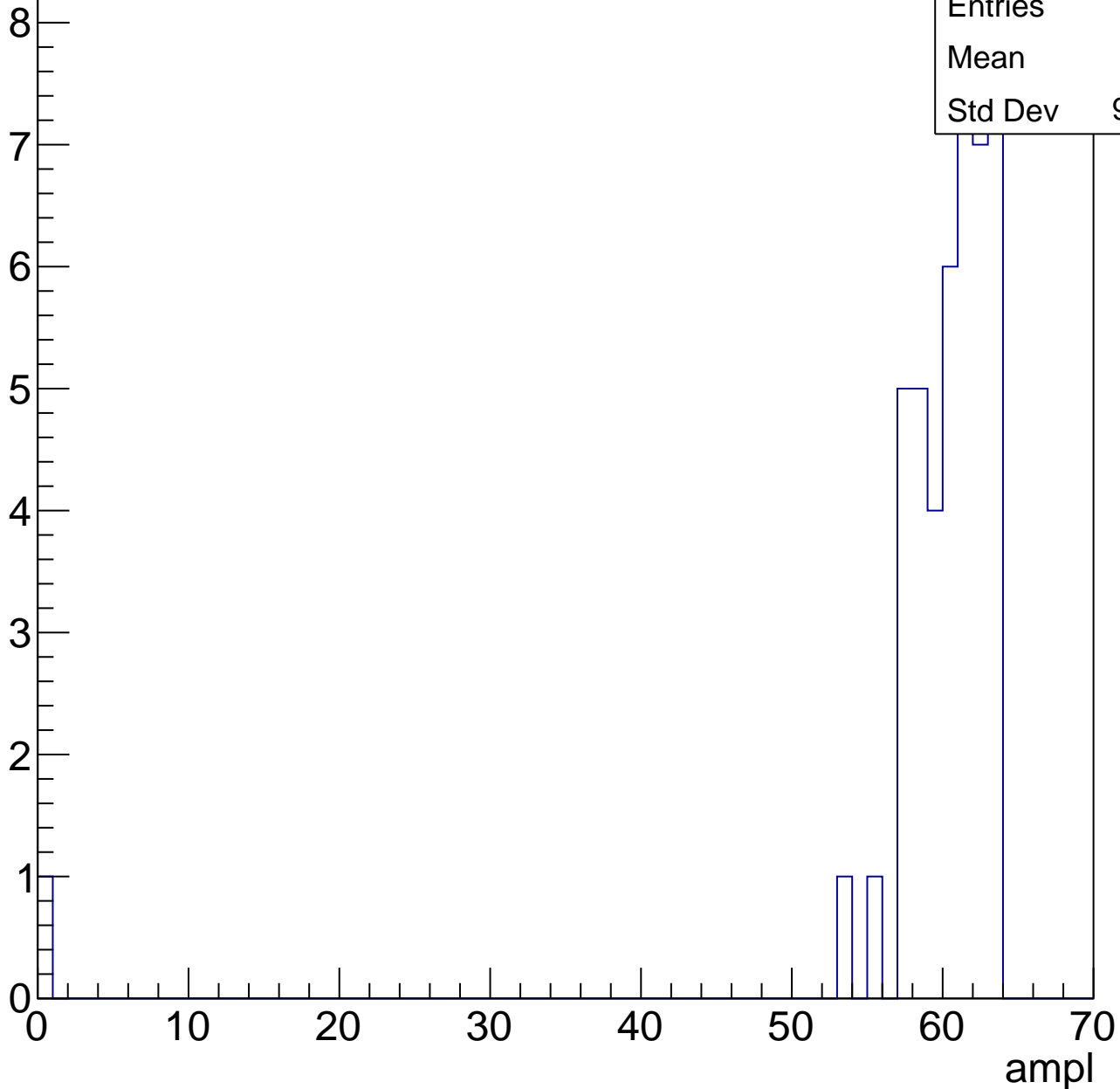


# B0L000S, U26-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

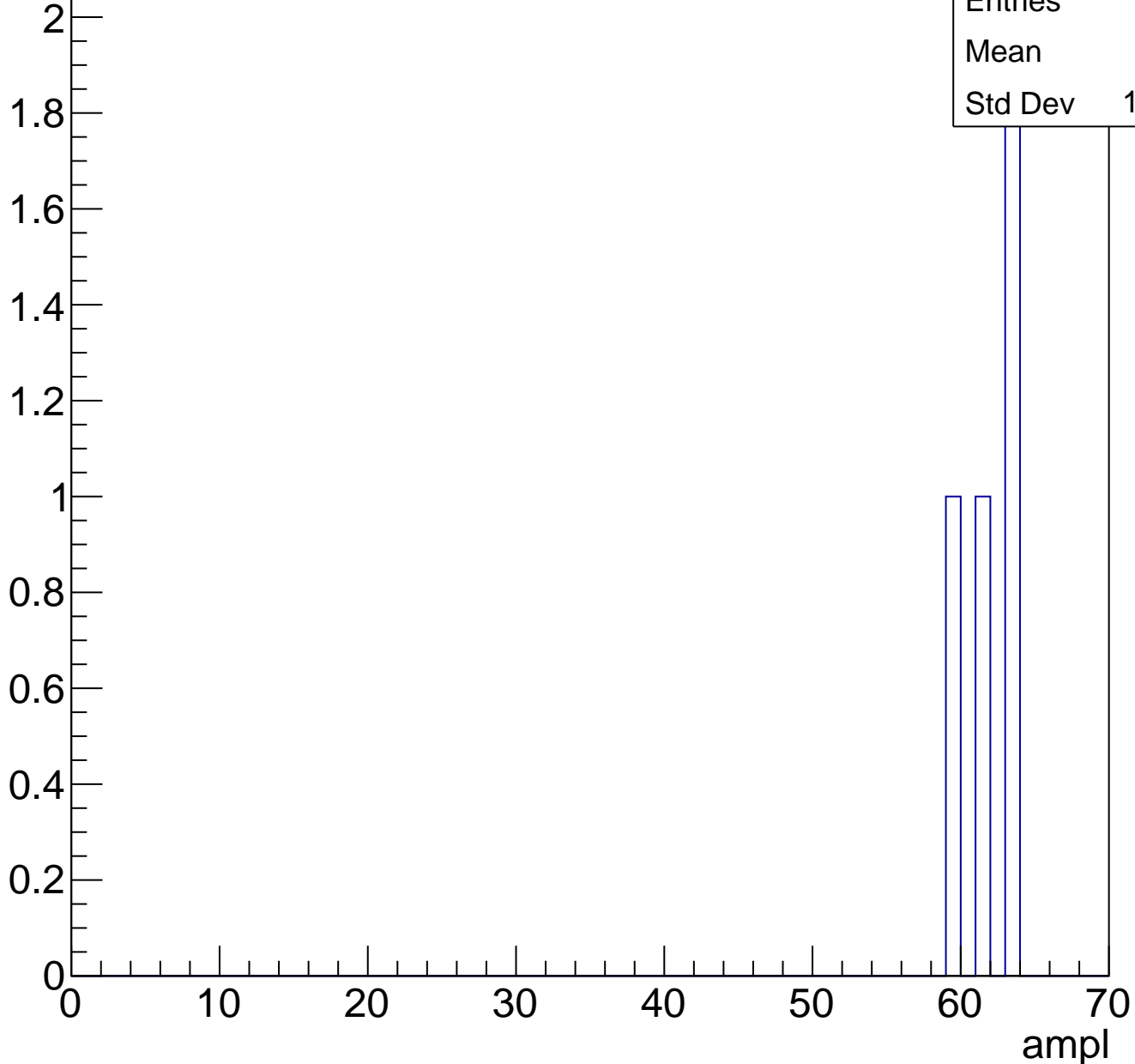
Entries	46
Mean	58.8
Std Dev	9.071



# B0L000S, U26-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



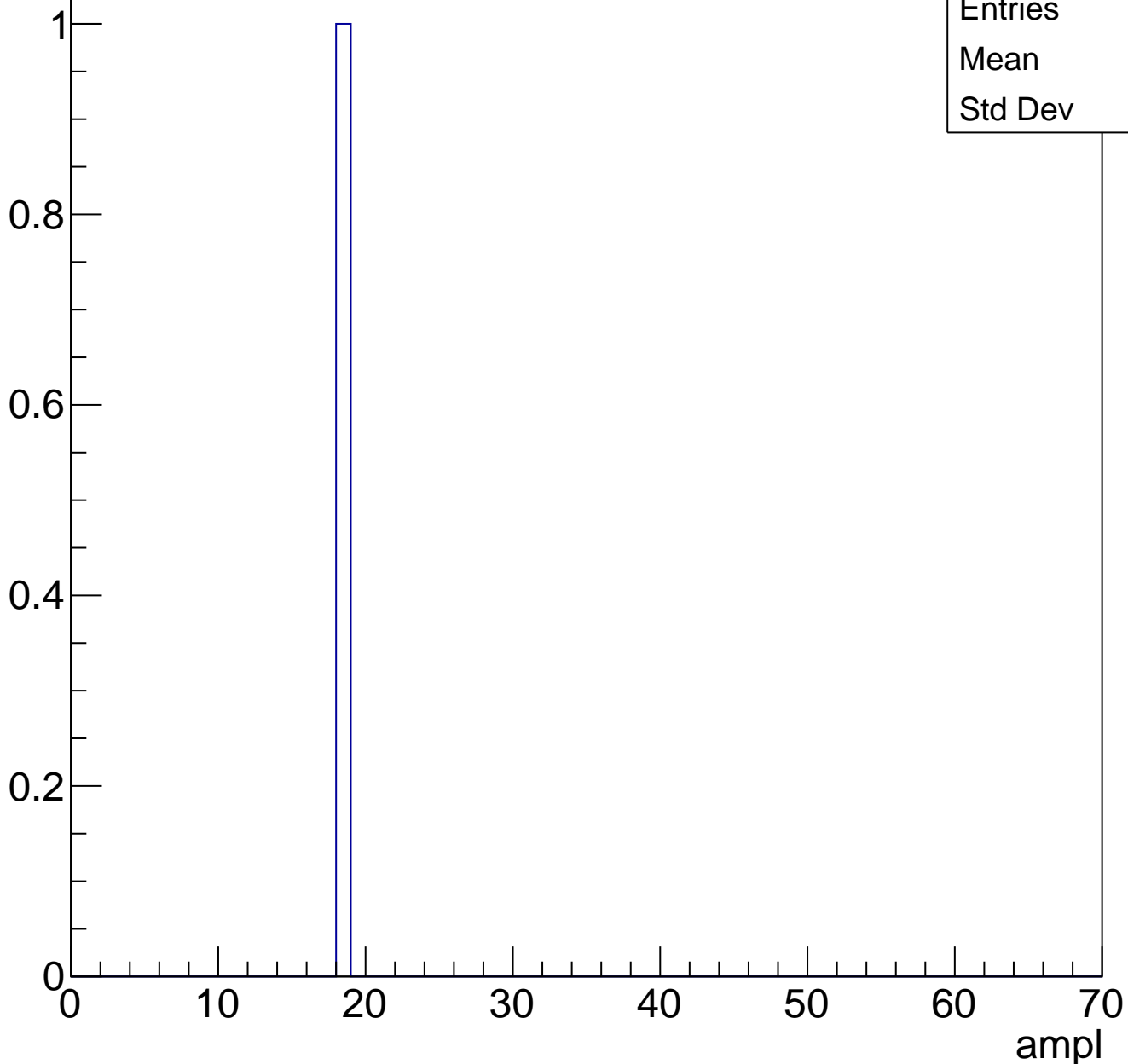
Entries	4
Mean	61.5
Std Dev	1.658



# B0L000S, U26-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	18
Std Dev	0

# B0L000S, U26-ch48, adc0

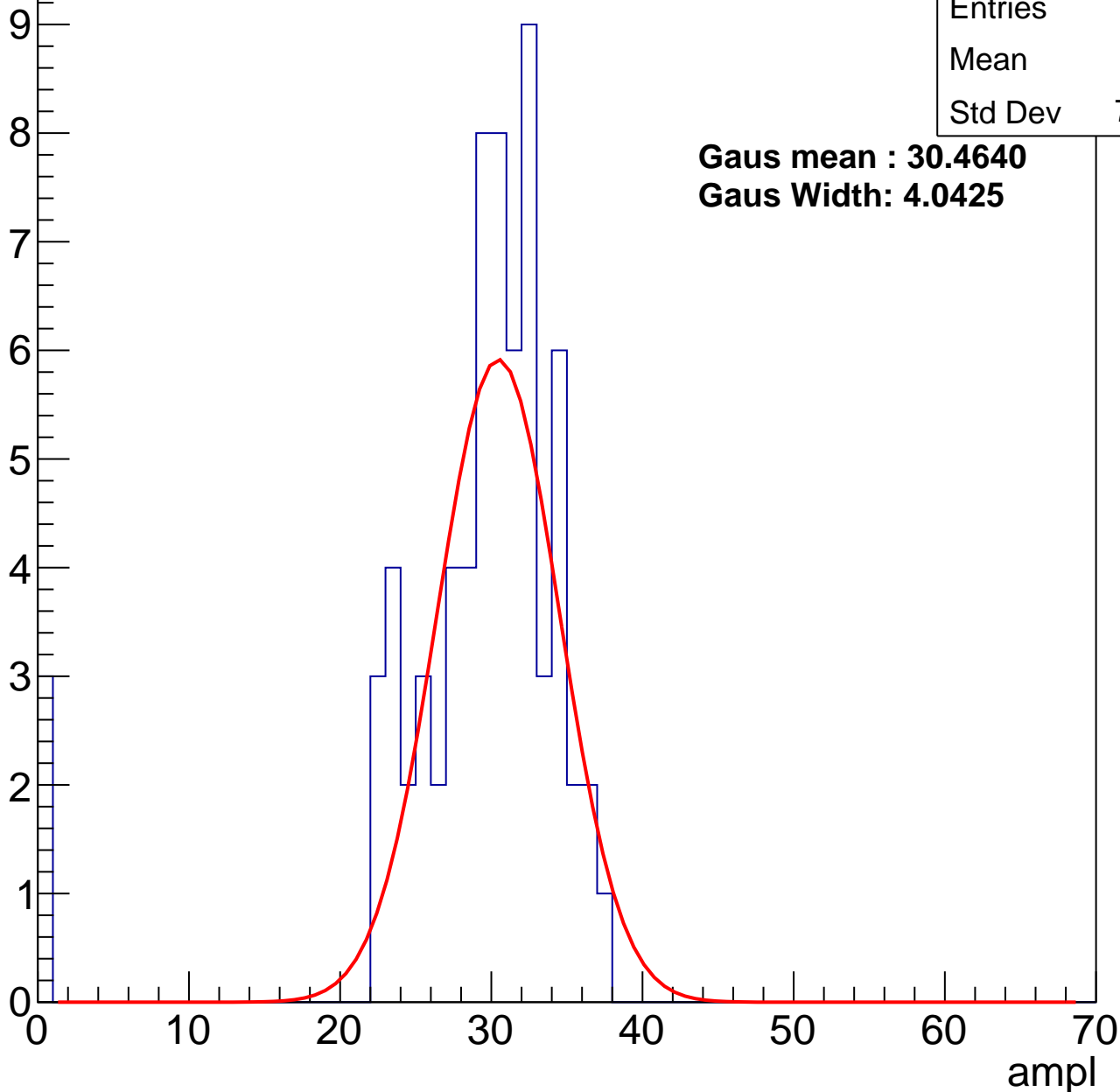
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	28.3
Std Dev	7.031

**Gaus mean : 30.4640**

**Gaus Width: 4.0425**



# B0L000S, U26-ch48, adc1

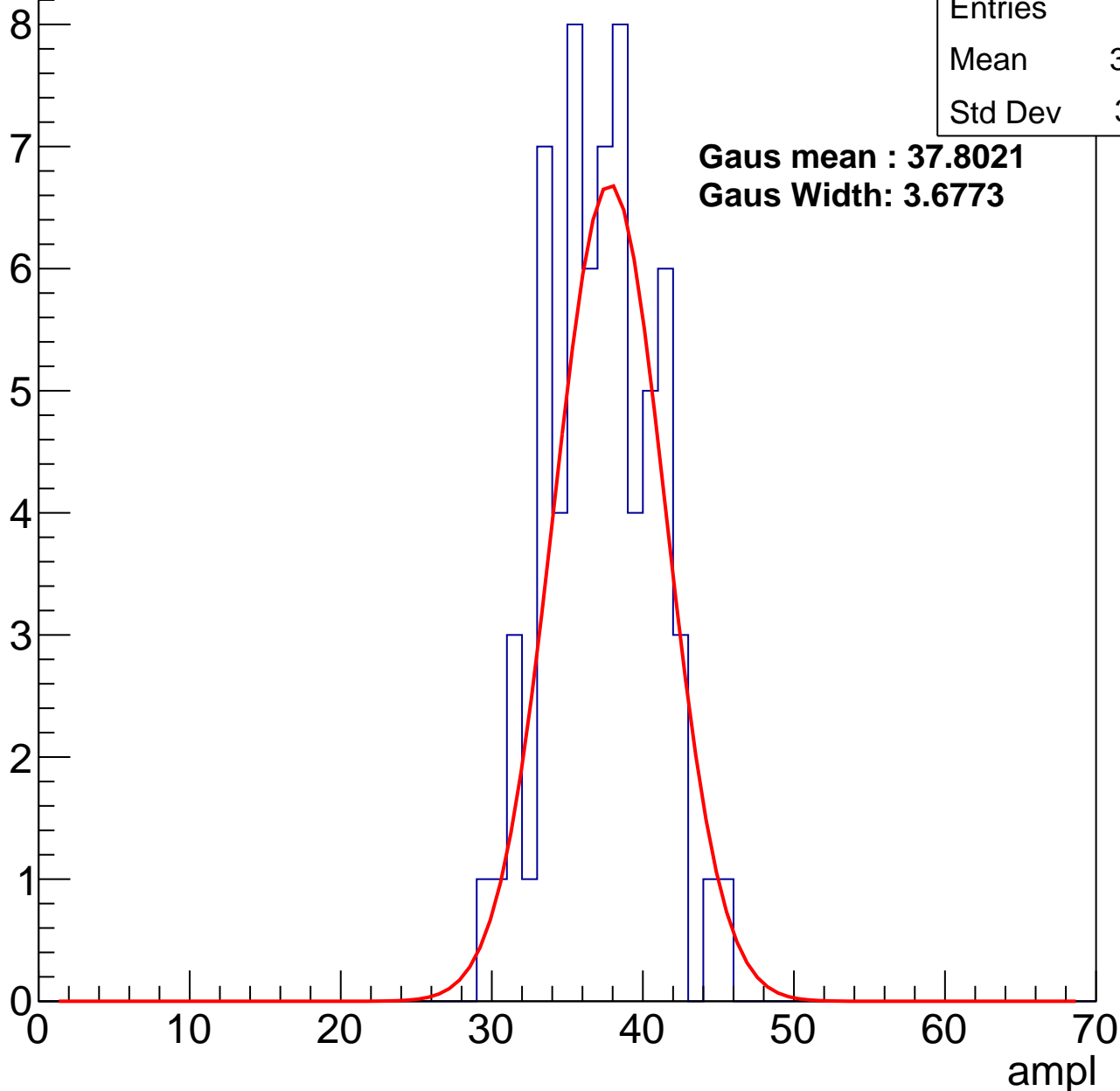
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.77
Std Dev	3.441

**Gaus mean : 37.8021**

**Gaus Width: 3.6773**



# B0L000S, U26-ch48, adc2

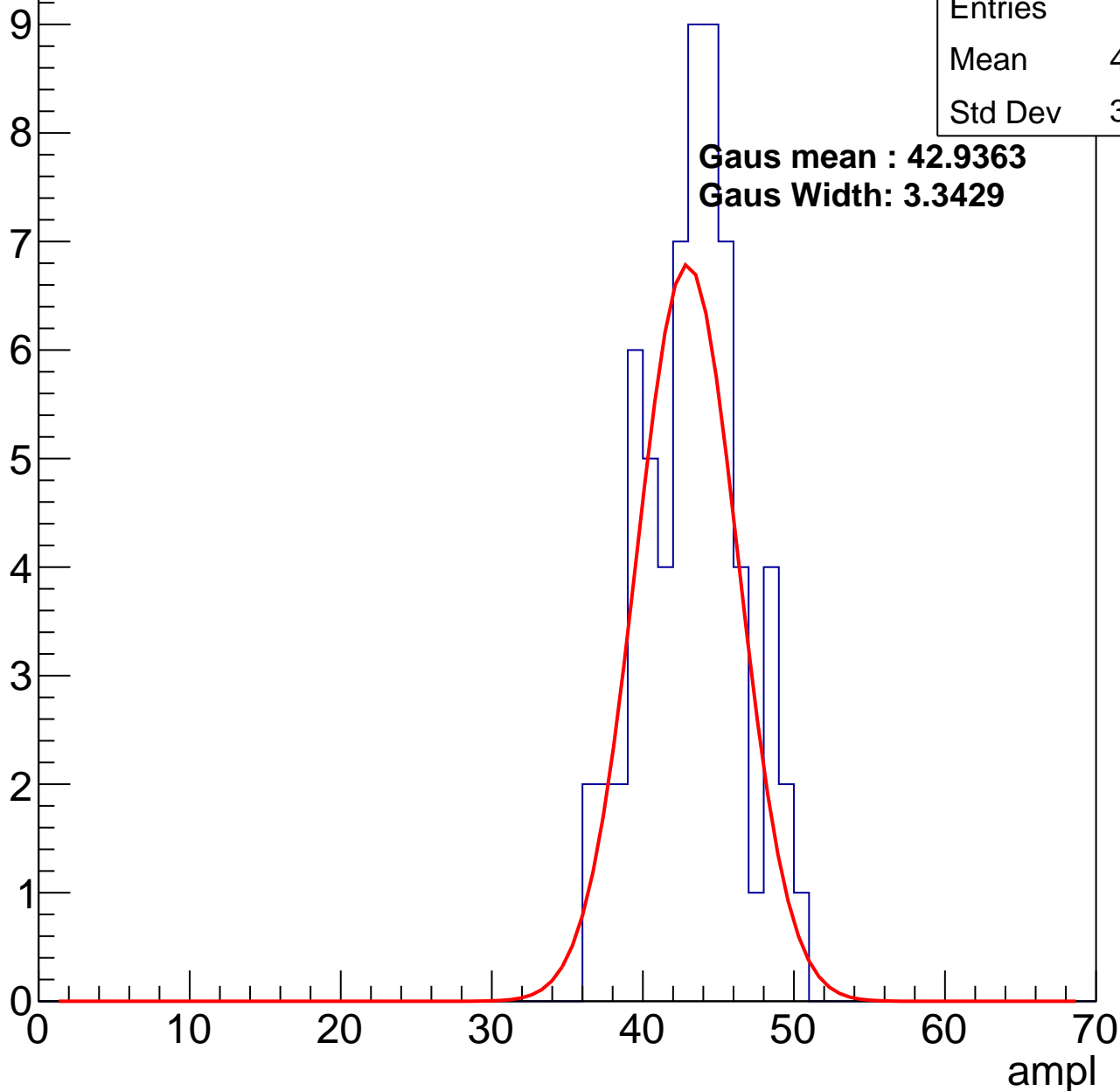
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.82
Std Dev	3.262

**Gaus mean : 42.9363**

**Gaus Width: 3.3429**

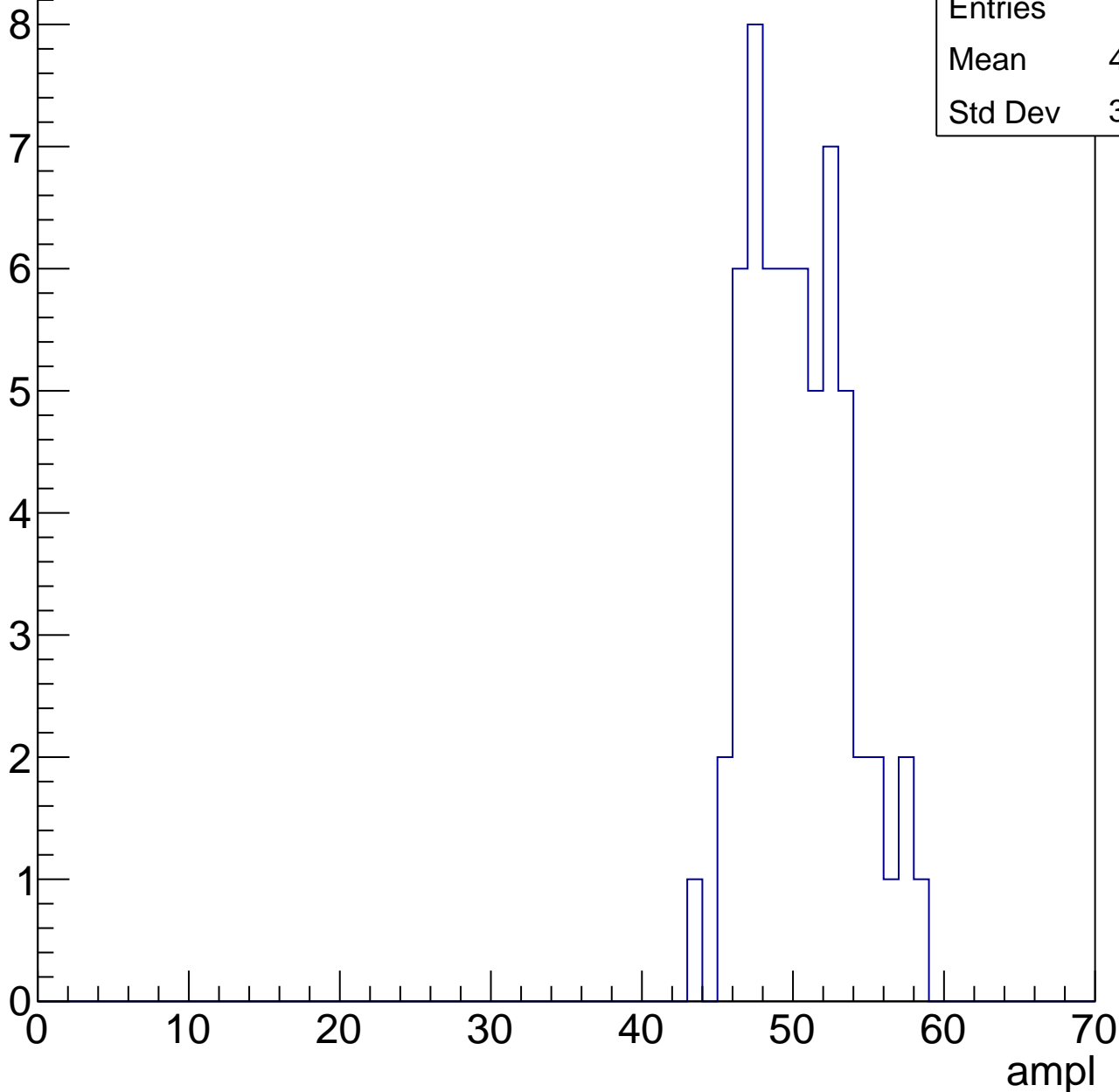


# B0L000S, U26-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	49.95
Std Dev	3.304



# B0L000S, U26-ch48, adc4

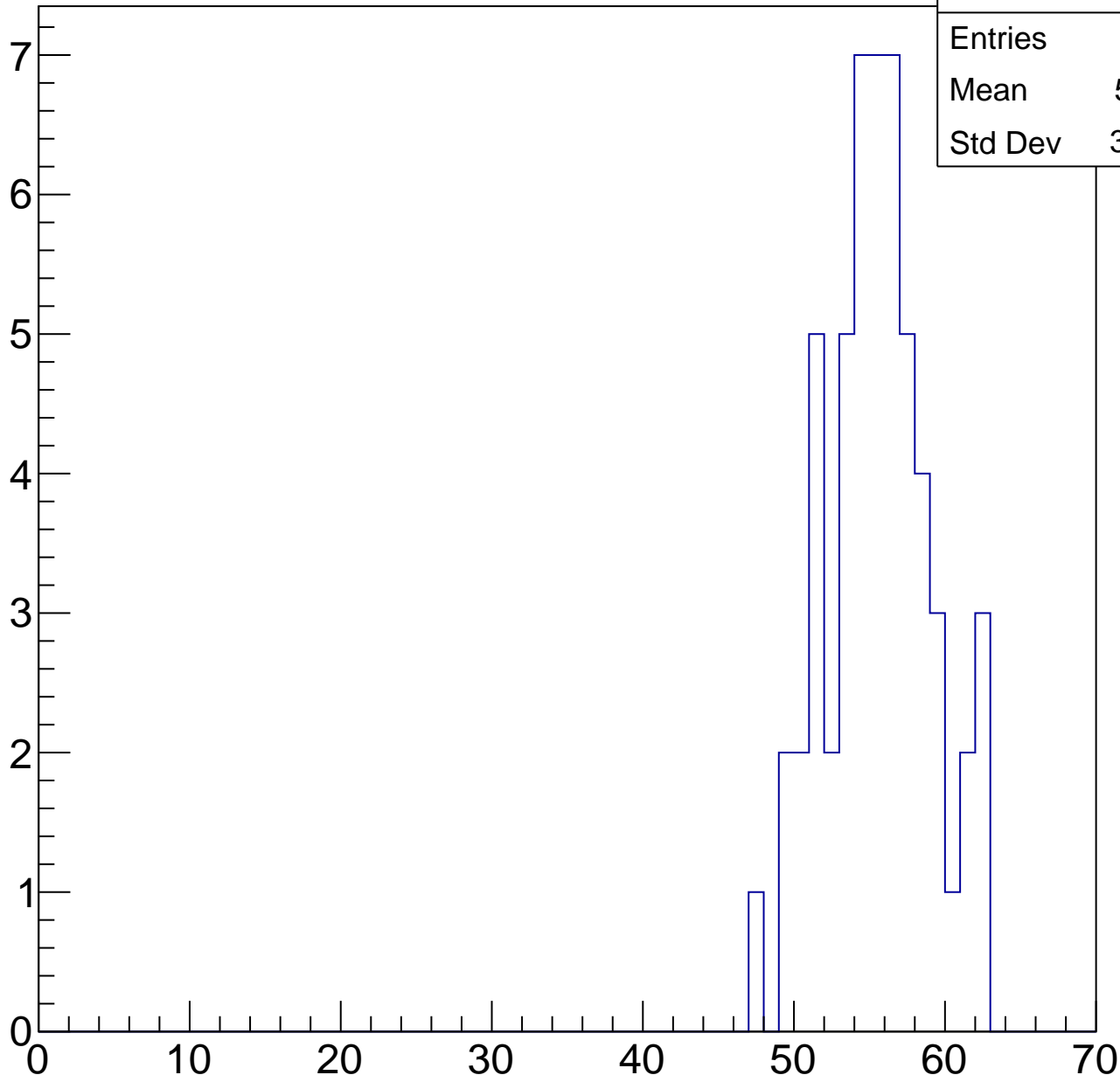
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	55.11
Std Dev	3.447

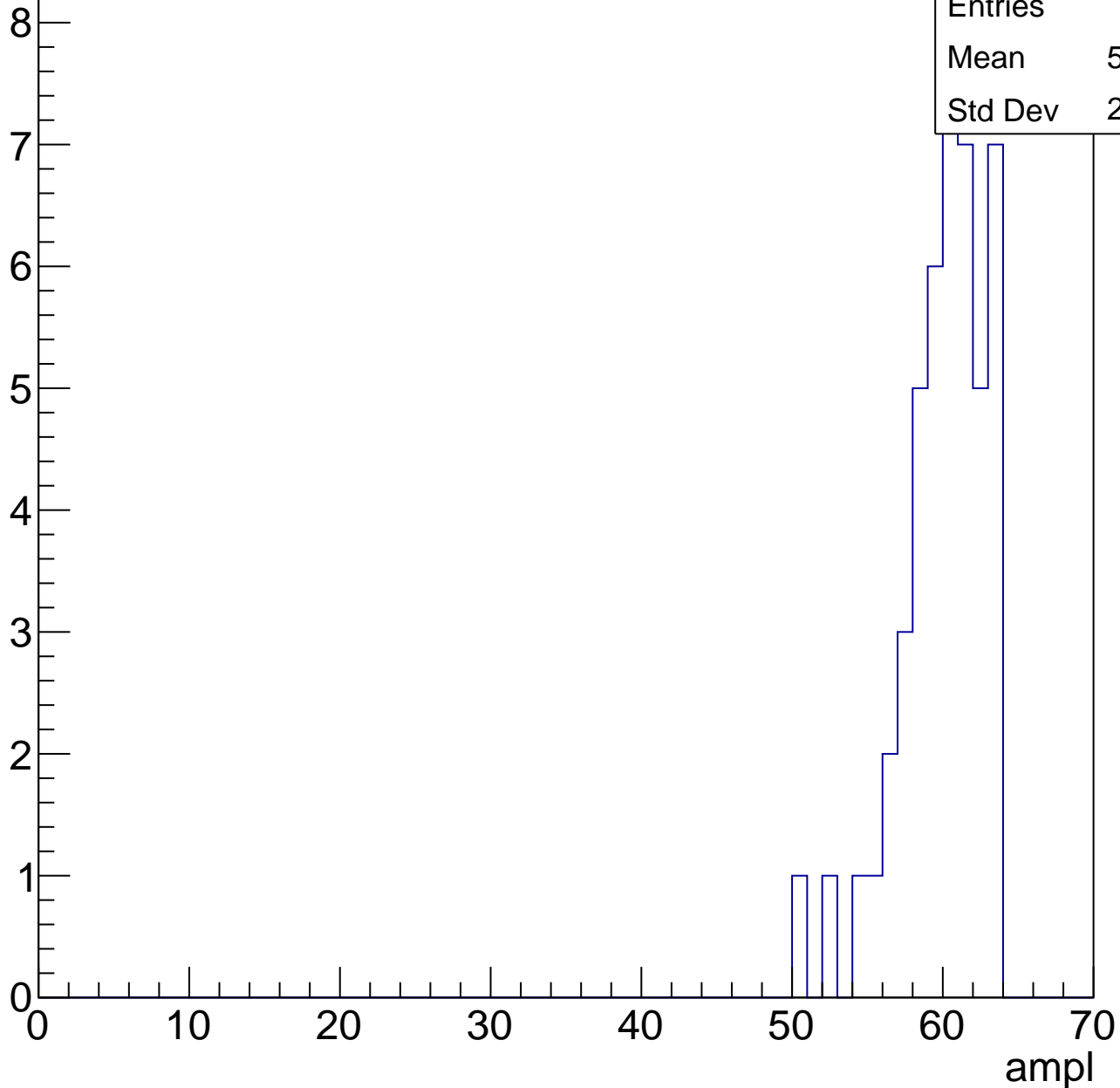
ampl



# B0L000S, U26-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

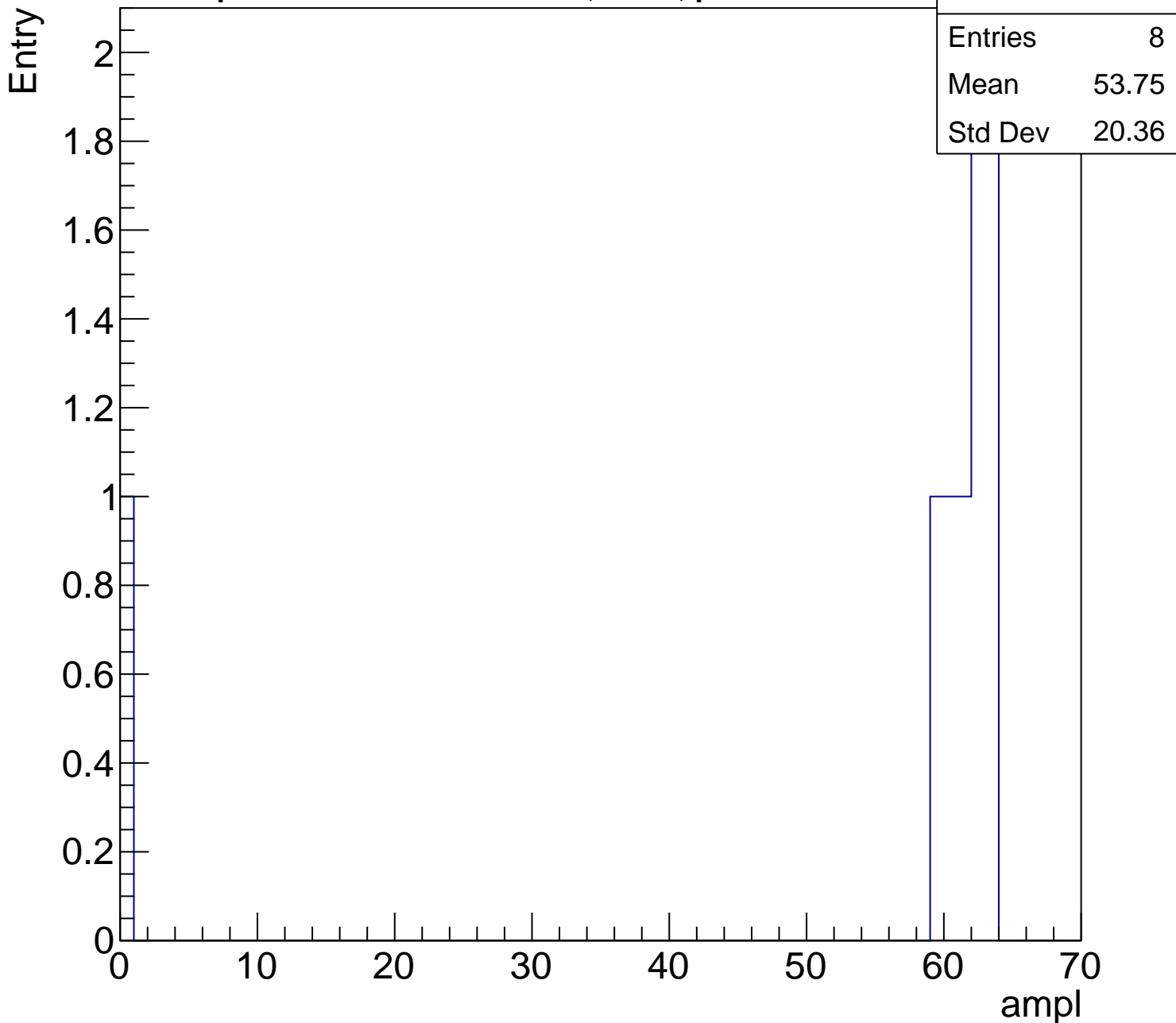
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.75
Std Dev	20.36

0 10 20 30 40 50 60 70

ampl





# B0L000S, U26-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch49, adc0

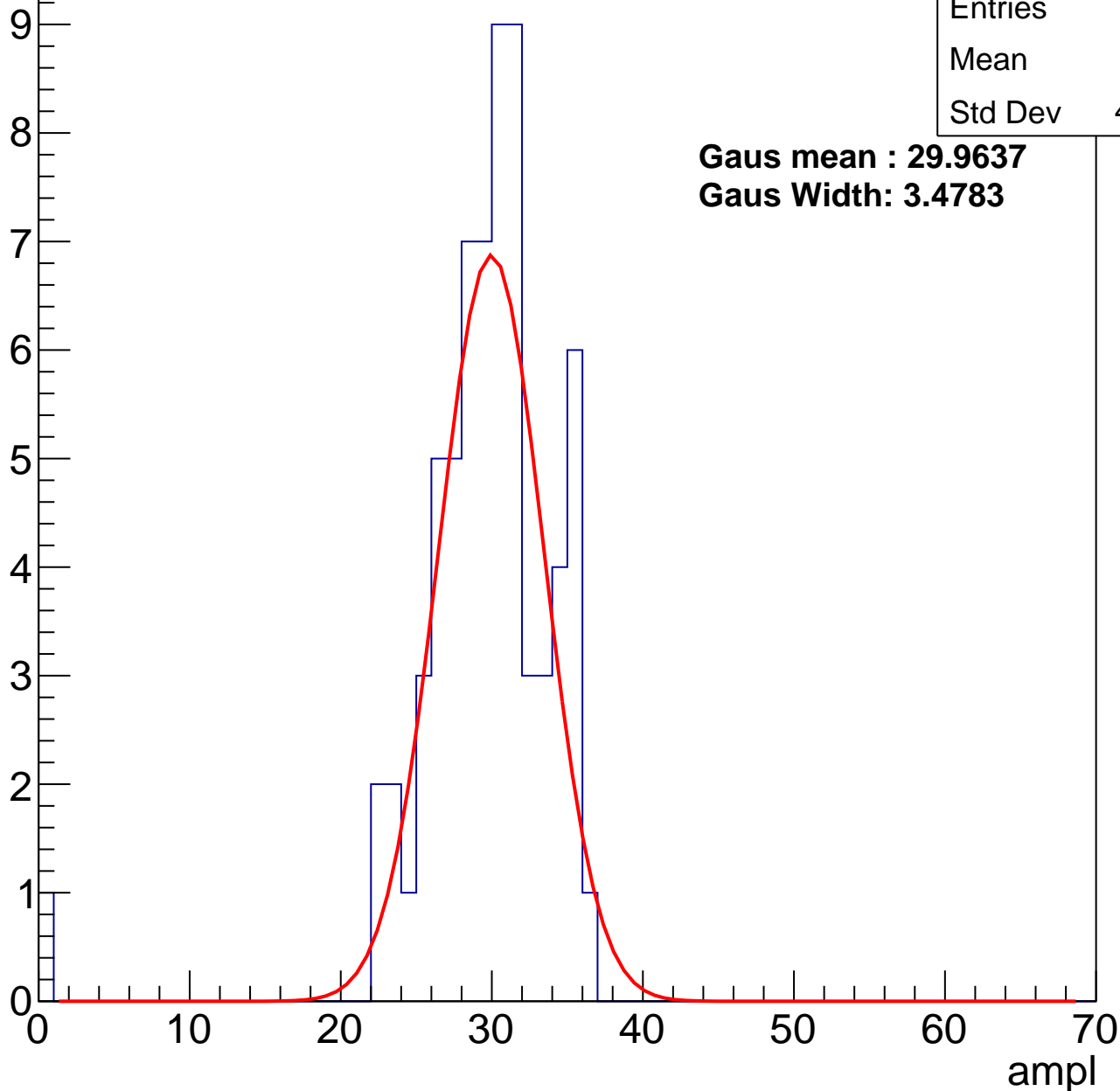
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	29.1
Std Dev	4.911

**Gaus mean : 29.9637**

**Gaus Width: 3.4783**



# B0L000S, U26-ch49, adc1

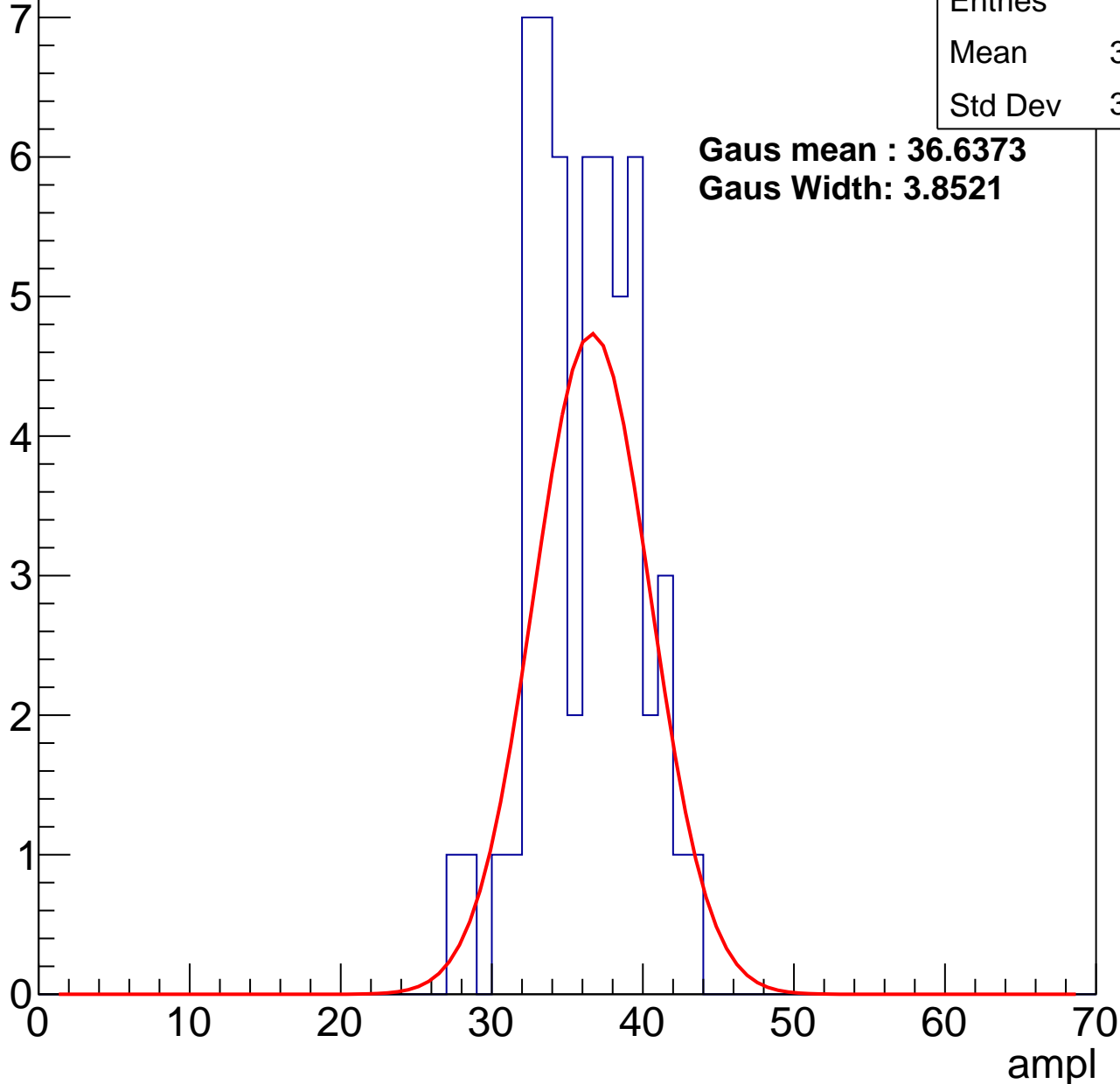
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	35.62
Std Dev	3.462

**Gaus mean : 36.6373**

**Gaus Width: 3.8521**



# B0L000S, U26-ch49, adc2

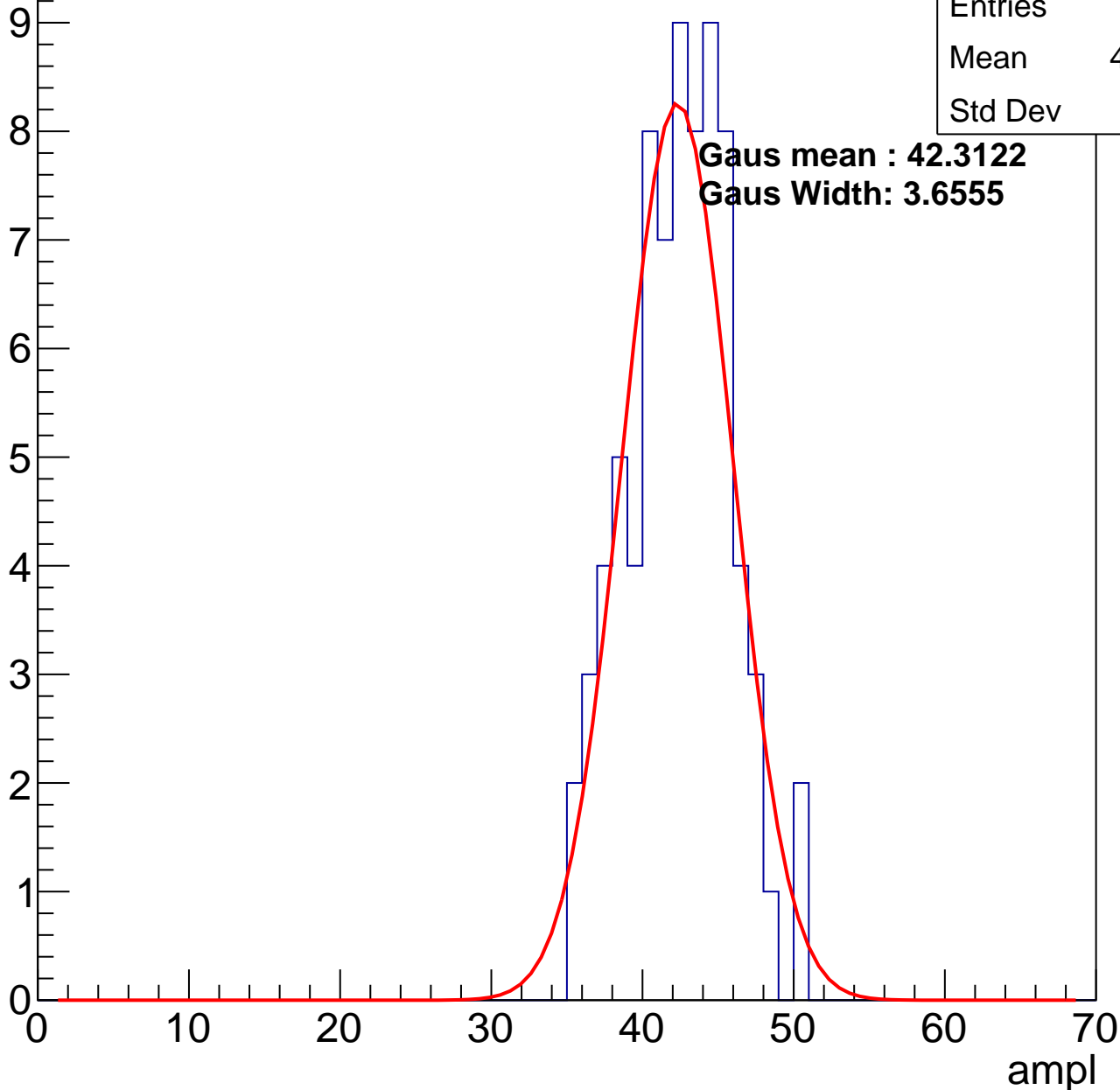
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	41.95
Std Dev	3.38

**Gaus mean : 42.3122**

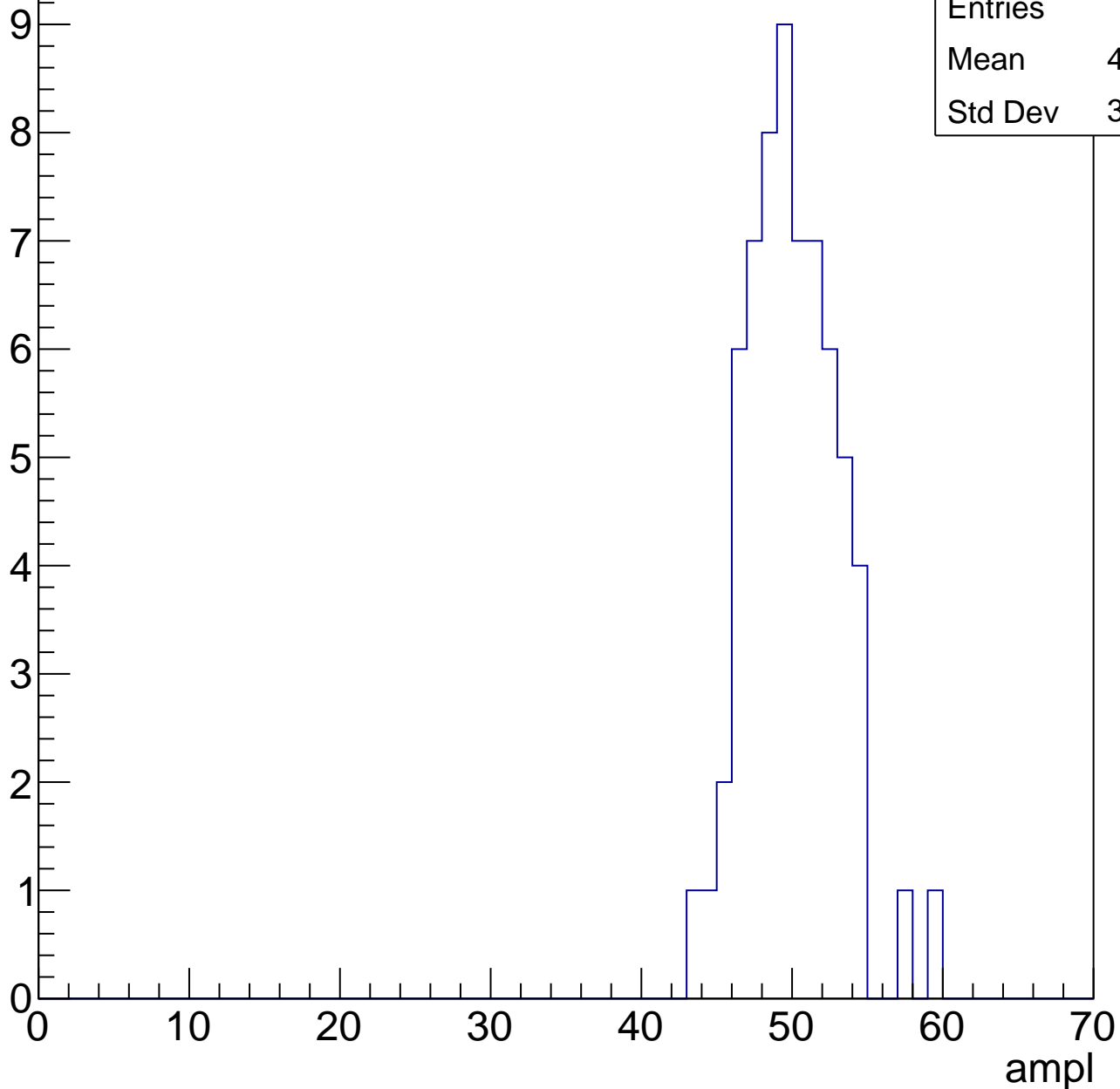
**Gaus Width: 3.6555**



# B0L000S, U26-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

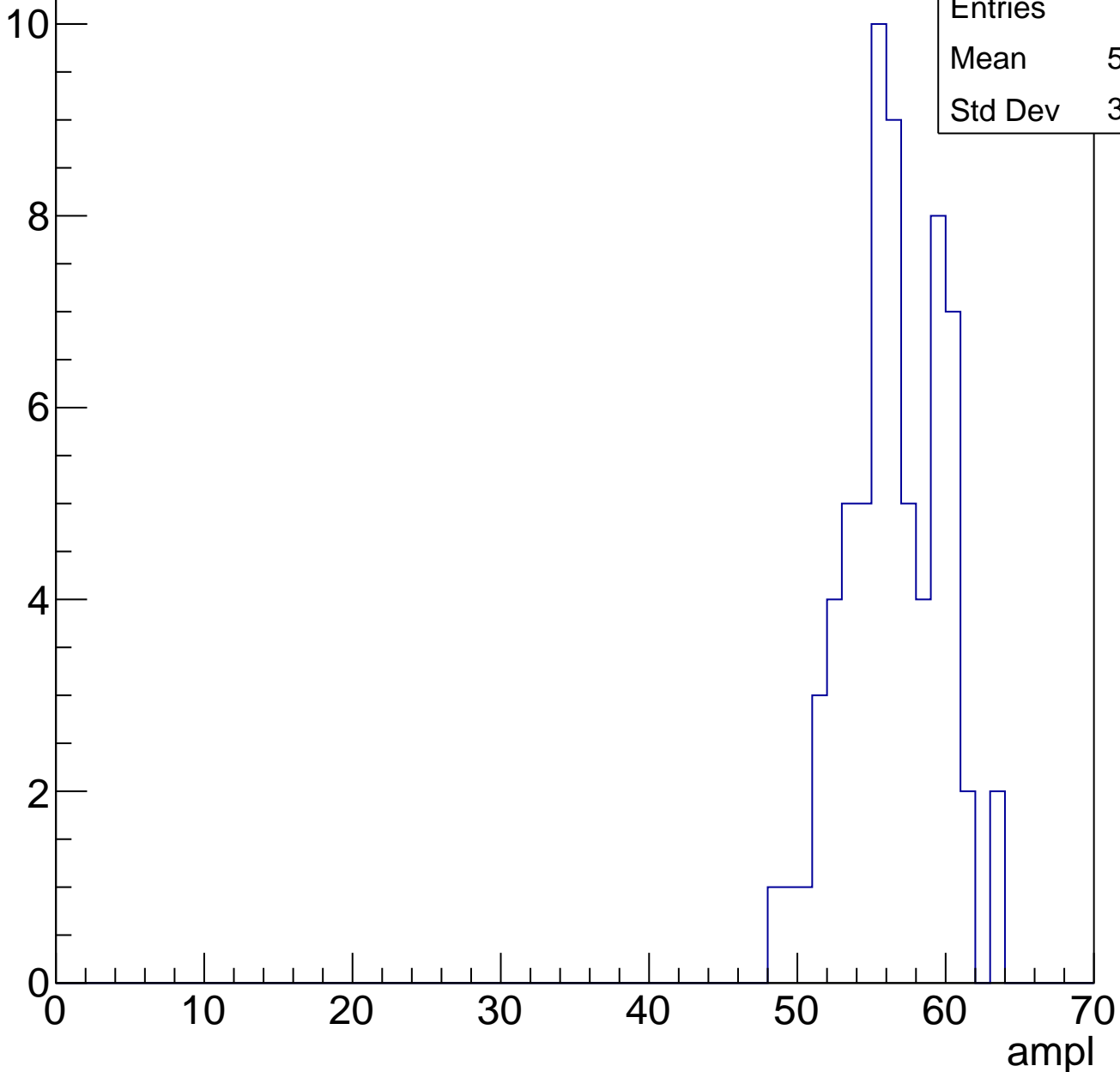


# B0L000S, U26-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	56.03
Std Dev	3.269

Entry

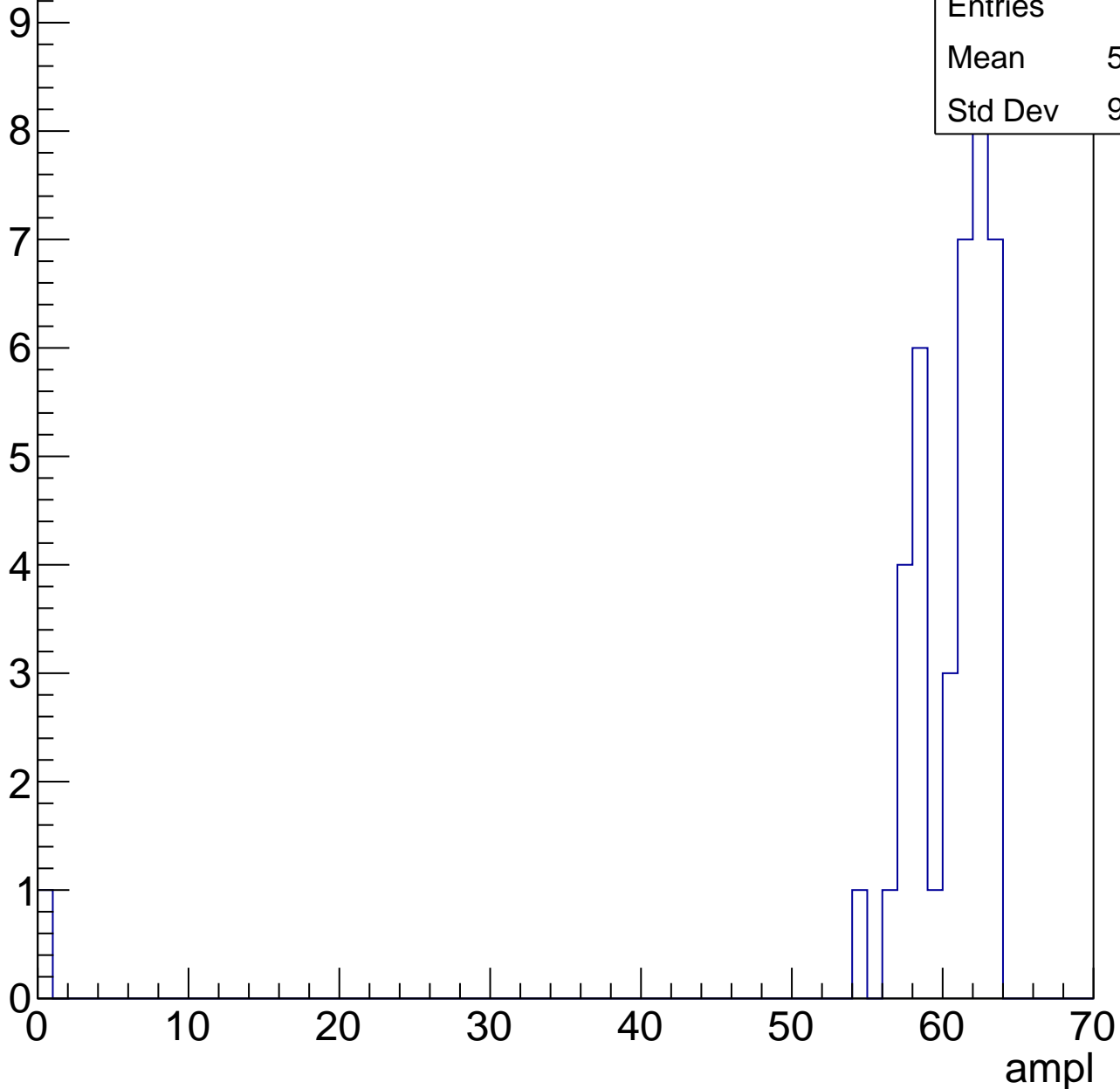


# B0L000S, U26-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.77
Std Dev	9.694



# B0L000S, U26-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	30.58
Std Dev	3.408

**Gaus mean : 31.4862**

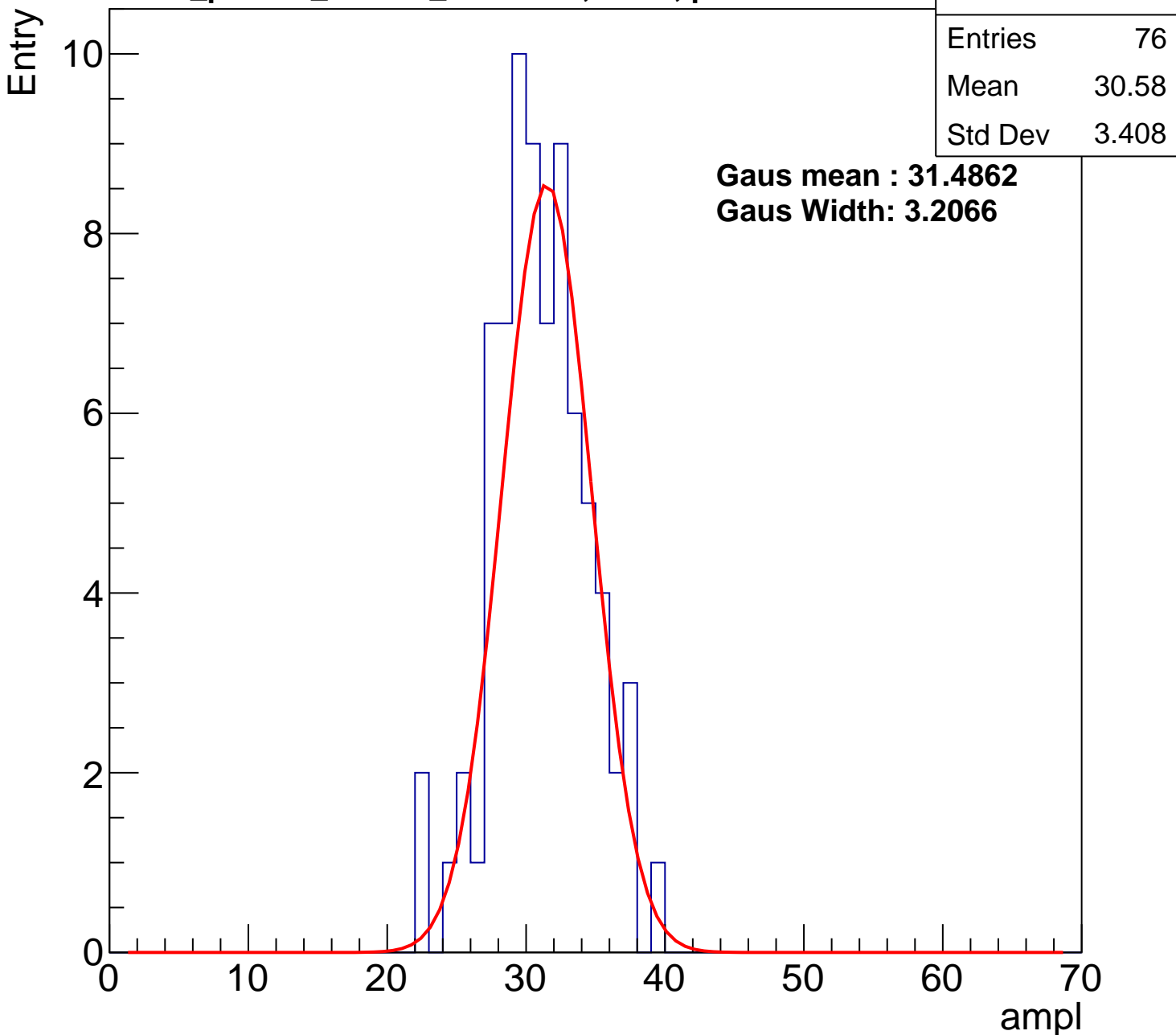
**Gaus Width: 3.2066**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

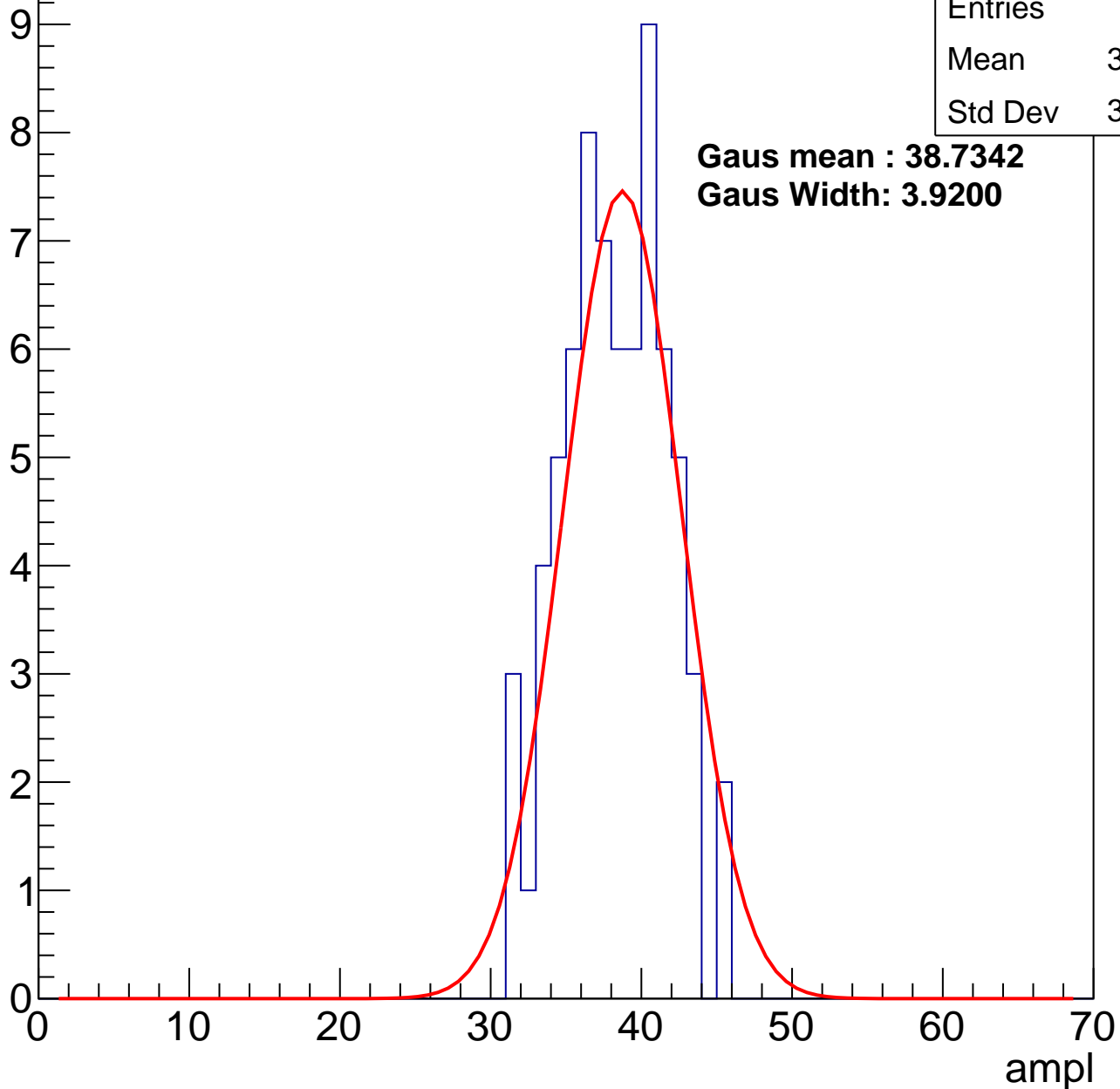
ampl



# B0L000S, U26-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch50, adc2

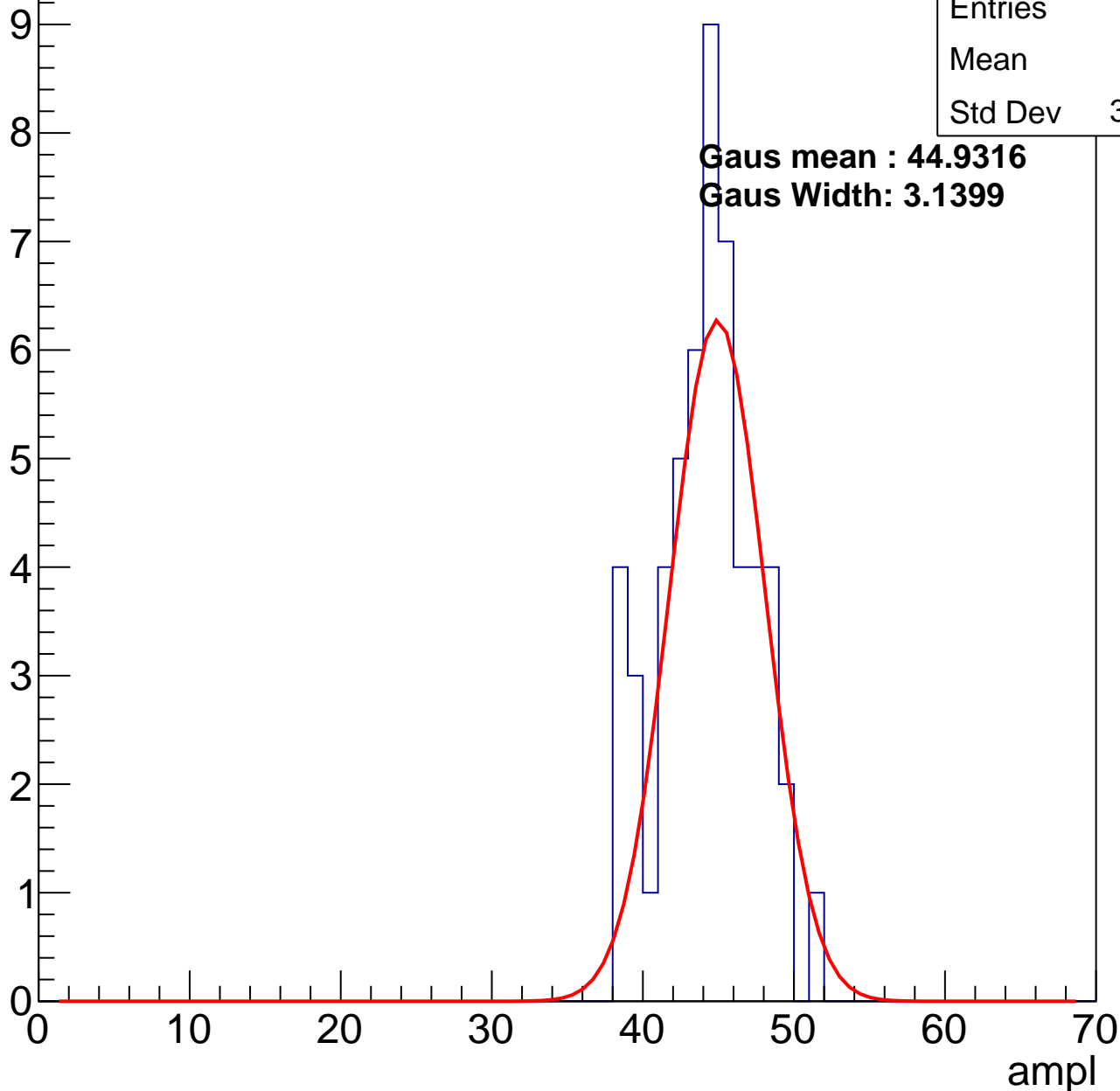
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	43.8
Std Dev	3.093

**Gaus mean : 44.9316**

**Gaus Width: 3.1399**

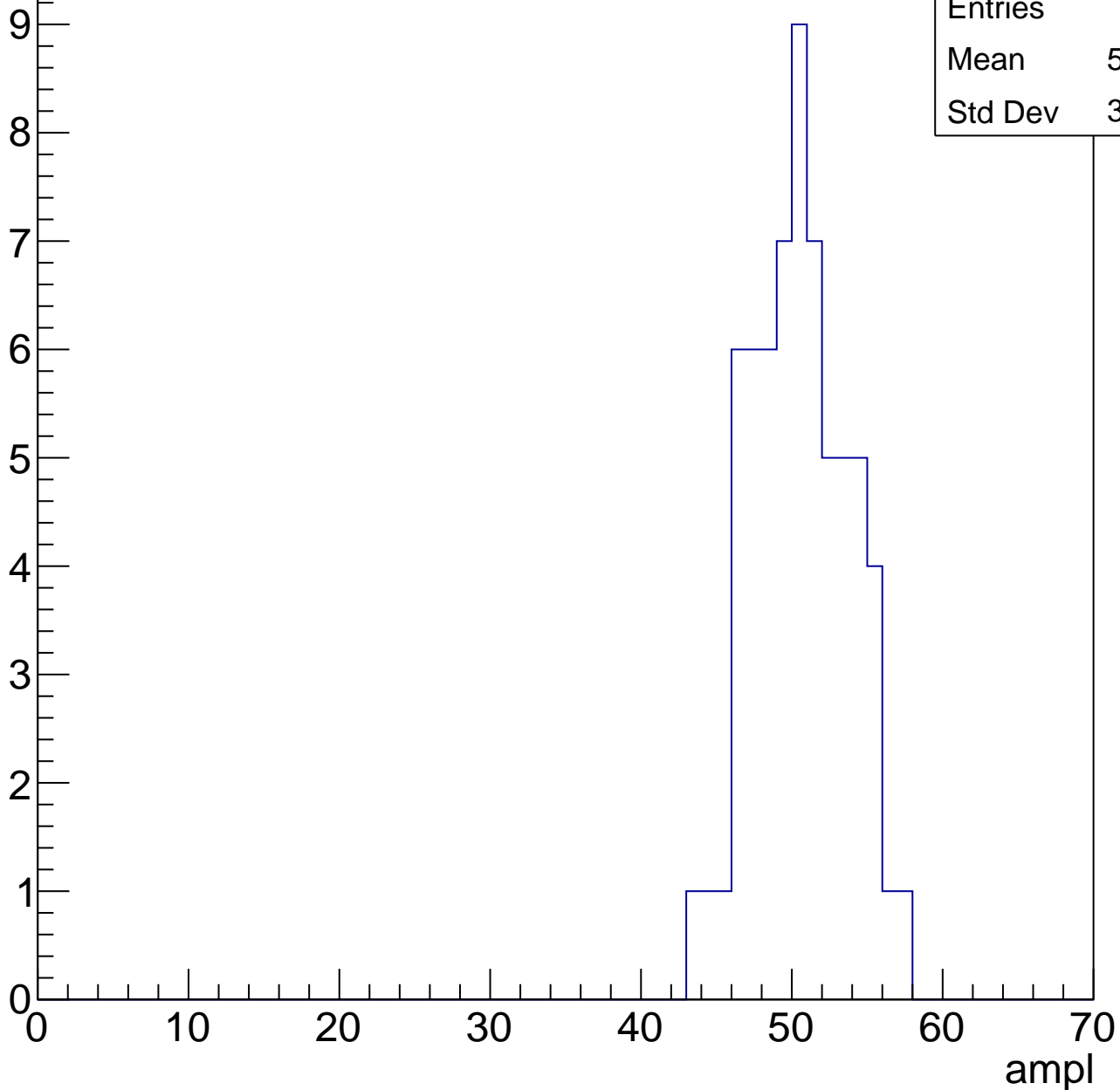


# B0L000S, U26-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

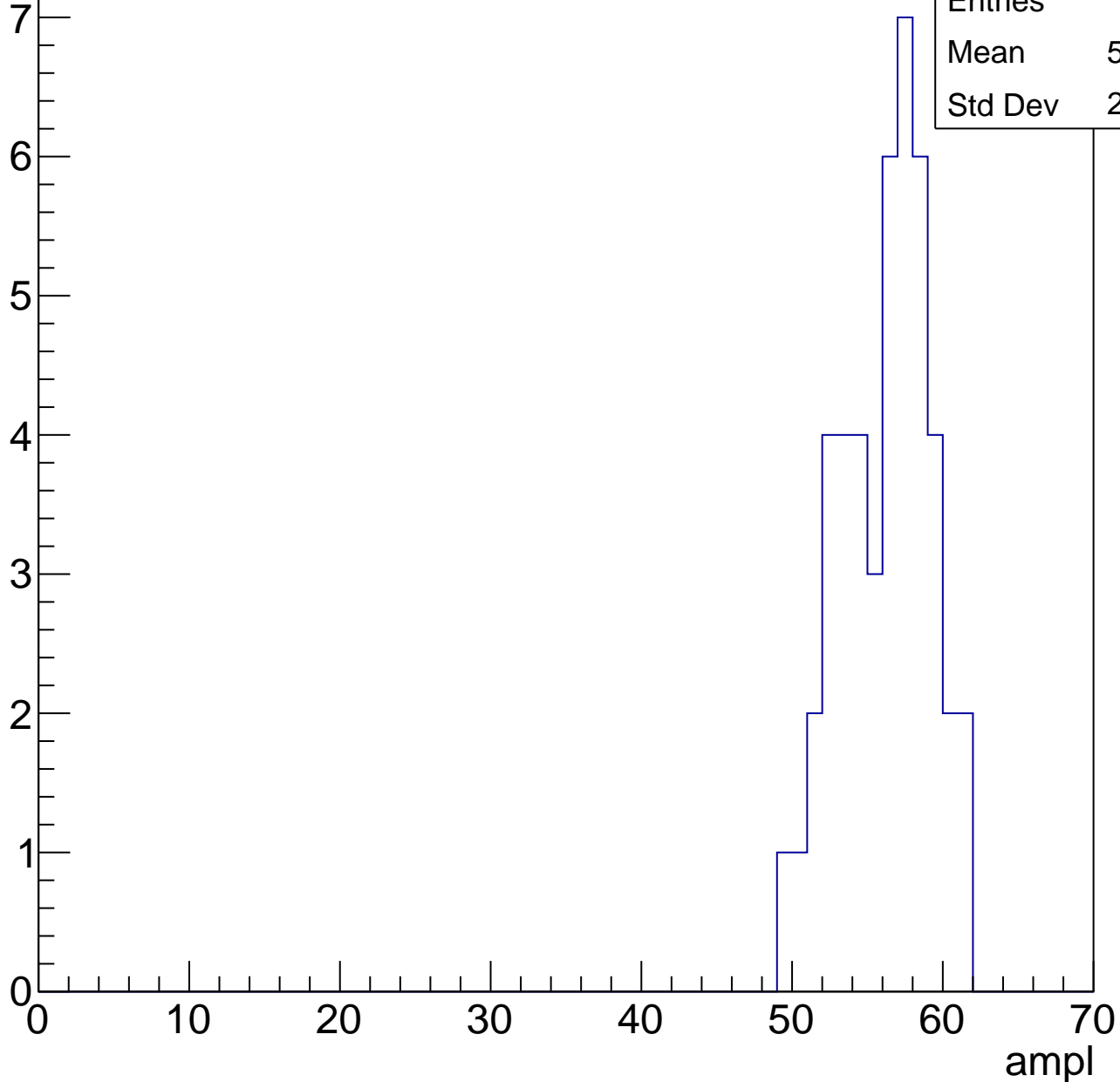
Entries	65
Mean	50.09
Std Dev	3.107



# B0L000S, U26-ch50, adc4

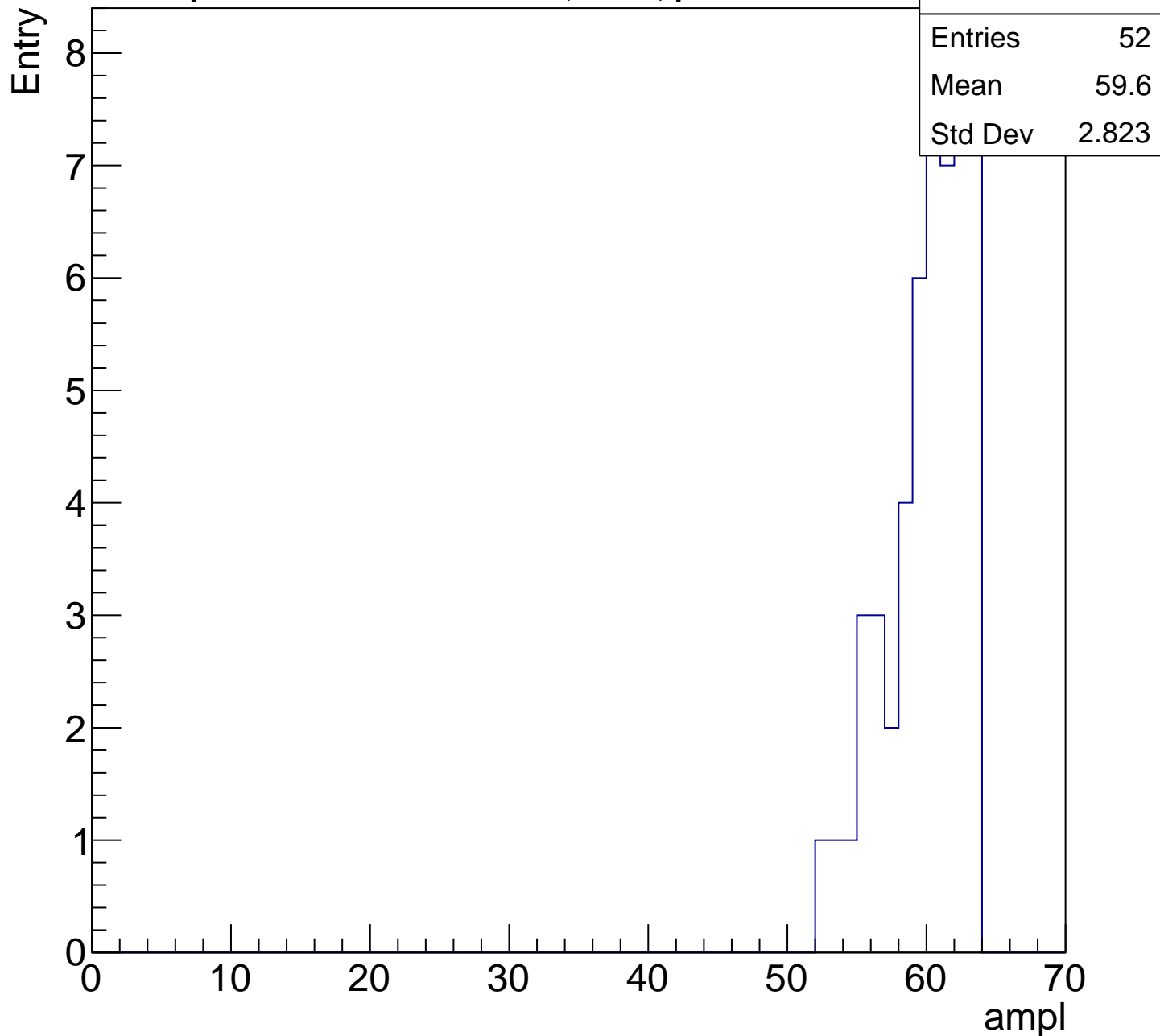
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U26-ch50, adc6

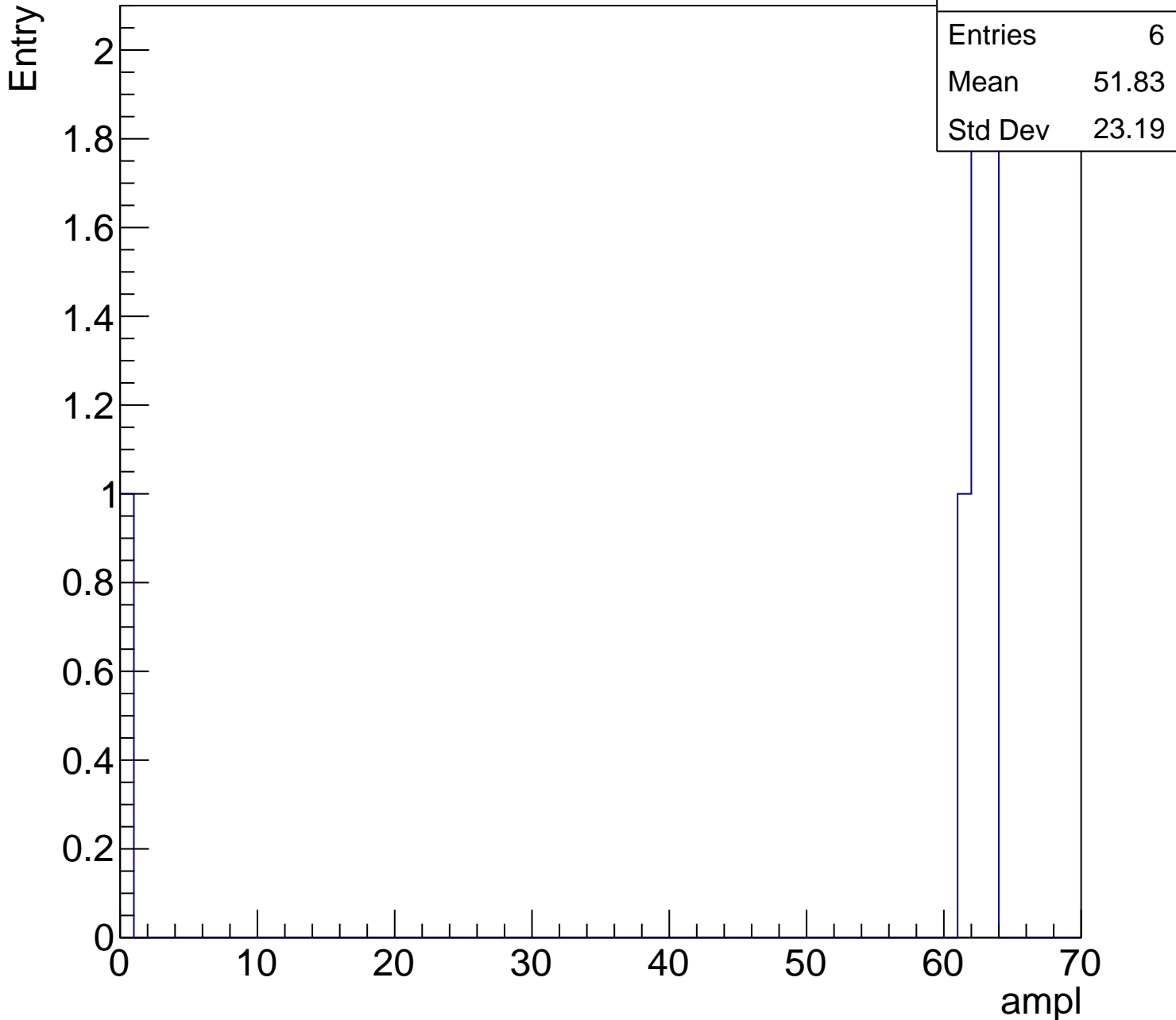
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.83
Std Dev	23.19

ampl





# B0L000S, U26-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch51, adc0

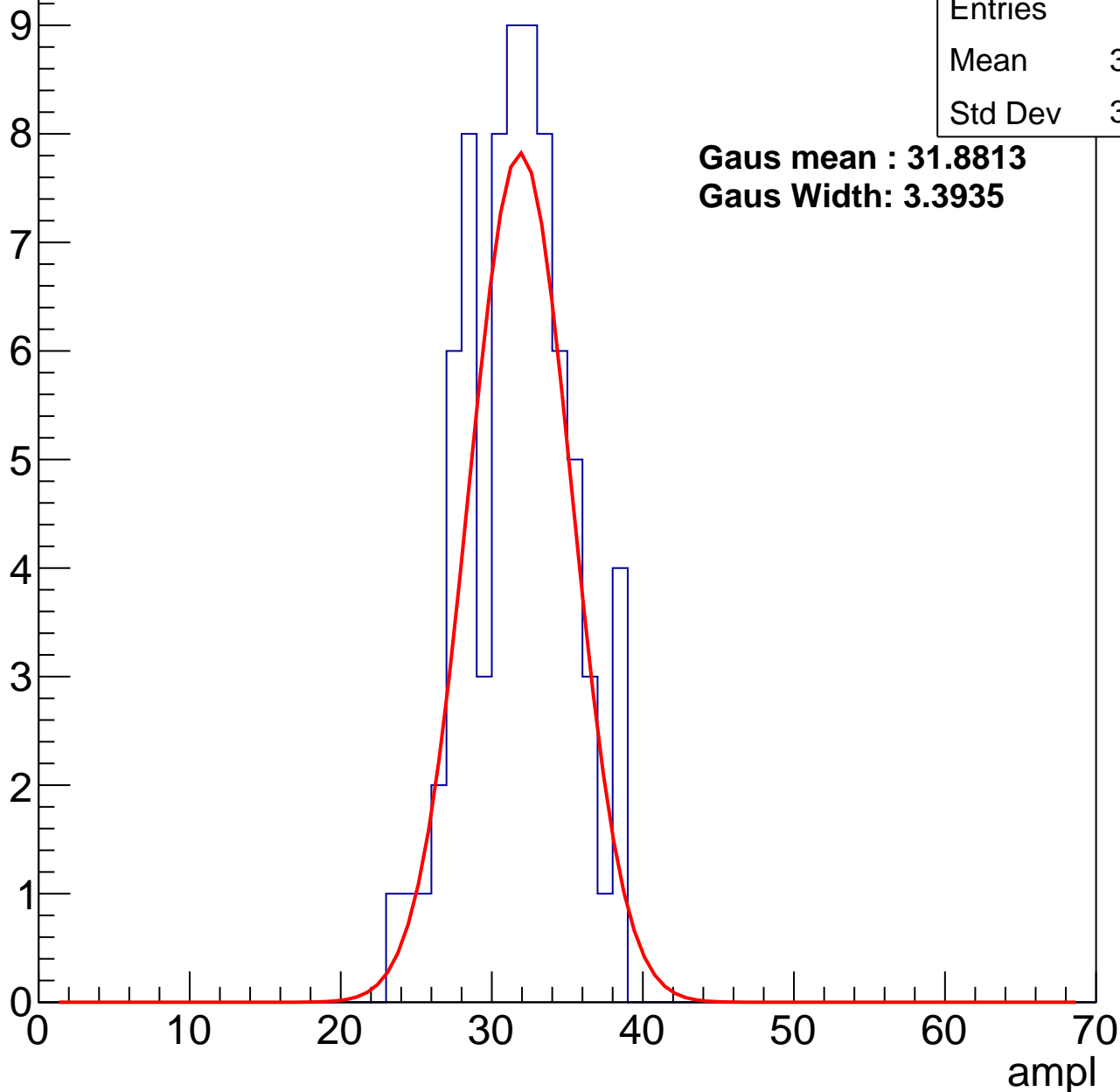
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	31.25
Std Dev	3.394

**Gaus mean : 31.8813**

**Gaus Width: 3.3935**



# B0L000S, U26-ch51, adc1

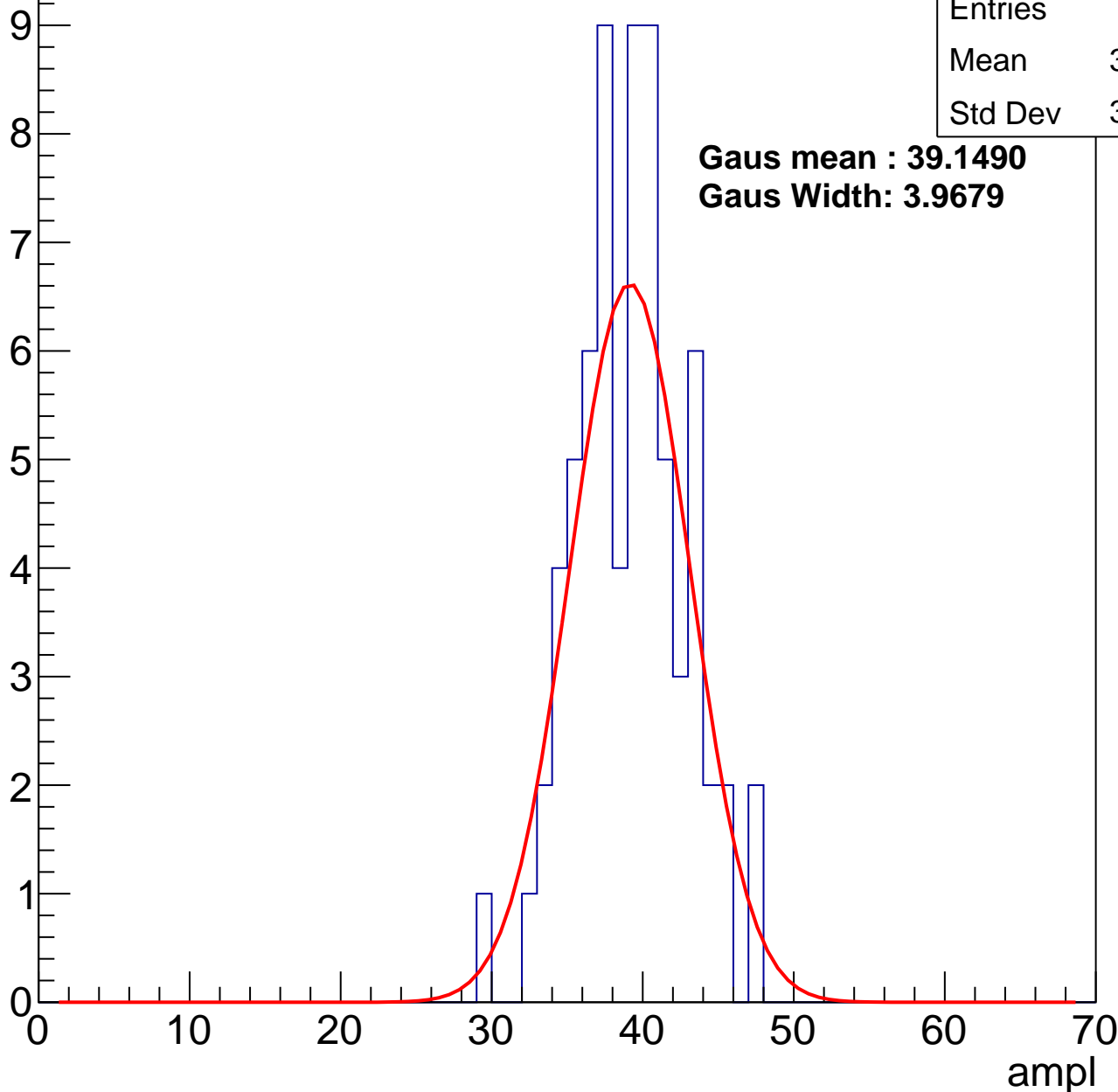
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	38.73
Std Dev	3.569

**Gaus mean : 39.1490**

**Gaus Width: 3.9679**



# B0L000S, U26-ch51, adc2

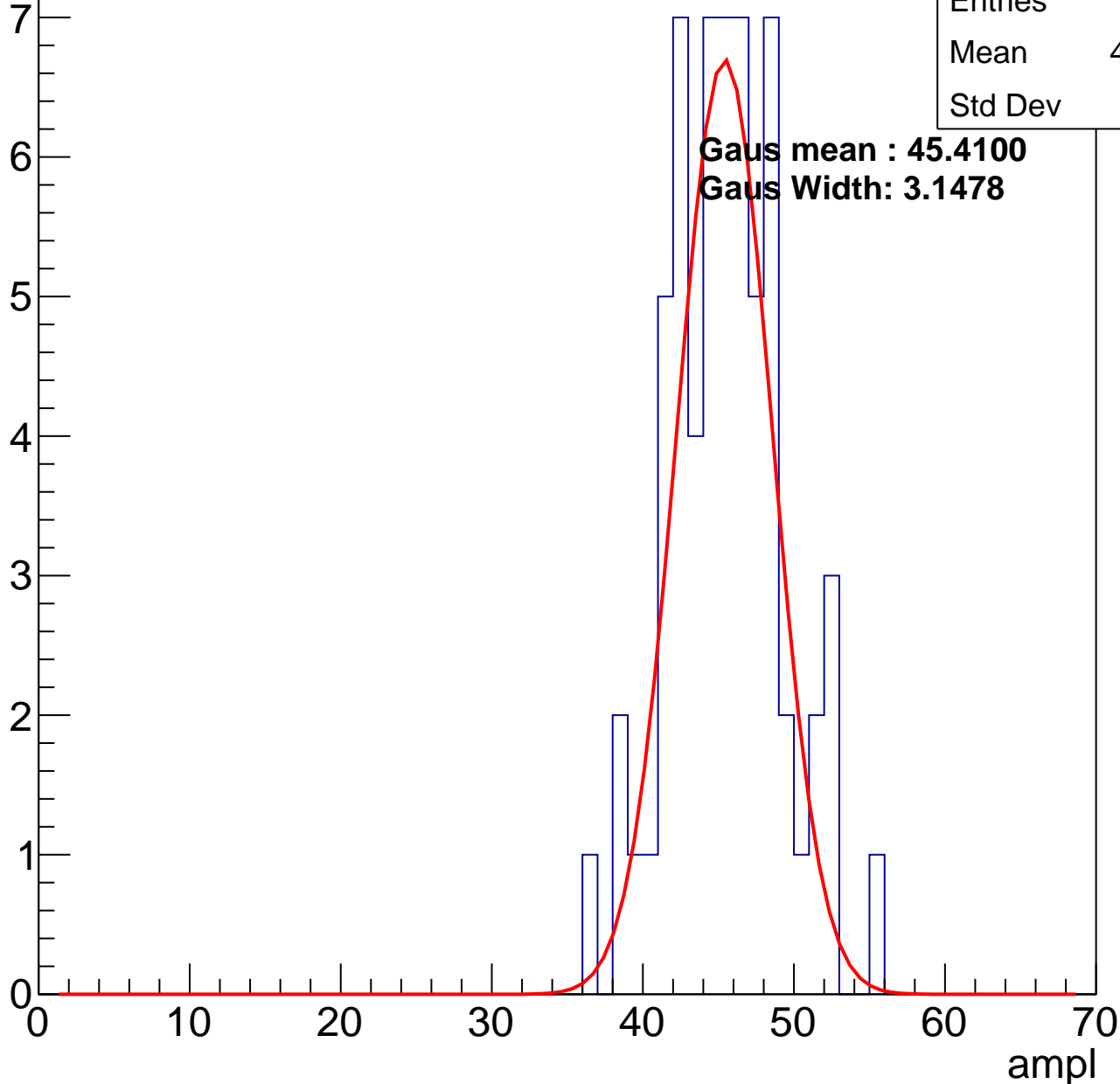
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	45.06
Std Dev	3.72

**Gaus mean : 45.4100**

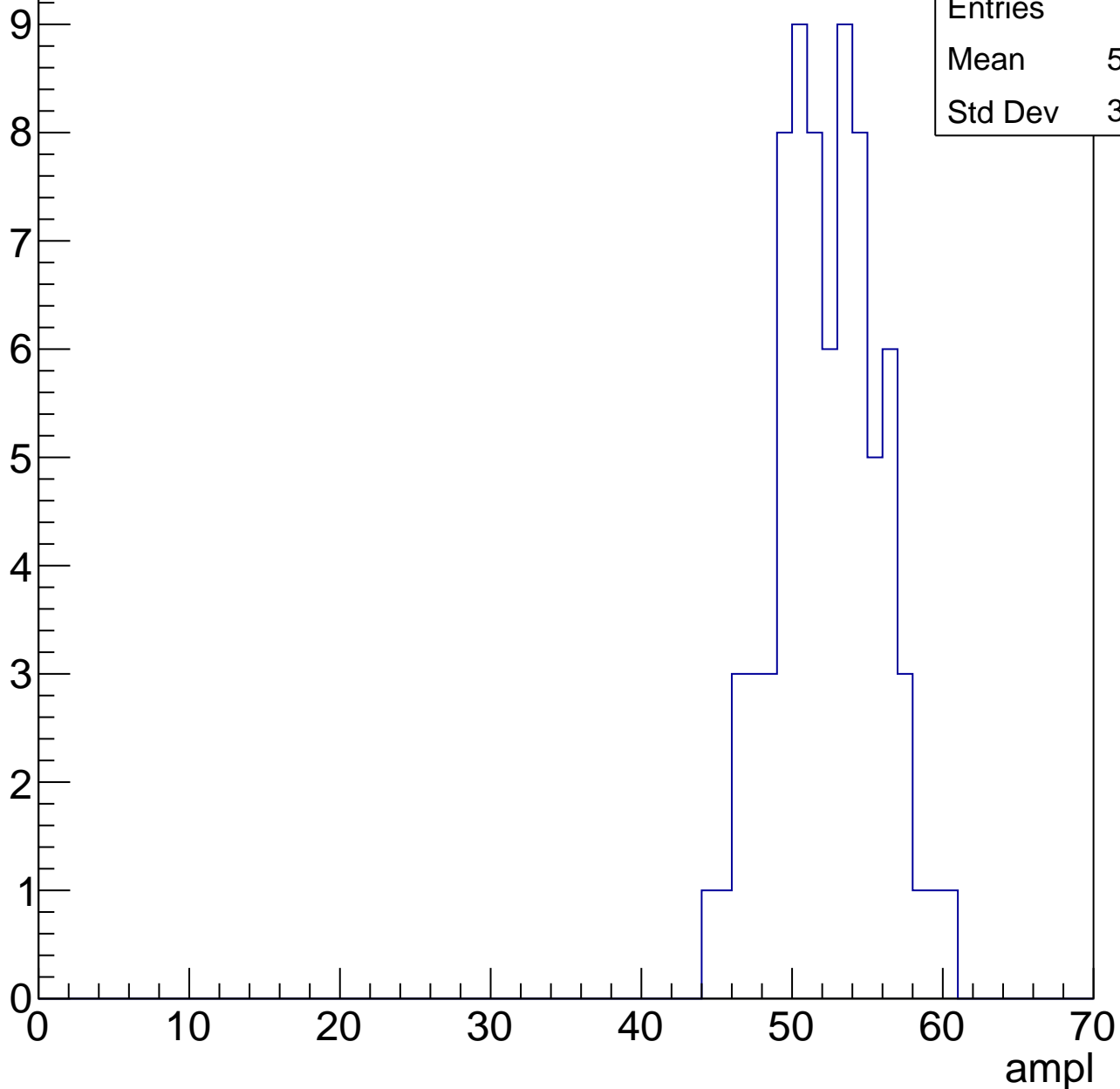
**Gaus Width: 3.1478**



# B0L000S, U26-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

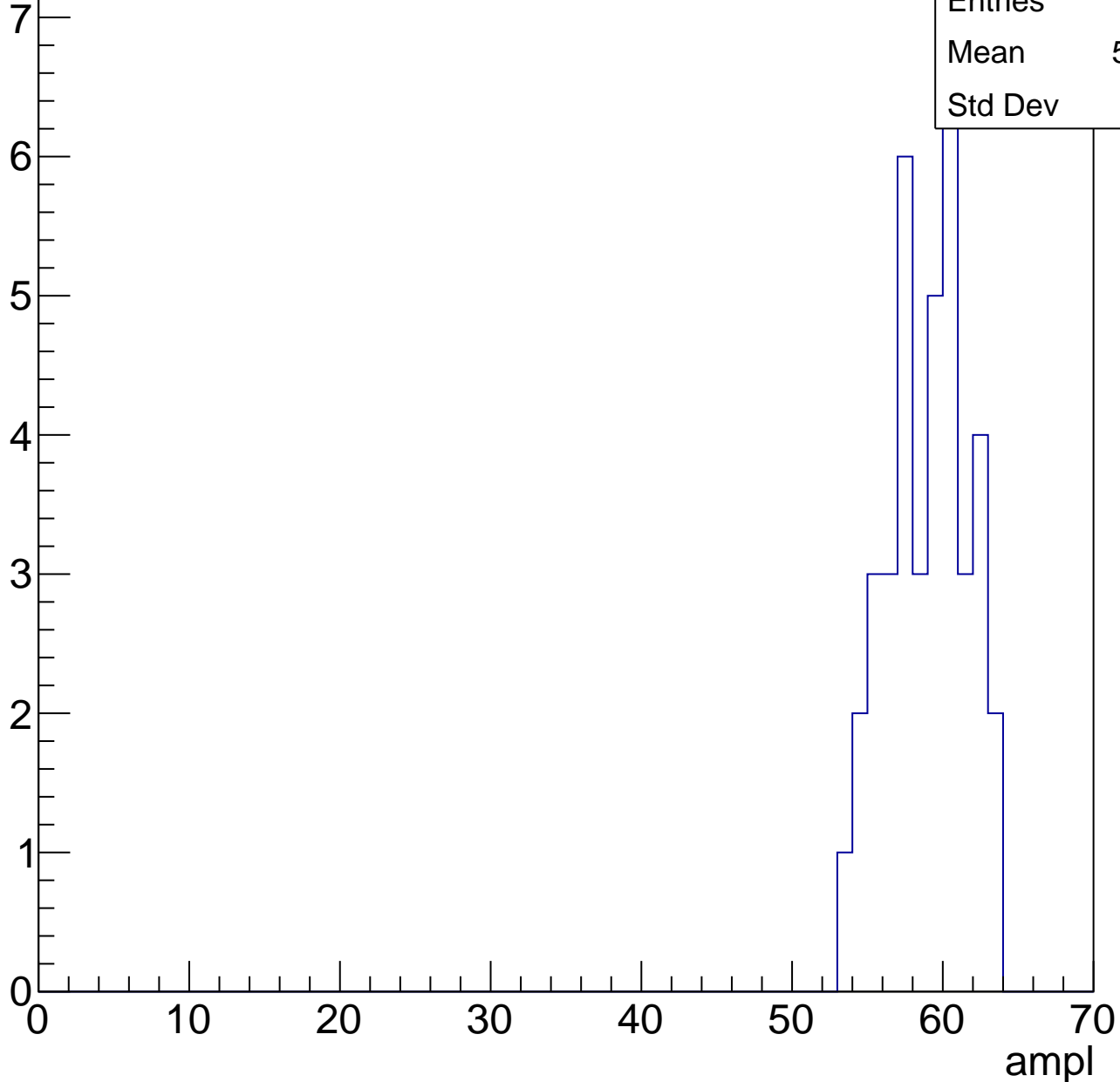
Entry



# B0L000S, U26-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

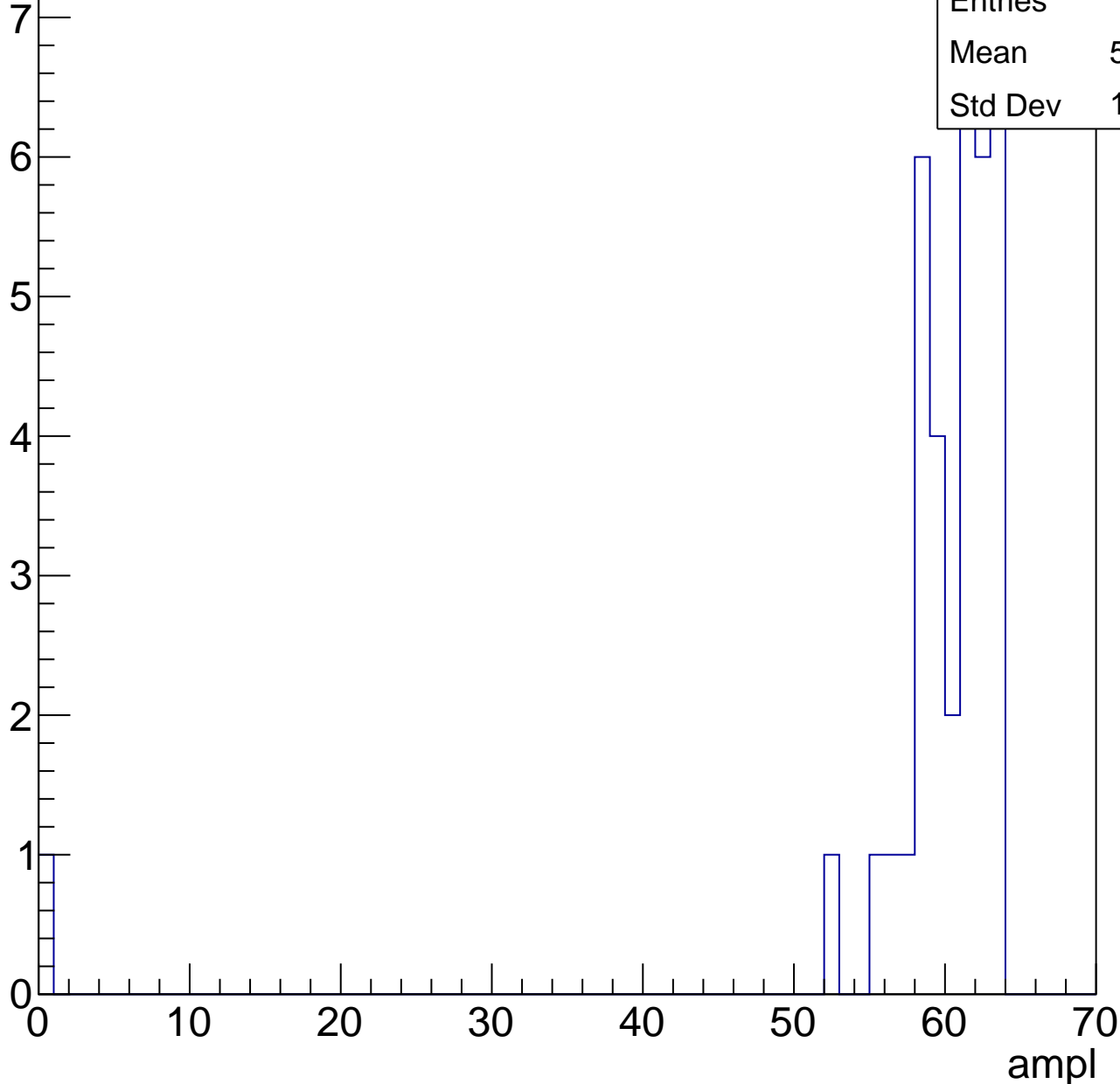


# B0L000S, U26-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.49
Std Dev	10.07



# B0L000S, U26-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch52, adc0

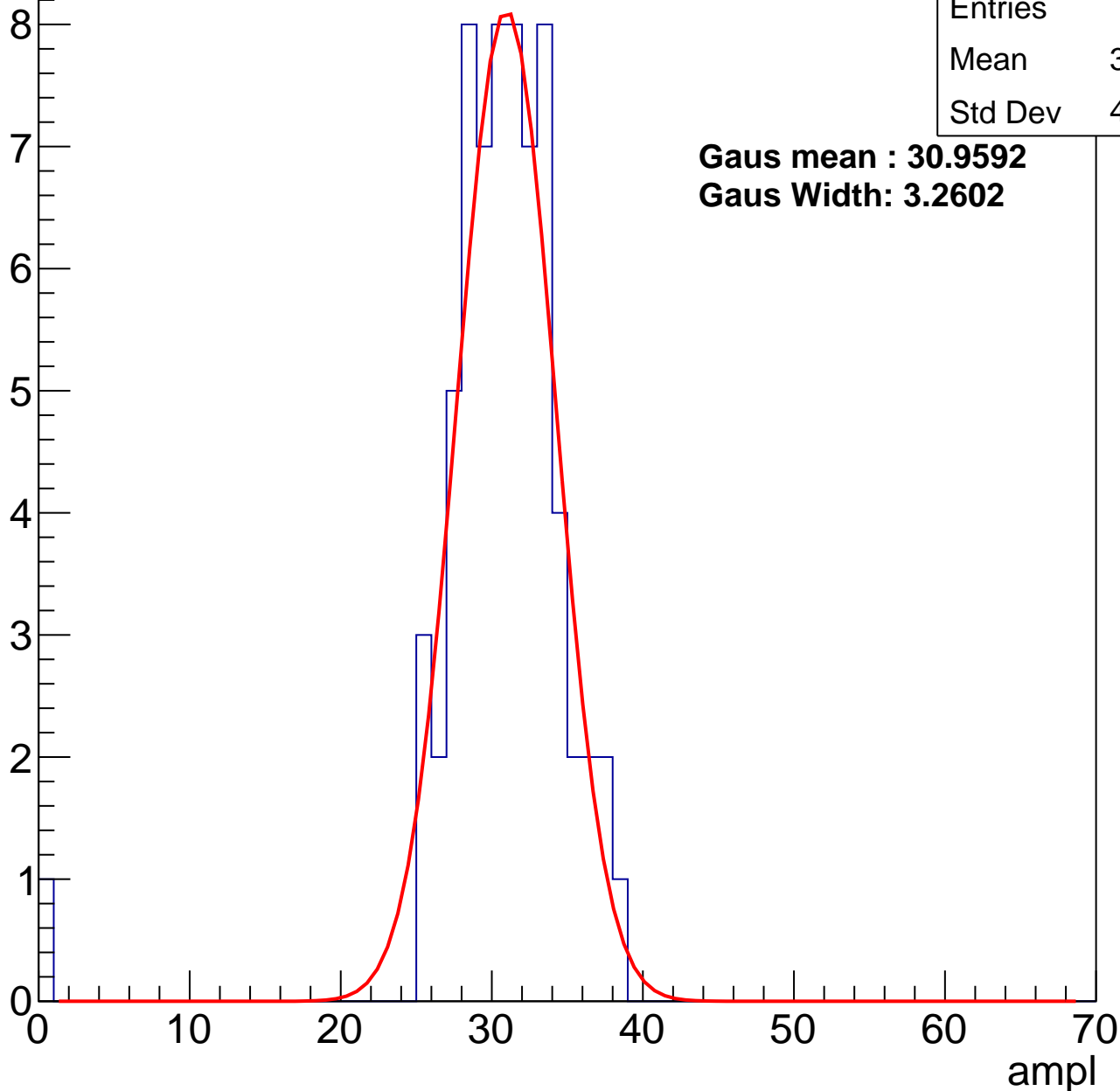
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.22
Std Dev	4.765

**Gaus mean : 30.9592**

**Gaus Width: 3.2602**



# B0L000S, U26-ch52, adc1

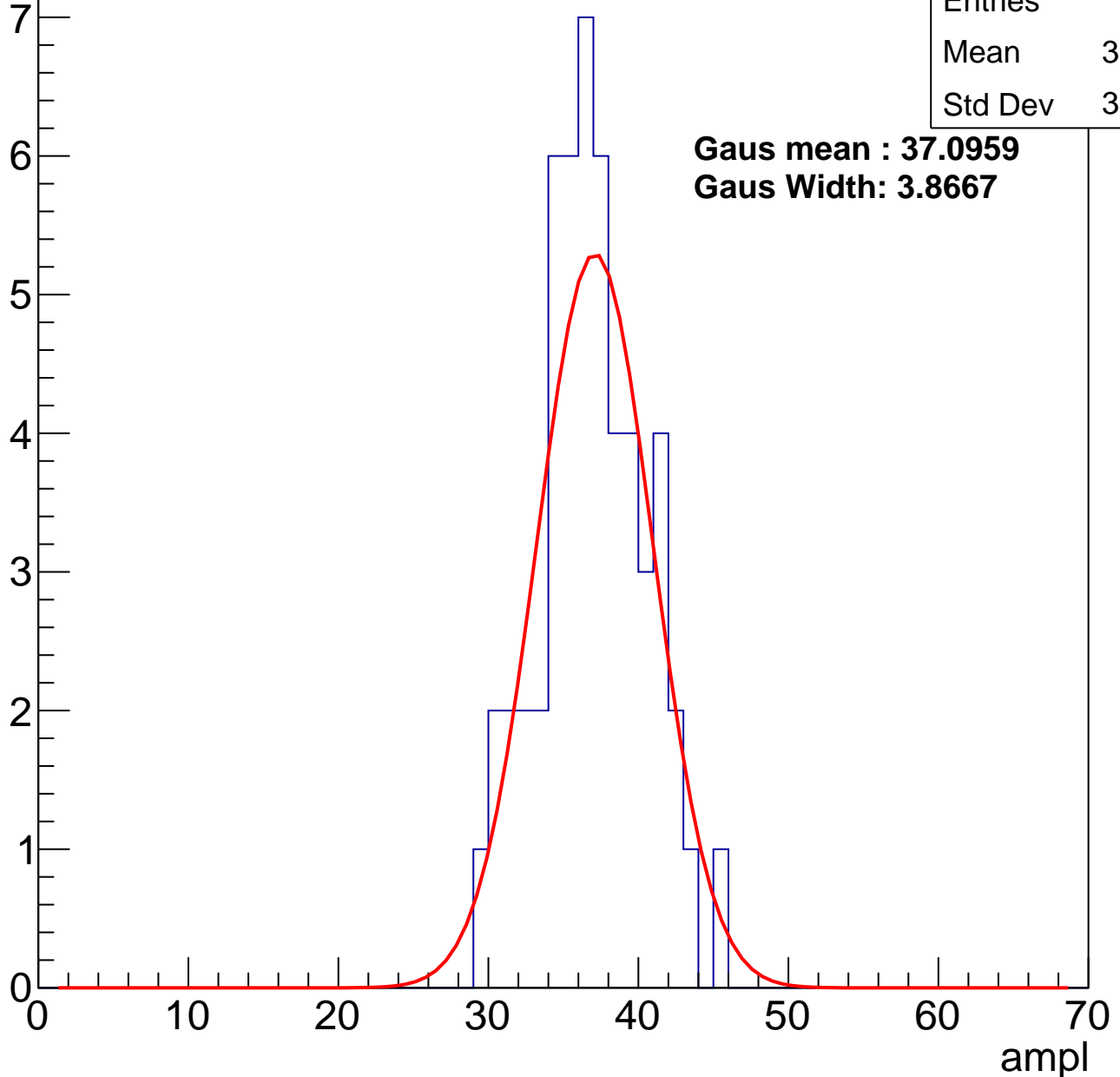
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.47
Std Dev	3.494

**Gaus mean : 37.0959**

**Gaus Width: 3.8667**



# B0L000S, U26-ch52, adc2

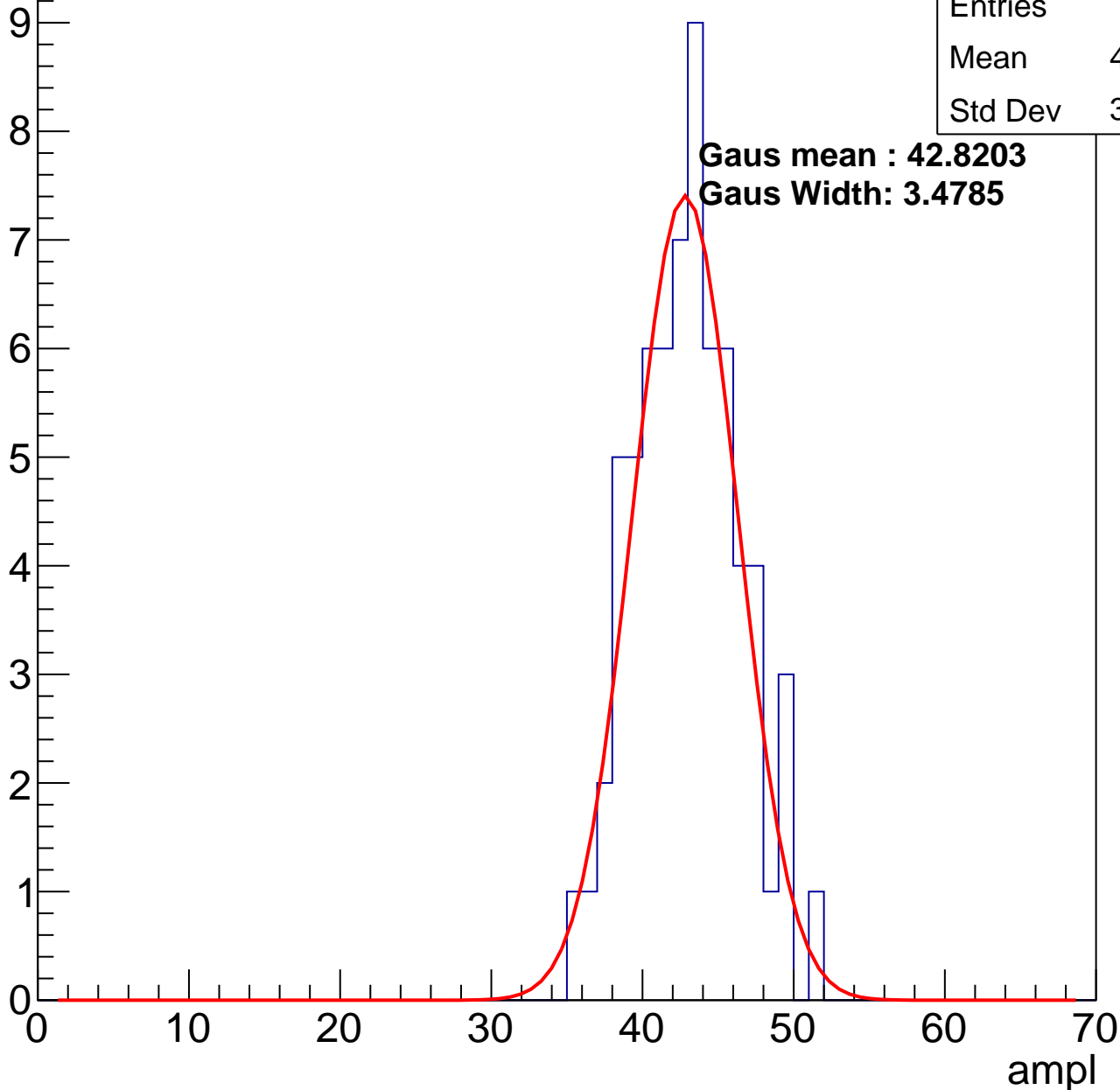
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	42.52
Std Dev	3.427

**Gaus mean : 42.8203**

**Gaus Width: 3.4785**

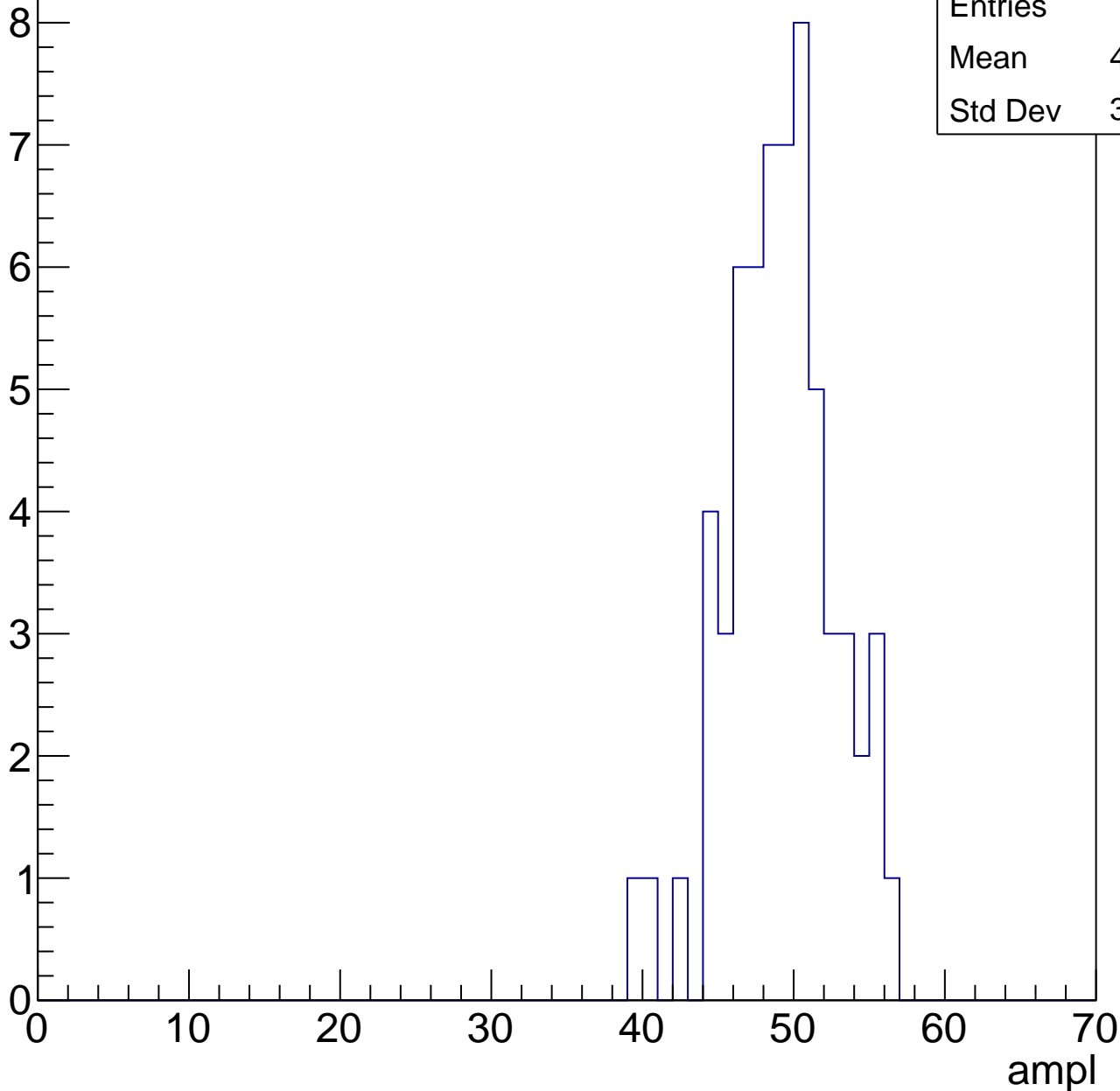


# B0L000S, U26-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

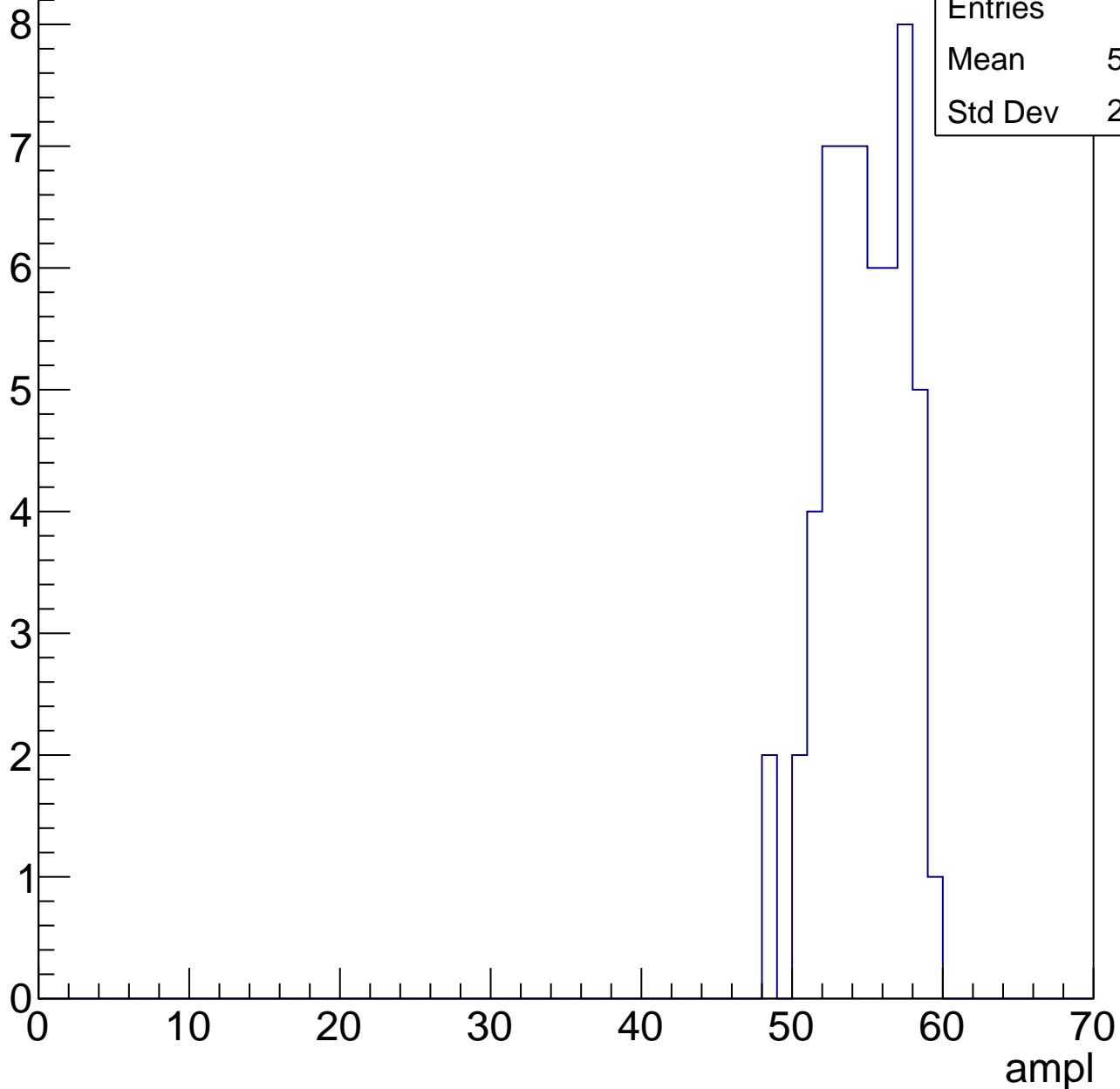
Entries	61
Mean	48.66
Std Dev	3.543



# B0L000S, U26-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch52, adc5

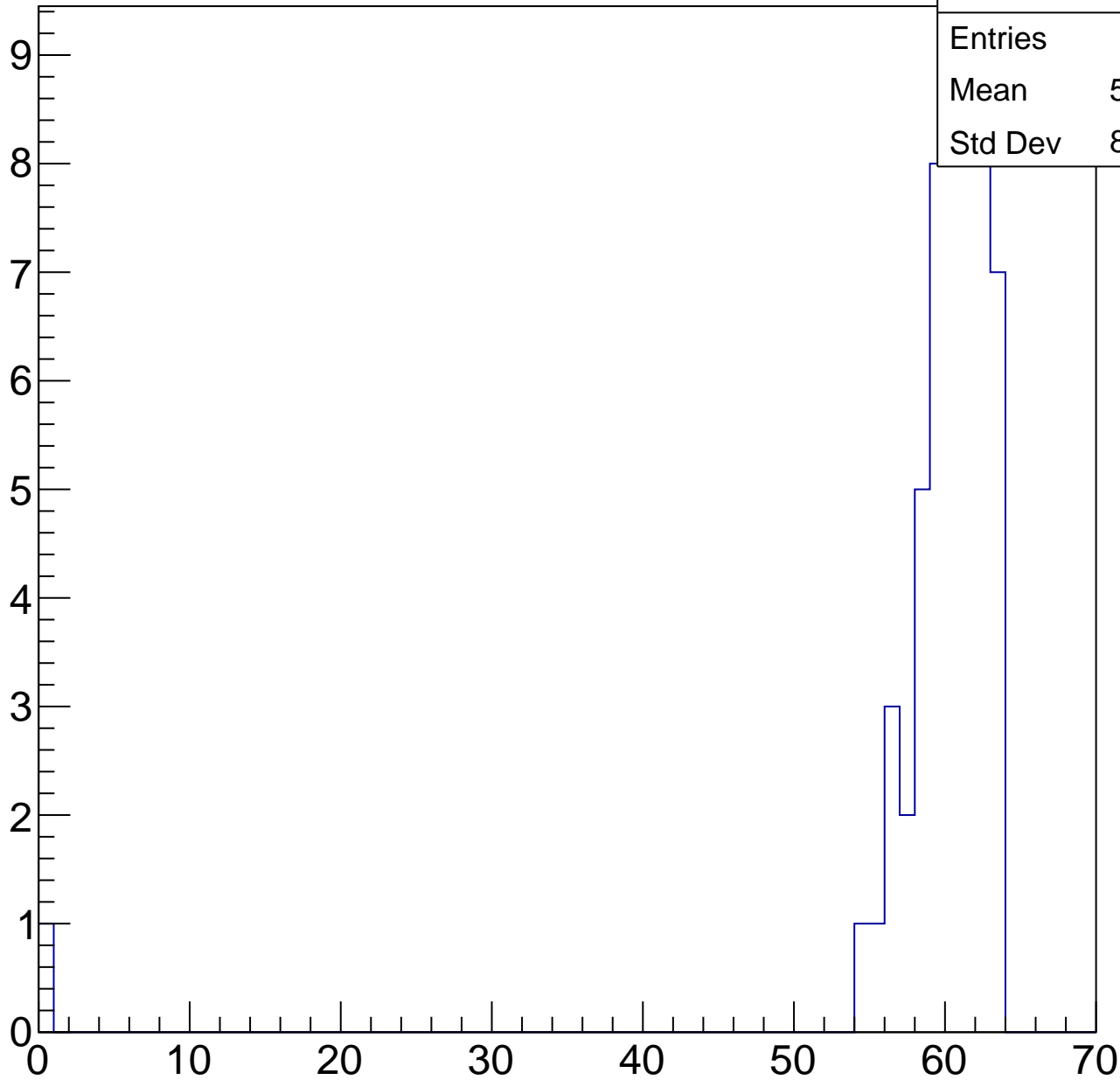
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.87
Std Dev	8.378

ampl



# B0L000S, U26-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

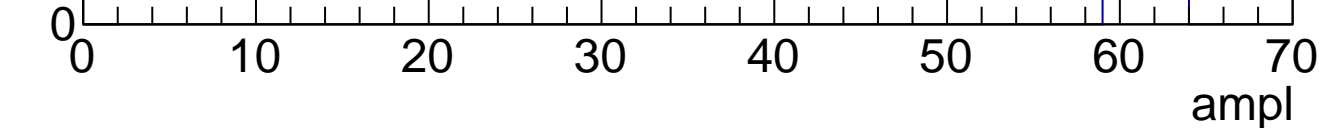
8

Mean

61.62

Std Dev

1.409





# B0L000S, U26-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl



# B0L000S, U26-ch53, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	29.03
Std Dev	2.917

**Gaus mean : 29.7286**

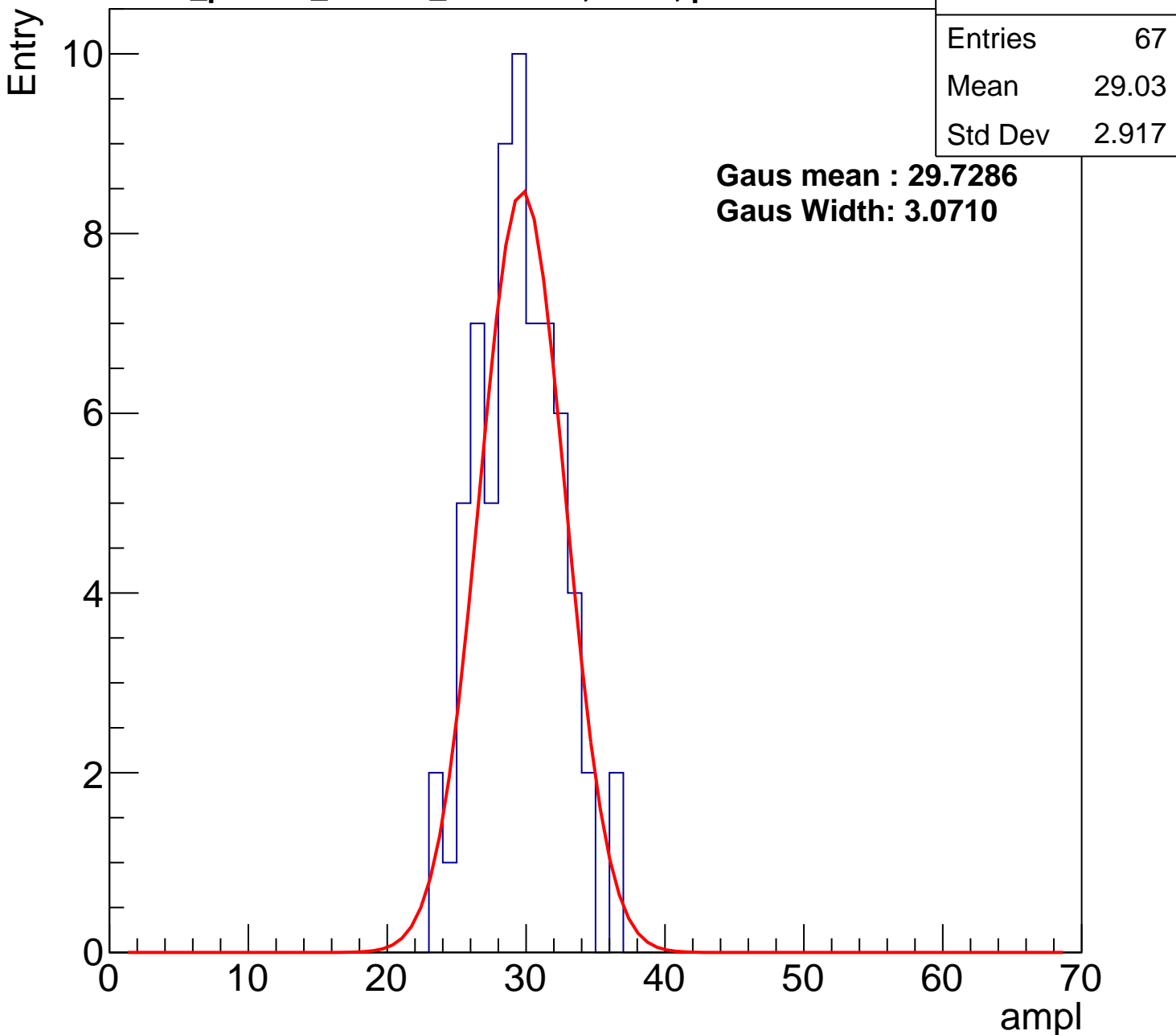
**Gaus Width: 3.0710**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch53, adc1

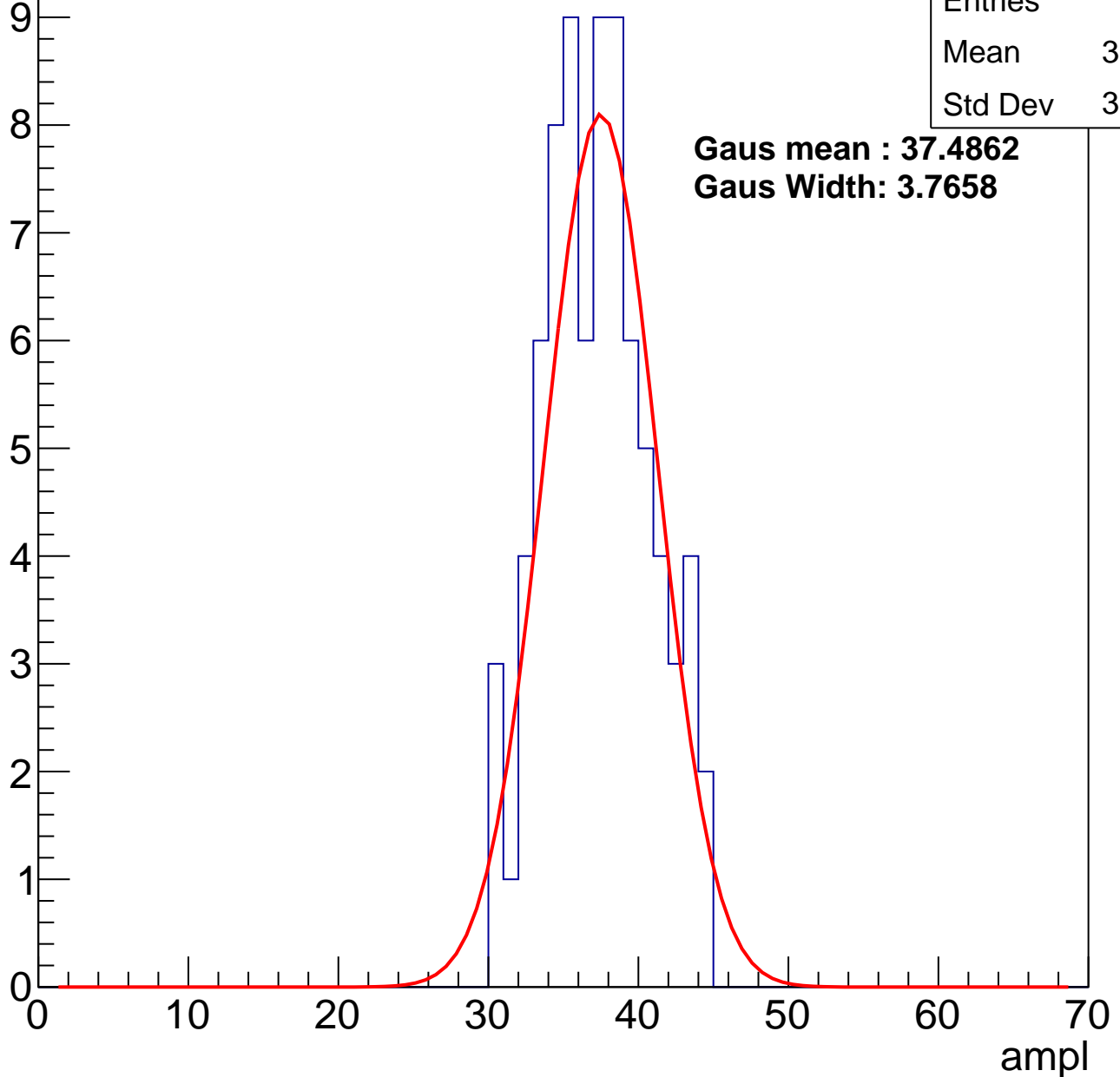
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	36.82
Std Dev	3.467

**Gaus mean : 37.4862**

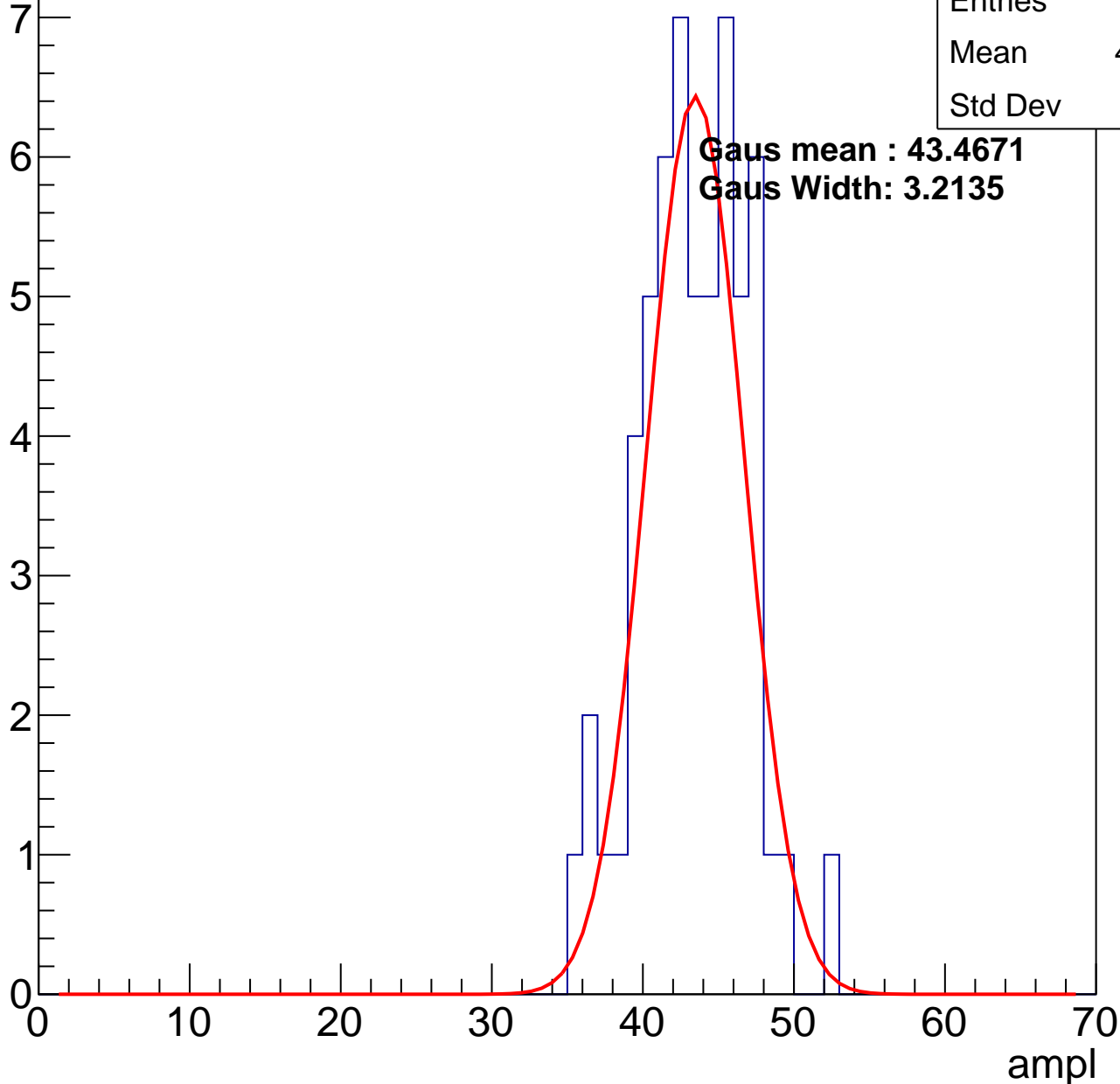
**Gaus Width: 3.7658**



# B0L000S, U26-ch53, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

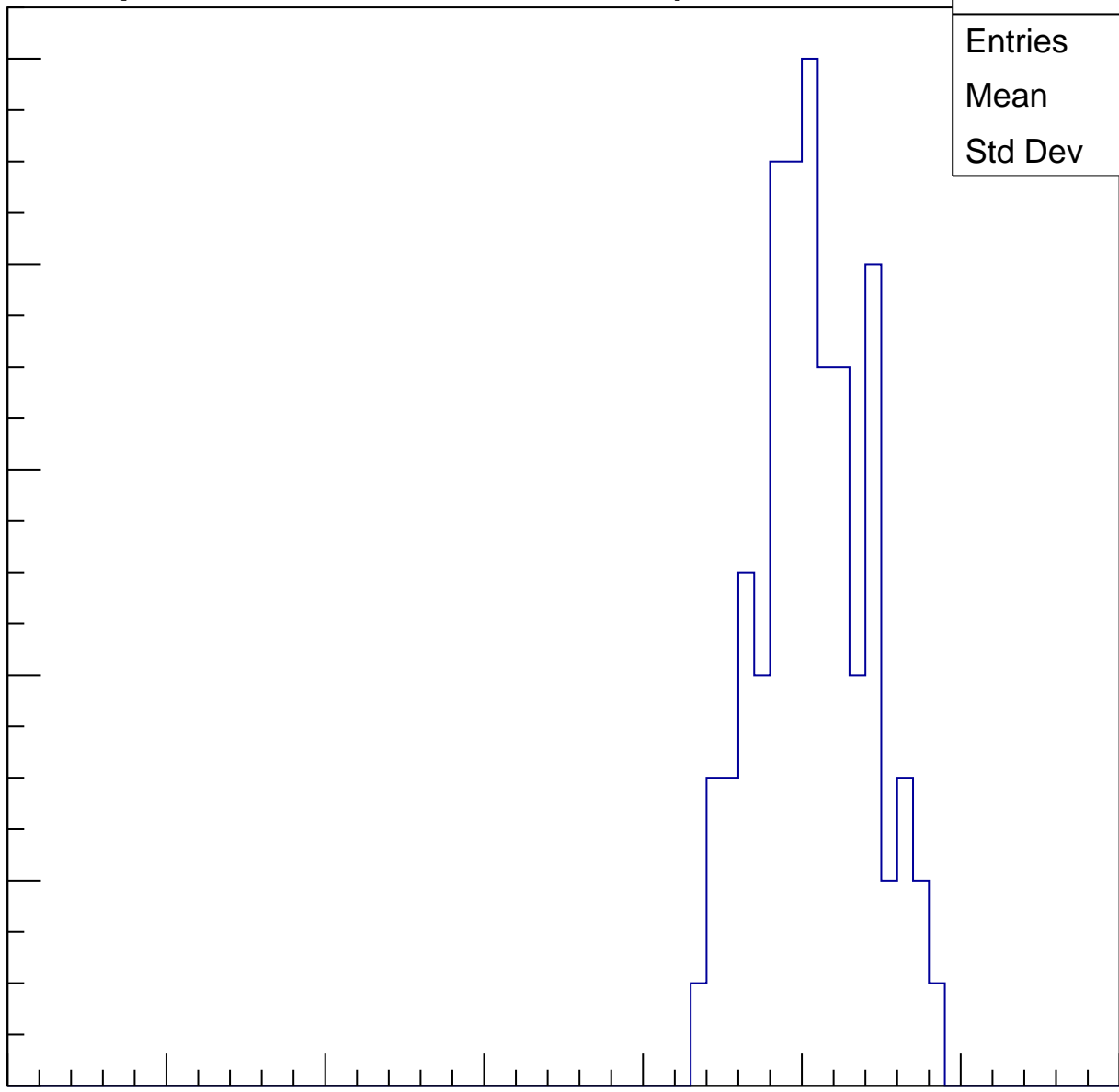
Entries	78
Mean	50.21
Std Dev	3.413

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

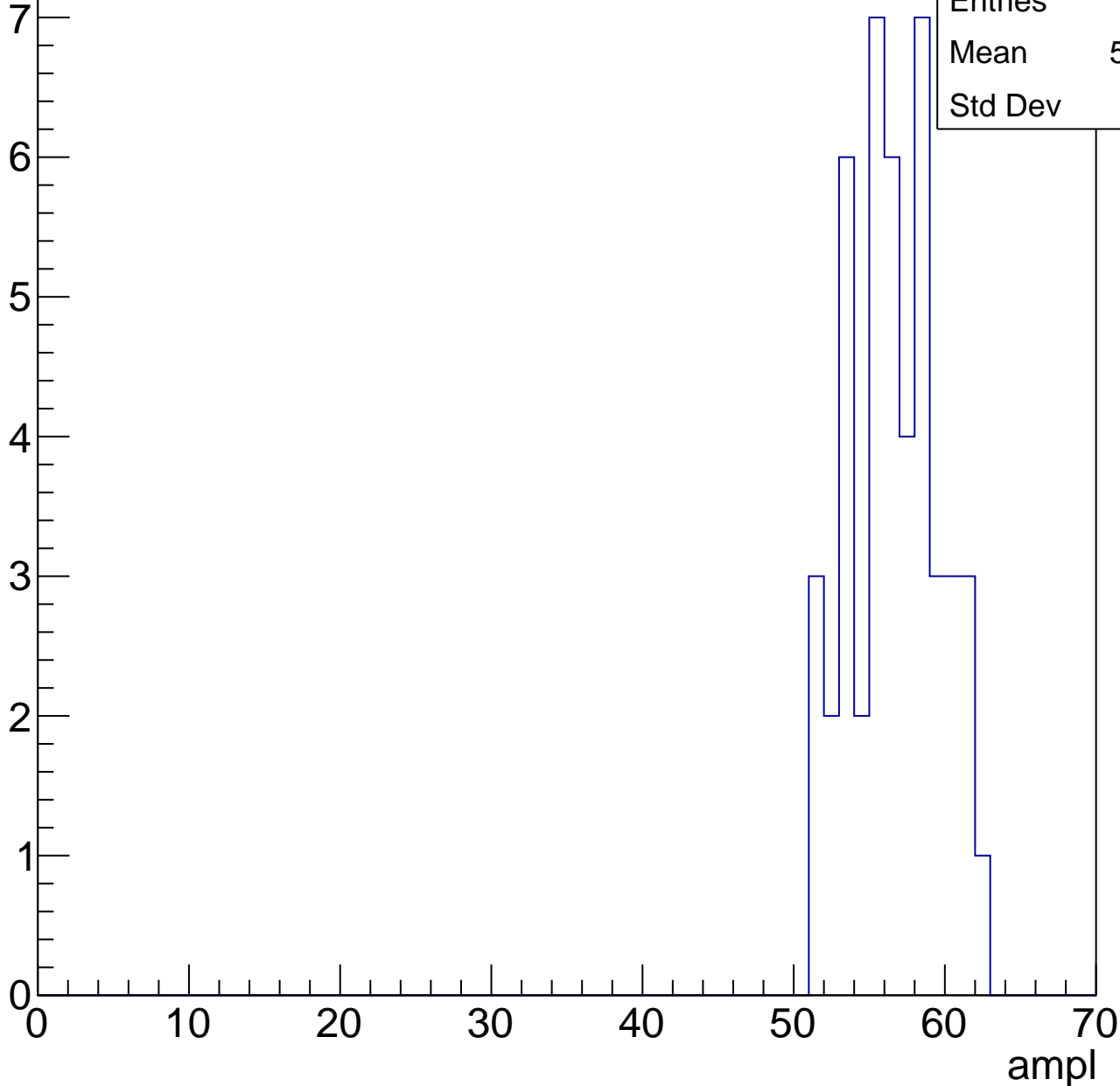


# B0L000S, U26-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

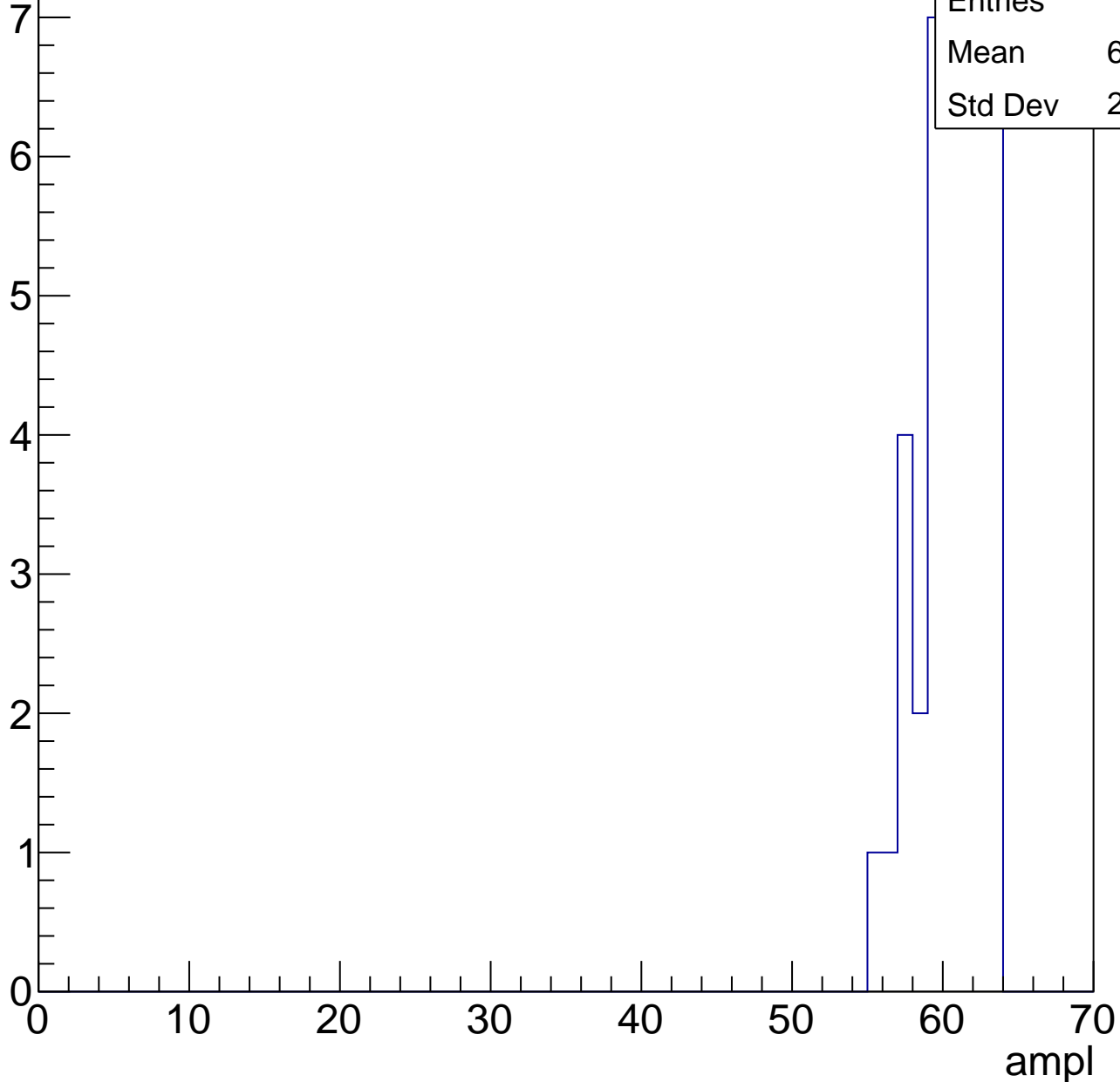
Entries	47
Mean	56.17
Std Dev	2.89



# B0L000S, U26-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

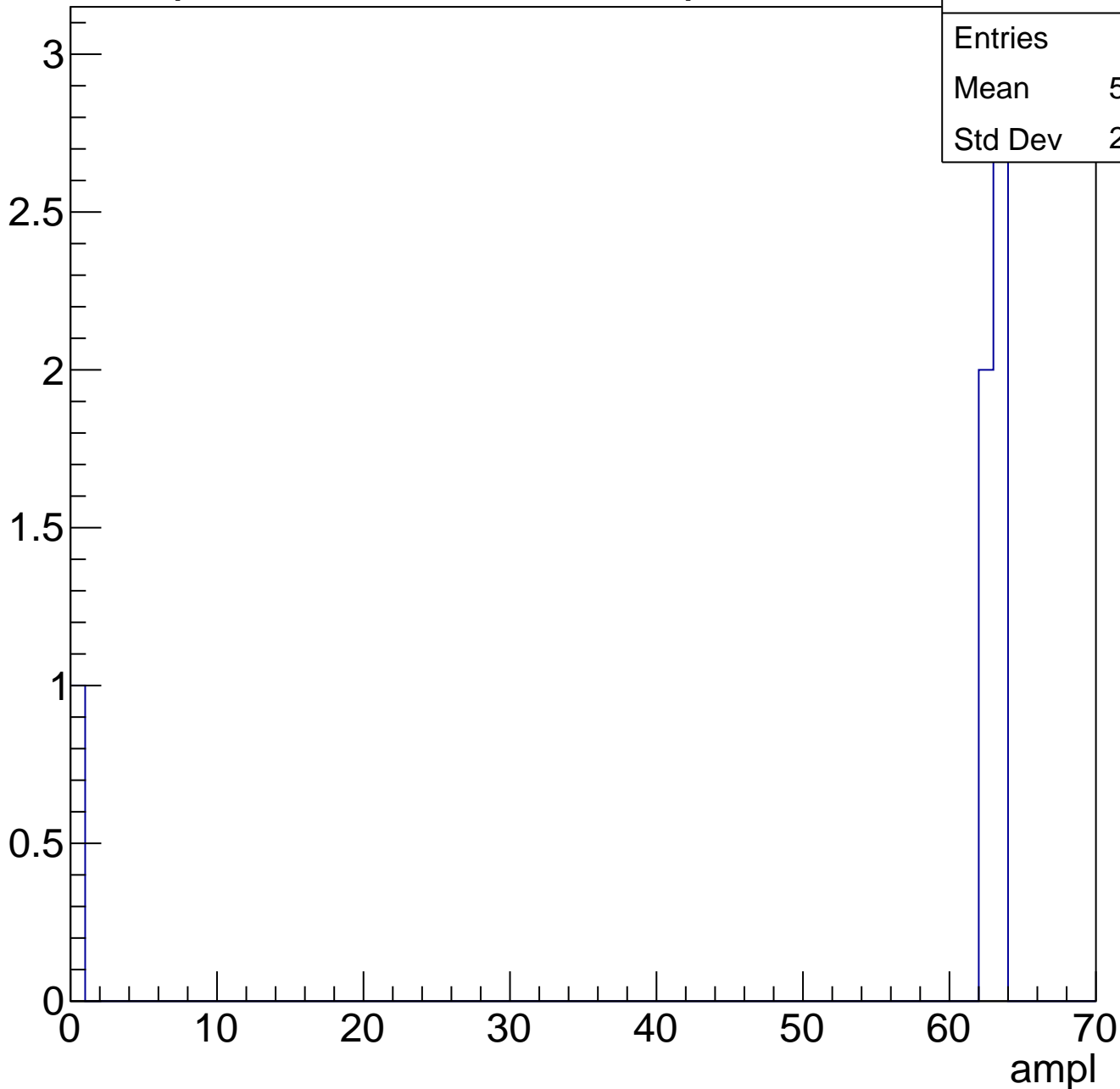
Entry



# B0L000S, U26-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch54, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	29.33
Std Dev	3.175

**Gaus mean : 29.6055**

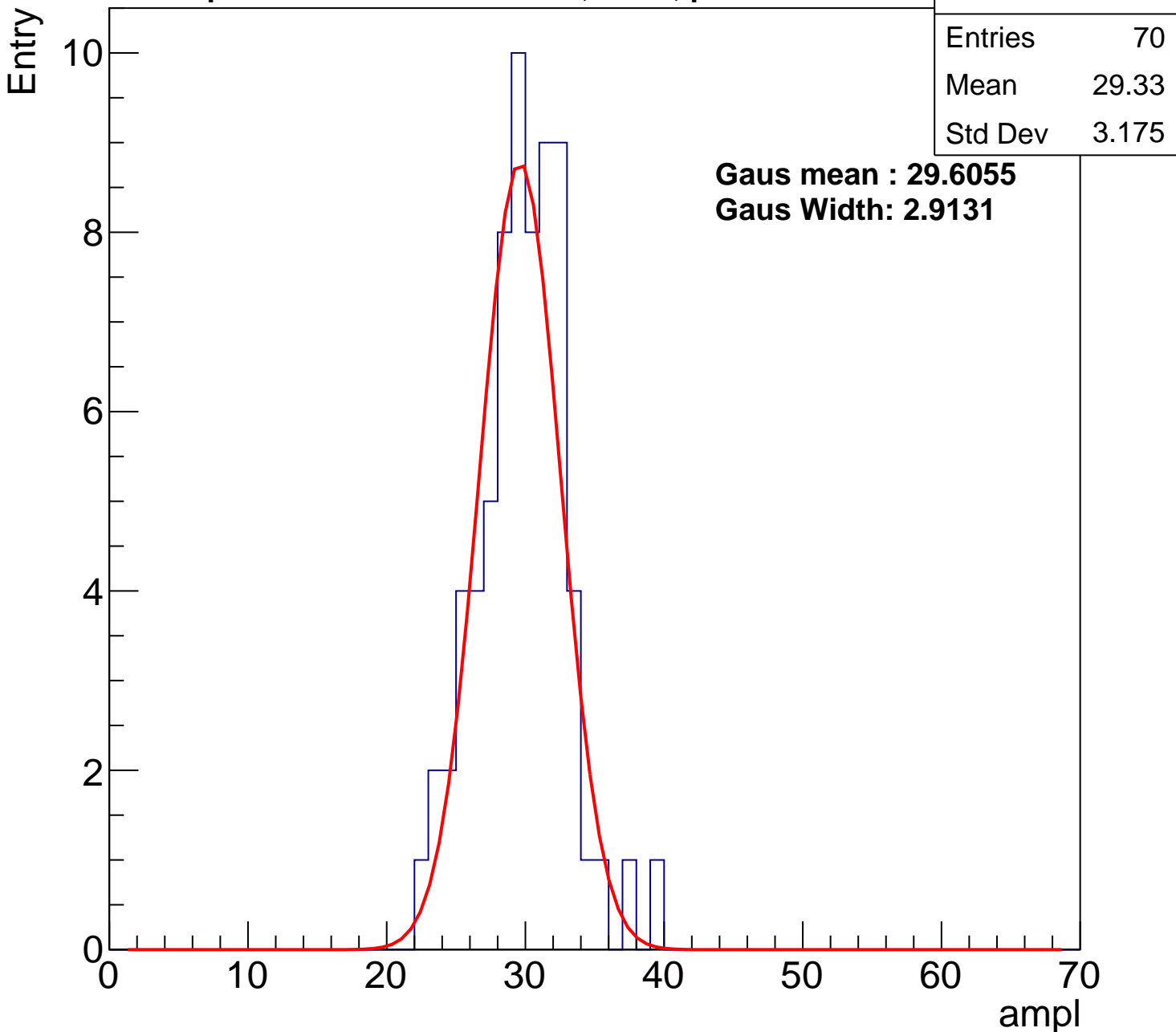
**Gaus Width: 2.9131**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



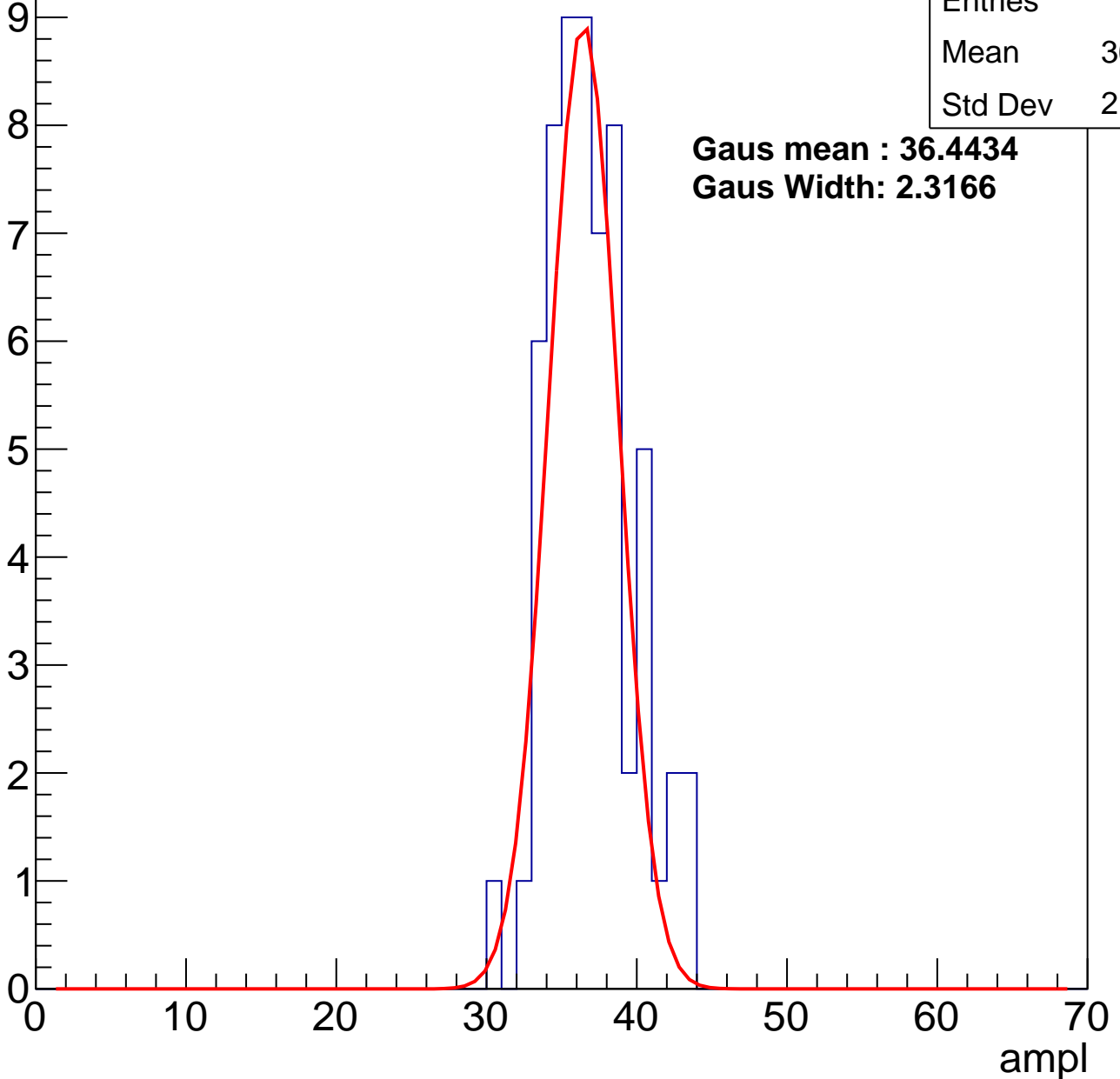
# B0L000S, U26-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.44
Std Dev	2.767

**Gaus mean : 36.4434**  
**Gaus Width: 2.3166**



# B0L000S, U26-ch54, adc2

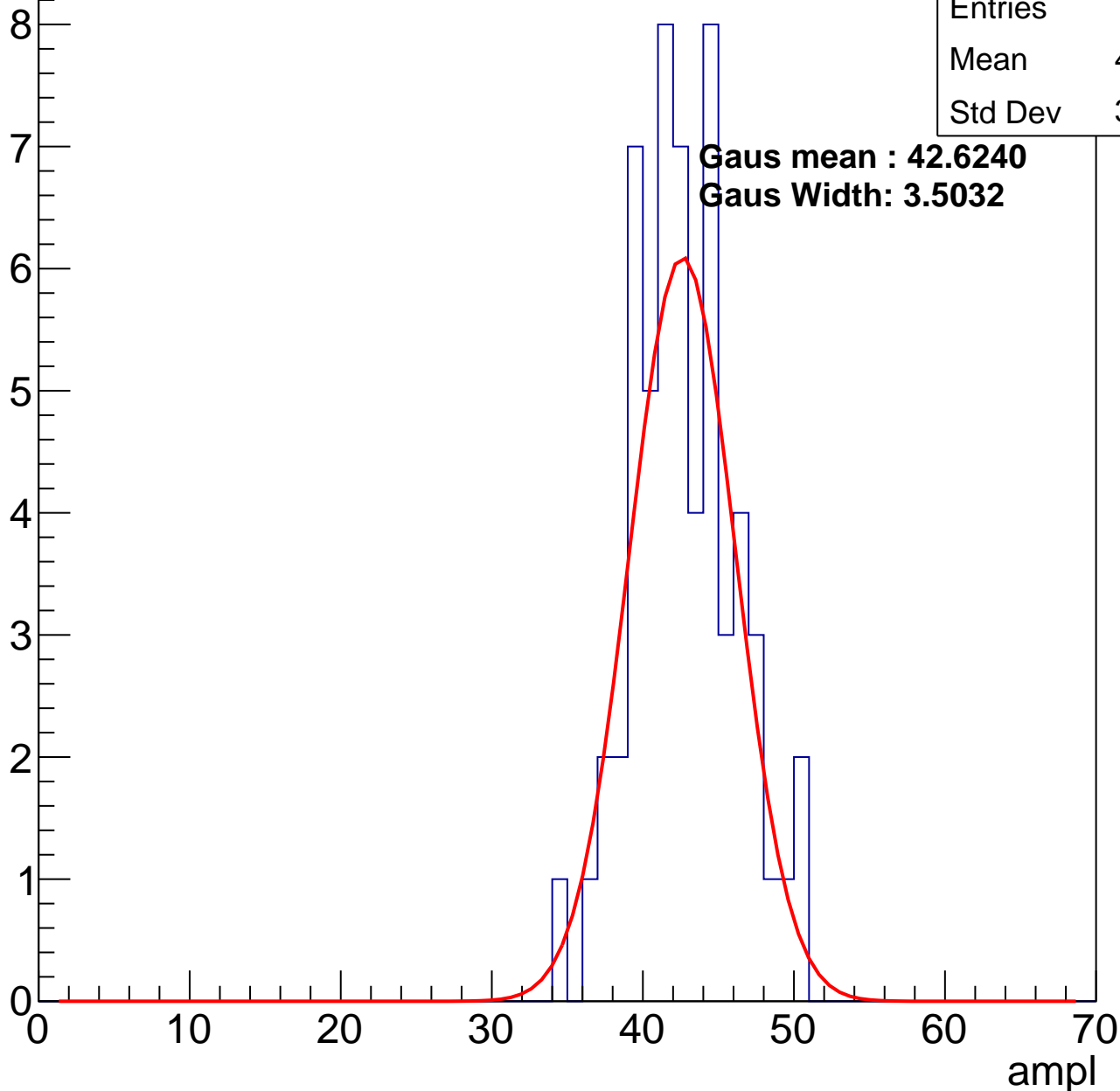
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.31
Std Dev	3.401

**Gaus mean : 42.6240**

**Gaus Width: 3.5032**

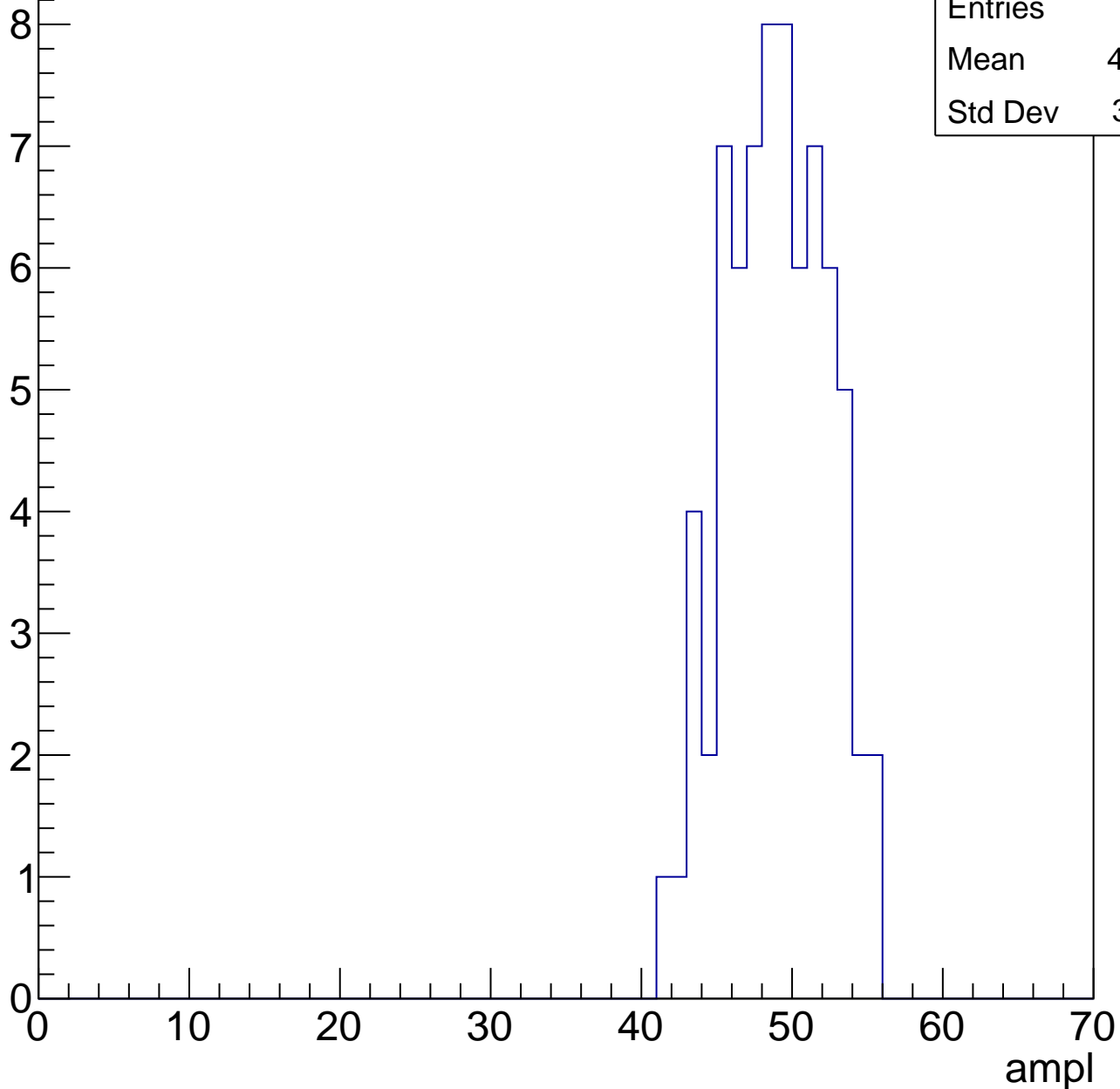


# B0L000S, U26-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	48.49
Std Dev	3.291

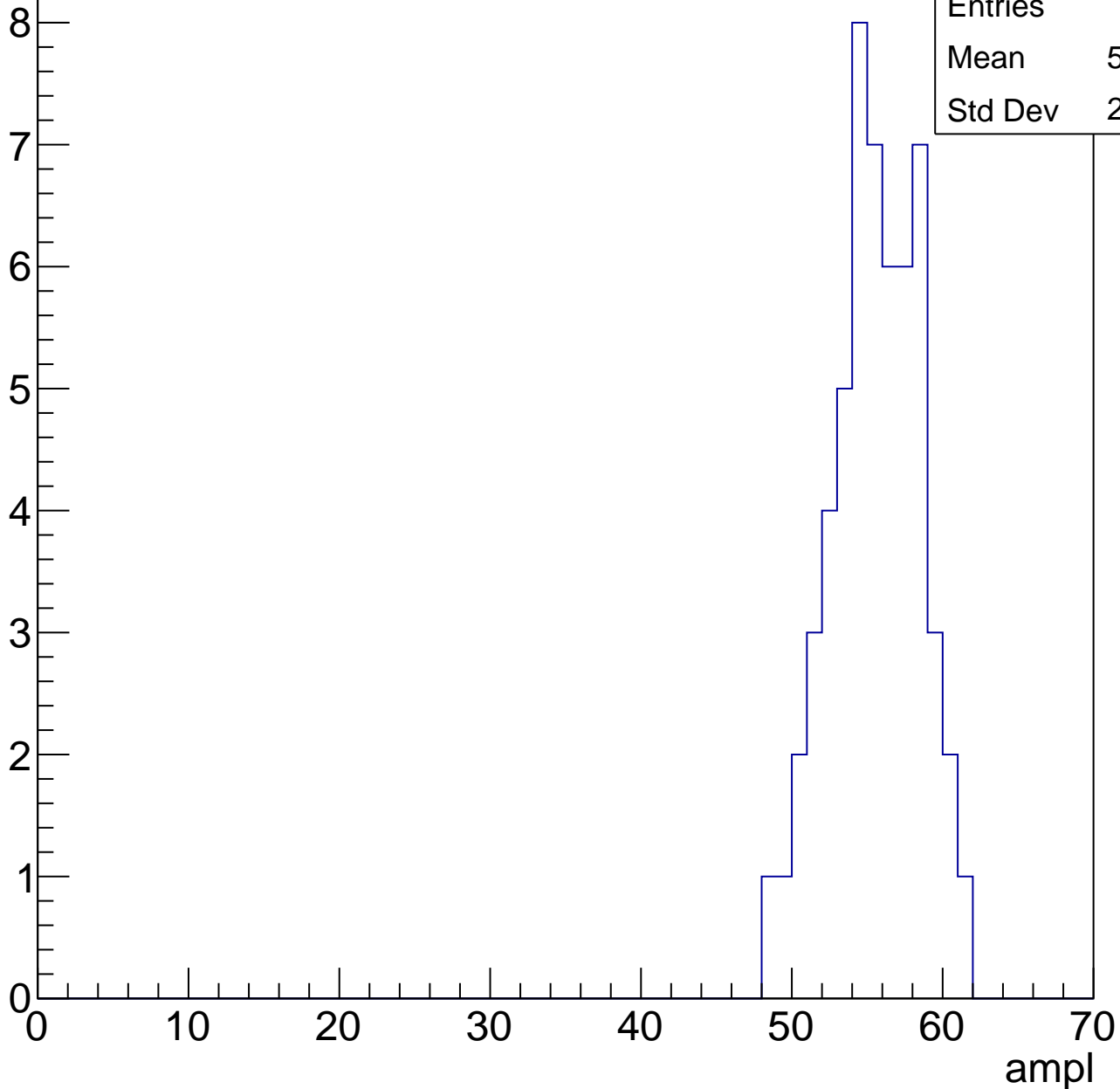


# B0L000S, U26-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.04
Std Dev	2.909

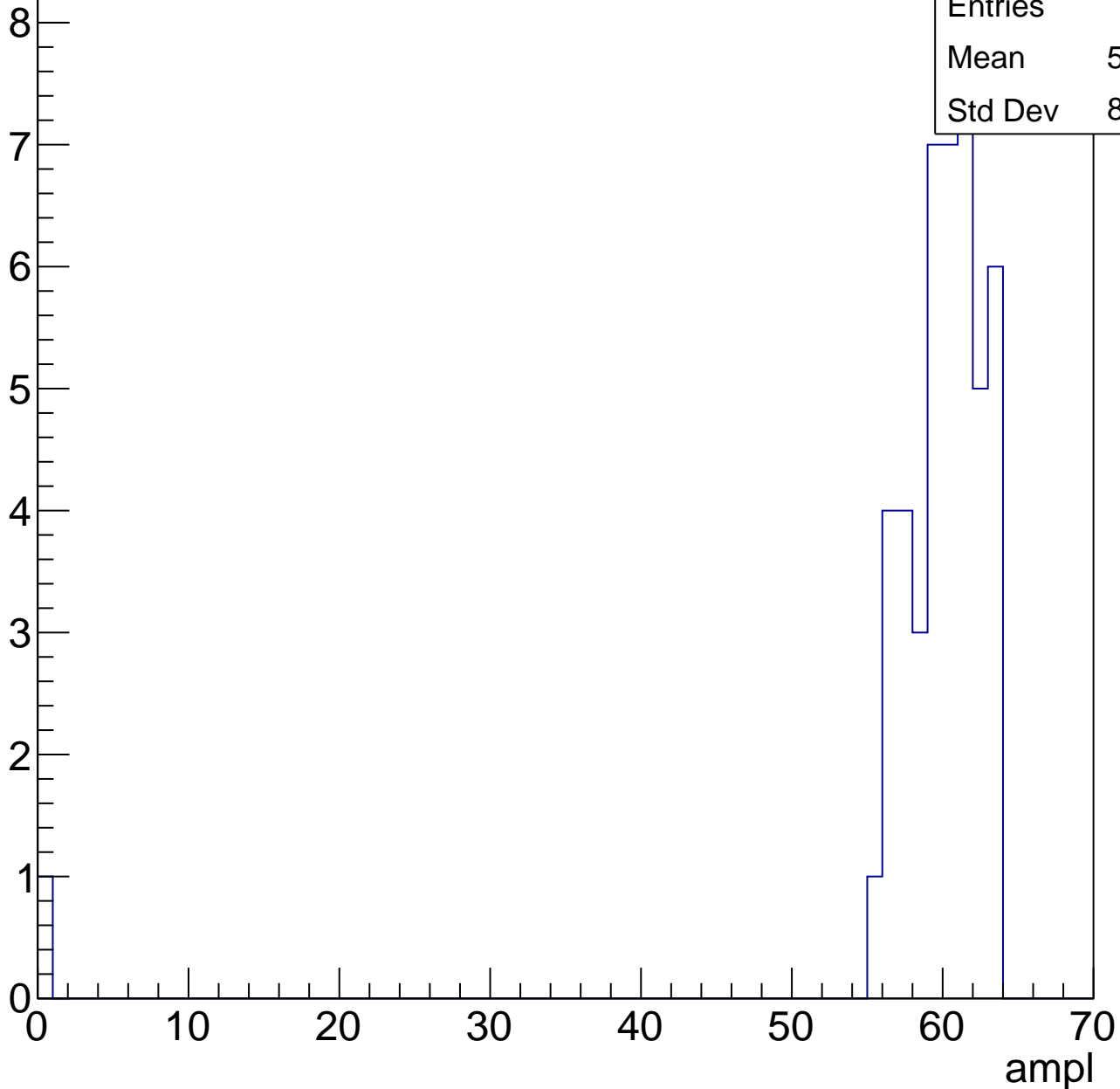


# B0L000S, U26-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.48
Std Dev	8.992

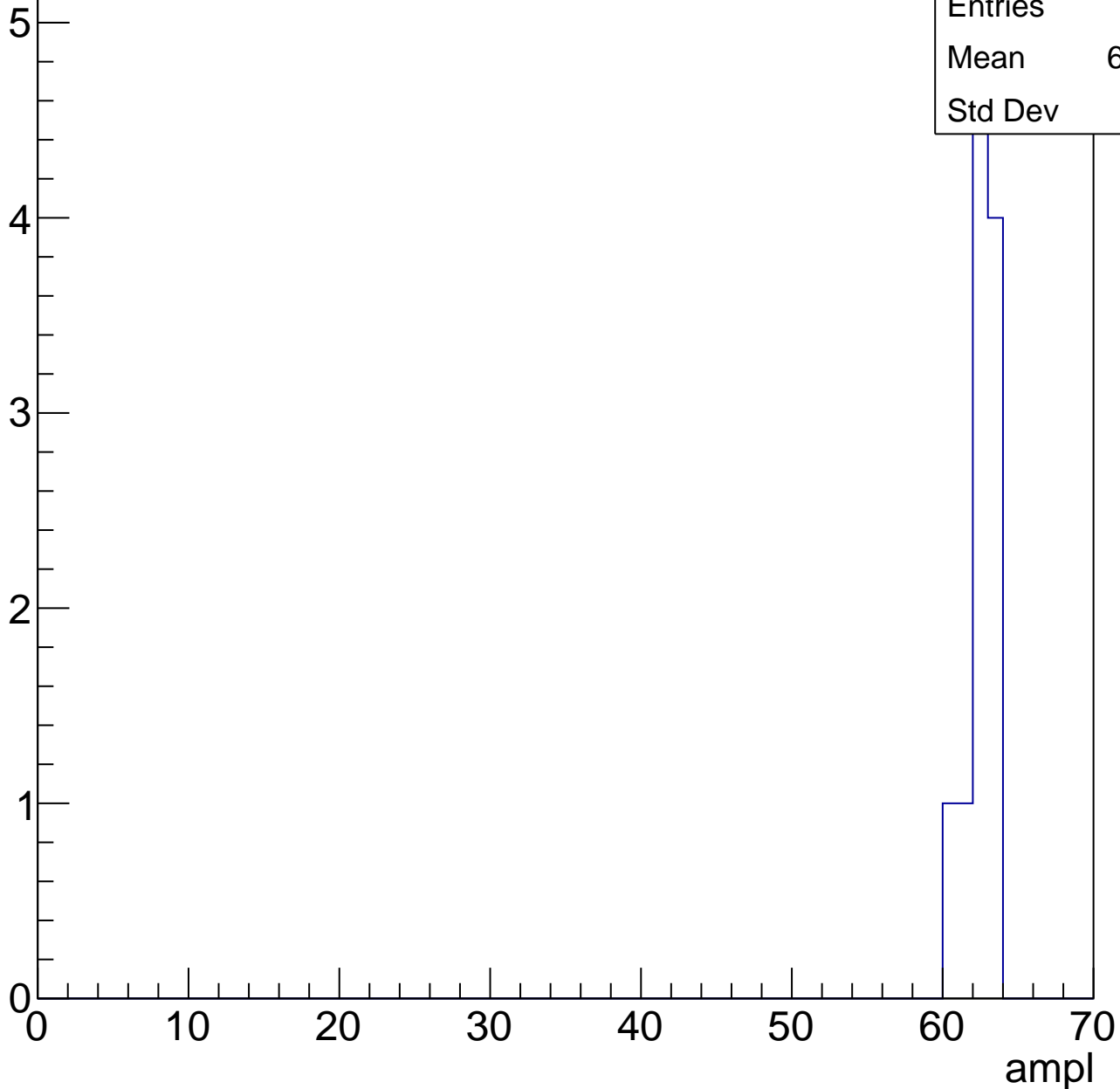


# B0L000S, U26-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	62.09
Std Dev	0.9





# B0L000S, U26-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B0L000S, U26-ch55, adc0

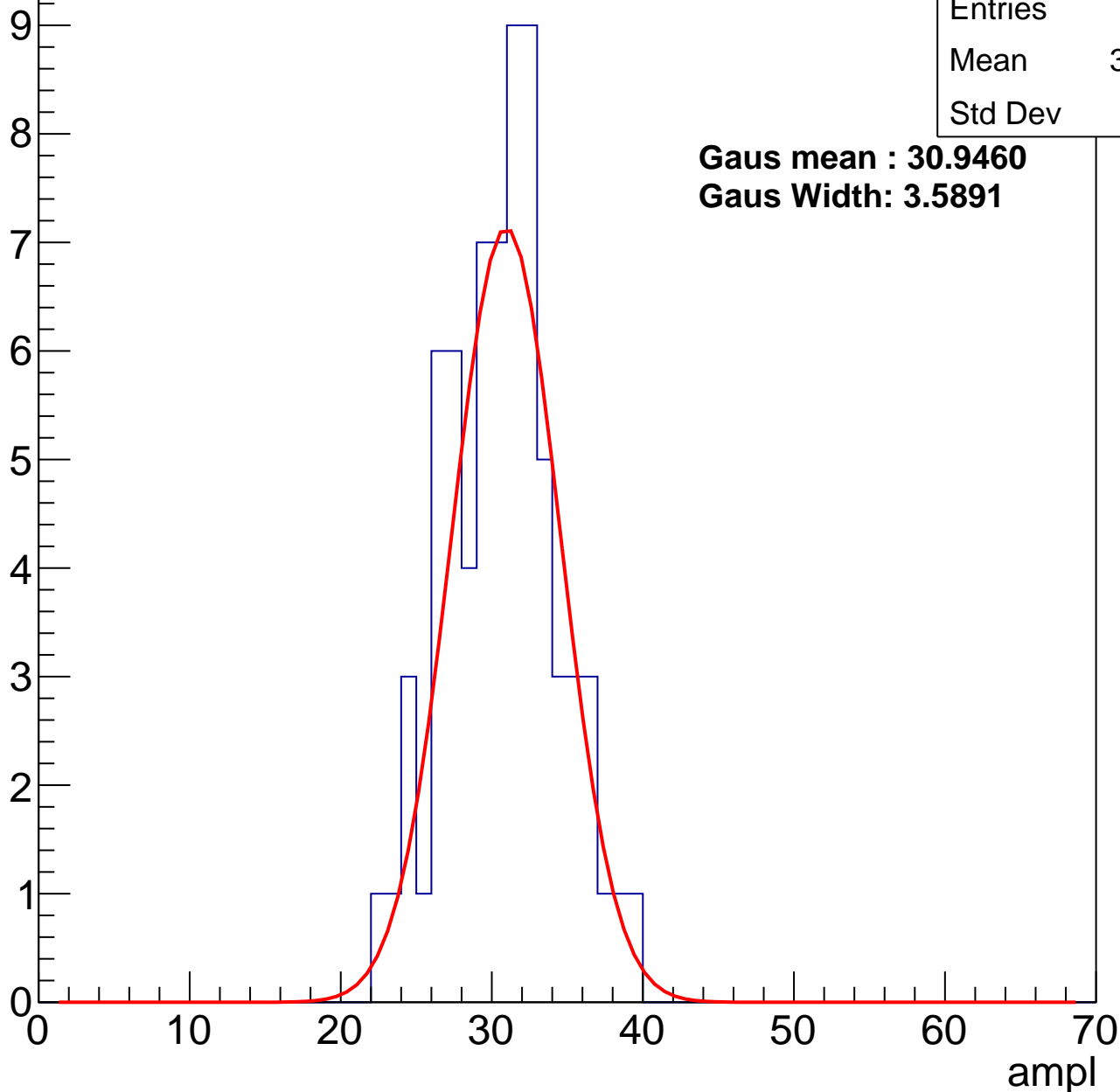
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.23
Std Dev	3.62

**Gaus mean : 30.9460**

**Gaus Width: 3.5891**



# B0L000S, U26-ch55, adc1

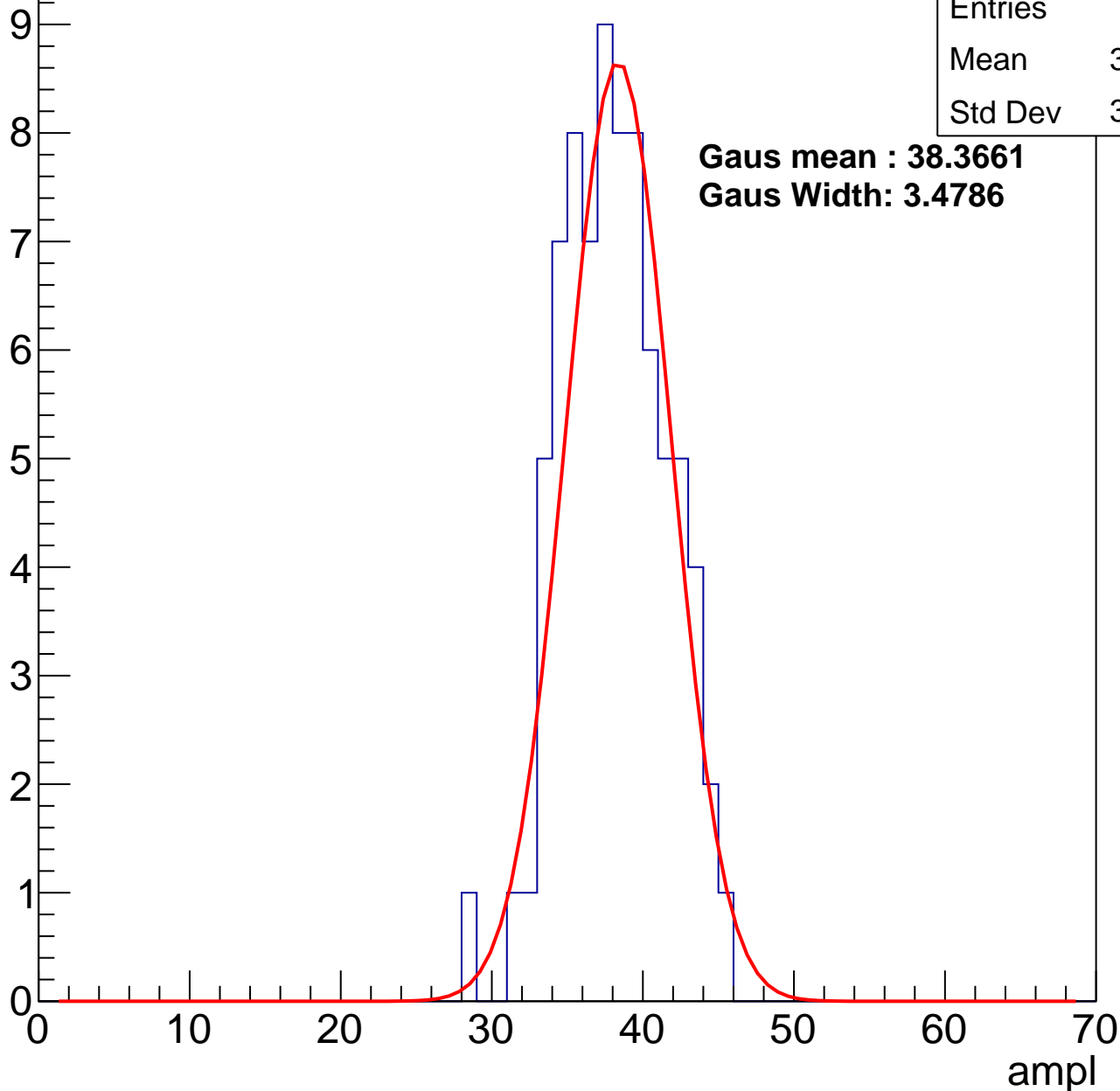
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	37.63
Std Dev	3.393

**Gaus mean : 38.3661**

**Gaus Width: 3.4786**



# B0L000S, U26-ch55, adc2

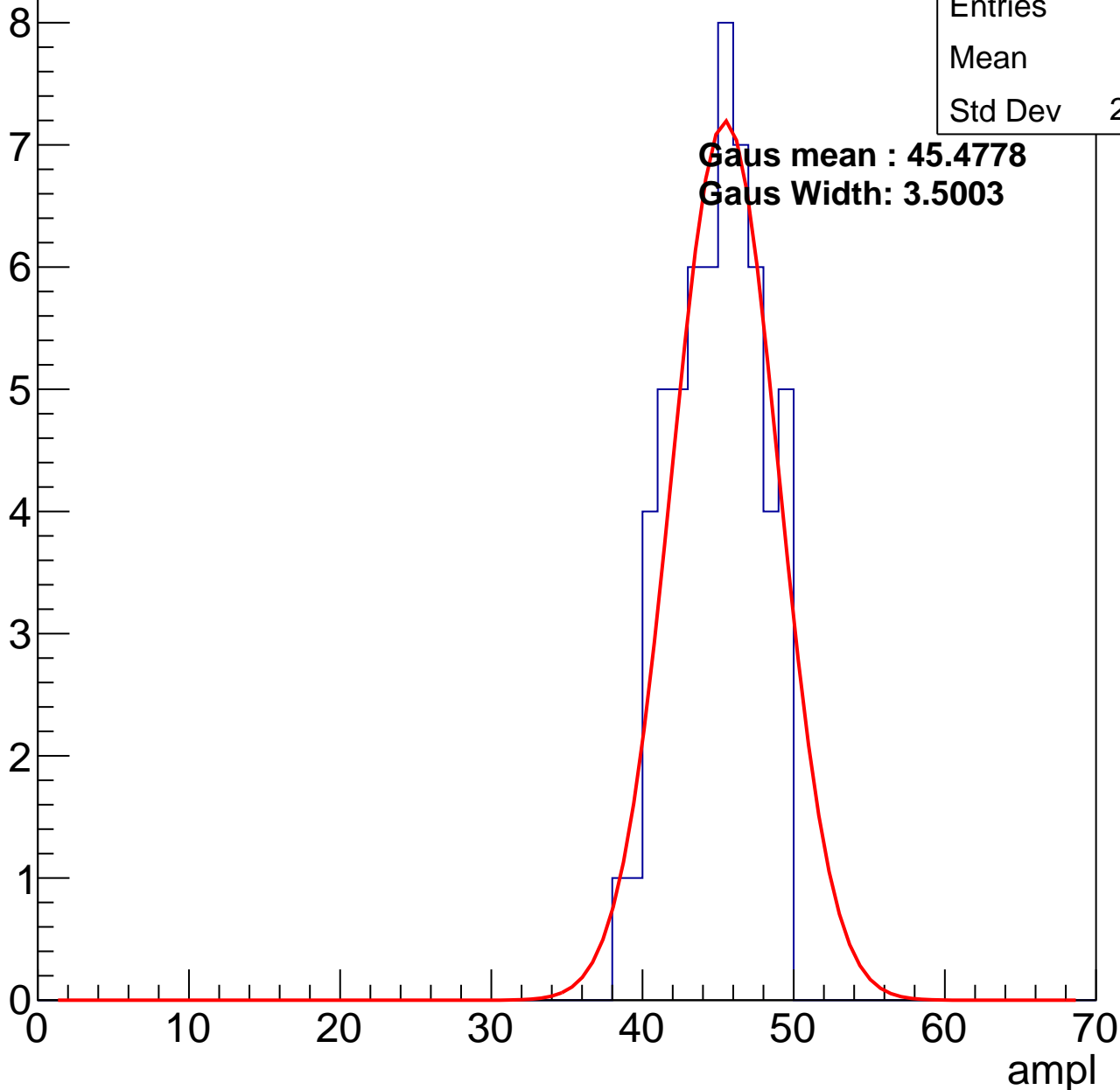
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.4
Std Dev	2.834

**Gaus mean : 45.4778**

**Gaus Width: 3.5003**

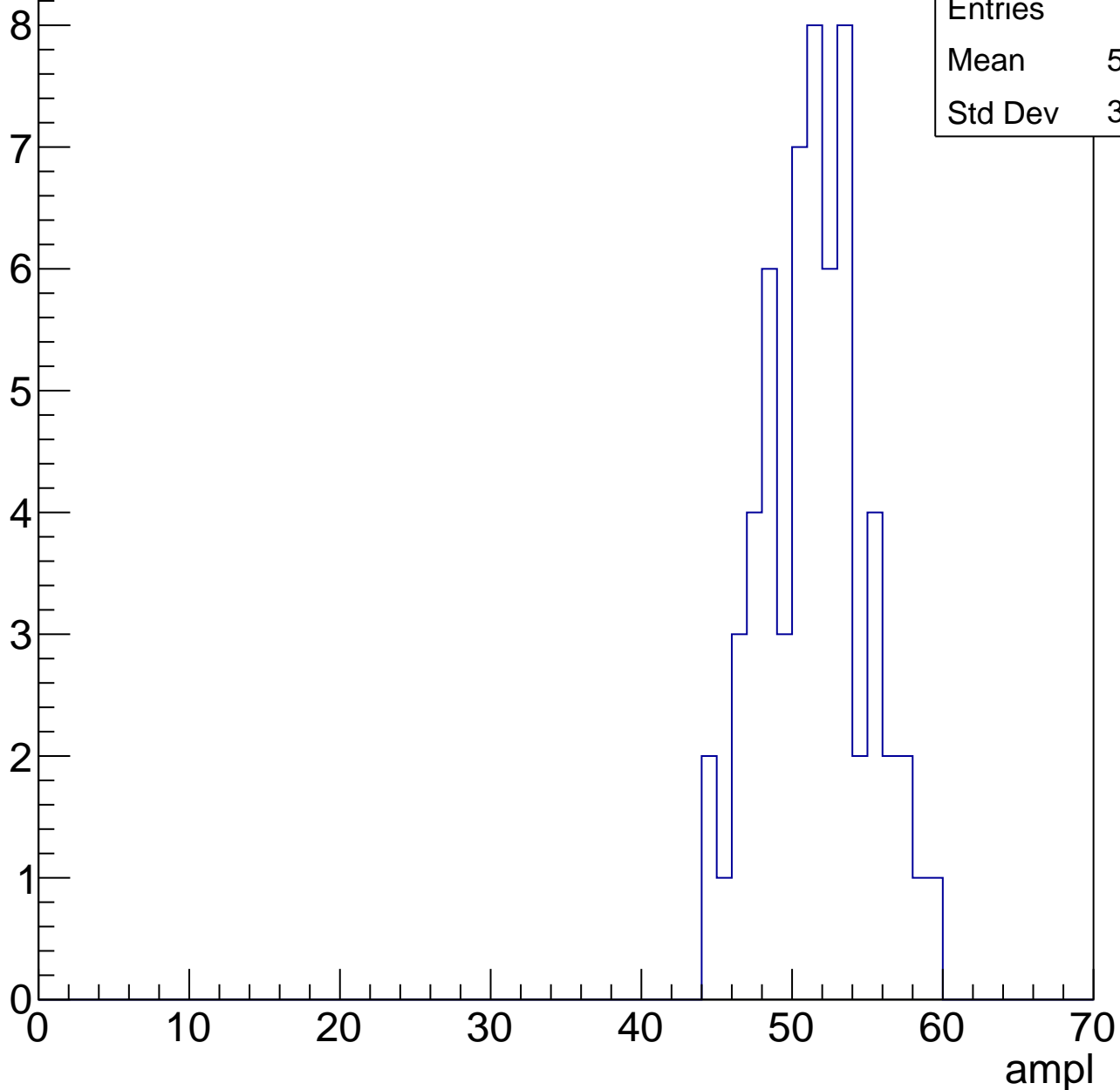


# B0L000S, U26-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.98
Std Dev	3.418

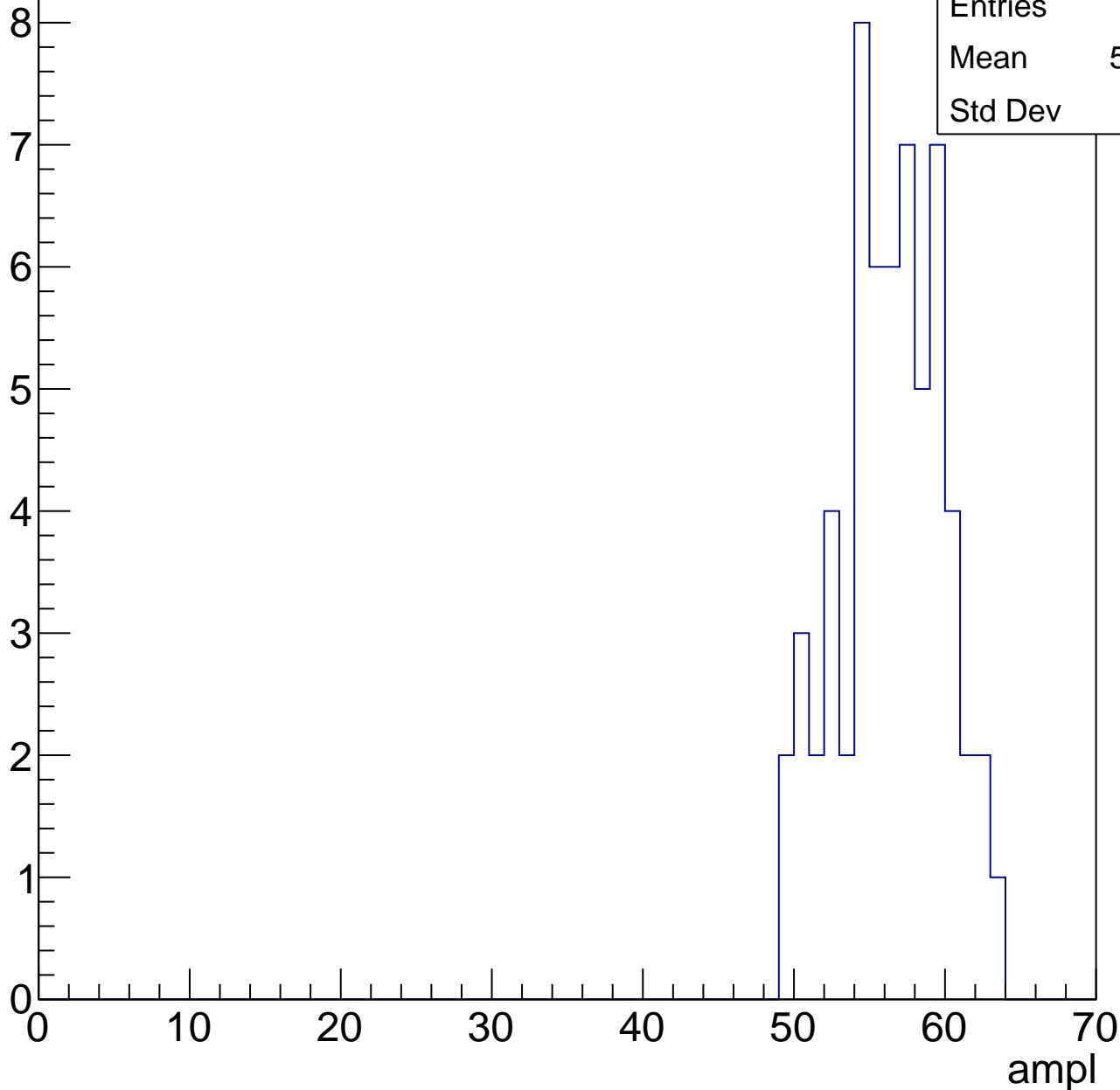


# B0L000S, U26-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	55.95
Std Dev	3.39



# B0L000S, U26-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

60.5

Std Dev

2.033

ampl

0

10

20

30

40

50

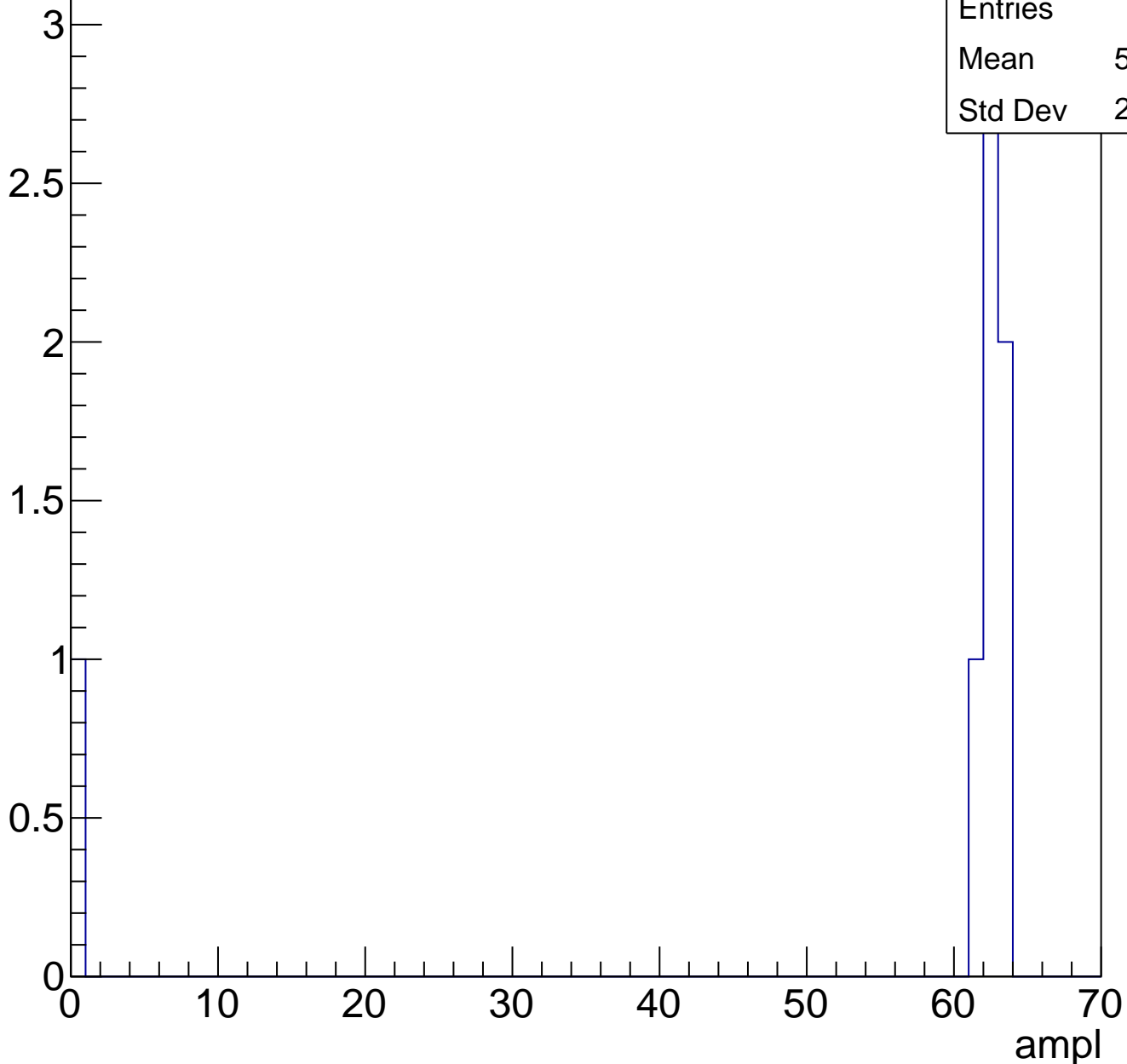
60

70

# B0L000S, U26-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch56, adc0

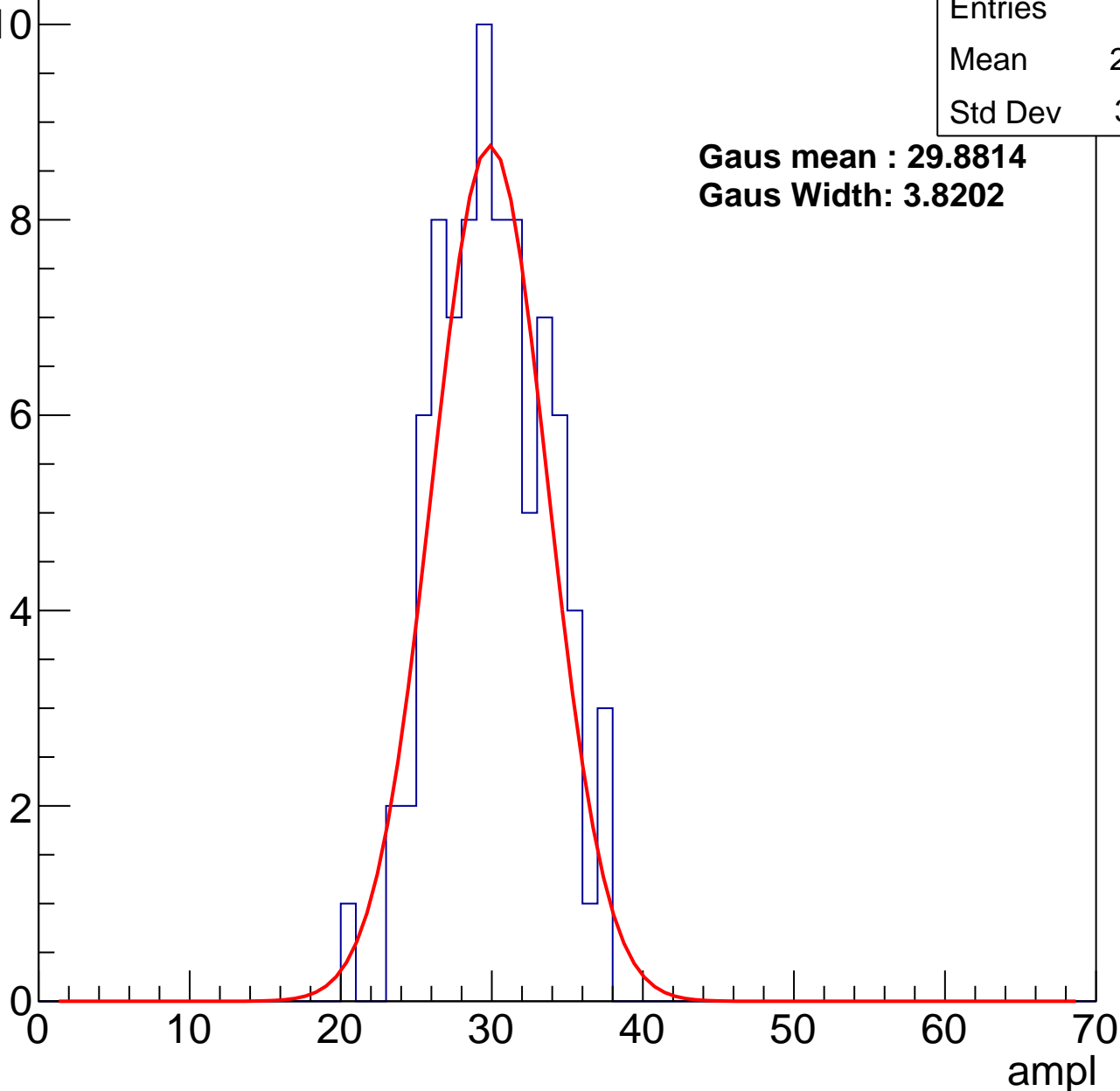
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	29.59
Std Dev	3.591

**Gaus mean : 29.8814**

**Gaus Width: 3.8202**



# B0L000S, U26-ch56, adc1

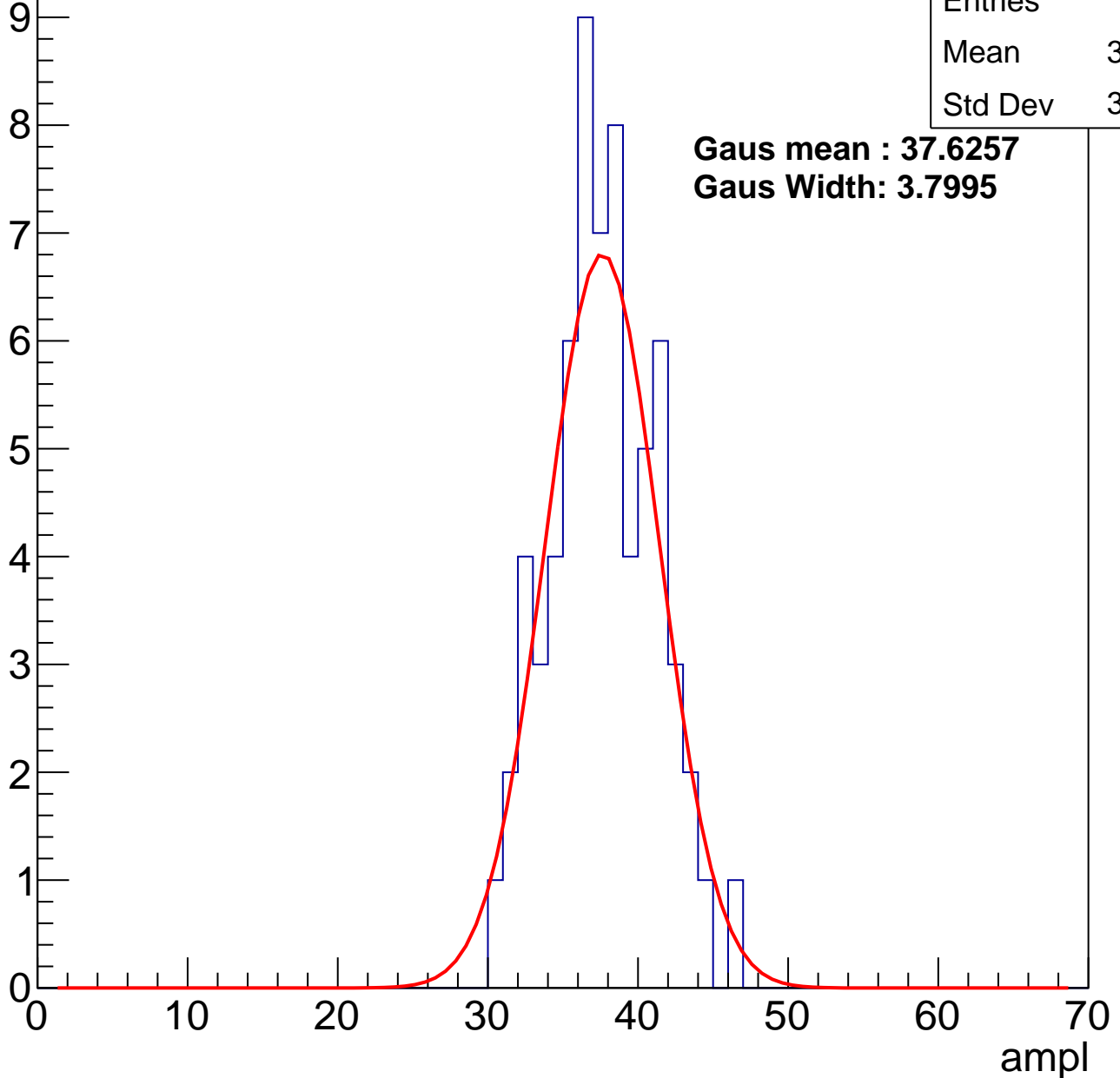
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.21
Std Dev	3.431

**Gaus mean : 37.6257**

**Gaus Width: 3.7995**



# B0L000S, U26-ch56, adc2

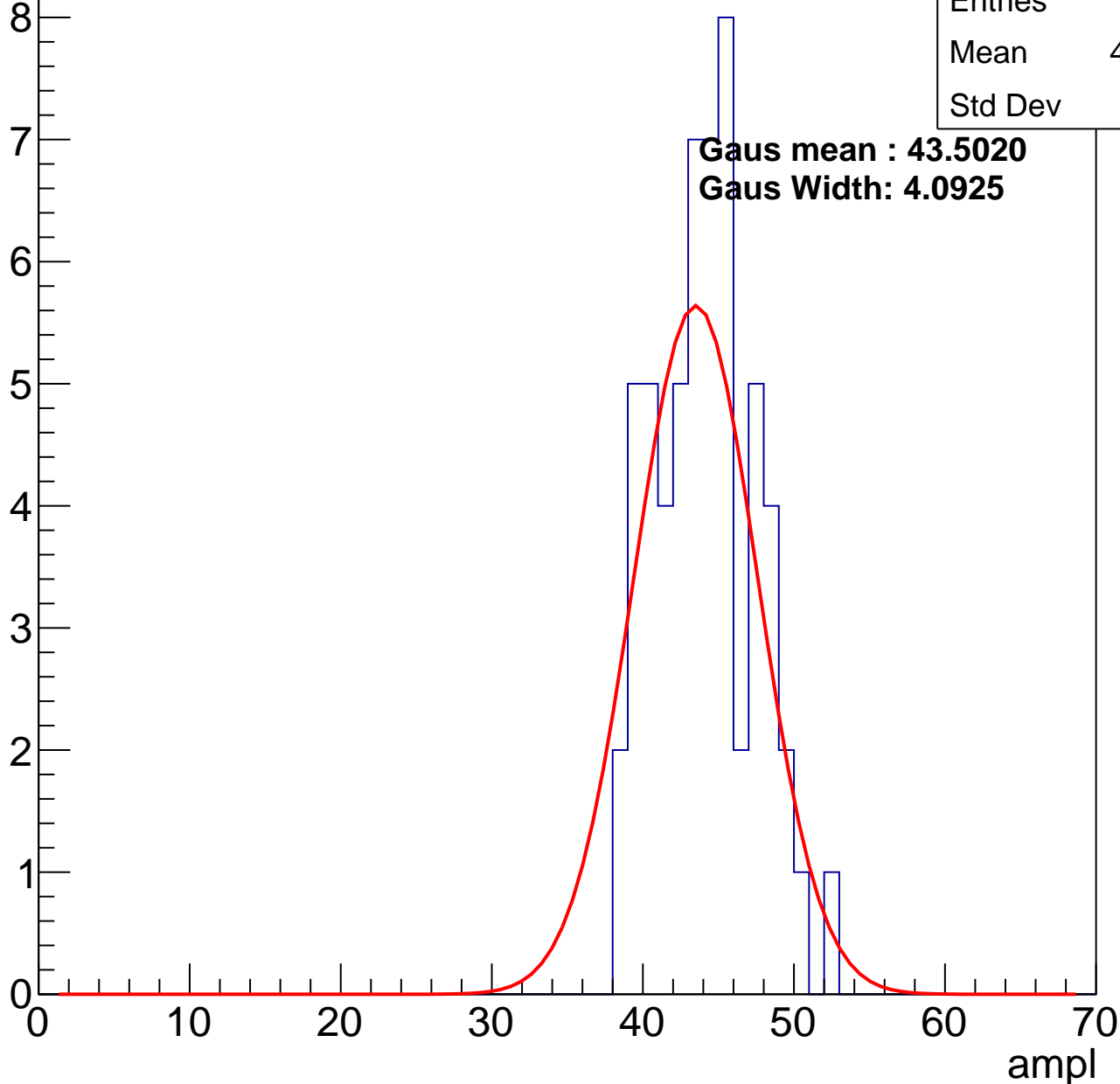
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.67
Std Dev	3.24

**Gaus mean : 43.5020**

**Gaus Width: 4.0925**

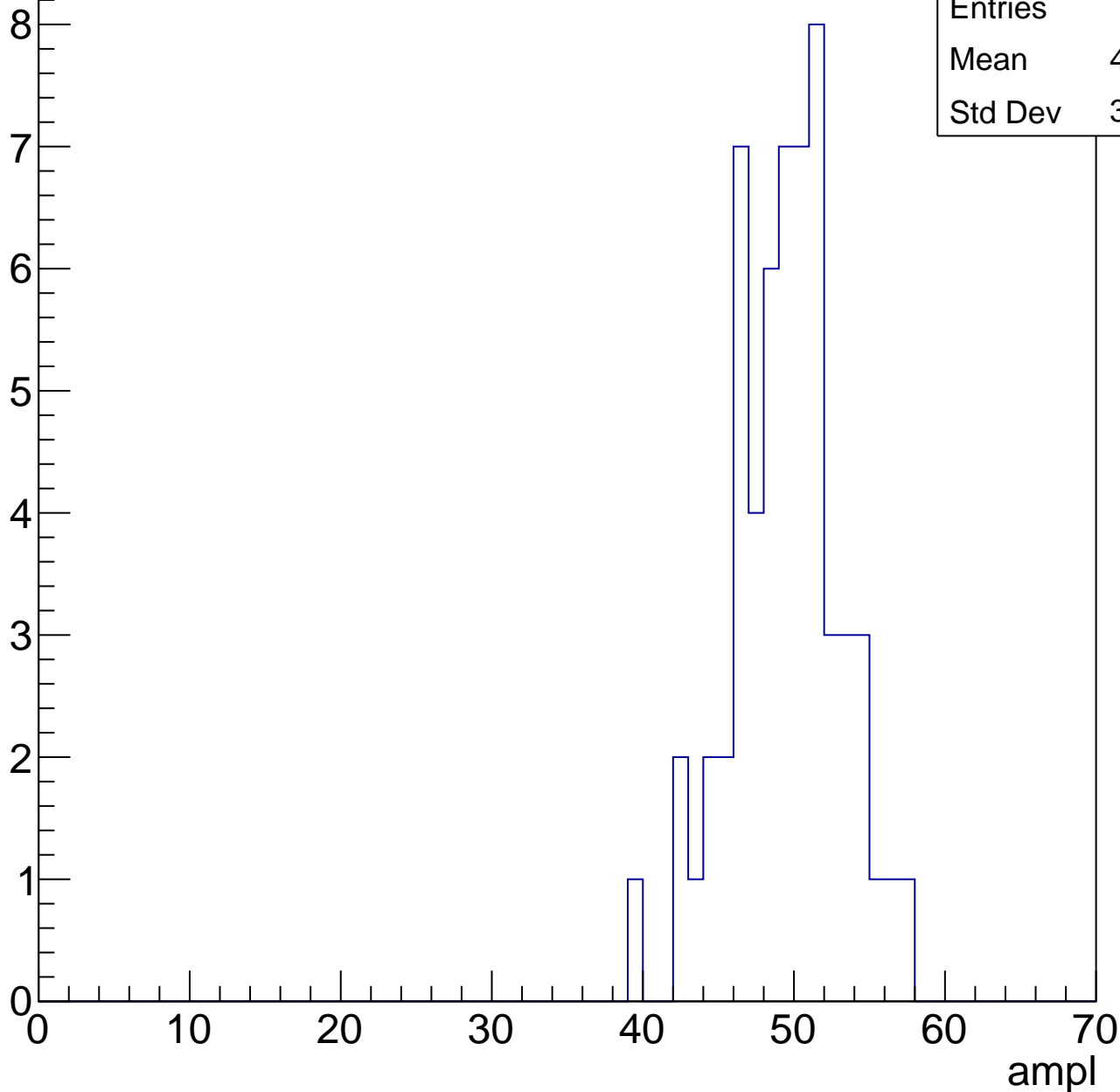


# B0L000S, U26-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	48.95
Std Dev	3.539

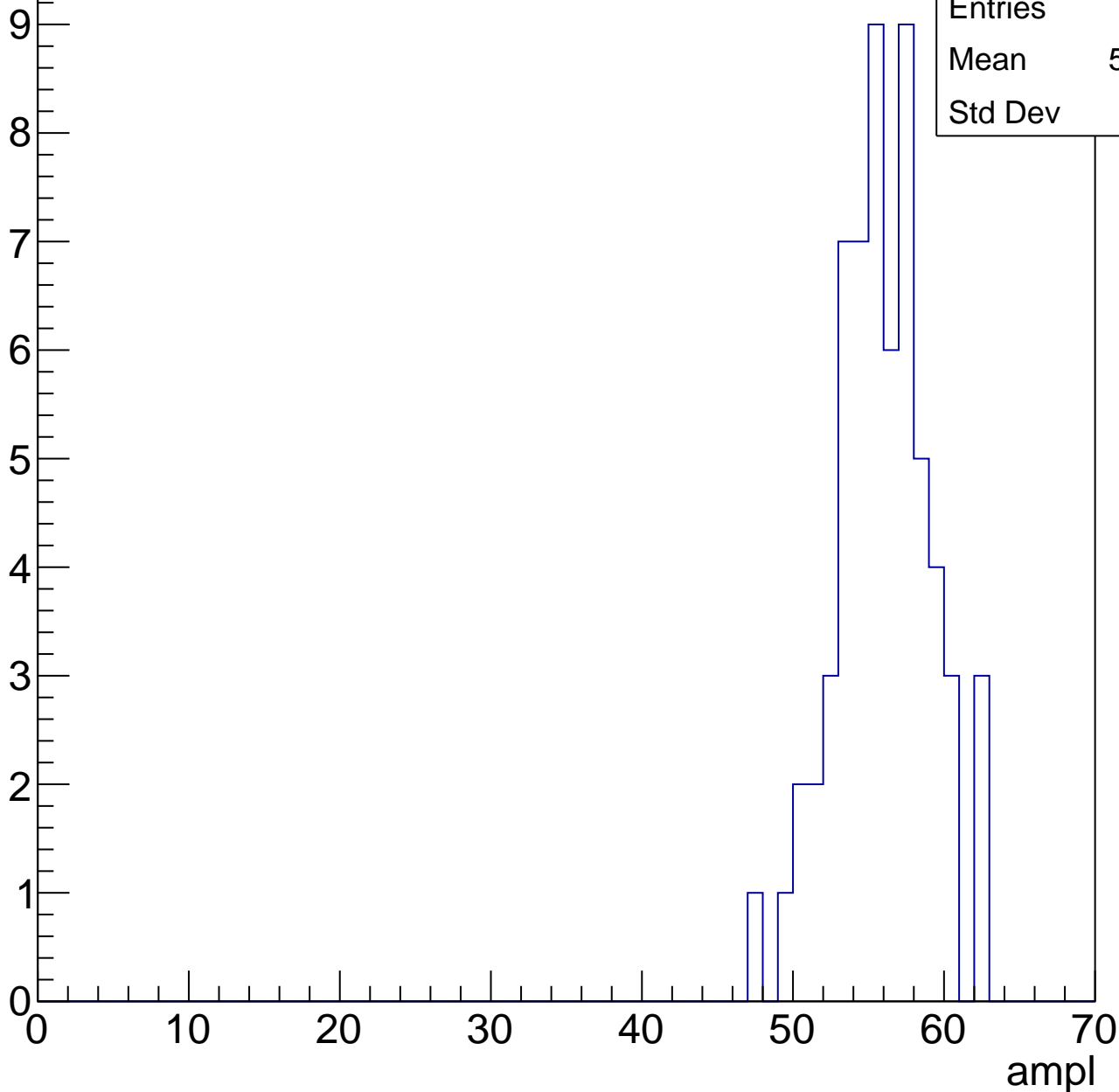


# B0L000S, U26-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

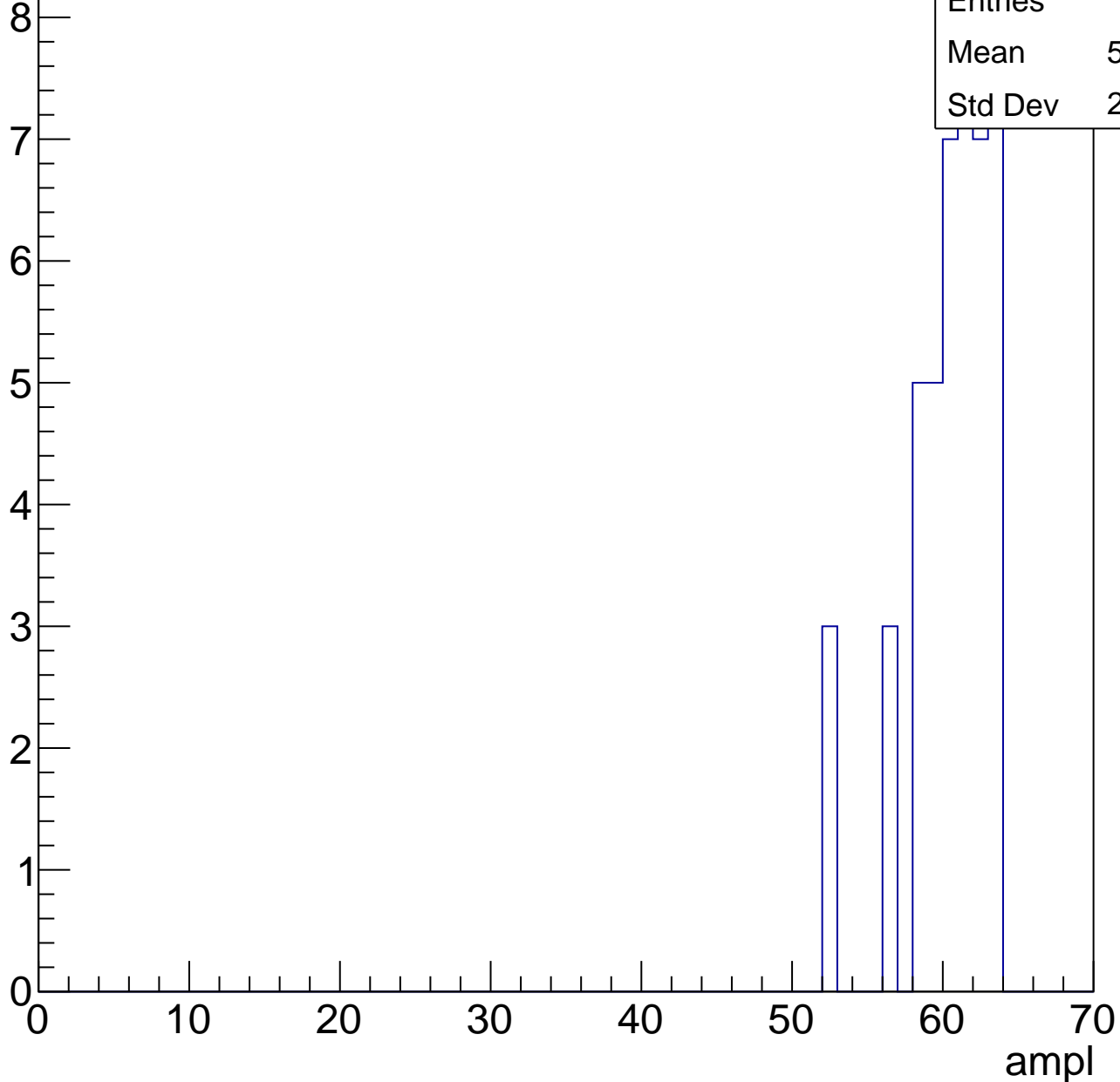
Entries	62
Mean	55.47
Std Dev	3.12



# B0L000S, U26-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

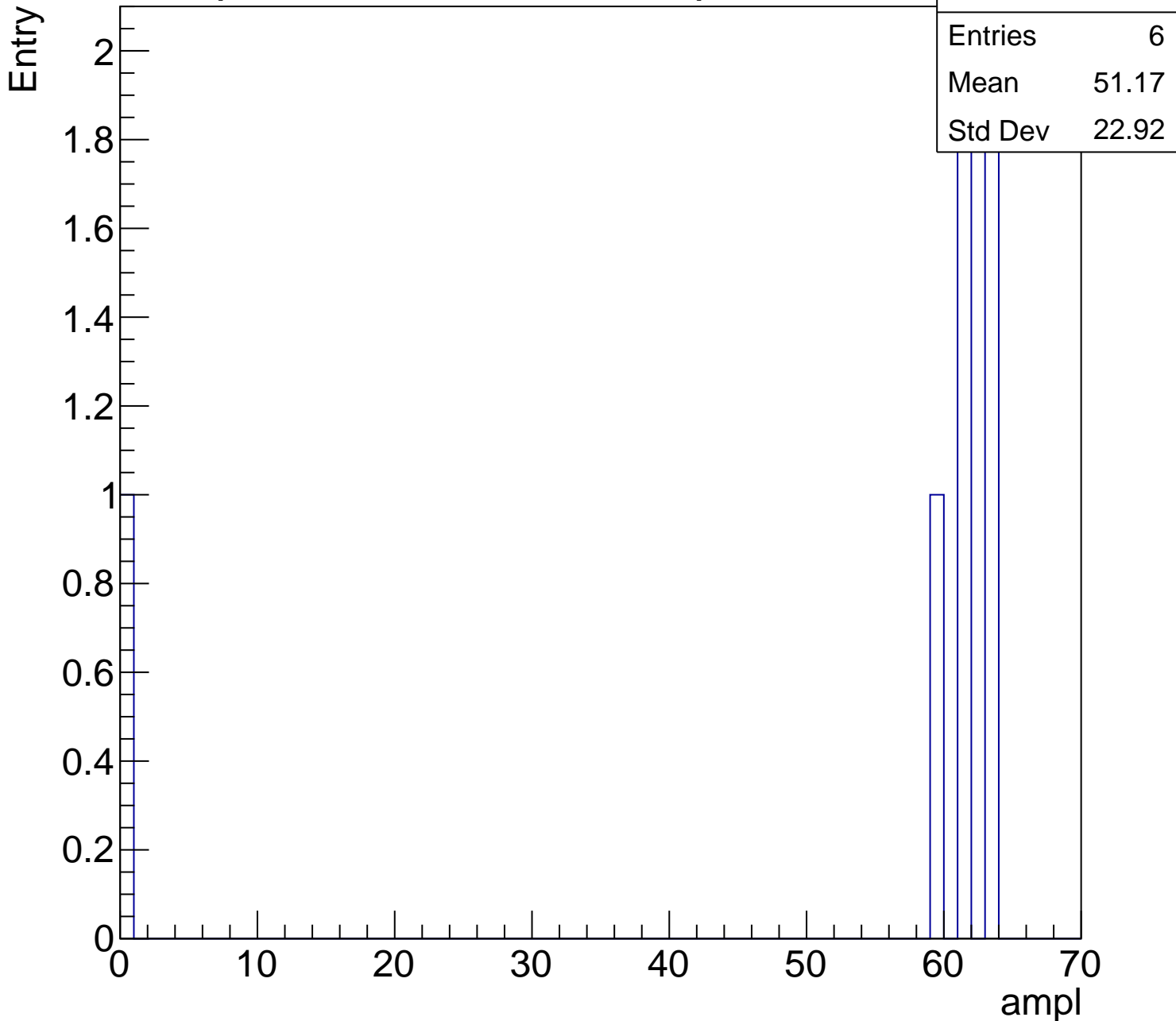
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.17
Std Dev	22.92

0 10 20 30 40 50 60 70

ampl





# B0L000S, U26-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch57, adc0

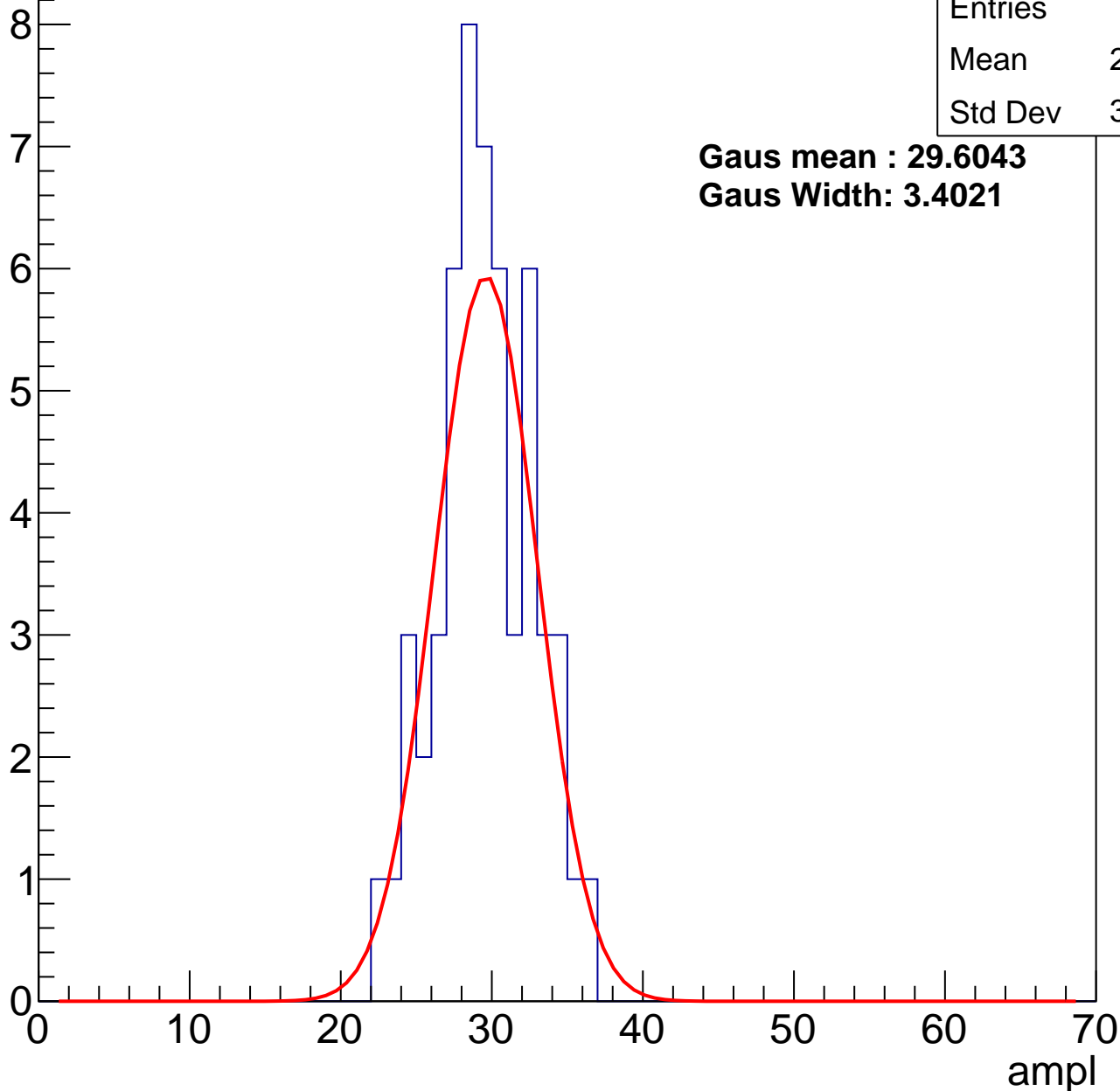
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	29.09
Std Dev	3.134

**Gaus mean : 29.6043**

**Gaus Width: 3.4021**



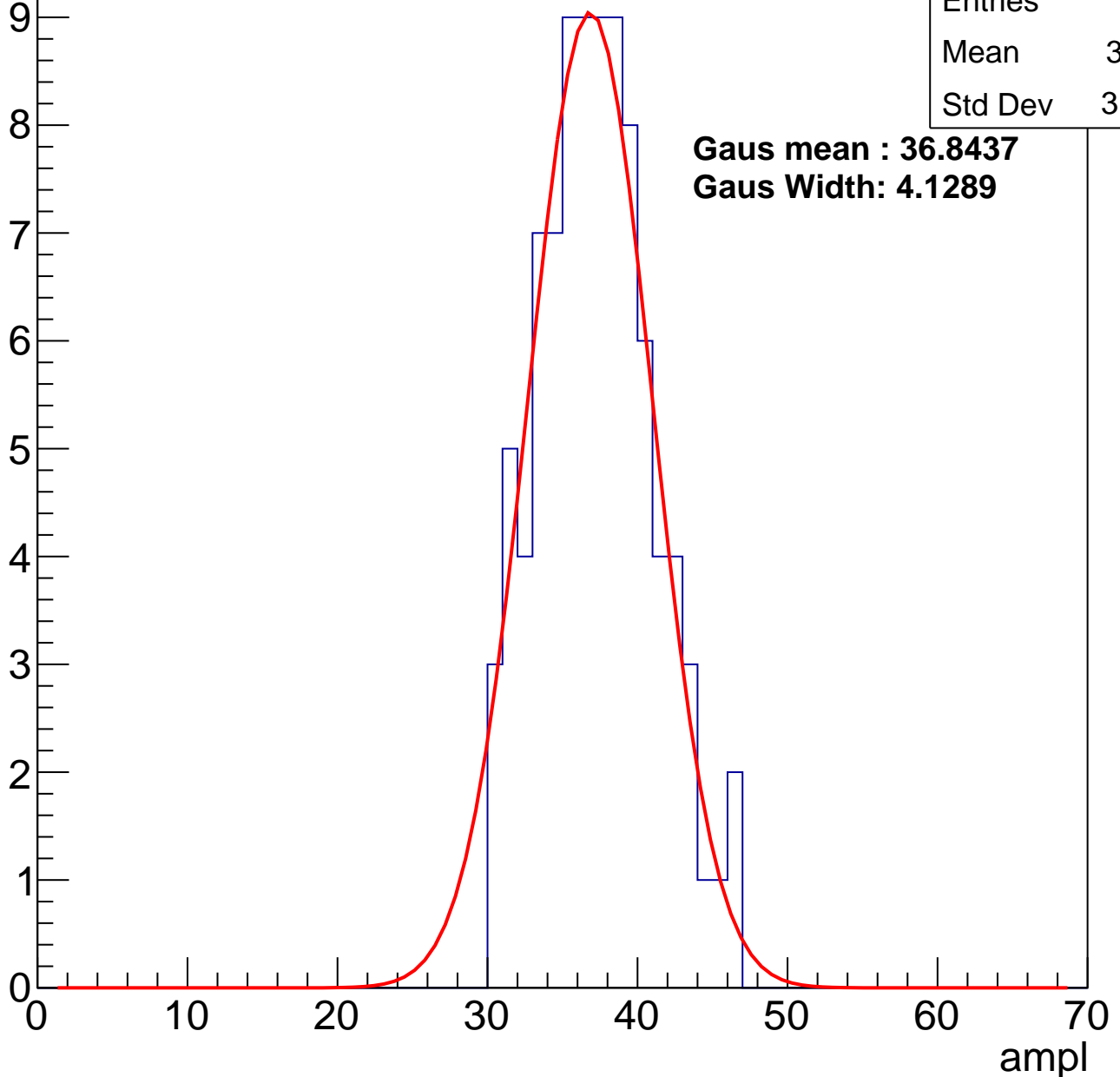
# B0L000S, U26-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	91
Mean	36.81
Std Dev	3.759

**Gaus mean : 36.8437**  
**Gaus Width: 4.1289**



# B0L000S, U26-ch57, adc2

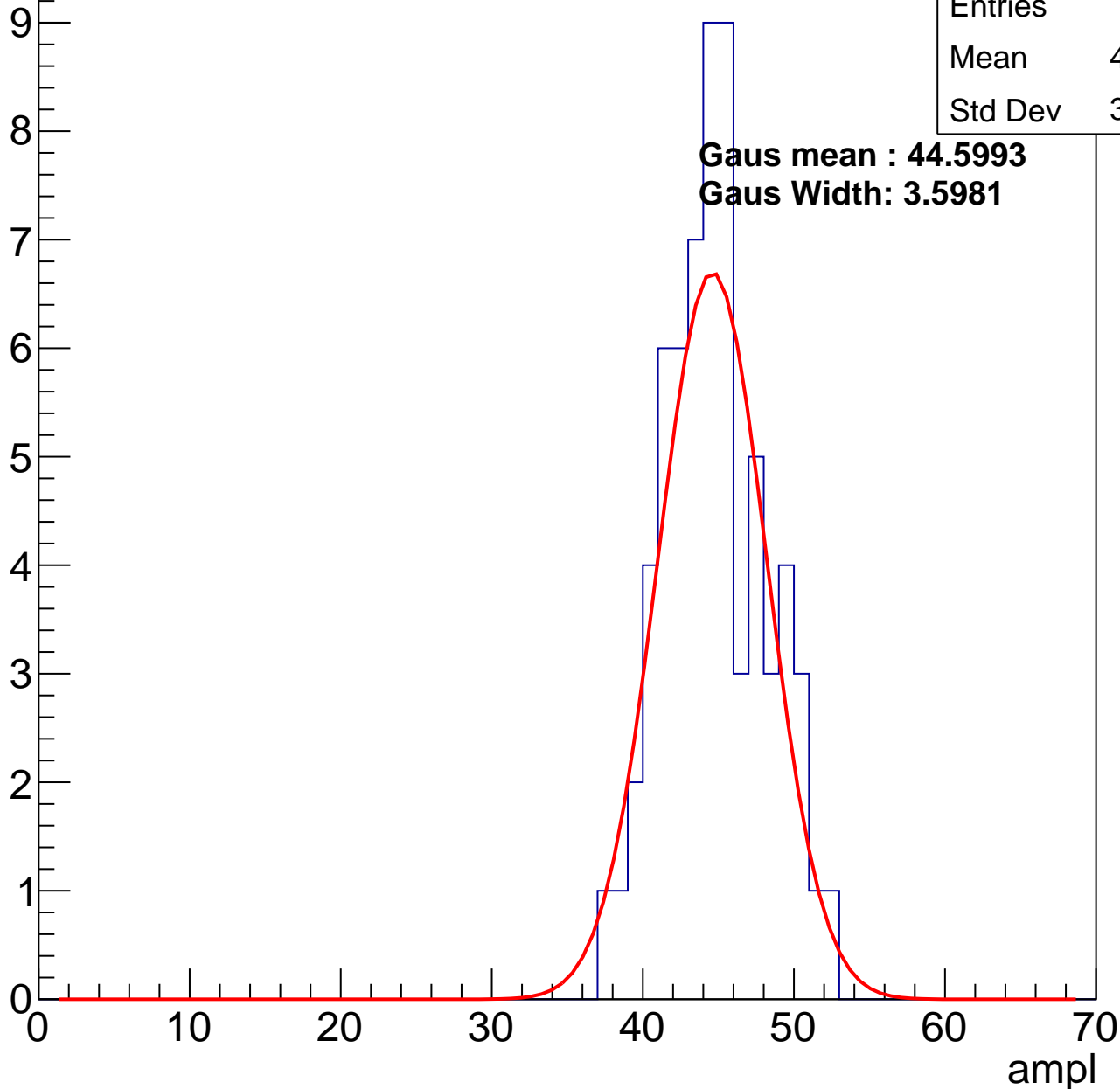
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.29
Std Dev	3.313

**Gaus mean : 44.5993**

**Gaus Width: 3.5981**

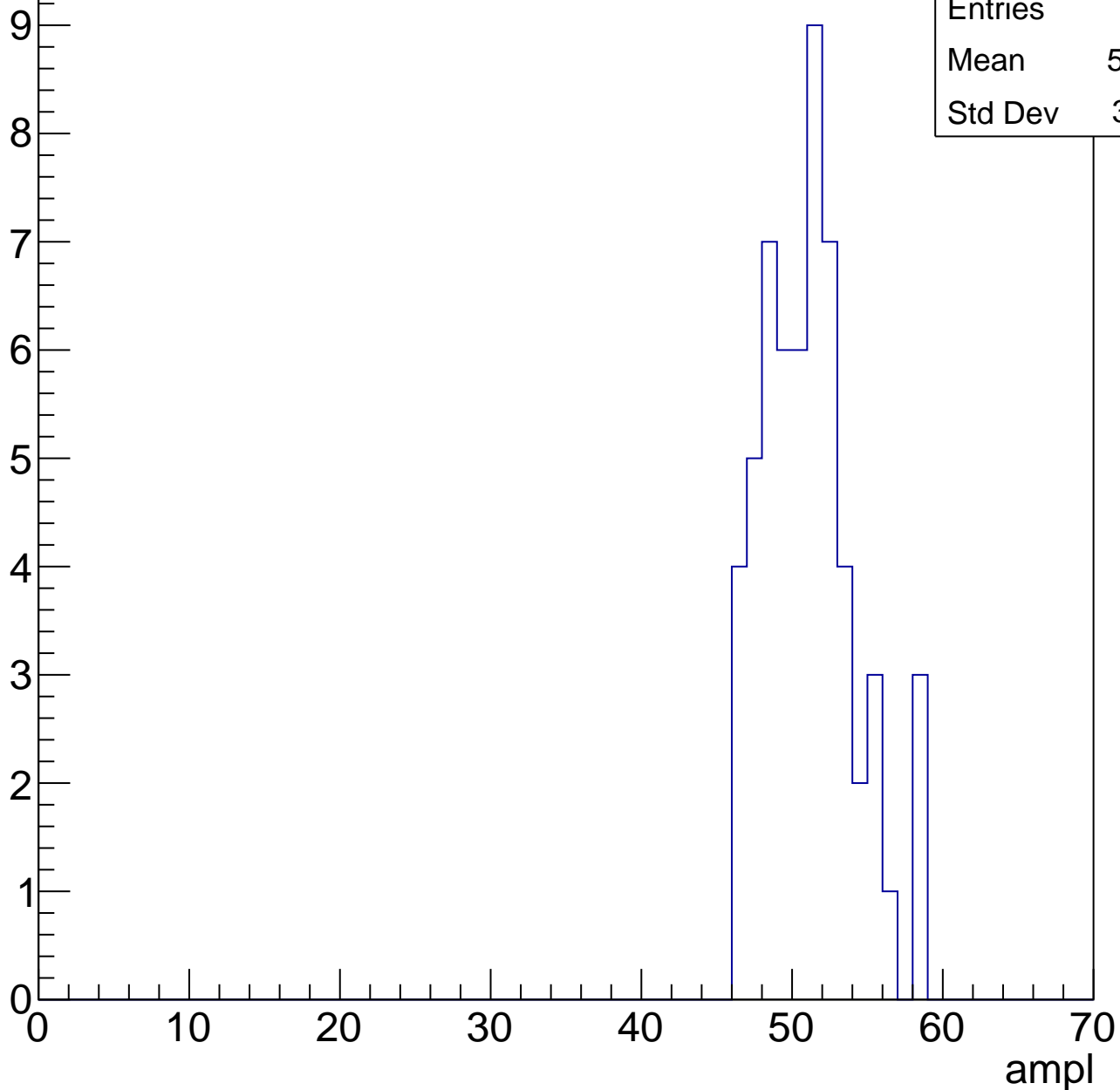


# B0L000S, U26-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

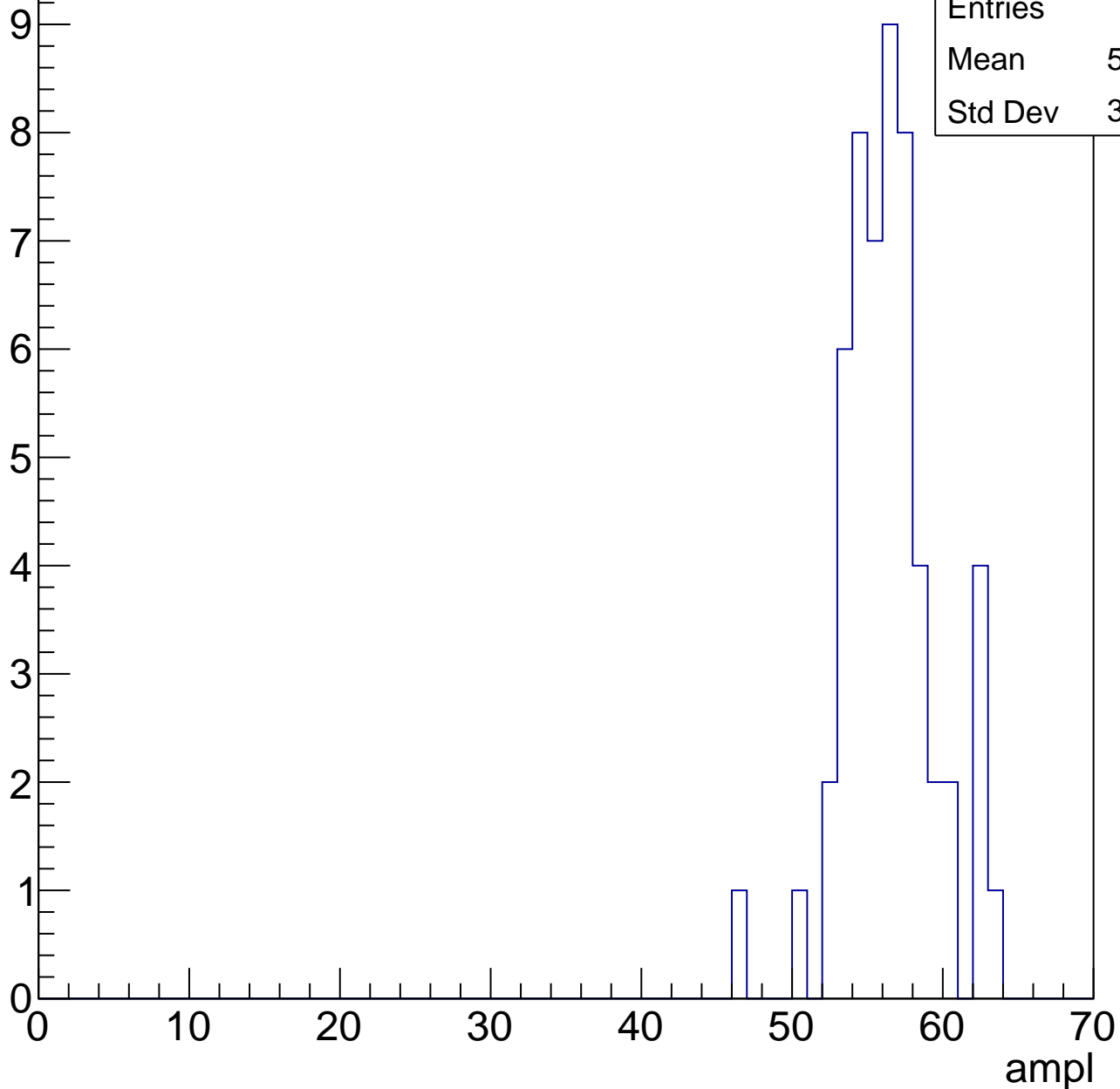
Entries	57
Mean	50.65
Std Dev	3.041



# B0L000S, U26-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

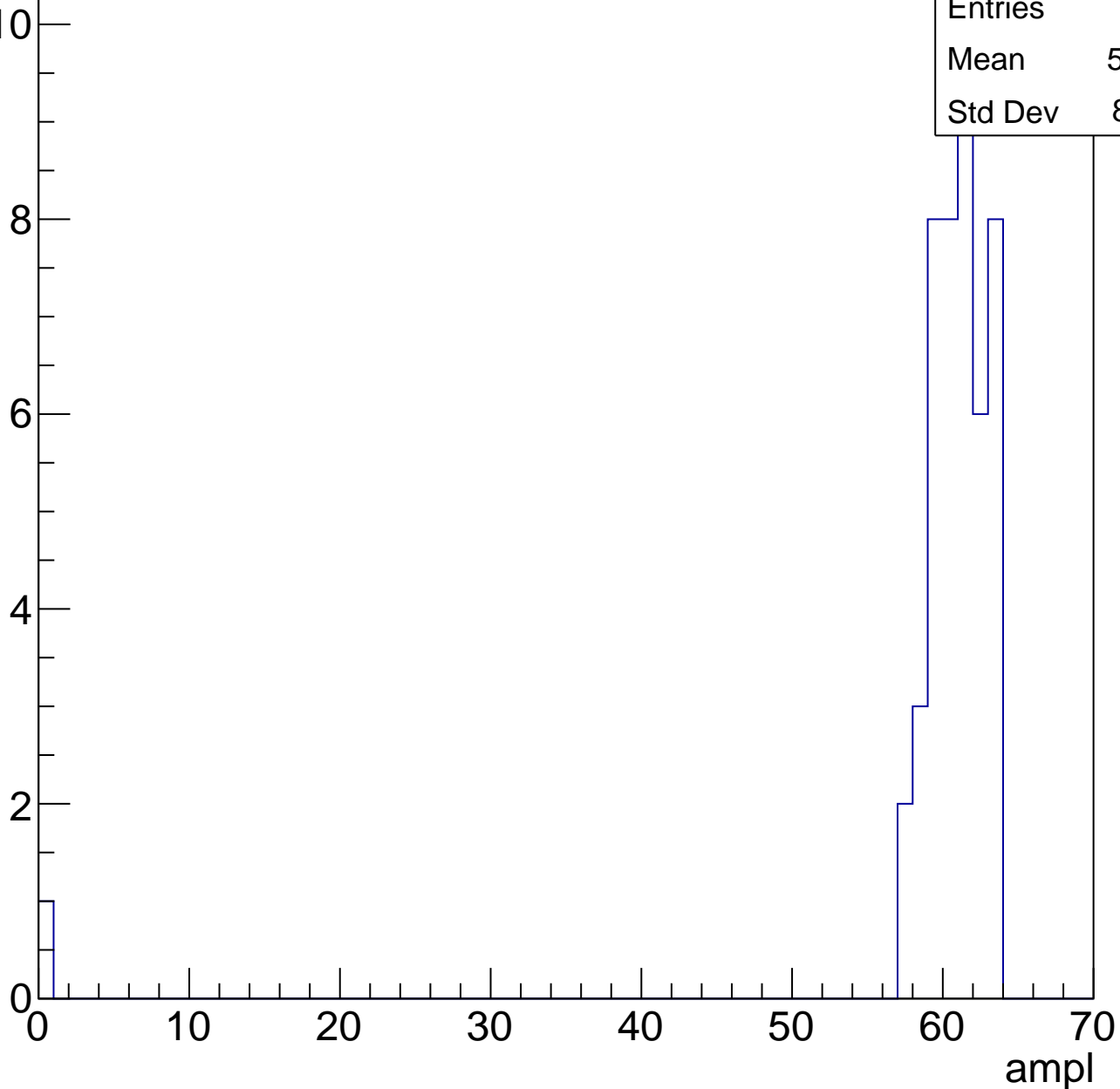


# B0L000S, U26-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	59.26
Std Dev	8.991



# B0L000S, U26-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch58, adc0

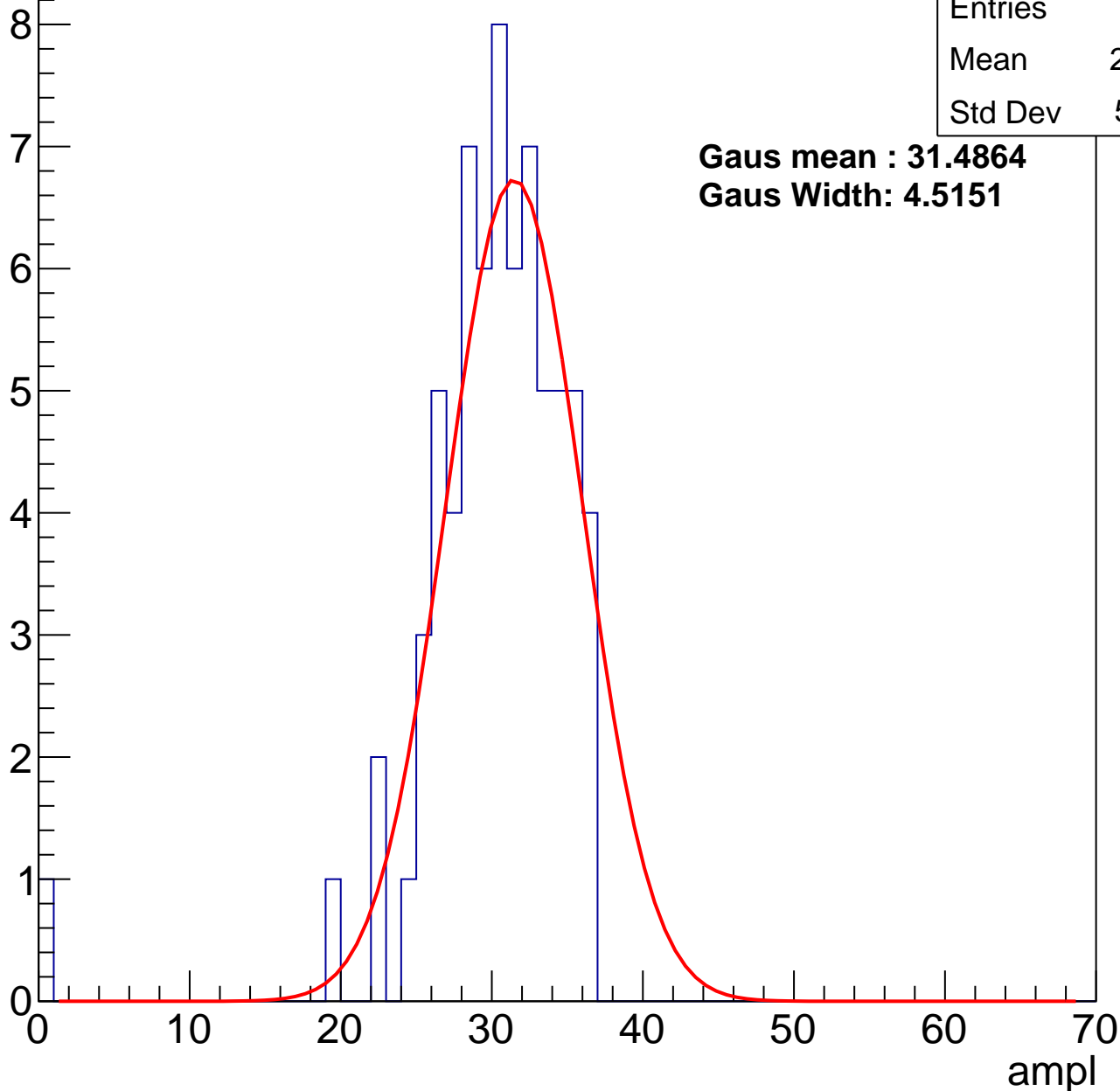
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.63
Std Dev	5.111

**Gaus mean : 31.4864**

**Gaus Width: 4.5151**



# B0L000S, U26-ch58, adc1

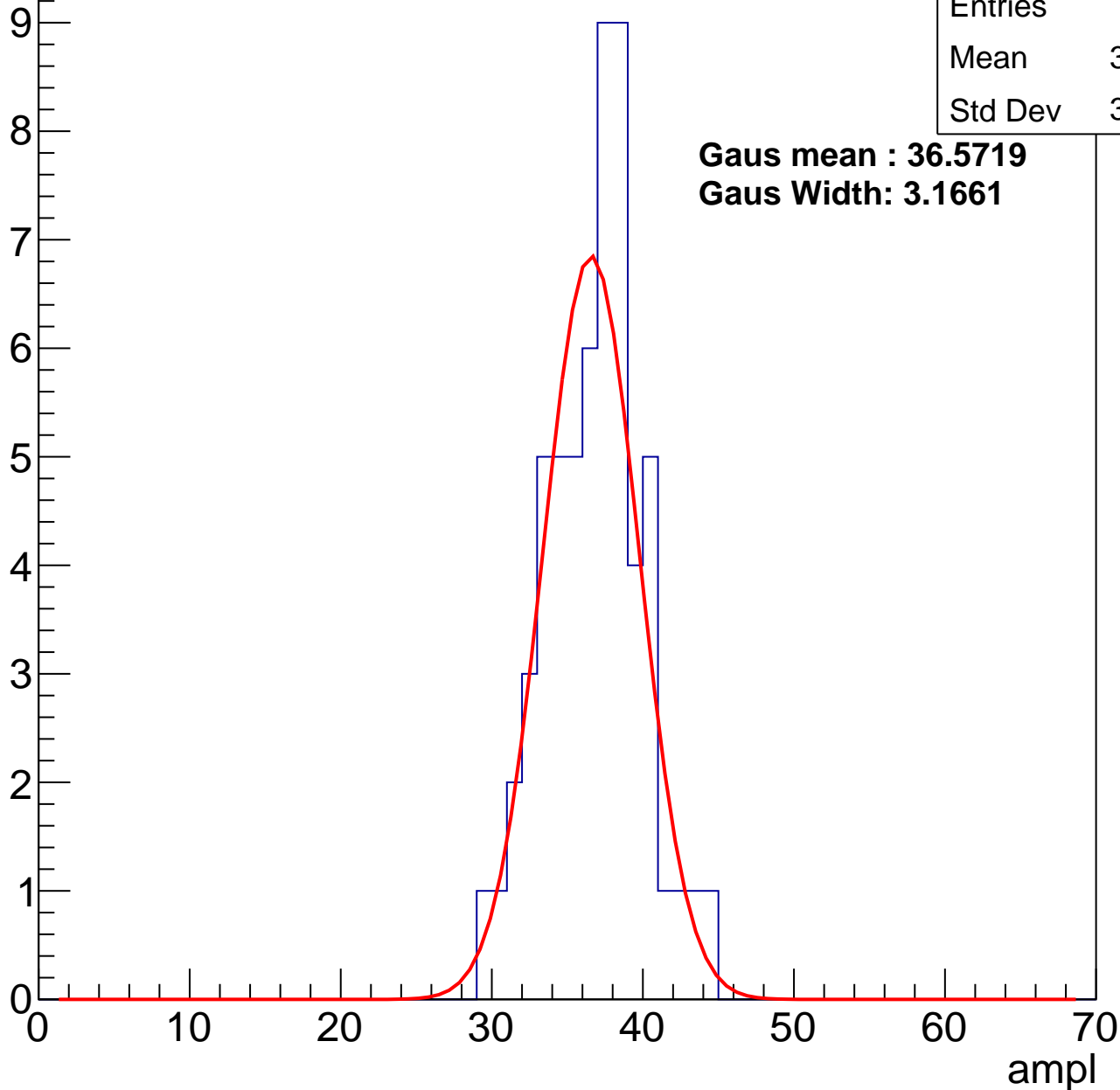
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.34
Std Dev	3.139

**Gaus mean : 36.5719**

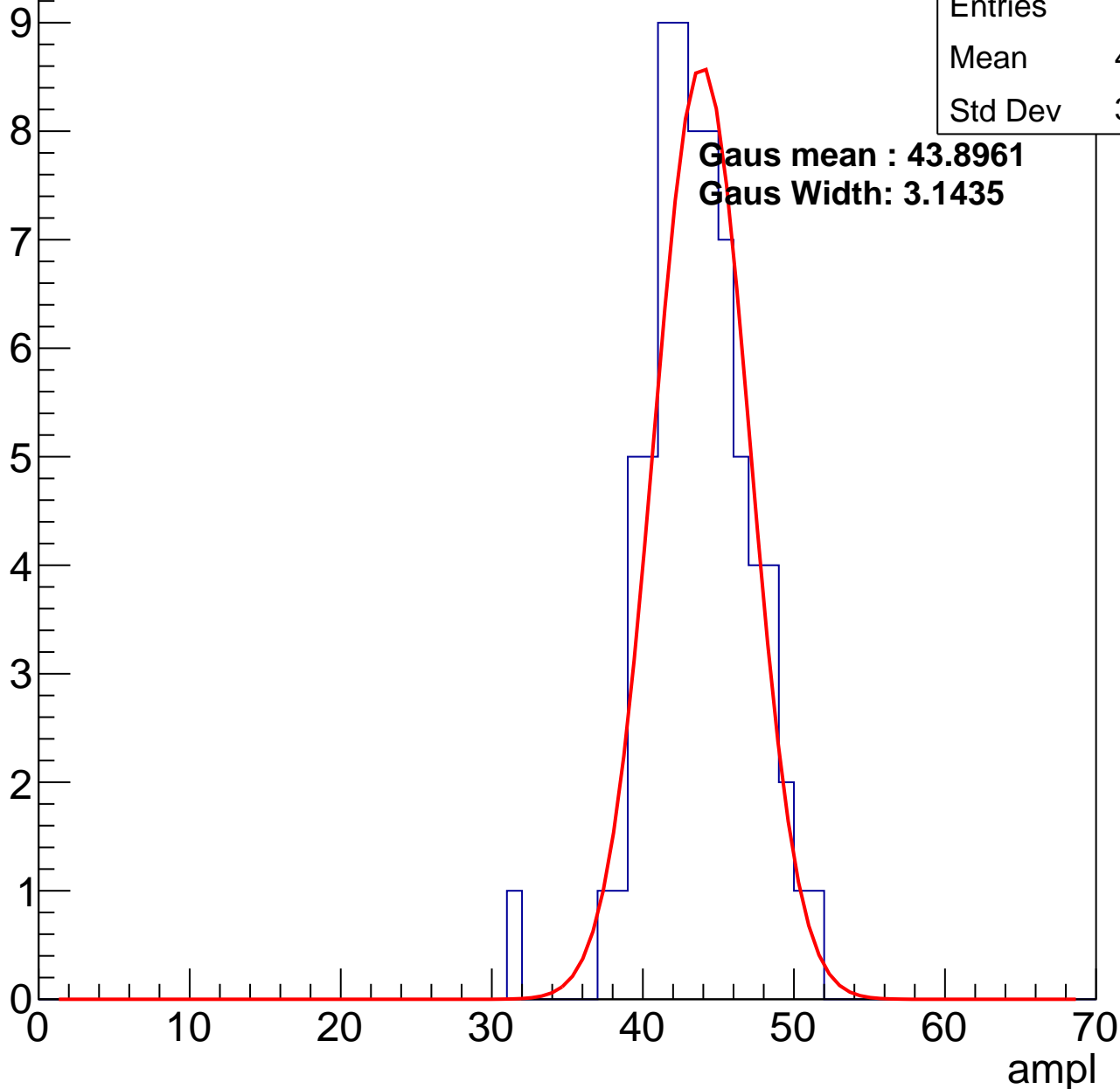
**Gaus Width: 3.1661**



# B0L000S, U26-ch58, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

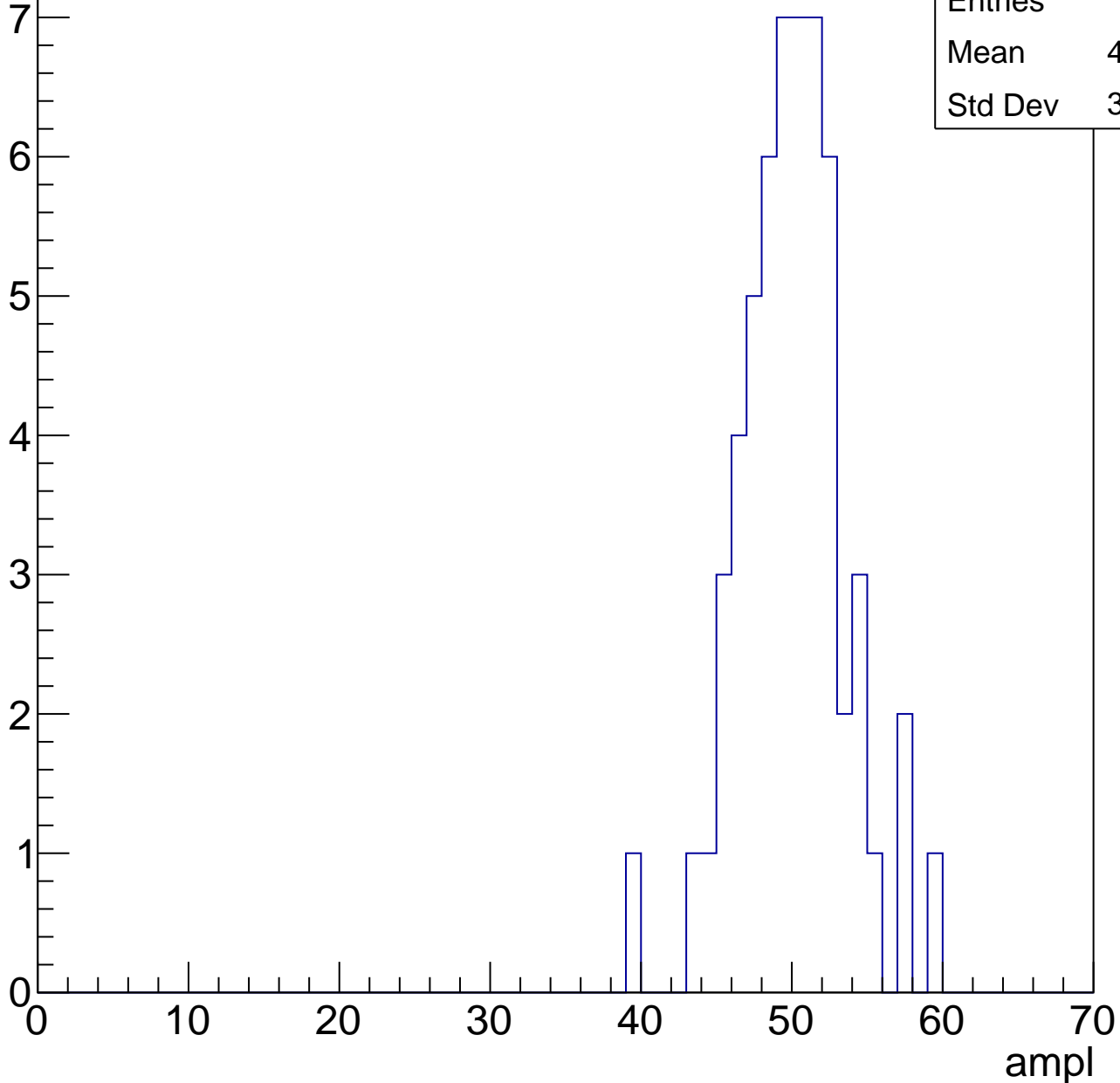


# B0L000S, U26-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	49.58
Std Dev	3.534

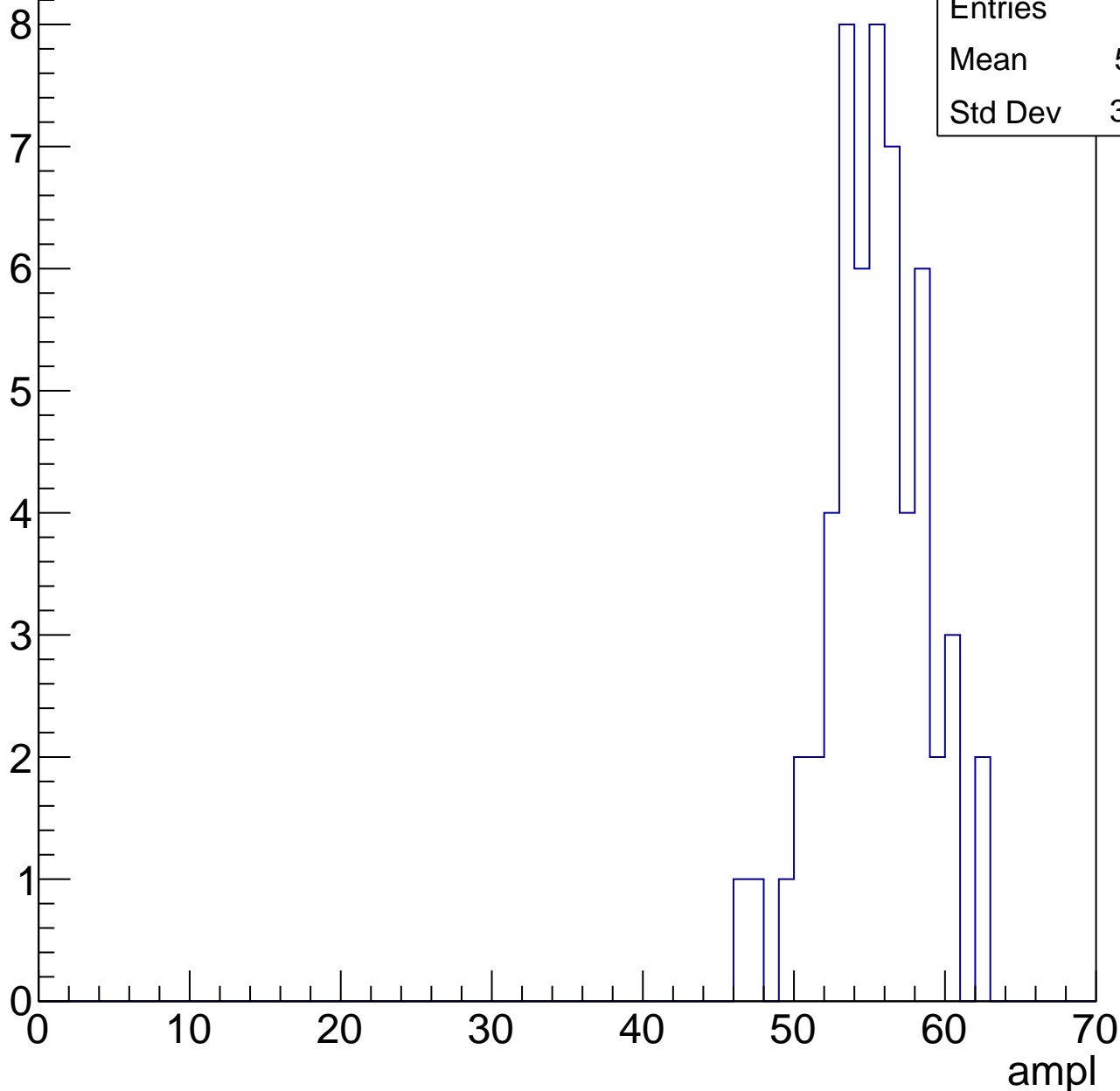


# B0L000S, U26-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	54.91
Std Dev	3.294

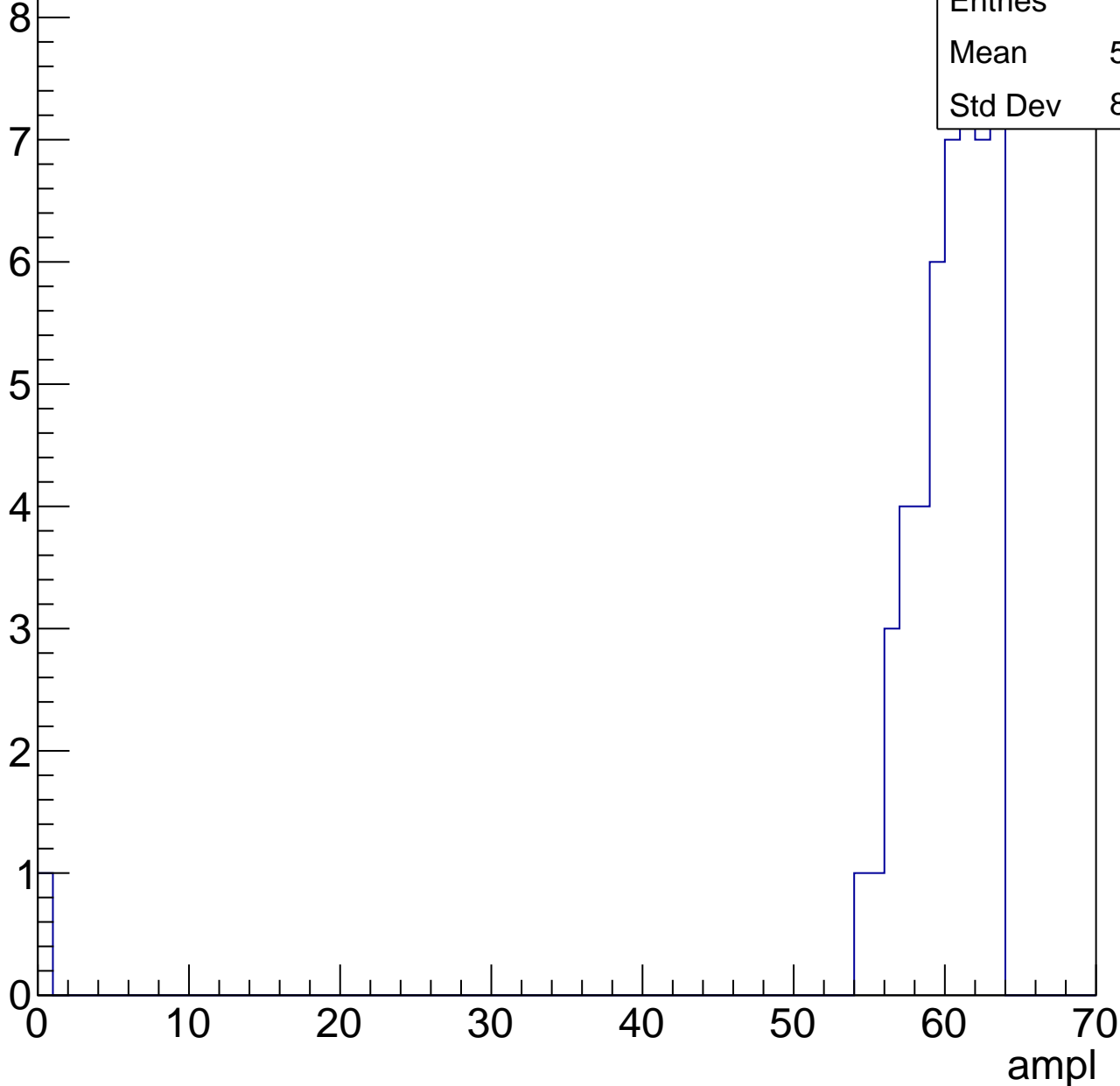


# B0L000S, U26-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	58.74
Std Dev	8.713



# B0L000S, U26-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	1.384

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch59, adc0

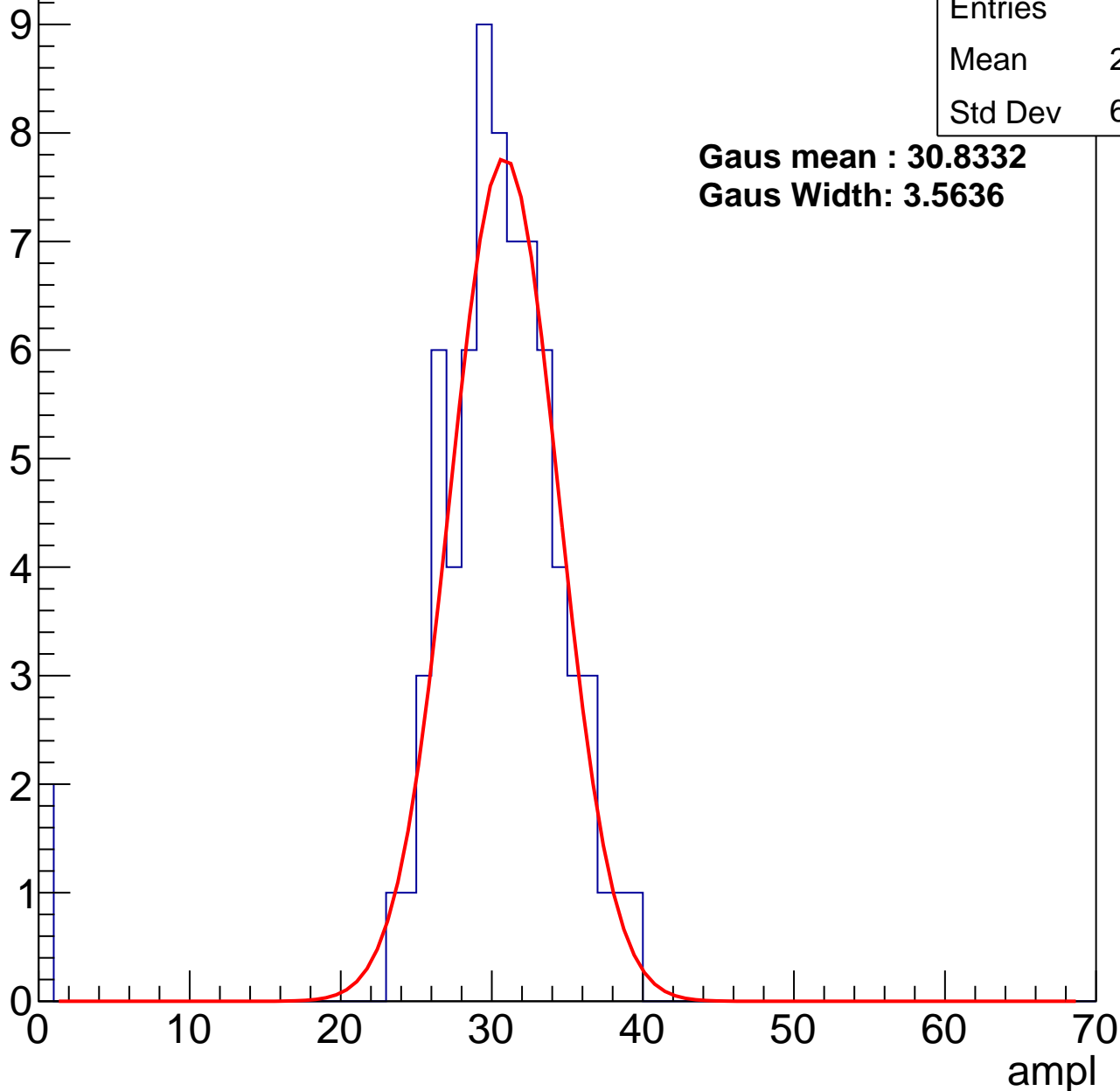
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.55
Std Dev	6.014

**Gaus mean : 30.8332**

**Gaus Width: 3.5636**



# B0L000S, U26-ch59, adc1

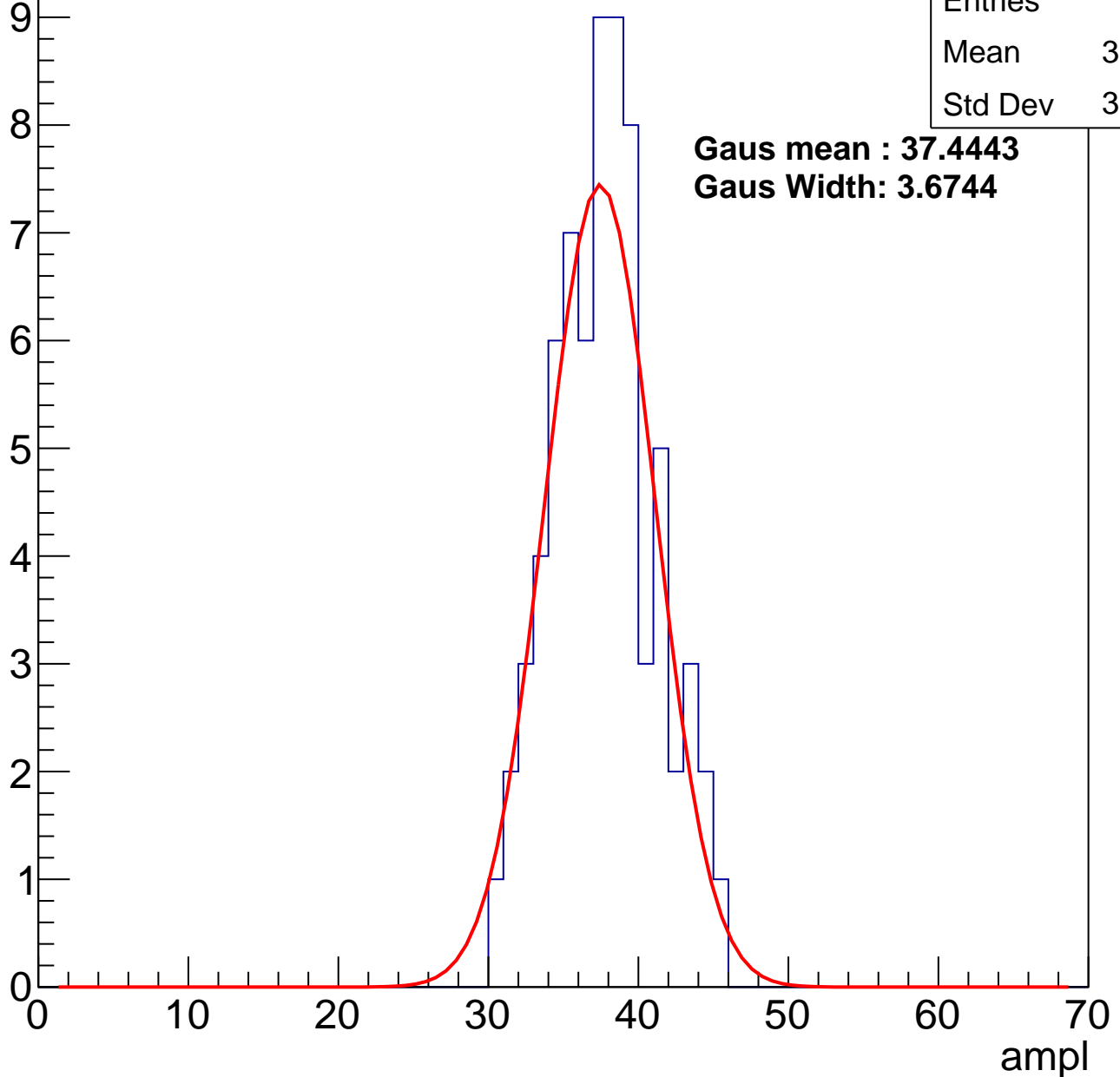
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.23
Std Dev	3.383

**Gaus mean : 37.4443**

**Gaus Width: 3.6744**



# B0L000S, U26-ch59, adc2

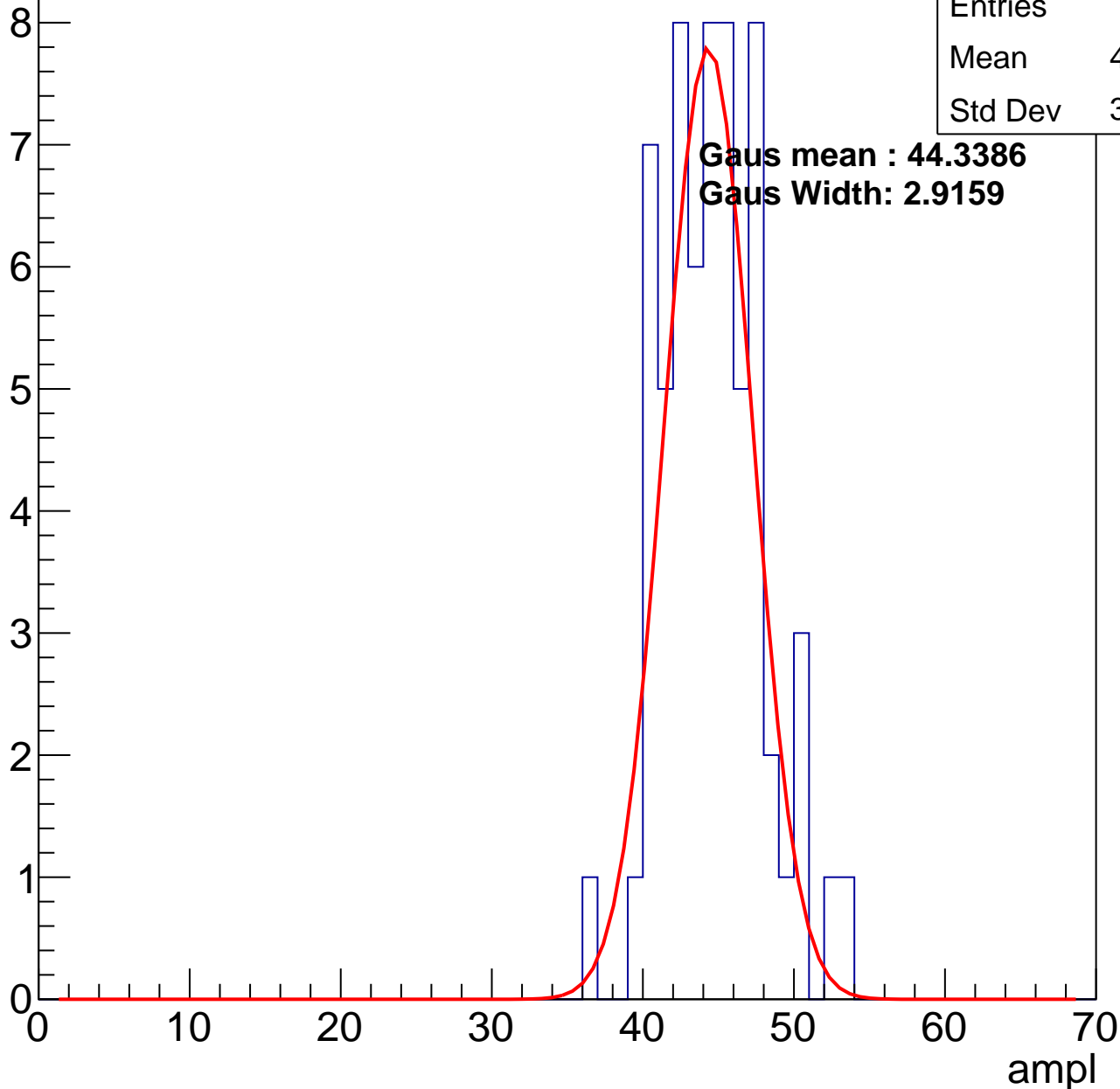
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.18
Std Dev	3.262

**Gaus mean : 44.3386**

**Gaus Width: 2.9159**

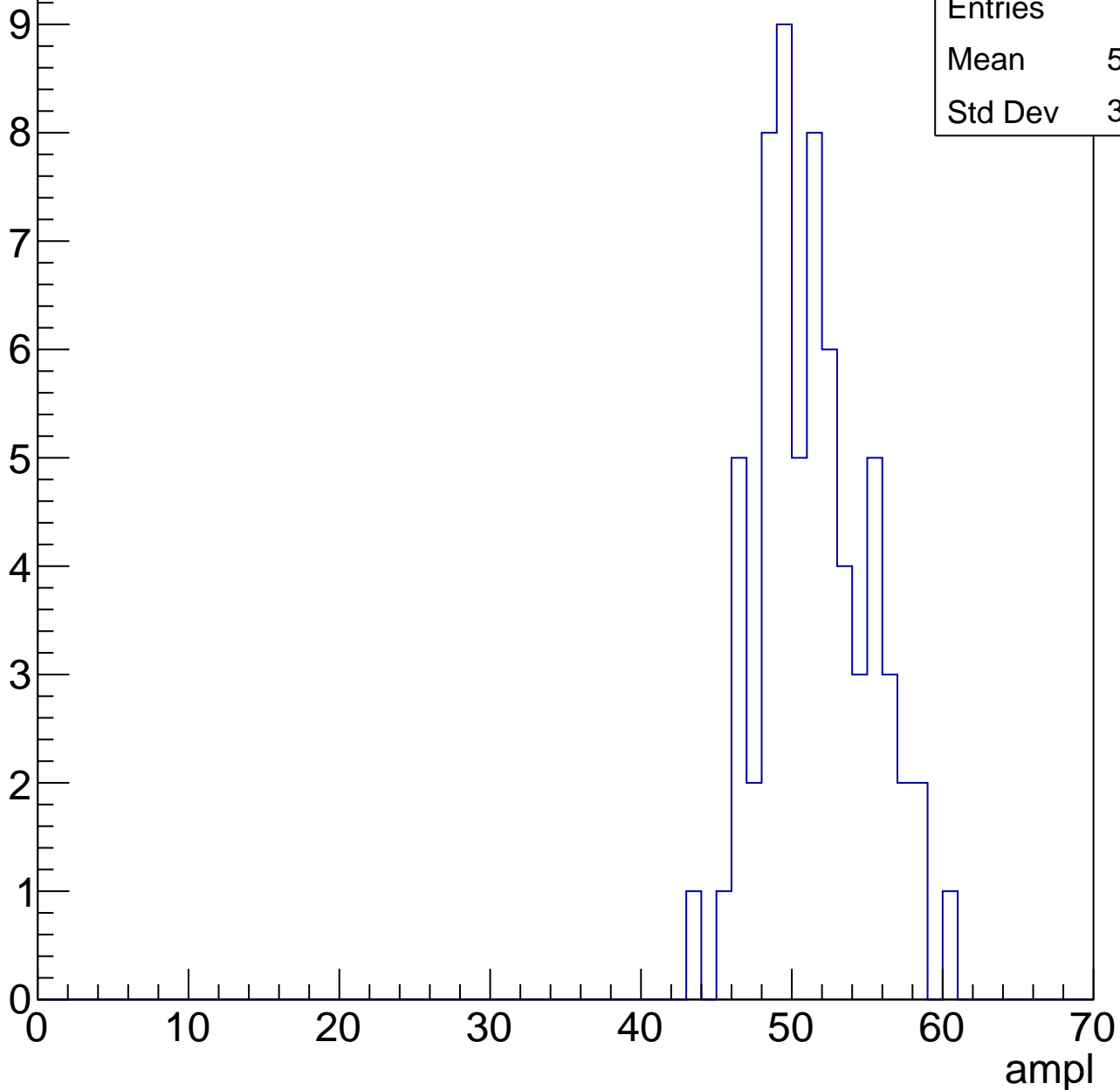


# B0L000S, U26-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	50.98
Std Dev	3.563

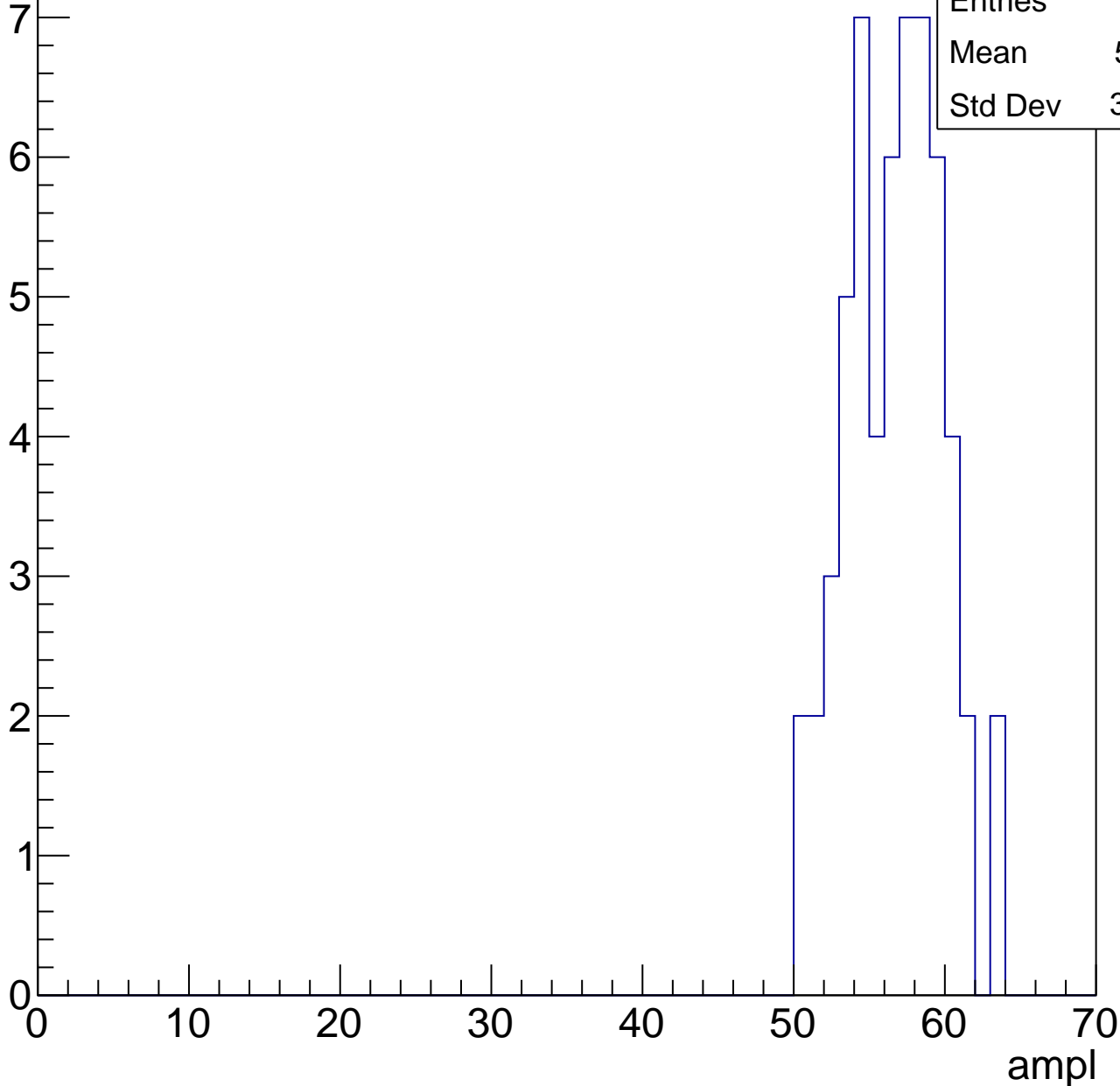


# B0L000S, U26-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	56.21
Std Dev	3.093

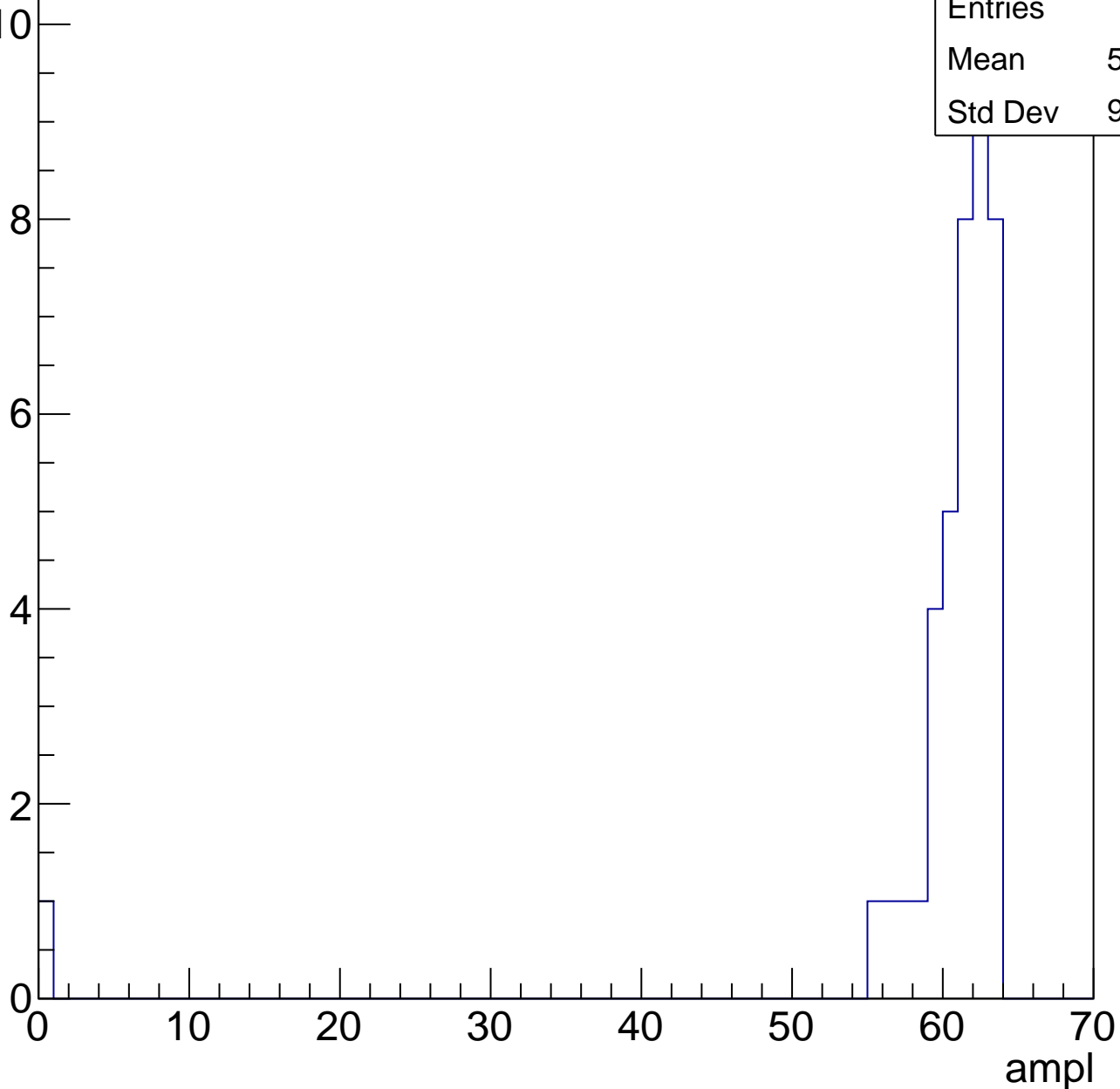


# B0L000S, U26-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	59.35
Std Dev	9.697



# B0L000S, U26-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U26-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	93
Mean	30.33
Std Dev	3.839

**Gaus mean : 30.9297**

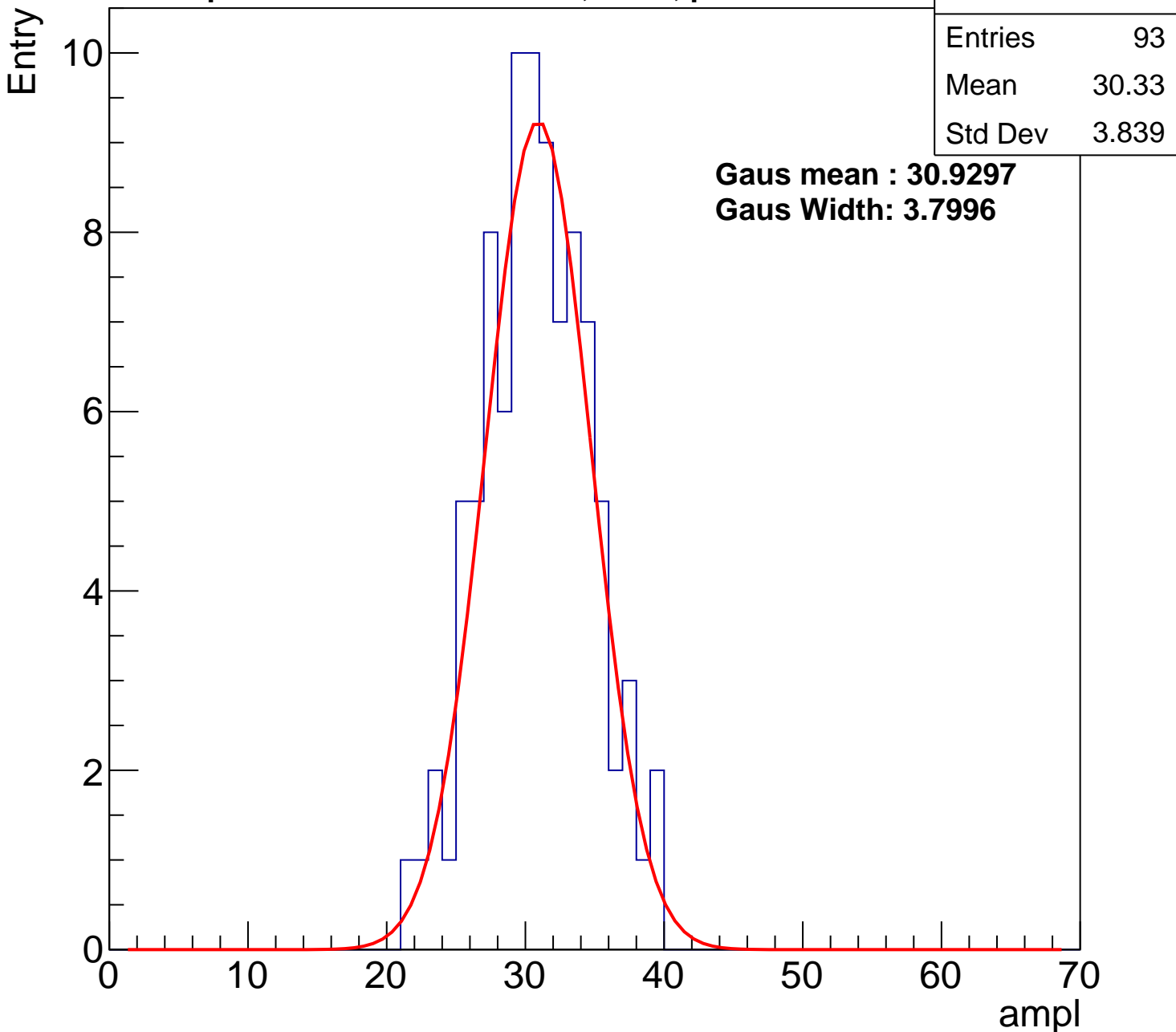
**Gaus Width: 3.7996**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch60, adc1

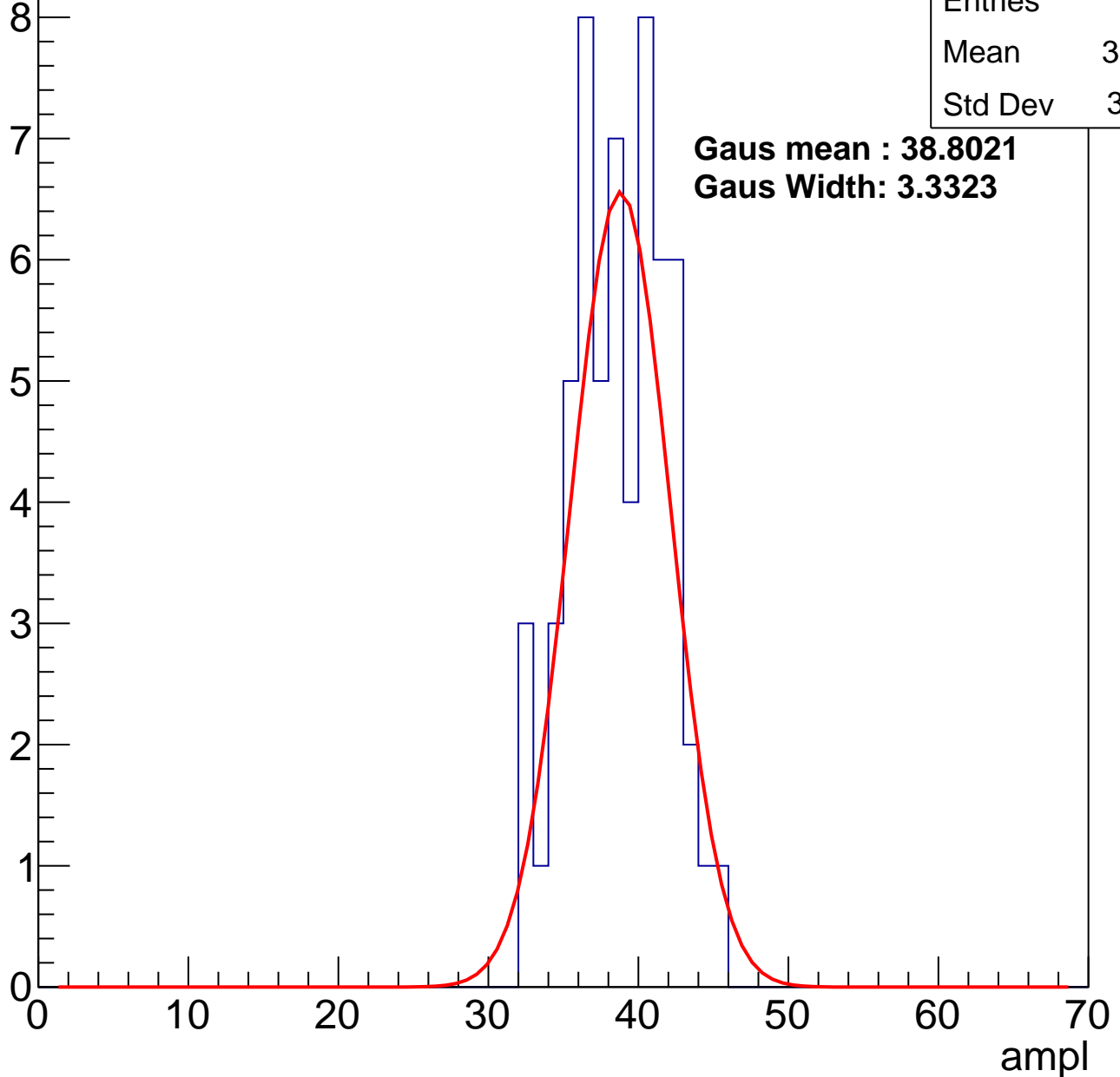
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	38.23
Std Dev	3.111

**Gaus mean : 38.8021**

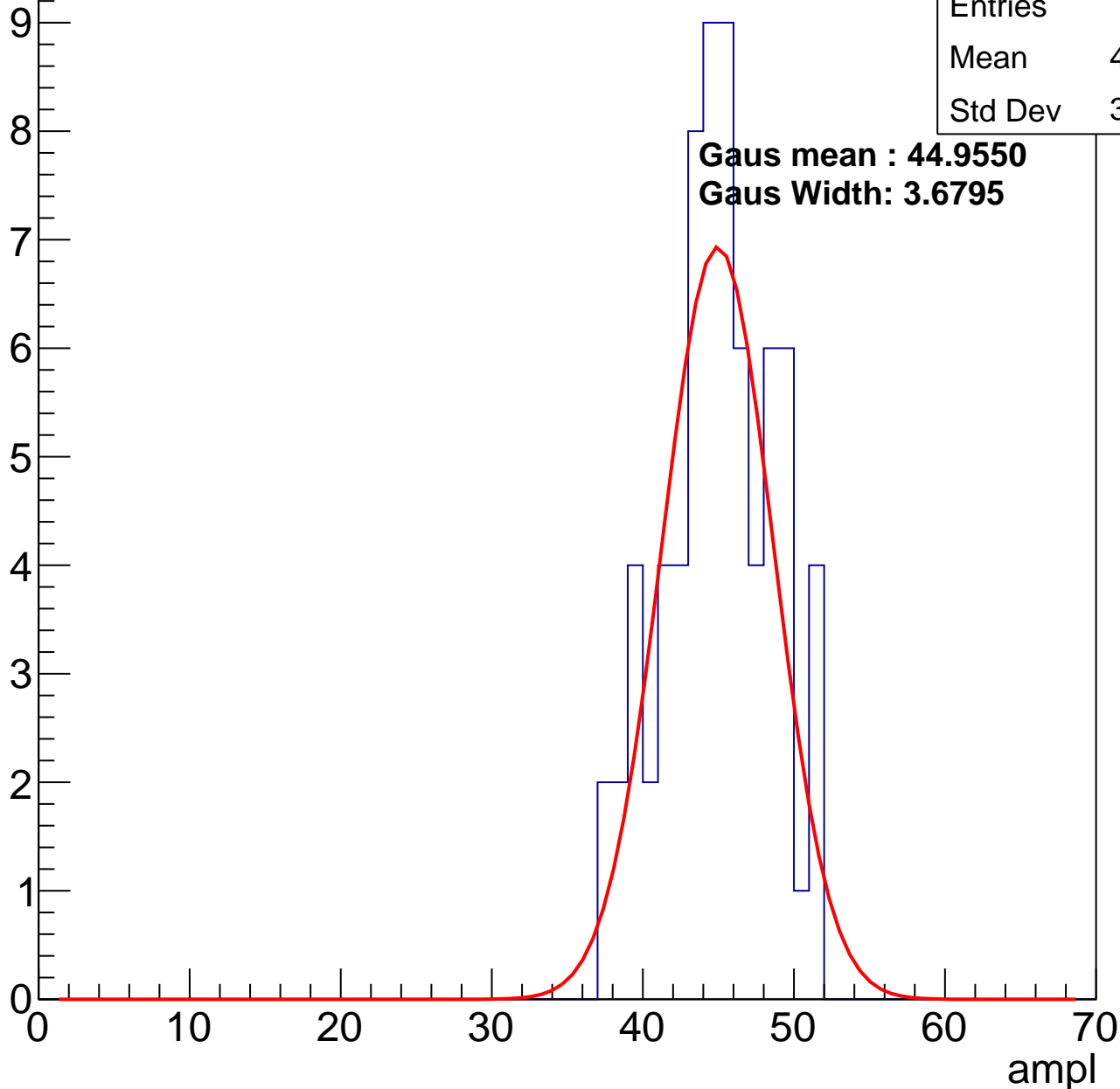
**Gaus Width: 3.3323**



# B0L000S, U26-ch60, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

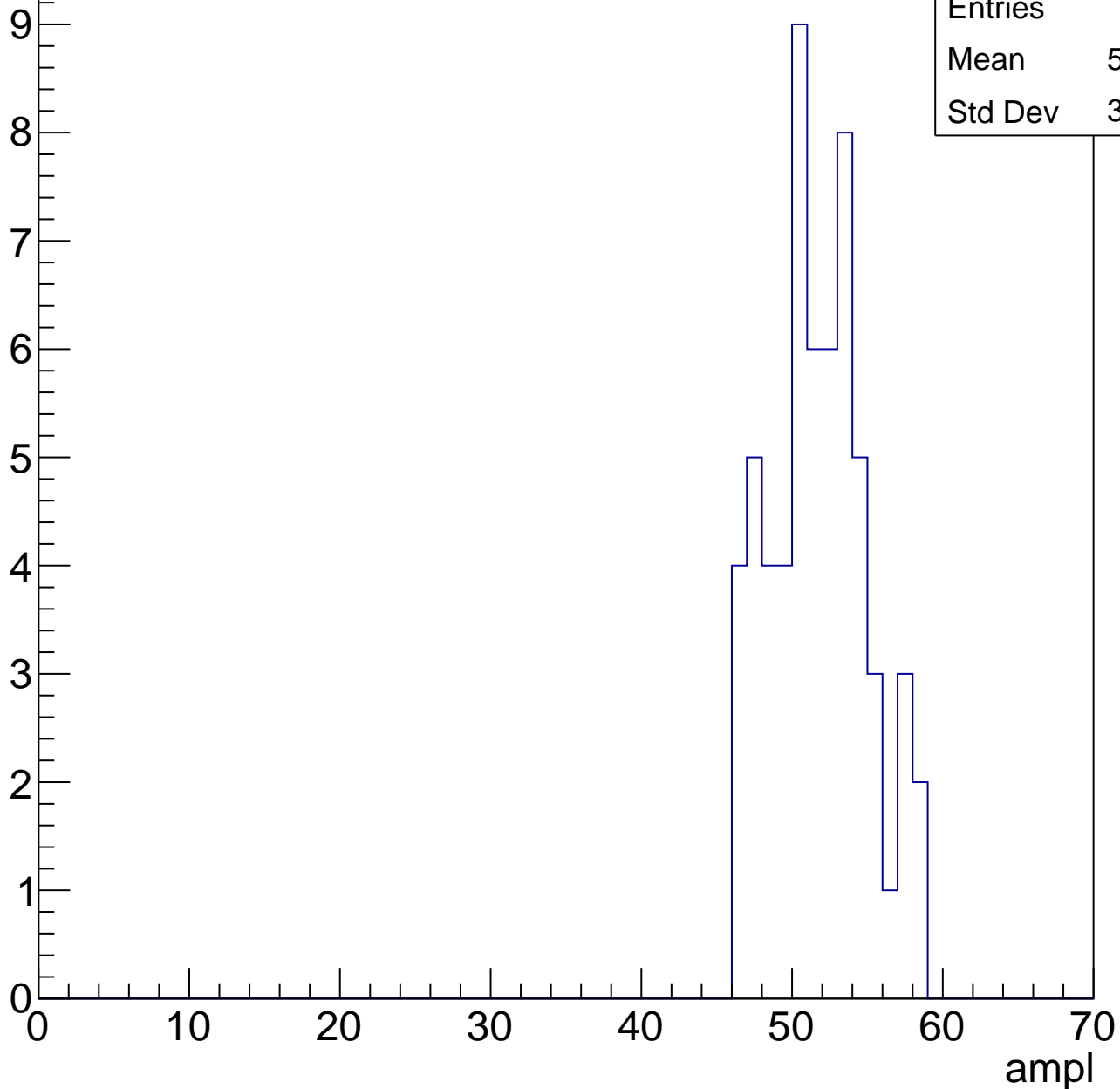


# B0L000S, U26-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	51.28
Std Dev	3.157

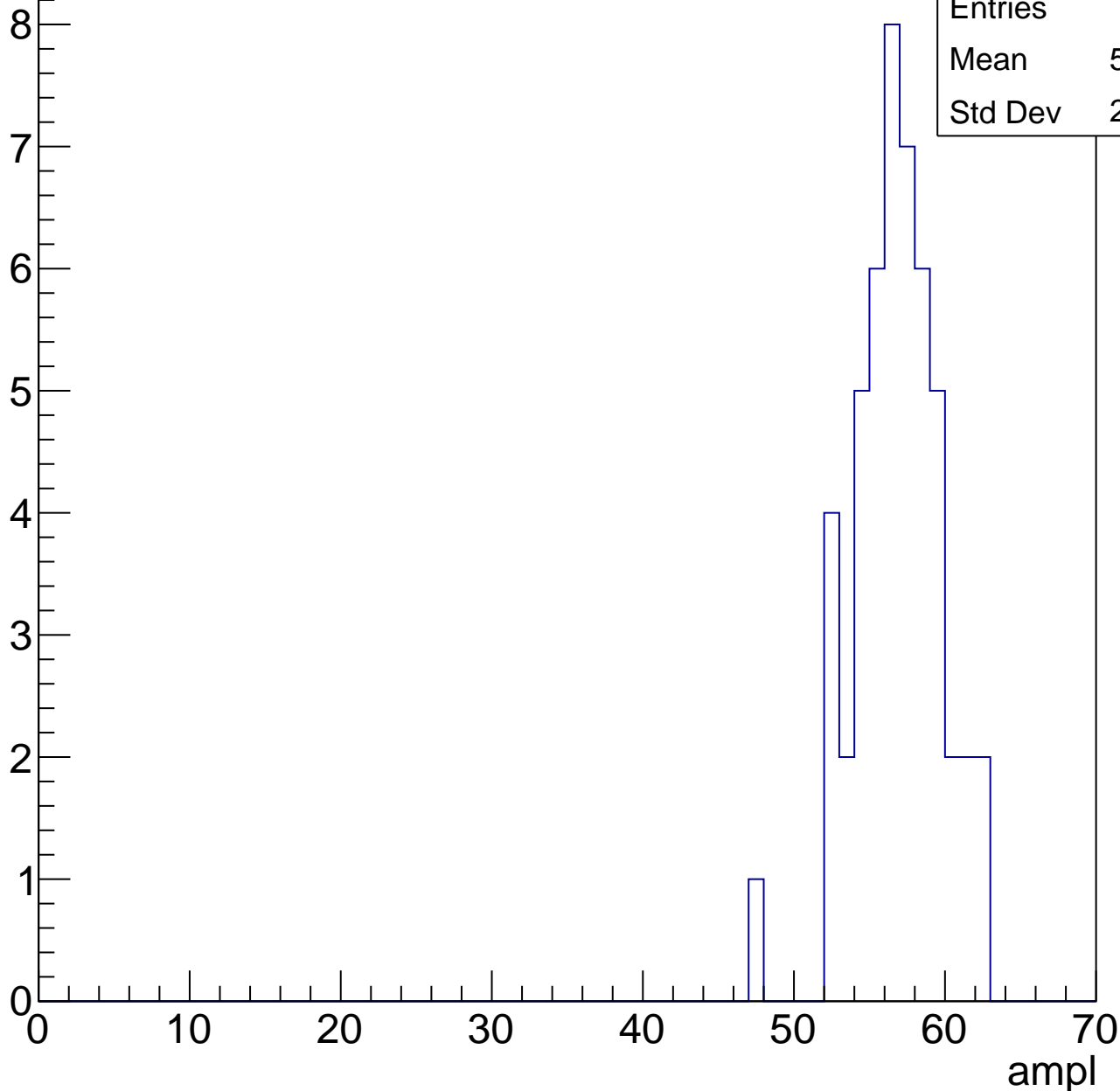


# B0L000S, U26-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

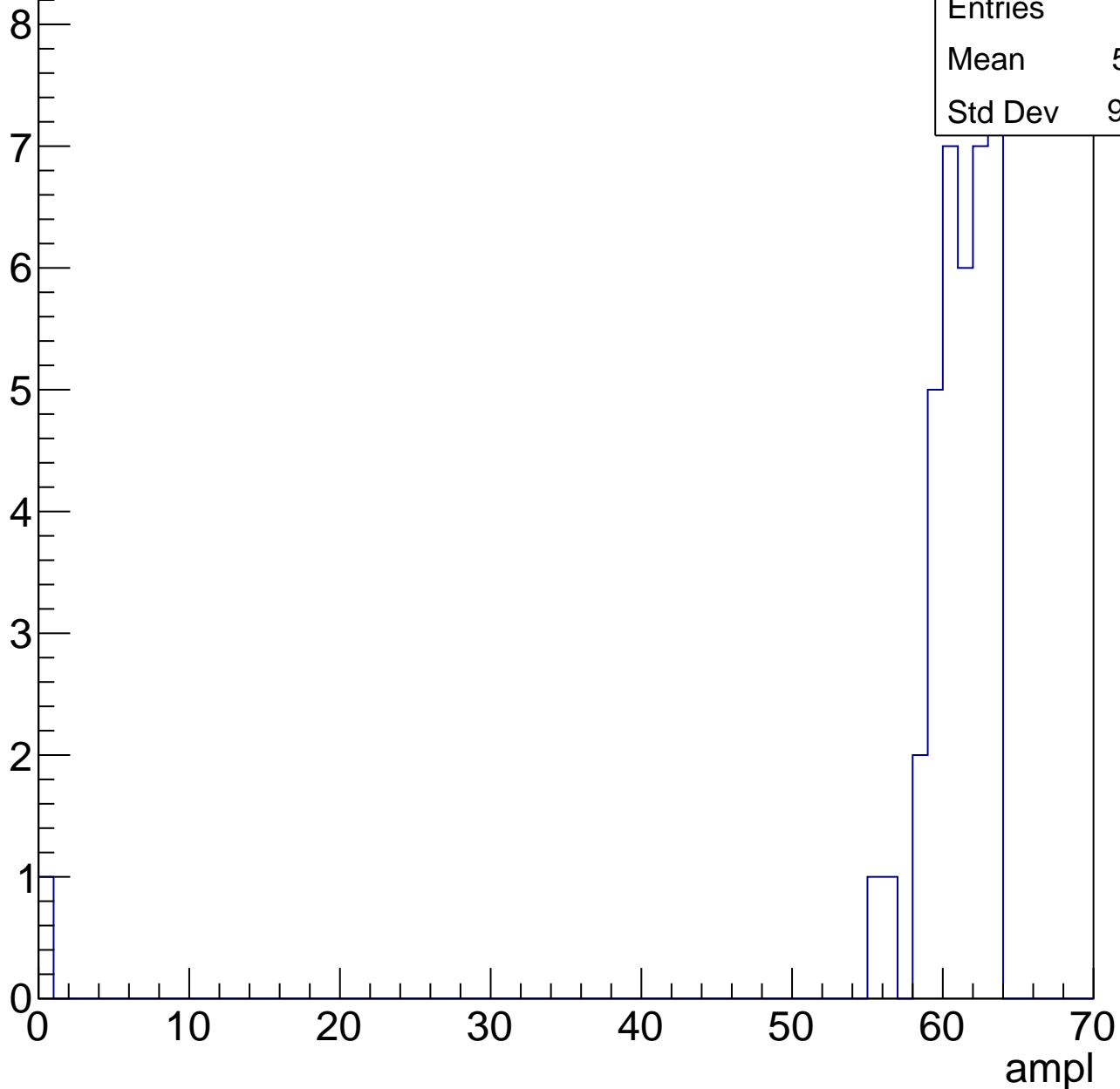
Entries	50
Mean	56.34
Std Dev	2.875



# B0L000S, U26-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch61, adc0

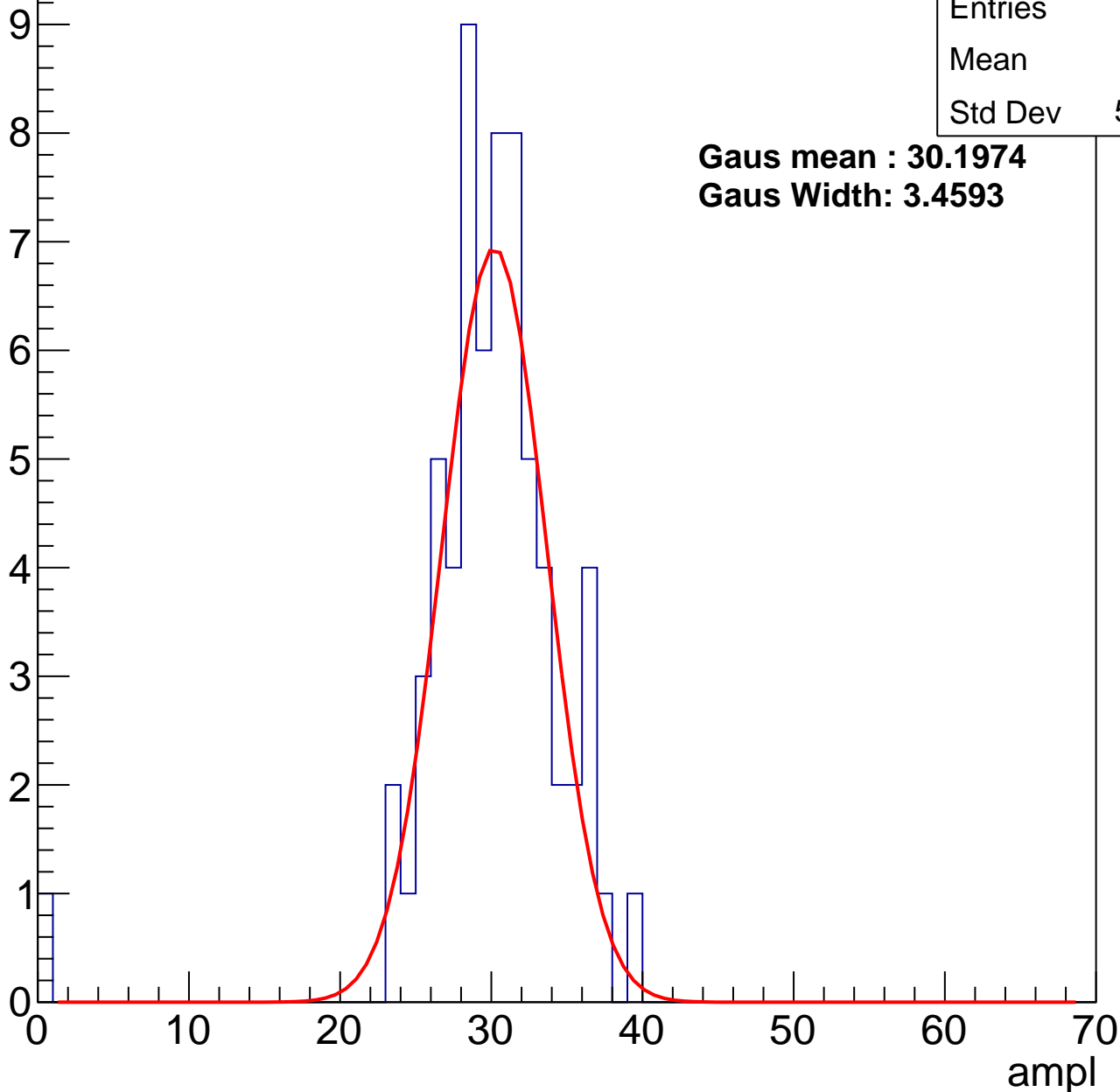
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.5
Std Dev	5.031

**Gaus mean : 30.1974**

**Gaus Width: 3.4593**



# B0L000S, U26-ch61, adc1

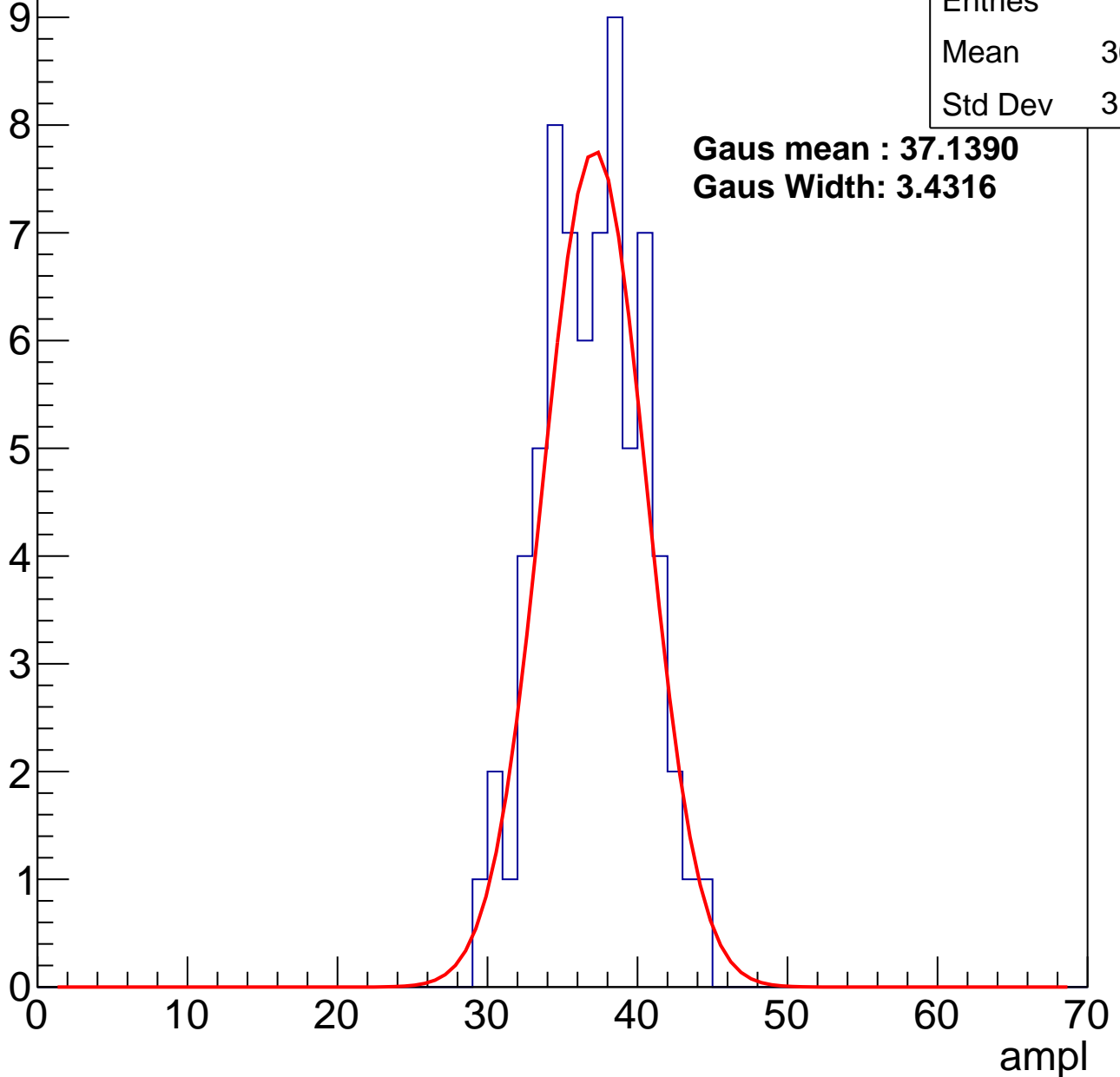
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.53
Std Dev	3.294

**Gaus mean : 37.1390**

**Gaus Width: 3.4316**



# B0L000S, U26-ch61, adc2

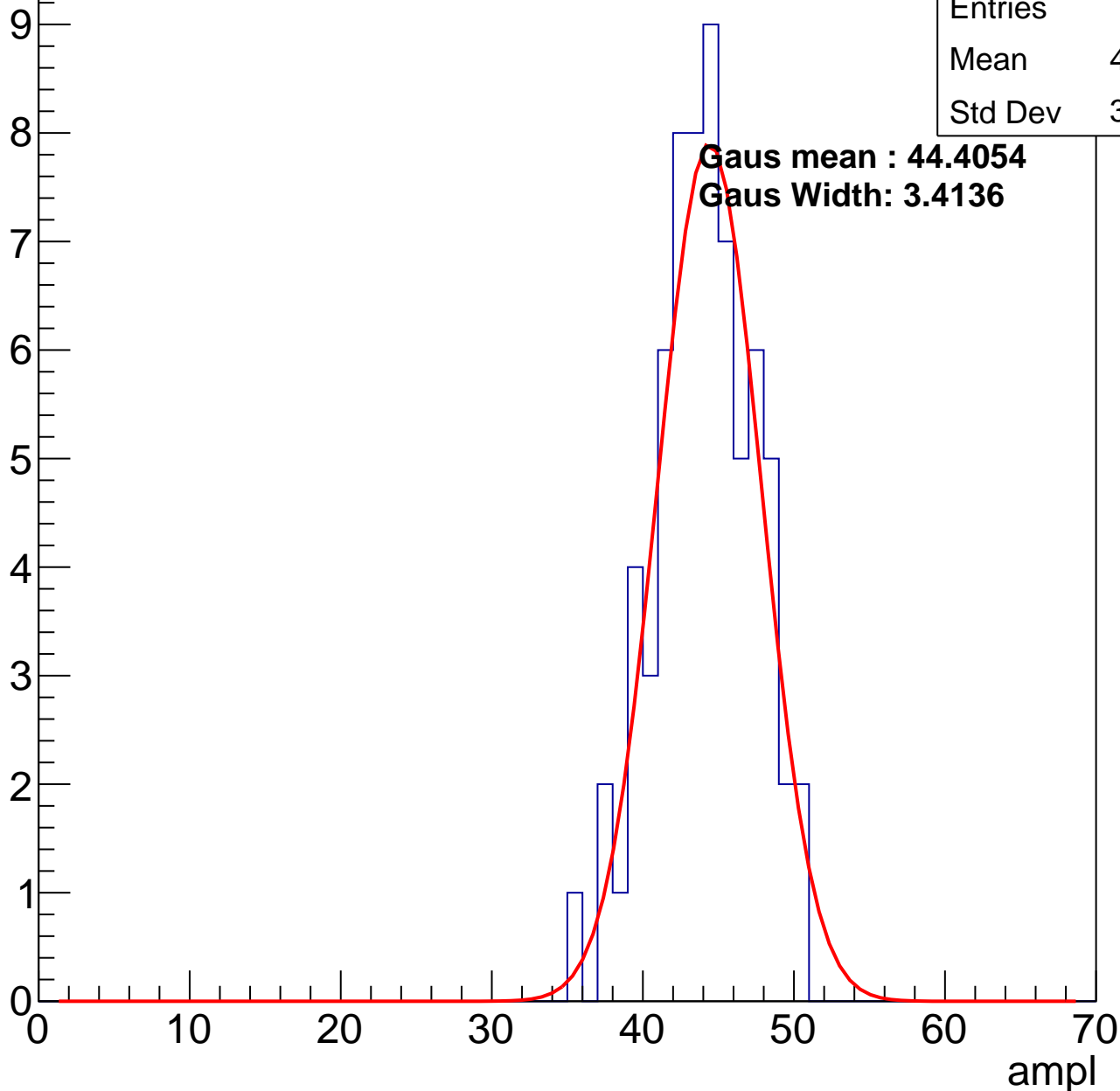
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.62
Std Dev	3.253

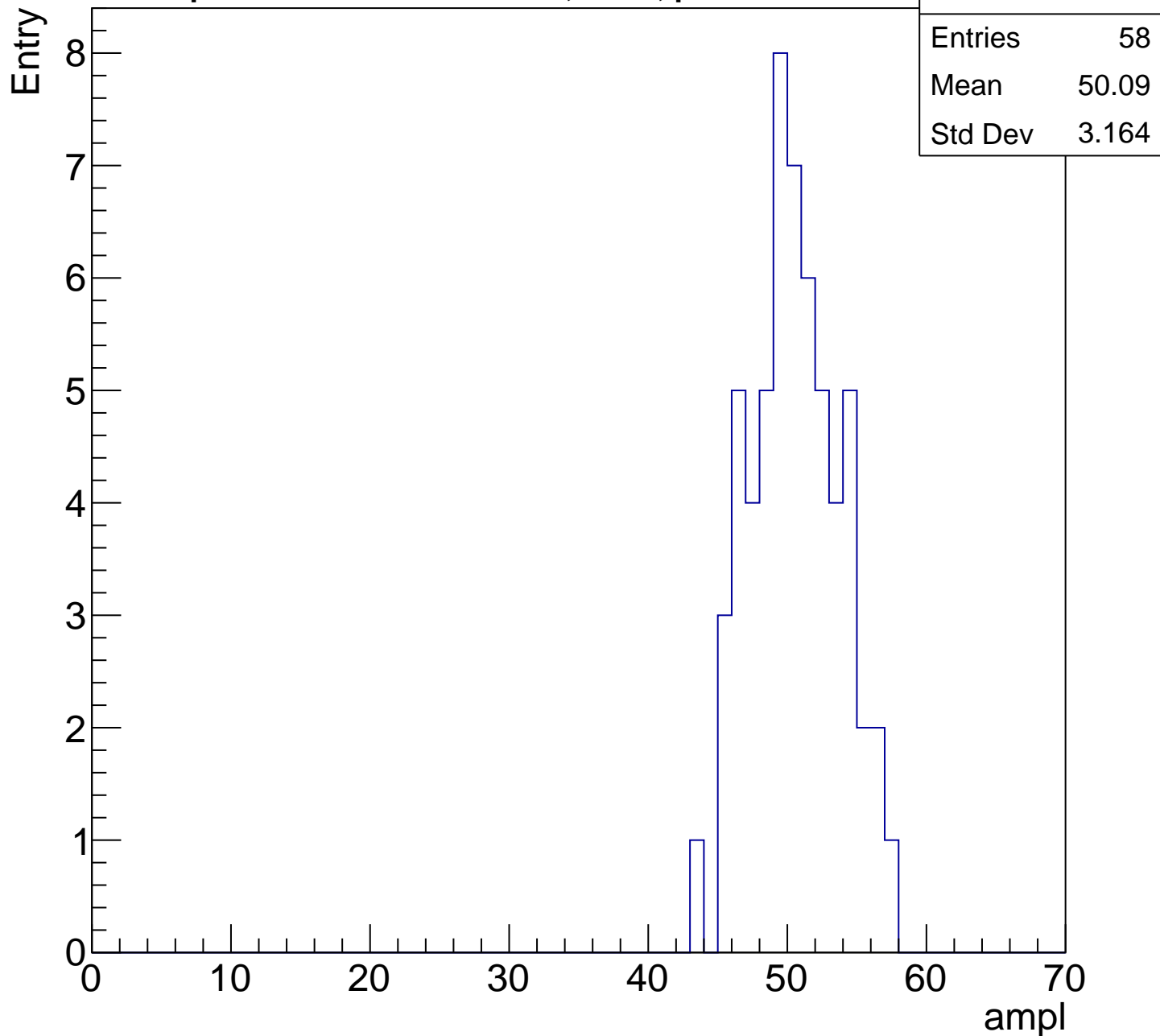
**Gaus mean : 44.4054**

**Gaus Width: 3.4136**



# B0L000S, U26-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

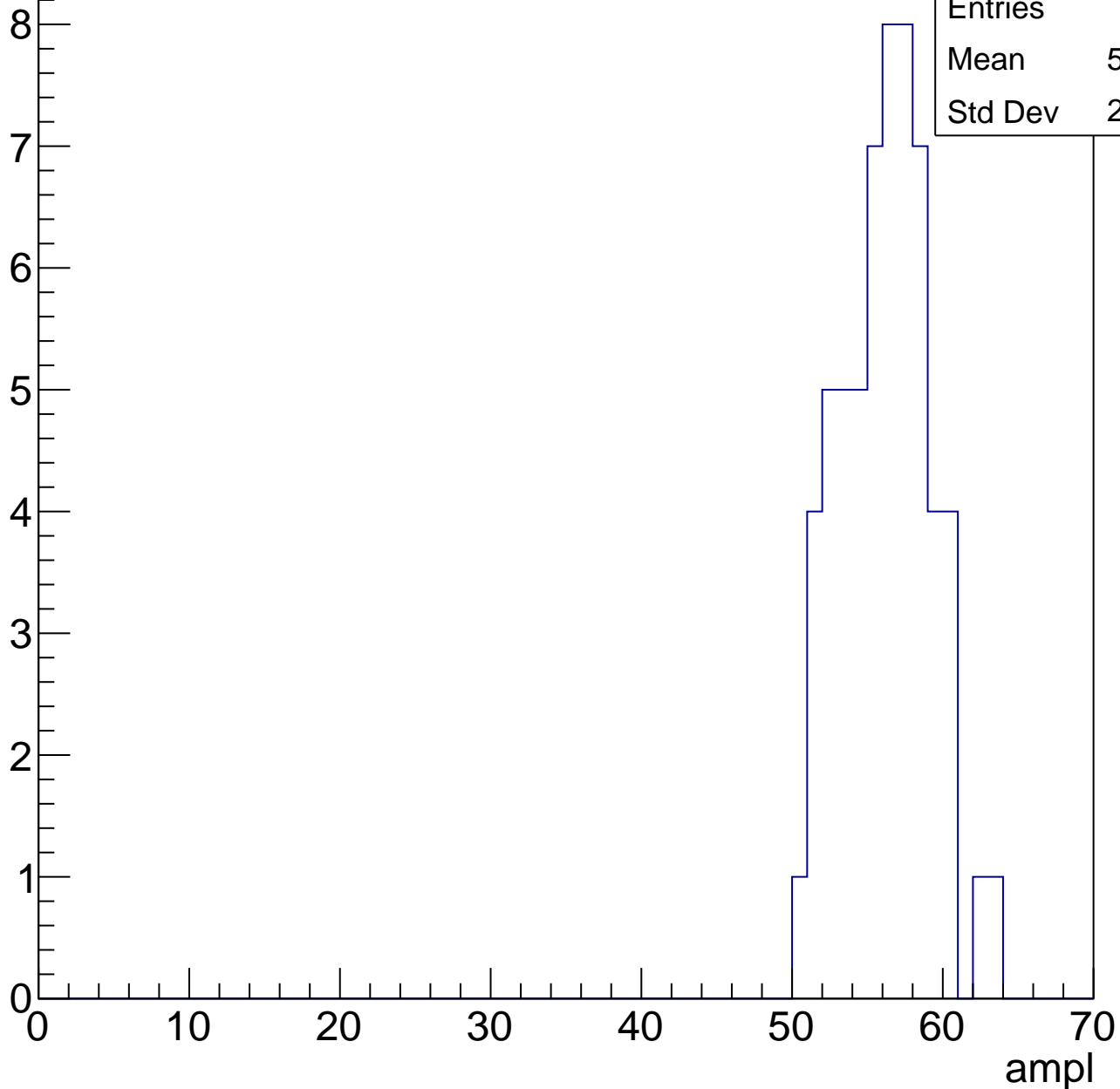


# B0L000S, U26-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	55.75
Std Dev	2.902

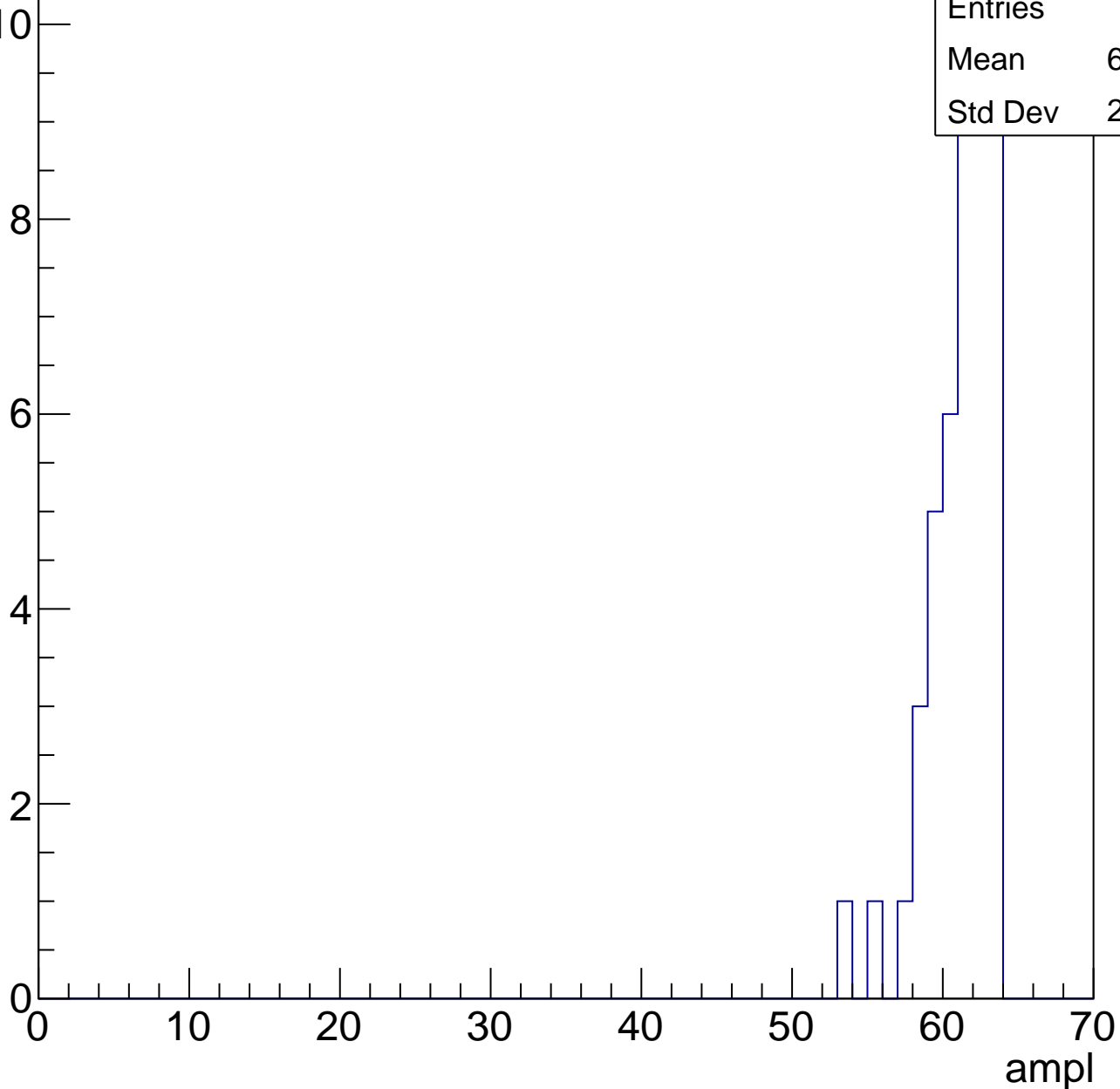


# B0L000S, U26-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

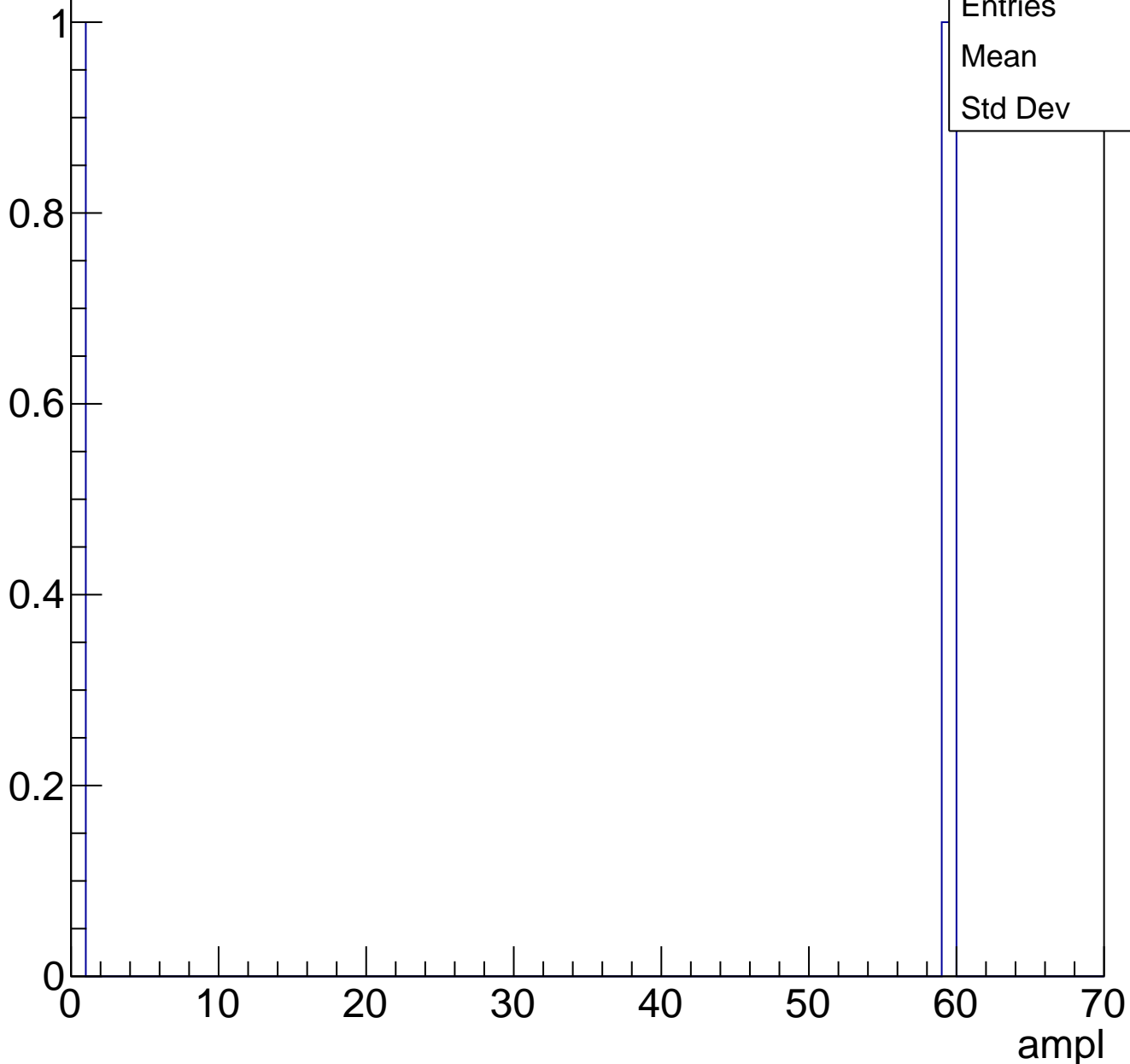
Entries	45
Mean	60.64
Std Dev	2.152



# B0L000S, U26-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U26-ch62, adc0

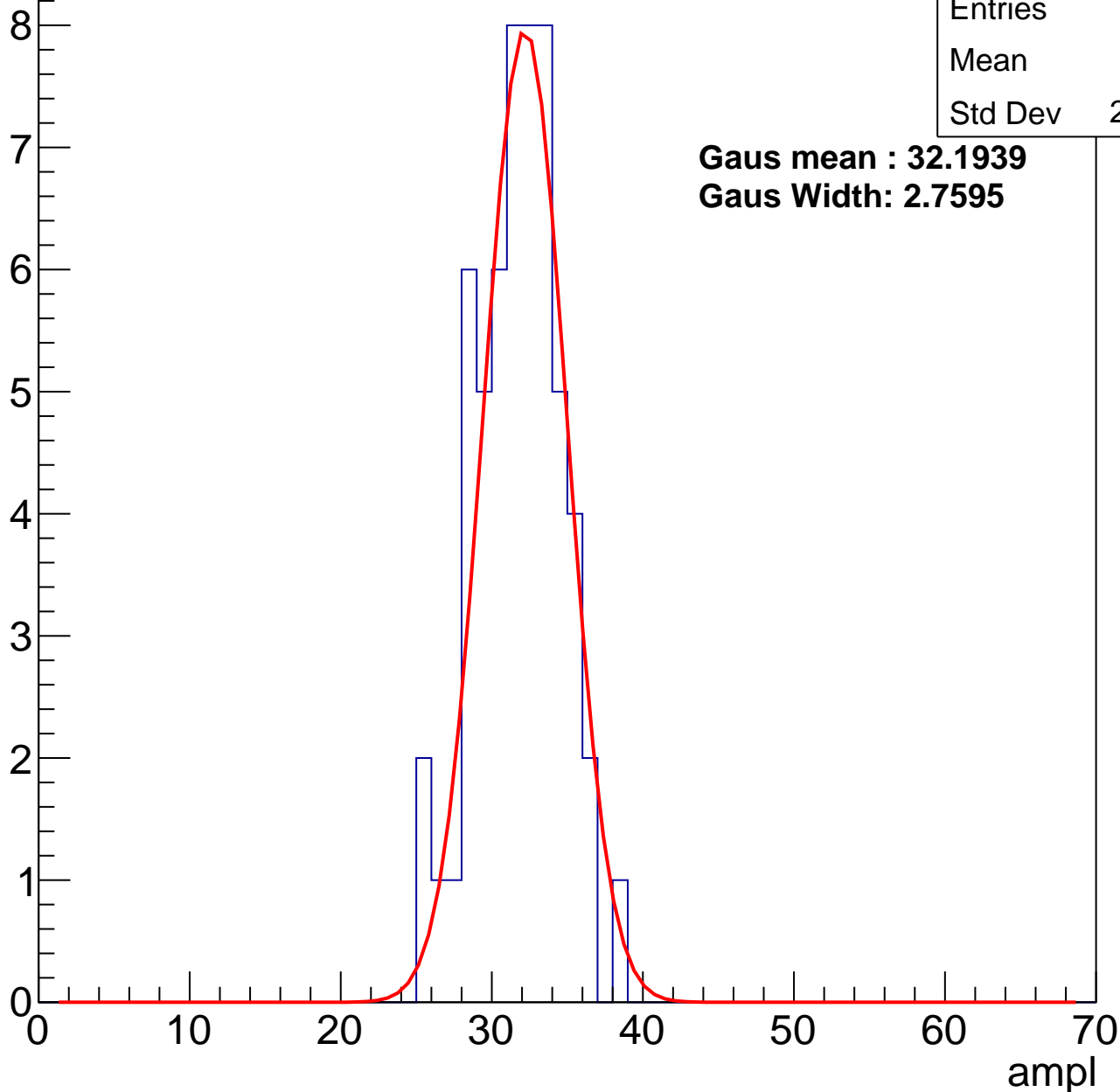
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	31.3
Std Dev	2.765

**Gaus mean : 32.1939**

**Gaus Width: 2.7595**



# B0L000S, U26-ch62, adc1

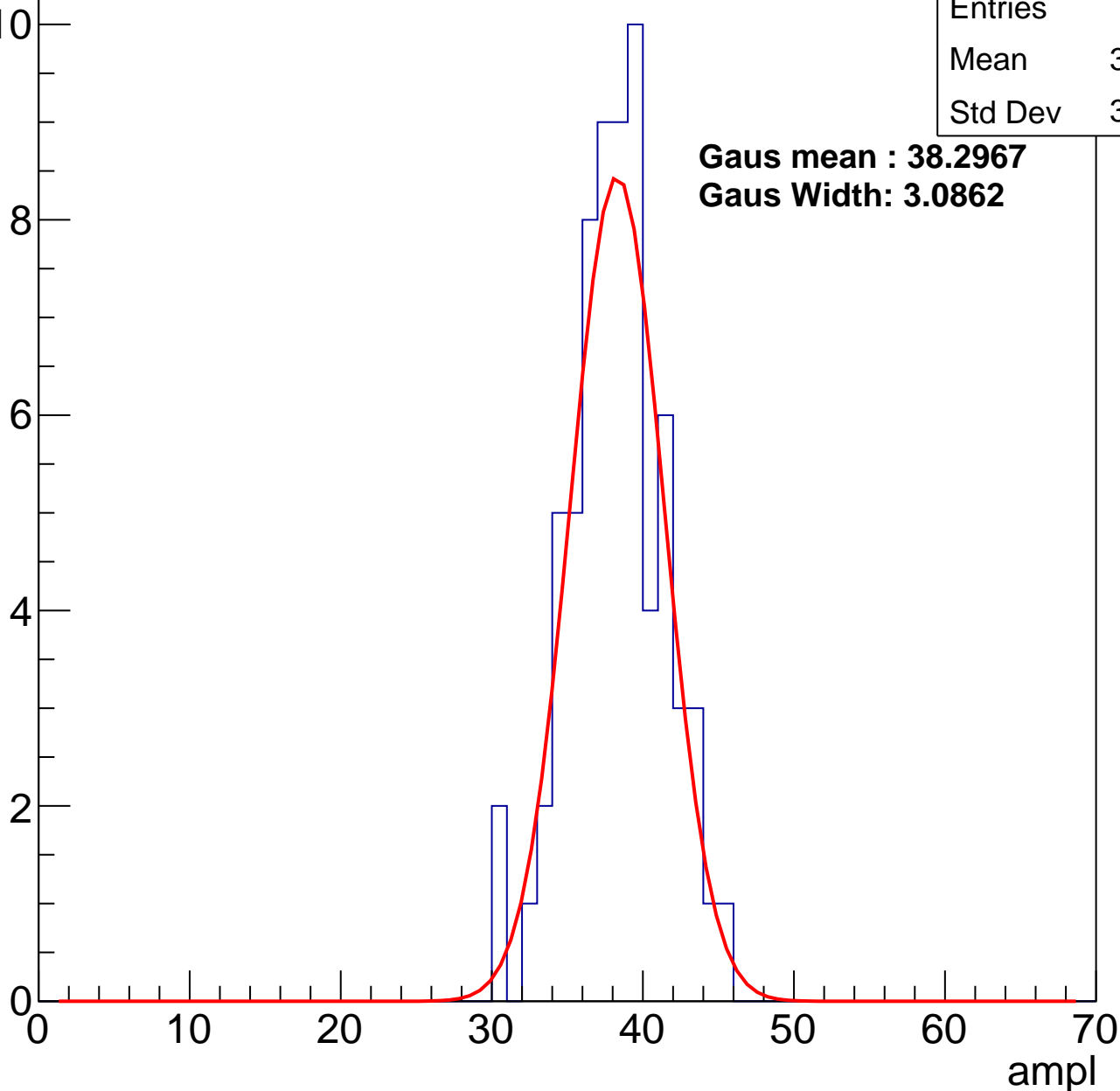
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	37.77
Std Dev	3.103

**Gaus mean : 38.2967**

**Gaus Width: 3.0862**



# B0L000S, U26-ch62, adc2

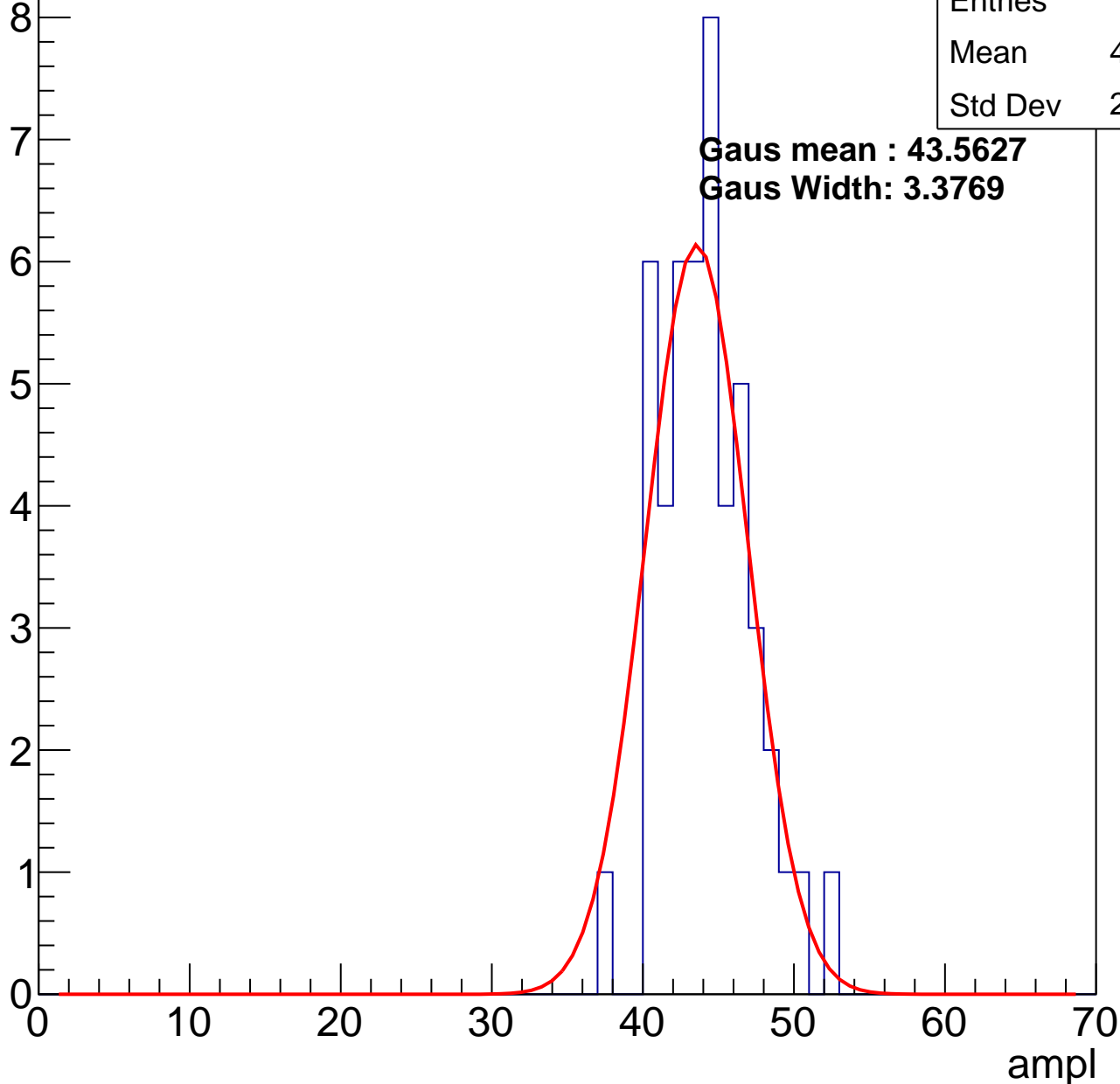
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	43.77
Std Dev	2.946

**Gaus mean : 43.5627**

**Gaus Width: 3.3769**



# B0L000S, U26-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	50.52
Std Dev	3.855

Entry

10

8

6

4

2

0

0

10

20

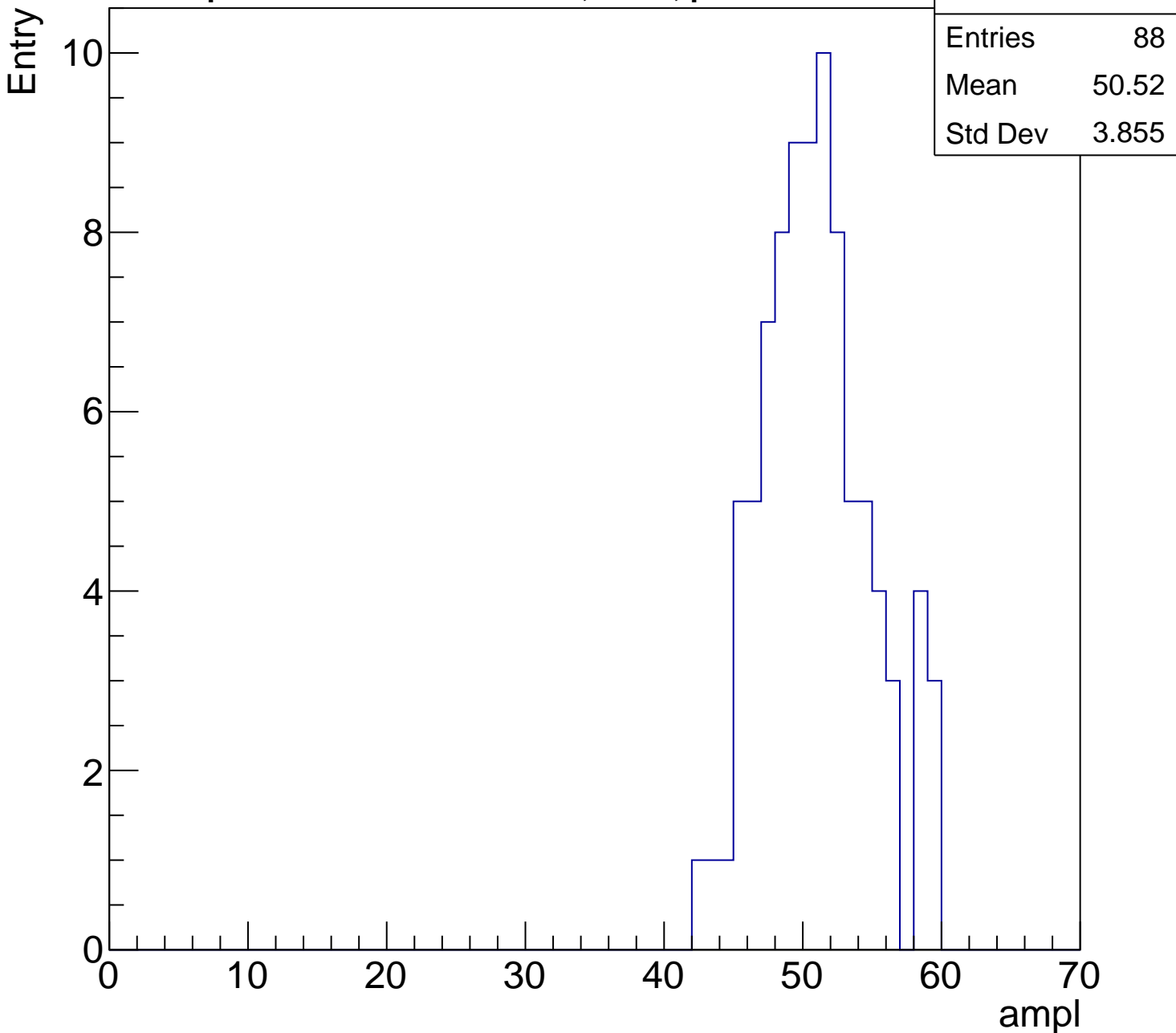
30

40

50

60

ampl

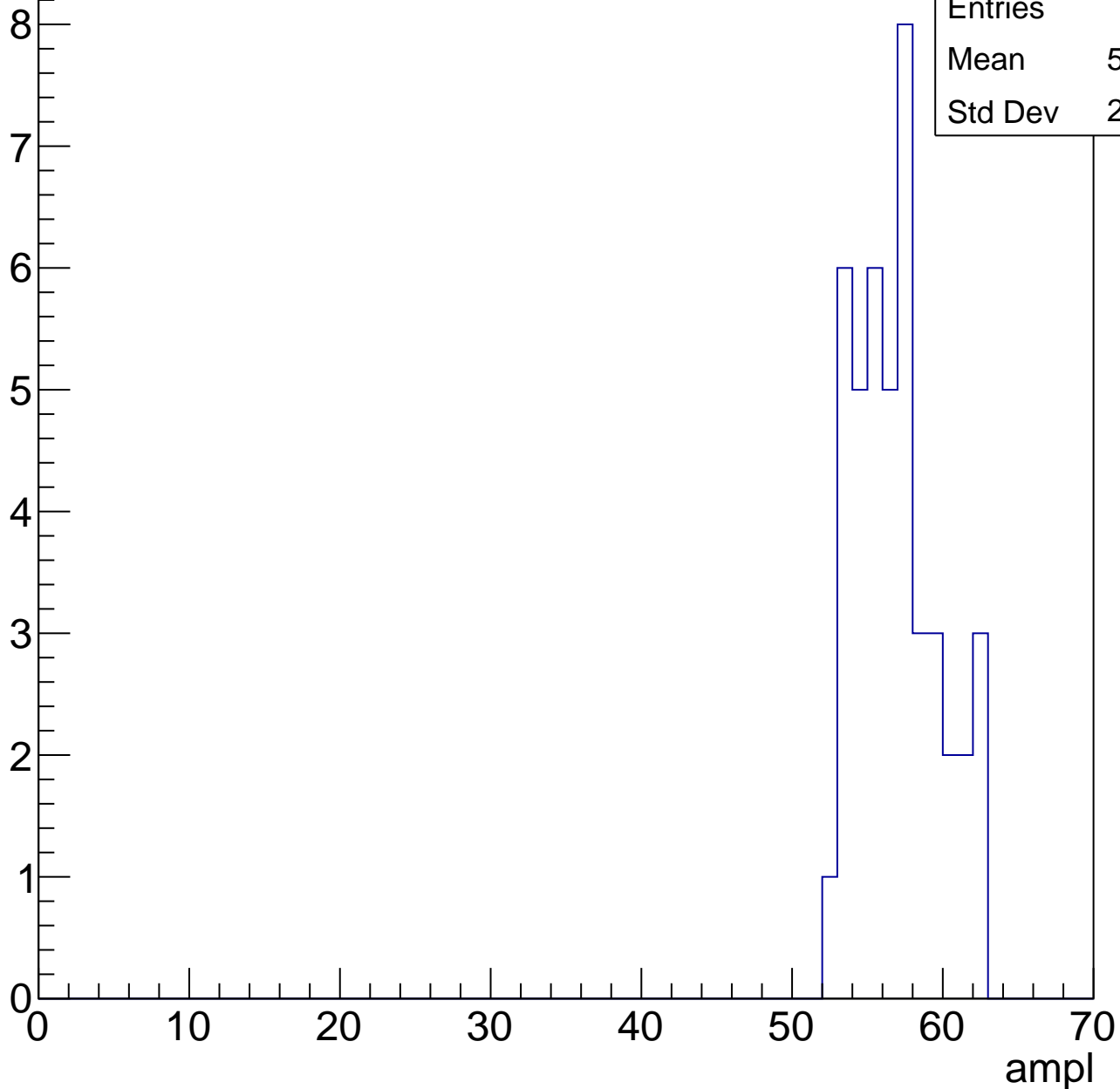


# B0L000S, U26-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	56.48
Std Dev	2.709

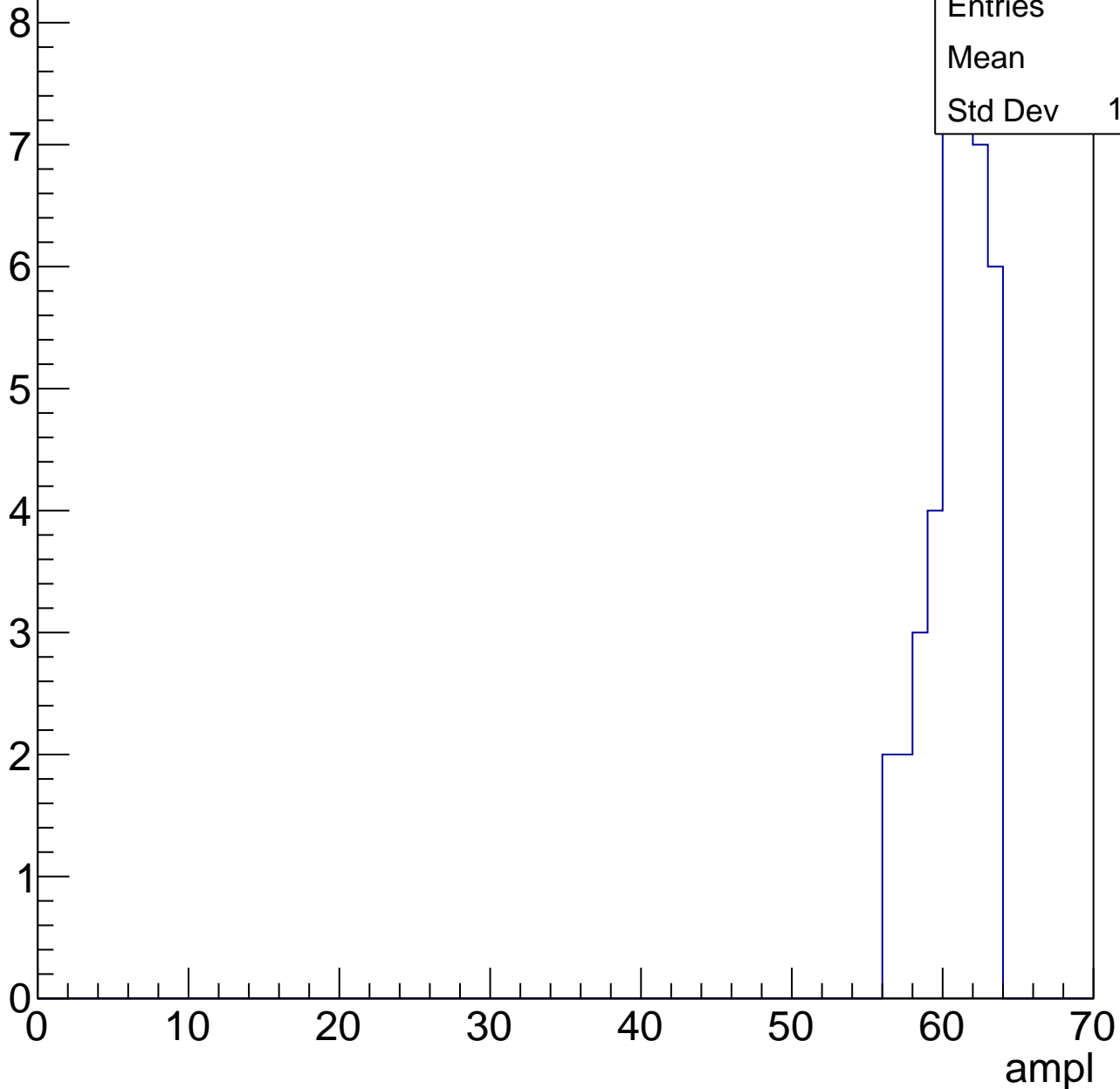


# B0L000S, U26-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

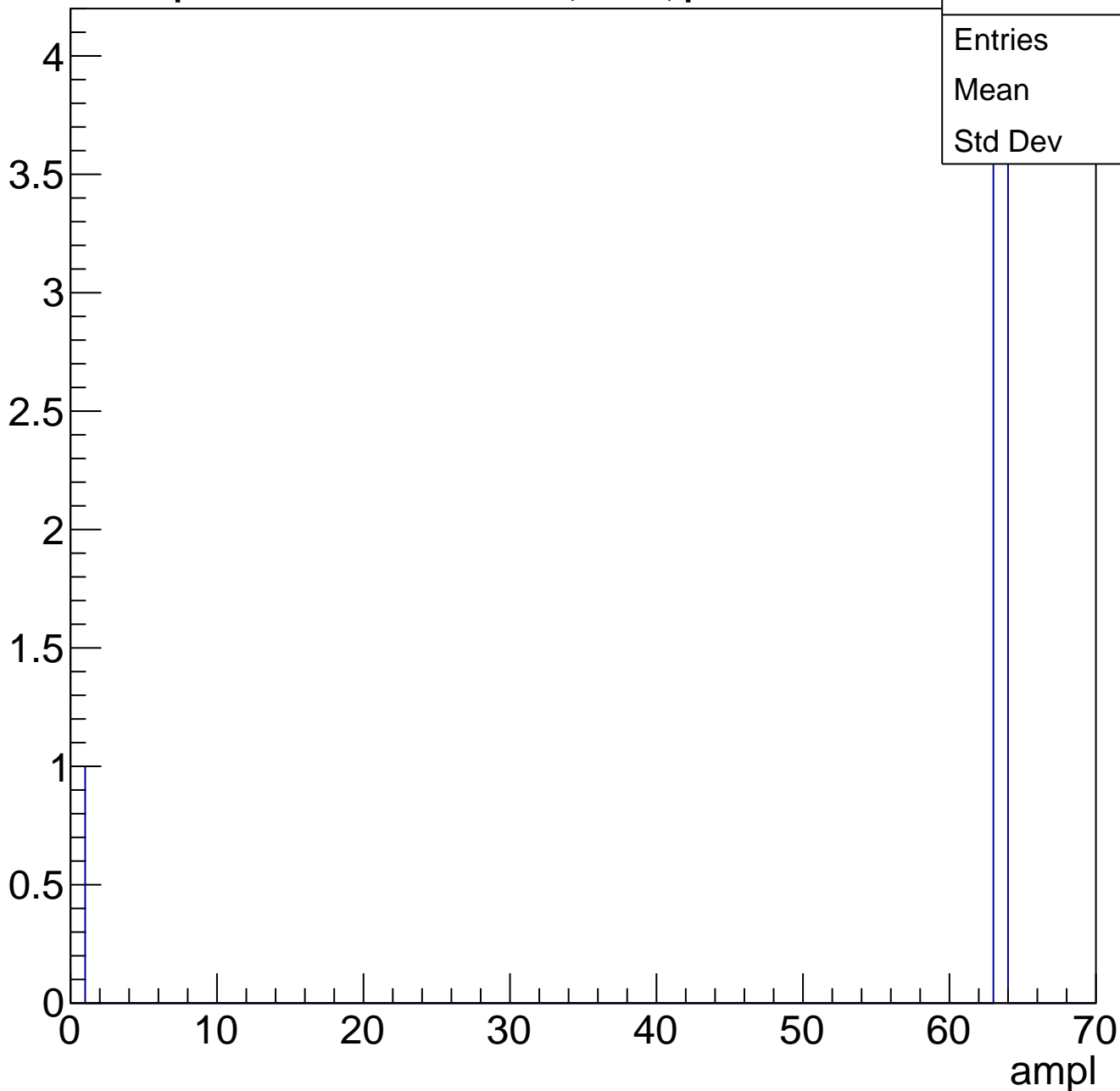
Entries	40
Mean	60.4
Std Dev	1.934



# B0L000S, U26-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	29.86
Std Dev	4.724

**Gaus mean : 30.4644**

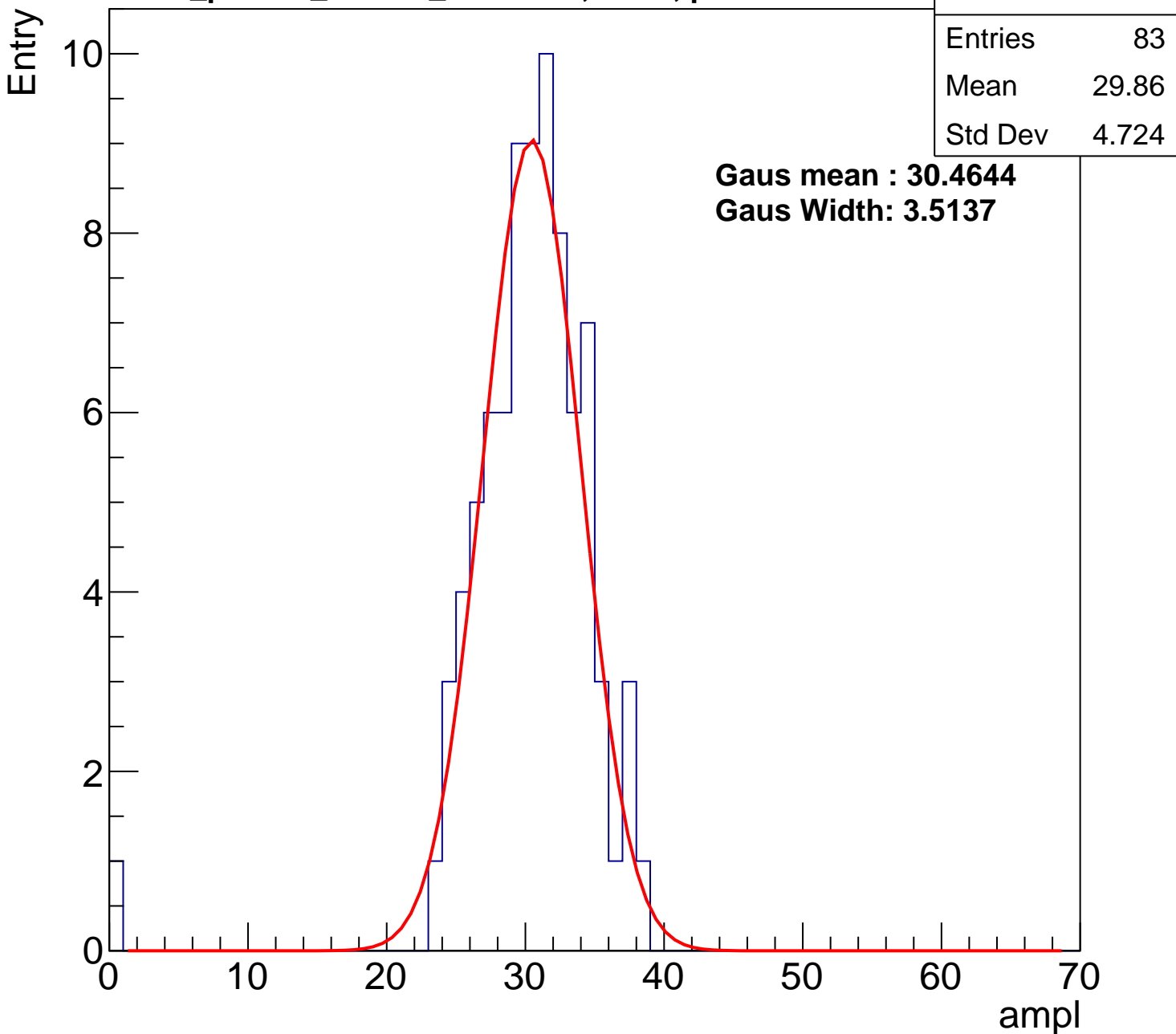
**Gaus Width: 3.5137**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



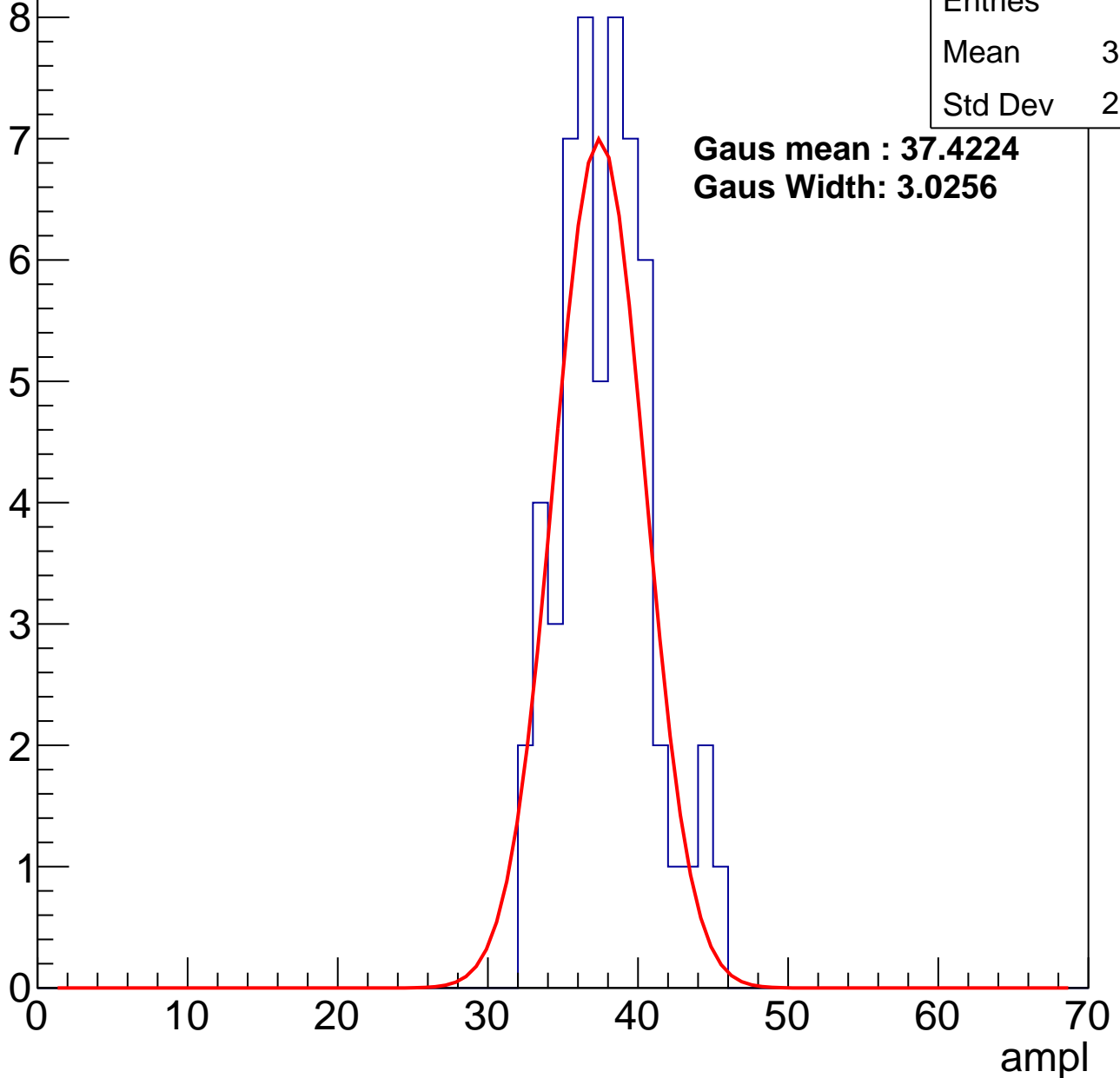
# B0L000S, U26-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	37.42
Std Dev	2.997

**Gaus mean : 37.4224**  
**Gaus Width: 3.0256**



# B0L000S, U26-ch63, adc2

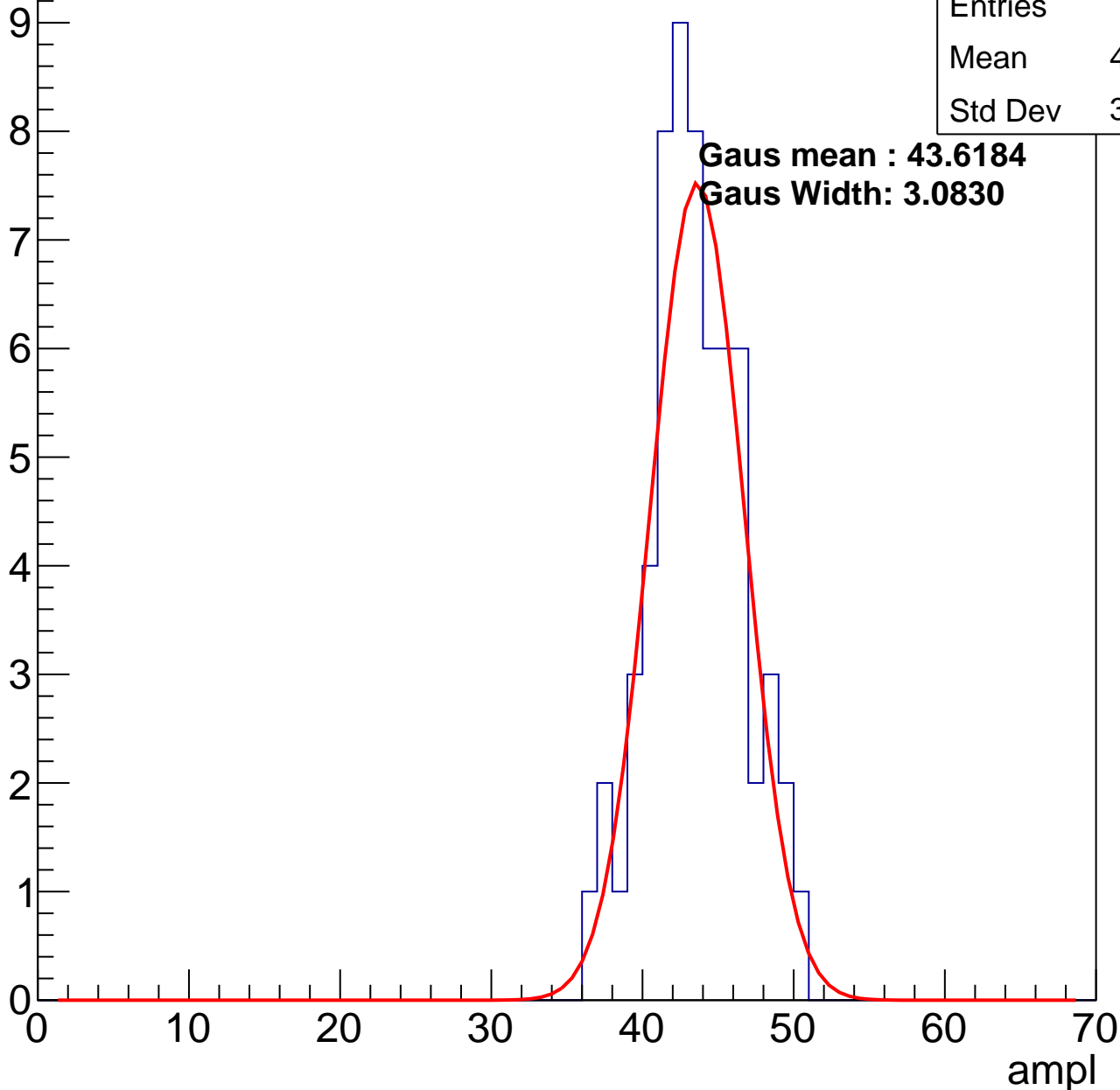
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.08
Std Dev	3.065

**Gaus mean : 43.6184**

**Gaus Width: 3.0830**

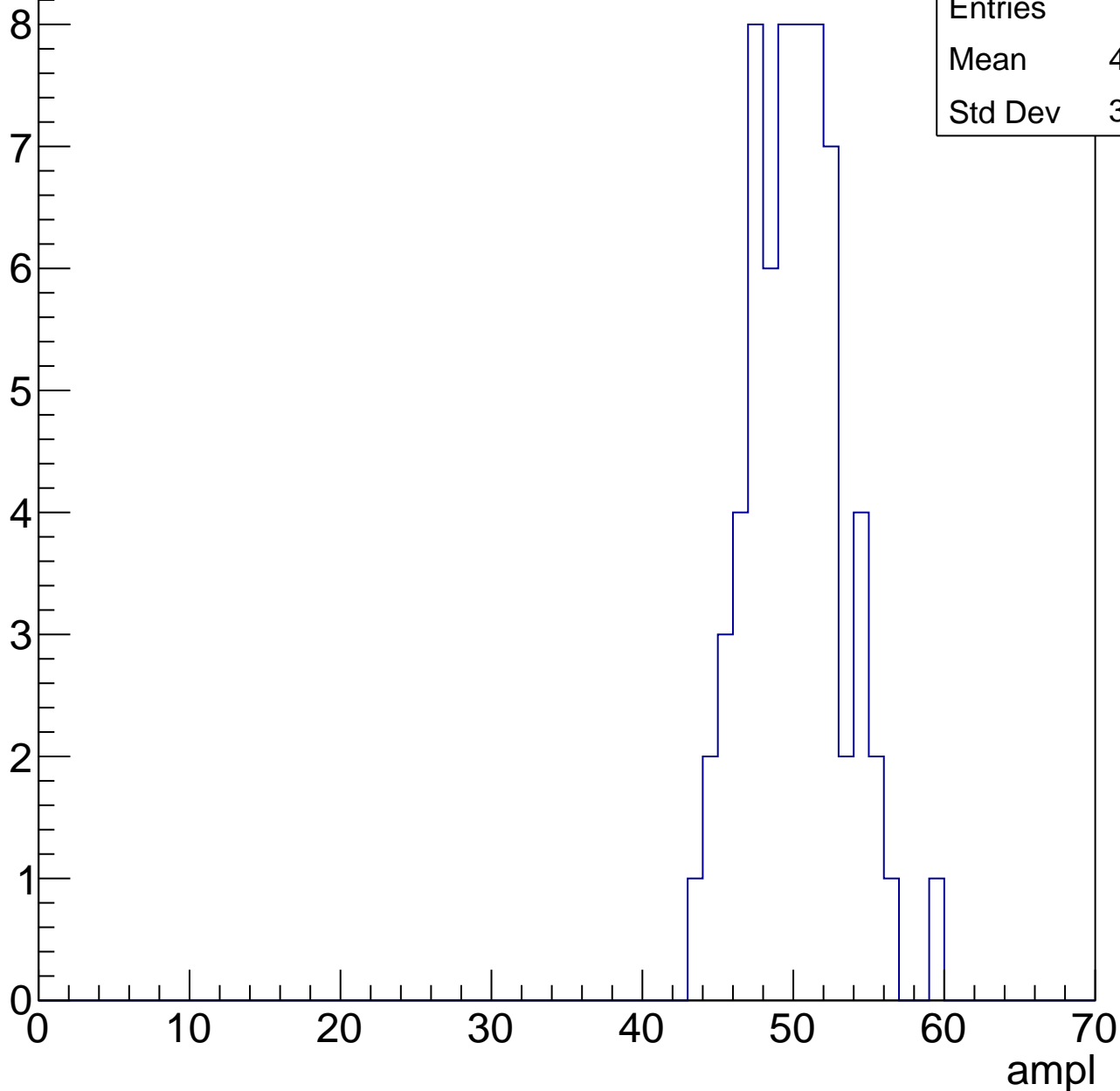


# B0L000S, U26-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	49.62
Std Dev	3.136

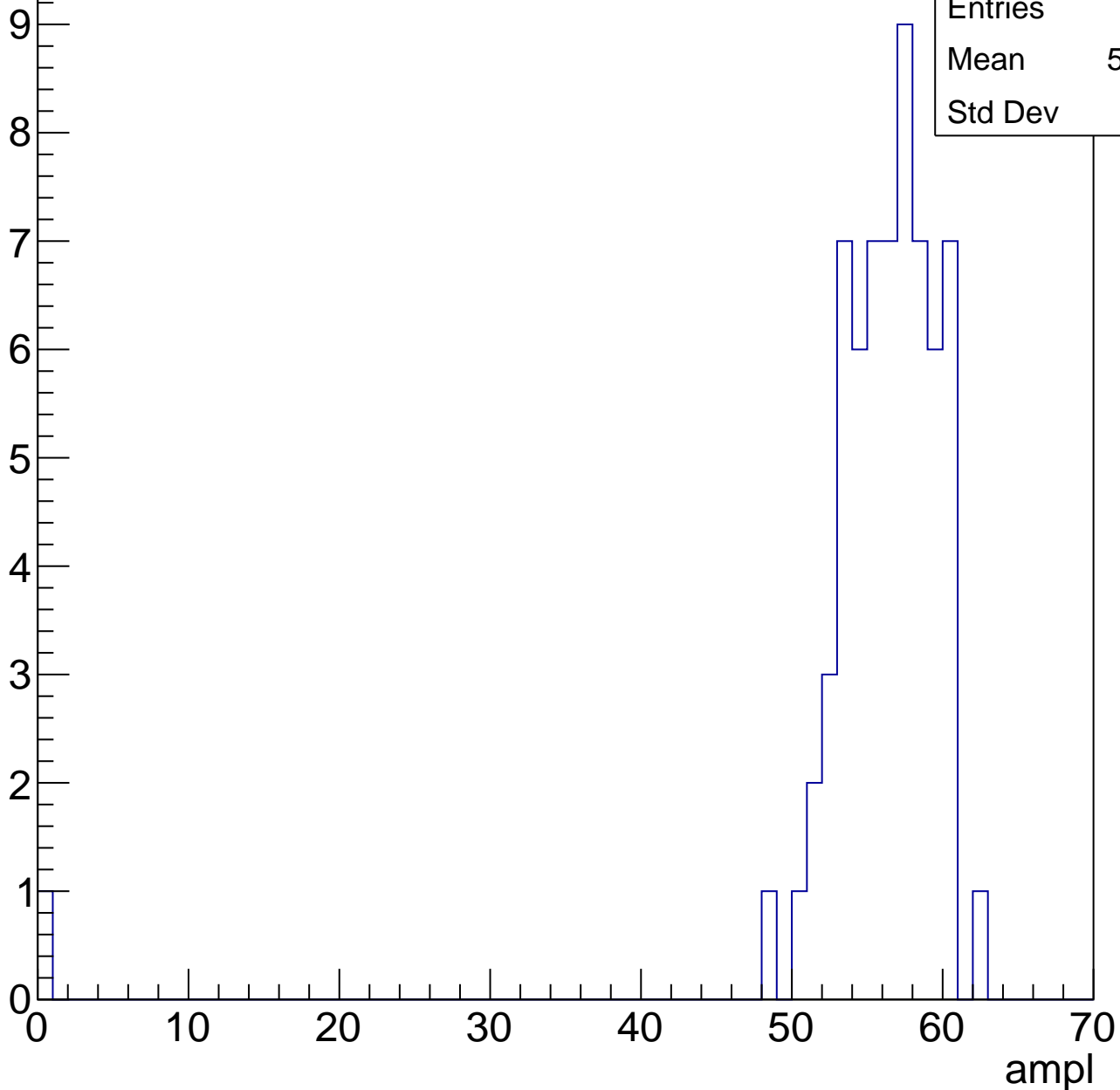


# B0L000S, U26-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	55.12
Std Dev	7.46

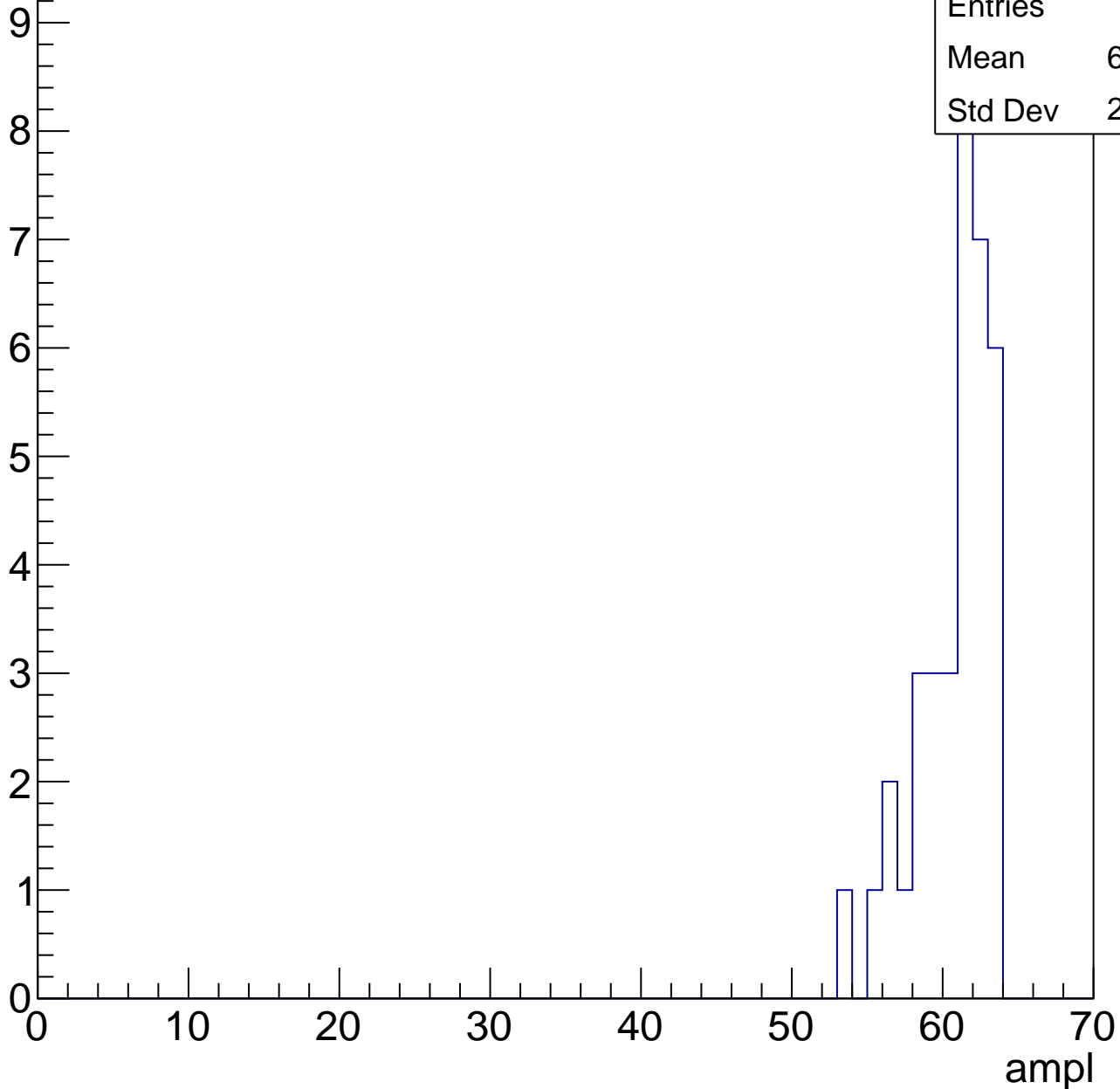


# B0L000S, U26-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	60.25
Std Dev	2.465



# B0L000S, U26-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch64, adc0

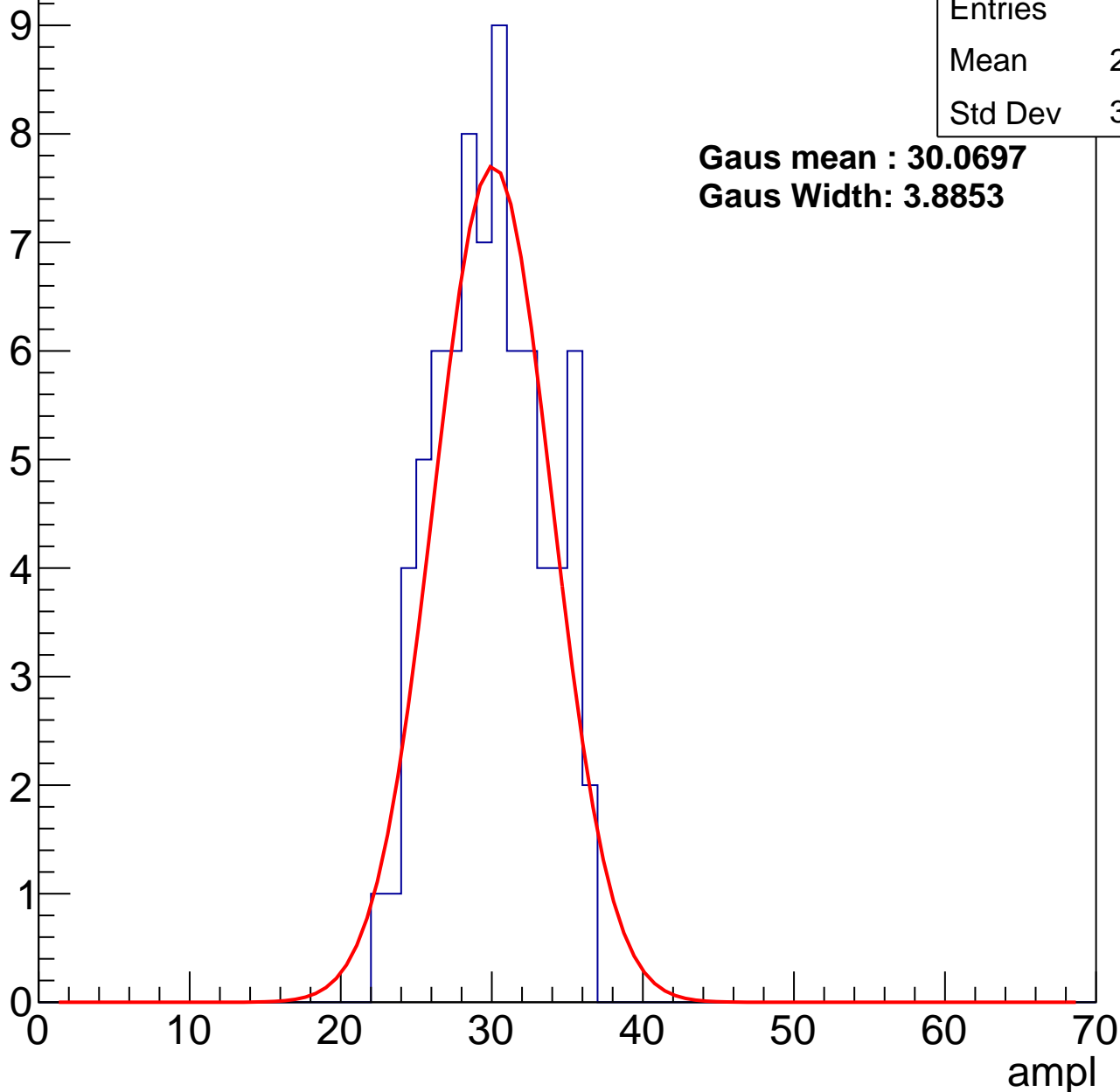
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.45
Std Dev	3.465

**Gaus mean : 30.0697**

**Gaus Width: 3.8853**



# B0L000S, U26-ch64, adc1

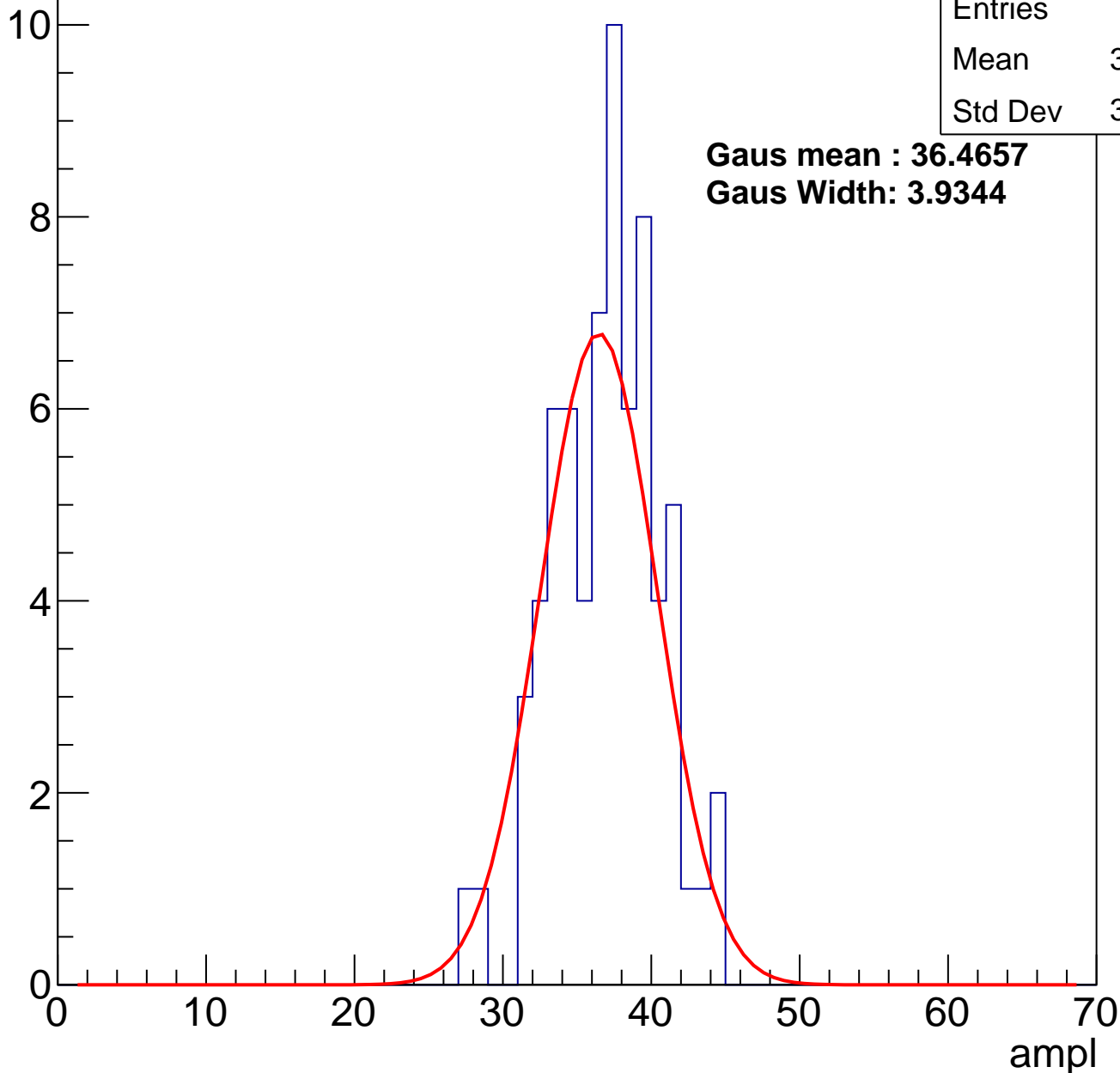
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	36.49
Std Dev	3.525

**Gaus mean : 36.4657**

**Gaus Width: 3.9344**

Entry



# B0L000S, U26-ch64, adc2

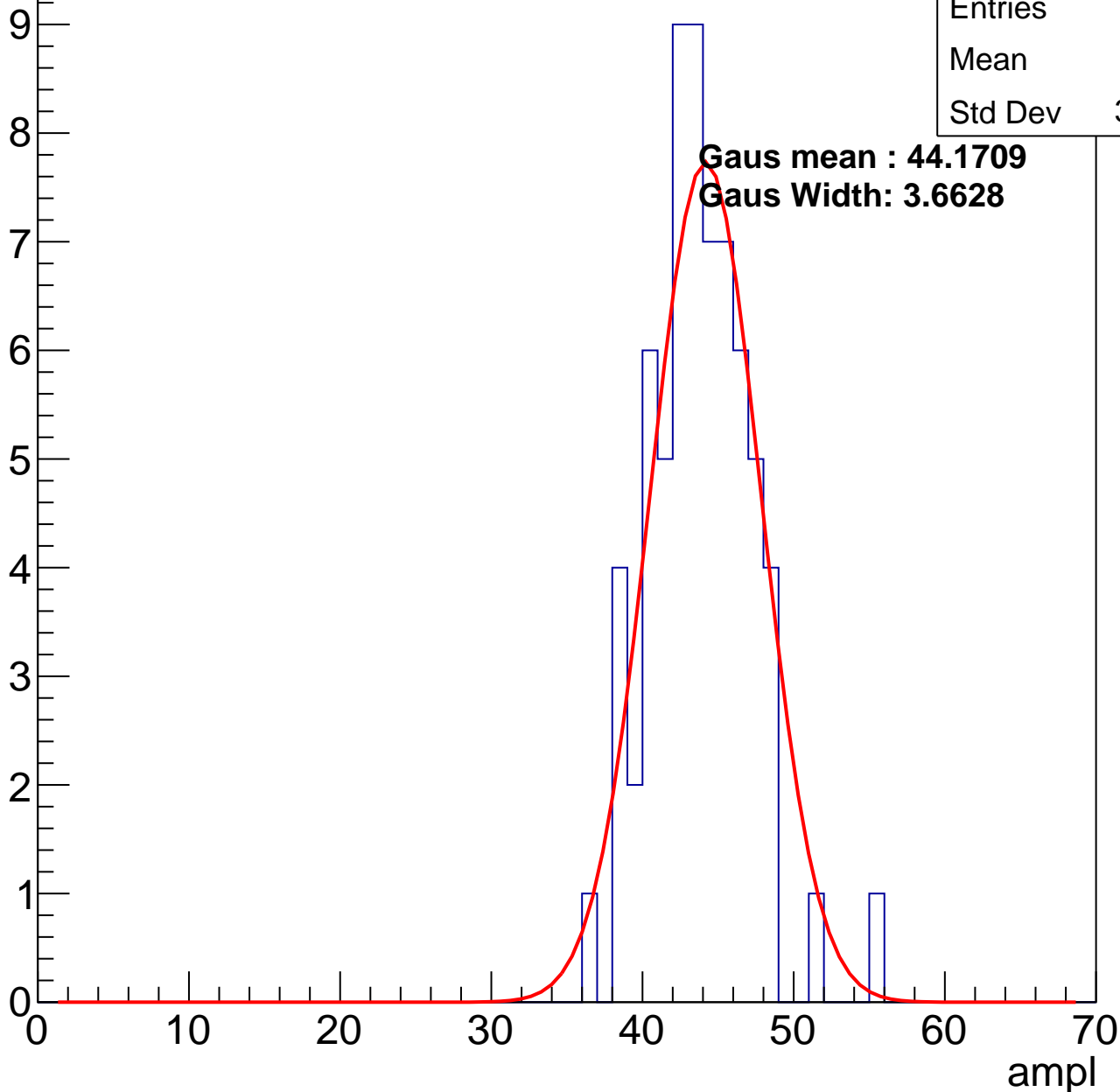
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	43.4
Std Dev	3.301

**Gaus mean : 44.1709**

**Gaus Width: 3.6628**

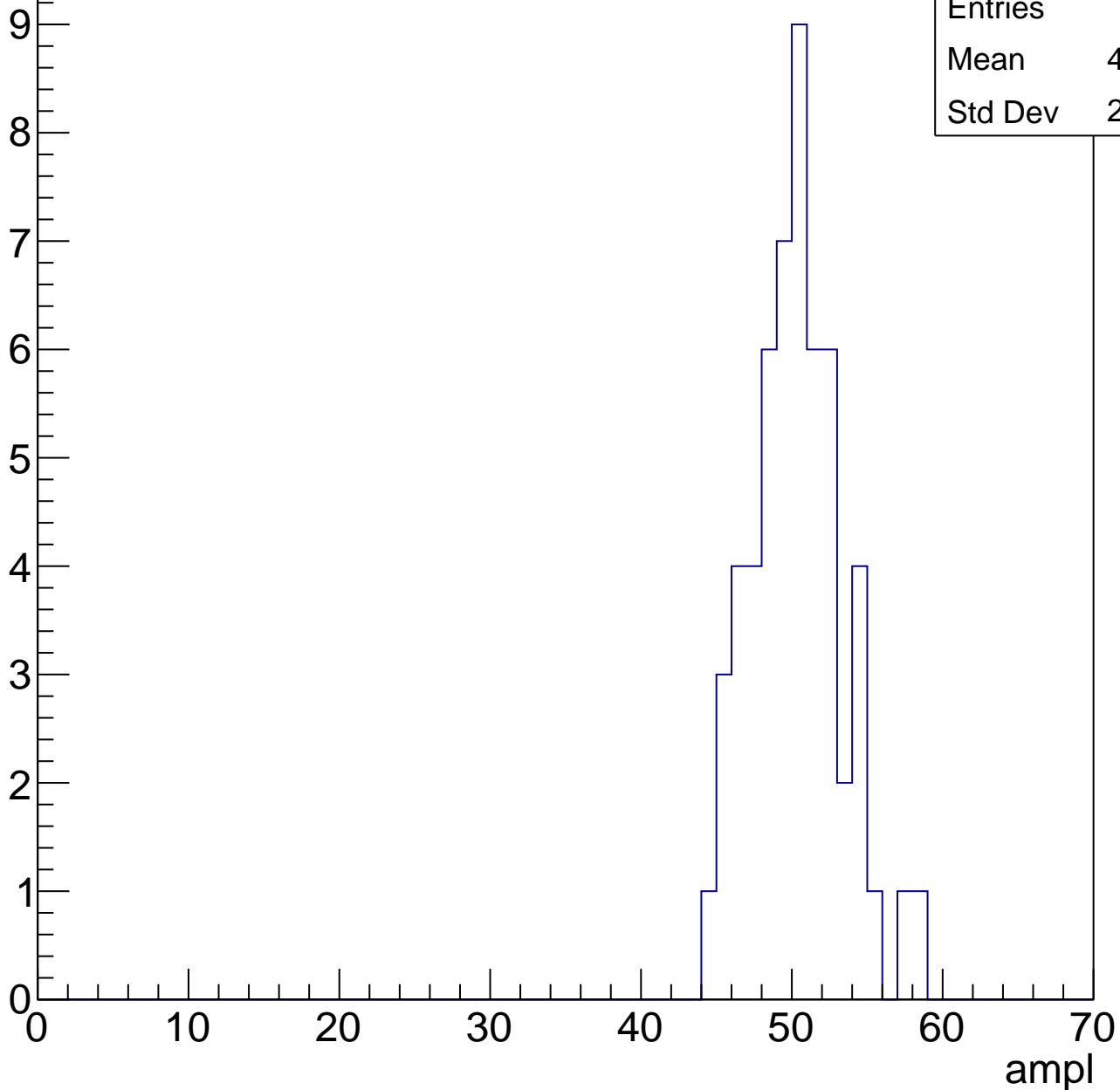


# B0L000S, U26-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.85
Std Dev	2.987

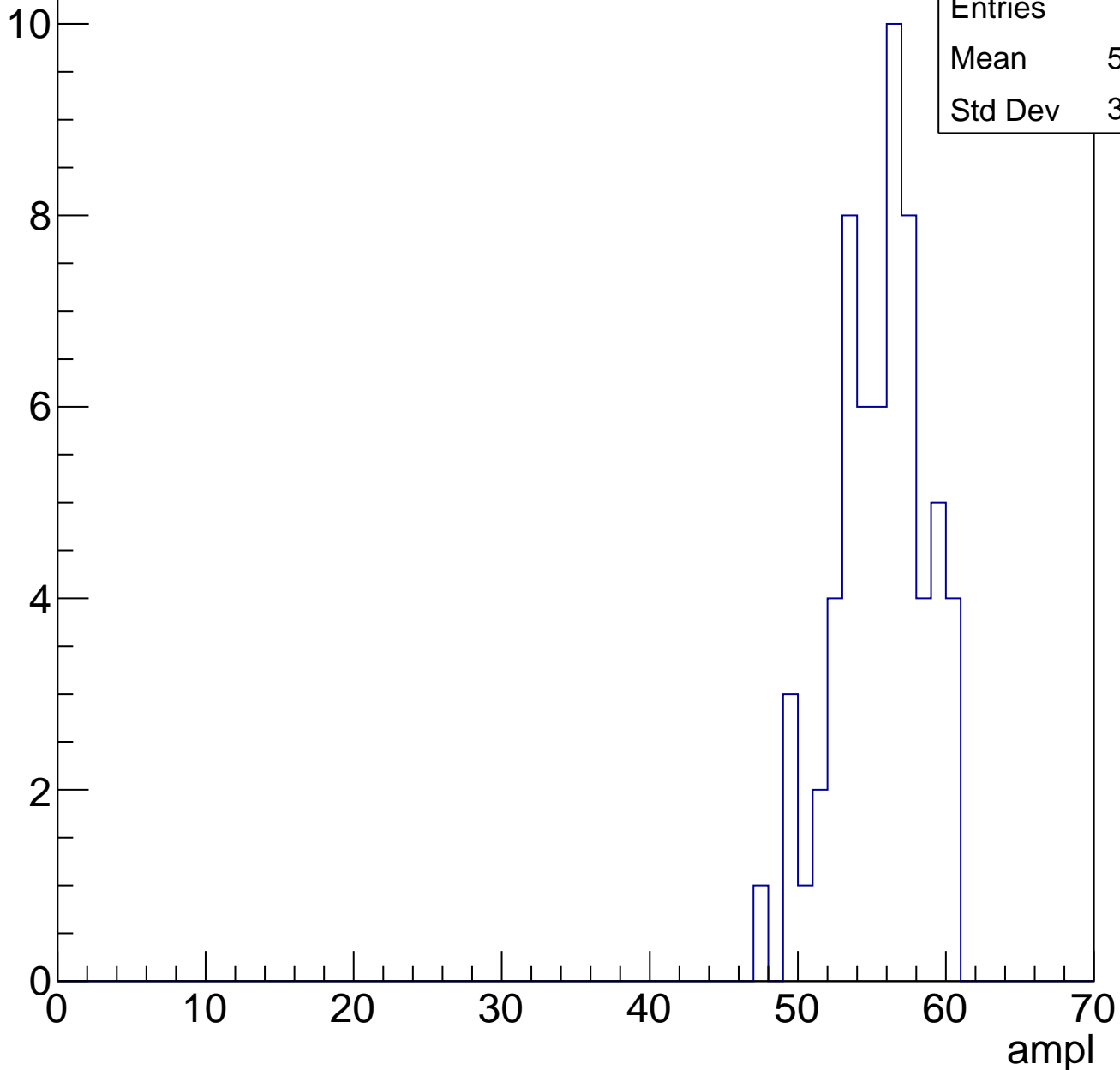


# B0L000S, U26-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	55.08
Std Dev	3.007

Entry

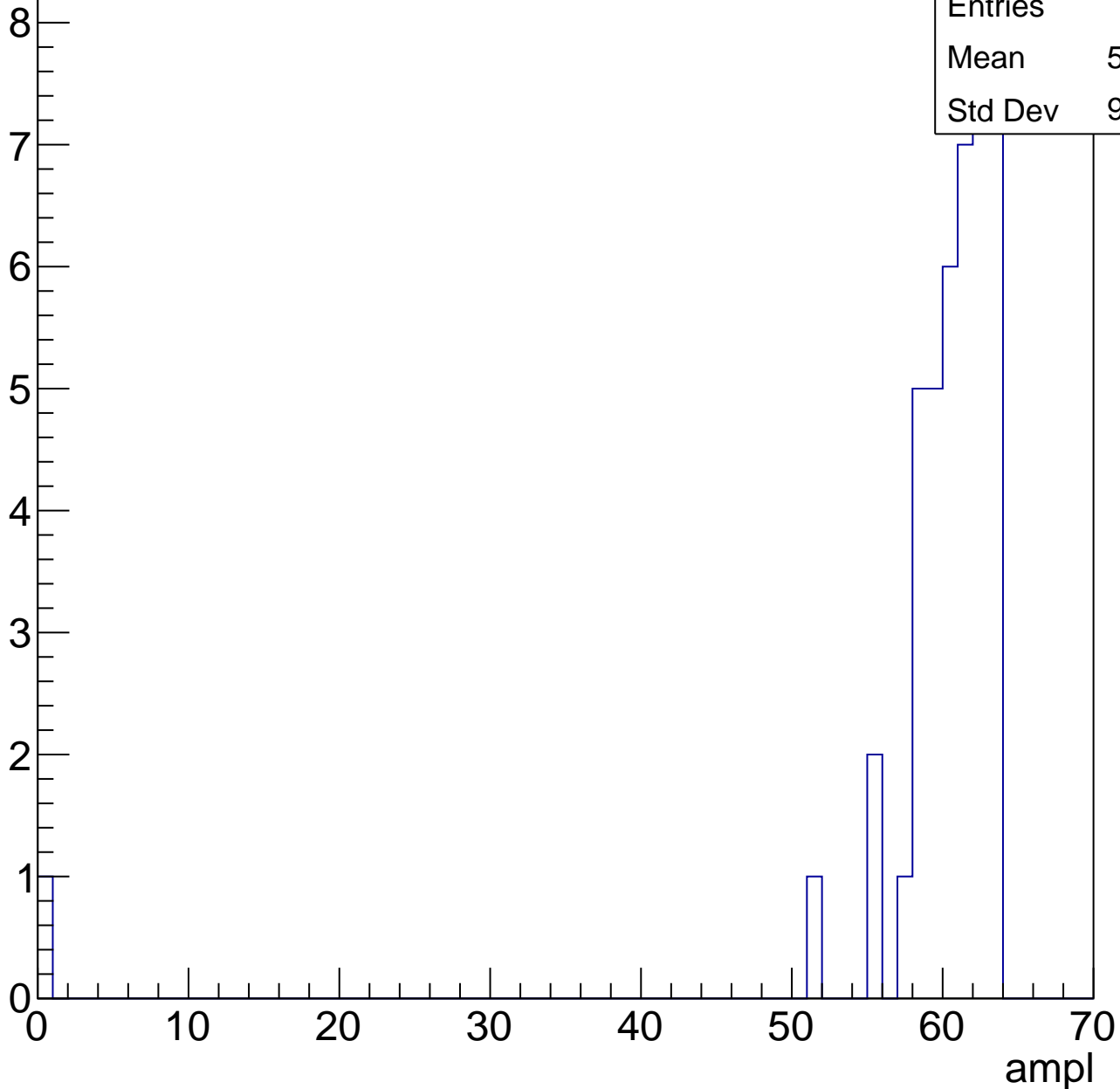


# B0L000S, U26-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

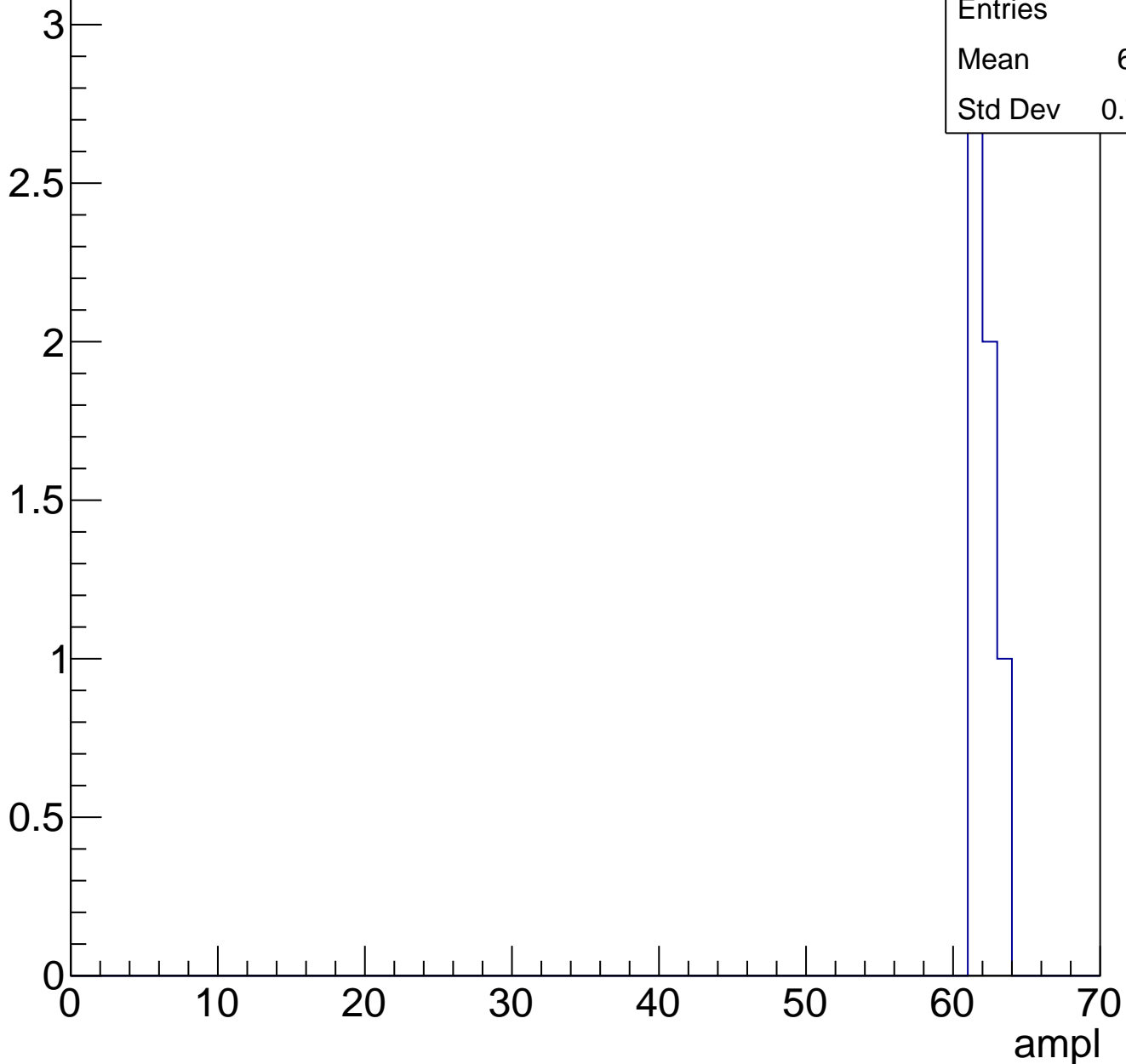
Entries	44
Mean	58.86
Std Dev	9.317



# B0L000S, U26-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

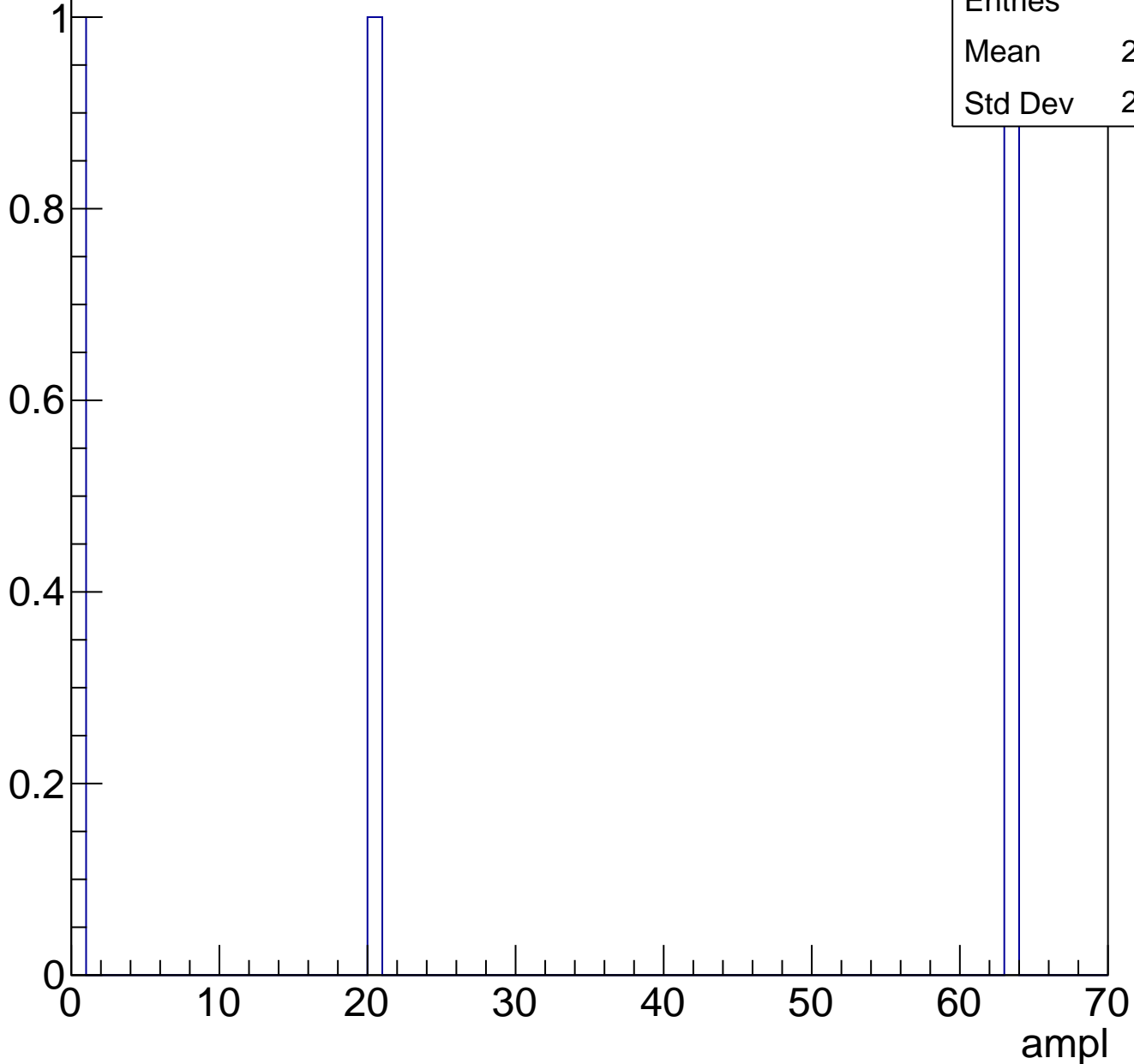




# B0L000S, U26-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	27.67
Std Dev	26.28

# B0L000S, U26-ch65, adc0

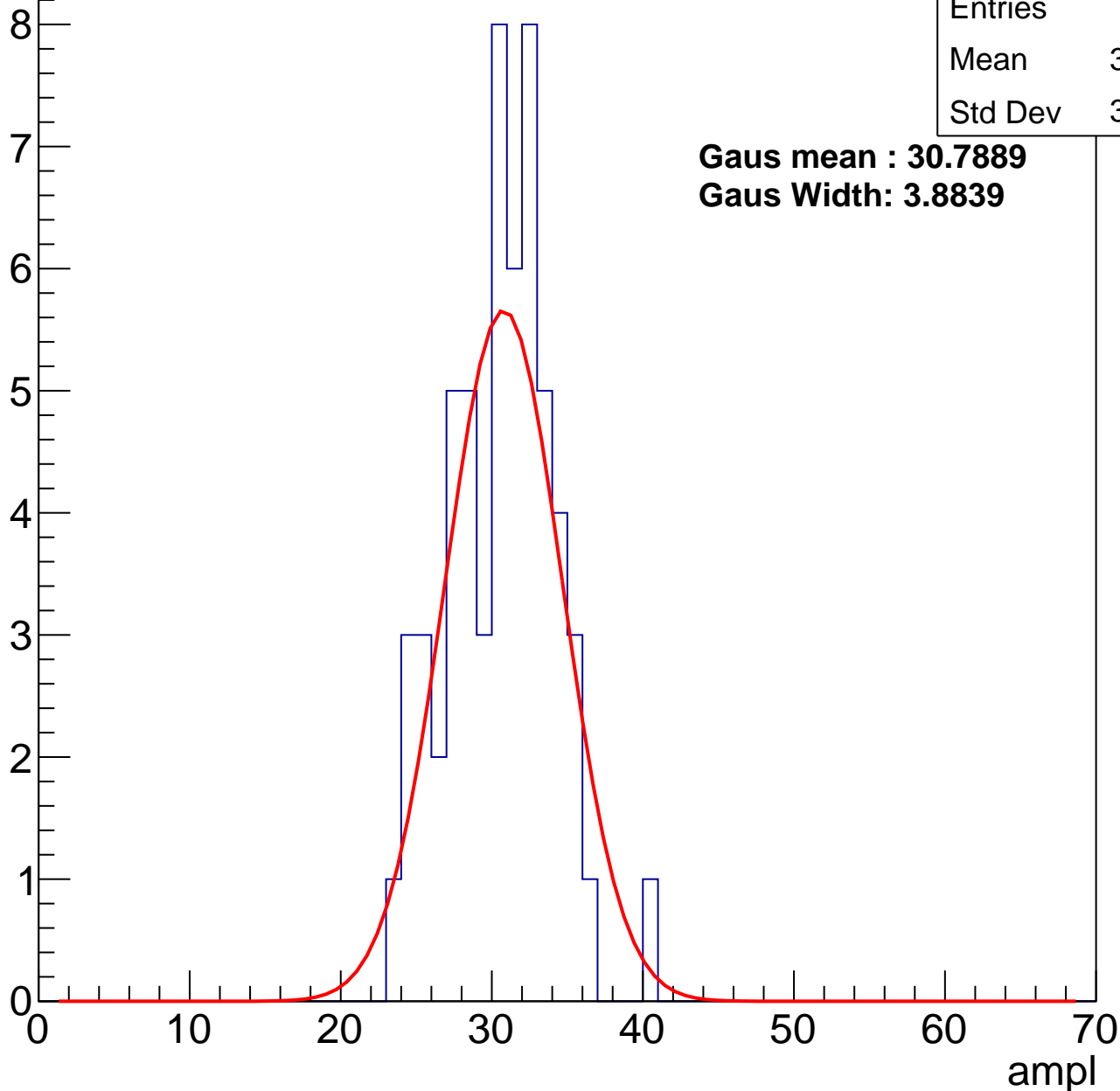
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	30.14
Std Dev	3.446

**Gaus mean : 30.7889**

**Gaus Width: 3.8839**



# B0L000S, U26-ch65, adc1

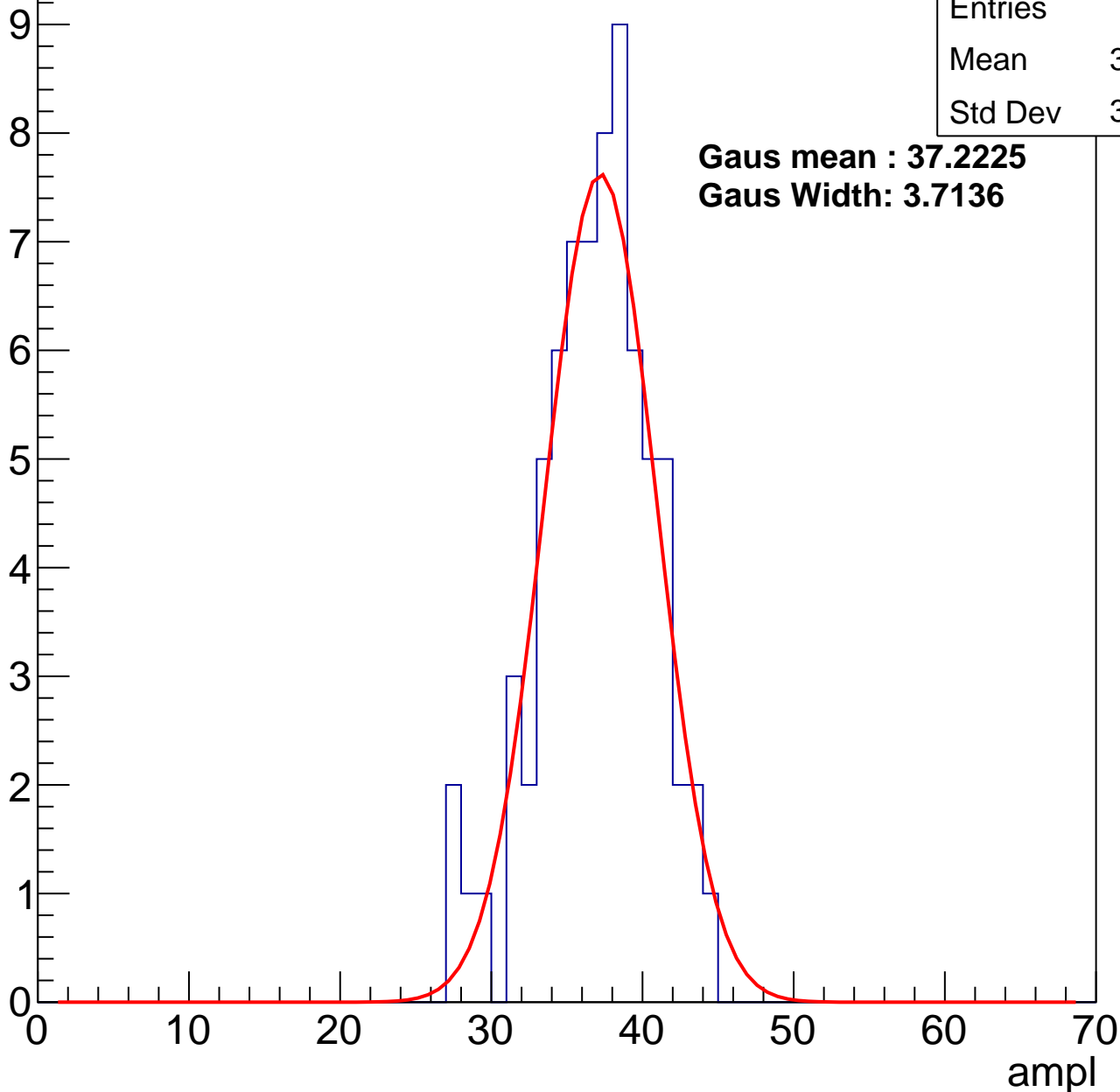
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.46
Std Dev	3.689

**Gaus mean : 37.2225**

**Gaus Width: 3.7136**



# B0L000S, U26-ch65, adc2

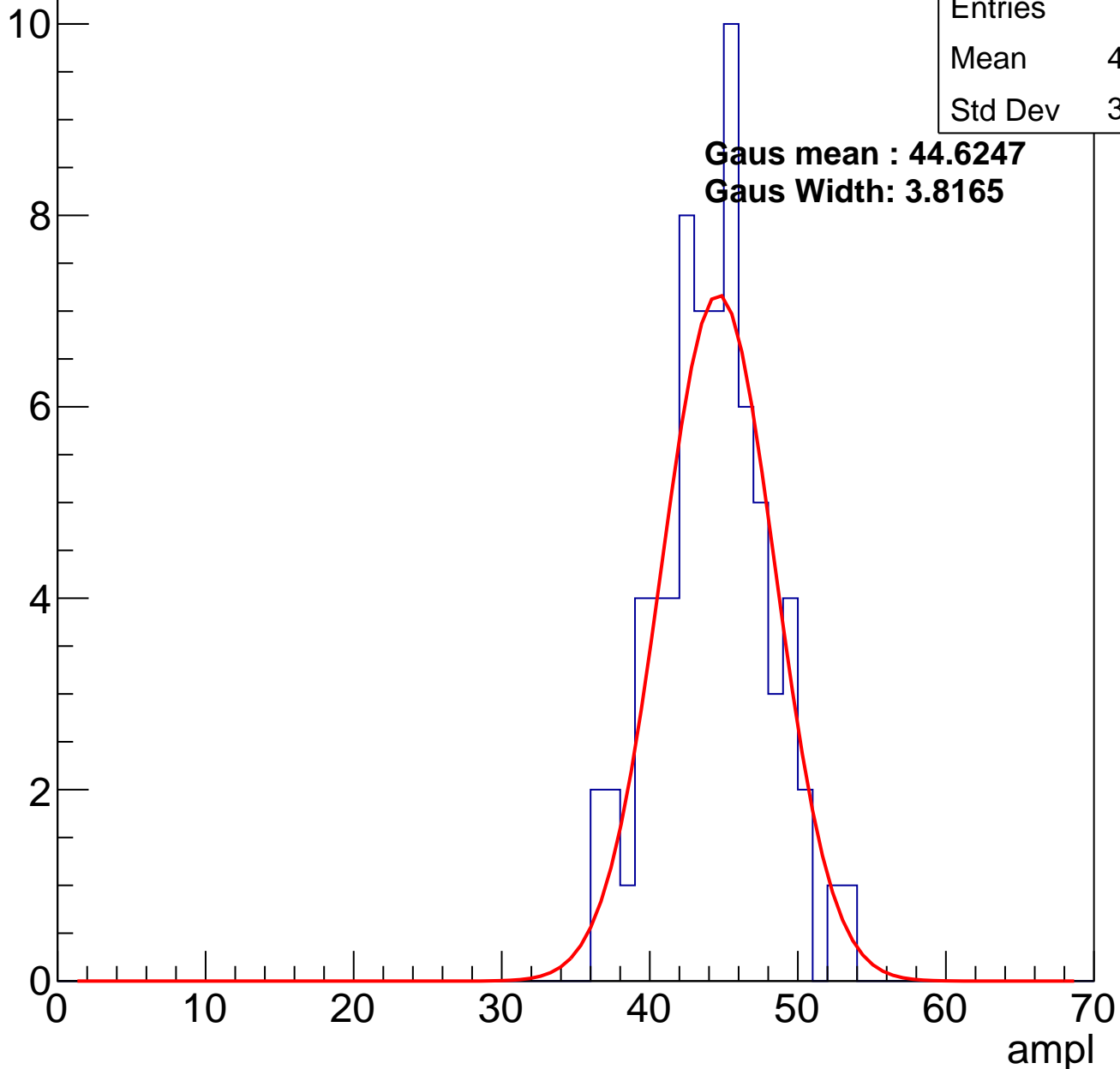
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	43.87
Std Dev	3.646

**Gaus mean : 44.6247**

**Gaus Width: 3.8165**

Entry

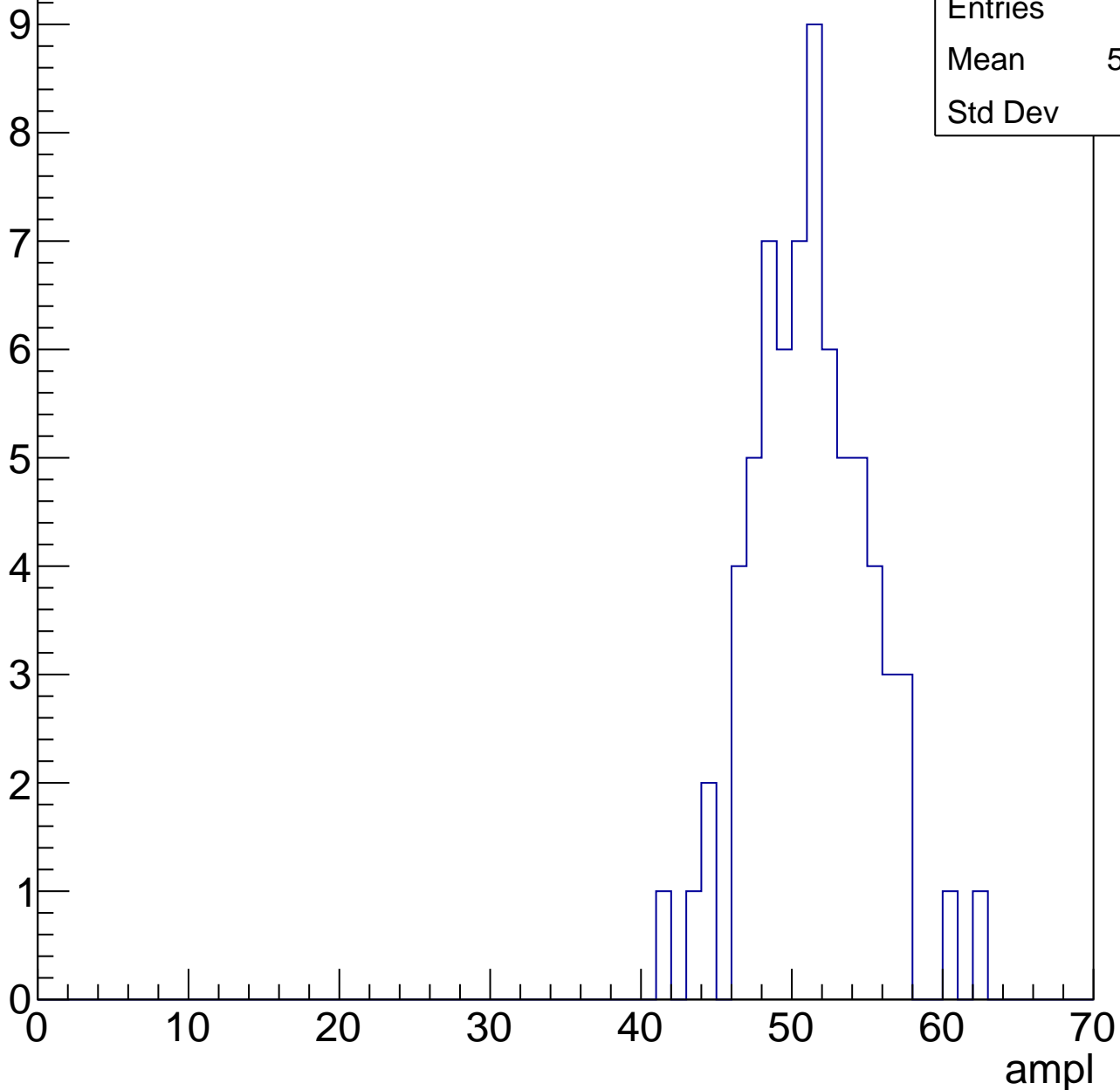


# B0L000S, U26-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	50.83
Std Dev	3.88

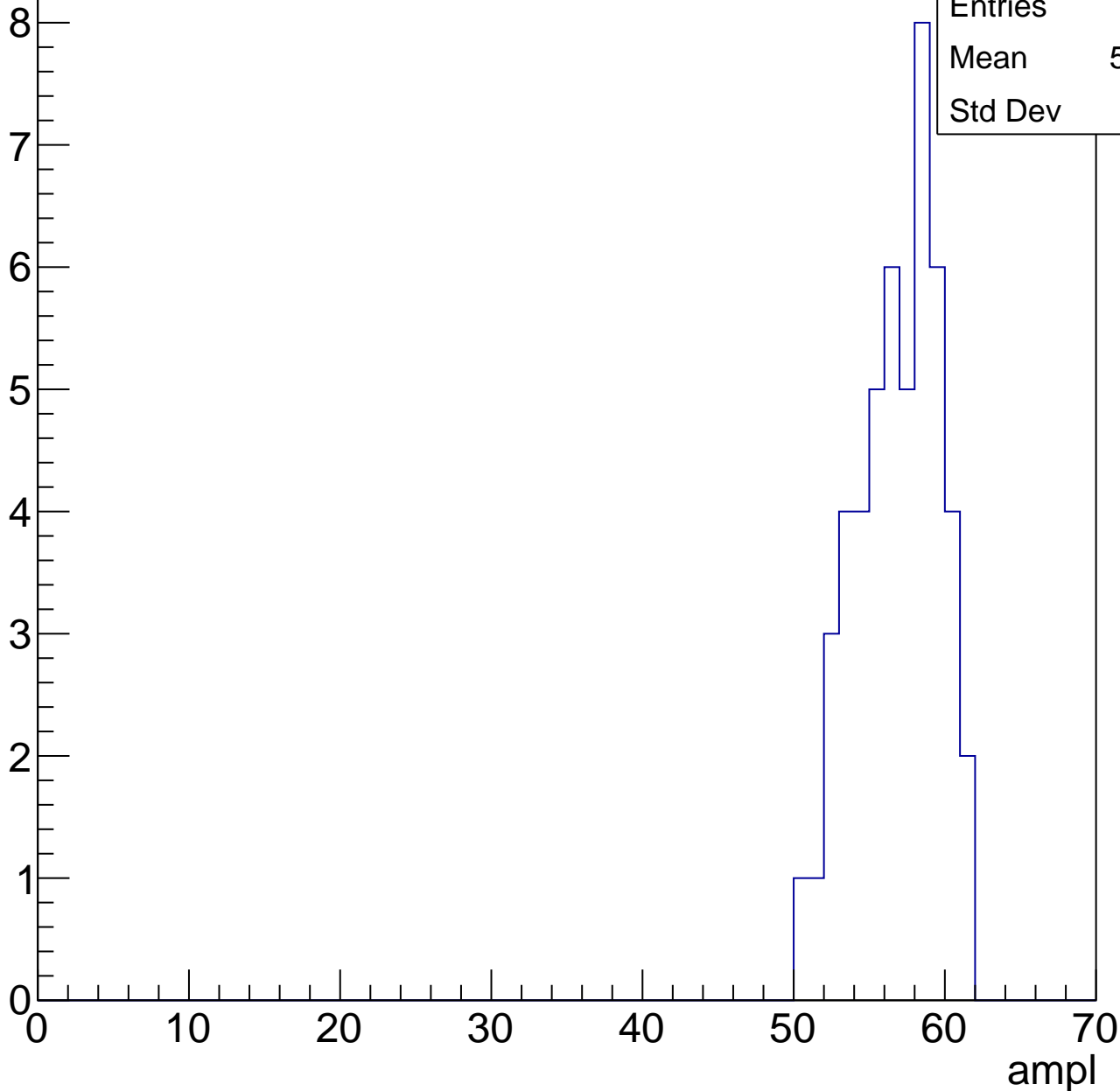


# B0L000S, U26-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	56.35
Std Dev	2.73



# B0L000S, U26-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	59.16
Std Dev	9.403

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

# B0L000S, U26-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

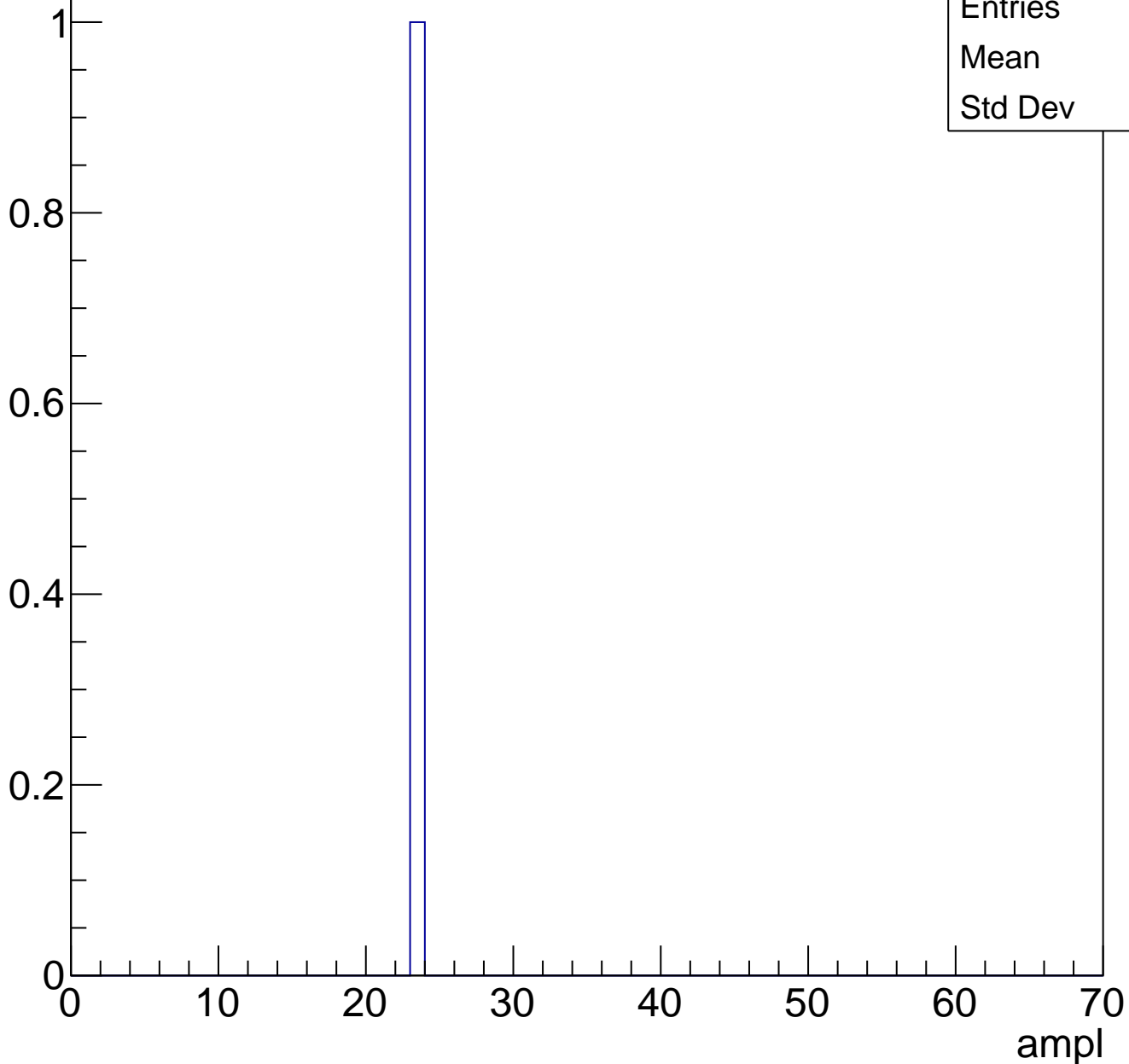




# B0L000S, U26-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch66, adc0

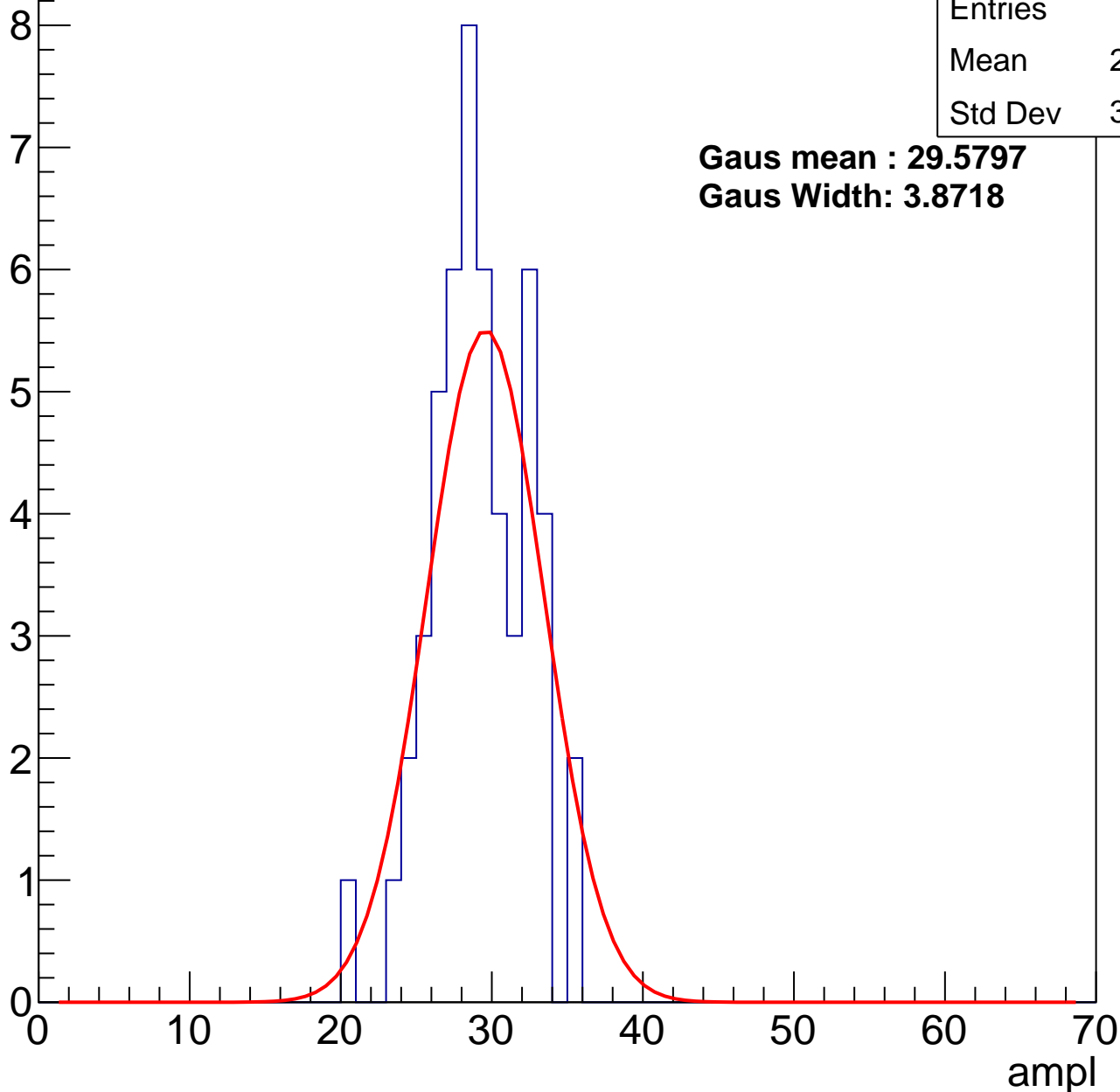
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	28.69
Std Dev	3.109

**Gaus mean : 29.5797**

**Gaus Width: 3.8718**



# B0L000S, U26-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	35.1
Std Dev	3.776

**Gaus mean : 35.2713**

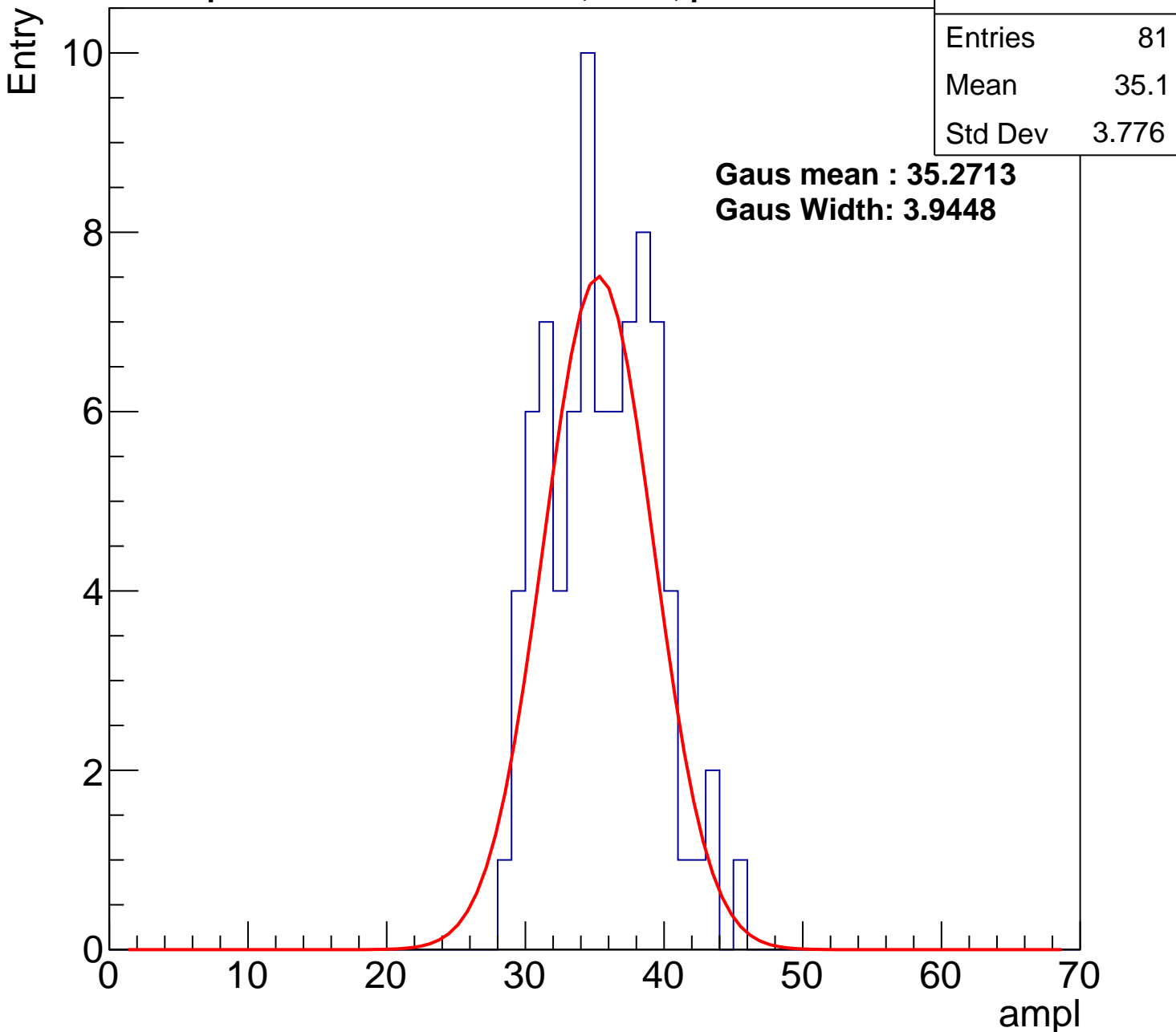
**Gaus Width: 3.9448**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch66, adc2

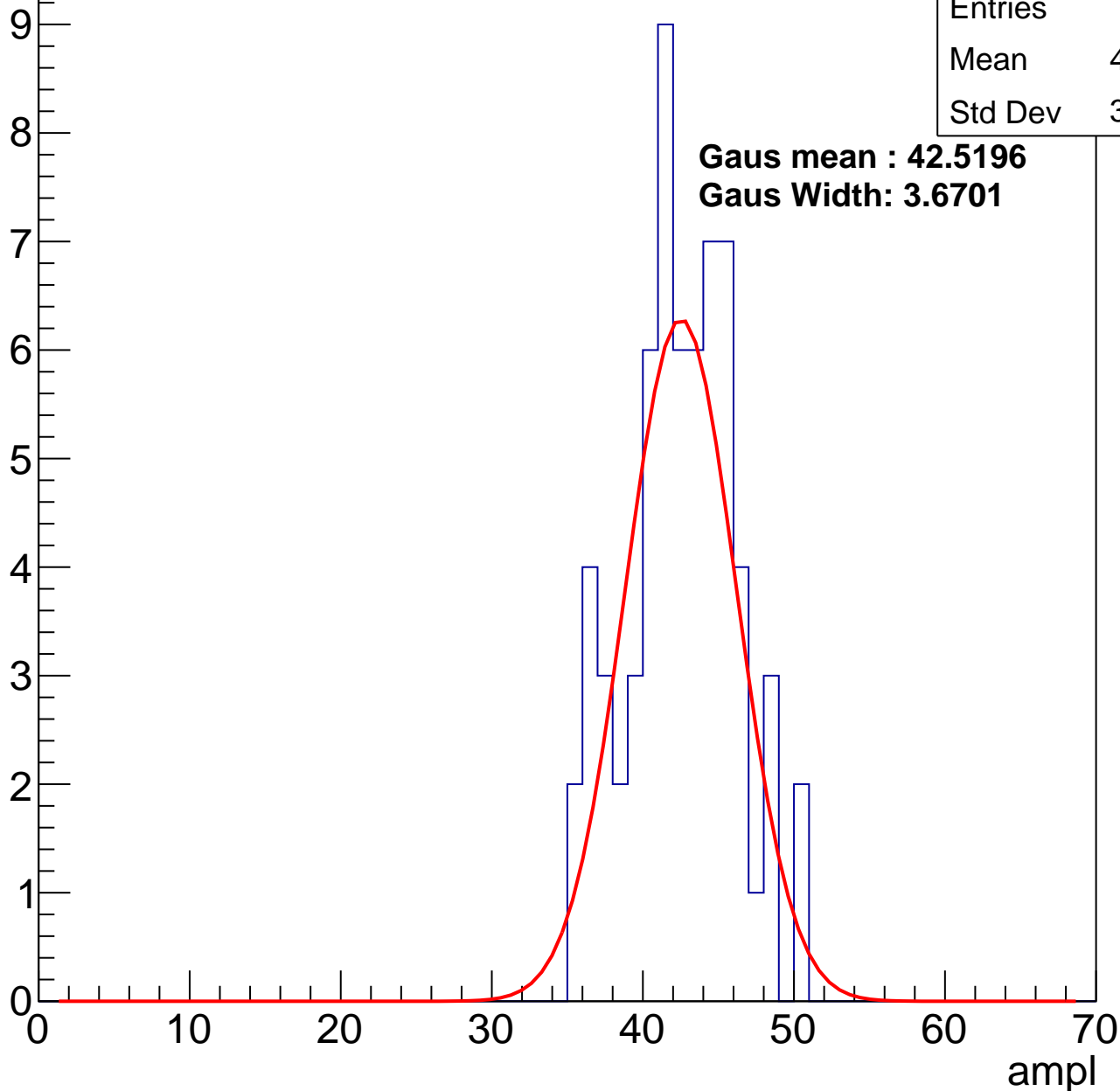
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.08
Std Dev	3.575

**Gaus mean : 42.5196**

**Gaus Width: 3.6701**

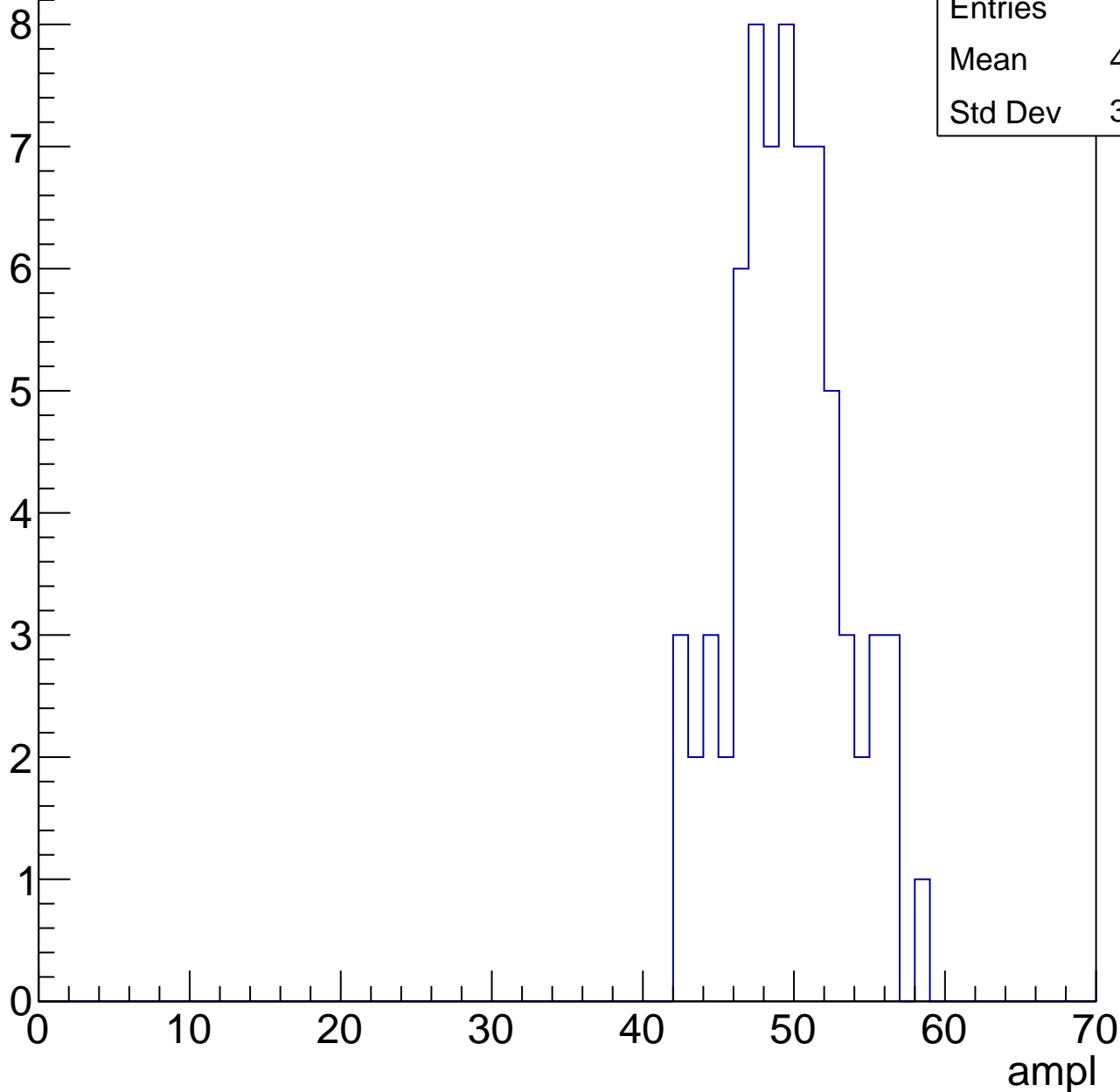


# B0L000S, U26-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.13
Std Dev	3.649

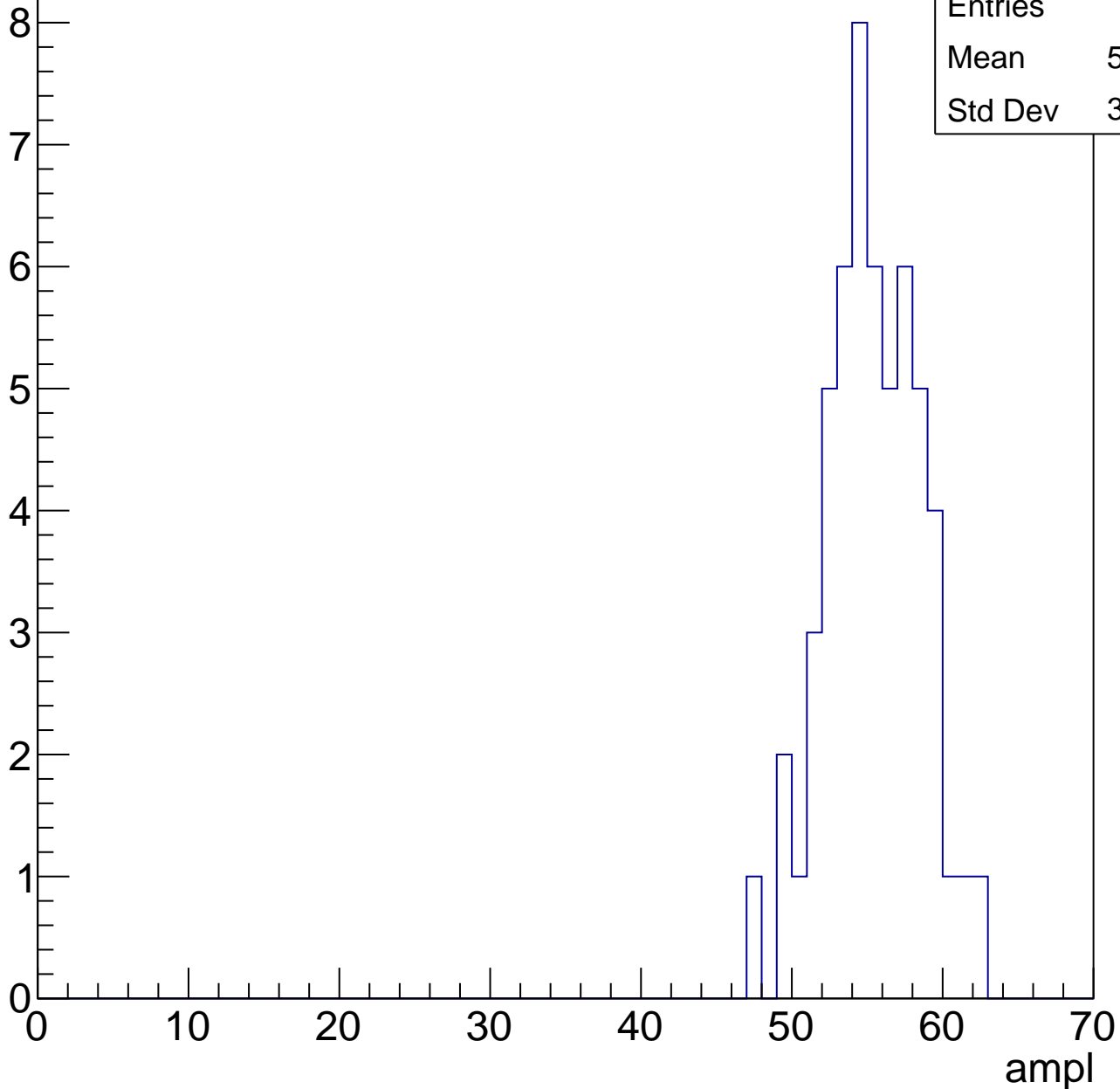


# B0L000S, U26-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	54.89
Std Dev	3.114

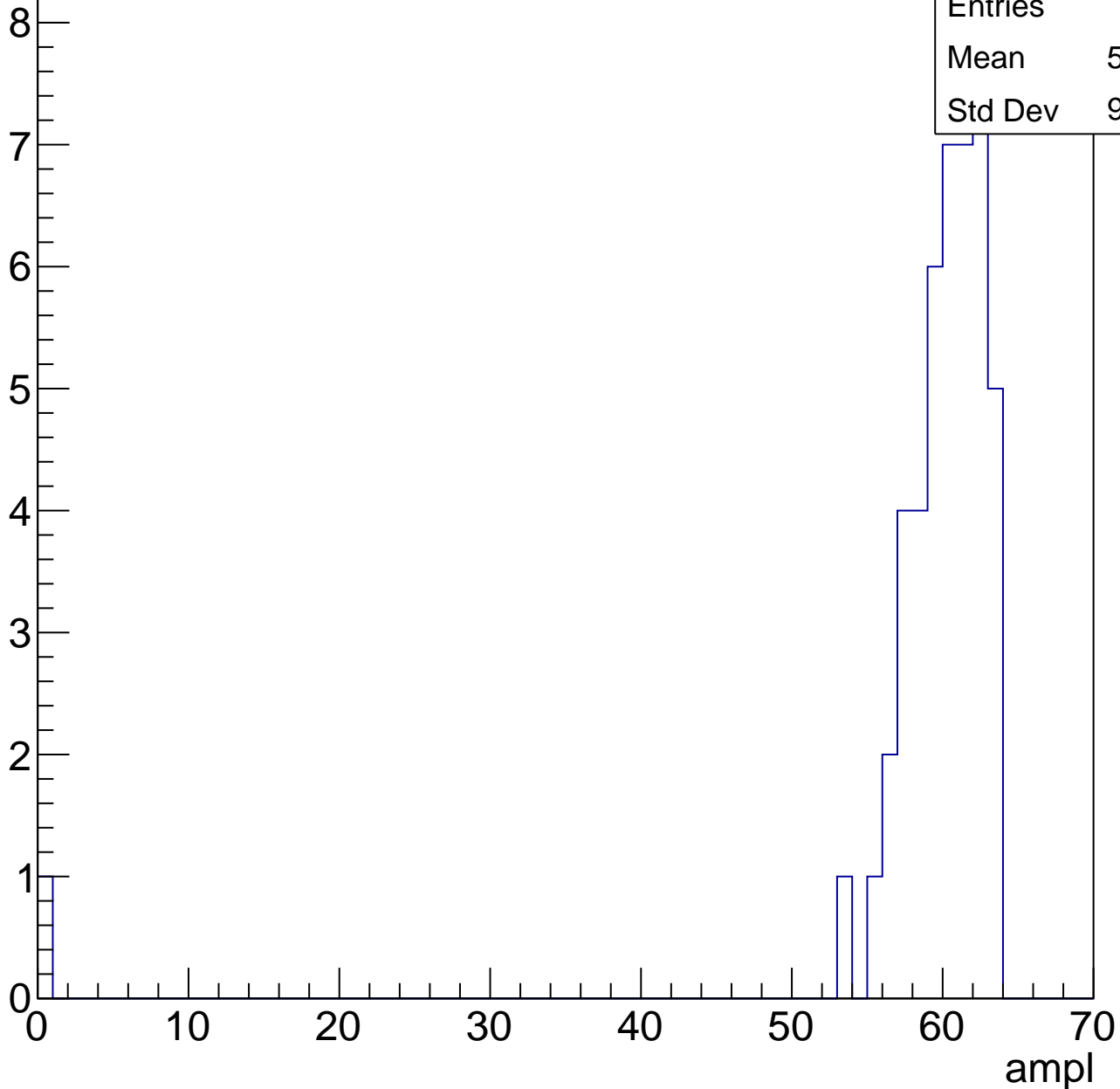


# B0L000S, U26-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

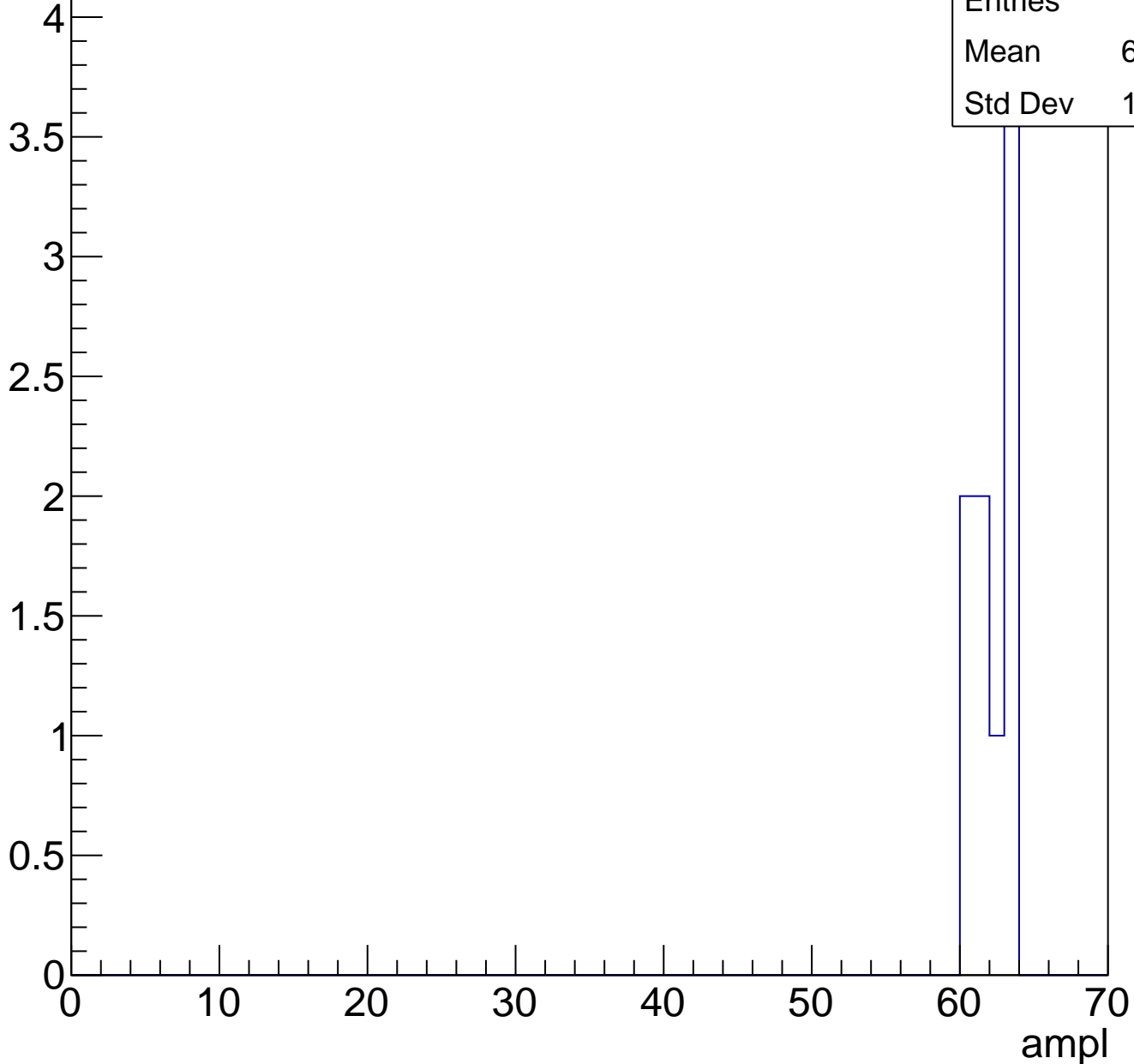
Entries	46
Mean	58.52
Std Dev	9.026



# B0L000S, U26-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	9
Mean	61.78
Std Dev	1.227



# B0L000S, U26-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch67, adc0

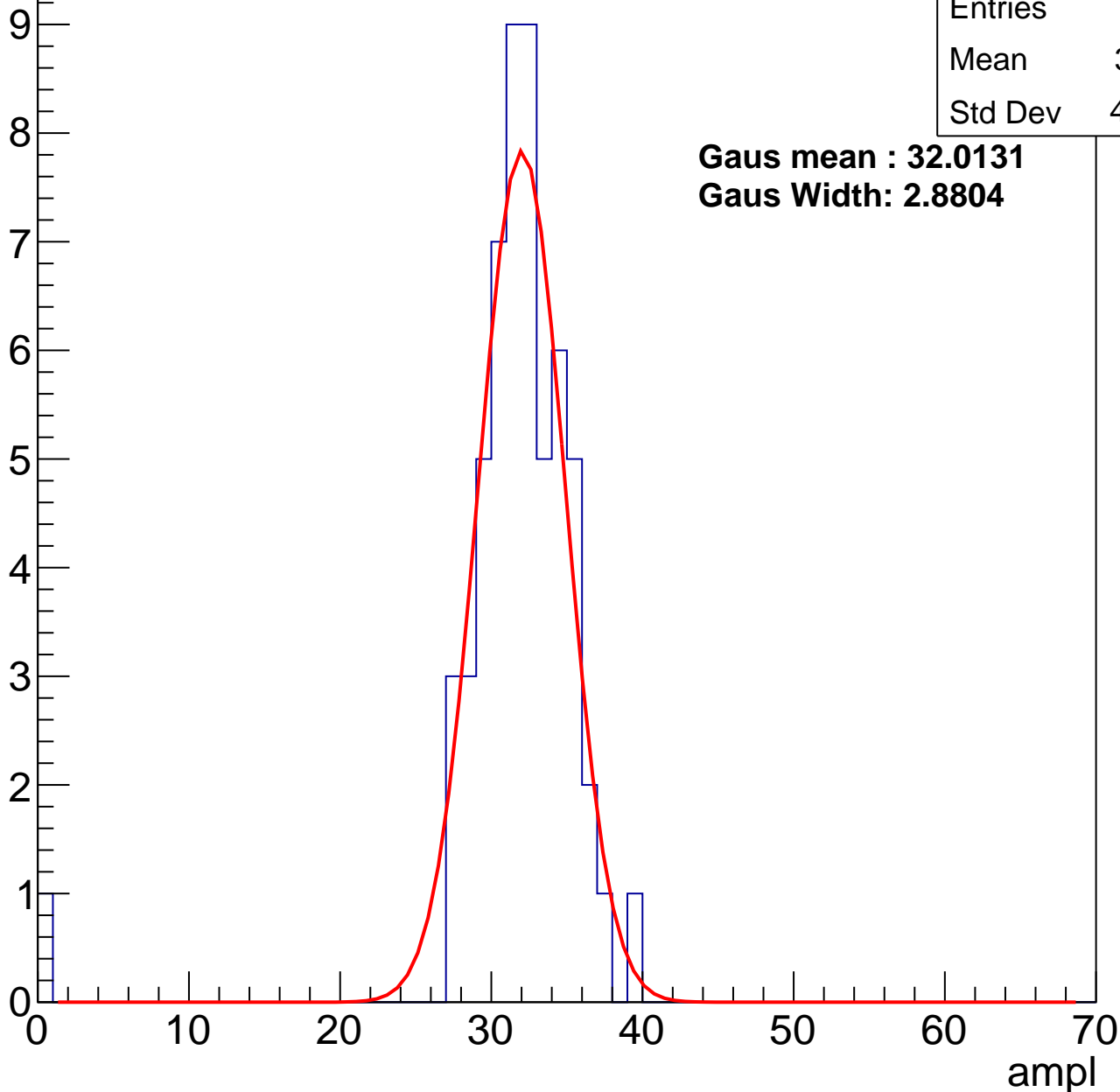
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	31.21
Std Dev	4.909

**Gaus mean : 32.0131**

**Gaus Width: 2.8804**



# B0L000S, U26-ch67, adc1

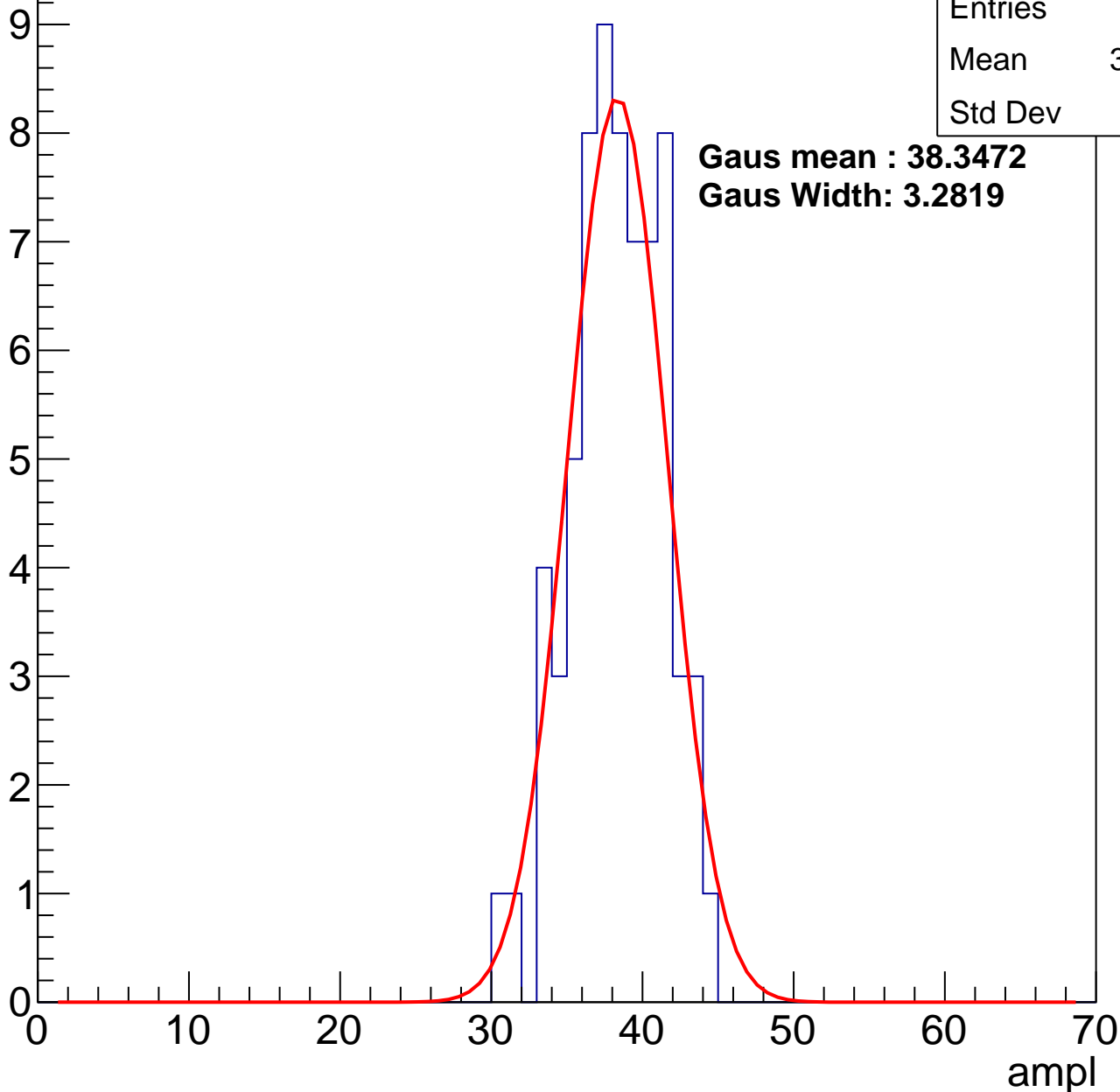
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.87
Std Dev	3

**Gaus mean : 38.3472**

**Gaus Width: 3.2819**



# B0L000S, U26-ch67, adc2

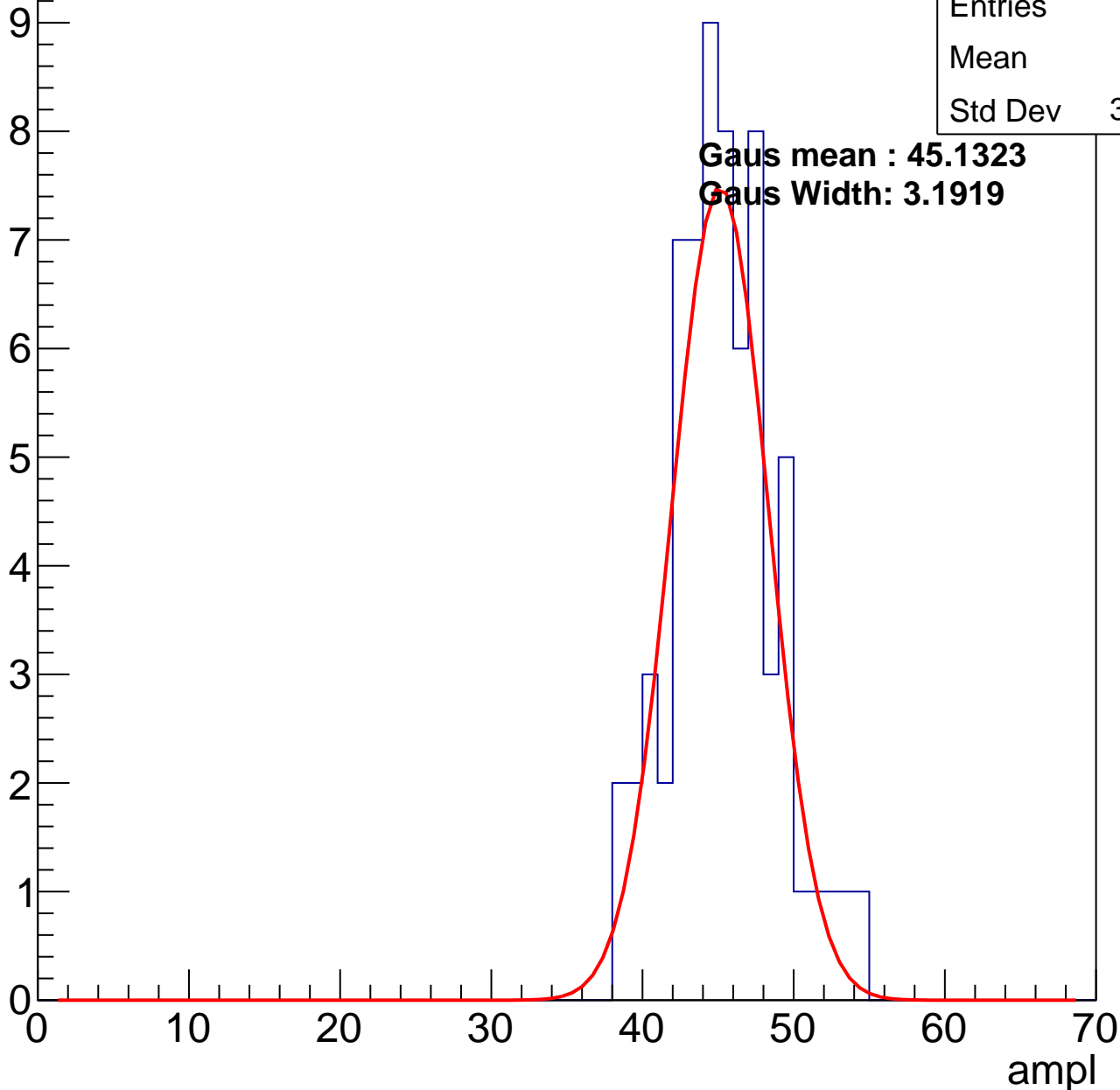
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	44.9
Std Dev	3.404

Gaus mean : 45.1323

Gaus Width: 3.1919

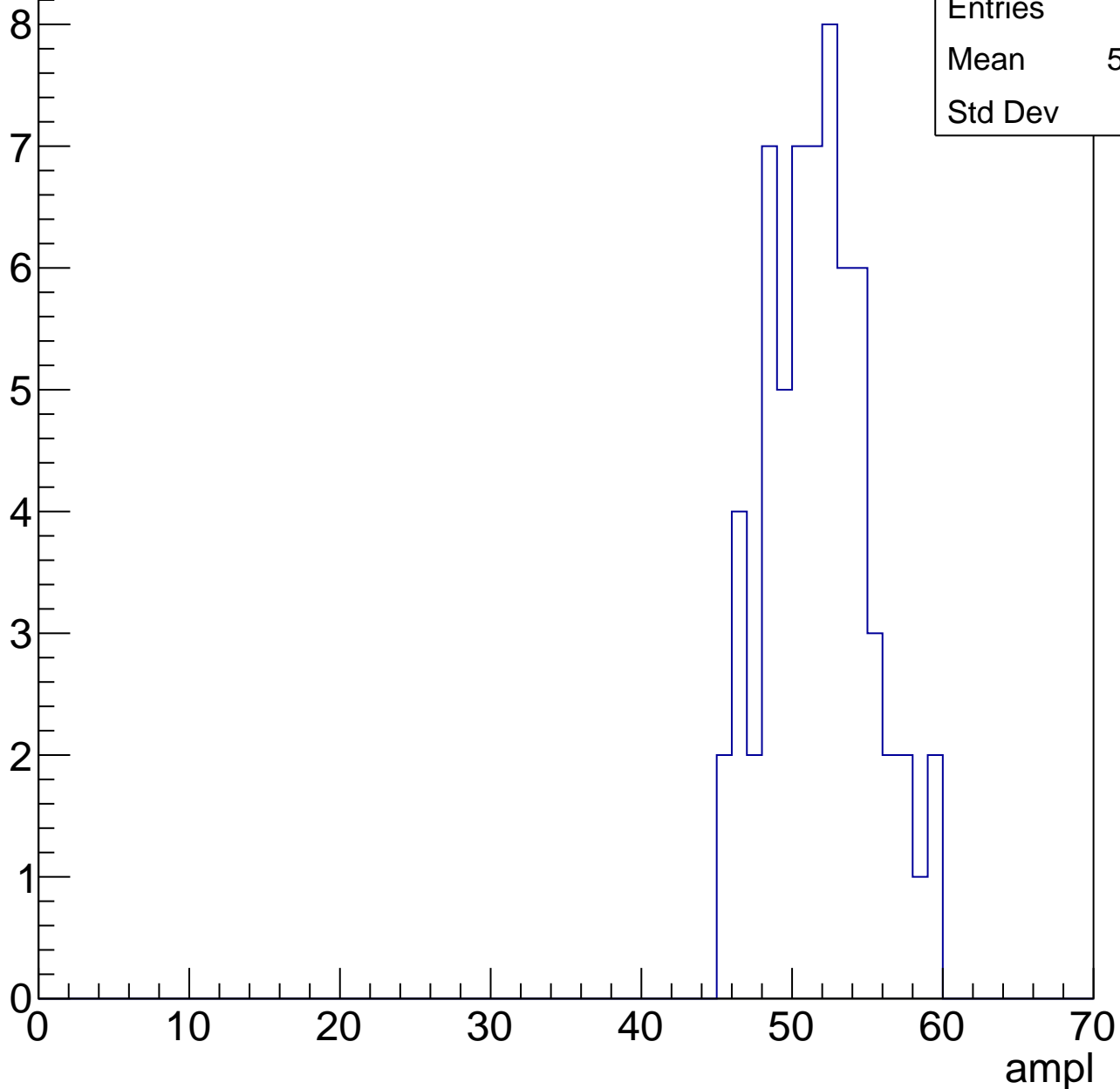


# B0L000S, U26-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	51.27
Std Dev	3.36

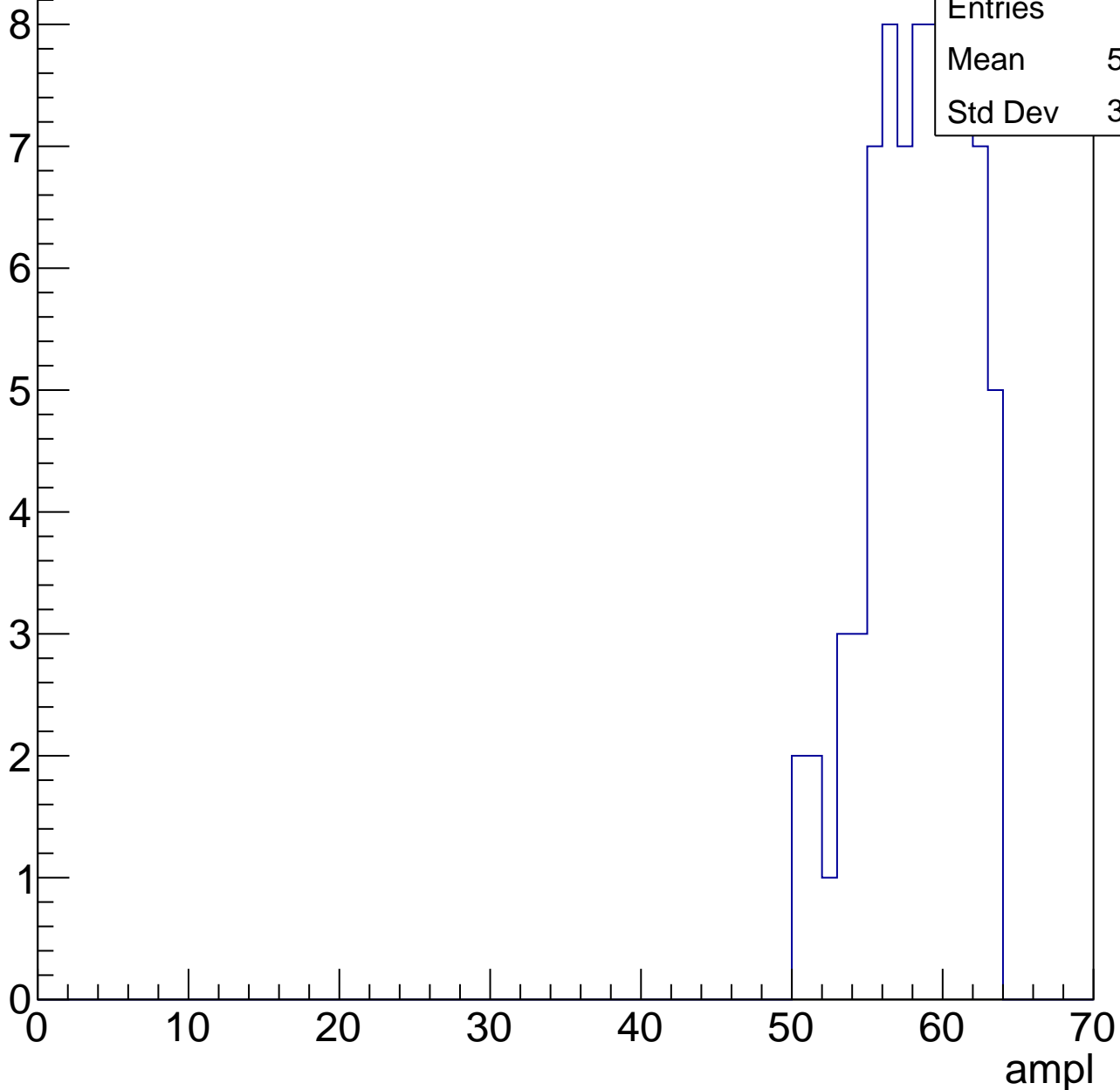


# B0L000S, U26-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	57.92
Std Dev	3.294

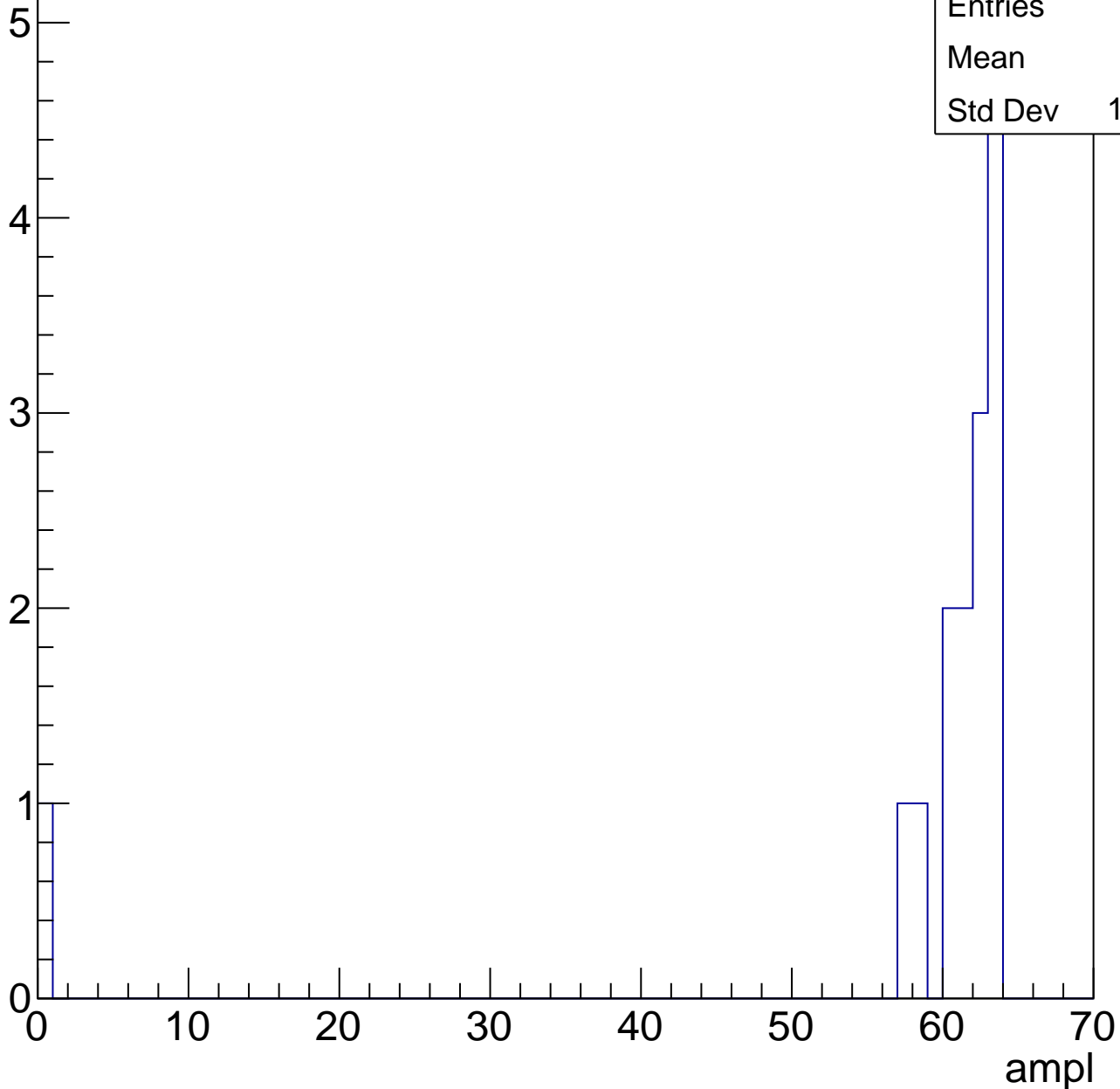


# B0L000S, U26-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	57.2
Std Dev	15.39



# B0L000S, U26-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

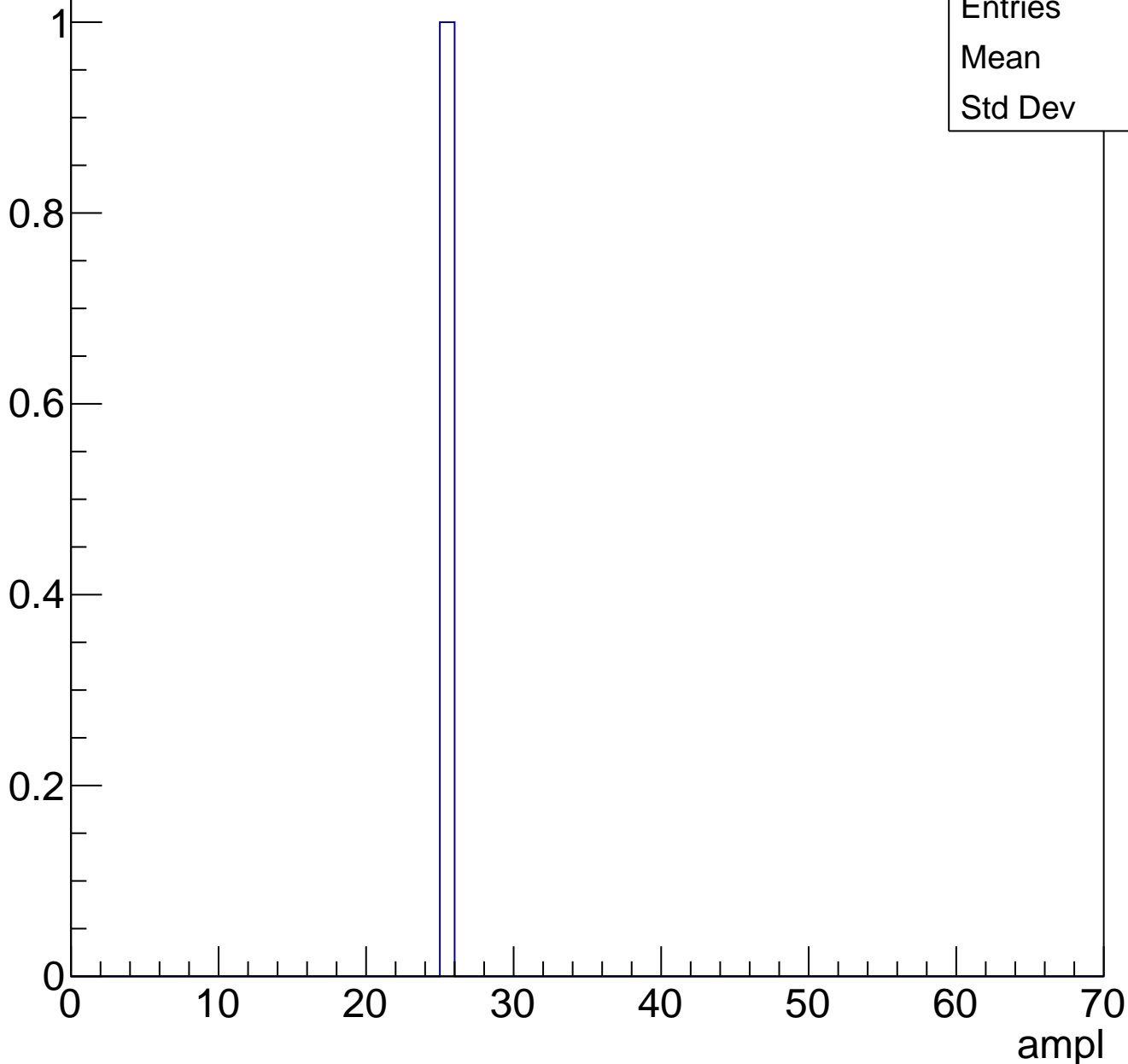




# B0L000S, U26-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L000S, U26-ch68, adc0

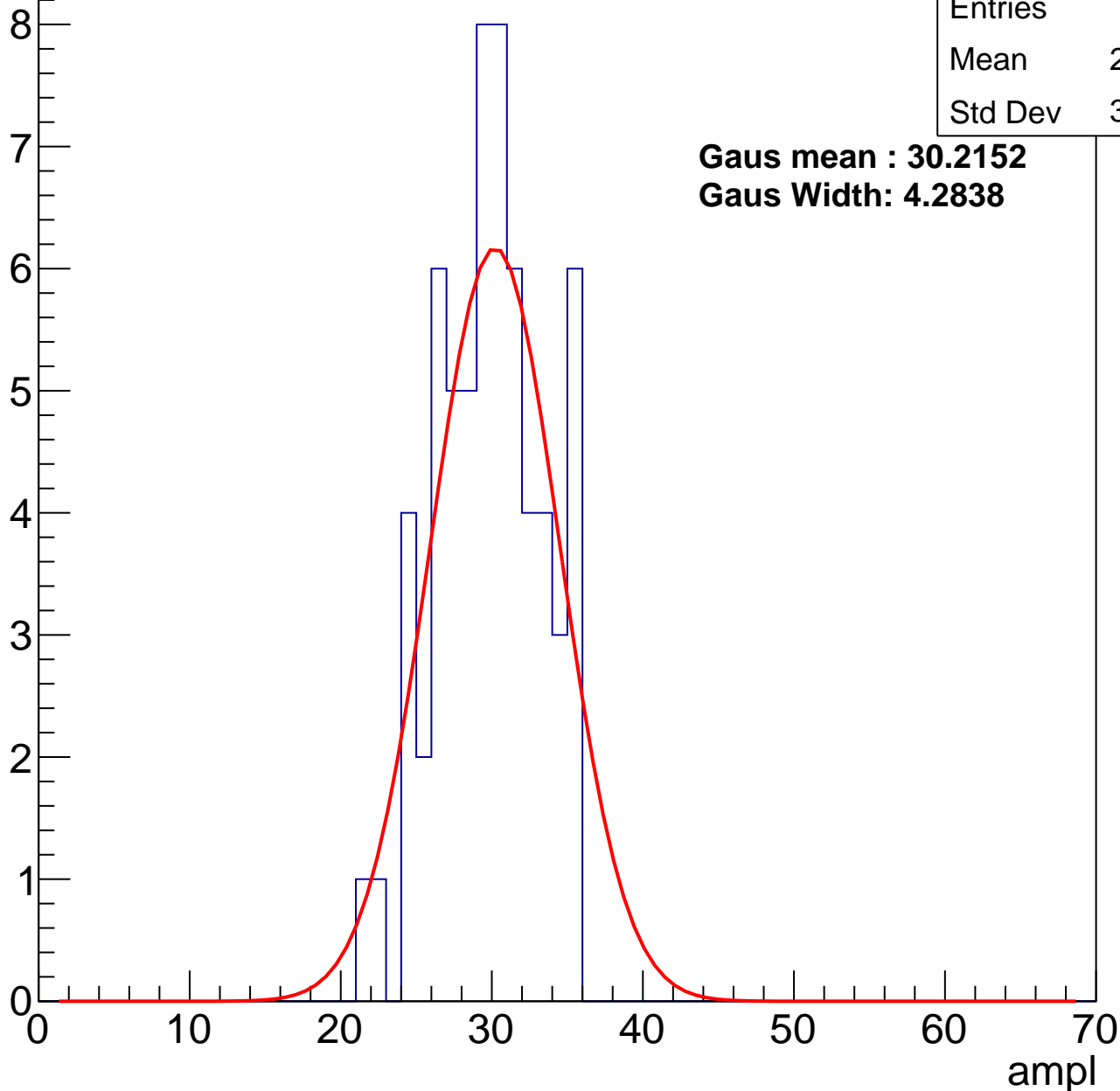
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.37
Std Dev	3.424

**Gaus mean : 30.2152**

**Gaus Width: 4.2838**



# B0L000S, U26-ch68, adc1

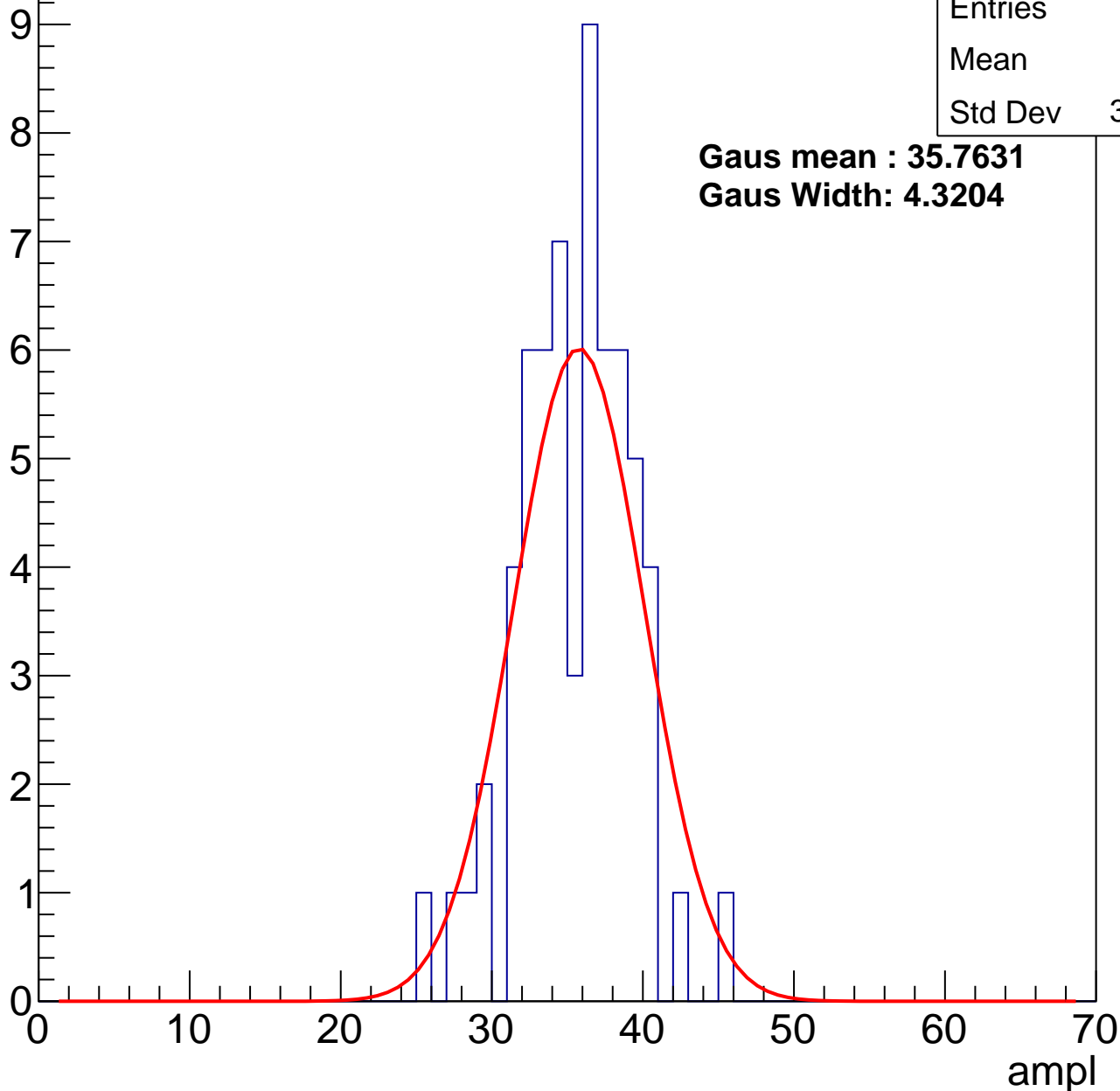
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	35.1
Std Dev	3.672

**Gaus mean : 35.7631**

**Gaus Width: 4.3204**



# B0L000S, U26-ch68, adc2

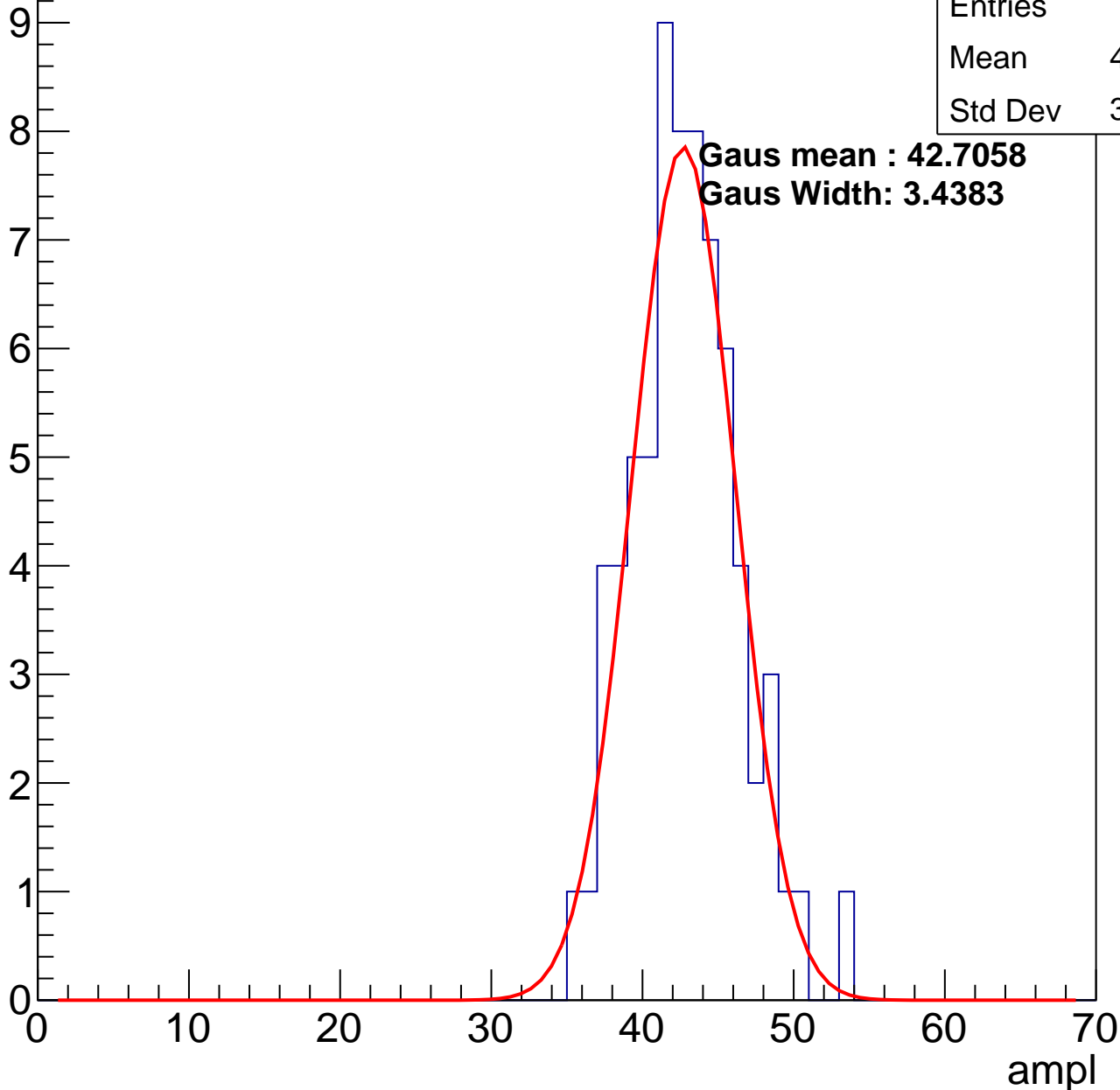
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.39
Std Dev	3.502

**Gaus mean : 42.7058**

**Gaus Width: 3.4383**

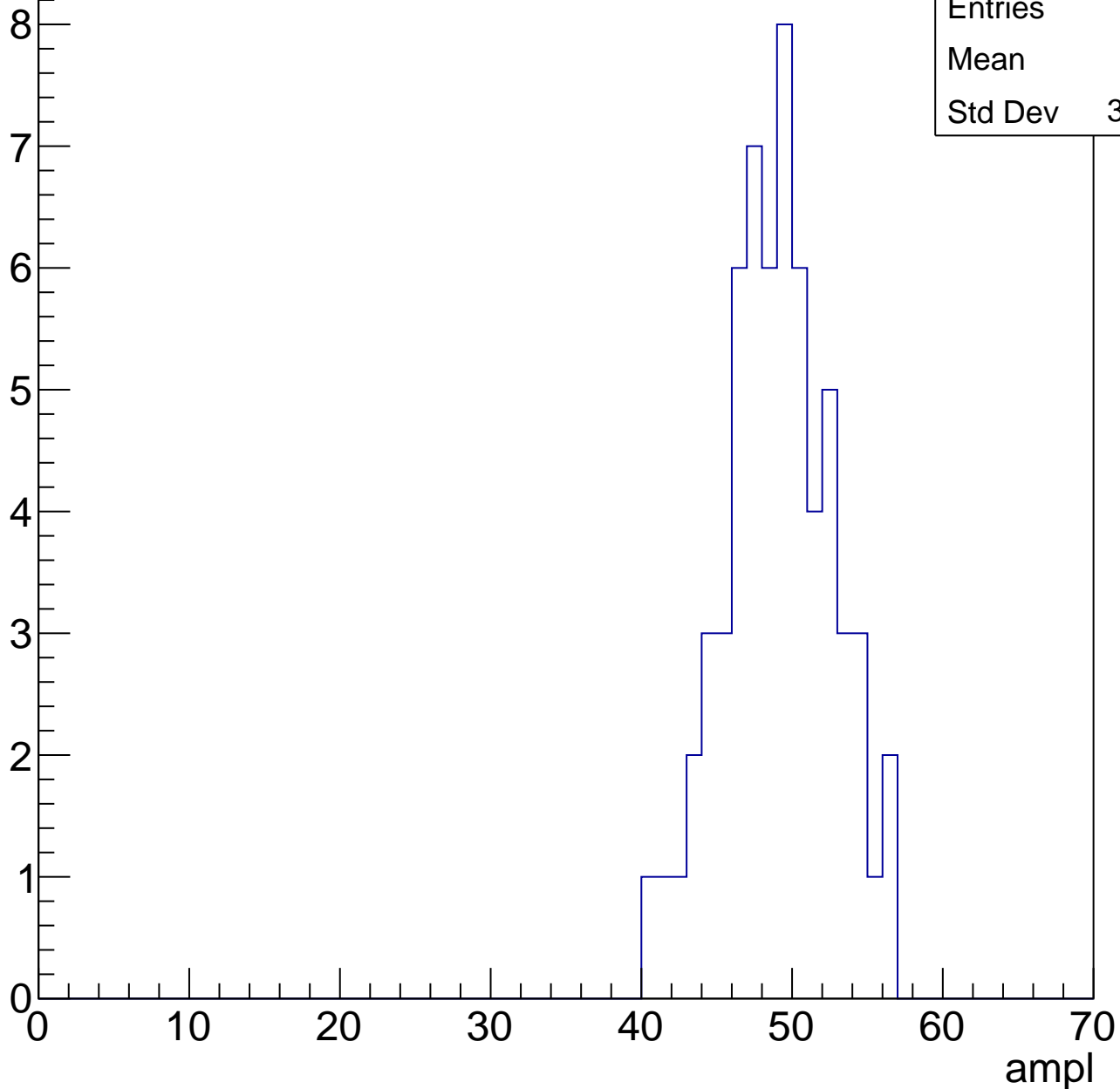


# B0L000S, U26-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

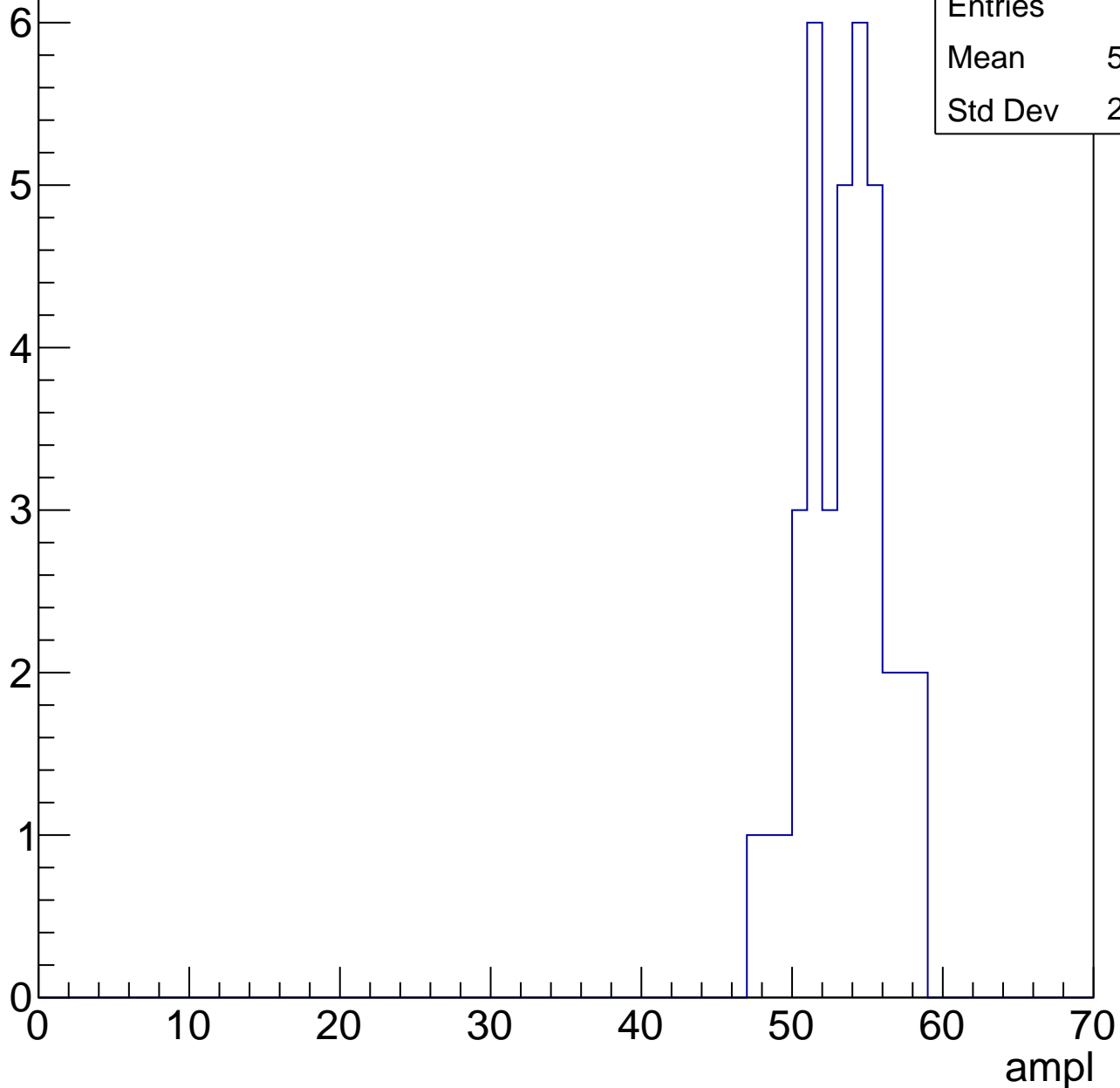
Entries	62
Mean	48.6
Std Dev	3.572



# B0L000S, U26-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	37
Mean	53.03
Std Dev	2.635

# B0L000S, U26-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

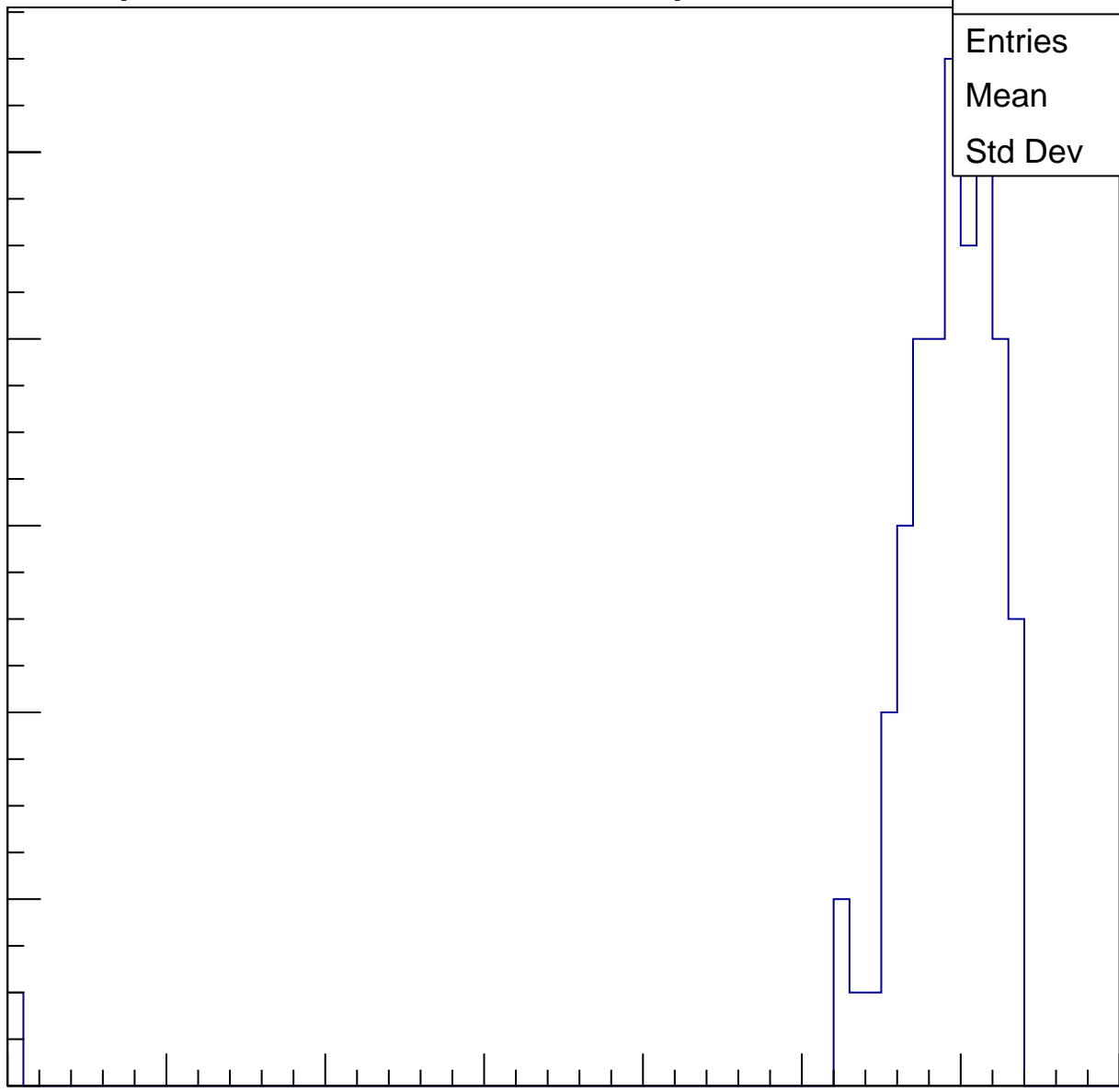
50

60

70

ampl

Entries	74
Mean	58.07
Std Dev	7.292



# B0L000S, U26-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

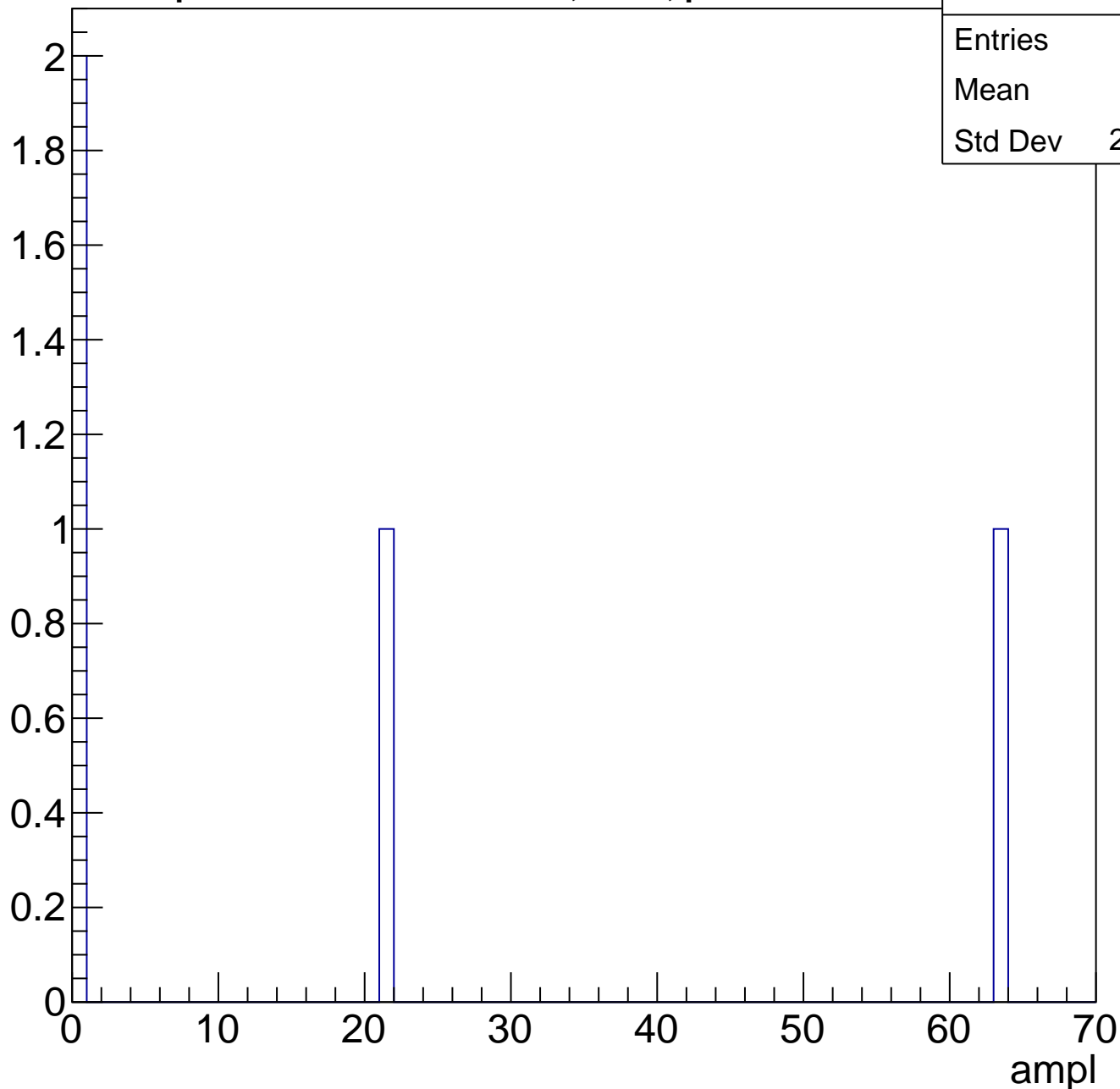




# B0L000S, U26-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	21
Std Dev	25.72

# B0L000S, U26-ch69, adc0

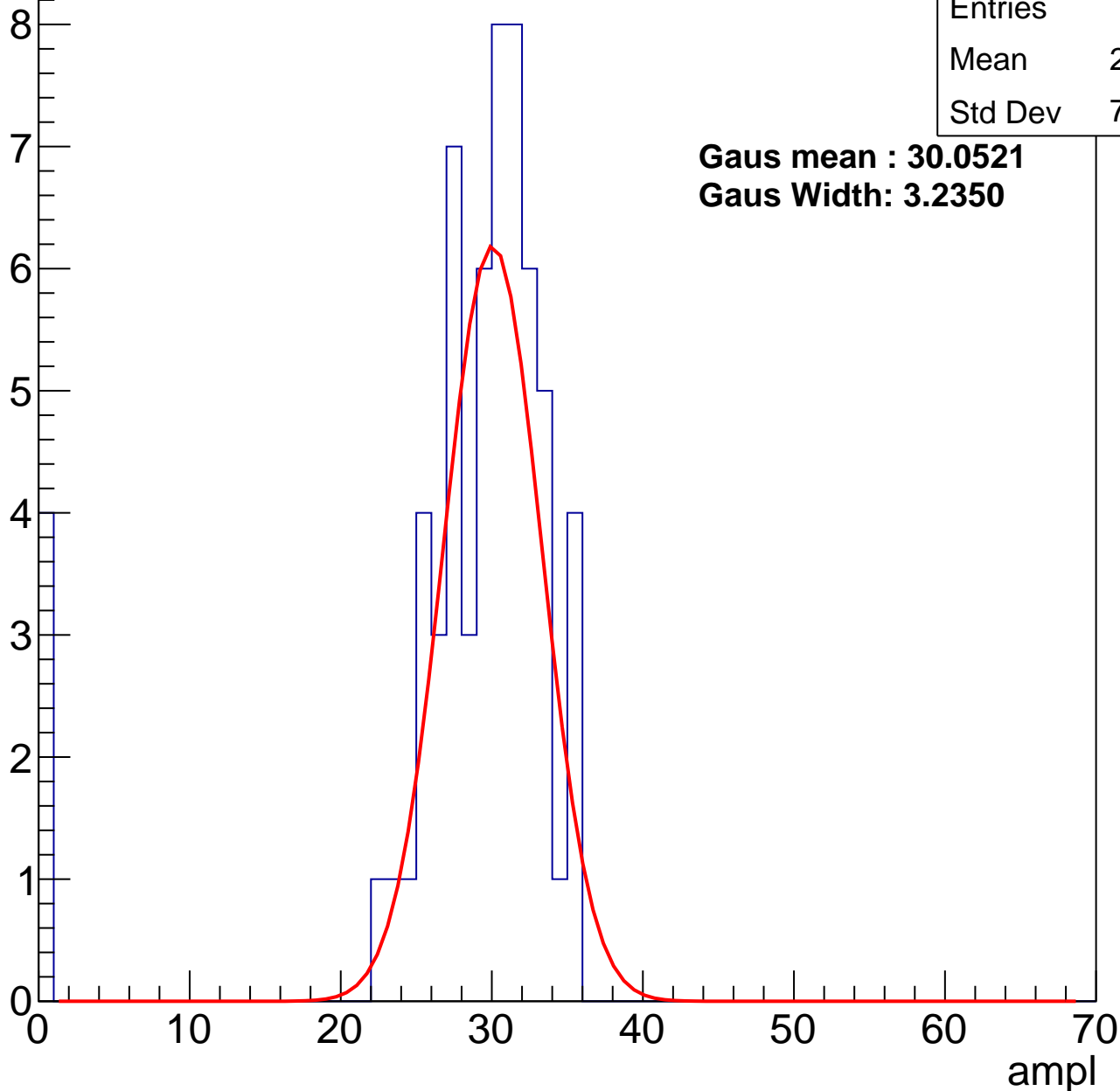
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	27.63
Std Dev	7.852

**Gaus mean : 30.0521**

**Gaus Width: 3.2350**



# B0L000S, U26-ch69, adc1

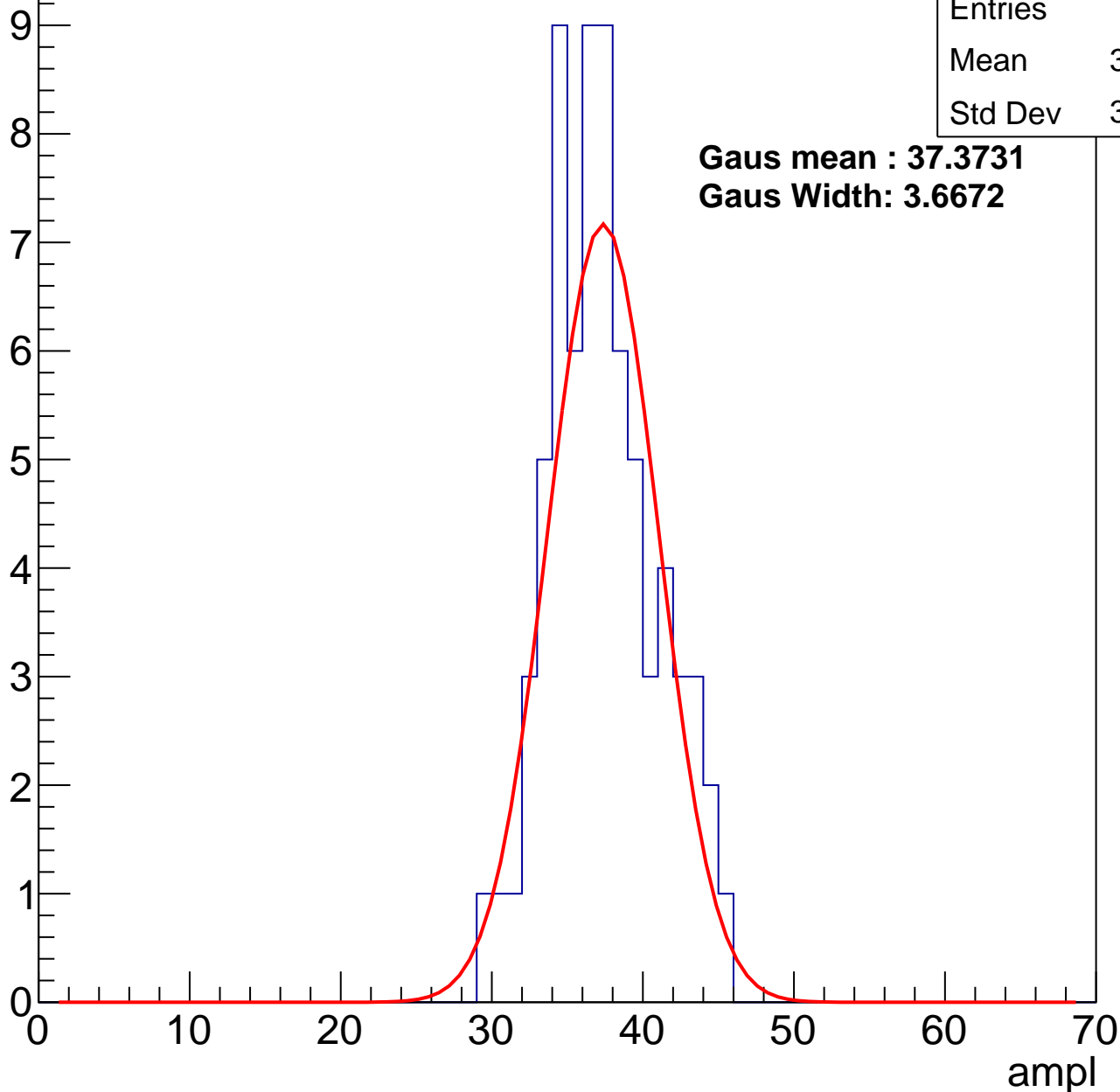
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.89
Std Dev	3.519

**Gaus mean : 37.3731**

**Gaus Width: 3.6672**



# B0L000S, U26-ch69, adc2

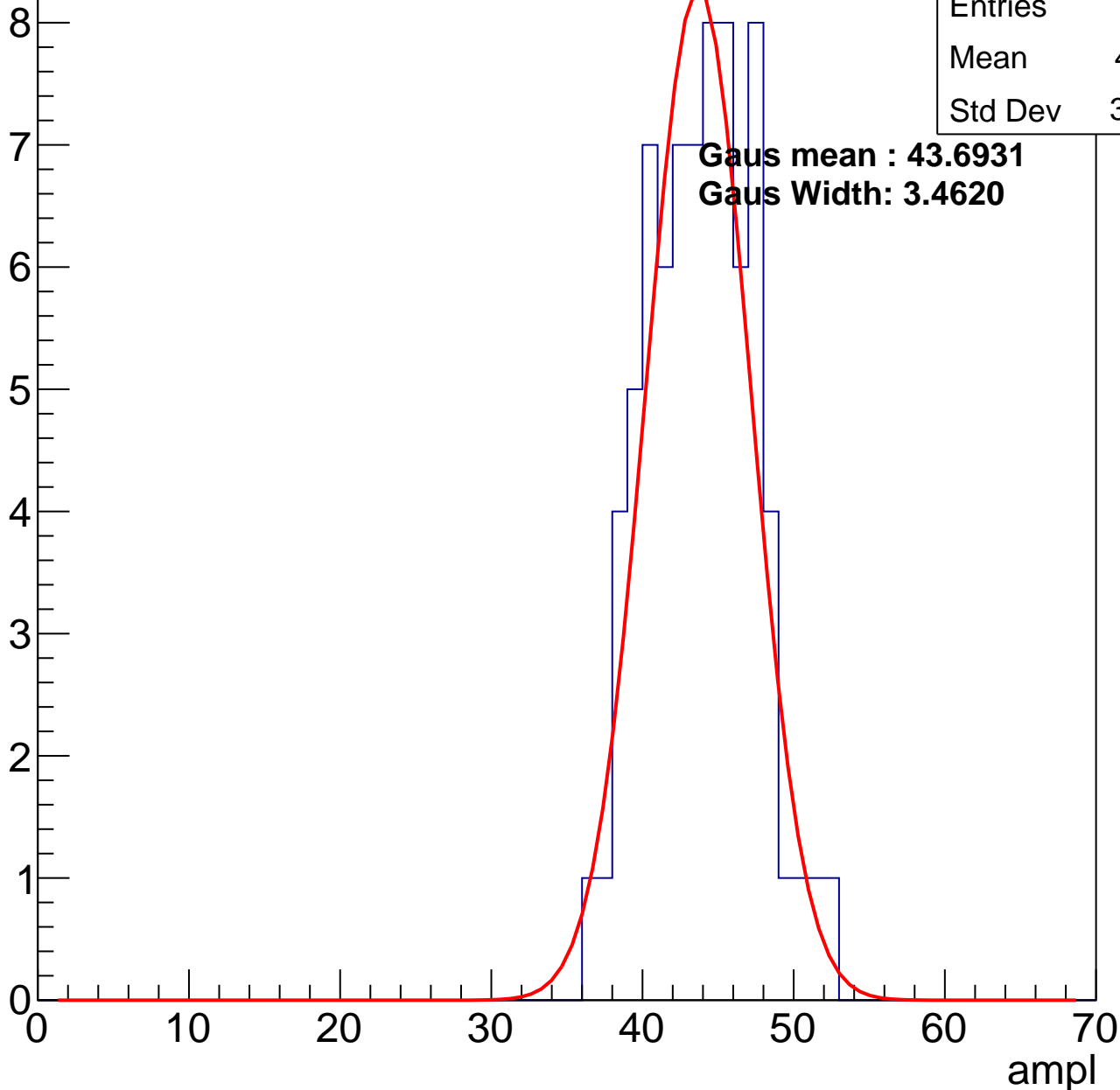
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	43.41
Std Dev	3.438

**Gaus mean : 43.6931**

**Gaus Width: 3.4620**

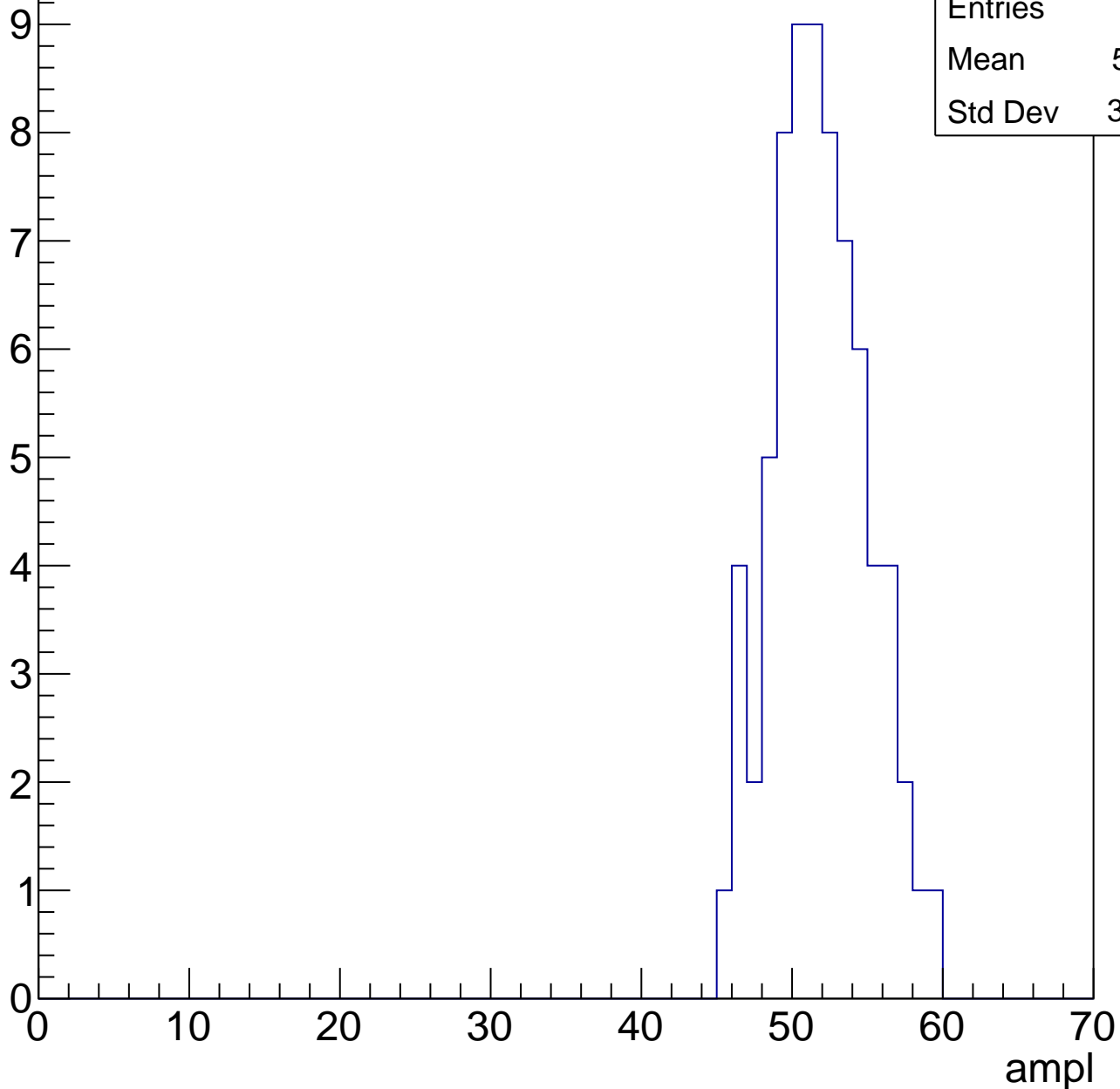


# B0L000S, U26-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

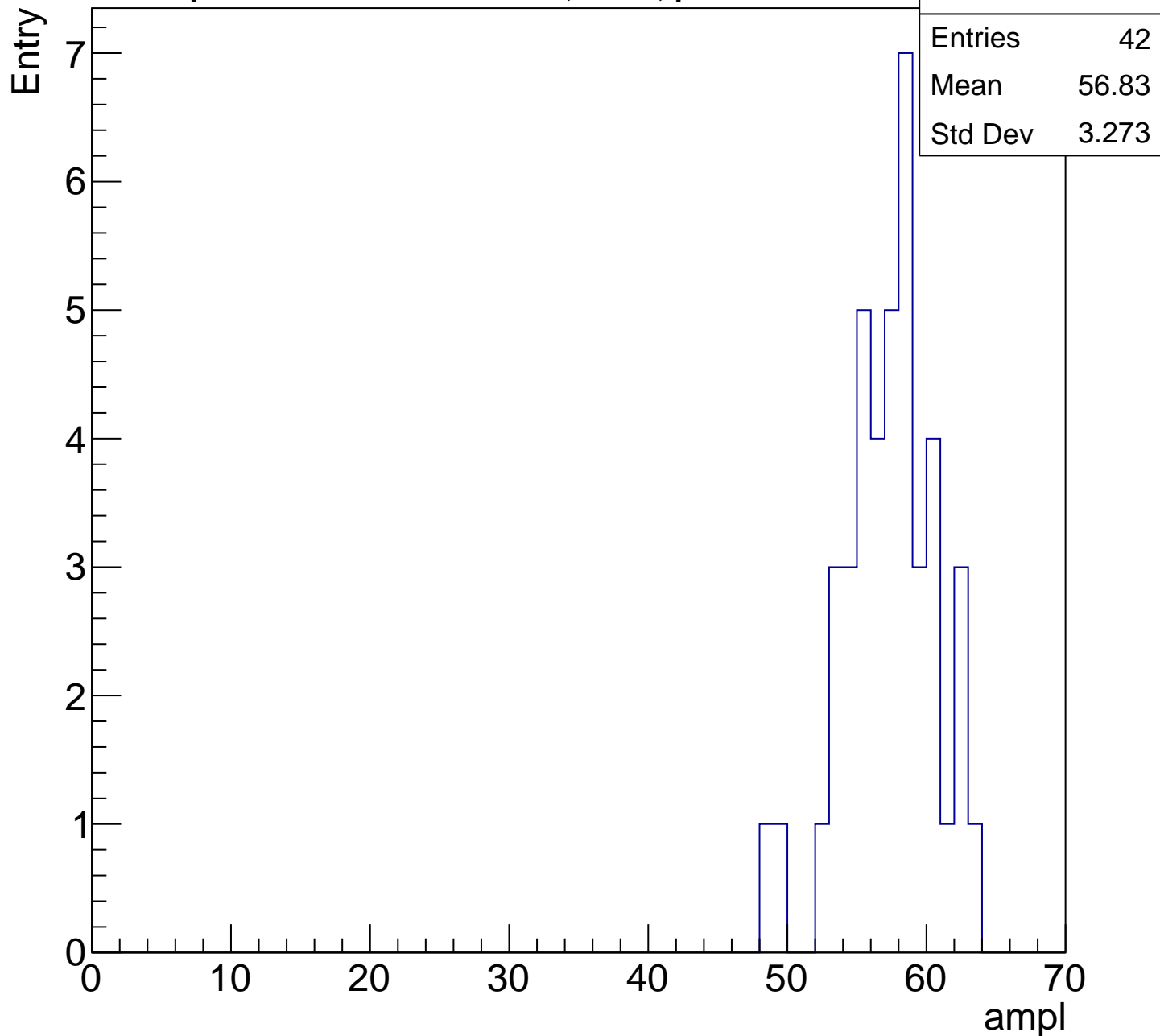
Entry

Entries	71
Mean	51.41
Std Dev	3.097



# B0L000S, U26-ch69, adc4

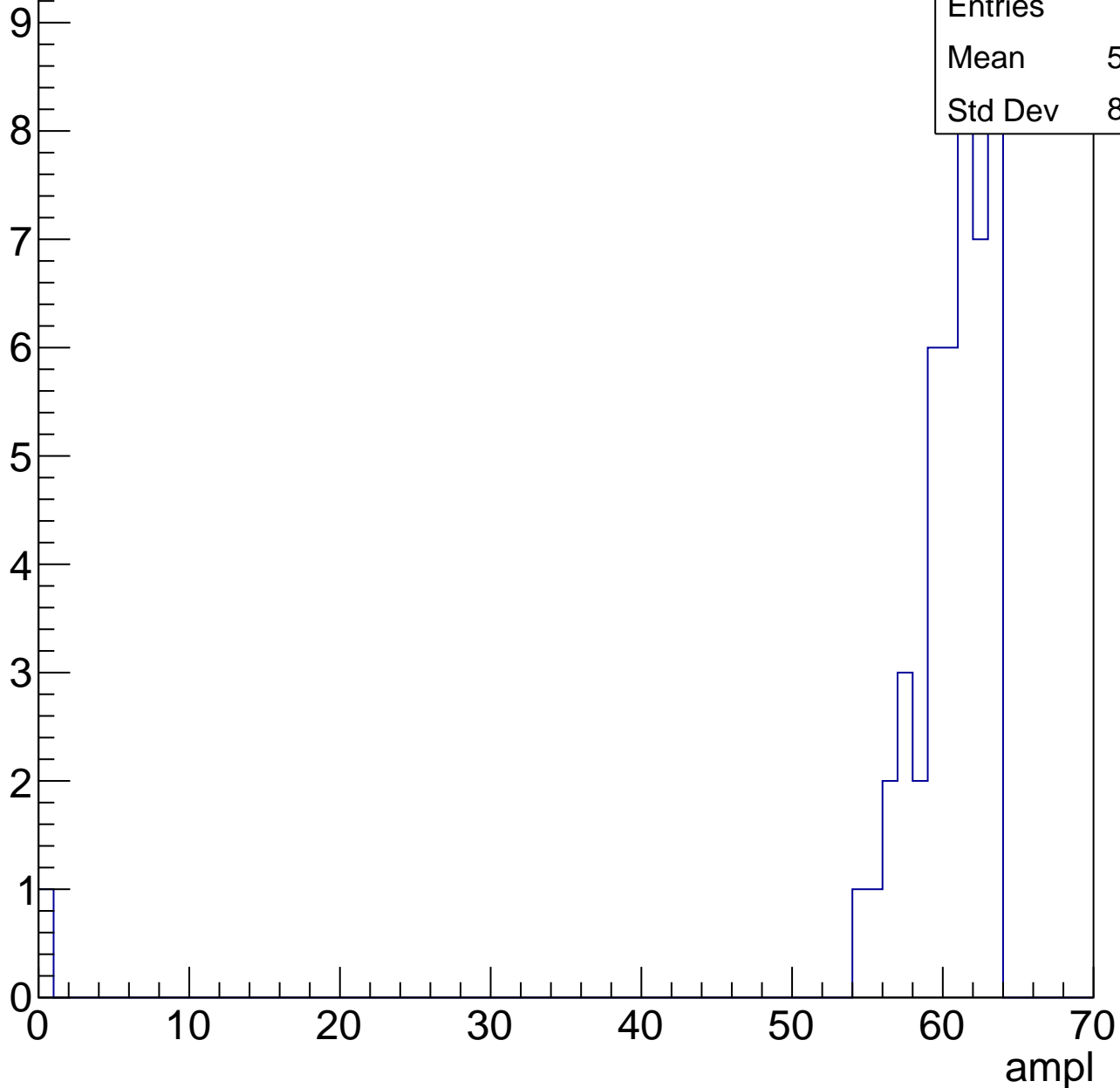
calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U26-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U26-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch70, adc0

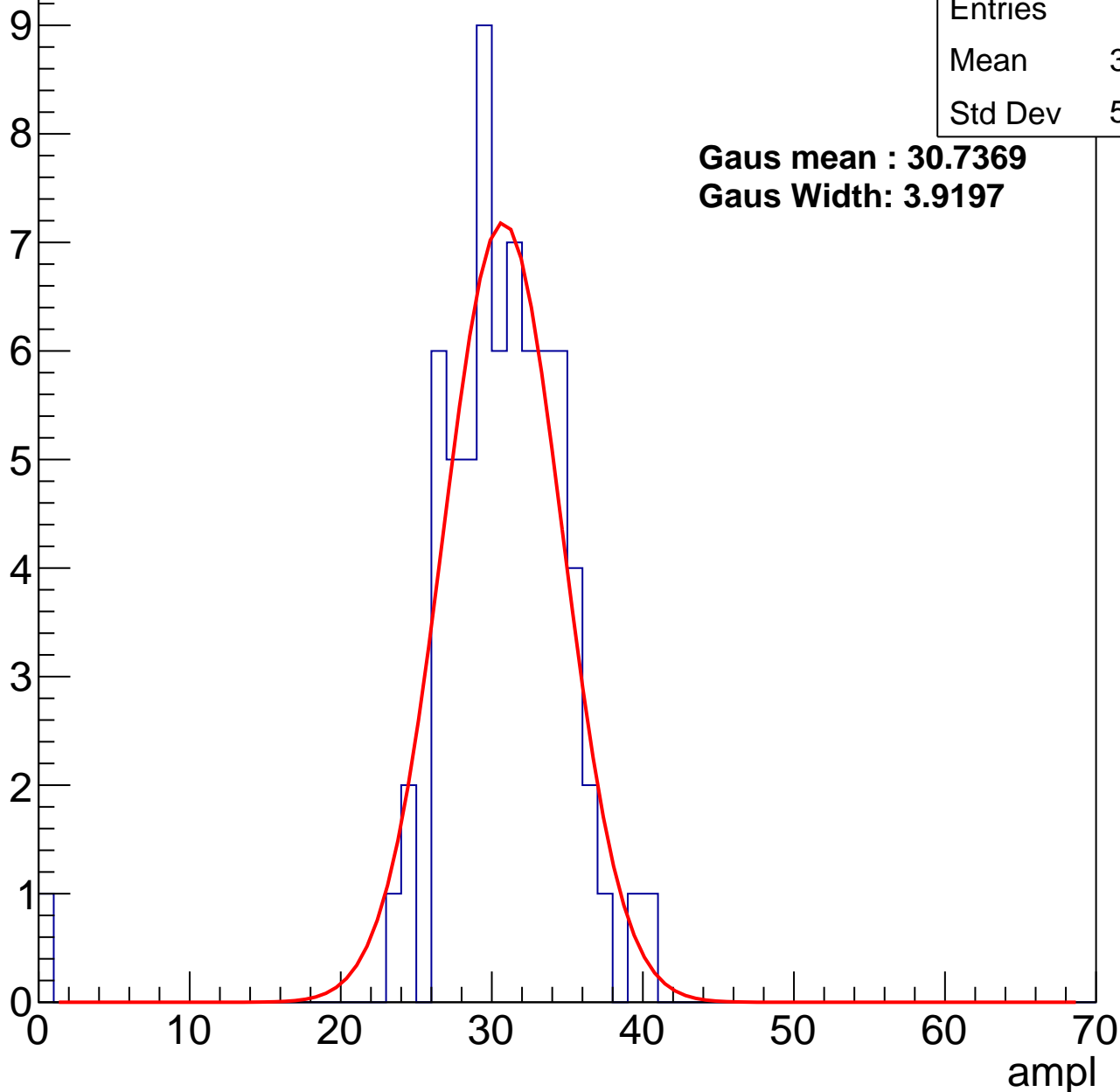
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.17
Std Dev	5.068

**Gaus mean : 30.7369**

**Gaus Width: 3.9197**



# B0L000S, U26-ch70, adc1

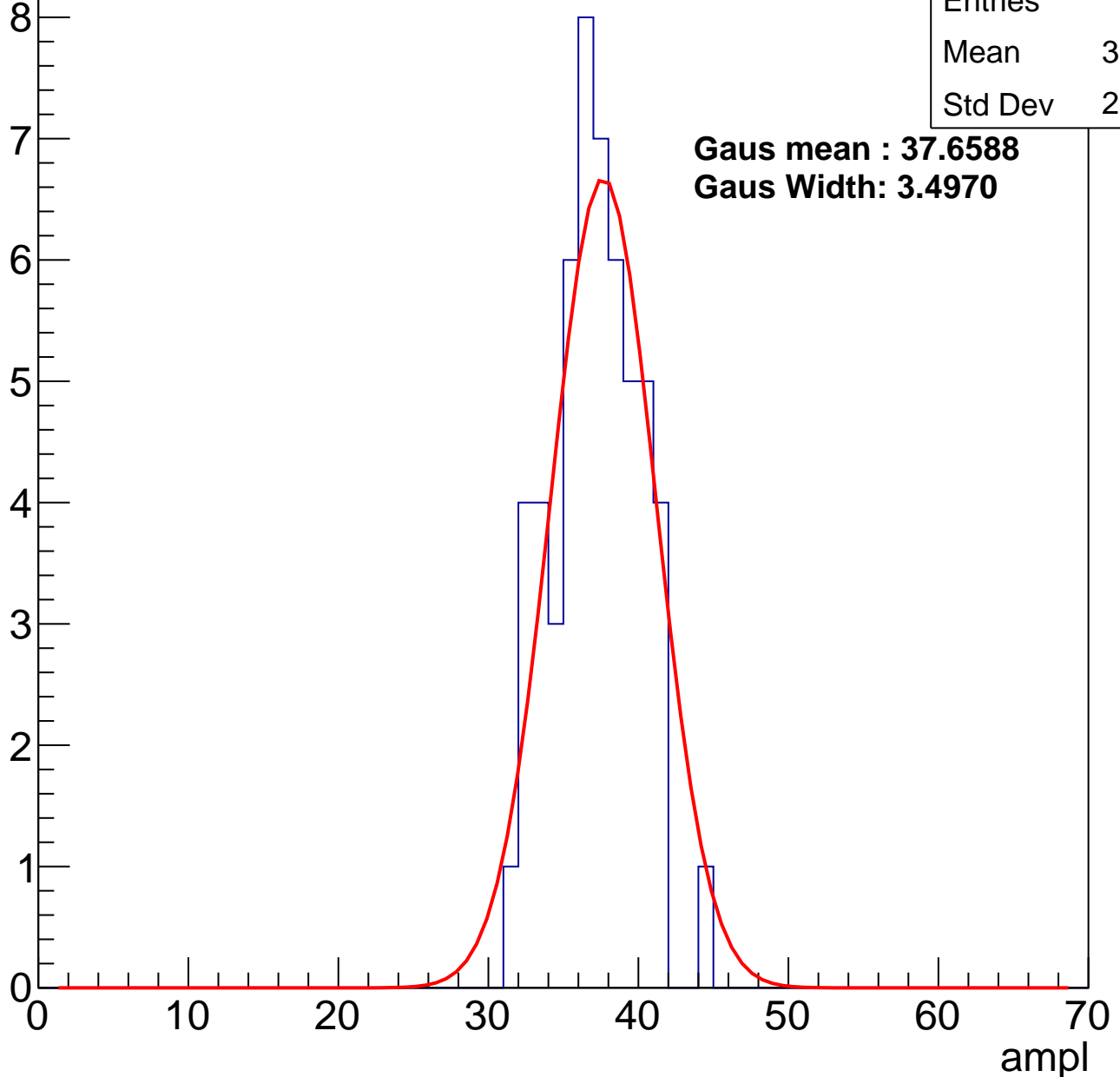
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	36.69
Std Dev	2.847

**Gaus mean : 37.6588**

**Gaus Width: 3.4970**

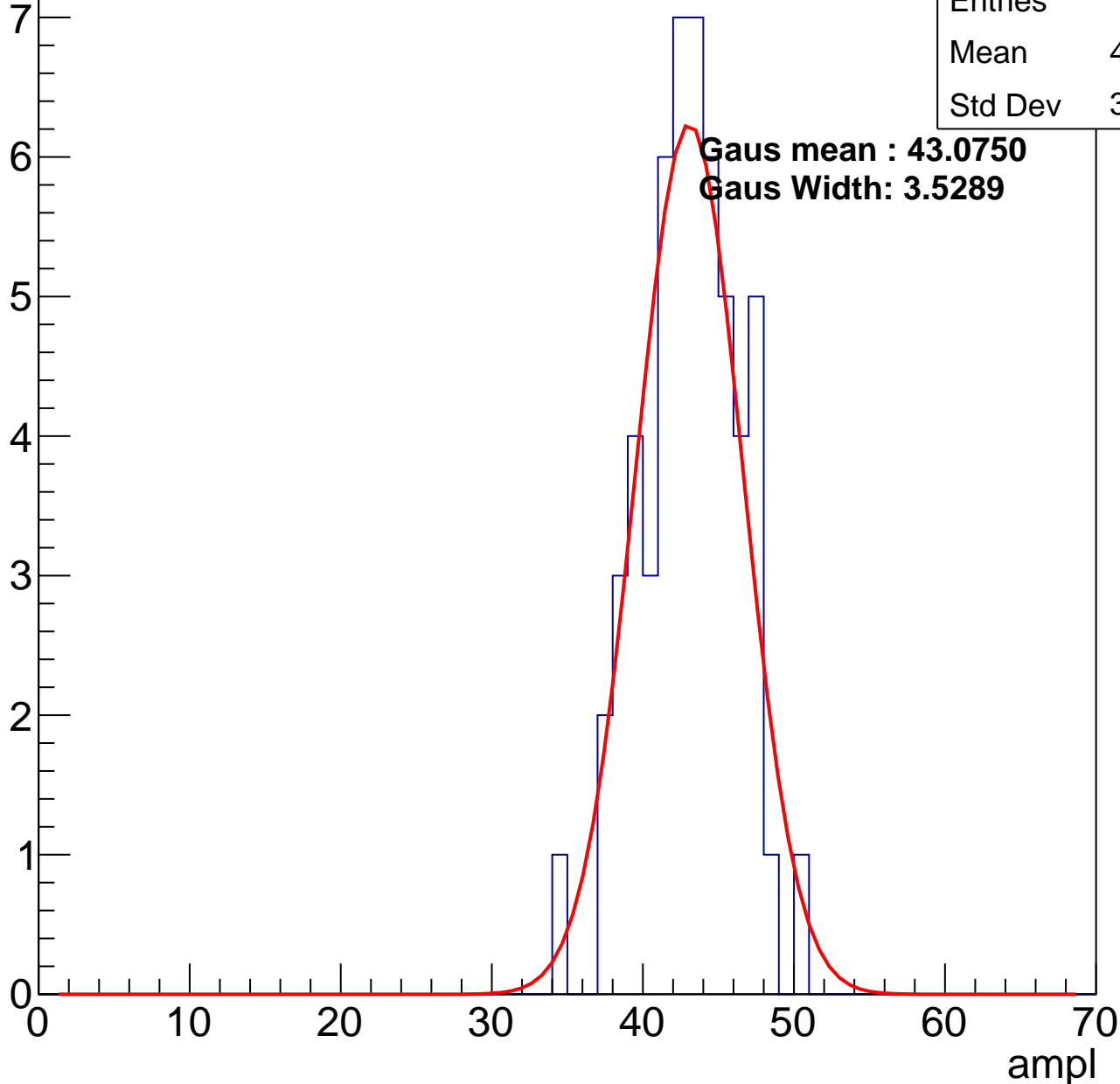


# B0L000S, U26-ch70, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	42.64
Std Dev	3.187

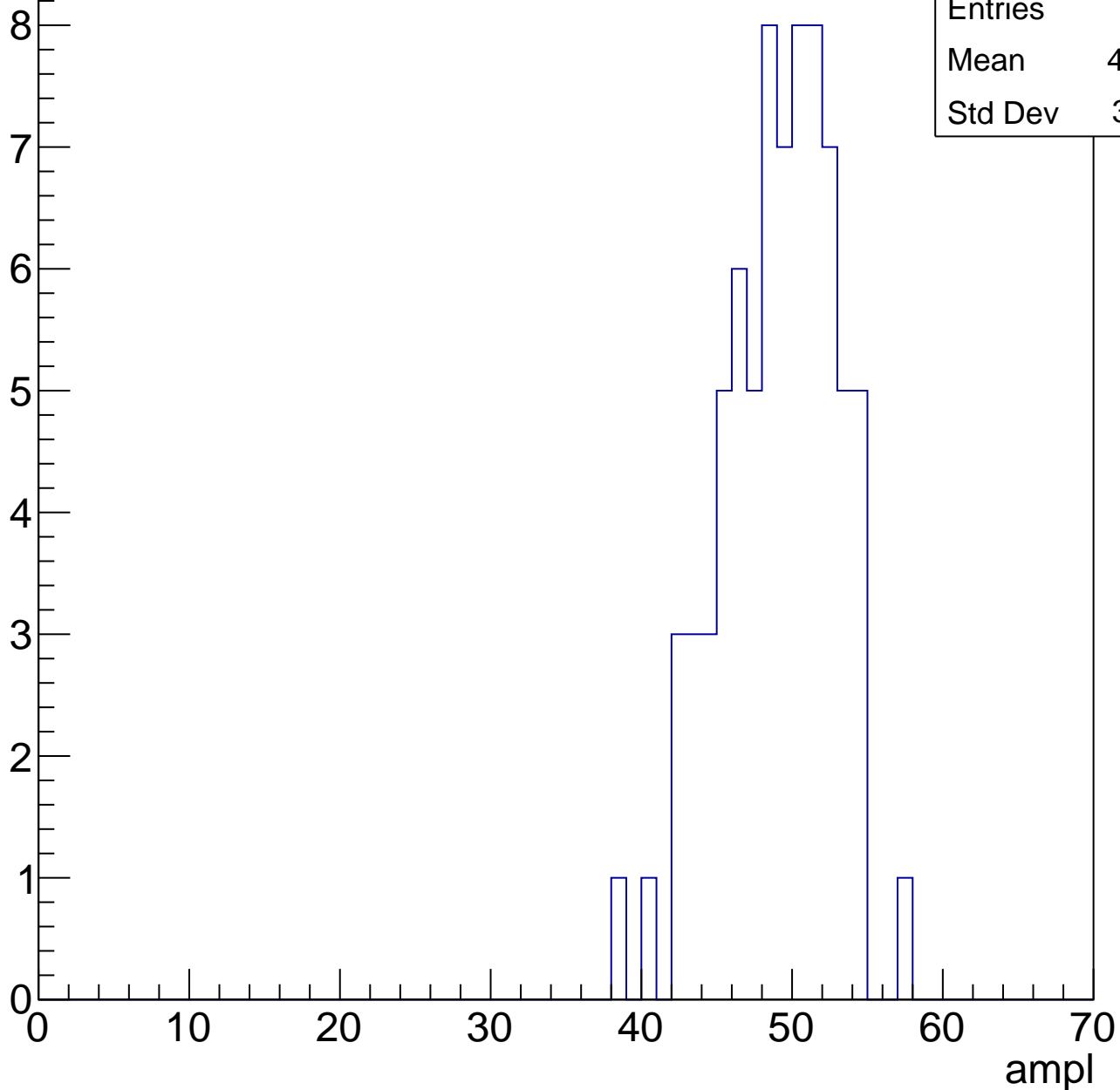


# B0L000S, U26-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	48.58
Std Dev	3.721

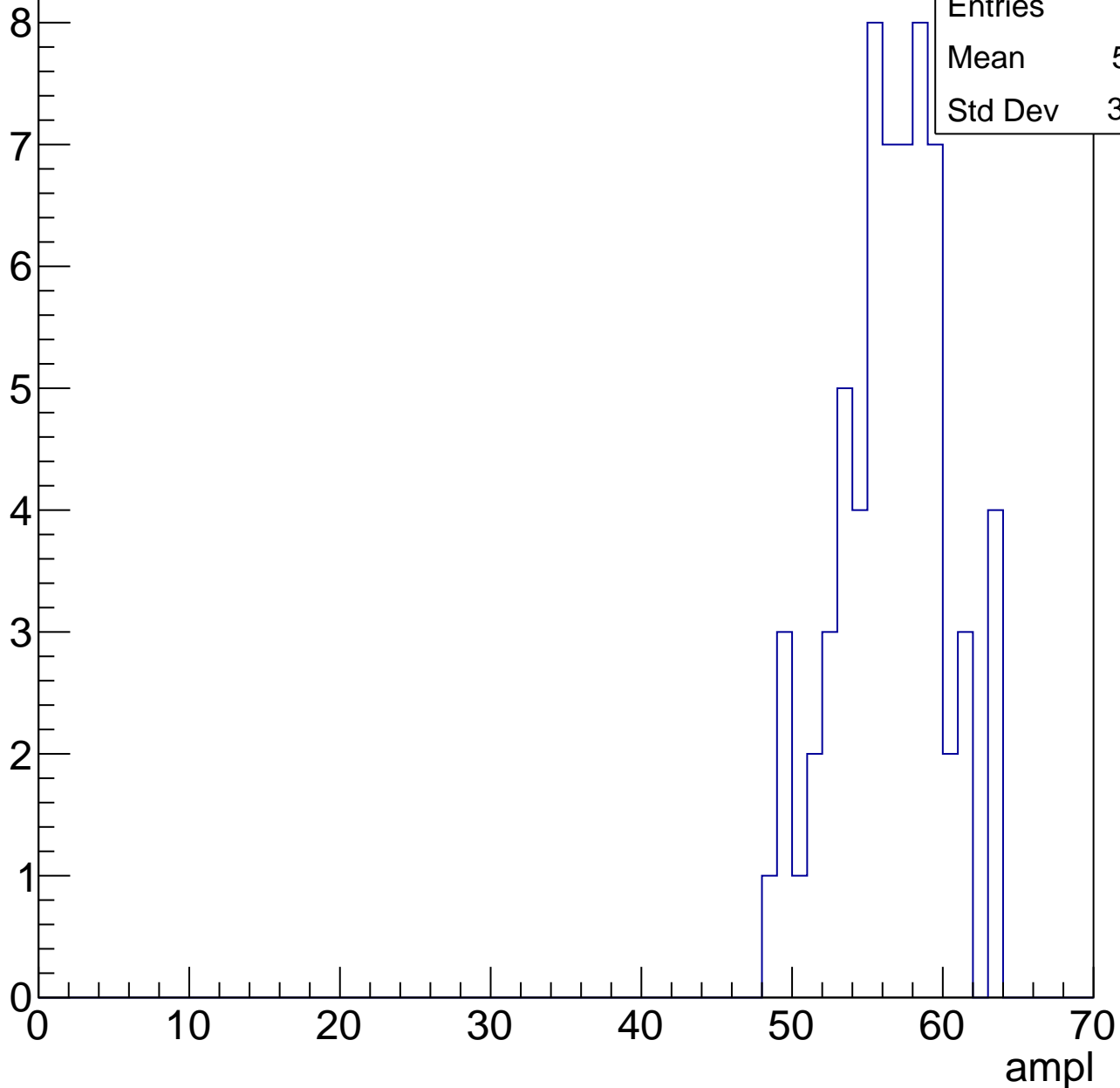


# B0L000S, U26-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	56.11
Std Dev	3.548

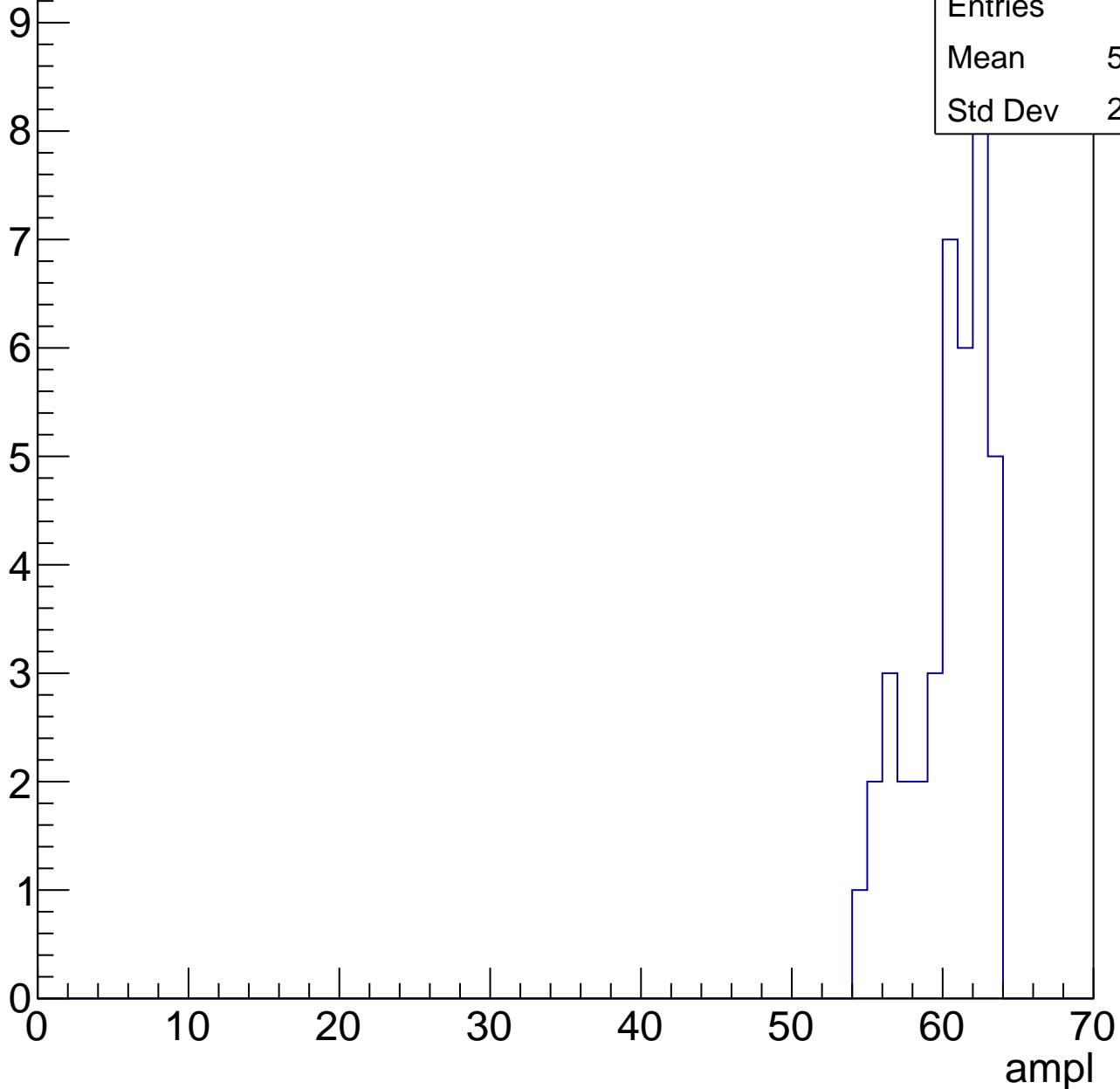


# B0L000S, U26-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

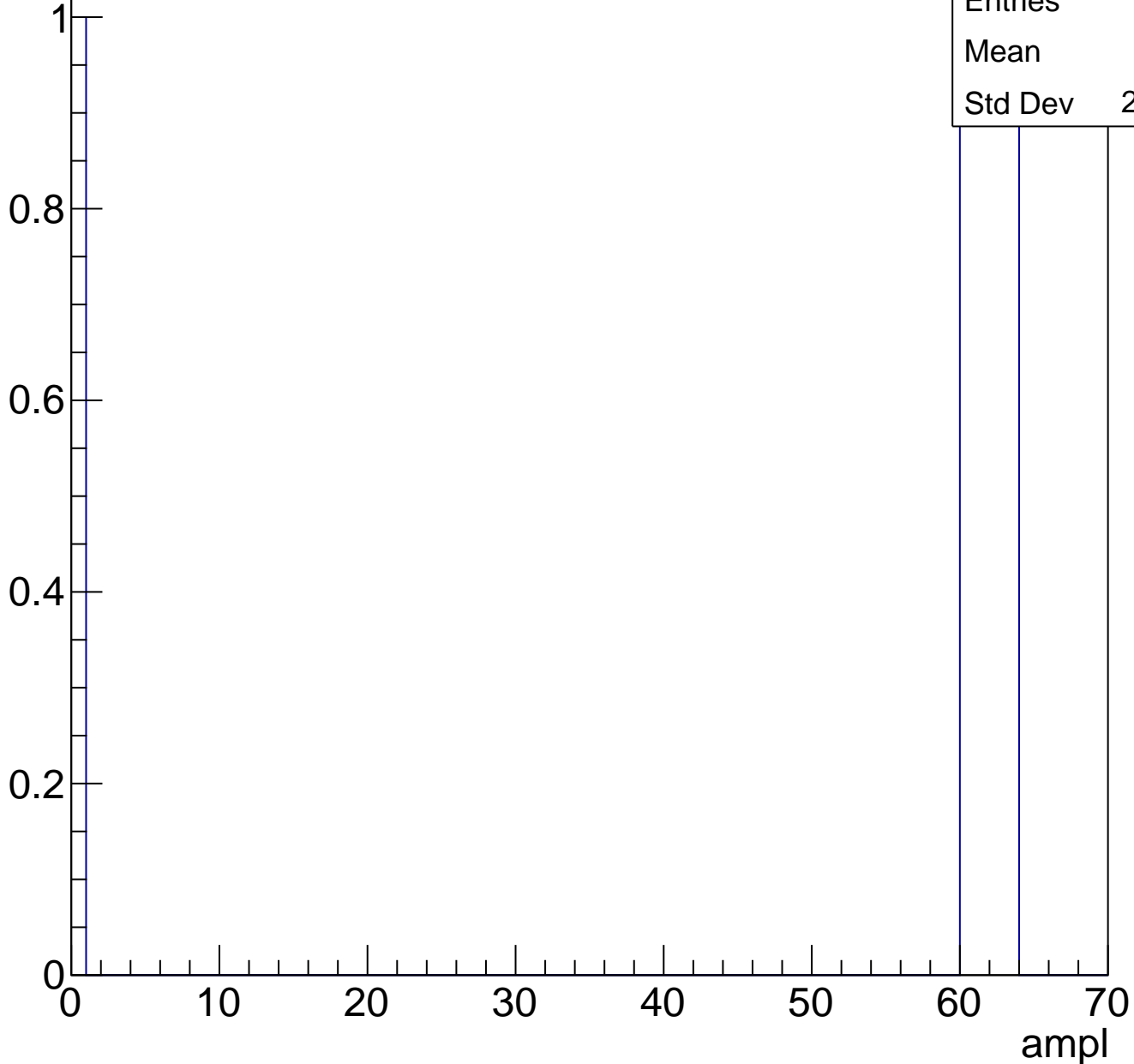
Entries	40
Mean	59.95
Std Dev	2.499



# B0L000S, U26-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch71, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	30.55
Std Dev	3.061

**Gaus mean : 30.8224**

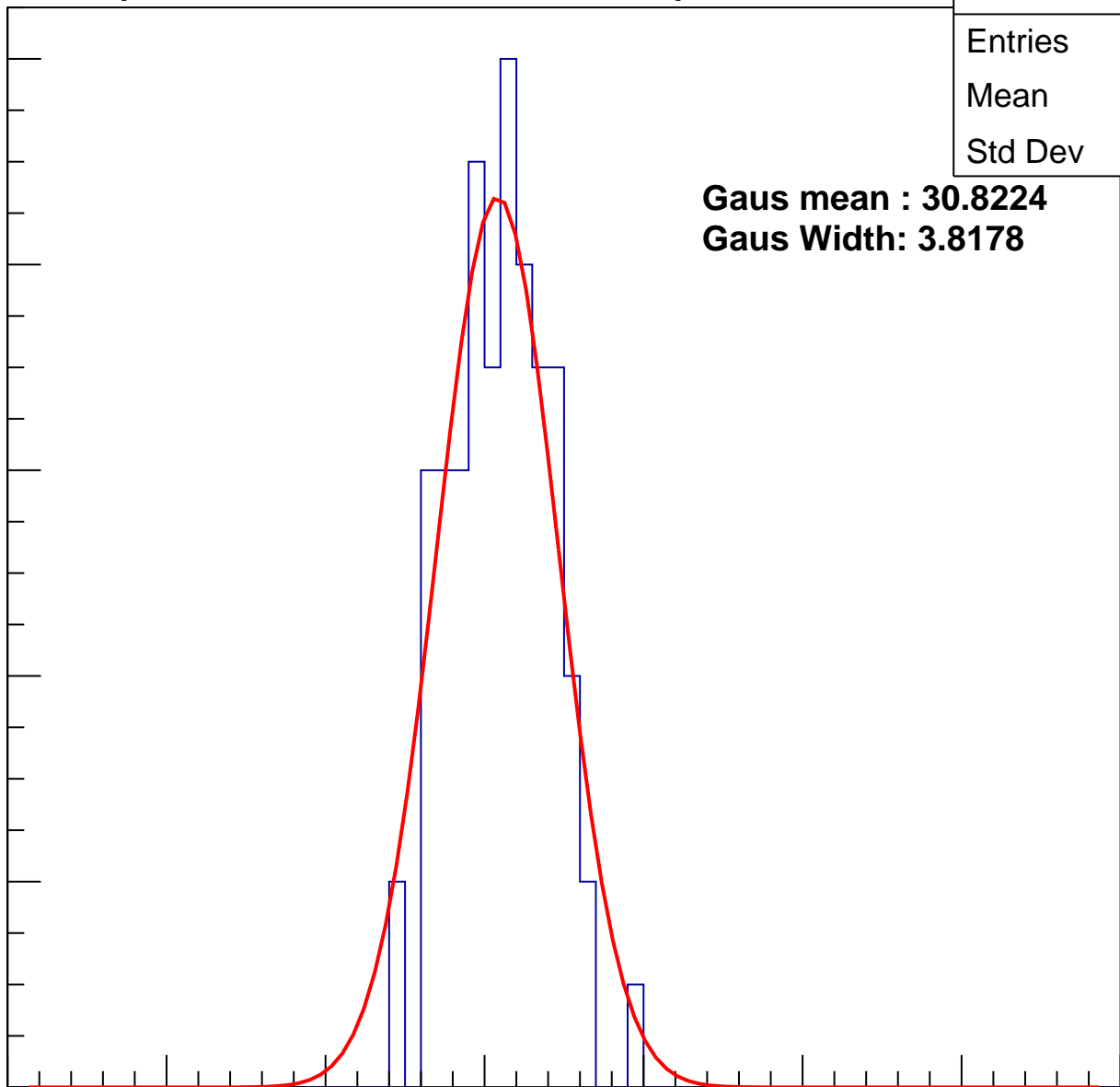
**Gaus Width: 3.8178**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch71, adc1

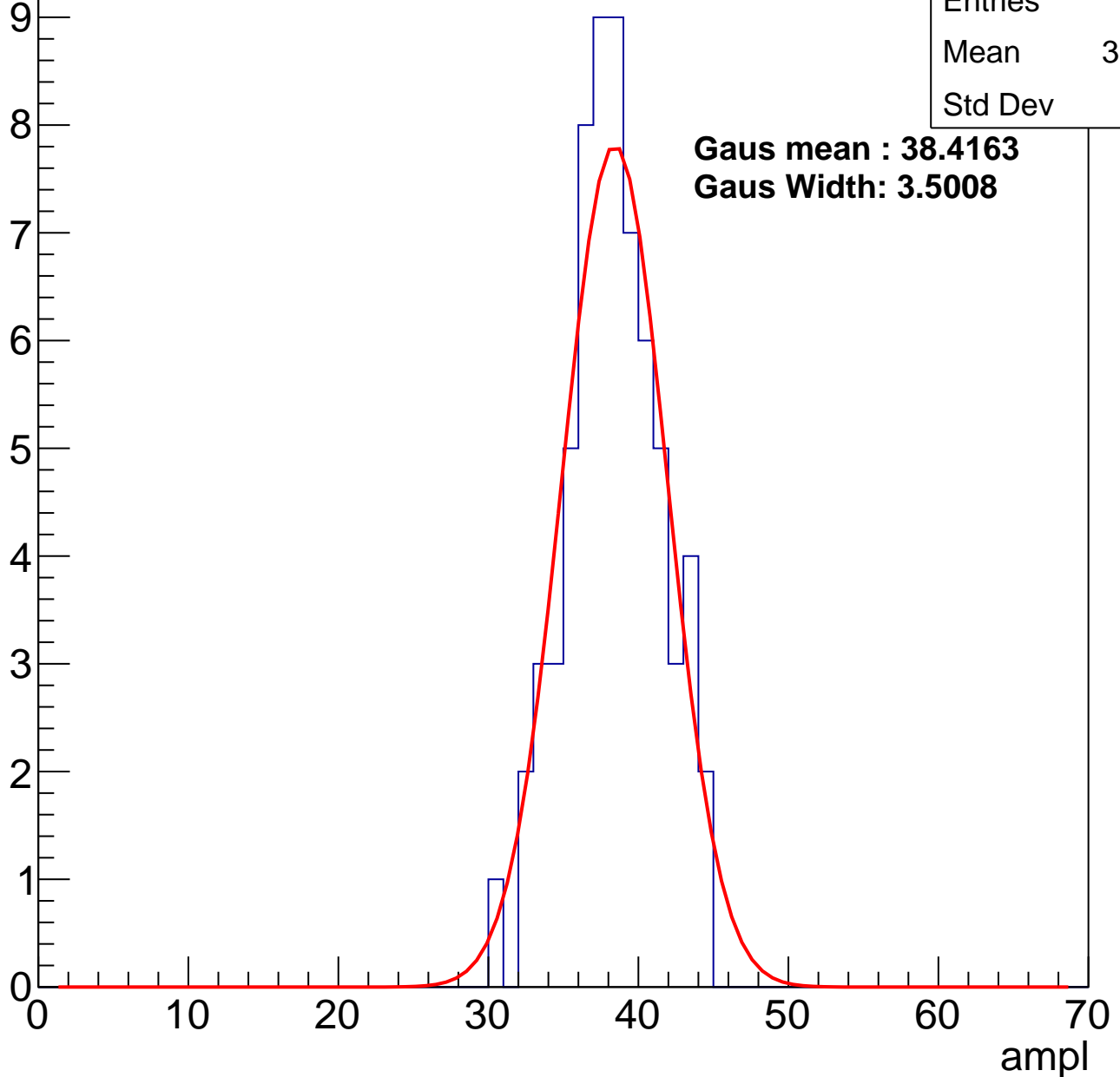
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.87
Std Dev	3.09

**Gaus mean : 38.4163**

**Gaus Width: 3.5008**



# B0L000S, U26-ch71, adc2

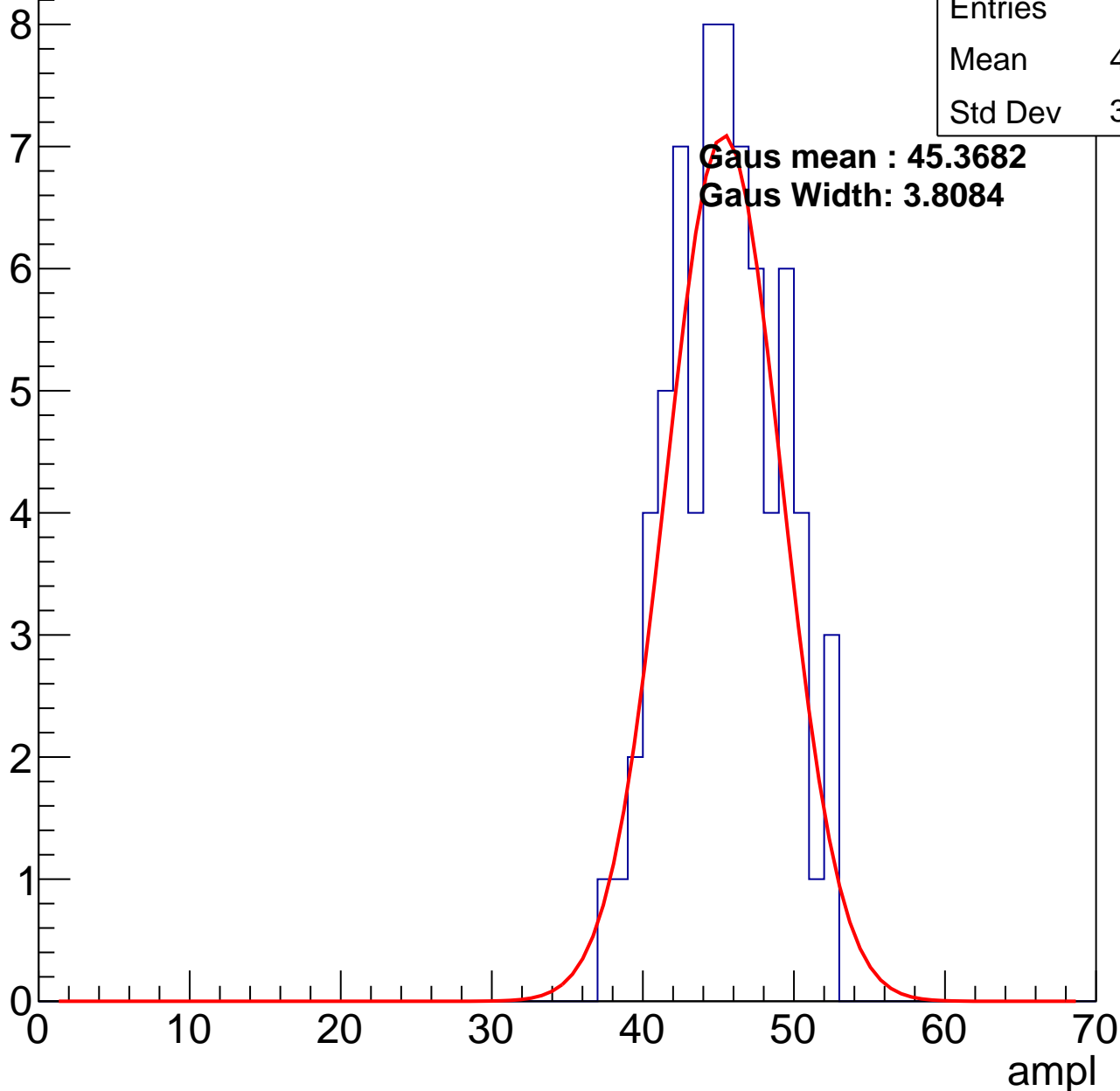
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	44.97
Std Dev	3.556

**Gaus mean : 45.3682**

**Gaus Width: 3.8084**

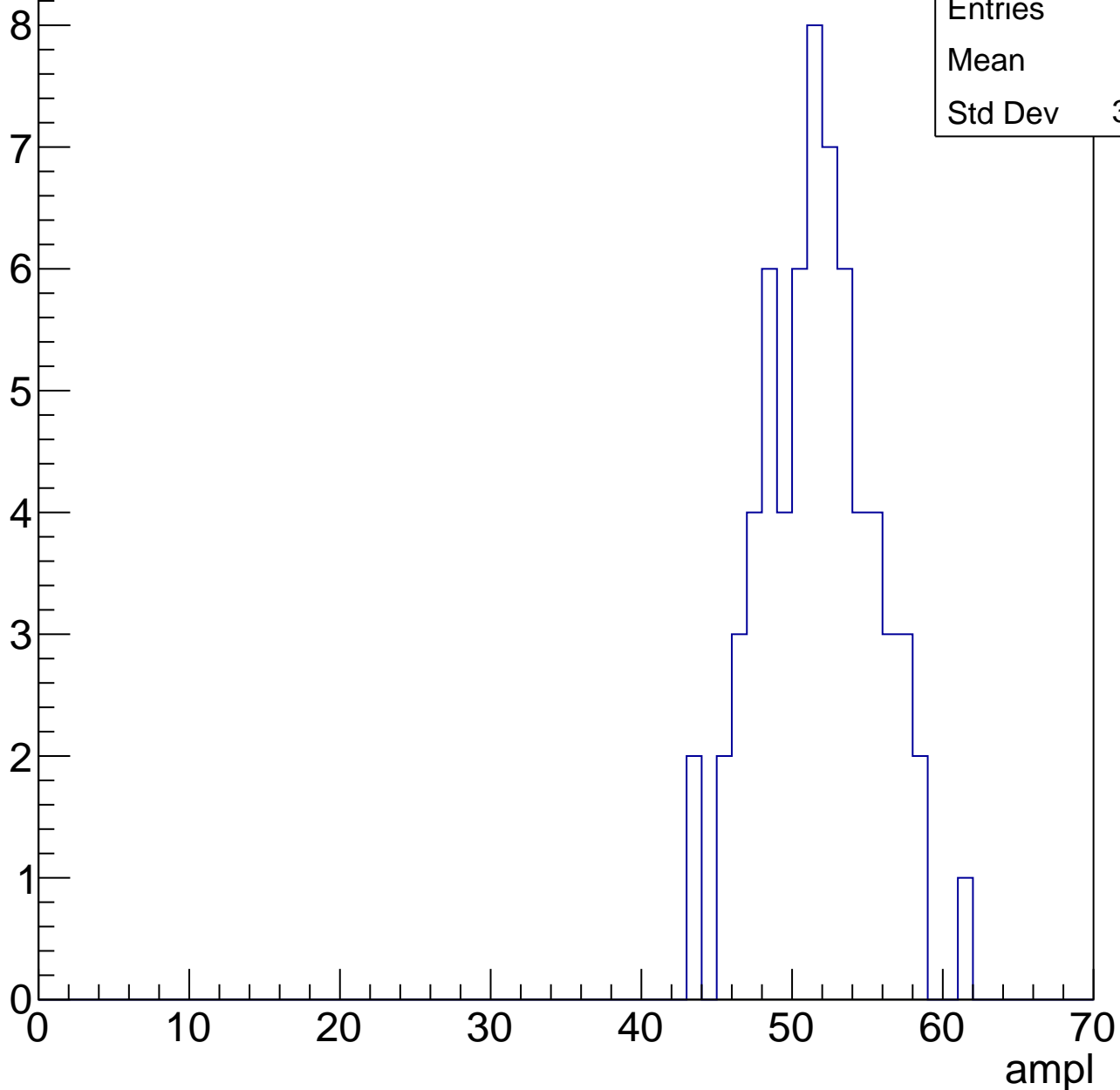


# B0L000S, U26-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	51.2
Std Dev	3.771



# B0L000S, U26-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 45

Mean 56.49

Std Dev 2.428

ampl

0

10

20

30

40

50

60

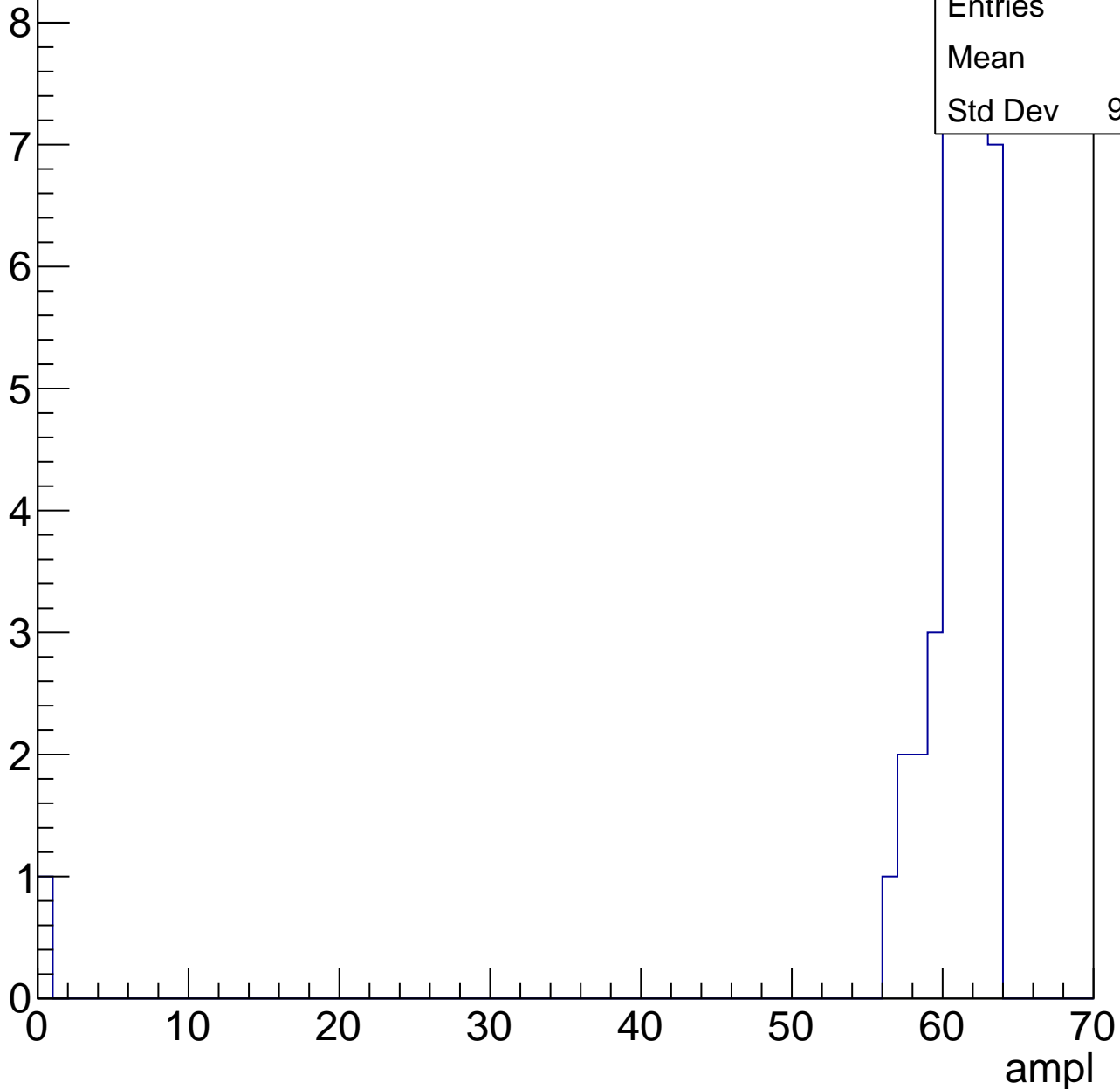
70

# B0L000S, U26-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	59.2
Std Dev	9.647



# B0L000S, U26-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch72, adc0

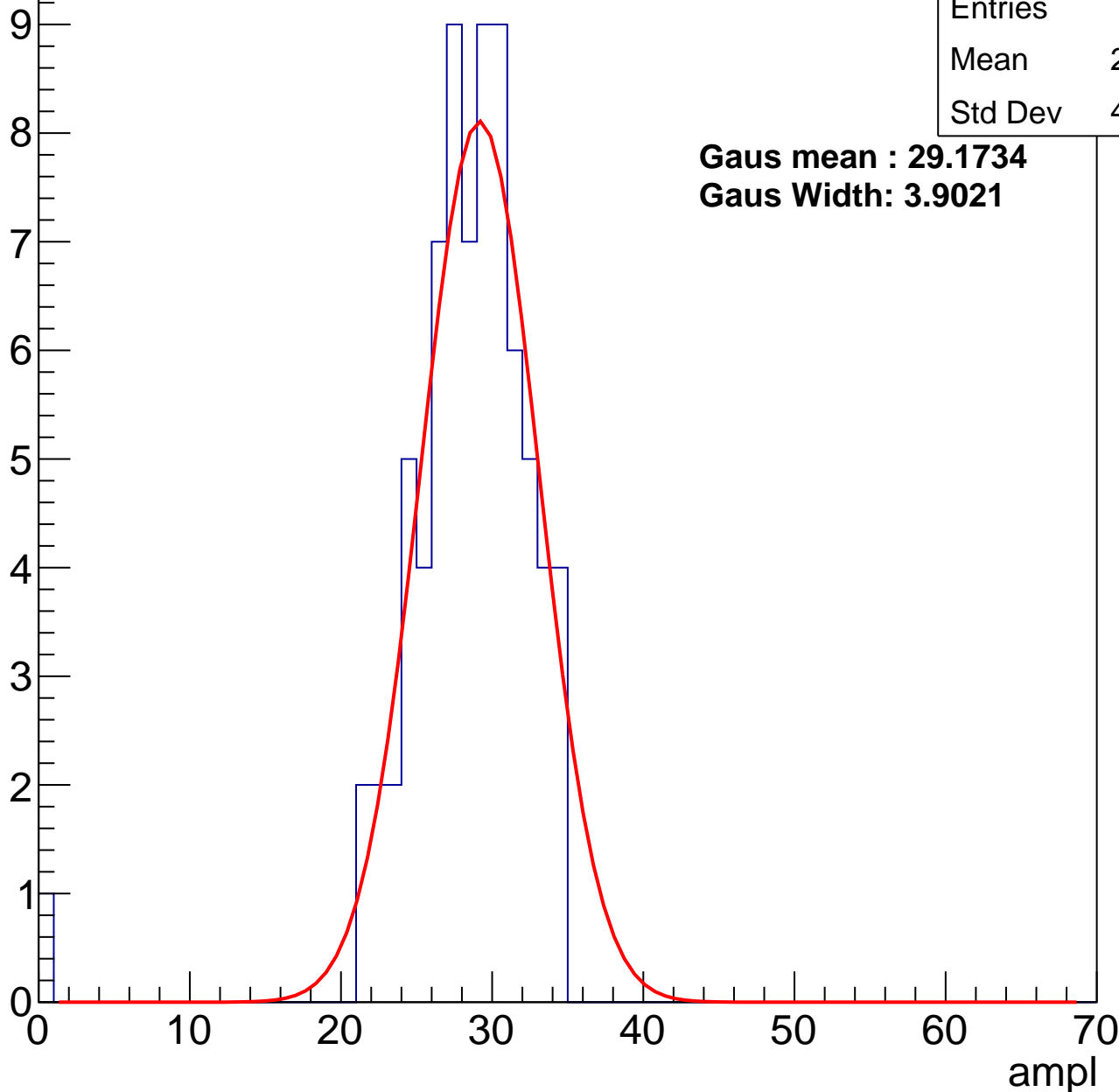
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	27.87
Std Dev	4.558

**Gaus mean : 29.1734**

**Gaus Width: 3.9021**



# B0L000S, U26-ch72, adc1

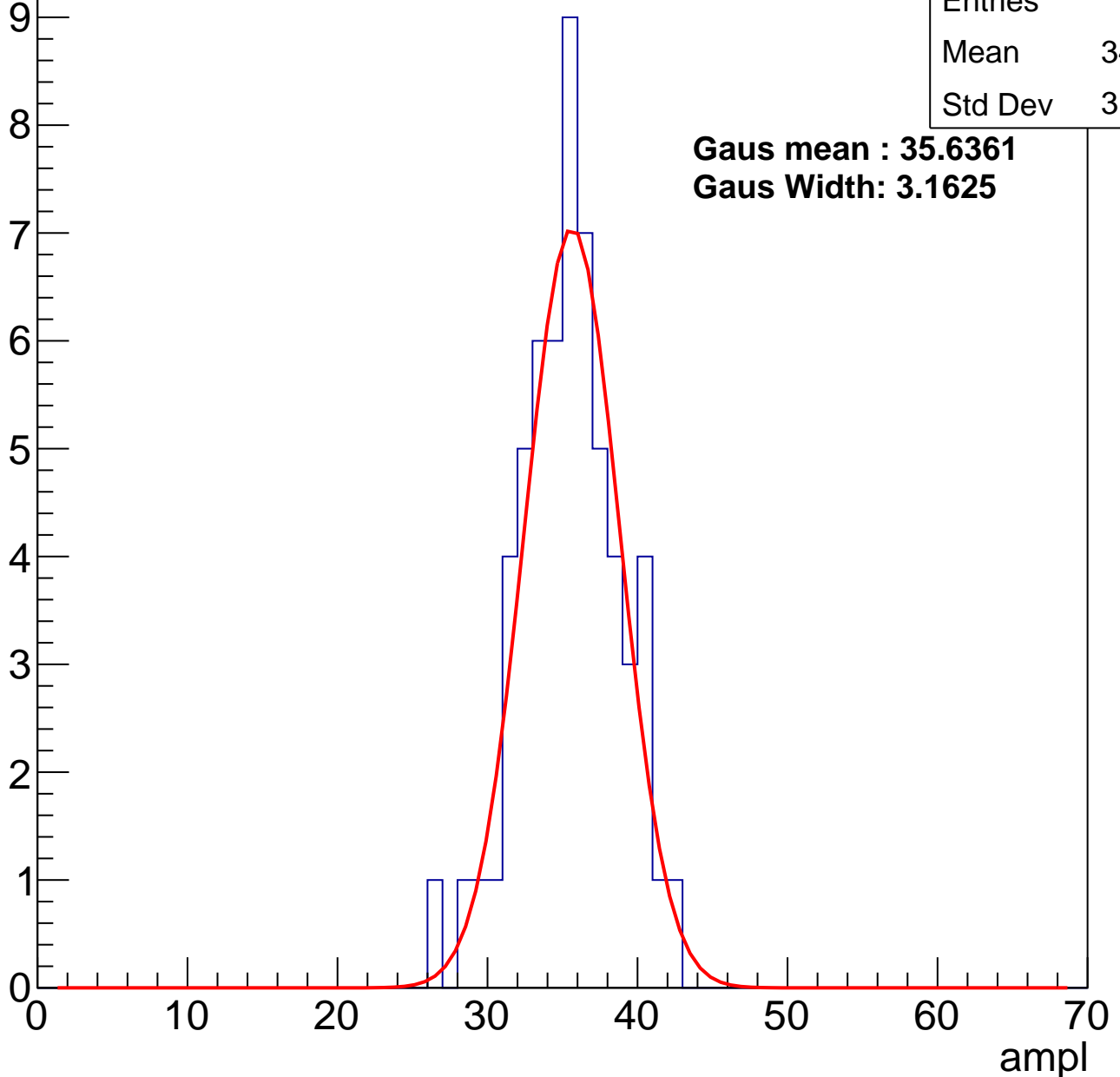
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	34.97
Std Dev	3.257

**Gaus mean : 35.6361**

**Gaus Width: 3.1625**



# B0L000S, U26-ch72, adc2

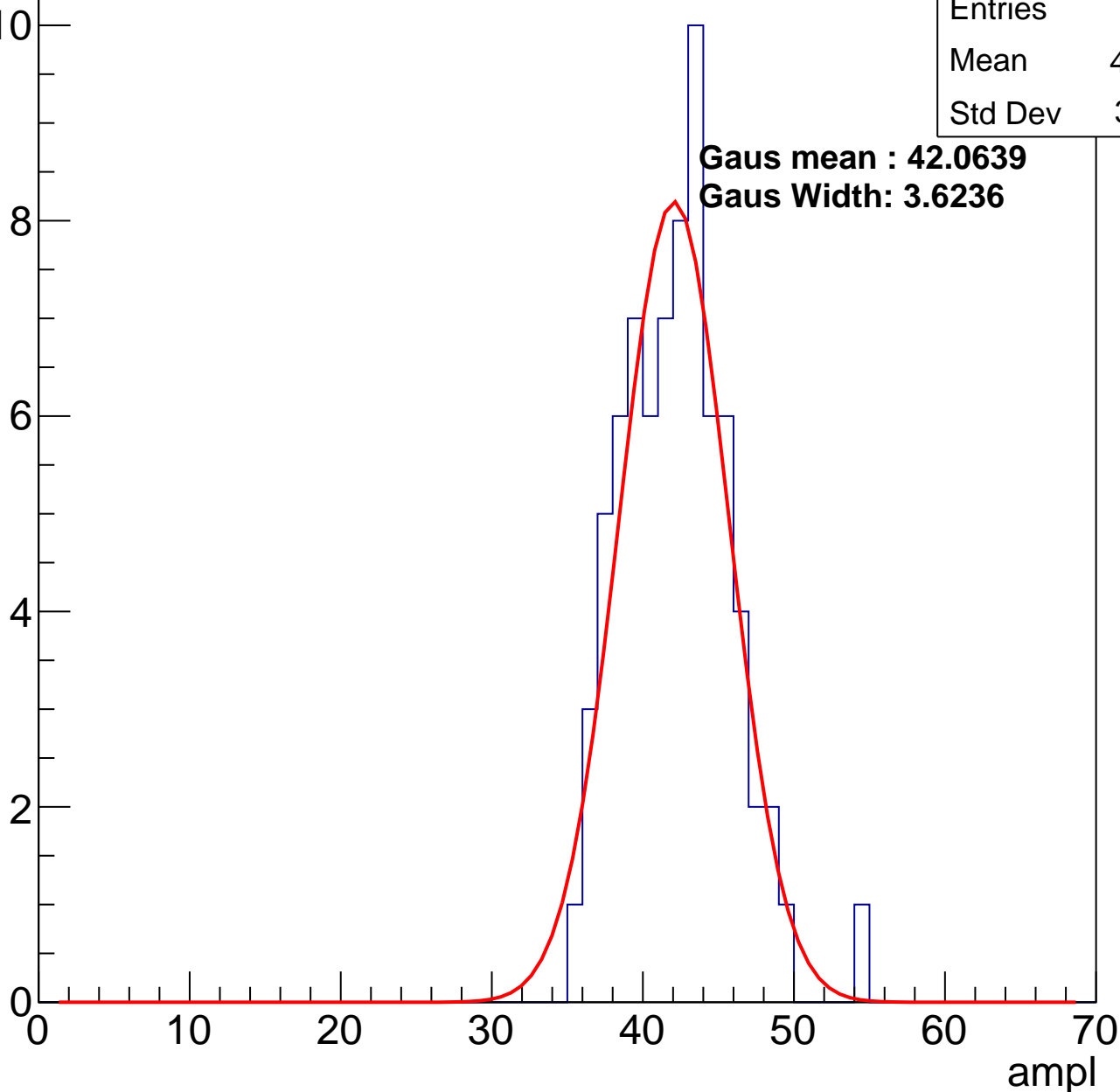
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	41.77
Std Dev	3.531

**Gaus mean : 42.0639**

**Gaus Width: 3.6236**

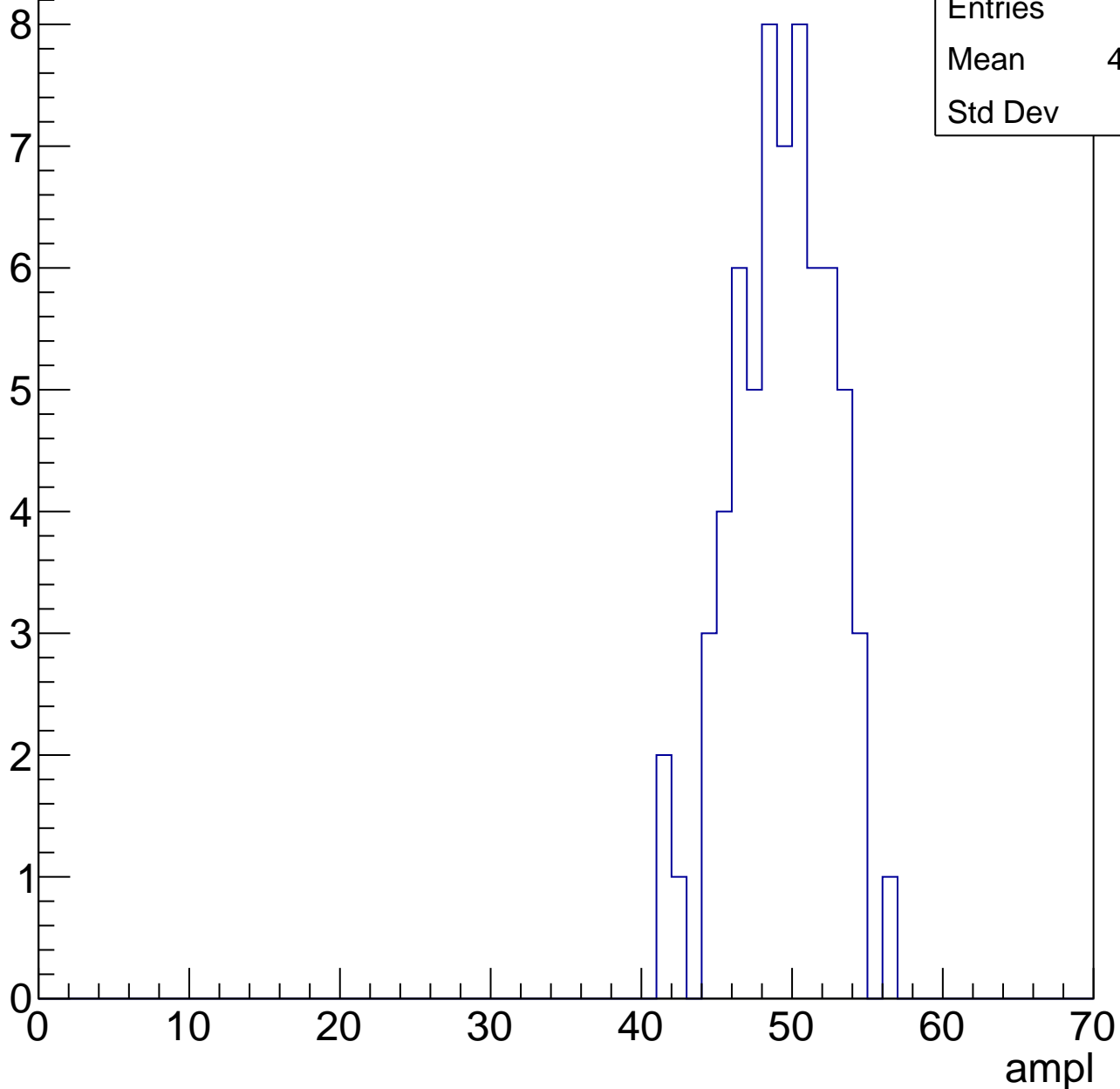


# B0L000S, U26-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.85
Std Dev	3.25

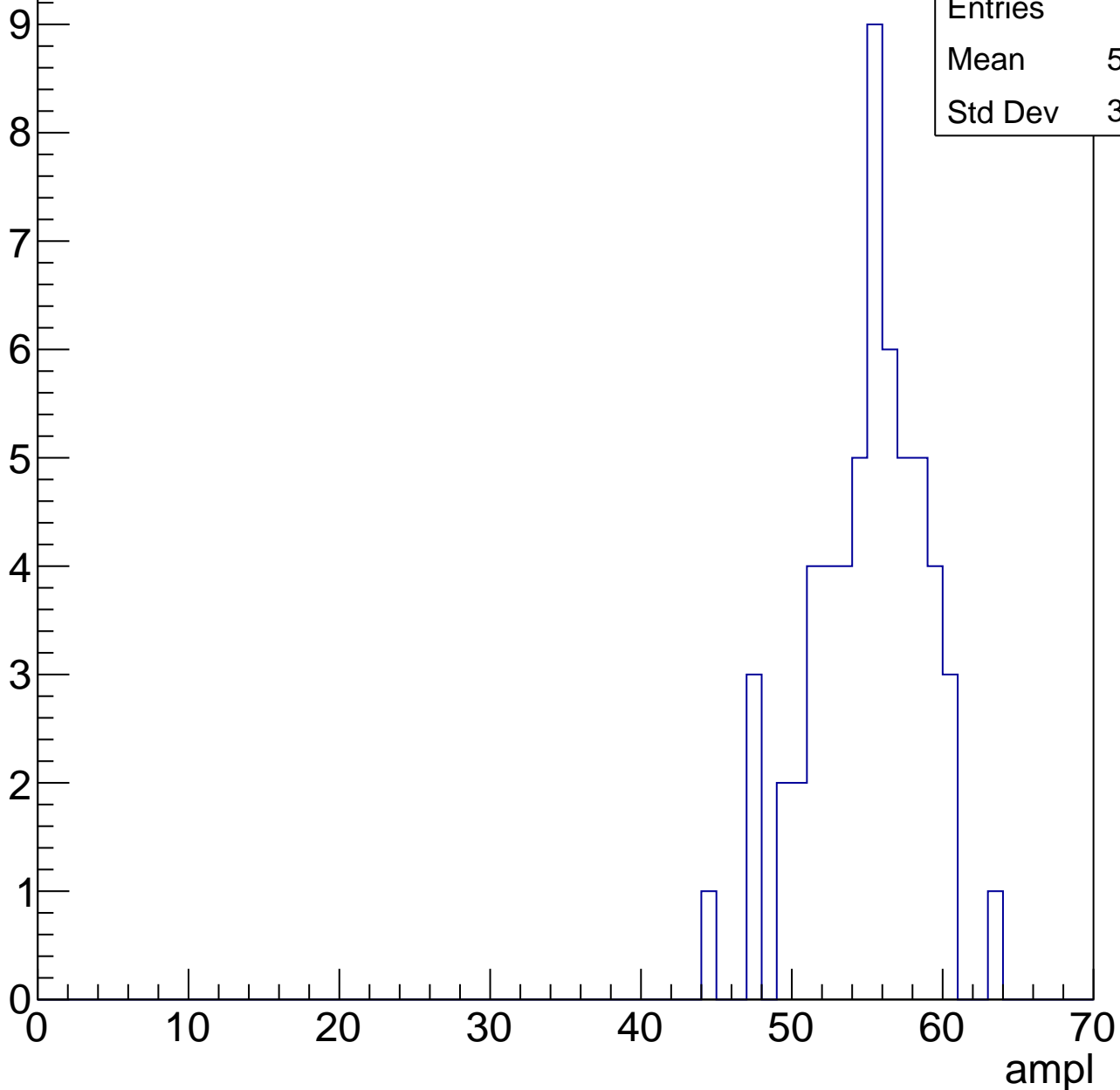


# B0L000S, U26-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	54.52
Std Dev	3.752

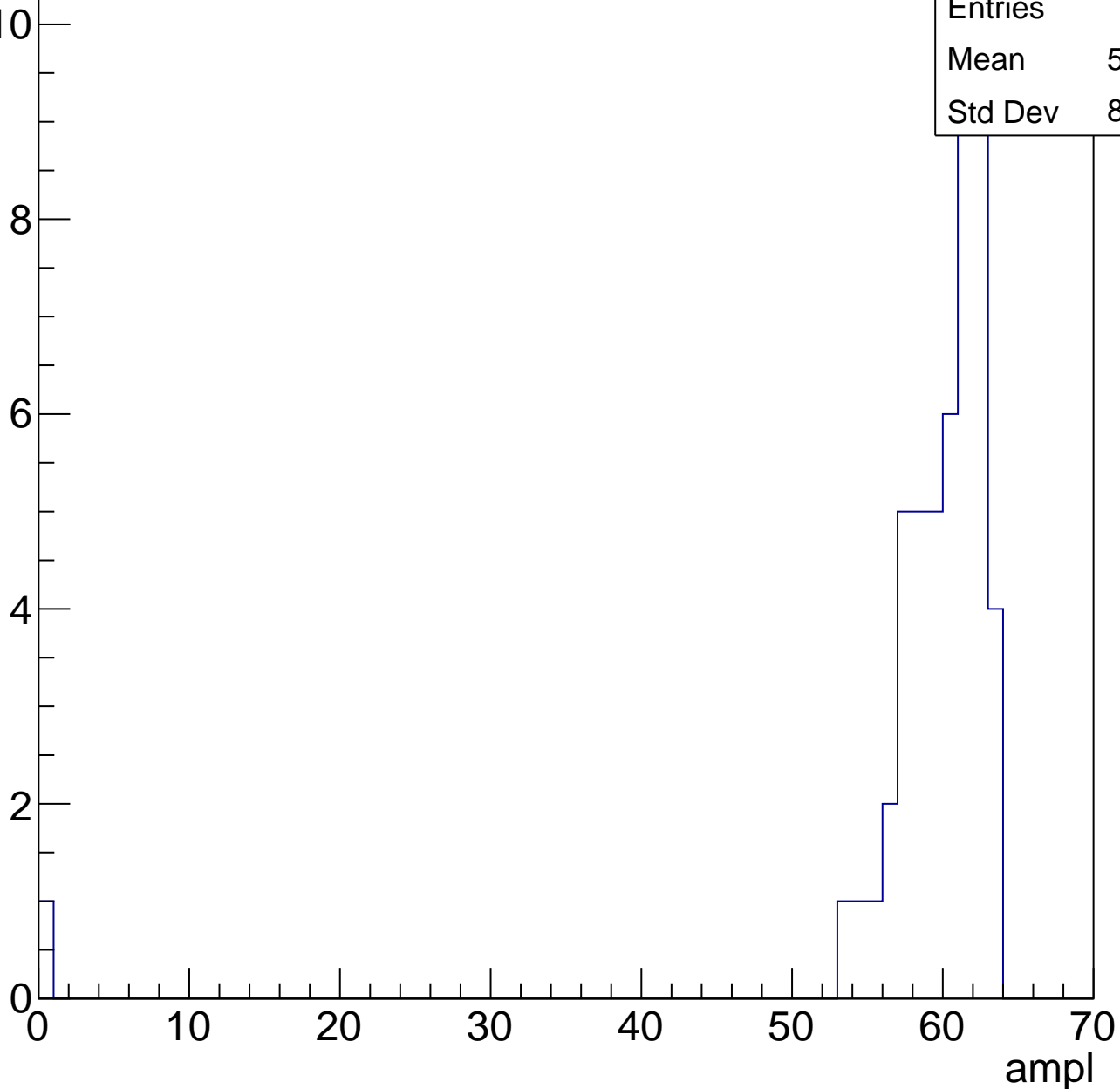


# B0L000S, U26-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	58.48
Std Dev	8.693

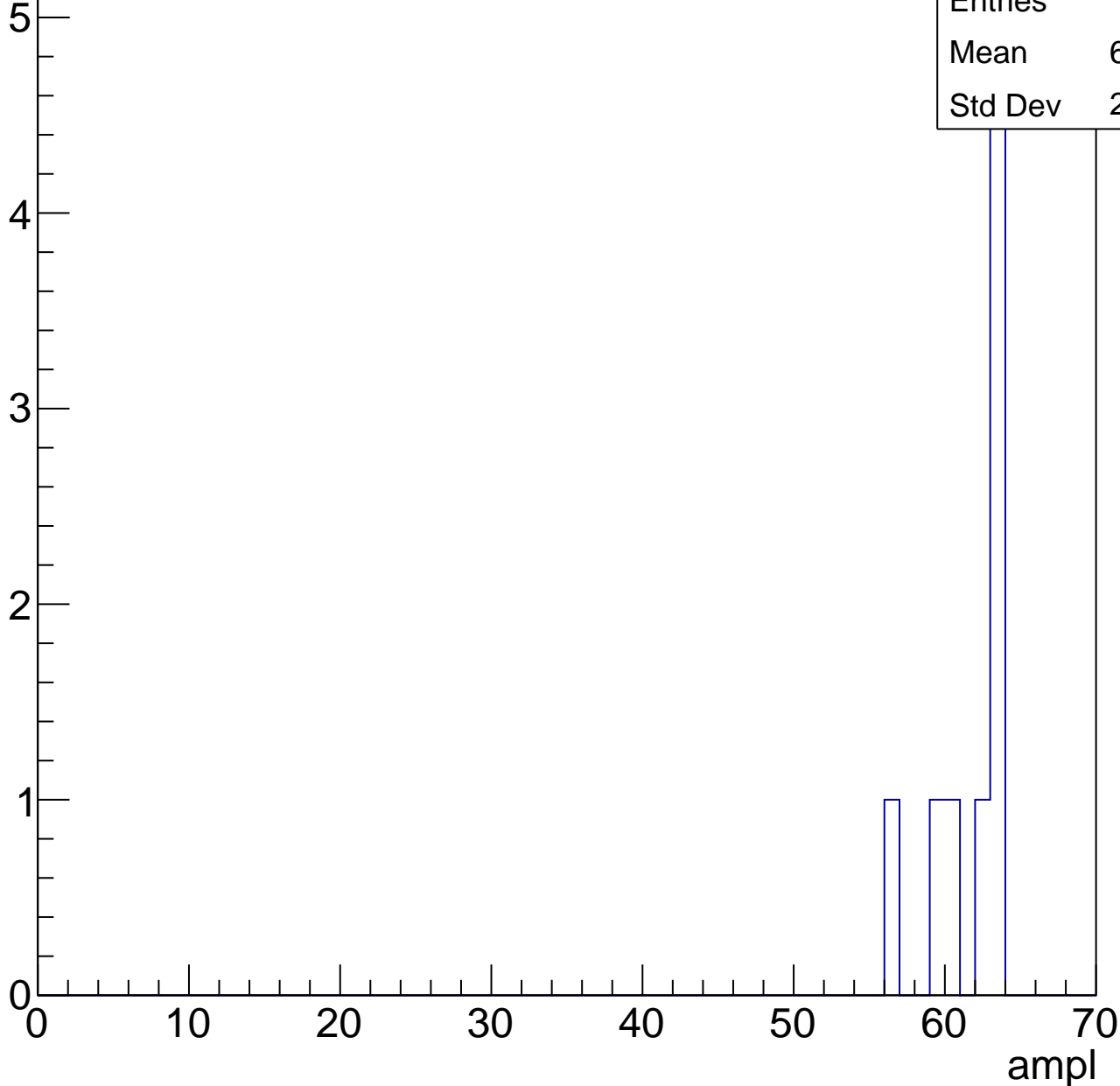


# B0L000S, U26-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	61.33
Std Dev	2.357





# B0L000S, U26-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U26-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	29.98
Std Dev	4.835

**Gaus mean : 30.8633**

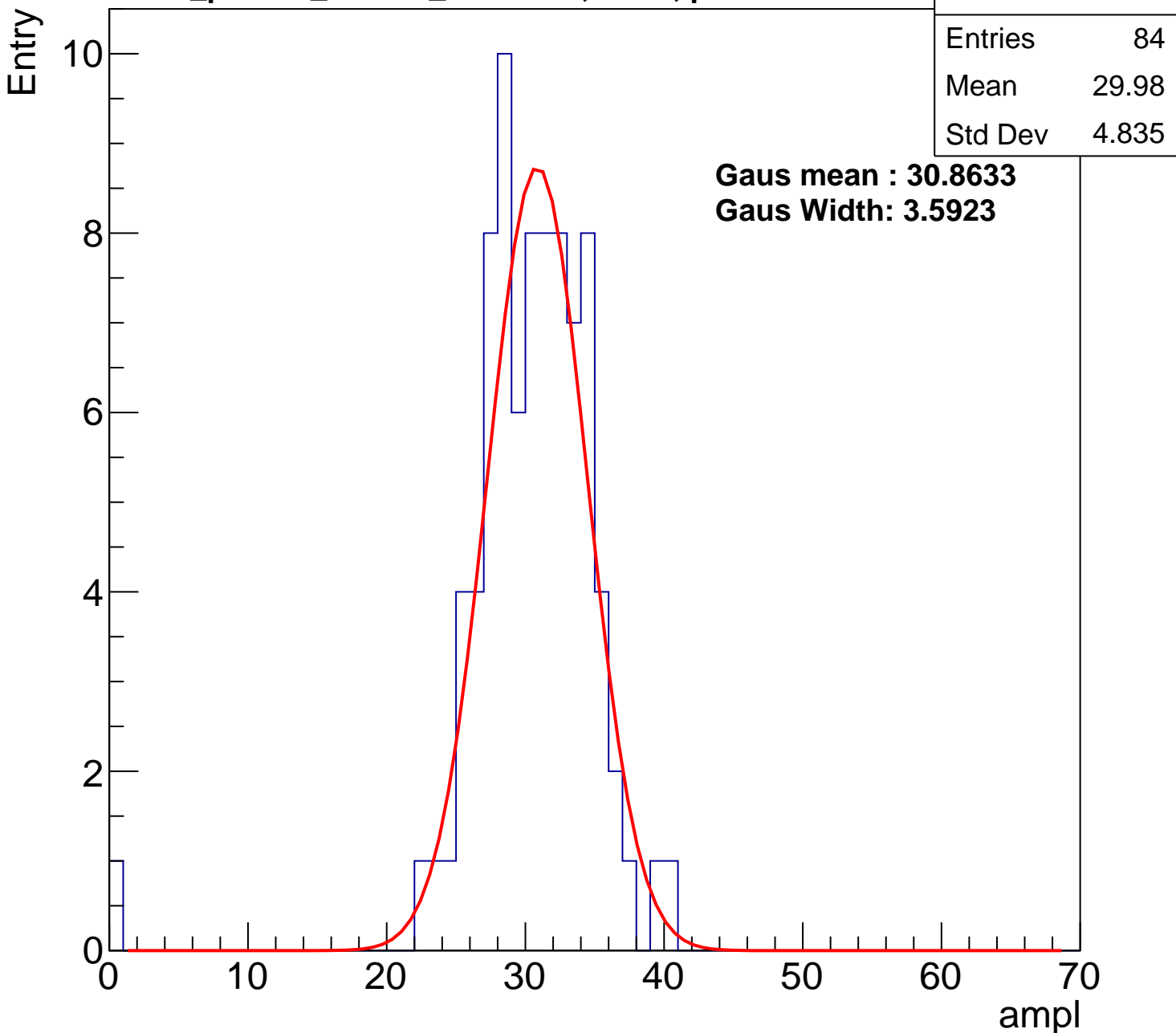
**Gaus Width: 3.5923**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch73, adc1

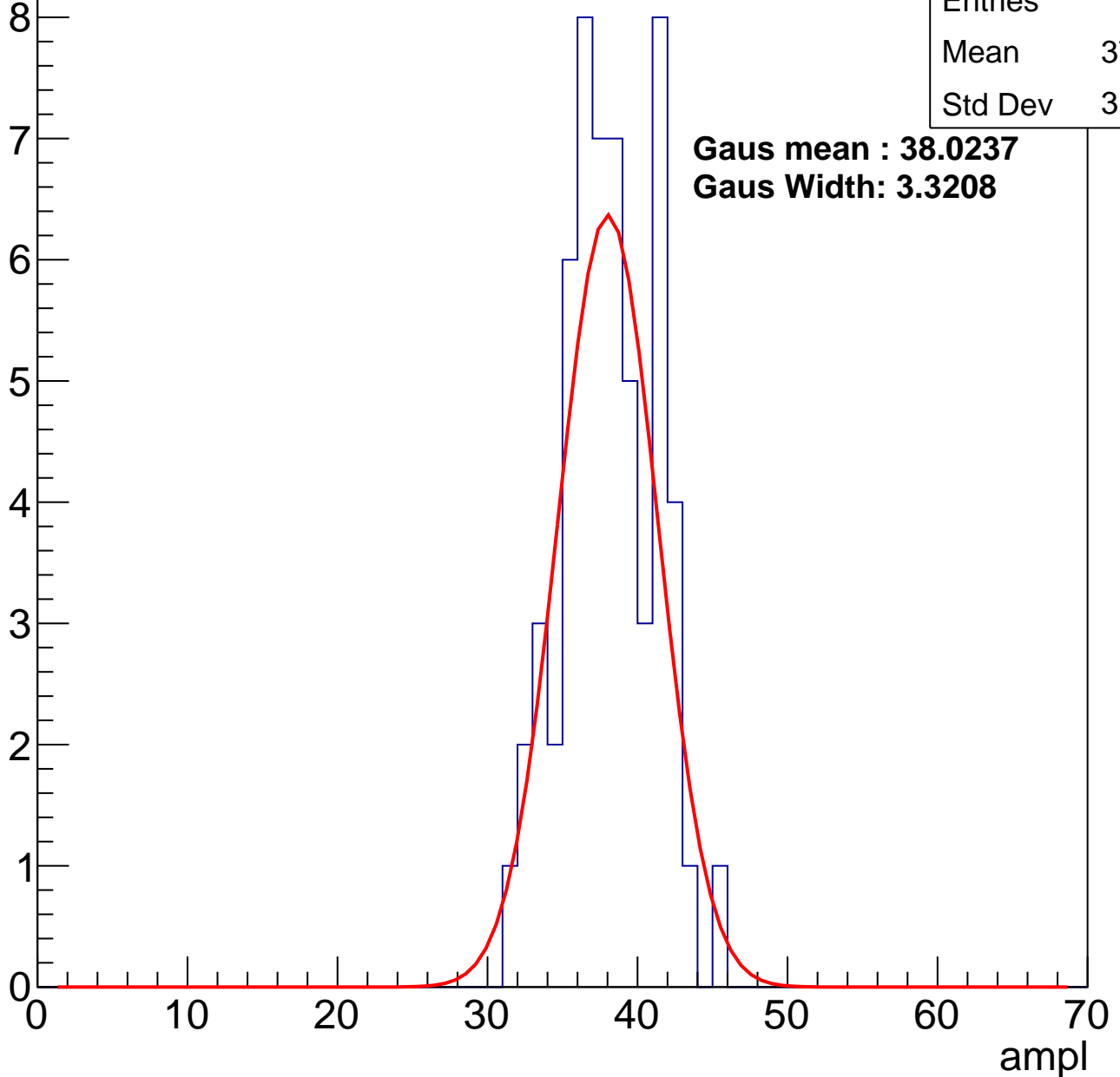
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	37.66
Std Dev	3.054

**Gaus mean : 38.0237**

**Gaus Width: 3.3208**



# B0L000S, U26-ch73, adc2

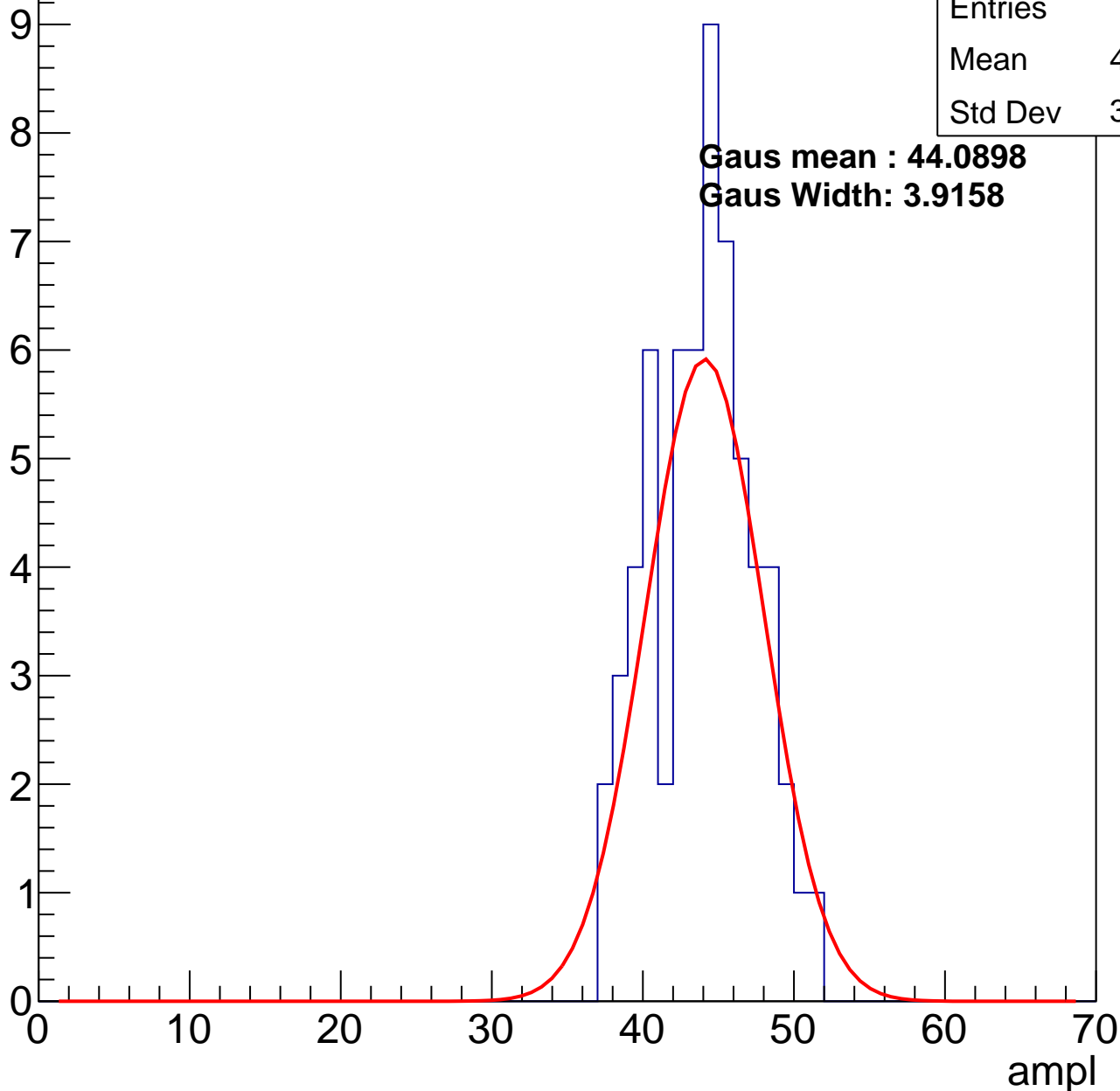
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.48
Std Dev	3.349

**Gaus mean : 44.0898**

**Gaus Width: 3.9158**

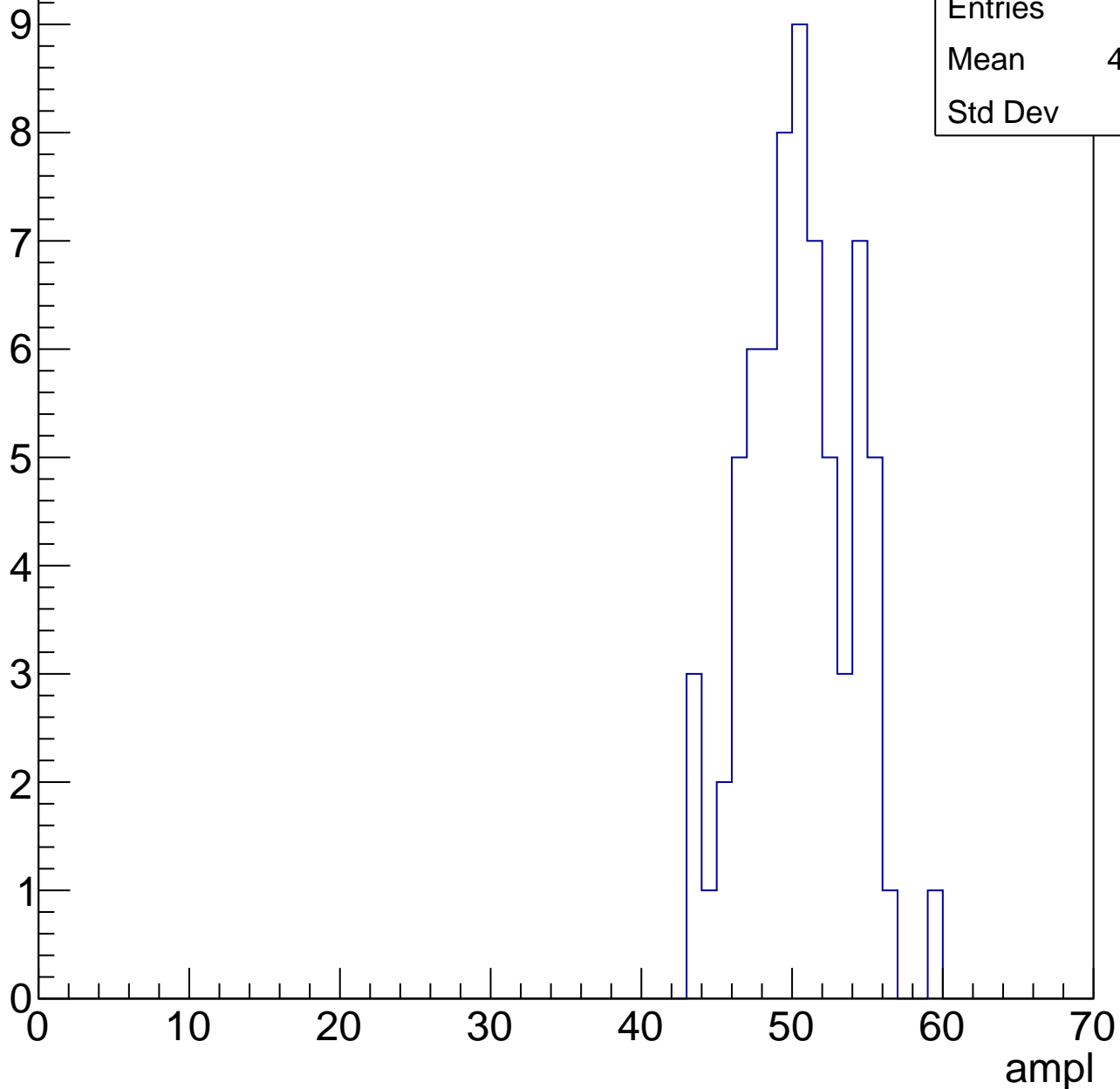


# B0L000S, U26-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.99
Std Dev	3.42

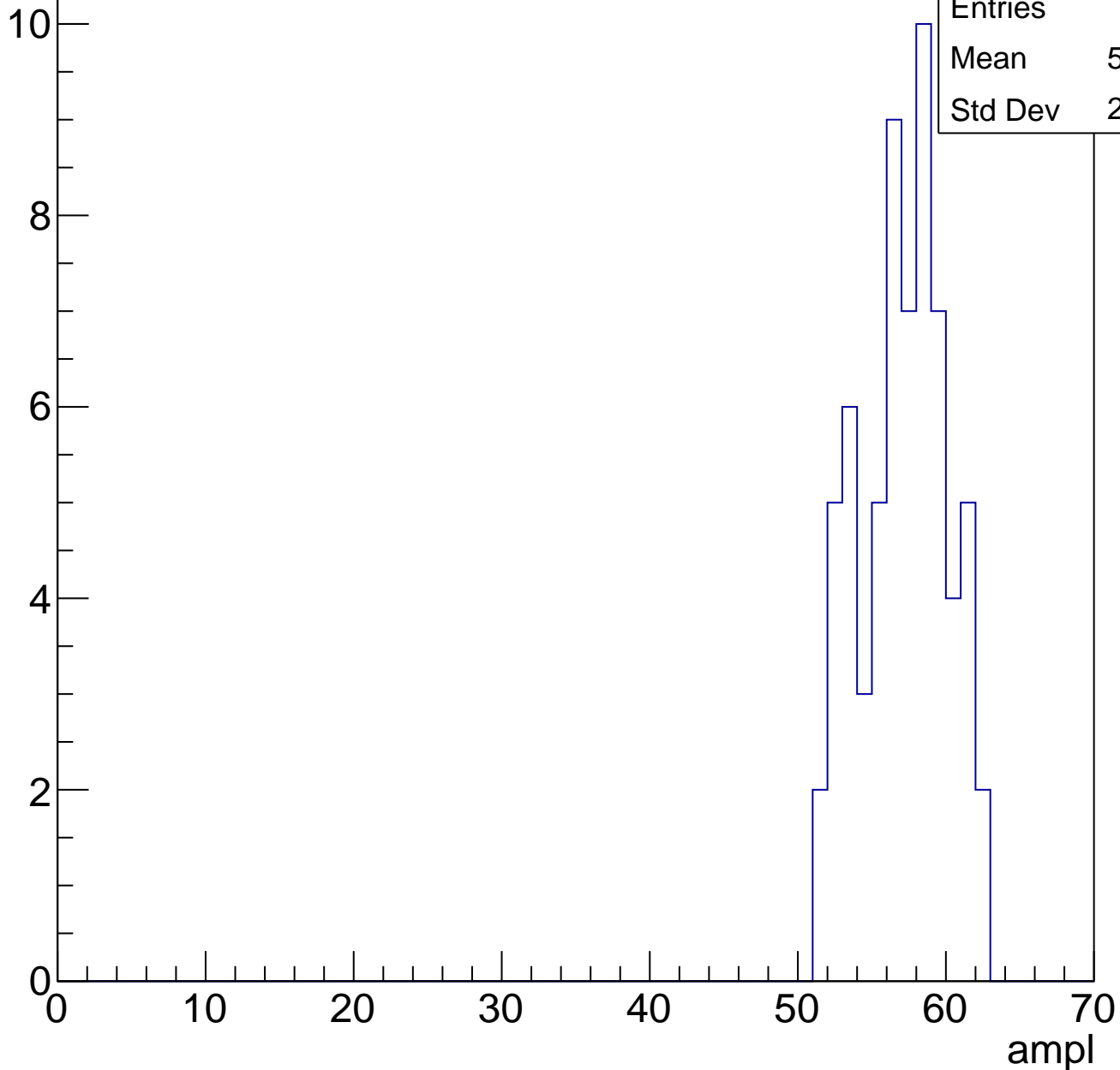


# B0L000S, U26-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	65
Mean	56.65
Std Dev	2.895

Entry

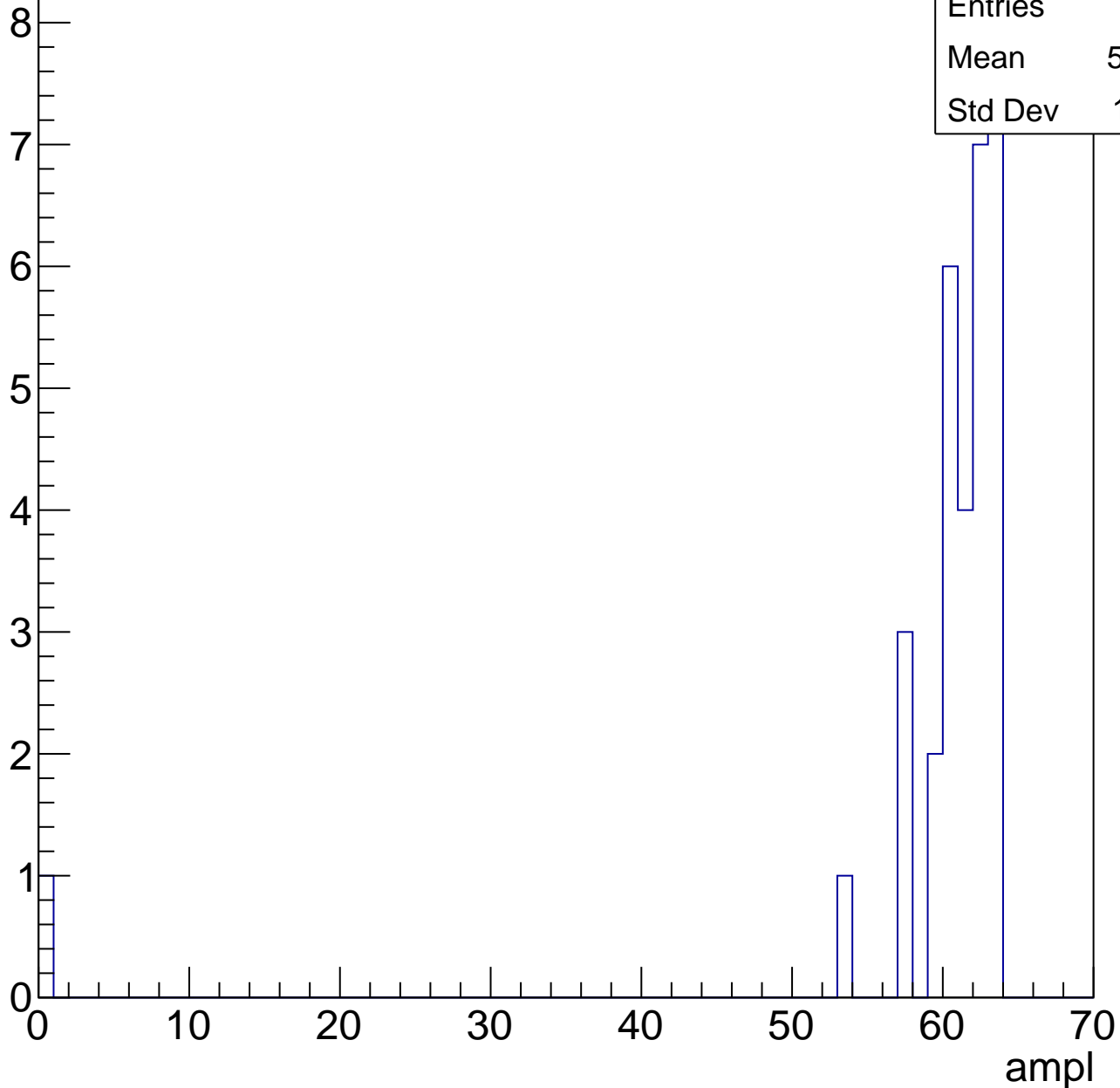


# B0L000S, U26-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.88
Std Dev	10.81



# B0L000S, U26-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch74, adc0

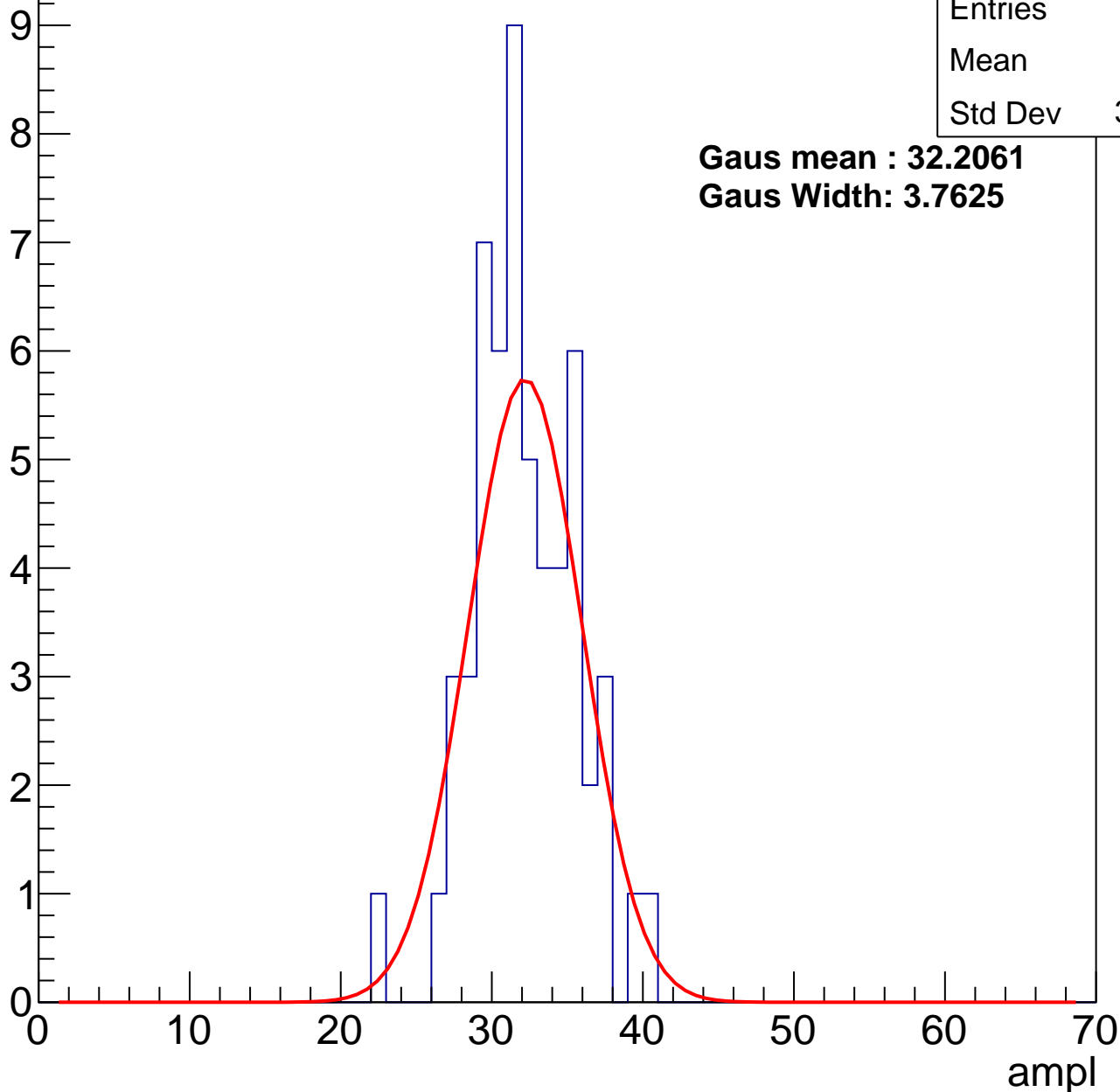
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	31.7
Std Dev	3.391

**Gaus mean : 32.2061**

**Gaus Width: 3.7625**



# B0L000S, U26-ch74, adc1

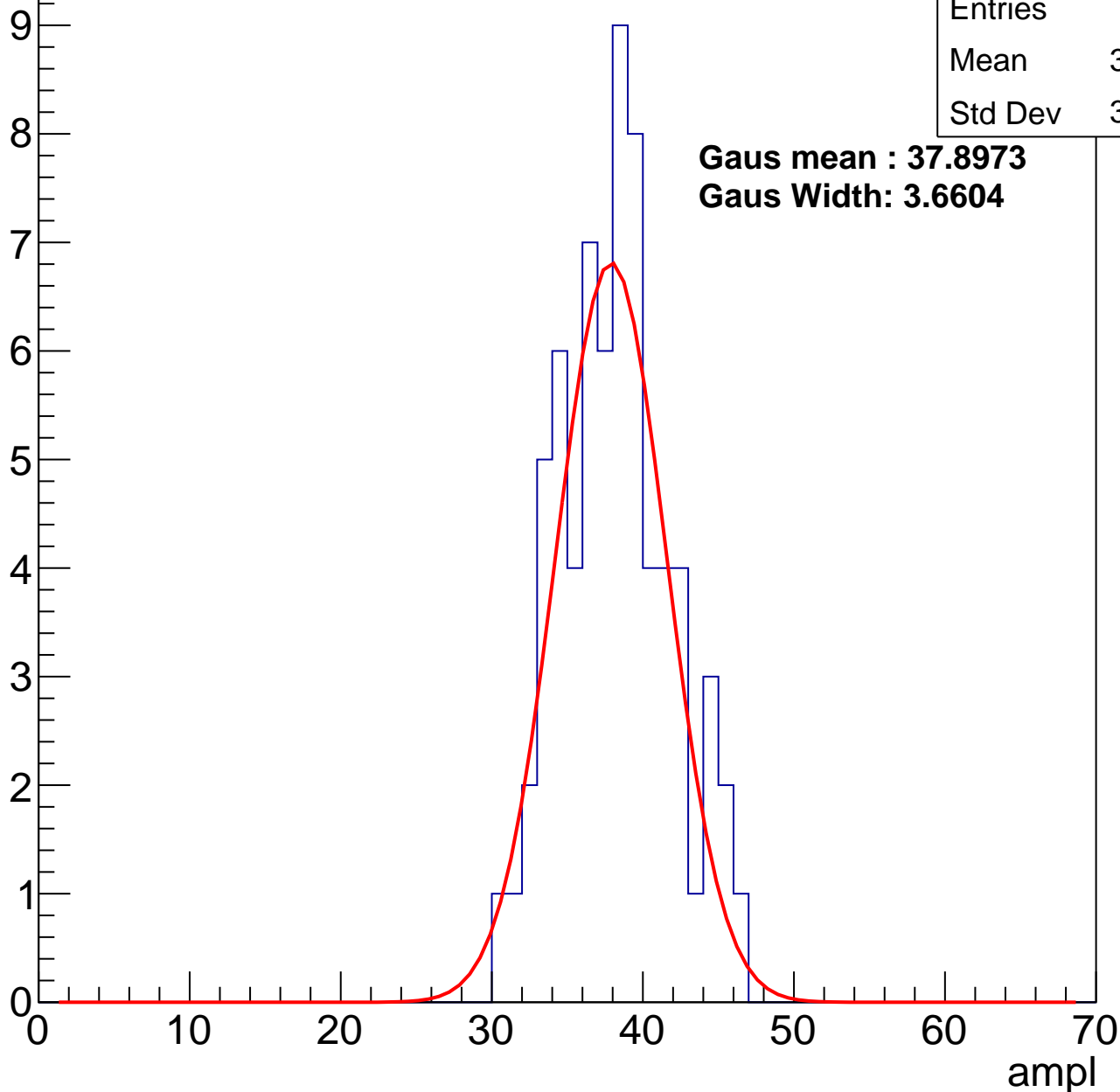
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.72
Std Dev	3.617

**Gaus mean : 37.8973**

**Gaus Width: 3.6604**



# B0L000S, U26-ch74, adc2

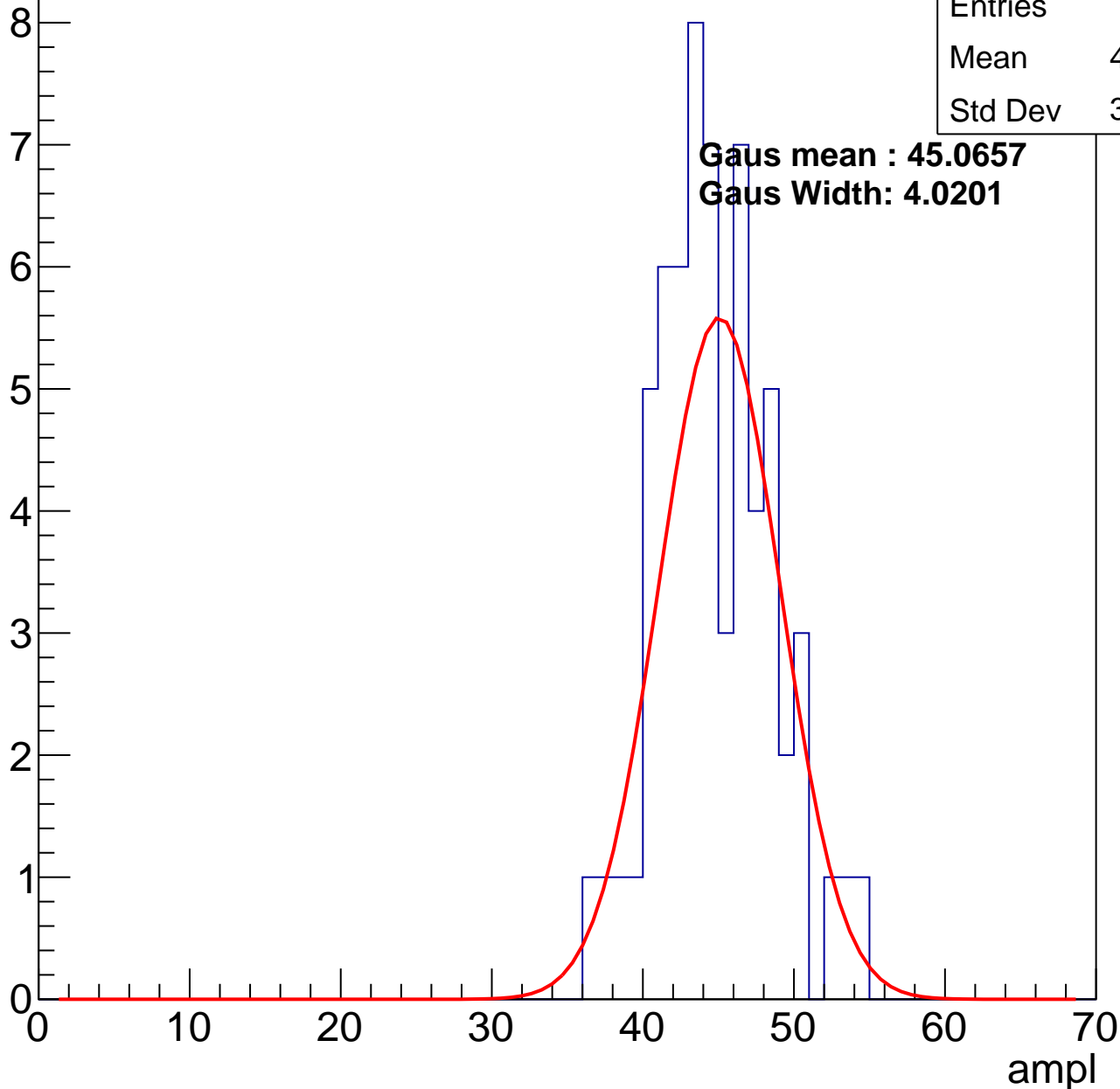
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	44.32
Std Dev	3.745

**Gaus mean : 45.0657**

**Gaus Width: 4.0201**



# B0L000S, U26-ch74, adc3

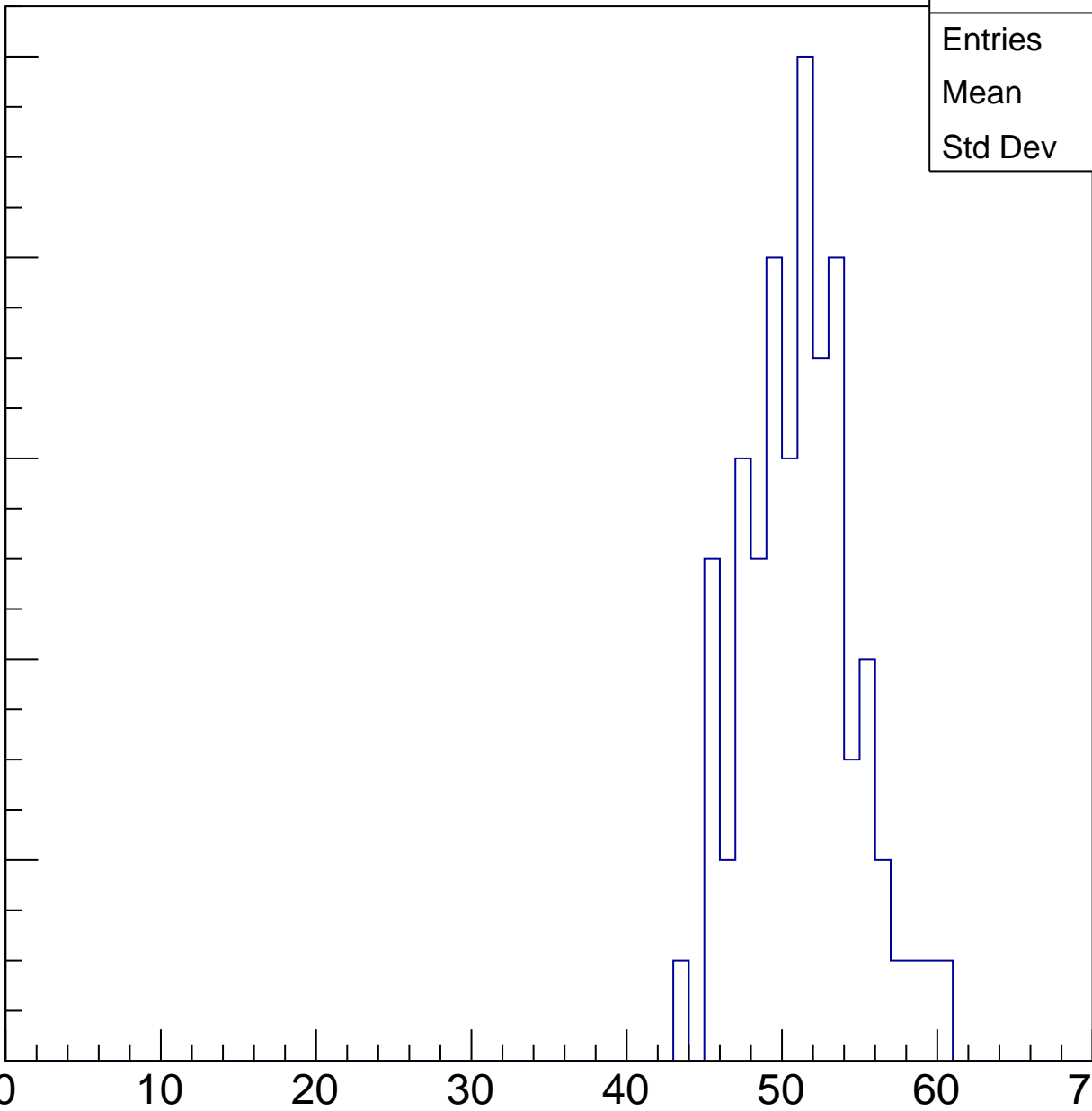
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	50.7
Std Dev	3.514

Entry

10  
8  
6  
4  
2  
0

ampl

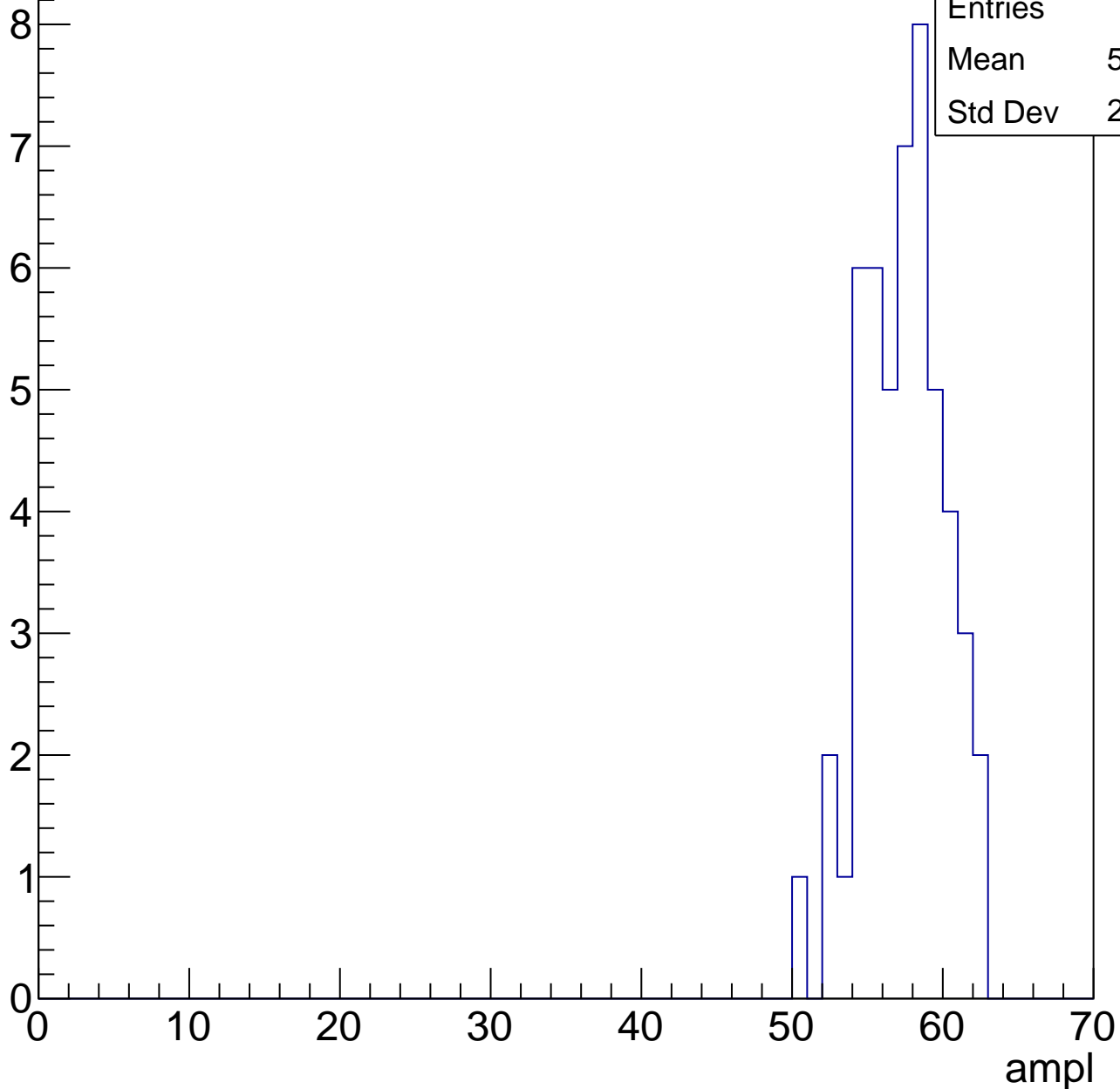


# B0L000S, U26-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

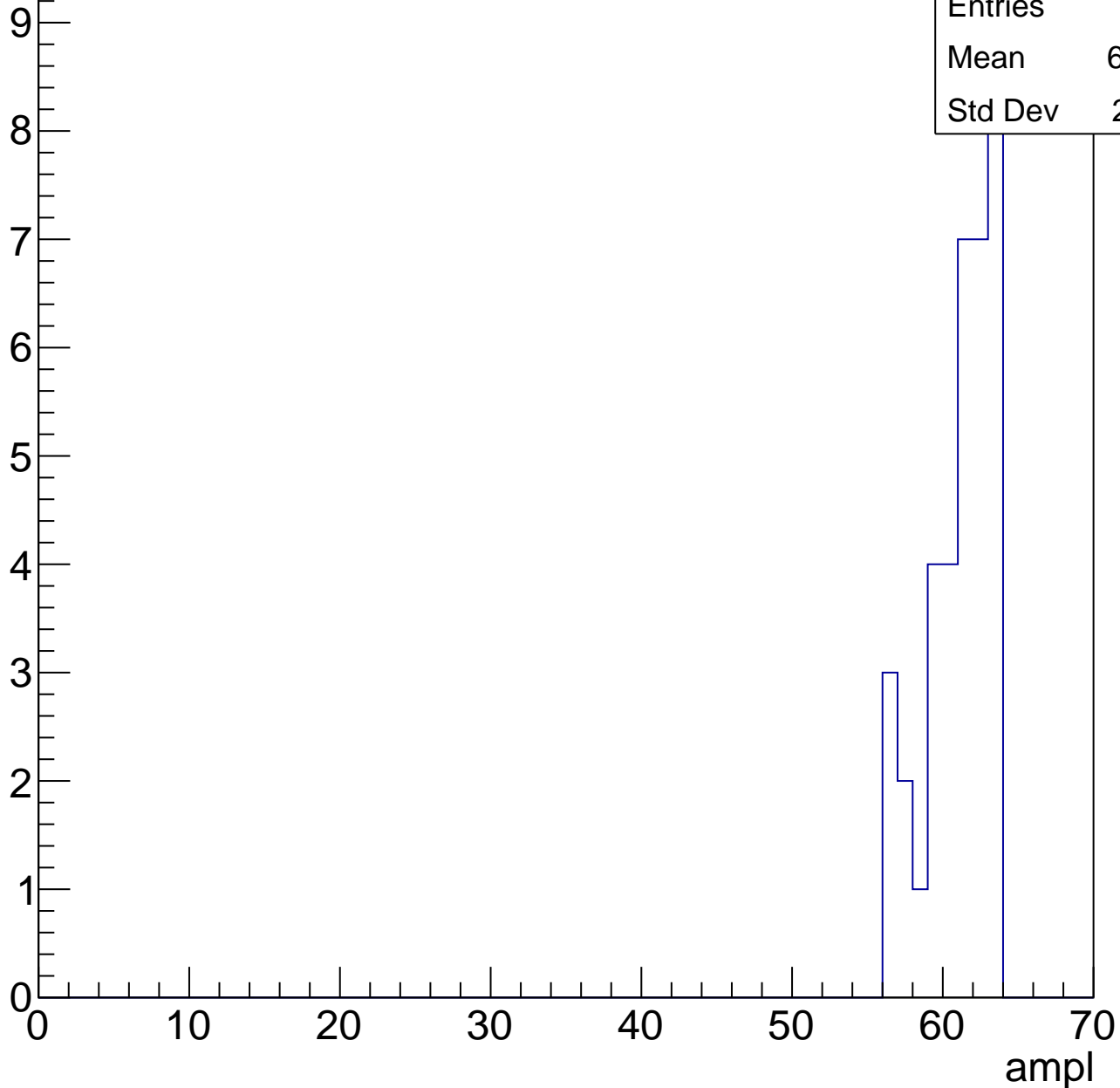
Entries	50
Mean	56.92
Std Dev	2.682



# B0L000S, U26-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

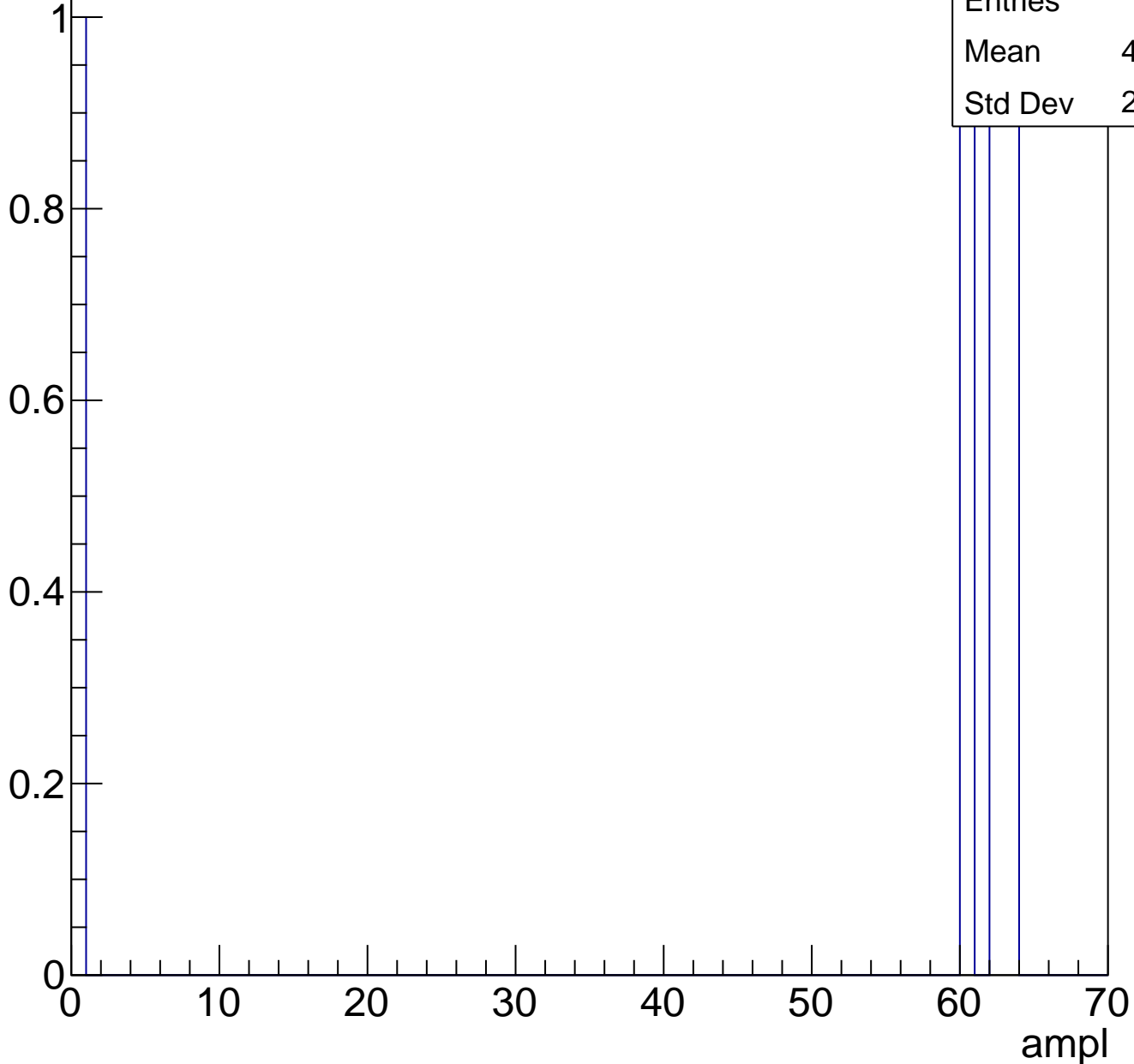
Entry



# B0L000S, U26-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch75, adc0

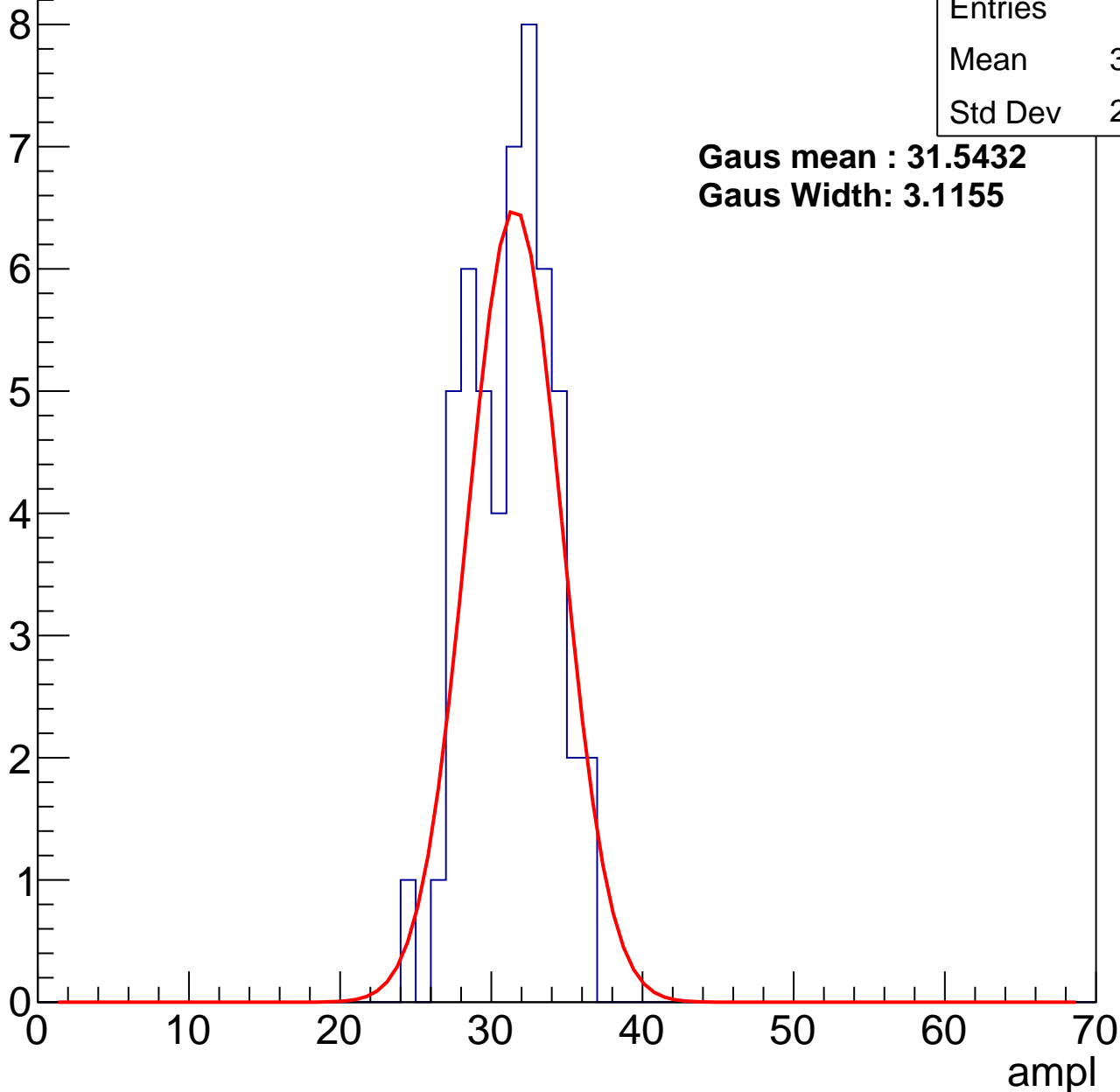
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	30.79
Std Dev	2.734

**Gaus mean : 31.5432**

**Gaus Width: 3.1155**



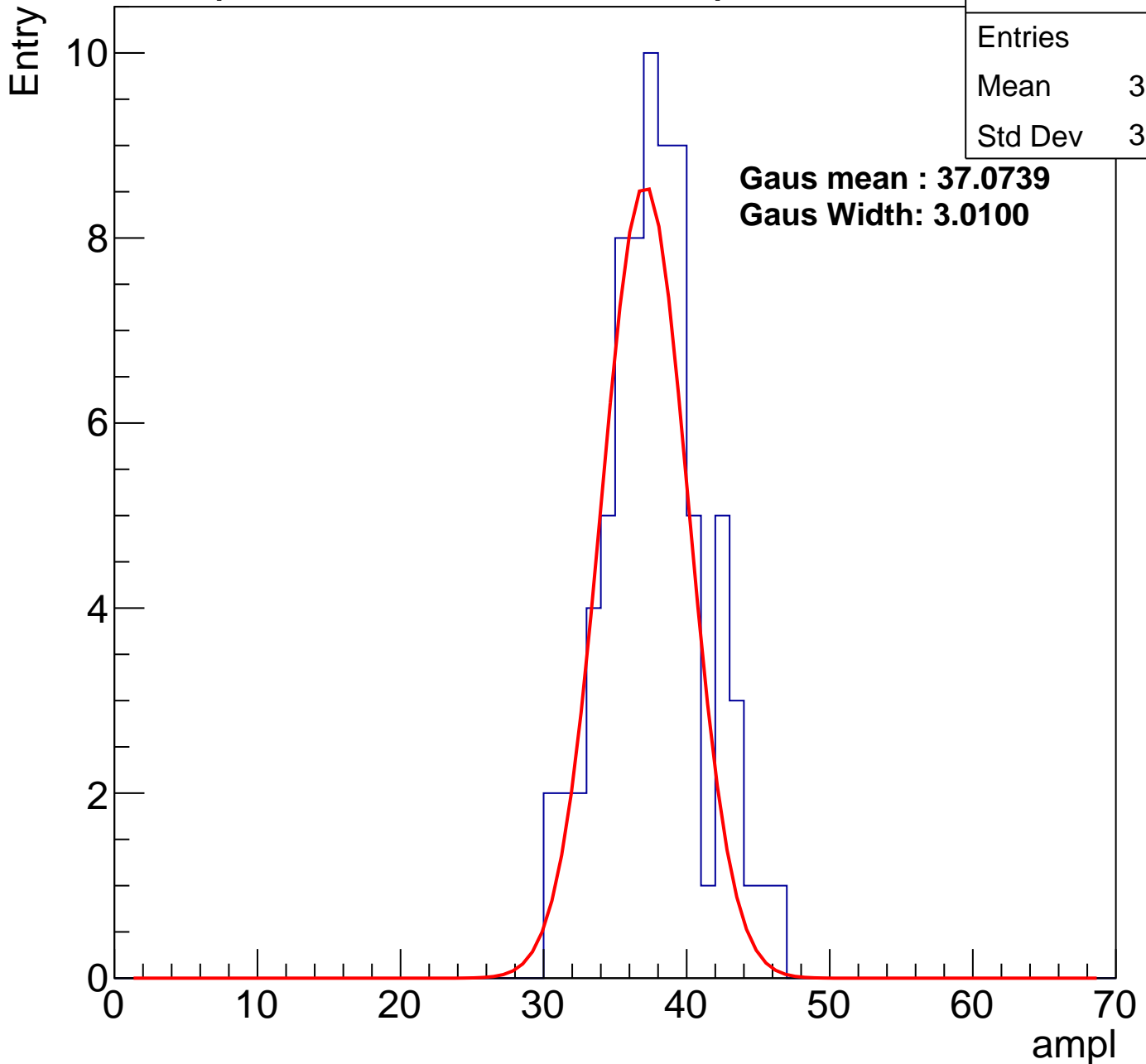
# B0L000S, U26-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	37.29
Std Dev	3.433

**Gaus mean : 37.0739**

**Gaus Width: 3.0100**



# B0L000S, U26-ch75, adc2

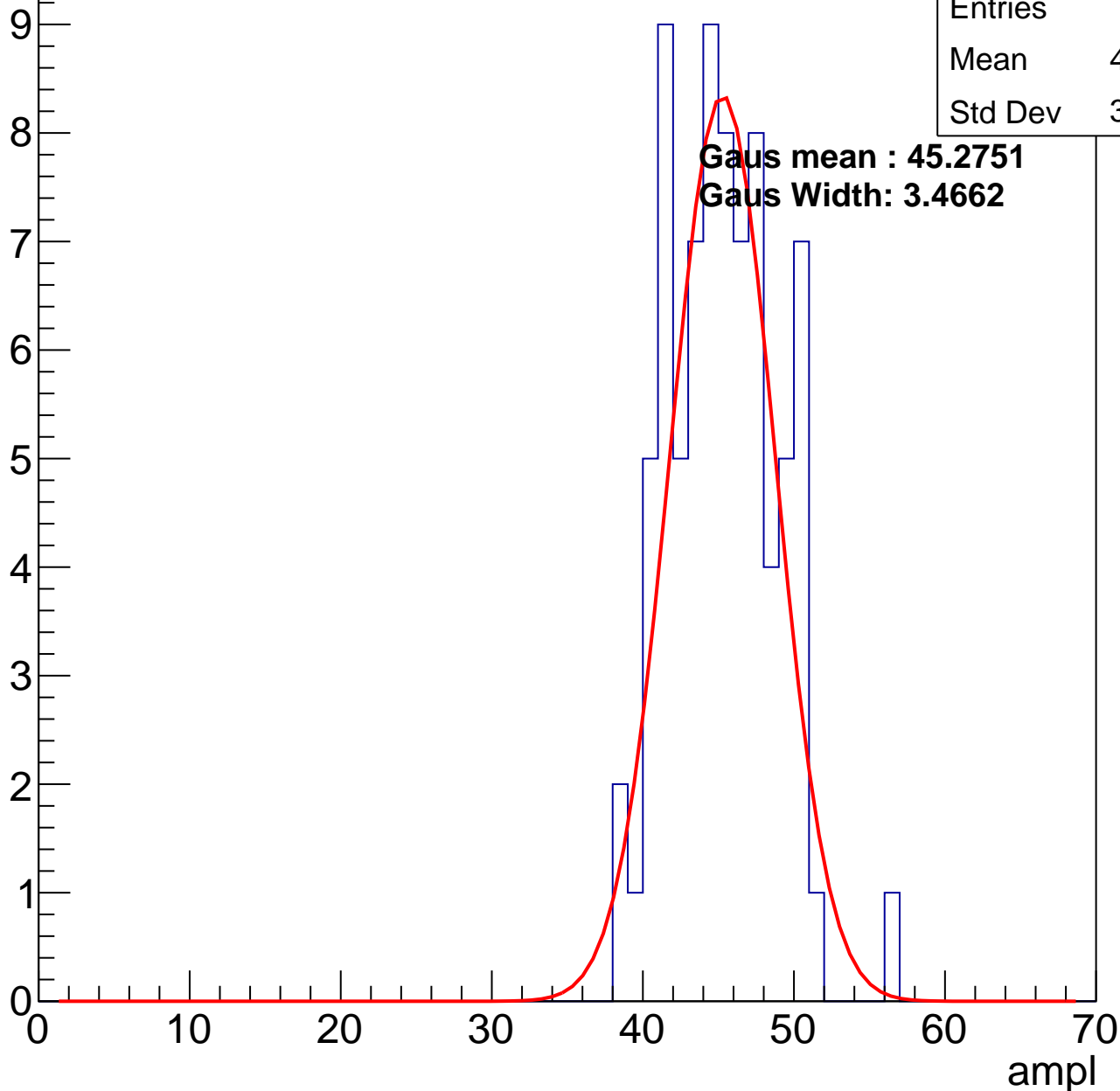
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	44.85
Std Dev	3.504

**Gaus mean : 45.2751**

**Gaus Width: 3.4662**

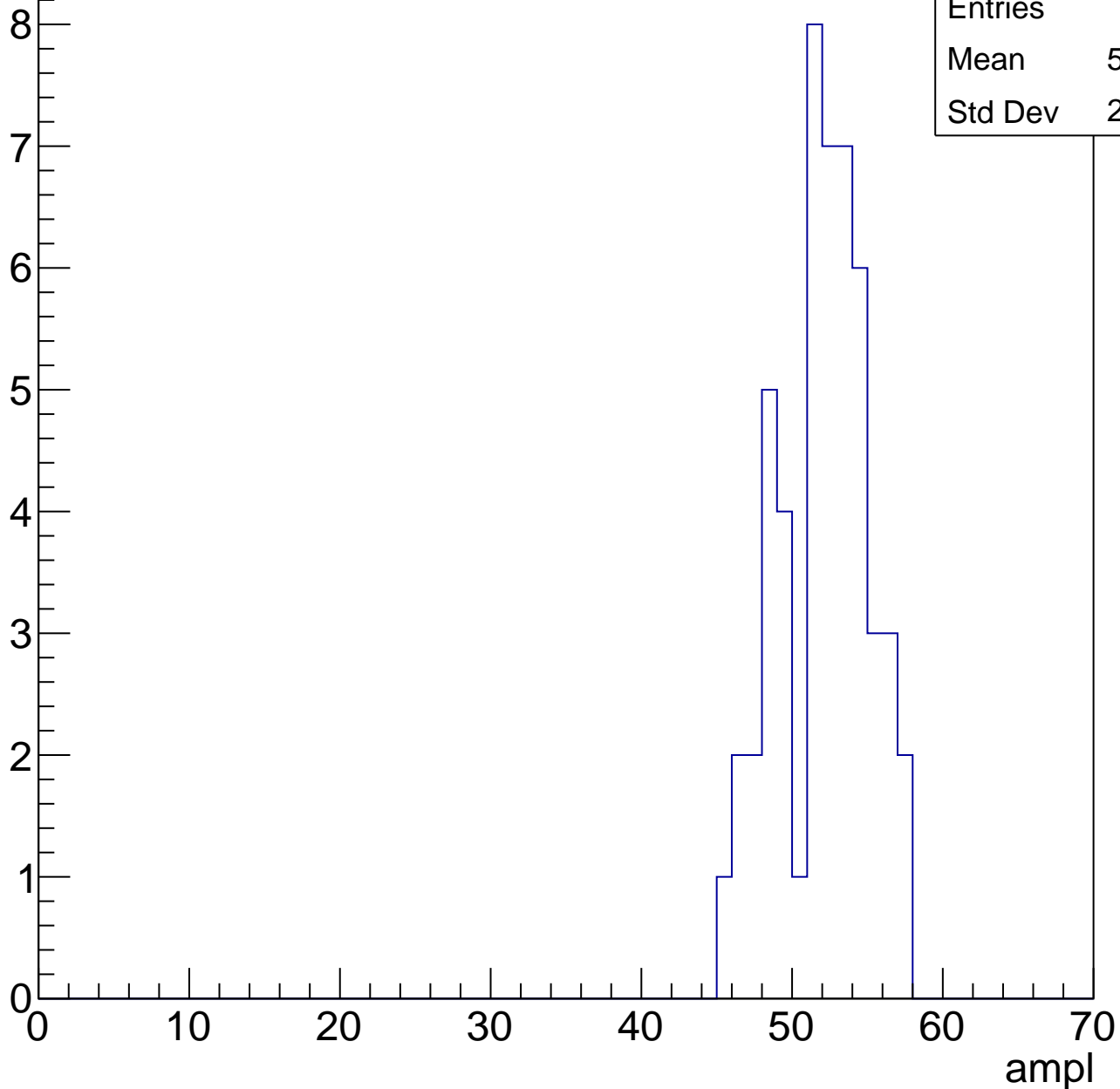


# B0L000S, U26-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	51.59
Std Dev	2.958

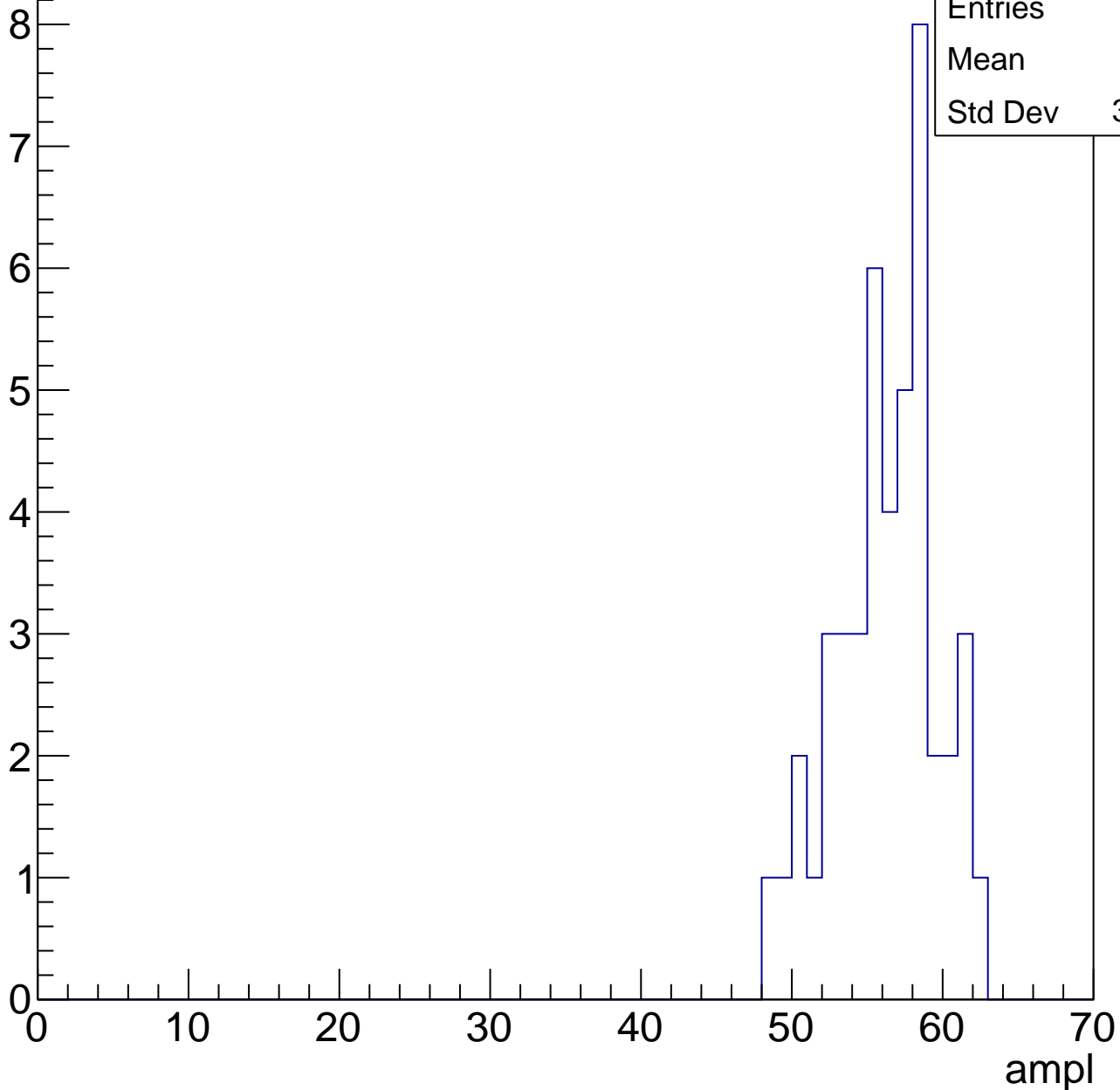


# B0L000S, U26-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

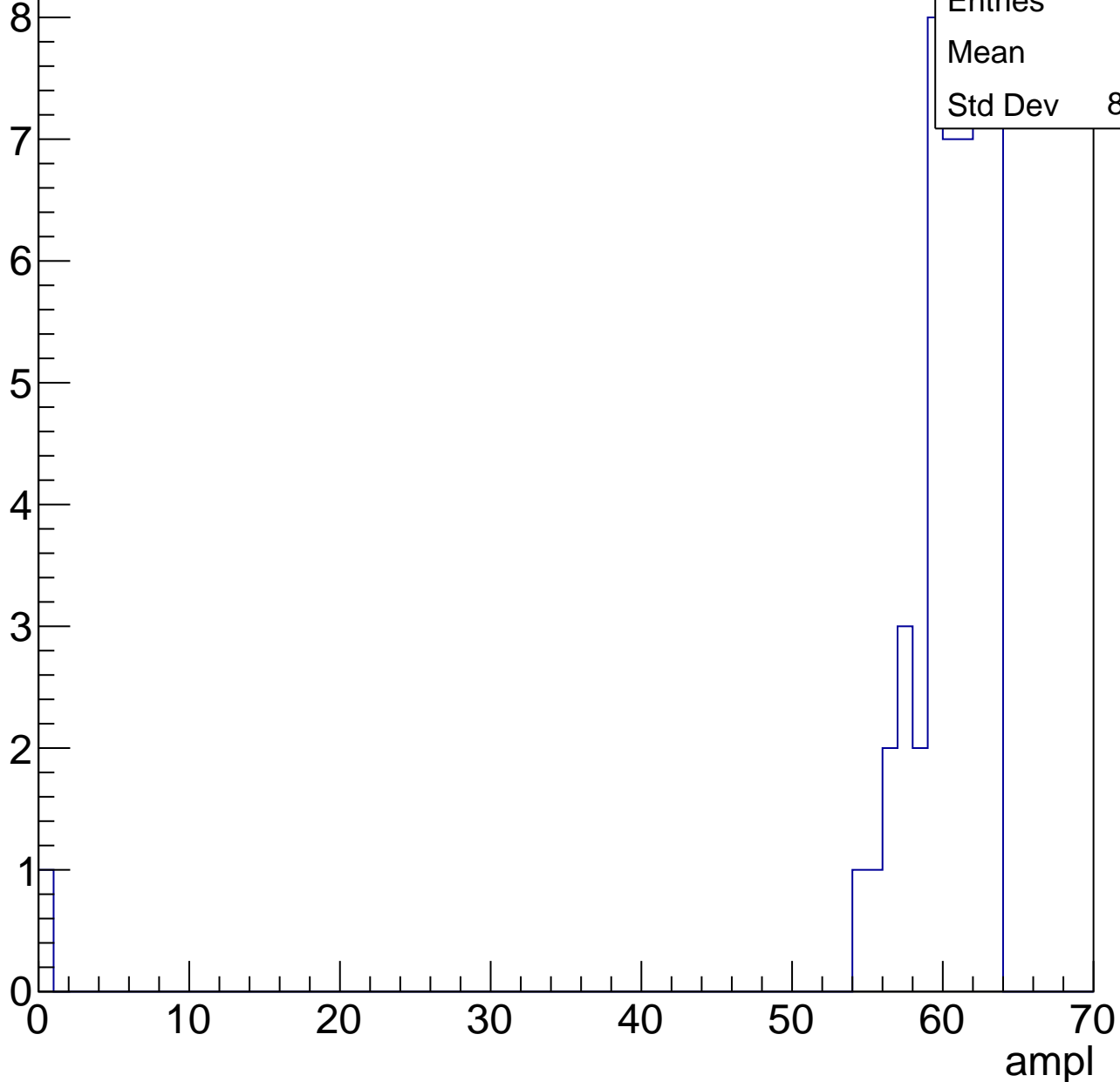
Entries	45
Mean	55.8
Std Dev	3.331



# B0L000S, U26-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch76, adc0

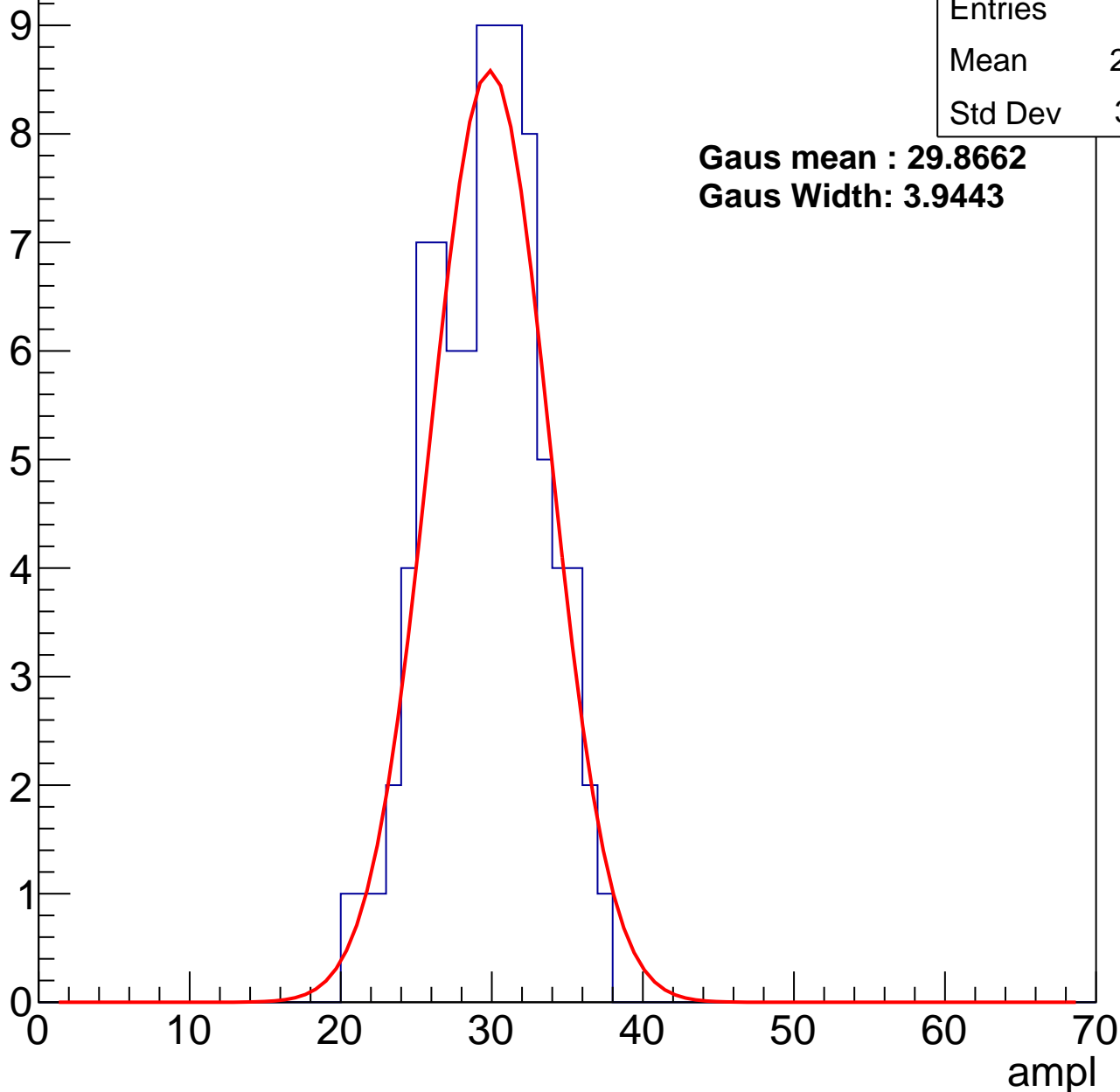
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	29.16
Std Dev	3.691

**Gaus mean : 29.8662**

**Gaus Width: 3.9443**



# B0L000S, U26-ch76, adc1

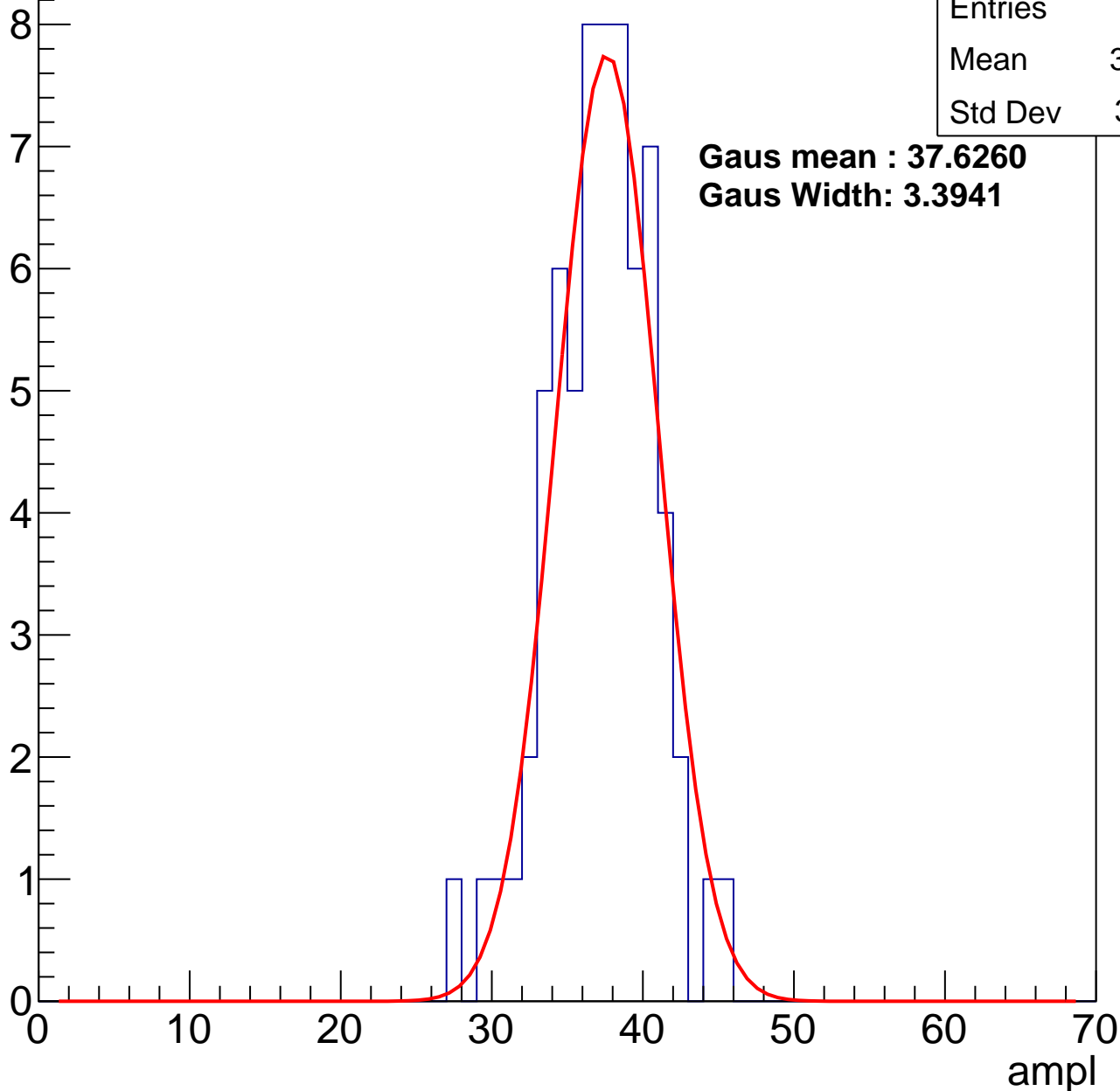
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.78
Std Dev	3.411

**Gaus mean : 37.6260**

**Gaus Width: 3.3941**



# B0L000S, U26-ch76, adc2

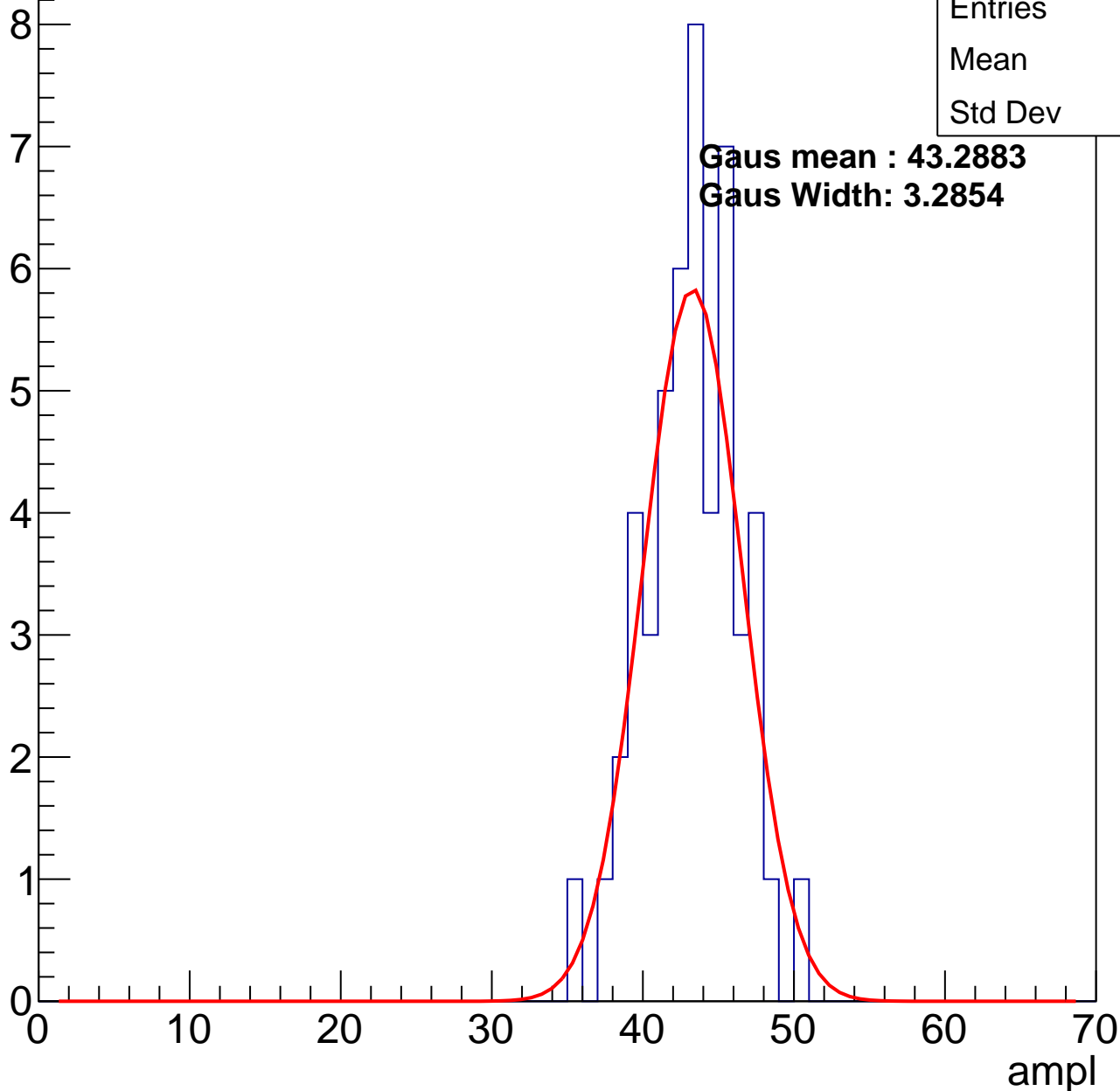
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	42.8
Std Dev	3.04

**Gaus mean : 43.2883**

**Gaus Width: 3.2854**

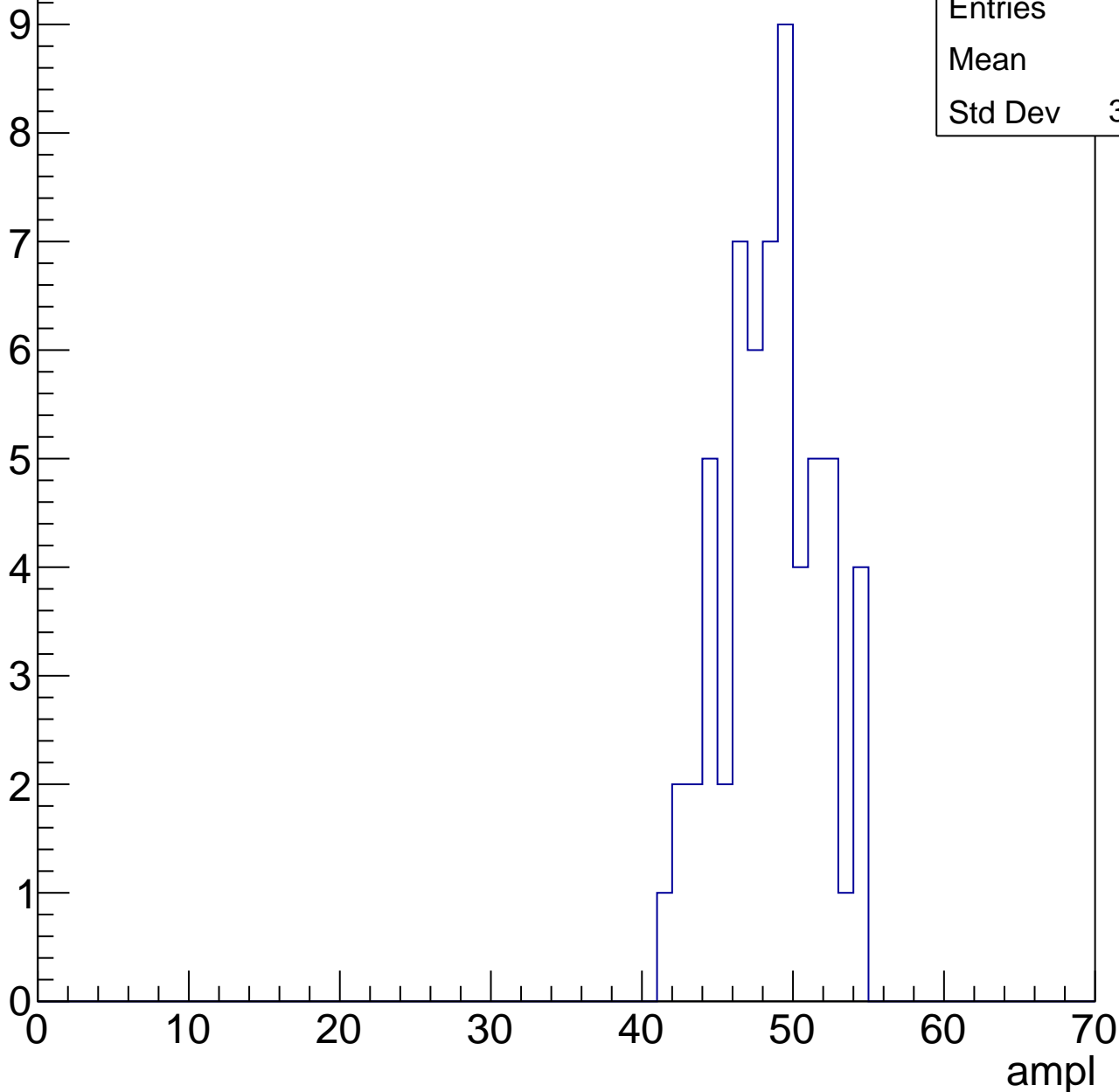


# B0L000S, U26-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

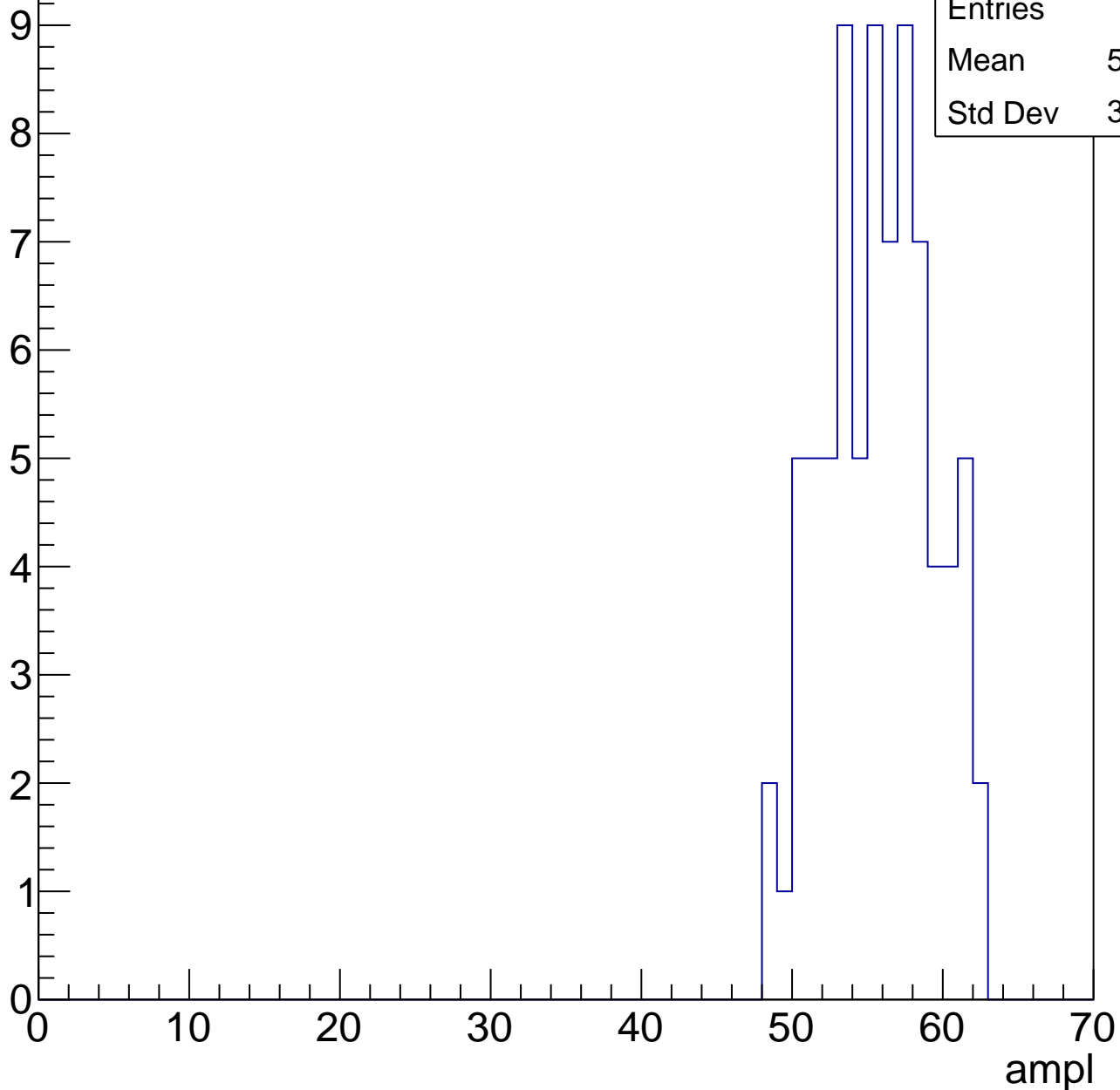
Entries	60
Mean	48.1
Std Dev	3.218



# B0L000S, U26-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 40

Mean 58.45

Std Dev 9.654

ampl

0

10

20

30

40

50

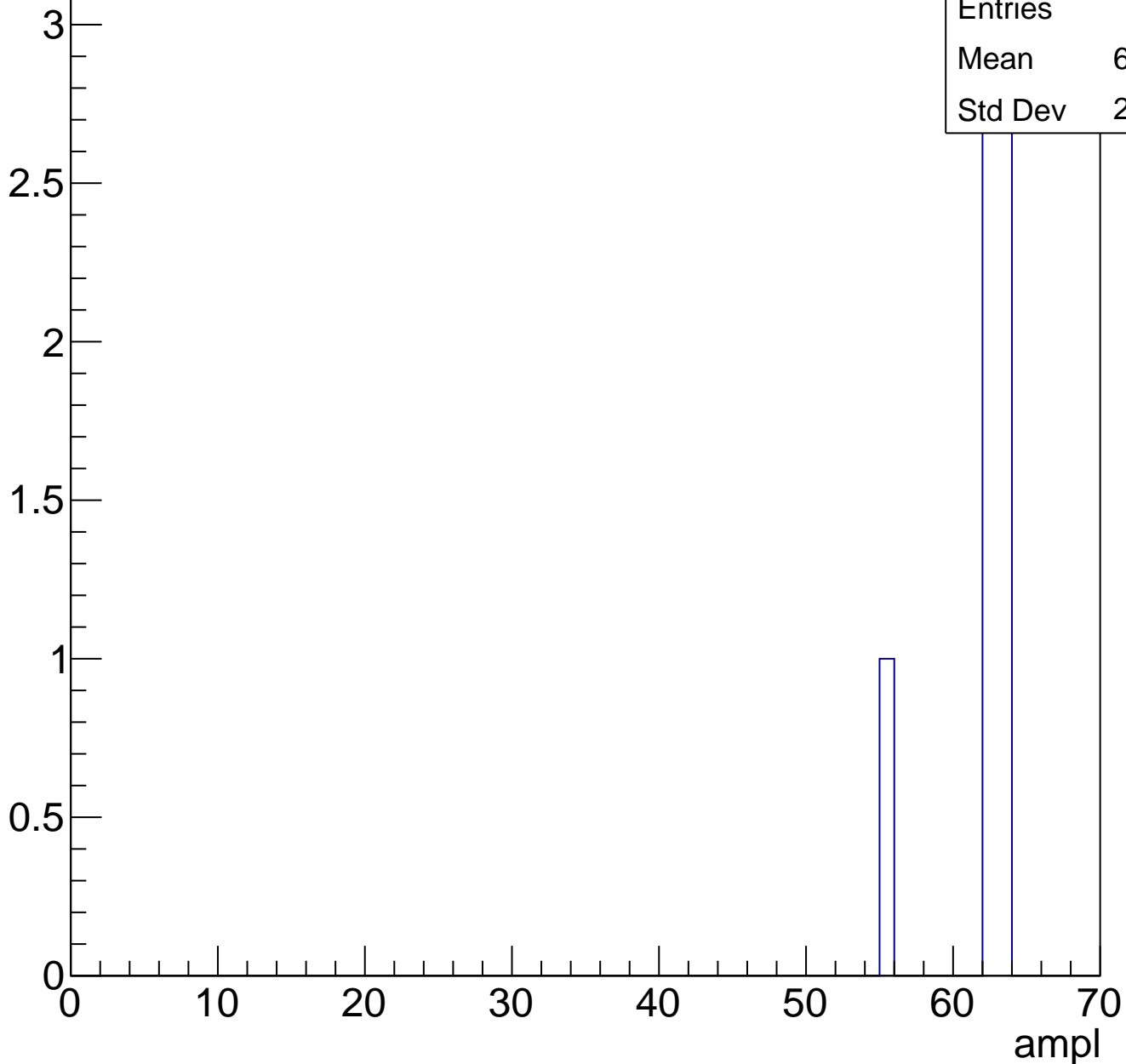
60

70

# B0L000S, U26-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch77, adc0

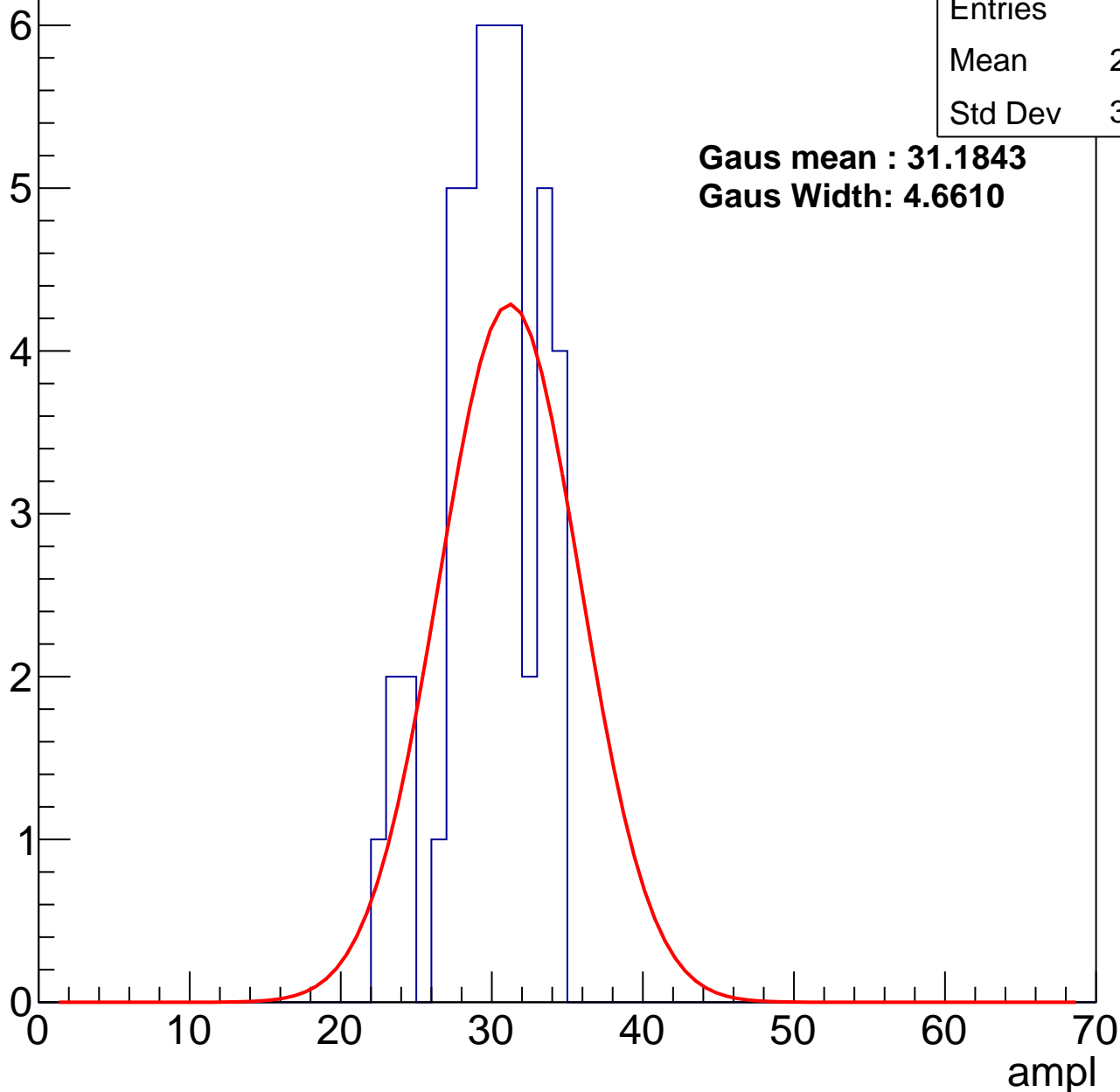
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	29.38
Std Dev	3.079

**Gaus mean : 31.1843**

**Gaus Width: 4.6610**



# B0L000S, U26-ch77, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	36.27
Std Dev	3.466

**Gaus mean : 36.4190**

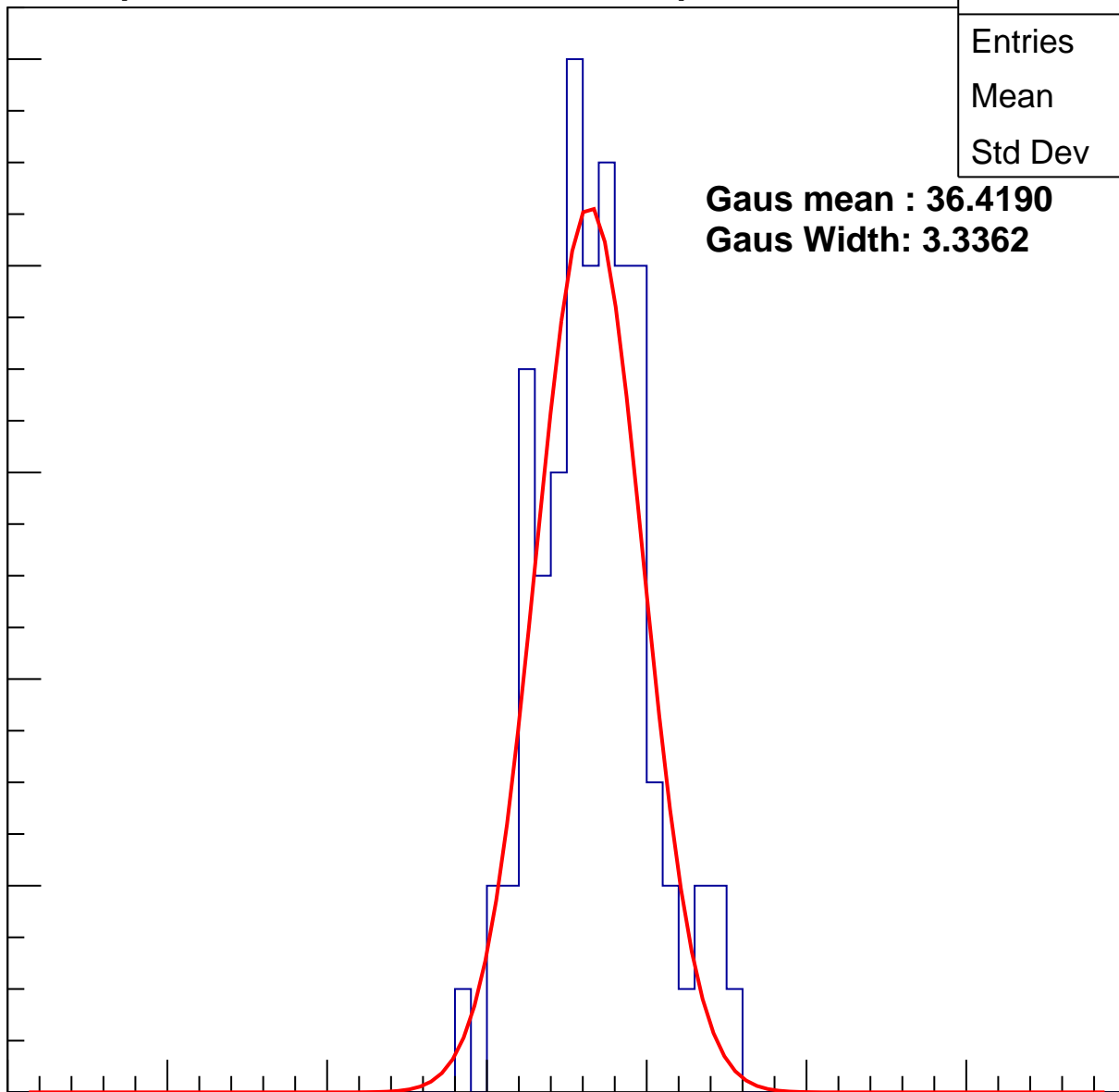
**Gaus Width: 3.3362**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch77, adc2

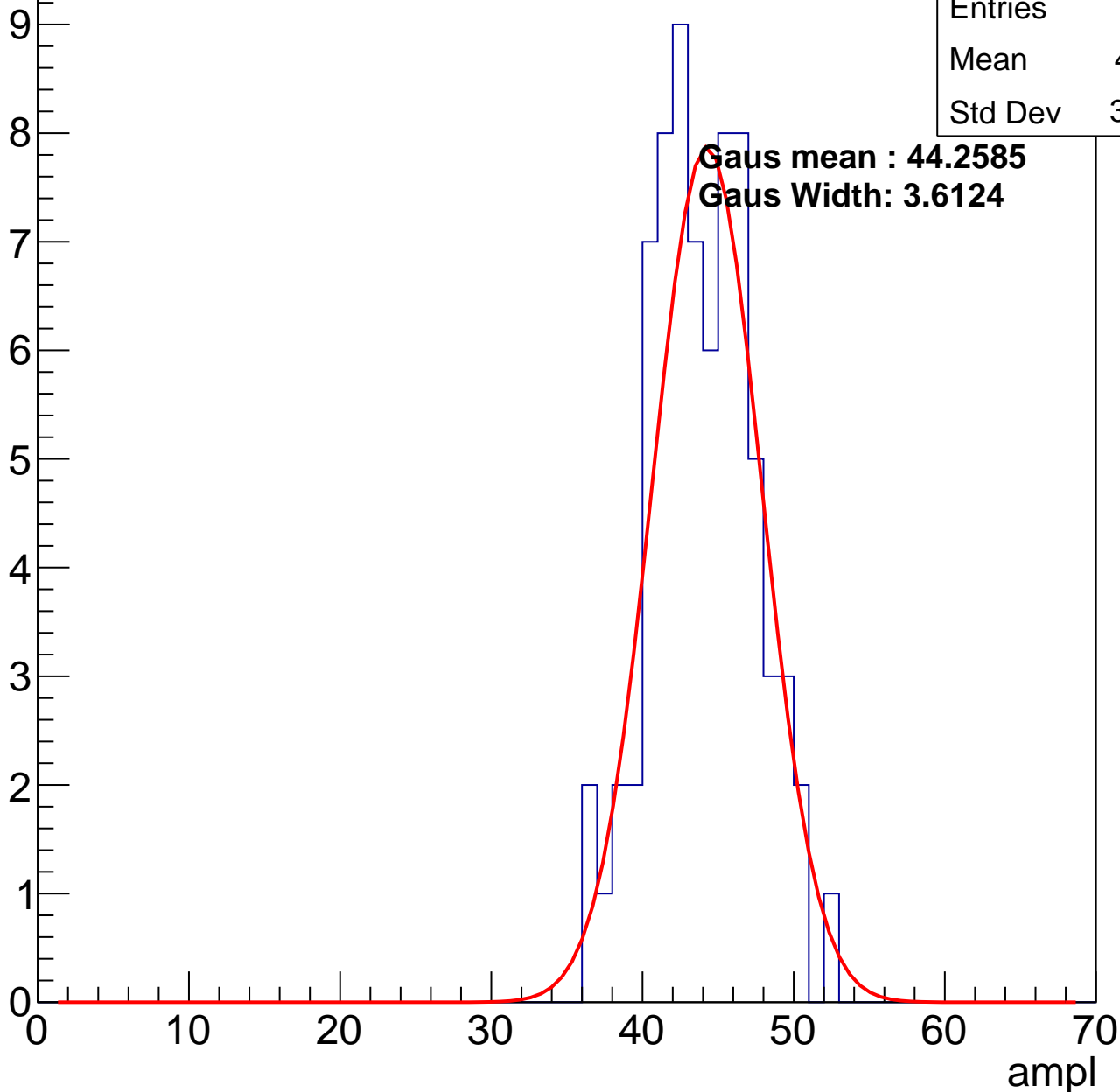
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.51
Std Dev	3.394

**Gaus mean : 44.2585**

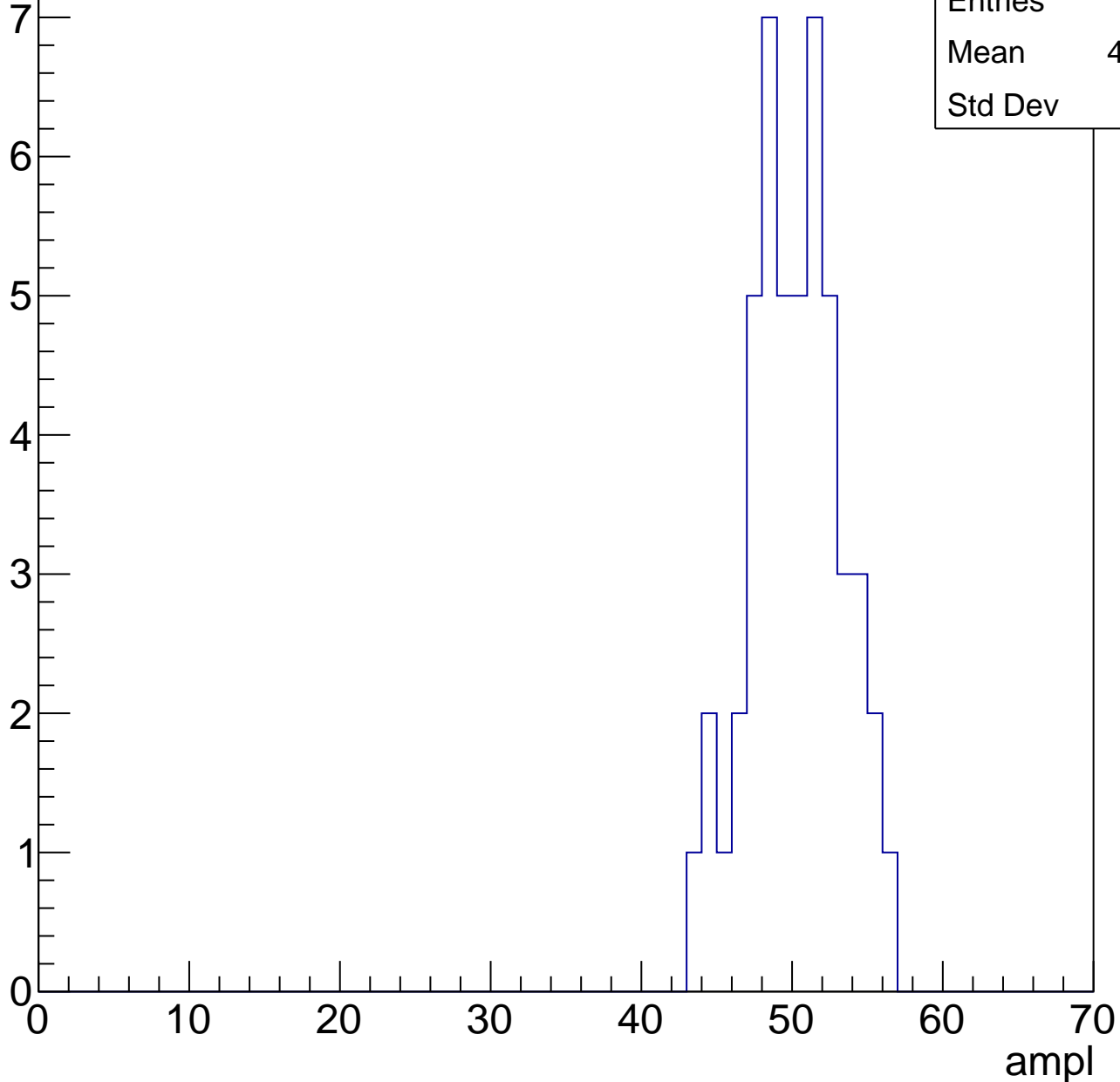
**Gaus Width: 3.6124**



# B0L000S, U26-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

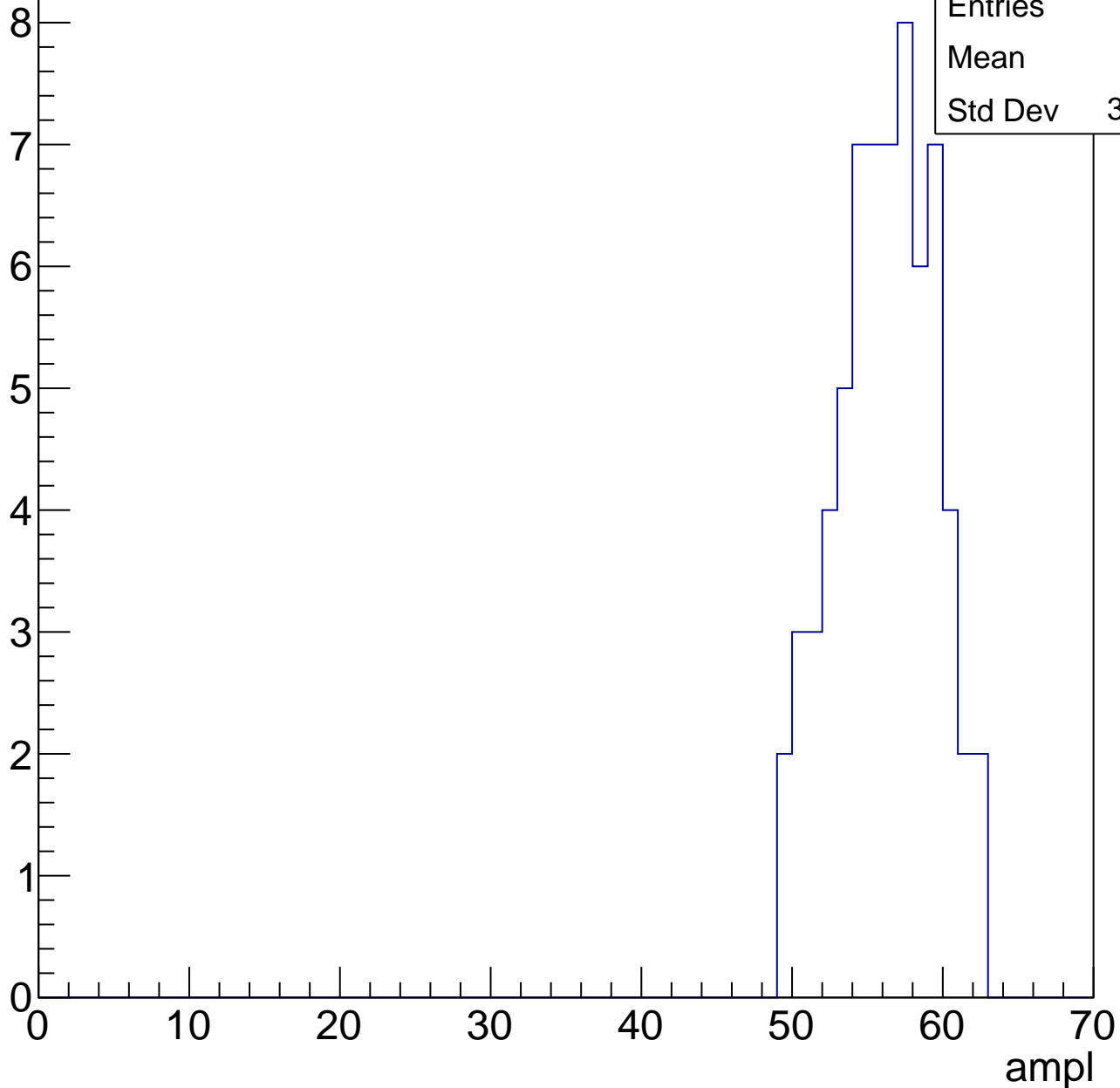
Entry



# B0L000S, U26-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

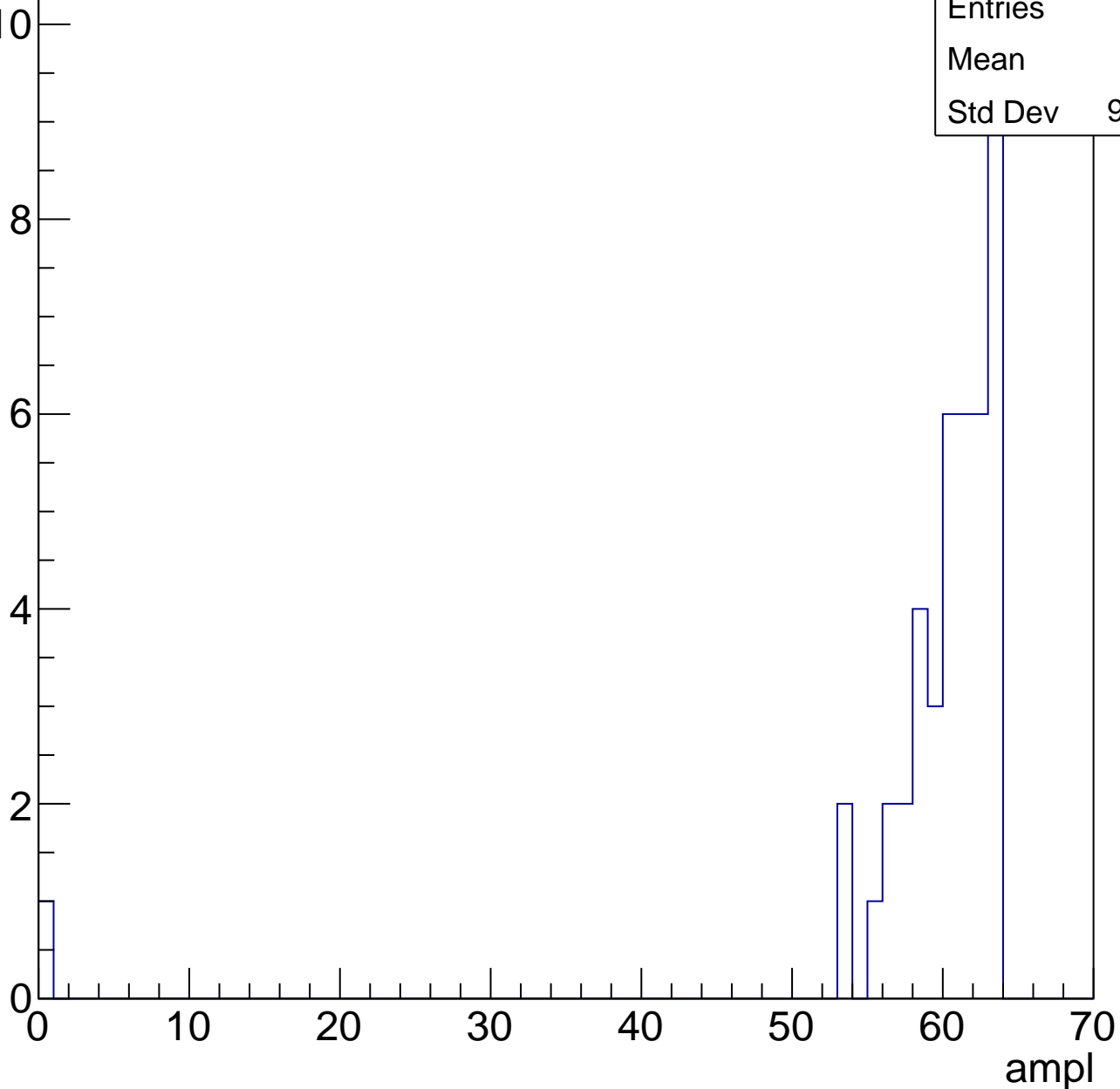


# B0L000S, U26-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

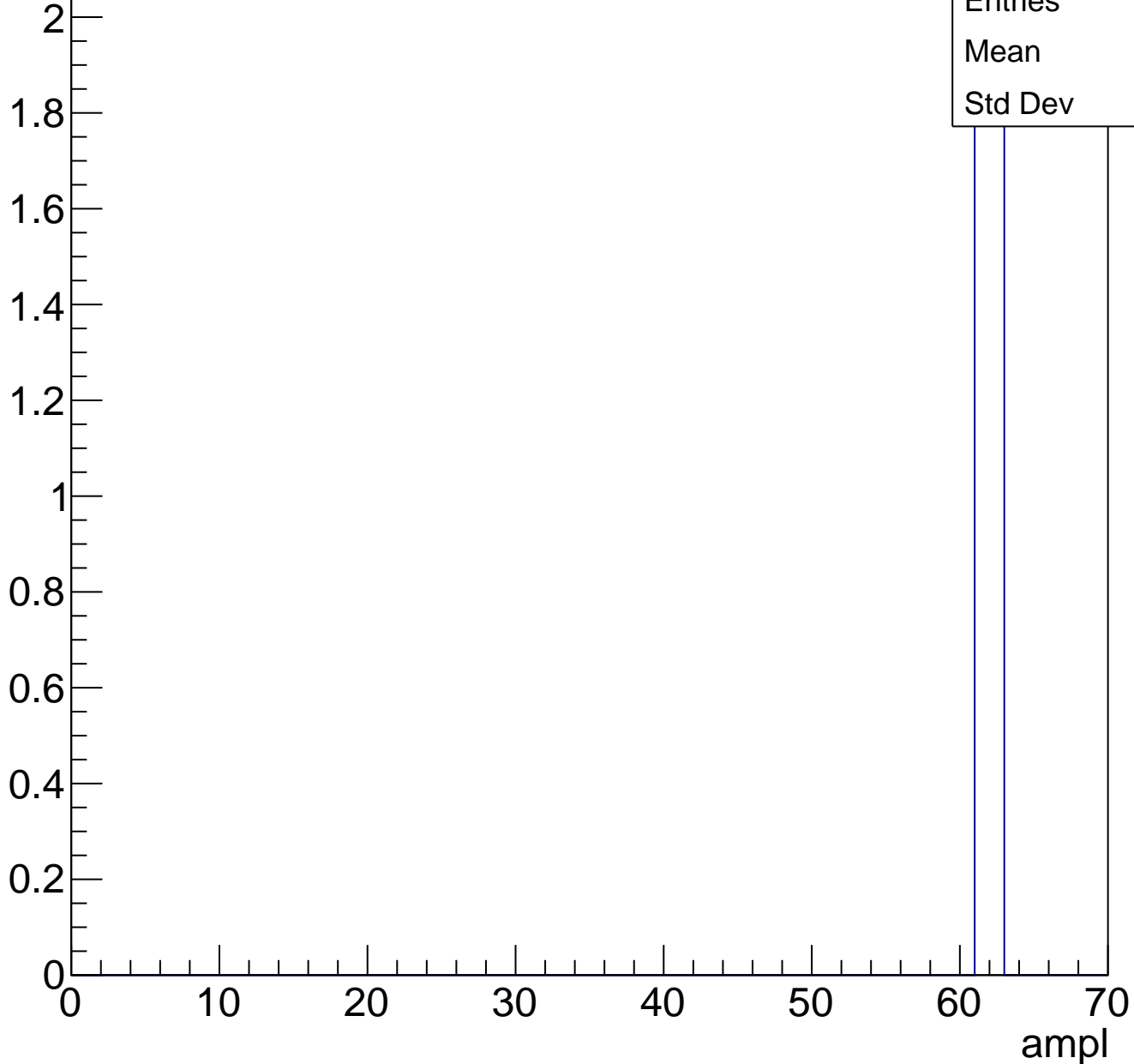
Entries	43
Mean	58.7
Std Dev	9.449



# B0L000S, U26-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch78, adc0

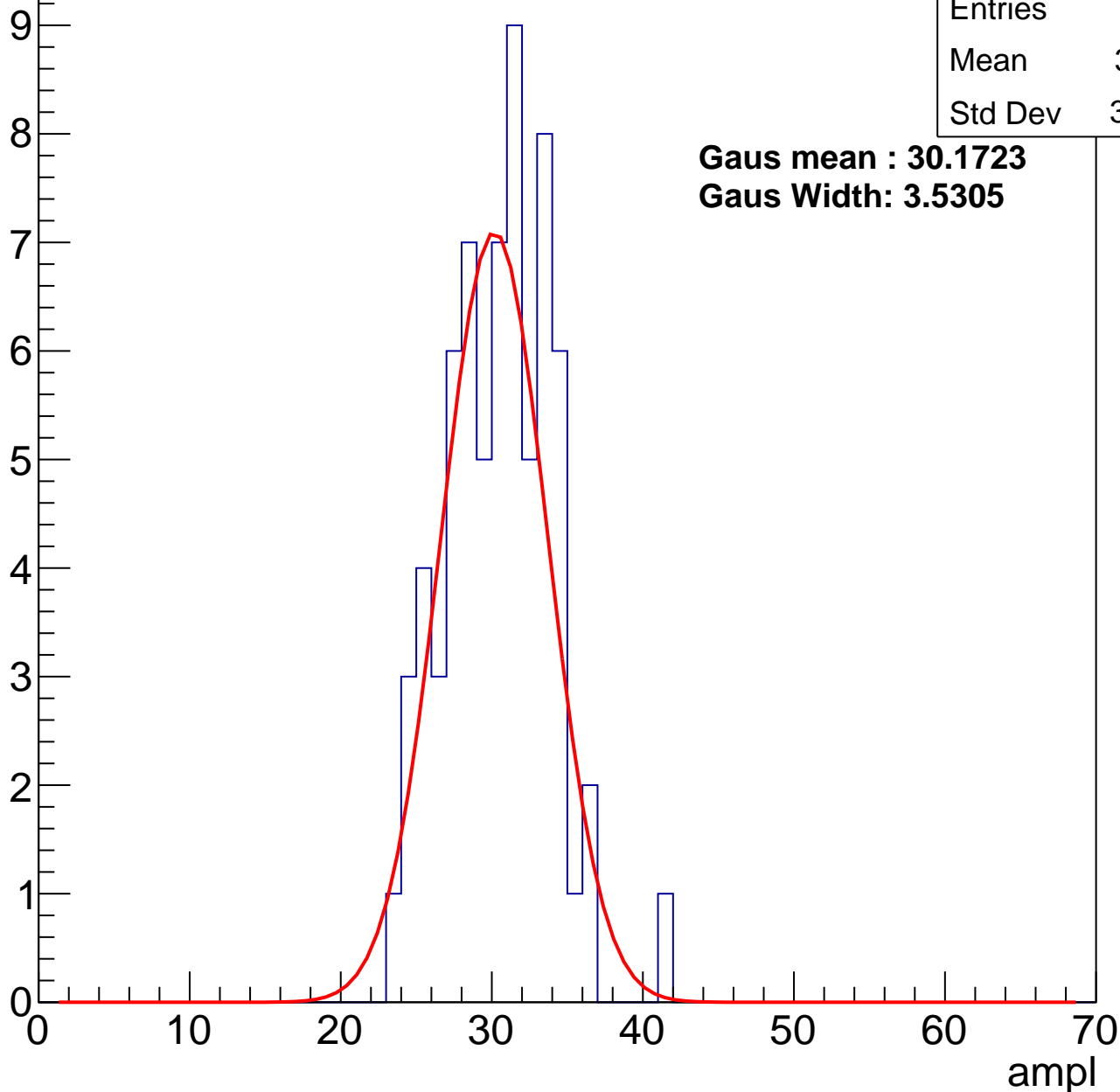
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.01
Std Dev	3.445

**Gaus mean : 30.1723**

**Gaus Width: 3.5305**



# B0L000S, U26-ch78, adc1

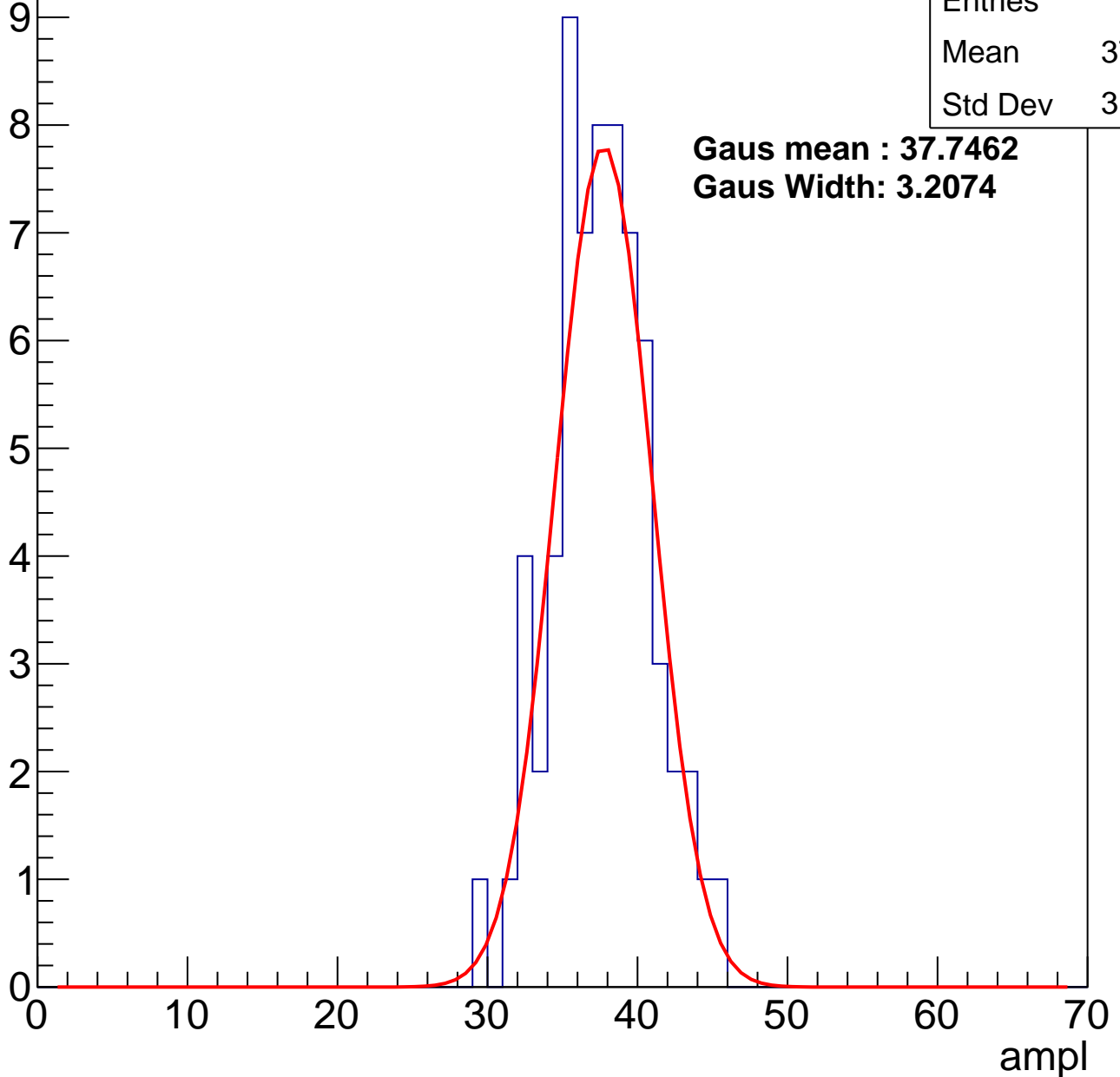
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.15
Std Dev	3.216

**Gaus mean : 37.7462**

**Gaus Width: 3.2074**



# B0L000S, U26-ch78, adc2

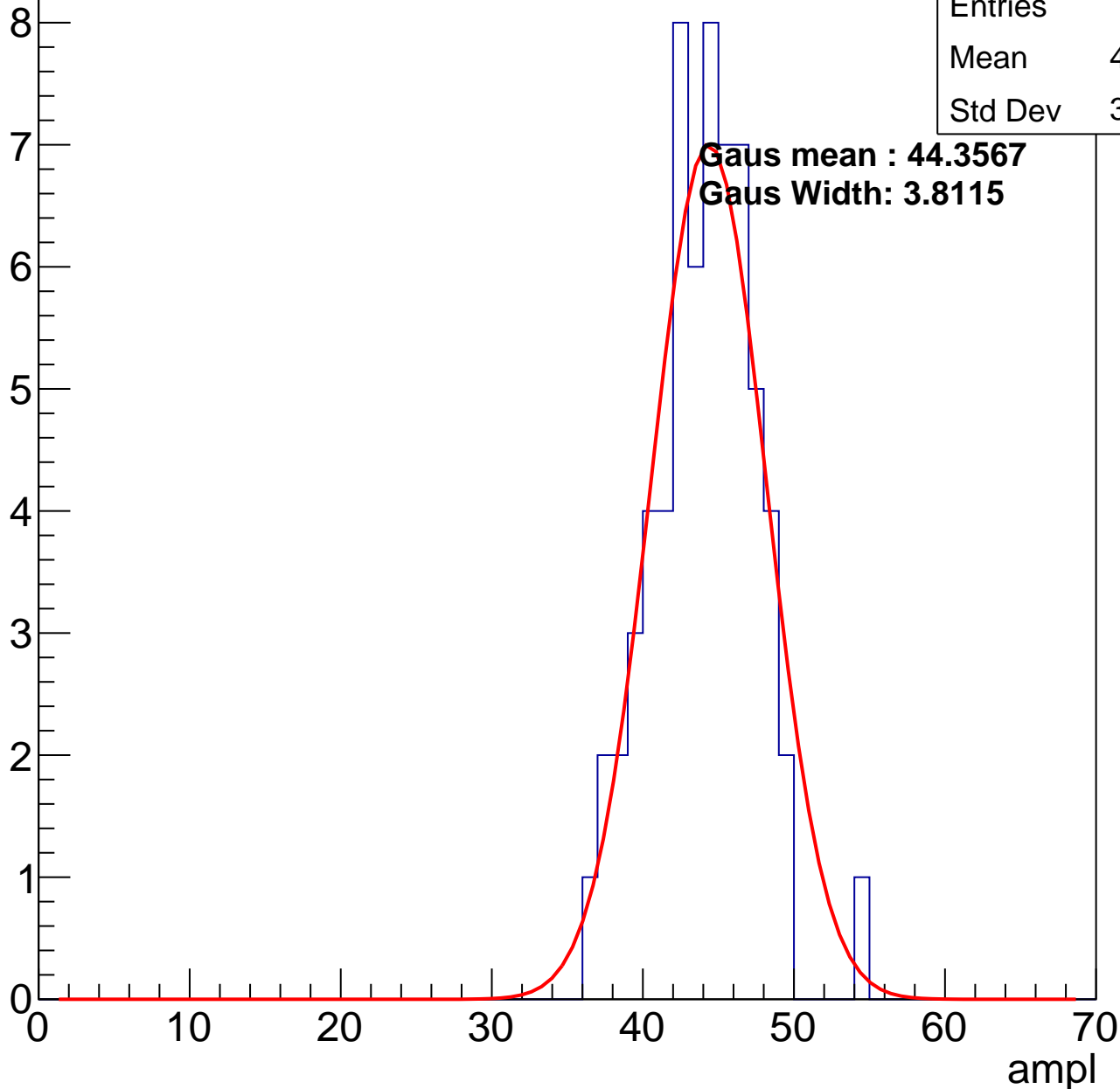
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.58
Std Dev	3.376

**Gaus mean : 44.3567**

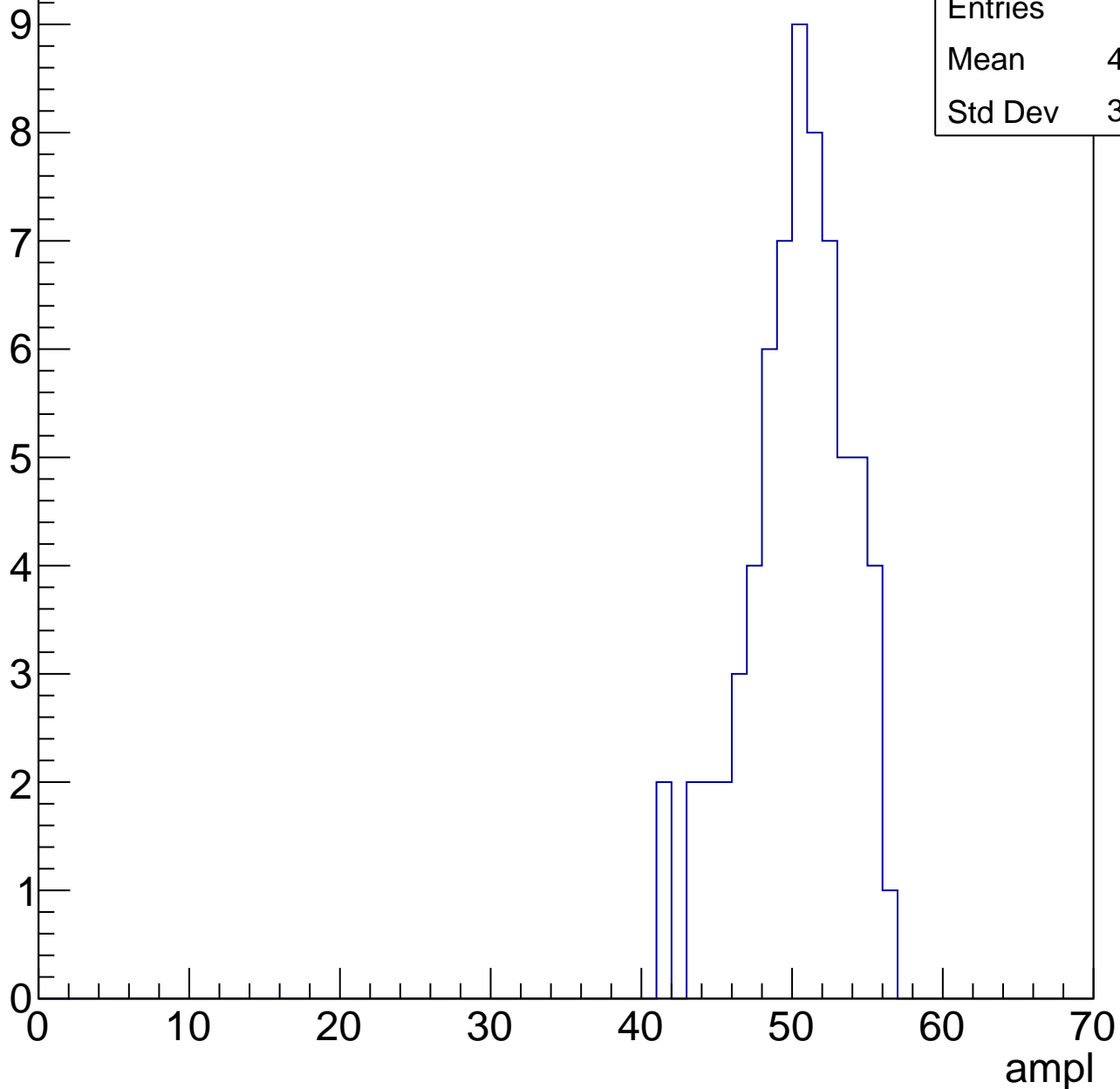
**Gaus Width: 3.8115**



# B0L000S, U26-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

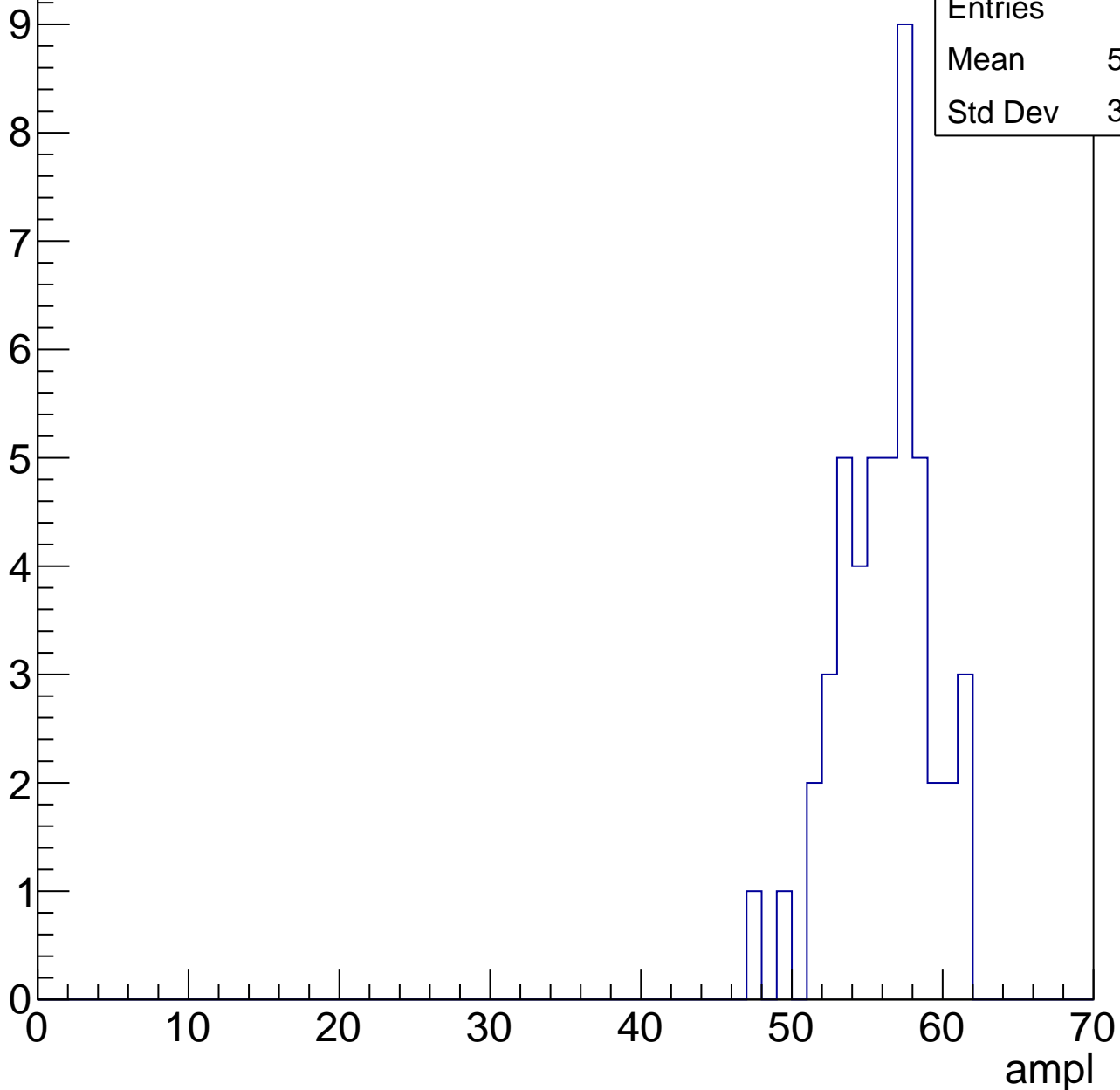


# B0L000S, U26-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	55.62
Std Dev	3.064

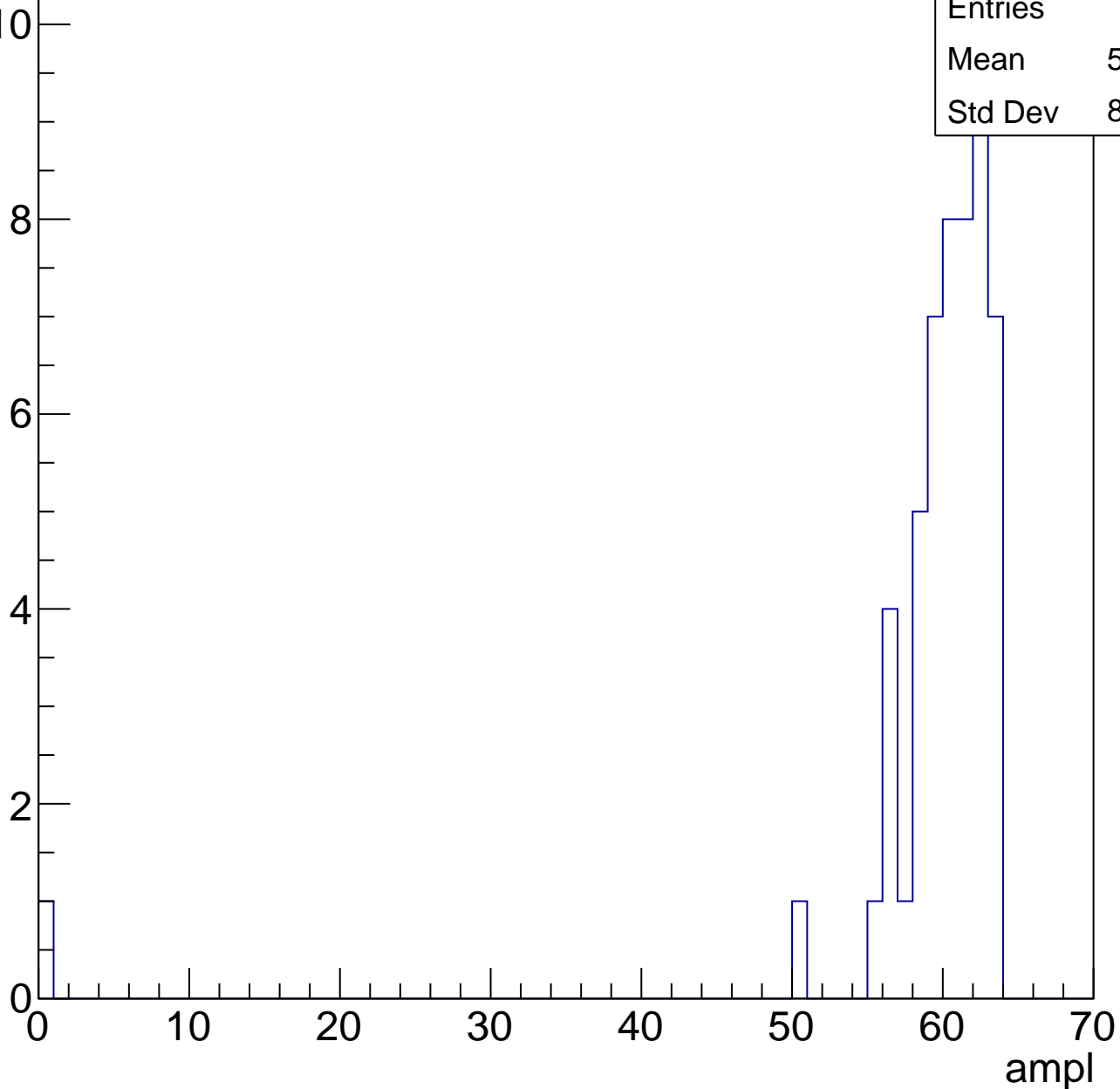


# B0L000S, U26-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

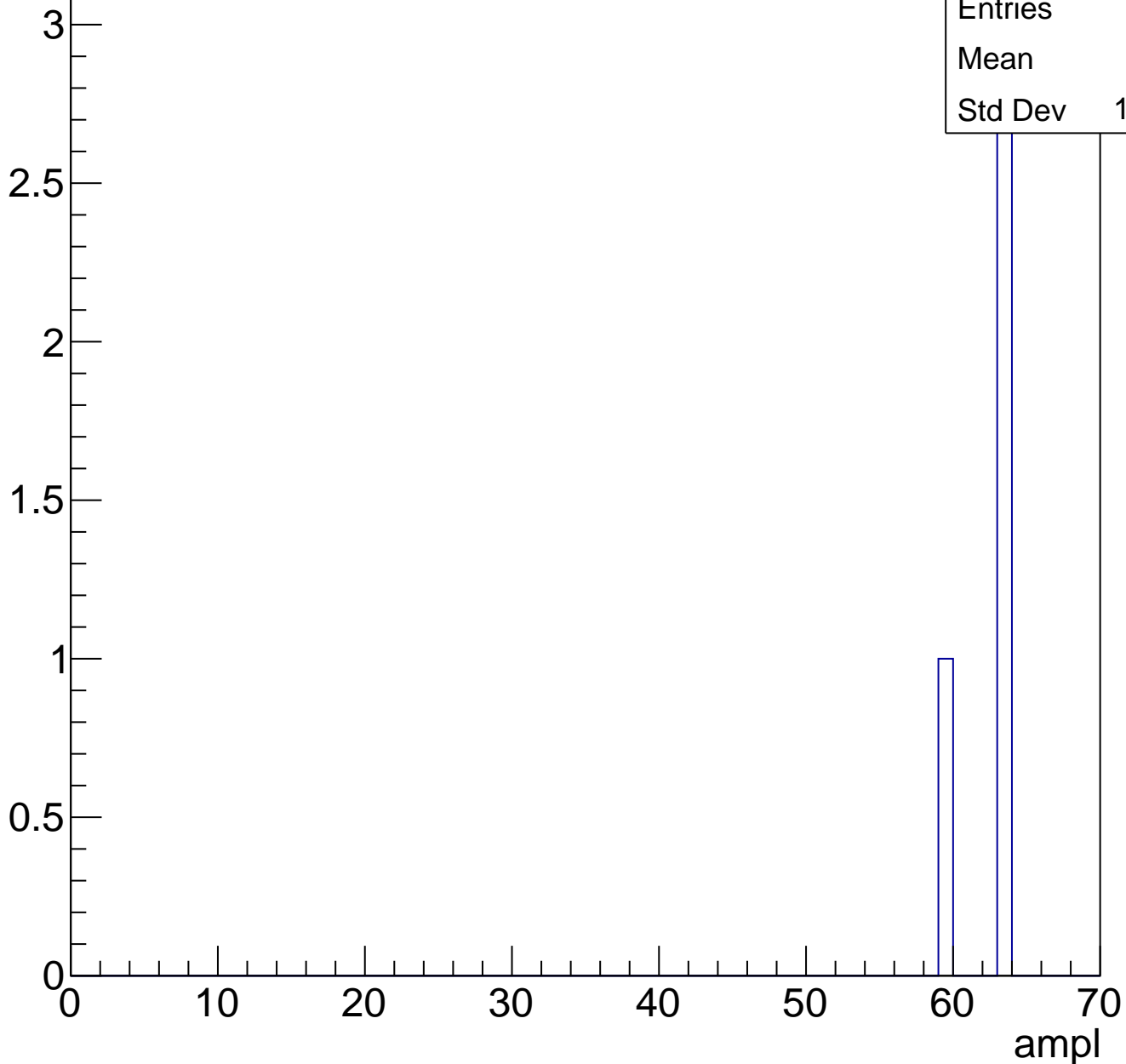
Entries	53
Mean	58.83
Std Dev	8.538



# B0L000S, U26-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch79, adc0

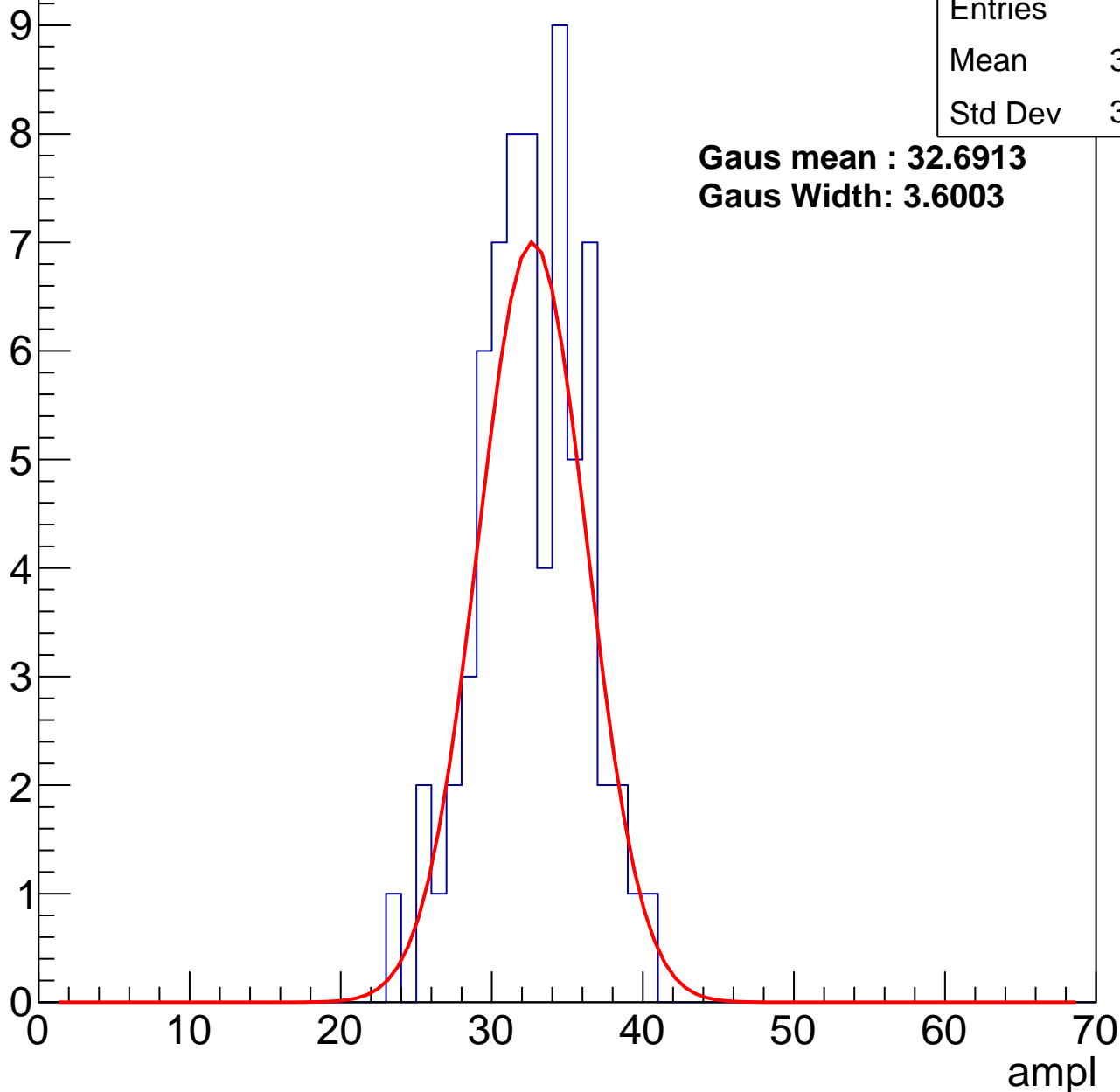
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	32.16
Std Dev	3.475

**Gaus mean : 32.6913**

**Gaus Width: 3.6003**



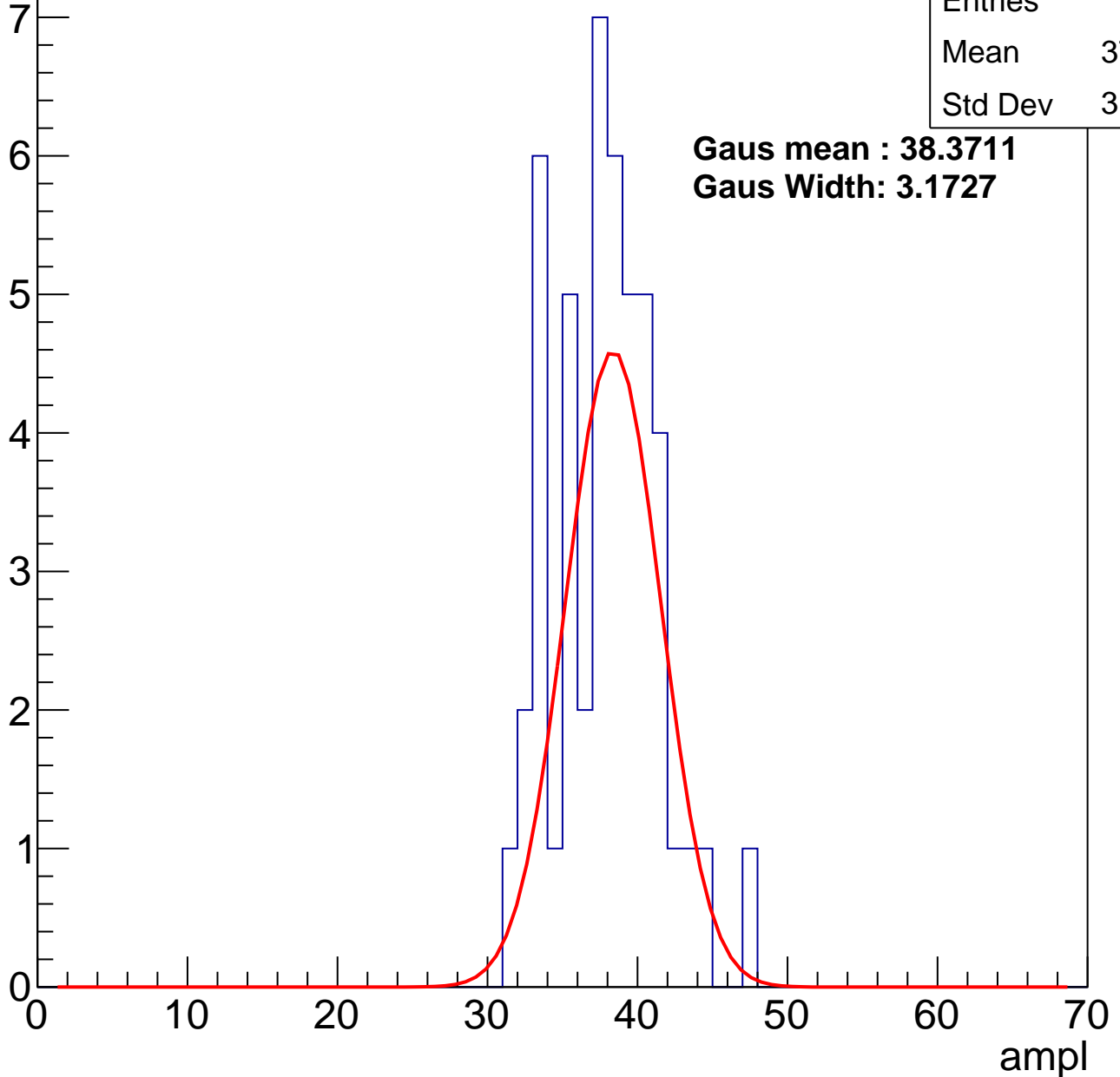
# B0L000S, U26-ch79, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	37.42
Std Dev	3.384

**Gaus mean : 38.3711**  
**Gaus Width: 3.1727**



# B0L000S, U26-ch79, adc2

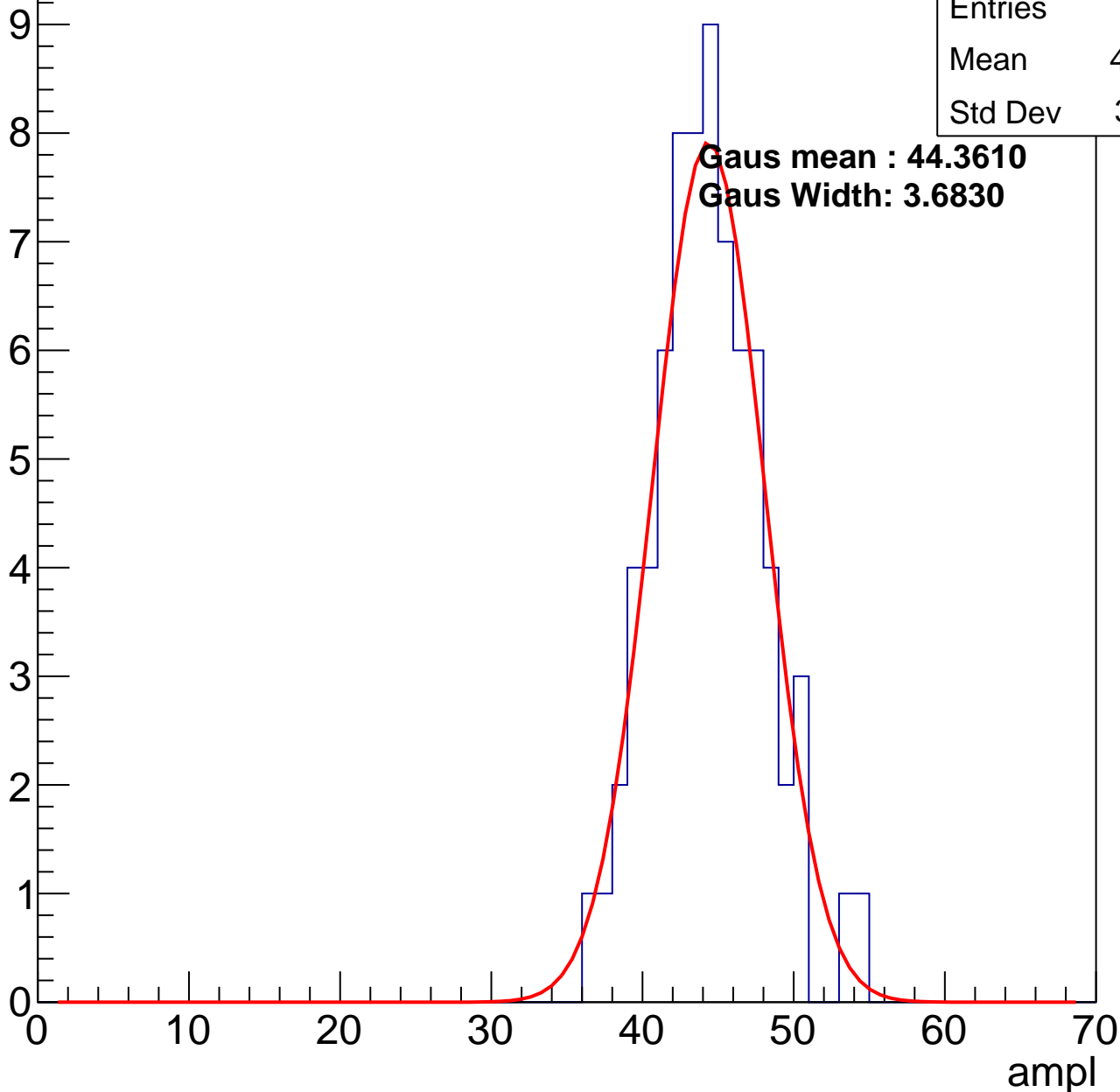
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	43.93
Std Dev	3.571

**Gaus mean : 44.3610**

**Gaus Width: 3.6830**

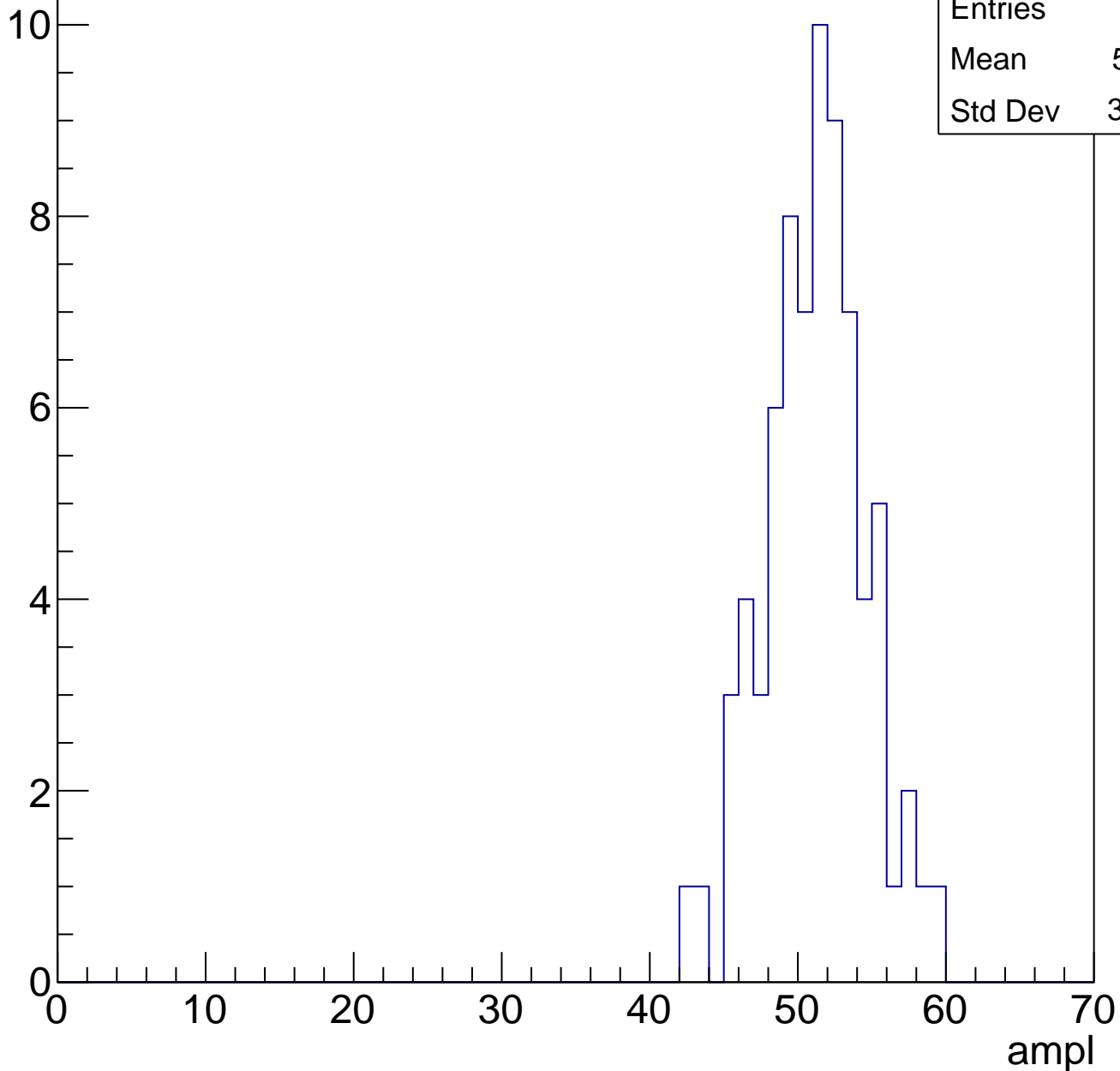


# B0L000S, U26-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	50.71
Std Dev	3.414

Entry

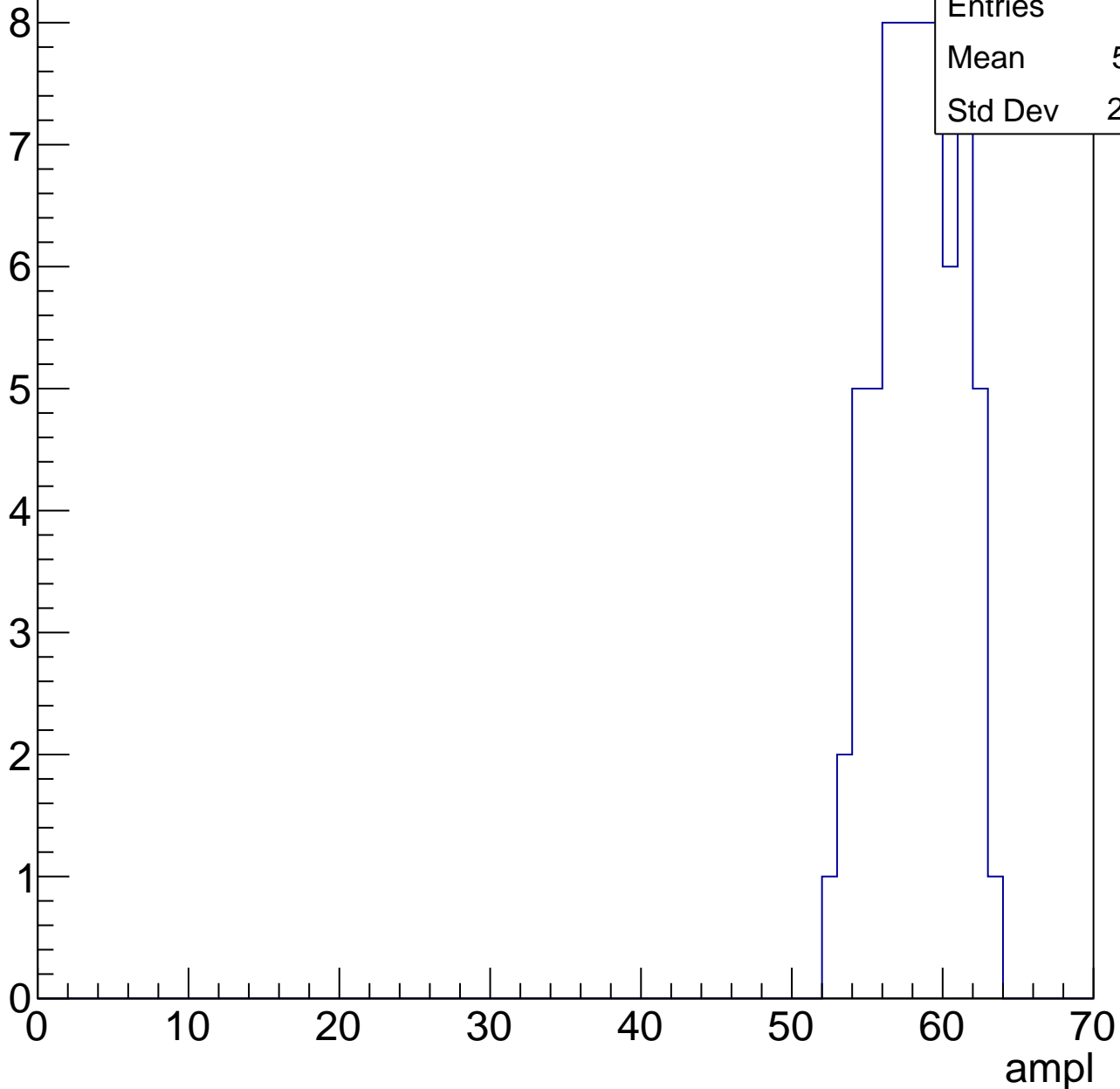


# B0L000S, U26-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	57.91
Std Dev	2.659

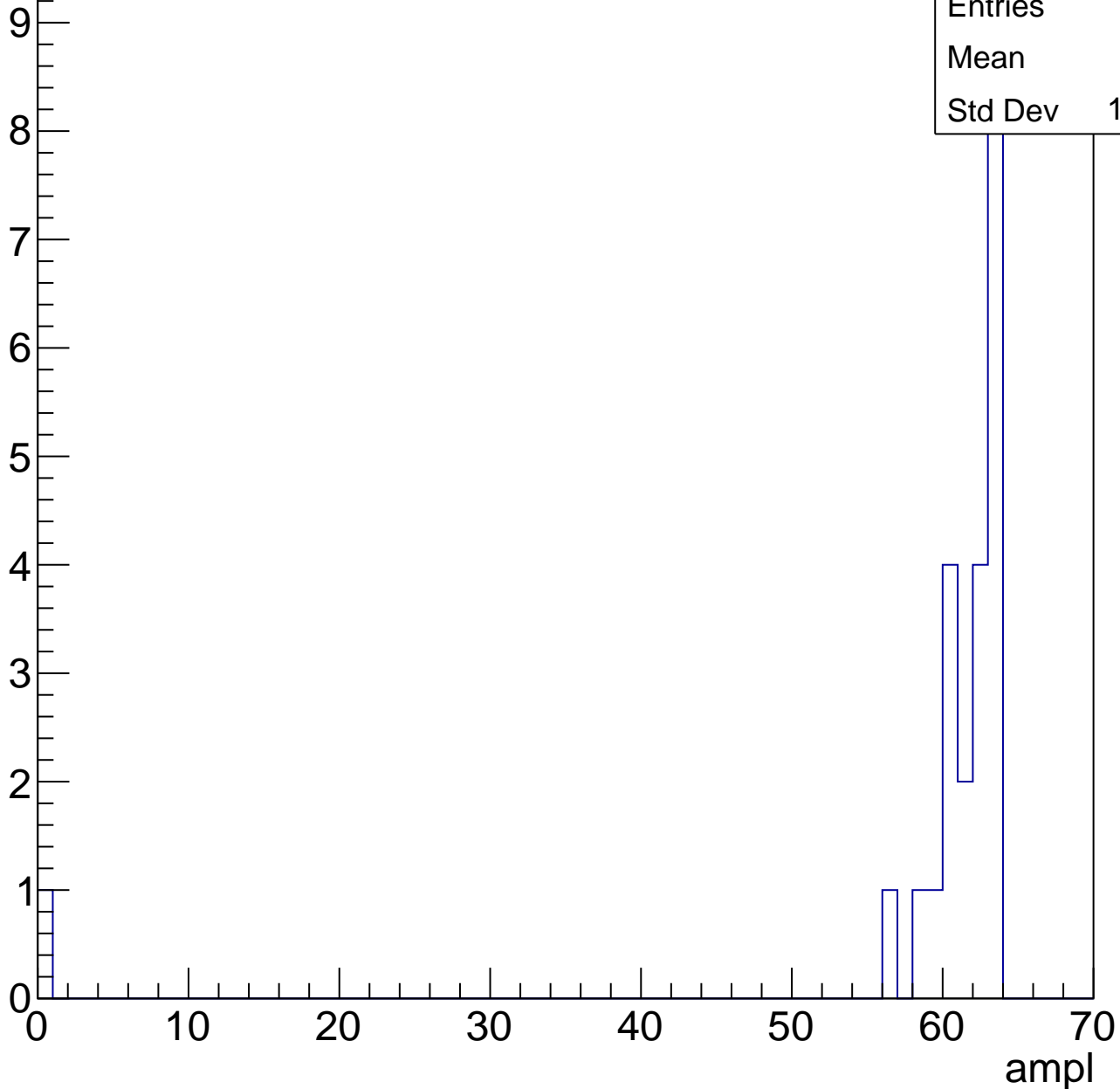


# B0L000S, U26-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	58.7
Std Dev	12.65



# B0L000S, U26-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	62
Std Dev	0



# B0L000S, U26-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

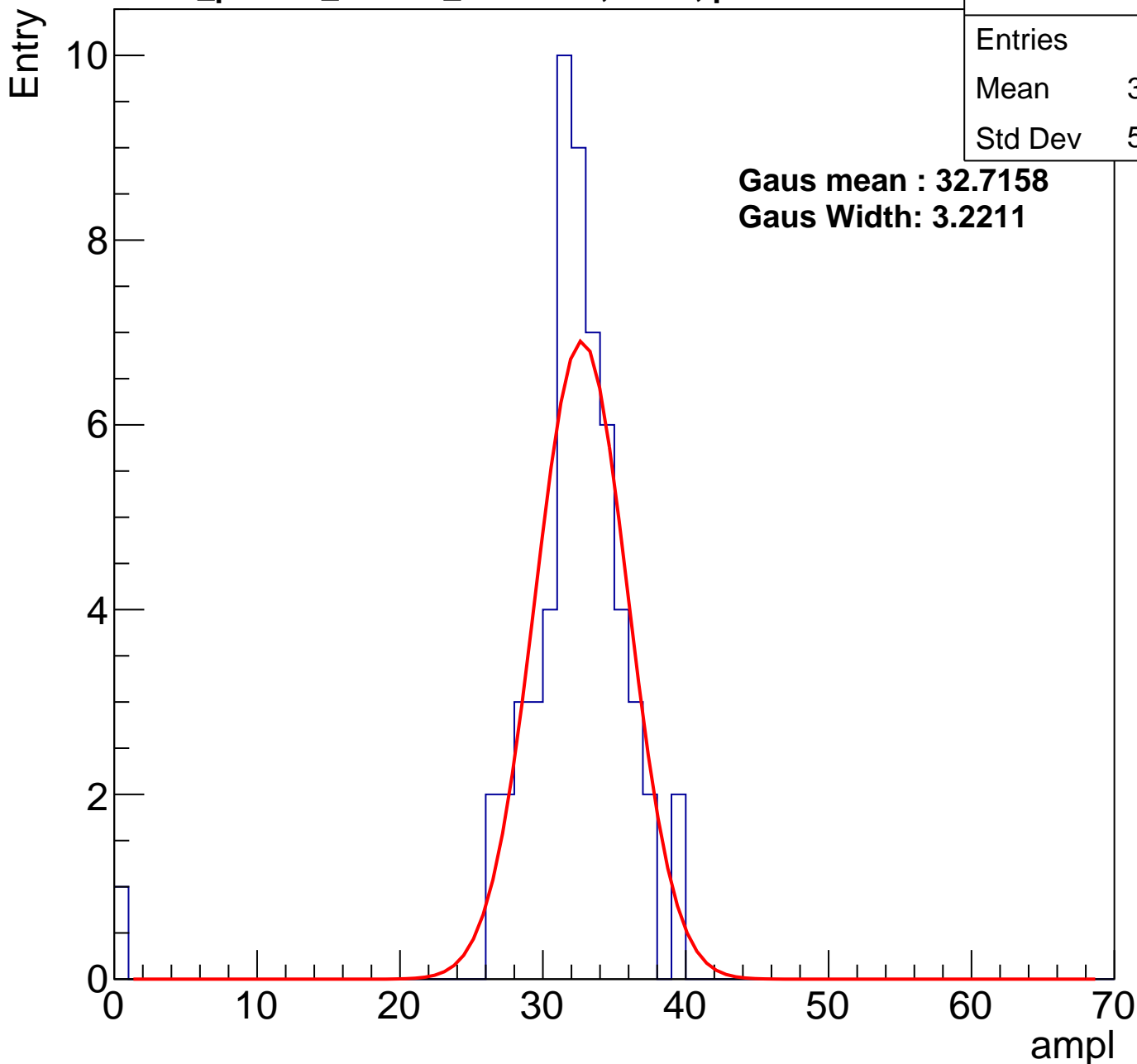
# B0L000S, U26-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	58
Mean	31.55
Std Dev	5.083

**Gaus mean : 32.7158**

**Gaus Width: 3.2211**



# B0L000S, U26-ch80, adc1

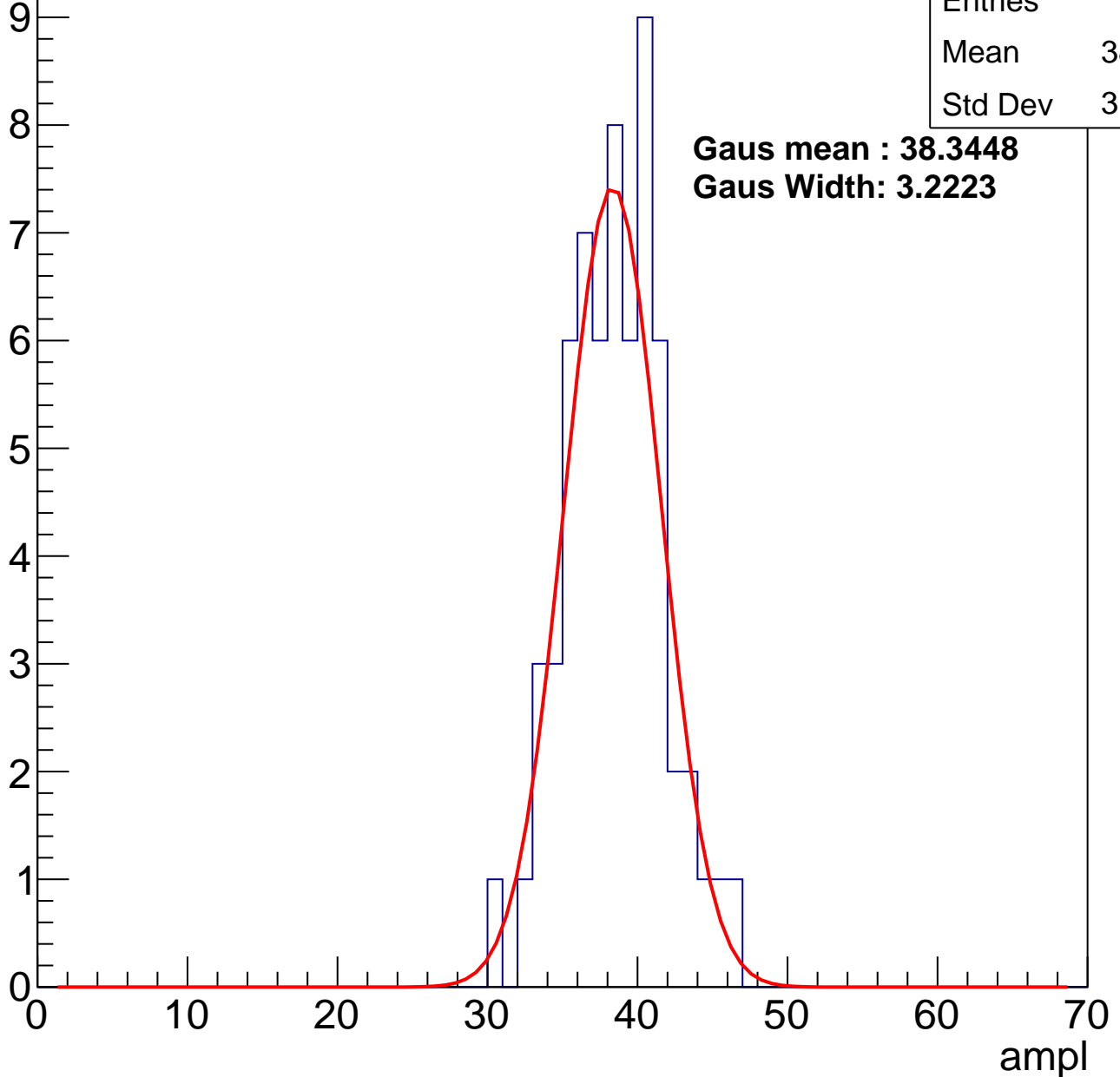
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	38.03
Std Dev	3.182

**Gaus mean : 38.3448**

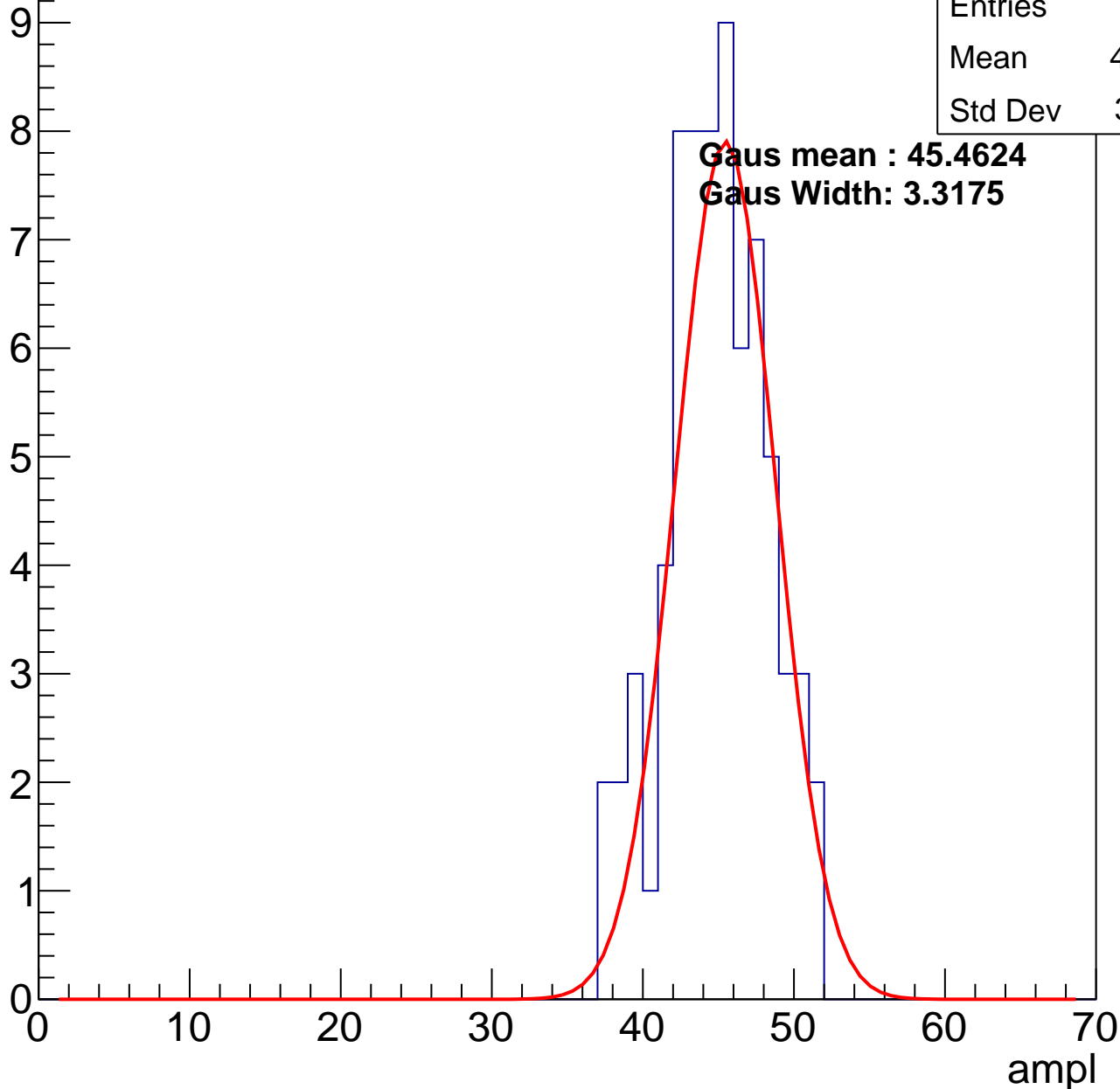
**Gaus Width: 3.2223**



# B0L000S, U26-ch80, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

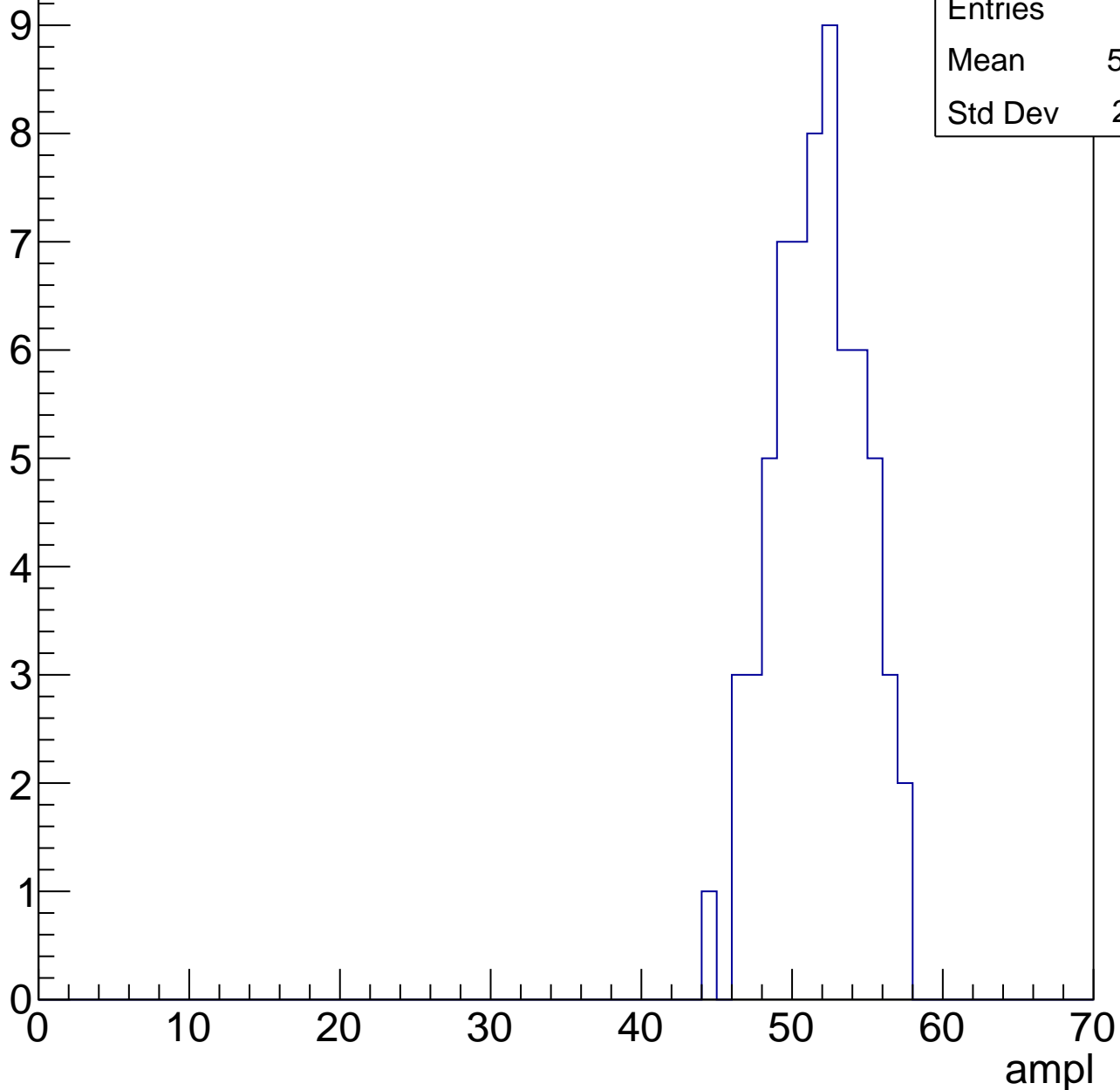
Entry



# B0L000S, U26-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

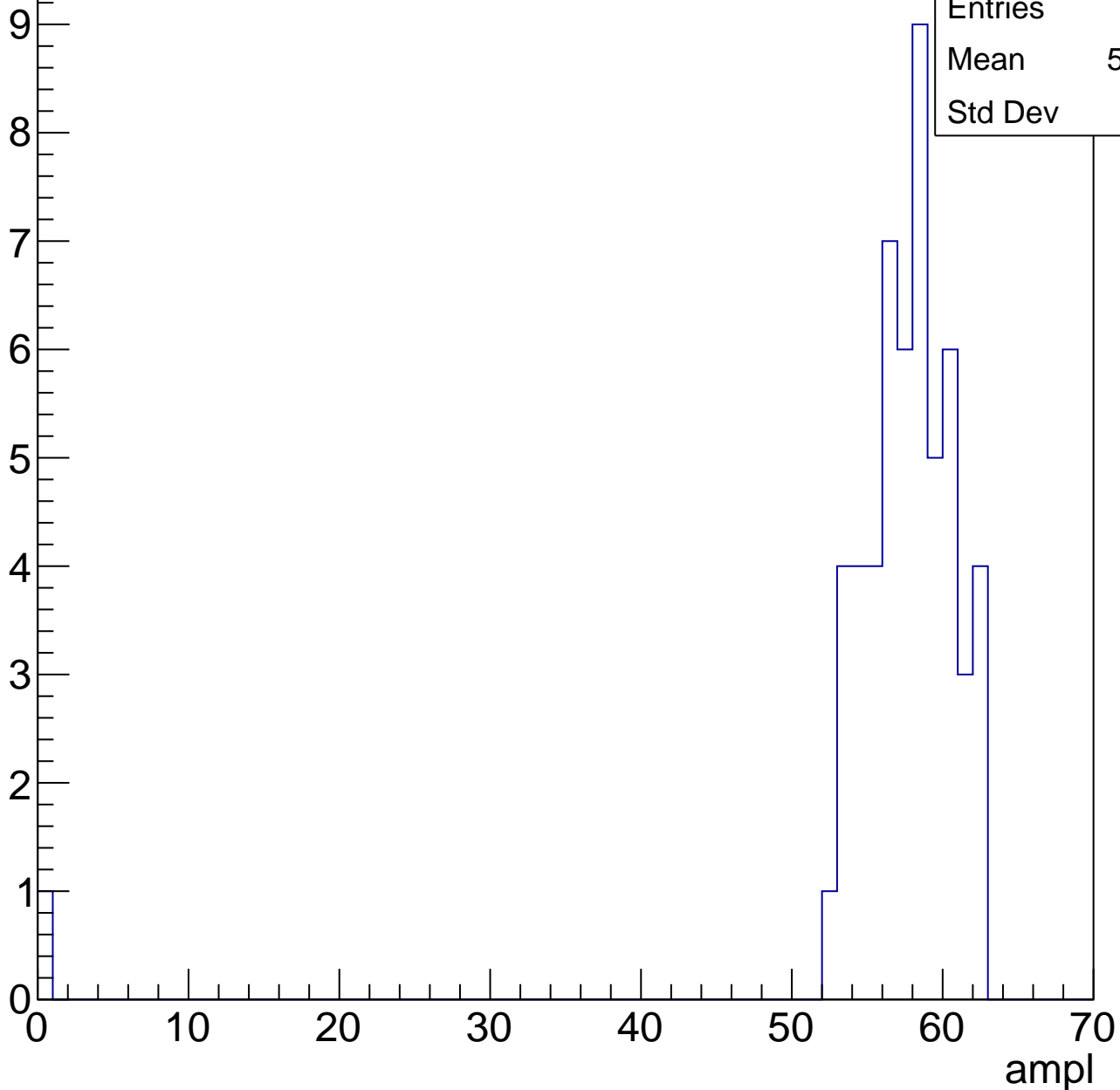
Entry



# B0L000S, U26-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	35
Mean	60.86
Std Dev	2.044

Entry

10

8

6

4

2

0

0

10

20

30

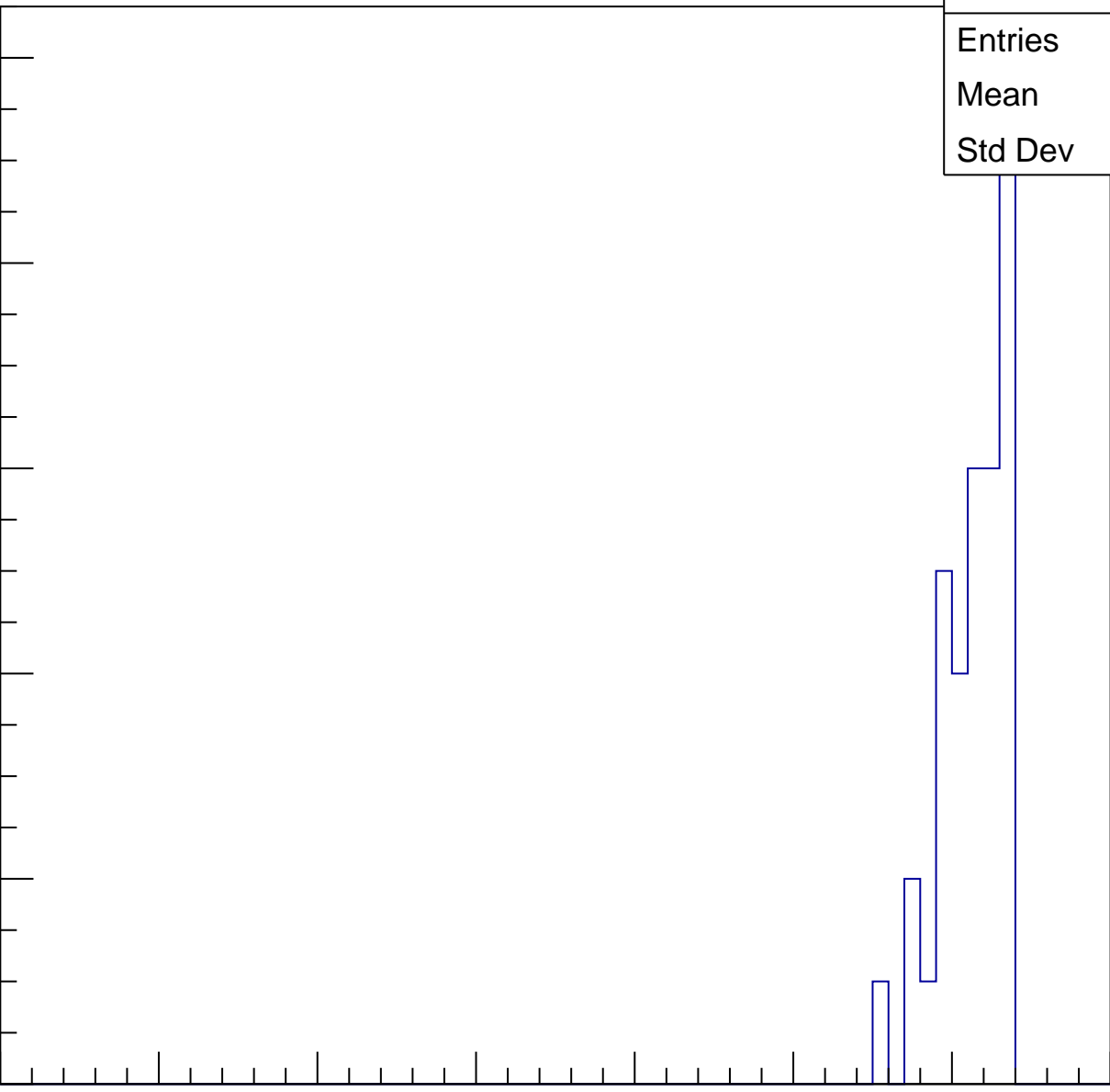
40

50

60

ampl

70



# B0L000S, U26-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U26-ch81, adc0

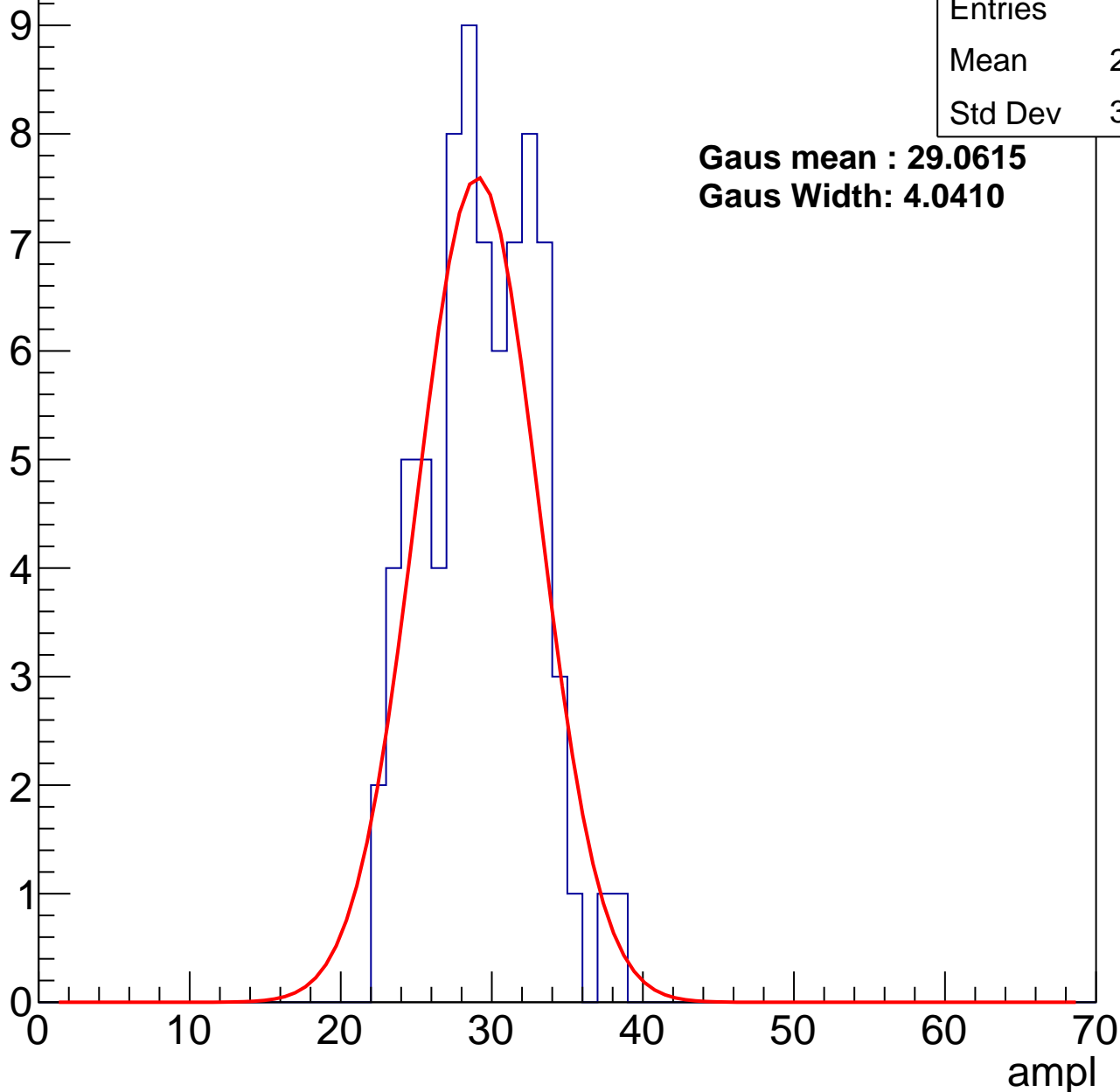
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.87
Std Dev	3.575

**Gaus mean : 29.0615**

**Gaus Width: 4.0410**



# B0L000S, U26-ch81, adc1

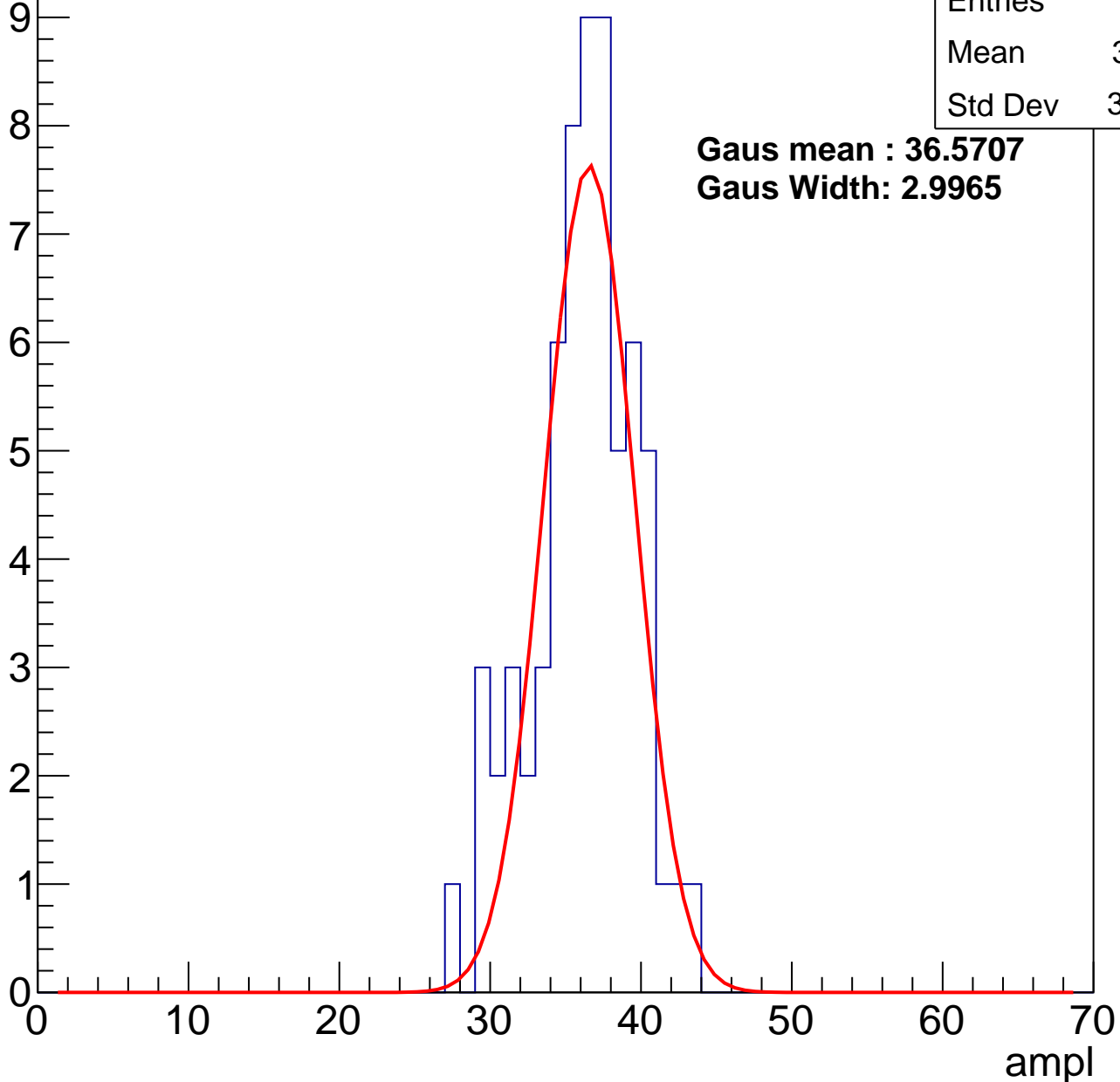
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.71
Std Dev	3.359

**Gaus mean : 36.5707**

**Gaus Width: 2.9965**



# B0L000S, U26-ch81, adc2

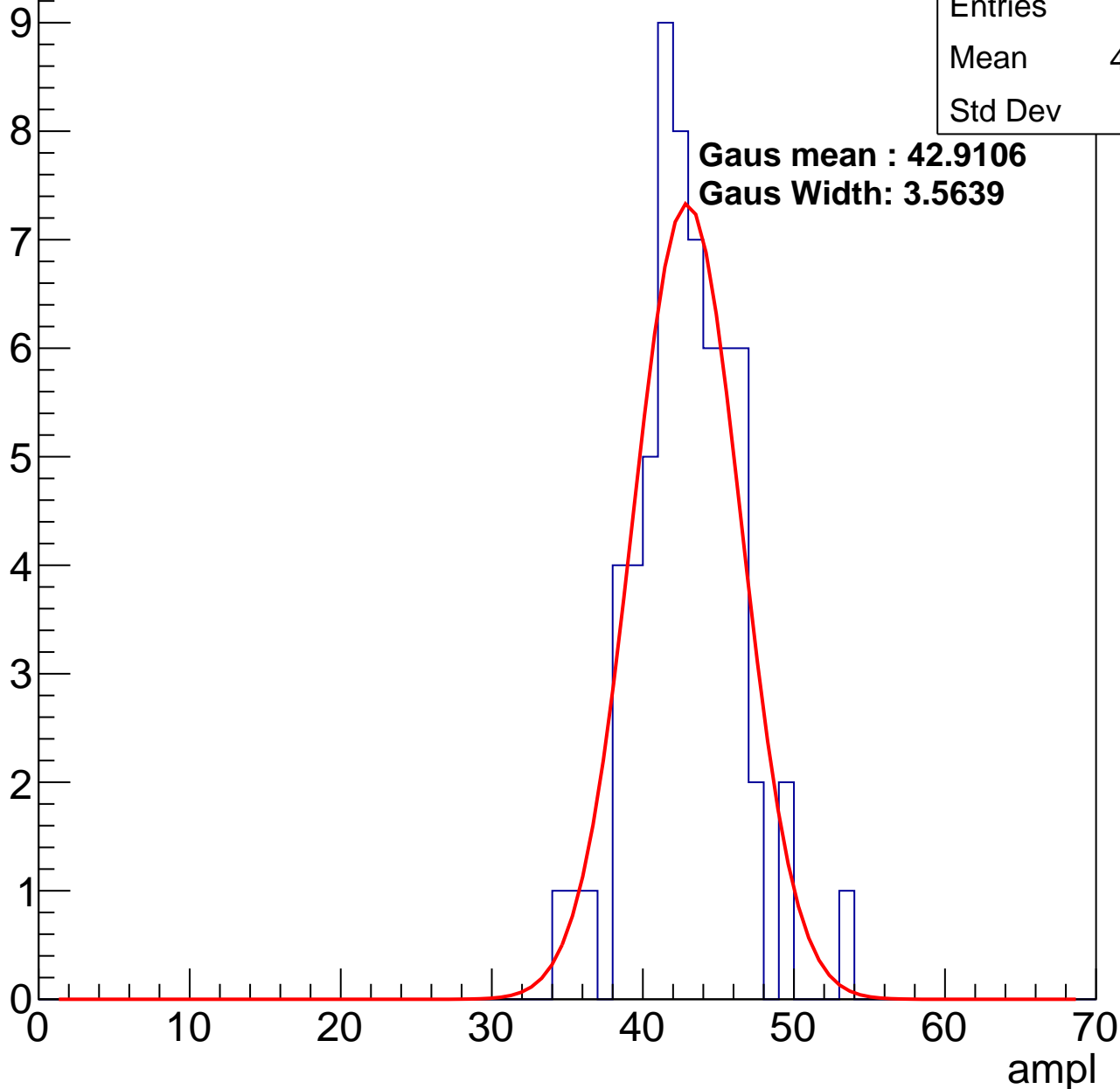
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.44
Std Dev	3.37

**Gaus mean : 42.9106**

**Gaus Width: 3.5639**



# B0L000S, U26-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

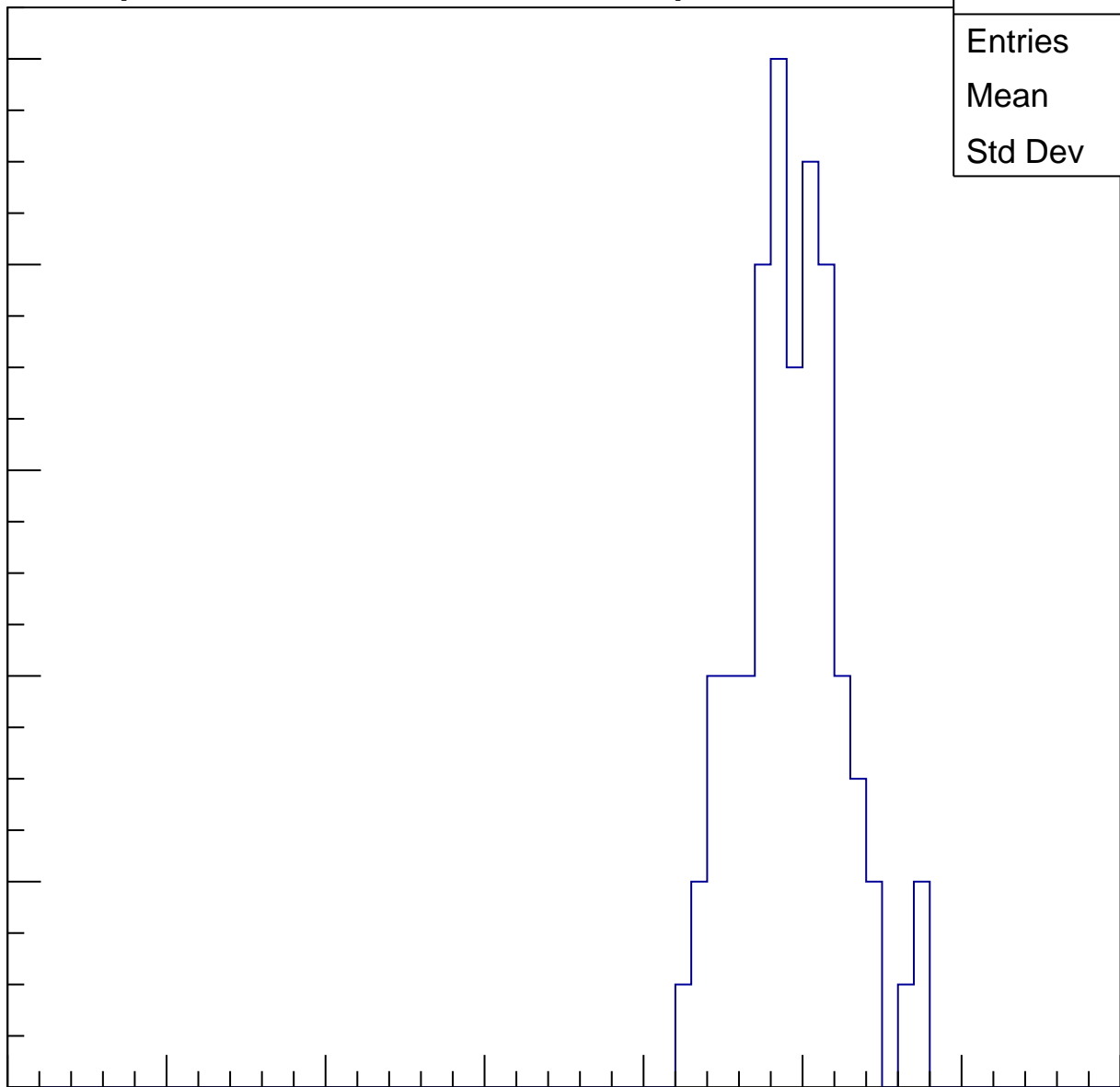
Entries	69
Mean	48.84
Std Dev	3.215

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

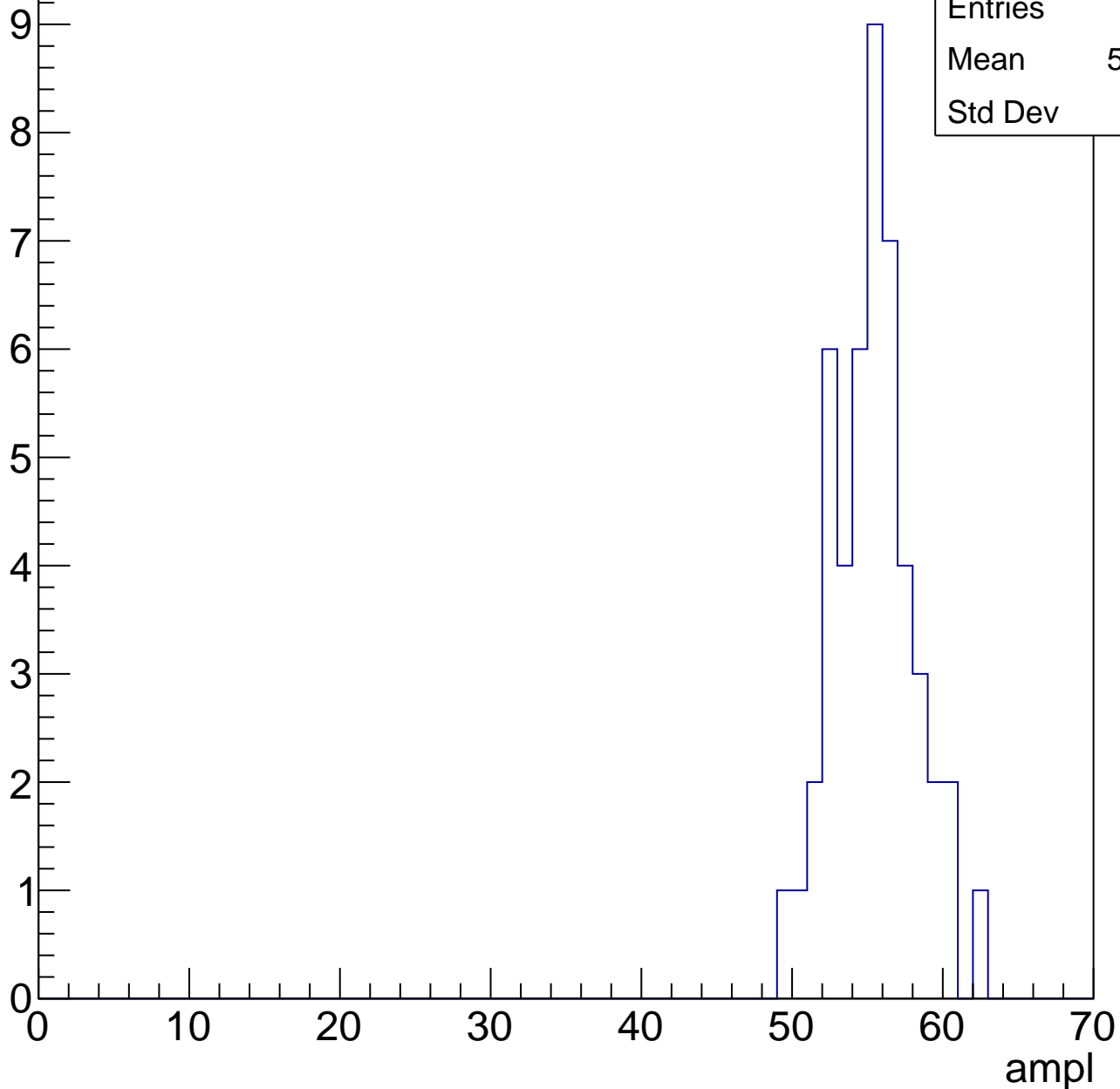
ampl



# B0L000S, U26-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	48
Mean	54.96
Std Dev	2.7

# B0L000S, U26-ch81, adc5

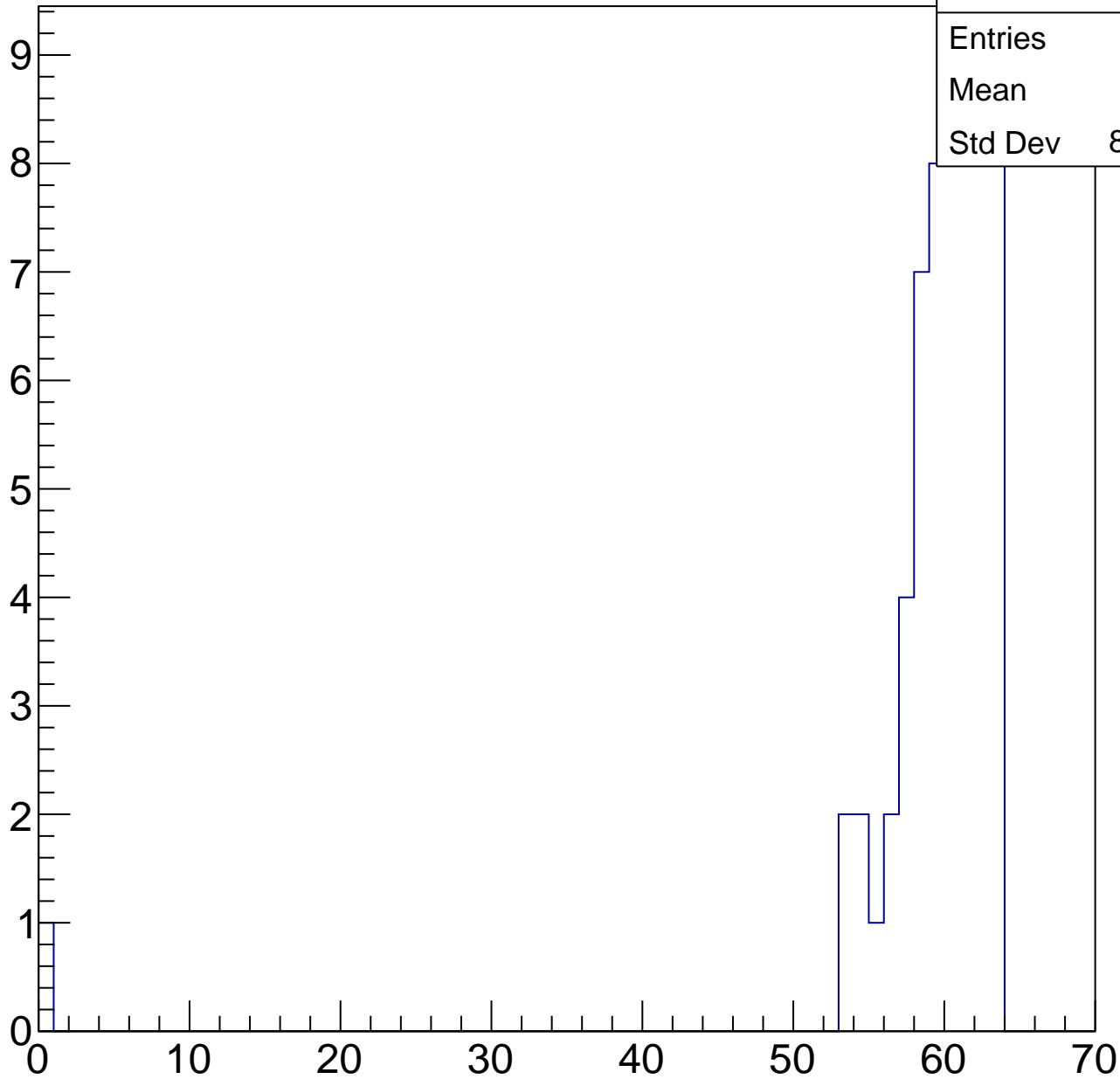
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	58.6
Std Dev	8.056

ampl



# B0L000S, U26-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	27.47
Std Dev	3.297

**Gaus mean : 27.6774**

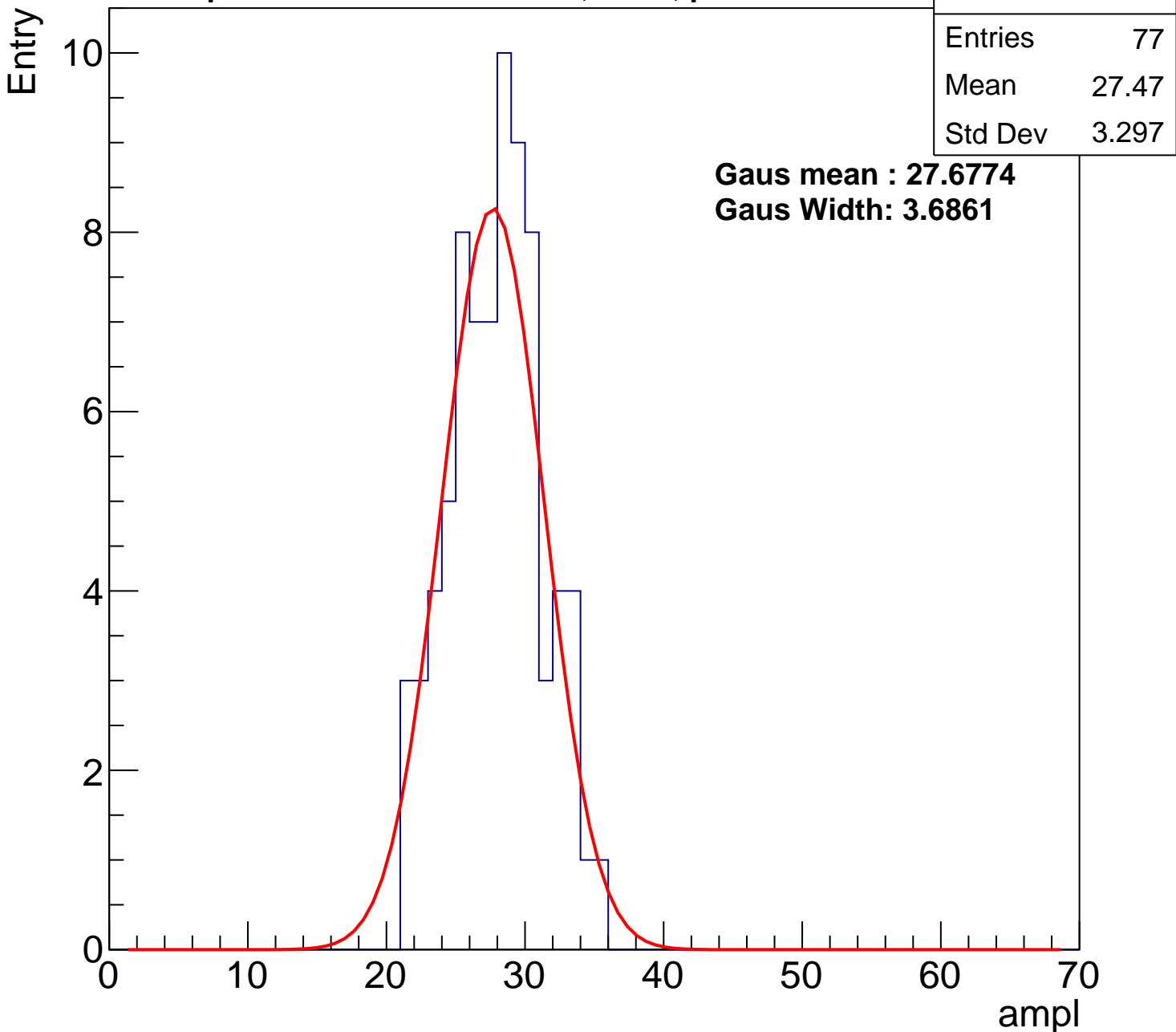
**Gaus Width: 3.6861**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch82, adc1

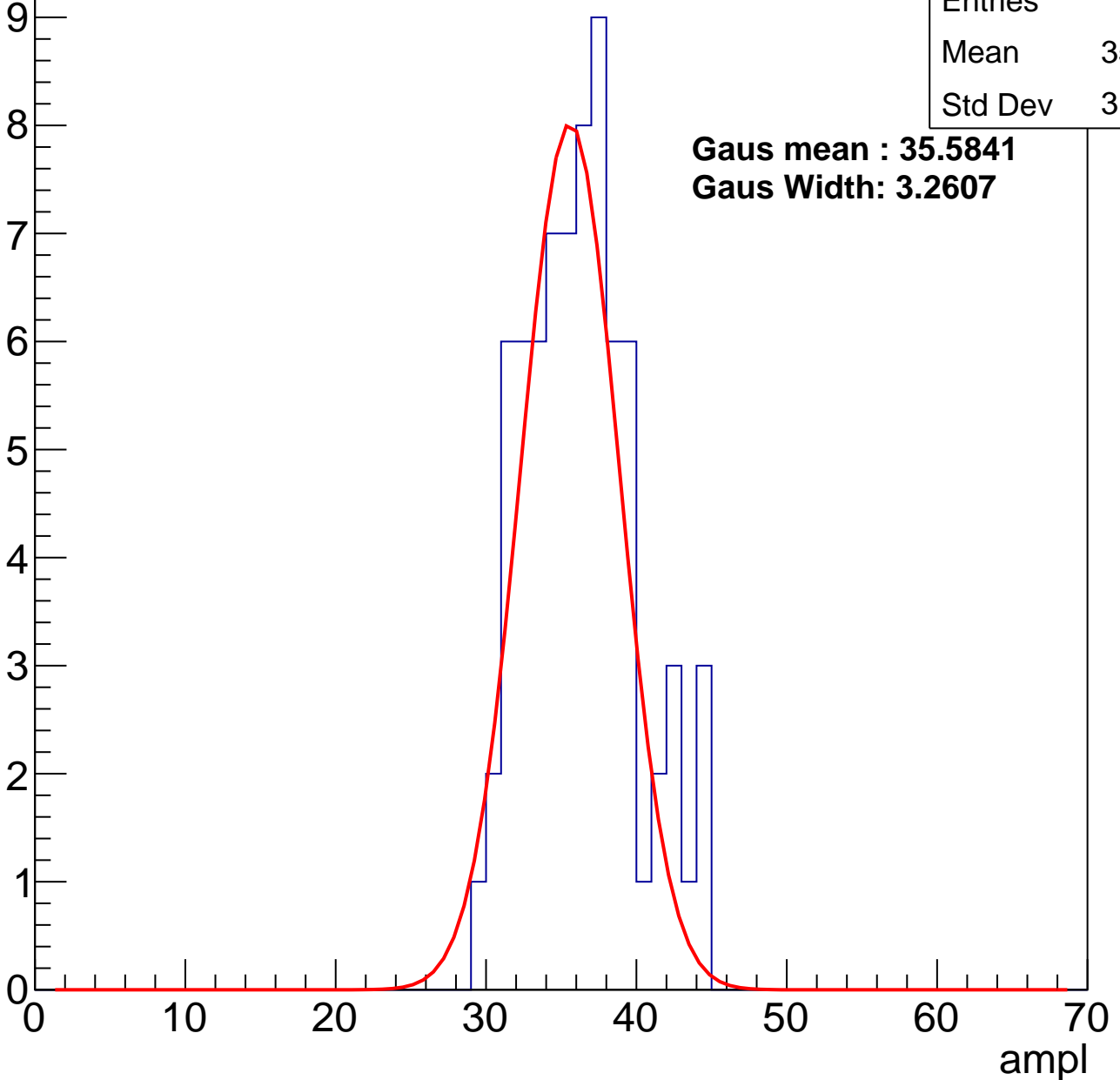
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	35.86
Std Dev	3.596

**Gaus mean : 35.5841**

**Gaus Width: 3.2607**



# B0L000S, U26-ch82, adc2

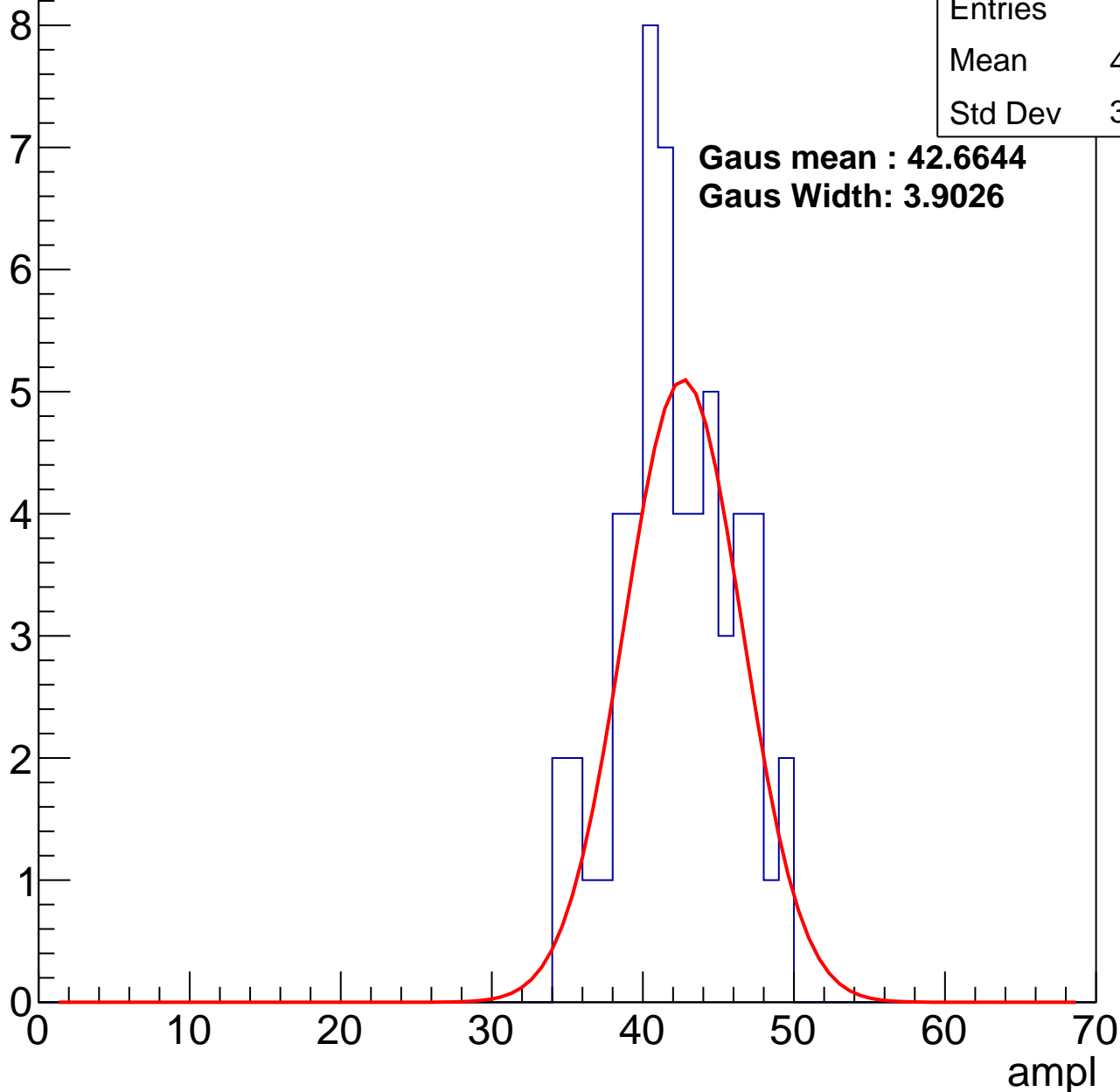
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	41.77
Std Dev	3.713

**Gaus mean : 42.6644**

**Gaus Width: 3.9026**

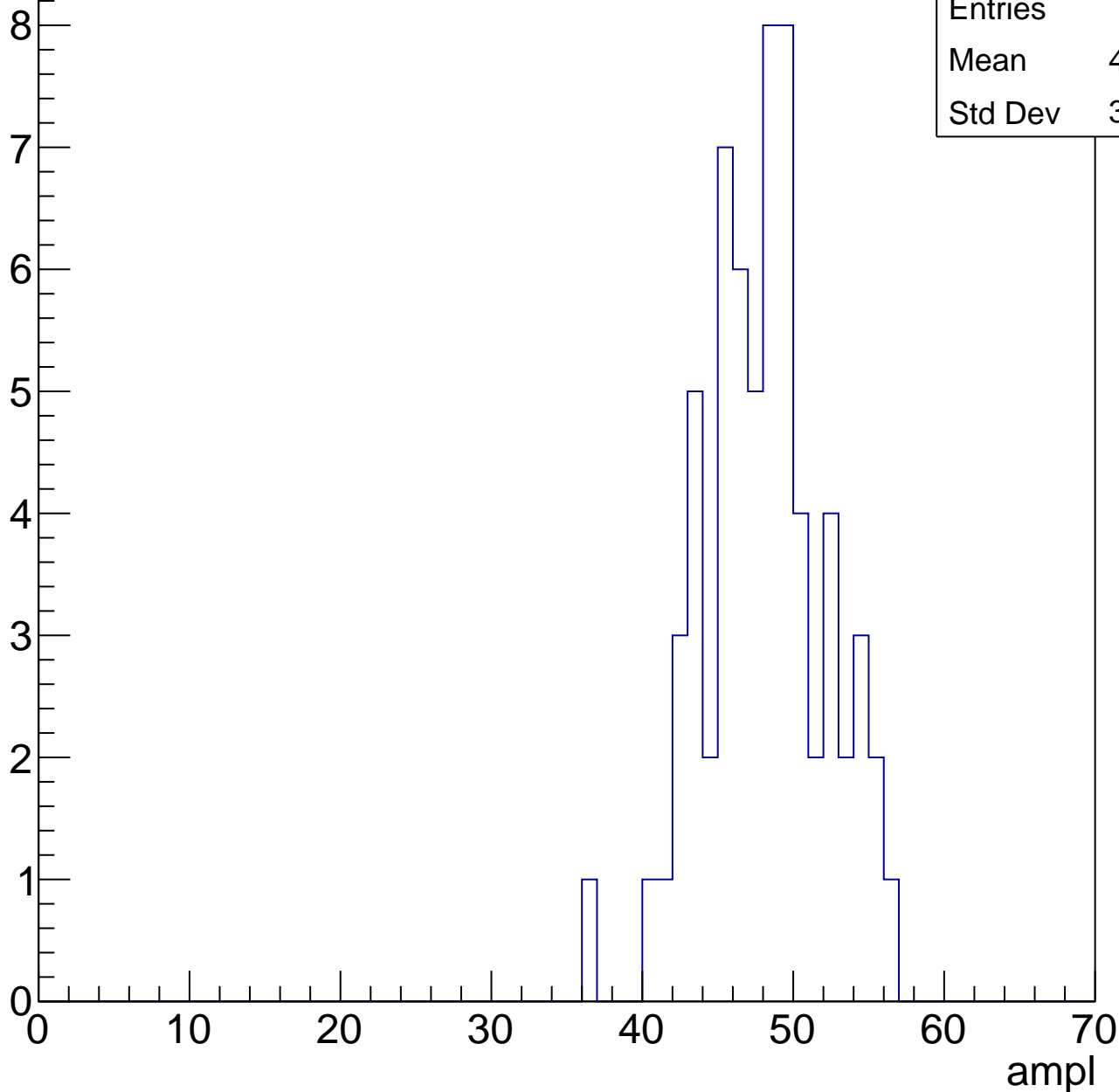


# B0L000S, U26-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	47.57
Std Dev	3.977

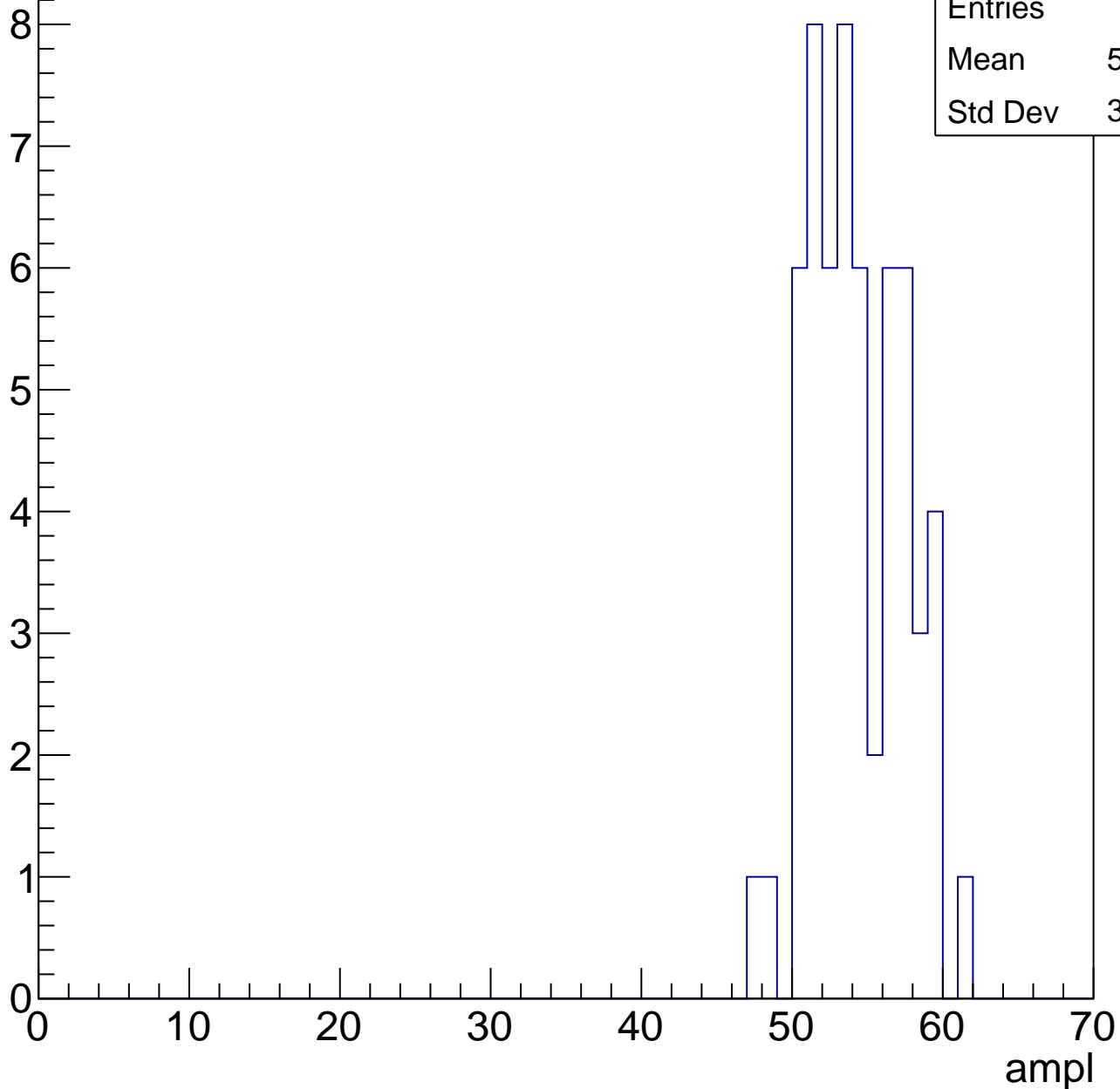


# B0L000S, U26-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	53.83
Std Dev	3.108



# B0L000S, U26-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

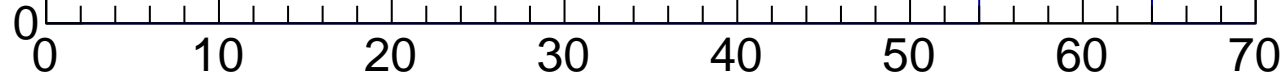
50

60

70

ampl

Entries	56
Mean	59.38
Std Dev	2.468

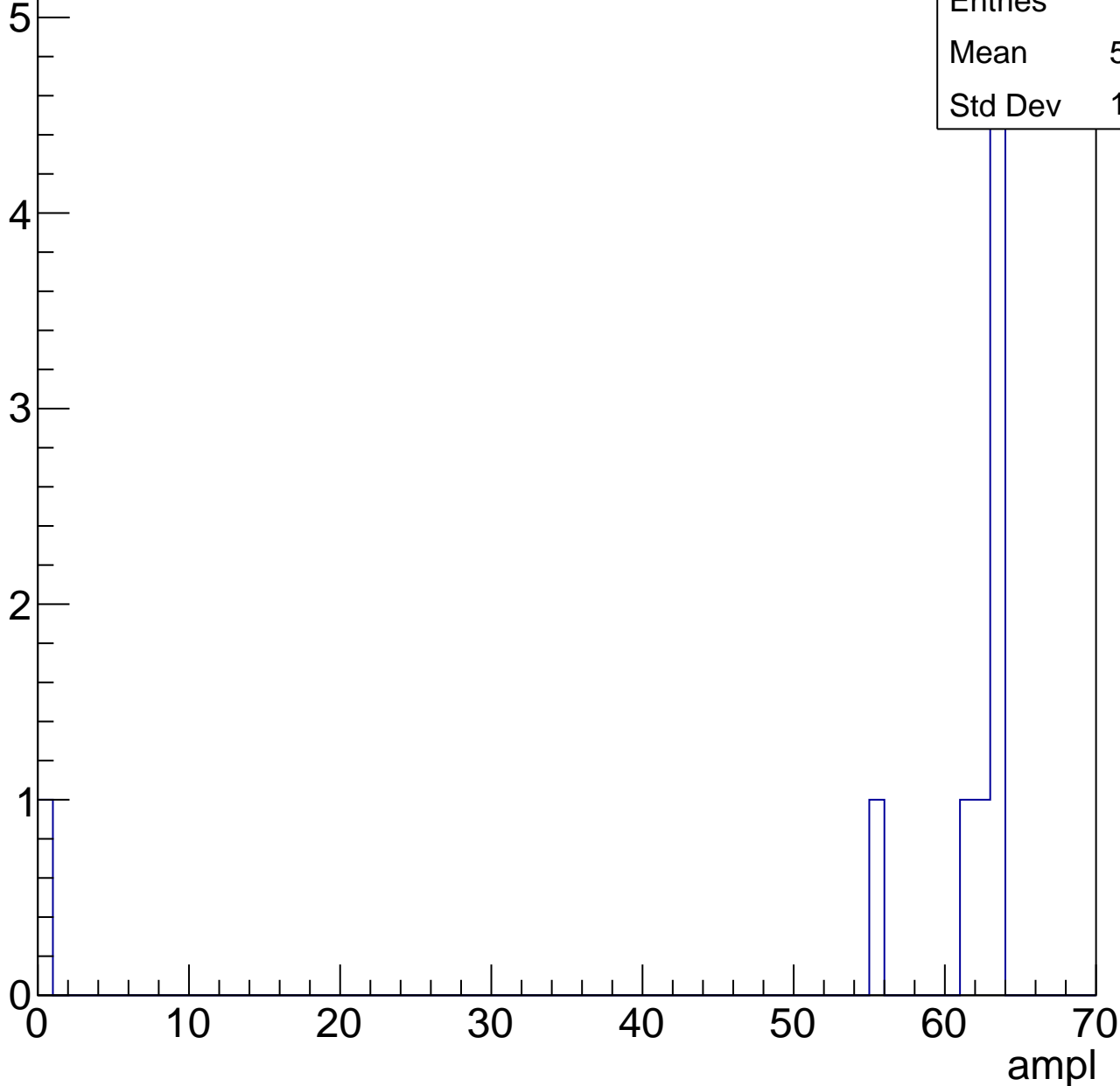


# B0L000S, U26-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	54.78
Std Dev	19.52

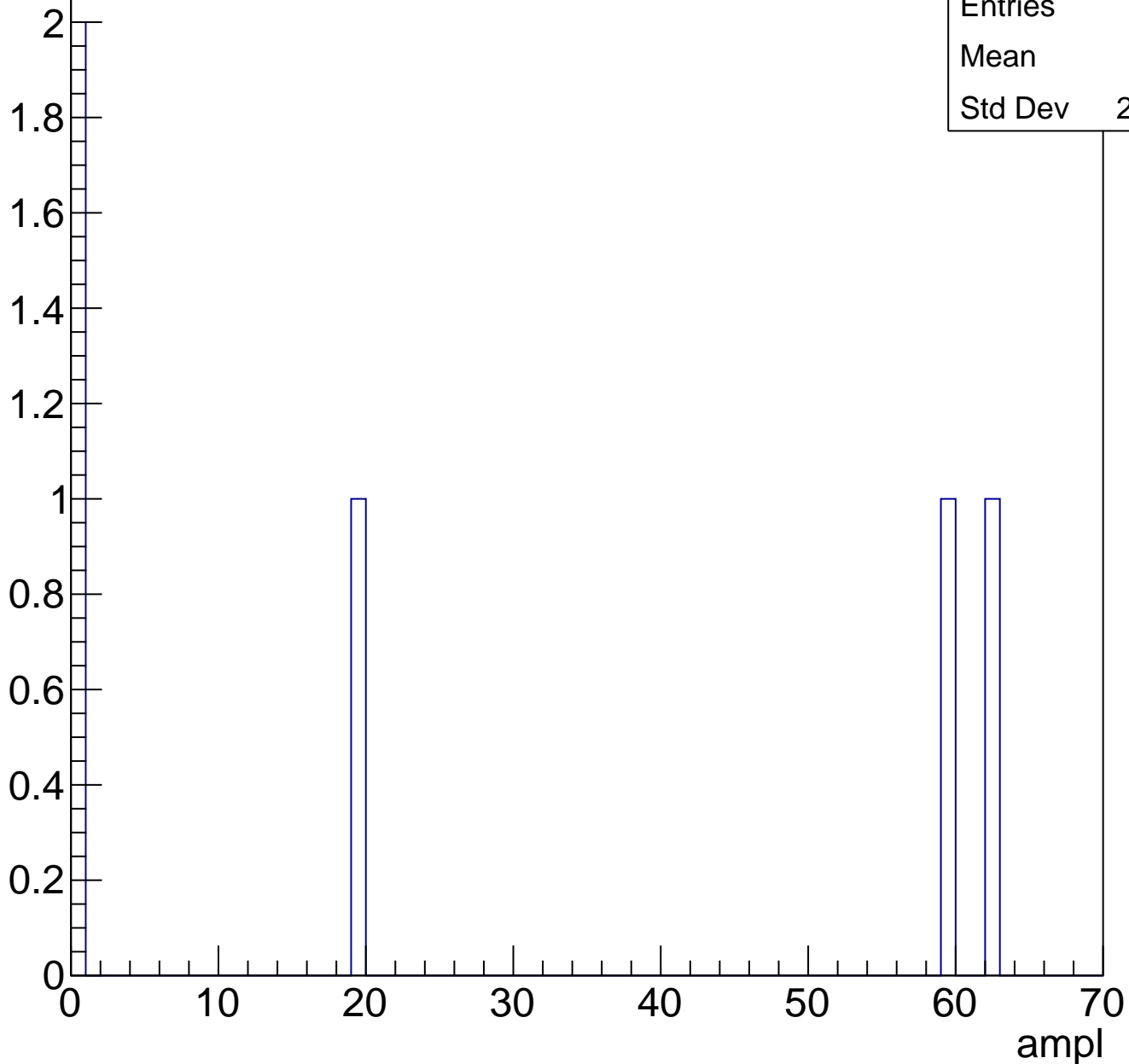




# B0L000S, U26-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	28
Std Dev	27.44

# B0L000S, U26-ch83, adc0

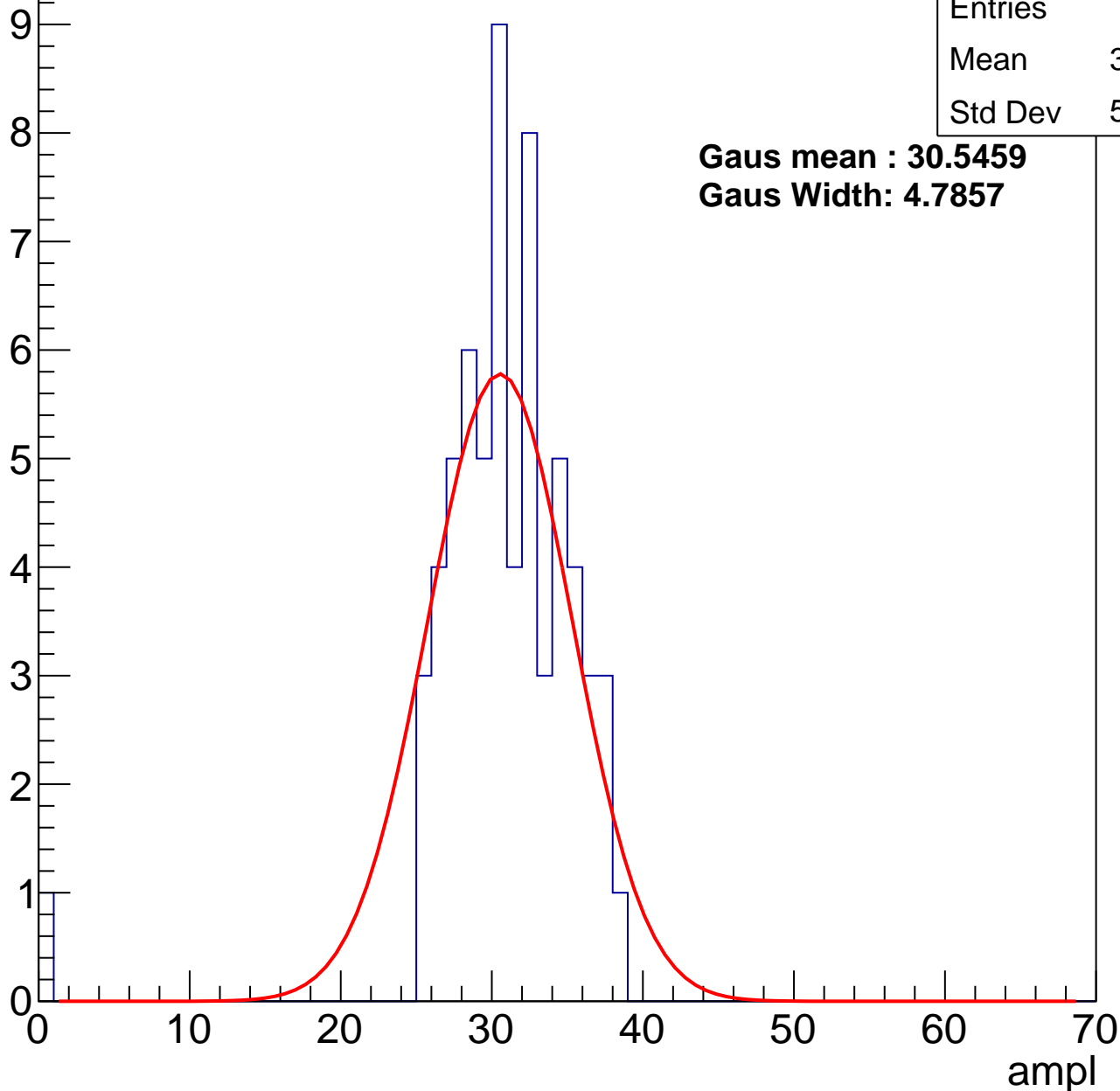
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.36
Std Dev	5.103

**Gaus mean : 30.5459**

**Gaus Width: 4.7857**



# B0L000S, U26-ch83, adc1

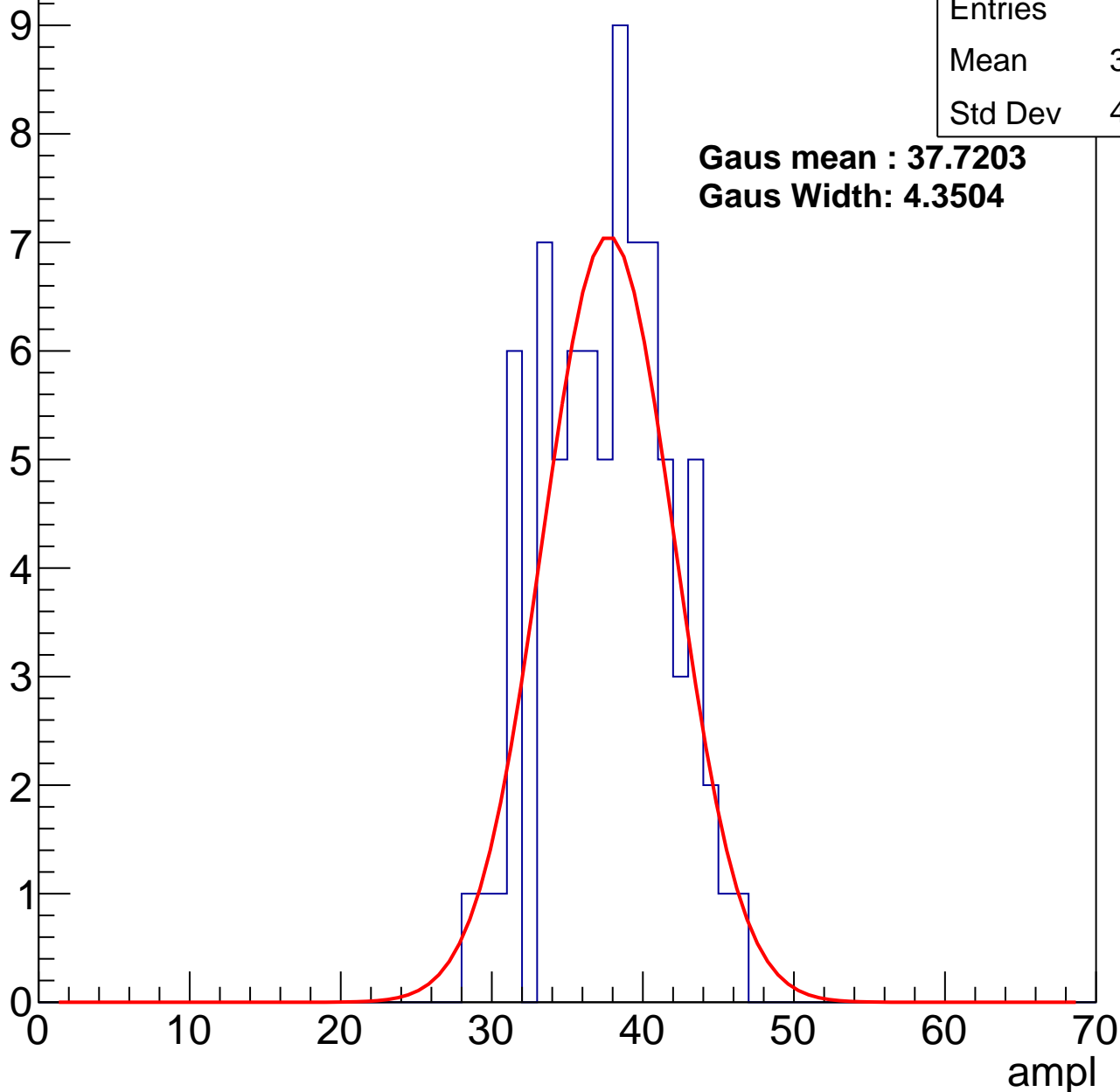
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	37.24
Std Dev	4.036

**Gaus mean : 37.7203**

**Gaus Width: 4.3504**

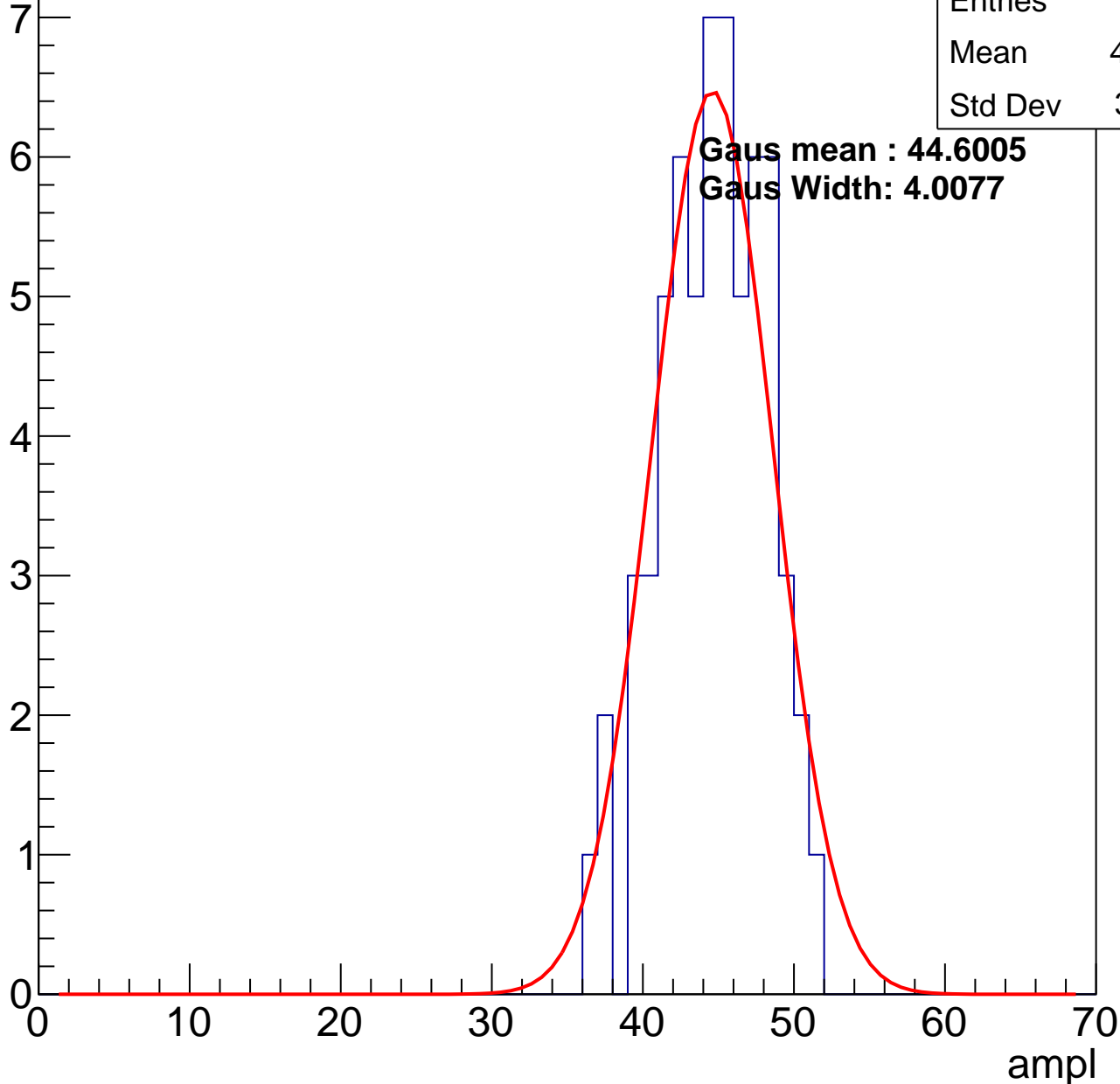


# B0L000S, U26-ch83, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	44.19
Std Dev	3.431

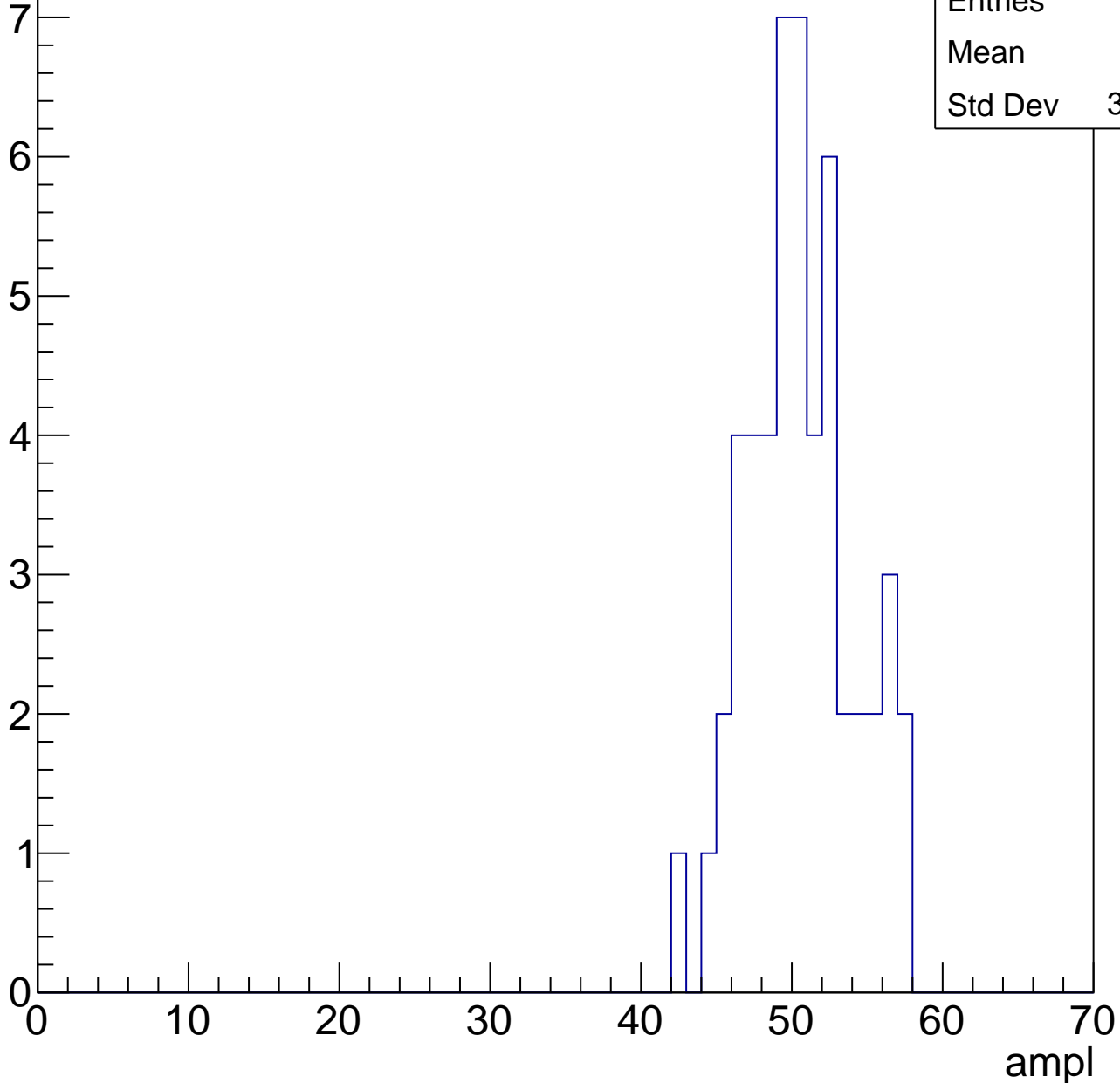


# B0L000S, U26-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	50.1
Std Dev	3.449

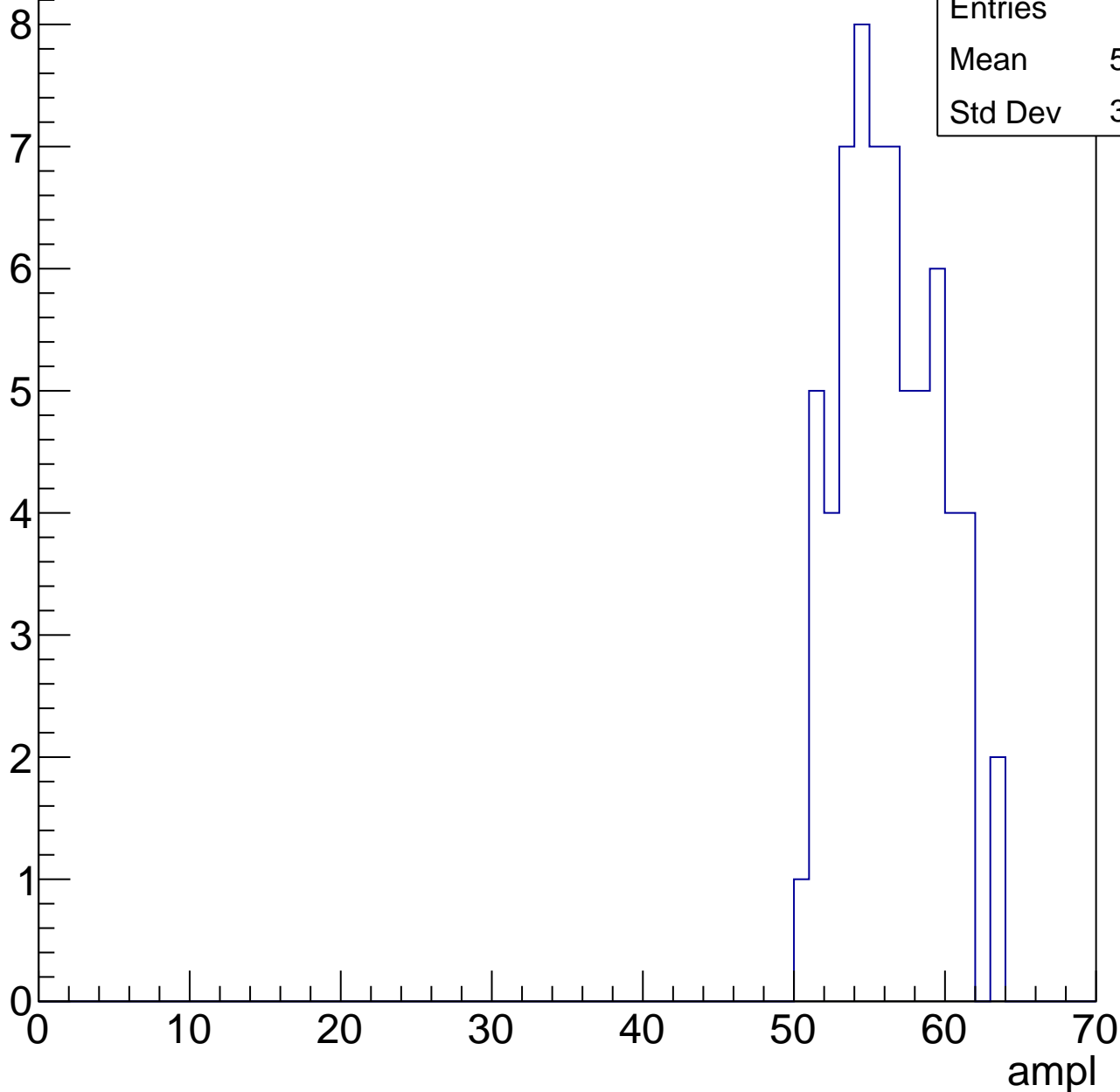


# B0L000S, U26-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	55.88
Std Dev	3.203

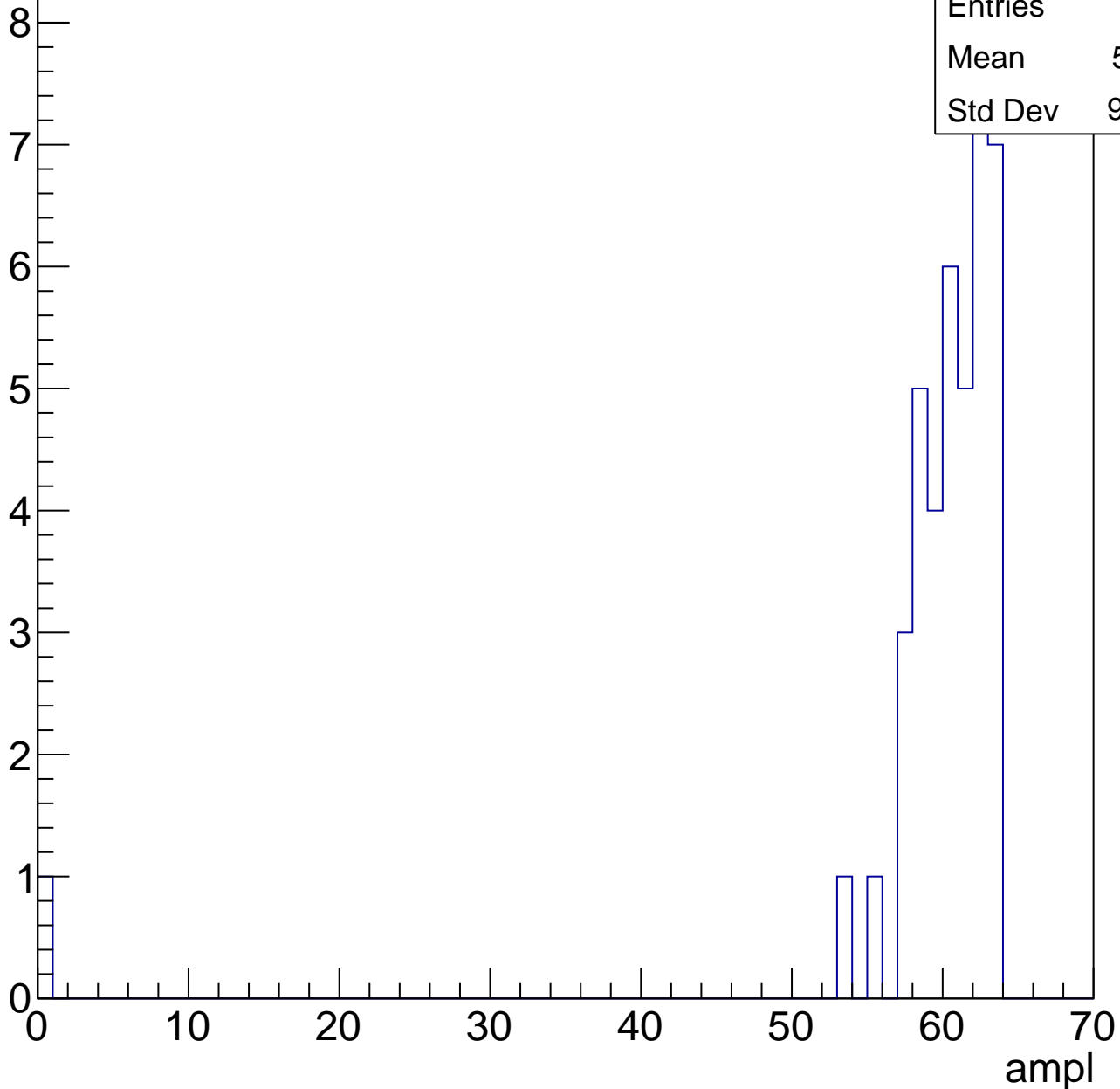


# B0L000S, U26-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.71
Std Dev	9.572



# B0L000S, U26-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	62
Std Dev	0.7071



# B0L000S, U26-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch84, adc0

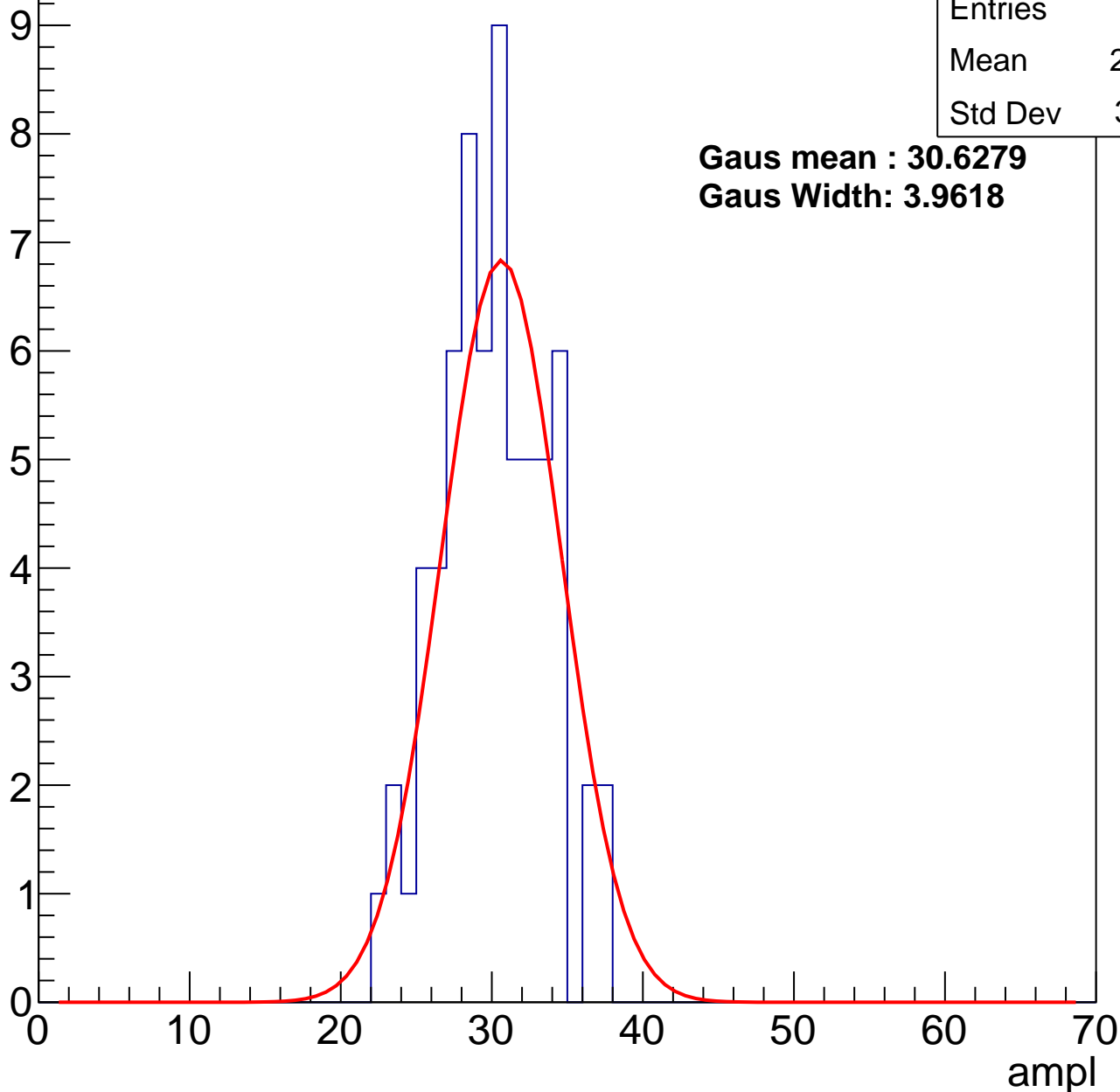
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.64
Std Dev	3.441

**Gaus mean : 30.6279**

**Gaus Width: 3.9618**



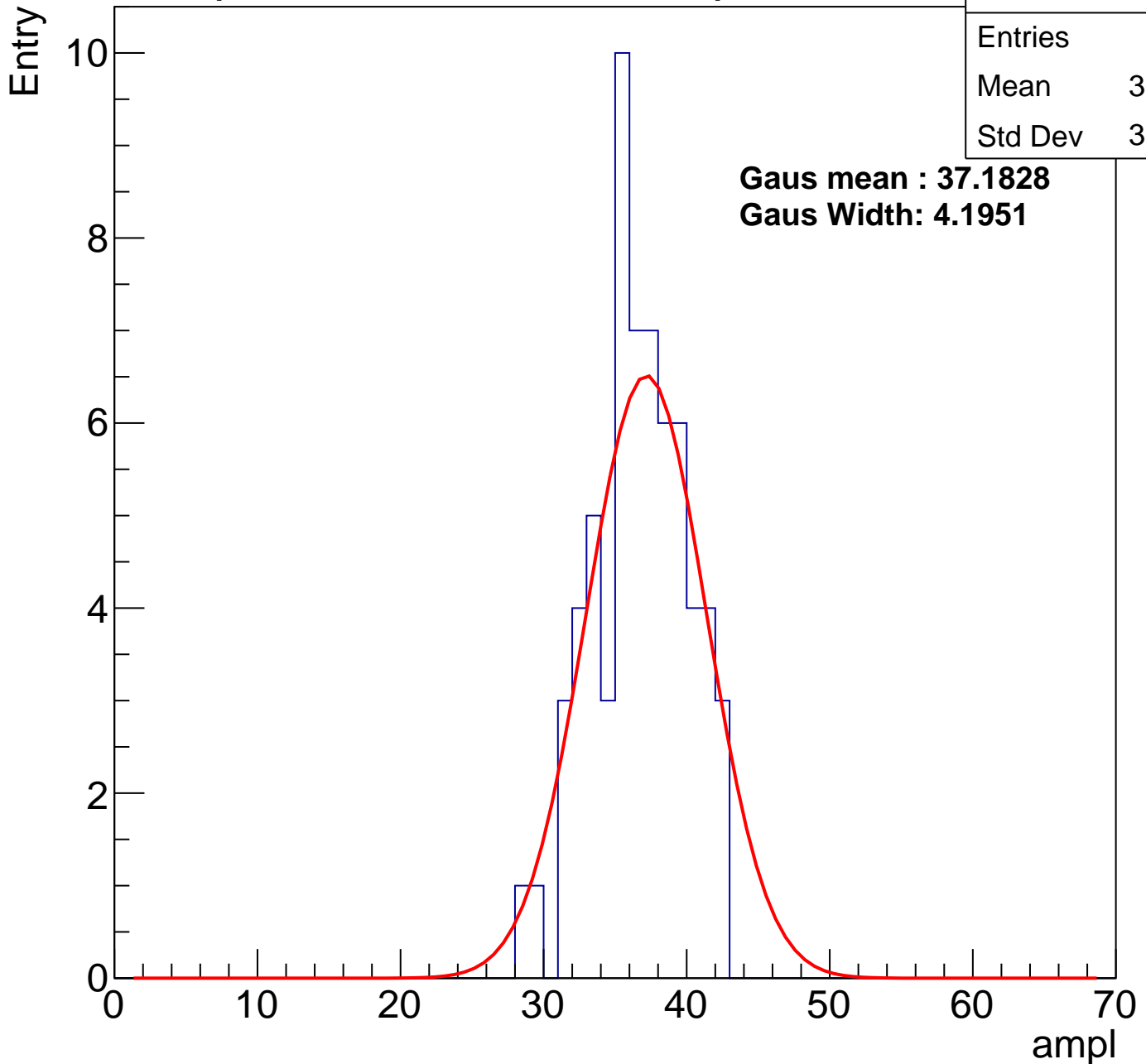
# B0L000S, U26-ch84, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	36.22
Std Dev	3.243

**Gaus mean : 37.1828**

**Gaus Width: 4.1951**



# B0L000S, U26-ch84, adc2

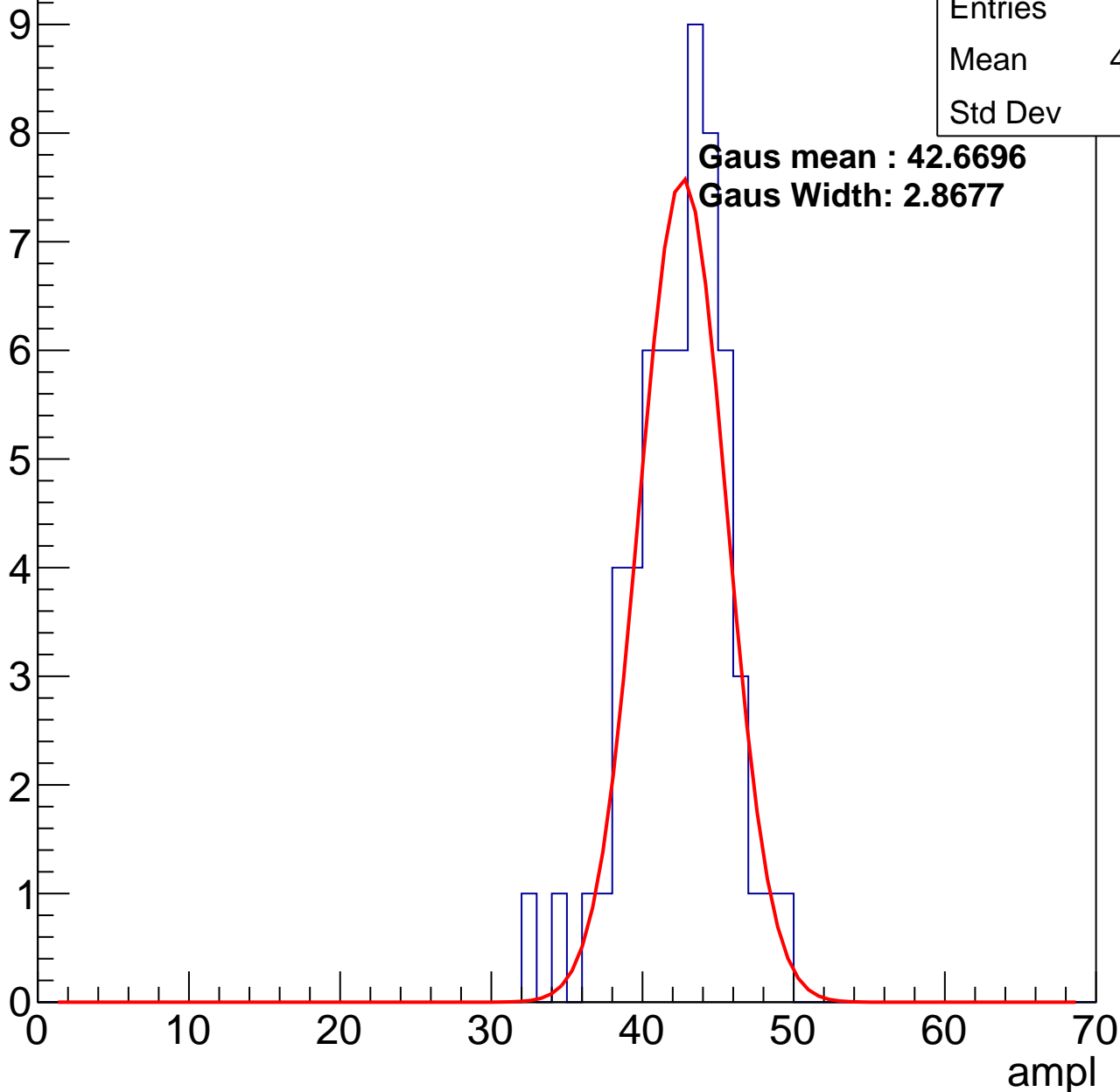
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	41.97
Std Dev	3.21

**Gaus mean : 42.6696**

**Gaus Width: 2.8677**

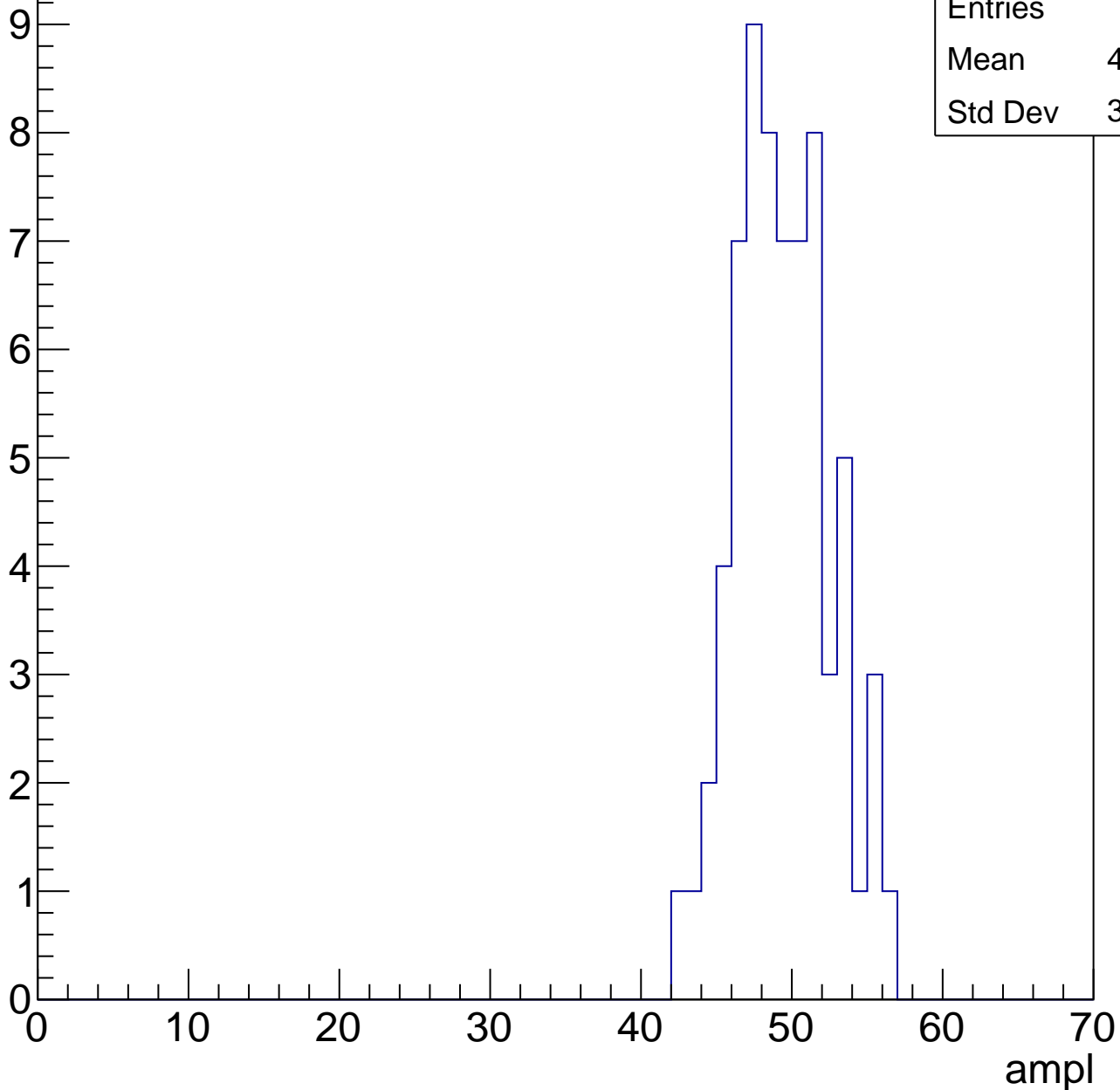


# B0L000S, U26-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.94
Std Dev	3.076

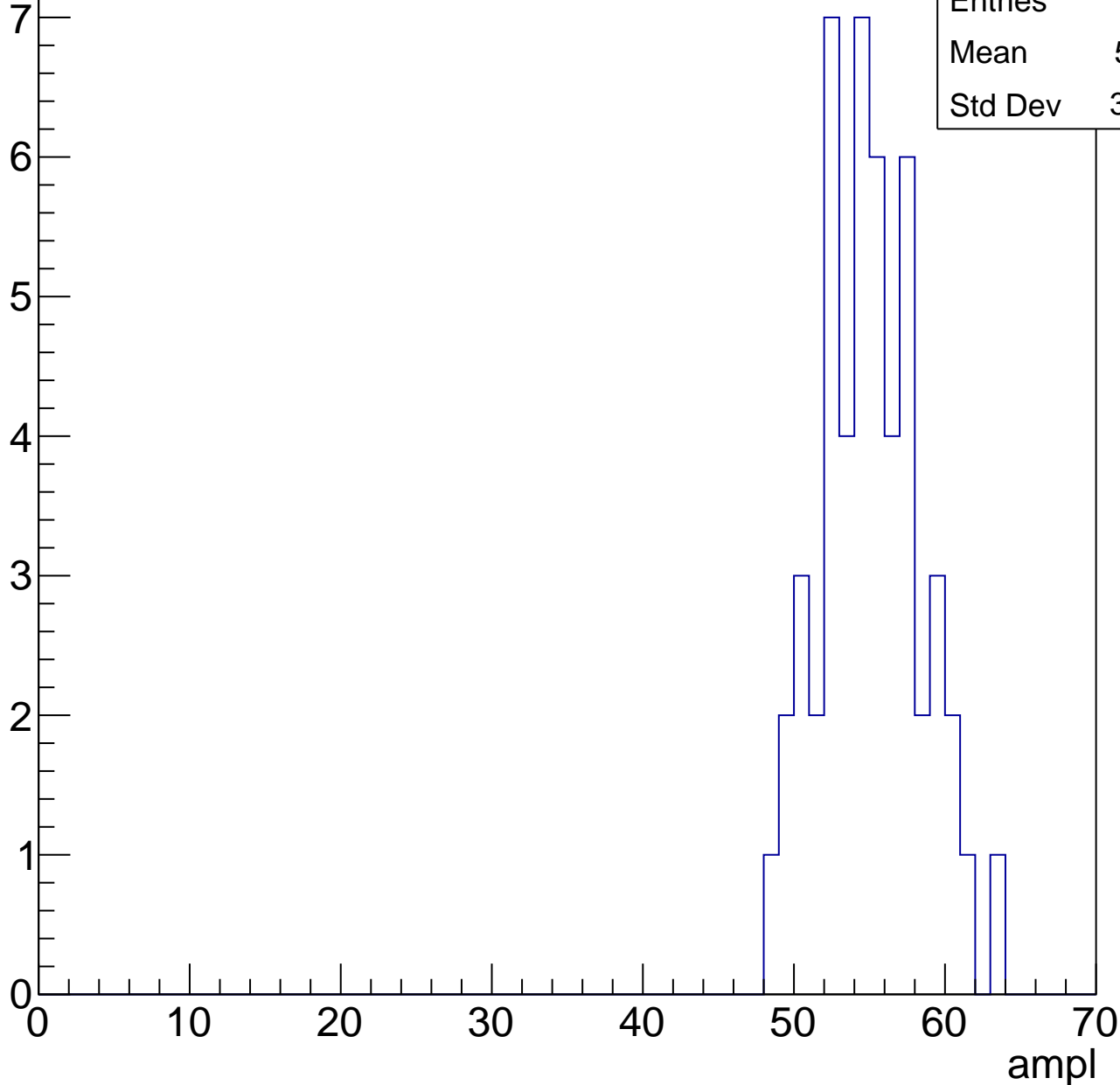


# B0L000S, U26-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	54.61
Std Dev	3.284

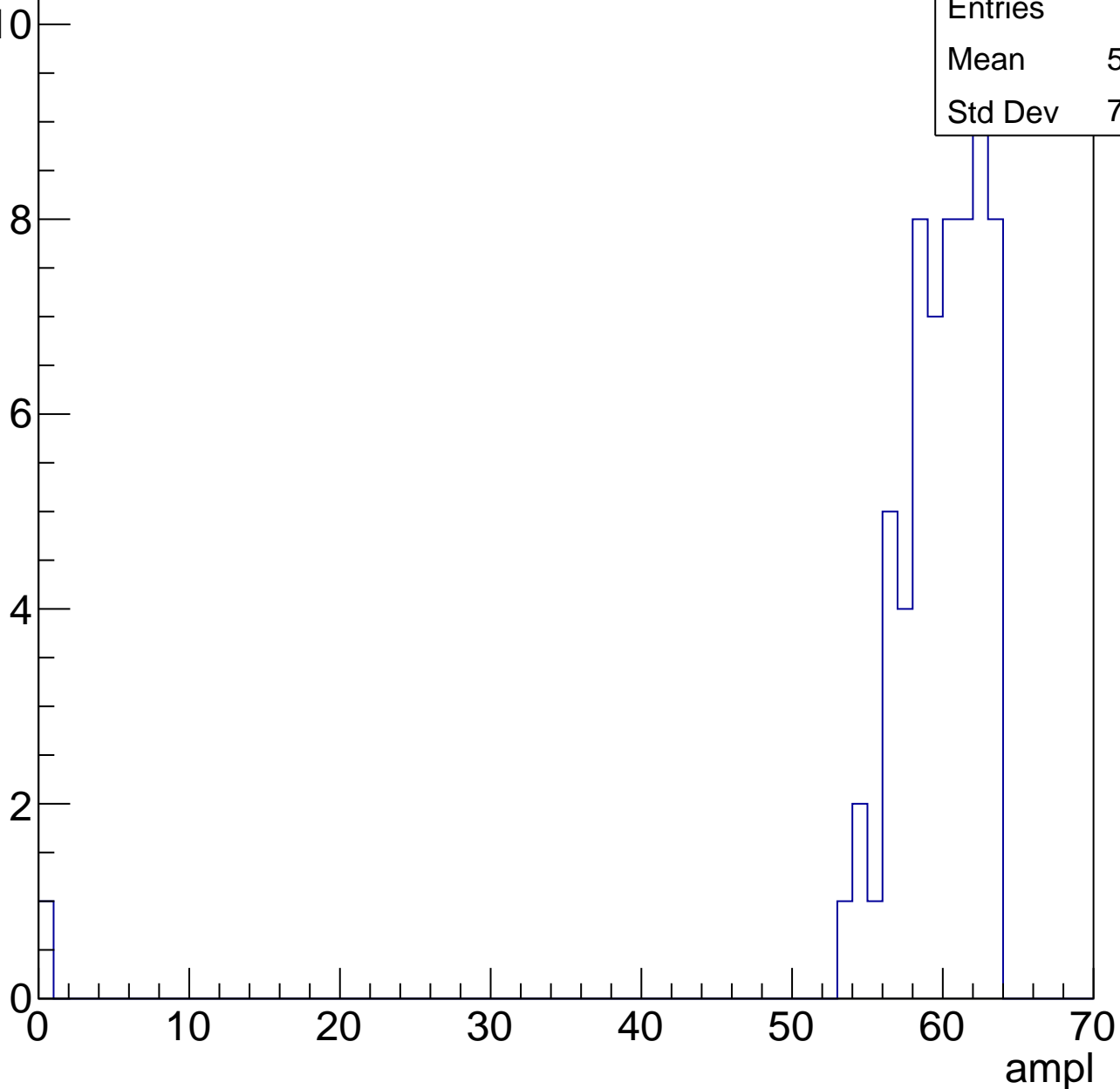


# B0L000S, U26-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	58.62
Std Dev	7.869



# B0L000S, U26-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch85, adc0

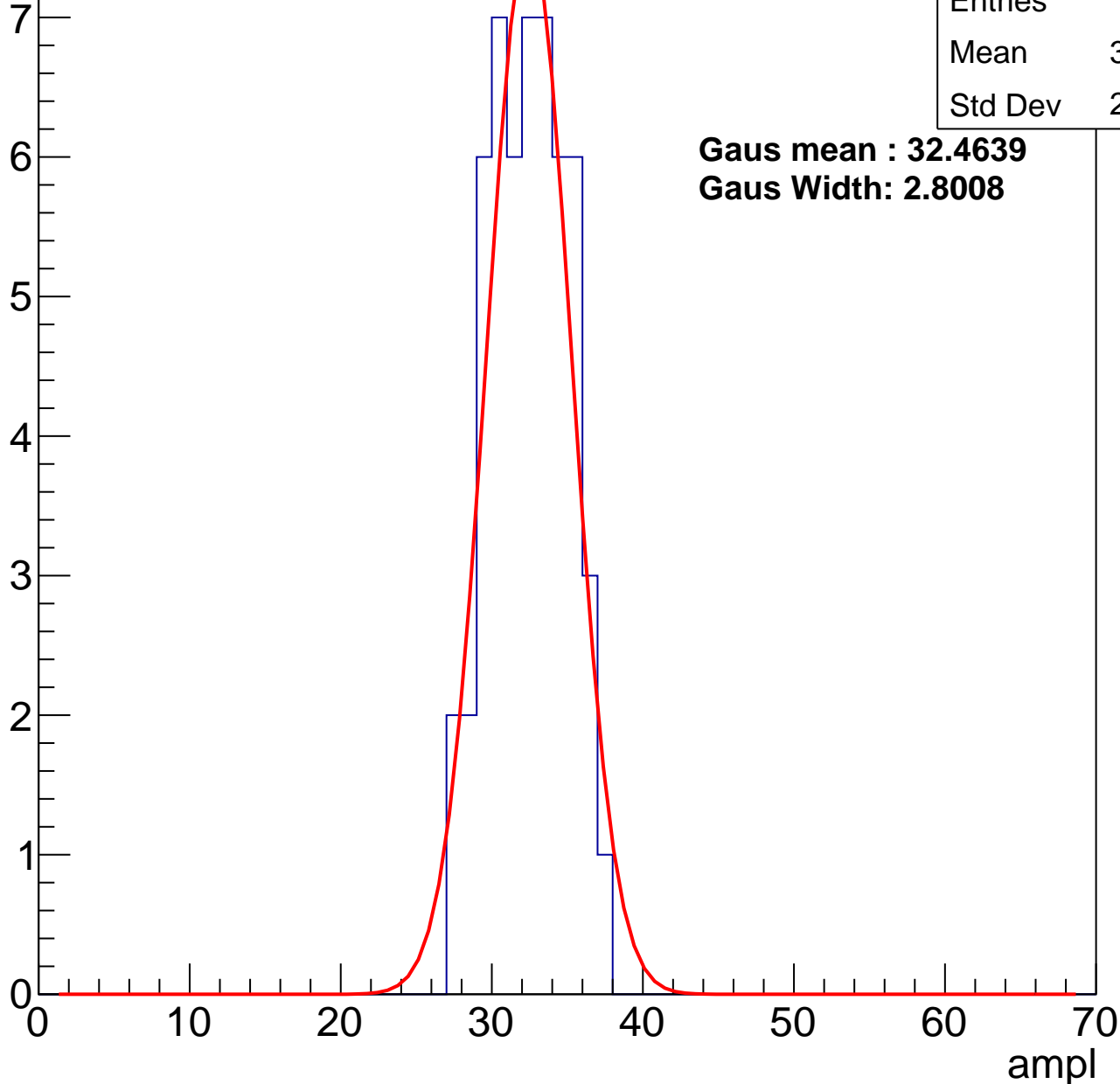
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	31.96
Std Dev	2.487

**Gaus mean : 32.4639**

**Gaus Width: 2.8008**



# B0L000S, U26-ch85, adc1

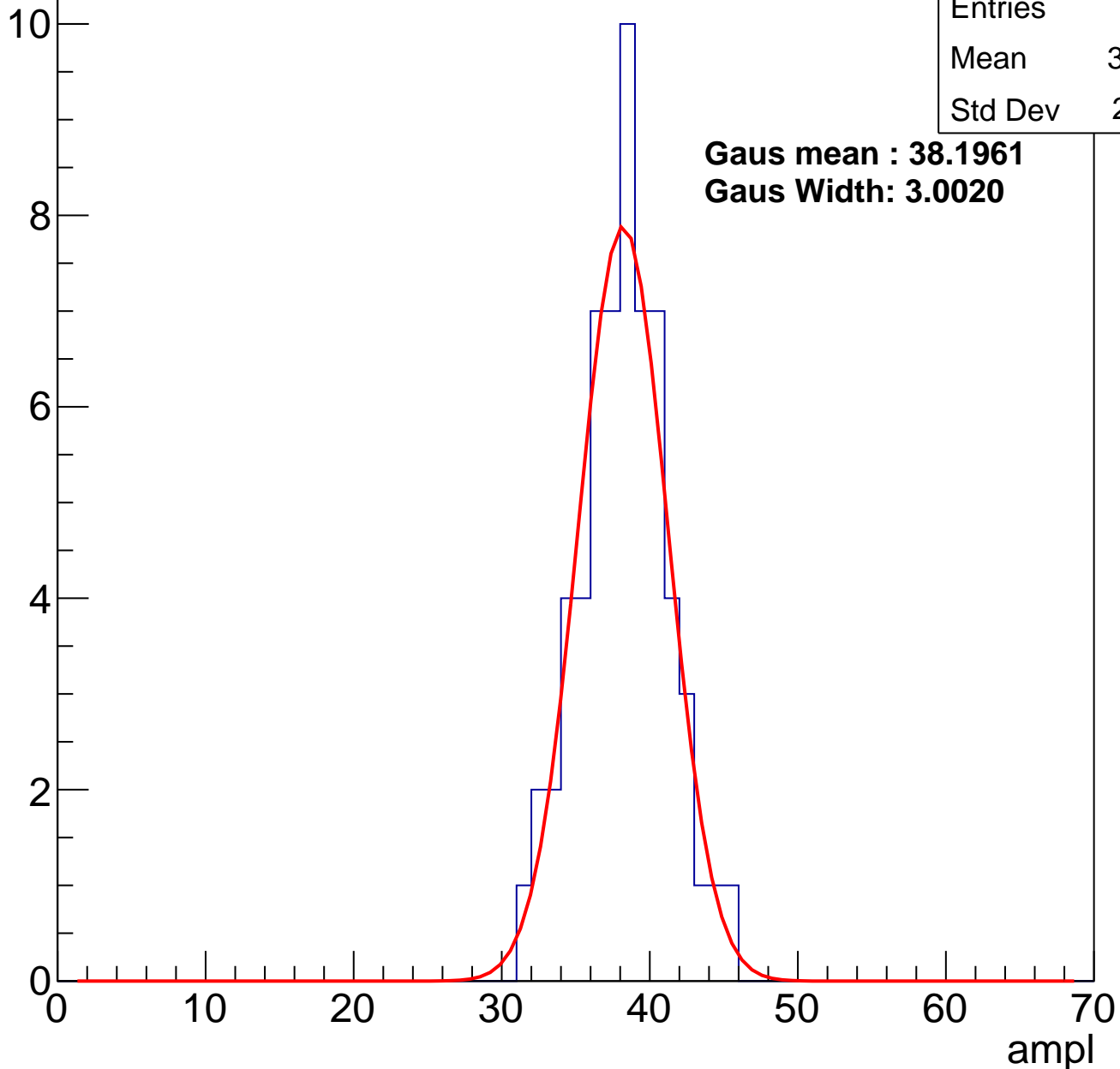
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	61
Mean	37.75
Std Dev	2.951

**Gaus mean : 38.1961**

**Gaus Width: 3.0020**

Entry



# B0L000S, U26-ch85, adc2

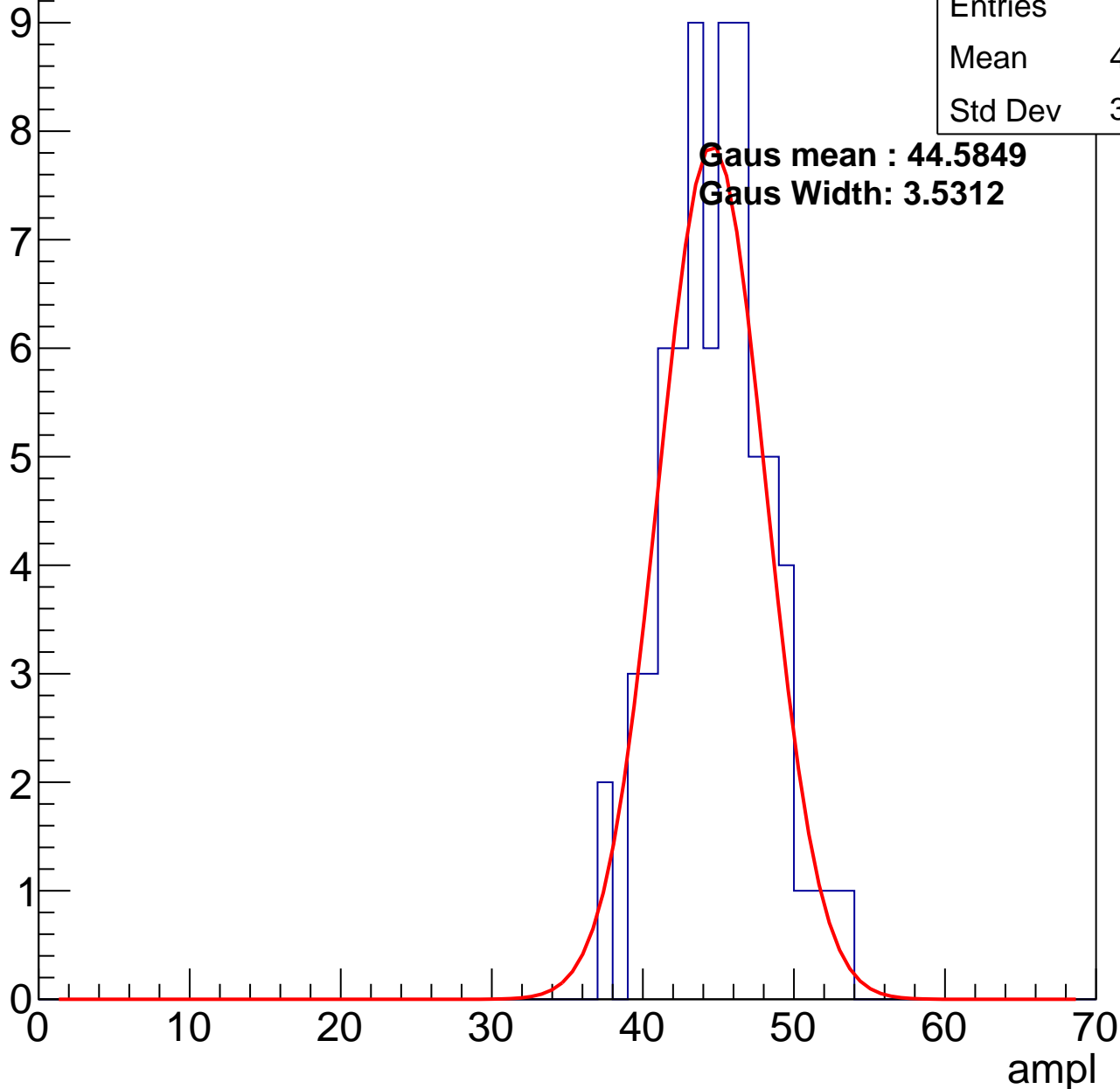
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	44.45
Std Dev	3.343

**Gaus mean : 44.5849**

**Gaus Width: 3.5312**

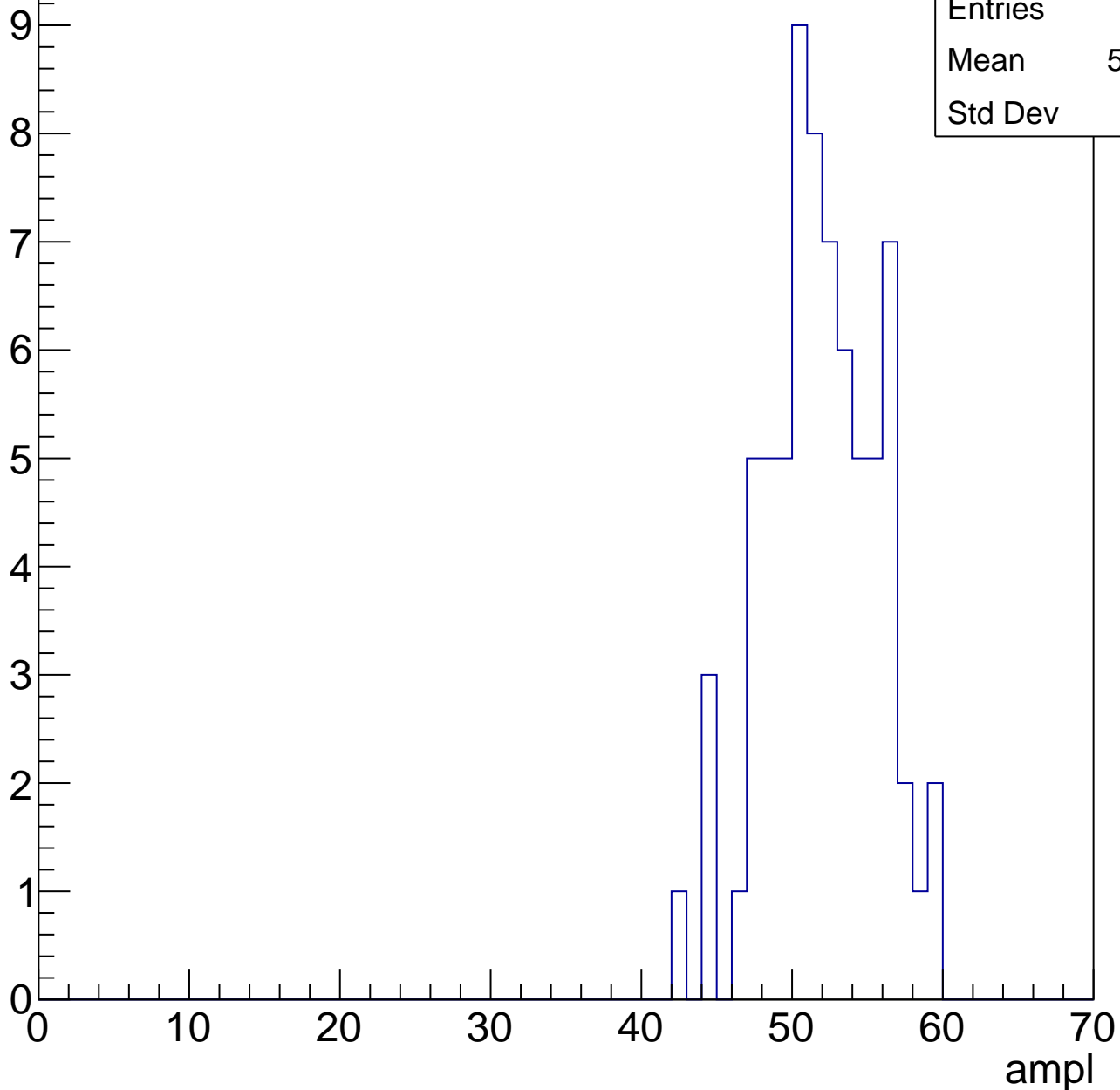


# B0L000S, U26-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	51.49
Std Dev	3.67

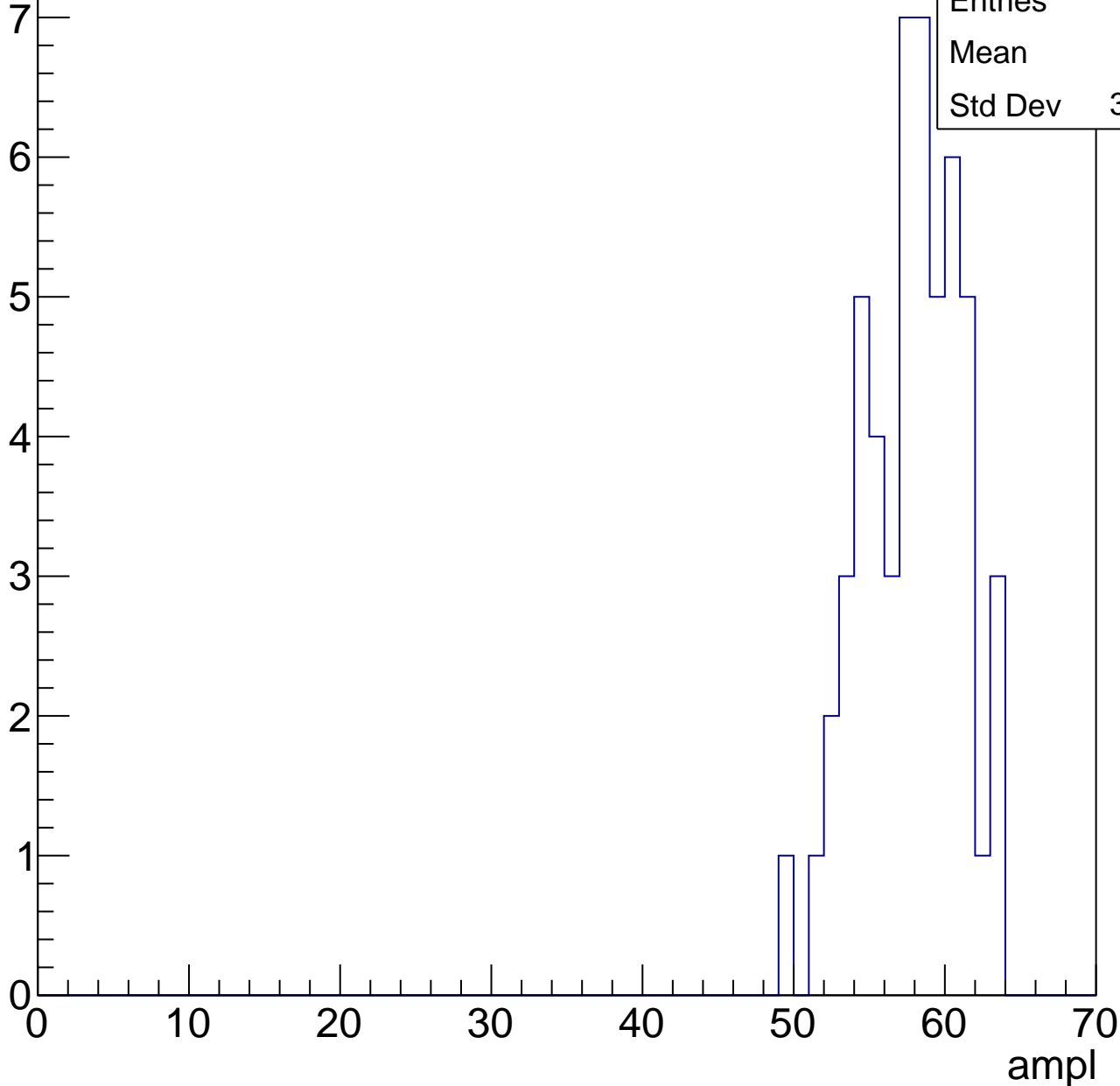


# B0L000S, U26-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.3
Std Dev	3.225

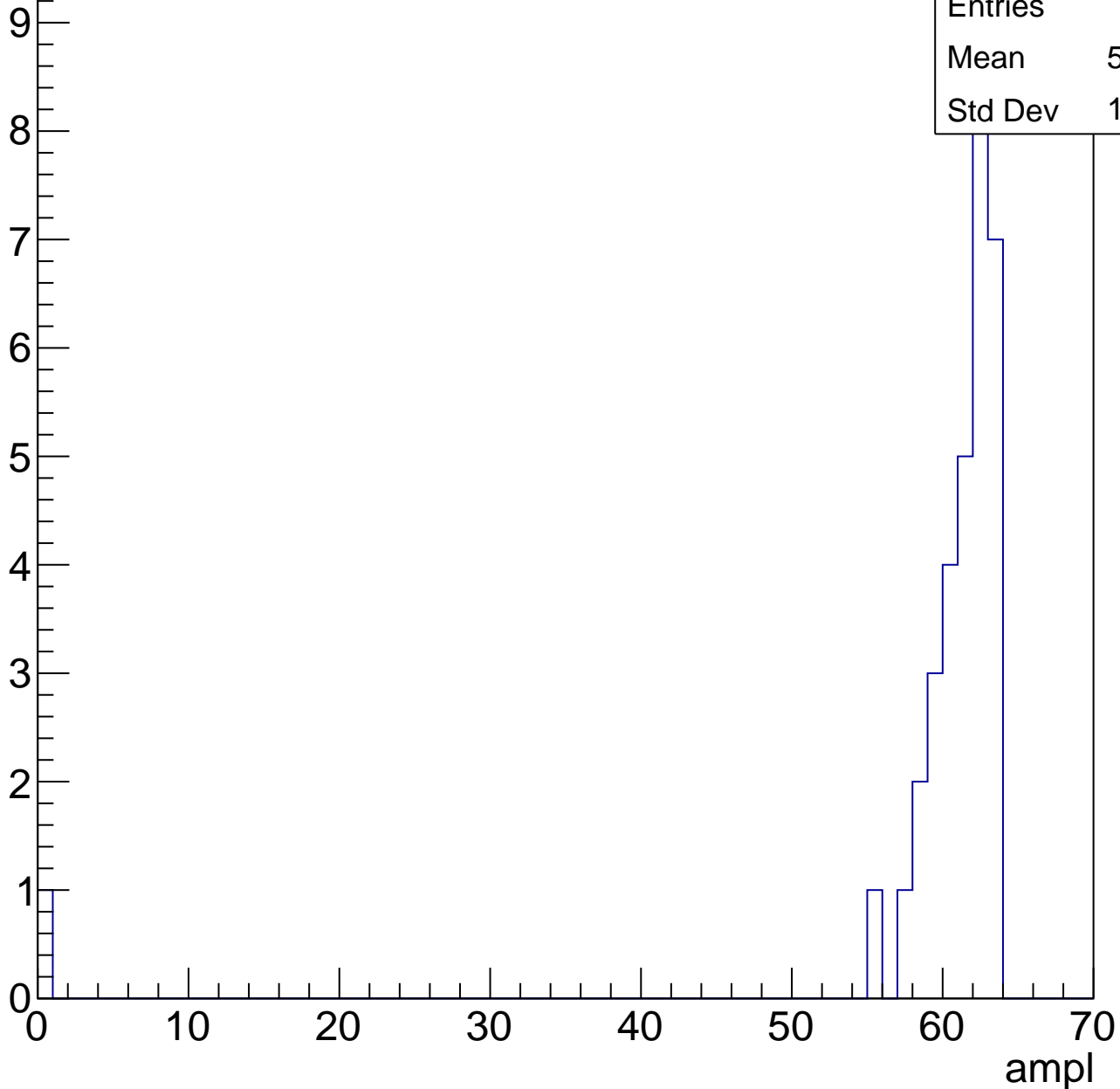


# B0L000S, U26-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	59.06
Std Dev	10.62



# B0L000S, U26-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

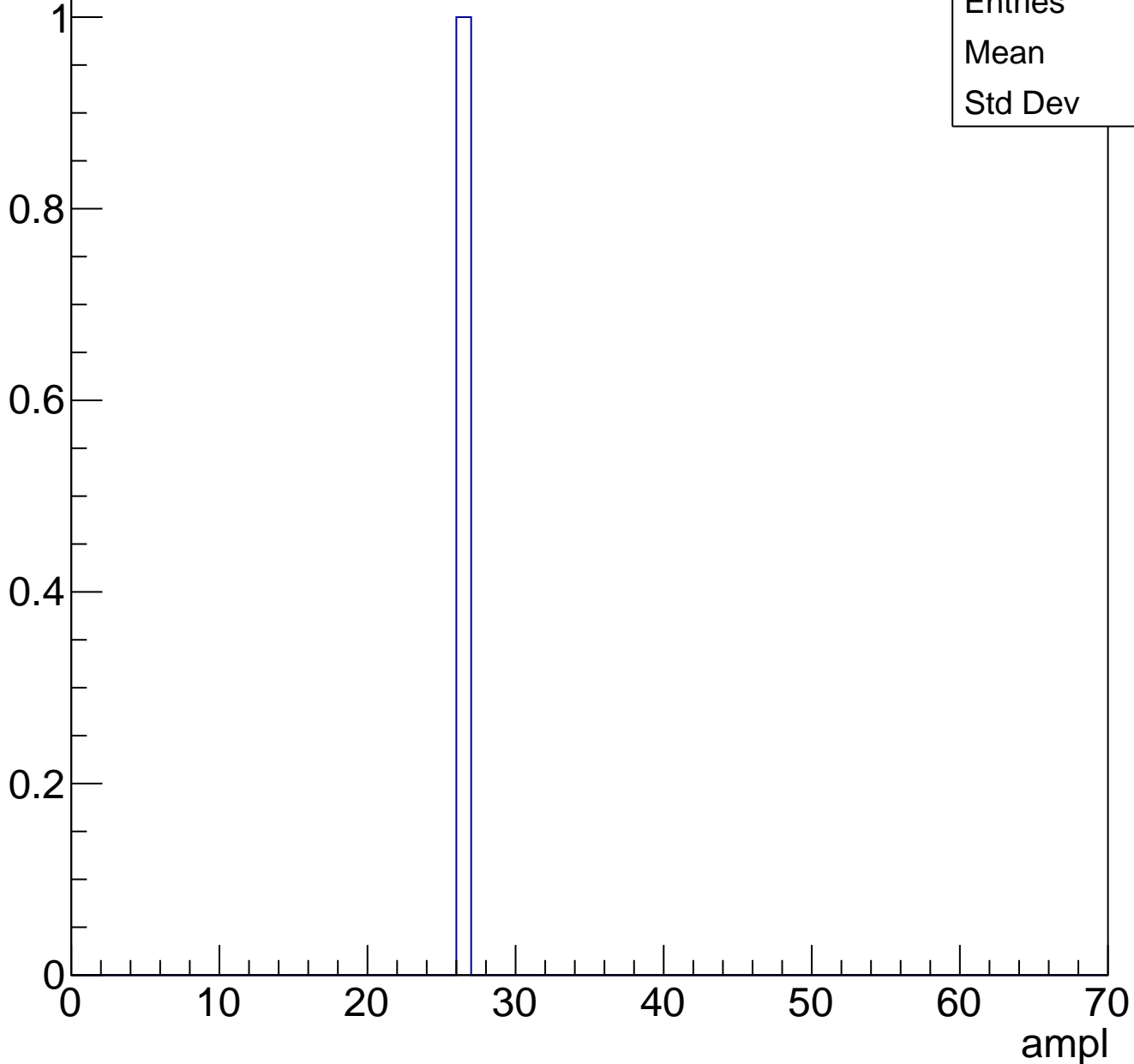




# B0L000S, U26-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	26
Std Dev	0

# B0L000S, U26-ch86, adc0

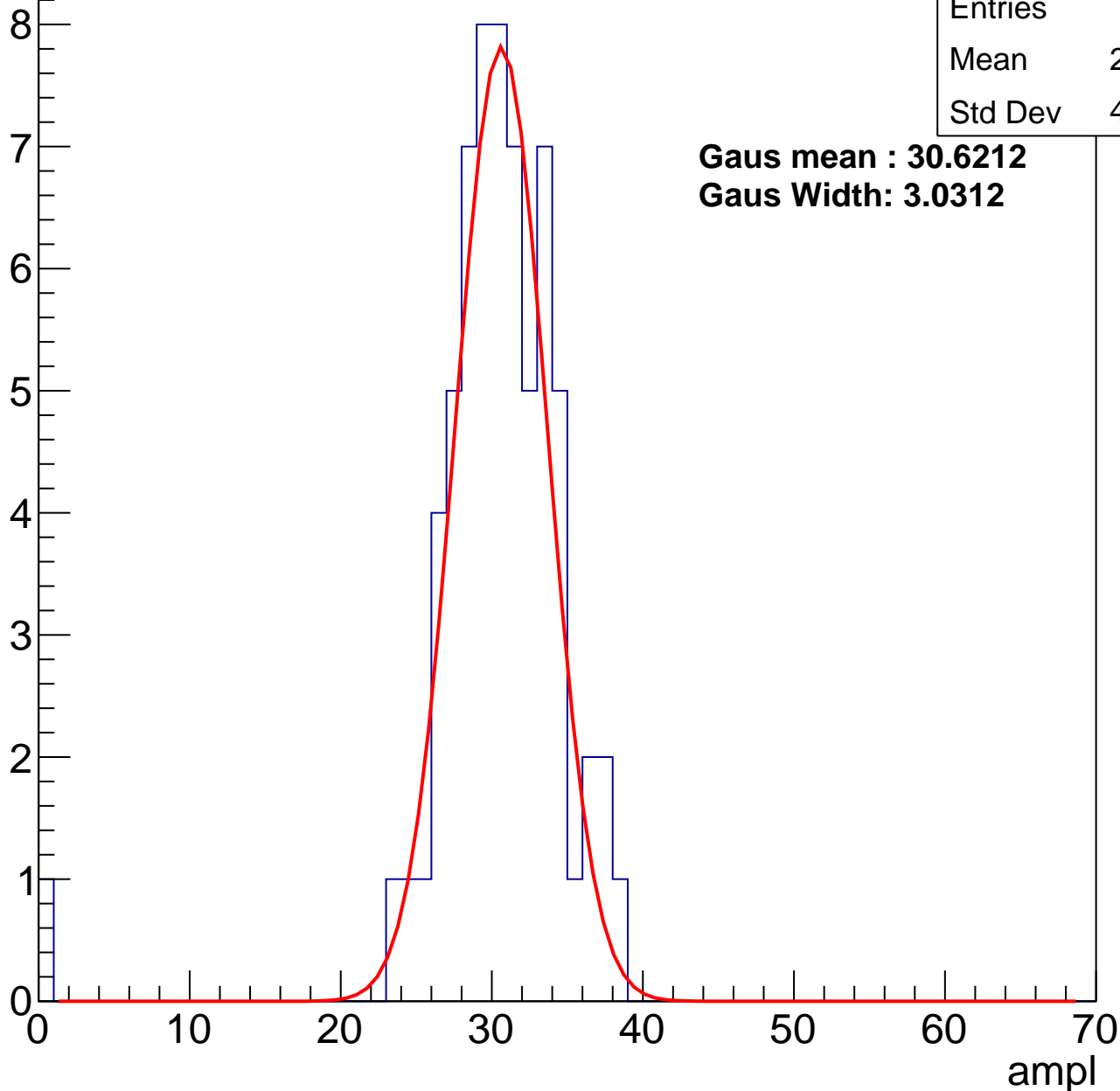
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.94
Std Dev	4.899

**Gaus mean : 30.6212**

**Gaus Width: 3.0312**



# B0L000S, U26-ch86, adc1

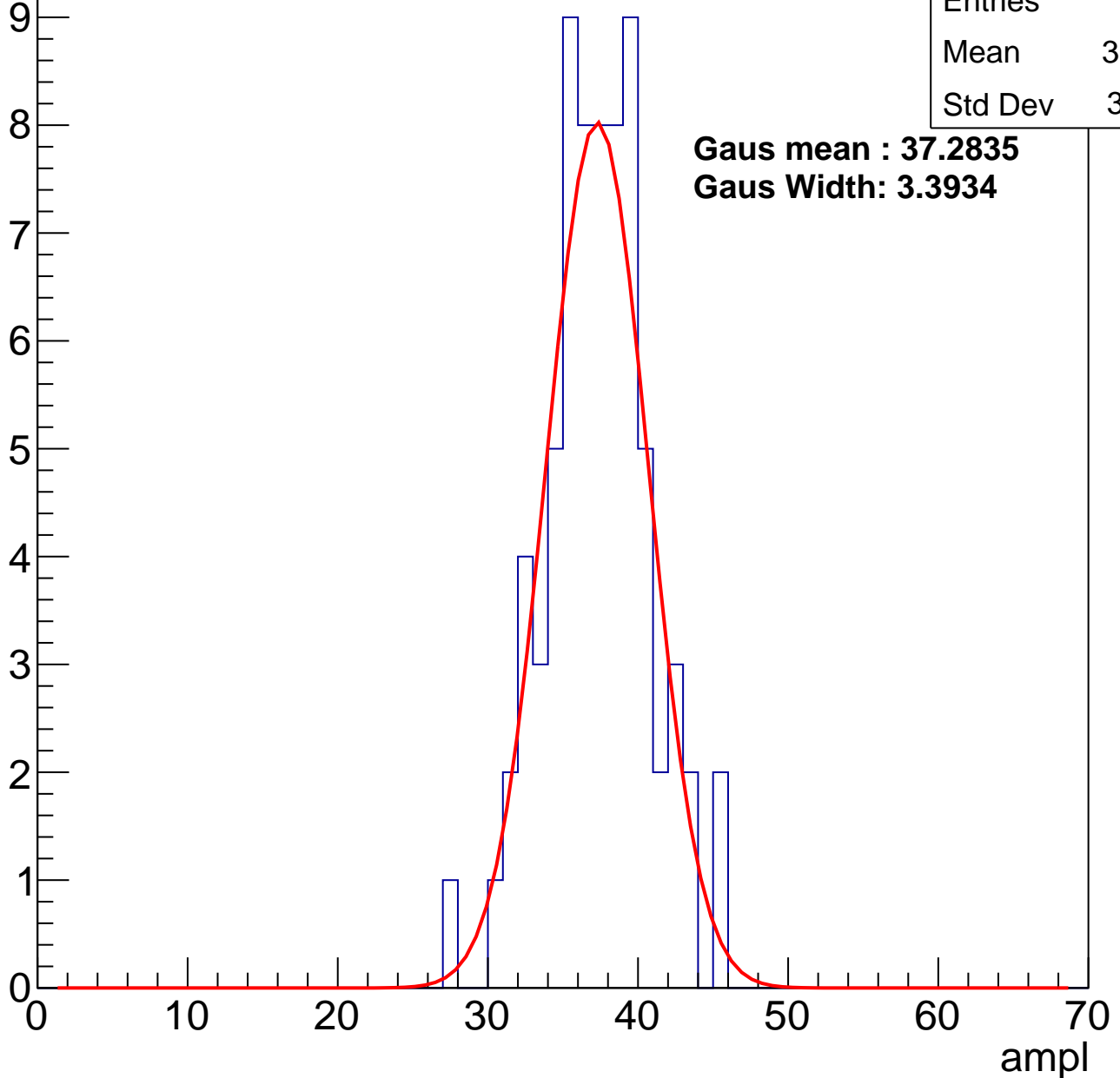
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.86
Std Dev	3.441

**Gaus mean : 37.2835**

**Gaus Width: 3.3934**



# B0L000S, U26-ch86, adc2

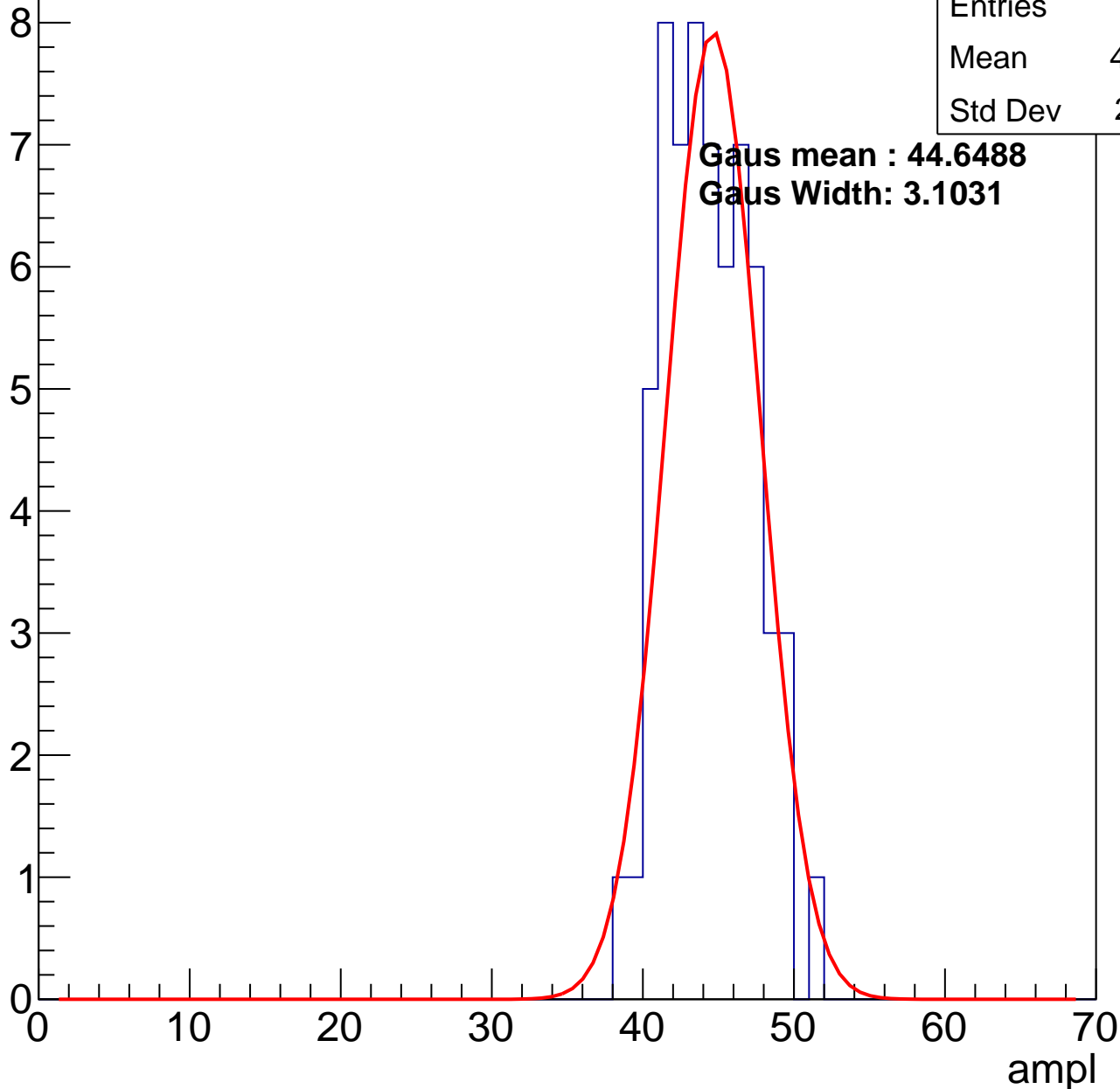
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	43.92
Std Dev	2.841

**Gaus mean : 44.6488**

**Gaus Width: 3.1031**

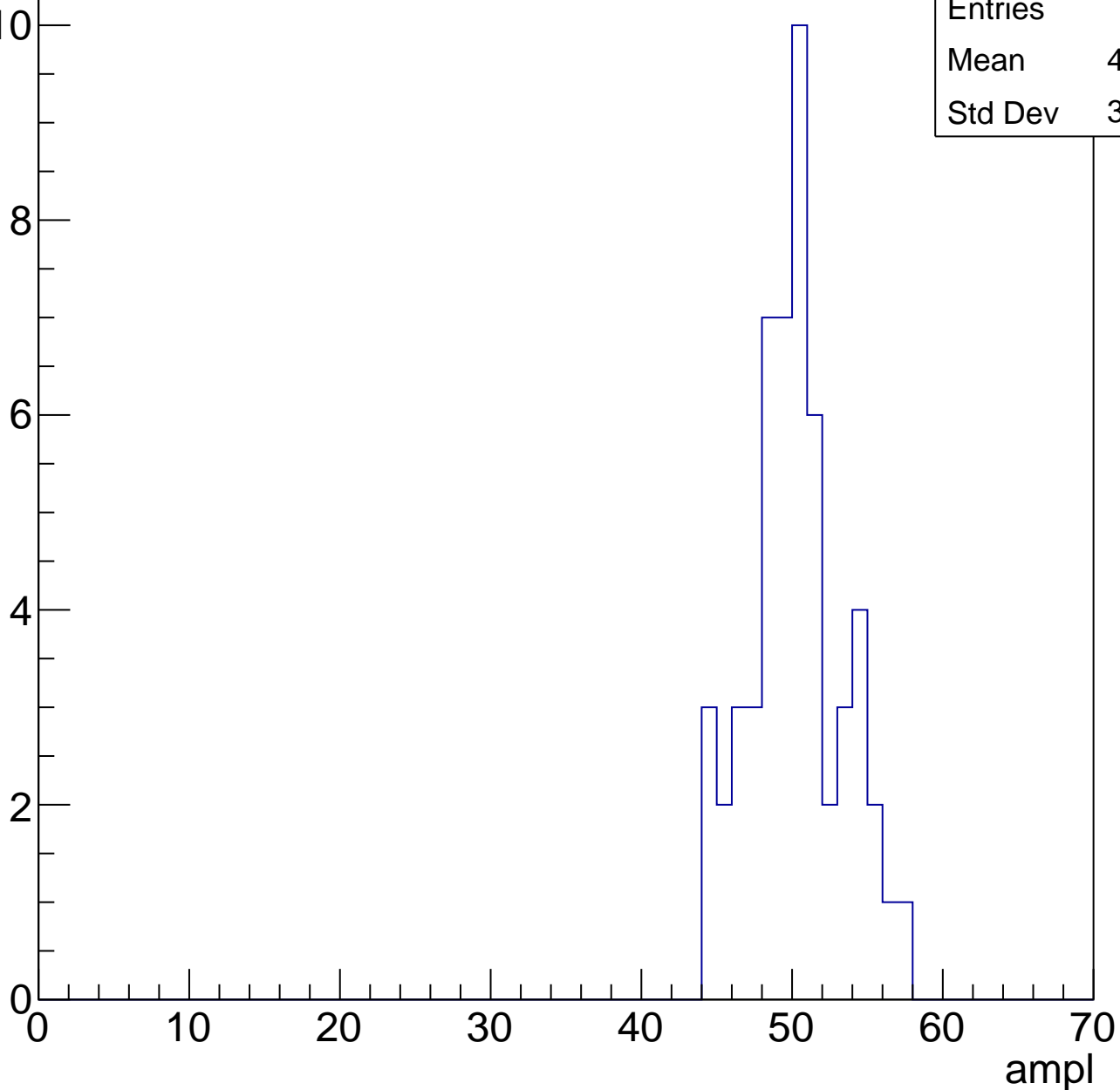


# B0L000S, U26-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	49.78
Std Dev	3.059

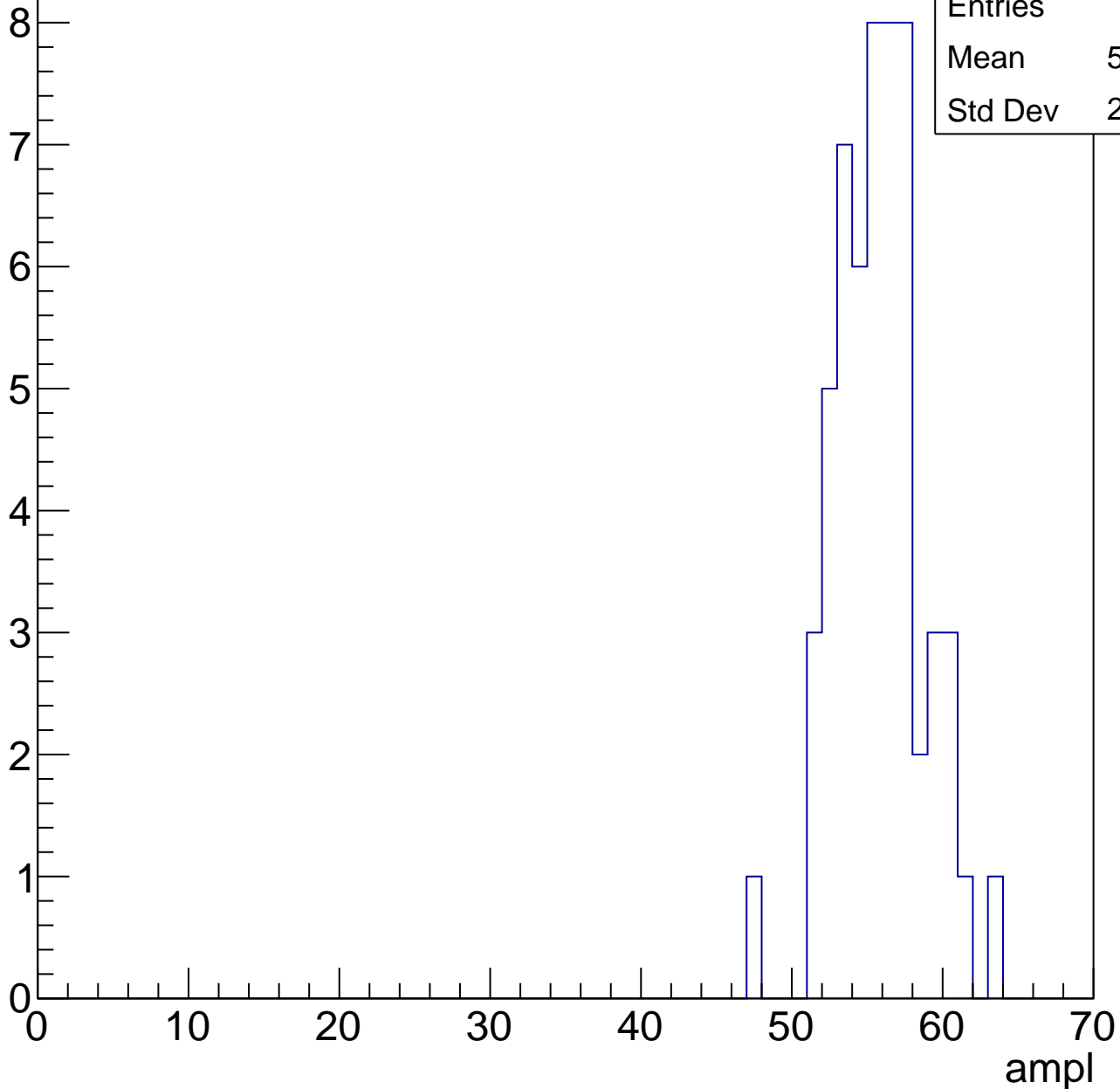


# B0L000S, U26-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.29
Std Dev	2.889

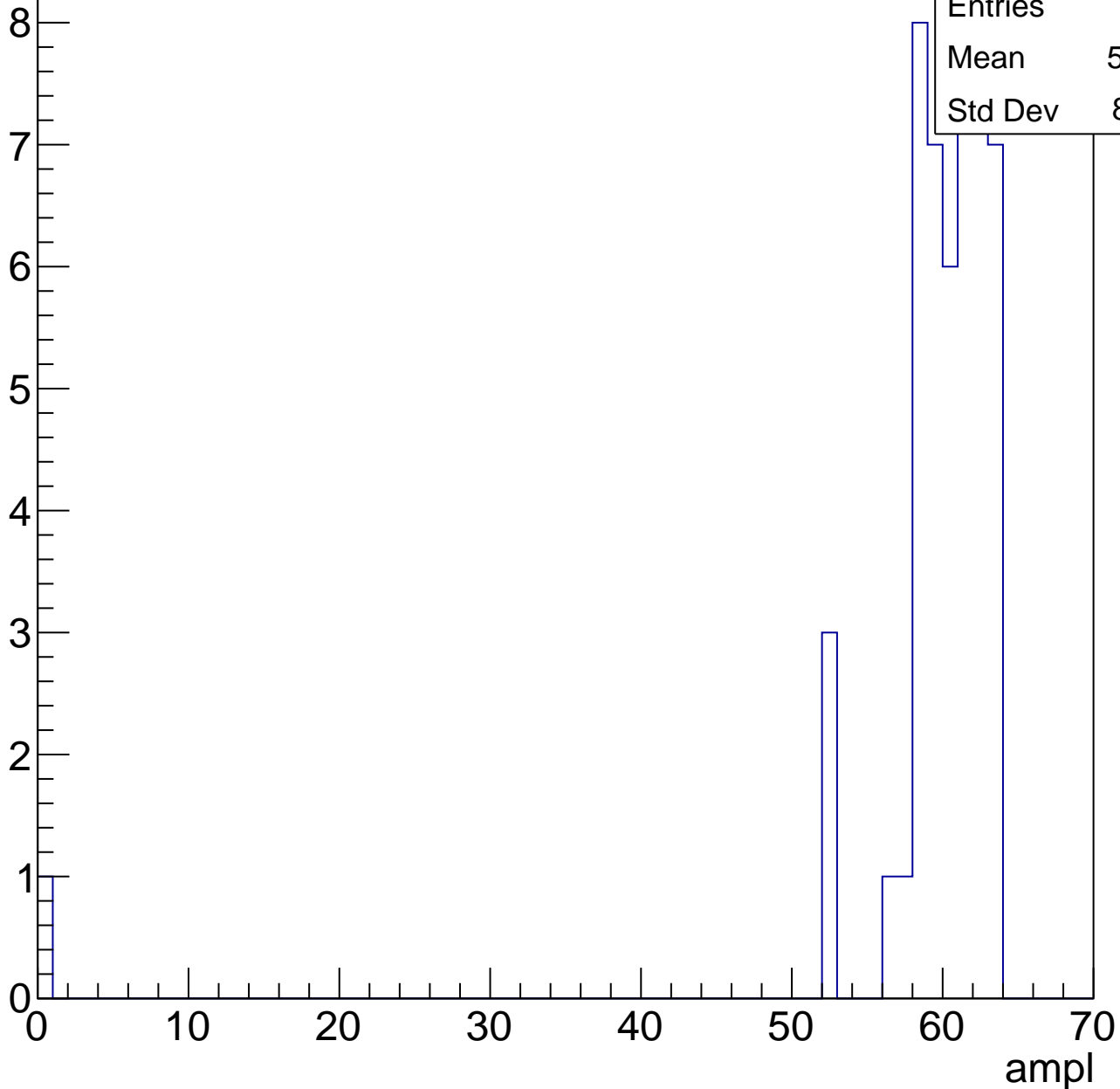


# B0L000S, U26-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	58.62
Std Dev	8.791



# B0L000S, U26-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

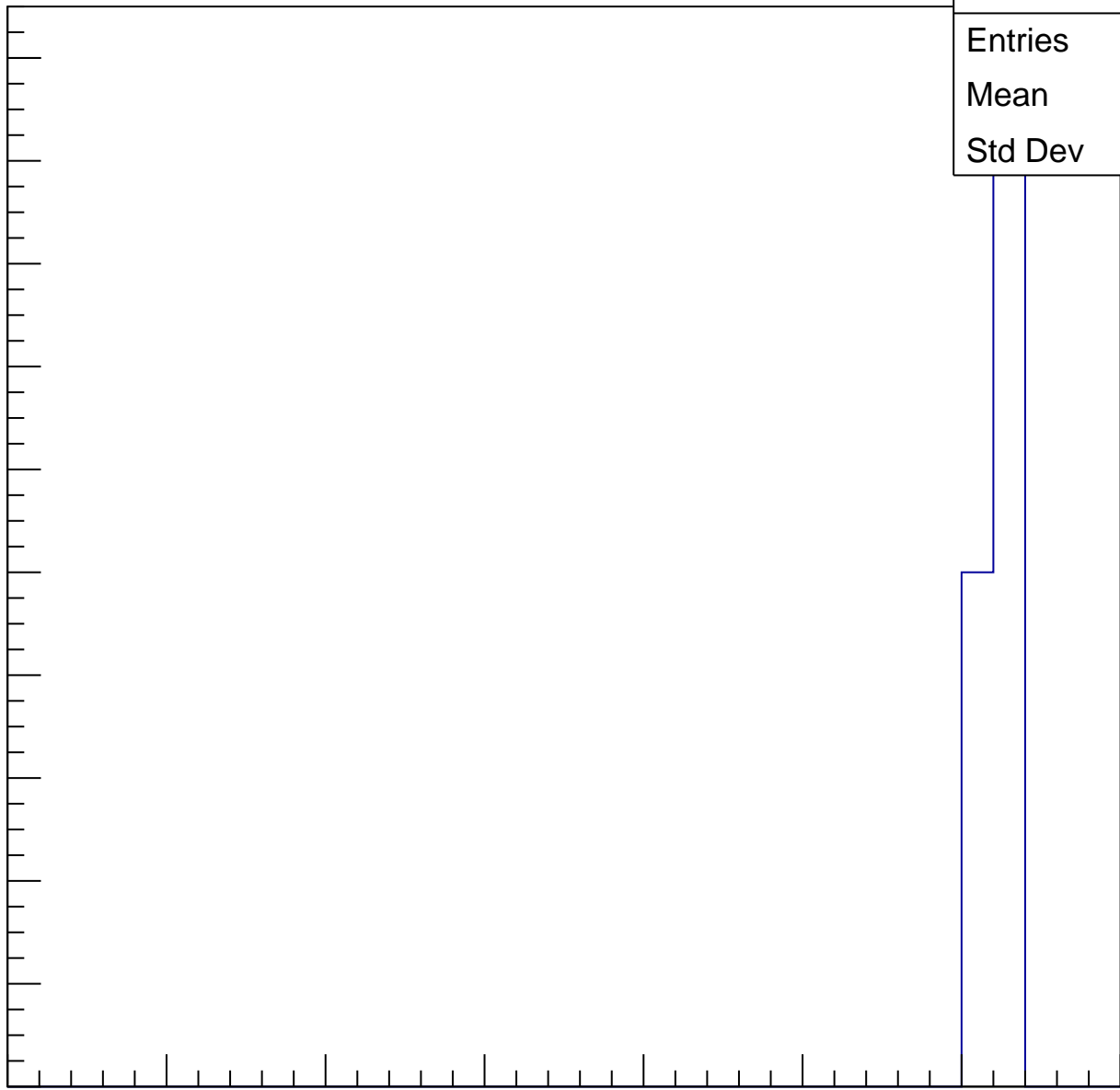
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70





# B0L000S, U26-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

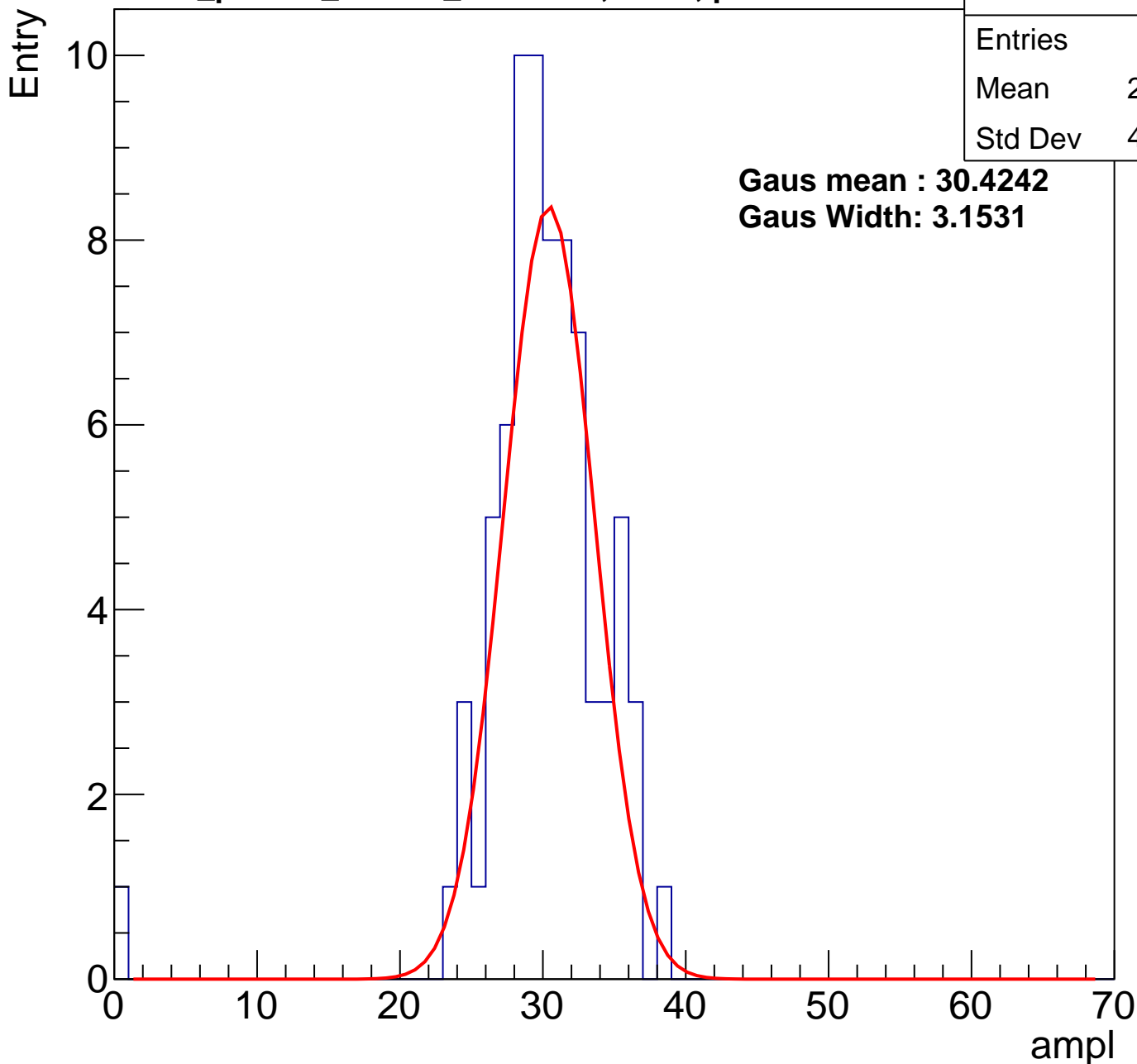
# B0L000S, U26-ch87, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	29.55
Std Dev	4.703

**Gaus mean : 30.4242**

**Gaus Width: 3.1531**



# B0L000S, U26-ch87, adc1

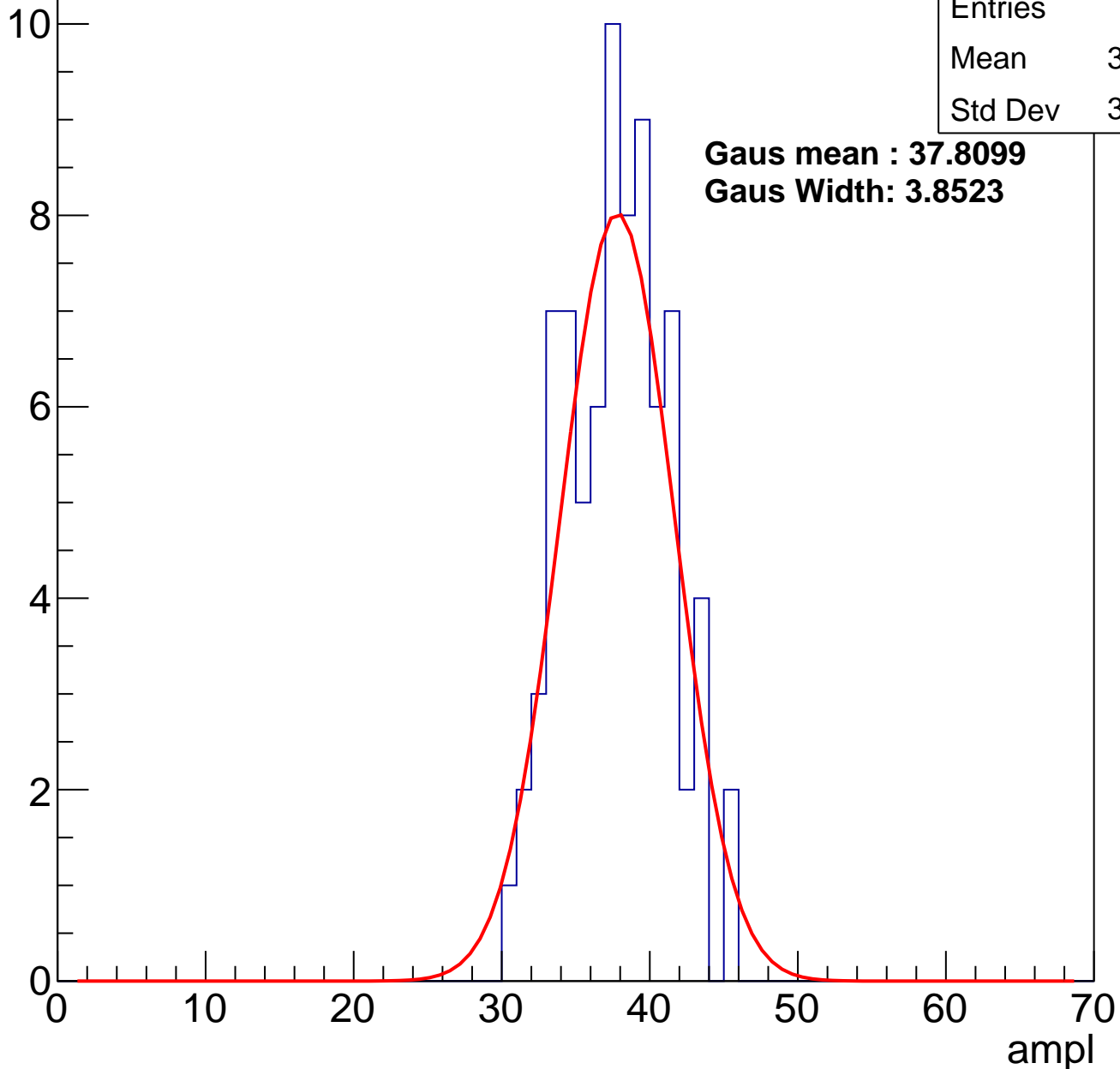
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	37.29
Std Dev	3.417

**Gaus mean : 37.8099**

**Gaus Width: 3.8523**

Entry



# B0L000S, U26-ch87, adc2

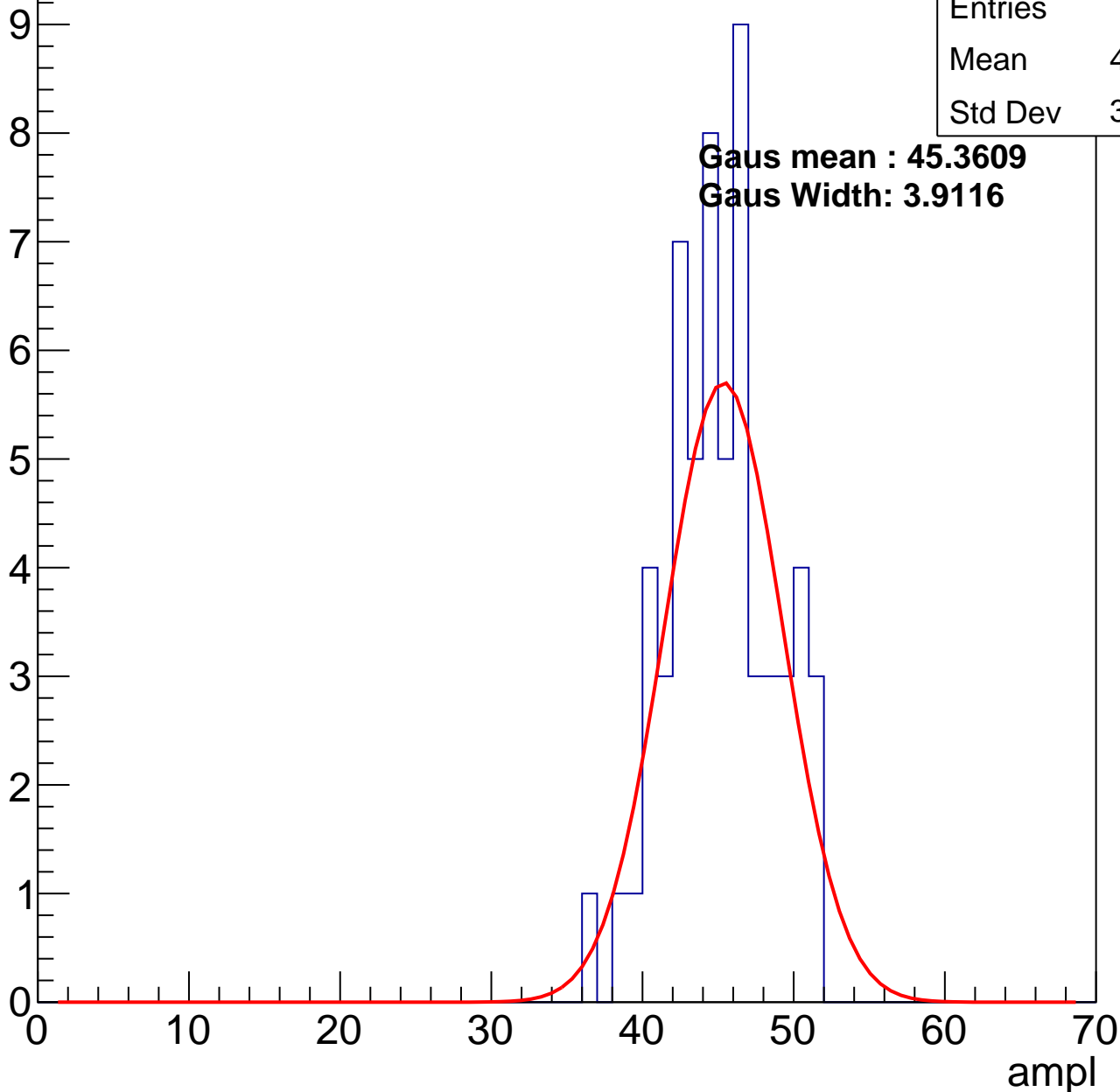
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.68
Std Dev	3.423

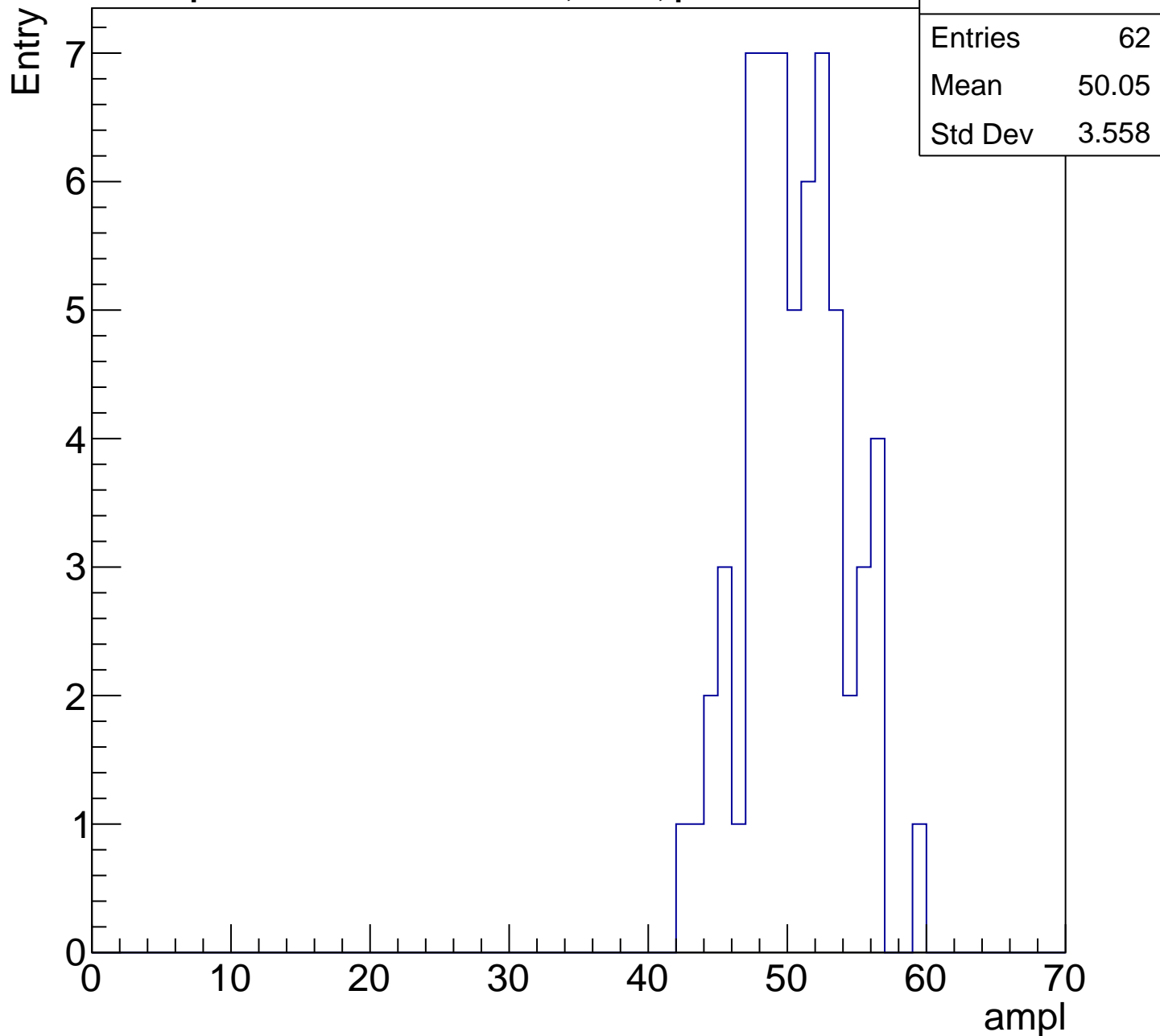
**Gaus mean : 45.3609**

**Gaus Width: 3.9116**



# B0L000S, U26-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

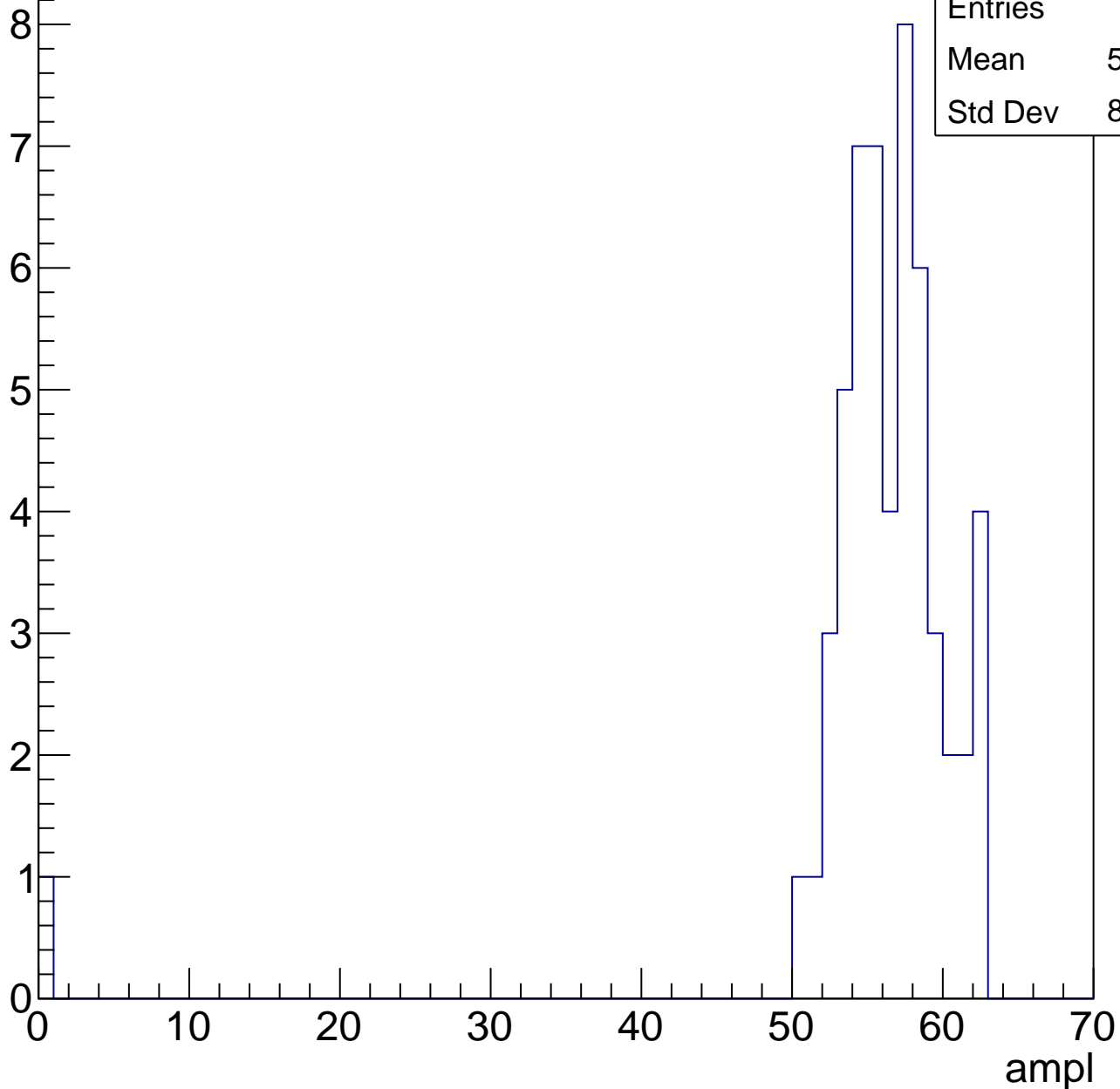


# B0L000S, U26-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.19
Std Dev	8.136

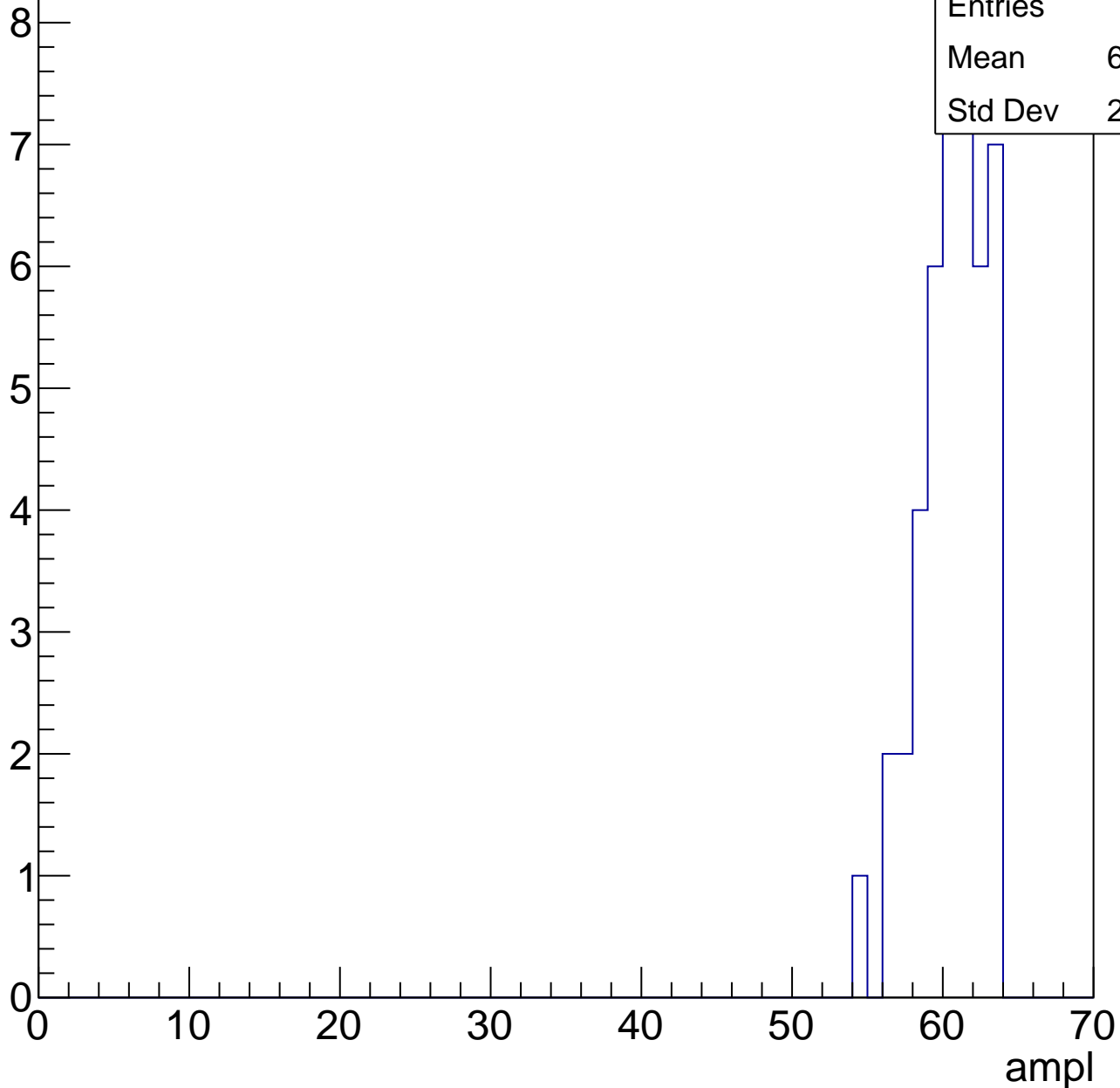


# B0L000S, U26-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	60.16
Std Dev	2.142



# B0L000S, U26-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch88, adc0

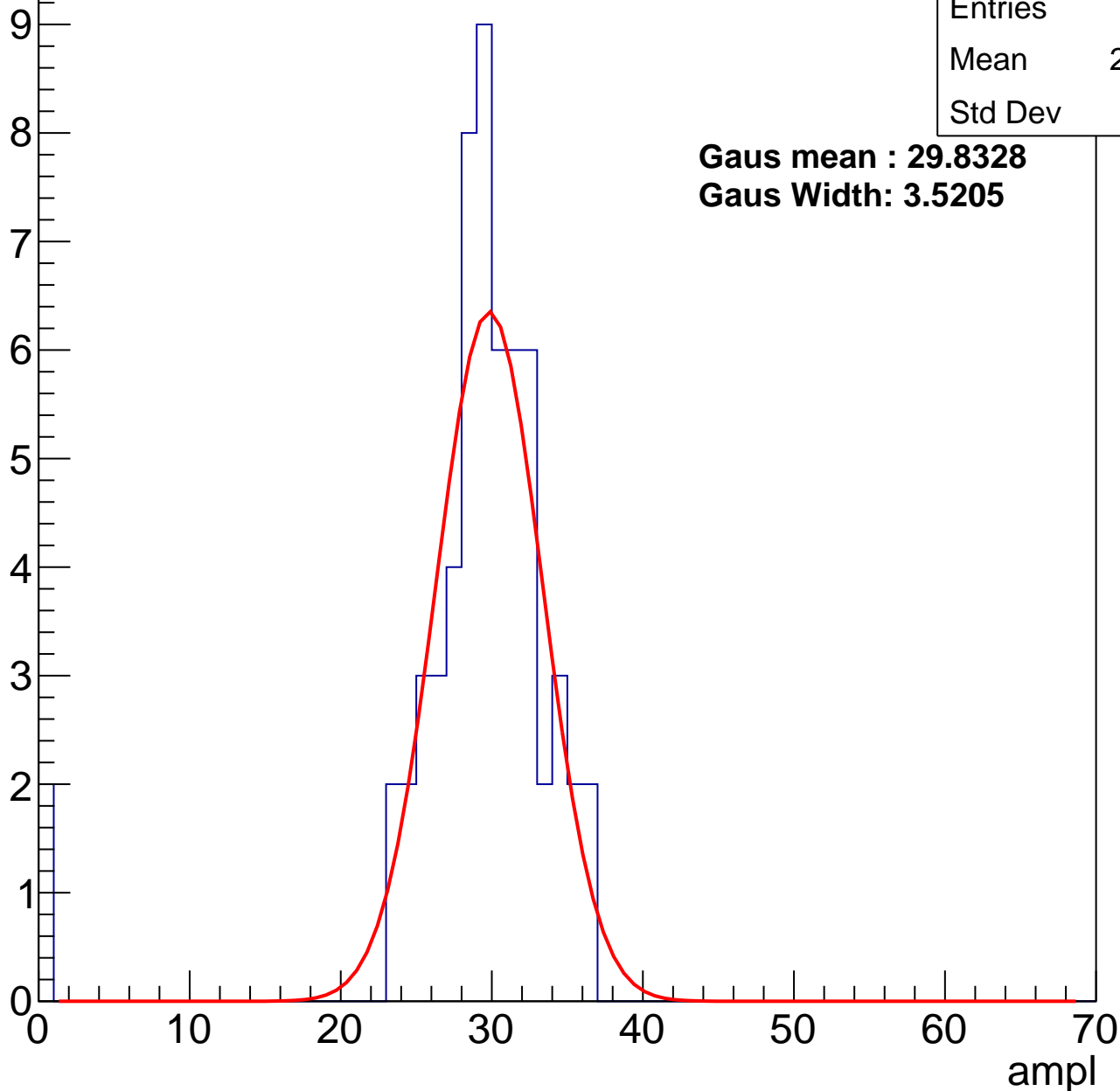
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	28.47
Std Dev	6.12

**Gaus mean : 29.8328**

**Gaus Width: 3.5205**



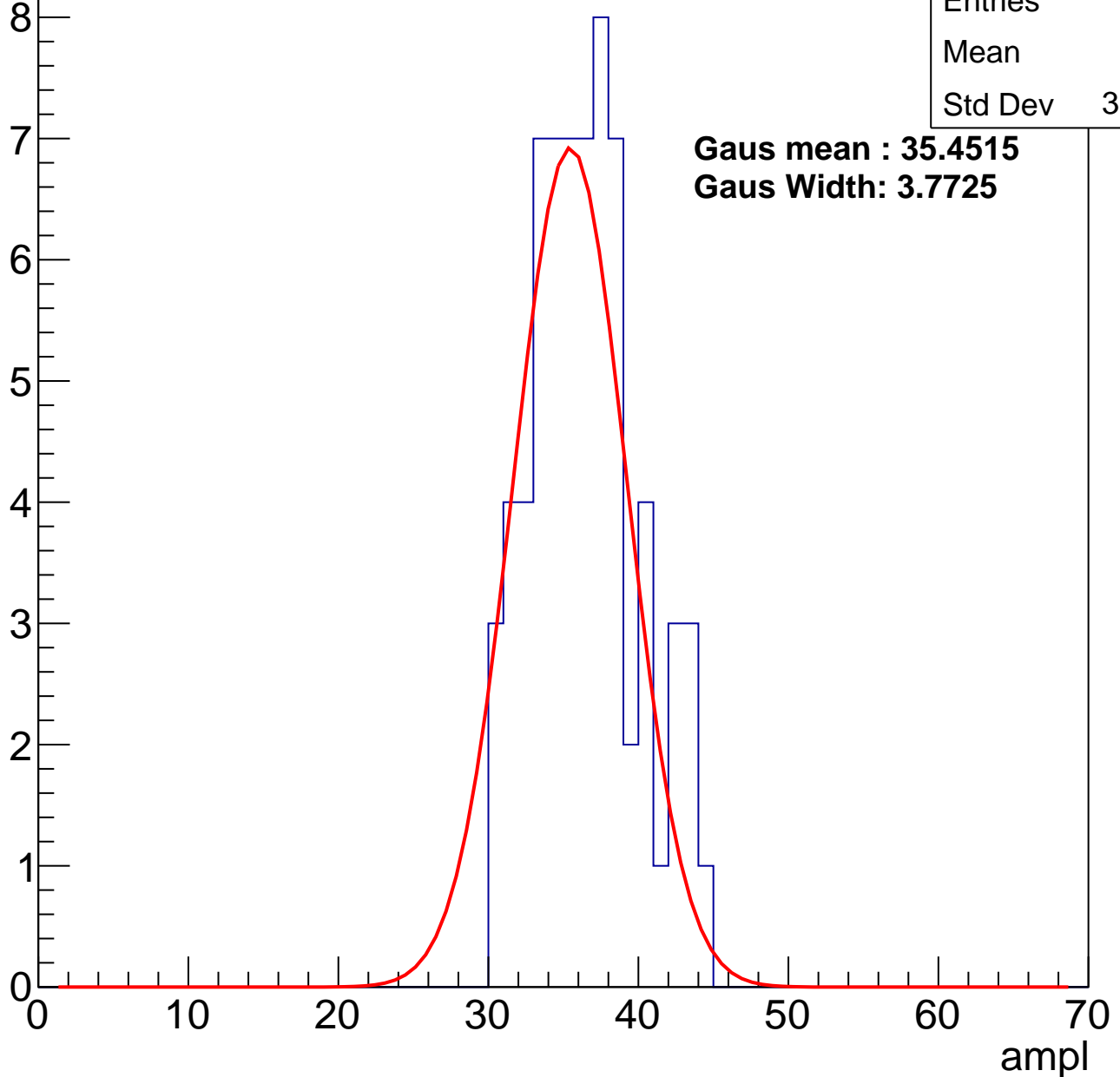
# B0L000S, U26-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36
Std Dev	3.498

**Gaus mean : 35.4515**  
**Gaus Width: 3.7725**



# B0L000S, U26-ch88, adc2

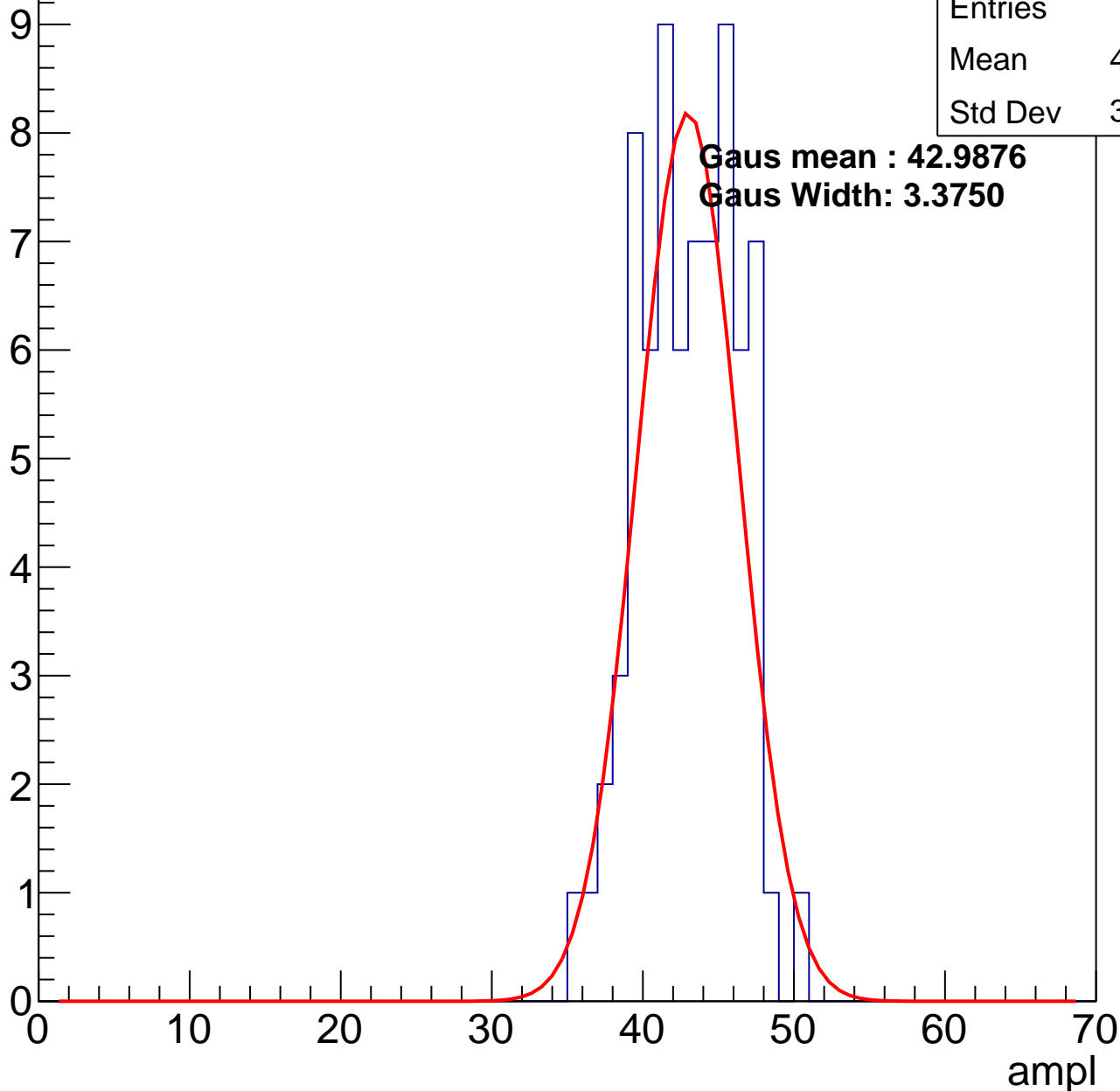
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.55
Std Dev	3.188

**Gaus mean : 42.9876**

**Gaus Width: 3.3750**

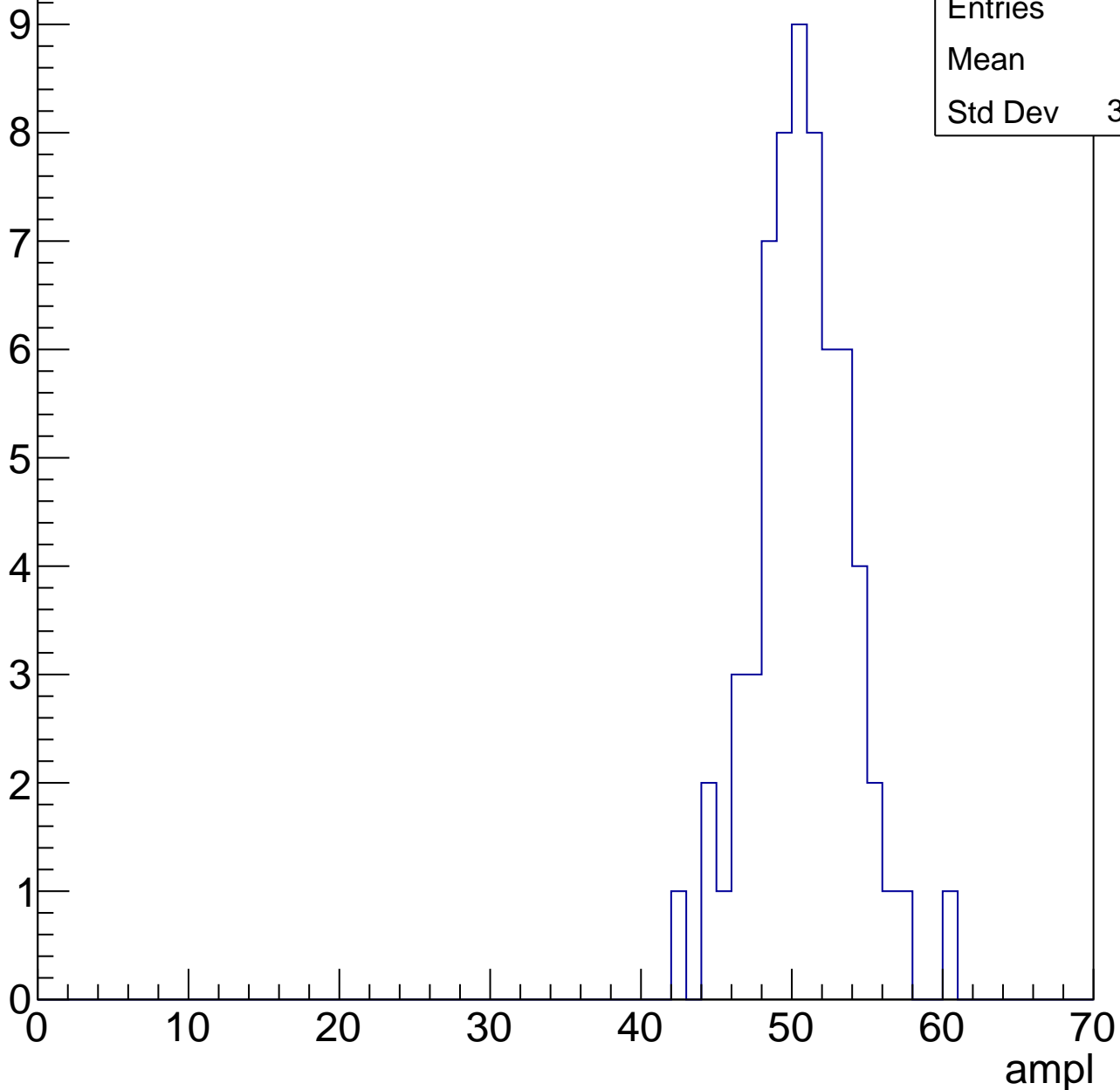


# B0L000S, U26-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	50.3
Std Dev	3.215

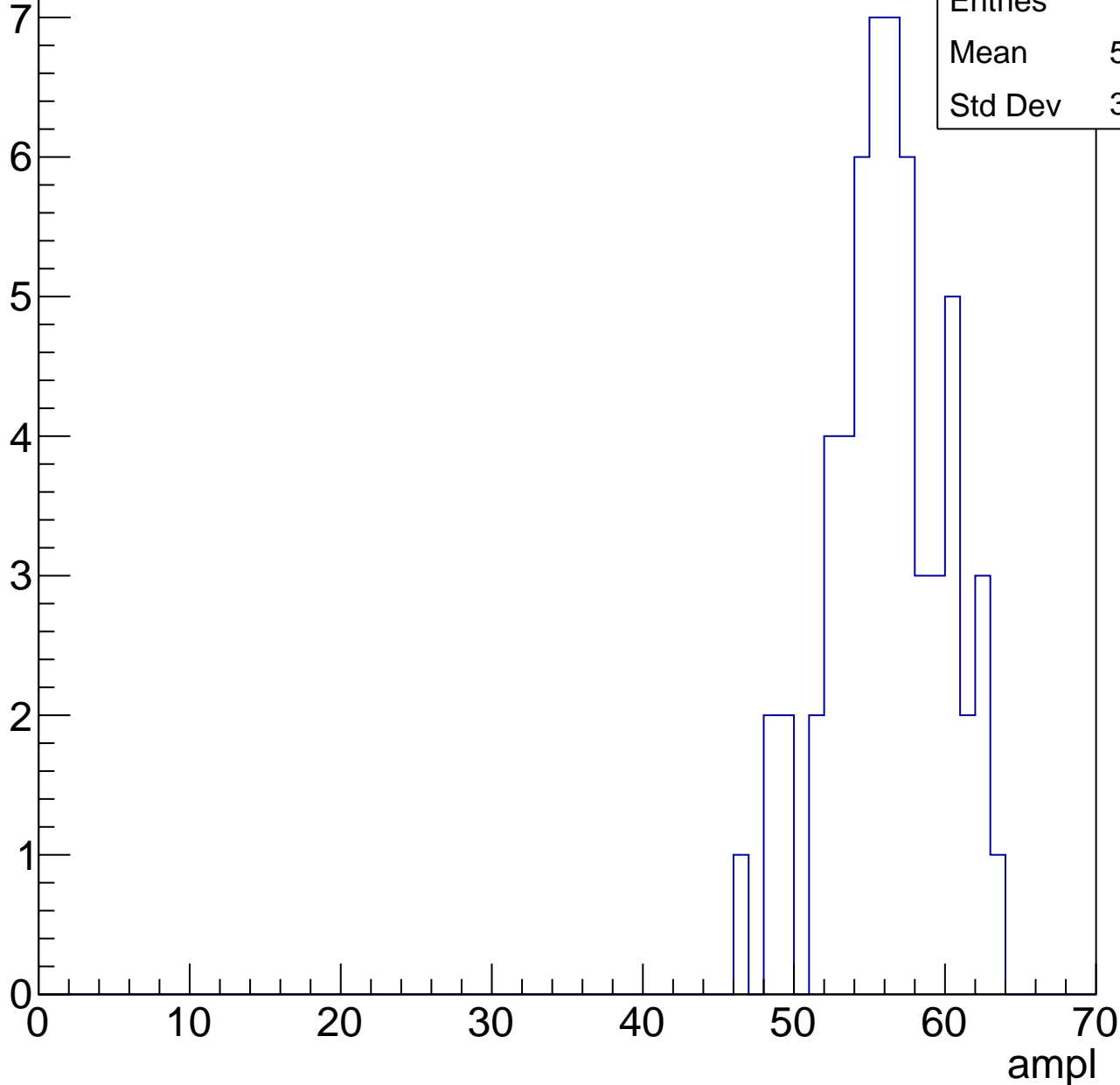


# B0L000S, U26-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

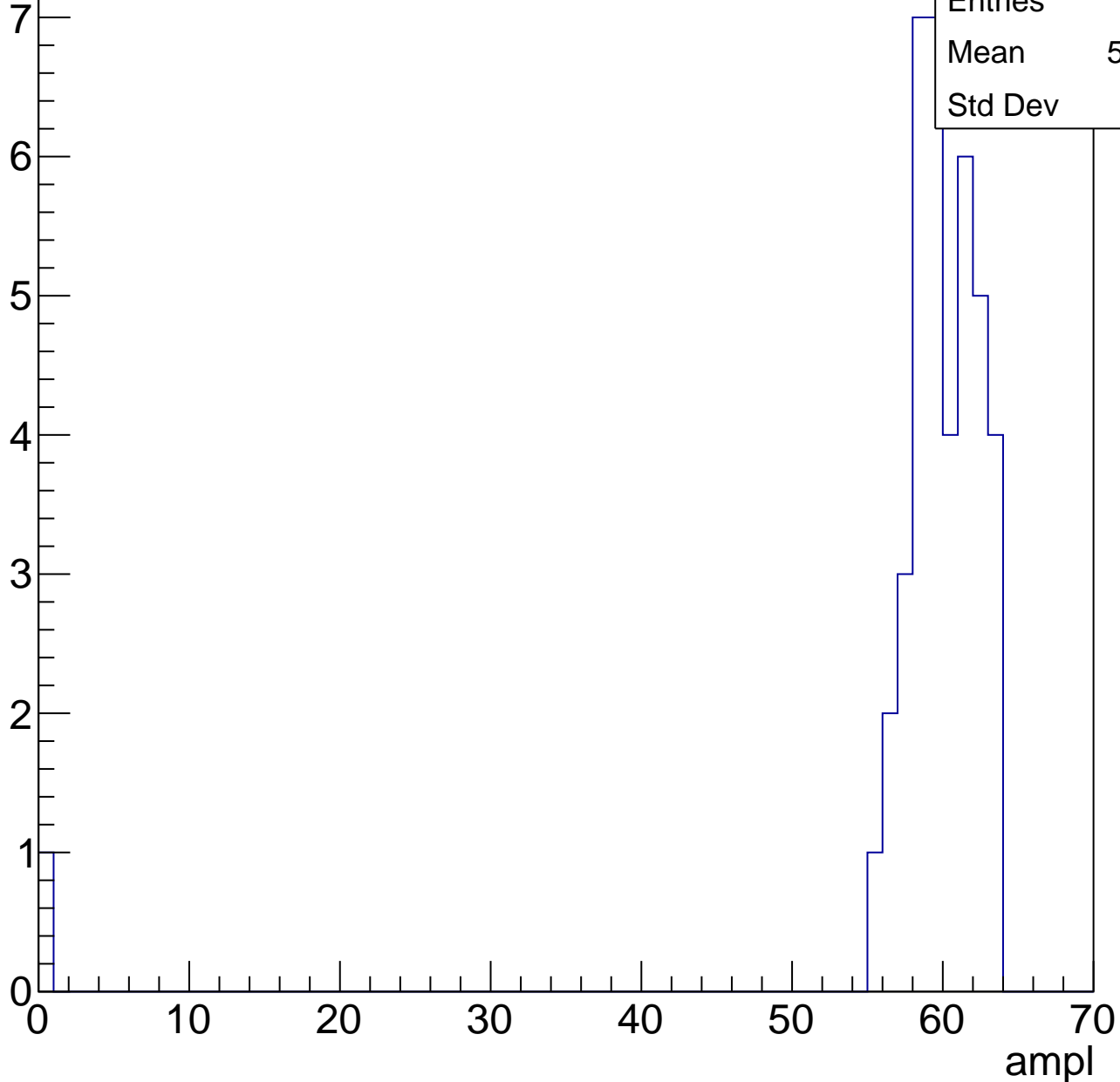
Entries	58
Mean	55.64
Std Dev	3.786



# B0L000S, U26-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

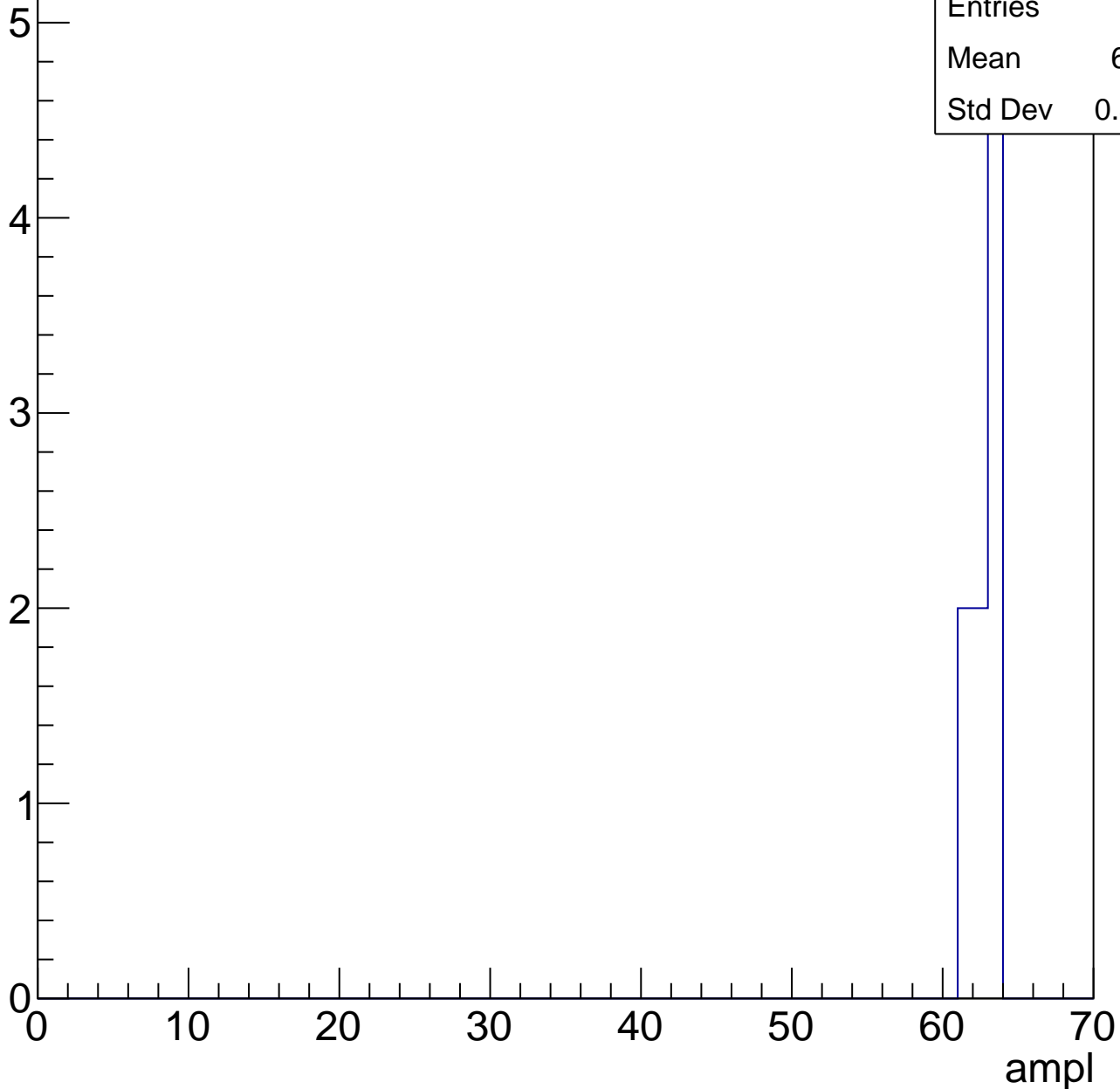


# B0L000S, U26-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.33
Std Dev	0.8165

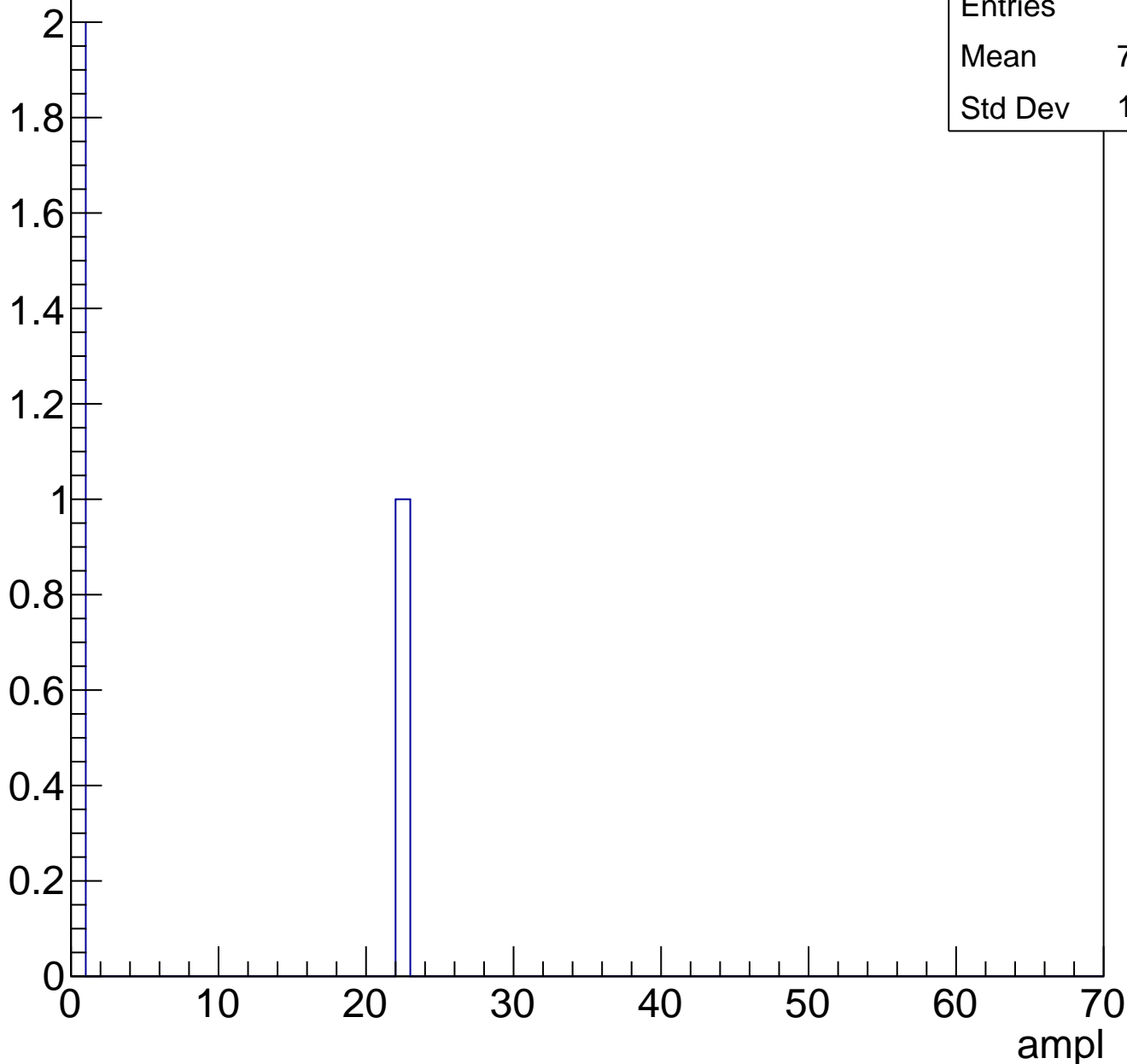




# B0L000S, U26-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U26-ch89, adc0

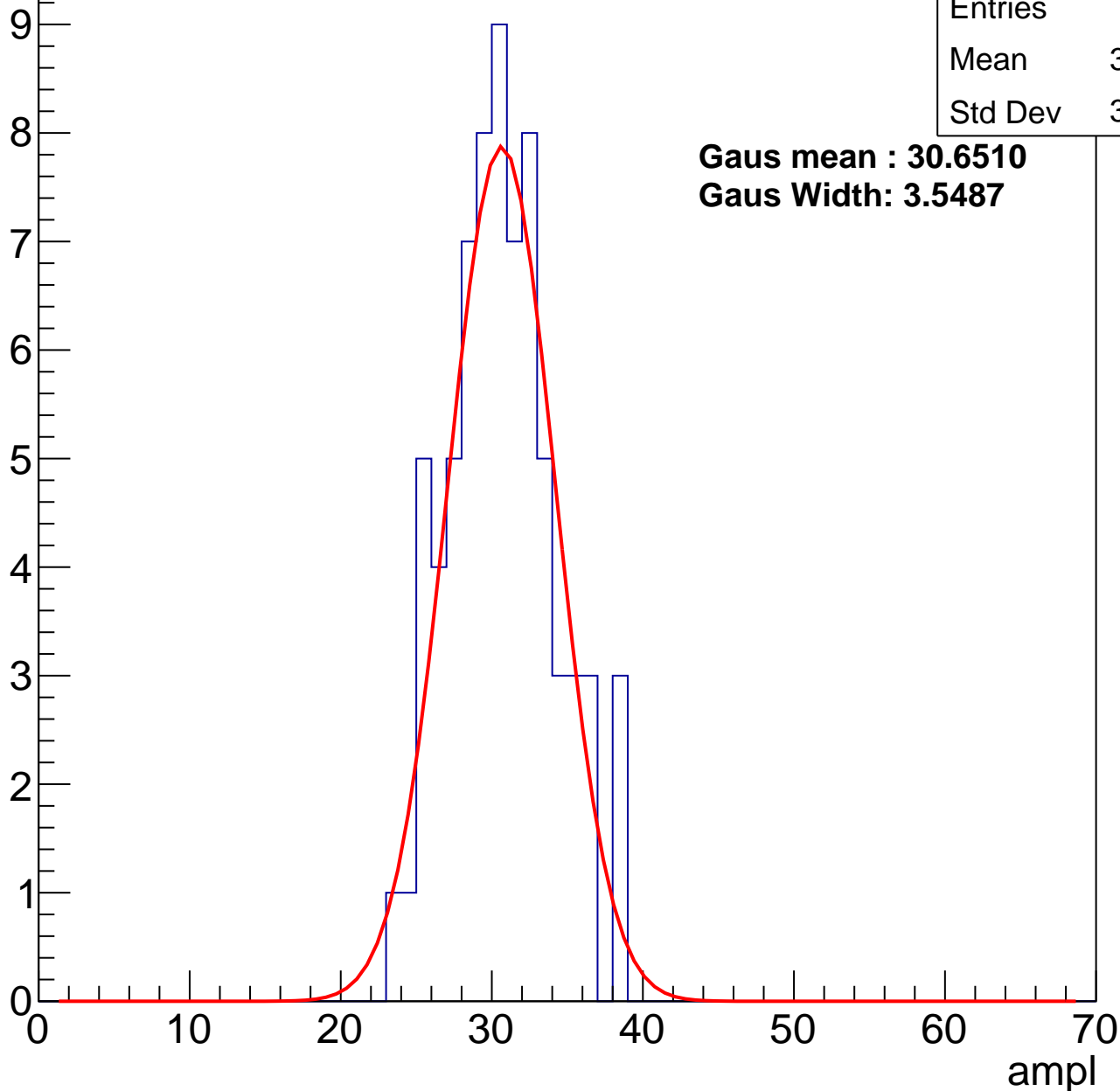
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.22
Std Dev	3.453

**Gaus mean : 30.6510**

**Gaus Width: 3.5487**



# B0L000S, U26-ch89, adc1

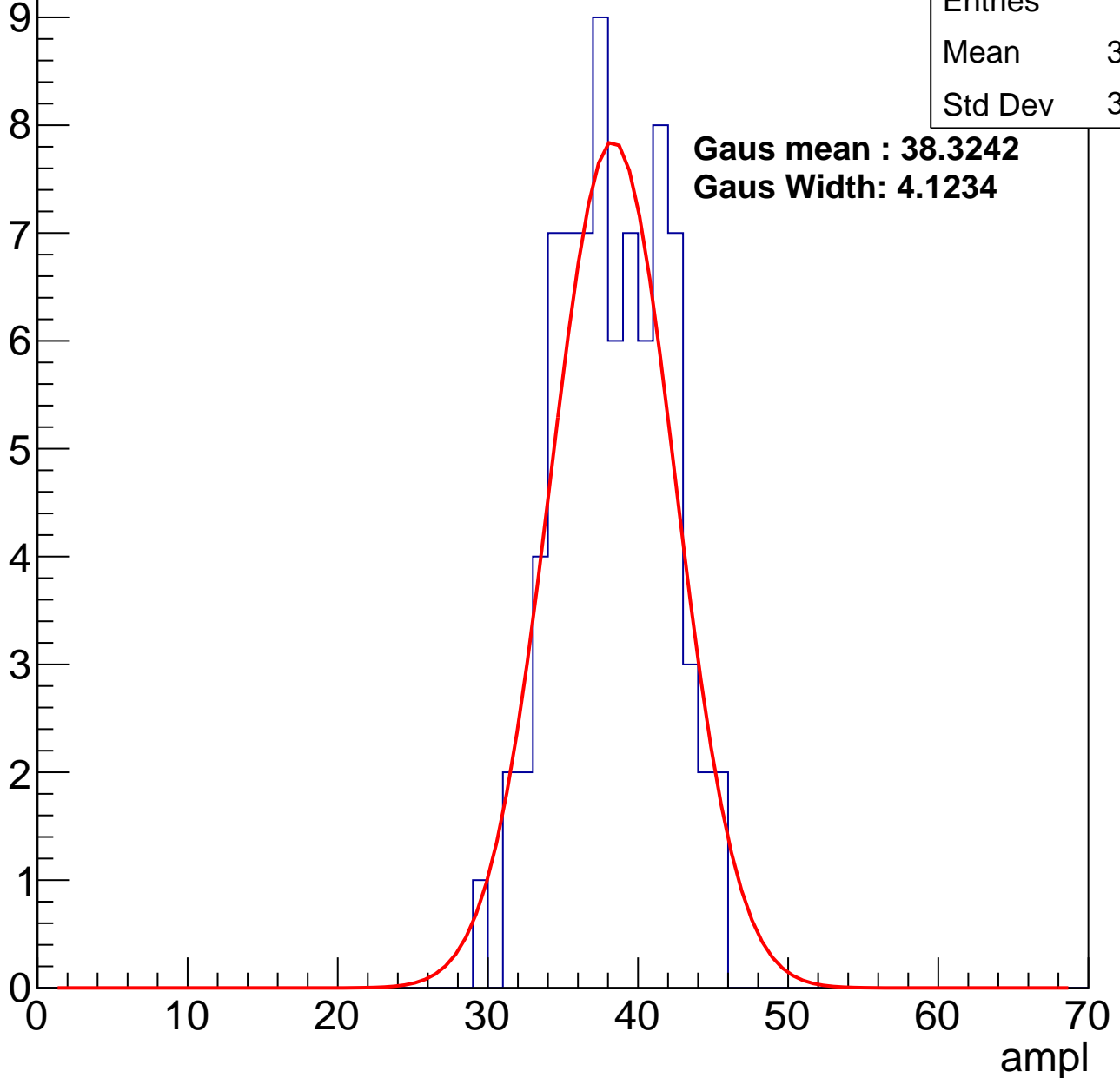
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	37.81
Std Dev	3.571

**Gaus mean : 38.3242**

**Gaus Width: 4.1234**



# B0L000S, U26-ch89, adc2

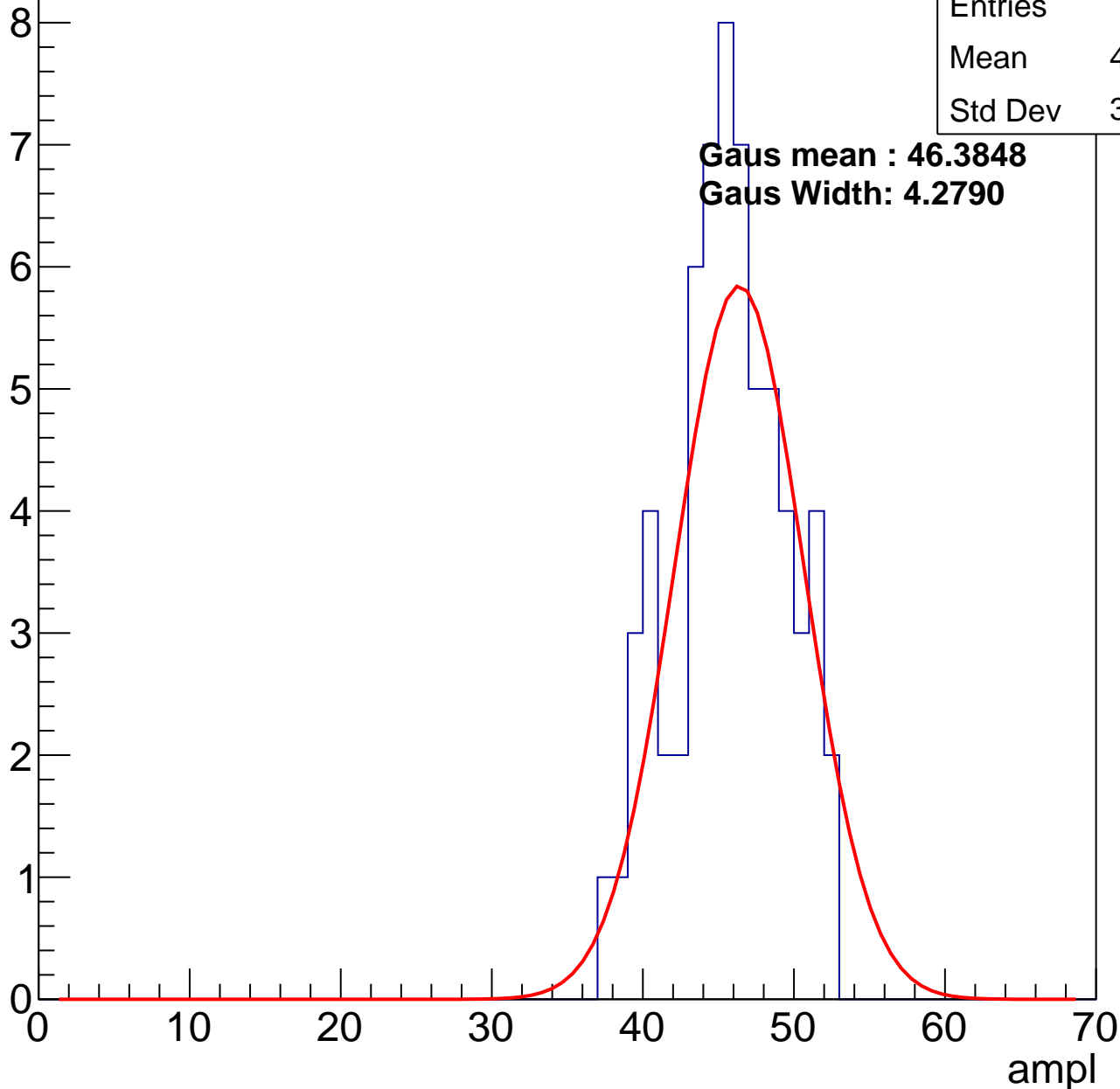
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	45.23
Std Dev	3.648

**Gaus mean : 46.3848**

**Gaus Width: 4.2790**

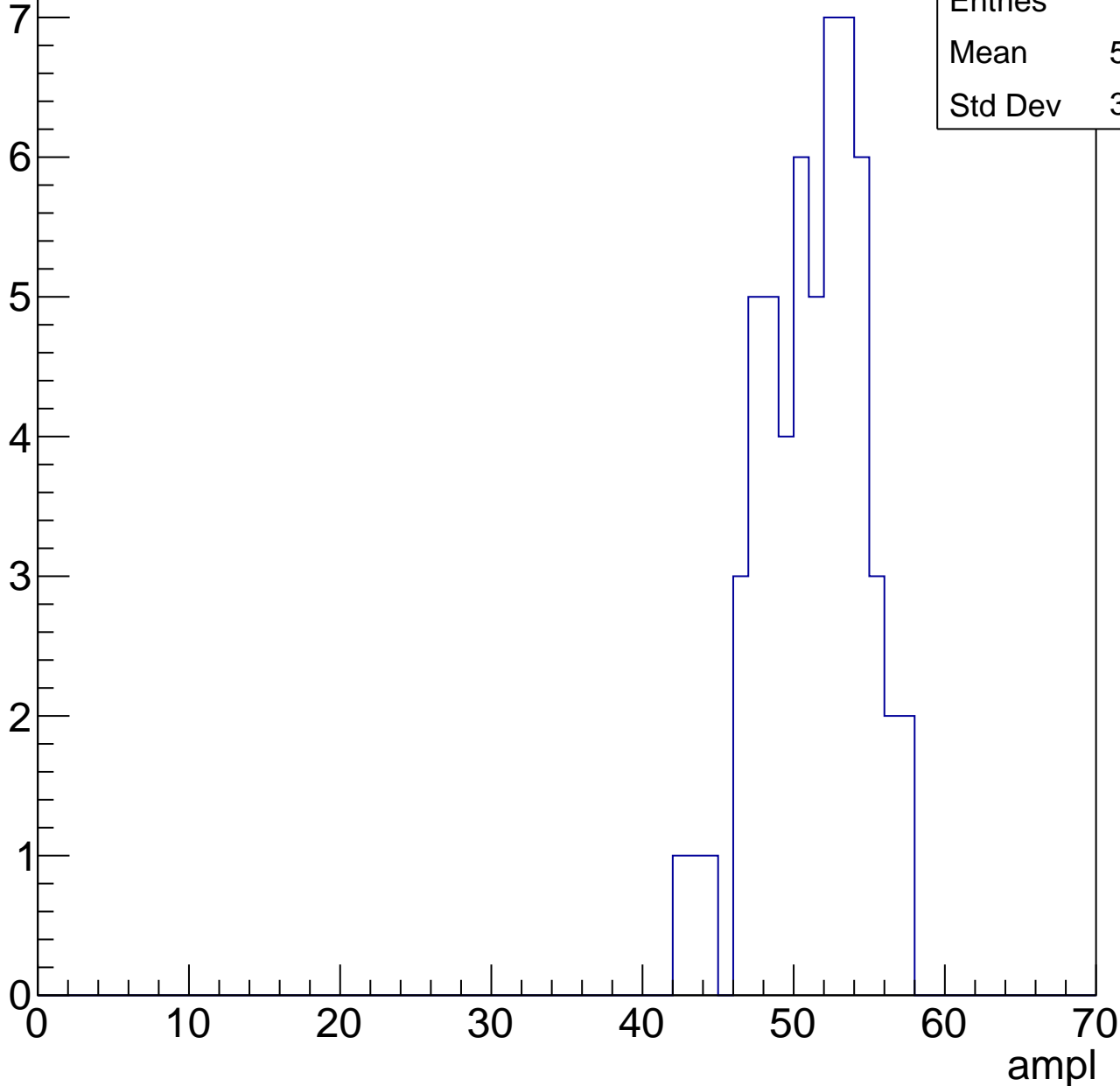


# B0L000S, U26-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	50.74
Std Dev	3.402

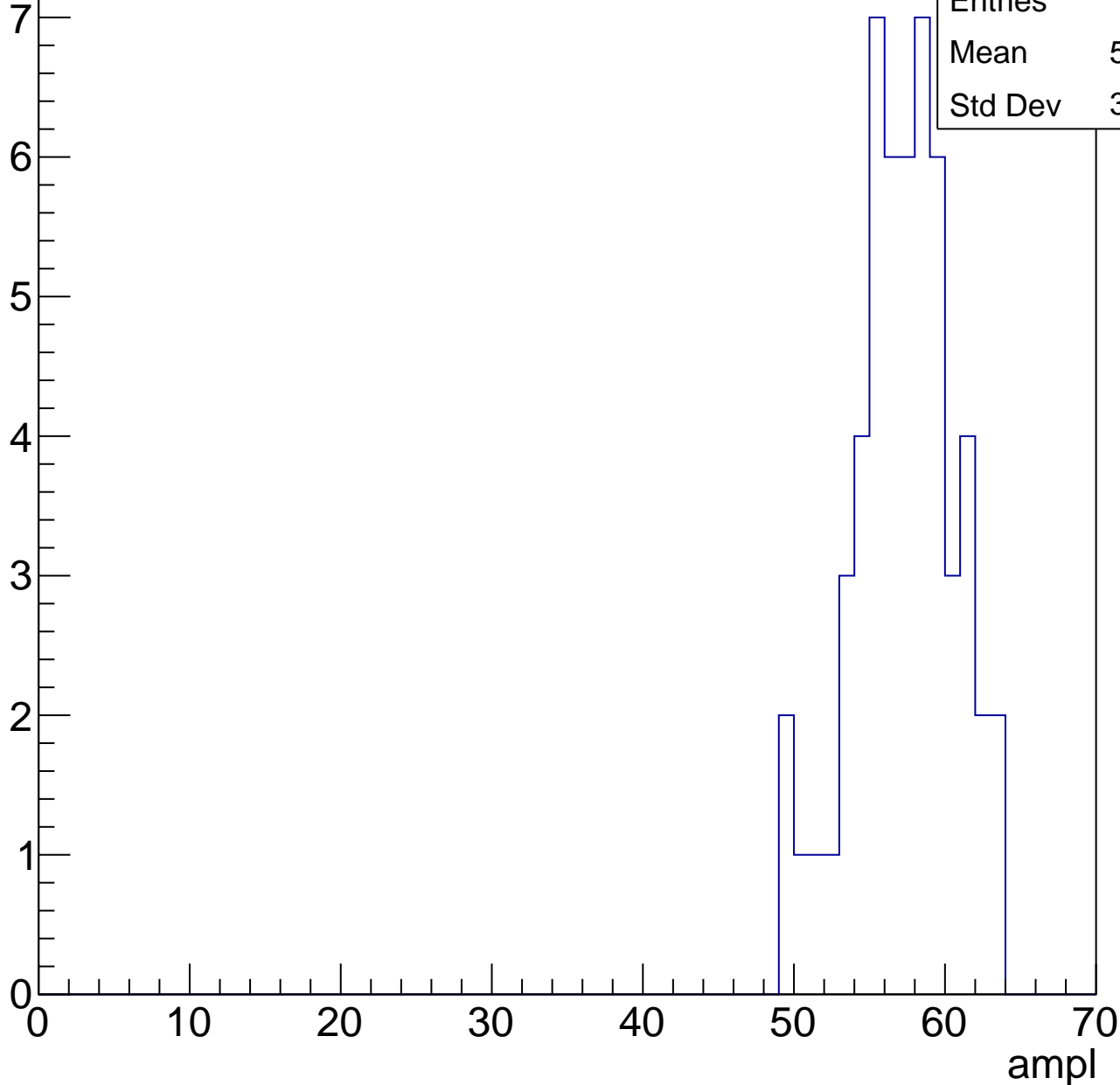


# B0L000S, U26-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.78
Std Dev	3.296

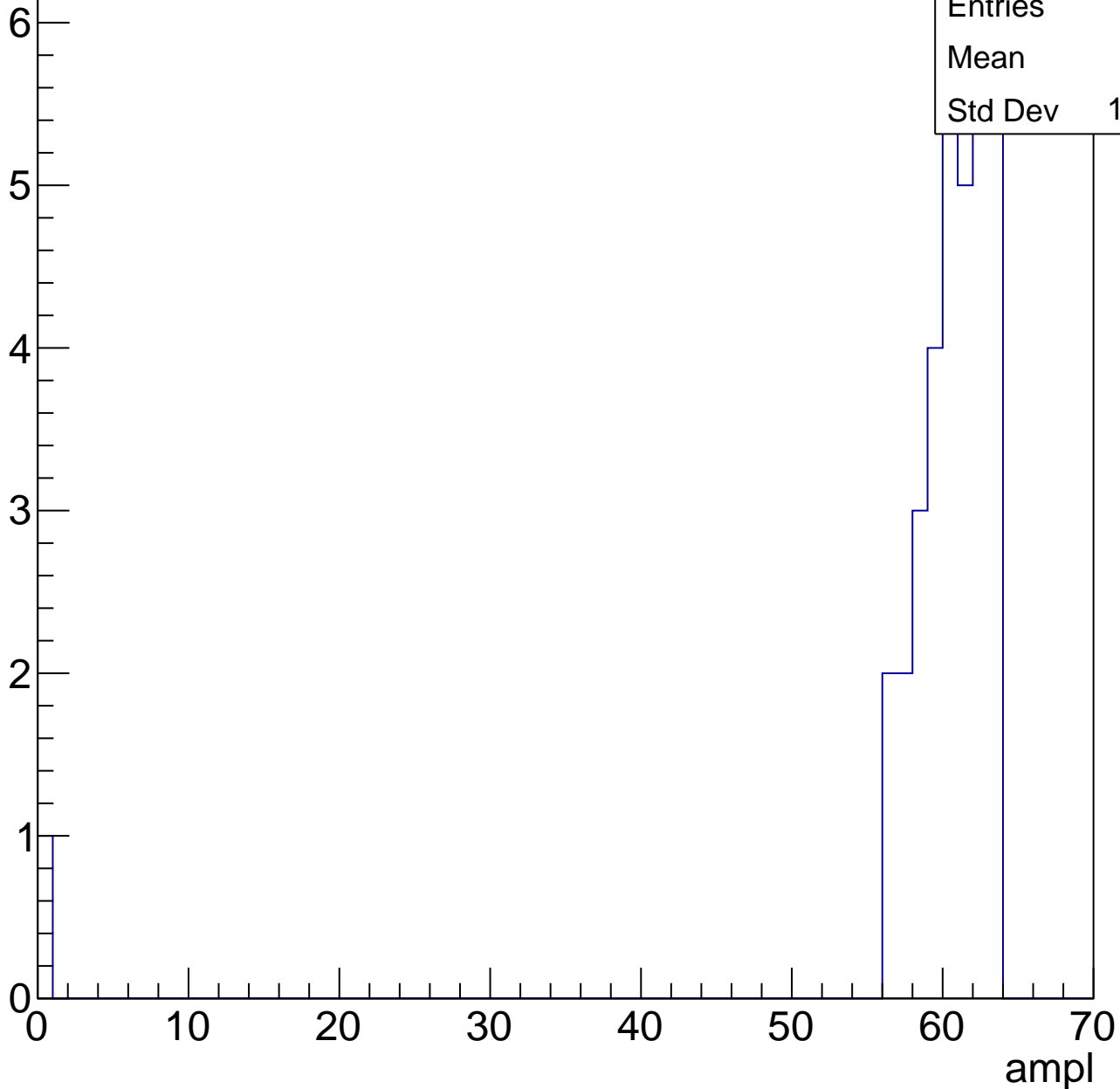


# B0L000S, U26-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.6
Std Dev	10.25



# B0L000S, U26-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U26-ch90, adc0

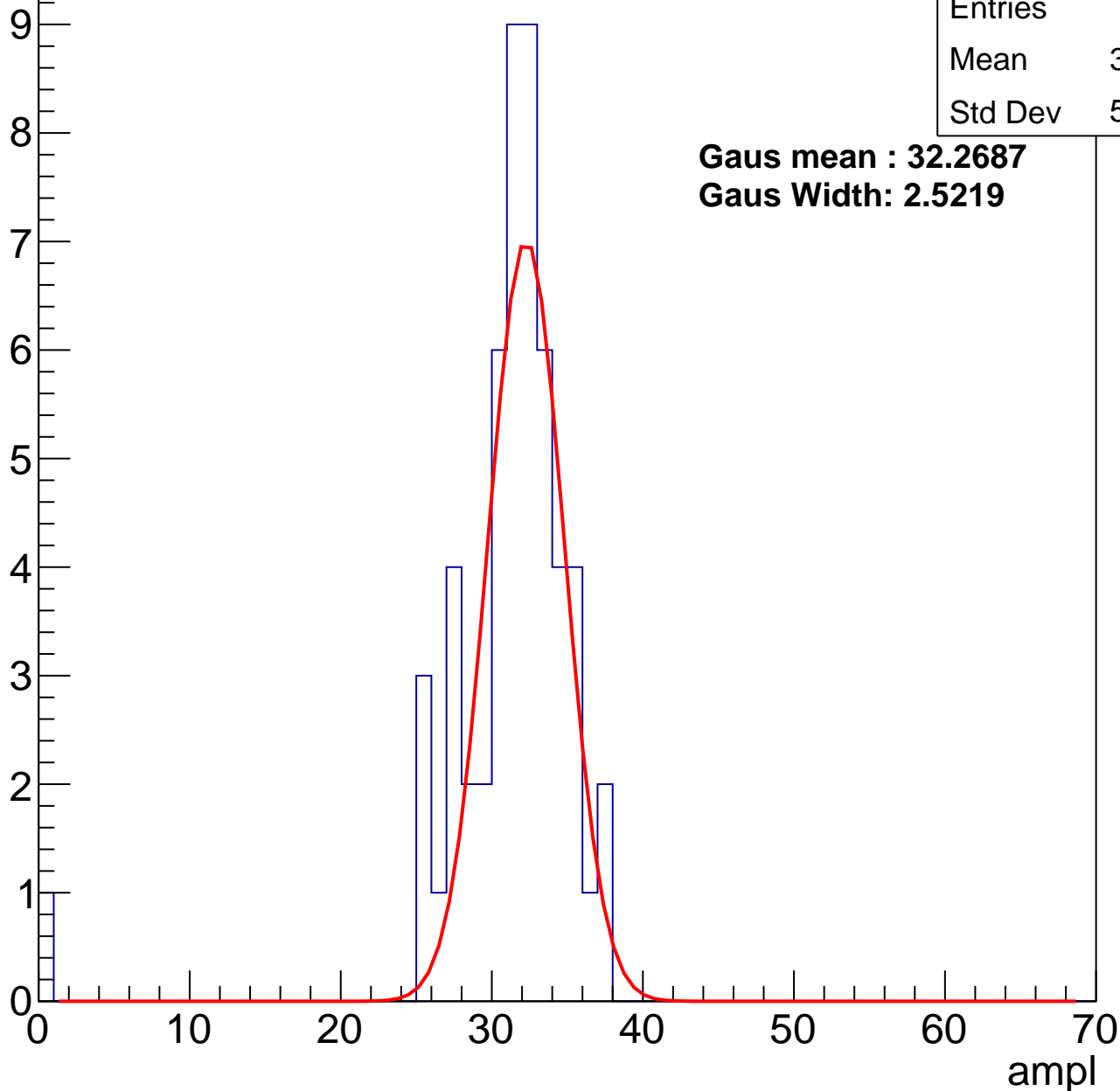
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	30.63
Std Dev	5.115

**Gaus mean : 32.2687**

**Gaus Width: 2.5219**



# B0L000S, U26-ch90, adc1

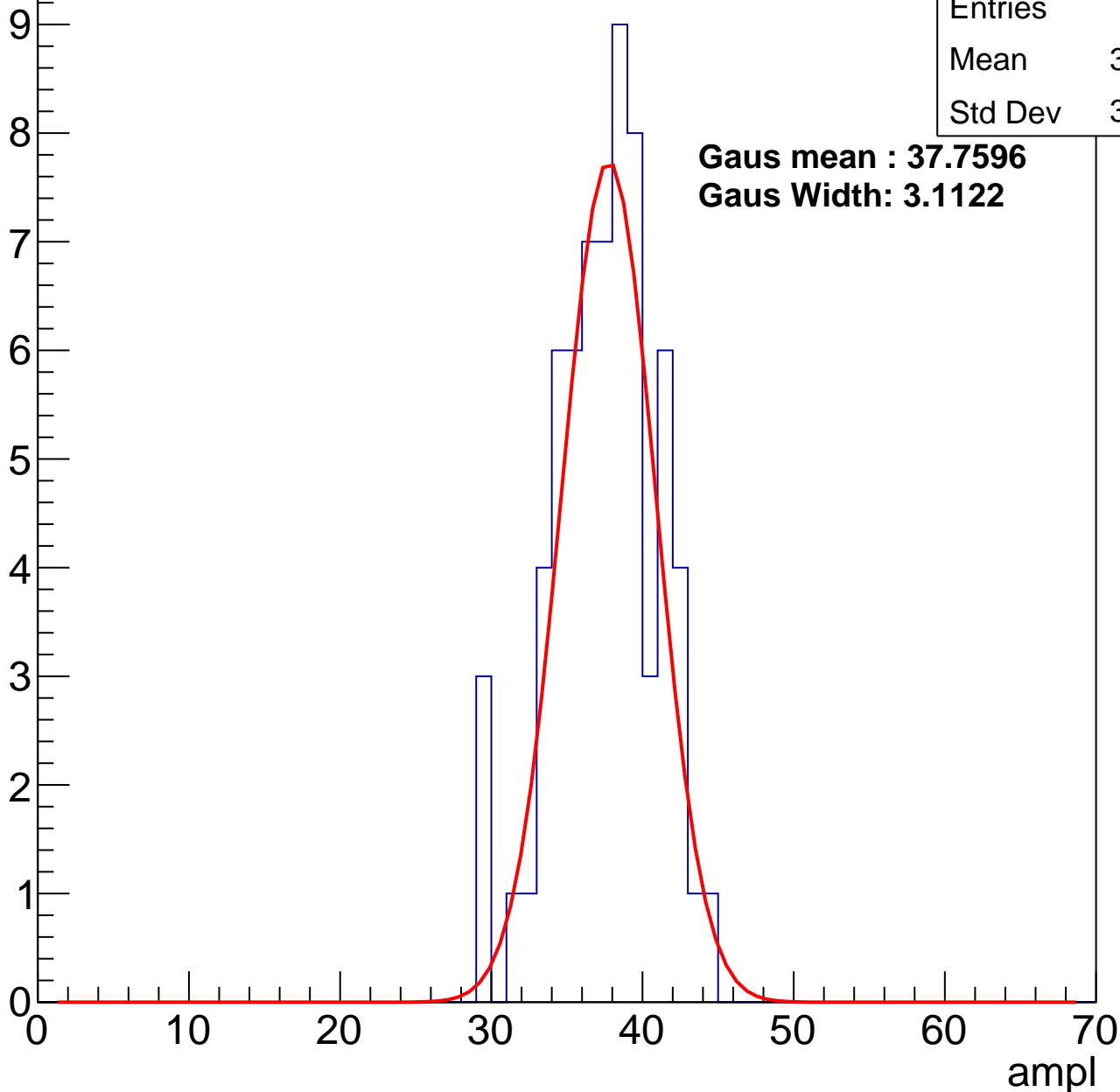
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.04
Std Dev	3.348

**Gaus mean : 37.7596**

**Gaus Width: 3.1122**



# B0L000S, U26-ch90, adc2

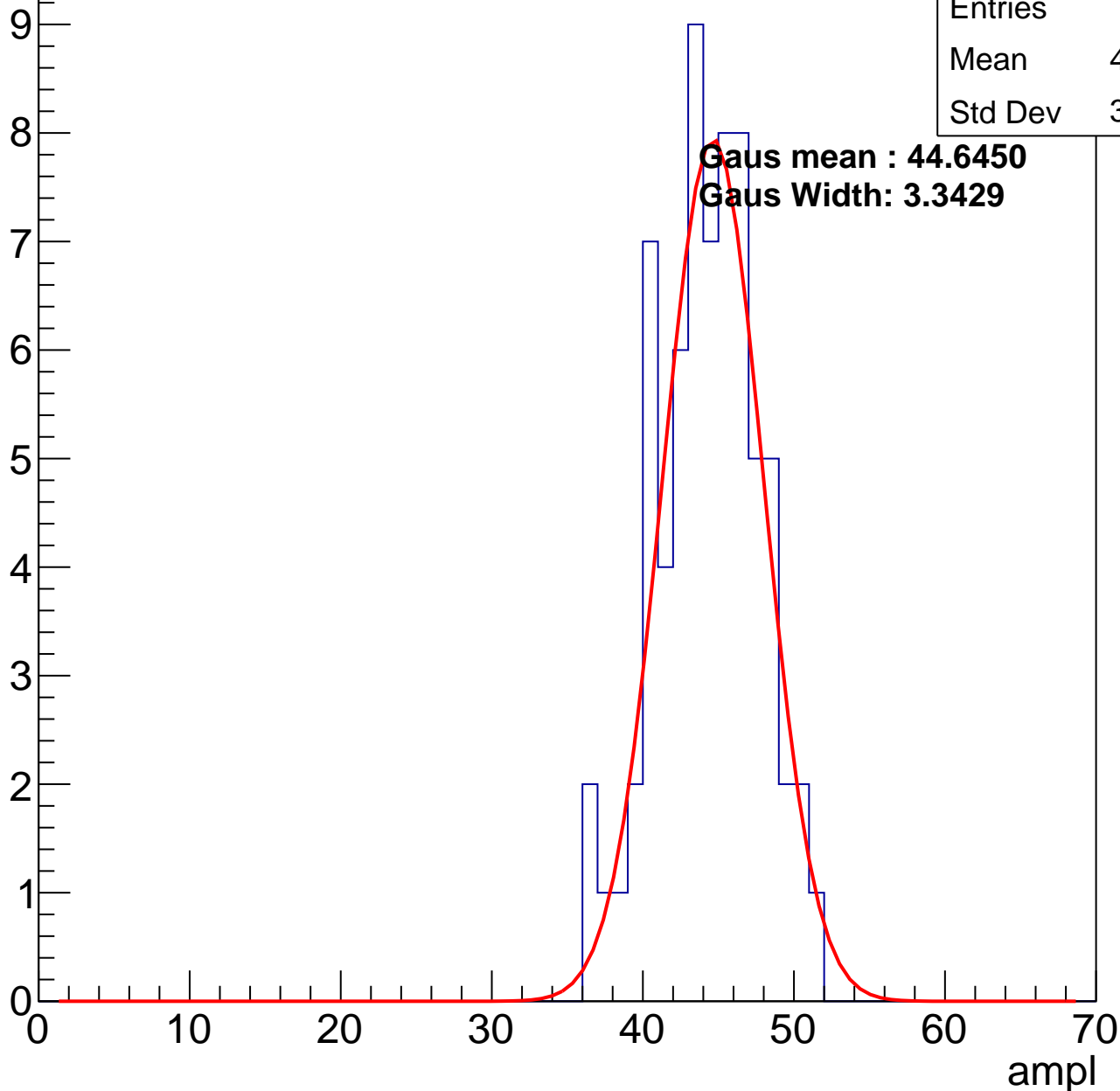
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.83
Std Dev	3.334

**Gaus mean : 44.6450**

**Gaus Width: 3.3429**

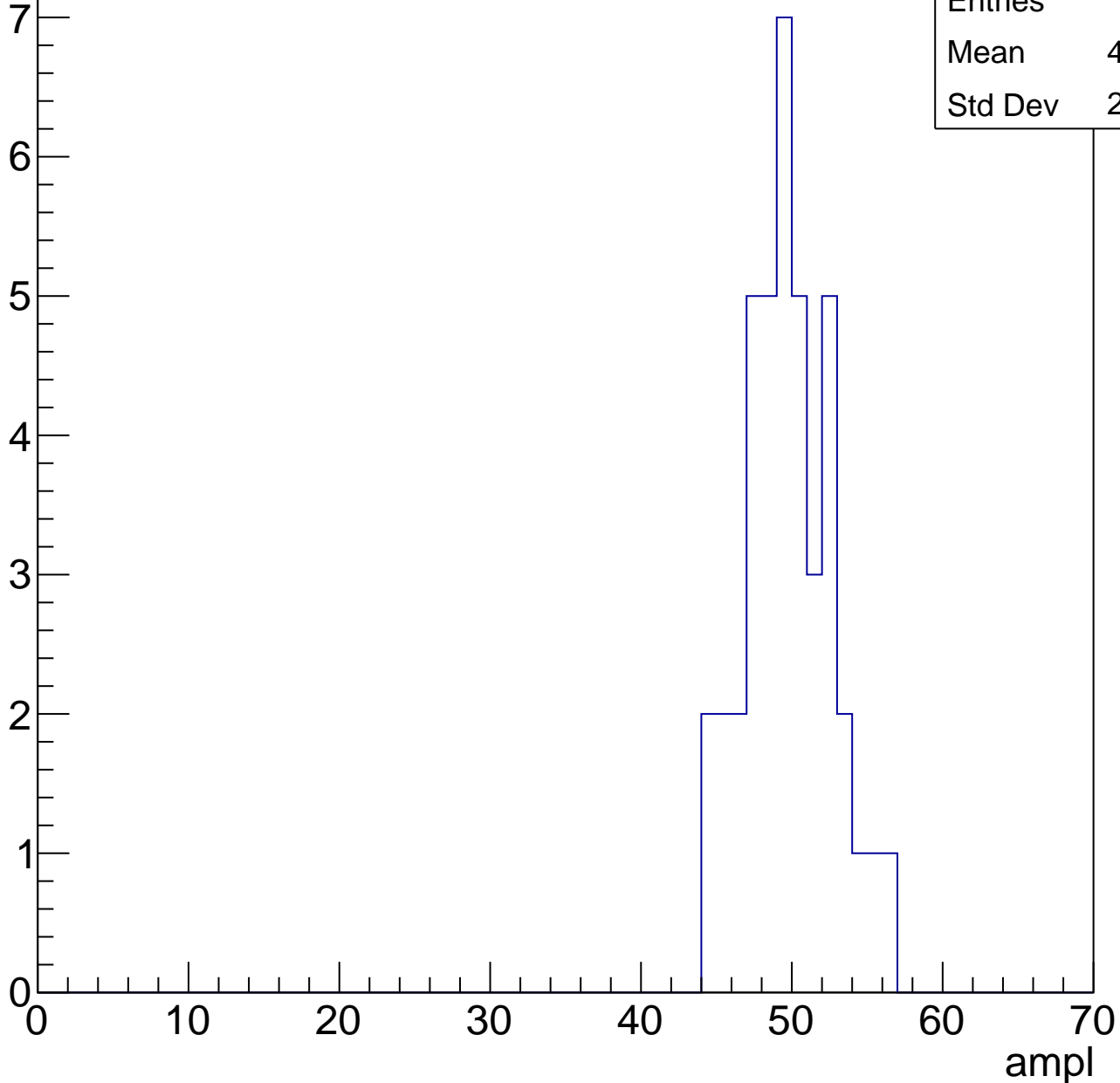


# B0L000S, U26-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	49.32
Std Dev	2.815

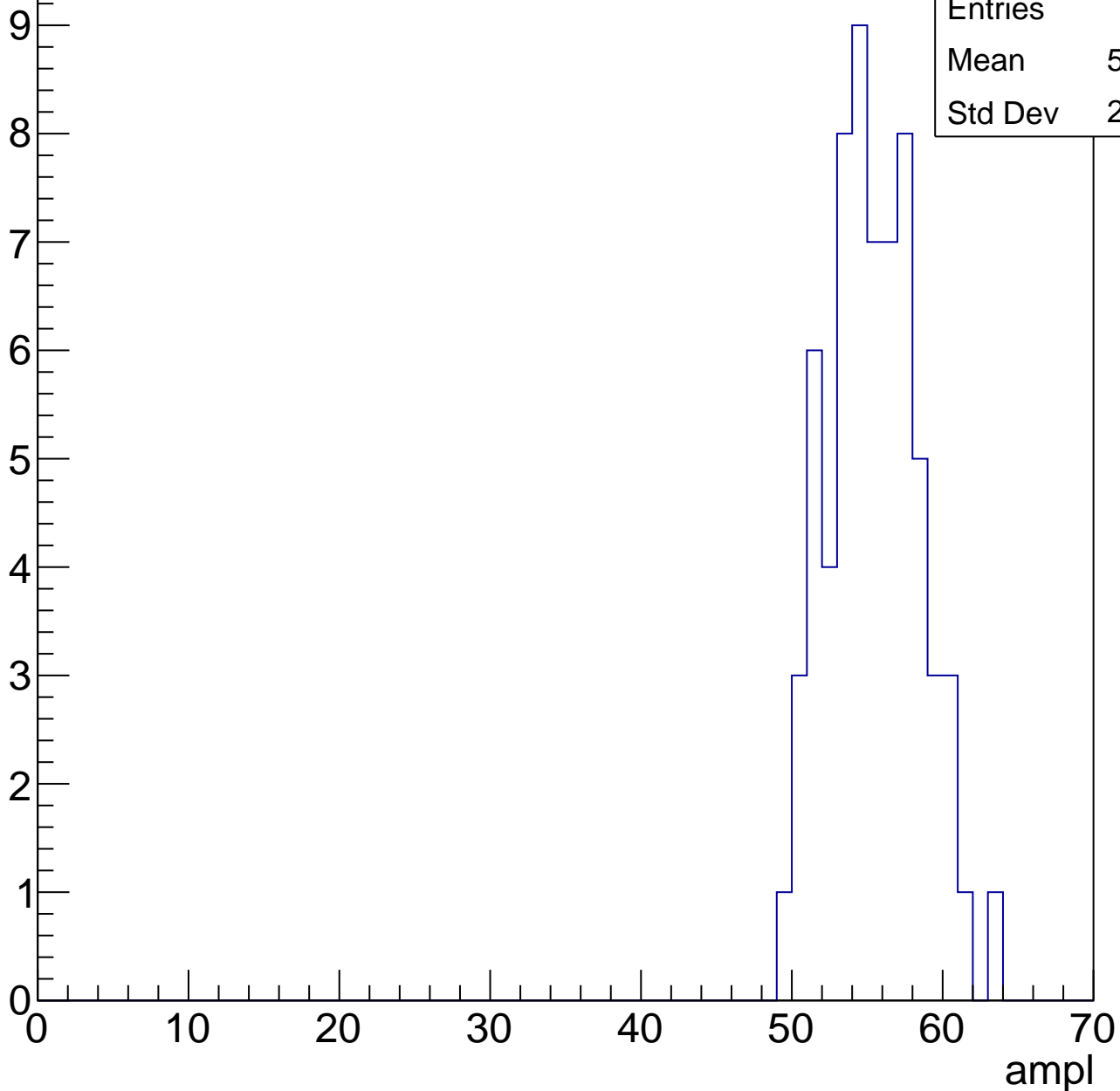


# B0L000S, U26-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	54.95
Std Dev	2.992

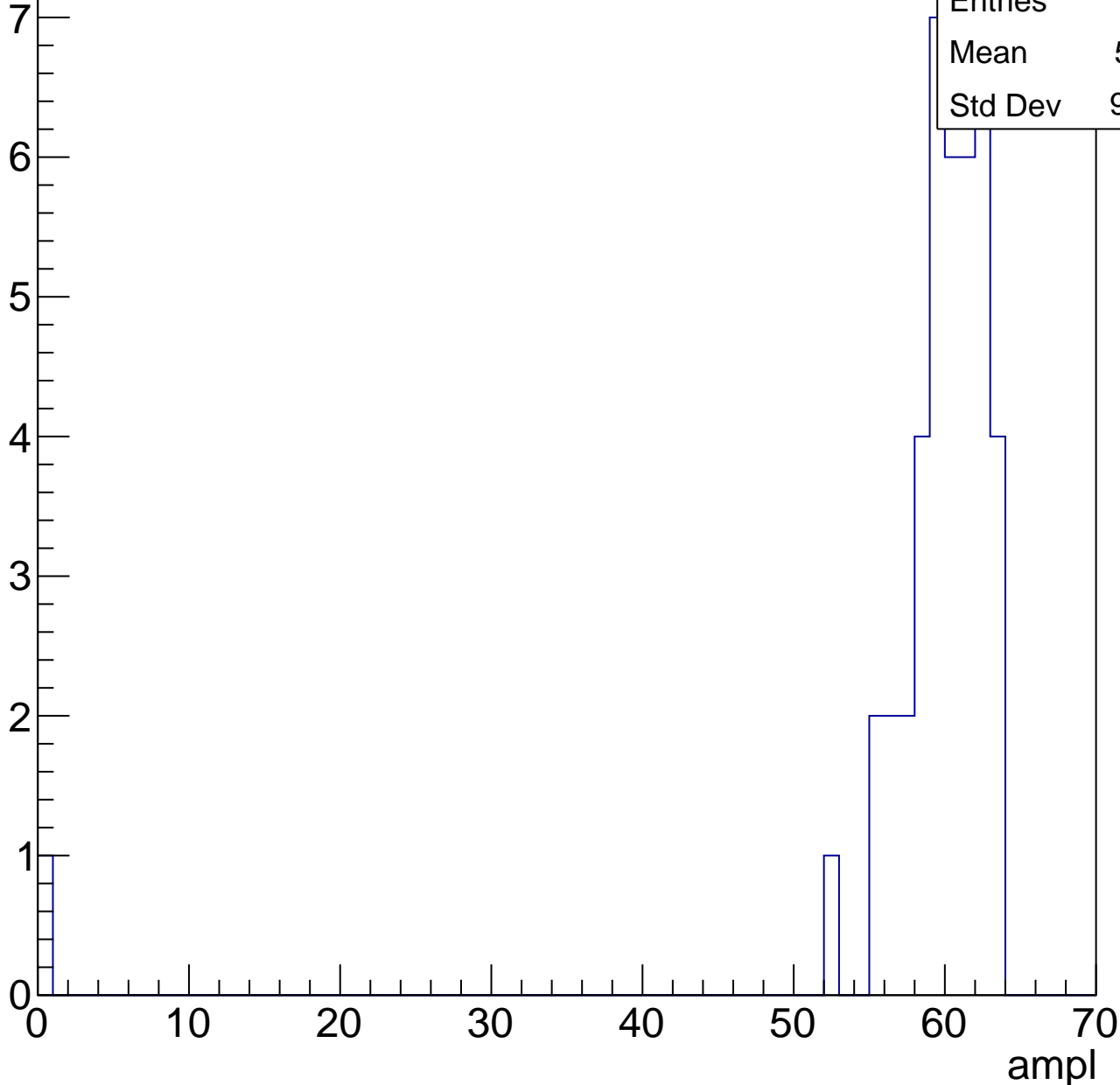


# B0L000S, U26-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.21
Std Dev	9.415

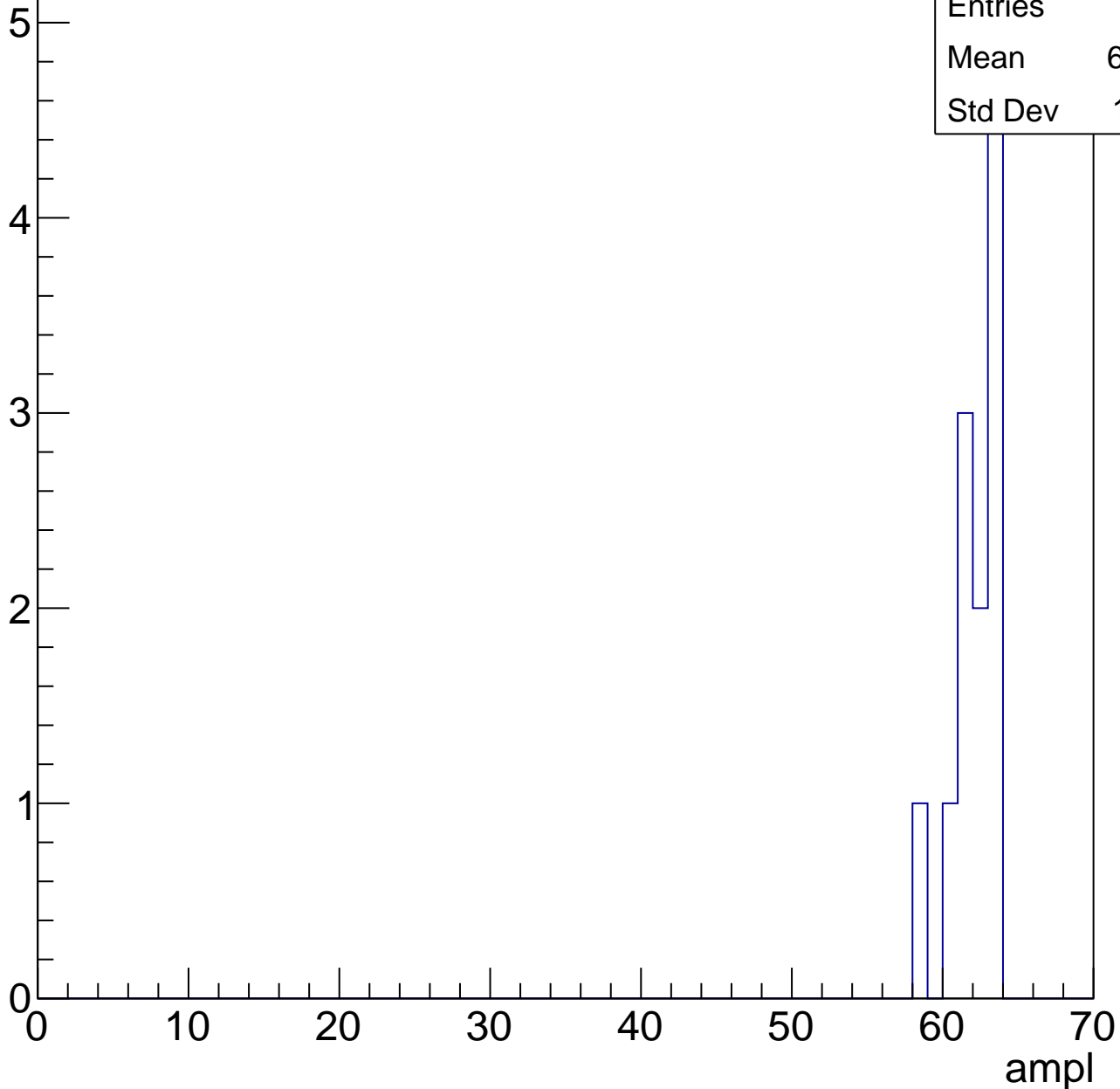


# B0L000S, U26-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	61.67
Std Dev	1.491

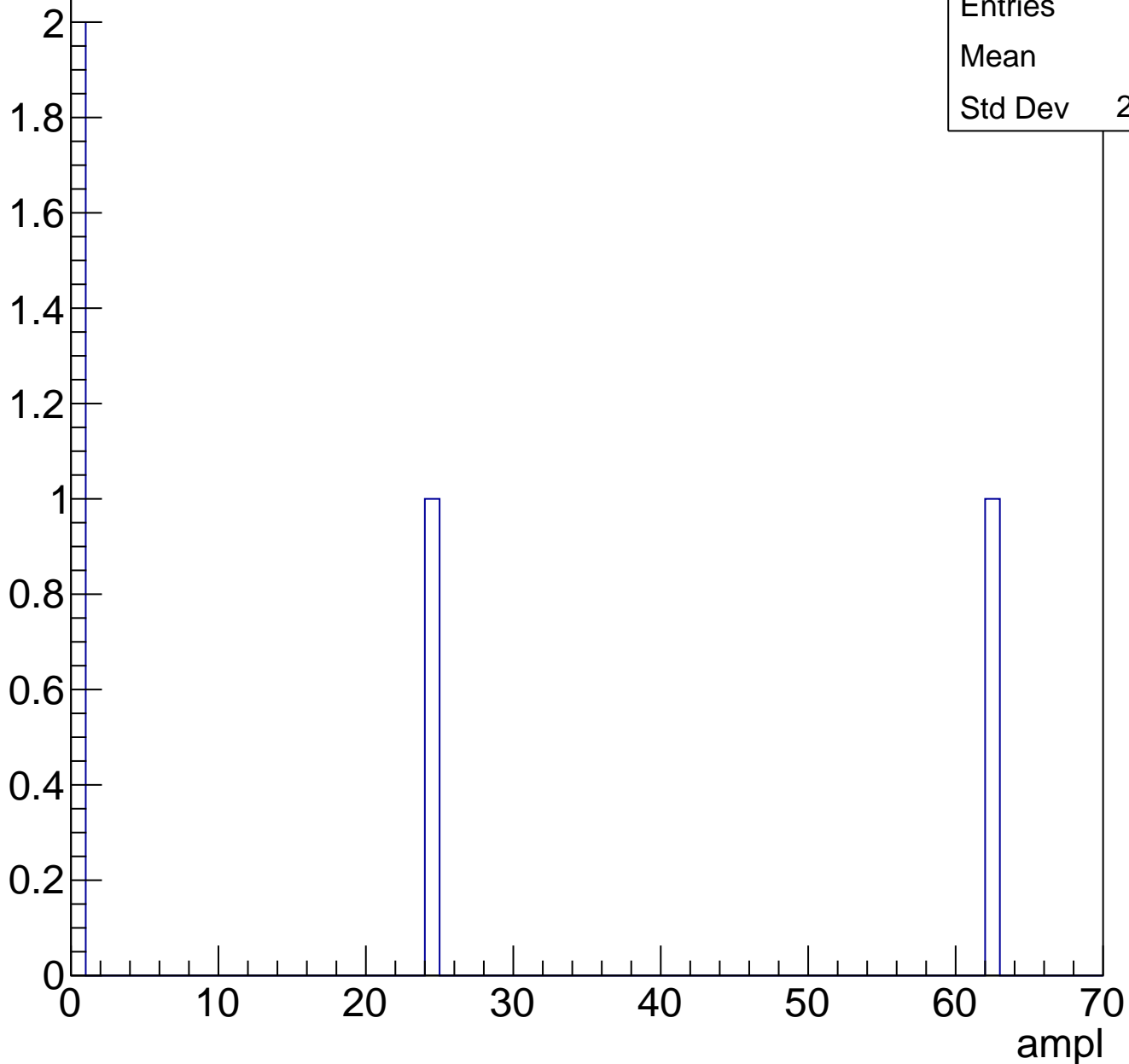




# B0L000S, U26-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	21.5
Std Dev	25.35

# B0L000S, U26-ch91, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	28.3
Std Dev	4.53

**Gaus mean : 29.0578**

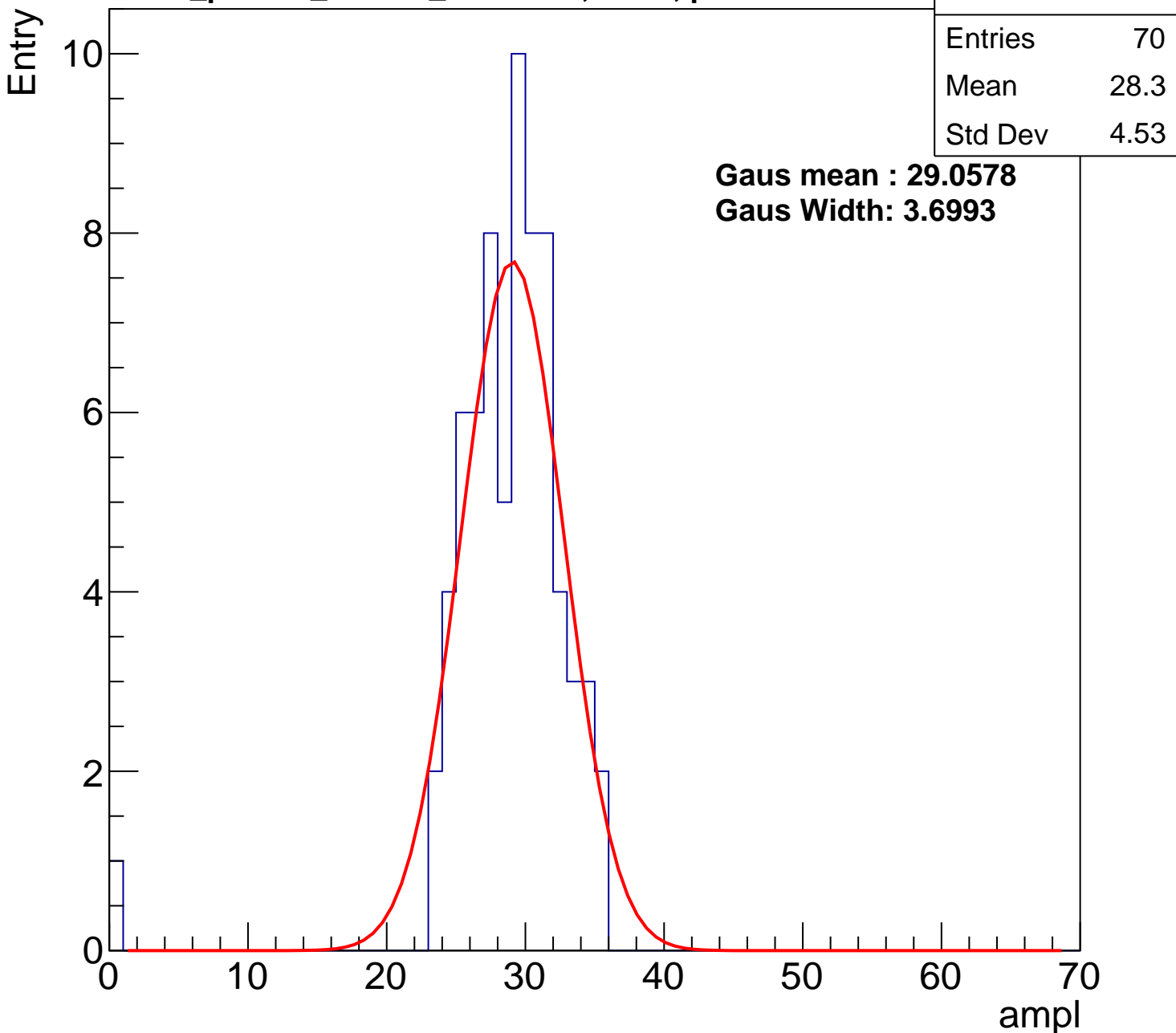
**Gaus Width: 3.6993**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch91, adc1

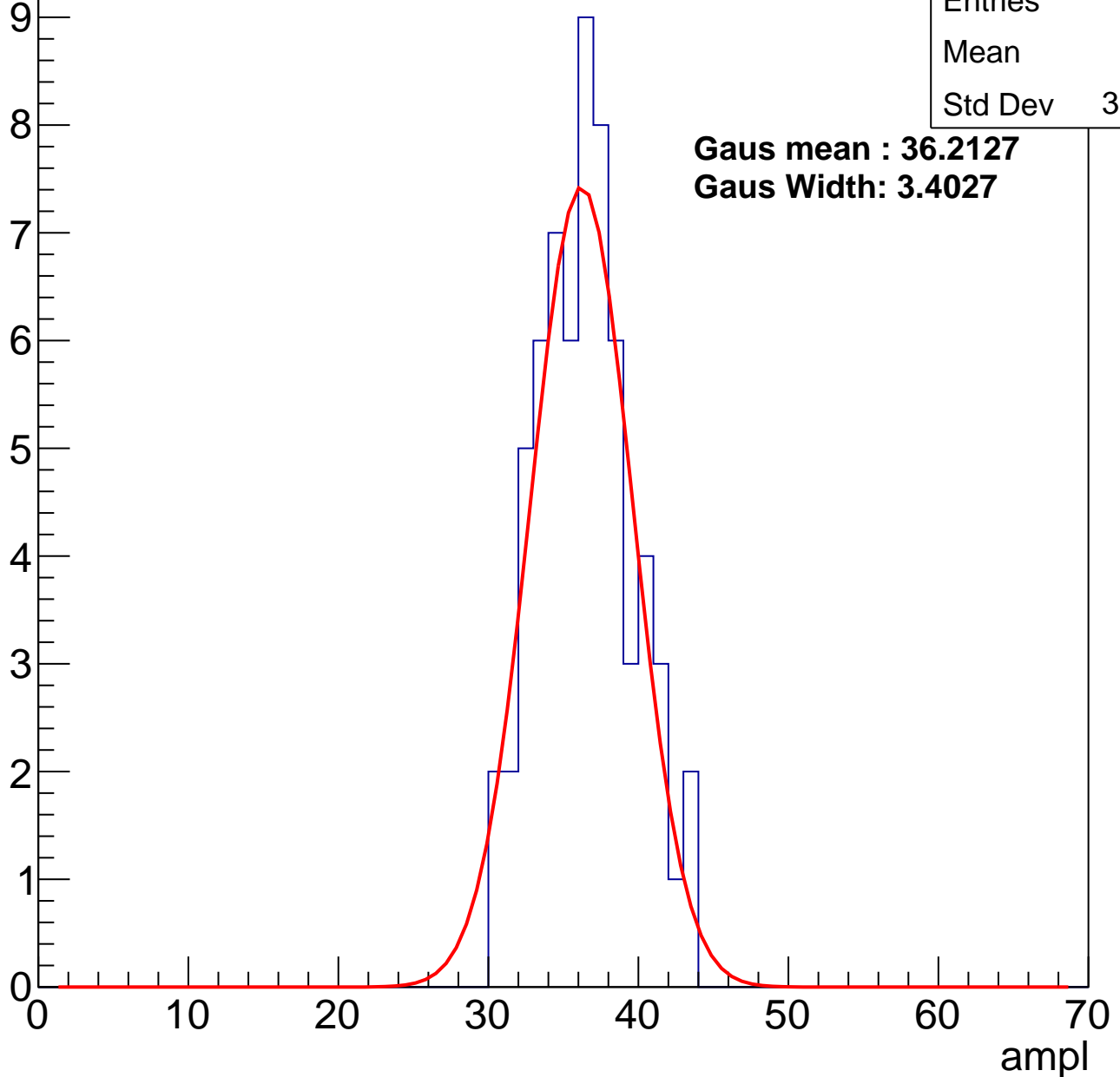
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36
Std Dev	3.117

**Gaus mean : 36.2127**

**Gaus Width: 3.4027**



# B0L000S, U26-ch91, adc2

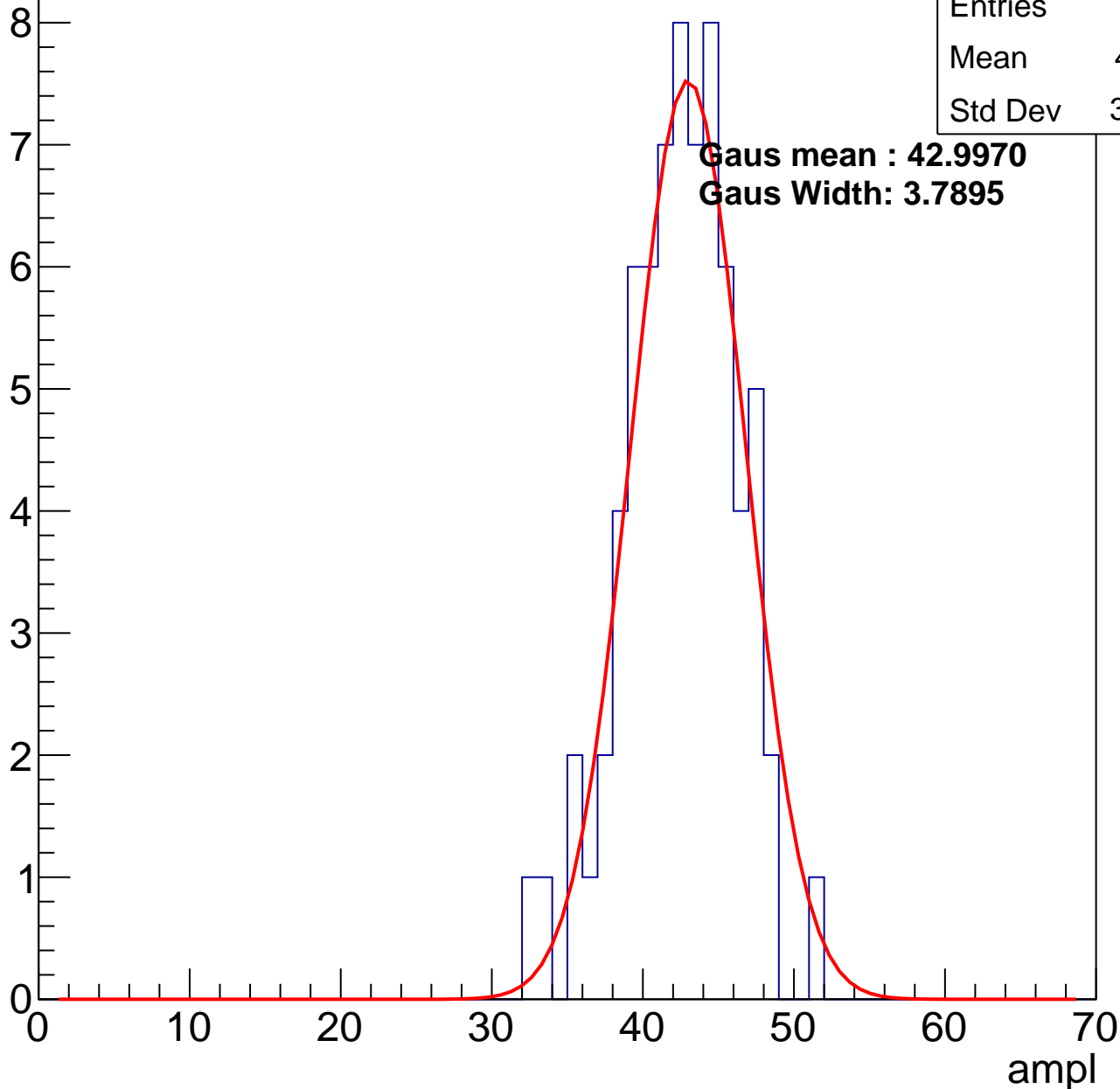
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	42.01
Std Dev	3.656

**Gaus mean : 42.9970**

**Gaus Width: 3.7895**

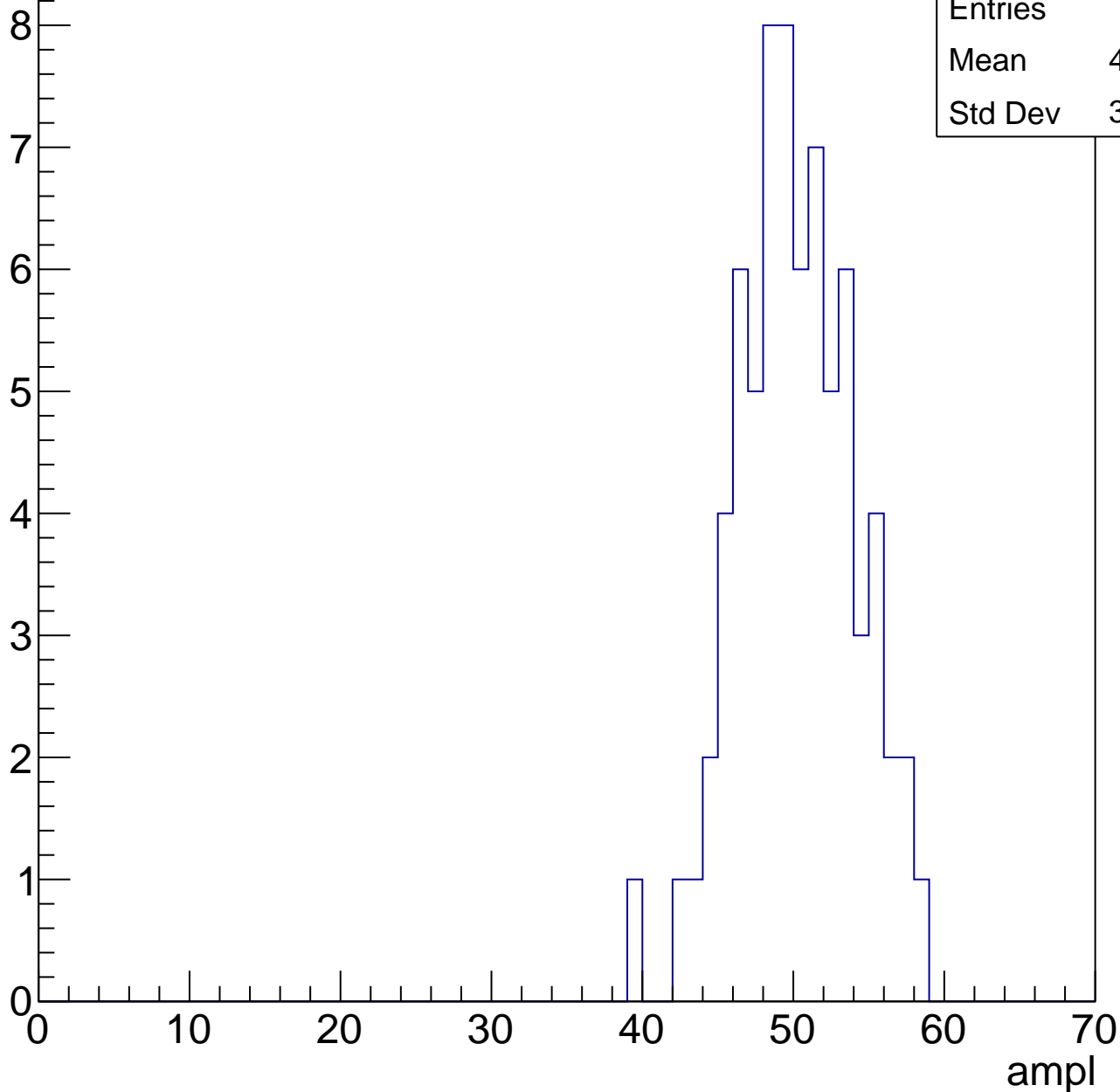


# B0L000S, U26-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	49.72
Std Dev	3.809

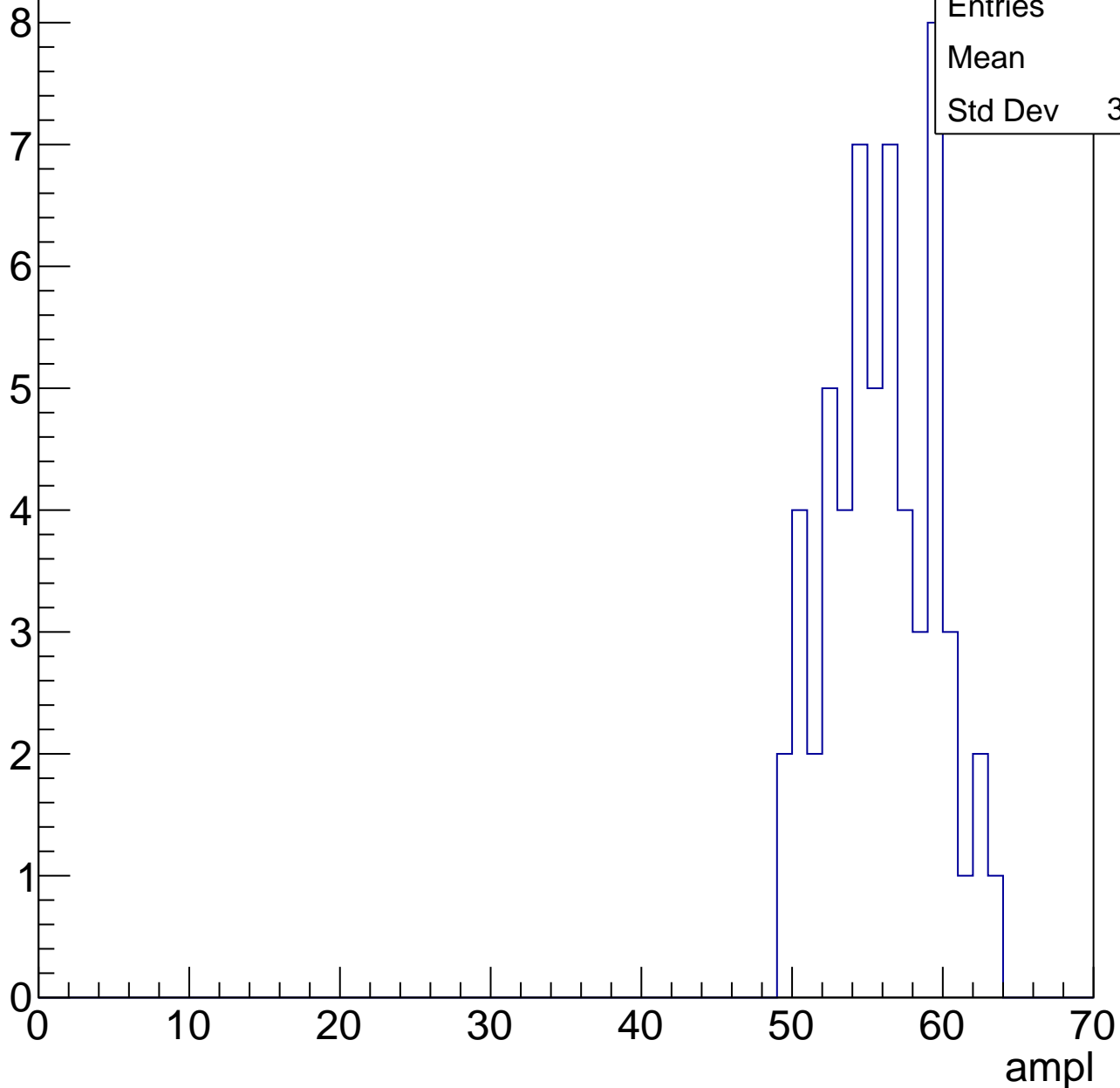


# B0L000S, U26-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.5
Std Dev	3.495

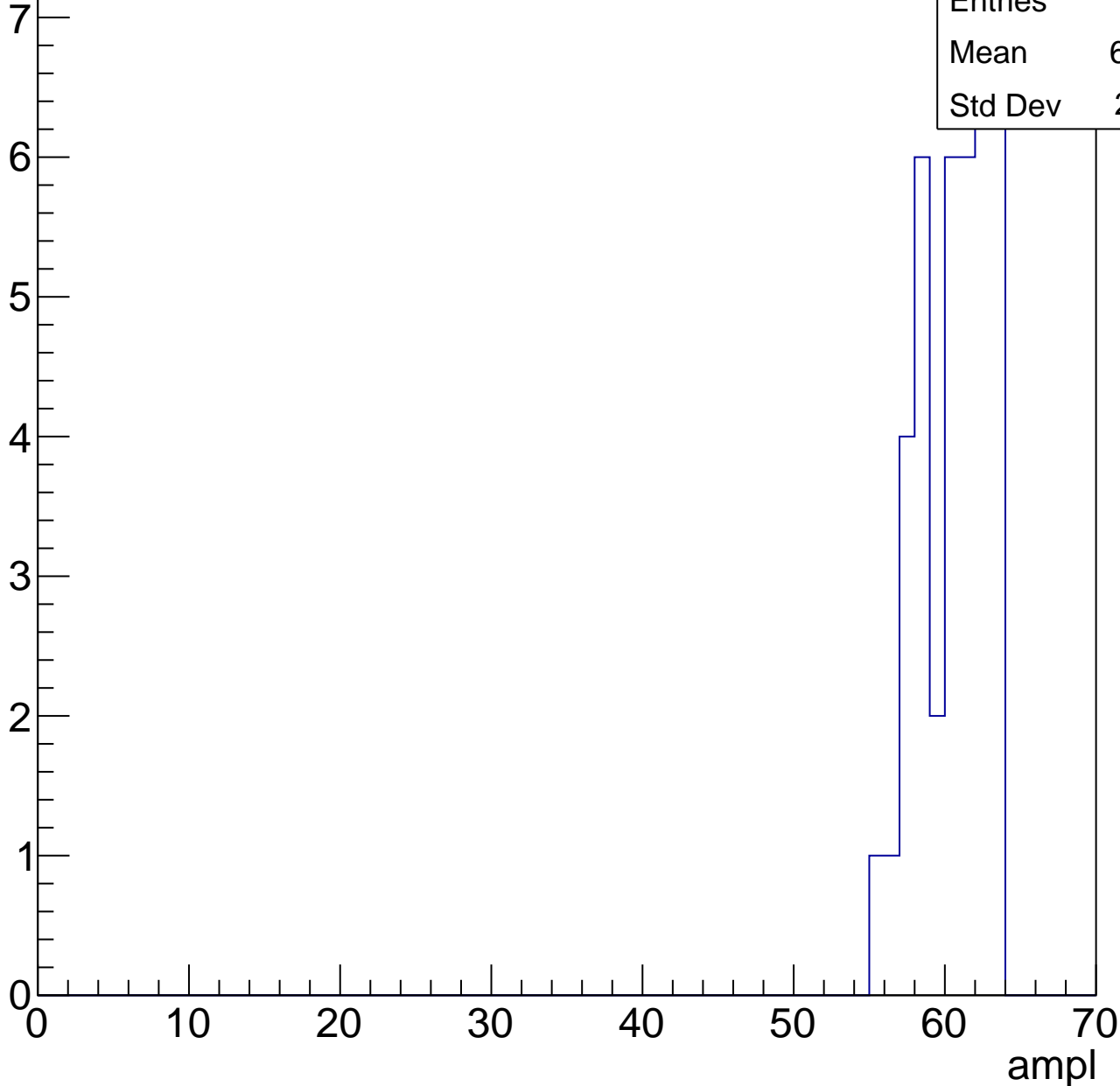


# B0L000S, U26-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

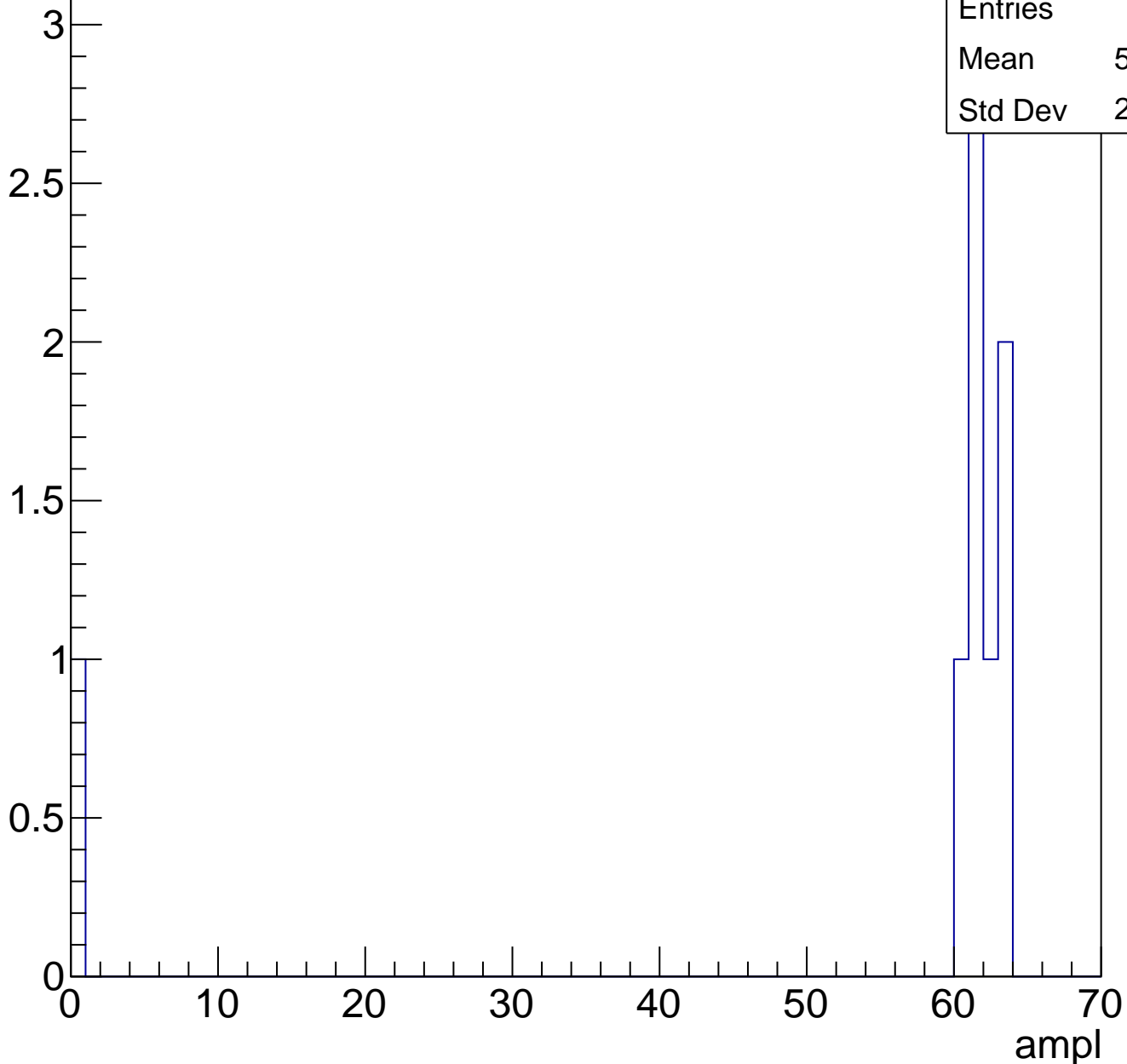
Entries	40
Mean	60.15
Std Dev	2.231



# B0L000S, U26-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch92, adc0

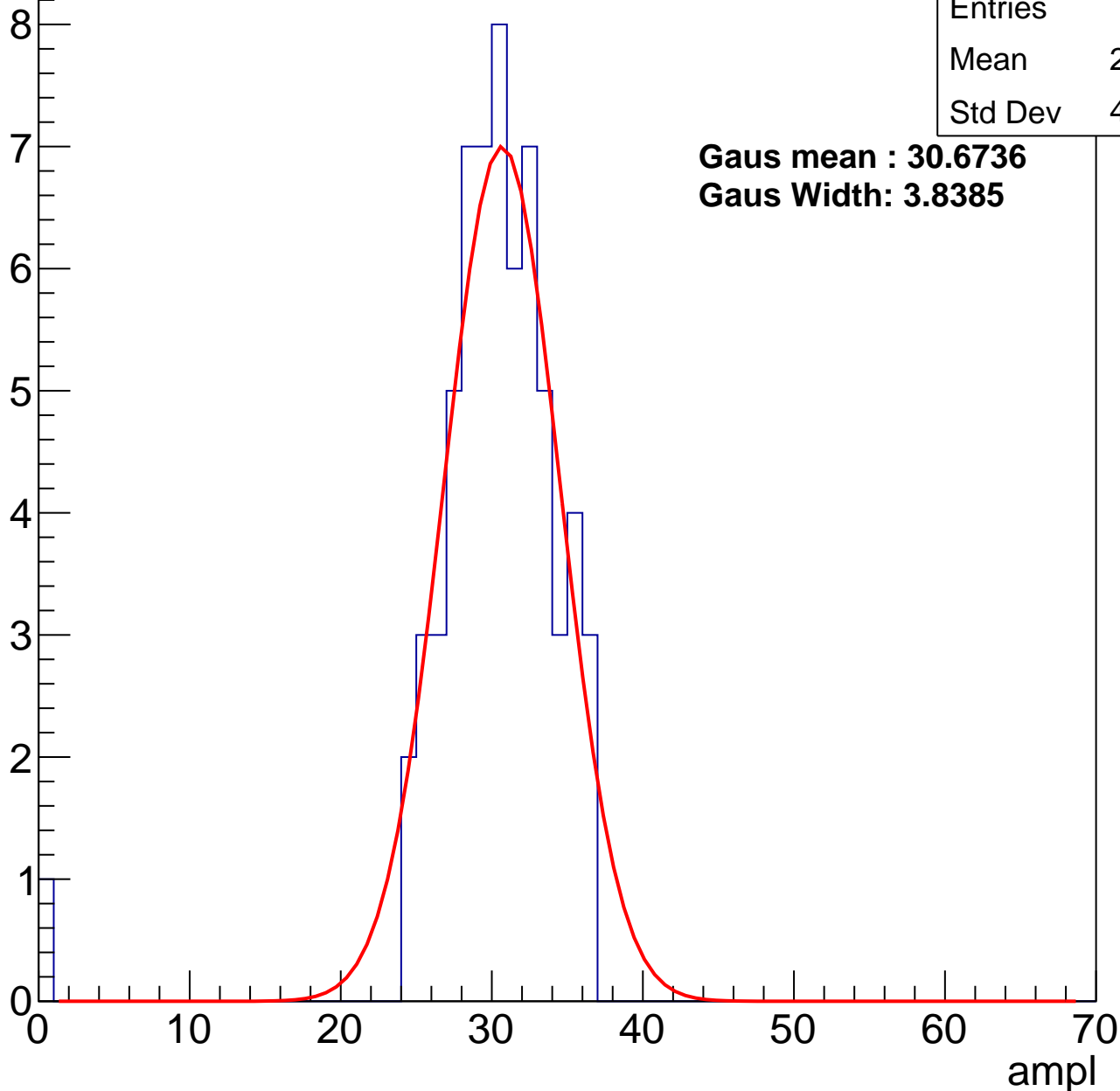
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.69
Std Dev	4.847

**Gaus mean : 30.6736**

**Gaus Width: 3.8385**



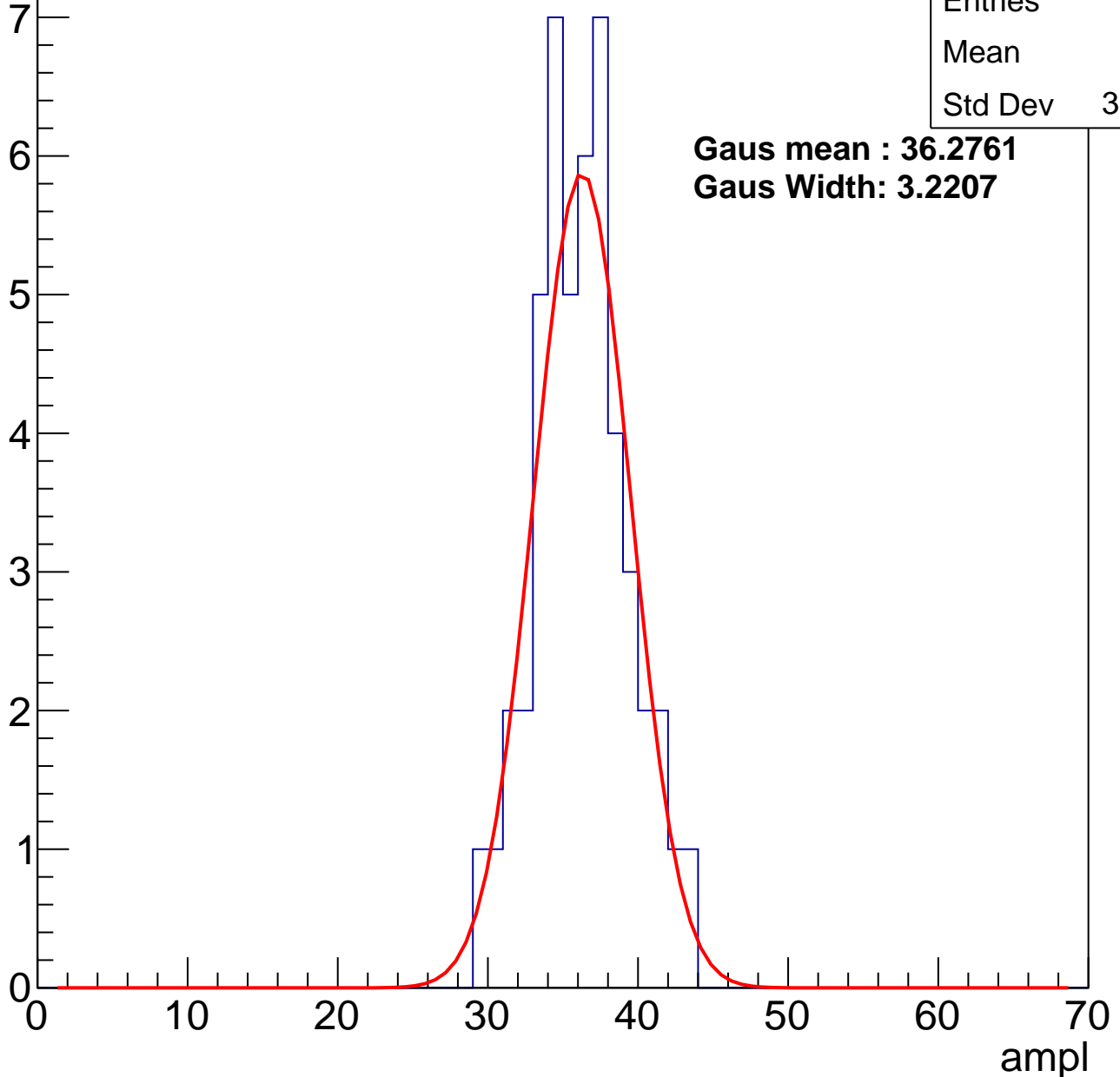
# B0L000S, U26-ch92, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	35.8
Std Dev	3.064

**Gaus mean : 36.2761**  
**Gaus Width: 3.2207**



# B0L000S, U26-ch92, adc2

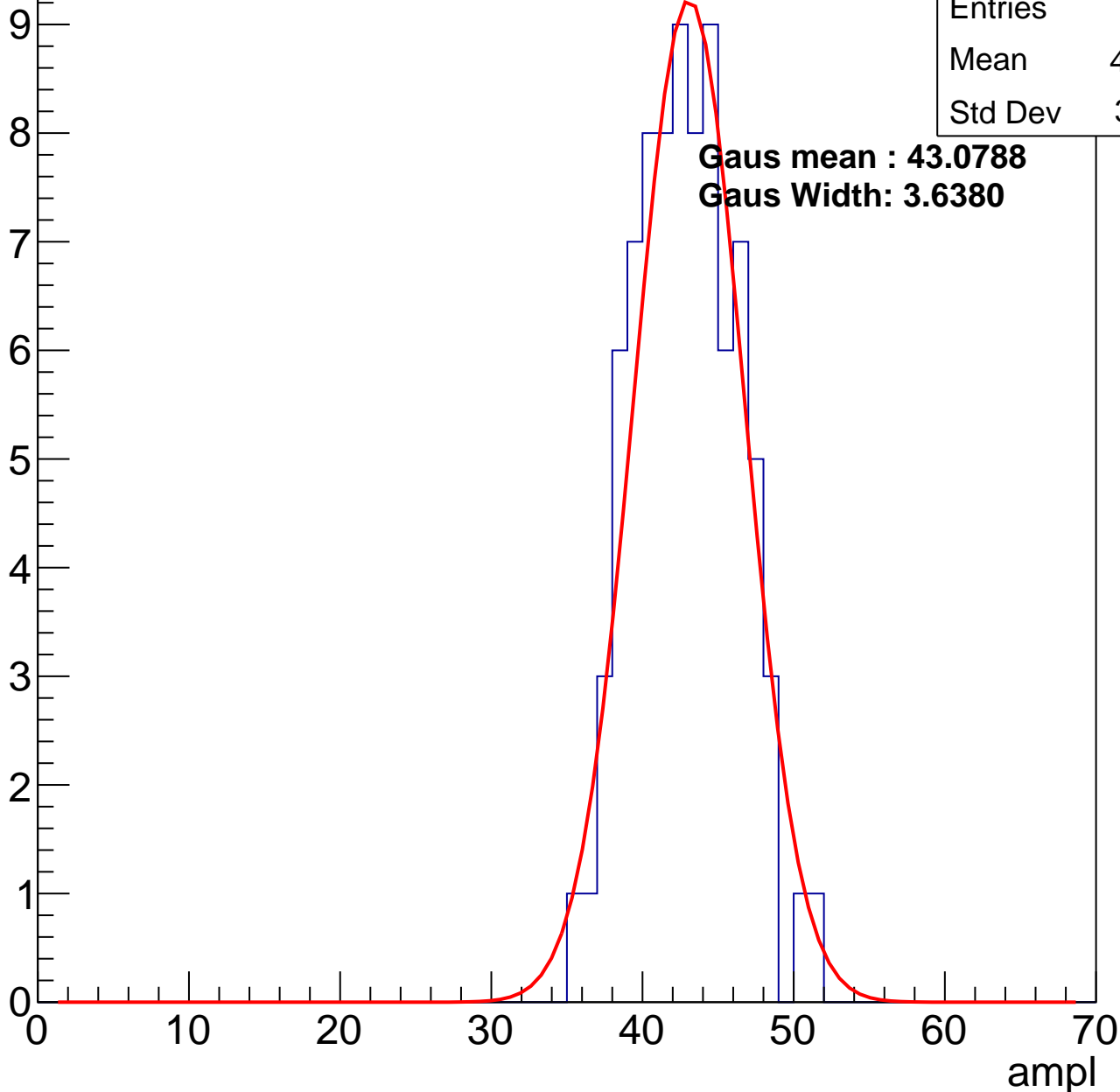
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	42.42
Std Dev	3.351

**Gaus mean : 43.0788**

**Gaus Width: 3.6380**

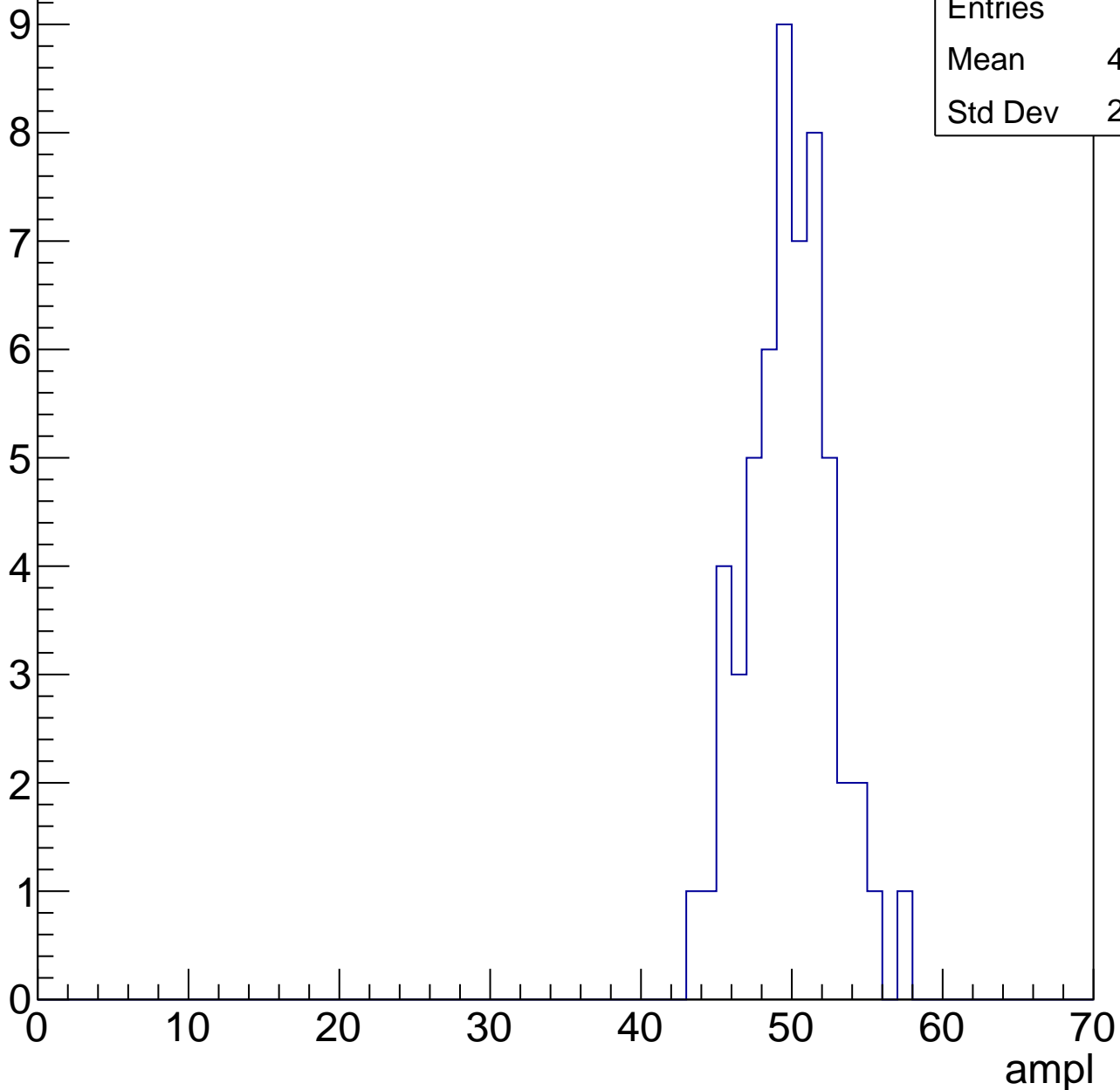


# B0L000S, U26-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.33
Std Dev	2.822

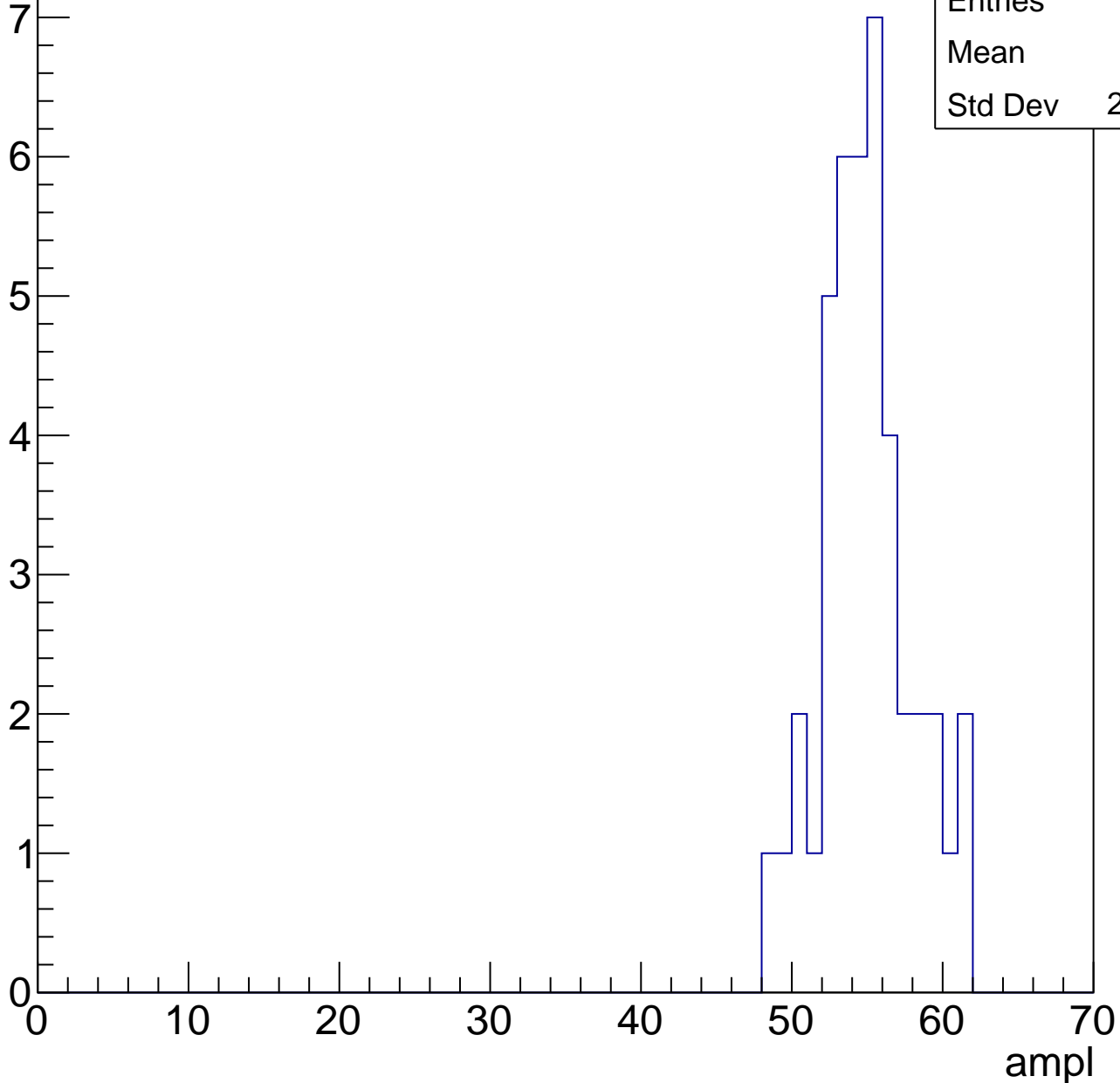


# B0L000S, U26-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	54.5
Std Dev	2.986

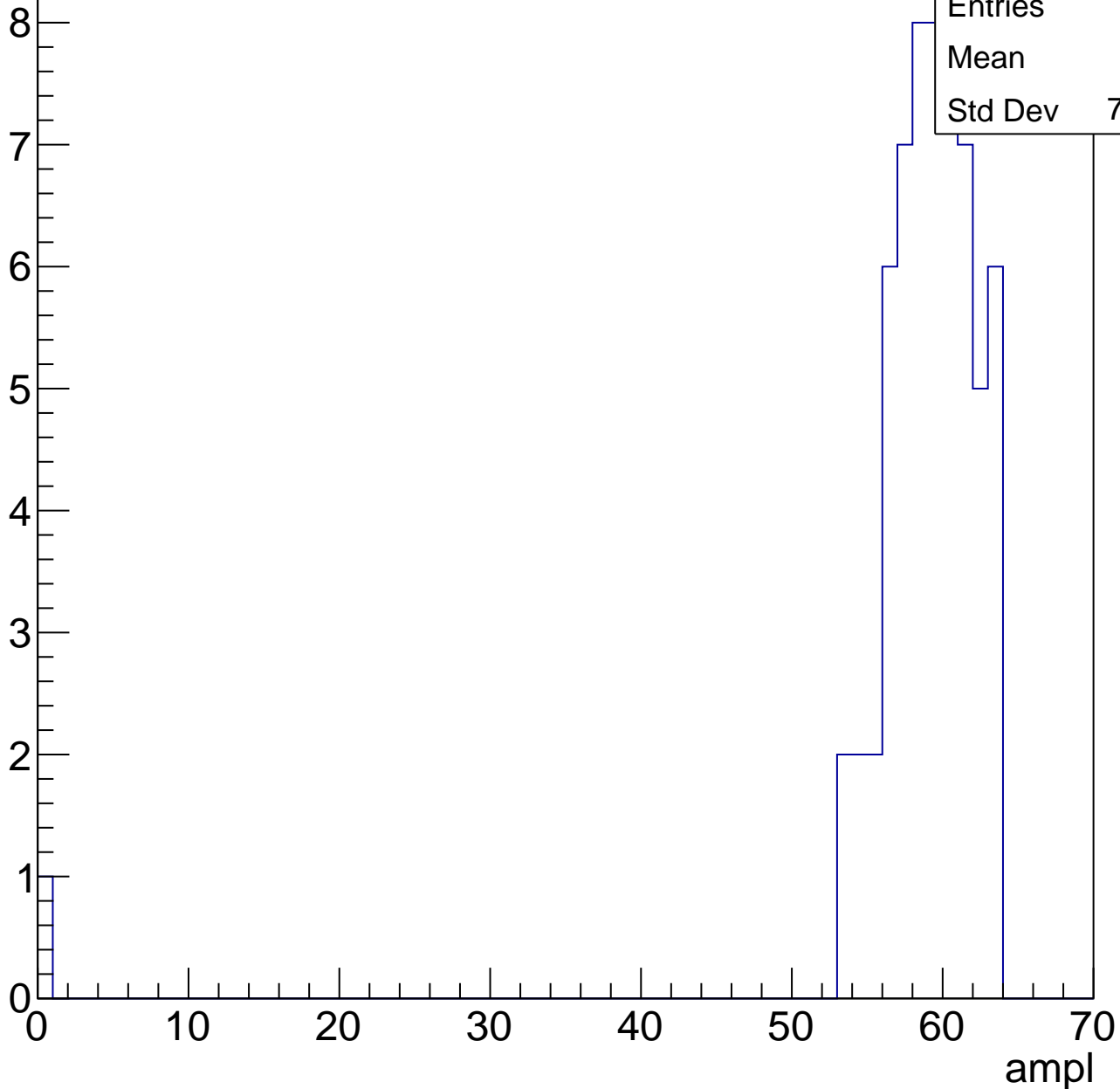


# B0L000S, U26-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	57.9
Std Dev	7.857

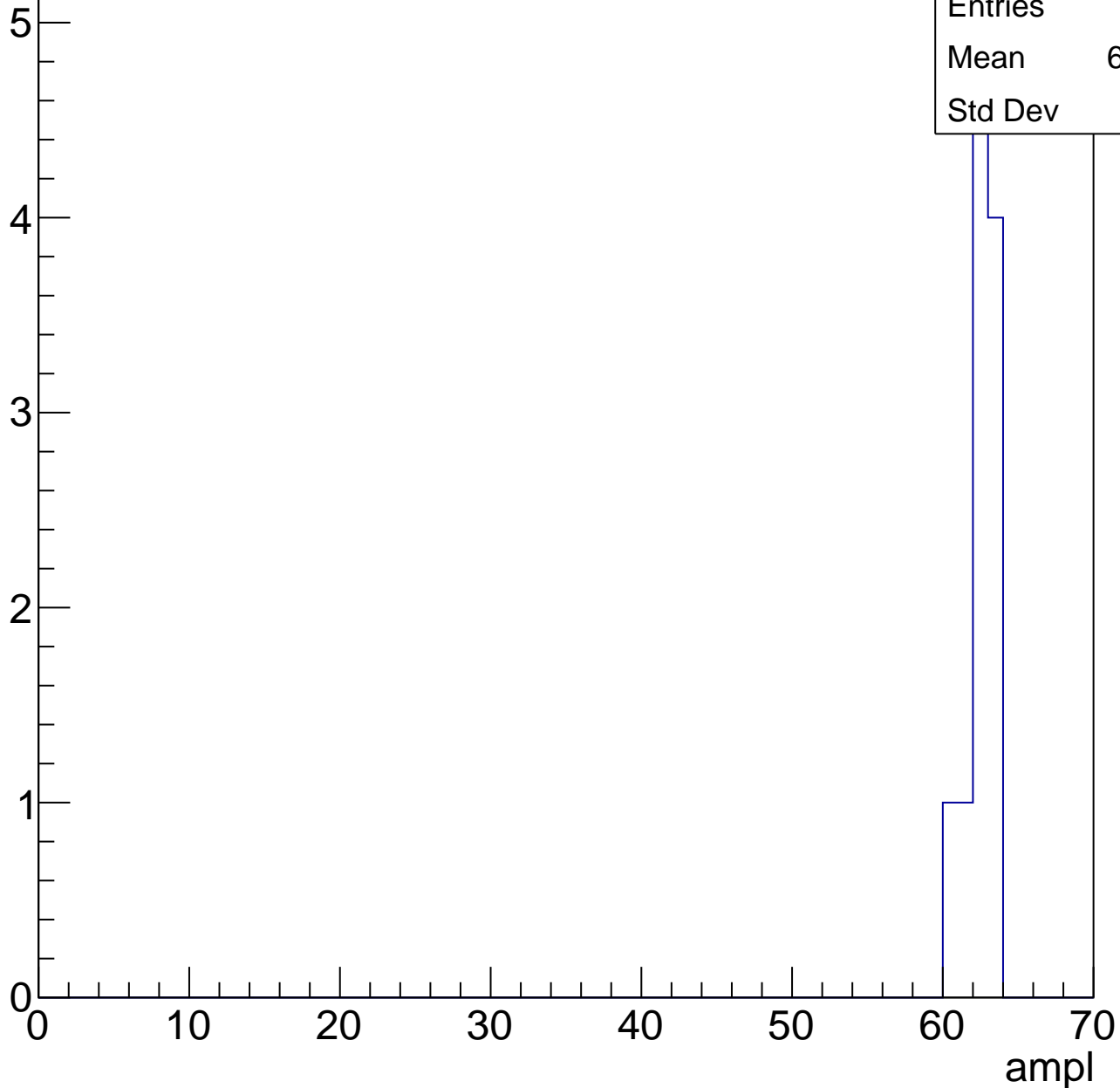


# B0L000S, U26-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	62.09
Std Dev	0.9





# B0L000S, U26-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch93, adc0

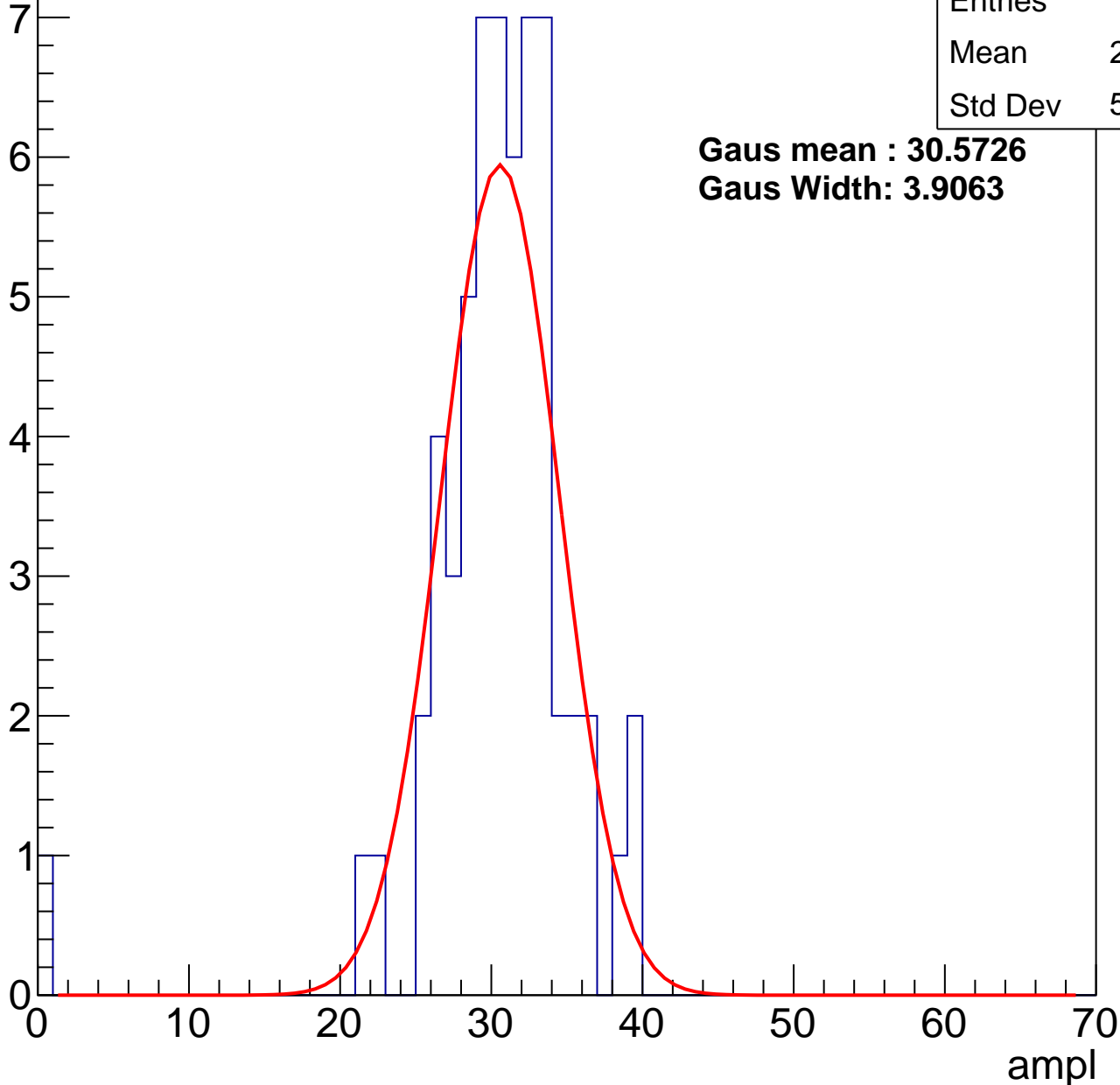
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.97
Std Dev	5.307

**Gaus mean : 30.5726**

**Gaus Width: 3.9063**



# B0L000S, U26-ch93, adc1

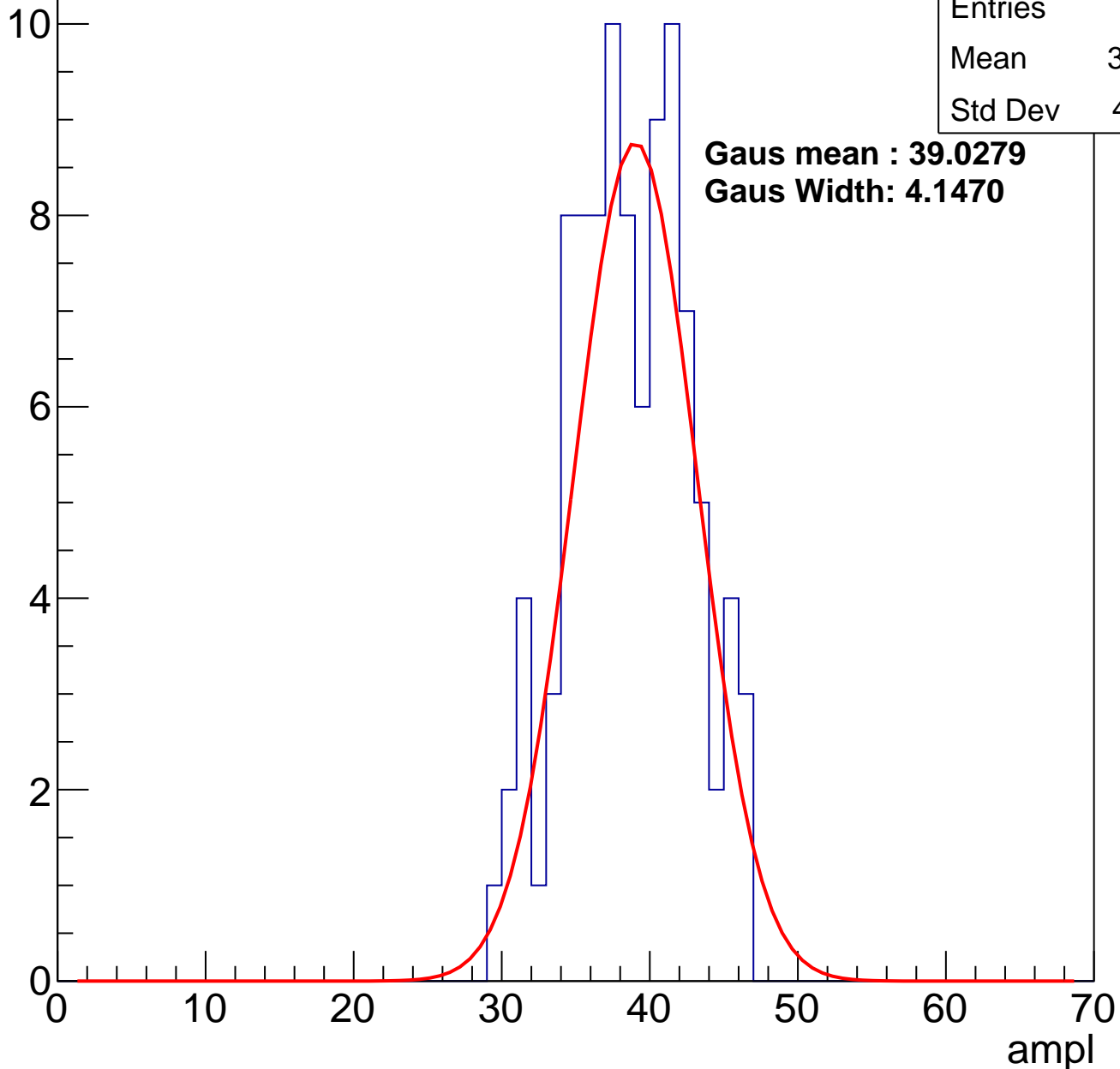
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	99
Mean	38.15
Std Dev	4.001

**Gaus mean : 39.0279**

**Gaus Width: 4.1470**

Entry



# B0L000S, U26-ch93, adc2

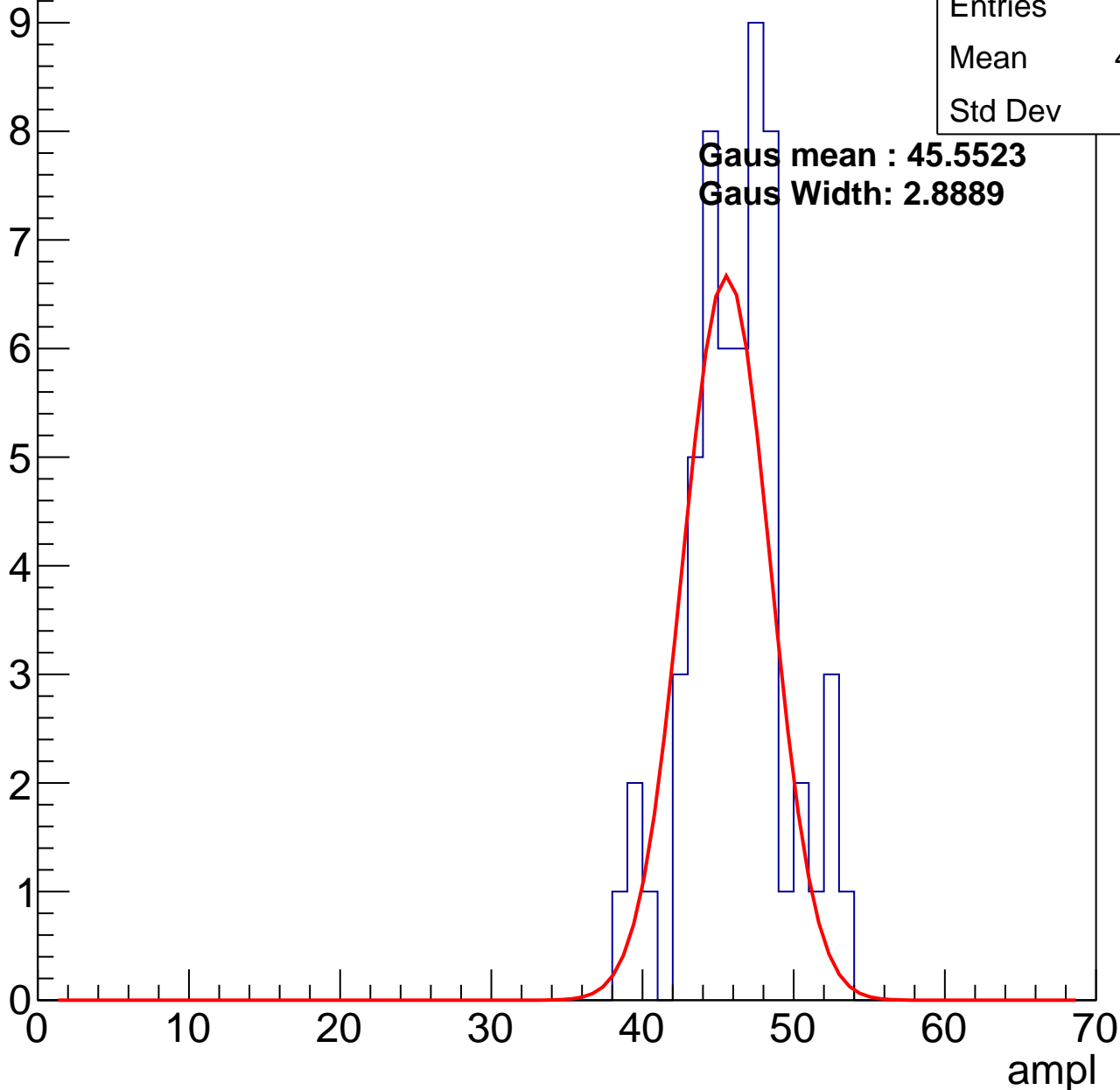
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	45.81
Std Dev	3.22

**Gaus mean : 45.5523**

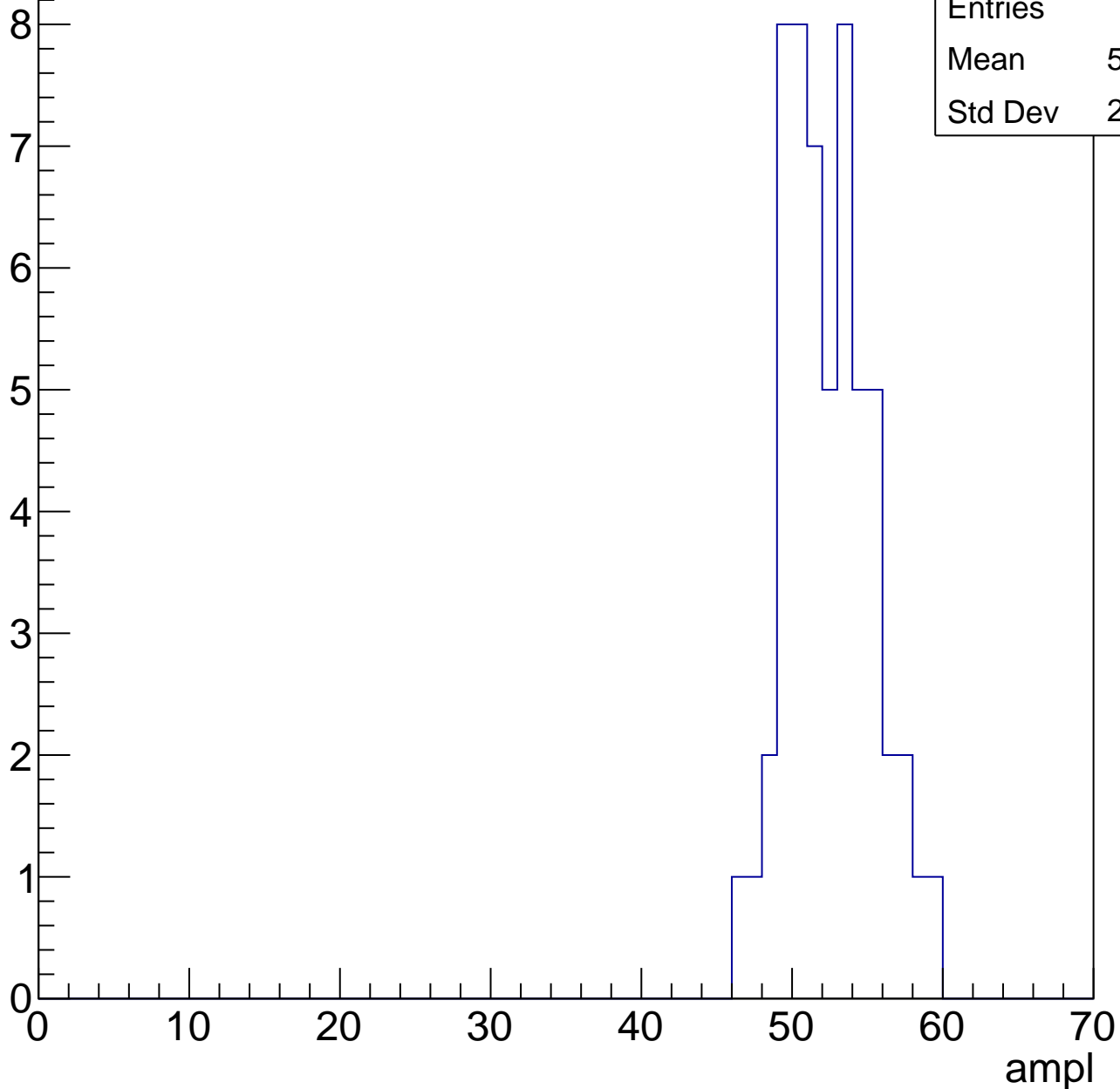
**Gaus Width: 2.8889**



# B0L000S, U26-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

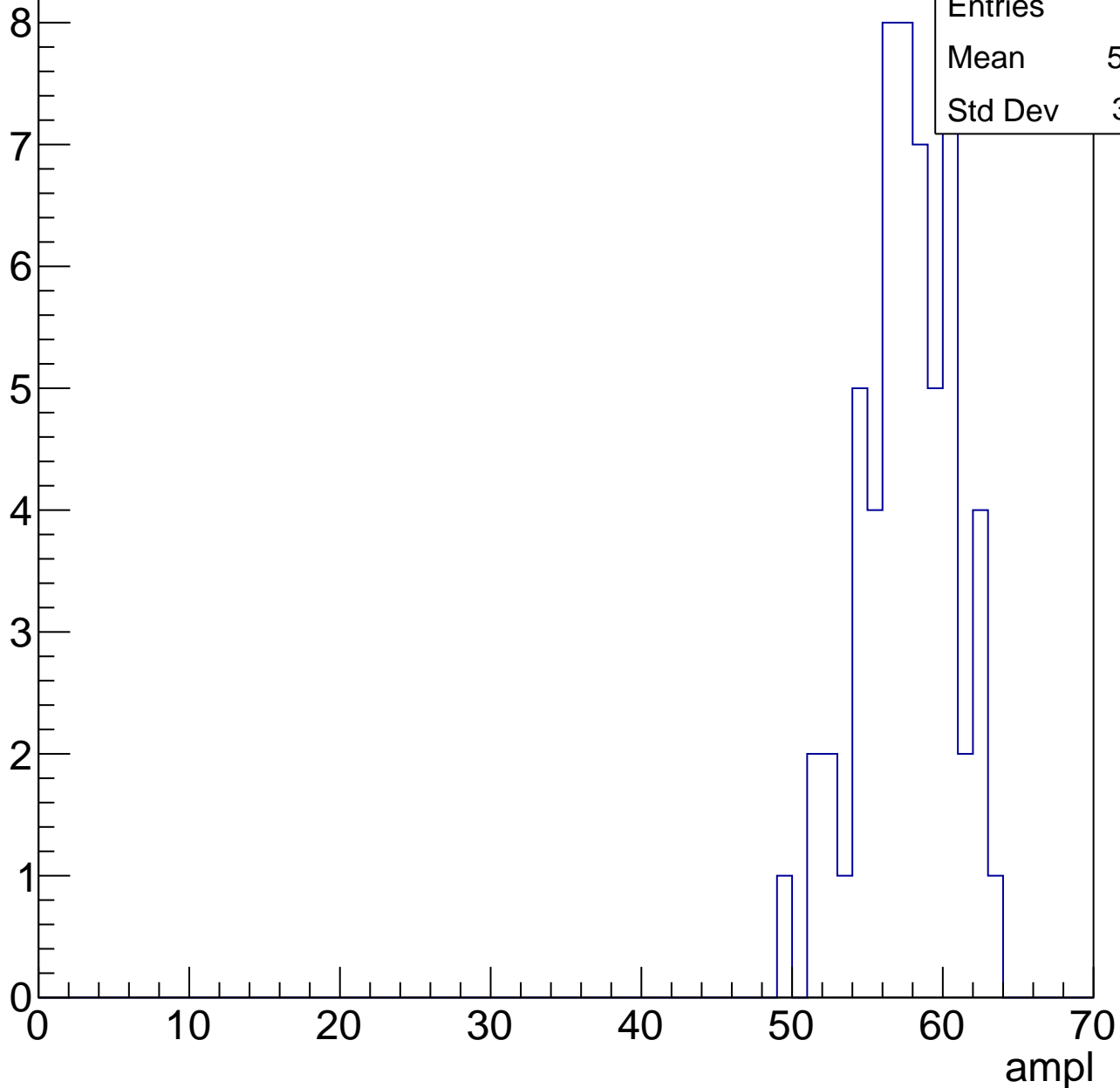


# B0L000S, U26-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	57.17
Std Dev	3.041

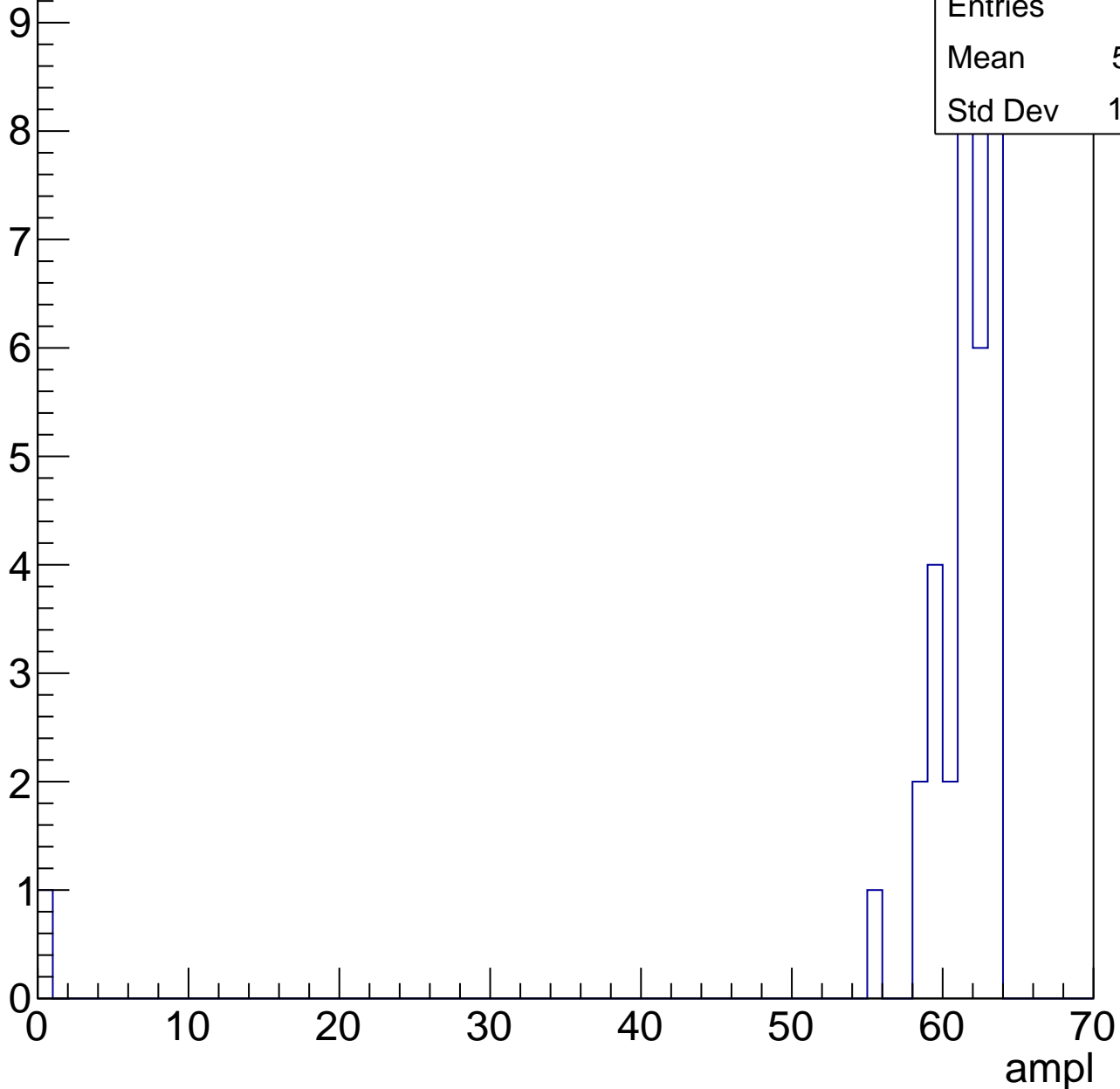


# B0L000S, U26-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	59.21
Std Dev	10.63



# B0L000S, U26-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U26-ch94, adc0

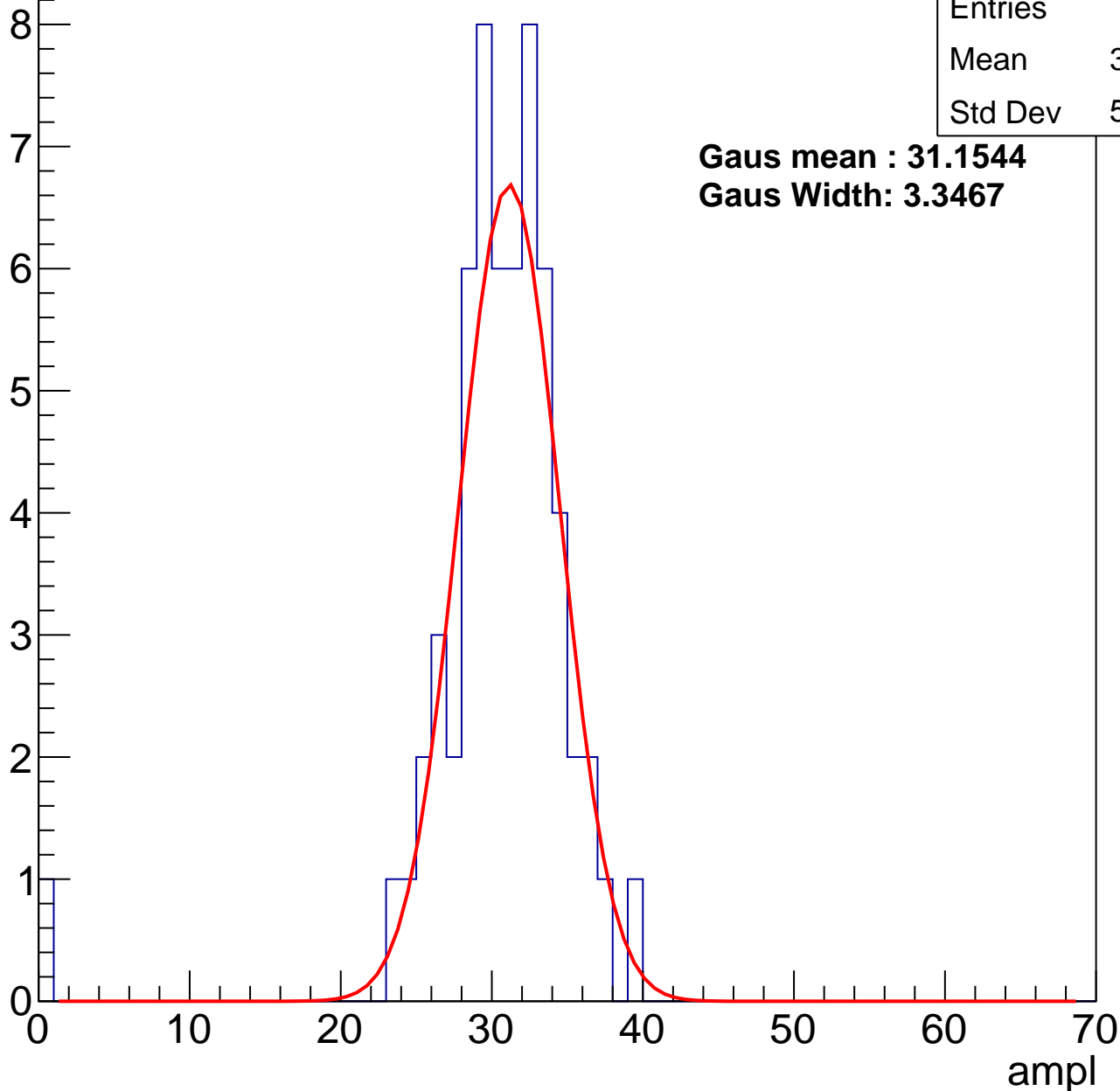
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.05
Std Dev	5.068

**Gaus mean : 31.1544**

**Gaus Width: 3.3467**



# B0L000S, U26-ch94, adc1

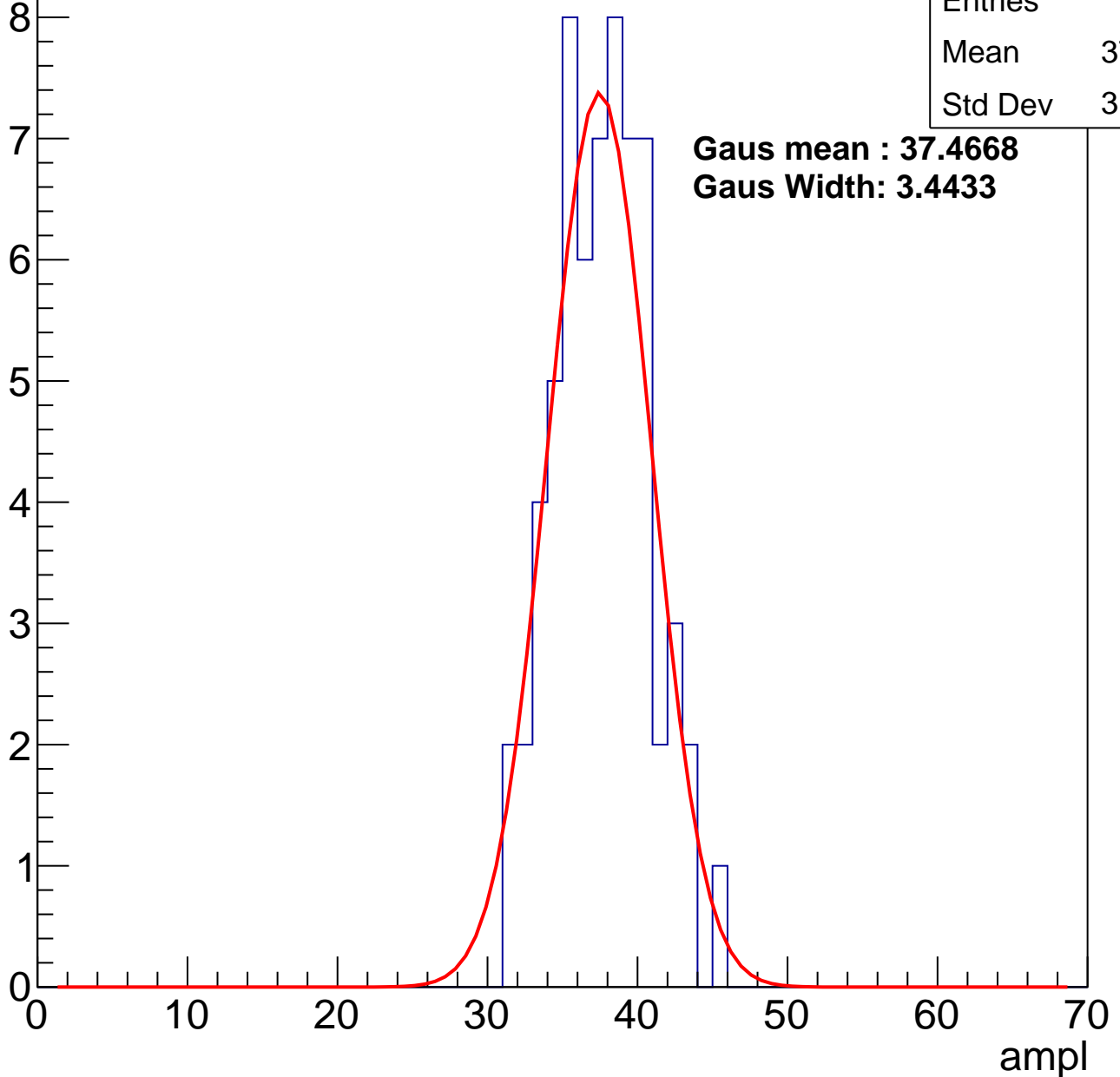
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	37.17
Std Dev	3.085

**Gaus mean : 37.4668**

**Gaus Width: 3.4433**



# B0L000S, U26-ch94, adc2

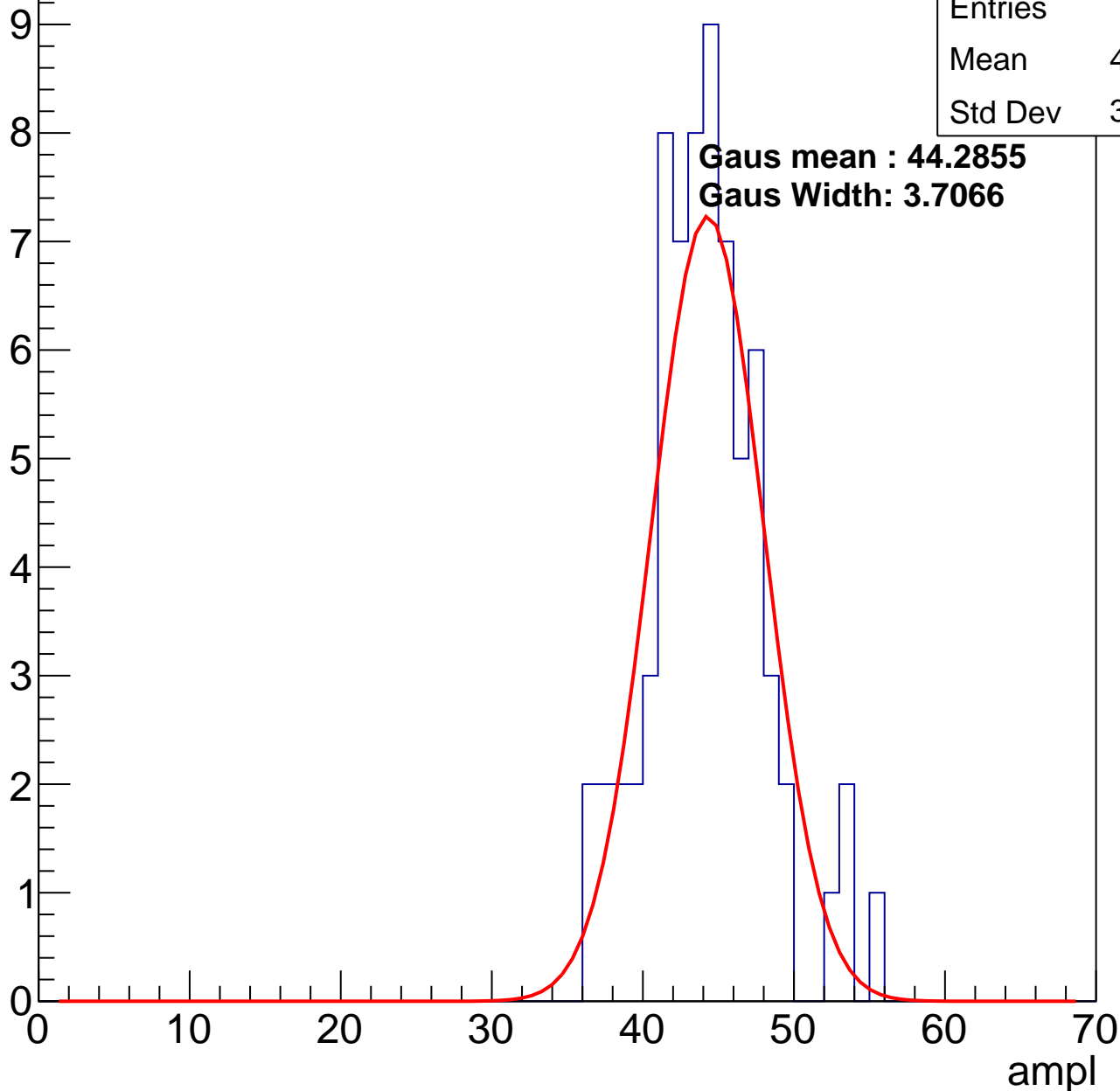
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.77
Std Dev	3.829

**Gaus mean : 44.2855**

**Gaus Width: 3.7066**

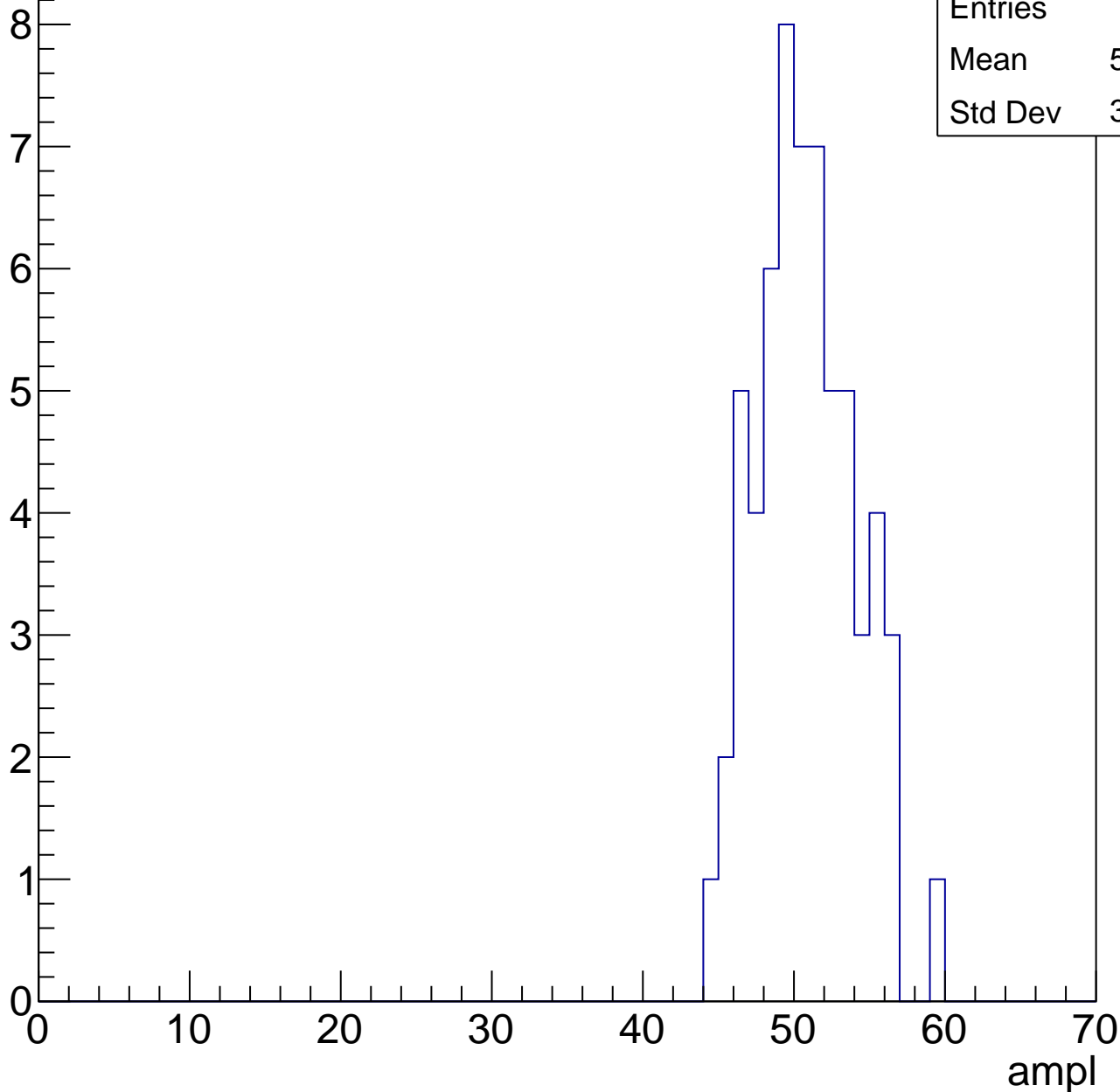


# B0L000S, U26-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	50.38
Std Dev	3.225

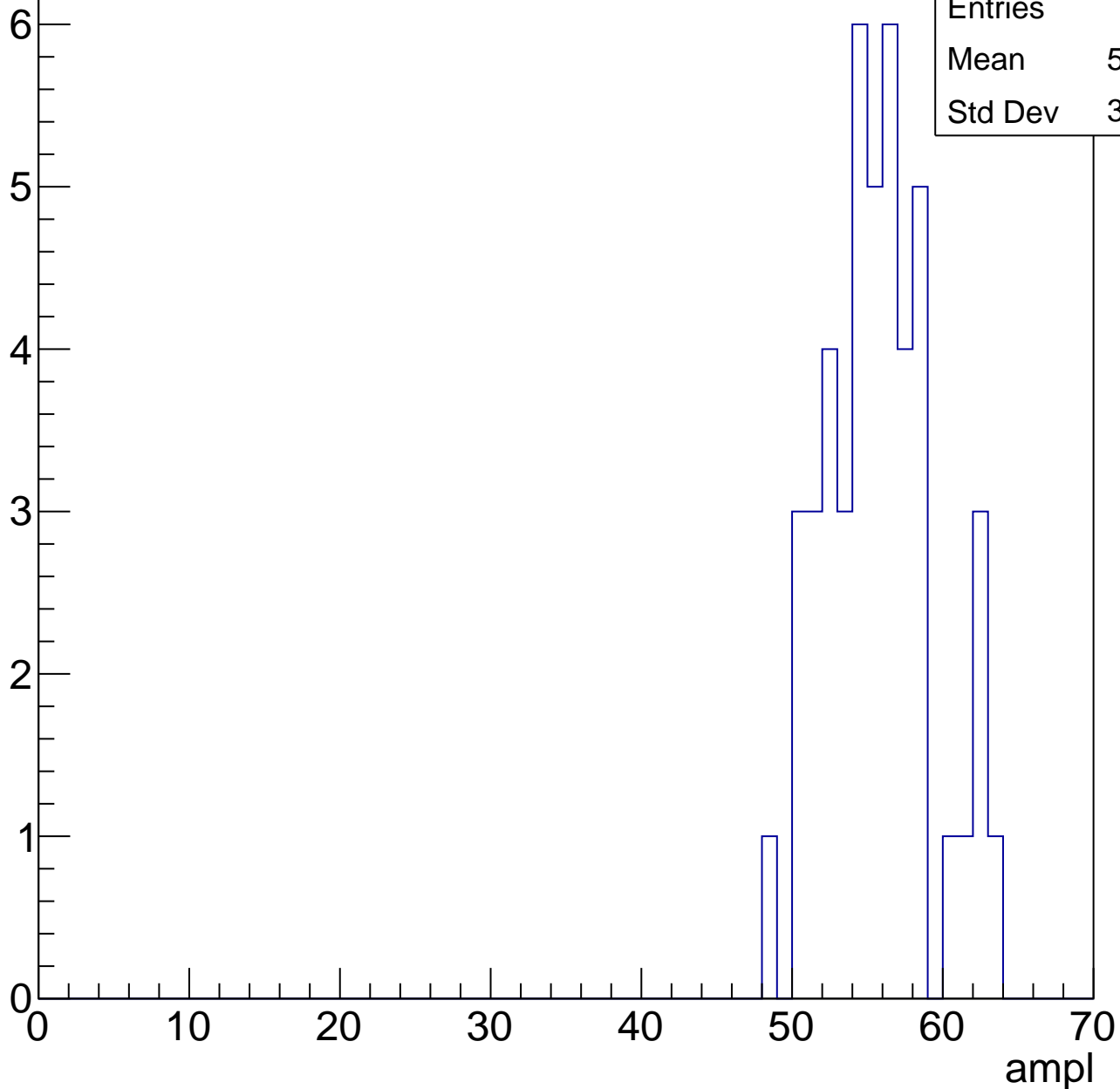


# B0L000S, U26-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	55.24
Std Dev	3.496

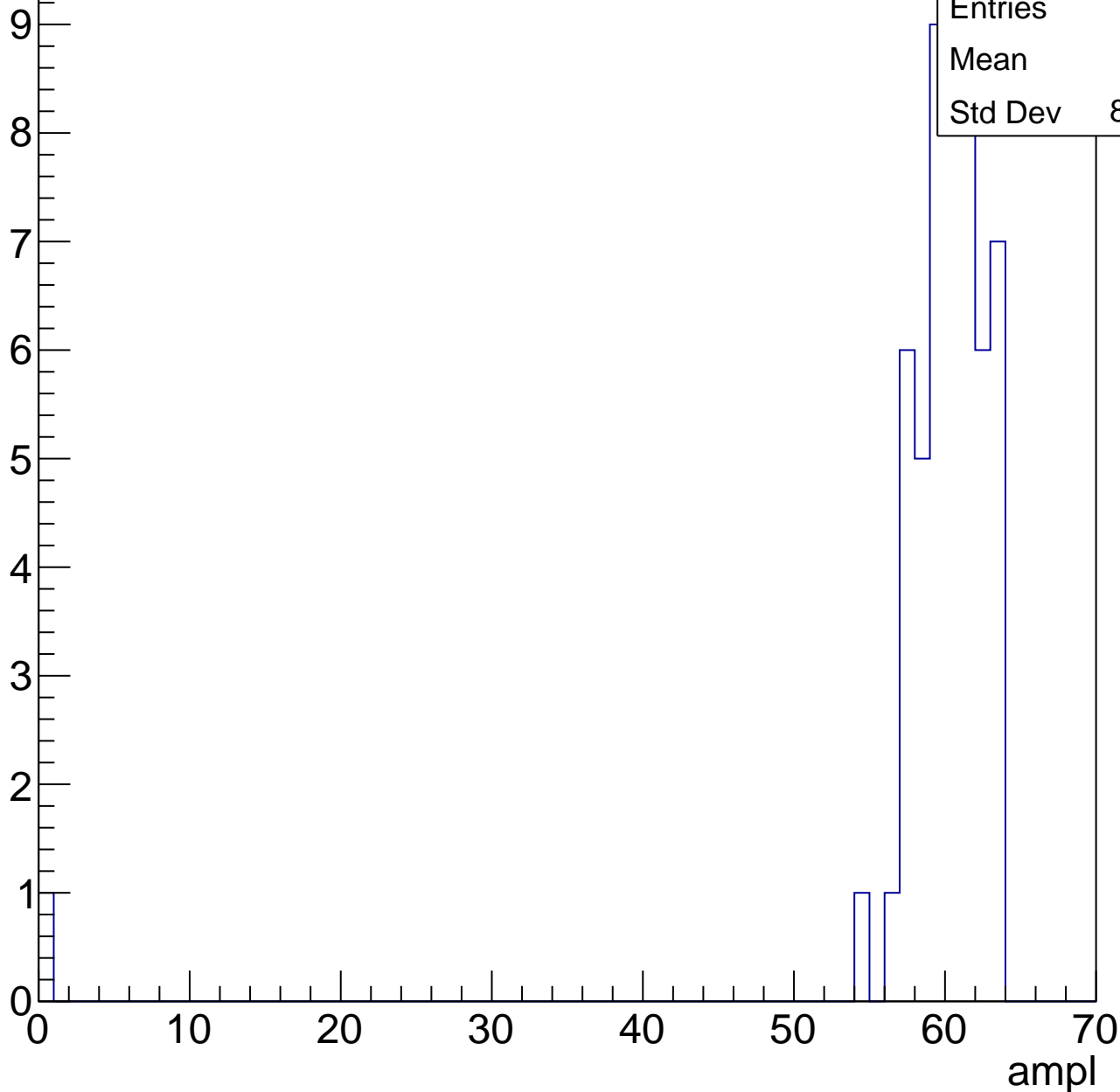


# B0L000S, U26-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	58.8
Std Dev	8.336



# B0L000S, U26-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch95, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	31.05
Std Dev	3.856

**Gaus mean : 32.2026**

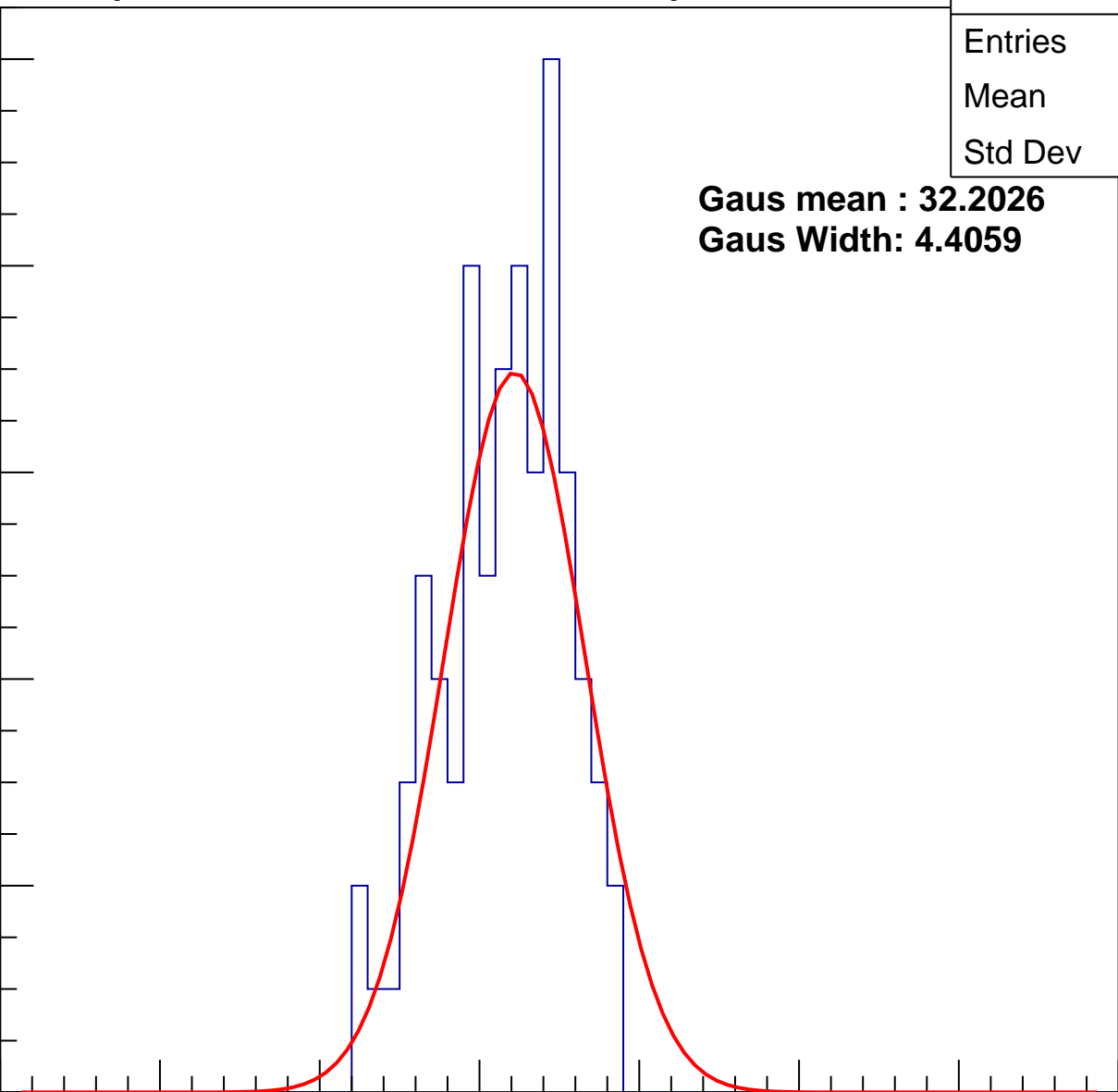
**Gaus Width: 4.4059**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch95, adc1

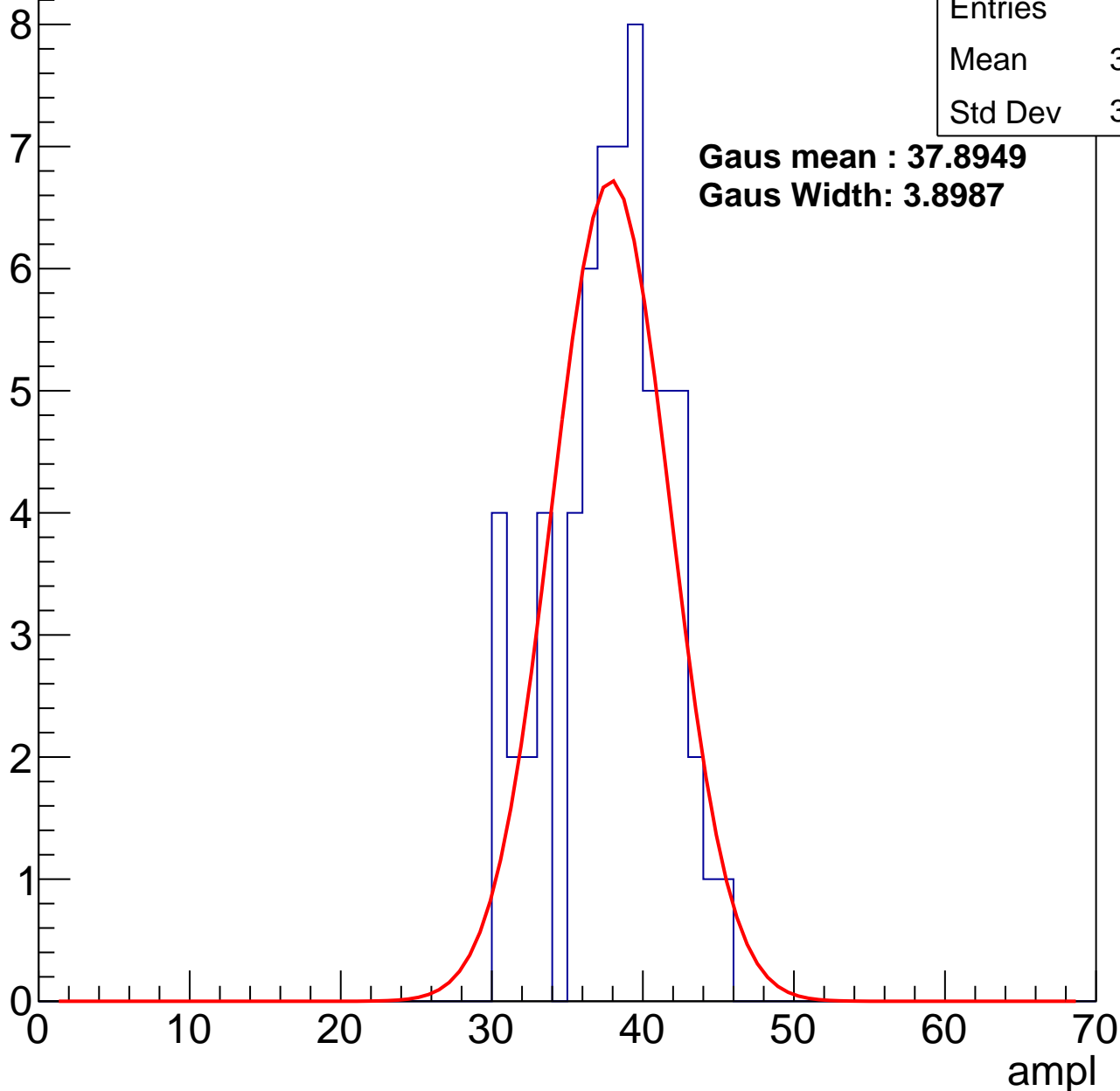
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	37.48
Std Dev	3.703

**Gaus mean : 37.8949**

**Gaus Width: 3.8987**



# B0L000S, U26-ch95, adc2

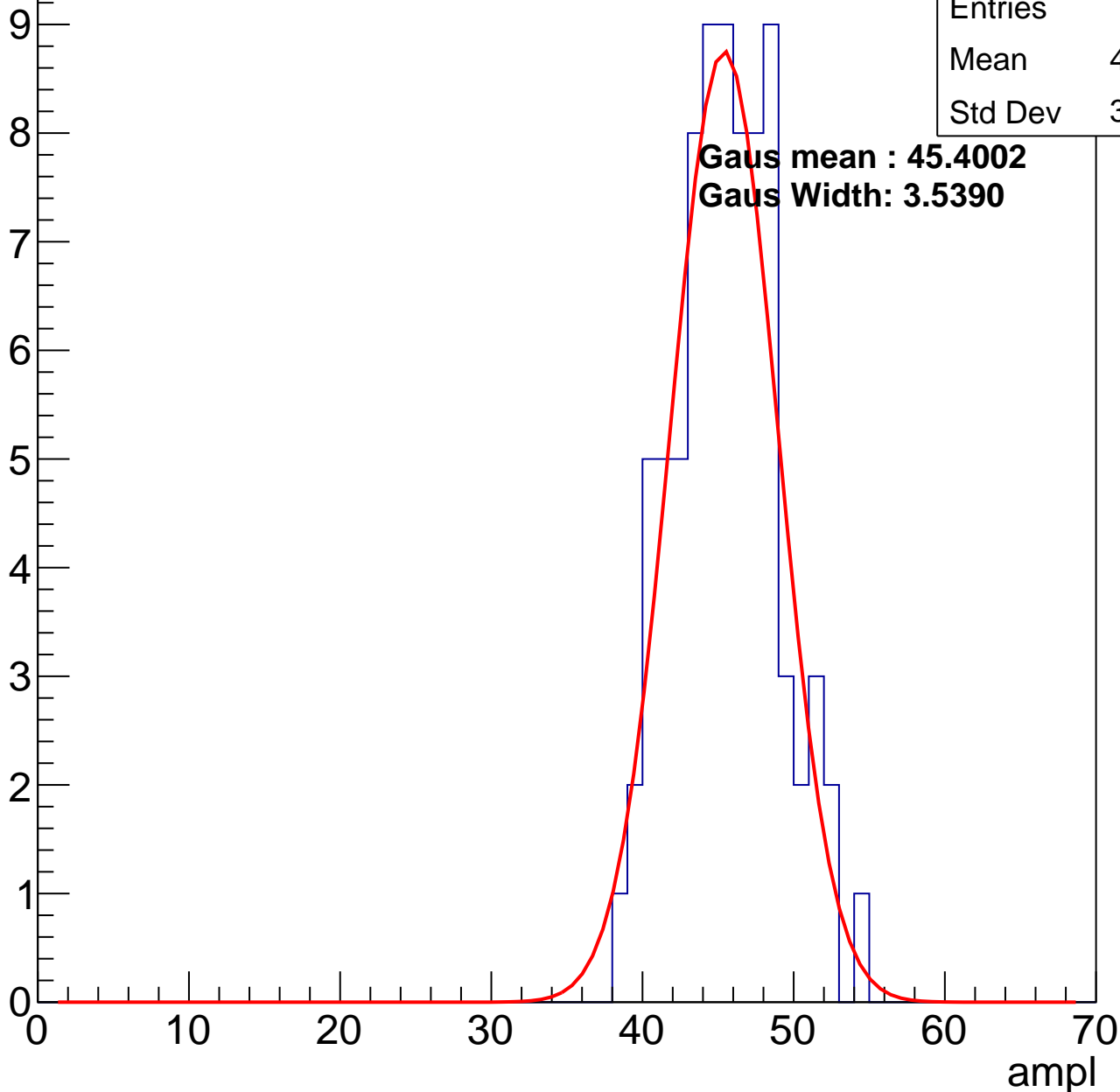
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	45.12
Std Dev	3.385

**Gaus mean : 45.4002**

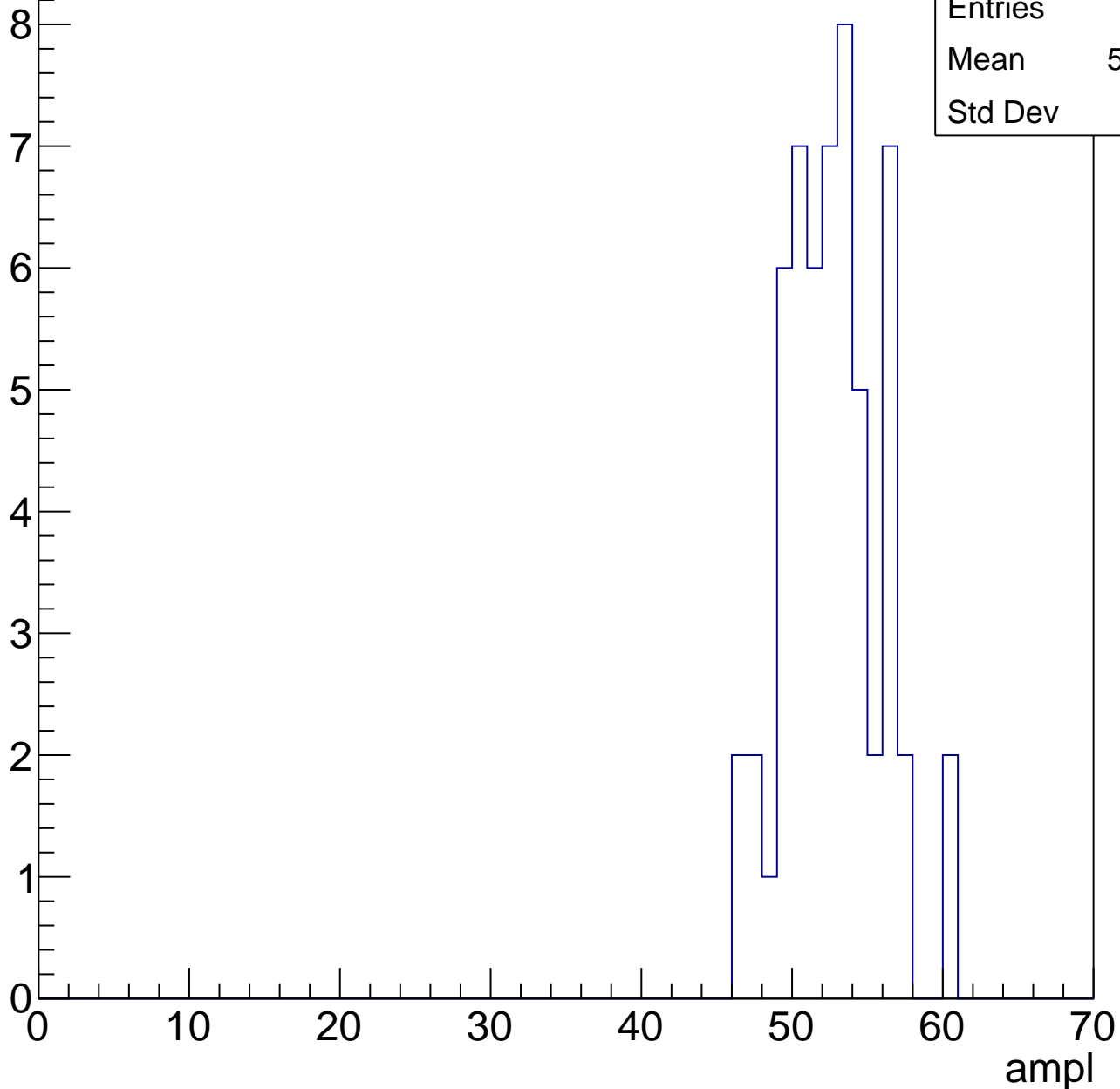
**Gaus Width: 3.5390**



# B0L000S, U26-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

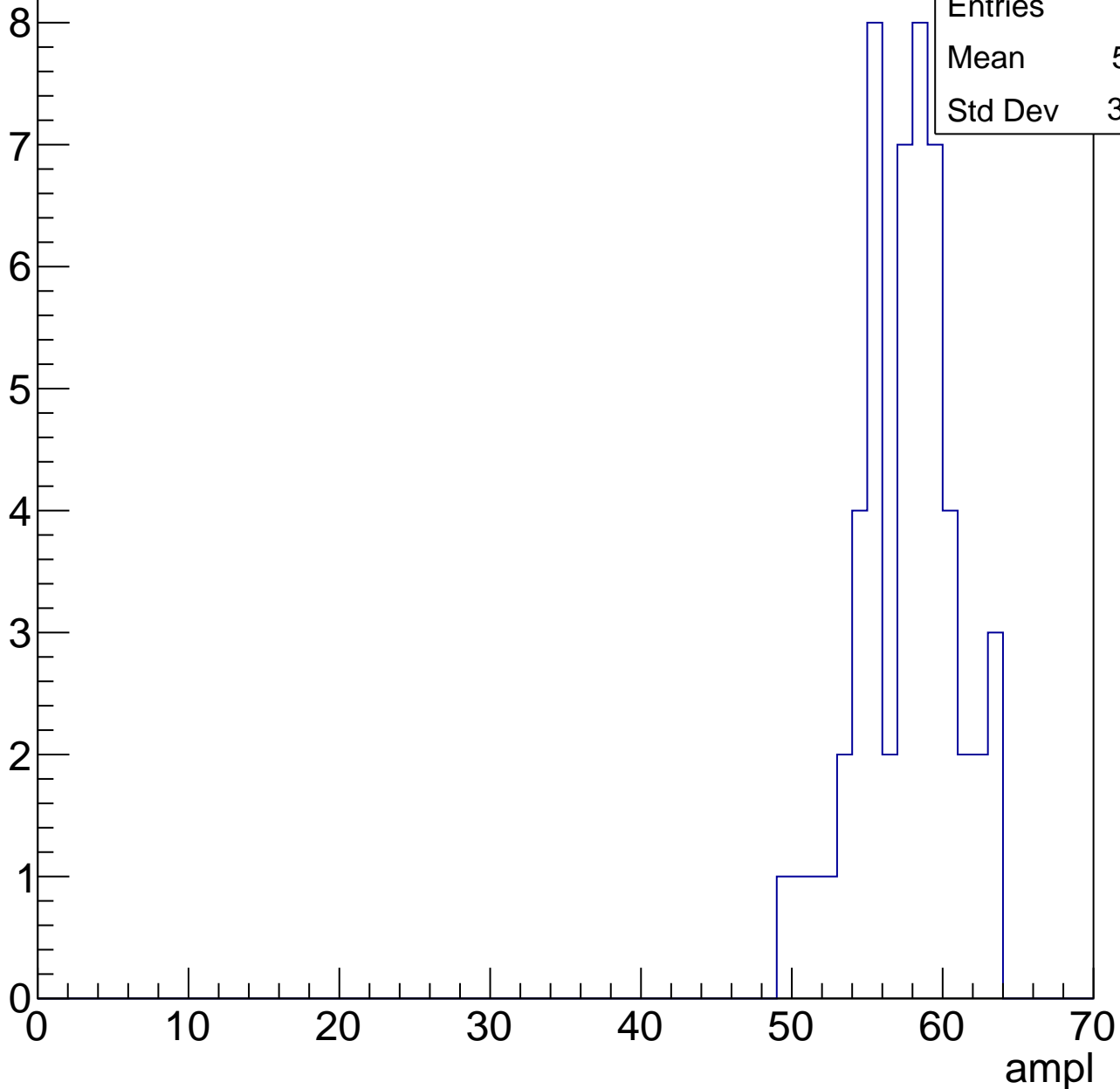


# B0L000S, U26-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.11
Std Dev	3.184

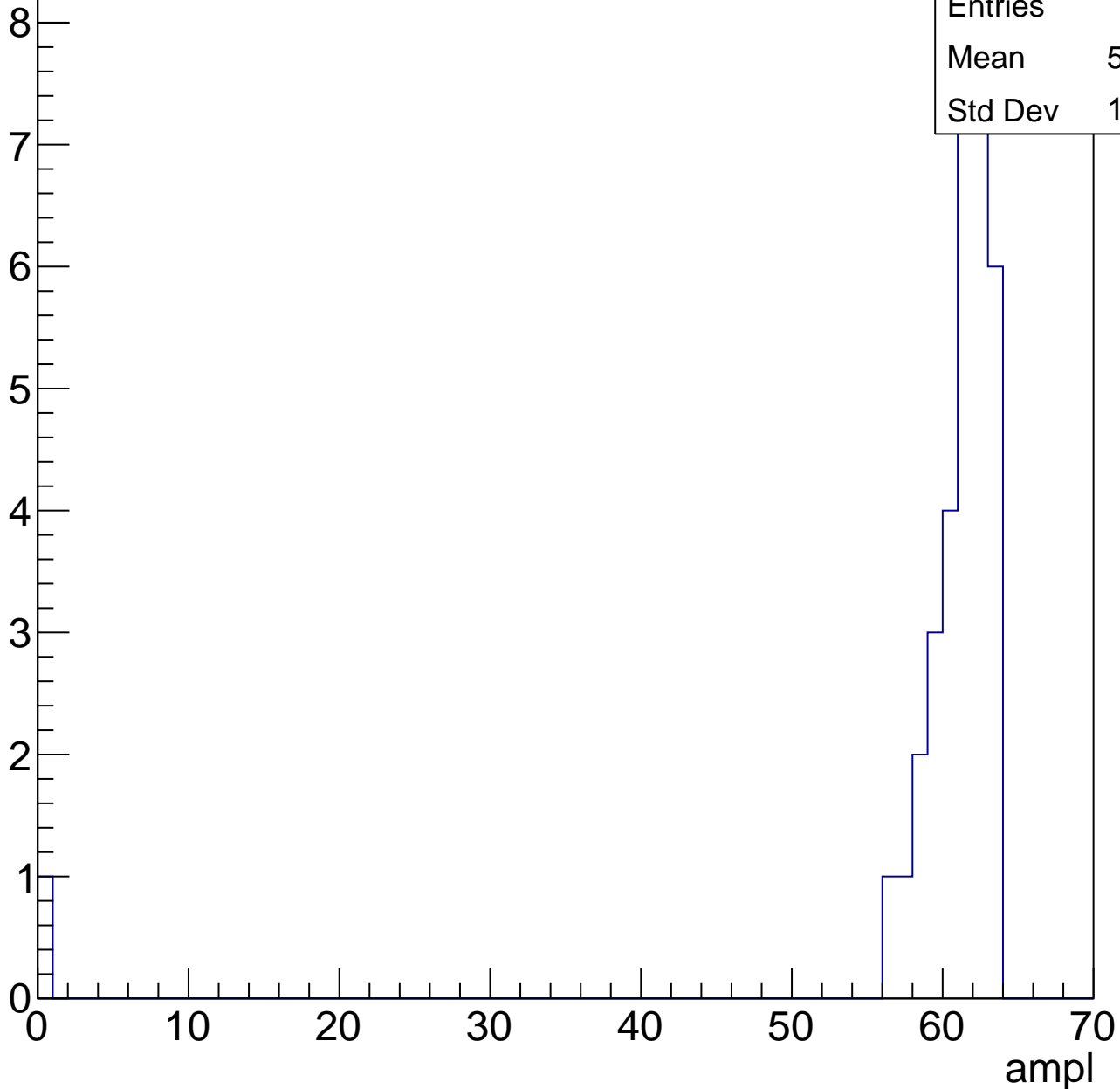


# B0L000S, U26-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	59.06
Std Dev	10.43



# B0L000S, U26-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch96, adc0

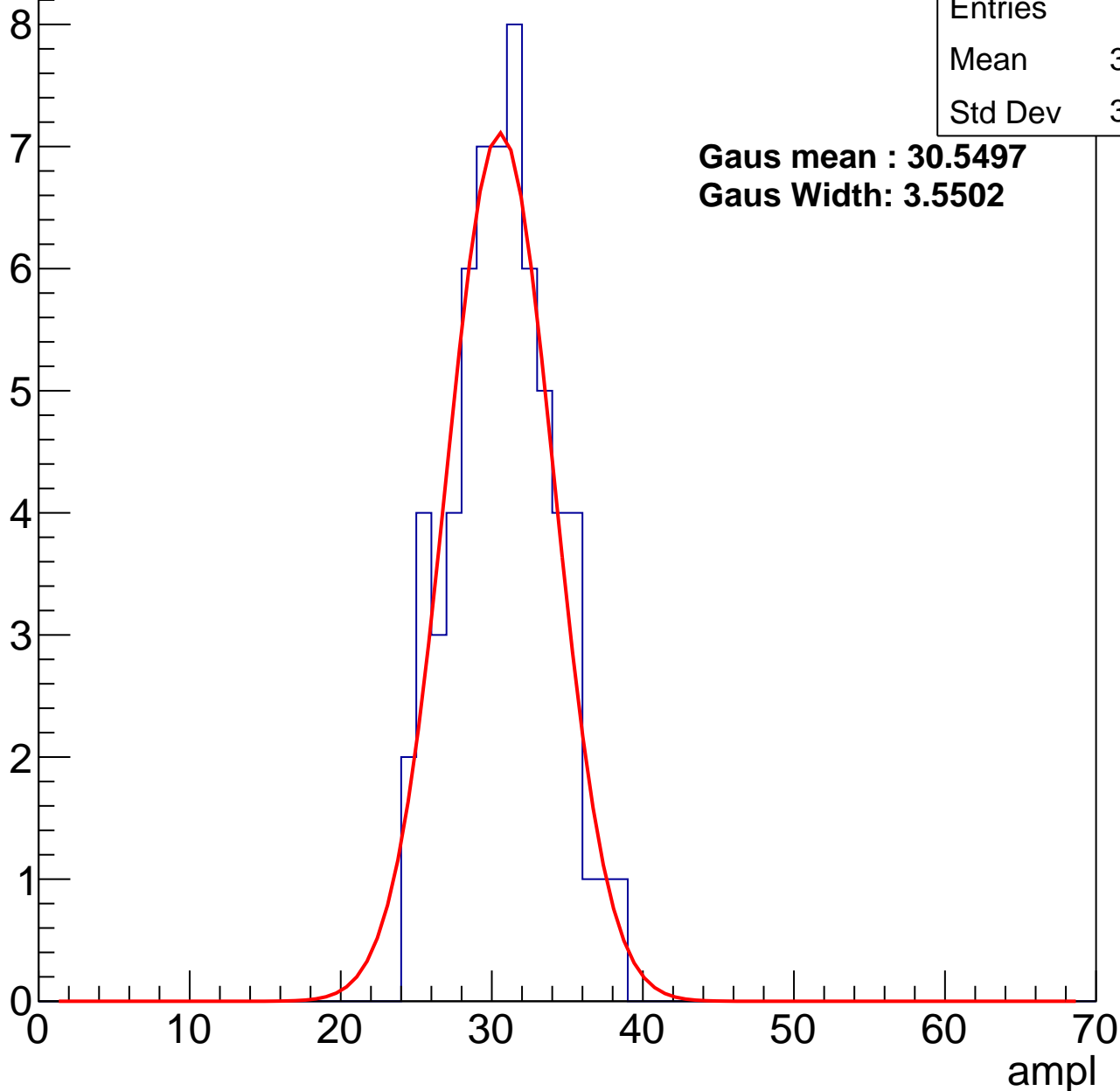
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.27
Std Dev	3.267

**Gaus mean : 30.5497**

**Gaus Width: 3.5502**



# B0L000S, U26-ch96, adc1

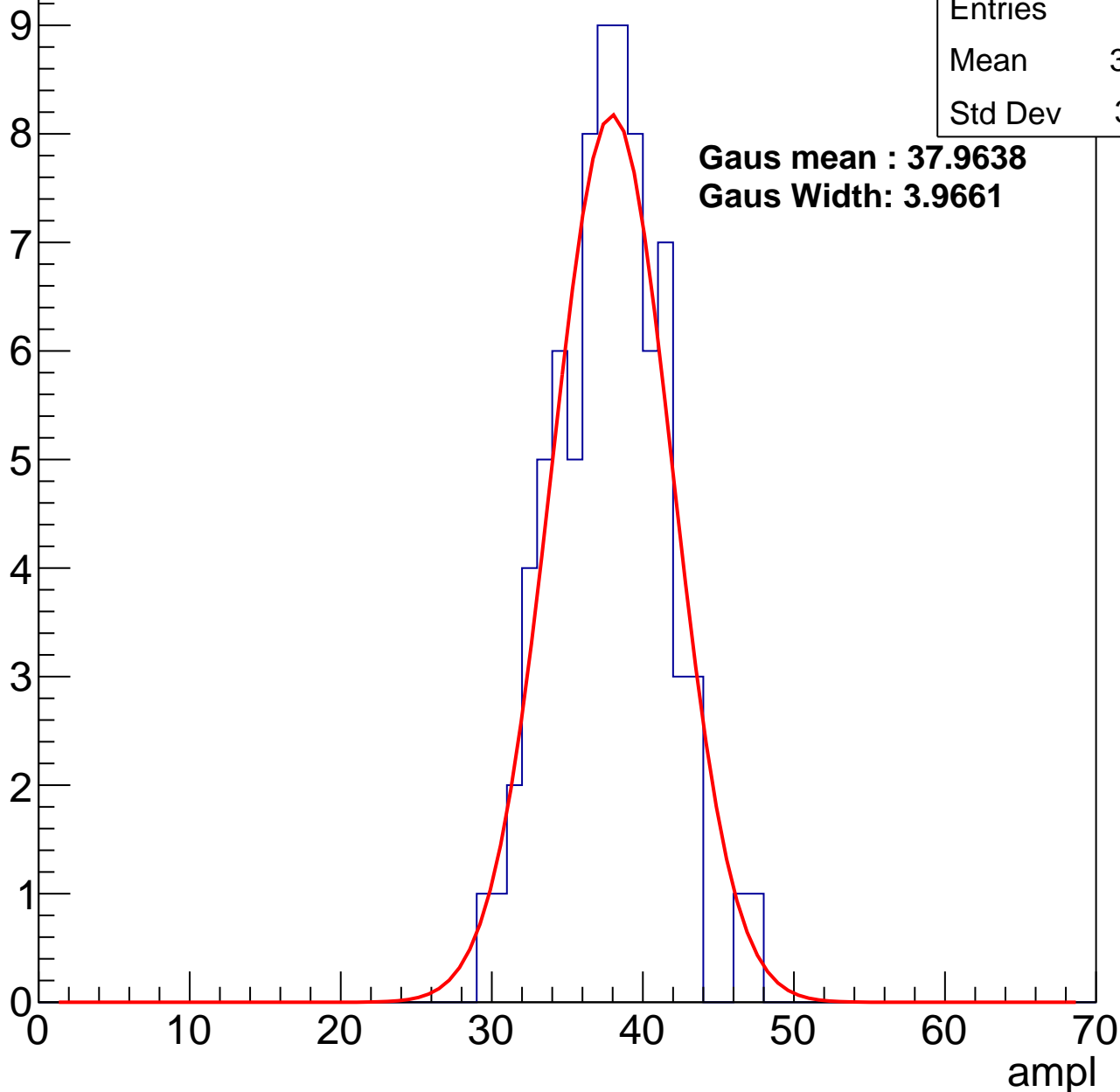
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	37.25
Std Dev	3.581

**Gaus mean : 37.9638**

**Gaus Width: 3.9661**



# B0L000S, U26-ch96, adc2

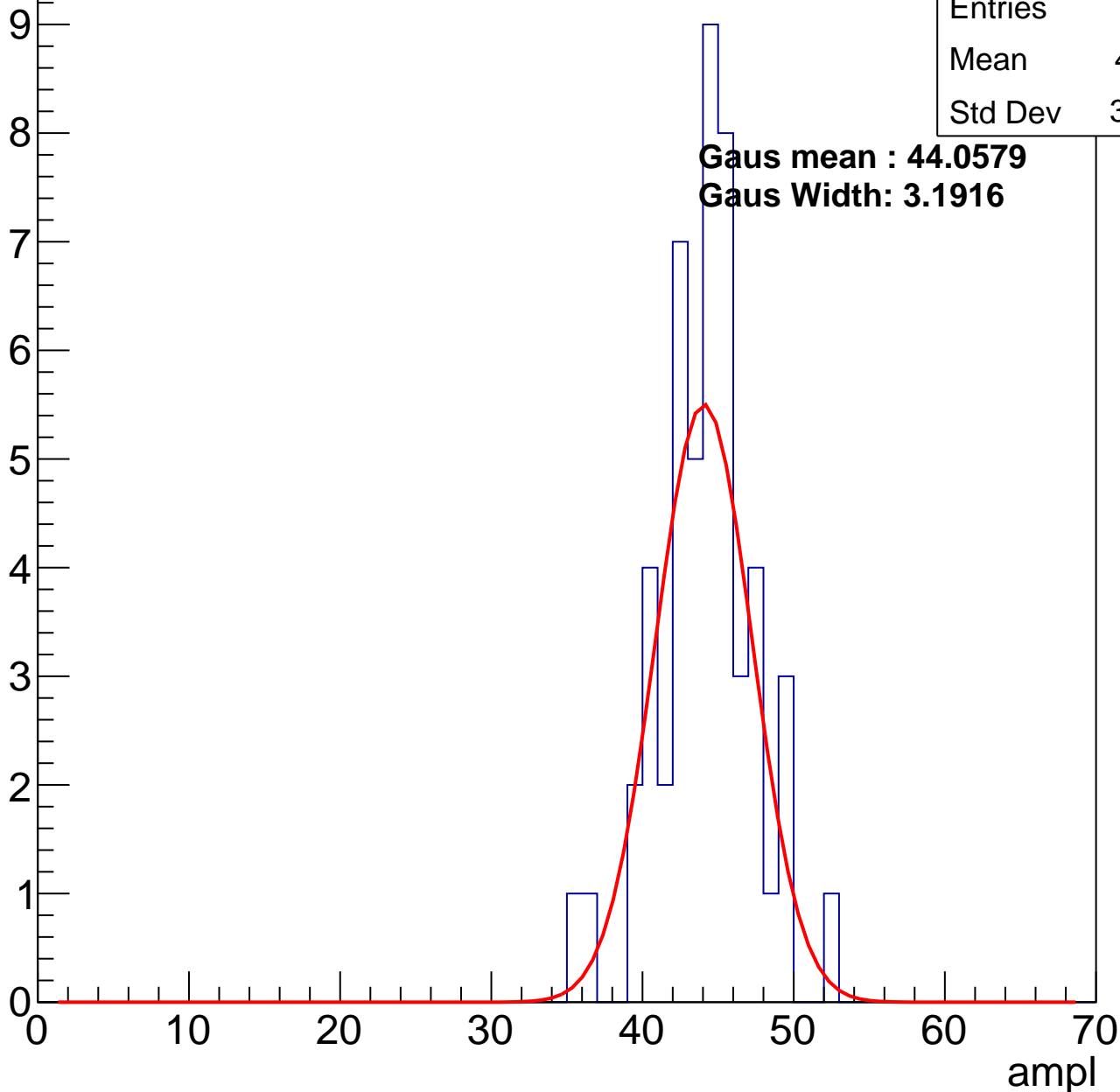
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	43.71
Std Dev	3.183

**Gaus mean : 44.0579**

**Gaus Width: 3.1916**

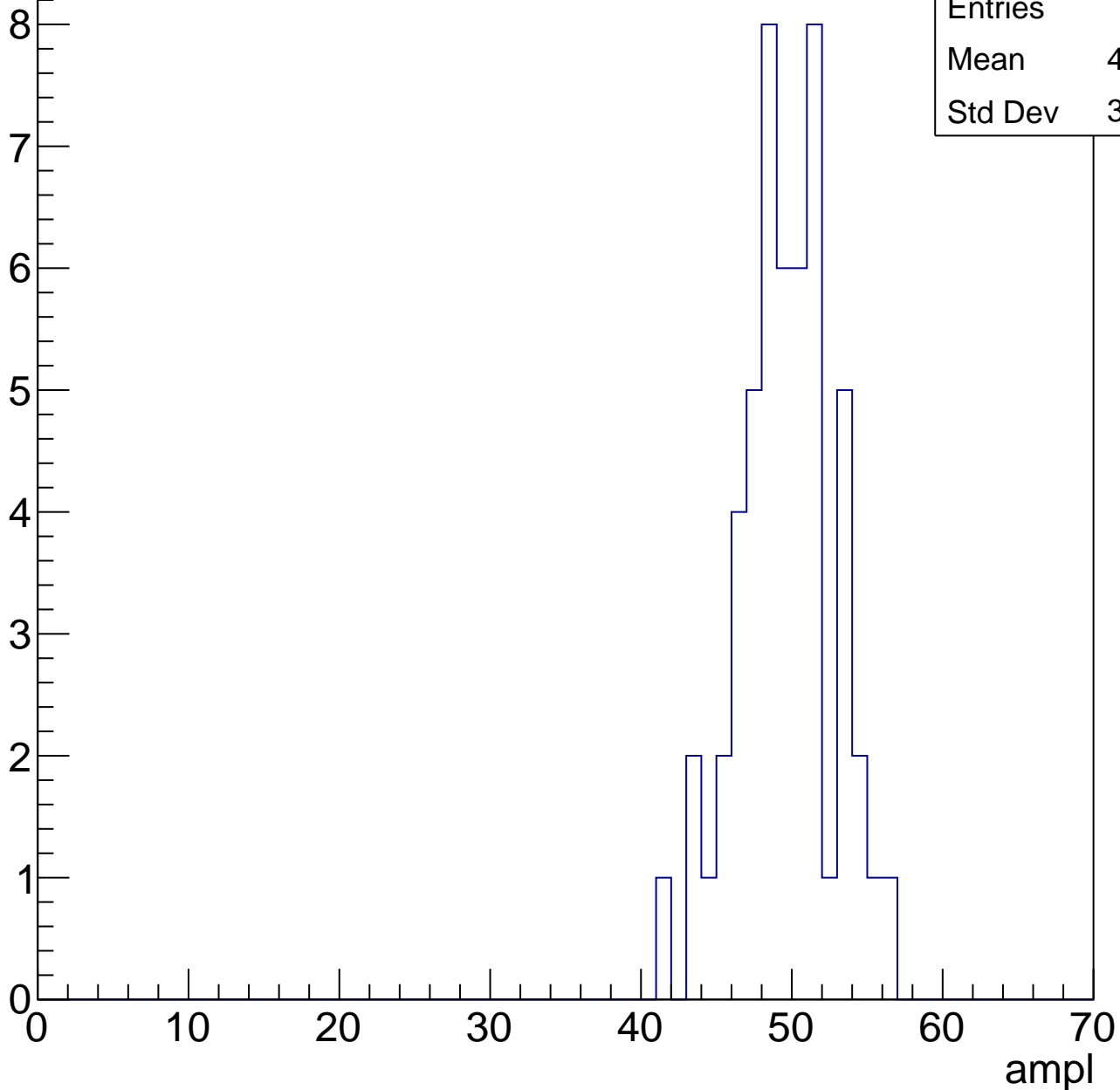


# B0L000S, U26-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

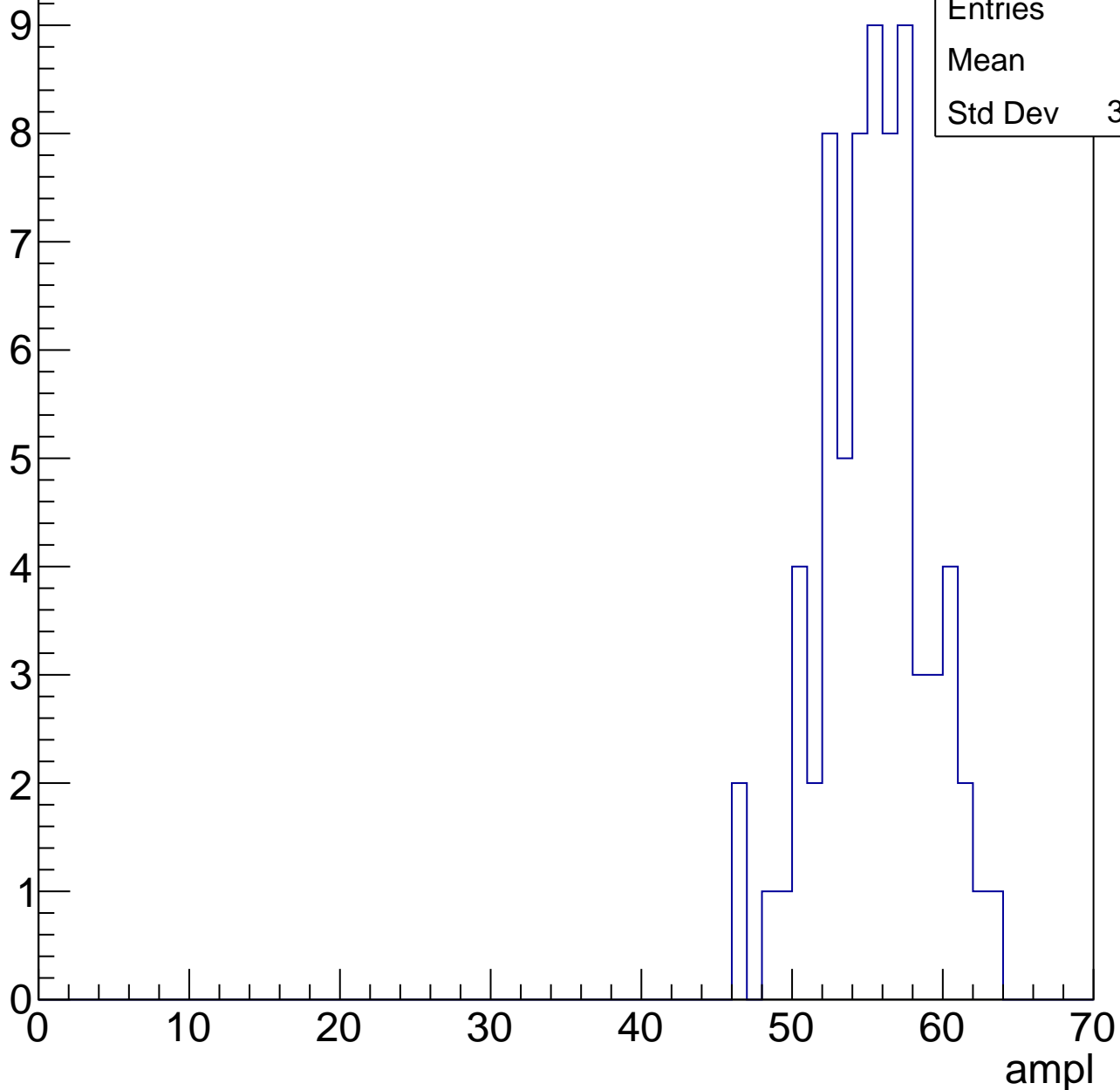
Entries	53
Mean	49.09
Std Dev	3.128



# B0L000S, U26-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

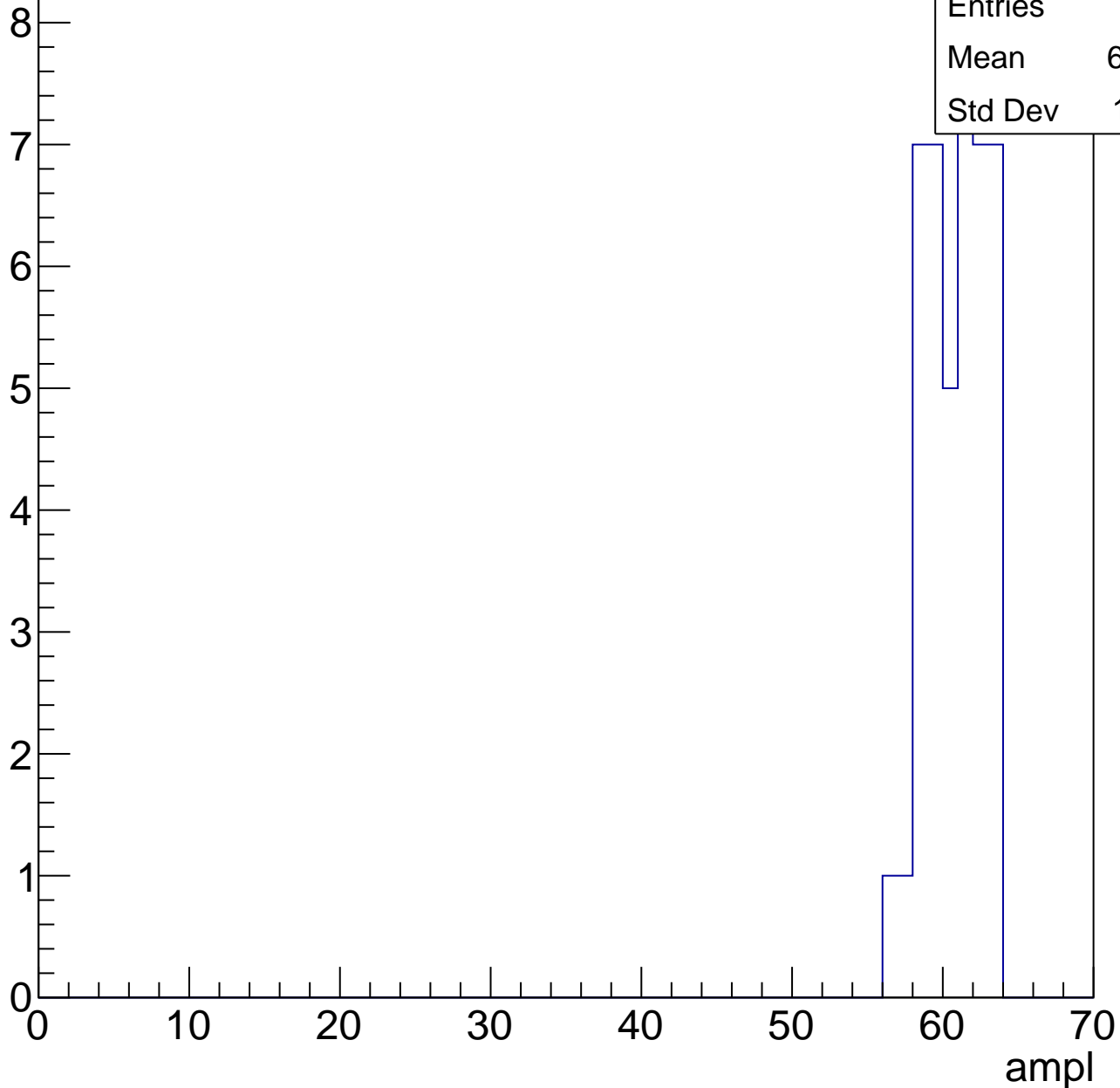
Entry



# B0L000S, U26-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

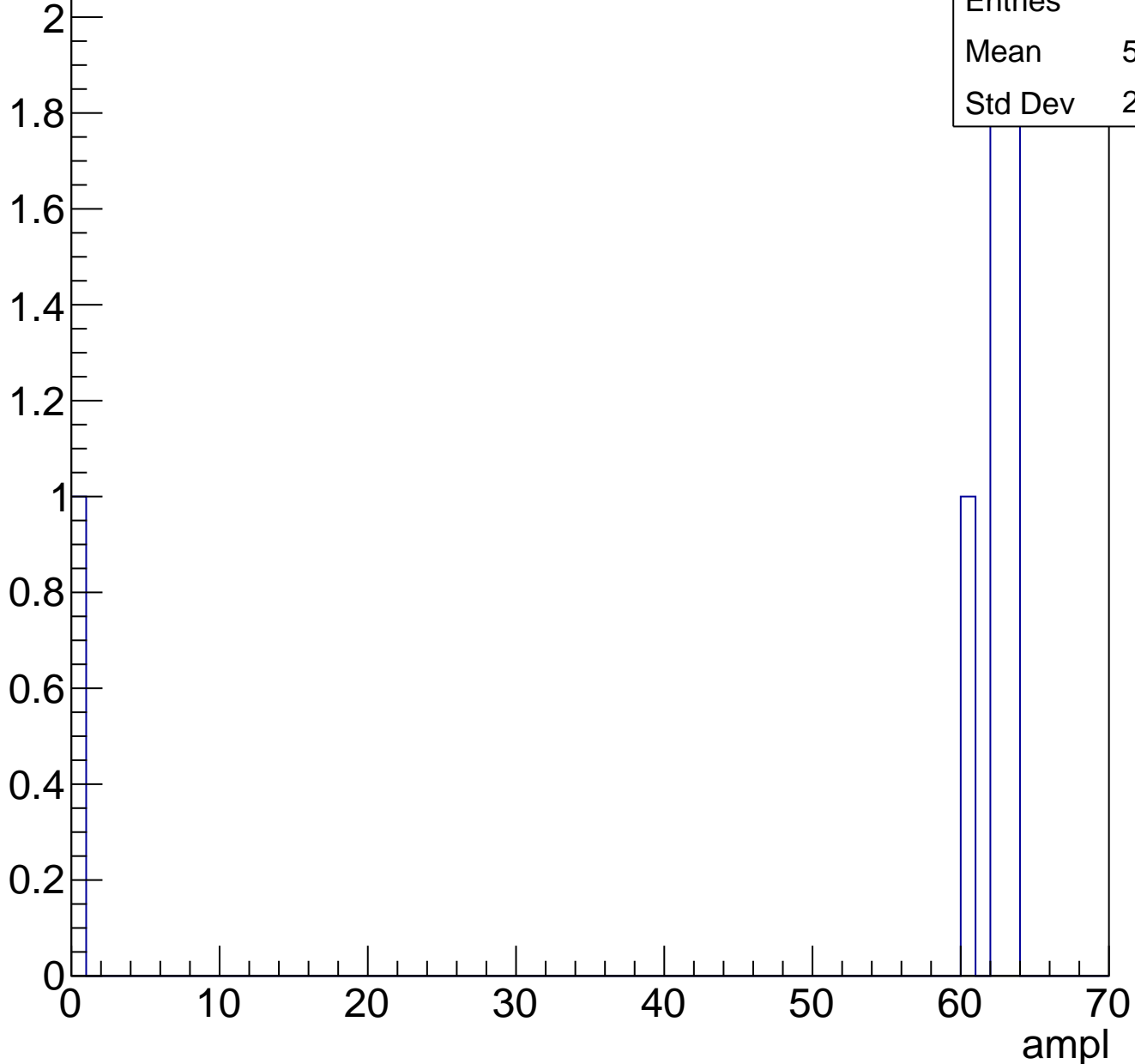
Entry



# B0L000S, U26-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

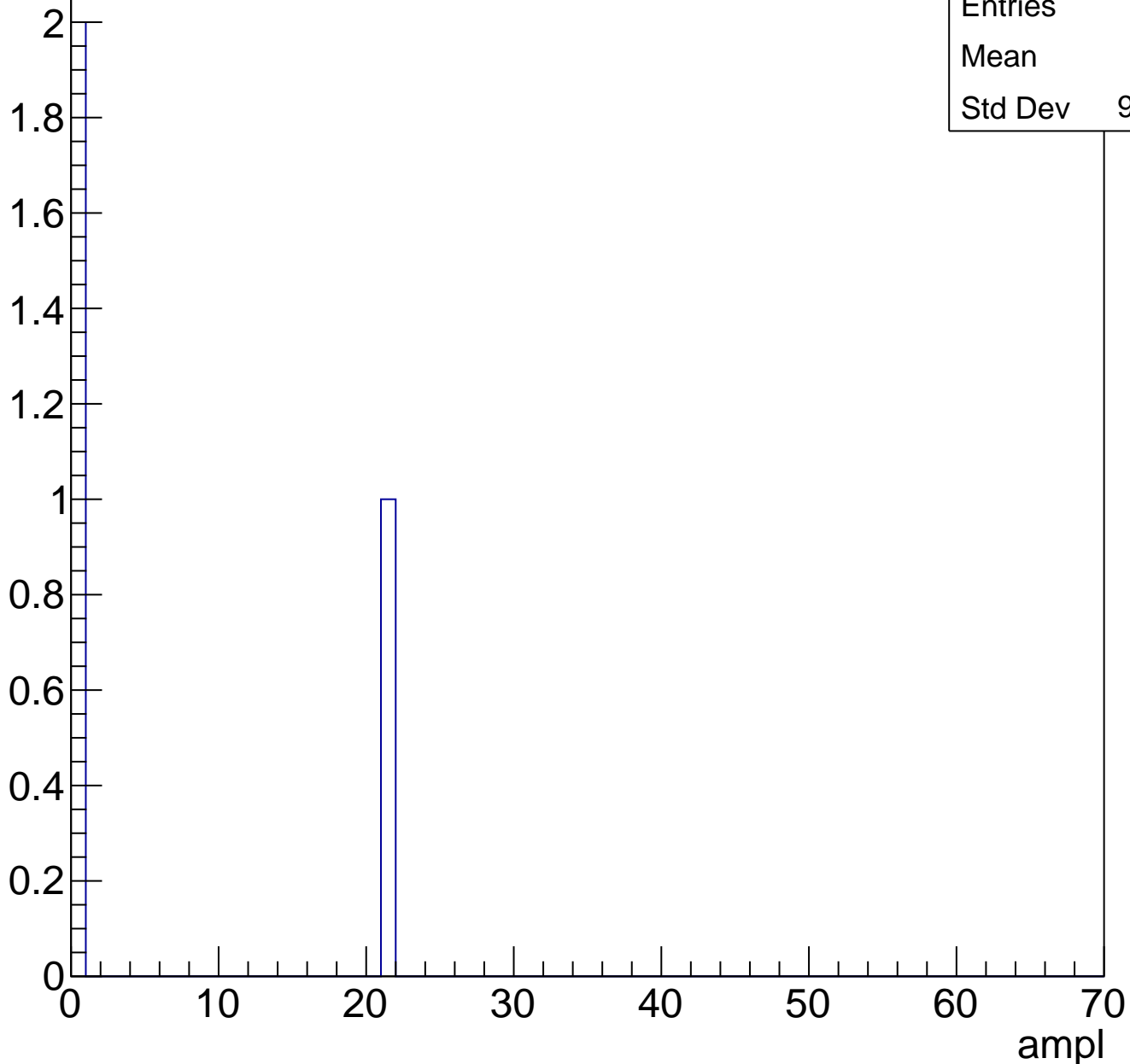




# B0L000S, U26-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7
Std Dev	9.899

# B0L000S, U26-ch97, adc0

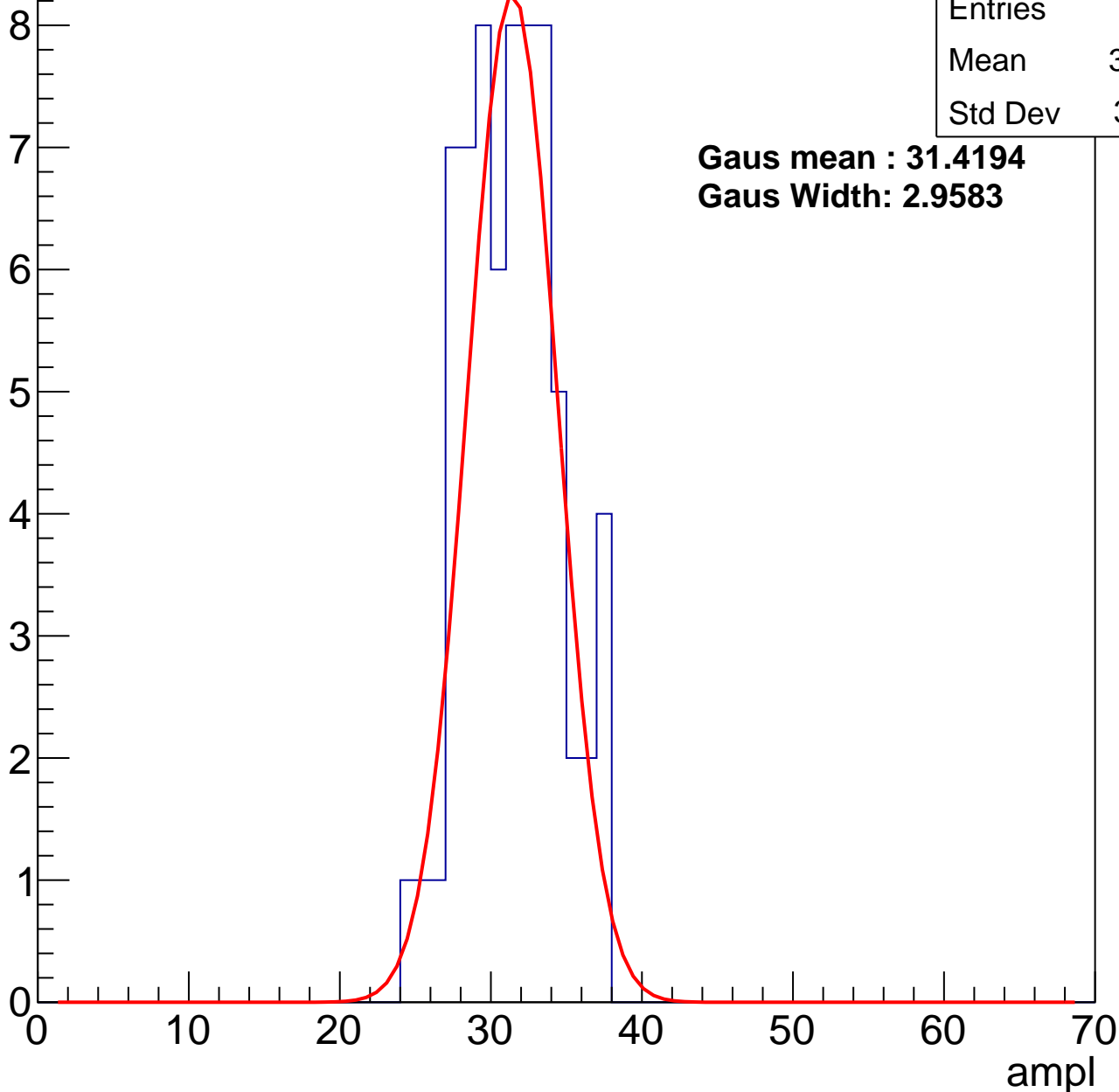
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.88
Std Dev	3.051

**Gaus mean : 31.4194**

**Gaus Width: 2.9583**



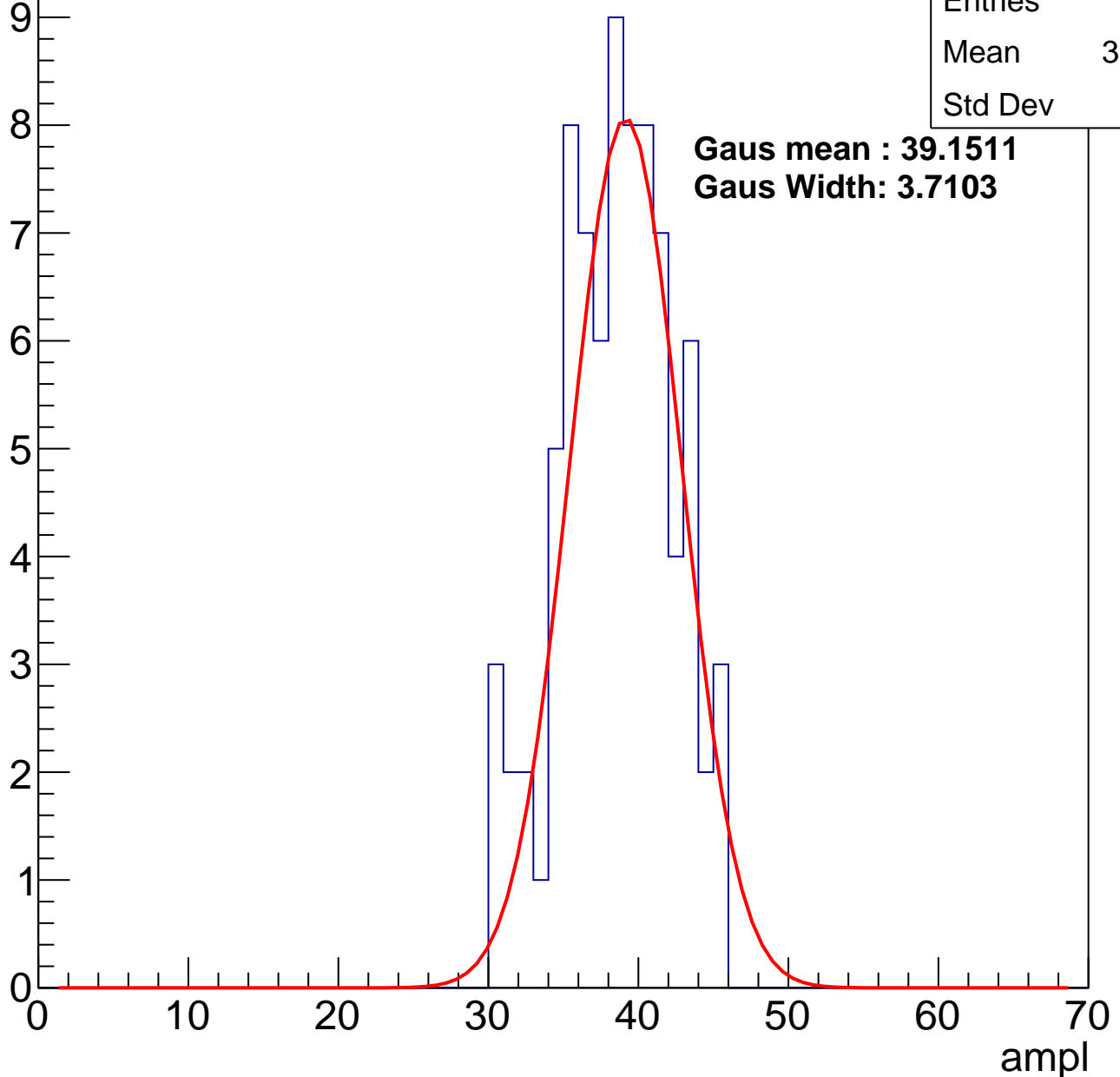
# B0L000S, U26-ch97, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	38.06
Std Dev	3.7

**Gaus mean : 39.1511**  
**Gaus Width: 3.7103**



# B0L000S, U26-ch97, adc2

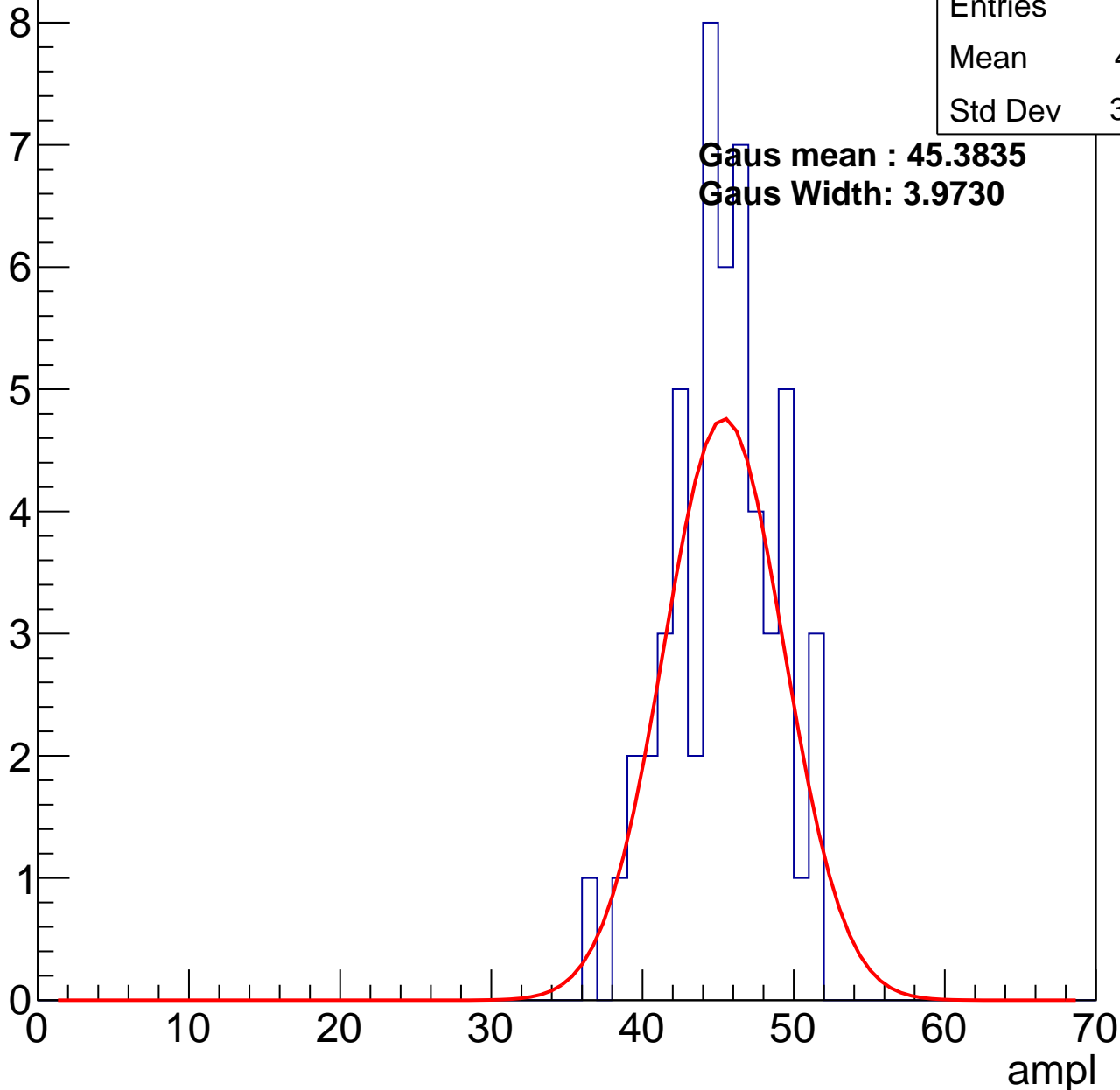
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	44.81
Std Dev	3.426

**Gaus mean : 45.3835**

**Gaus Width: 3.9730**

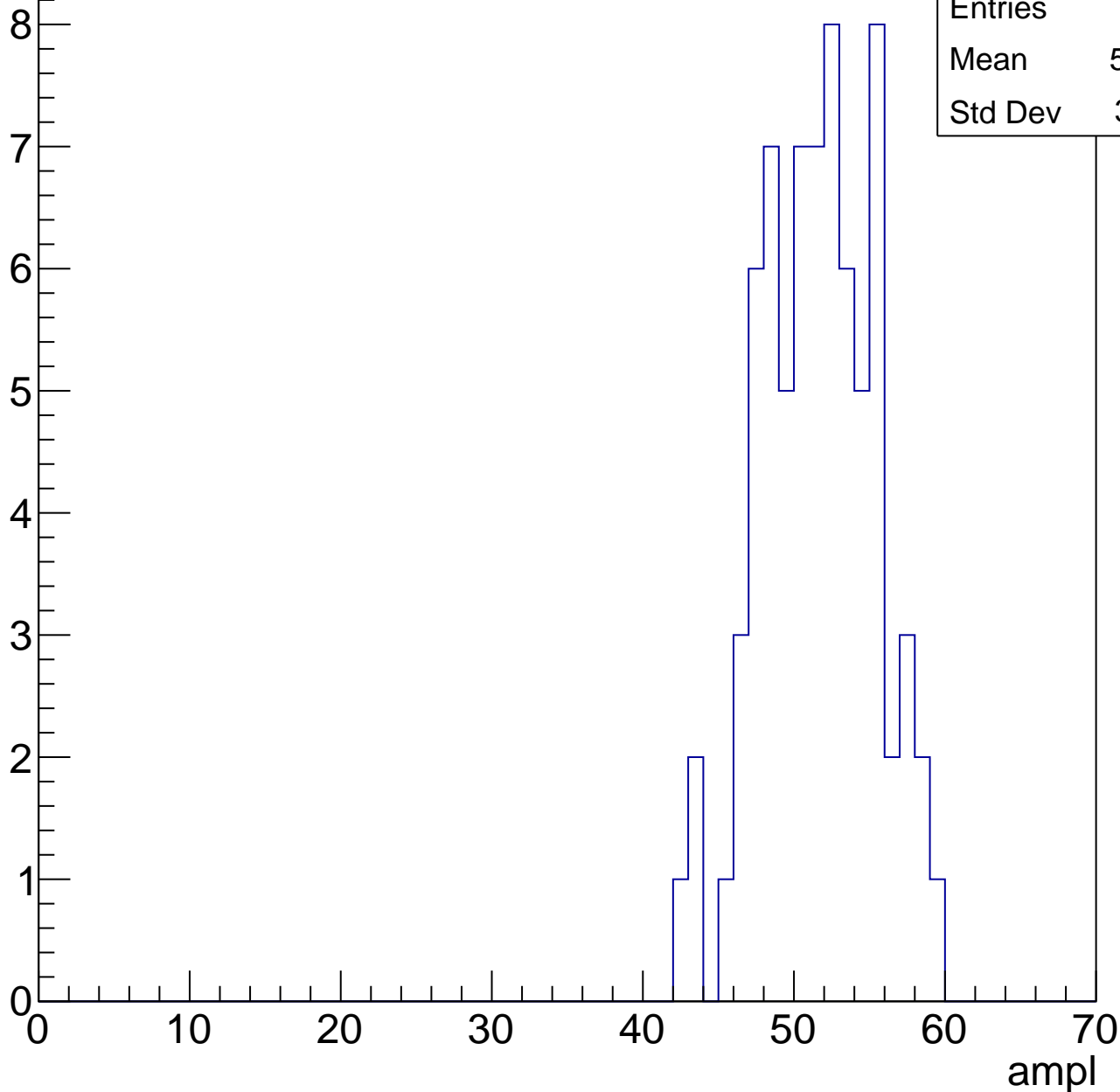


# B0L000S, U26-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	51.12
Std Dev	3.731

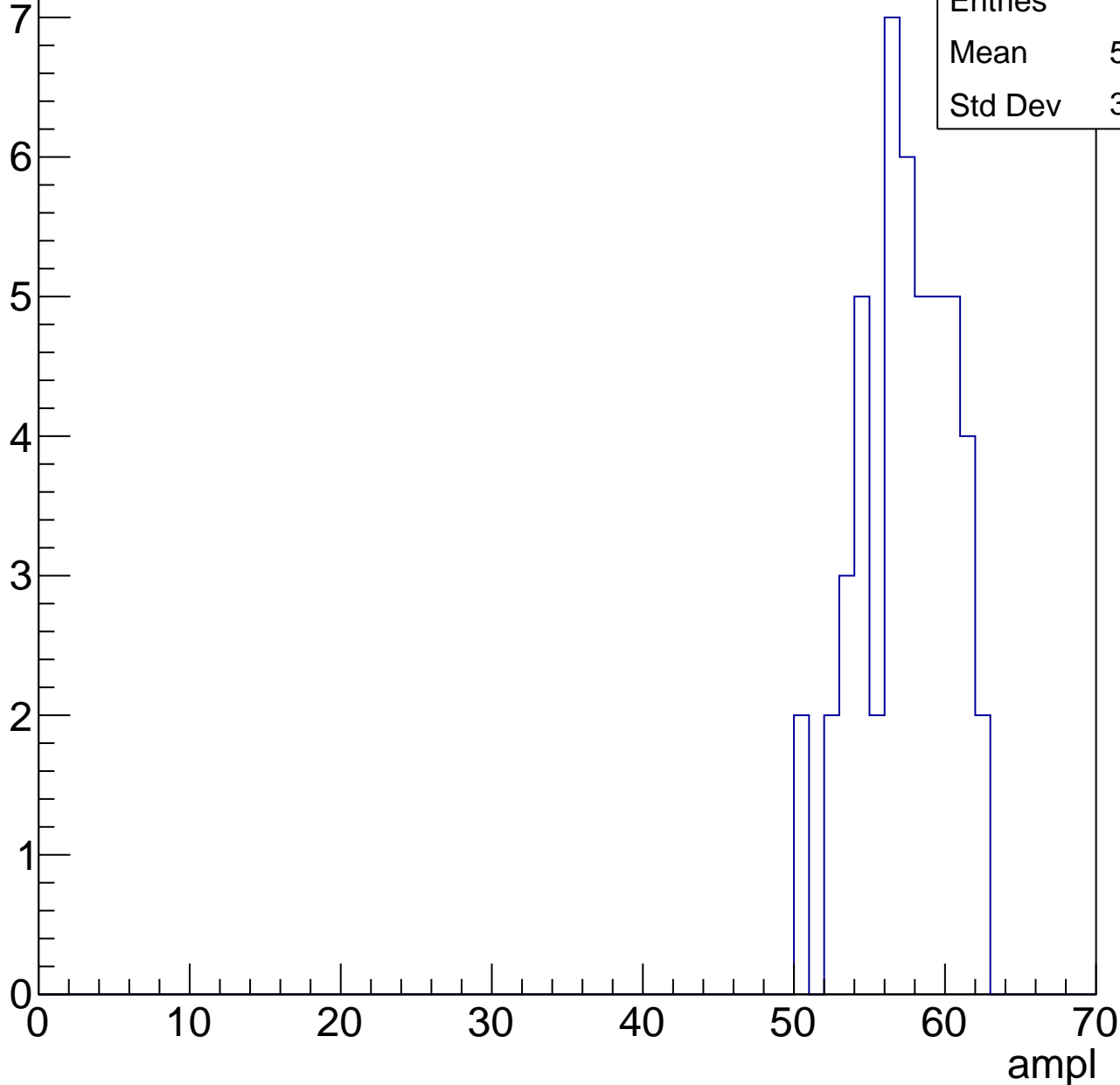


# B0L000S, U26-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	56.88
Std Dev	3.025

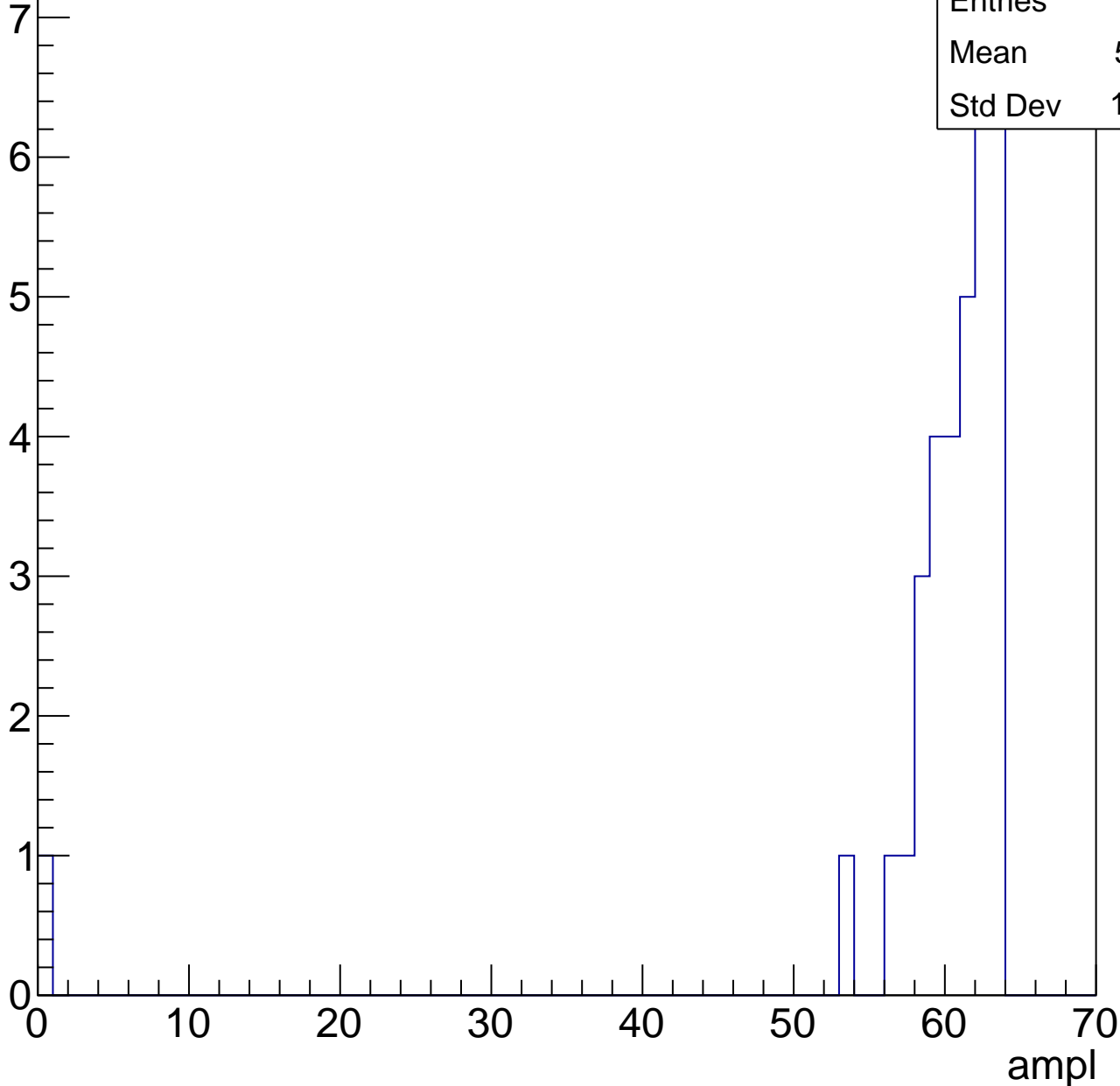


# B0L000S, U26-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.71
Std Dev	10.47



# B0L000S, U26-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U26-ch98, adc0

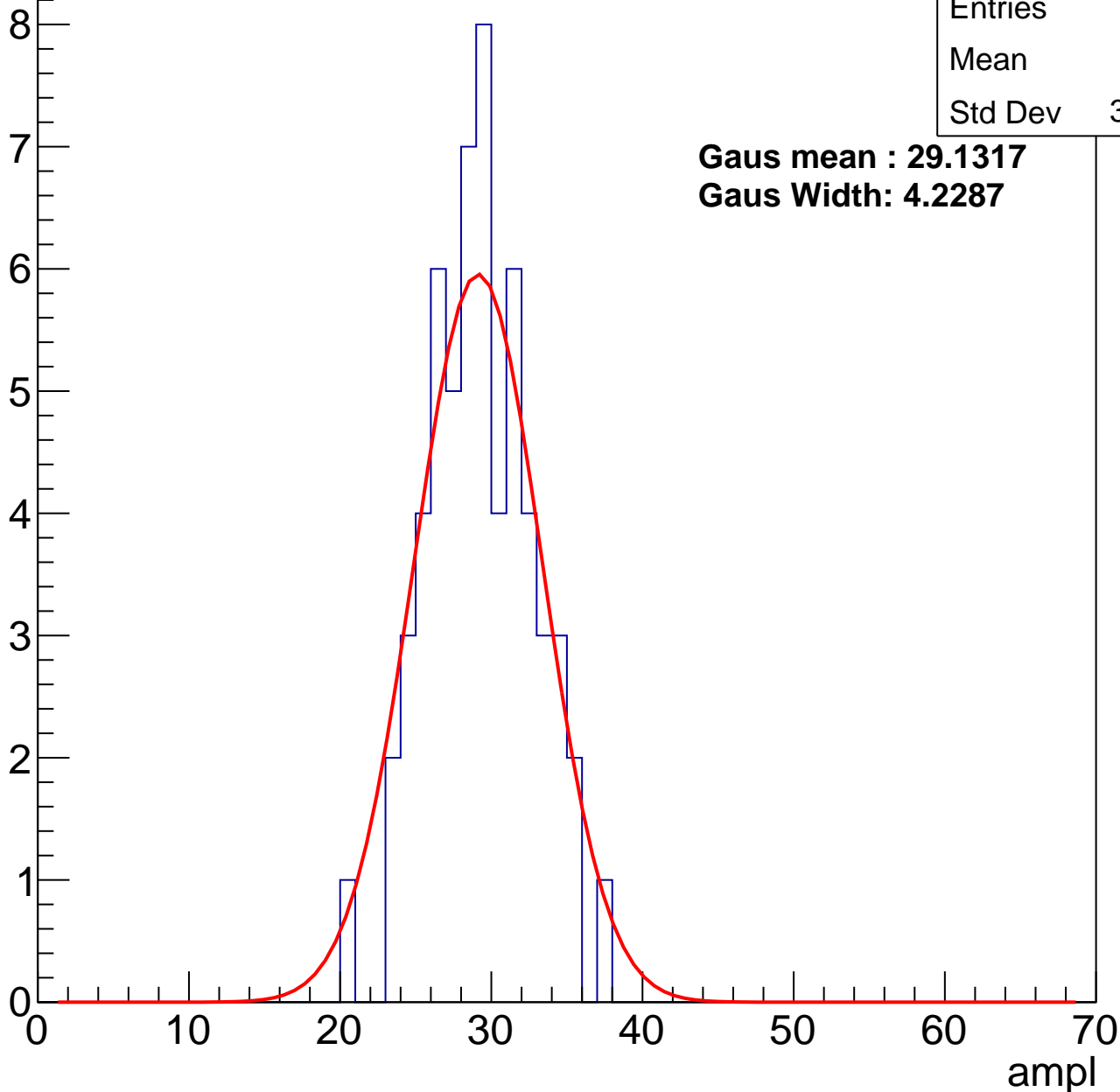
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	28.8
Std Dev	3.429

**Gaus mean : 29.1317**

**Gaus Width: 4.2287**



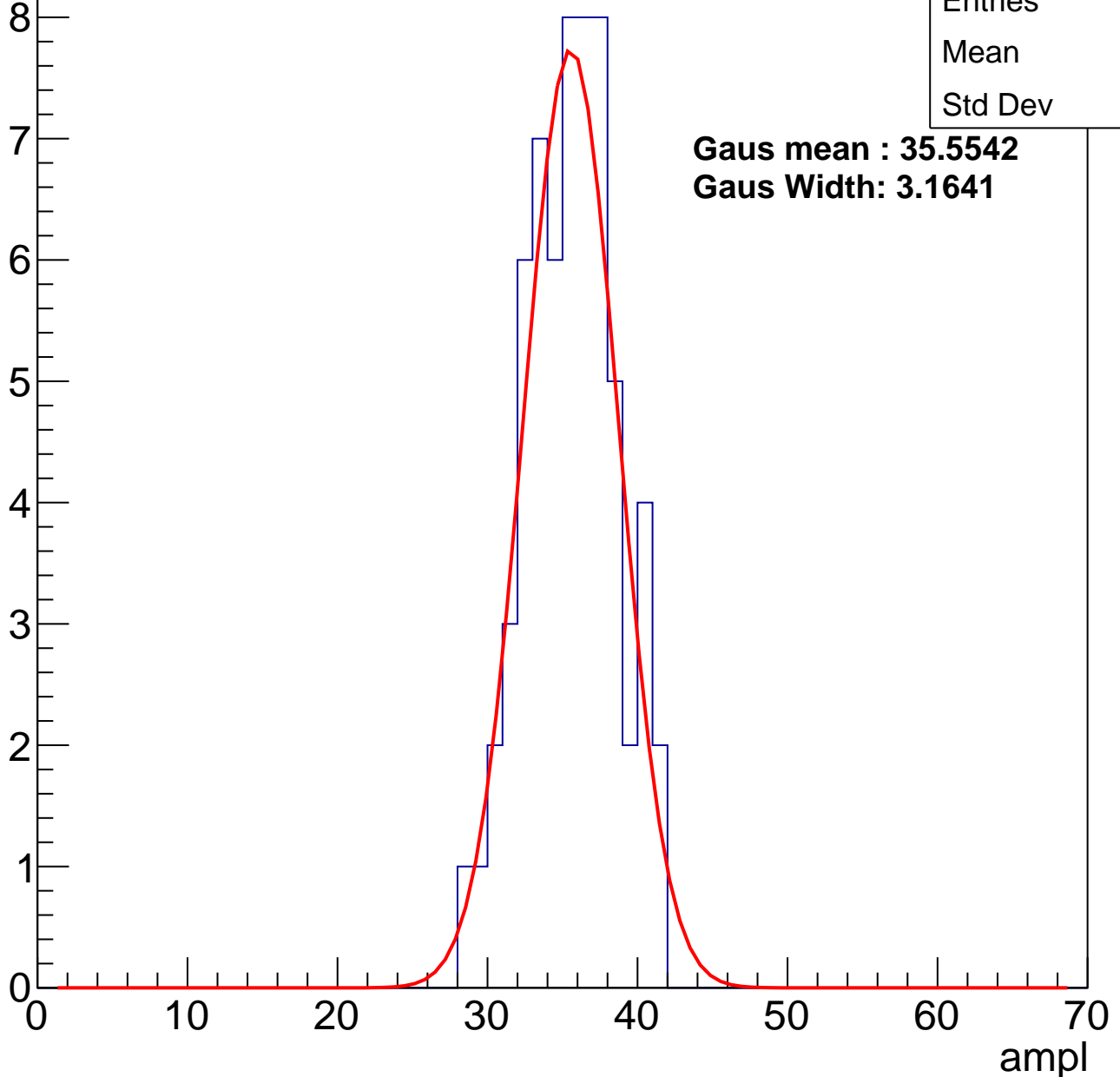
# B0L000S, U26-ch98, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	35.1
Std Dev	2.98

**Gaus mean : 35.5542**  
**Gaus Width: 3.1641**



# B0L000S, U26-ch98, adc2

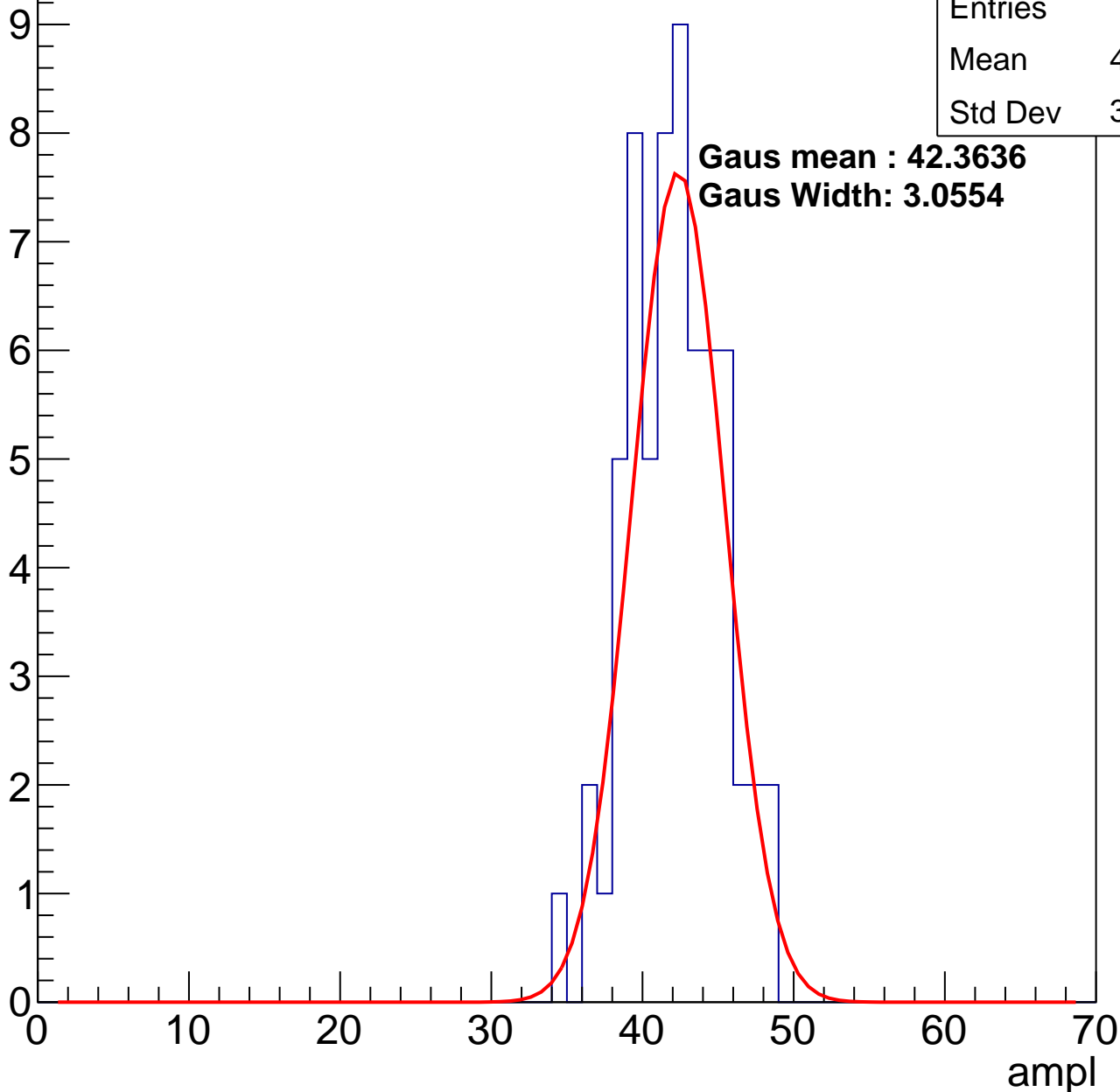
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	41.67
Std Dev	3.013

**Gaus mean : 42.3636**

**Gaus Width: 3.0554**

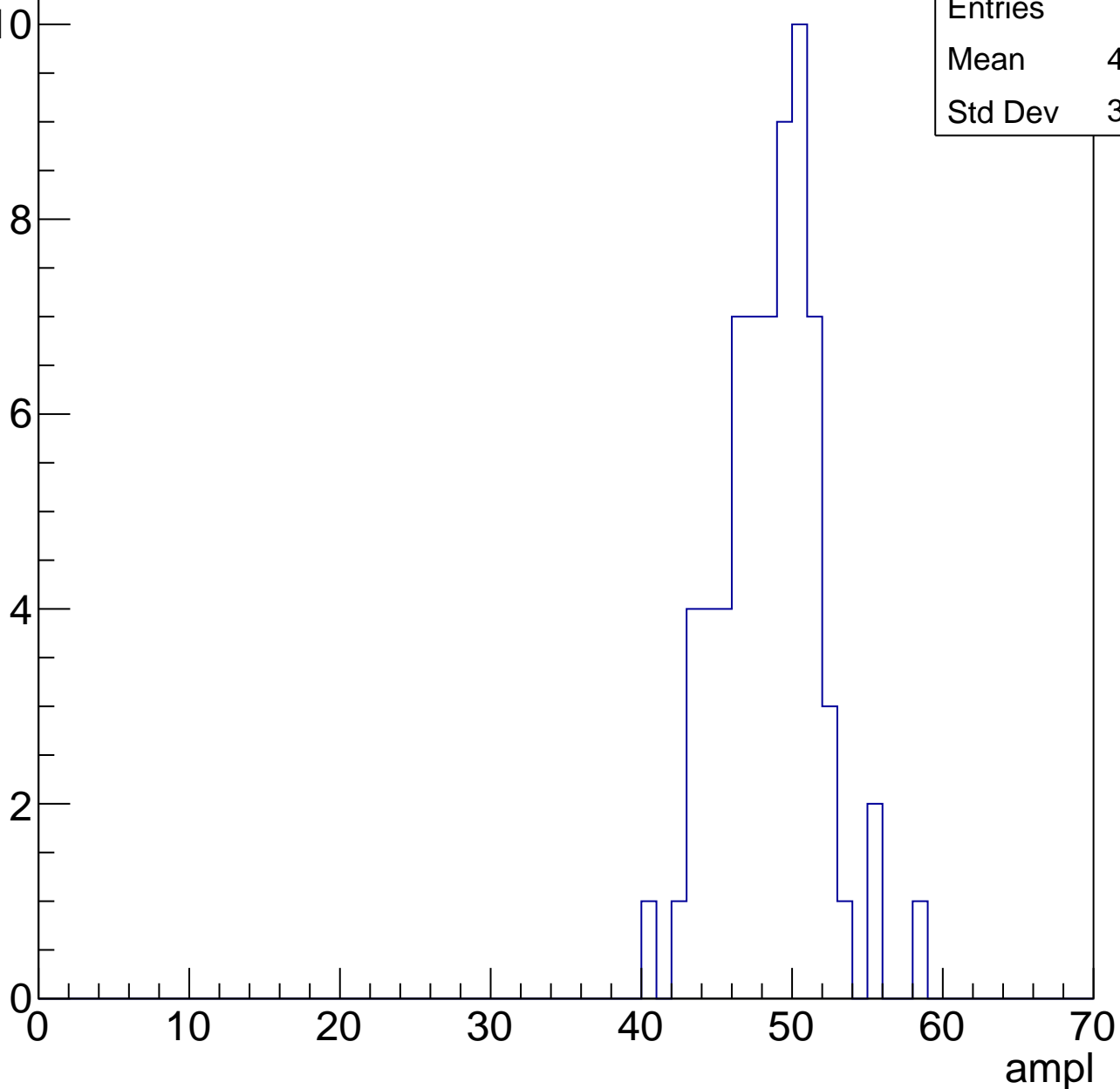


# B0L000S, U26-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

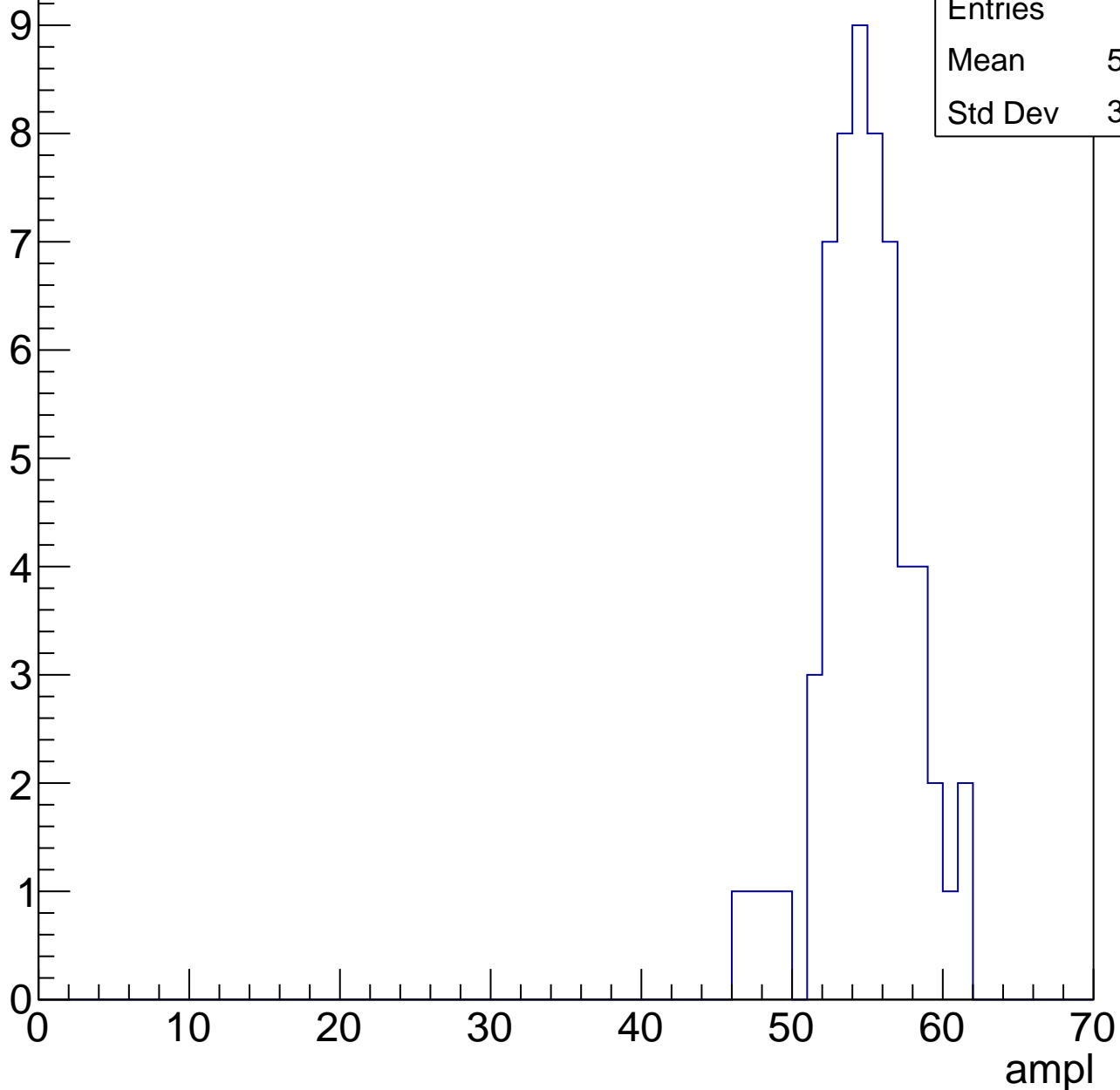
Entries	68
Mean	48.12
Std Dev	3.247



# B0L000S, U26-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

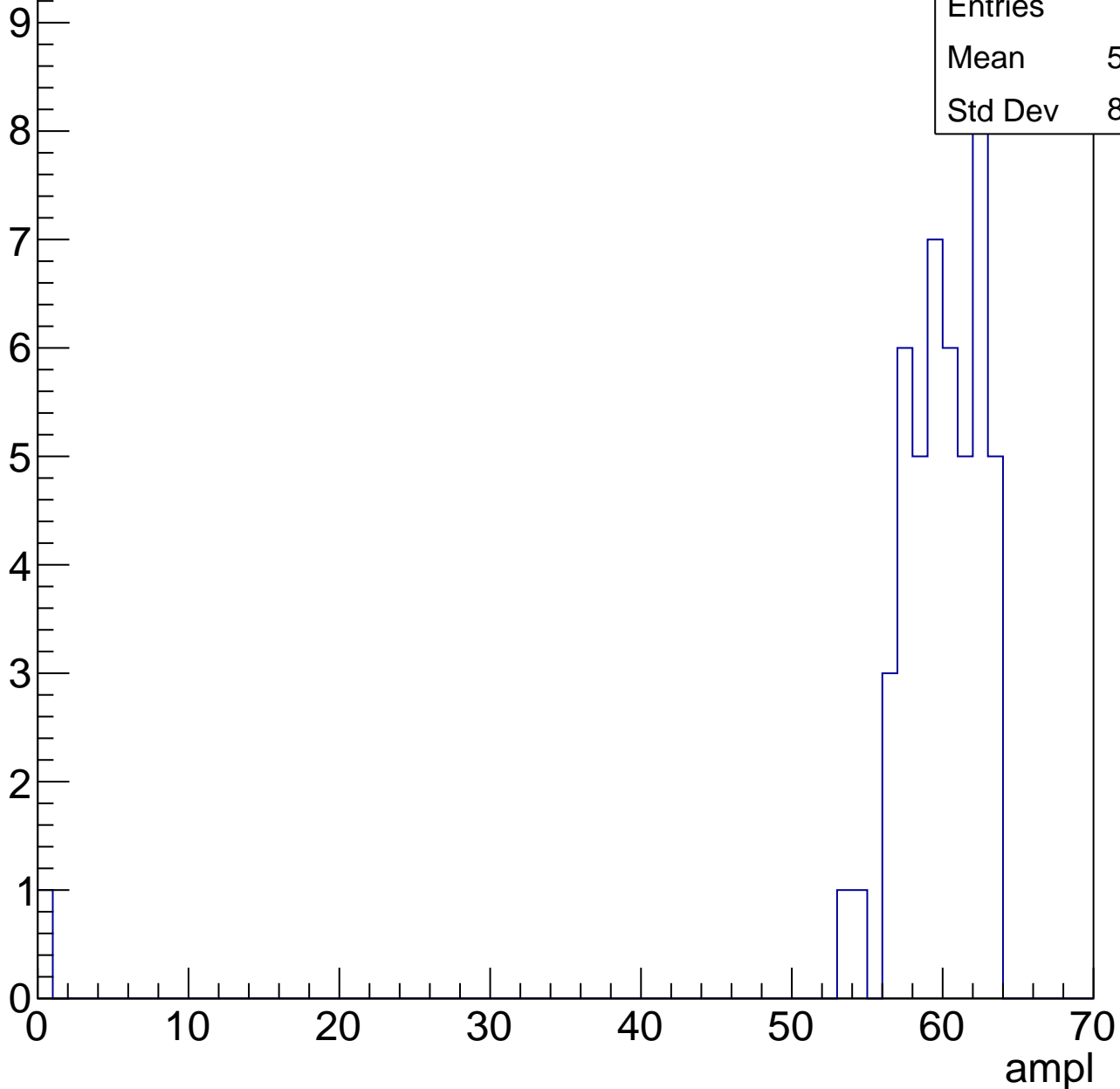


# B0L000S, U26-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	58.33
Std Dev	8.763

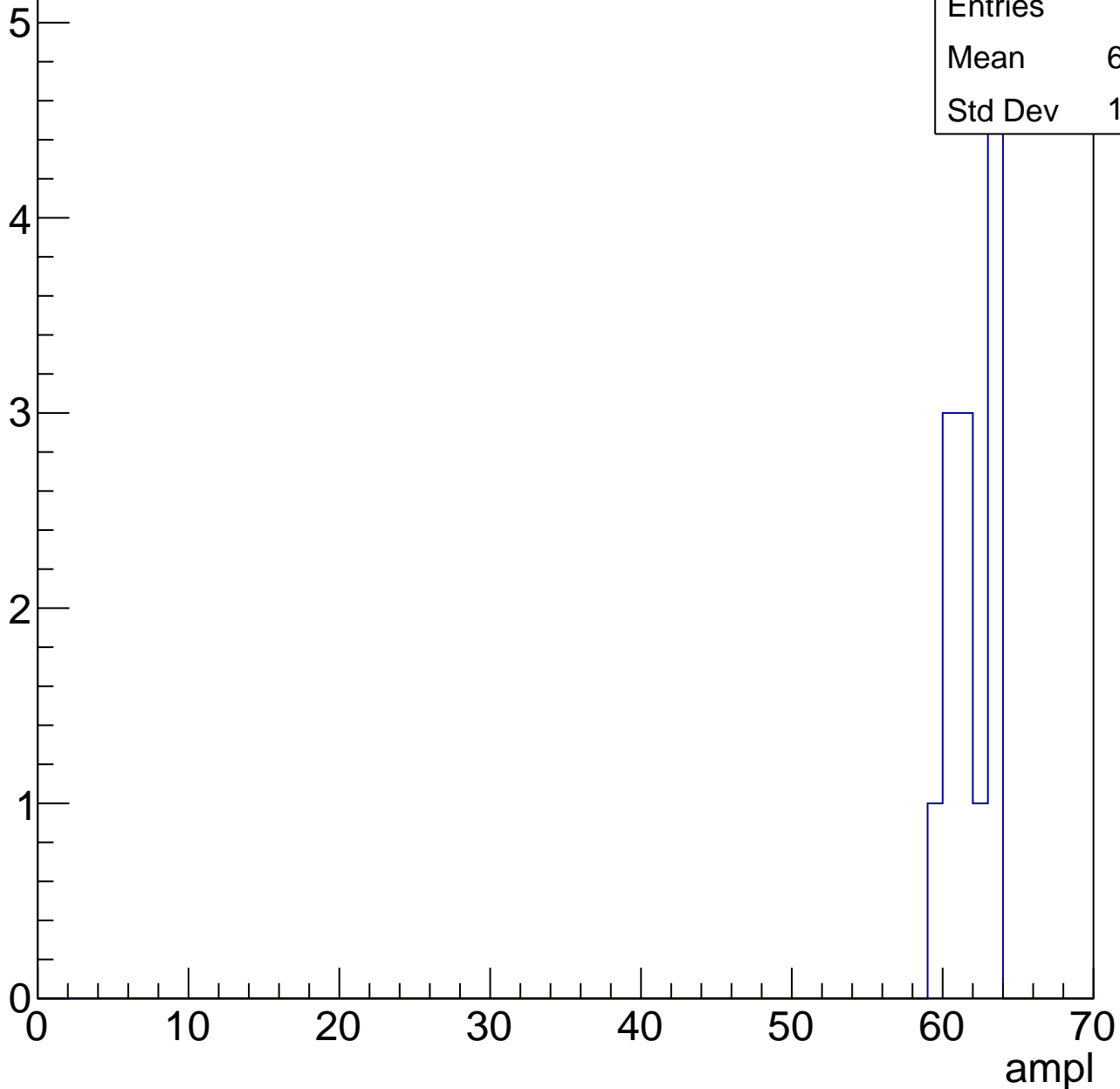


# B0L000S, U26-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	61.46
Std Dev	1.393





# B0L000S, U26-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch99, adc0

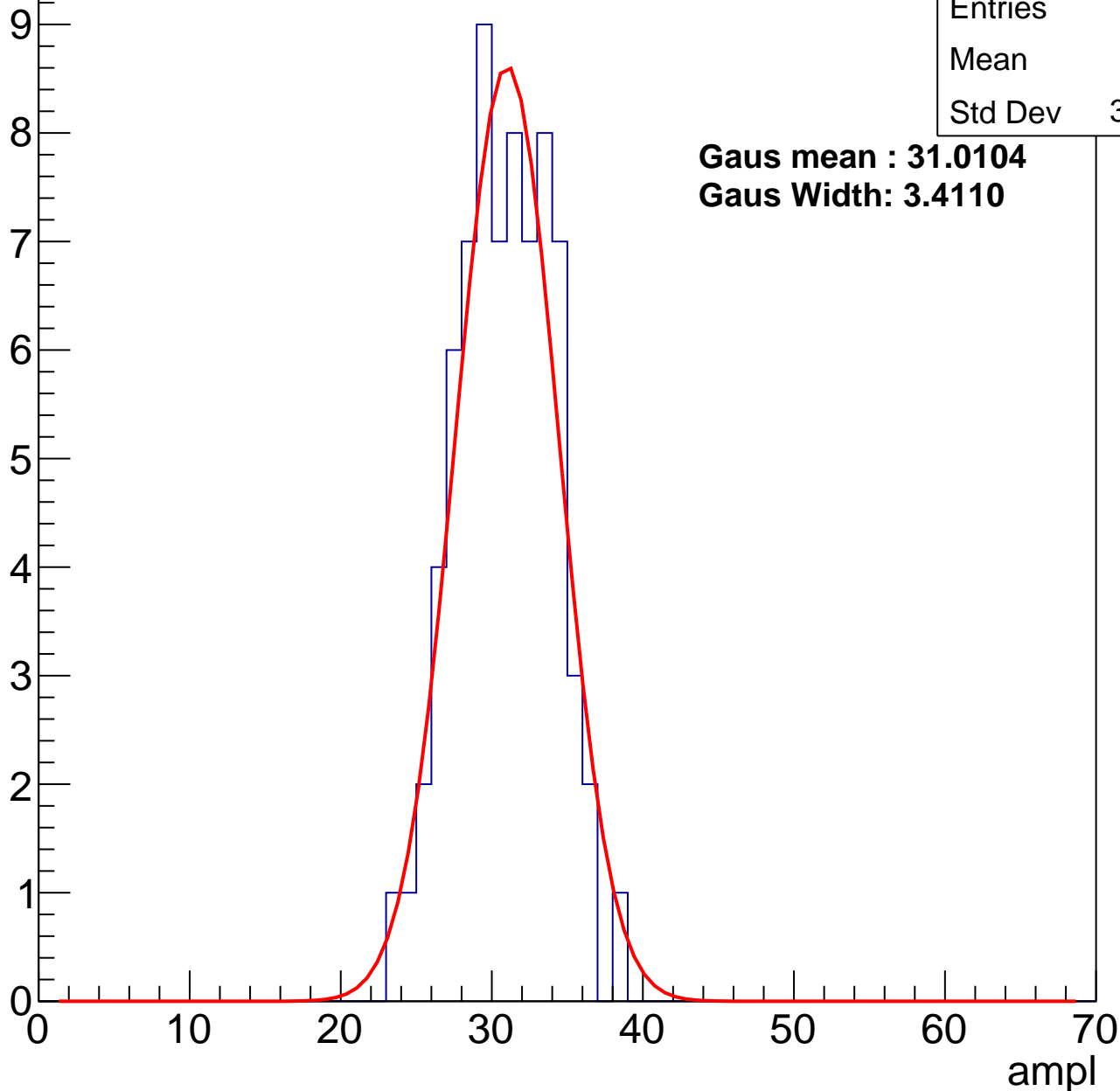
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	30.4
Std Dev	3.118

**Gaus mean : 31.0104**

**Gaus Width: 3.4110**



# B0L000S, U26-ch99, adc1

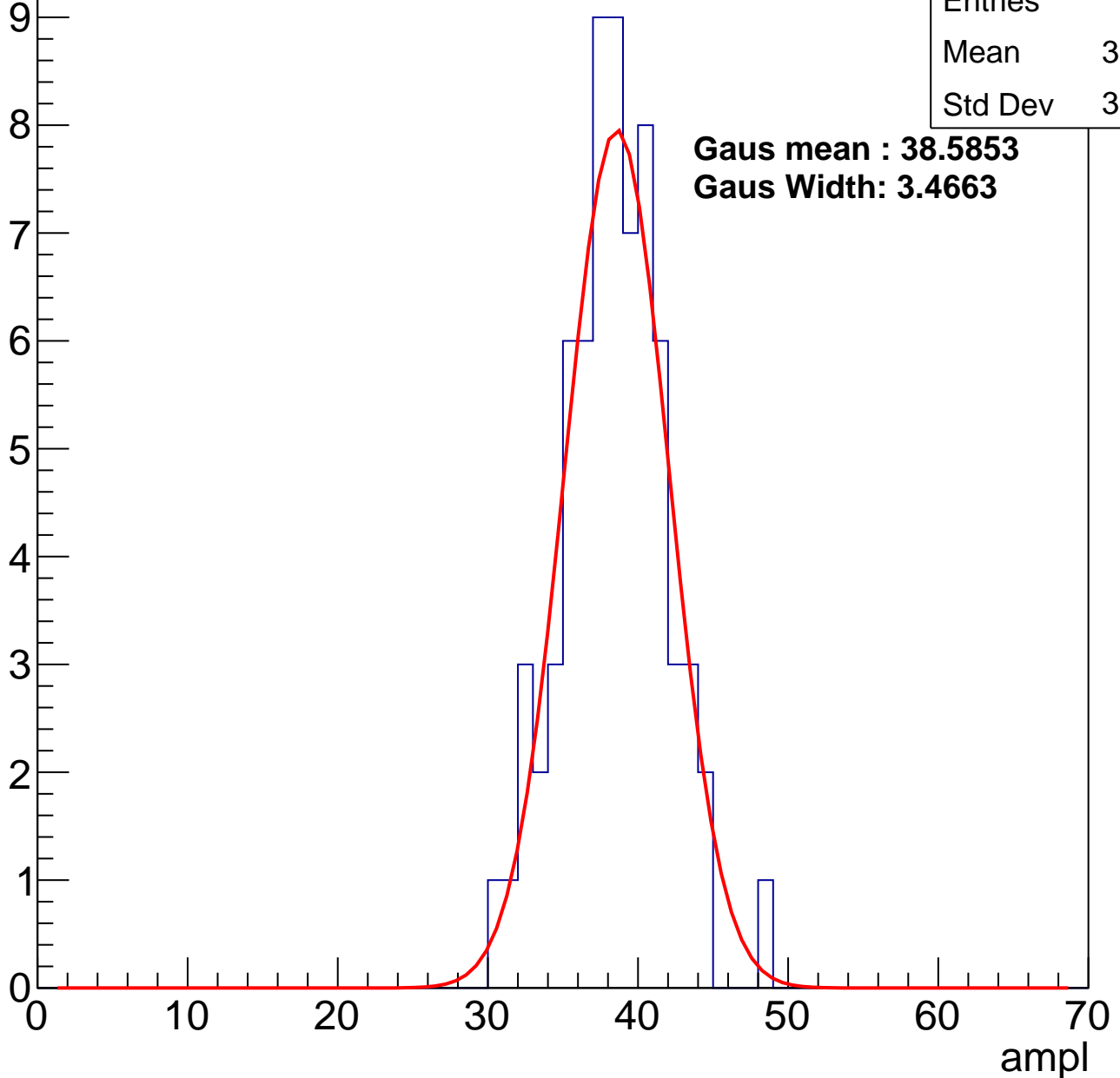
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.94
Std Dev	3.367

**Gaus mean : 38.5853**

**Gaus Width: 3.4663**



# B0L000S, U26-ch99, adc2

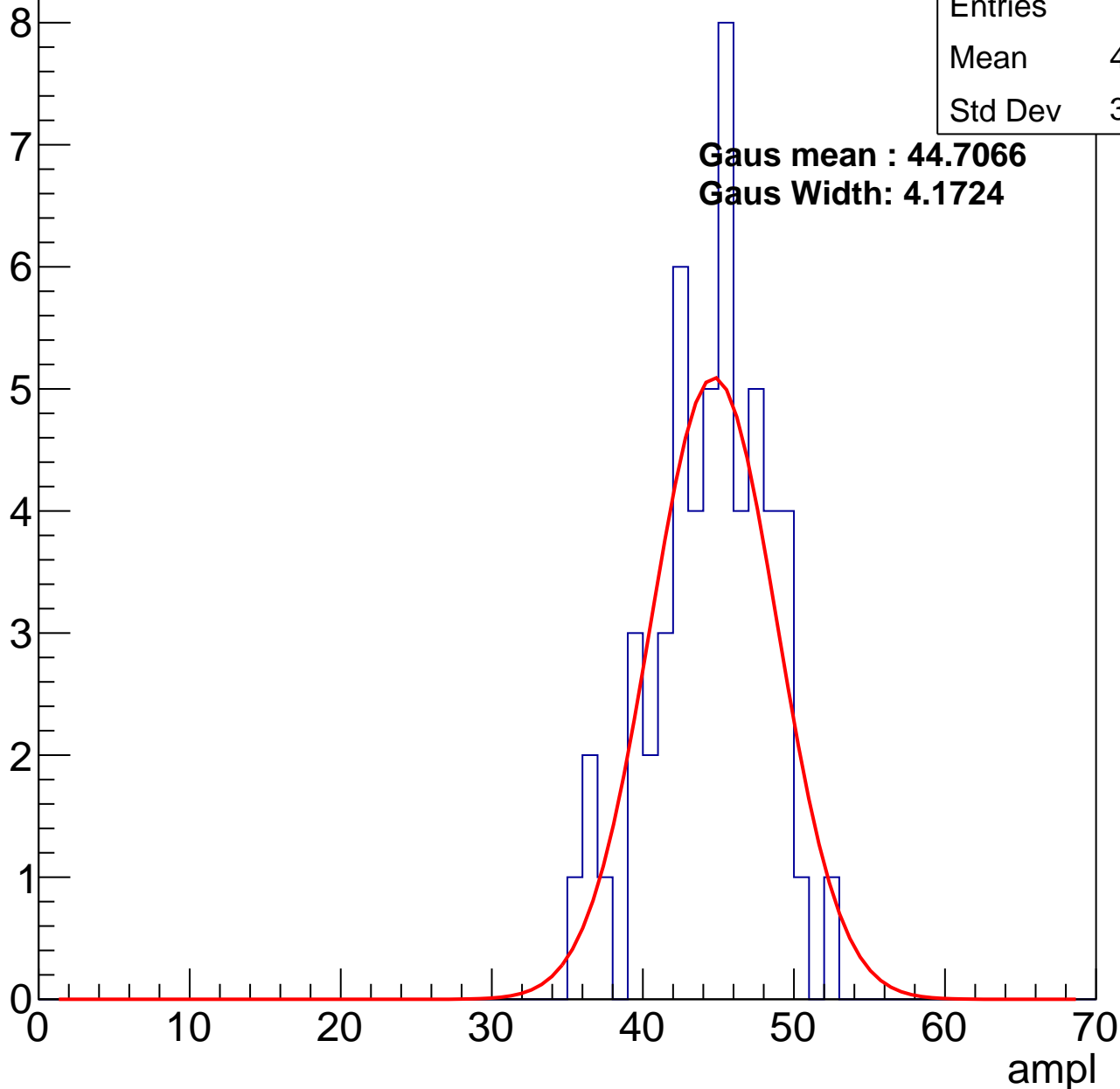
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	44.02
Std Dev	3.734

**Gaus mean : 44.7066**

**Gaus Width: 4.1724**

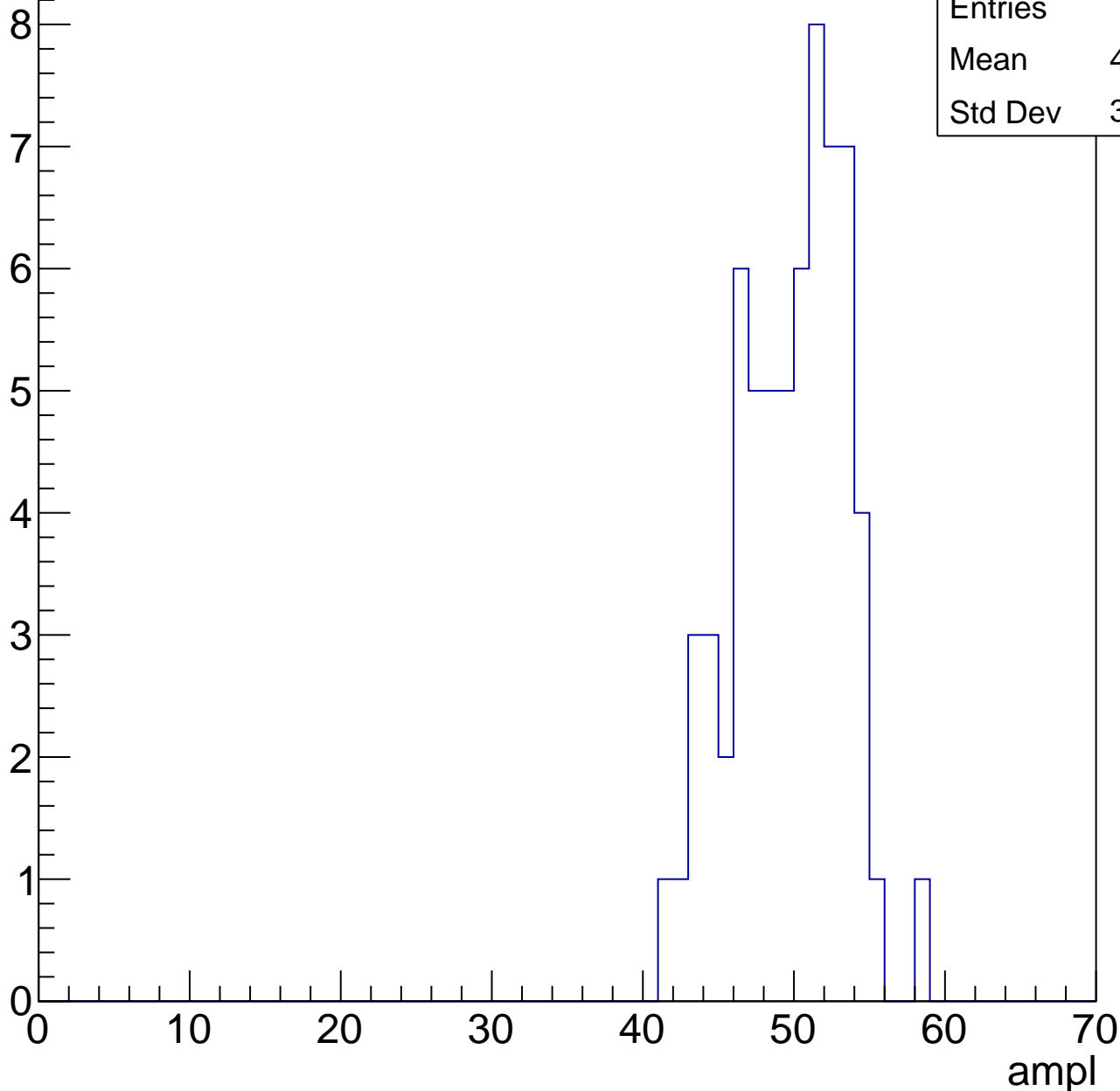


# B0L000S, U26-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

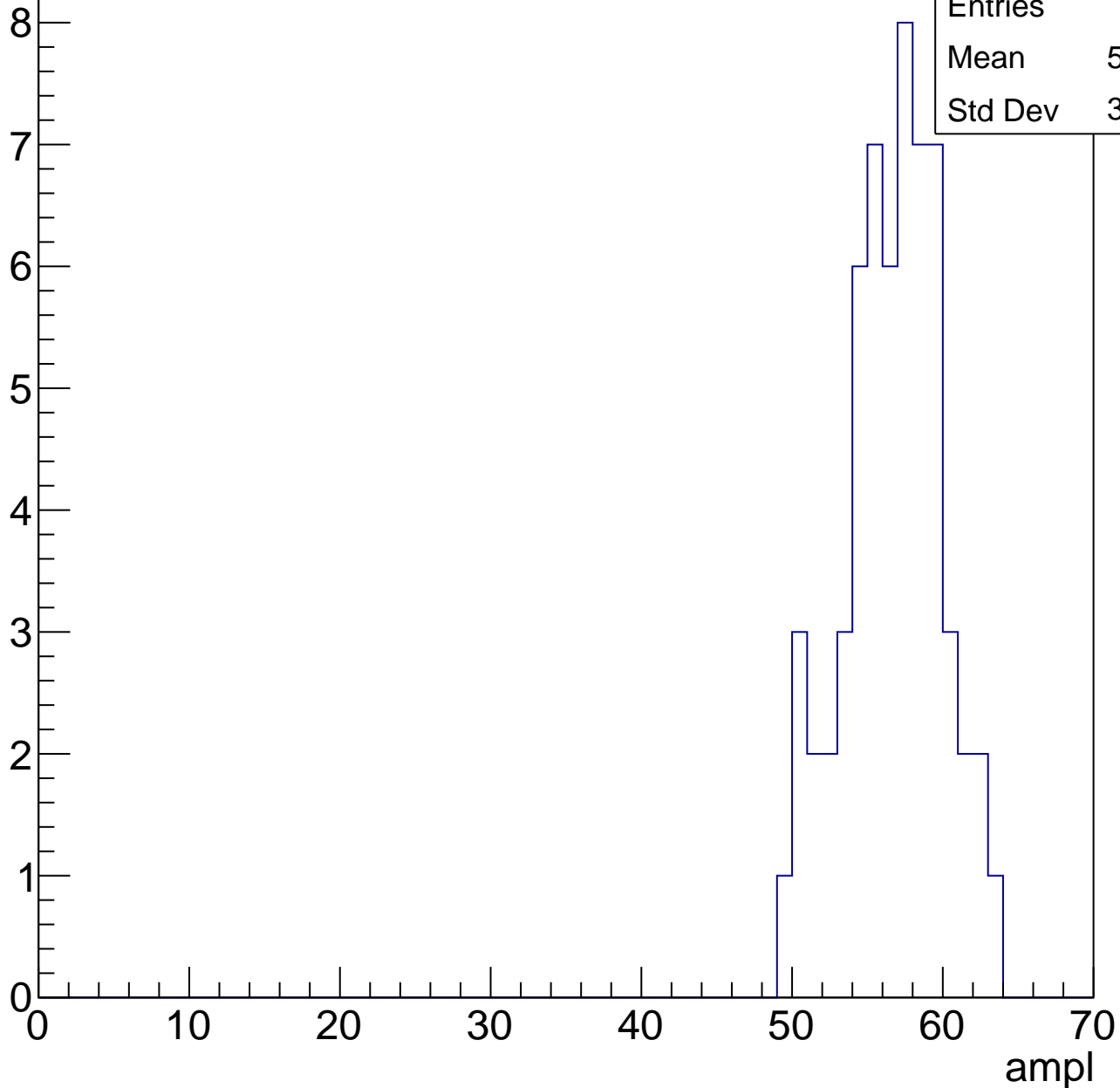
Entries	65
Mean	49.26
Std Dev	3.579



# B0L000S, U26-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



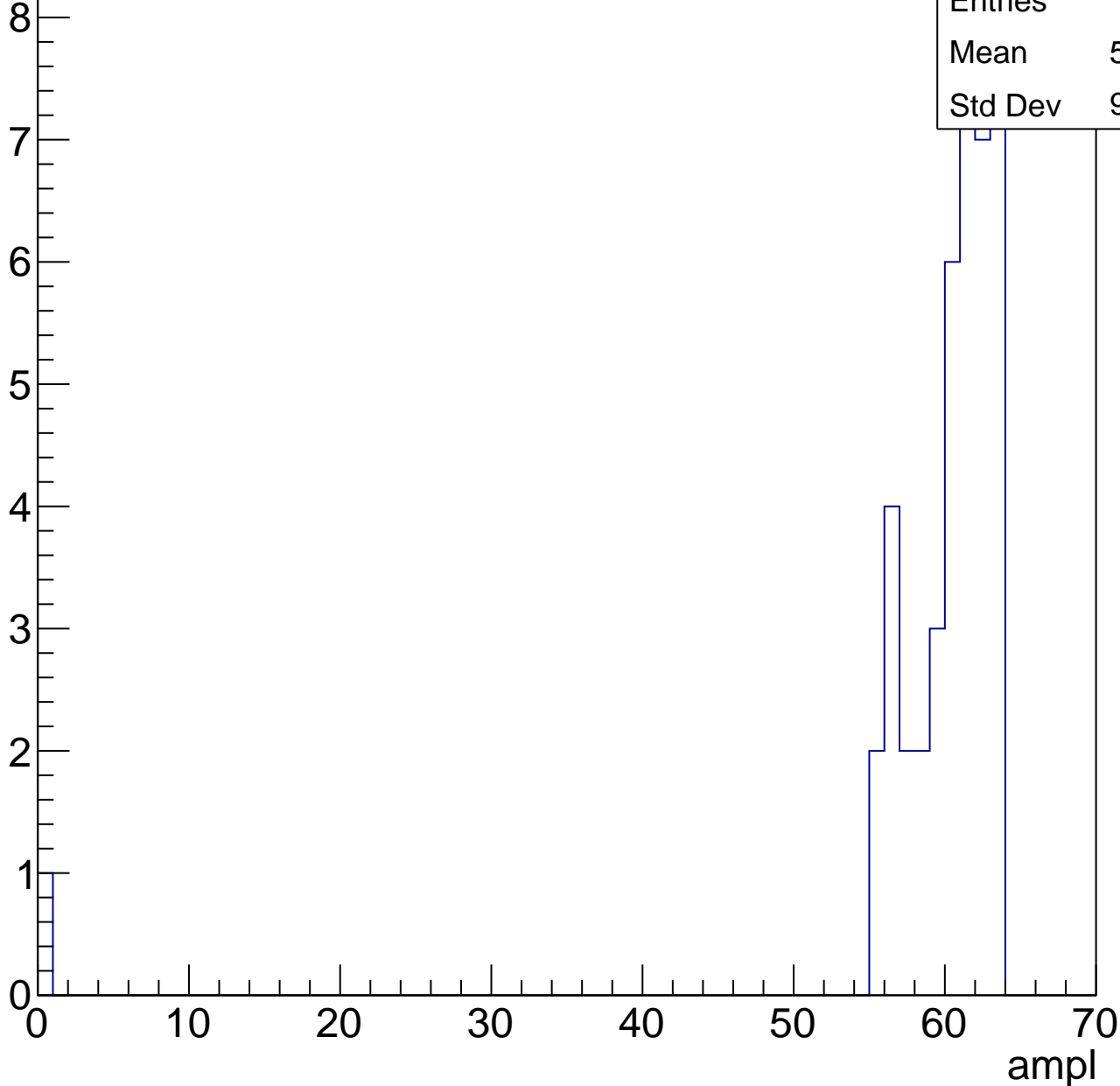
Entries	60
Mean	56.22
Std Dev	3.194

# B0L000S, U26-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

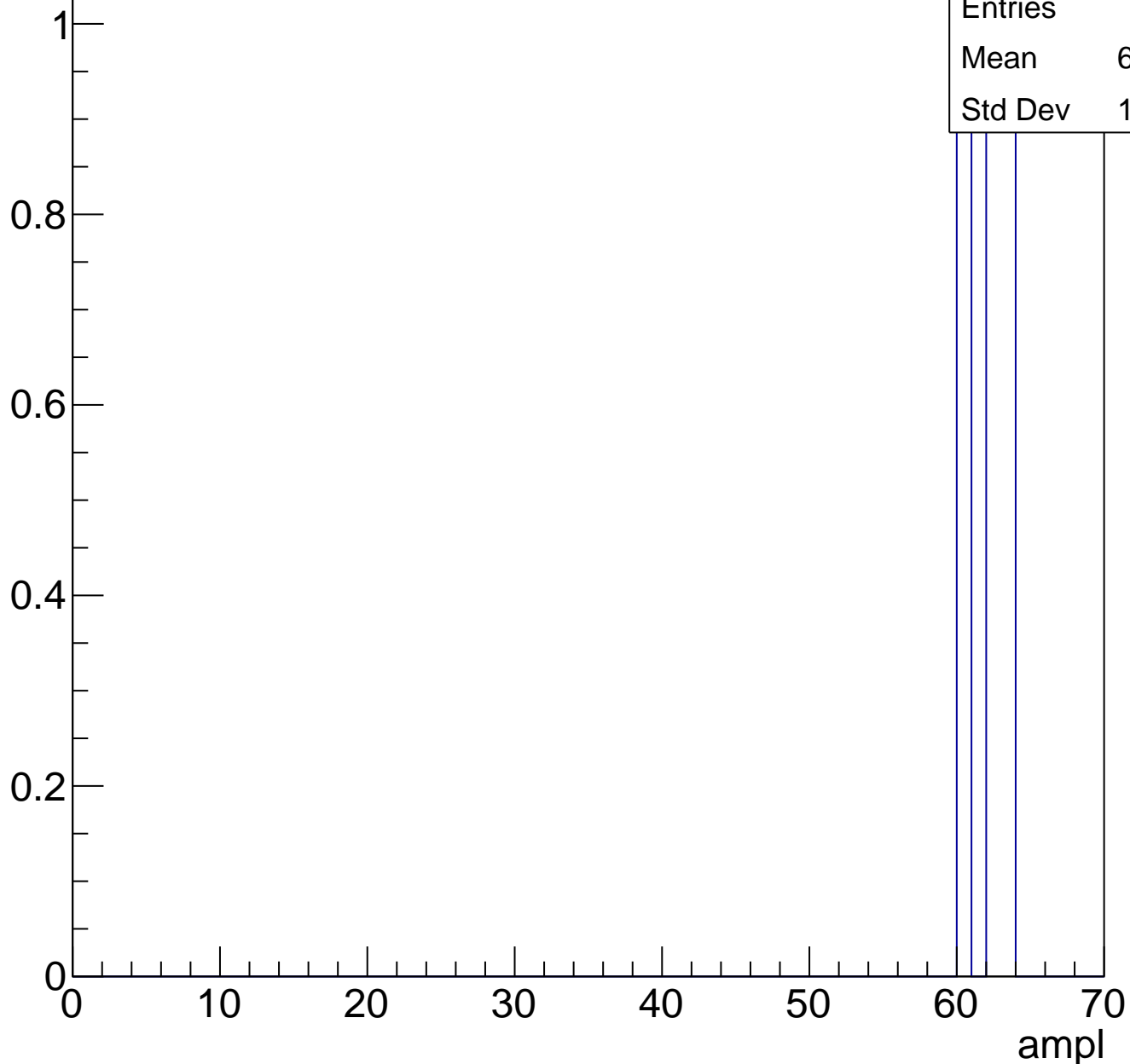
Entries	43
Mean	58.77
Std Dev	9.383



# B0L000S, U26-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch100, adc0

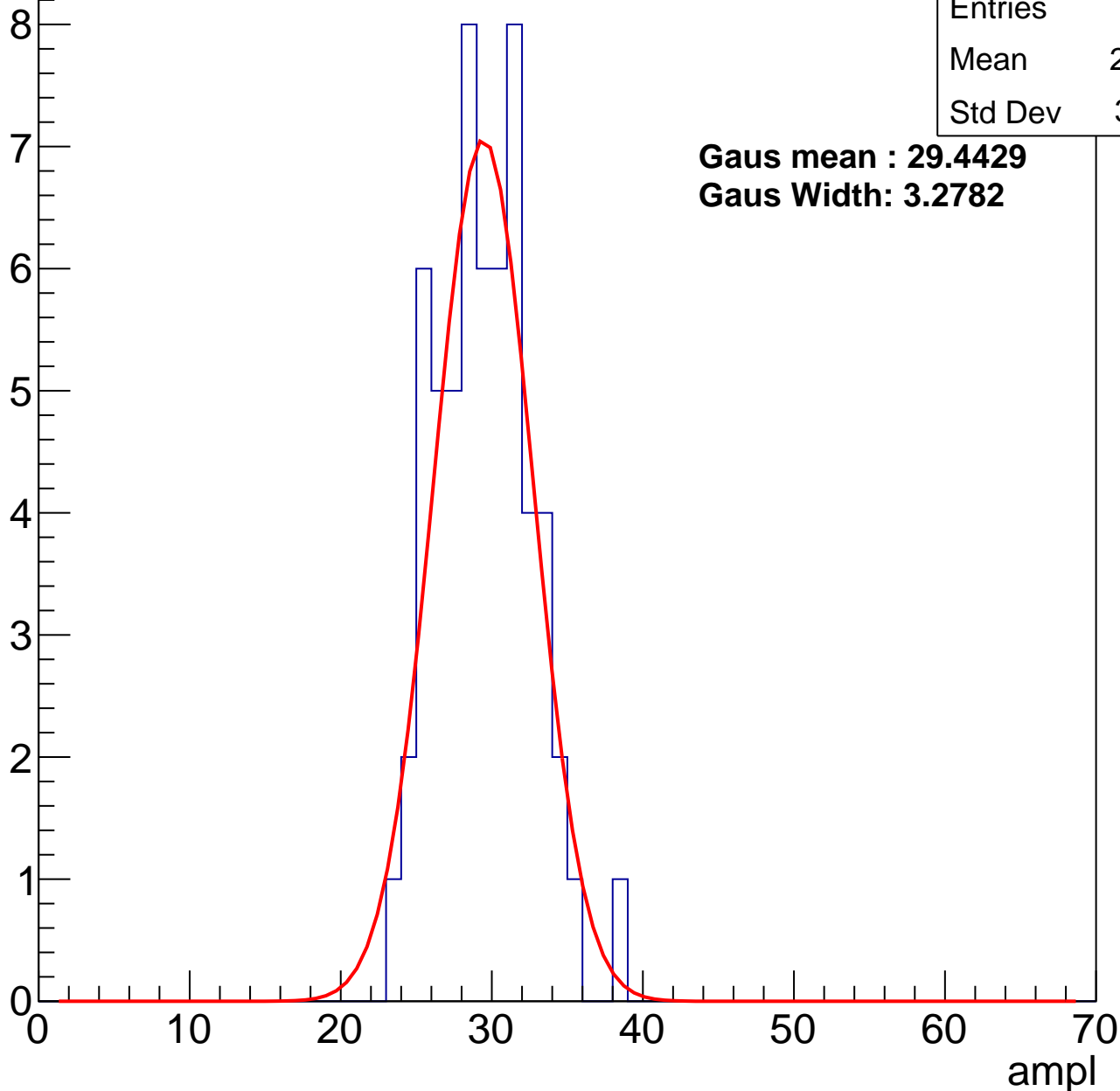
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.03
Std Dev	3.081

**Gaus mean : 29.4429**

**Gaus Width: 3.2782**



# B0L000S, U26-ch100, adc1

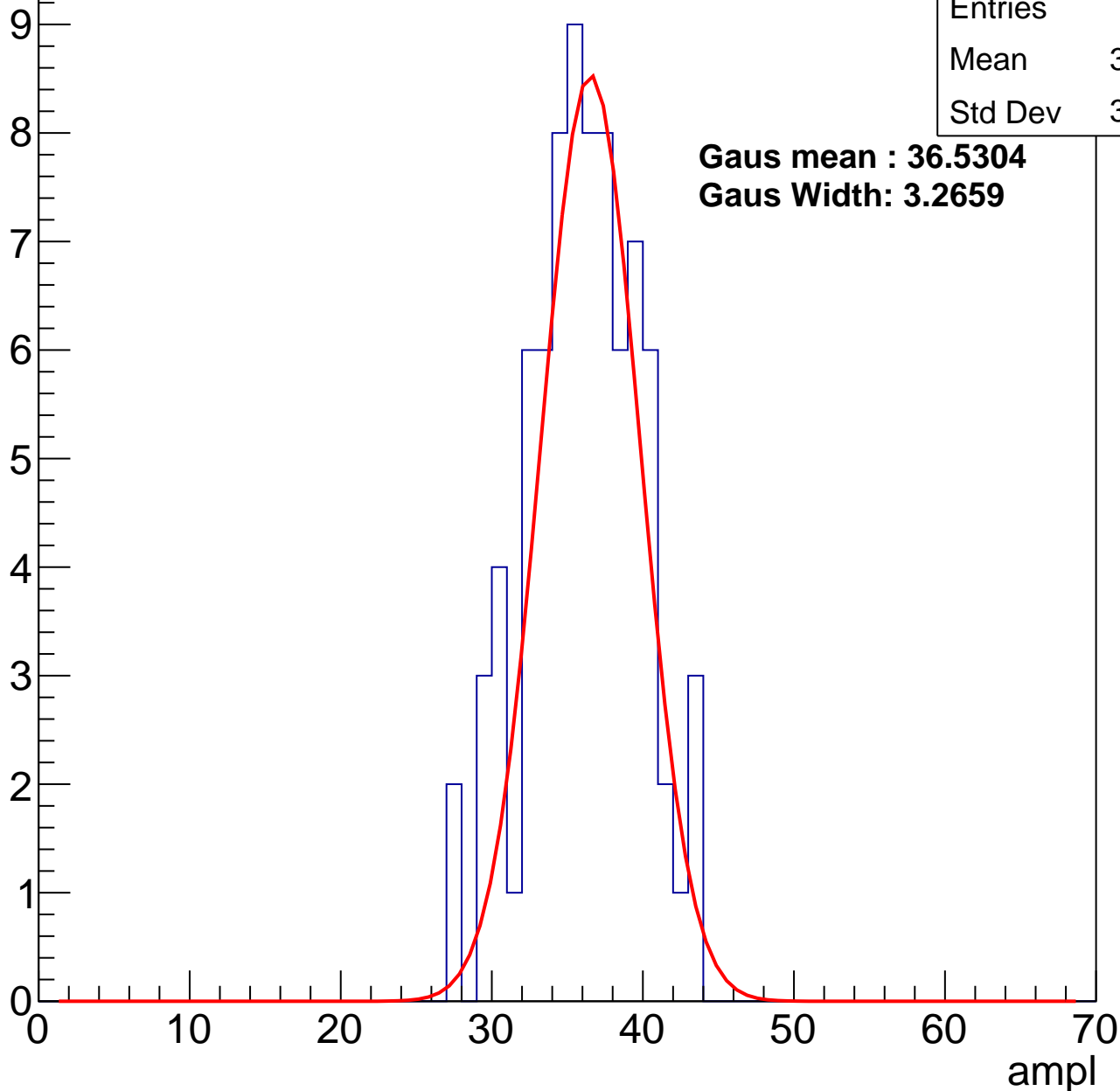
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	35.59
Std Dev	3.673

**Gaus mean : 36.5304**

**Gaus Width: 3.2659**



# B0L000S, U26-ch100, adc2

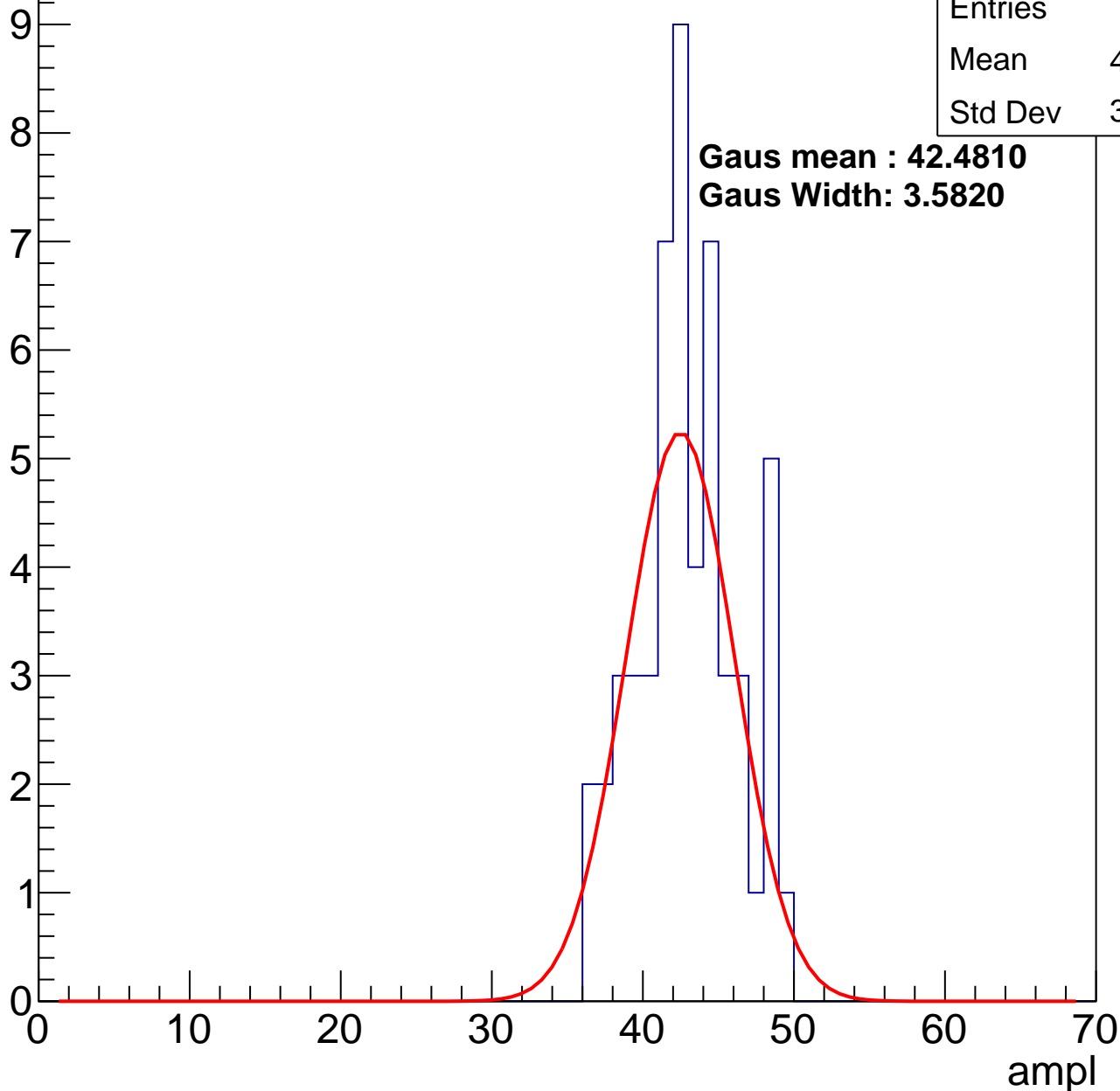
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	42.47
Std Dev	3.266

**Gaus mean : 42.4810**

**Gaus Width: 3.5820**

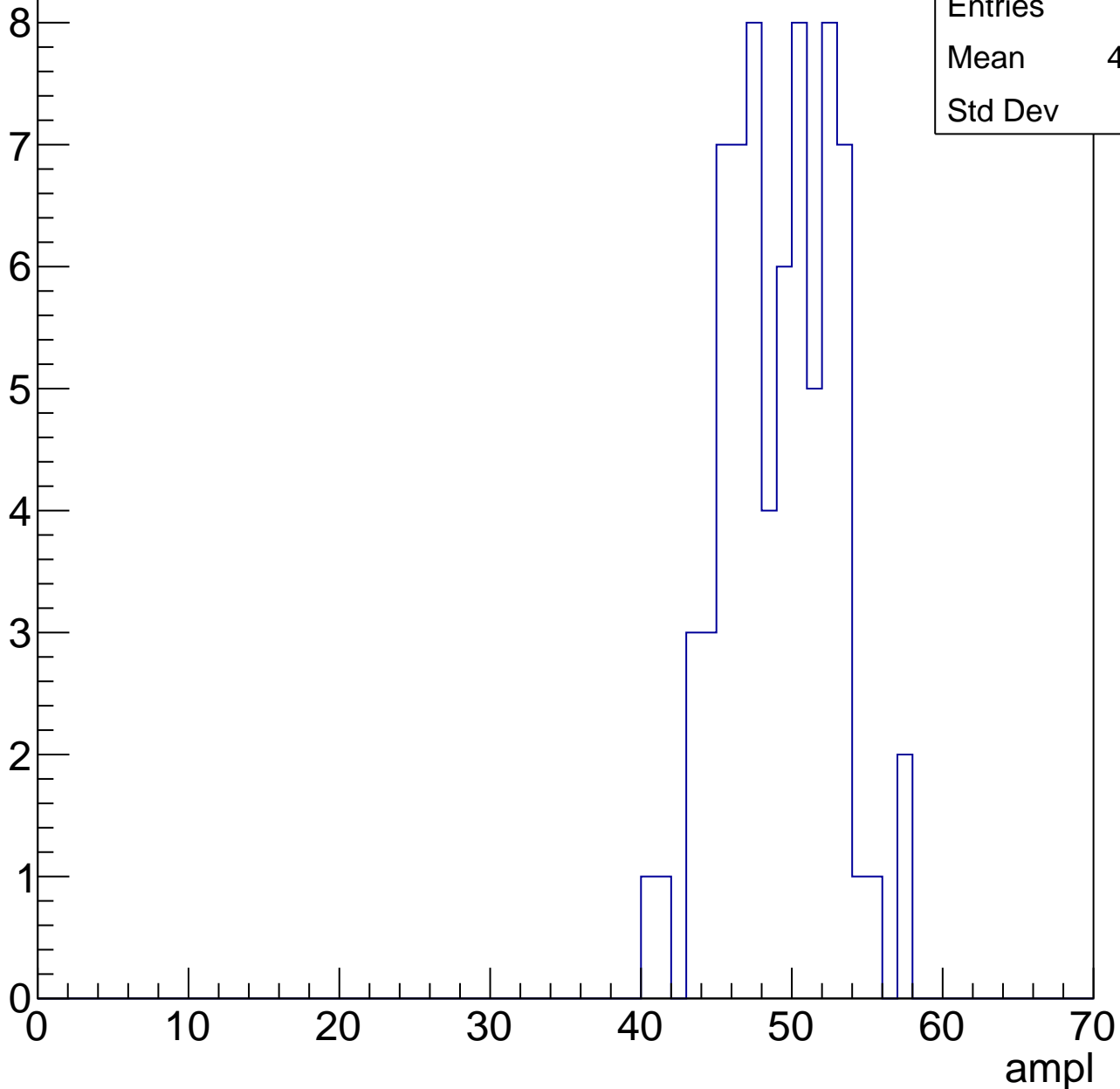


# B0L000S, U26-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	48.69
Std Dev	3.6

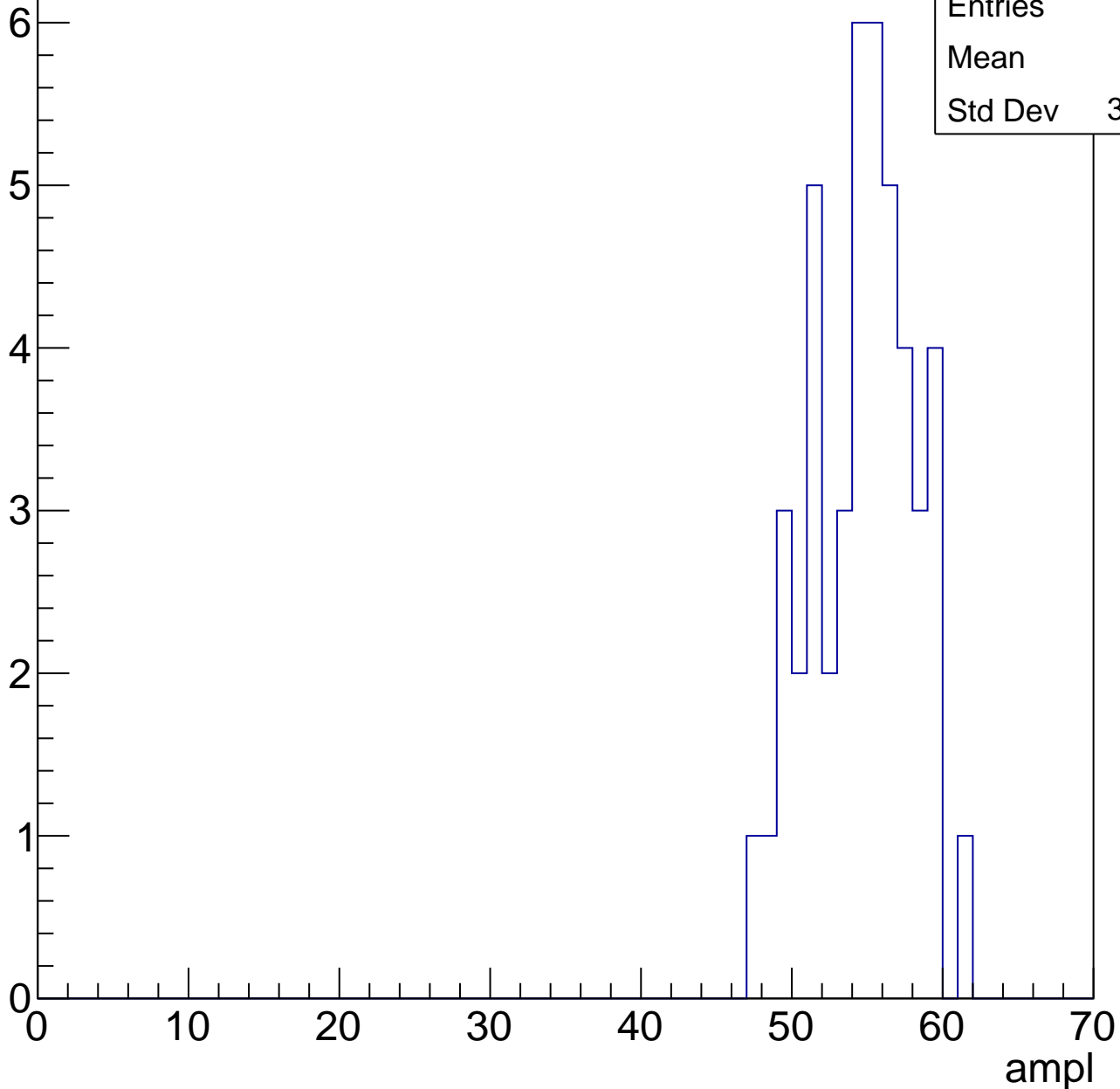


# B0L000S, U26-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	54.2
Std Dev	3.314

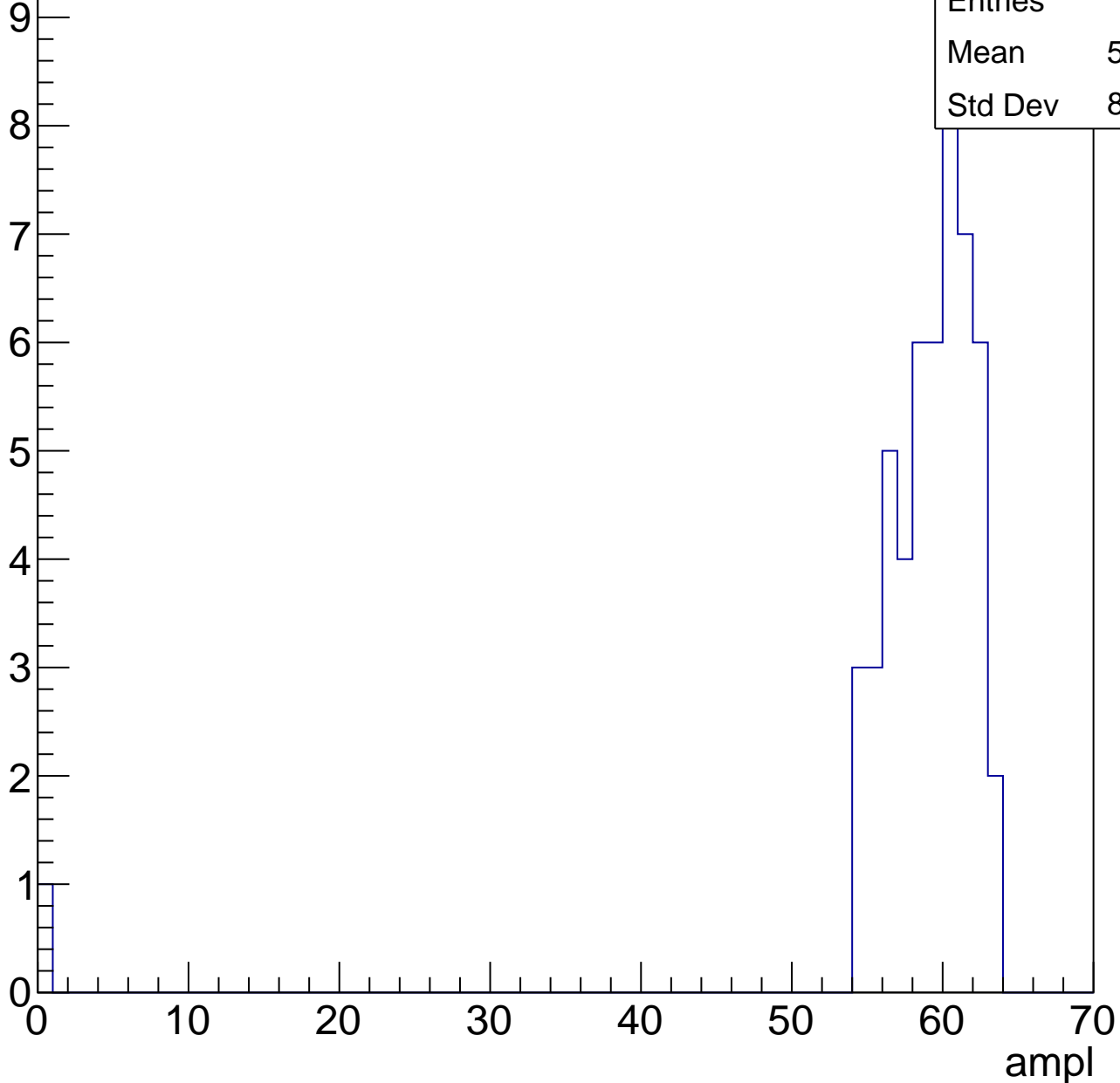


# B0L000S, U26-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	57.73
Std Dev	8.447

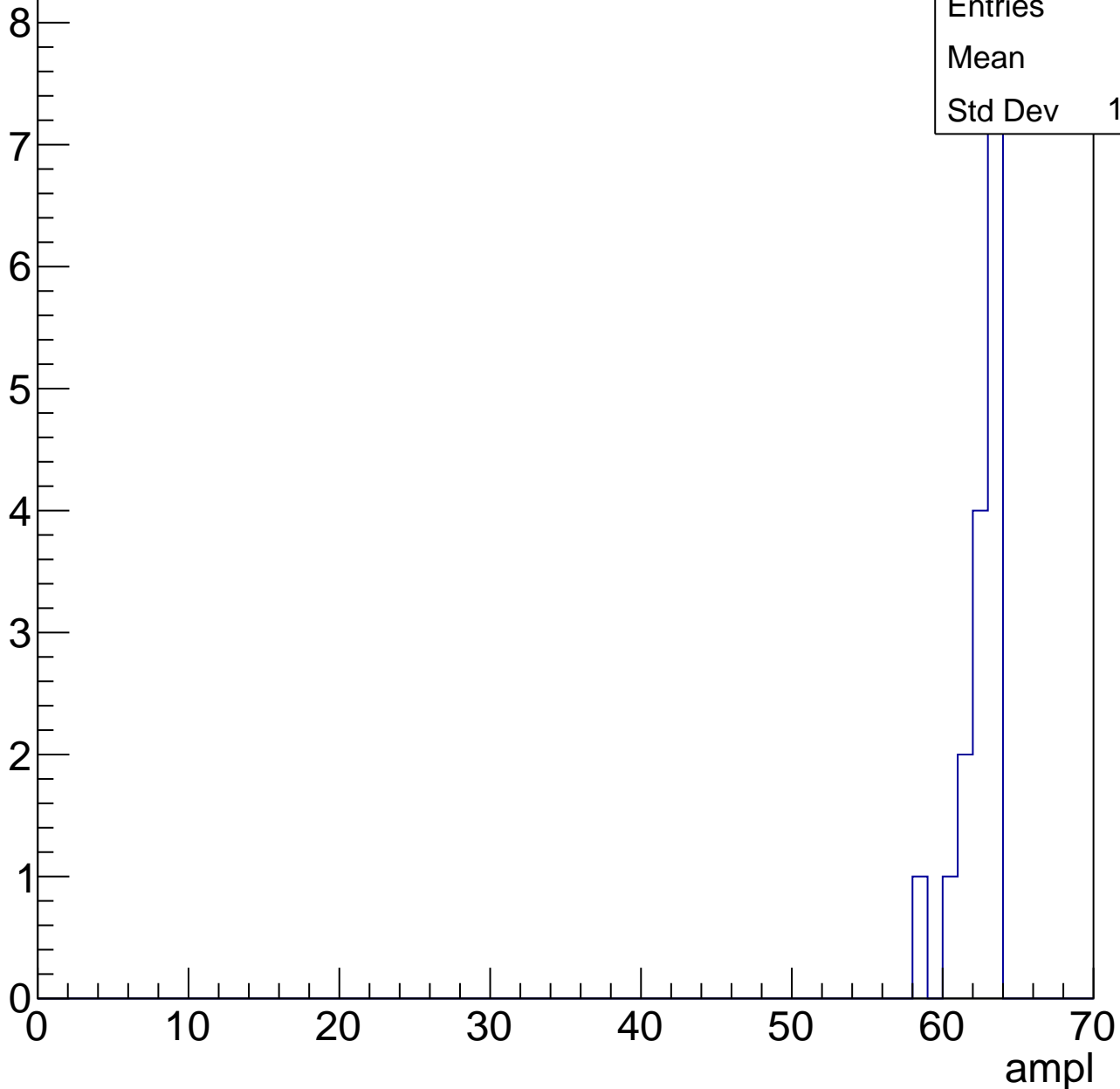


# B0L000S, U26-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	62
Std Dev	1.369





# B0L000S, U26-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch101, adc0

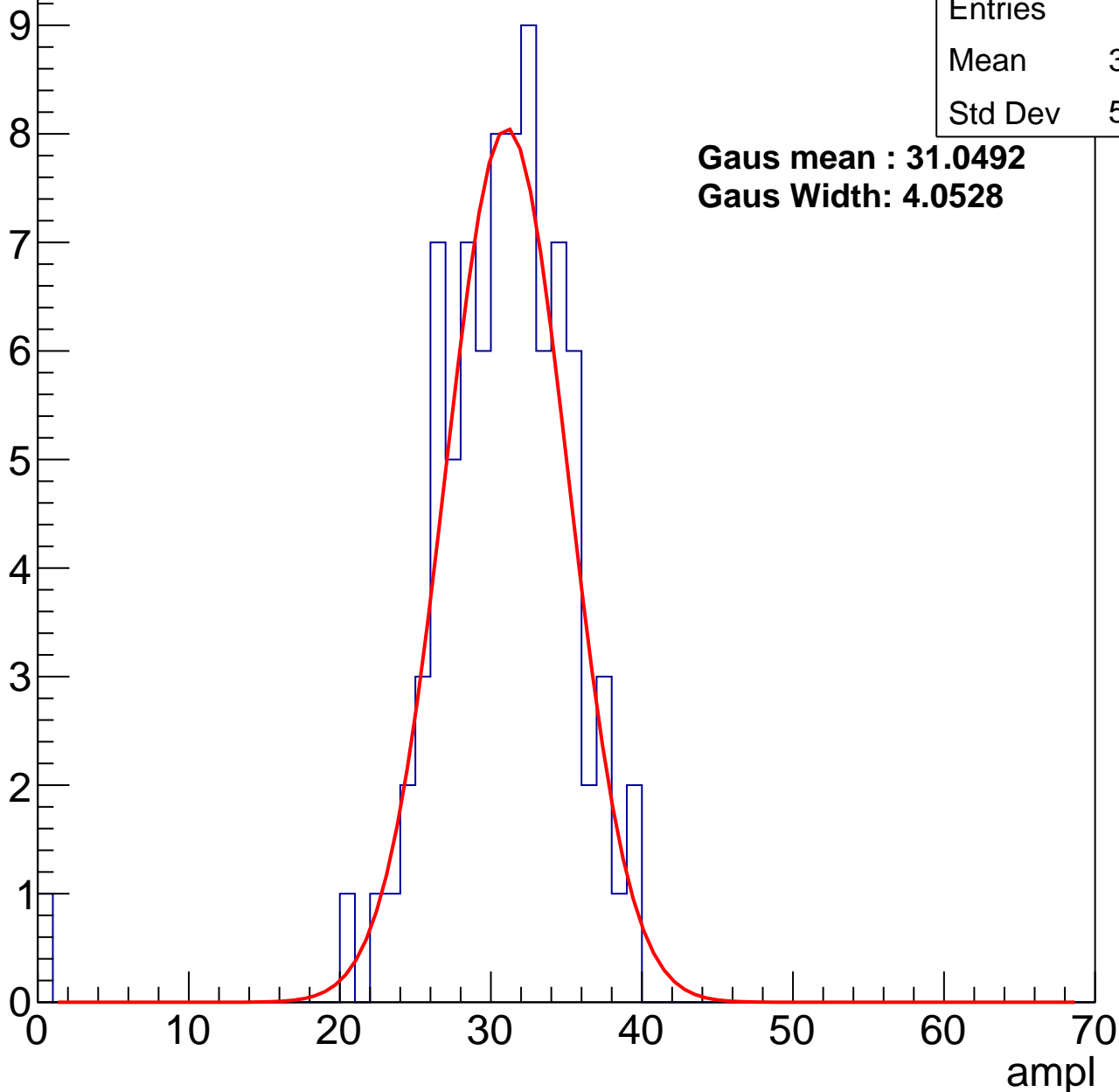
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	30.19
Std Dev	5.118

**Gaus mean : 31.0492**

**Gaus Width: 4.0528**



# B0L000S, U26-ch101, adc1

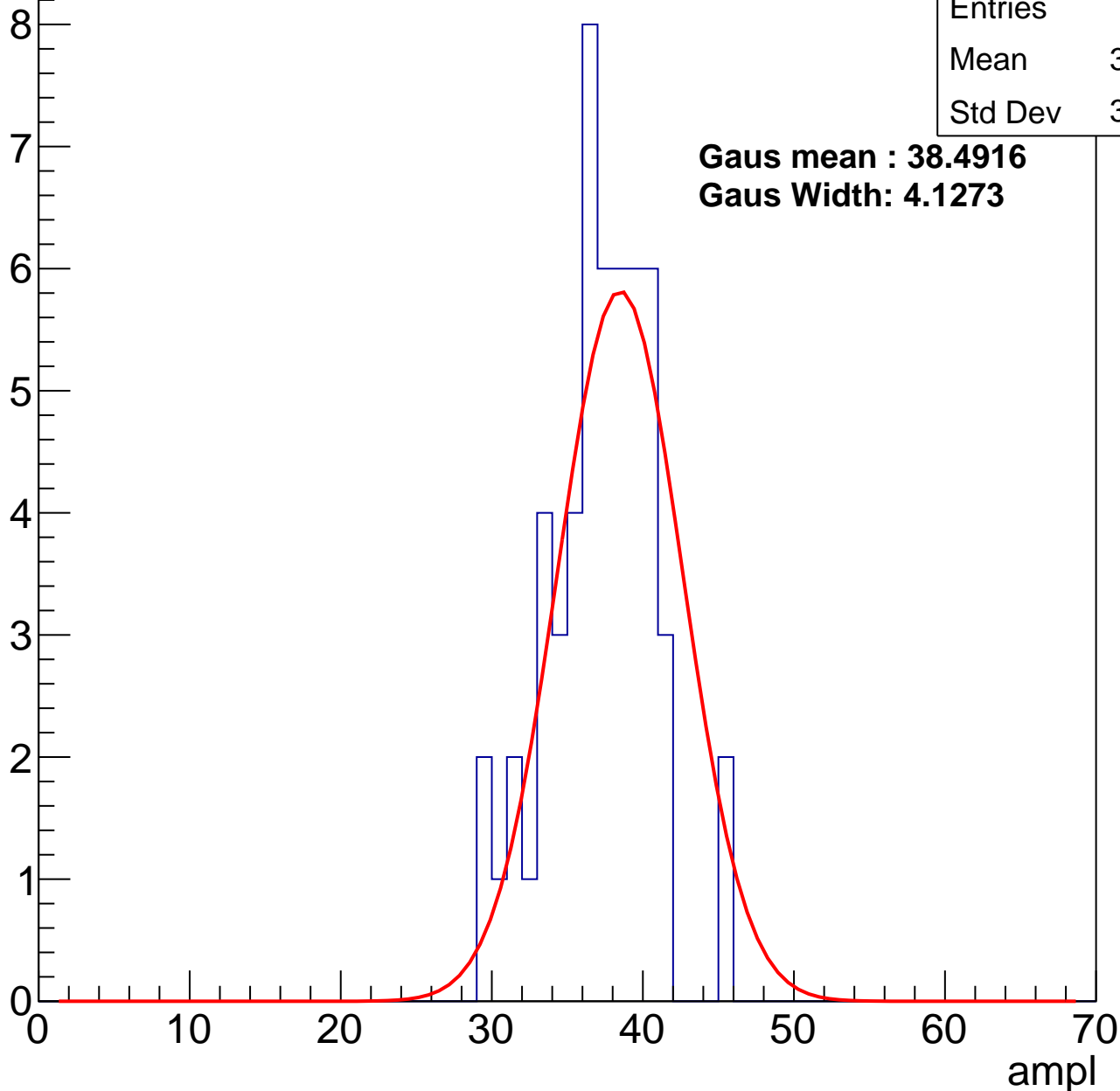
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	36.69
Std Dev	3.452

**Gaus mean : 38.4916**

**Gaus Width: 4.1273**



# B0L000S, U26-ch101, adc2

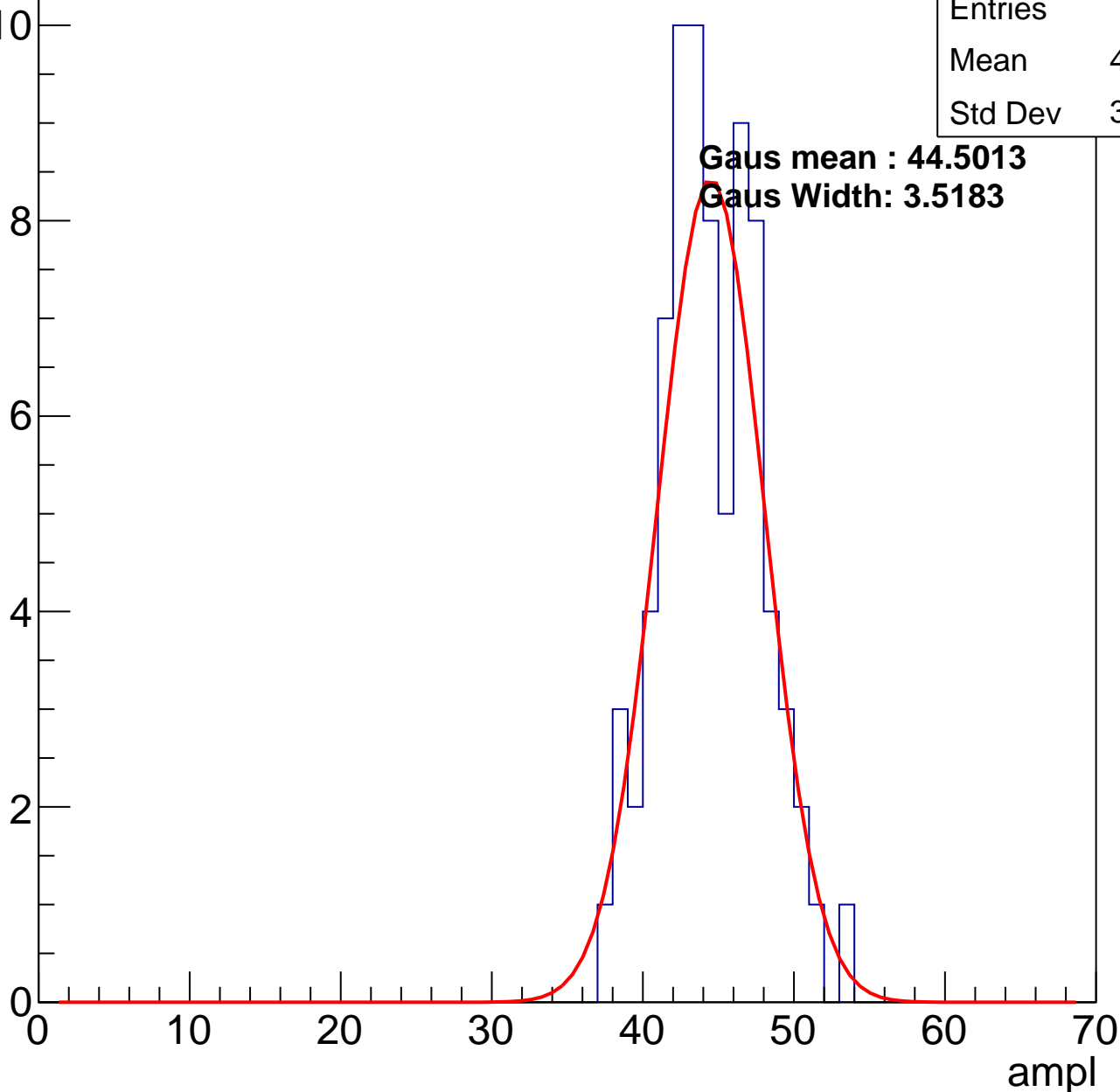
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	44.05
Std Dev	3.277

**Gaus mean : 44.5013**

**Gaus Width: 3.5183**

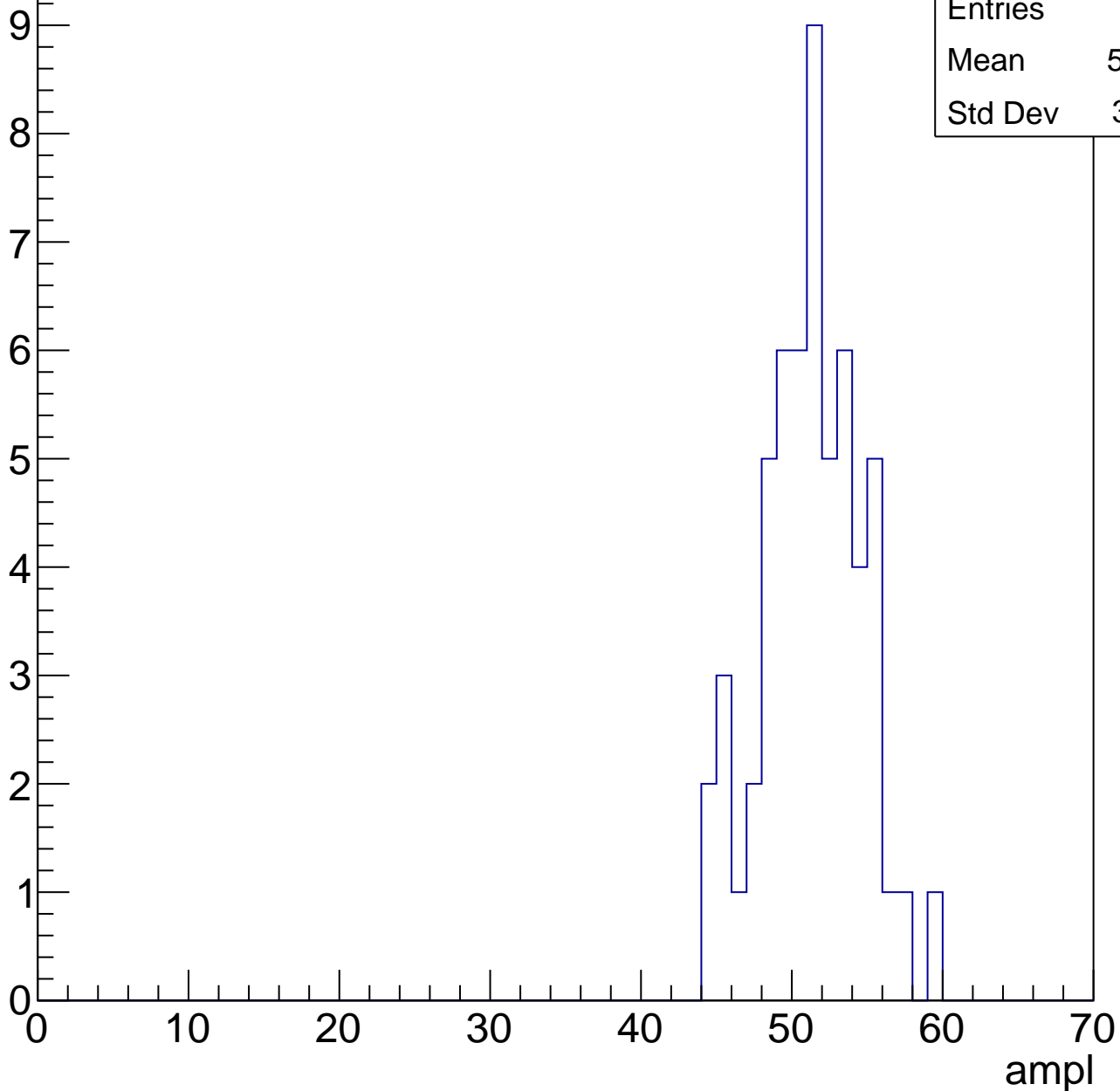


# B0L000S, U26-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	50.82
Std Dev	3.261

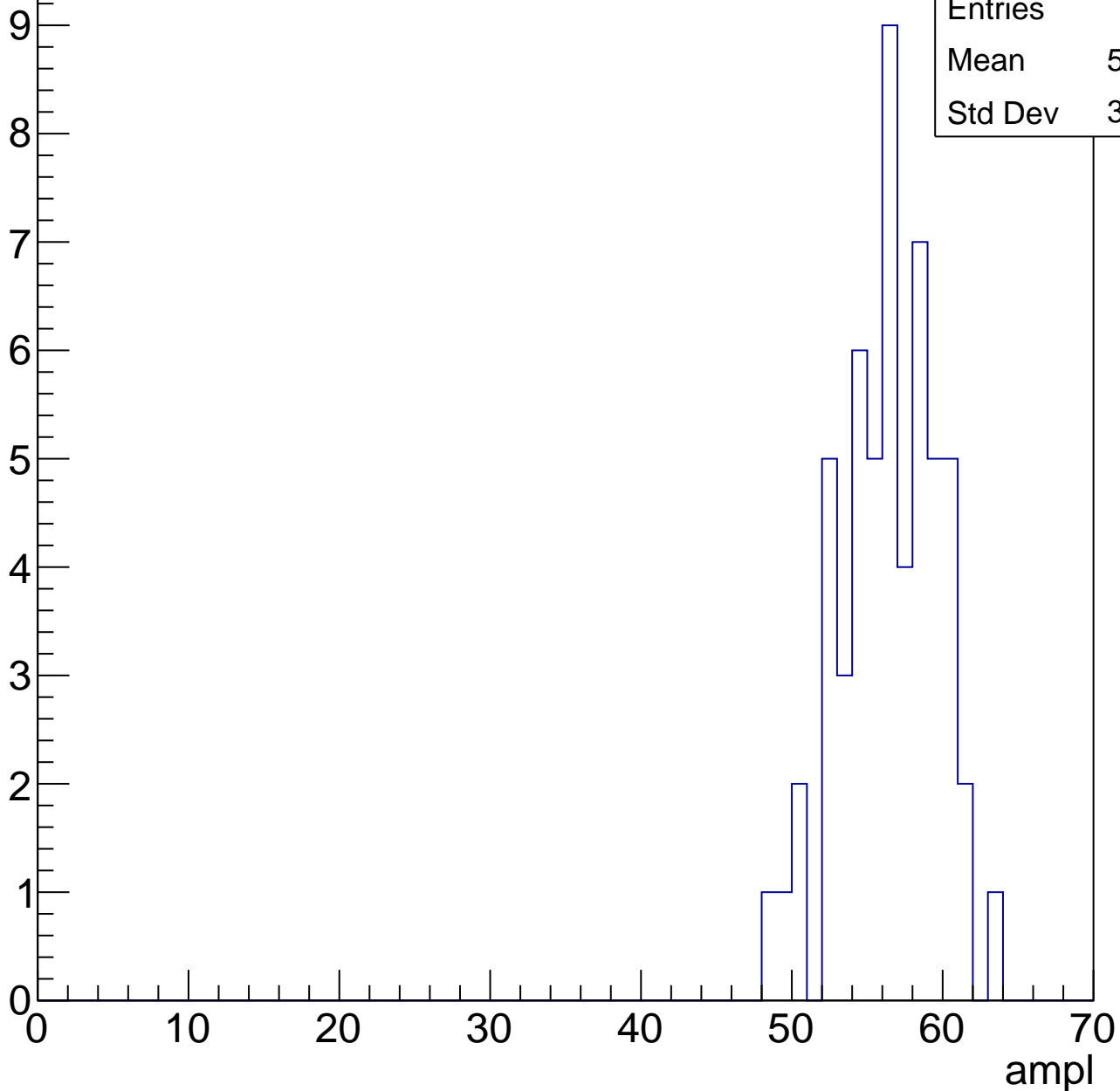


# B0L000S, U26-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.95
Std Dev	3.209

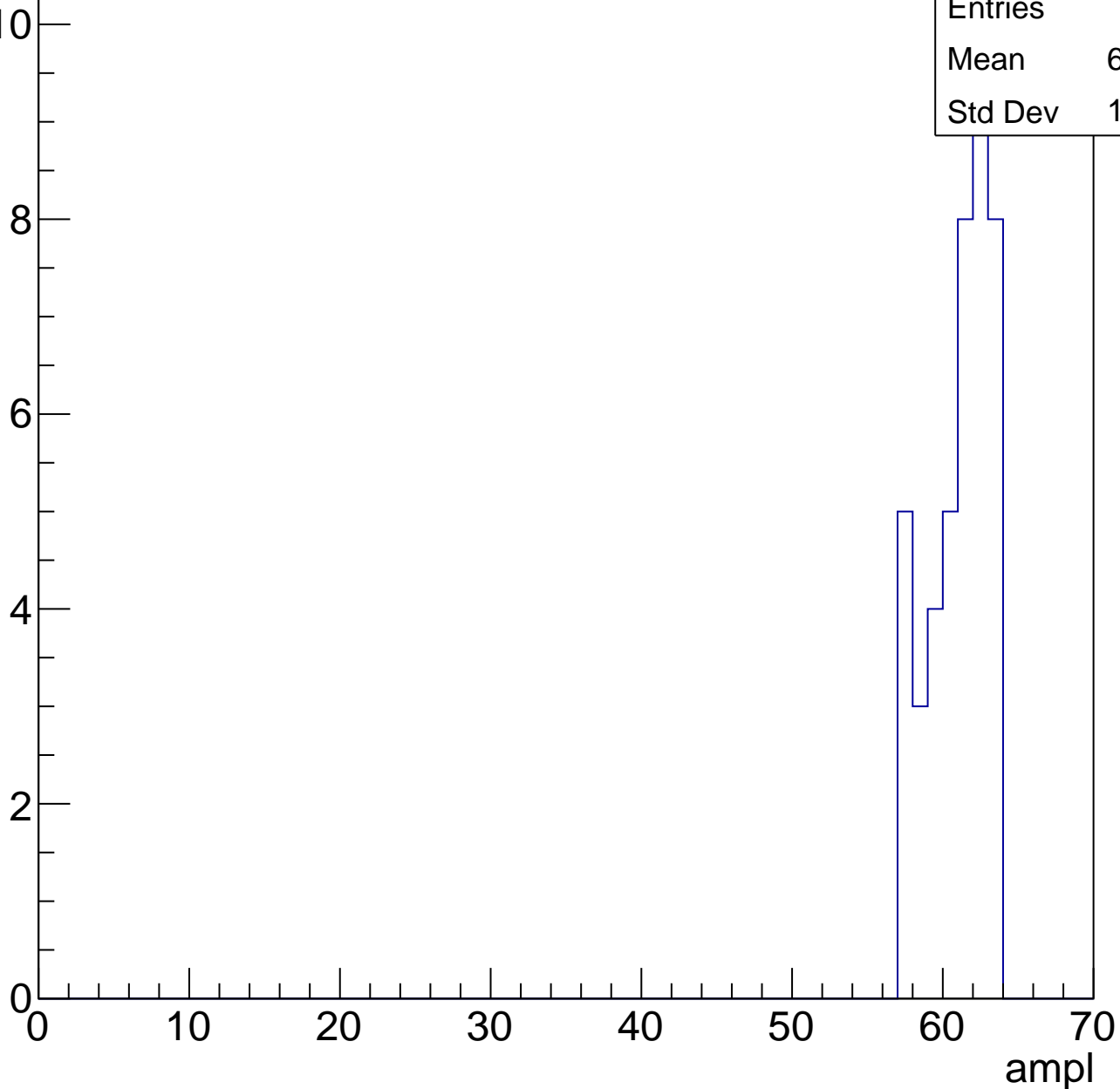


# B0L000S, U26-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

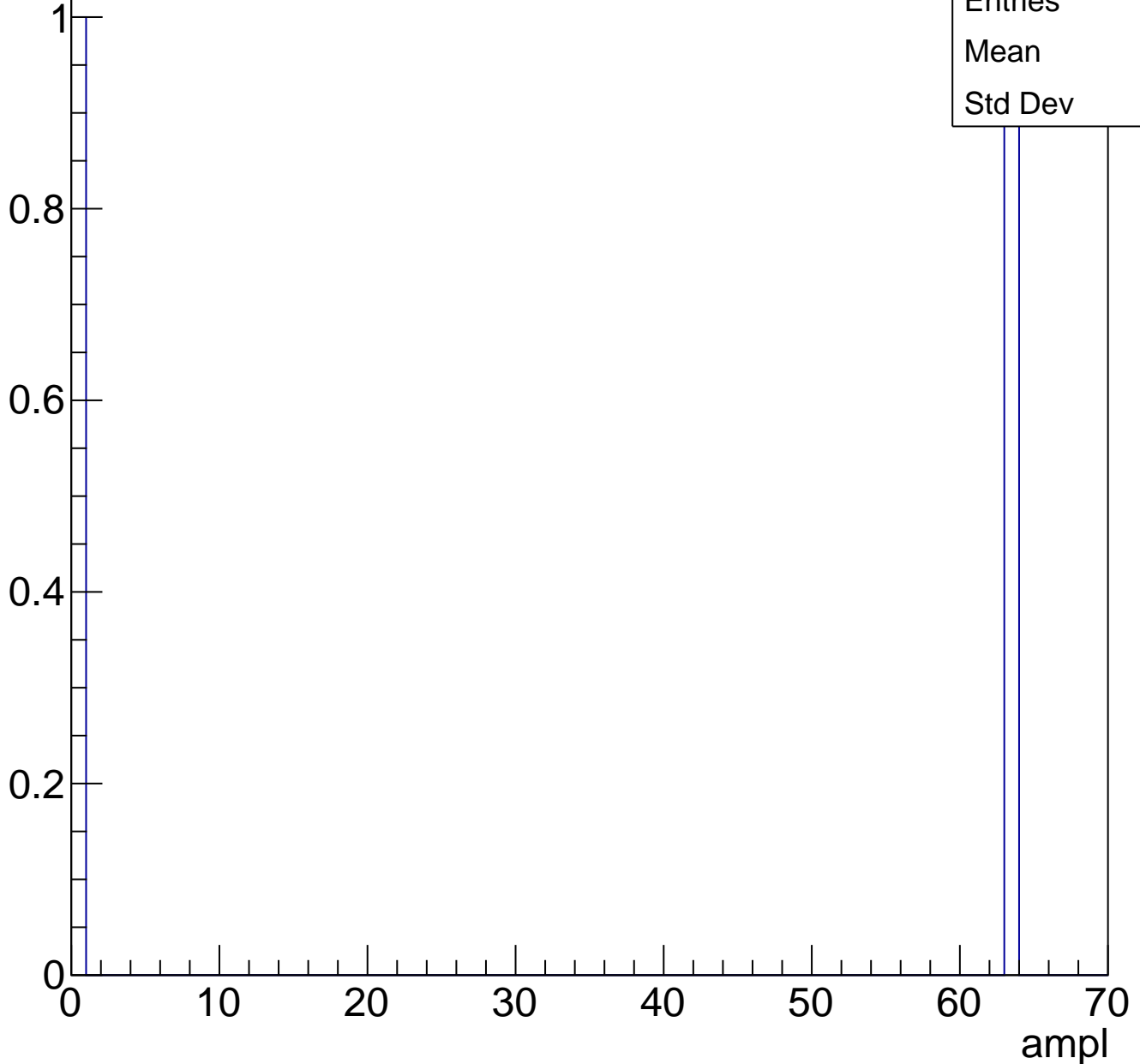
Entries	43
Mean	60.63
Std Dev	1.953



# B0L000S, U26-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

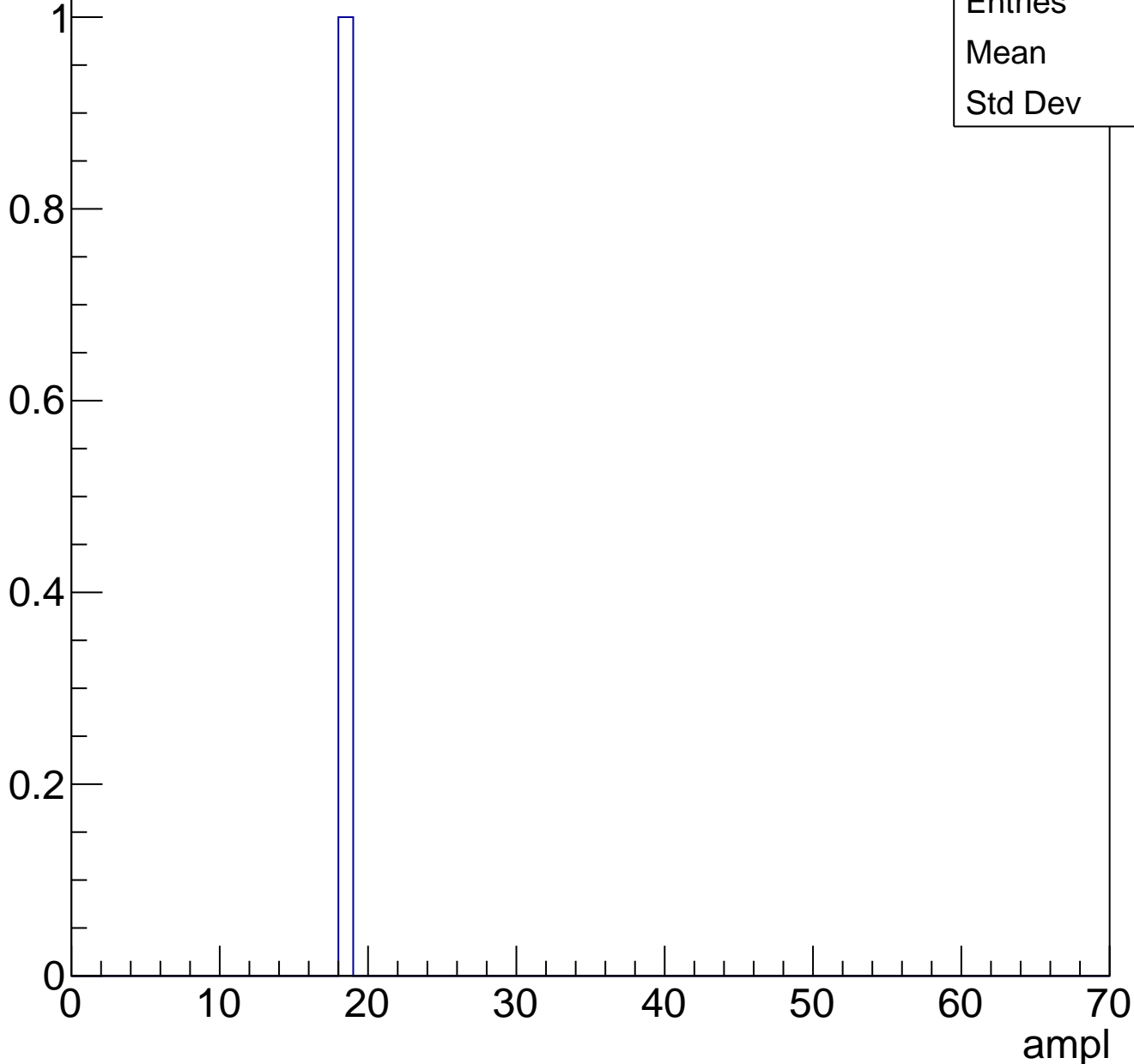




# B0L000S, U26-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	91
Mean	30.11
Std Dev	5.779

**Gaus mean : 30.7511**

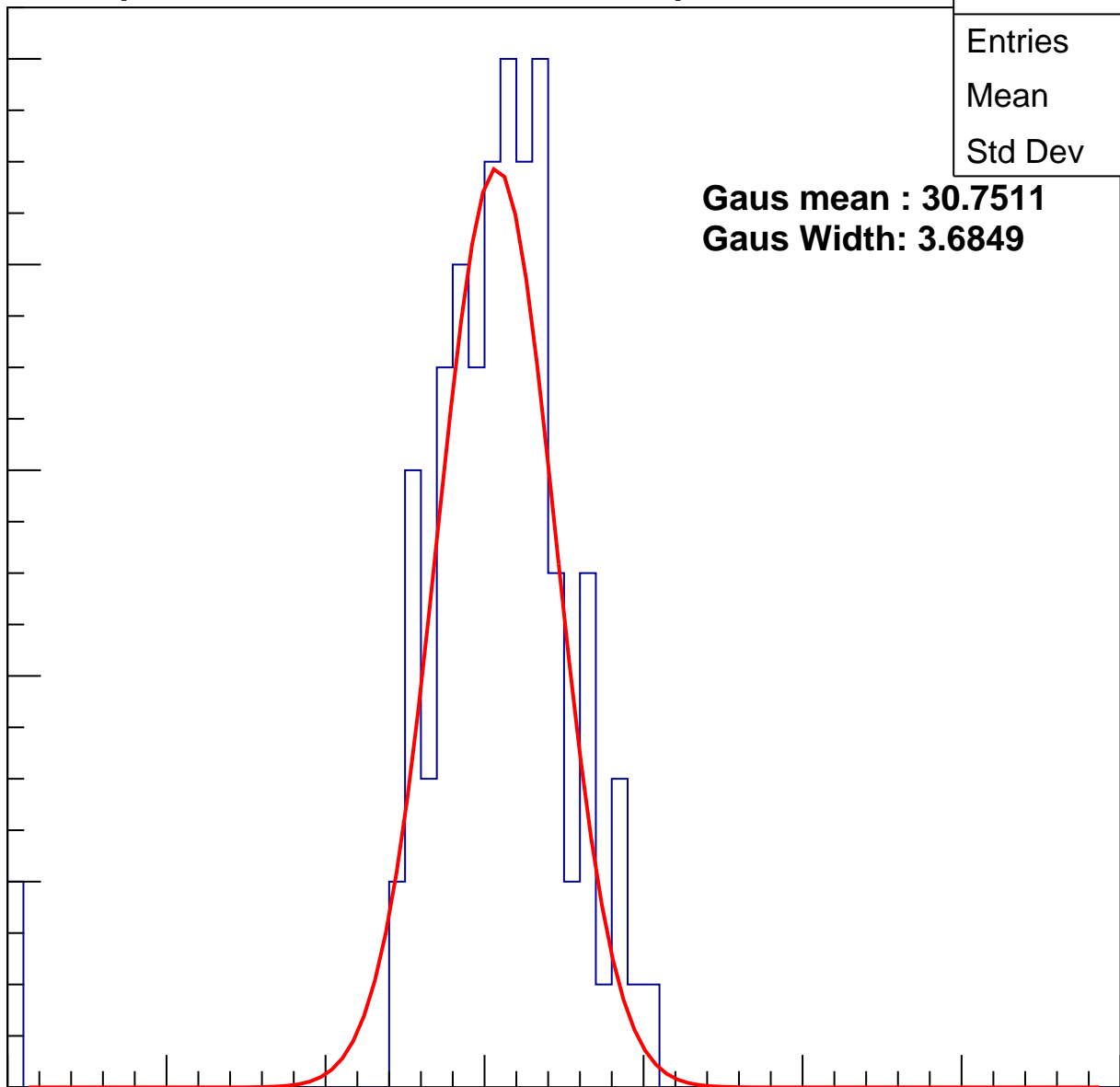
**Gaus Width: 3.6849**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch102, adc1

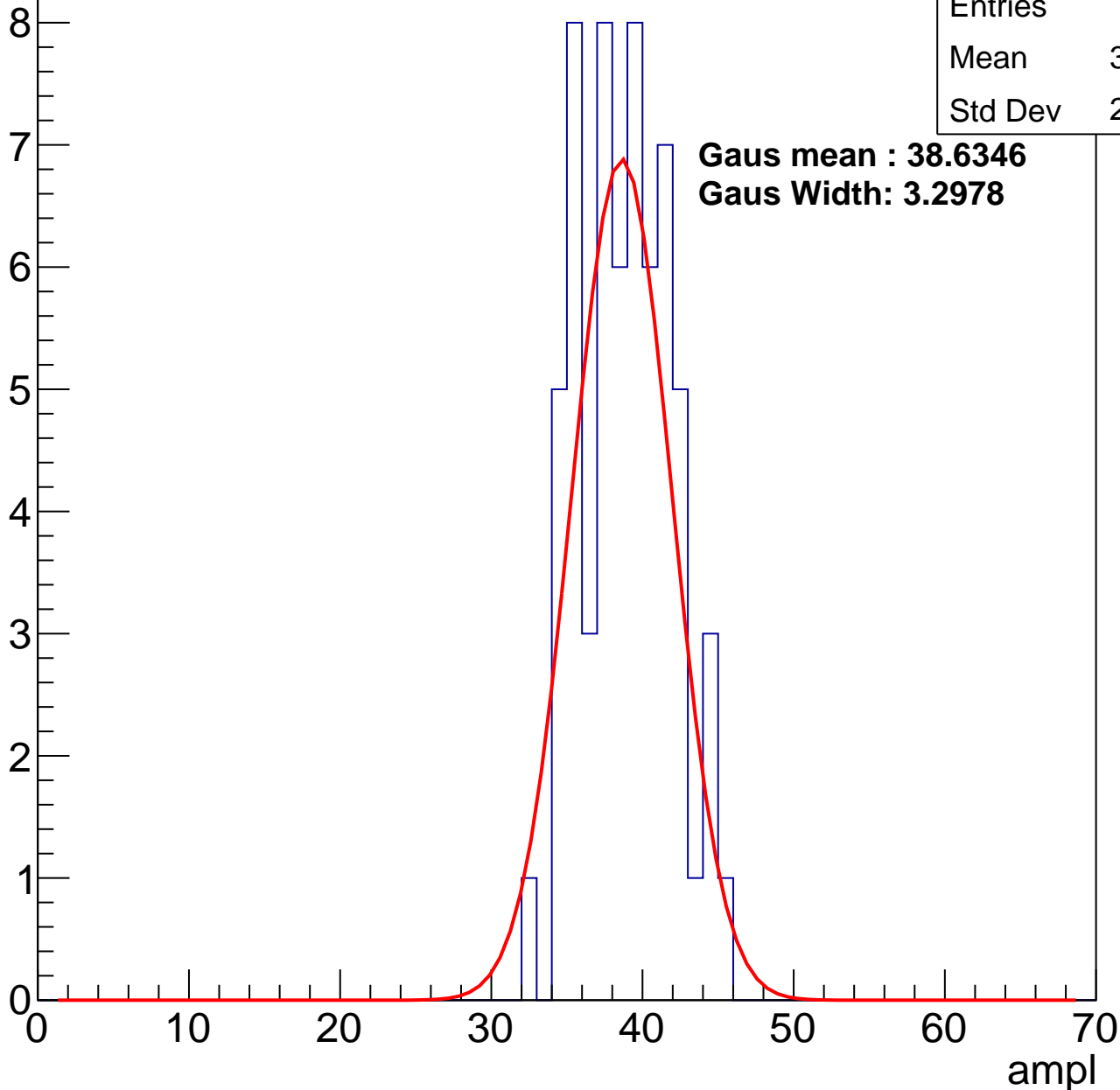
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.44
Std Dev	2.987

**Gaus mean : 38.6346**

**Gaus Width: 3.2978**



# B0L000S, U26-ch102, adc2

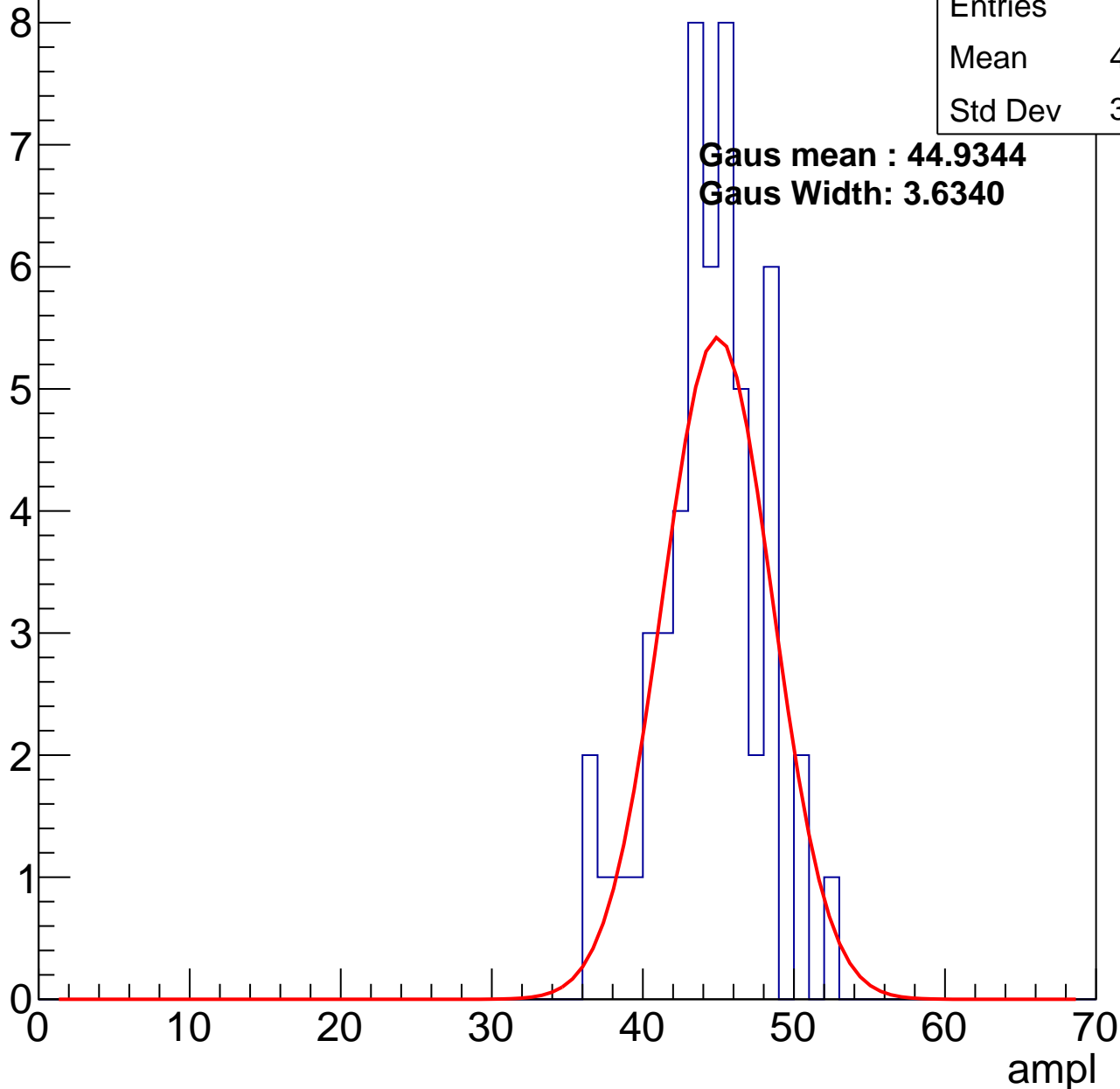
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.94
Std Dev	3.406

**Gaus mean : 44.9344**

**Gaus Width: 3.6340**

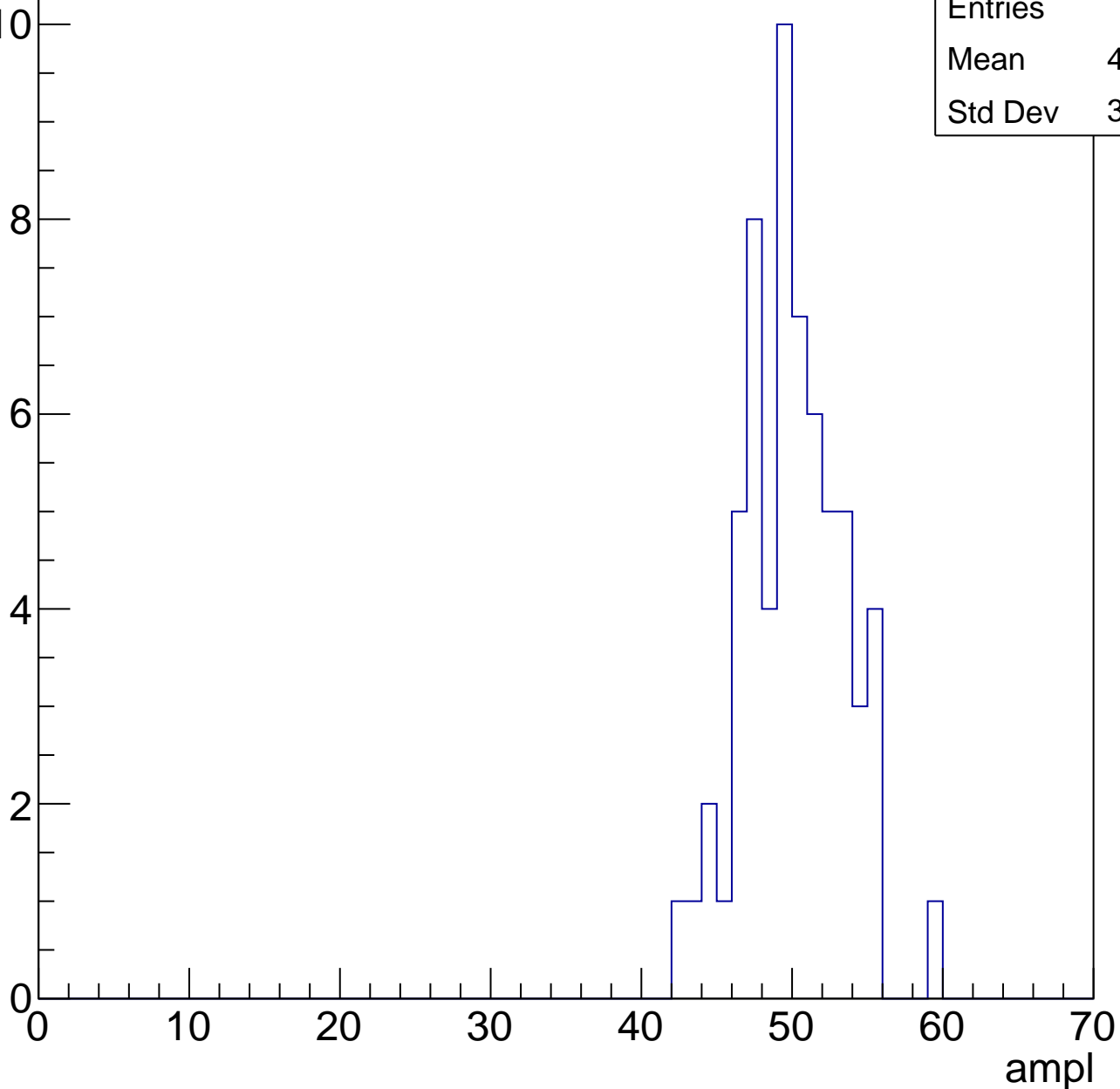


# B0L000S, U26-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

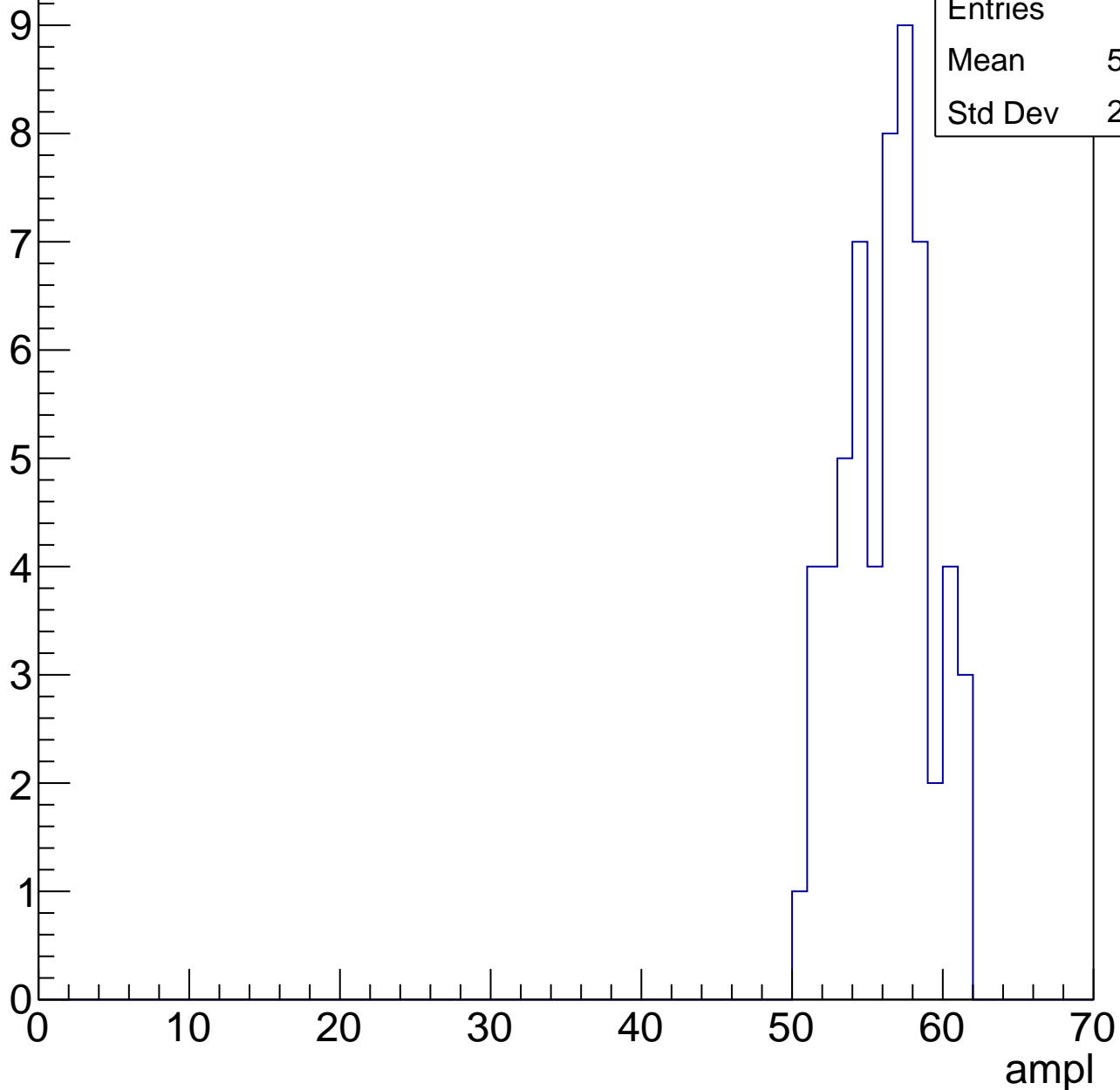
Entries	63
Mean	49.65
Std Dev	3.286



# B0L000S, U26-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	58
Mean	55.74
Std Dev	2.838

# B0L000S, U26-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	45
Mean	59.11
Std Dev	9.185

Entry

10

8

6

4

2

0

0

10

20

30

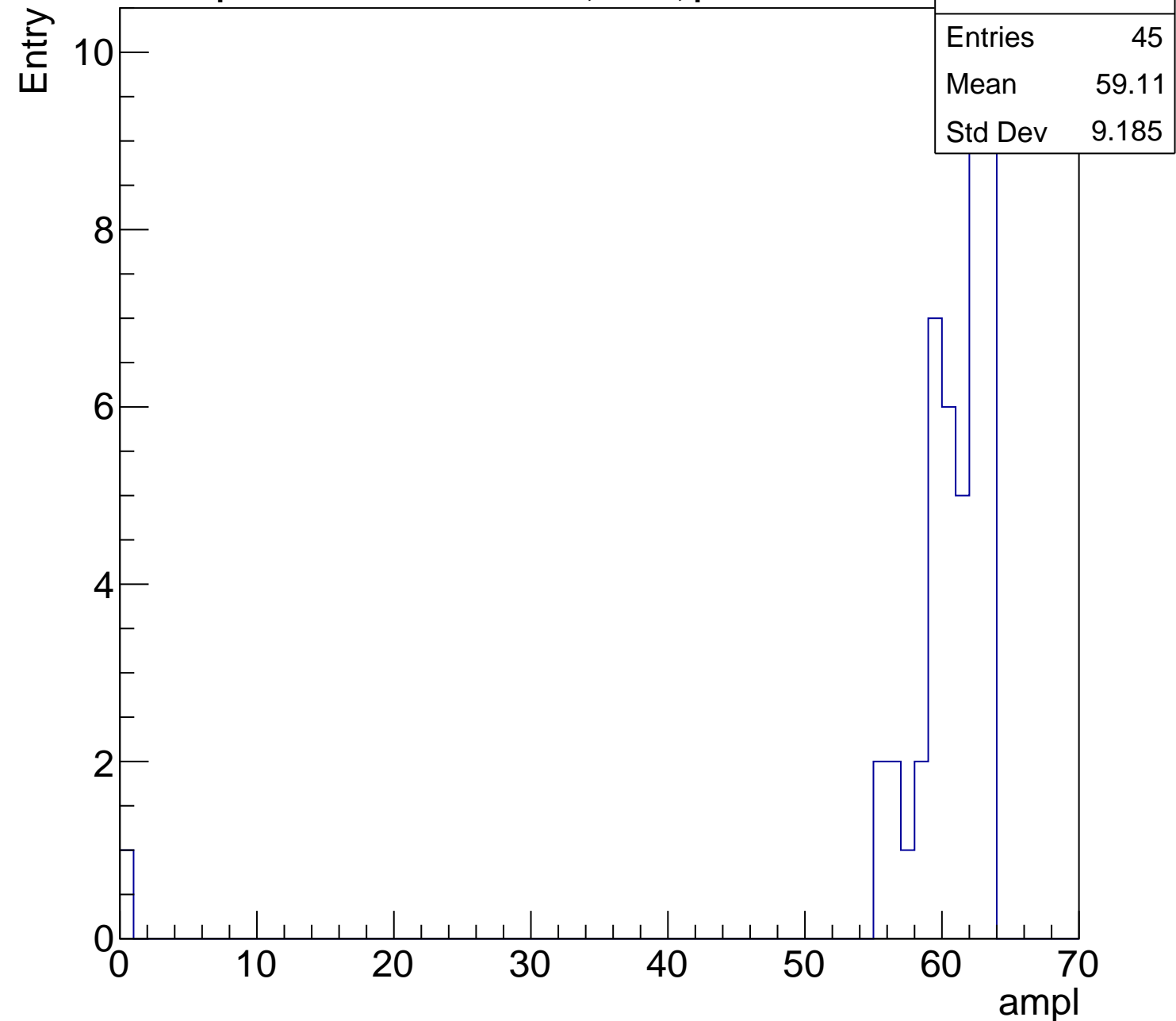
40

50

ampl

60

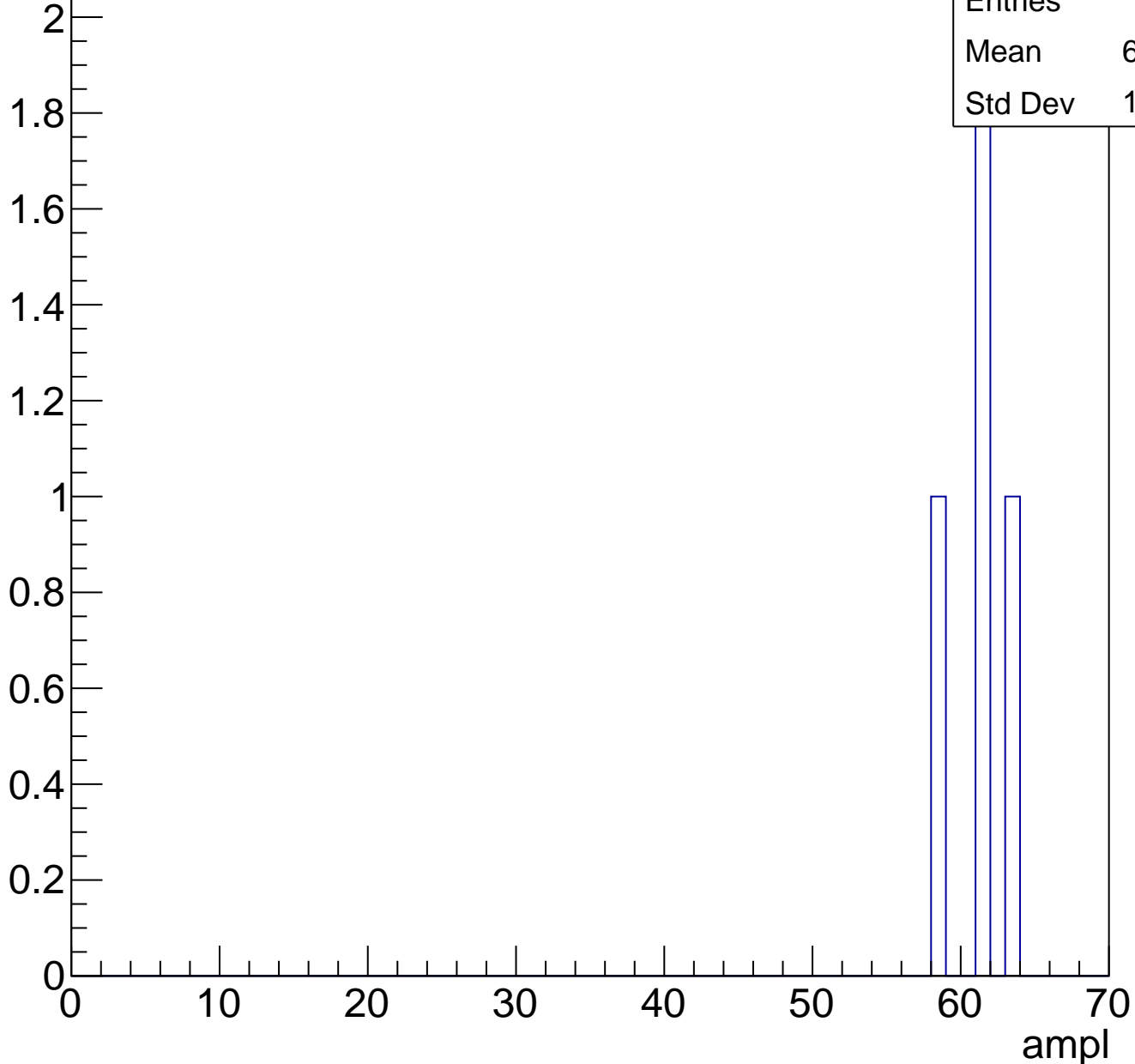
70



# B0L000S, U26-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch103, adc0

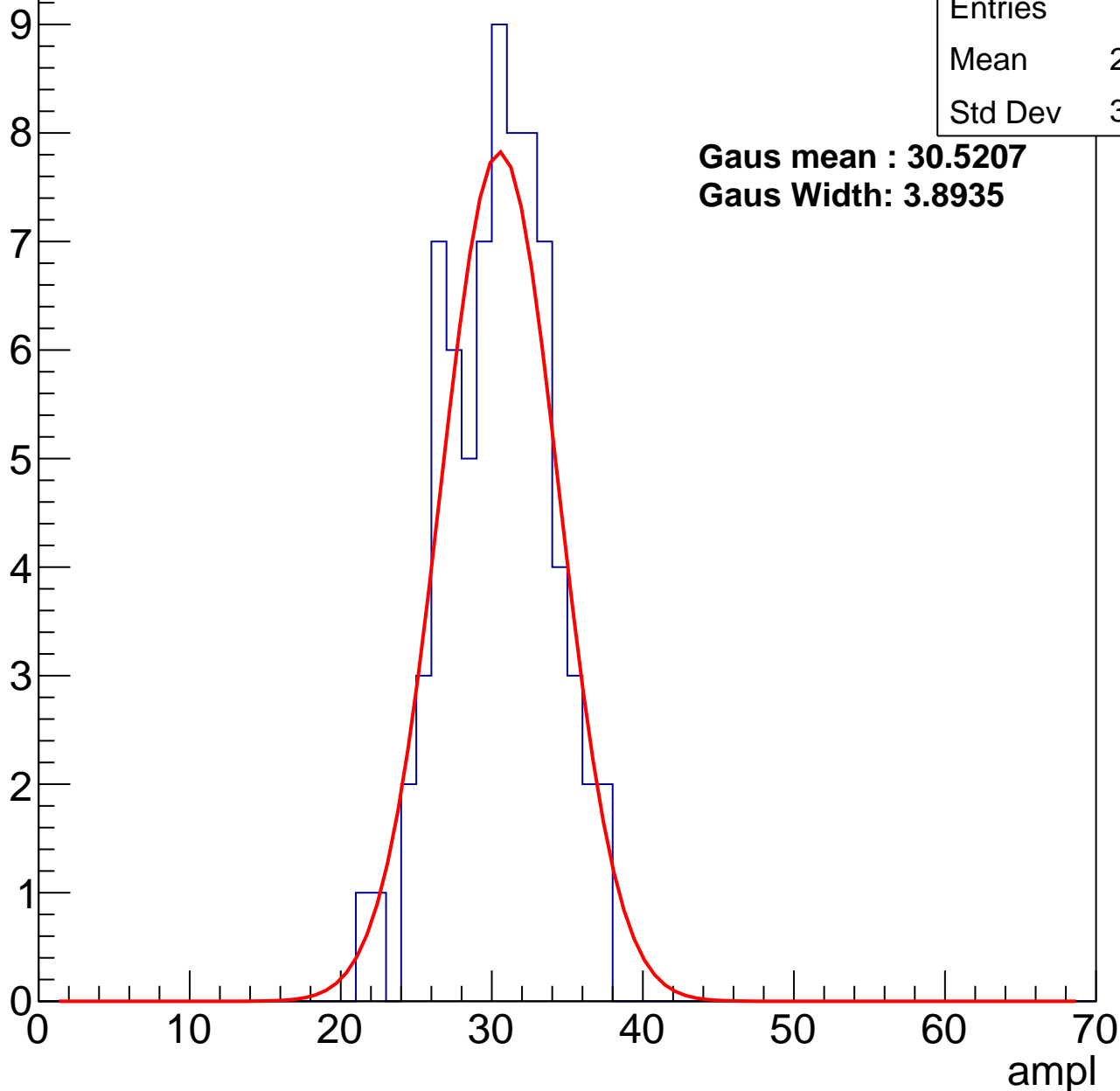
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.93
Std Dev	3.458

**Gaus mean : 30.5207**

**Gaus Width: 3.8935**



# B0L000S, U26-ch103, adc1

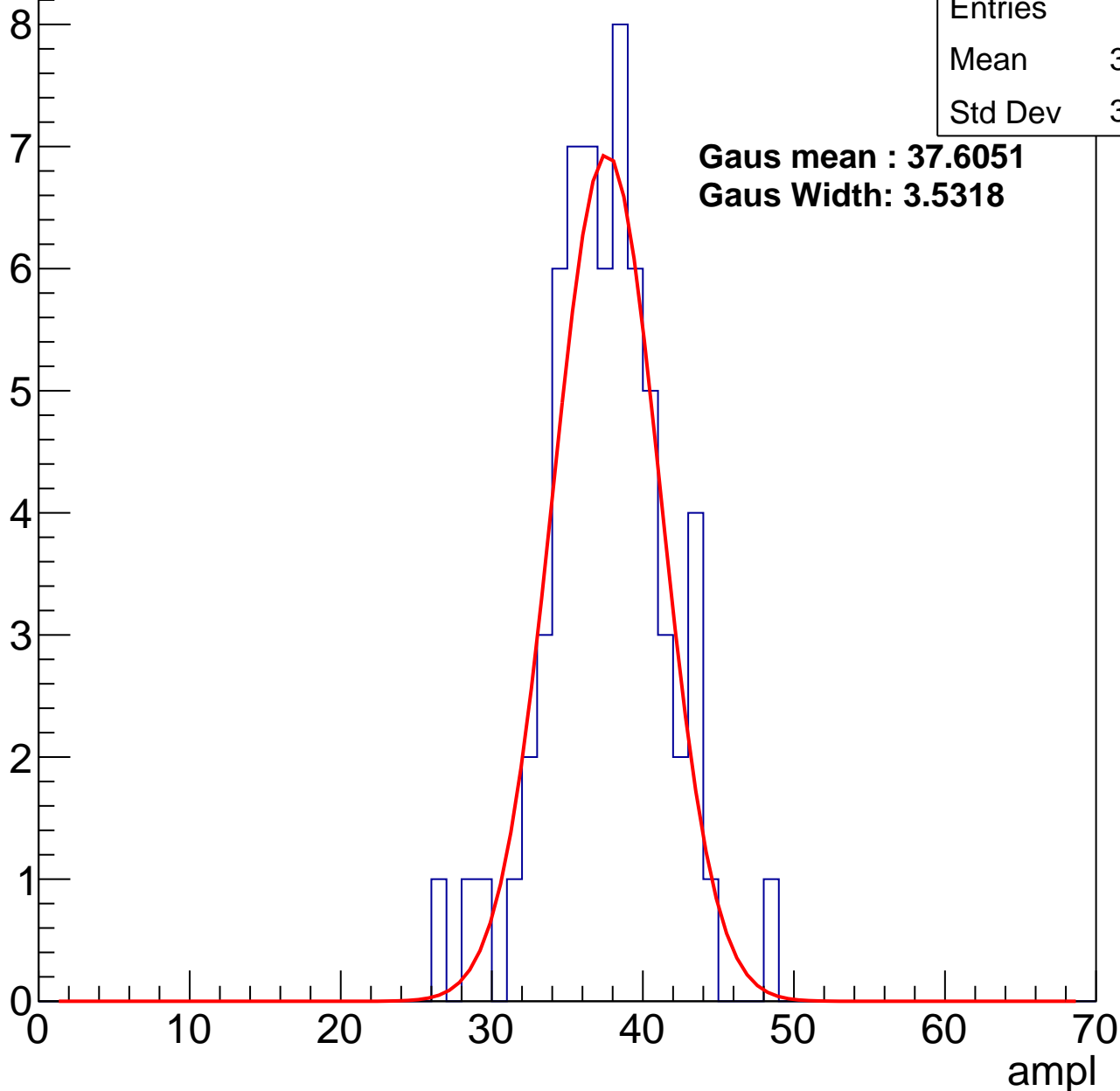
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.06
Std Dev	3.878

**Gaus mean : 37.6051**

**Gaus Width: 3.5318**



# B0L000S, U26-ch103, adc2

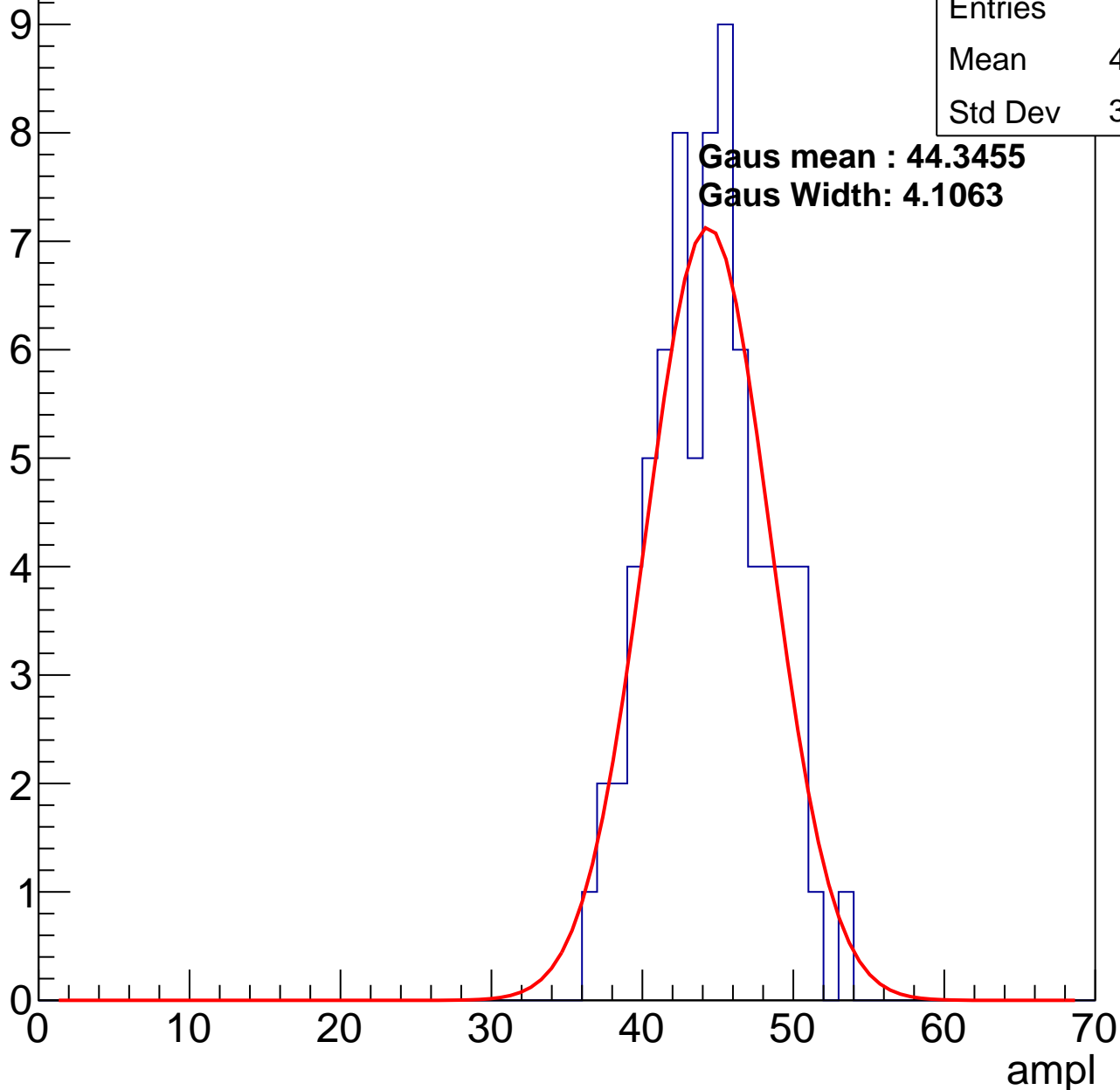
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.95
Std Dev	3.698

**Gaus mean : 44.3455**

**Gaus Width: 4.1063**

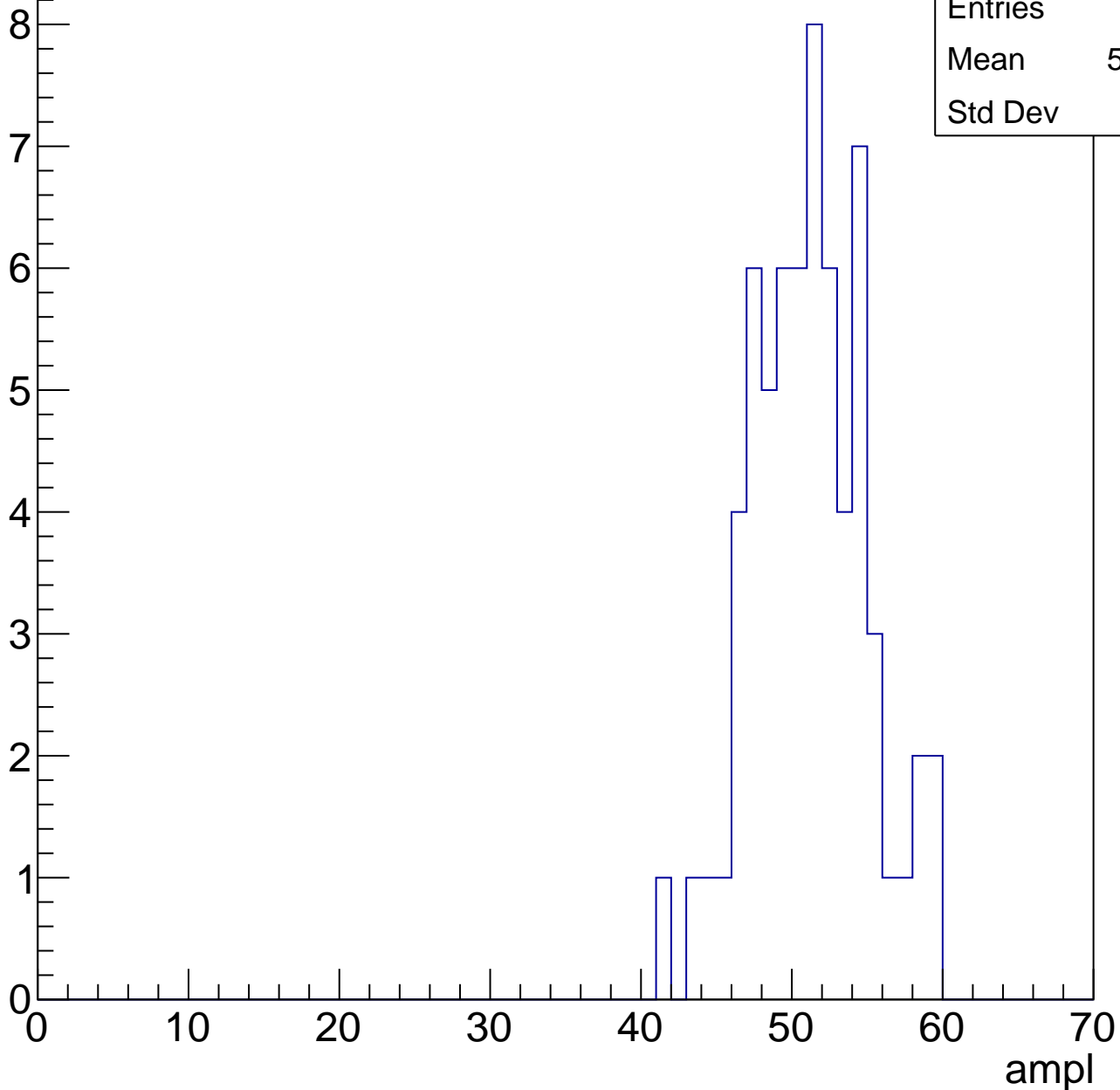


# B0L000S, U26-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

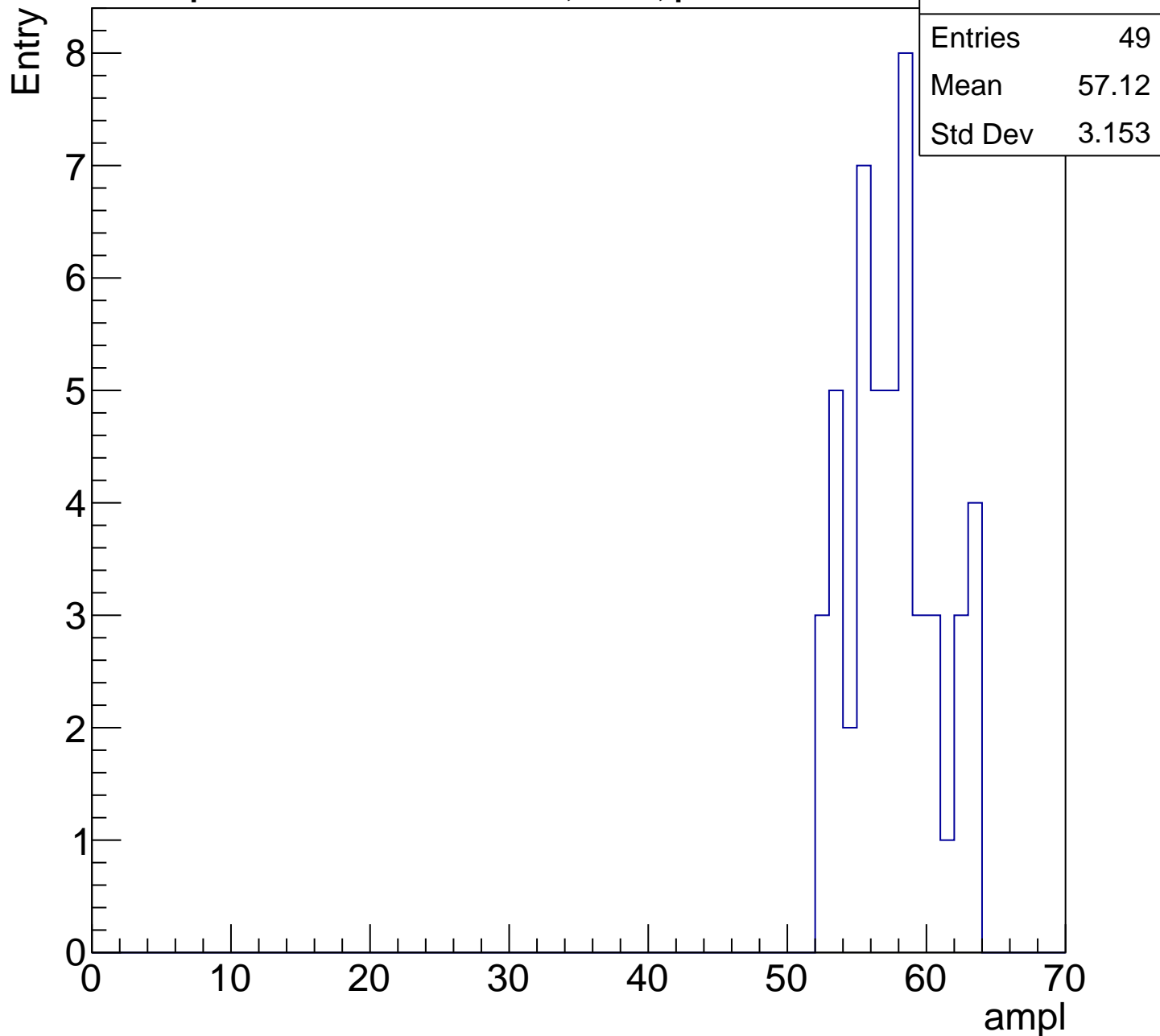
Entry

Entries	65
Mean	50.69
Std Dev	3.79



# B0L000S, U26-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

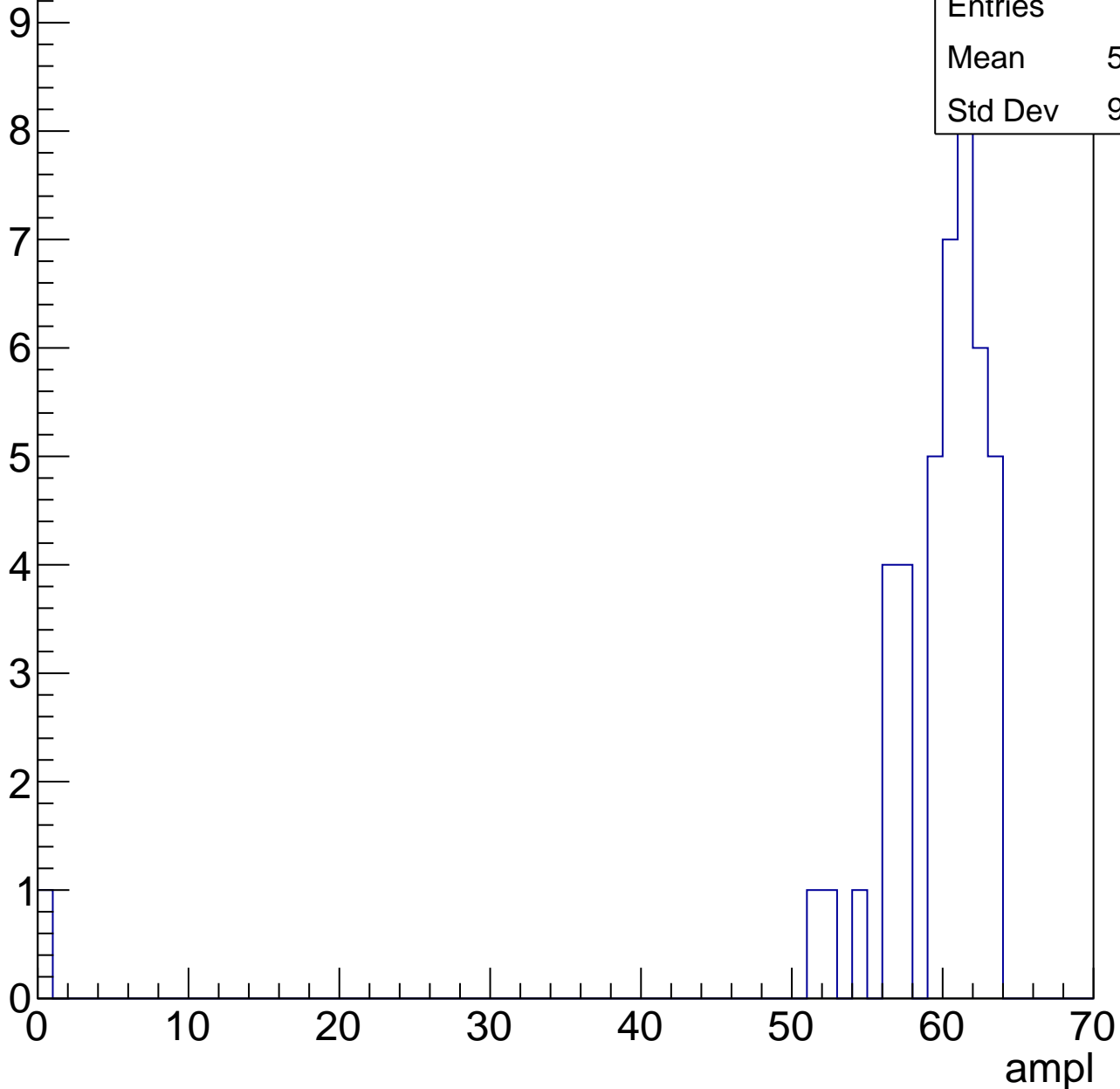


# B0L000S, U26-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.18
Std Dev	9.316



# B0L000S, U26-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch104, adc0

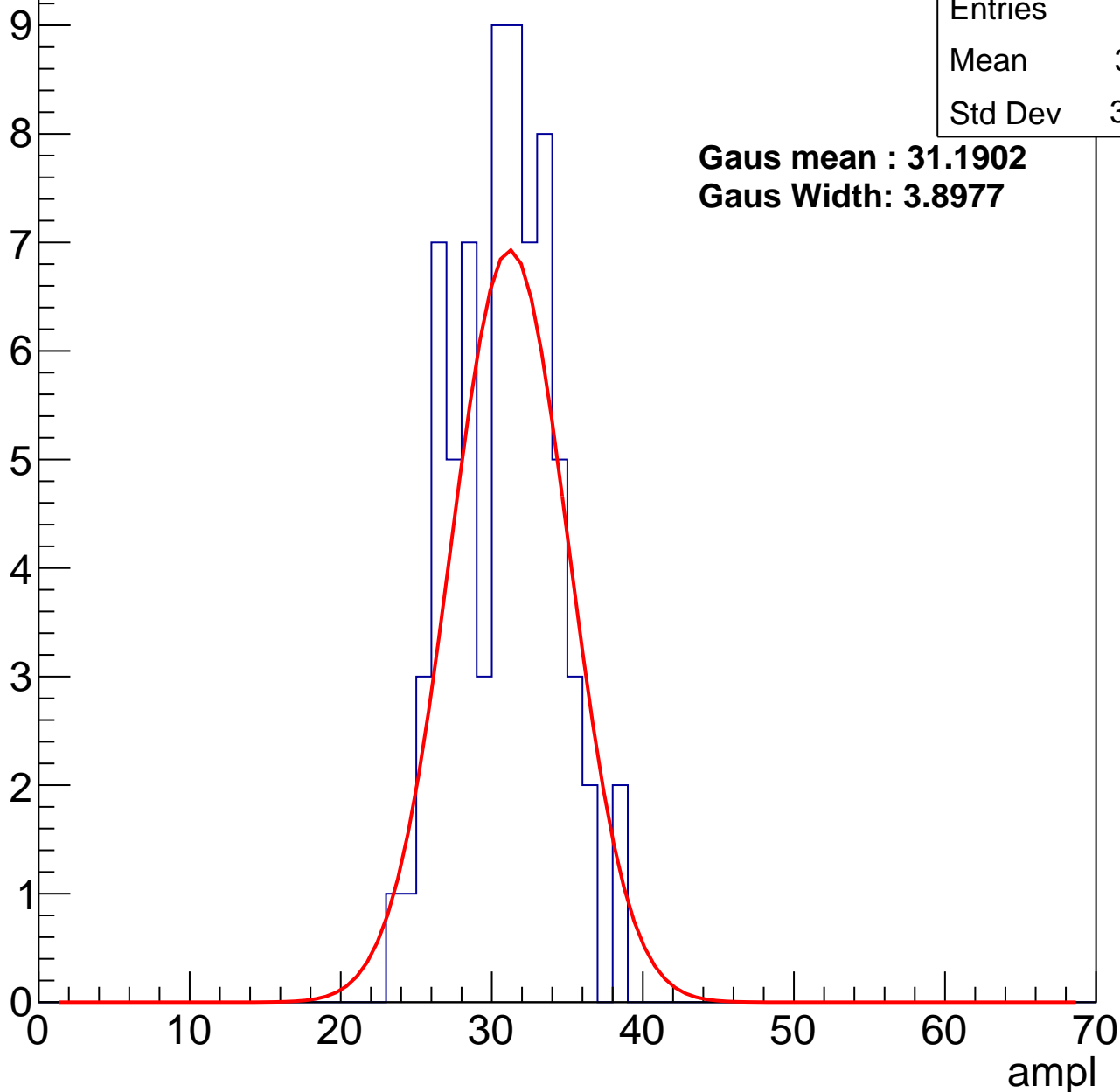
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.31
Std Dev	3.344

**Gaus mean : 31.1902**

**Gaus Width: 3.8977**



# B0L000S, U26-ch104, adc1

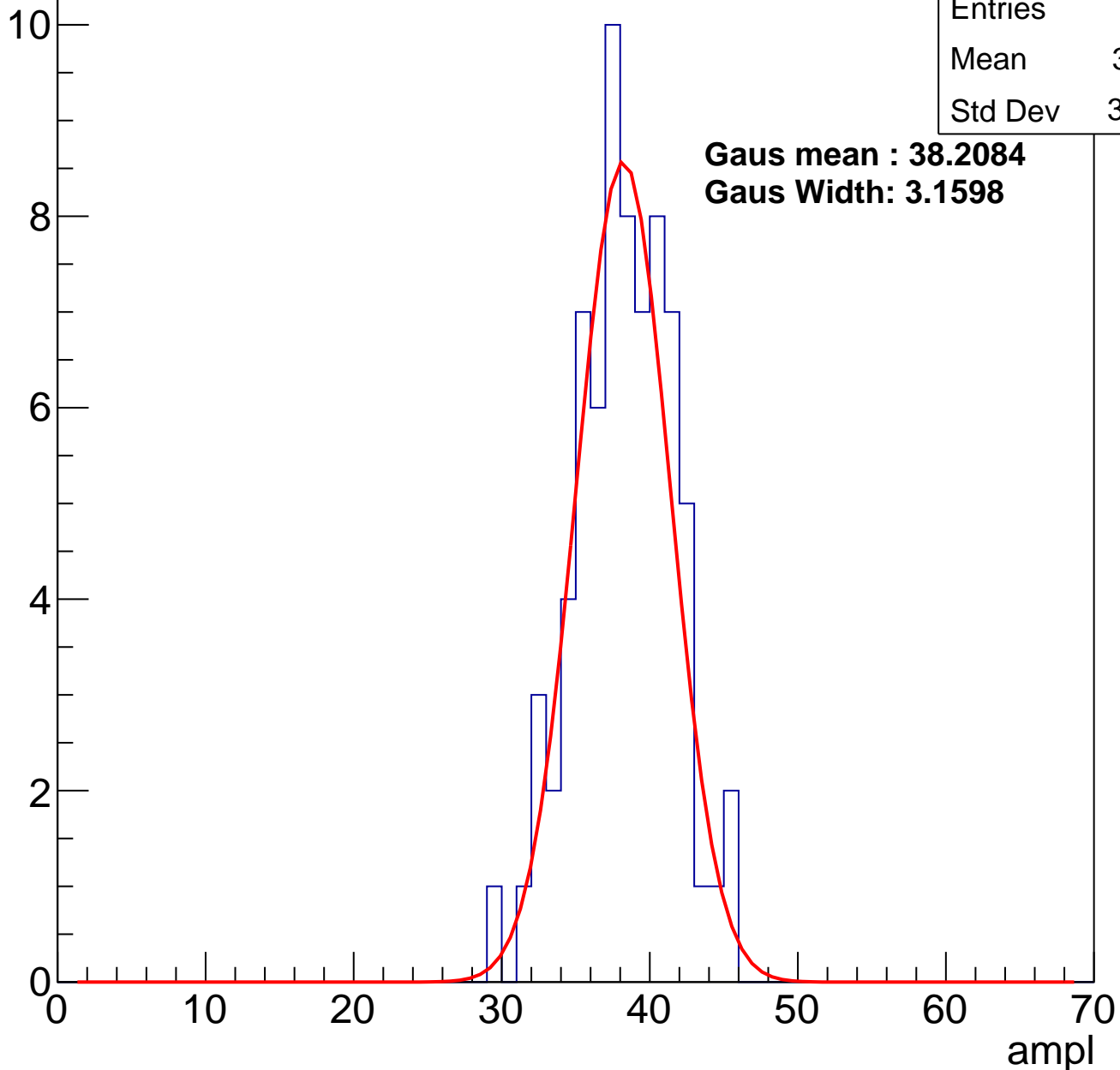
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	37.81
Std Dev	3.284

**Gaus mean : 38.2084**

**Gaus Width: 3.1598**

Entry



# B0L000S, U26-ch104, adc2

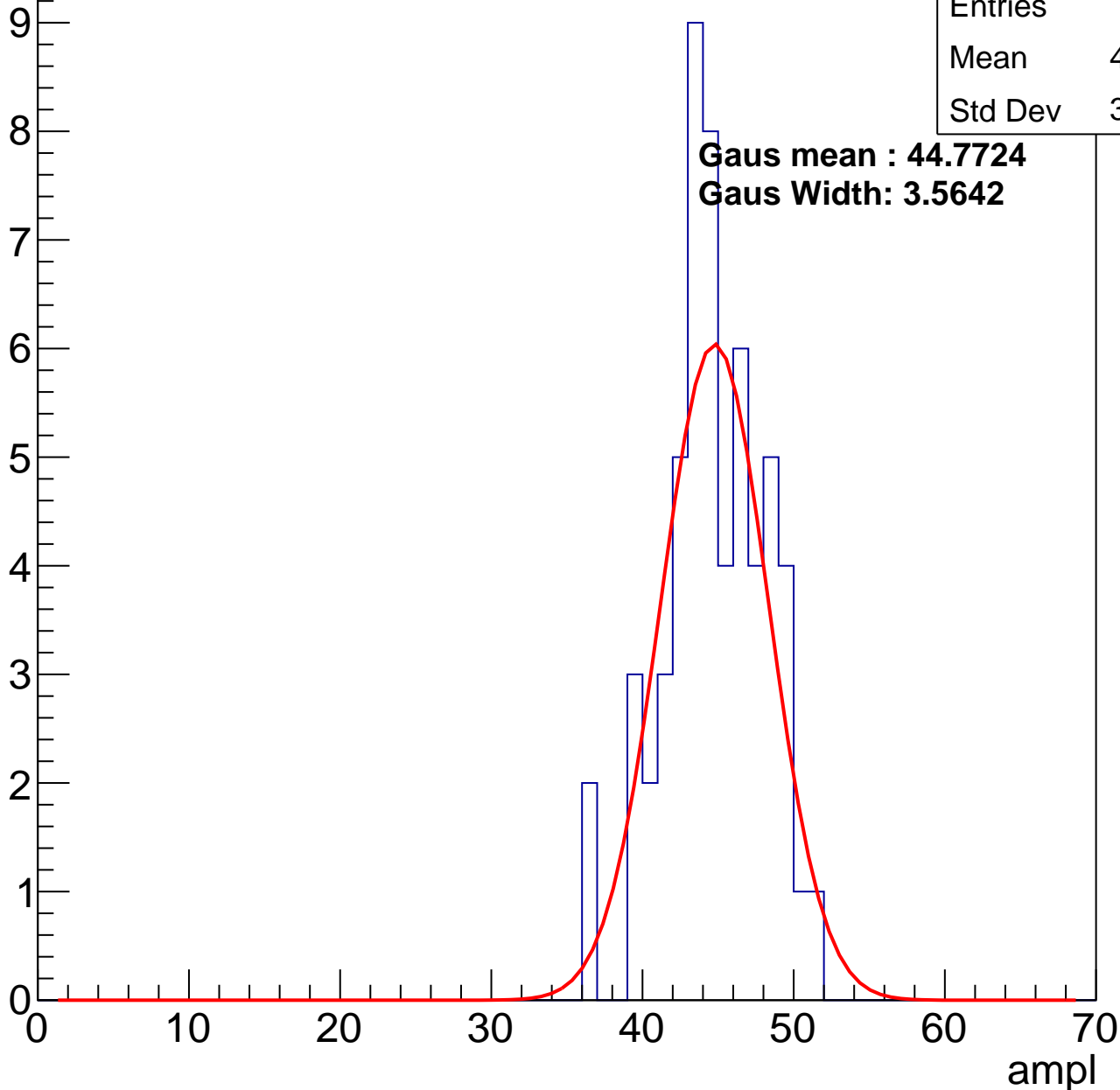
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	44.25
Std Dev	3.289

**Gaus mean : 44.7724**

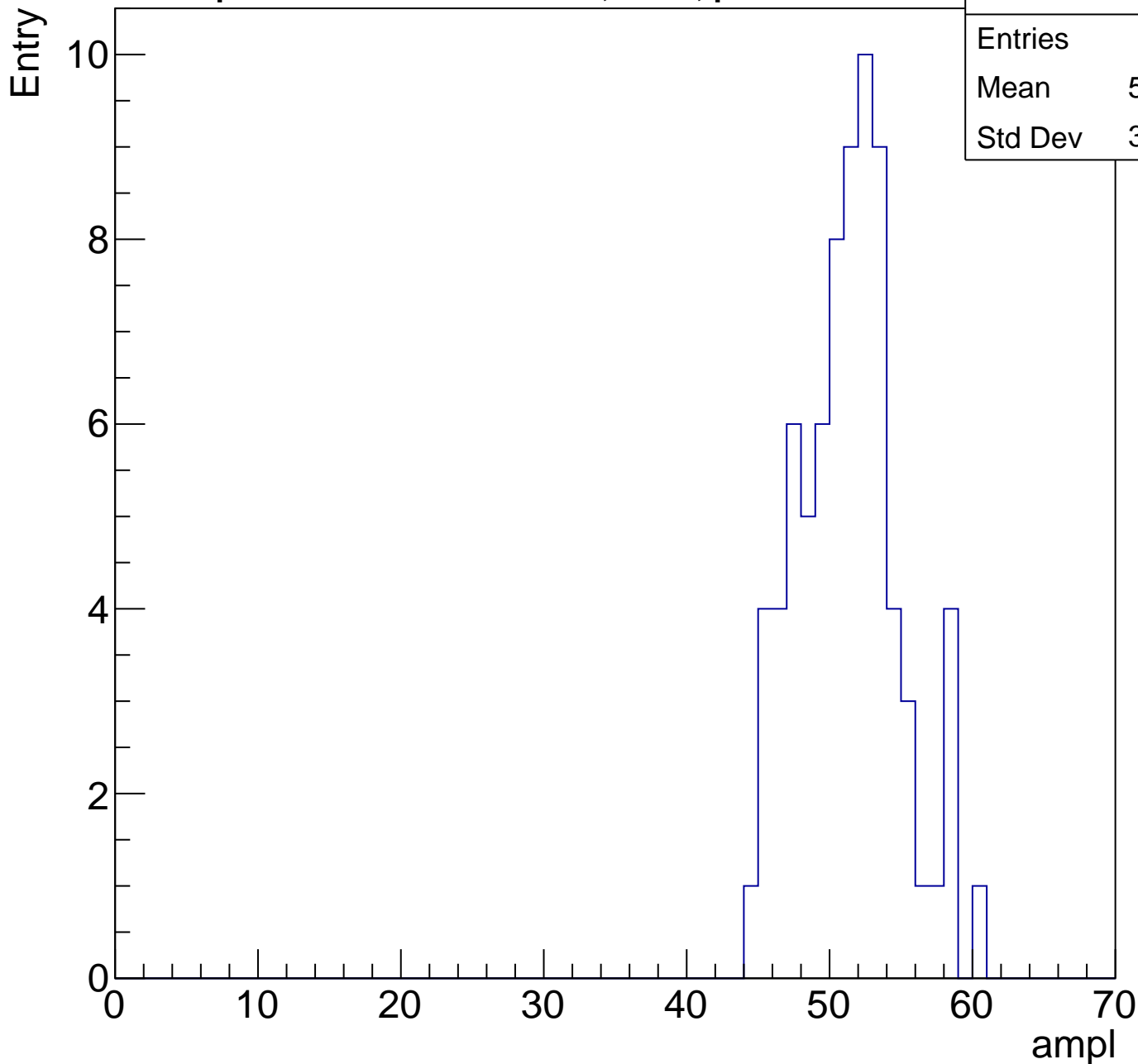
**Gaus Width: 3.5642**



# B0L000S, U26-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	50.87
Std Dev	3.503

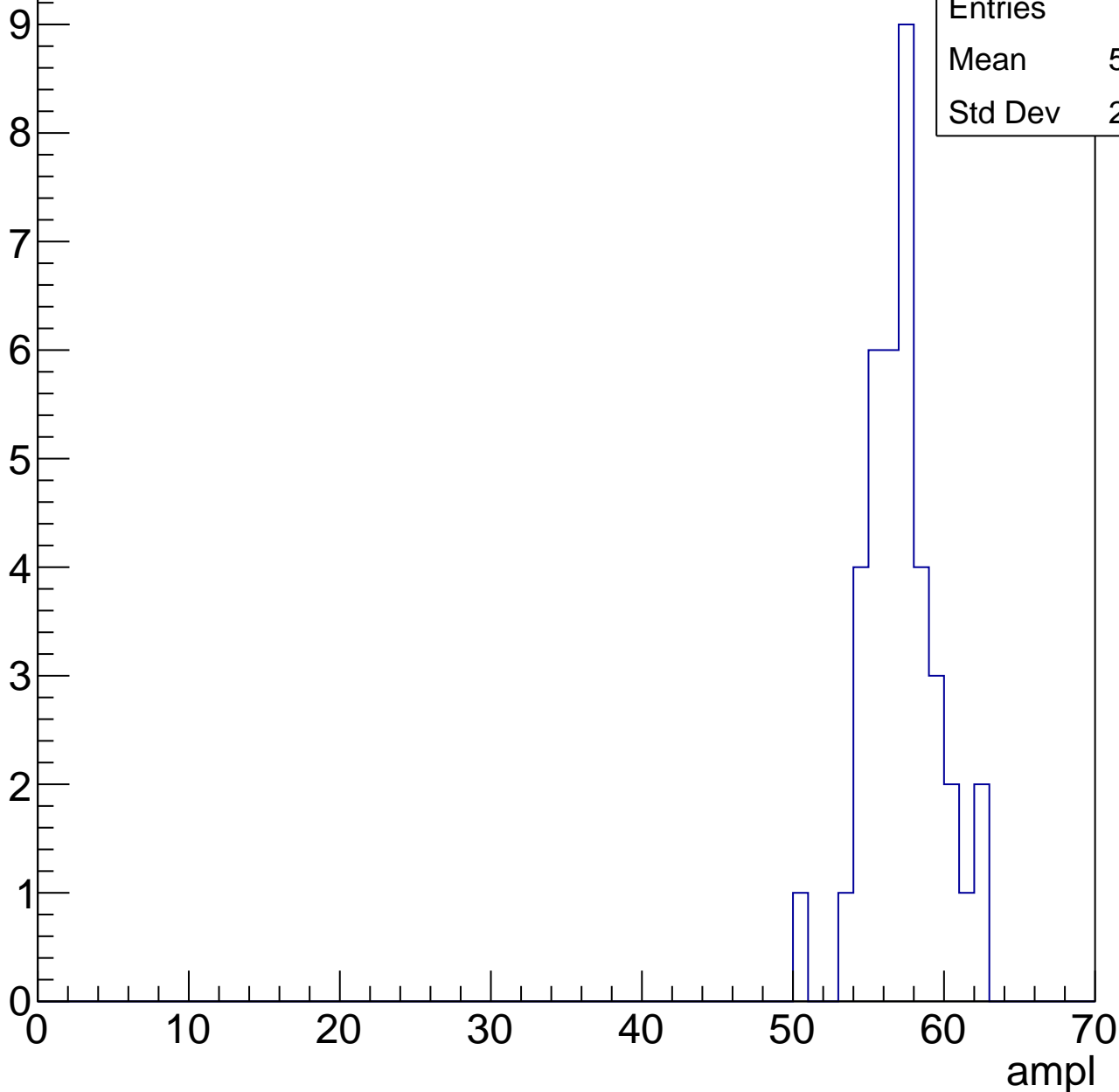


# B0L000S, U26-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	56.72
Std Dev	2.417

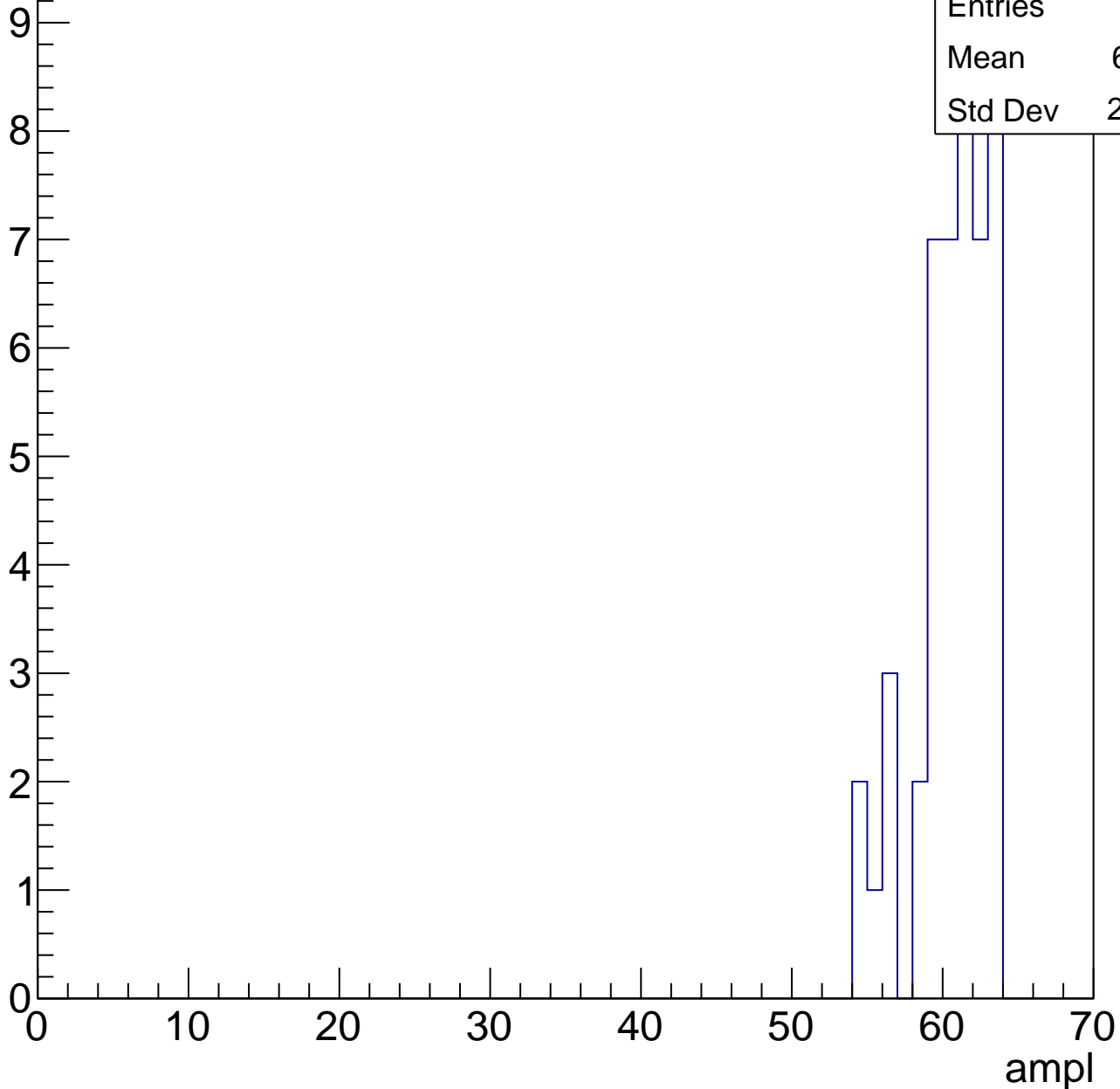


# B0L000S, U26-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

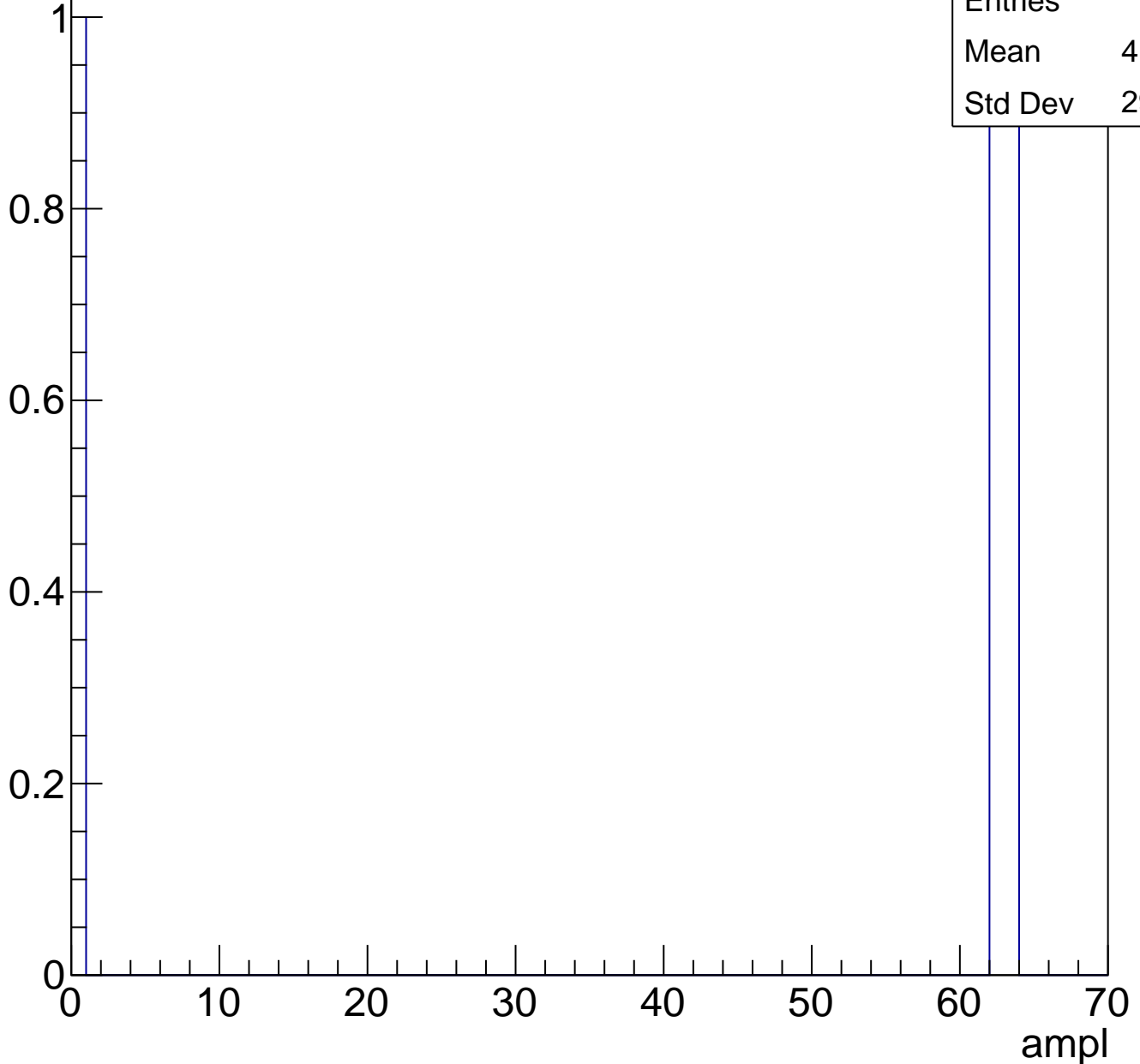
Entries	47
Mean	60.21
Std Dev	2.423



# B0L000S, U26-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch105, adc0

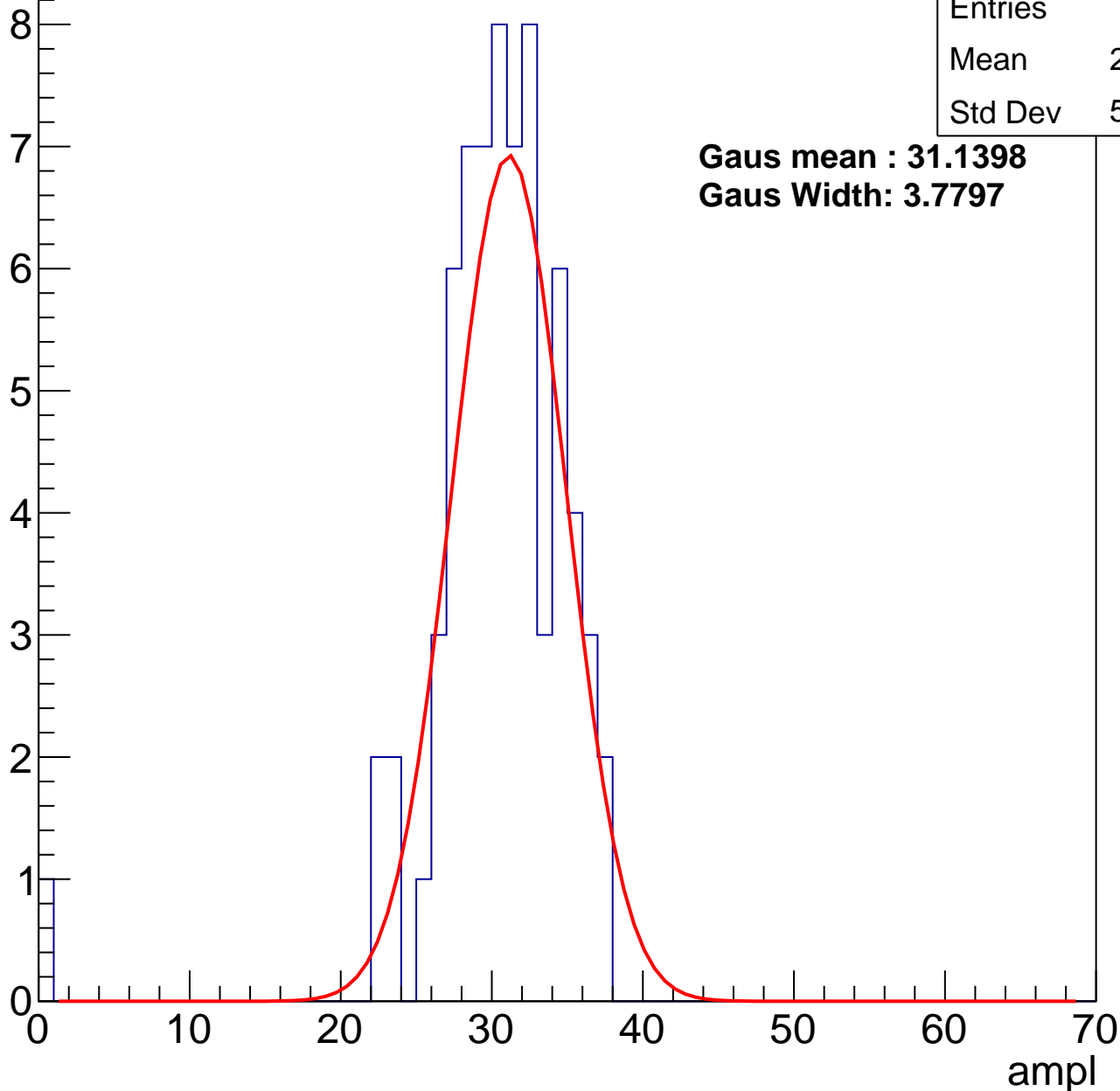
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.89
Std Dev	5.007

**Gaus mean : 31.1398**

**Gaus Width: 3.7797**



# B0L000S, U26-ch105, adc1

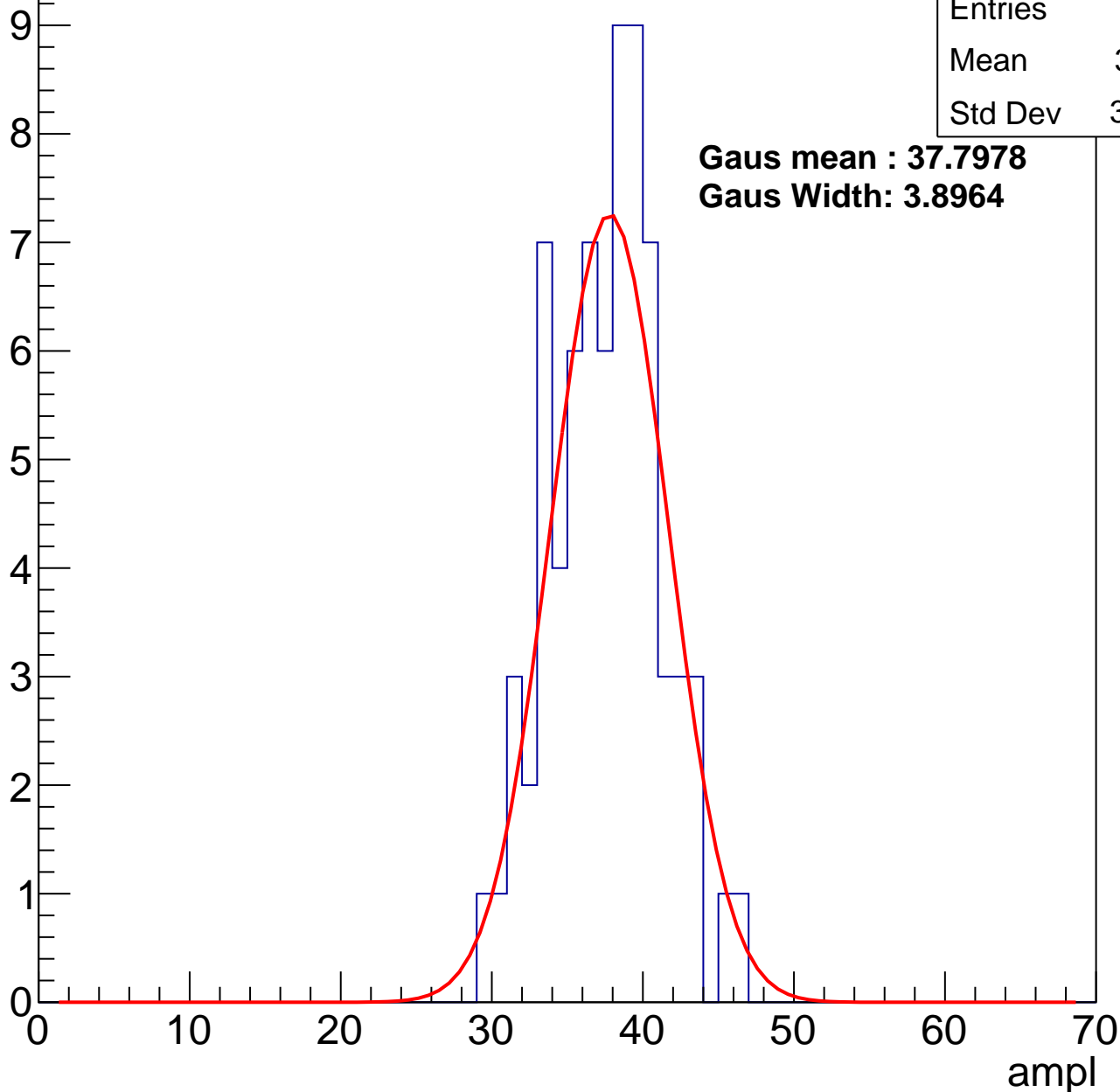
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	37.11
Std Dev	3.575

**Gaus mean : 37.7978**

**Gaus Width: 3.8964**



# B0L000S, U26-ch105, adc2

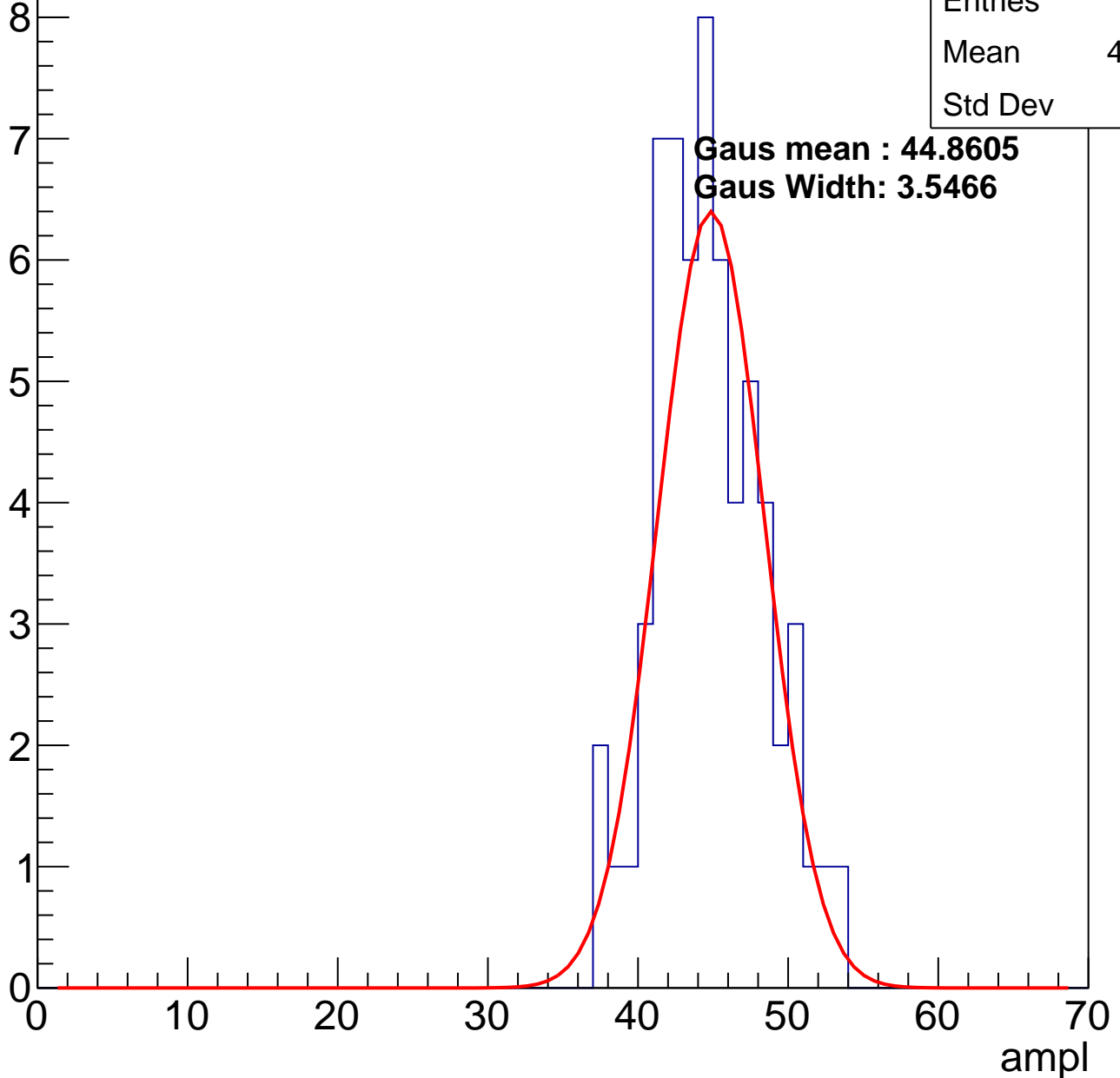
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	44.31
Std Dev	3.55

**Gaus mean : 44.8605**

**Gaus Width: 3.5466**

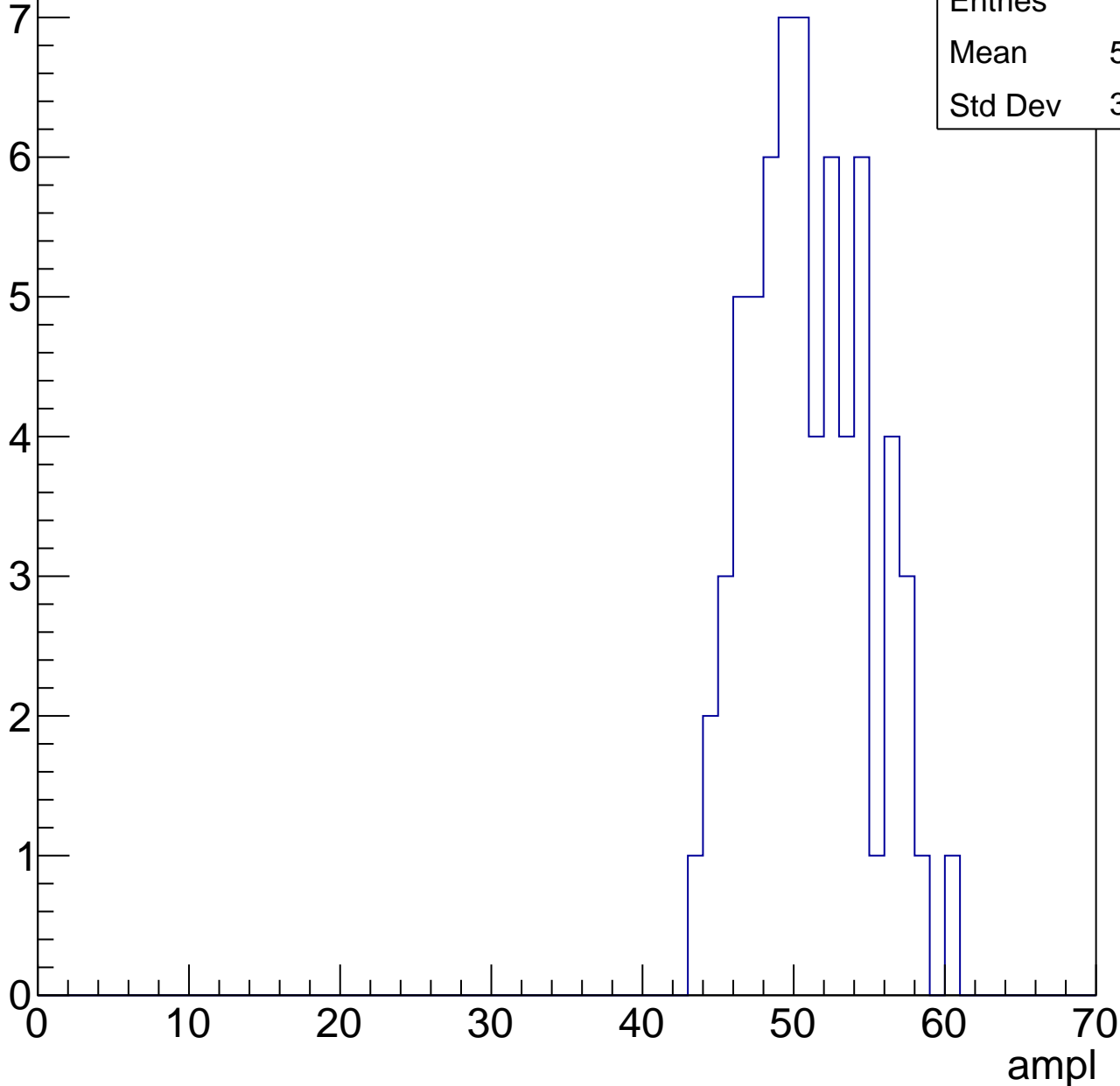


# B0L000S, U26-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.48
Std Dev	3.846

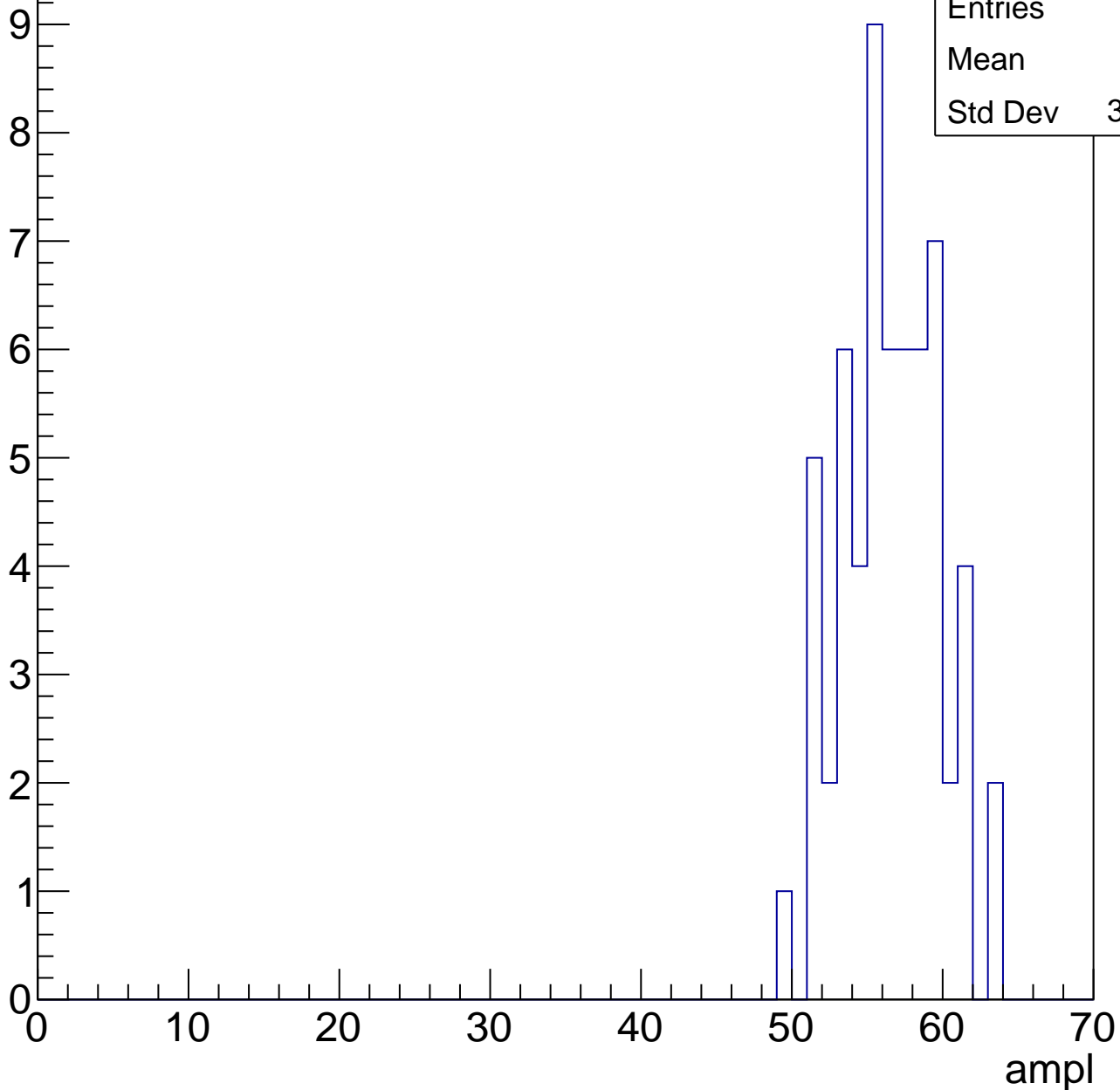


# B0L000S, U26-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	56.1
Std Dev	3.182

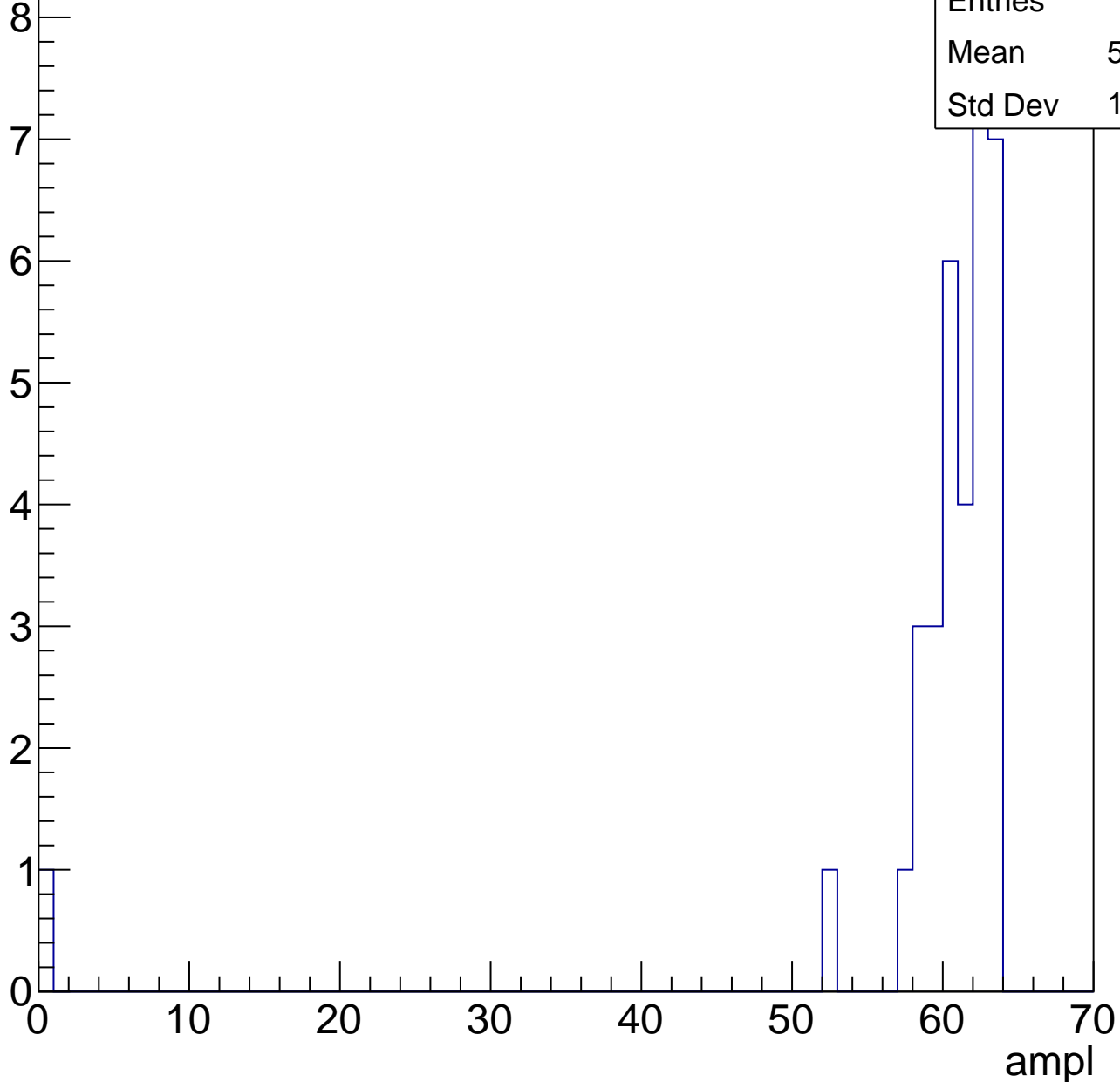


# B0L000S, U26-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.85
Std Dev	10.49



# B0L000S, U26-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	0.9574

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch106, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	30.56
Std Dev	3.34

**Gaus mean : 30.9636**

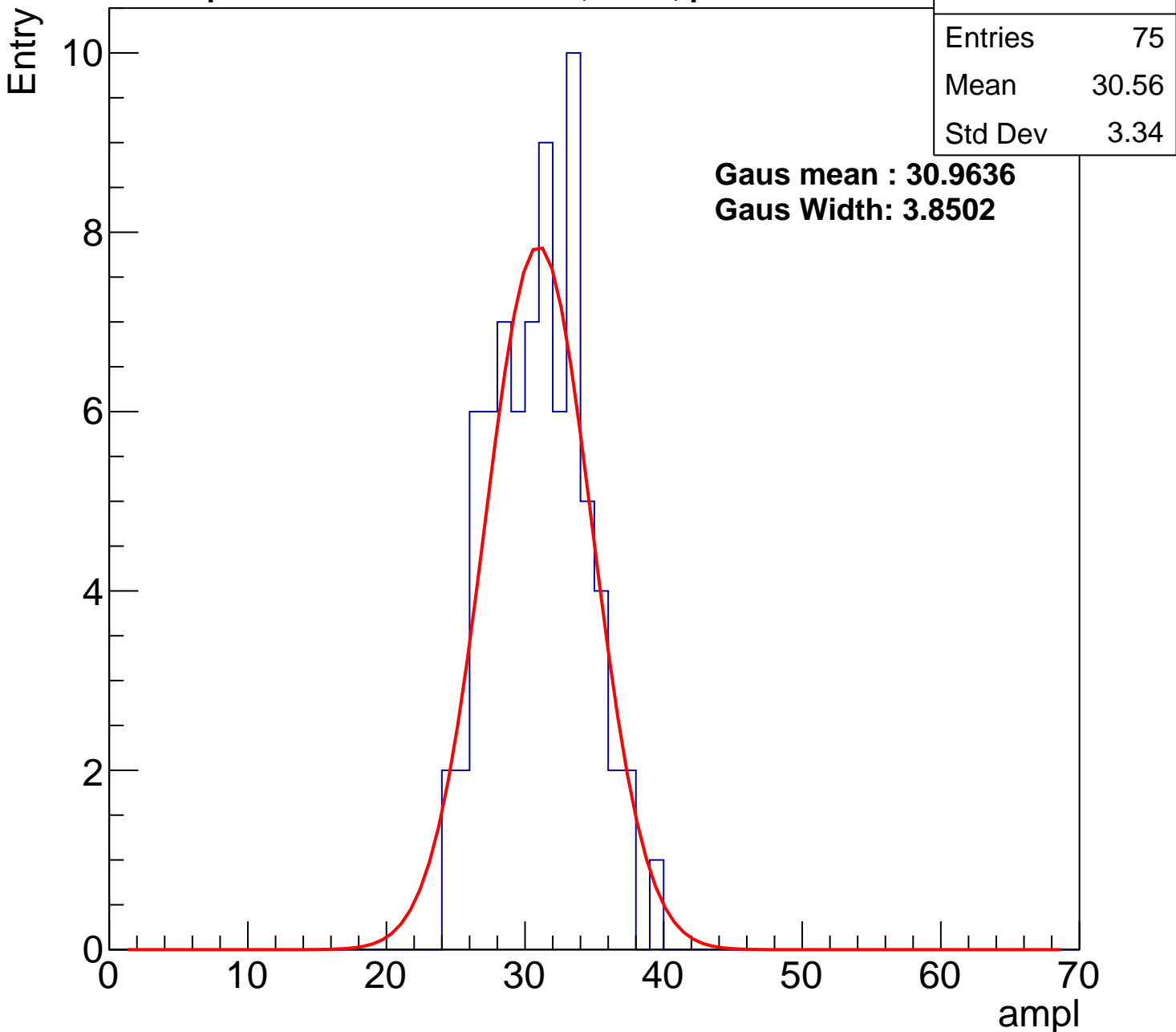
**Gaus Width: 3.8502**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch106, adc1

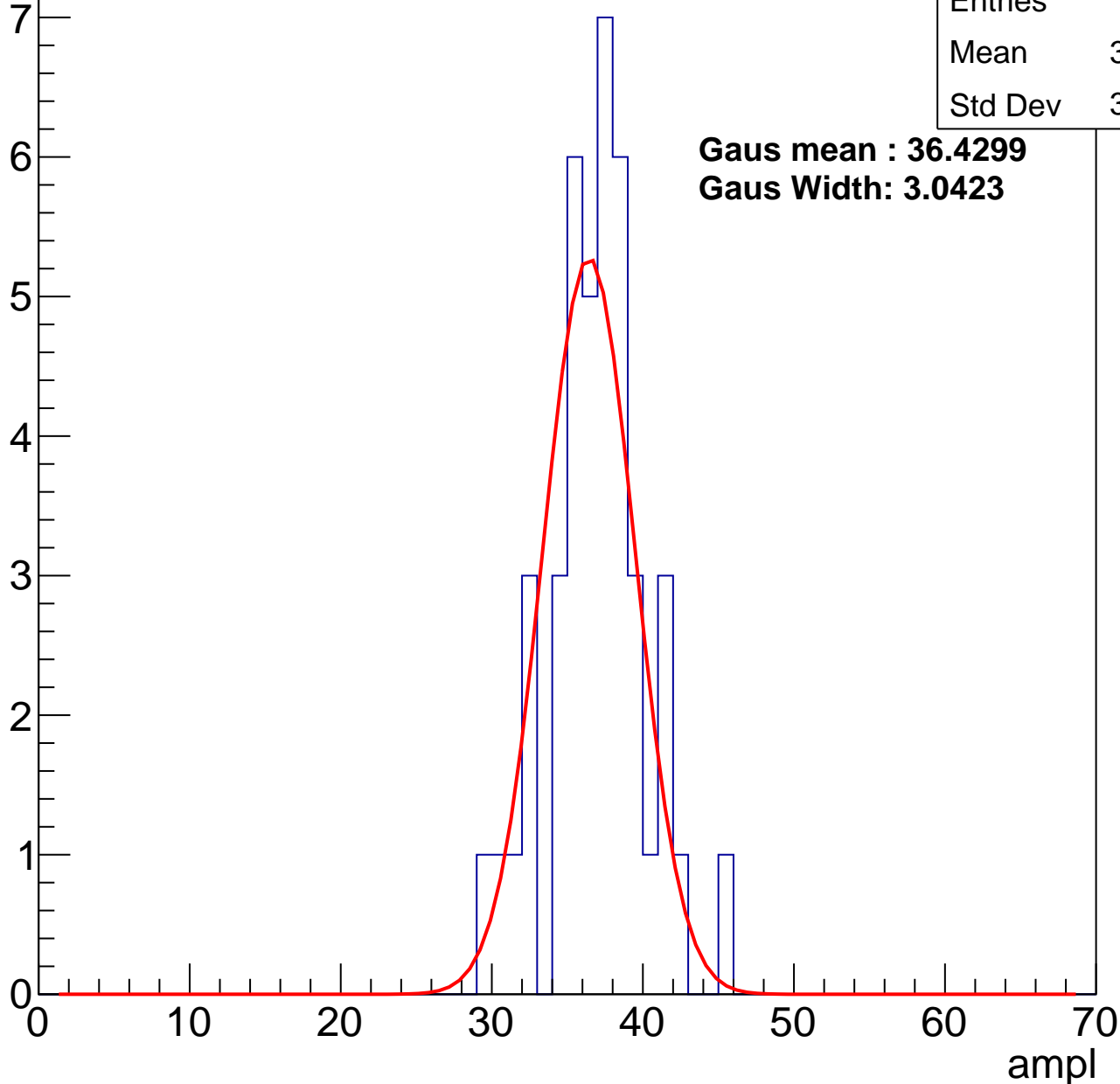
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	36.48
Std Dev	3.209

**Gaus mean : 36.4299**

**Gaus Width: 3.0423**



# B0L000S, U26-ch106, adc2

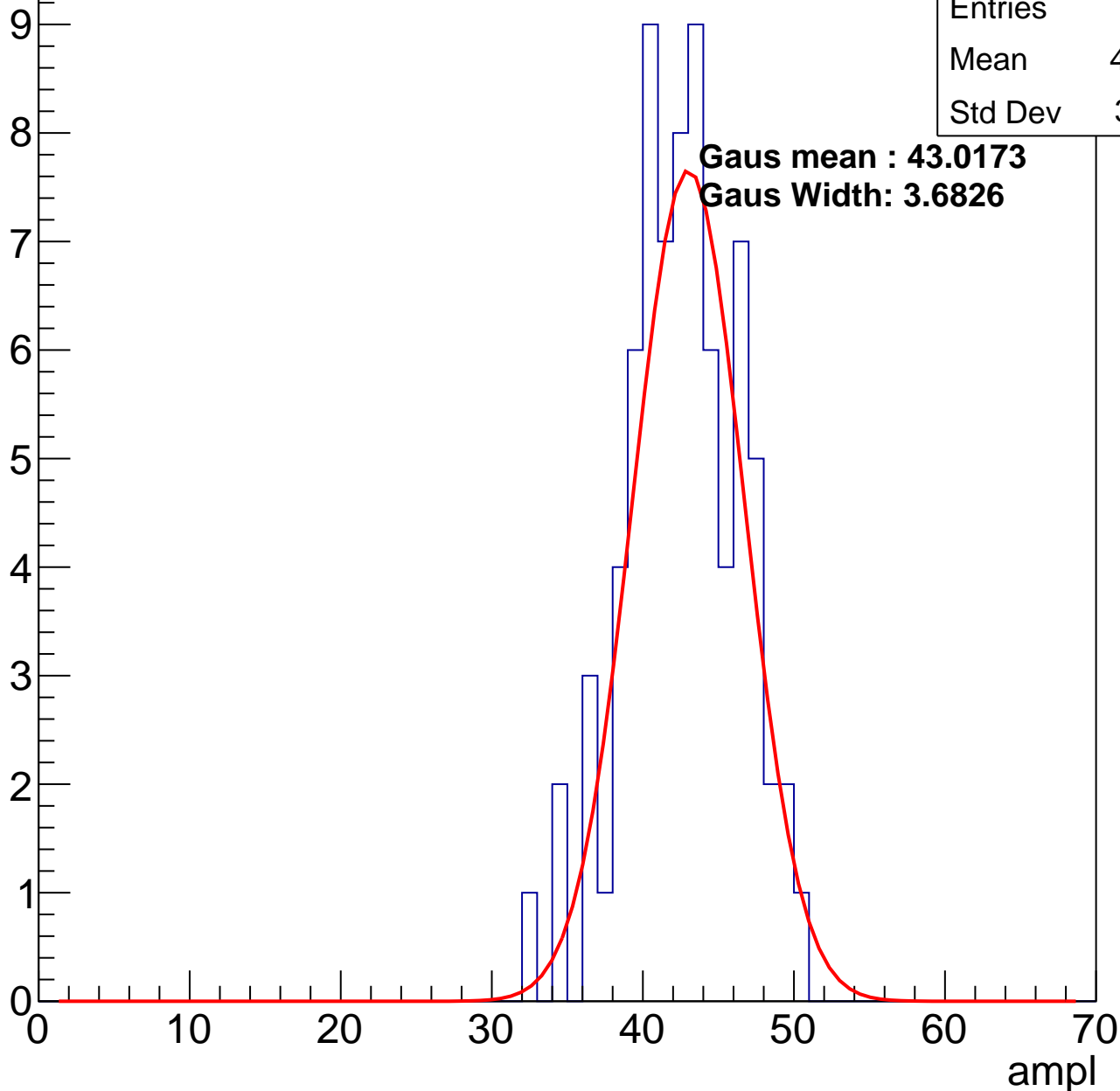
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	42.16
Std Dev	3.711

**Gaus mean : 43.0173**

**Gaus Width: 3.6826**

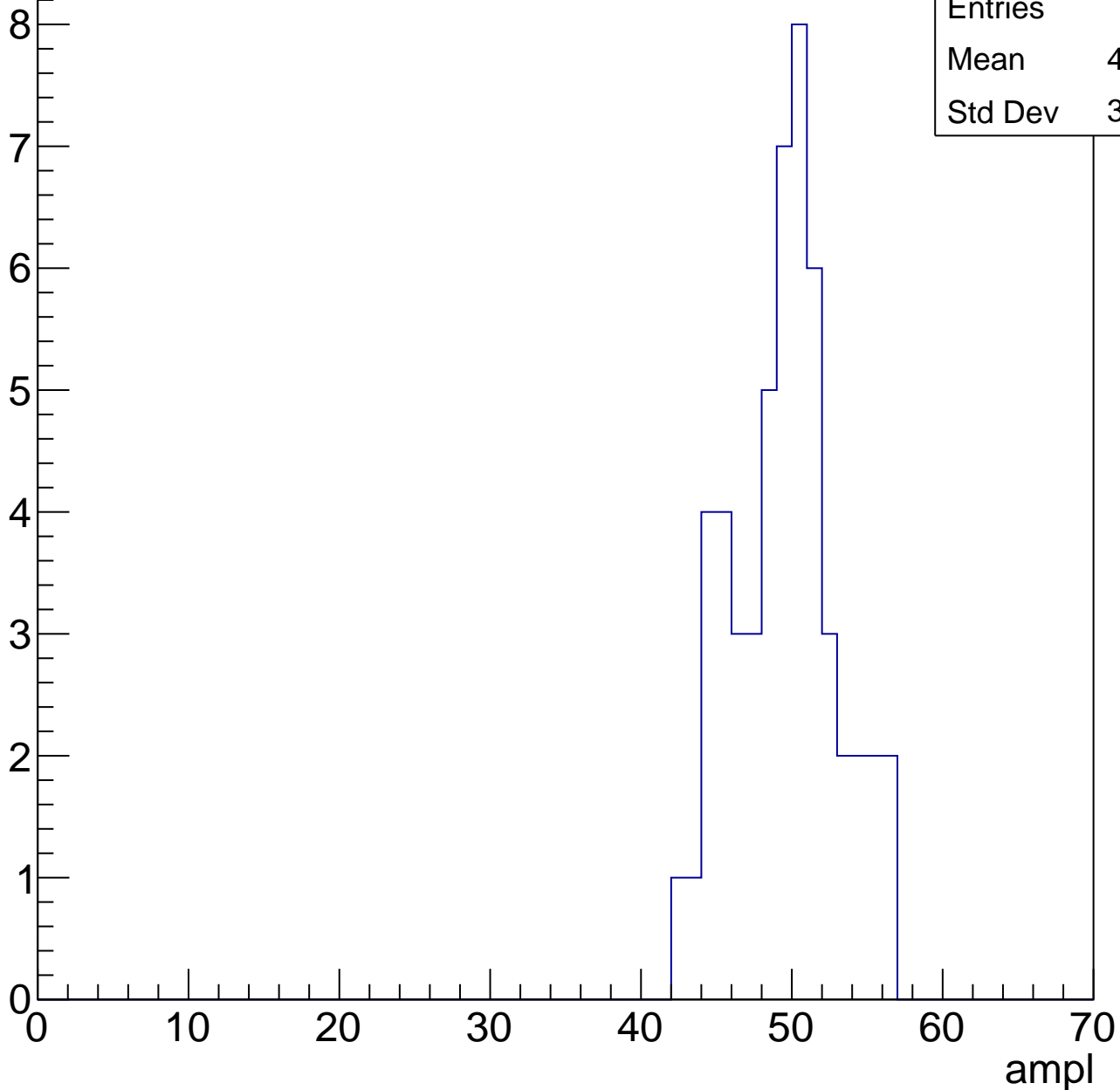


# B0L000S, U26-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	49.08
Std Dev	3.375

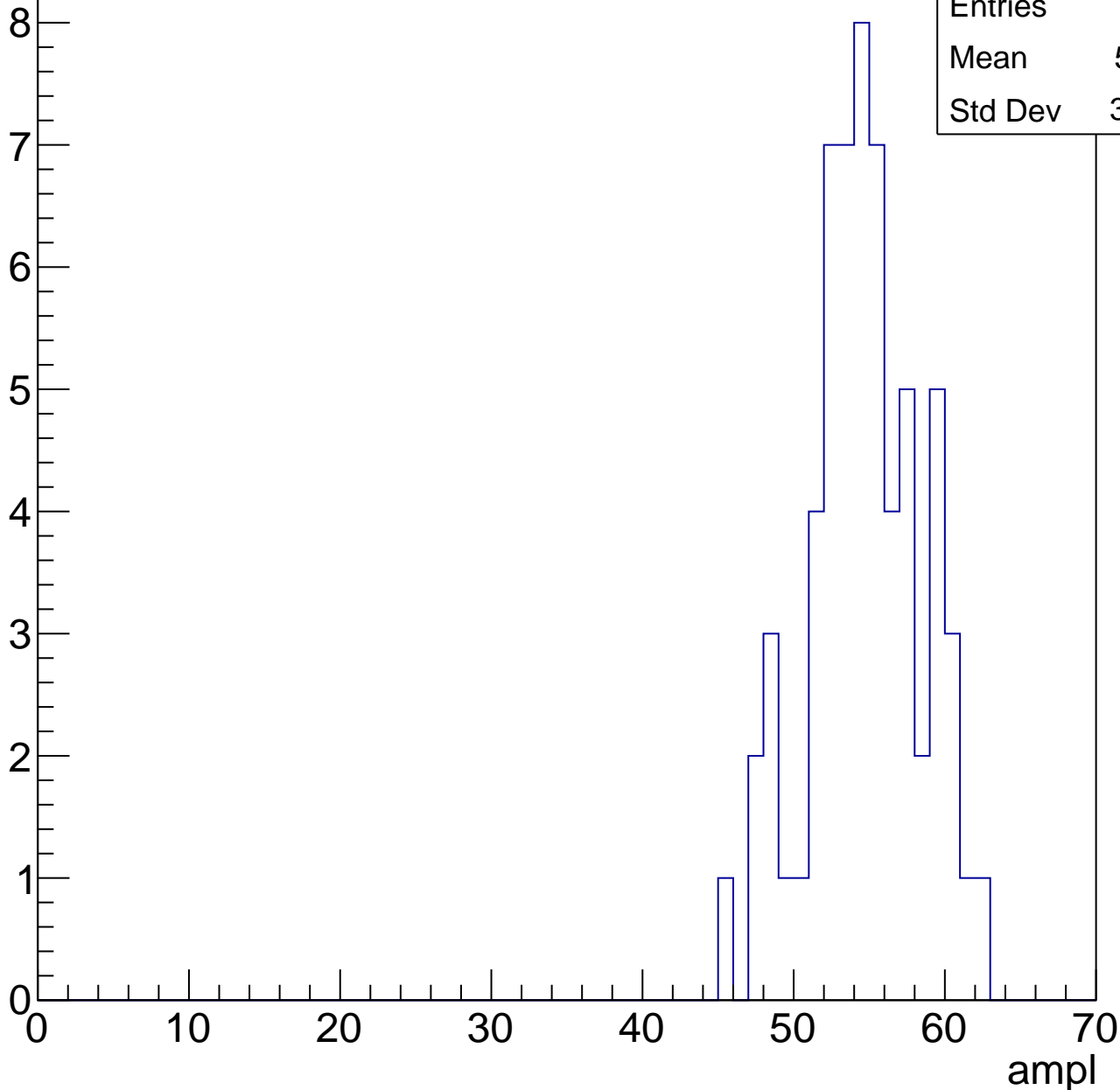


# B0L000S, U26-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	54.21
Std Dev	3.686

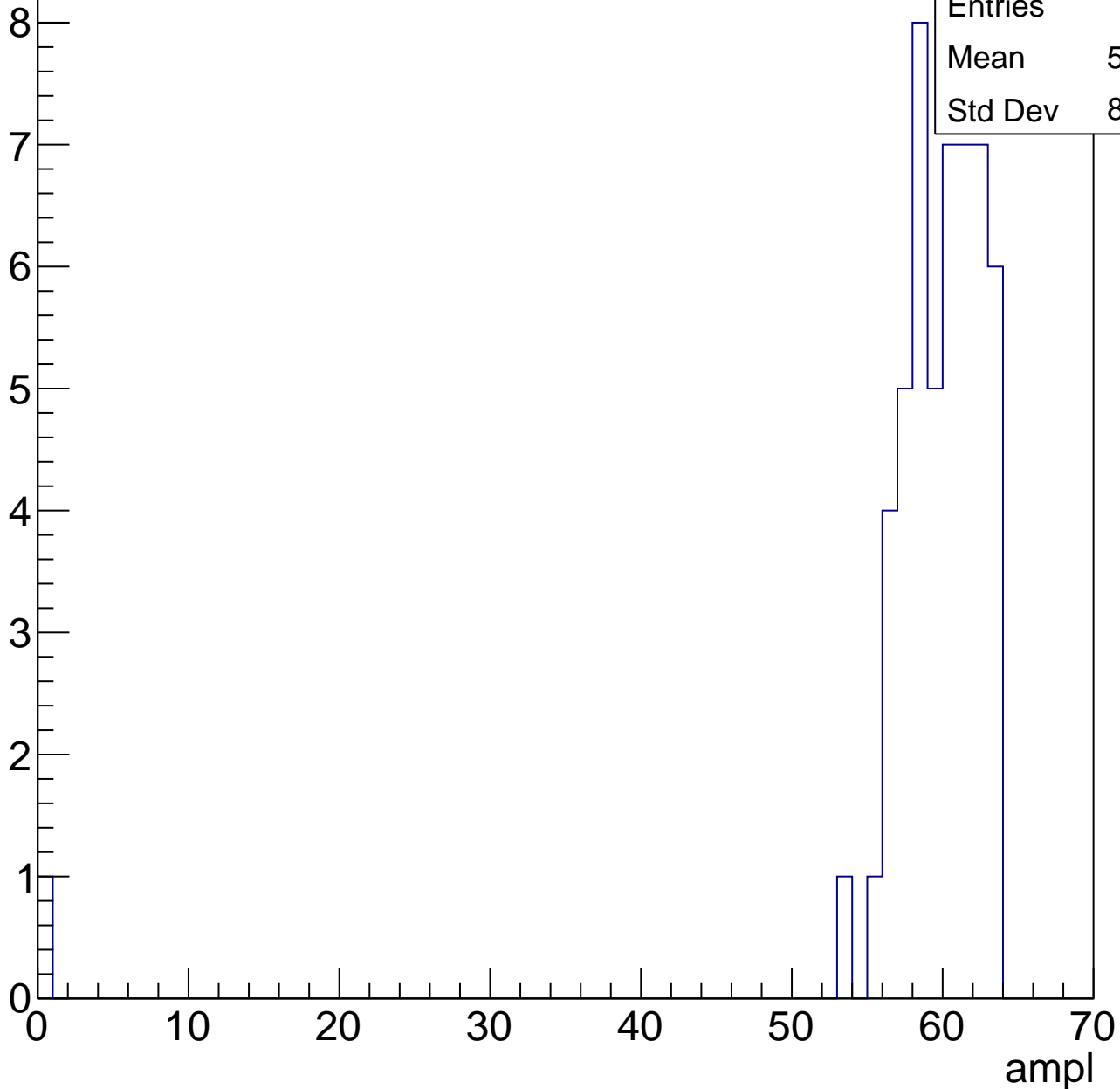


# B0L000S, U26-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.37
Std Dev	8.515



# B0L000S, U26-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

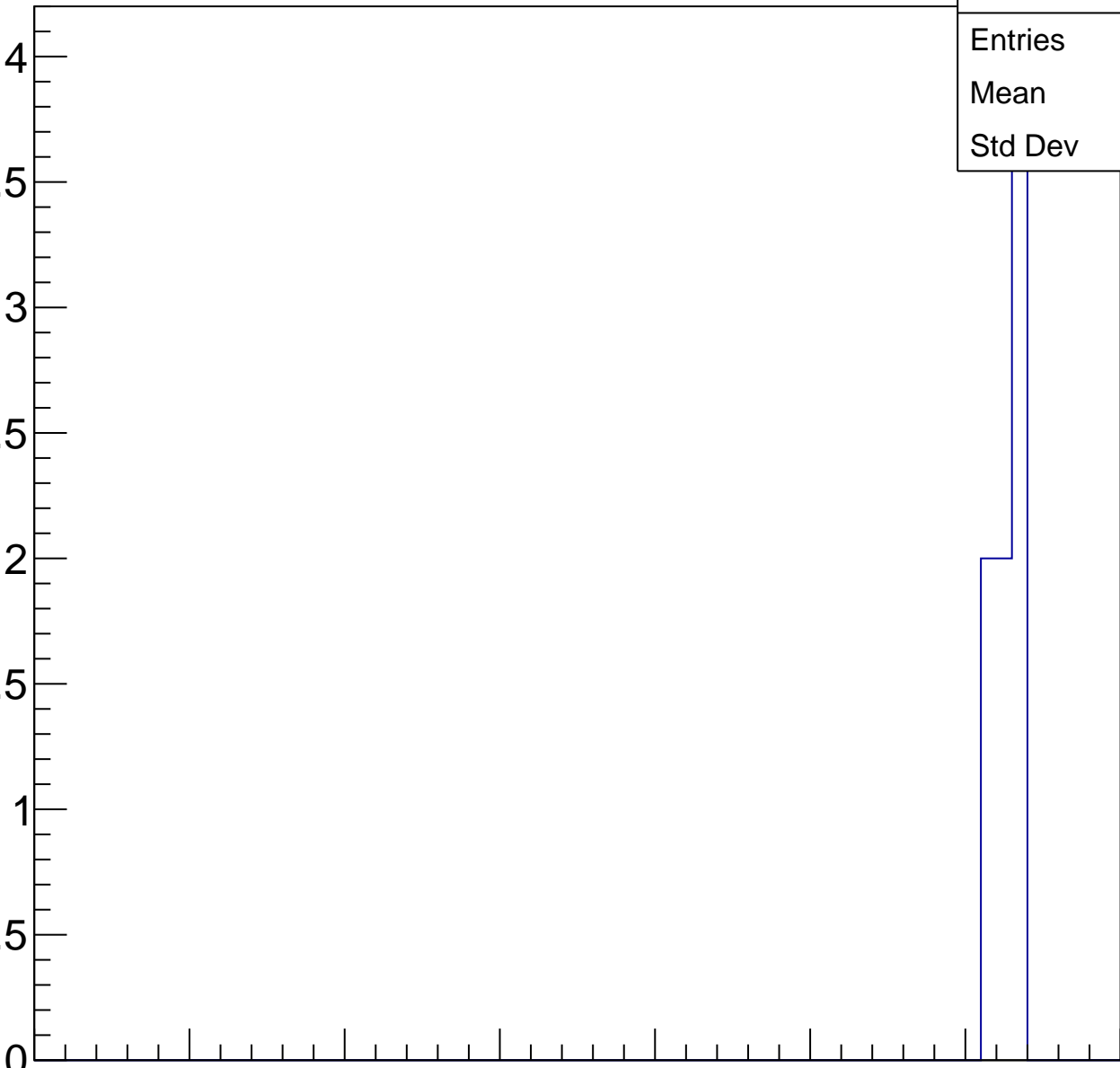
Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	8
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl





# B0L000S, U26-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch107, adc0

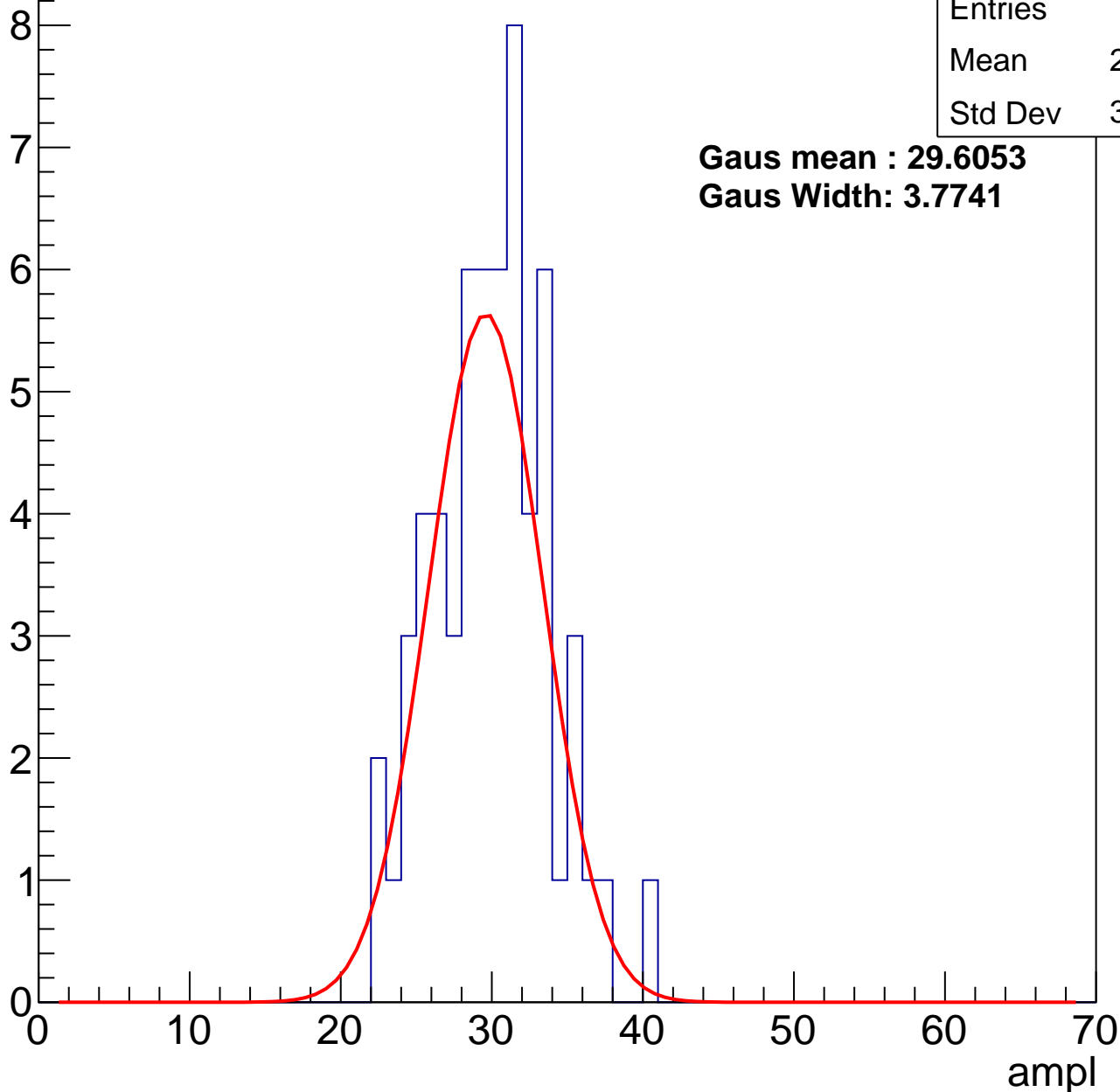
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.53
Std Dev	3.757

**Gaus mean : 29.6053**

**Gaus Width: 3.7741**



# B0L000S, U26-ch107, adc1

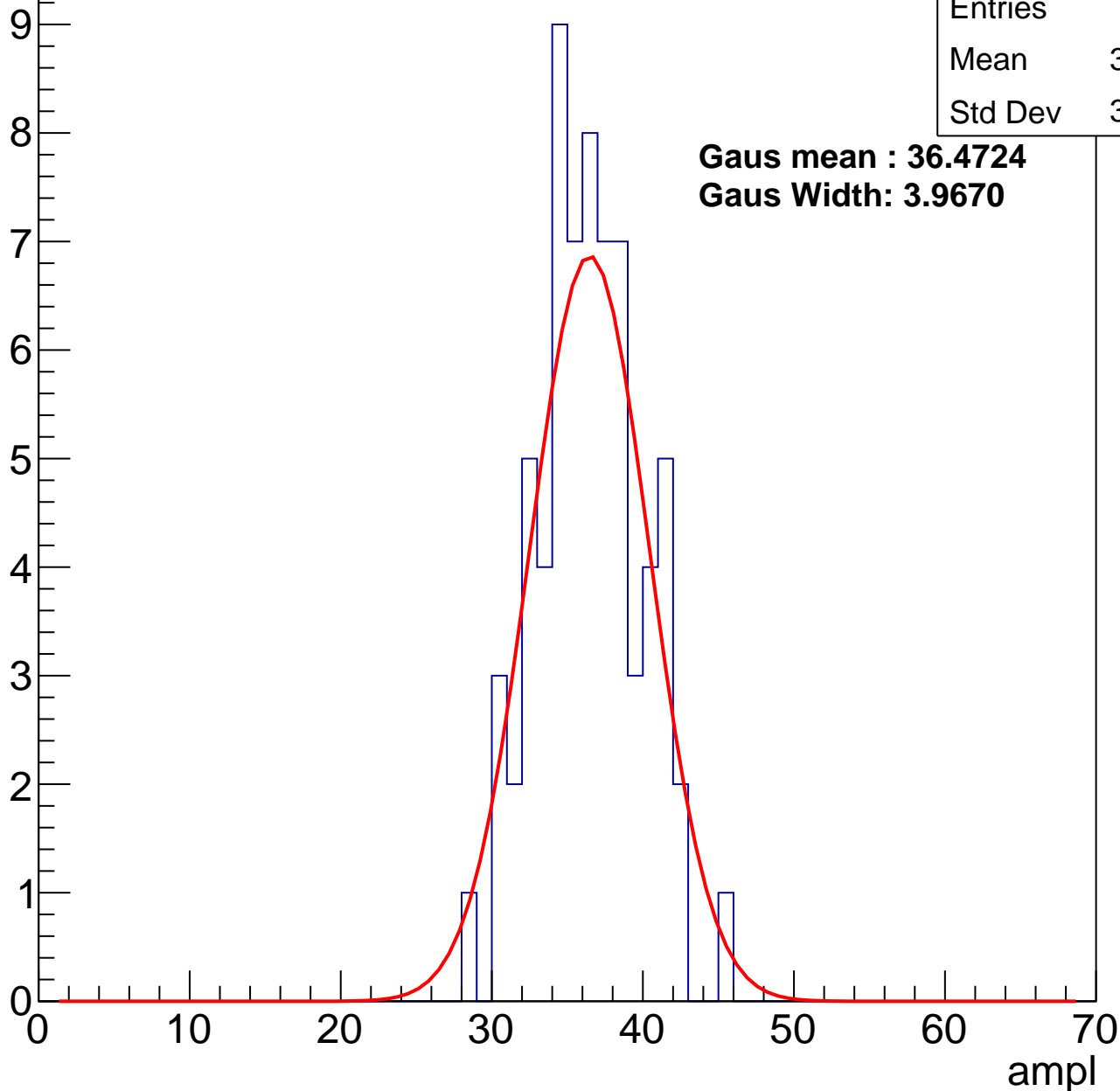
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	35.99
Std Dev	3.398

**Gaus mean : 36.4724**

**Gaus Width: 3.9670**



# B0L000S, U26-ch107, adc2

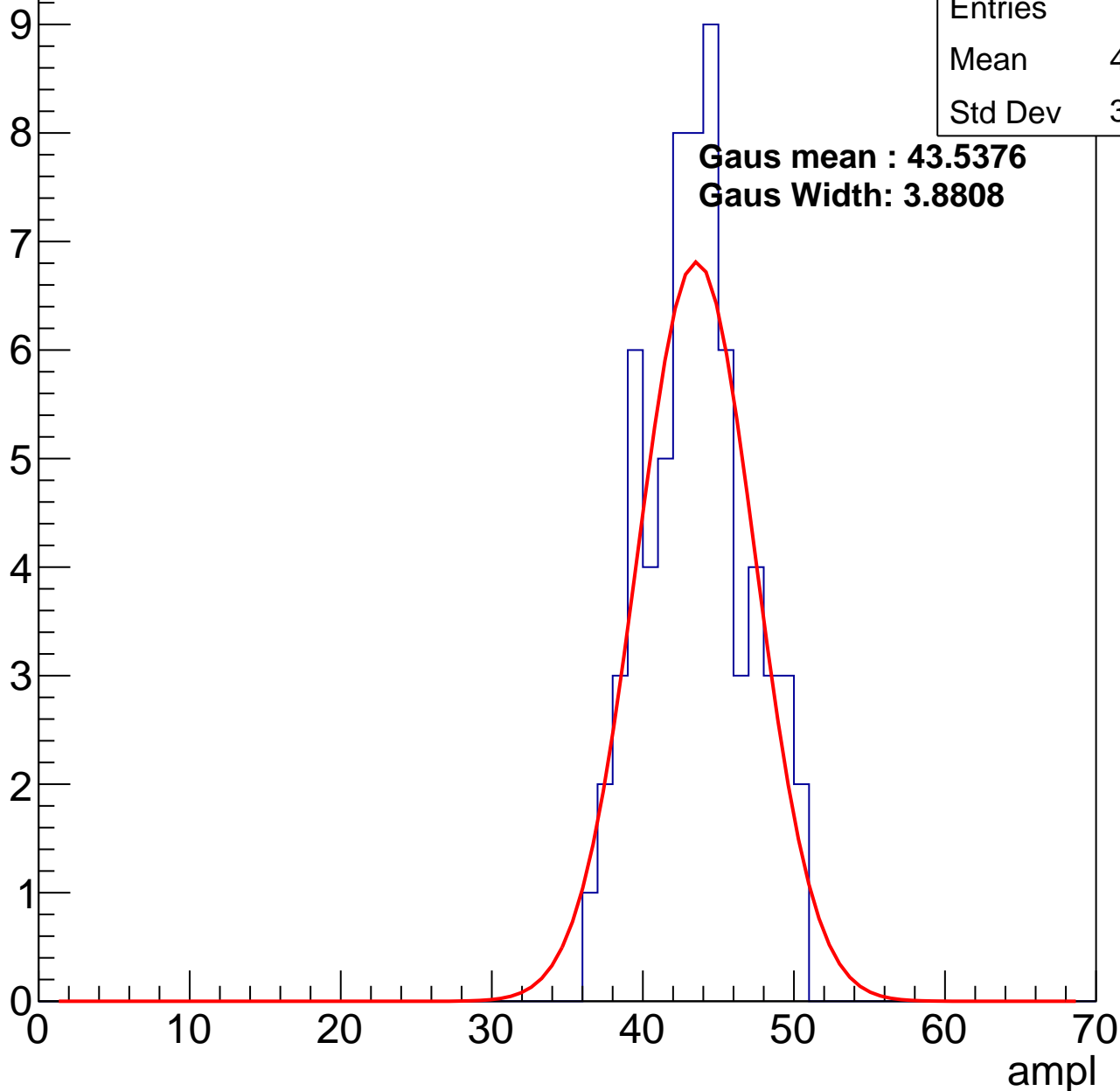
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	43.07
Std Dev	3.369

**Gaus mean : 43.5376**

**Gaus Width: 3.8808**

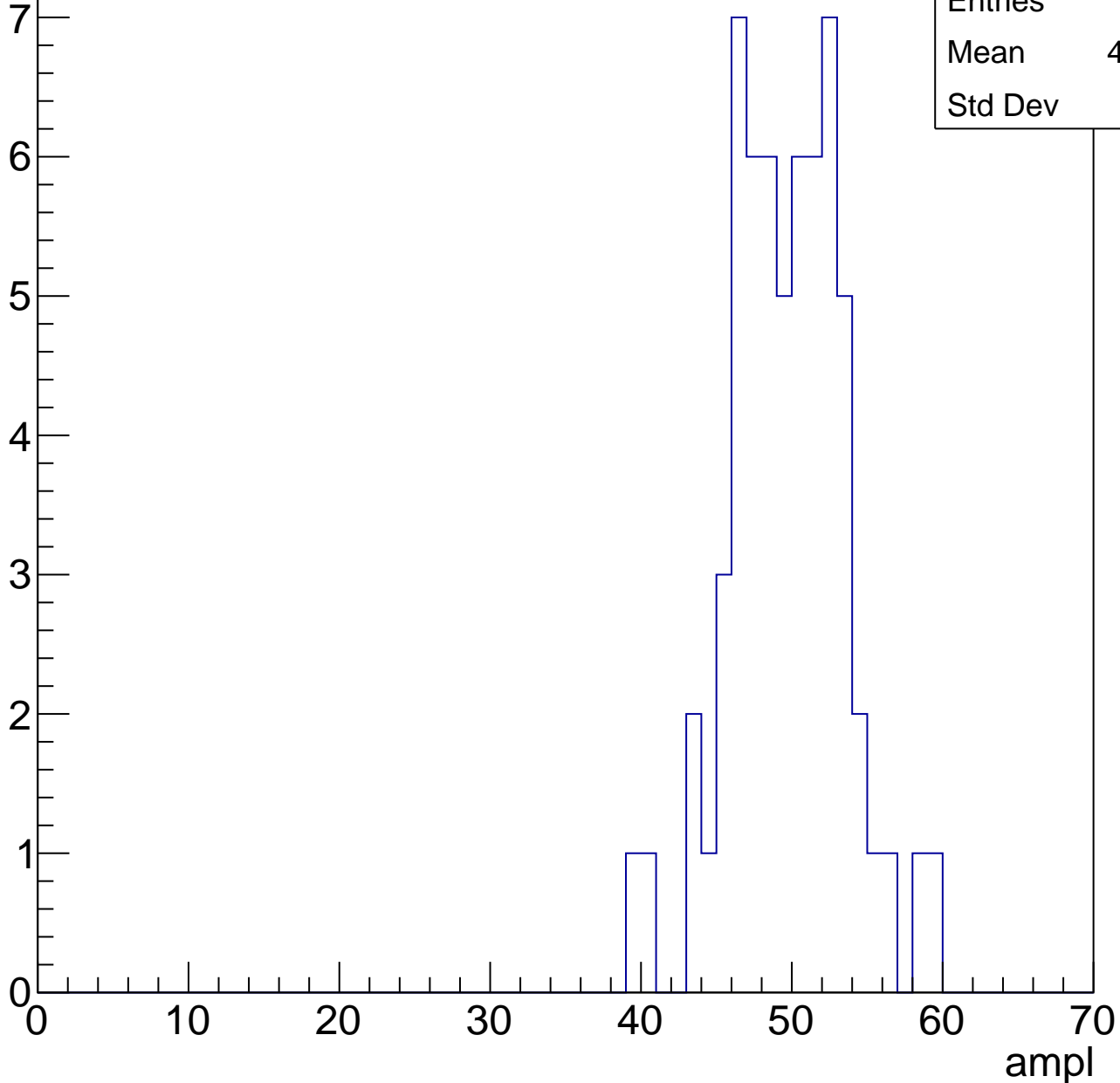


# B0L000S, U26-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

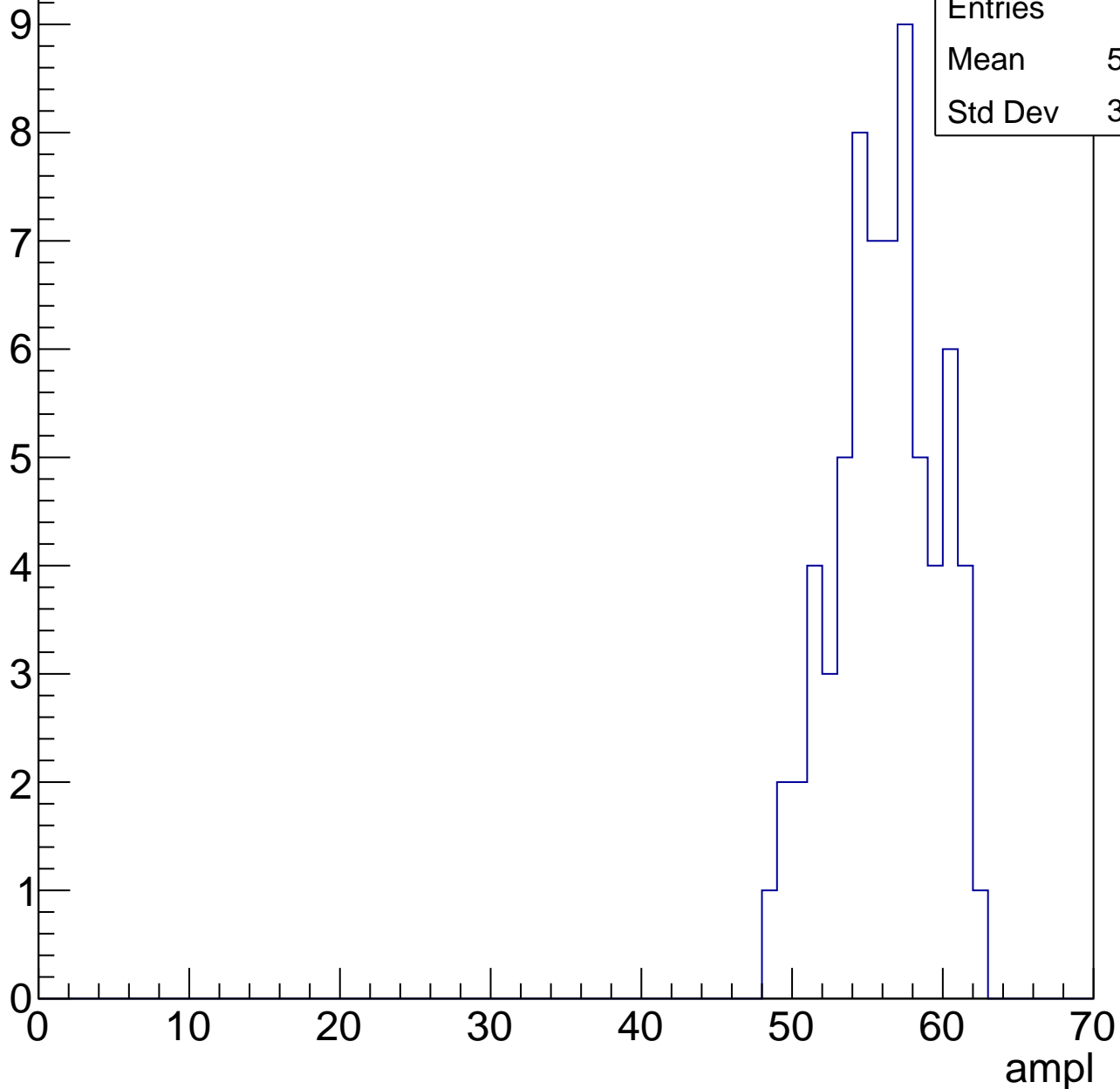
Entries	62
Mean	49.23
Std Dev	3.82



# B0L000S, U26-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

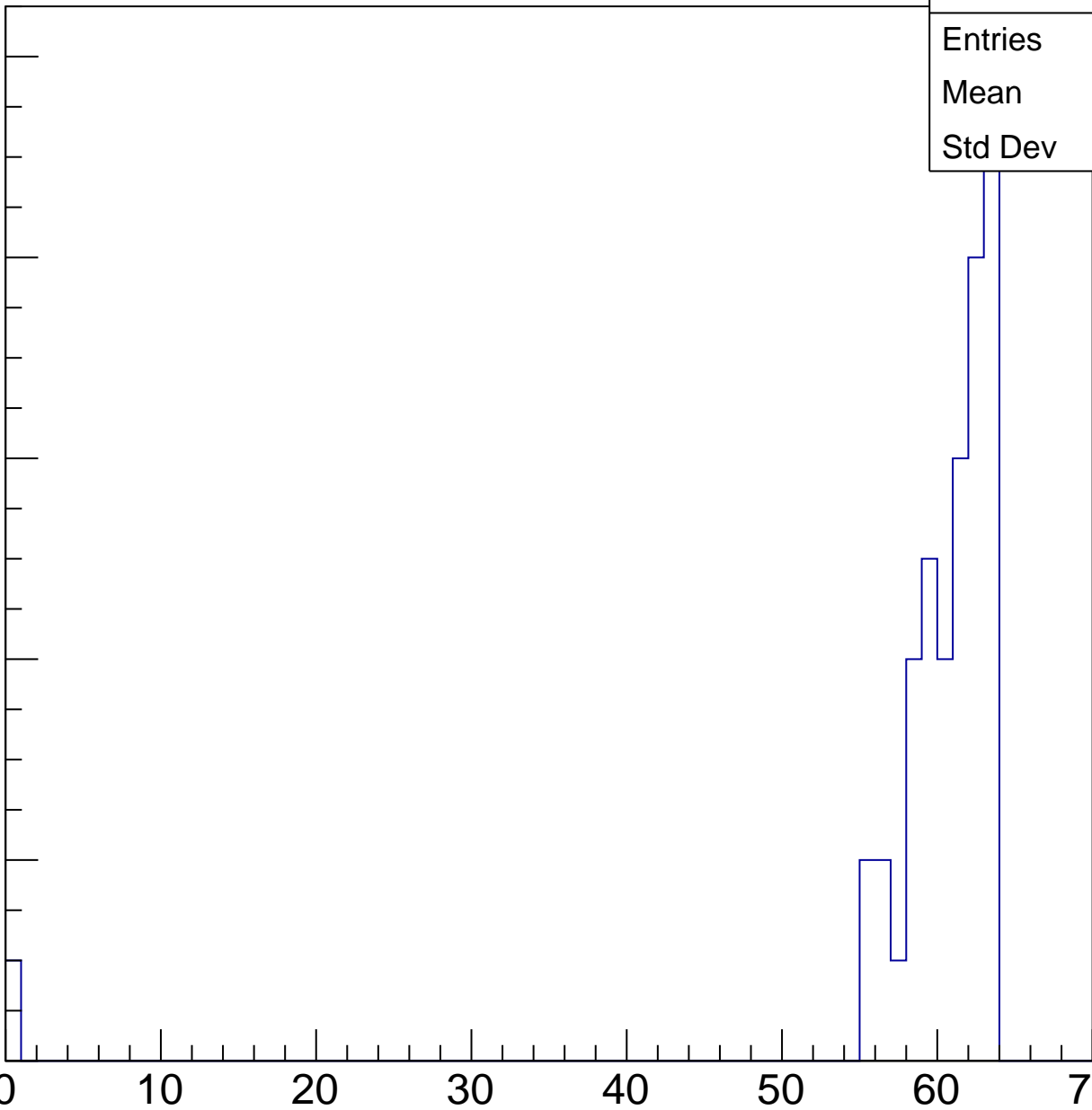
Entries	43
Mean	59.02
Std Dev	9.399

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch108, adc0

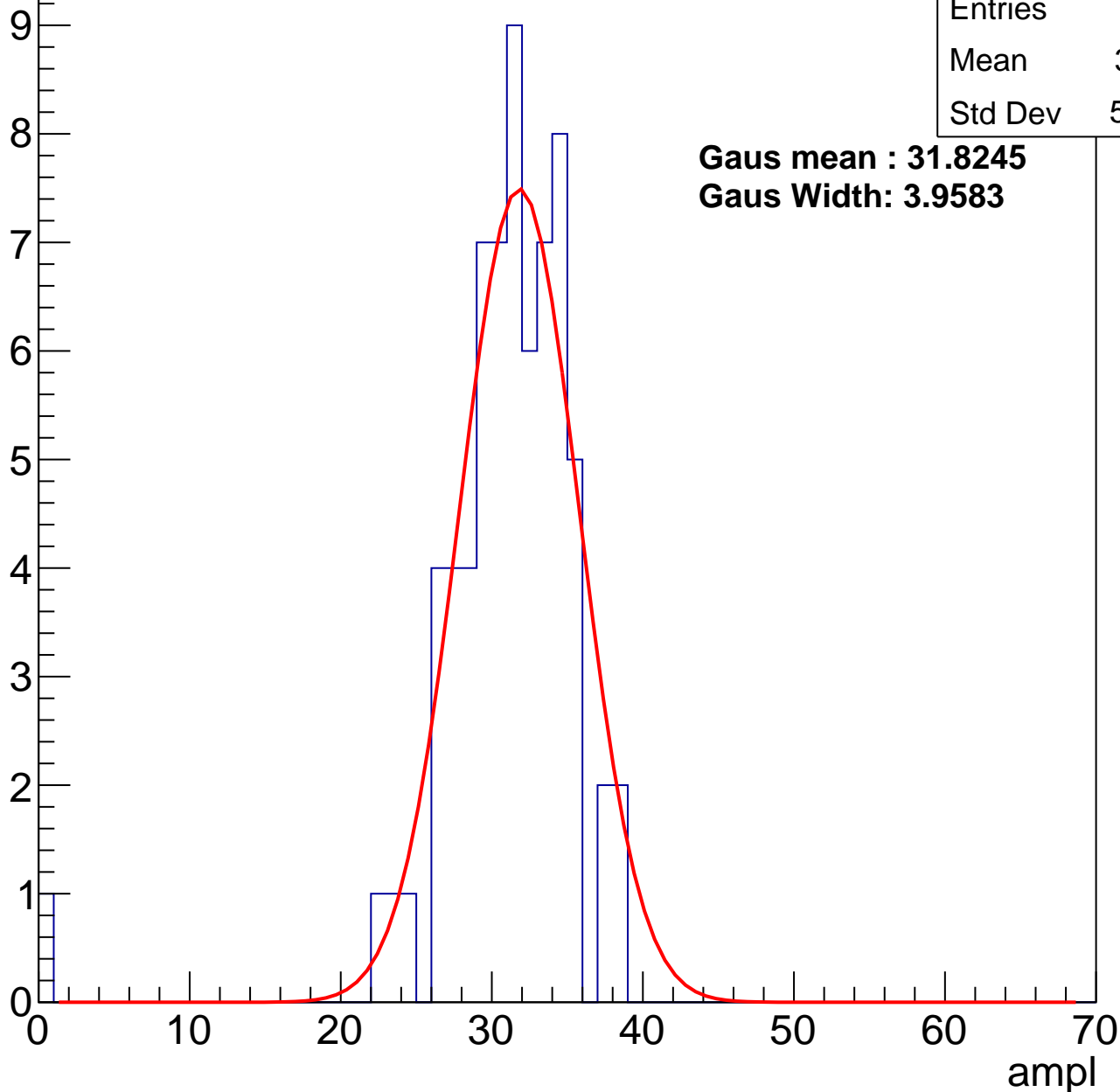
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.51
Std Dev	5.006

**Gaus mean : 31.8245**

**Gaus Width: 3.9583**



# B0L000S, U26-ch108, adc1

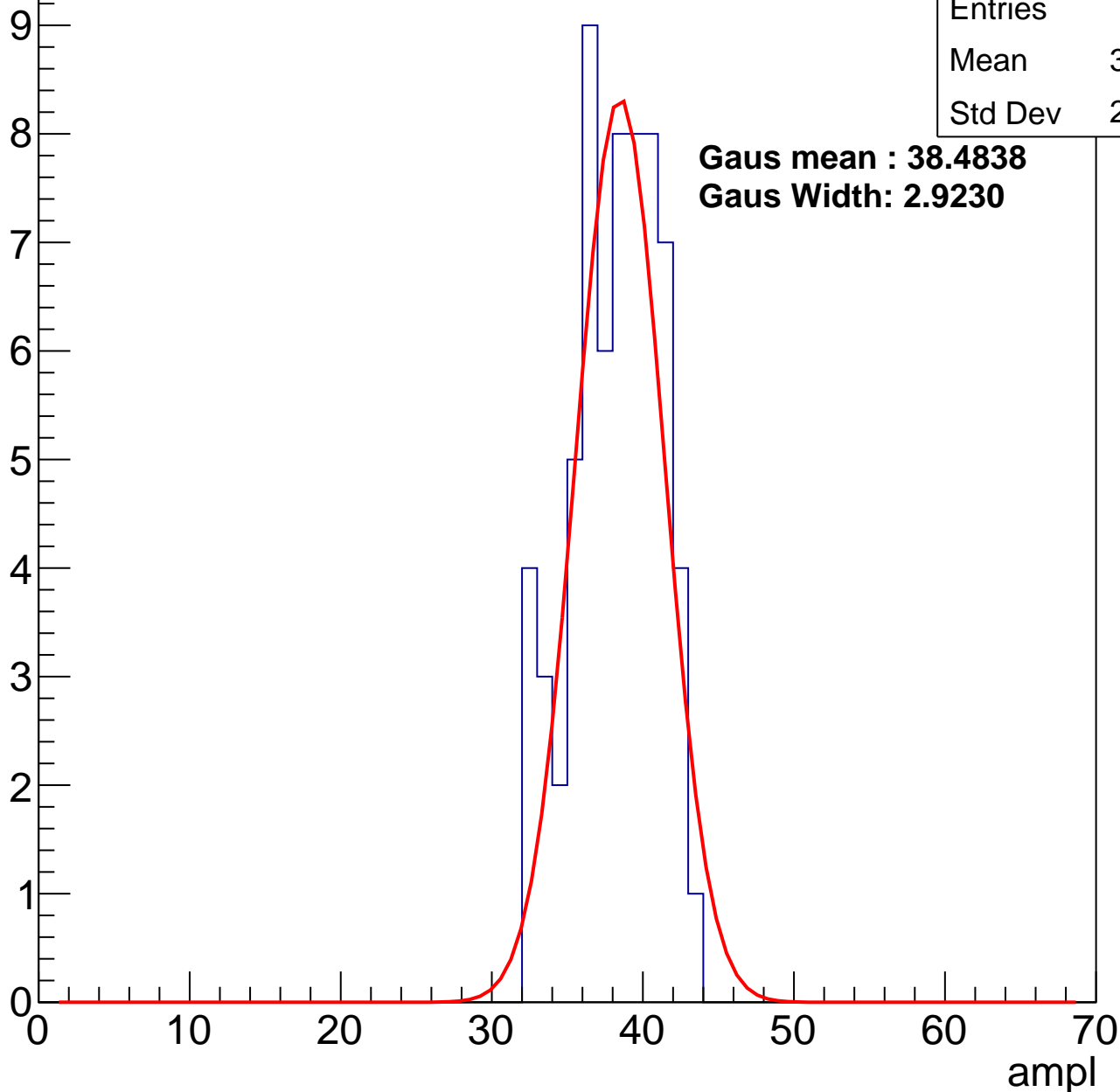
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.69
Std Dev	2.839

**Gaus mean : 38.4838**

**Gaus Width: 2.9230**



# B0L000S, U26-ch108, adc2

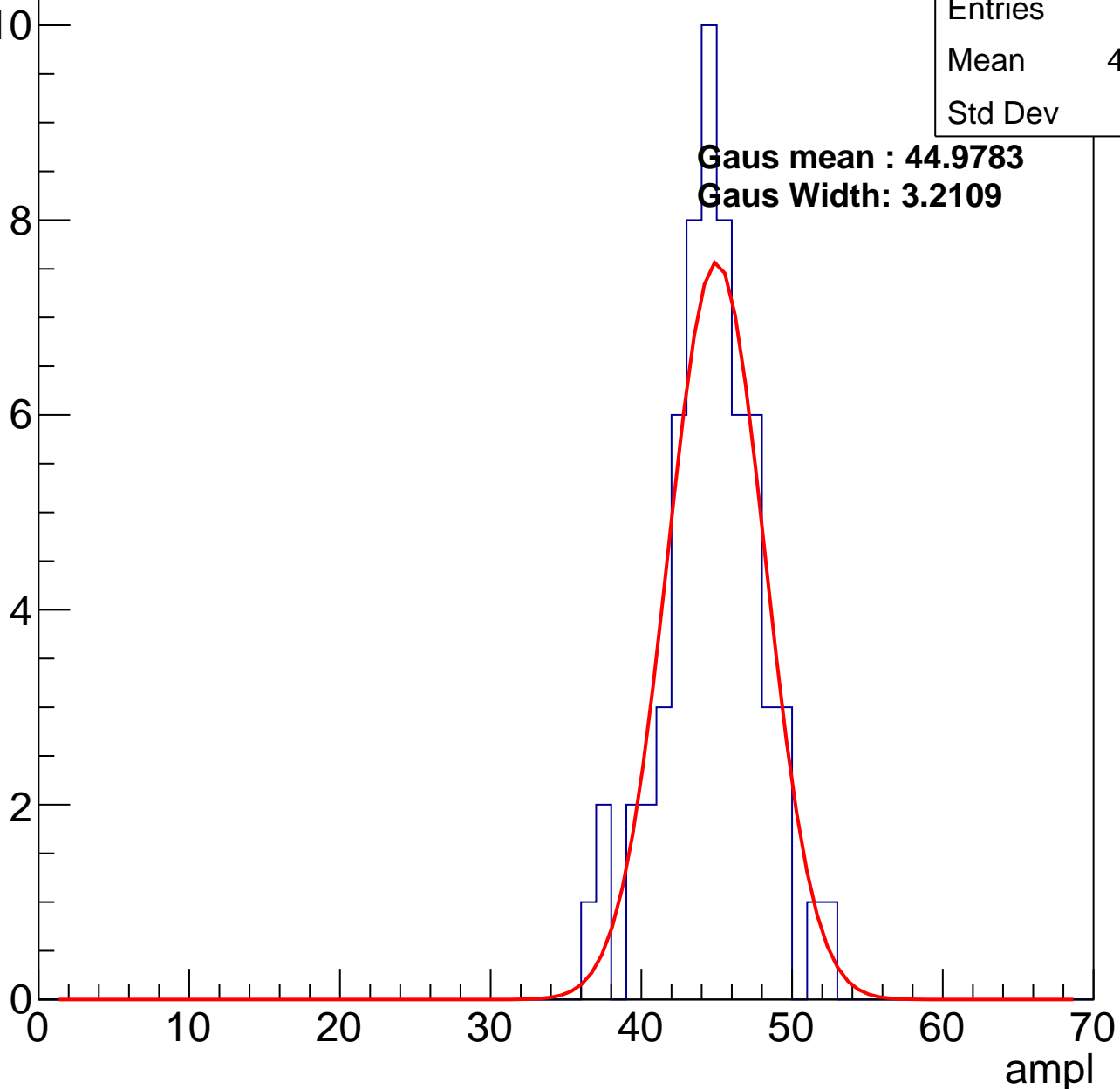
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	44.18
Std Dev	3.17

**Gaus mean : 44.9783**

**Gaus Width: 3.2109**



# B0L000S, U26-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

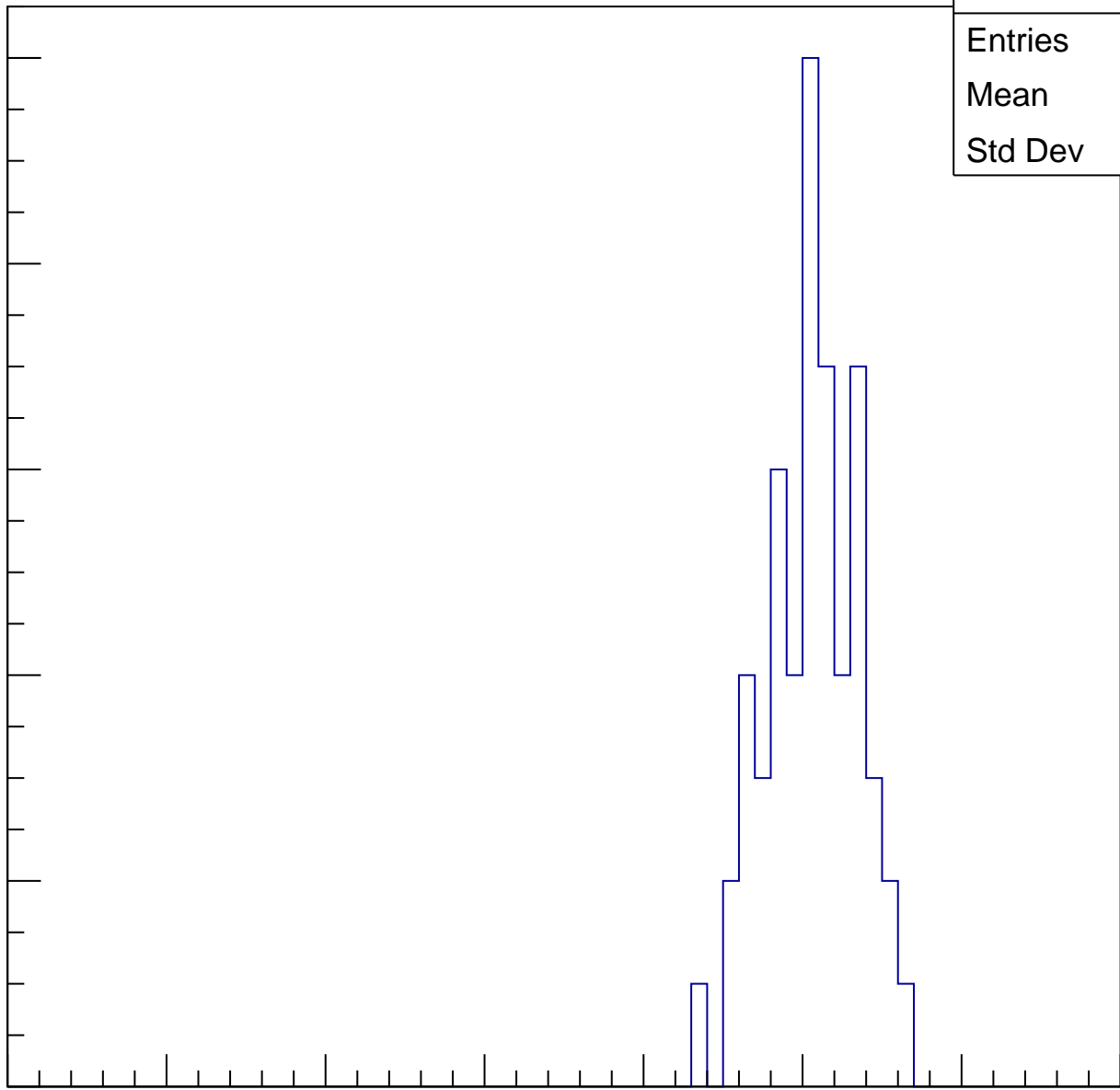
Entries	54
Mean	50.11
Std Dev	2.846

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

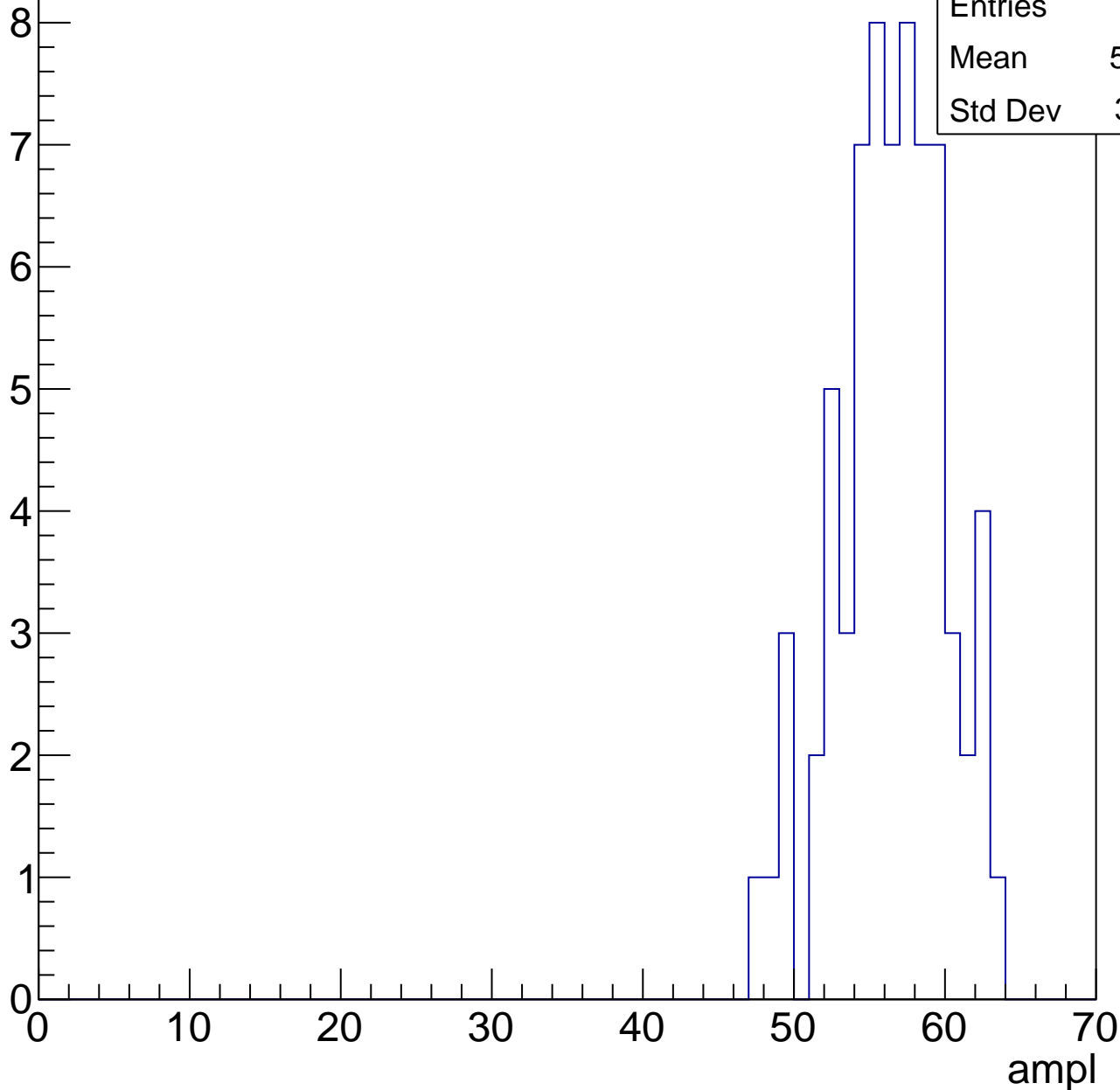


# B0L000S, U26-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	55.96
Std Dev	3.561

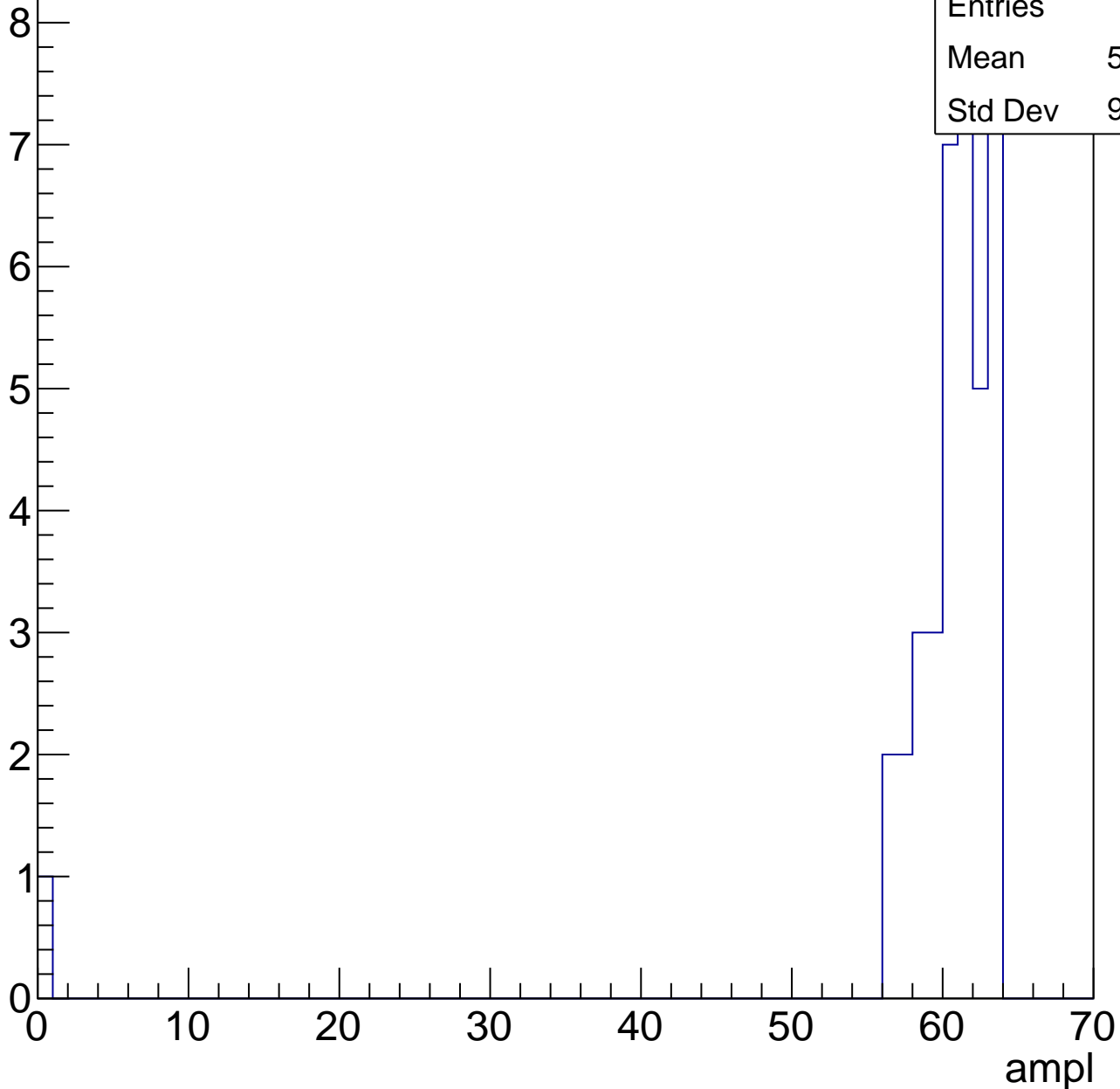


# B0L000S, U26-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.95
Std Dev	9.769



# B0L000S, U26-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	62.5
Std Dev	0.5



# B0L000S, U26-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch109, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	30.61
Std Dev	3.321

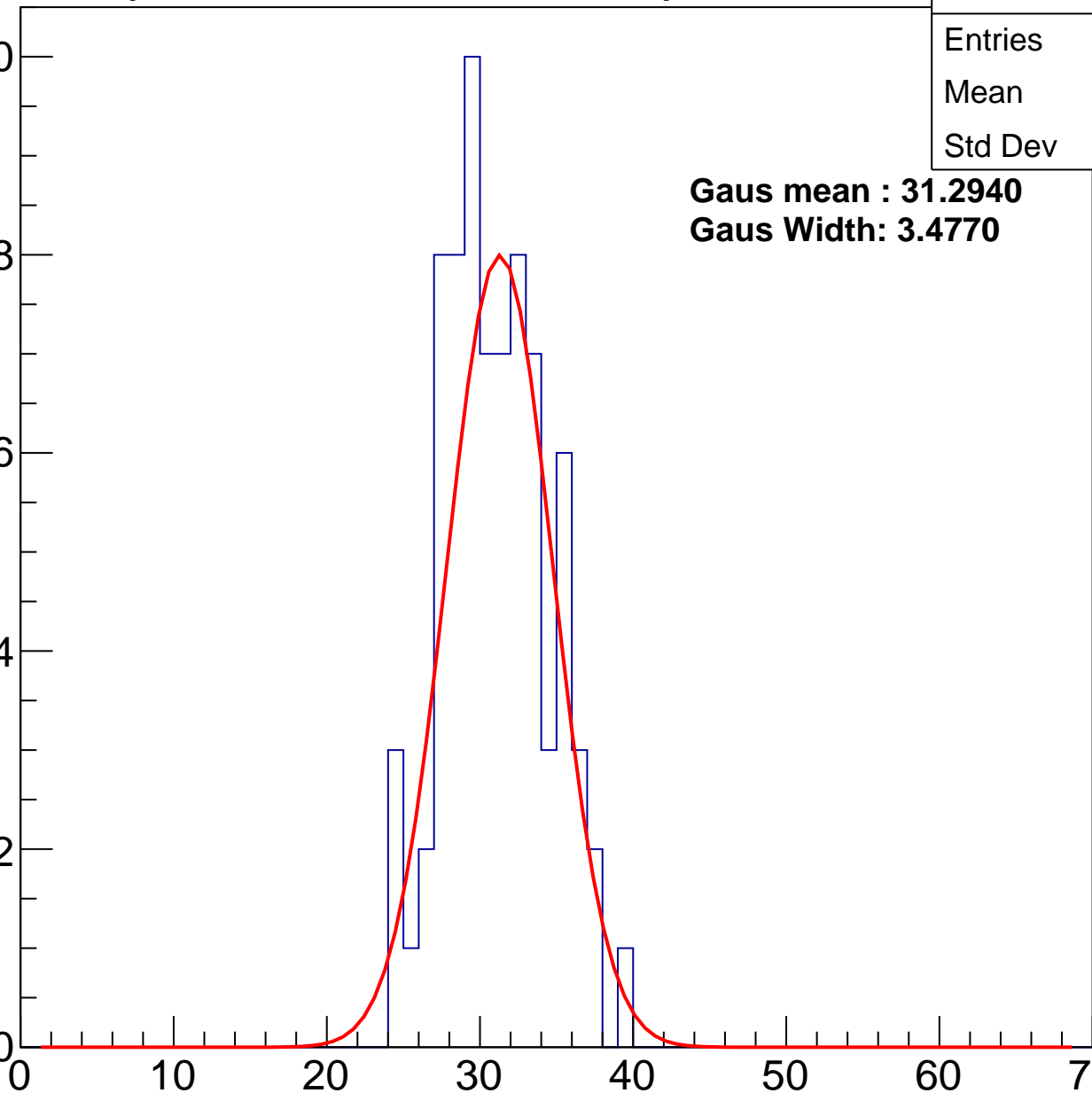
**Gaus mean : 31.2940**

**Gaus Width: 3.4770**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch109, adc1

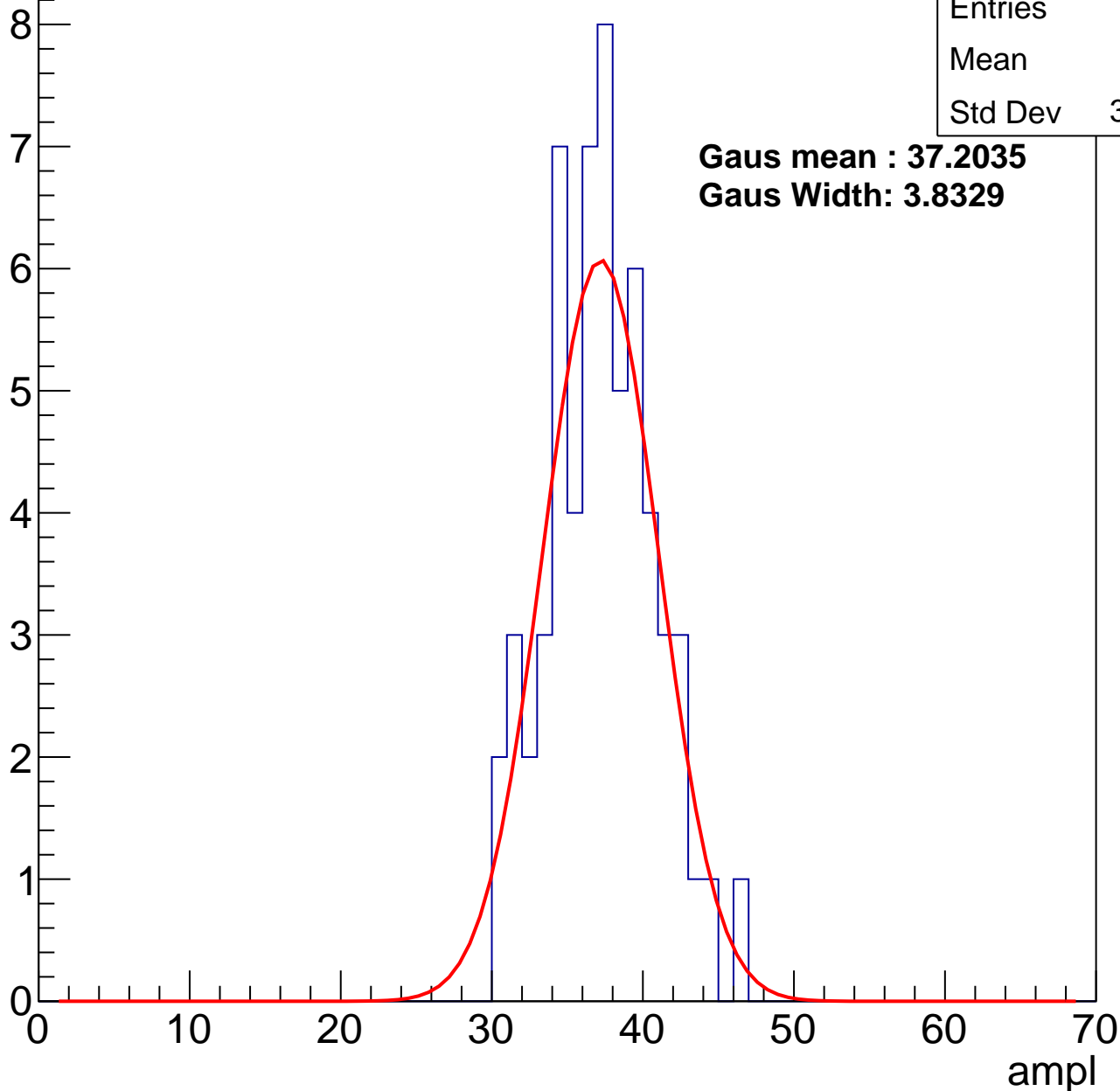
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.8
Std Dev	3.516

**Gaus mean : 37.2035**

**Gaus Width: 3.8329**



# B0L000S, U26-ch109, adc2

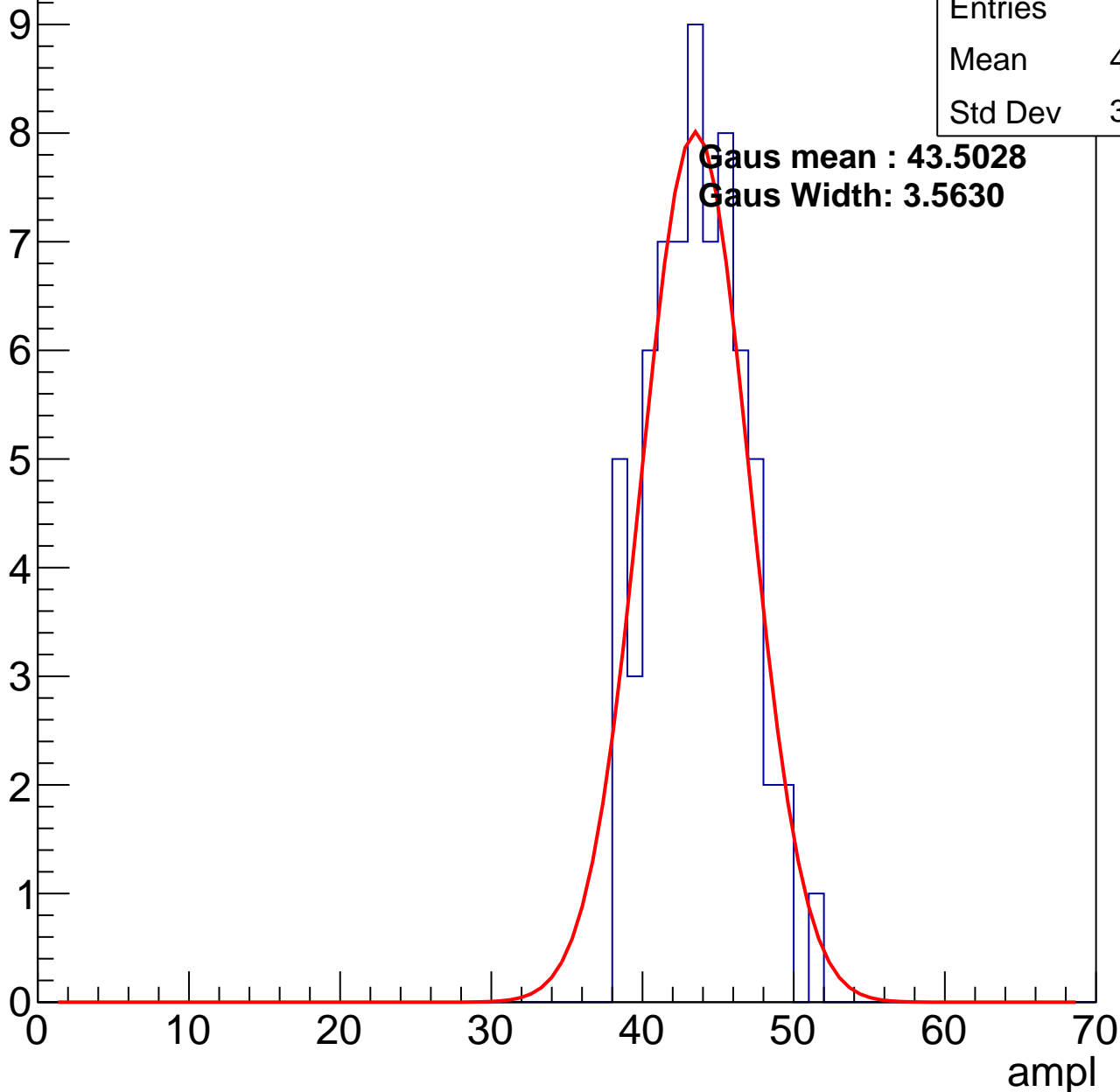
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.22
Std Dev	3.014

**Gaus mean : 43.5028**

**Gaus Width: 3.5630**

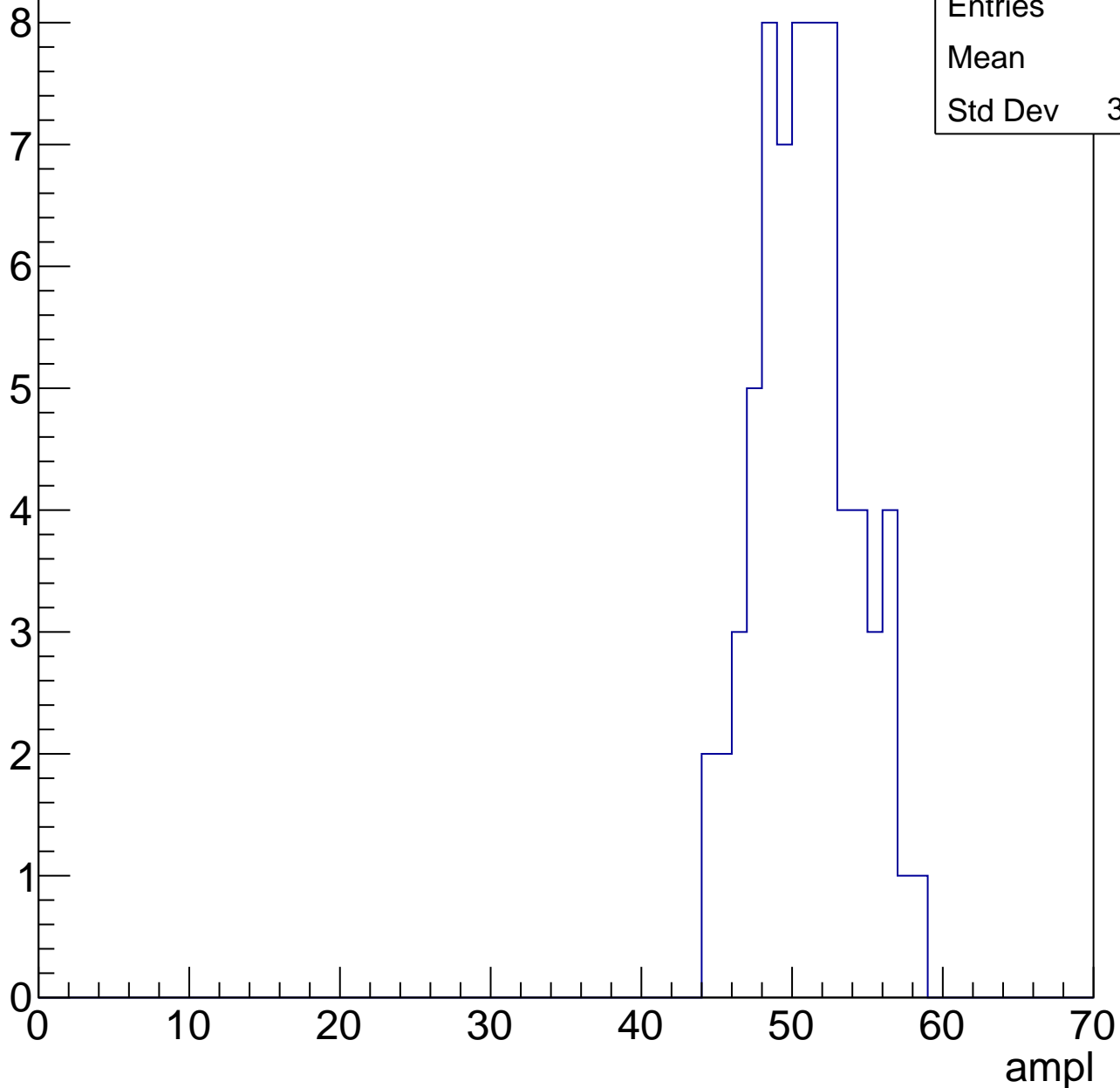


# B0L000S, U26-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	50.5
Std Dev	3.229

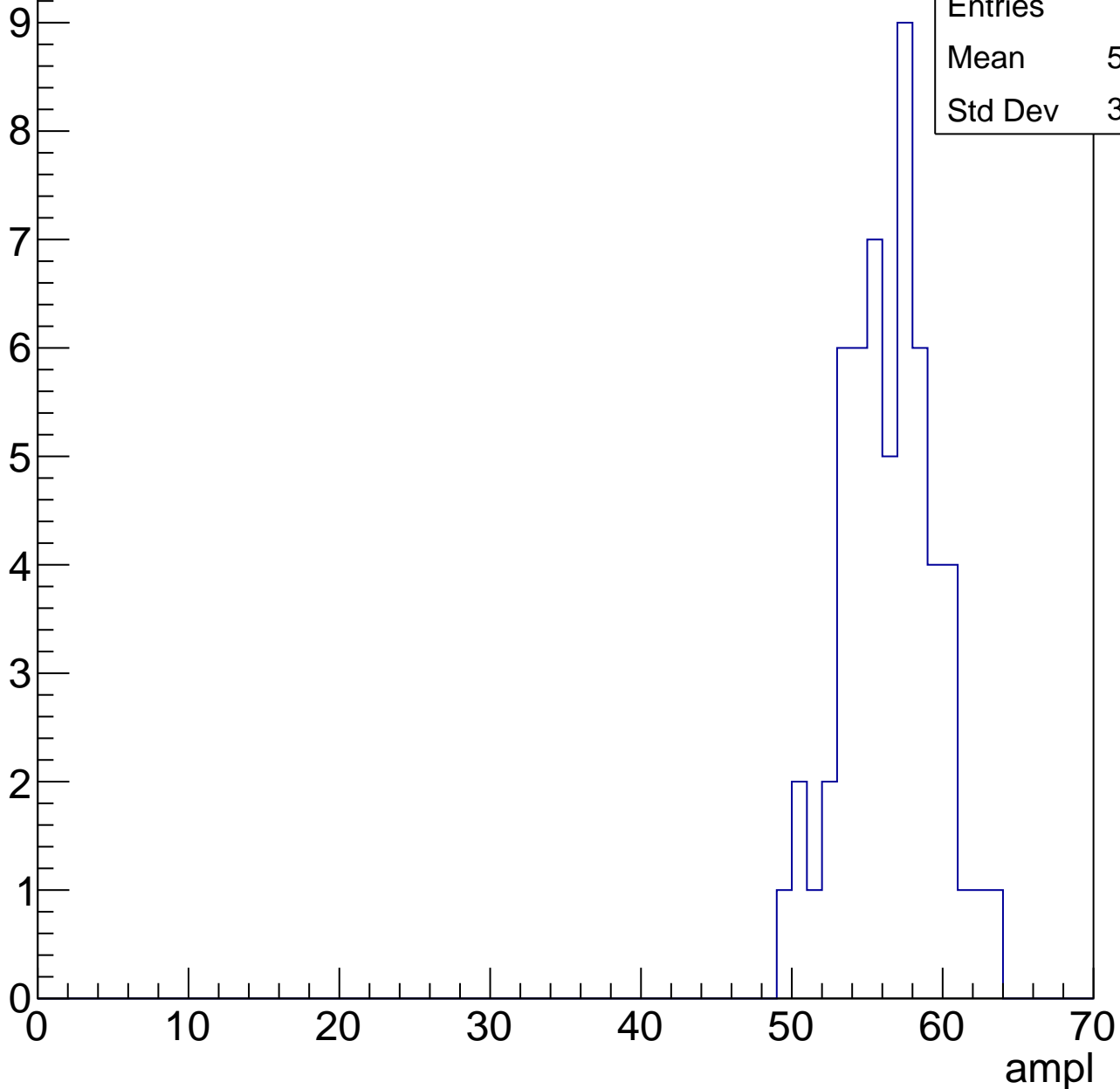


# B0L000S, U26-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.96
Std Dev	3.006

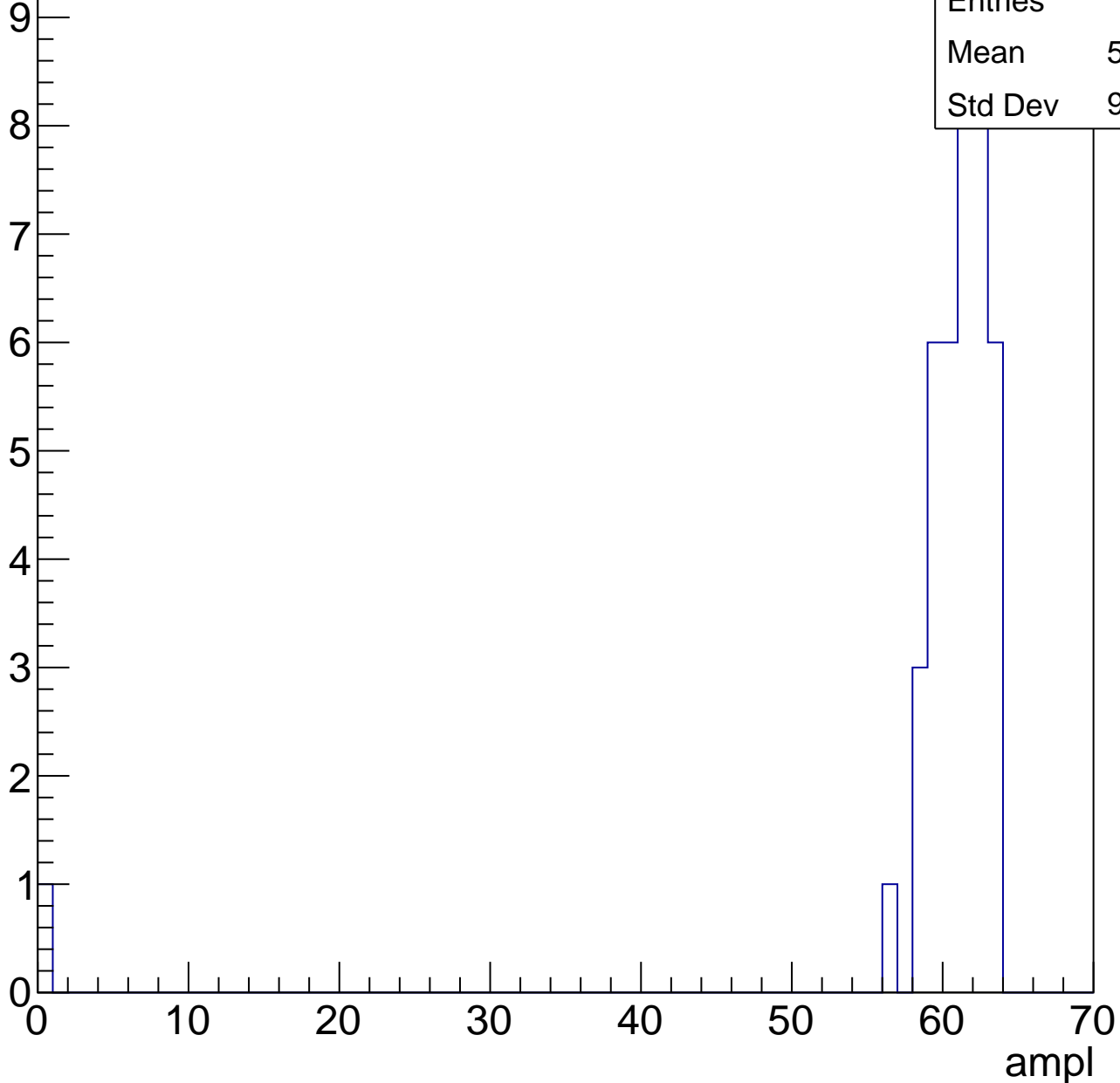


# B0L000S, U26-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	59.24
Std Dev	9.512



# B0L000S, U26-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	29.99
Std Dev	4.744

**Gaus mean : 30.3307**

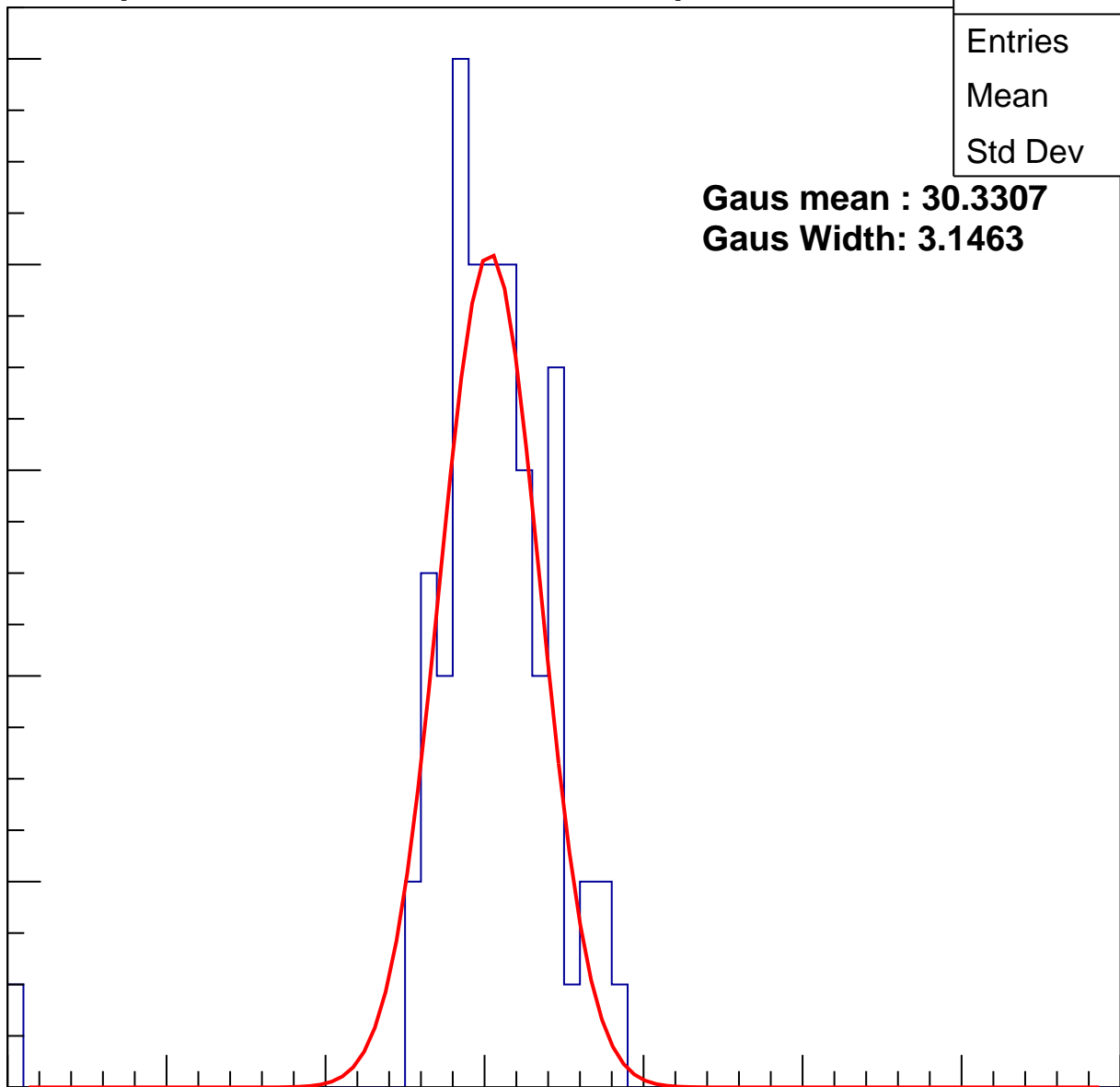
**Gaus Width: 3.1463**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch110, adc1

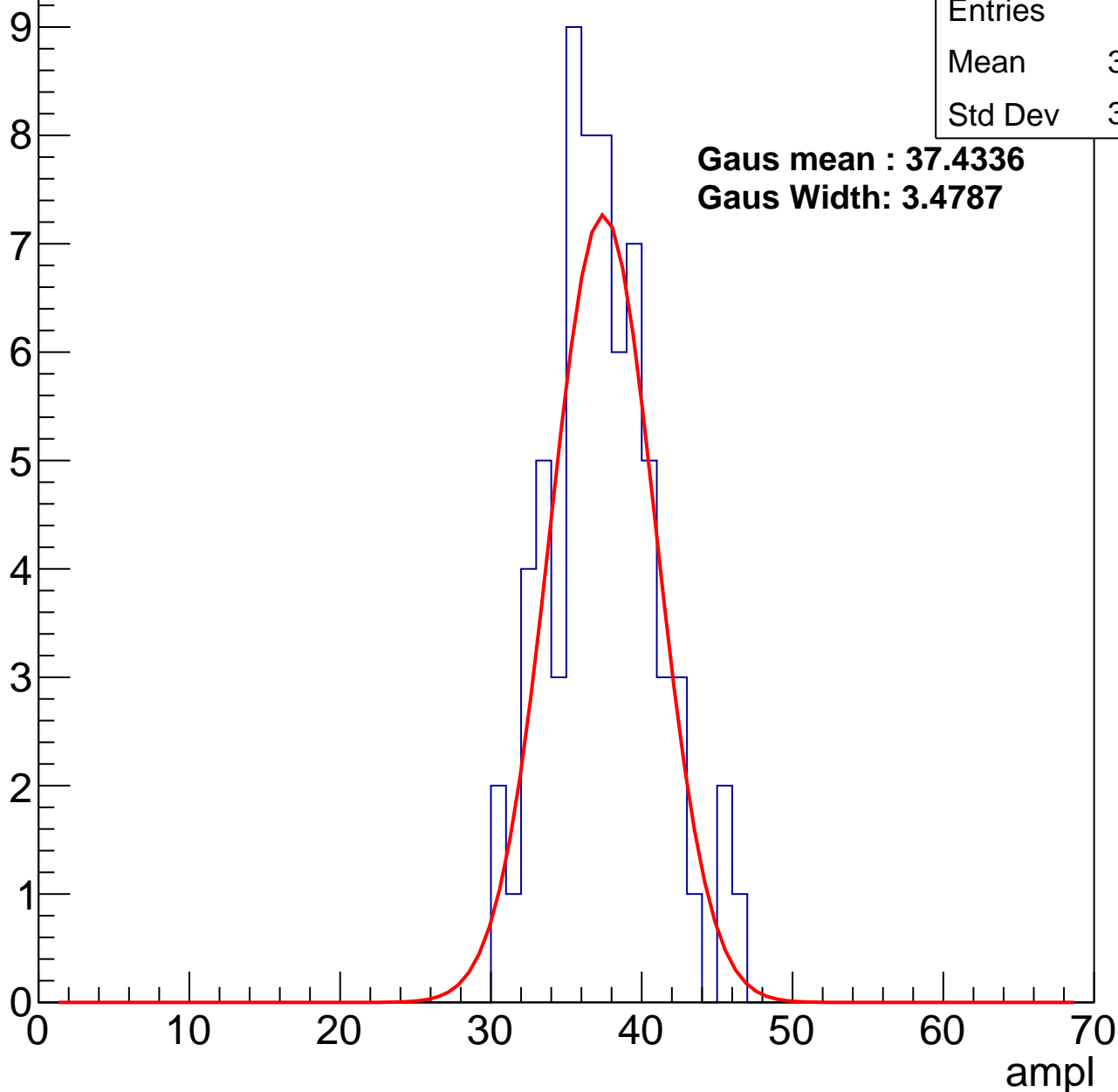
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.97
Std Dev	3.498

**Gaus mean : 37.4336**

**Gaus Width: 3.4787**



# B0L000S, U26-ch110, adc2

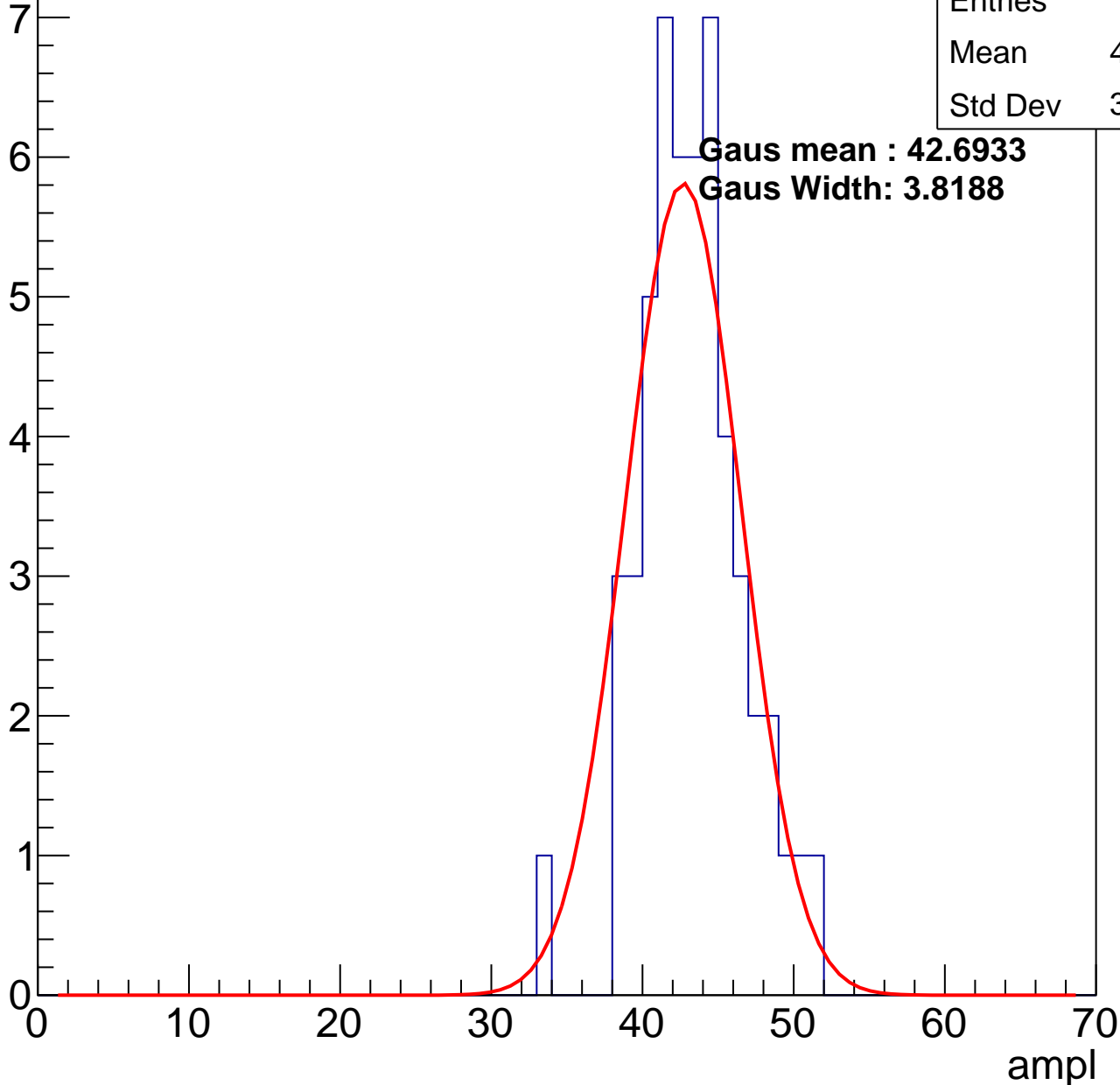
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	42.83
Std Dev	3.344

**Gaus mean : 42.6933**

**Gaus Width: 3.8188**

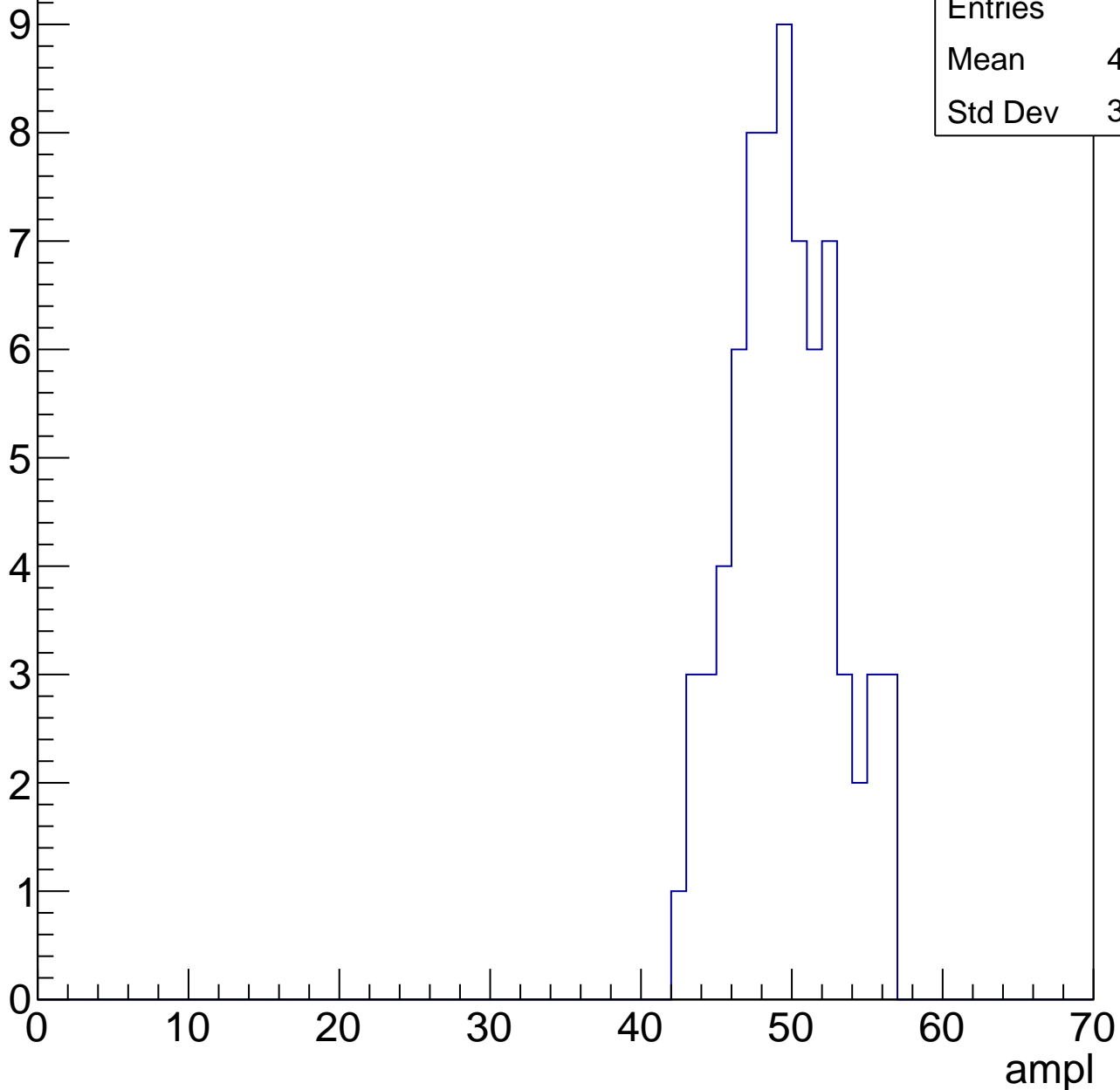


# B0L000S, U26-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	49.04
Std Dev	3.386

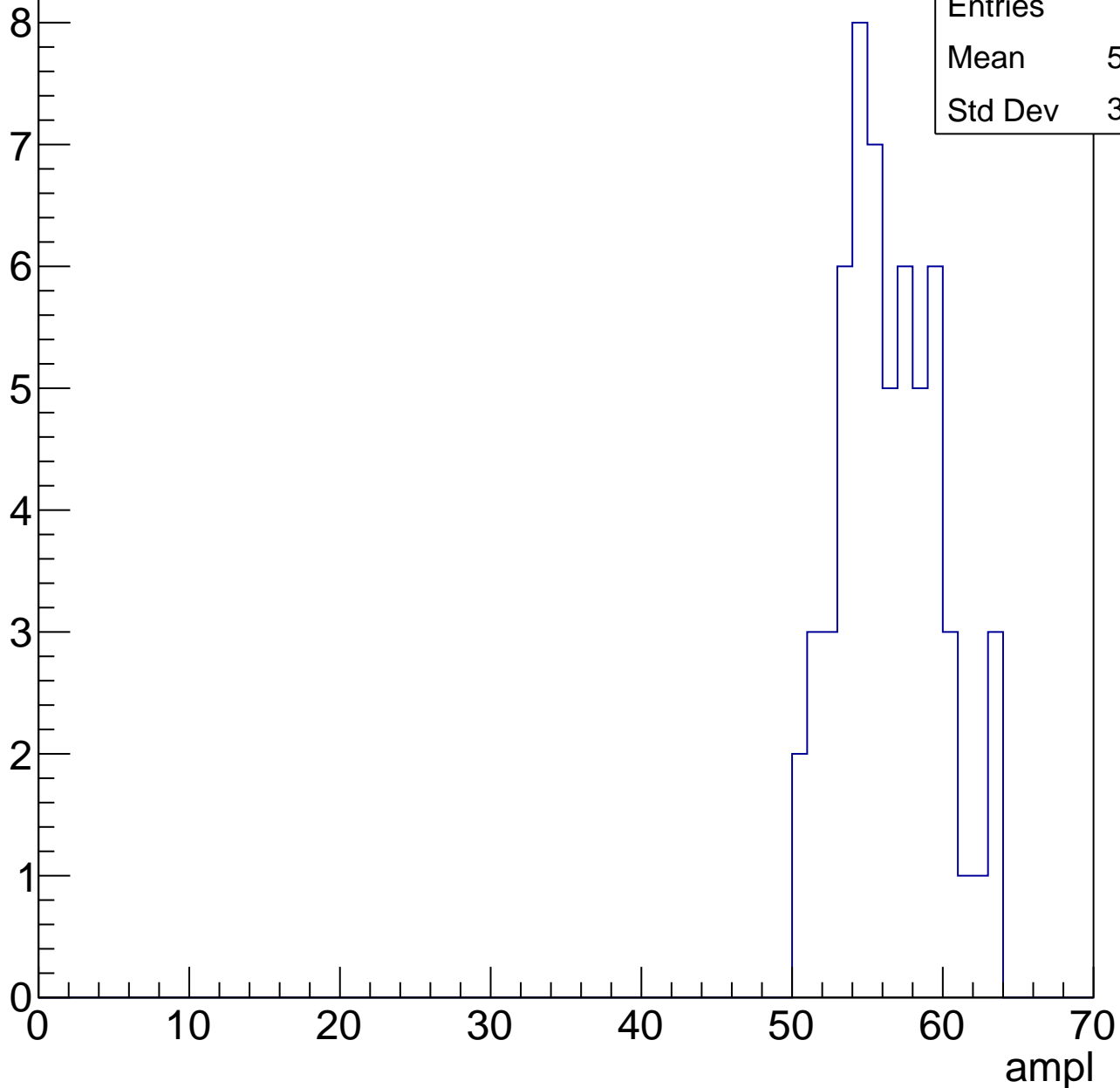


# B0L000S, U26-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

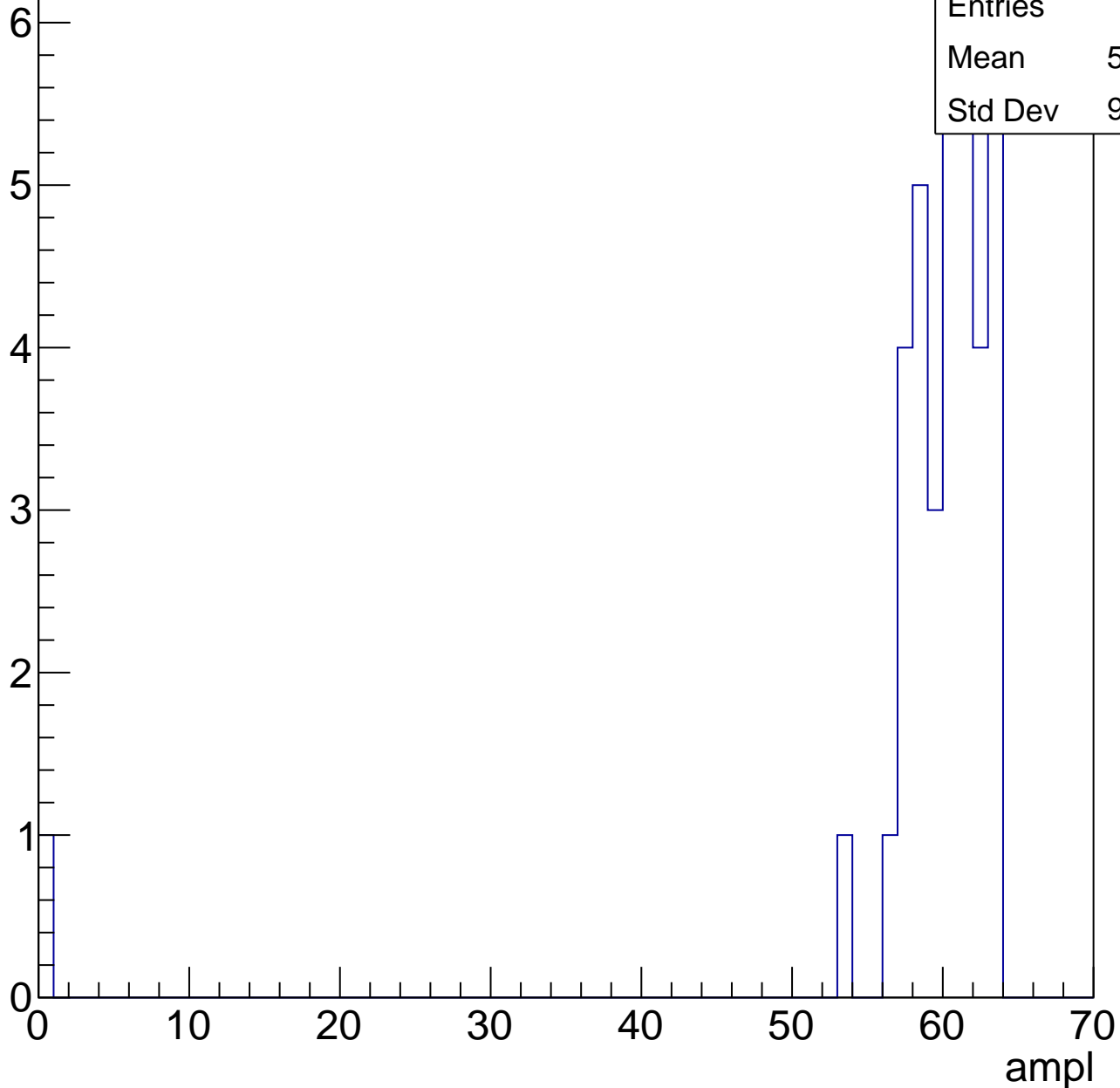
Entries	59
Mean	55.97
Std Dev	3.252



# B0L000S, U26-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

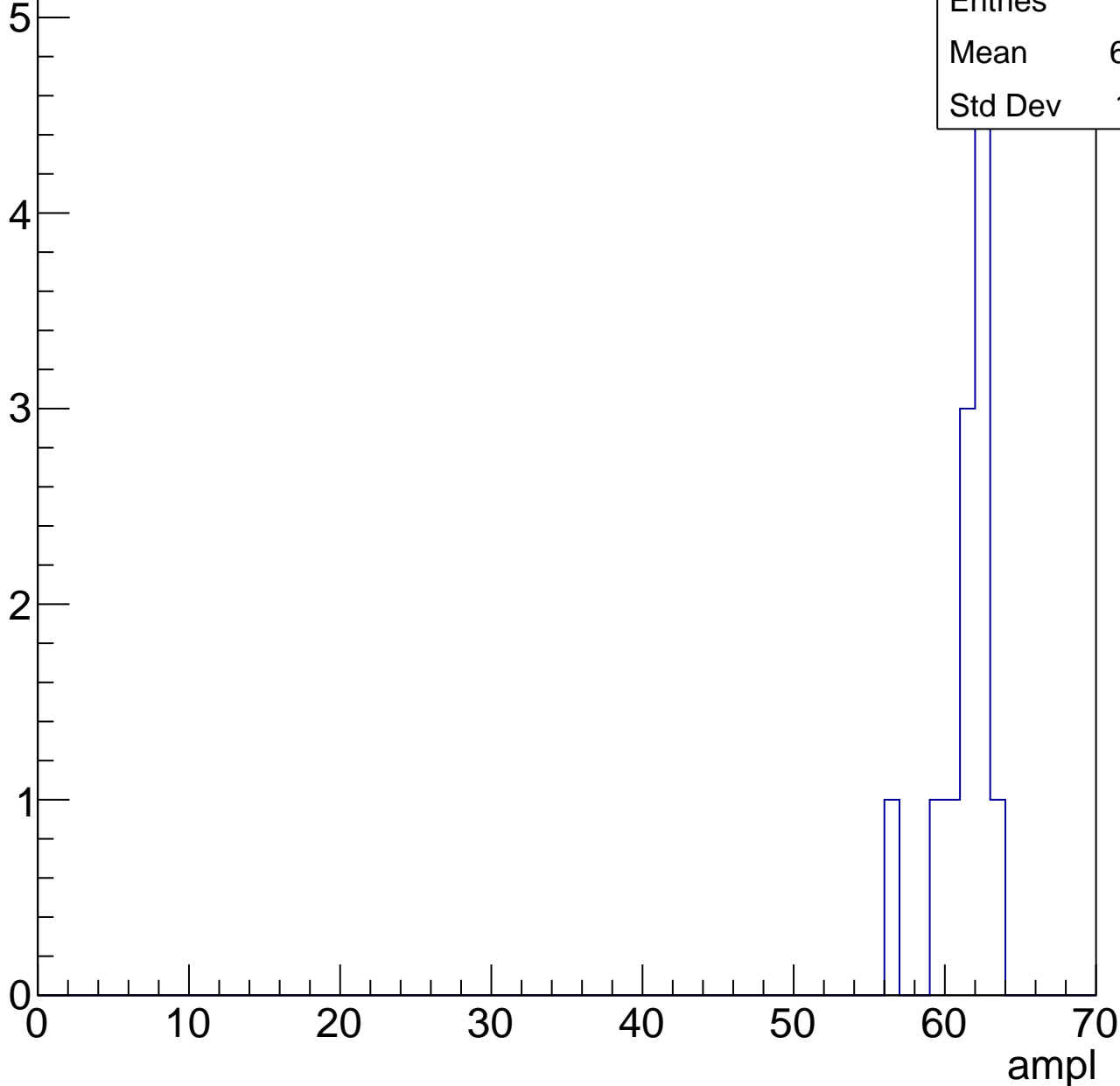


# B0L000S, U26-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	60.92
Std Dev	1.801





# B0L000S, U26-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U26-ch111, adc0

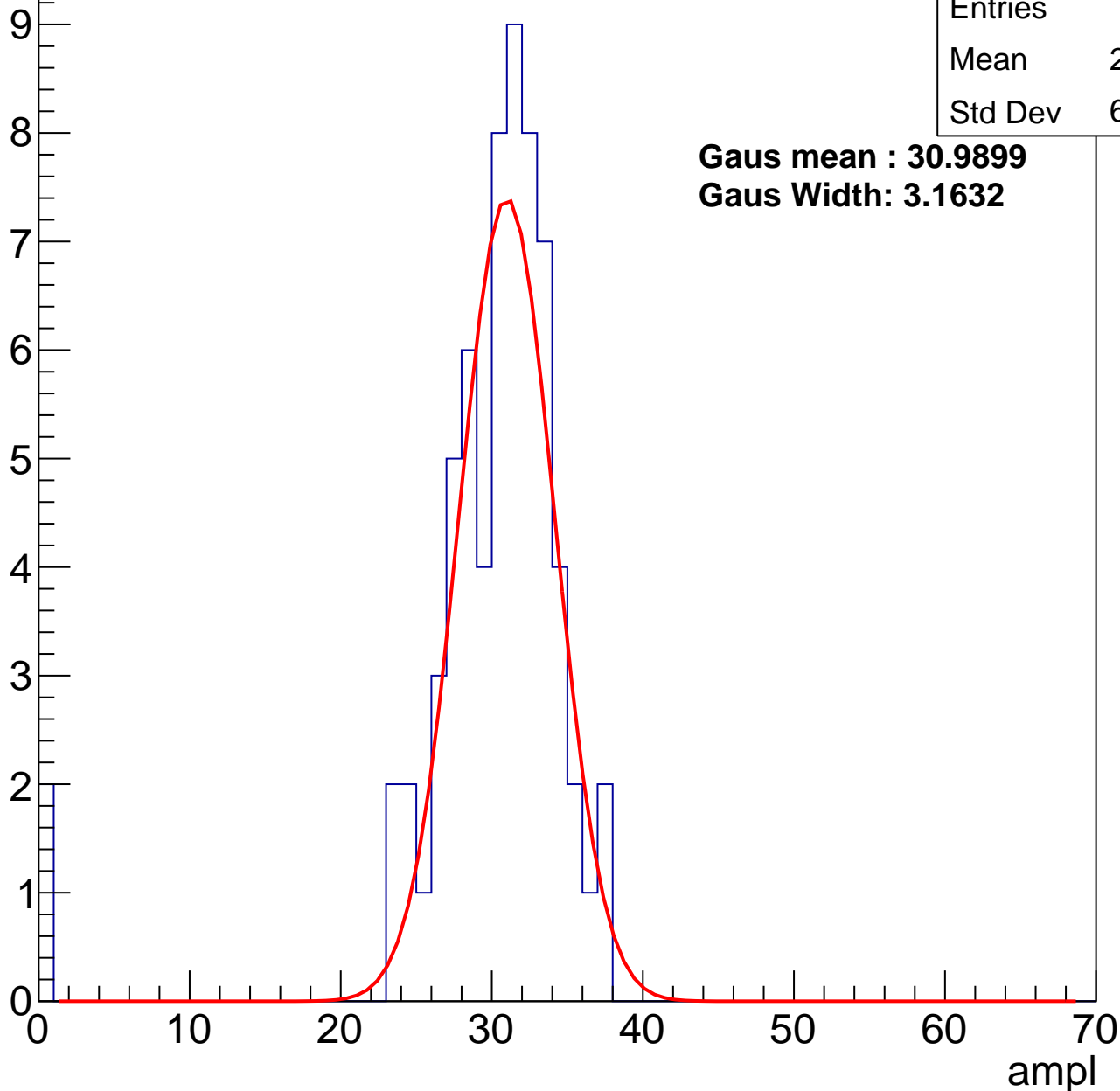
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.36
Std Dev	6.082

**Gaus mean : 30.9899**

**Gaus Width: 3.1632**



# B0L000S, U26-ch111, adc1

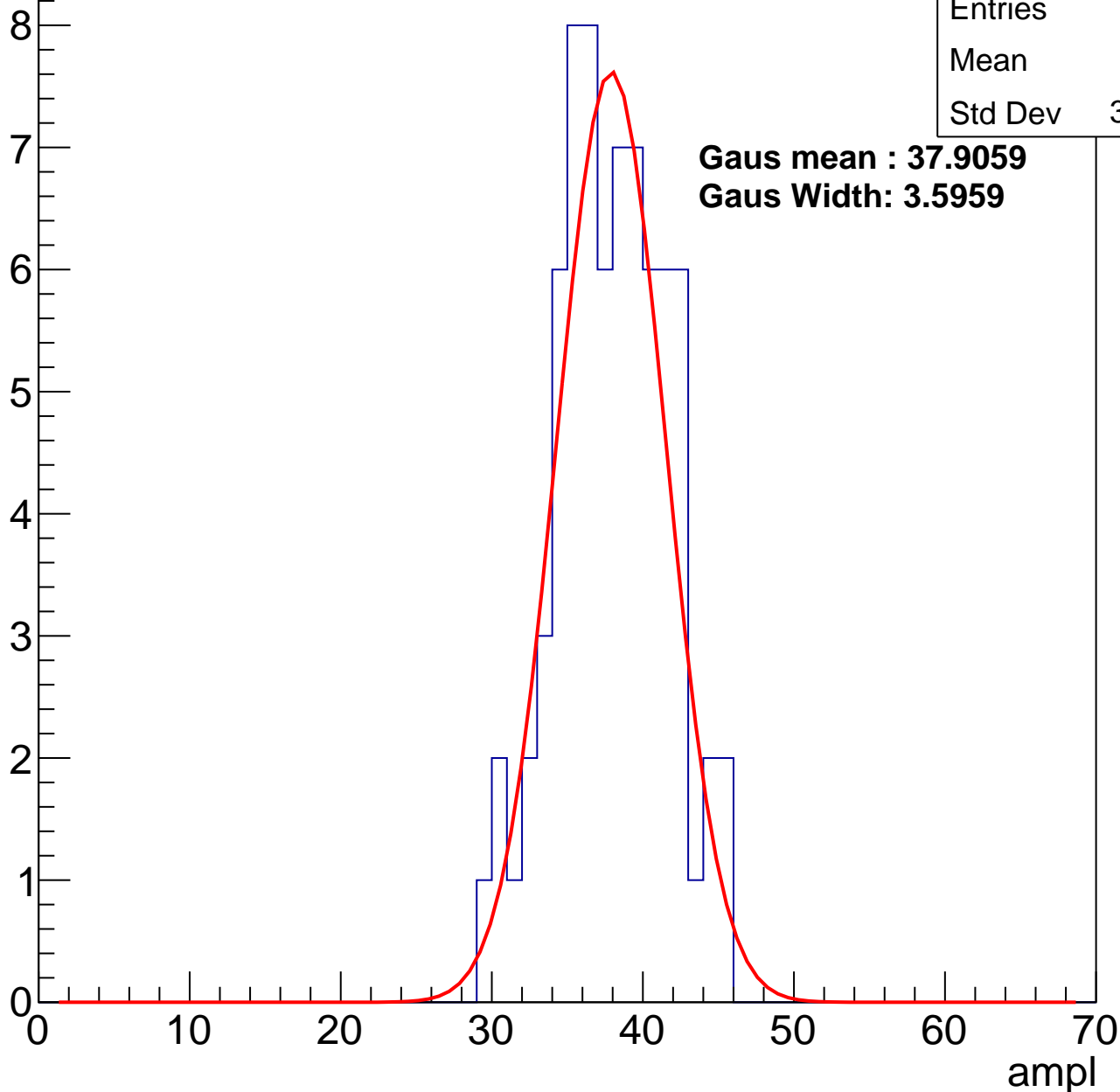
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.5
Std Dev	3.618

**Gaus mean : 37.9059**

**Gaus Width: 3.5959**



# B0L000S, U26-ch111, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.23
Std Dev	3.435

**Gaus mean : 45.2164**

**Gaus Width: 4.1619**

10

8

6

4

2

0

0

10

20

30

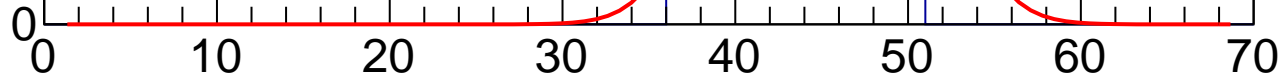
40

50

60

70

ampl

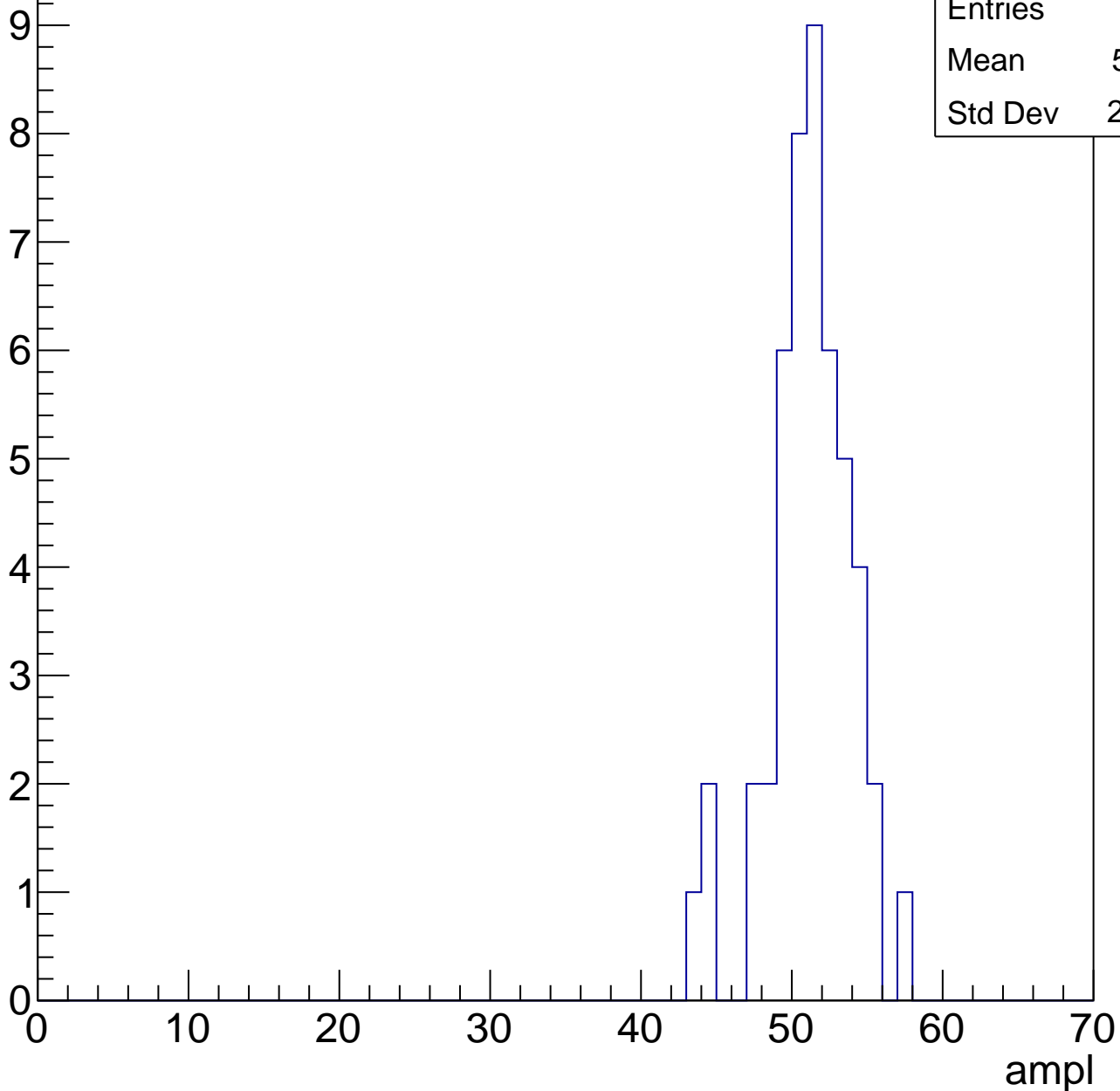


# B0L000S, U26-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	50.71
Std Dev	2.776

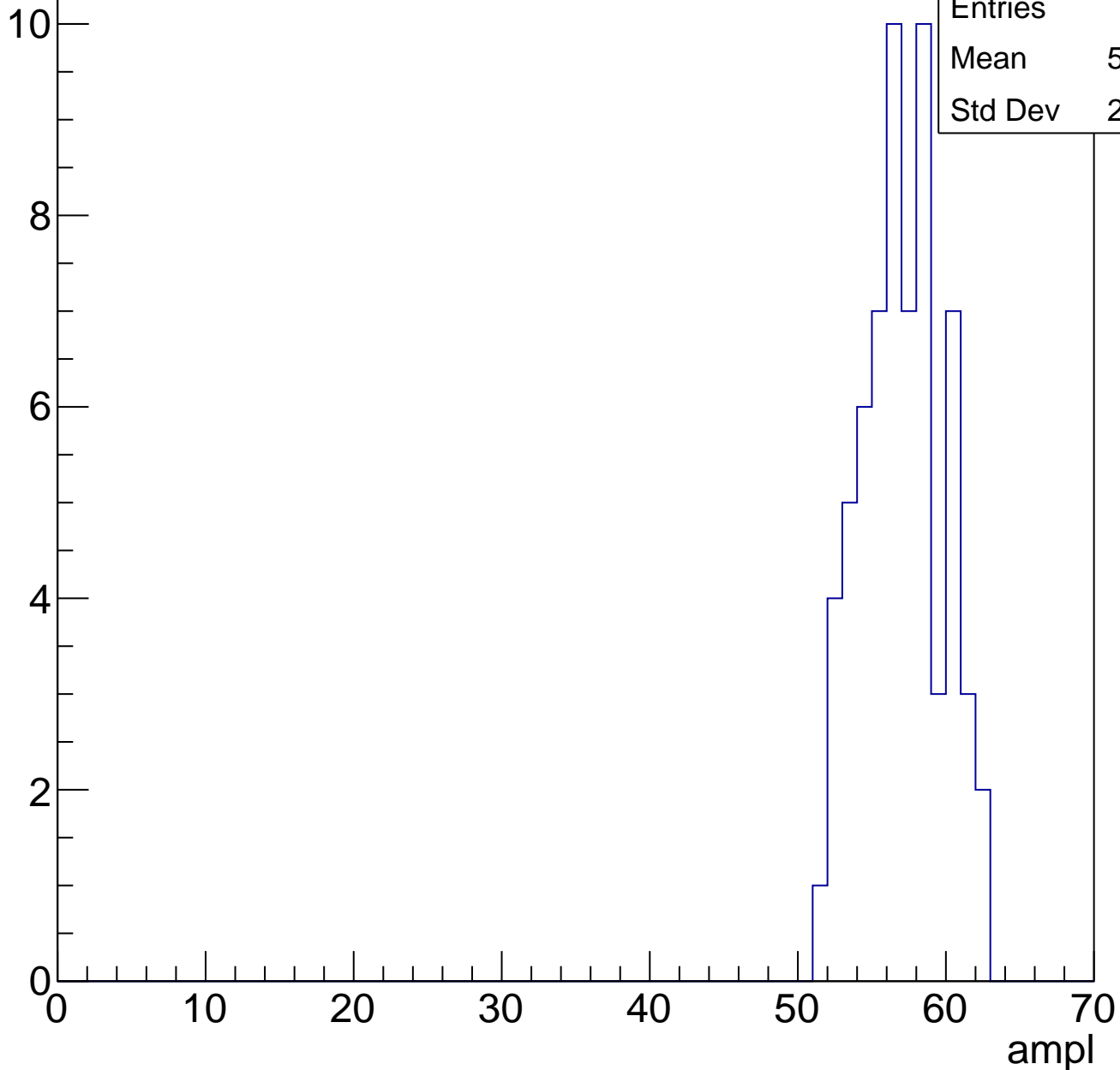


# B0L000S, U26-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	65
Mean	56.55
Std Dev	2.712

Entry

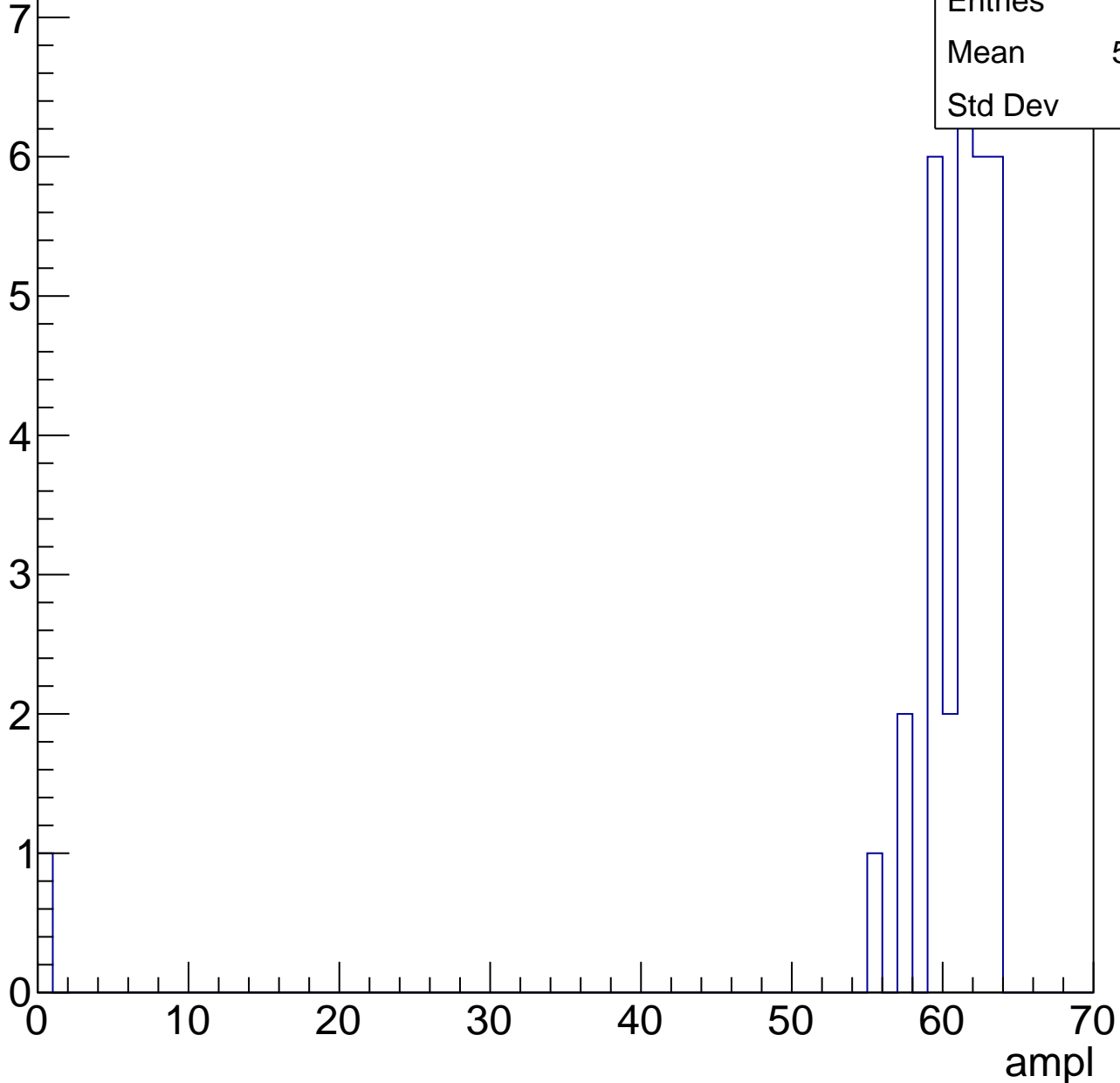


# B0L000S, U26-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

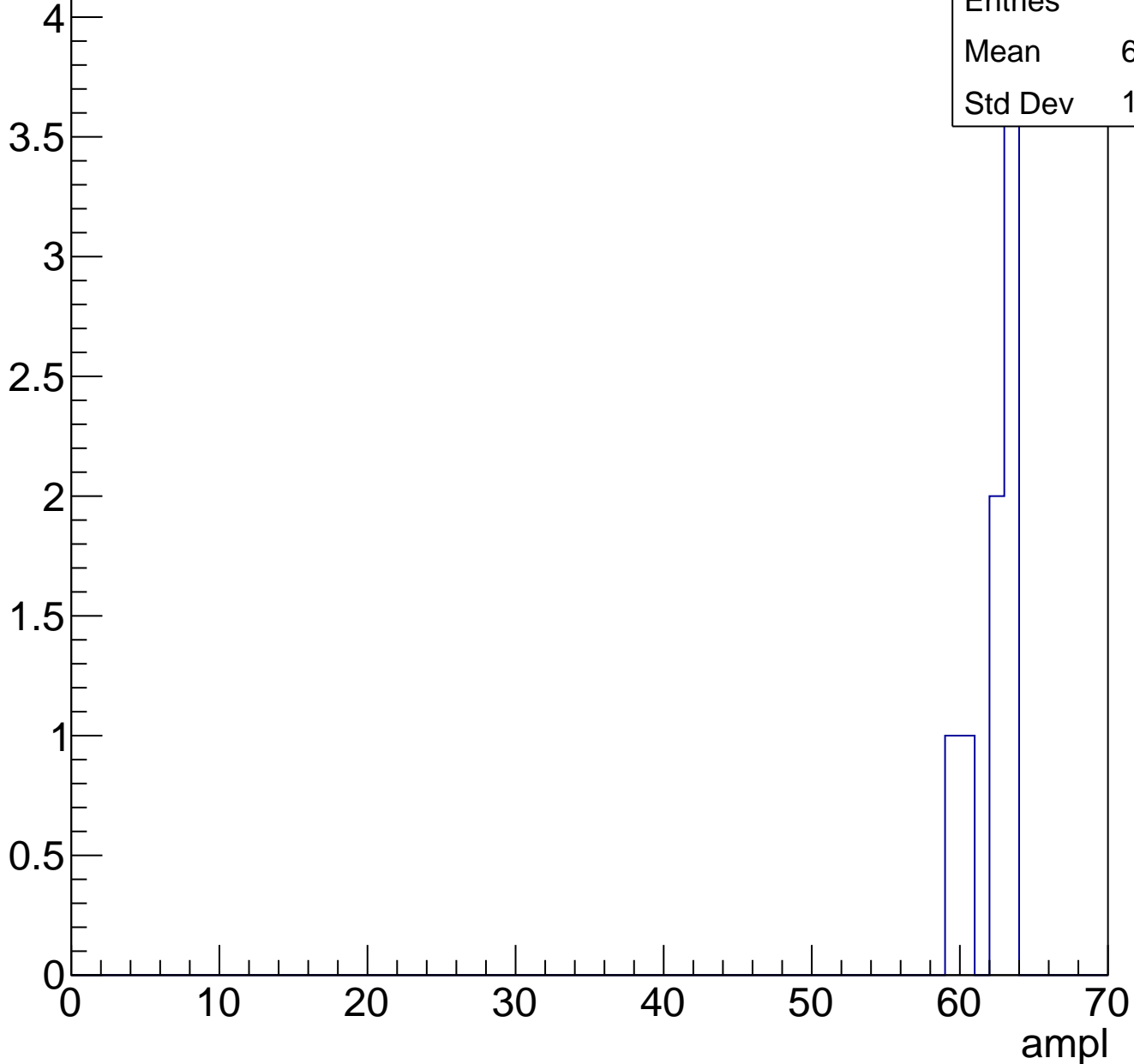
Entries	31
Mean	58.71
Std Dev	10.9



# B0L000S, U26-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch112, adc0

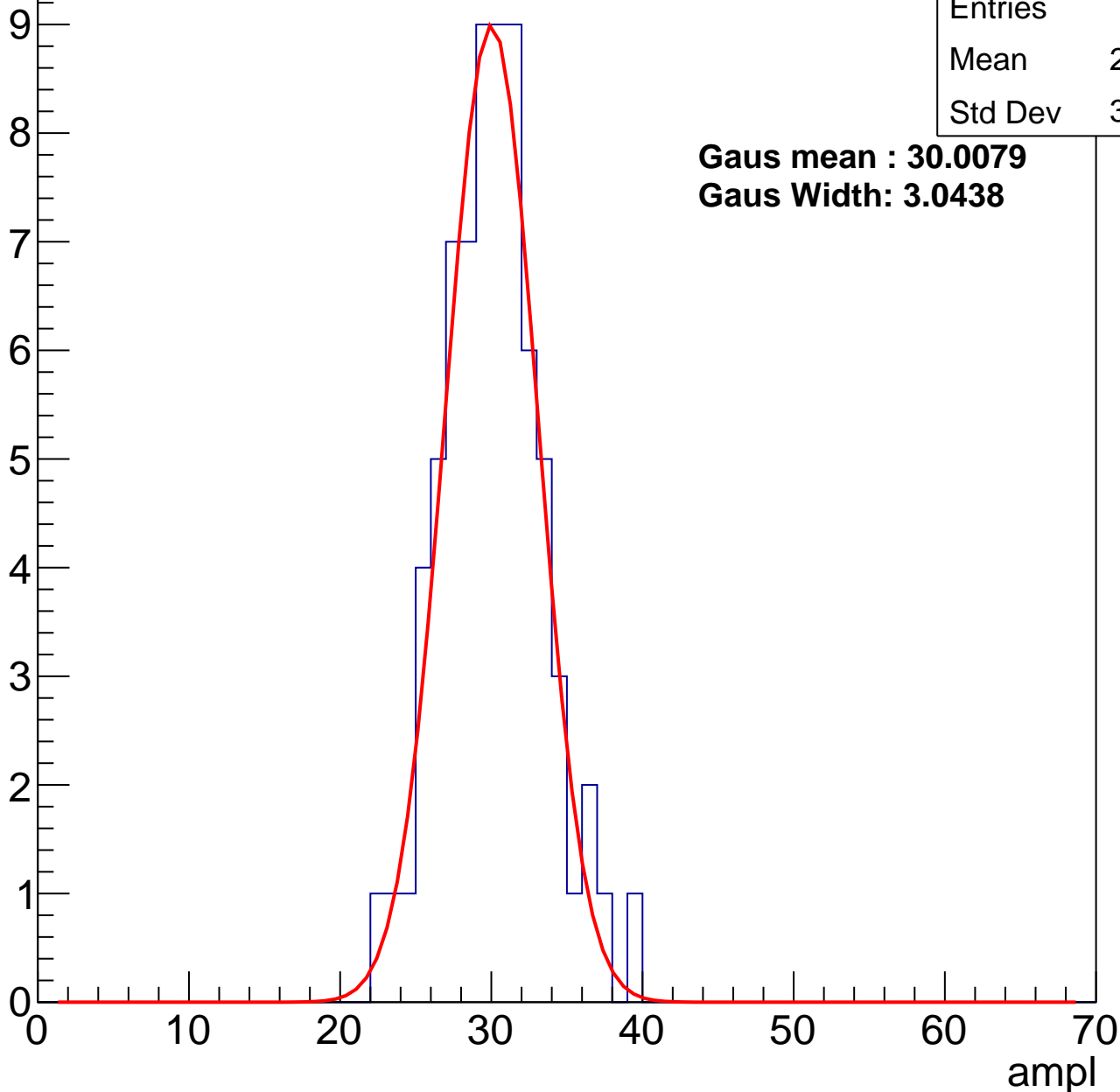
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	29.67
Std Dev	3.279

**Gaus mean : 30.0079**

**Gaus Width: 3.0438**



# B0L000S, U26-ch112, adc1

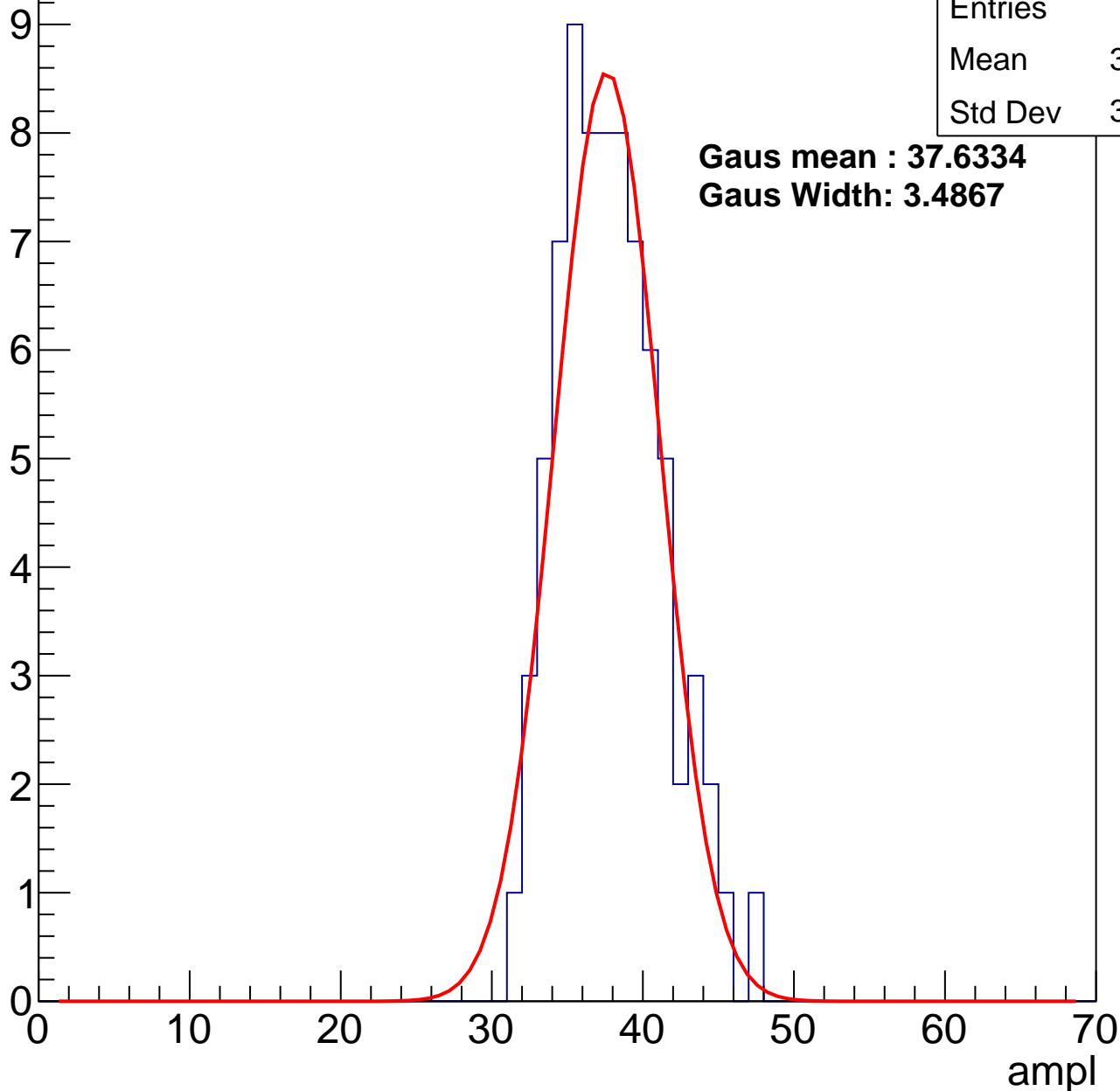
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	37.42
Std Dev	3.392

**Gaus mean : 37.6334**

**Gaus Width: 3.4867**



# B0L000S, U26-ch112, adc2

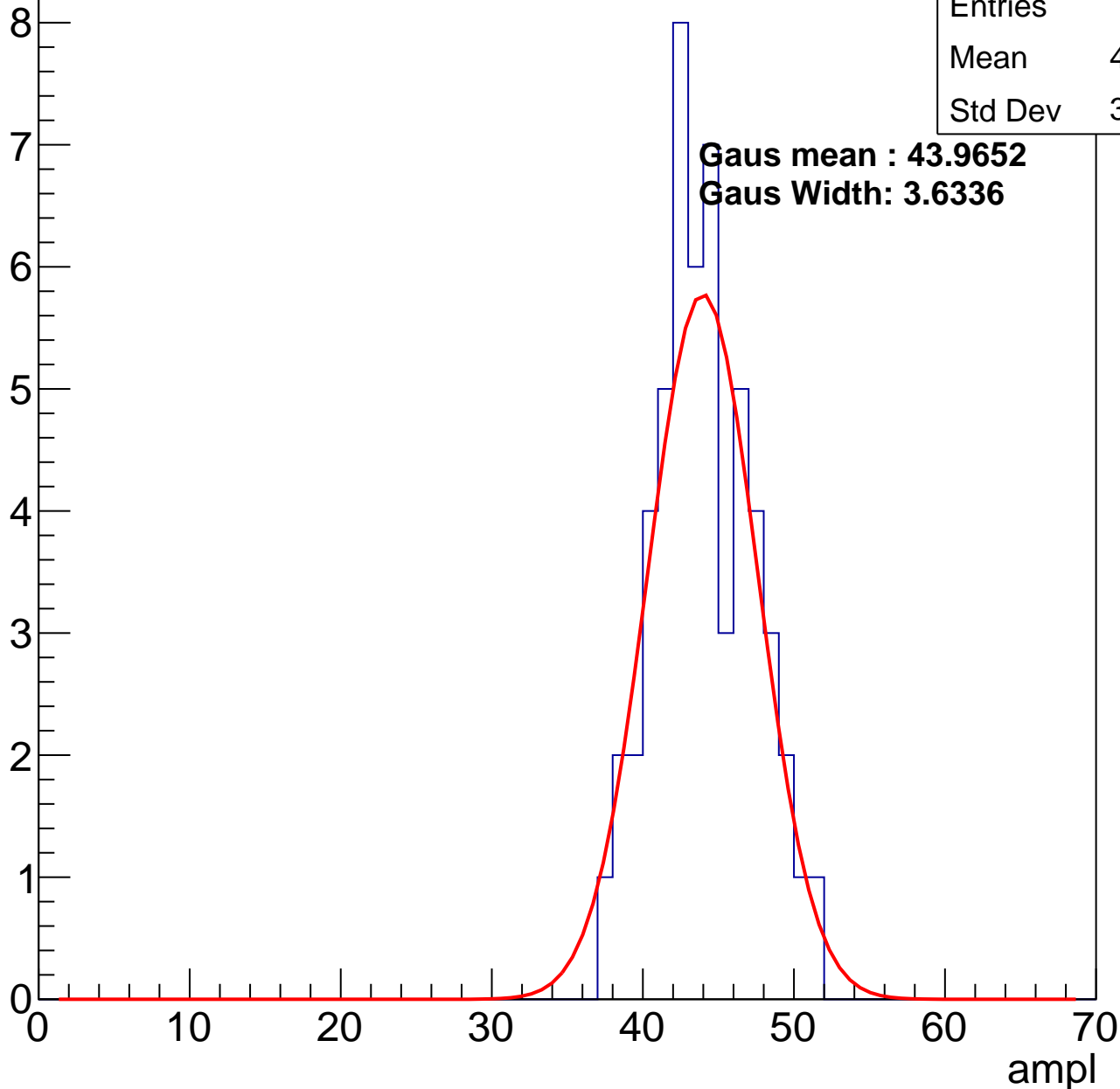
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	43.59
Std Dev	3.194

**Gaus mean : 43.9652**

**Gaus Width: 3.6336**

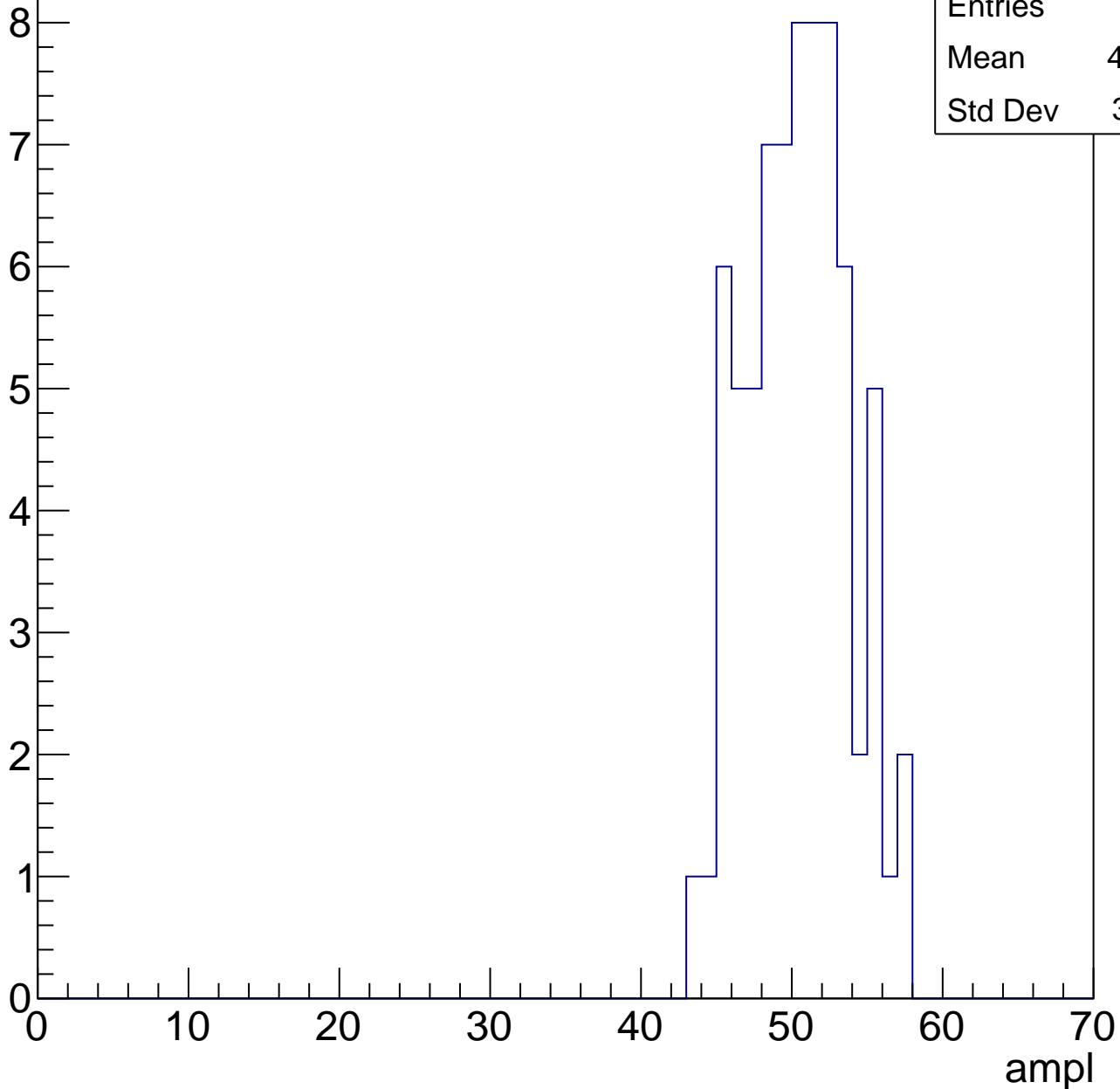


# B0L000S, U26-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	49.94
Std Dev	3.291

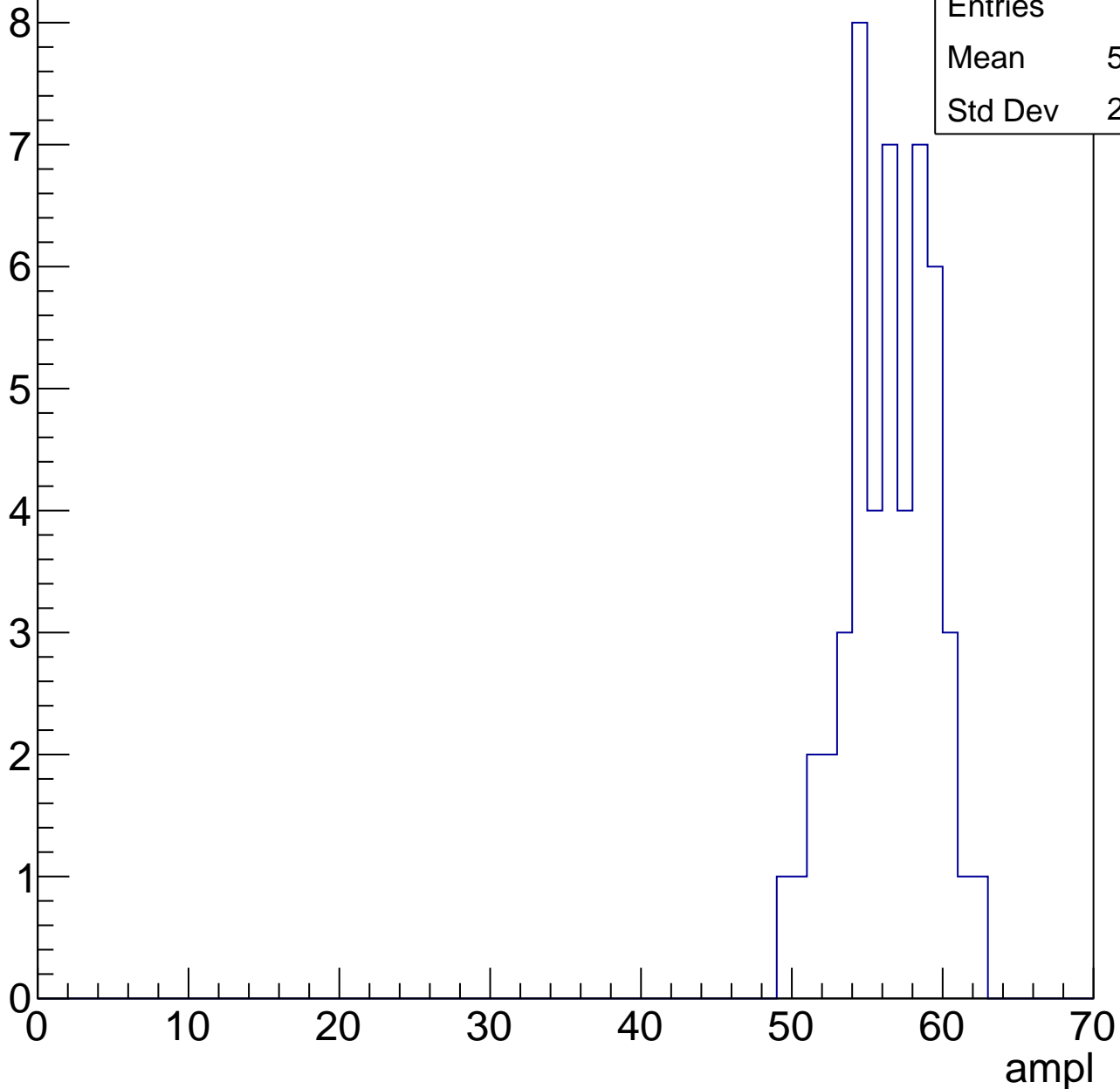


# B0L000S, U26-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	55.98
Std Dev	2.915

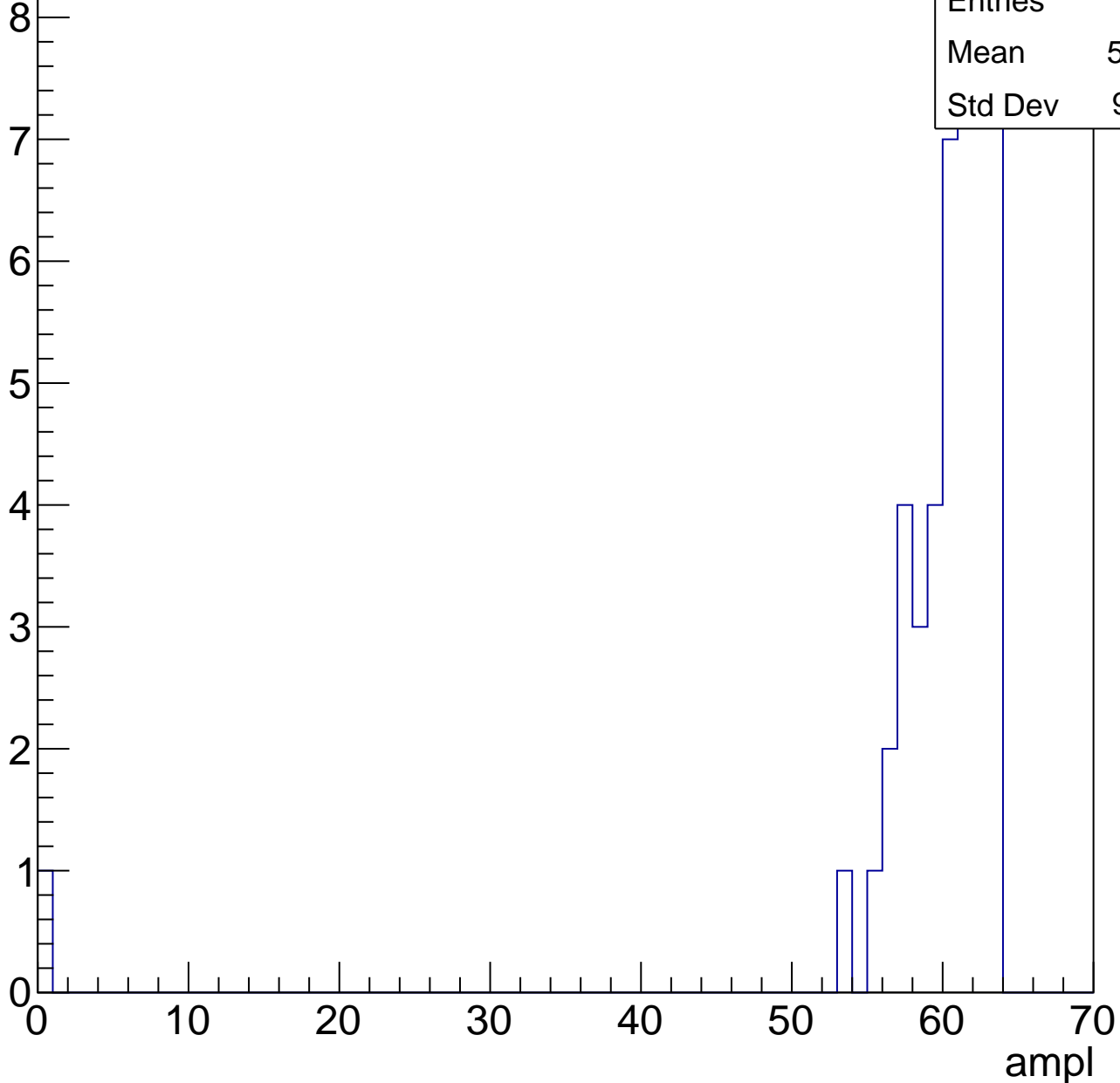


# B0L000S, U26-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	58.85
Std Dev	9.001



# B0L000S, U26-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch113, adc0

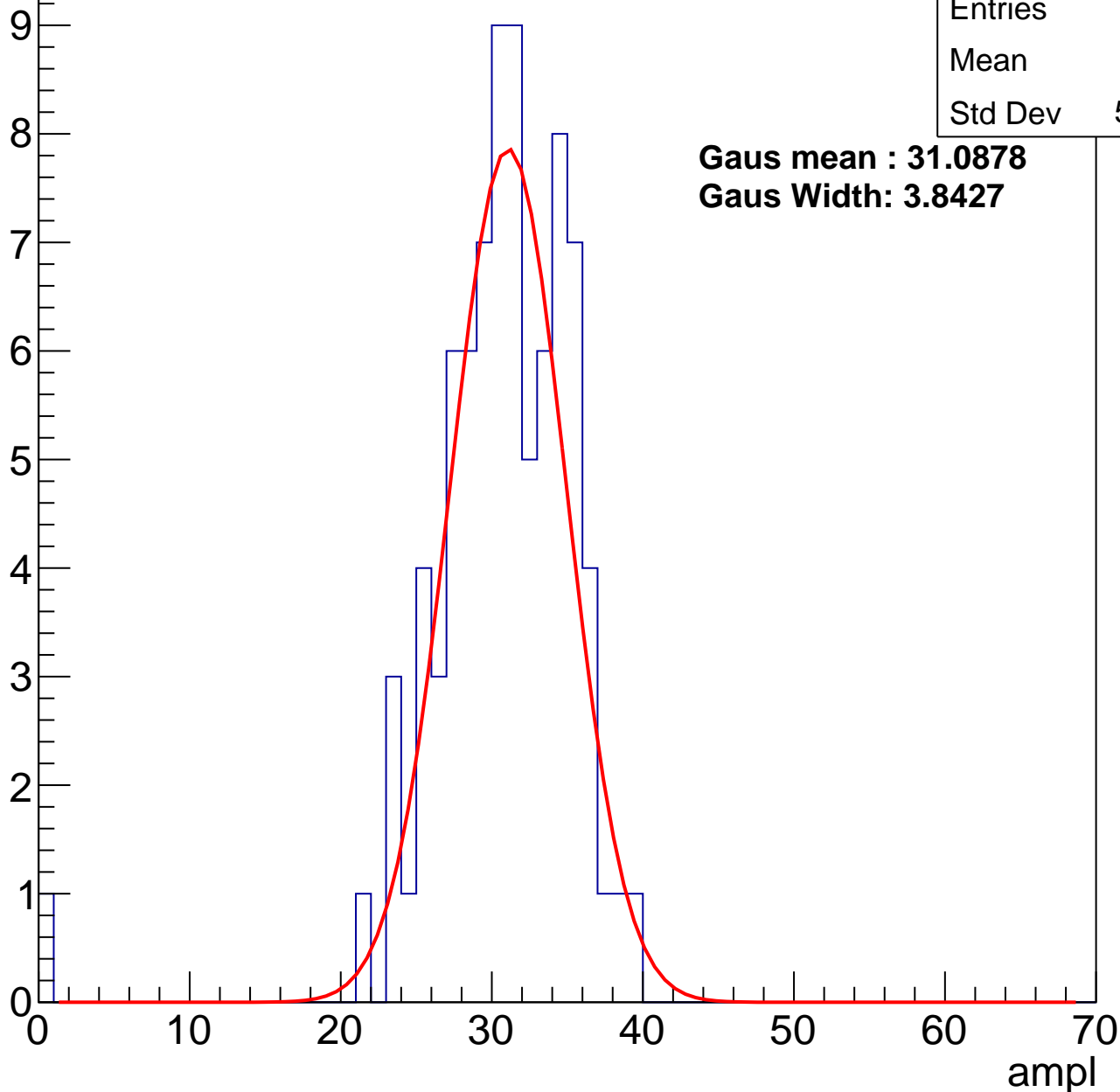
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	30.2
Std Dev	5.051

**Gaus mean : 31.0878**

**Gaus Width: 3.8427**



# B0L000S, U26-ch113, adc1

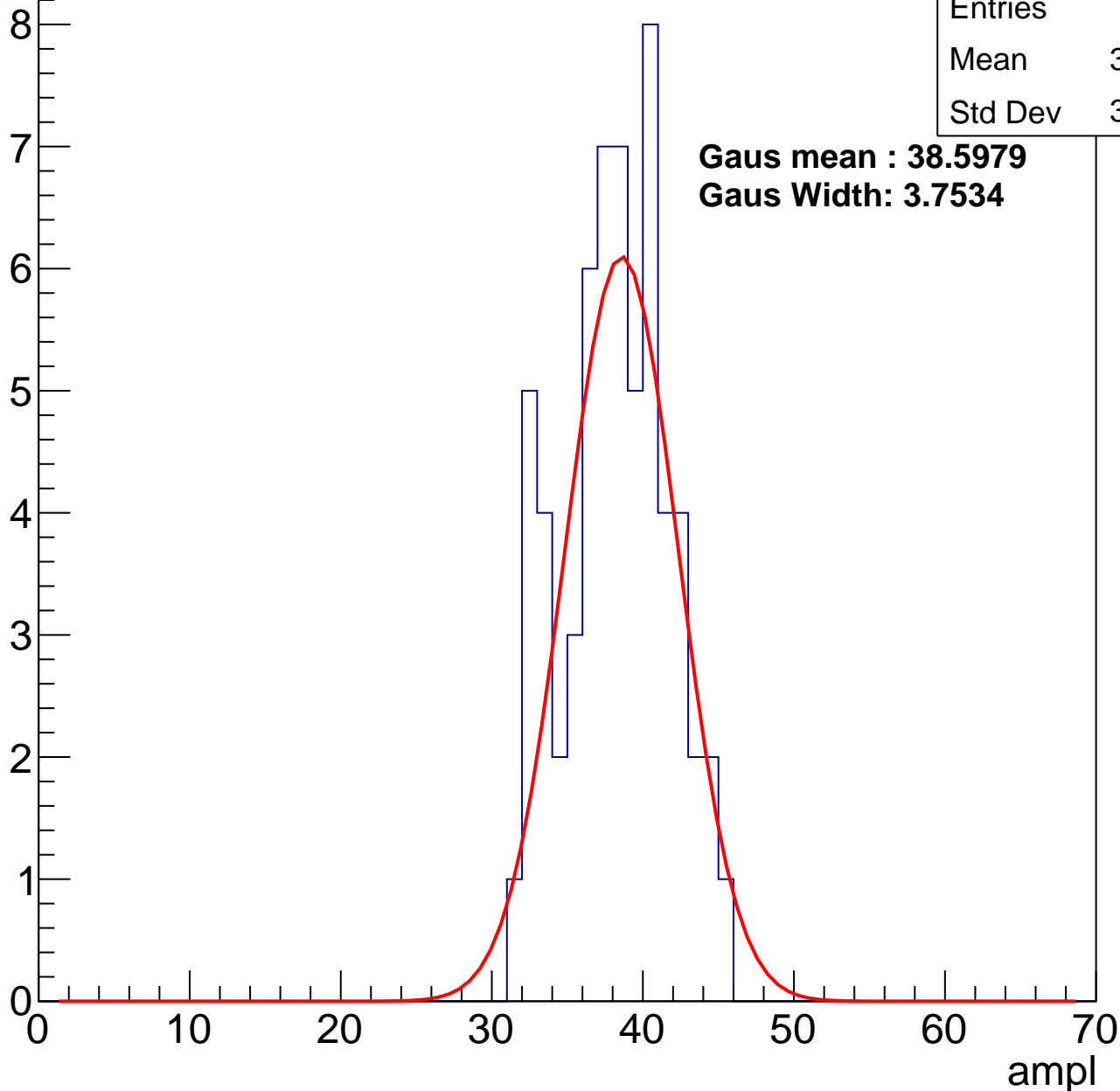
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.75
Std Dev	3.443

**Gaus mean : 38.5979**

**Gaus Width: 3.7534**



# B0L000S, U26-ch113, adc2

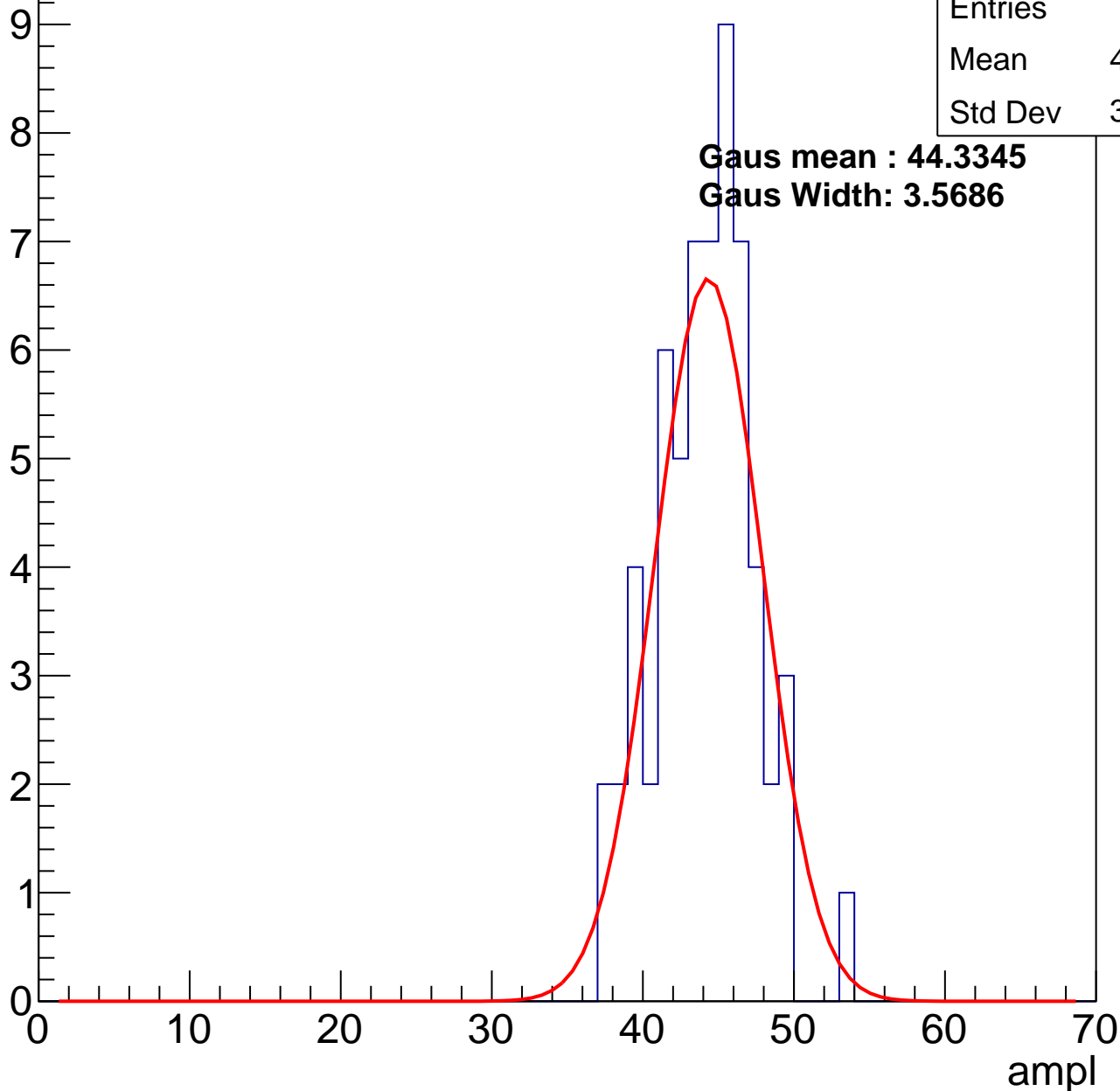
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.64
Std Dev	3.229

**Gaus mean : 44.3345**

**Gaus Width: 3.5686**



# B0L000S, U26-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	50.93
Std Dev	3.465

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

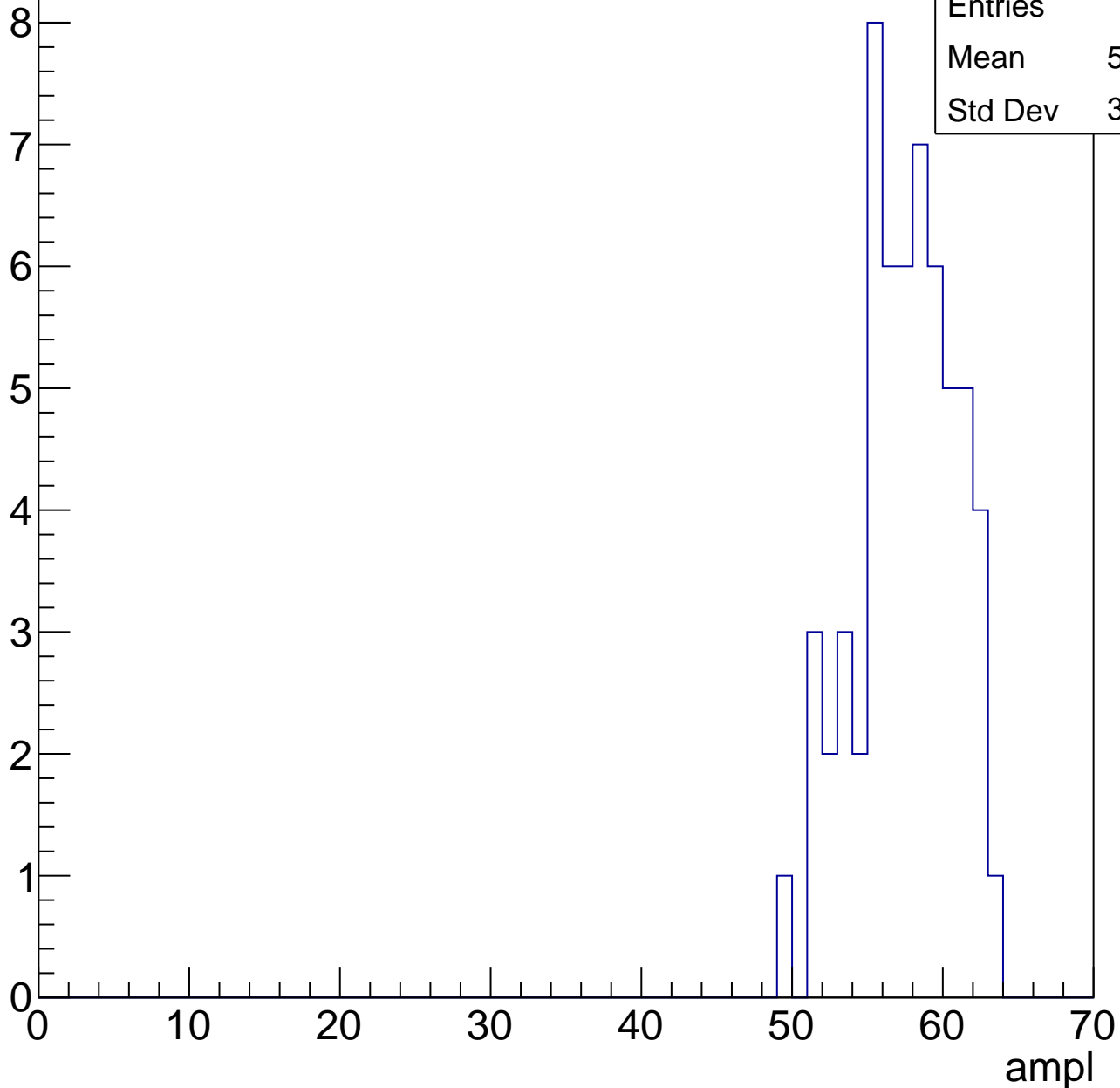
10

# B0L000S, U26-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

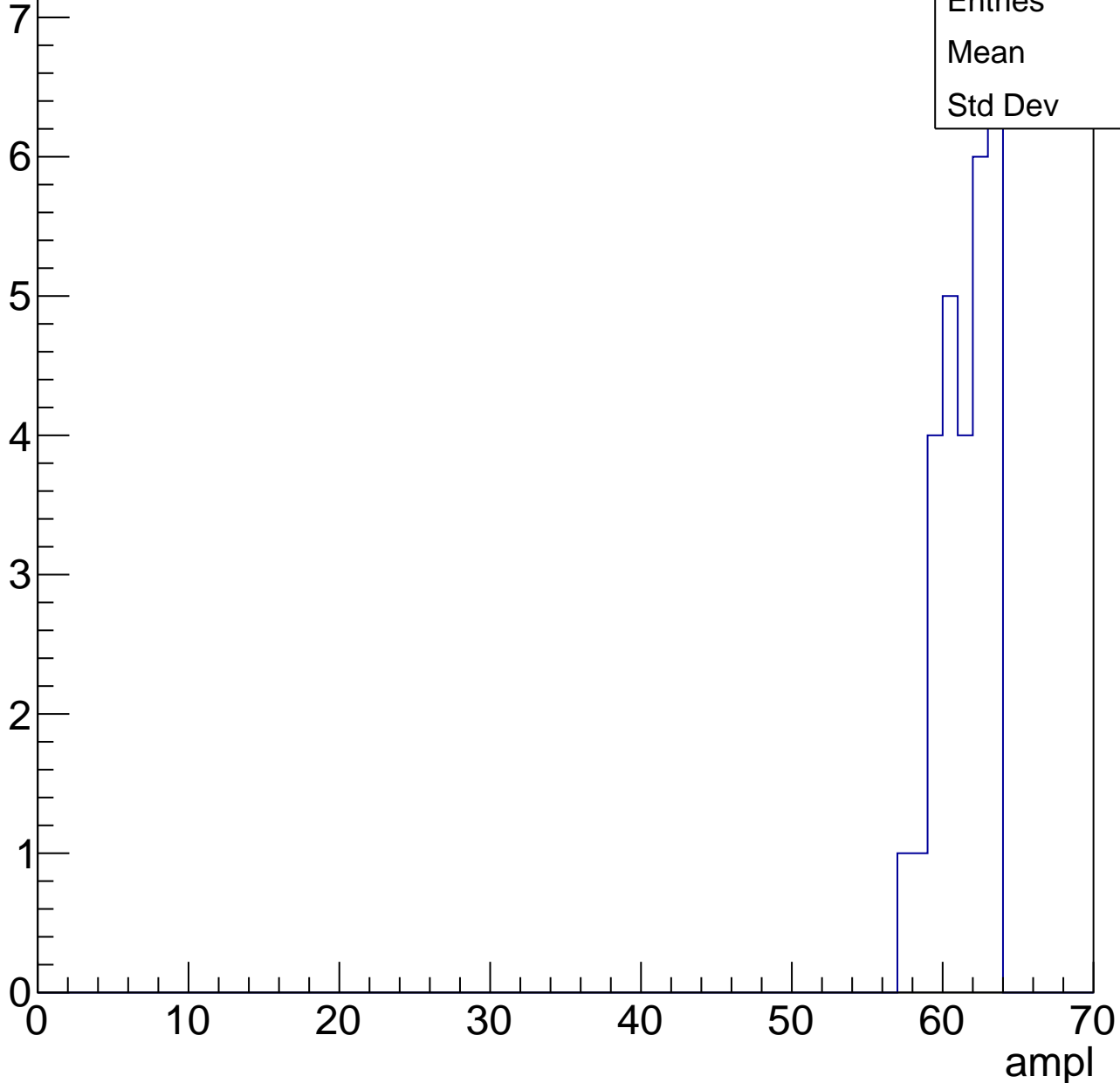
Entries	59
Mean	57.07
Std Dev	3.236



# B0L000S, U26-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

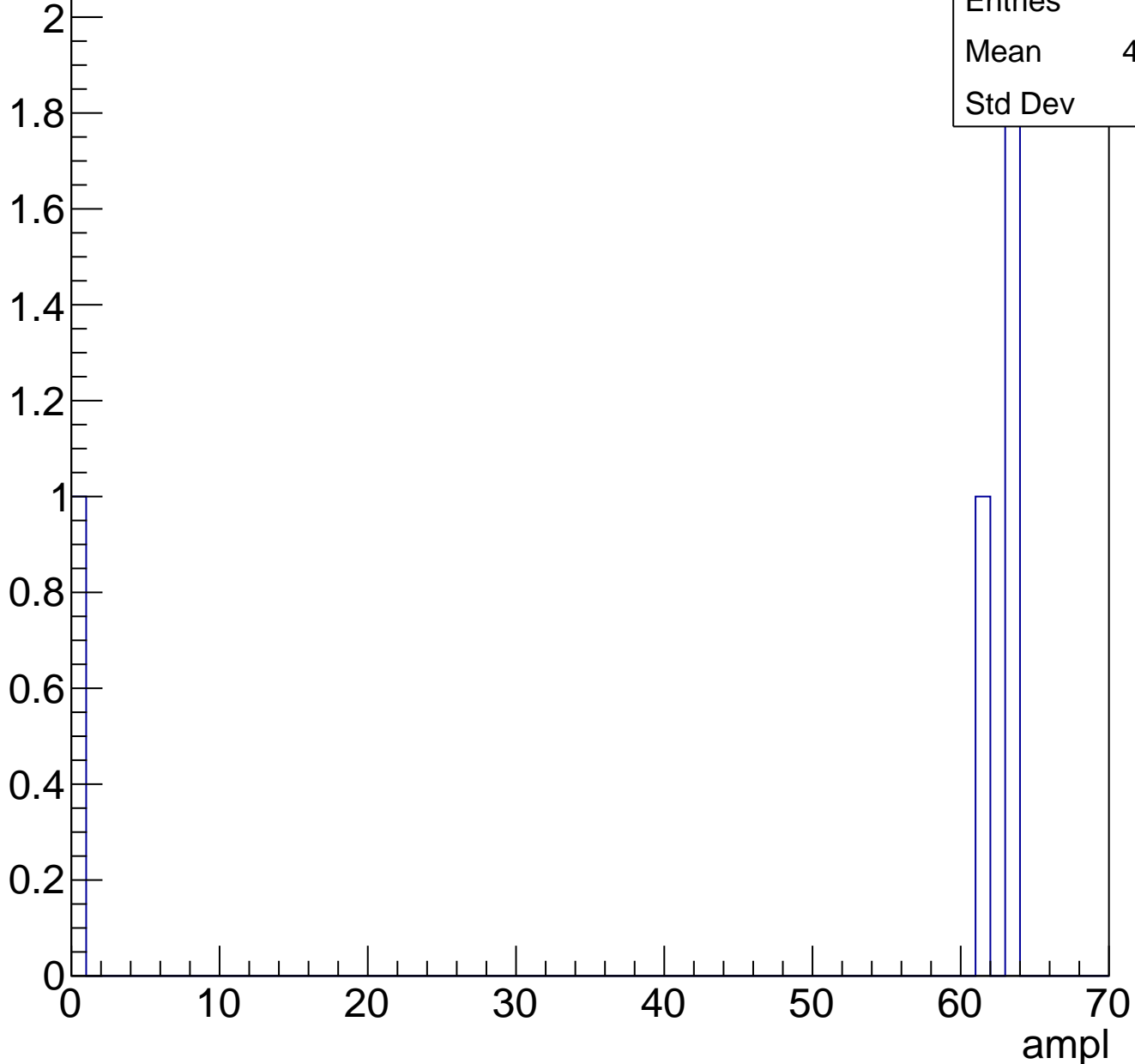


Entries	28
Mean	61
Std Dev	1.69

# B0L000S, U26-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch114, adc0

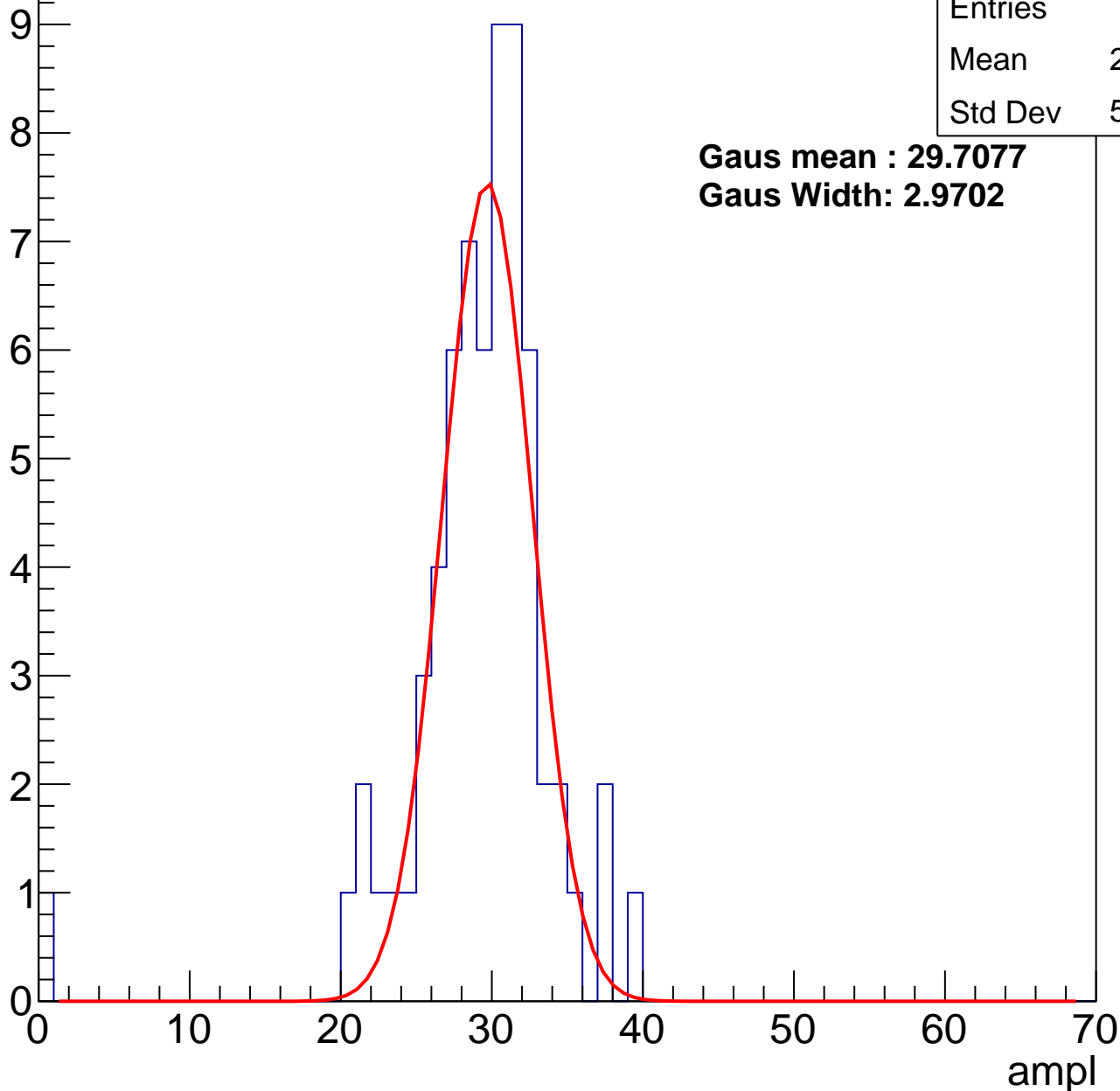
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	28.69
Std Dev	5.135

**Gaus mean : 29.7077**

**Gaus Width: 2.9702**



# B0L000S, U26-ch114, adc1

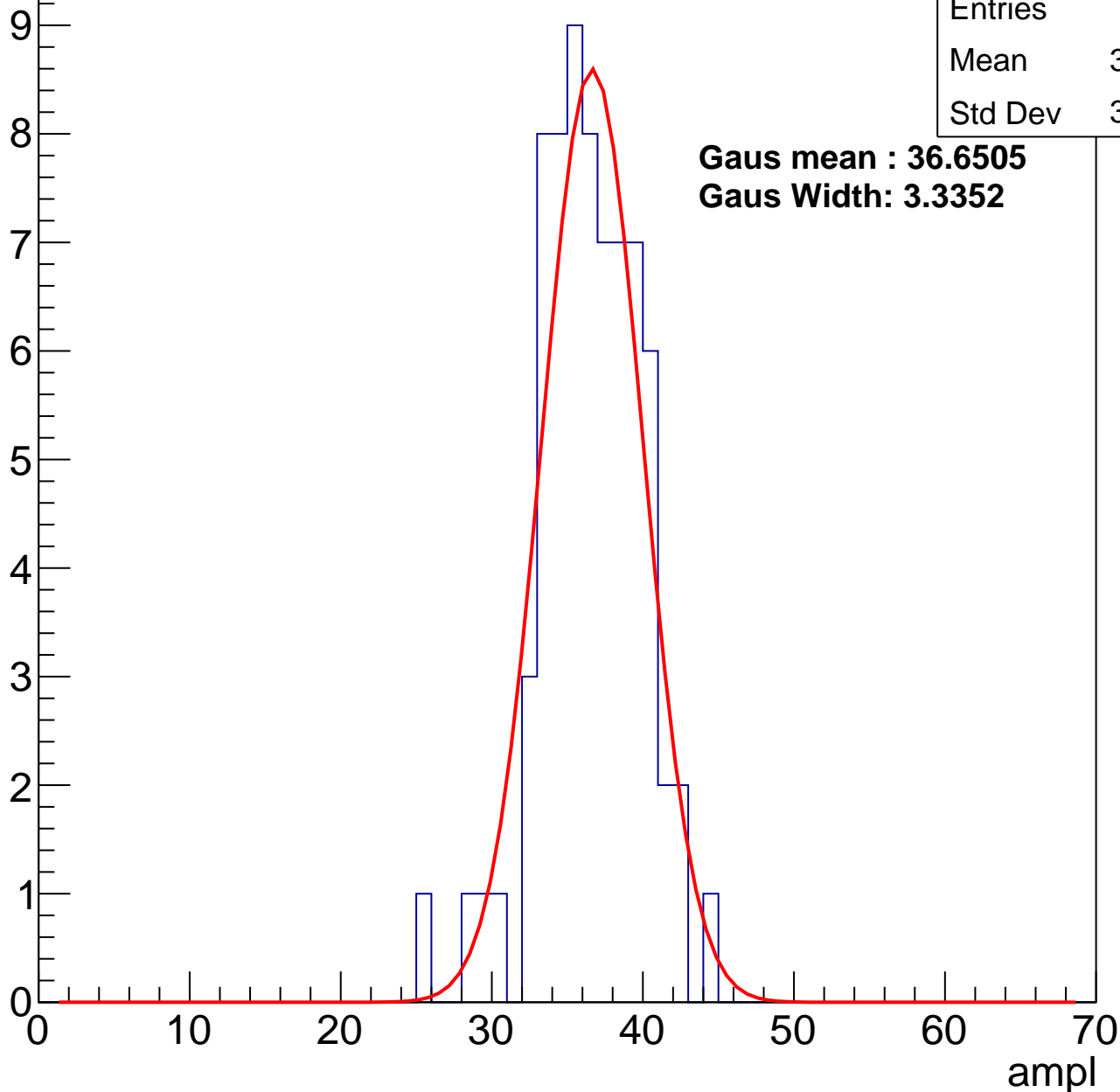
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.04
Std Dev	3.356

**Gaus mean : 36.6505**

**Gaus Width: 3.3352**



# B0L000S, U26-ch114, adc2

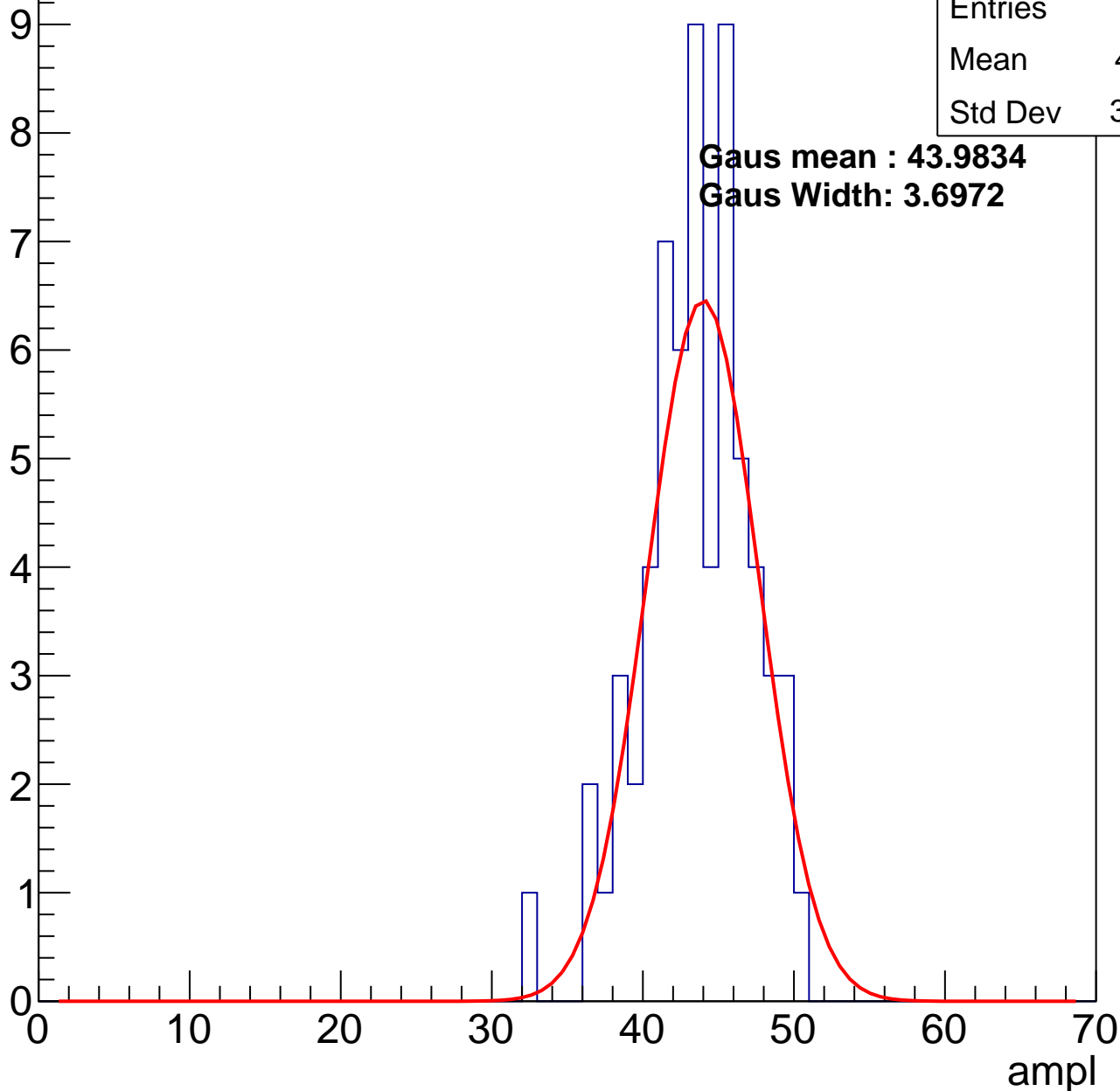
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.11
Std Dev	3.562

**Gaus mean : 43.9834**

**Gaus Width: 3.6972**

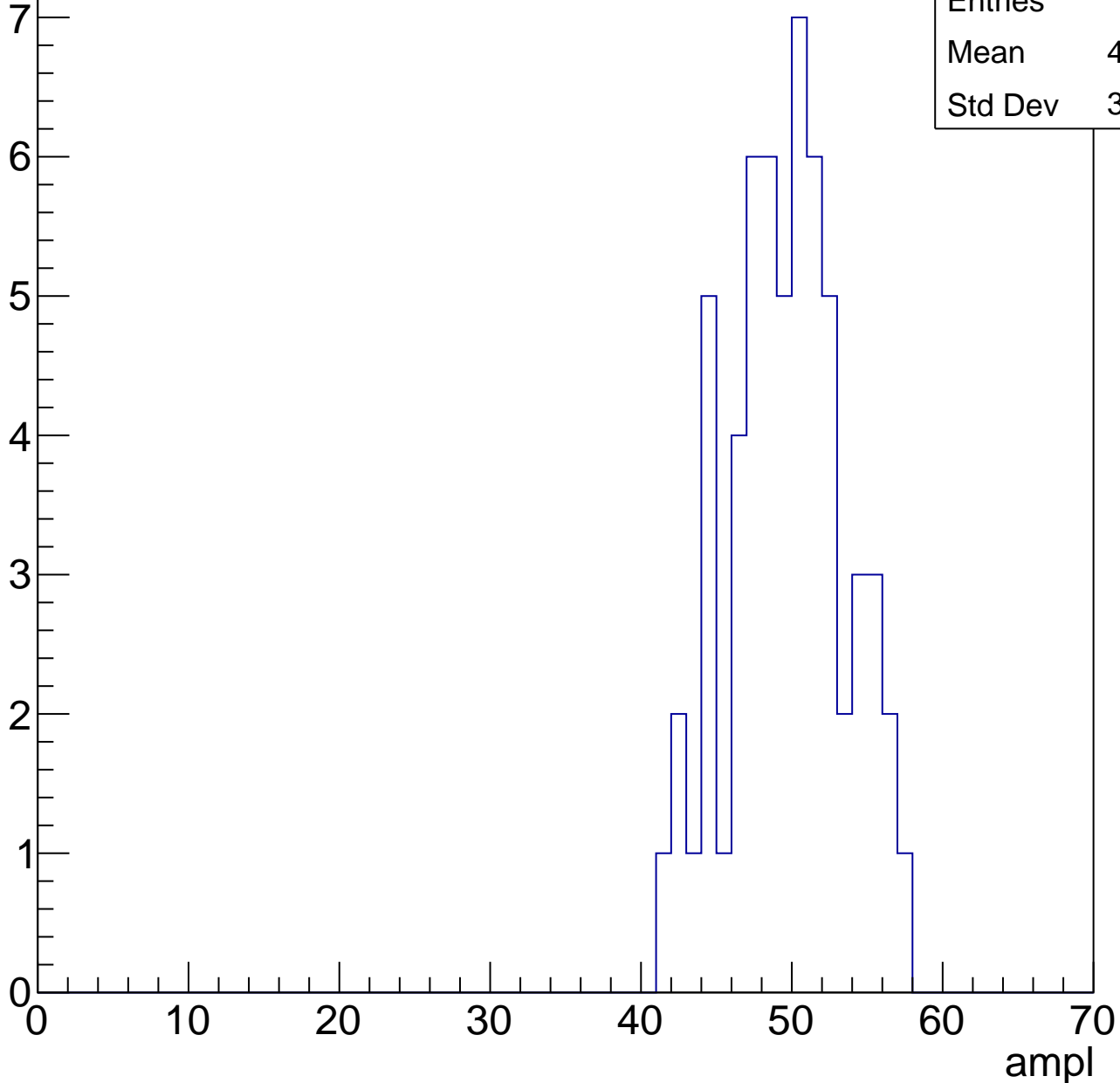


# B0L000S, U26-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	49.17
Std Dev	3.778

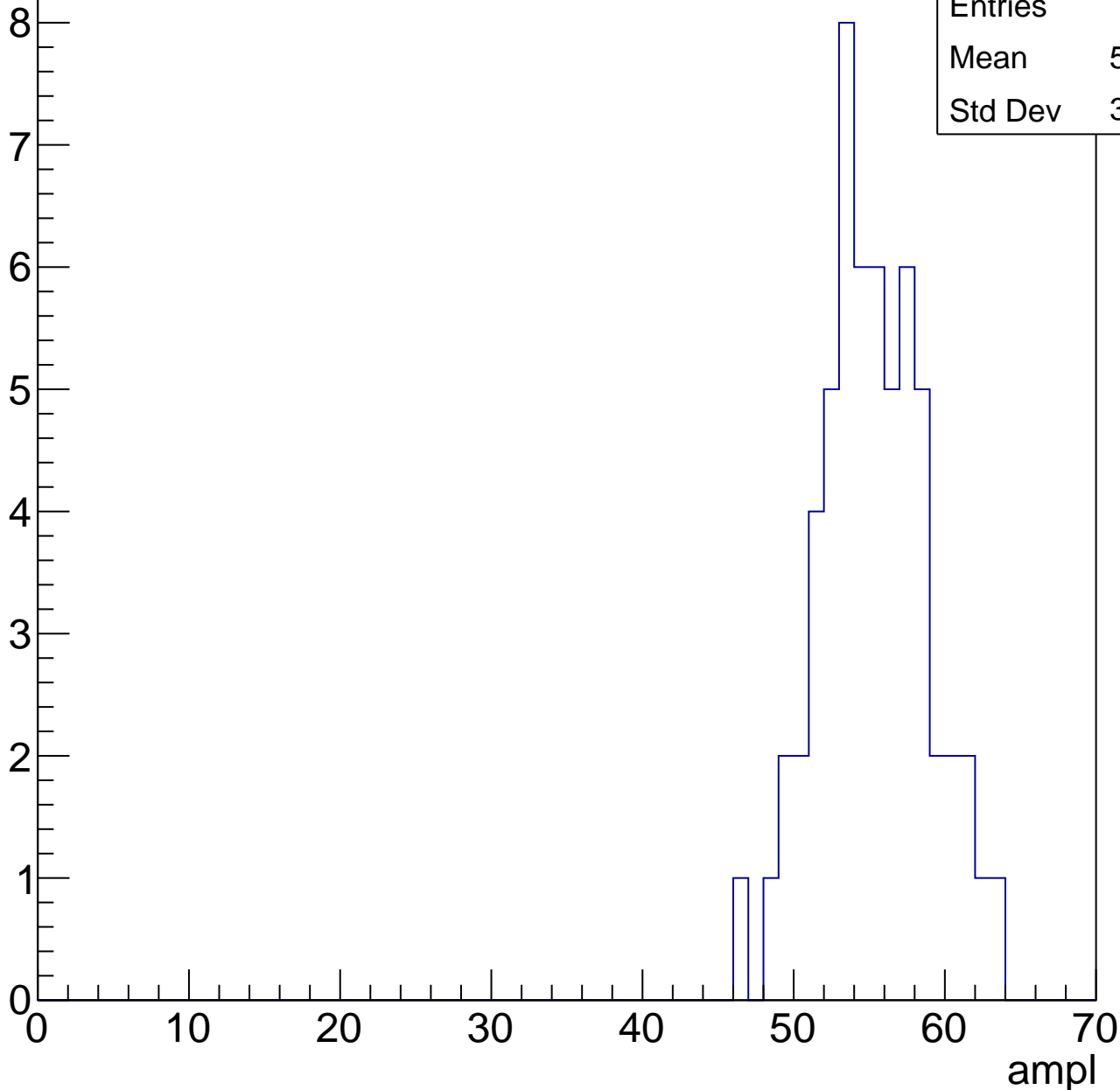


# B0L000S, U26-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54.76
Std Dev	3.534

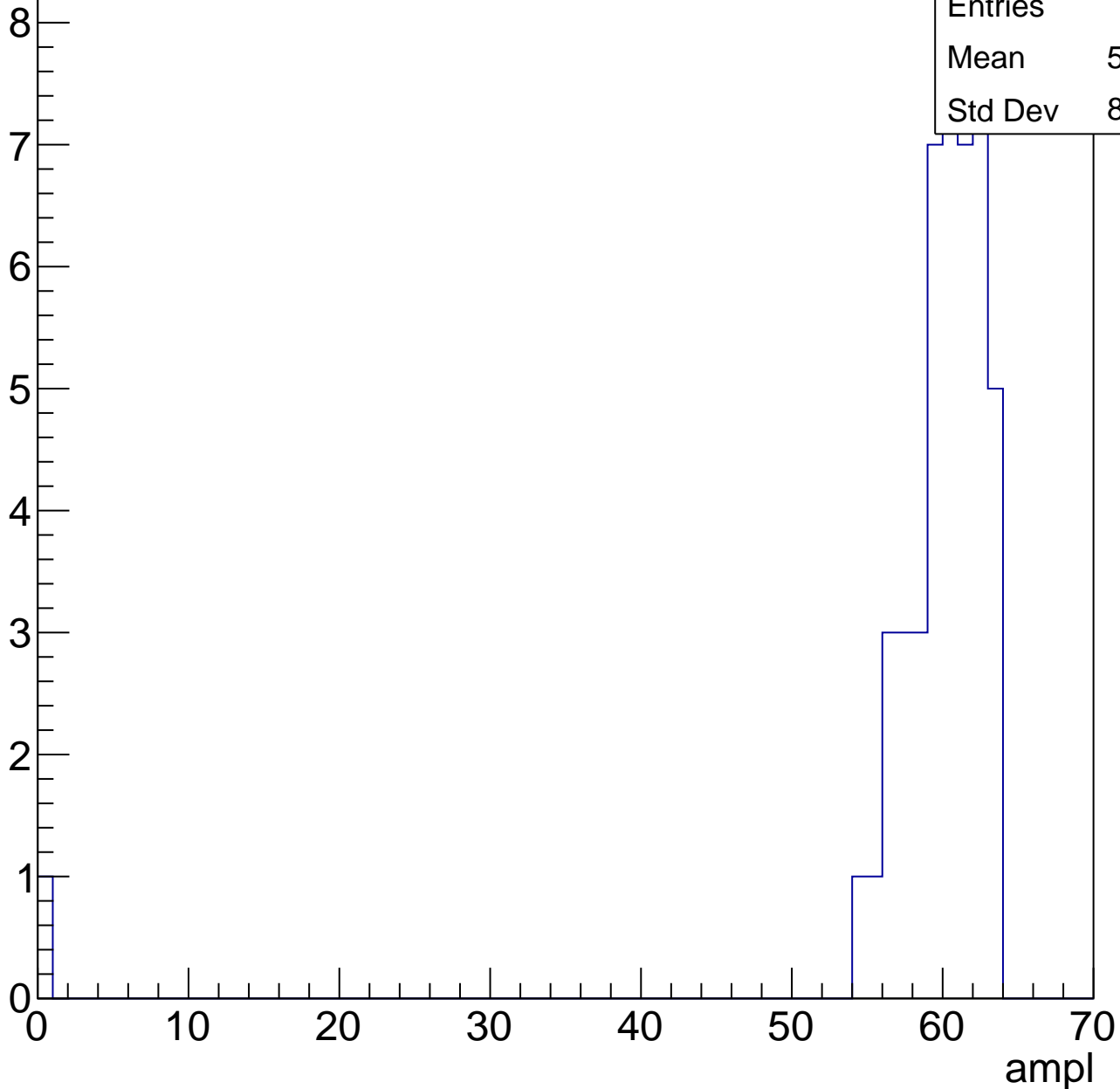


# B0L000S, U26-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

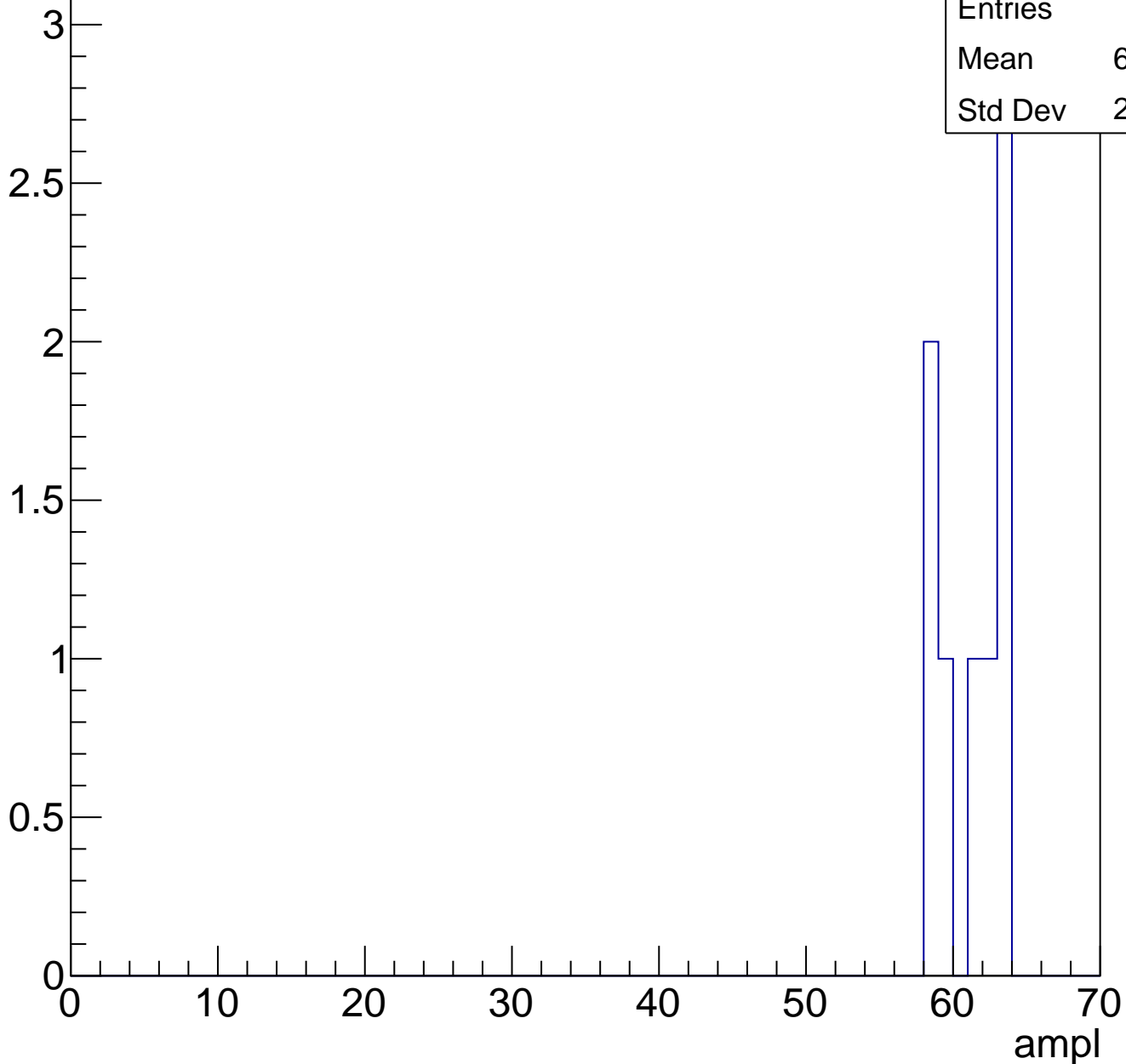
Entries	47
Mean	58.57
Std Dev	8.925



# B0L000S, U26-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

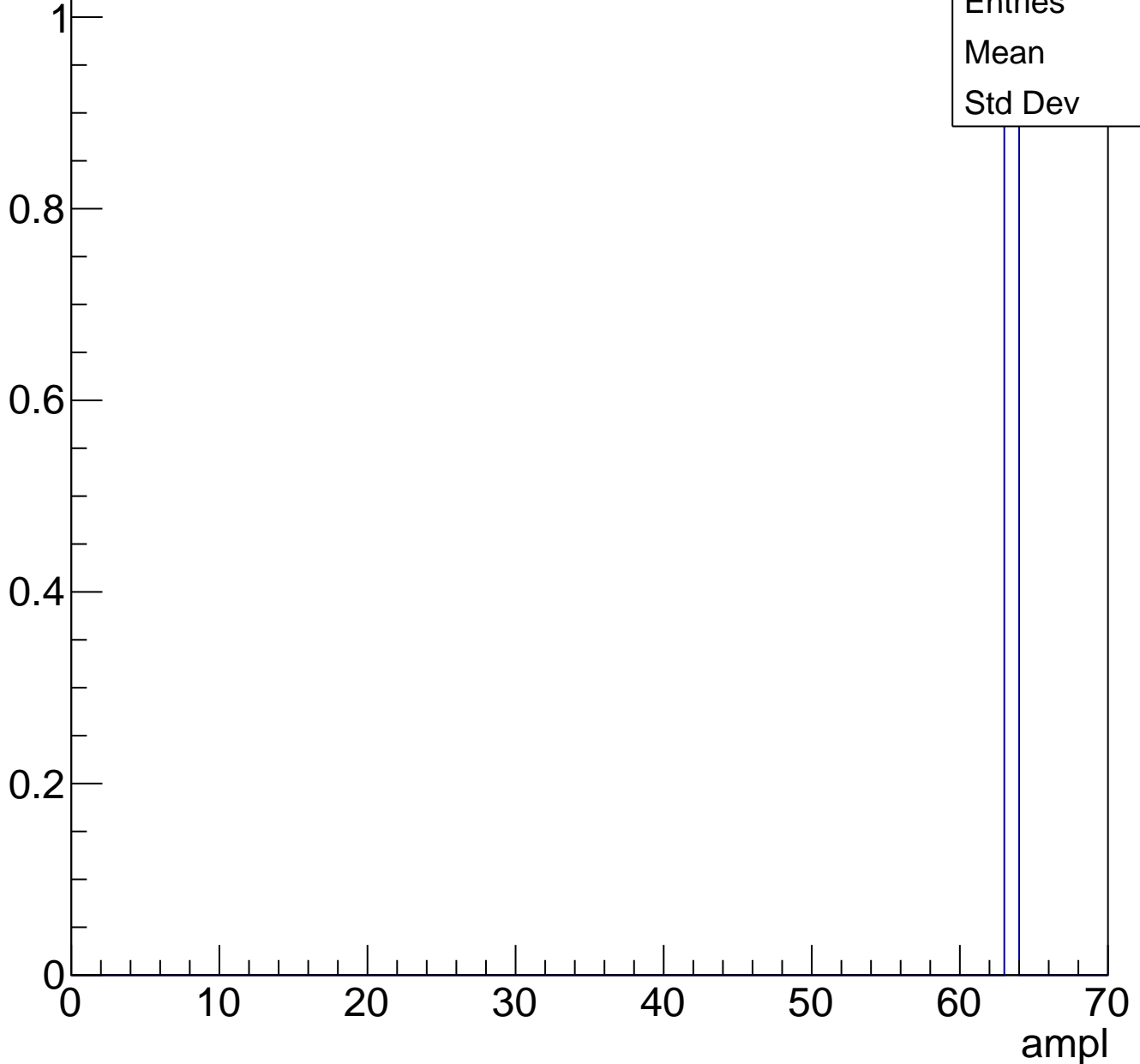




# B0L000S, U26-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch115, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	30.79
Std Dev	3.249

**Gaus mean : 30.9831**

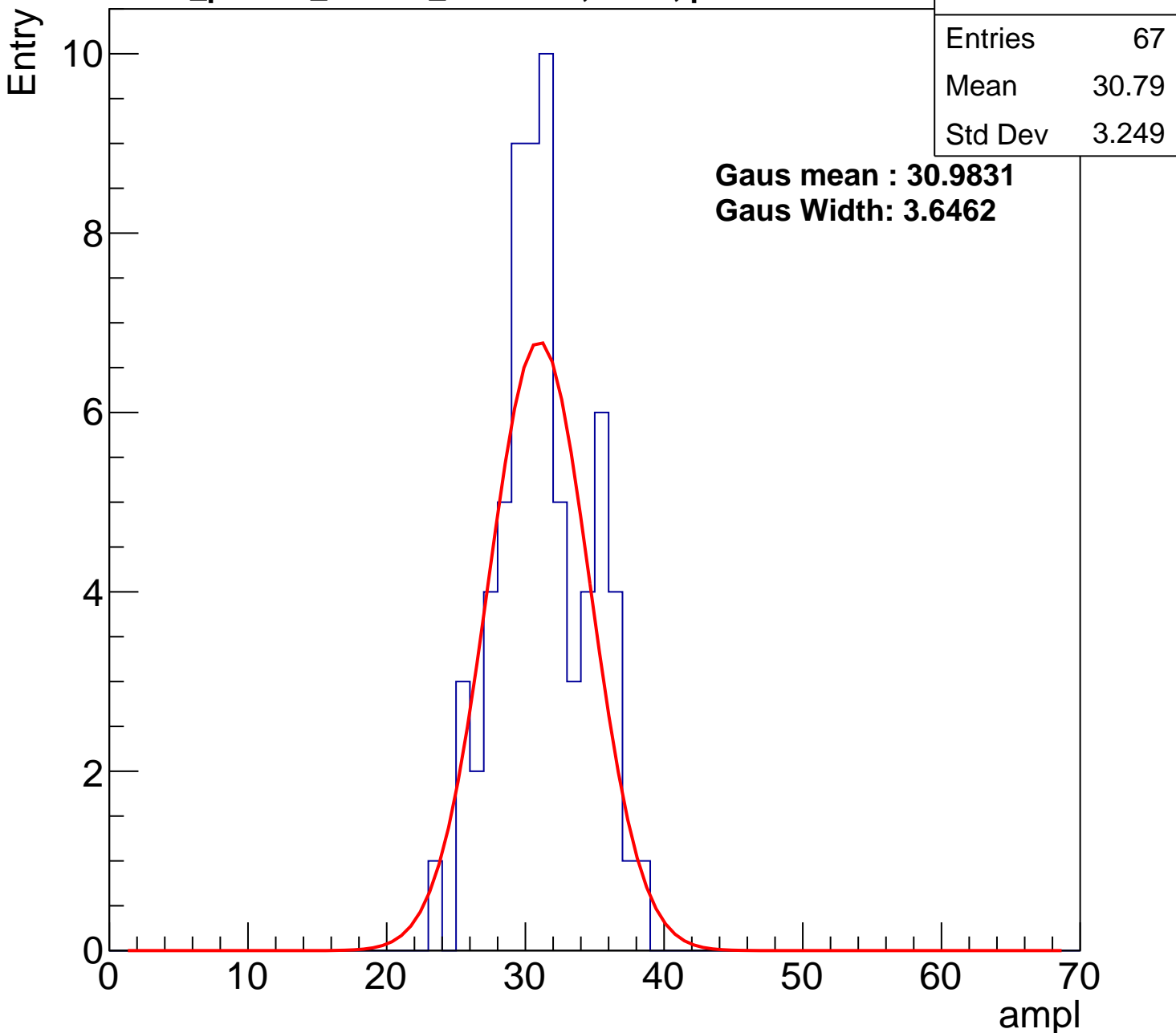
**Gaus Width: 3.6462**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch115, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	38.21
Std Dev	4.057

**Gaus mean : 38.1519**

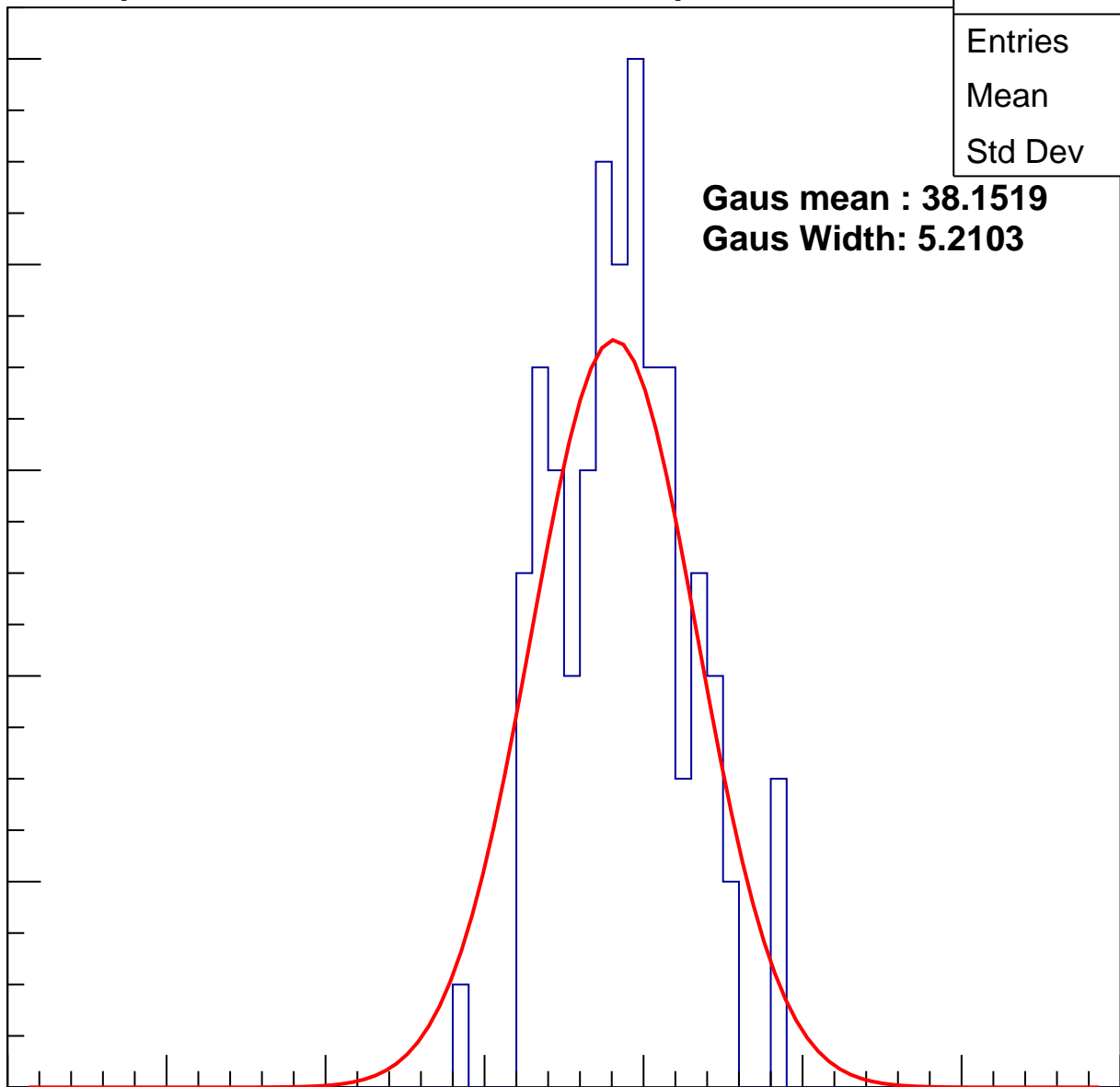
**Gaus Width: 5.2103**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch115, adc2

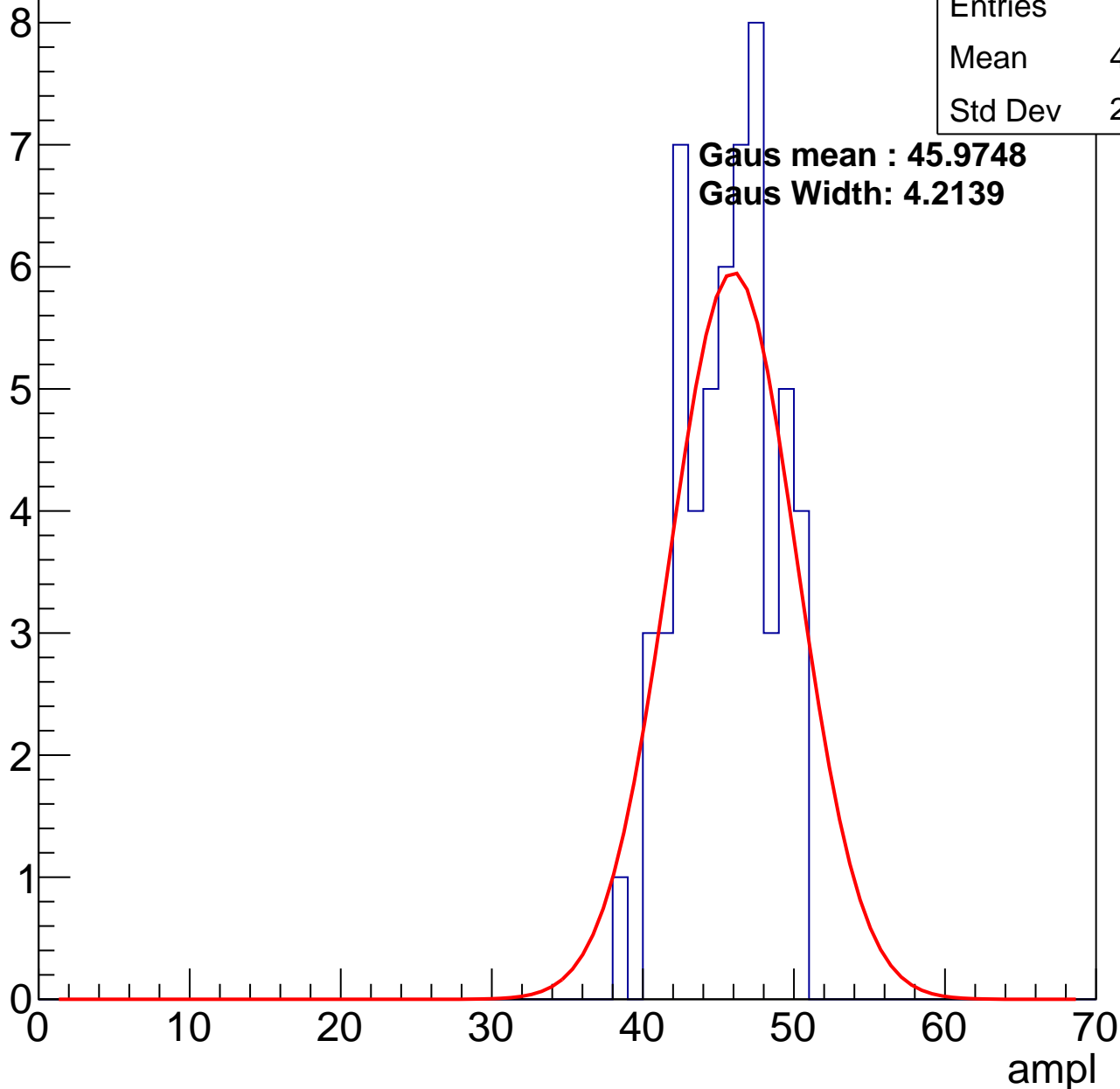
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	45.07
Std Dev	2.993

**Gaus mean : 45.9748**

**Gaus Width: 4.2139**

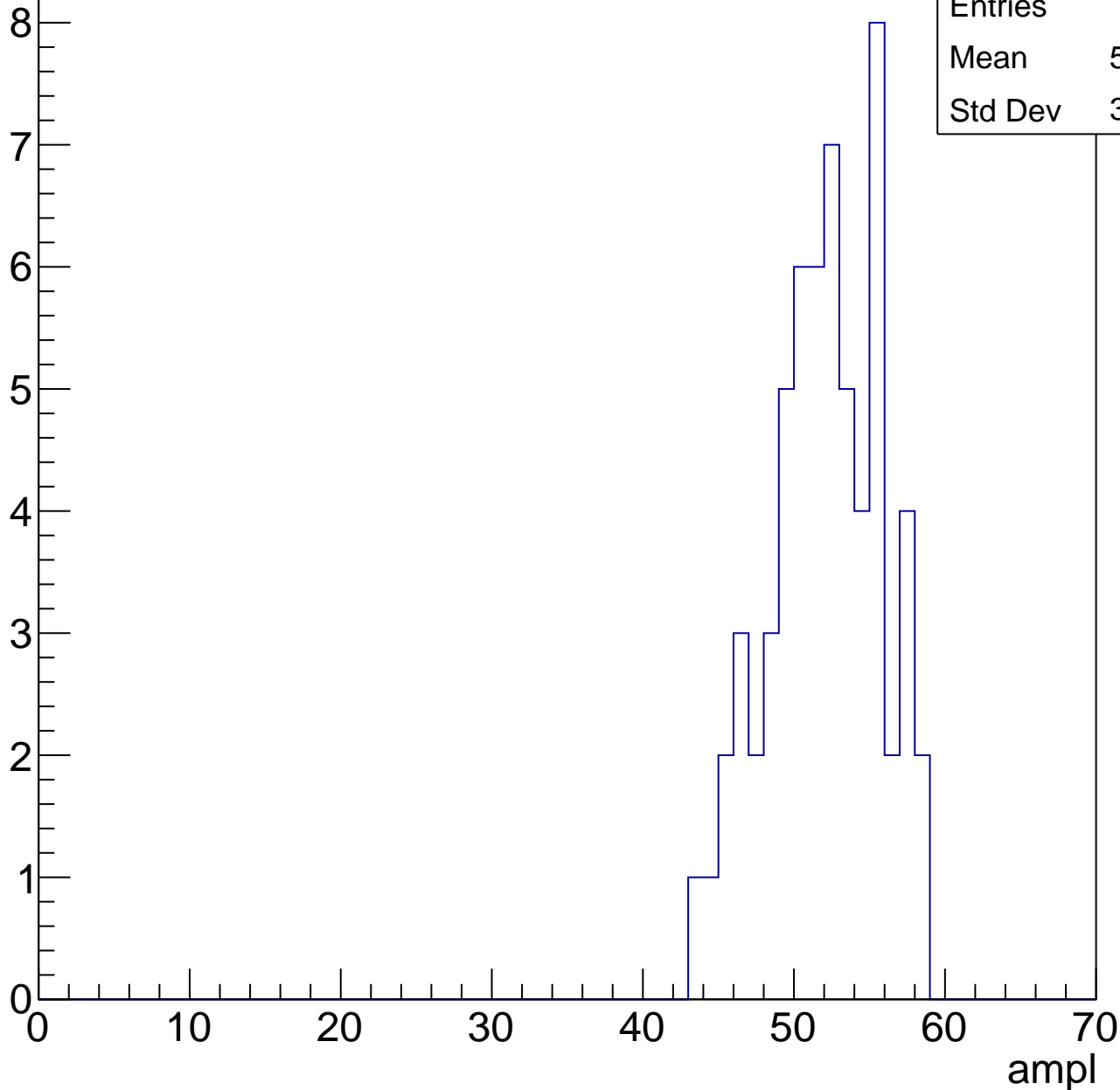


# B0L000S, U26-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	51.56
Std Dev	3.642

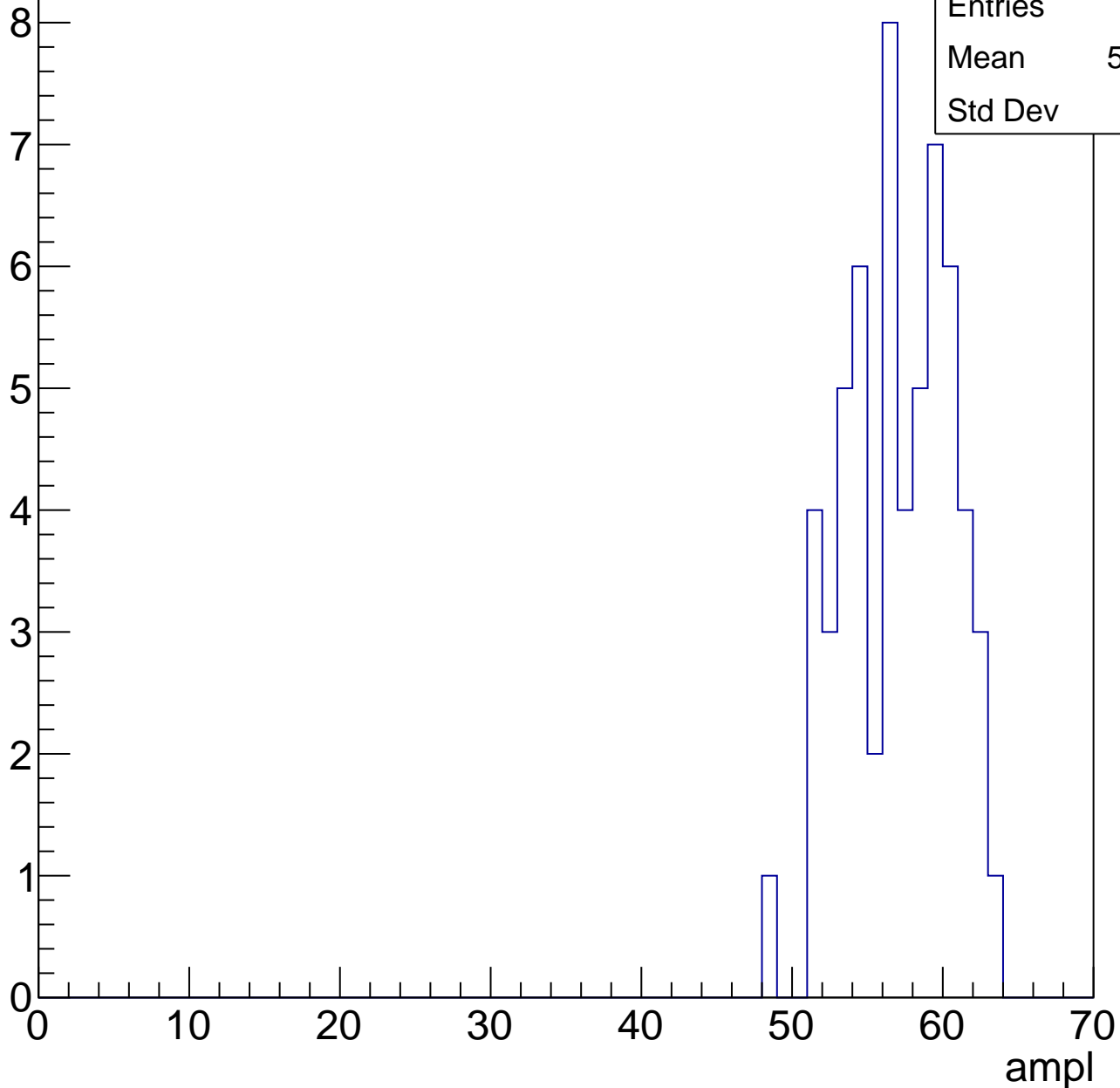


# B0L000S, U26-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

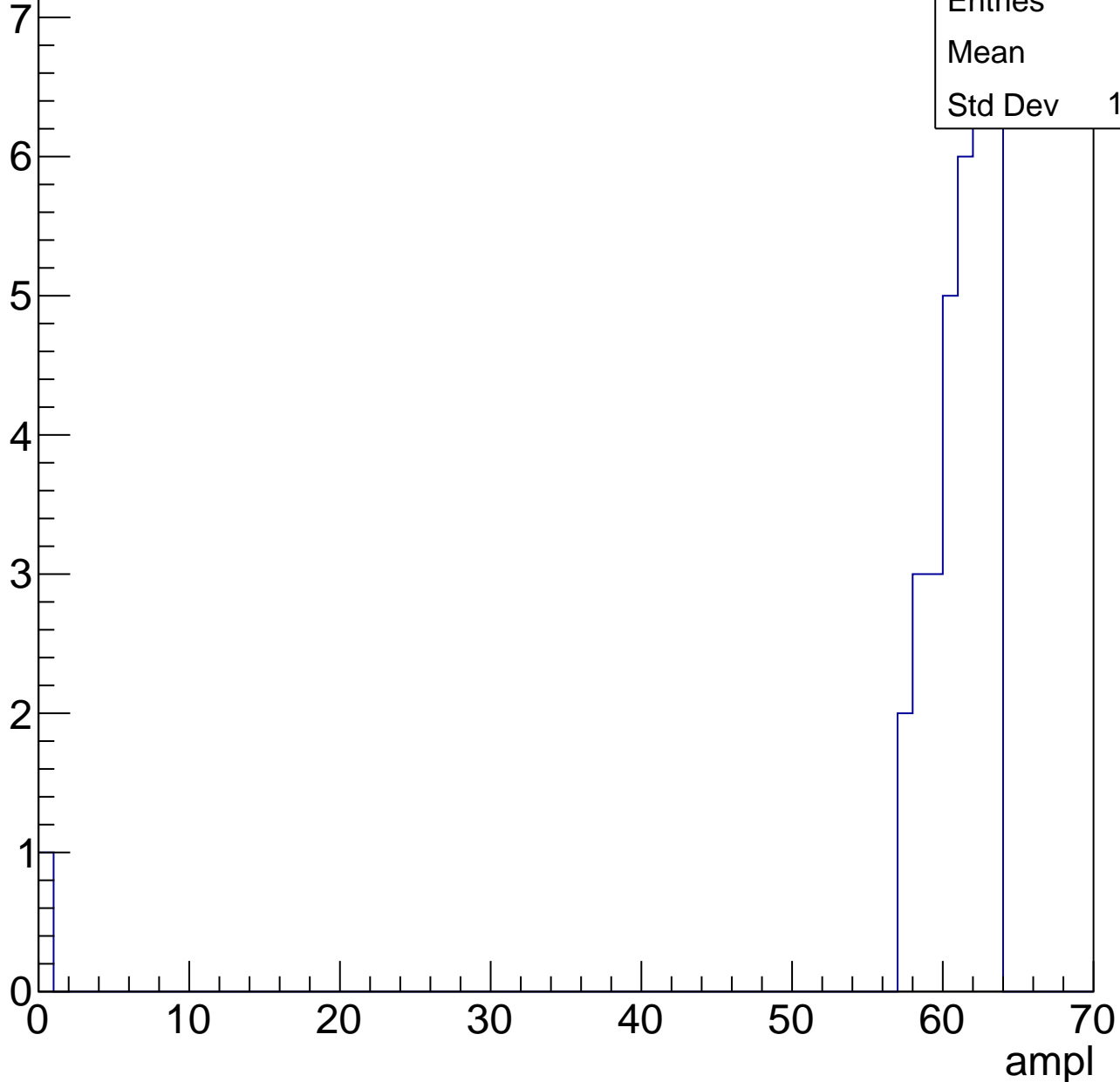
Entries	59
Mean	56.59
Std Dev	3.45



# B0L000S, U26-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



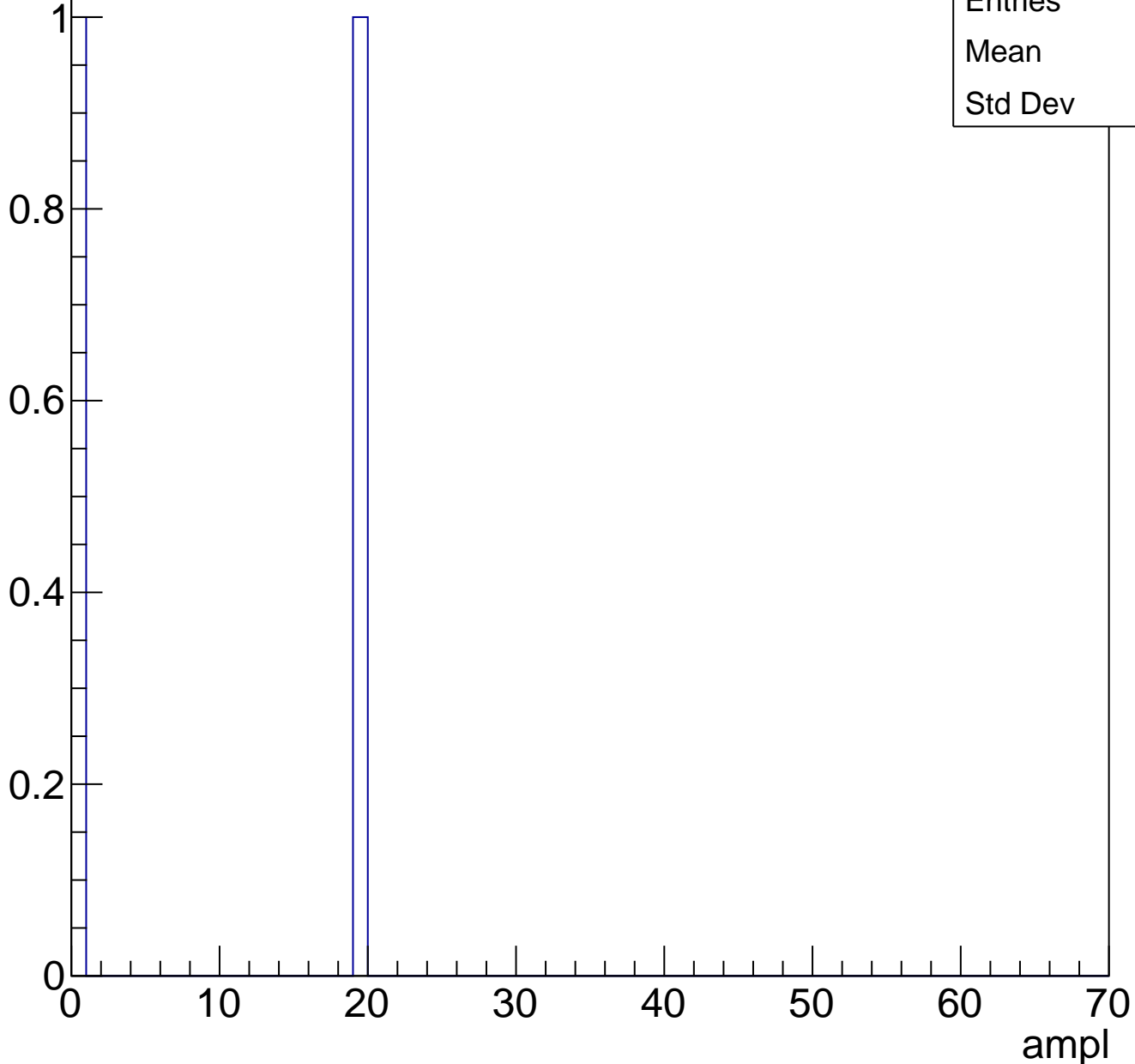
Entries	2
Mean	63
Std Dev	0



# B0L000S, U26-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B0L000S, U26-ch116, adc0

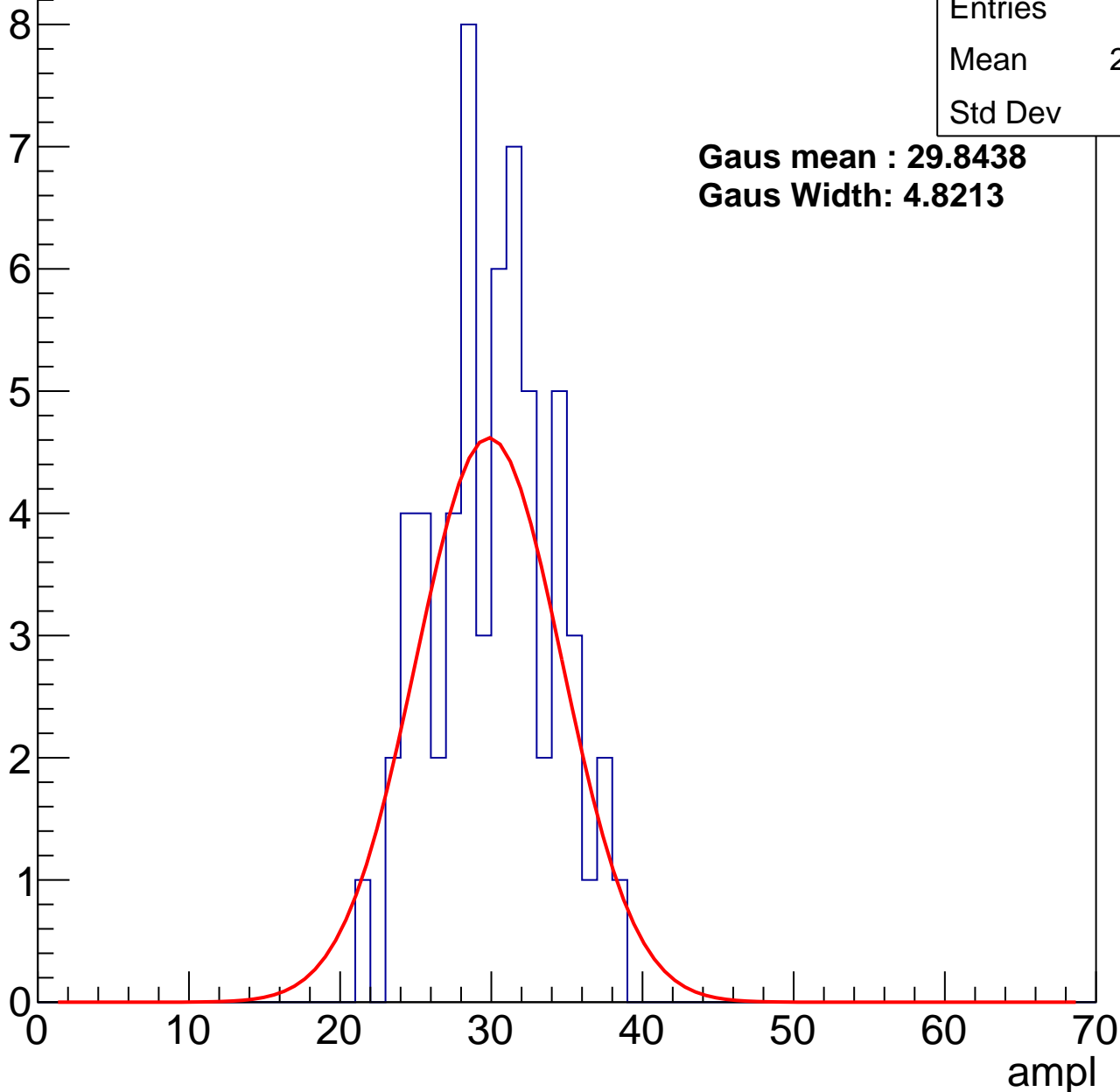
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.67
Std Dev	3.91

**Gaus mean : 29.8438**

**Gaus Width: 4.8213**



# B0L000S, U26-ch116, adc1

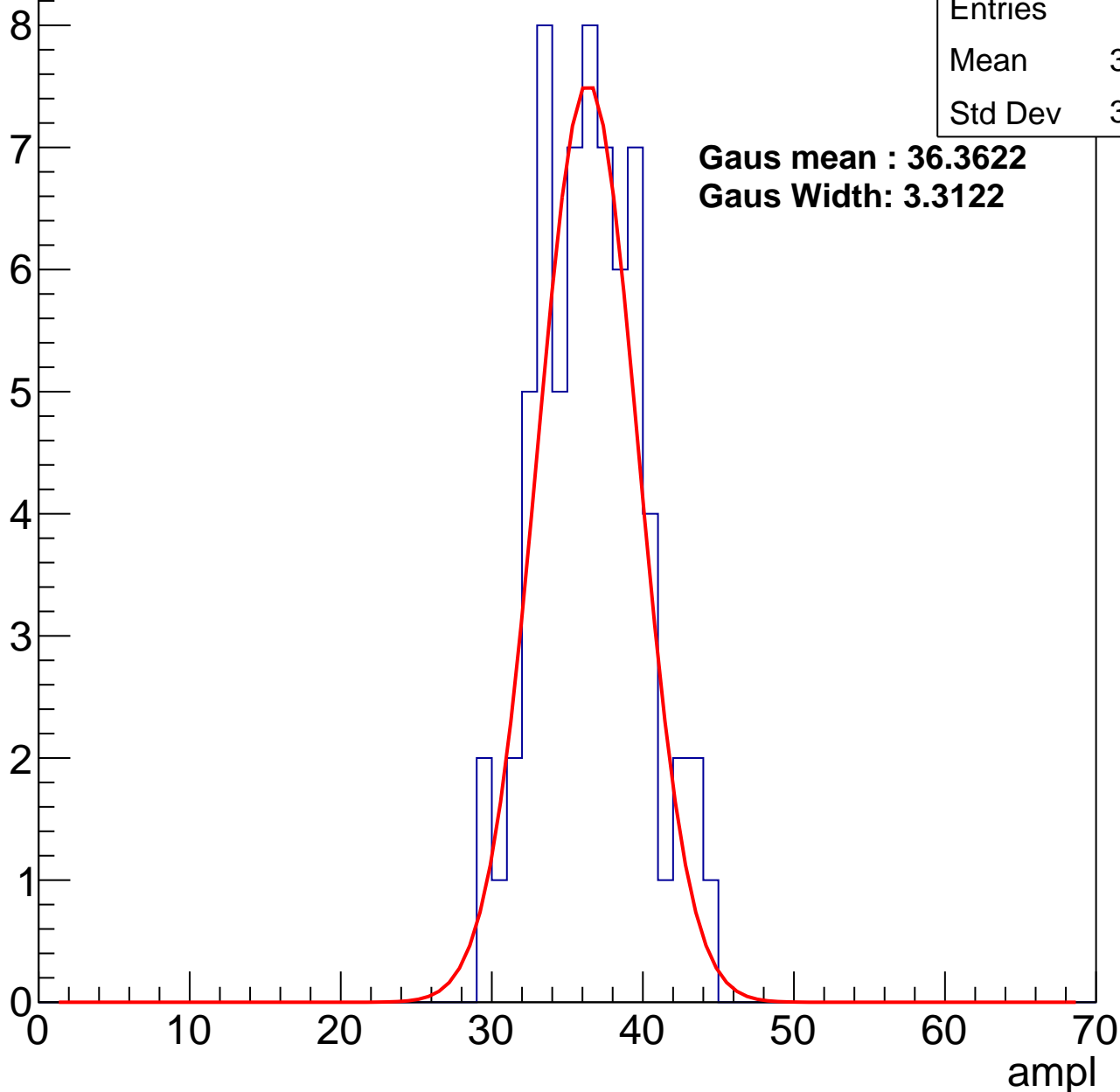
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.06
Std Dev	3.386

**Gaus mean : 36.3622**

**Gaus Width: 3.3122**



# B0L000S, U26-ch116, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	43.42
Std Dev	3.334

**Gaus mean : 43.7541**

**Gaus Width: 3.3434**

10

8

6

4

2

0

0

10

20

30

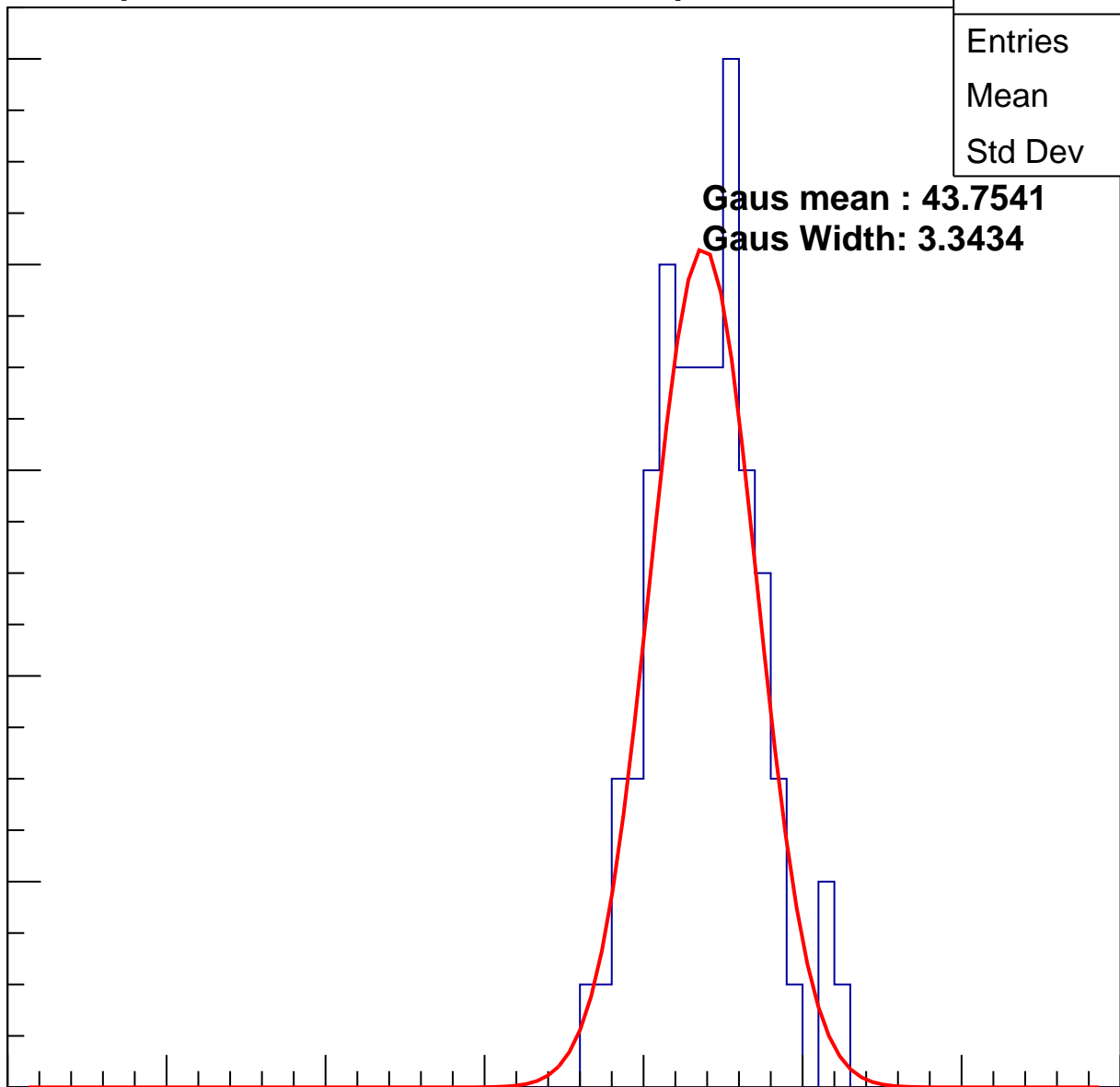
40

50

60

70

ampl

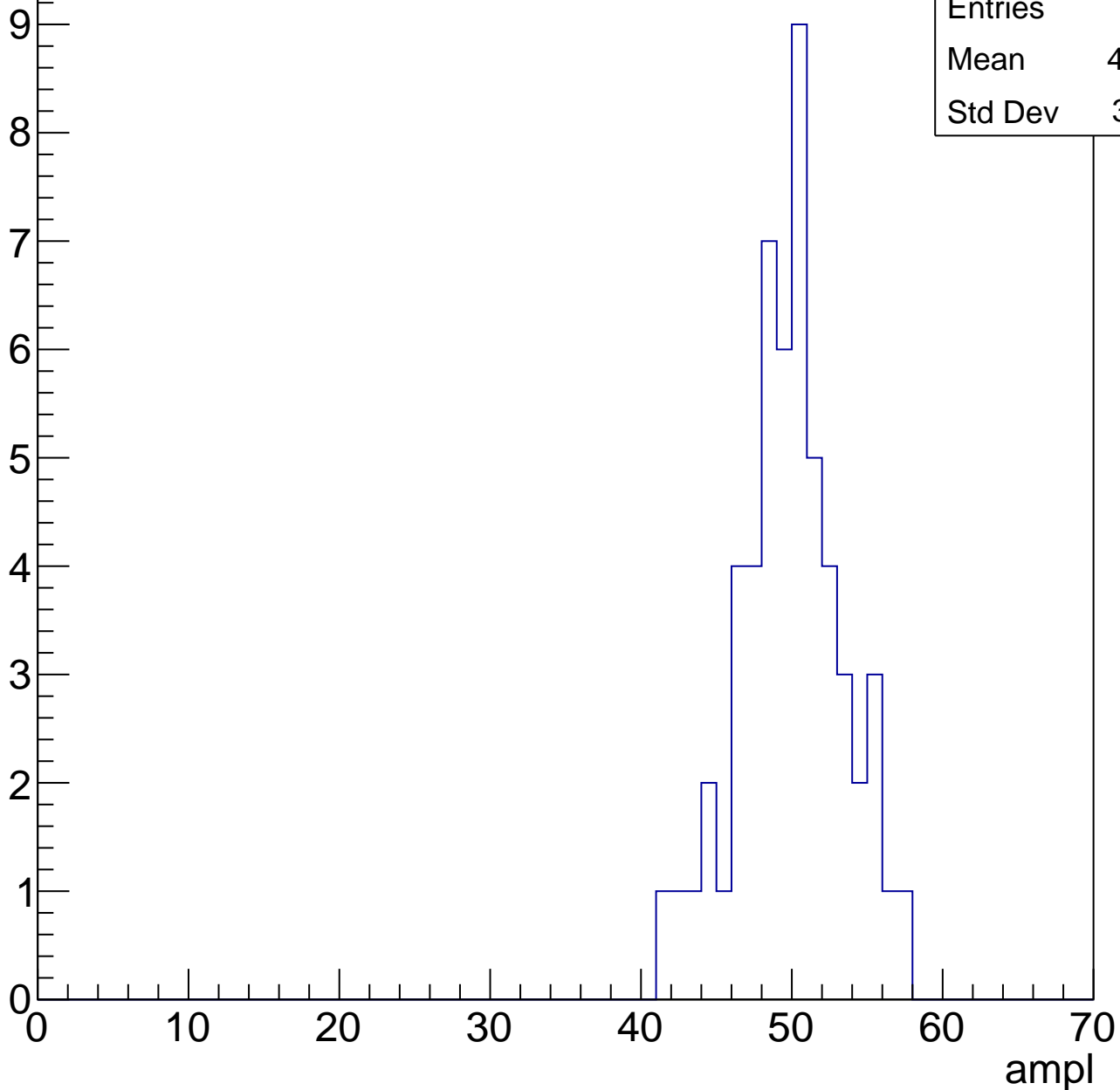


# B0L000S, U26-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.44
Std Dev	3.431

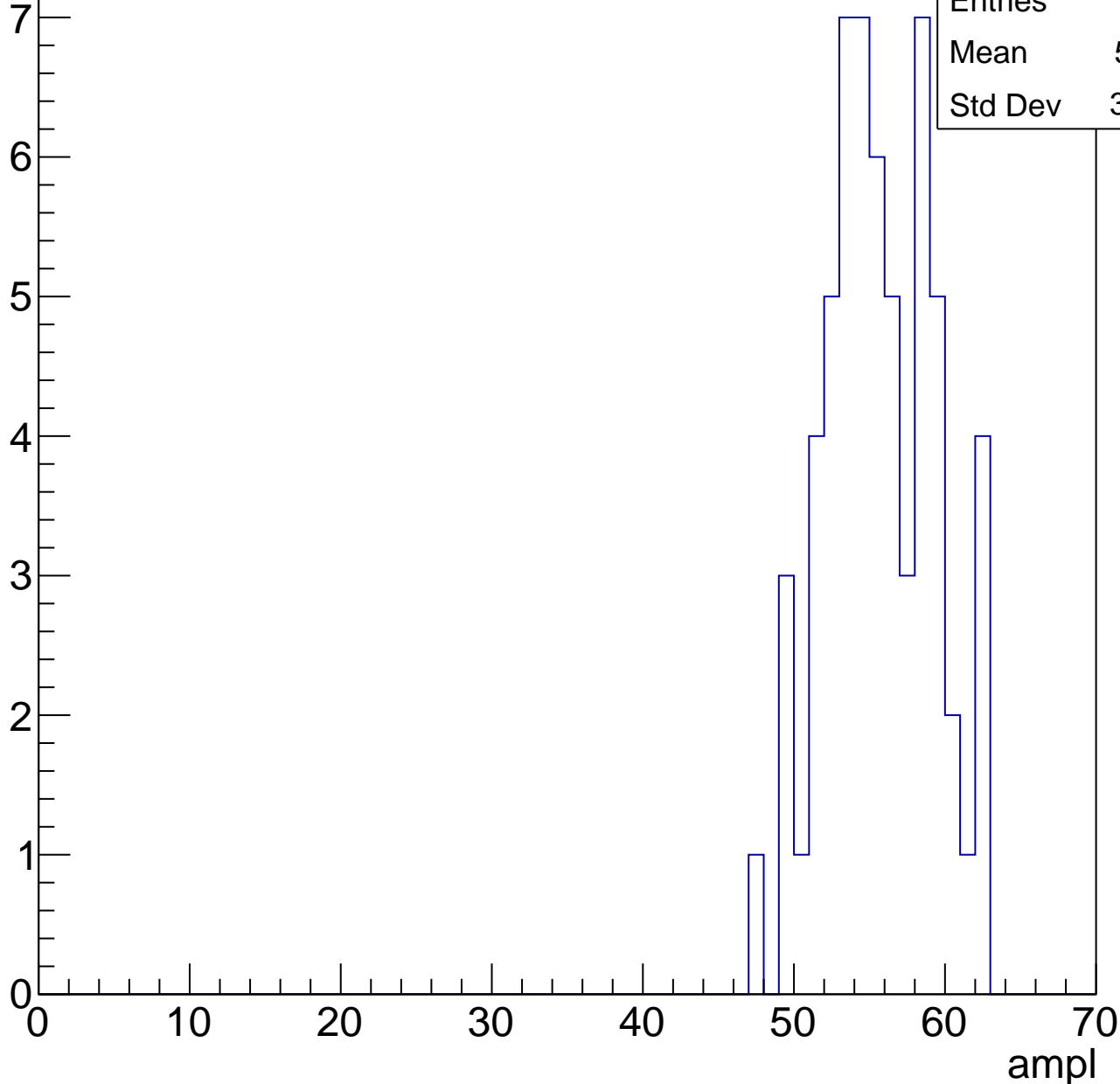


# B0L000S, U26-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

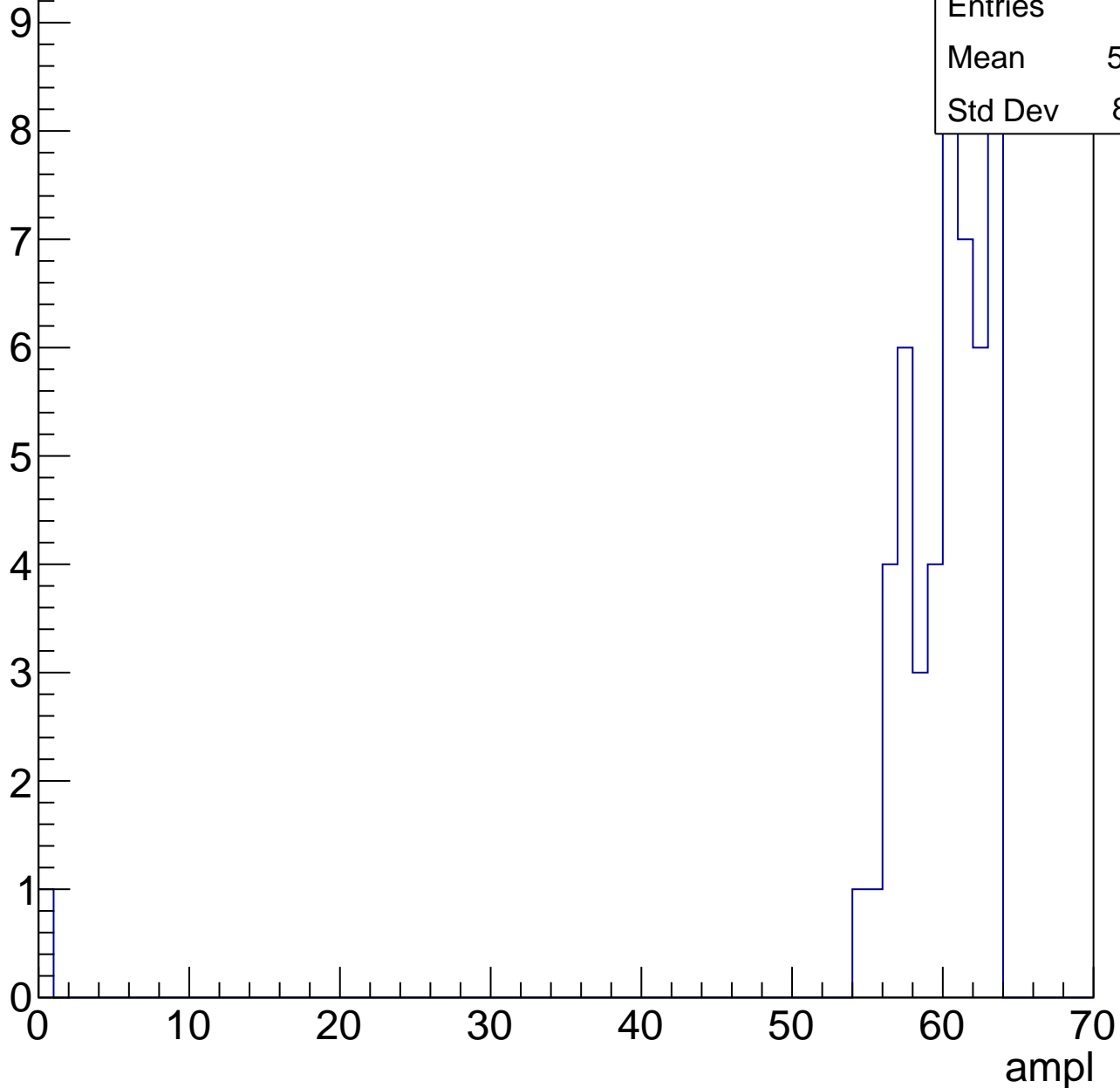
Entries	61
Mean	55.21
Std Dev	3.576



# B0L000S, U26-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

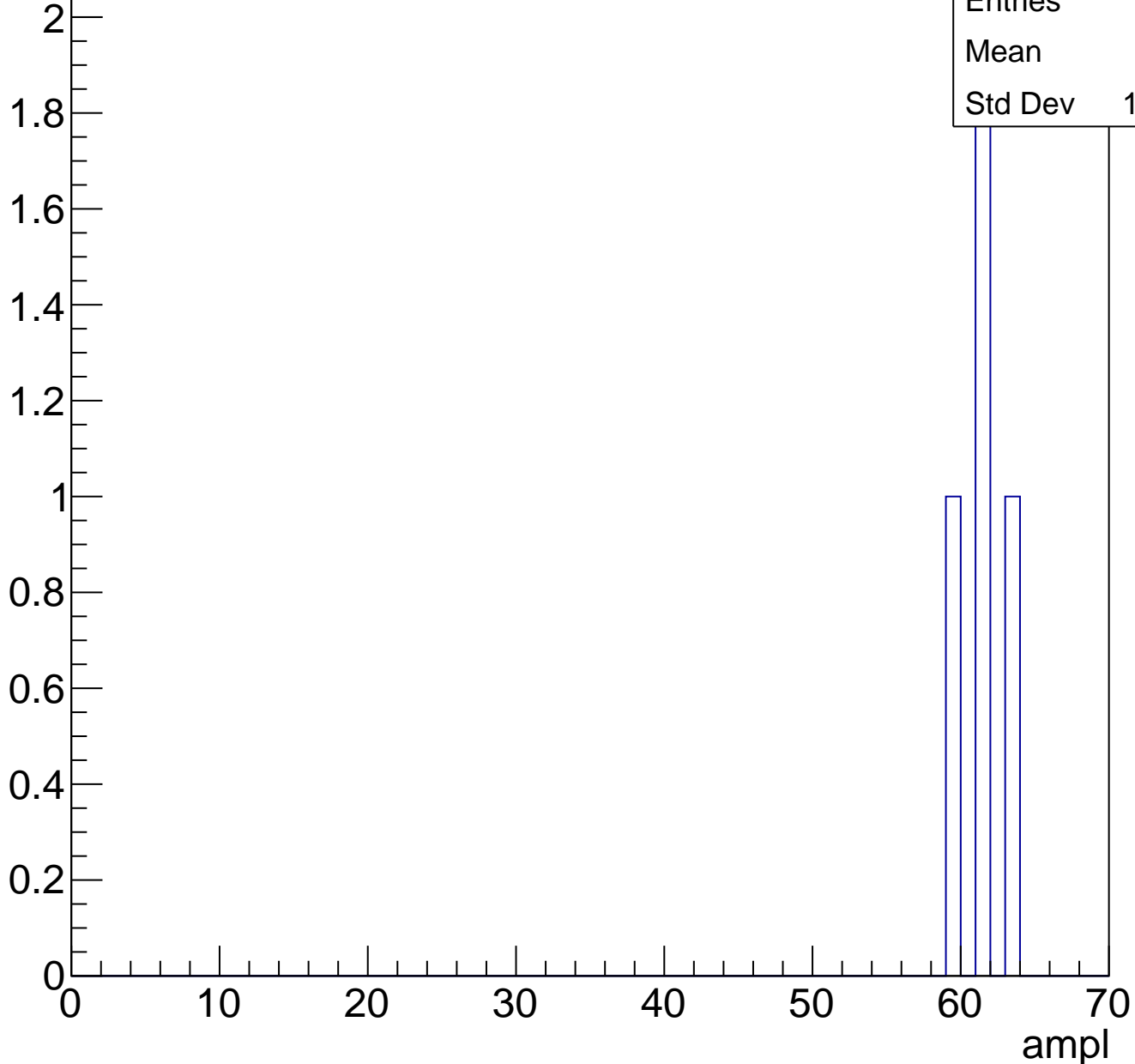
Entry



# B0L000S, U26-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	61
Std Dev	1.414



# B0L000S, U26-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch117, adc0

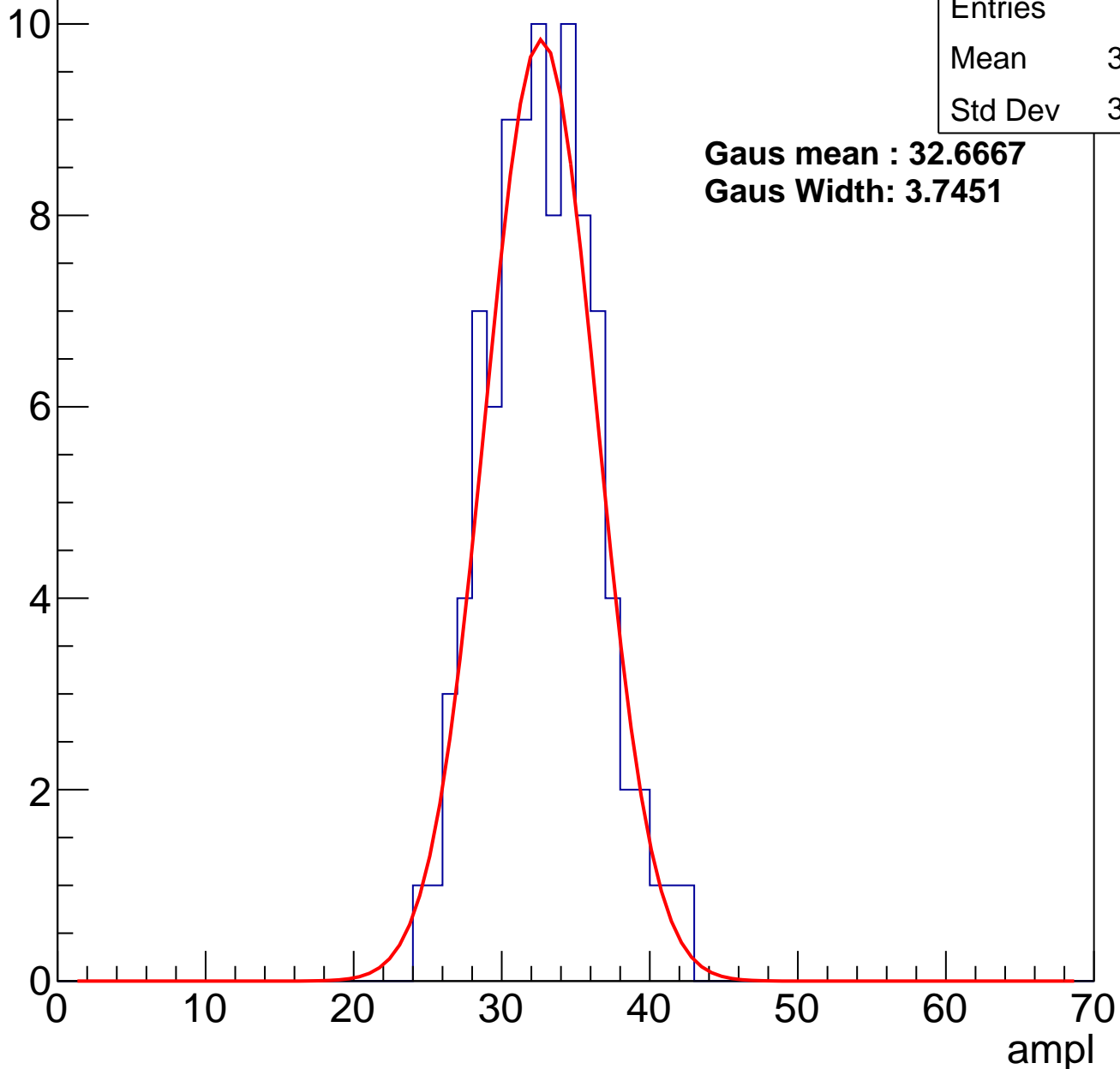
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	94
Mean	32.29
Std Dev	3.677

**Gaus mean : 32.6667**

**Gaus Width: 3.7451**

Entry



# B0L000S, U26-ch117, adc1

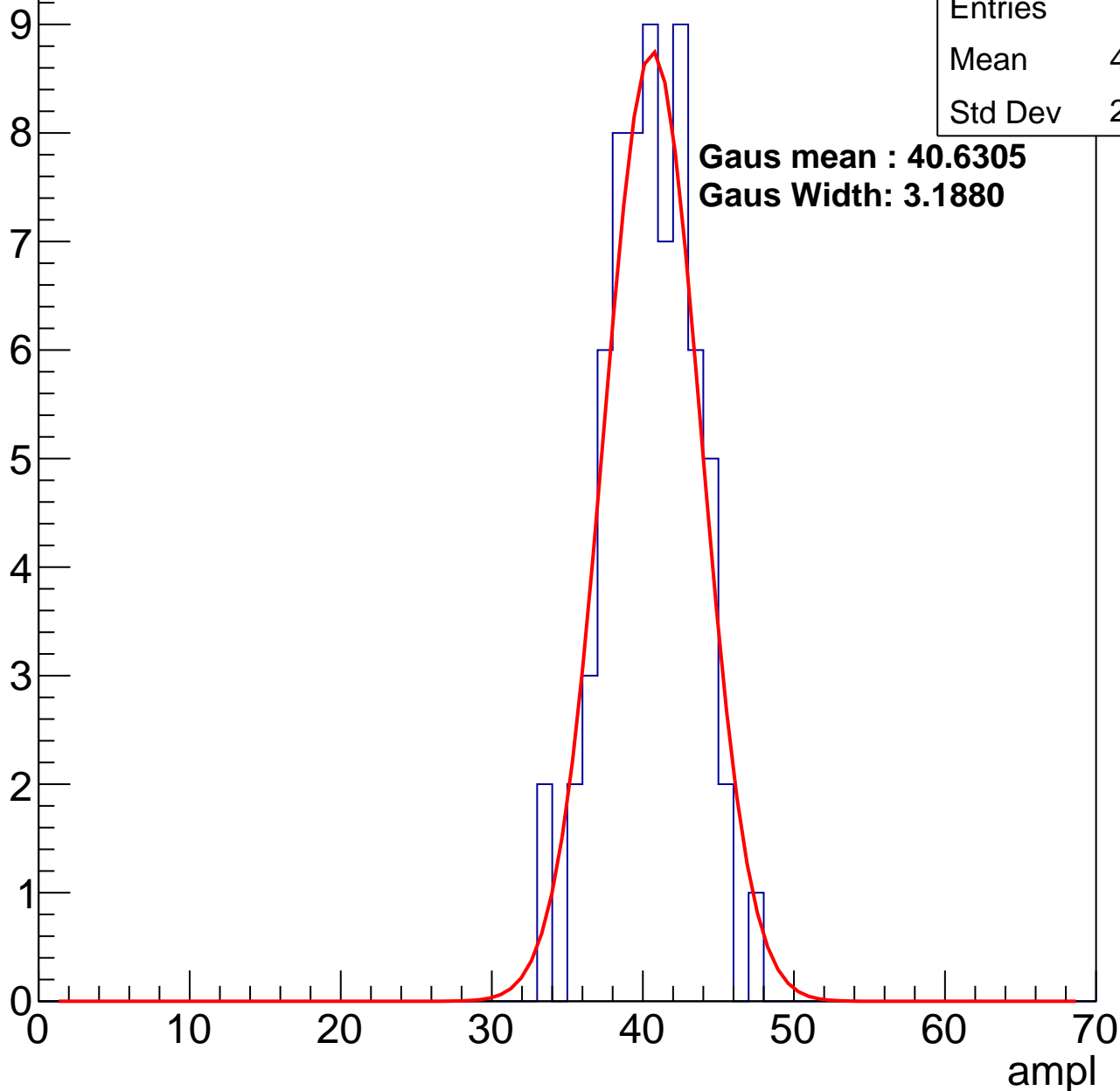
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	40.03
Std Dev	2.885

**Gaus mean : 40.6305**

**Gaus Width: 3.1880**



# B0L000S, U26-ch117, adc2

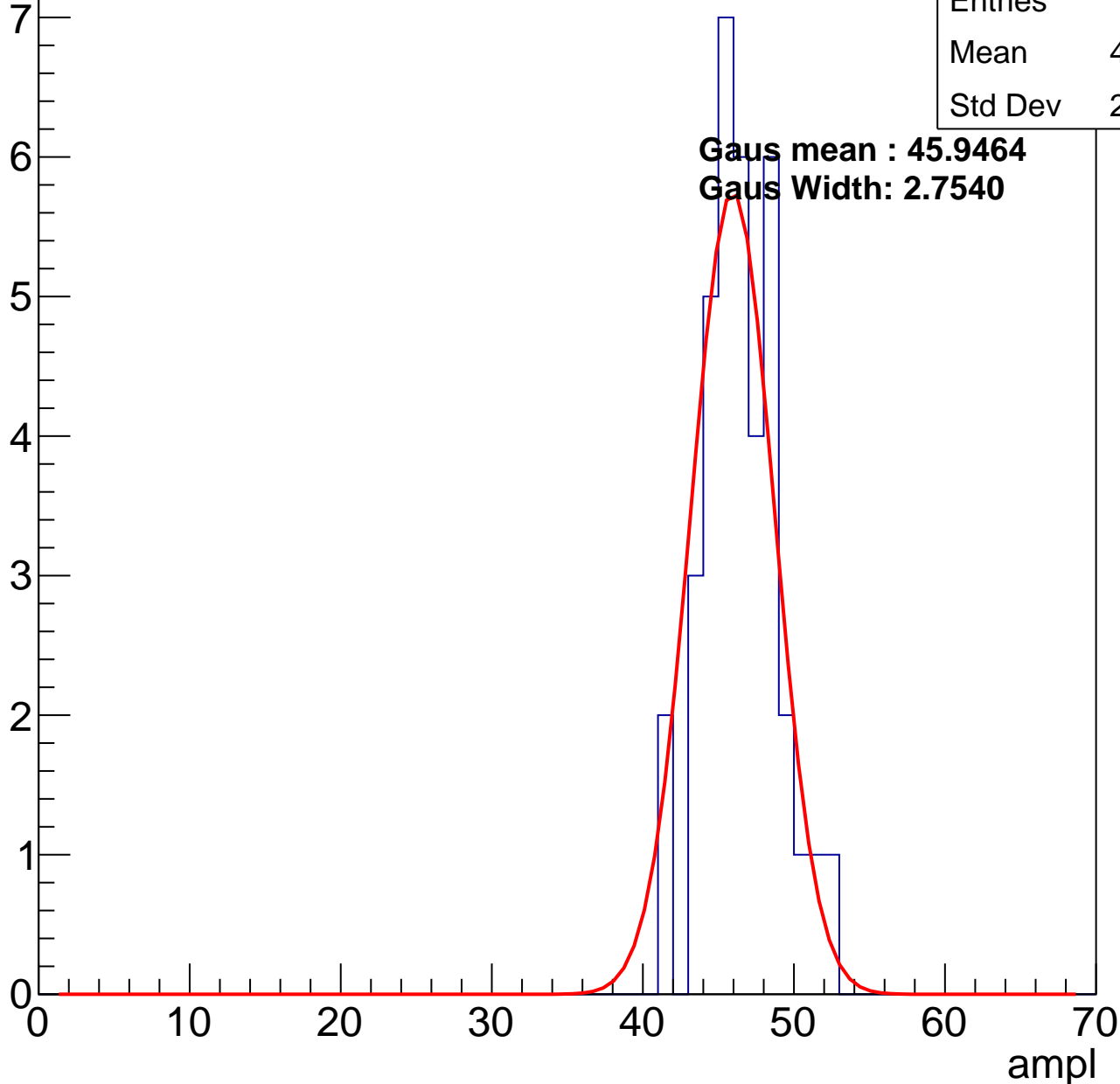
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	46.03
Std Dev	2.444

**Gaus mean : 45.9464**

**Gaus Width: 2.7540**



# B0L000S, U26-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	61
Mean	50.51
Std Dev	2.962

Entry

10

8

6

4

2

0

0

10

20

30

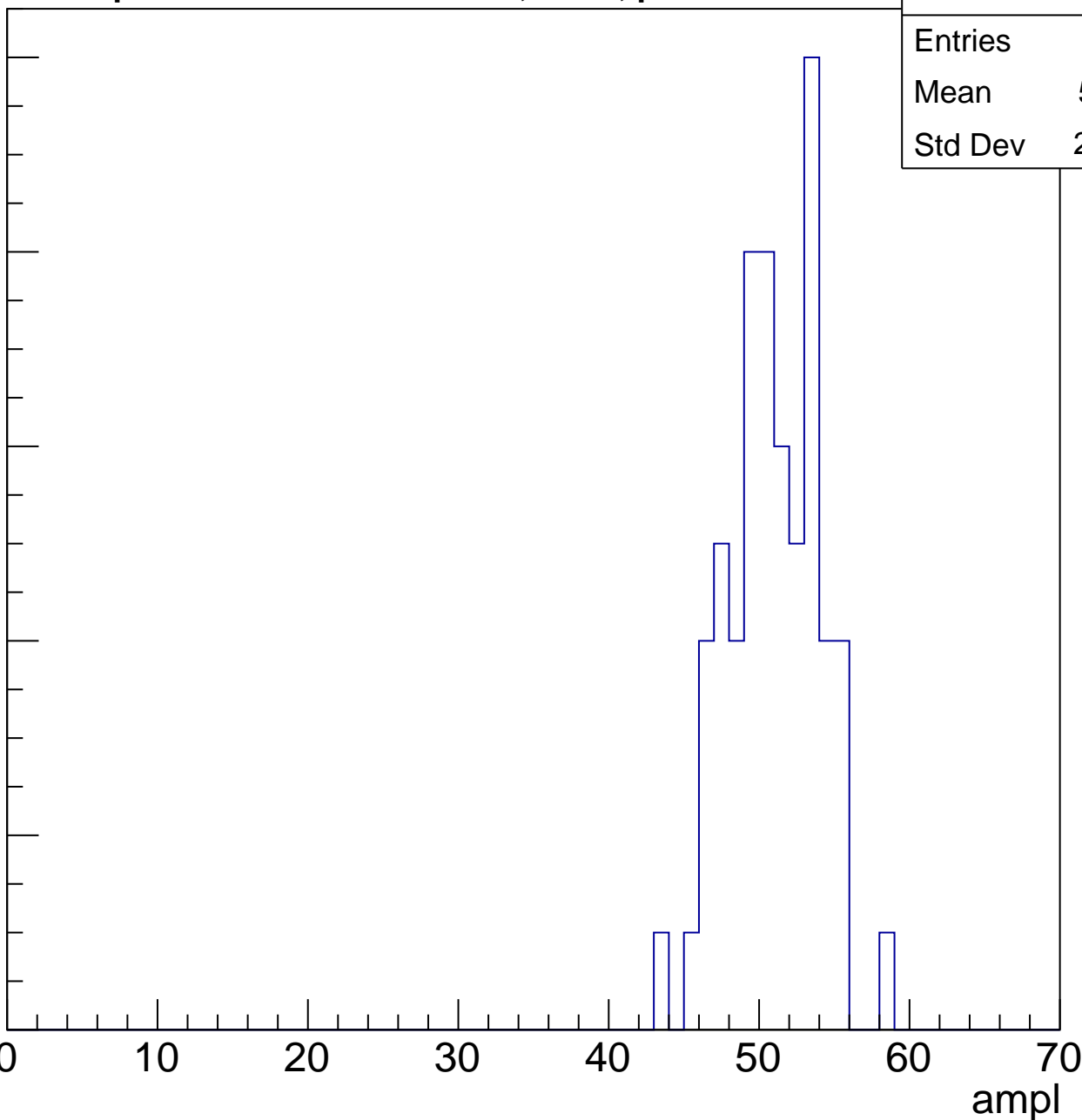
40

50

60

70

ampl

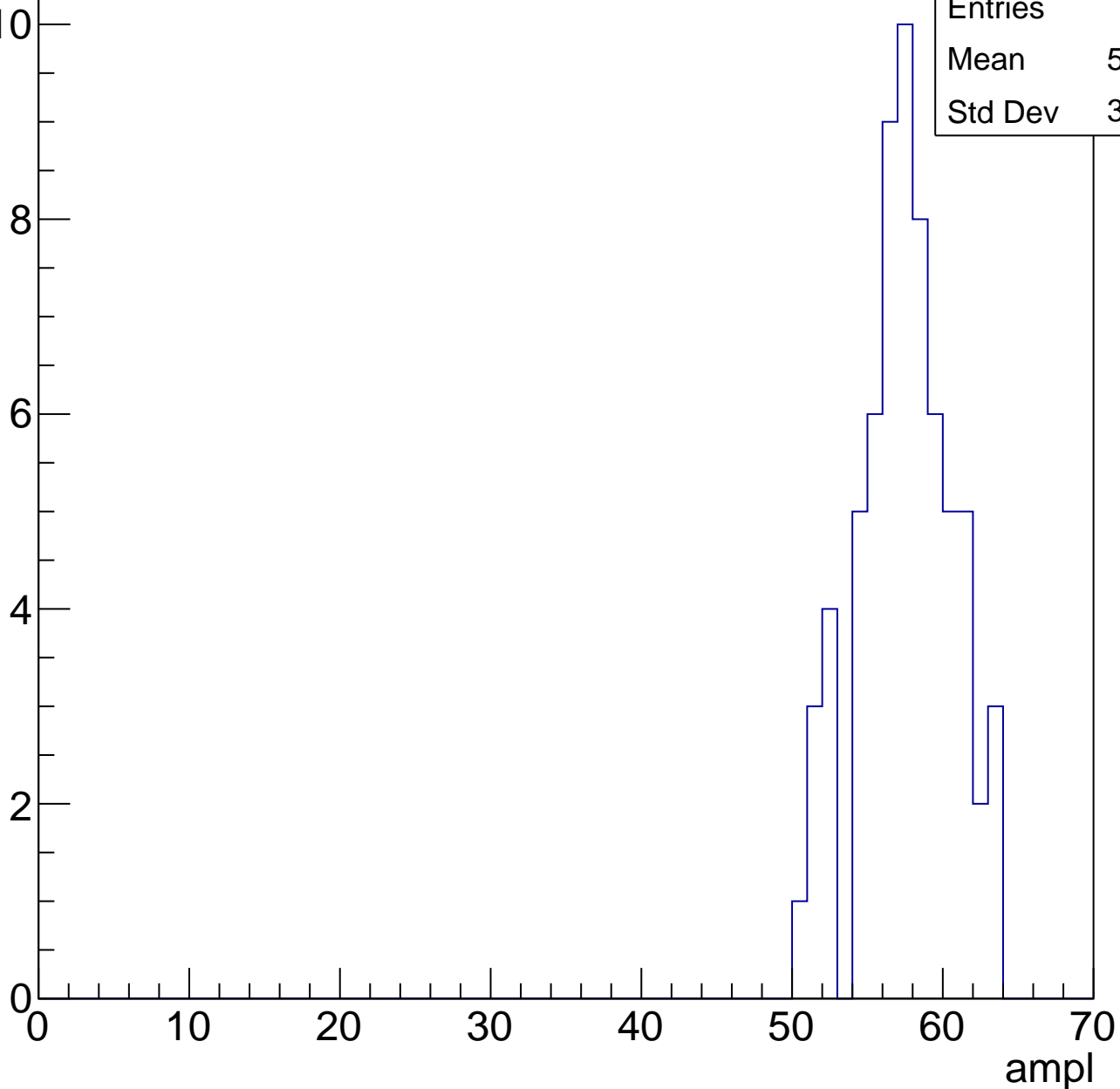


# B0L000S, U26-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	57.03
Std Dev	3.115

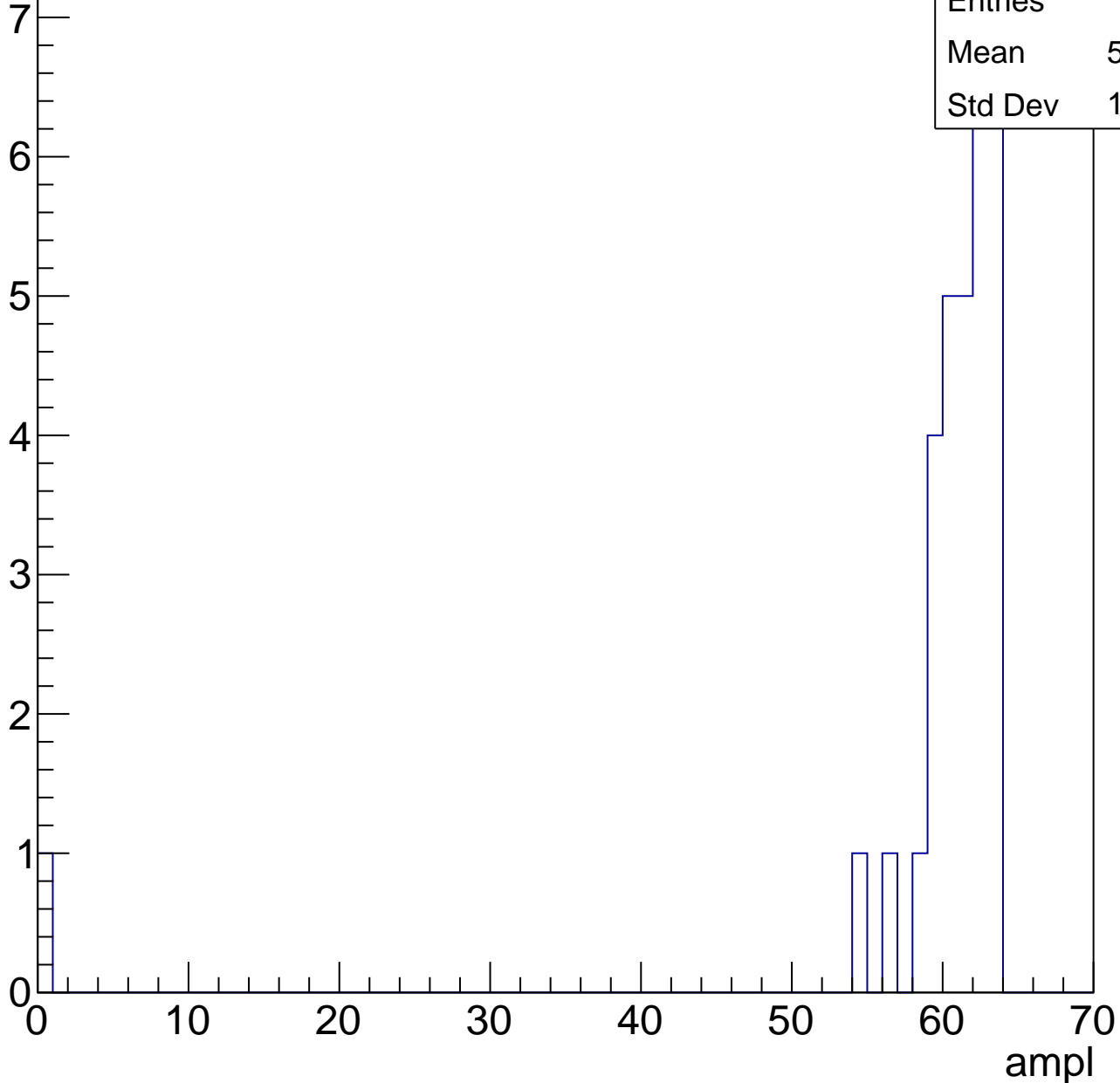


# B0L000S, U26-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.88
Std Dev	10.78



# B0L000S, U26-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U26-ch118, adc0

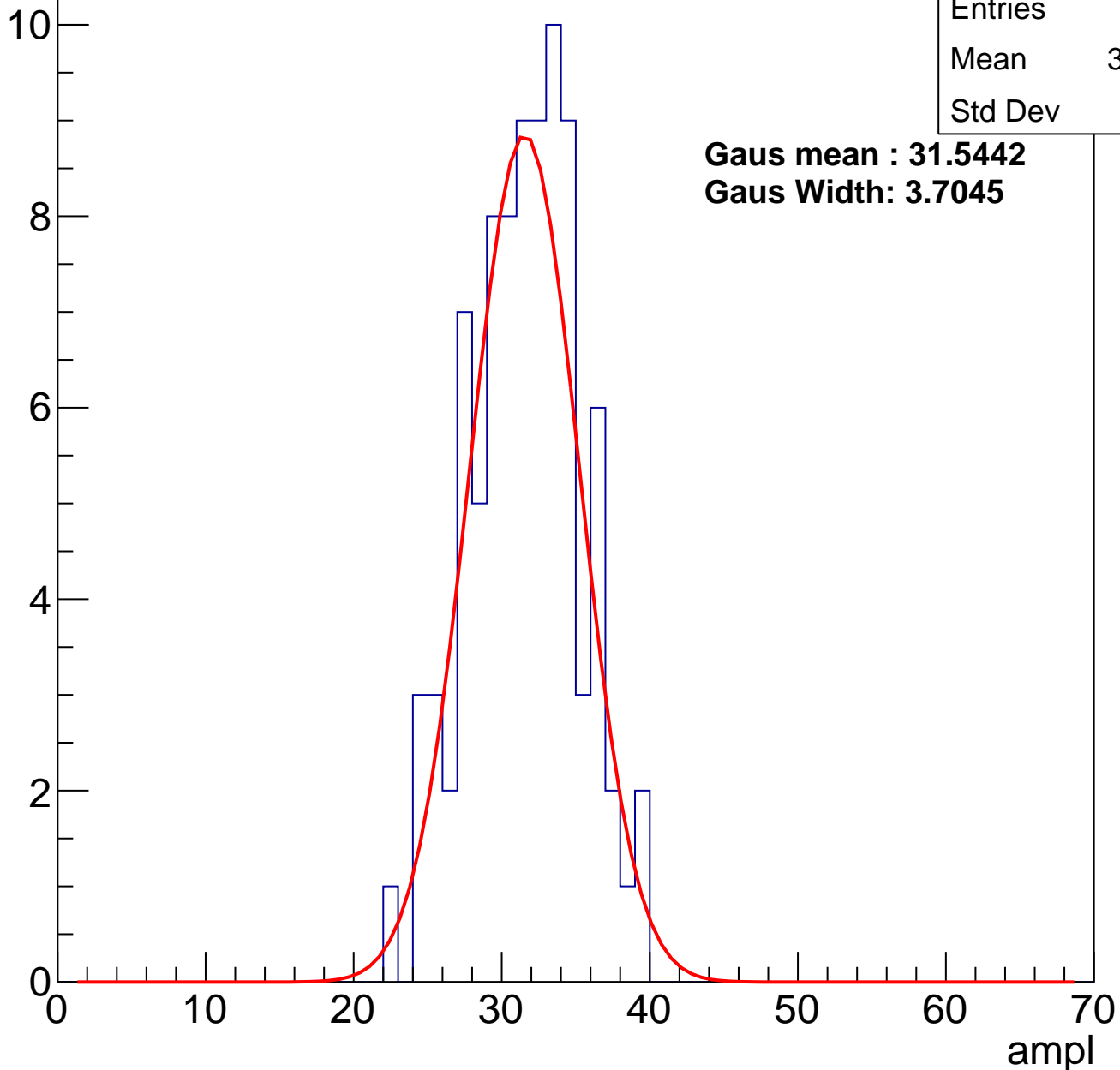
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	31.09
Std Dev	3.63

**Gaus mean : 31.5442**

**Gaus Width: 3.7045**

Entry



# B0L000S, U26-ch118, adc1

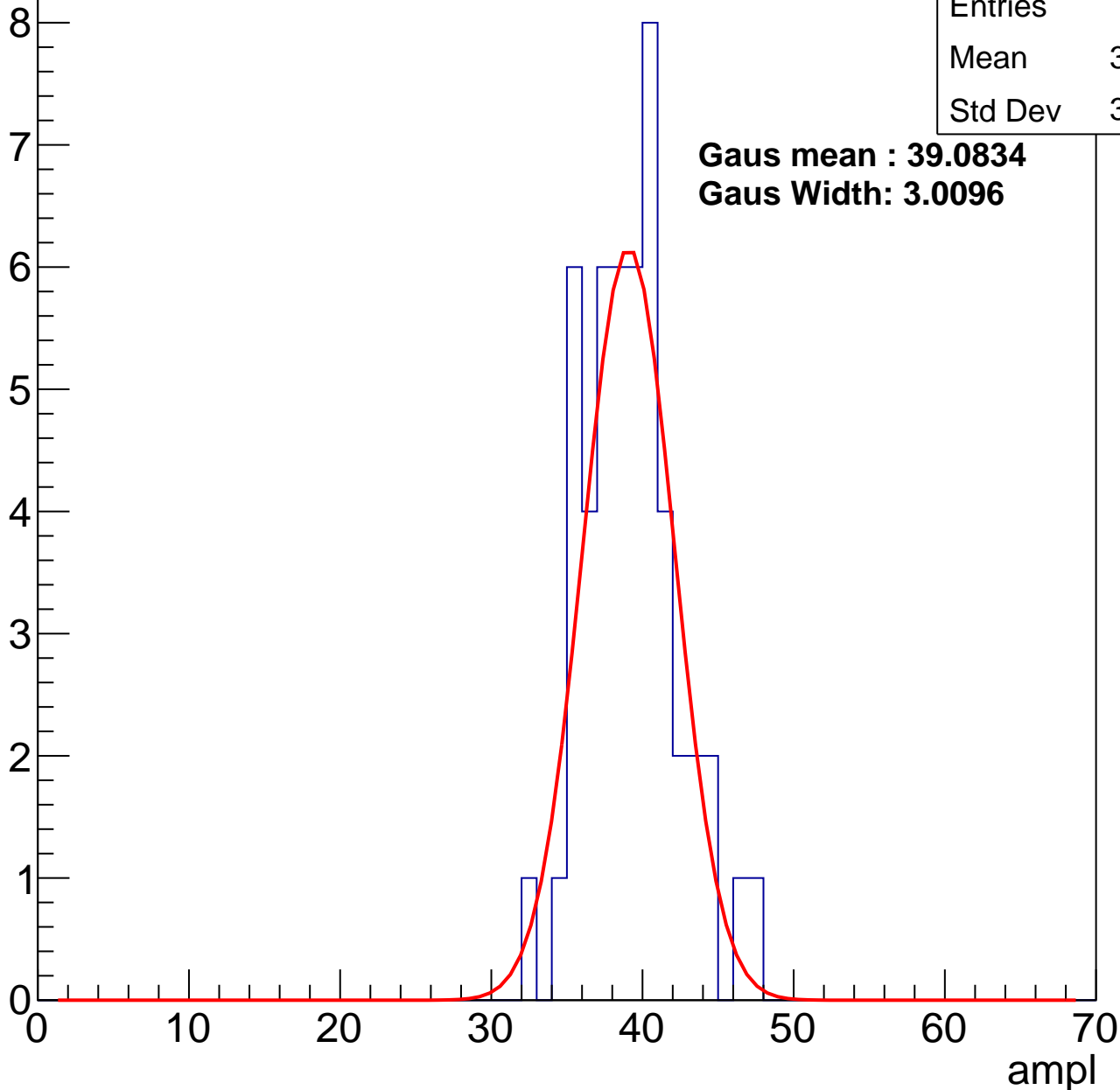
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	38.78
Std Dev	3.068

**Gaus mean : 39.0834**

**Gaus Width: 3.0096**



# B0L000S, U26-ch118, adc2

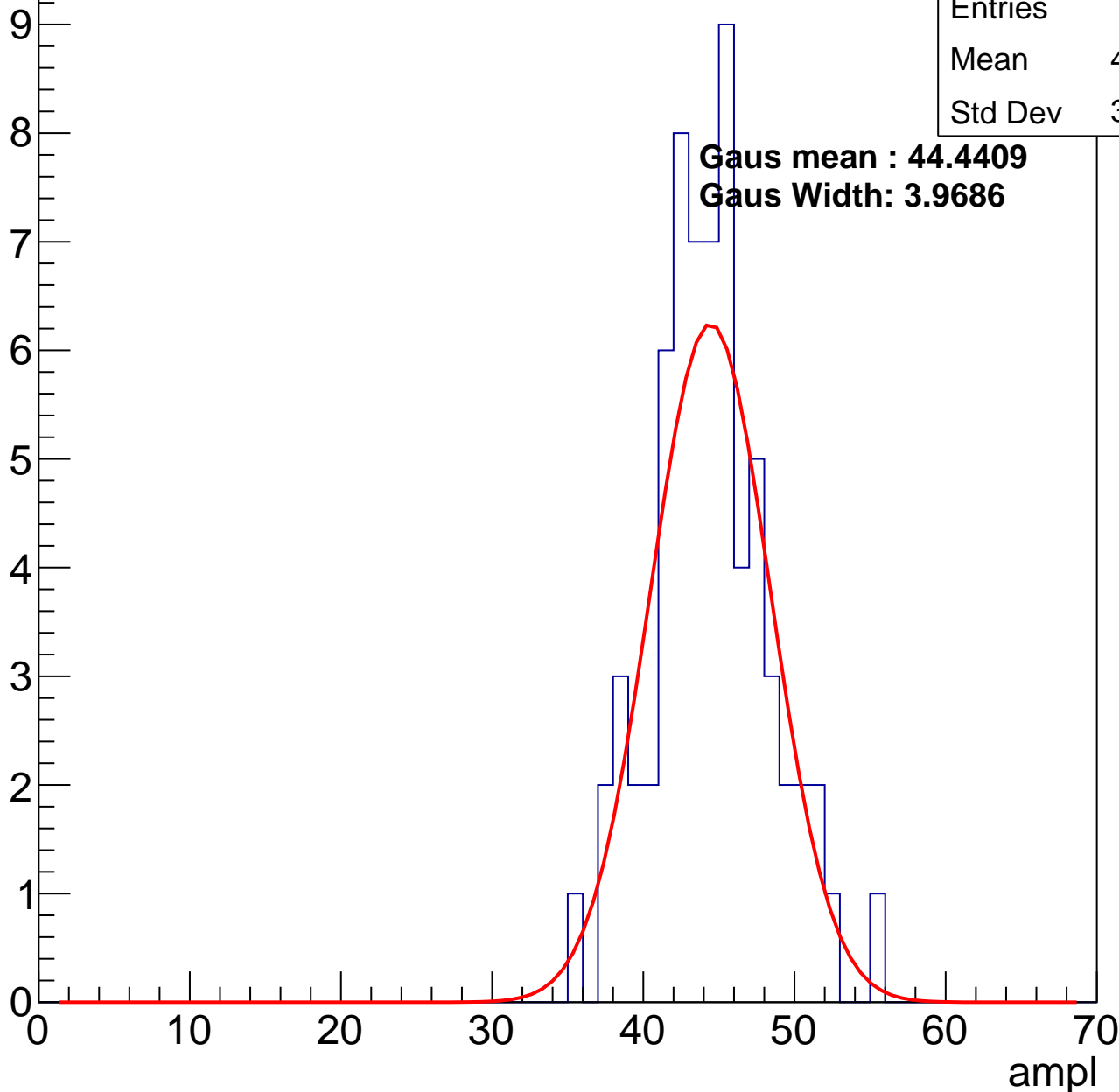
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	43.99
Std Dev	3.834

**Gaus mean : 44.4409**

**Gaus Width: 3.9686**

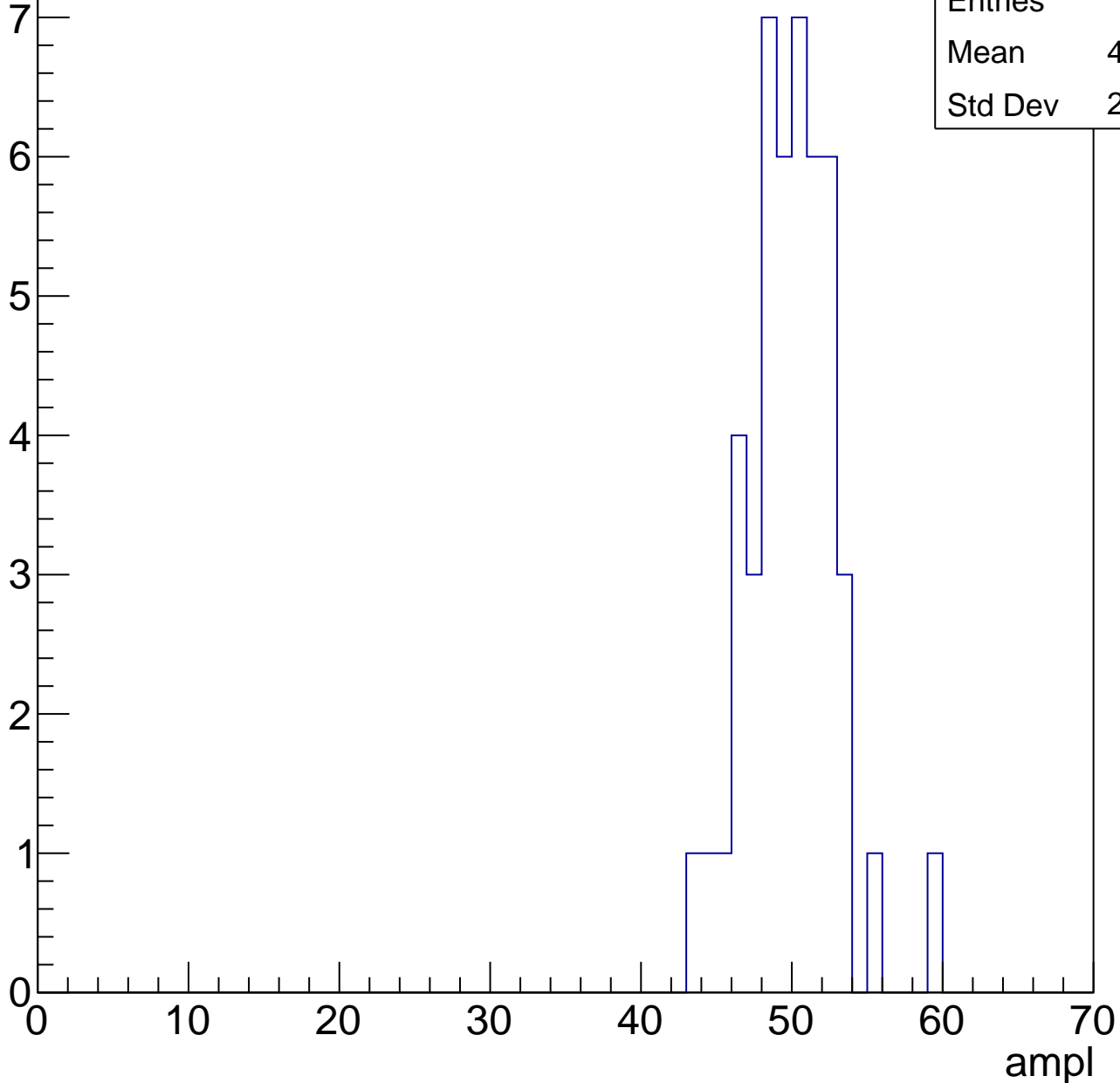


# B0L000S, U26-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	49.53
Std Dev	2.872

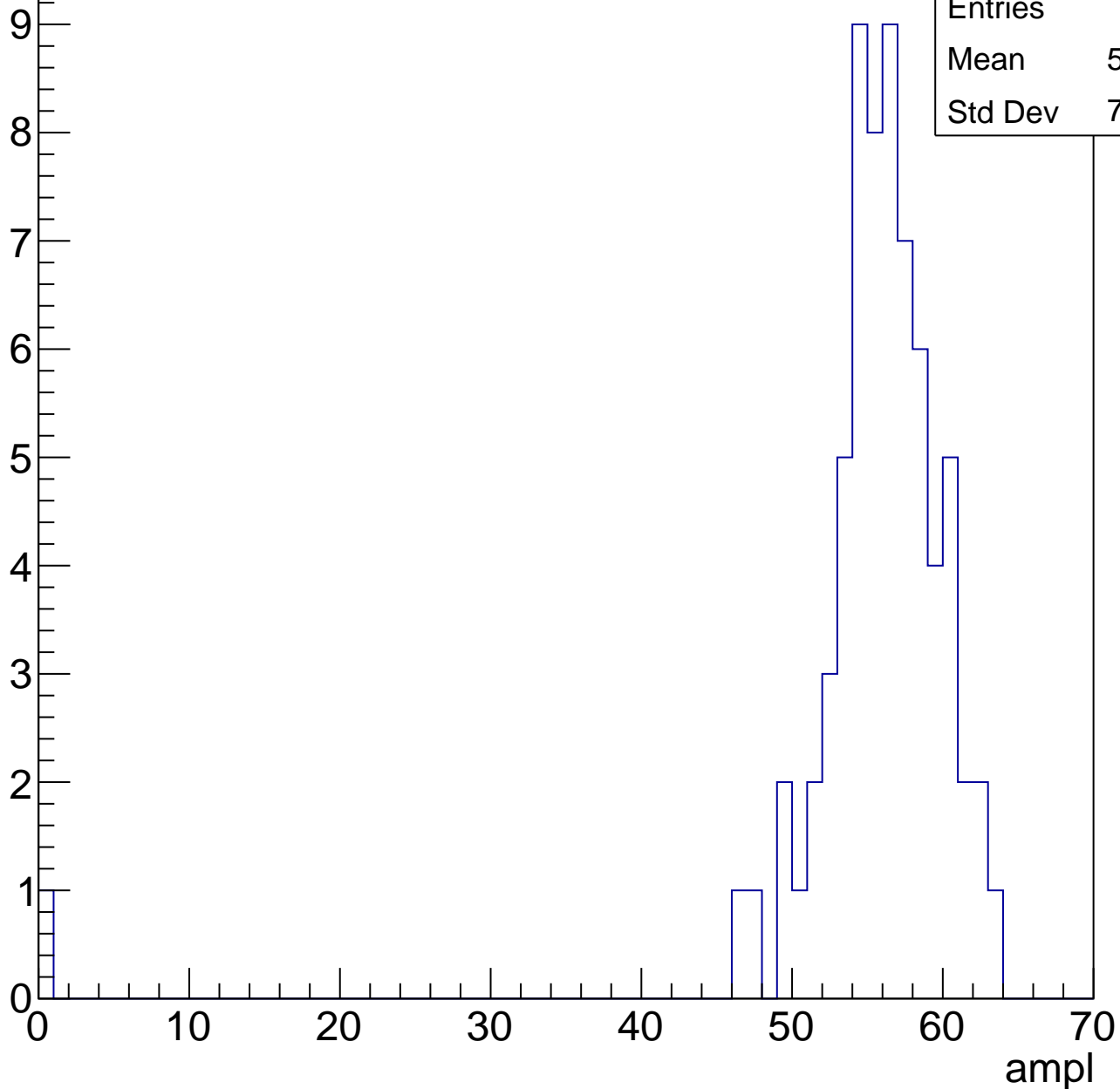


# B0L000S, U26-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	54.87
Std Dev	7.487

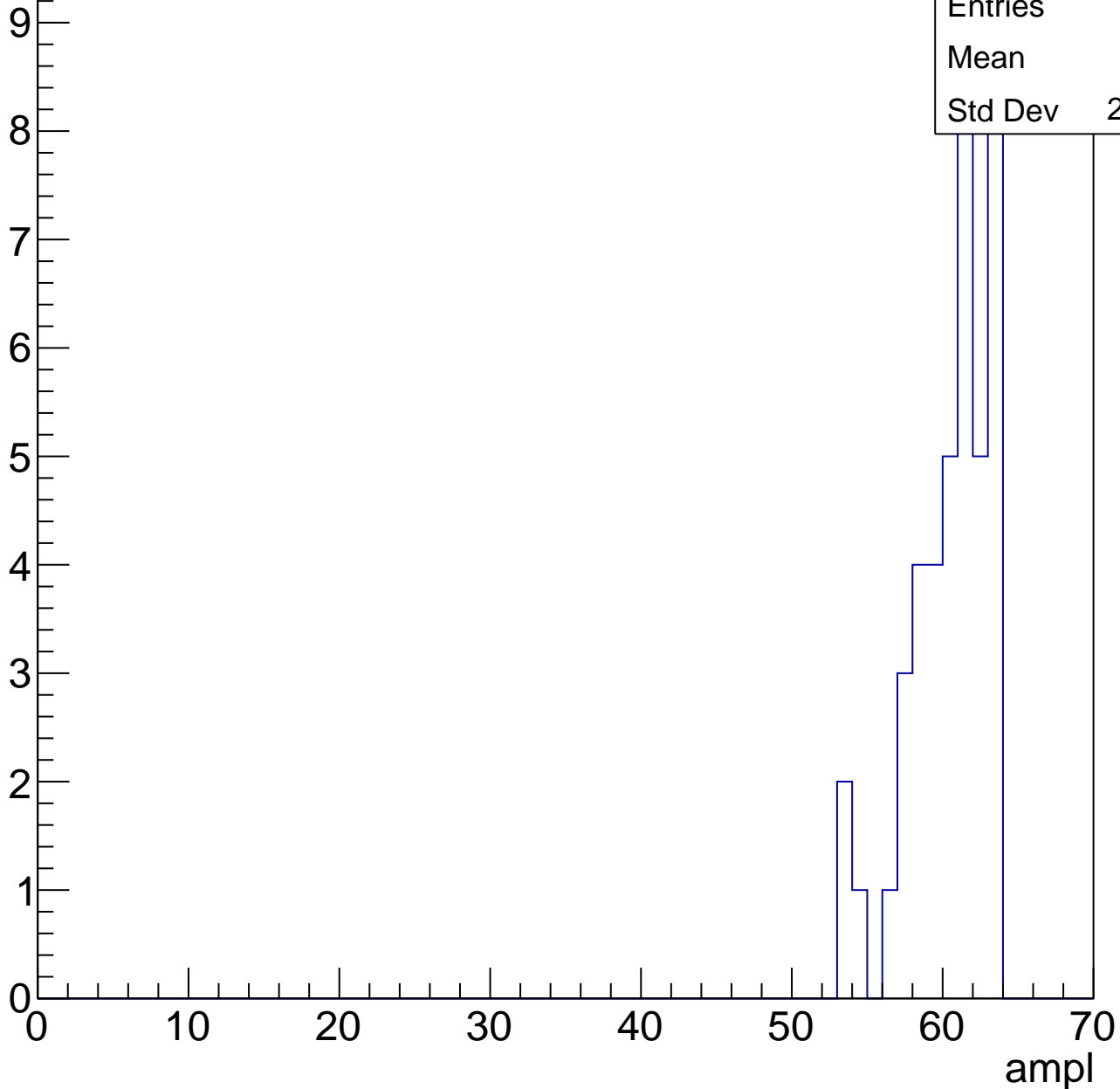


# B0L000S, U26-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

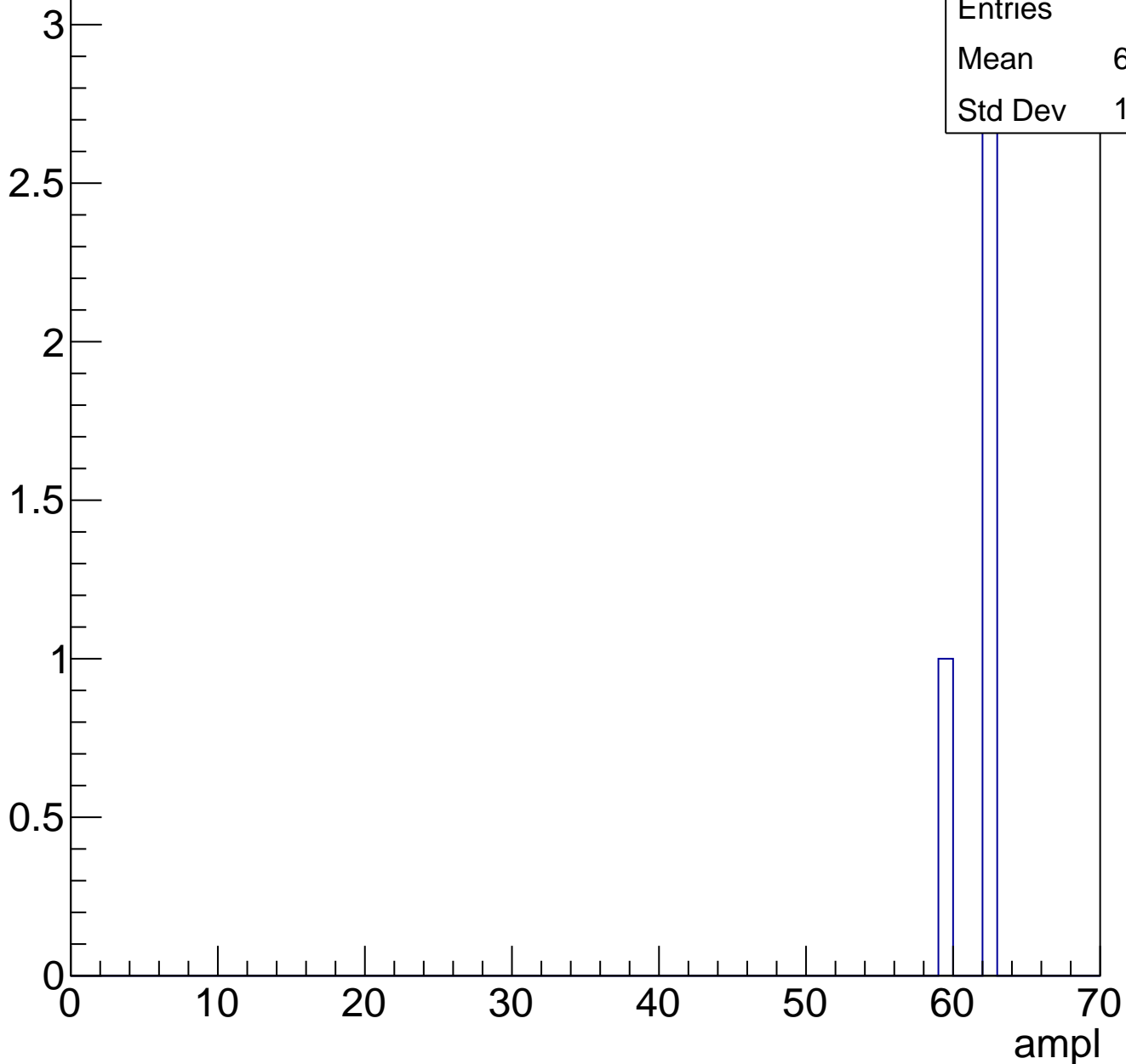
Entries	42
Mean	60
Std Dev	2.699



# B0L000S, U26-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U26-ch119, adc0

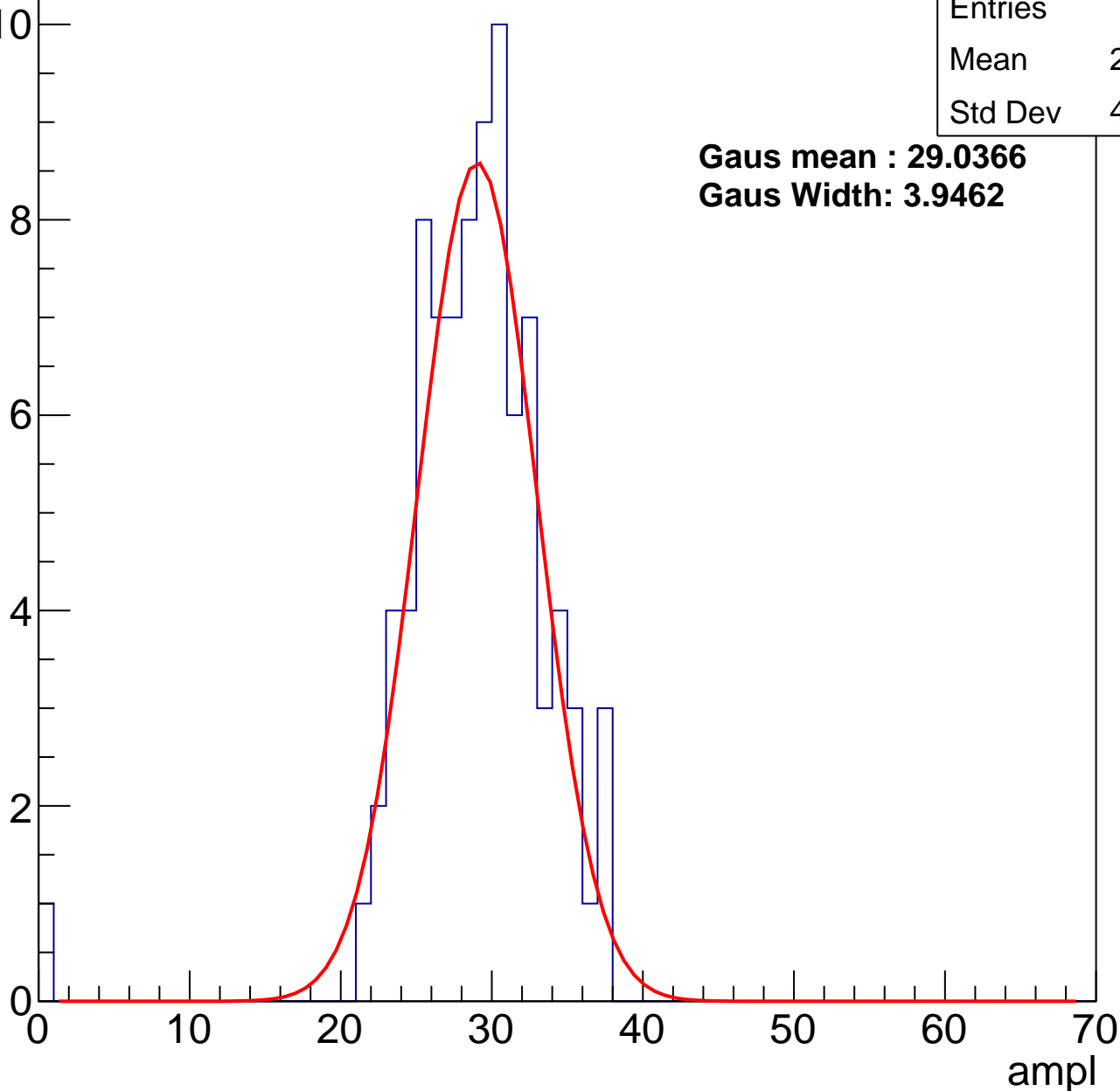
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	88
Mean	28.48
Std Dev	4.819

**Gaus mean : 29.0366**

**Gaus Width: 3.9462**



# B0L000S, U26-ch119, adc1

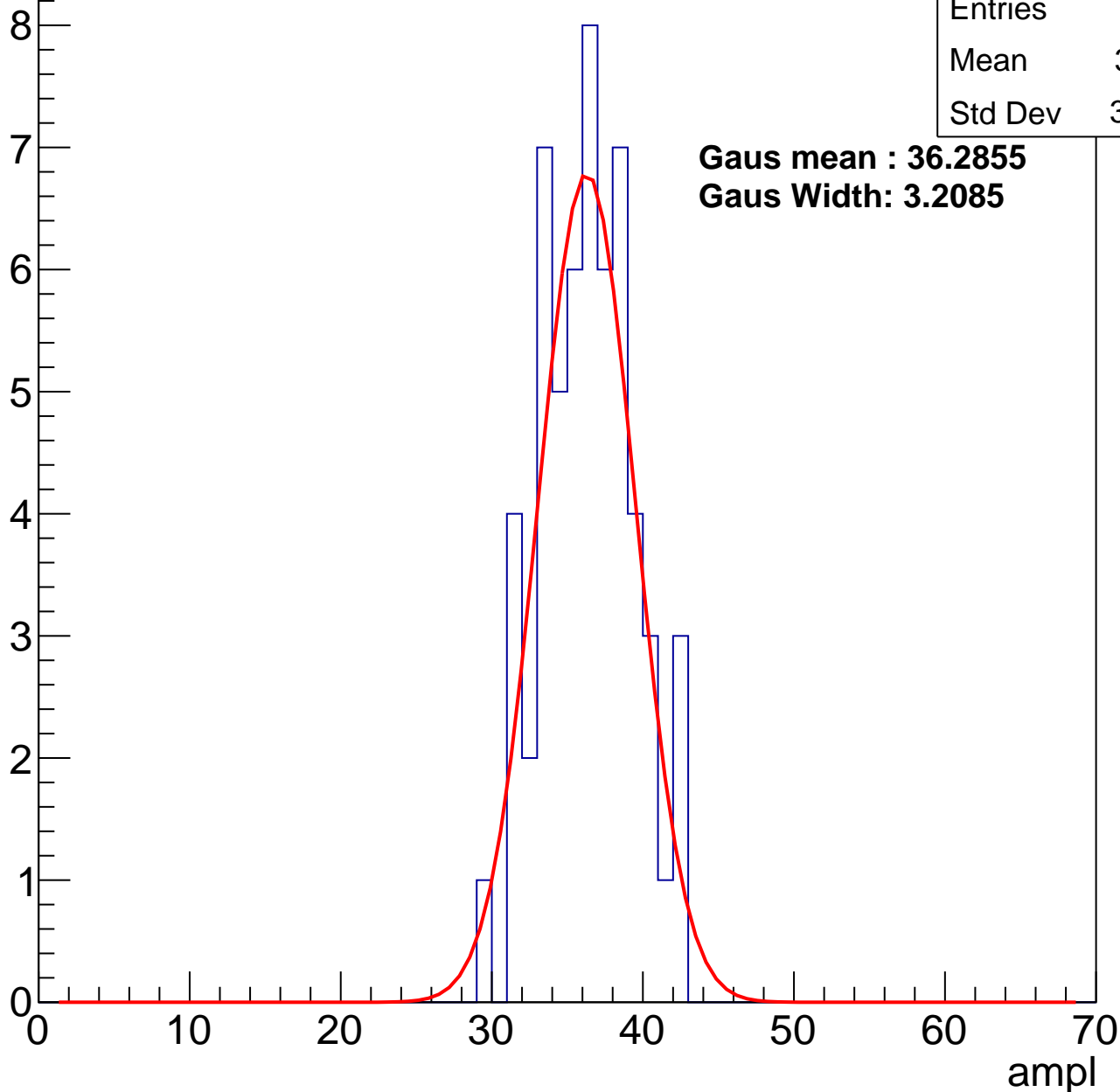
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.91
Std Dev	3.022

**Gaus mean : 36.2855**

**Gaus Width: 3.2085**



# B0L000S, U26-ch119, adc2

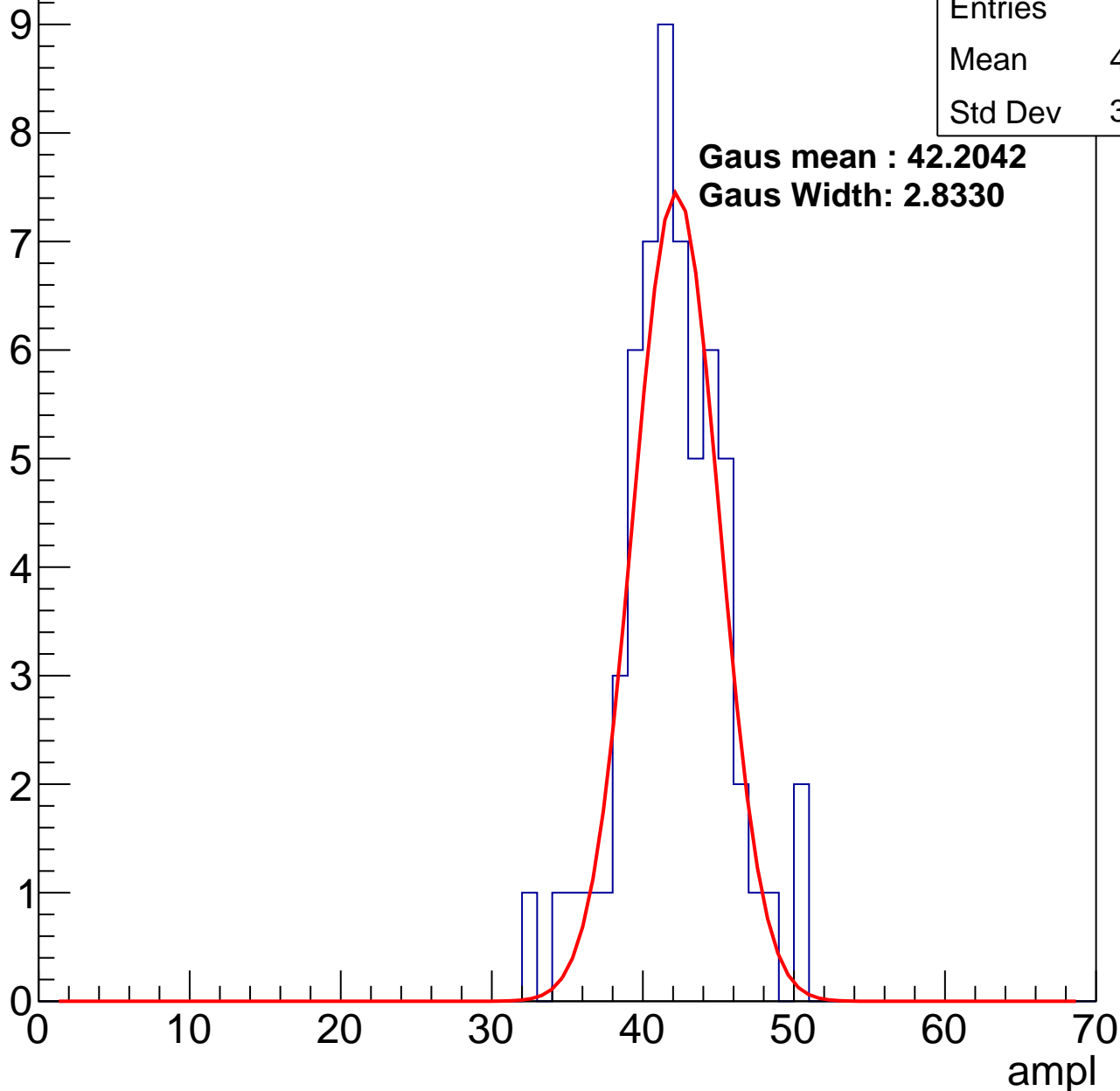
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	41.63
Std Dev	3.444

**Gaus mean : 42.2042**

**Gaus Width: 2.8330**

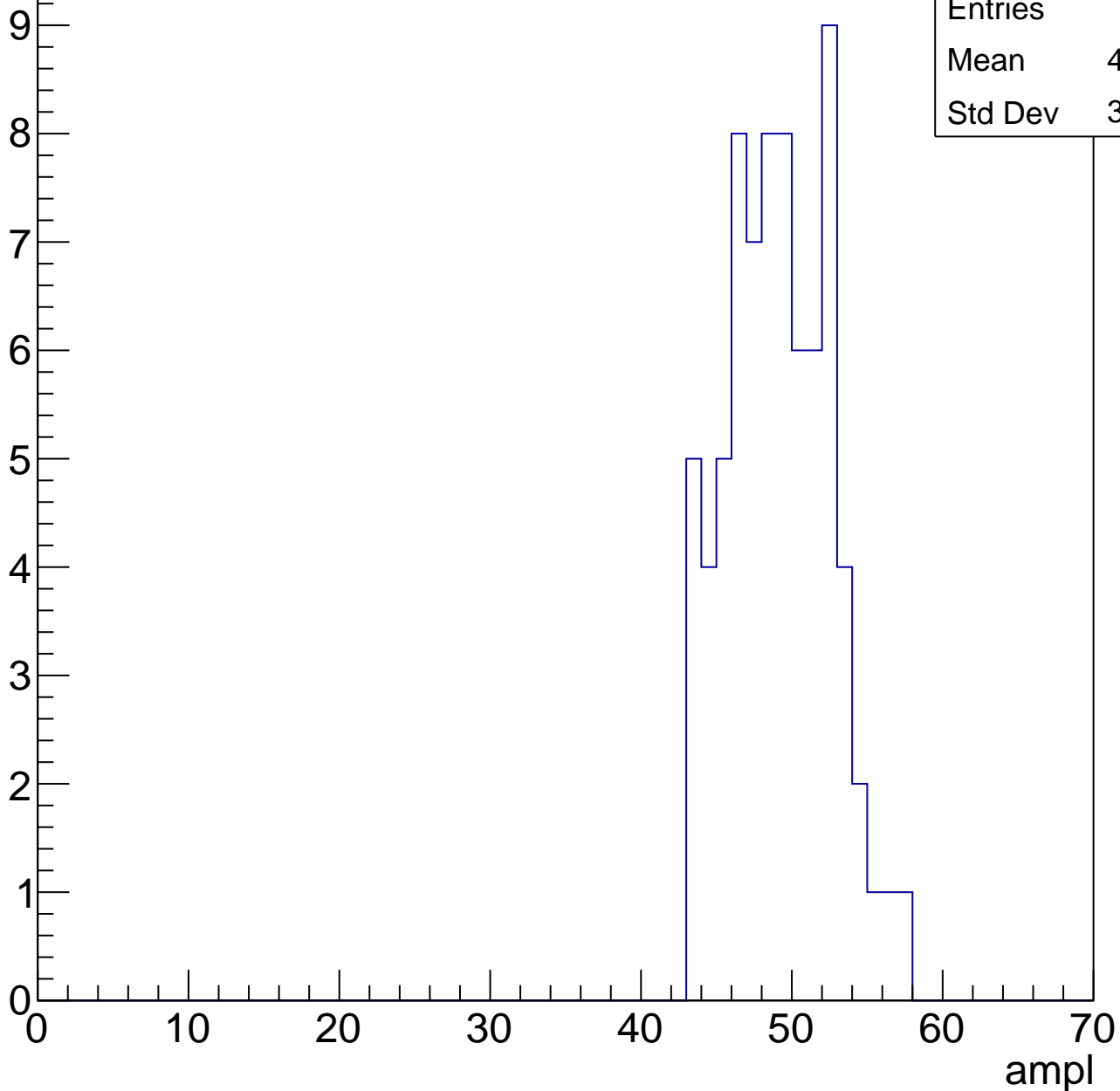


# B0L000S, U26-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	48.68
Std Dev	3.335

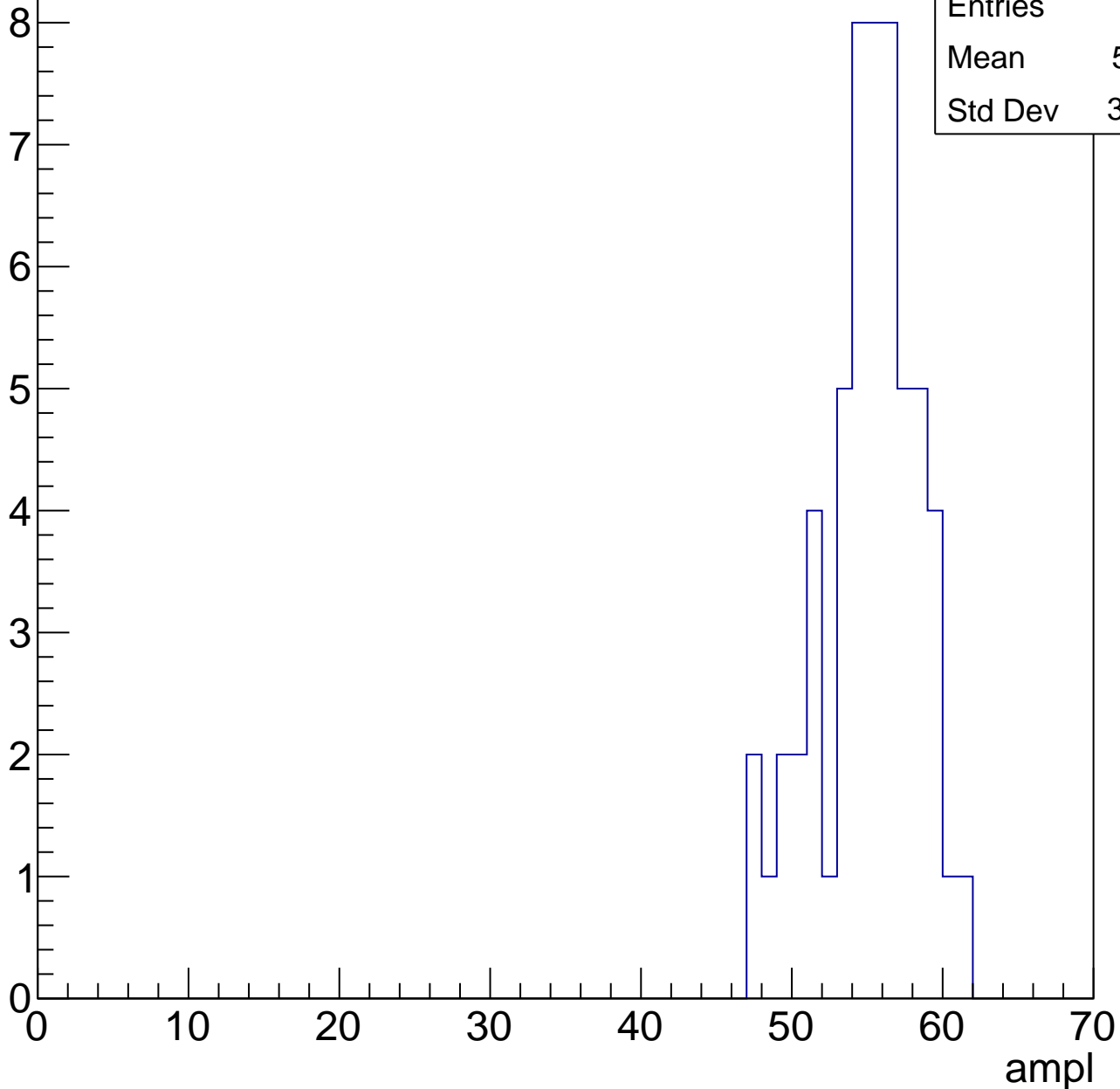


# B0L000S, U26-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	54.61
Std Dev	3.216

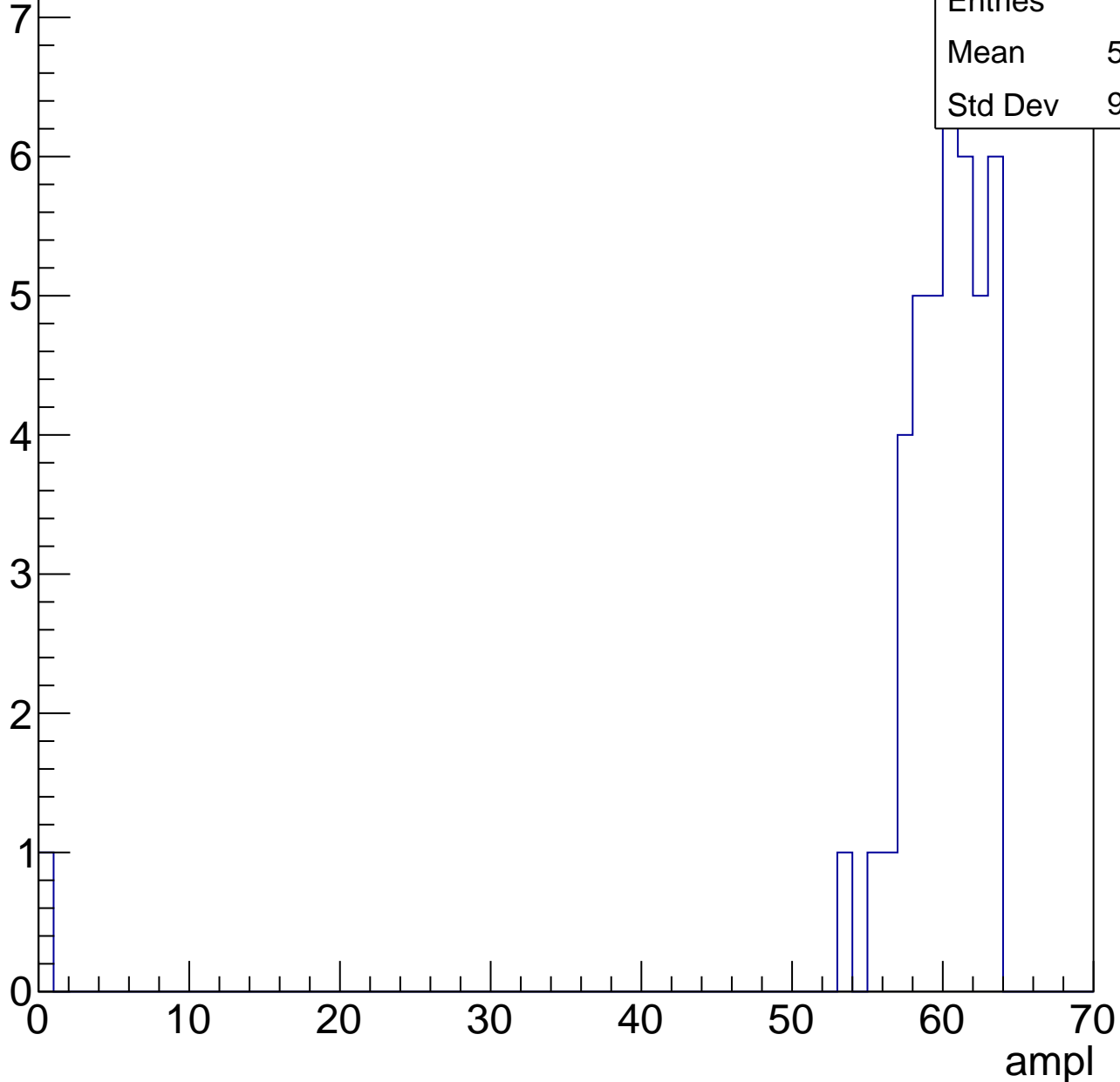


# B0L000S, U26-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.36
Std Dev	9.408

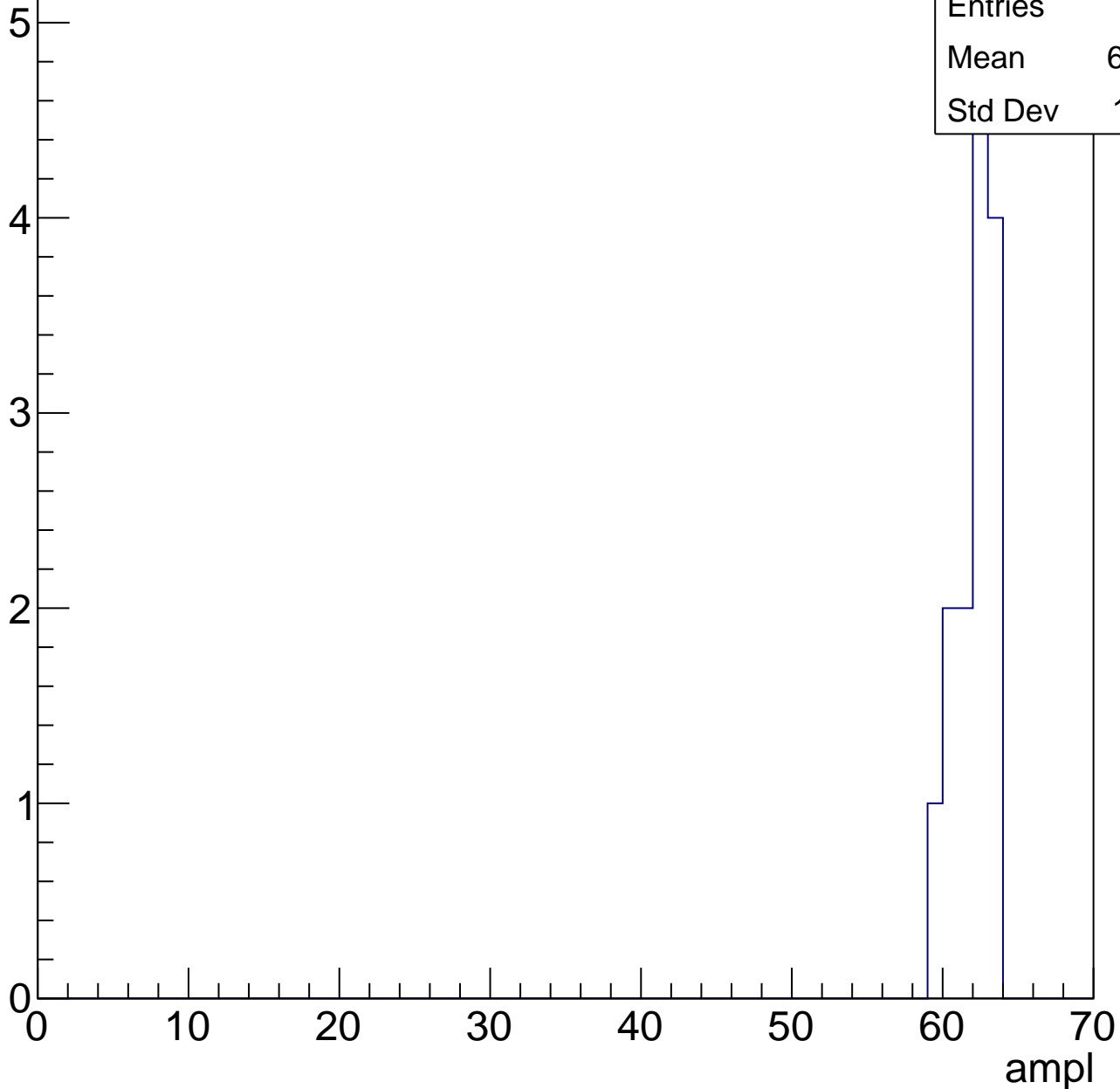


# B0L000S, U26-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	61.64
Std Dev	1.231

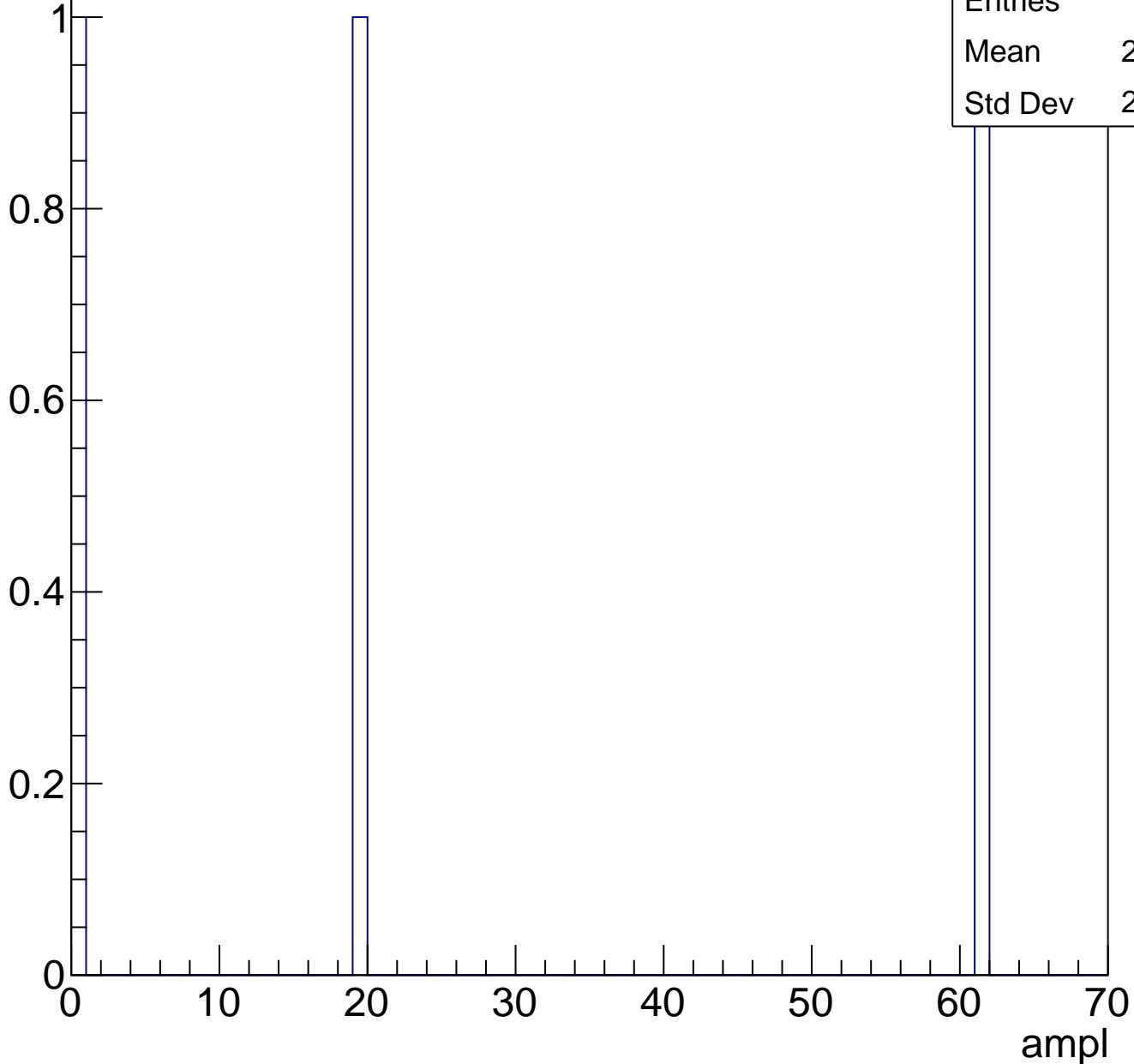




# B0L000S, U26-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	26.67
Std Dev	25.49

# B0L000S, U26-ch120, adc0

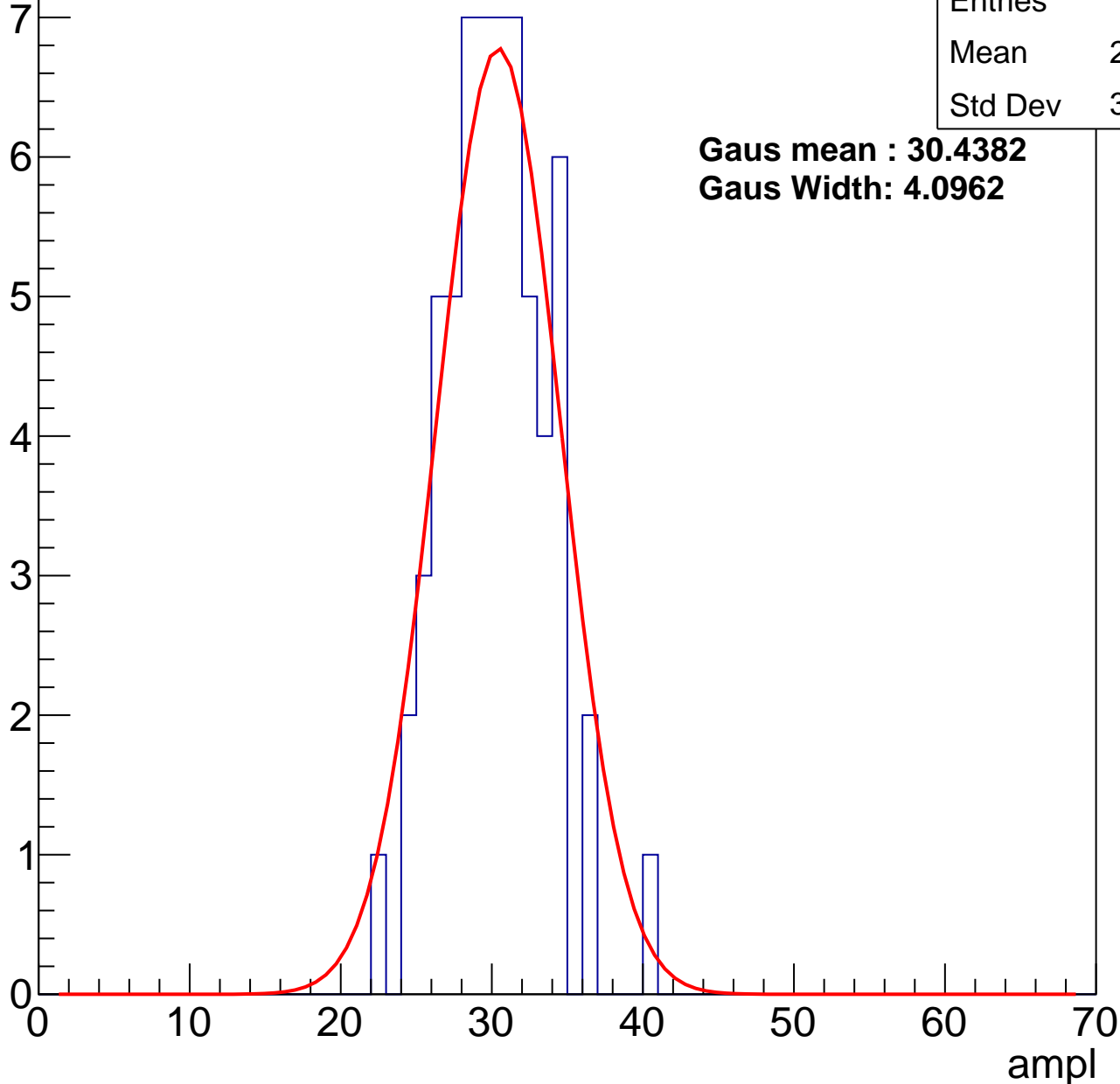
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	29.74
Std Dev	3.355

**Gaus mean : 30.4382**

**Gaus Width: 4.0962**



# B0L000S, U26-ch120, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	36.12
Std Dev	3.239

**Gaus mean : 36.6756**

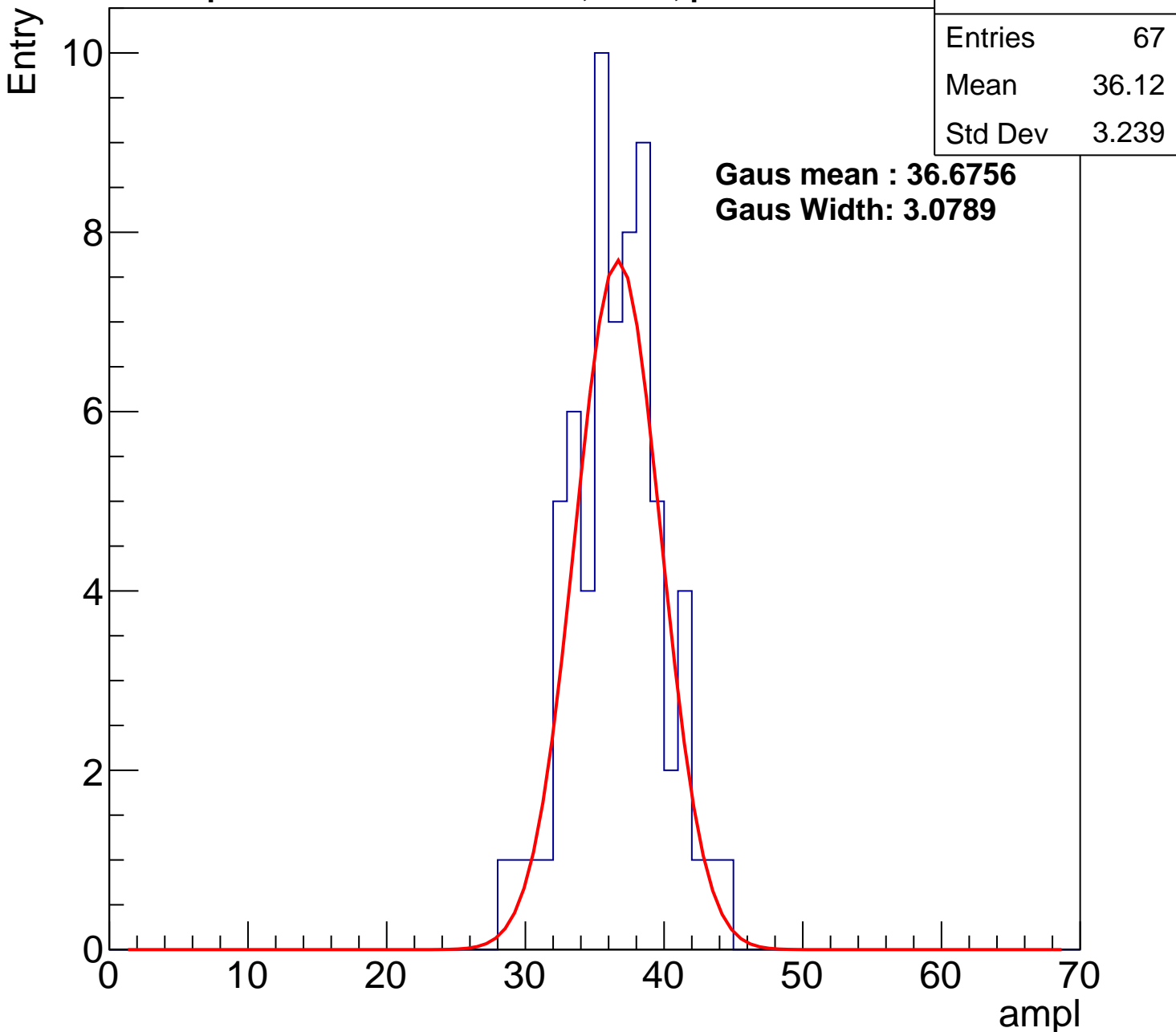
**Gaus Width: 3.0789**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch120, adc2

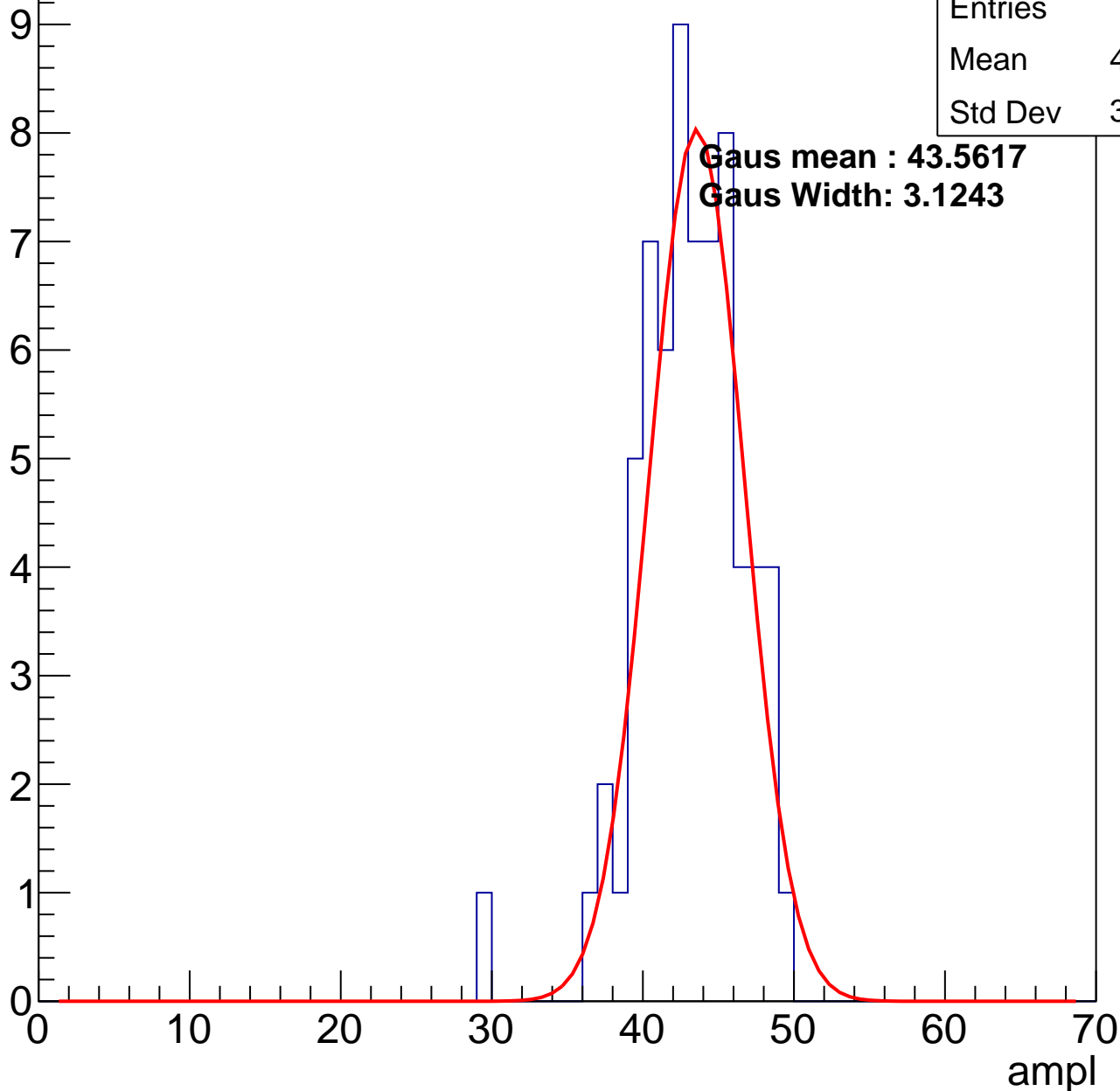
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	42.66
Std Dev	3.423

**Gaus mean : 43.5617**

**Gaus Width: 3.1243**

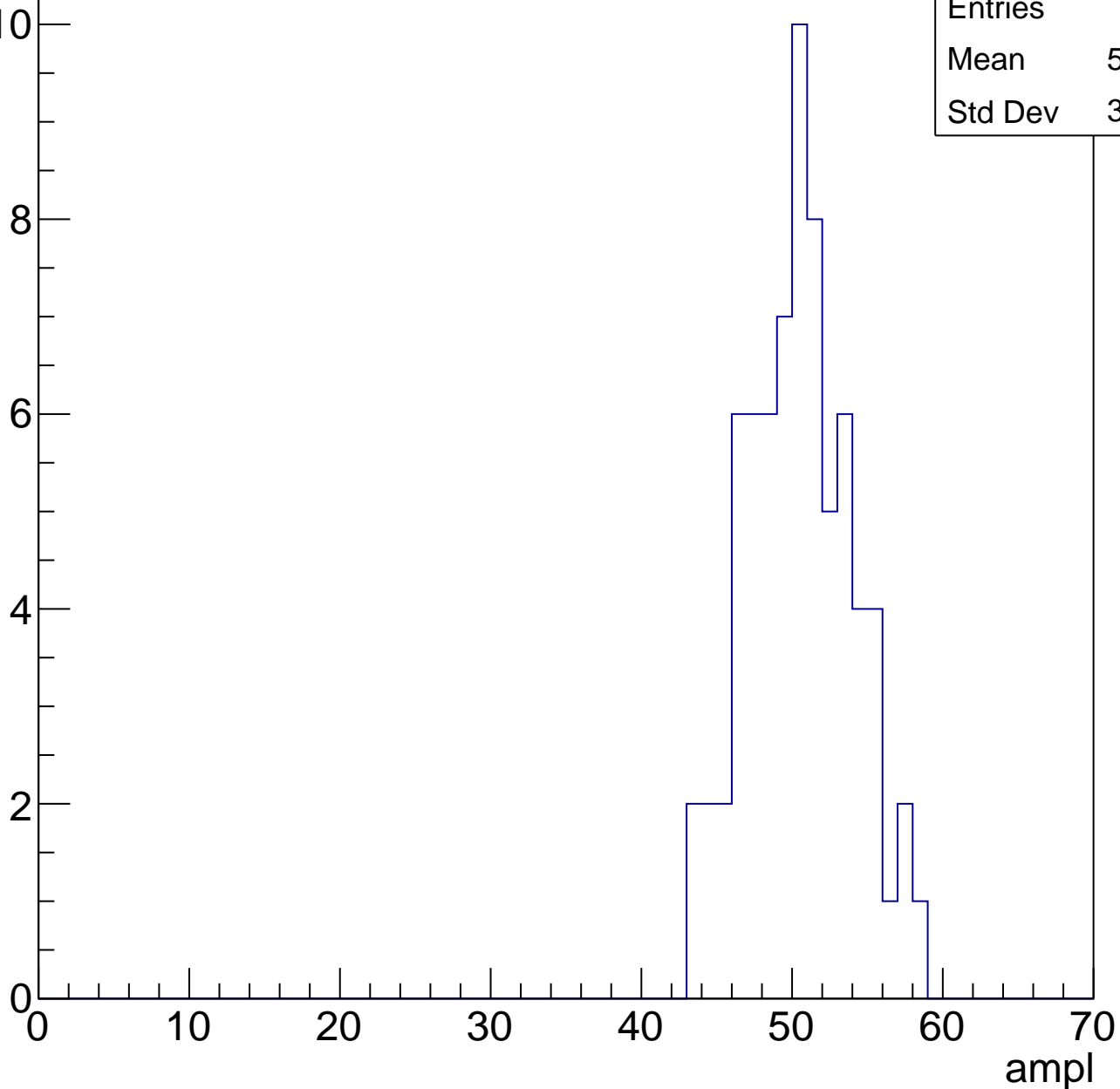


# B0L000S, U26-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	50.04
Std Dev	3.426

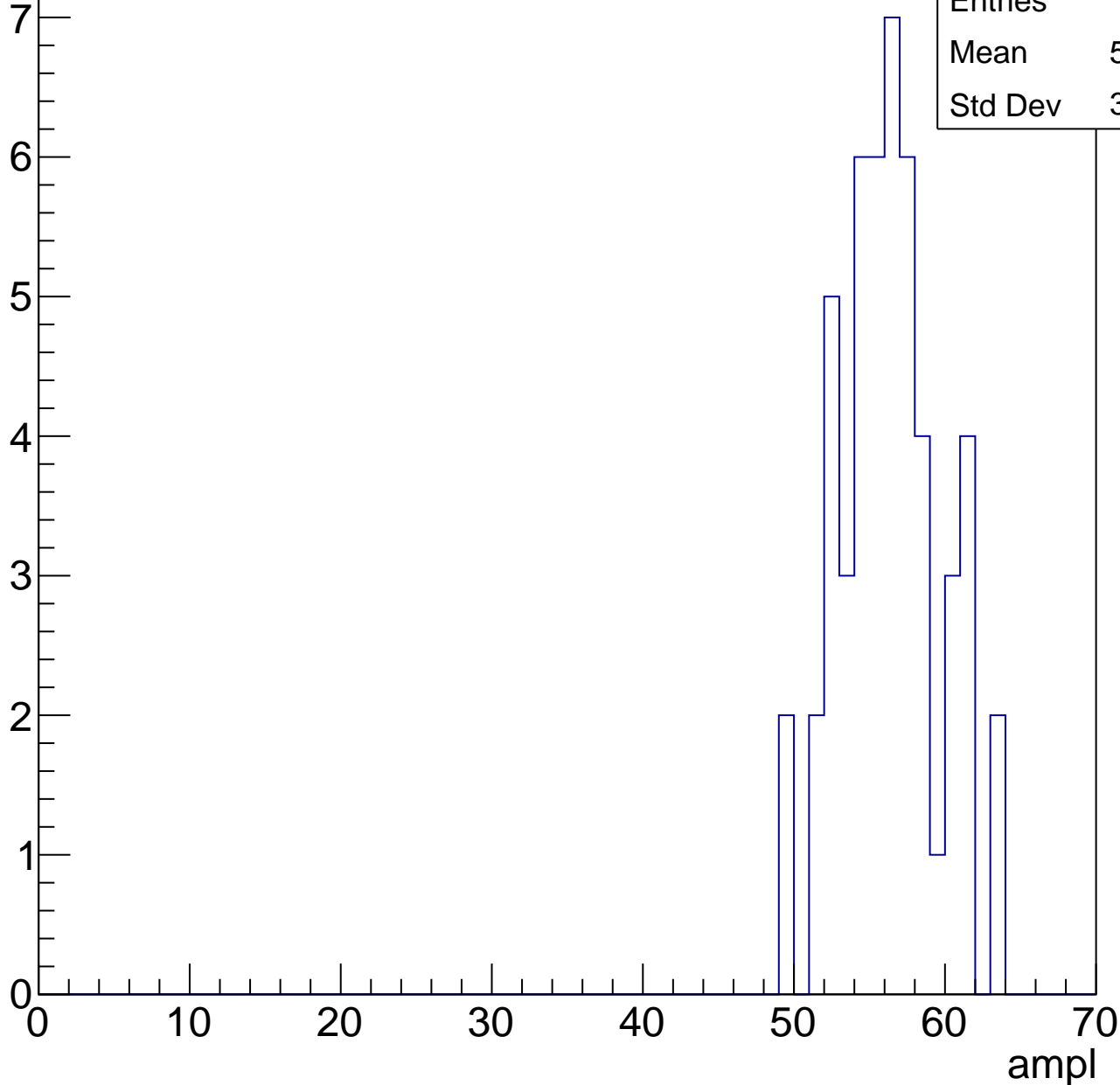


# B0L000S, U26-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

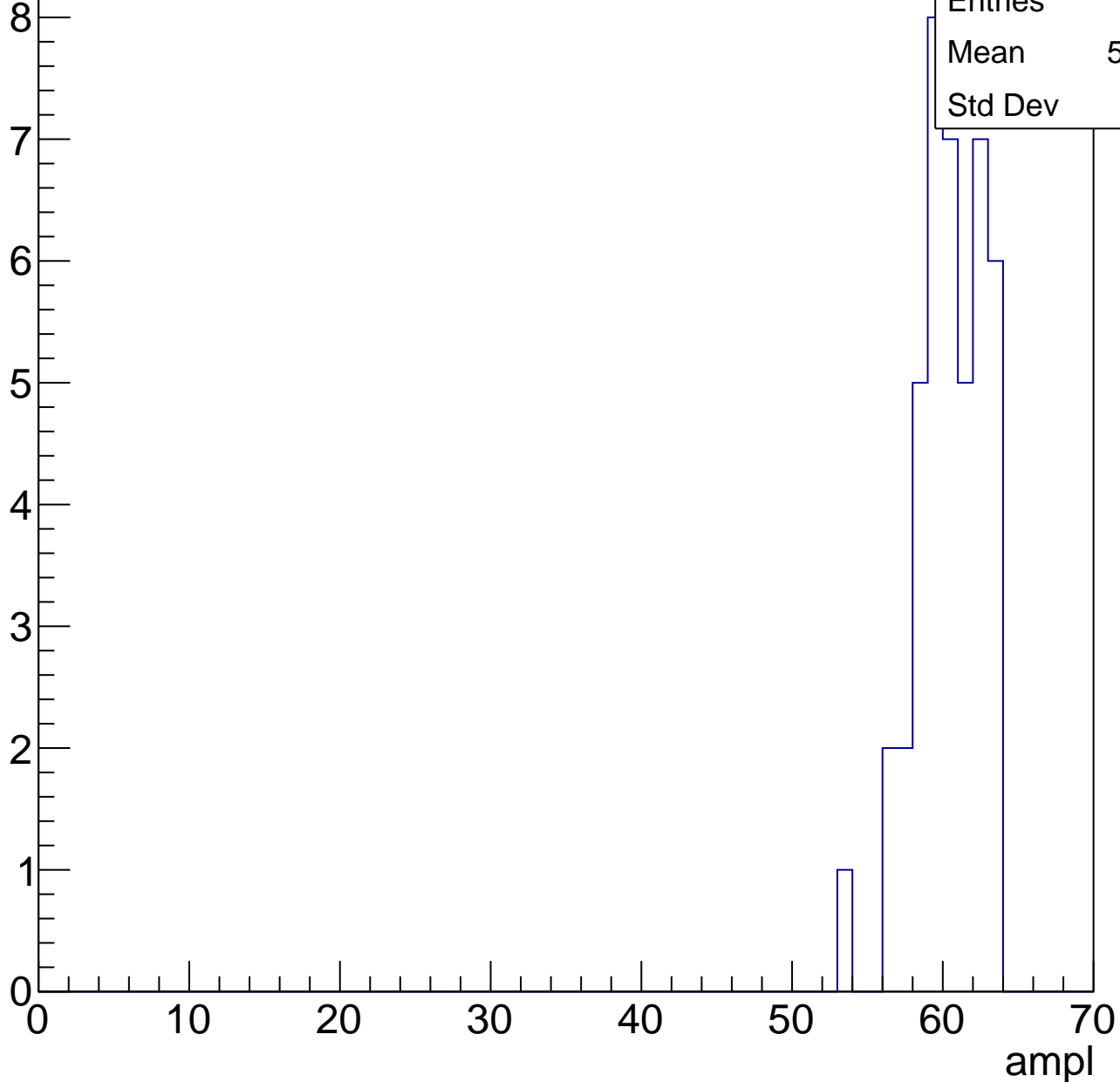
Entries	51
Mean	55.84
Std Dev	3.316



# B0L000S, U26-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

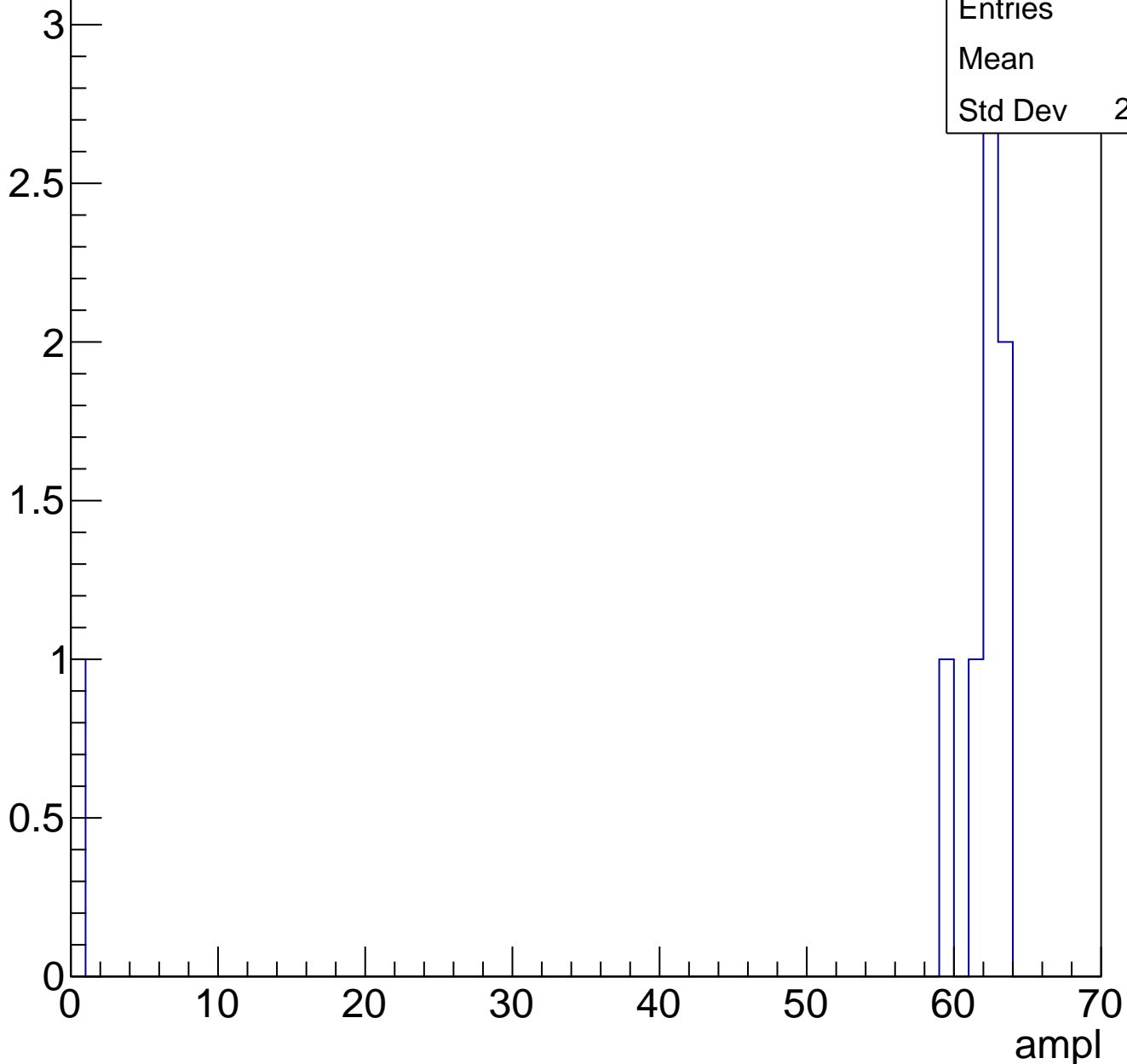
Entry



# B0L000S, U26-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	8
Mean	54
Std Dev	20.45



# B0L000S, U26-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	29.32
Std Dev	4.877

**Gaus mean : 30.2333**

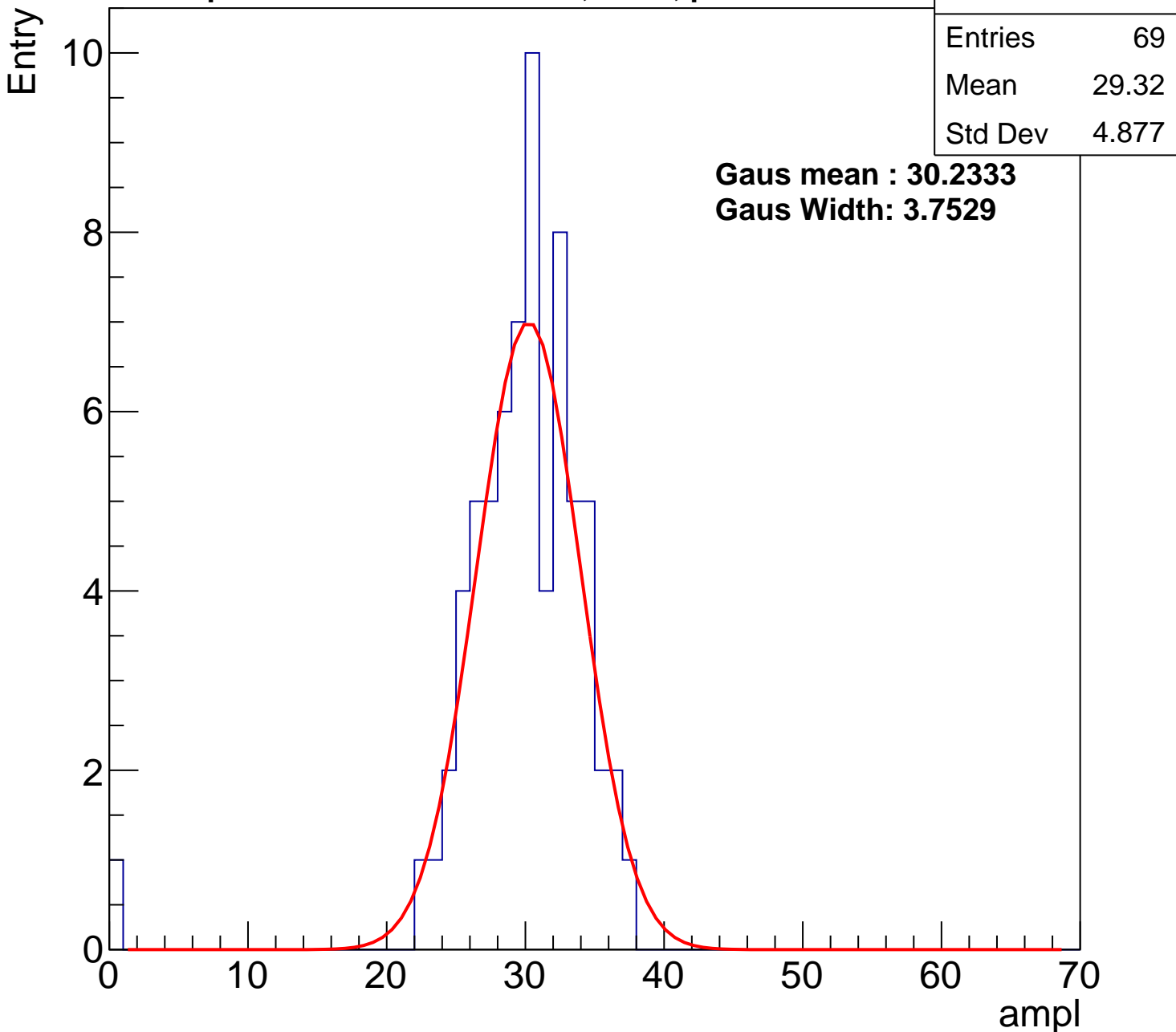
**Gaus Width: 3.7529**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch121, adc1

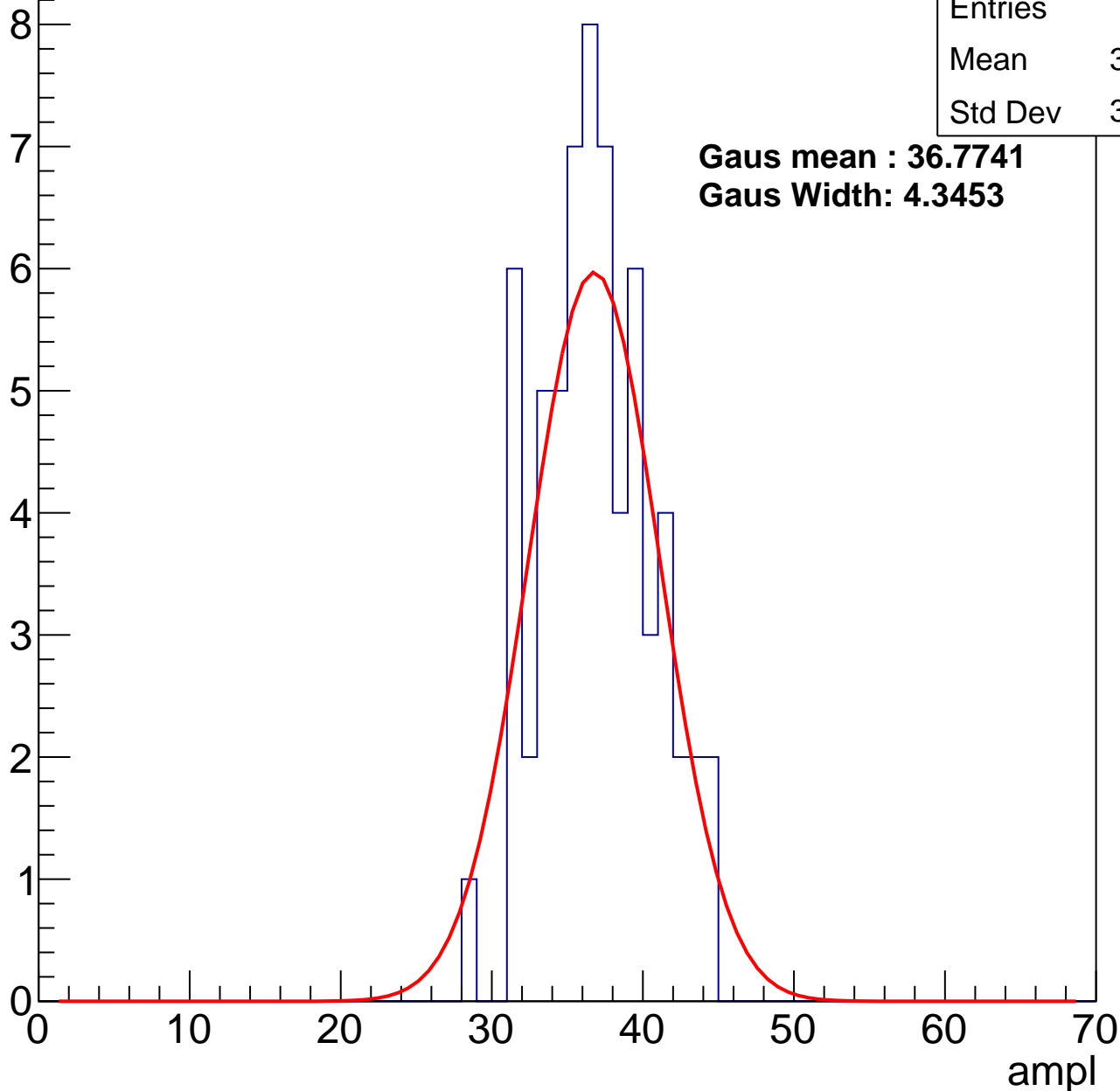
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.45
Std Dev	3.597

**Gaus mean : 36.7741**

**Gaus Width: 4.3453**



# B0L000S, U26-ch121, adc2

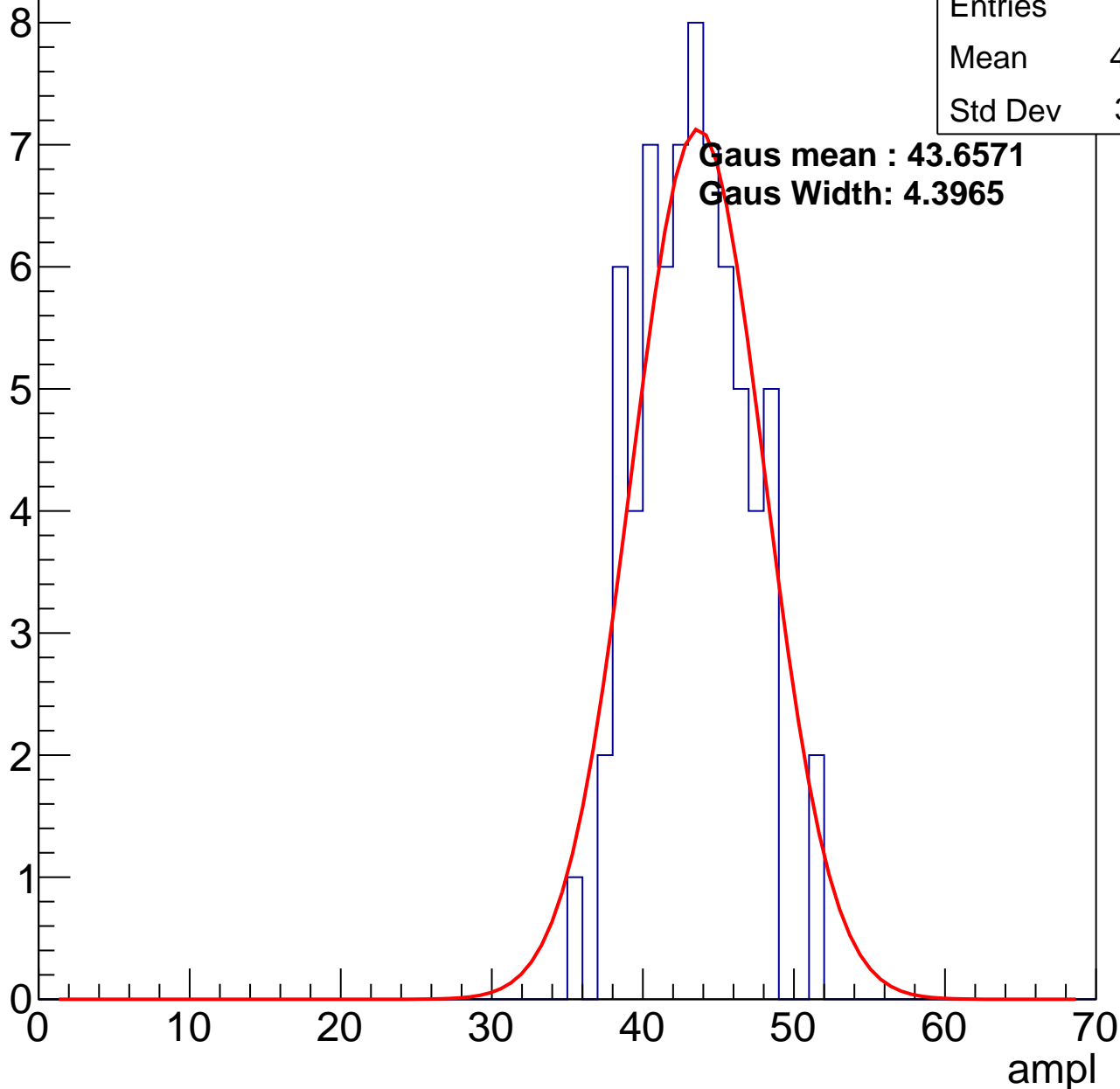
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.79
Std Dev	3.451

**Gaus mean : 43.6571**

**Gaus Width: 4.3965**

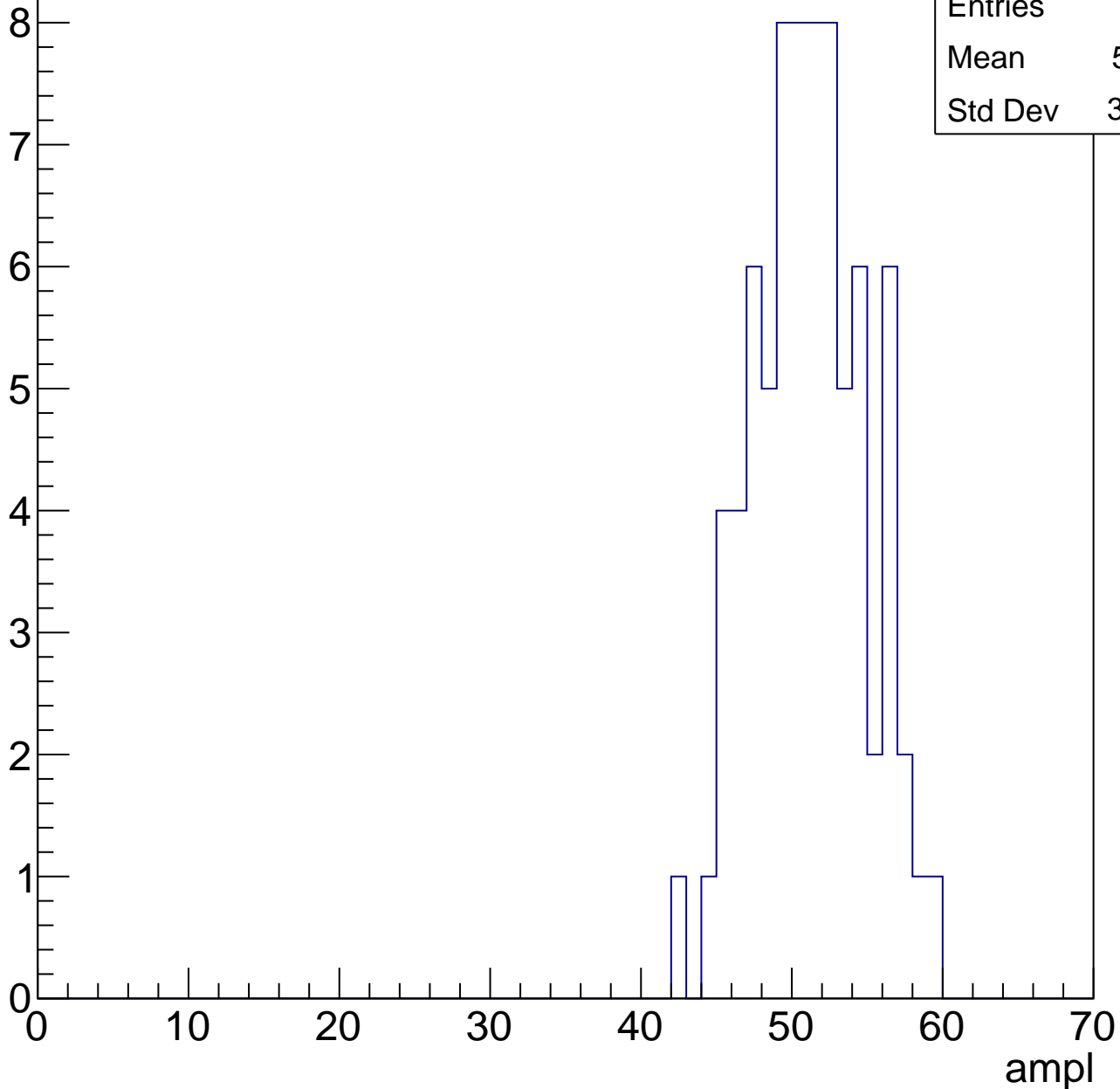


# B0L000S, U26-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

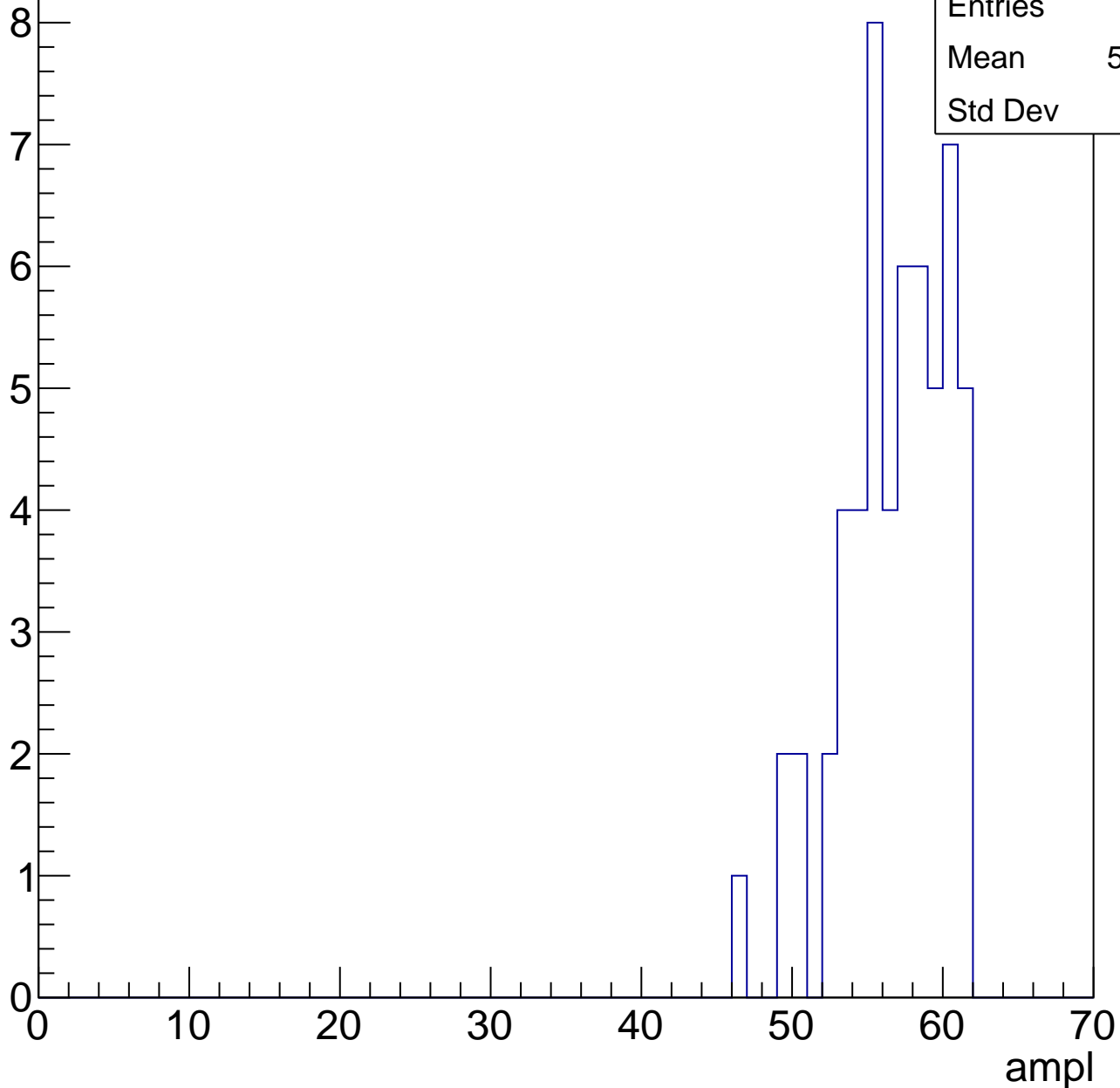
Entries	76
Mean	50.71
Std Dev	3.623



# B0L000S, U26-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

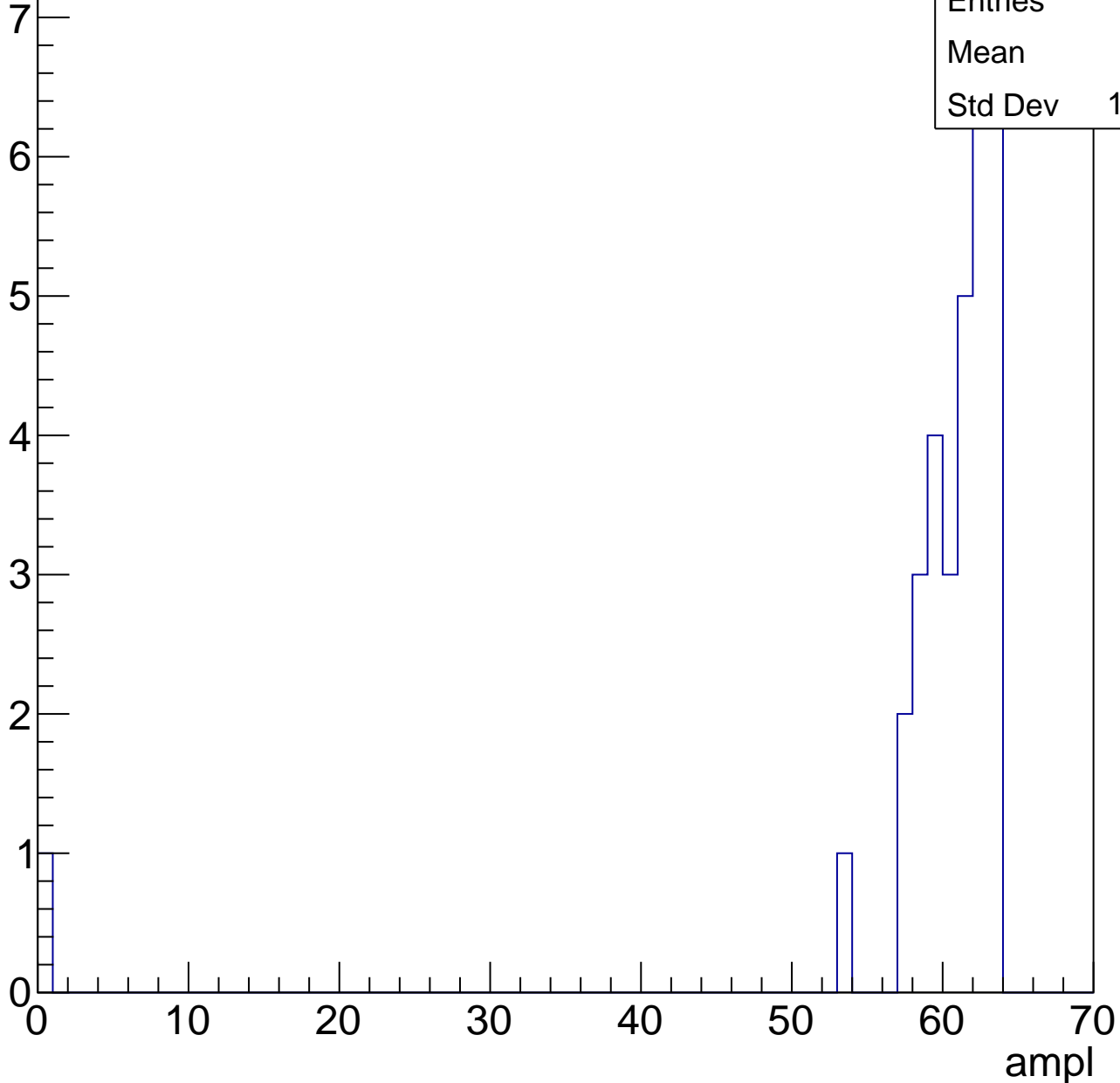


# B0L000S, U26-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

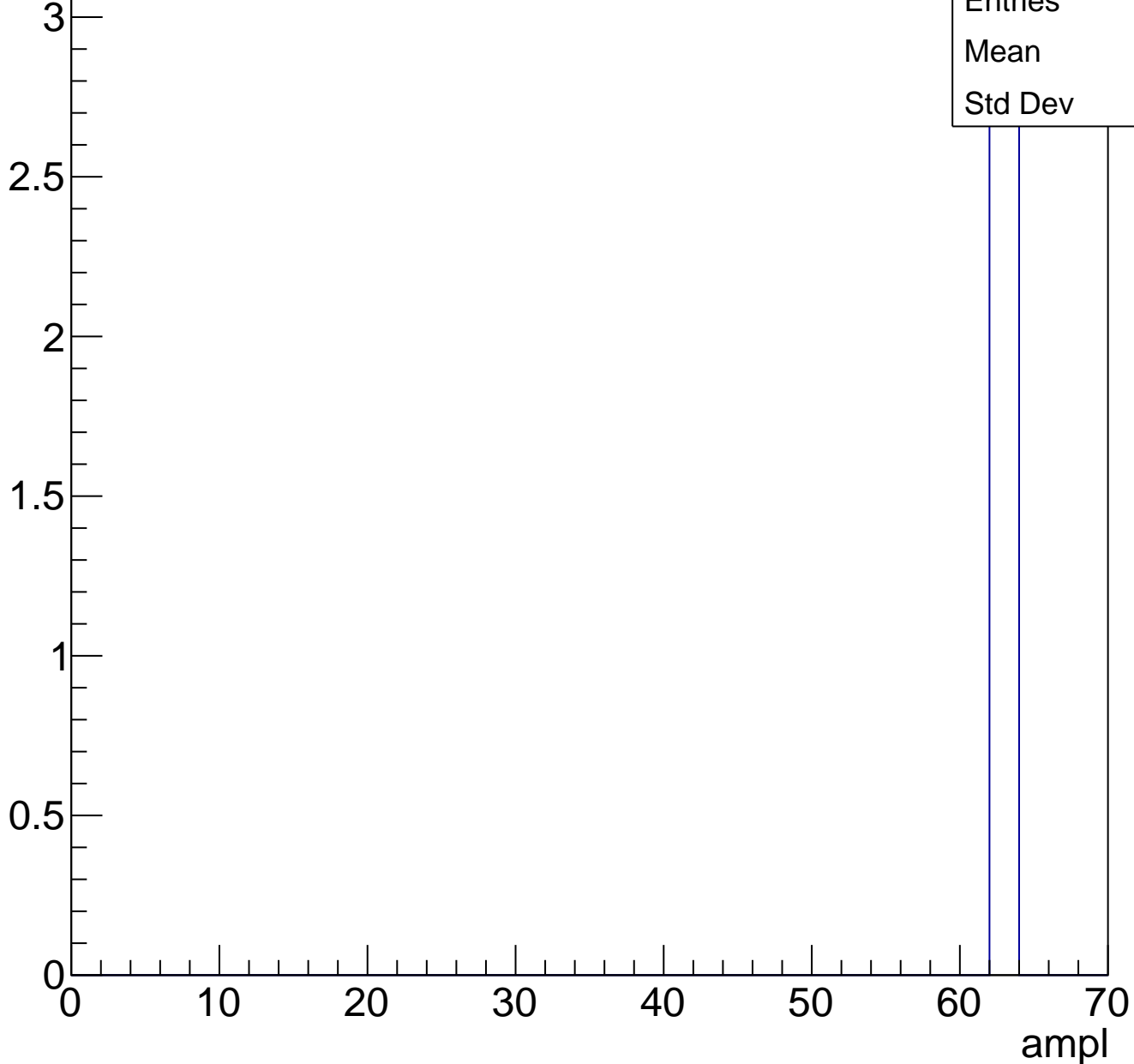
Entries	33
Mean	58.7
Std Dev	10.62



# B0L000S, U26-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch122, adc0

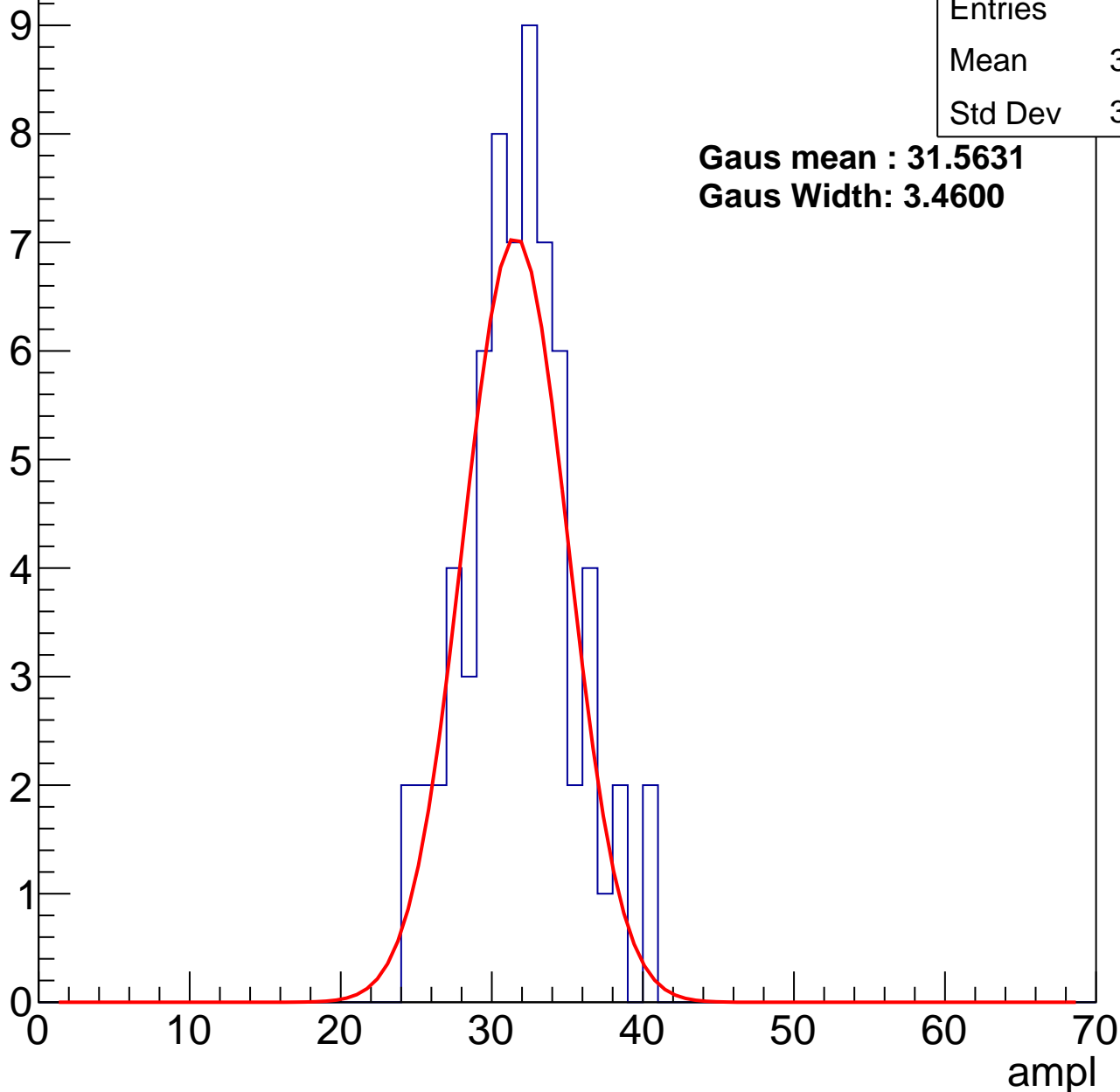
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	31.39
Std Dev	3.566

**Gaus mean : 31.5631**

**Gaus Width: 3.4600**



# B0L000S, U26-ch122, adc1

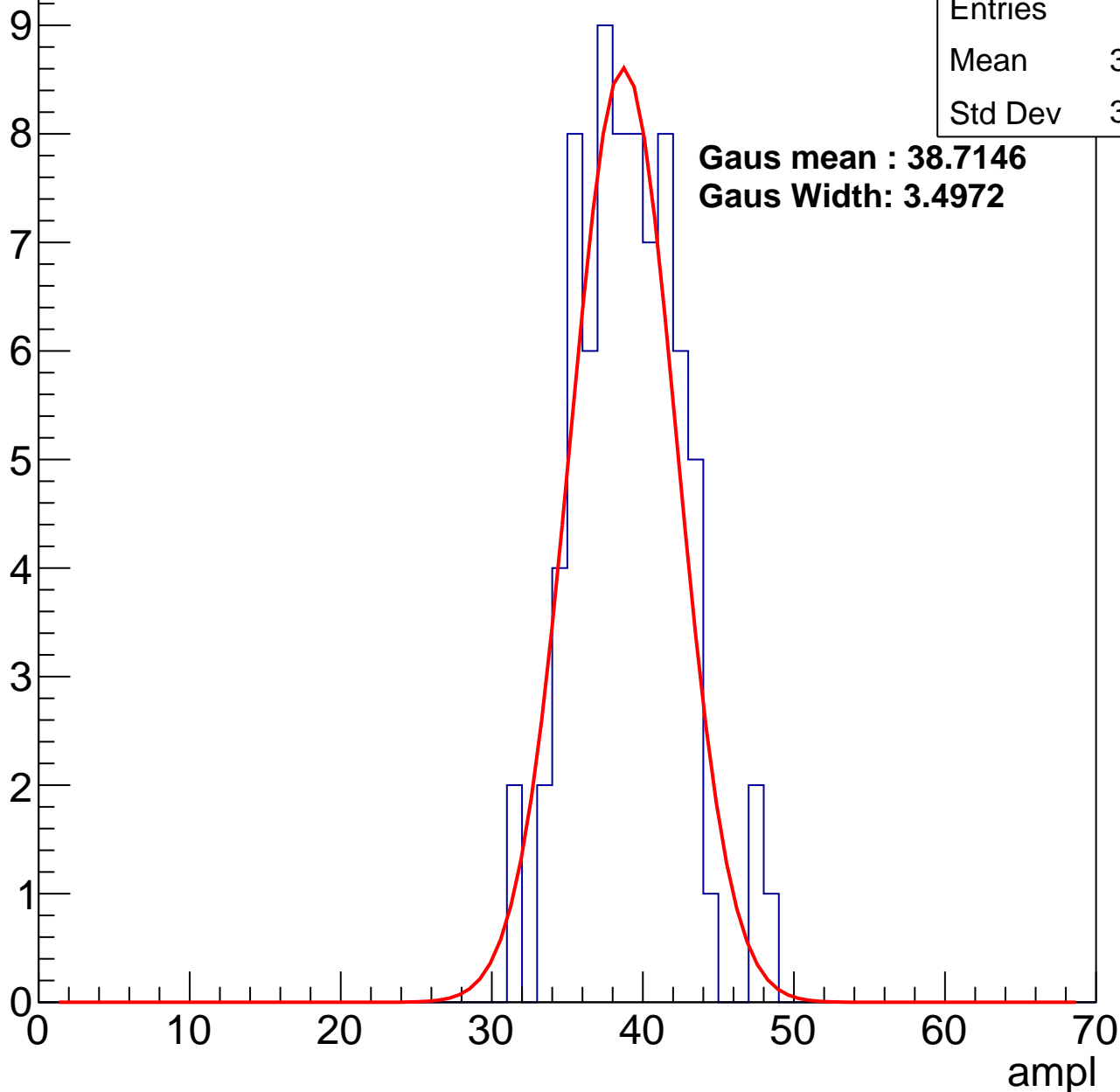
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	38.57
Std Dev	3.454

**Gaus mean : 38.7146**

**Gaus Width: 3.4972**



# B0L000S, U26-ch122, adc2

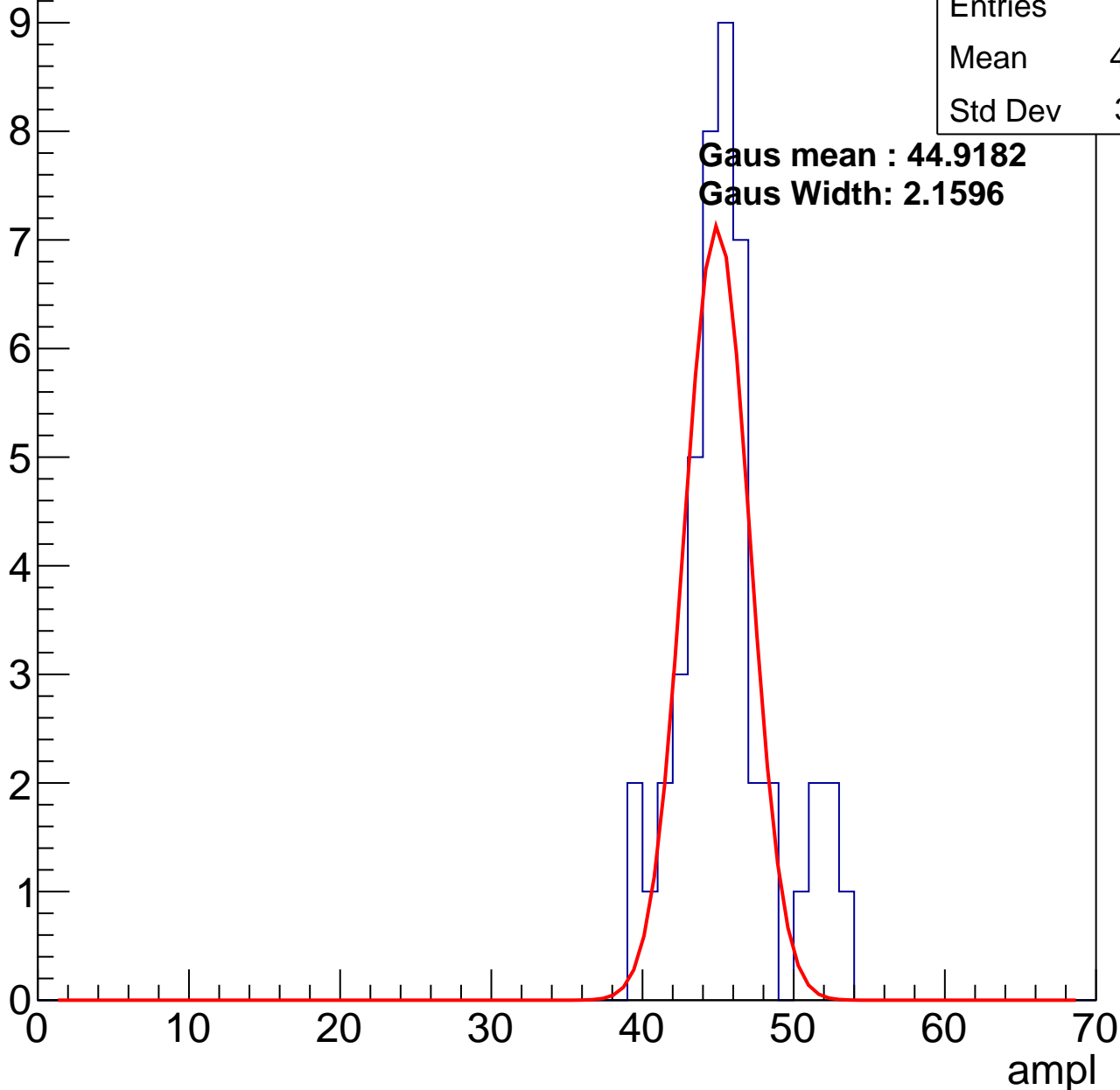
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	45.09
Std Dev	3.181

**Gaus mean : 44.9182**

**Gaus Width: 2.1596**

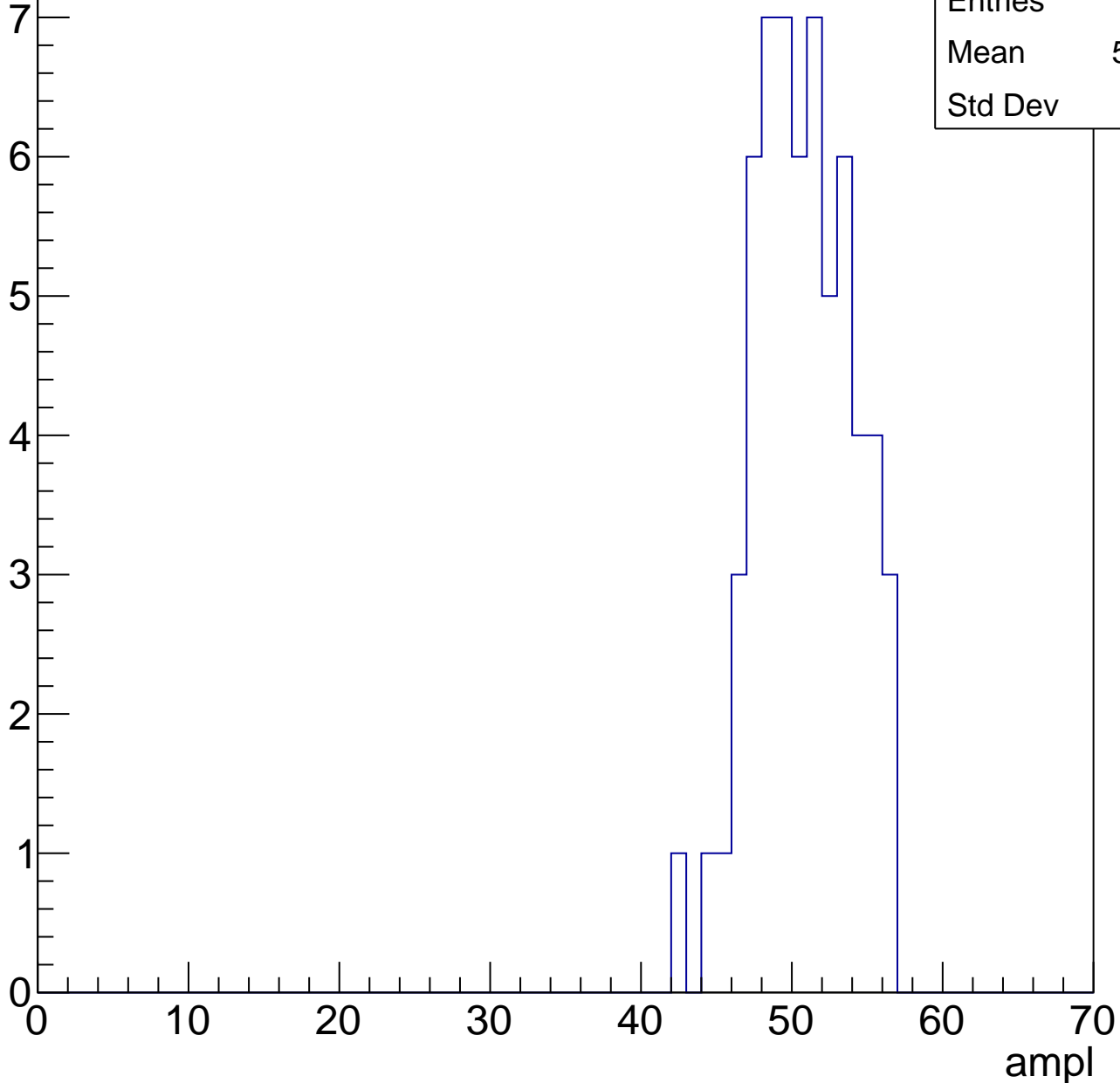


# B0L000S, U26-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	50.31
Std Dev	3.16

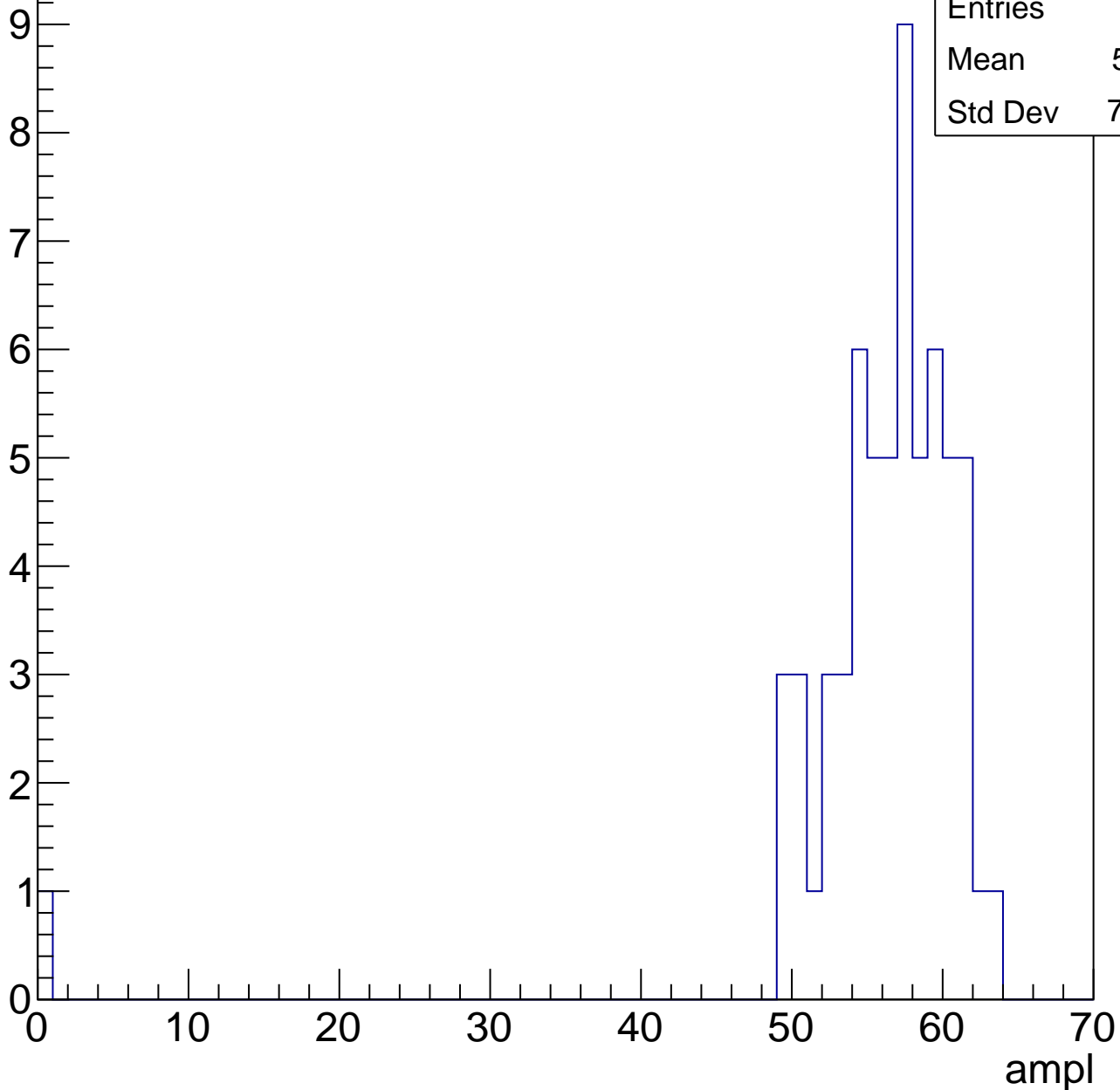


# B0L000S, U26-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.31
Std Dev	7.894

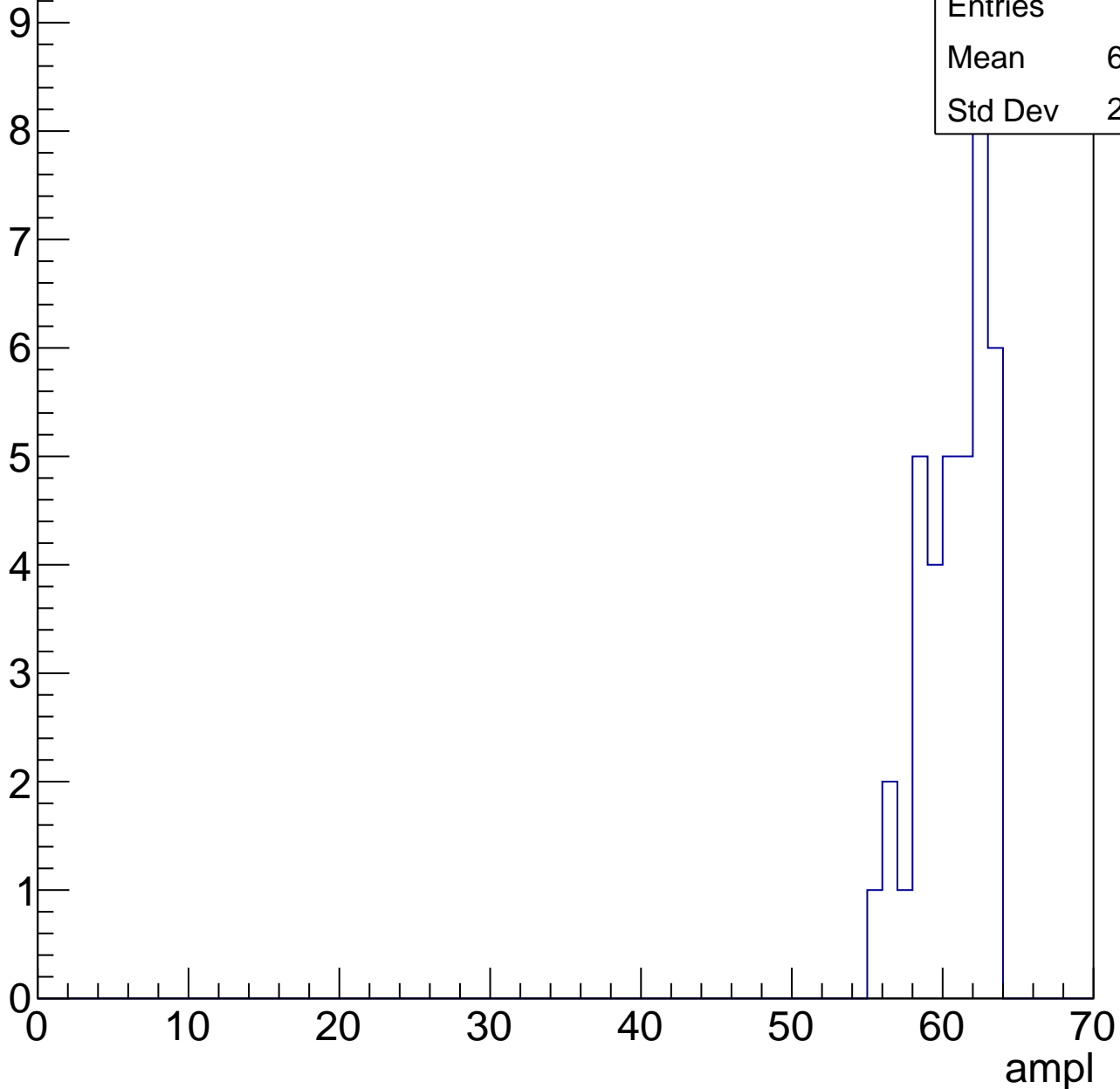


# B0L000S, U26-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	60.29
Std Dev	2.187



# B0L000S, U26-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

63

Std Dev

0



# B0L000S, U26-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch123, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	29.1
Std Dev	3.557

**Gaus mean : 29.6980**

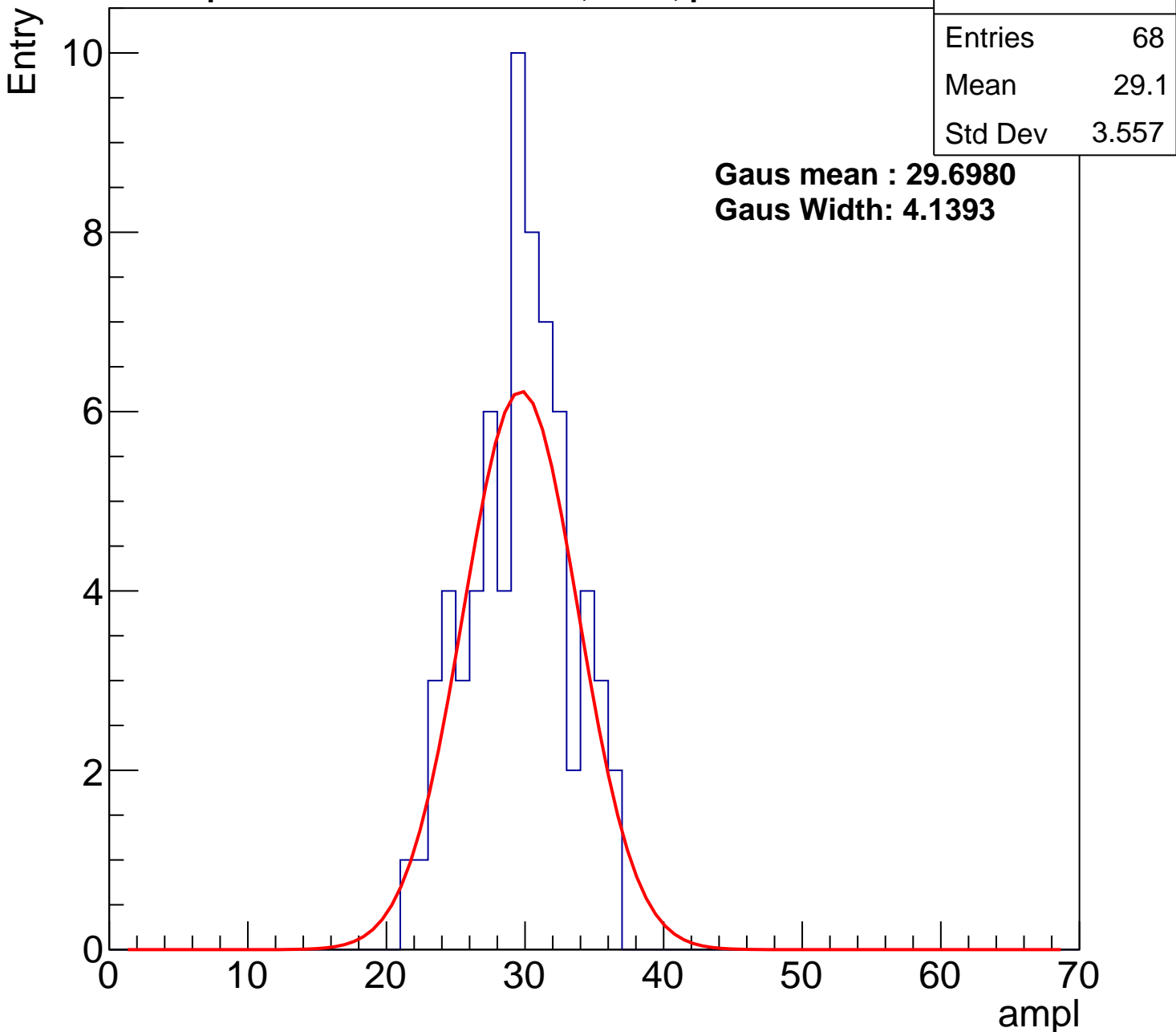
**Gaus Width: 4.1393**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U26-ch123, adc1

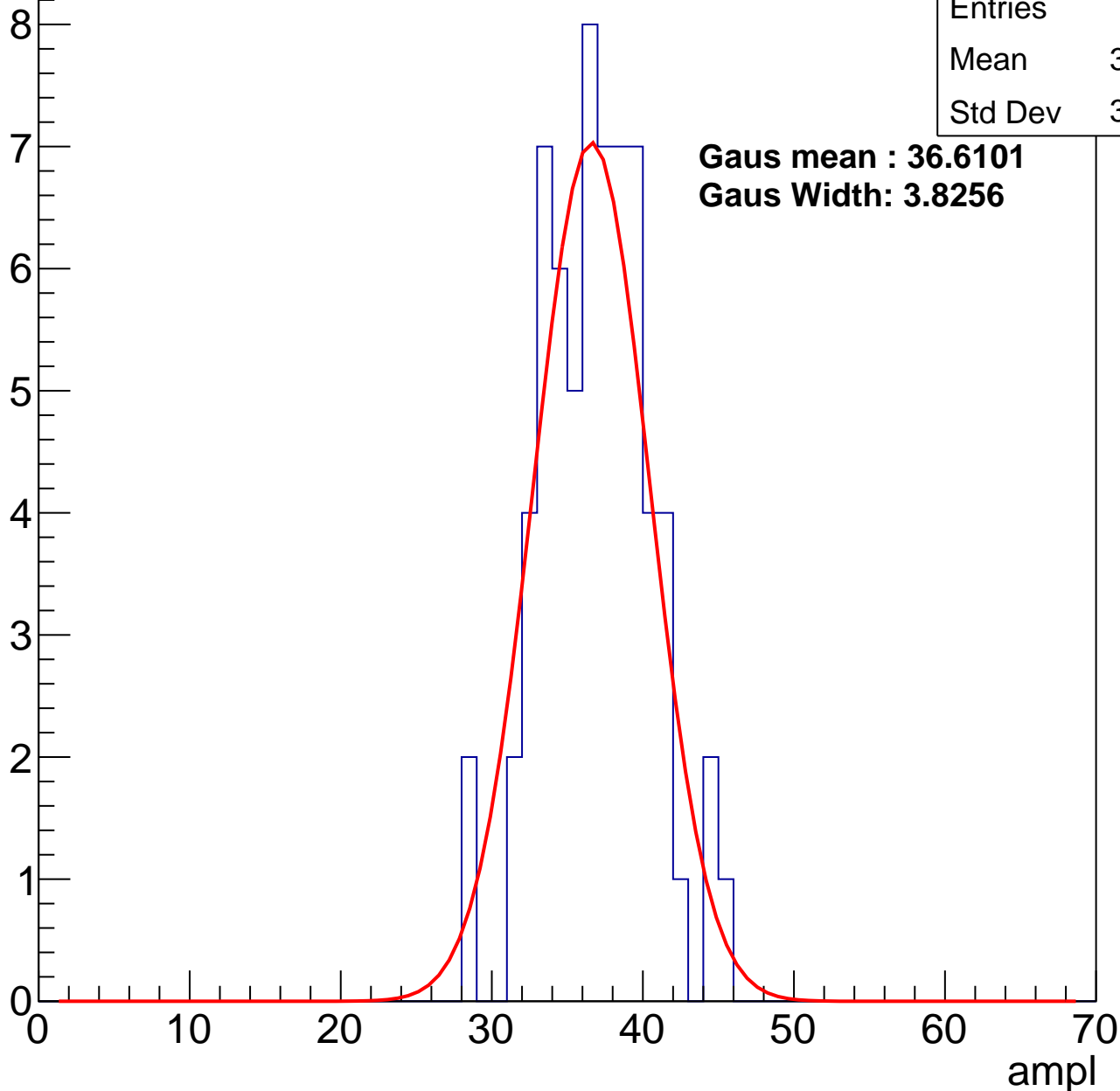
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.43
Std Dev	3.516

**Gaus mean : 36.6101**

**Gaus Width: 3.8256**



# B0L000S, U26-ch123, adc2

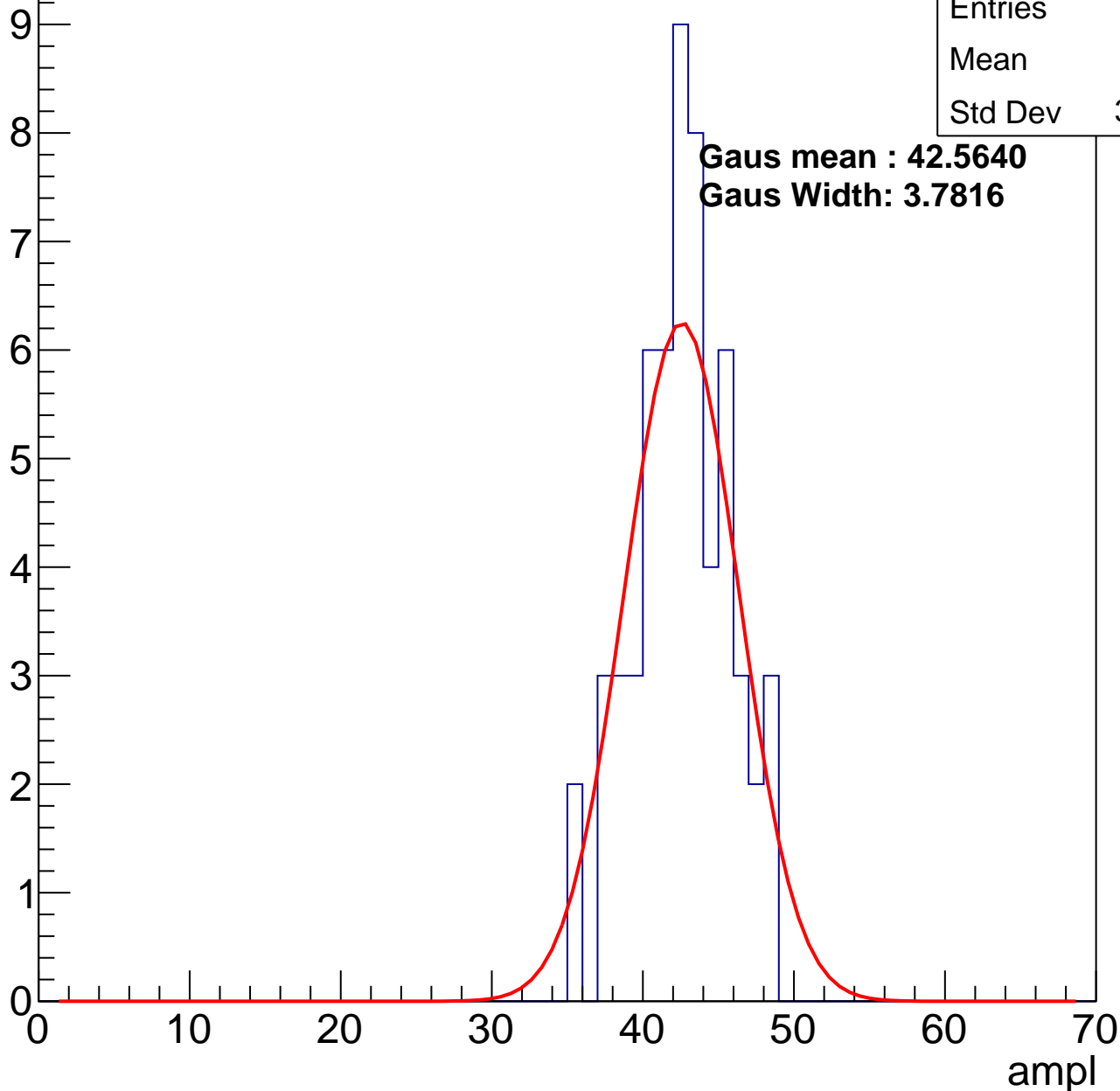
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.1
Std Dev	3.111

**Gaus mean : 42.5640**

**Gaus Width: 3.7816**

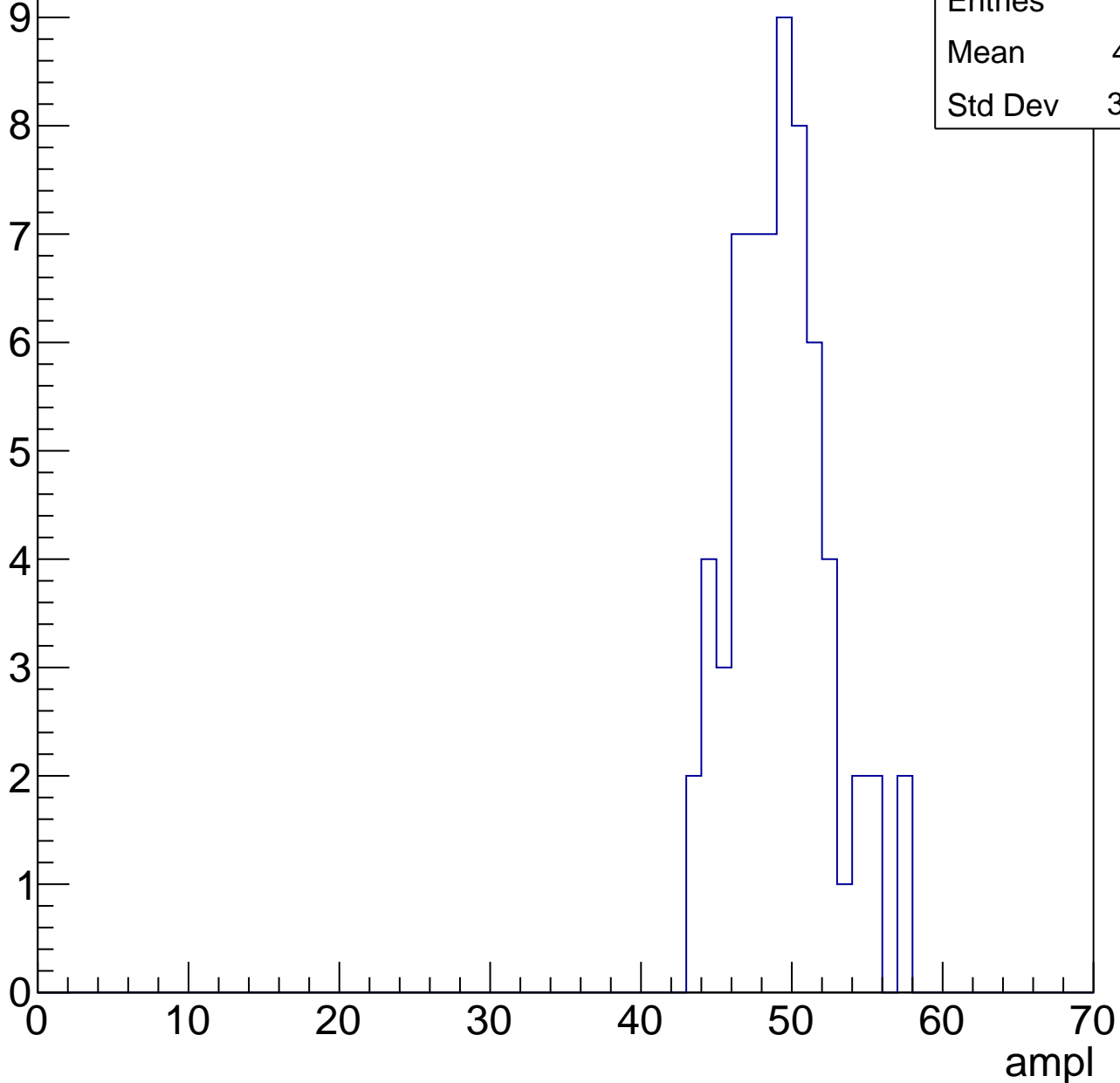


# B0L000S, U26-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	48.81
Std Dev	3.186

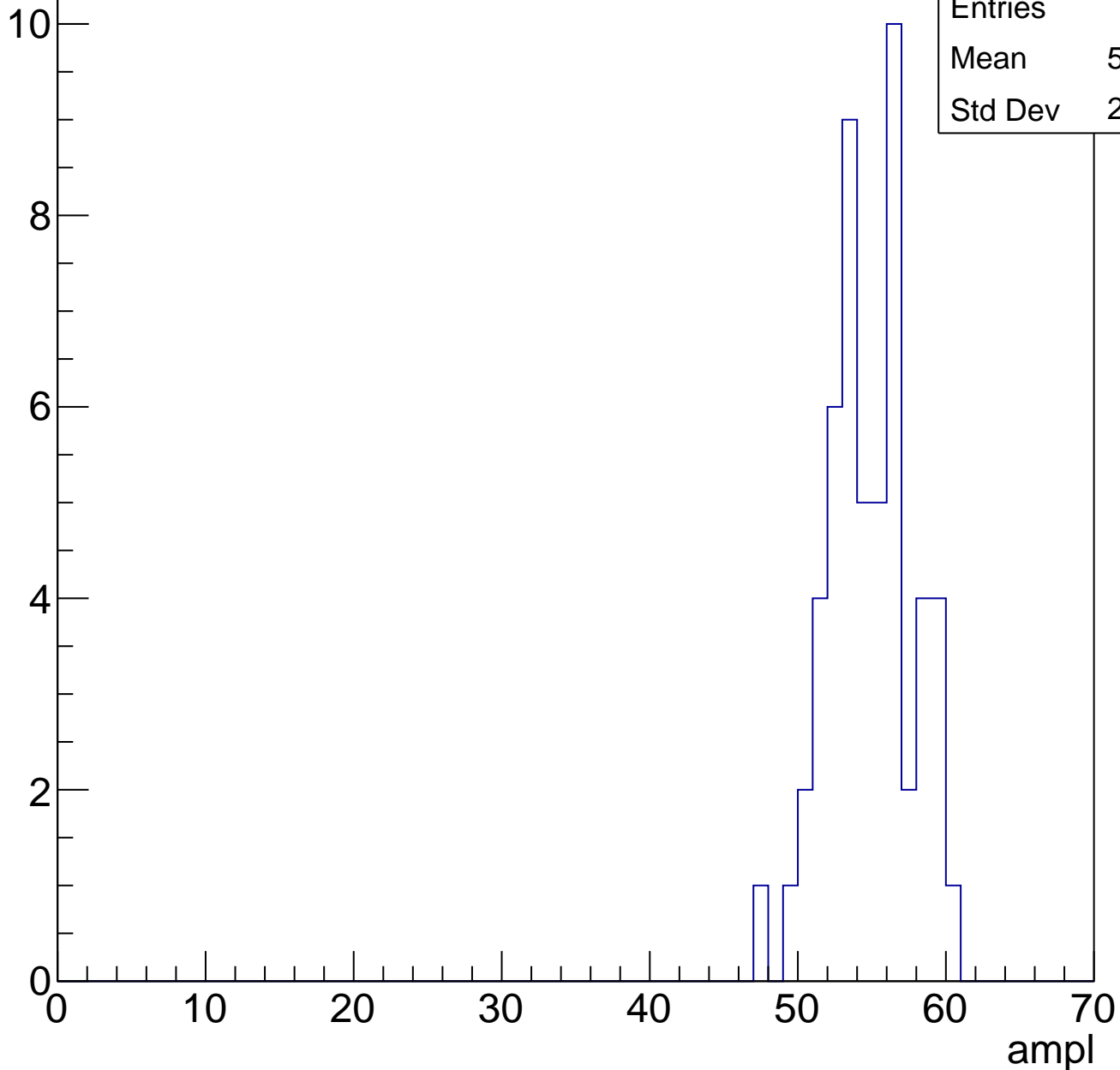


# B0L000S, U26-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	54
Mean	54.37
Std Dev	2.824

Entry

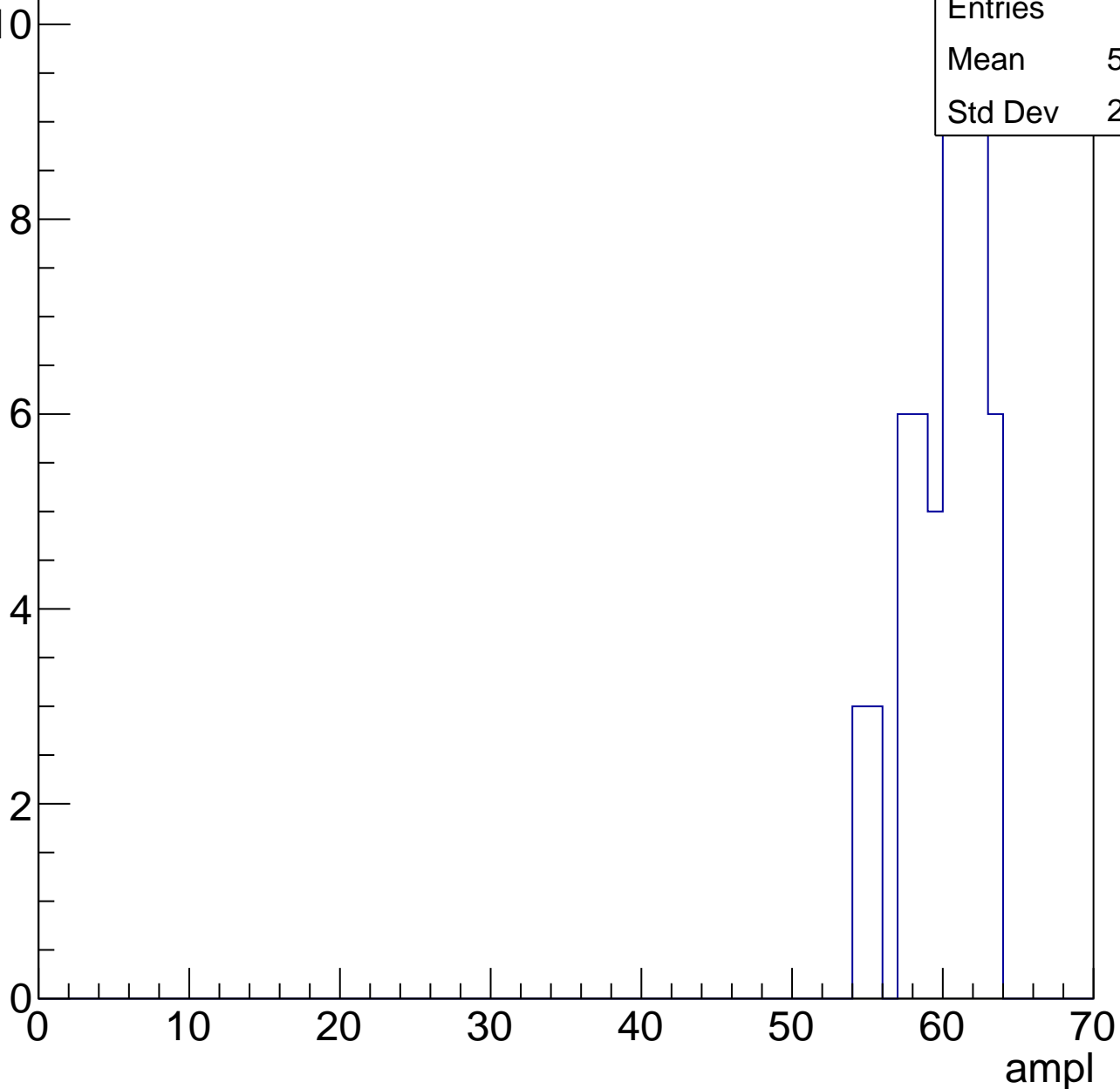


# B0L000S, U26-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

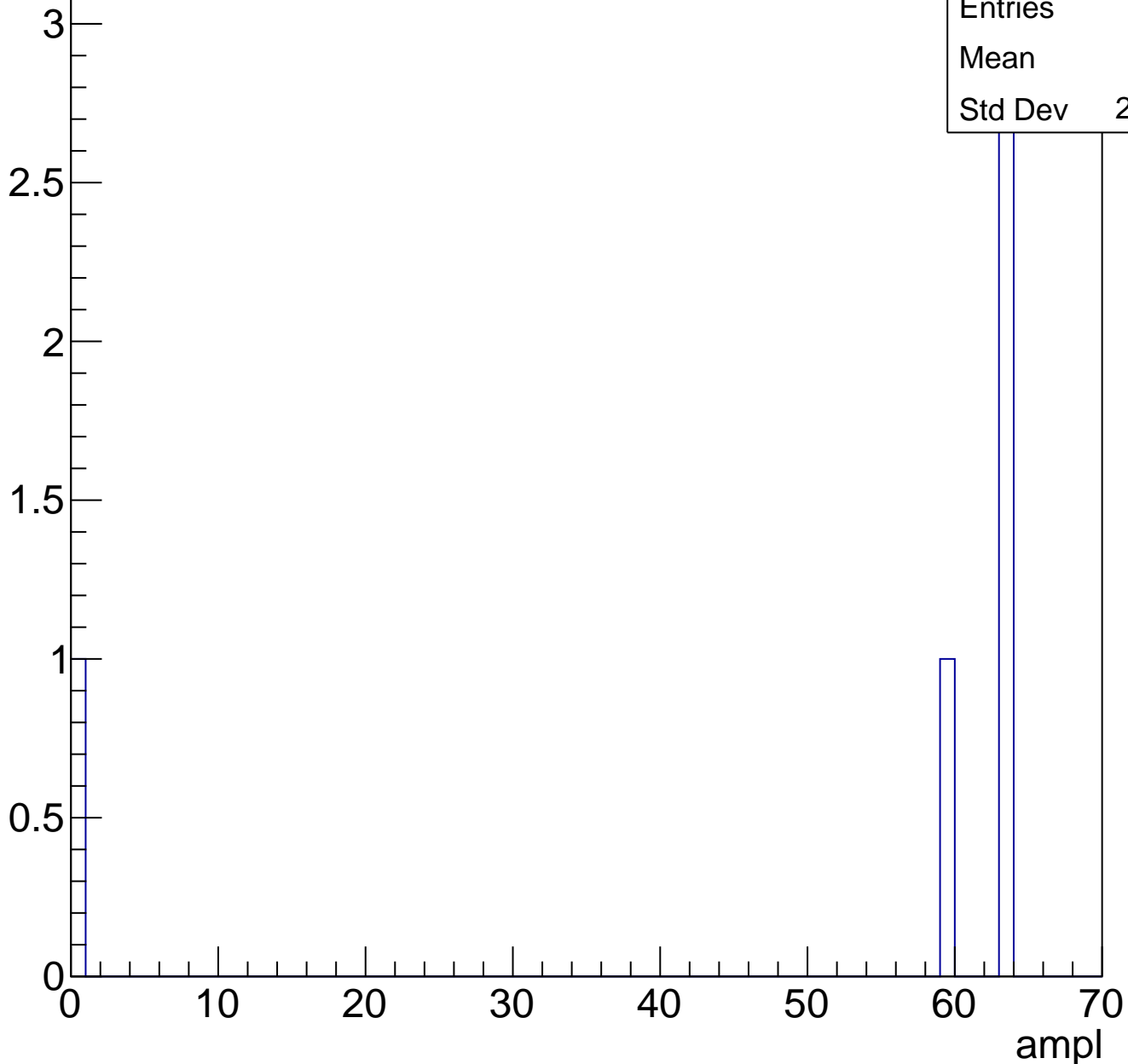
Entries	58
Mean	59.66
Std Dev	2.502



# B0L000S, U26-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch124, adc0

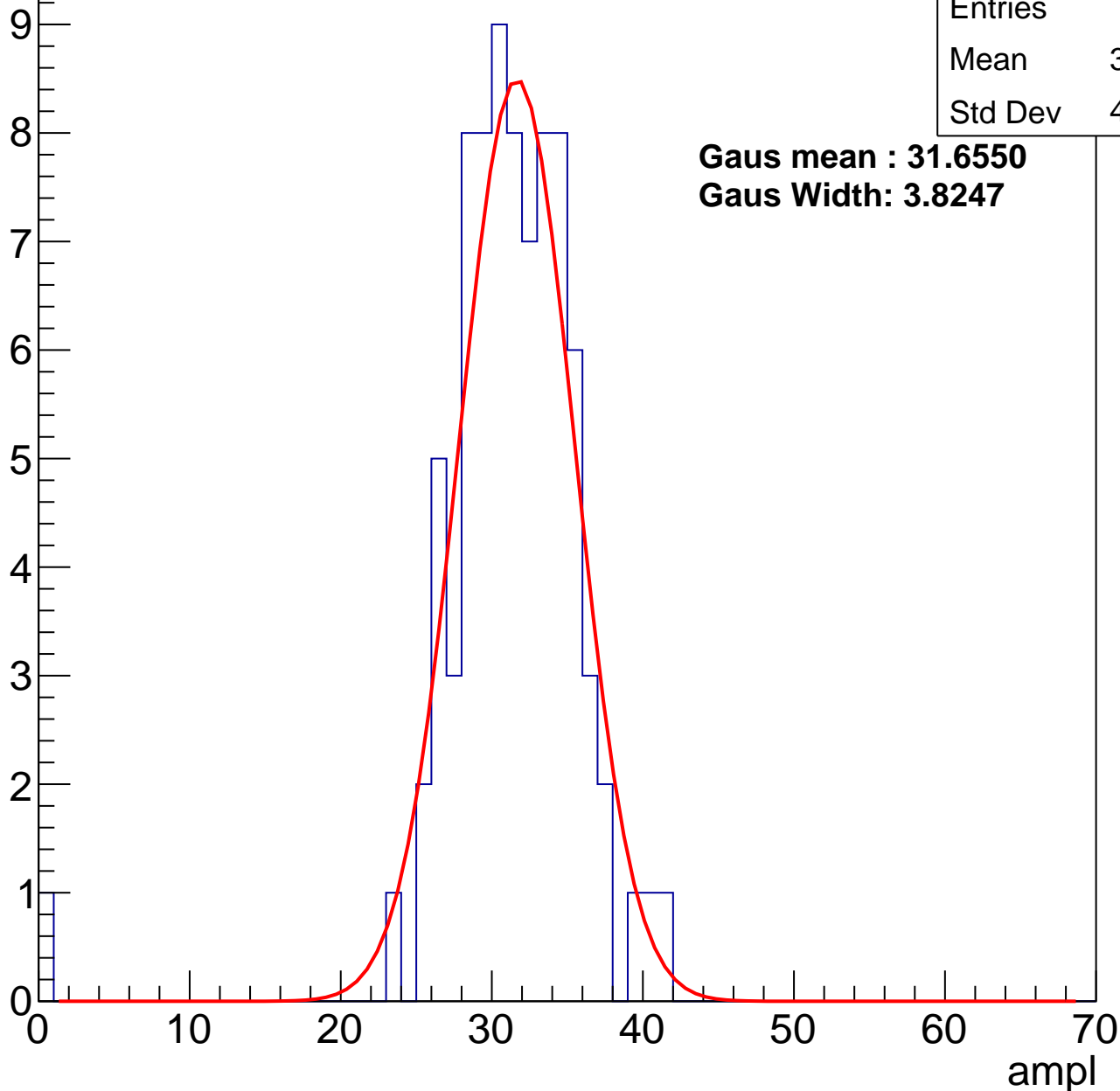
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	30.85
Std Dev	4.914

**Gaus mean : 31.6550**

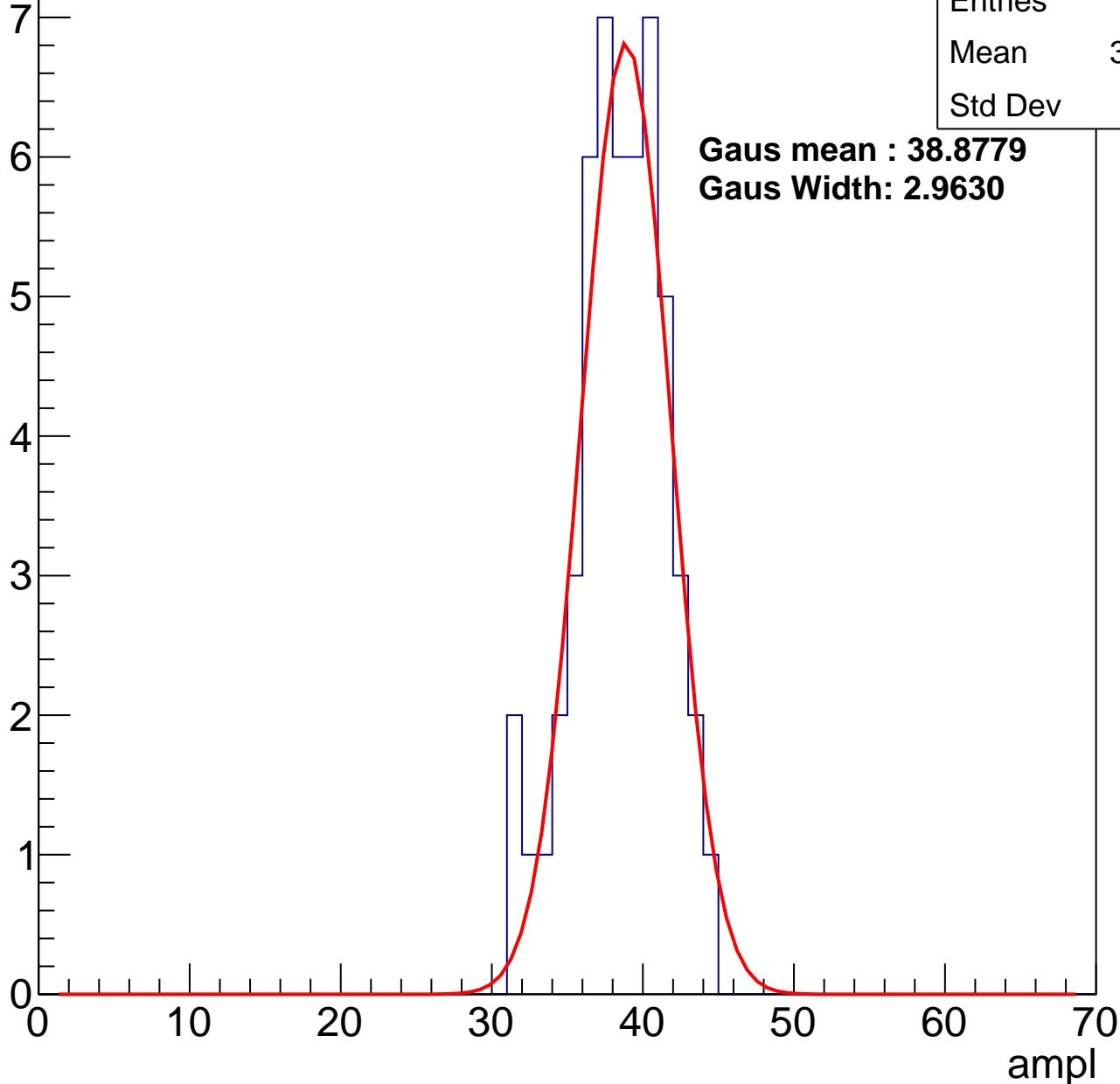
**Gaus Width: 3.8247**



# B0L000S, U26-ch124, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch124, adc2

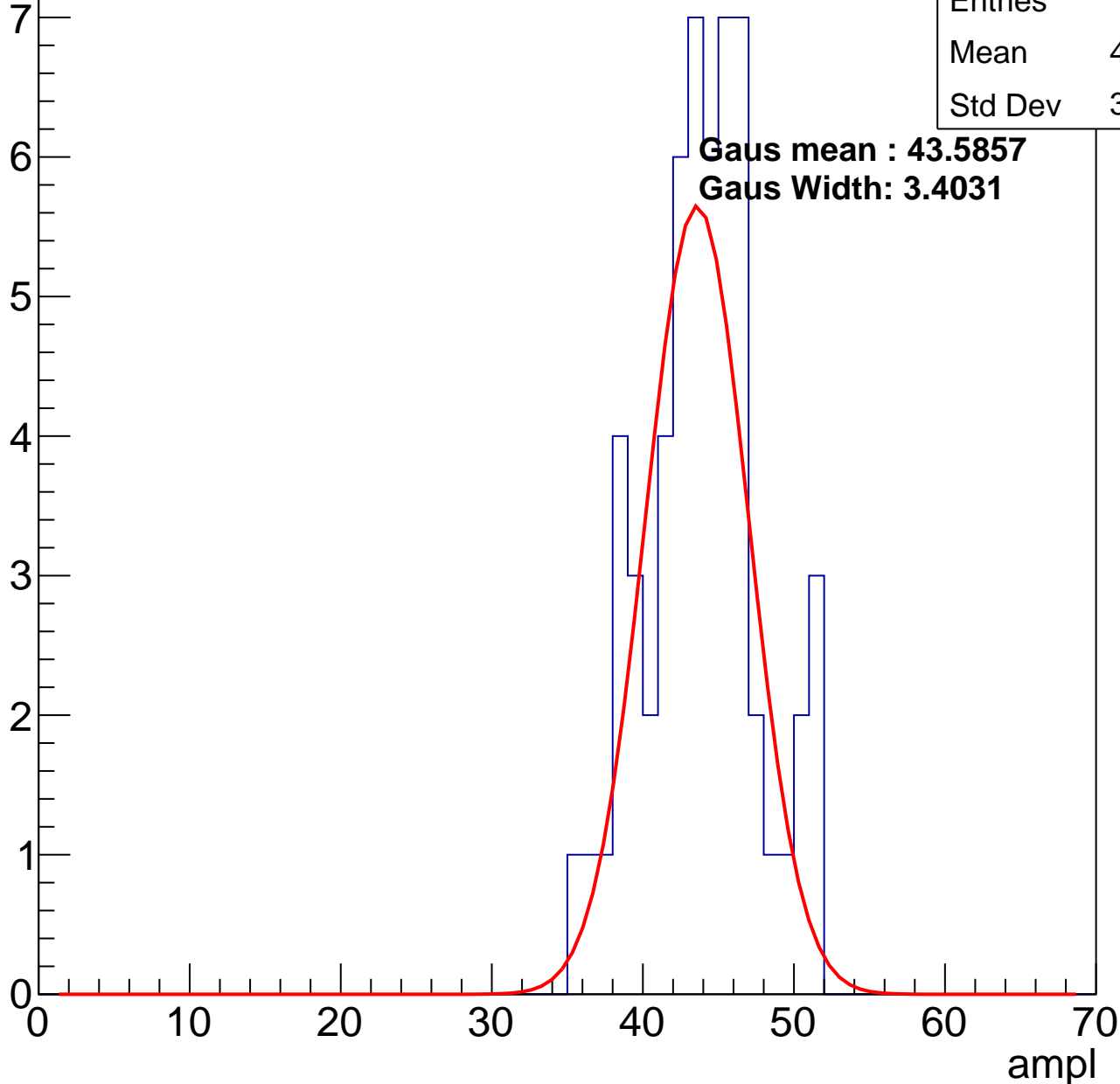
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.43
Std Dev	3.728

**Gaus mean : 43.5857**

**Gaus Width: 3.4031**

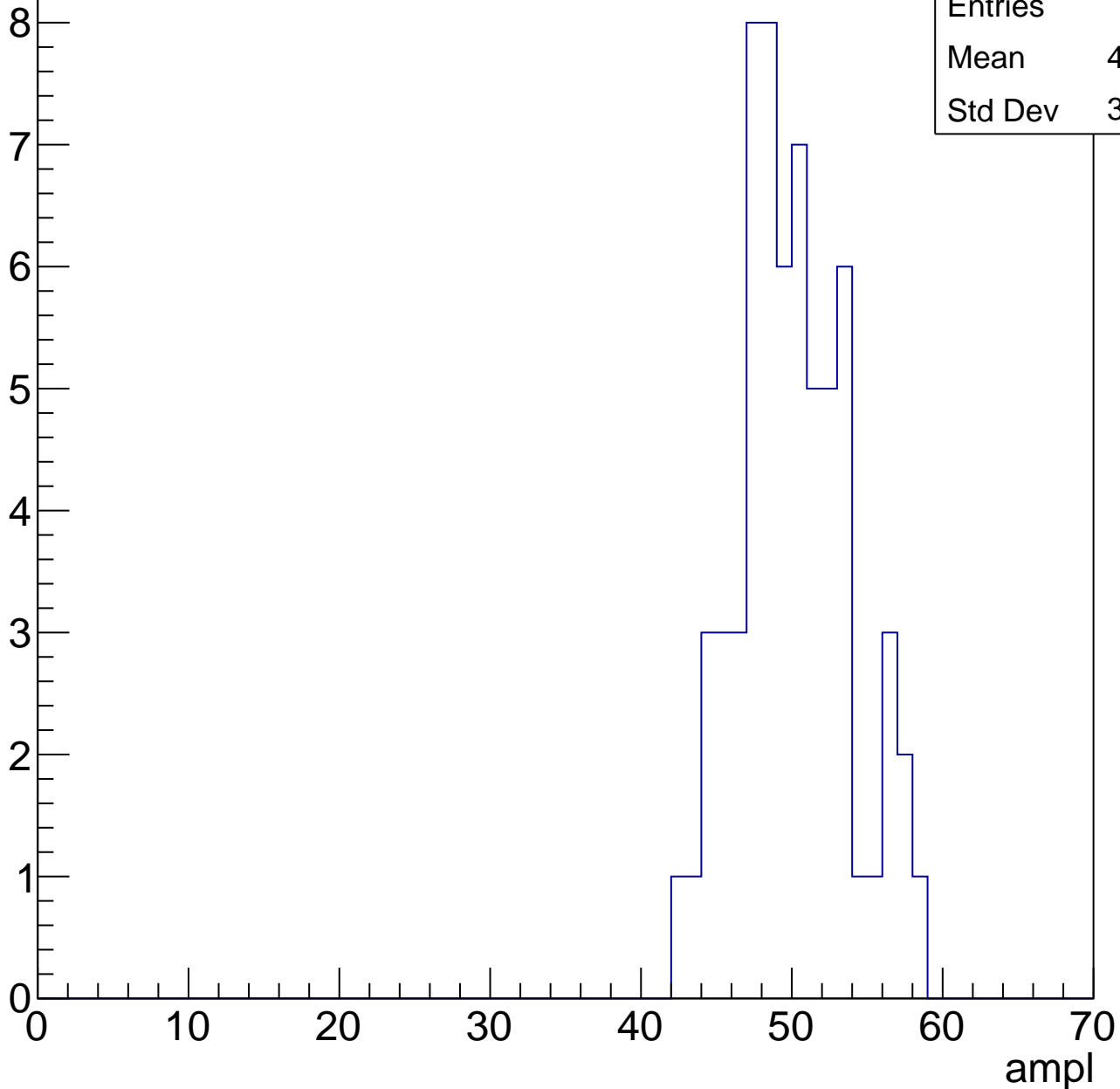


# B0L000S, U26-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.62
Std Dev	3.612

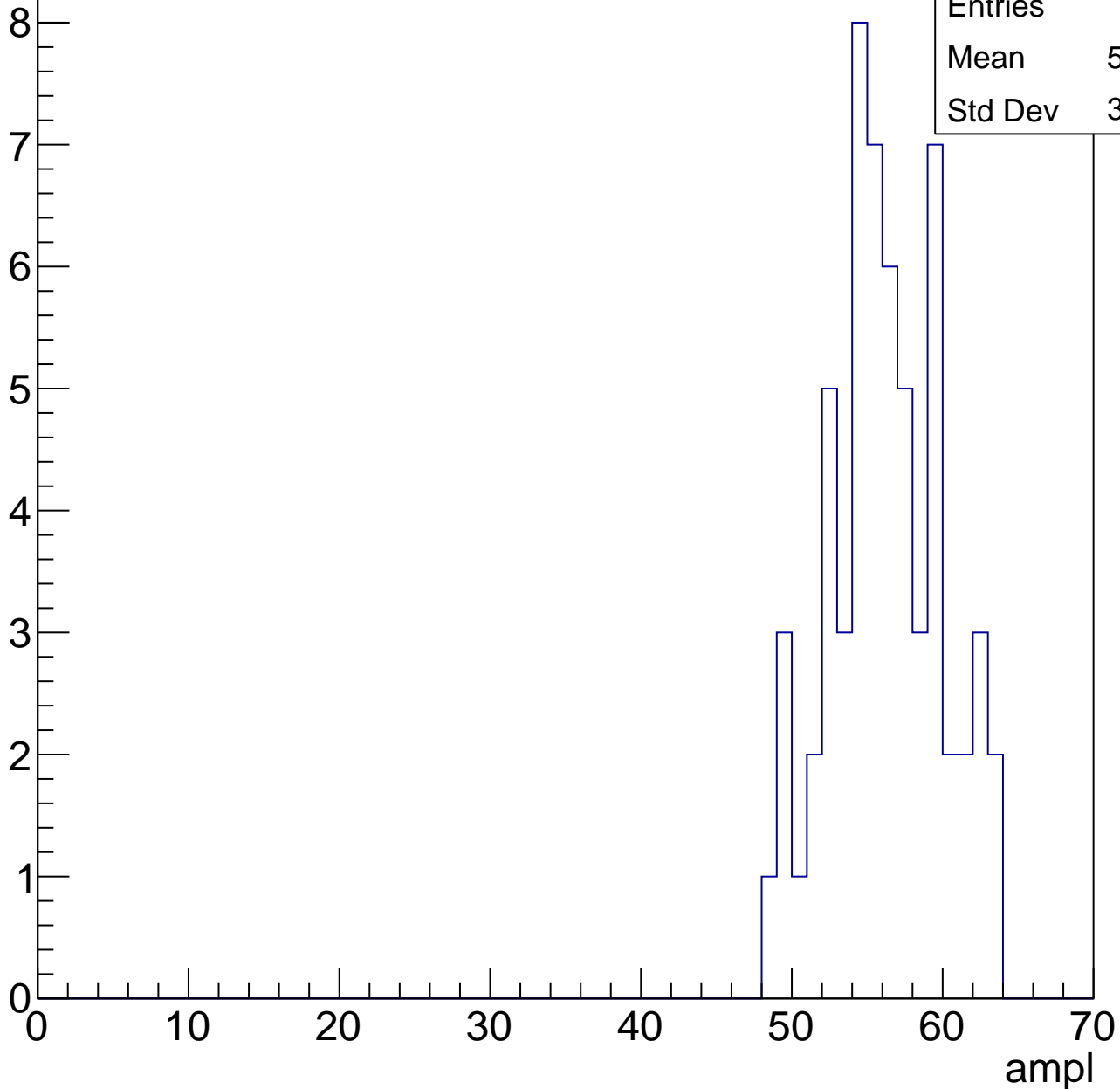


# B0L000S, U26-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	55.75
Std Dev	3.668

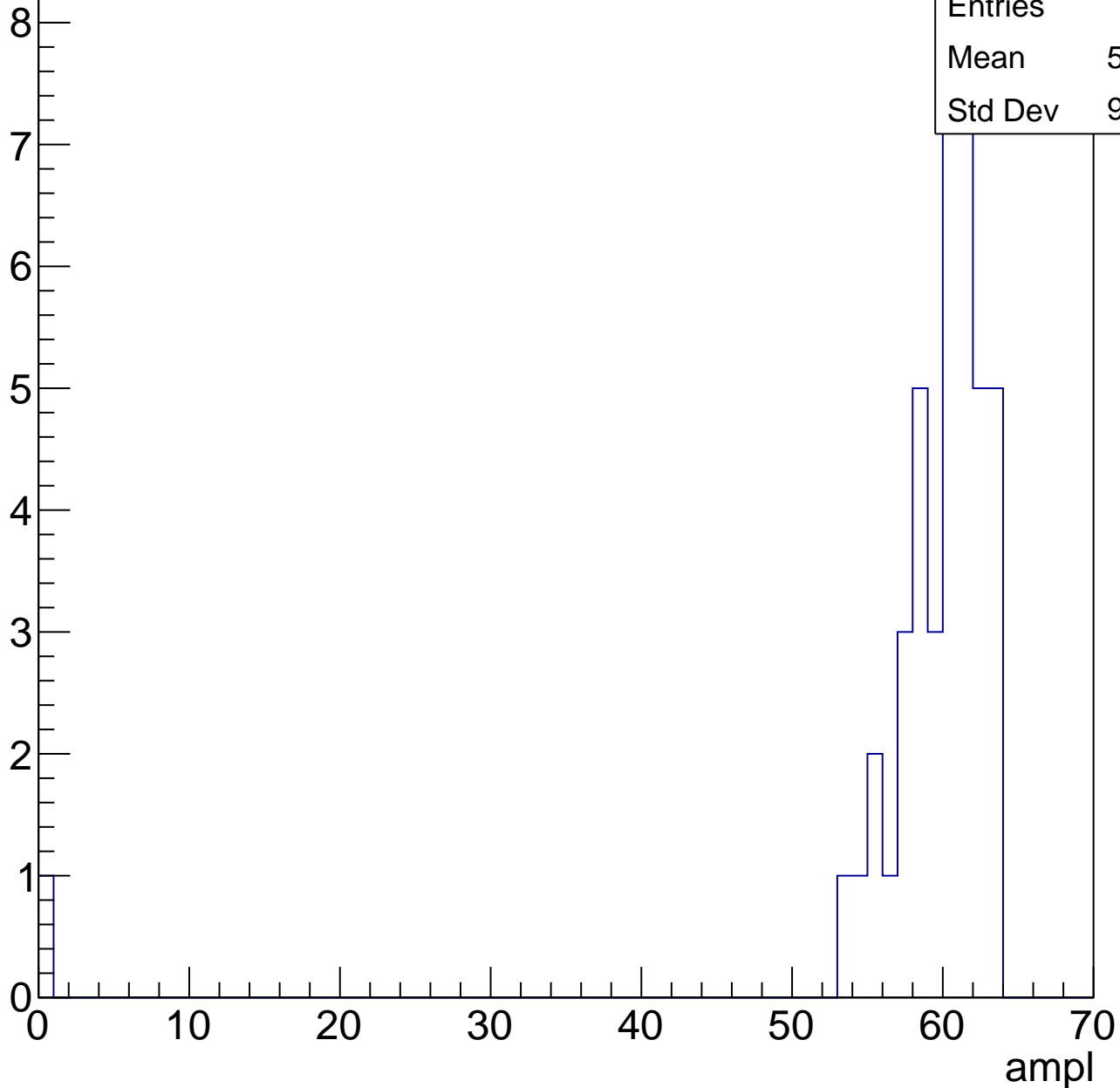


# B0L000S, U26-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

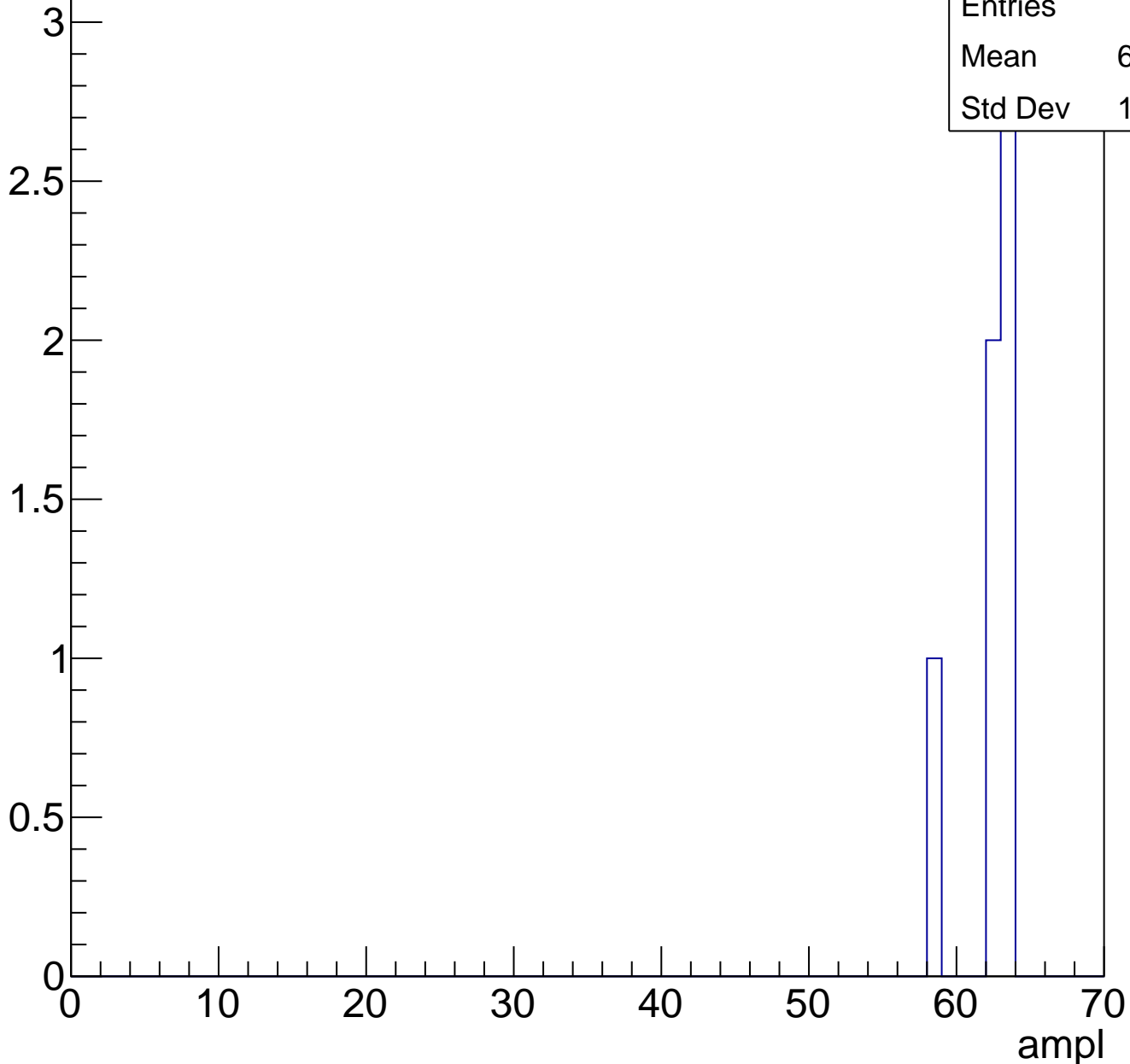
Entries	43
Mean	58.23
Std Dev	9.326



# B0L000S, U26-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch125, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	29.49
Std Dev	5.021

**Gaus mean : 30.1794**

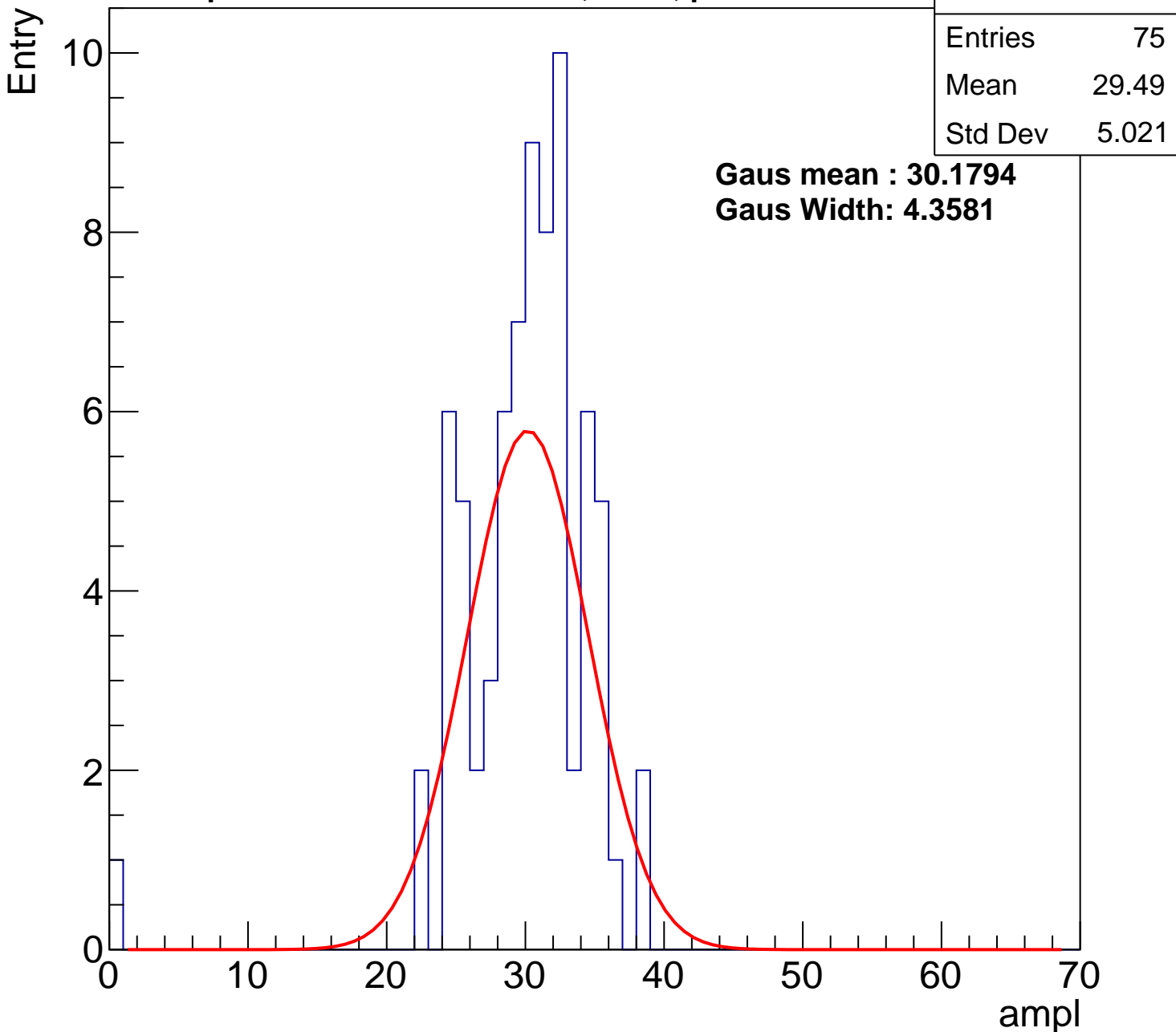
**Gaus Width: 4.3581**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U26-ch125, adc1

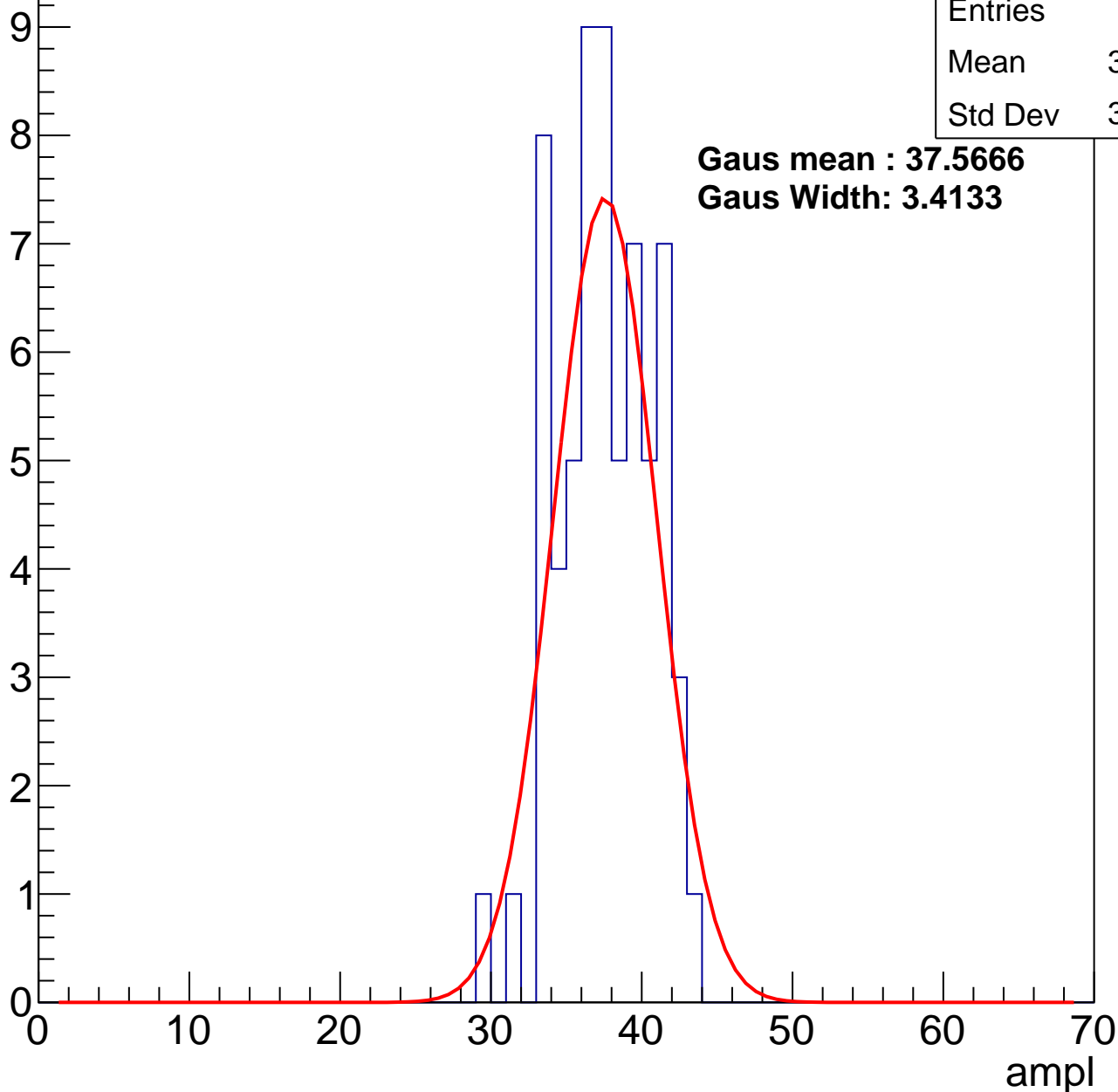
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.09
Std Dev	3.022

**Gaus mean : 37.5666**

**Gaus Width: 3.4133**



# B0L000S, U26-ch125, adc2

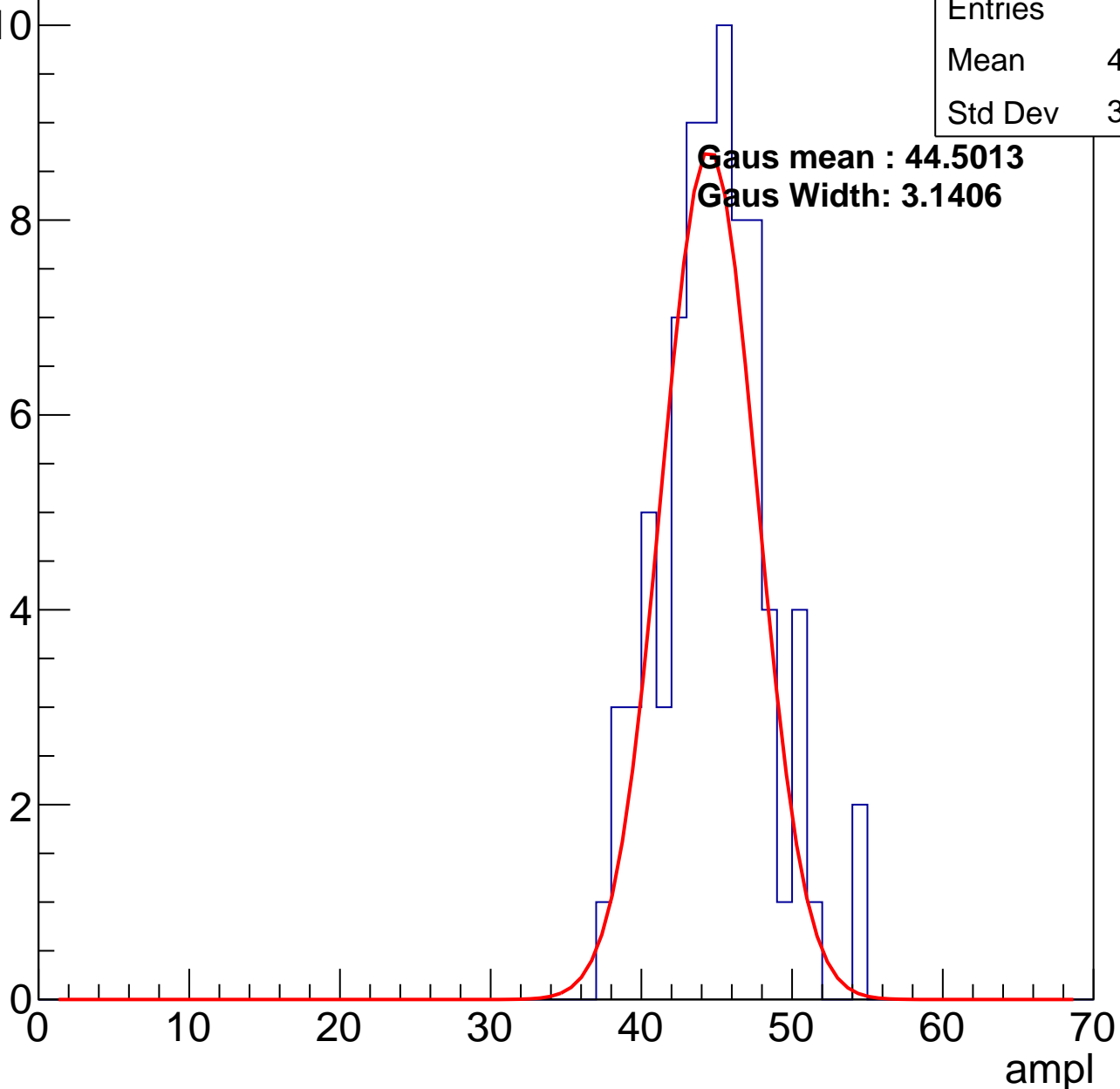
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	44.38
Std Dev	3.509

**Gaus mean : 44.5013**

**Gaus Width: 3.1406**

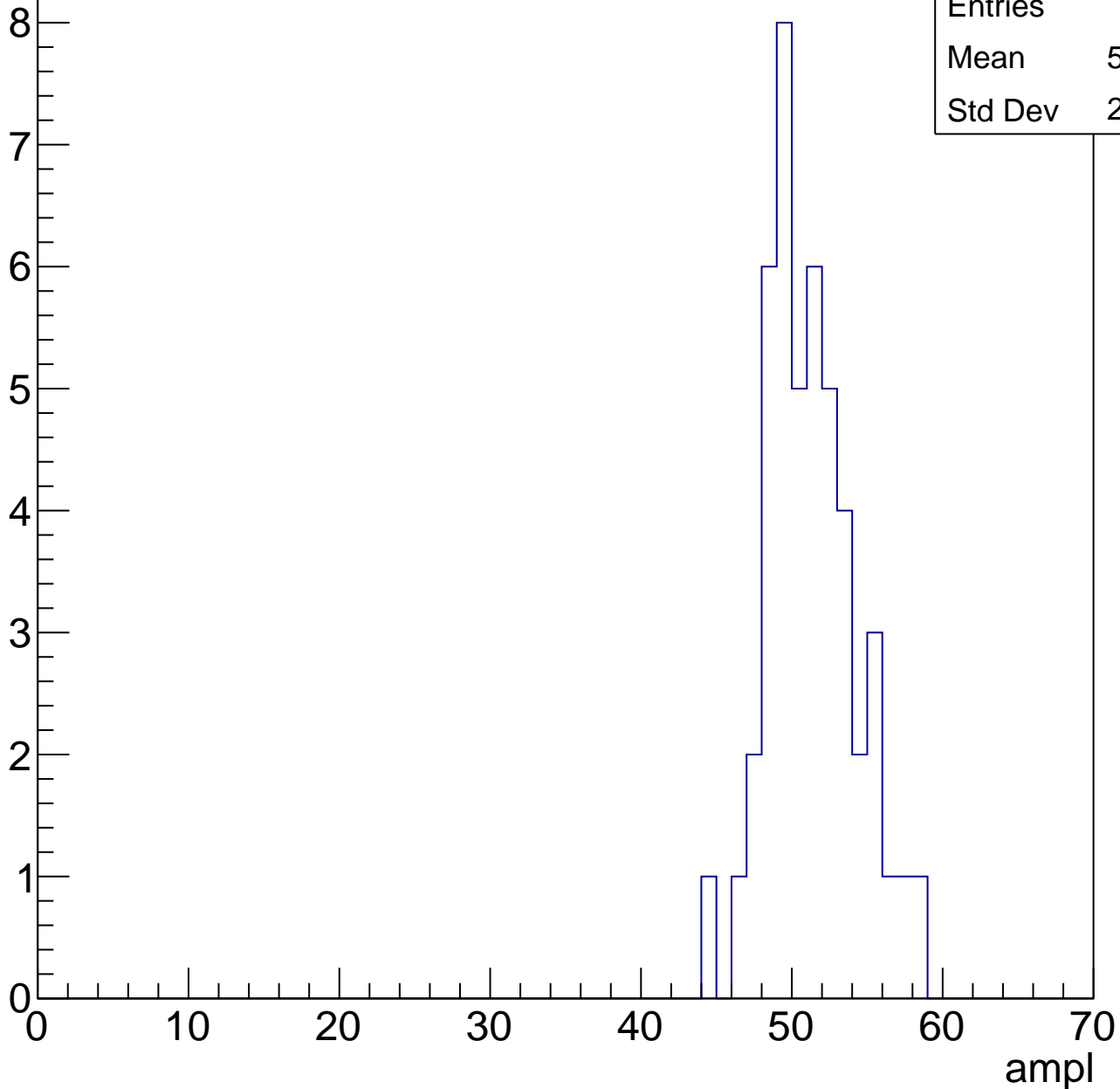


# B0L000S, U26-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	50.78
Std Dev	2.919

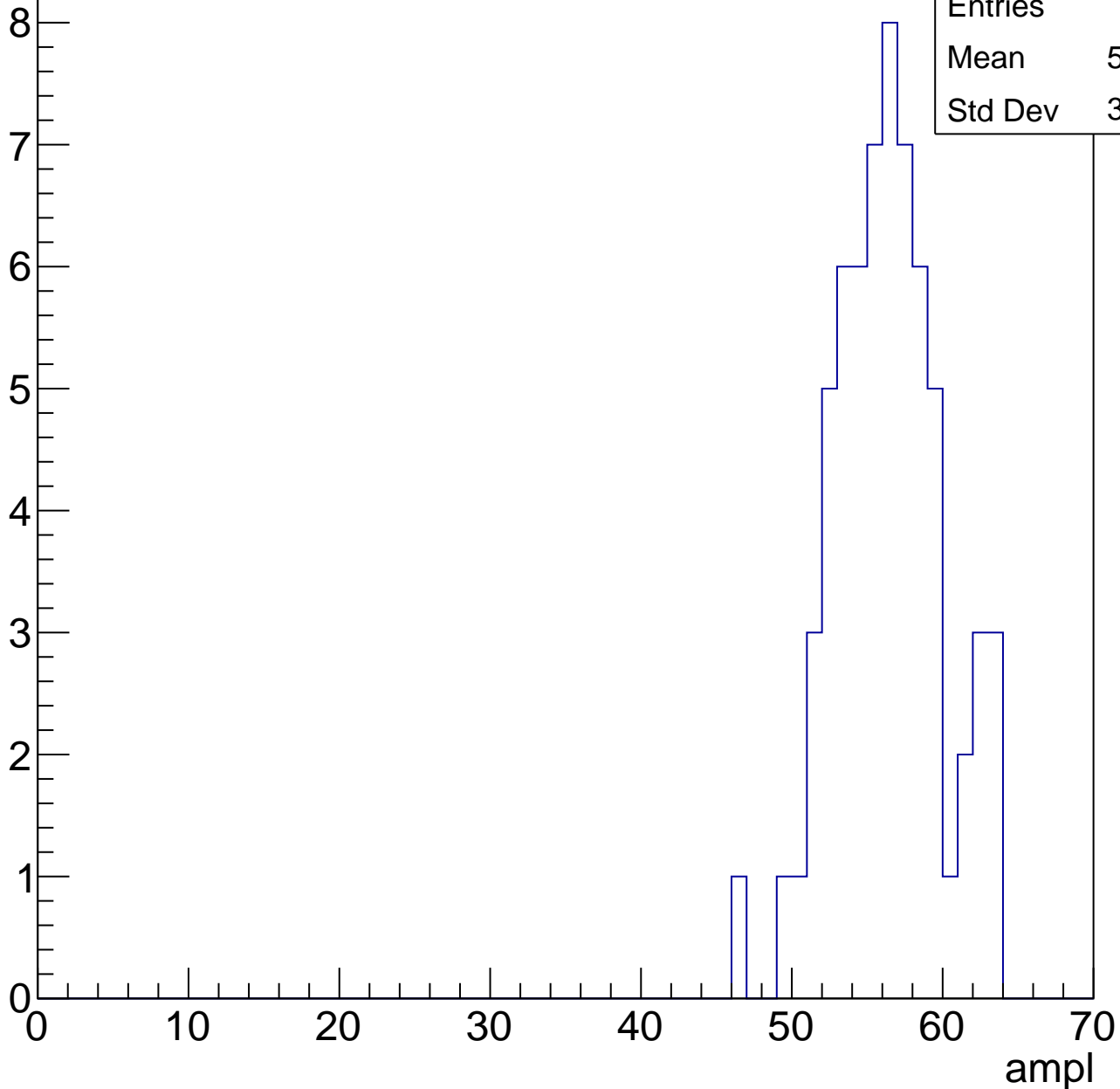


# B0L000S, U26-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

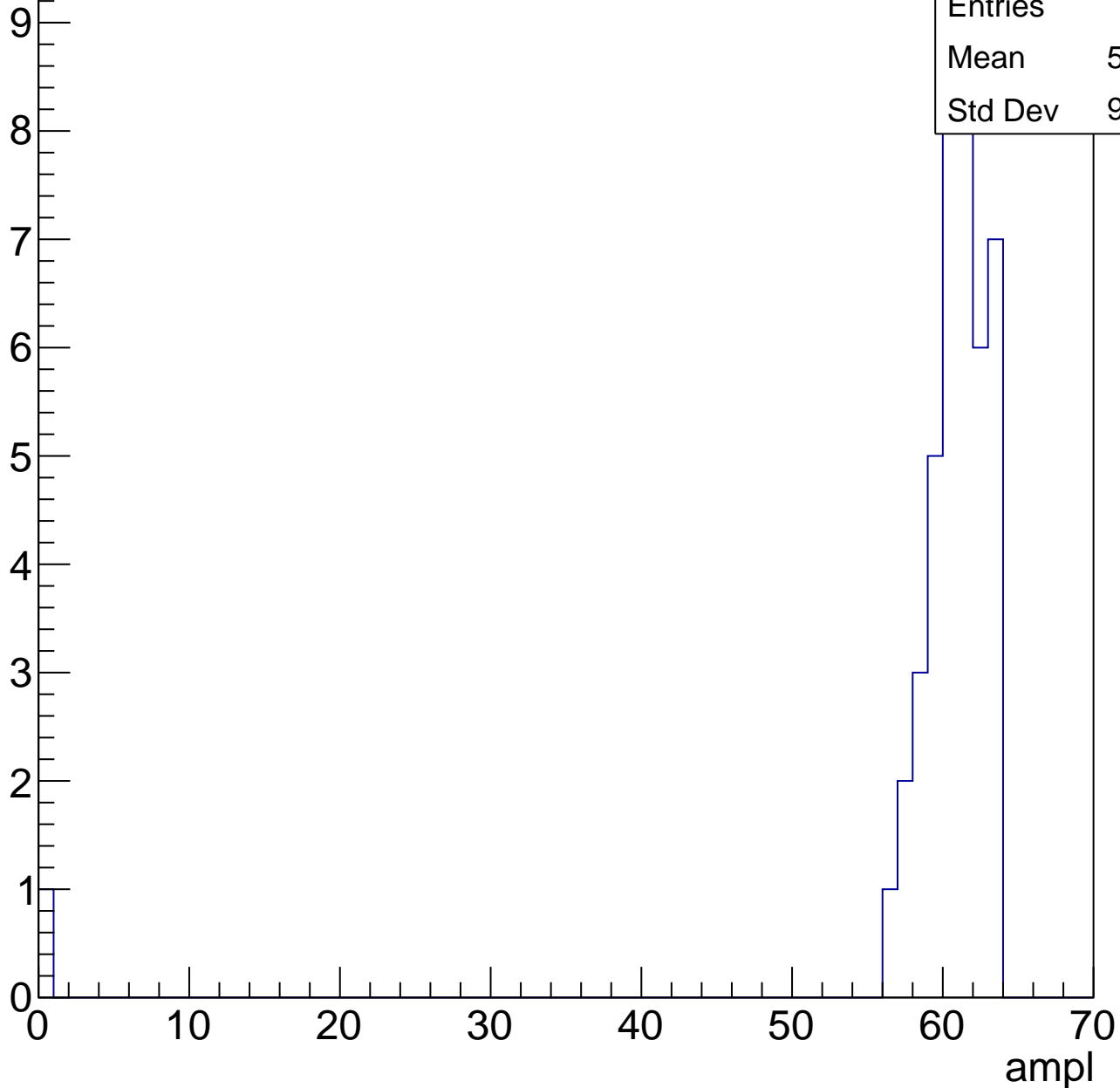
Entries	65
Mean	55.88
Std Dev	3.554



# B0L000S, U26-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U26-ch126, adc0

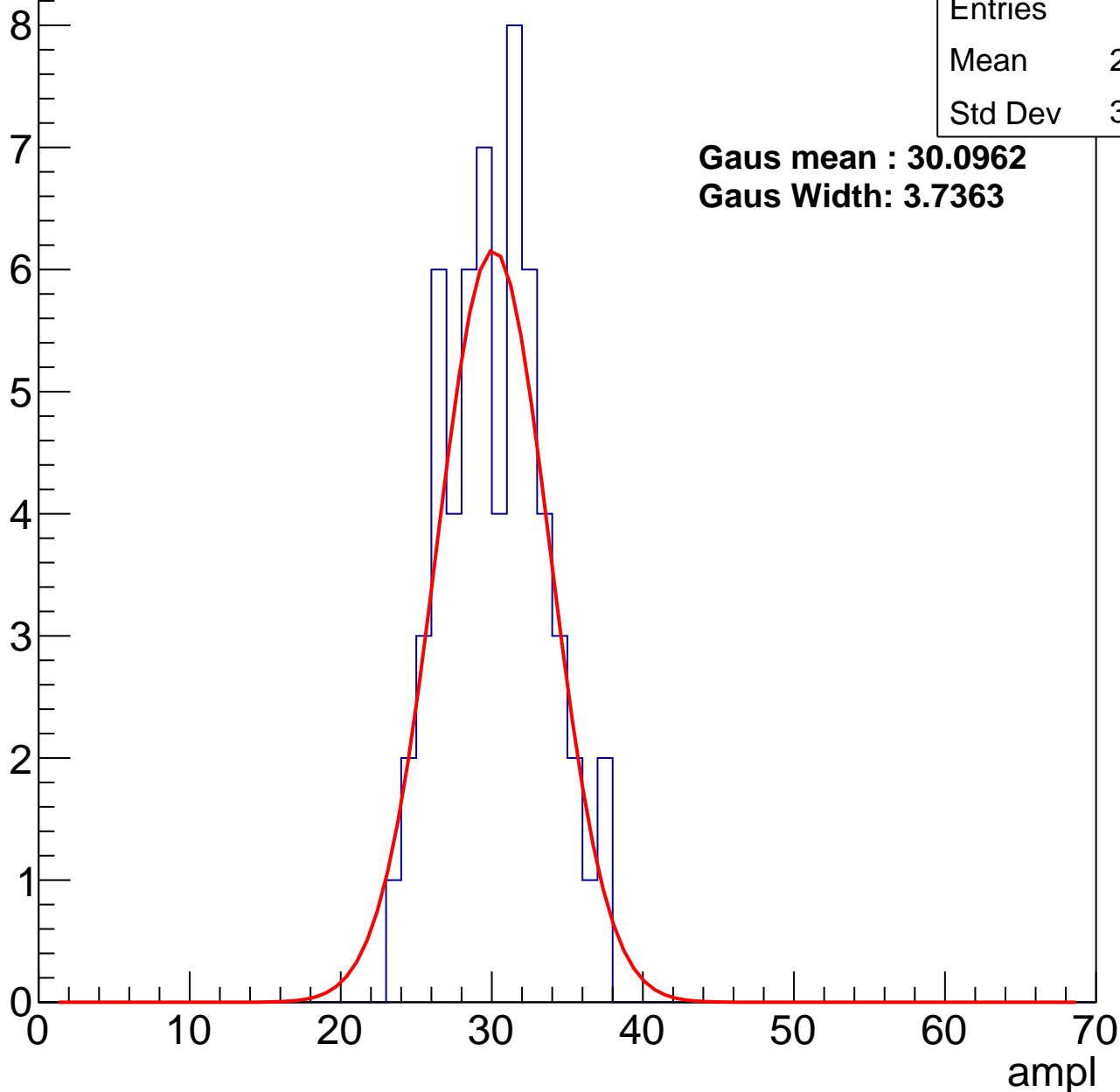
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.75
Std Dev	3.332

**Gaus mean : 30.0962**

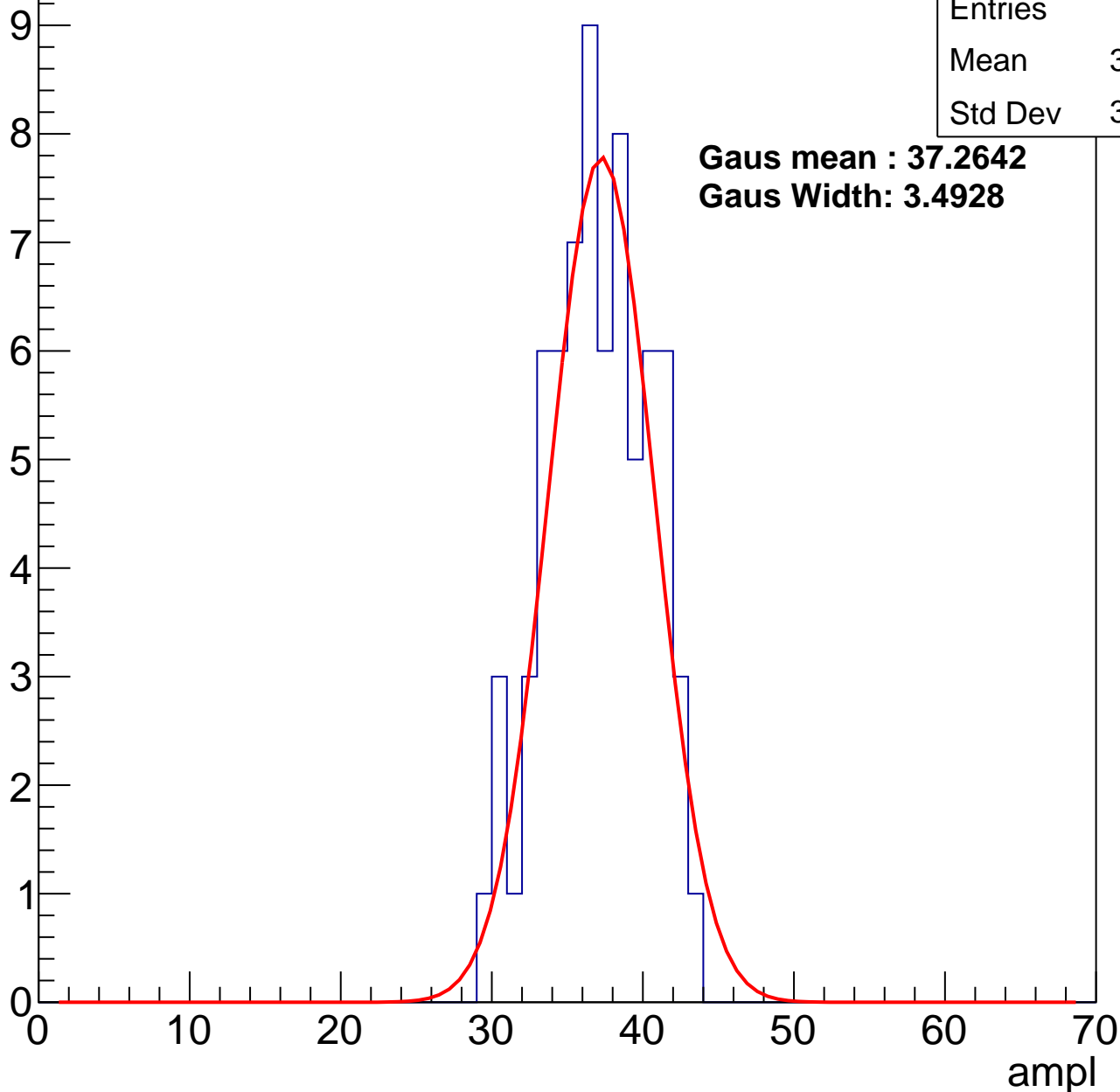
**Gaus Width: 3.7363**



# B0L000S, U26-ch126, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch126, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	43.12
Std Dev	3.292

**Gaus mean : 43.9087**

**Gaus Width: 3.7169**

Entry

10  
8  
6  
4  
2  
0

ampl

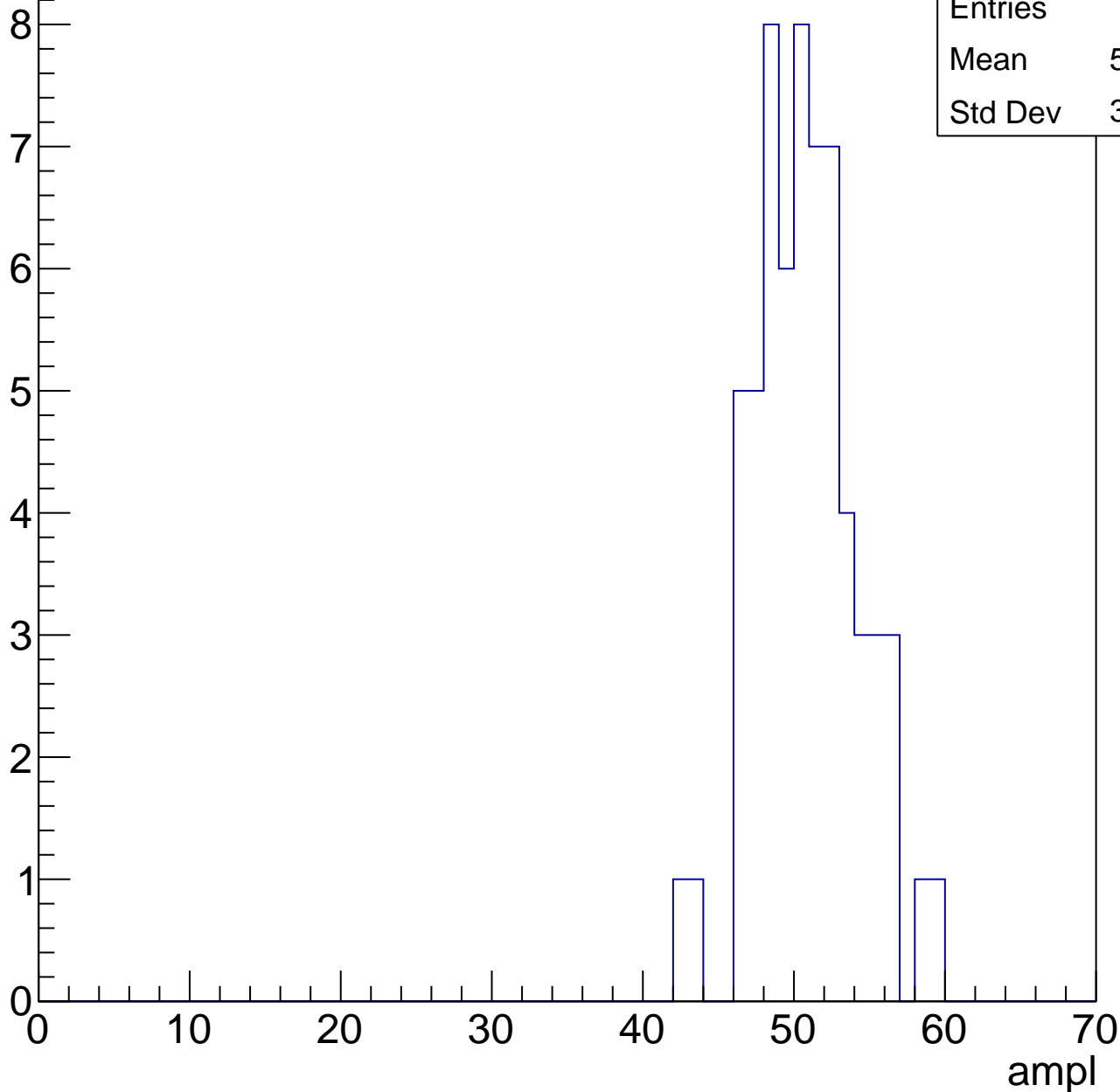
0 10 20 30 40 50 60 70

# B0L000S, U26-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	50.37
Std Dev	3.368

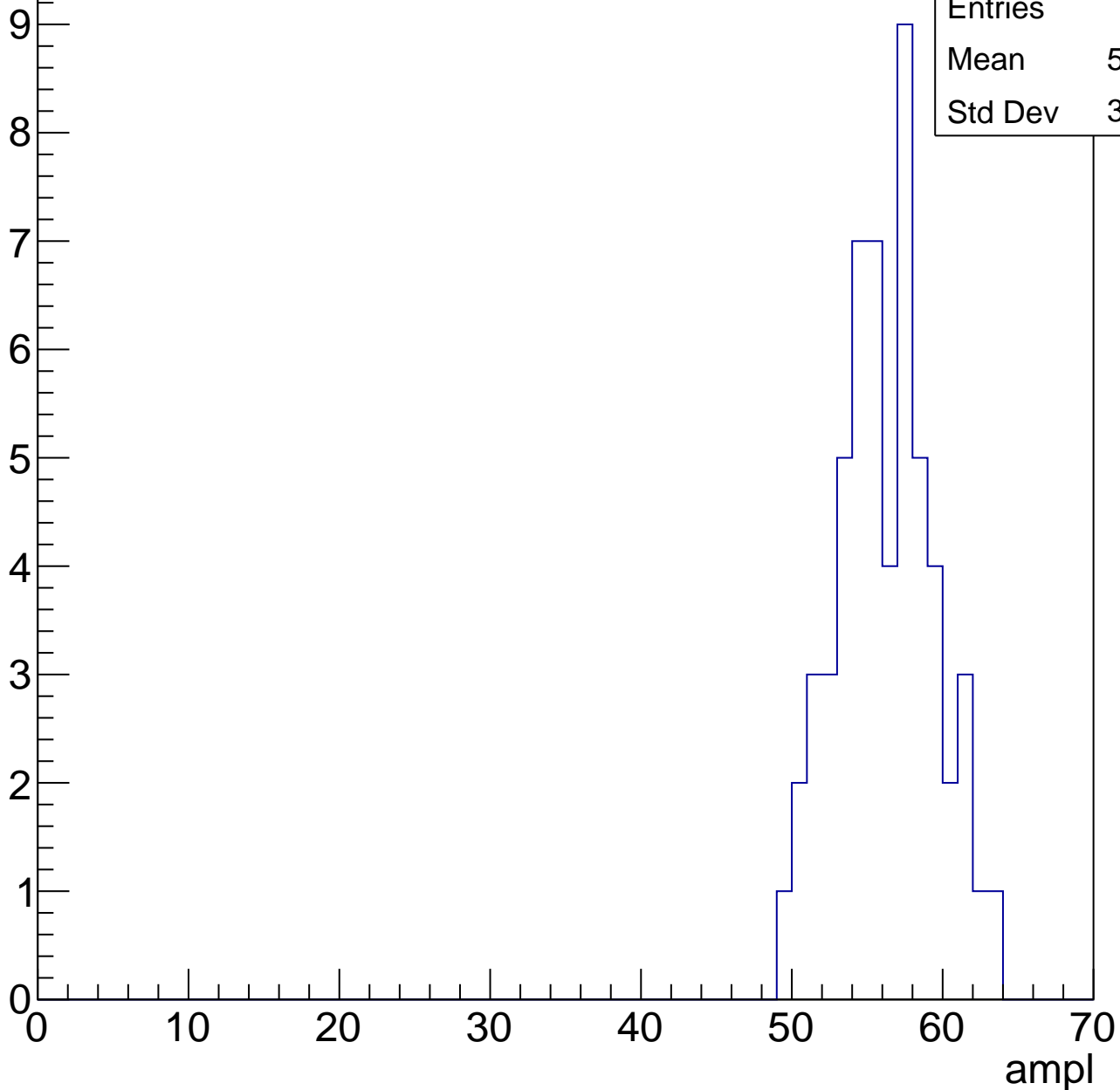


# B0L000S, U26-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	55.74
Std Dev	3.182

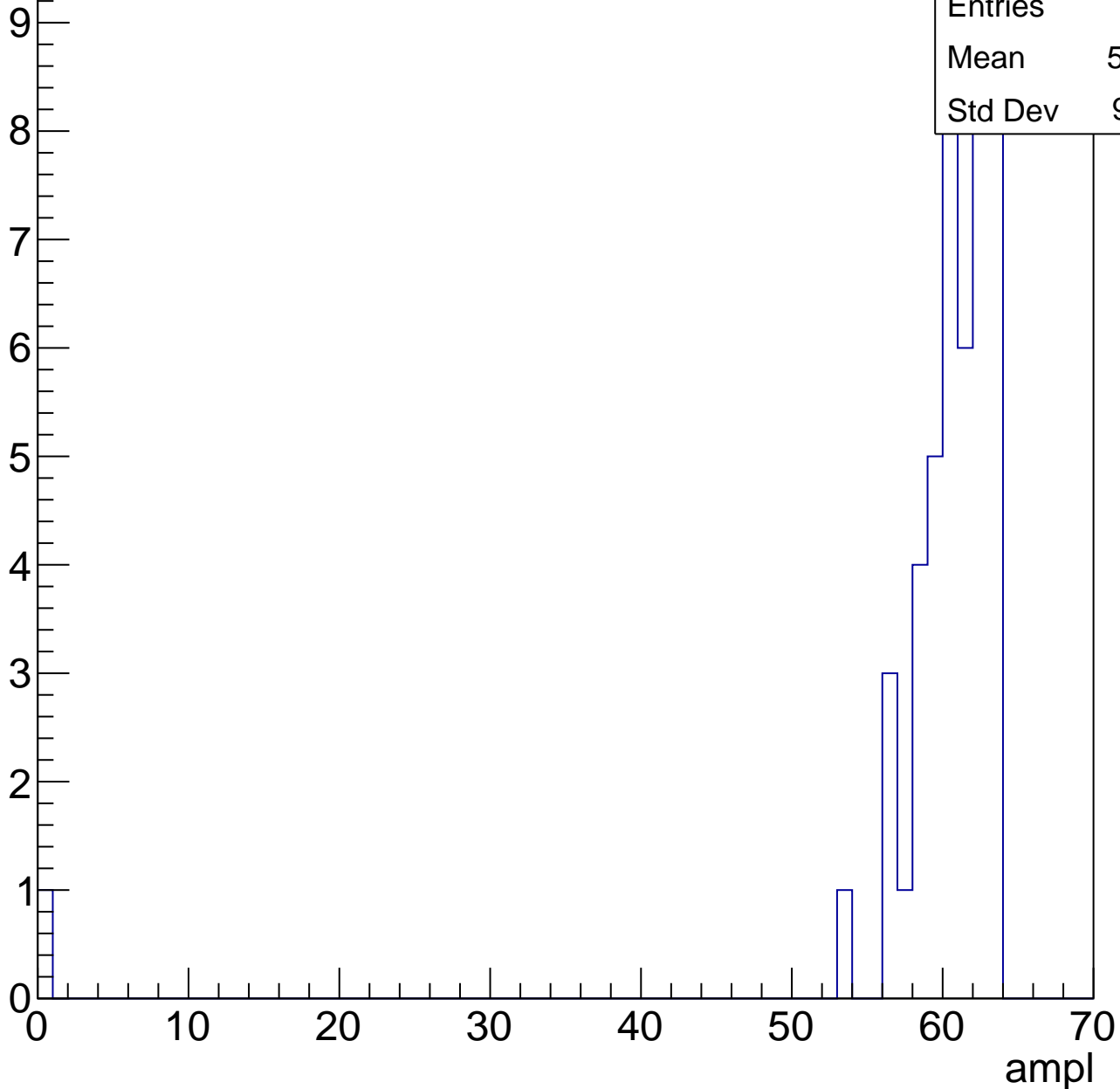


# B0L000S, U26-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.98
Std Dev	9.081



# B0L000S, U26-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U26-ch127, adc0

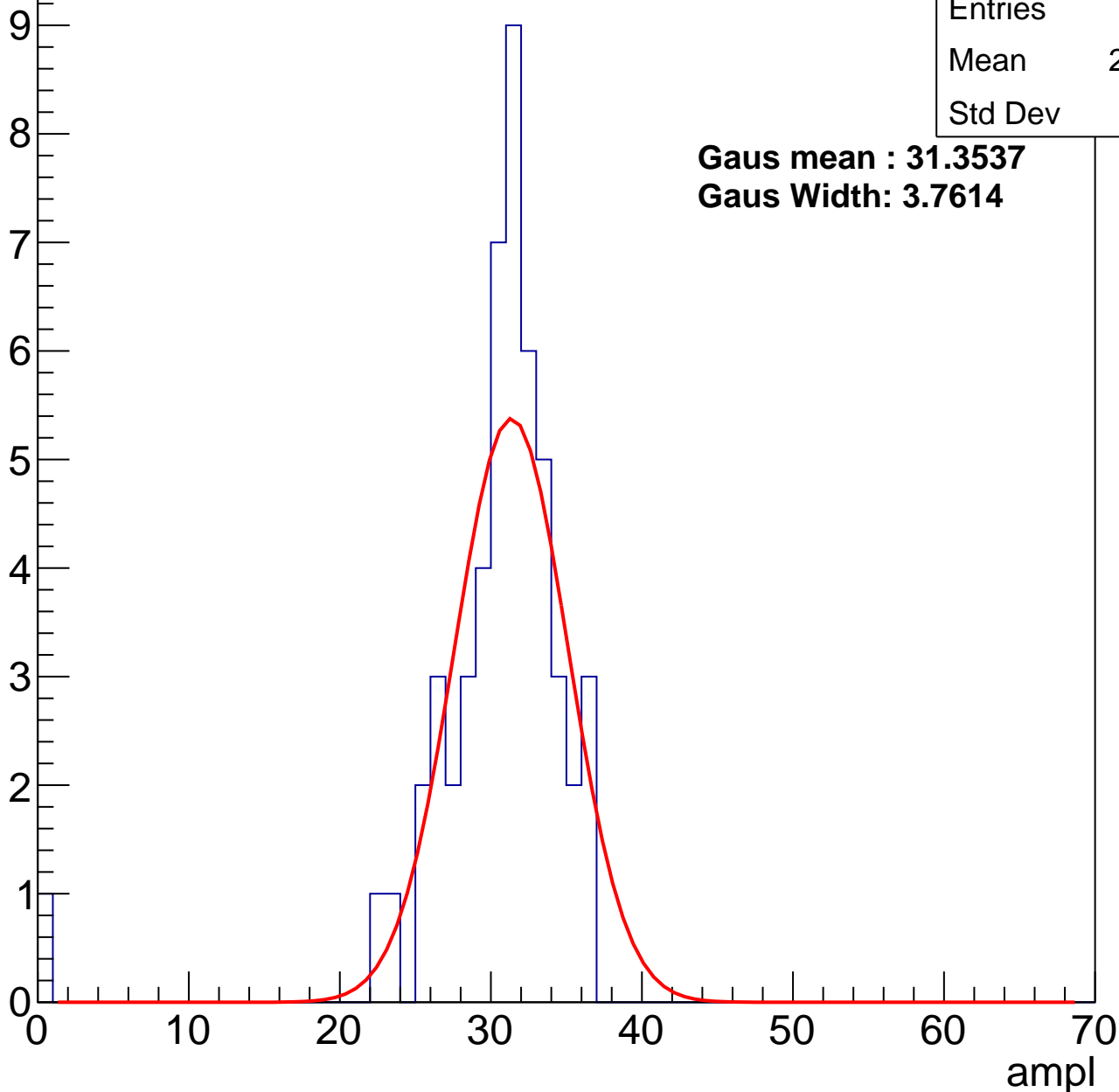
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	29.87
Std Dev	5.24

**Gaus mean : 31.3537**

**Gaus Width: 3.7614**



# B0L000S, U26-ch127, adc1

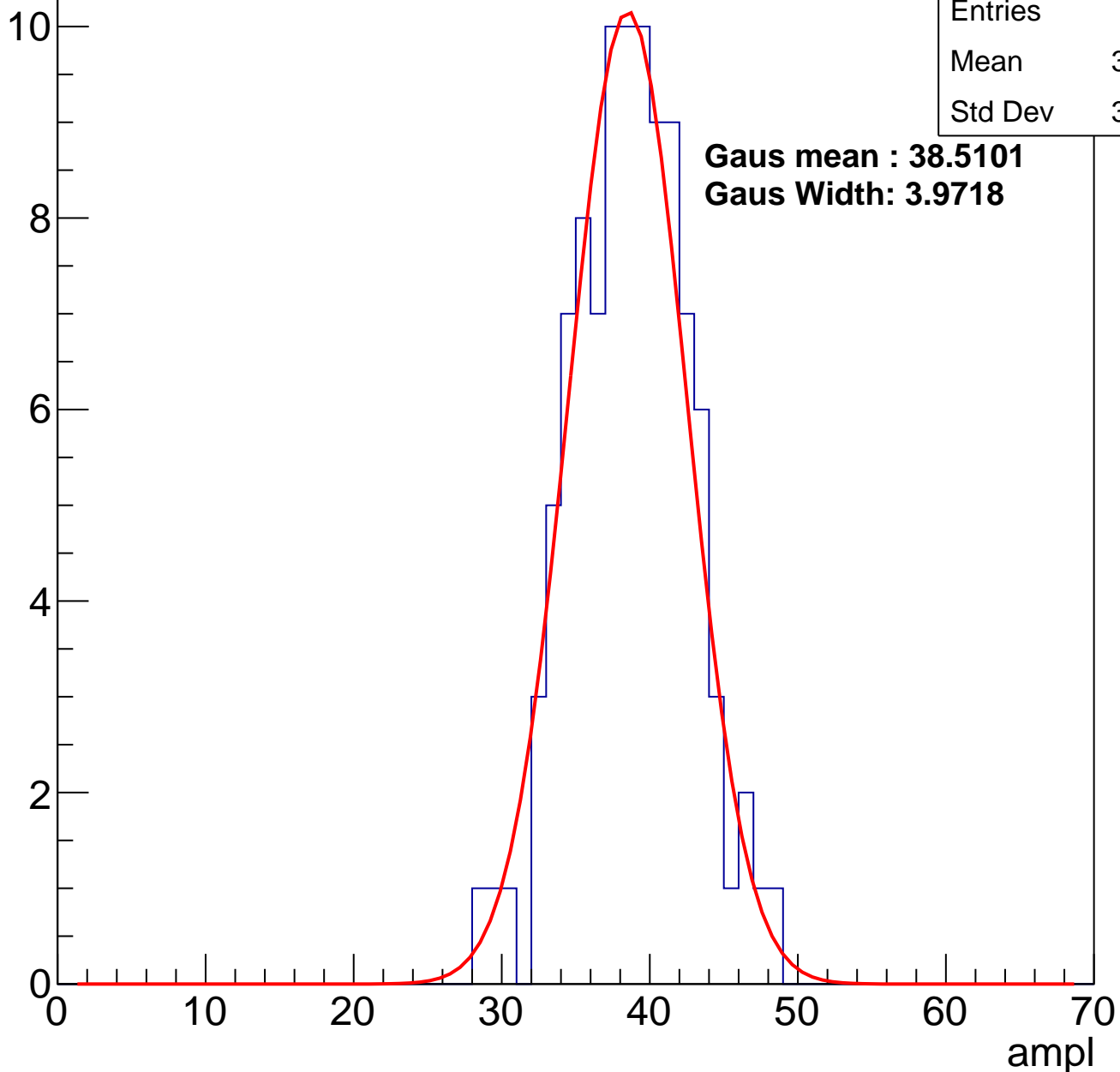
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	102
Mean	38.26
Std Dev	3.896

**Gaus mean : 38.5101**

**Gaus Width: 3.9718**

Entry



# B0L000S, U26-ch127, adc2

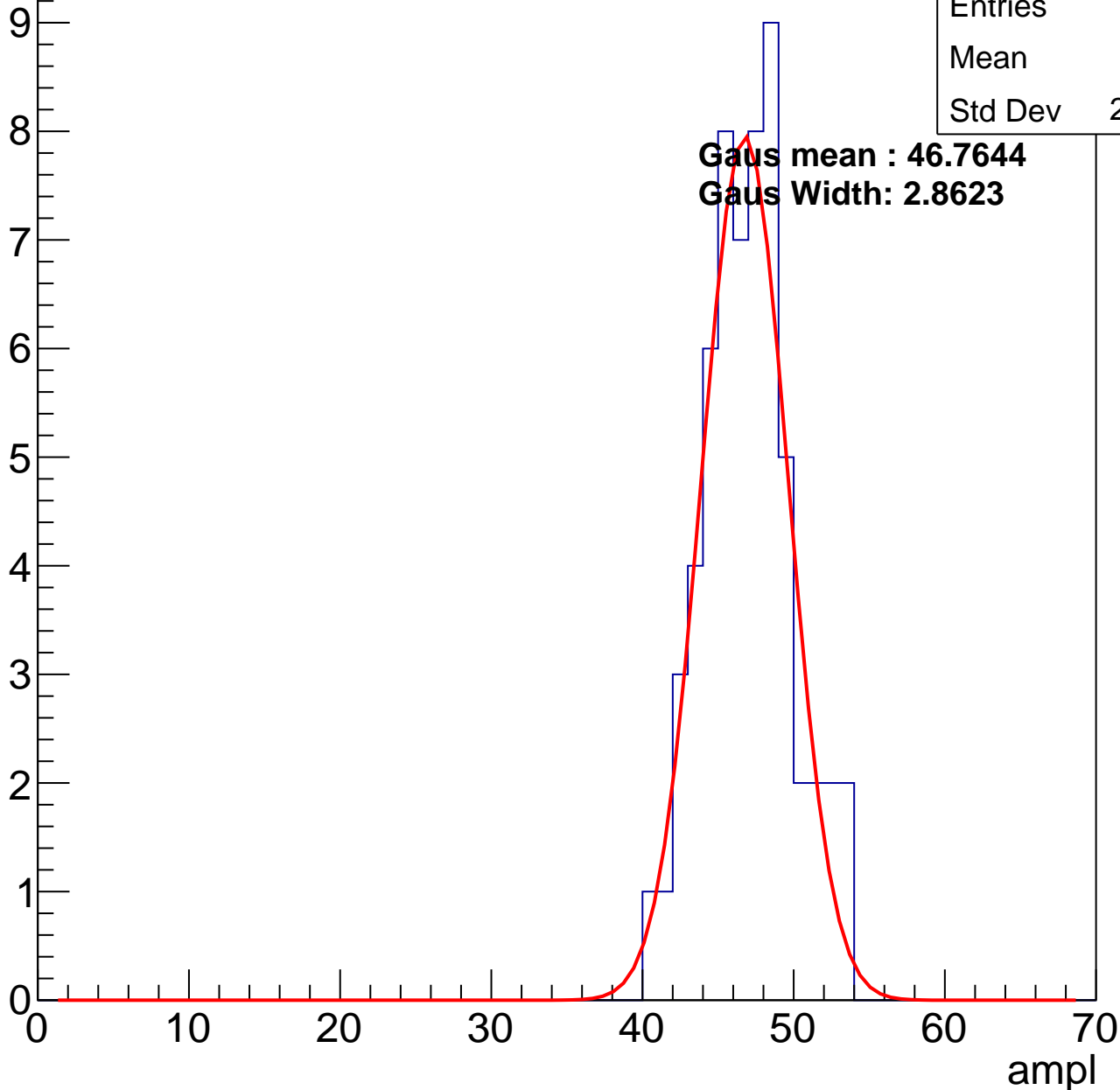
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	46.5
Std Dev	2.895

Gaus mean : 46.7644

Gaus Width: 2.8623

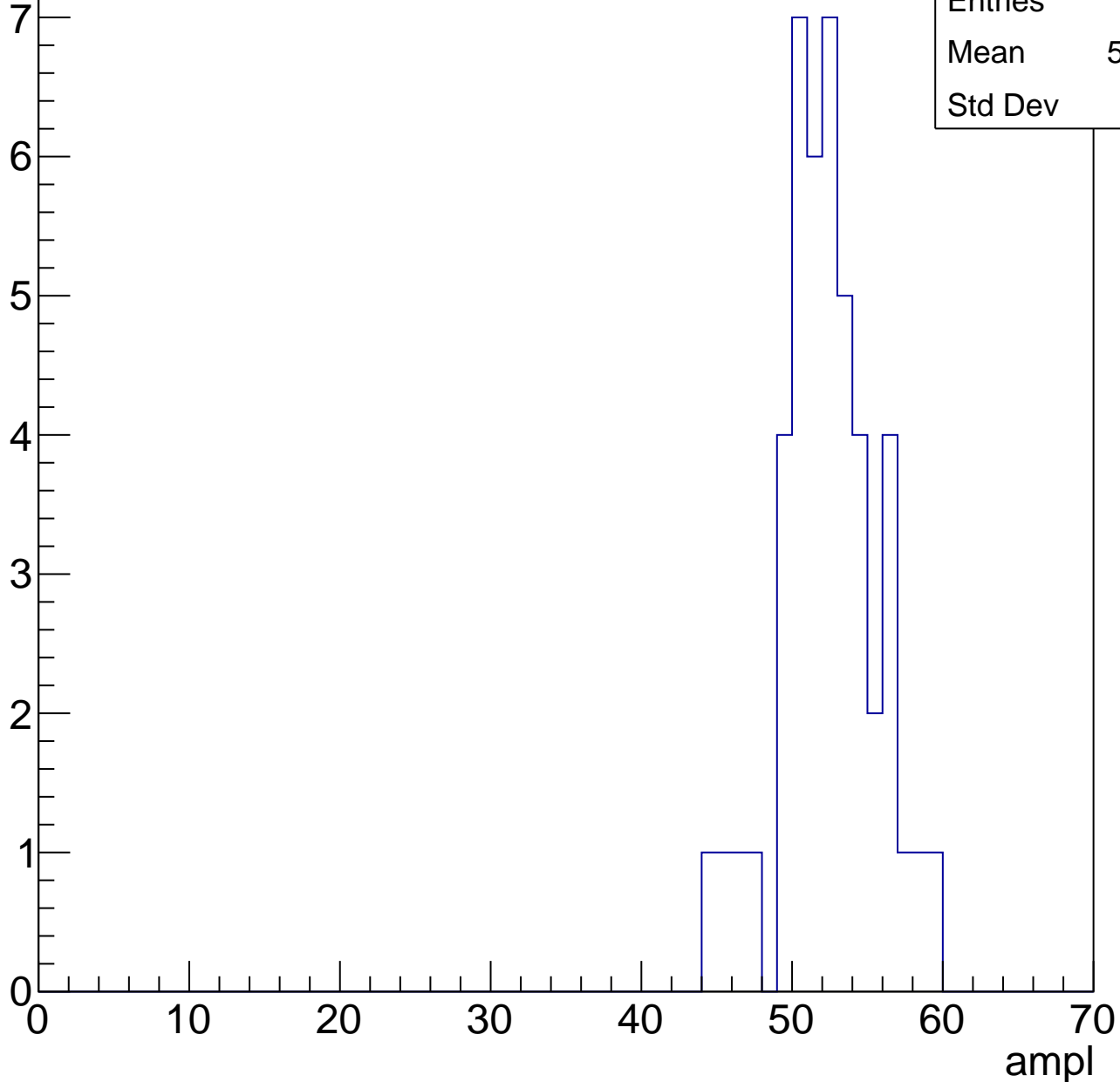


# B0L000S, U26-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	51.89
Std Dev	3.15

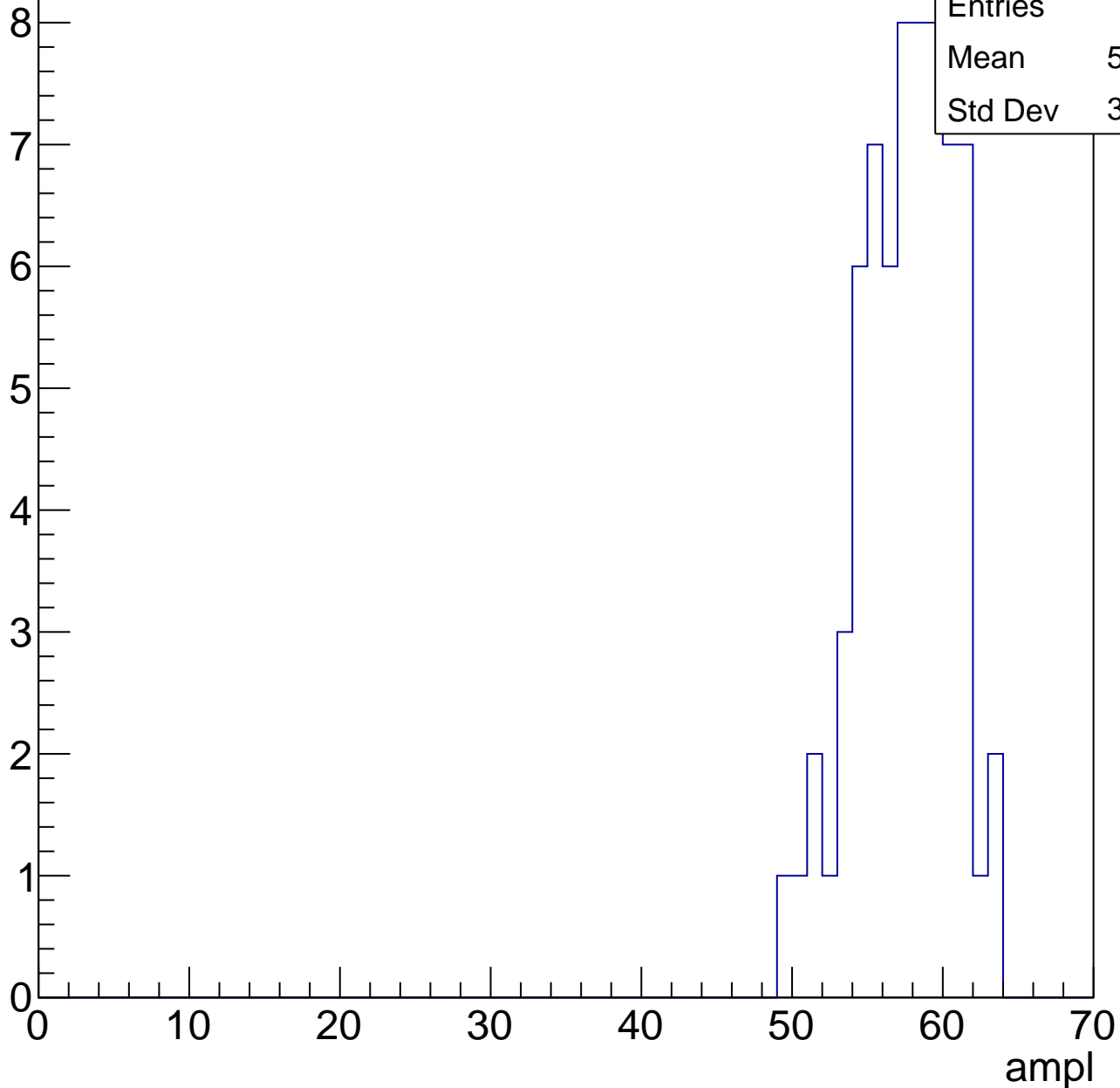


# B0L000S, U26-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	57.12
Std Dev	3.108

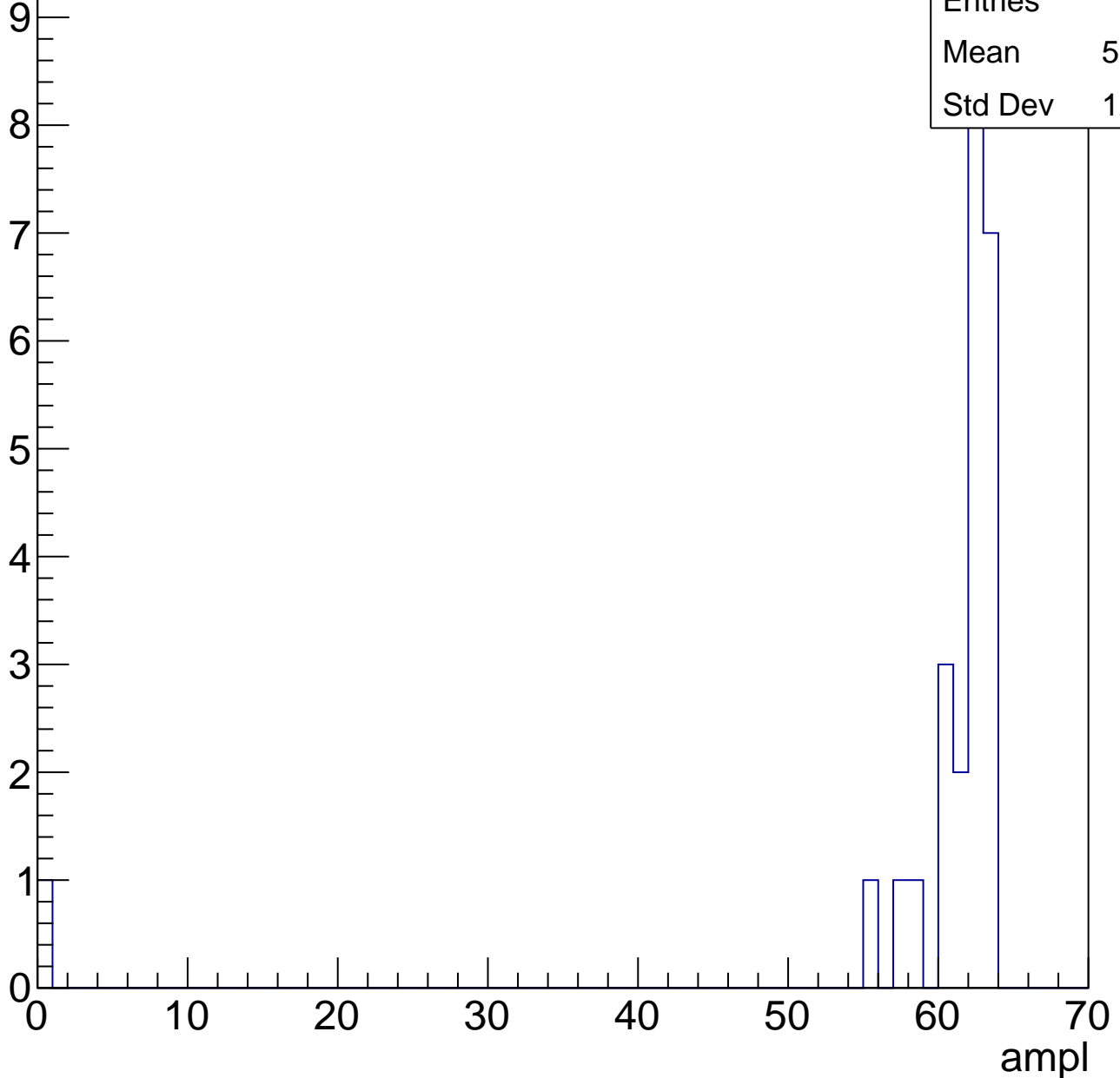


# B0L000S, U26-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

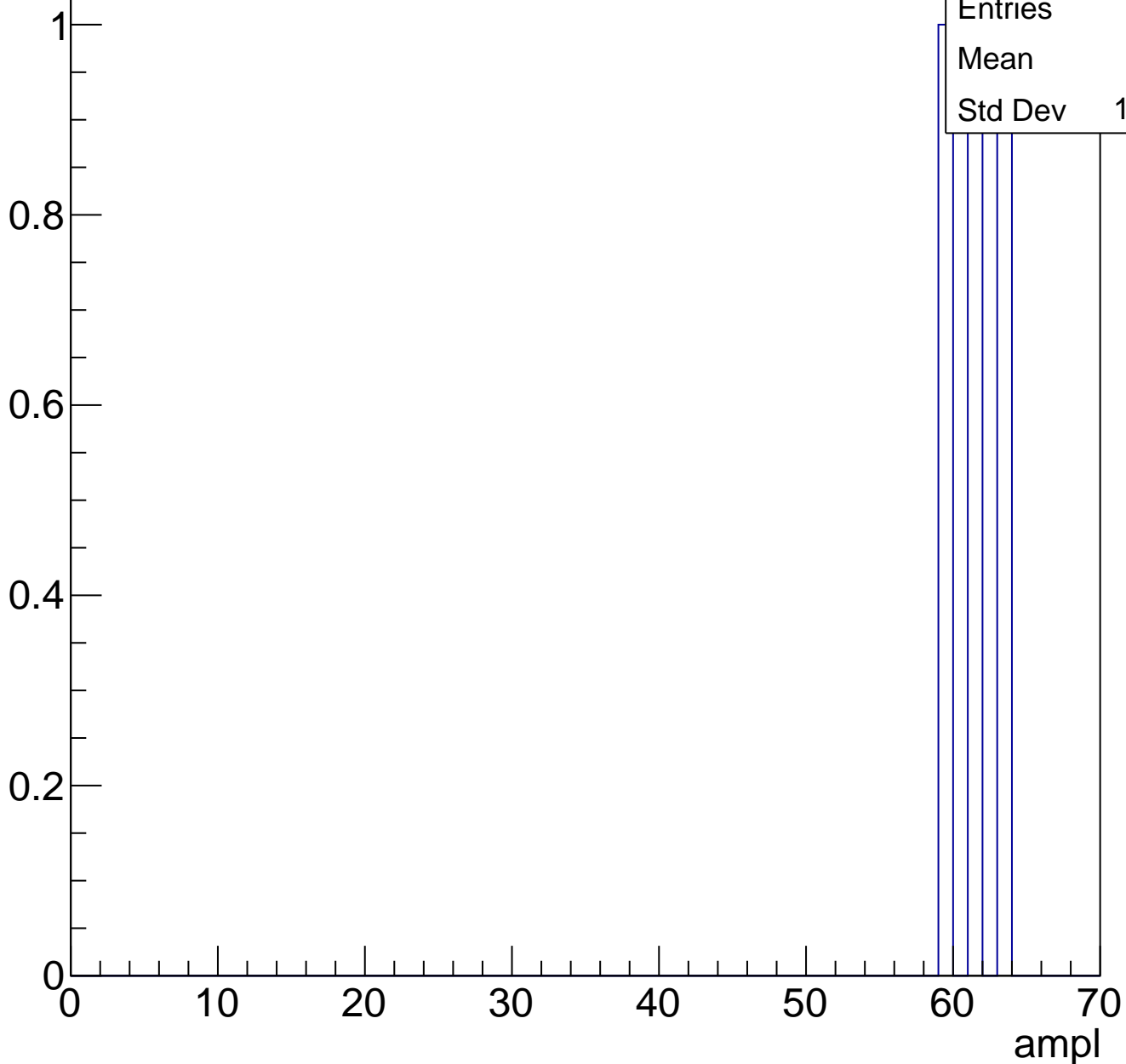
Entries	25
Mean	58.84
Std Dev	12.17



# B0L000S, U26-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U26-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U26-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0