



# B0L000S, U26-ch0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	357
Mean	45.44
Std Dev	10.76

**Turn on : 29.0049**

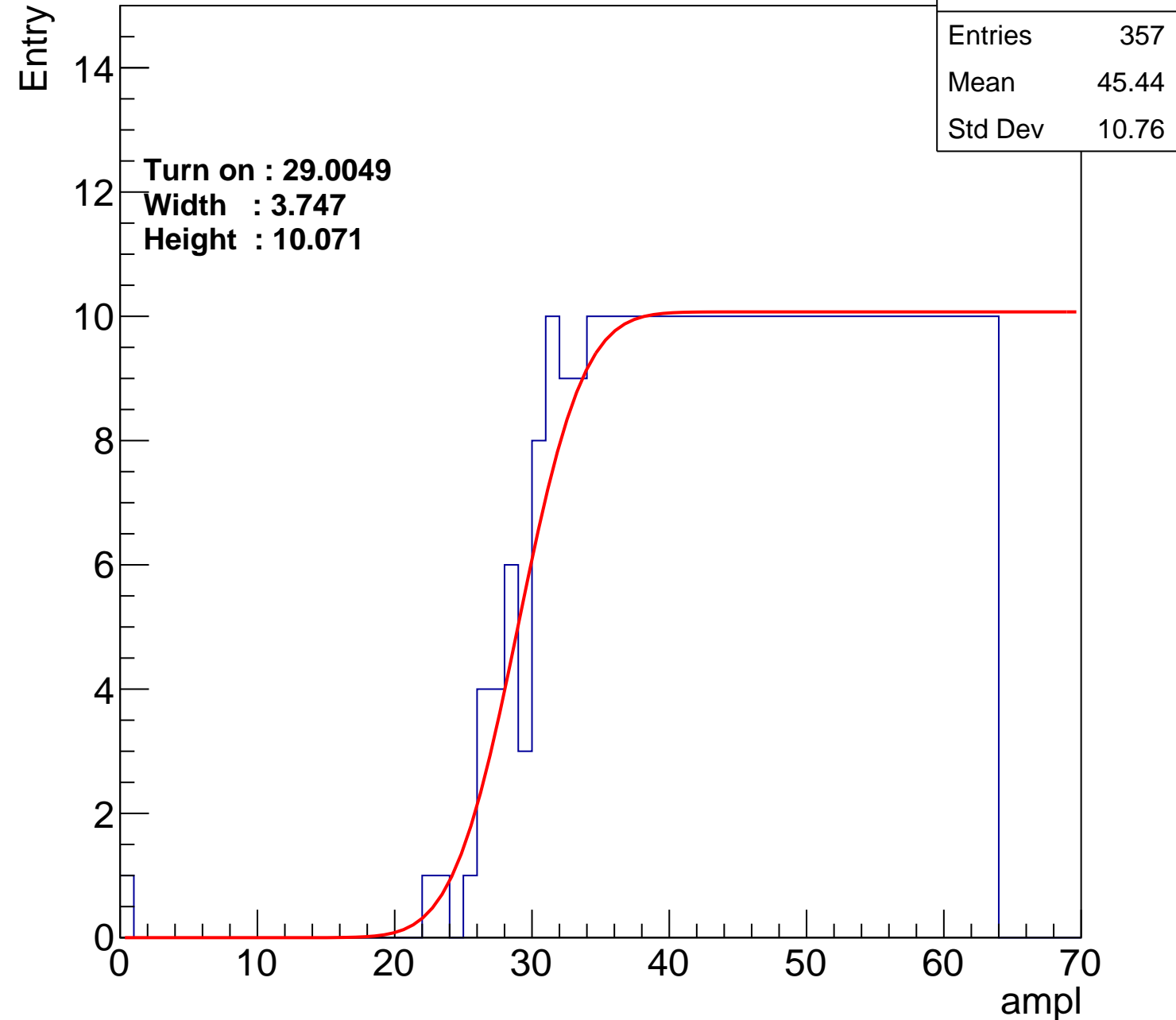
**Width : 3.747**

**Height : 10.071**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.83
Std Dev	11.38

Turn on : 27.8875

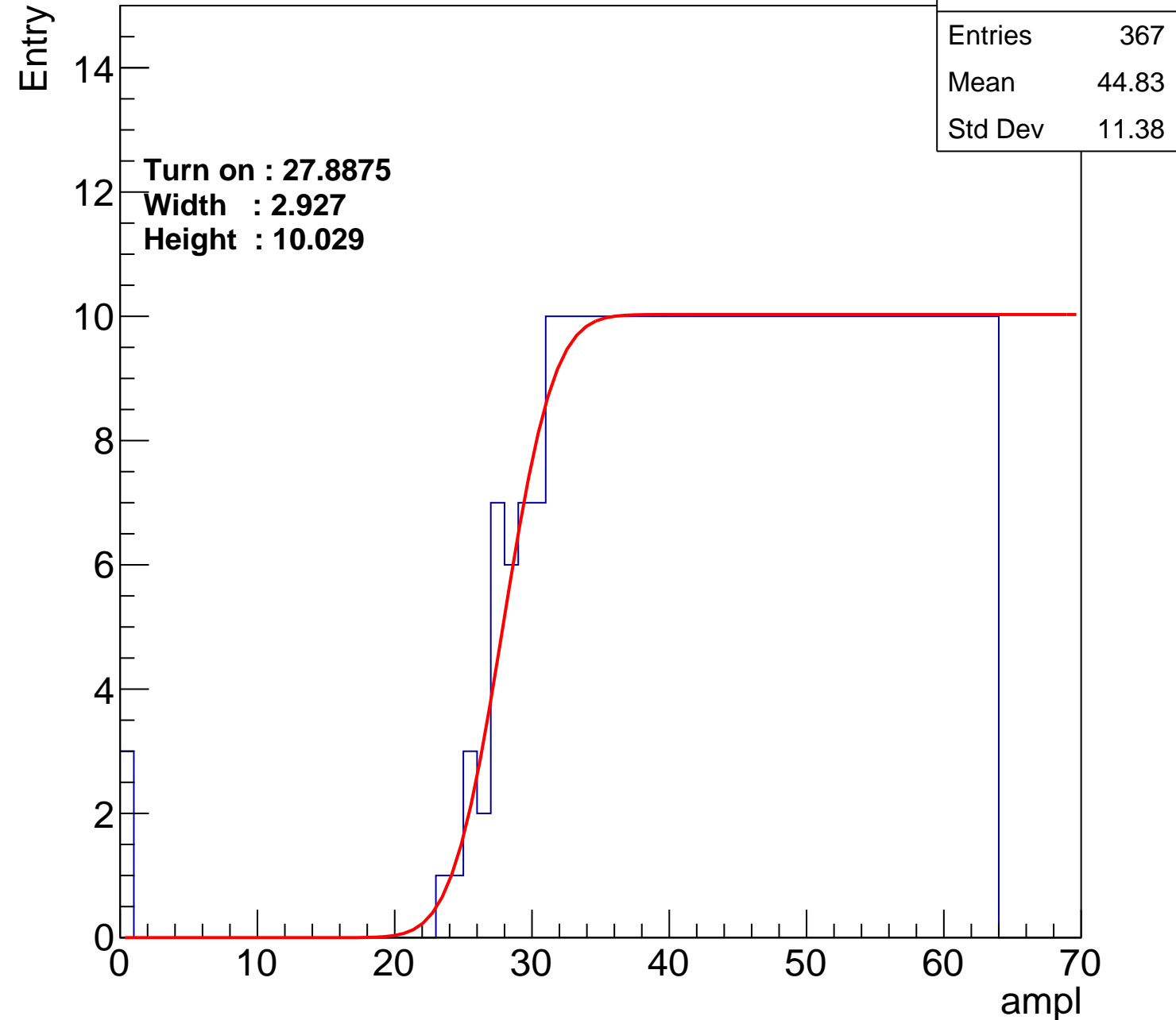
Width : 2.927

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.12
Std Dev	12.06

Turn on : 27.5381

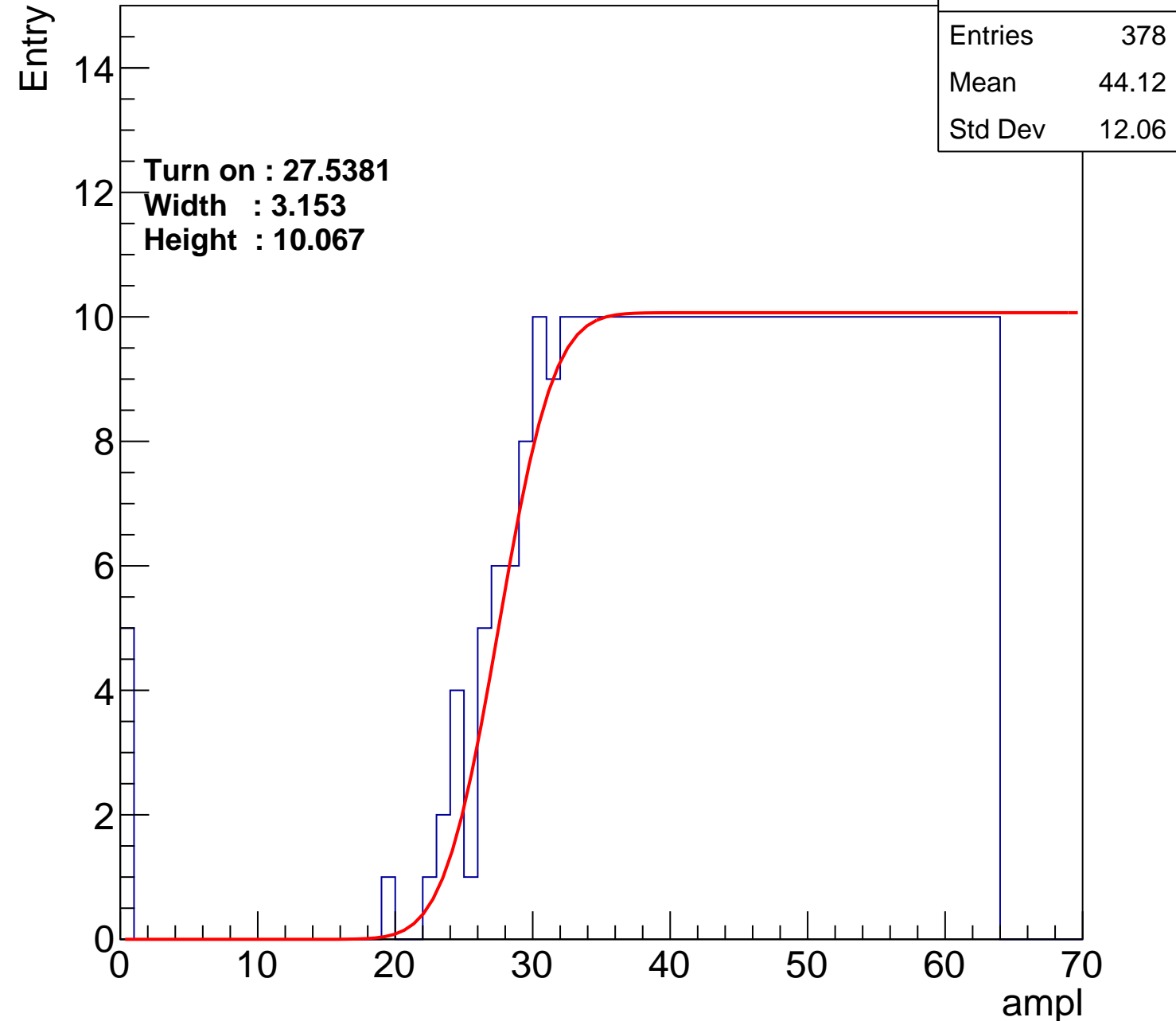
Width : 3.153

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.81
Std Dev	11.37

**Turn on : 27.5036**

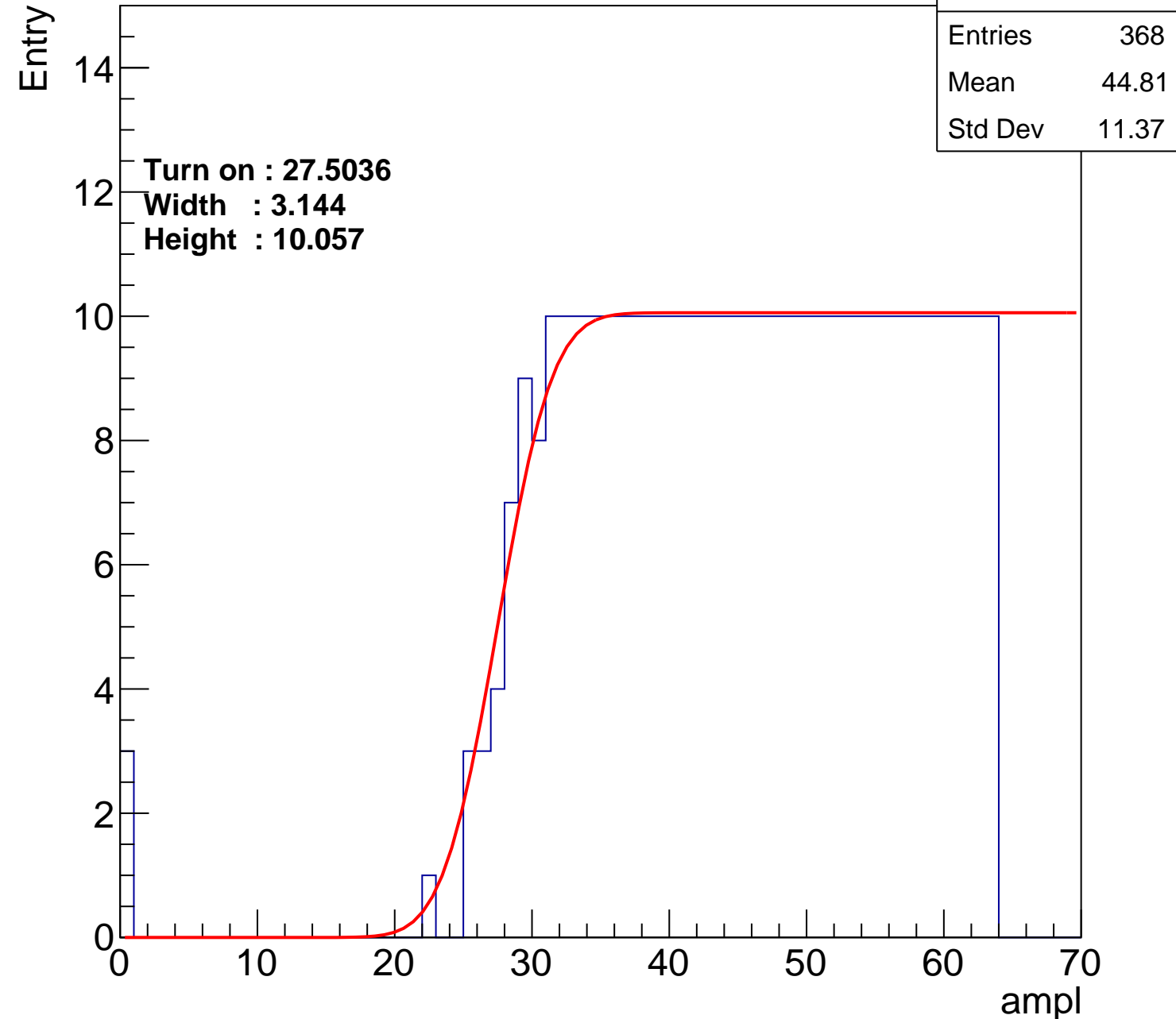
**Width : 3.144**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch4

calib\_packv5\_042523\_0143.root, FC#5, port B1

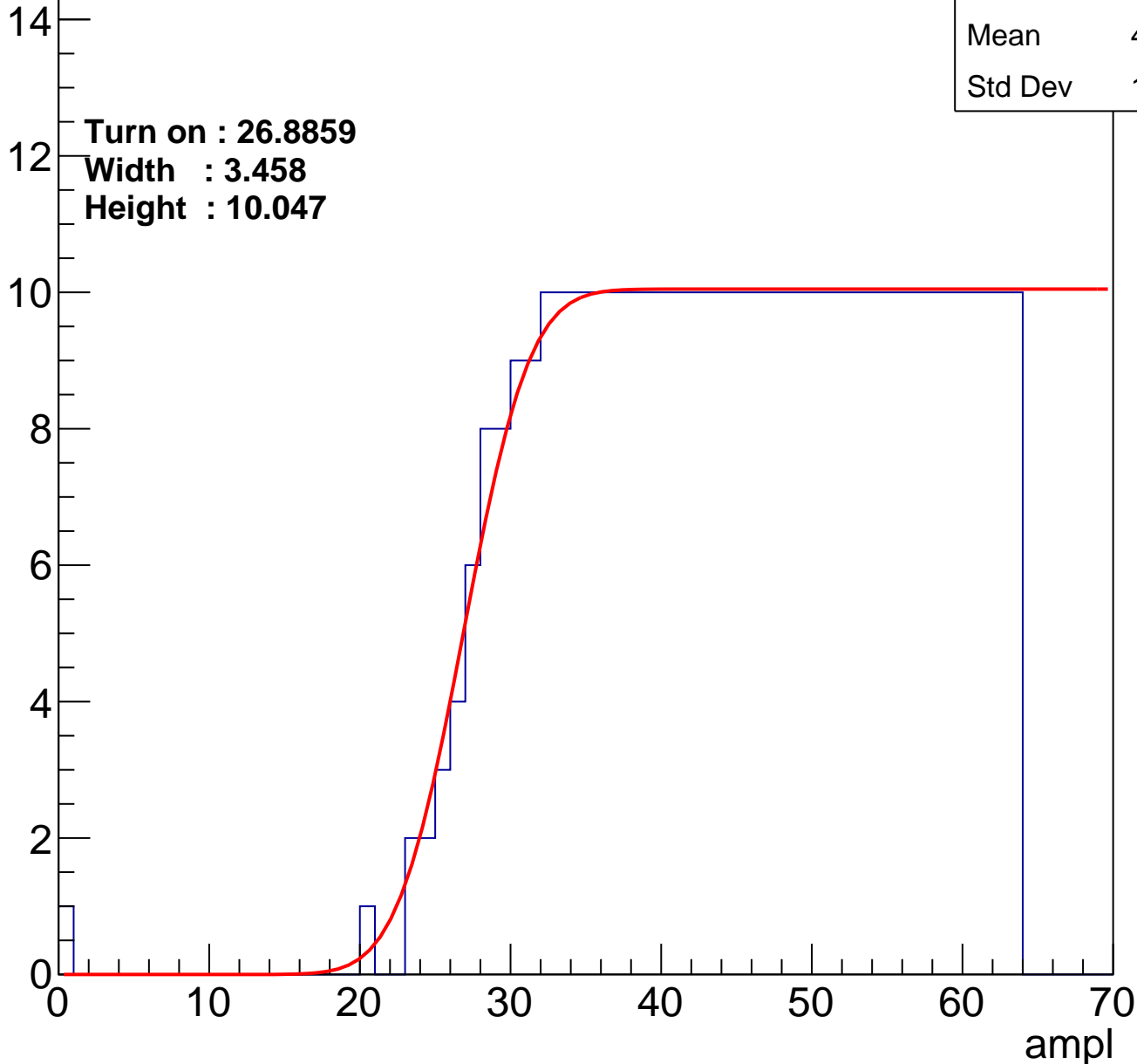
Entries	373
Mean	44.66
Std Dev	11.16

Turn on : 26.8859

Width : 3.458

Height : 10.047

Entry



# B0L000S, U26-ch5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	351
Mean	45.84
Std Dev	10.69

Turn on : 30.9527

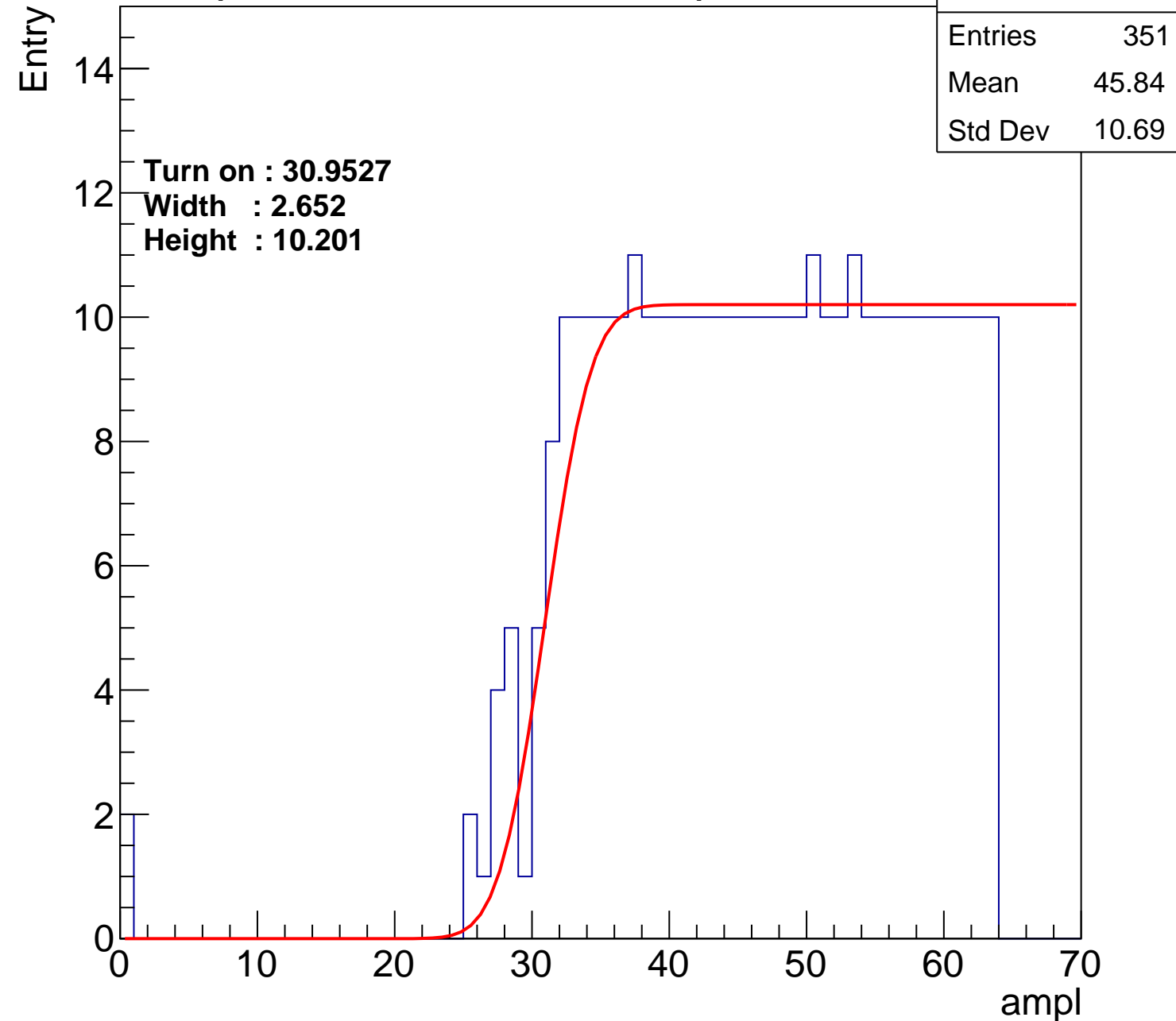
Width : 2.652

Height : 10.201

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	351
Mean	45.62
Std Dev	10.99

Turn on : 29.7008

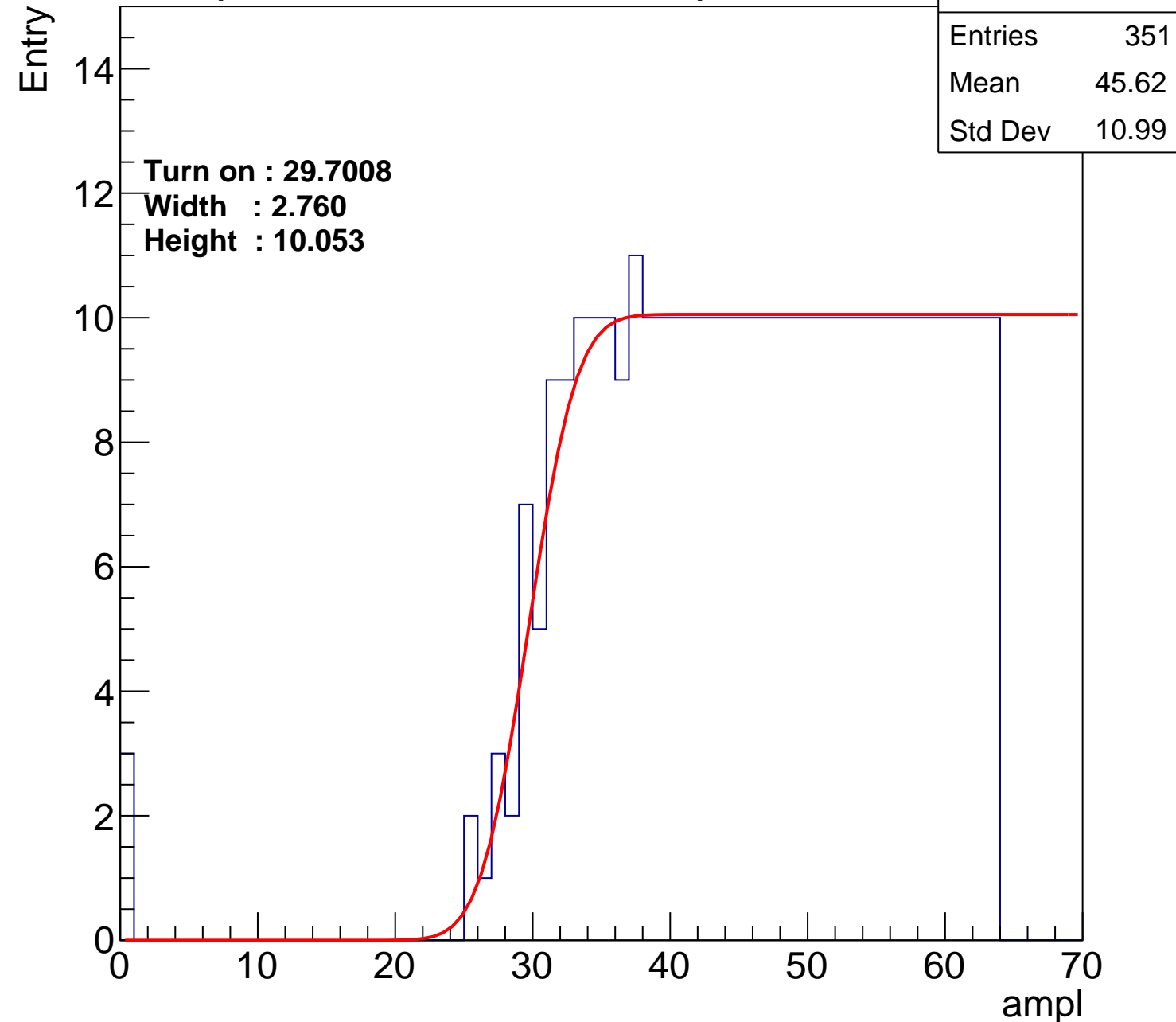
Width : 2.760

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.27
Std Dev	11.02

**Turn on : 28.2703**

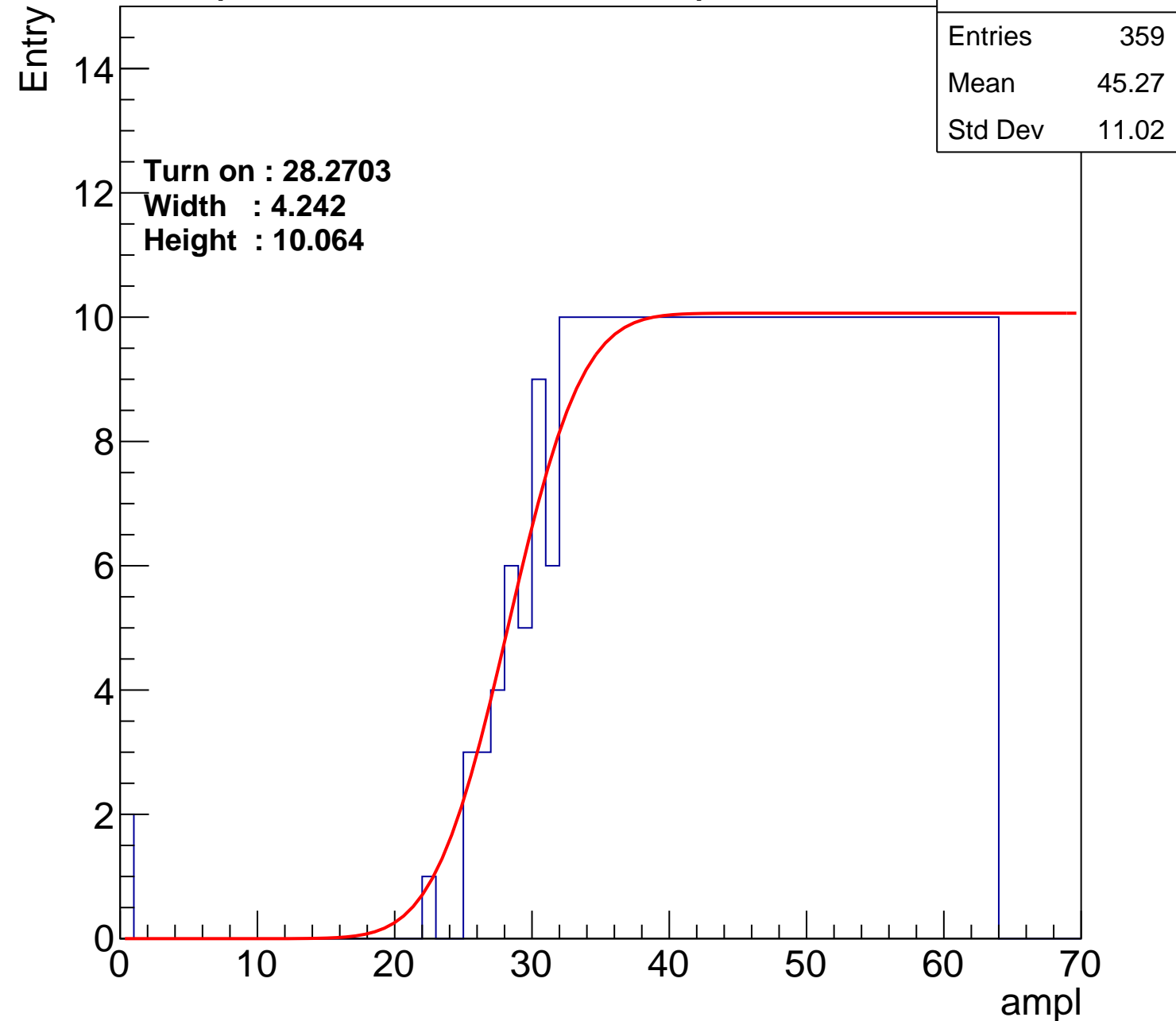
**Width : 4.242**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch8

calib\_packv5\_042523\_0143.root, FC#5, port B1

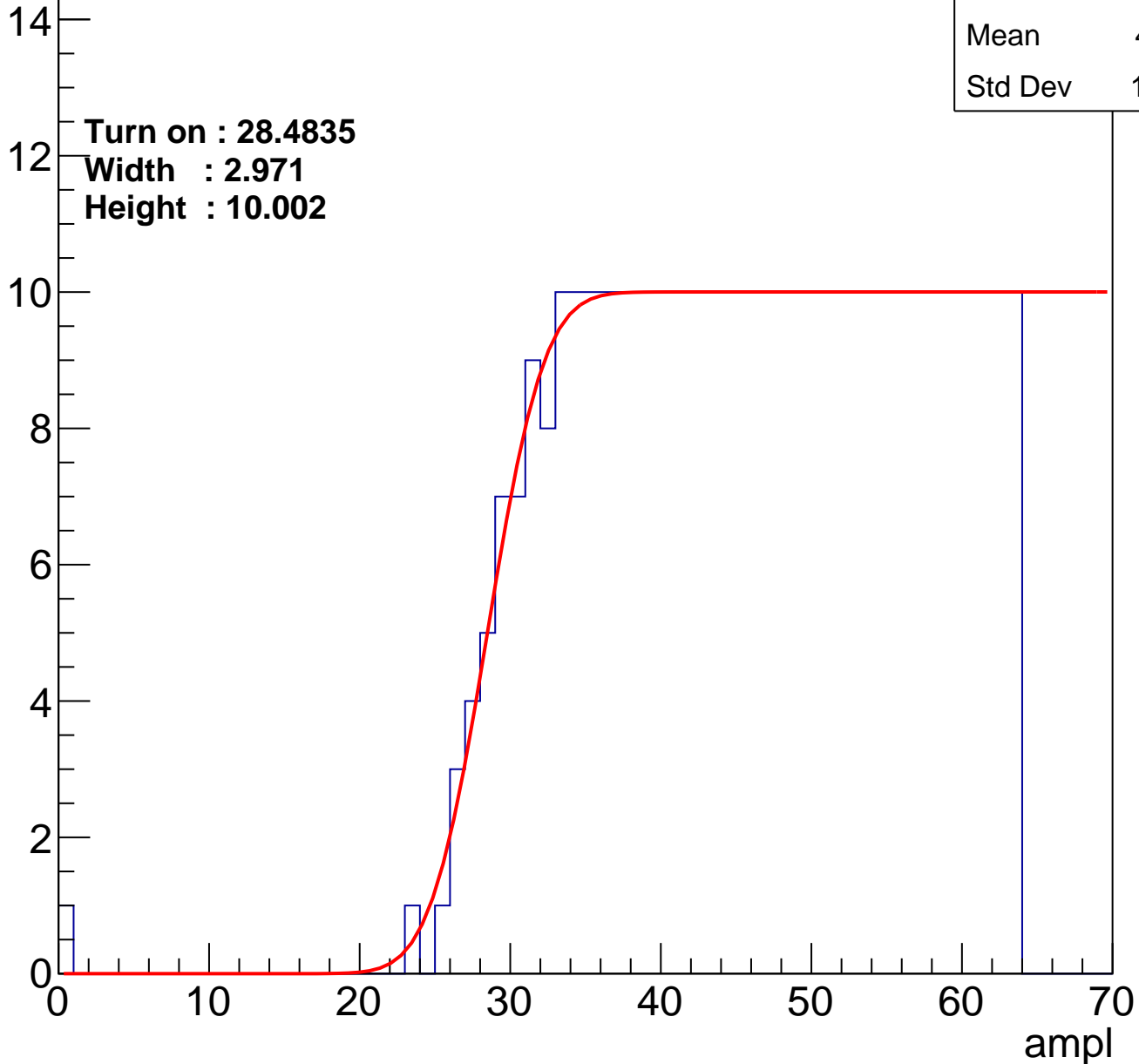
Entries	356
Mean	45.51
Std Dev	10.69

**Turn on : 28.4835**

**Width : 2.971**

**Height : 10.002**

Entry



# B0L000S, U26-ch9

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.3
Std Dev	11.77

**Turn on : 26.5977**

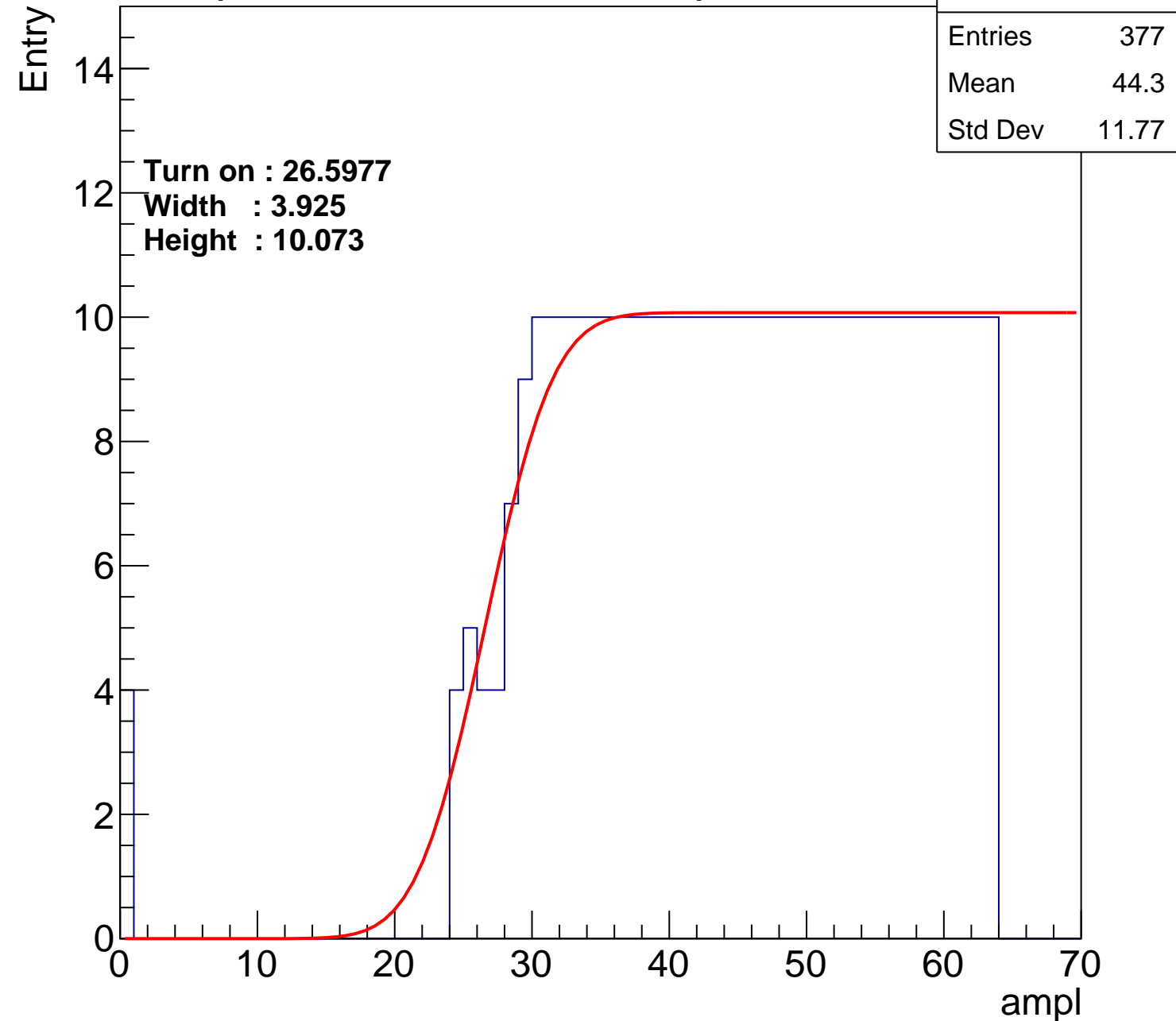
**Width : 3.925**

**Height : 10.073**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch10

calib\_packv5\_042523\_0143.root, FC#5, port B1

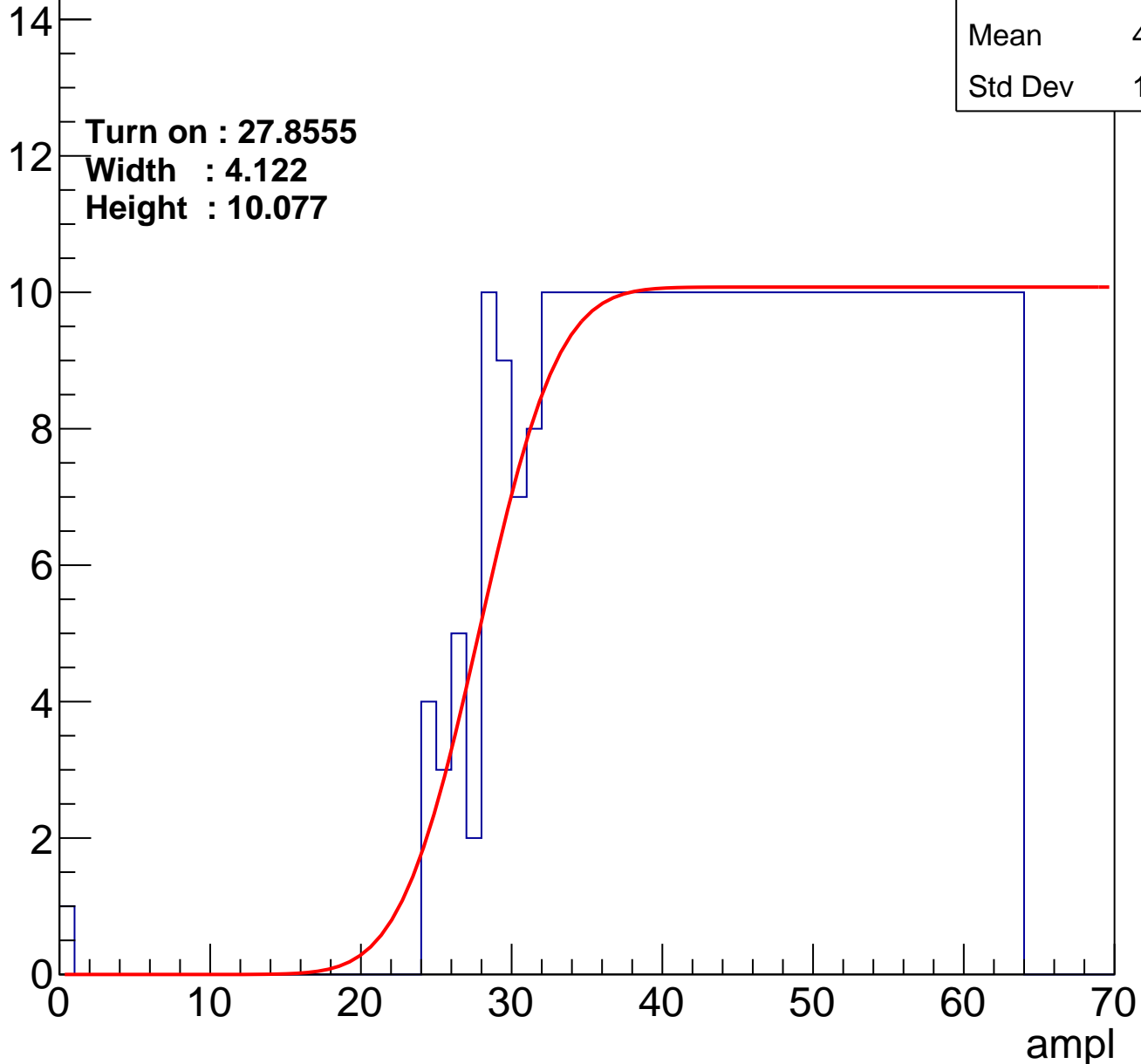
Entries	369
Mean	44.86
Std Dev	11.05

Turn on : 27.8555

Width : 4.122

Height : 10.077

Entry



# B0L000S, U26-ch11

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	393
Mean	43.64
Std Dev	11.84

Turn on : 25.1703

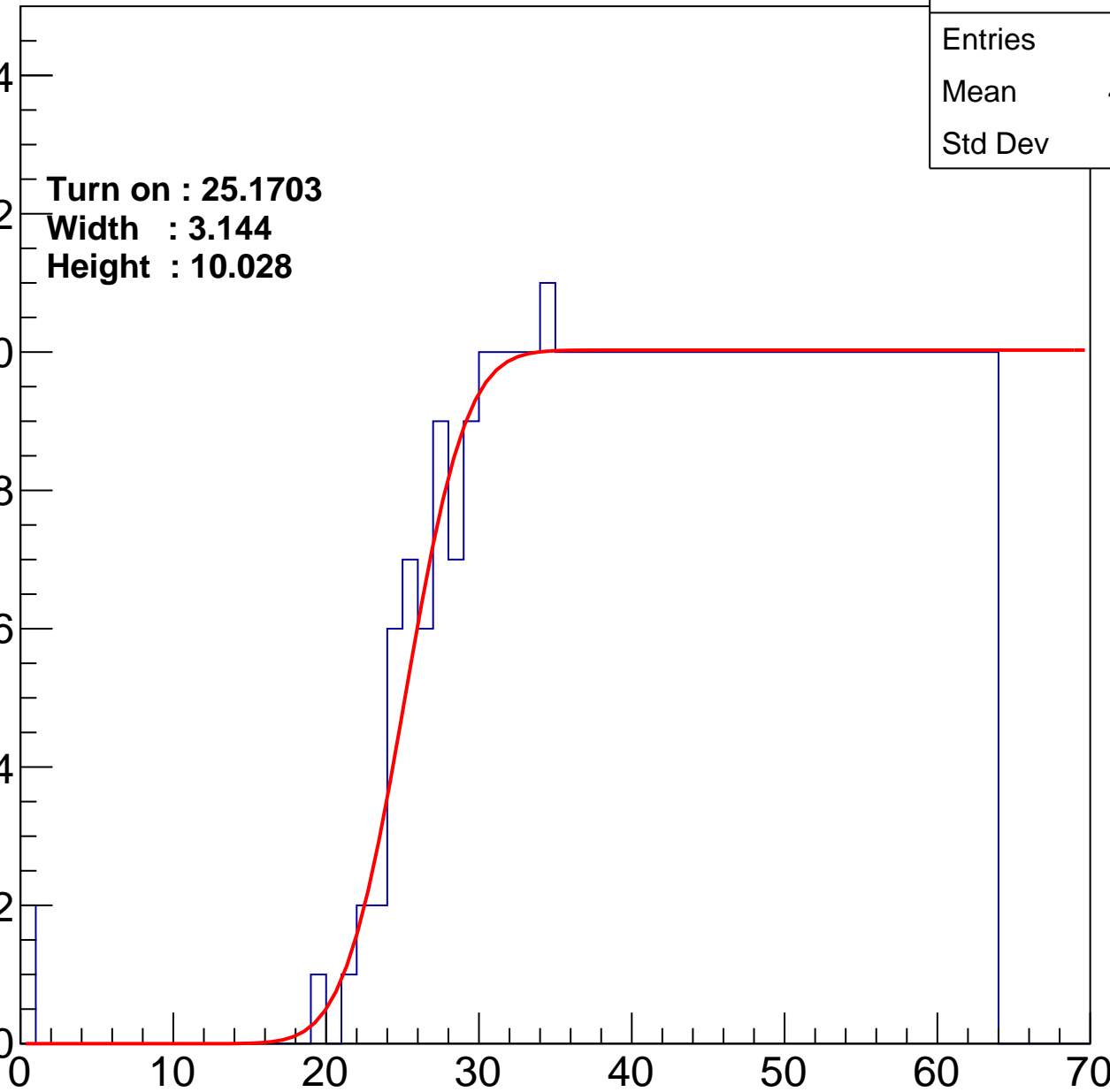
Width : 3.144

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch12

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	361
Mean	45.29
Std Dev	10.77

Turn on : 27.9562

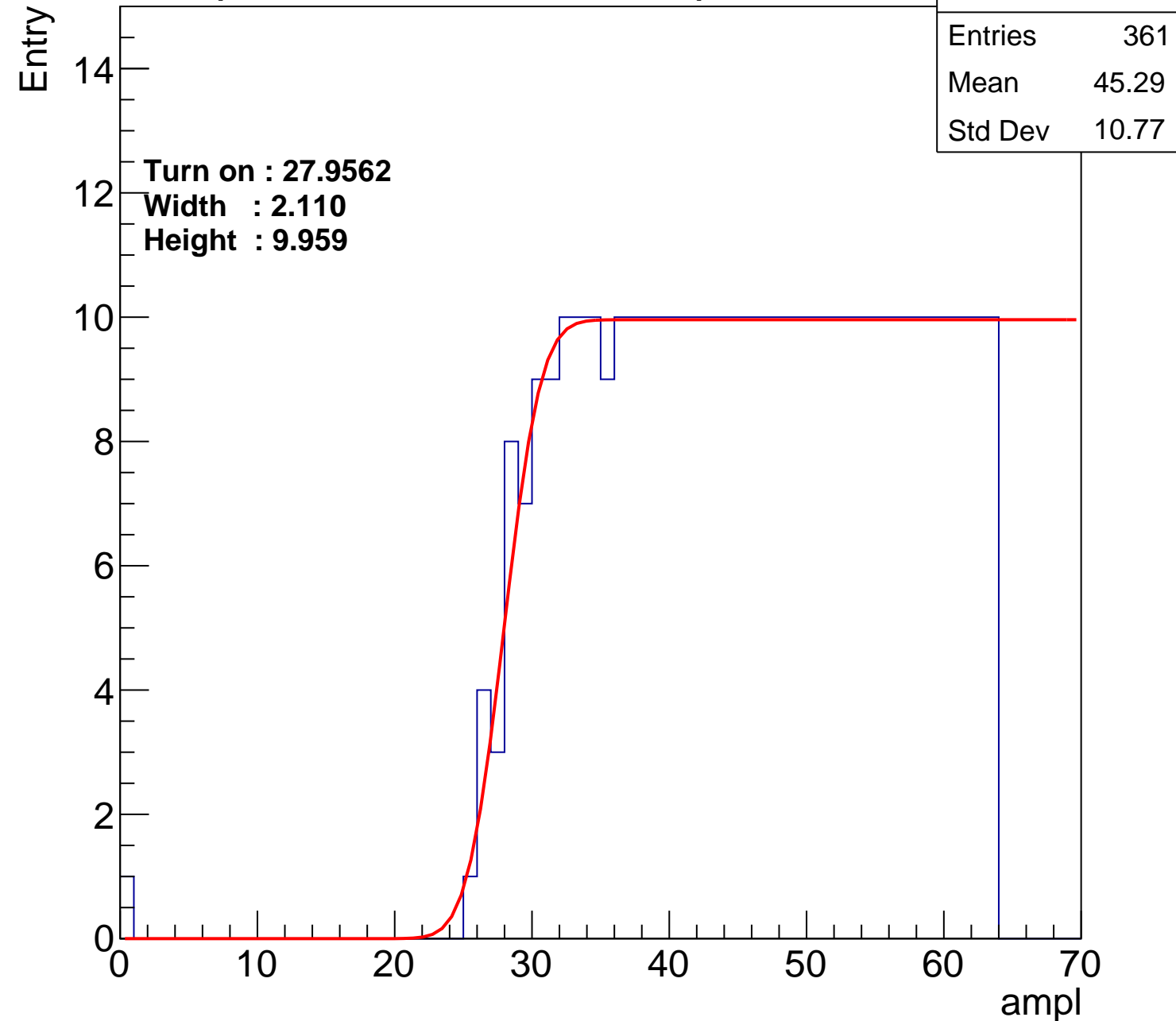
Width : 2.110

Height : 9.959

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch13

calib\_packv5\_042523\_0143.root, FC#5, port B1

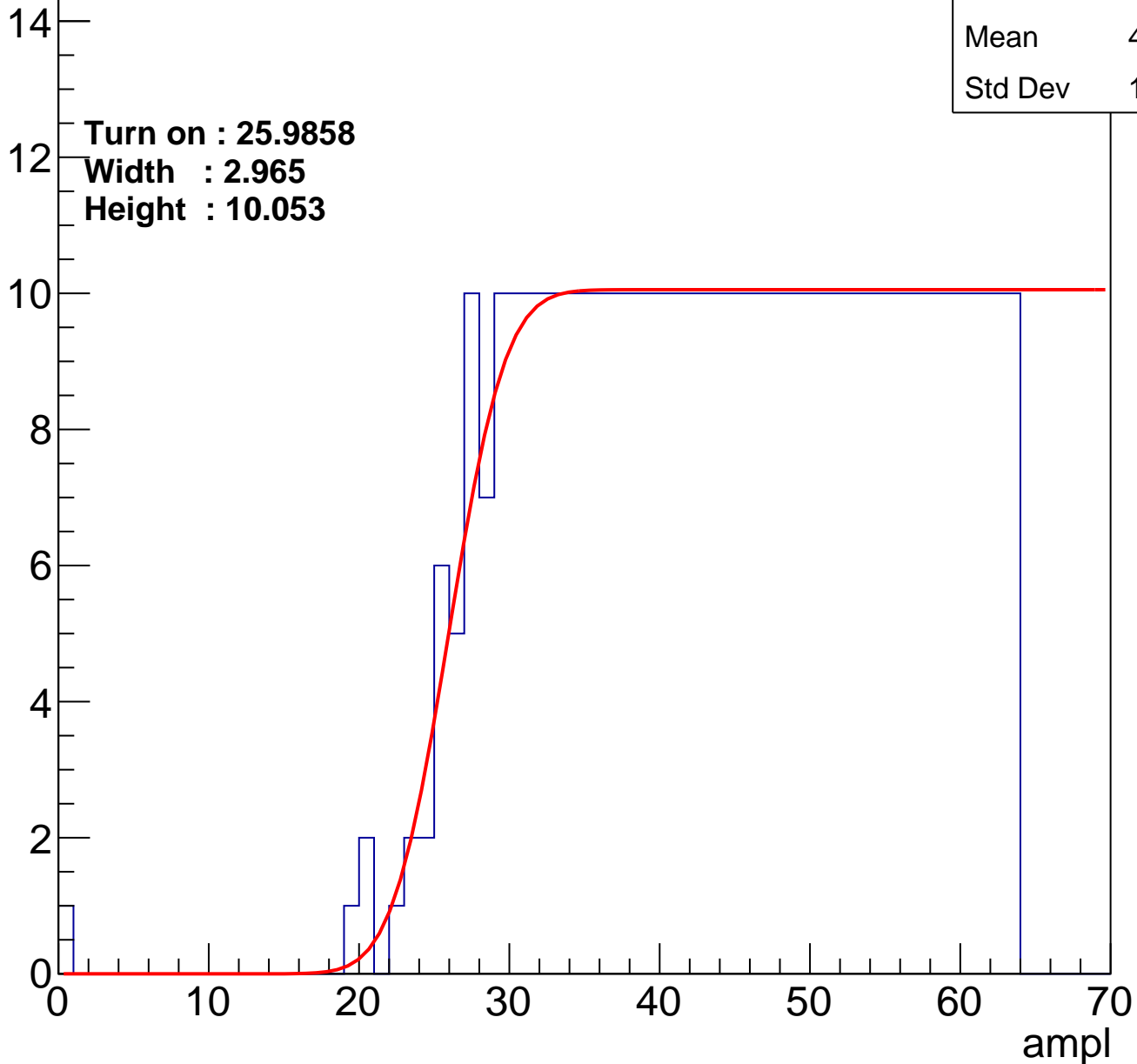
Entries	387
Mean	43.98
Std Dev	11.53

Turn on : 25.9858

Width : 2.965

Height : 10.053

Entry



# B0L000S, U26-ch14

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.78
Std Dev	11.04

Turn on : 27.1478

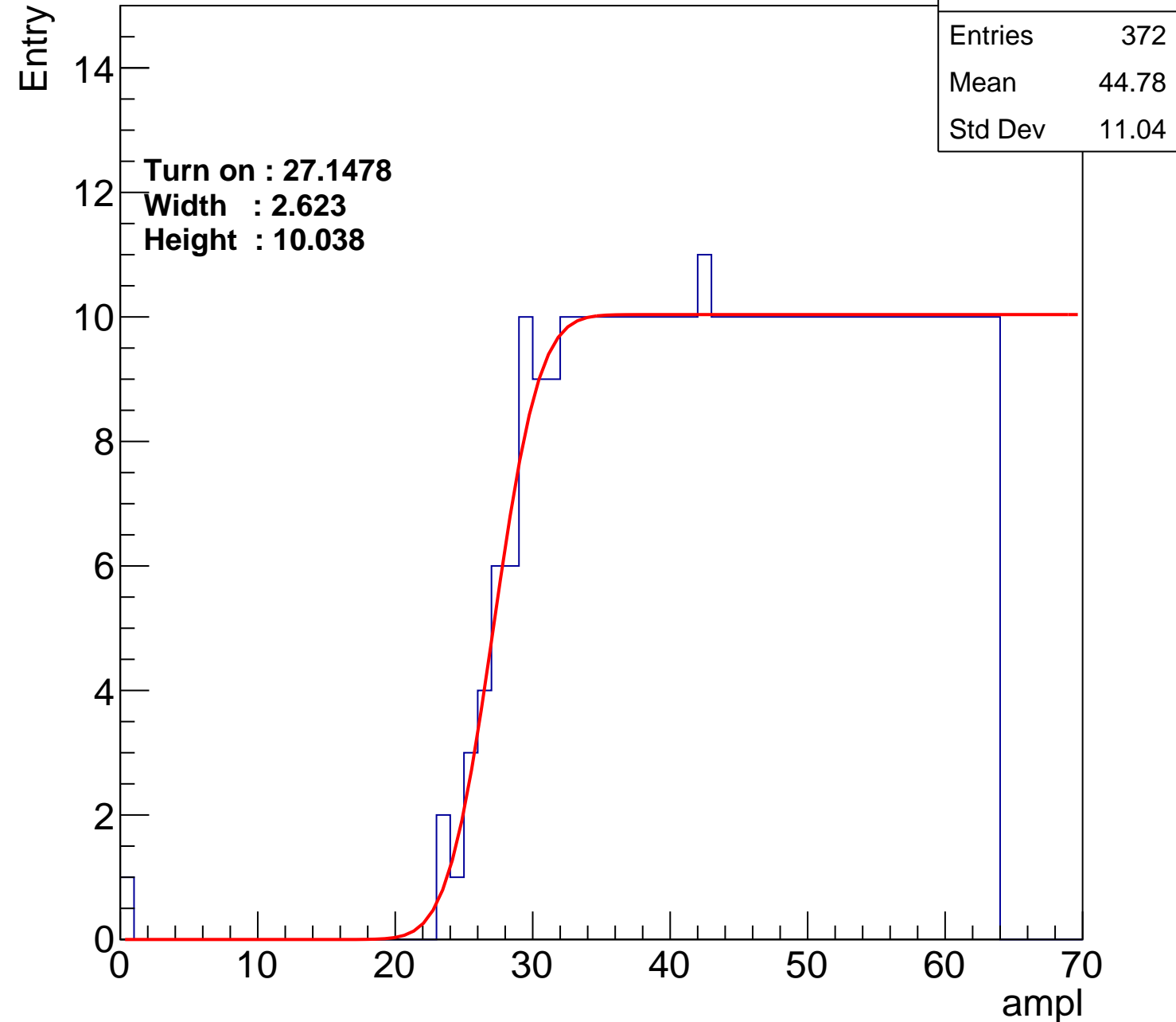
Width : 2.623

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch15

calib\_packv5\_042523\_0143.root, FC#5, port B1

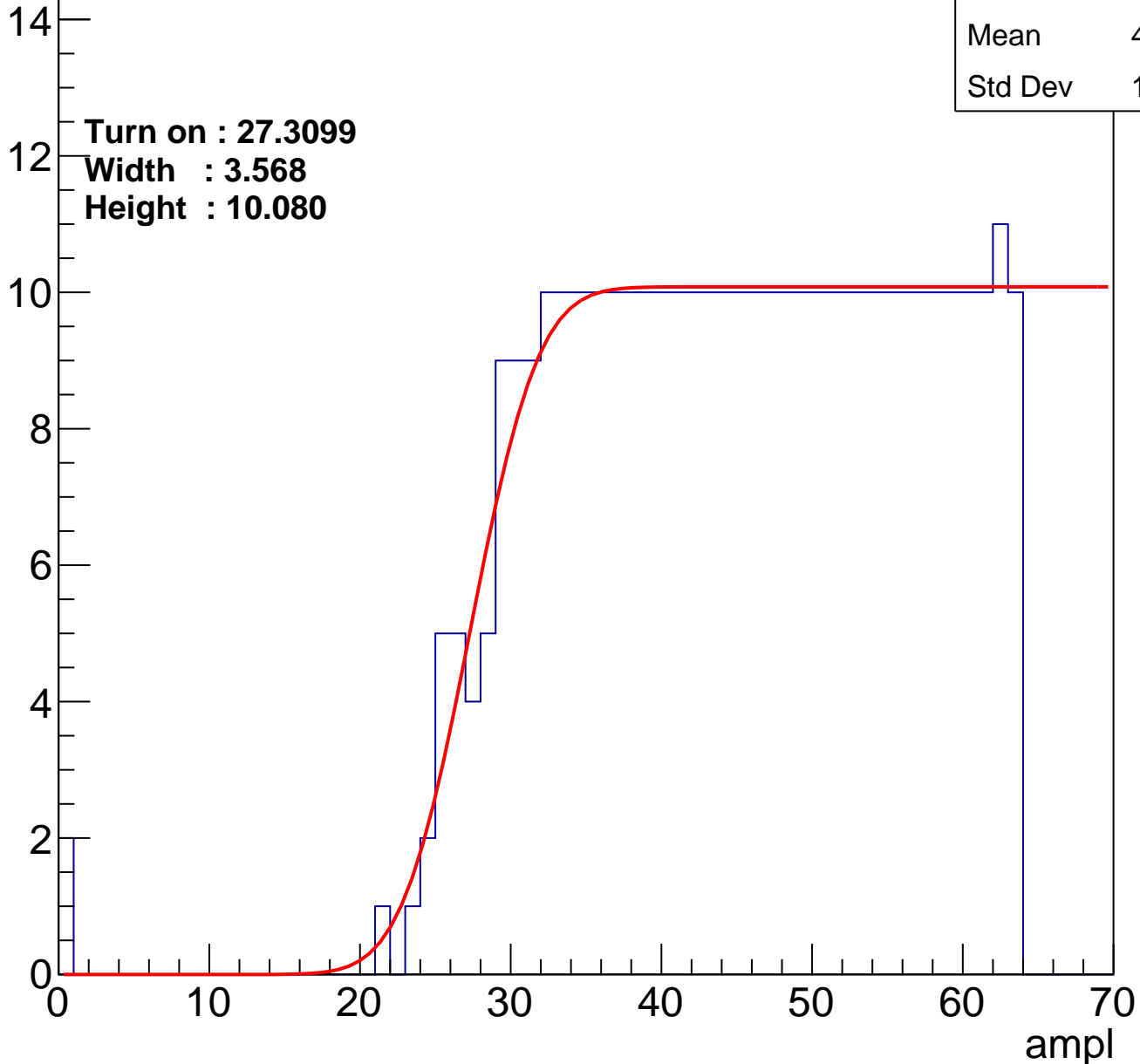
Entries	373
Mean	44.68
Std Dev	11.36

Turn on : 27.3099

Width : 3.568

Height : 10.080

Entry



# B0L000S, U26-ch16

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	45
Std Dev	10.94

Turn on : 27.6501

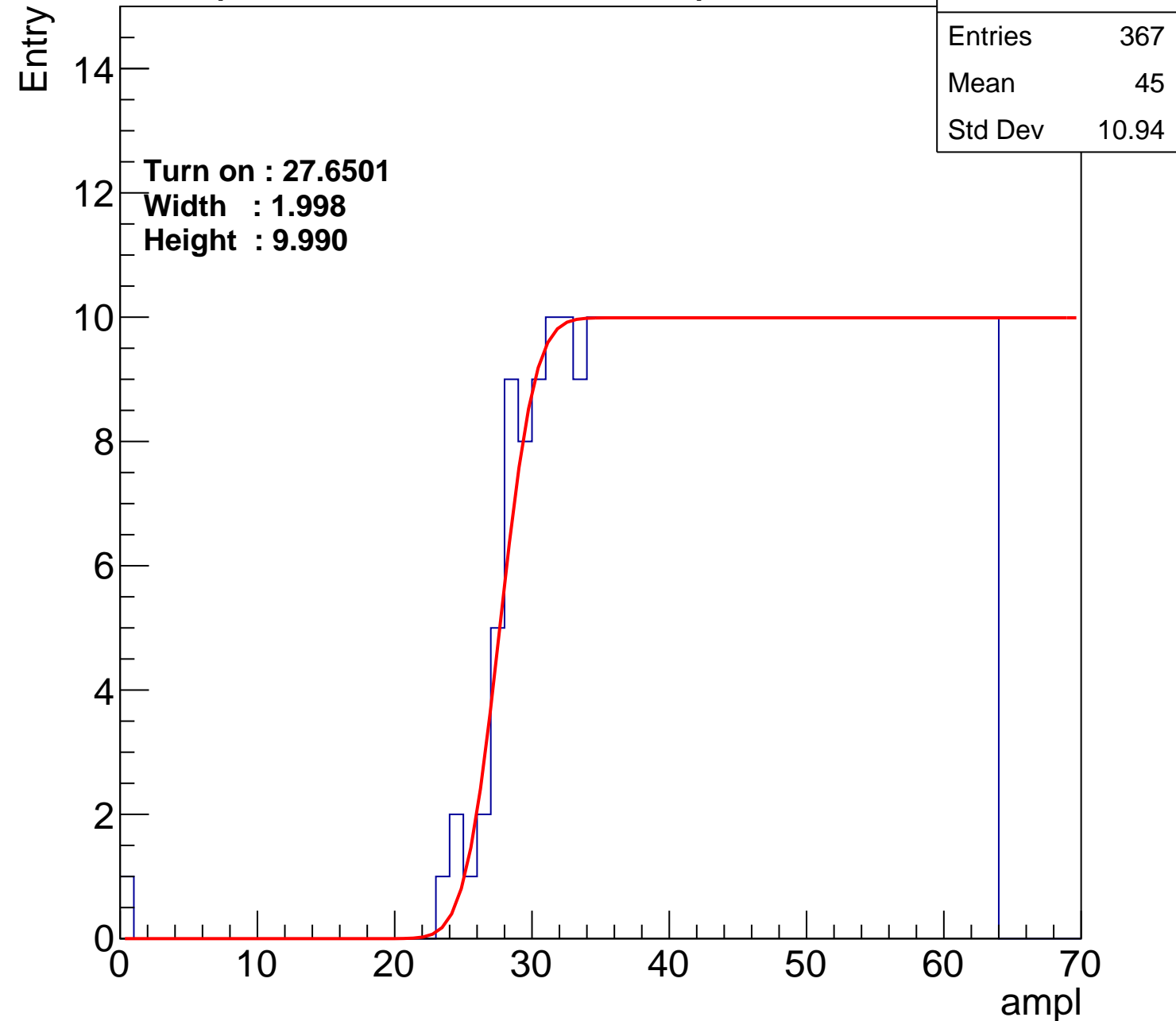
Width : 1.998

Height : 9.990

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch17

calib\_packv5\_042523\_0143.root, FC#5, port B1

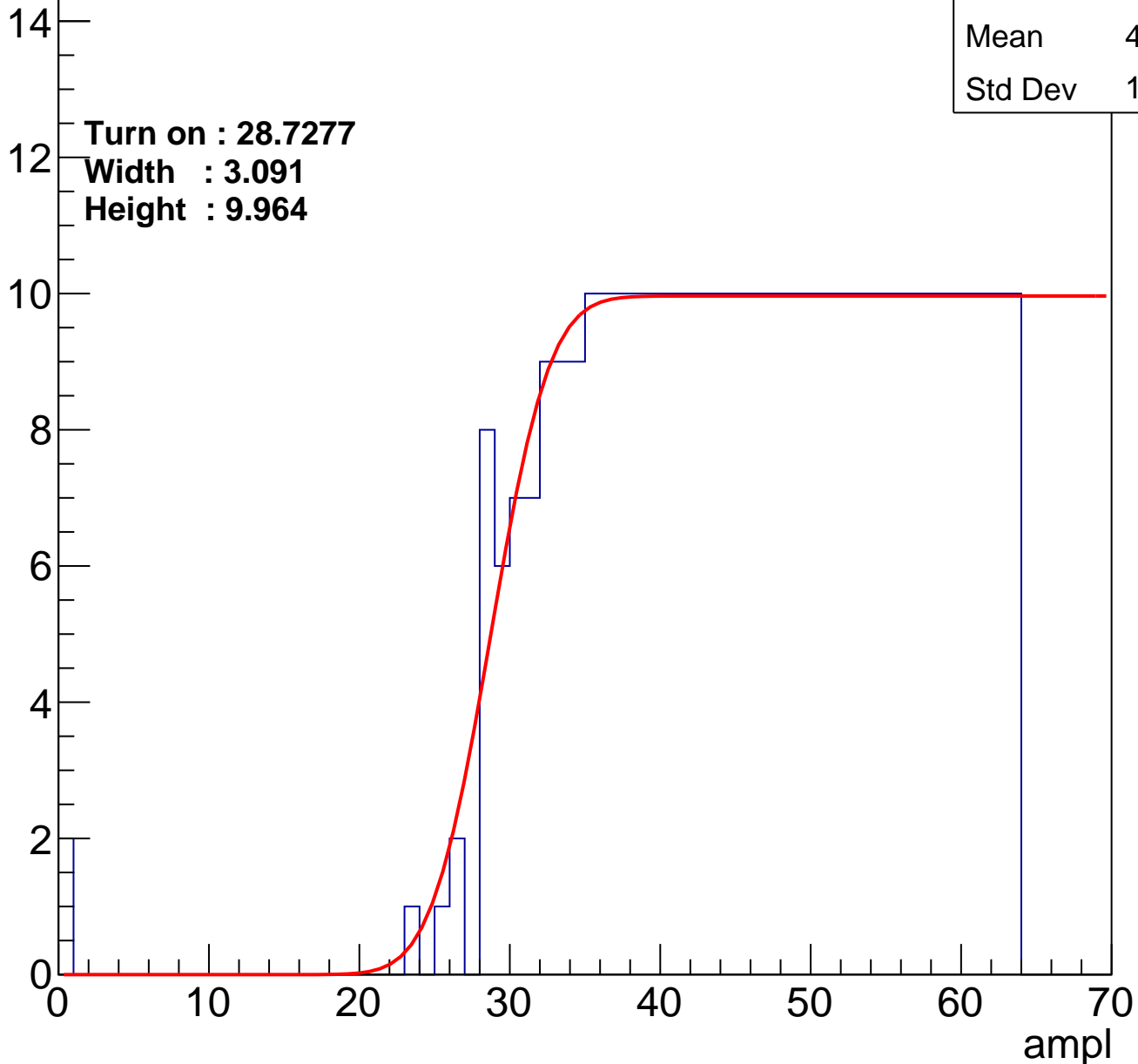
Entries	351
Mean	45.66
Std Dev	10.83

**Turn on : 28.7277**

**Width : 3.091**

**Height : 9.964**

Entry



# B0L000S, U26-ch18

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.65
Std Dev	11.27

Turn on : 26.8462

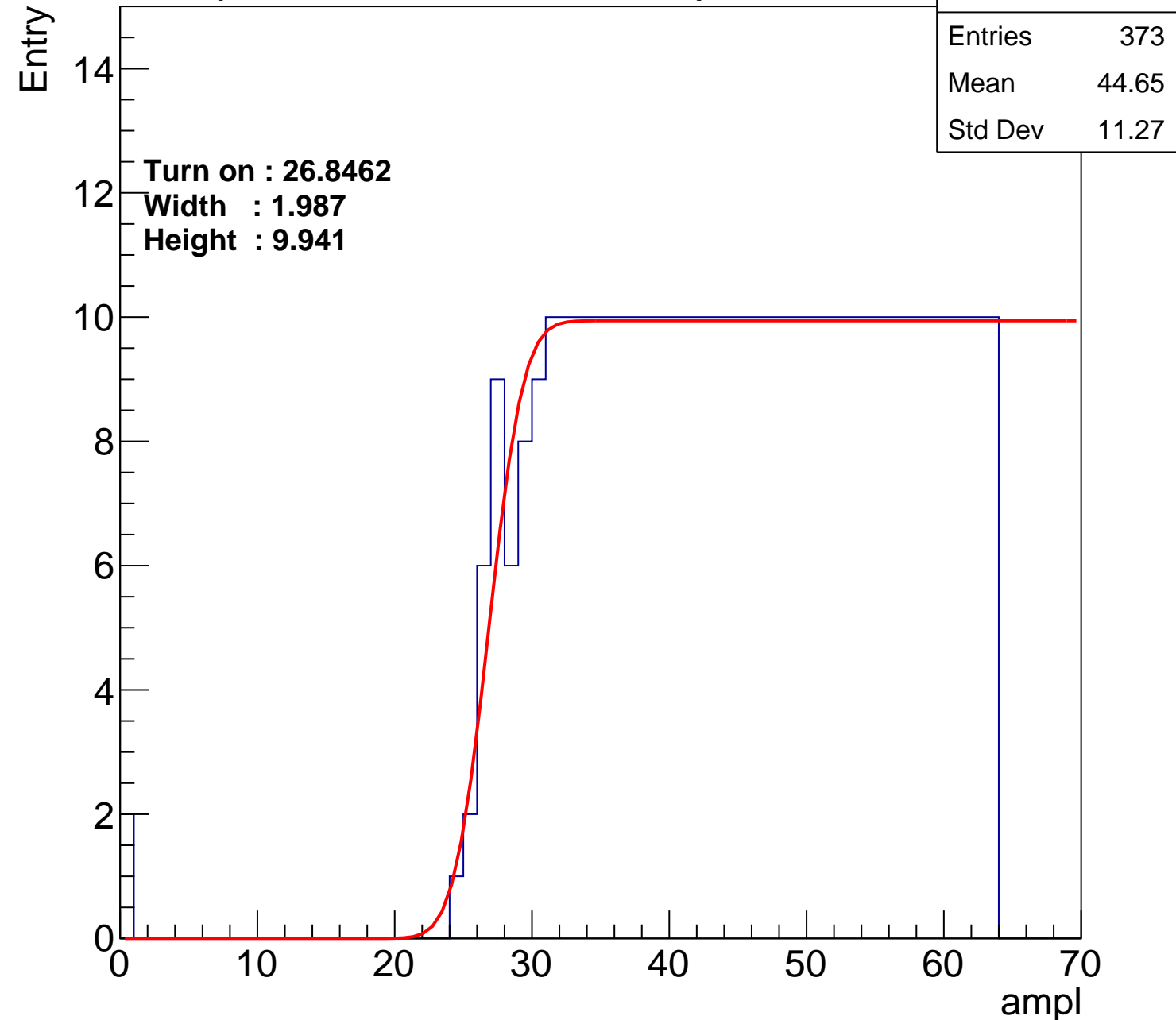
Width : 1.987

Height : 9.941

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch19

calib\_packv5\_042523\_0143.root, FC#5, port B1

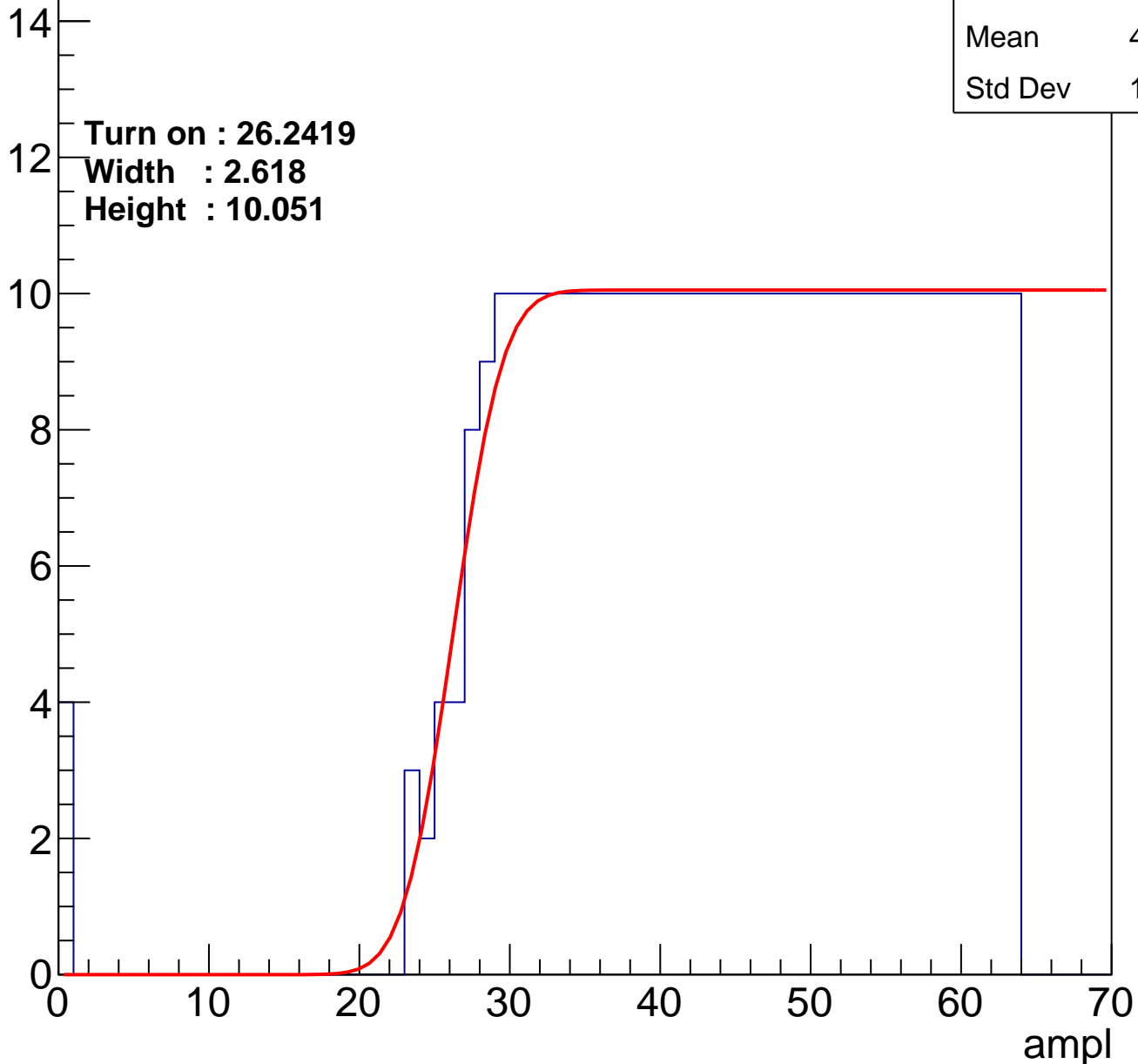
Entries	384
Mean	43.98
Std Dev	11.89

Turn on : 26.2419

Width : 2.618

Height : 10.051

Entry



# B0L000S, U26-ch20

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	44.93
Std Dev	11.1

Turn on : 27.8343

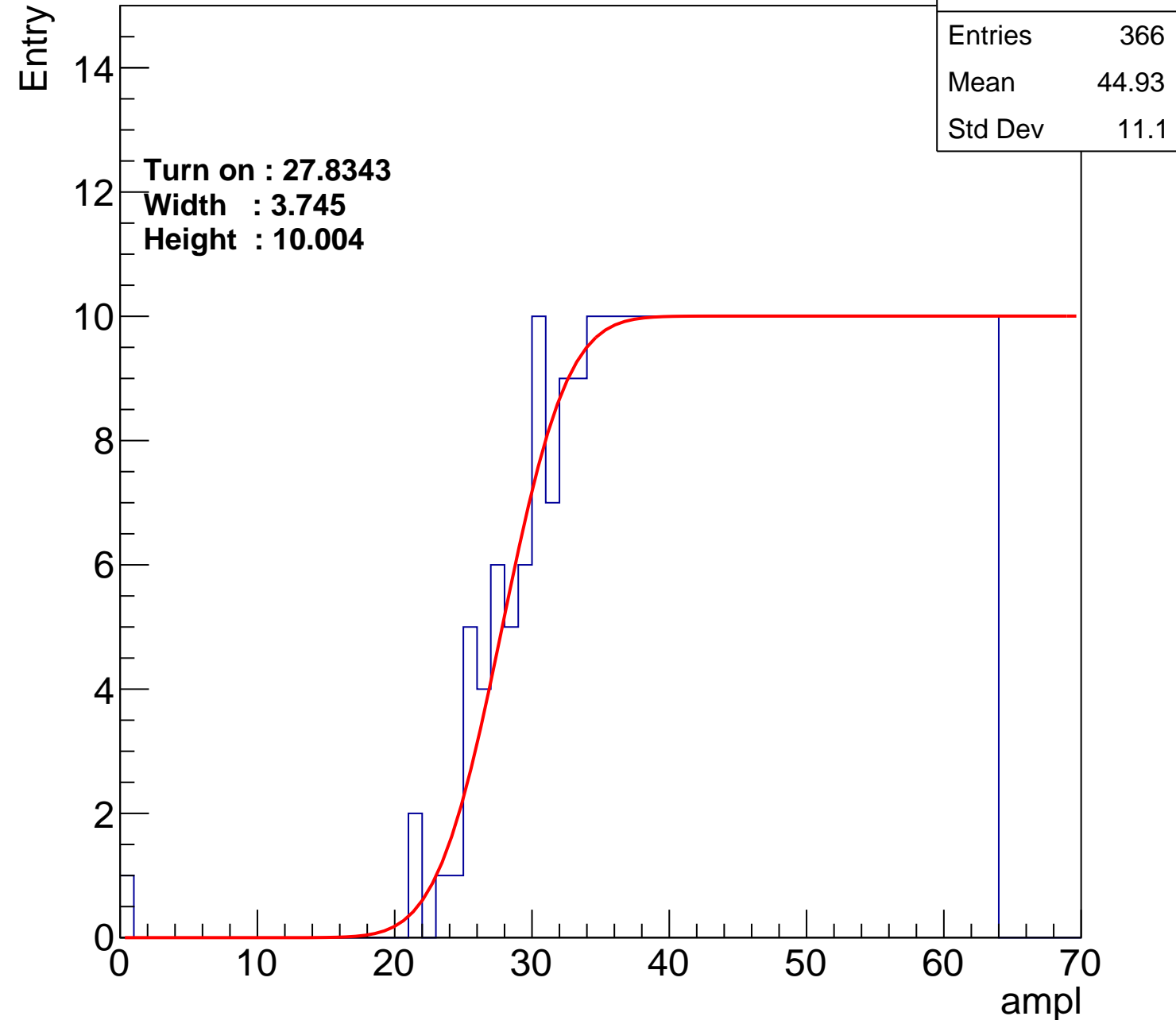
Width : 3.745

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch21

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.9414

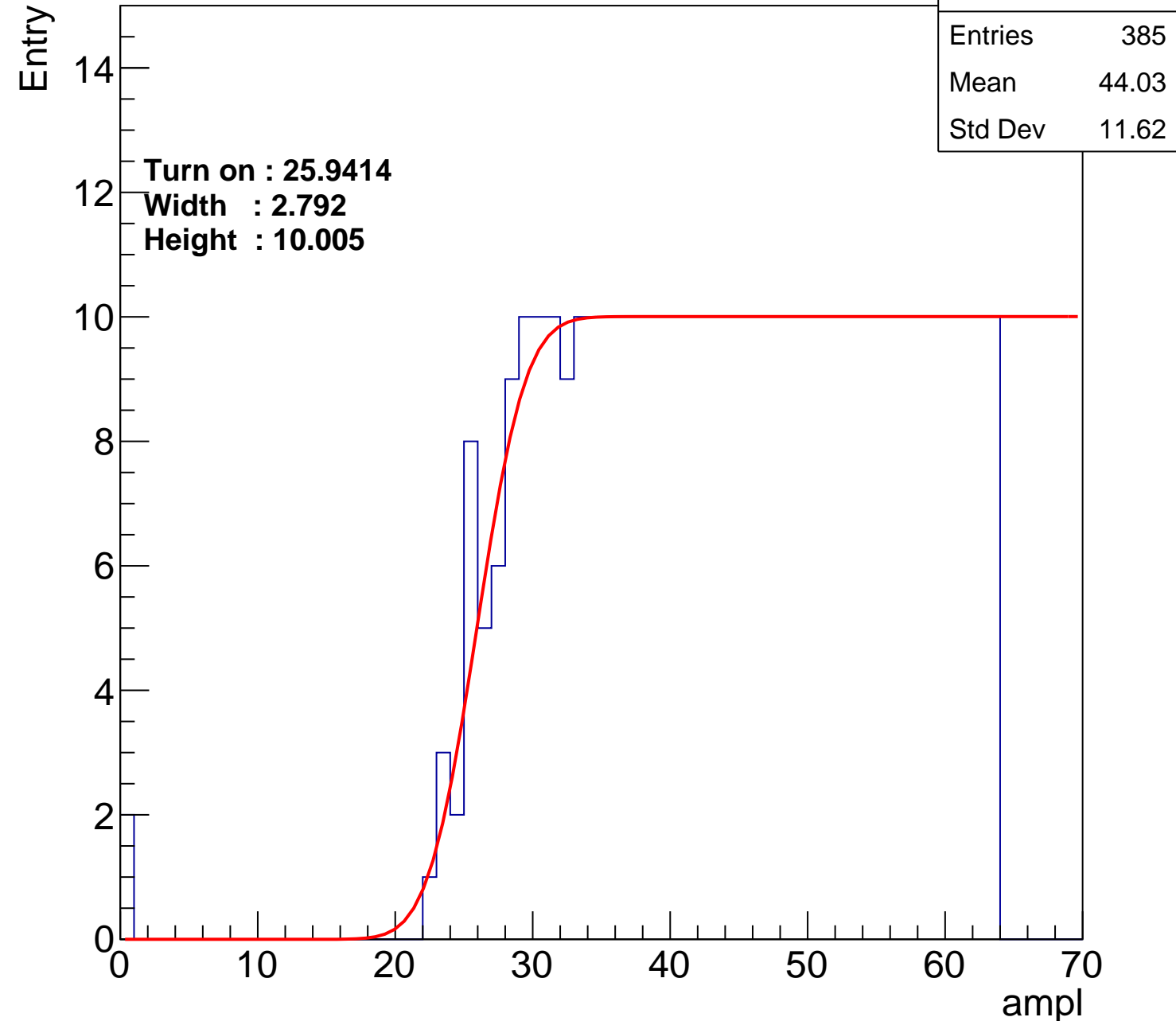
Width : 2.792

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch22

calib\_packv5\_042523\_0143.root, FC#5, port B1

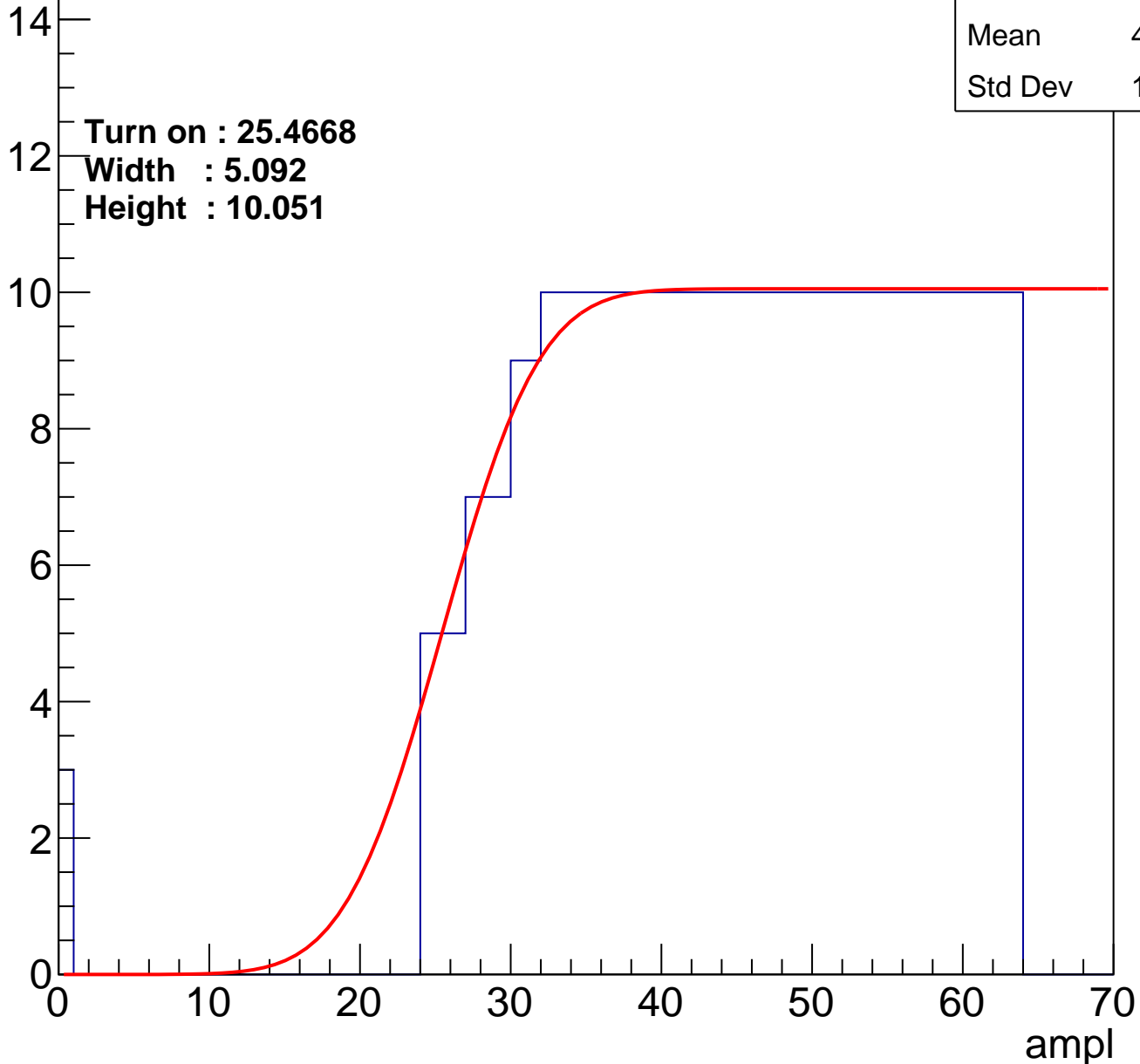
Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 25.4668

Width : 5.092

Height : 10.051

Entry





# B0L000S, U26-ch23

calib\_packv5\_042523\_0143.root, FC#5, port B1

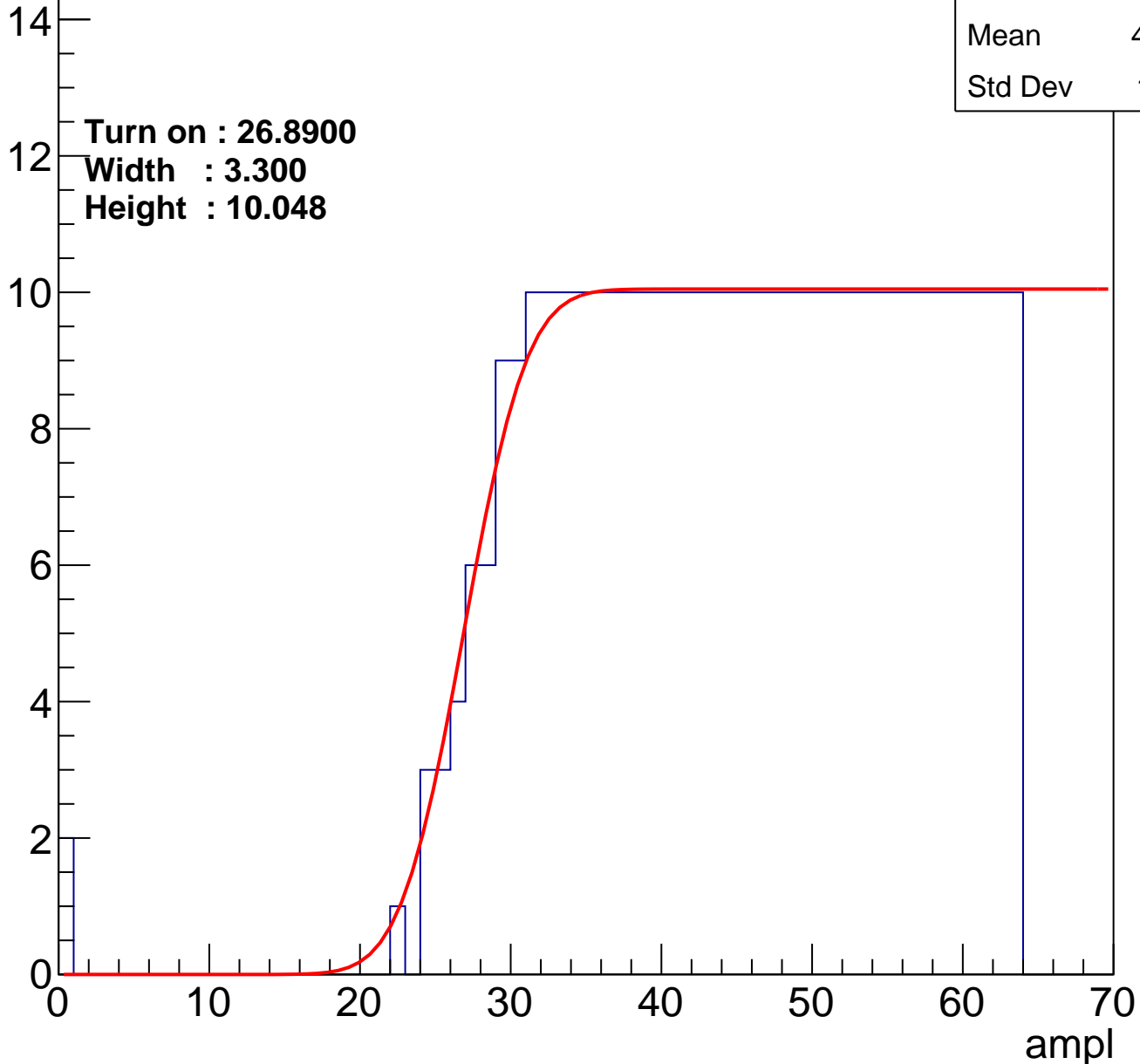
Entries	373
Mean	44.62
Std Dev	11.31

**Turn on : 26.8900**

**Width : 3.300**

**Height : 10.048**

Entry



# B0L000S, U26-ch24

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 25.8149

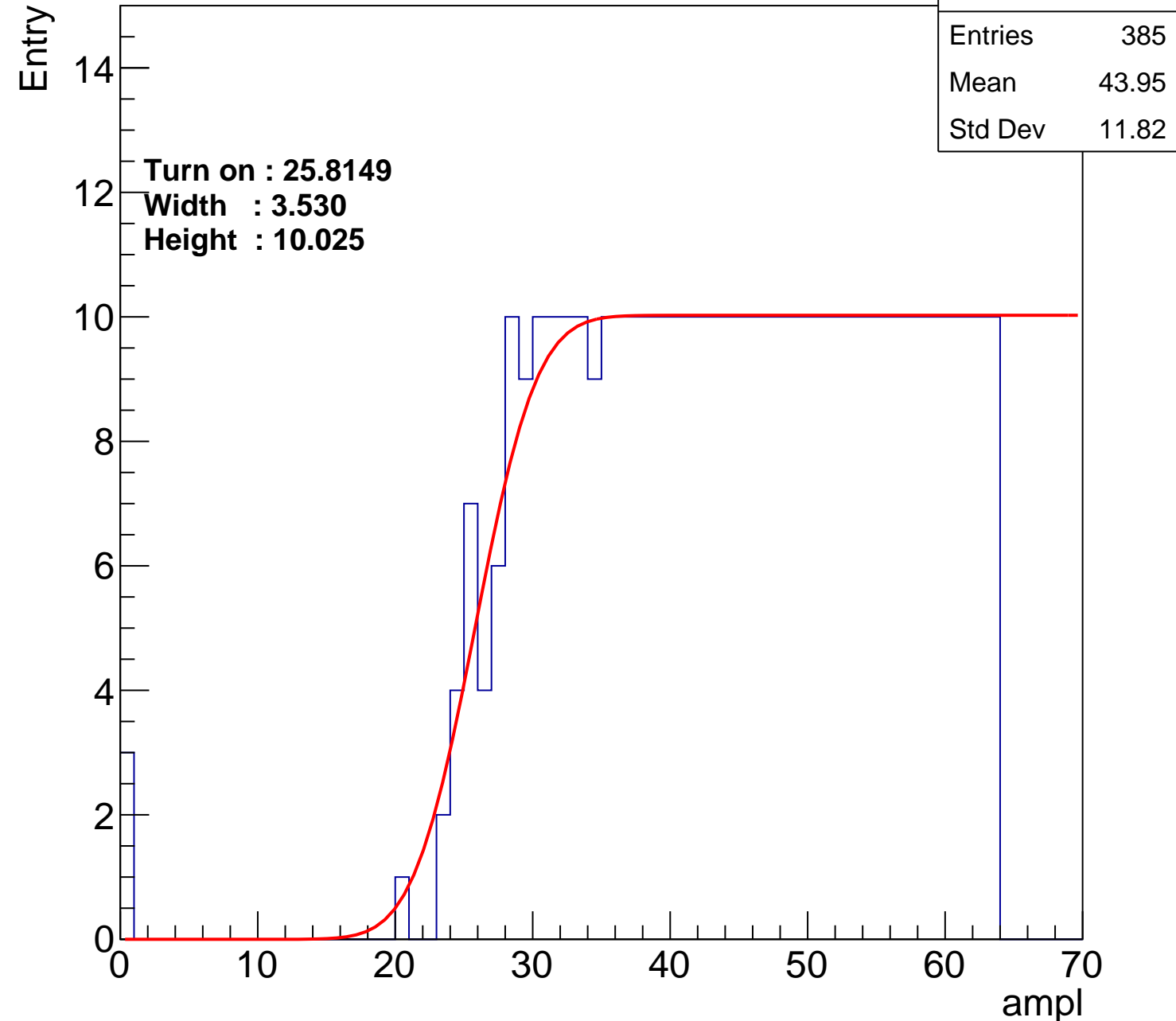
Width : 3.530

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch25

calib\_packv5\_042523\_0143.root, FC#5, port B1

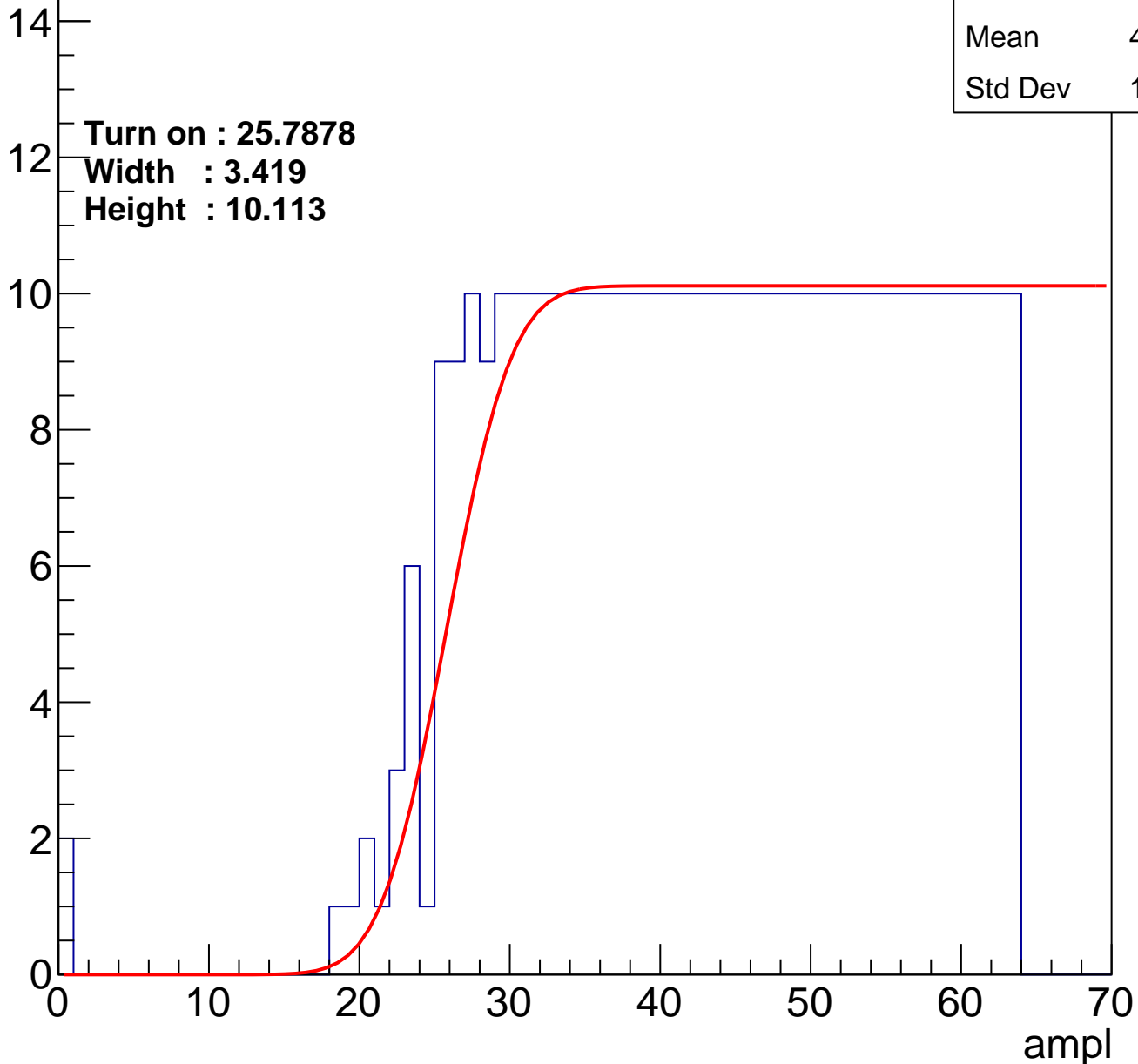
Entries	404
Mean	43.09
Std Dev	12.13

**Turn on : 25.7878**

**Width : 3.419**

**Height : 10.113**

Entry



# B0L000S, U26-ch26

calib\_packv5\_042523\_0143.root, FC#5, port B1

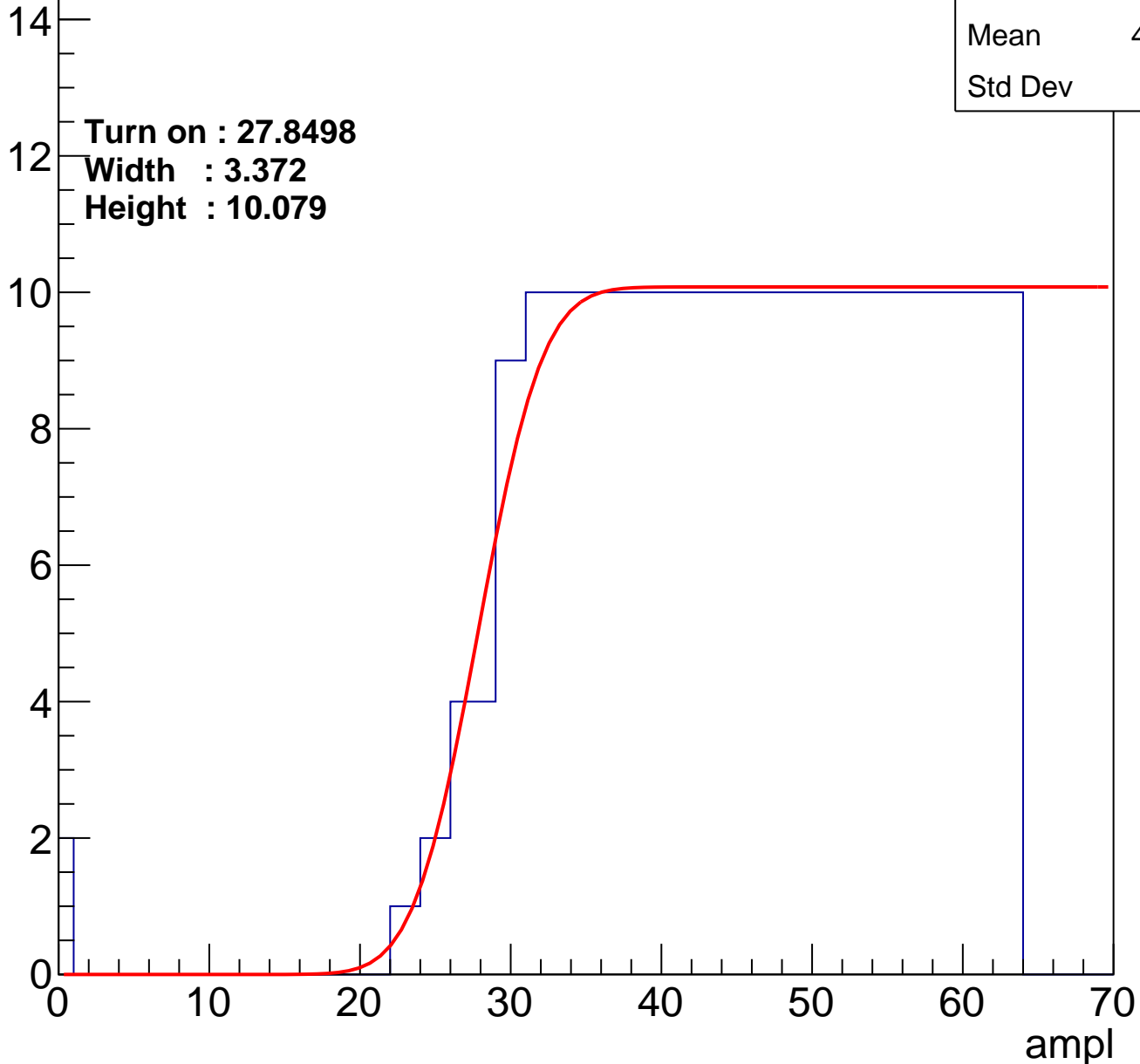
Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 27.8498

Width : 3.372

Height : 10.079

Entry



# B0L000S, U26-ch27

calib\_packv5\_042523\_0143.root, FC#5, port B1

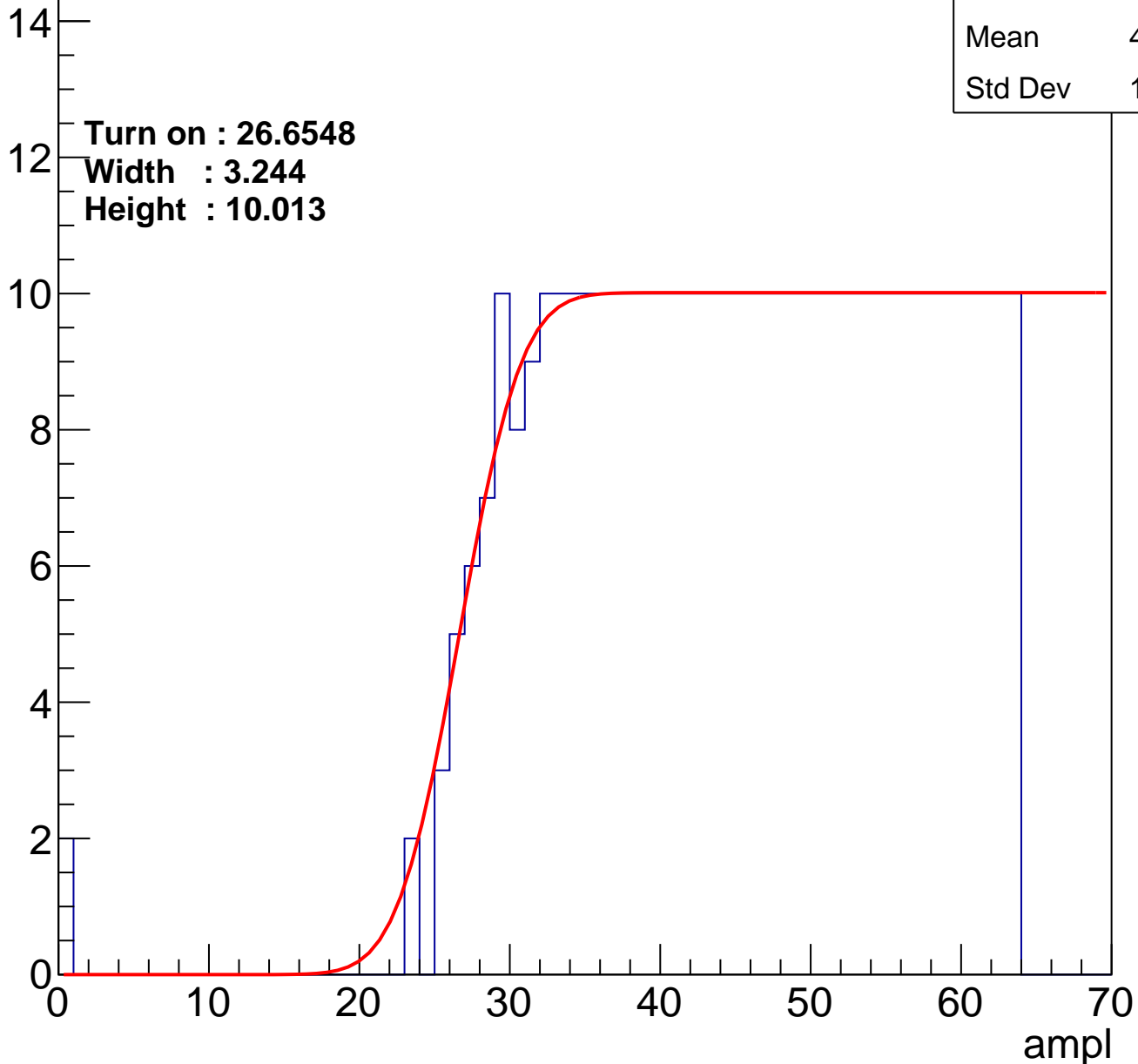
Entries	372
Mean	44.67
Std Dev	11.28

Turn on : 26.6548

Width : 3.244

Height : 10.013

Entry



# B0L000S, U26-ch28

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.8738

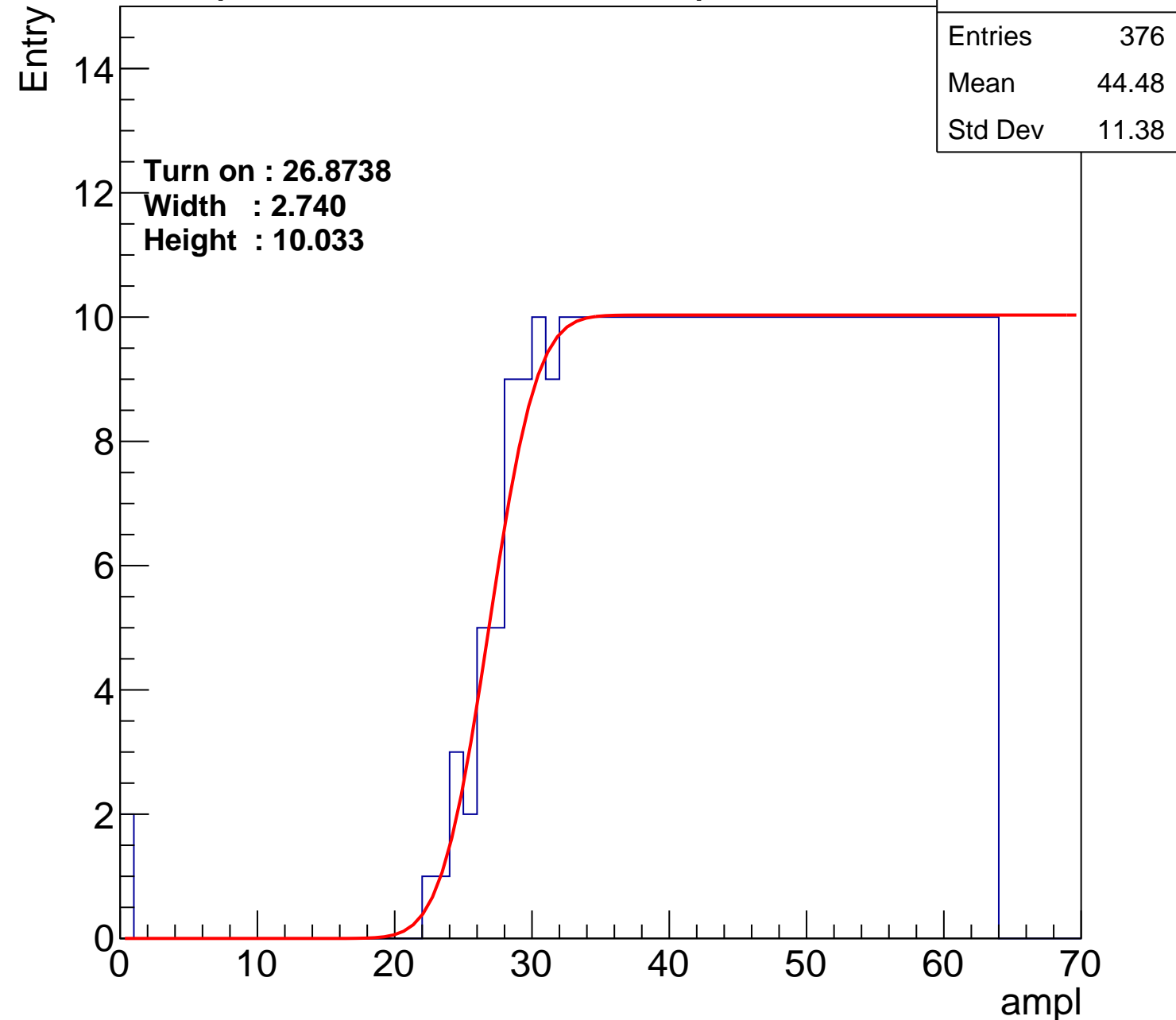
Width : 2.740

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch29

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.82
Std Dev	11.39

Turn on : 27.6184

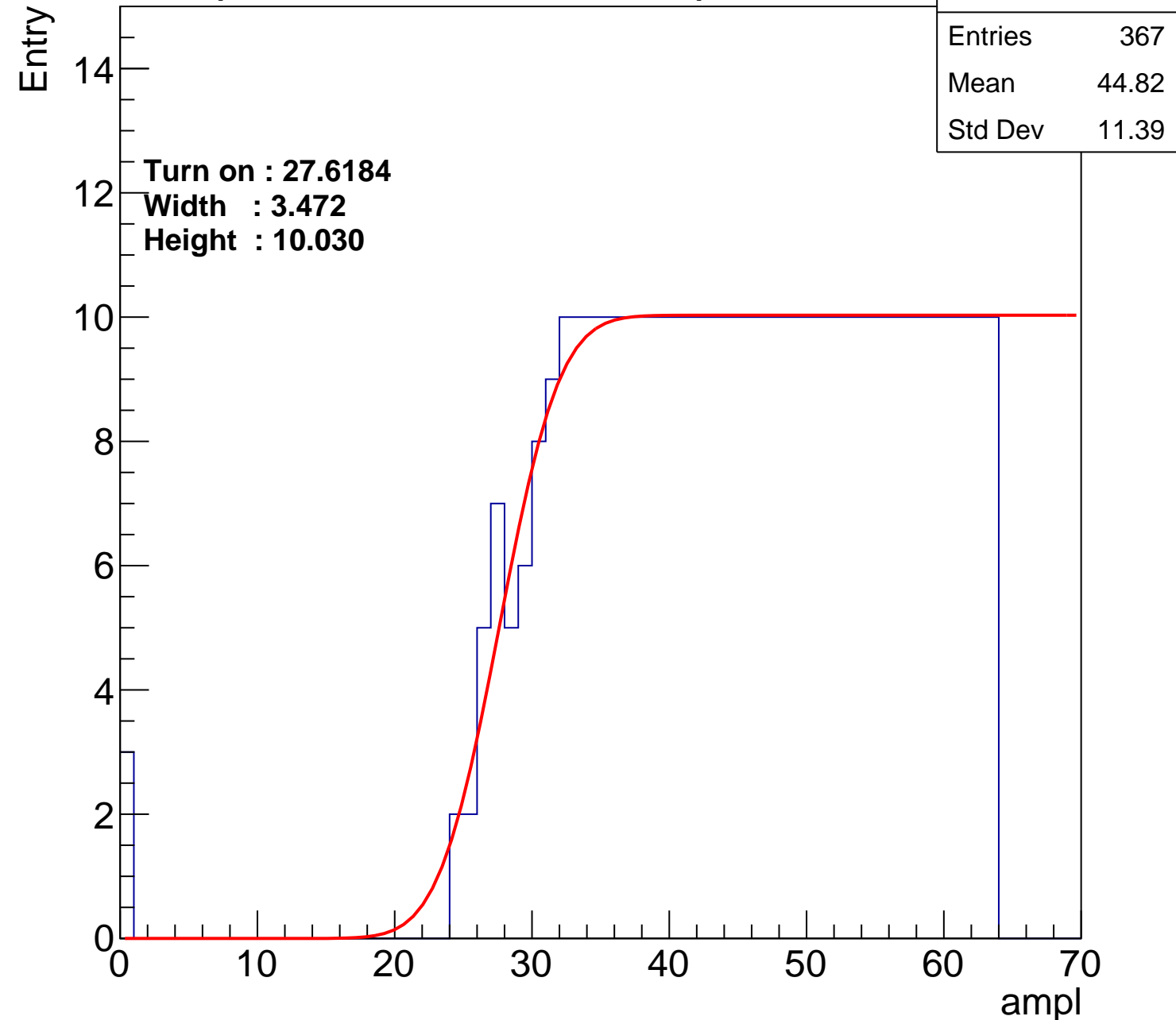
Width : 3.472

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch30

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.3711

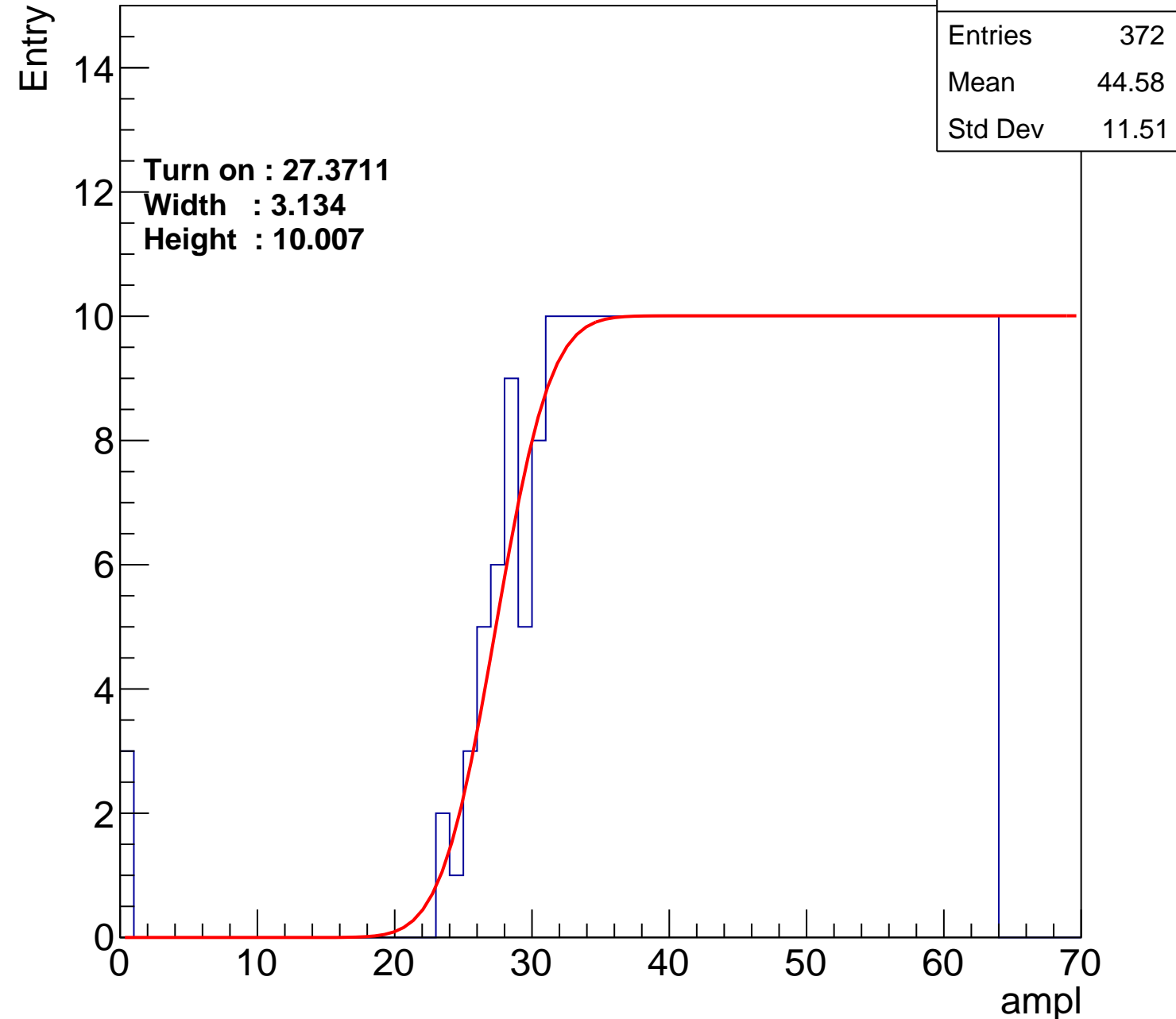
Width : 3.134

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch31

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	363
Mean	45.2
Std Dev	10.82

**Turn on : 28.3279**

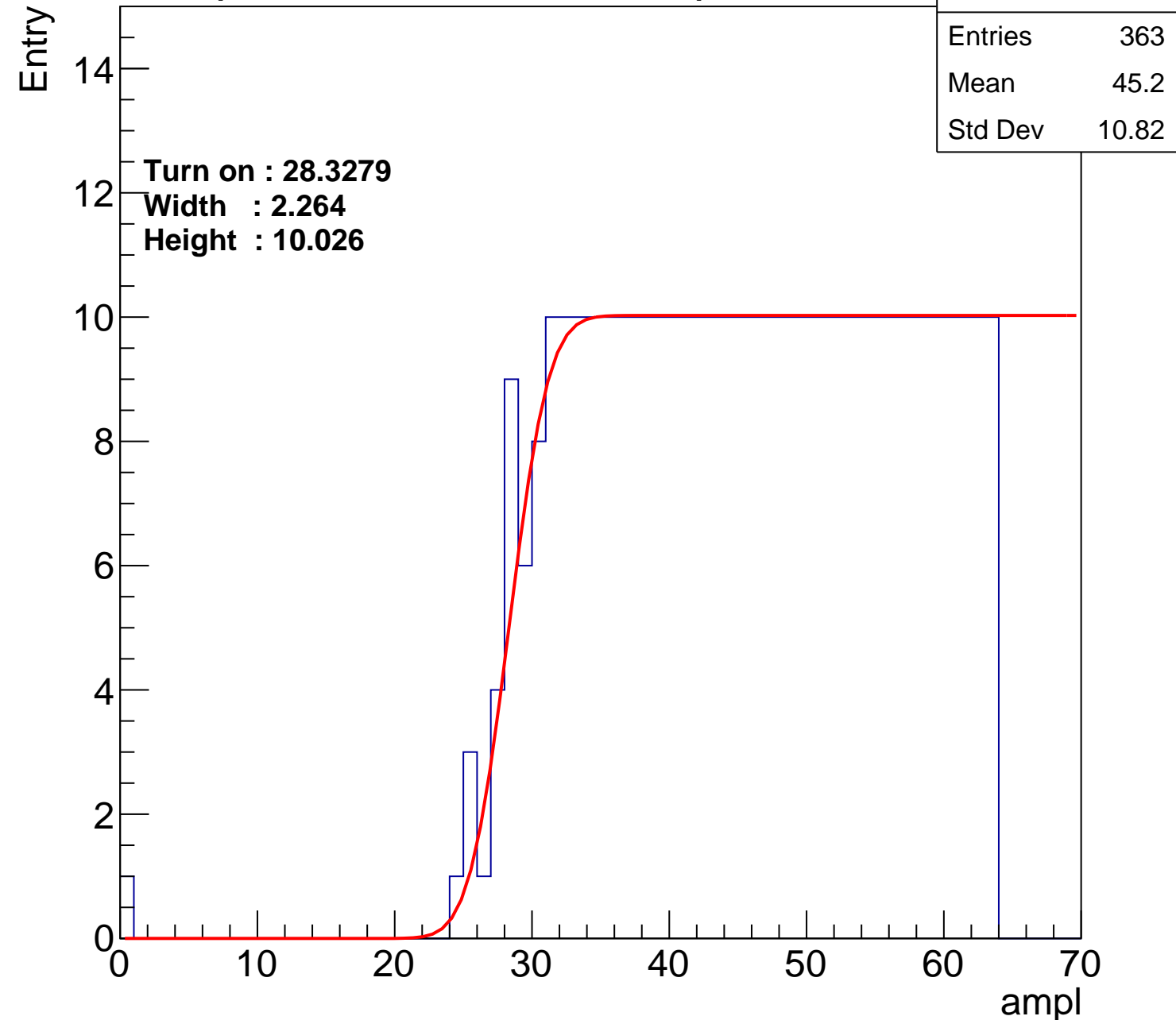
**Width : 2.264**

**Height : 10.026**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch32

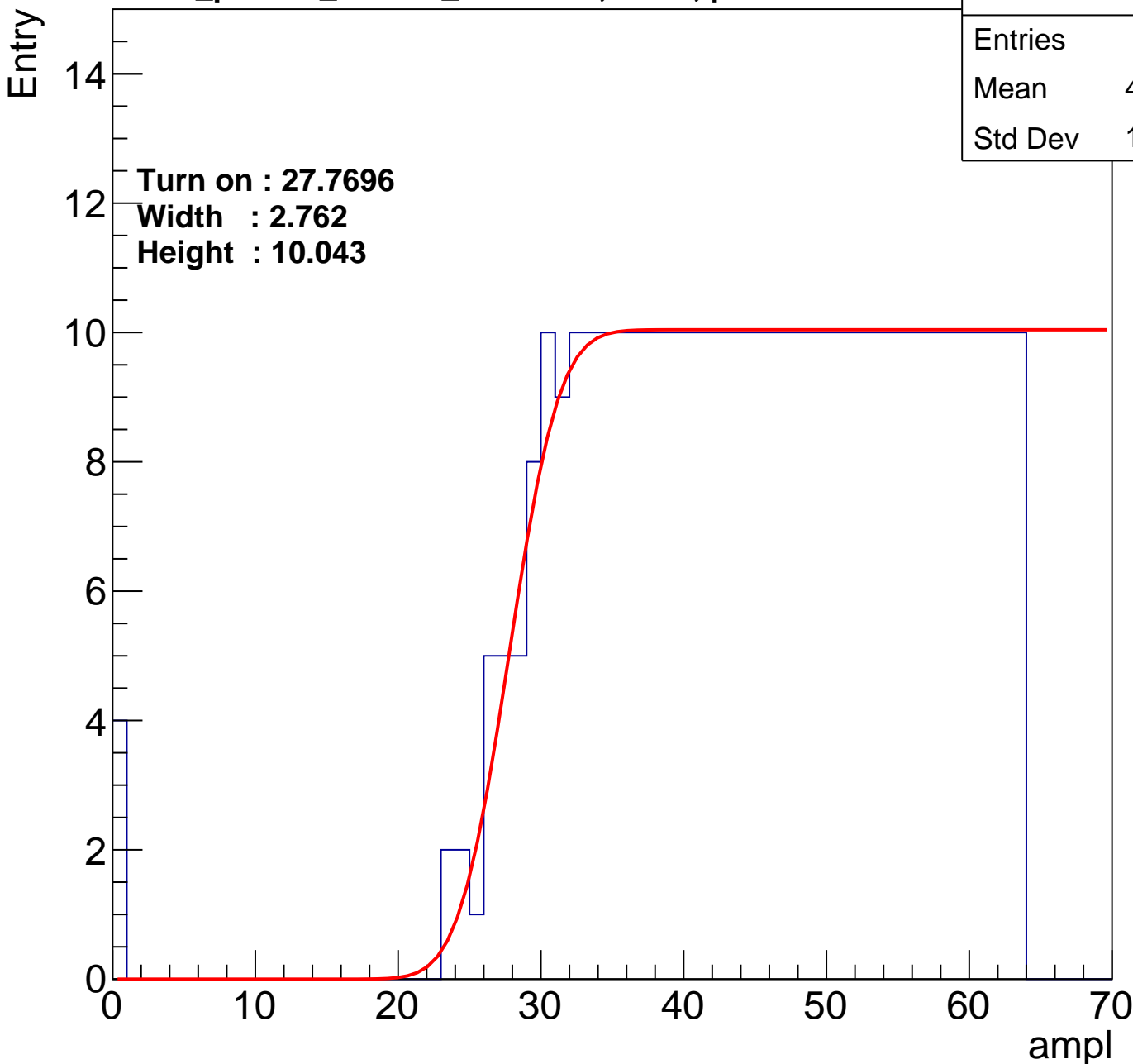
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	371
Mean	44.57
Std Dev	11.67

**Turn on : 27.7696**

**Width : 2.762**

**Height : 10.043**



# B0L000S, U26-ch33

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	43.93
Std Dev	11.96

Turn on : 25.7991

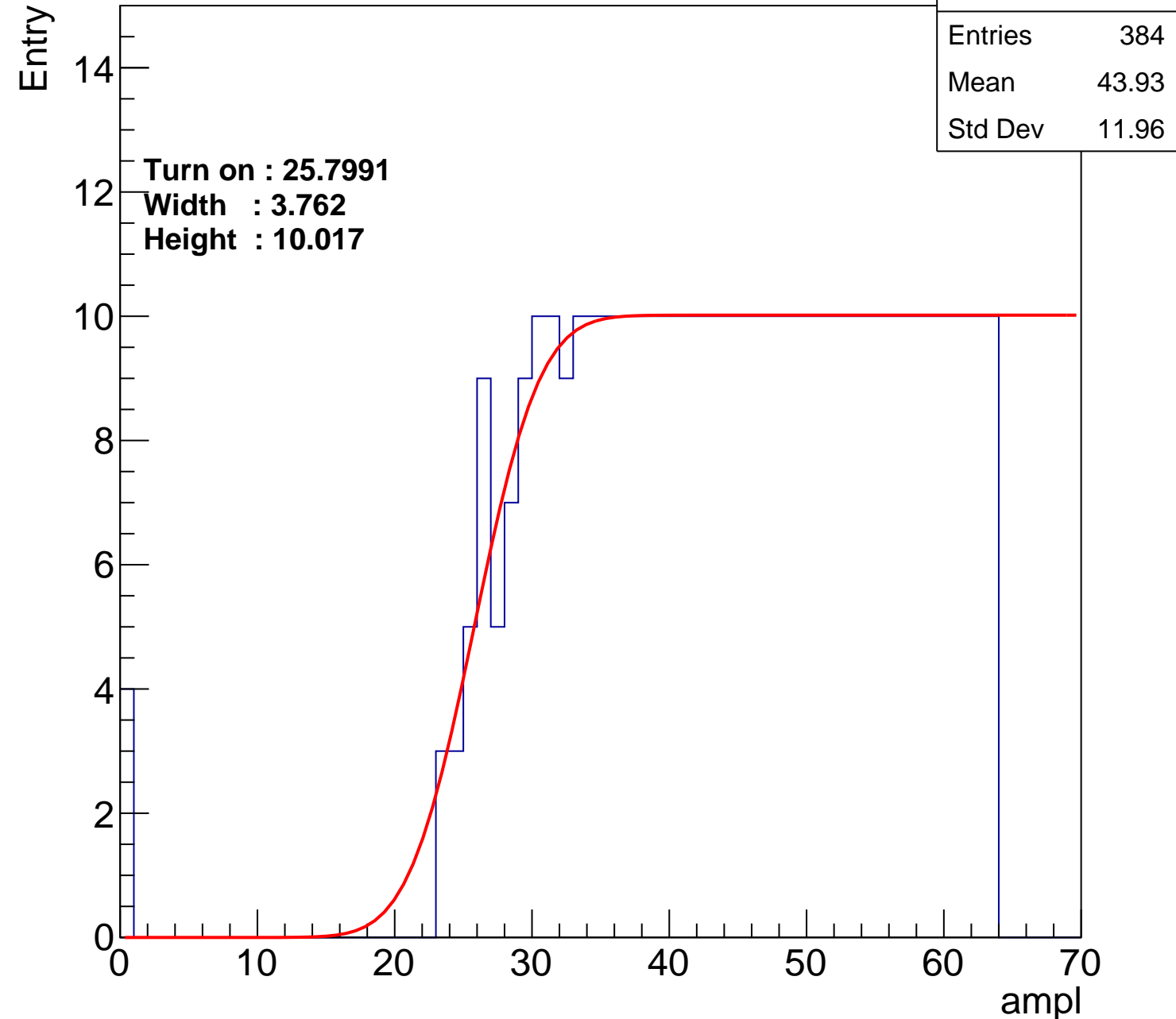
Width : 3.762

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch34

calib\_packv5\_042523\_0143.root, FC#5, port B1

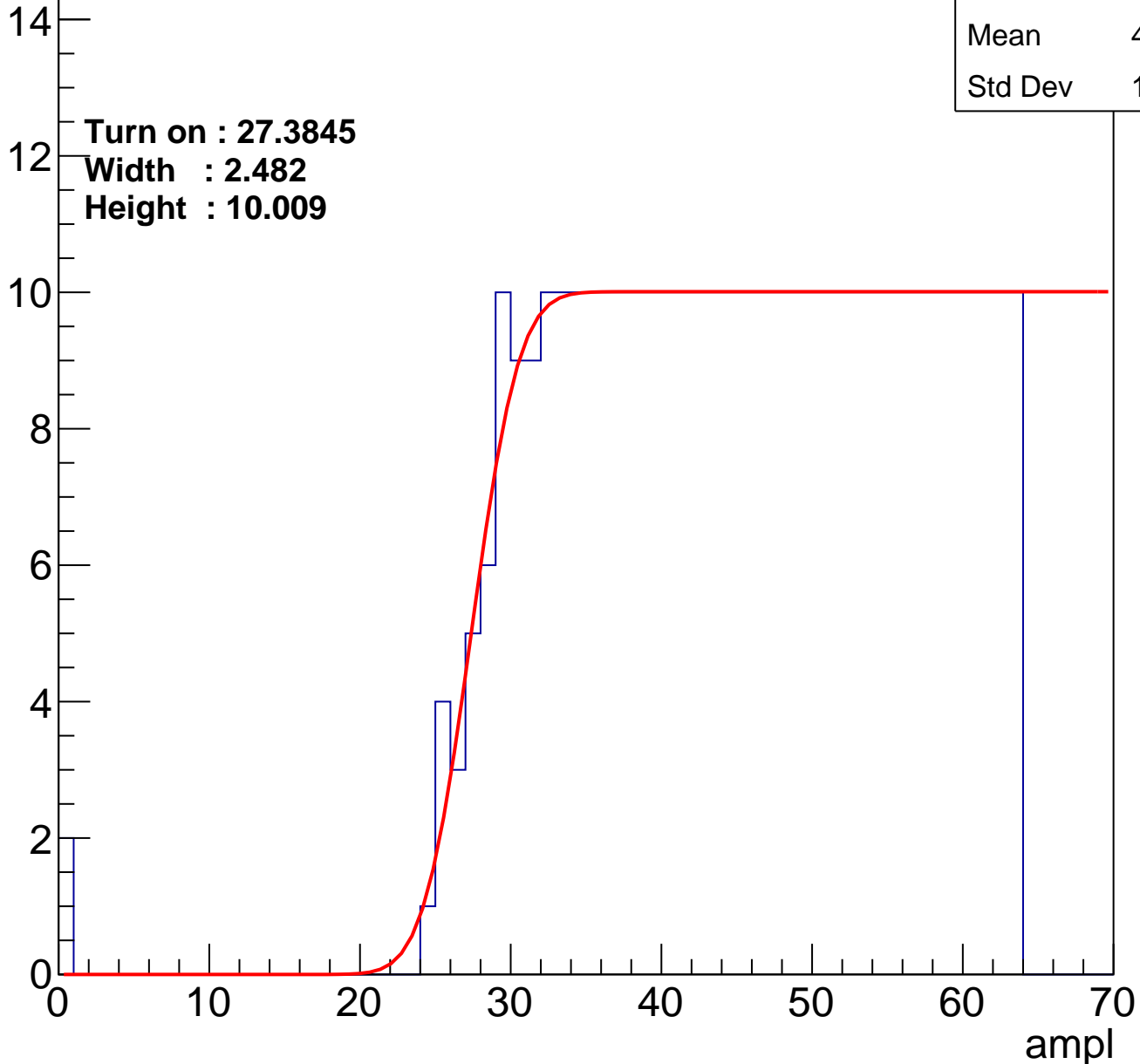
Entries	369
Mean	44.83
Std Dev	11.18

Turn on : 27.3845

Width : 2.482

Height : 10.009

Entry



# B0L000S, U26-ch35

calib\_packv5\_042523\_0143.root, FC#5, port B1

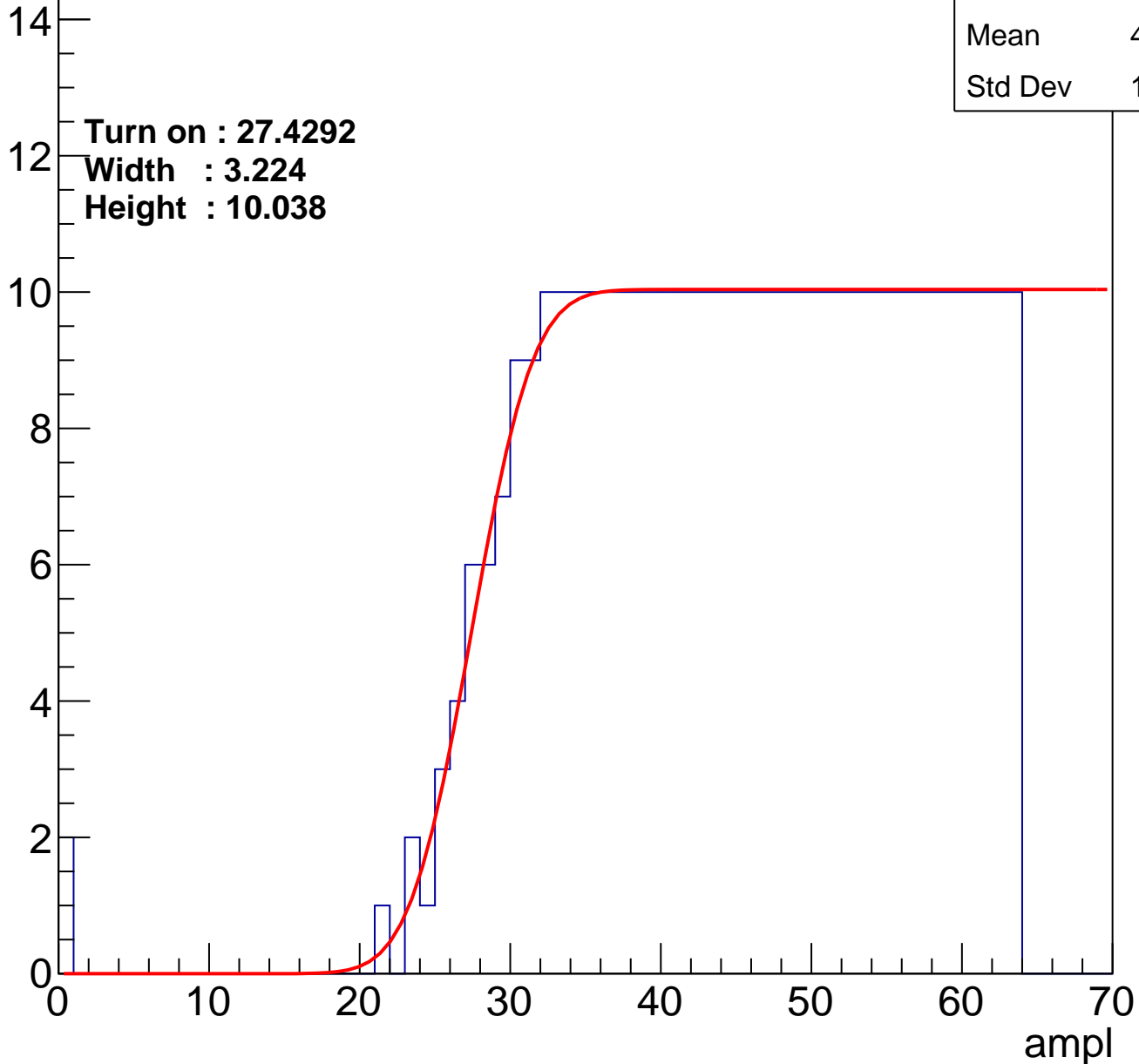
Entries	370
Mean	44.74
Std Dev	11.29

Turn on : 27.4292

Width : 3.224

Height : 10.038

Entry



# B0L000S, U26-ch36

calib\_packv5\_042523\_0143.root, FC#5, port B1

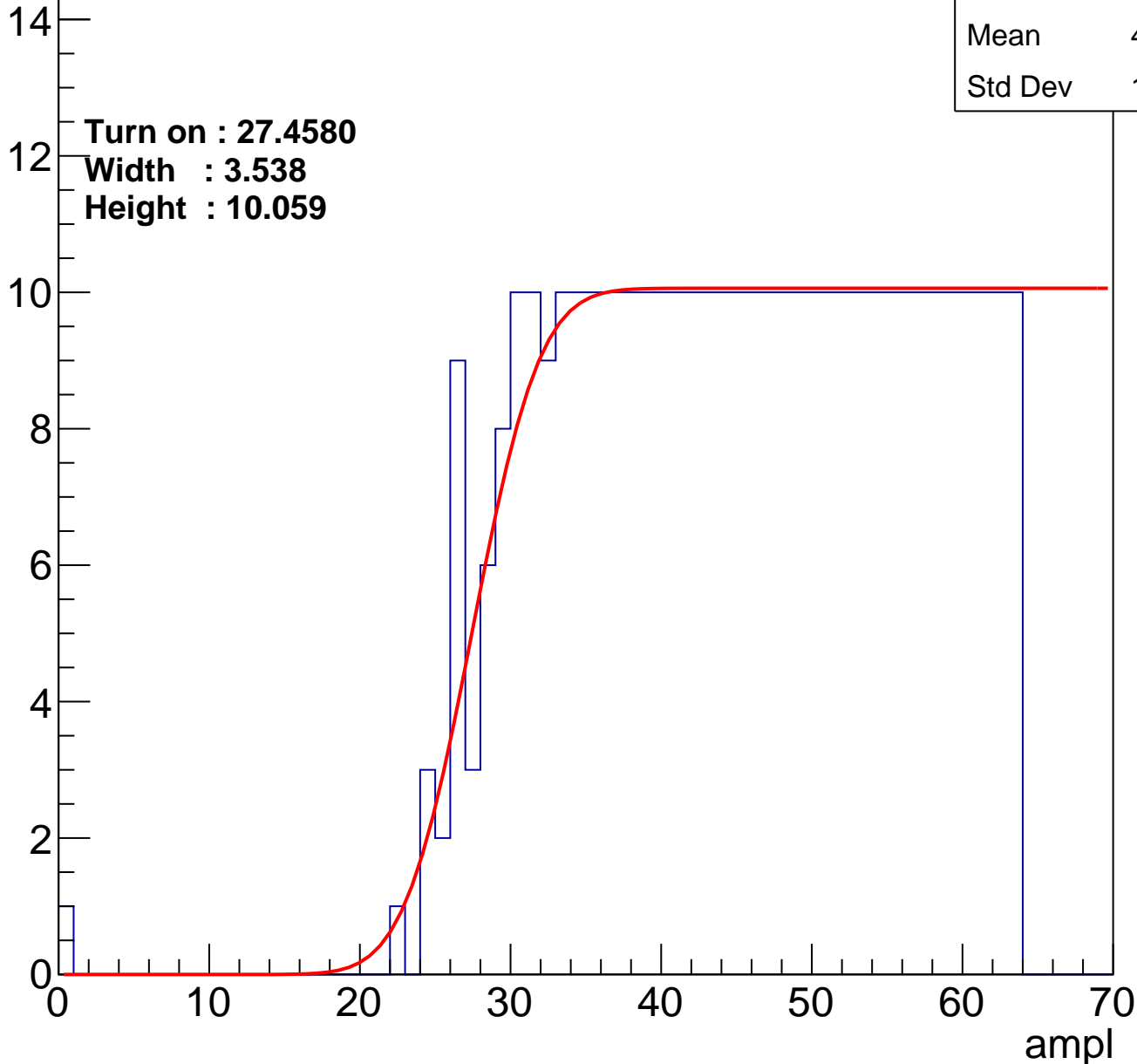
Entries	372
Mean	44.72
Std Dev	11.12

Turn on : 27.4580

Width : 3.538

Height : 10.059

Entry



# B0L000S, U26-ch37

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.7612

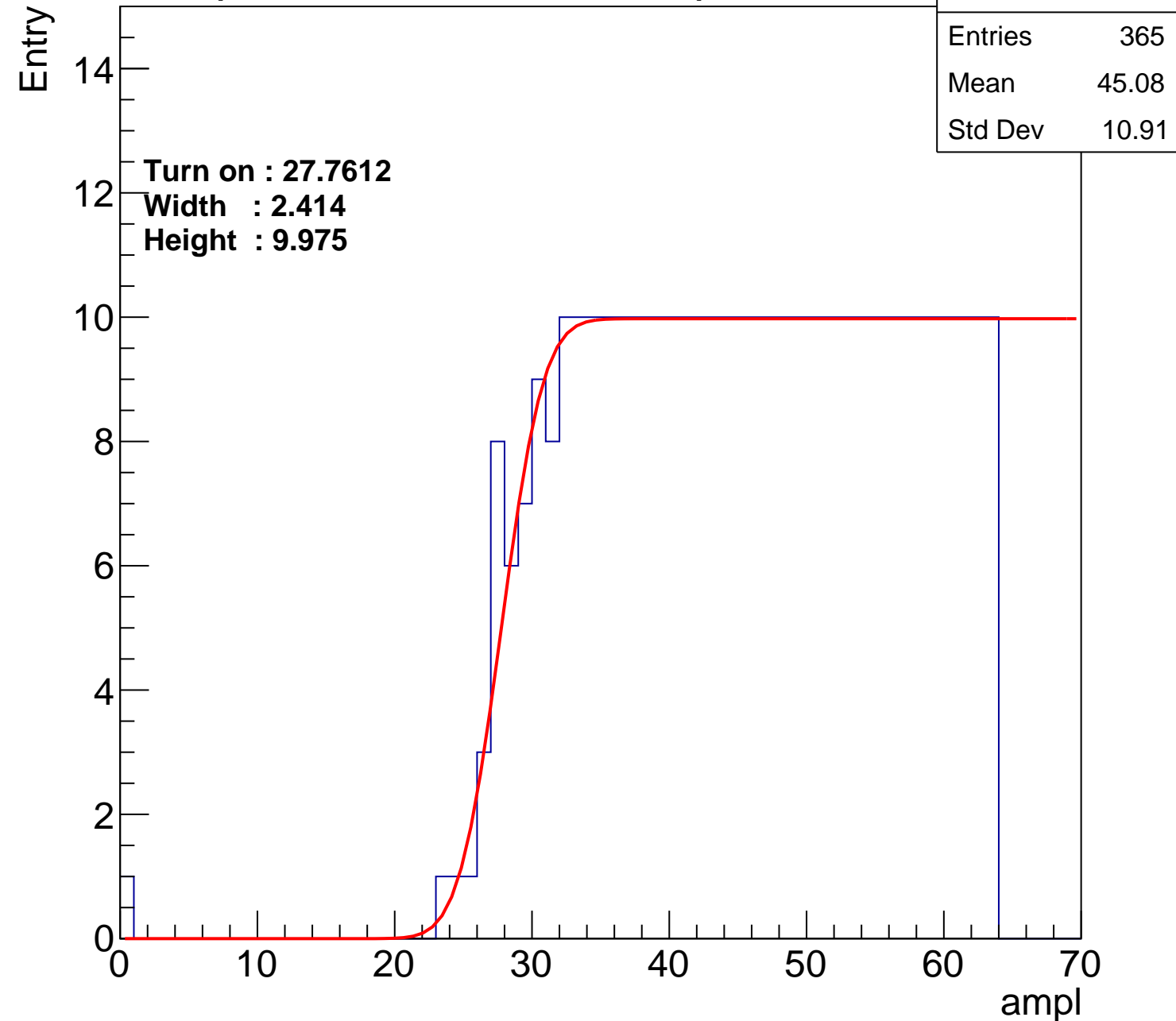
Width : 2.414

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch38

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.45
Std Dev	11.69

Turn on : 27.4973

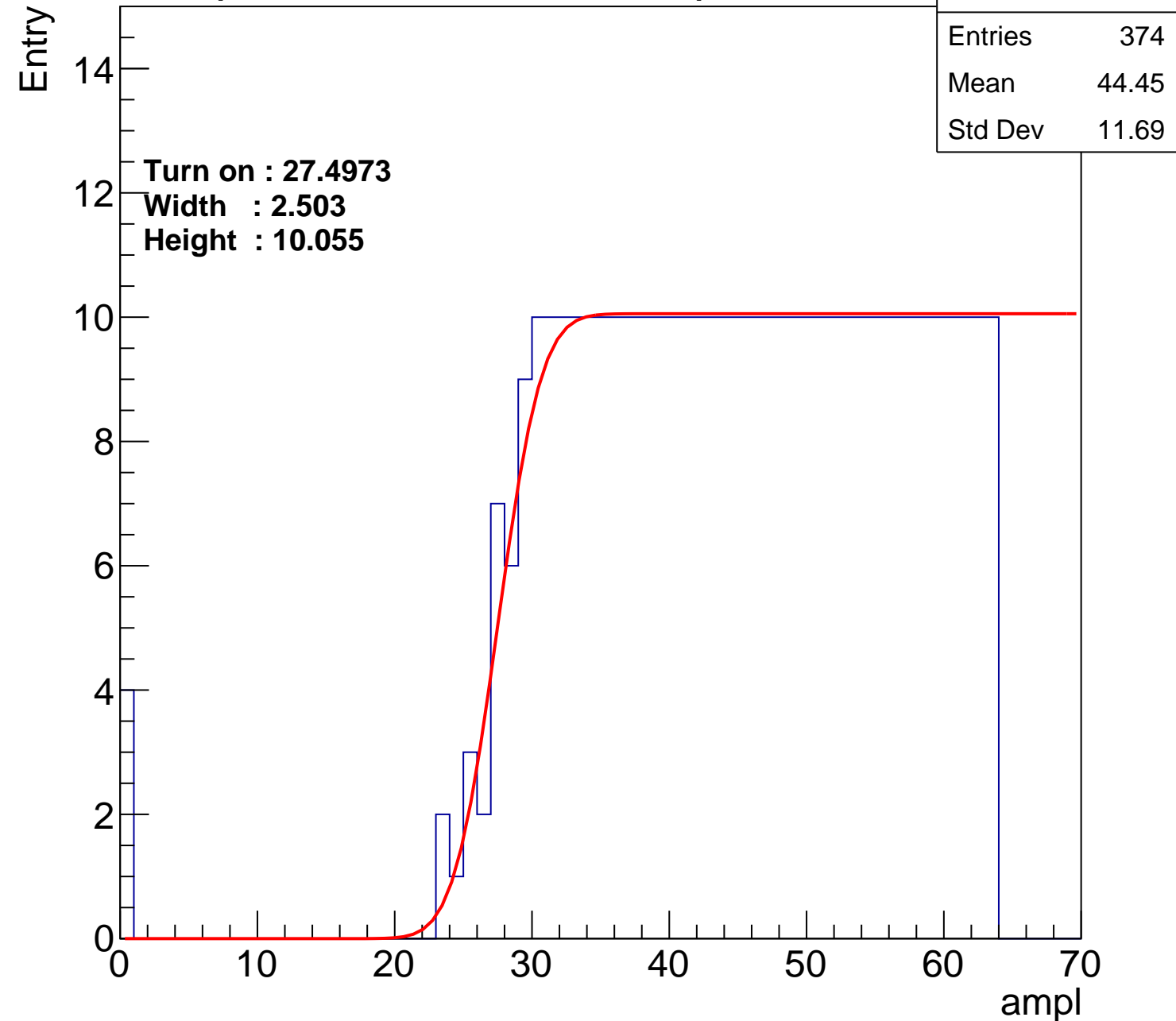
Width : 2.503

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch39

calib\_packv5\_042523\_0143.root, FC#5, port B1

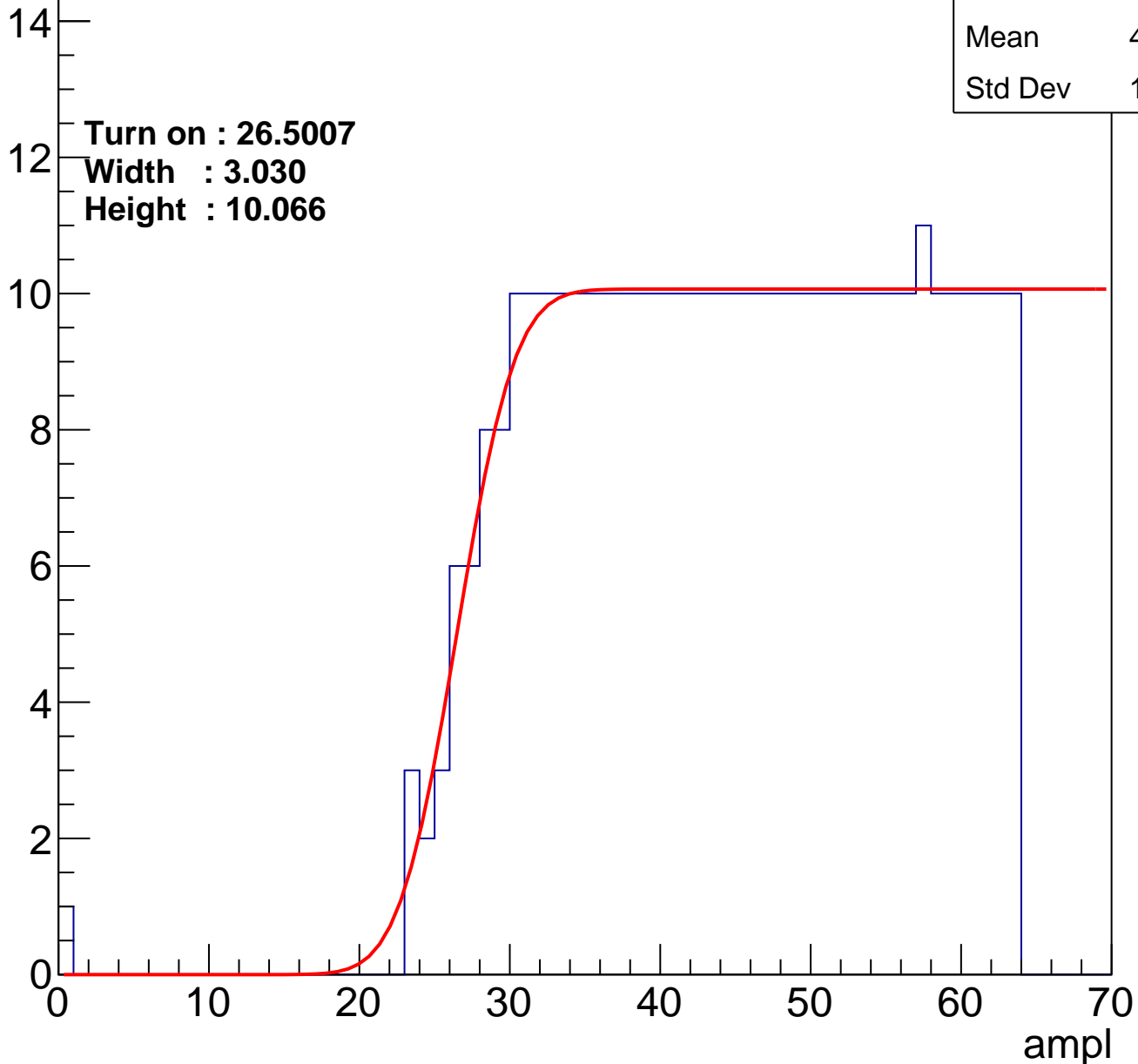
Entries	378
Mean	44.53
Std Dev	11.22

Turn on : 26.5007

Width : 3.030

Height : 10.066

Entry



# B0L000S, U26-ch40

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.64
Std Dev	11.48

**Turn on : 27.3728**

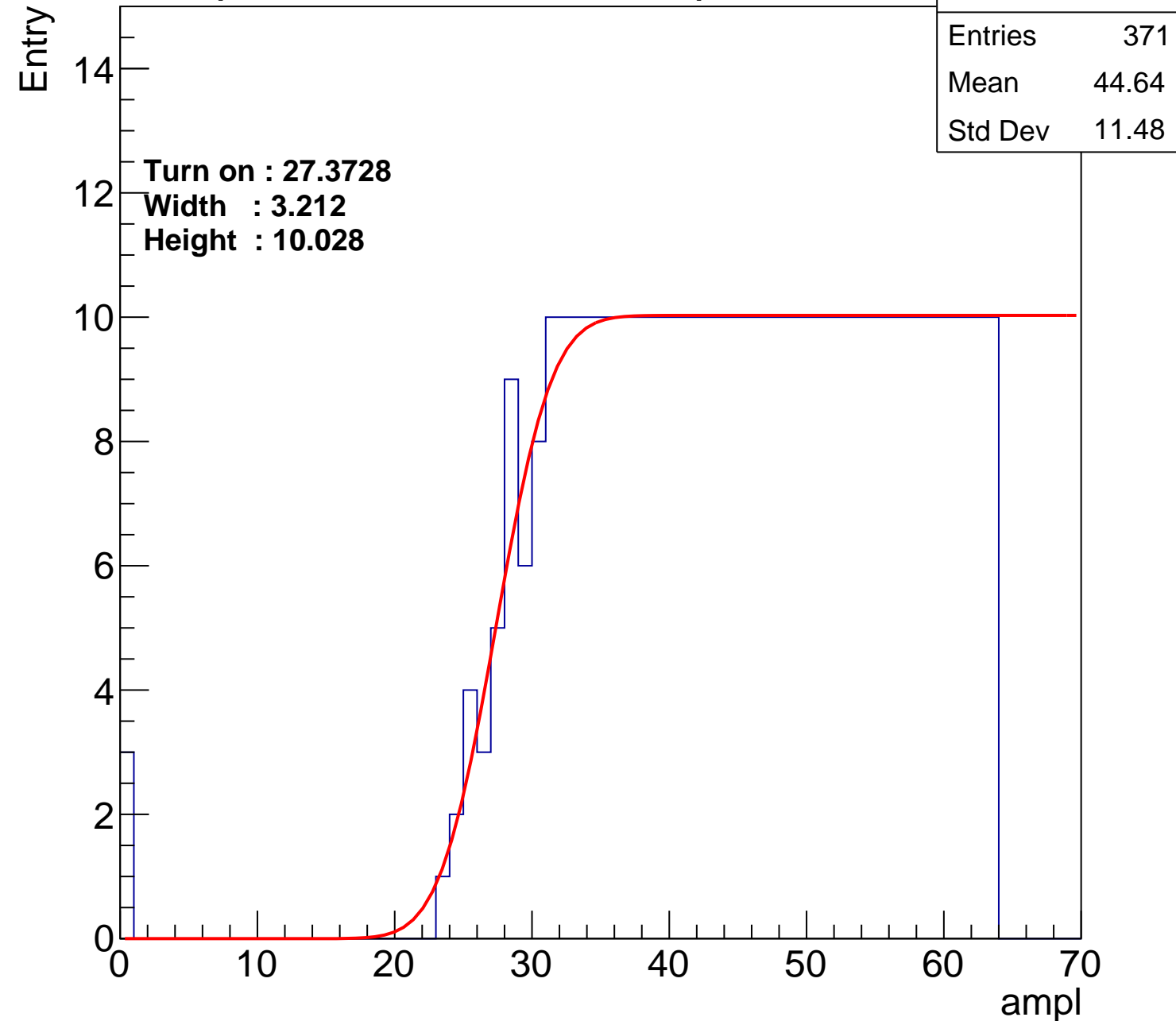
**Width : 3.212**

**Height : 10.028**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch41

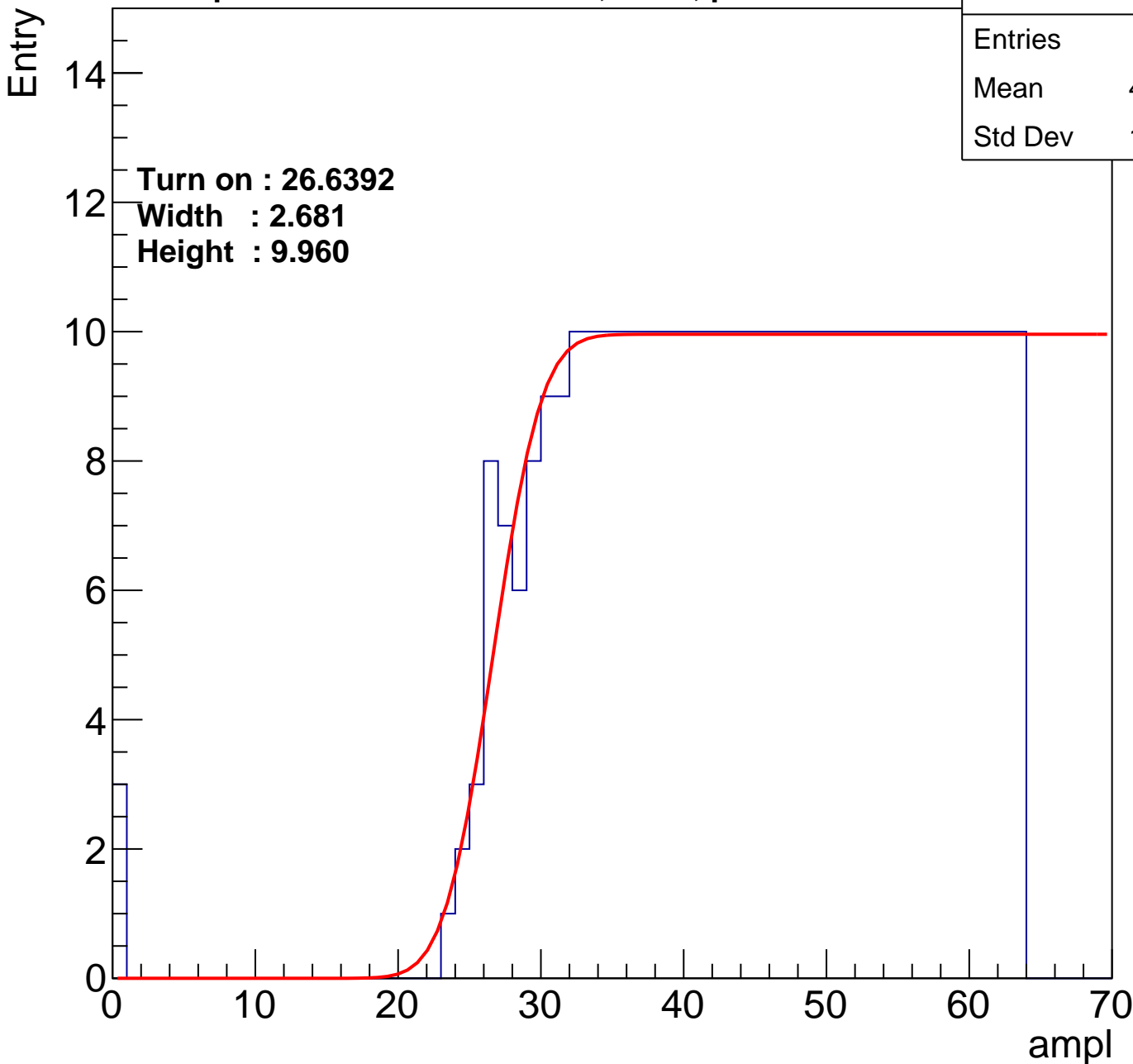
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.39
Std Dev	11.59

Turn on : 26.6392

Width : 2.681

Height : 9.960



# B0L000S, U26-ch42

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.42
Std Dev	11.42

**Turn on : 26.6784**

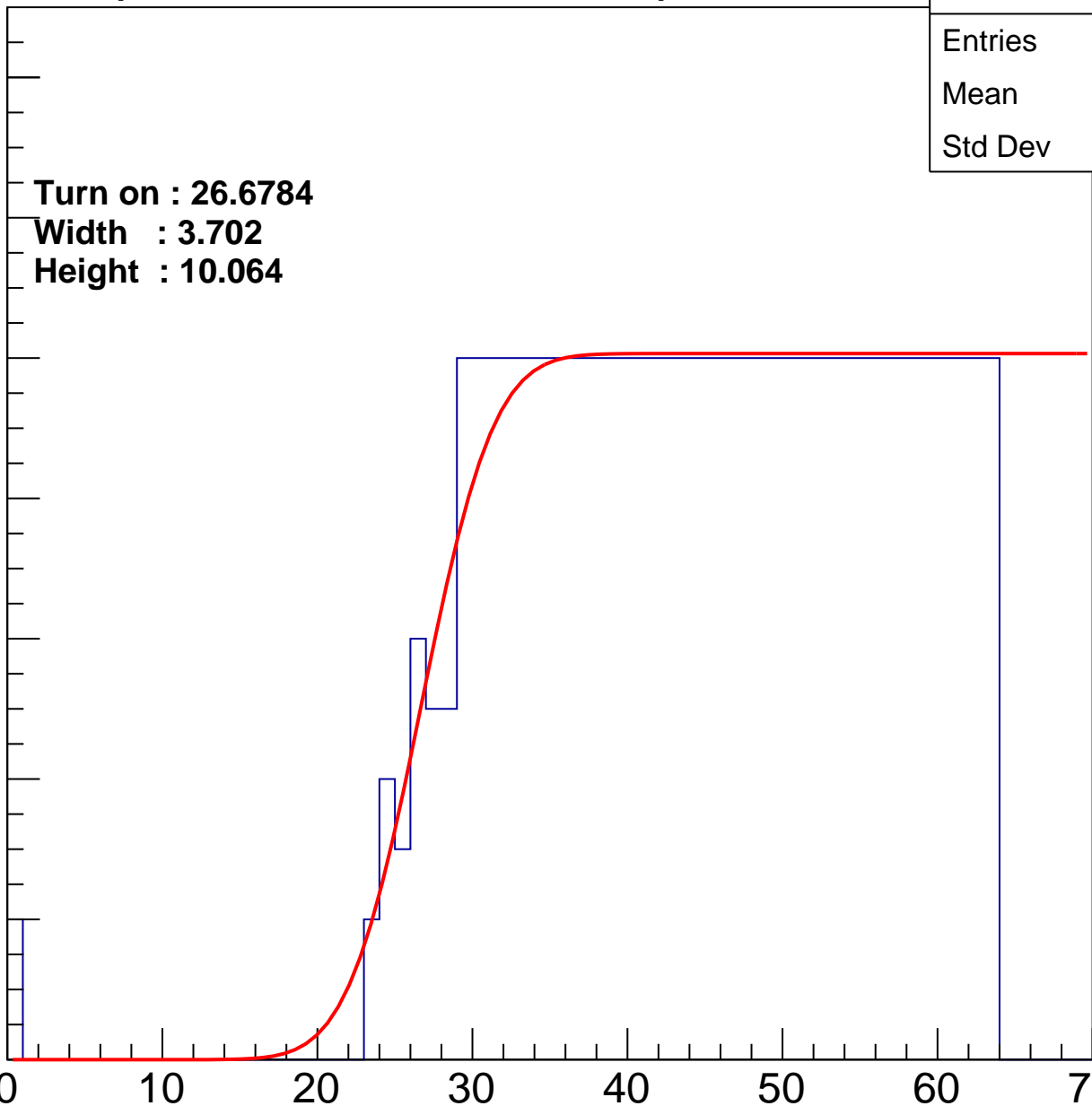
**Width : 3.702**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch43

calib\_packv5\_042523\_0143.root, FC#5, port B1

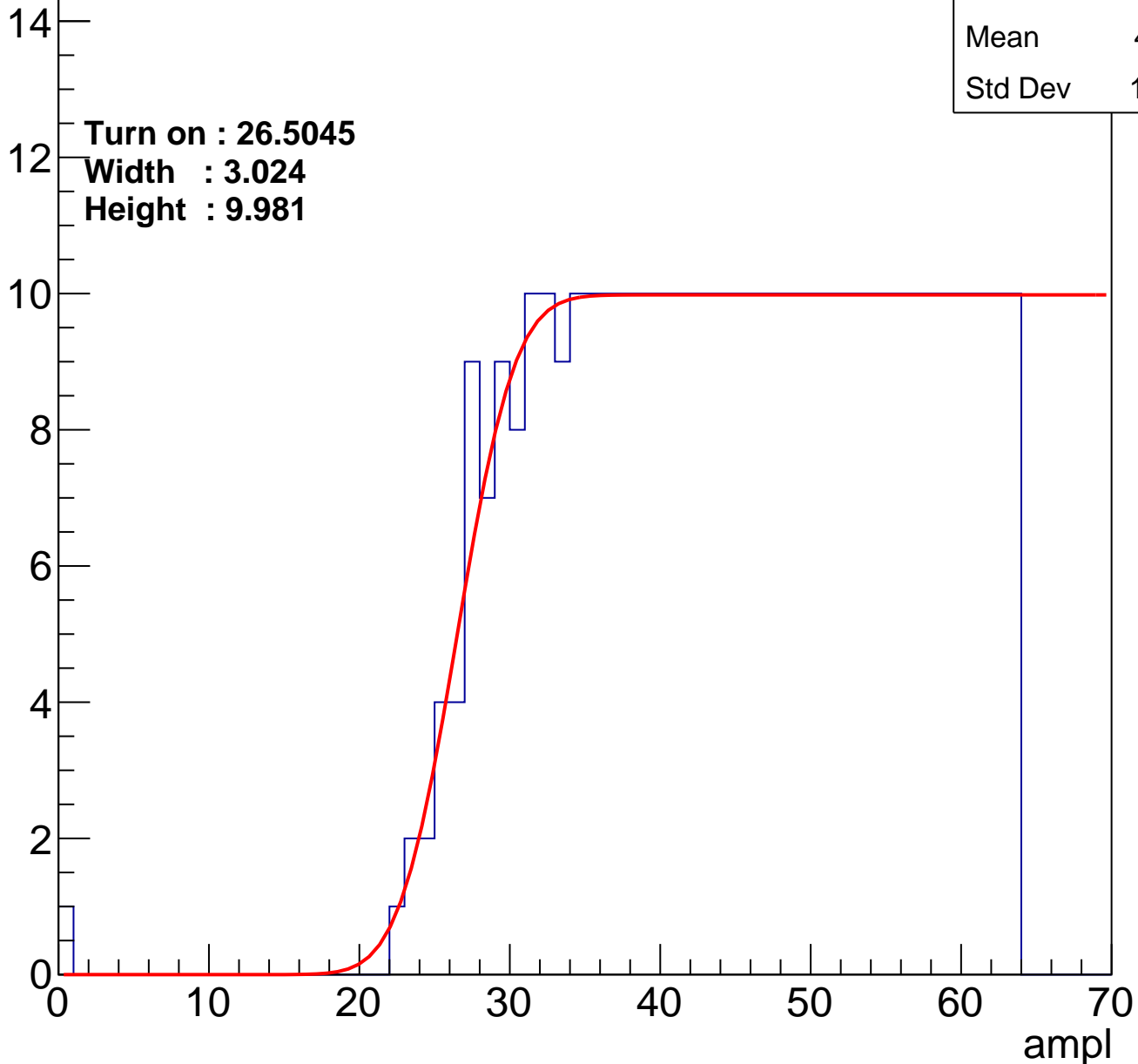
Entries	376
Mean	44.51
Std Dev	11.24

Turn on : 26.5045

Width : 3.024

Height : 9.981

Entry



# B0L000S, U26-ch44

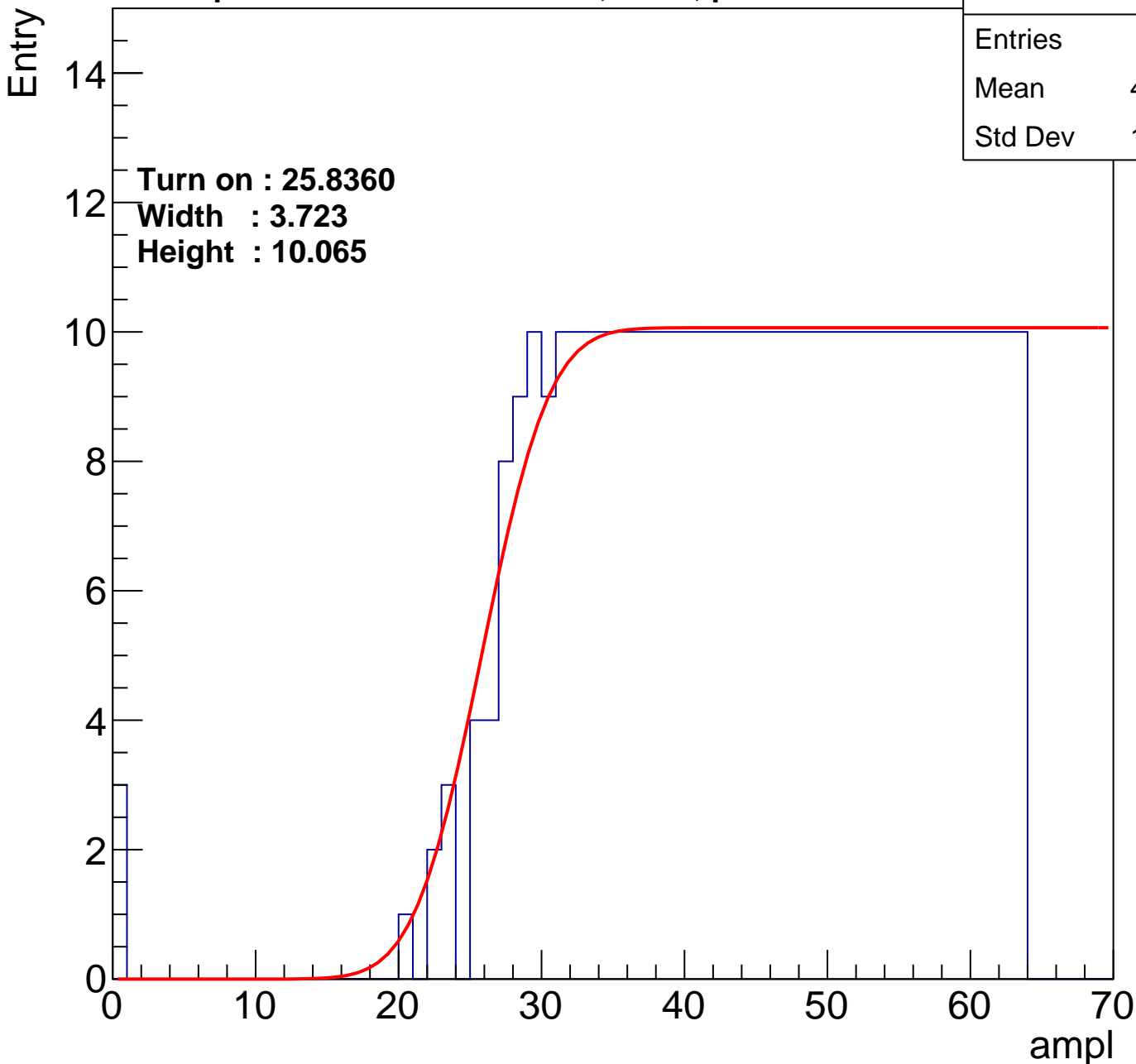
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.06
Std Dev	11.76

Turn on : 25.8360

Width : 3.723

Height : 10.065



# B0L000S, U26-ch45

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	361
Mean	45.28
Std Dev	10.8

Turn on : 27.7672

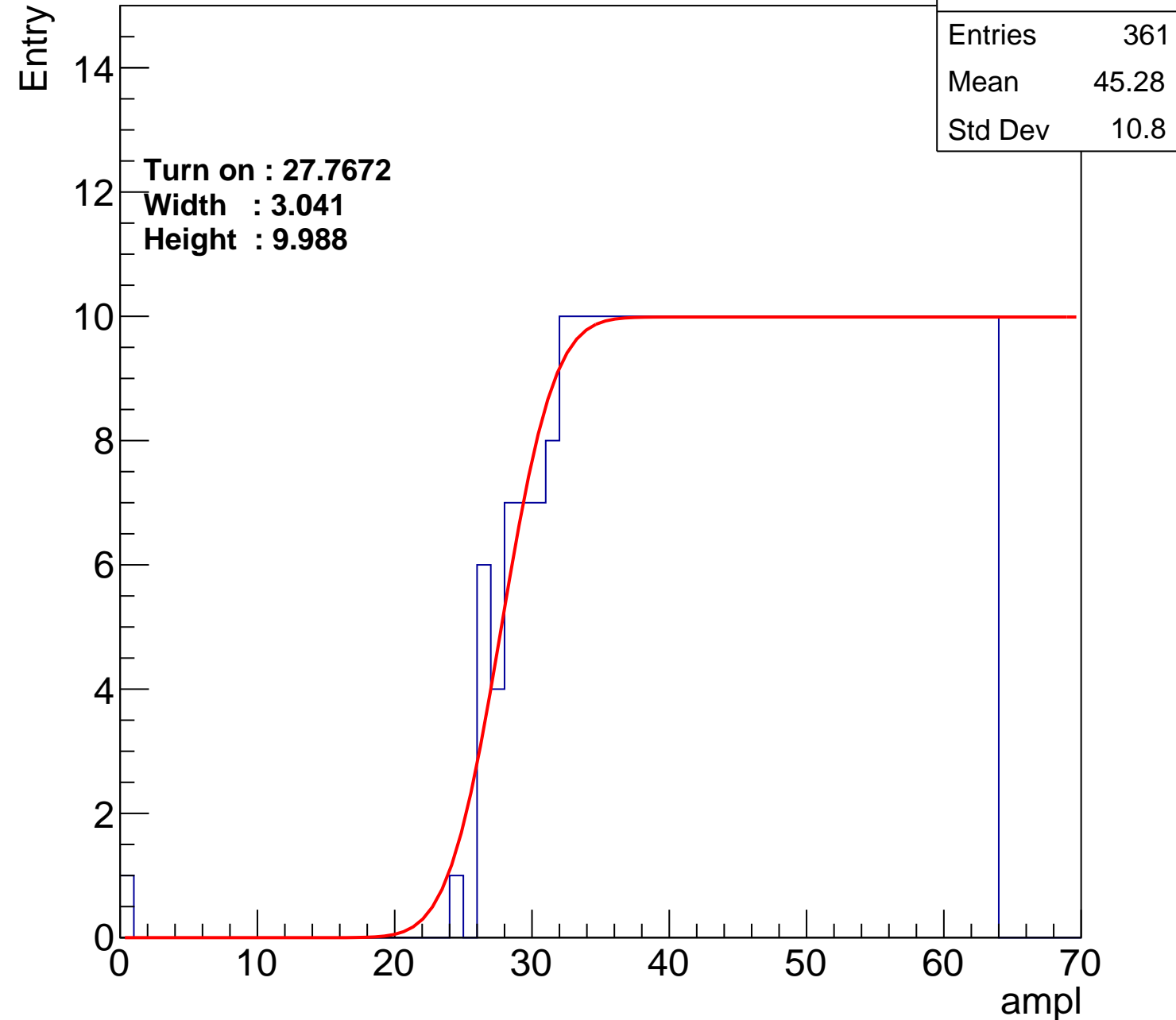
Width : 3.041

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch46

calib\_packv5\_042523\_0143.root, FC#5, port B1

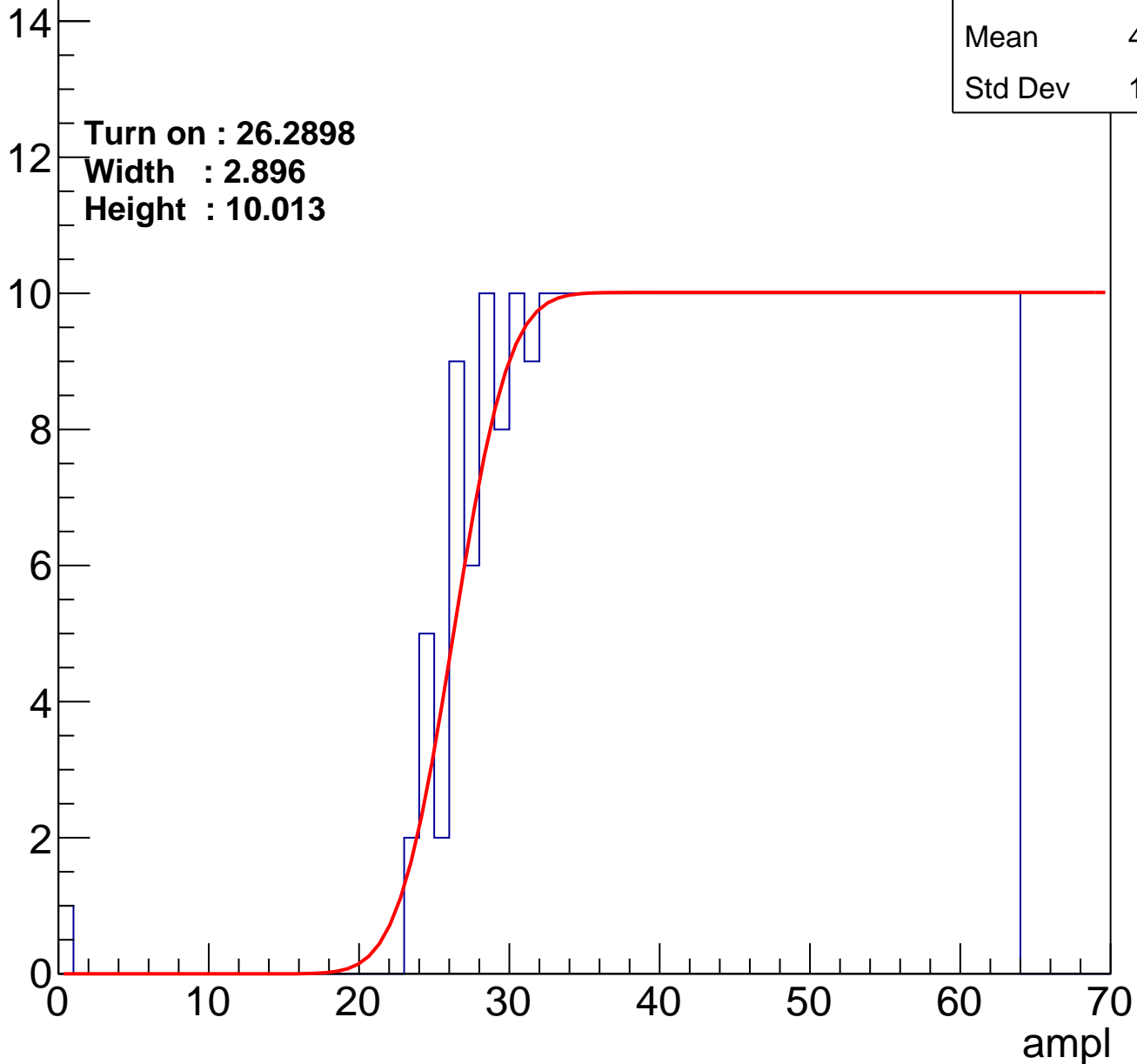
Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.2898

Width : 2.896

Height : 10.013

Entry





# B0L000S, U26-ch47

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.27
Std Dev	11.39

**Turn on : 26.3555**

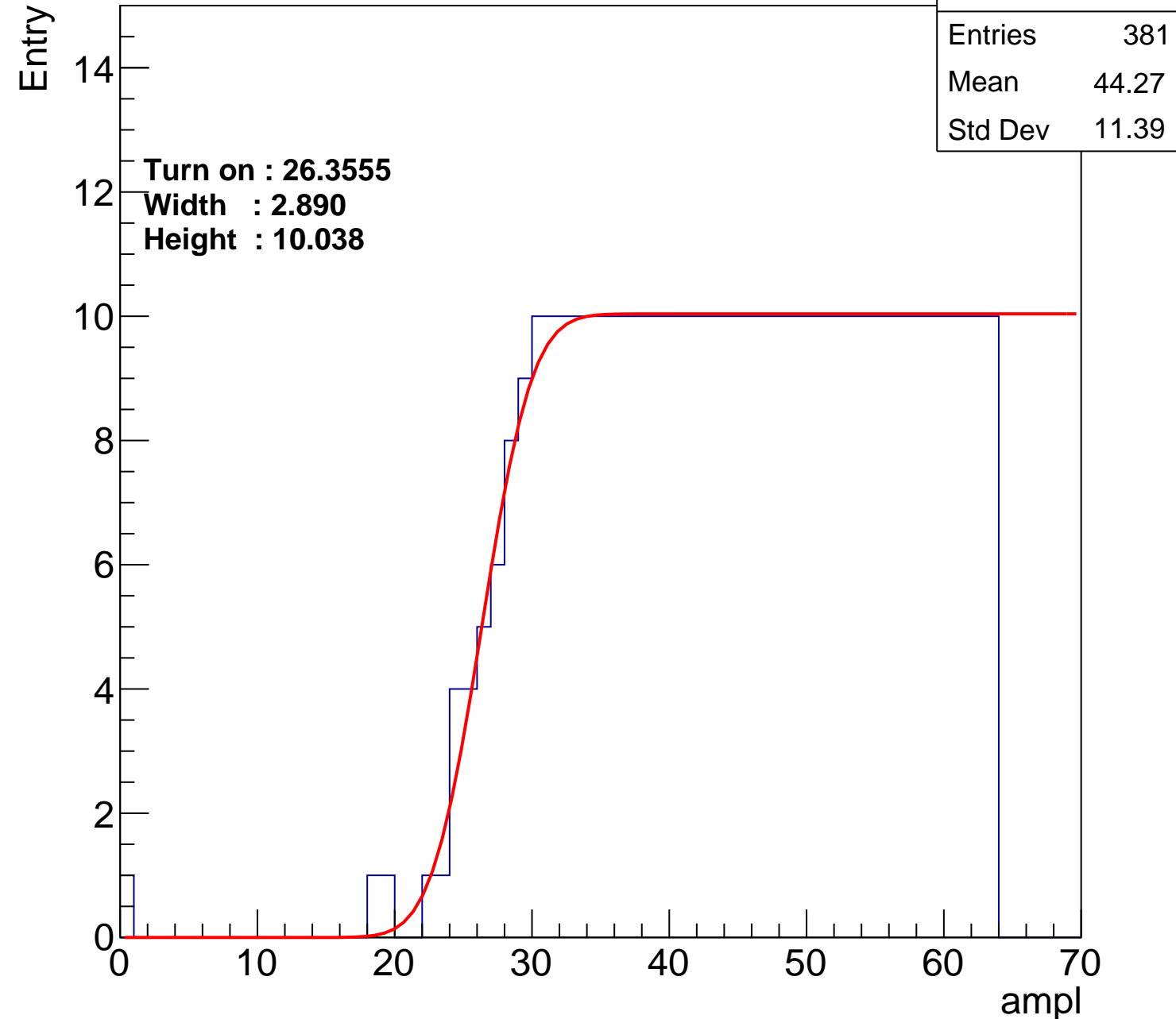
**Width : 2.890**

**Height : 10.038**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch48

calib\_packv5\_042523\_0143.root, FC#5, port B1

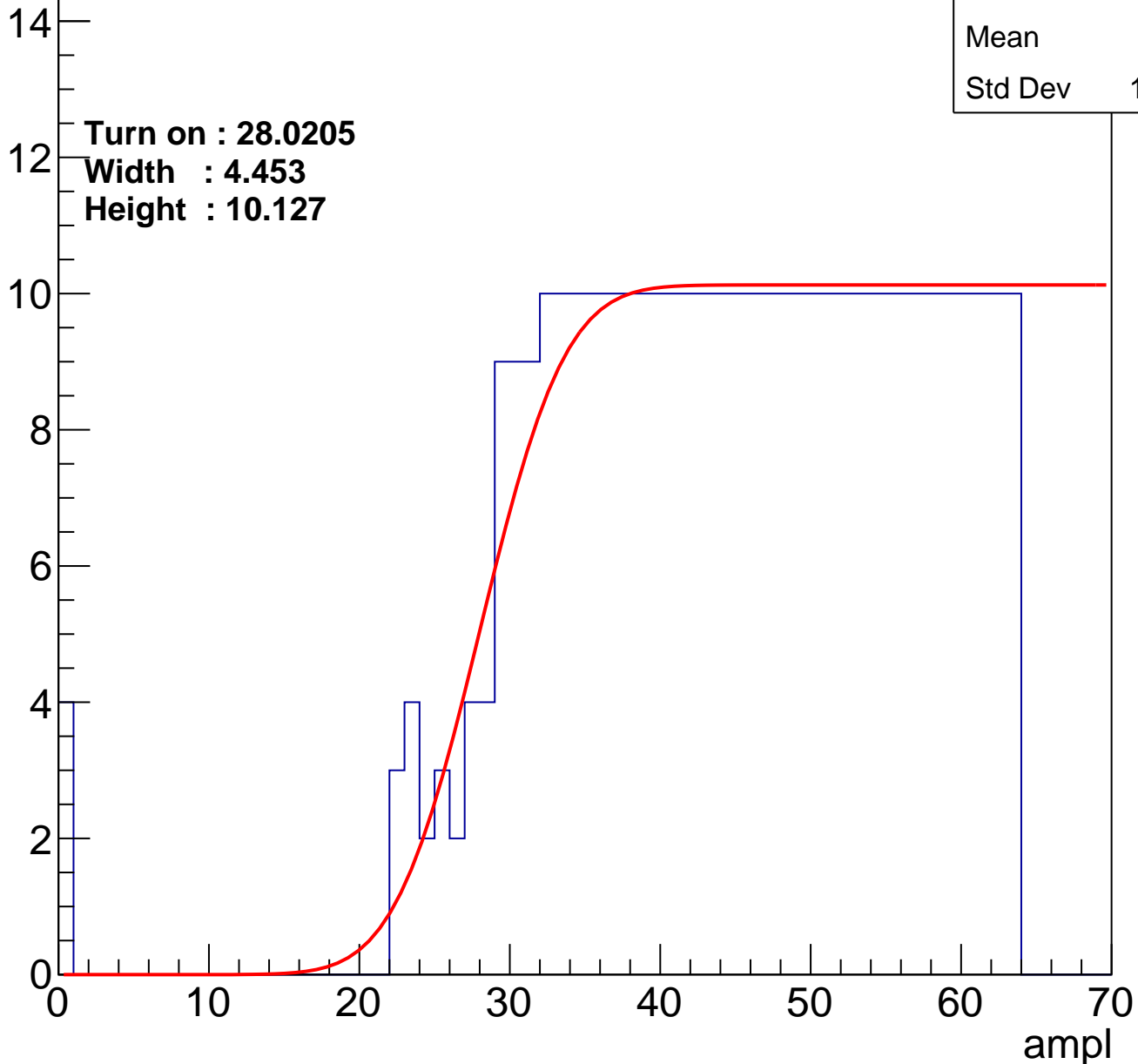
Entries	373
Mean	44.4
Std Dev	11.82

Turn on : 28.0205

Width : 4.453

Height : 10.127

Entry



# B0L000S, U26-ch49

calib\_packv5\_042523\_0143.root, FC#5, port B1

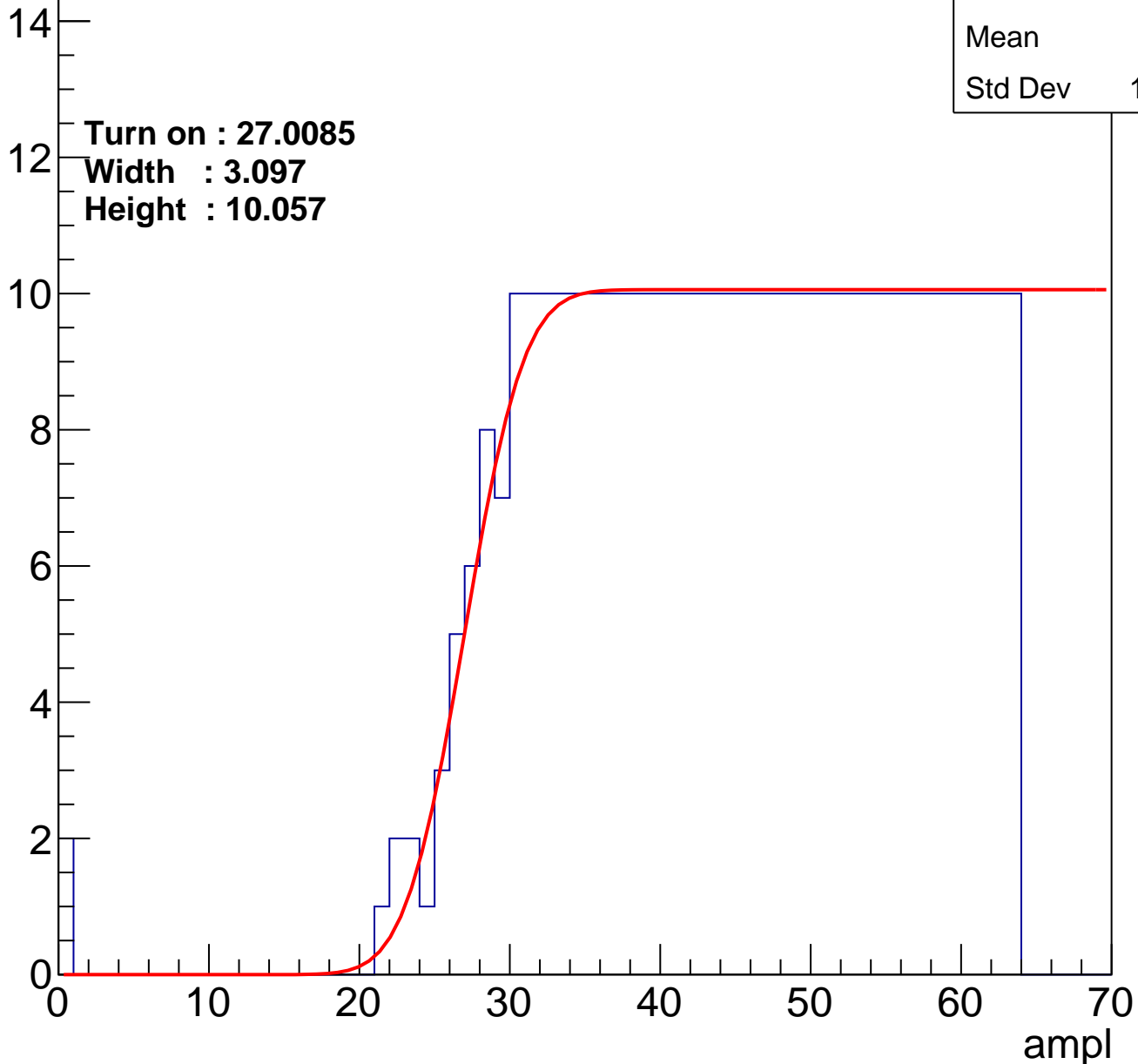
Entries	377
Mean	44.4
Std Dev	11.46

Turn on : 27.0085

Width : 3.097

Height : 10.057

Entry



# B0L000S, U26-ch50

calib\_packv5\_042523\_0143.root, FC#5, port B1

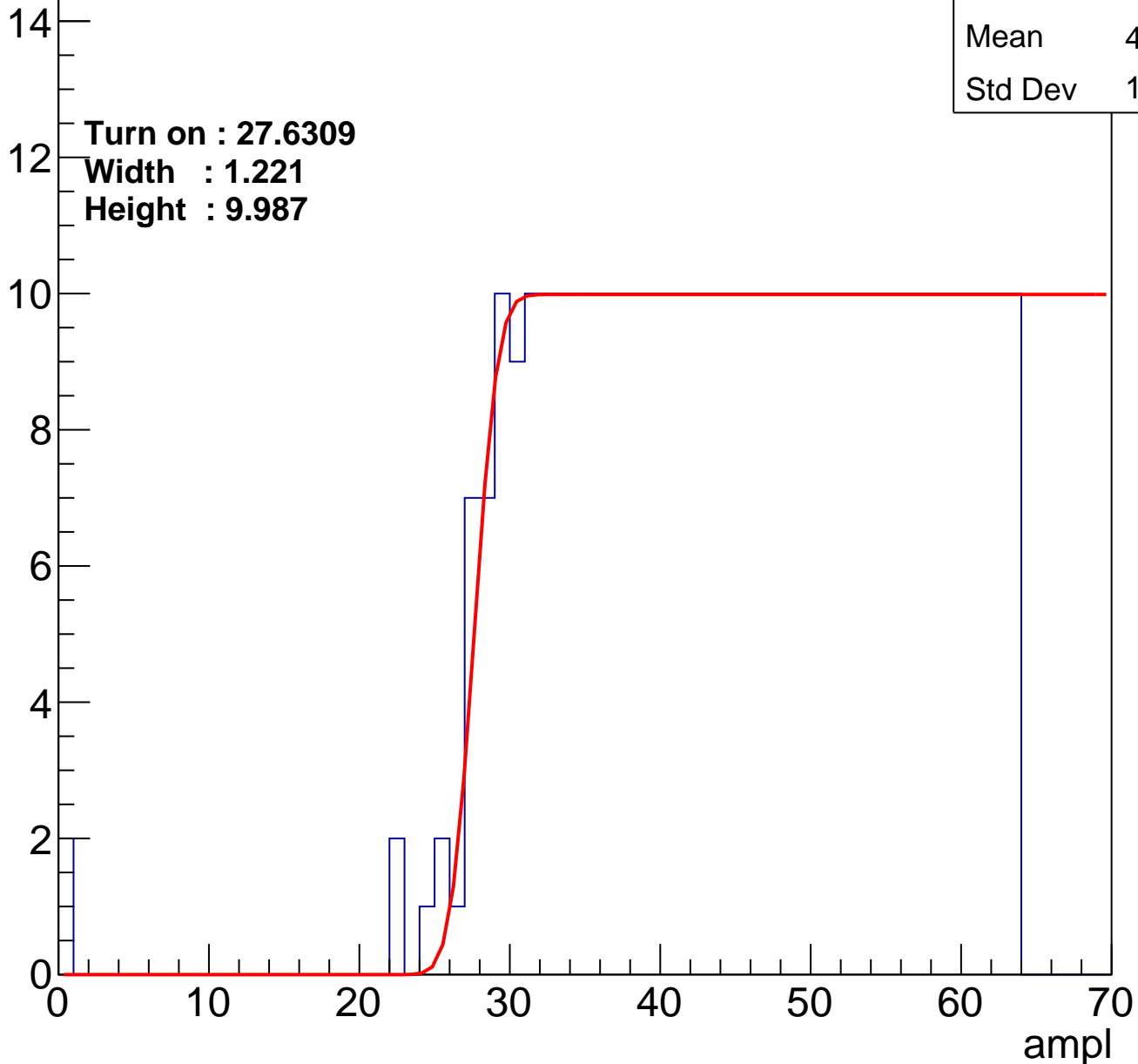
Entries	371
Mean	44.74
Std Dev	11.23

**Turn on : 27.6309**

**Width : 1.221**

**Height : 9.987**

Entry



# B0L000S, U26-ch51

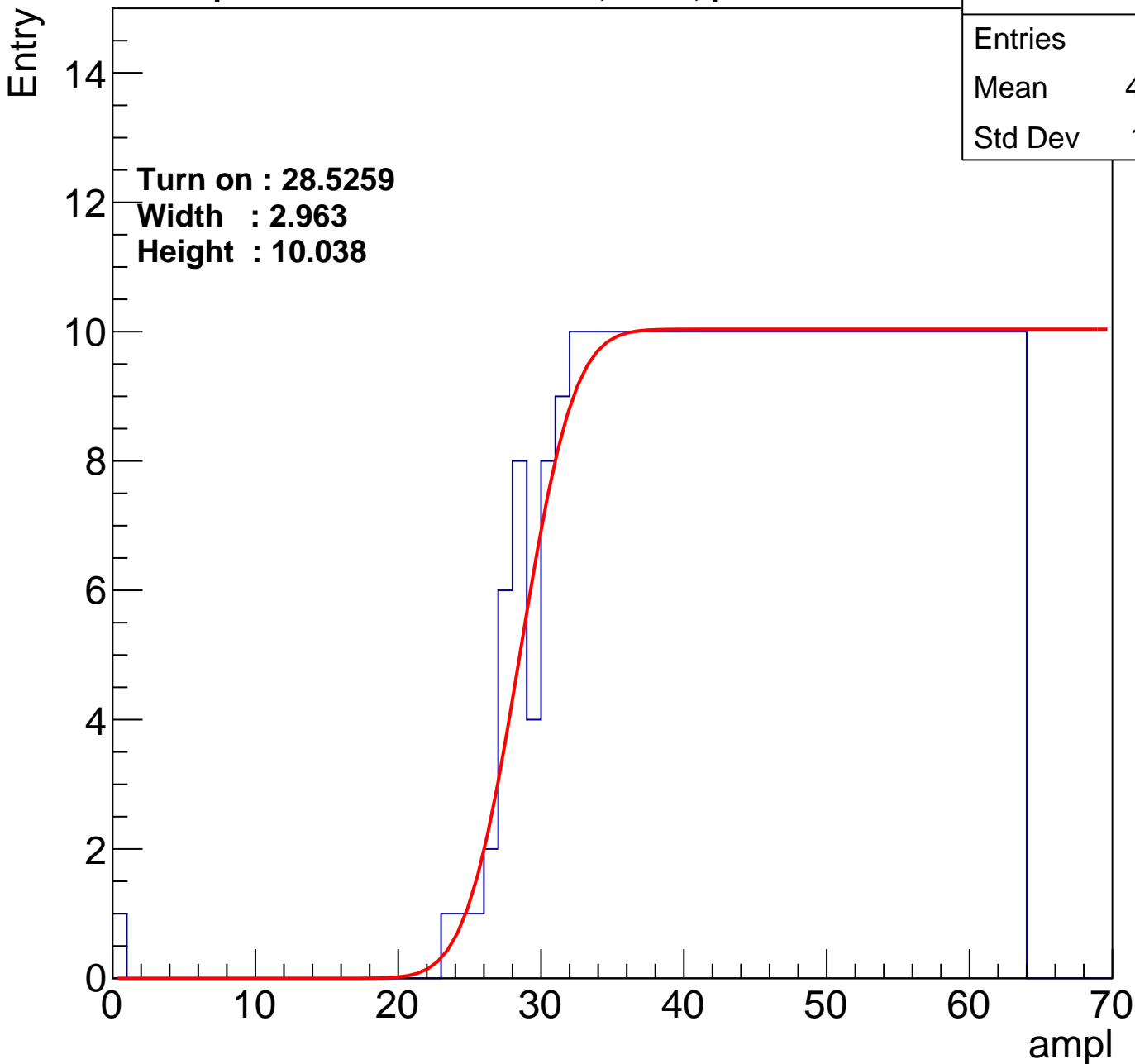
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	361
Mean	45.28
Std Dev	10.81

**Turn on : 28.5259**

**Width : 2.963**

**Height : 10.038**



# B0L000S, U26-ch52

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.92
Std Dev	11.15

Turn on : 27.5323

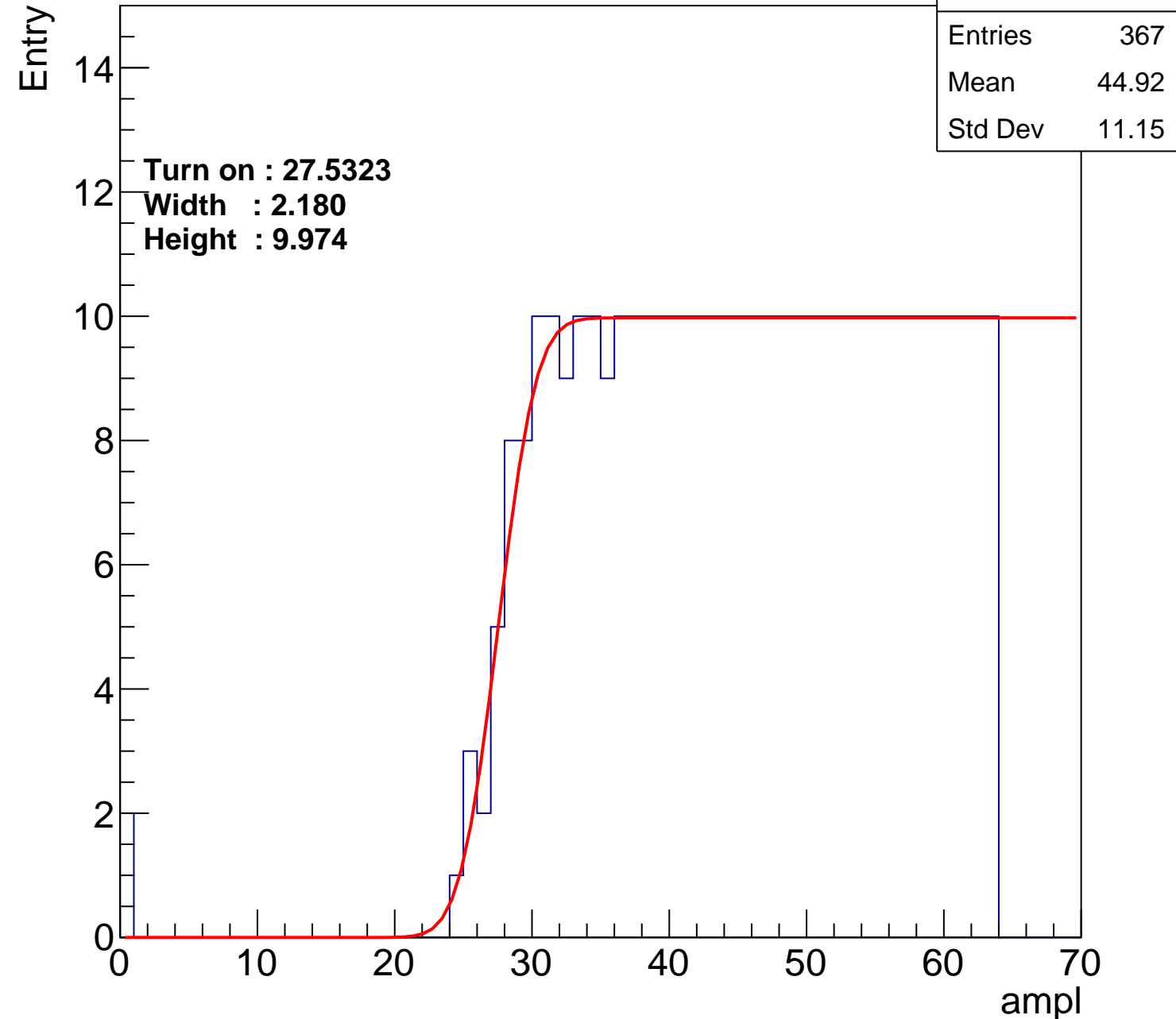
Width : 2.180

Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

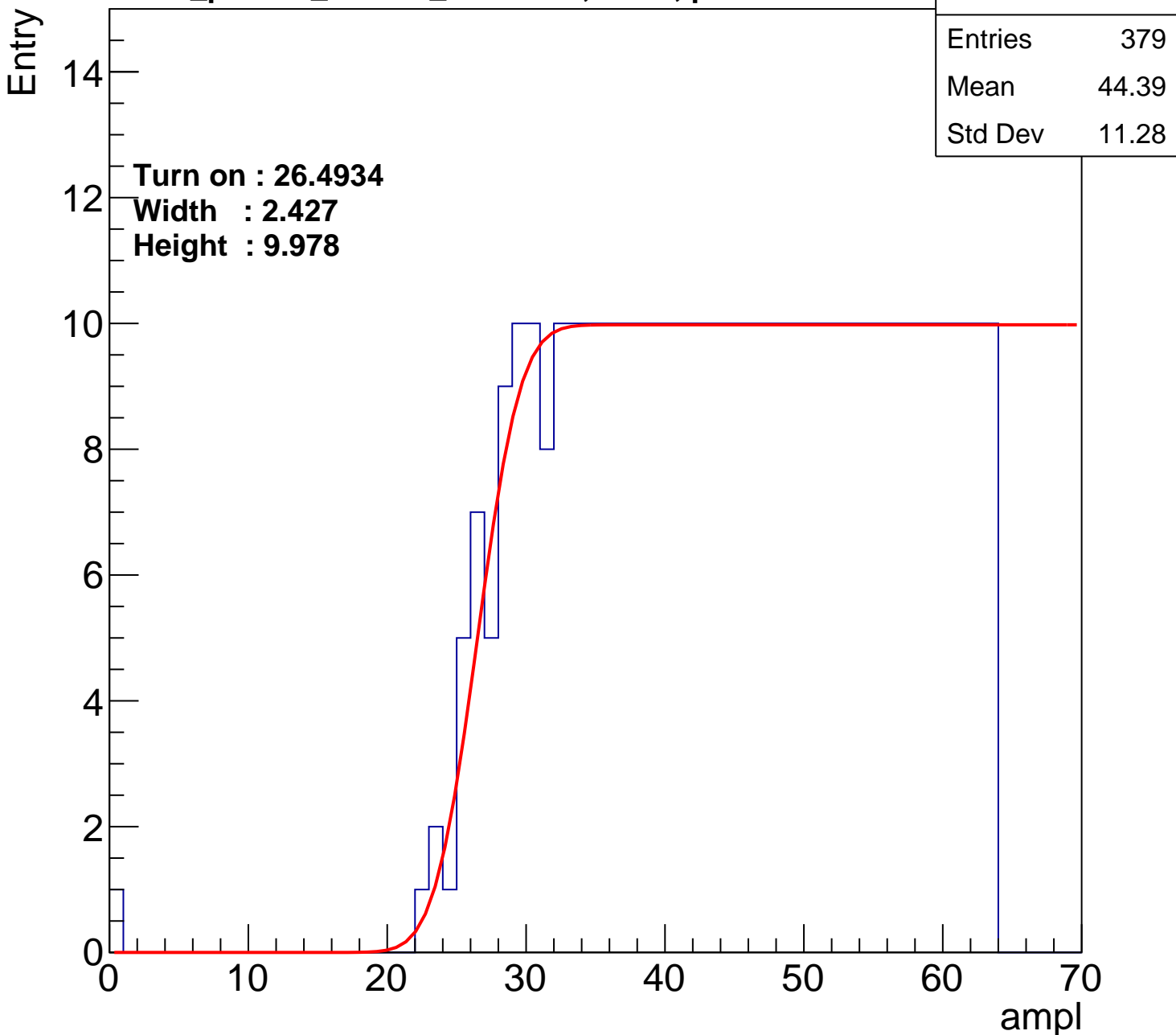
ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**Turn on : 26.4934**  
**Width : 2.427**  
**Height : 9.978**



# B0L000S, U26-ch54

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 26.7397

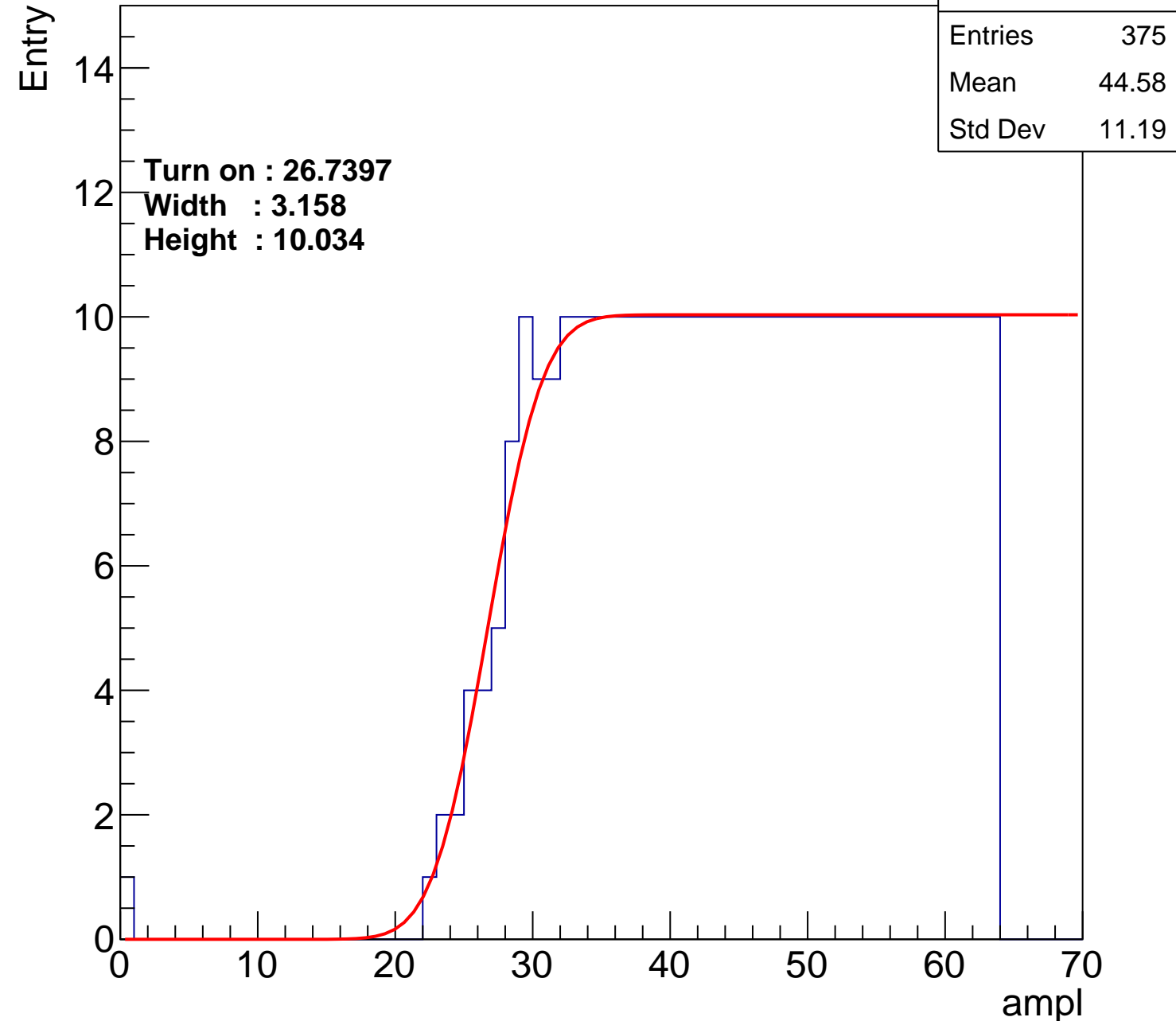
Width : 3.158

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch55

calib\_packv5\_042523\_0143.root, FC#5, port B1

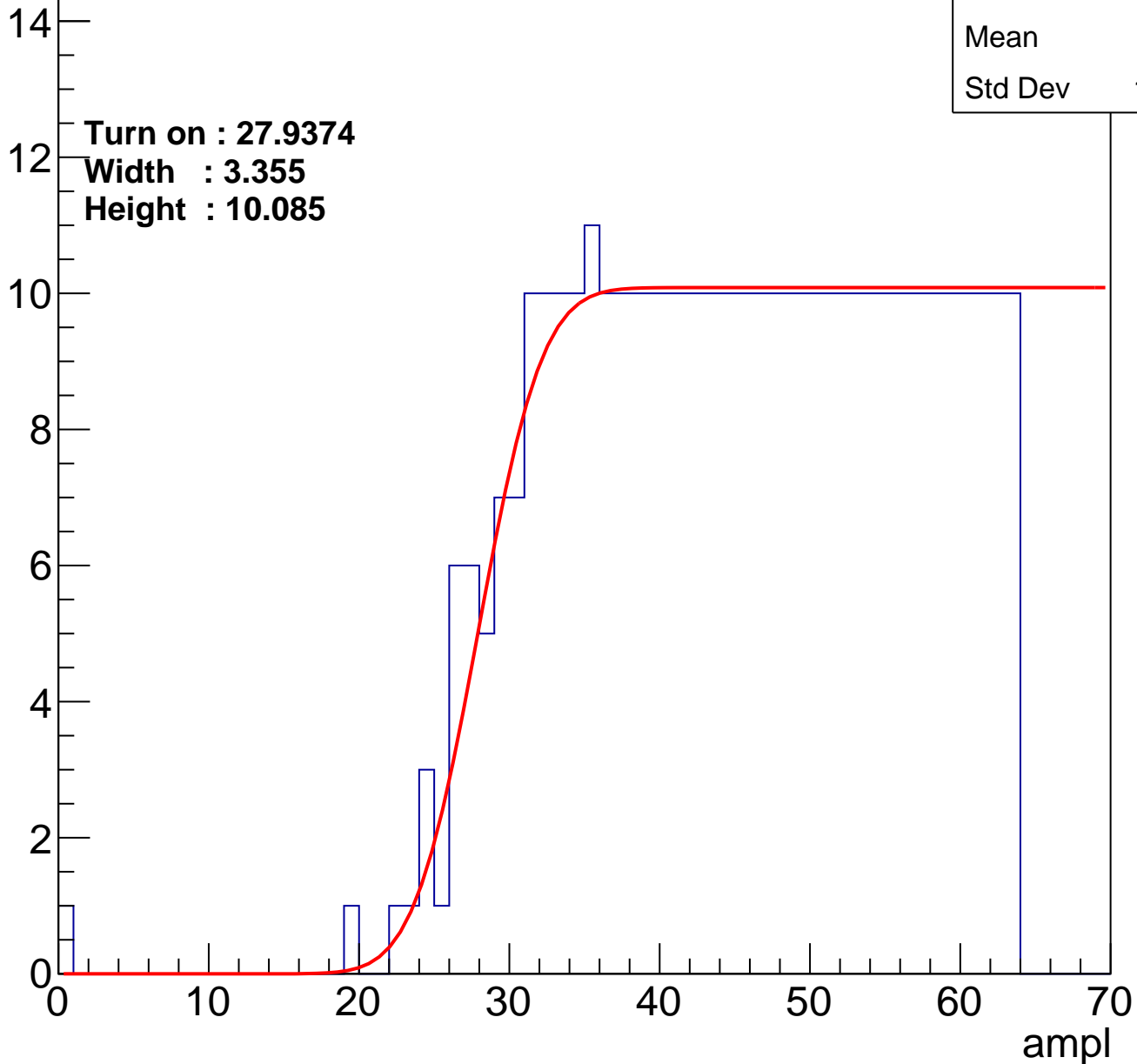
Entries	370
Mean	44.8
Std Dev	11.11

Turn on : 27.9374

Width : 3.355

Height : 10.085

Entry



# B0L000S, U26-ch56

calib\_packv5\_042523\_0143.root, FC#5, port B1

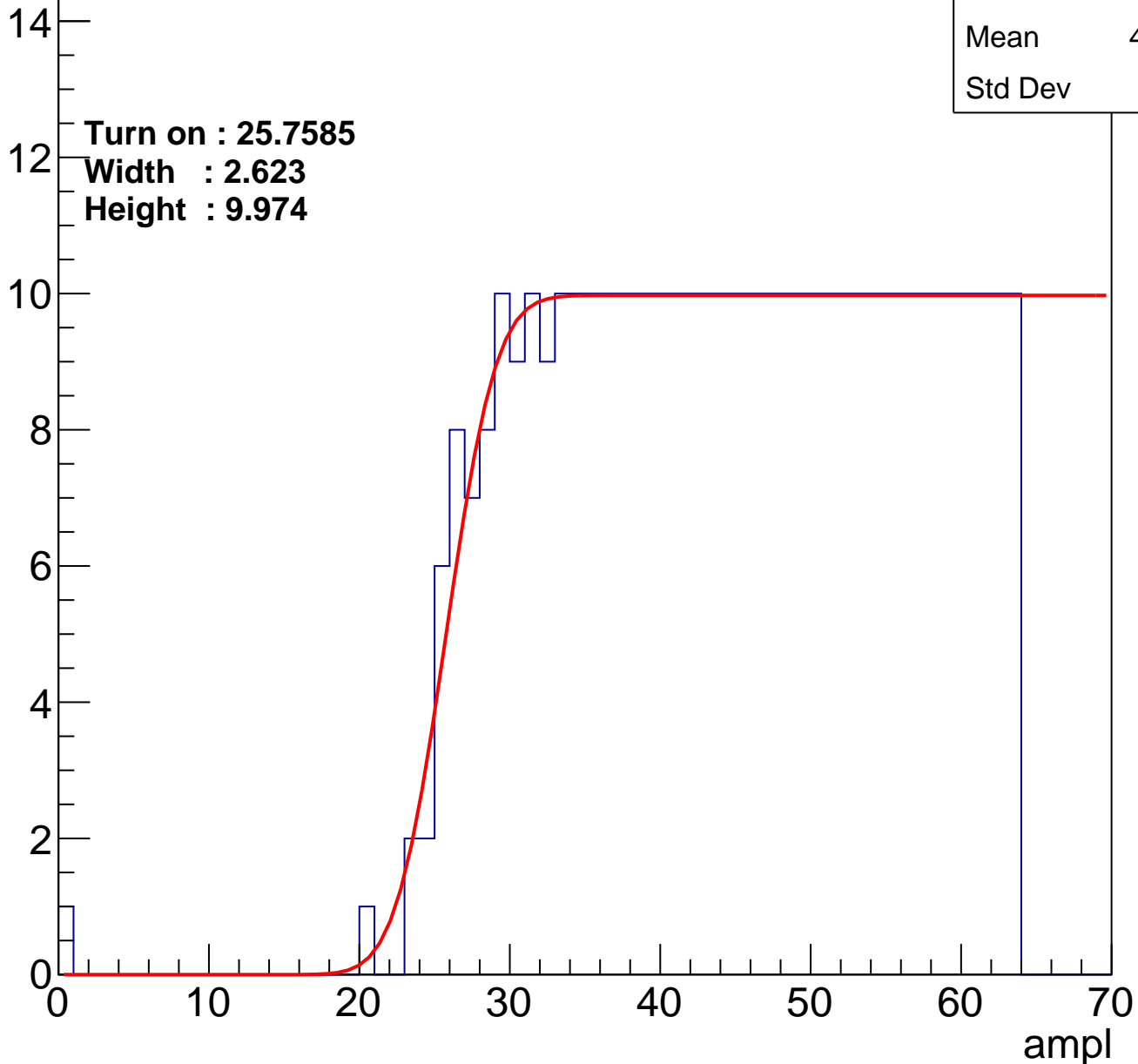
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.7585

Width : 2.623

Height : 9.974

Entry



# B0L000S, U26-ch57

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.79
Std Dev	11.11

Turn on : 27.4173

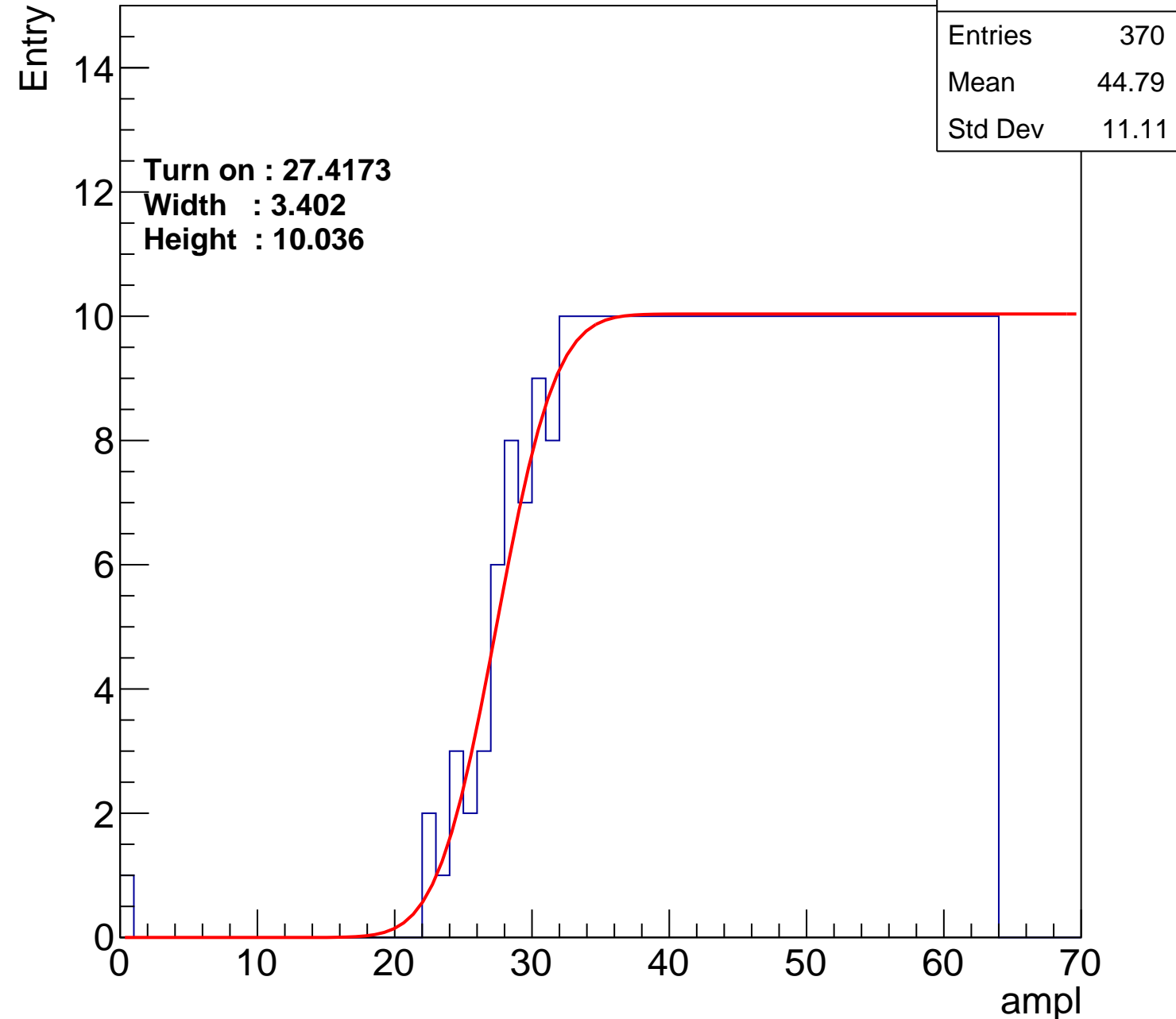
Width : 3.402

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch58

calib\_packv5\_042523\_0143.root, FC#5, port B1

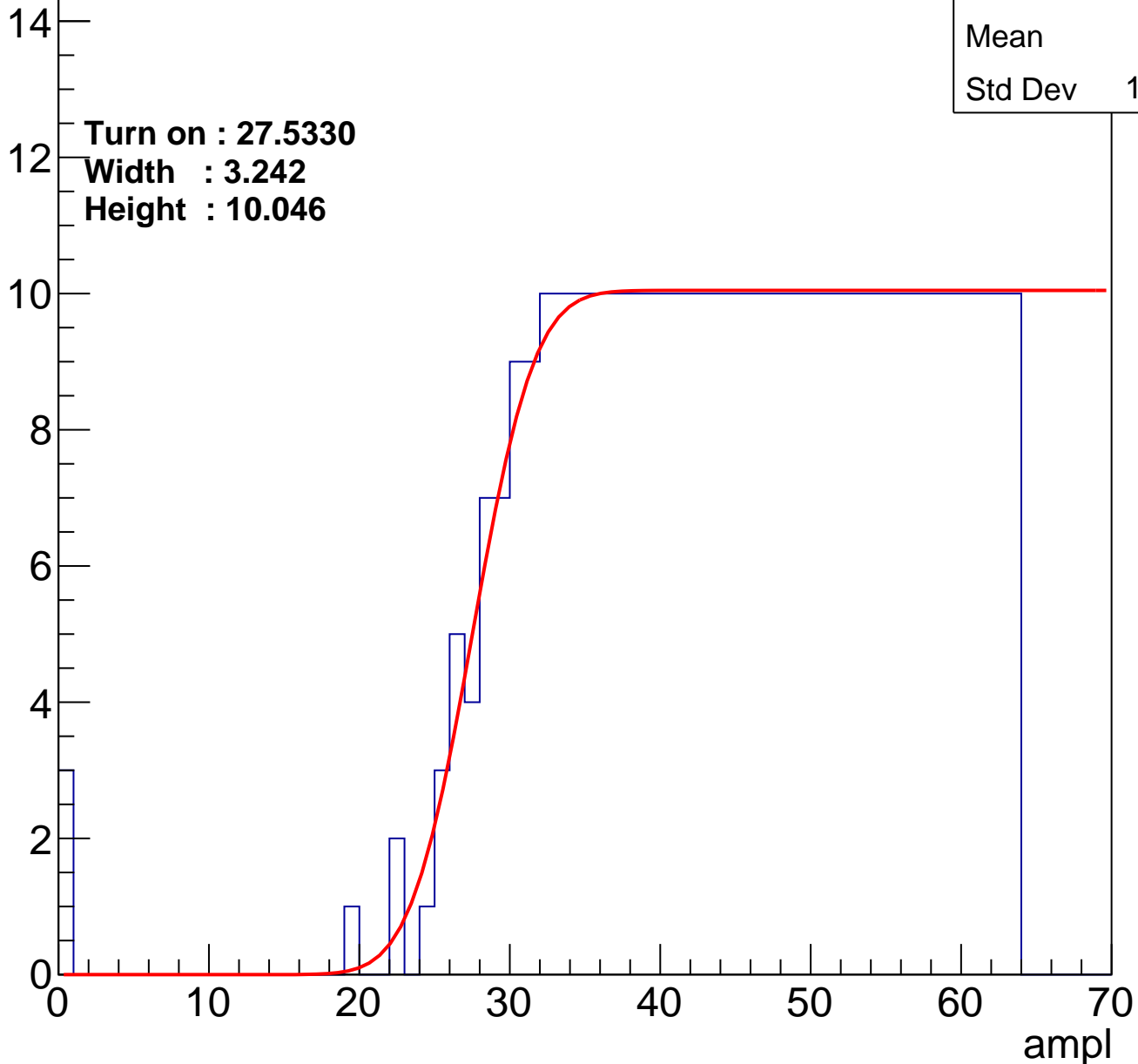
Entries	371
Mean	44.6
Std Dev	11.54

Turn on : 27.5330

Width : 3.242

Height : 10.046

Entry



# B0L000S, U26-ch59

calib\_packv5\_042523\_0143.root, FC#5, port B1

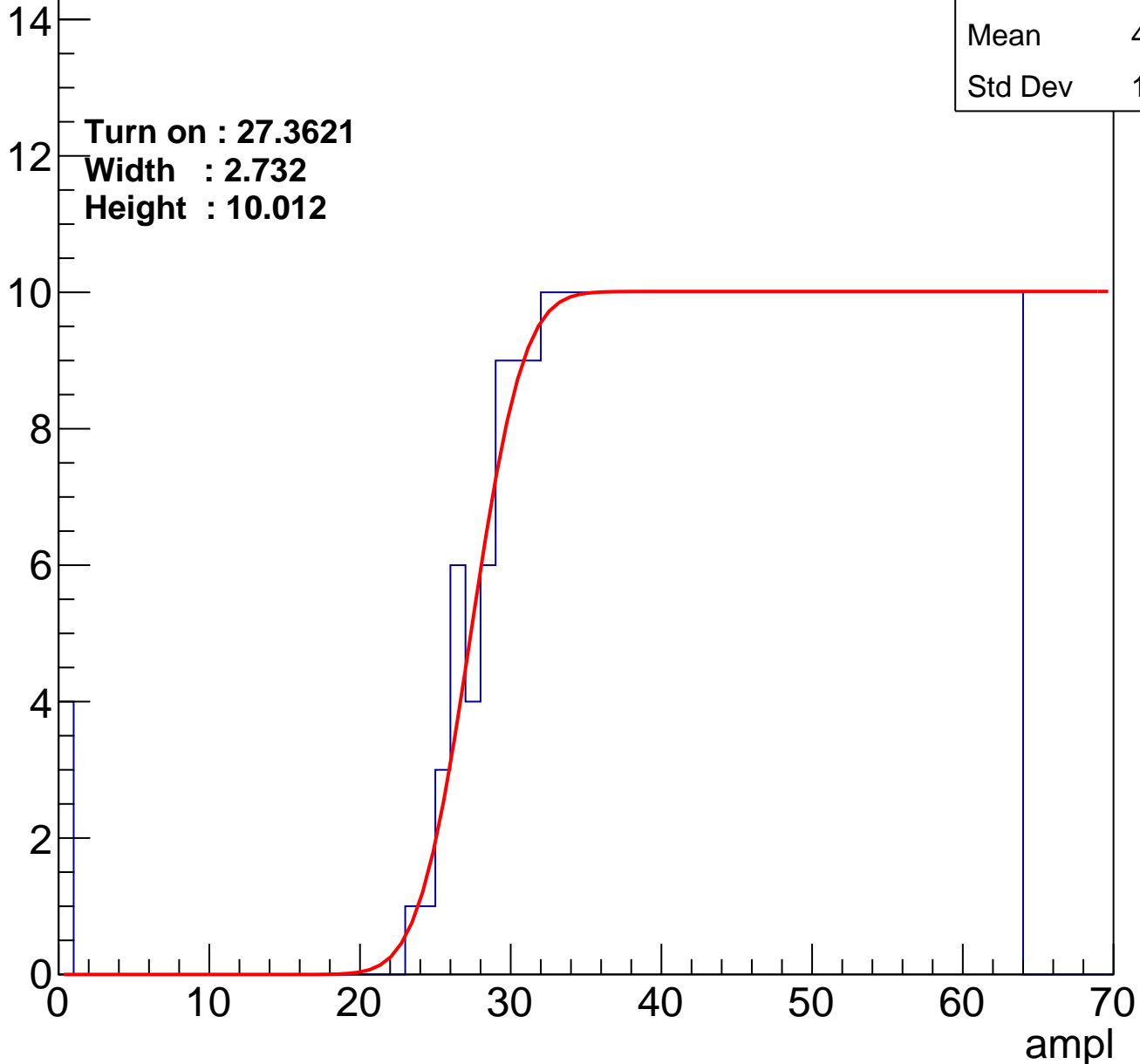
Entries	372
Mean	44.53
Std Dev	11.68

Turn on : 27.3621

Width : 2.732

Height : 10.012

Entry



# B0L000S, U26-ch60

calib\_packv5\_042523\_0143.root, FC#5, port B1

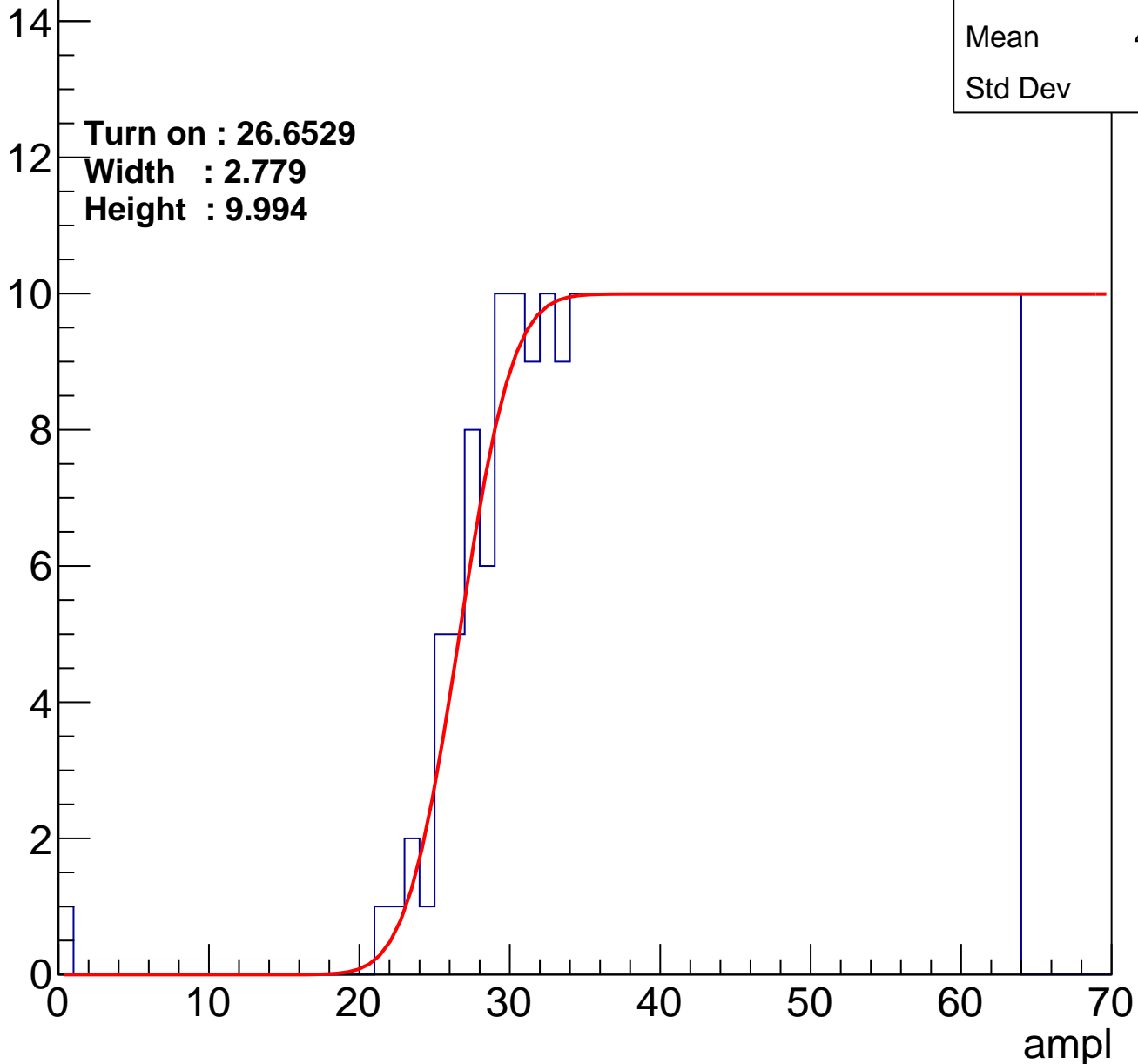
Entries	378
Mean	44.41
Std Dev	11.3

Turn on : 26.6529

Width : 2.779

Height : 9.994

Entry



# B0L000S, U26-ch61

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.55
Std Dev	11.55

**Turn on : 27.2977**

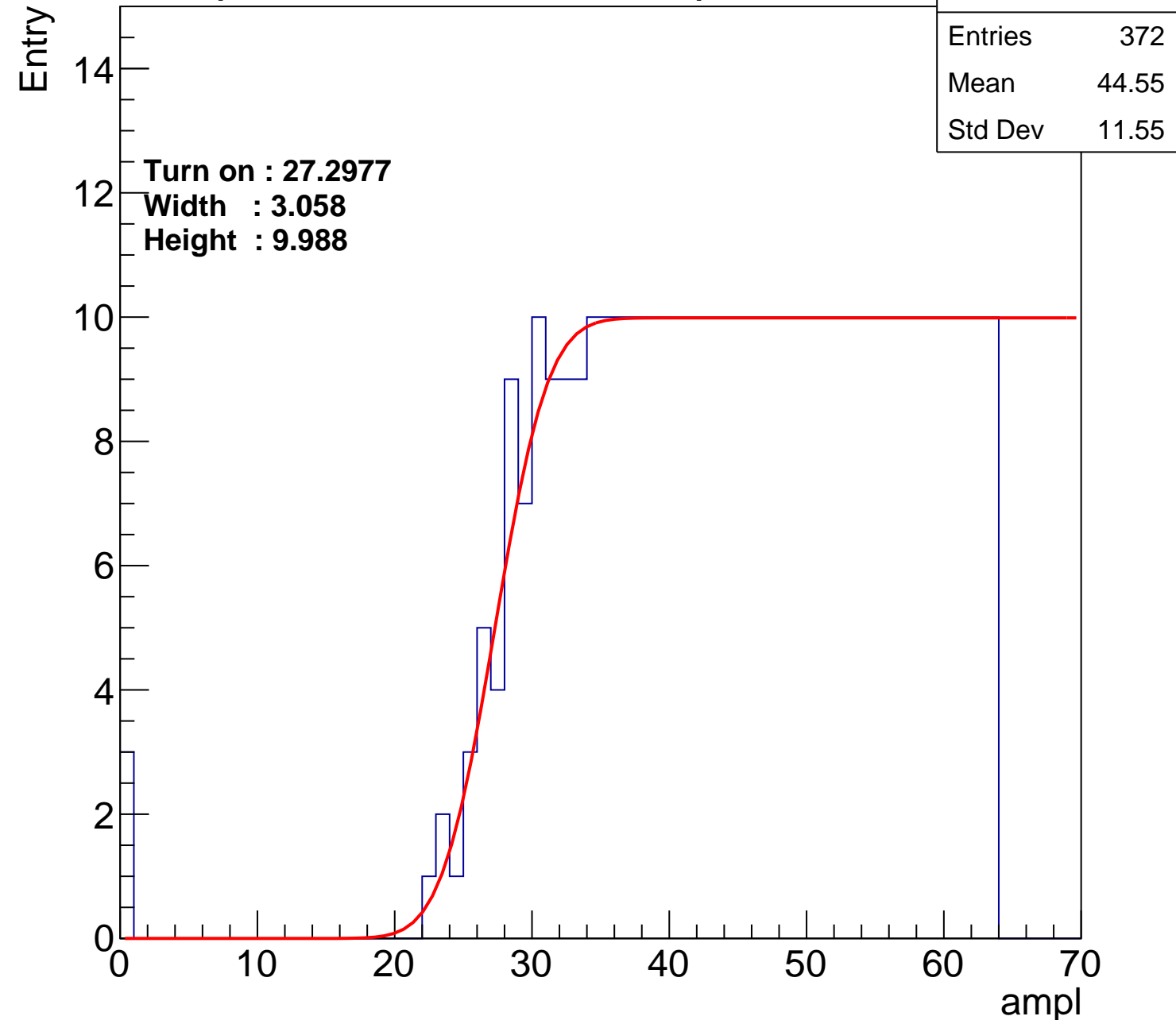
**Width : 3.058**

**Height : 9.988**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch62

calib\_packv5\_042523\_0143.root, FC#5, port B1

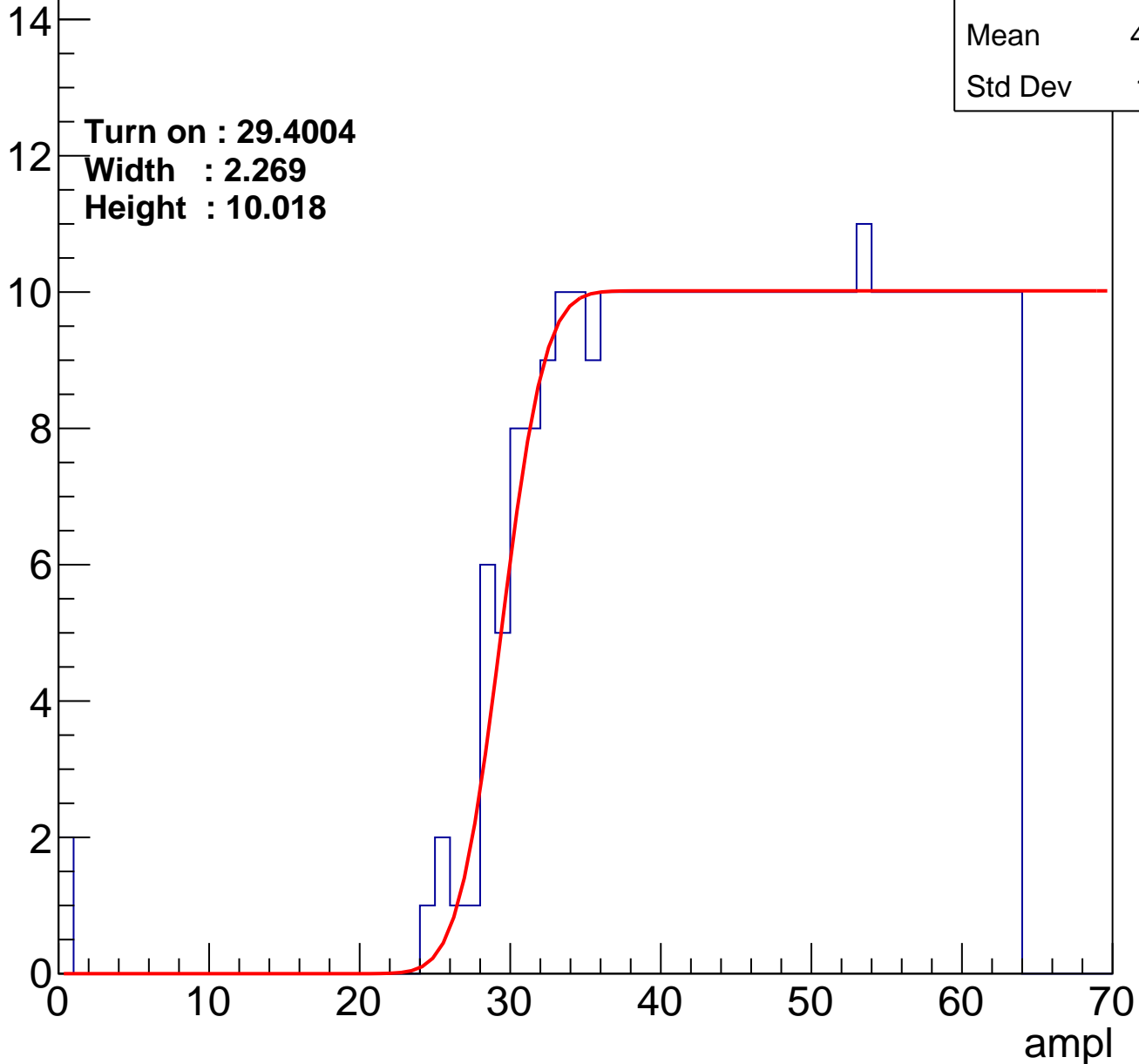
Entries	353
Mean	45.65
Std Dev	10.81

**Turn on : 29.4004**

**Width : 2.269**

**Height : 10.018**

Entry





# B0L000S, U26-ch63

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.4
Std Dev	11.58

**Turn on : 26.6811**

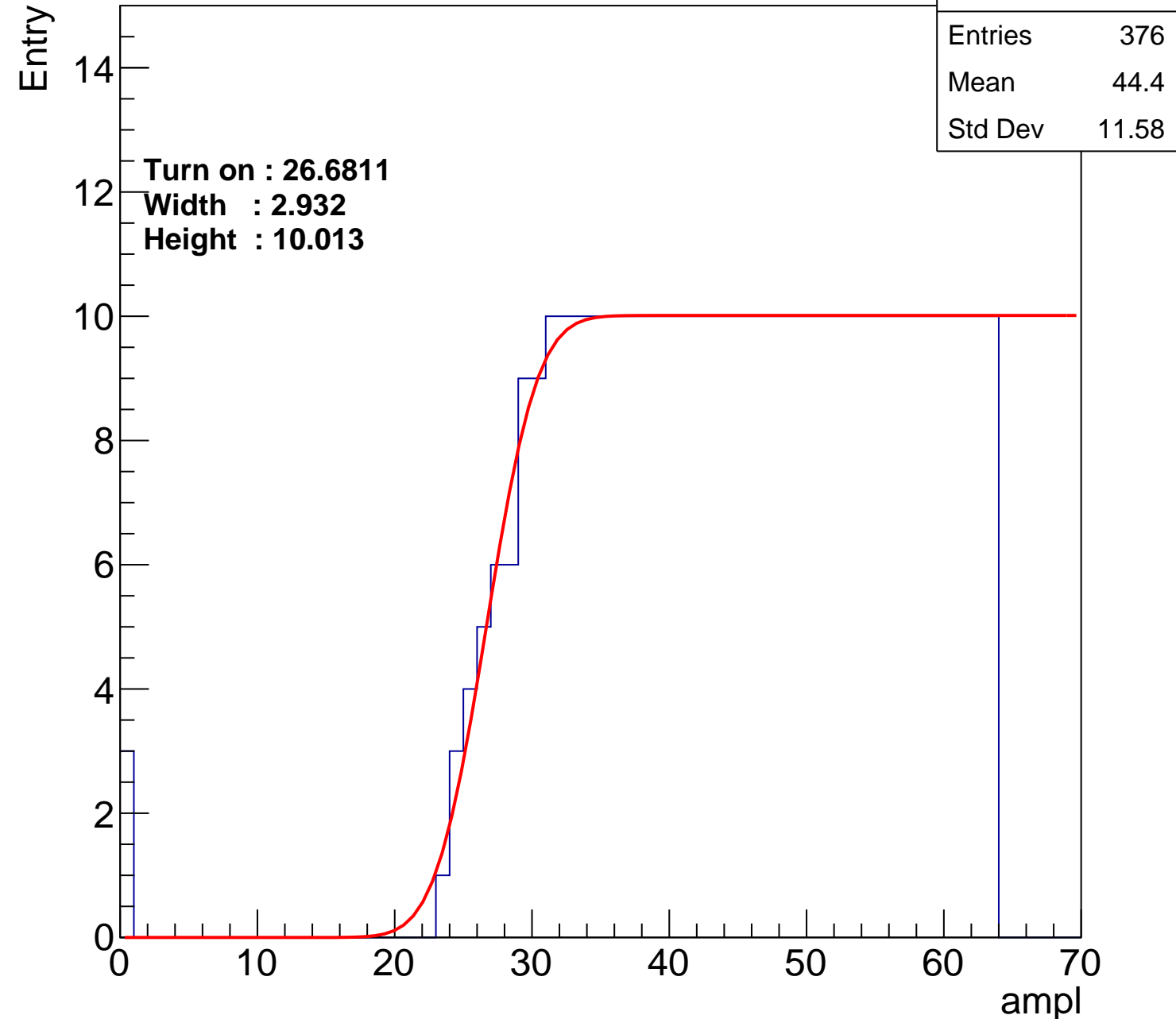
**Width : 2.932**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch64

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.19
Std Dev	11.58

Turn on : 26.0826

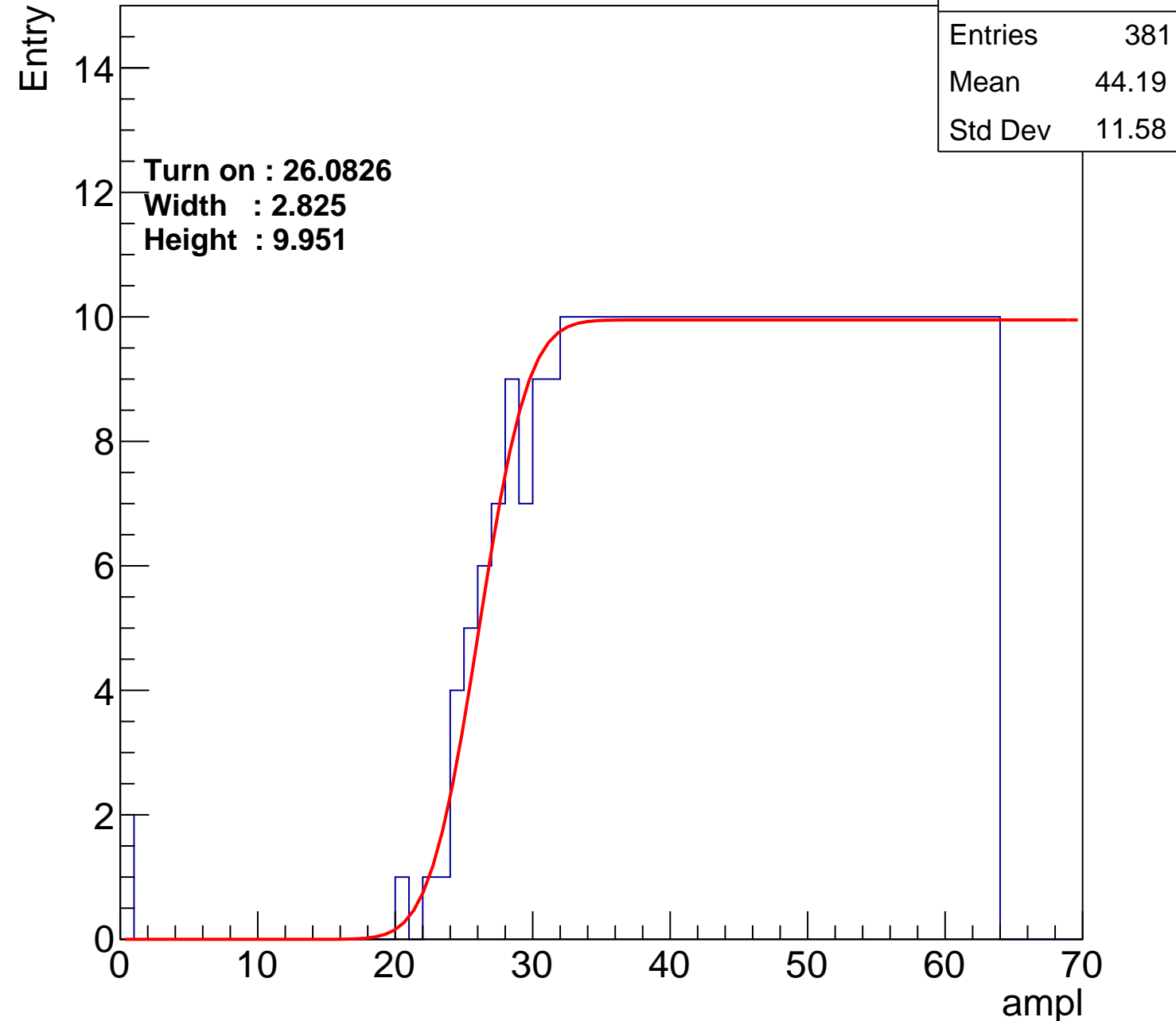
Width : 2.825

Height : 9.951

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch65

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	45.03
Std Dev	10.99

Turn on : 28.1408

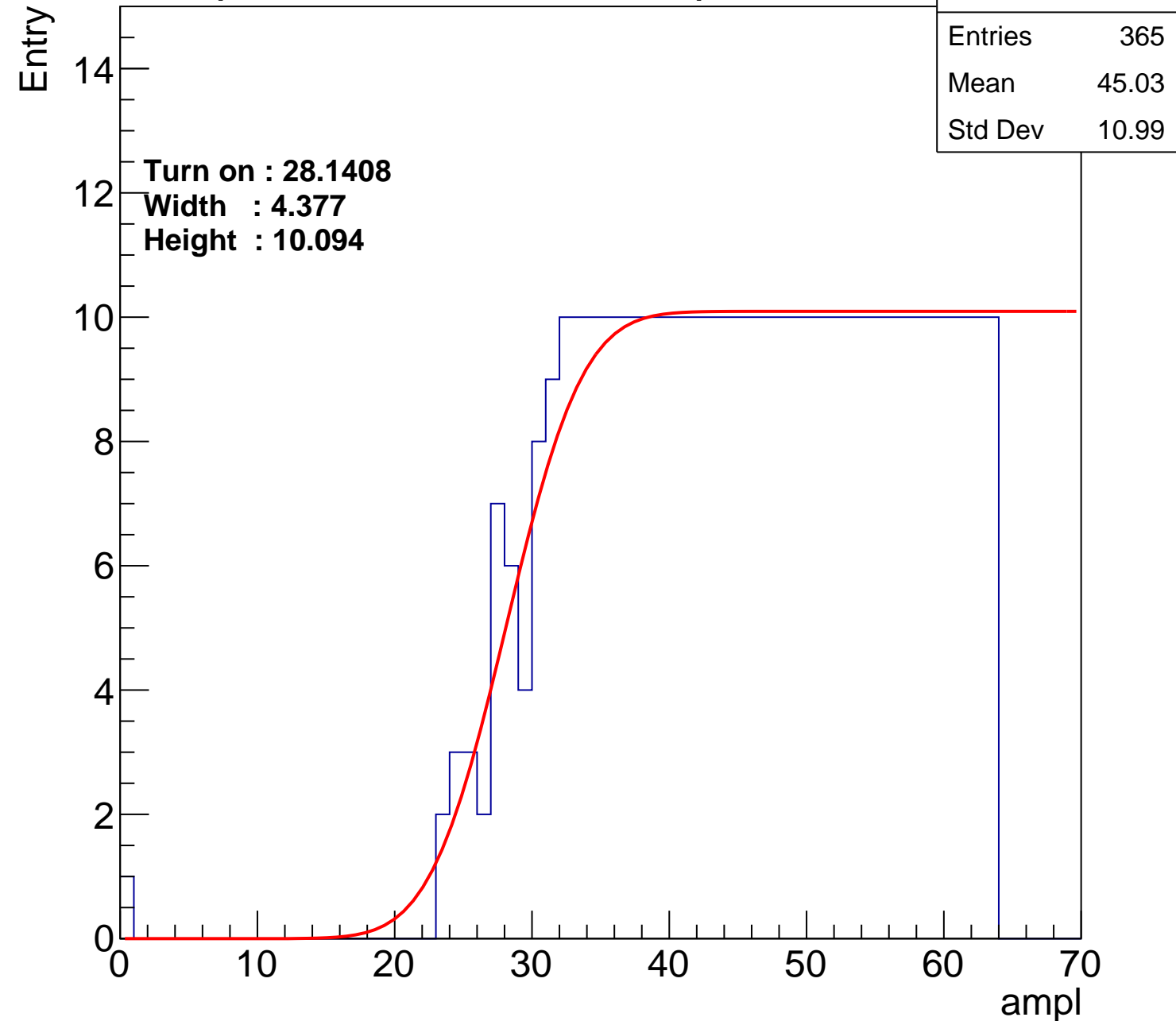
Width : 4.377

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch66

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.47
Std Dev	11.21

**Turn on : 26.5157**

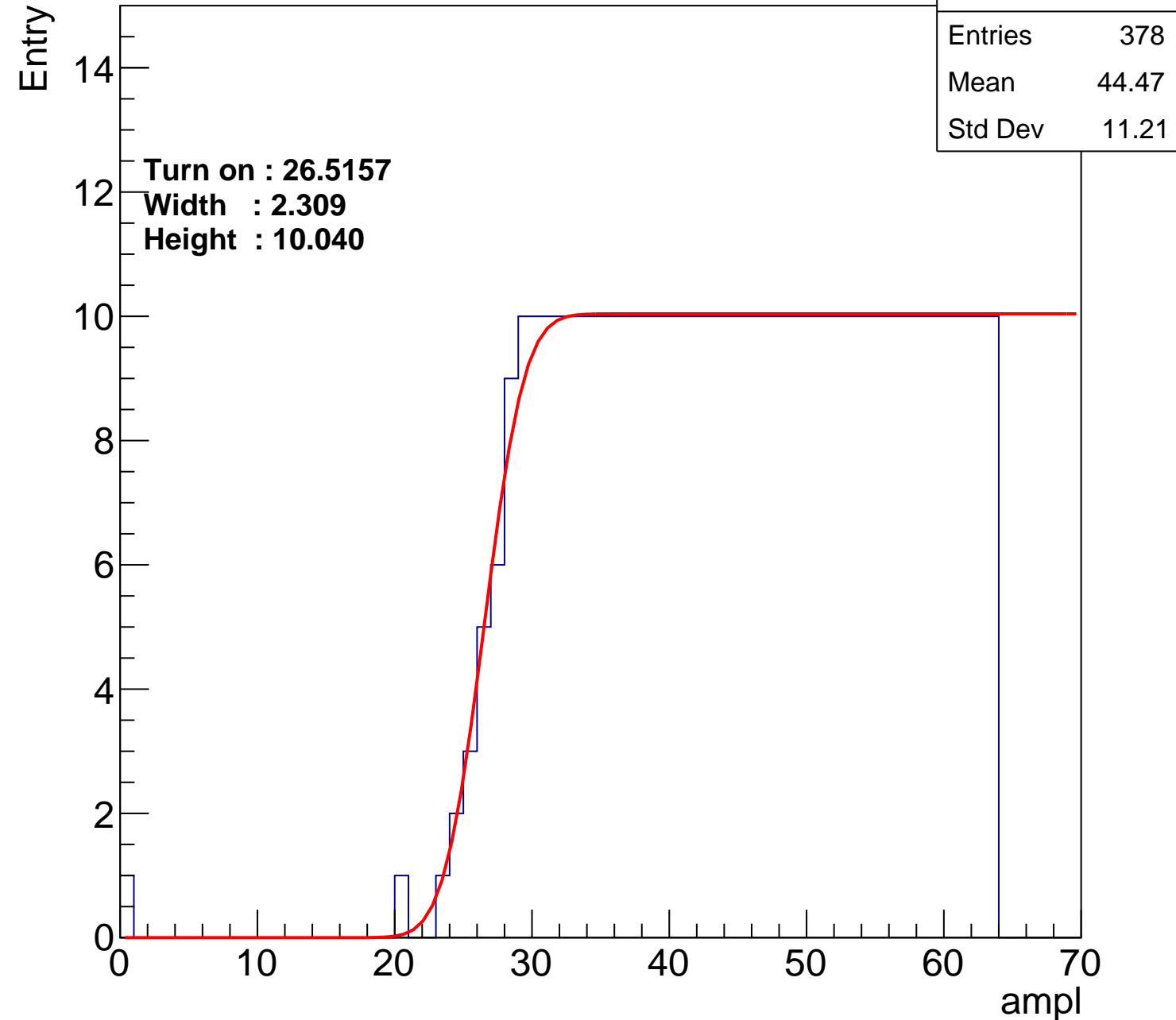
**Width : 2.309**

**Height : 10.040**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**B0L000S, U26-ch67**

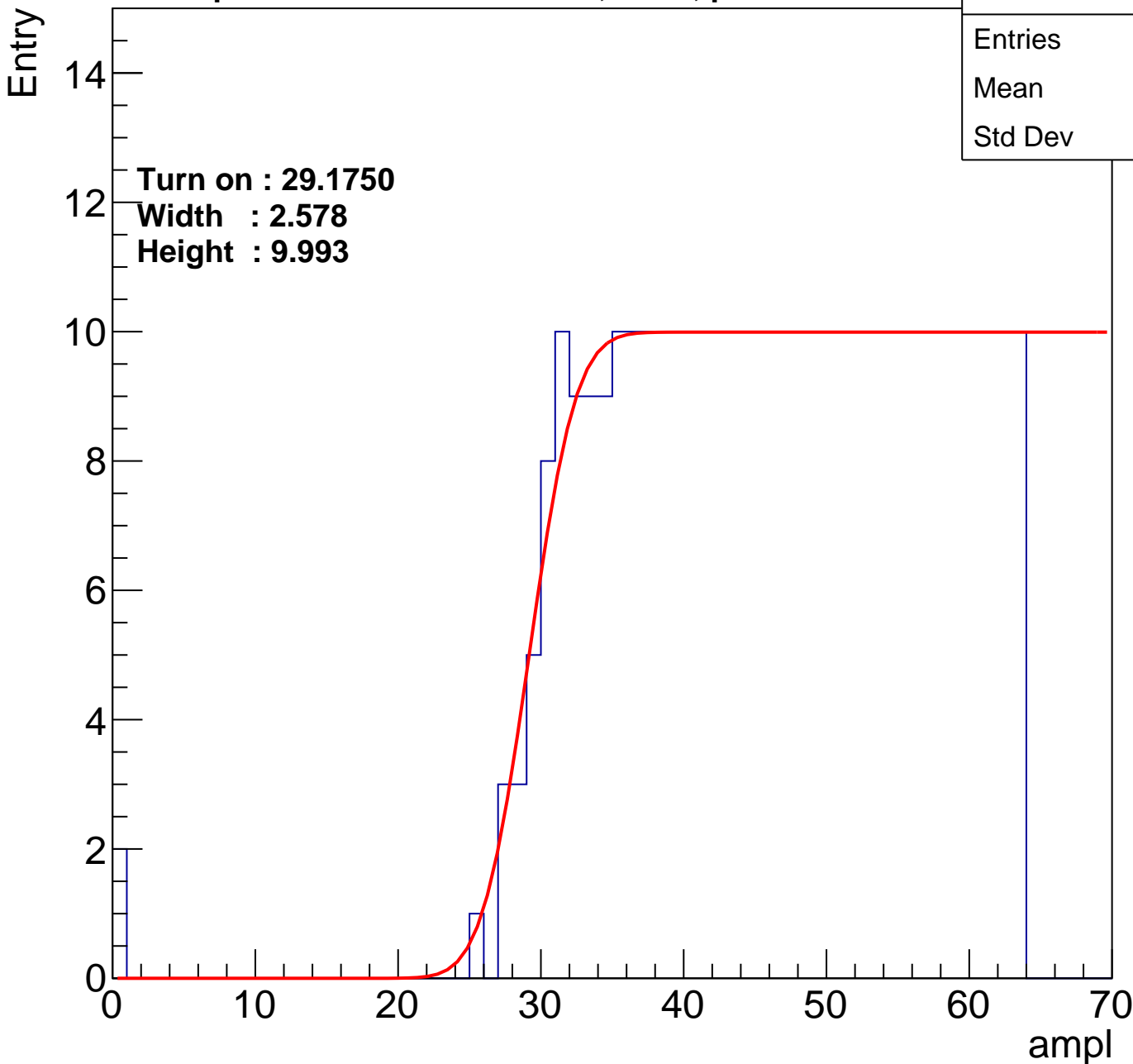
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	349
Mean	45.81
Std Dev	10.7

**Turn on : 29.1750**

**Width : 2.578**

**Height : 9.993**



# B0L000S, U26-ch68

calib\_packv5\_042523\_0143.root, FC#5, port B1

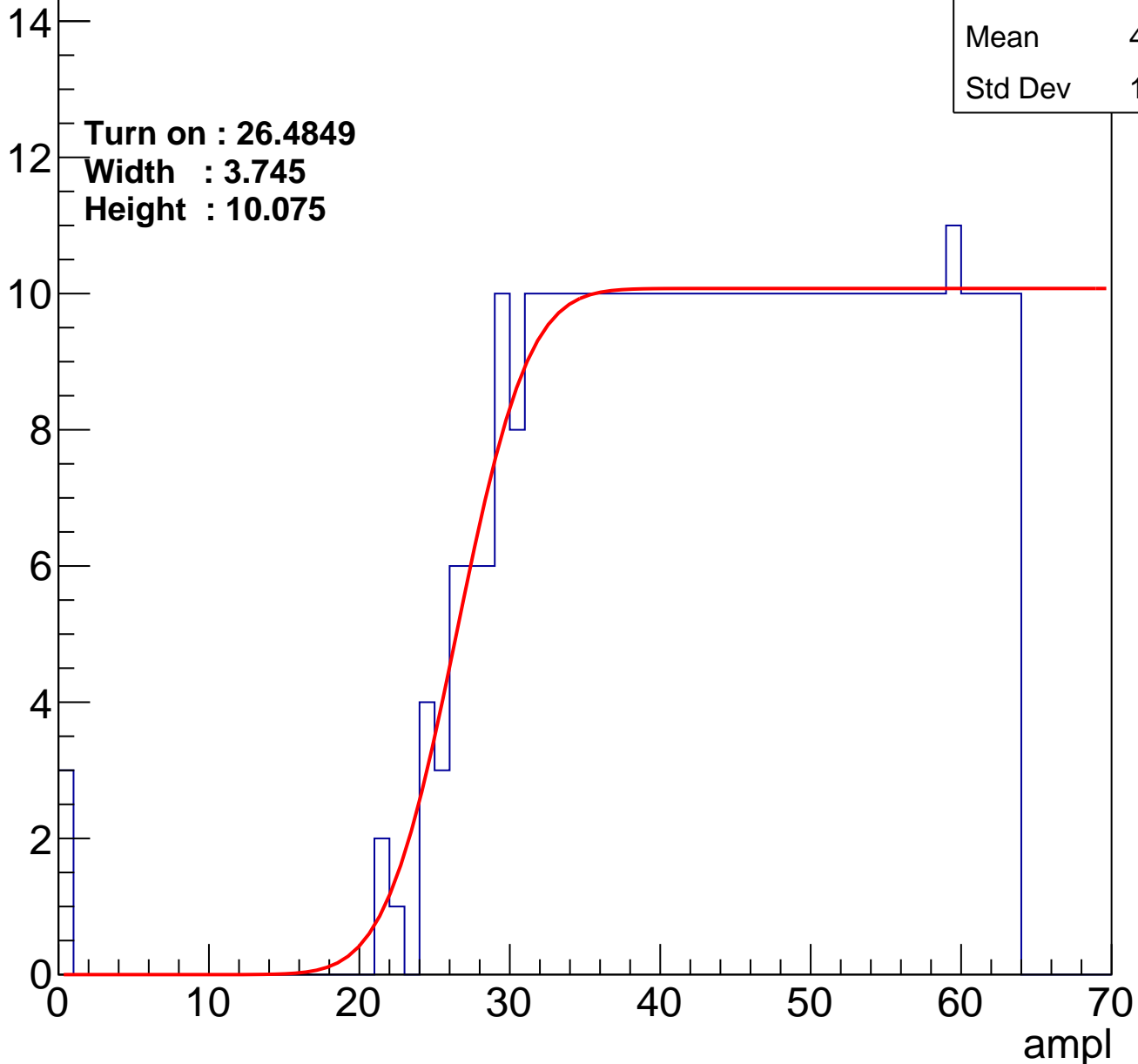
Entries	380
Mean	44.26
Std Dev	11.72

Turn on : 26.4849

Width : 3.745

Height : 10.075

Entry



# B0L000S, U26-ch69

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.43
Std Dev	12.08

Turn on : 28.2100

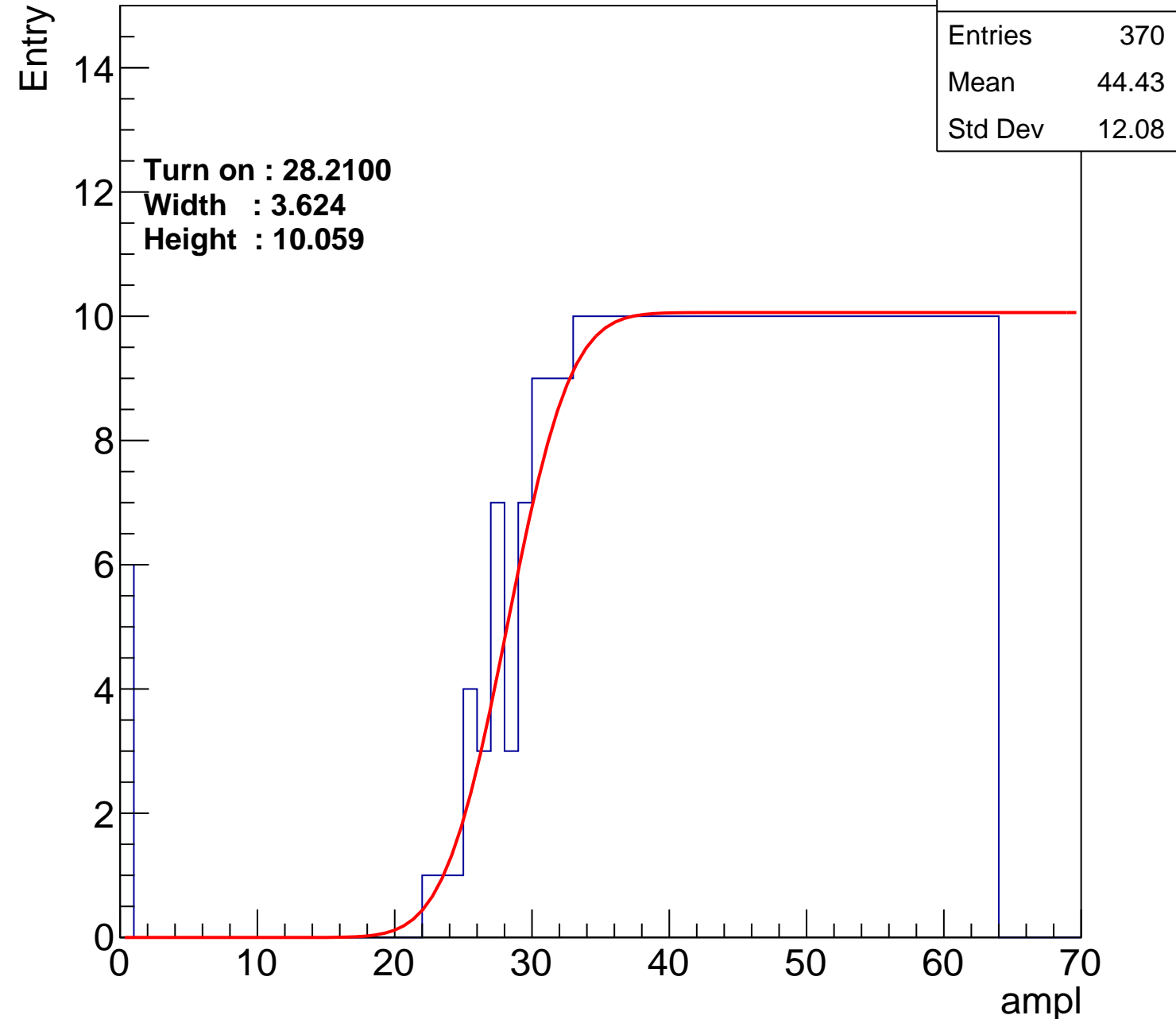
Width : 3.624

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch70

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	44.91
Std Dev	11.37

Turn on : 27.6028

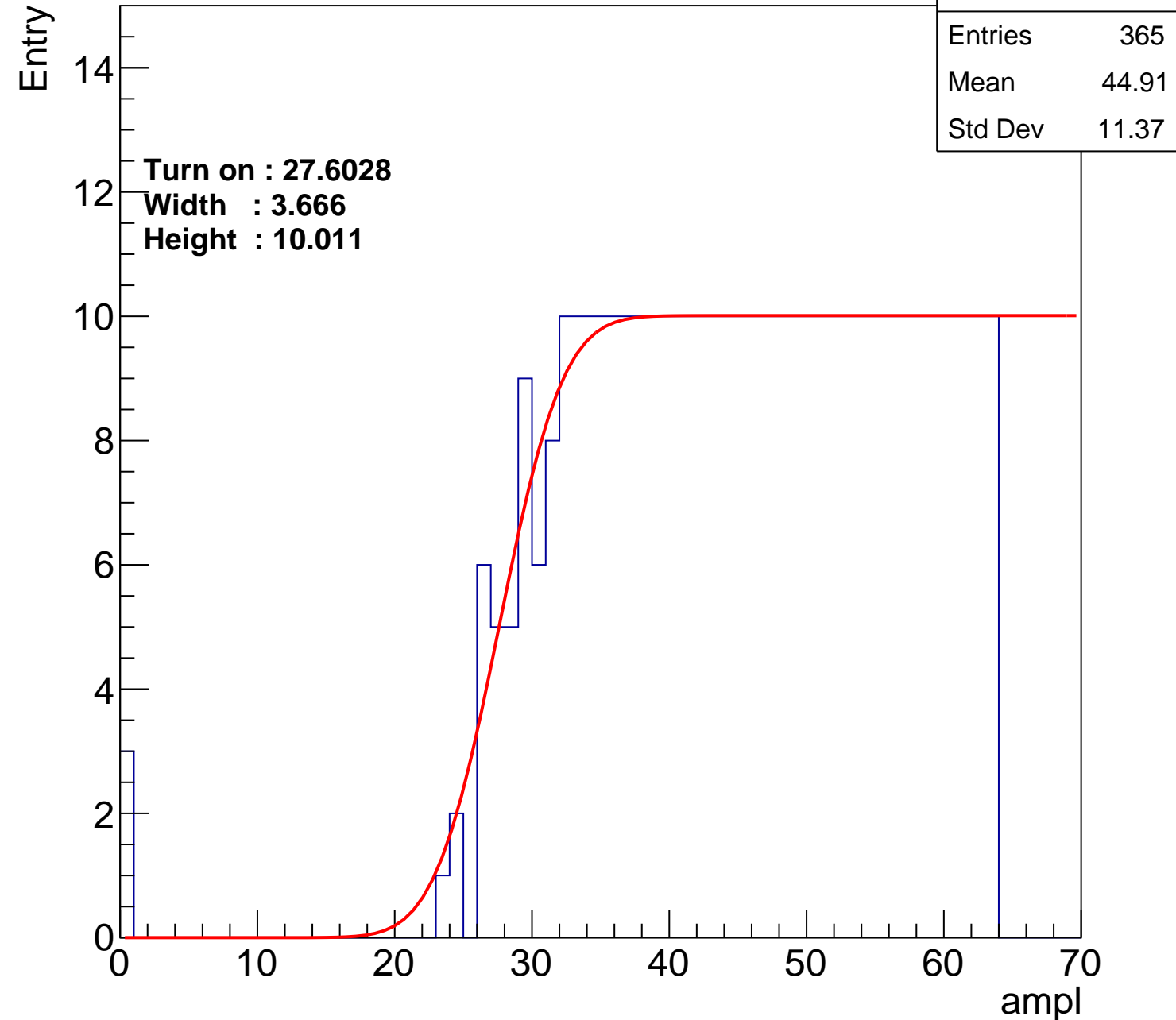
Width : 3.666

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch71

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 26.7597

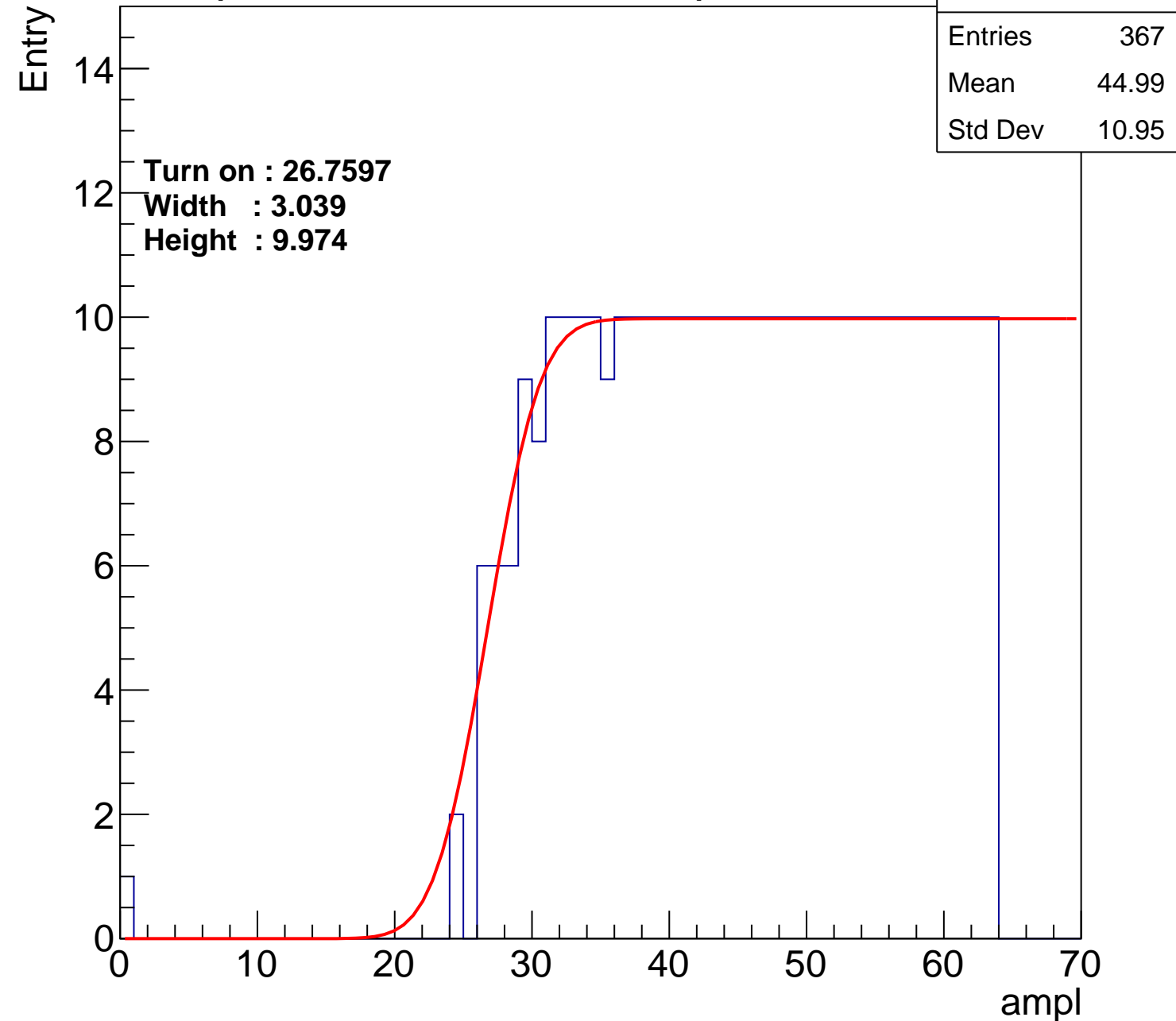
Width : 3.039

Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch72

calib\_packv5\_042523\_0143.root, FC#5, port B1

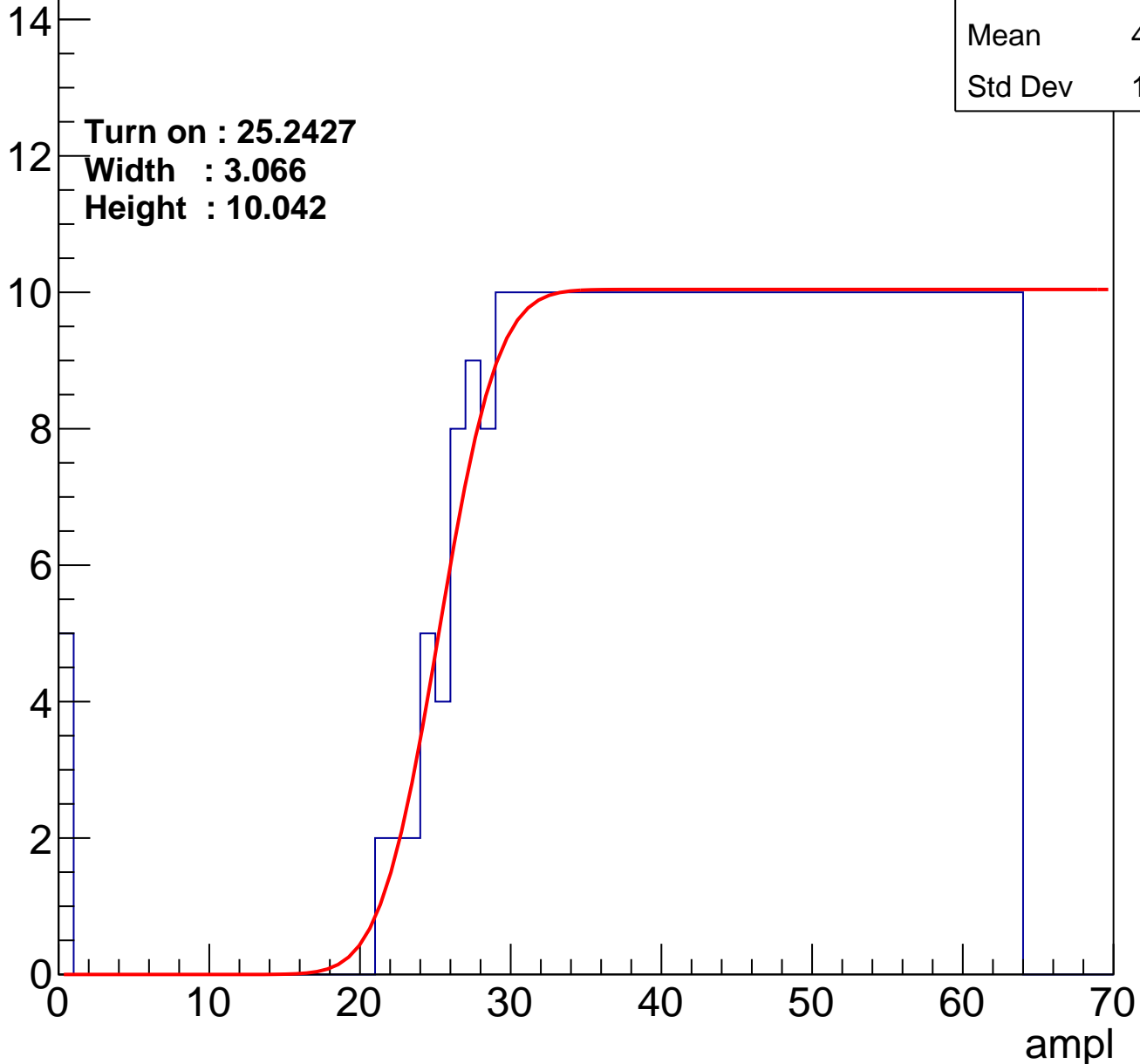
Entries	395
Mean	43.36
Std Dev	12.35

Turn on : 25.2427

Width : 3.066

Height : 10.042

Entry



# B0L000S, U26-ch73

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.55
Std Dev	11.37

Turn on : 26.9513

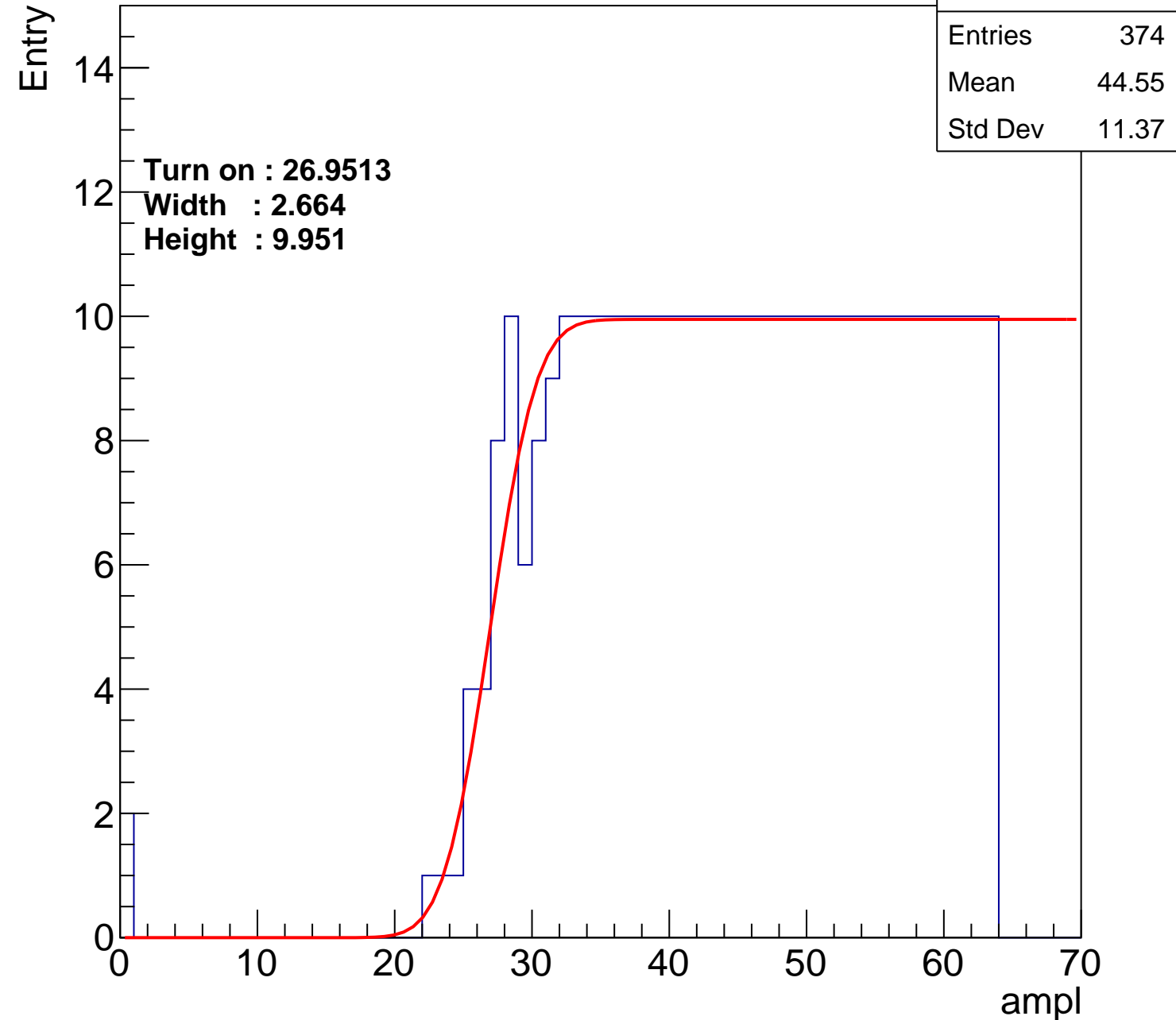
Width : 2.664

Height : 9.951

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch74

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	352
Mean	45.47
Std Dev	11.27

Turn on : 29.2143

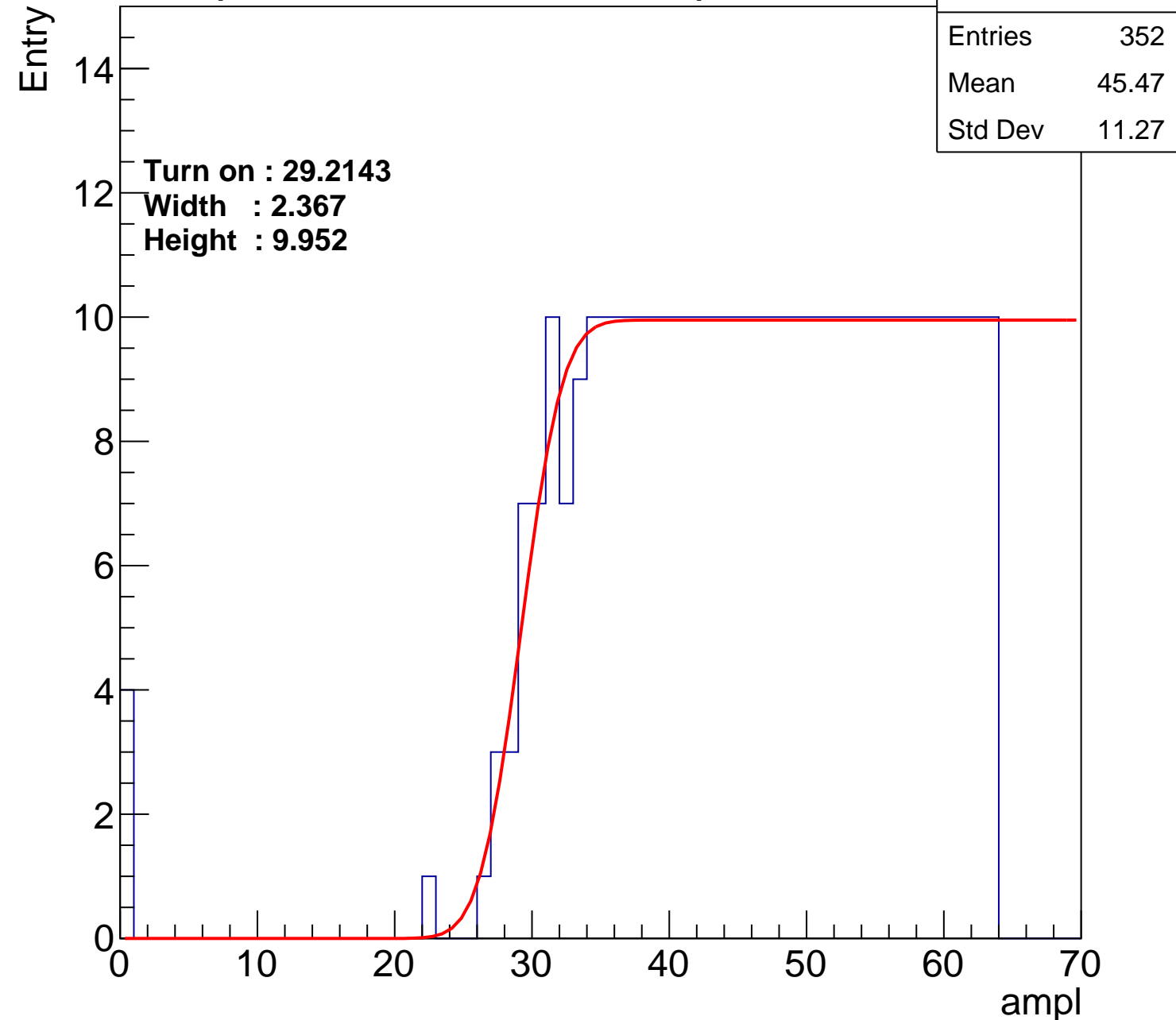
Width : 2.367

Height : 9.952

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch75

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	355
Mean	45.62
Std Dev	10.58

Turn on : 28.9647

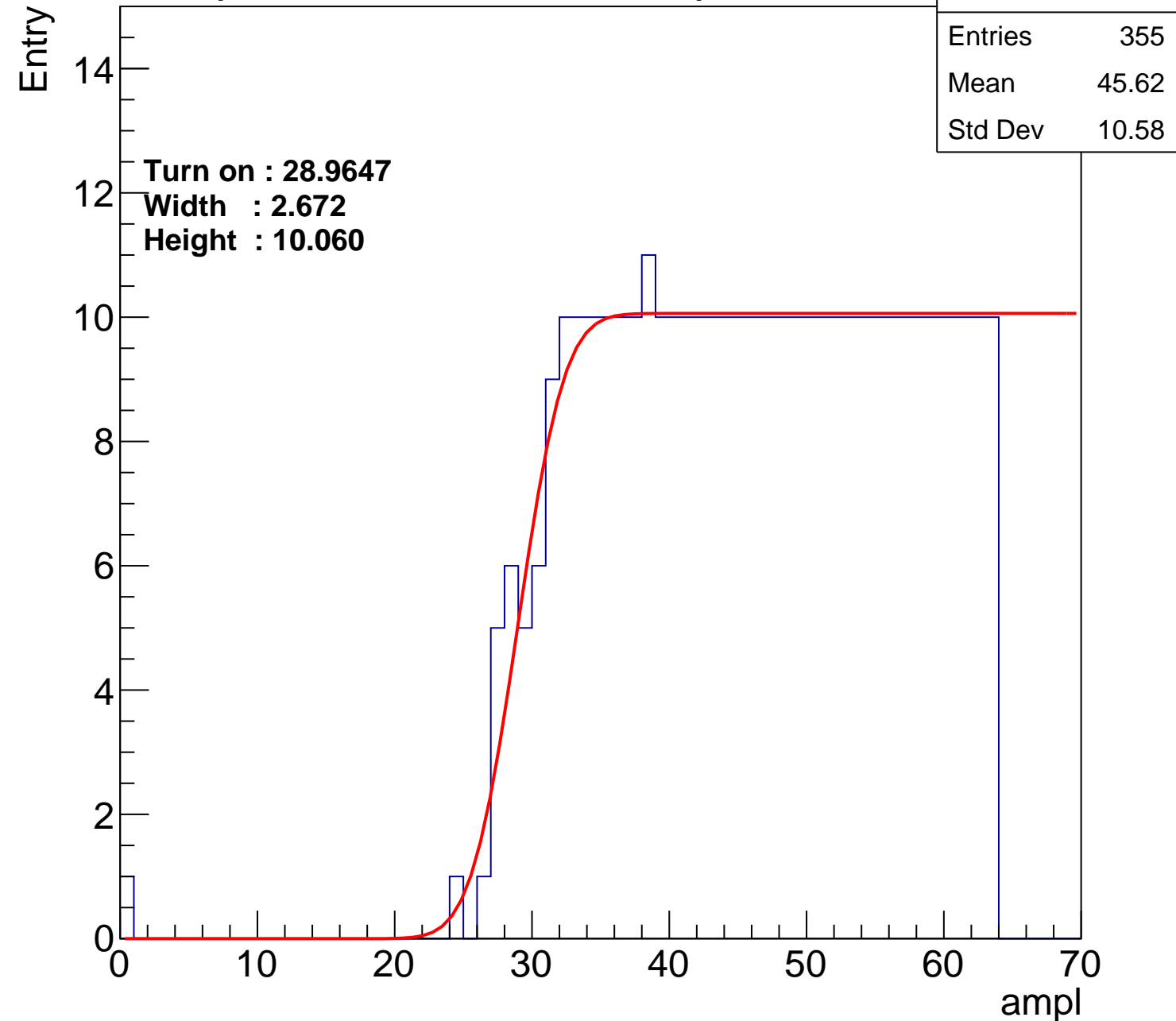
Width : 2.672

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch76

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.93
Std Dev	11.74

Turn on : 25.6604

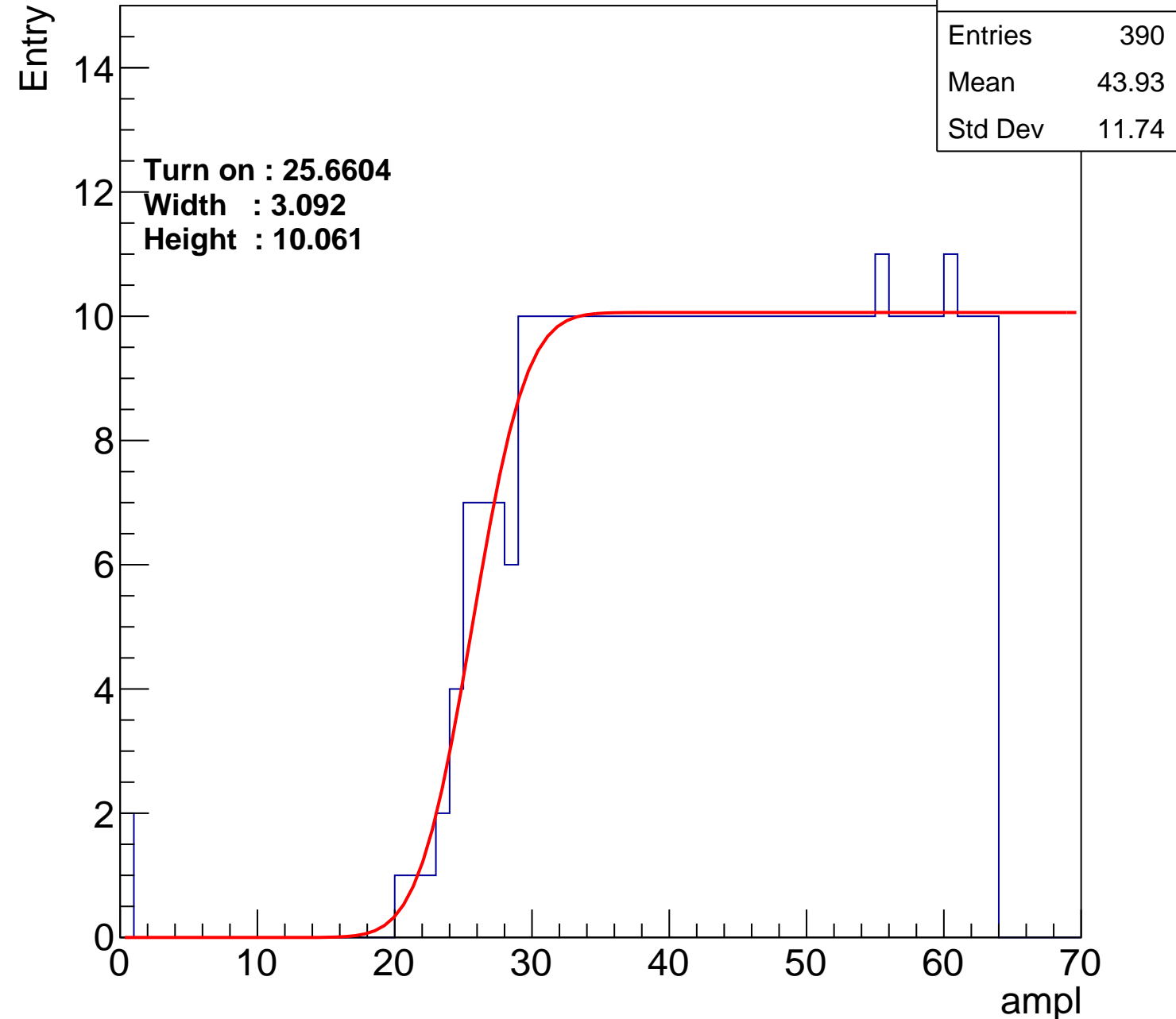
Width : 3.092

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch77

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	361
Mean	45.08
Std Dev	11.31

**Turn on : 28.7639**

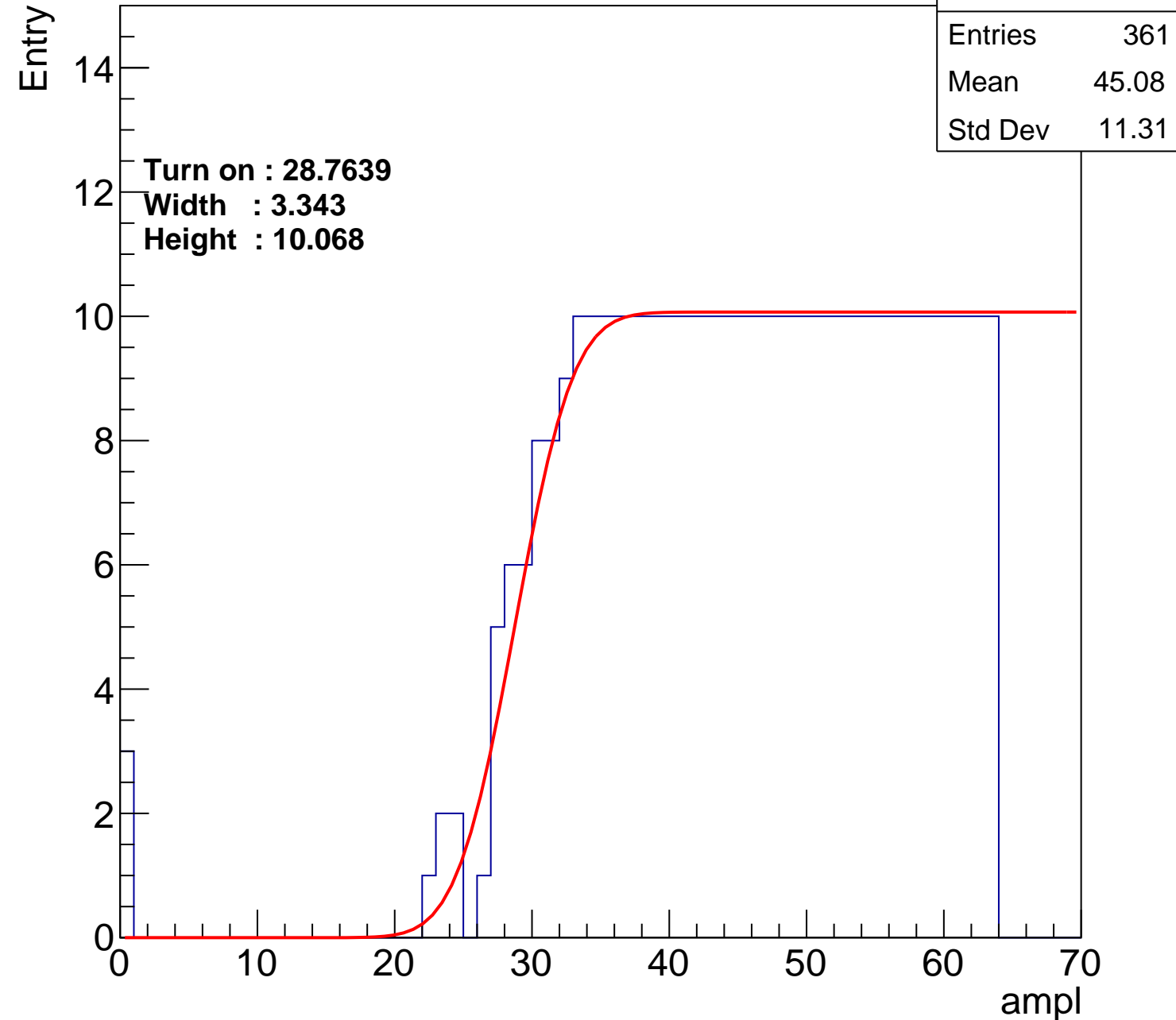
**Width : 3.343**

**Height : 10.068**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch78

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.98
Std Dev	11.03

Turn on : 27.5016

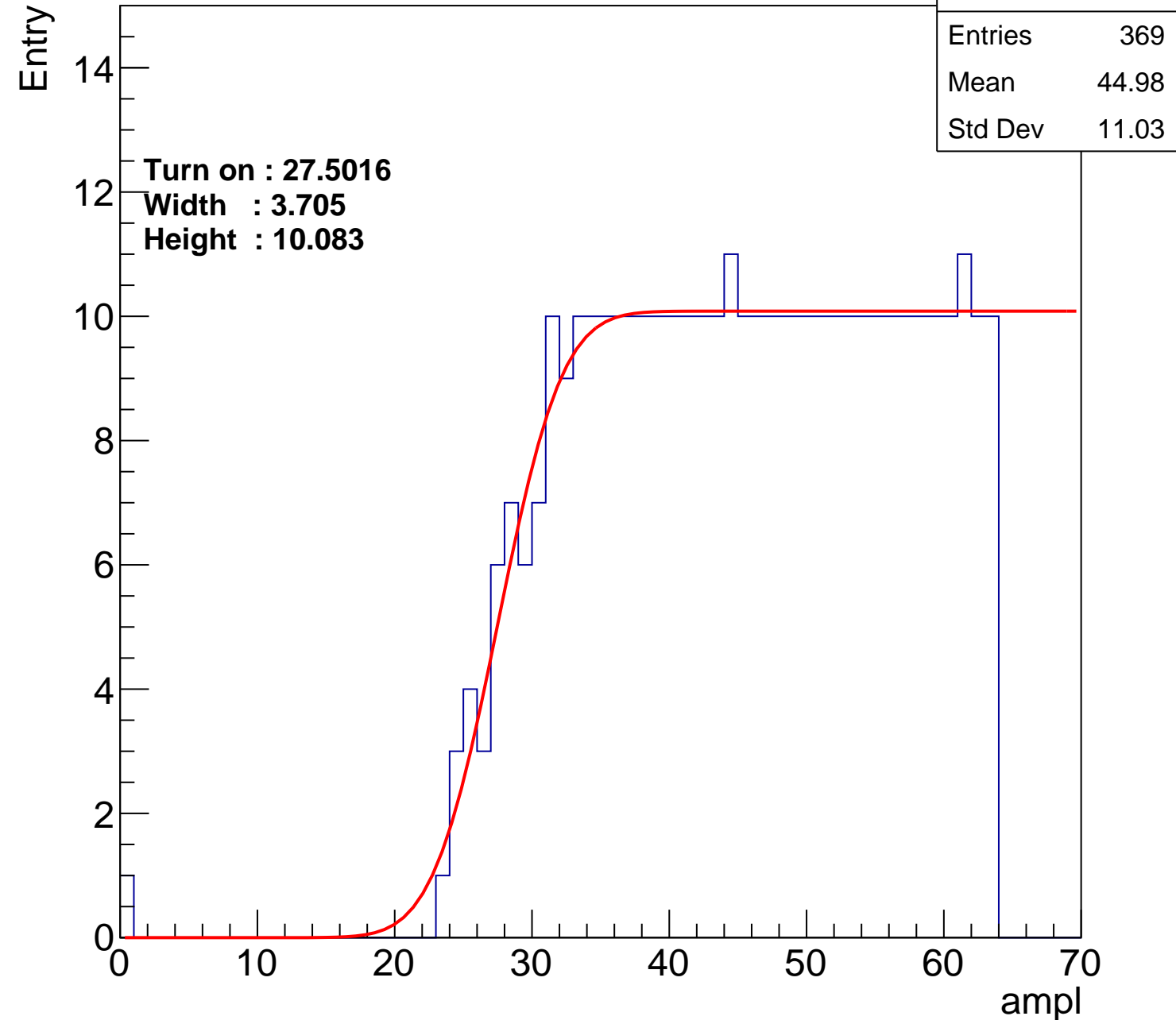
Width : 3.705

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch79

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	354
Mean	45.54
Std Dev	10.87

**Turn on : 29.3205**

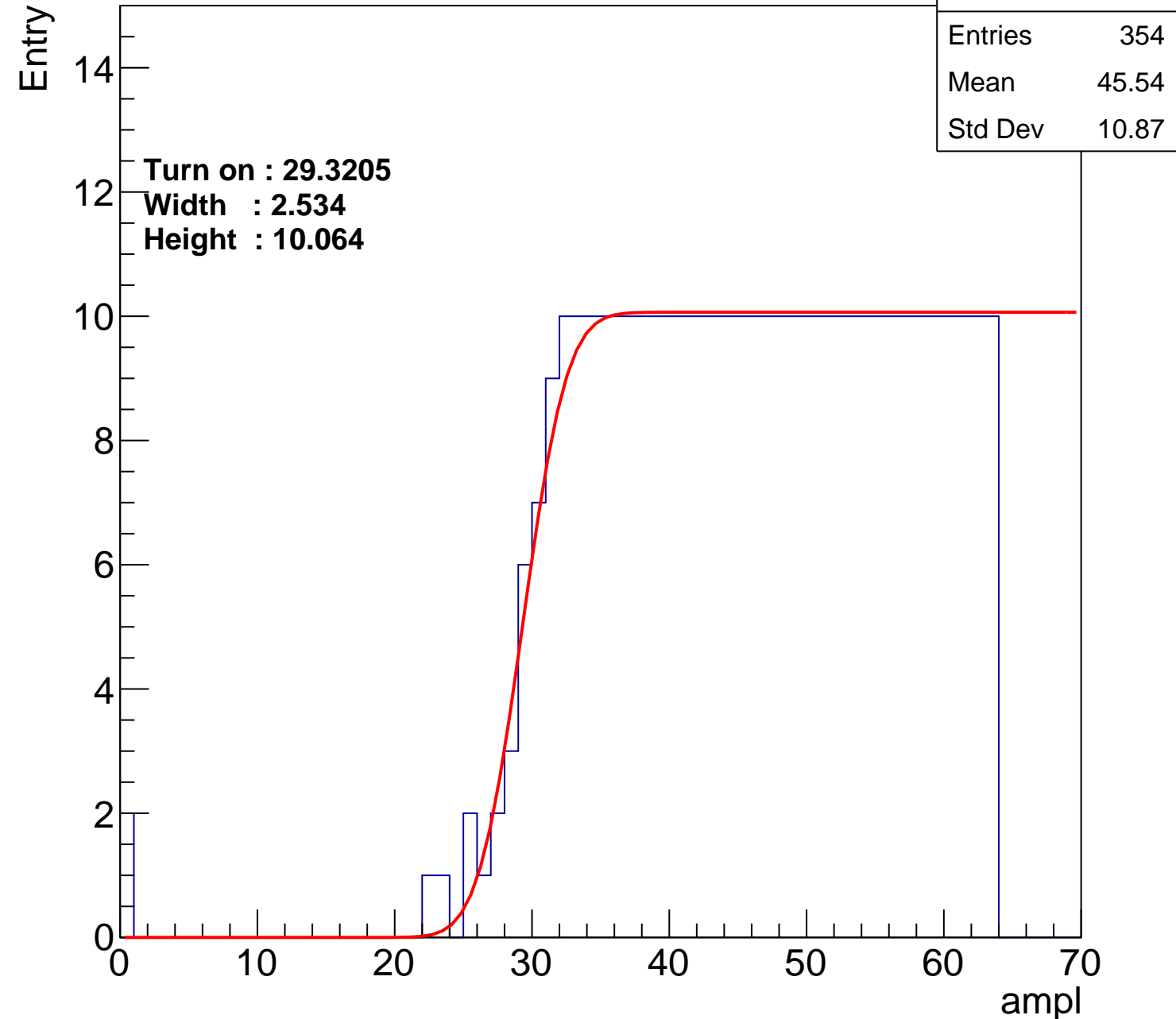
**Width : 2.534**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch80

calib\_packv5\_042523\_0143.root, FC#5, port B1

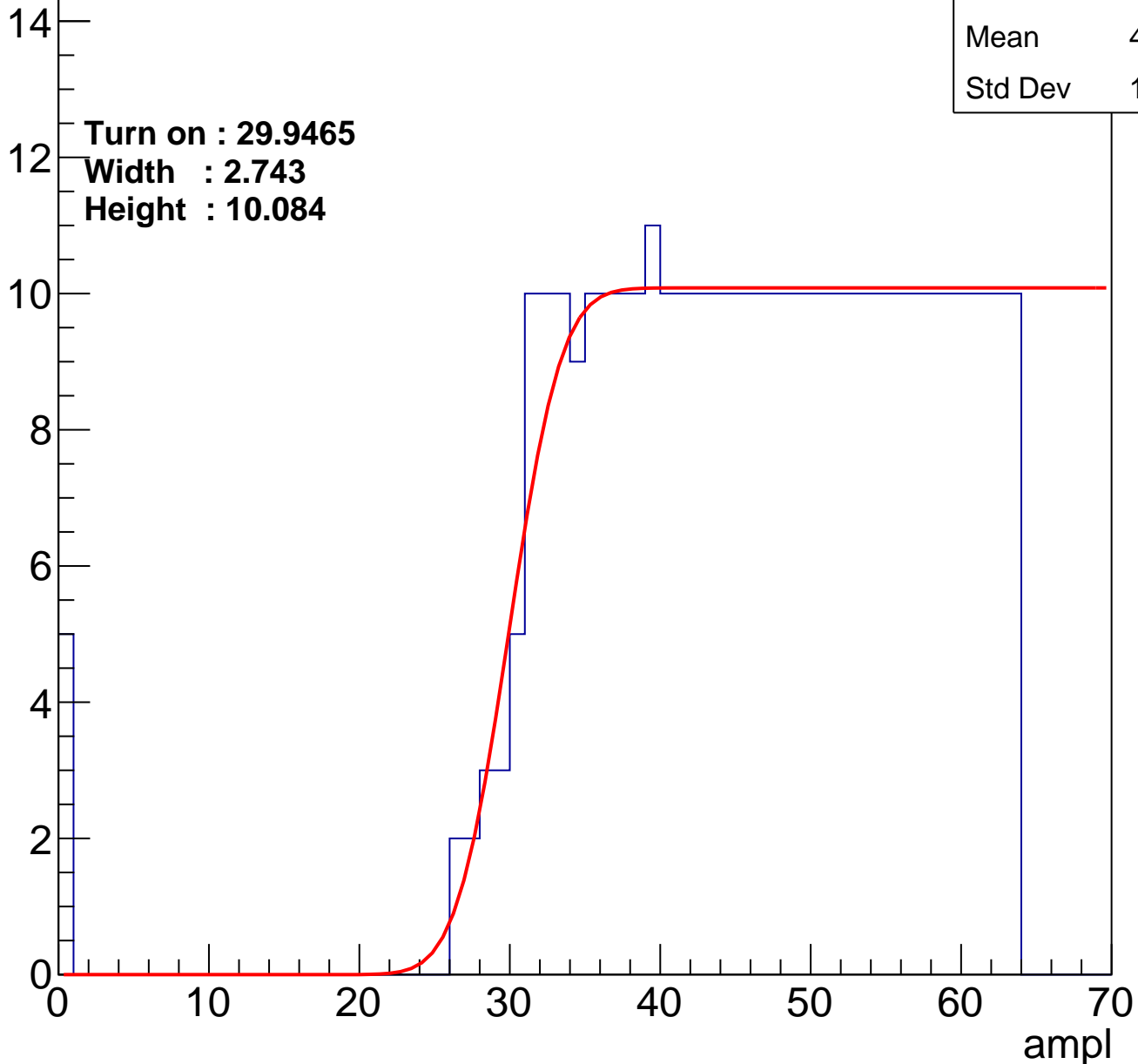
Entries	350
Mean	45.55
Std Dev	11.38

**Turn on : 29.9465**

**Width : 2.743**

**Height : 10.084**

Entry



# B0L000S, U26-ch81

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	388
Mean	43.92
Std Dev	11.57

Turn on : 25.4433

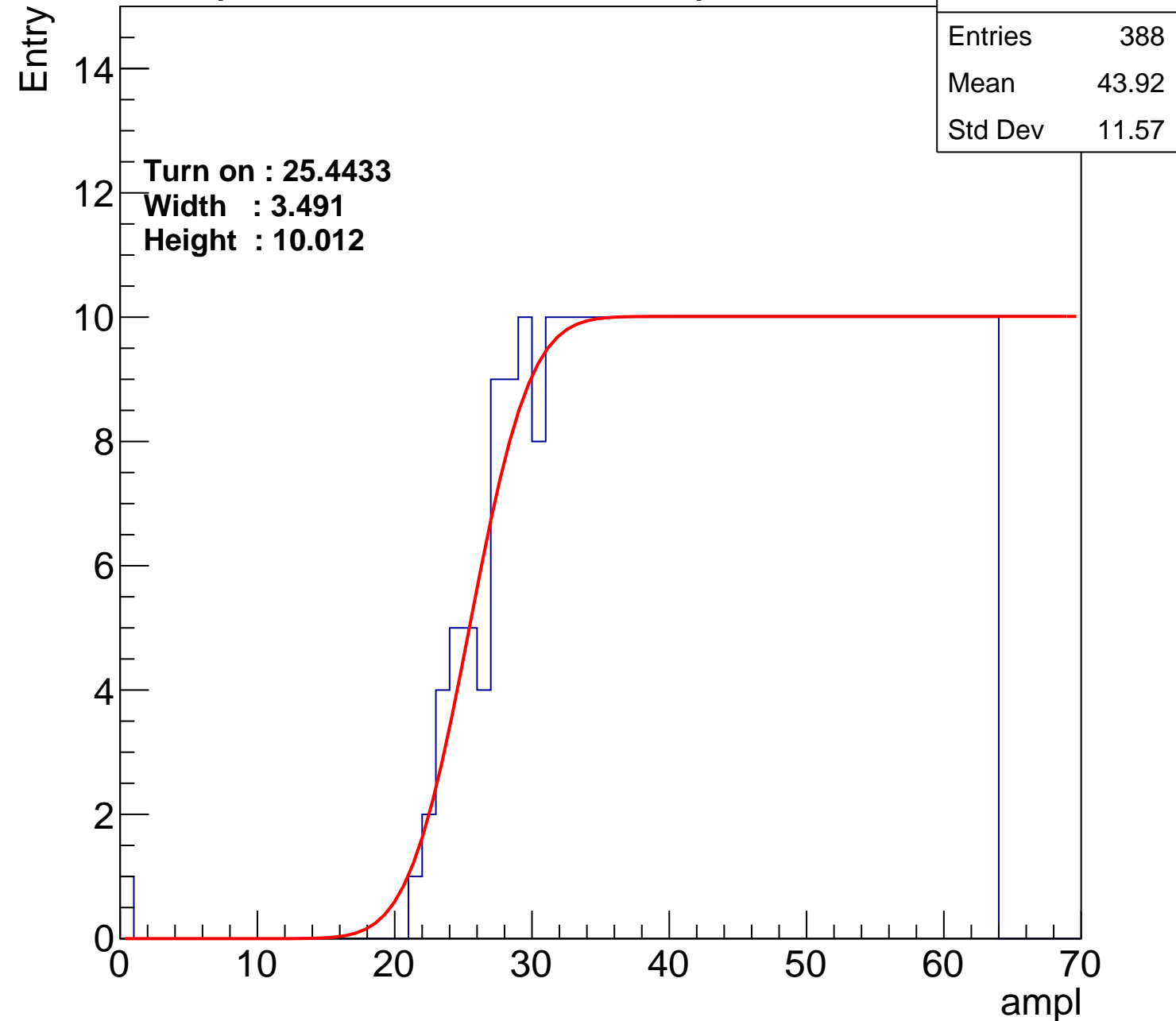
Width : 3.491

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch82

calib\_packv5\_042523\_0143.root, FC#5, port B1

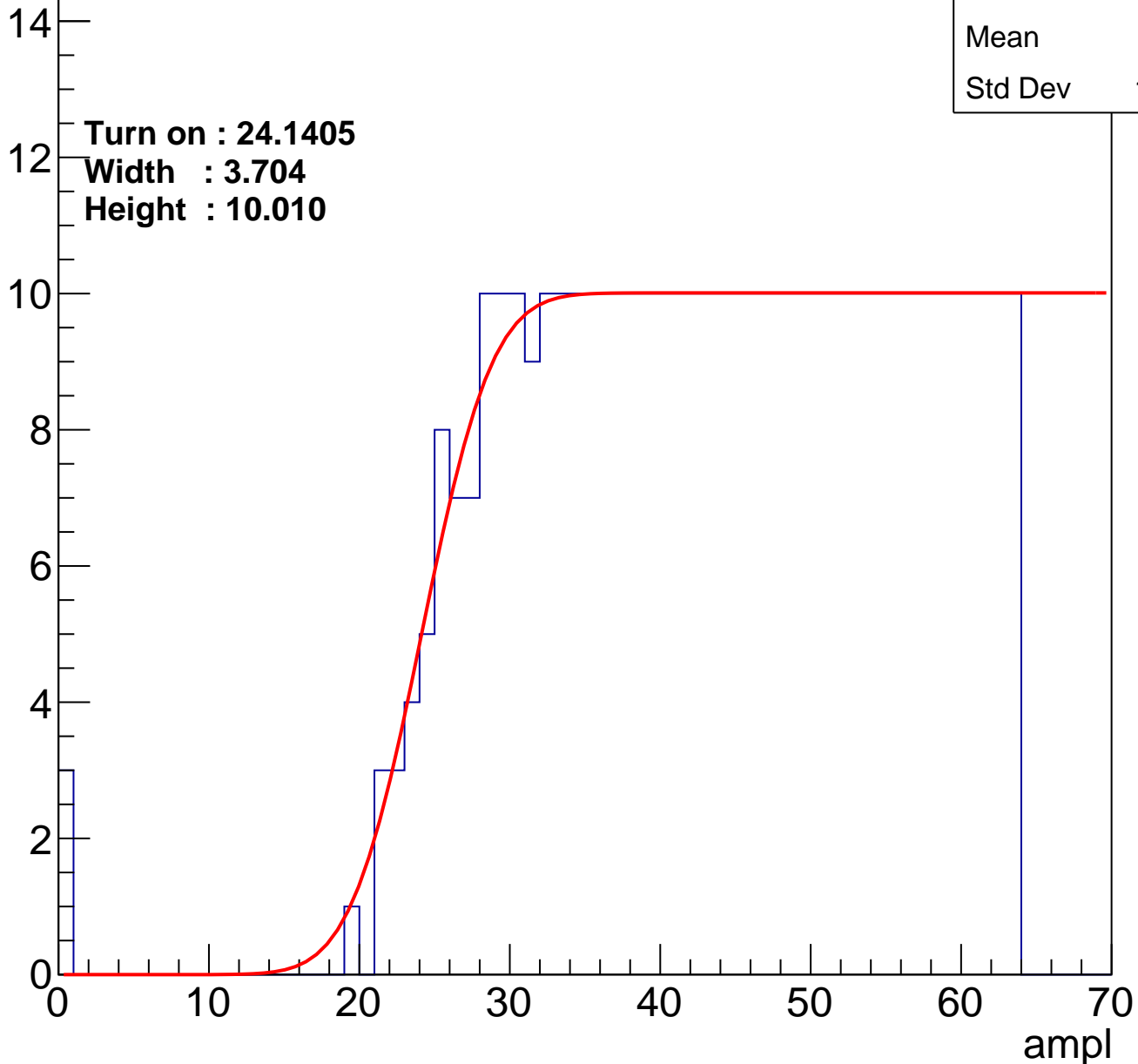
Entries	400
Mean	43.2
Std Dev	12.21

Turn on : 24.1405

Width : 3.704

Height : 10.010

Entry



# B0L000S, U26-ch83

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.83
Std Dev	11.38

Turn on : 27.3850

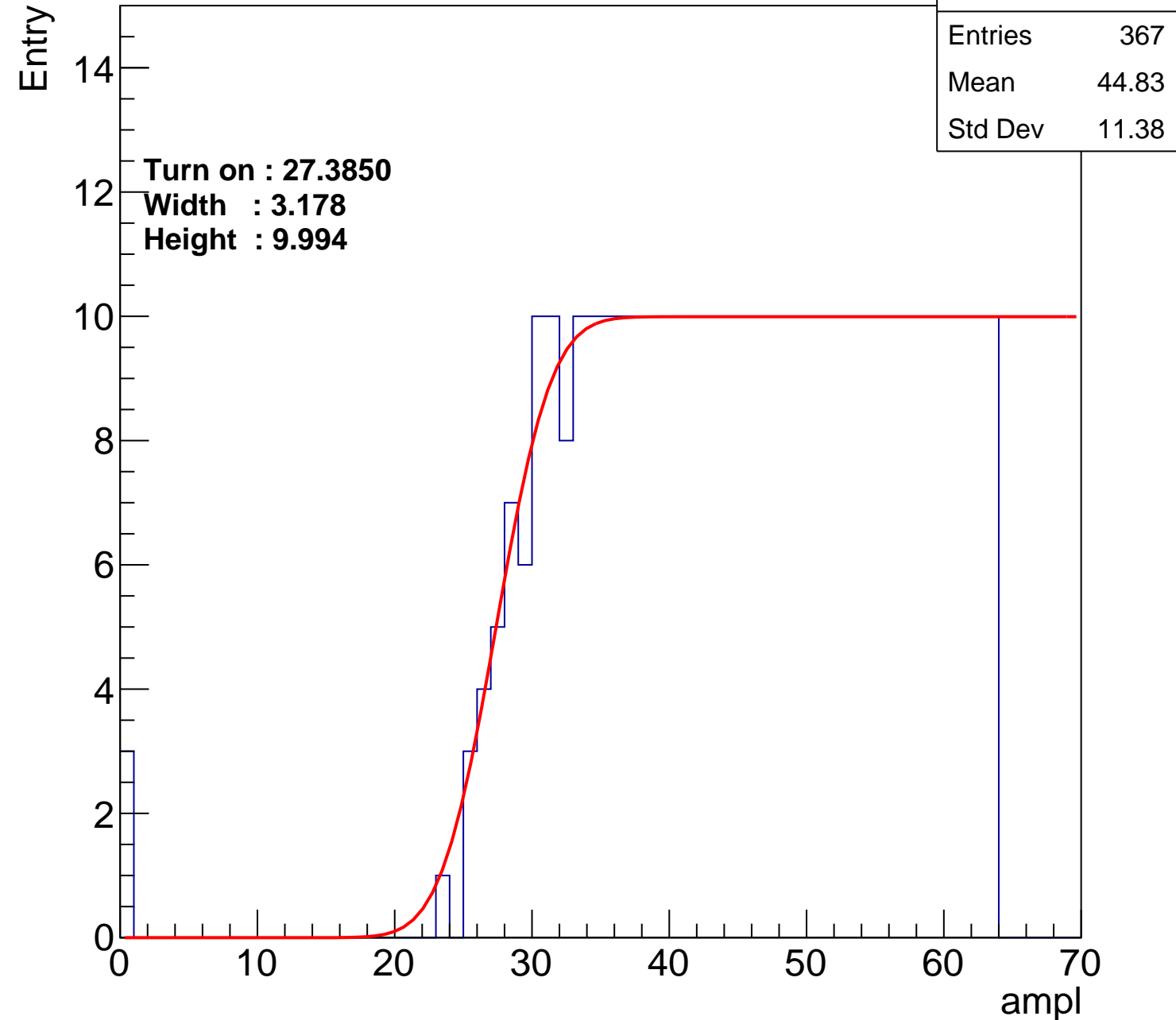
Width : 3.178

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch84

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.0604

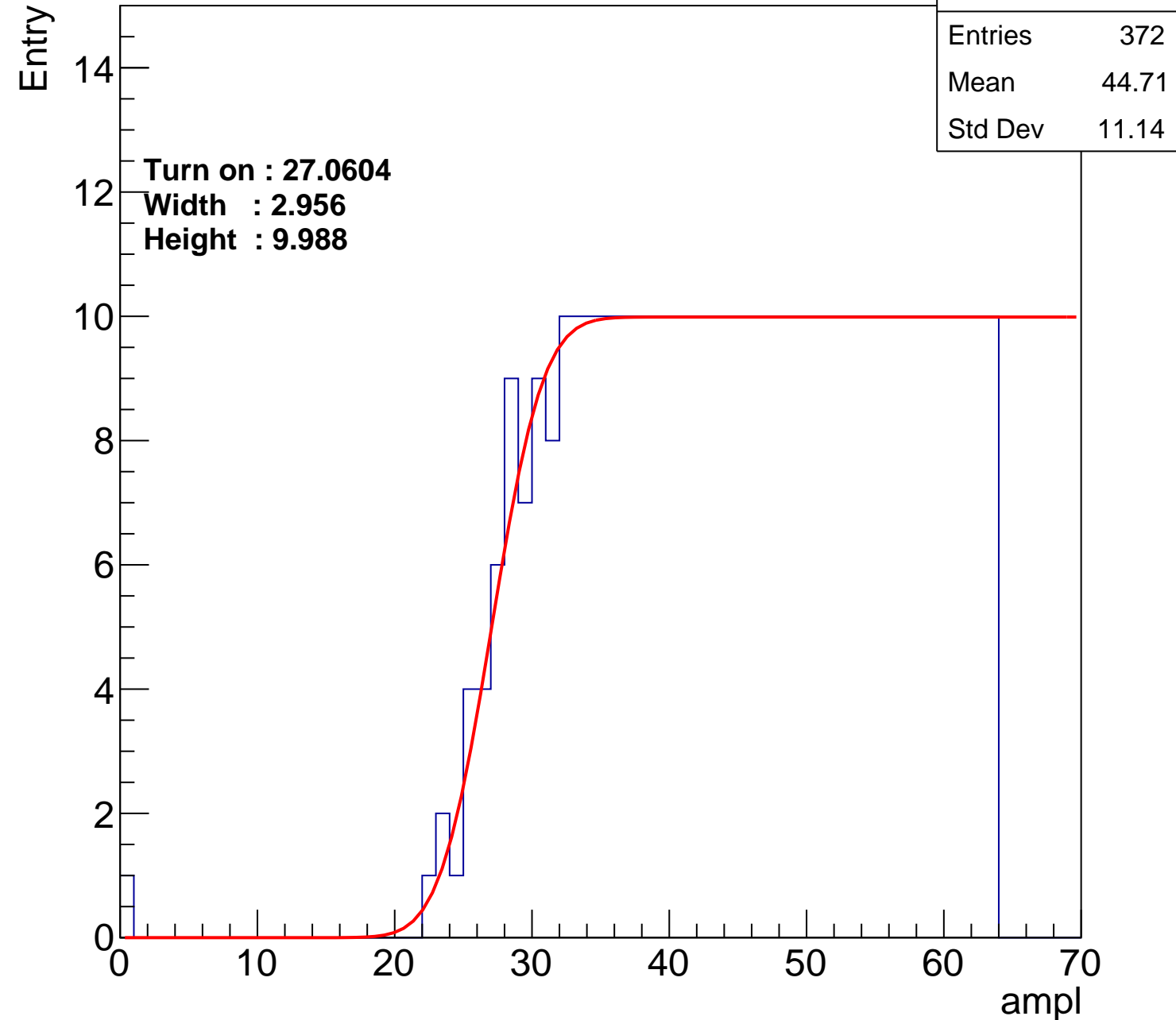
Width : 2.956

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch85

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	344
Mean	46.14
Std Dev	10.32

Turn on : 29.7551

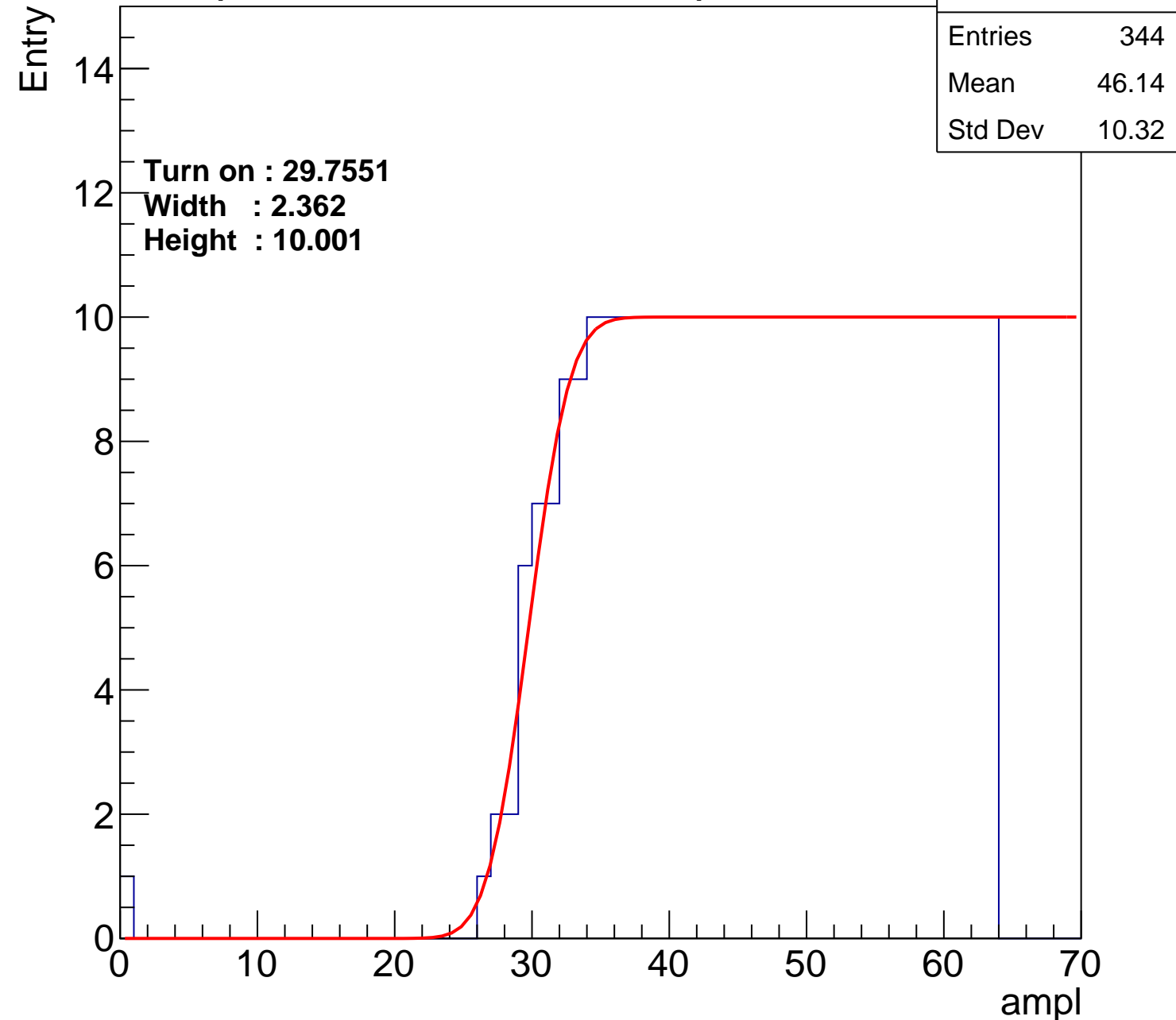
Width : 2.362

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch86

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.79
Std Dev	11.39

Turn on : 27.4627

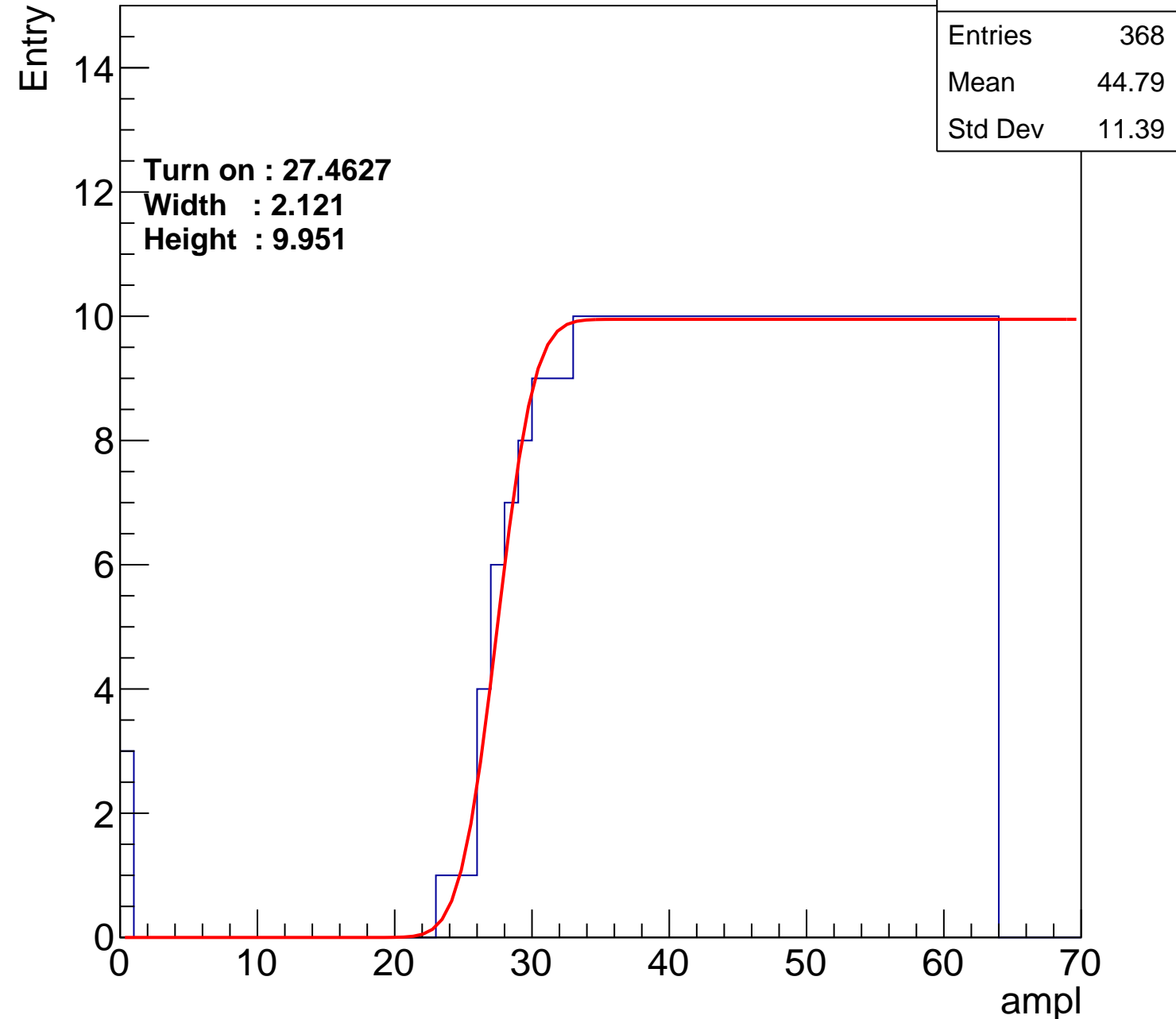
Width : 2.121

Height : 9.951

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch87

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.23
Std Dev	11.77

**Turn on : 26.9383**

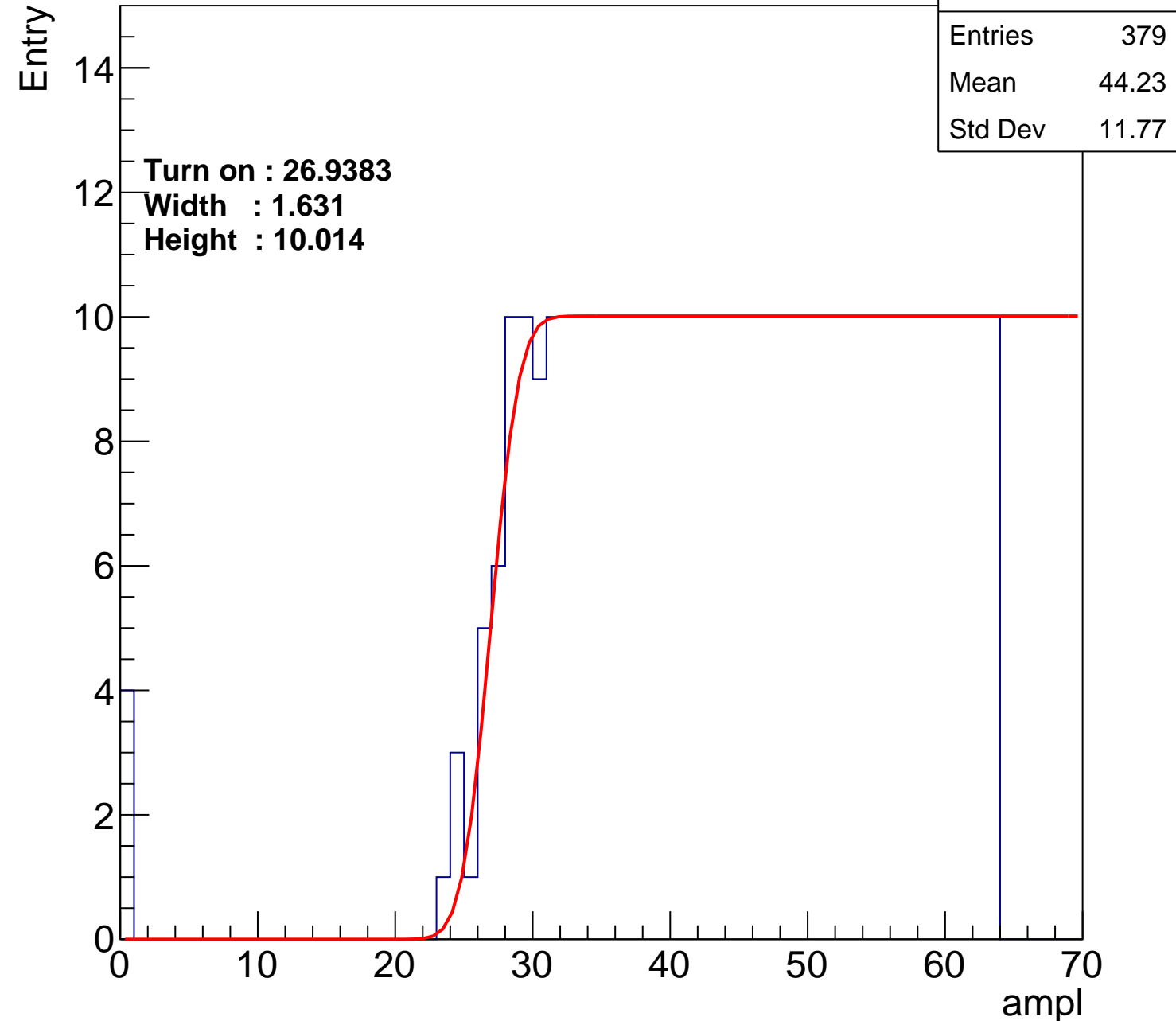
**Width : 1.631**

**Height : 10.014**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch88

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.29
Std Dev	11.96

Turn on : 27.2884

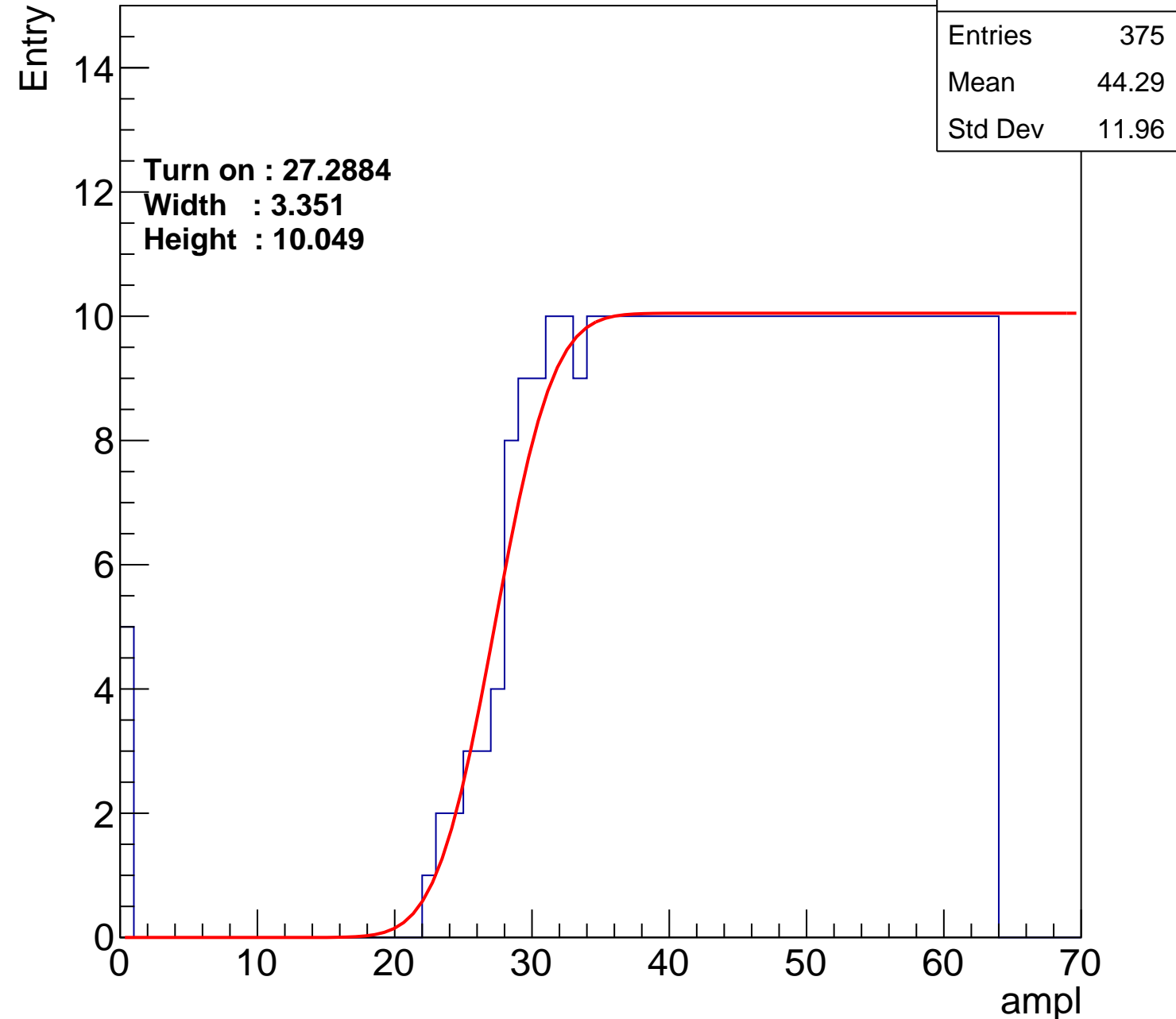
Width : 3.351

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch89

calib\_packv5\_042523\_0143.root, FC#5, port B1

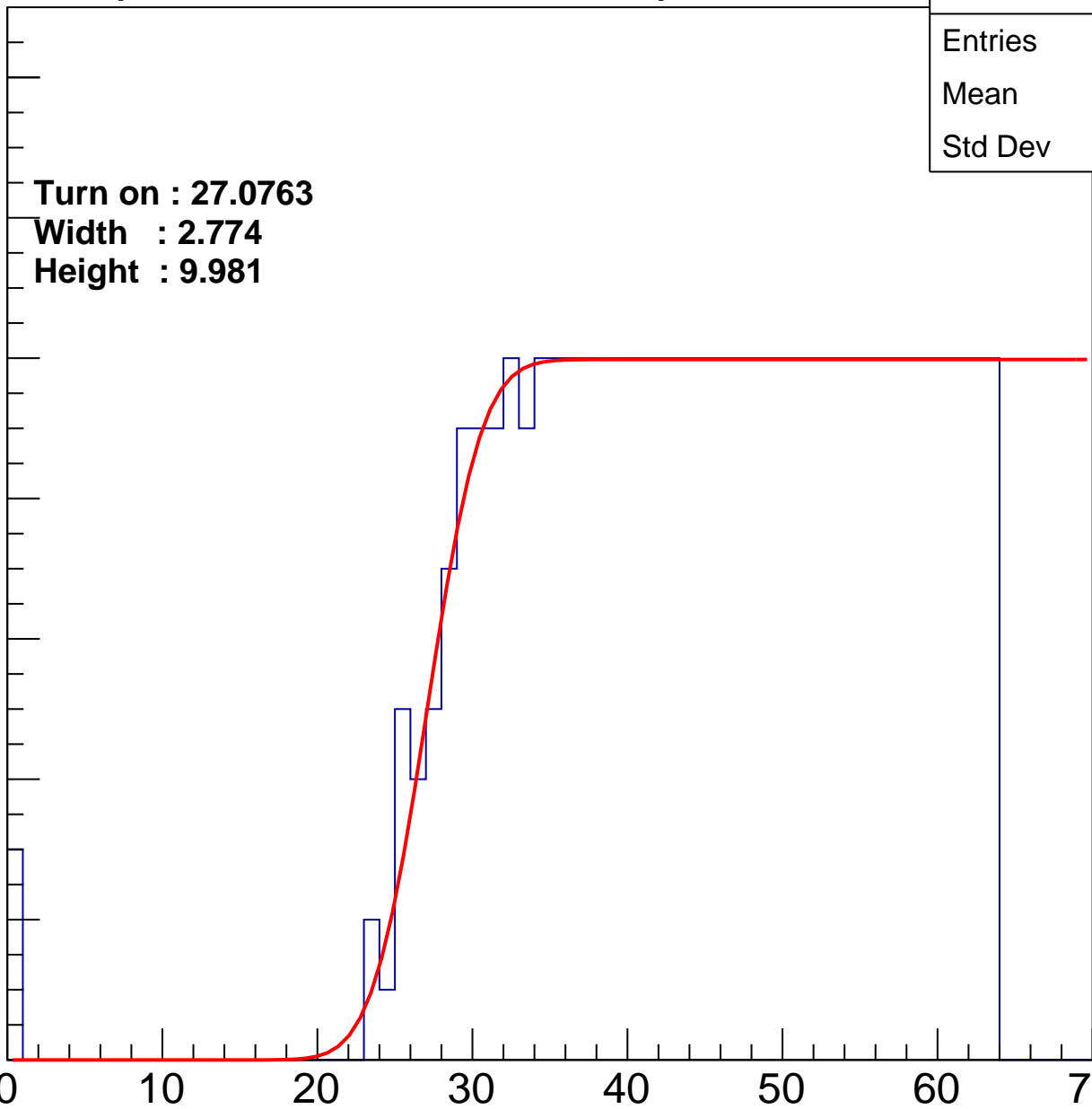
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.0763**  
**Width : 2.774**  
**Height : 9.981**

Entries	373
Mean	44.52
Std Dev	11.54

ampl



# B0L000S, U26-ch90

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	356
Mean	45.29
Std Dev	11.34

**Turn on : 29.5772**

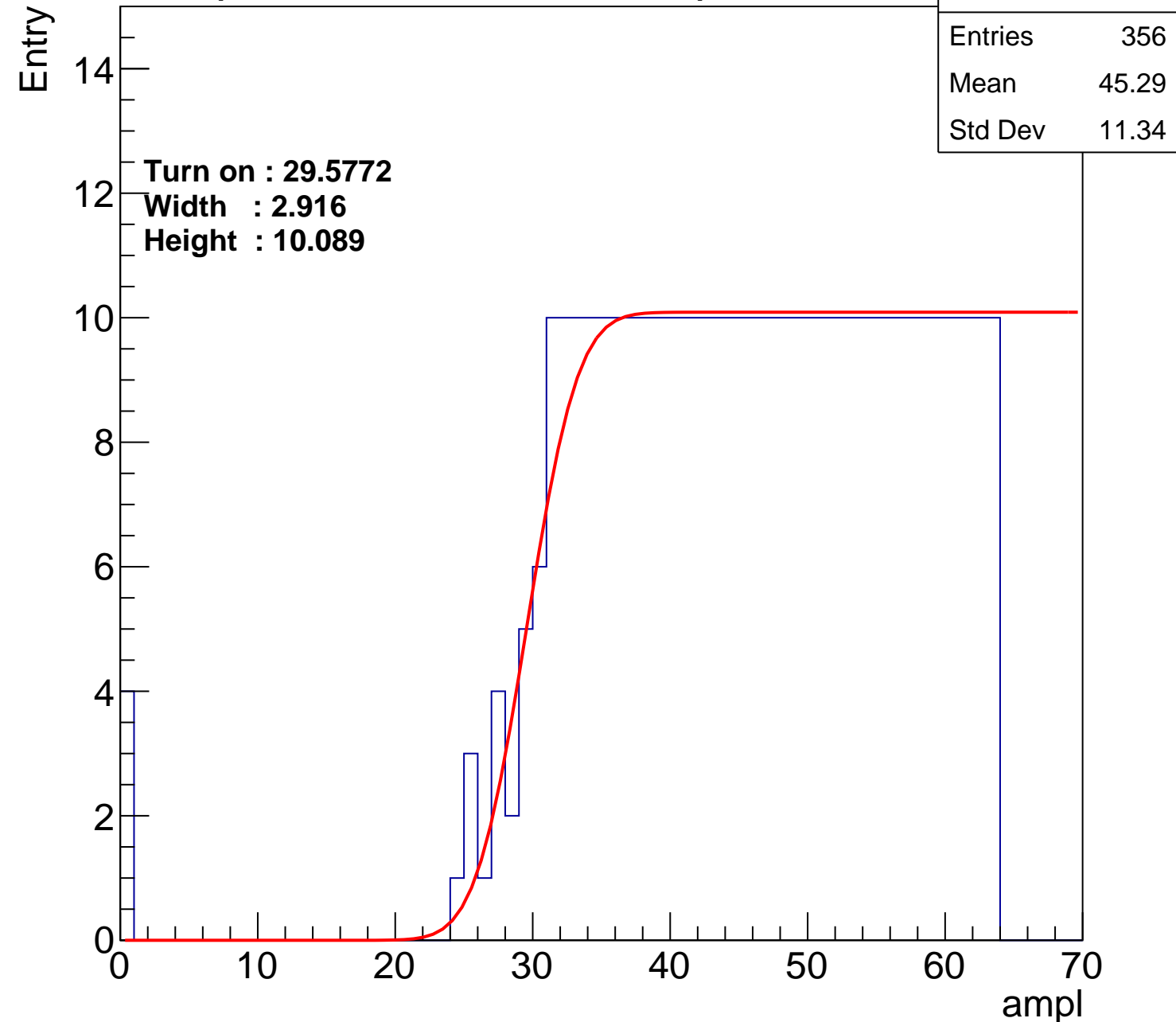
**Width : 2.916**

**Height : 10.089**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch91

calib\_packv5\_042523\_0143.root, FC#5, port B1

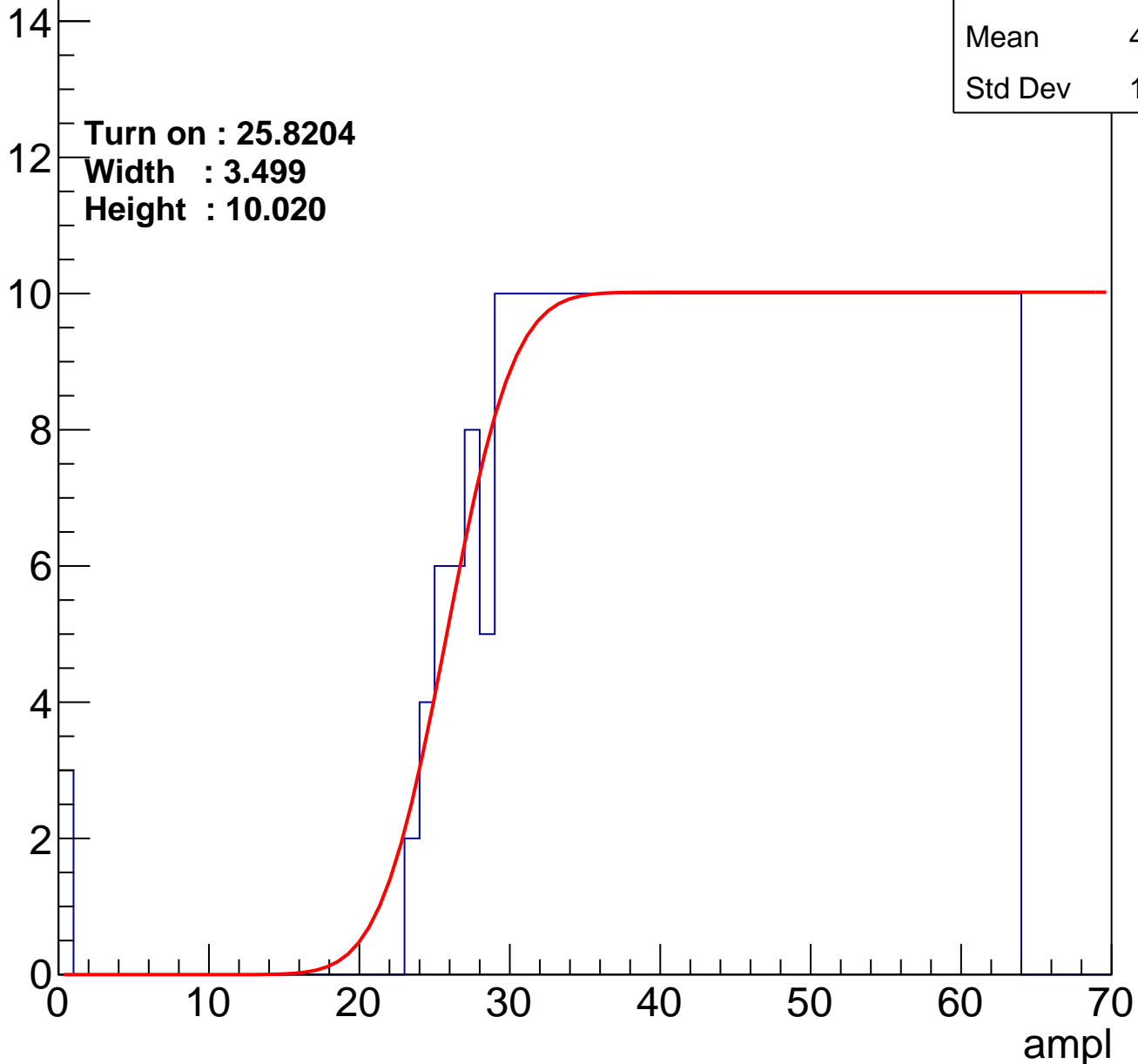
Entries	384
Mean	44.02
Std Dev	11.76

Turn on : 25.8204

Width : 3.499

Height : 10.020

Entry



# B0L000S, U26-ch92

calib\_packv5\_042523\_0143.root, FC#5, port B1

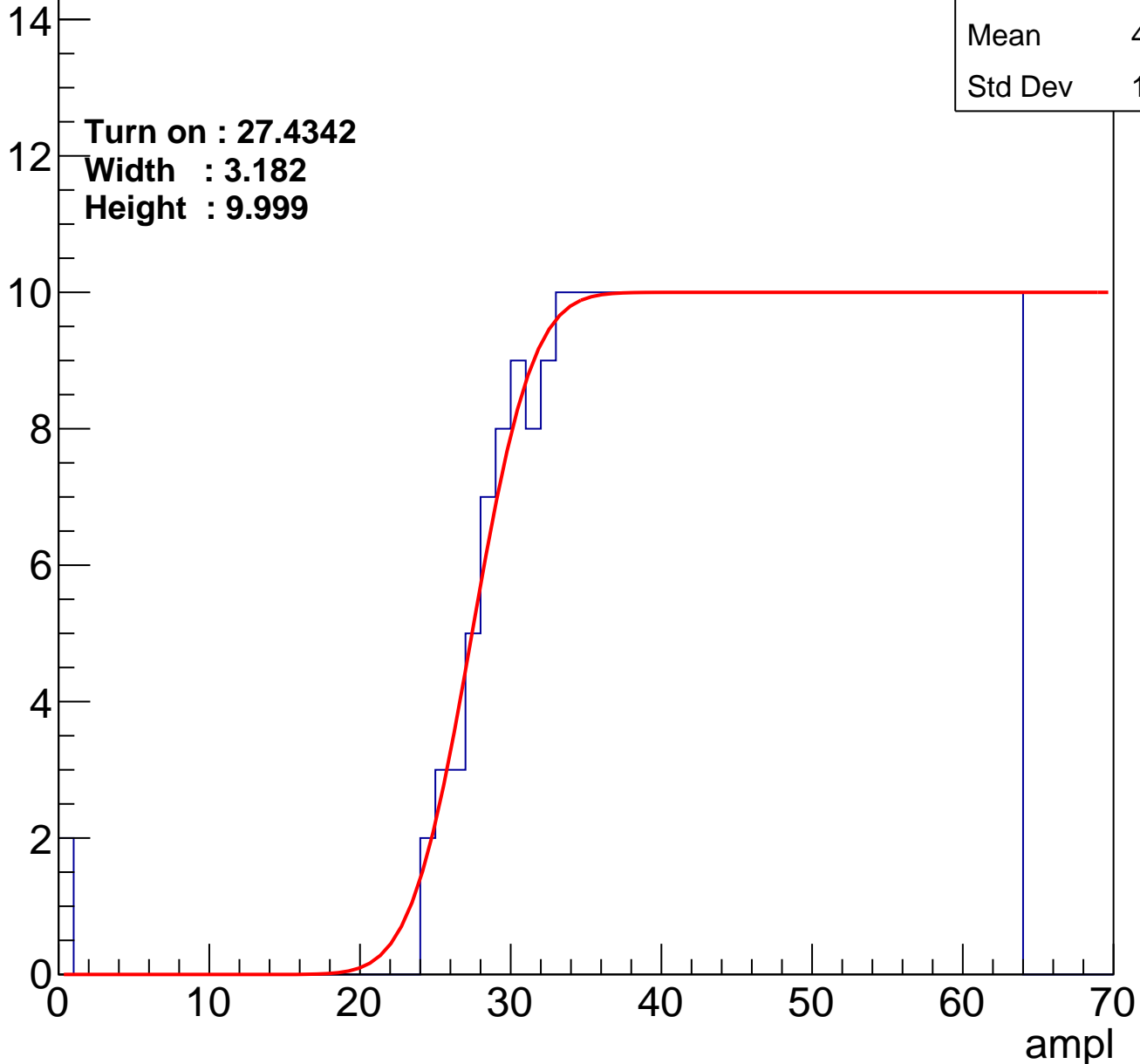
Entries	366
Mean	44.95
Std Dev	11.16

Turn on : 27.4342

Width : 3.182

Height : 9.999

Entry



# B0L000S, U26-ch93

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	44.84
Std Dev	11.57

Turn on : 28.0594

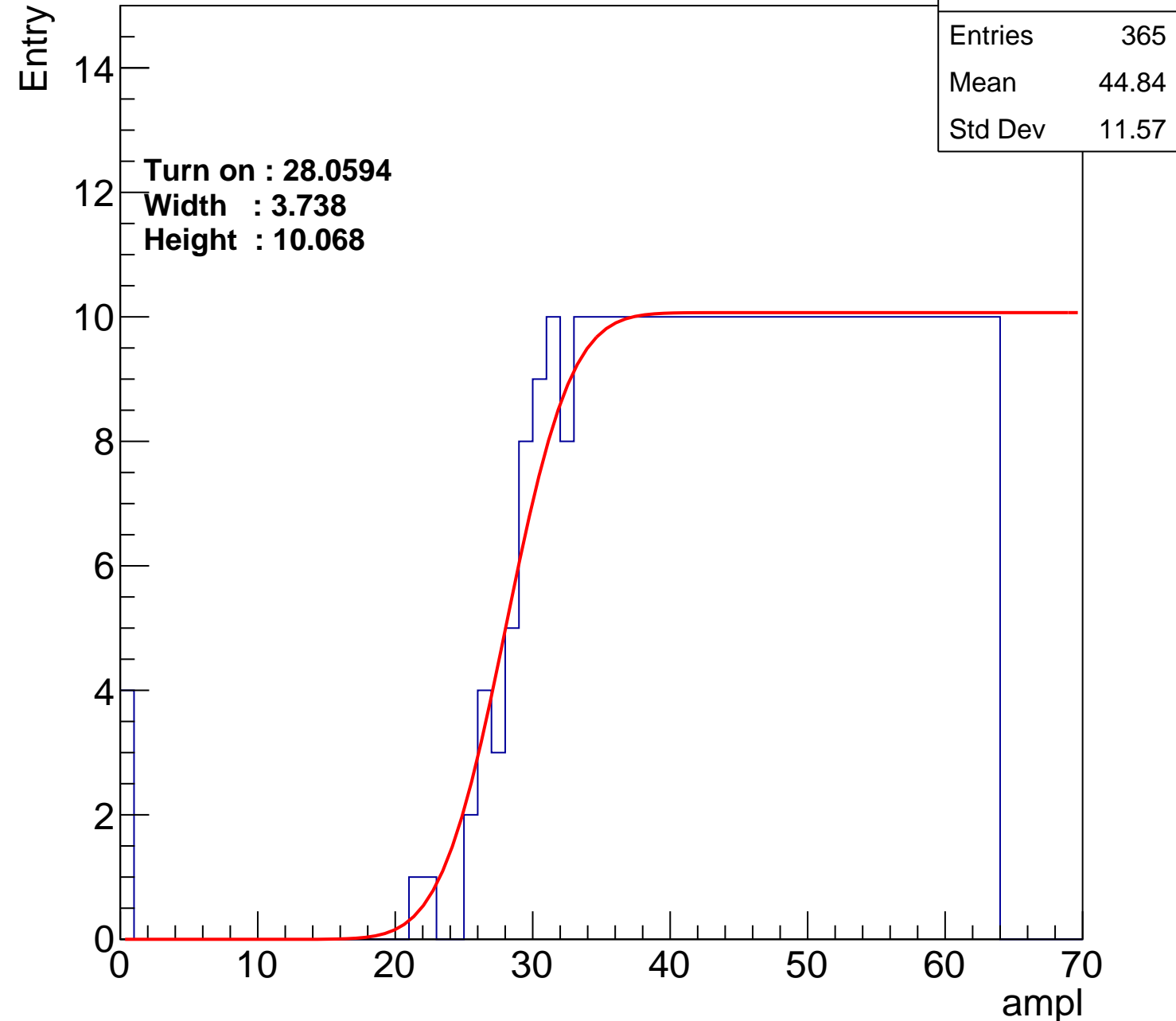
Width : 3.738

Height : 10.068

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch94

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.19
Std Dev	11.24

Turn on : 28.7488

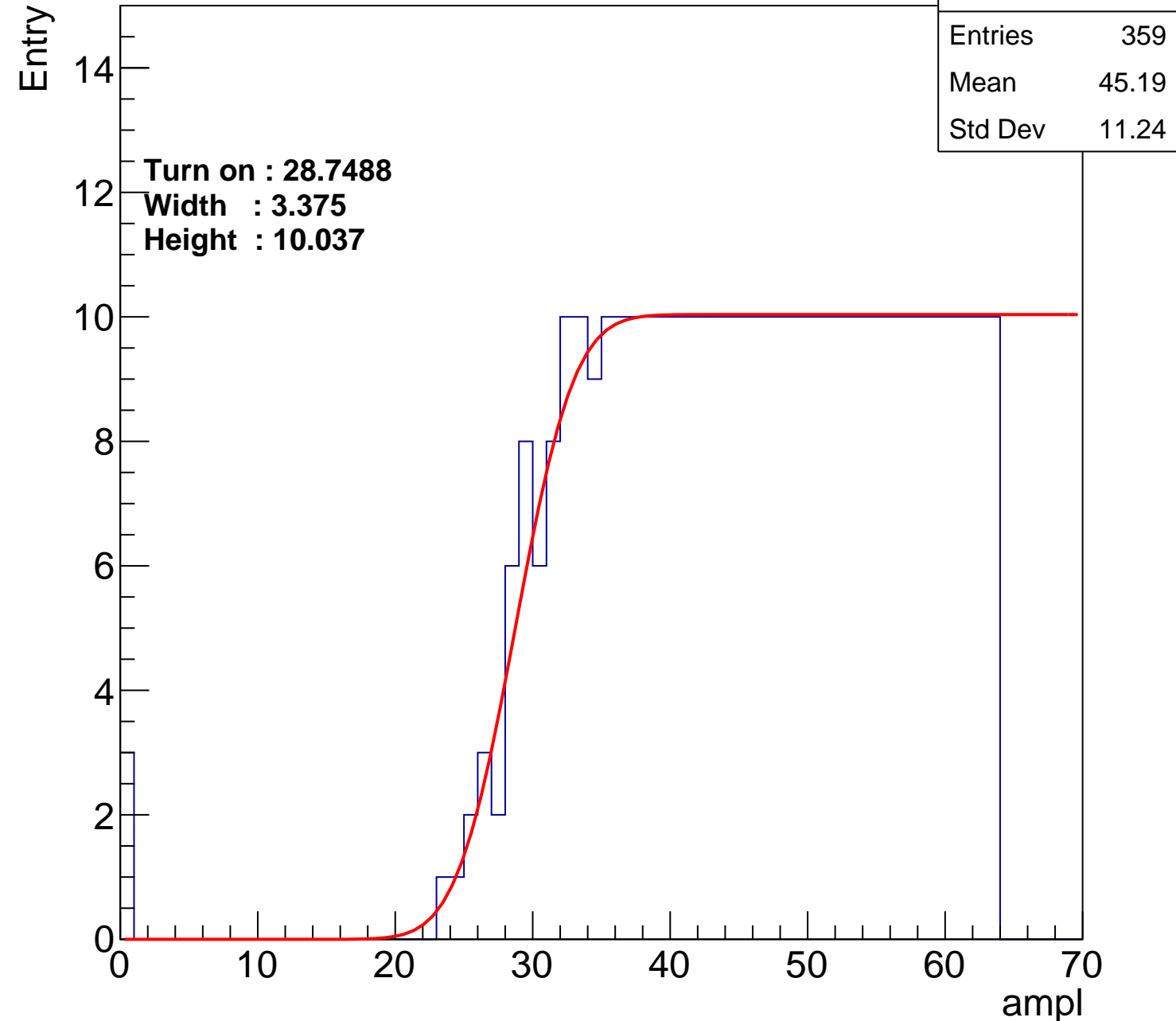
Width : 3.375

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch95

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.75
Std Dev	11.46

Turn on : 28.2095

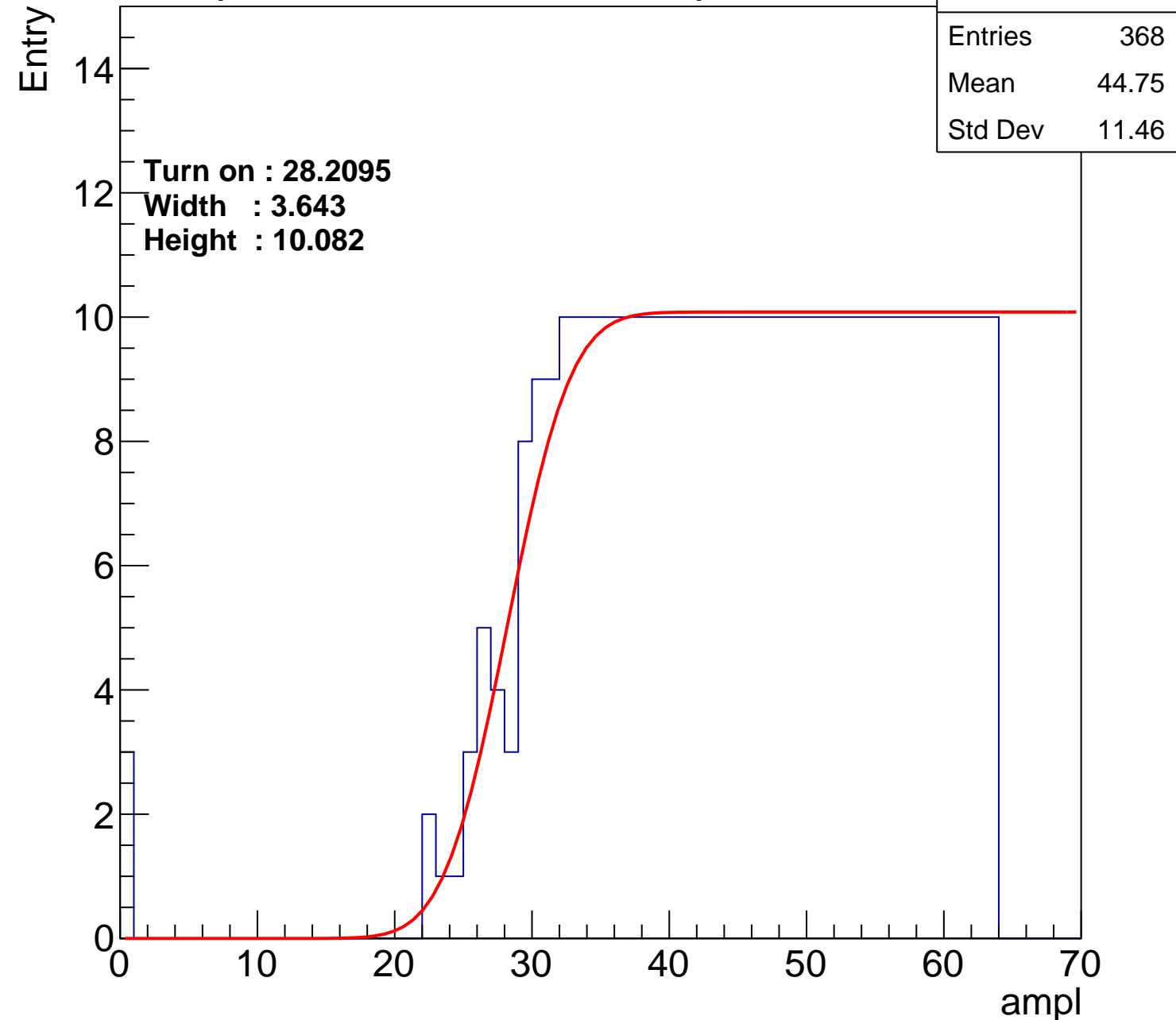
Width : 3.643

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch96

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.73
Std Dev	11.44

**Turn on : 27.5370**

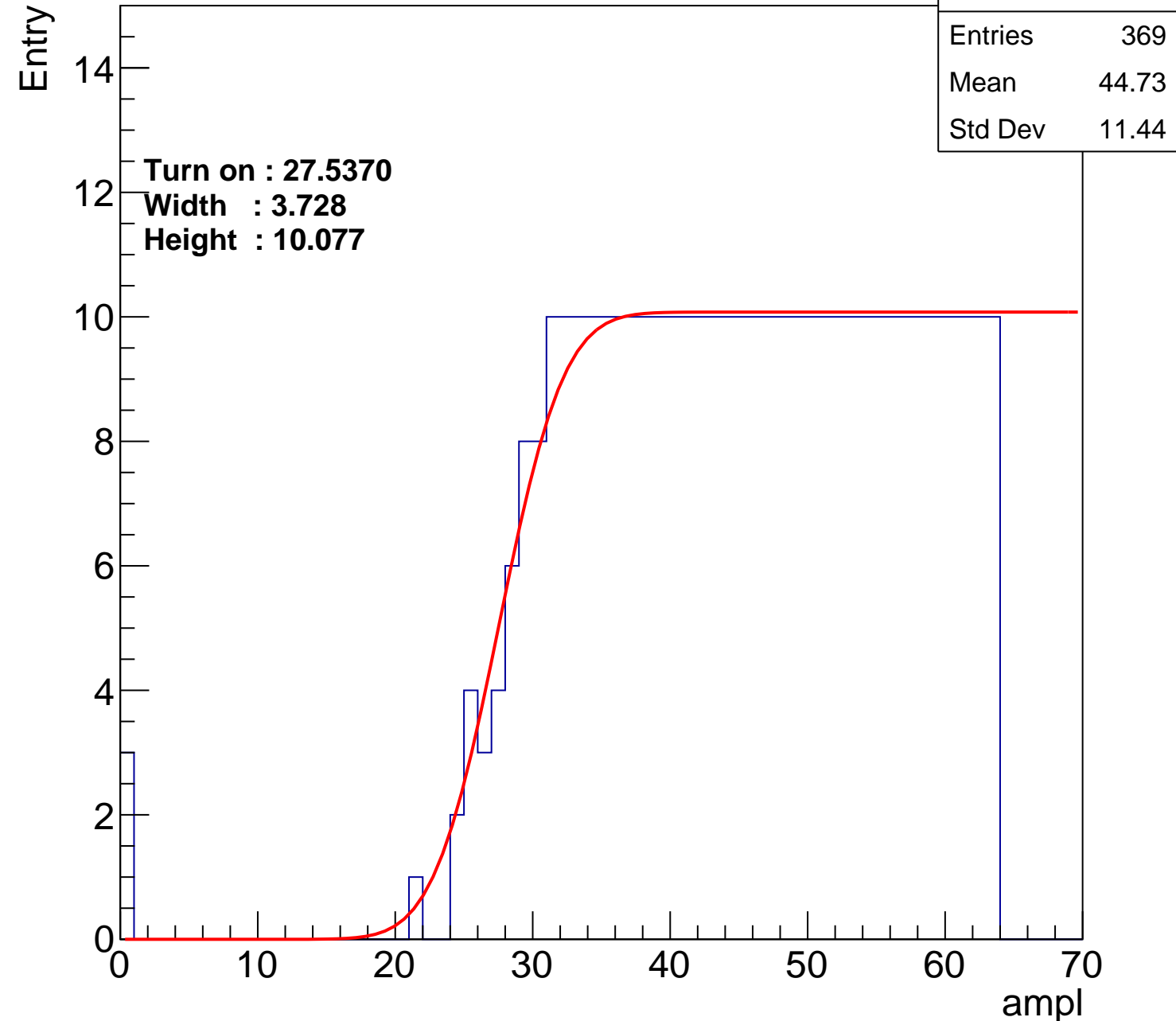
**Width : 3.728**

**Height : 10.077**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch97

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.86
Std Dev	11.34

Turn on : 27.8377

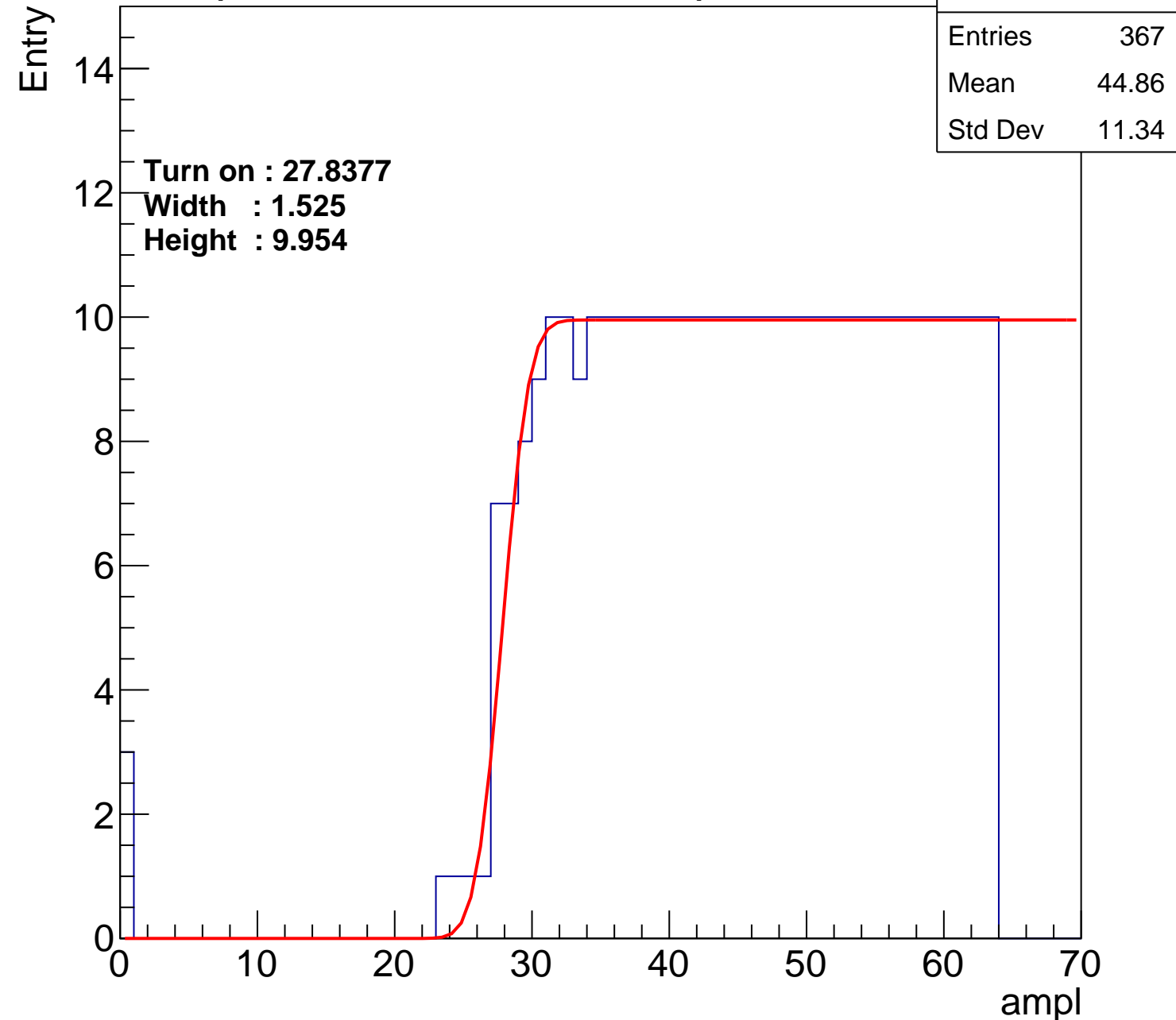
Width : 1.525

Height : 9.954

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch98

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.58
Std Dev	11.24

Turn on : 26.5907

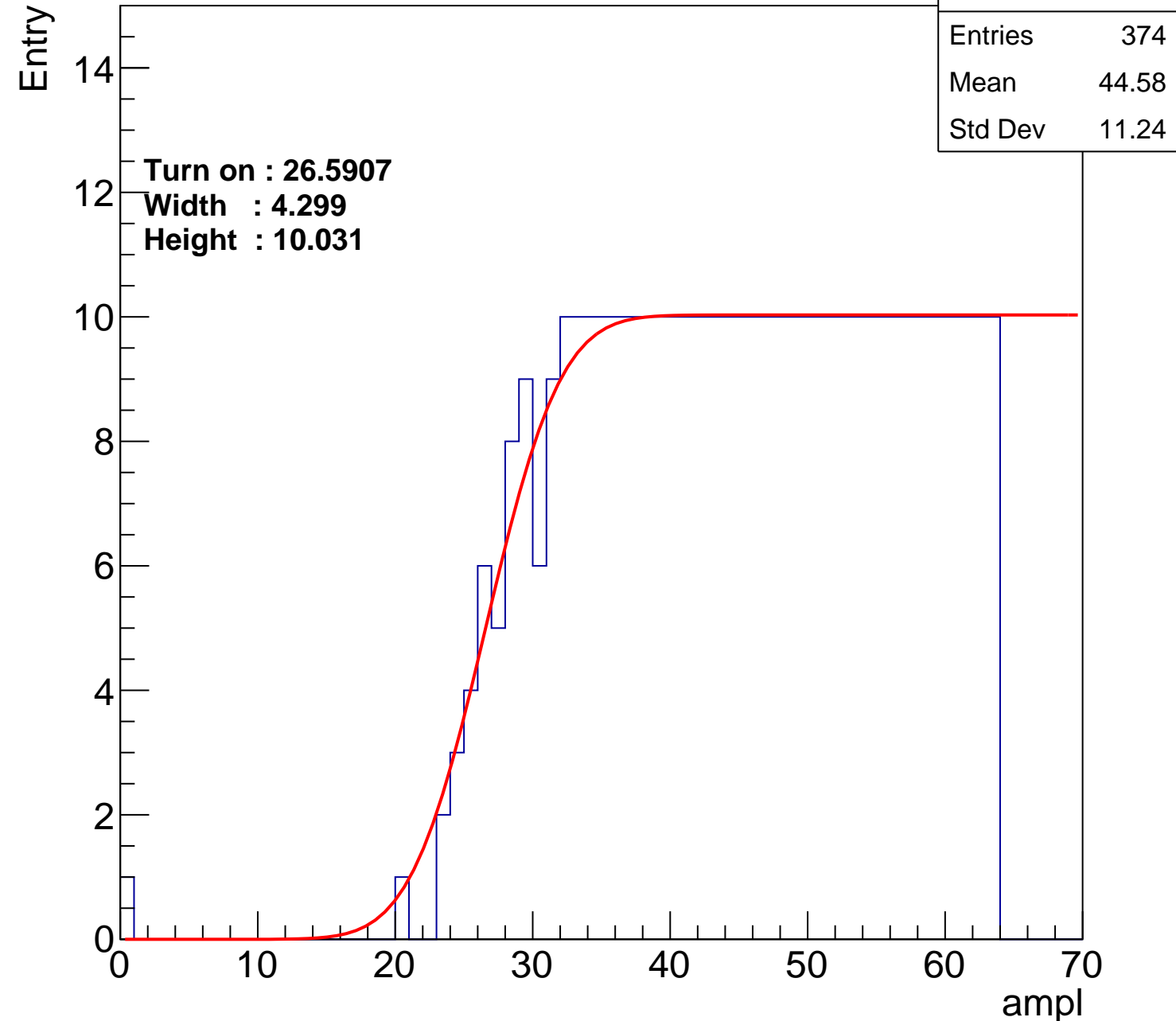
Width : 4.299

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch99

calib\_packv5\_042523\_0143.root, FC#5, port B1

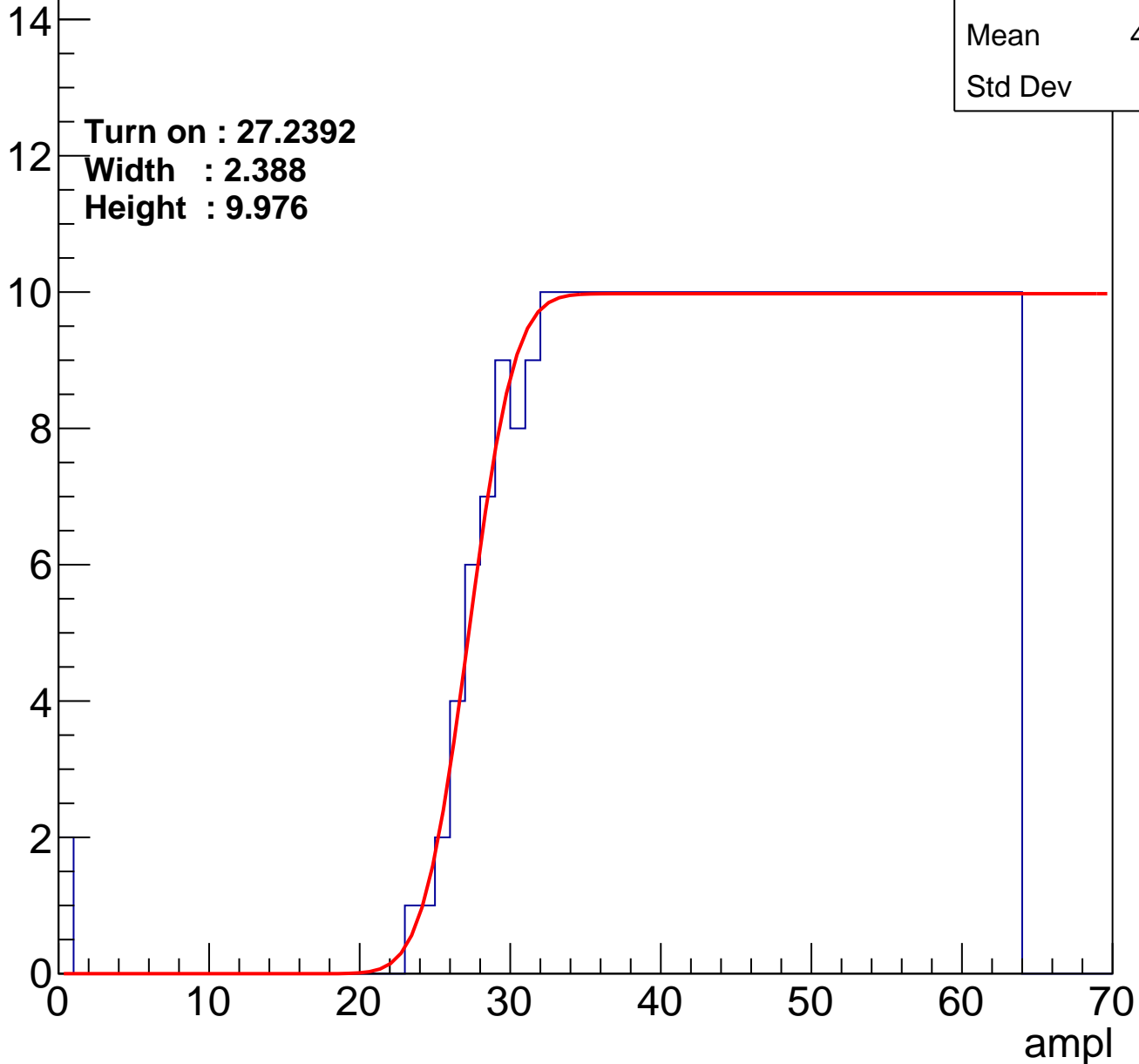
Entries	369
Mean	44.82
Std Dev	11.2

Turn on : 27.2392

Width : 2.388

Height : 9.976

Entry



# B0L000S, U26-ch100

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.34
Std Dev	11.44

**Turn on : 26.1939**

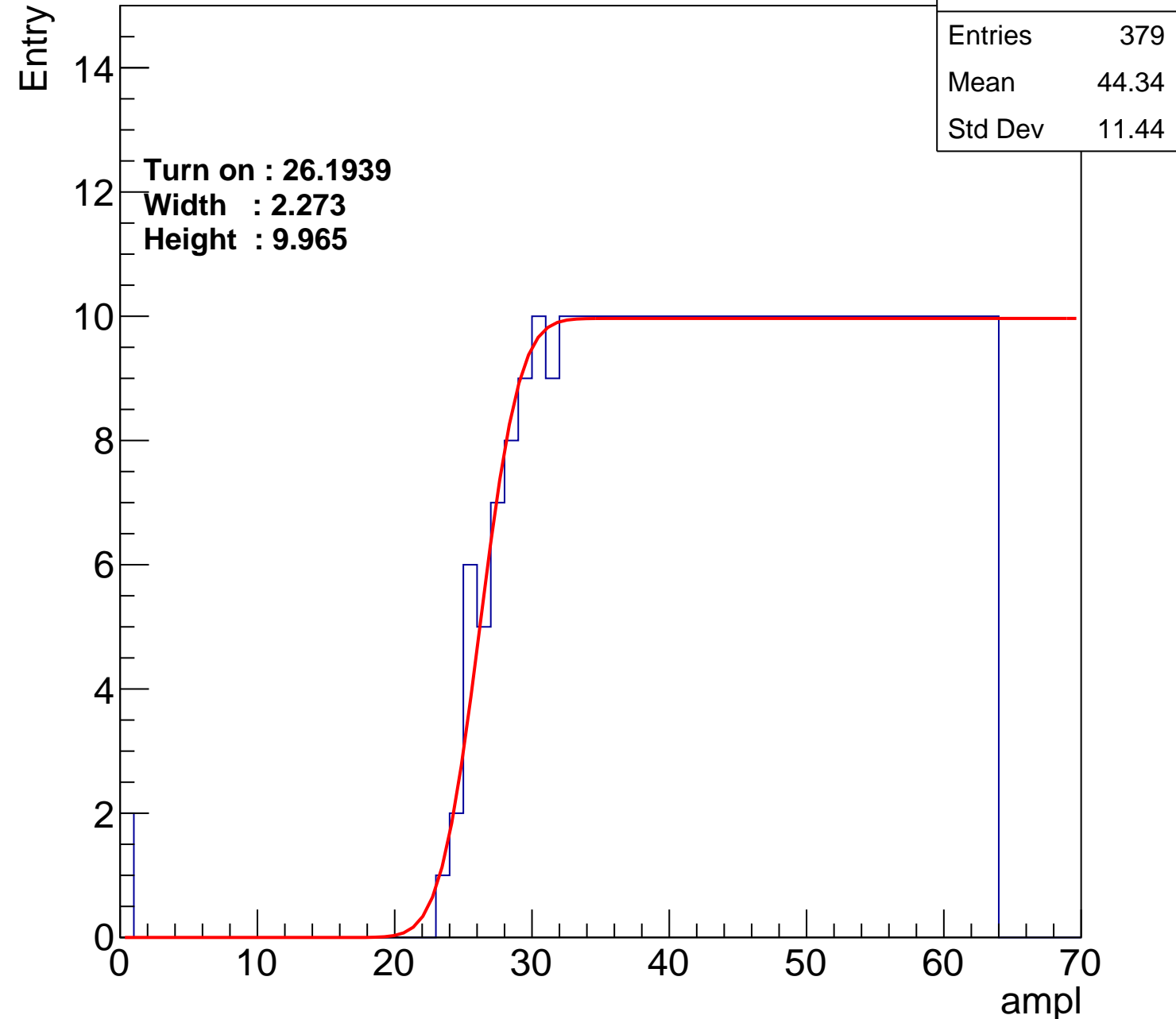
**Width : 2.273**

**Height : 9.965**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch101

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.38
Std Dev	11.5

Turn on : 26.8856

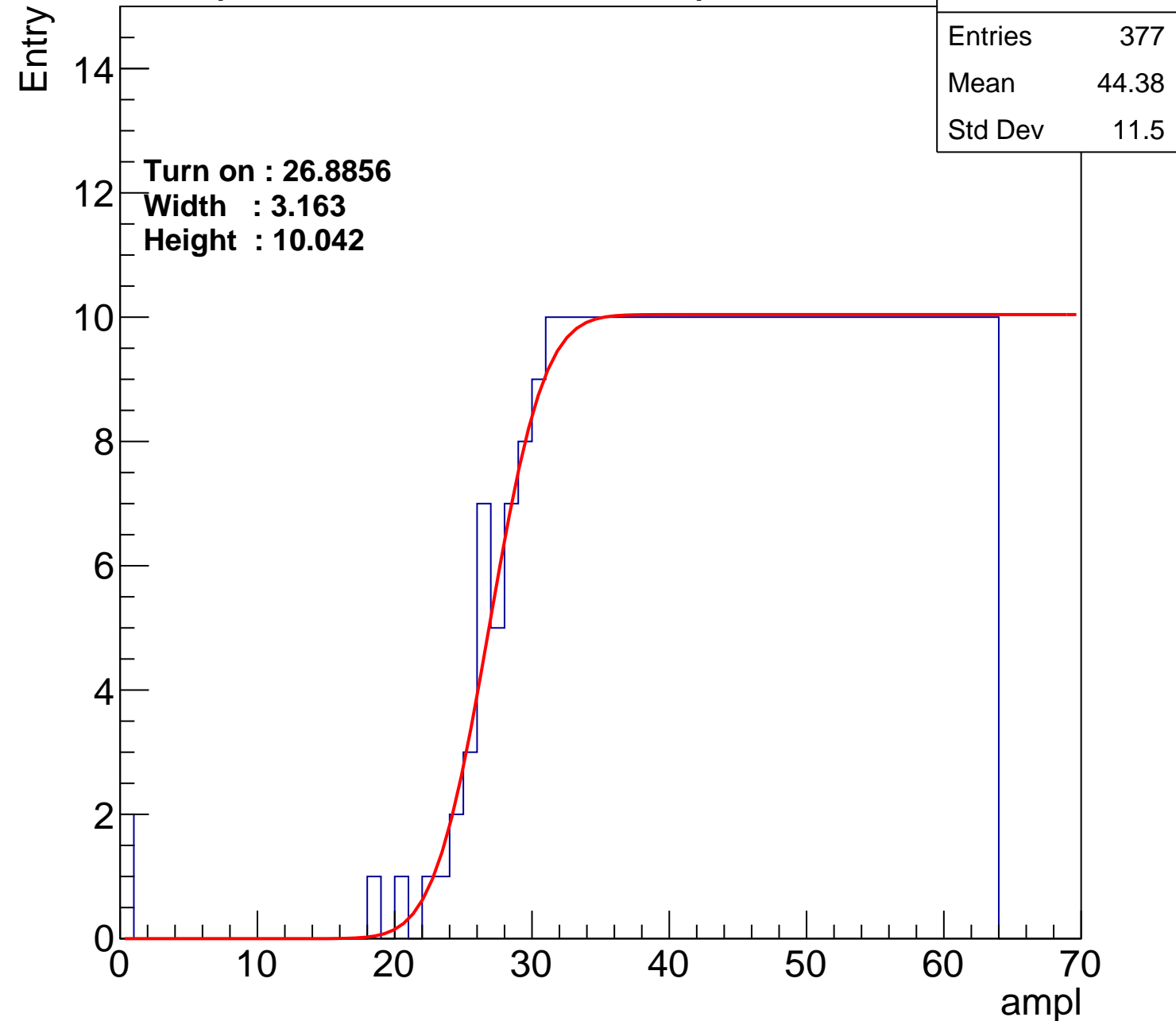
Width : 3.163

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch102

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.4
Std Dev	11.57

Turn on : 26.8502

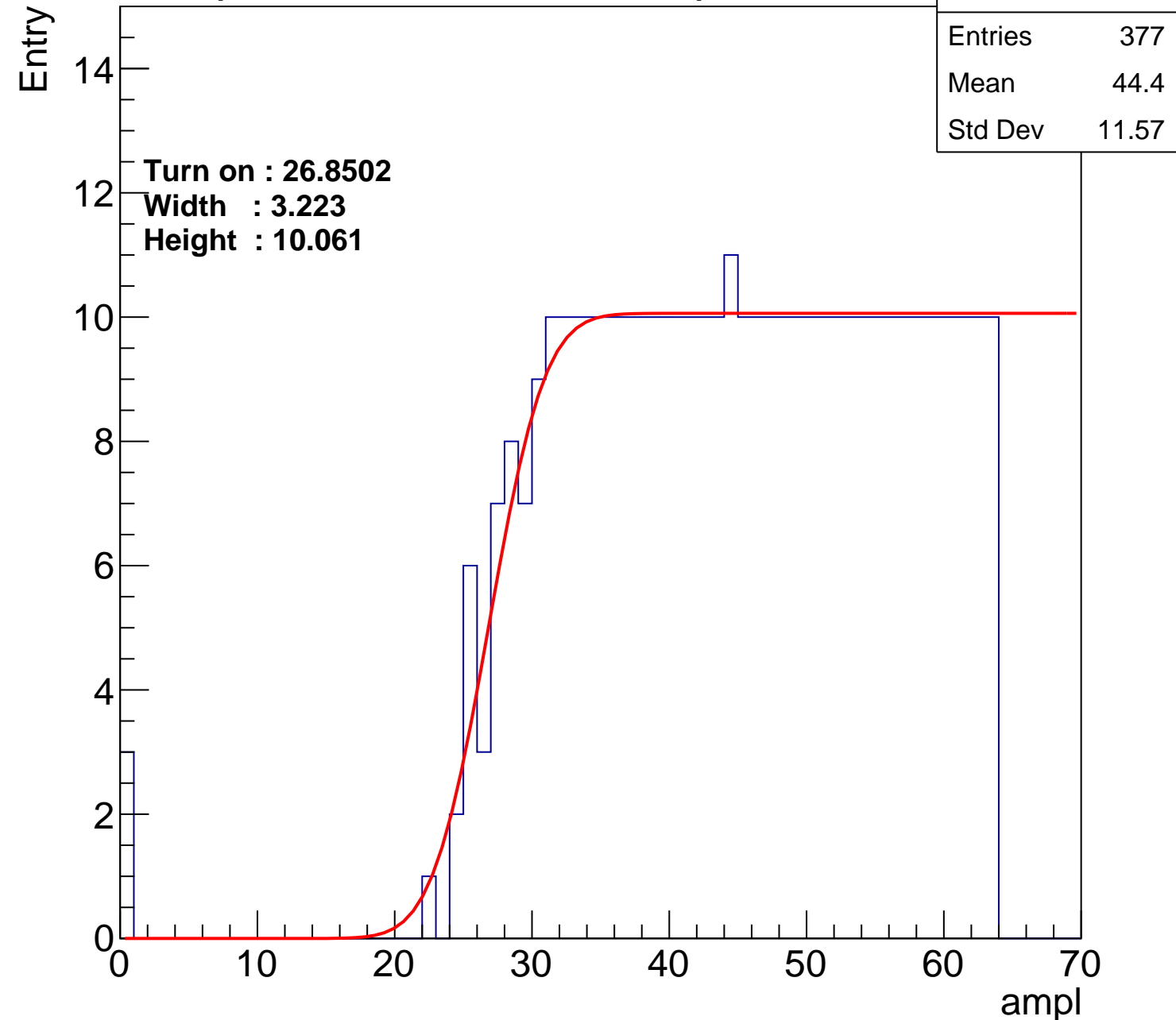
Width : 3.223

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch103

calib\_packv5\_042523\_0143.root, FC#5, port B1

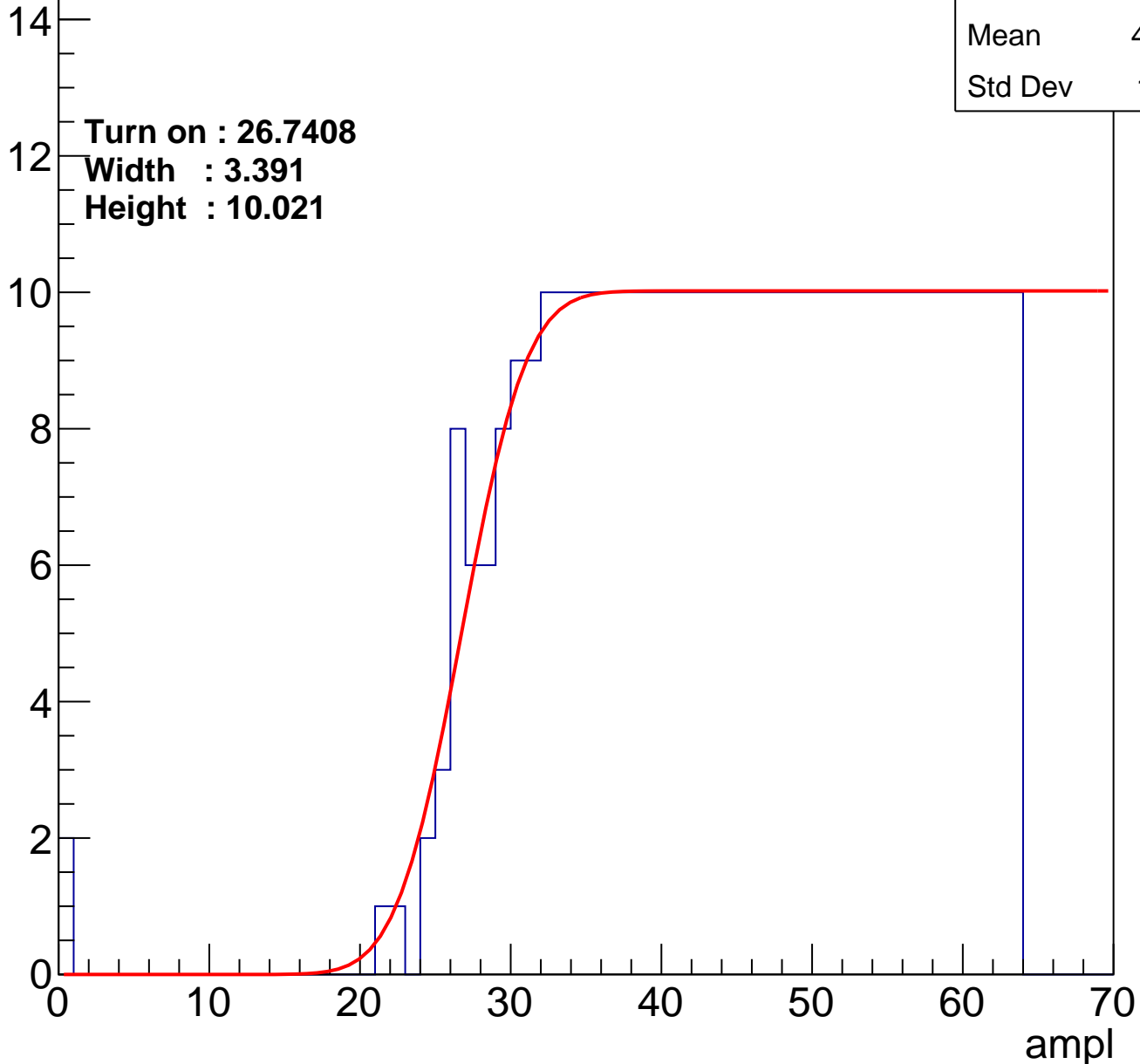
Entries	375
Mean	44.49
Std Dev	11.41

Turn on : 26.7408

Width : 3.391

Height : 10.021

Entry



# B0L000S, U26-ch104

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.95
Std Dev	11.01

Turn on : 27.8023

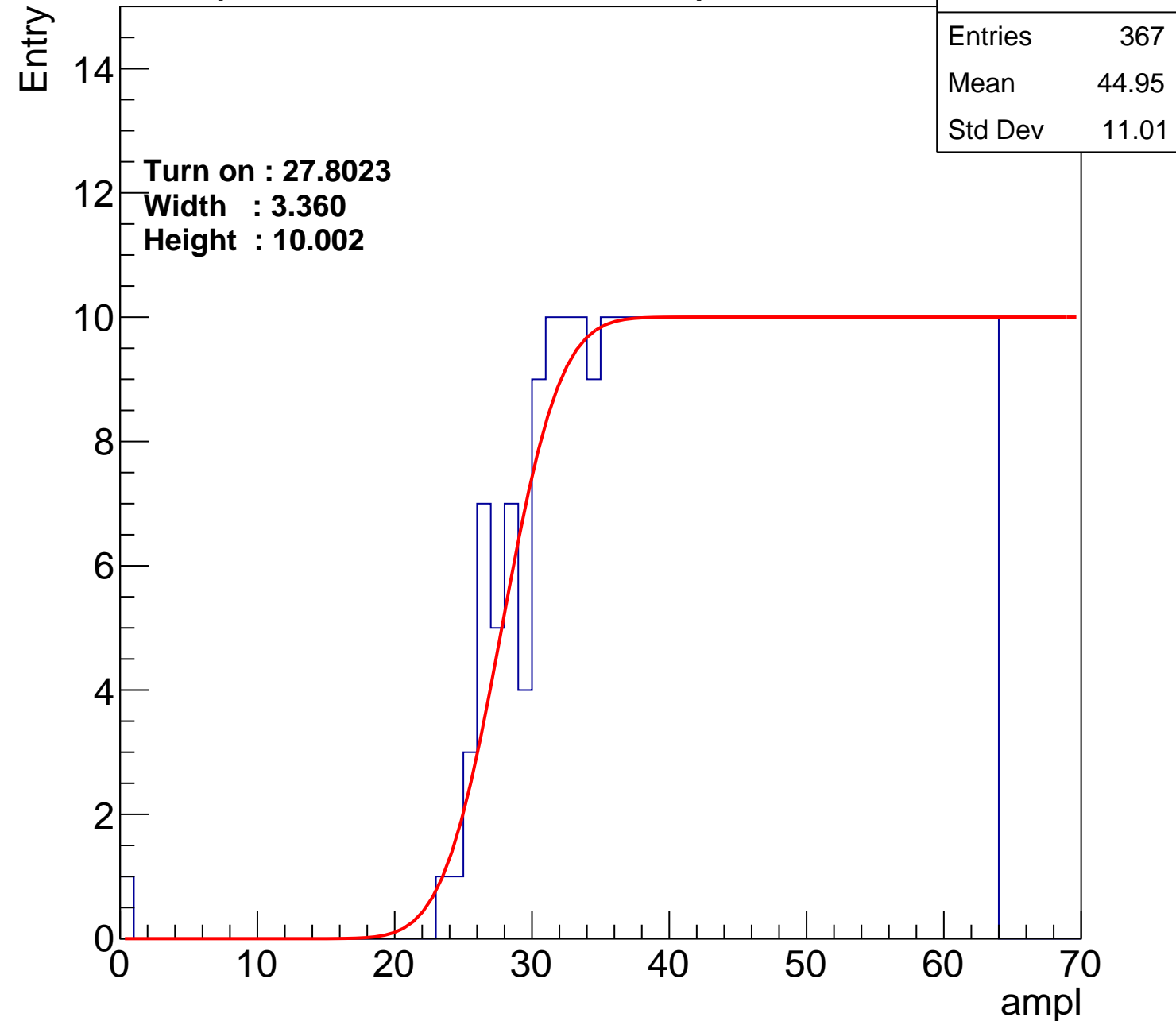
Width : 3.360

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch105

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.79
Std Dev	11.24

Turn on : 27.5968

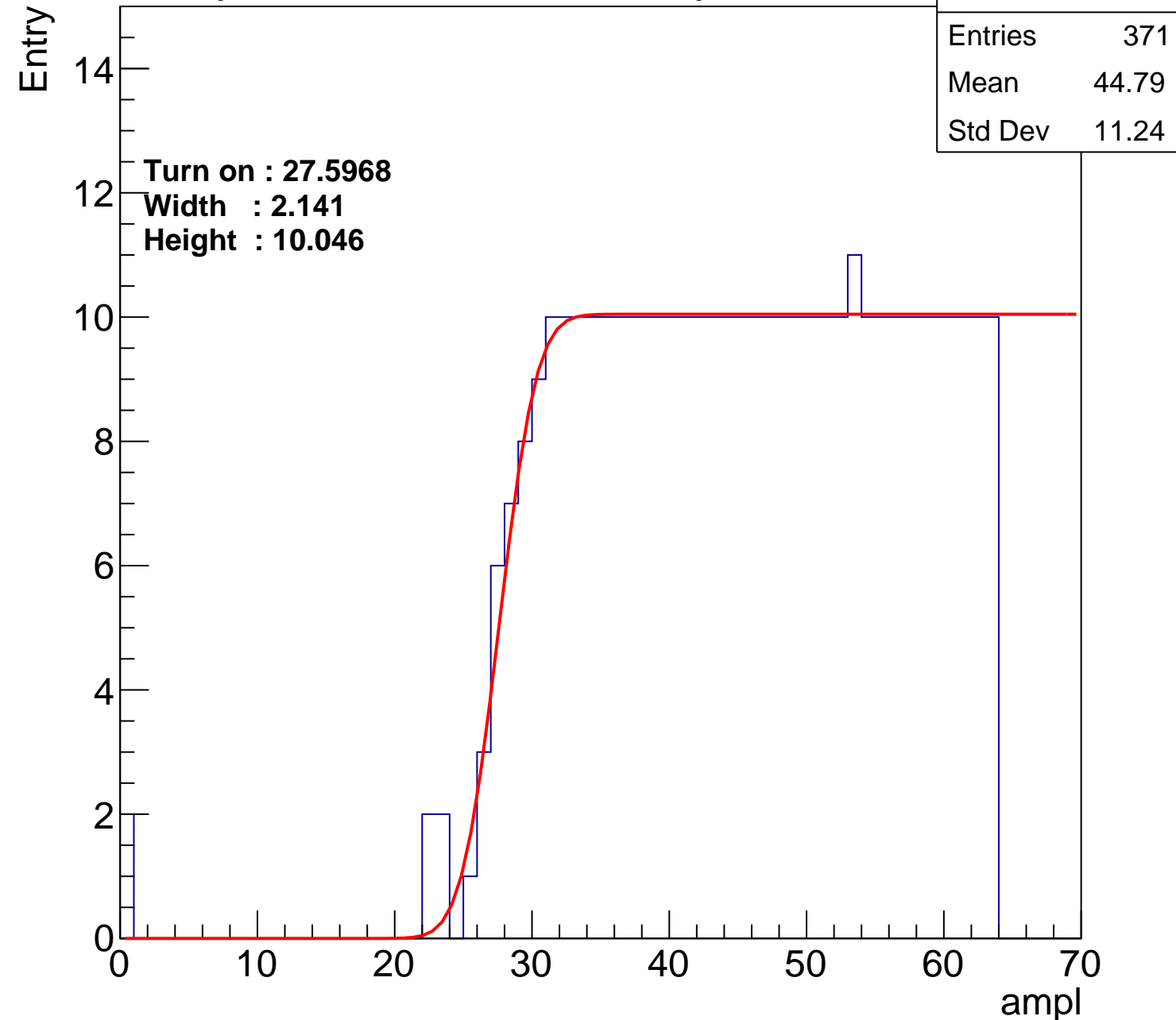
Width : 2.141

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch106

calib\_packv5\_042523\_0143.root, FC#5, port B1

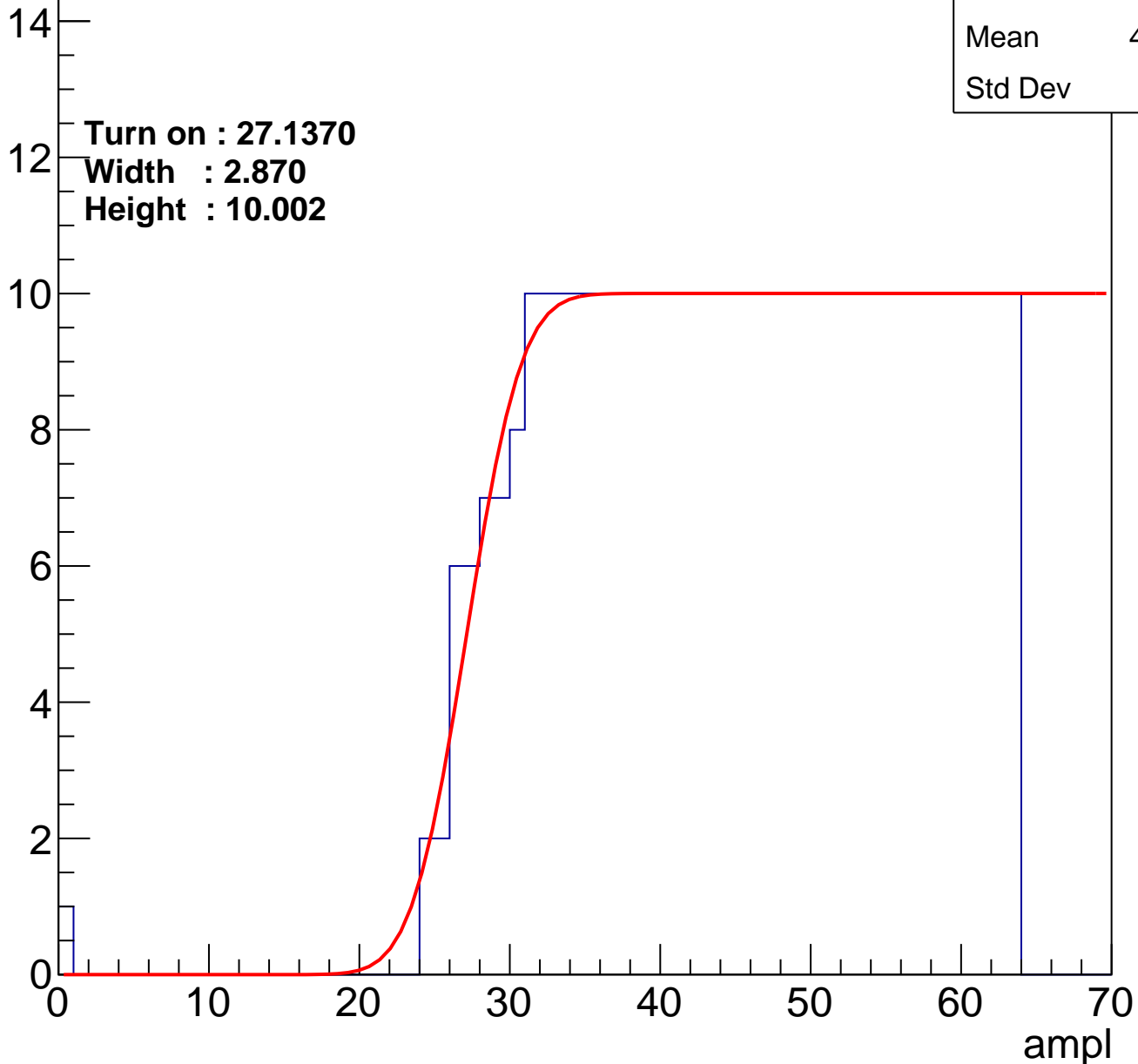
Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.1370

Width : 2.870

Height : 10.002

Entry



# B0L000S, U26-ch107

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.83
Std Dev	11.11

Turn on : 27.6390

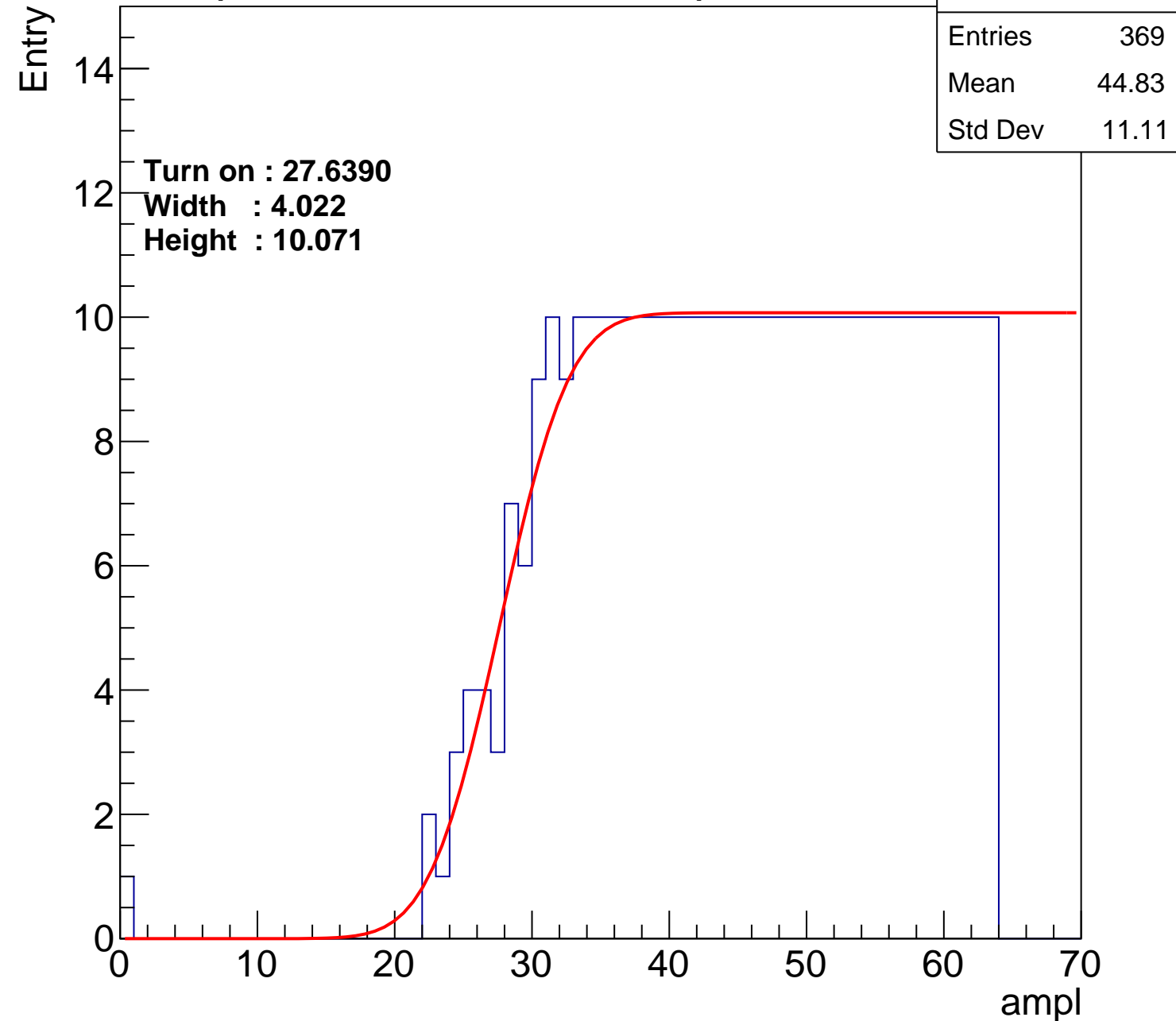
Width : 4.022

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch108

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	361
Mean	45.11
Std Dev	11.27

**Turn on : 28.3027**

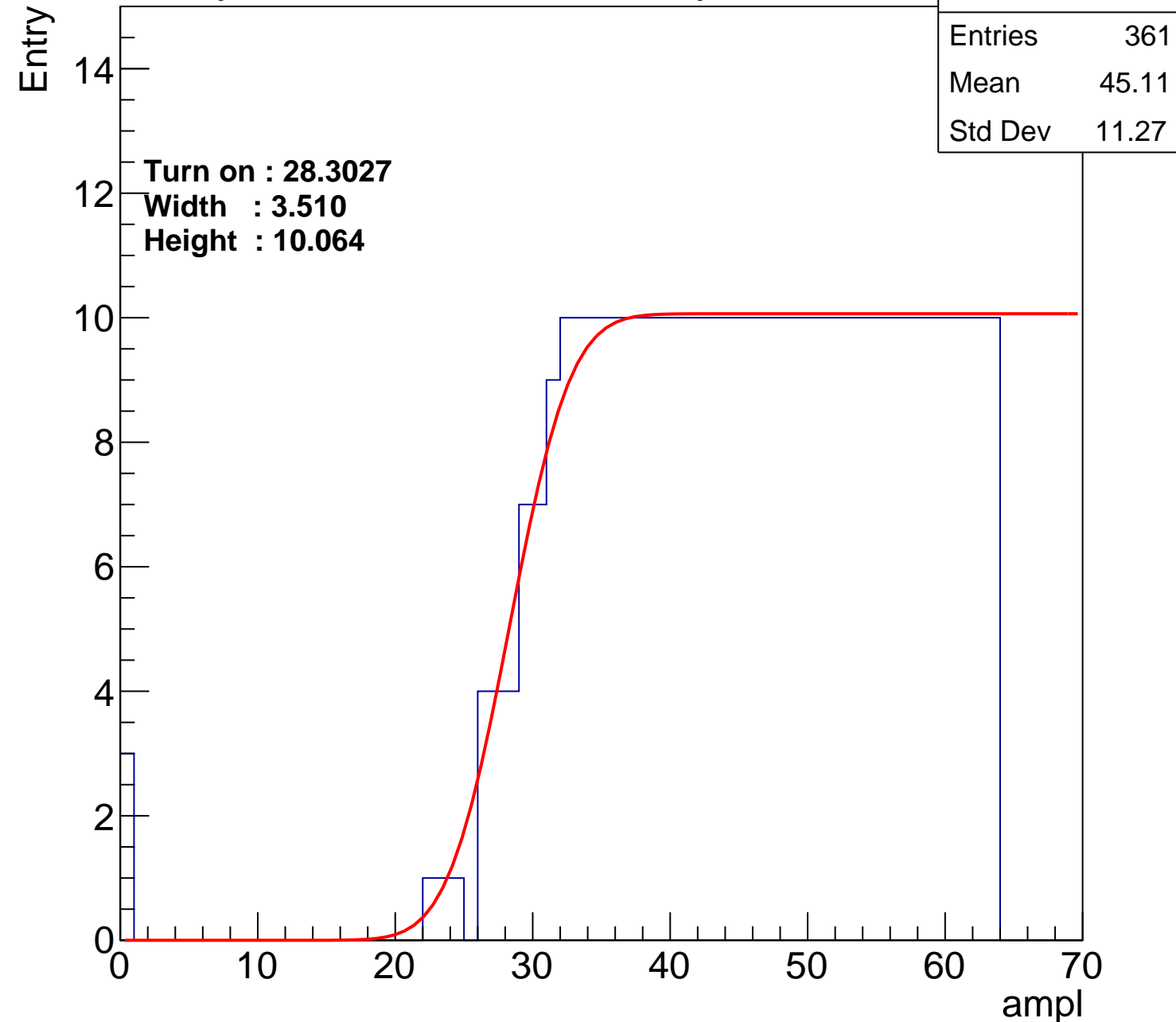
**Width : 3.510**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch109

calib\_packv5\_042523\_0143.root, FC#5, port B1

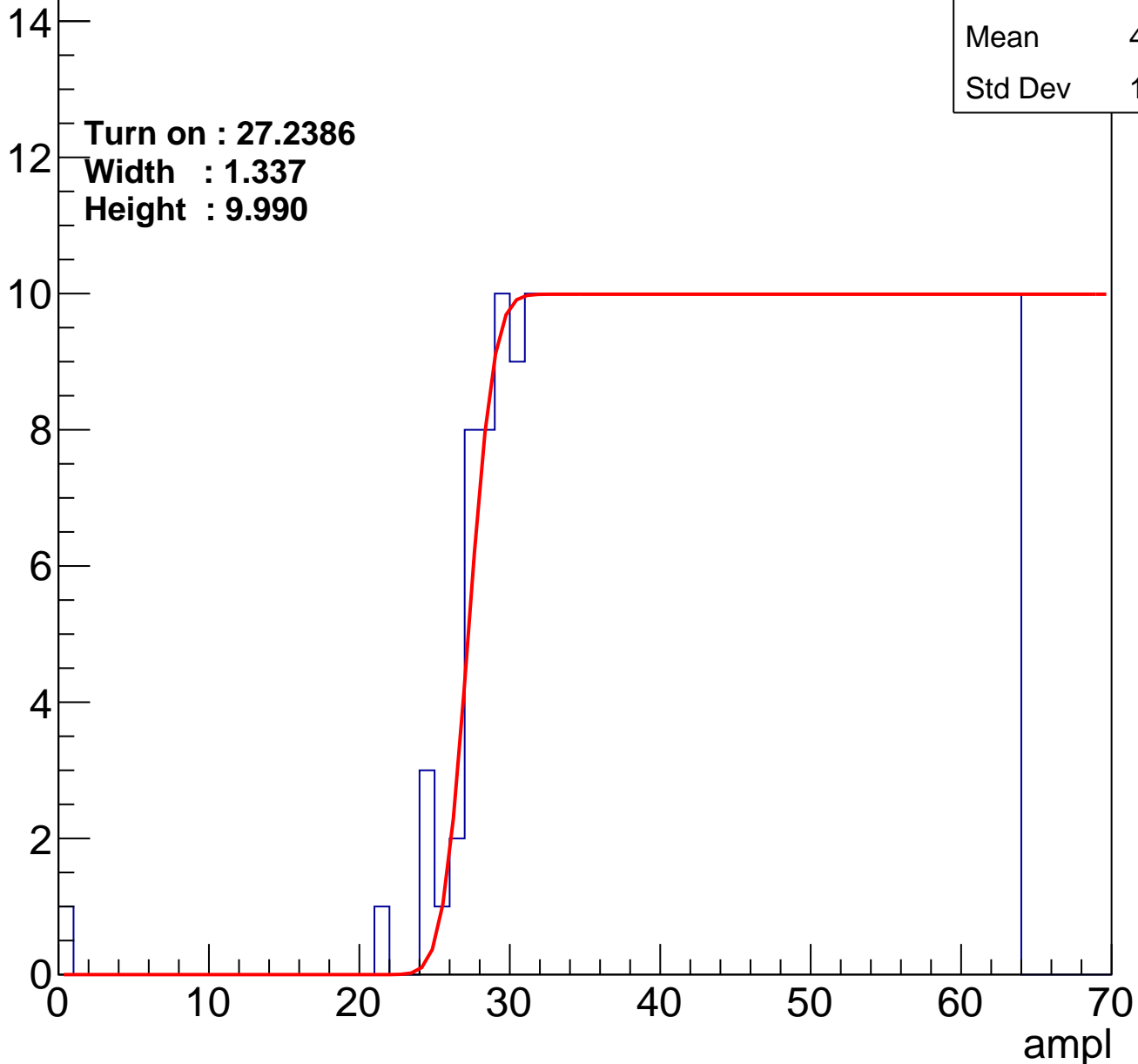
Entries	373
Mean	44.72
Std Dev	11.07

**Turn on : 27.2386**

**Width : 1.337**

**Height : 9.990**

Entry



# B0L000S, U26-ch110

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.51
Std Dev	11.66

Turn on : 27.1873

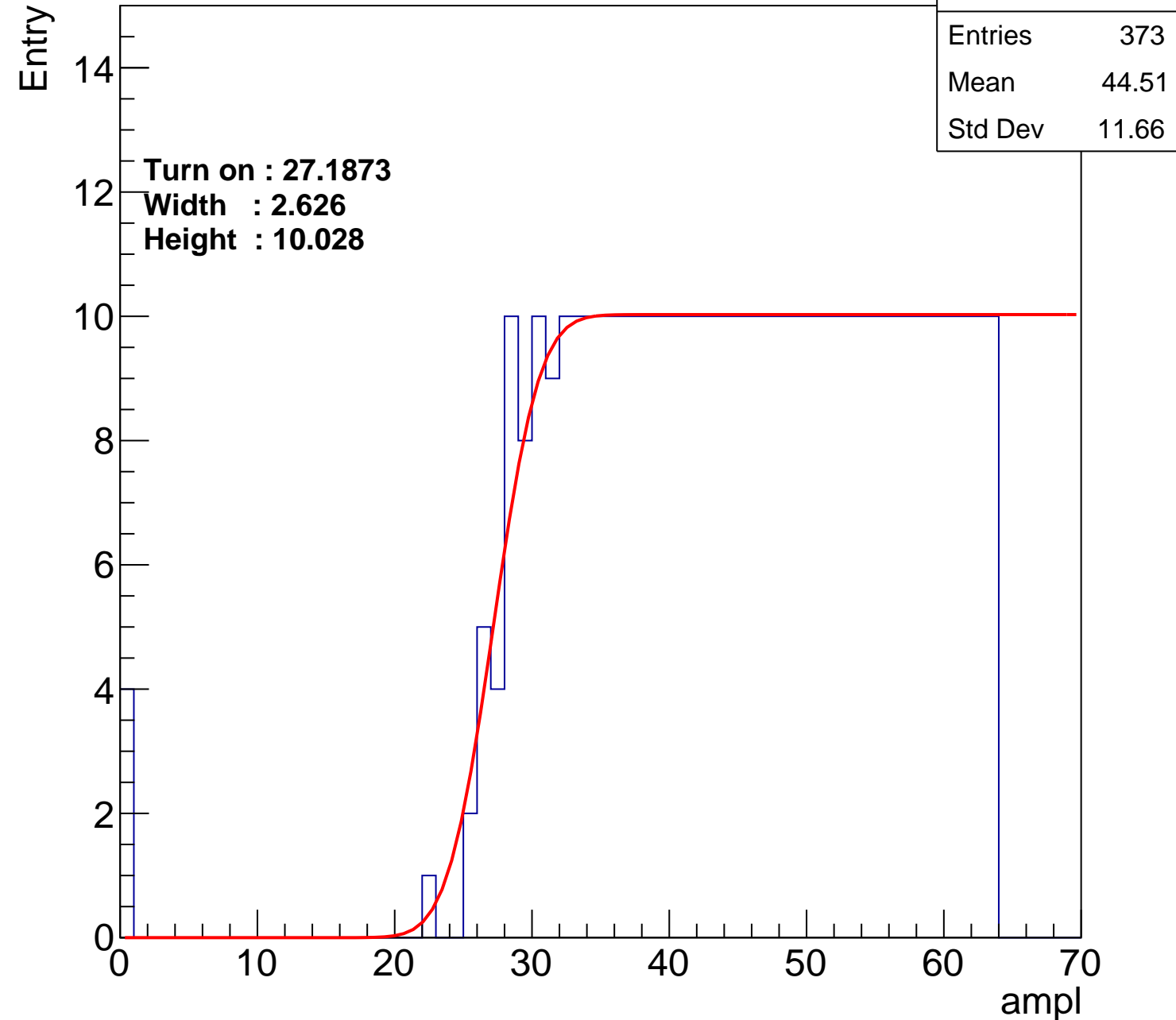
Width : 2.626

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch111

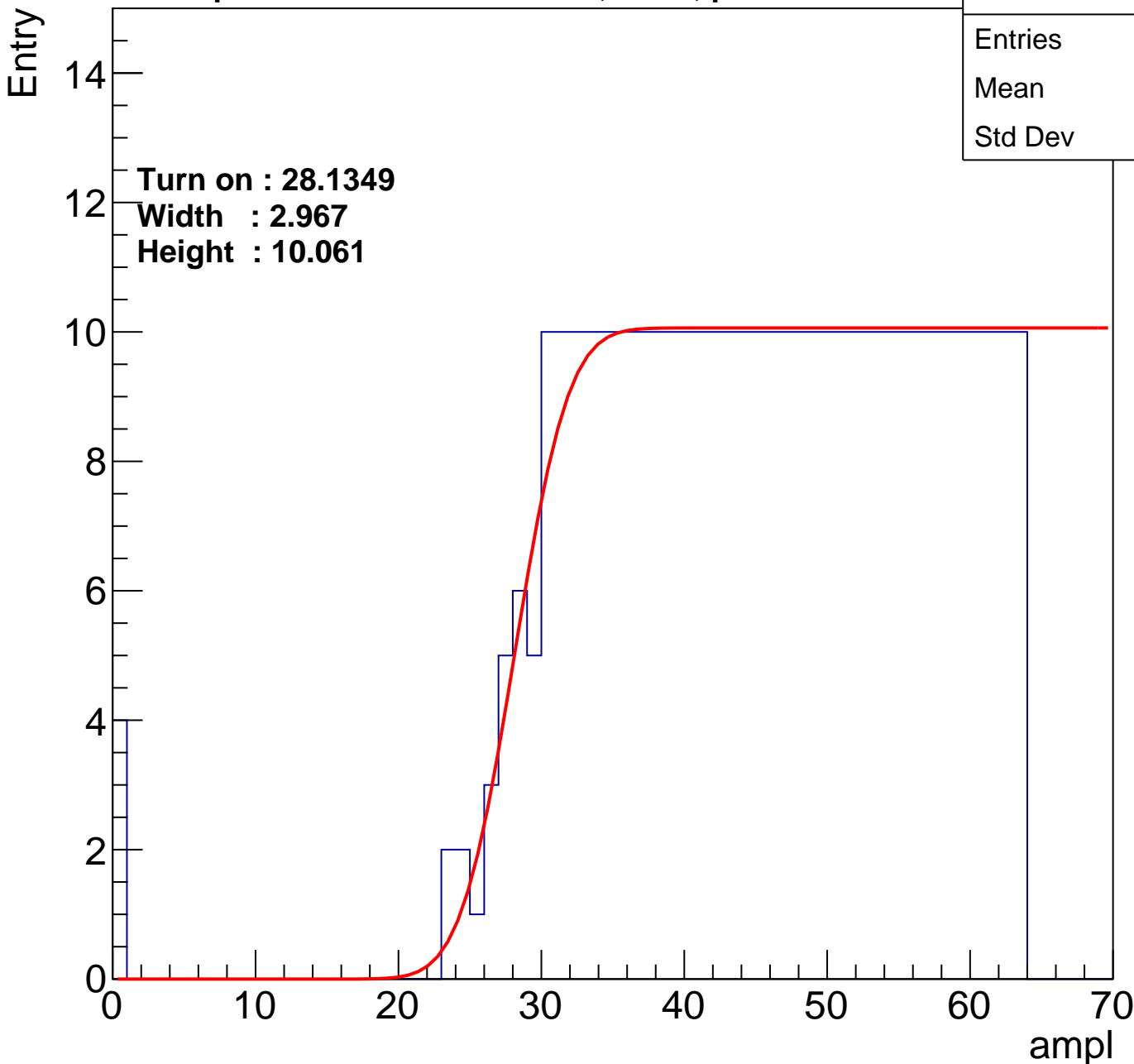
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	368
Mean	44.71
Std Dev	11.6

**Turn on : 28.1349**

**Width : 2.967**

**Height : 10.061**



# B0L000S, U26-ch112

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.35
Std Dev	11.62

**Turn on : 26.7787**

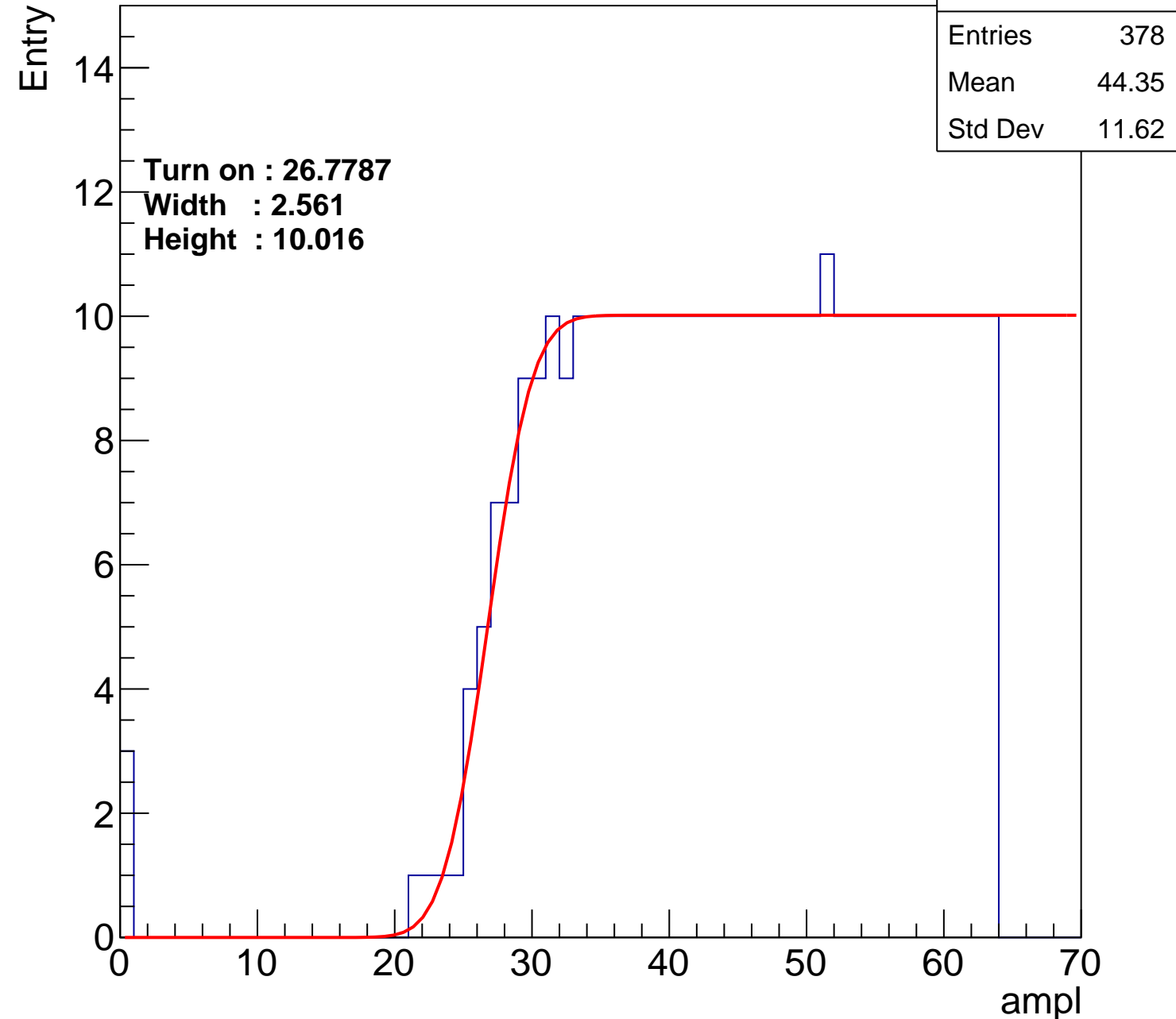
**Width : 2.561**

**Height : 10.016**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch113

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.52
Std Dev	11.56

Turn on : 27.4600

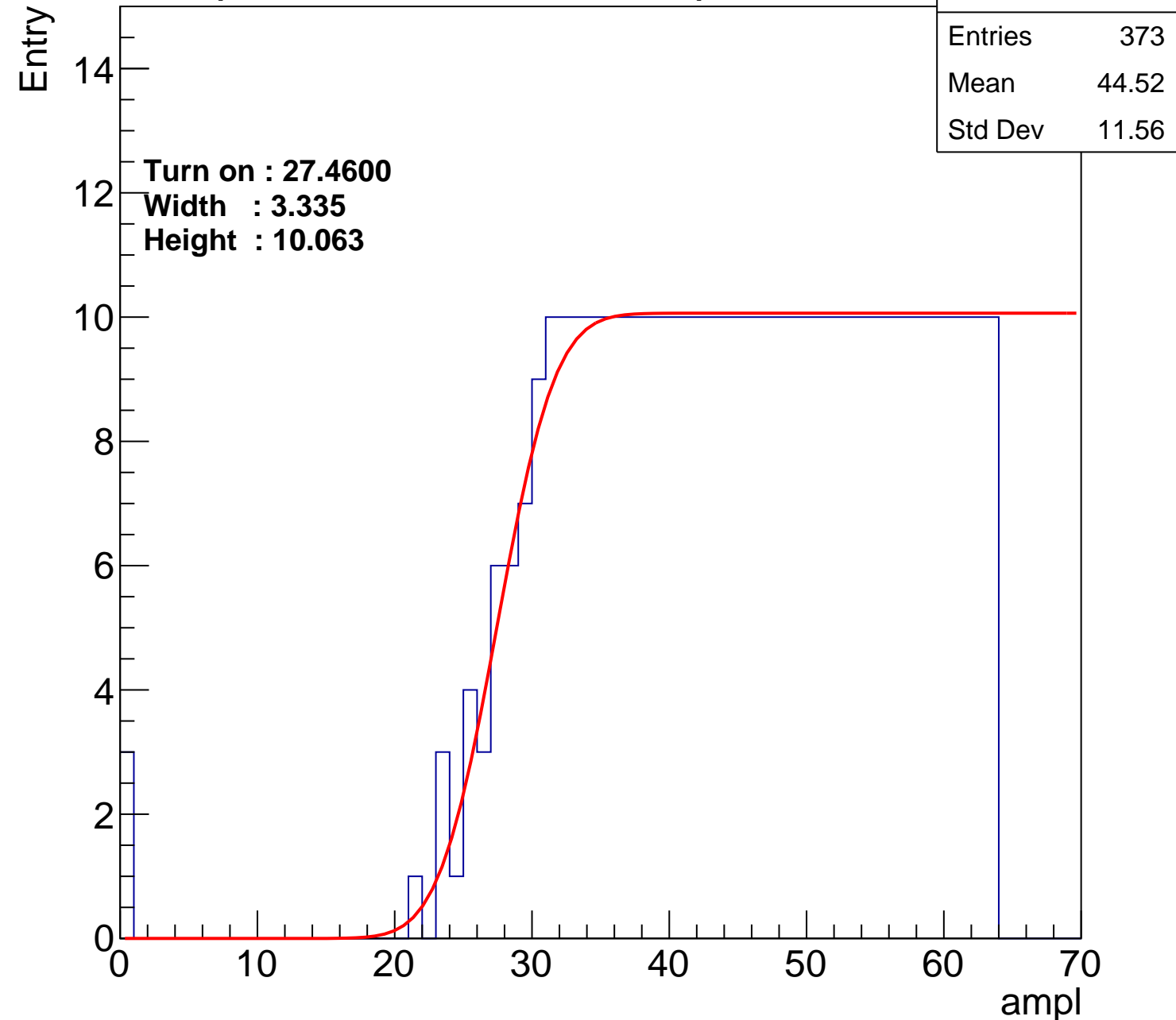
Width : 3.335

Height : 10.063

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch114

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.42
Std Dev	11.48

Turn on : 27.1348

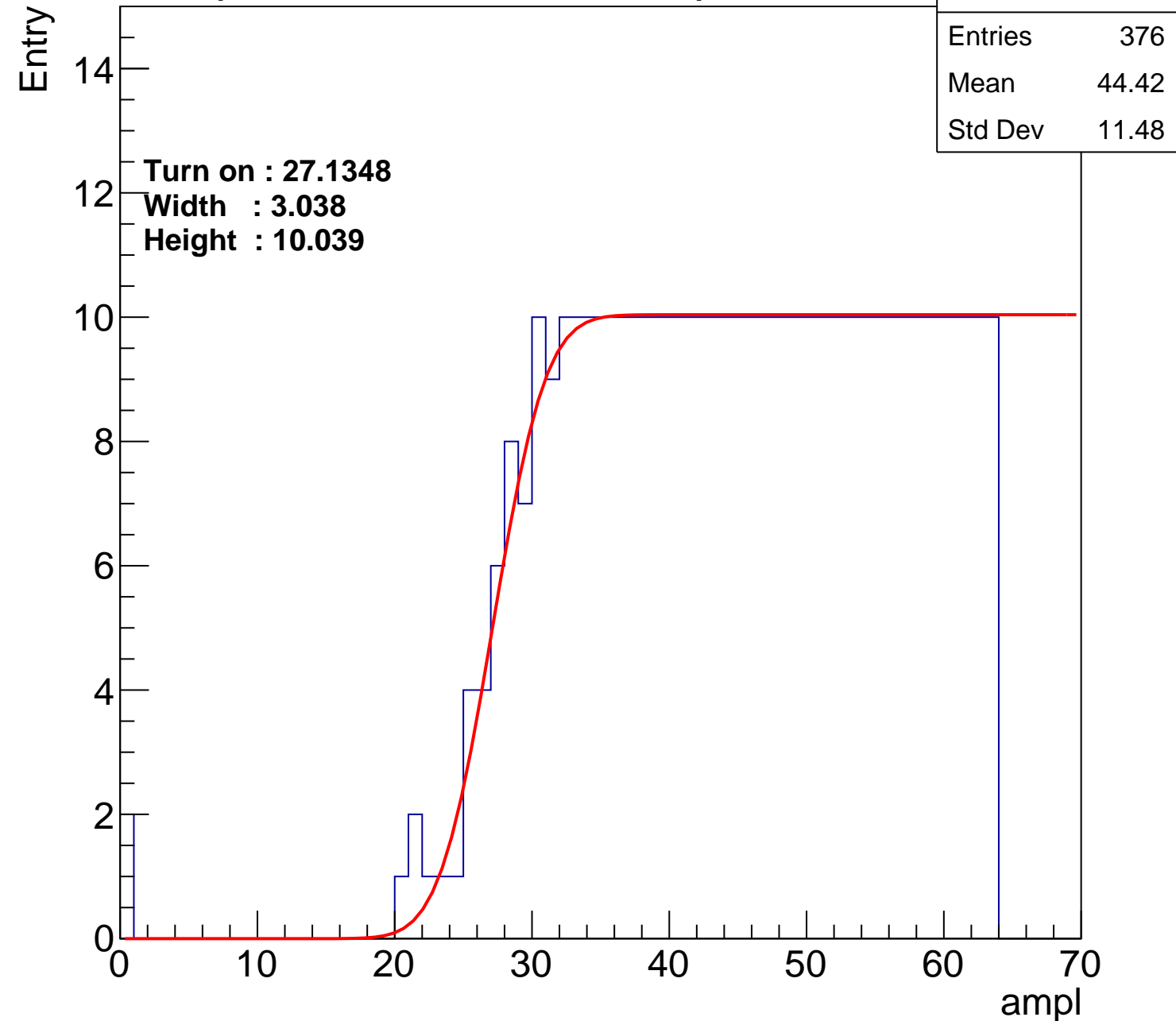
Width : 3.038

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch115

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.96
Std Dev	11.17

Turn on : 27.8564

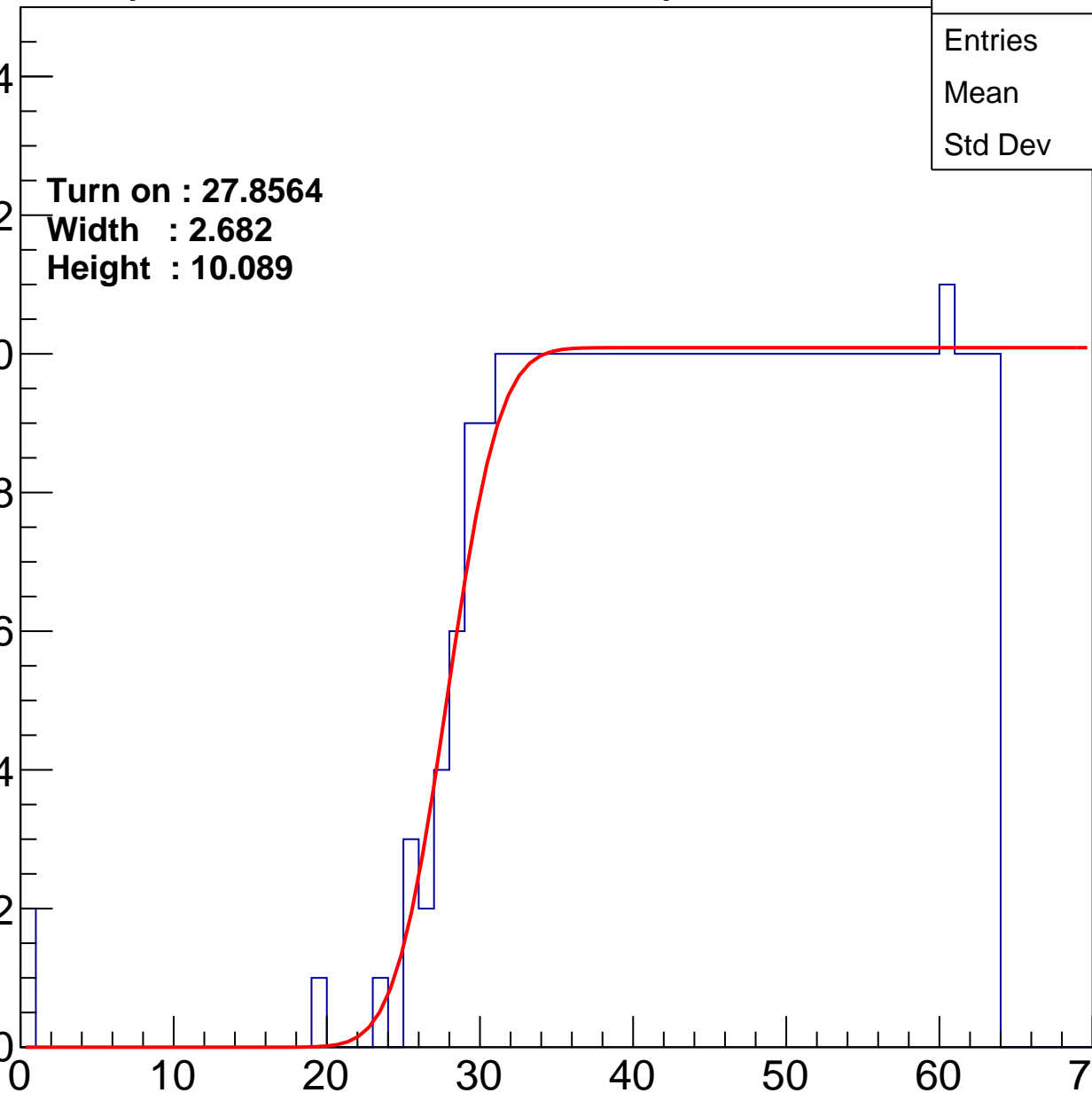
Width : 2.682

Height : 10.089

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch116

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.8
Std Dev	11.3

Turn on : 28.0163

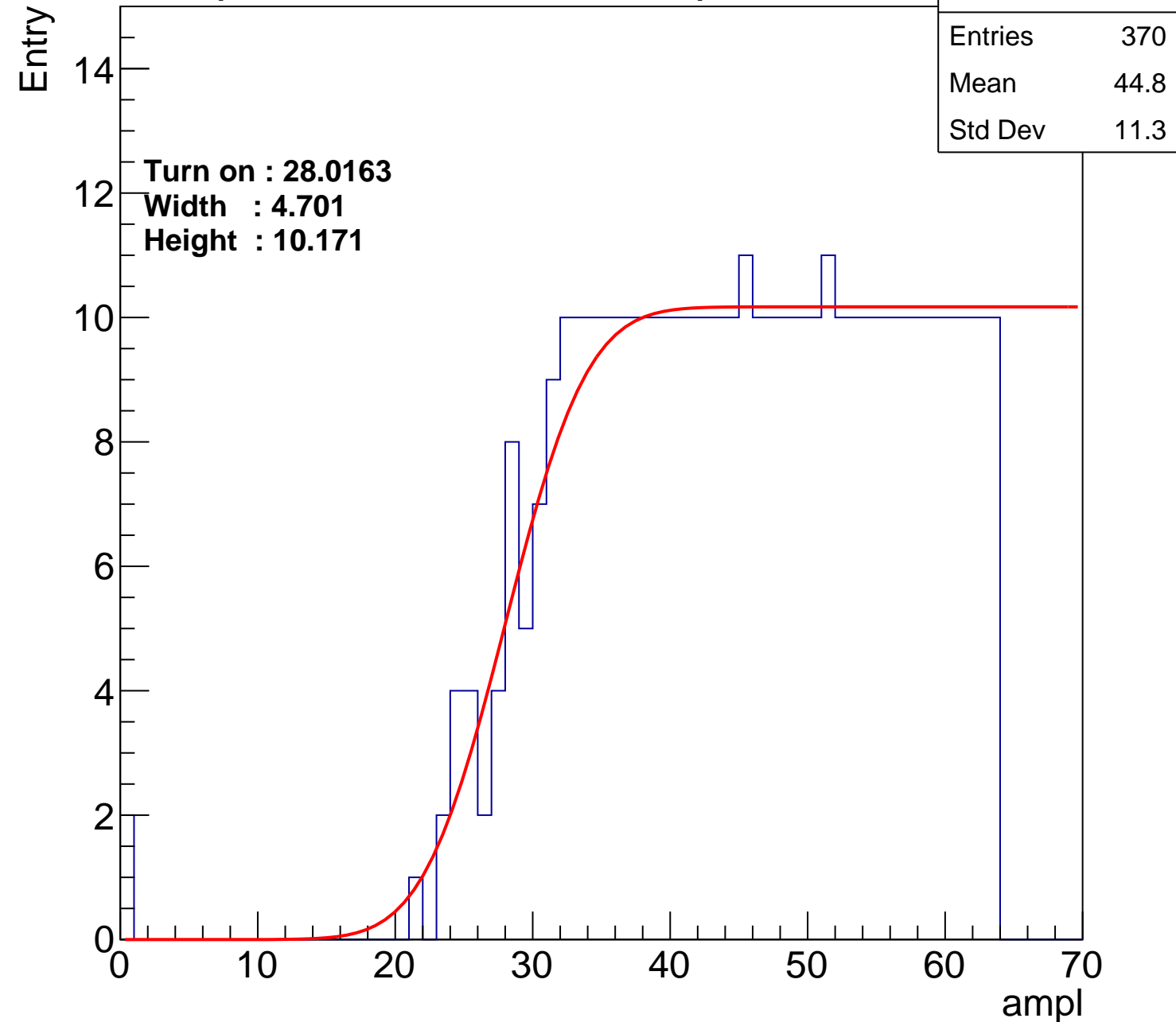
Width : 4.701

Height : 10.171

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch117

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 28.0949

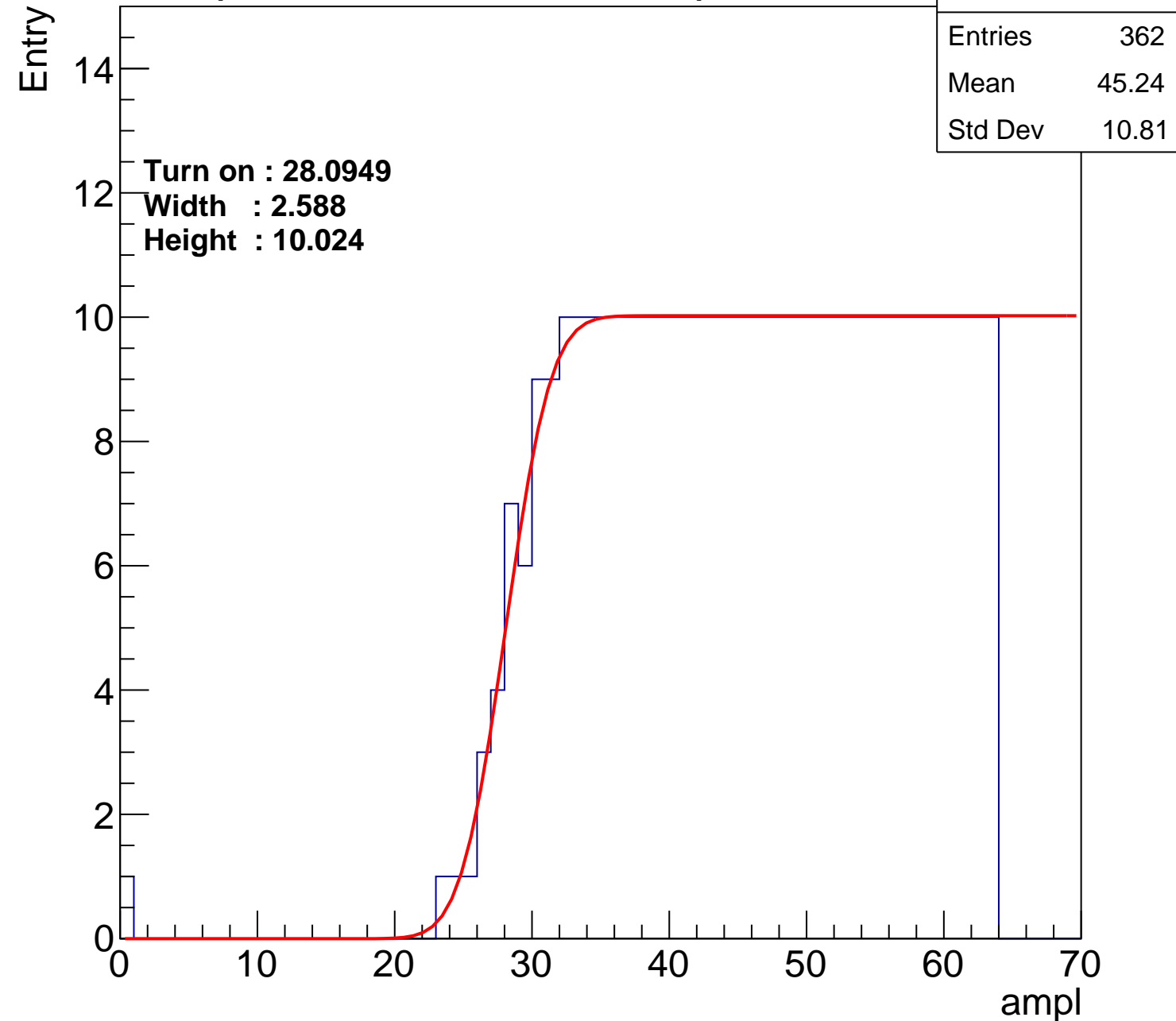
Width : 2.588

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch118

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 27.7417

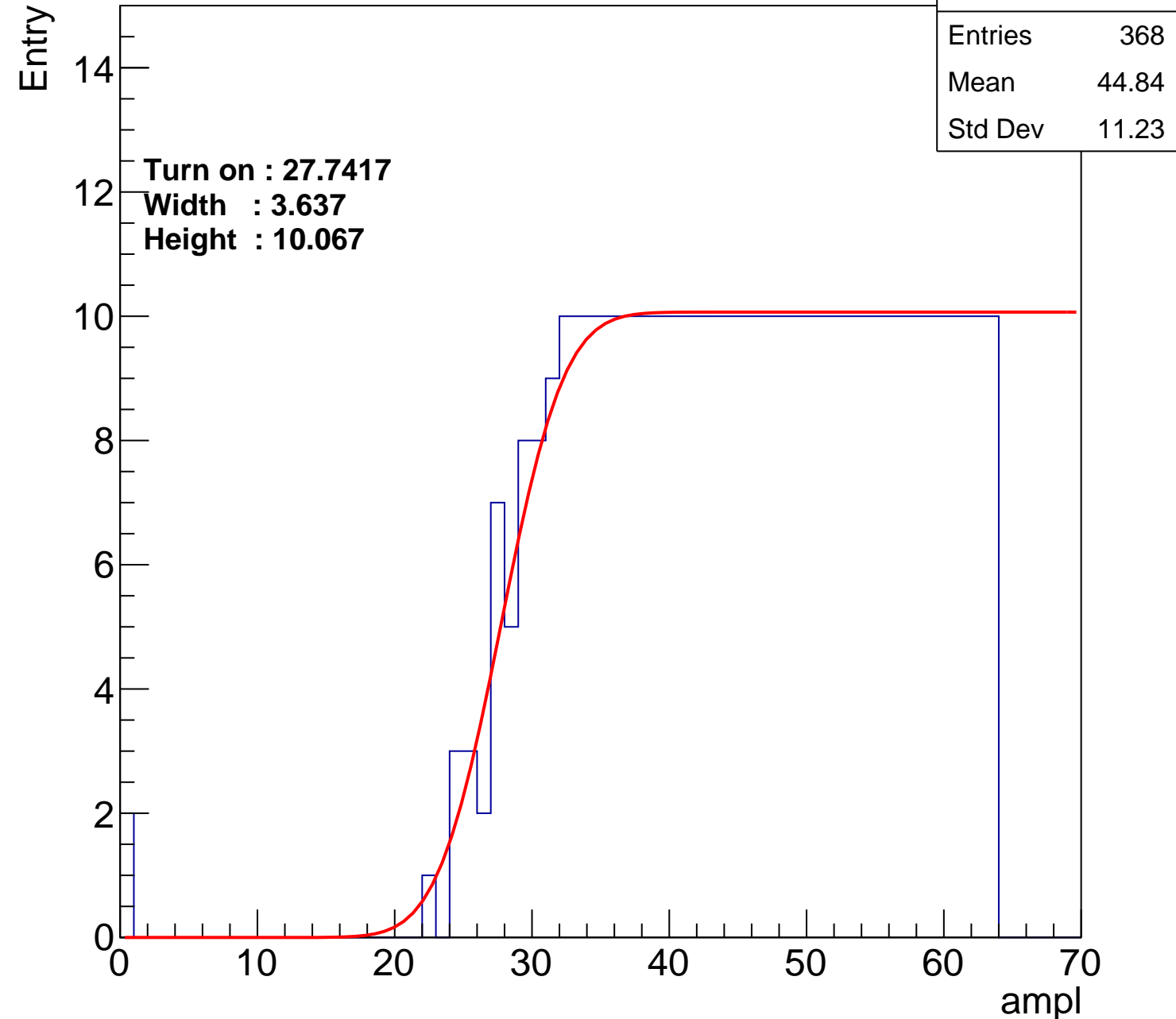
Width : 3.637

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch119

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	395
Mean	43.46
Std Dev	12.07

**Turn on : 24.8795**

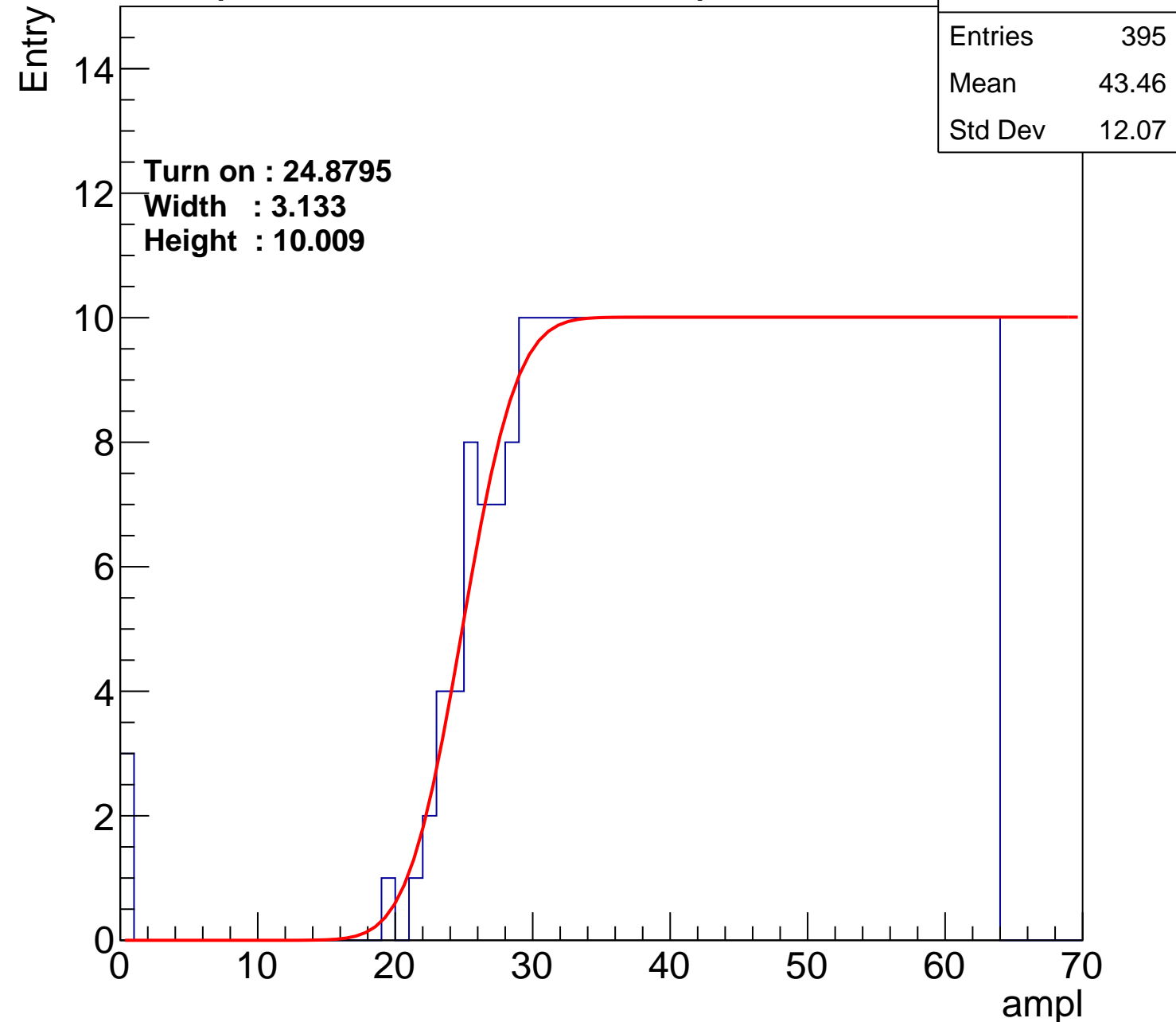
**Width : 3.133**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch120

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.82
Std Dev	11.07

**Turn on : 26.9390**

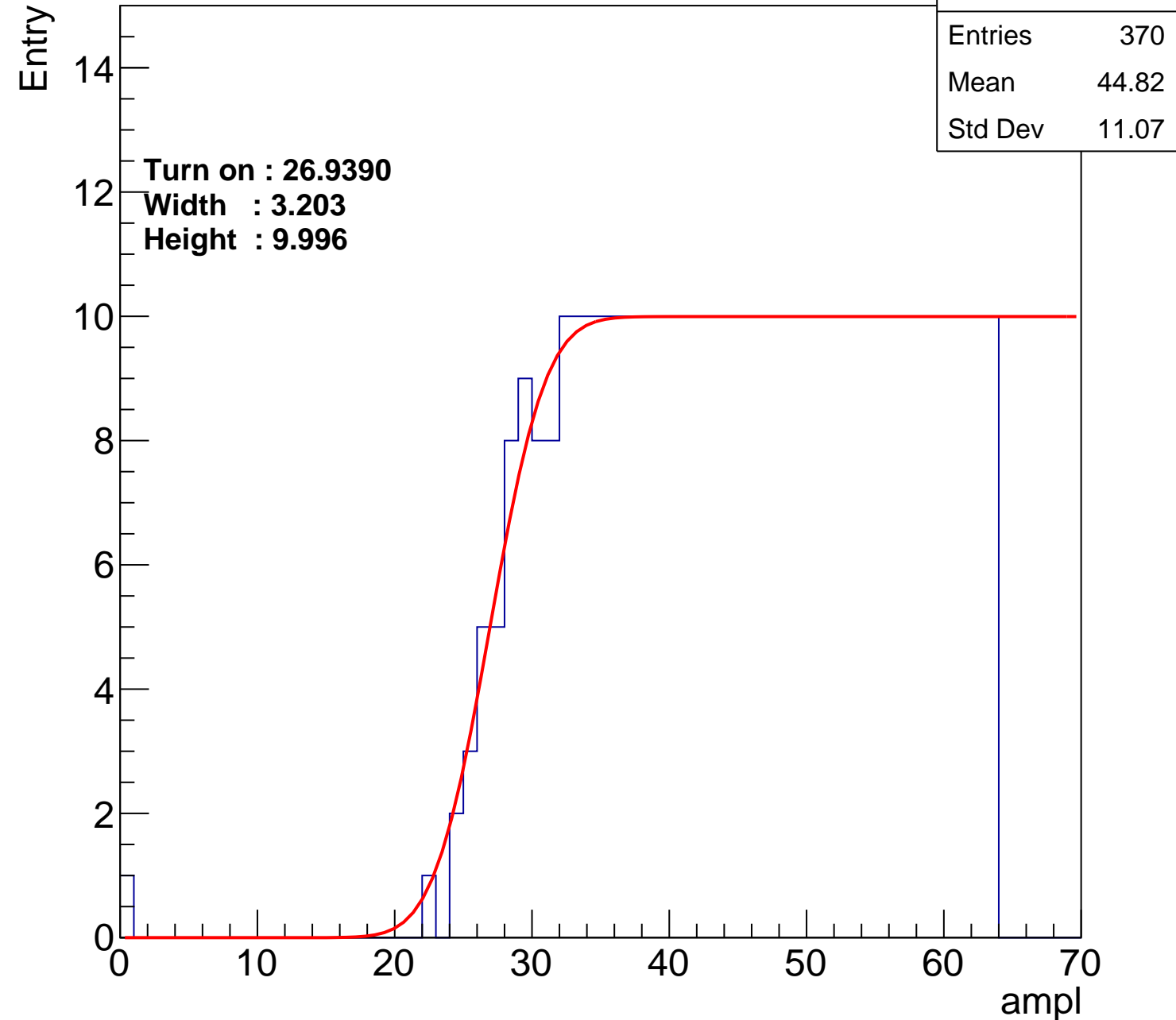
**Width : 3.203**

**Height : 9.996**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch121

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.45
Std Dev	11.57

Turn on : 26.9314

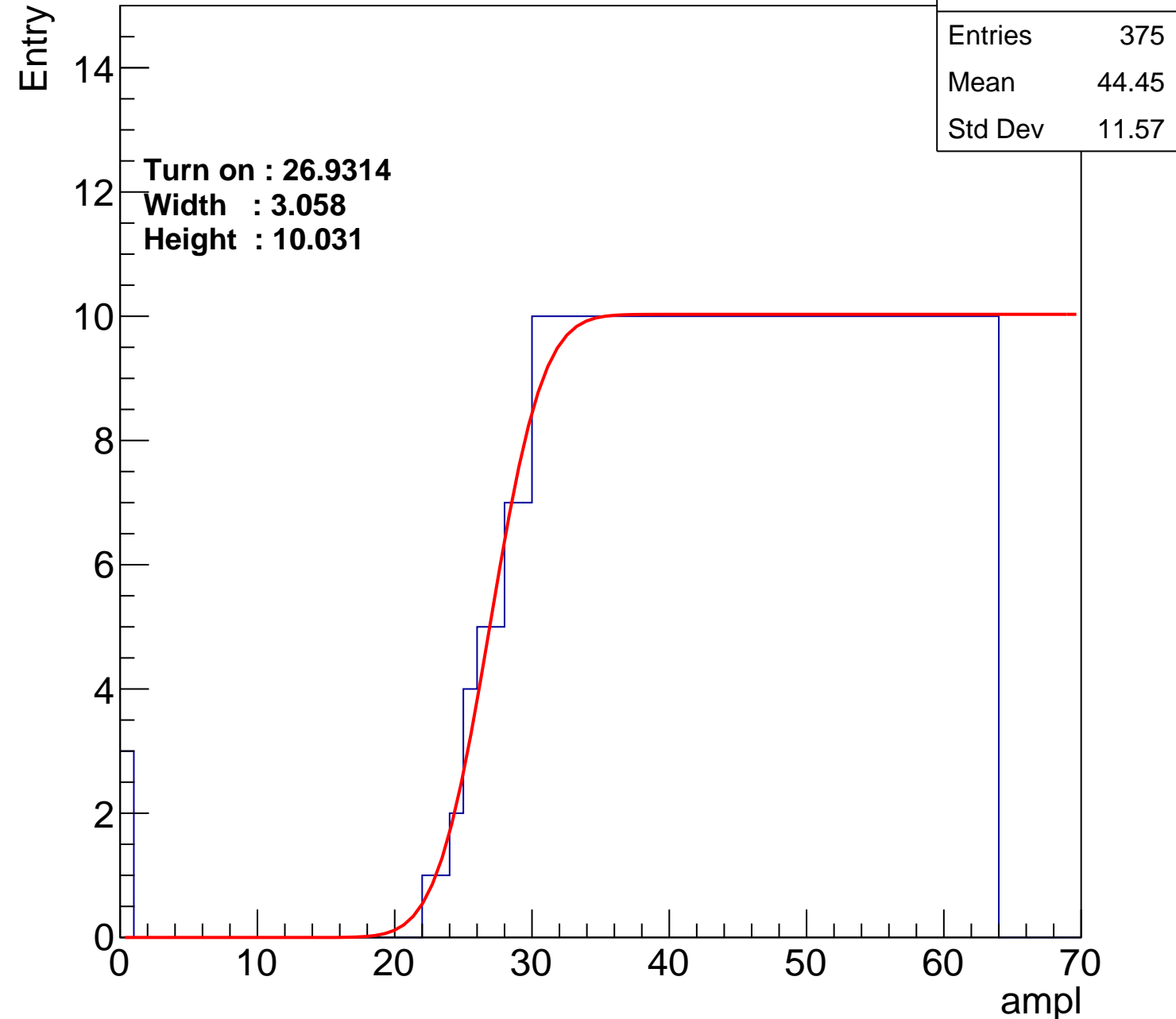
Width : 3.058

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch122

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	355
Mean	45.55
Std Dev	10.69

**Turn on : 28.8692**

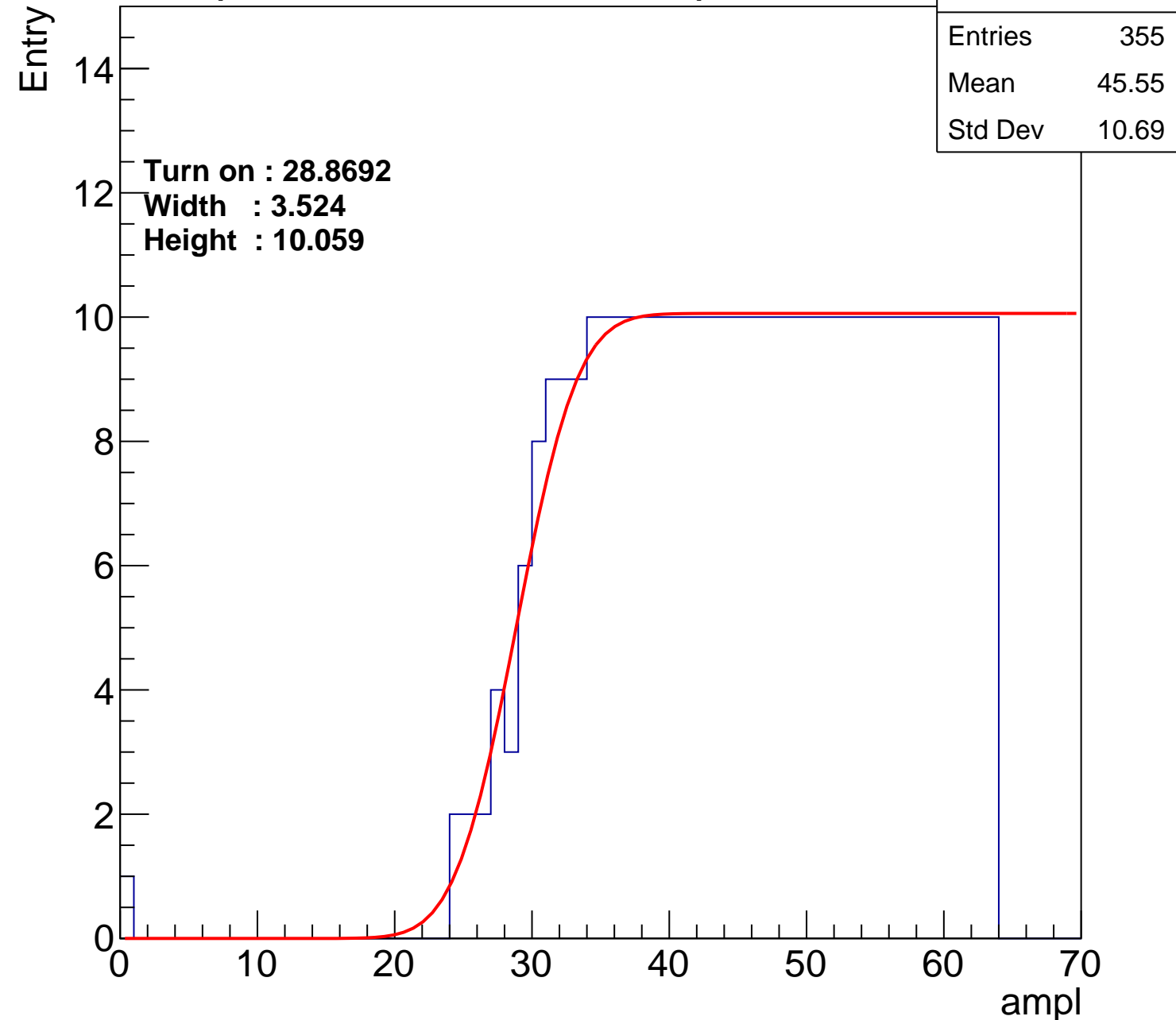
**Width : 3.524**

**Height : 10.059**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch123

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.4
Std Dev	11.51

Turn on : 26.7564

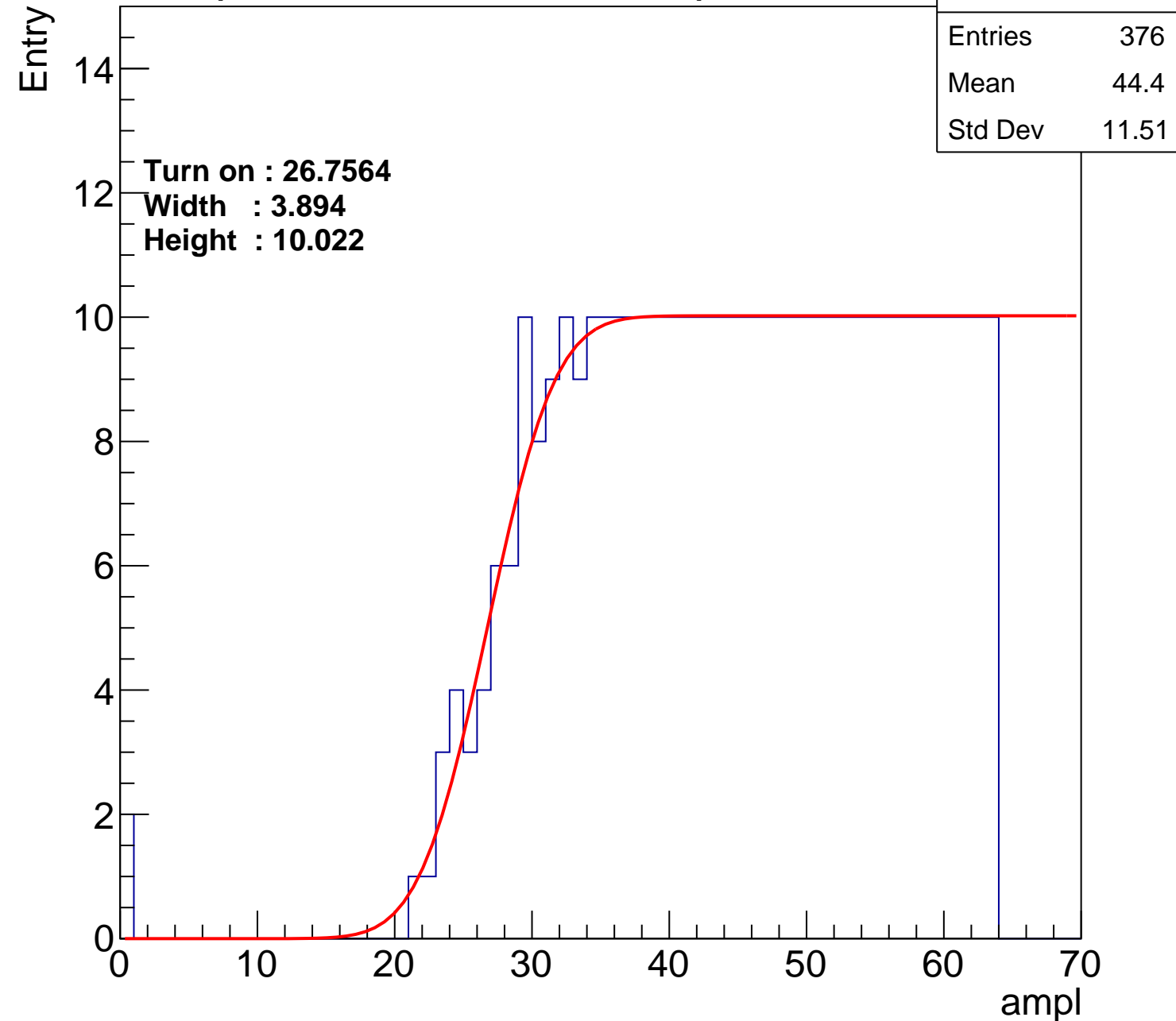
Width : 3.894

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U26-ch124

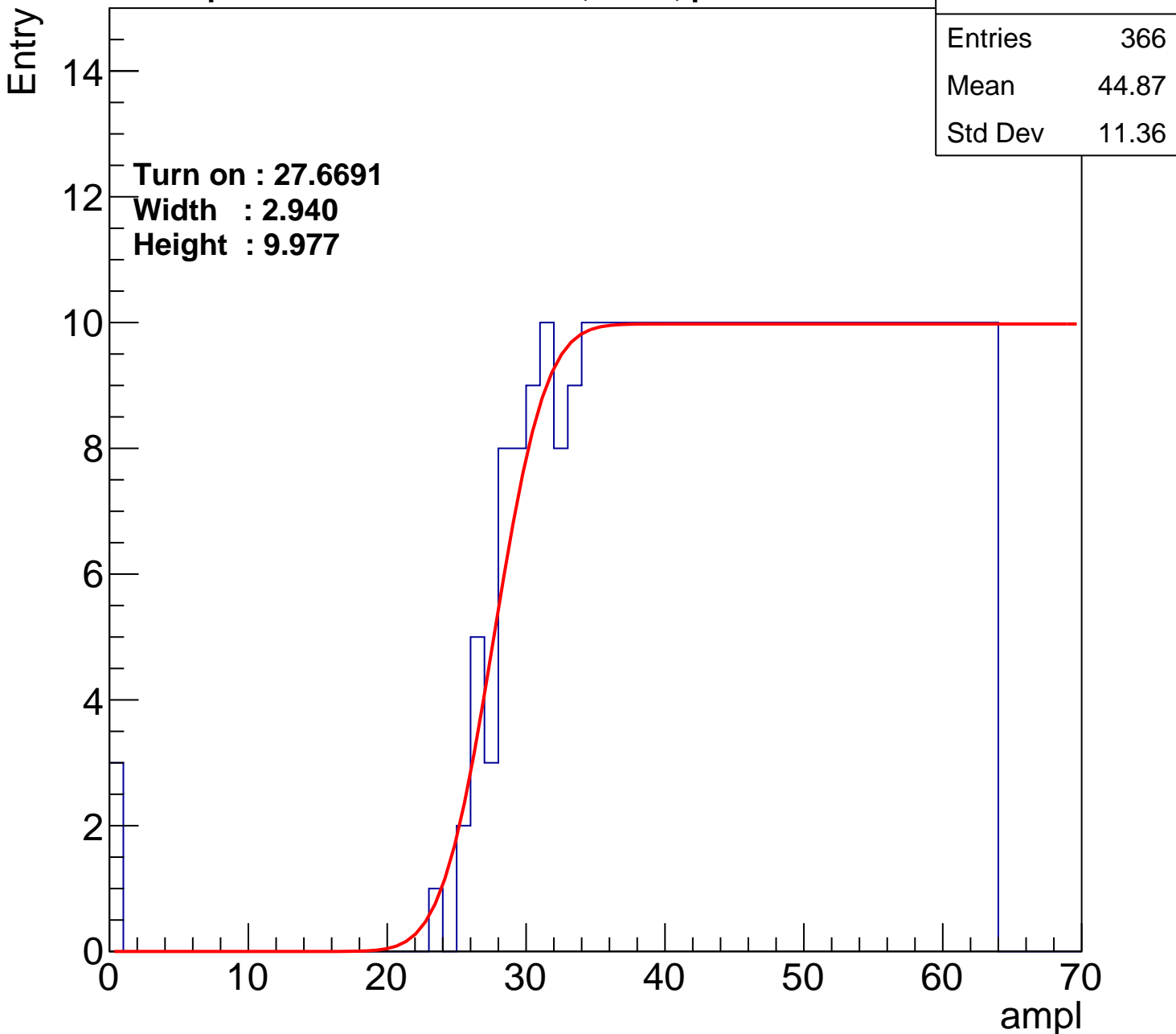
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	366
Mean	44.87
Std Dev	11.36

**Turn on : 27.6691**

**Width : 2.940**

**Height : 9.977**



# B0L000S, U26-ch125

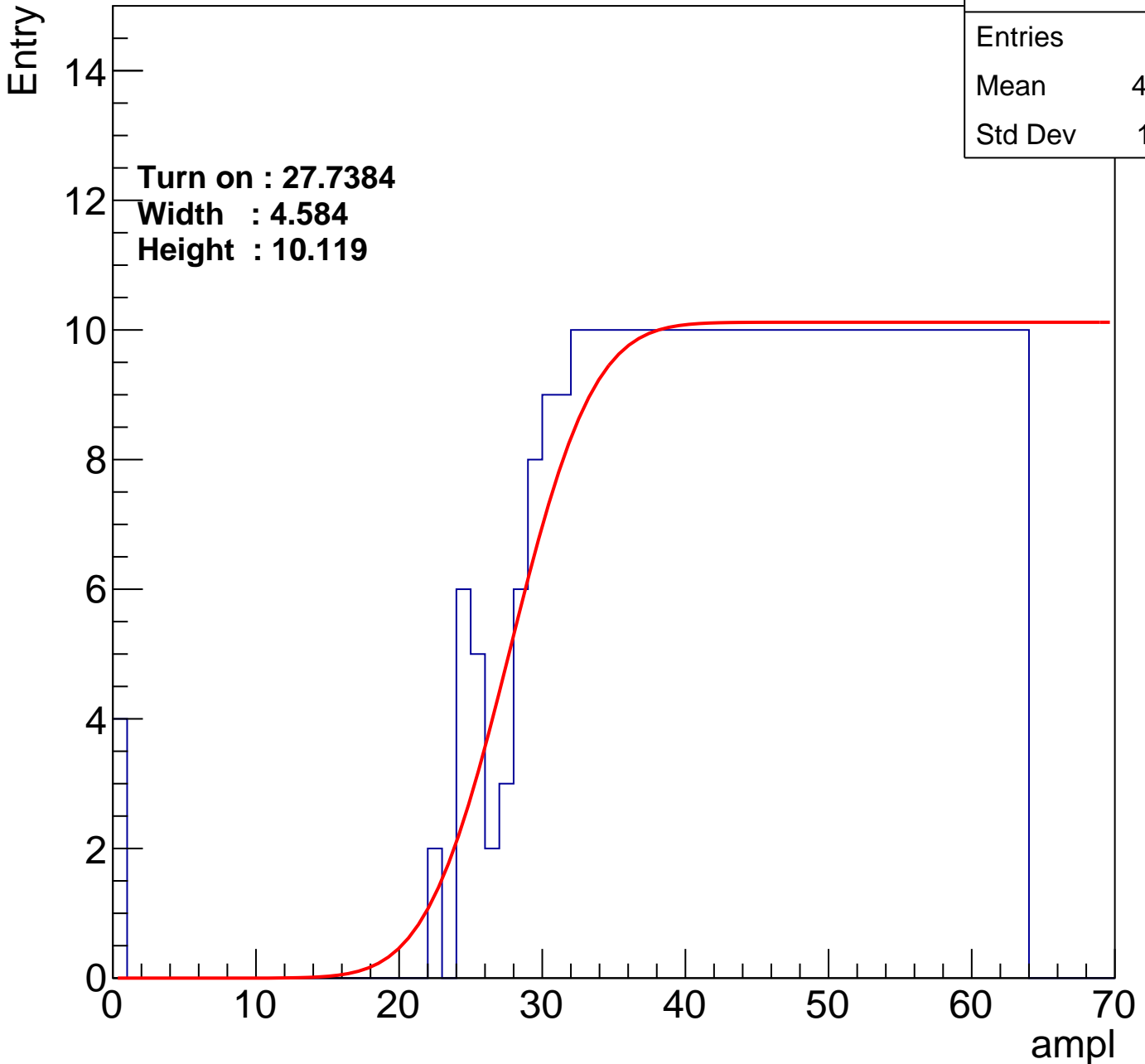
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.37
Std Dev	11.81

**Turn on : 27.7384**

**Width : 4.584**

**Height : 10.119**



# B0L000S, U26-ch126

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	44.99
Std Dev	11

**Turn on : 27.5023**

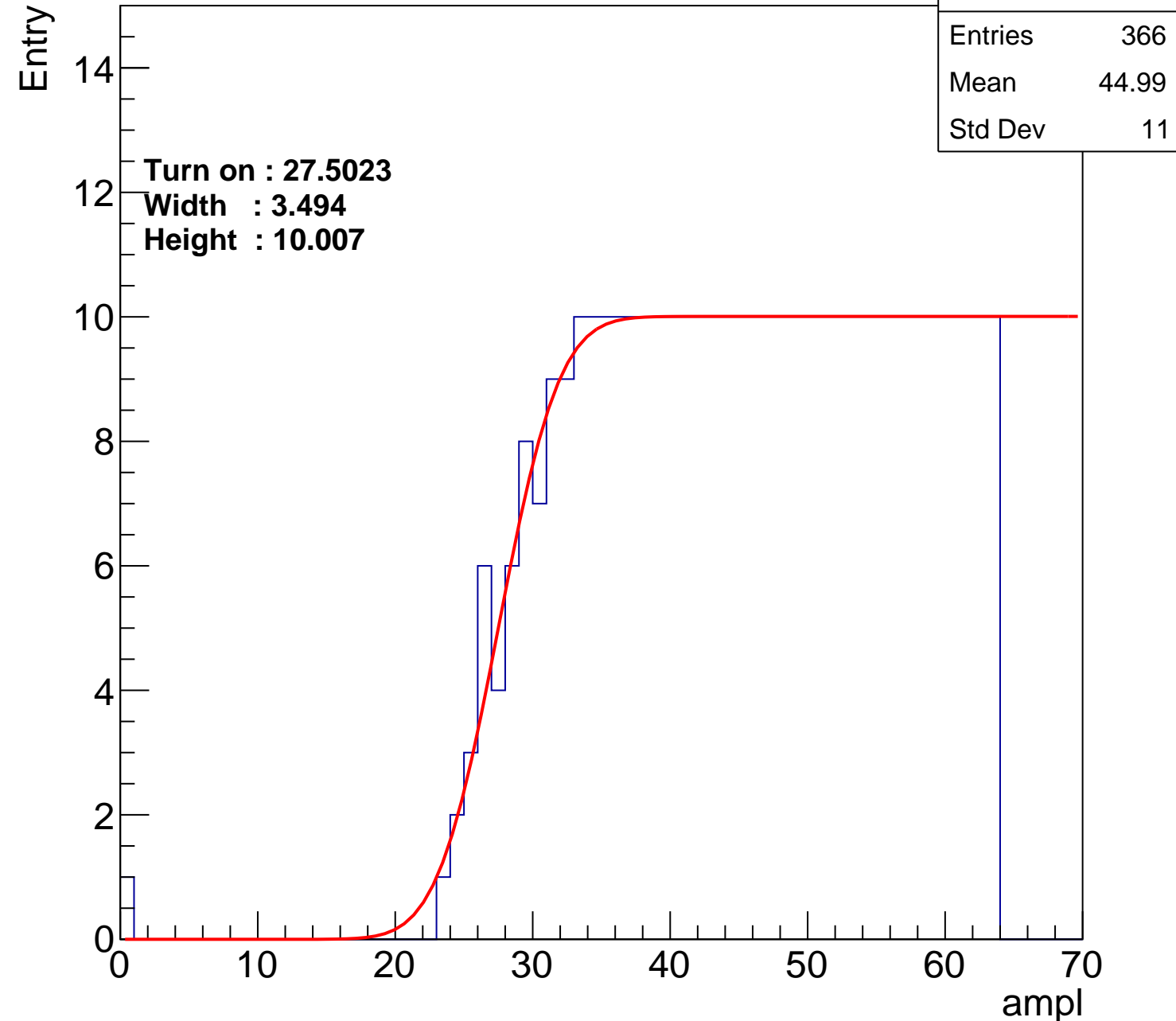
**Width : 3.494**

**Height : 10.007**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U26-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

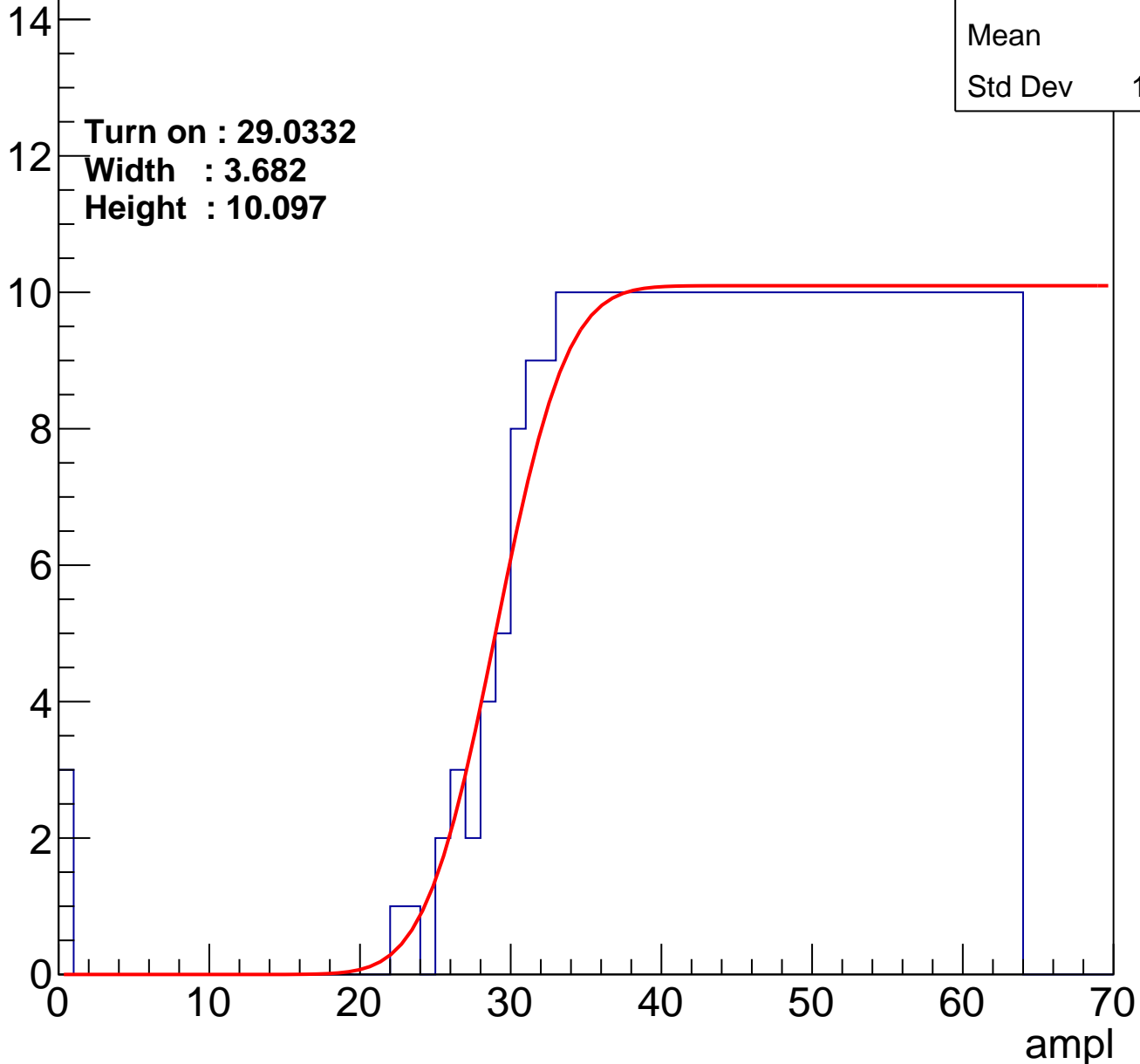
Entries	357
Mean	45.3
Std Dev	11.19

**Turn on : 29.0332**

**Width : 3.682**

**Height : 10.097**

Entry



# B0L000S, U26-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	357
Mean	45.3
Std Dev	11.19

**Turn on : 29.0332**

**Width : 3.682**

**Height : 10.097**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

