

B0L103S, U22-ch0

calib_packv5_040323_1717.root, FC#2, port C3

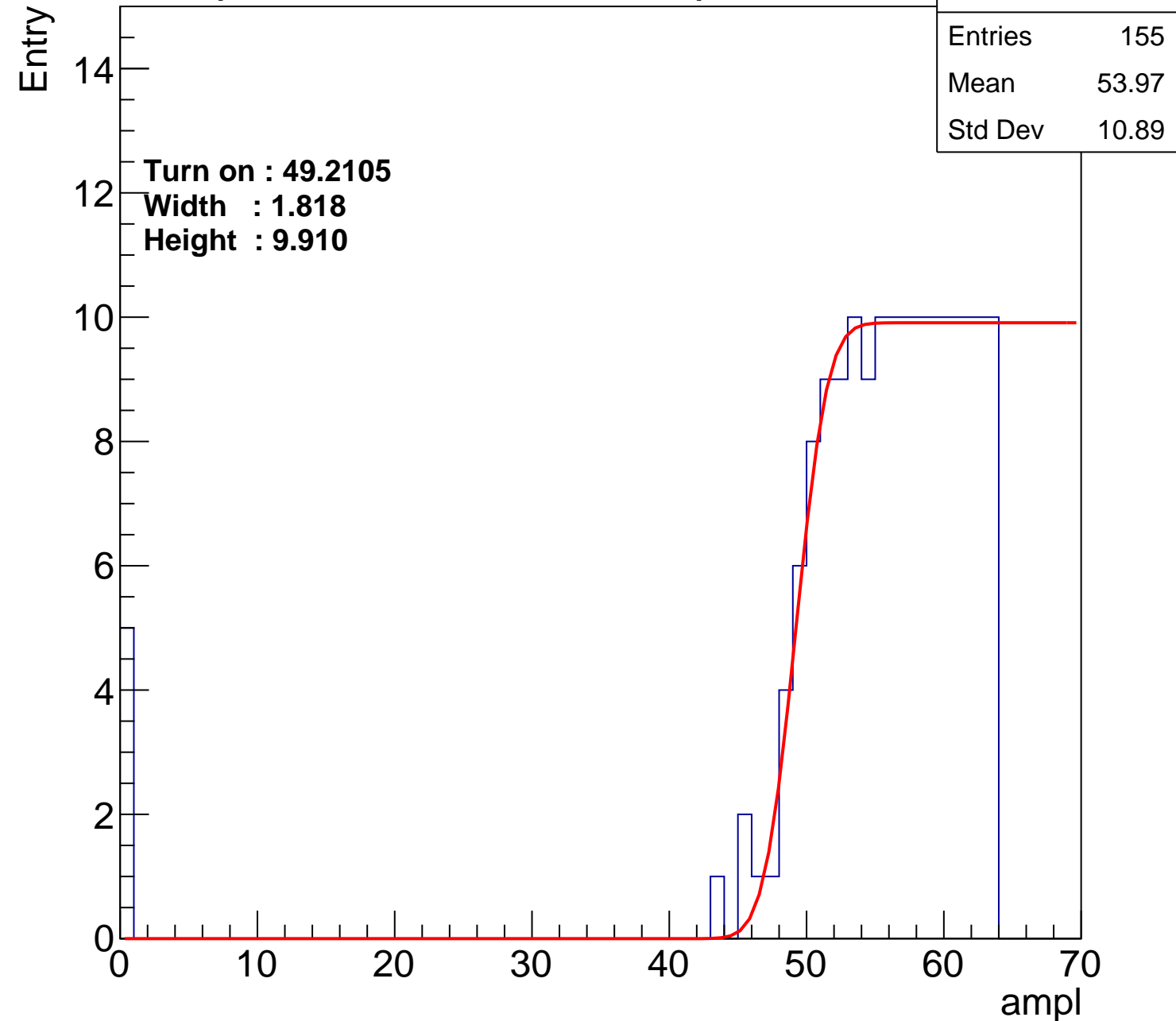
Entry

14
12
10
8
6
4
2
0

Turn on : 49.2105
Width : 1.818
Height : 9.910

Entries	155
Mean	53.97
Std Dev	10.89

ampl



B0L103S, U22-ch1

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.59
Std Dev	10.11

Turn on : 48.6606

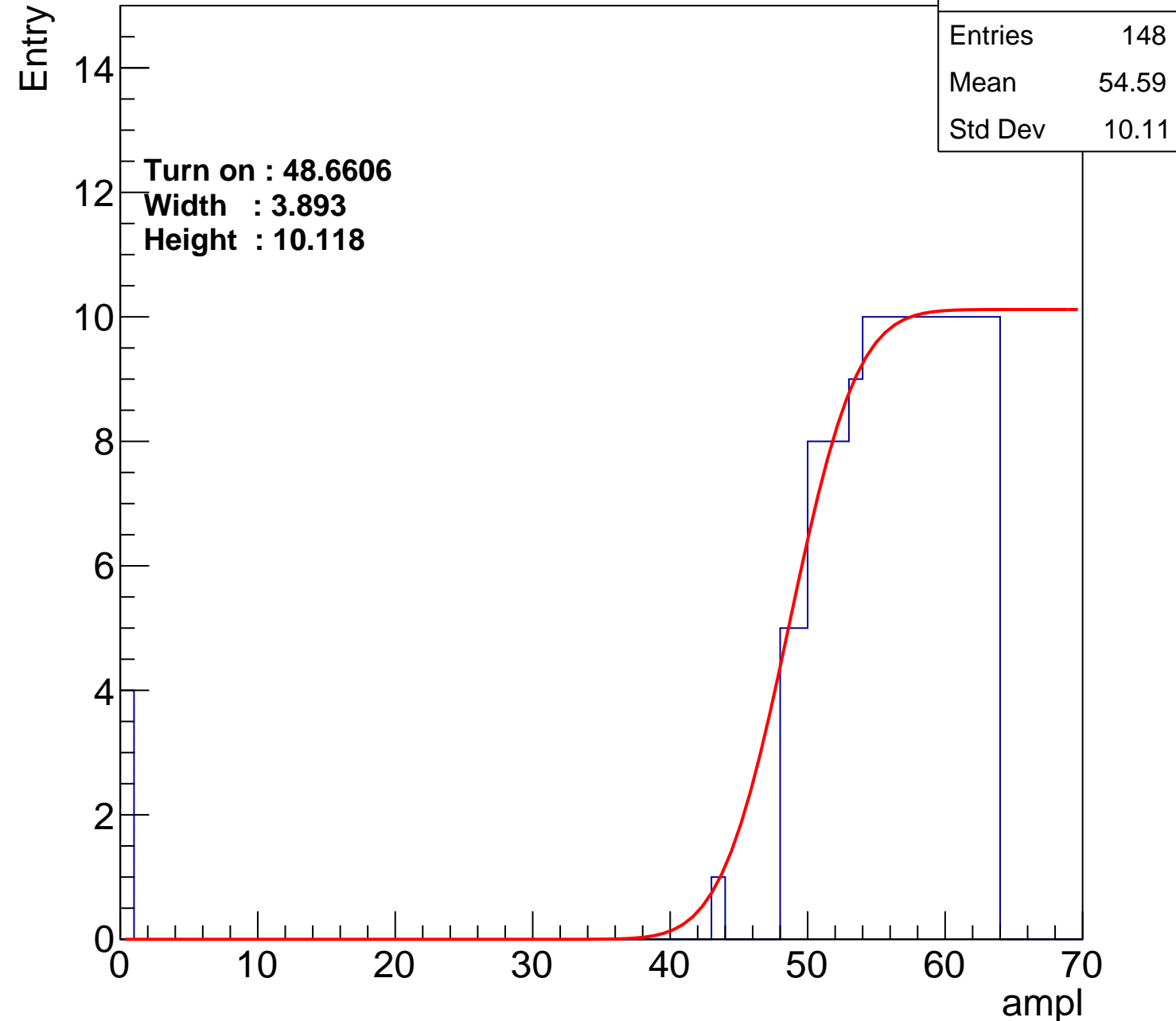
Width : 3.893

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch2

calib_packv5_040323_1717.root, FC#2, port C3

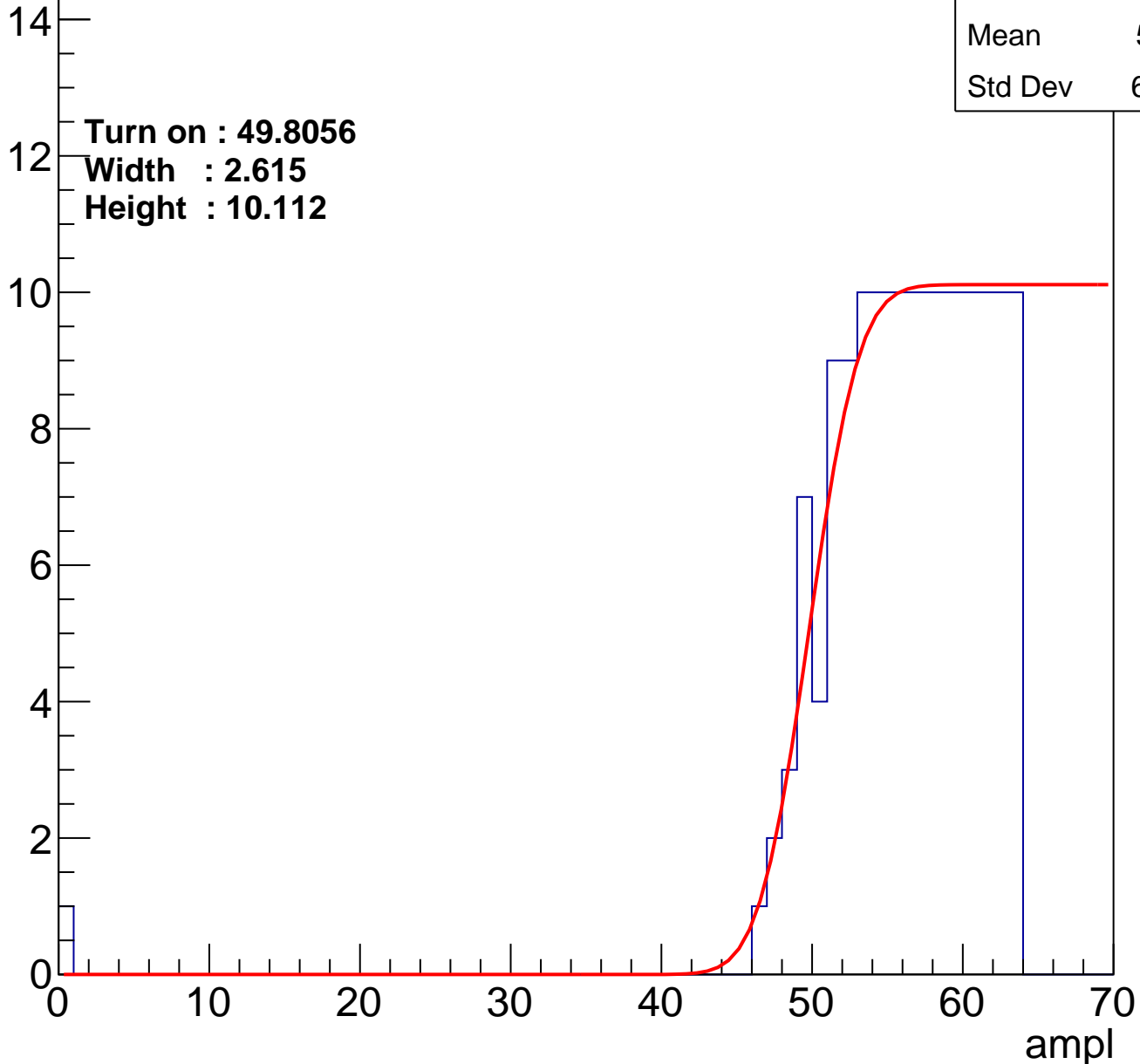
Entries	146
Mean	55.71
Std Dev	6.397

Turn on : 49.8056

Width : 2.615

Height : 10.112

Entry



B0L103S, U22-ch3

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	54.41
Std Dev	11.4

Turn on : 50.8088

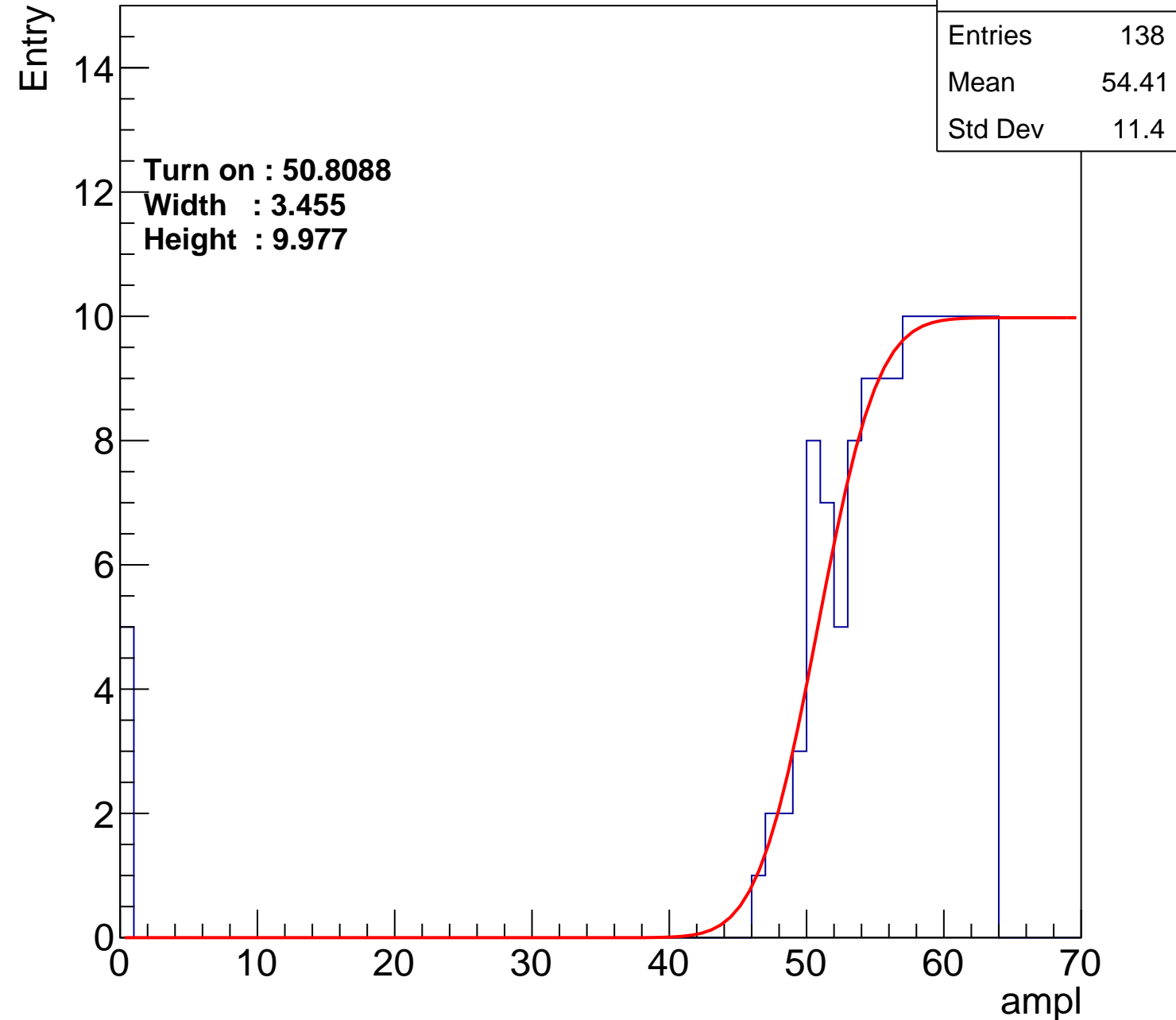
Width : 3.455

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch4

calib_packv5_040323_1717.root, FC#2, port C3

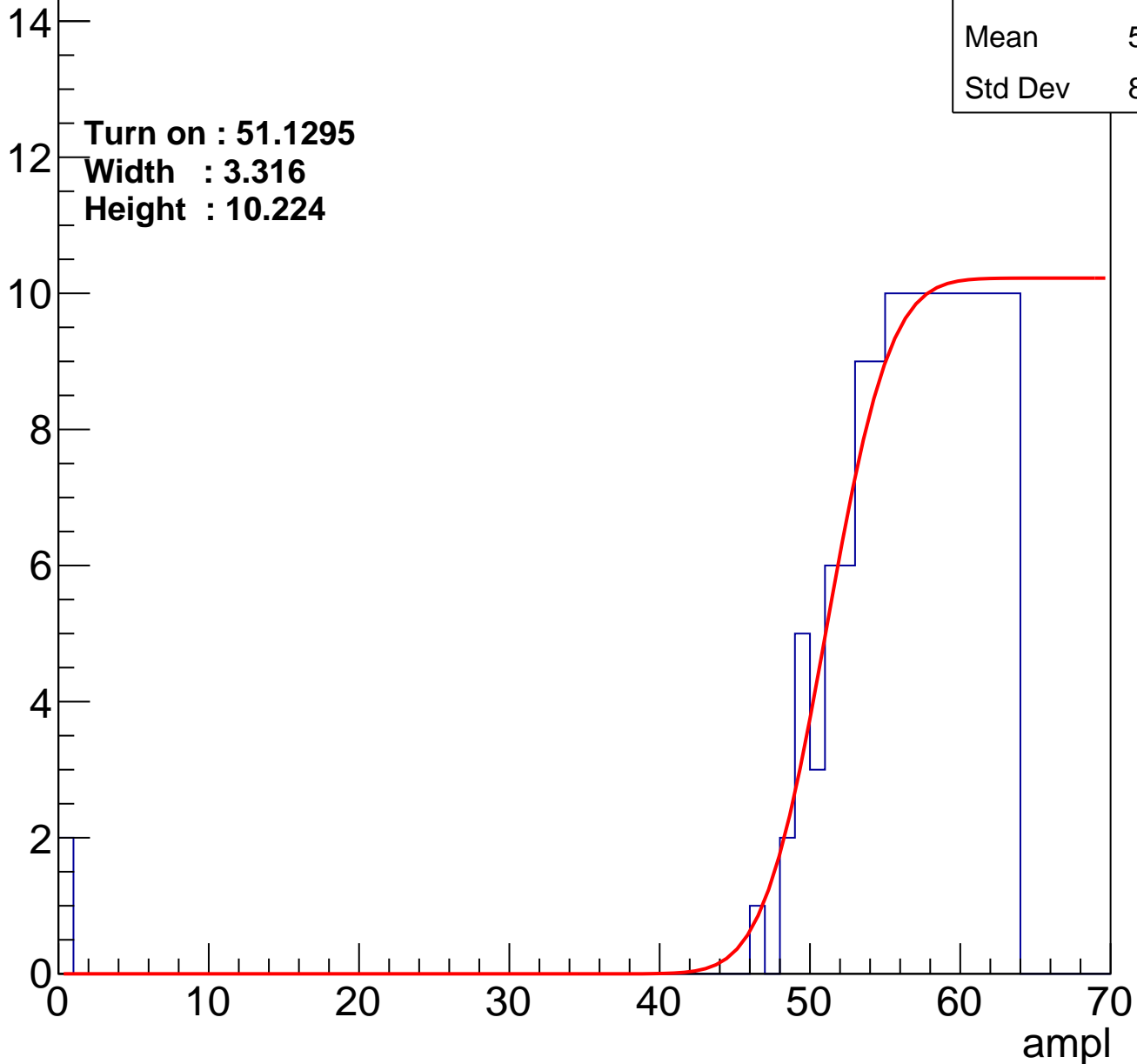
Entry

Entries	133
Mean	55.85
Std Dev	8.052

Turn on : 51.1295

Width : 3.316

Height : 10.224



B0L103S, U22-ch5

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.09
Std Dev	9.648

Turn on : 52.1283

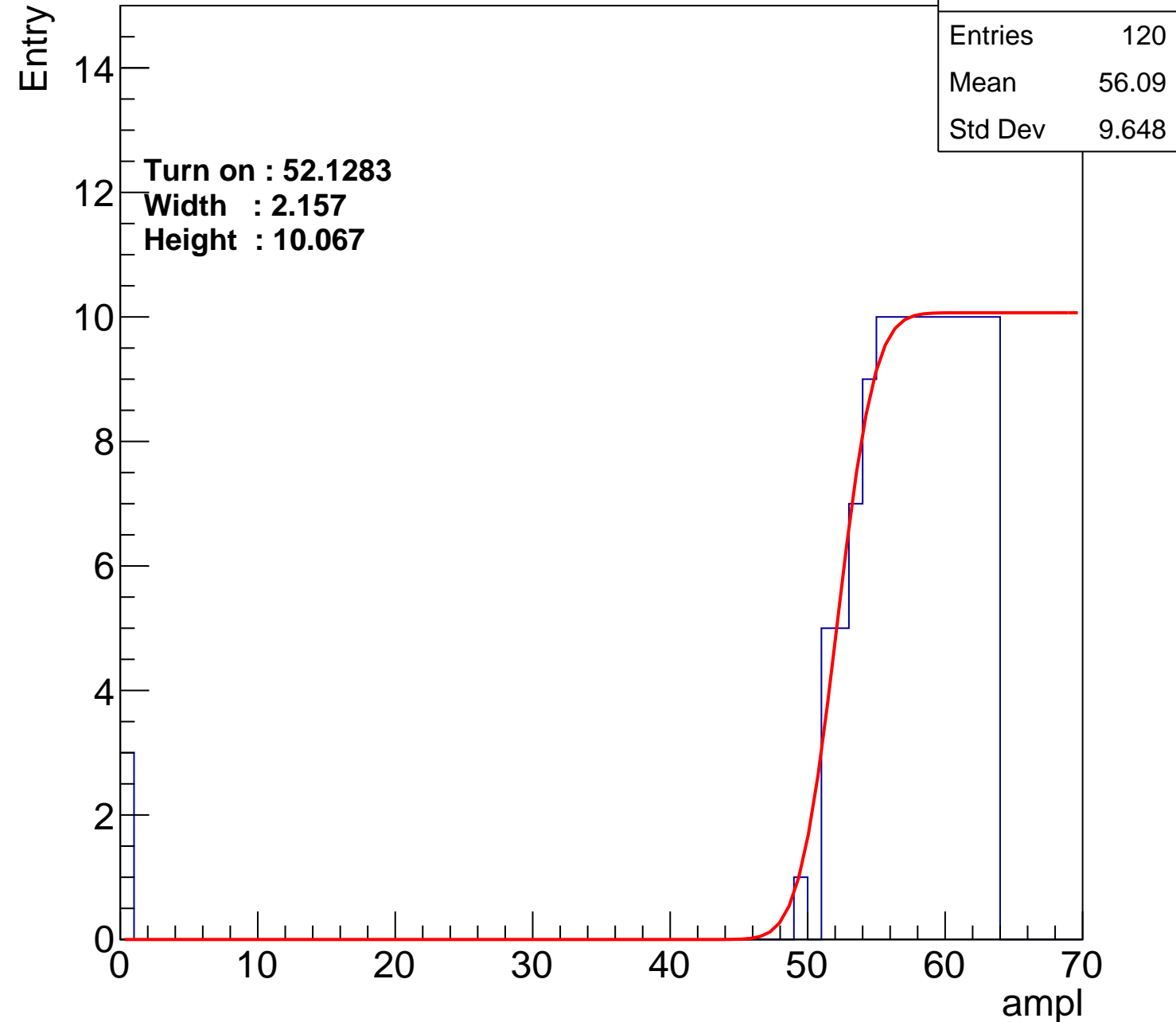
Width : 2.157

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch6

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	55.19
Std Dev	10.64

Turn on : 51.4070

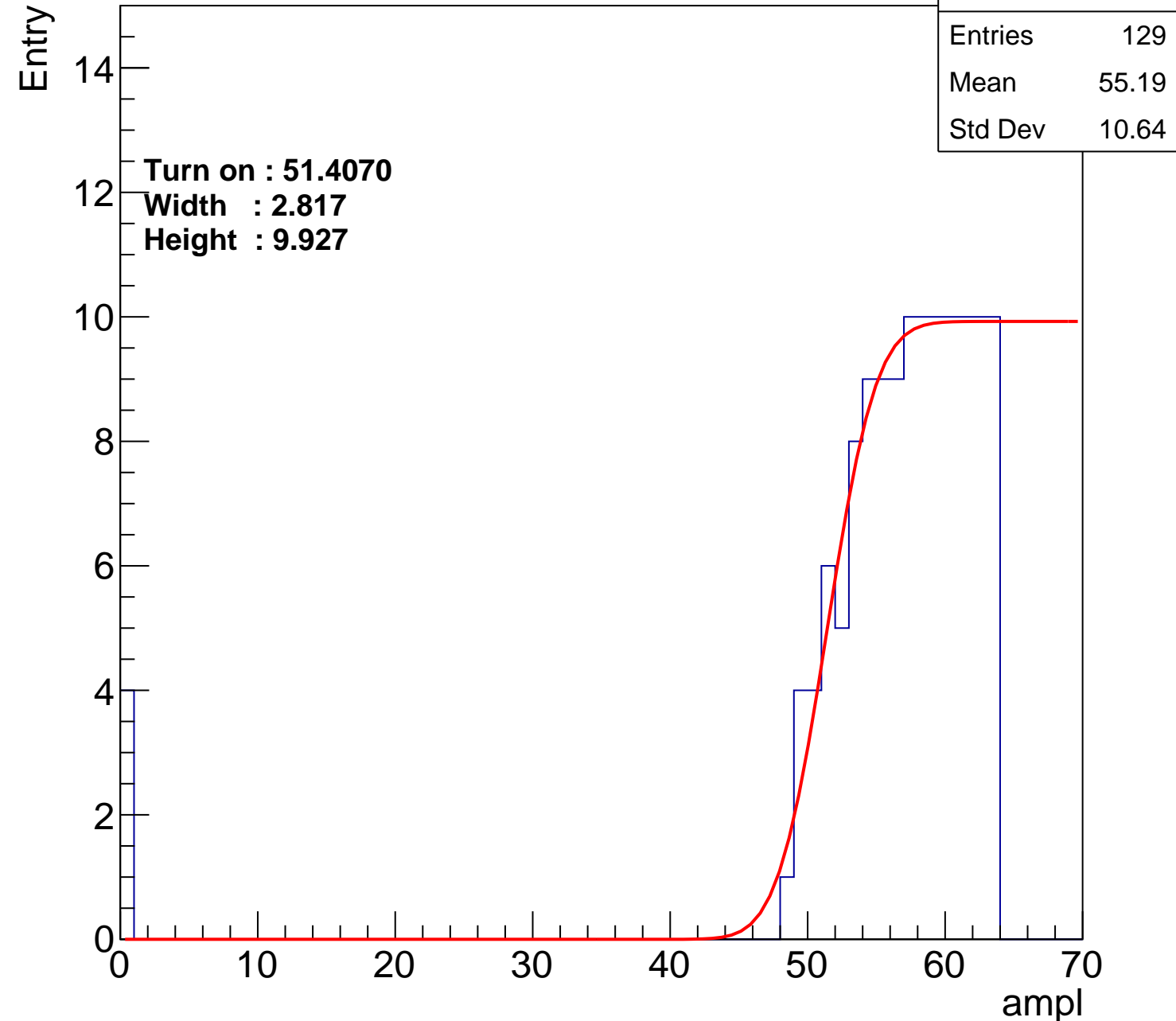
Width : 2.817

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch7

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.7
Std Dev	9.601

Turn on : 51.8914

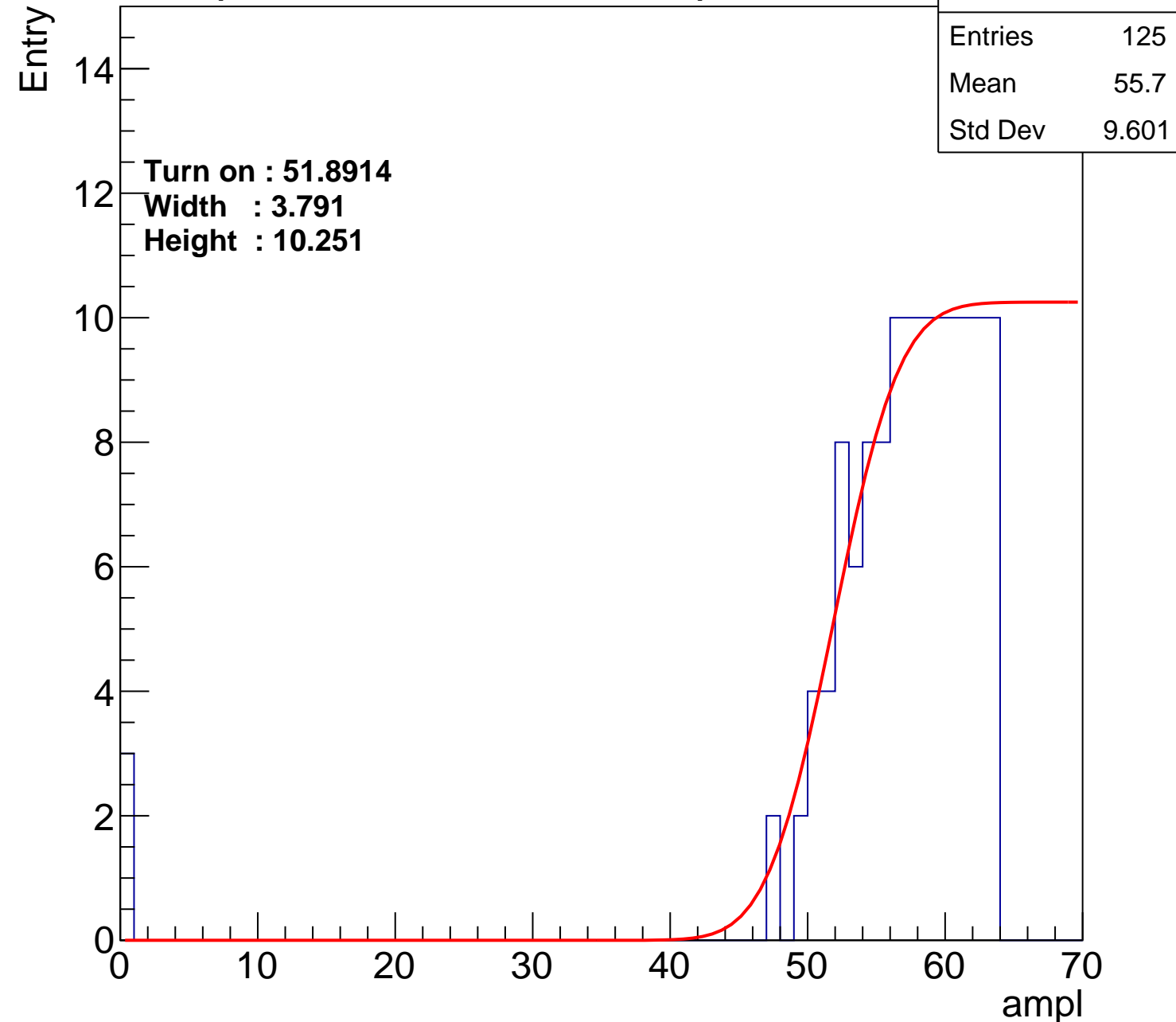
Width : 3.791

Height : 10.251

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch8

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.02
Std Dev	9.347

Turn on : 50.5623

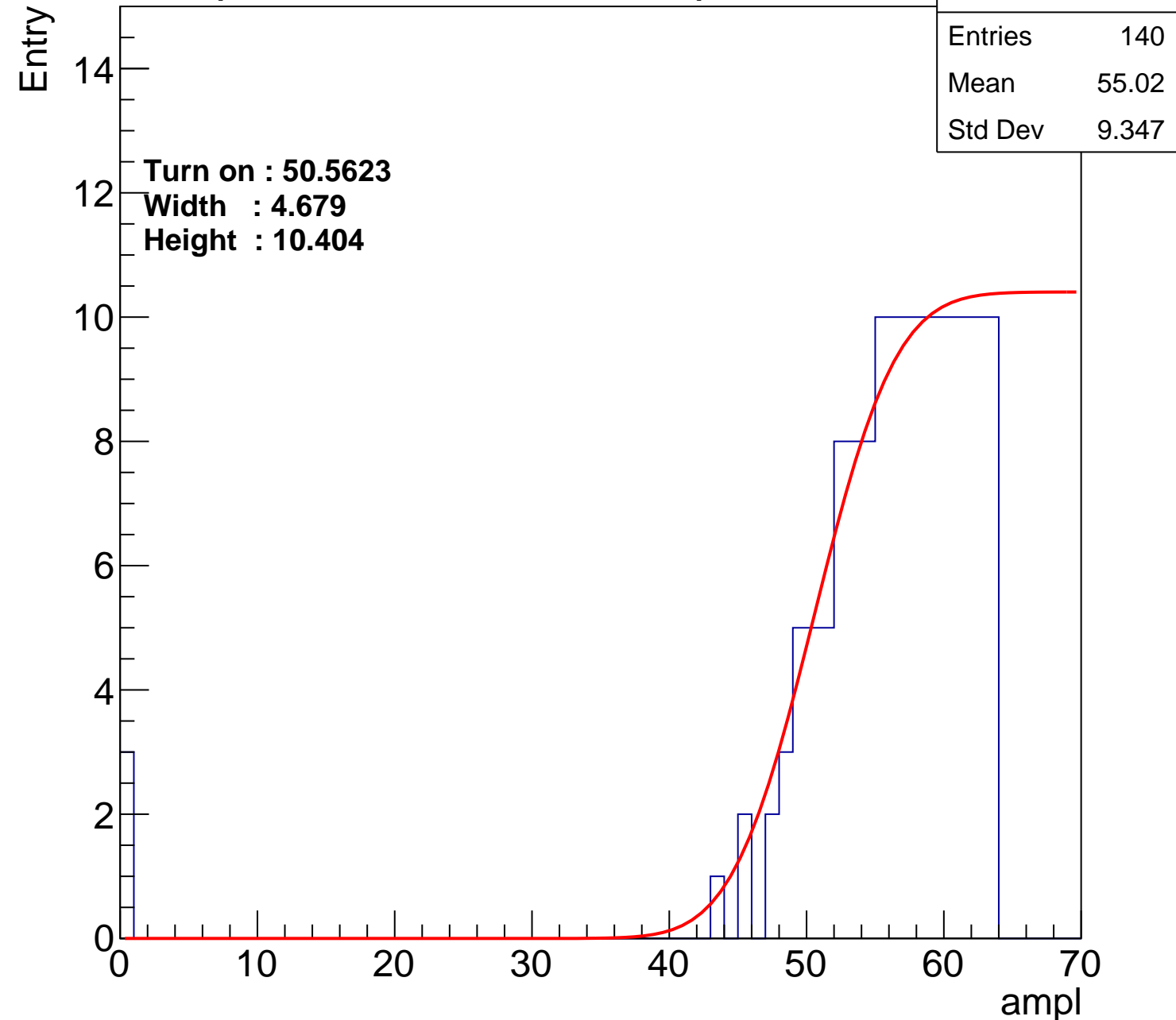
Width : 4.679

Height : 10.404

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch9

calib_packv5_040323_1717.root, FC#2, port C3

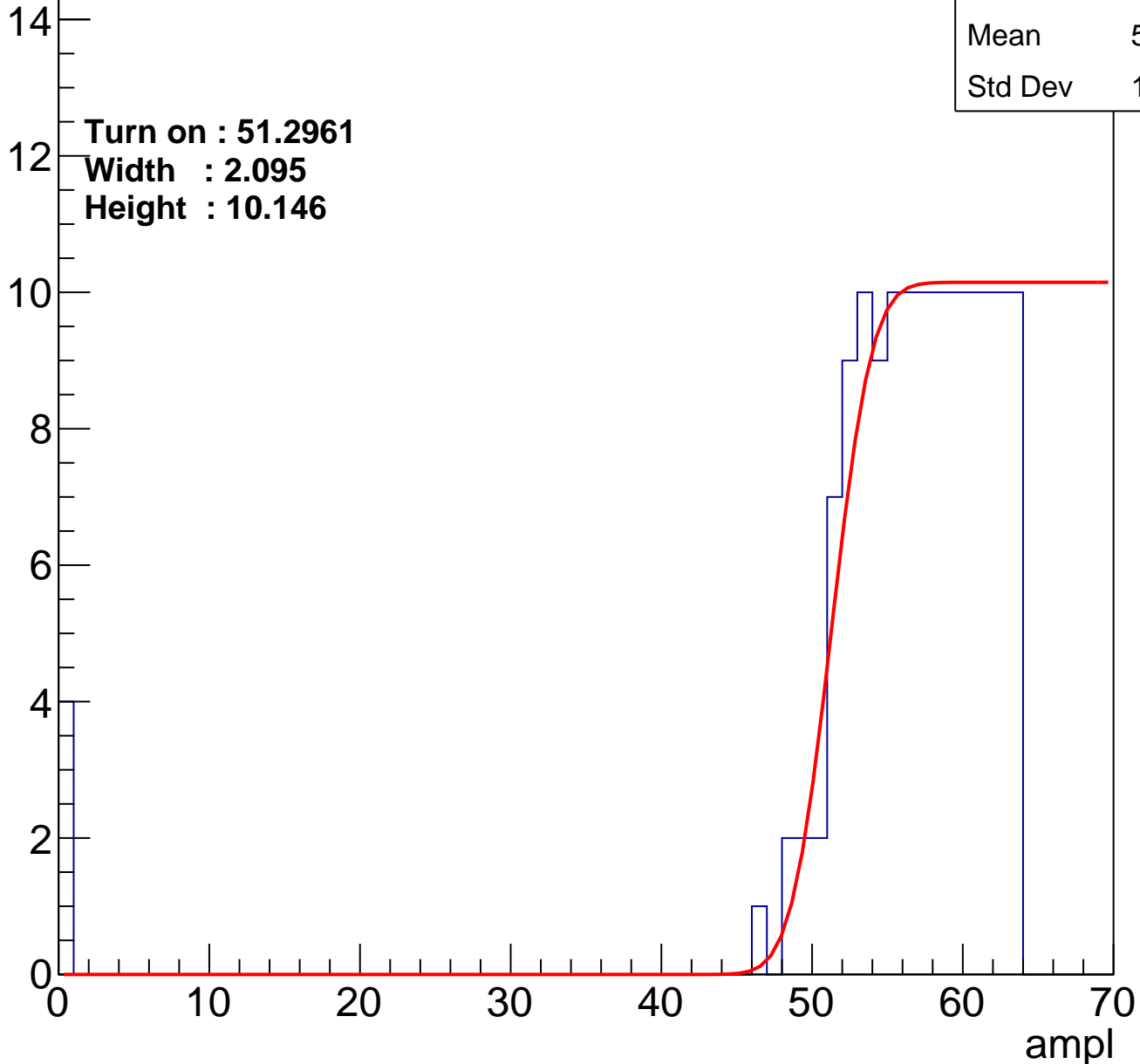
Entries	136
Mean	55.08
Std Dev	10.39

Turn on : 51.2961

Width : 2.095

Height : 10.146

Entry



B0L103S, U22-ch10

calib_packv5_040323_1717.root, FC#2, port C3

Entries	154
Mean	54.68
Std Dev	8.977

Turn on : 49.0113

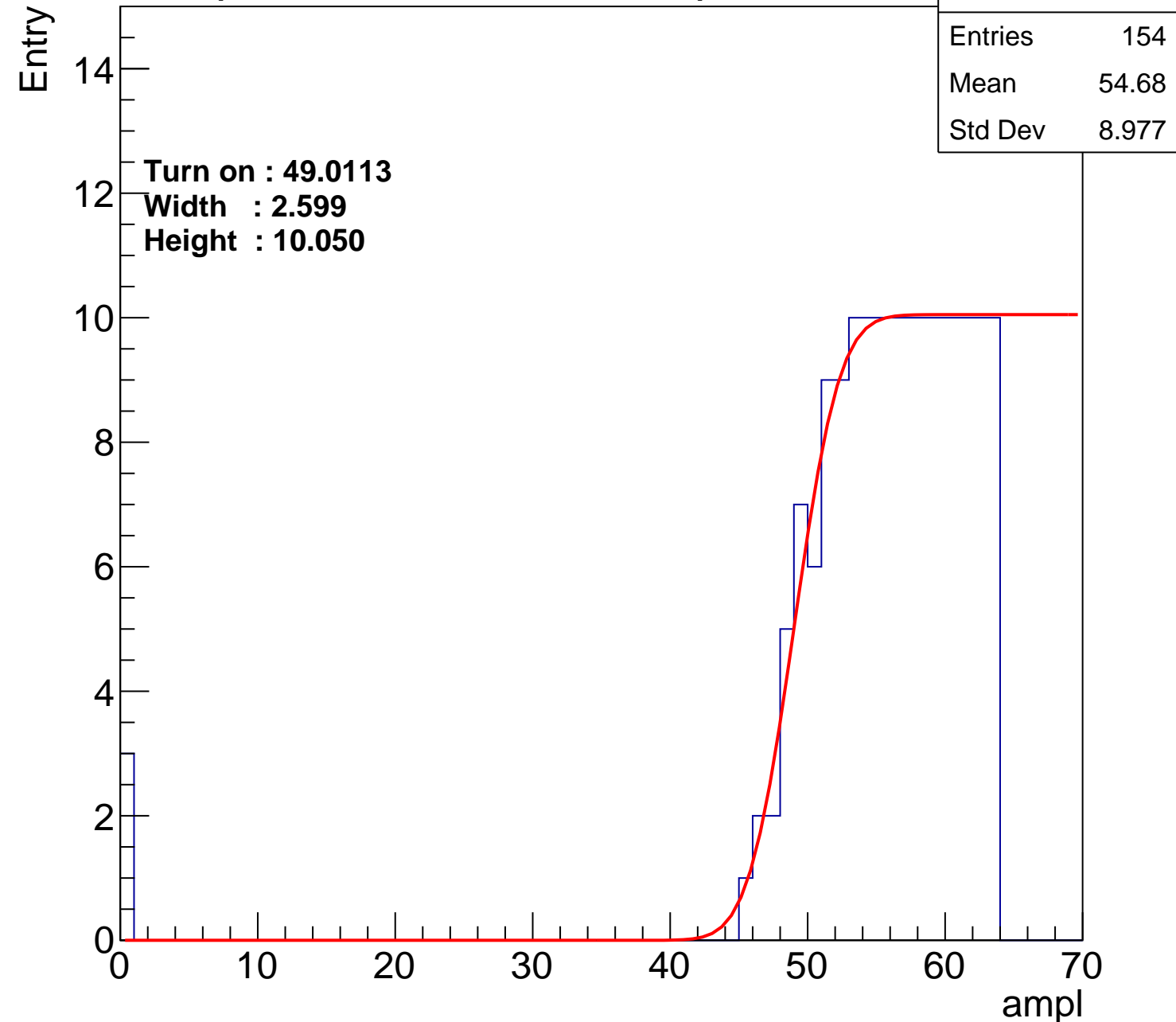
Width : 2.599

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch11

calib_packv5_040323_1717.root, FC#2, port C3

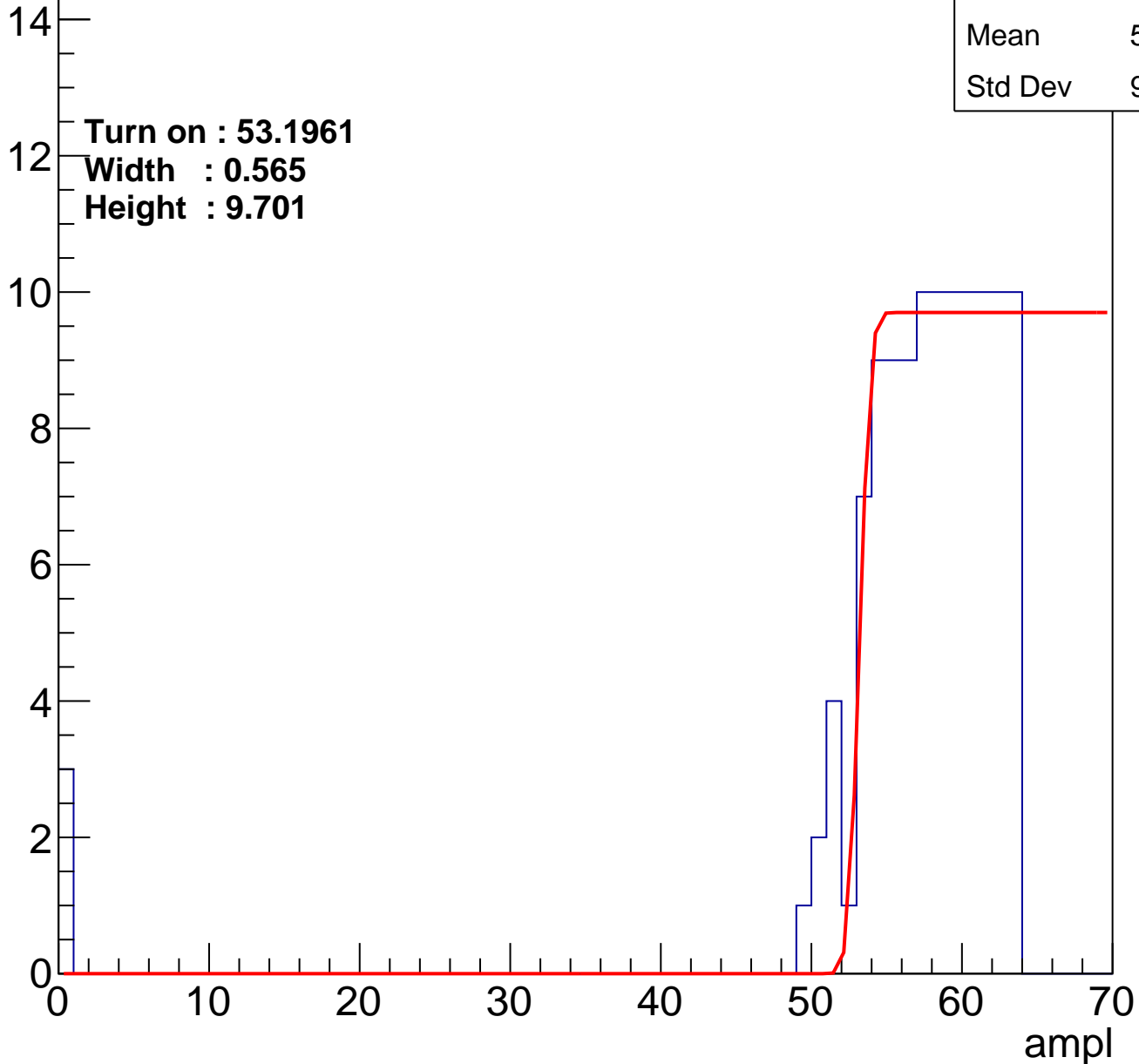
Entries	115
Mean	56.18
Std Dev	9.846

Turn on : 53.1961

Width : 0.565

Height : 9.701

Entry



B0L103S, U22-ch12

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	54.89
Std Dev	11.71

Turn on : 51.9957

Width : 2.995

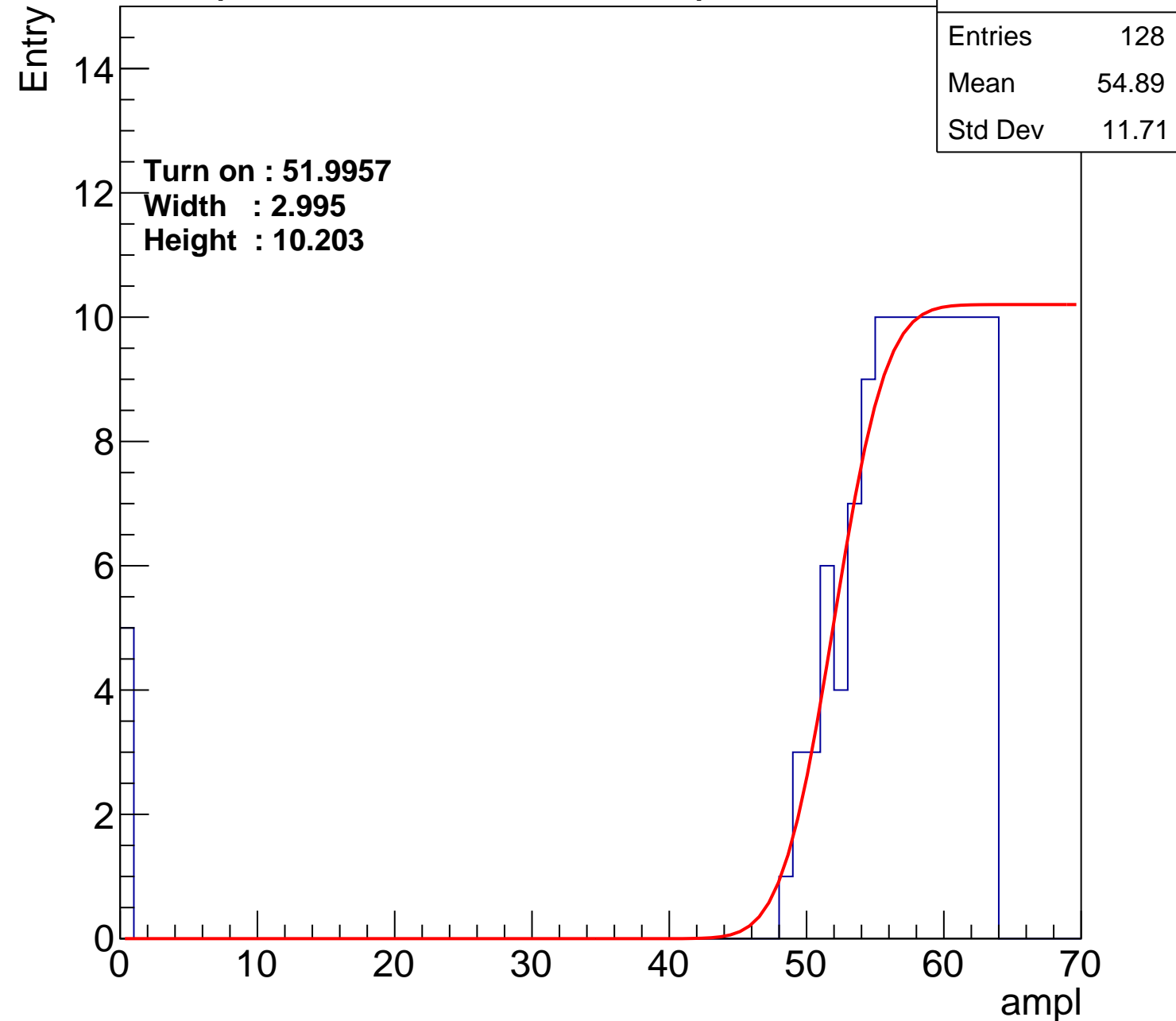
Height : 10.203

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U22-ch13

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	56.52
Std Dev	6.369

Turn on : 51.2100

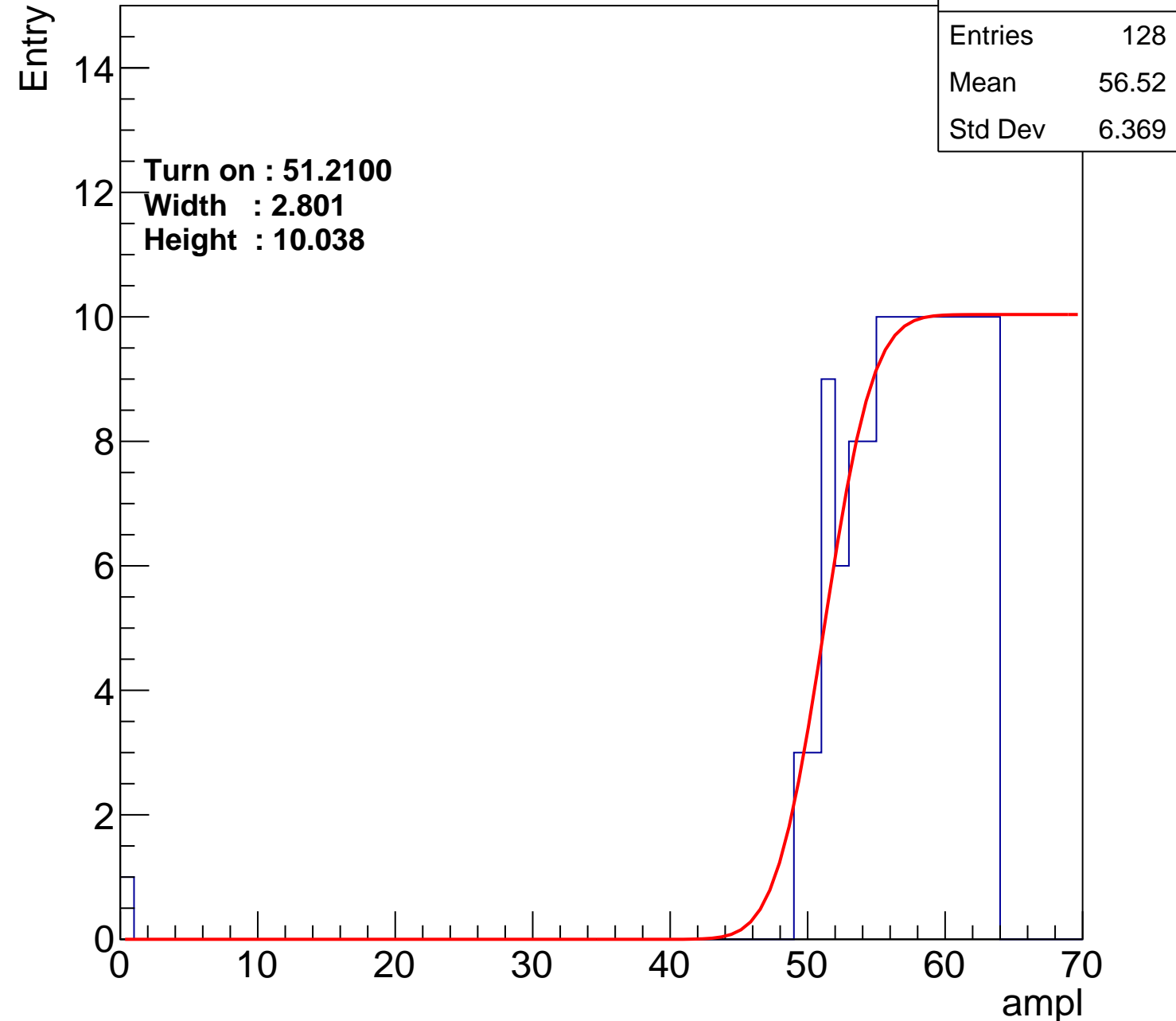
Width : 2.801

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch14

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	53.12
Std Dev	13.23

Turn on : 49.6999

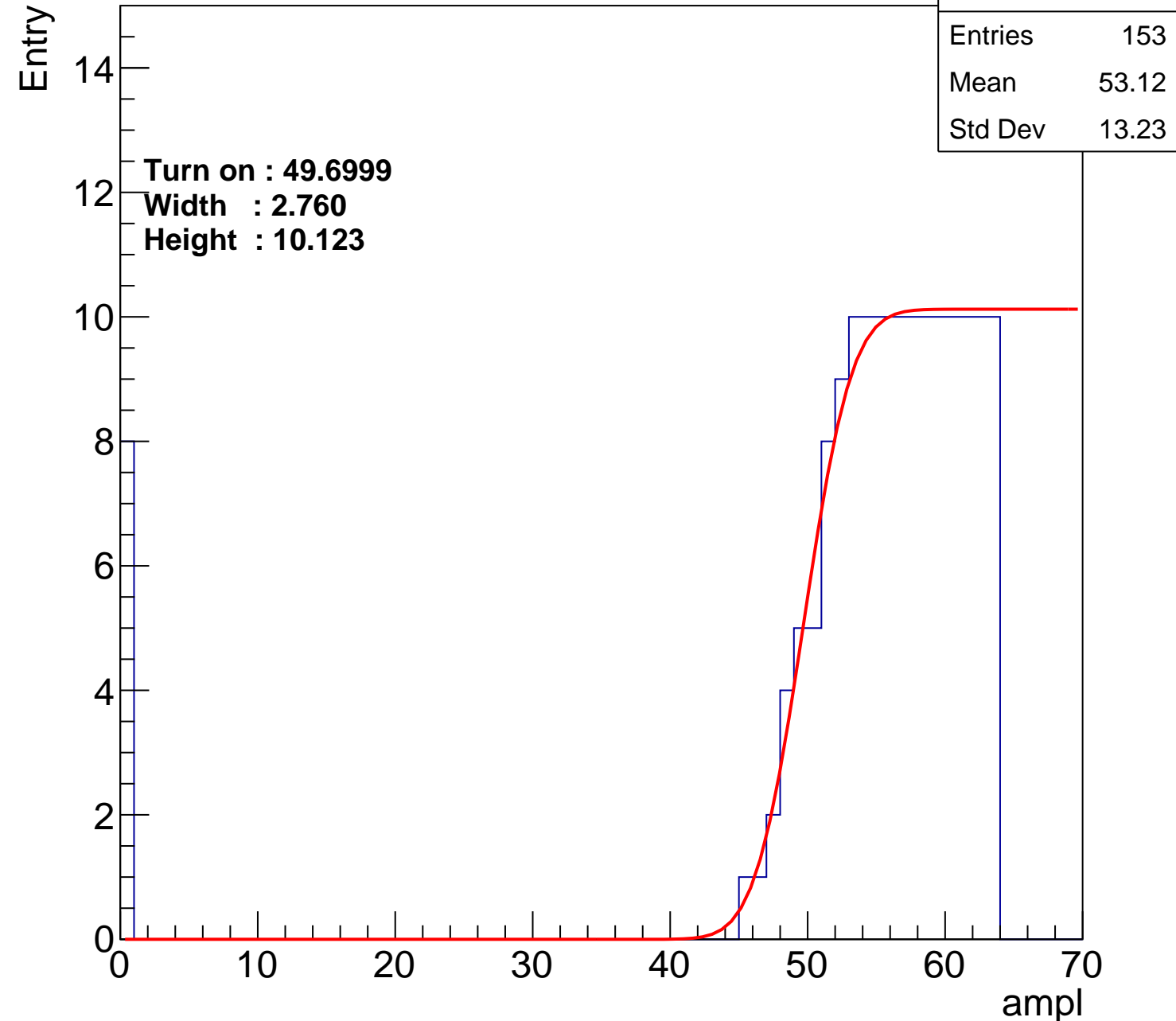
Width : 2.760

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch15

calib_packv5_040323_1717.root, FC#2, port C3

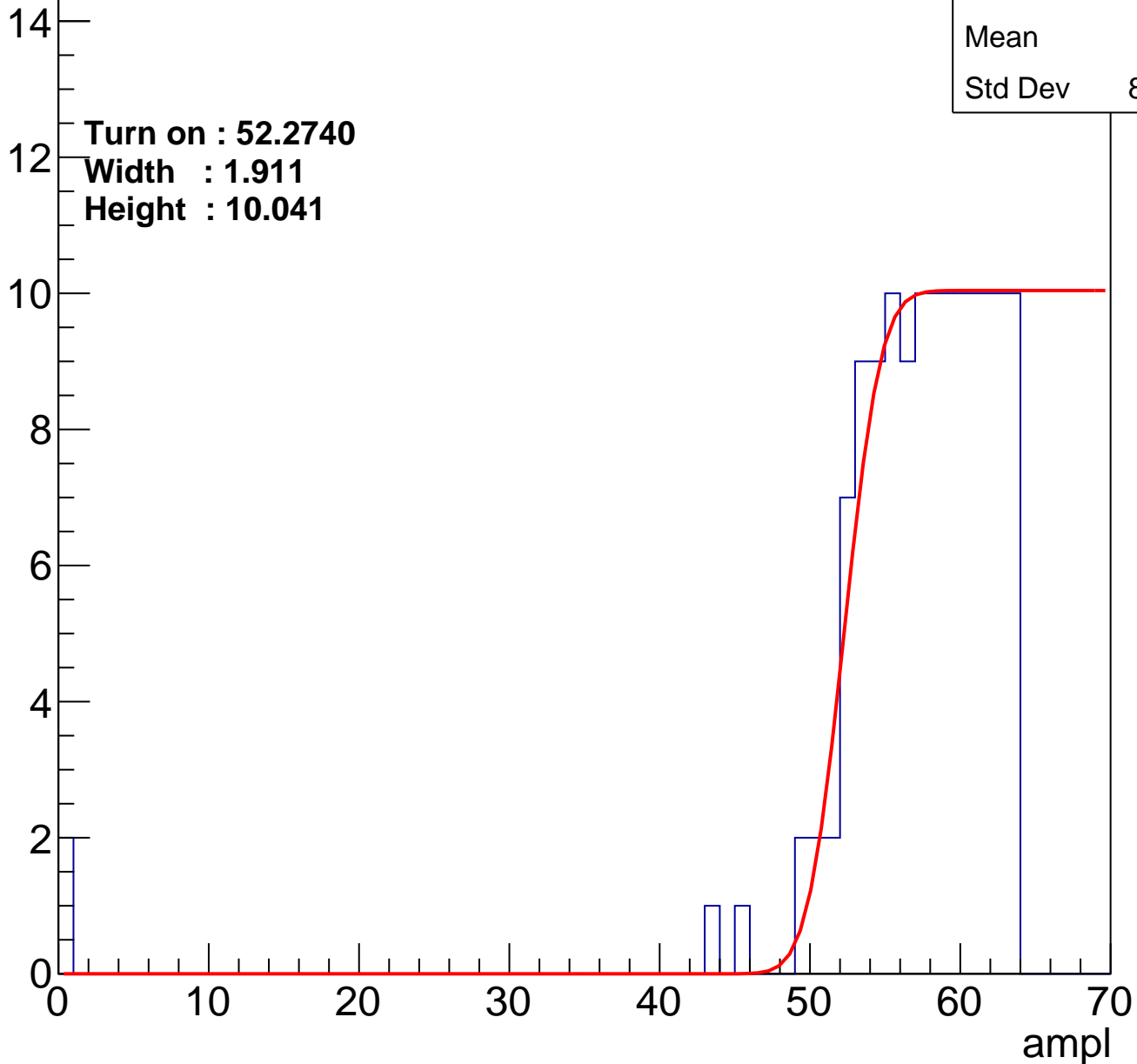
Entries	124
Mean	56.2
Std Dev	8.238

Turn on : 52.2740

Width : 1.911

Height : 10.041

Entry



B0L103S, U22-ch16

calib_packv5_040323_1717.root, FC#2, port C3

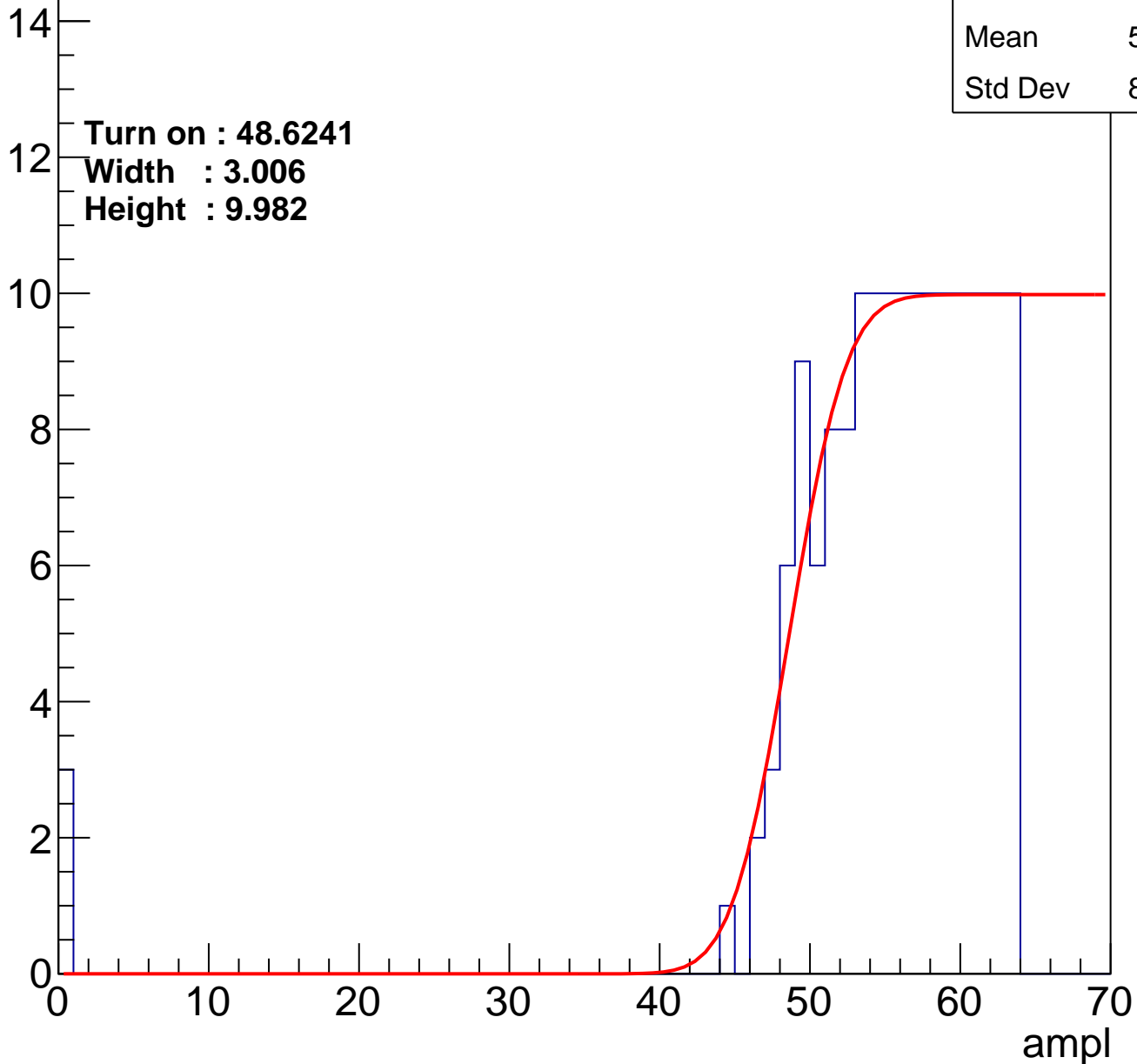
Entries	156
Mean	54.55
Std Dev	8.979

Turn on : 48.6241

Width : 3.006

Height : 9.982

Entry



B0L103S, U22-ch17

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.58
Std Dev	9.418

Turn on : 50.8119

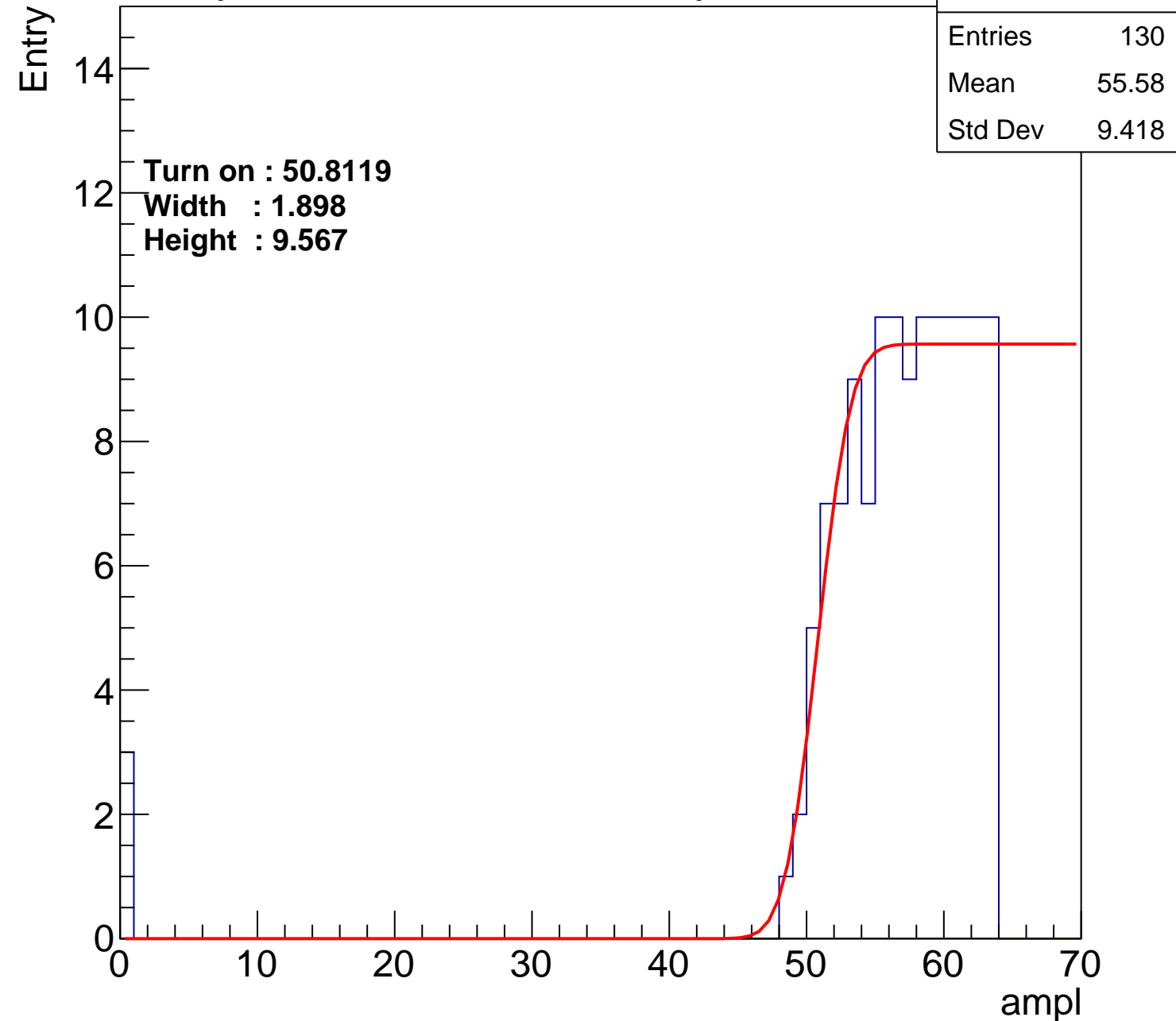
Width : 1.898

Height : 9.567

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch18

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.03
Std Dev	10.55

Turn on : 51.5676

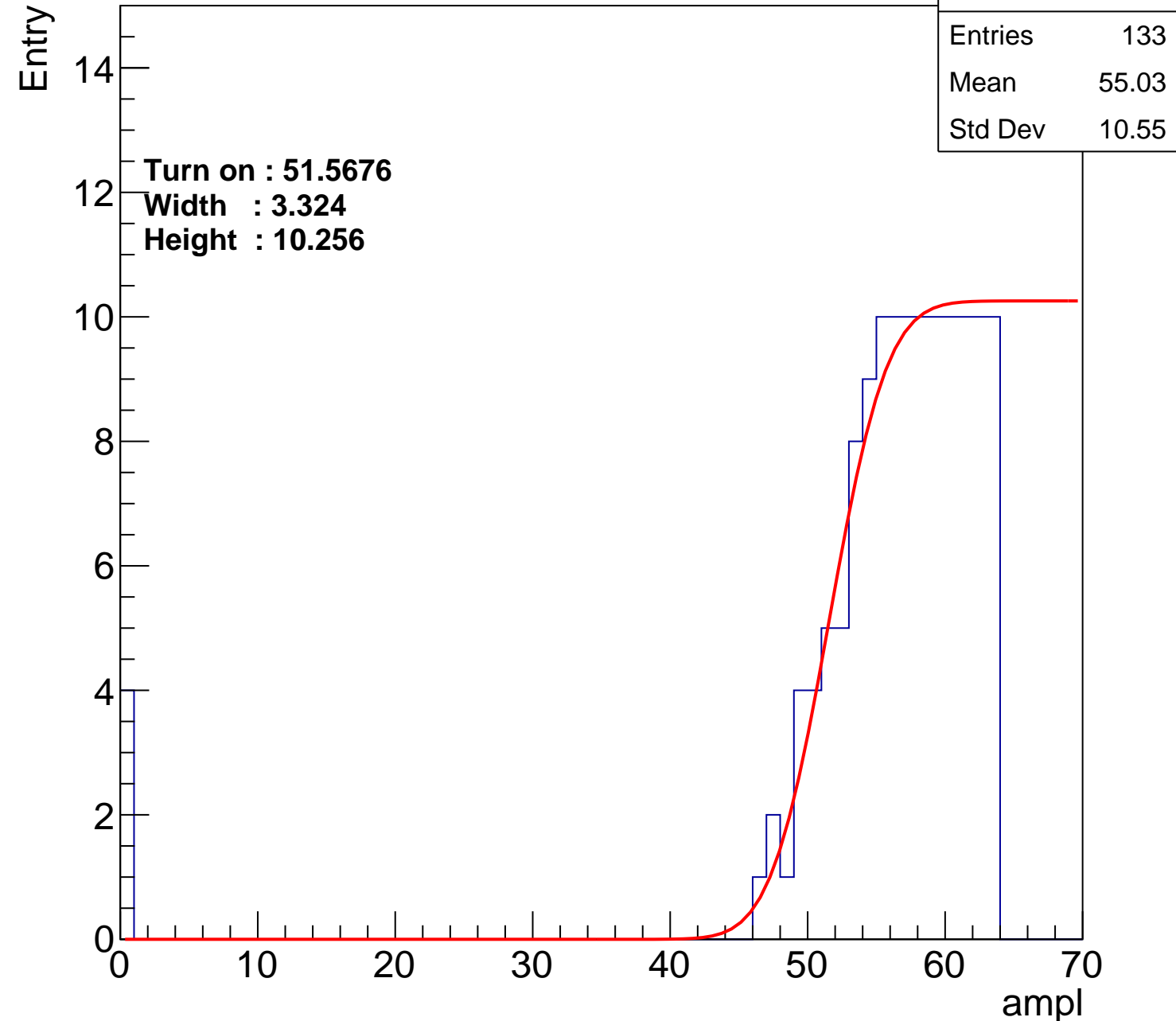
Width : 3.324

Height : 10.256

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch19

calib_packv5_040323_1717.root, FC#2, port C3

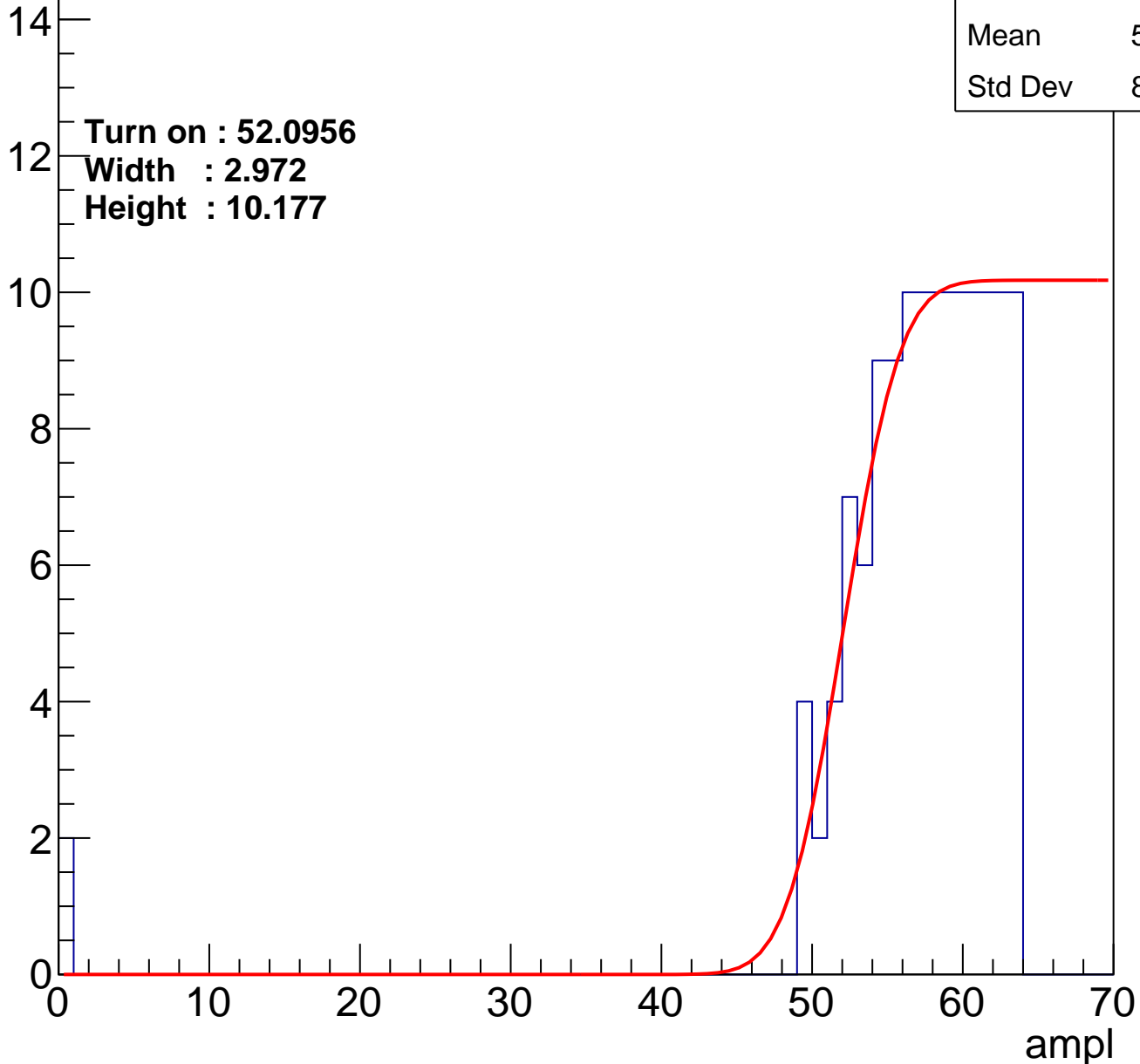
Entries	123
Mean	56.28
Std Dev	8.185

Turn on : 52.0956

Width : 2.972

Height : 10.177

Entry



B0L103S, U22-ch20

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	53.85
Std Dev	11.79

Turn on : 49.4544

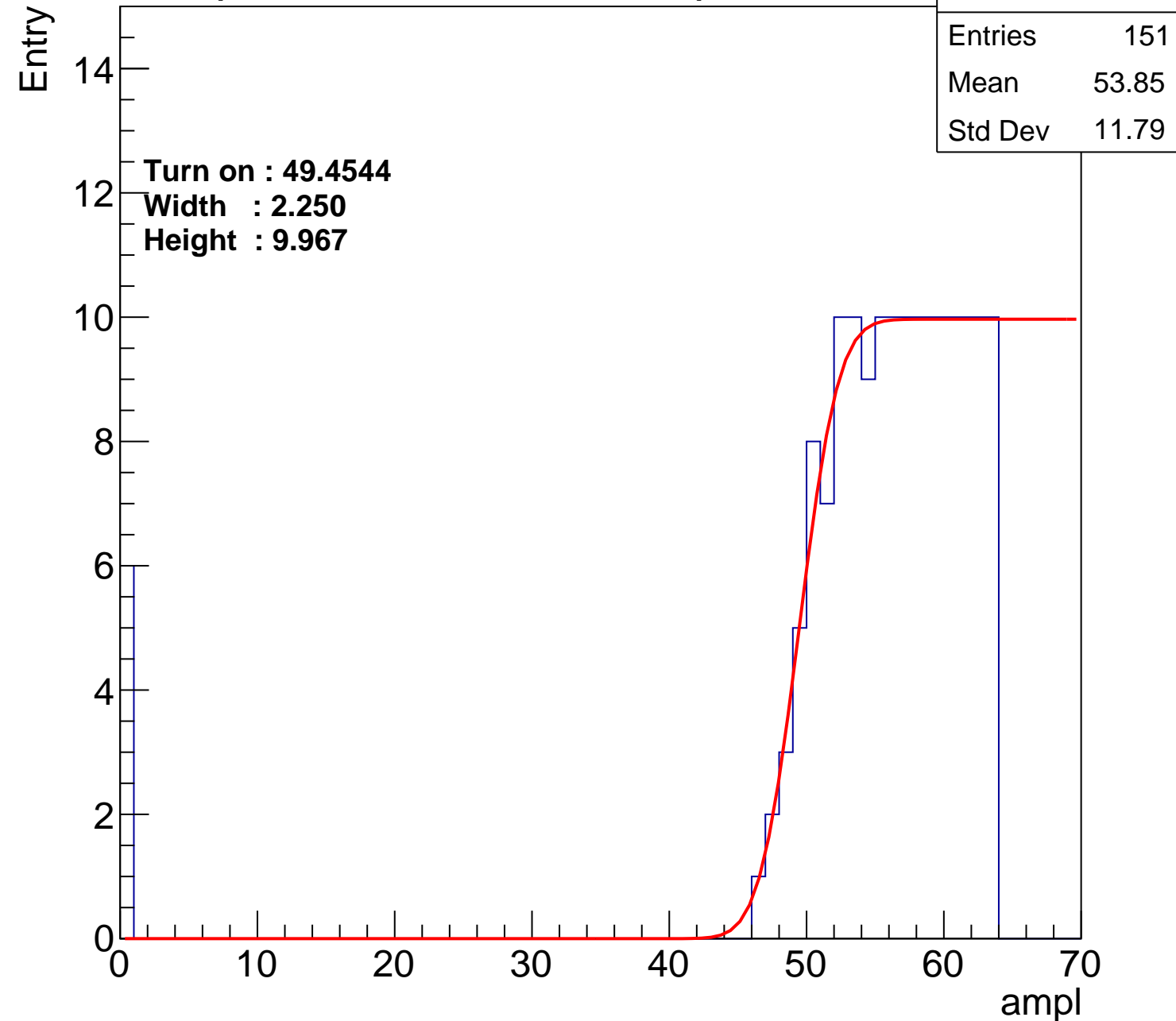
Width : 2.250

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch21

calib_packv5_040323_1717.root, FC#2, port C3

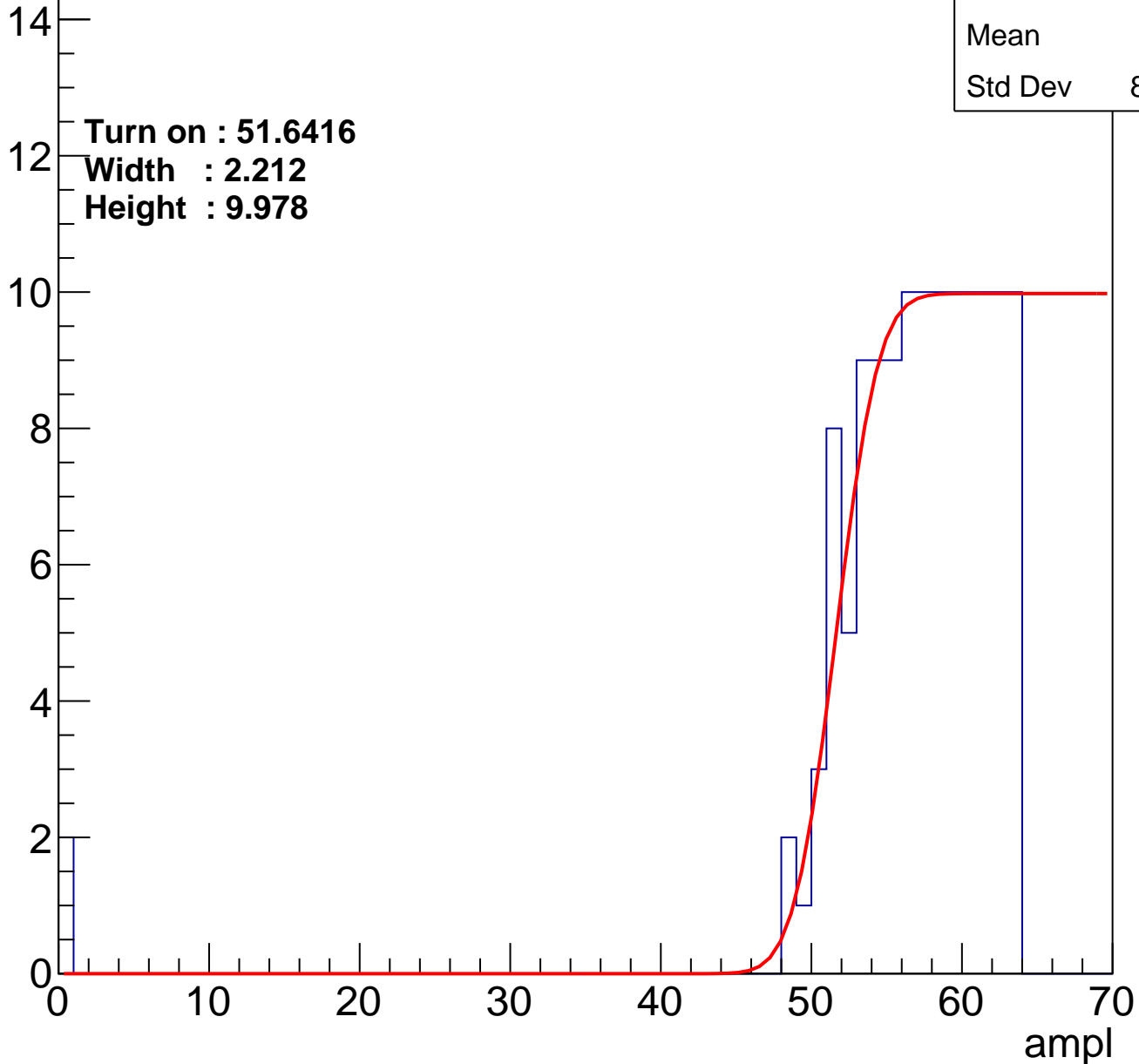
Entries	128
Mean	56.1
Std Dev	8.082

Turn on : 51.6416

Width : 2.212

Height : 9.978

Entry



B0L103S, U22-ch22

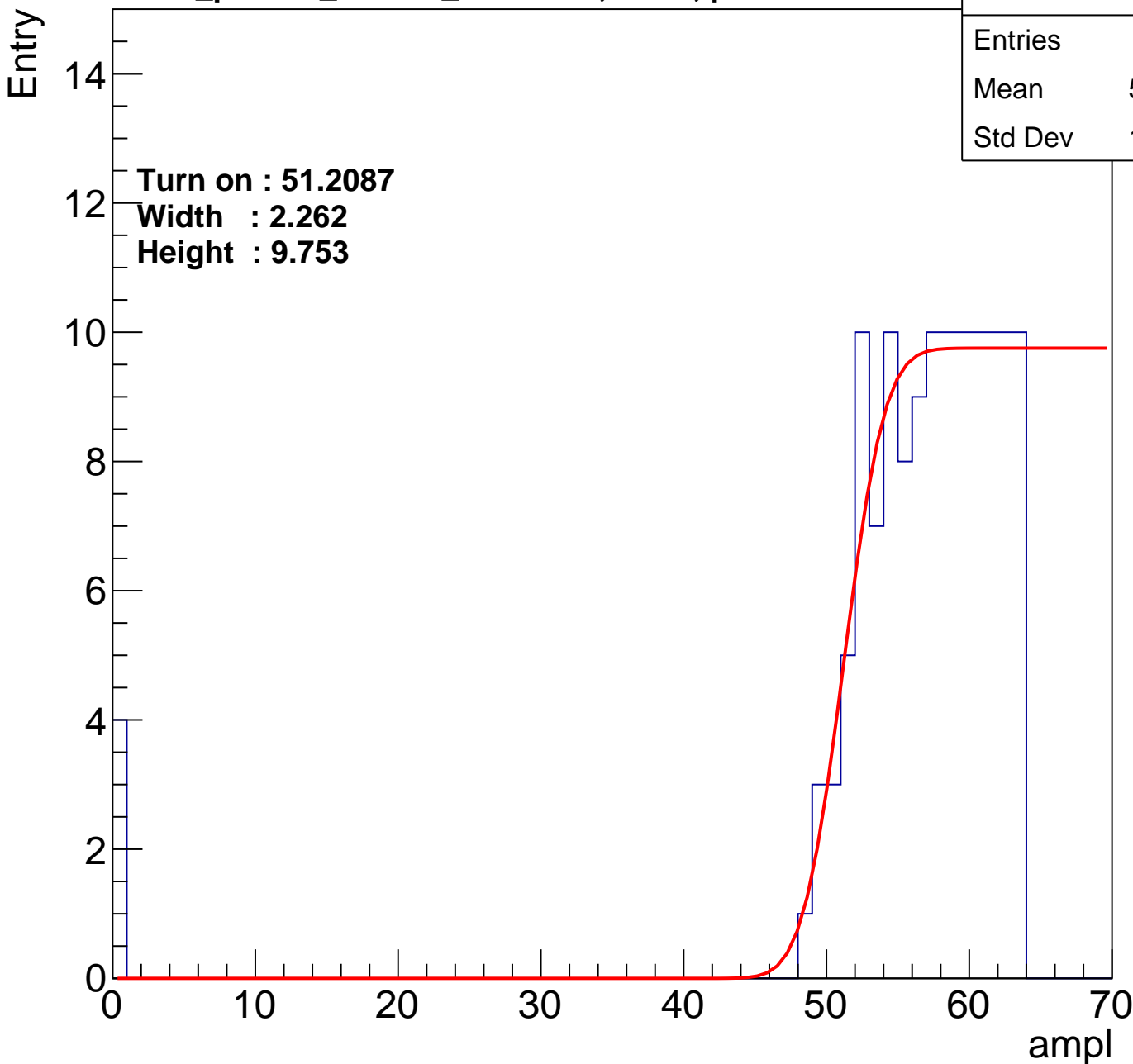
calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.19
Std Dev	10.59

Turn on : 51.2087

Width : 2.262

Height : 9.753



B0L103S, U22-ch23

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.99
Std Dev	8.31

Turn on : 51.7084

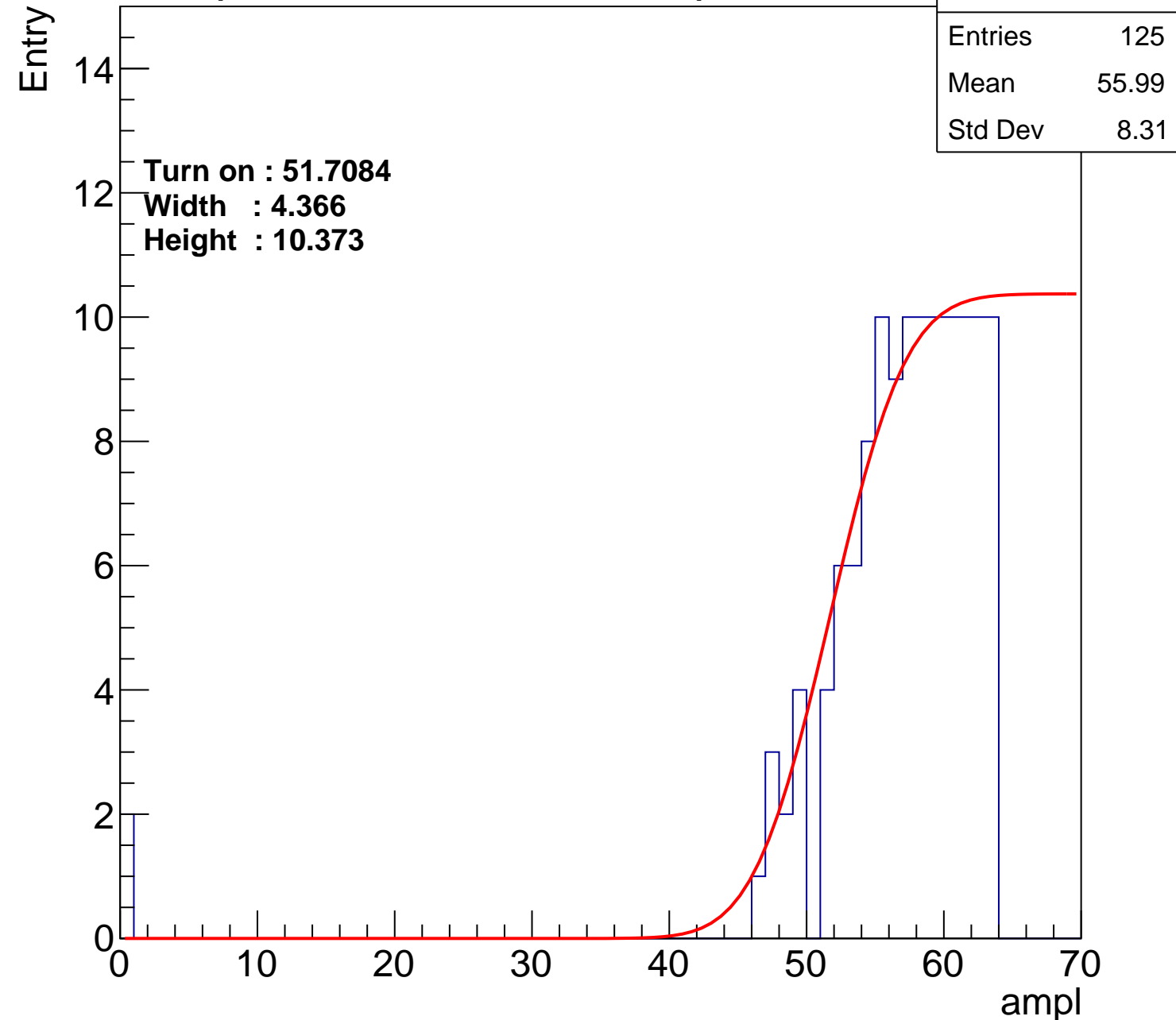
Width : 4.366

Height : 10.373

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch24

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.65
Std Dev	7.937

Turn on : 50.4745

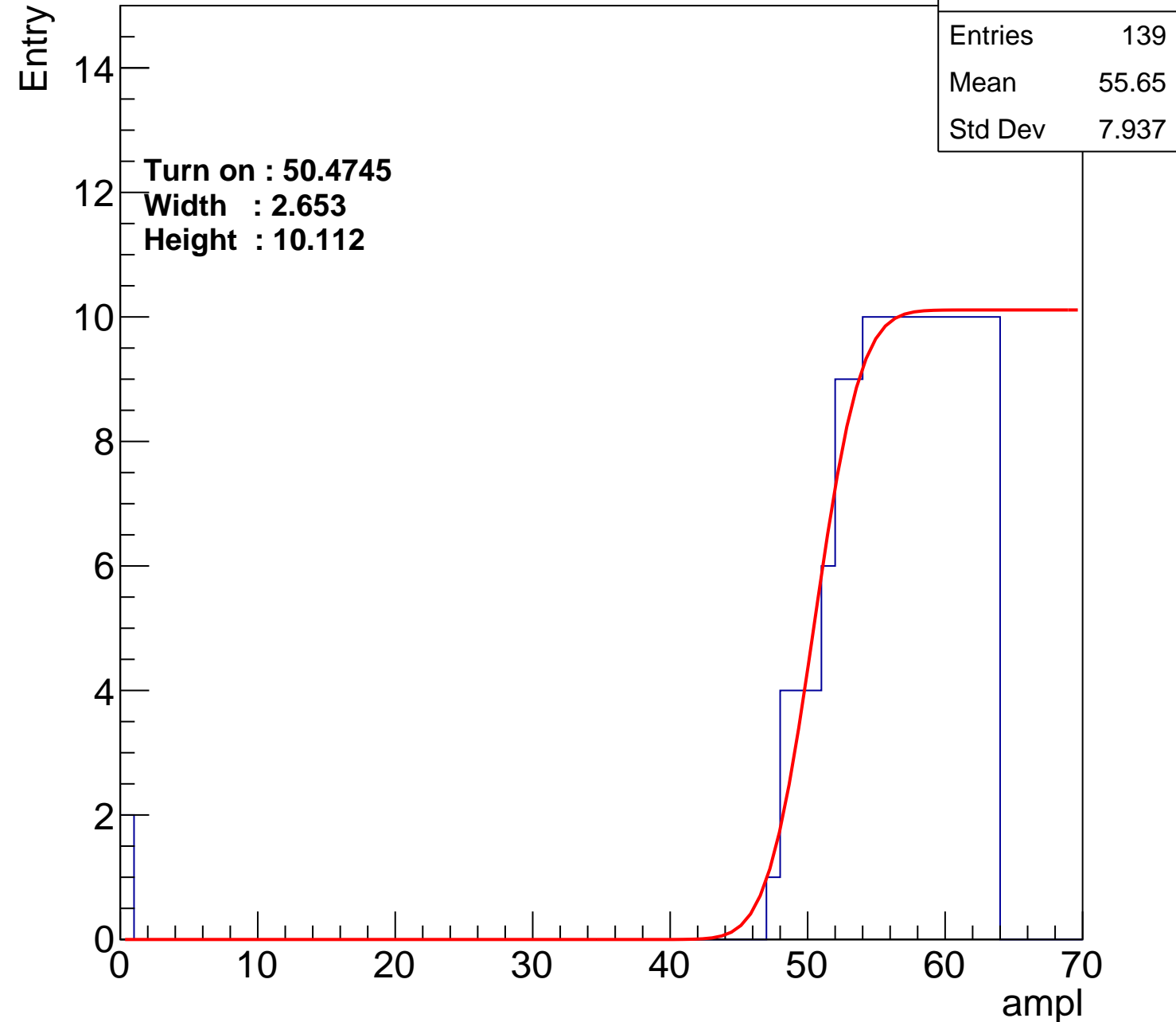
Width : 2.653

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch25

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.56
Std Dev	10.17

Turn on : 49.7971

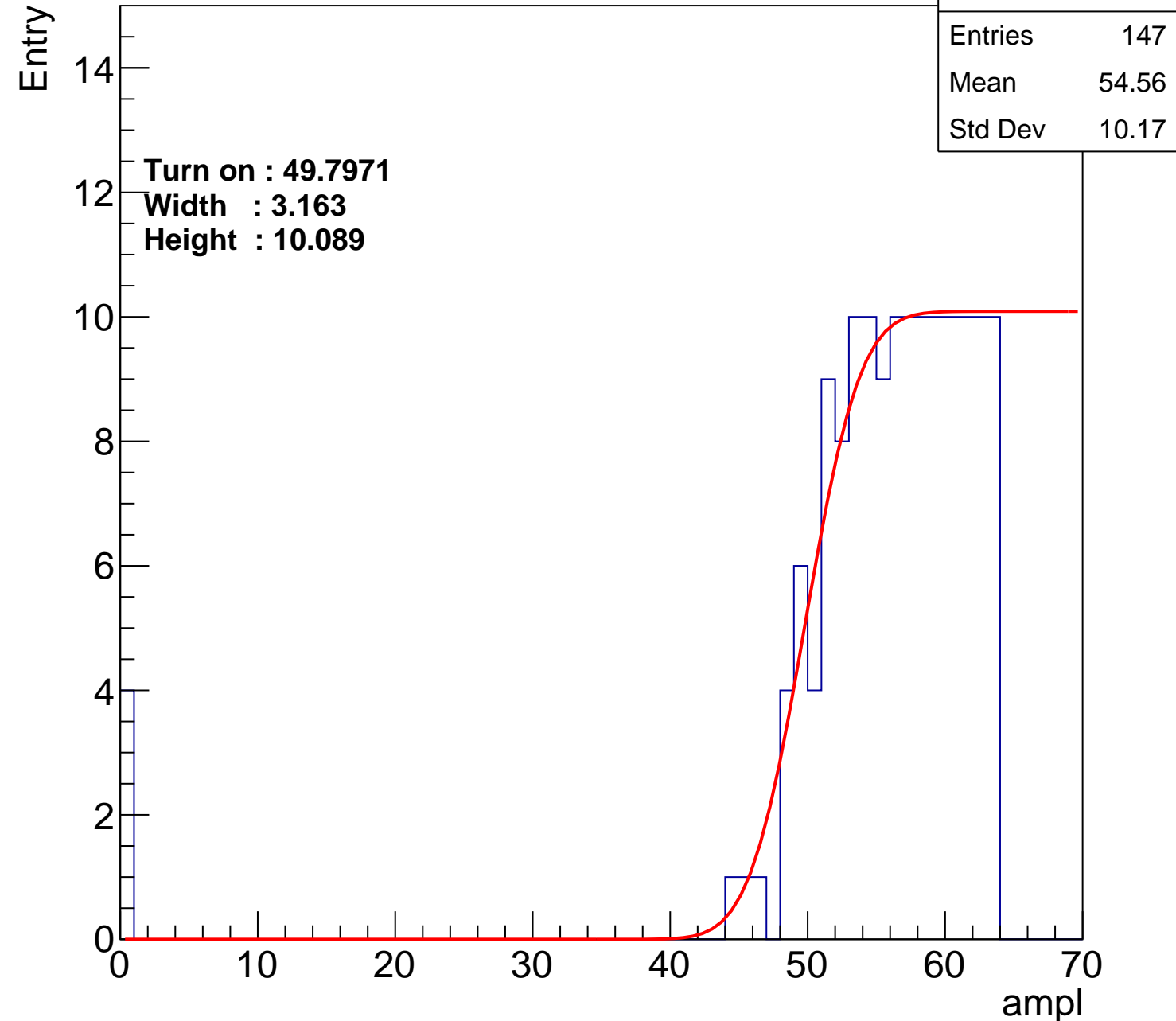
Width : 3.163

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch26

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	55.59
Std Dev	8.038

Turn on : 50.4044

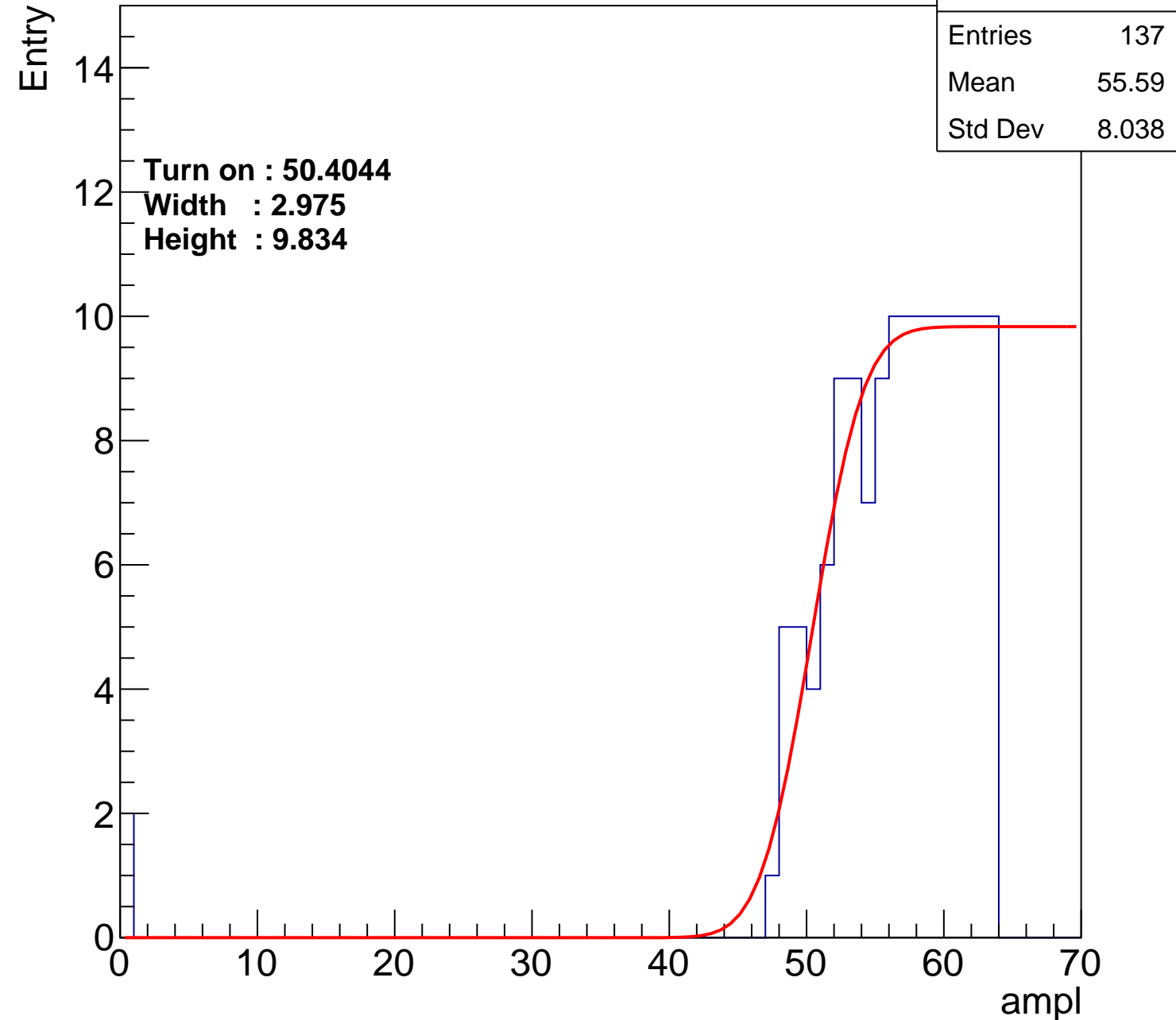
Width : 2.975

Height : 9.834

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch27

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.38
Std Dev	10.23

Turn on : 49.4648

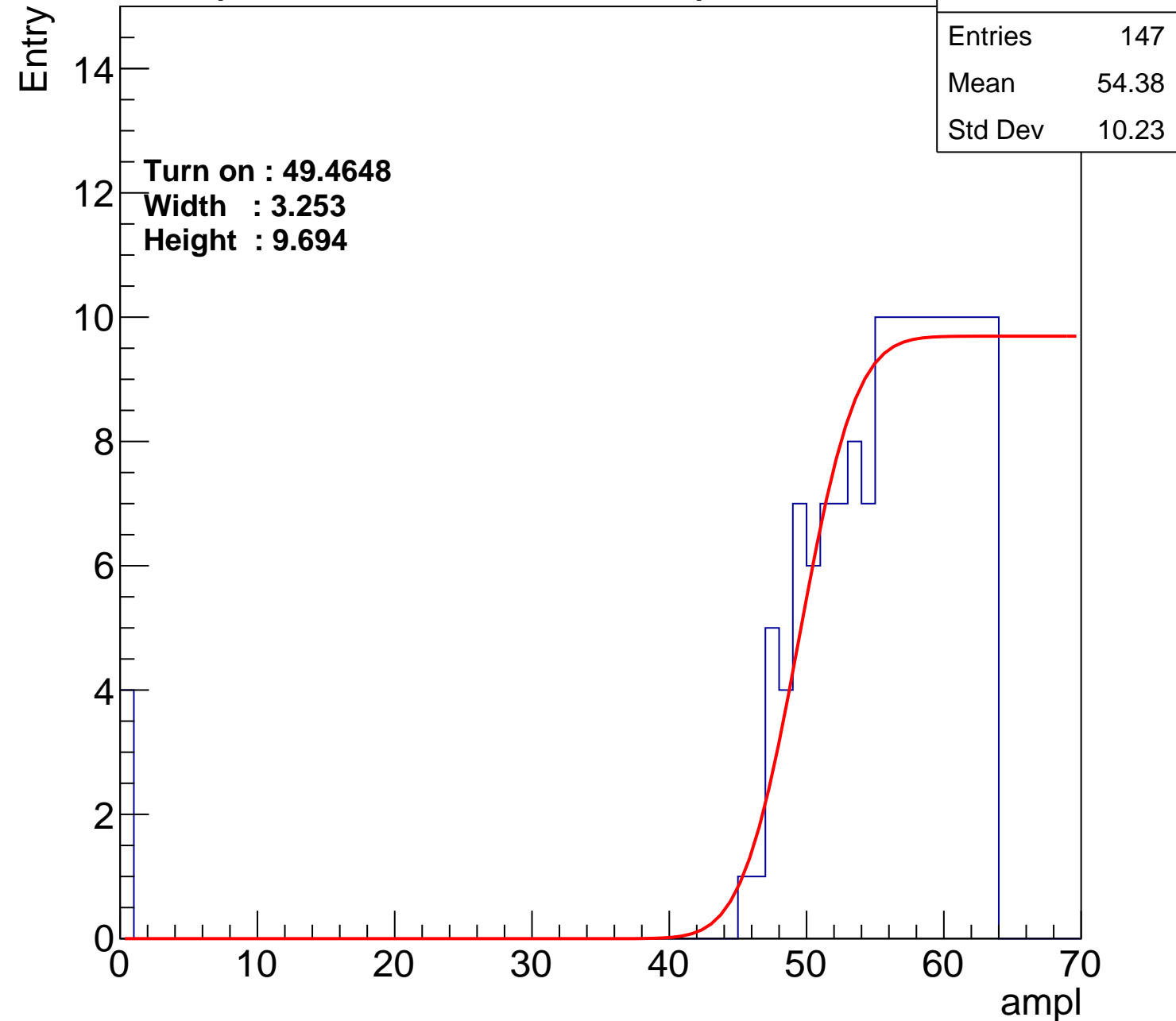
Width : 3.253

Height : 9.694

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch28

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.34
Std Dev	10.68

Turn on : 52.0008

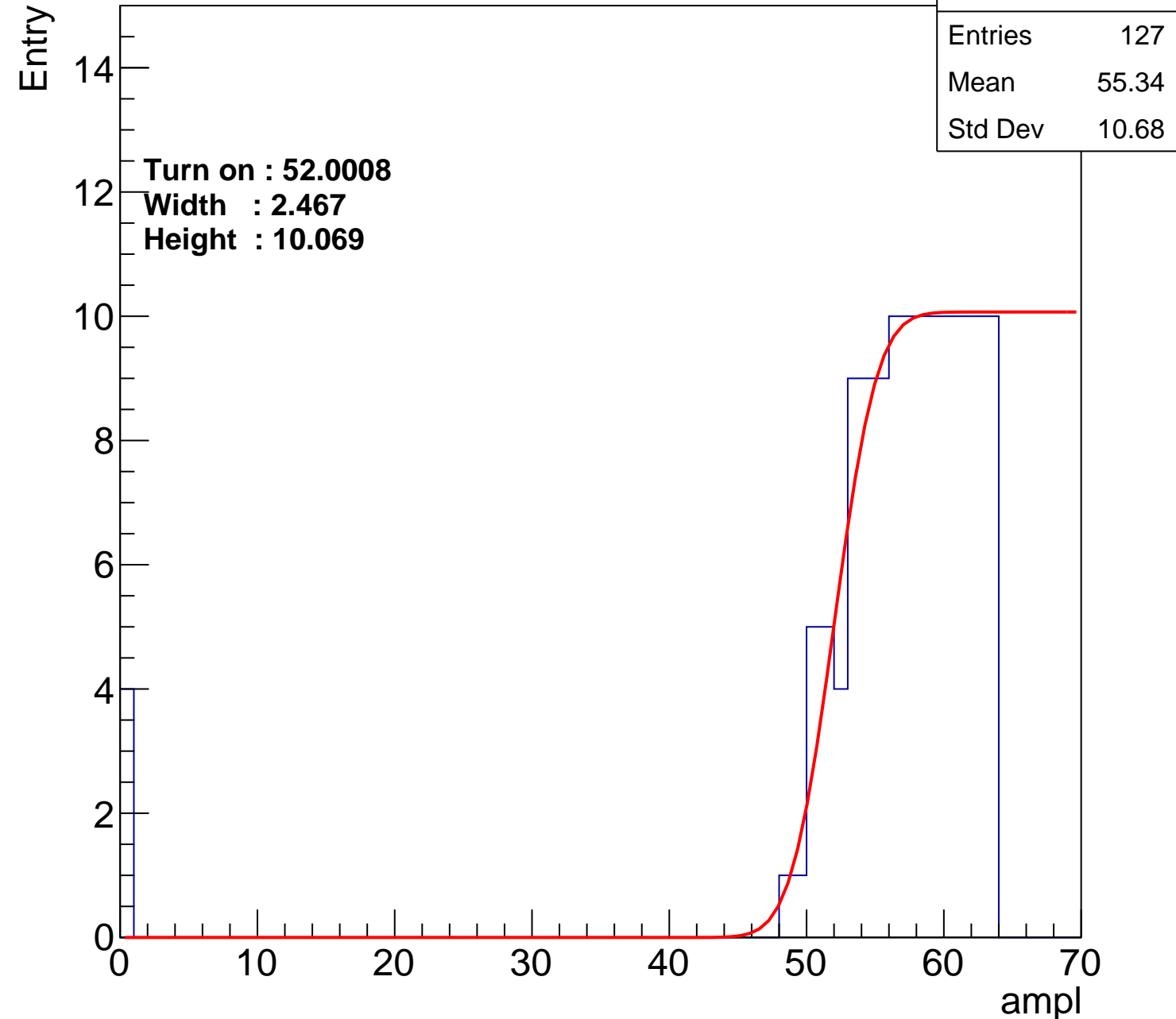
Width : 2.467

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch29

calib_packv5_040323_1717.root, FC#2, port C3

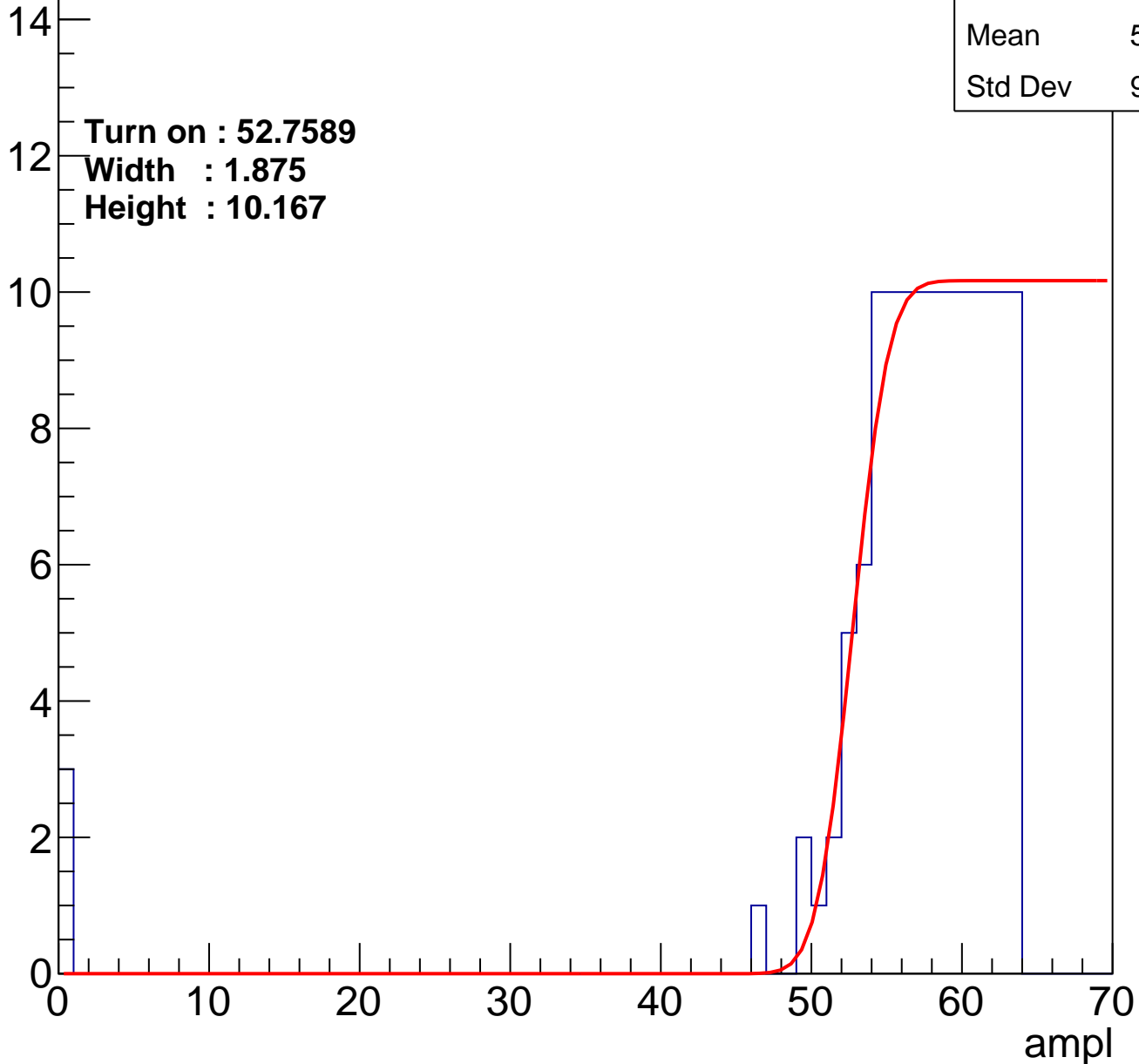
Entries	120
Mean	56.03
Std Dev	9.694

Turn on : 52.7589

Width : 1.875

Height : 10.167

Entry



B0L103S, U22-ch30

calib_packv5_040323_1717.root, FC#2, port C3

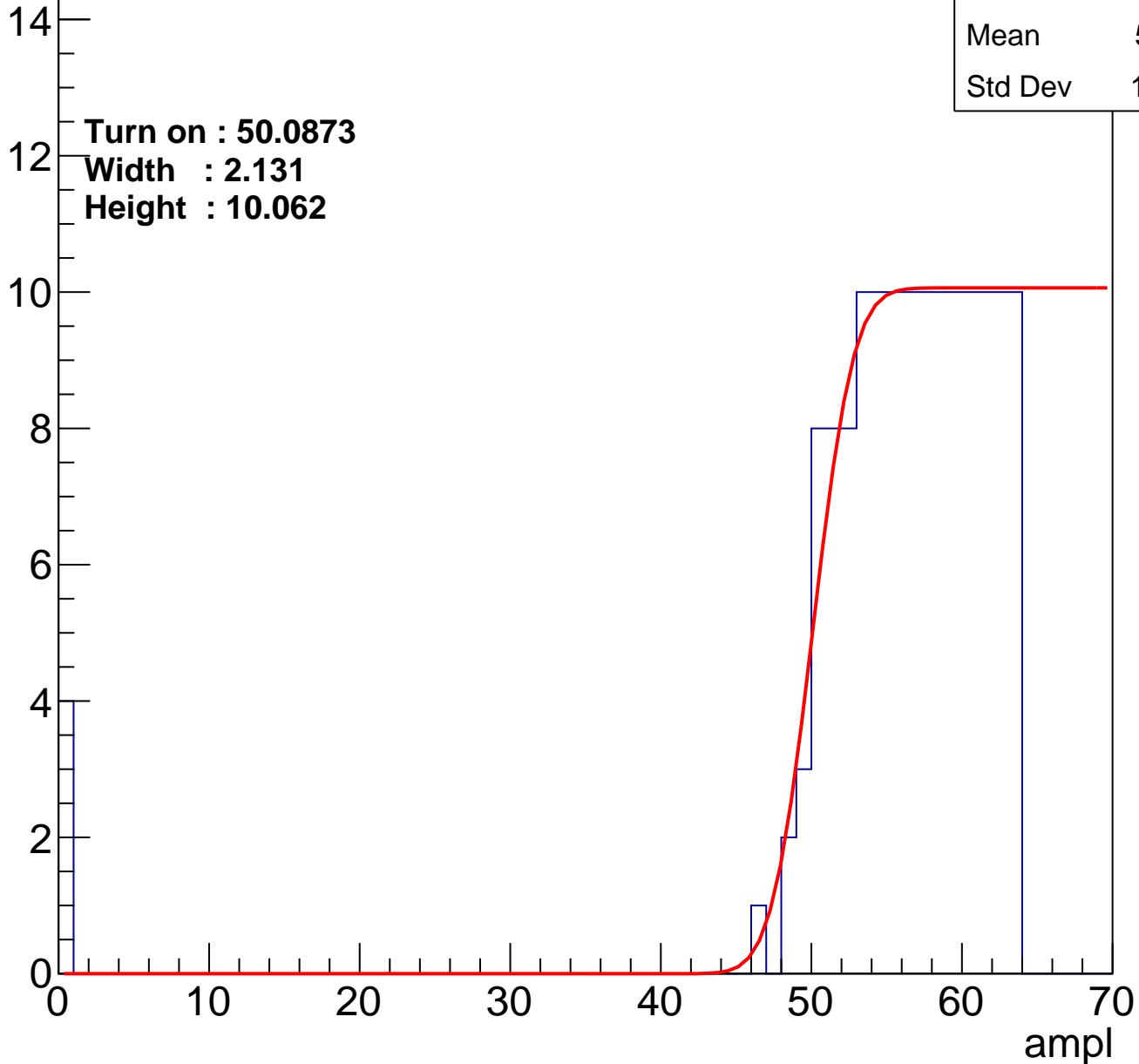
Entries	144
Mean	54.81
Std Dev	10.16

Turn on : 50.0873

Width : 2.131

Height : 10.062

Entry



B0L103S, U22-ch31

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.87
Std Dev	9.598

Turn on : 51.9472

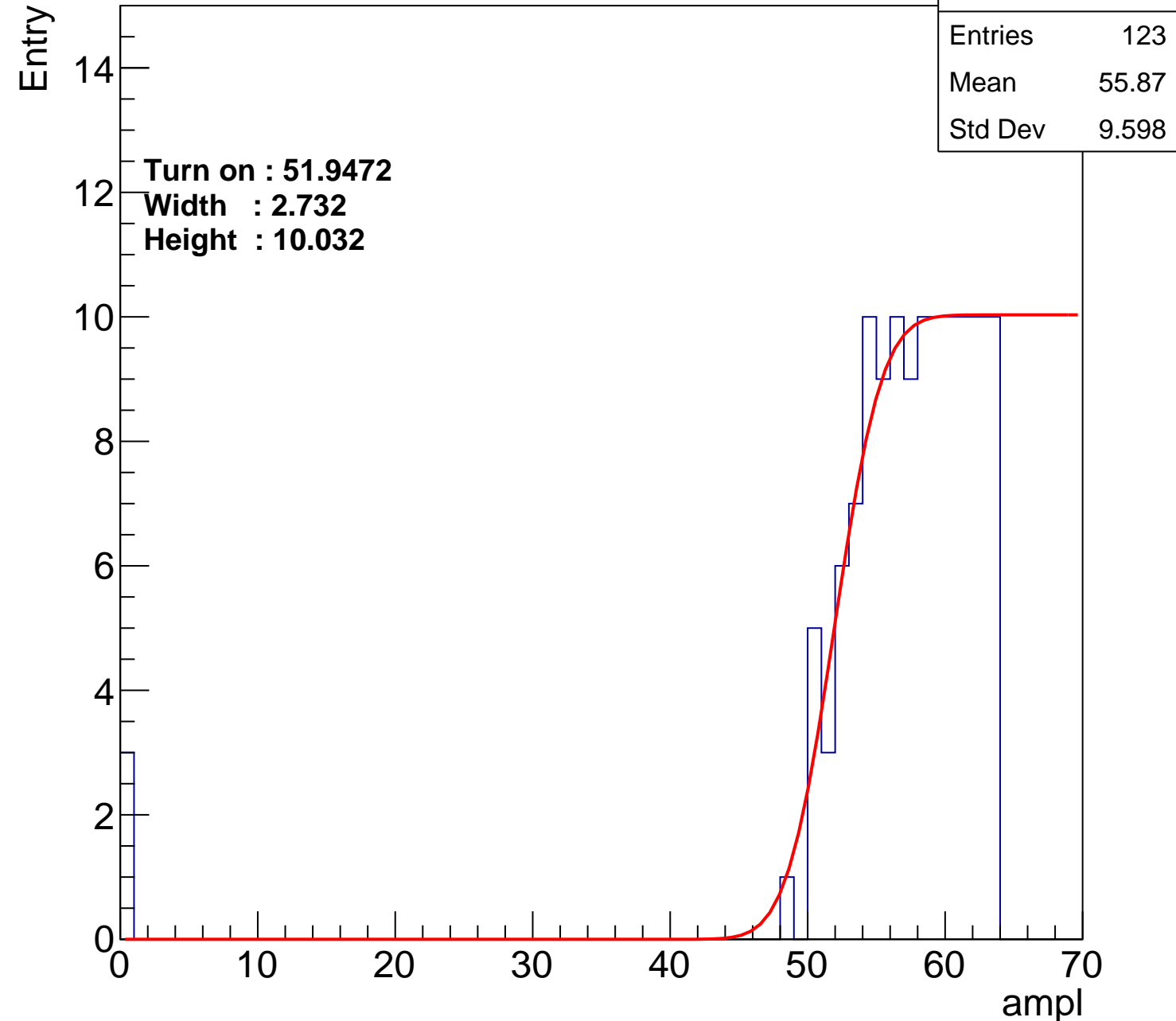
Width : 2.732

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch32

calib_packv5_040323_1717.root, FC#2, port C3

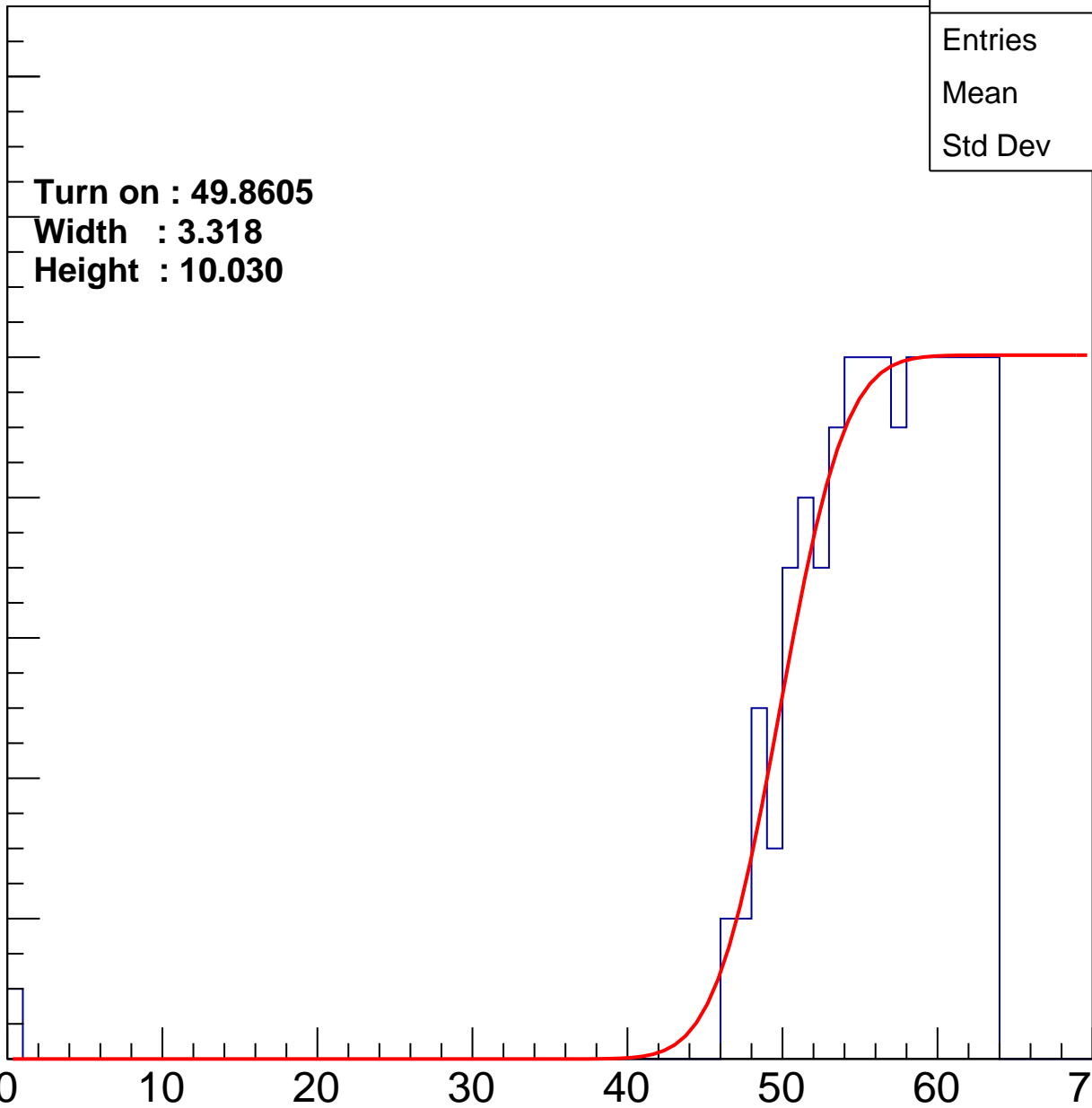
Entry

14
12
10
8
6
4
2
0

Turn on : 49.8605
Width : 3.318
Height : 10.030

Entries	143
Mean	55.7
Std Dev	6.502

ampl



B0L103S, U22-ch33

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.05
Std Dev	10.53

Turn on : 51.0938

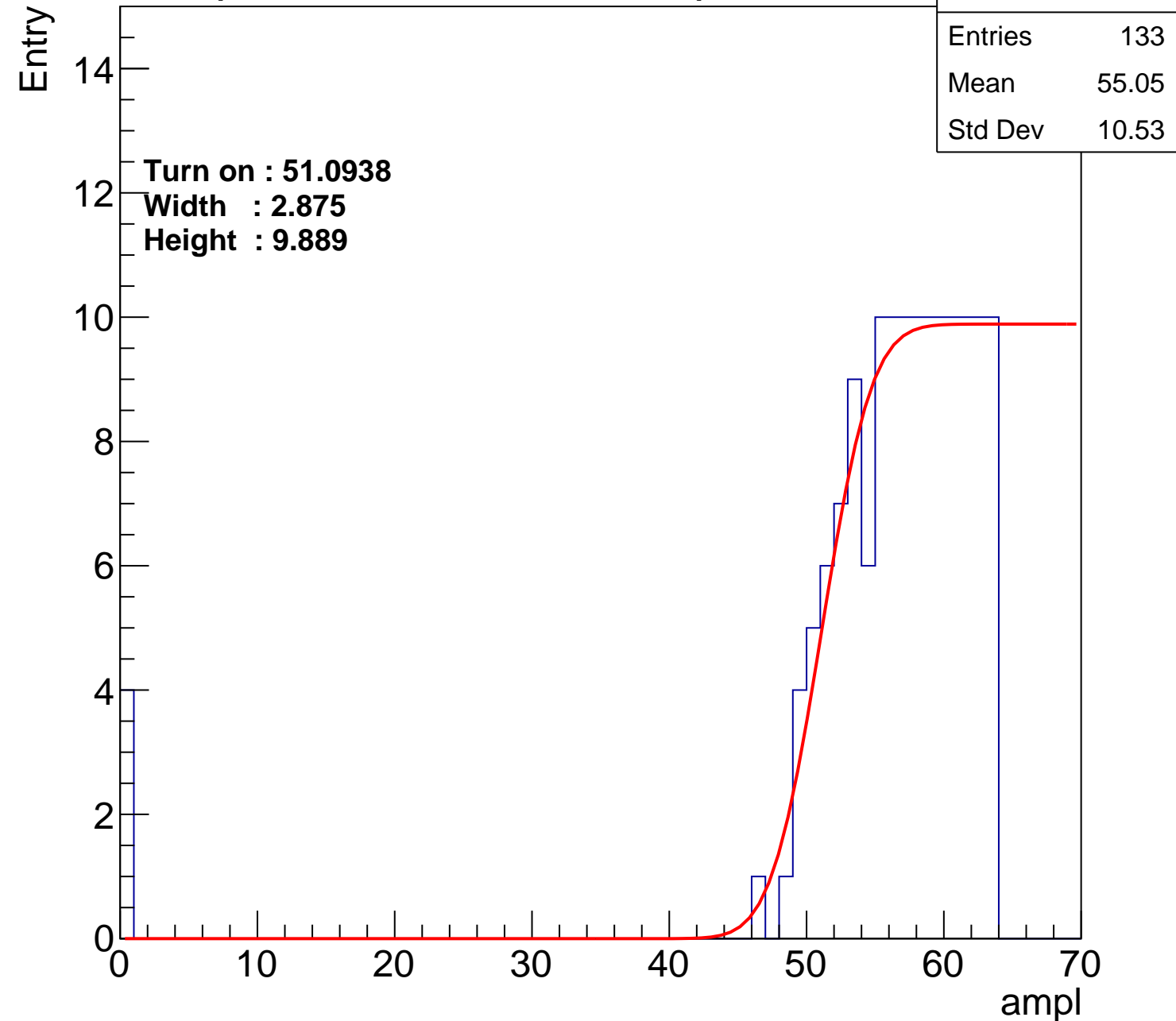
Width : 2.875

Height : 9.889

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch34

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.68
Std Dev	6.536

Turn on : 52.2164

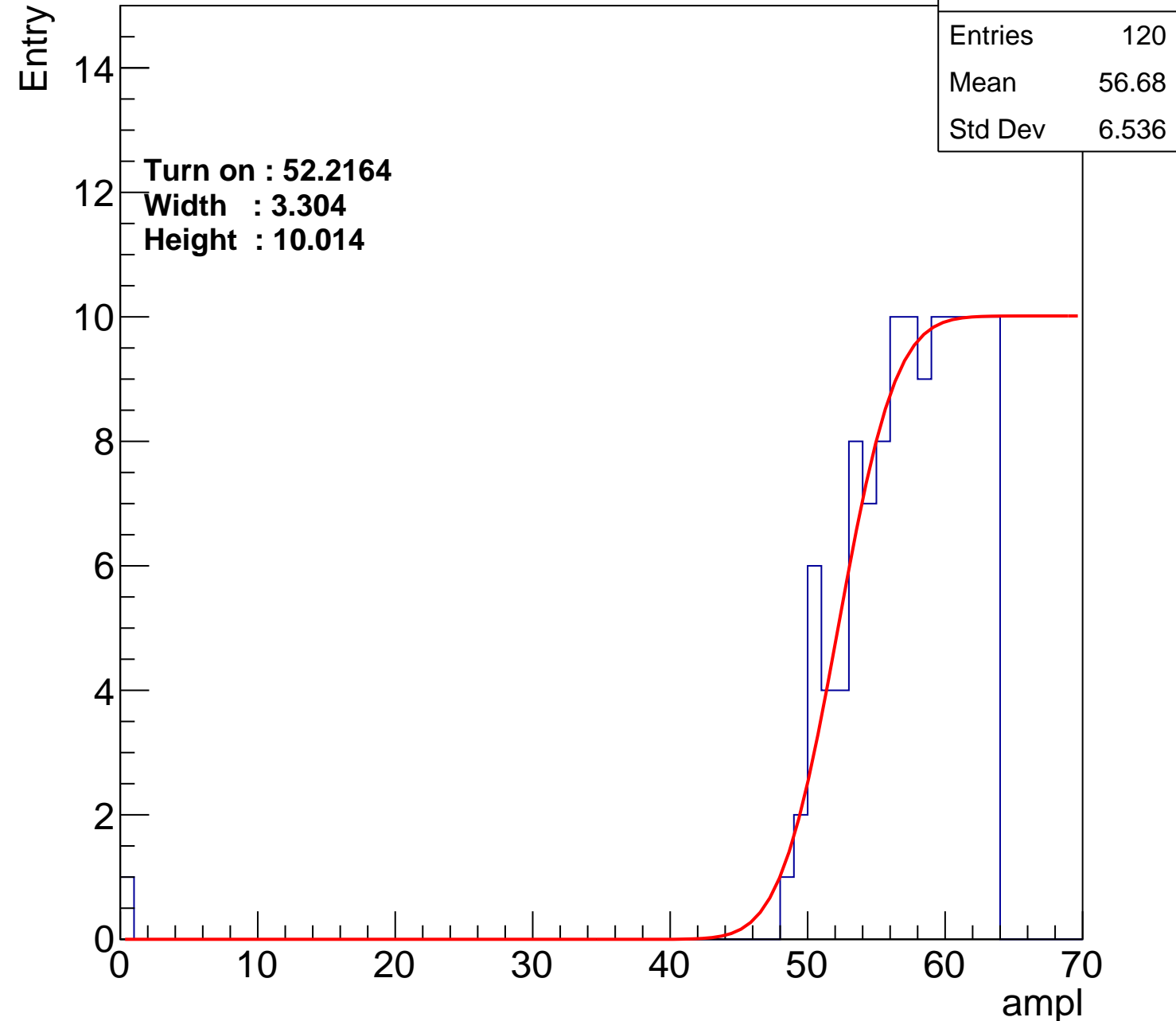
Width : 3.304

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch35

calib_packv5_040323_1717.root, FC#2, port C3

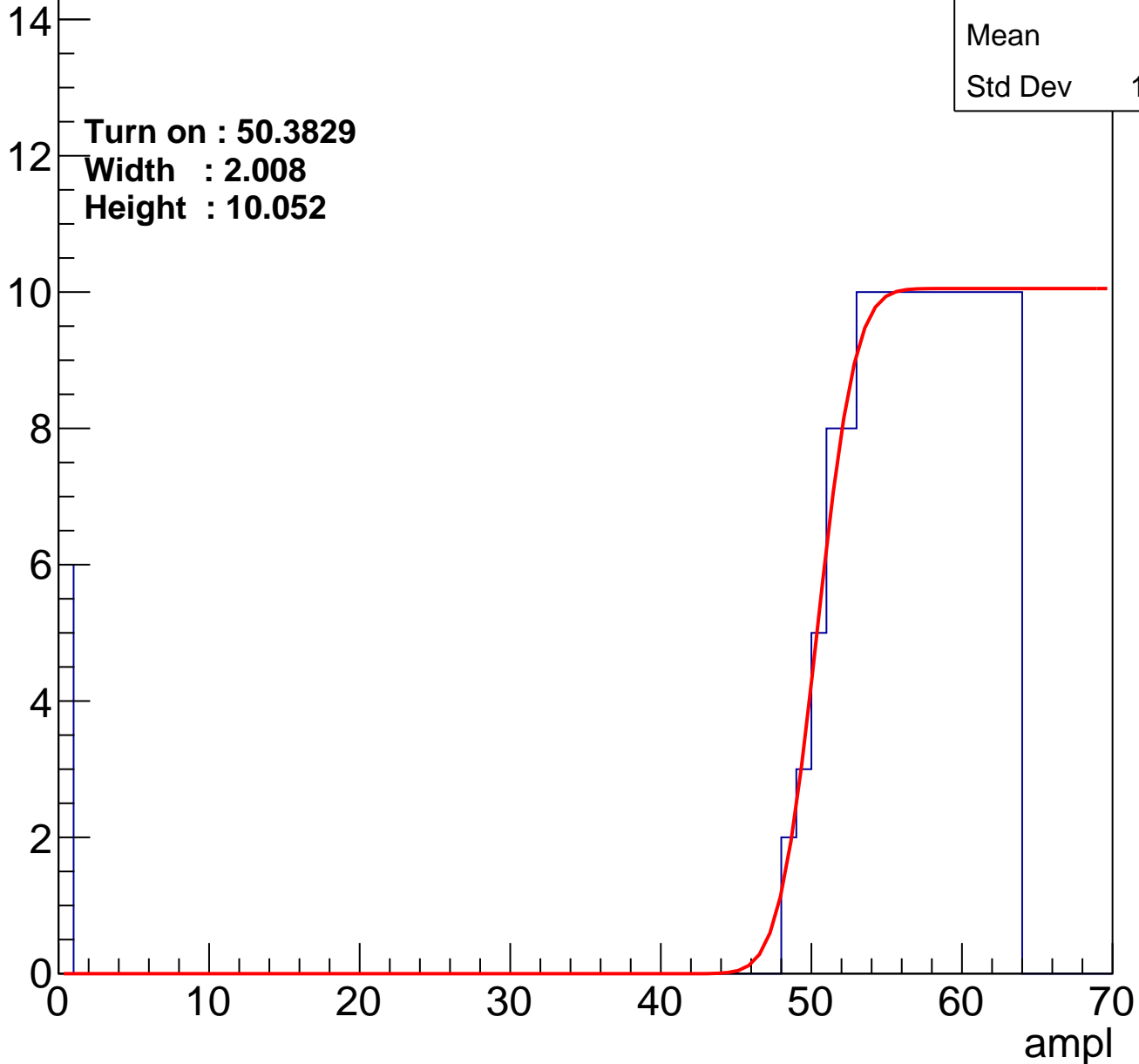
Entries	142
Mean	54.2
Std Dev	12.07

Turn on : 50.3829

Width : 2.008

Height : 10.052

Entry



B0L103S, U22-ch36

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.38
Std Dev	11.34

Turn on : 50.7138

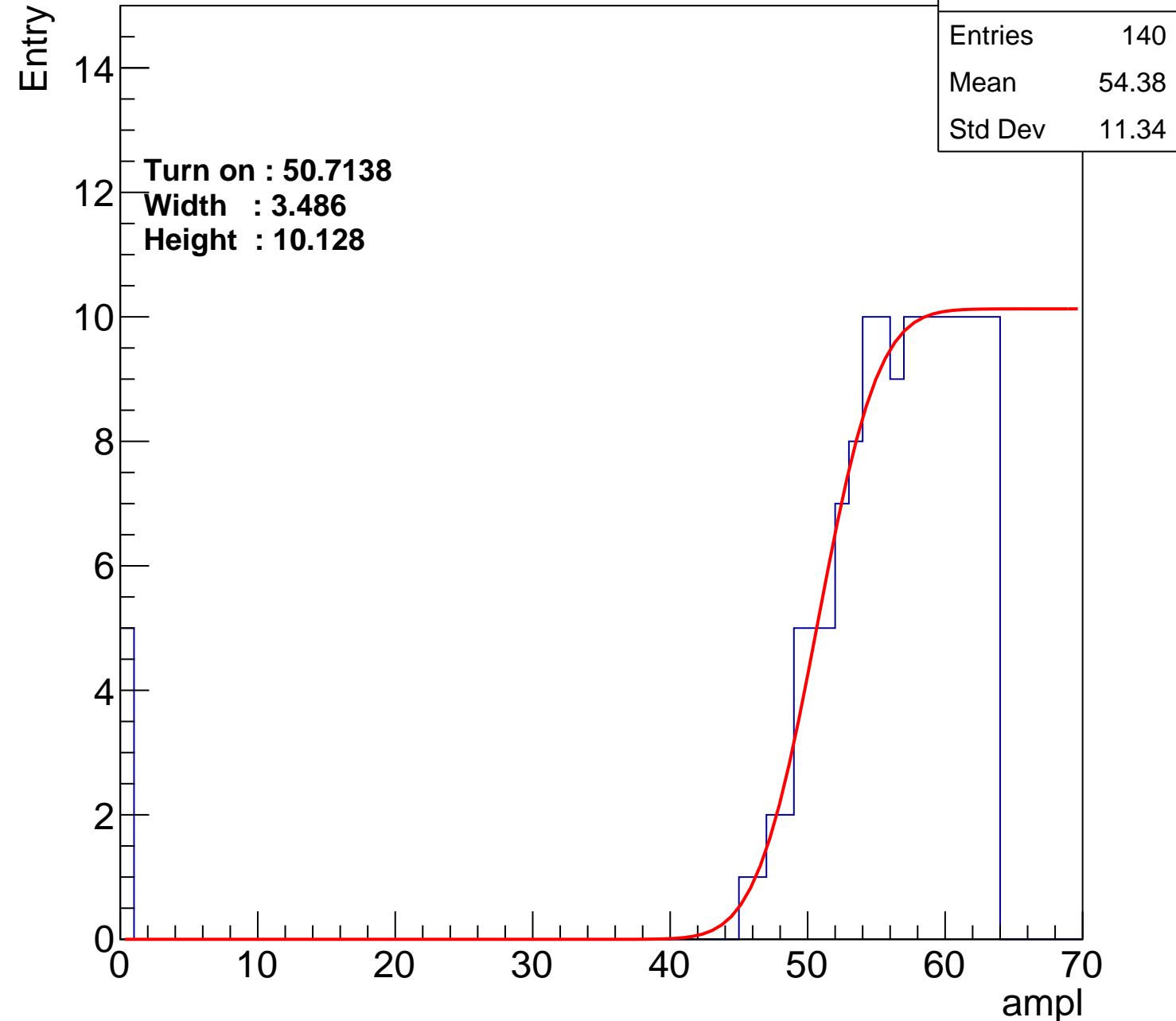
Width : 3.486

Height : 10.128

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch37

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	54.99
Std Dev	10.46

Turn on : 51.5355

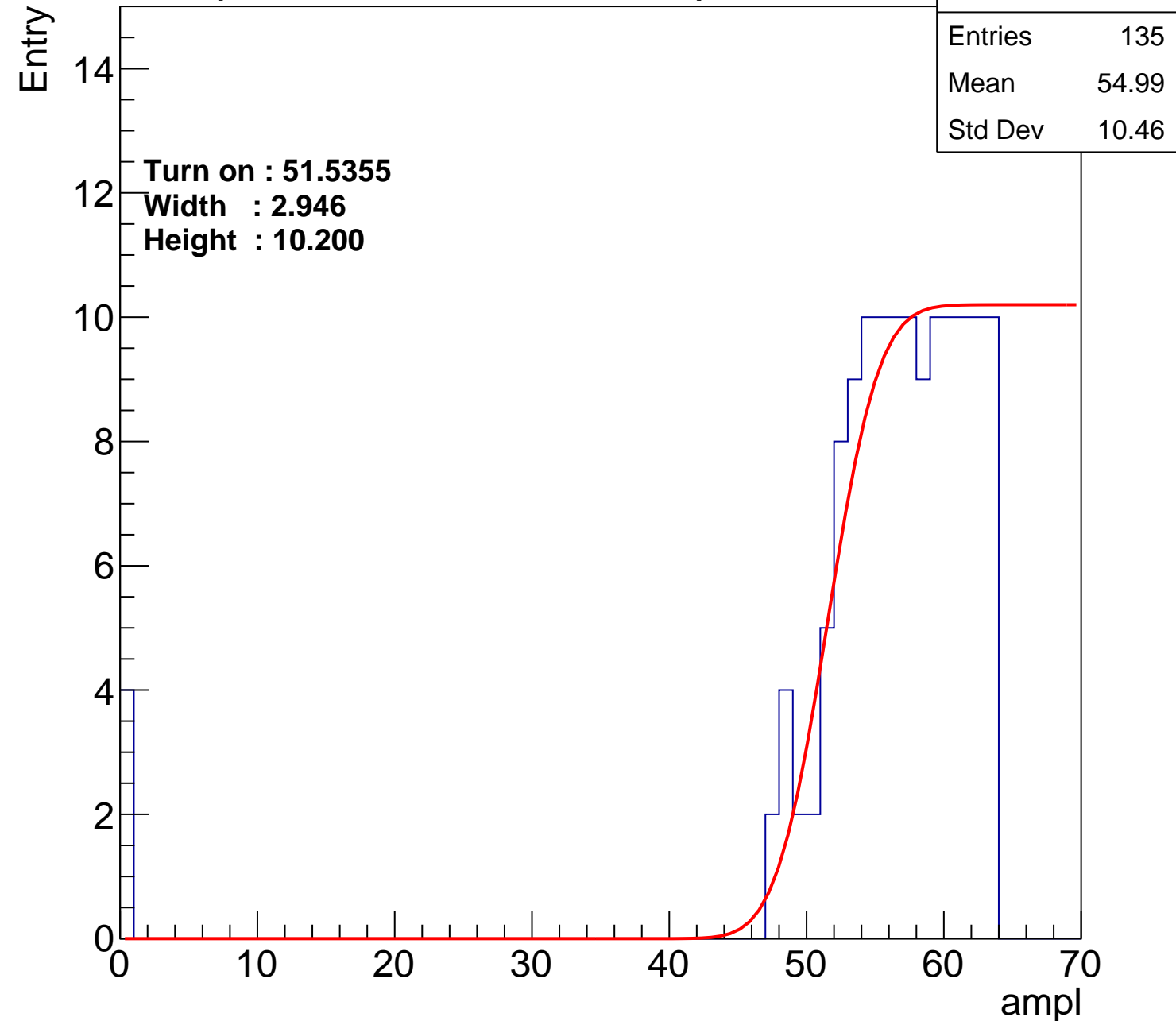
Width : 2.946

Height : 10.200

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch38

calib_packv5_040323_1717.root, FC#2, port C3

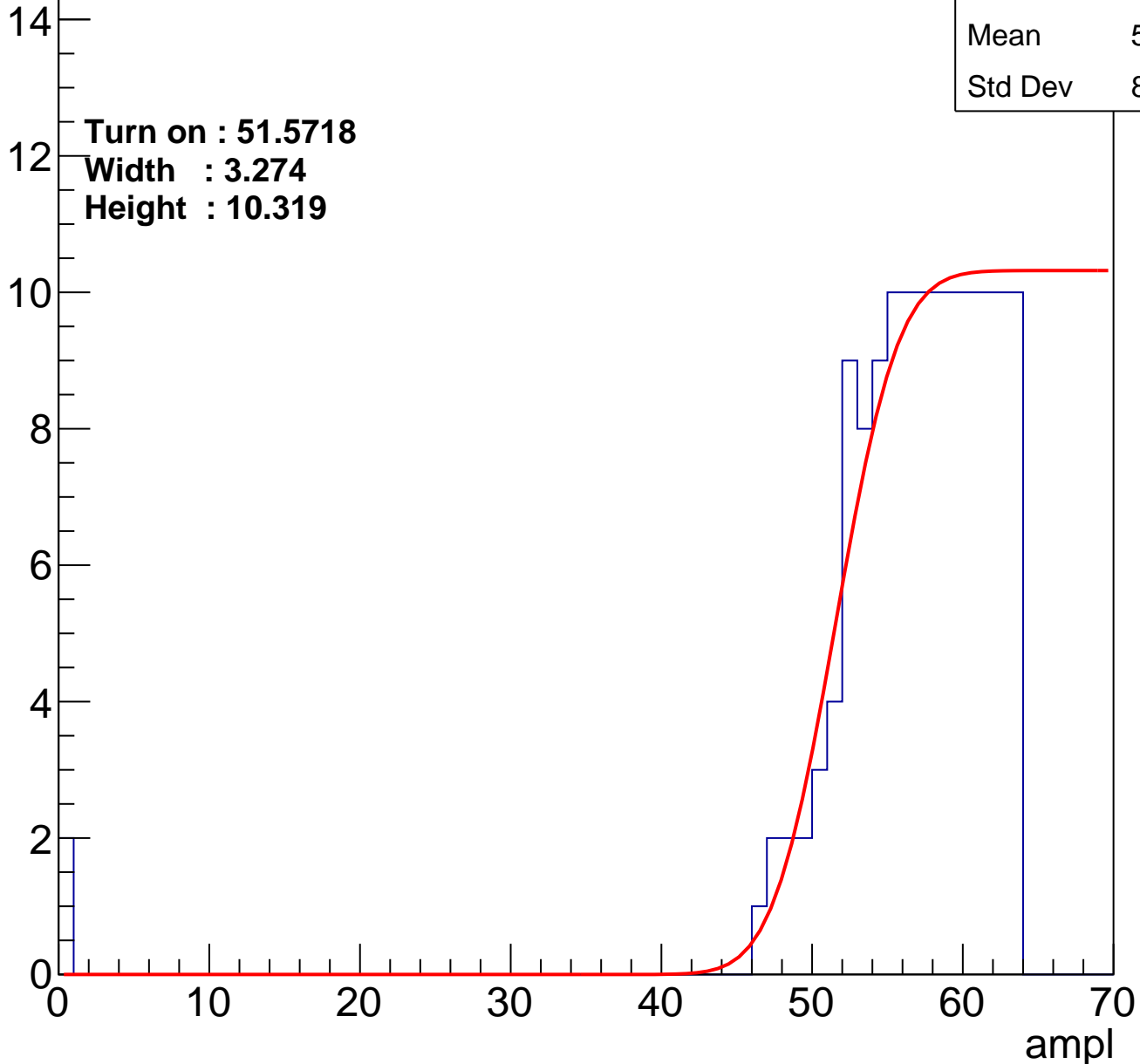
Entries	132
Mean	55.88
Std Dev	8.085

Turn on : 51.5718

Width : 3.274

Height : 10.319

Entry



B0L103S, U22-ch39

calib_packv5_040323_1717.root, FC#2, port C3

Entries	110
Mean	56.22
Std Dev	10.07

Turn on : 53.5710

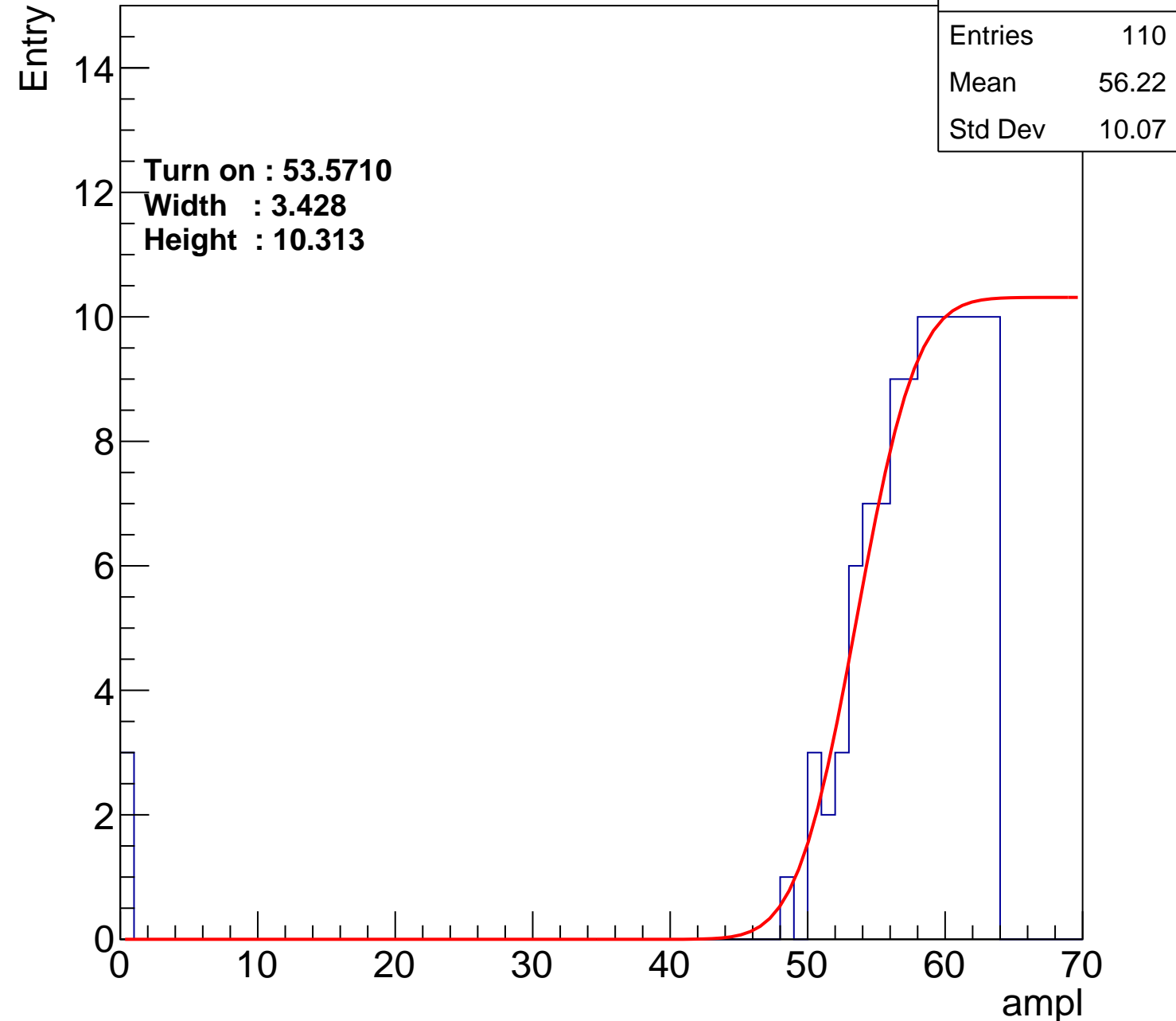
Width : 3.428

Height : 10.313

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch40

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	56.08
Std Dev	8.059

Turn on : 51.2599

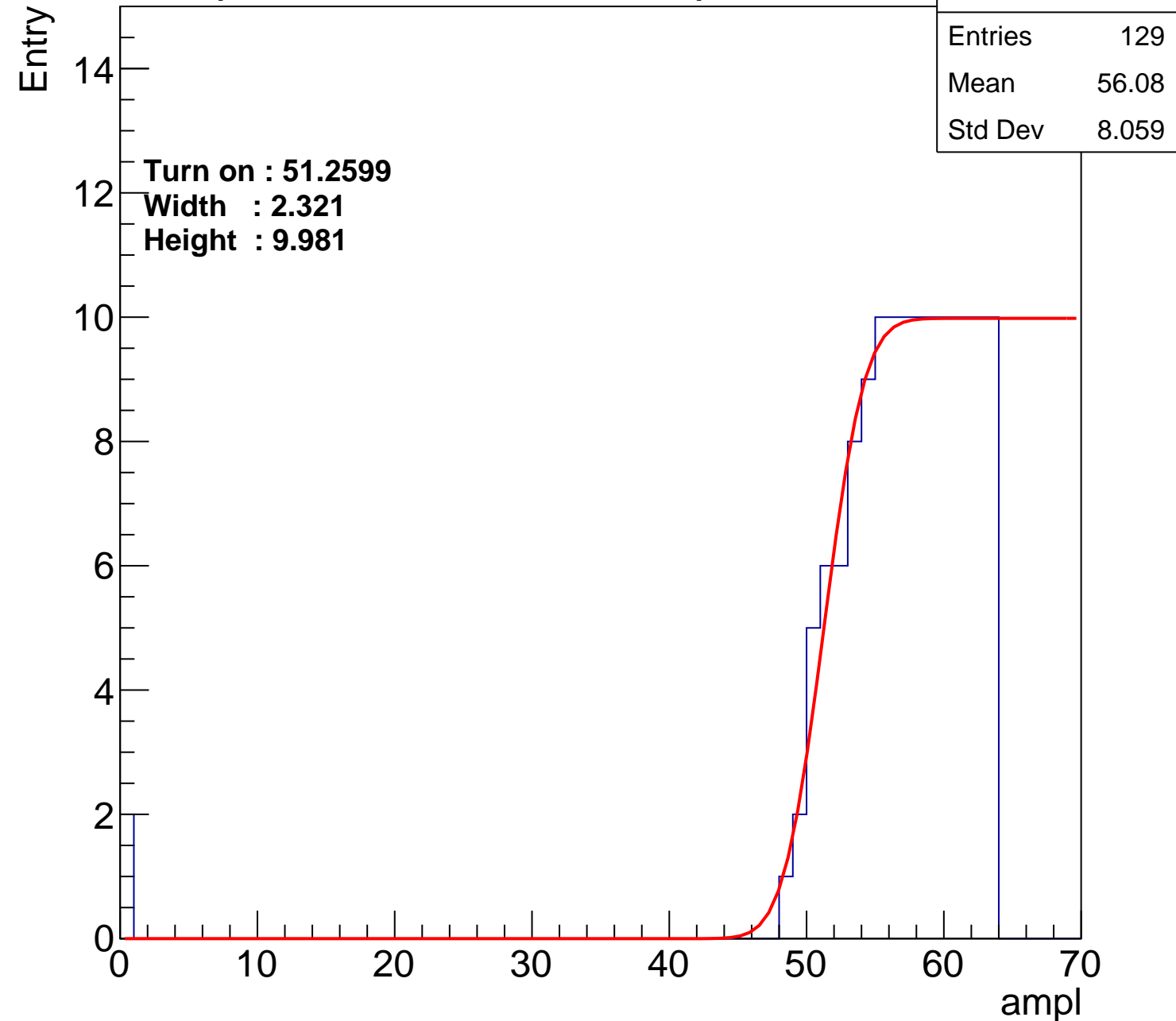
Width : 2.321

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch41

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.25
Std Dev	9.185

Turn on : 50.1530

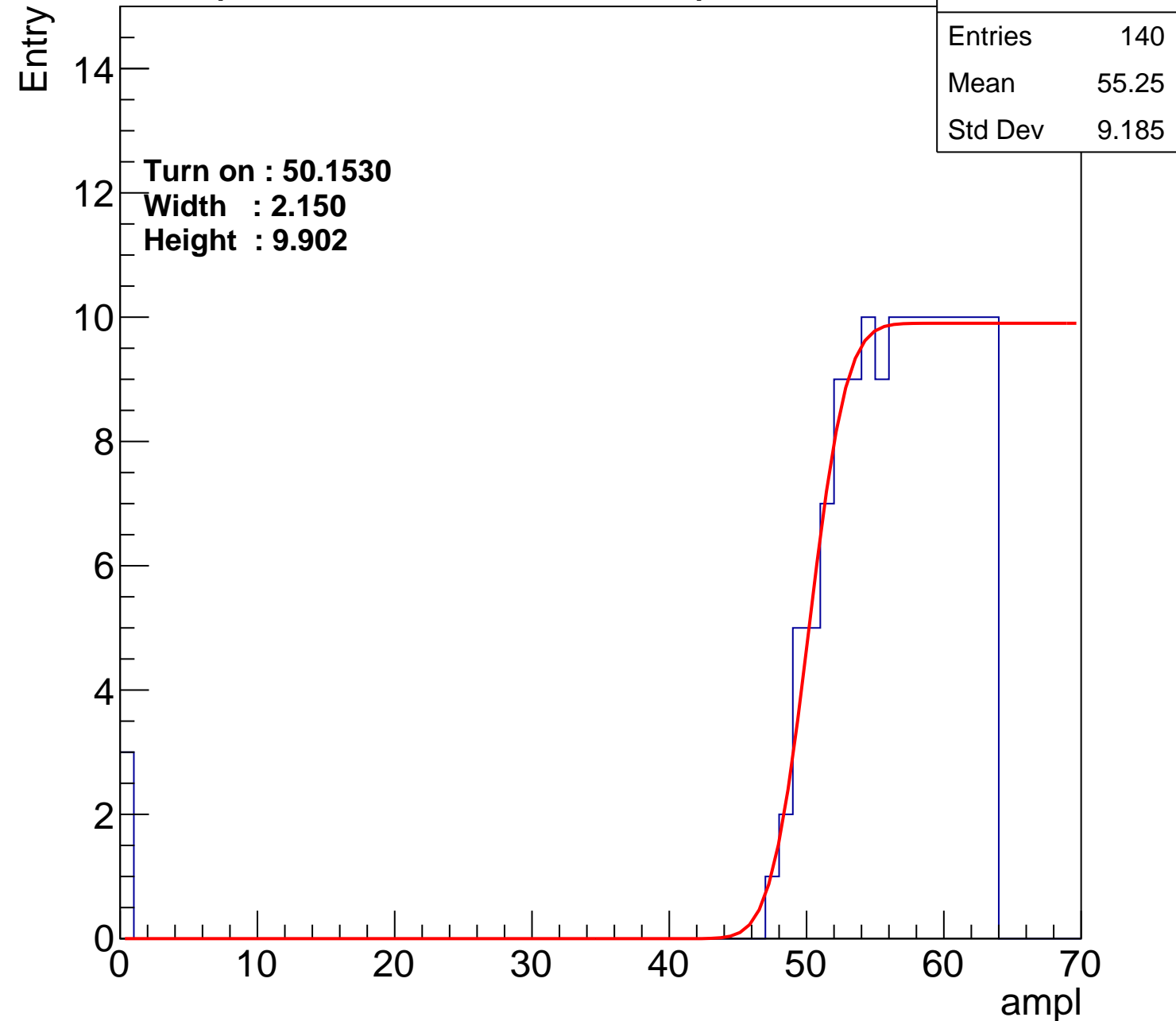
Width : 2.150

Height : 9.902

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch42

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.73
Std Dev	9.437

Turn on : 51.2152

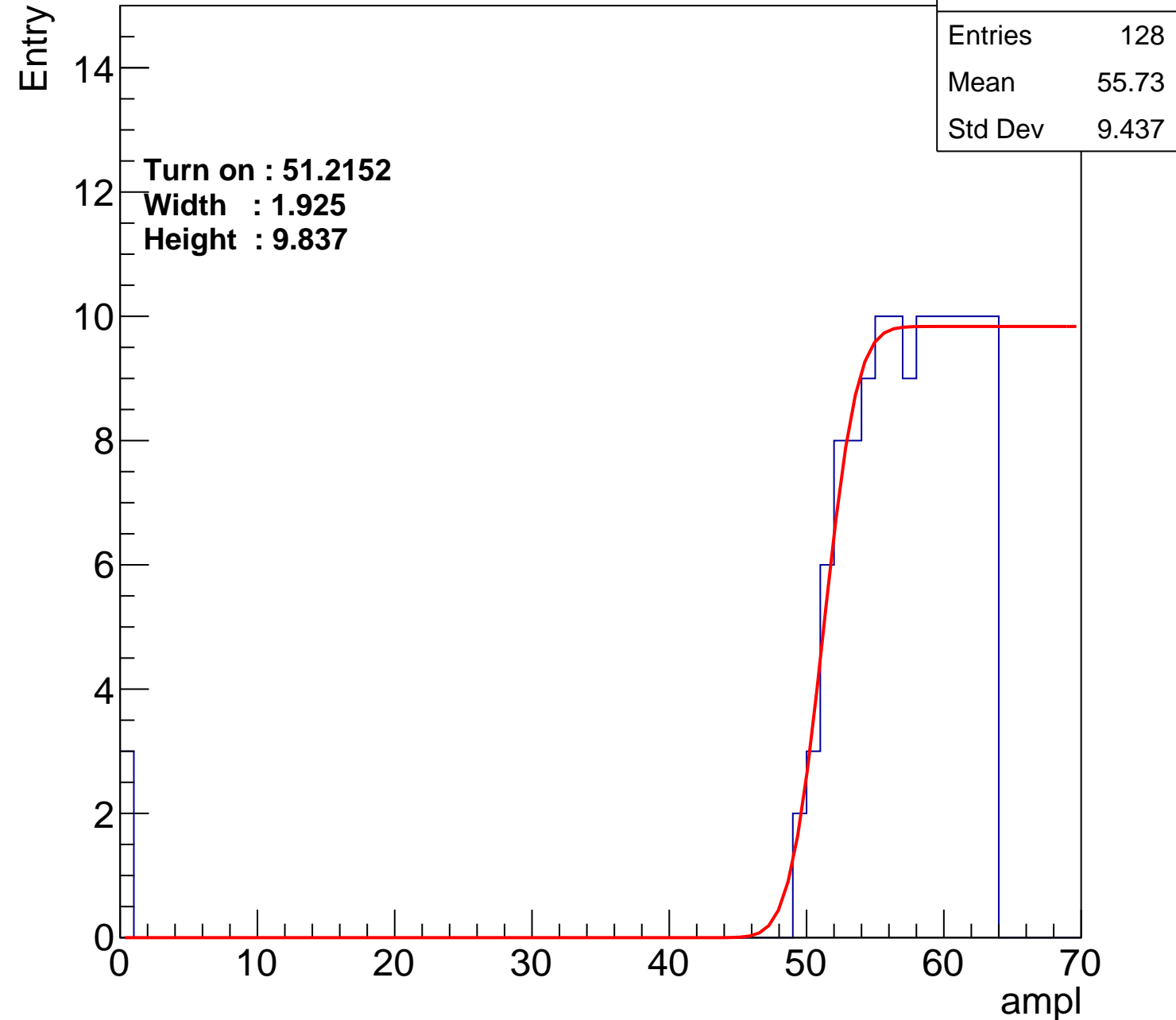
Width : 1.925

Height : 9.837

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch43

calib_packv5_040323_1717.root, FC#2, port C3

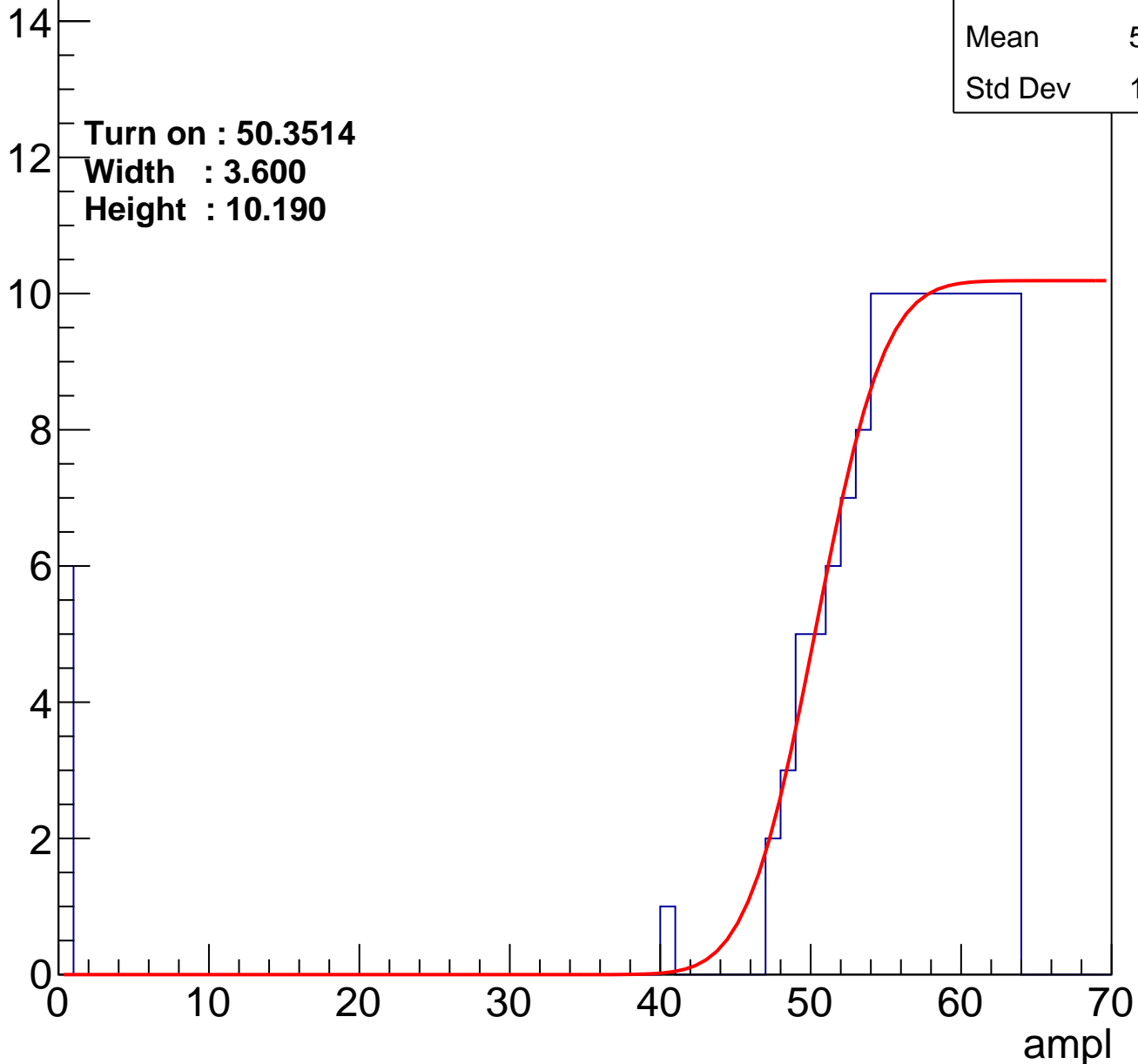
Entries	143
Mean	53.97
Std Dev	12.13

Turn on : 50.3514

Width : 3.600

Height : 10.190

Entry



B0L103S, U22-ch44

calib_packv5_040323_1717.root, FC#2, port C3

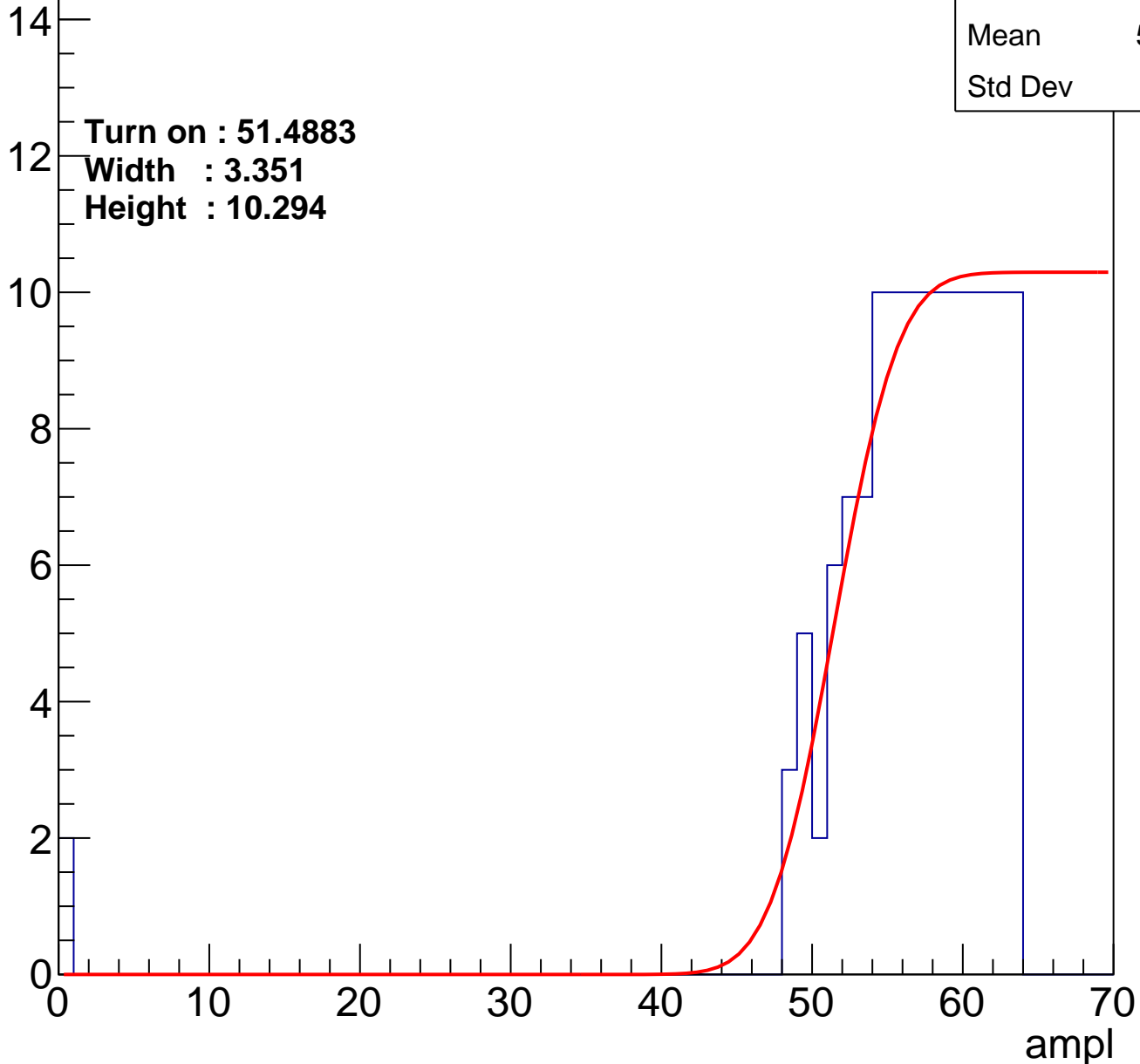
Entries	132
Mean	55.91
Std Dev	8.05

Turn on : 51.4883

Width : 3.351

Height : 10.294

Entry



B0L103S, U22-ch45

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.28
Std Dev	10.91

Turn on : 52.6062

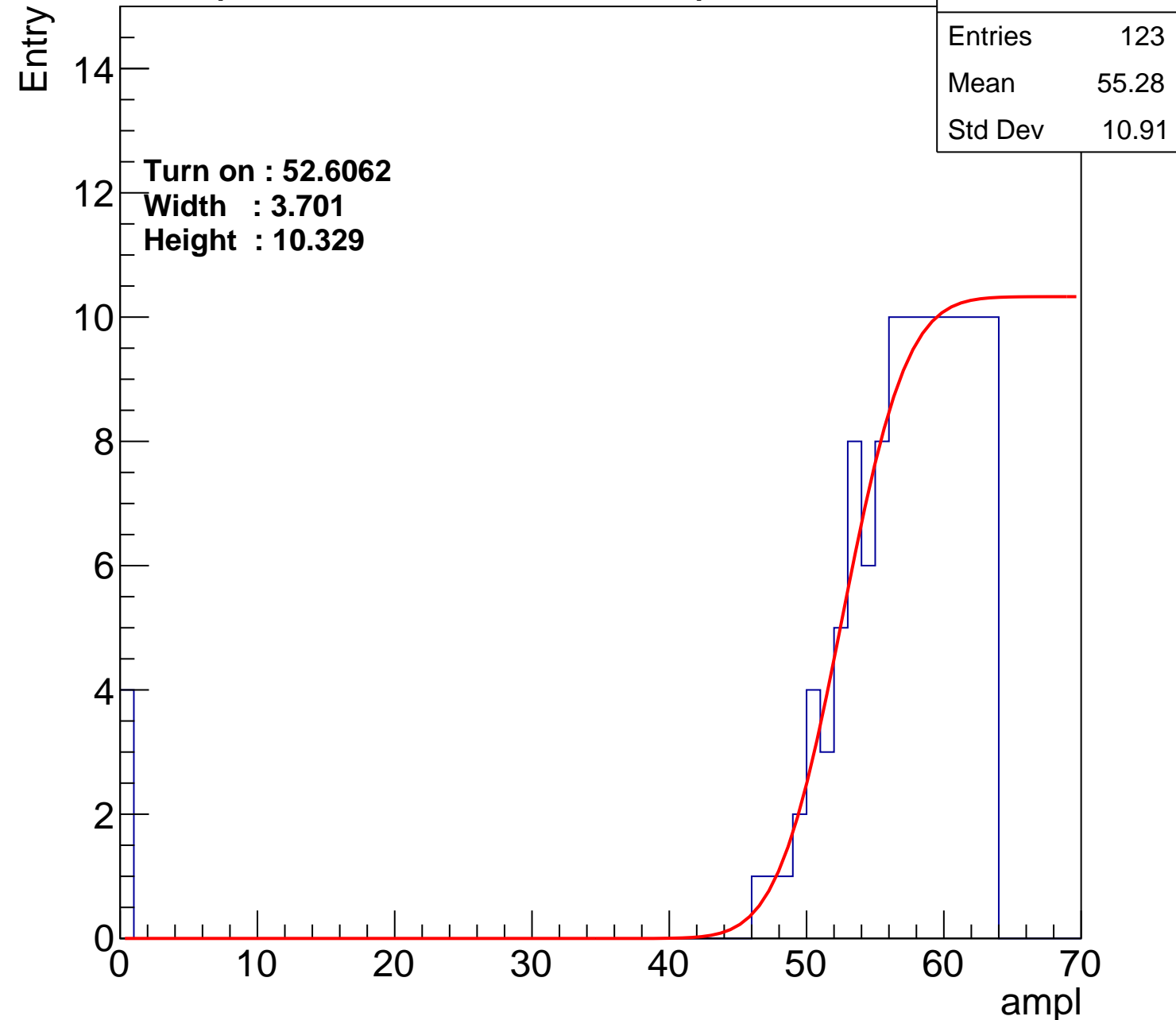
Width : 3.701

Height : 10.329

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch46

calib_packv5_040323_1717.root, FC#2, port C3

Entries	166
Mean	54.15
Std Dev	8.88

Turn on : 47.8799

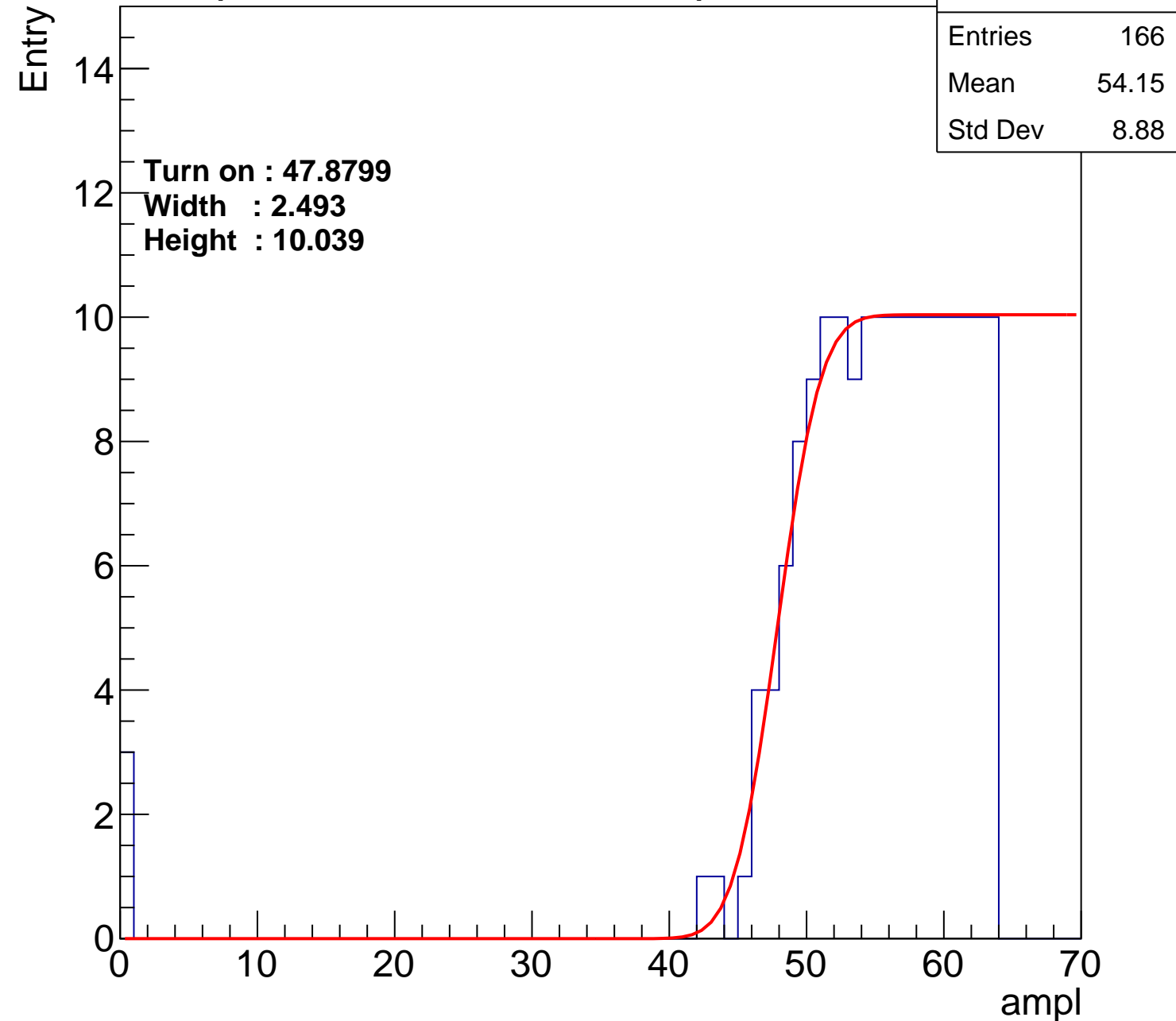
Width : 2.493

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch47

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.51
Std Dev	8.359

Turn on : 52.6693

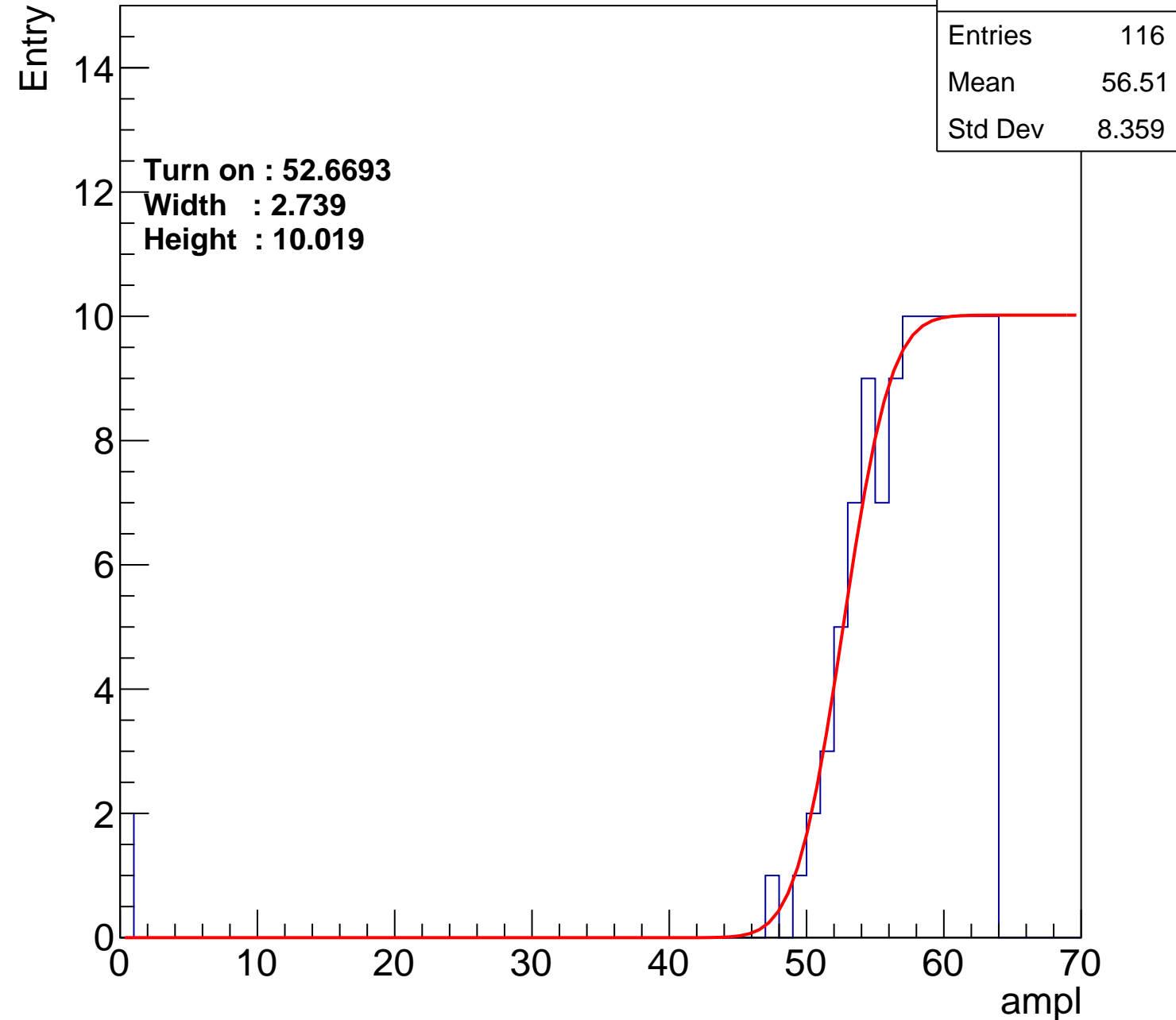
Width : 2.739

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch48

calib_packv5_040323_1717.root, FC#2, port C3

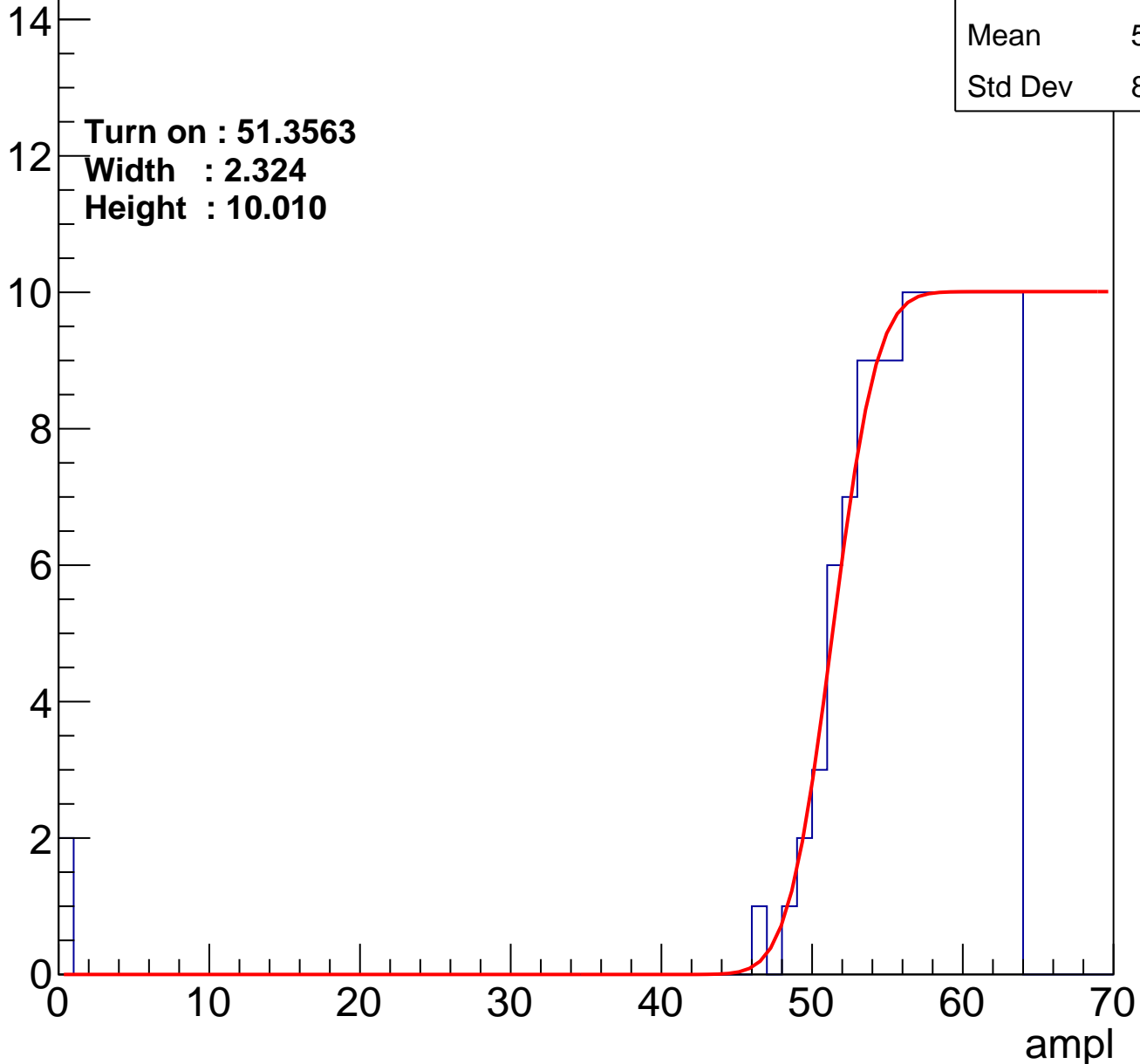
Entries	129
Mean	56.05
Std Dev	8.084

Turn on : 51.3563

Width : 2.324

Height : 10.010

Entry



B0L103S, U22-ch49

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.85
Std Dev	9.573

Turn on : 51.9155

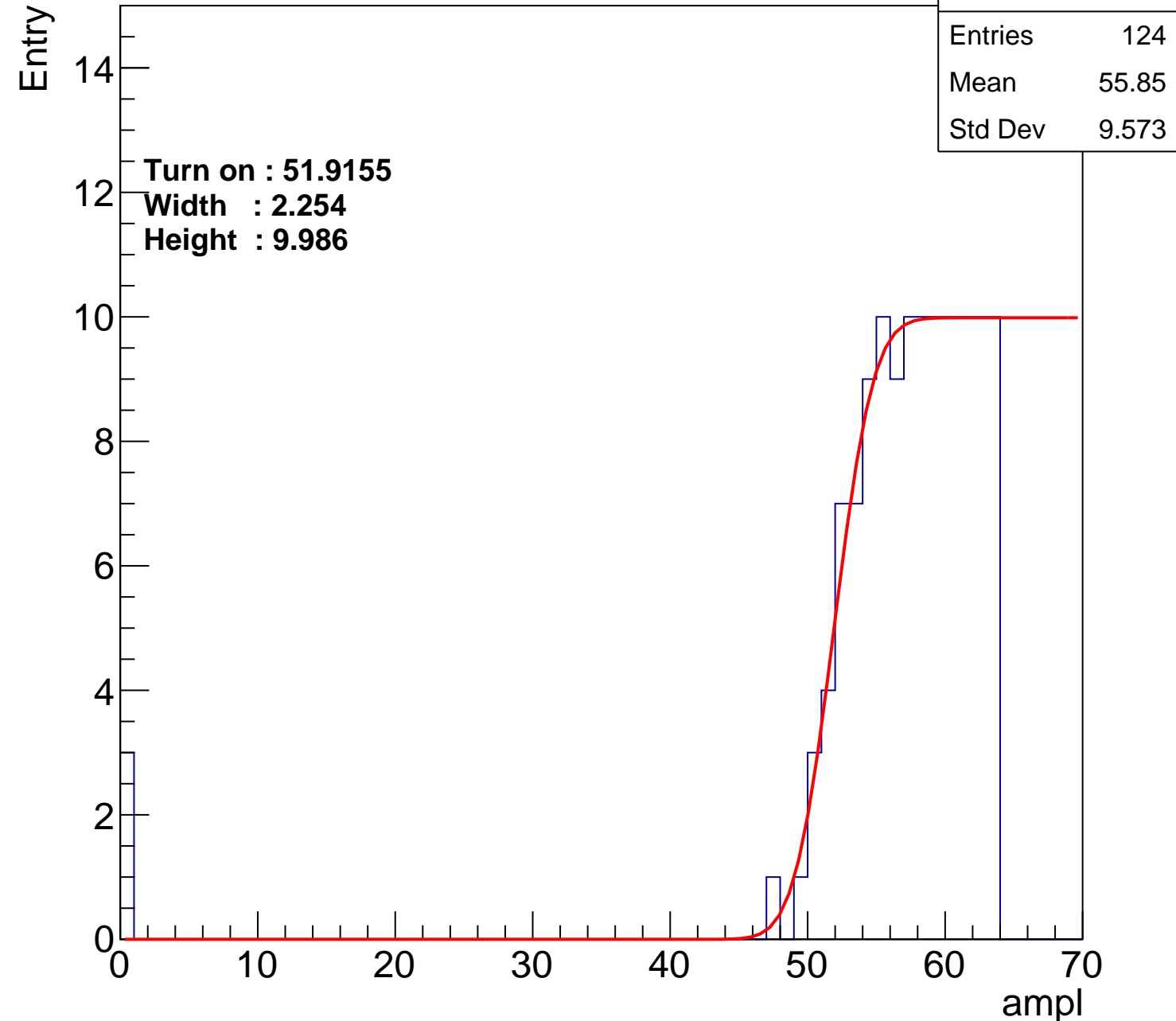
Width : 2.254

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch50

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.19
Std Dev	9.198

Turn on : 50.6002

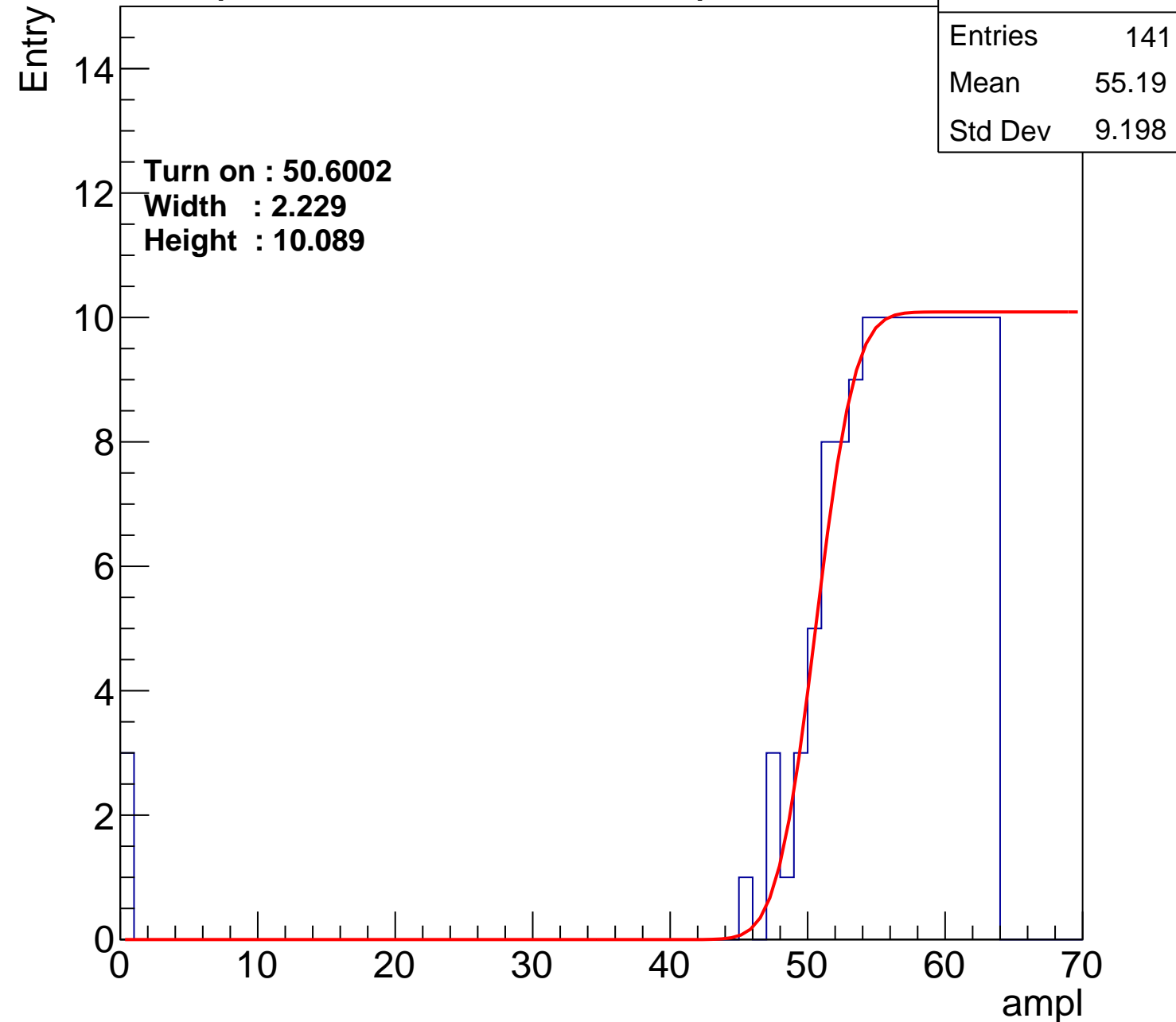
Width : 2.229

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch51

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.77
Std Dev	8.025

Turn on : 50.8658

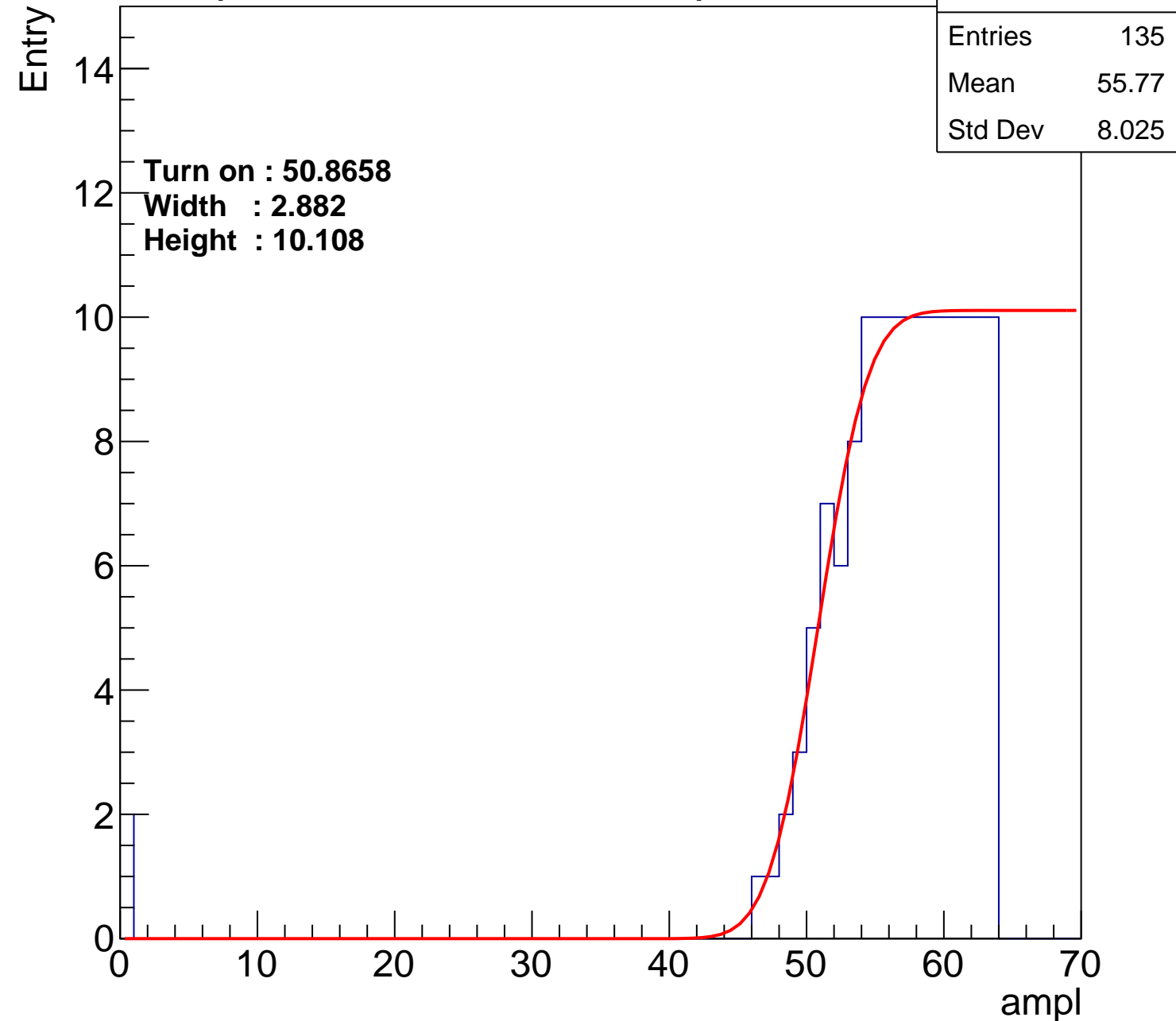
Width : 2.882

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch52

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	55.44
Std Dev	6.464

Turn on : 49.7581

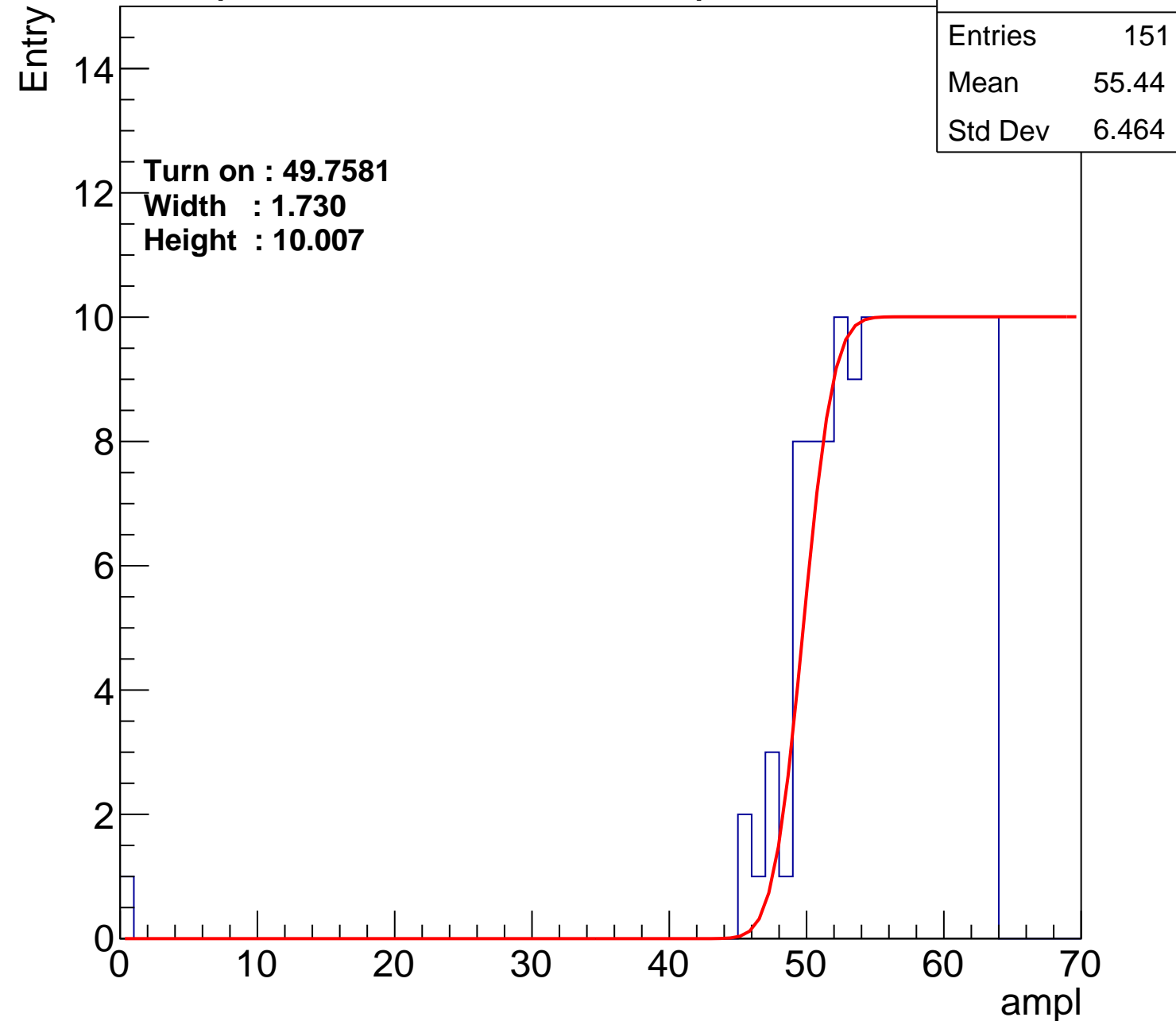
Width : 1.730

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch53

calib_packv5_040323_1717.root, FC#2, port C3

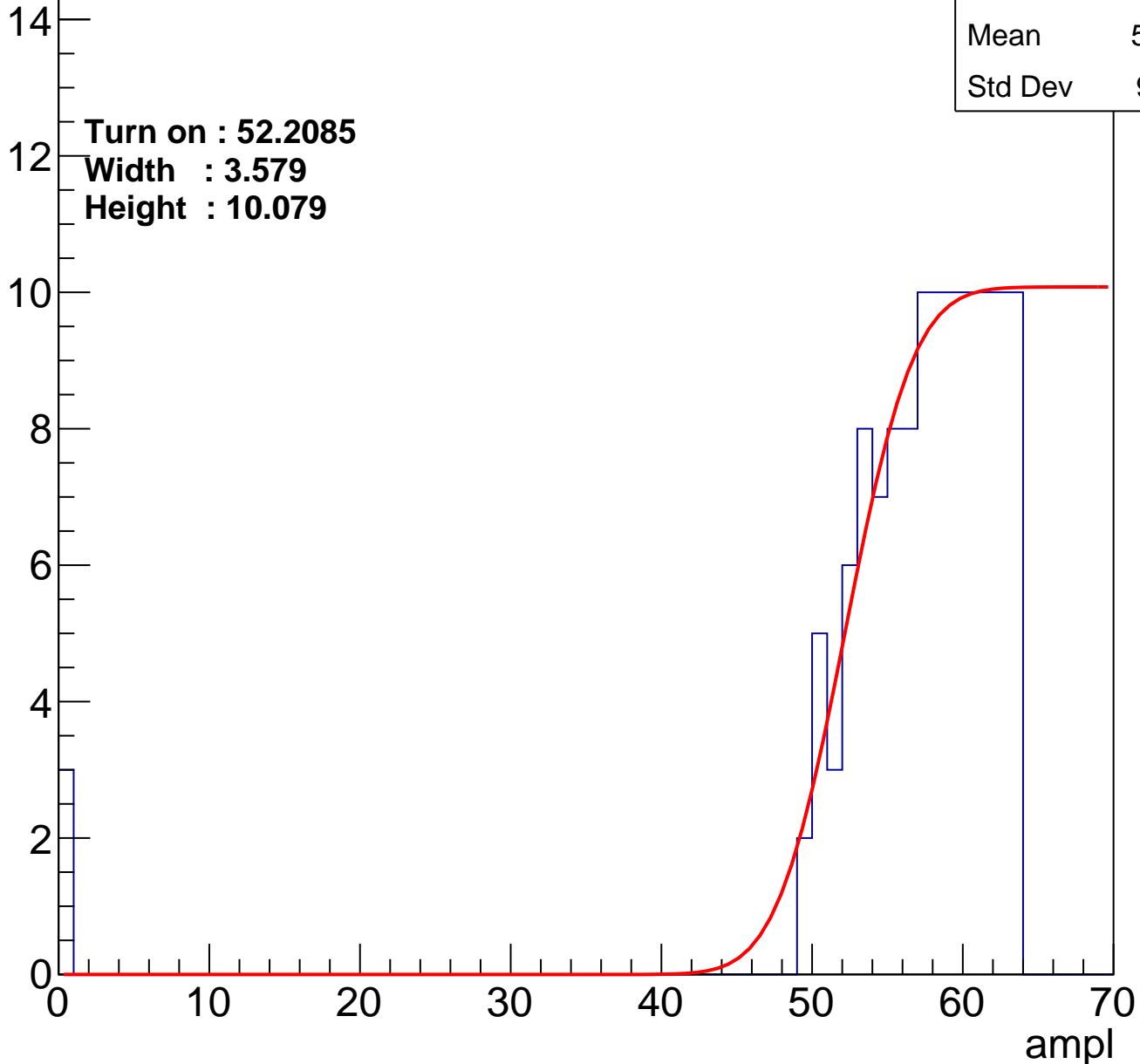
Entries	120
Mean	55.86
Std Dev	9.731

Turn on : 52.2085

Width : 3.579

Height : 10.079

Entry



B0L103S, U22-ch54

calib_packv5_040323_1717.root, FC#2, port C3

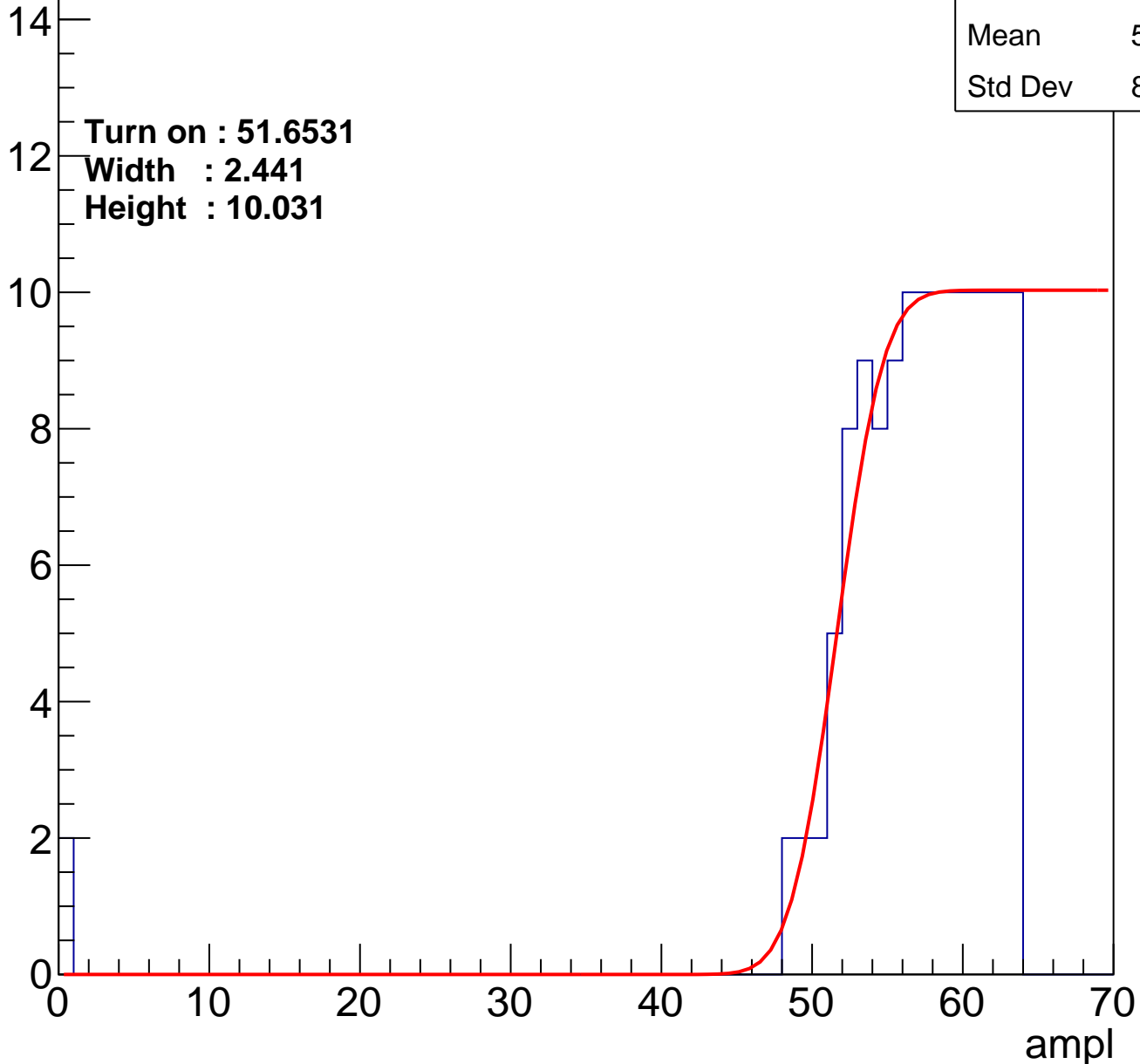
Entries	127
Mean	56.13
Std Dev	8.105

Turn on : 51.6531

Width : 2.441

Height : 10.031

Entry



B0L103S, U22-ch55

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	55.79
Std Dev	11.32

Turn on : 53.1244

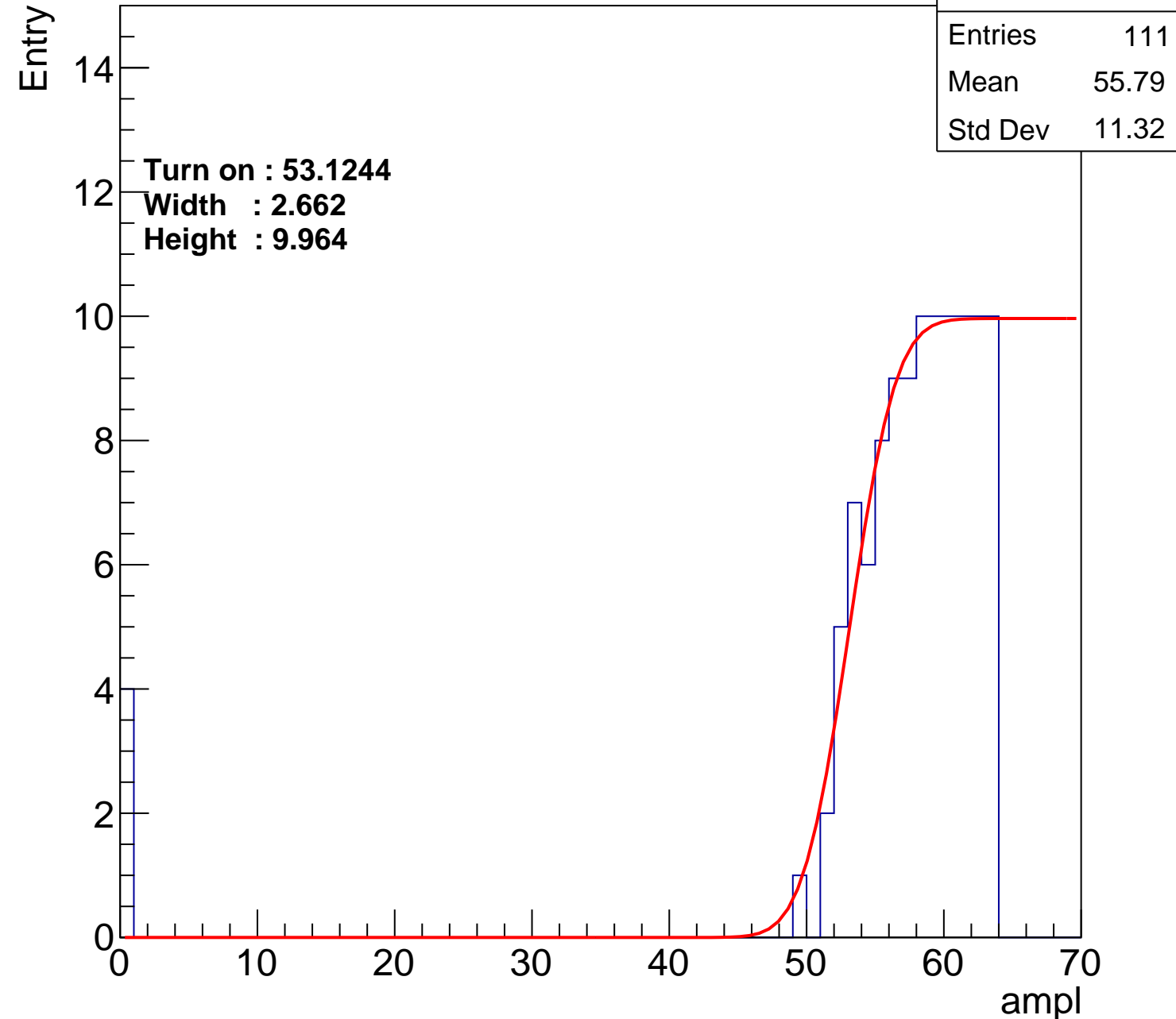
Width : 2.662

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_040323_1717.root, FC#2, port C3

Entries	157
Mean	54.57
Std Dev	8.938

Std Dev	8.938
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Height : 9.826



B0L103S, U22-ch57

calib_packv5_040323_1717.root, FC#2, port C3

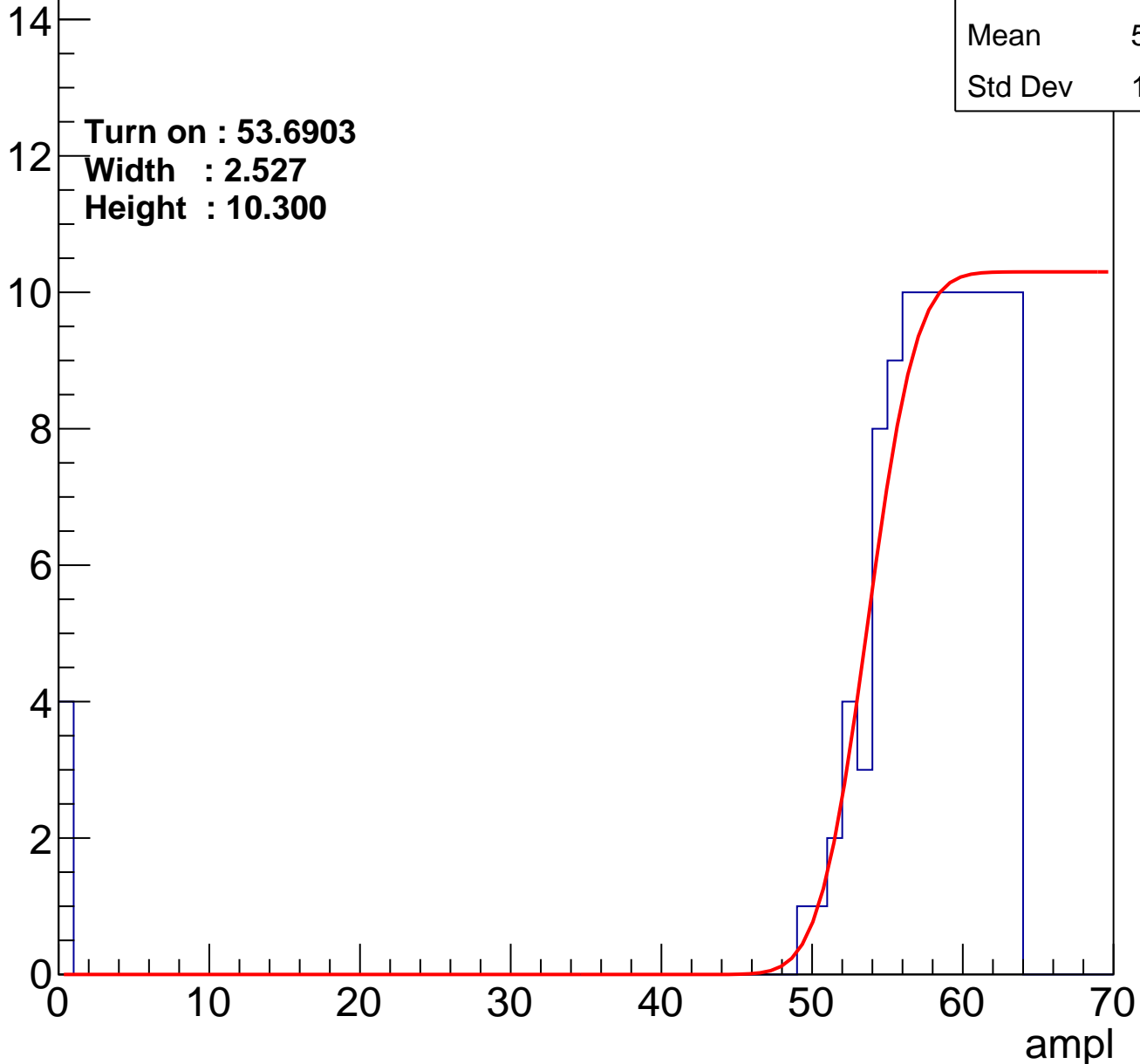
Entries	112
Mean	55.85
Std Dev	11.26

Turn on : 53.6903

Width : 2.527

Height : 10.300

Entry



B0L103S, U22-ch58

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	55.43
Std Dev	9.21

Turn on : 50.0308

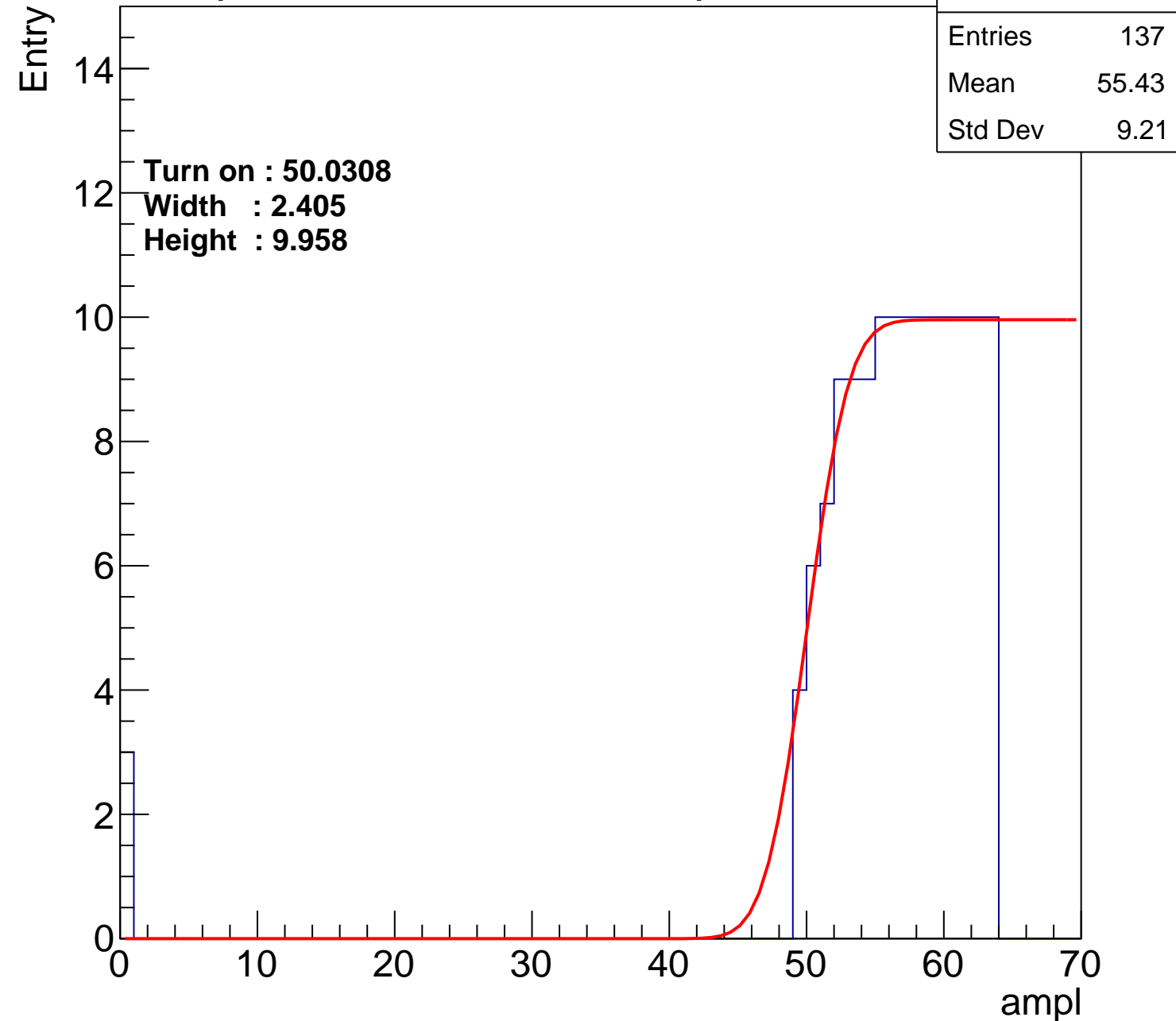
Width : 2.405

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch59

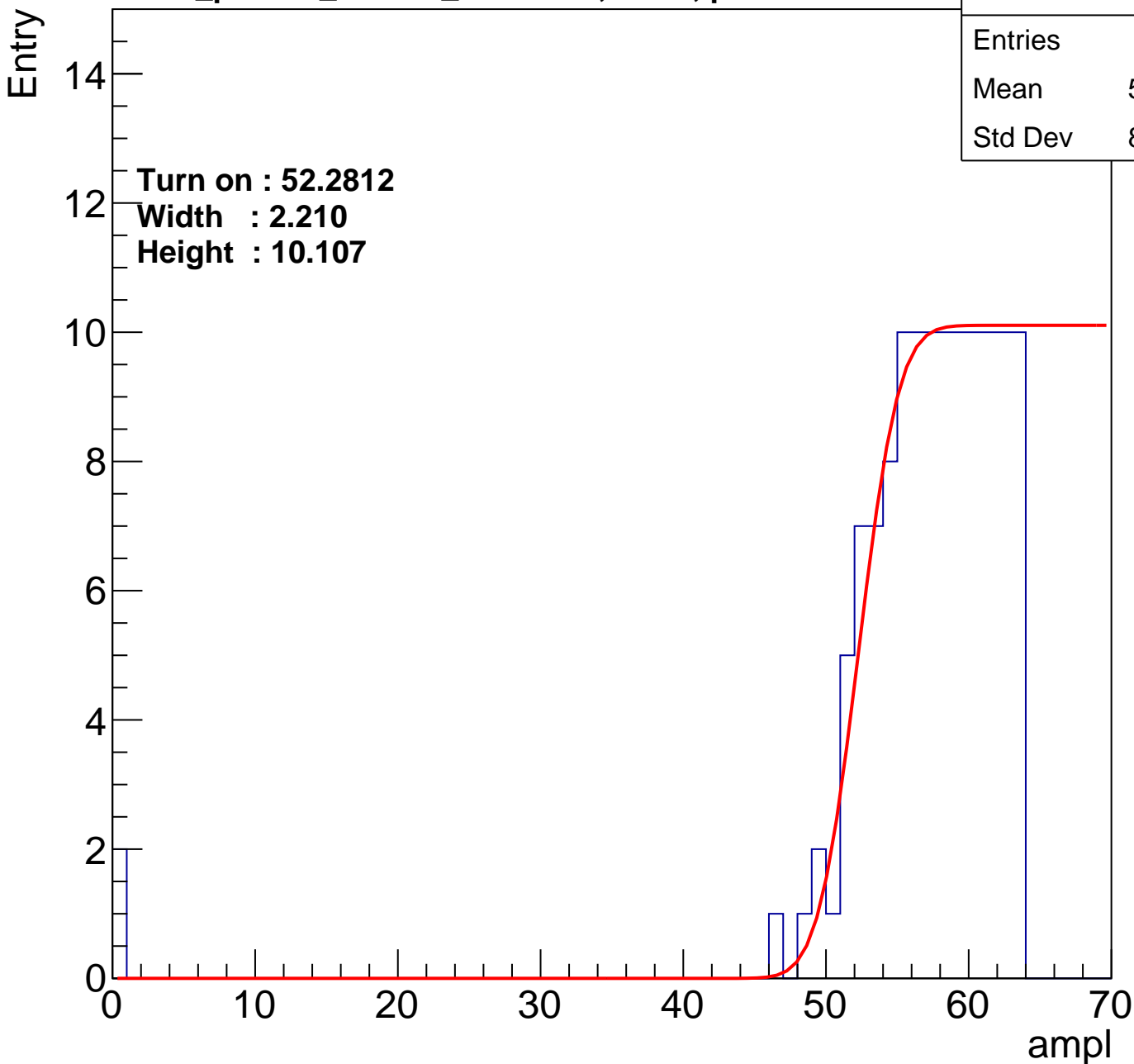
calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	56.24
Std Dev	8.184

Turn on : 52.2812

Width : 2.210

Height : 10.107



B0L103S, U22-ch60

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	56.24
Std Dev	6.456

Turn on : 51.0635

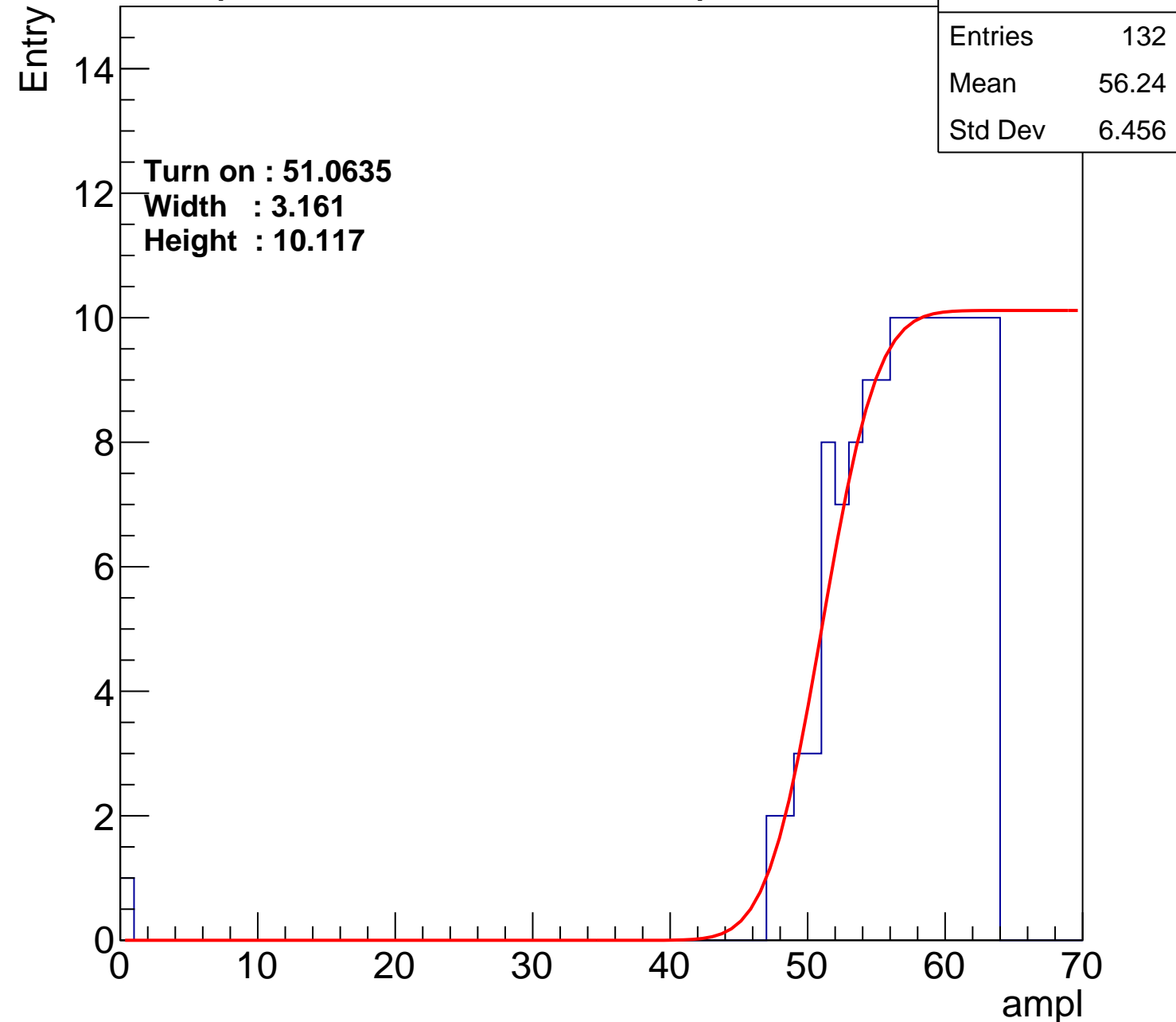
Width : 3.161

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch61

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	55.59
Std Dev	11.06

Turn on : 52.4738

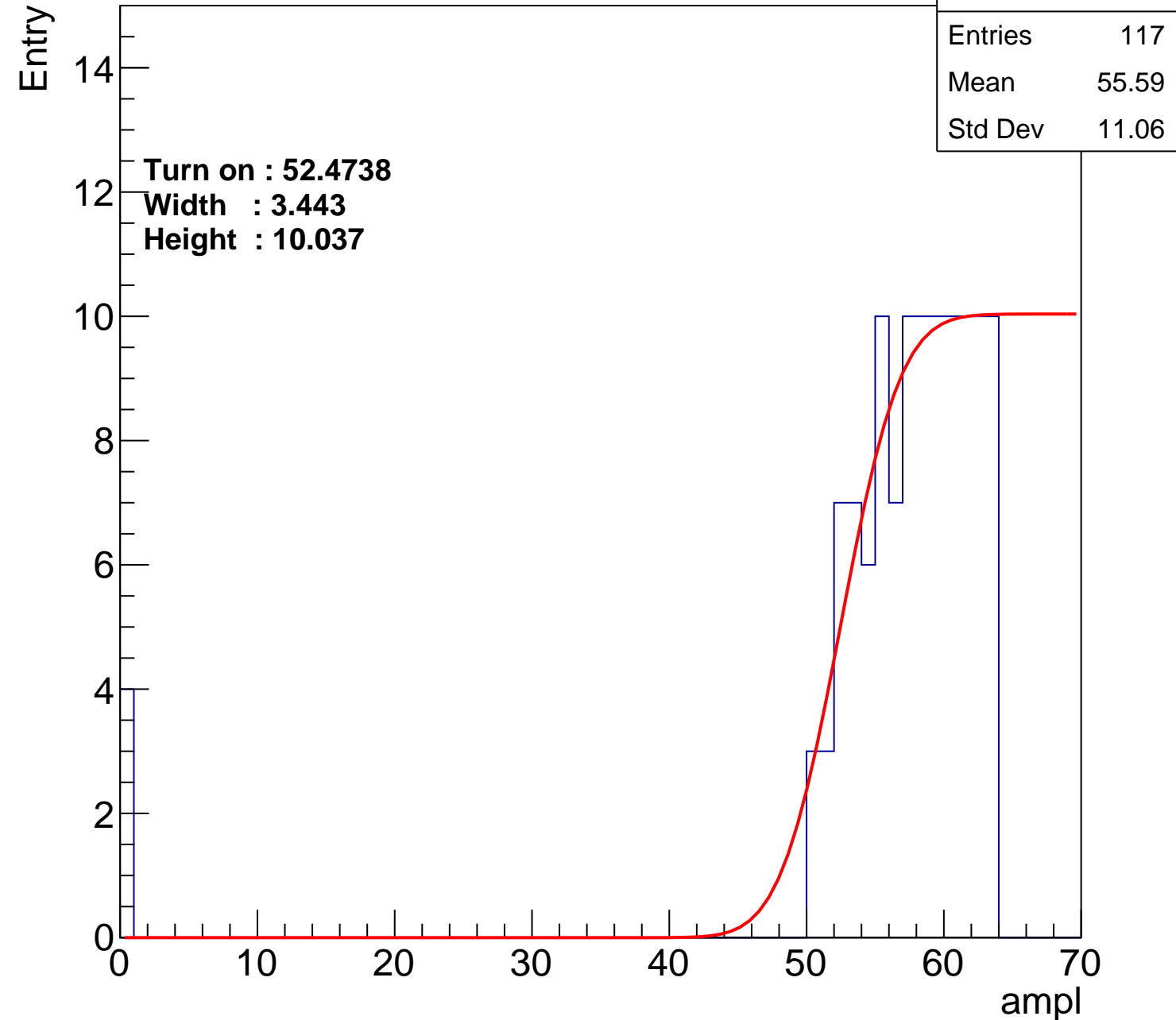
Width : 3.443

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch62

calib_packv5_040323_1717.root, FC#2, port C3

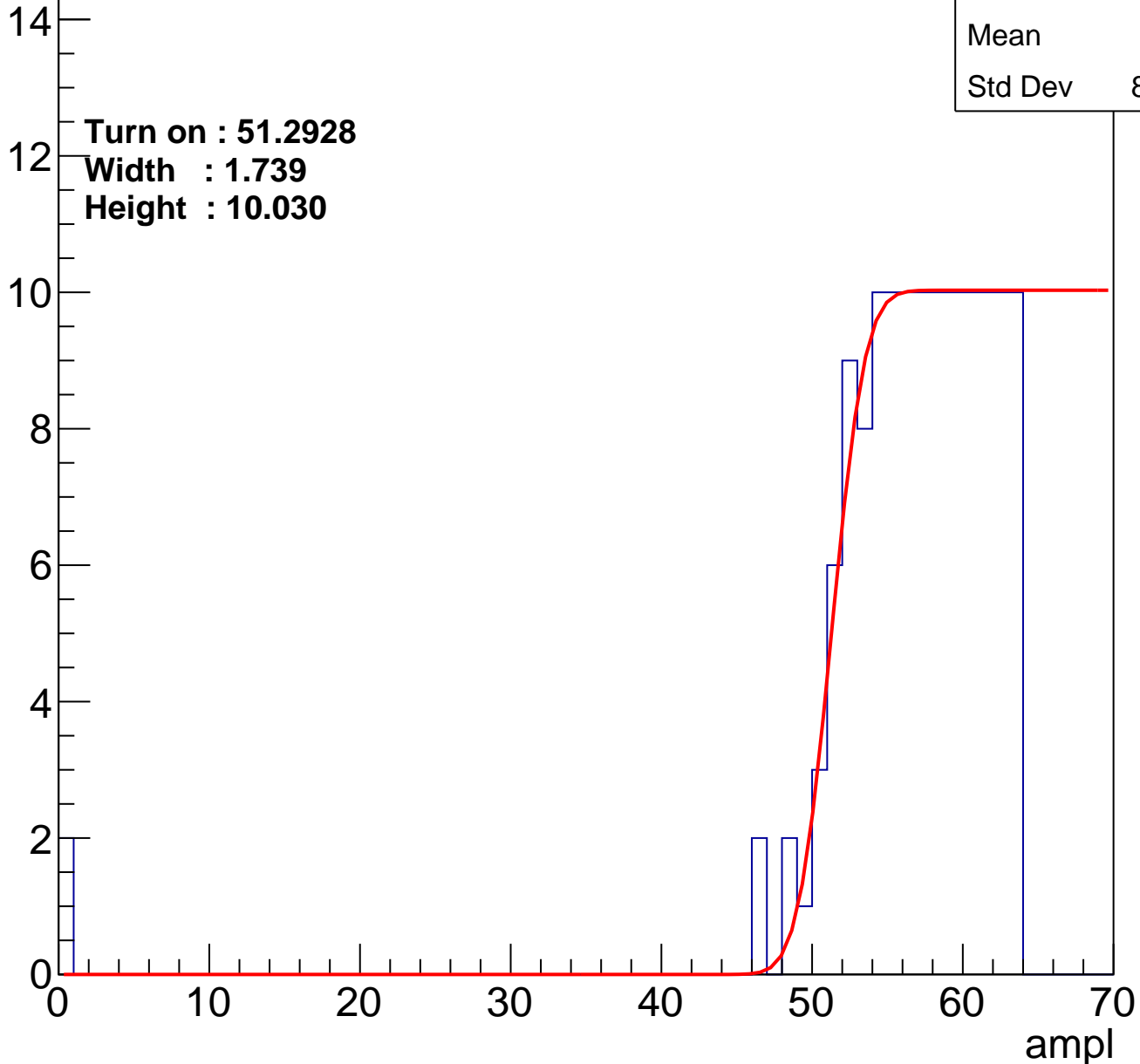
Entries	133
Mean	55.9
Std Dev	8.028

Turn on : 51.2928

Width : 1.739

Height : 10.030

Entry



B0L103S, U22-ch63

calib_packv5_040323_1717.root, FC#2, port C3

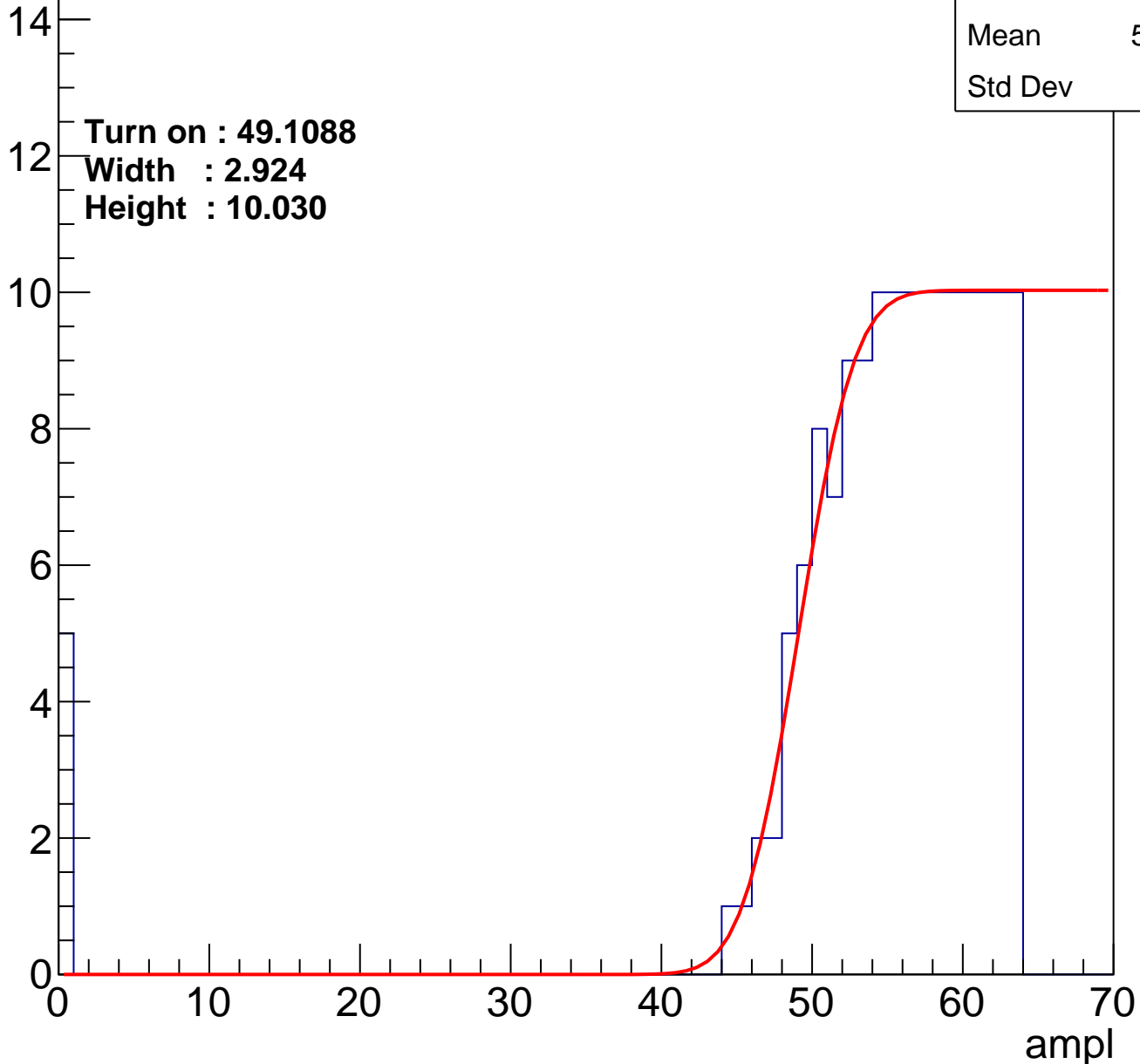
Entries	155
Mean	53.94
Std Dev	10.9

Turn on : 49.1088

Width : 2.924

Height : 10.030

Entry



B0L103S, U22-ch64

calib_packv5_040323_1717.root, FC#2, port C3

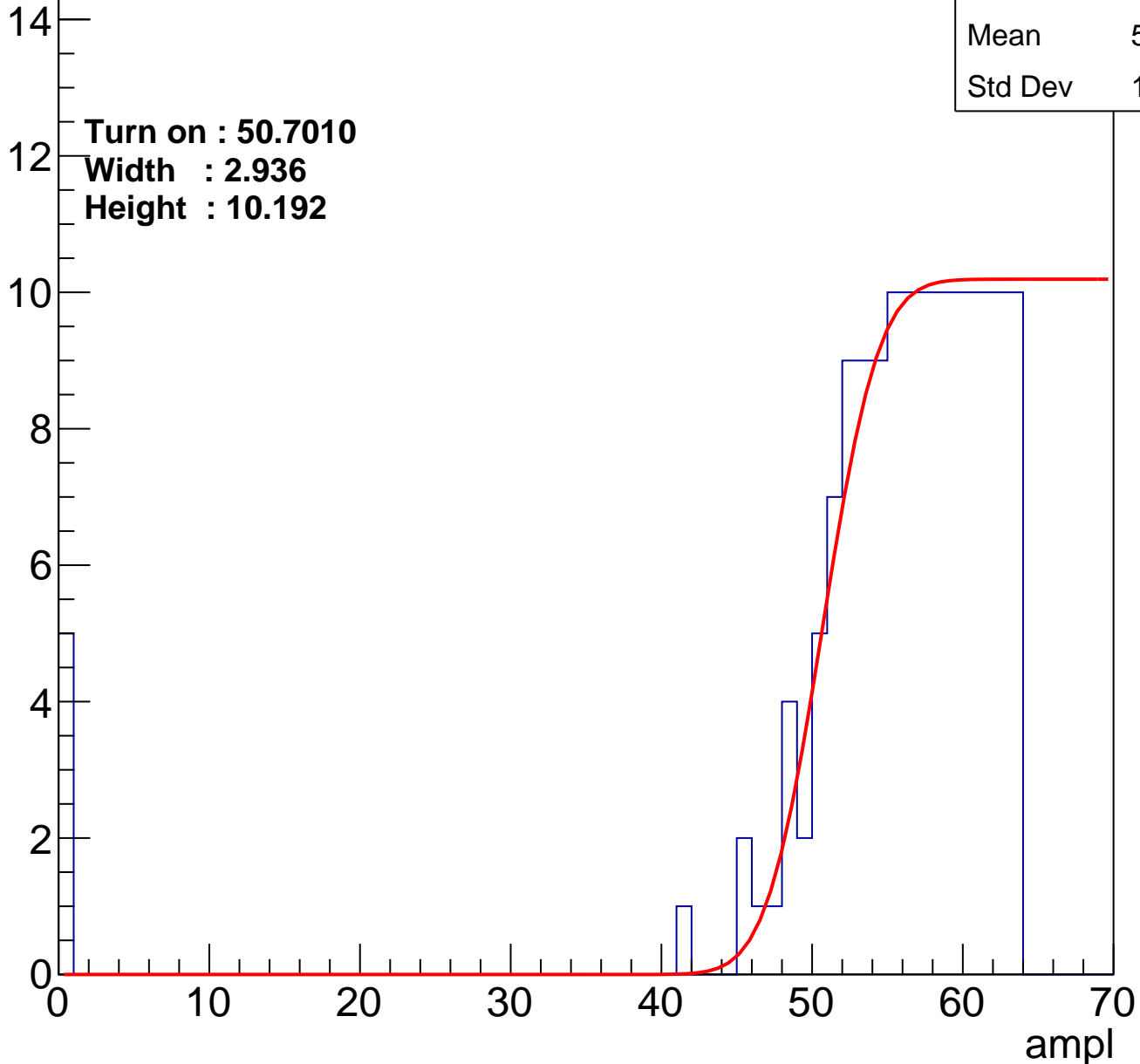
Entries	145
Mean	54.22
Std Dev	11.22

Turn on : 50.7010

Width : 2.936

Height : 10.192

Entry



B0L103S, U22-ch65

calib_packv5_040323_1717.root, FC#2, port C3

Entries	104
Mean	55.84
Std Dev	11.7

Turn on : 54.1635

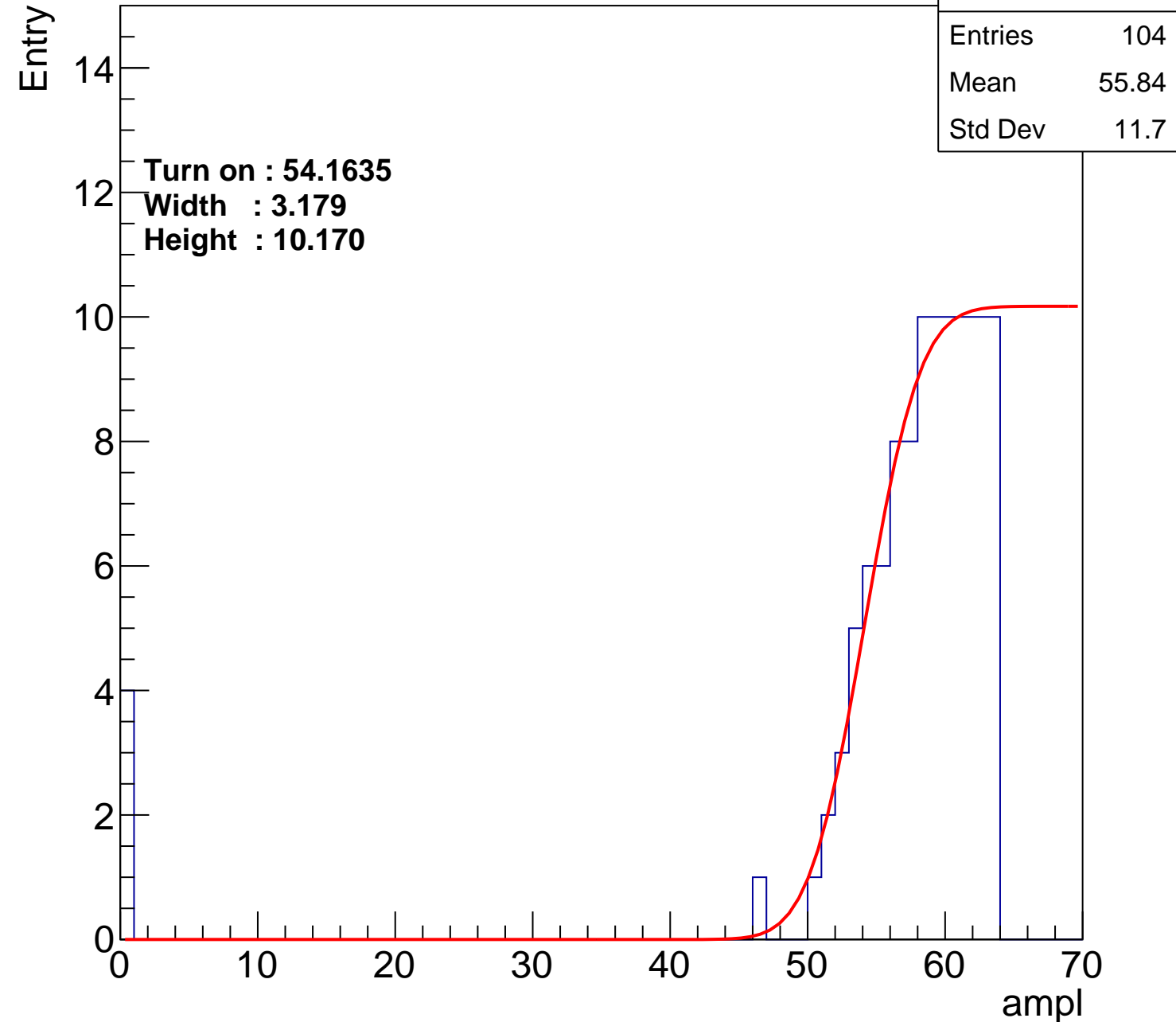
Width : 3.179

Height : 10.170

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch66

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	56.15
Std Dev	6.476

Turn on : 50.6472

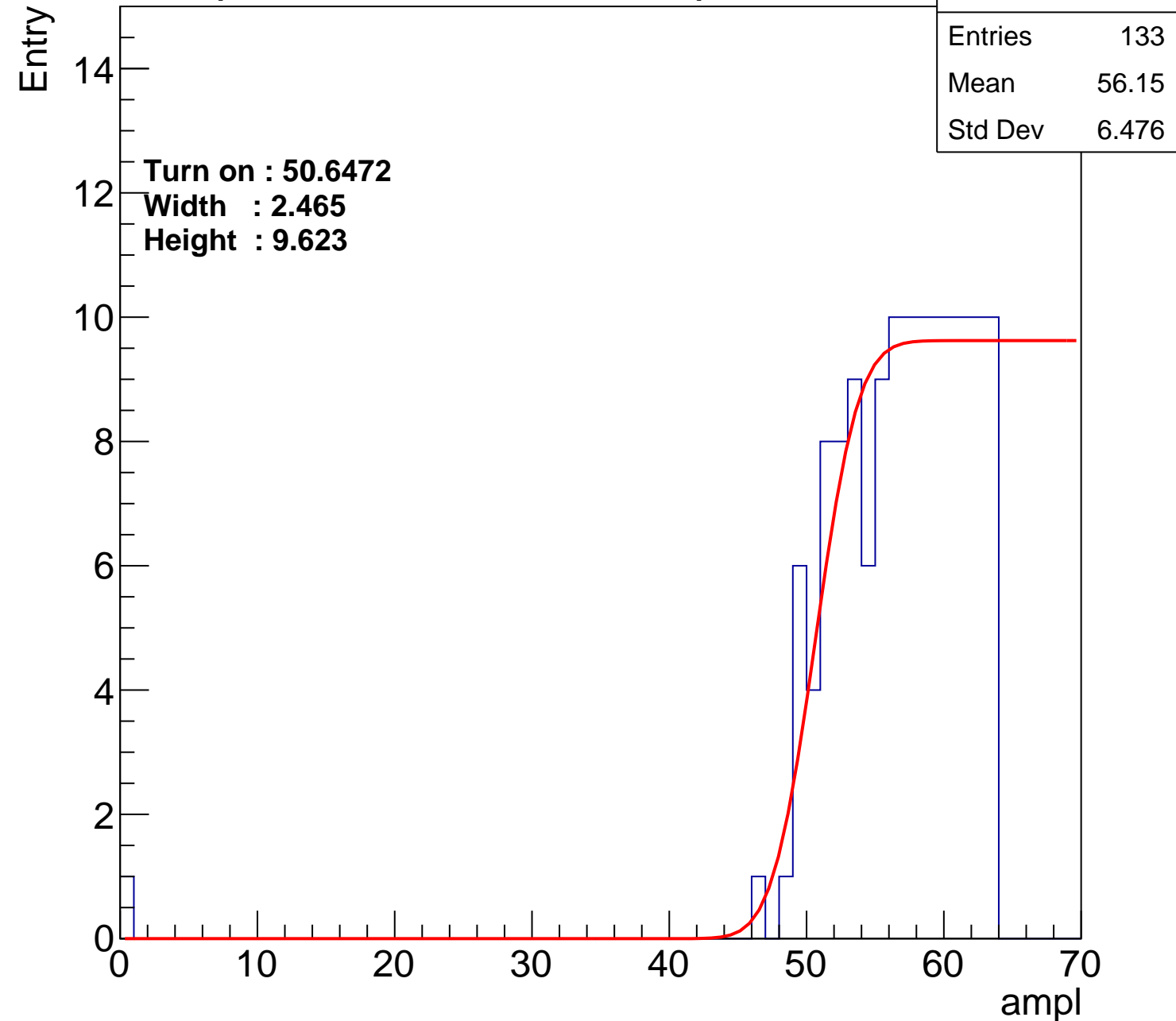
Width : 2.465

Height : 9.623

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch67

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	54.37
Std Dev	12.6

Turn on : 51.9704

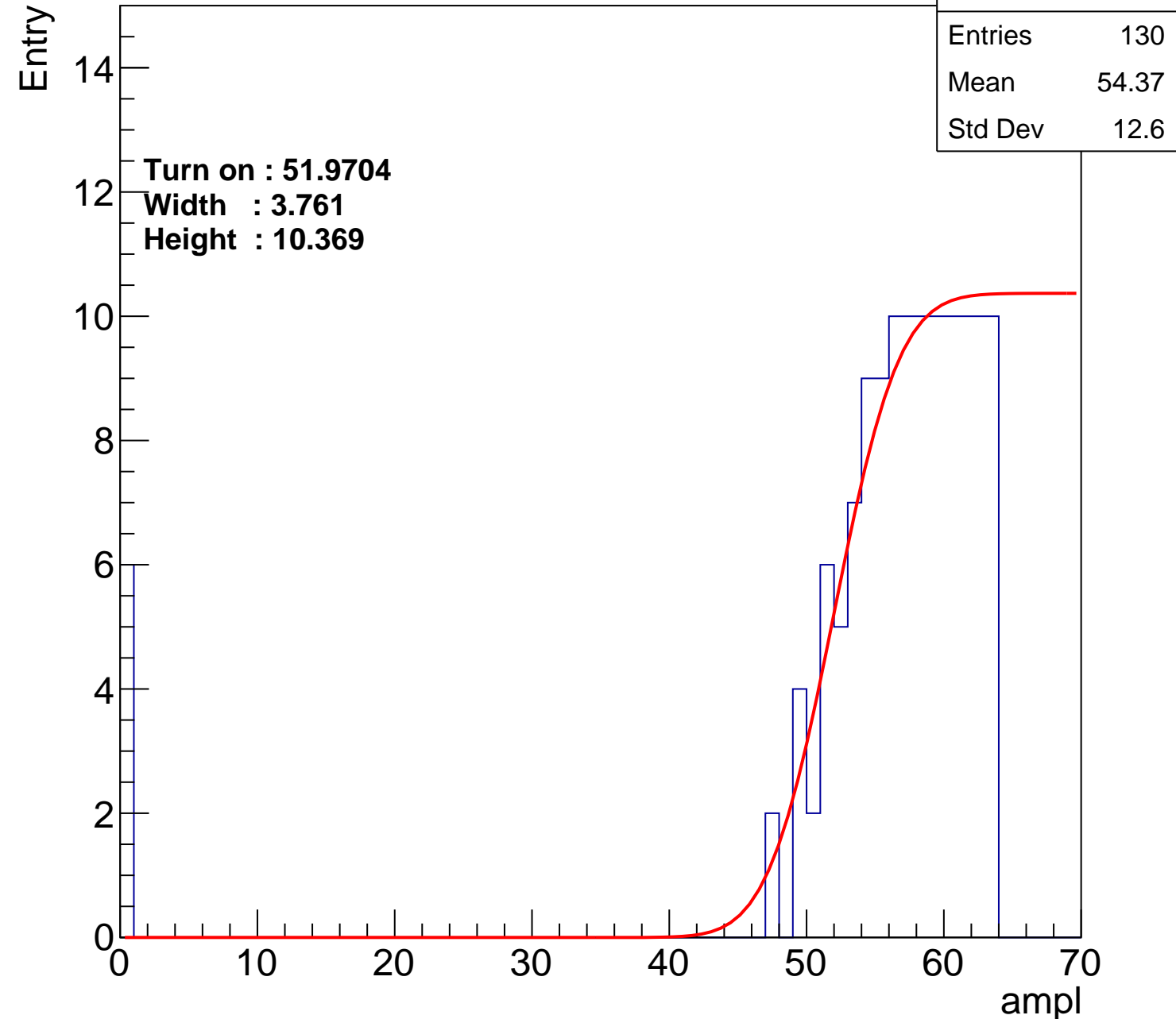
Width : 3.761

Height : 10.369

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch68

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	54.71
Std Dev	10.28

Turn on : 50.2427

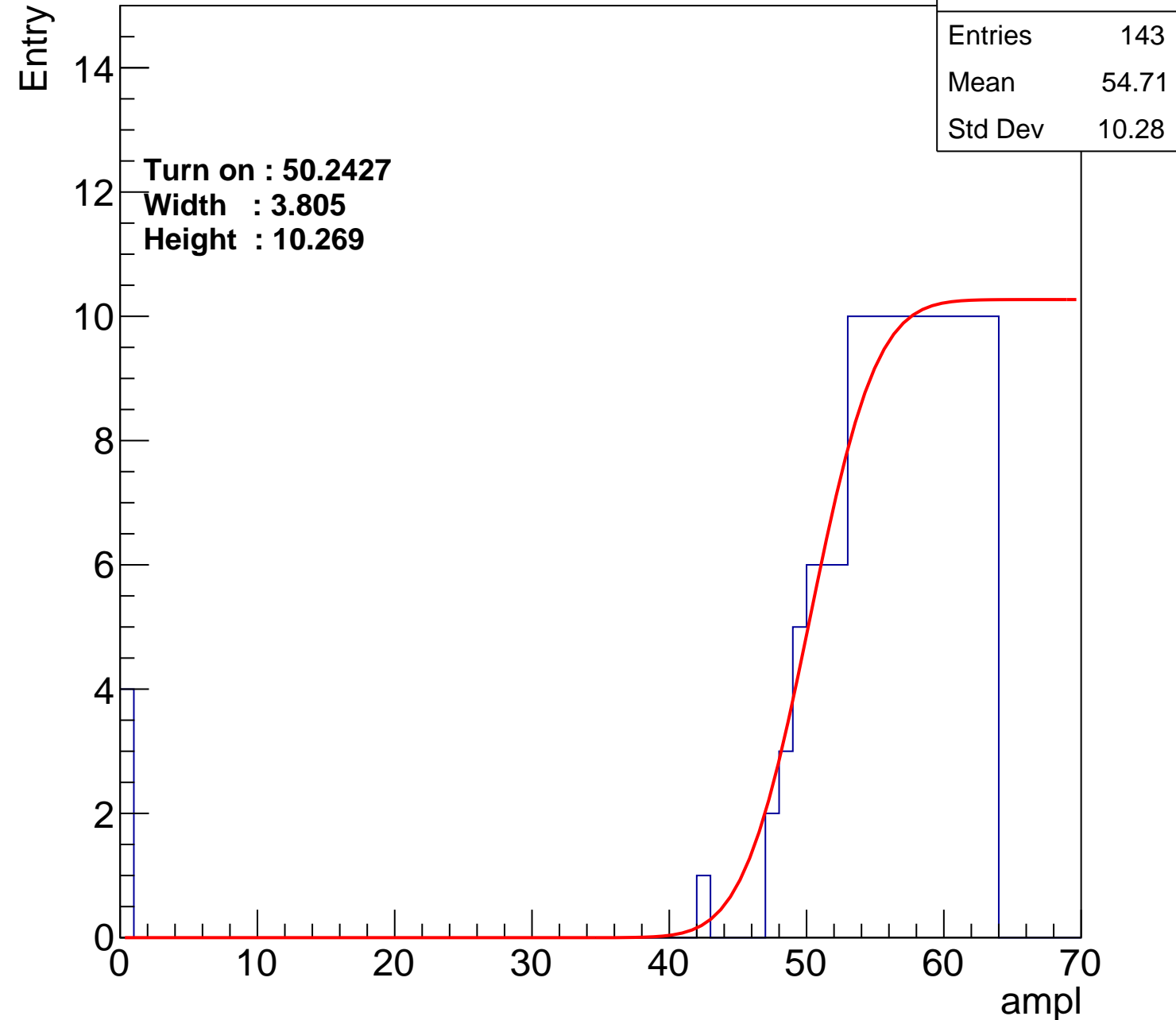
Width : 3.805

Height : 10.269

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch69

calib_packv5_040323_1717.root, FC#2, port C3

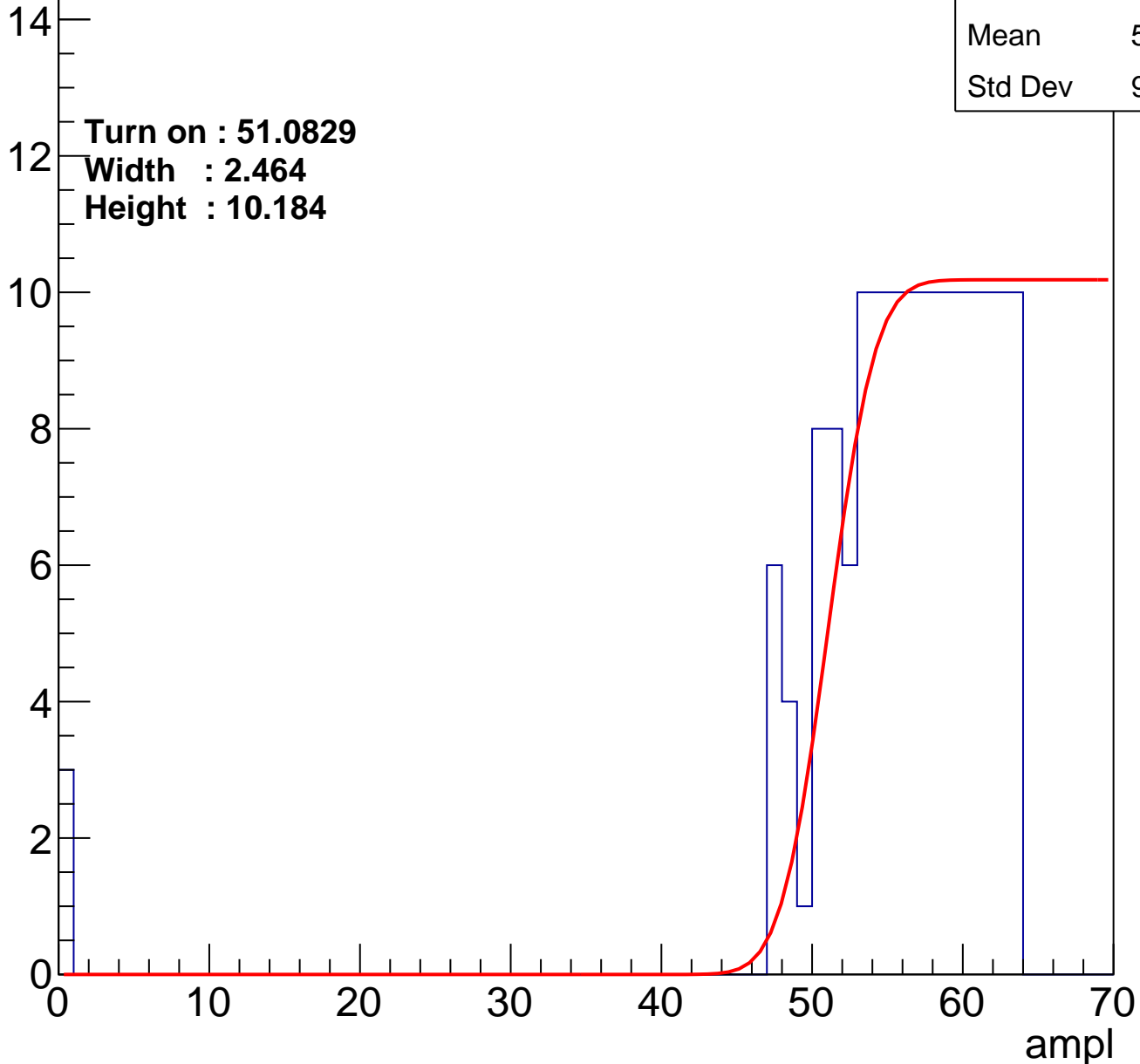
Entries	146
Mean	54.95
Std Dev	9.127

Turn on : 51.0829

Width : 2.464

Height : 10.184

Entry



B0L103S, U22-ch70

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.39
Std Dev	7.989

Turn on : 49.9853

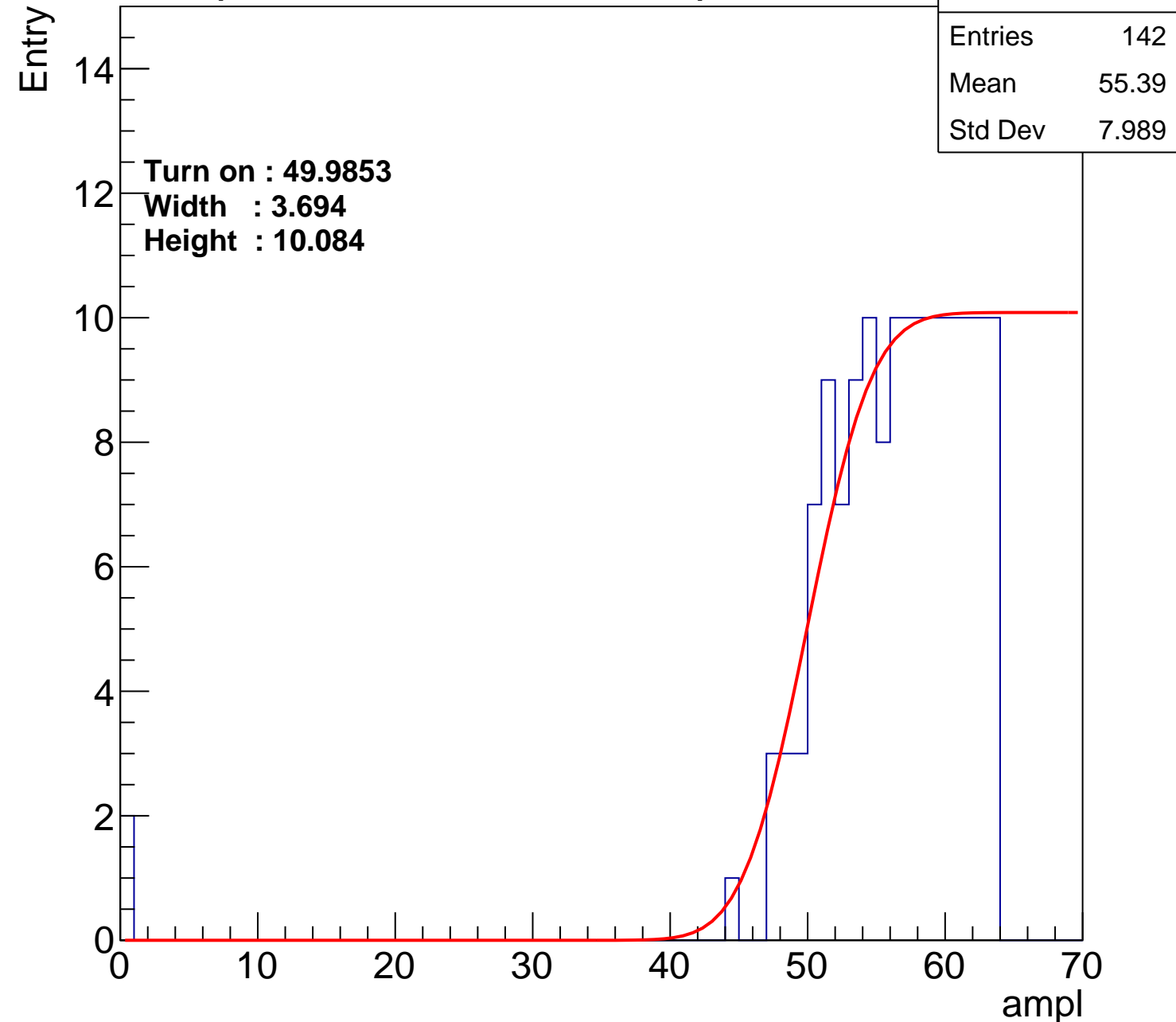
Width : 3.694

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch71

calib_packv5_040323_1717.root, FC#2, port C3

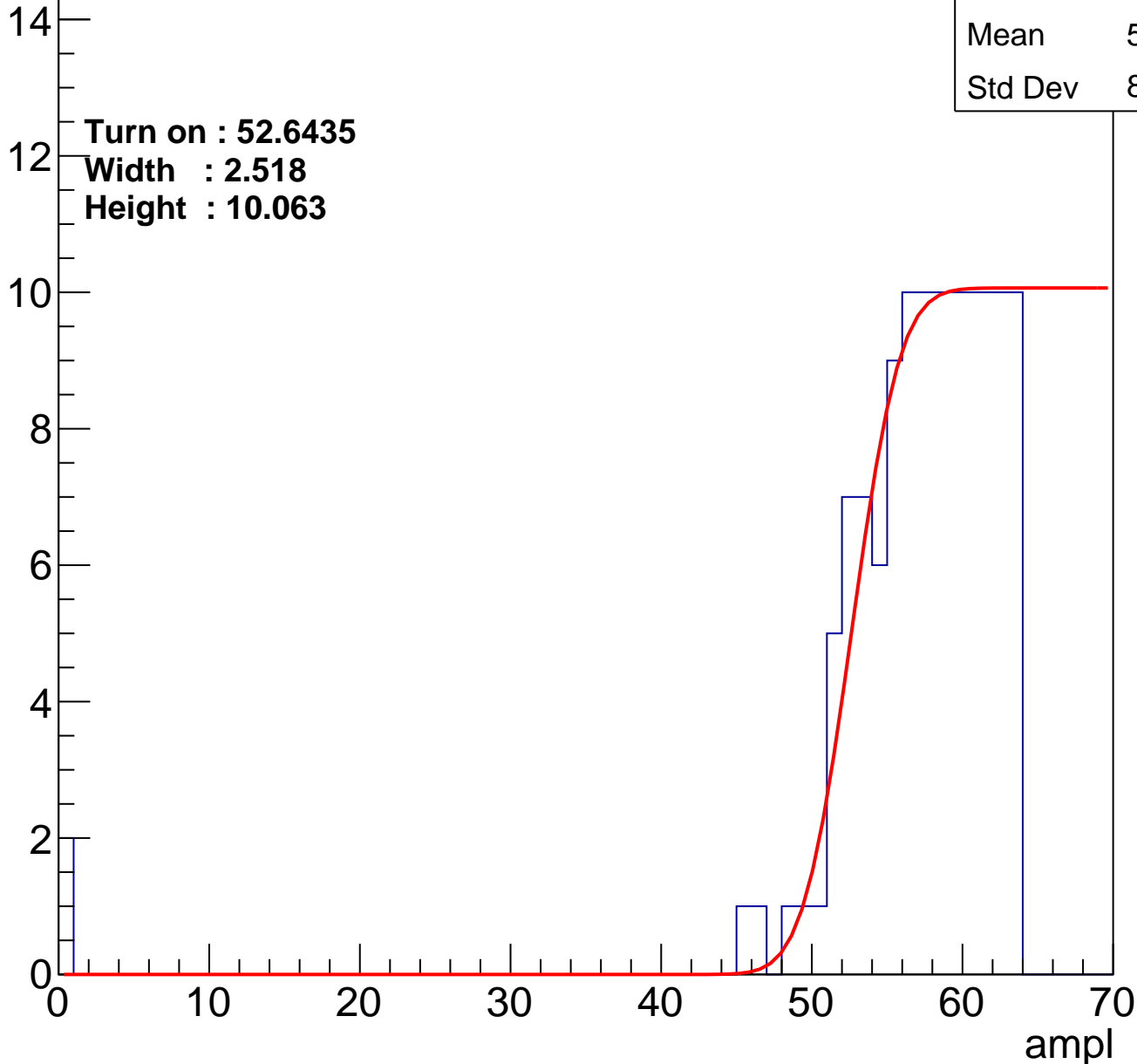
Entries	121
Mean	56.26
Std Dev	8.316

Turn on : 52.6435

Width : 2.518

Height : 10.063

Entry



B0L103S, U22-ch72

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.33
Std Dev	8.366

Turn on : 52.9433

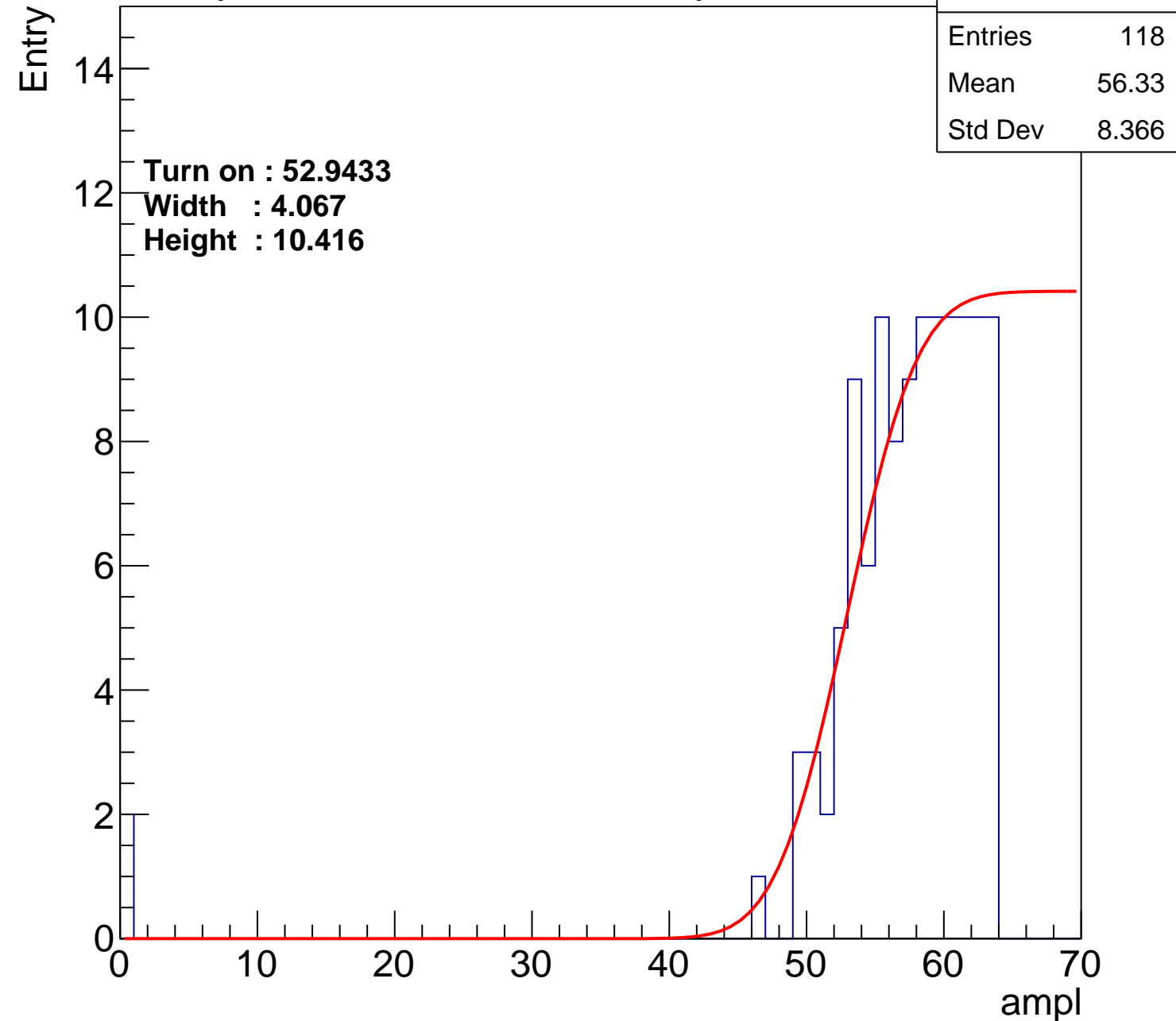
Width : 4.067

Height : 10.416

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch73

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.09
Std Dev	9.229

Turn on : 50.4954

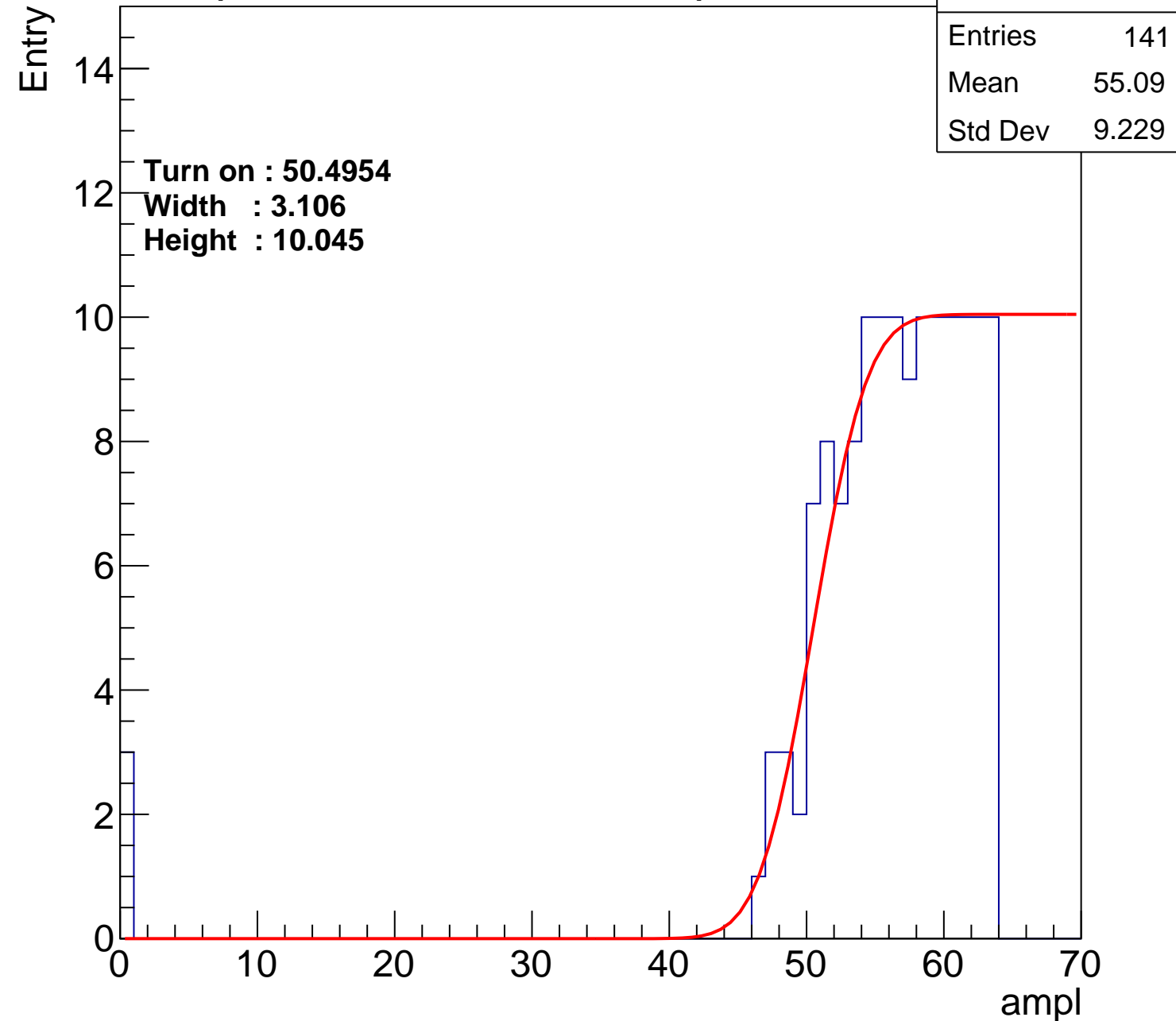
Width : 3.106

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch74

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
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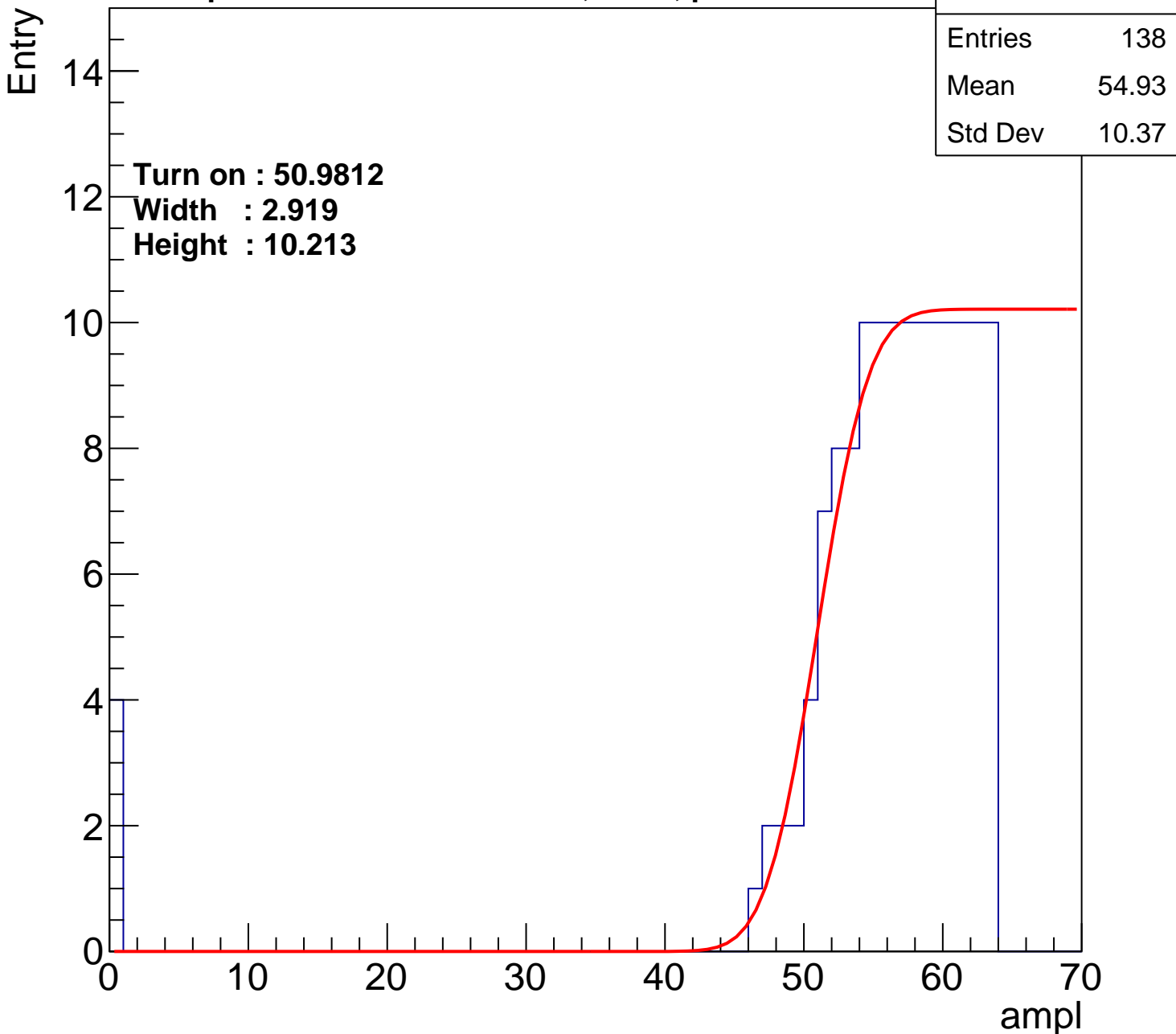
Mean	54.93
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Std Dev	10.37
---------	-------

Turn on : 50.9812

Width : 2.919

Height : 10.213



B0L103S, U22-ch75

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	54.29
Std Dev	11.1

Turn on : 49.5142

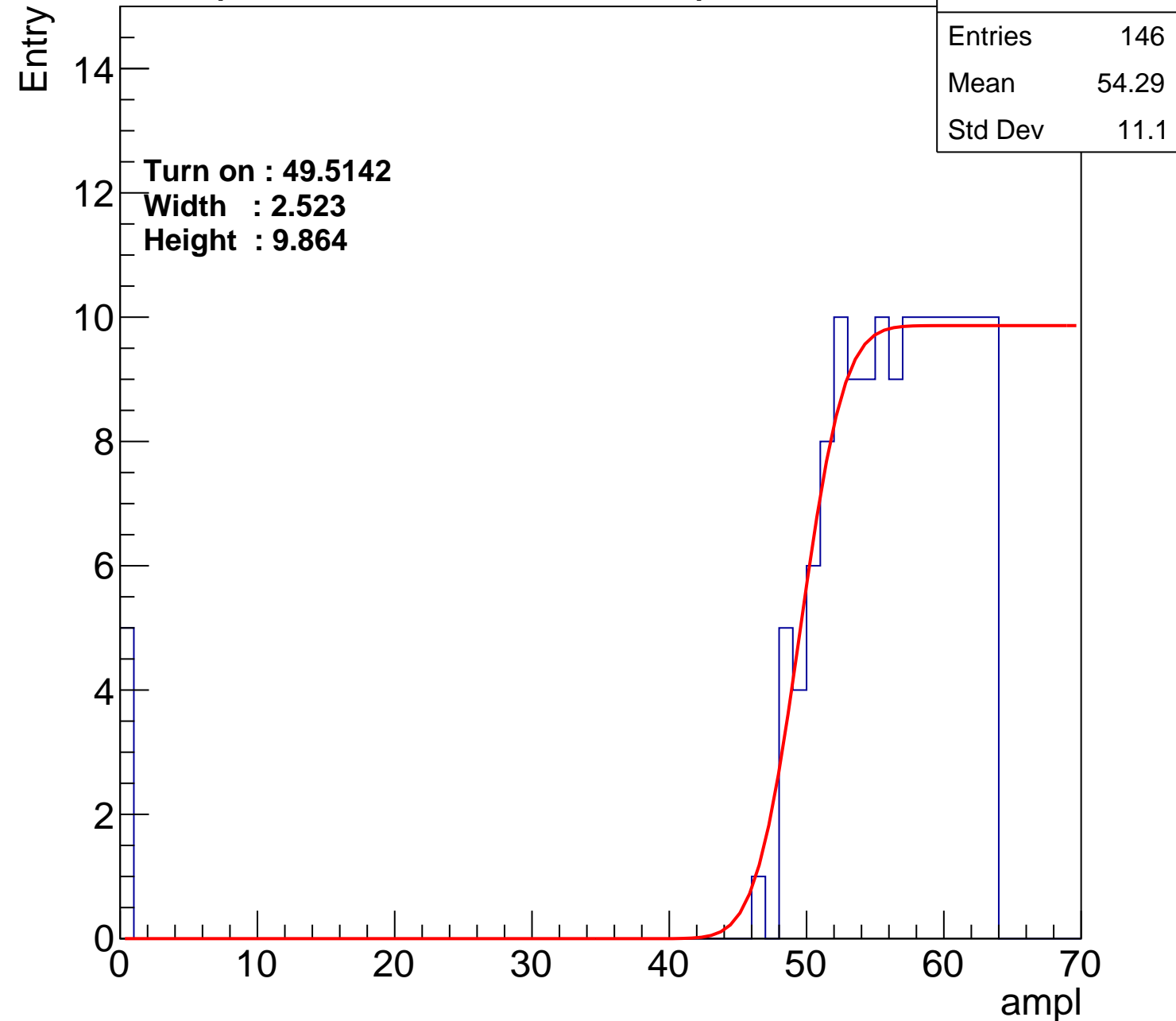
Width : 2.523

Height : 9.864

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch76

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	54.93
Std Dev	10.48

Turn on : 51.2726

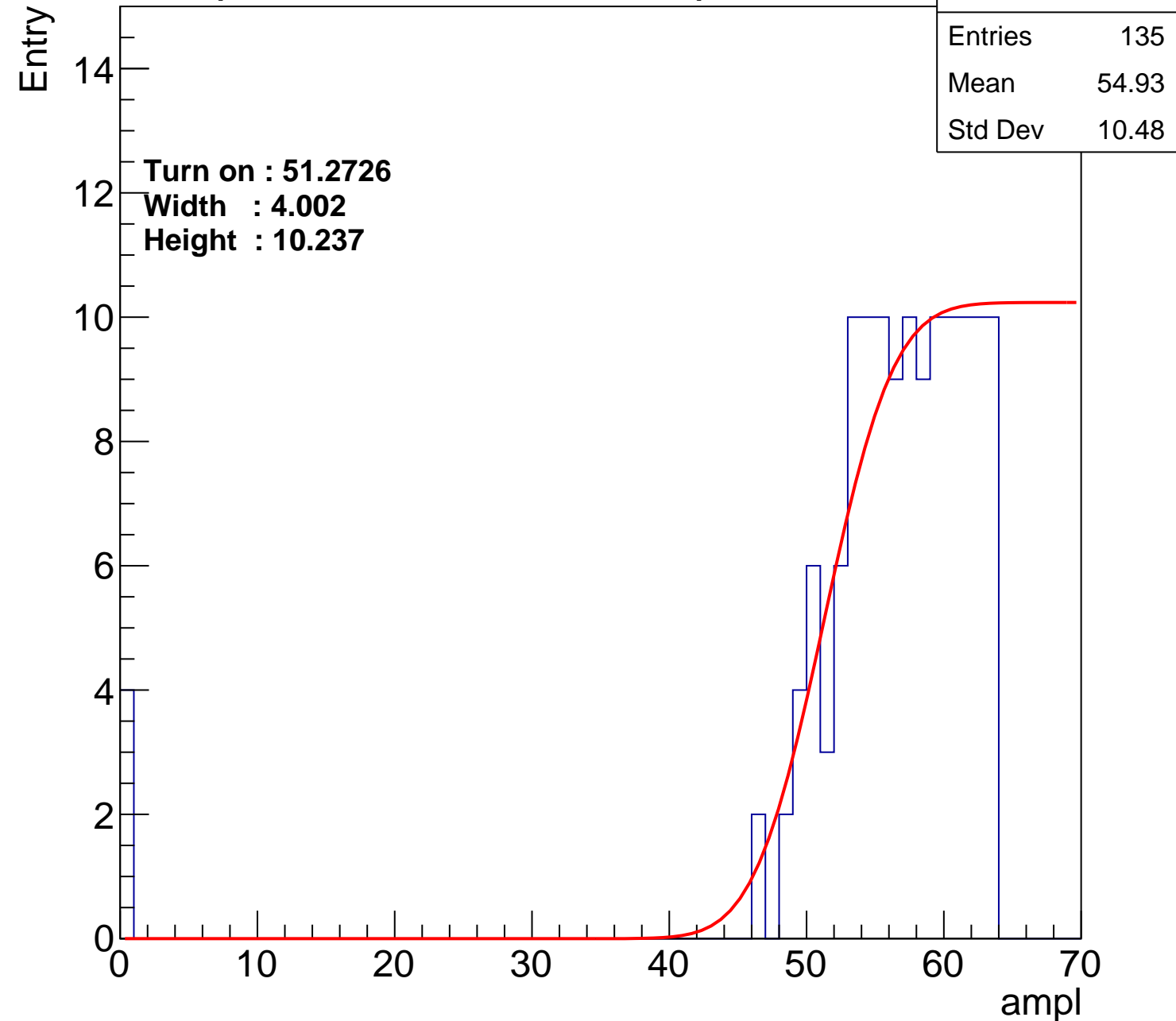
Width : 4.002

Height : 10.237

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch77

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.36
Std Dev	8.365

Turn on : 52.7498

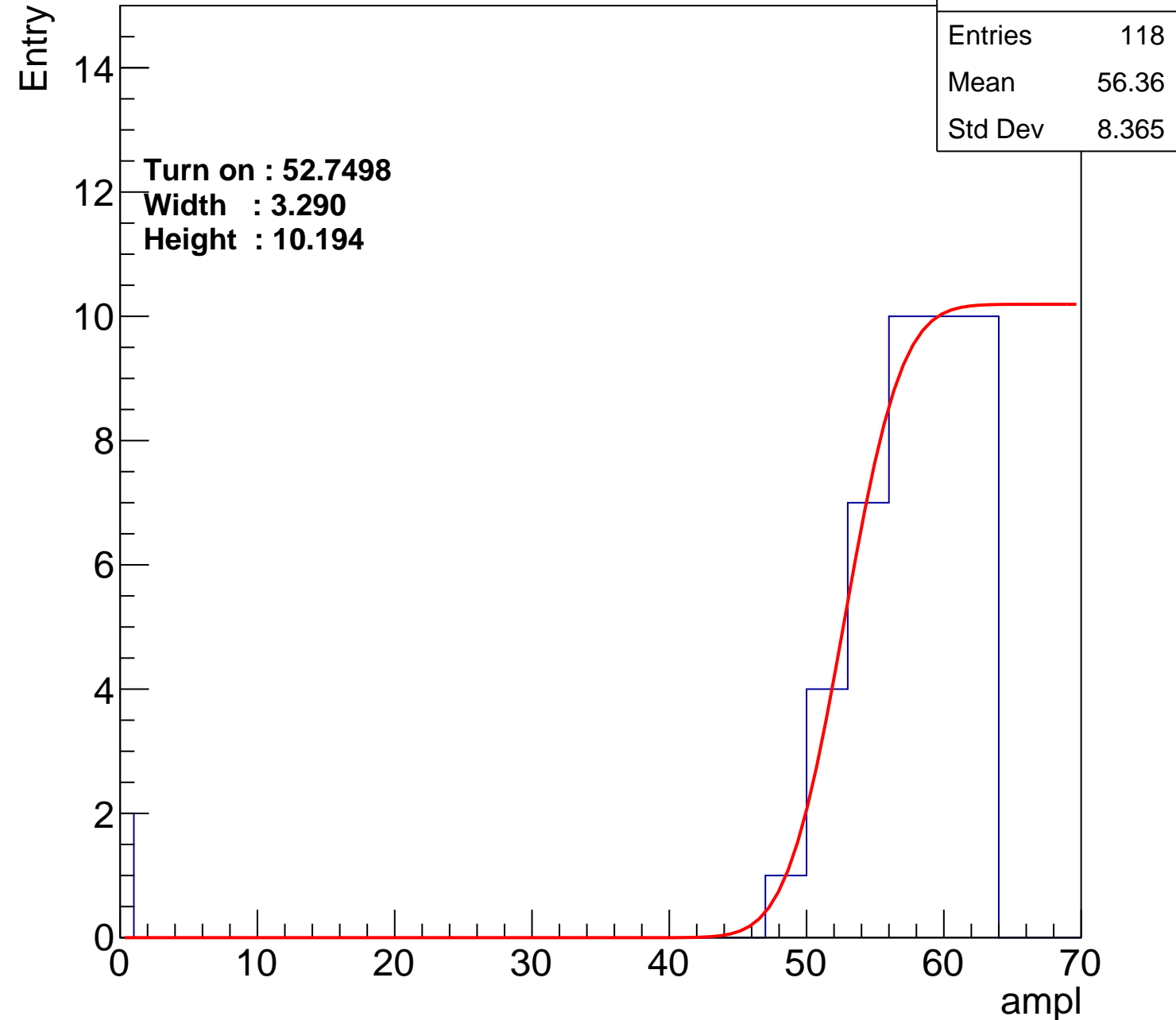
Width : 3.290

Height : 10.194

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch78

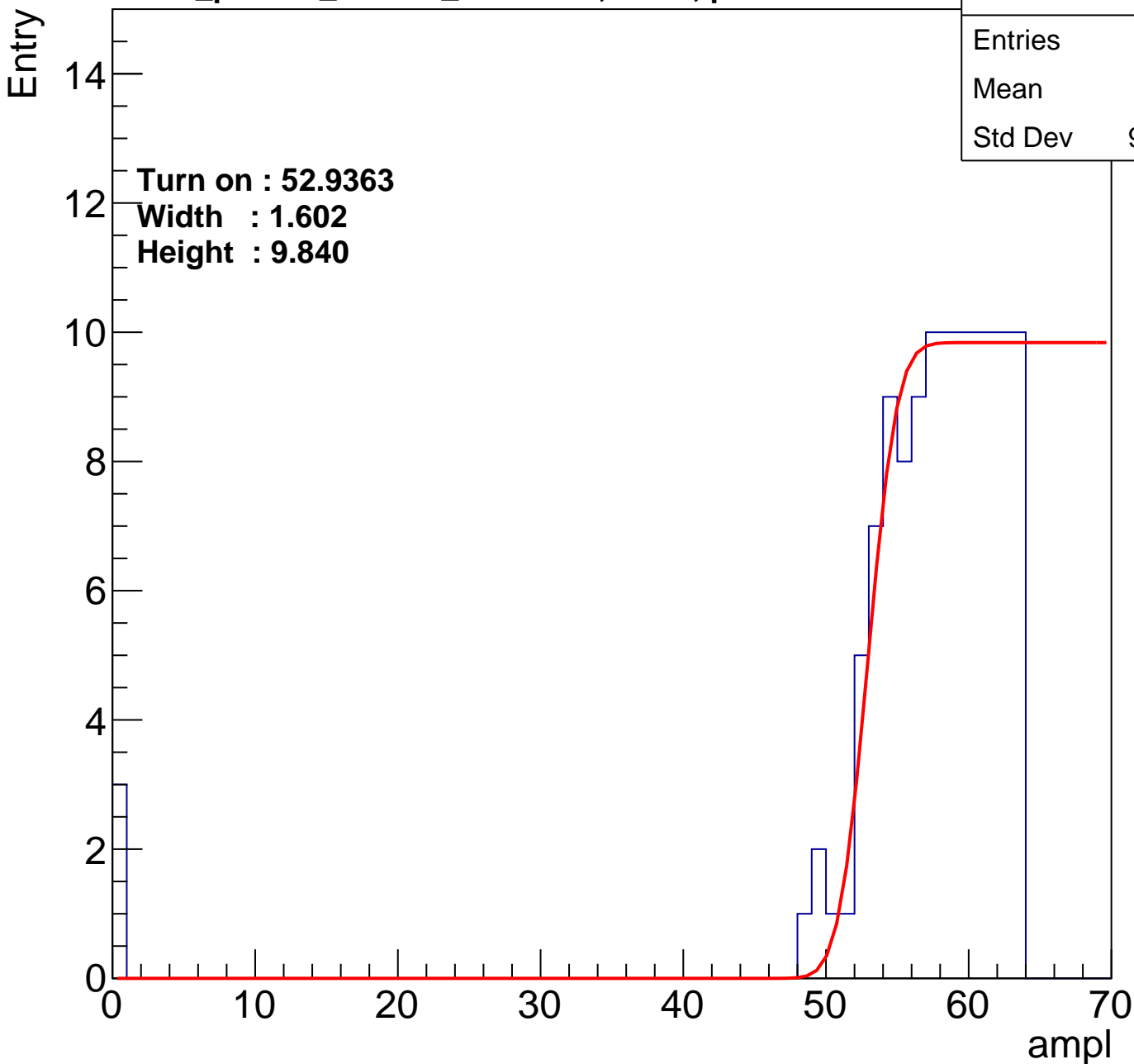
calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.1
Std Dev	9.833

Turn on : 52.9363

Width : 1.602

Height : 9.840



B0L103S, U22-ch79

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.18
Std Dev	8.174

Turn on : 52.1297

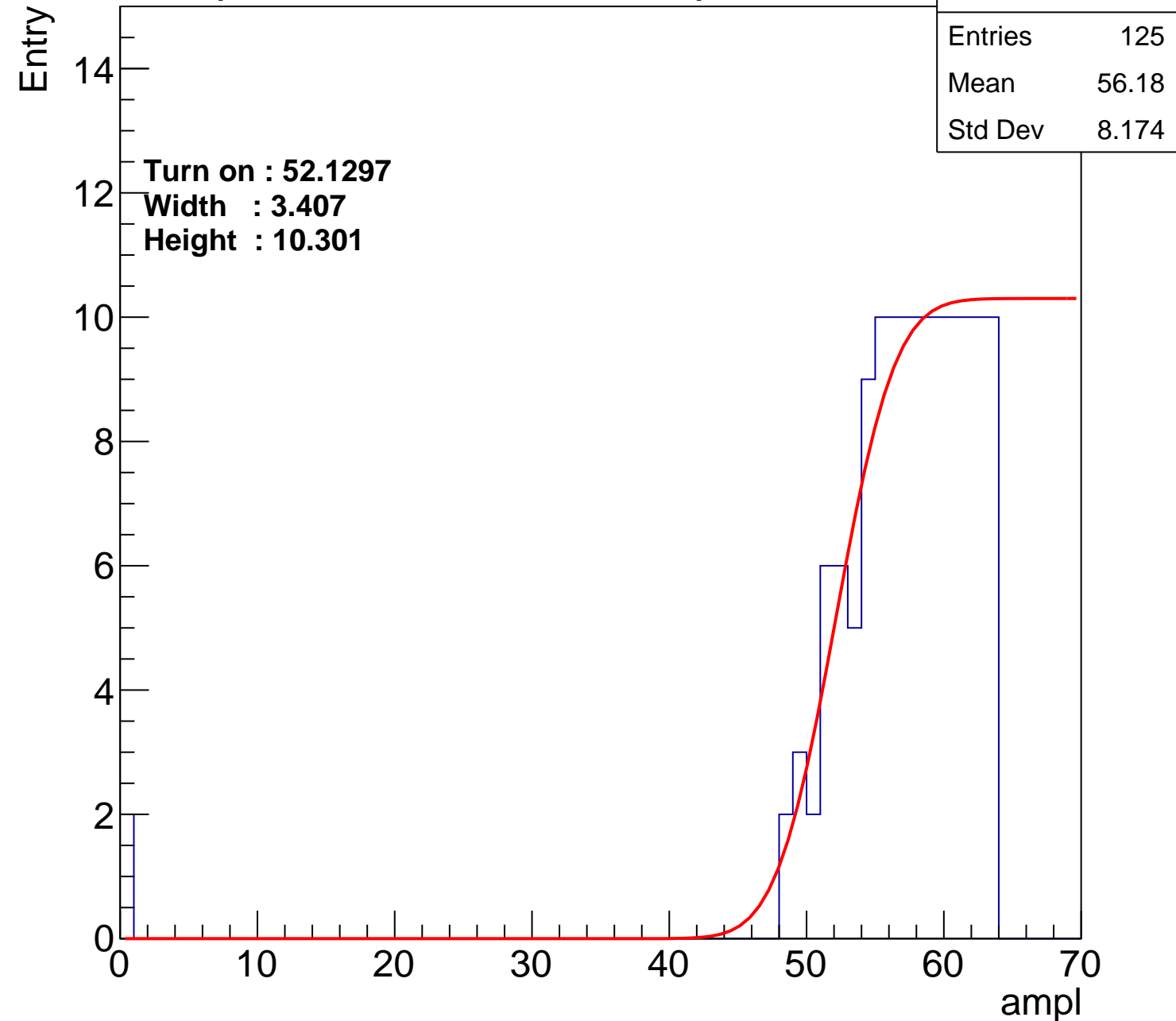
Width : 3.407

Height : 10.301

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch80

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.2
Std Dev	9.139

Turn on : 50.2102

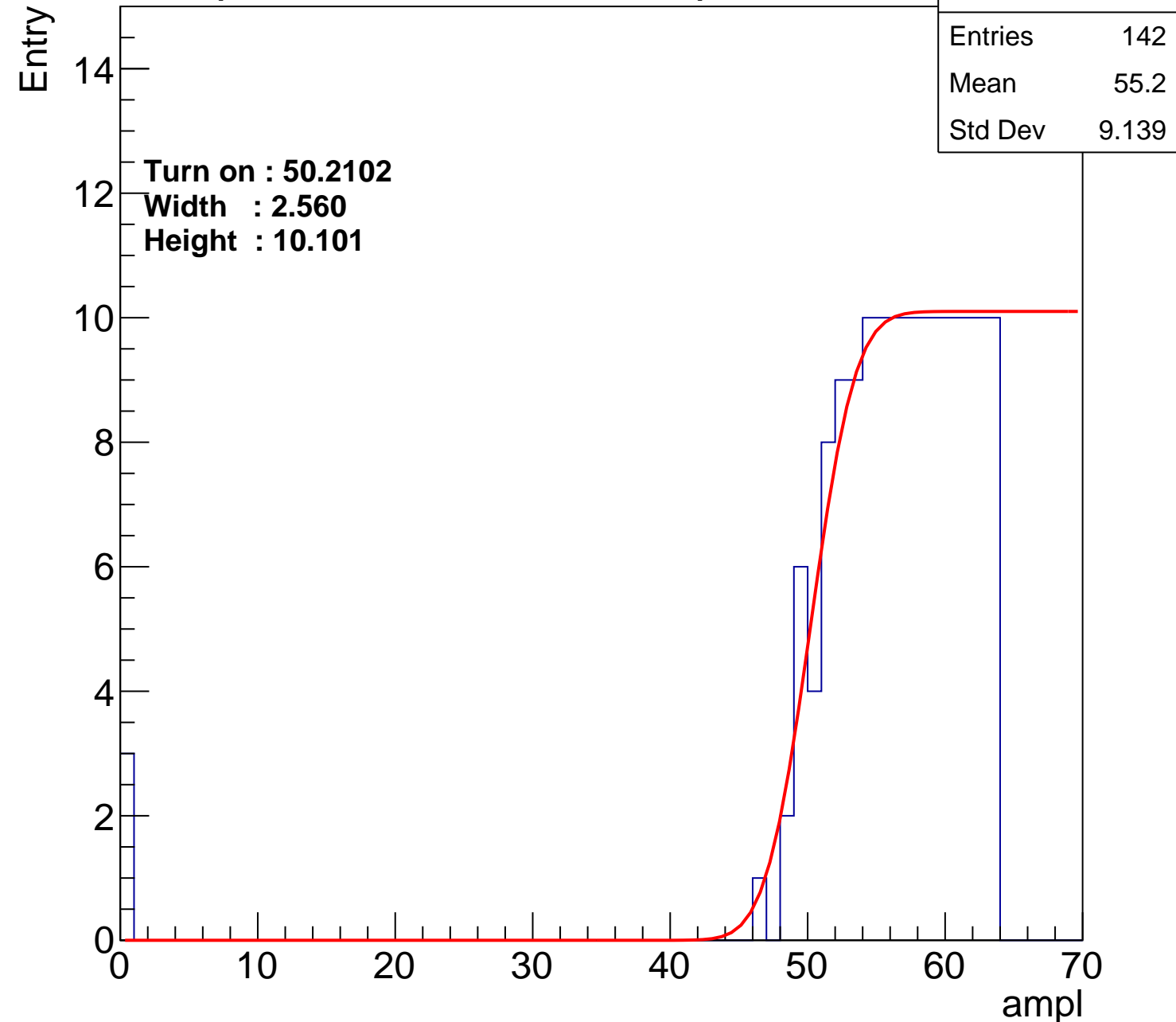
Width : 2.560

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch81

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.94
Std Dev	8.065

Turn on : 51.2158

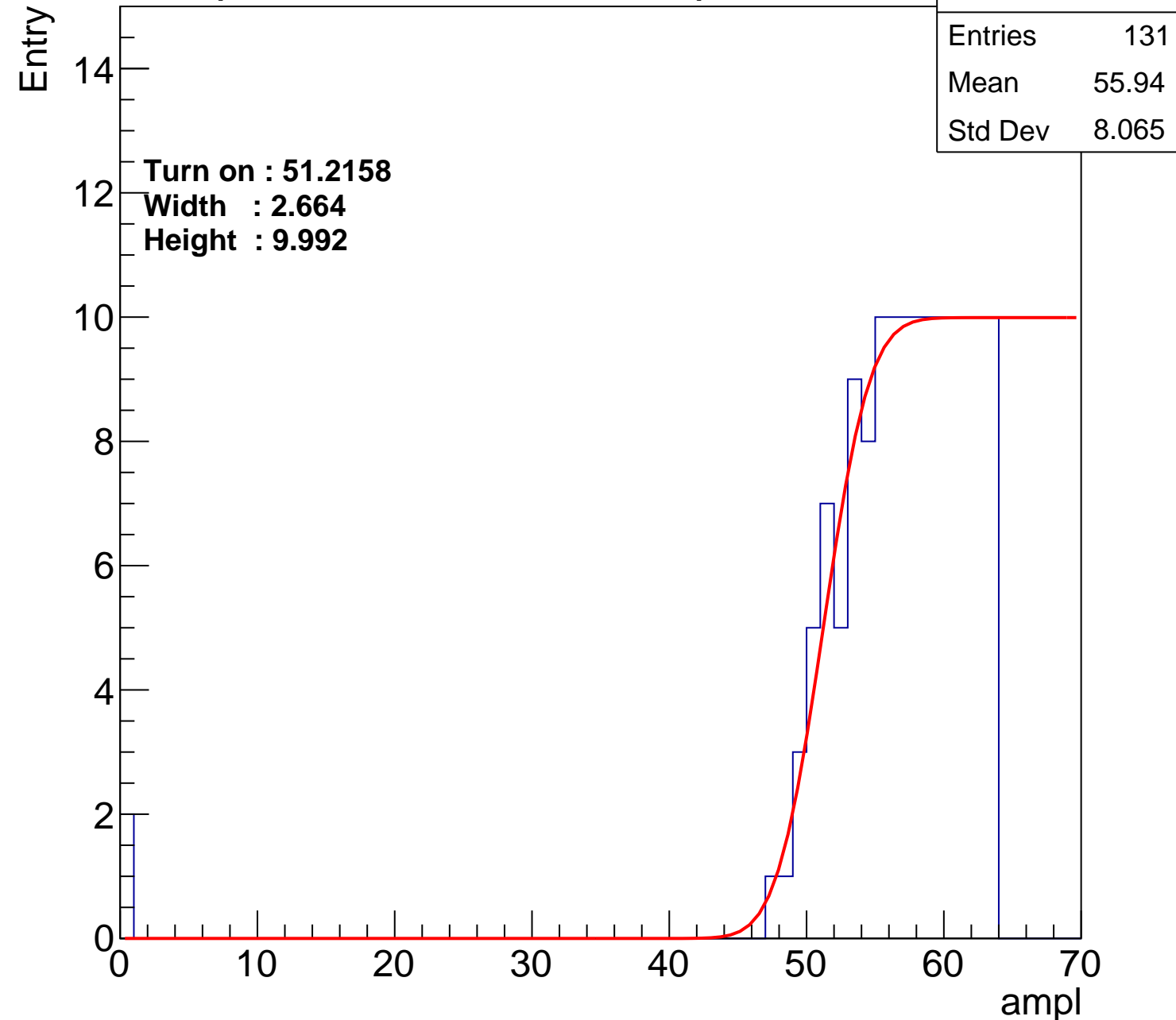
Width : 2.664

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch82

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.4
Std Dev	10.86

Turn on : 52.4686

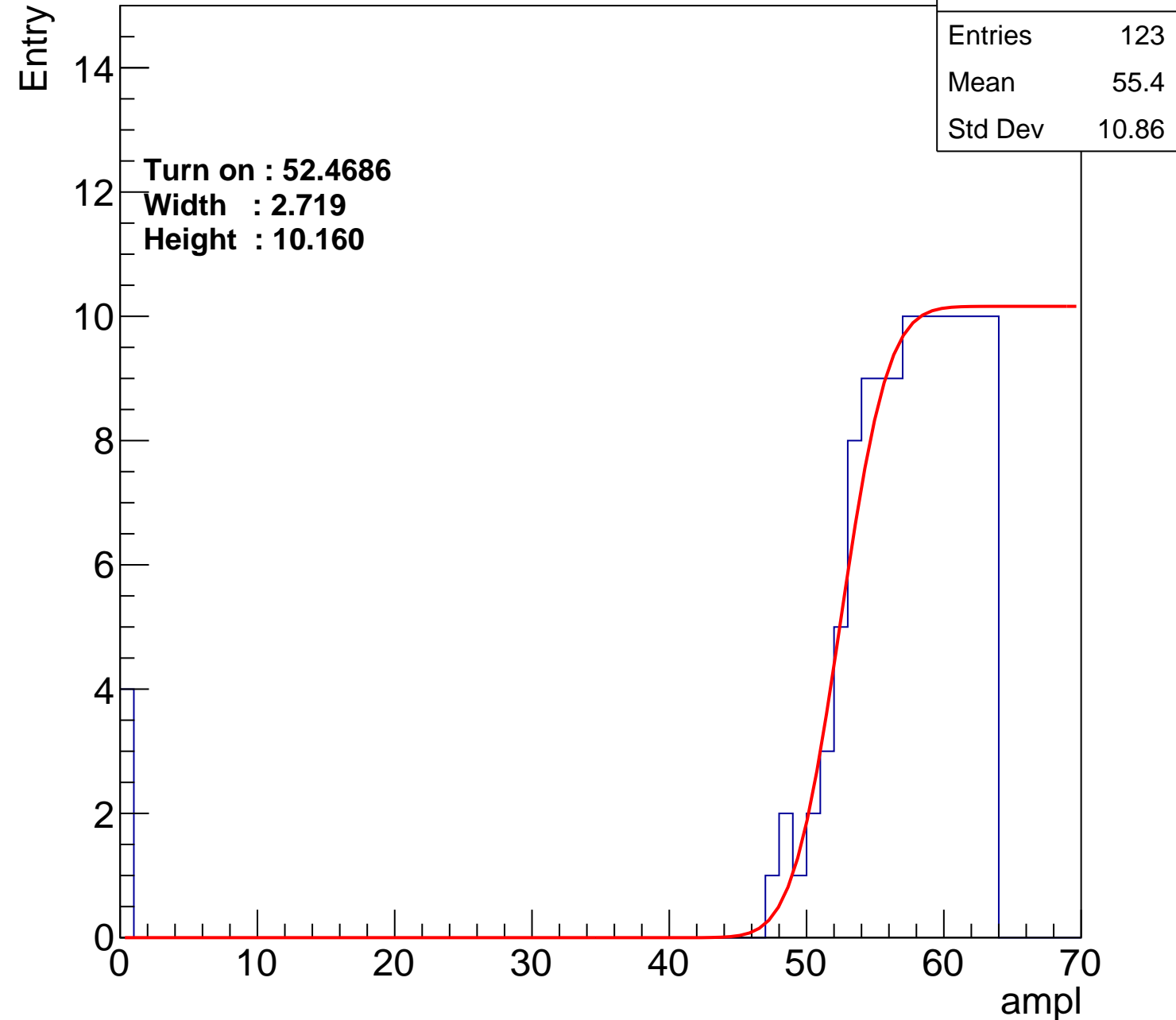
Width : 2.719

Height : 10.160

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch83

calib_packv5_040323_1717.root, FC#2, port C3

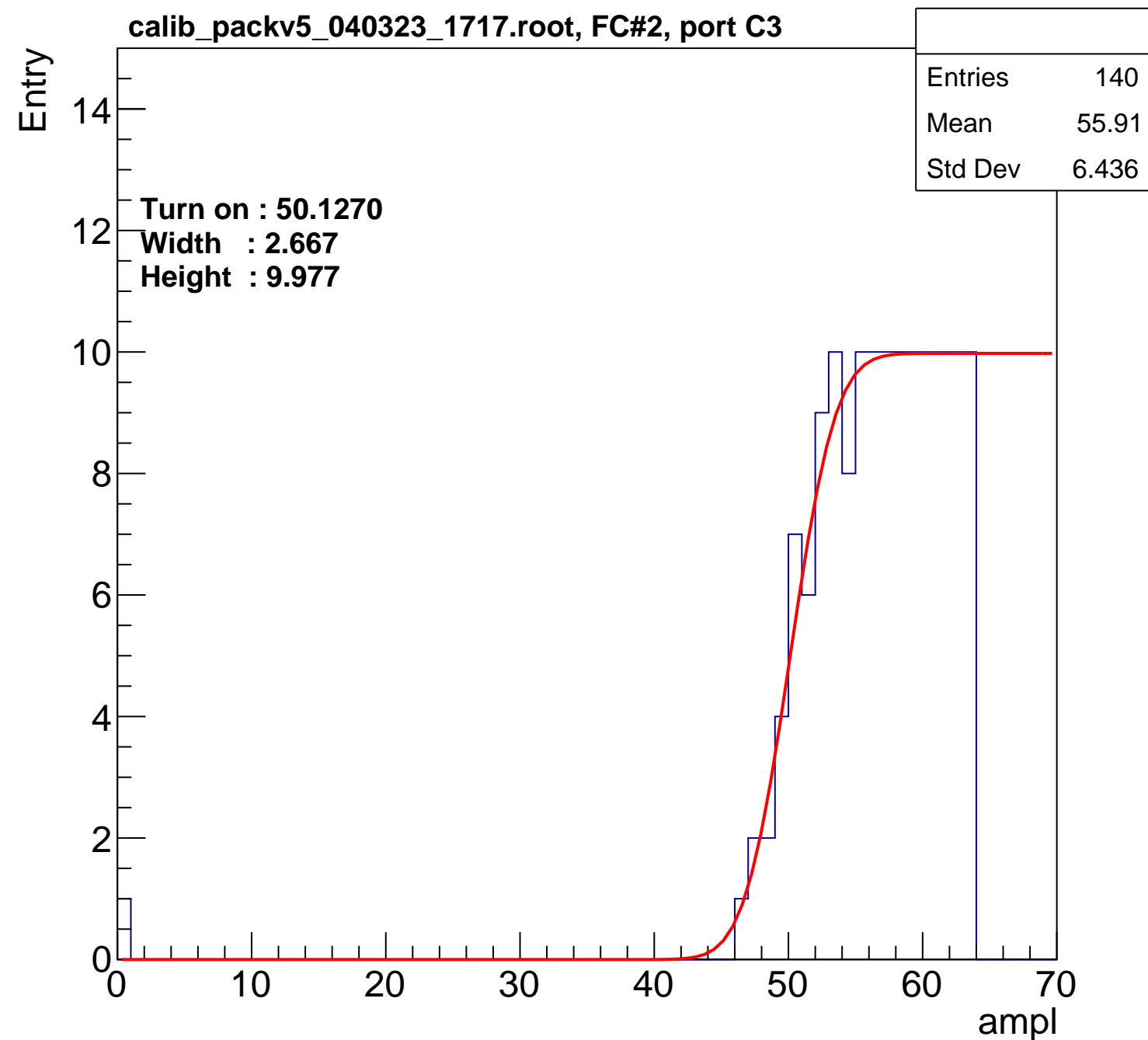
Entry

14
12
10
8
6
4
2
0

Turn on : 50.1270
Width : 2.667
Height : 9.977

Entries	140
Mean	55.91
Std Dev	6.436

ampl



B0L103S, U22-ch84

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	54.61
Std Dev	11.43

Turn on : 51.4637

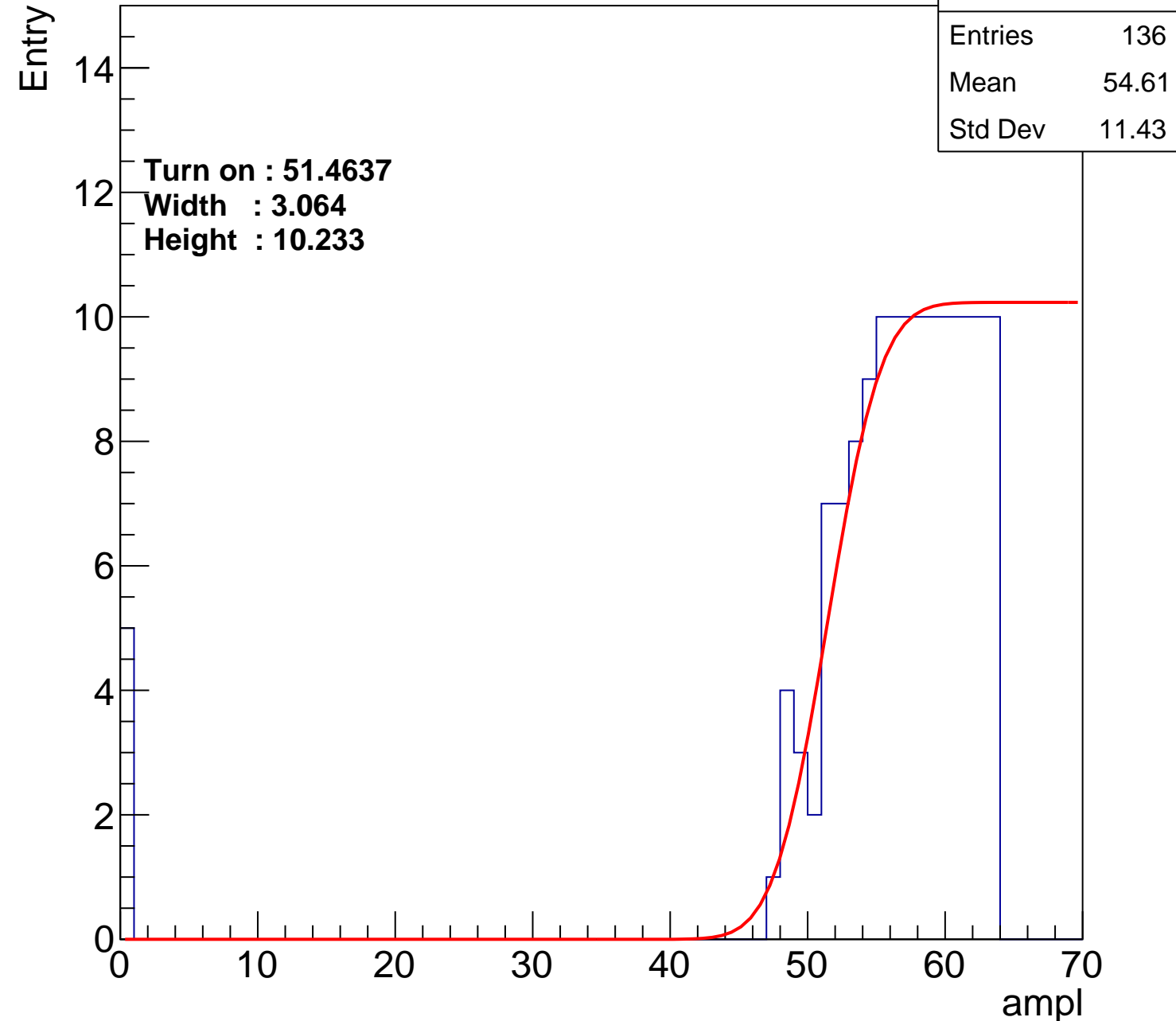
Width : 3.064

Height : 10.233

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch85

calib_packv5_040323_1717.root, FC#2, port C3

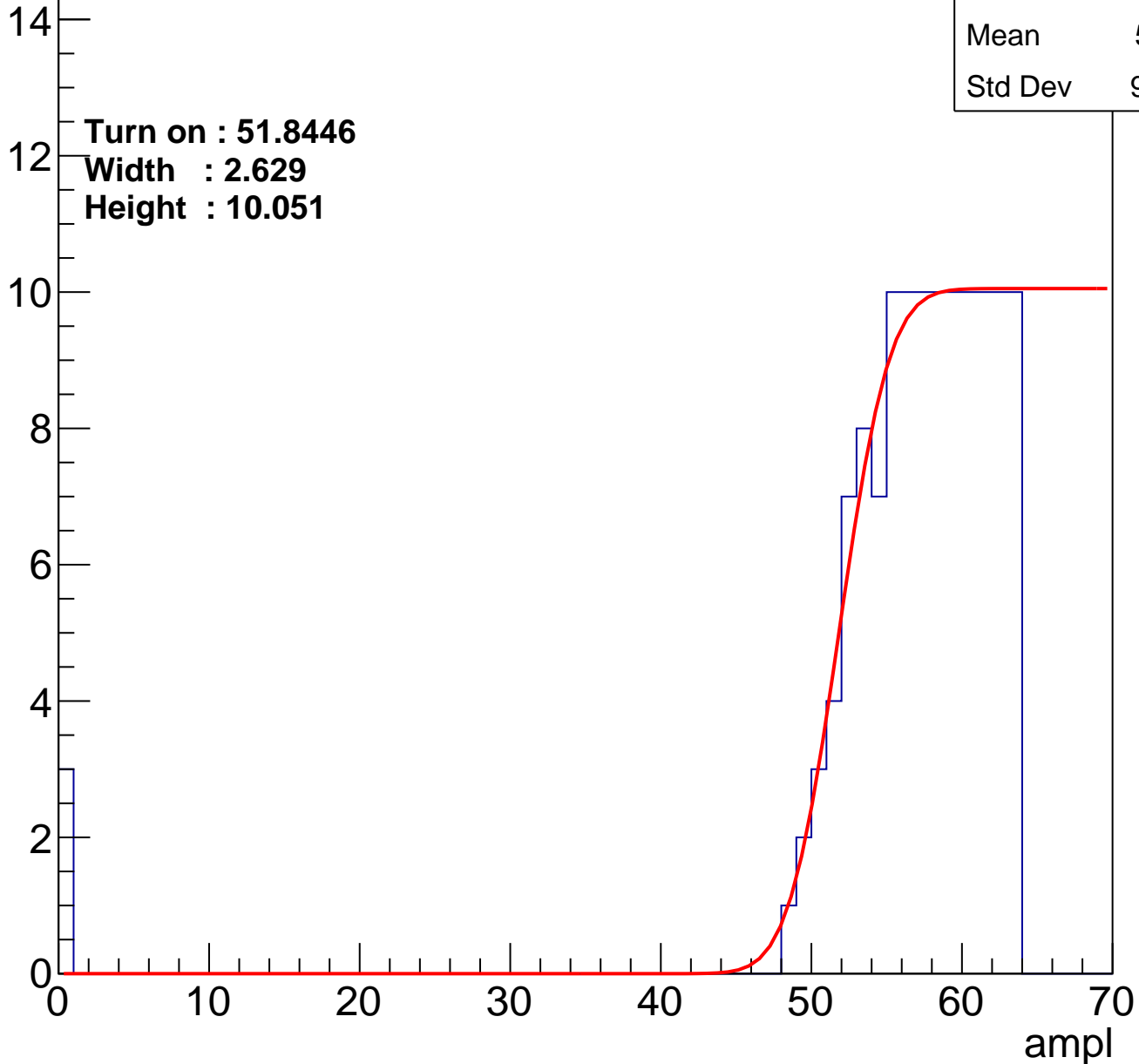
Entries	125
Mean	55.81
Std Dev	9.548

Turn on : 51.8446

Width : 2.629

Height : 10.051

Entry



B0L103S, U22-ch86

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.61
Std Dev	9.597

Turn on : 52.8182

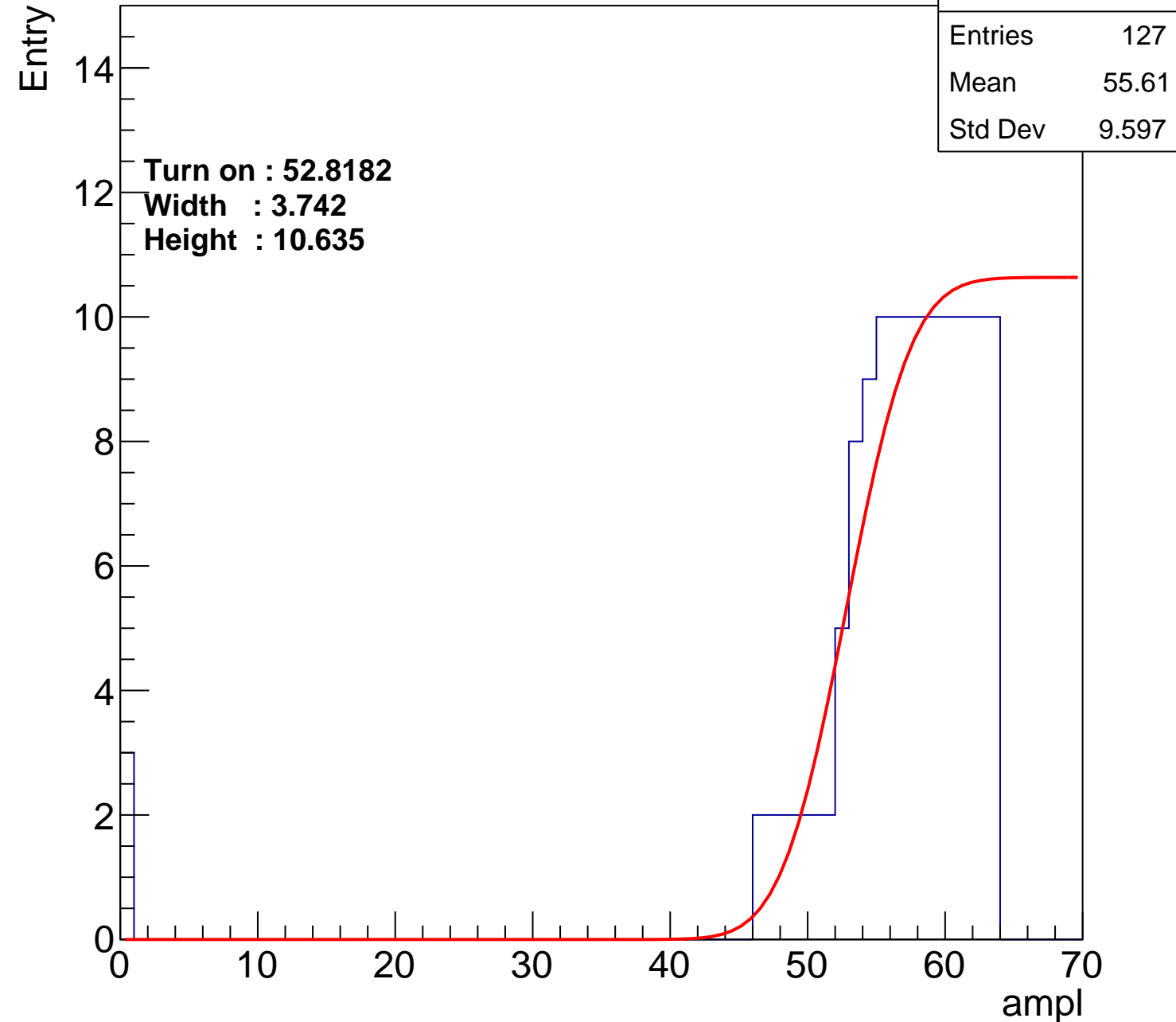
Width : 3.742

Height : 10.635

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch87

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	56.82
Std Dev	6.422

Turn on : 52.4067

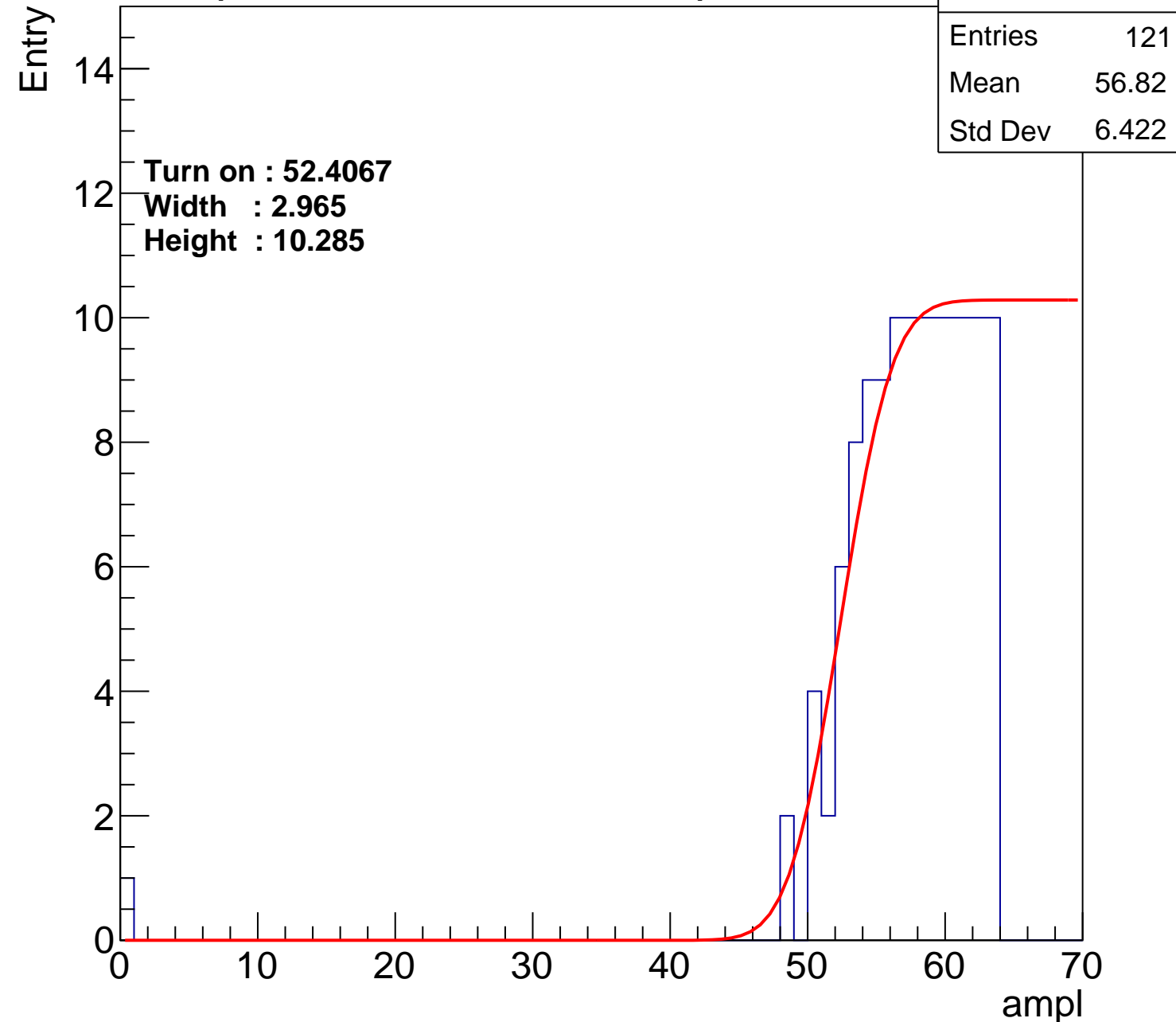
Width : 2.965

Height : 10.285

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch88

calib_packv5_040323_1717.root, FC#2, port C3

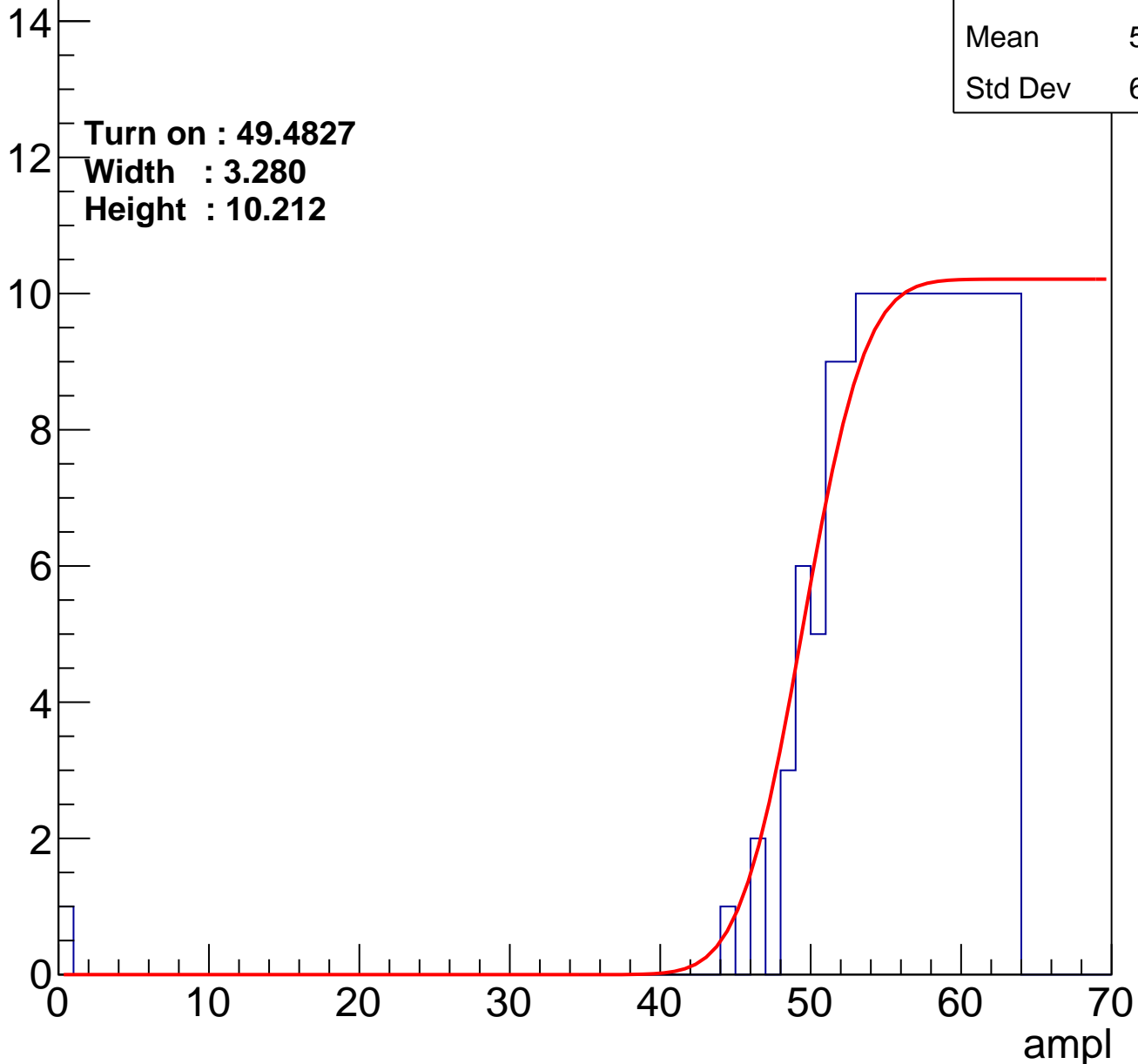
Entries	146
Mean	55.69
Std Dev	6.433

Turn on : 49.4827

Width : 3.280

Height : 10.212

Entry



B0L103S, U22-ch89

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.83
Std Dev	6.515

Turn on : 52.8094

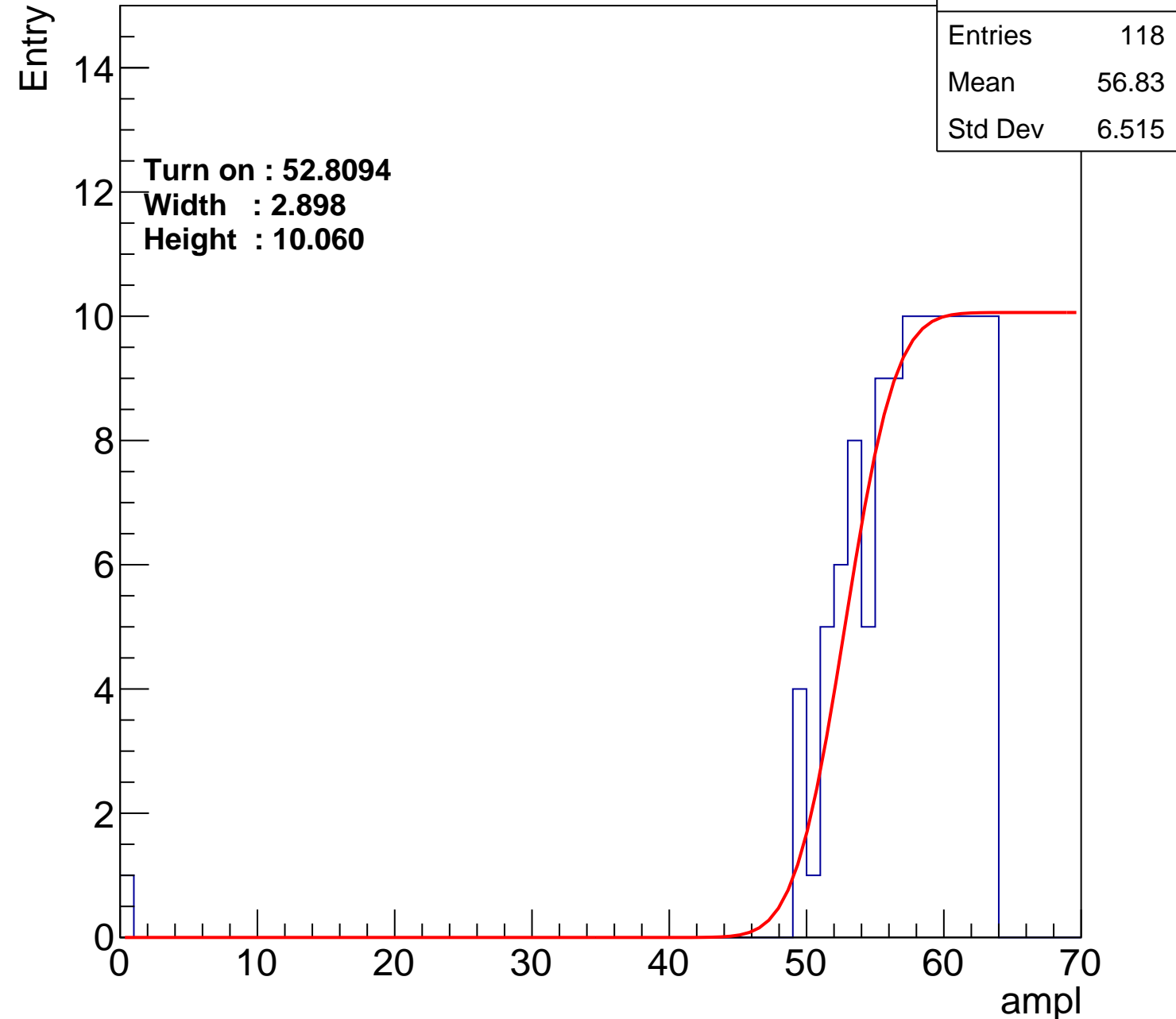
Width : 2.898

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch90

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.26
Std Dev	11.04

Turn on : 49.7365

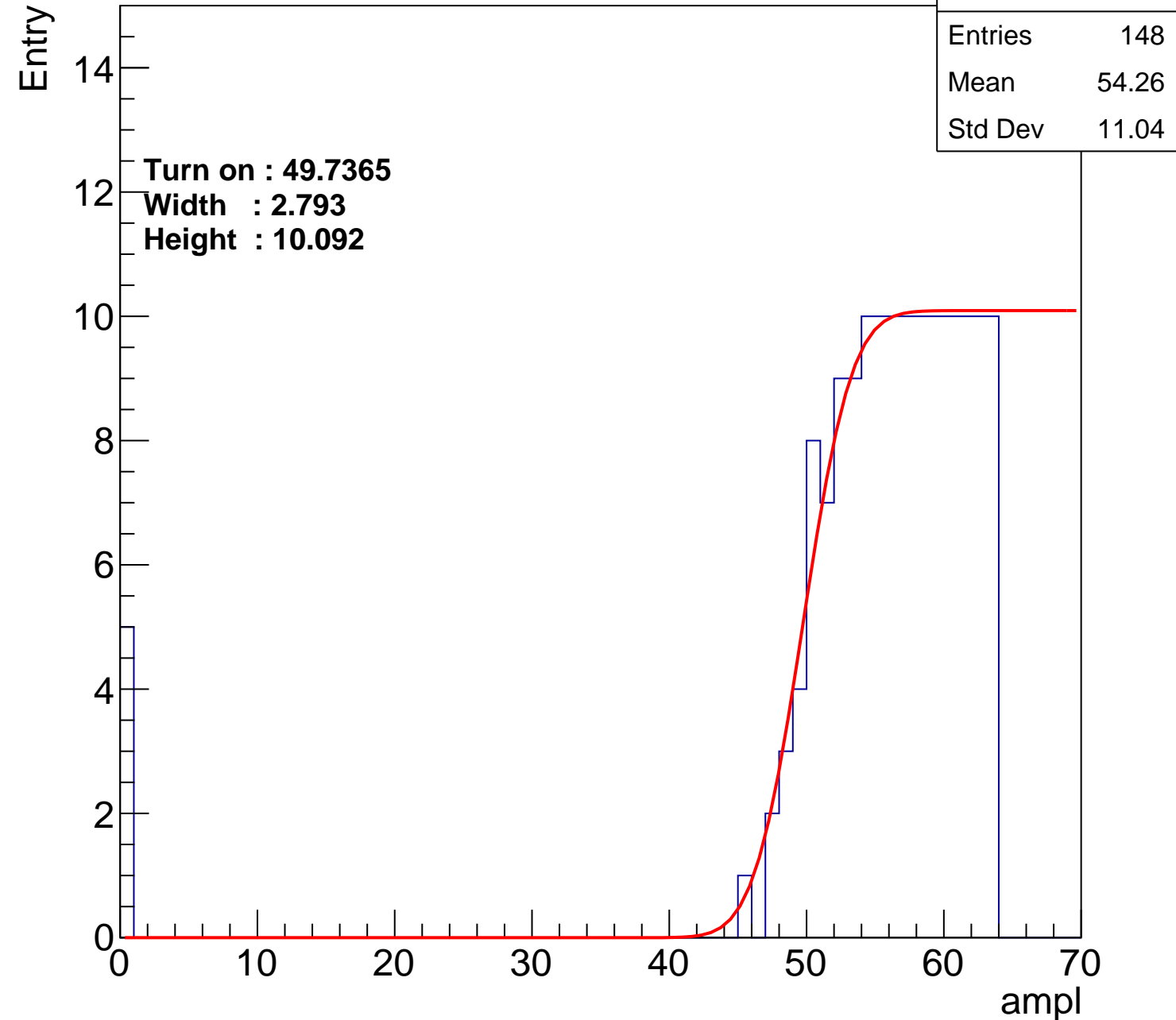
Width : 2.793

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch91

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.31
Std Dev	9.425

Turn on : 51.2556

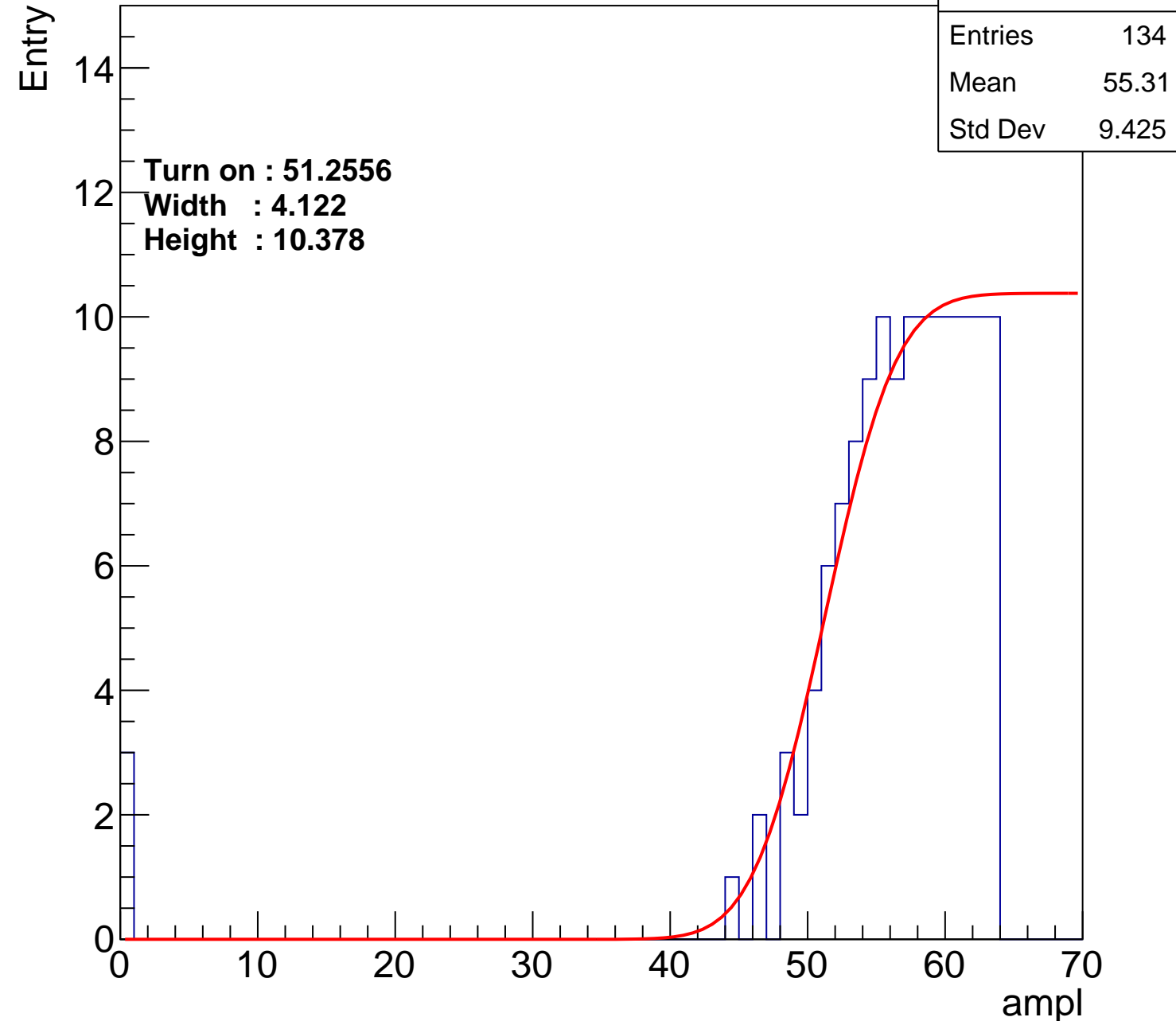
Width : 4.122

Height : 10.378

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch92

calib_packv5_040323_1717.root, FC#2, port C3

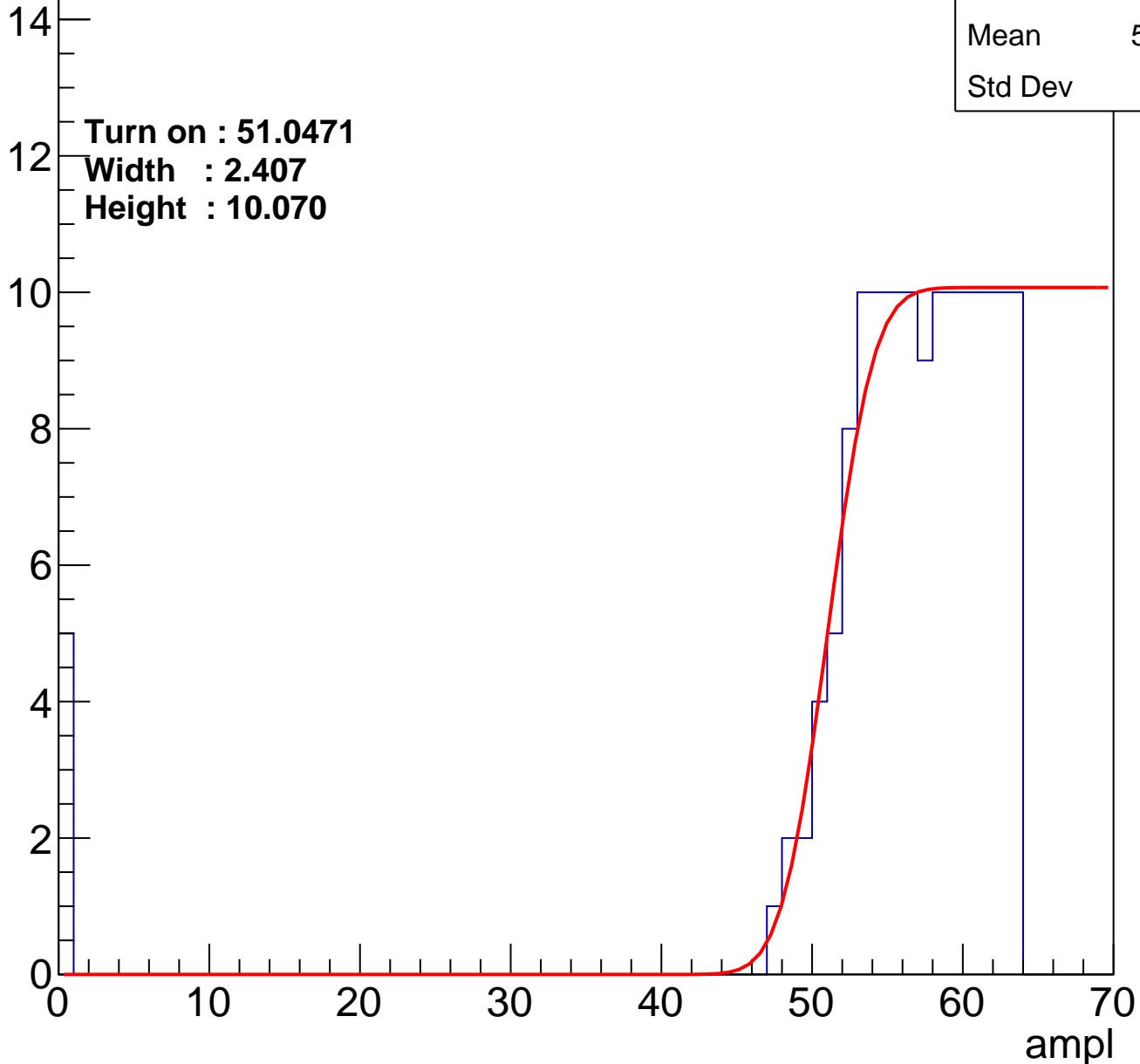
Entries	136
Mean	54.67
Std Dev	11.4

Turn on : 51.0471

Width : 2.407

Height : 10.070

Entry



B0L103S, U22-ch93

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.46
Std Dev	8.015

Turn on : 50.6504

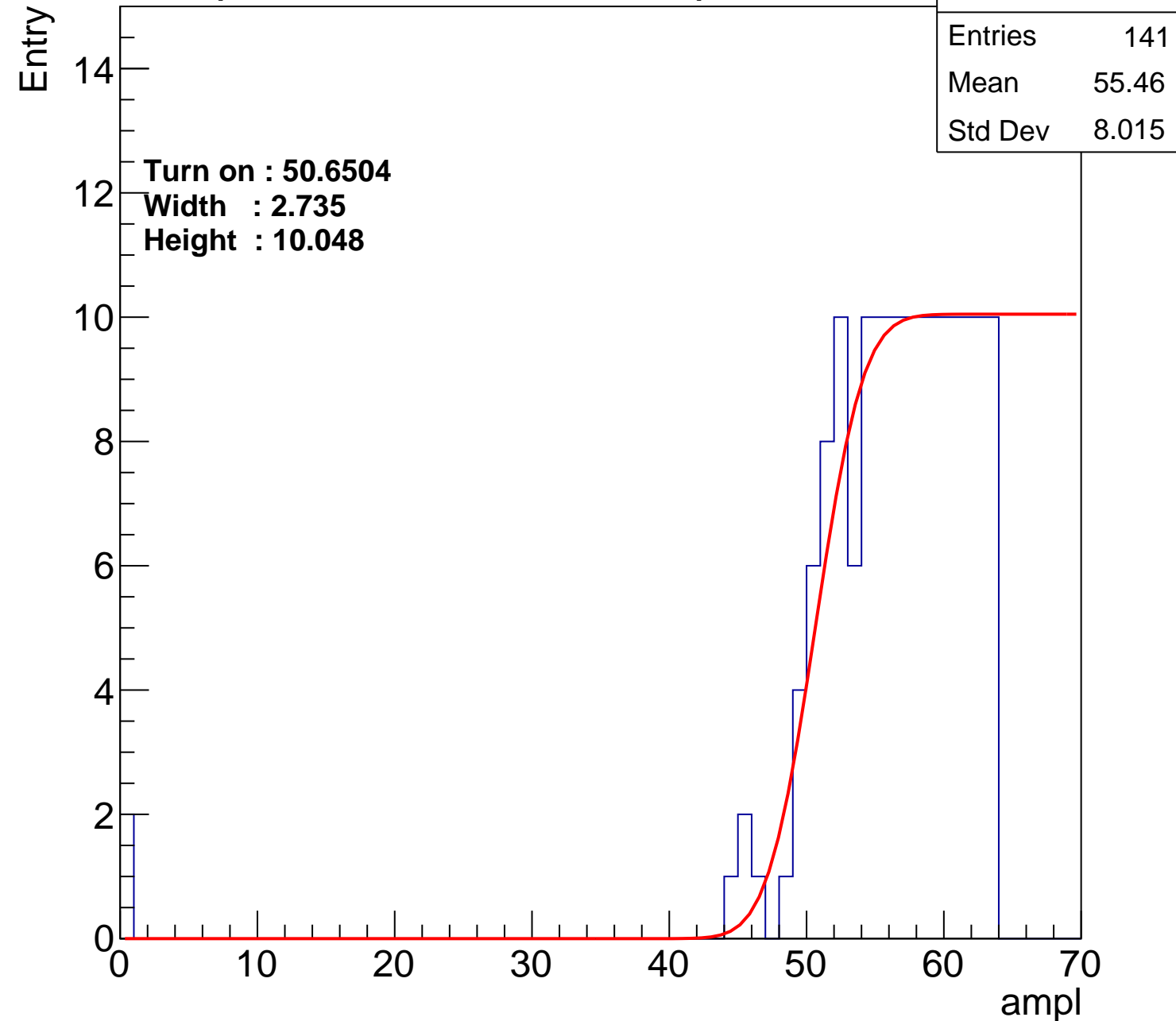
Width : 2.735

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch94

calib_packv5_040323_1717.root, FC#2, port C3

Entries	152
Mean	54.64
Std Dev	9.084

Turn on : 49.4627

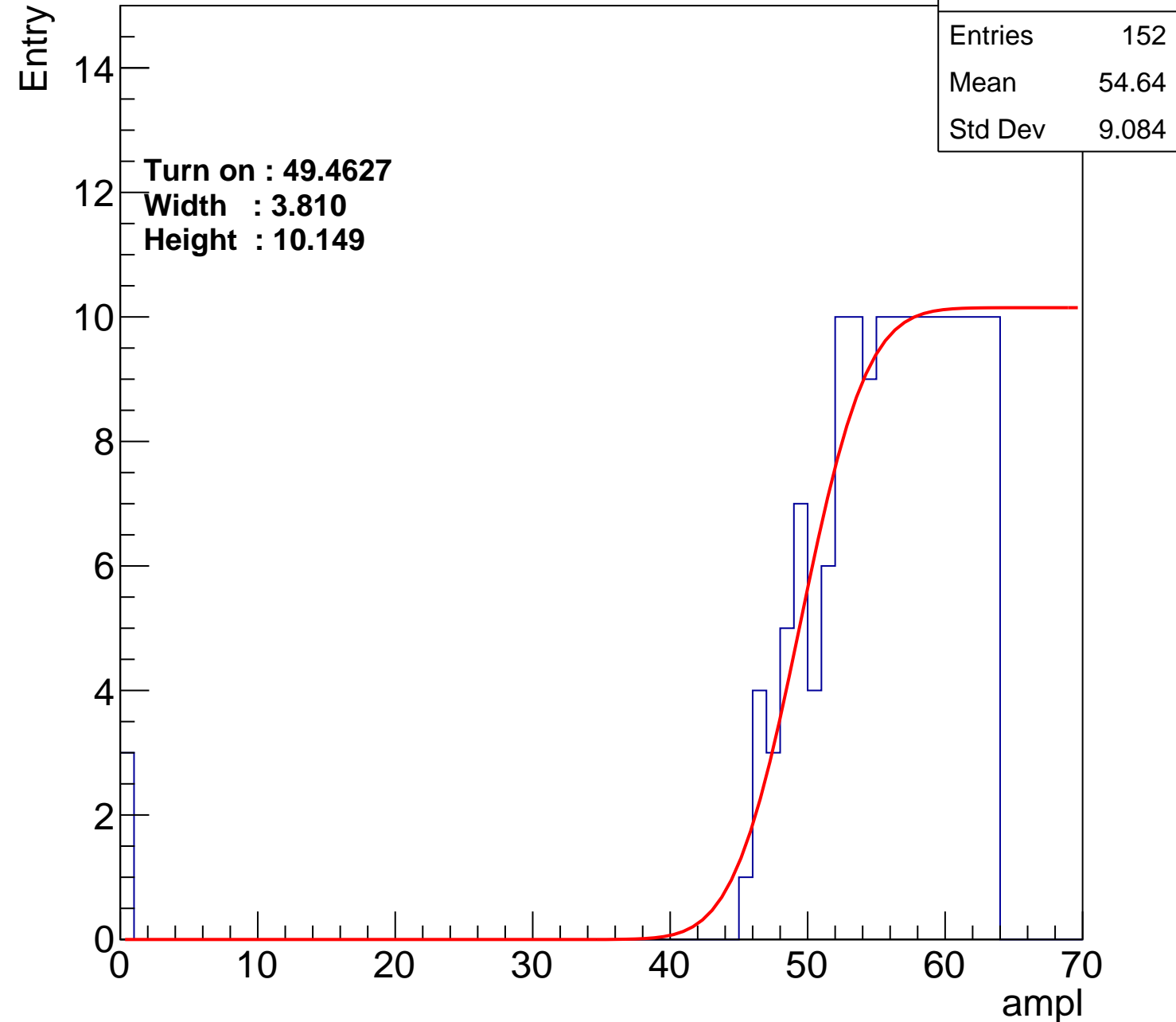
Width : 3.810

Height : 10.149

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch95

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.57
Std Dev	11.4

Turn on : 50.8069

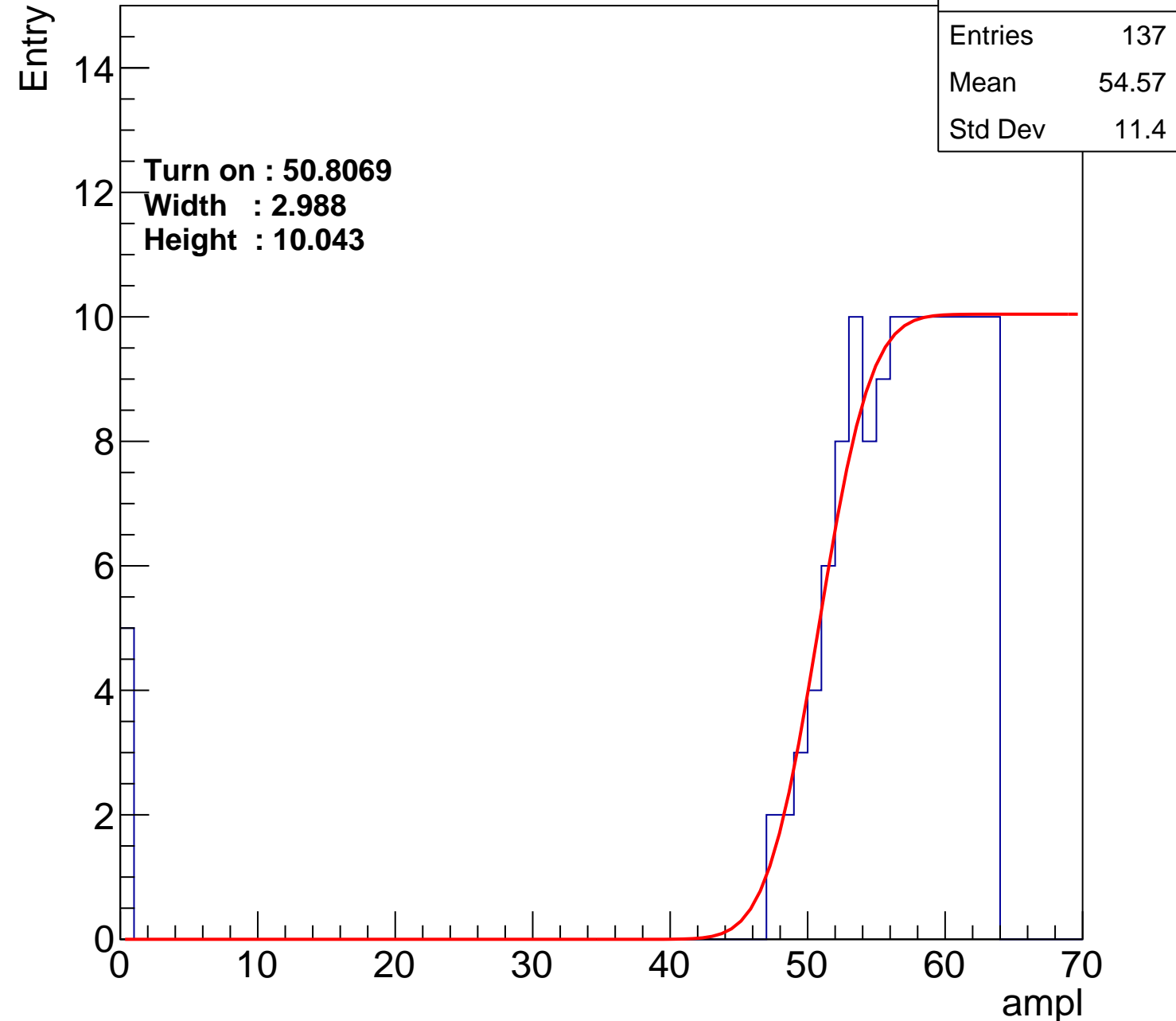
Width : 2.988

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch96

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	55.08
Std Dev	10.59

Turn on : 51.4064

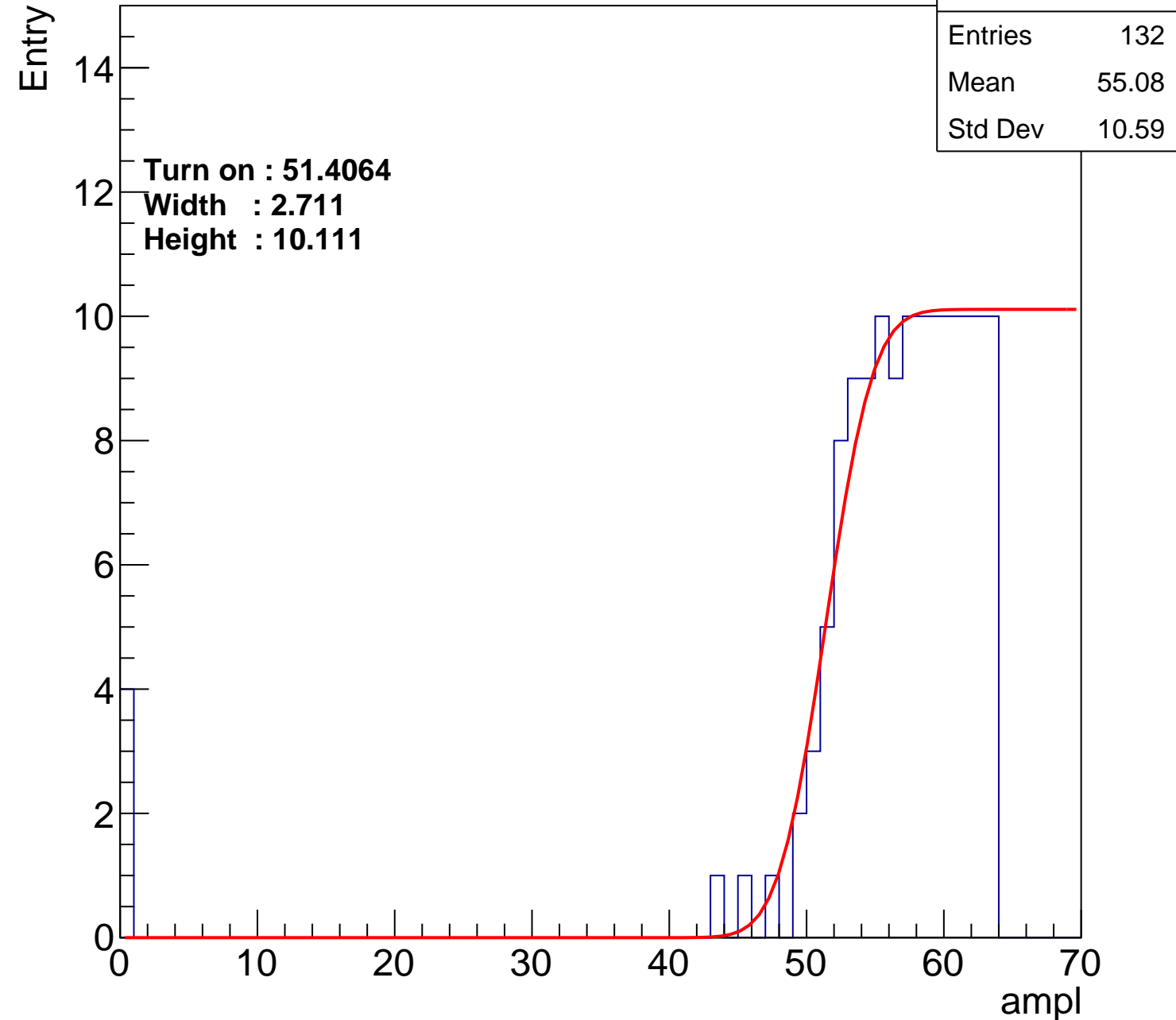
Width : 2.711

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch97

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	54.68
Std Dev	10.2

Turn on : 50.4086

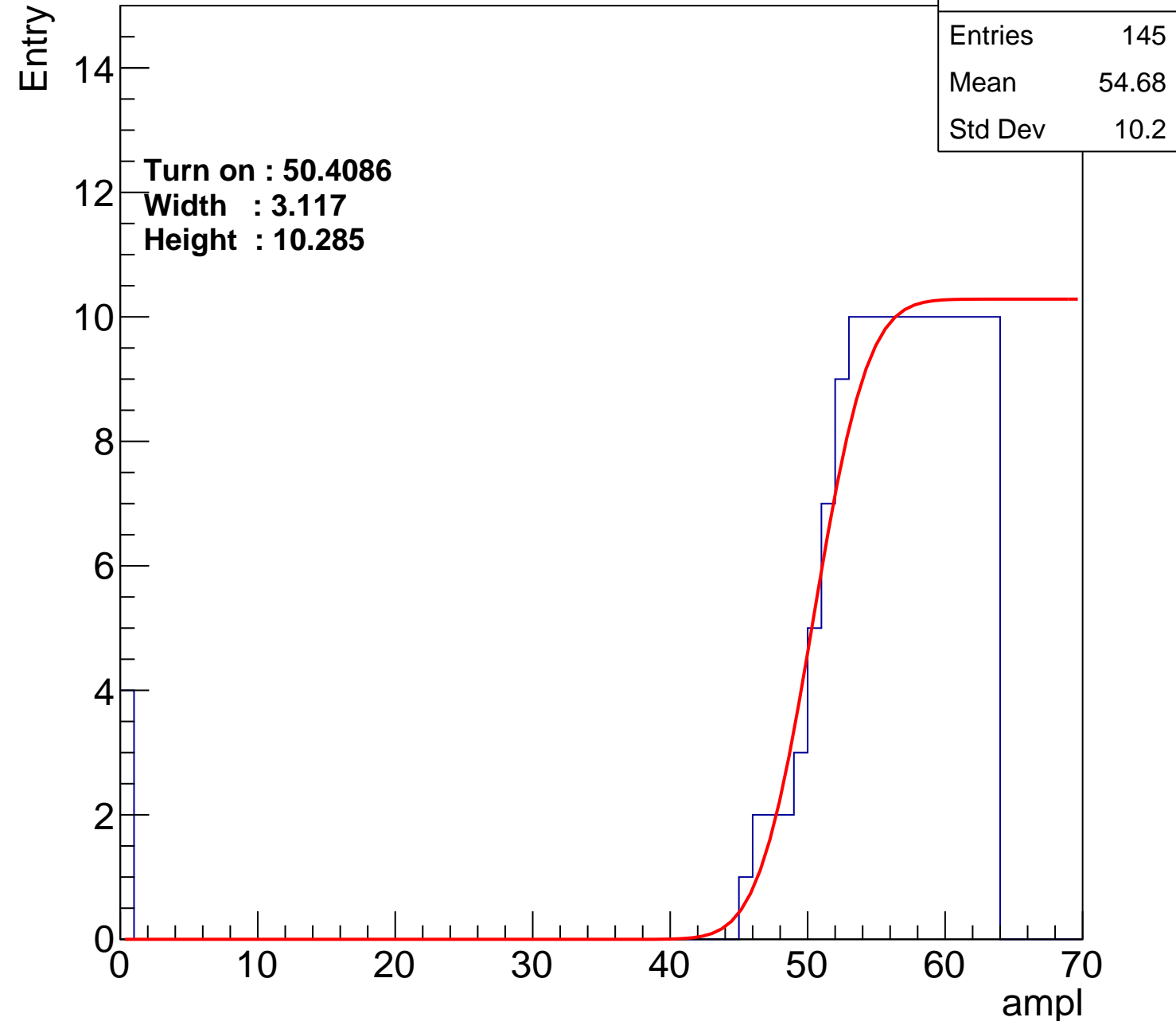
Width : 3.117

Height : 10.285

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch98

calib_packv5_040323_1717.root, FC#2, port C3

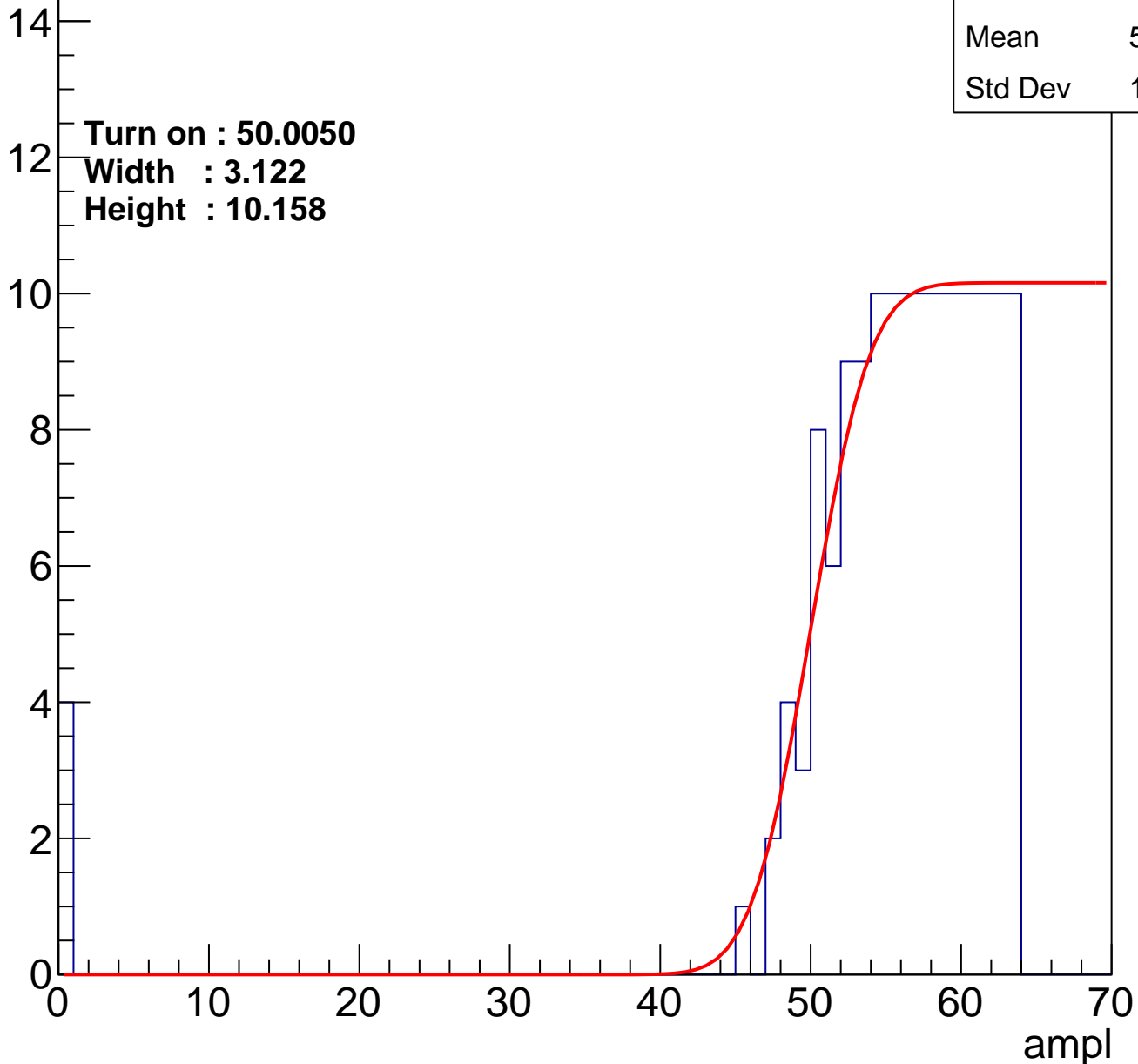
Entry

Entries	146
Mean	54.65
Std Dev	10.16

Turn on : 50.0050

Width : 3.122

Height : 10.158



B0L103S, U22-ch99

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.34
Std Dev	10.81

Turn on : 51.8882

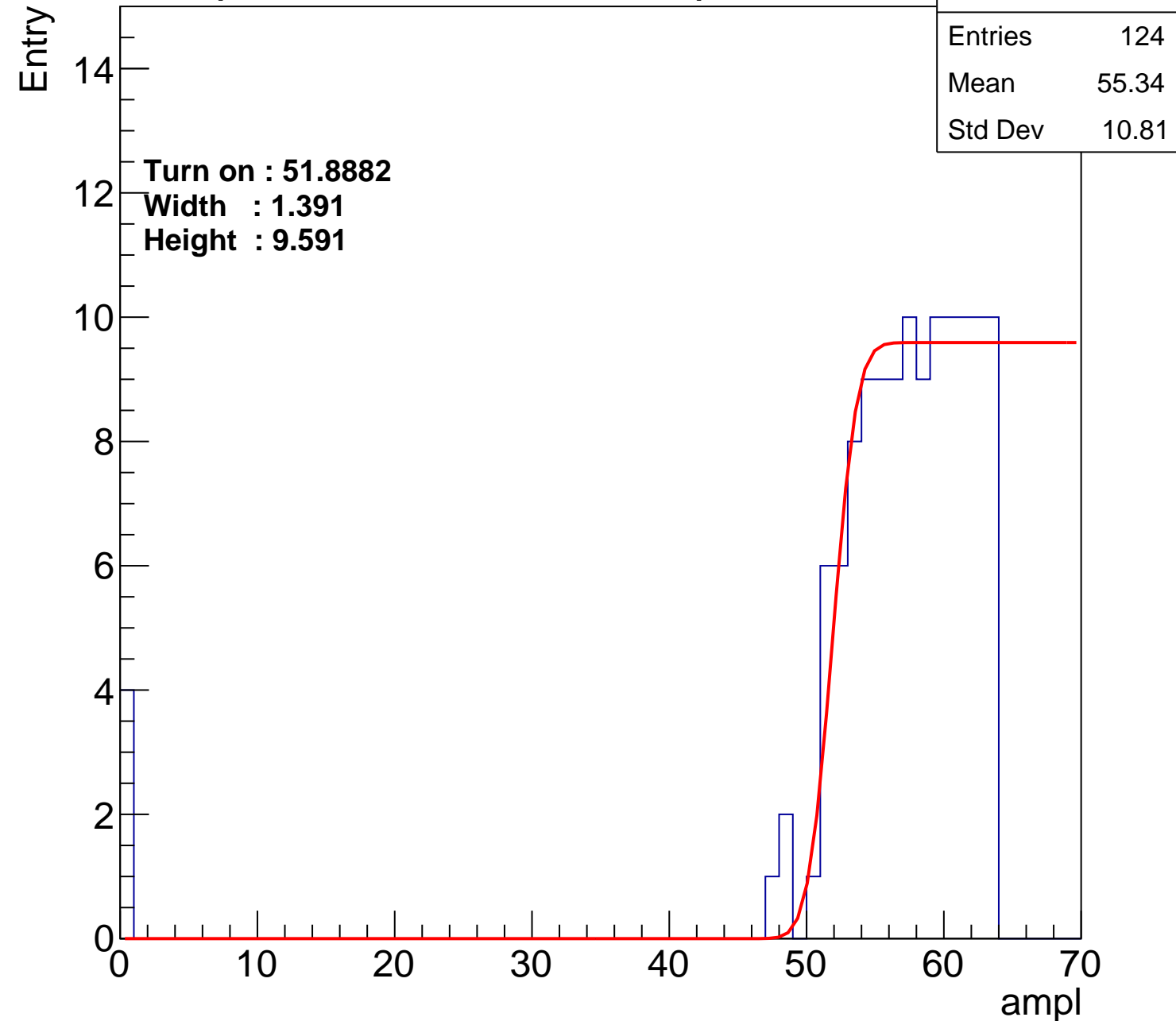
Width : 1.391

Height : 9.591

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch100

calib_packv5_040323_1717.root, FC#2, port C3

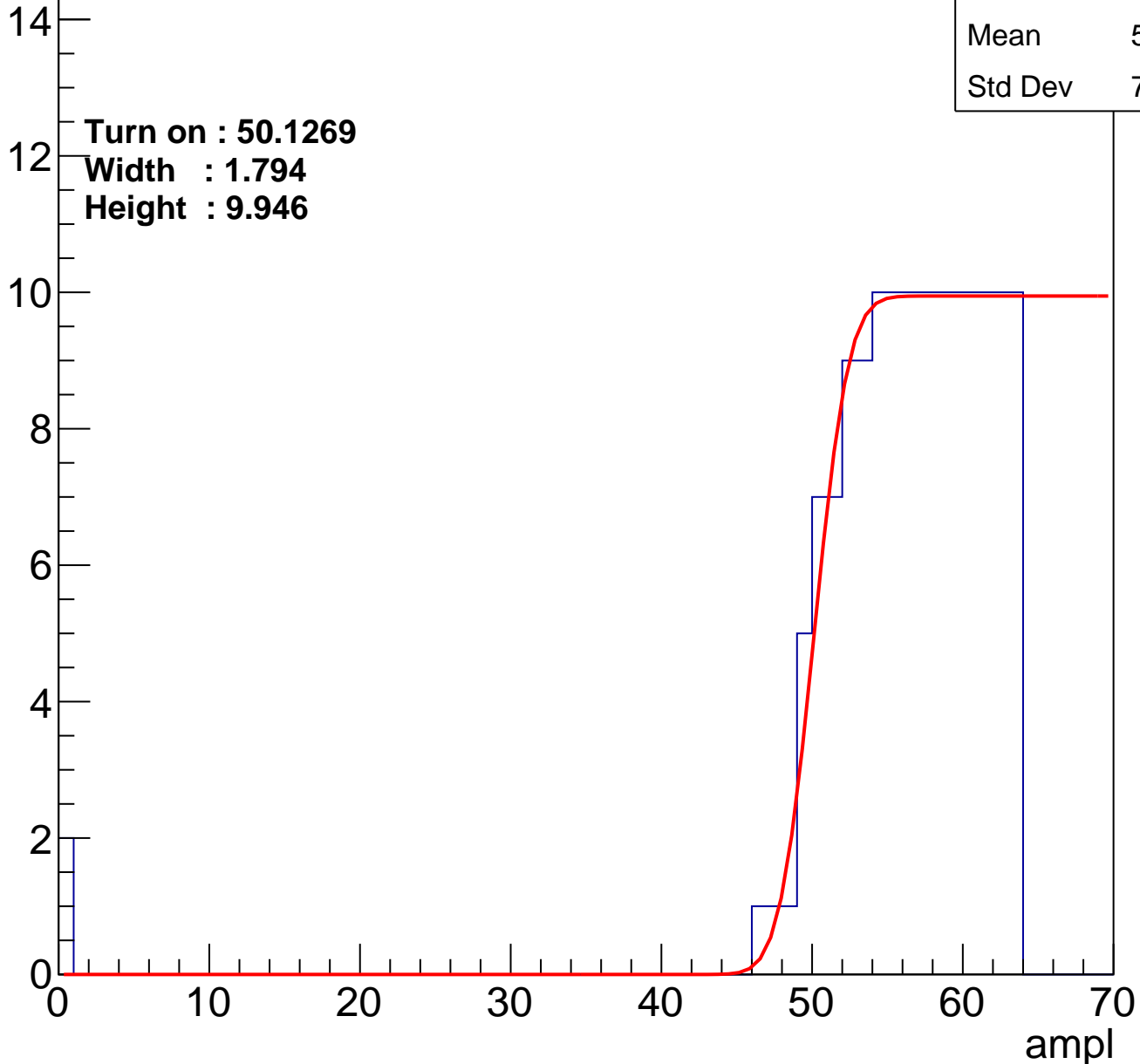
Entries	142
Mean	55.55
Std Dev	7.888

Turn on : 50.1269

Width : 1.794

Height : 9.946

Entry



B0L103S, U22-ch101

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	54.84
Std Dev	11.91

Turn on : 53.5854

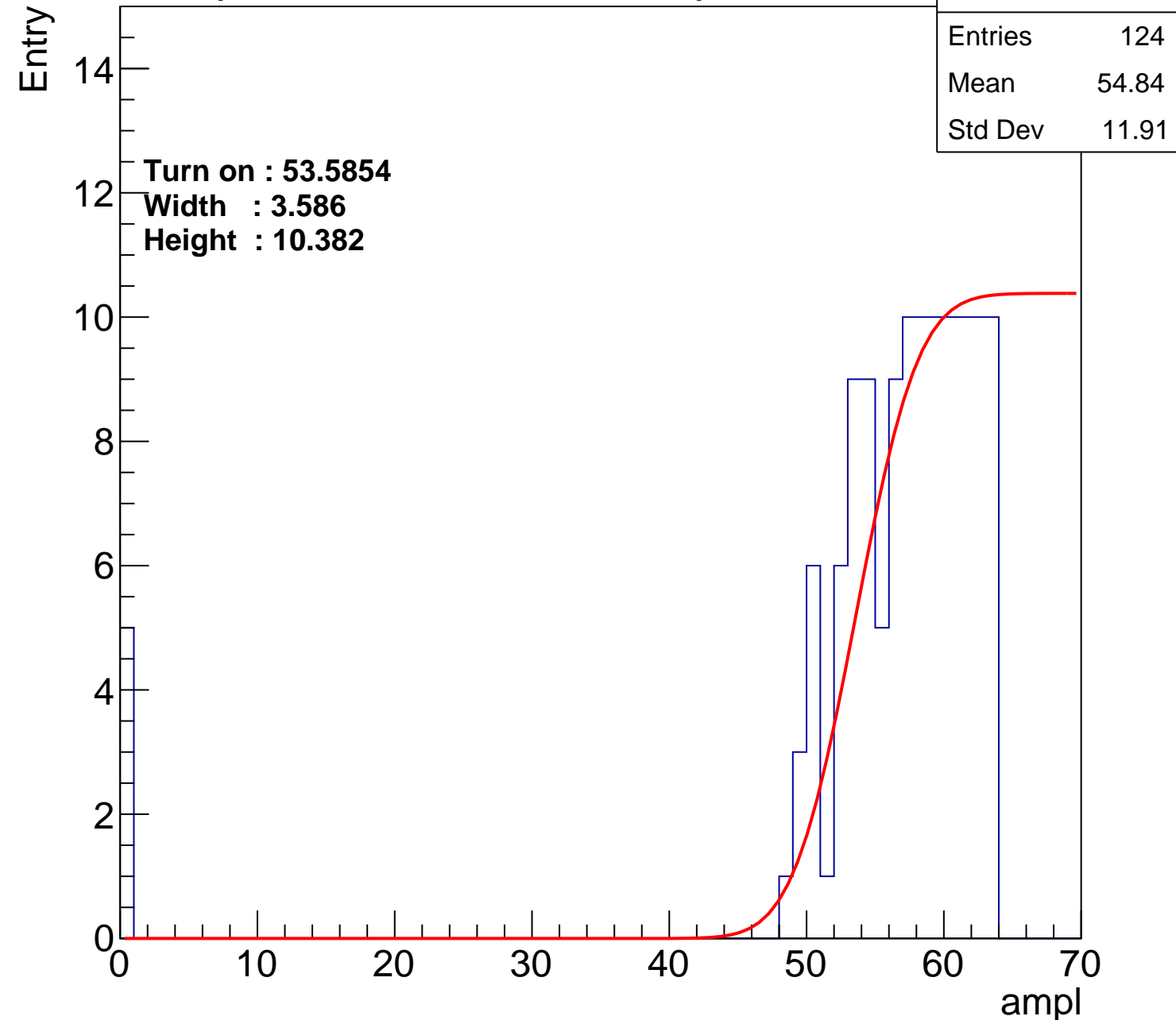
Width : 3.586

Height : 10.382

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch102

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.07
Std Dev	10.49

Turn on : 51.2205

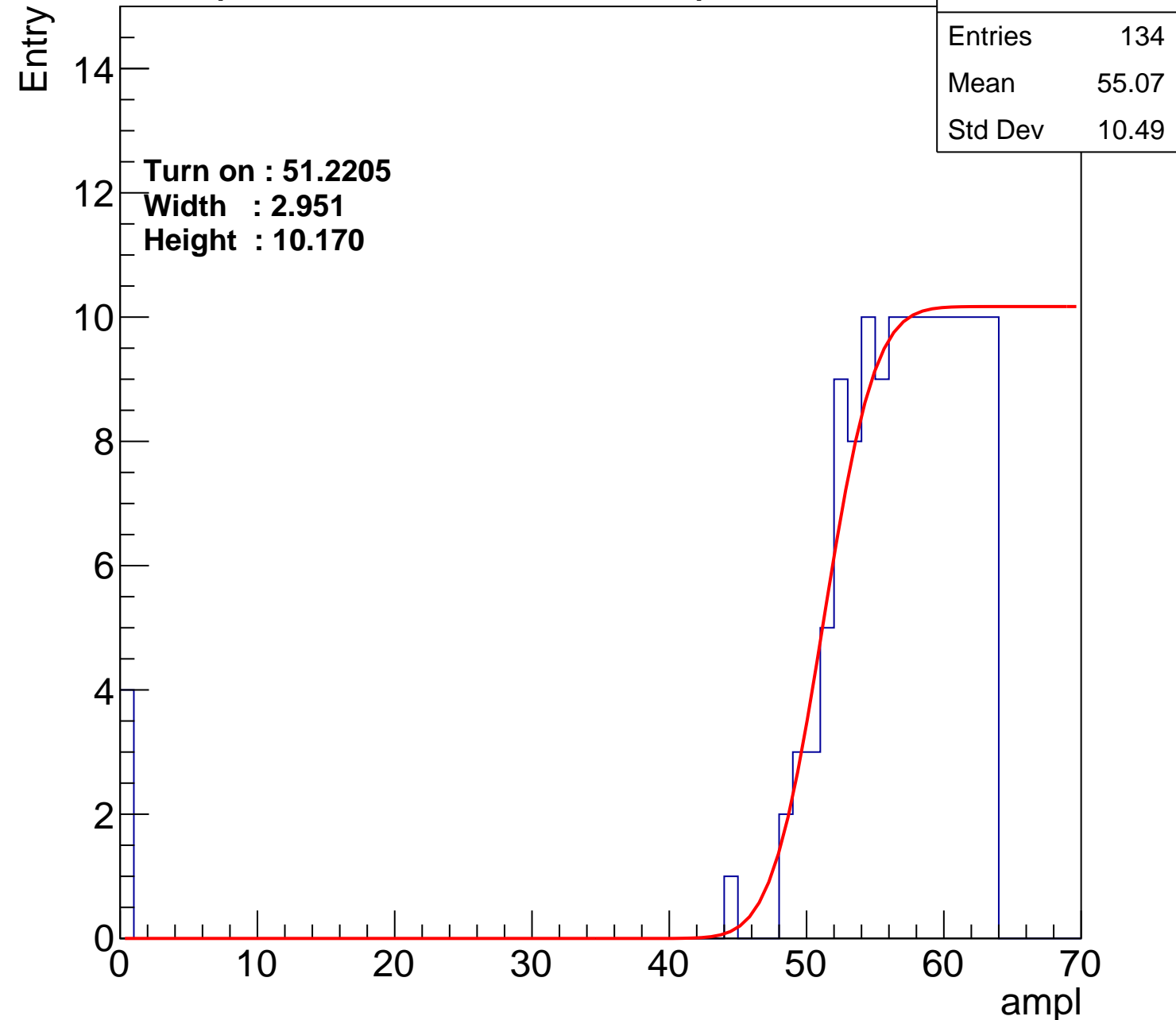
Width : 2.951

Height : 10.170

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch103

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	56.46
Std Dev	6.645

Turn on : 52.6958

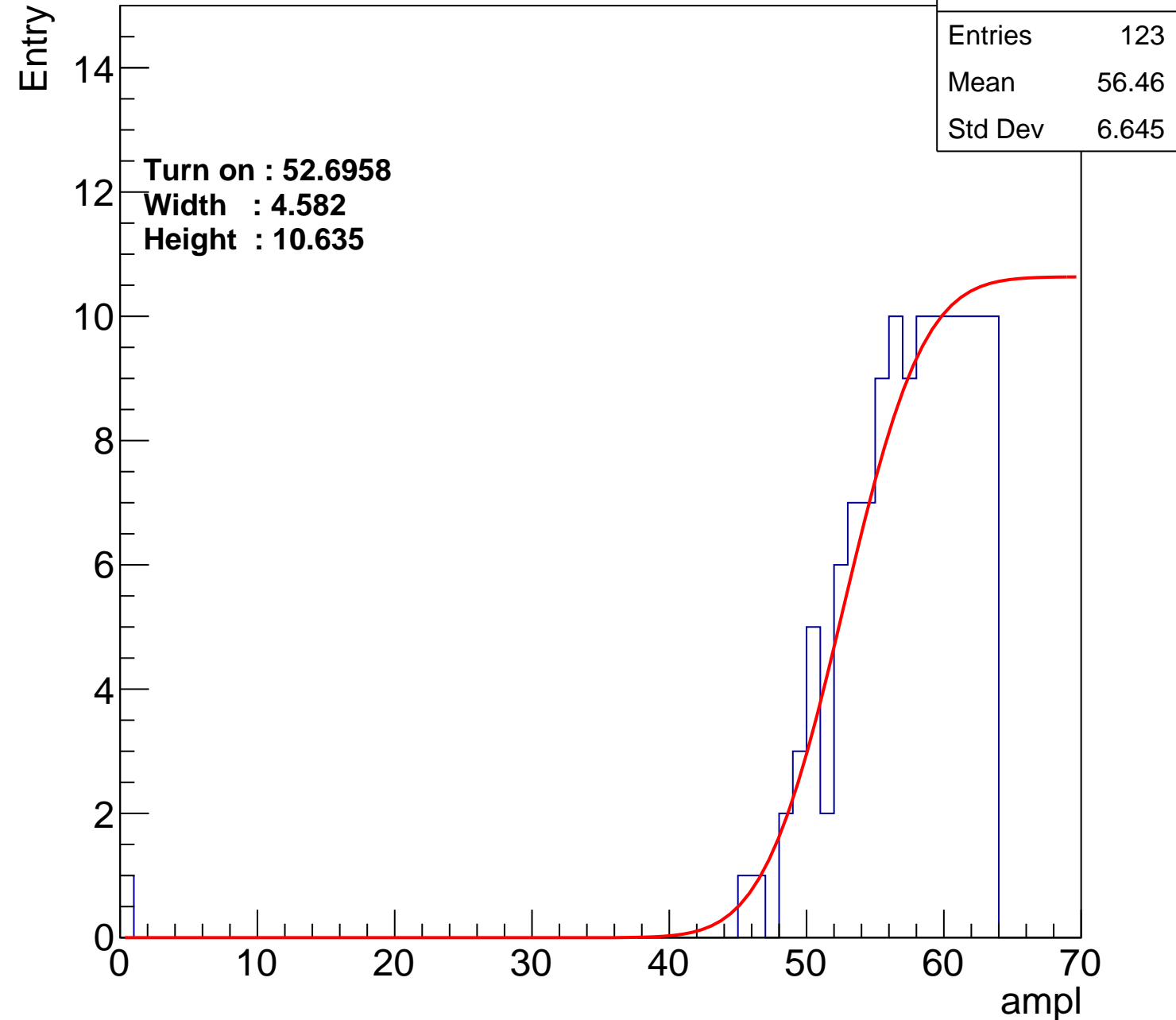
Width : 4.582

Height : 10.635

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch104

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	53.49
Std Dev	14.06

Turn on : 51.6132

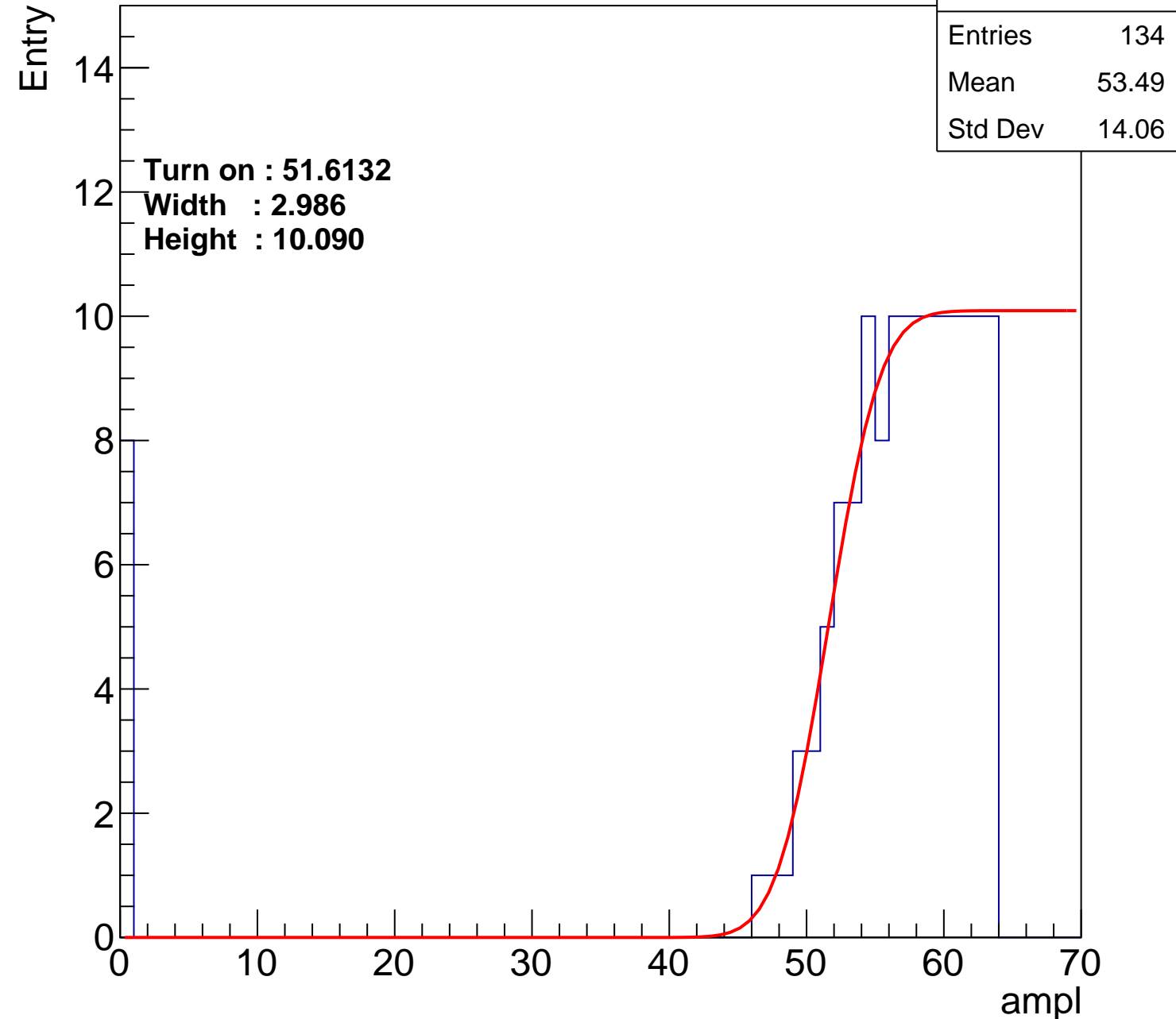
Width : 2.986

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch105

calib_packv5_040323_1717.root, FC#2, port C3

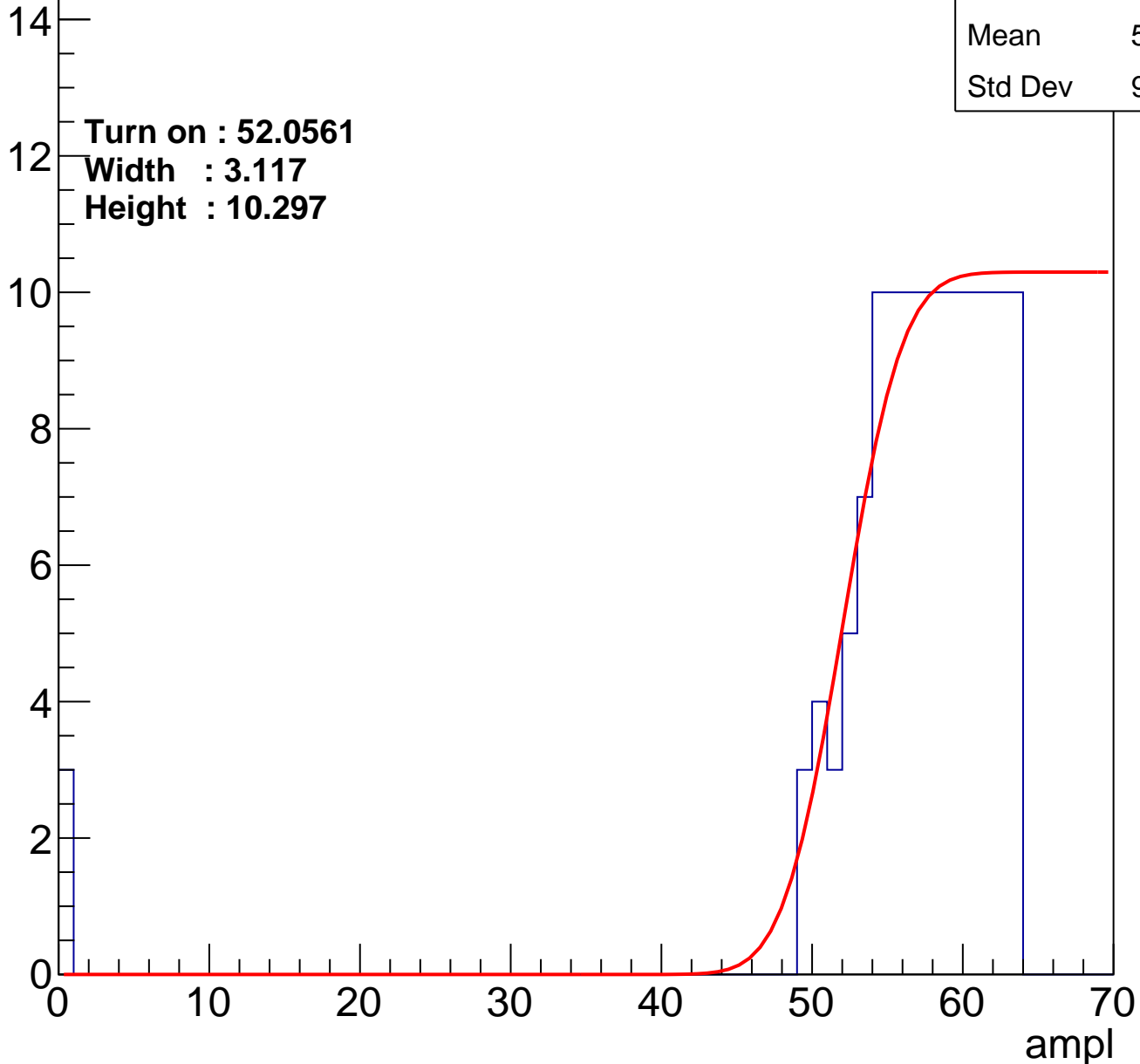
Entries	125
Mean	55.85
Std Dev	9.535

Turn on : 52.0561

Width : 3.117

Height : 10.297

Entry



B0L103S, U22-ch106

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	54.97
Std Dev	10.67

Turn on : 51.6629

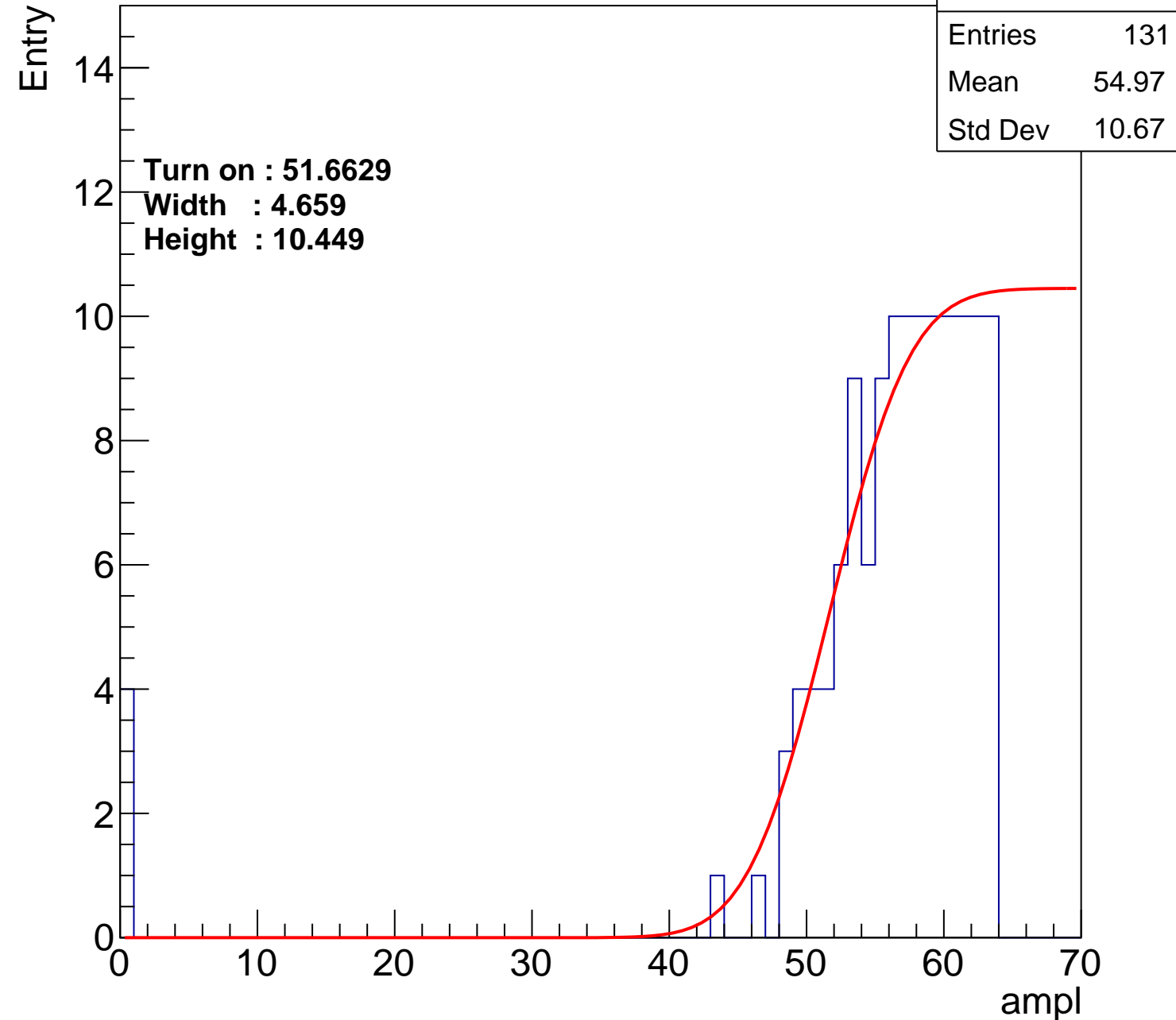
Width : 4.659

Height : 10.449

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch107

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.04
Std Dev	10.53

Turn on : 51.0546

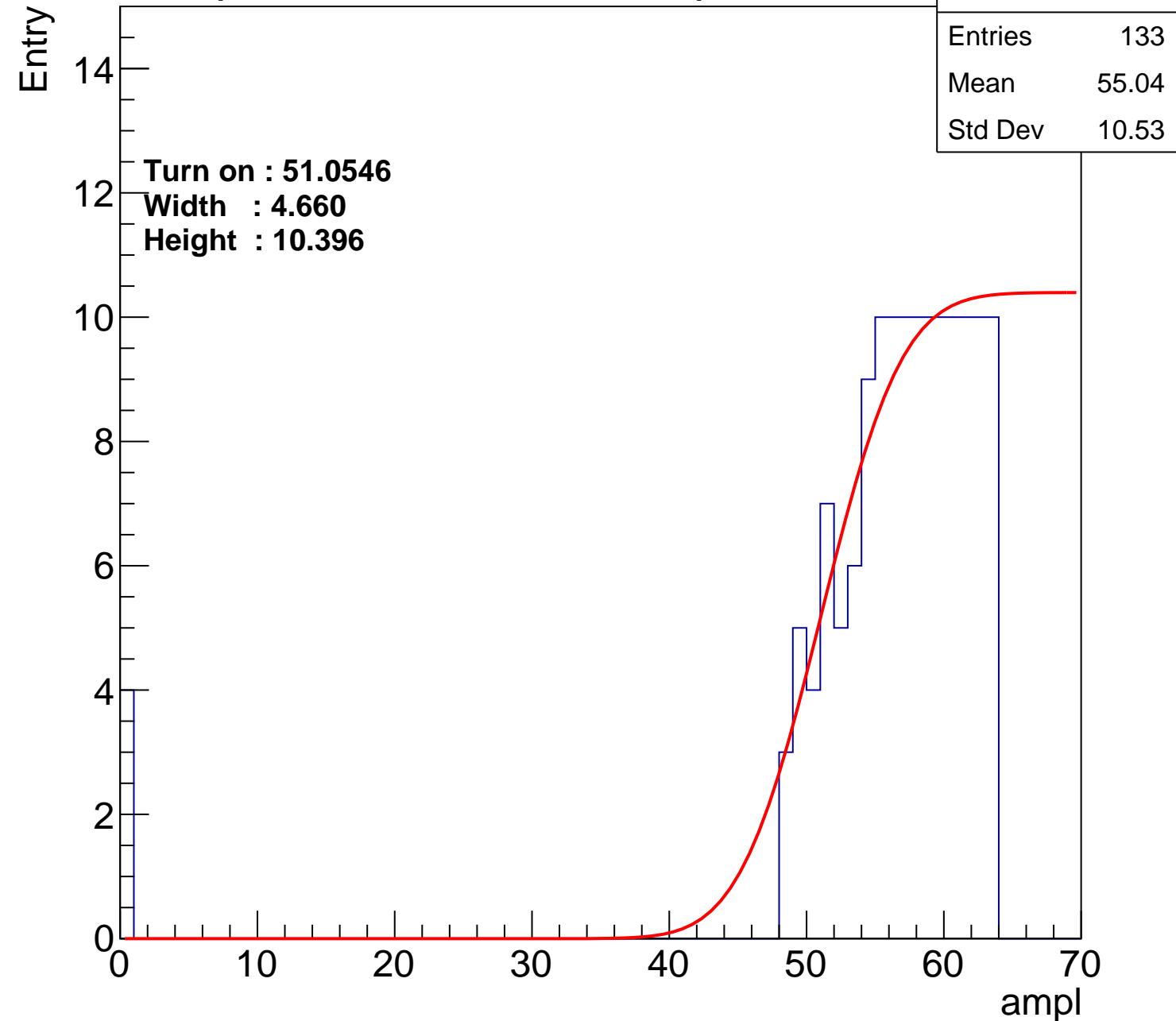
Width : 4.660

Height : 10.396

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch108

calib_packv5_040323_1717.root, FC#2, port C3

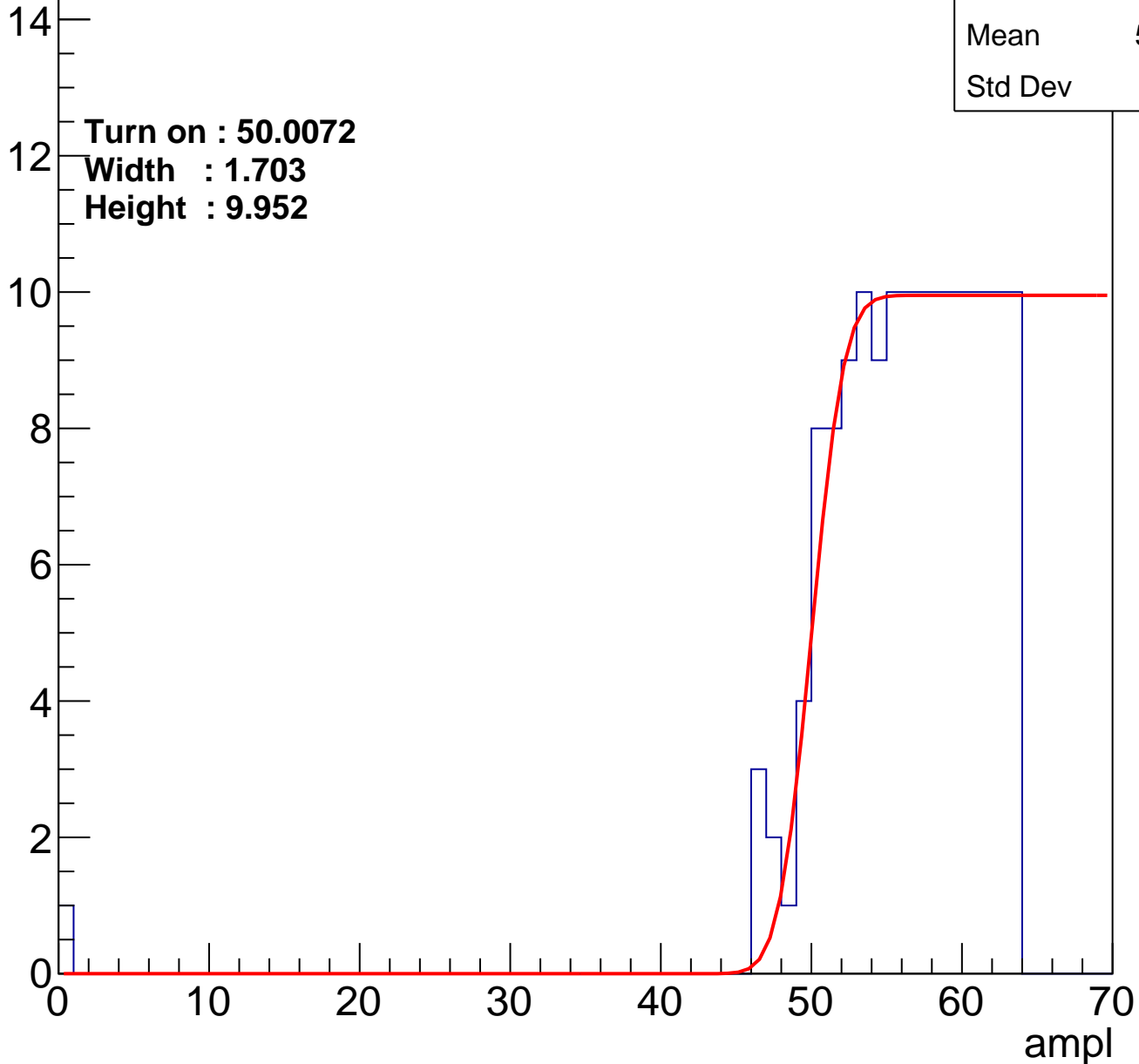
Entries	145
Mean	55.71
Std Dev	6.44

Turn on : 50.0072

Width : 1.703

Height : 9.952

Entry



B0L103S, U22-ch109

calib_packv5_040323_1717.root, FC#2, port C3

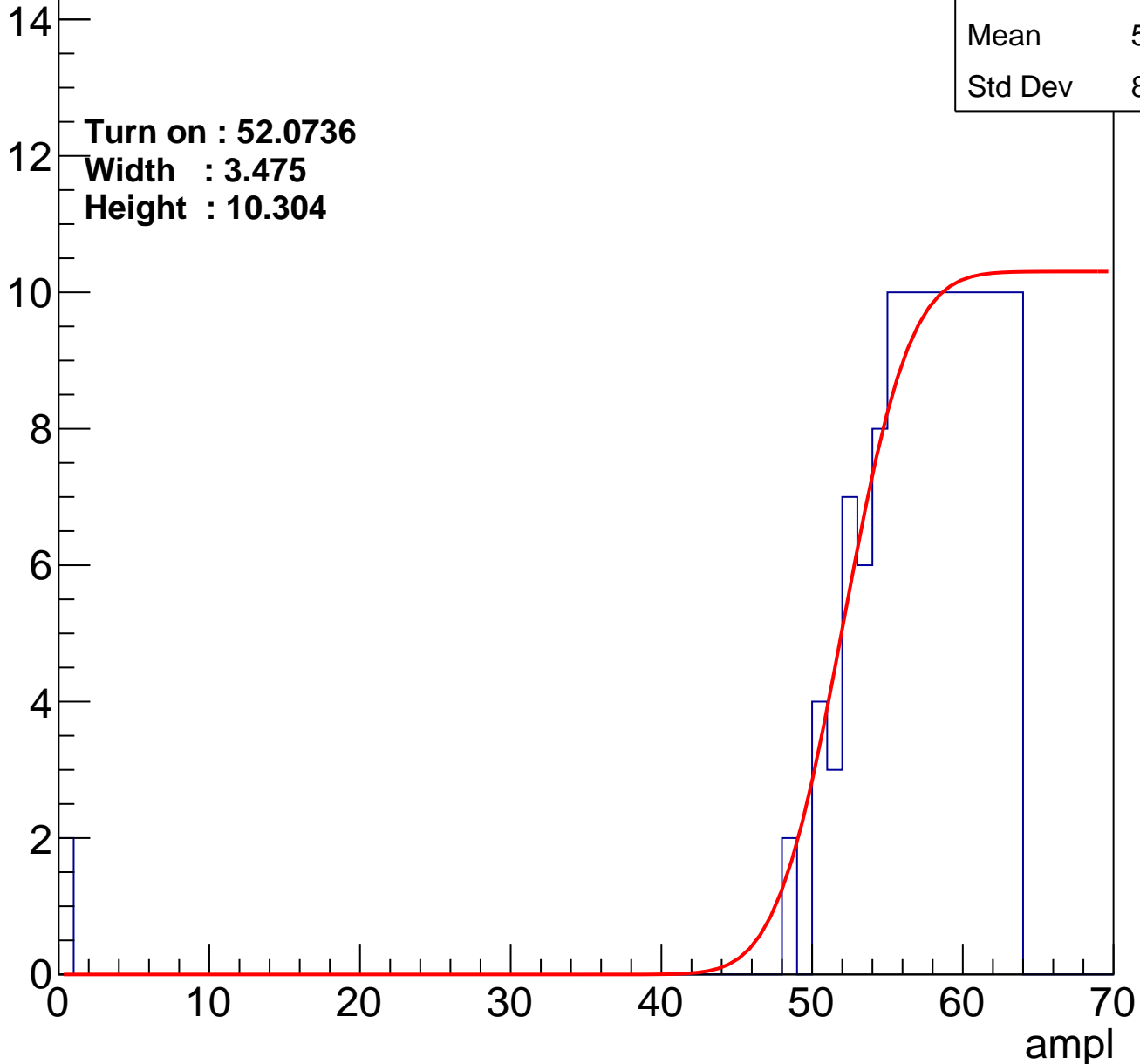
Entries	122
Mean	56.34
Std Dev	8.205

Turn on : 52.0736

Width : 3.475

Height : 10.304

Entry



B0L103S, U22-ch110

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.81
Std Dev	10.33

Turn on : 50.5515

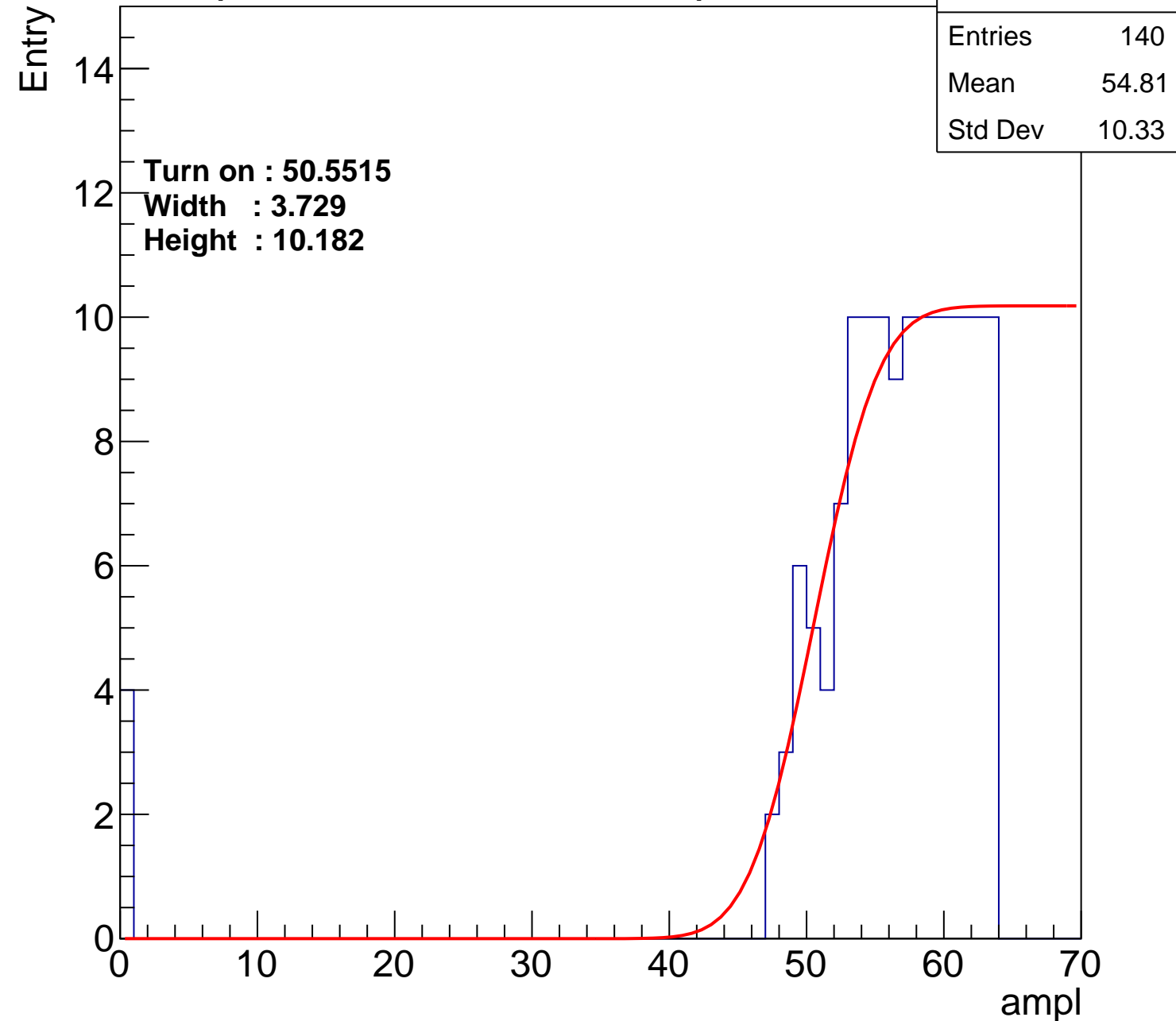
Width : 3.729

Height : 10.182

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch111

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.19
Std Dev	9.311

Turn on : 50.8401

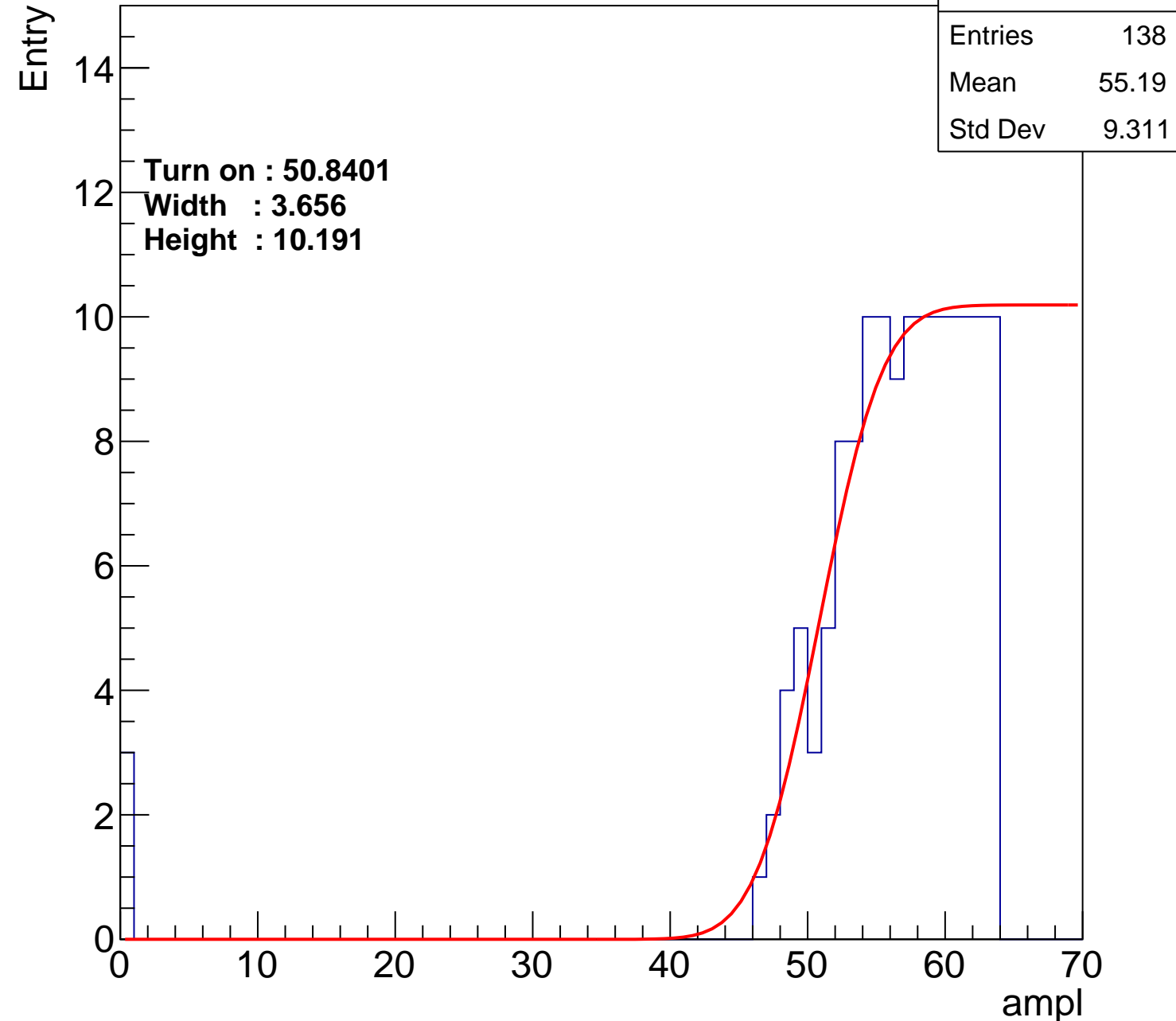
Width : 3.656

Height : 10.191

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch112

calib_packv5_040323_1717.root, FC#2, port C3

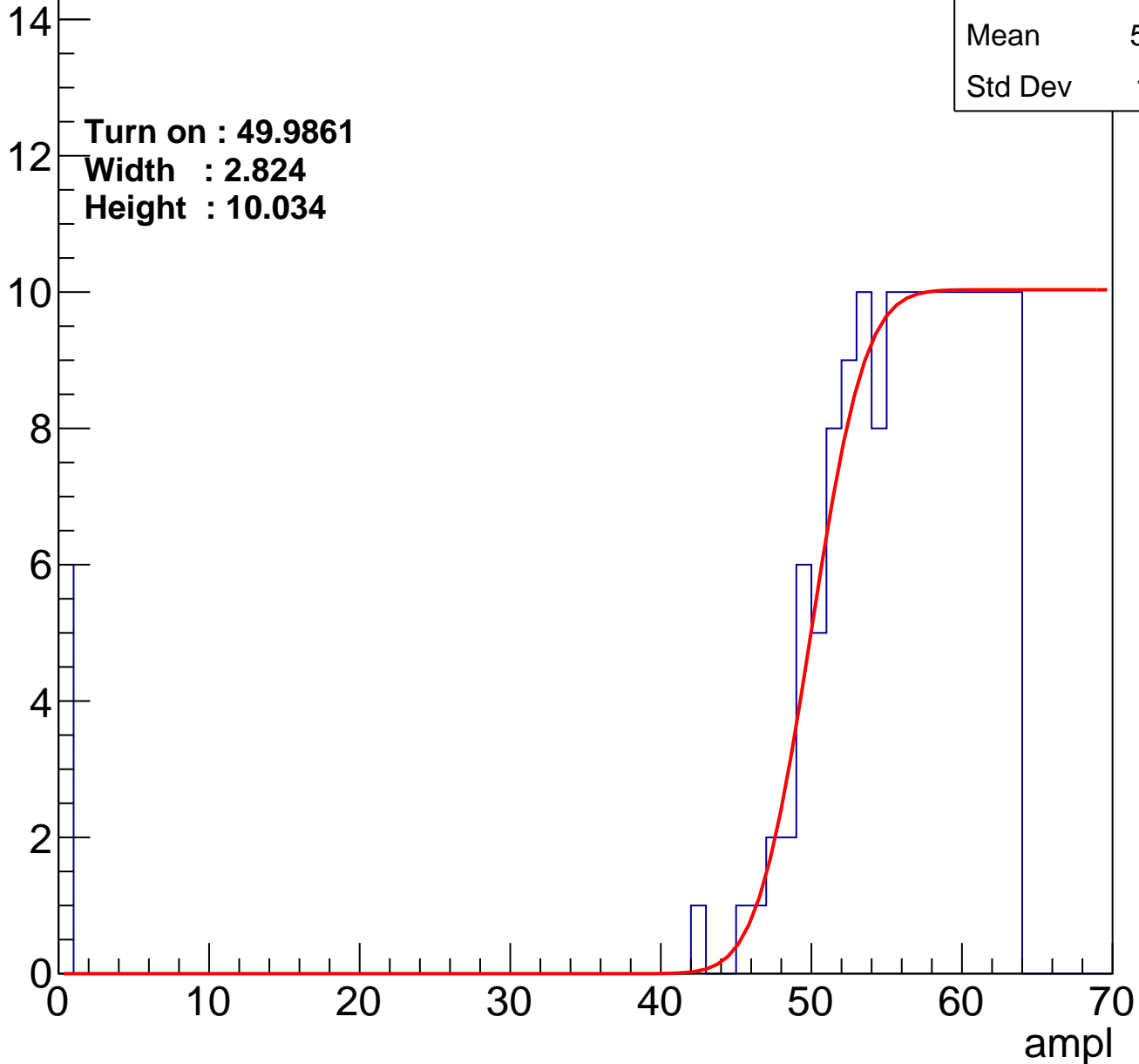
Entries	149
Mean	53.79
Std Dev	11.91

Turn on : 49.9861

Width : 2.824

Height : 10.034

Entry



B0L103S, U22-ch113

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	56.18
Std Dev	9.9

Turn on : 54.0603

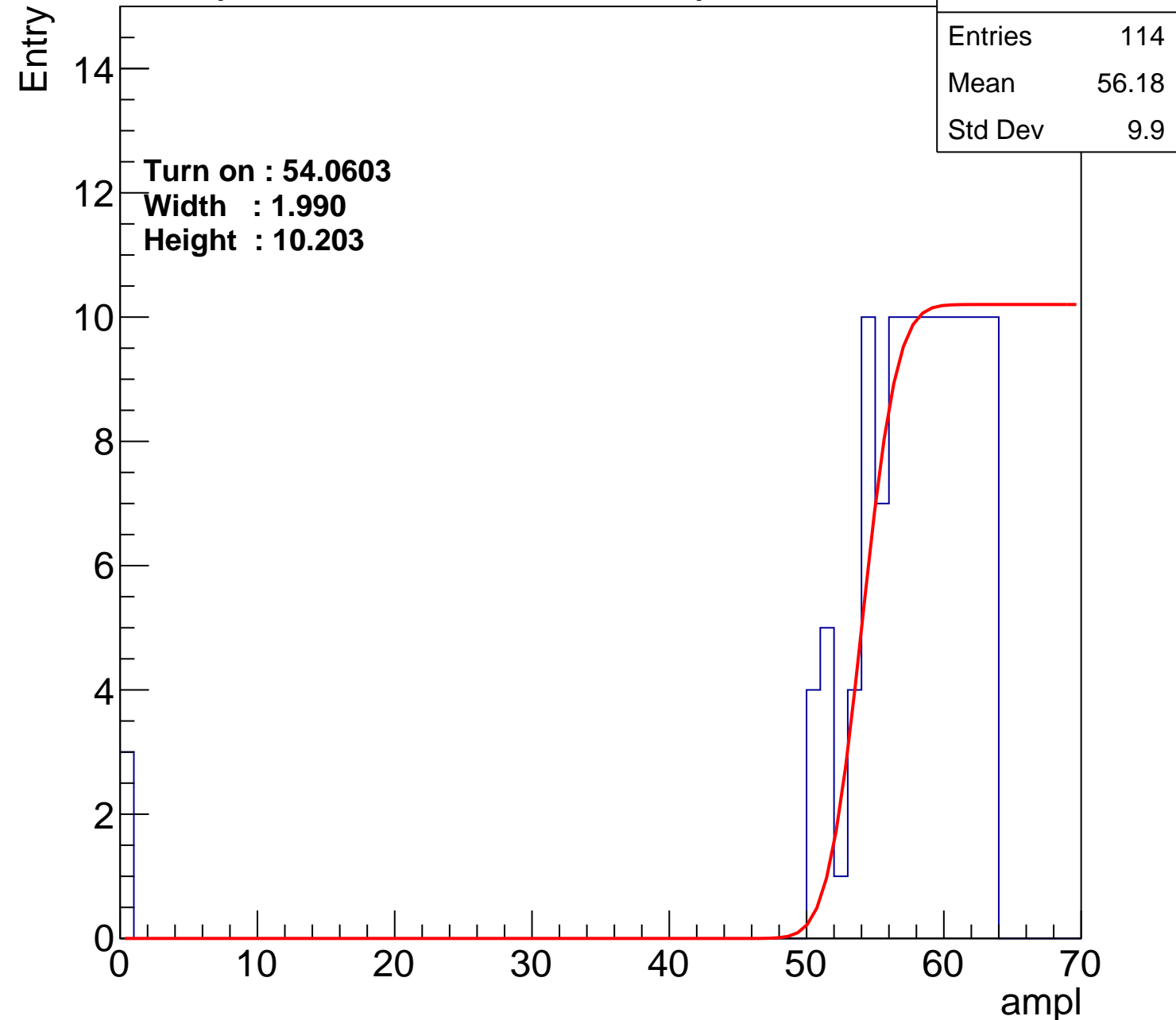
Width : 1.990

Height : 10.203

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch114

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.77
Std Dev	9.664

Turn on : 53.0060

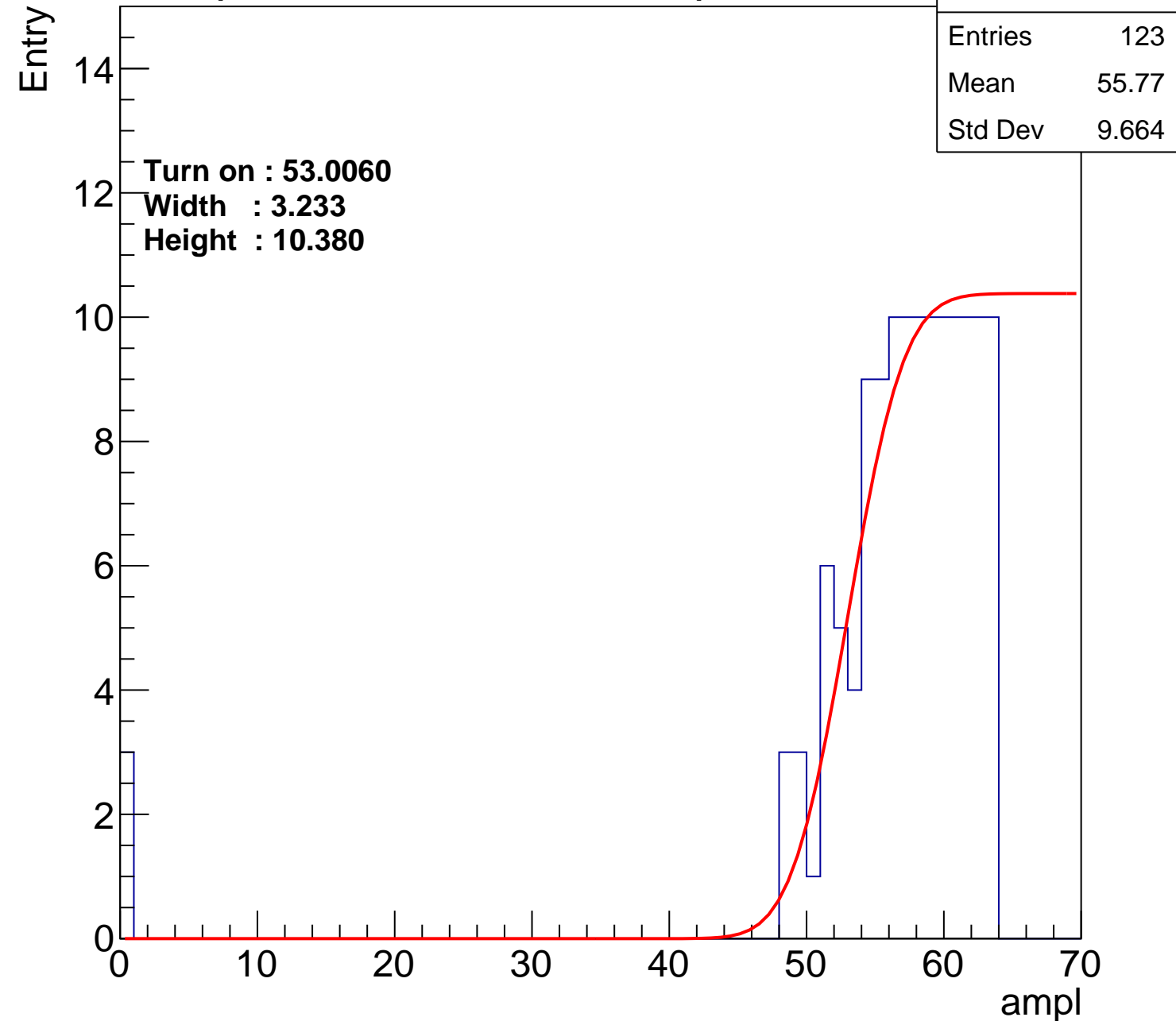
Width : 3.233

Height : 10.380

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch115

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	55.05
Std Dev	10.56

Turn on : 51.2327

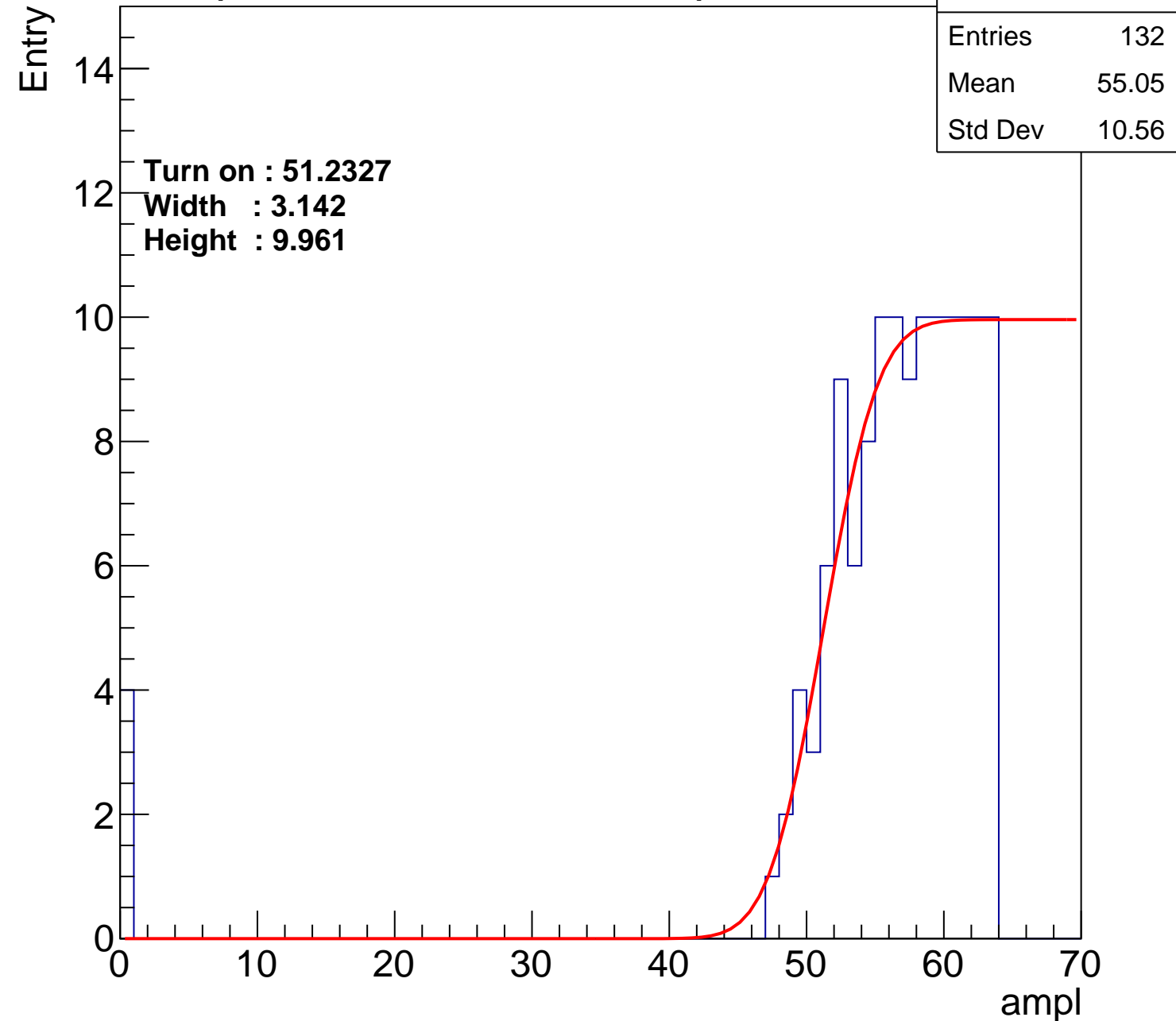
Width : 3.142

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch116

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.66
Std Dev	8.141

Turn on : 51.2833

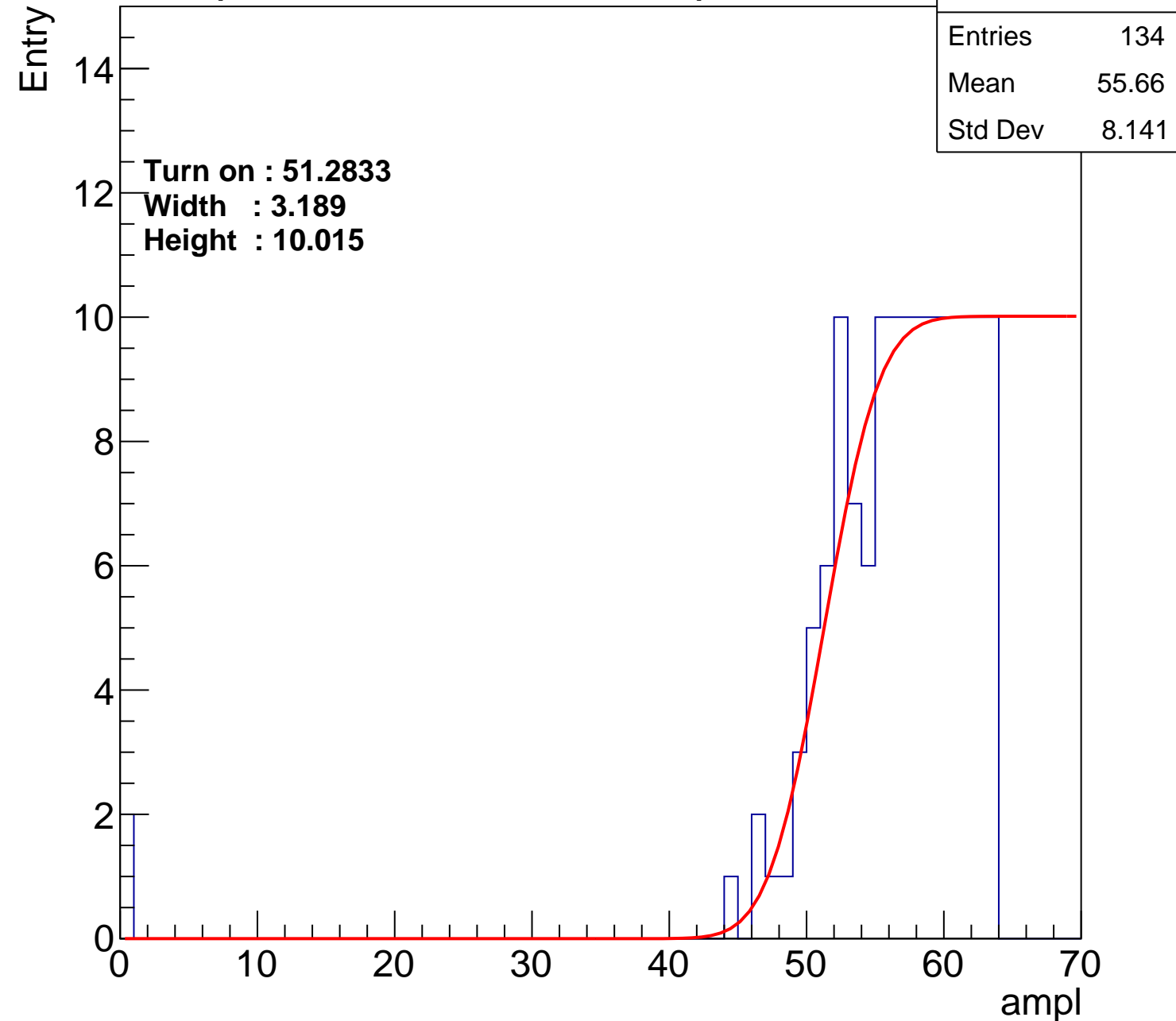
Width : 3.189

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch117

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	54.7
Std Dev	11.91

Turn on : 52.4683

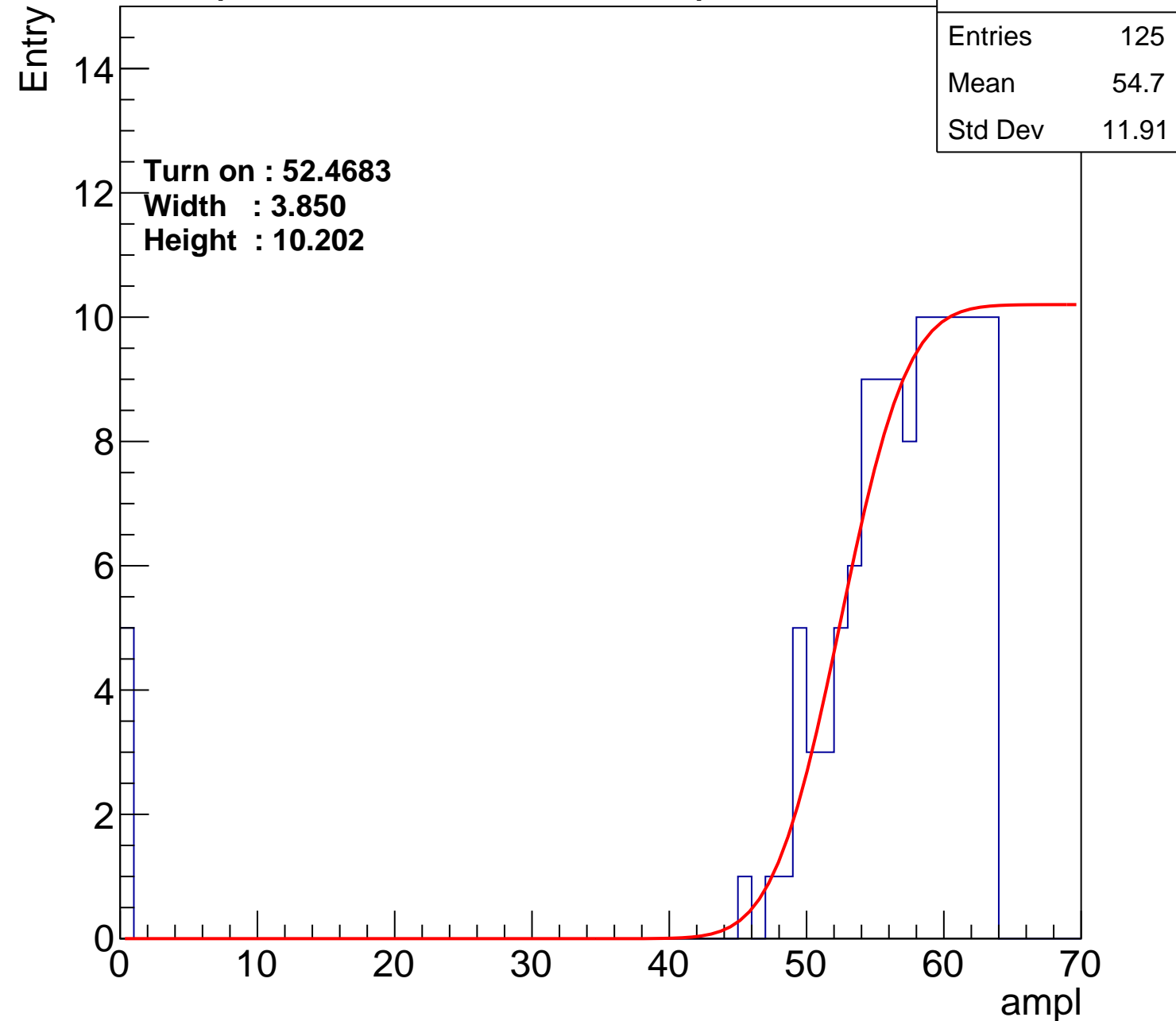
Width : 3.850

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch118

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.38
Std Dev	8.053

Turn on : 50.4568

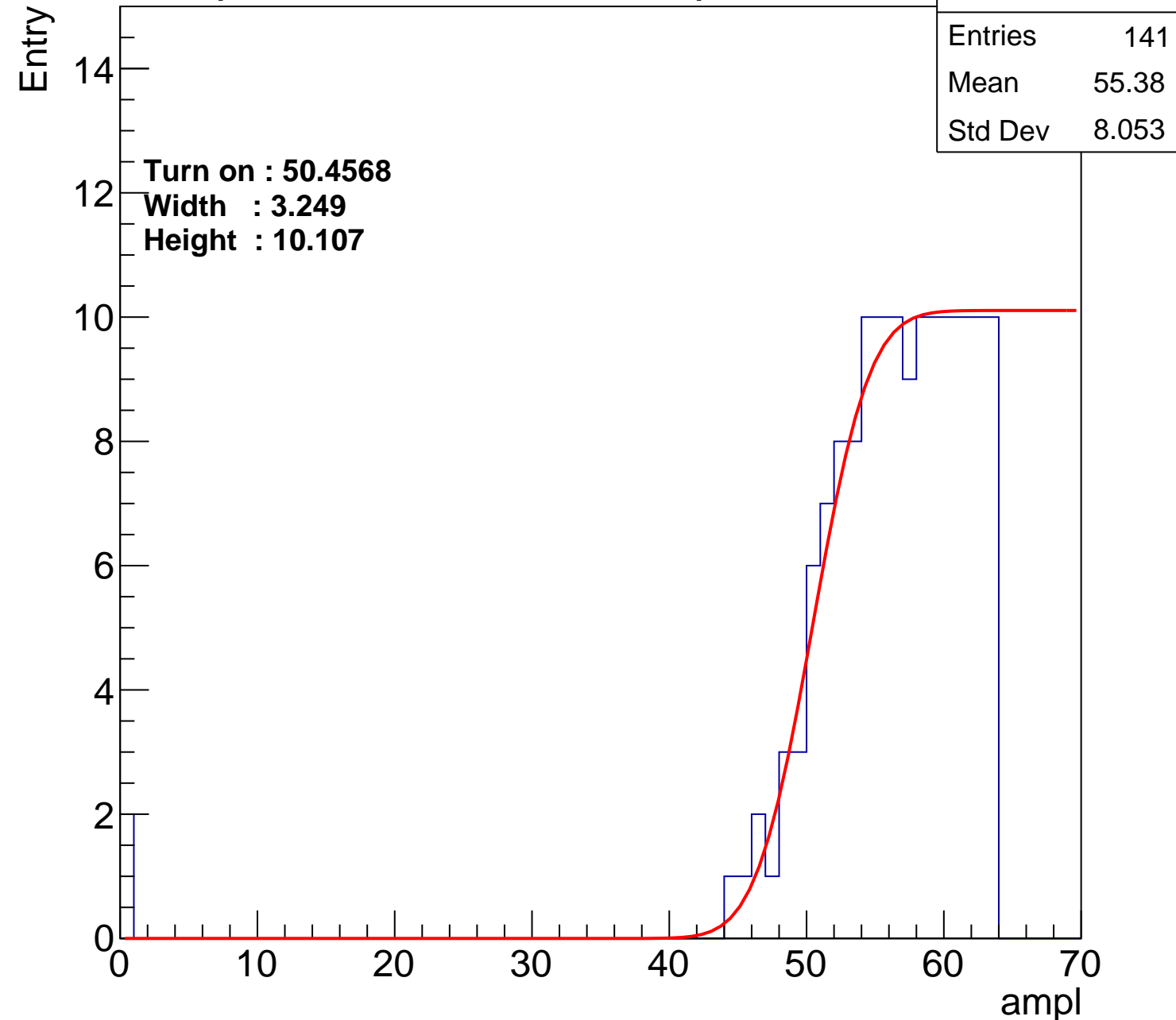
Width : 3.249

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch119

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.84
Std Dev	6.58

Turn on : 50.4032

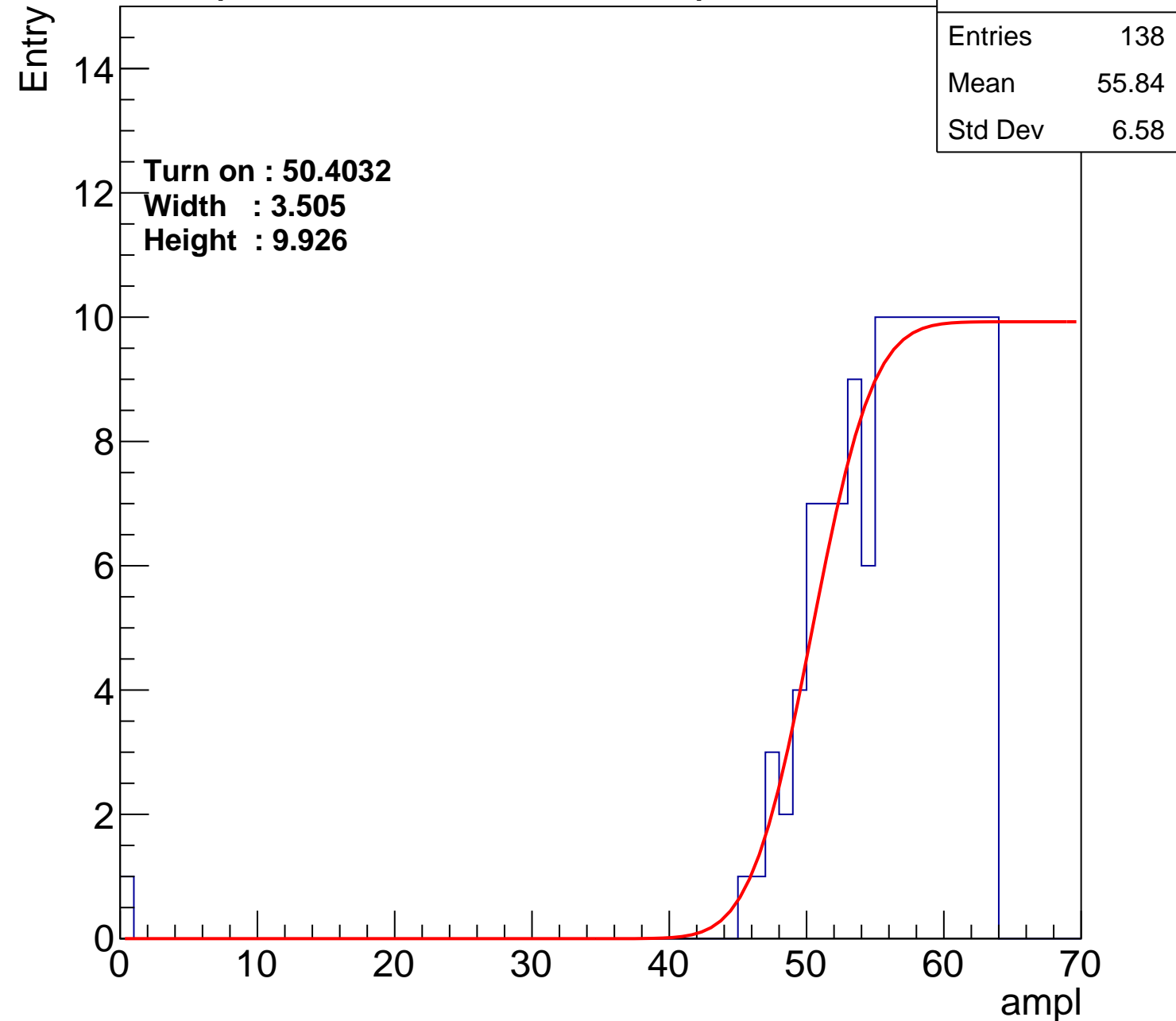
Width : 3.505

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch120

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	55.93
Std Dev	9.872

Turn on : 53.3767

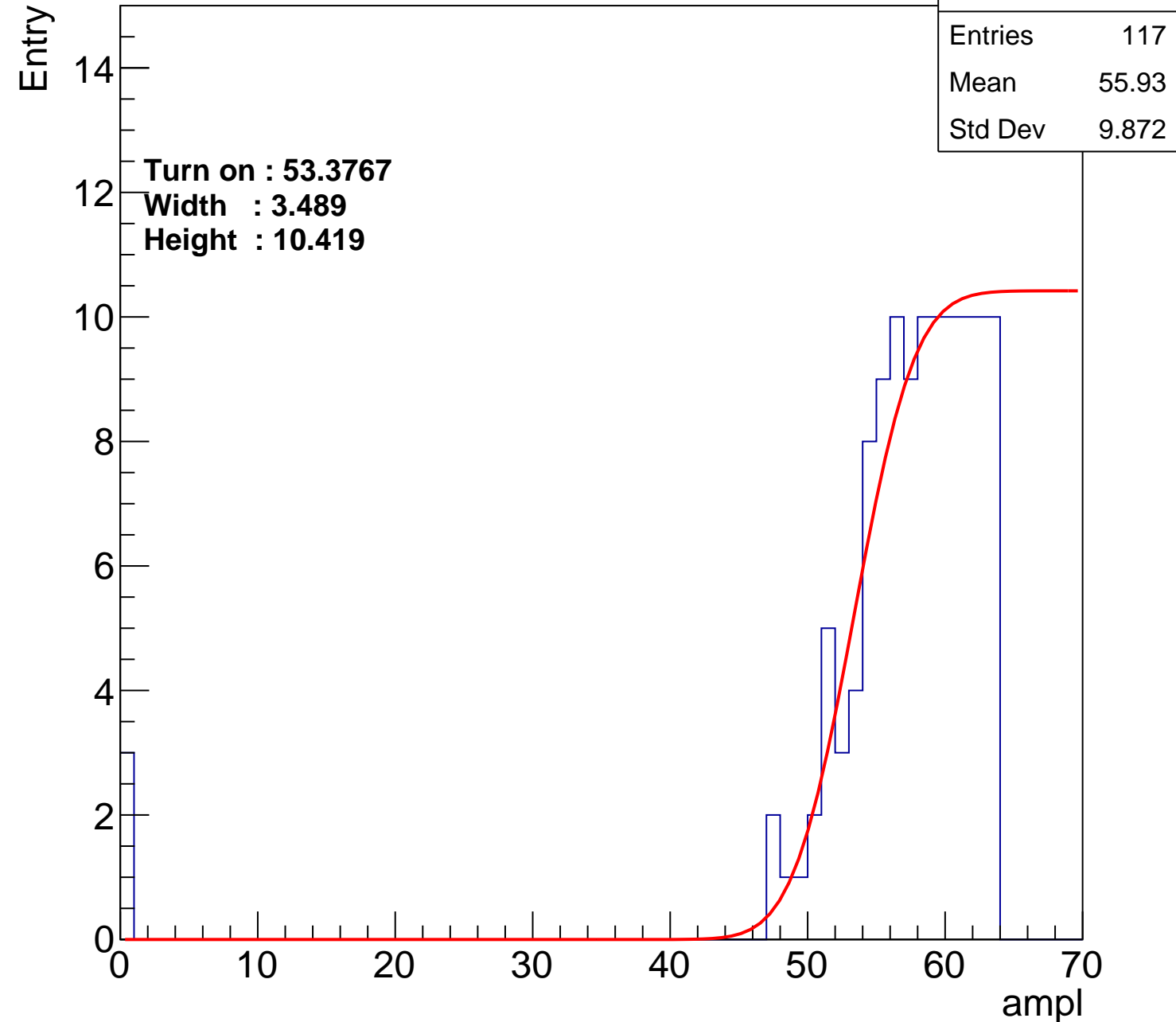
Width : 3.489

Height : 10.419

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch121

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.77
Std Dev	10.37

Turn on : 50.8193

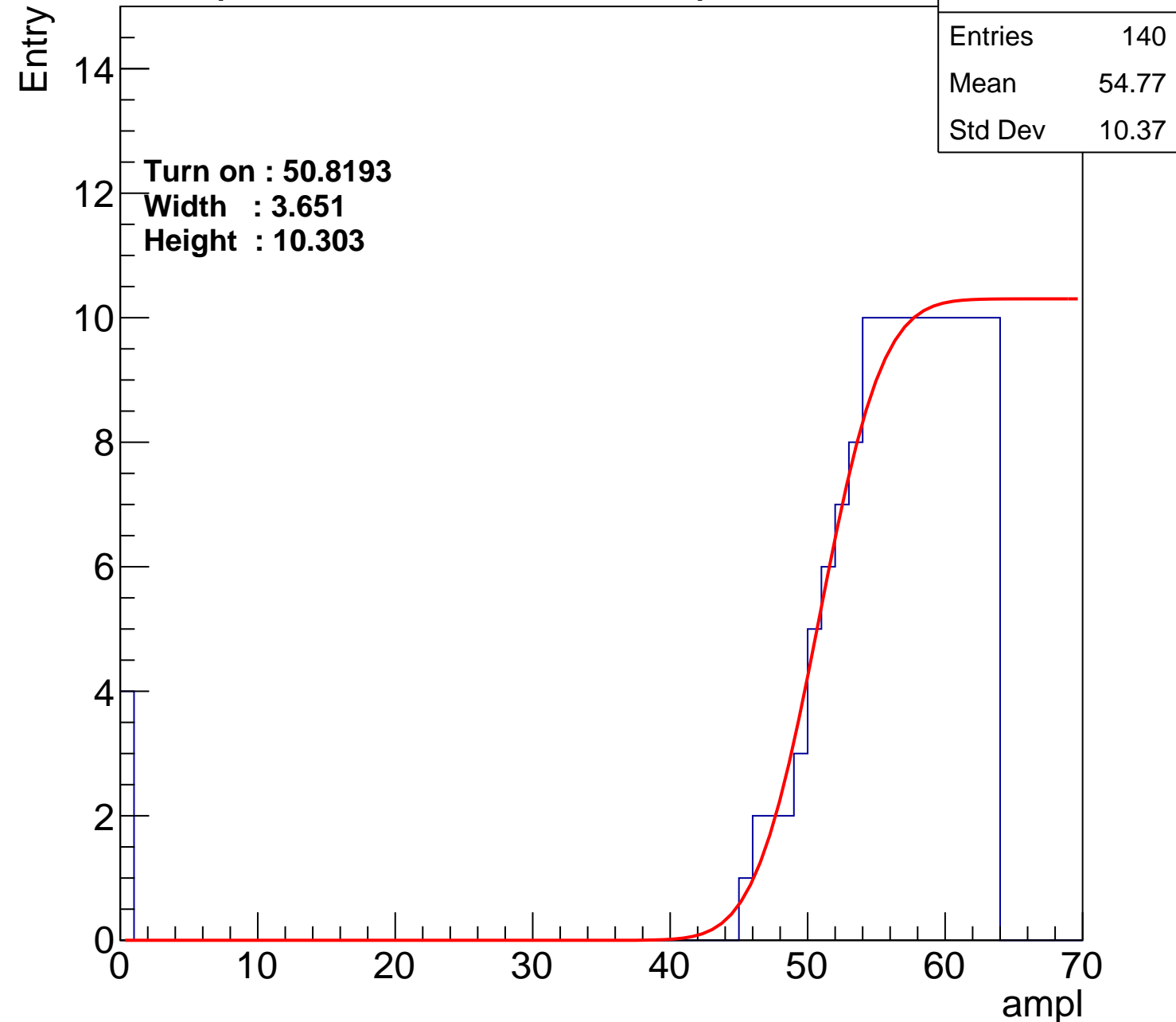
Width : 3.651

Height : 10.303

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch122

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	56.35
Std Dev	6.613

Turn on : 52.3733

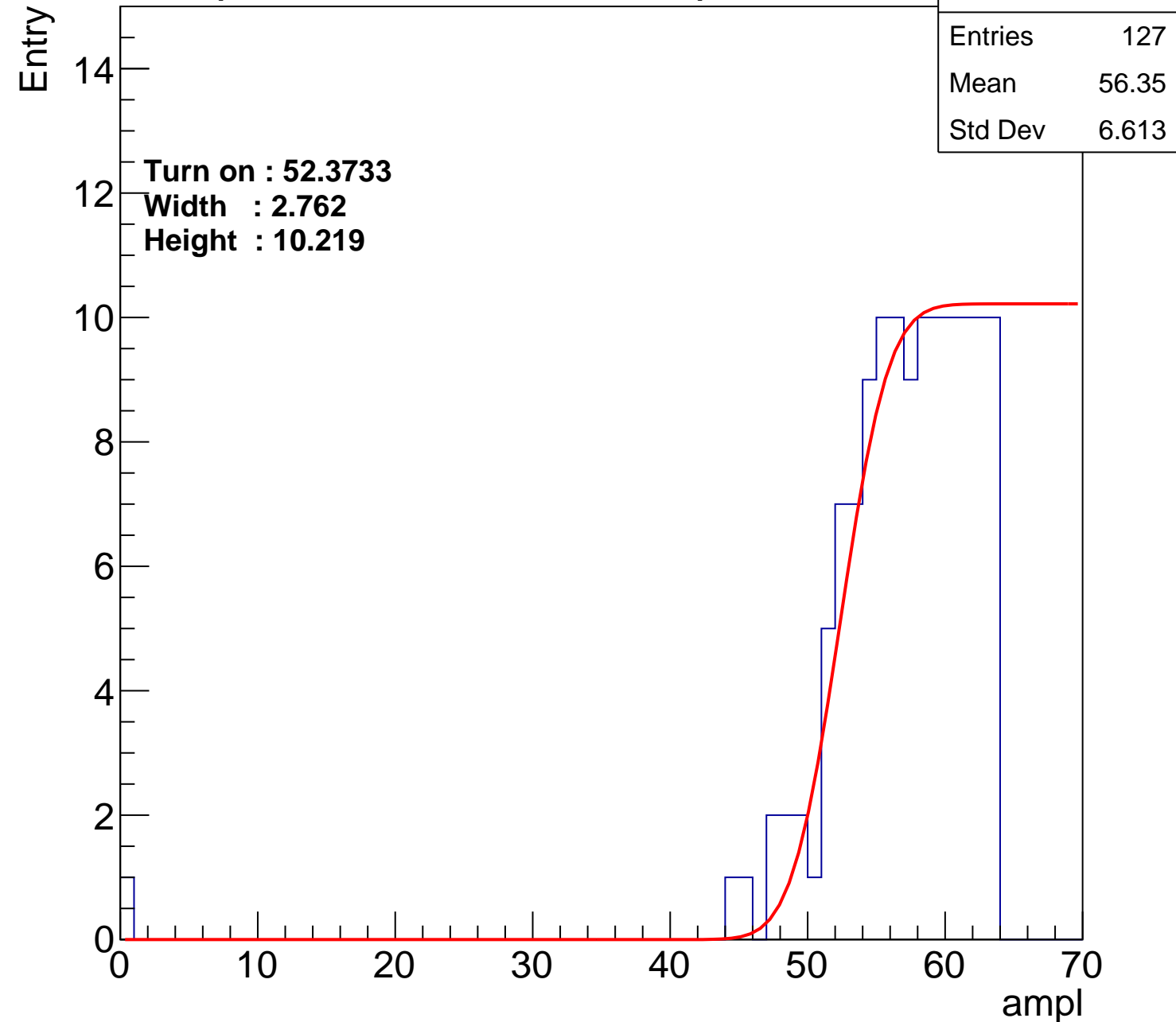
Width : 2.762

Height : 10.219

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch123

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.63
Std Dev	9.522

Turn on : 51.8773

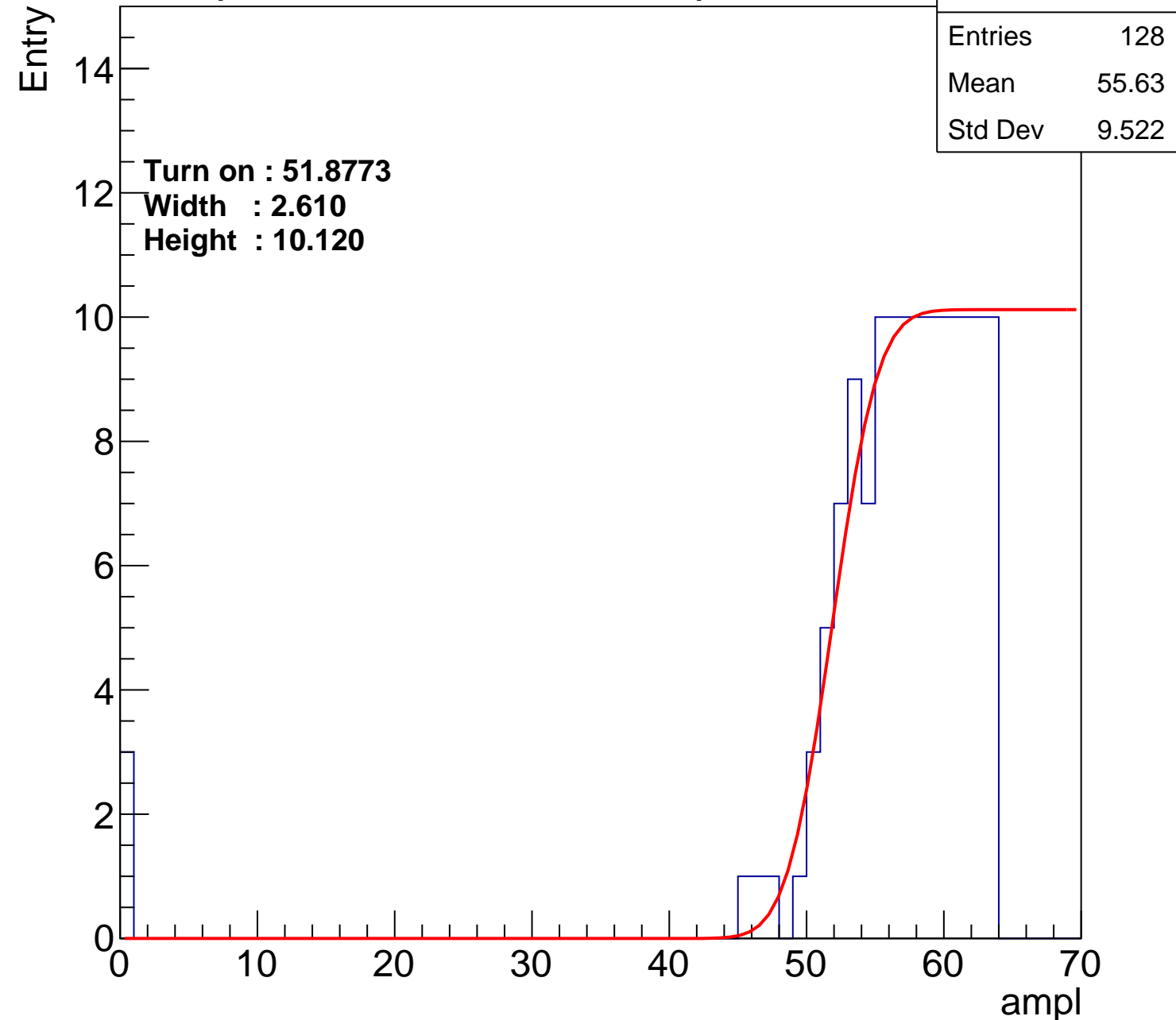
Width : 2.610

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch124

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	55.02
Std Dev	12.09

Turn on : 53.4877

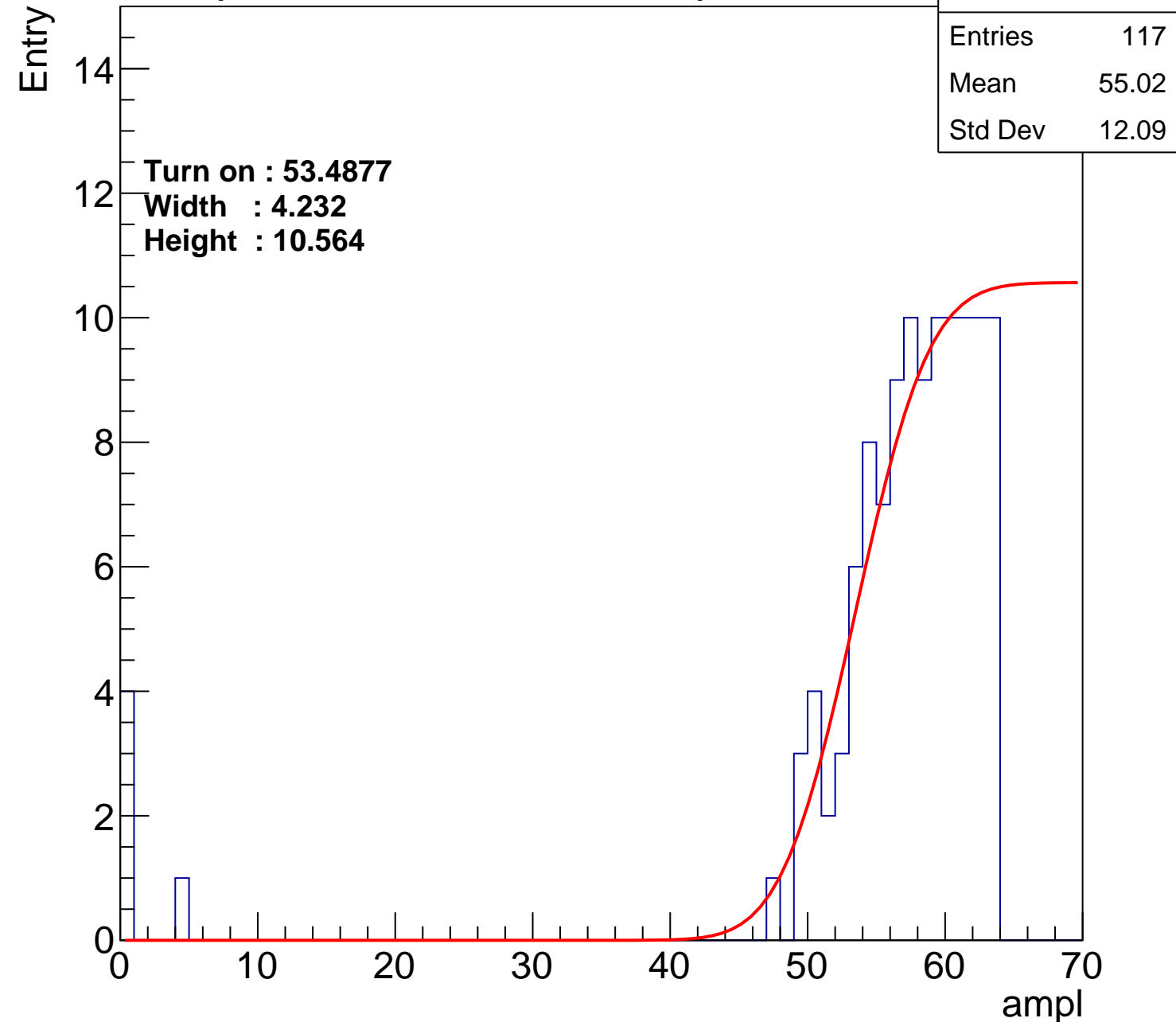
Width : 4.232

Height : 10.564

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch125

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	54.84
Std Dev	11.63

Turn on : 51.7833

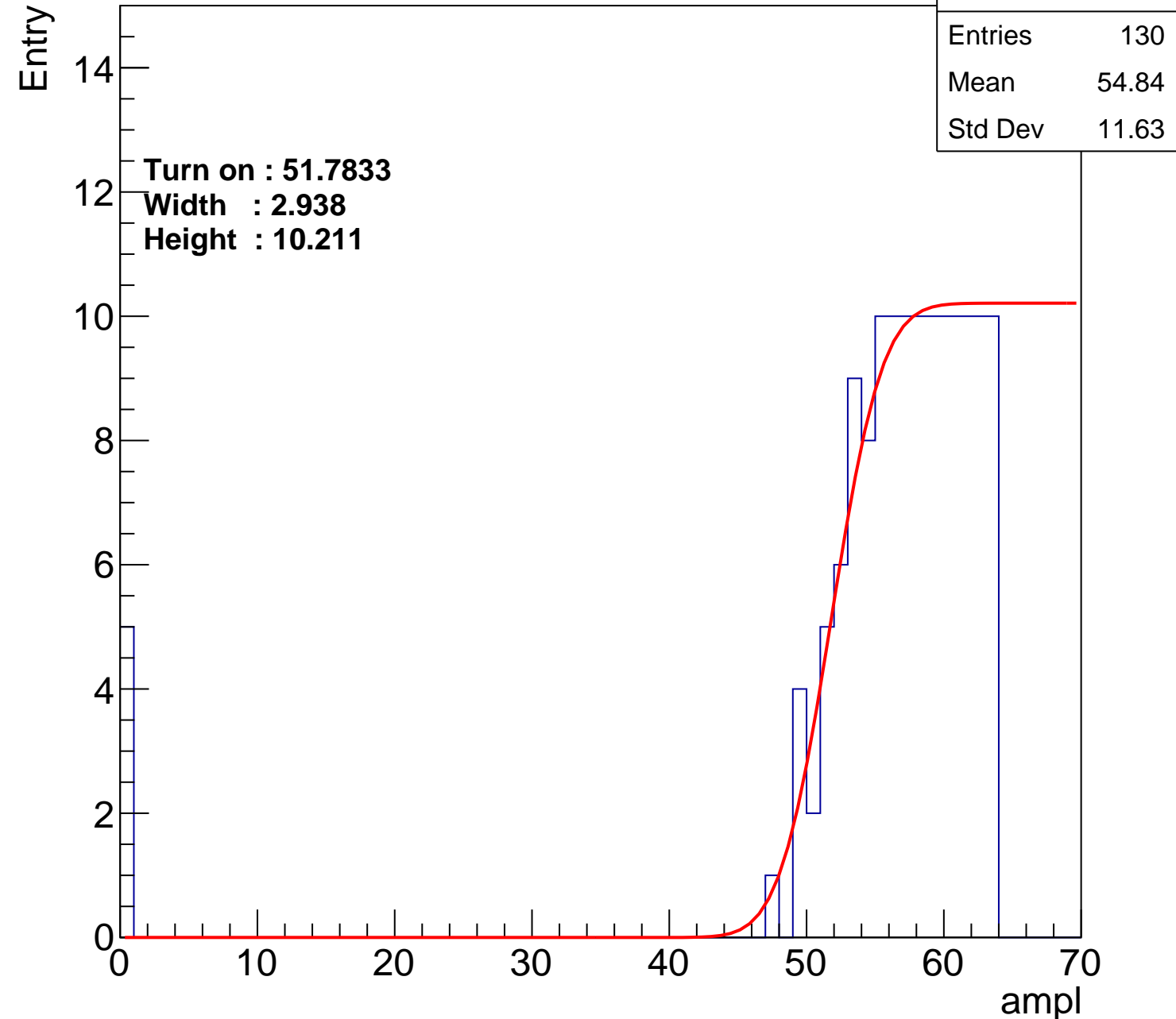
Width : 2.938

Height : 10.211

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch126

calib_packv5_040323_1717.root, FC#2, port C3

Entries	157
Mean	54.4
Std Dev	9.034

Turn on : 48.3909

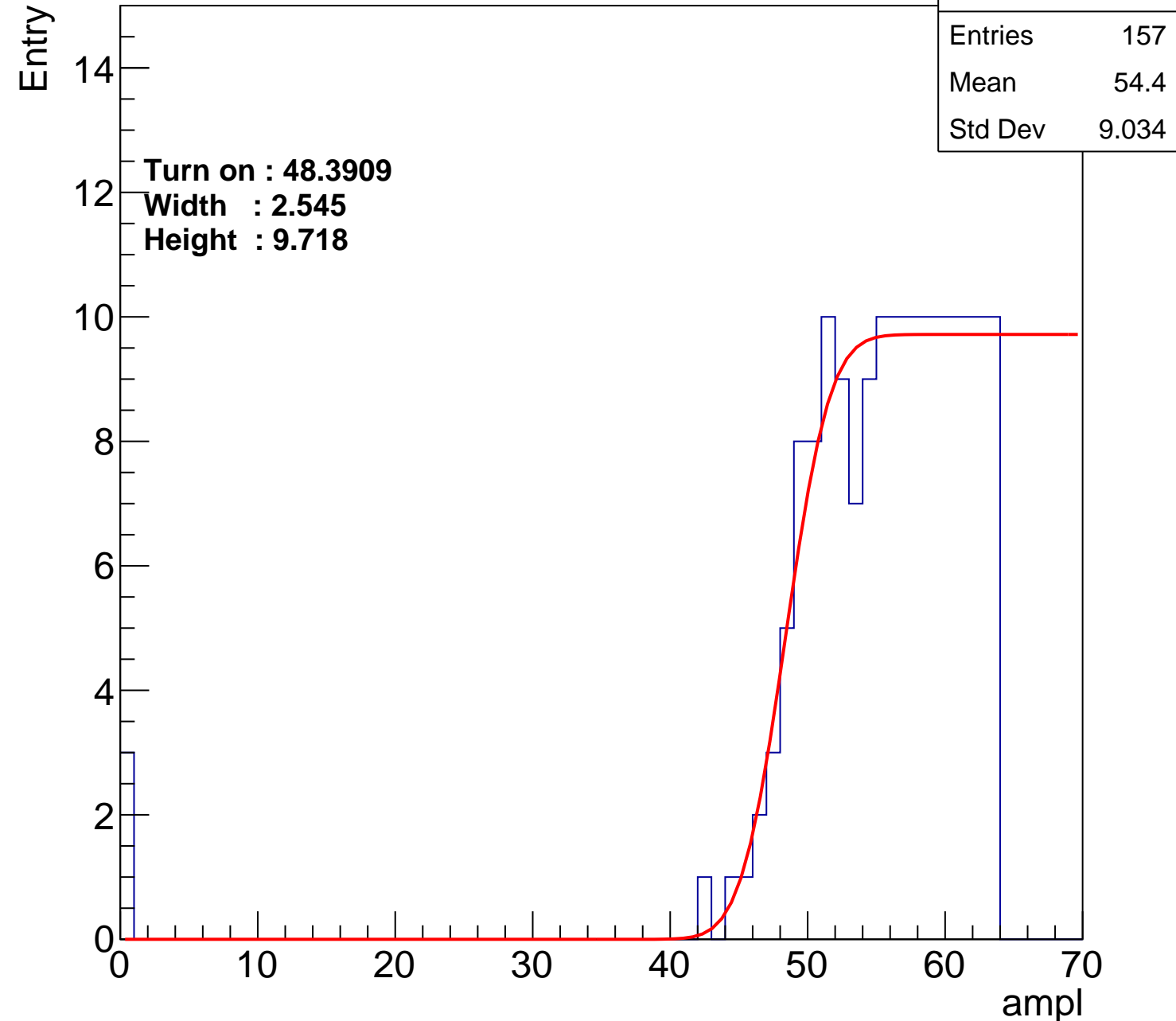
Width : 2.545

Height : 9.718

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.82
Std Dev	9.634

Turn on : 52.3887

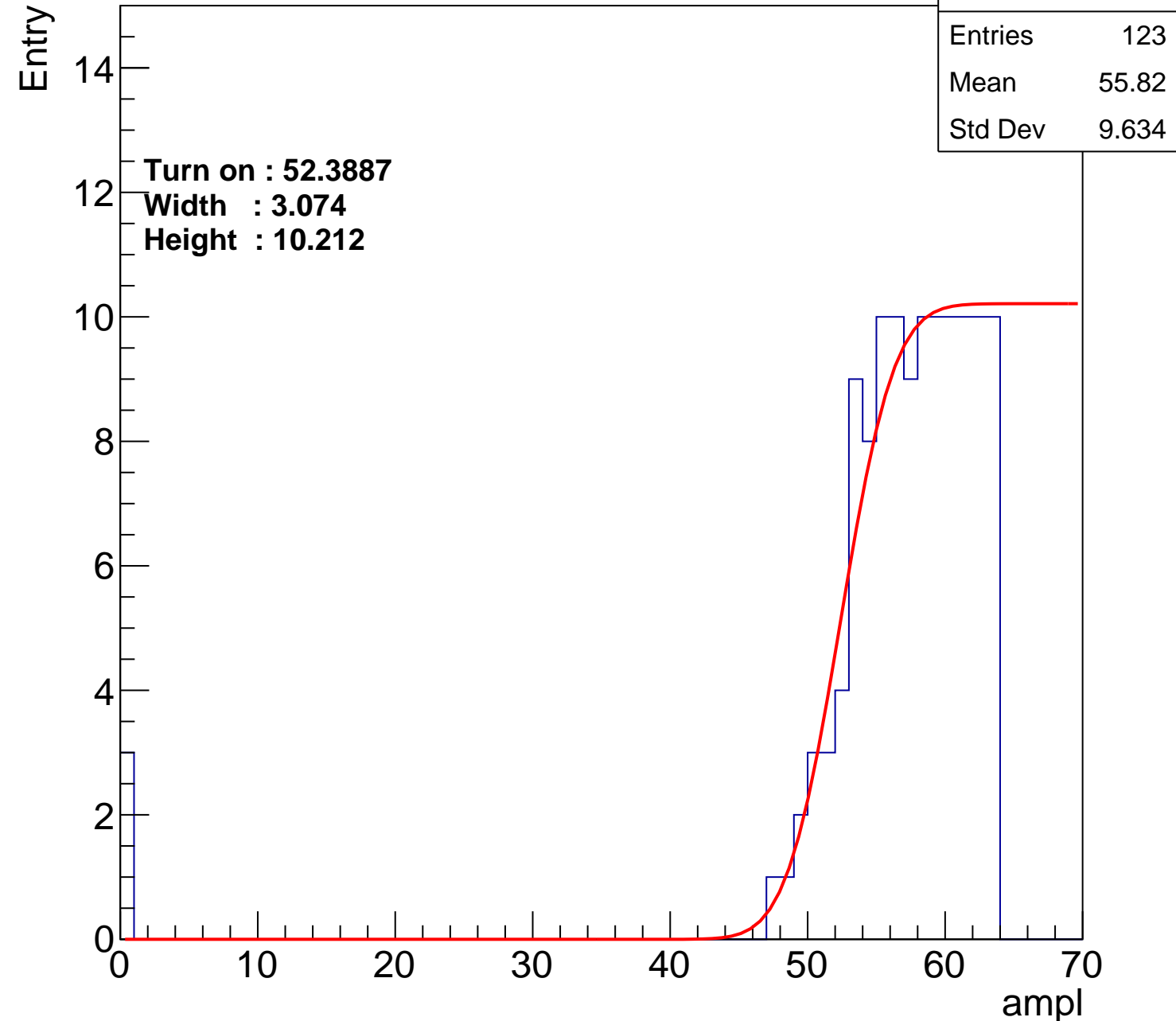
Width : 3.074

Height : 10.212

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U22-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.82
Std Dev	9.634

Turn on : 52.3887

Width : 3.074

Height : 10.212

Entry

14
12
10
8
6
4
2
0

ampl

