



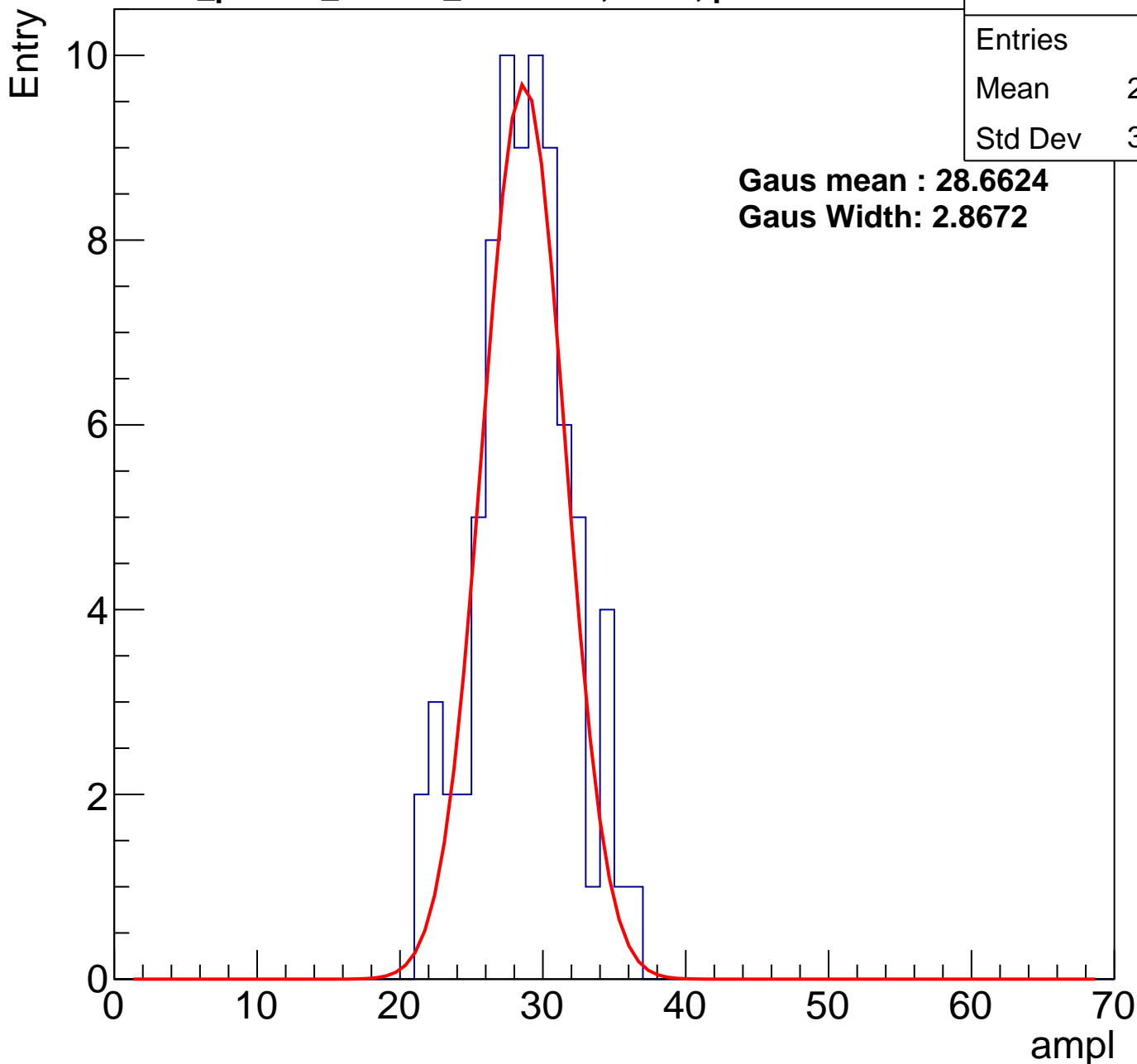
# B1L000S, U2-ch0, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	28.24
Std Dev	3.267

**Gaus mean : 28.6624**

**Gaus Width: 2.8672**



# B1L000S, U2-ch0, adc1

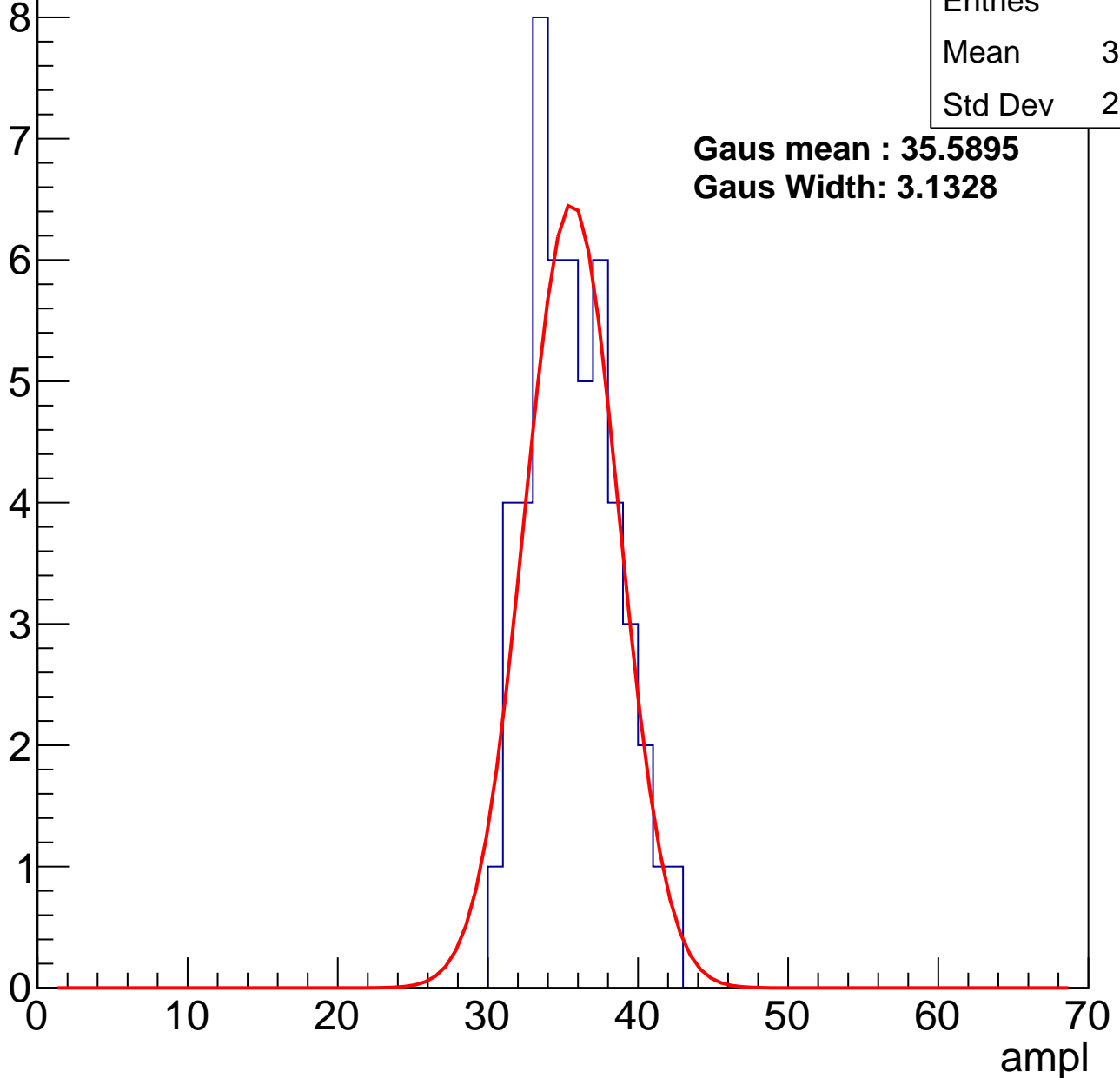
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	35.18
Std Dev	2.833

**Gaus mean : 35.5895**

**Gaus Width: 3.1328**



# B1L000S, U2-ch0, adc2

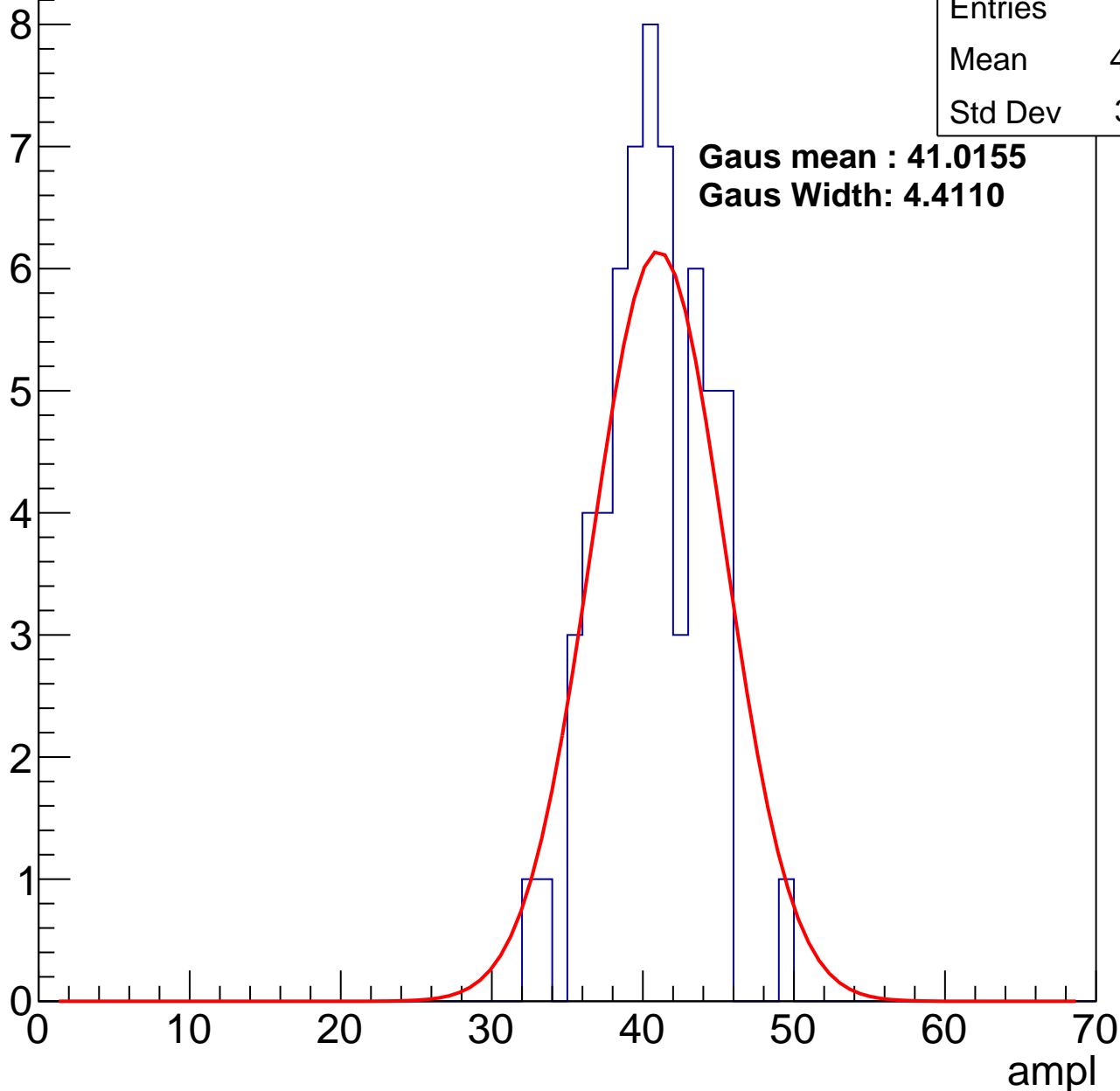
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	40.13
Std Dev	3.331

**Gaus mean : 41.0155**

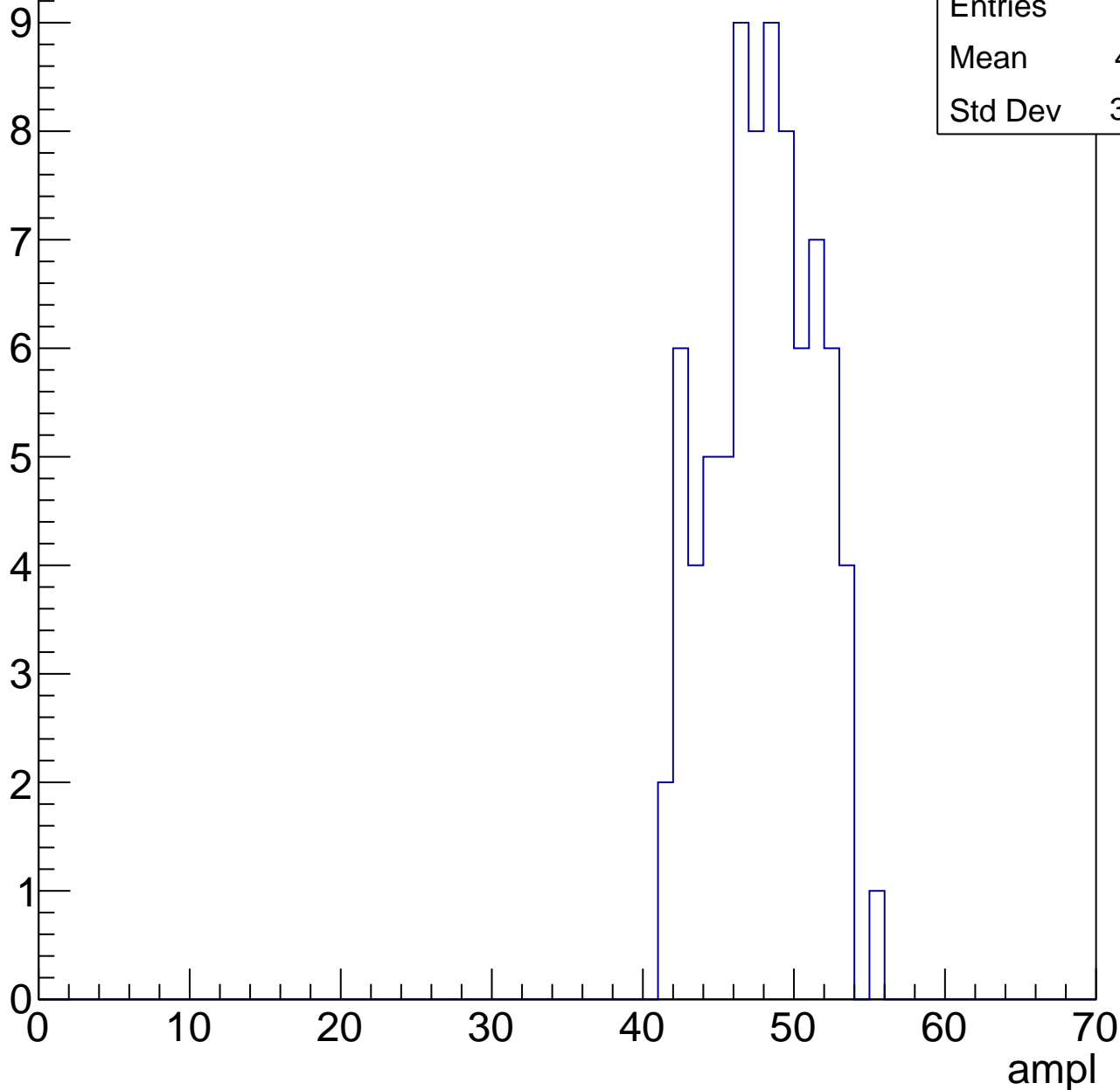
**Gaus Width: 4.4110**



# B1L000S, U2-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

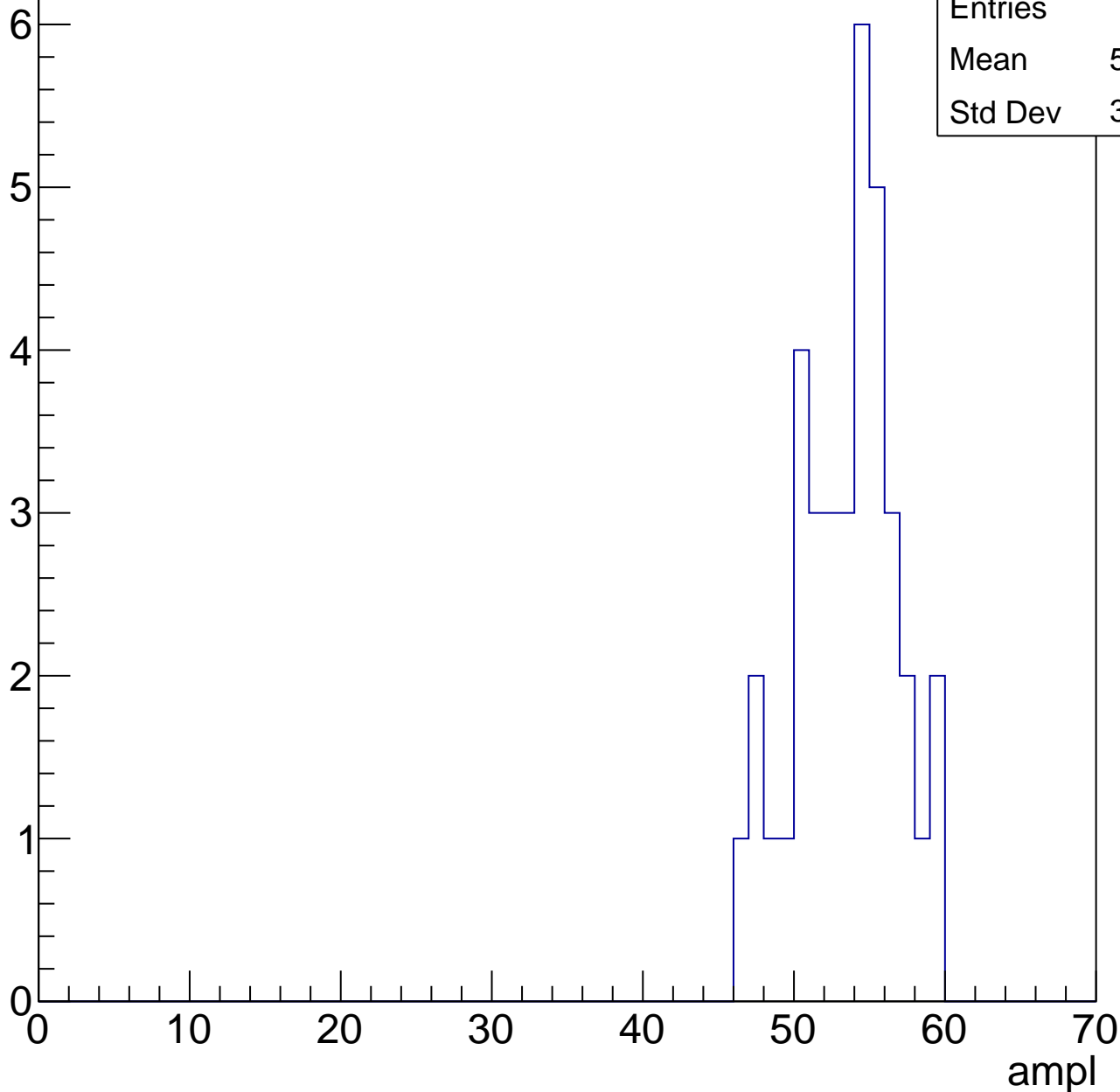


# B1L000S, U2-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	37
Mean	53.03
Std Dev	3.267

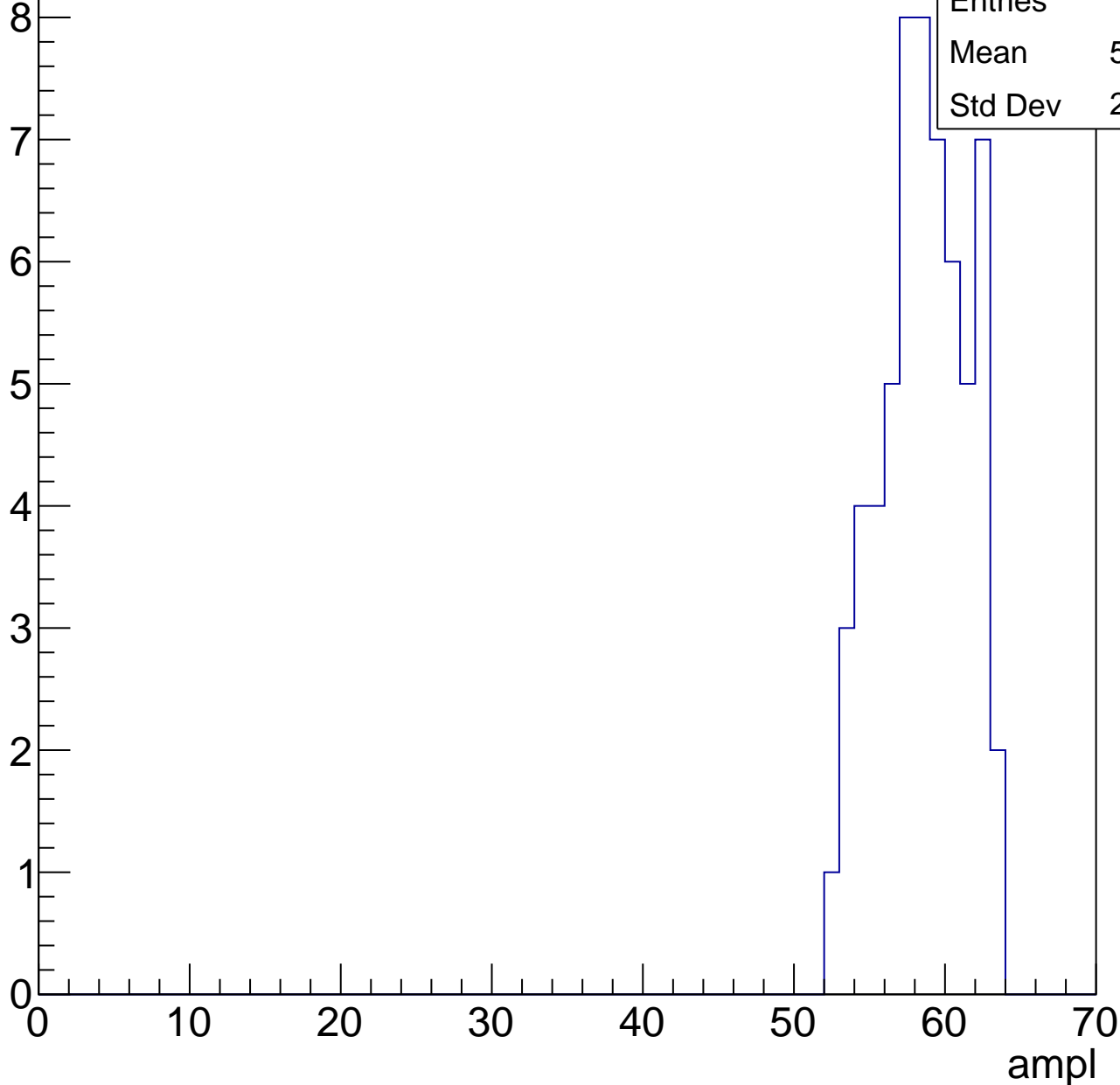


# B1L000S, U2-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	58.08
Std Dev	2.818

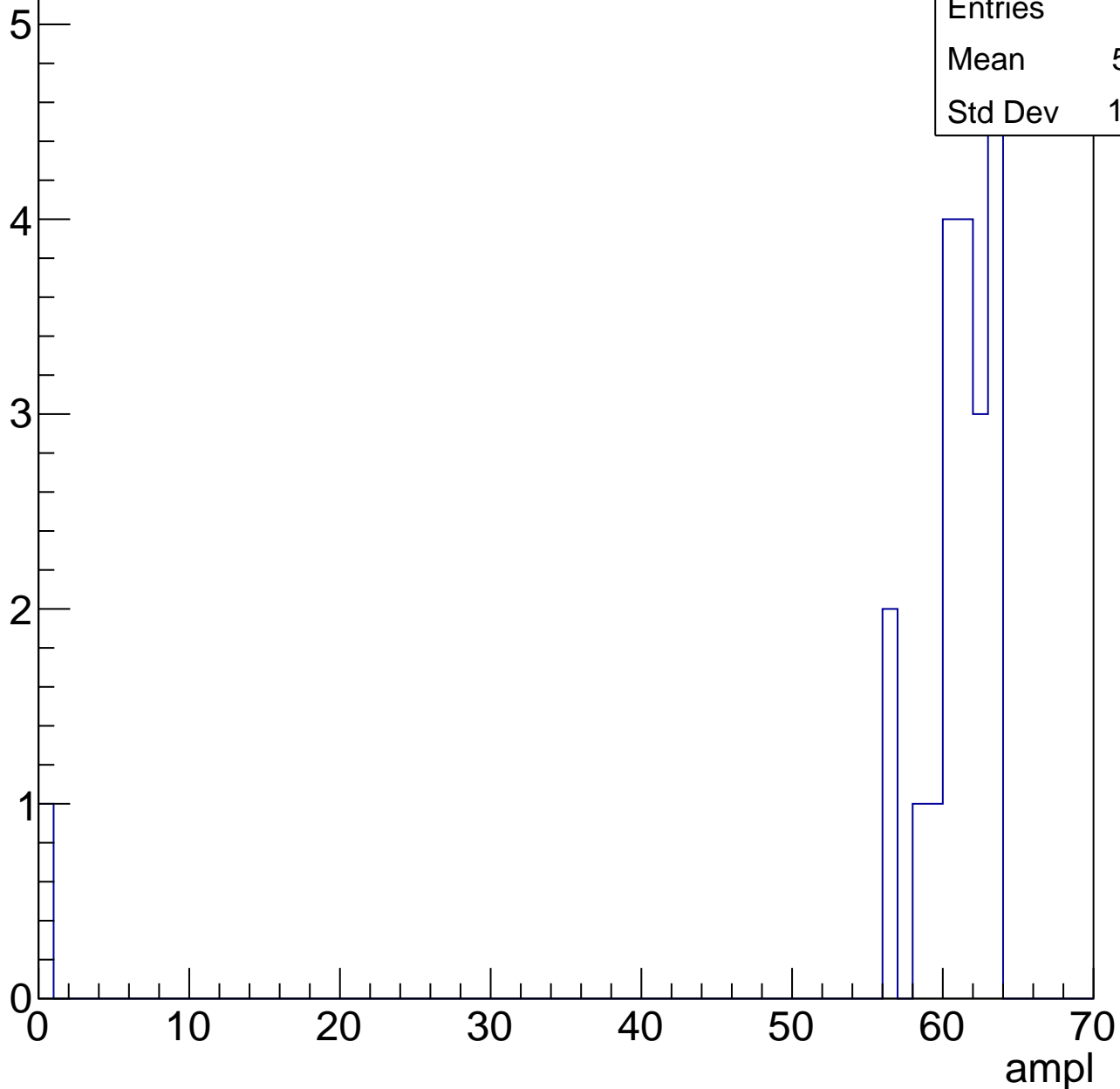


# B1L000S, U2-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	57.81
Std Dev	13.09

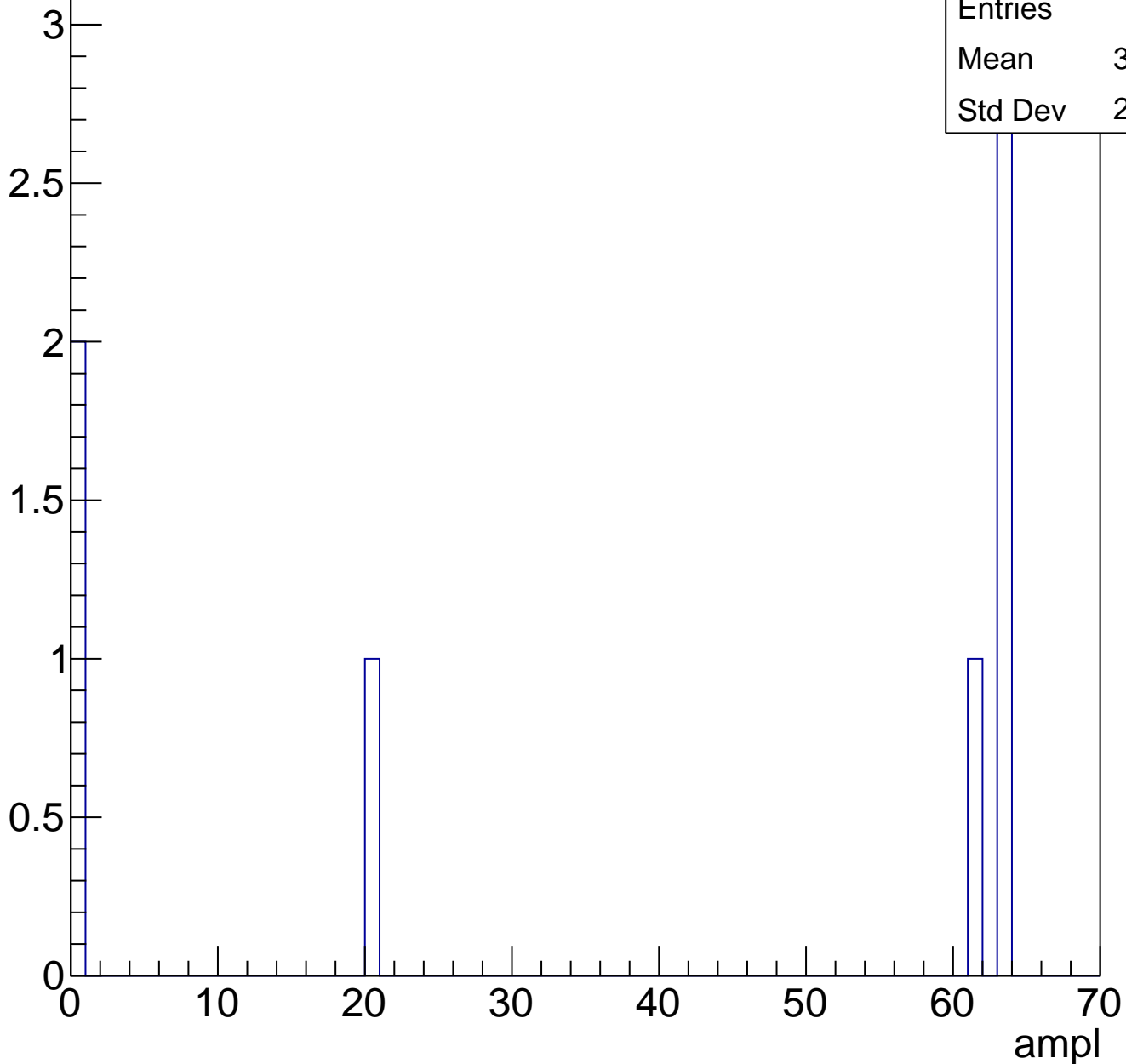




# B1L000S, U2-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	7
Mean	38.57
Std Dev	28.32

# B1L000S, U2-ch1, adc0

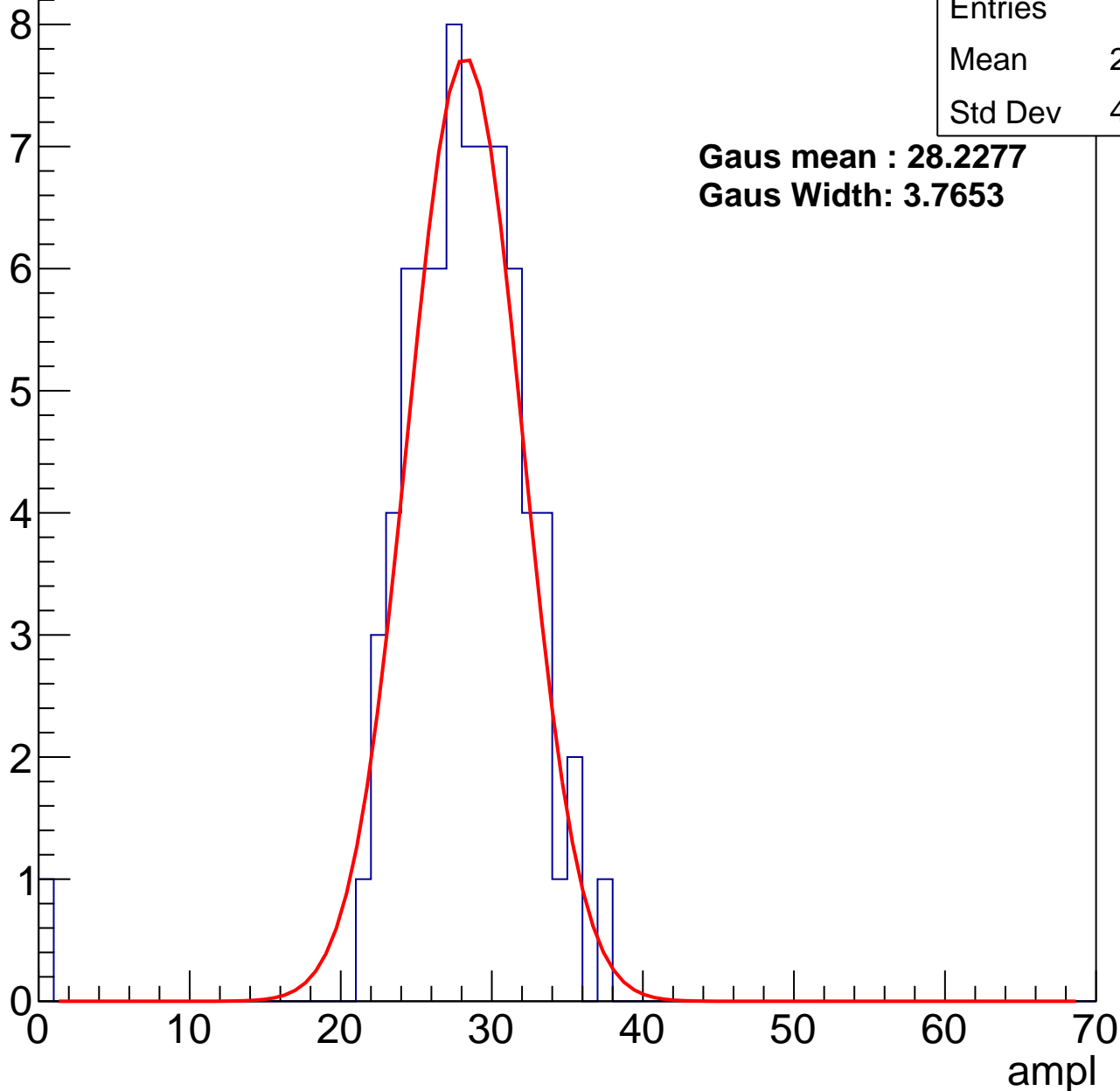
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	27.58
Std Dev	4.762

**Gaus mean : 28.2277**

**Gaus Width: 3.7653**



# B1L000S, U2-ch1, adc1

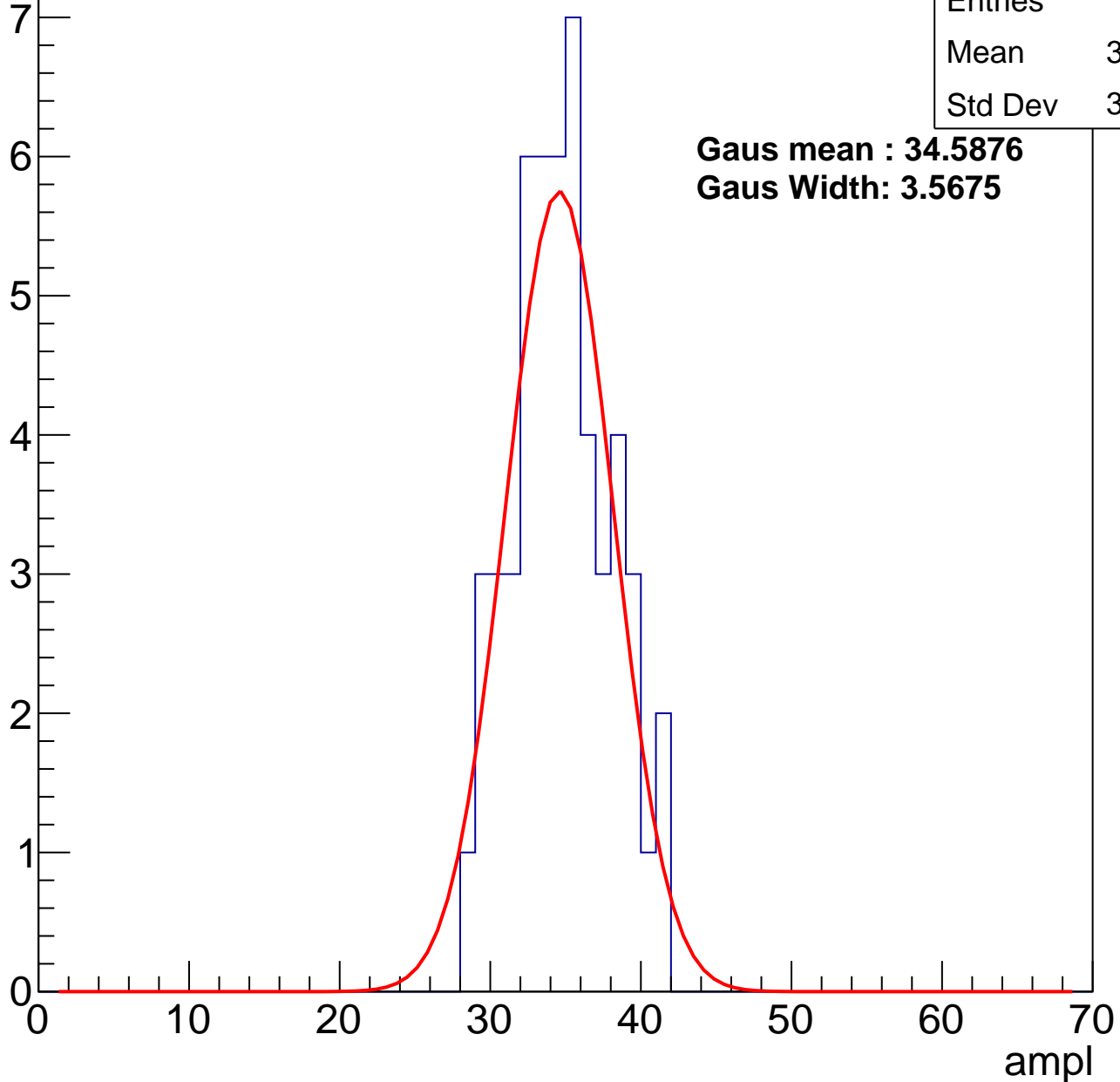
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	34.29
Std Dev	3.207

**Gaus mean : 34.5876**

**Gaus Width: 3.5675**



# B1L000S, U2-ch1, adc2

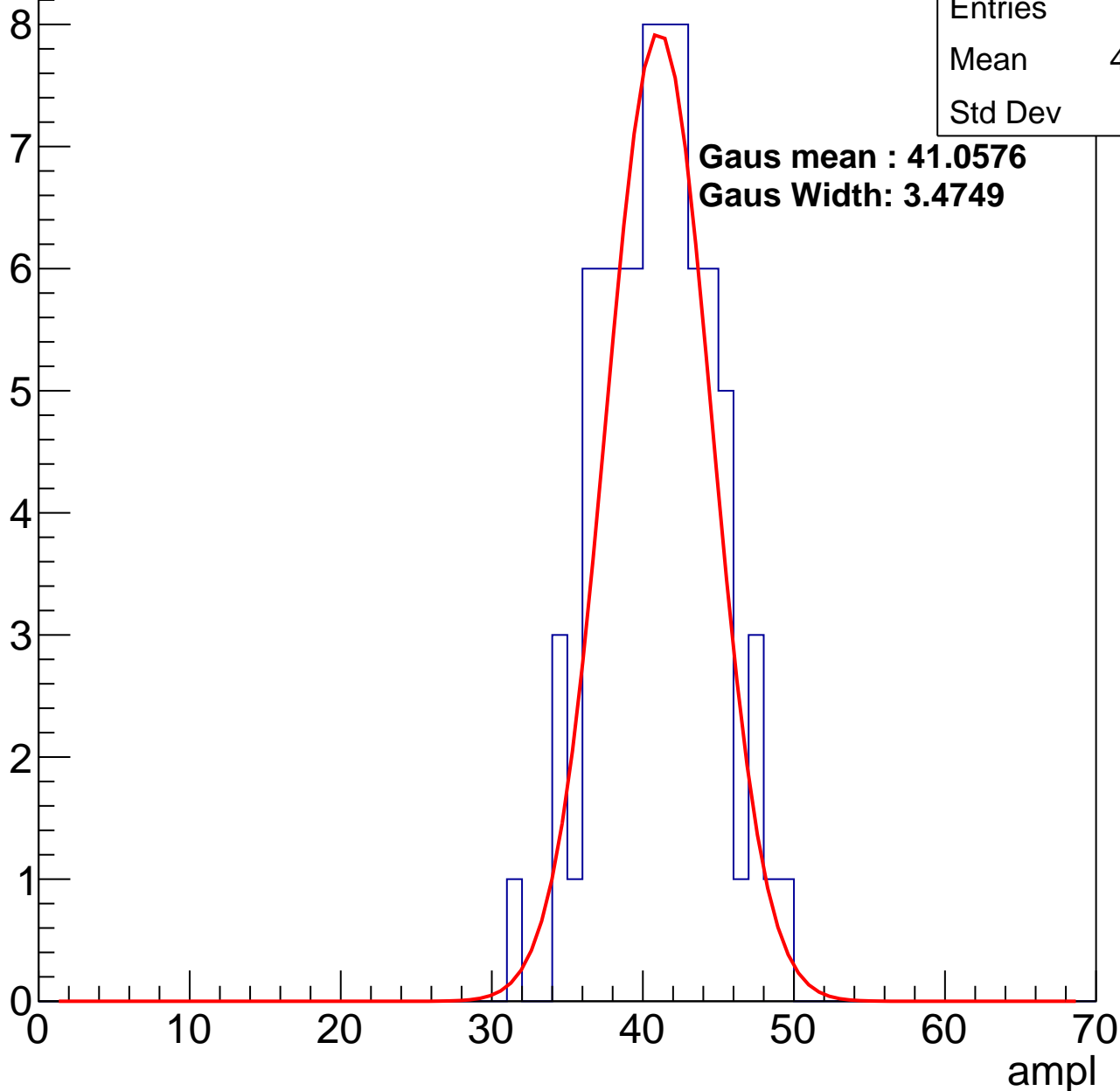
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	40.57
Std Dev	3.65

**Gaus mean : 41.0576**

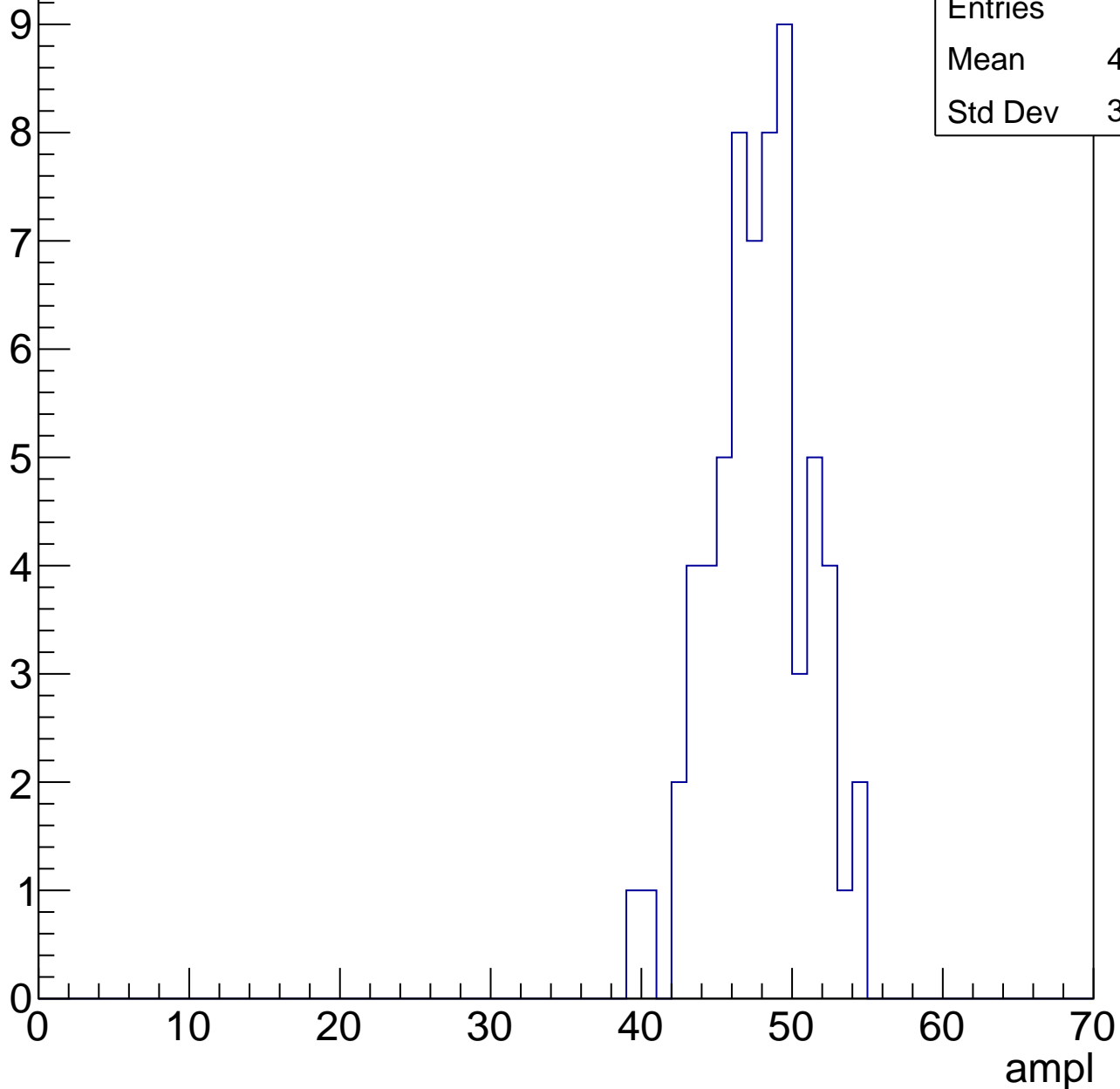
**Gaus Width: 3.4749**



# B1L000S, U2-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



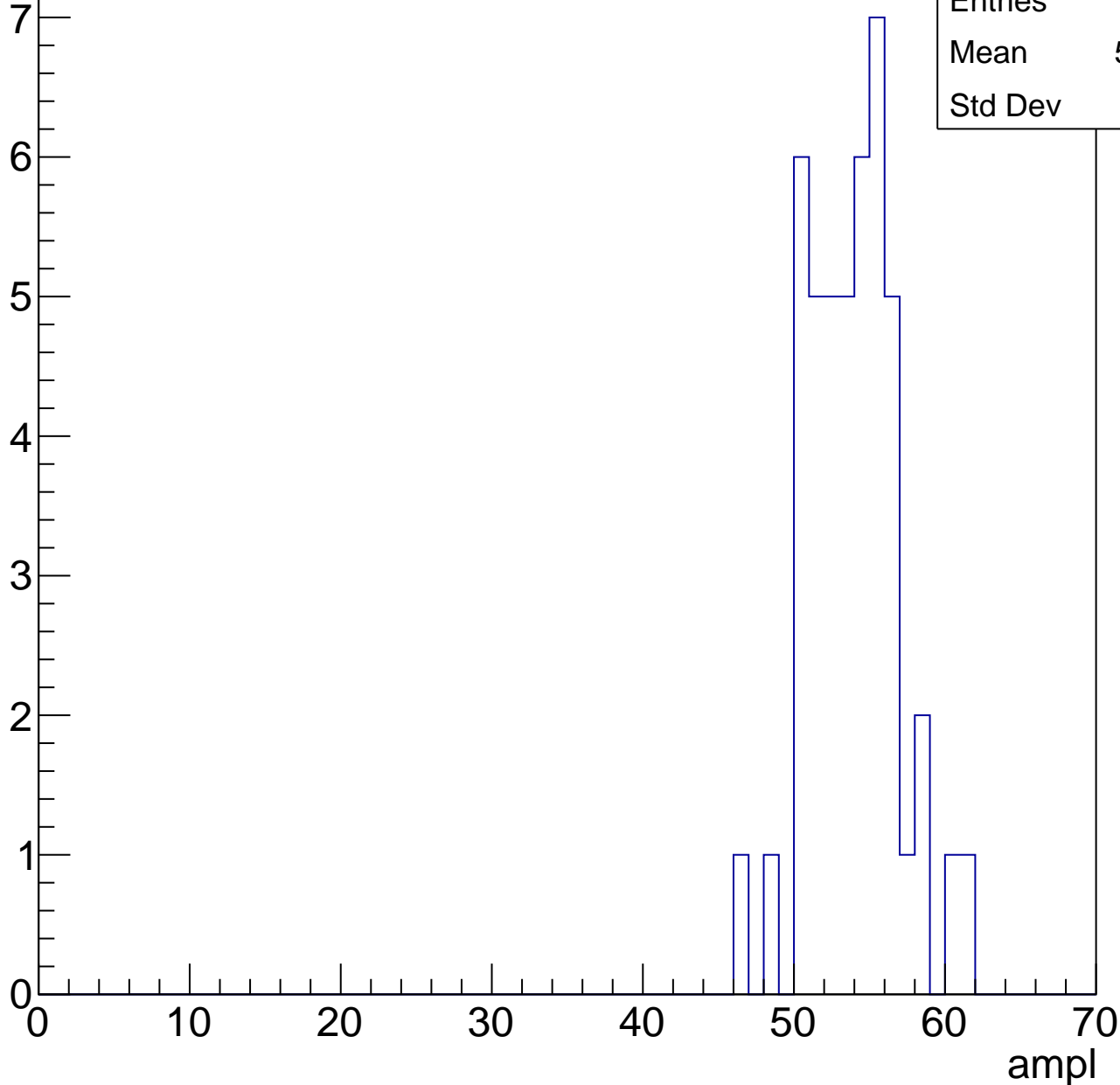
Entries	64
Mean	47.38
Std Dev	3.228

# B1L000S, U2-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	53.41
Std Dev	2.96



# B1L000S, U2-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

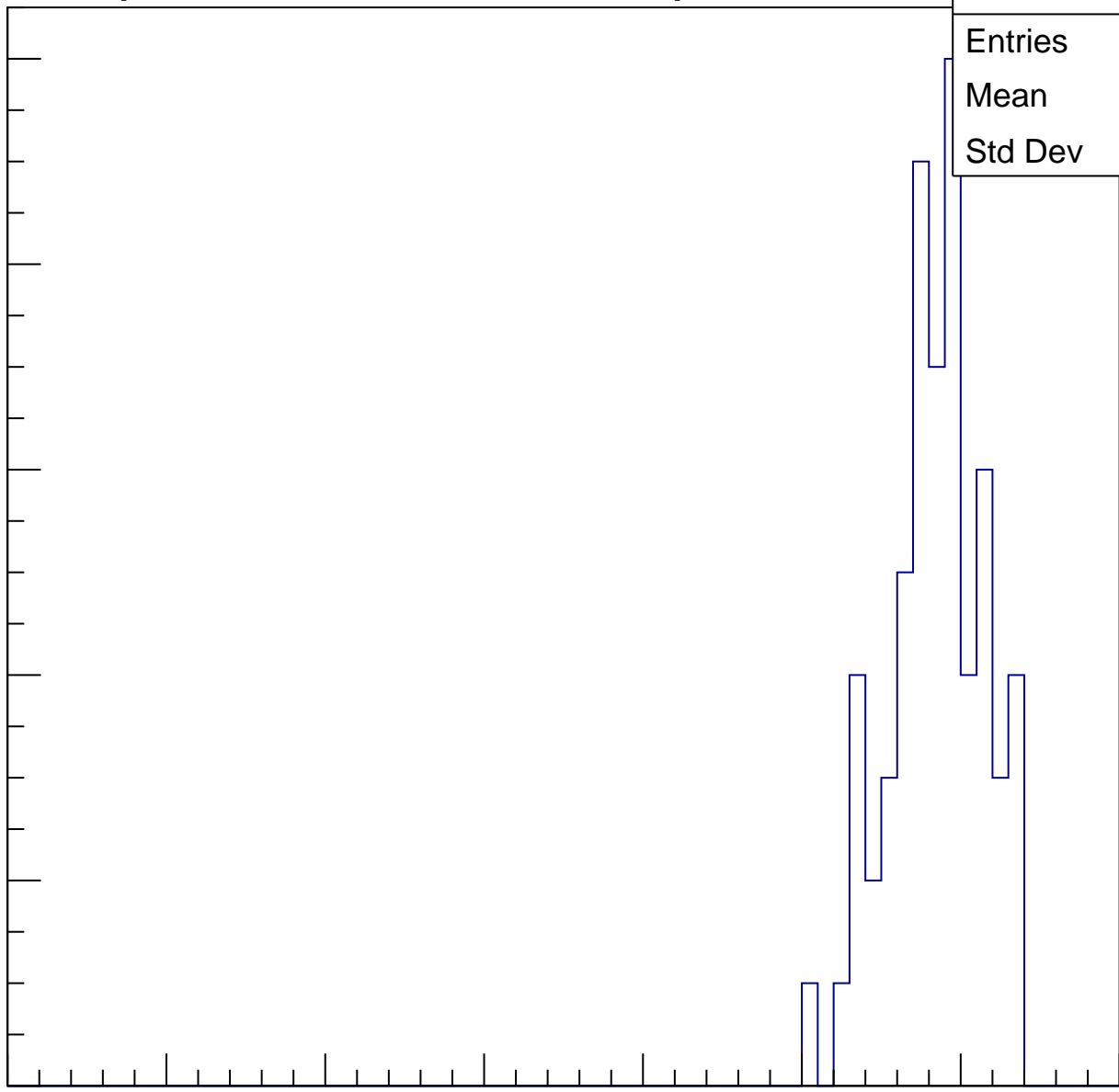
Entries	59
Mean	57.97
Std Dev	2.957

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

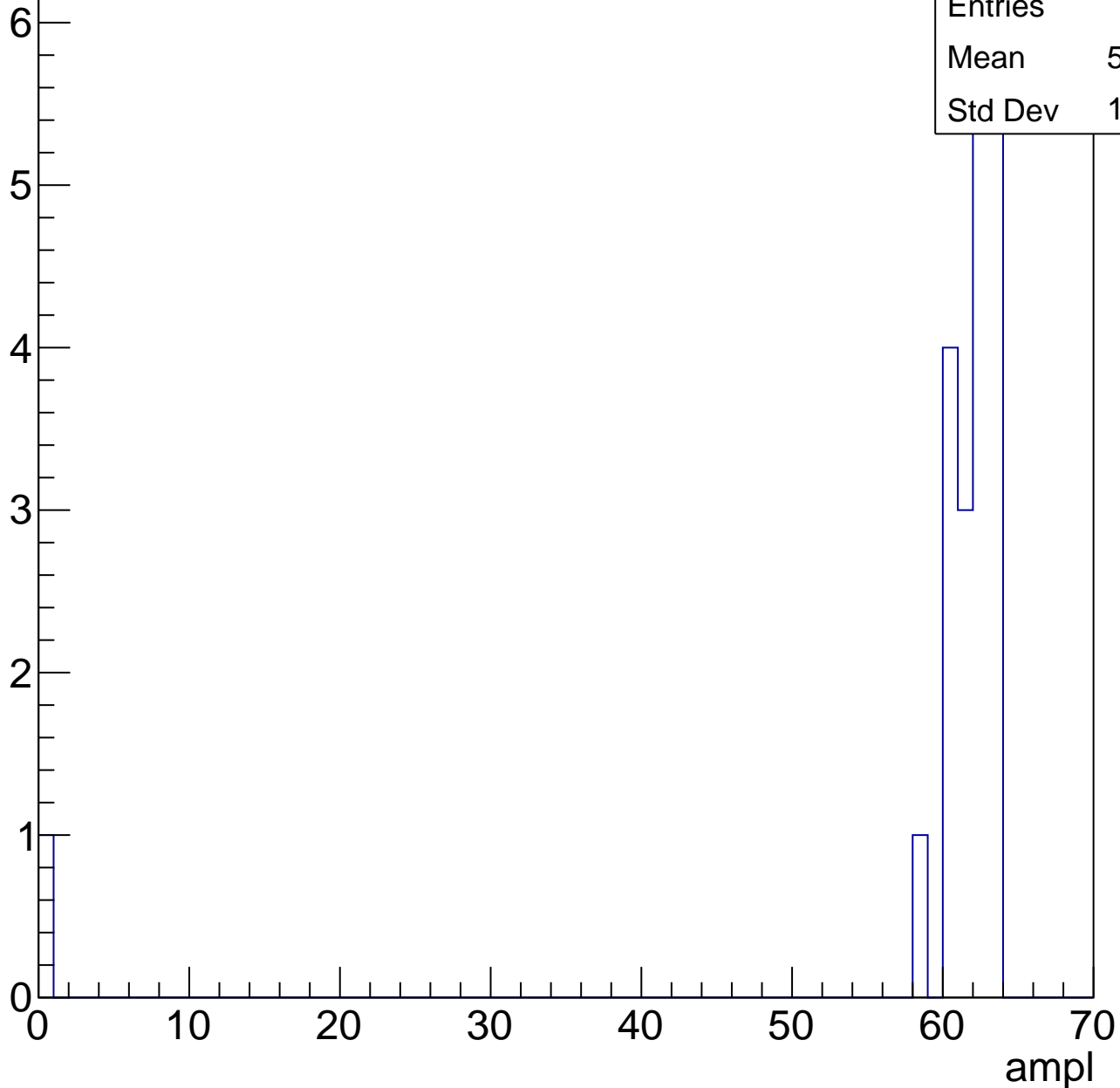
ampl



# B1L000S, U2-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

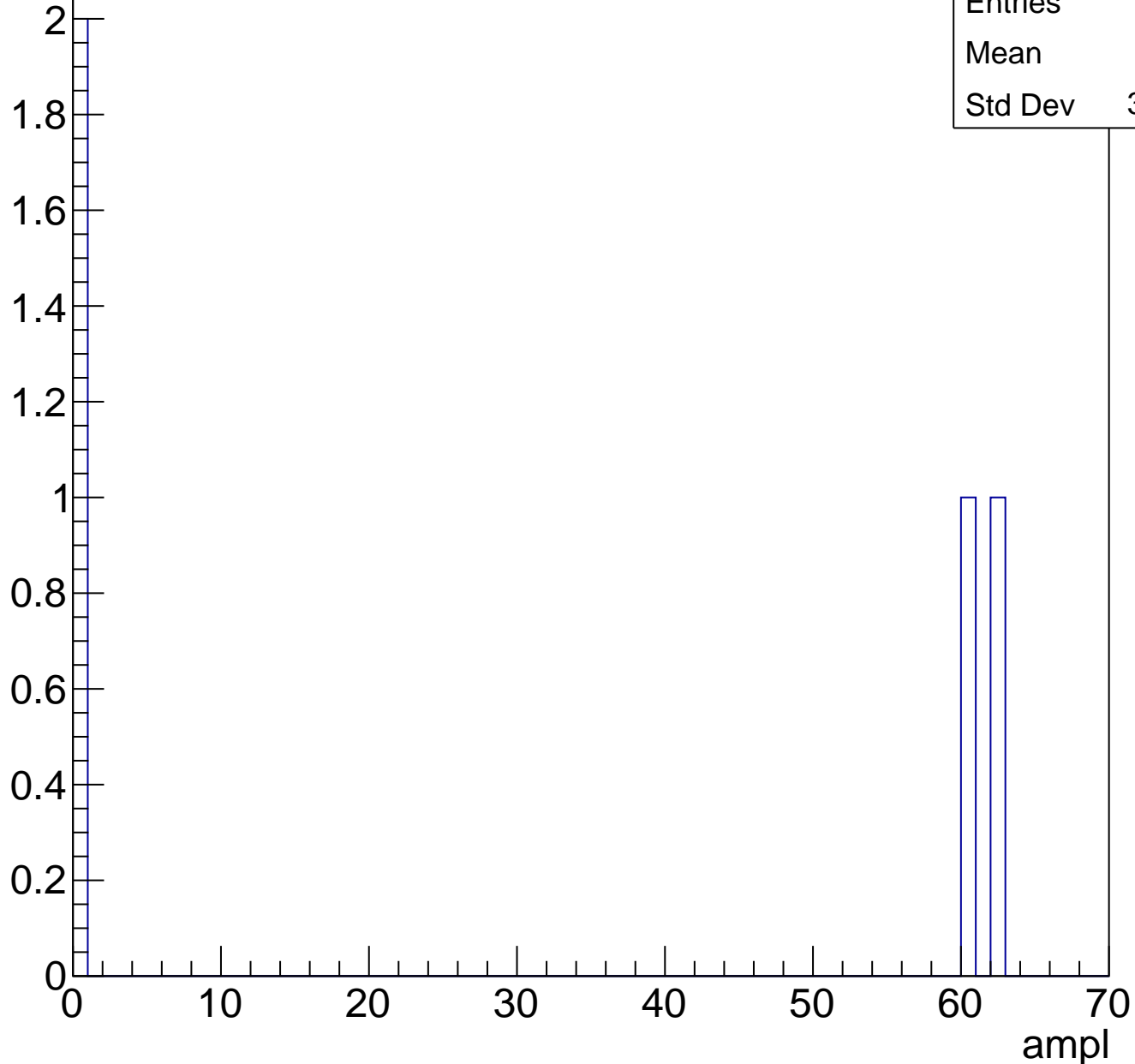




# B1L000S, U2-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	30.5
Std Dev	30.51

# B1L000S, U2-ch2, adc0

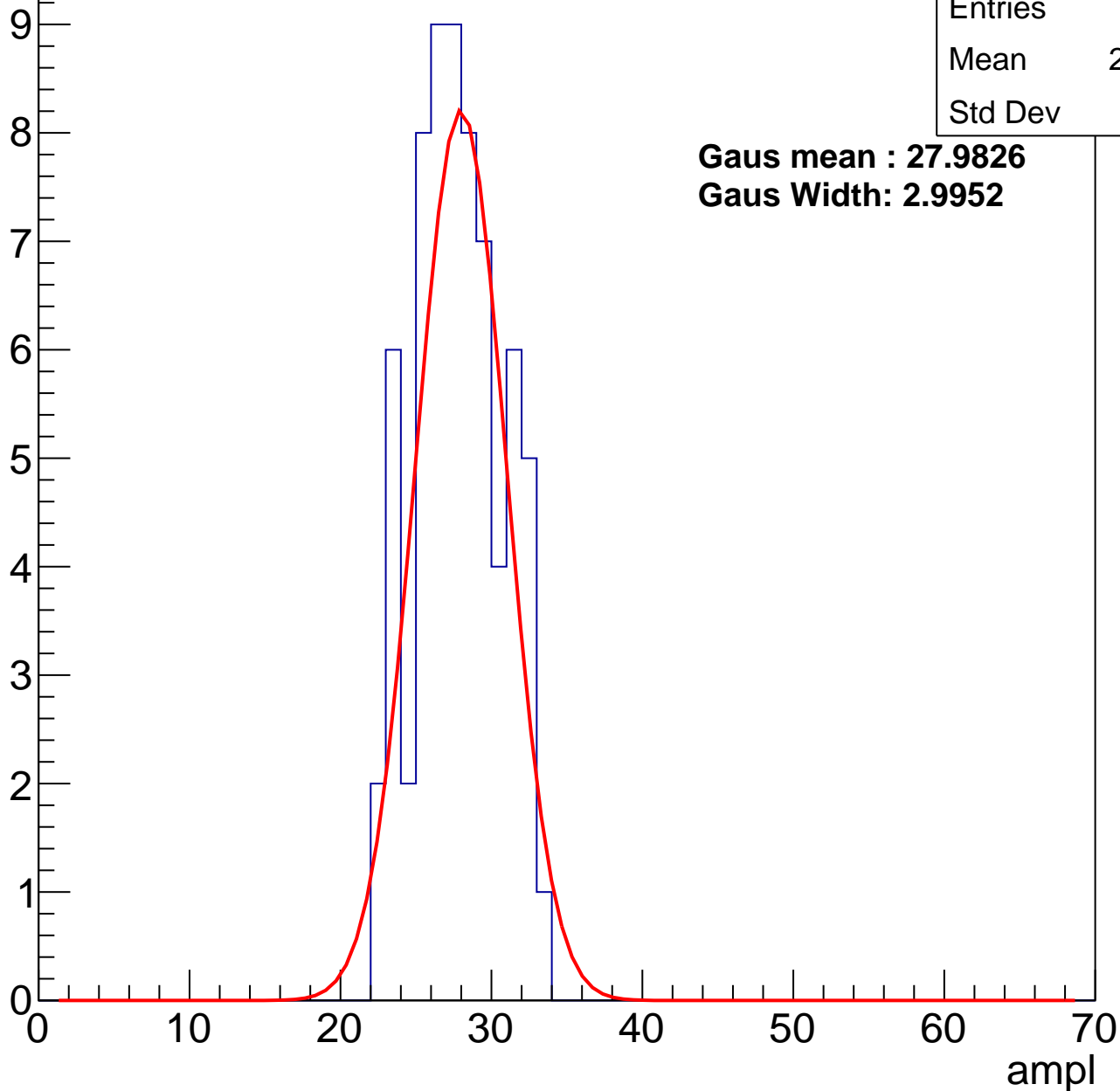
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	27.36
Std Dev	2.8

**Gaus mean : 27.9826**

**Gaus Width: 2.9952**



# B1L000S, U2-ch2, adc1

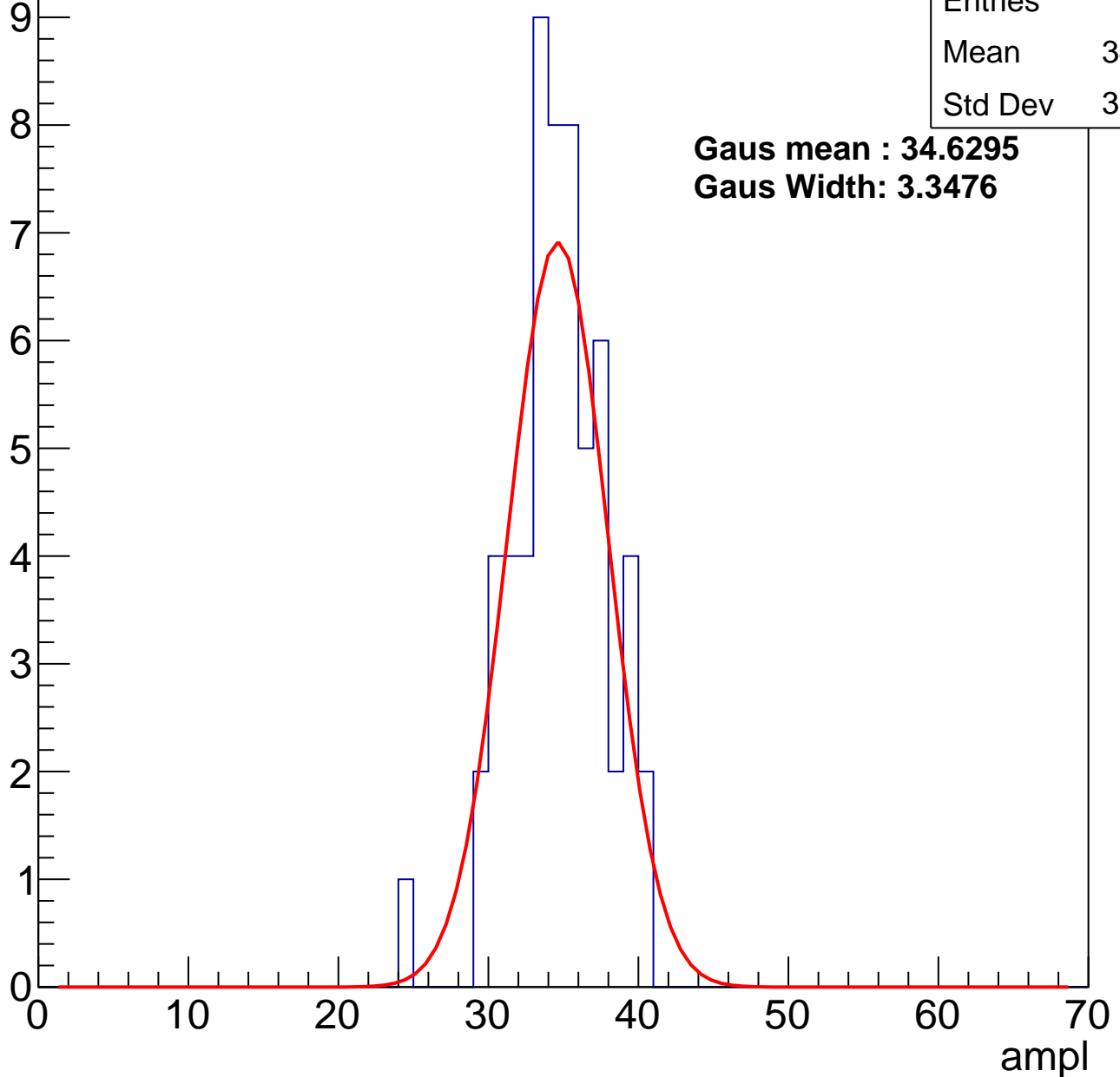
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	34.19
Std Dev	3.078

**Gaus mean : 34.6295**

**Gaus Width: 3.3476**



# B1L000S, U2-ch2, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	40.11
Std Dev	3.31

**Gaus mean : 40.6165**

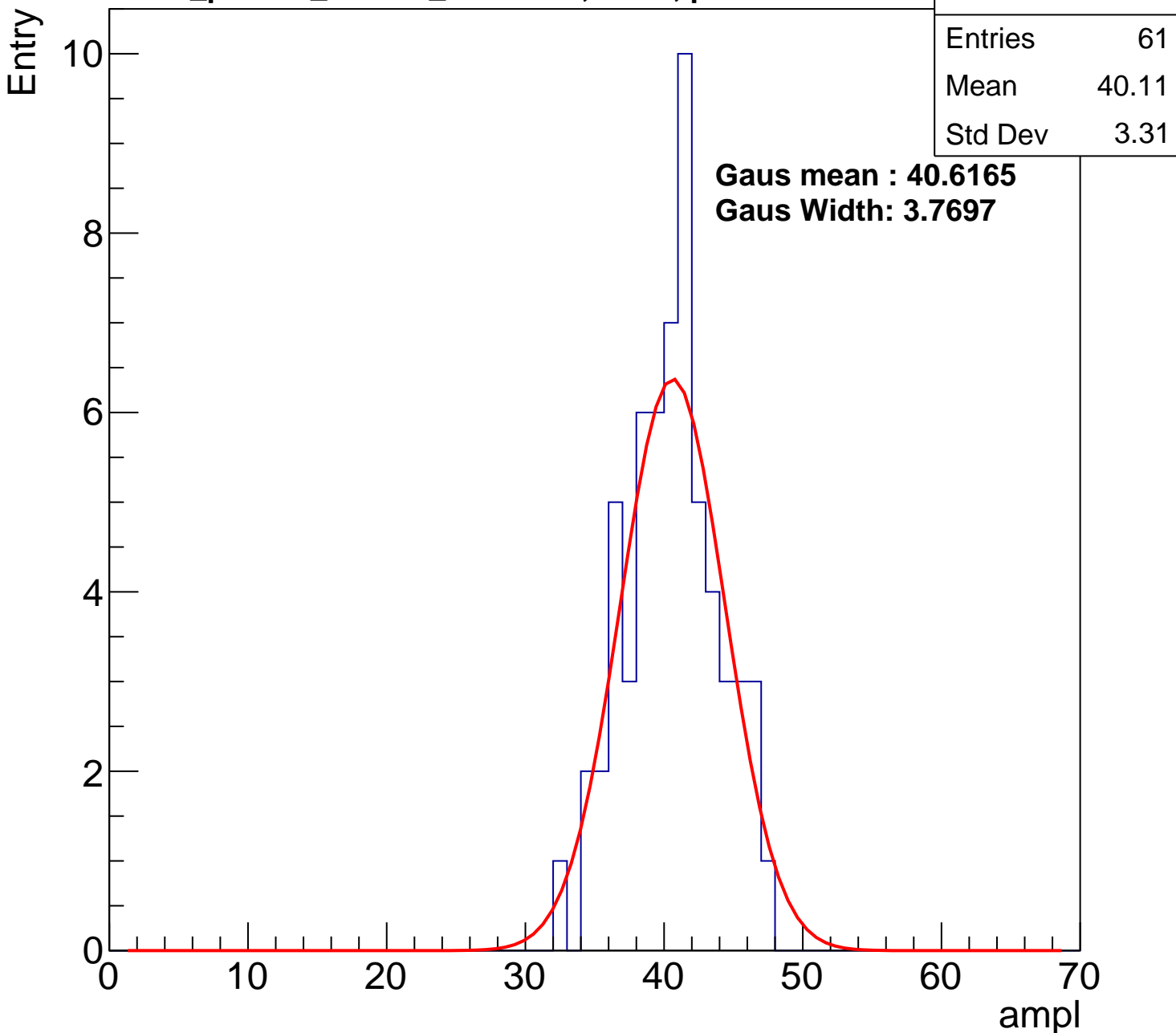
**Gaus Width: 3.7697**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

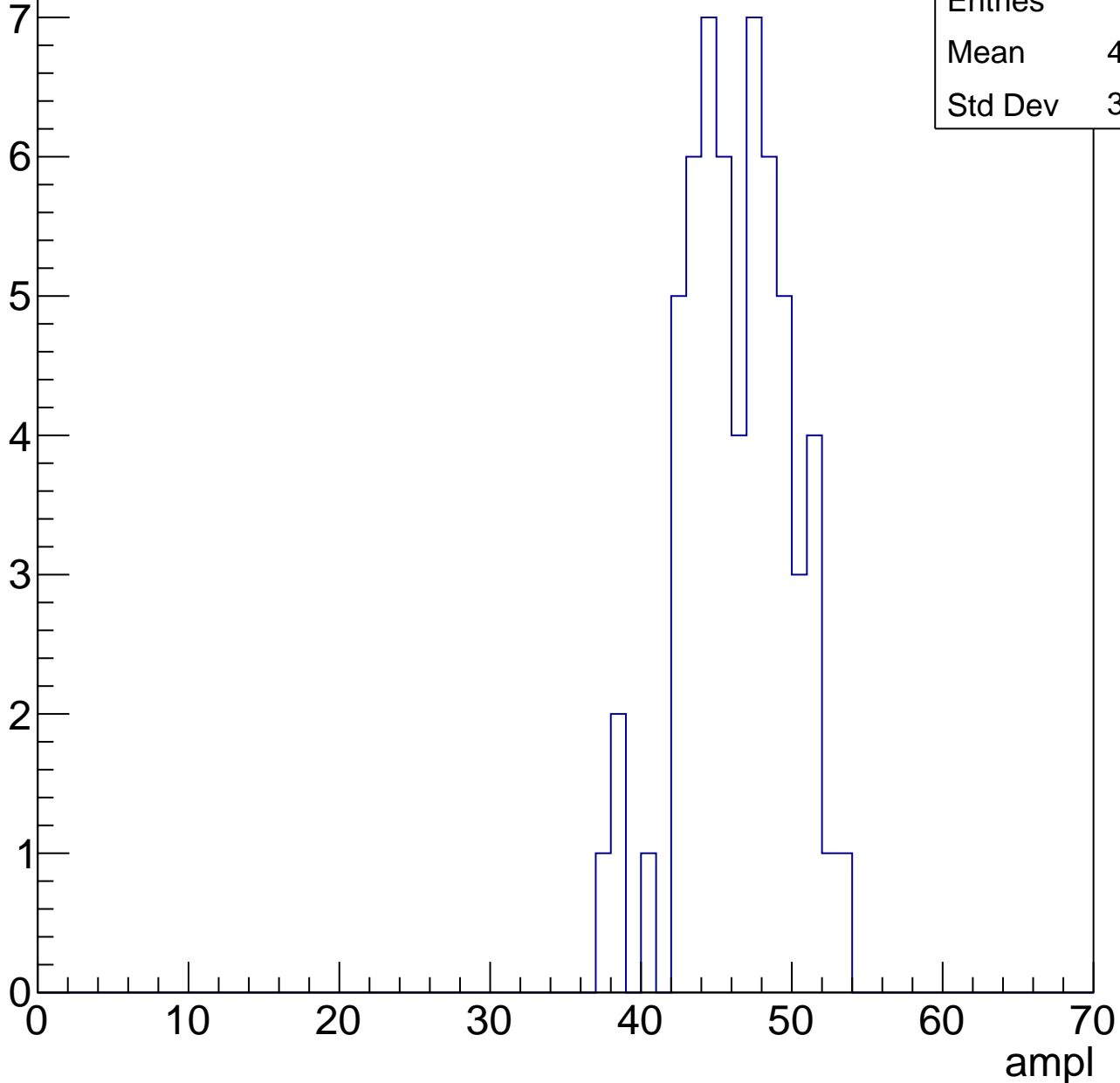


# B1L000S, U2-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	45.83
Std Dev	3.494

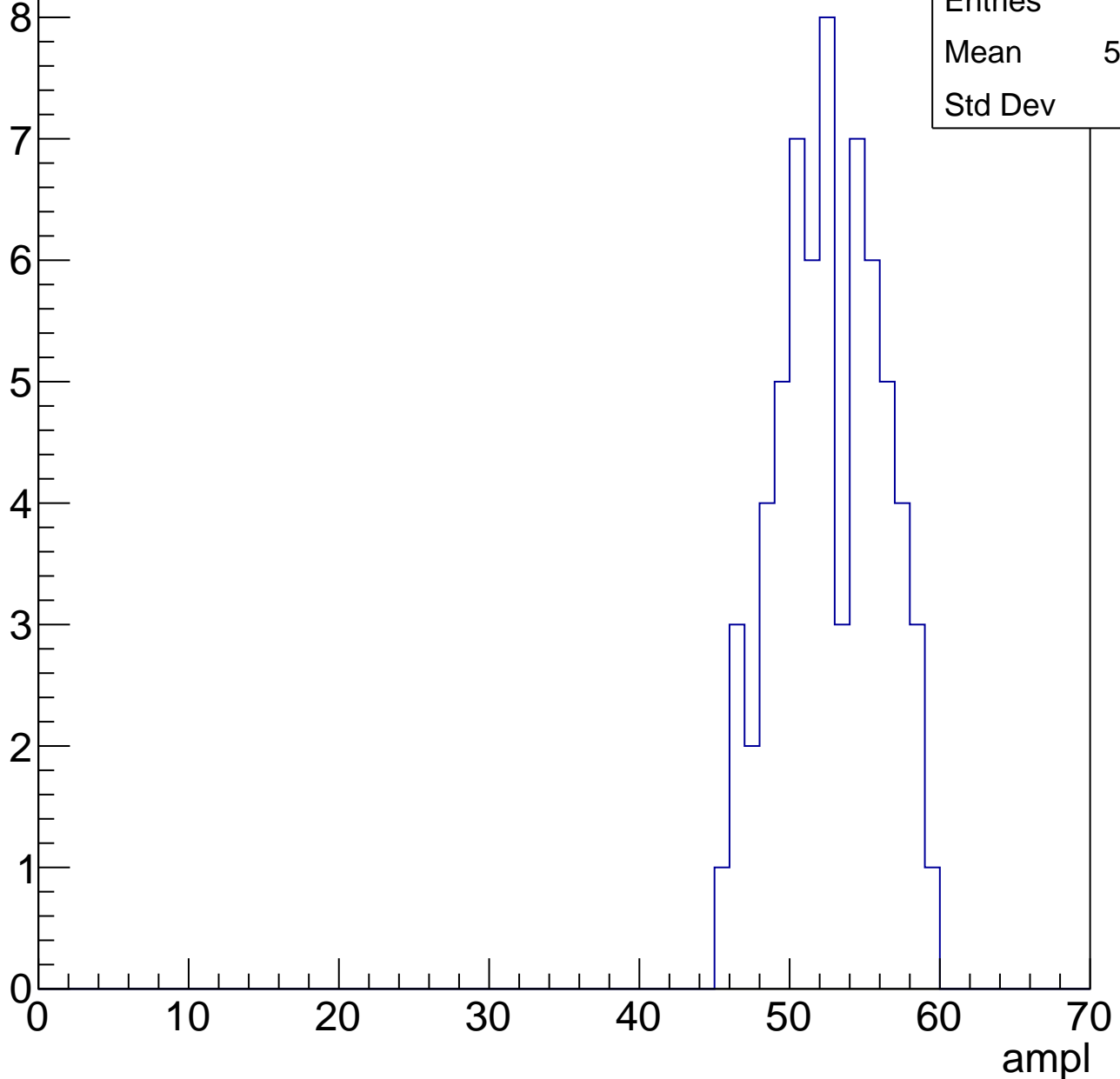


# B1L000S, U2-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	52.22
Std Dev	3.44

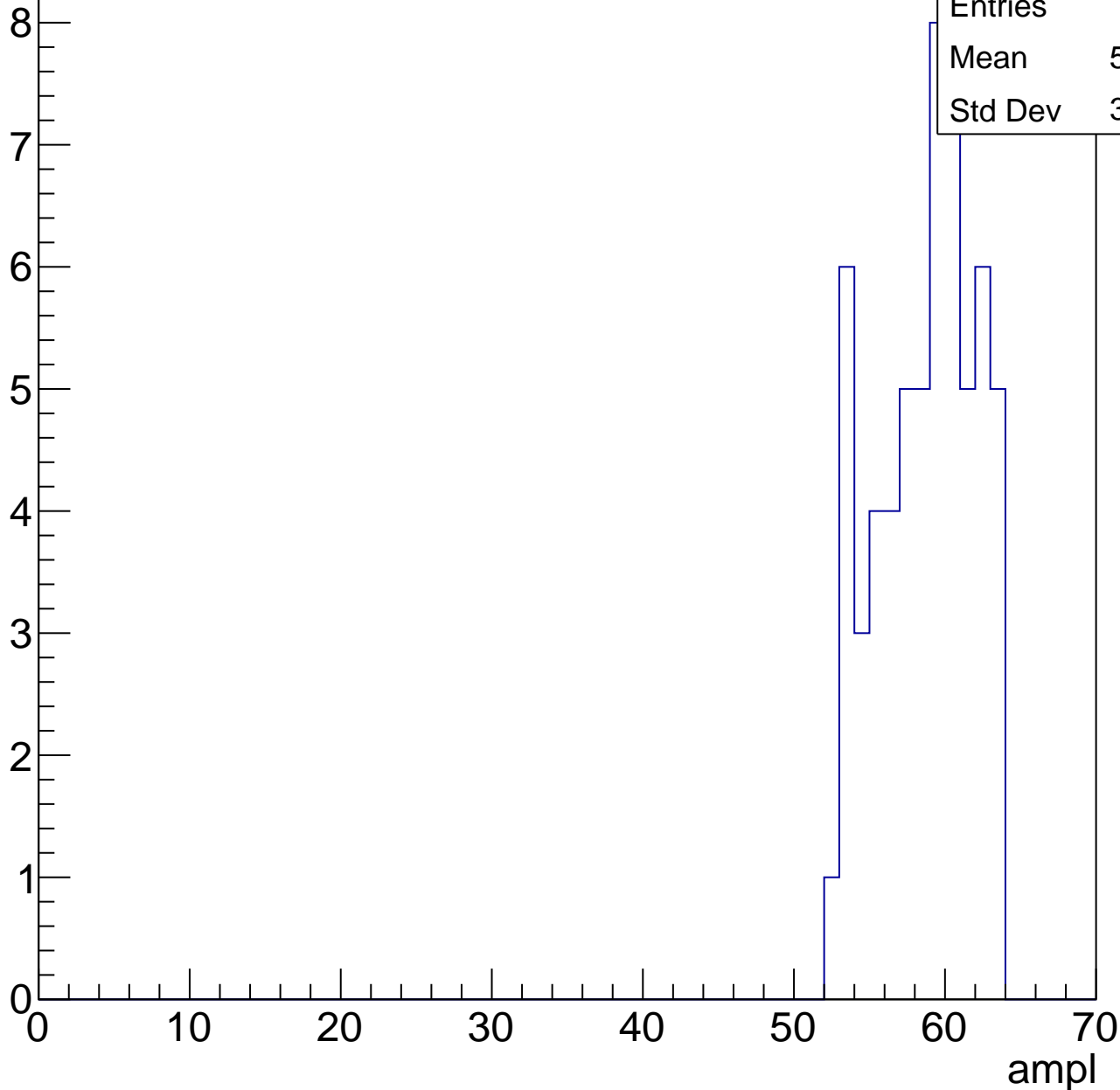


# B1L000S, U2-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	58.25
Std Dev	3.144

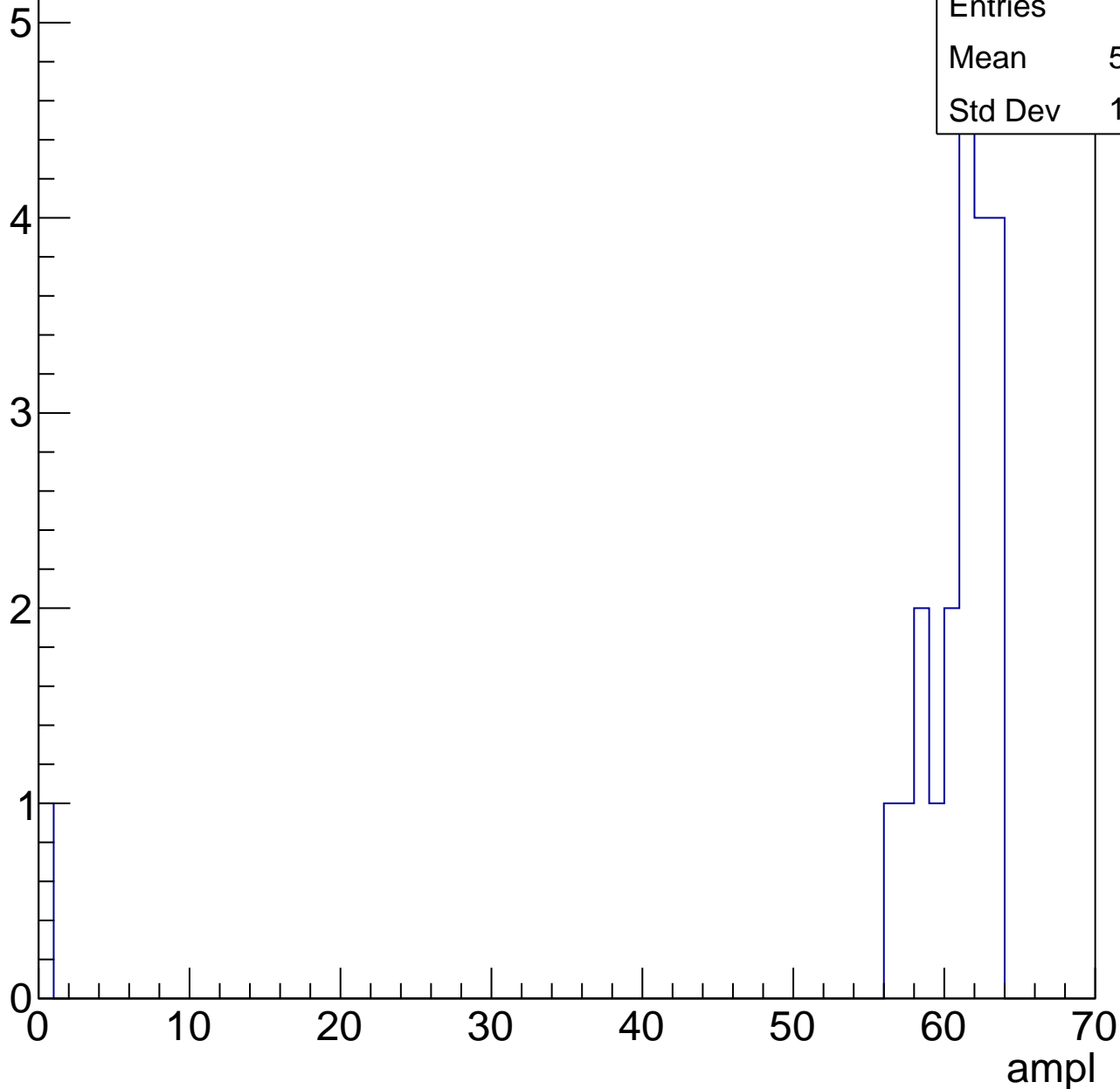


# B1L000S, U2-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	57.76
Std Dev	13.07





# B1L000S, U2-ch2, adc7

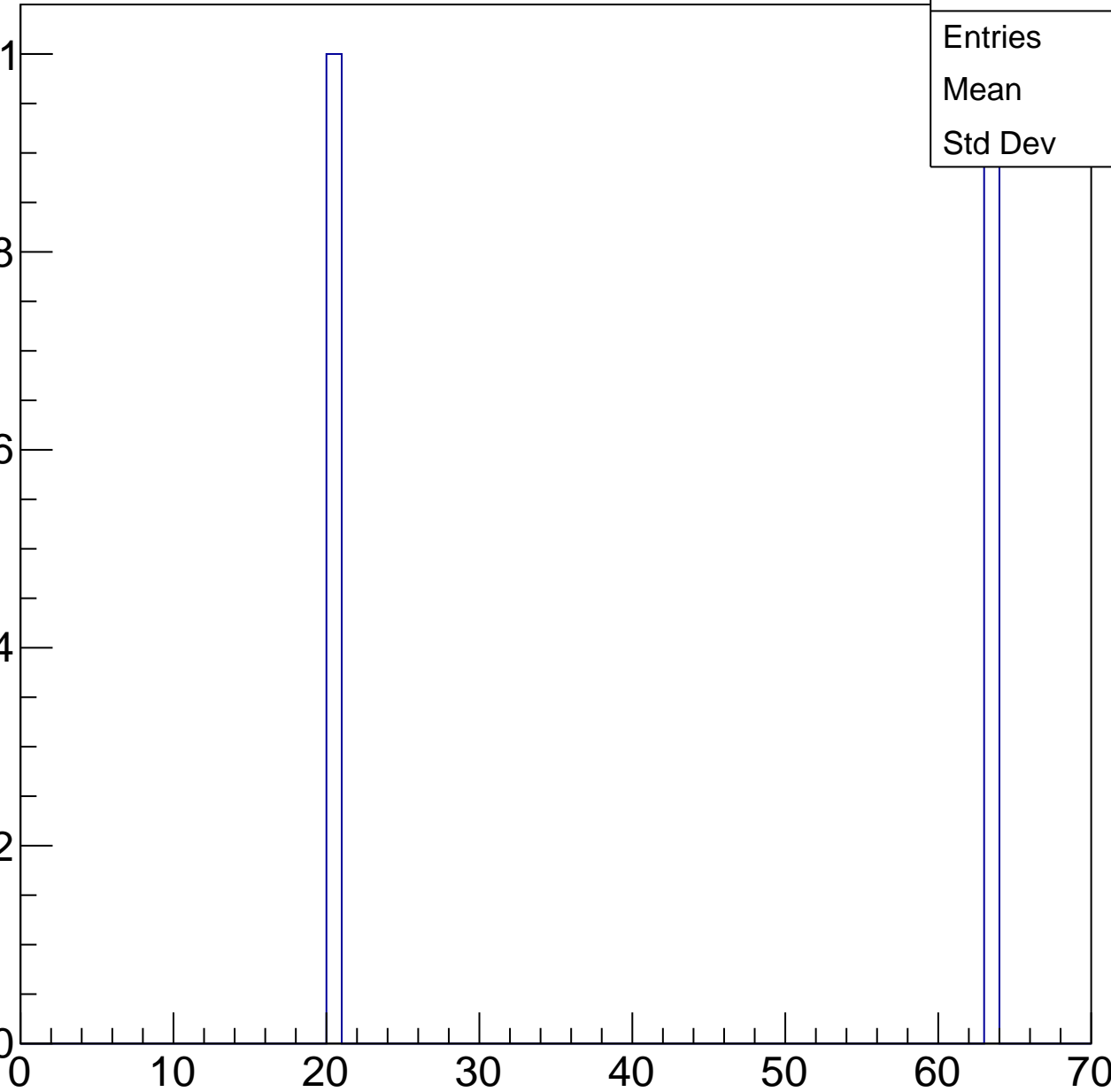
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	41.5
Std Dev	21.5

ampl



# B1L000S, U2-ch3, adc0

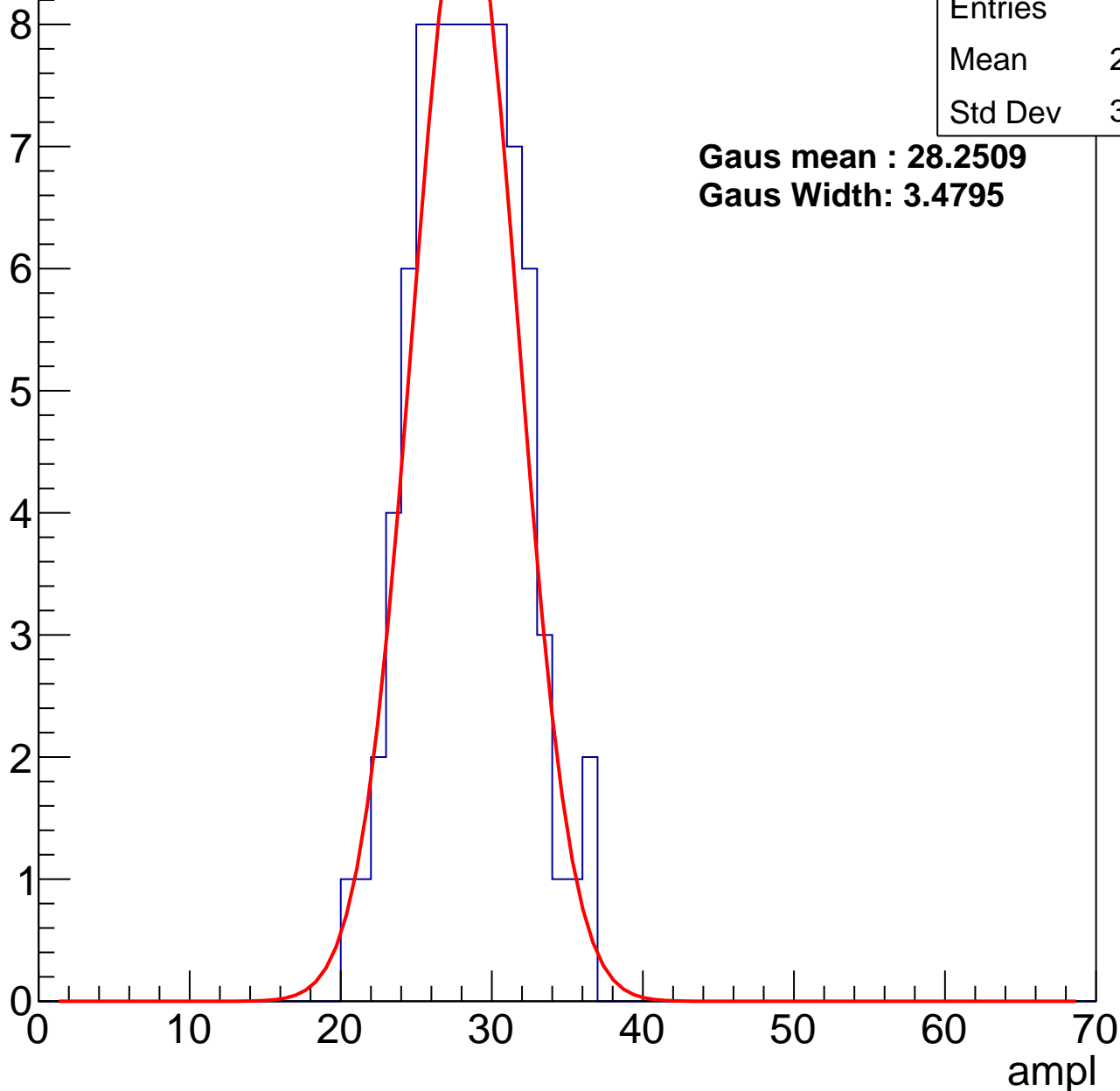
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	27.93
Std Dev	3.456

**Gaus mean : 28.2509**

**Gaus Width: 3.4795**



# B1L000S, U2-ch3, adc1

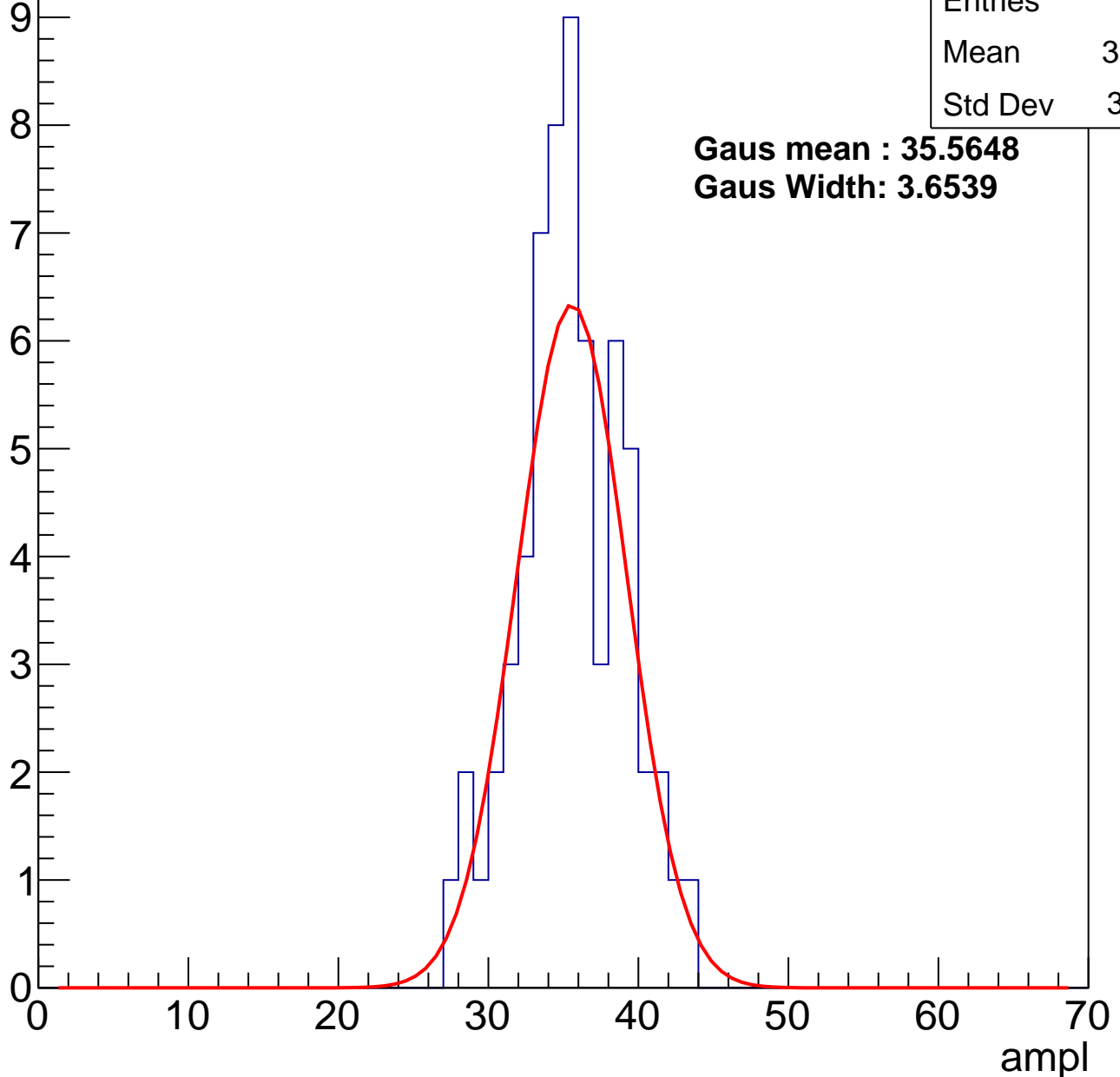
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.05
Std Dev	3.461

**Gaus mean : 35.5648**

**Gaus Width: 3.6539**



# B1L000S, U2-ch3, adc2

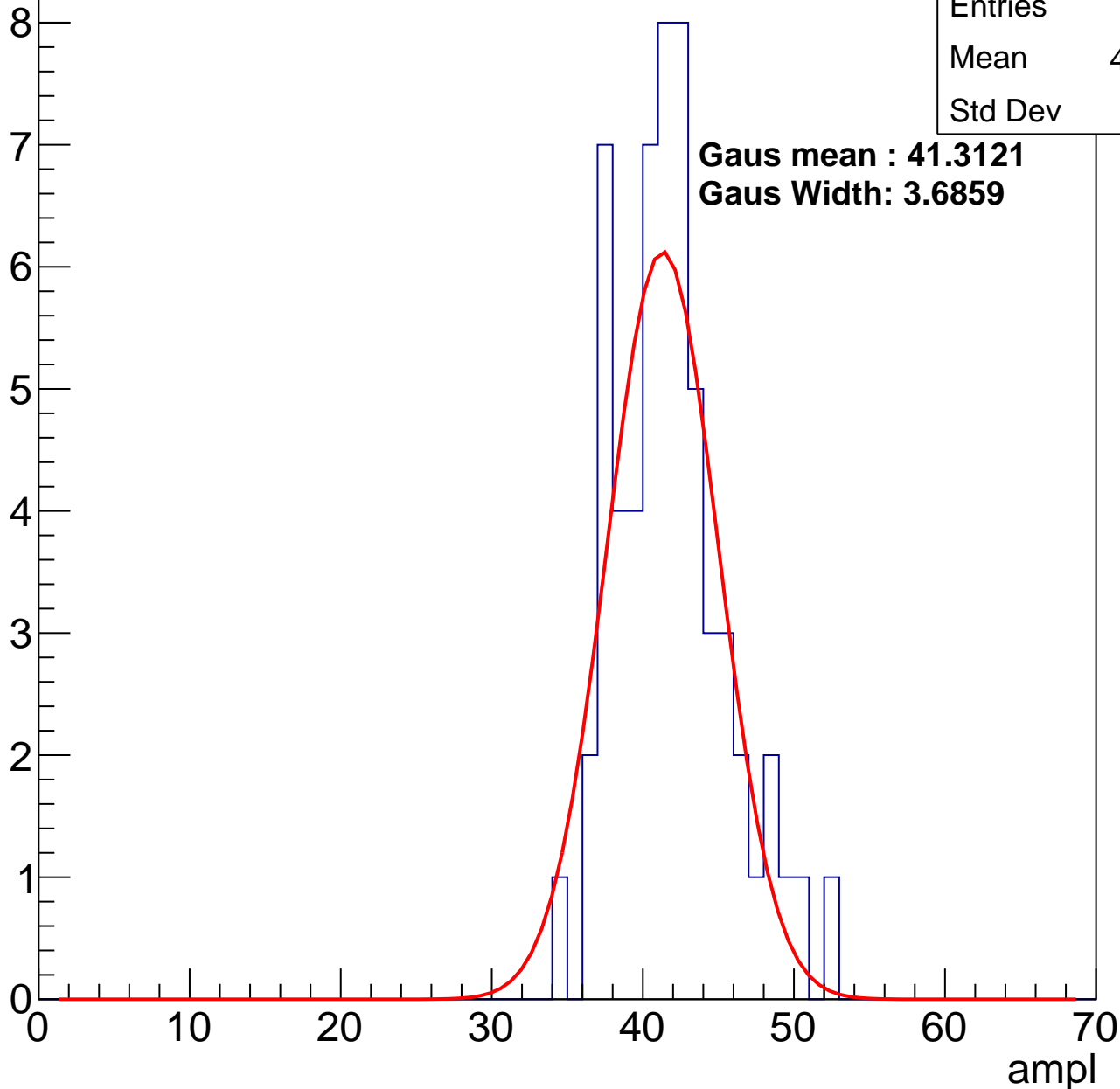
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.42
Std Dev	3.68

**Gaus mean : 41.3121**

**Gaus Width: 3.6859**

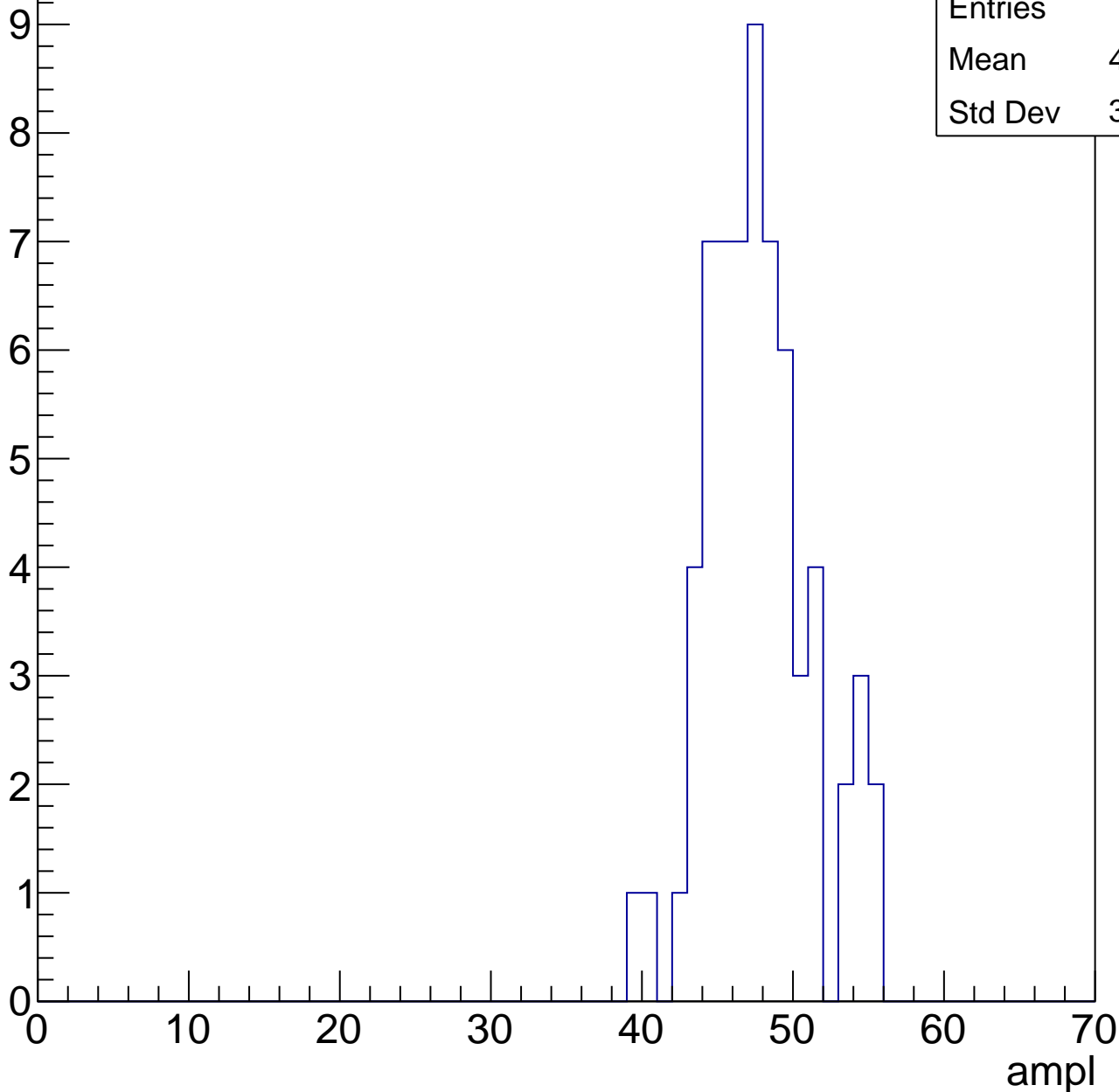


# B1L000S, U2-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	47.23
Std Dev	3.458

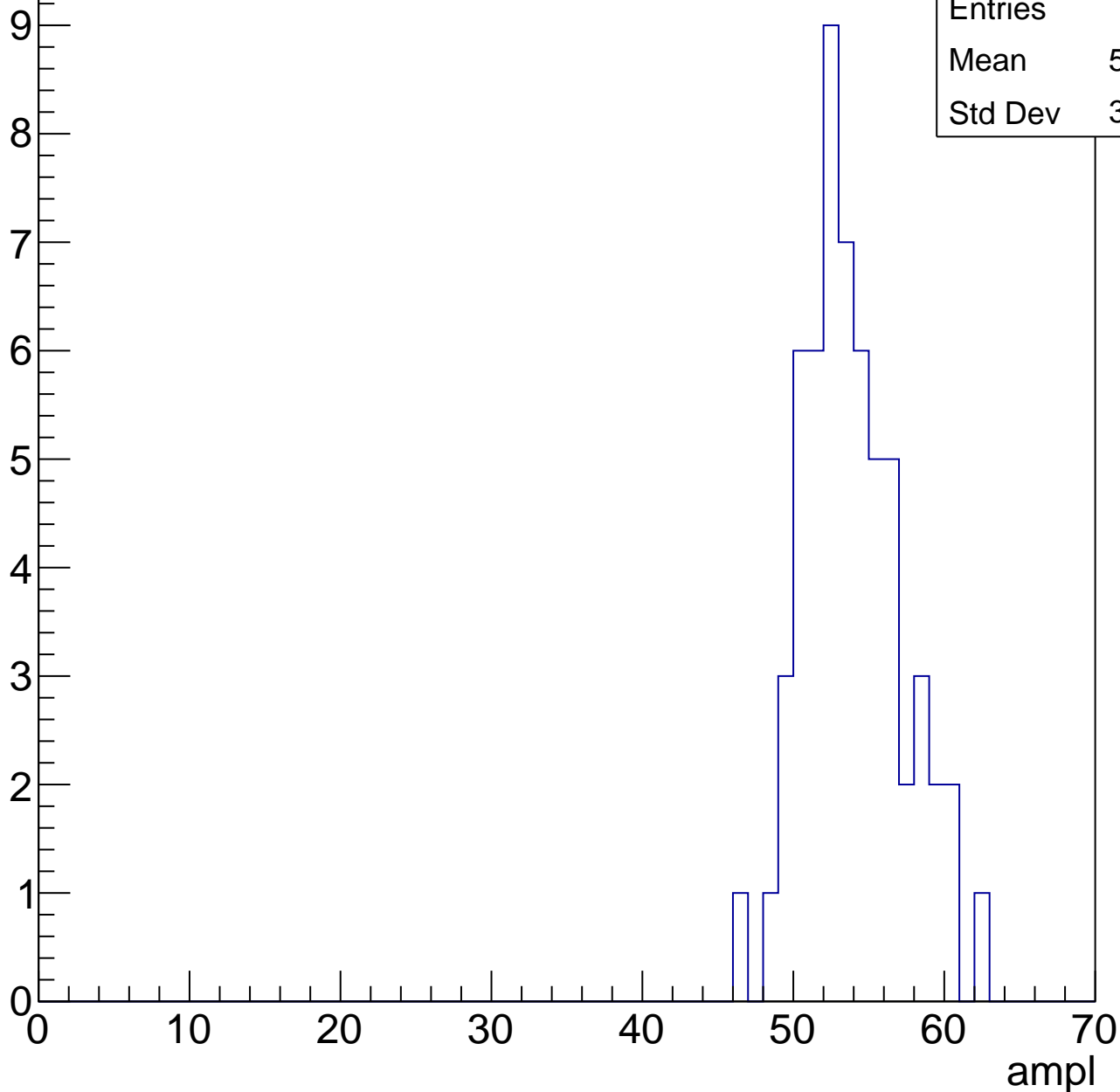


# B1L000S, U2-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	53.44
Std Dev	3.259

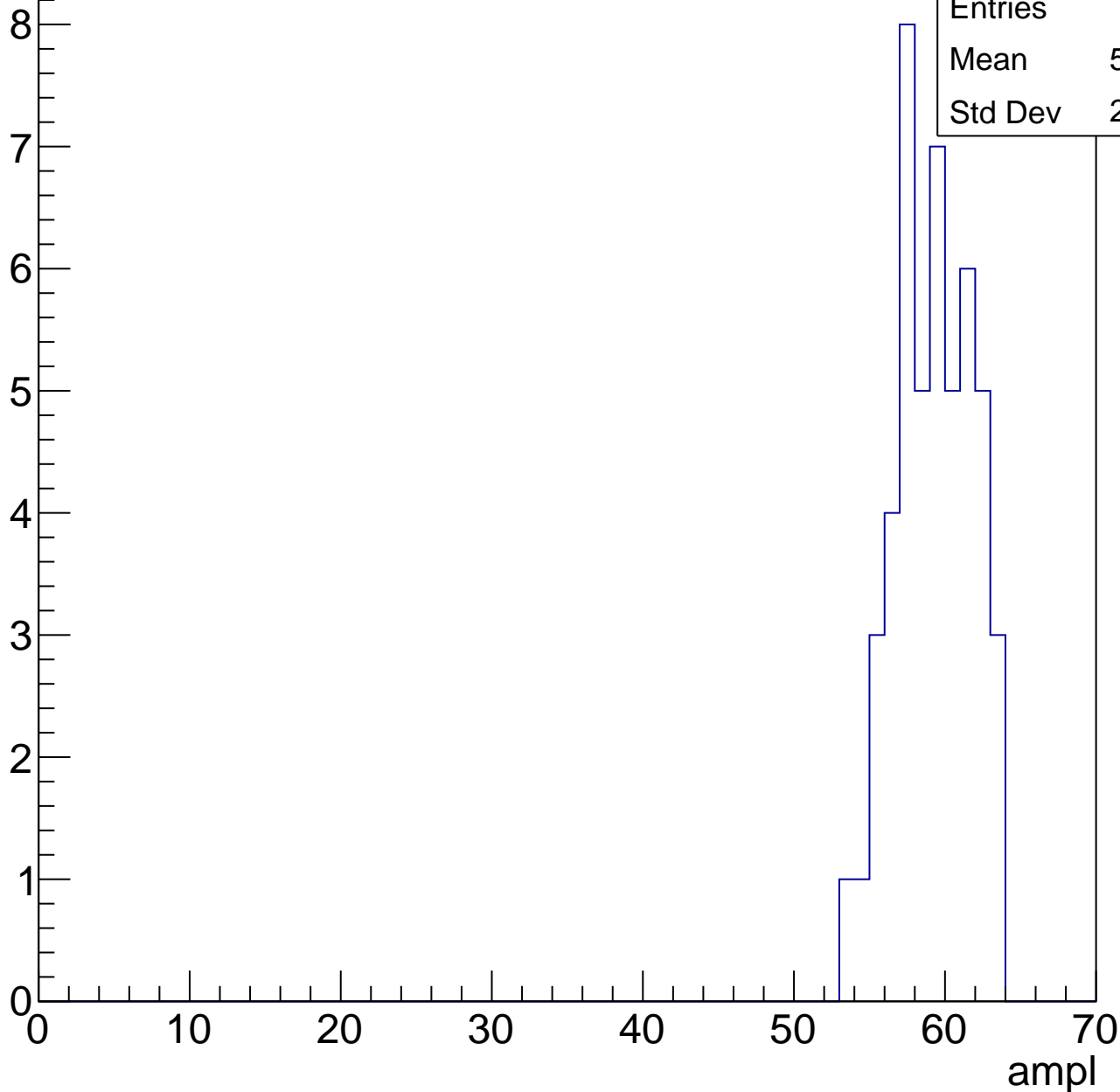


# B1L000S, U2-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	58.75
Std Dev	2.504

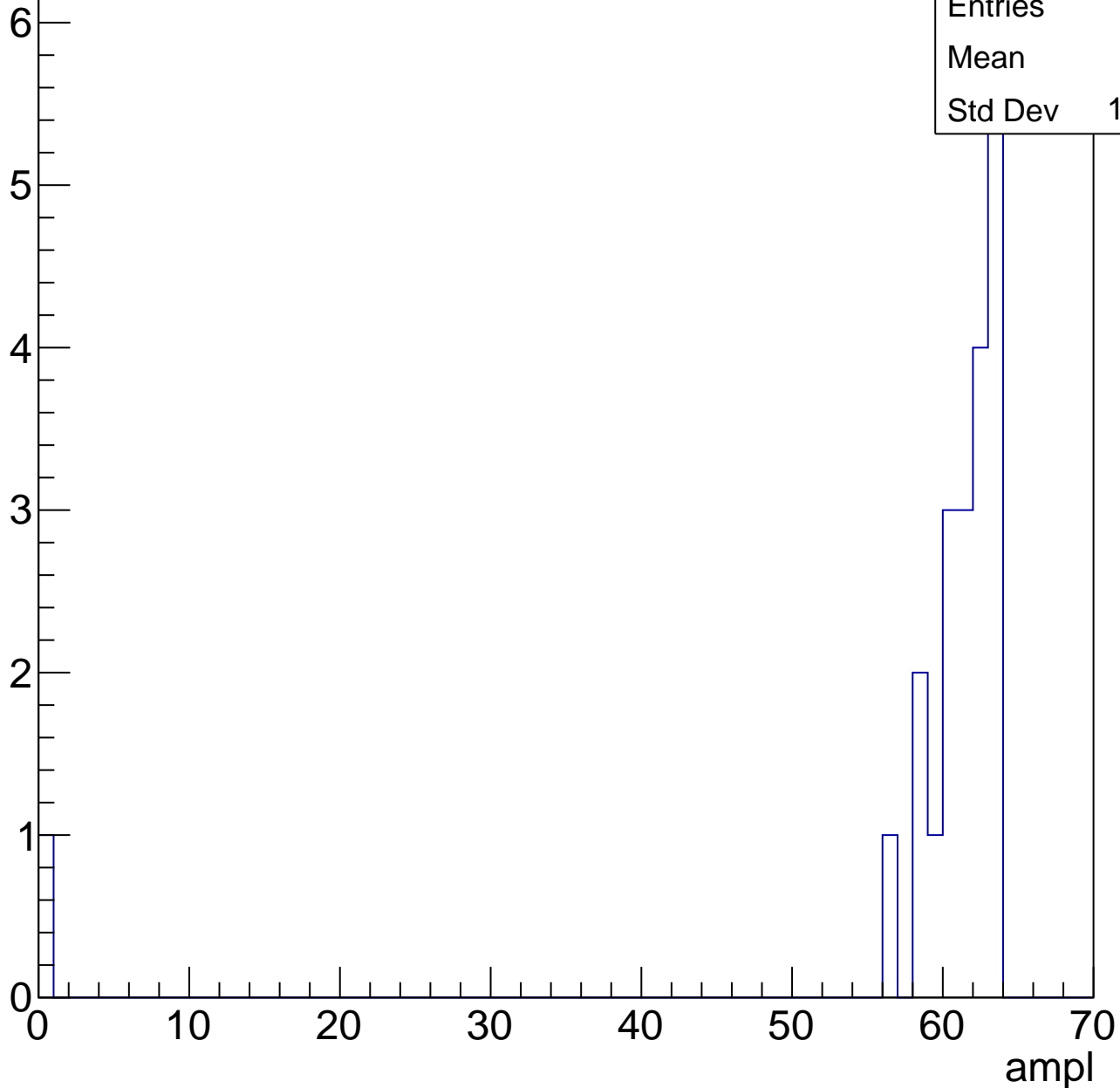


# B1L000S, U2-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.1
Std Dev	13.13

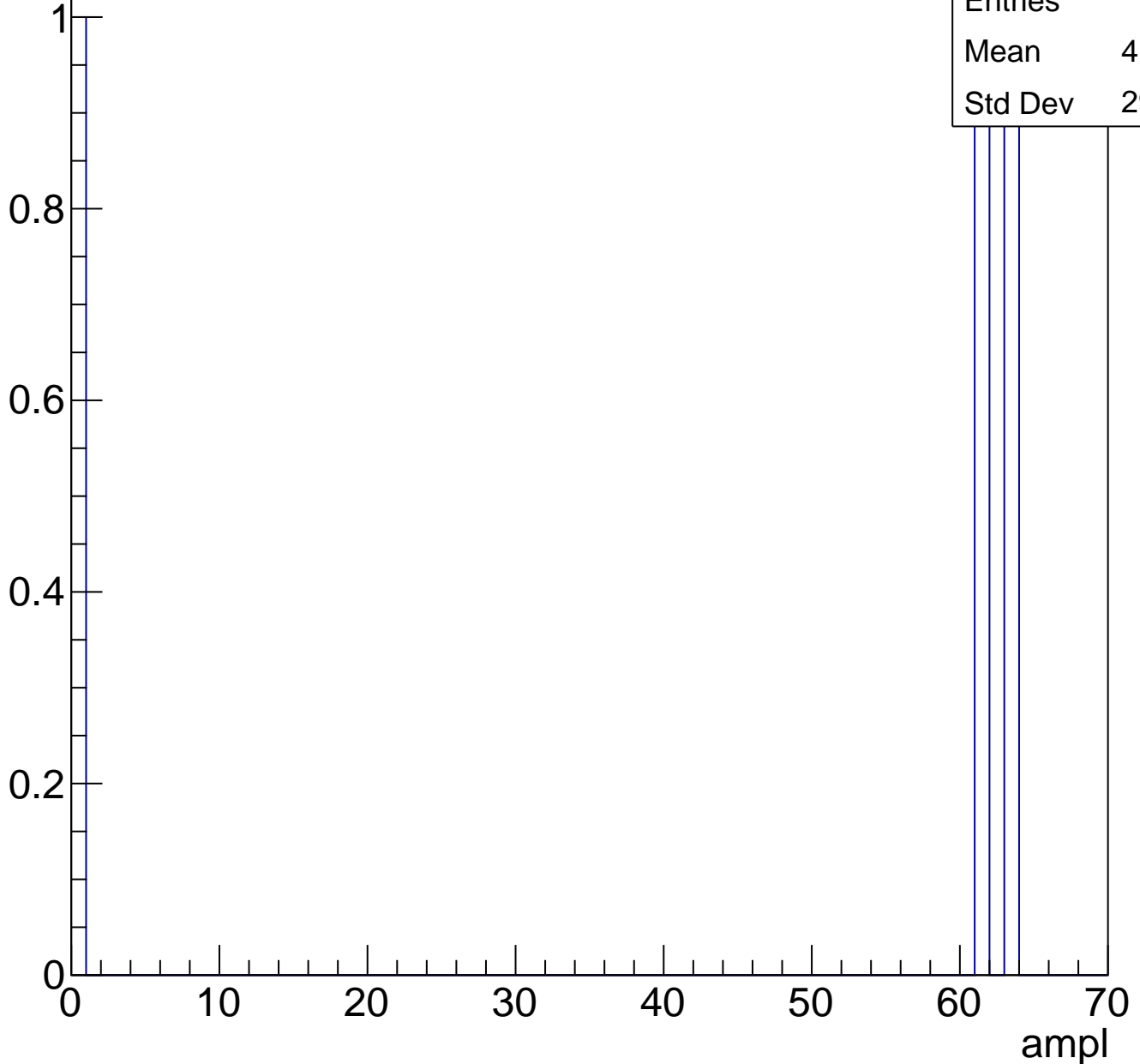




# B1L000S, U2-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch4, adc0

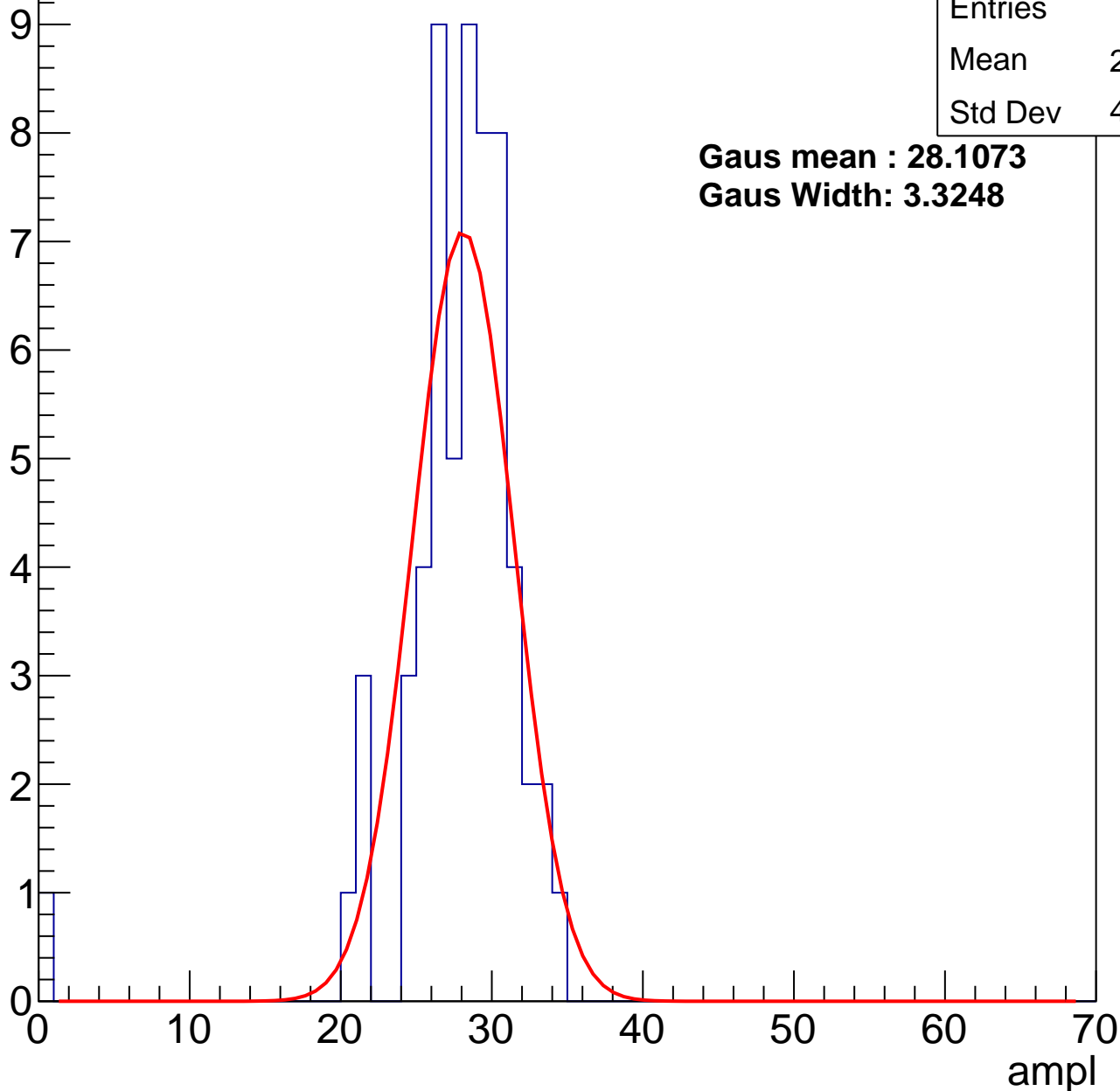
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.27
Std Dev	4.622

**Gaus mean : 28.1073**

**Gaus Width: 3.3248**



# B1L000S, U2-ch4, adc1

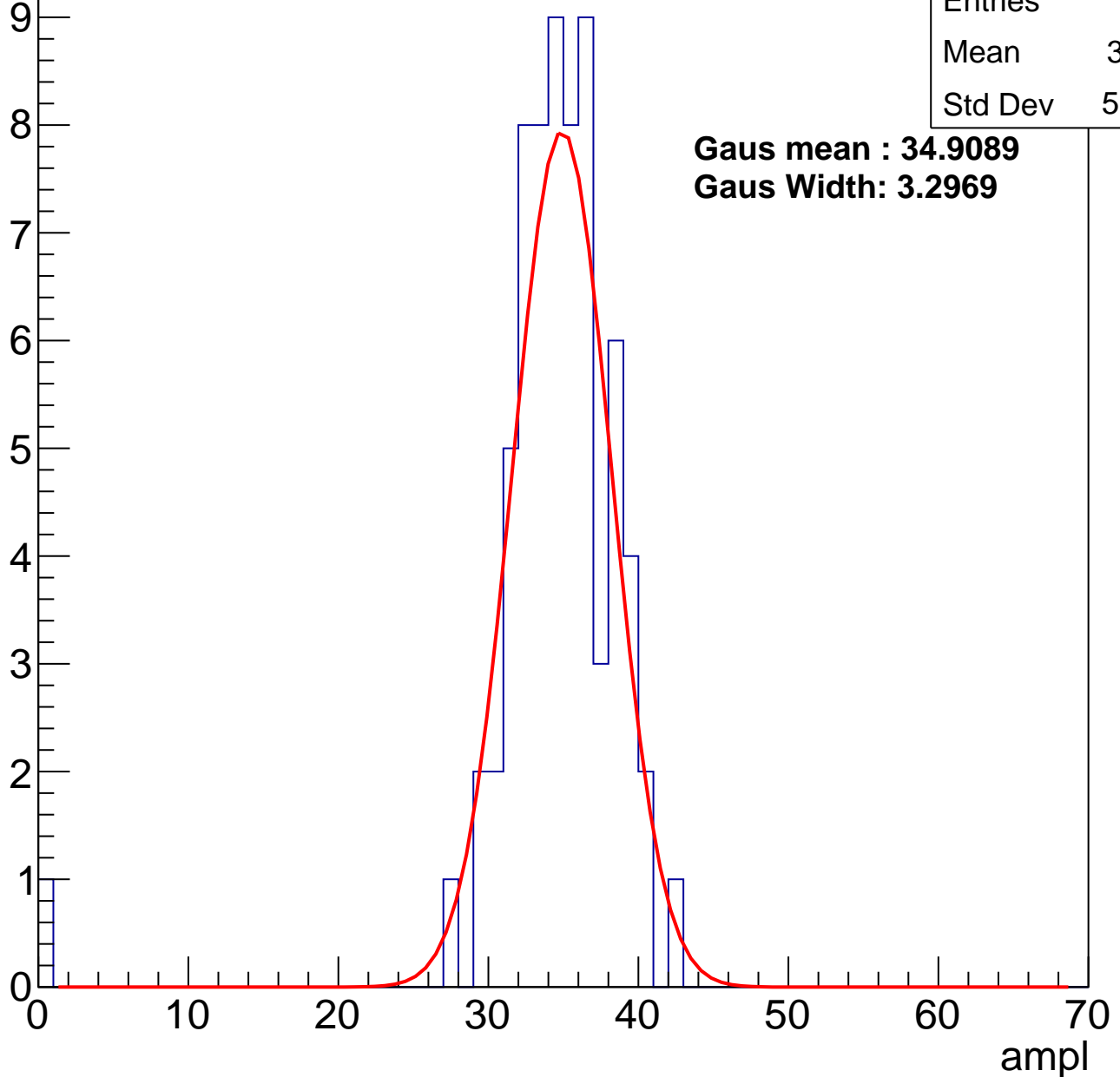
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.01
Std Dev	5.072

**Gaus mean : 34.9089**

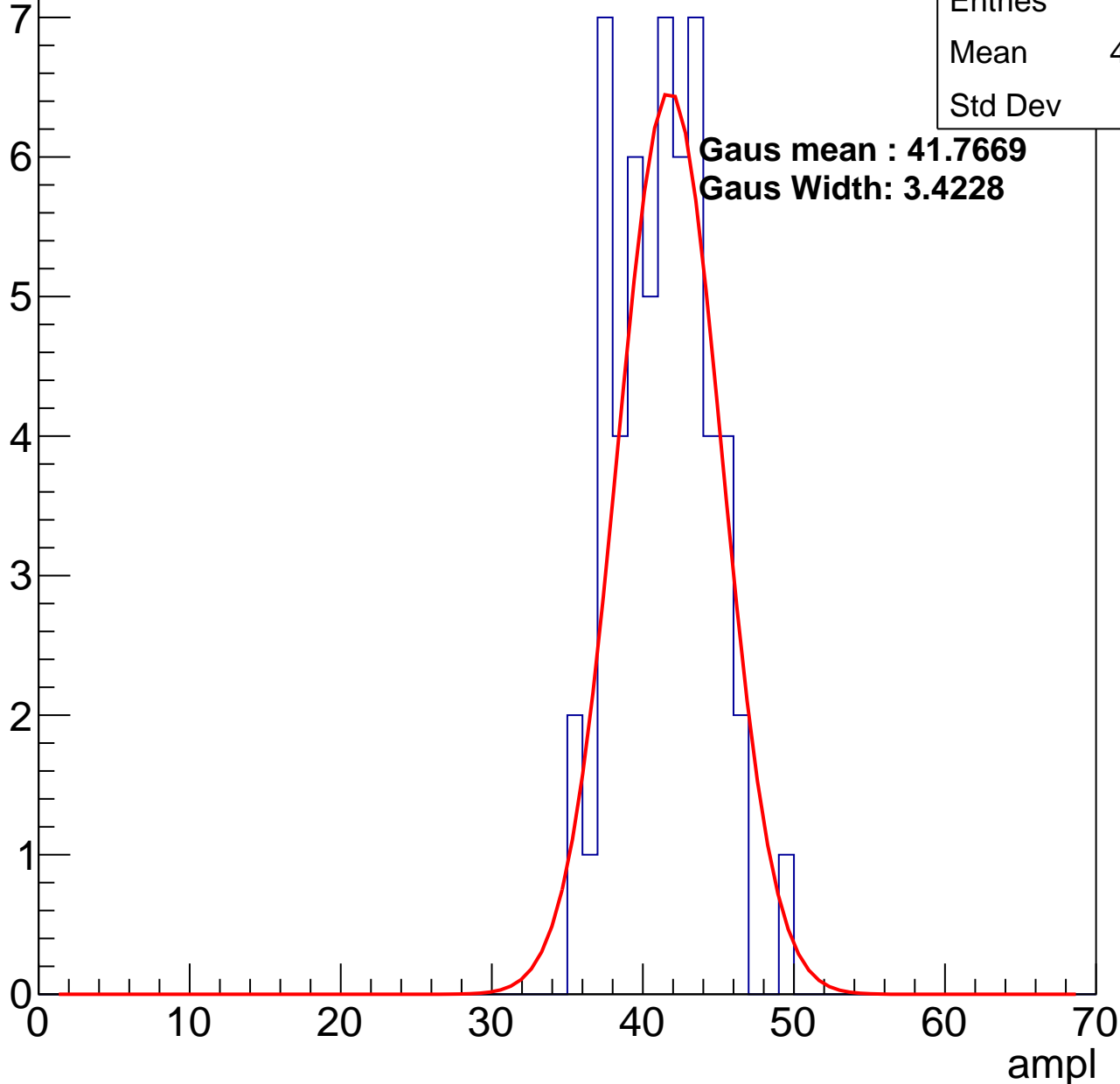
**Gaus Width: 3.2969**



# B1L000S, U2-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

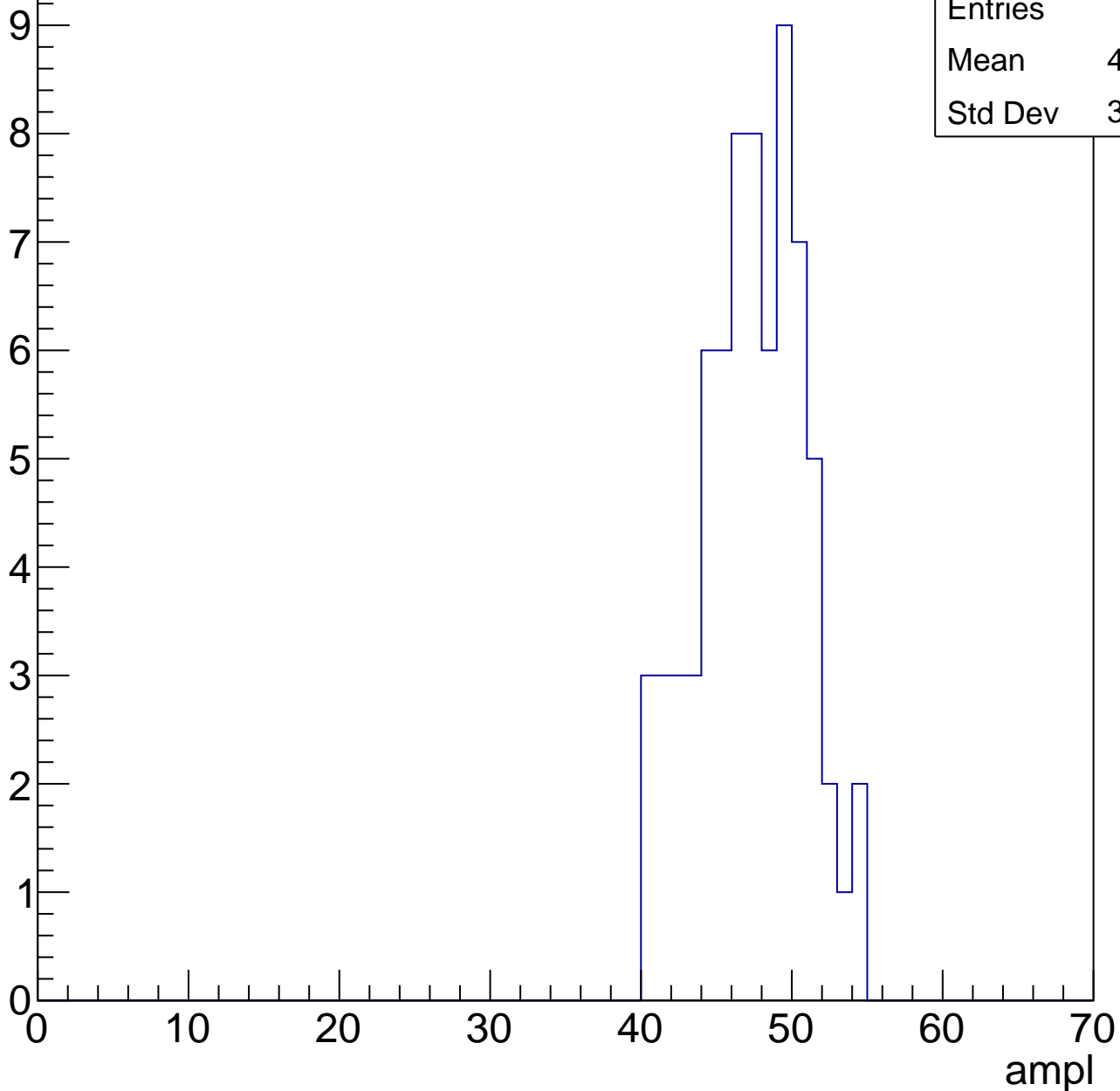
Entry



# B1L000S, U2-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



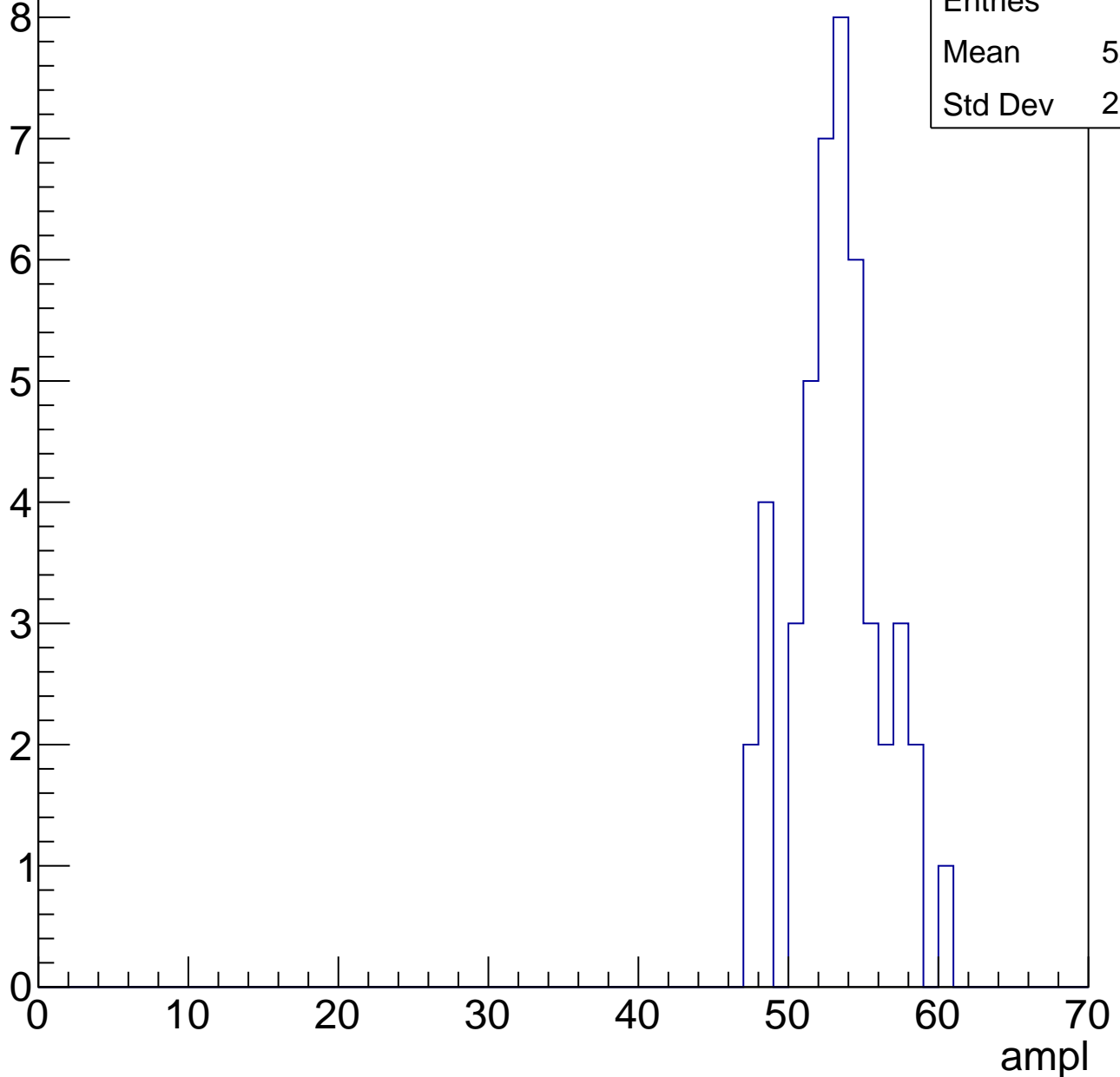
Entries	72
Mean	46.88
Std Dev	3.399

# B1L000S, U2-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

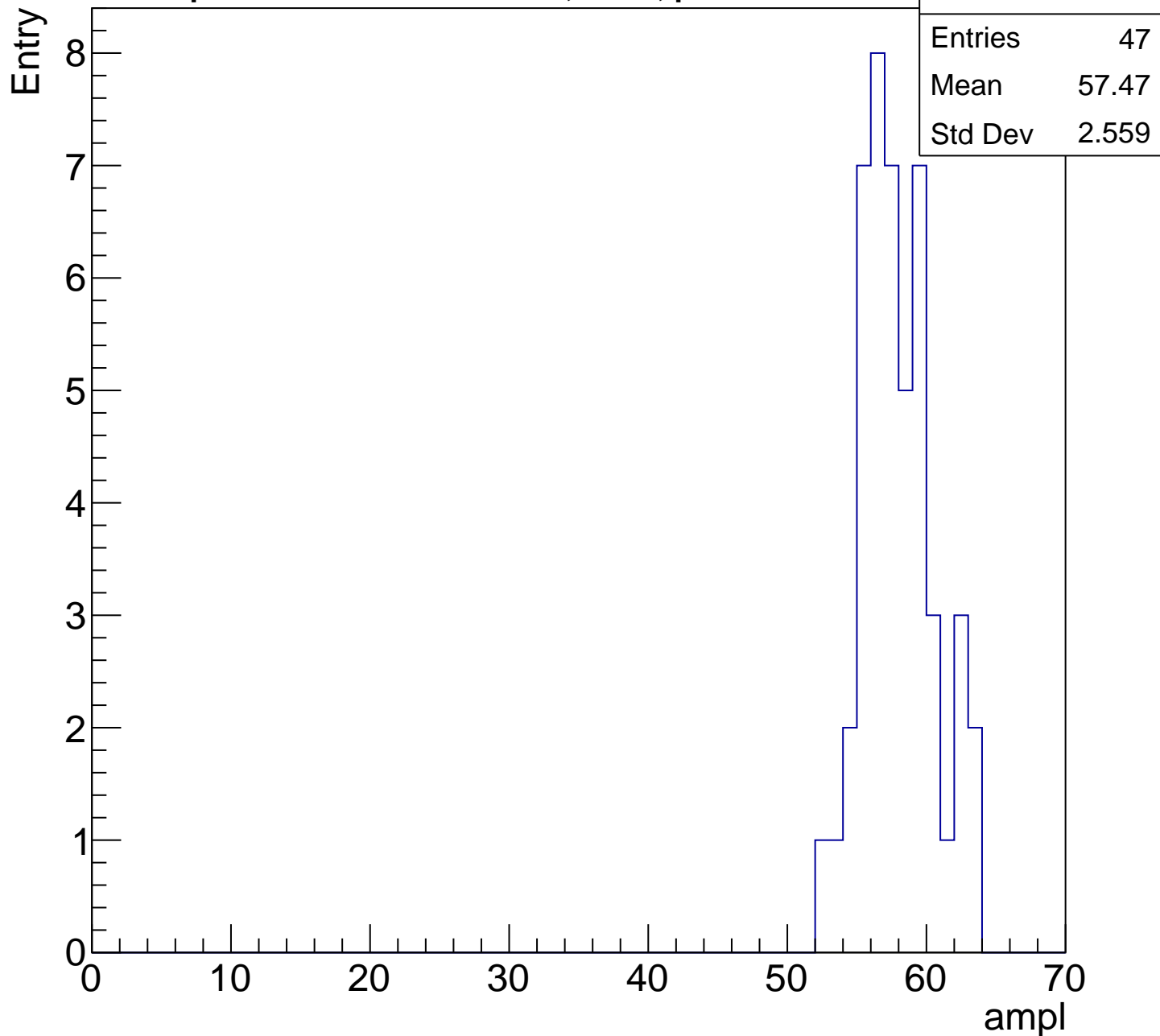
Entry

Entries	46
Mean	52.76
Std Dev	2.972



# B1L000S, U2-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch4, adc6

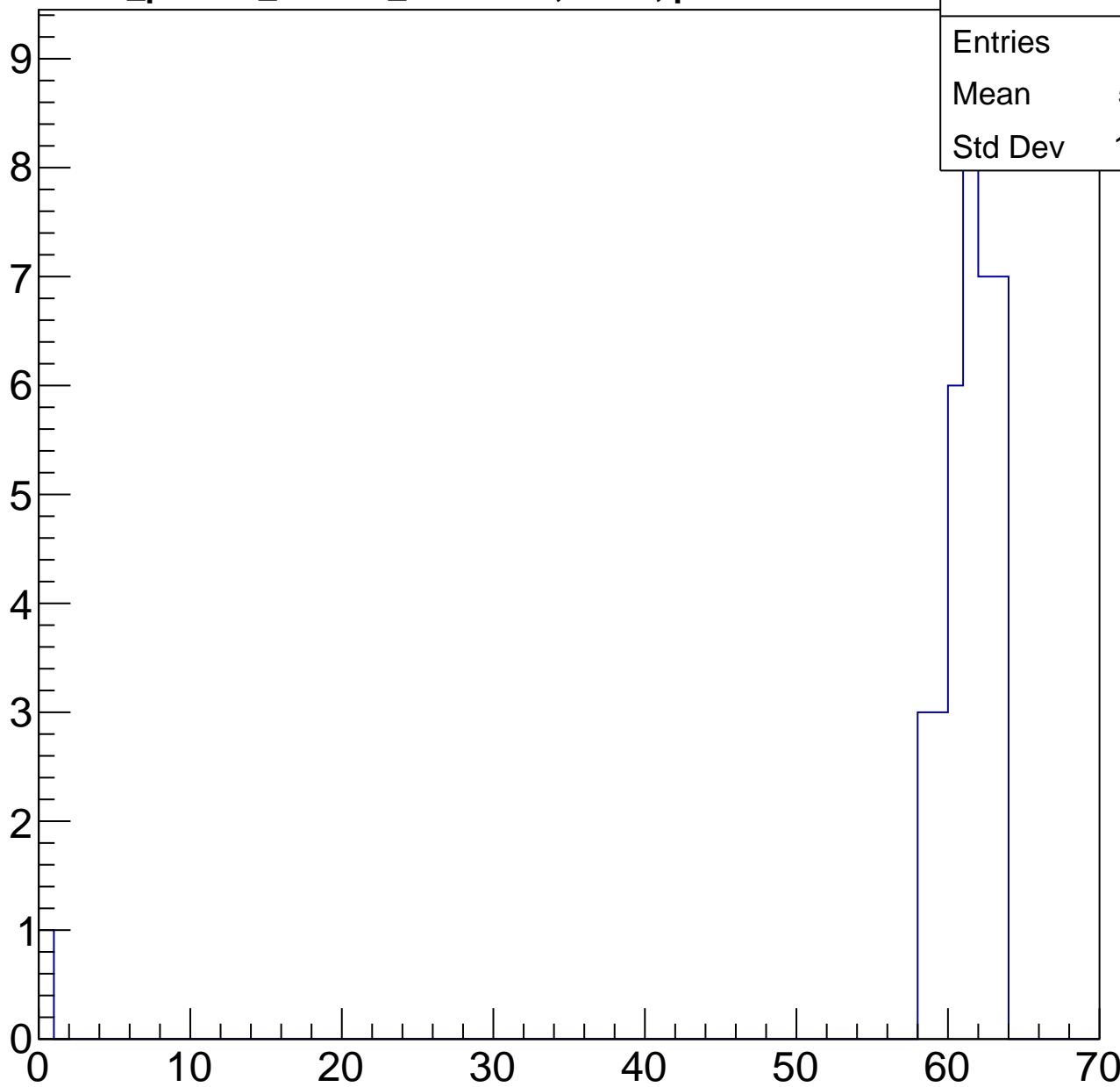
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	36
Mean	59.31
Std Dev	10.13

ampl





# B1L000S, U2-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch5, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	27.71
Std Dev	5.59

**Gaus mean : 28.7777**

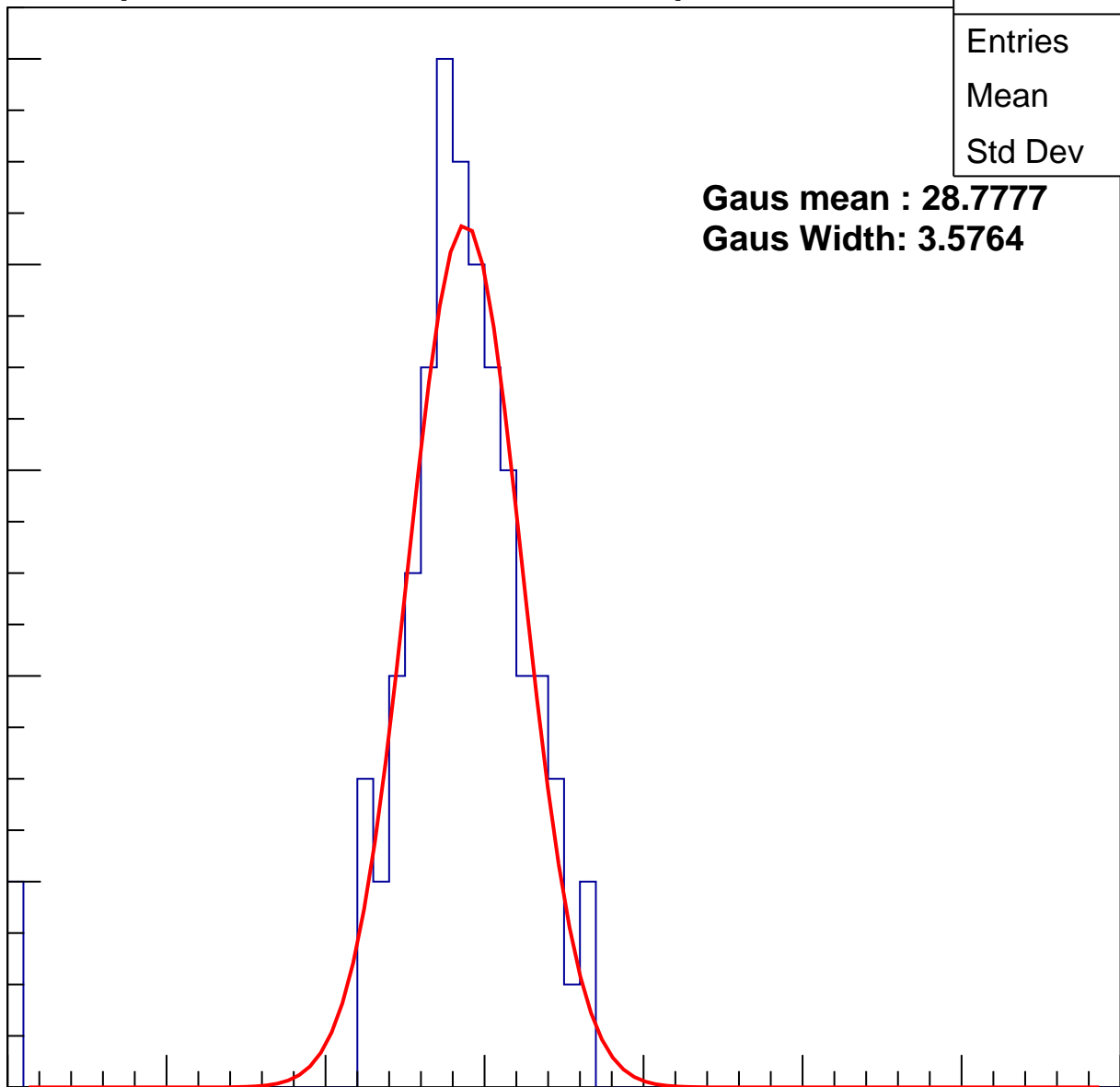
**Gaus Width: 3.5764**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



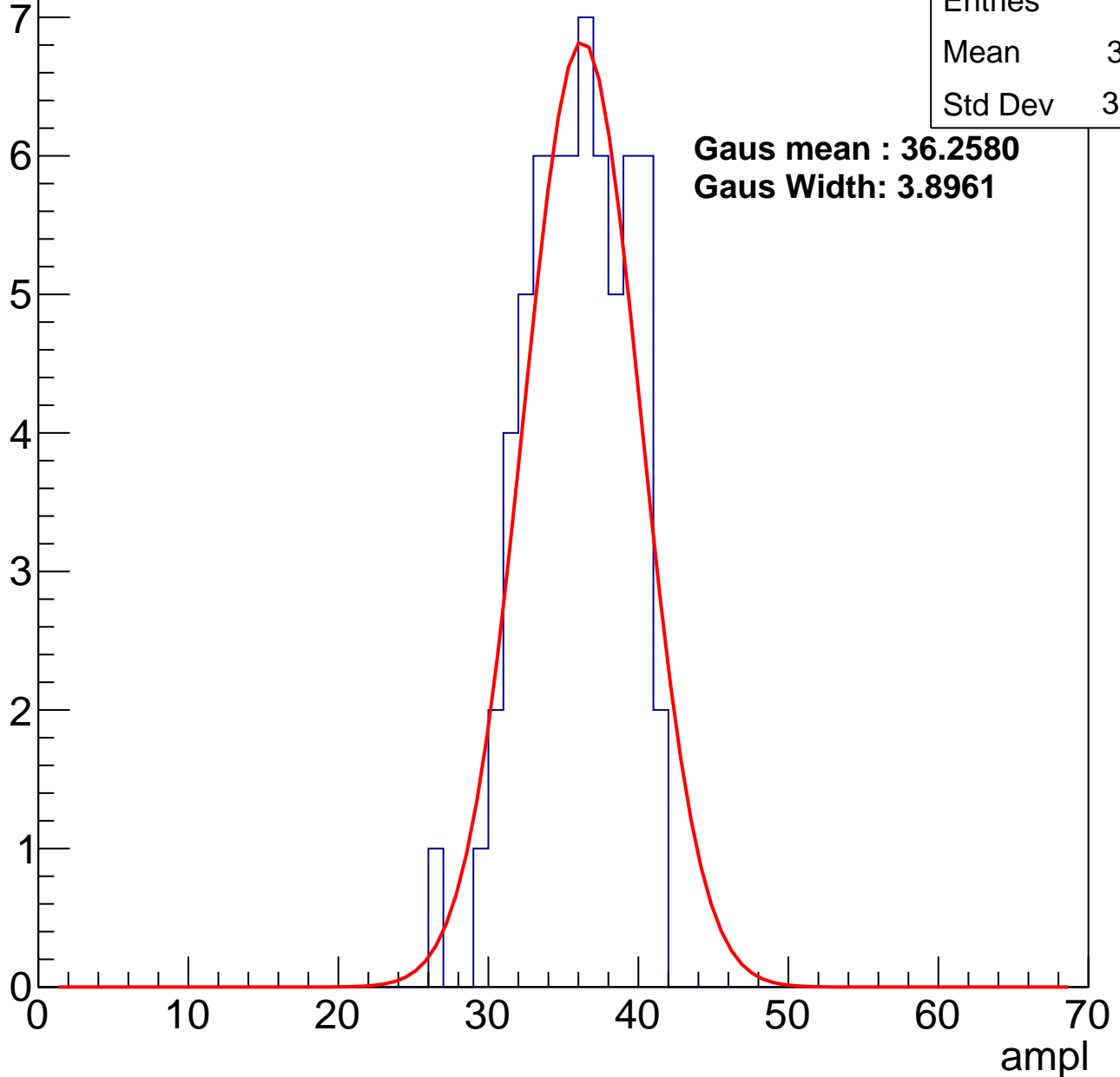
# B1L000S, U2-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.41
Std Dev	3.308

**Gaus mean : 36.2580**  
**Gaus Width: 3.8961**



# B1L000S, U2-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	41.13
Std Dev	3.101

**Gaus mean : 42.0143**

**Gaus Width: 3.2544**

Entry

10

8

6

4

2

0

0

10

20

30

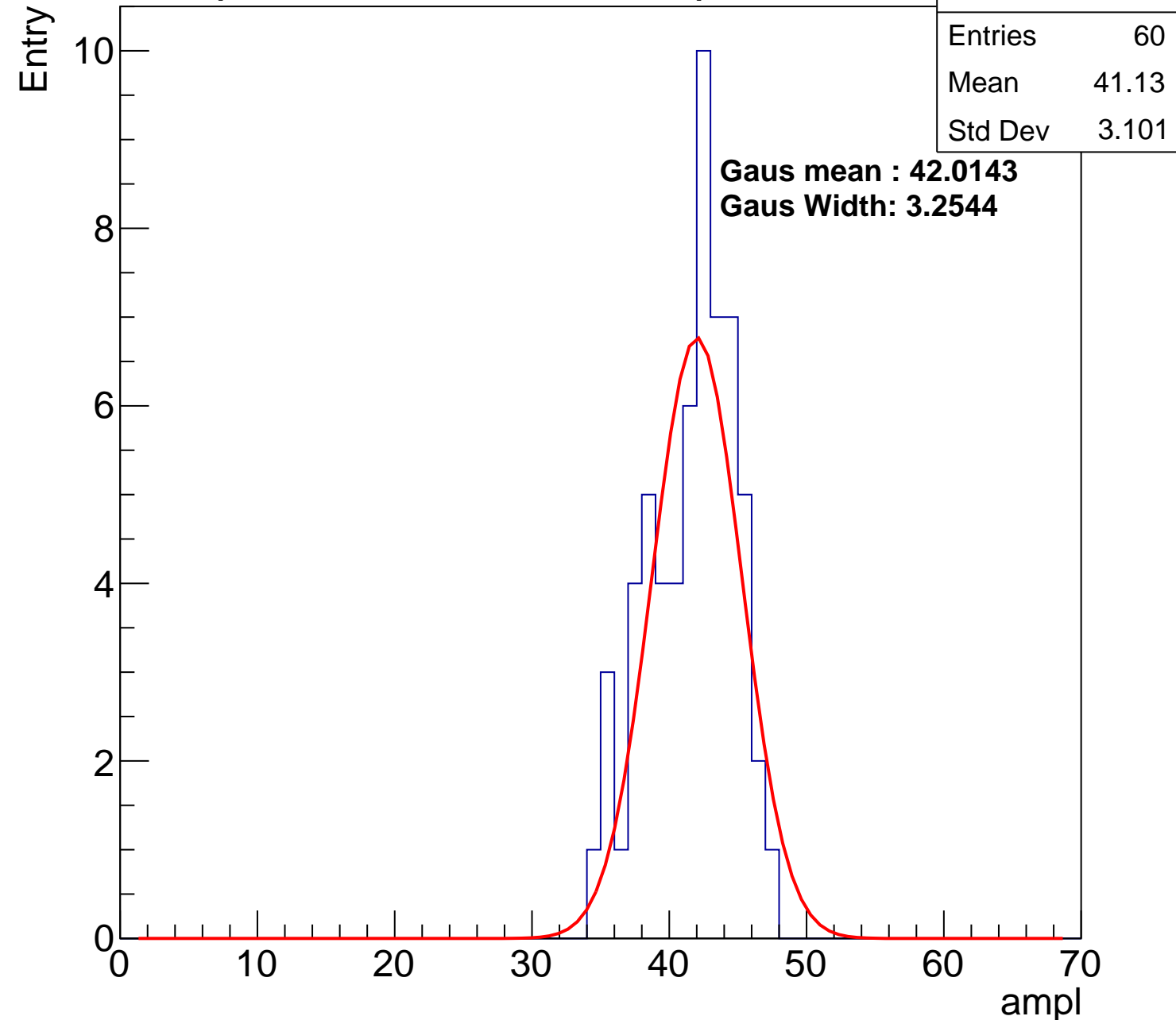
40

50

60

70

ampl

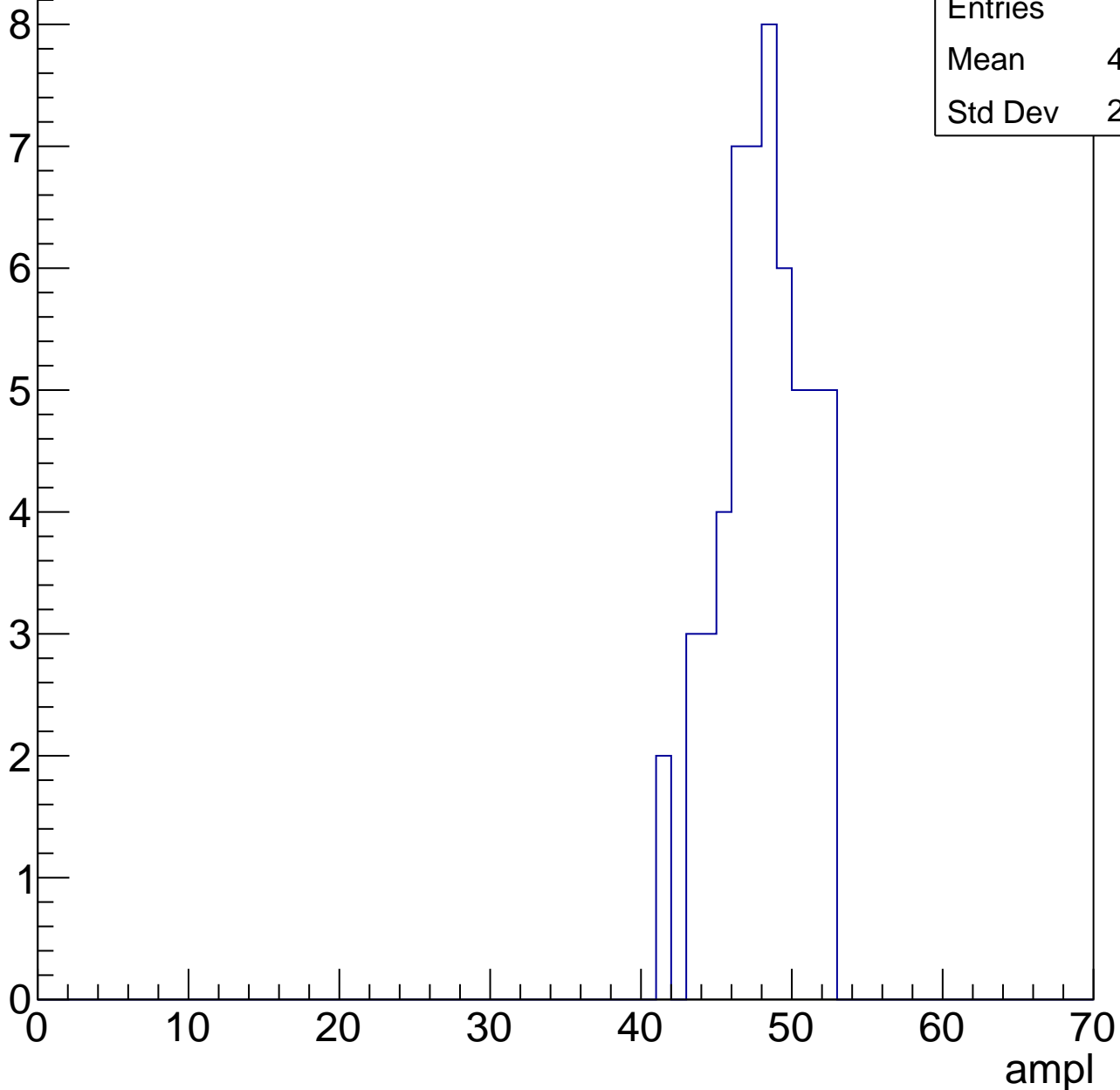


# B1L000S, U2-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

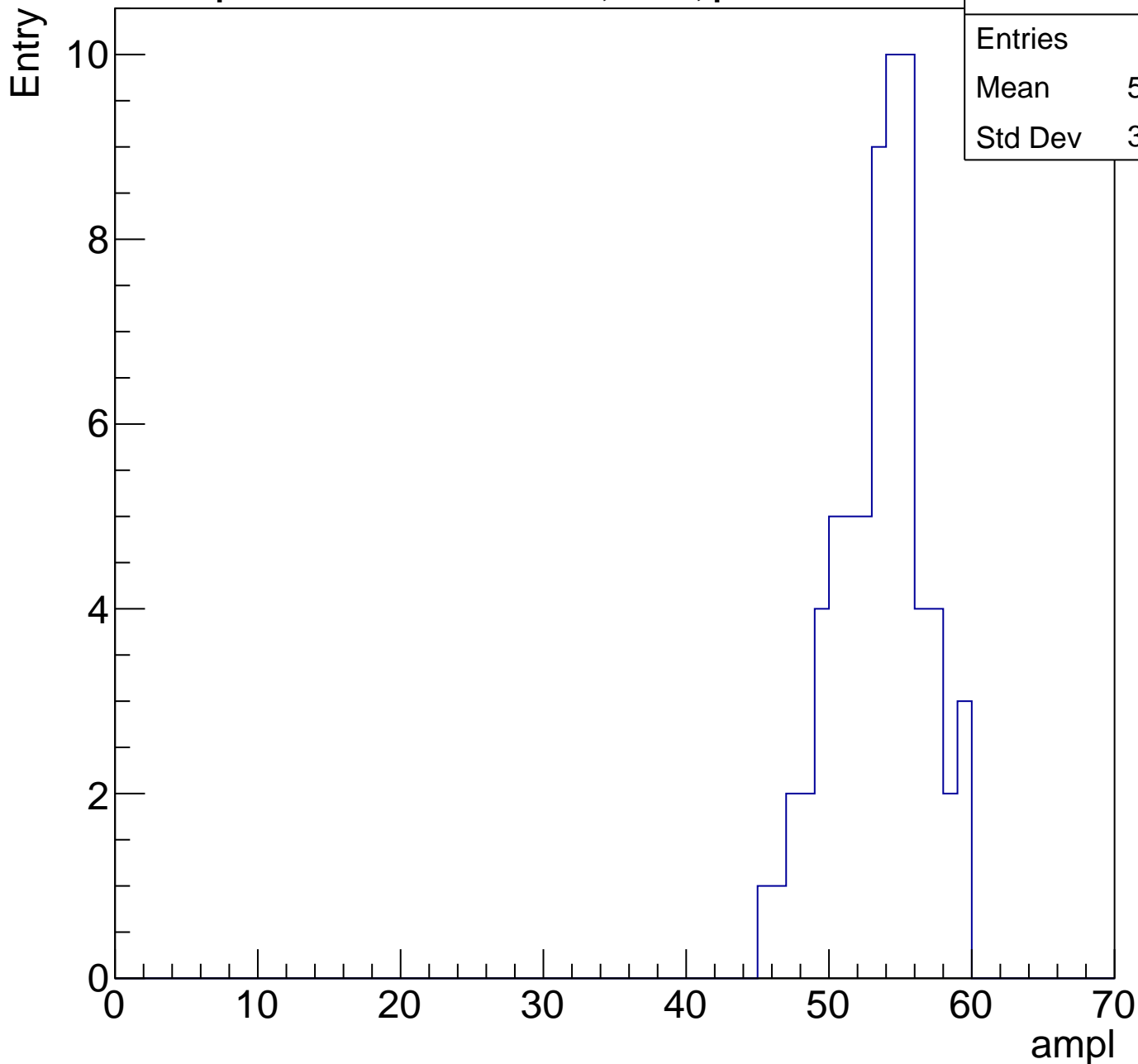
Entries	55
Mean	47.58
Std Dev	2.807



# B1L000S, U2-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	53.04
Std Dev	3.178

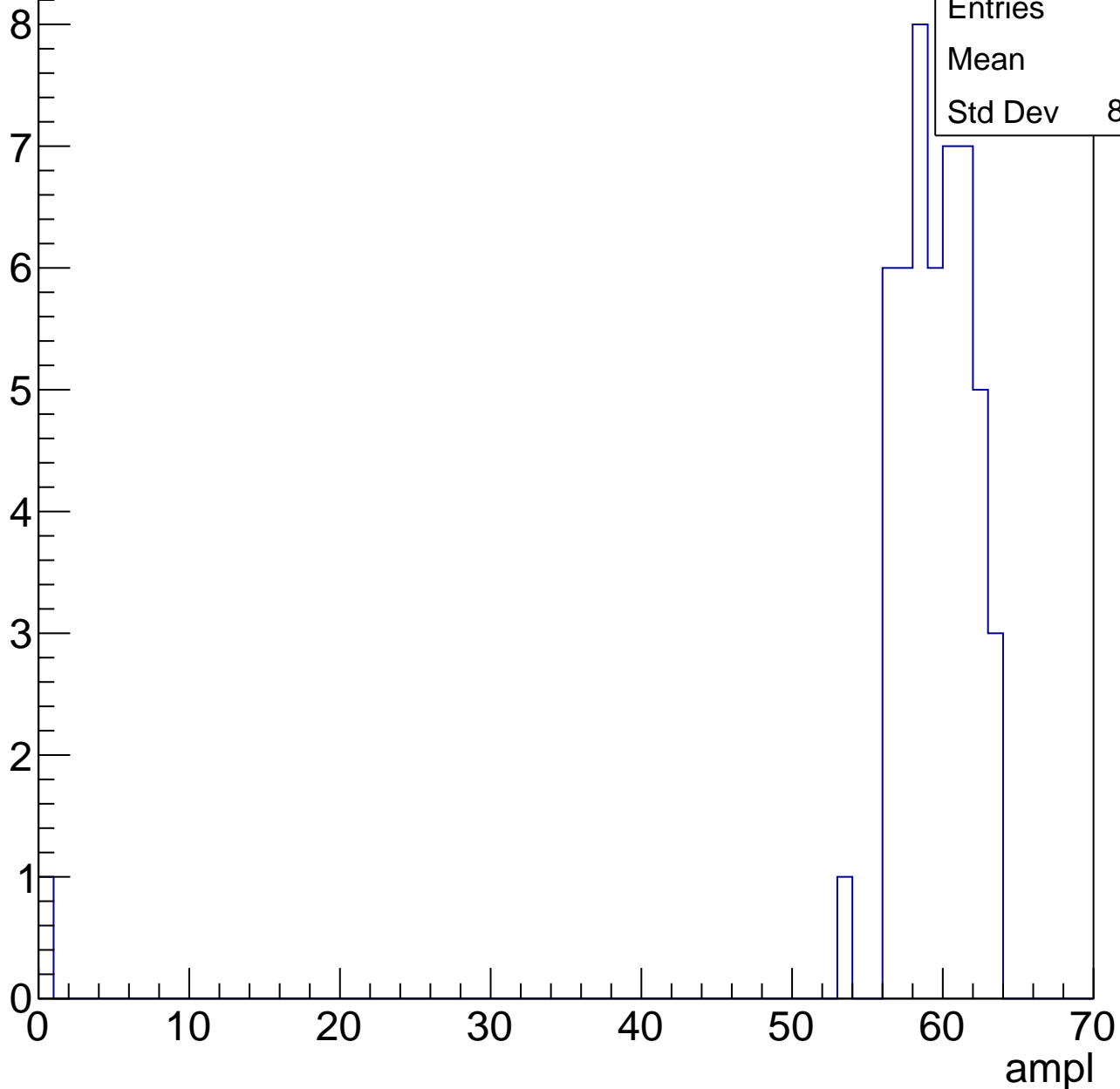


# B1L000S, U2-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	57.9
Std Dev	8.568

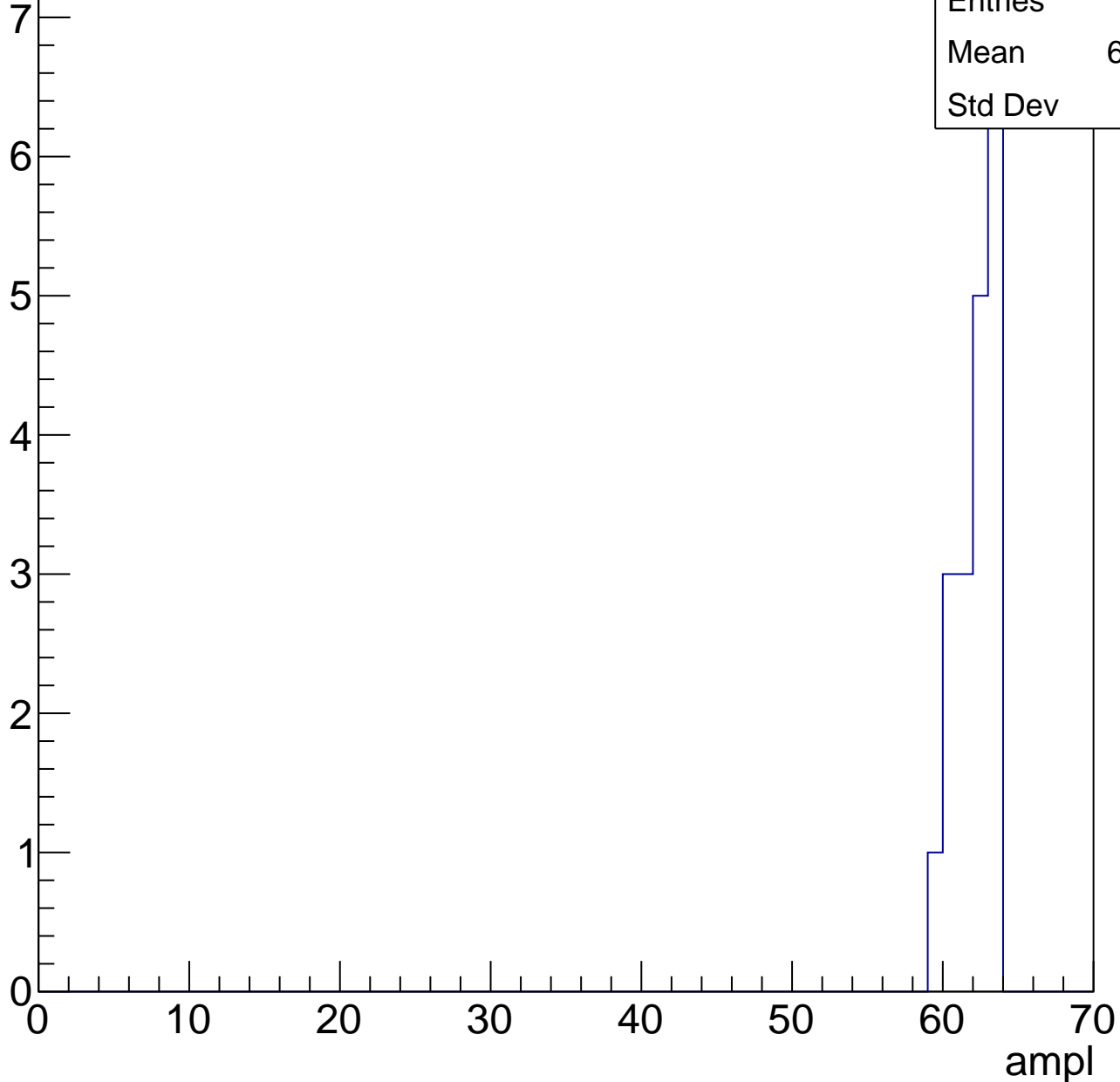


# B1L000S, U2-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	61.74
Std Dev	1.25

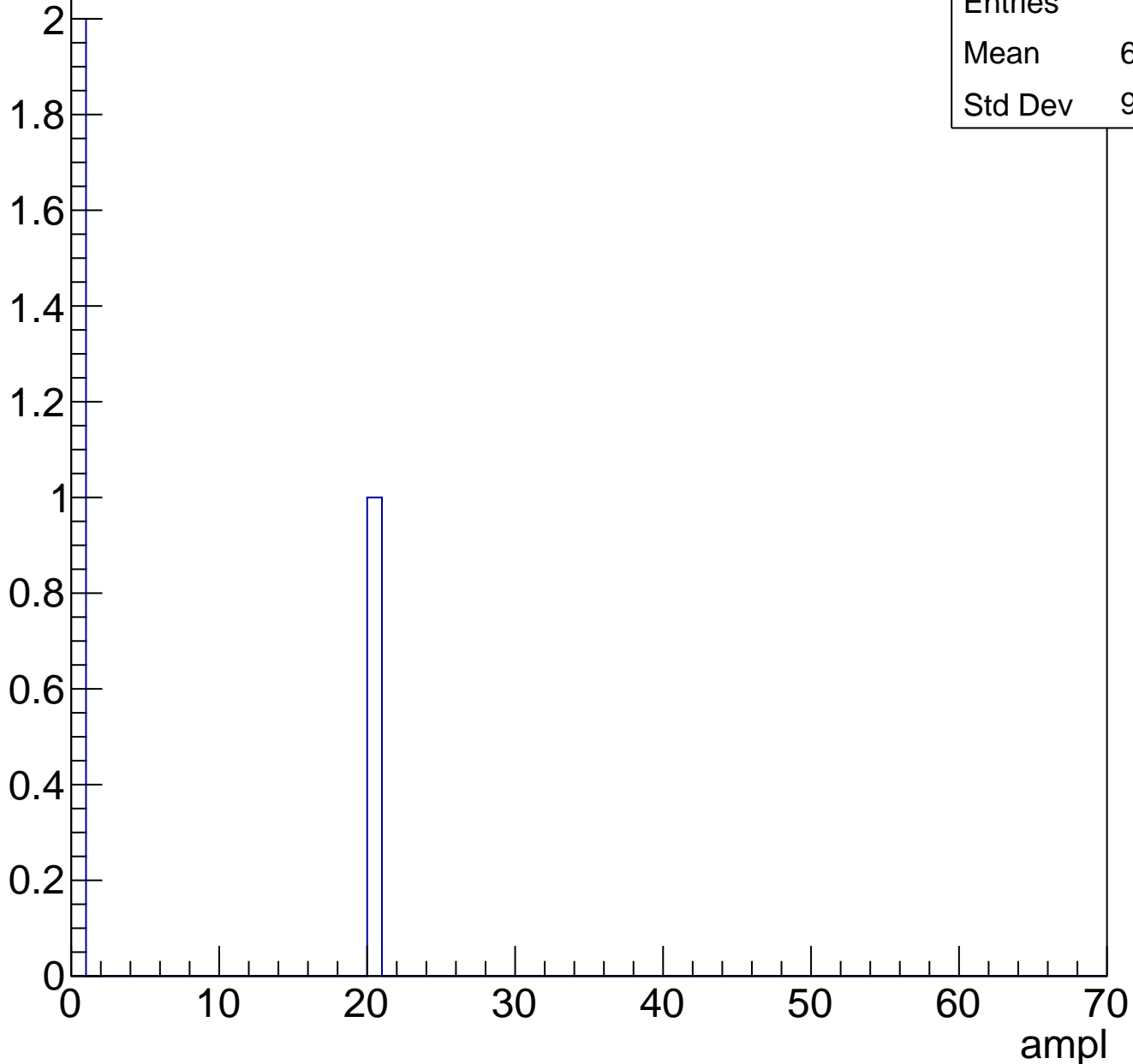




# B1L000S, U2-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B1L000S, U2-ch6, adc0

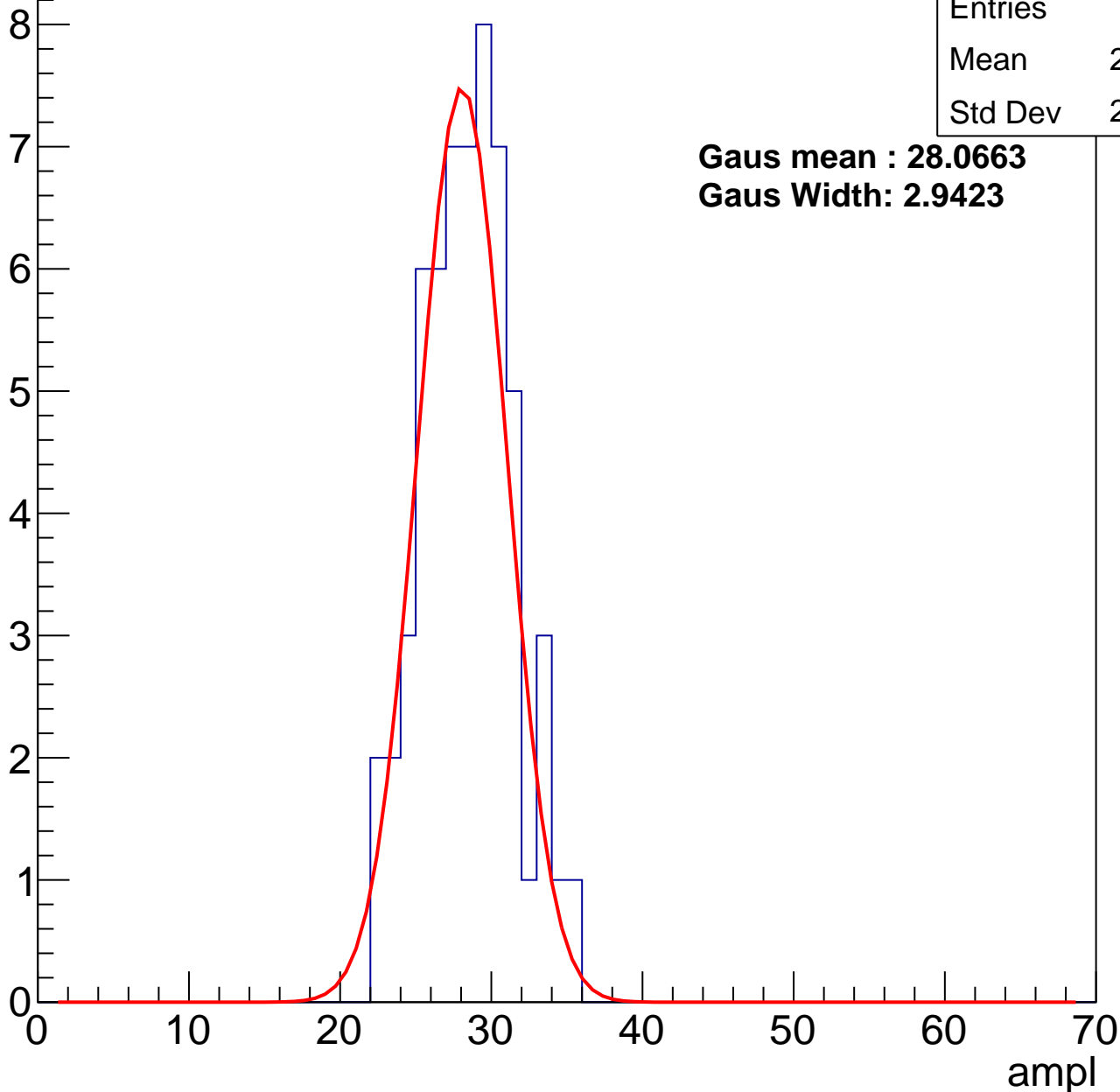
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.97
Std Dev	2.946

**Gaus mean : 28.0663**

**Gaus Width: 2.9423**



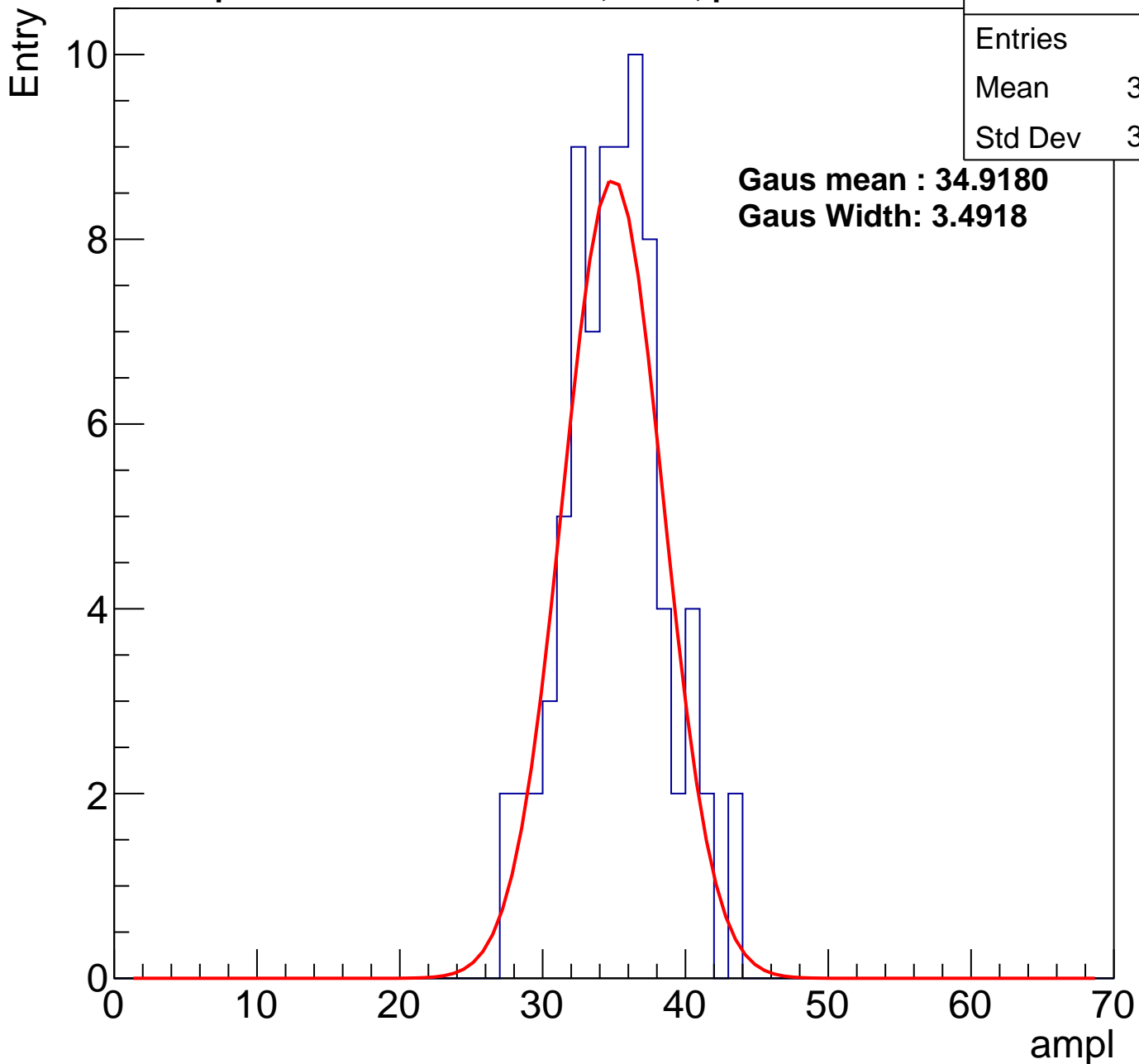
# B1L000S, U2-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	34.59
Std Dev	3.477

**Gaus mean : 34.9180**

**Gaus Width: 3.4918**



# B1L000S, U2-ch6, adc2

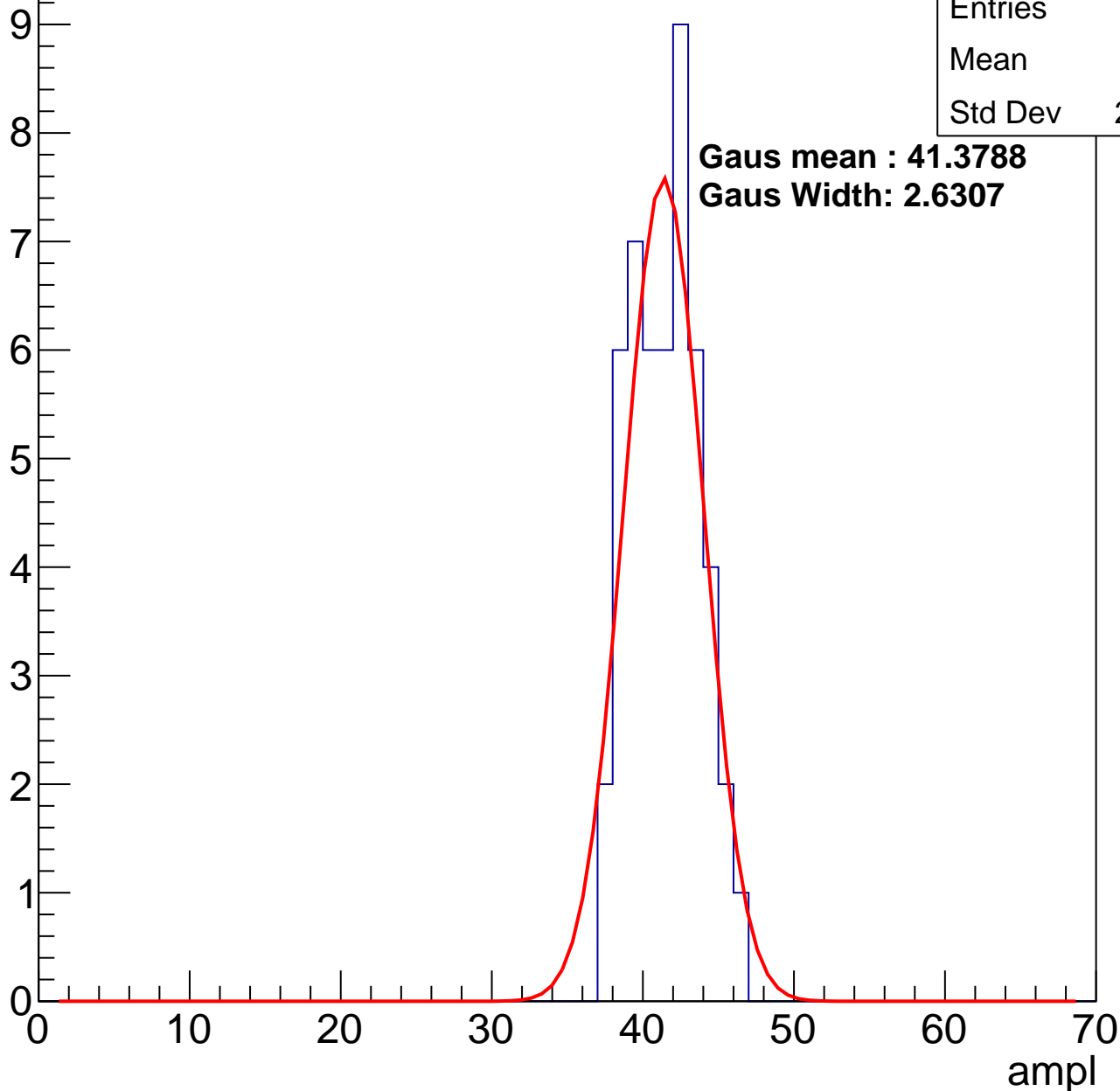
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	41
Std Dev	2.241

**Gaus mean : 41.3788**

**Gaus Width: 2.6307**

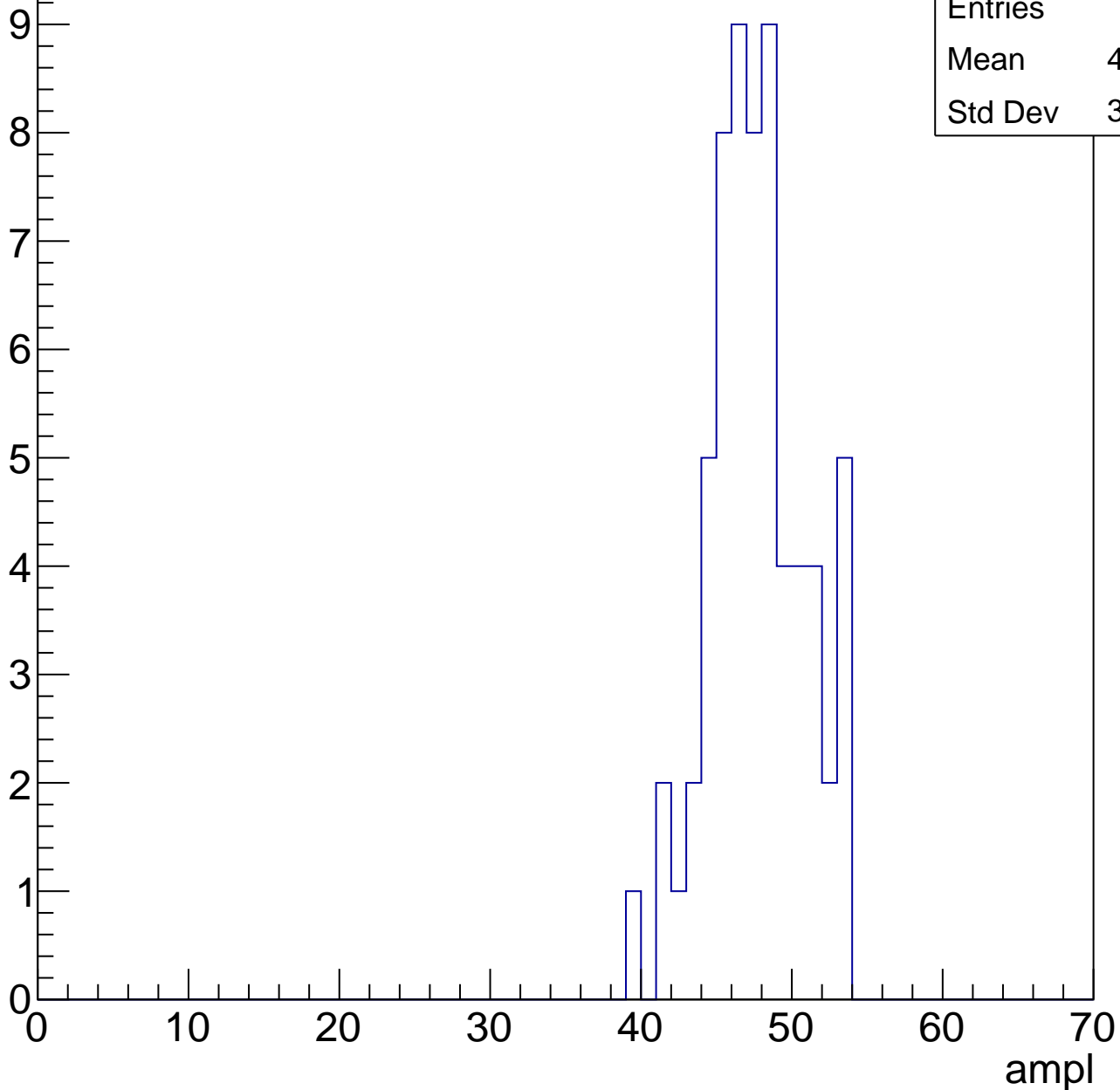


# B1L000S, U2-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	47.19
Std Dev	3.142

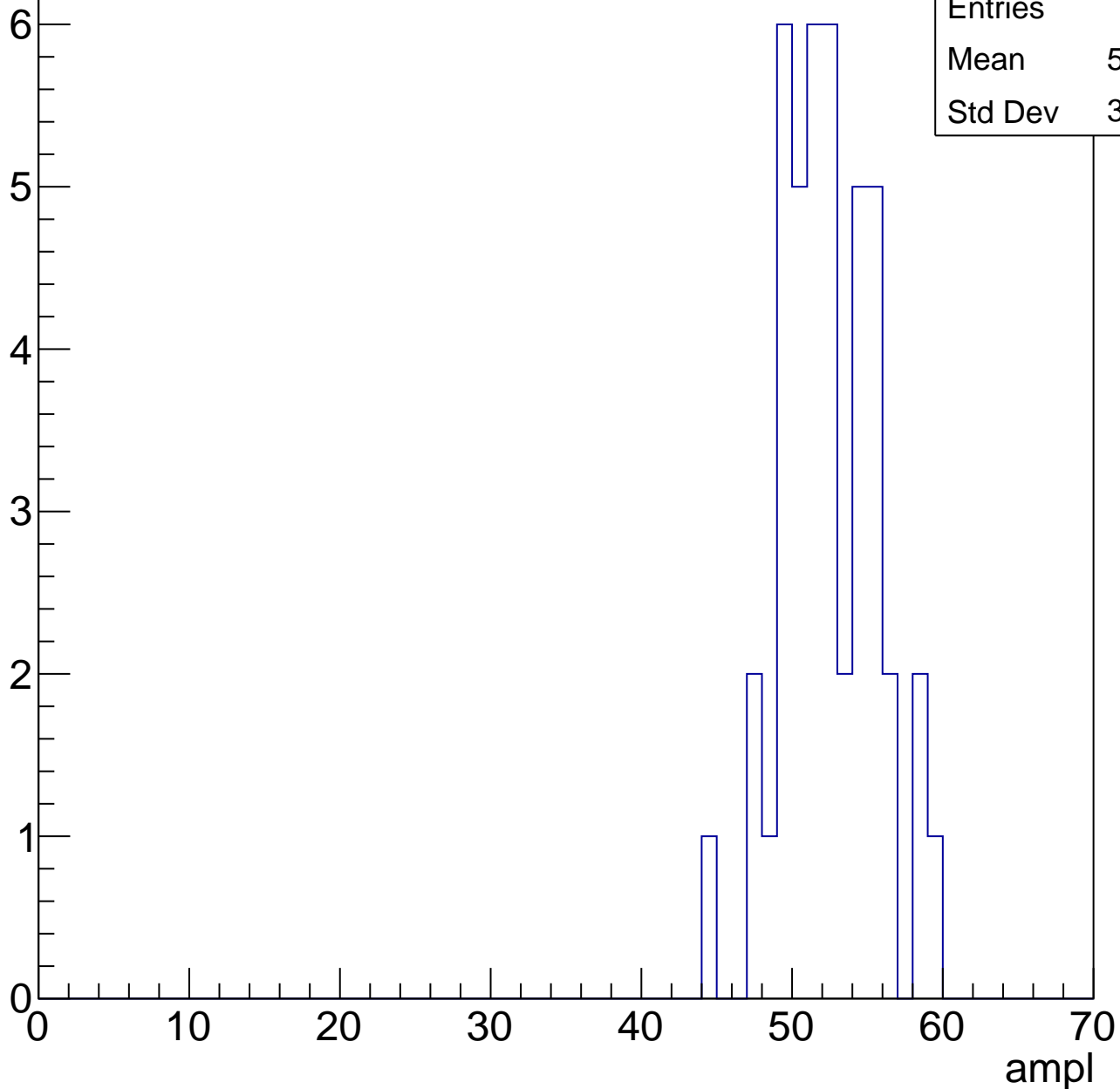


# B1L000S, U2-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	51.95
Std Dev	3.126

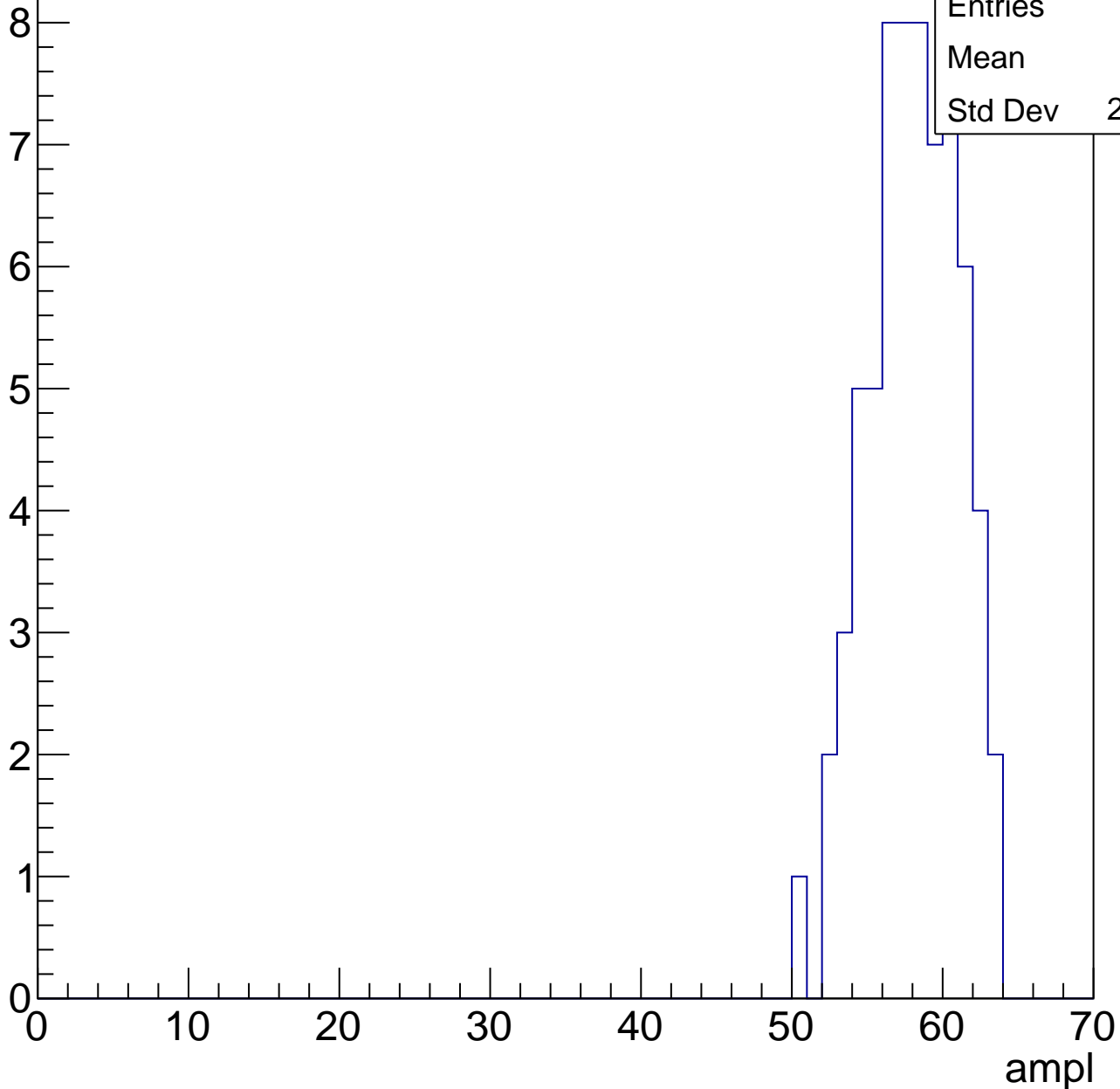


# B1L000S, U2-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	57.6
Std Dev	2.922

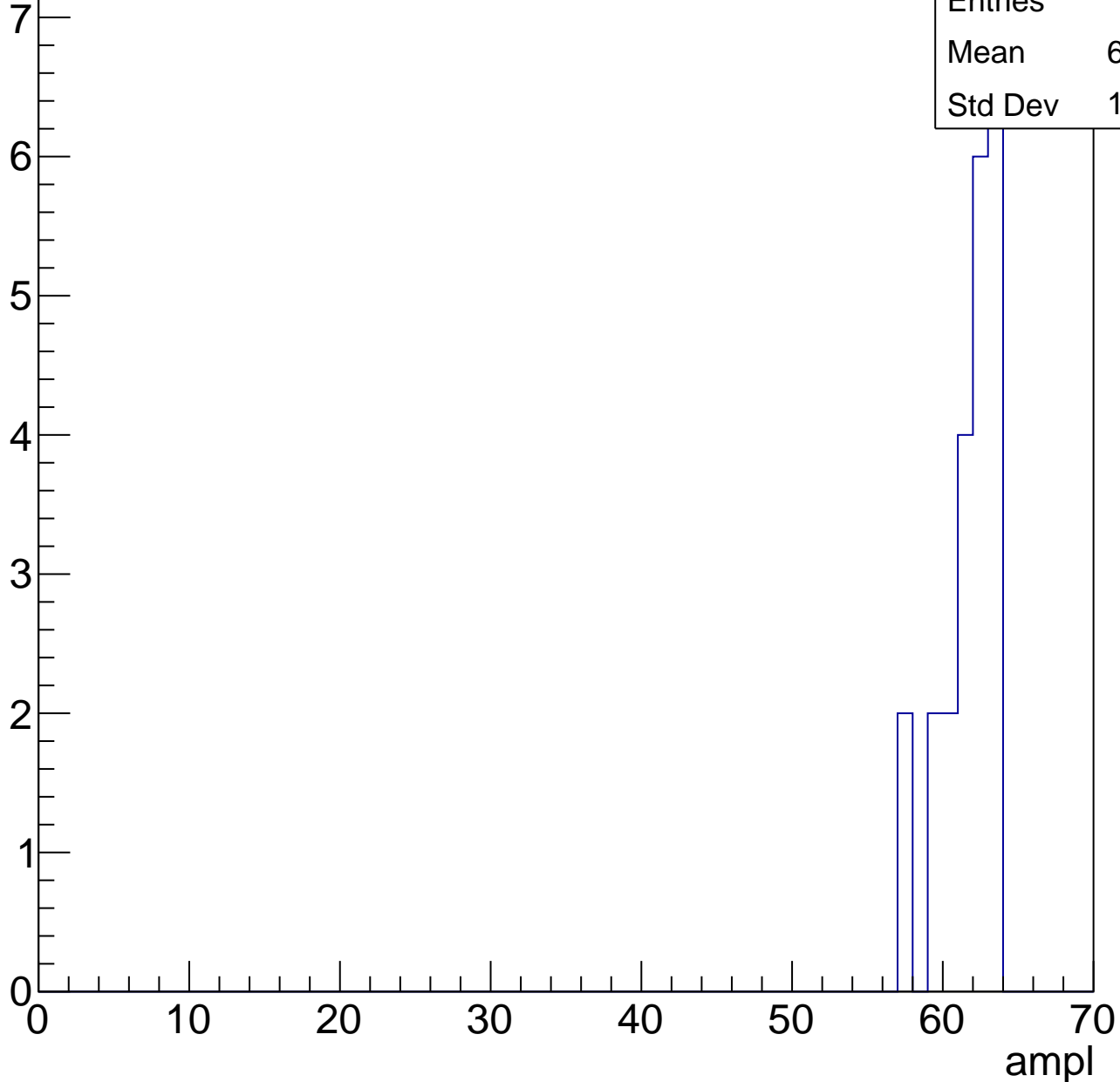


# B1L000S, U2-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	61.26
Std Dev	1.799





# B1L000S, U2-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

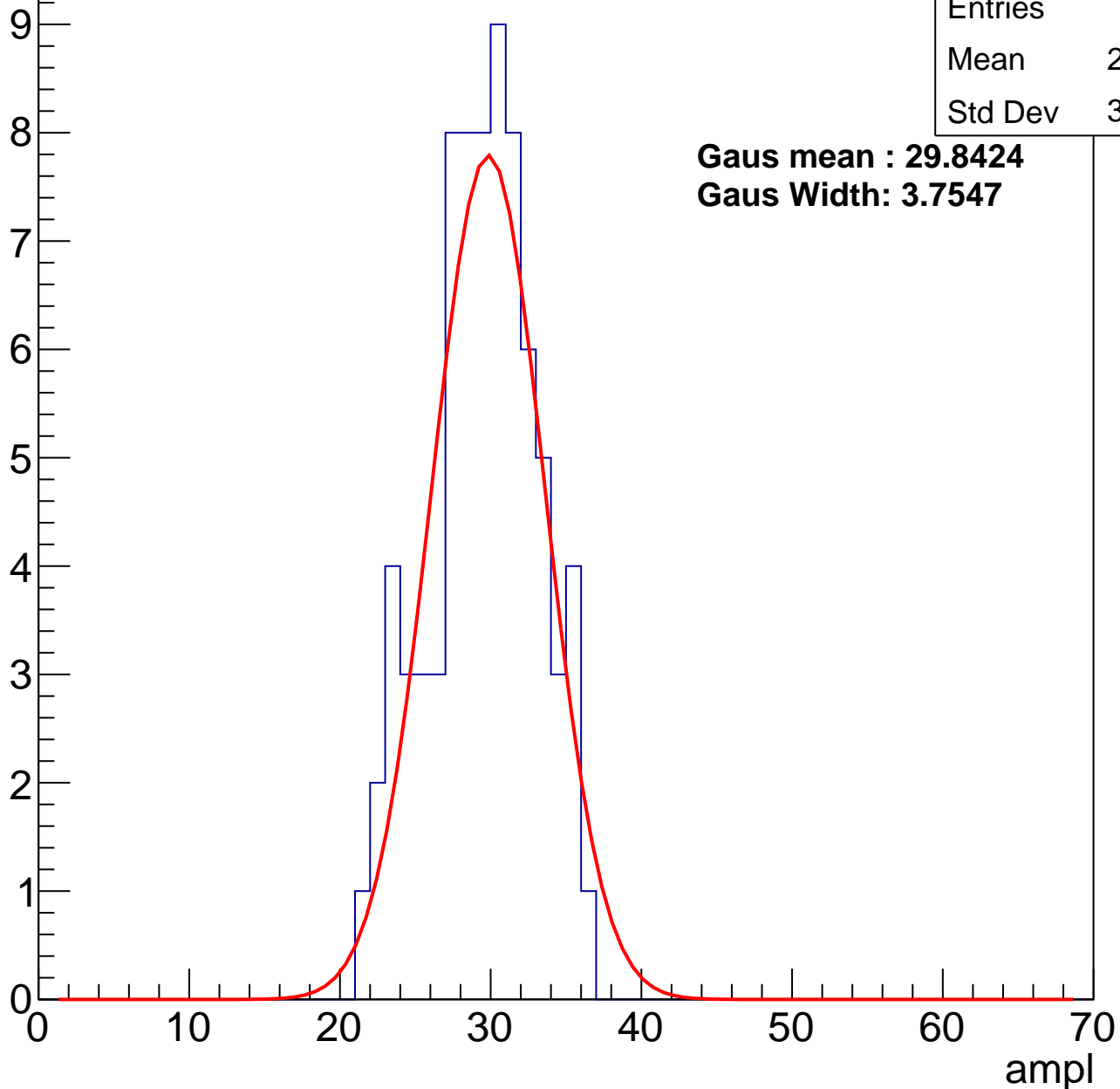


Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U2-ch7, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch7, adc1

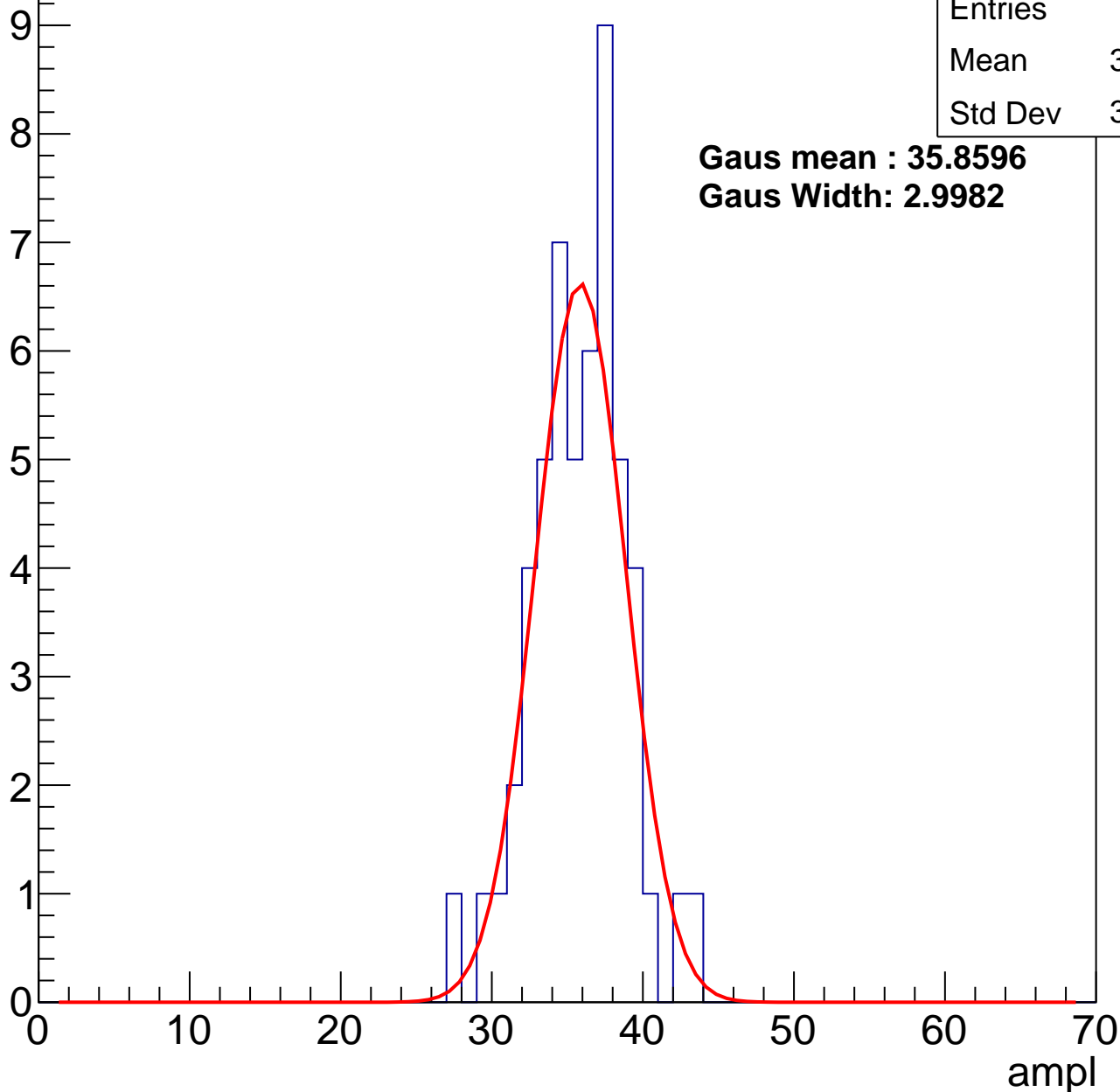
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	35.36
Std Dev	3.078

**Gaus mean : 35.8596**

**Gaus Width: 2.9982**



# B1L000S, U2-ch7, adc2

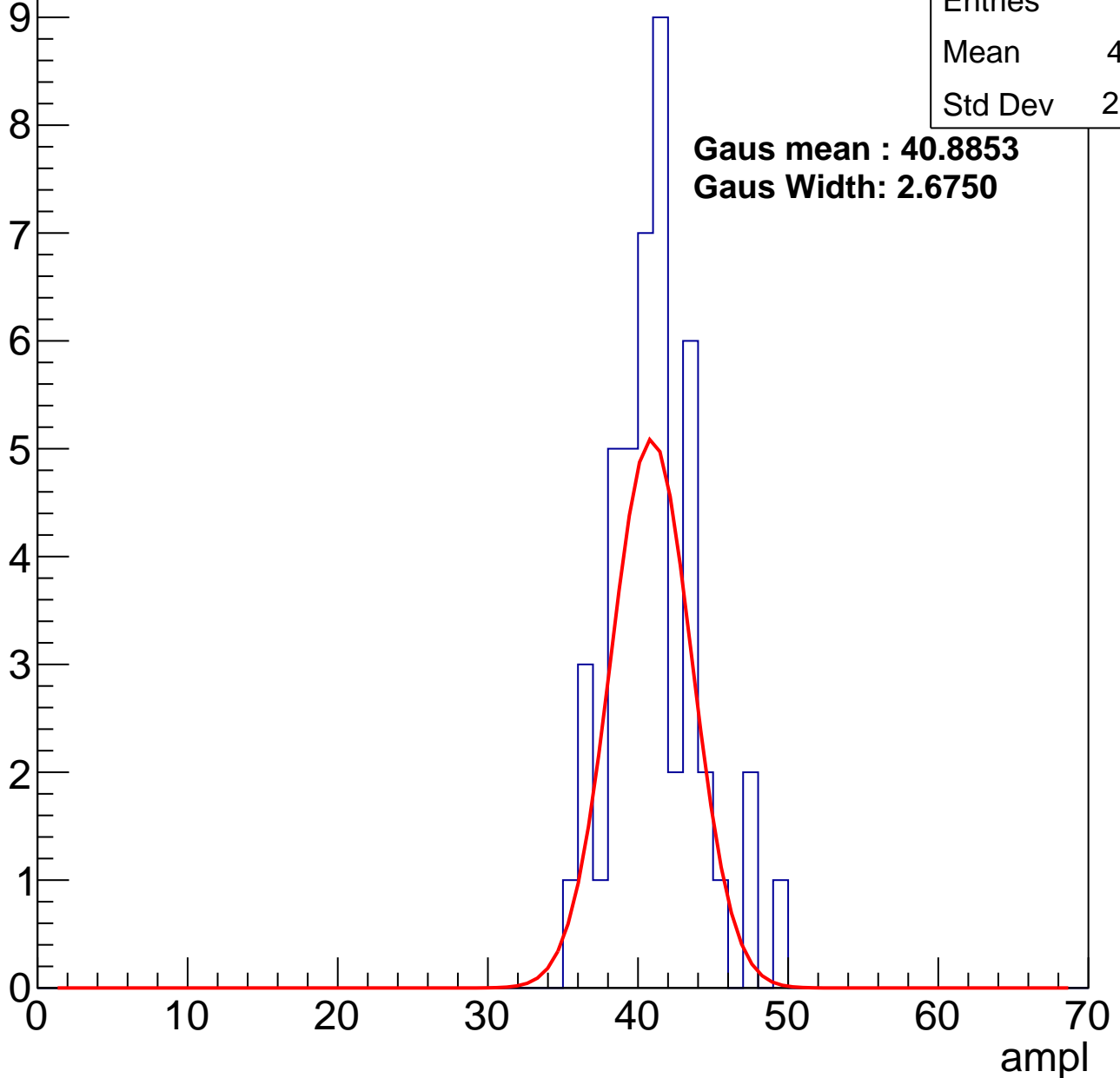
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	40.71
Std Dev	2.949

**Gaus mean : 40.8853**

**Gaus Width: 2.6750**

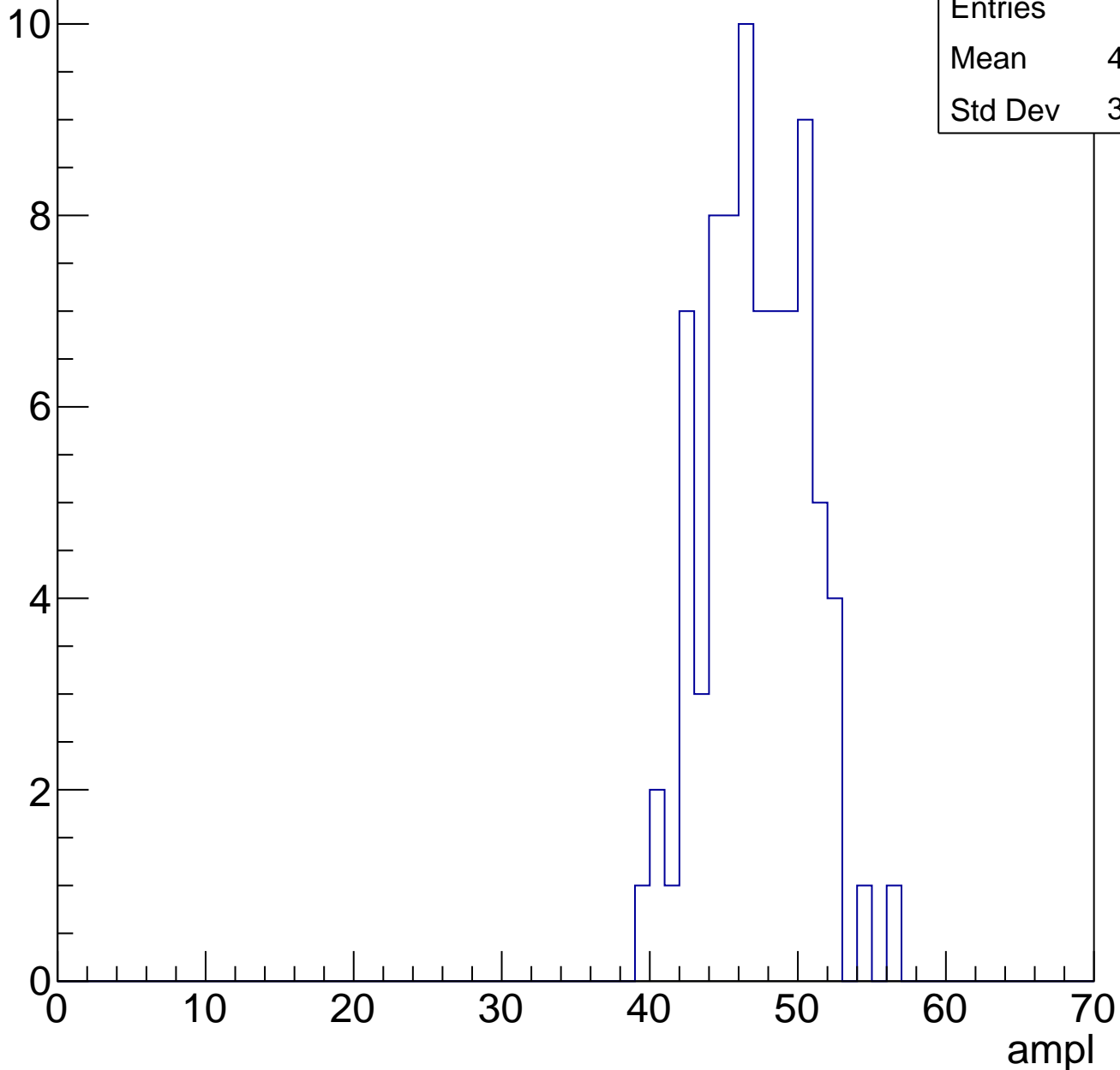


# B1L000S, U2-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	46.74
Std Dev	3.435

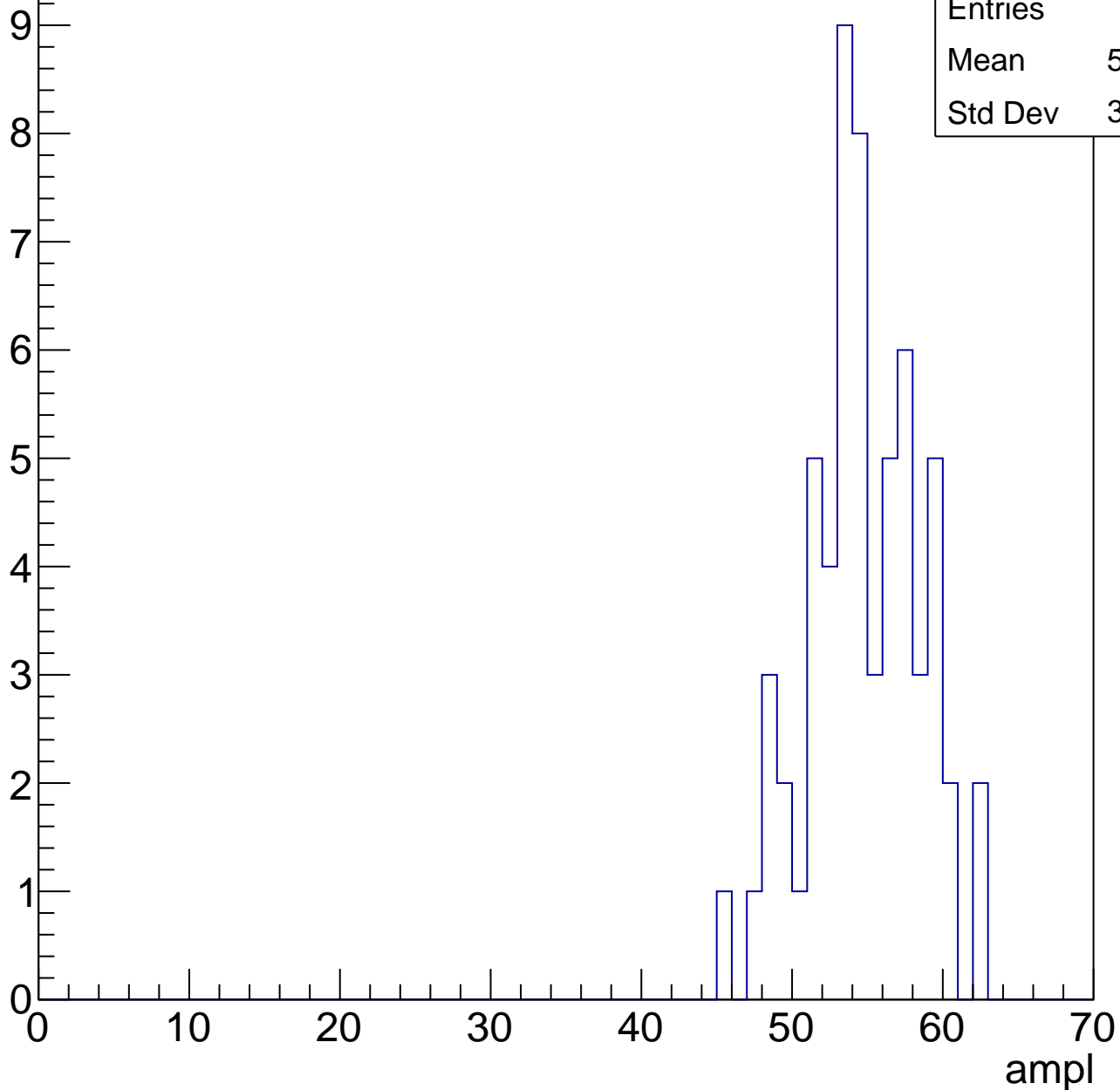
Entry



# B1L000S, U2-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



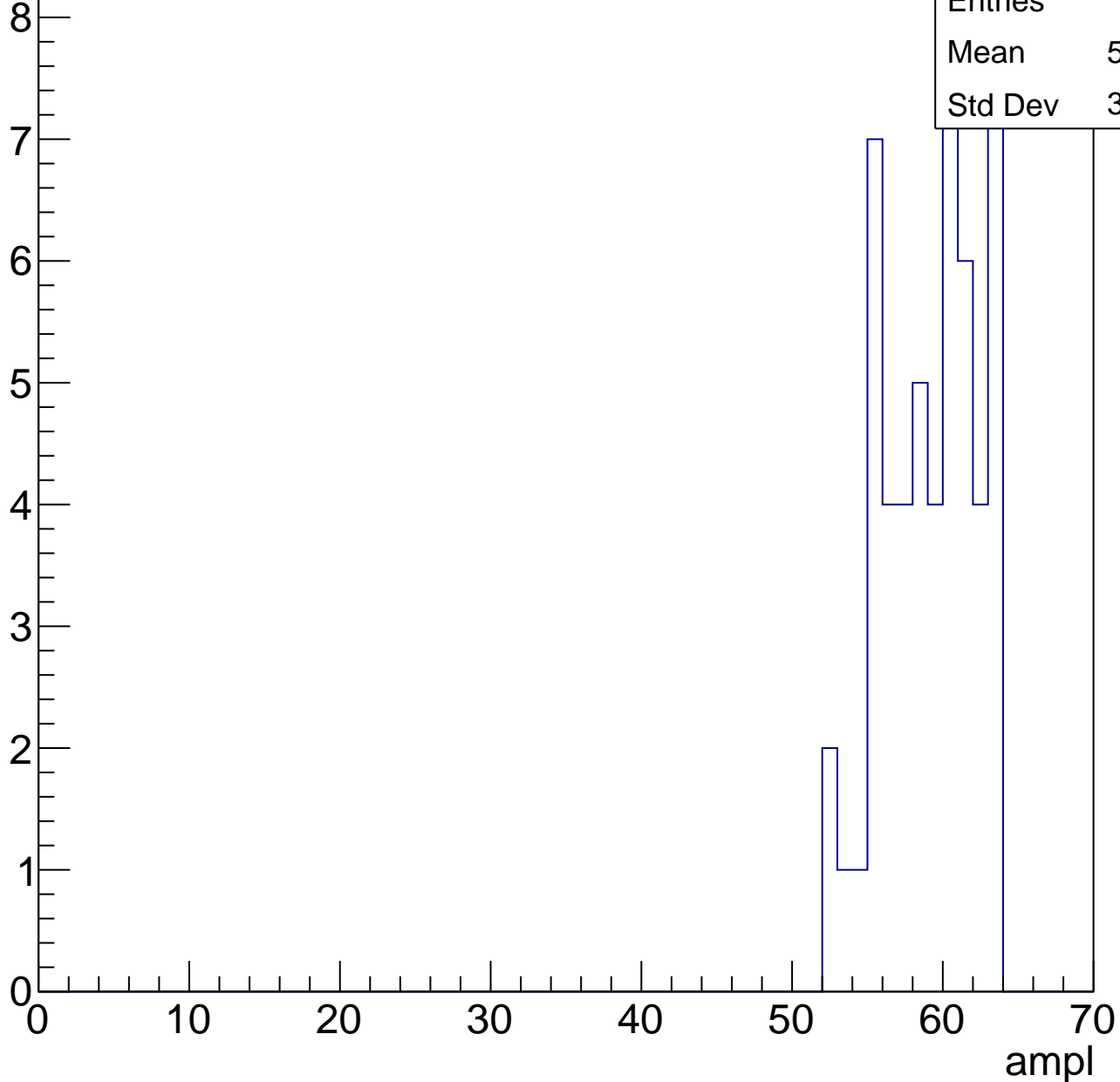
Entries	60
Mean	54.27
Std Dev	3.696

# B1L000S, U2-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

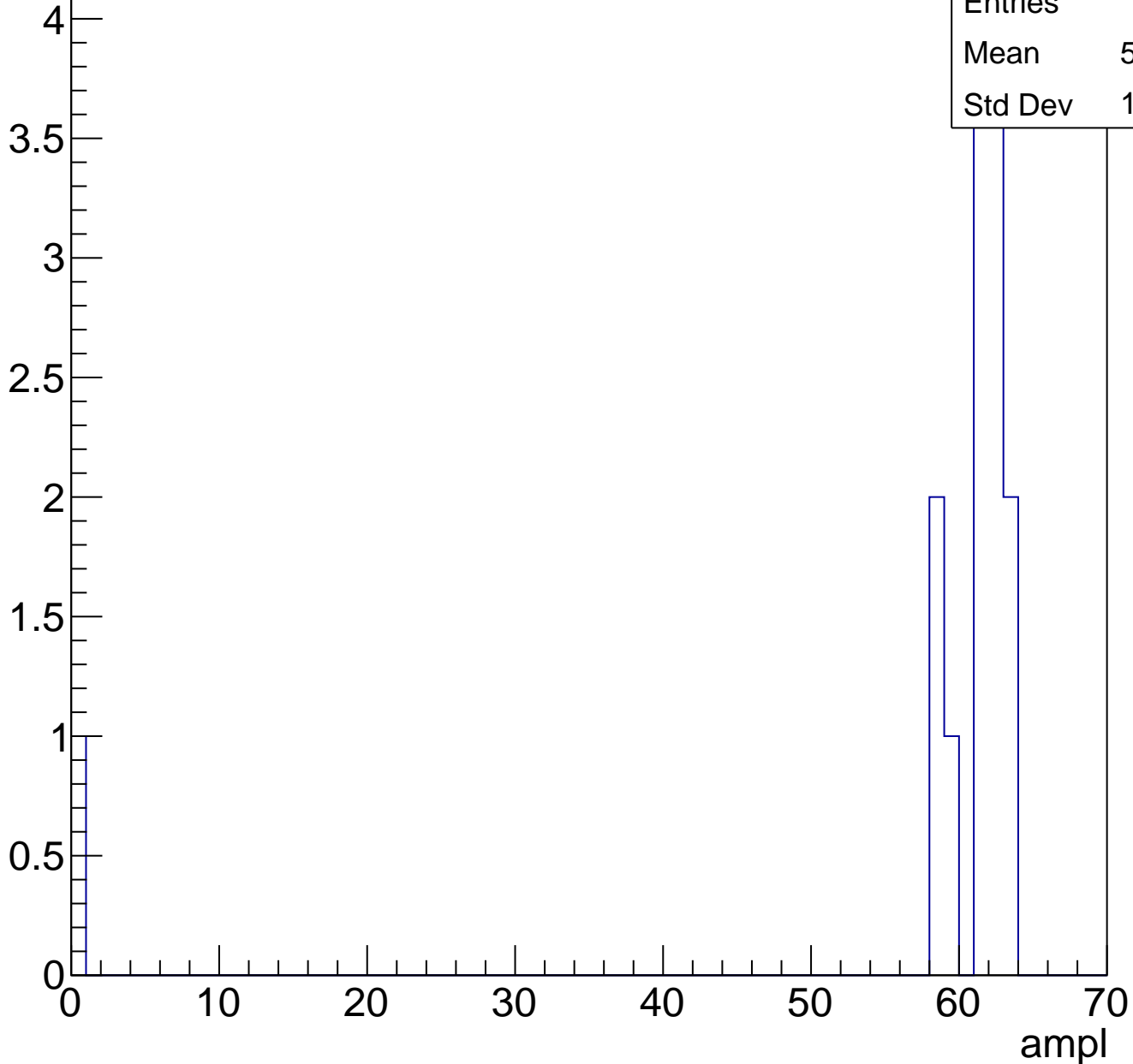
Entries	54
Mean	58.74
Std Dev	3.104



# B1L000S, U2-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U2-ch8, adc0

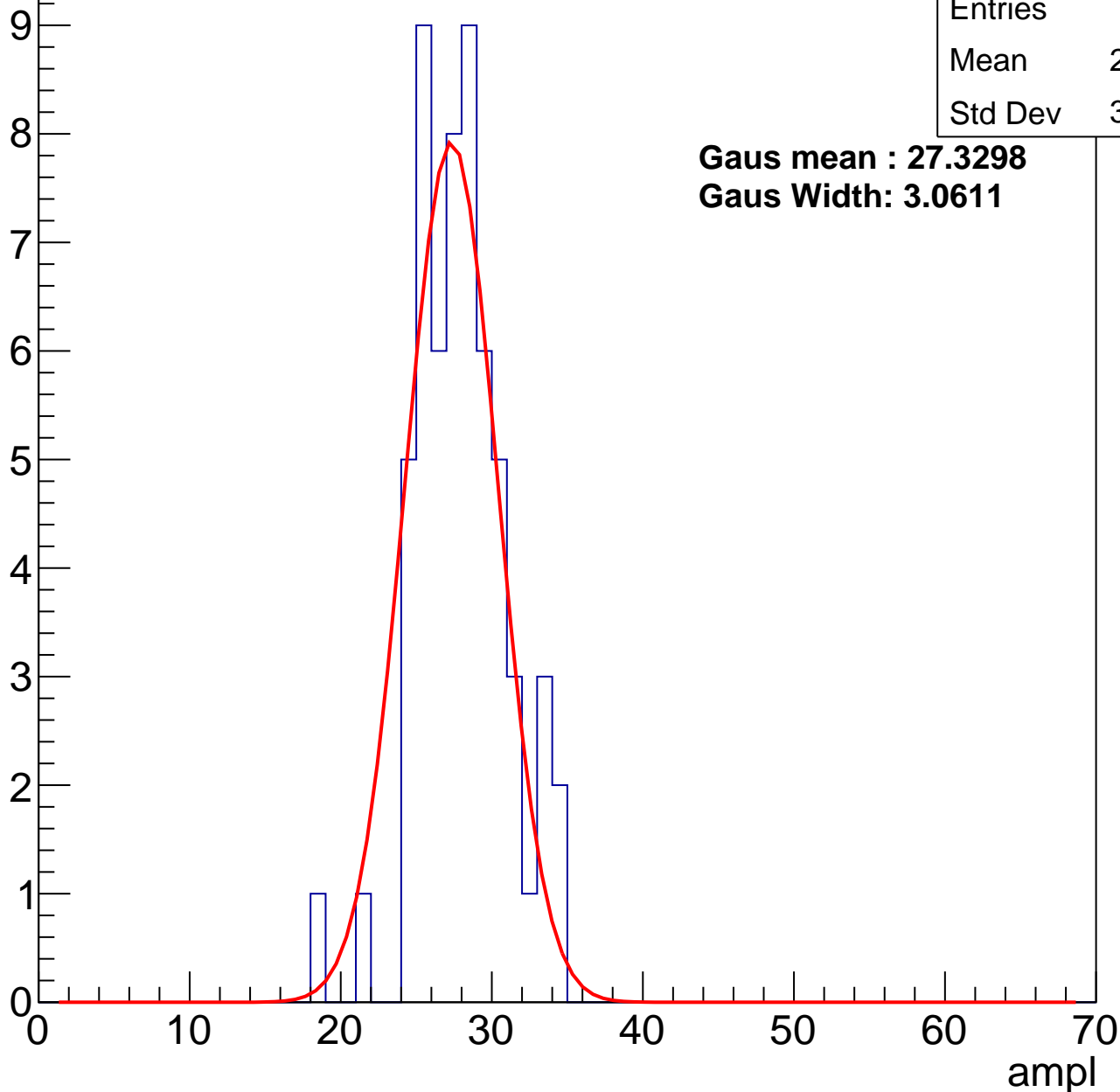
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.53
Std Dev	3.038

**Gaus mean : 27.3298**

**Gaus Width: 3.0611**



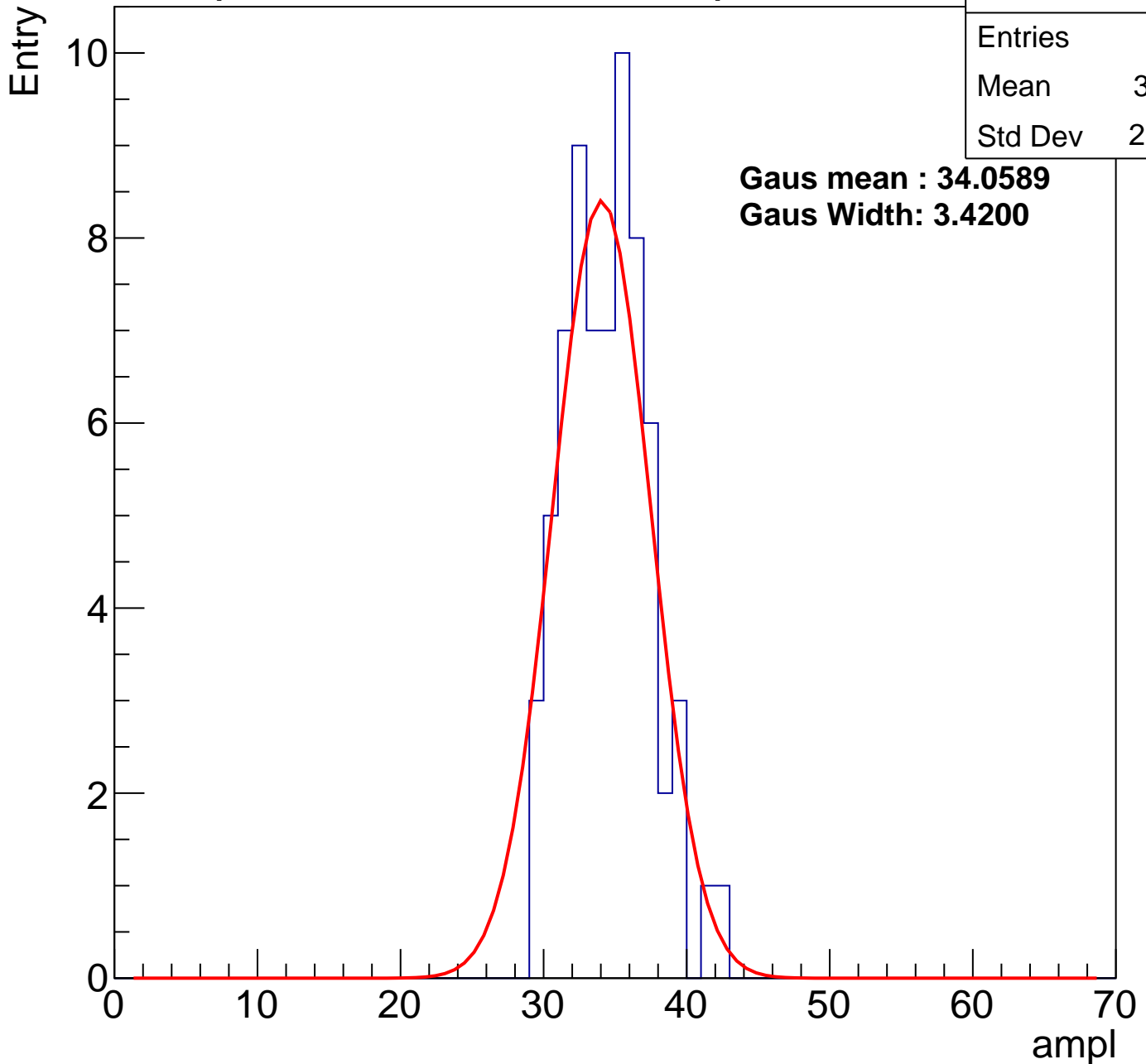
# B1L000S, U2-ch8, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	34.01
Std Dev	2.892

**Gaus mean : 34.0589**

**Gaus Width: 3.4200**



# B1L000S, U2-ch8, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	59
Mean	40.59
Std Dev	2.781

**Gaus mean : 40.8314**

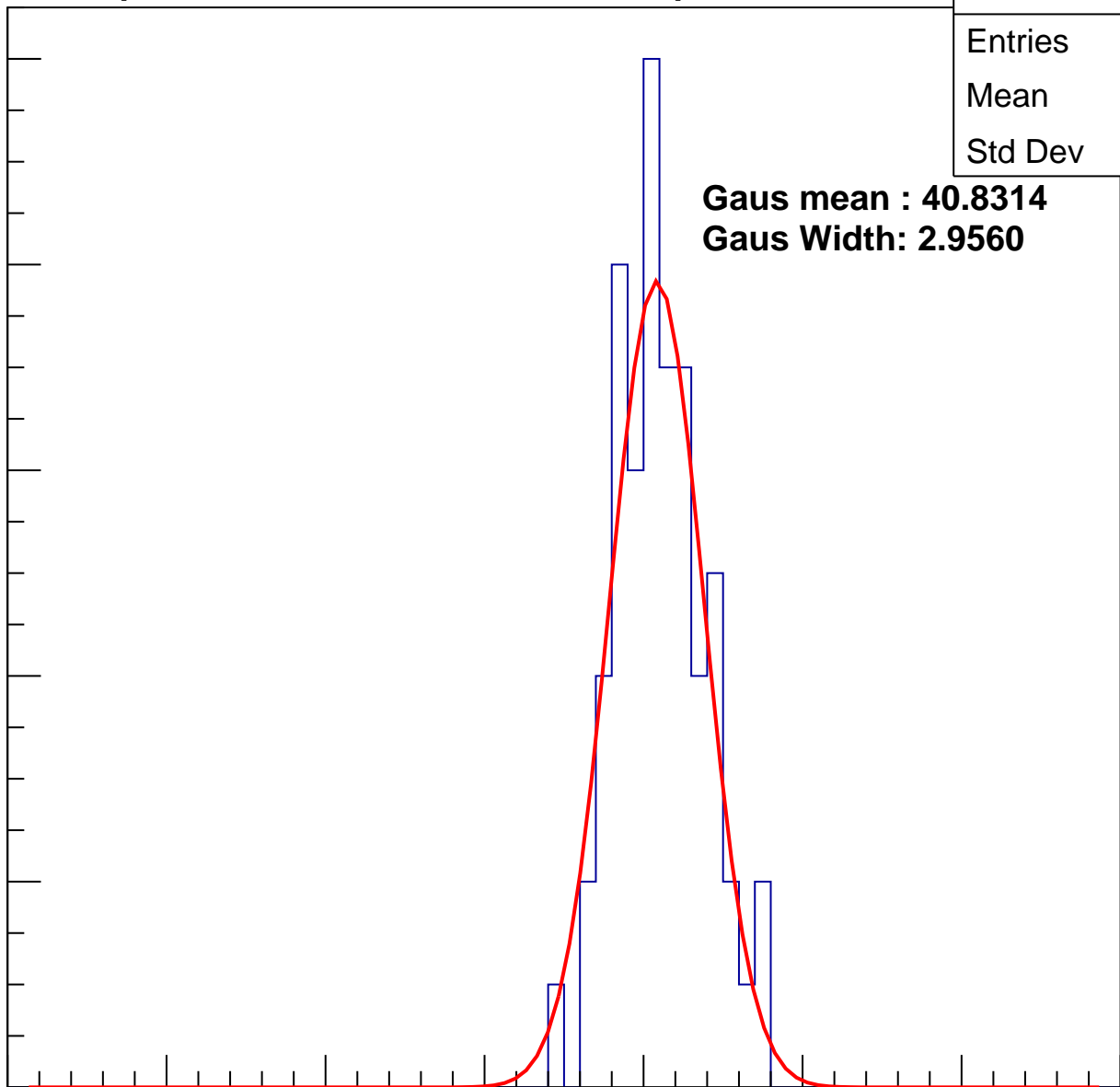
**Gaus Width: 2.9560**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

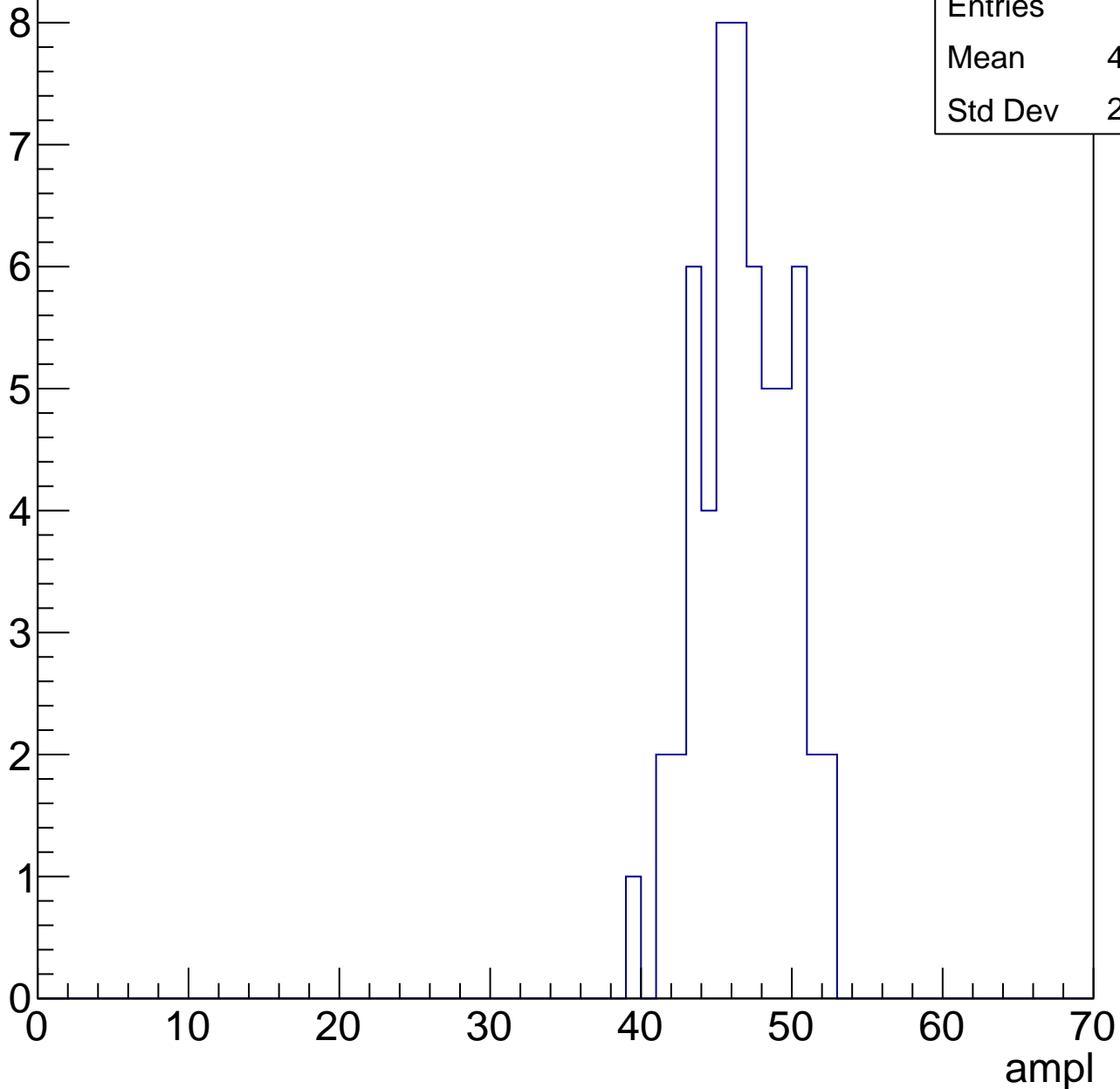


# B1L000S, U2-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	46.32
Std Dev	2.939

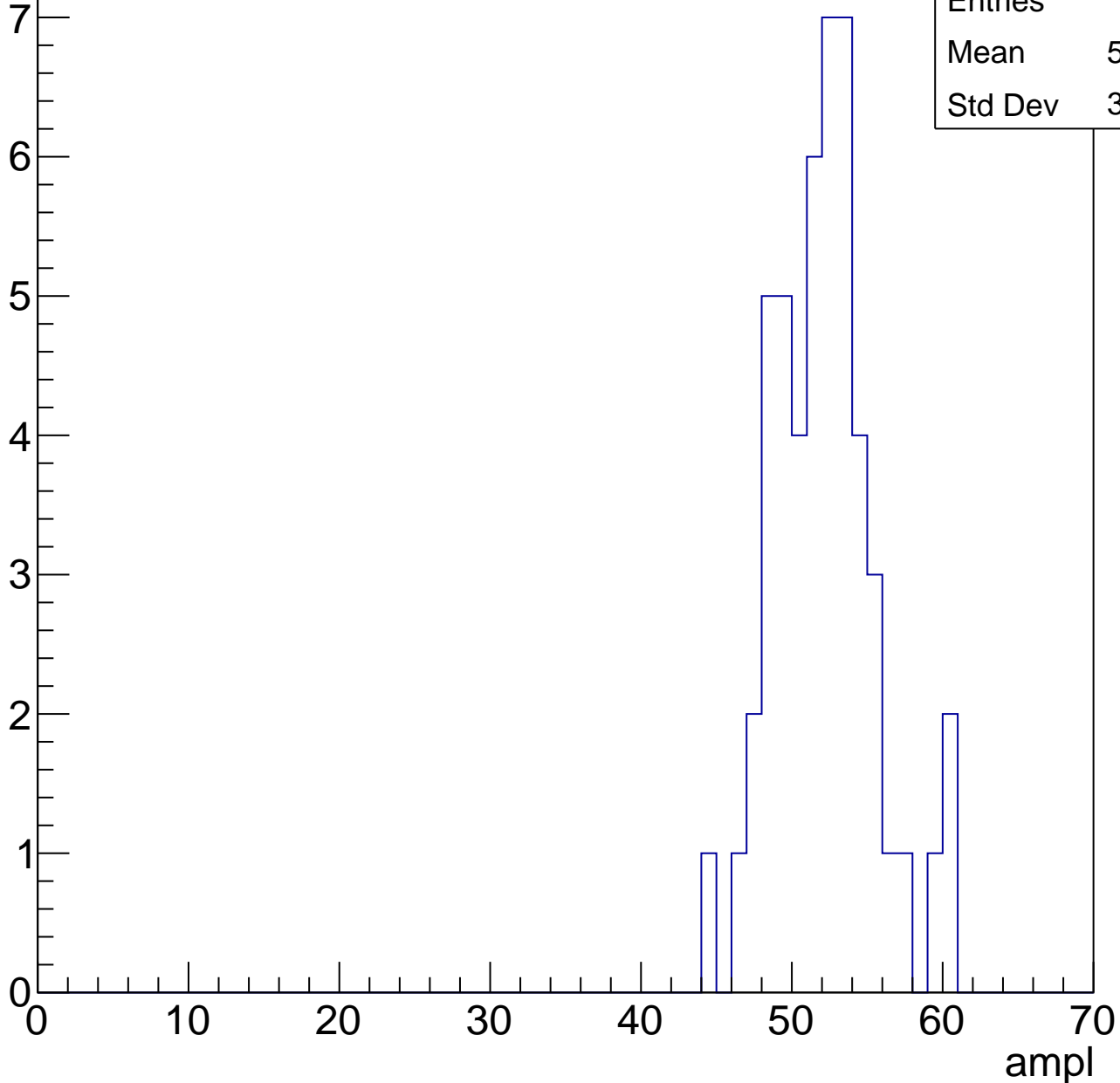


# B1L000S, U2-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	51.66
Std Dev	3.344



# B1L000S, U2-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	57.38
Std Dev	2.84

Entry

10

8

6

4

2

0

0

10

20

30

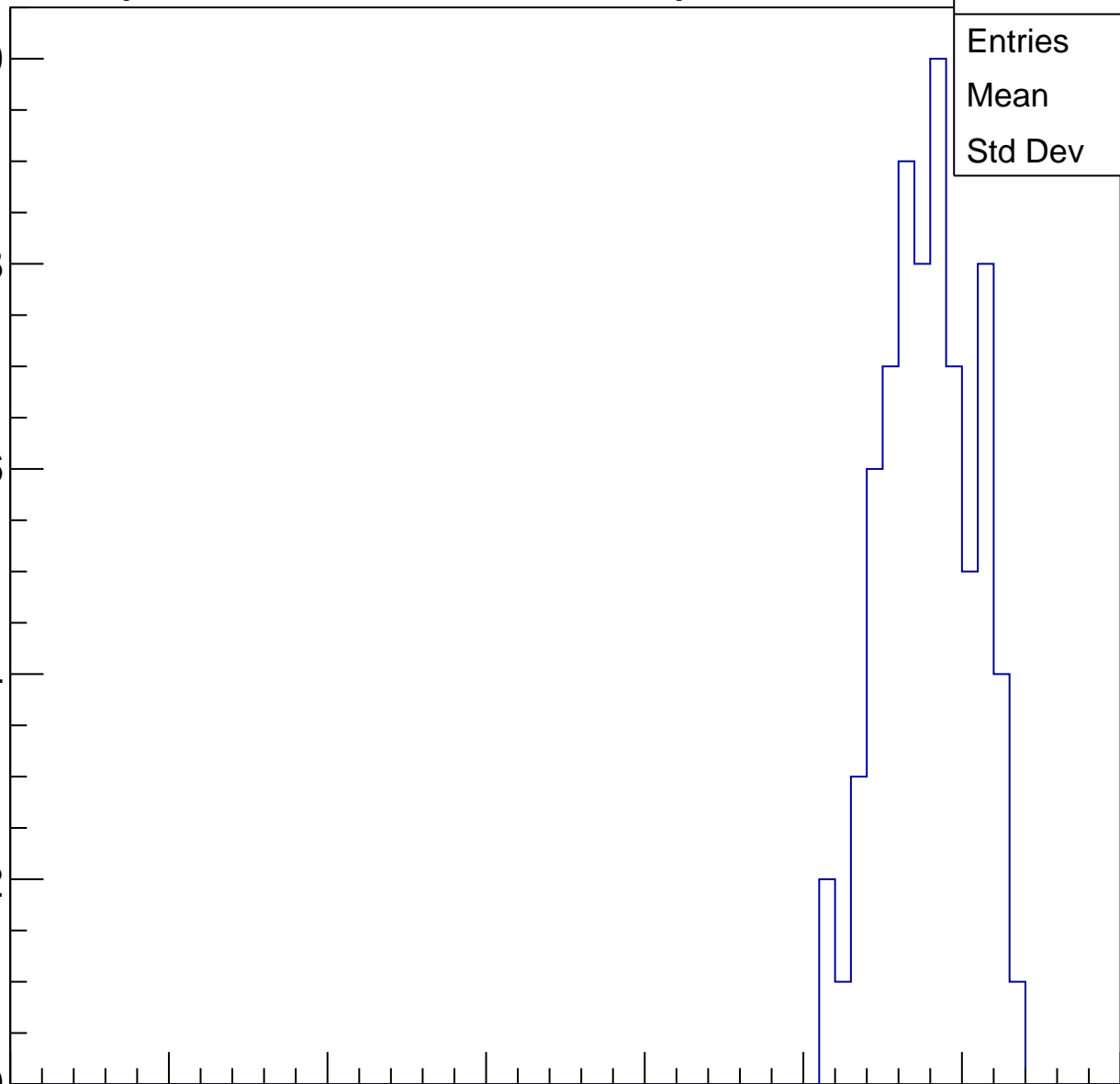
40

50

60

70

ampl

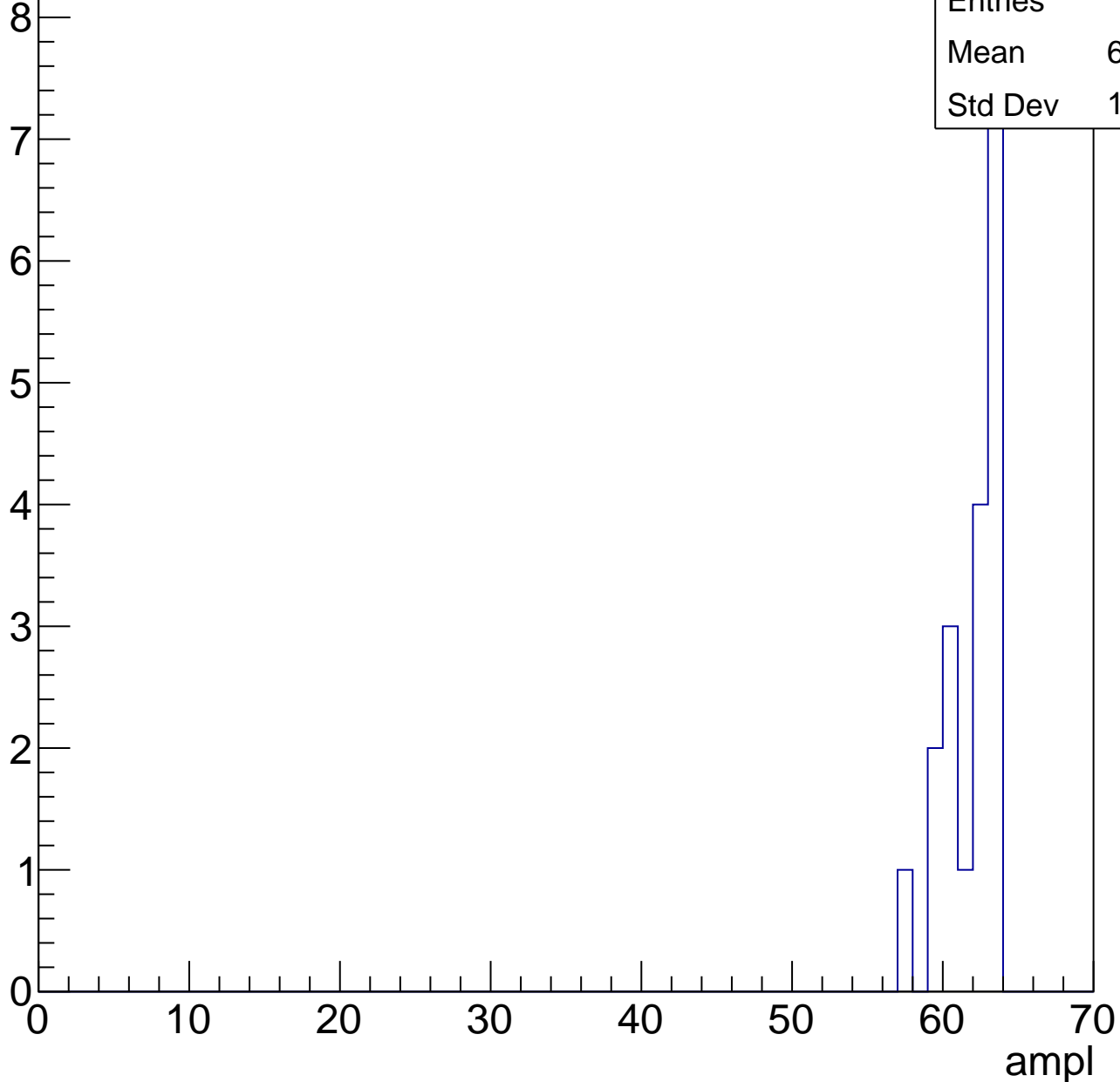


# B1L000S, U2-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	61.47
Std Dev	1.758

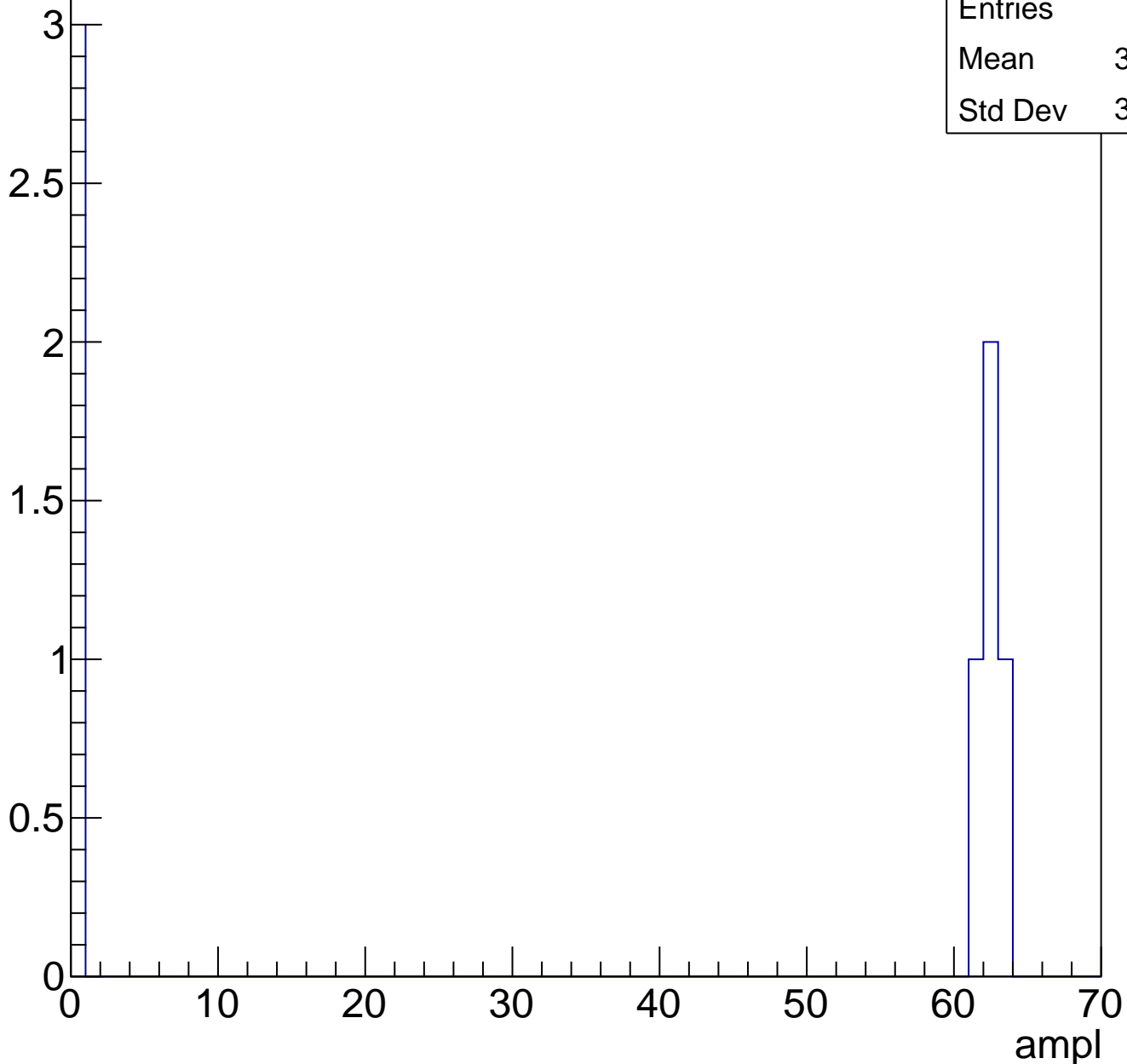




# B1L000S, U2-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch9, adc0

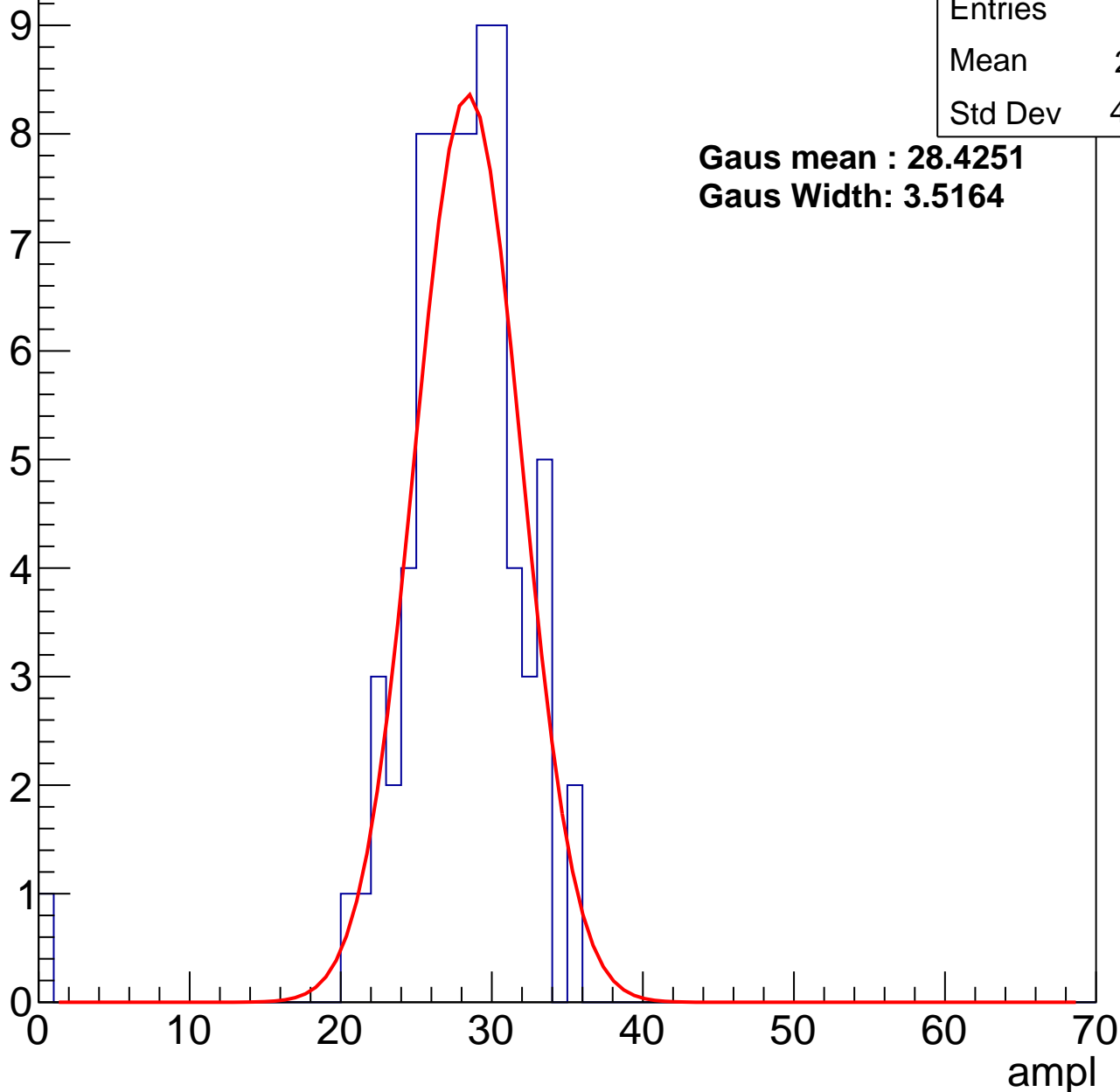
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	27.41
Std Dev	4.525

**Gaus mean : 28.4251**

**Gaus Width: 3.5164**



# B1L000S, U2-ch9, adc1

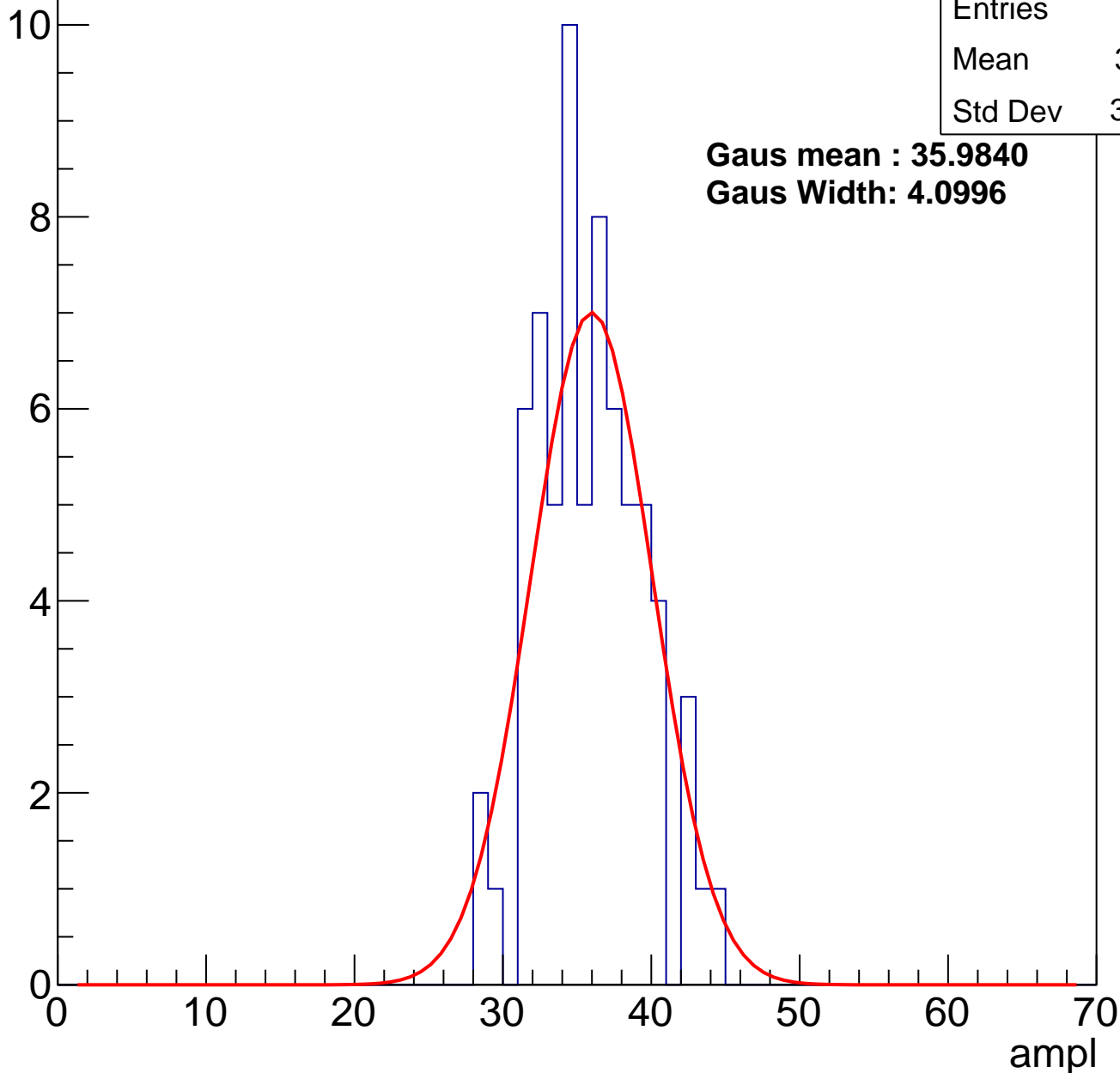
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	35.41
Std Dev	3.532

**Gaus mean : 35.9840**

**Gaus Width: 4.0996**

Entry

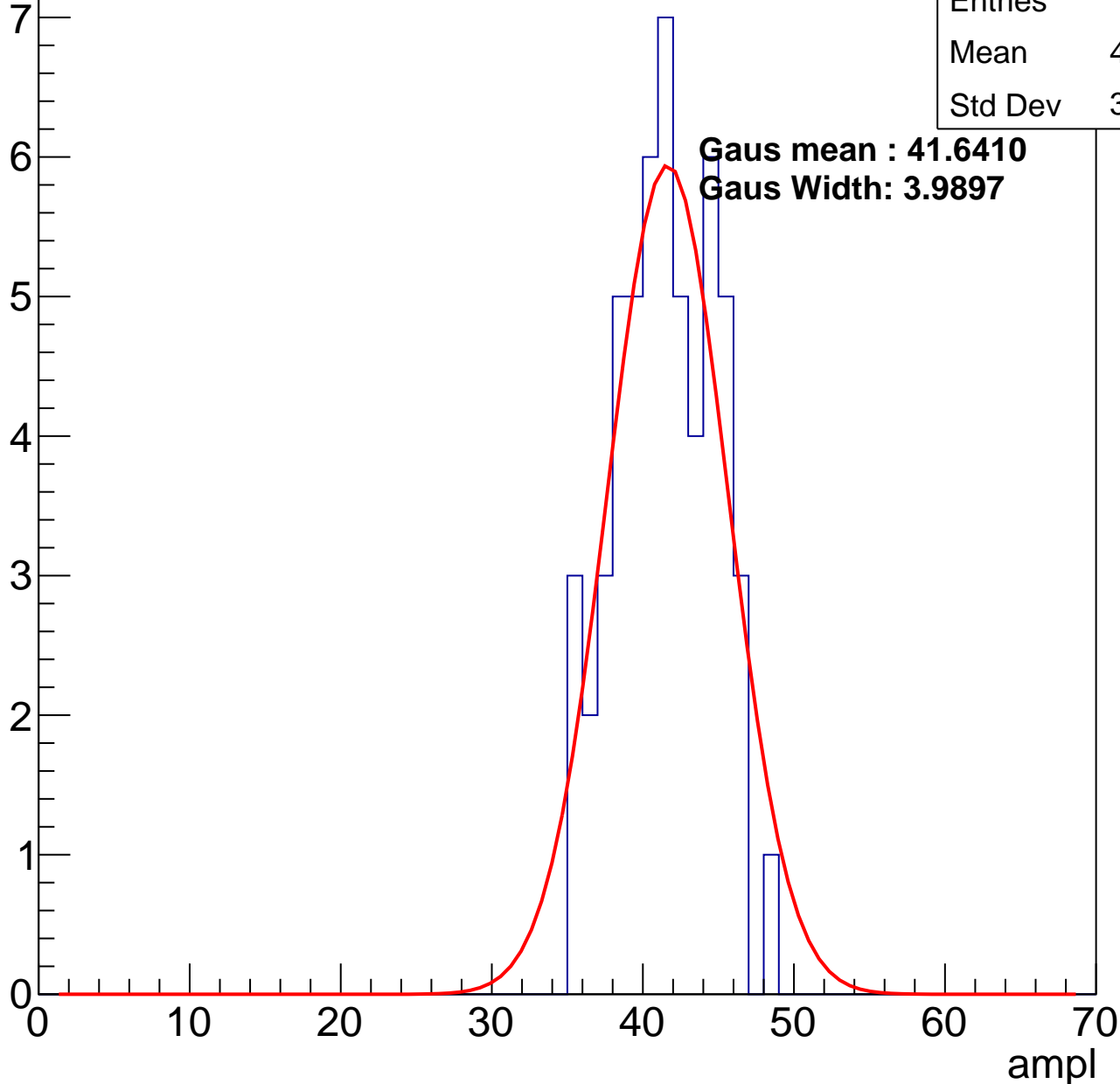


# B1L000S, U2-ch9, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.04
Std Dev	3.179

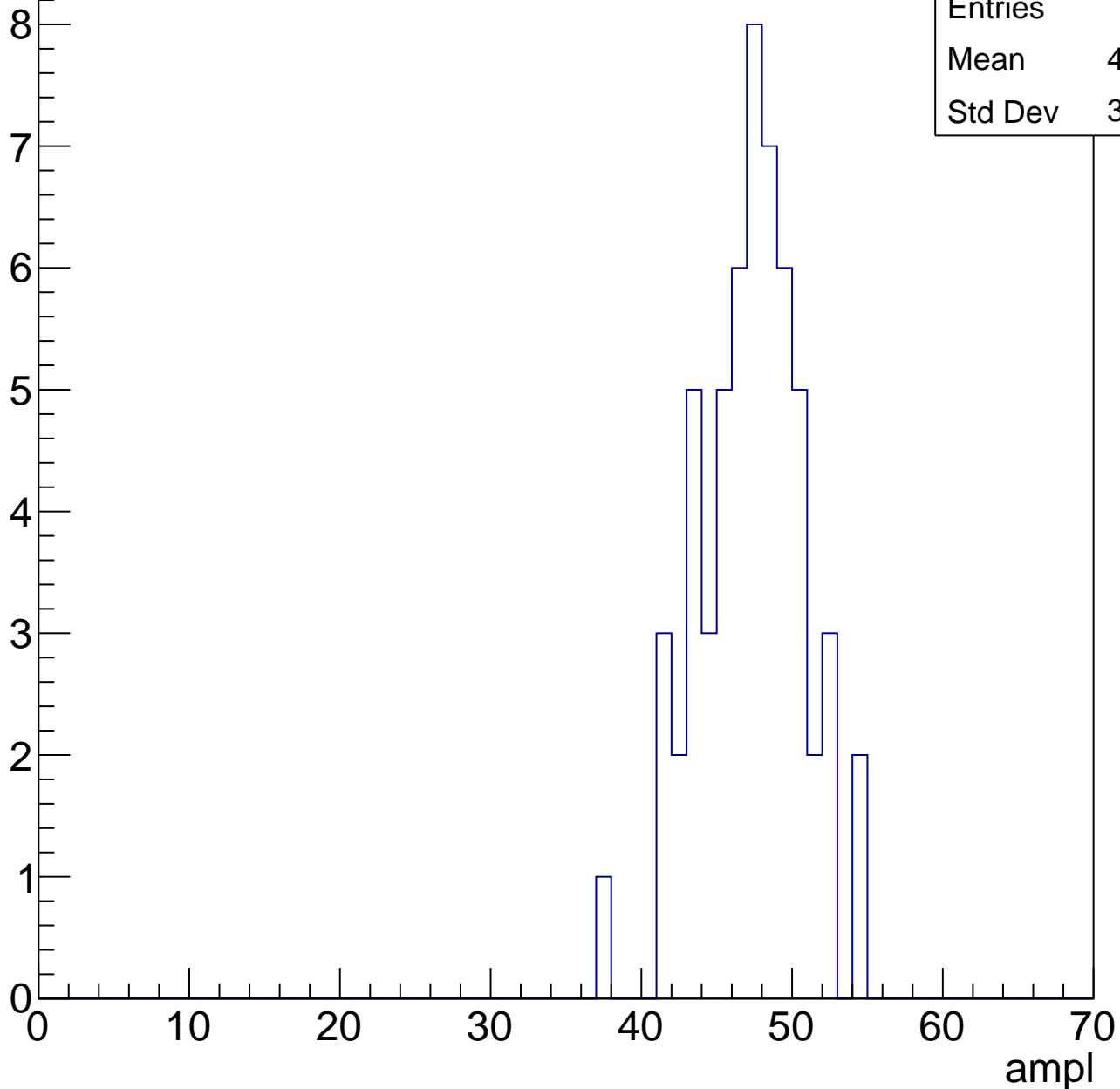


# B1L000S, U2-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	46.79
Std Dev	3.398

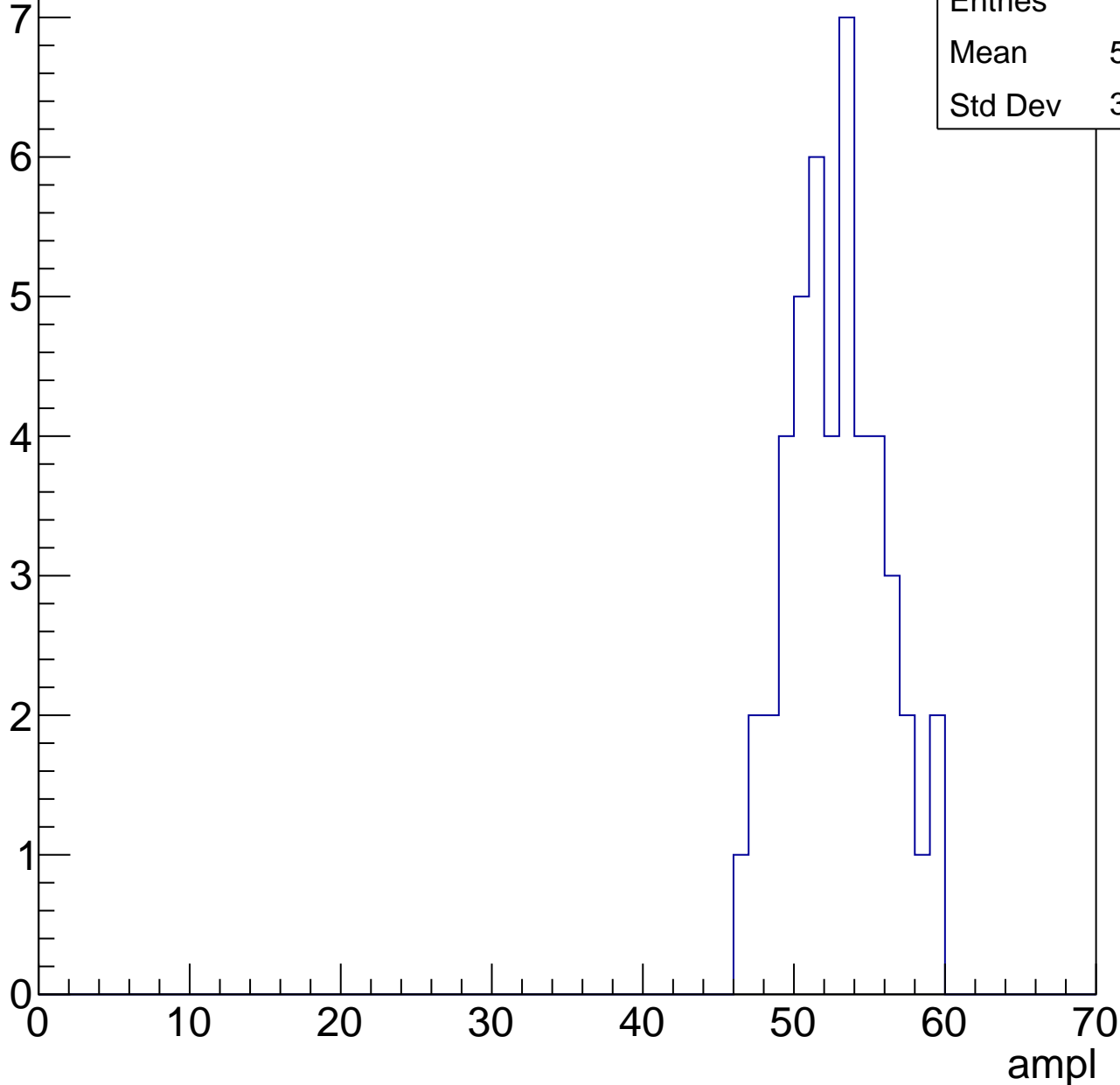


# B1L000S, U2-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	52.36
Std Dev	3.145



# B1L000S, U2-ch9, adc5

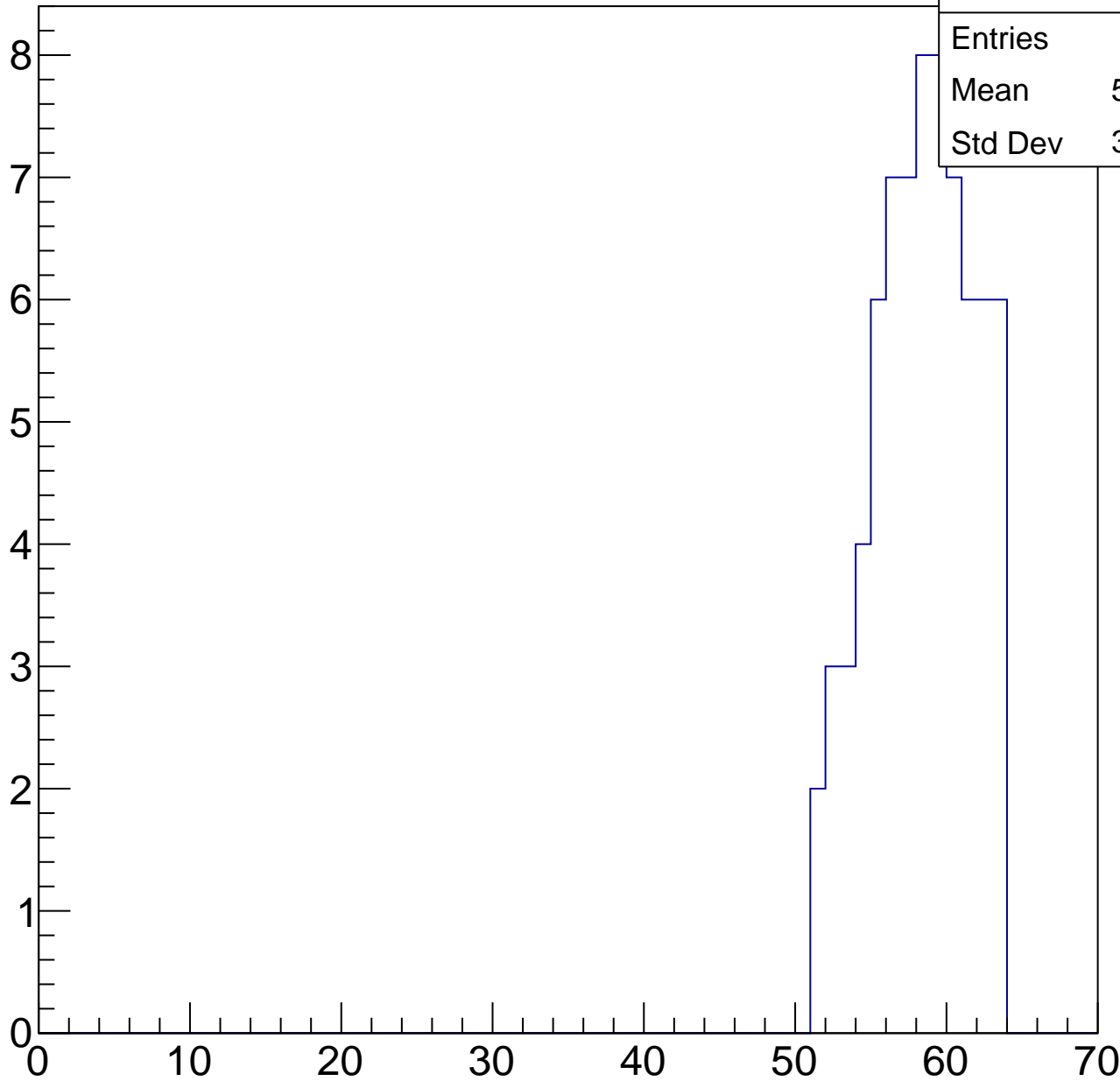
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	73
Mean	57.89
Std Dev	3.246

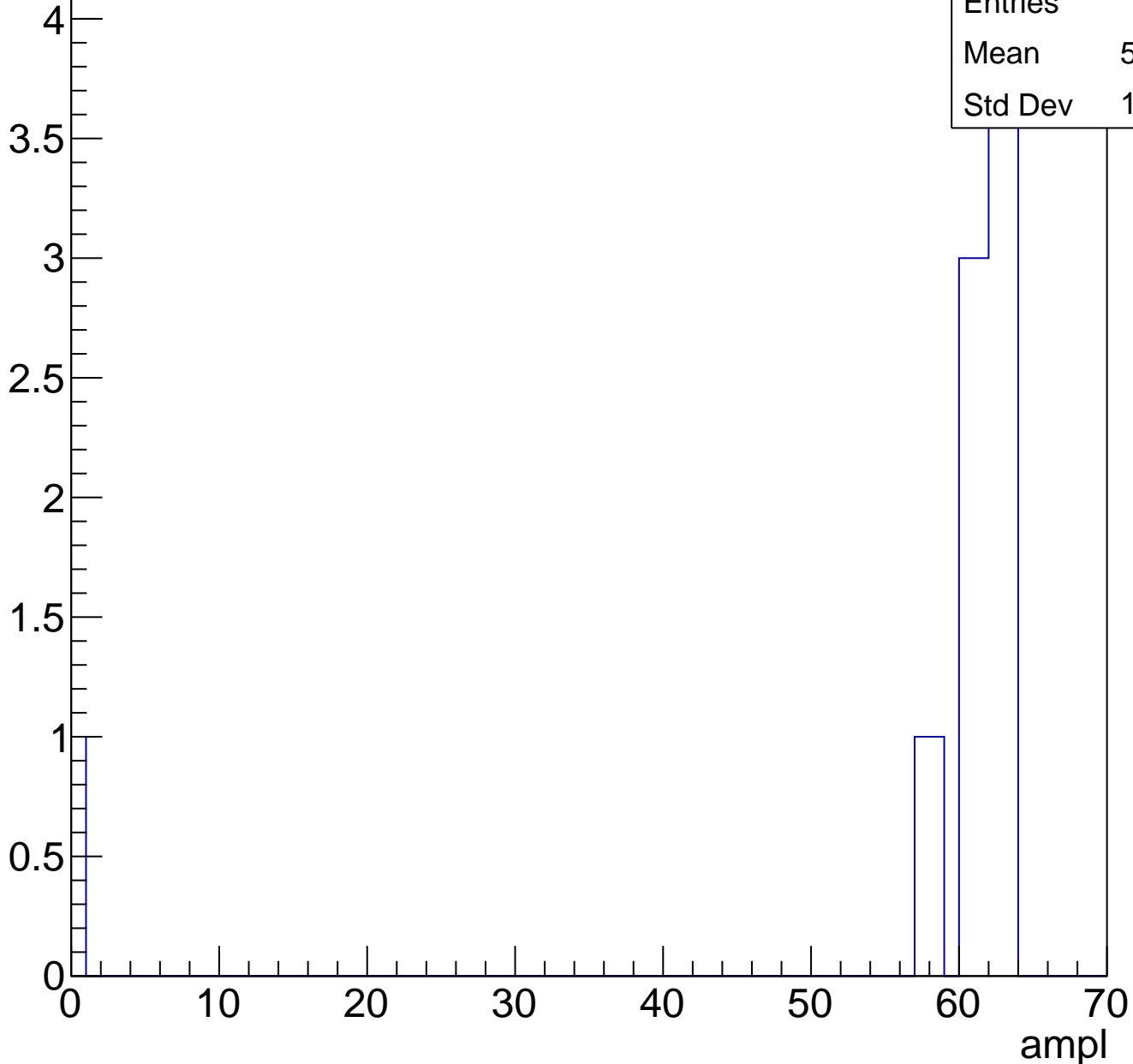
ampl



# B1L000S, U2-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	30.5
Std Dev	30.5

# B1L000S, U2-ch10, adc0

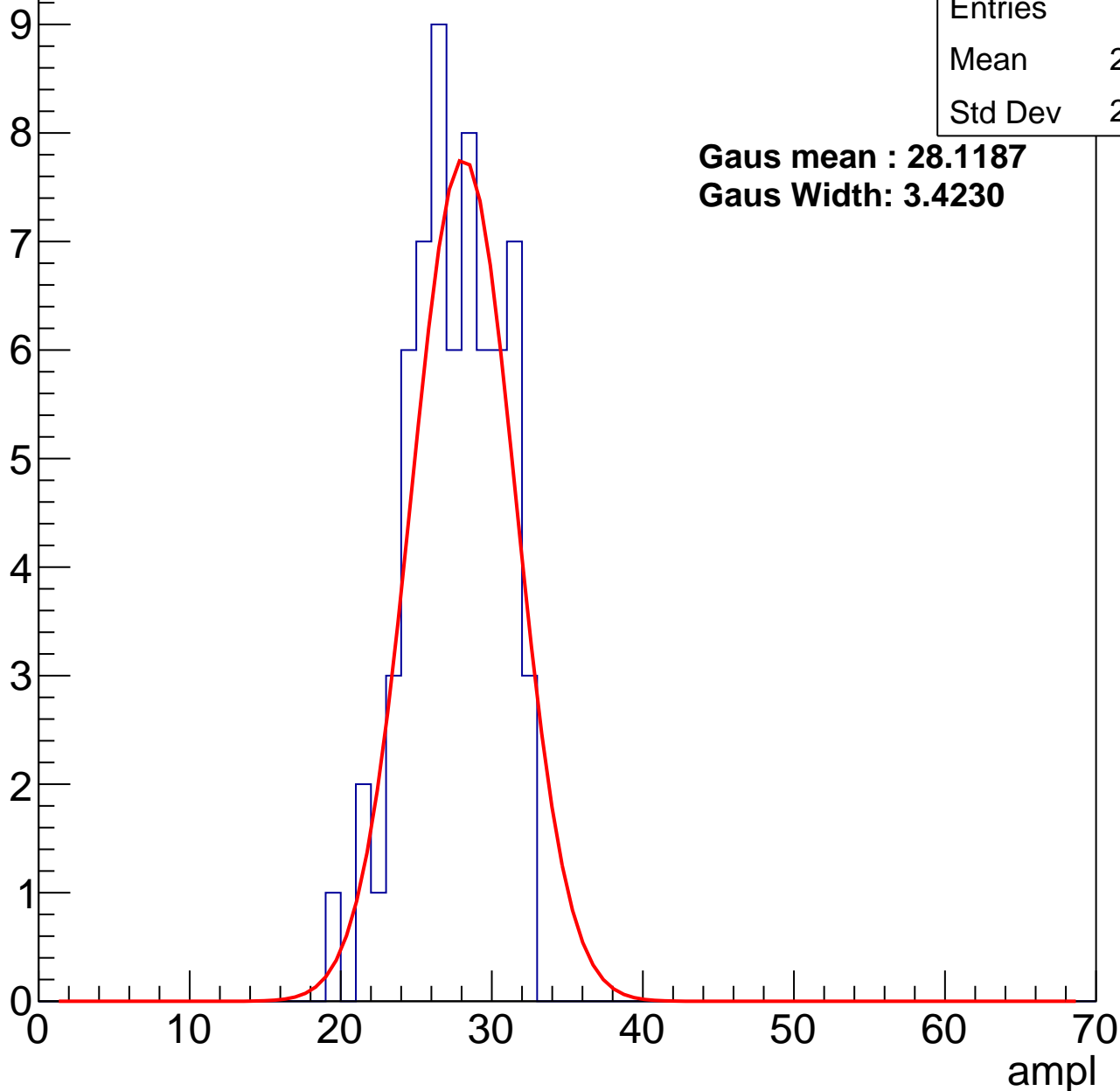
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	27.05
Std Dev	2.969

**Gaus mean : 28.1187**

**Gaus Width: 3.4230**



# B1L000S, U2-ch10, adc1

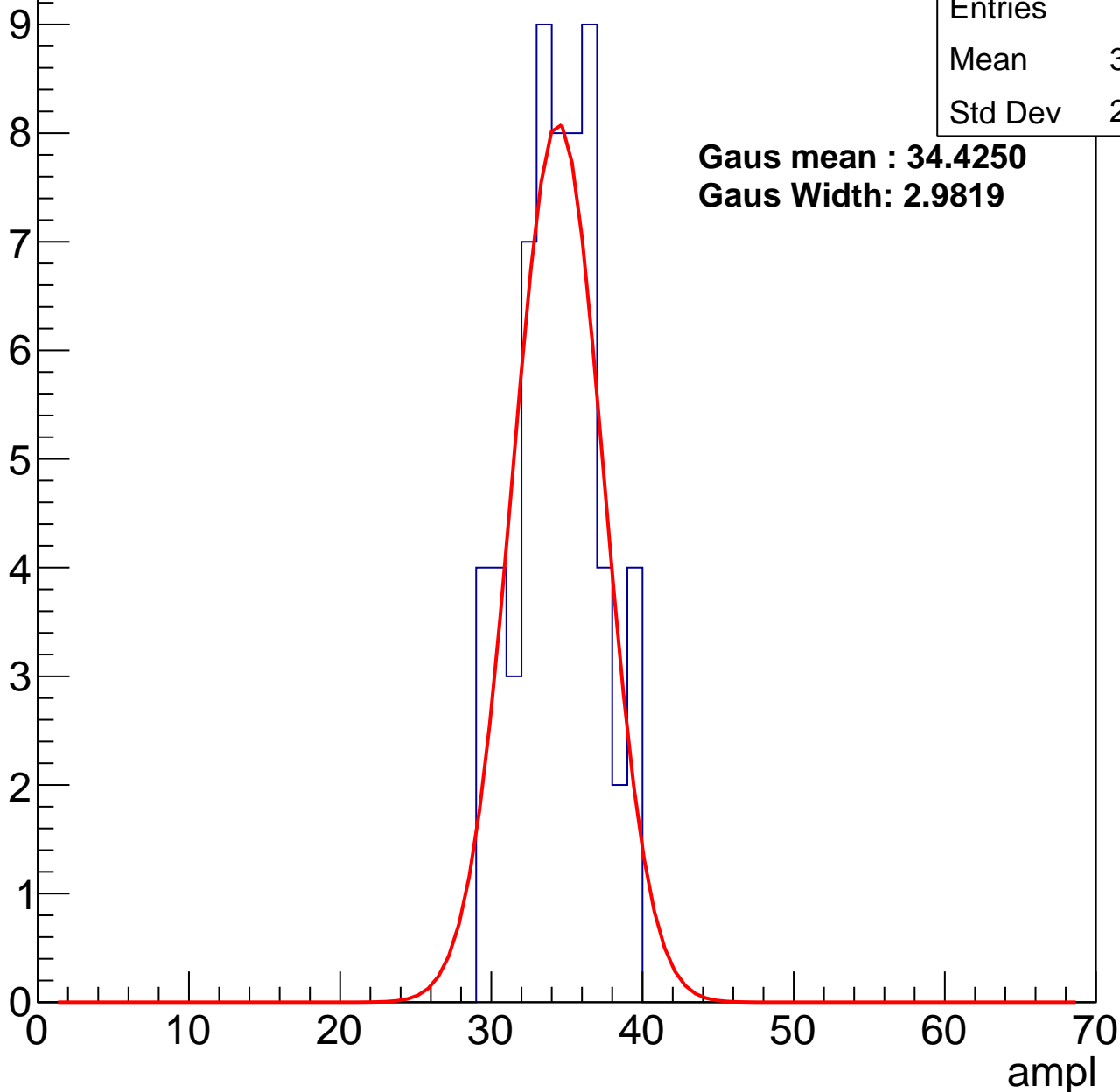
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	33.97
Std Dev	2.664

**Gaus mean : 34.4250**

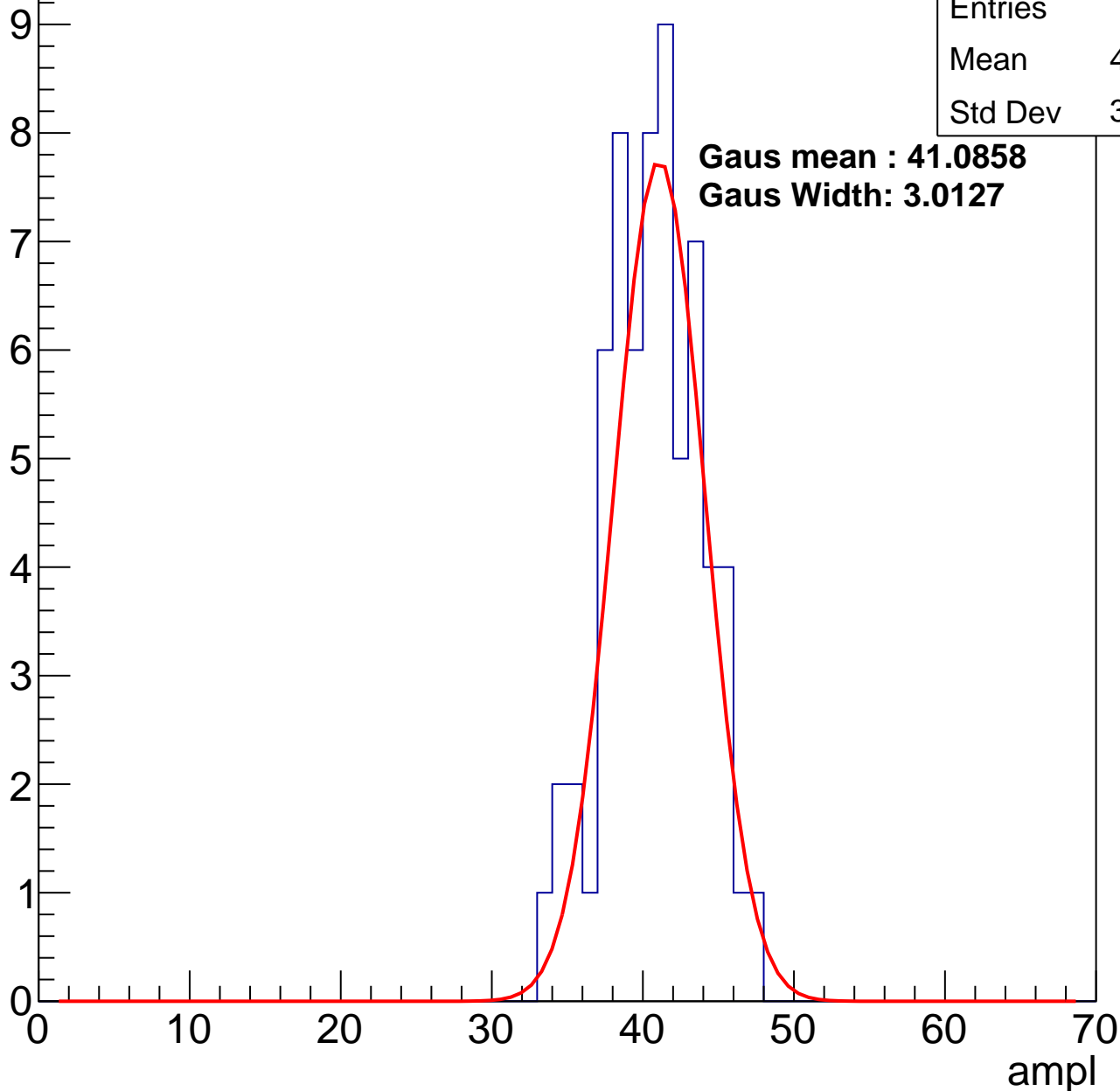
**Gaus Width: 2.9819**



# B1L000S, U2-ch10, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



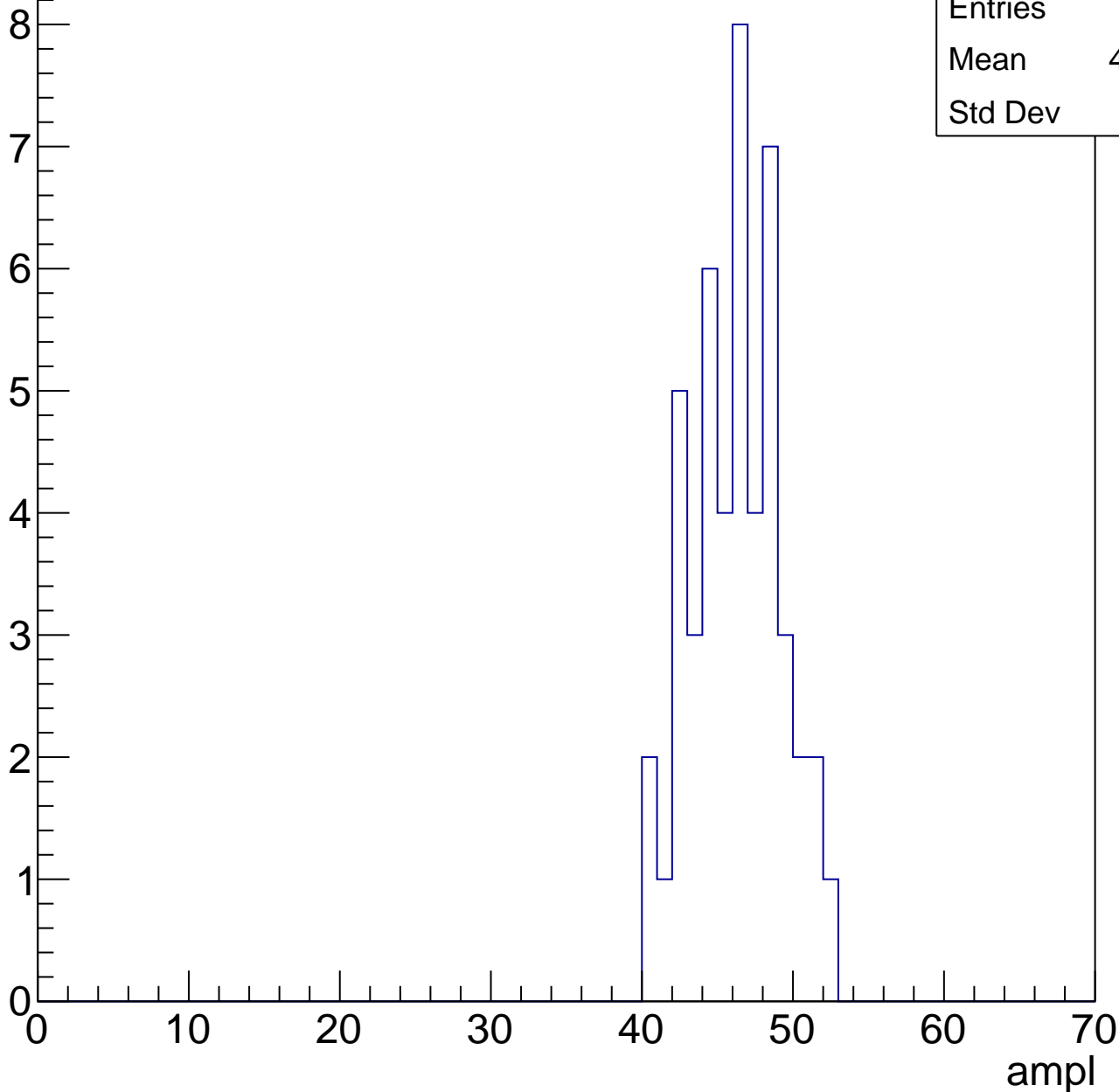
Entries	65
Mean	40.25
Std Dev	3.079

# B1L000S, U2-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	45.77
Std Dev	2.91

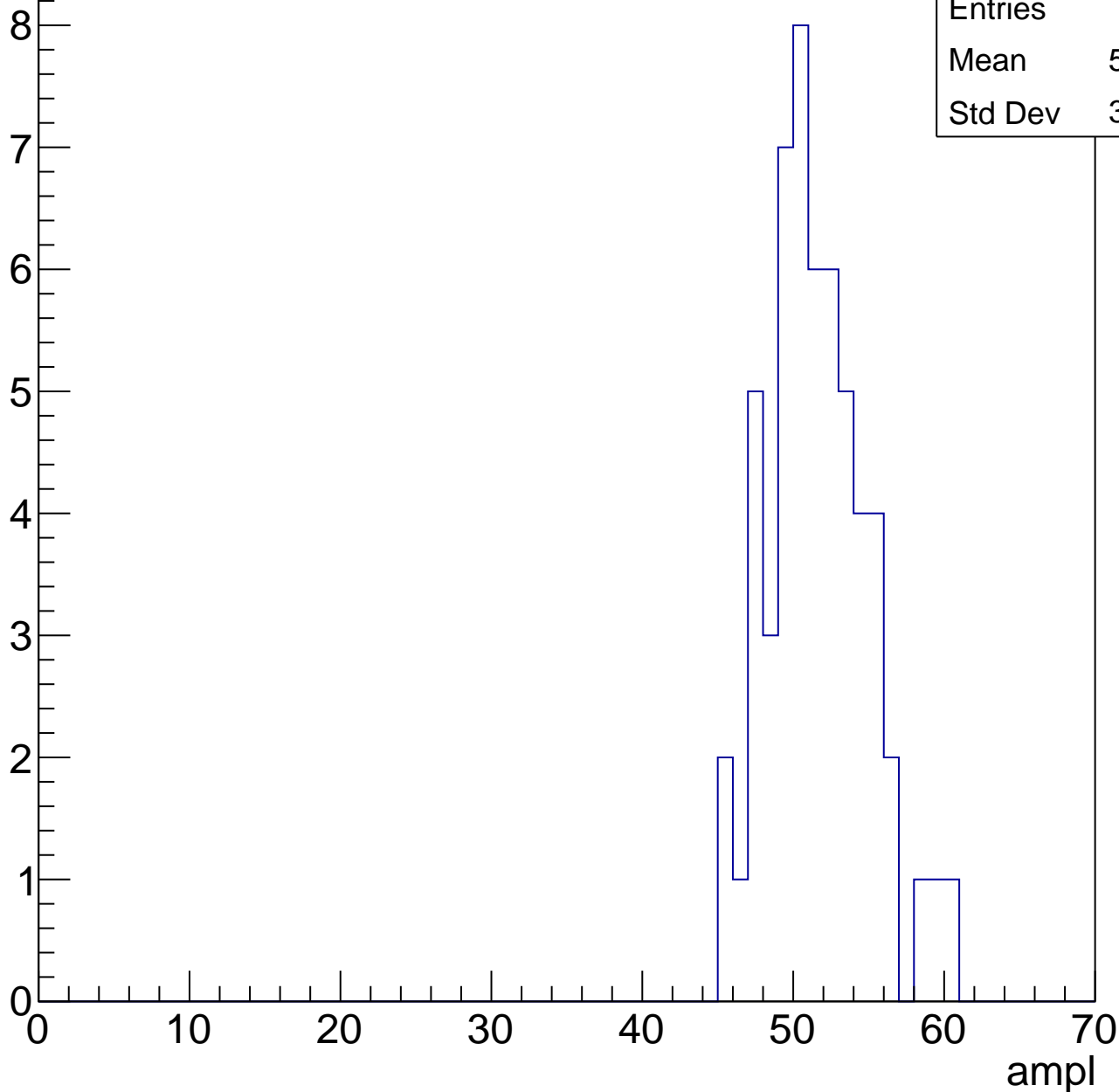


# B1L000S, U2-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

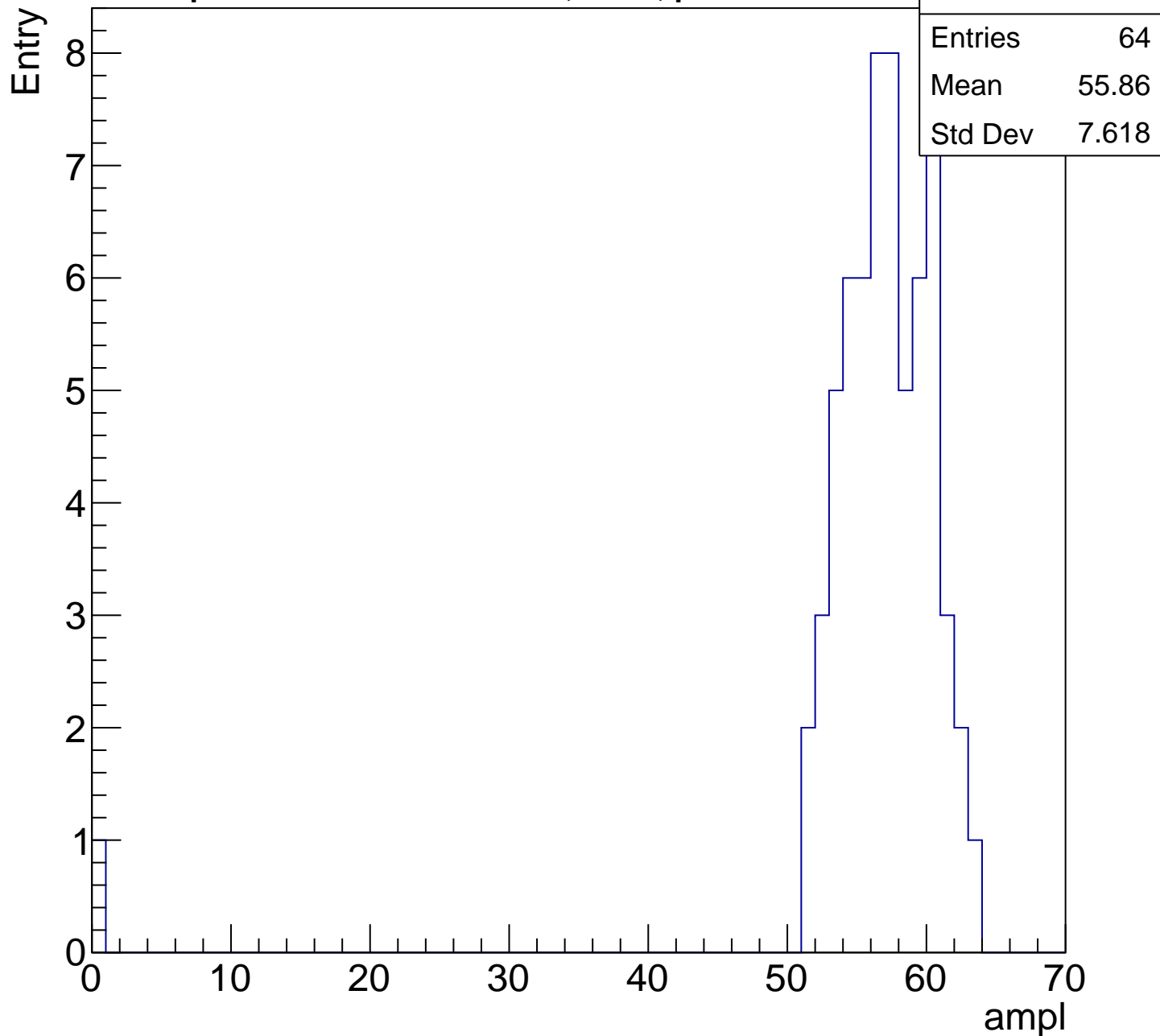
Entry

Entries	56
Mean	51.18
Std Dev	3.296



# B1L000S, U2-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

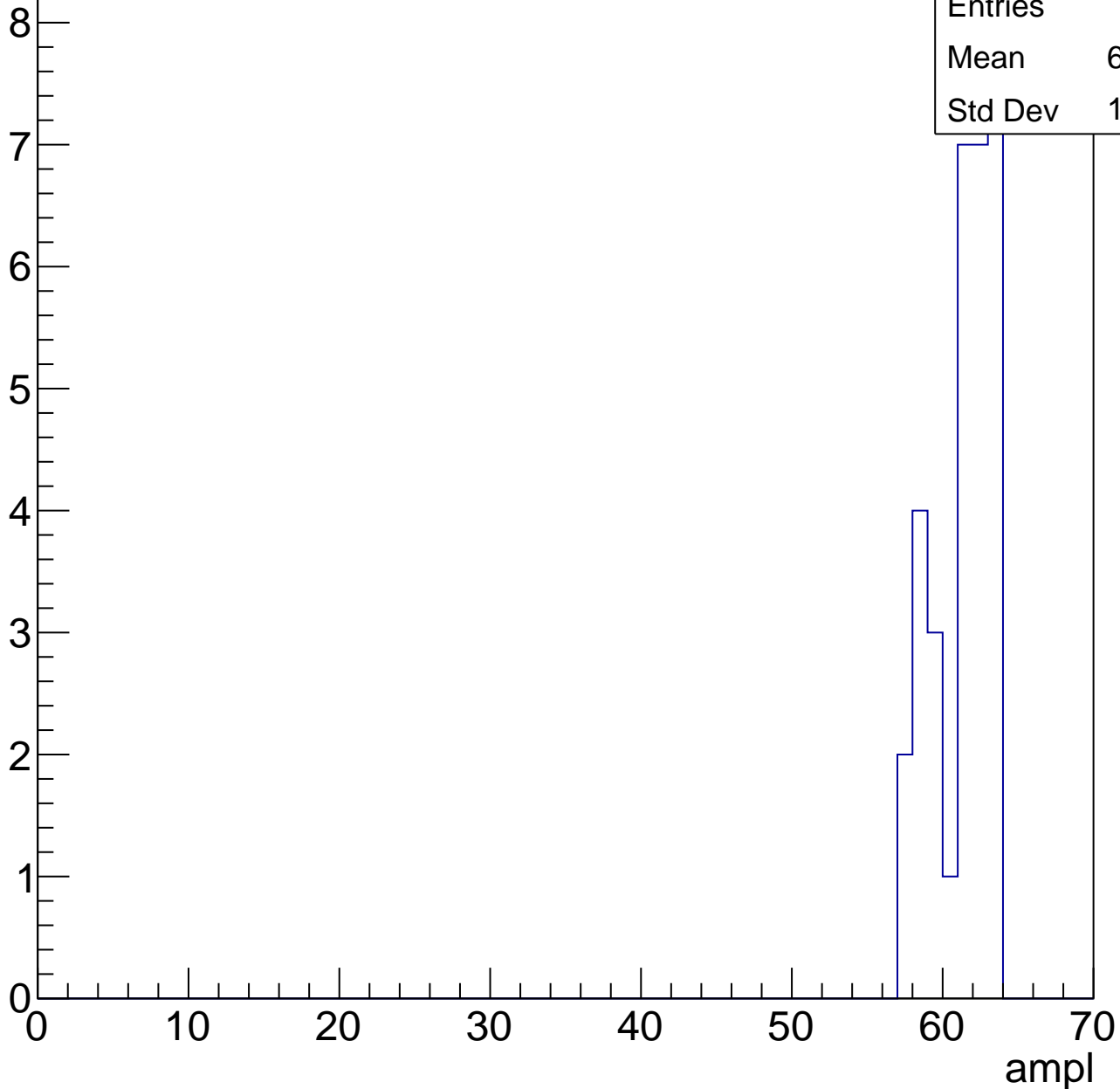


# B1L000S, U2-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	60.88
Std Dev	1.932





# B1L000S, U2-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch11, adc0

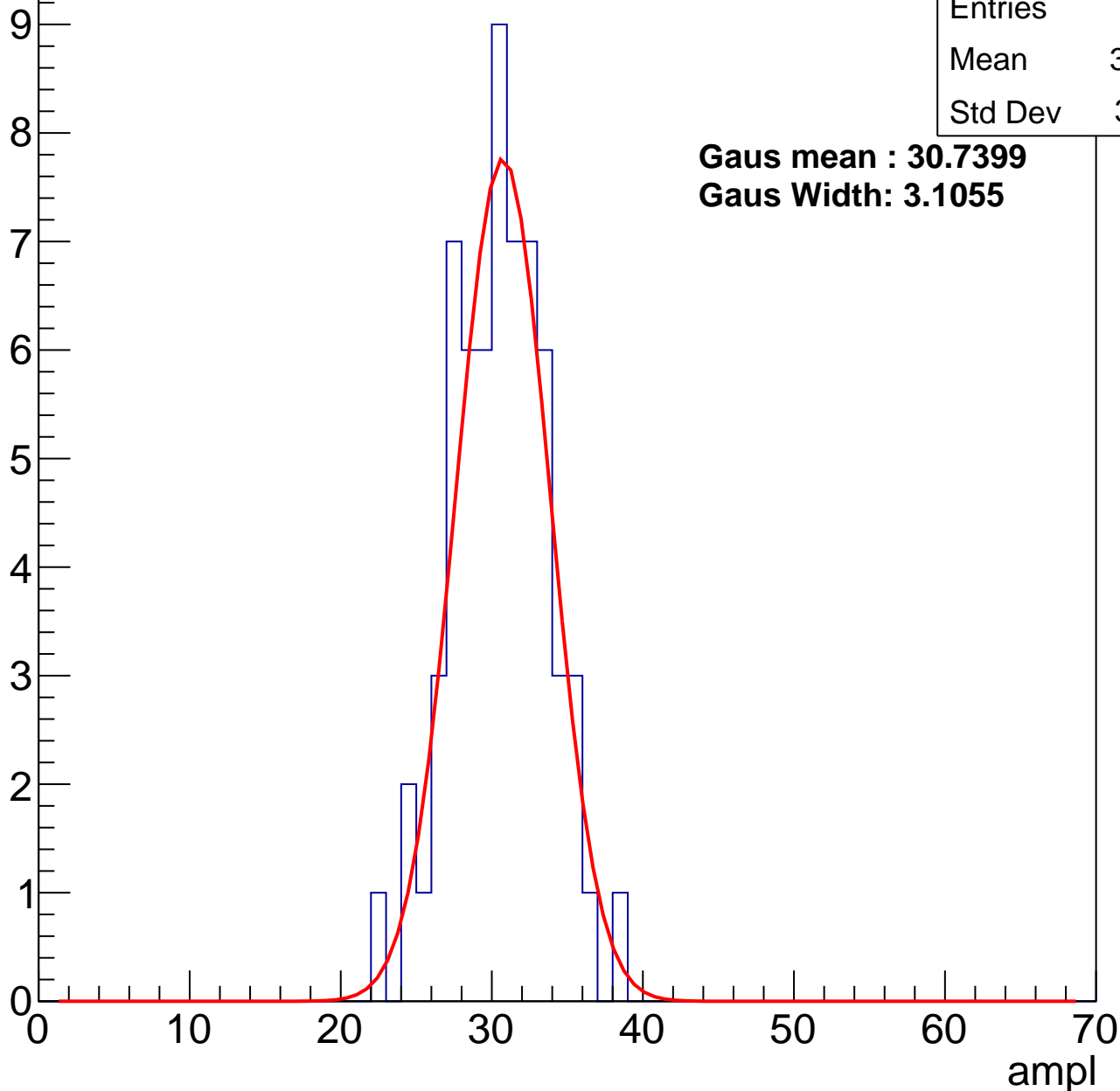
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	30.06
Std Dev	3.121

**Gaus mean : 30.7399**

**Gaus Width: 3.1055**



# B1L000S, U2-ch11, adc1

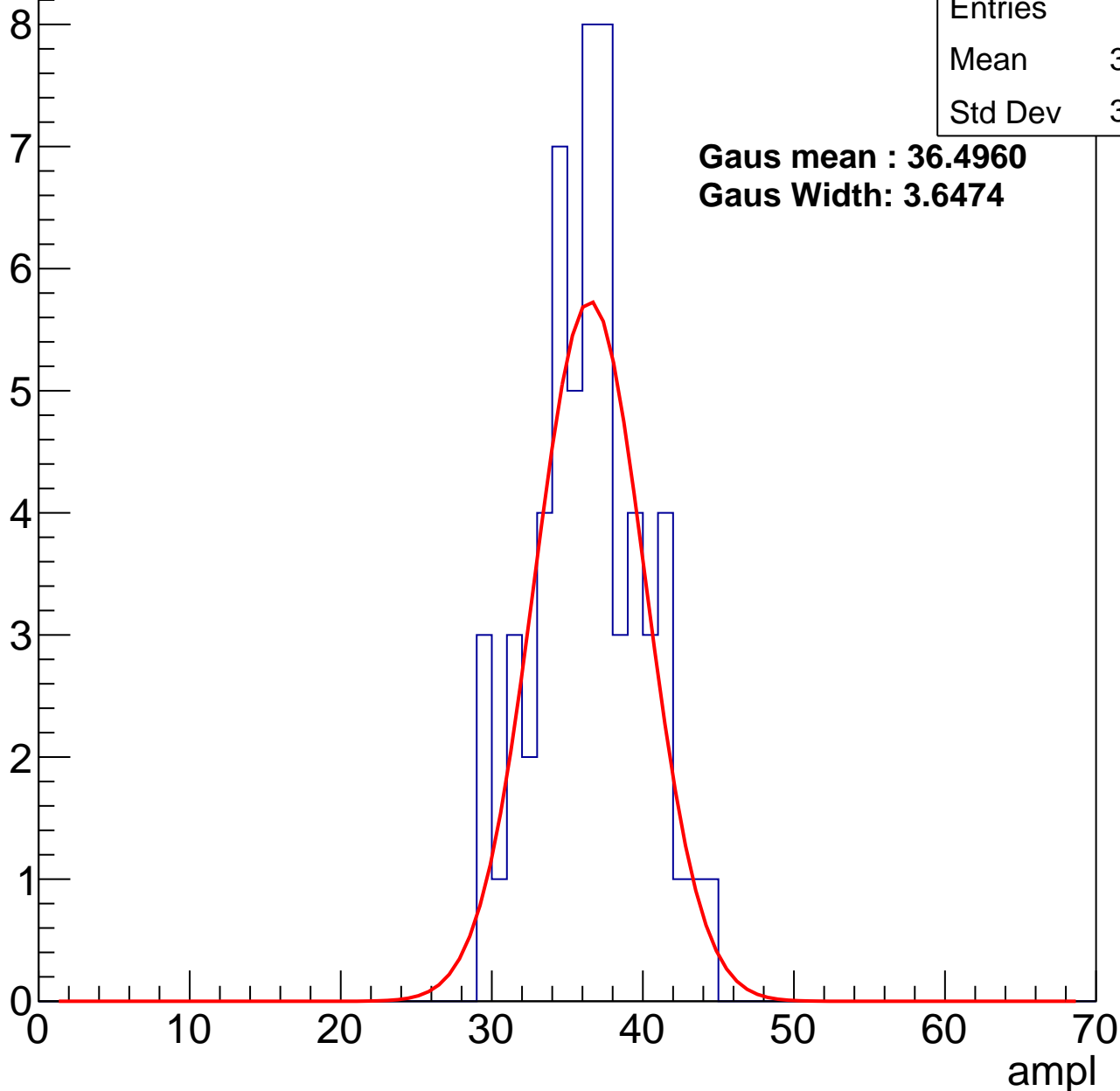
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.97
Std Dev	3.504

**Gaus mean : 36.4960**

**Gaus Width: 3.6474**



# B1L000S, U2-ch11, adc2

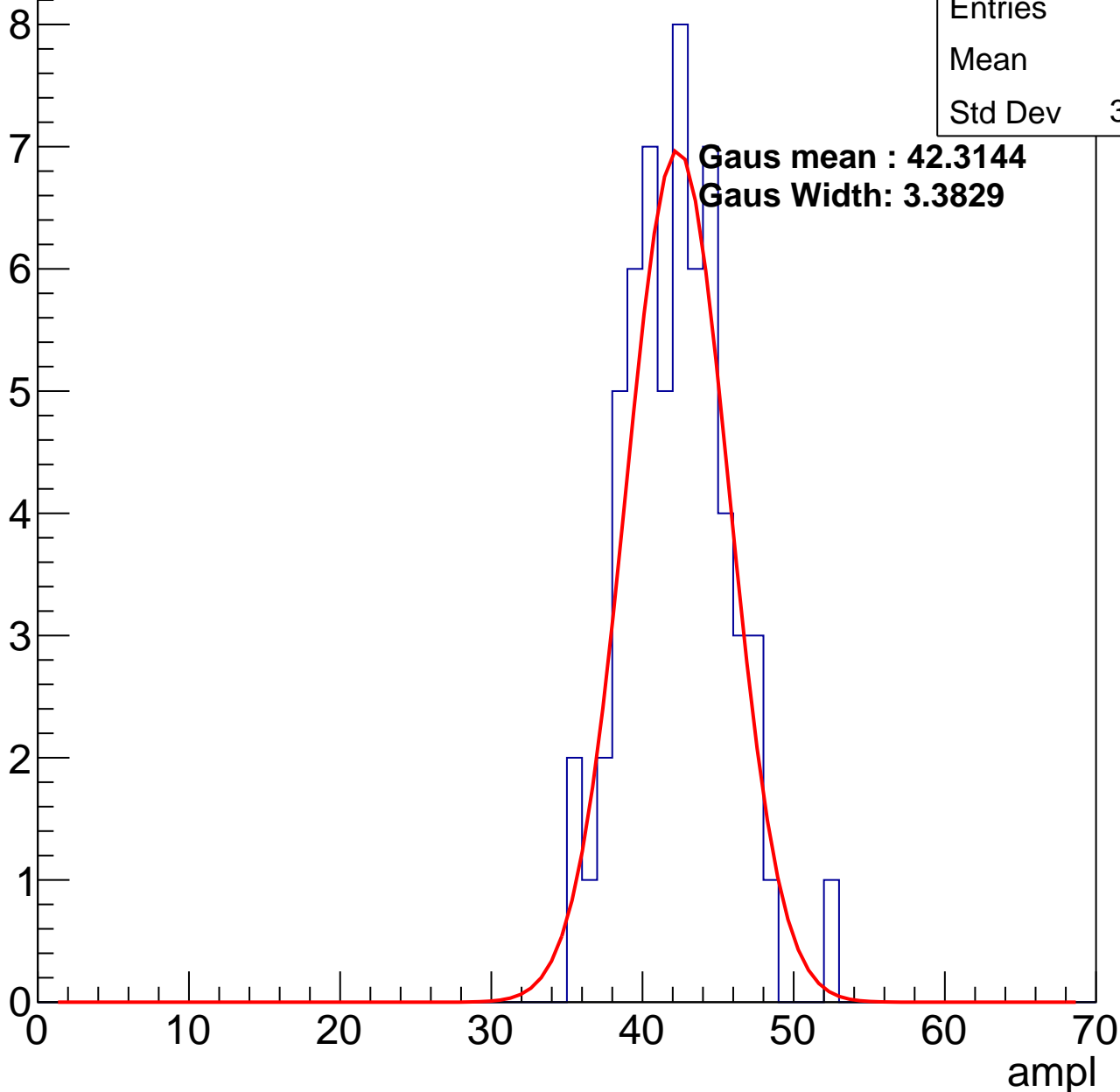
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.8
Std Dev	3.333

**Gaus mean : 42.3144**

**Gaus Width: 3.3829**

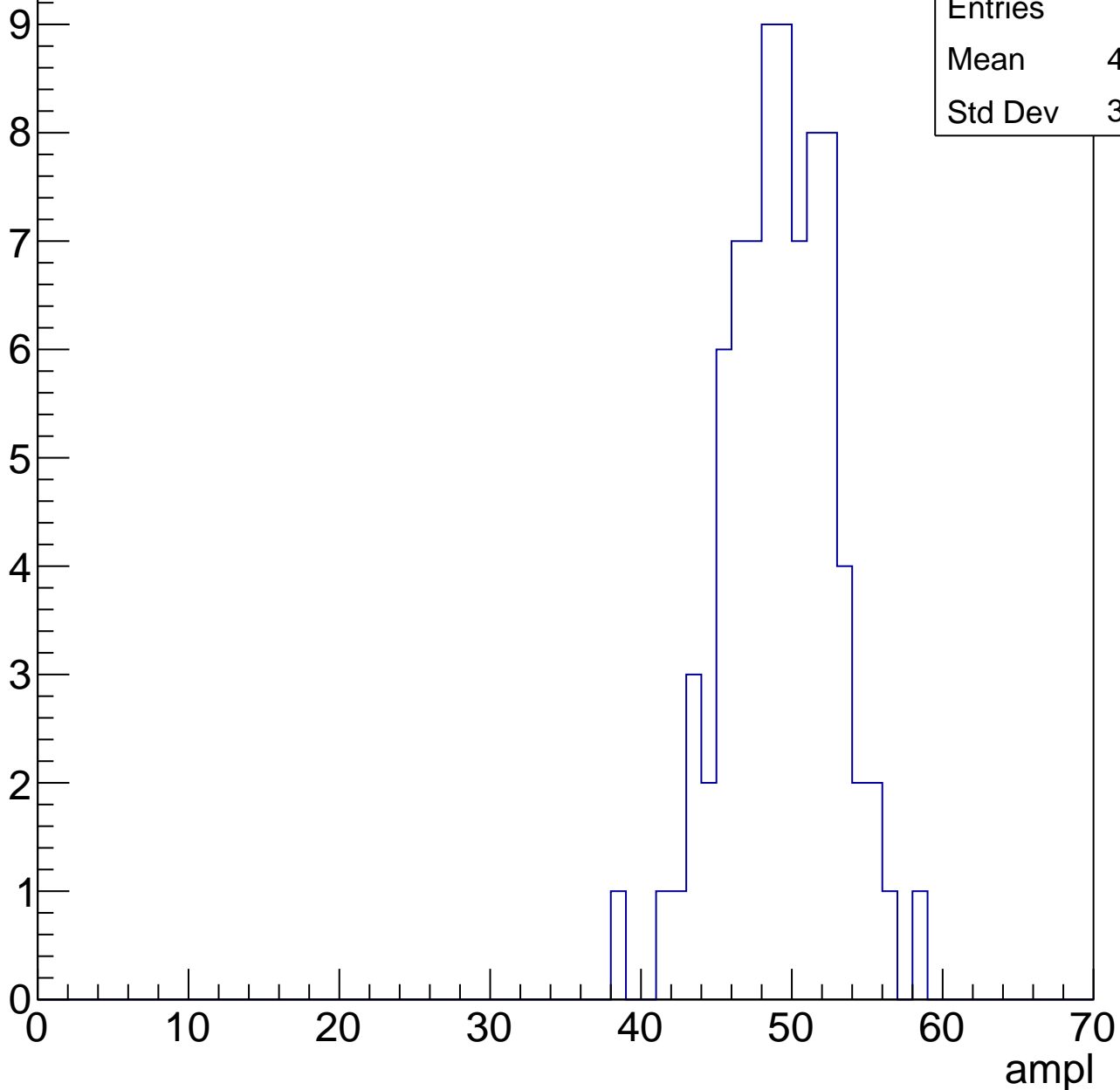


# B1L000S, U2-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	48.73
Std Dev	3.575

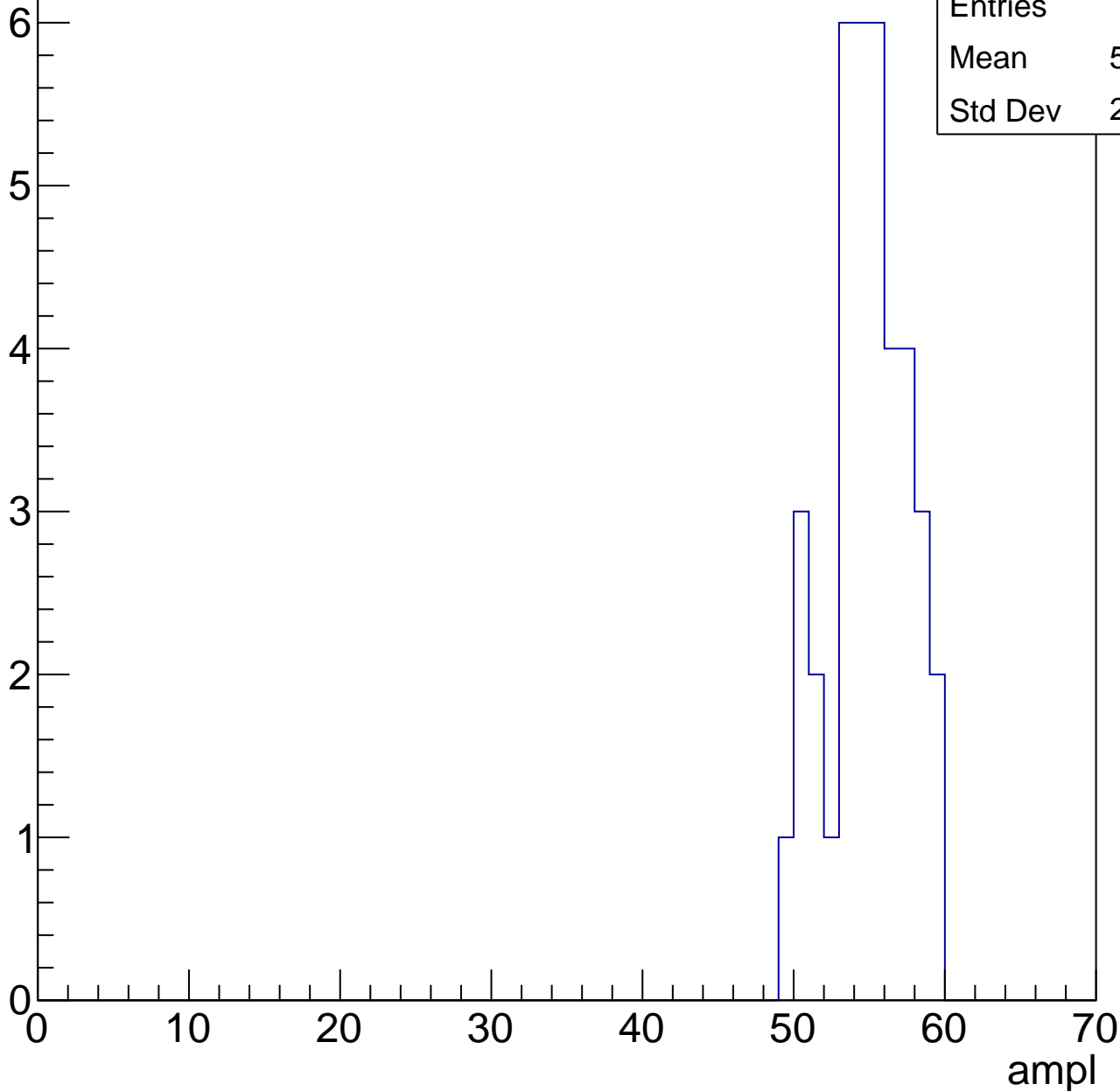


# B1L000S, U2-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	38
Mean	54.45
Std Dev	2.562



# B1L000S, U2-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

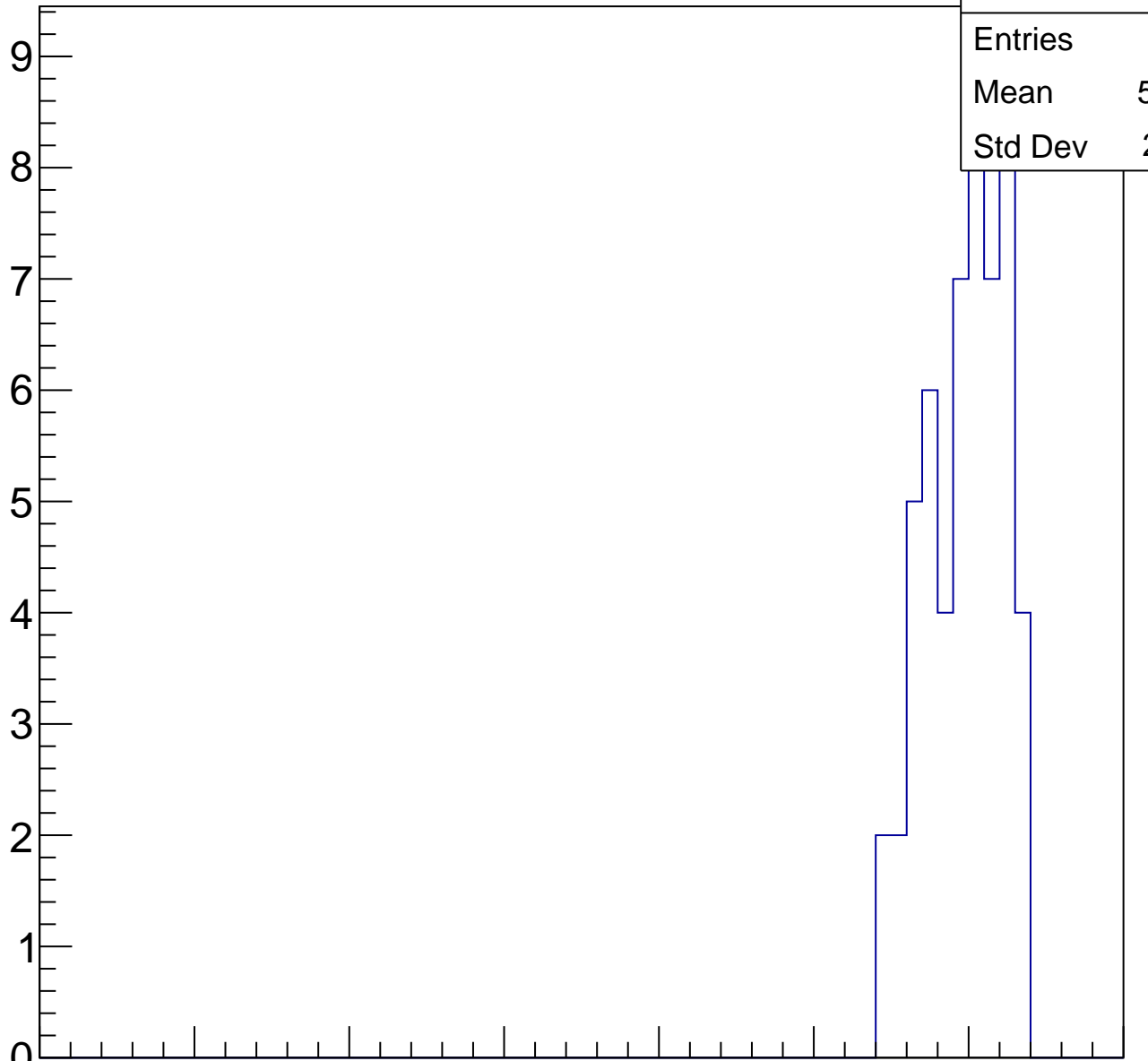
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	59.26
Std Dev	2.451

ampl

0 10 20 30 40 50 60 70

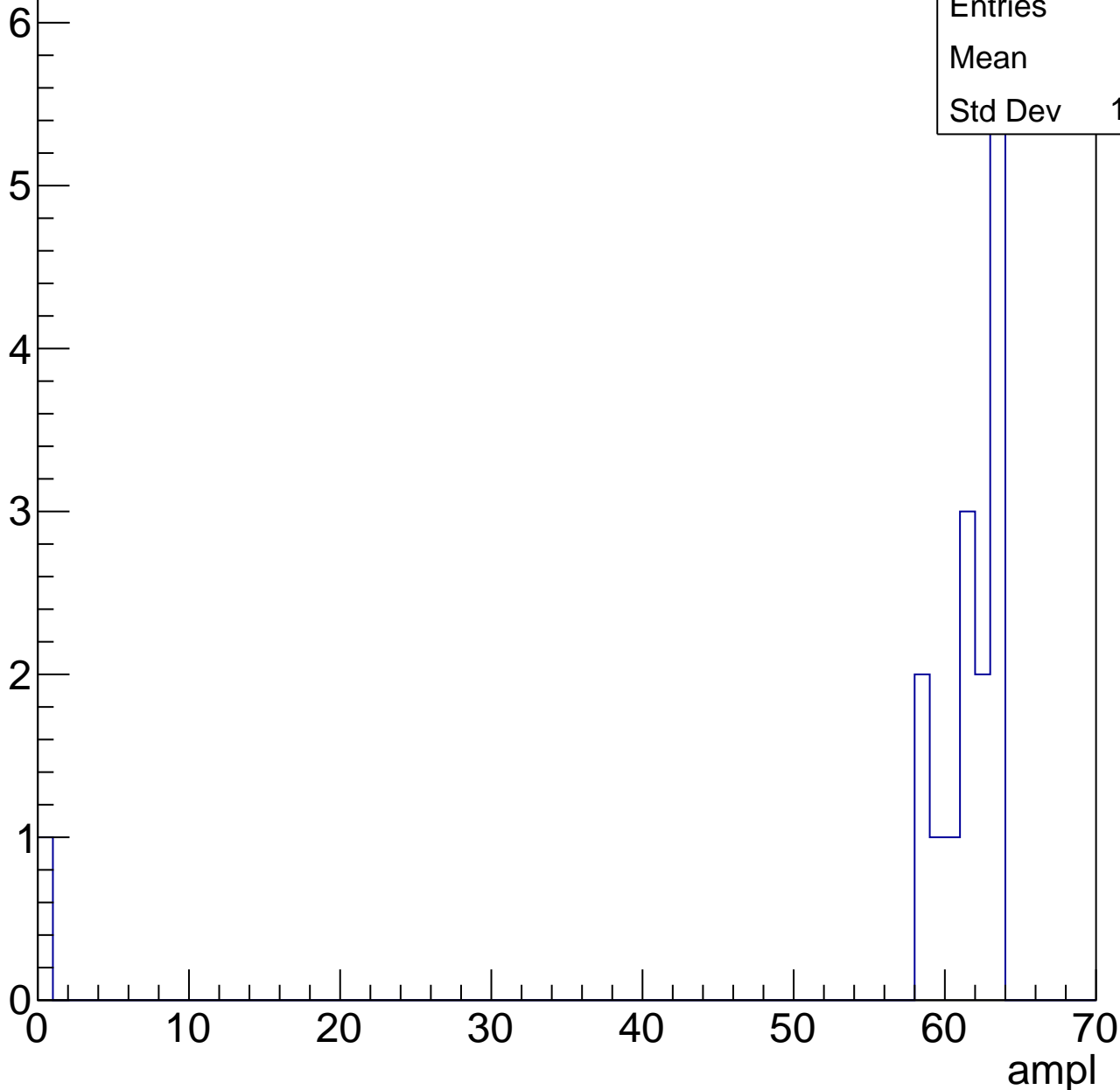


# B1L000S, U2-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.5
Std Dev	14.95





# B1L000S, U2-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L000S, U2-ch12, adc0

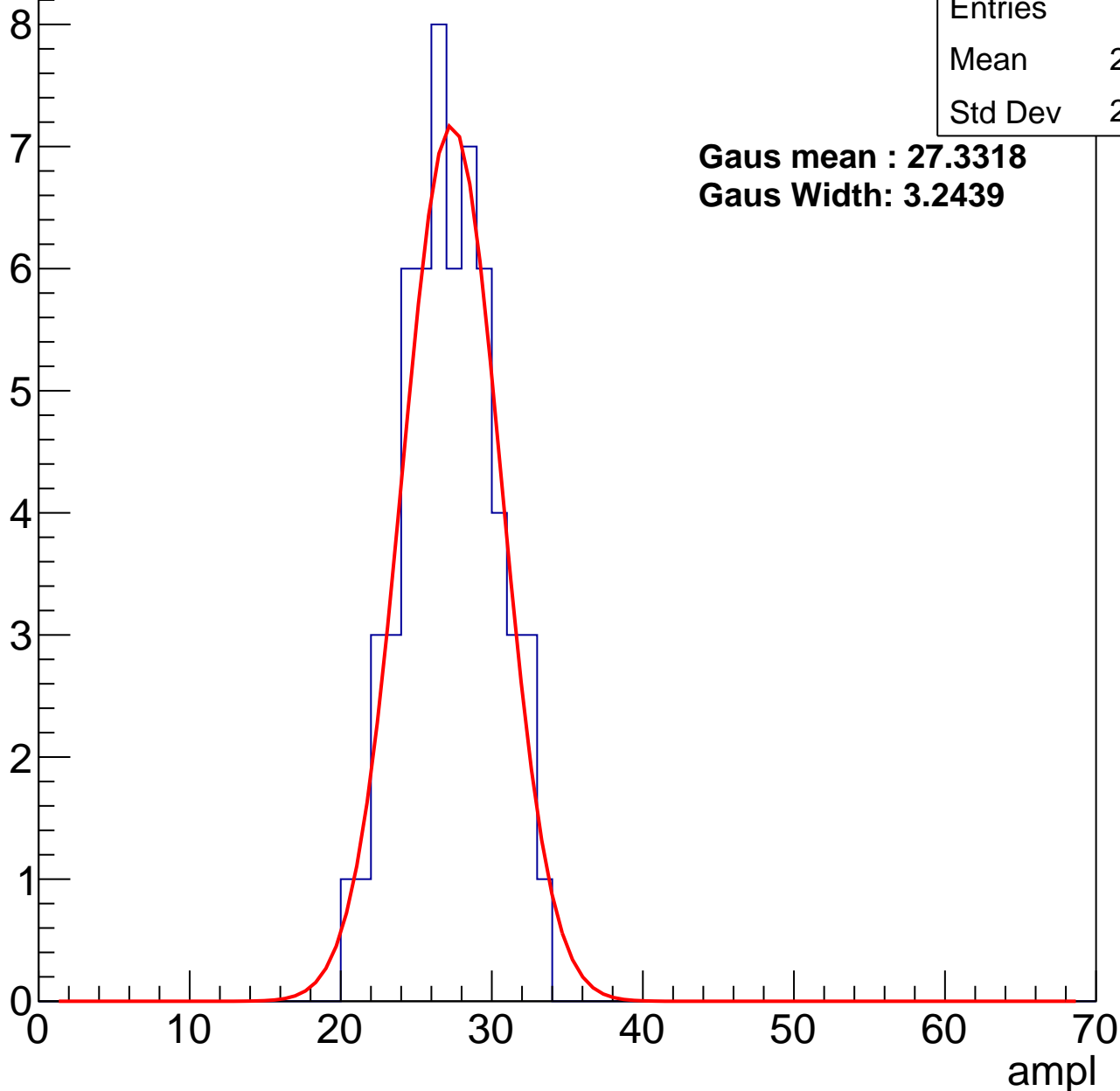
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	26.76
Std Dev	2.985

**Gaus mean : 27.3318**

**Gaus Width: 3.2439**



# B1L000S, U2-ch12, adc1

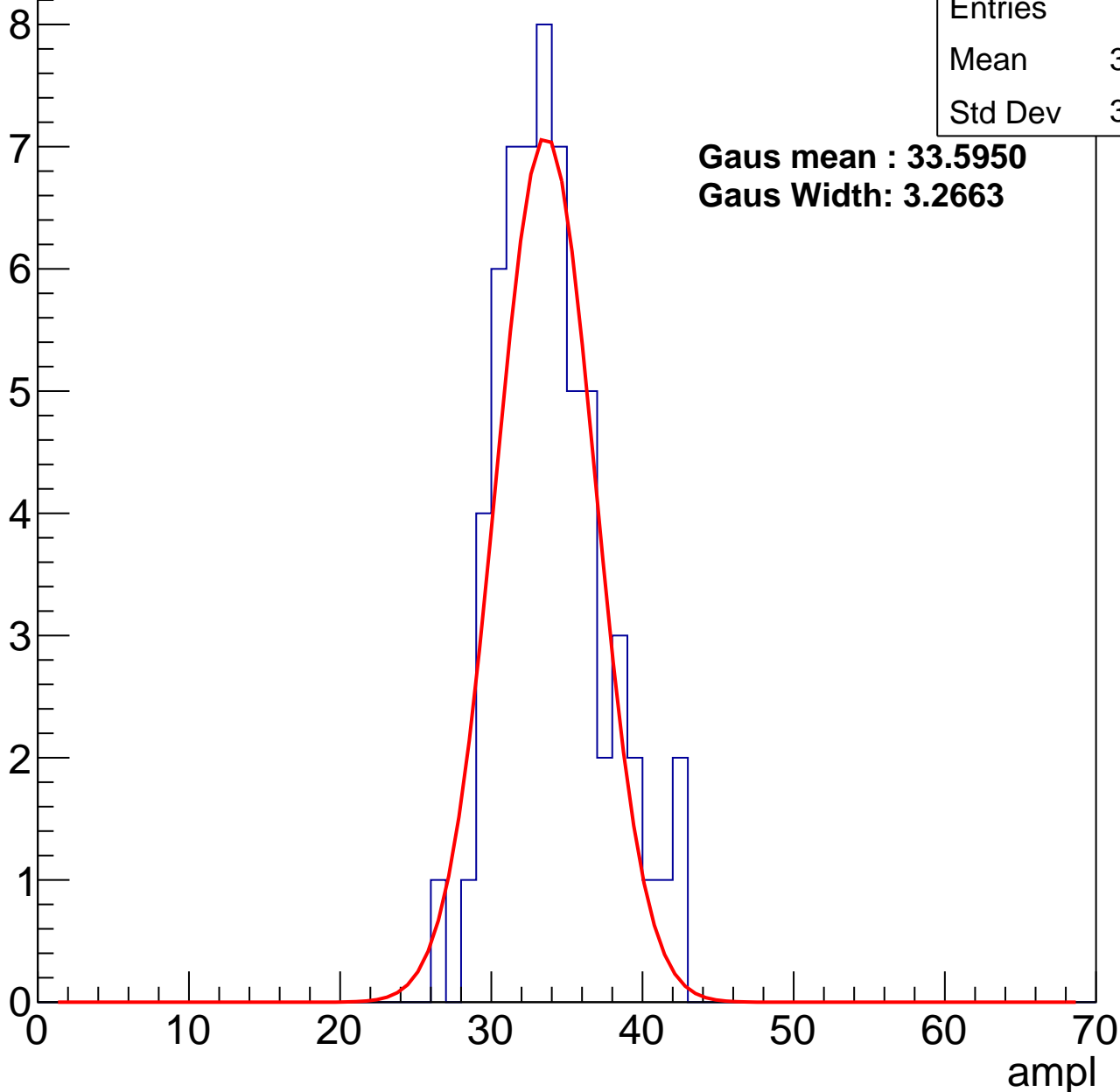
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	33.53
Std Dev	3.444

**Gaus mean : 33.5950**

**Gaus Width: 3.2663**



# B1L000S, U2-ch12, adc2

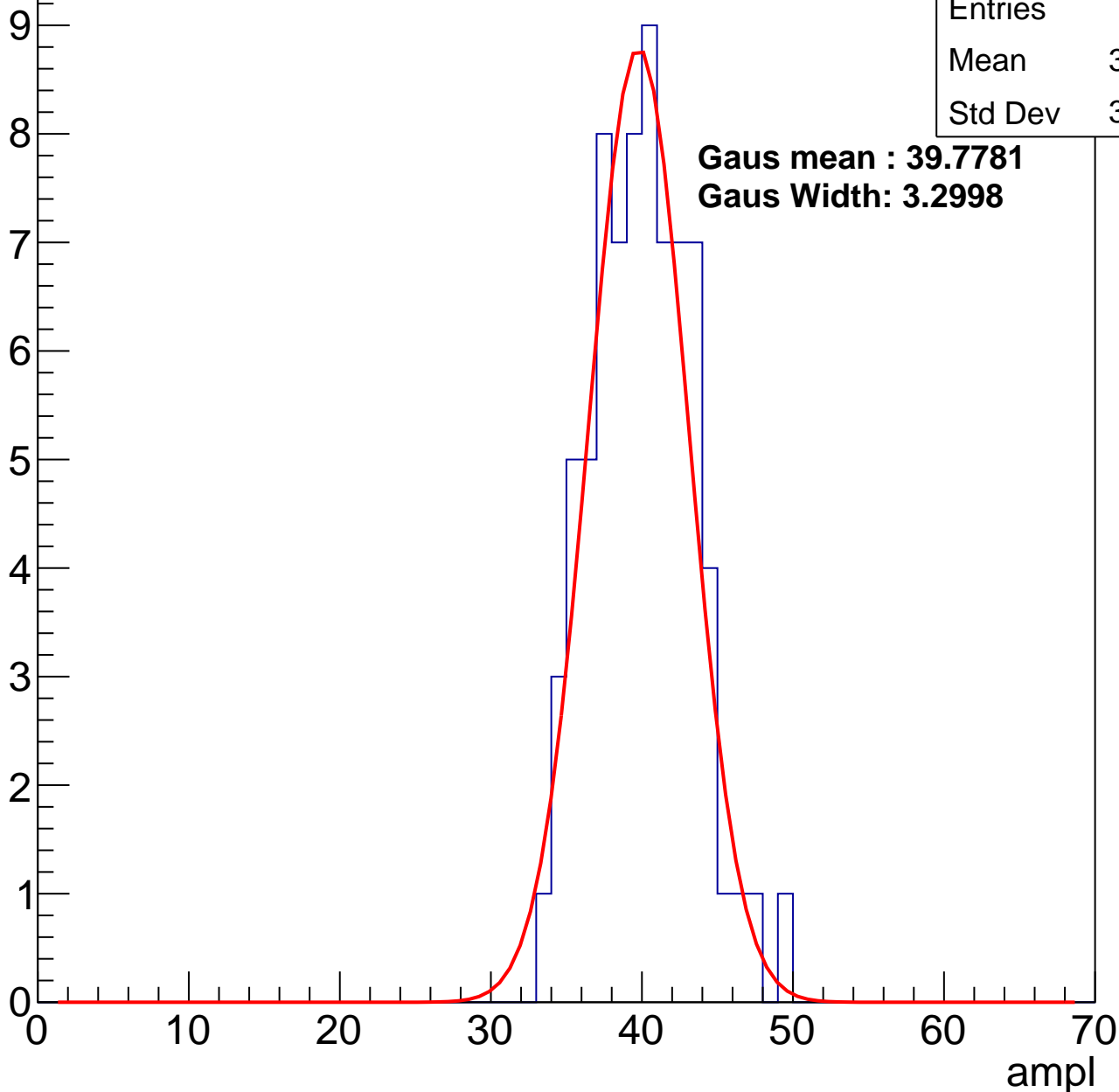
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	39.59
Std Dev	3.287

**Gaus mean : 39.7781**

**Gaus Width: 3.2998**

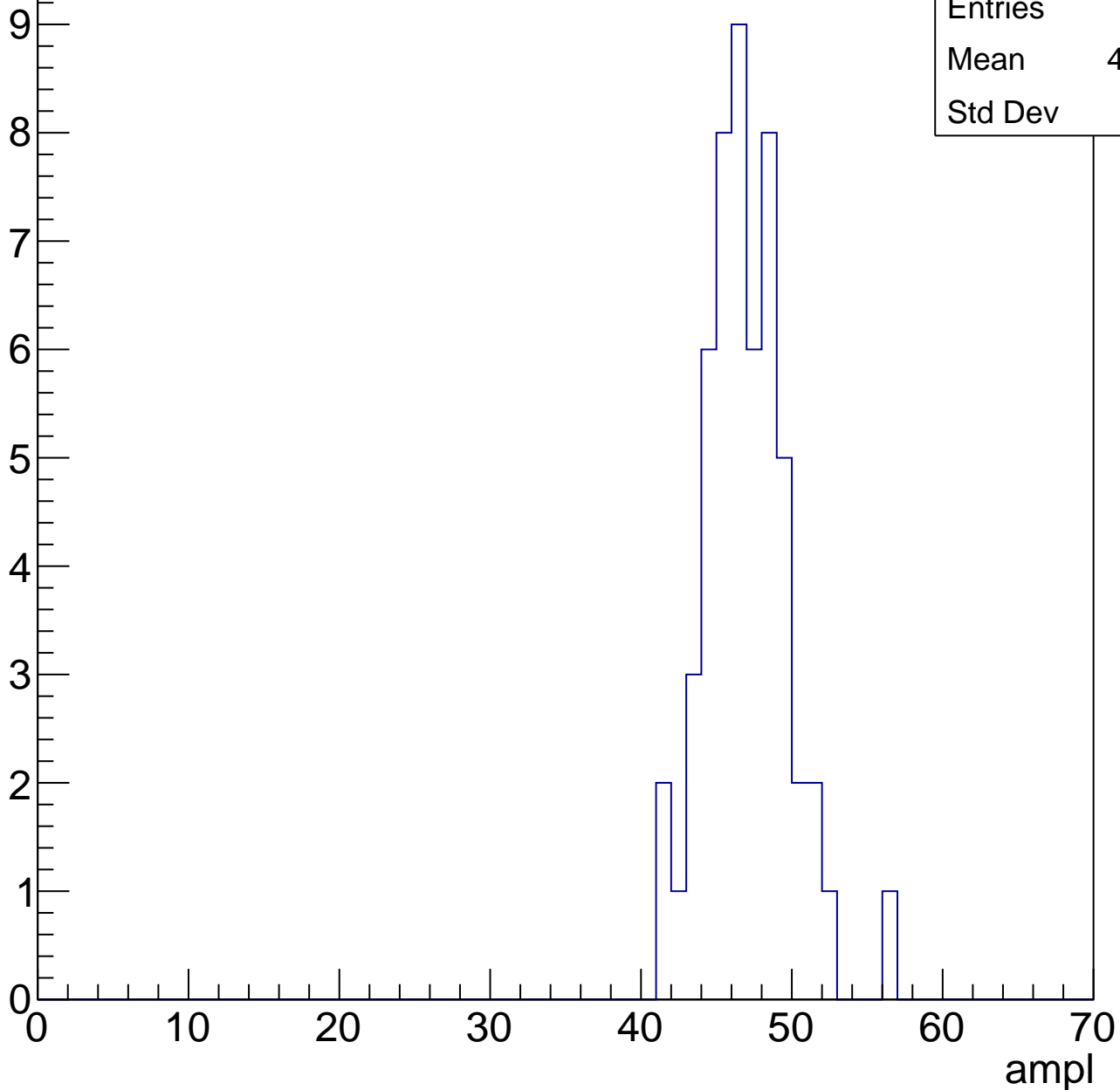


# B1L000S, U2-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	46.52
Std Dev	2.76



# B1L000S, U2-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

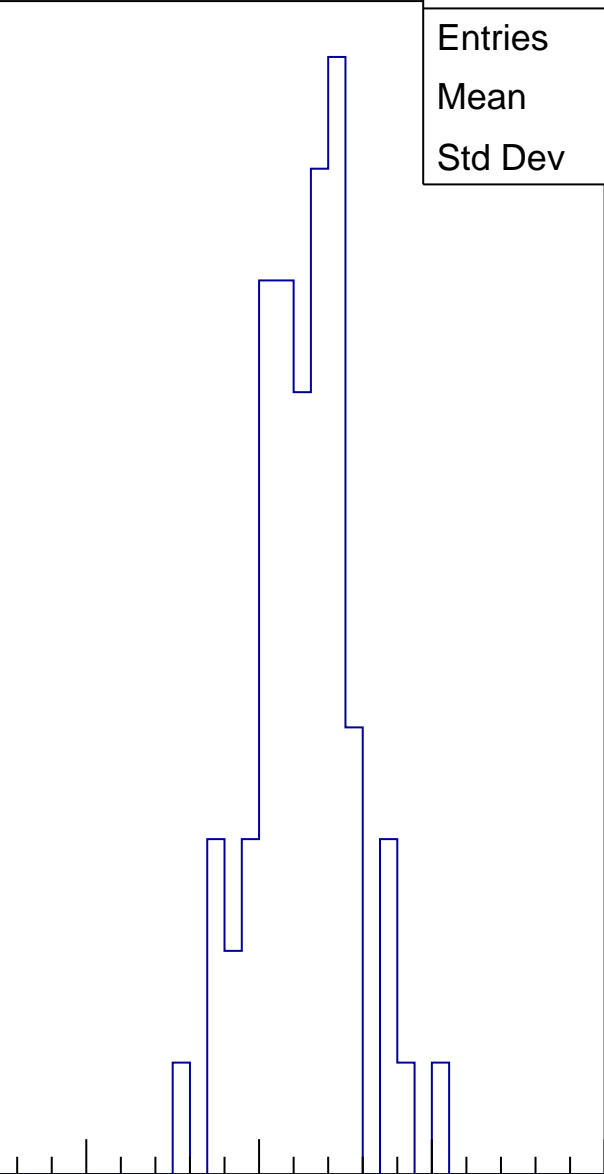
Entries	60
Mean	52.12
Std Dev	2.835

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

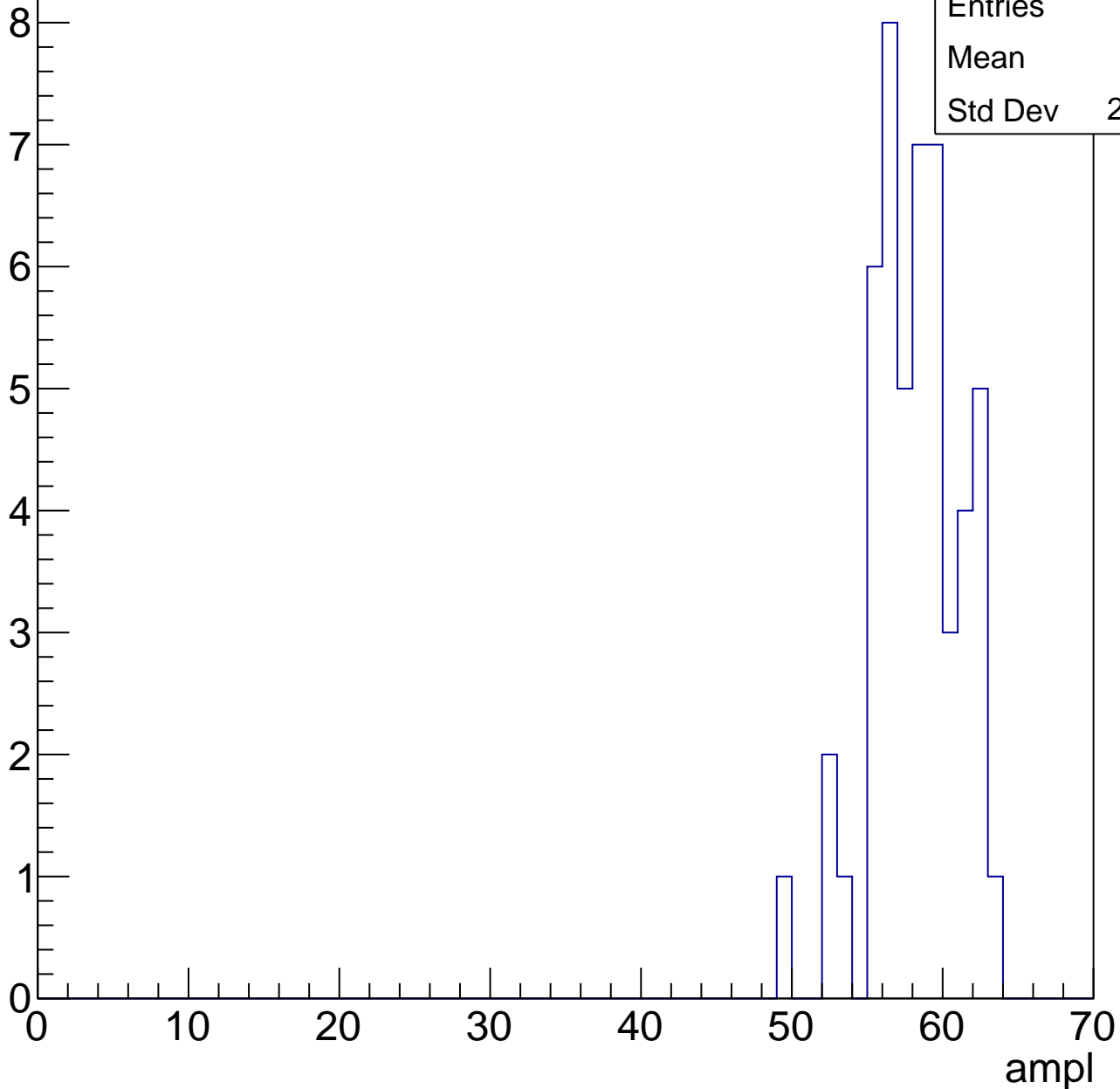


# B1L000S, U2-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	57.7
Std Dev	2.914

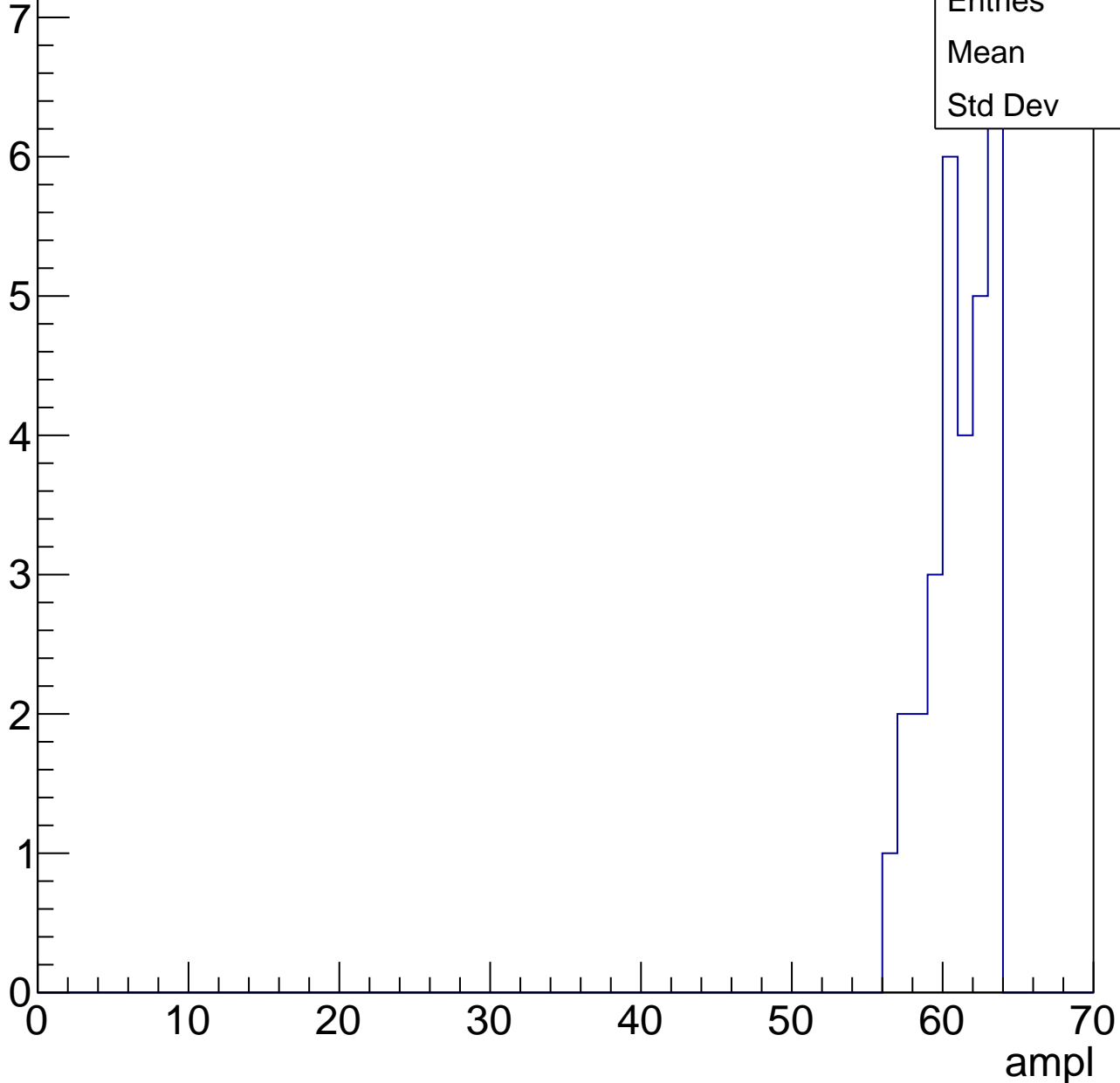


# B1L000S, U2-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	60.6
Std Dev	2.01

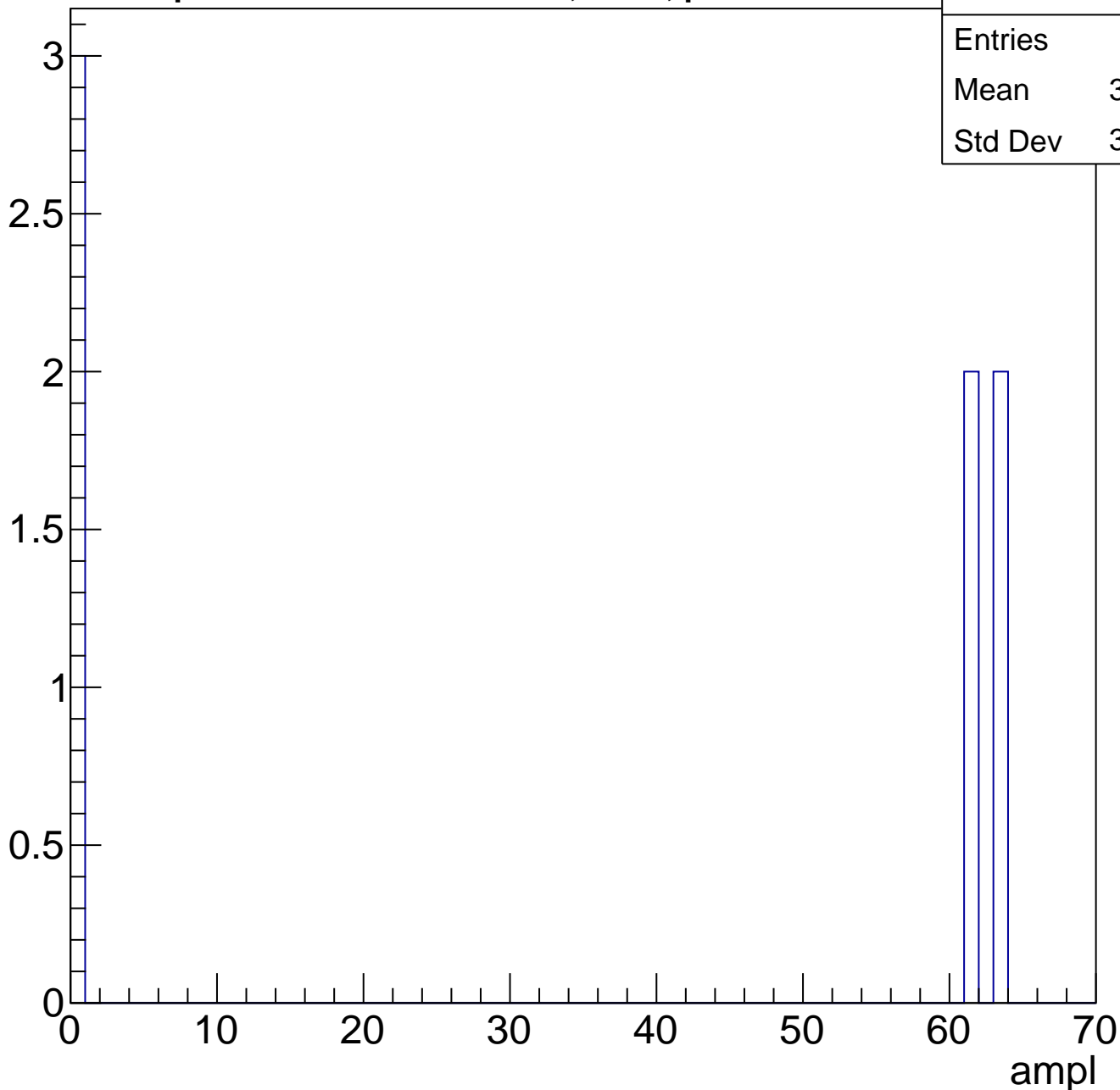




# B1L000S, U2-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch13, adc0

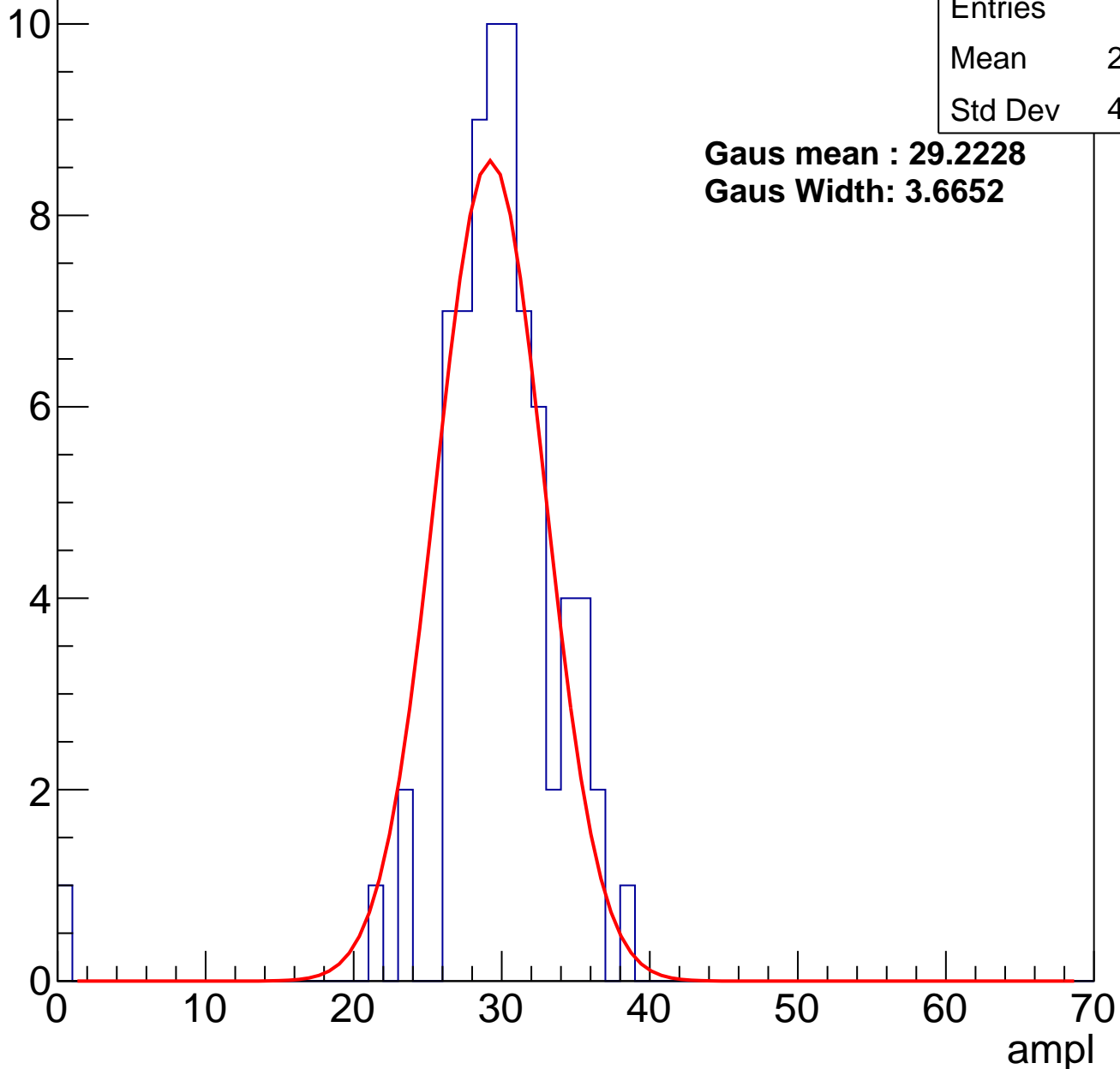
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	29.33
Std Dev	4.705

**Gaus mean : 29.2228**

**Gaus Width: 3.6652**

Entry



# B1L000S, U2-ch13, adc1

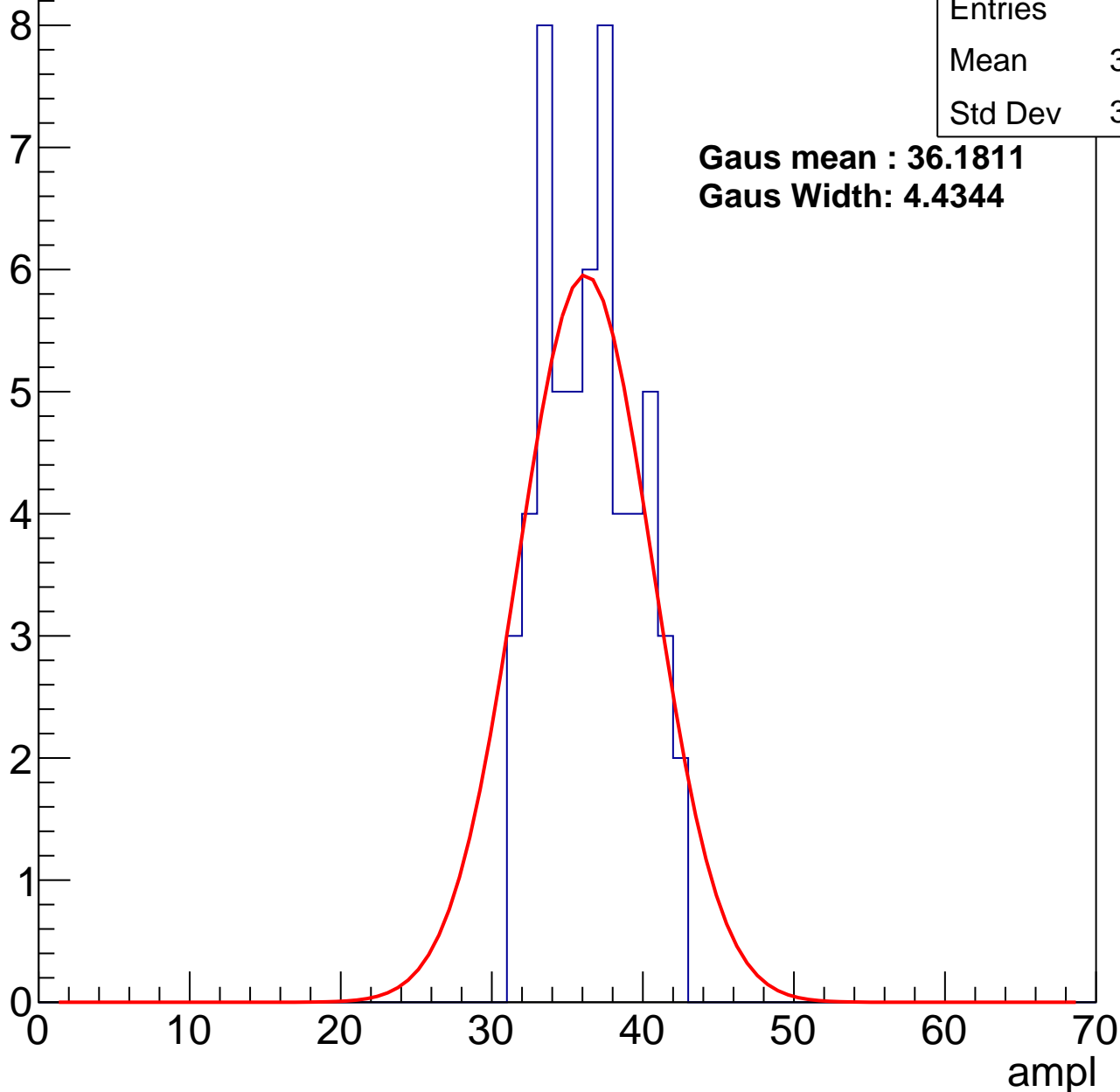
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	36.09
Std Dev	3.028

**Gaus mean : 36.1811**

**Gaus Width: 4.4344**



# B1L000S, U2-ch13, adc2

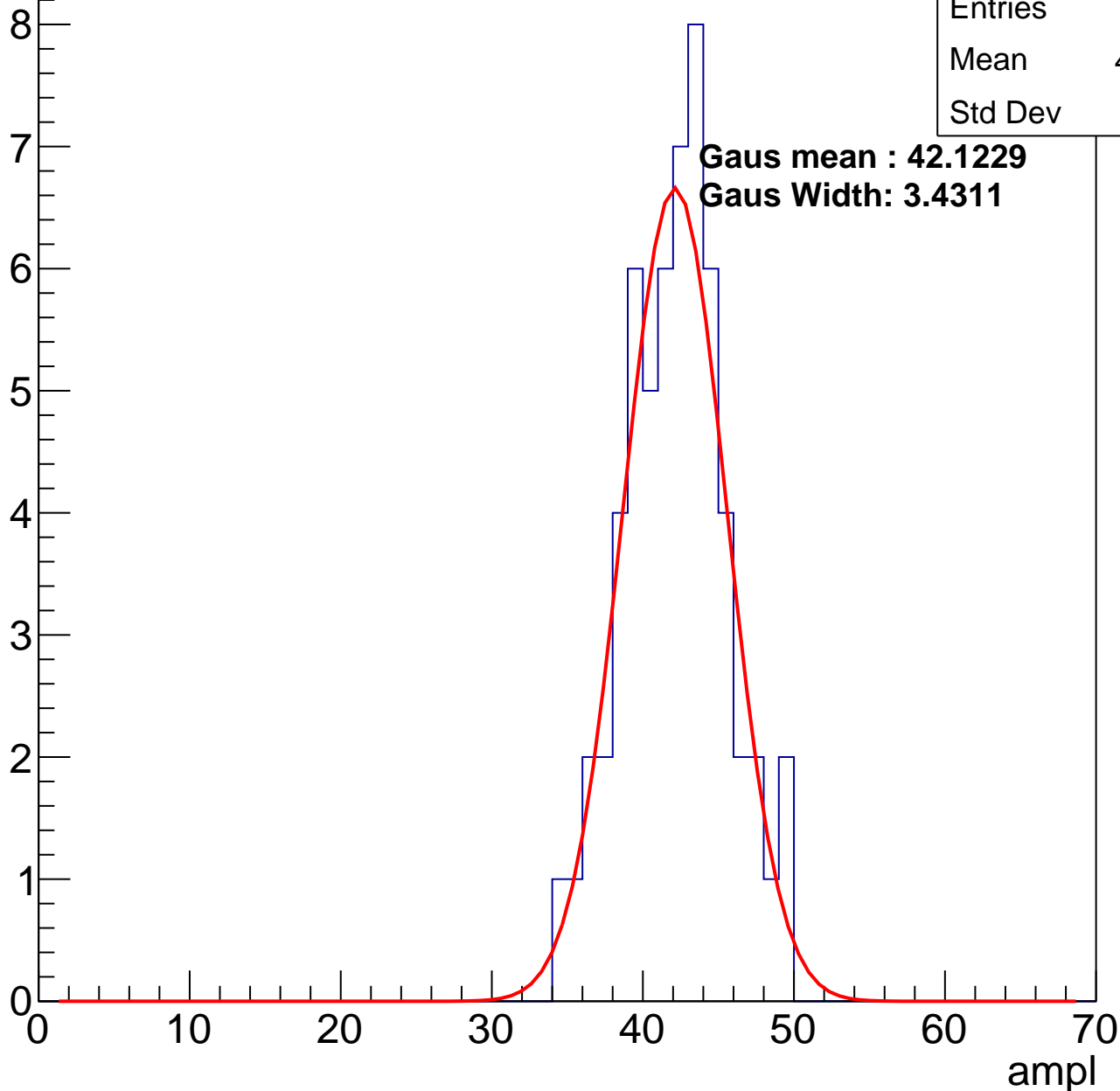
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.71
Std Dev	3.34

**Gaus mean : 42.1229**

**Gaus Width: 3.4311**

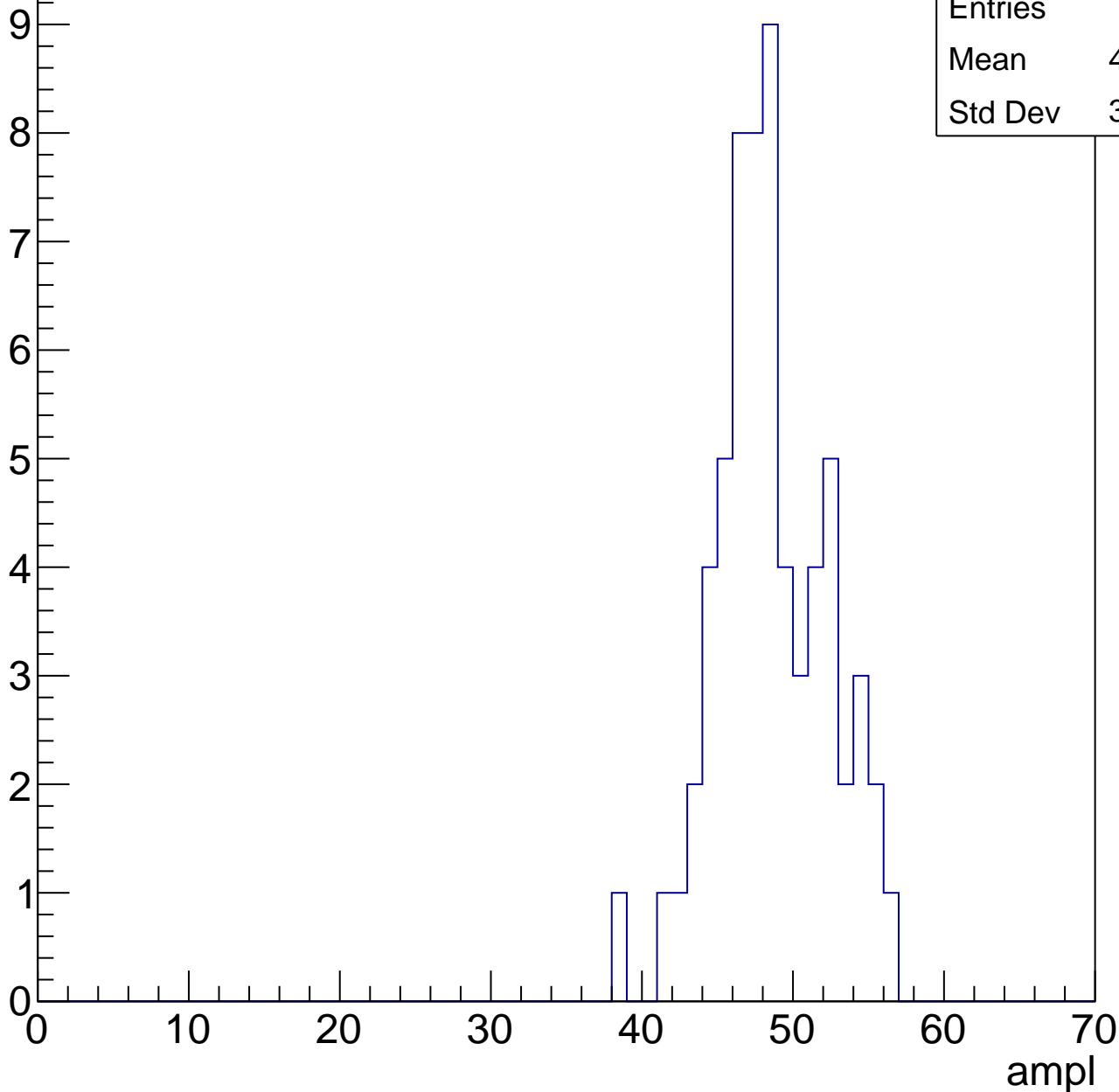


# B1L000S, U2-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

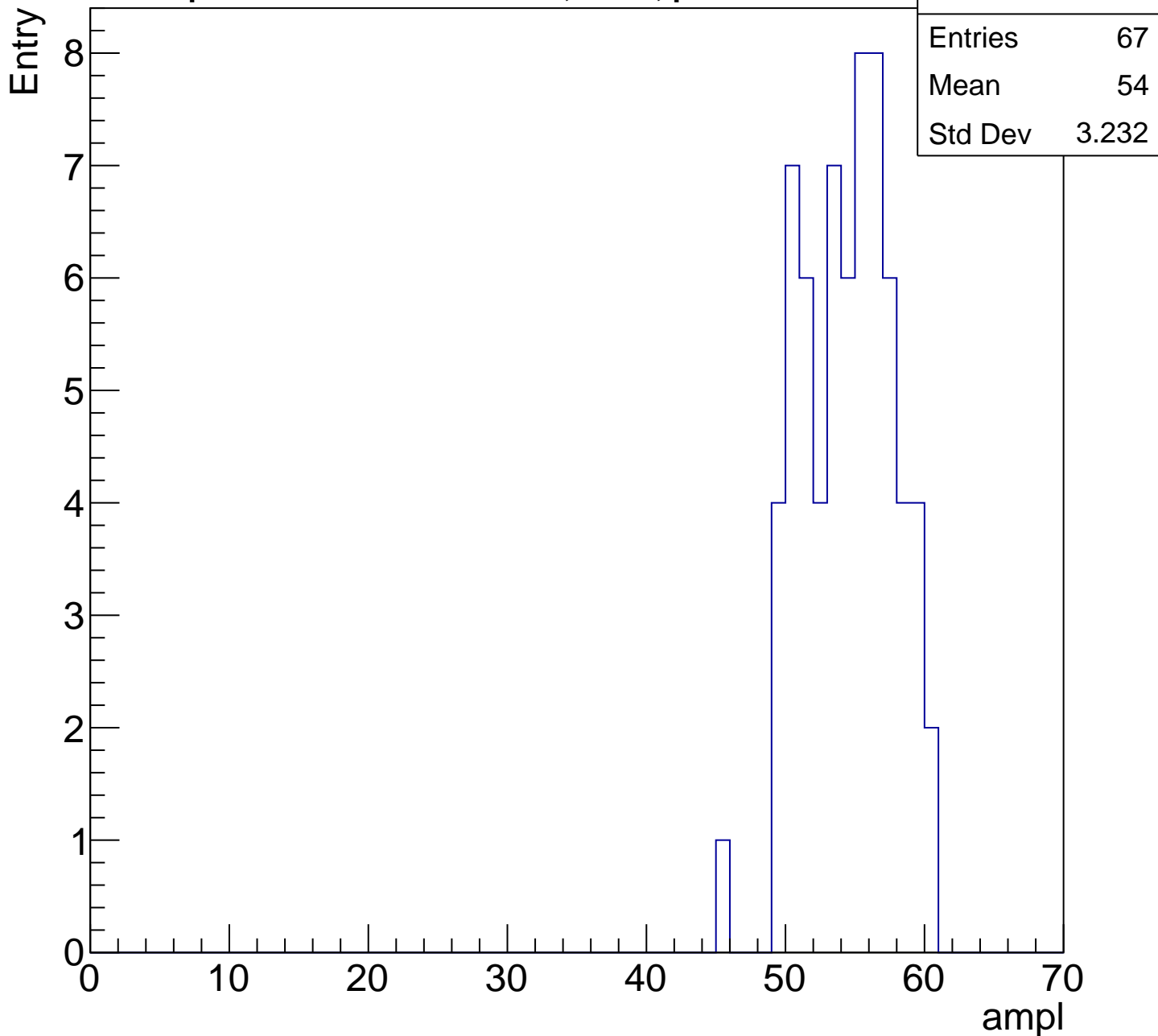
Entry

Entries	63
Mean	48.06
Std Dev	3.642



# B1L000S, U2-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

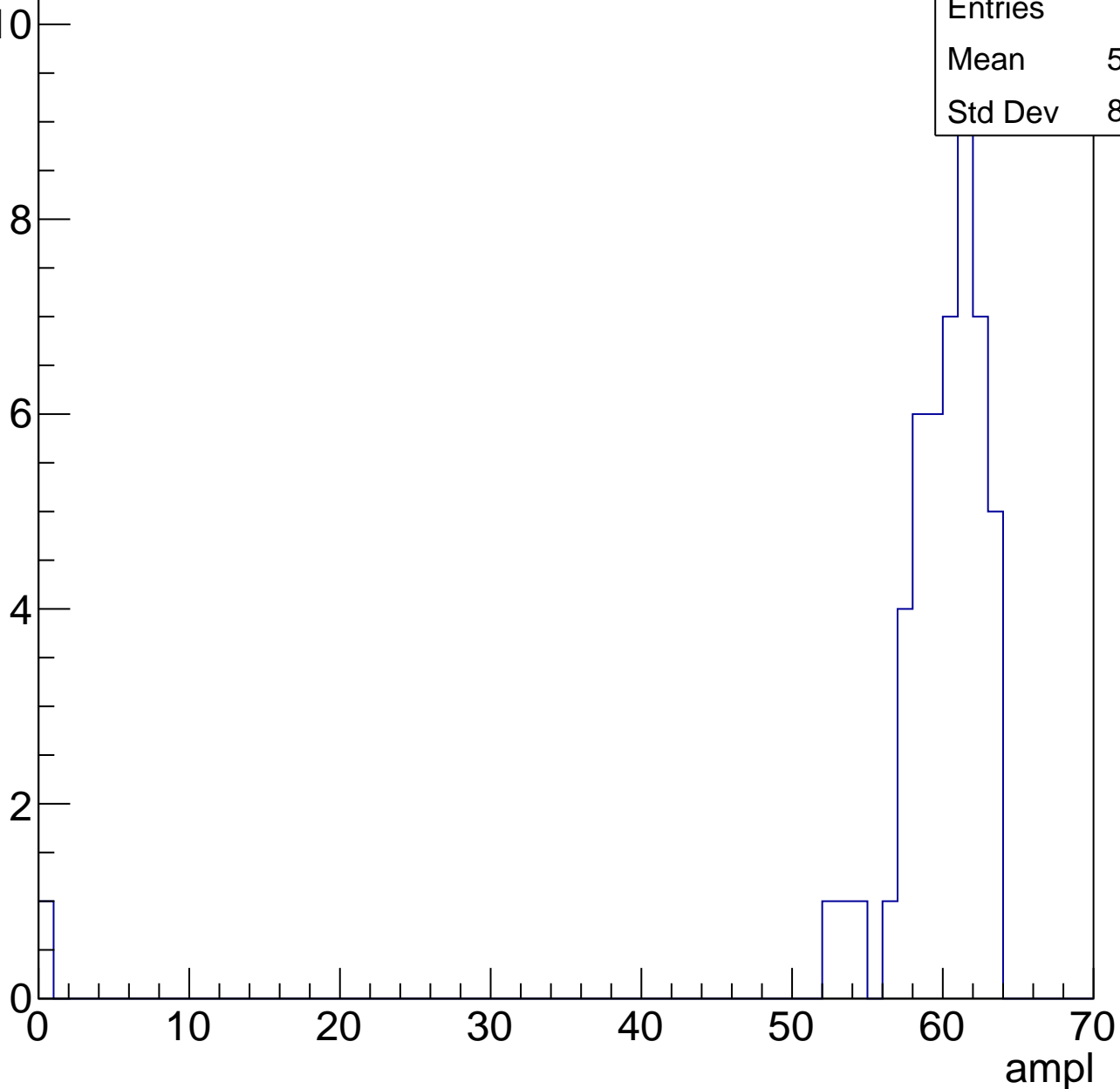


# B1L000S, U2-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.48
Std Dev	8.716

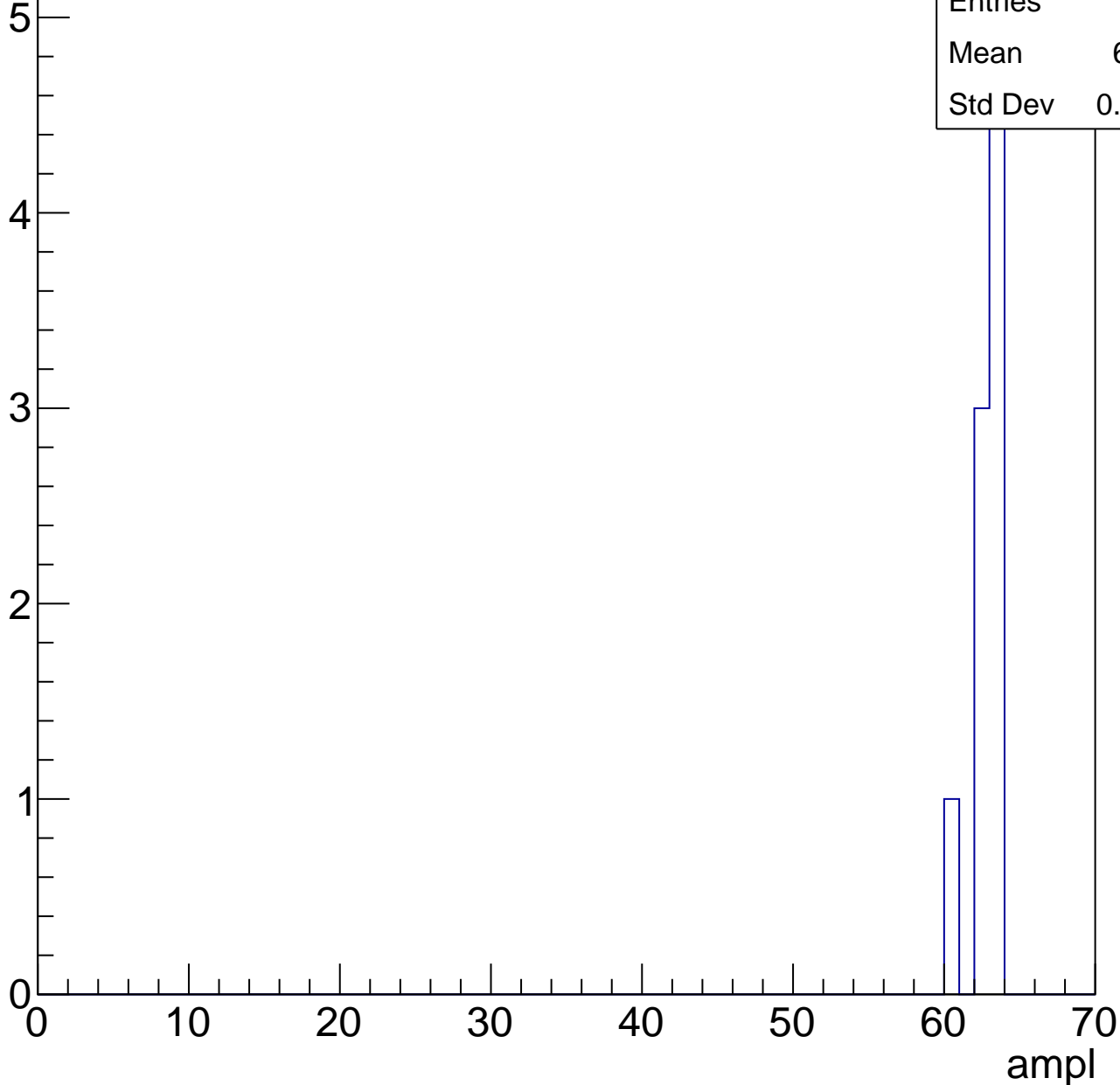


# B1L000S, U2-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	9
Mean	62.33
Std Dev	0.9428





# B1L000S, U2-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U2-ch14, adc0

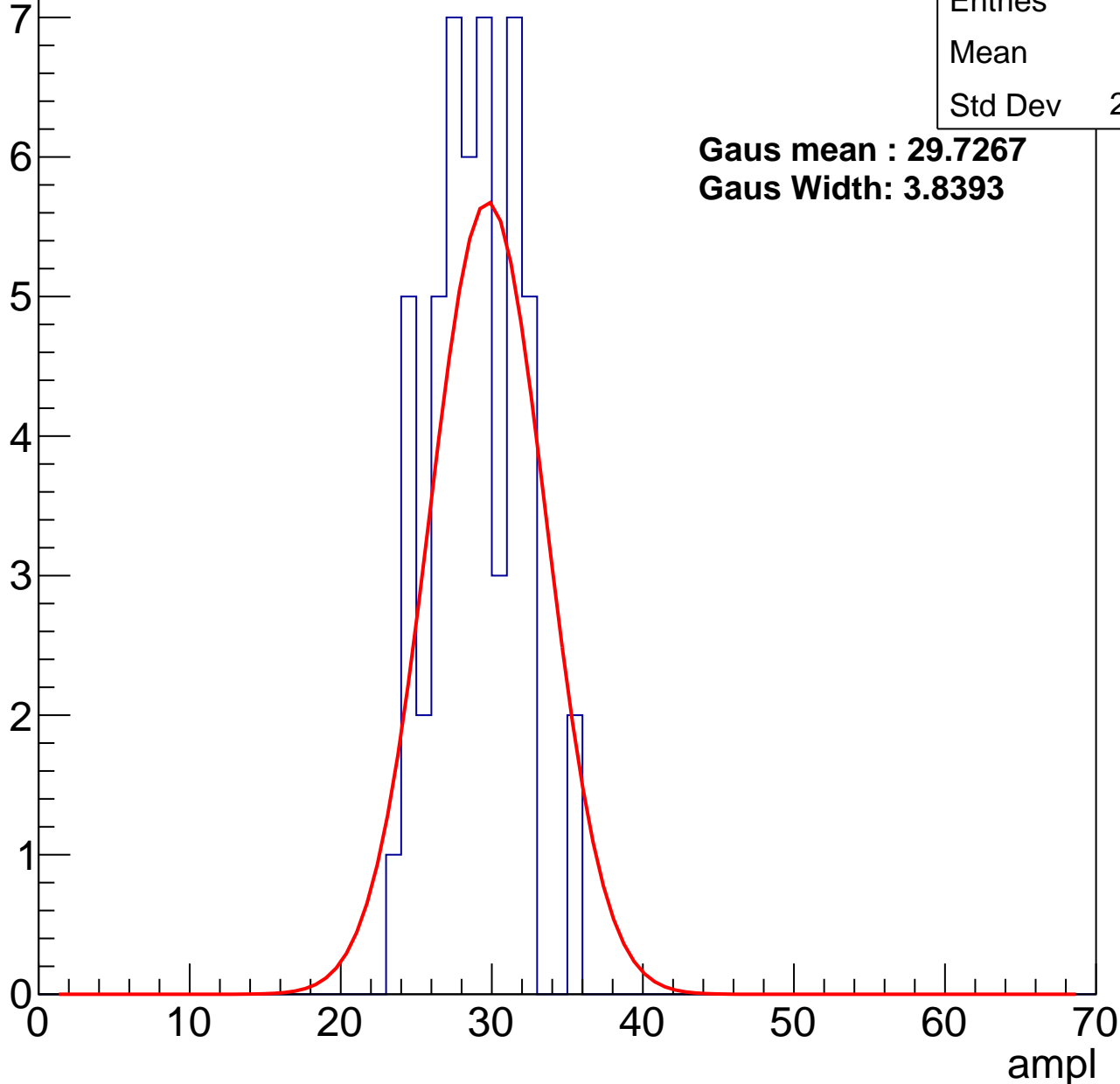
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	28.4
Std Dev	2.835

**Gaus mean : 29.7267**

**Gaus Width: 3.8393**



# B1L000S, U2-ch14, adc1

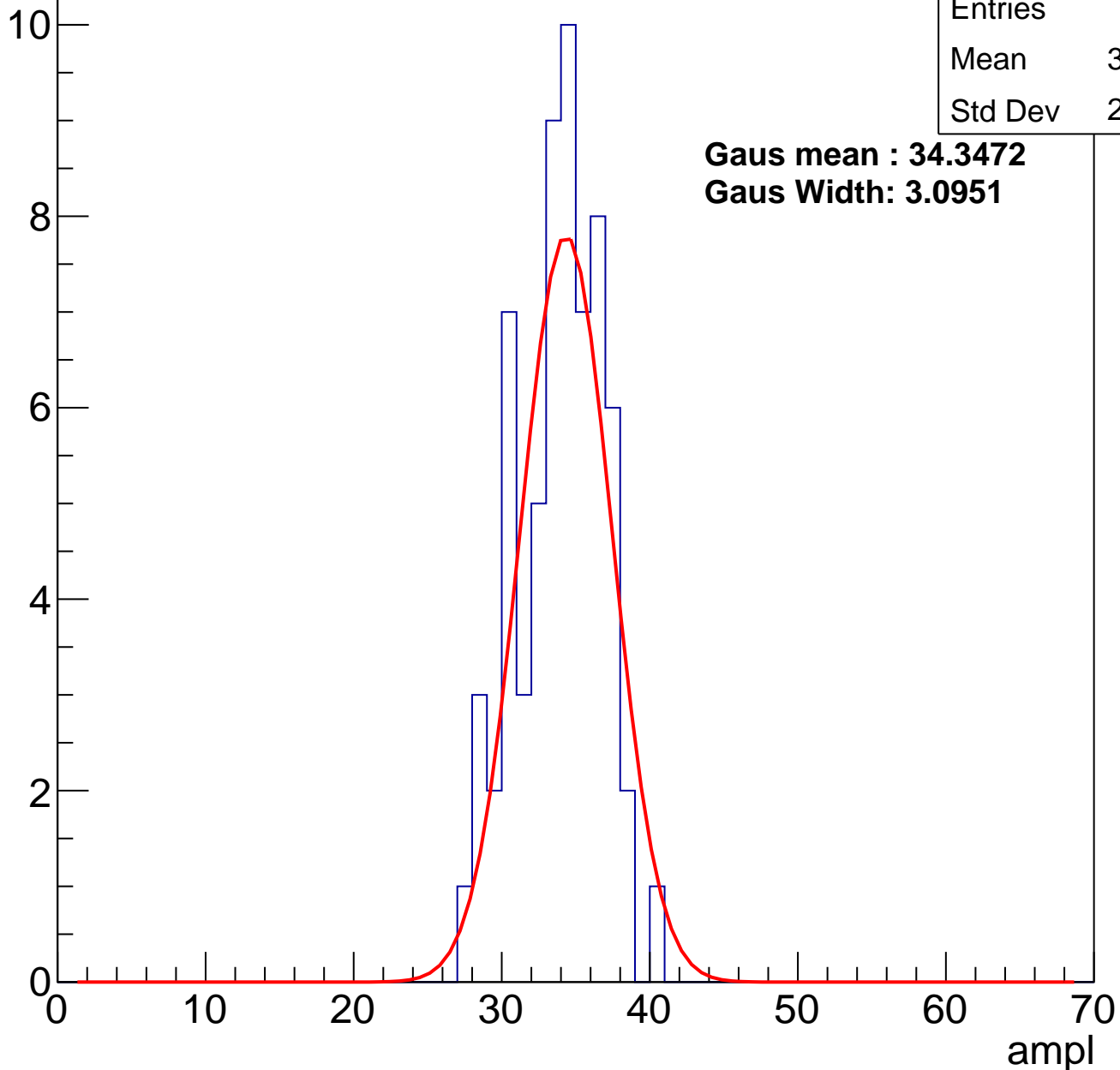
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	33.44
Std Dev	2.839

**Gaus mean : 34.3472**

**Gaus Width: 3.0951**

Entry



# B1L000S, U2-ch14, adc2

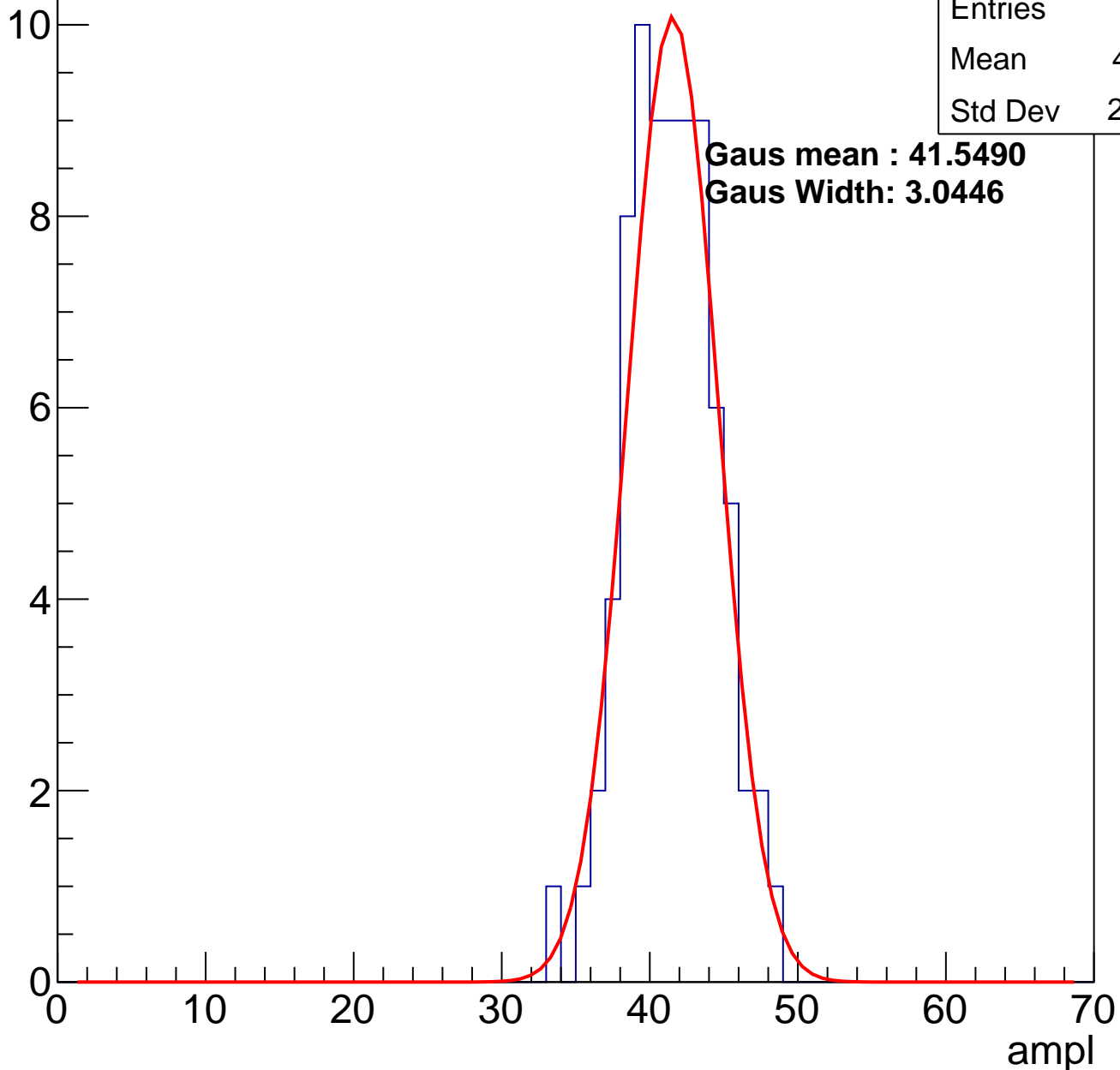
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	41.01
Std Dev	2.963

**Gaus mean : 41.5490**

**Gaus Width: 3.0446**

Entry

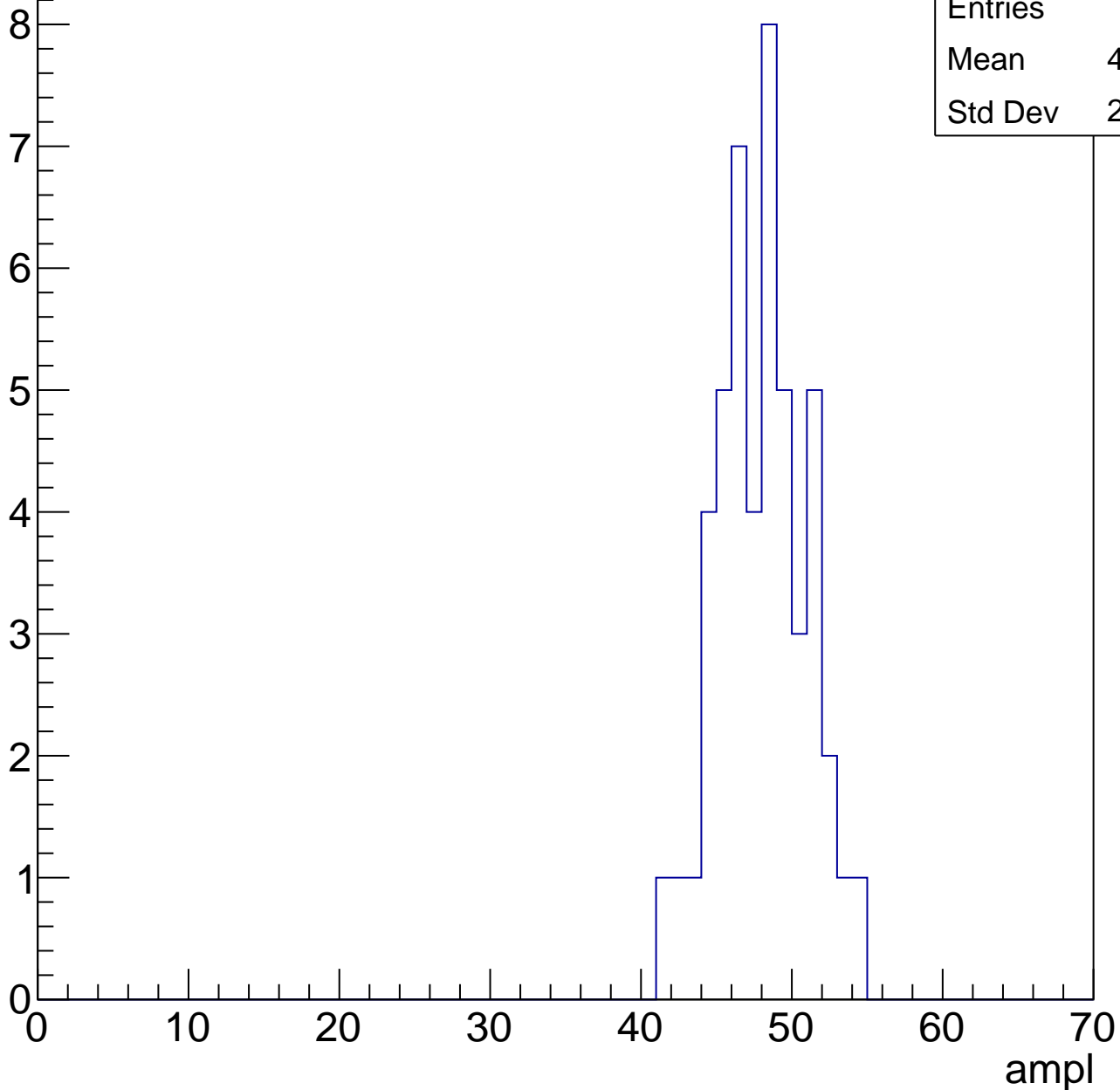


# B1L000S, U2-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

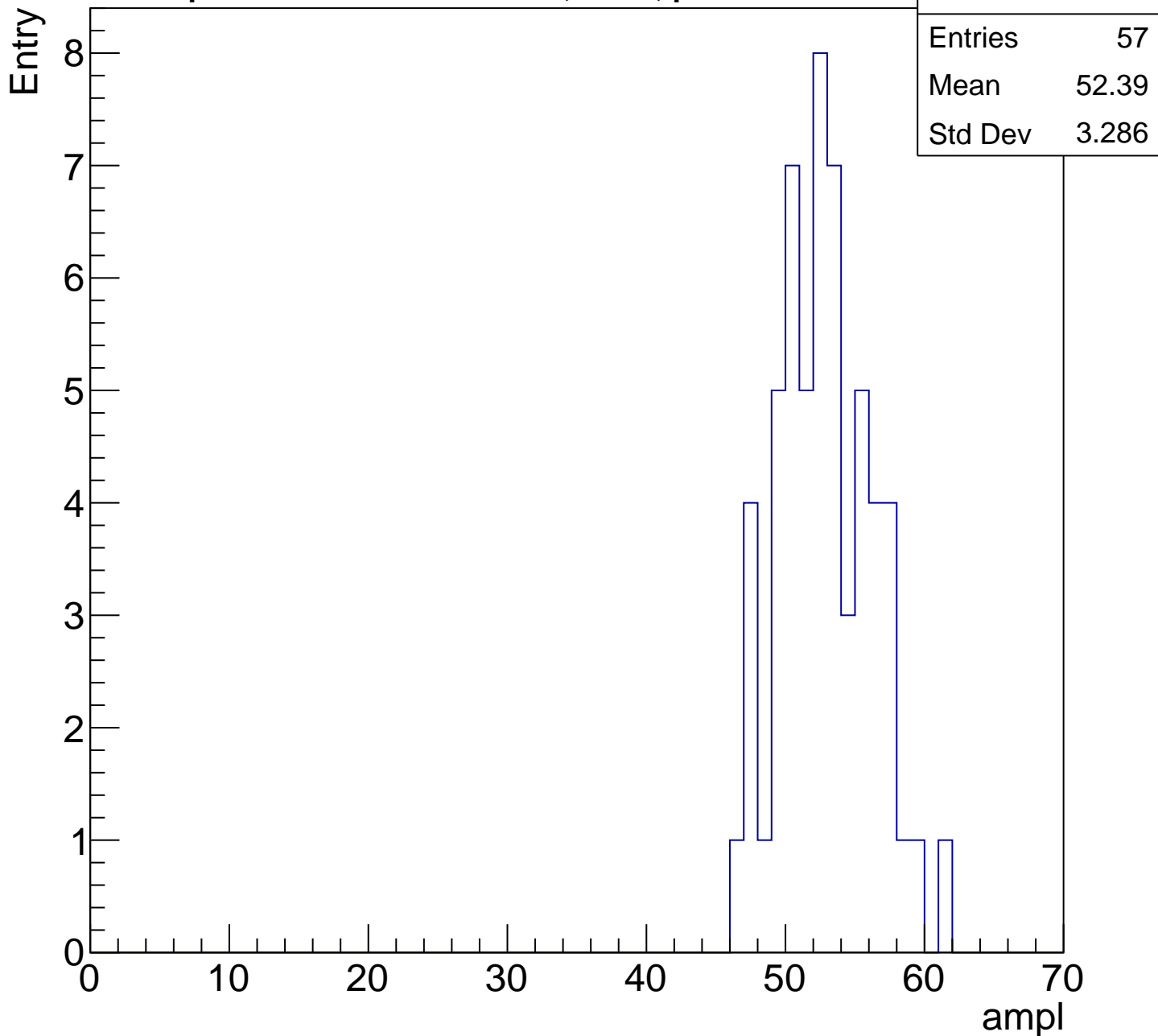
Entry

Entries	48
Mean	47.54
Std Dev	2.872



# B1L000S, U2-ch14, adc4

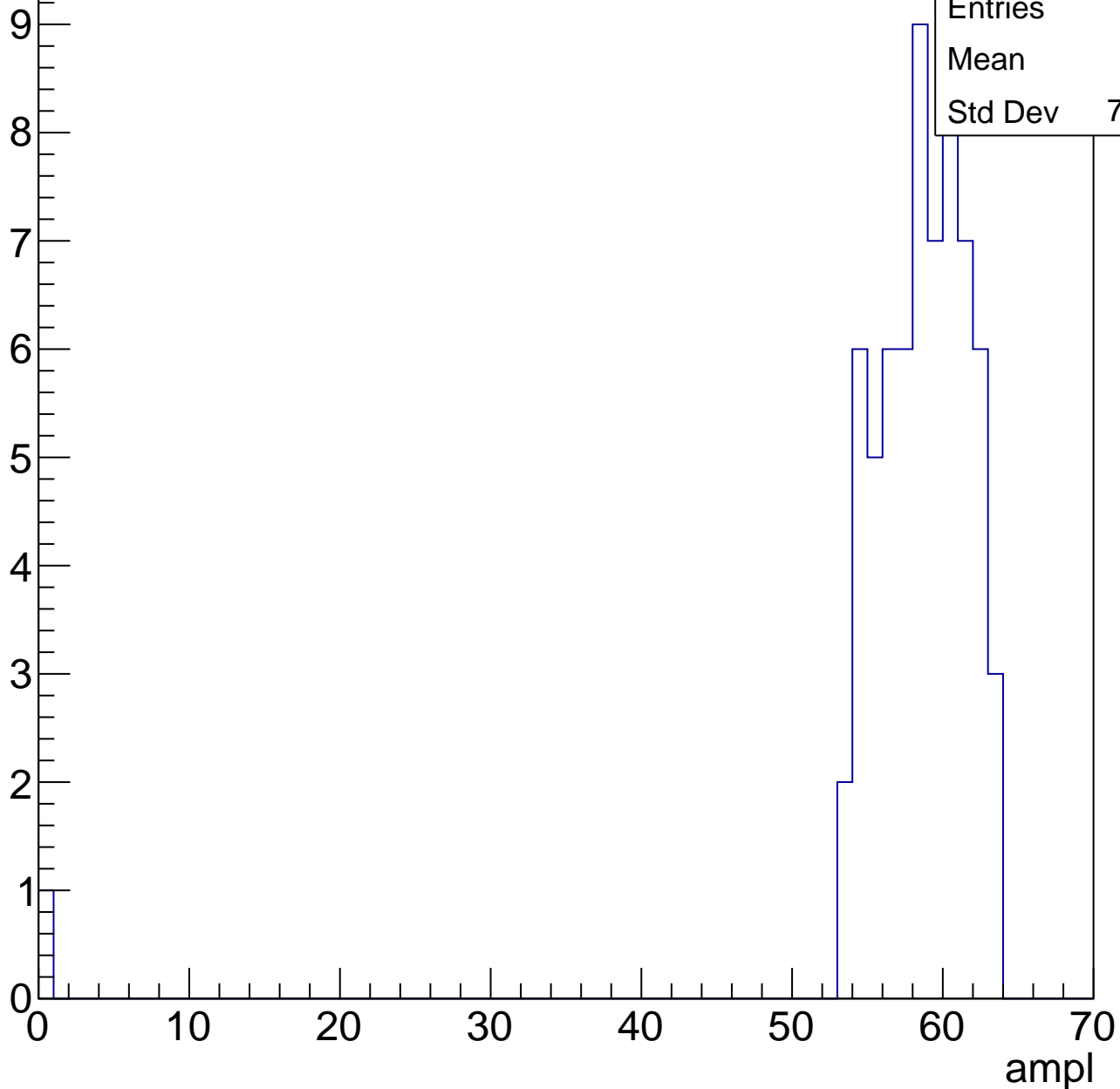
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

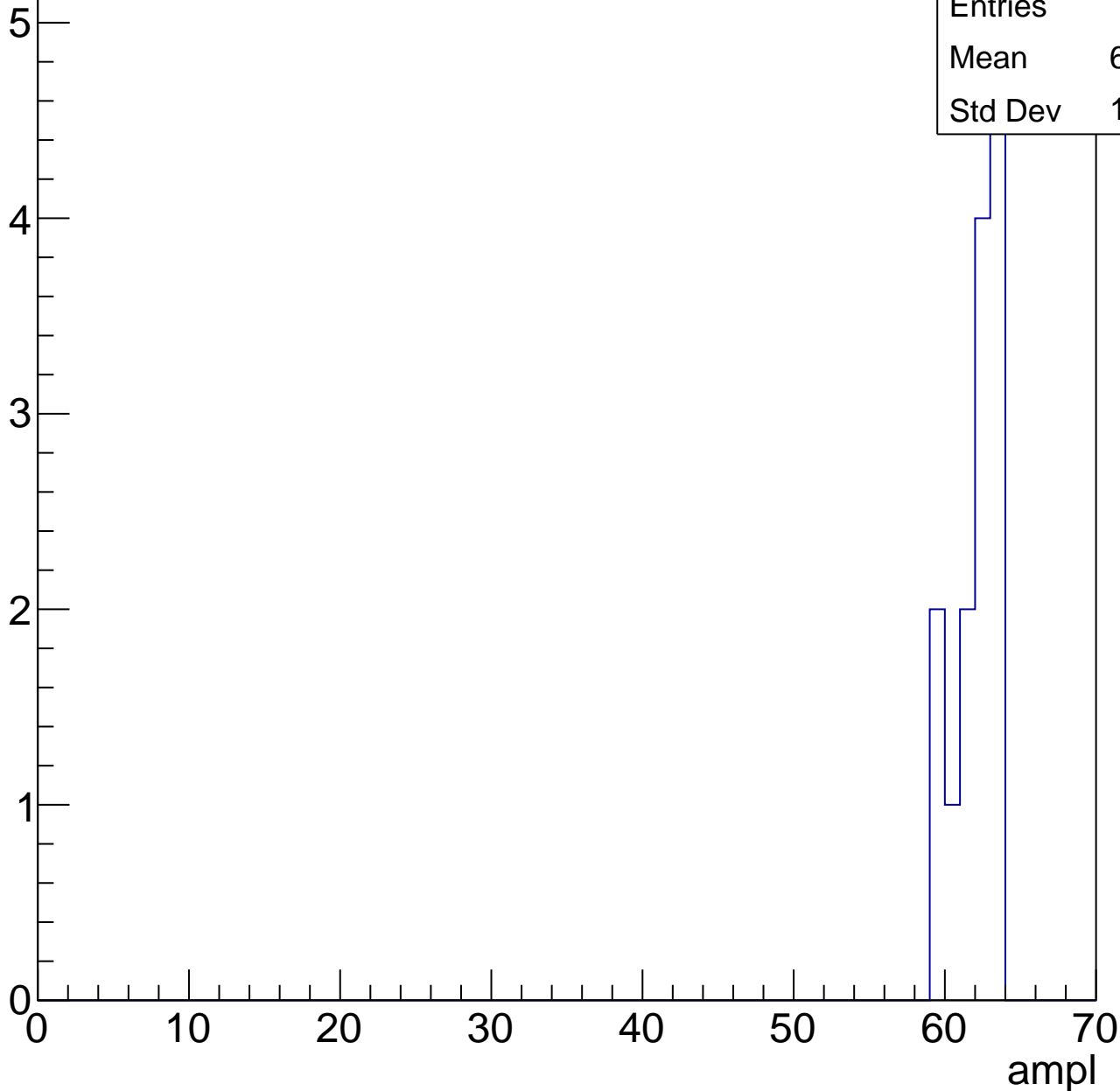


# B1L000S, U2-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.64
Std Dev	1.394





# B1L000S, U2-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch15, adc0

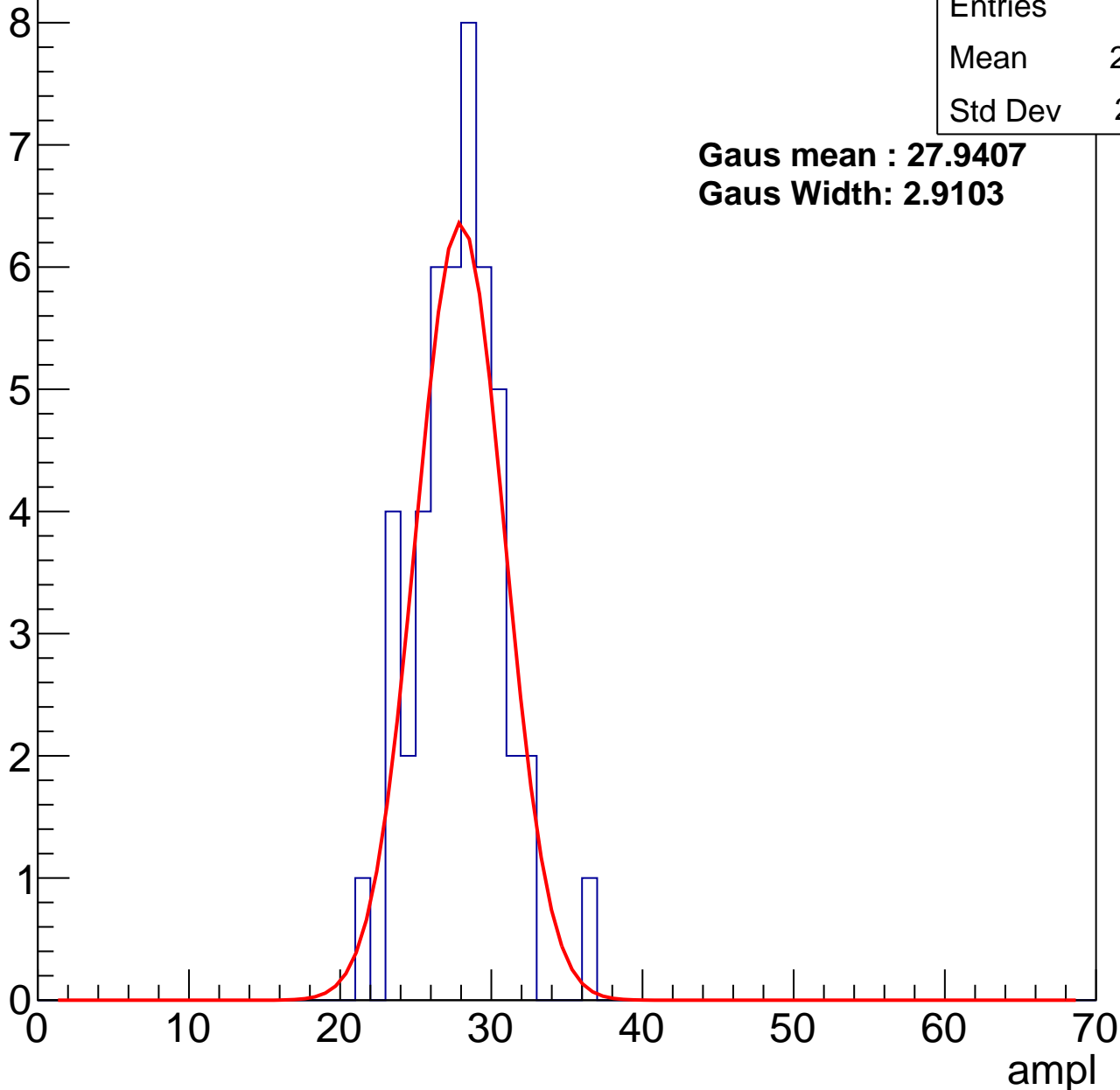
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	27.43
Std Dev	2.811

**Gaus mean : 27.9407**

**Gaus Width: 2.9103**



# B1L000S, U2-ch15, adc1

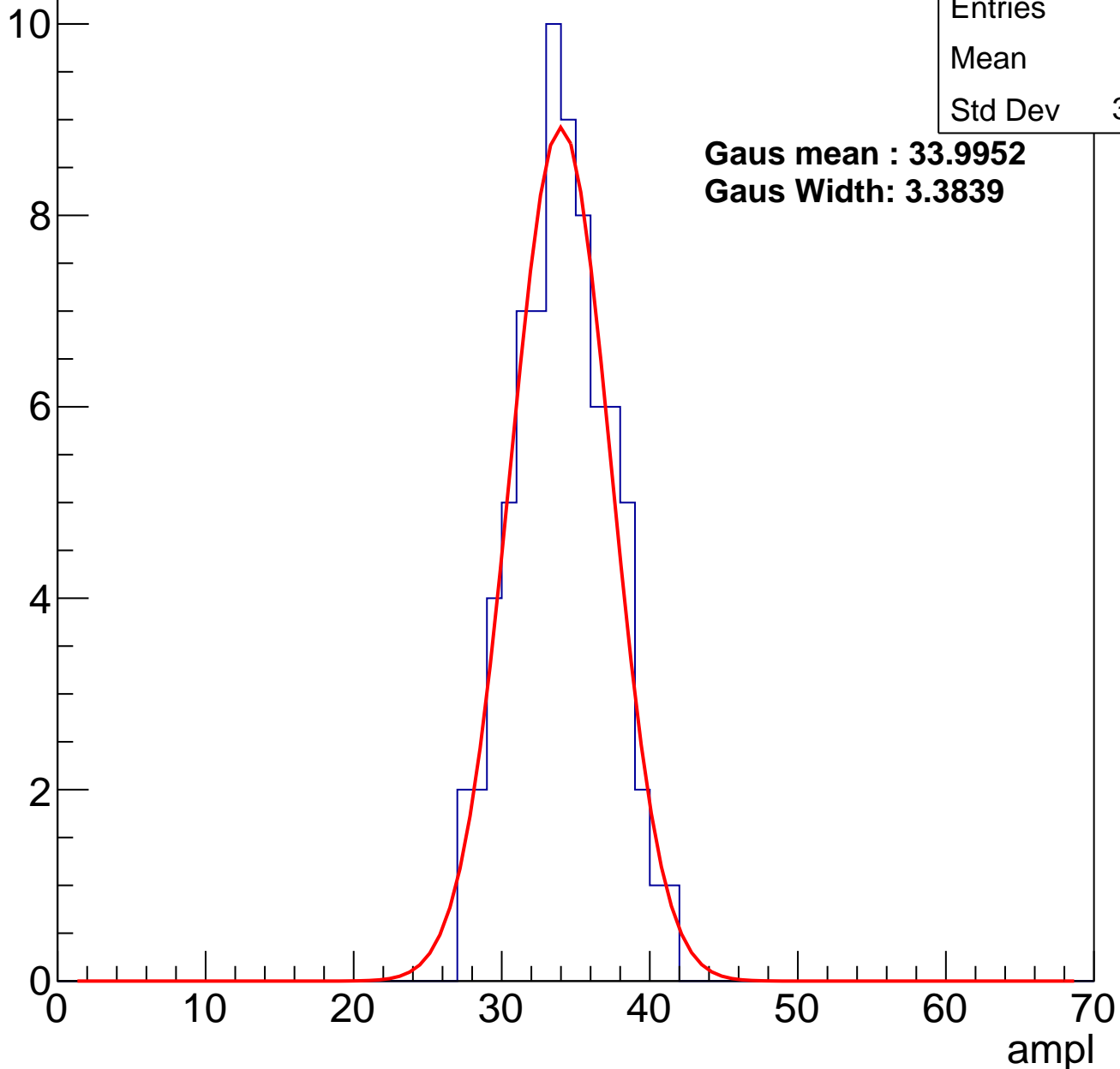
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	33.6
Std Dev	3.141

**Gaus mean : 33.9952**

**Gaus Width: 3.3839**

Entry



# B1L000S, U2-ch15, adc2

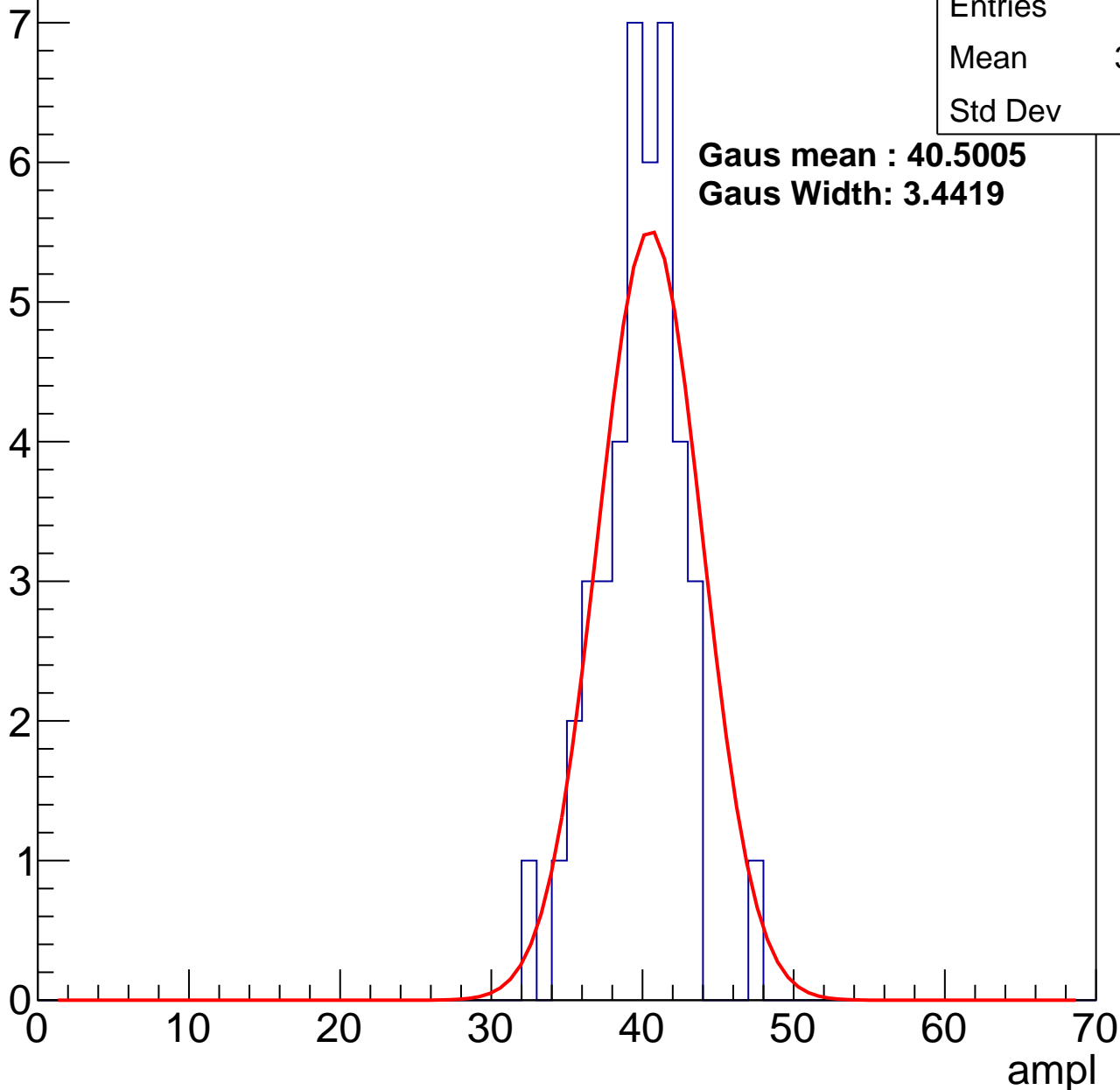
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	39.31
Std Dev	2.79

**Gaus mean : 40.5005**

**Gaus Width: 3.4419**

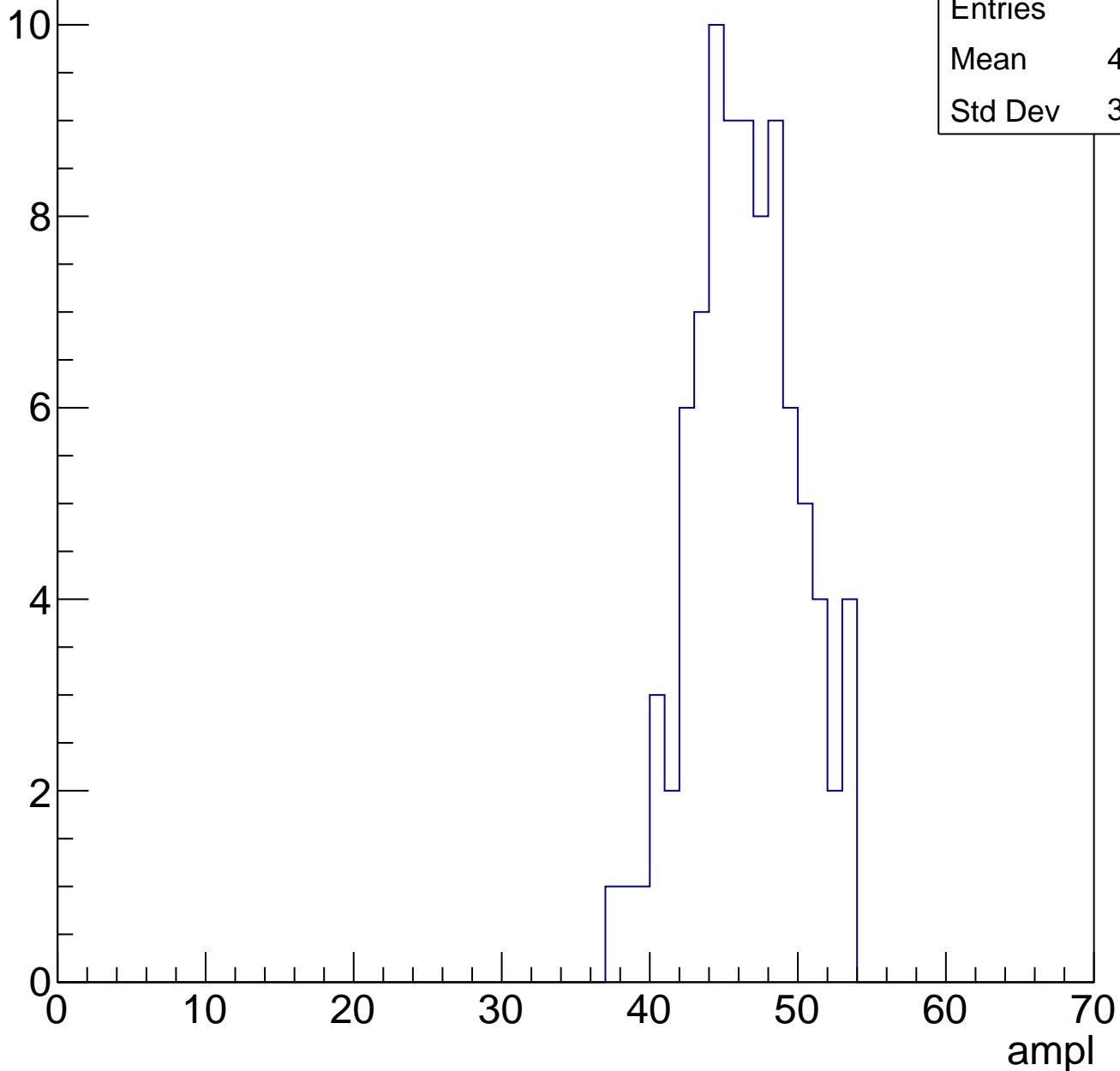


# B1L000S, U2-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	87
Mean	45.98
Std Dev	3.565

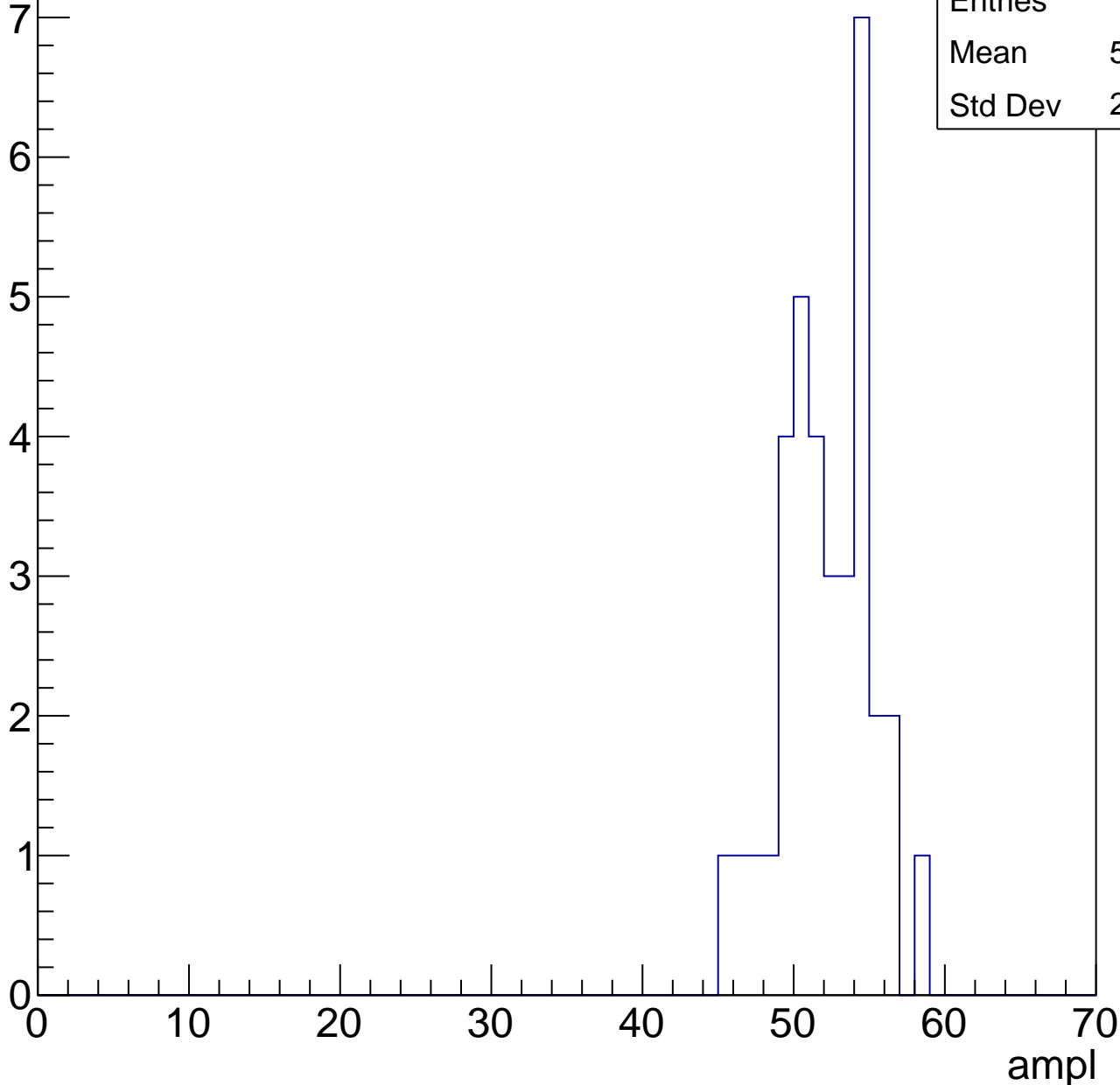


# B1L000S, U2-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

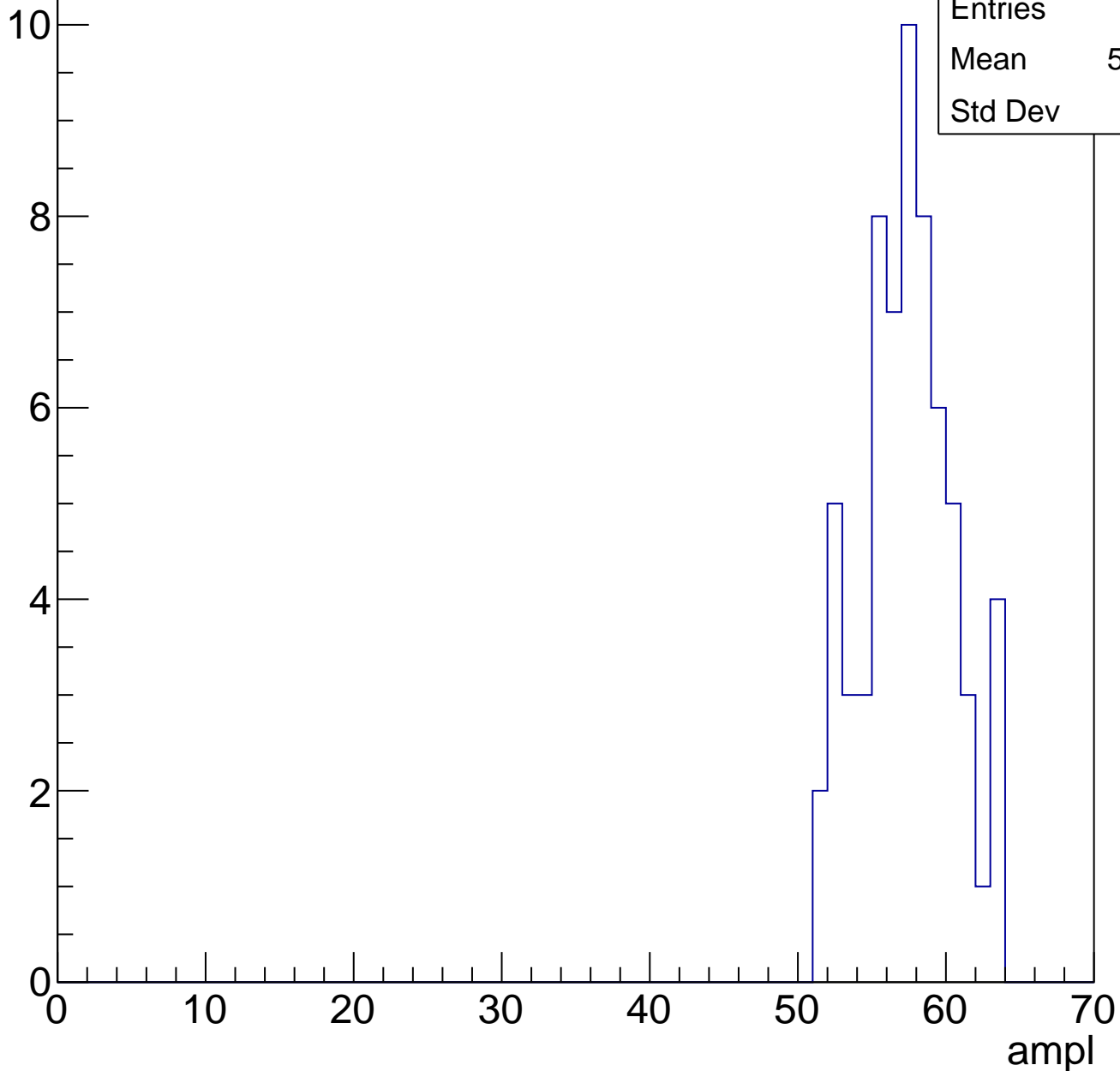
Entries	35
Mean	51.69
Std Dev	2.925



# B1L000S, U2-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

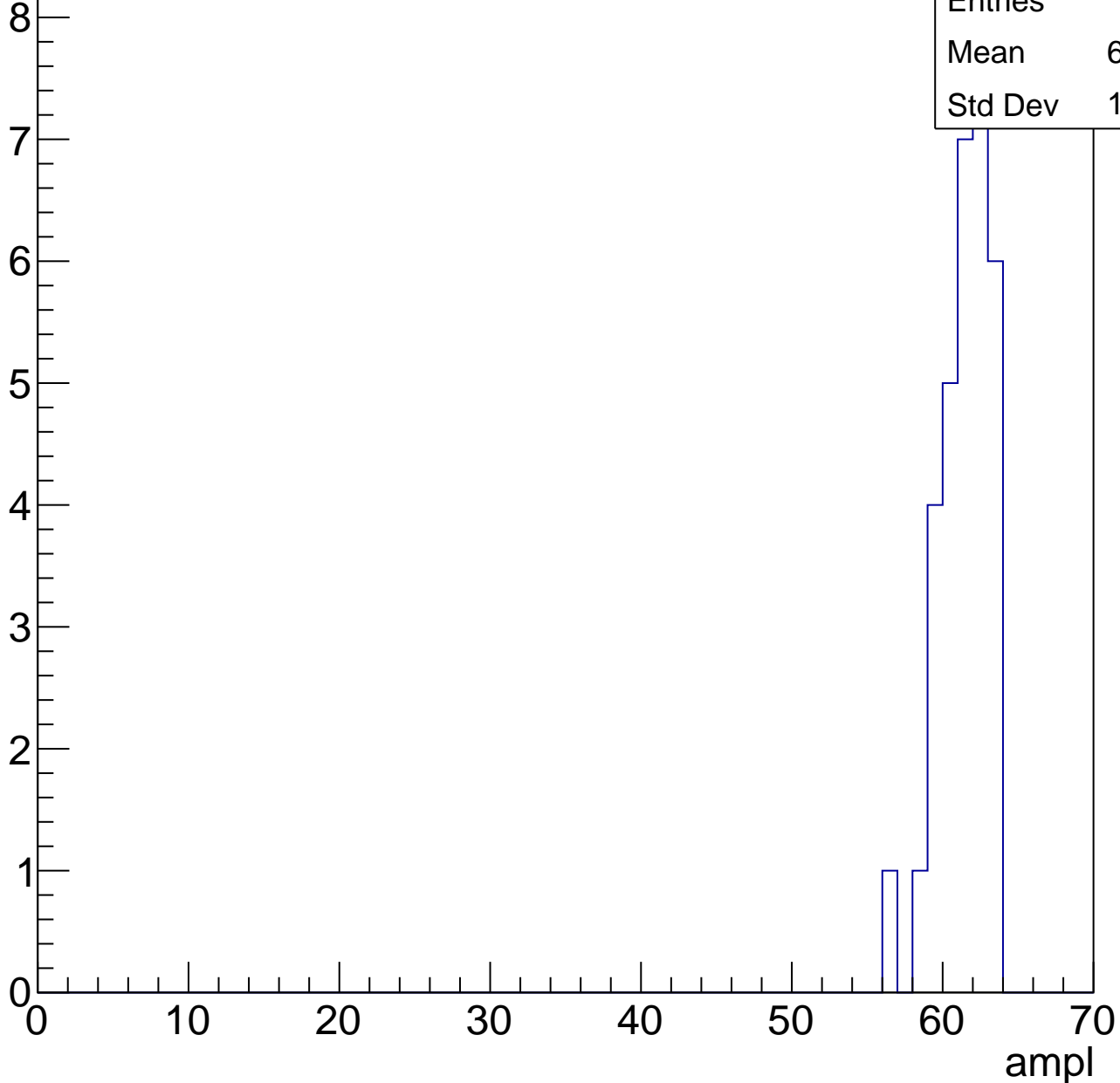


# B1L000S, U2-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	60.97
Std Dev	1.649





# B1L000S, U2-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch16, adc0

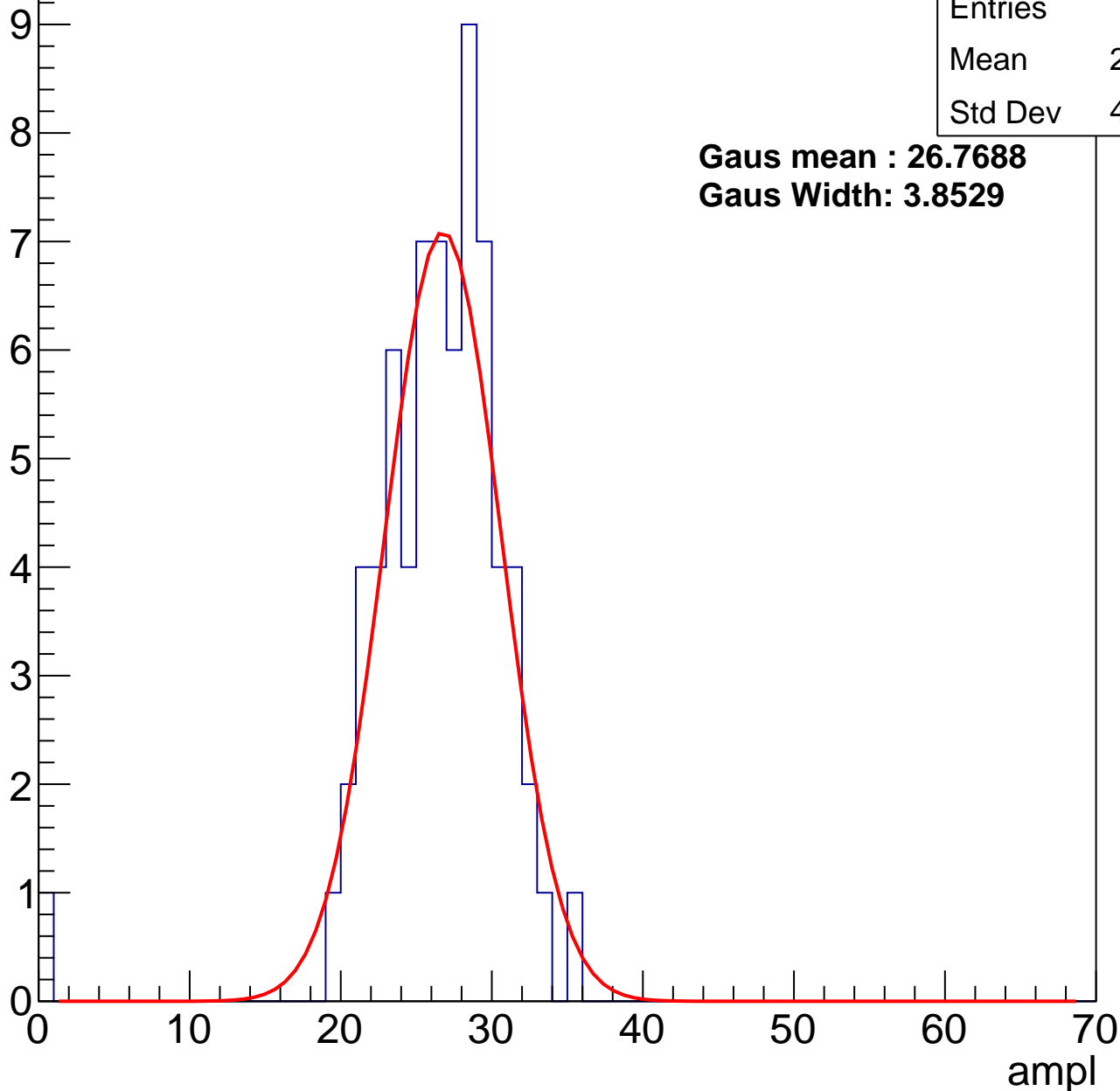
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	25.93
Std Dev	4.642

**Gaus mean : 26.7688**

**Gaus Width: 3.8529**



# B1L000S, U2-ch16, adc1

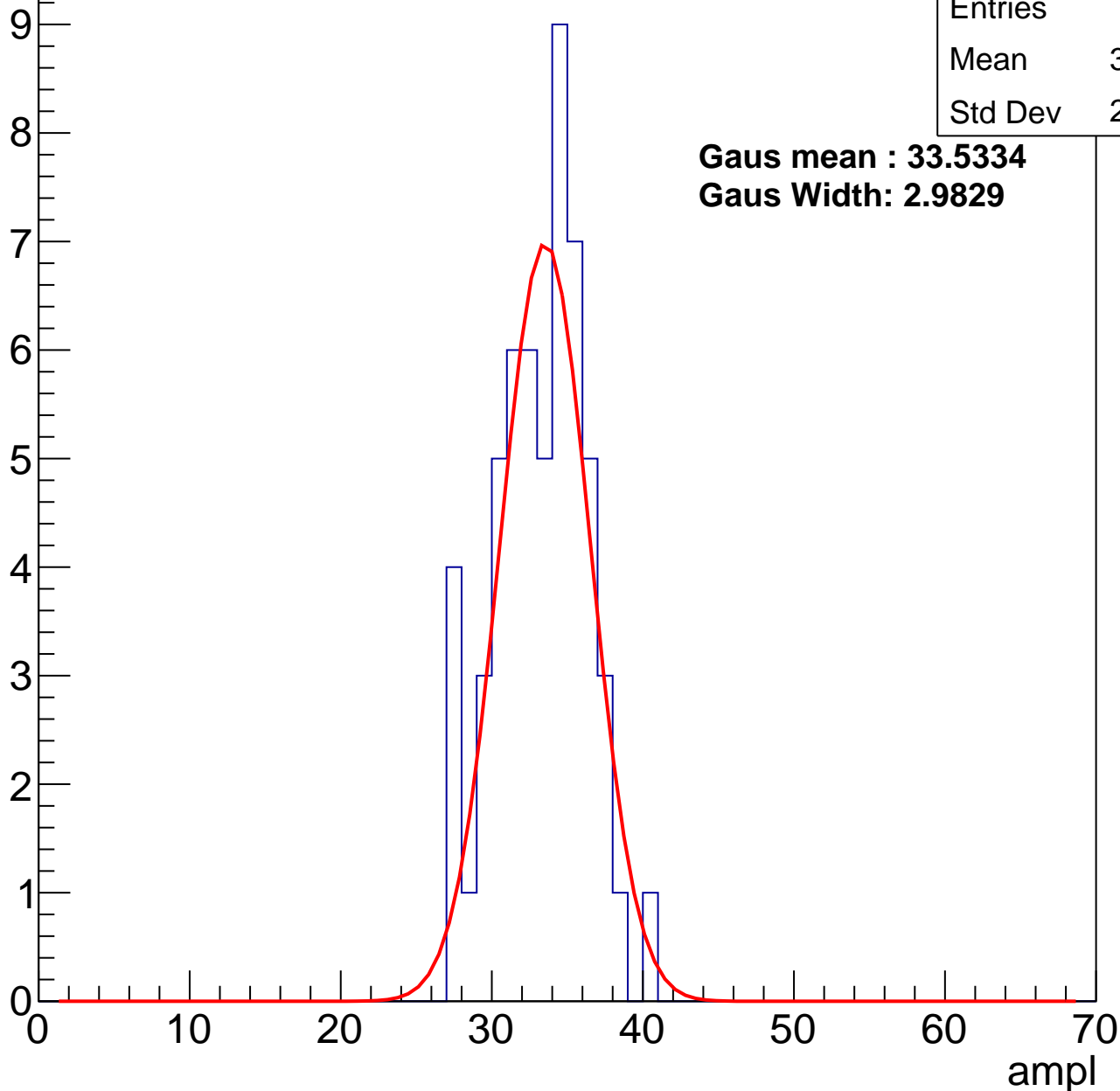
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	32.79
Std Dev	2.968

**Gaus mean : 33.5334**

**Gaus Width: 2.9829**



# B1L000S, U2-ch16, adc2

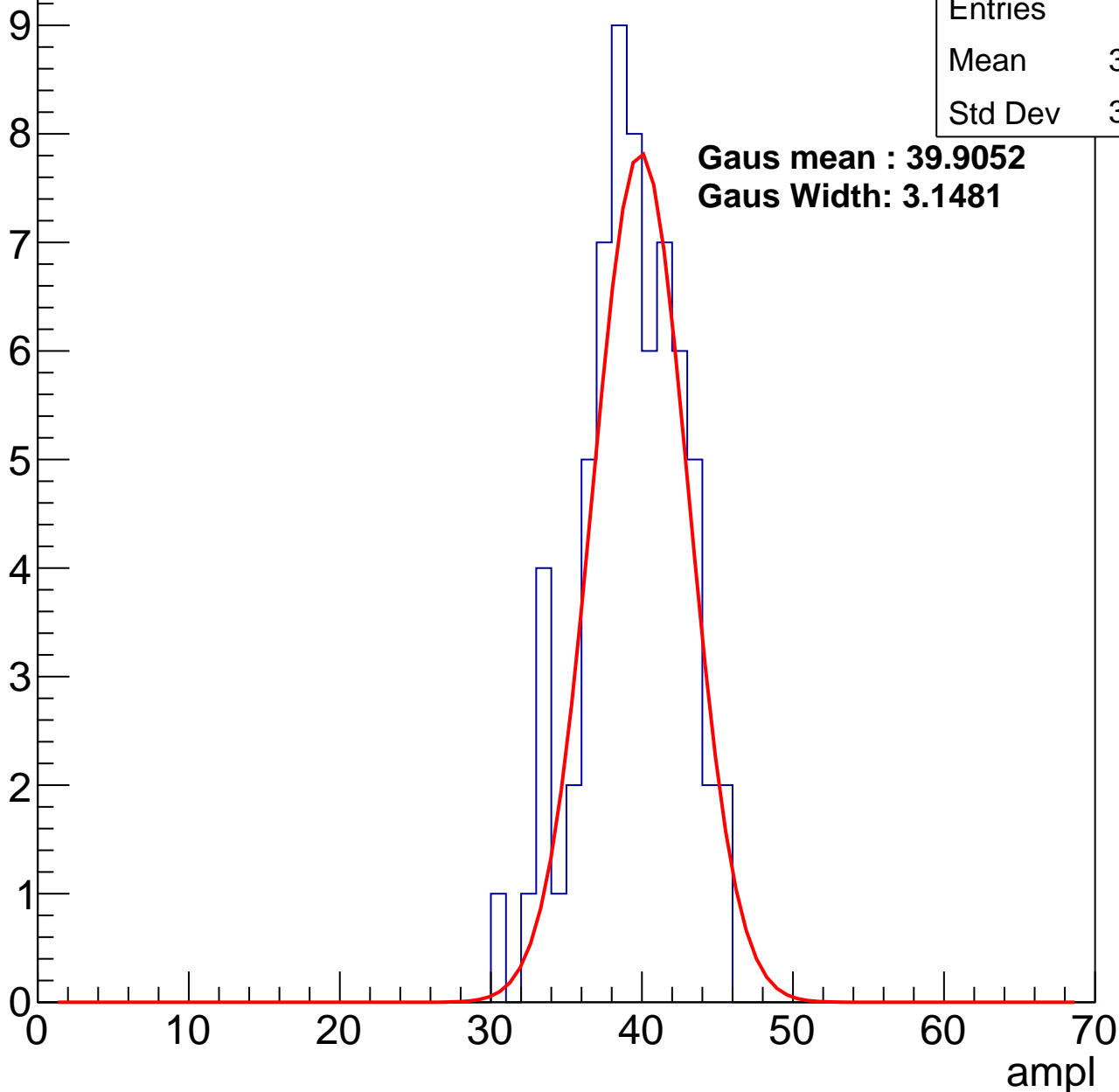
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	38.83
Std Dev	3.259

**Gaus mean : 39.9052**

**Gaus Width: 3.1481**

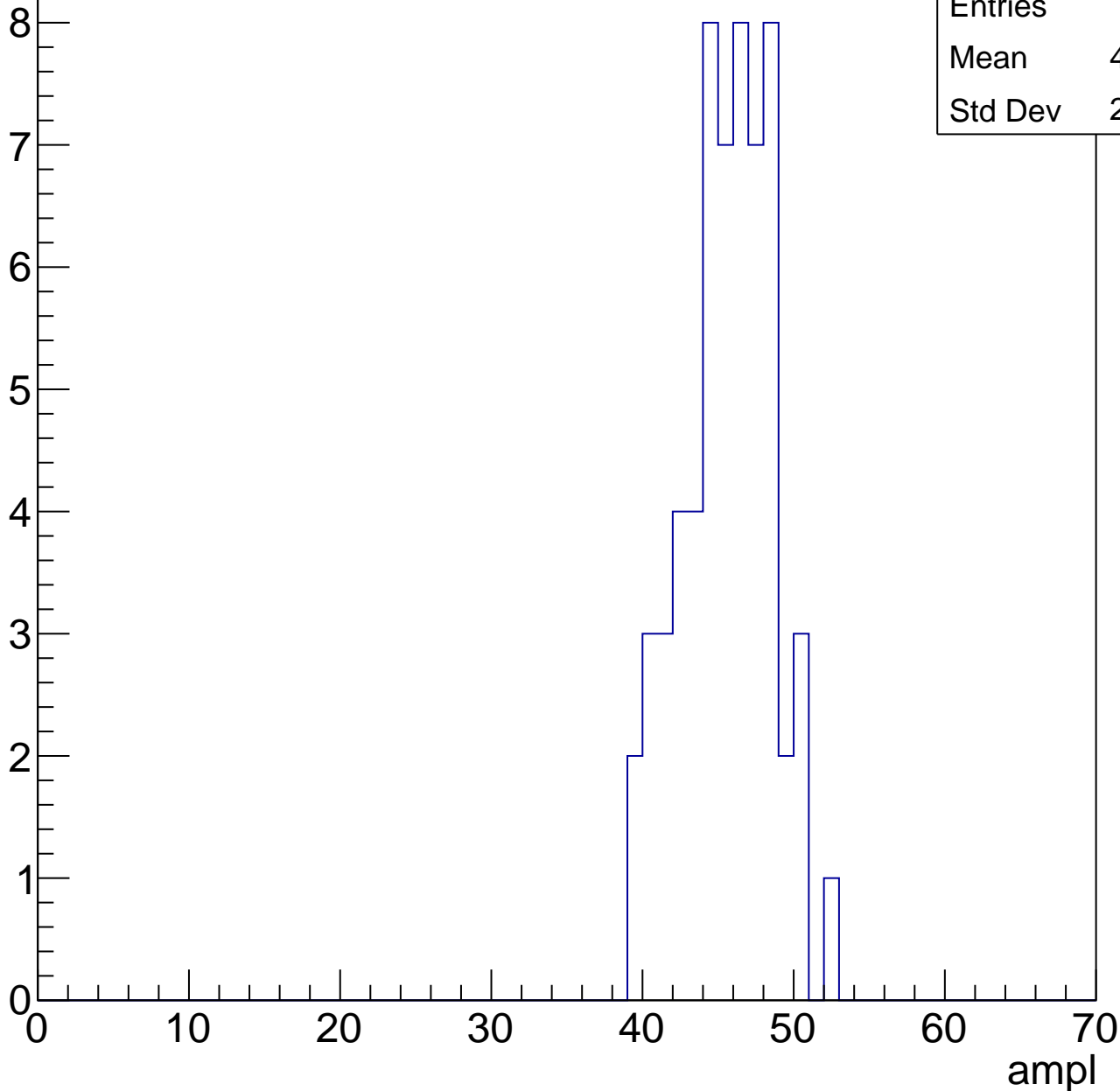


# B1L000S, U2-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	45.15
Std Dev	2.937

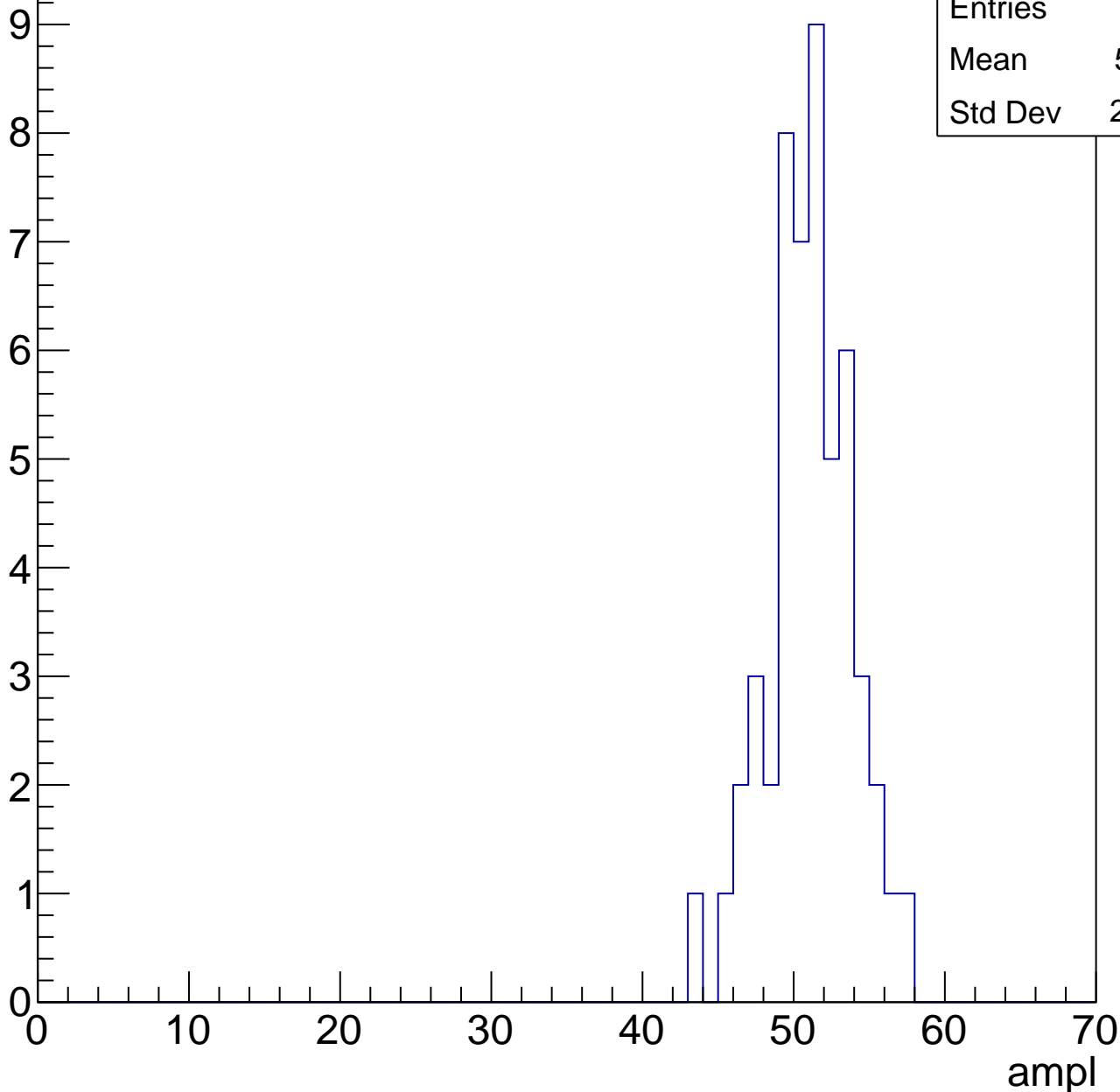


# B1L000S, U2-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

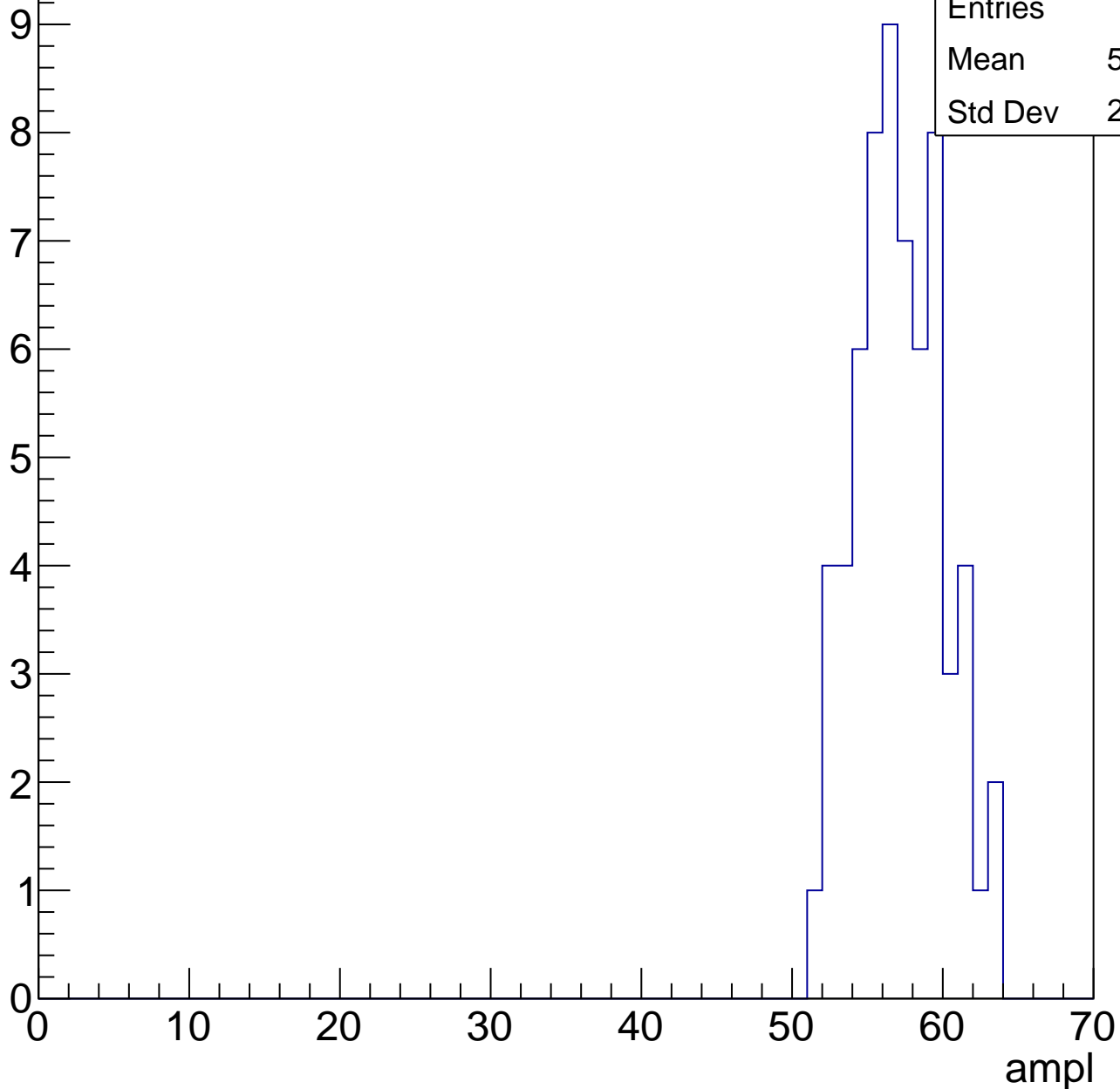
Entries	51
Mean	50.61
Std Dev	2.787



# B1L000S, U2-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

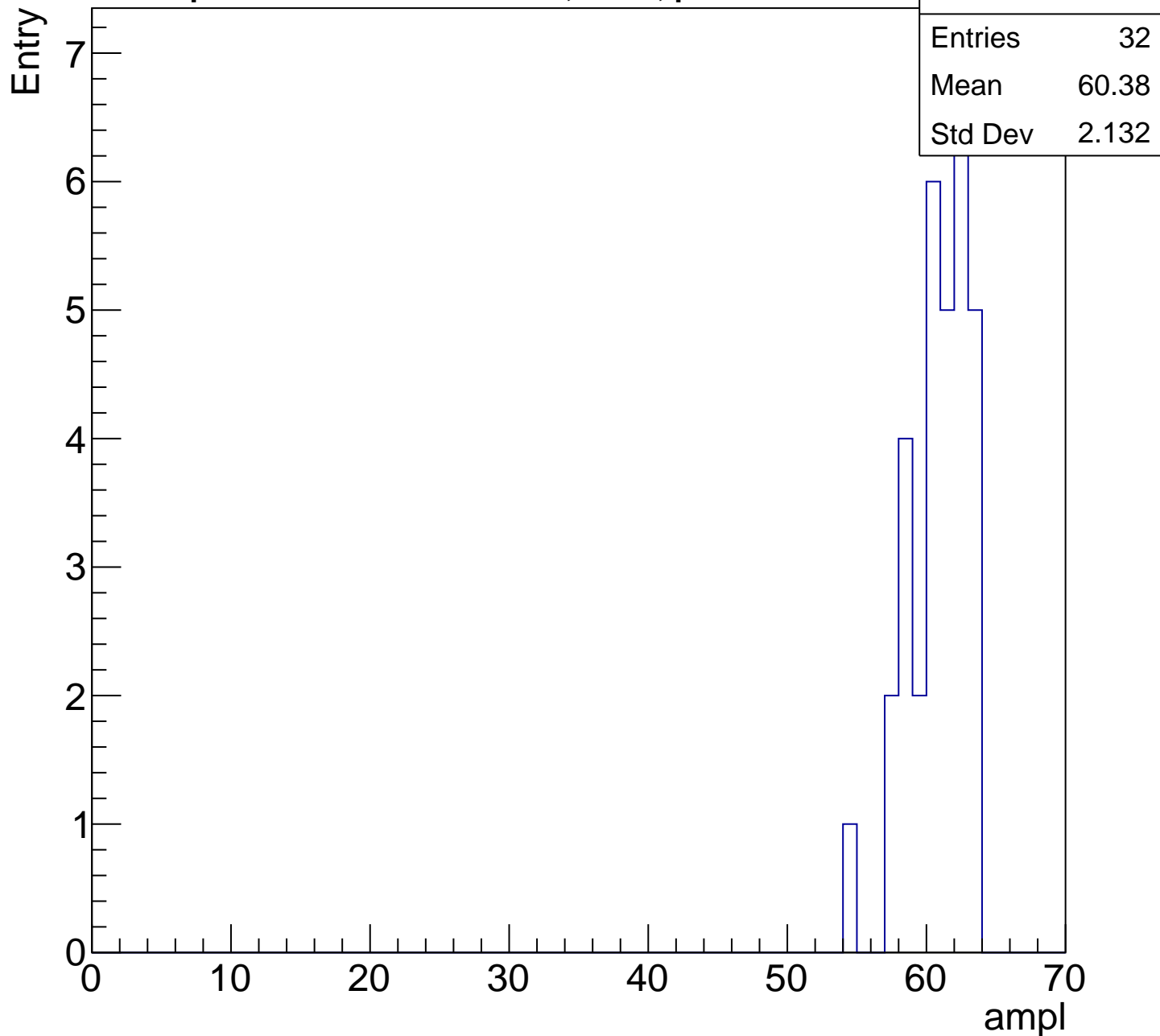
Entry



Entries	63
Mean	56.67
Std Dev	2.856

# B1L000S, U2-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

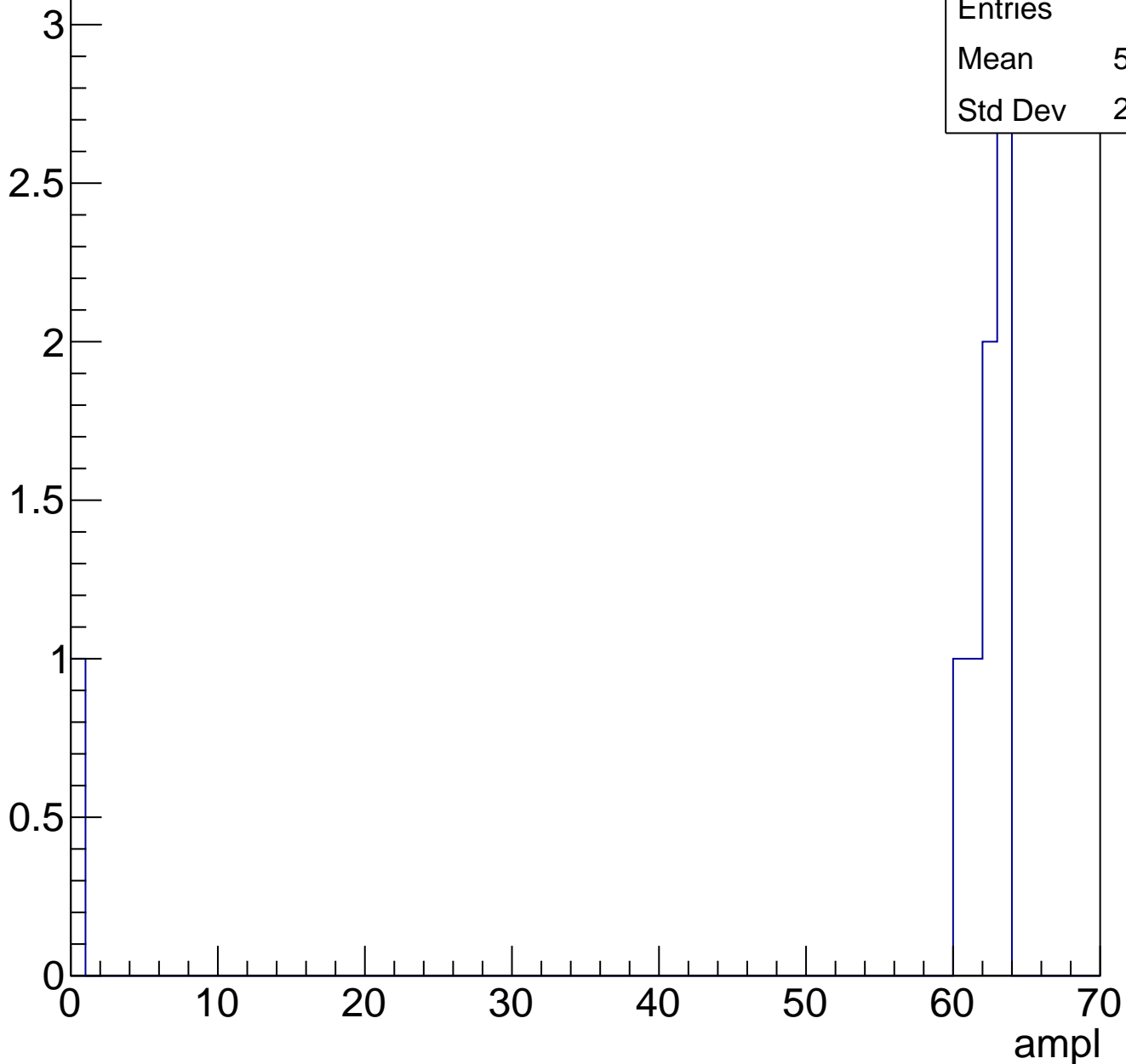




# B1L000S, U2-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch17, adc0

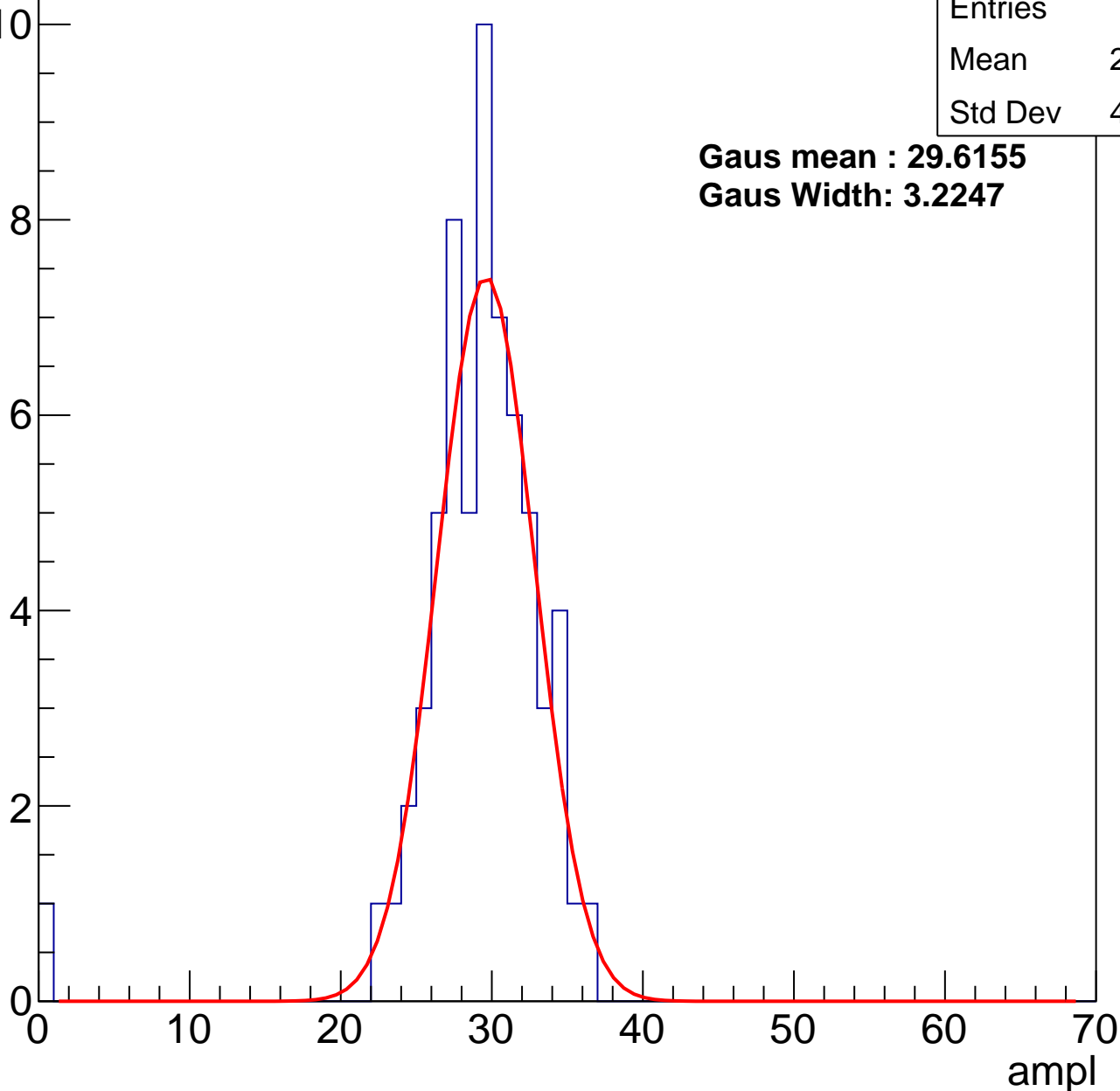
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	28.67
Std Dev	4.727

**Gaus mean : 29.6155**

**Gaus Width: 3.2247**



# B1L000S, U2-ch17, adc1

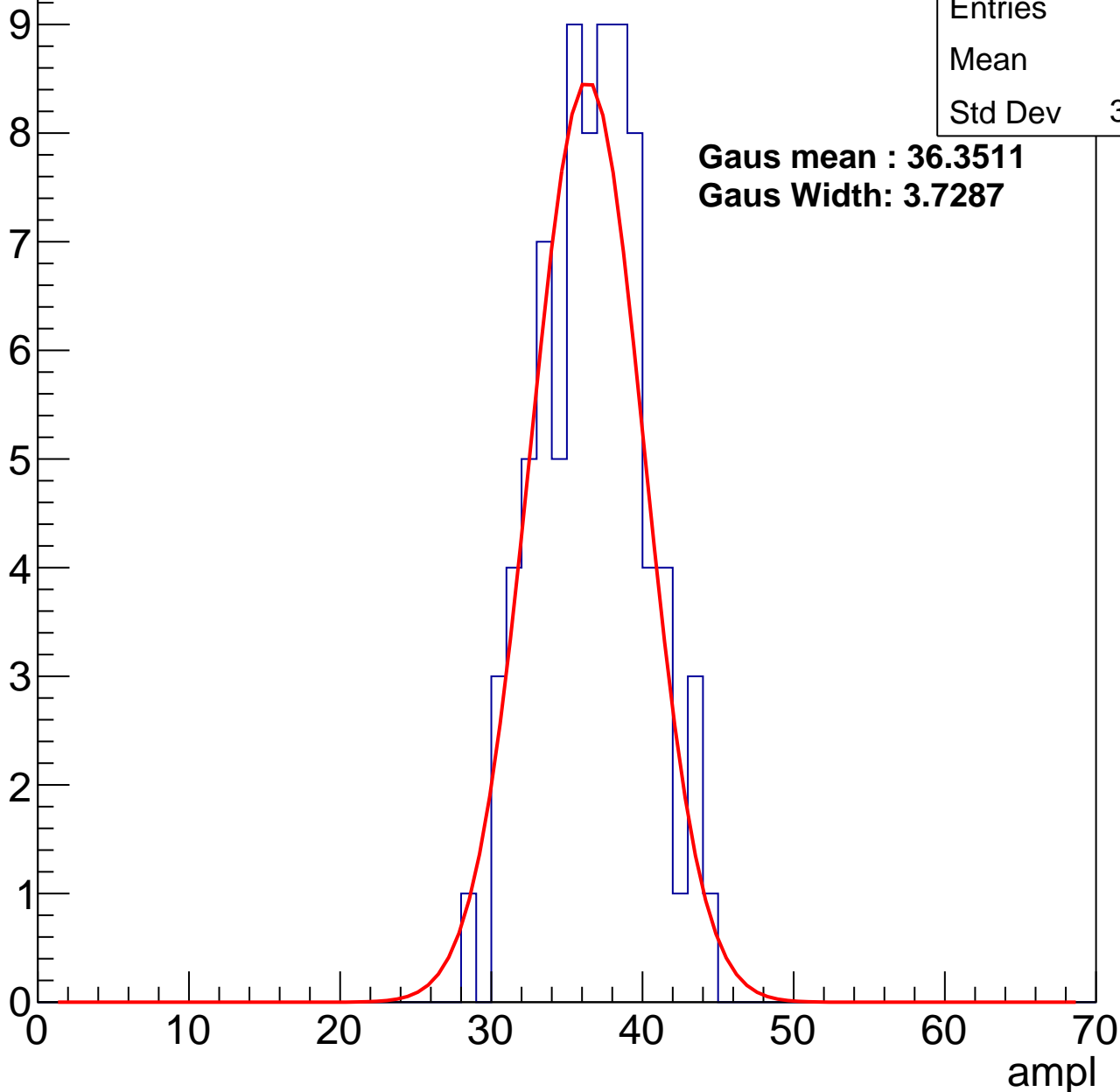
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	36.2
Std Dev	3.458

**Gaus mean : 36.3511**

**Gaus Width: 3.7287**



# B1L000S, U2-ch17, adc2

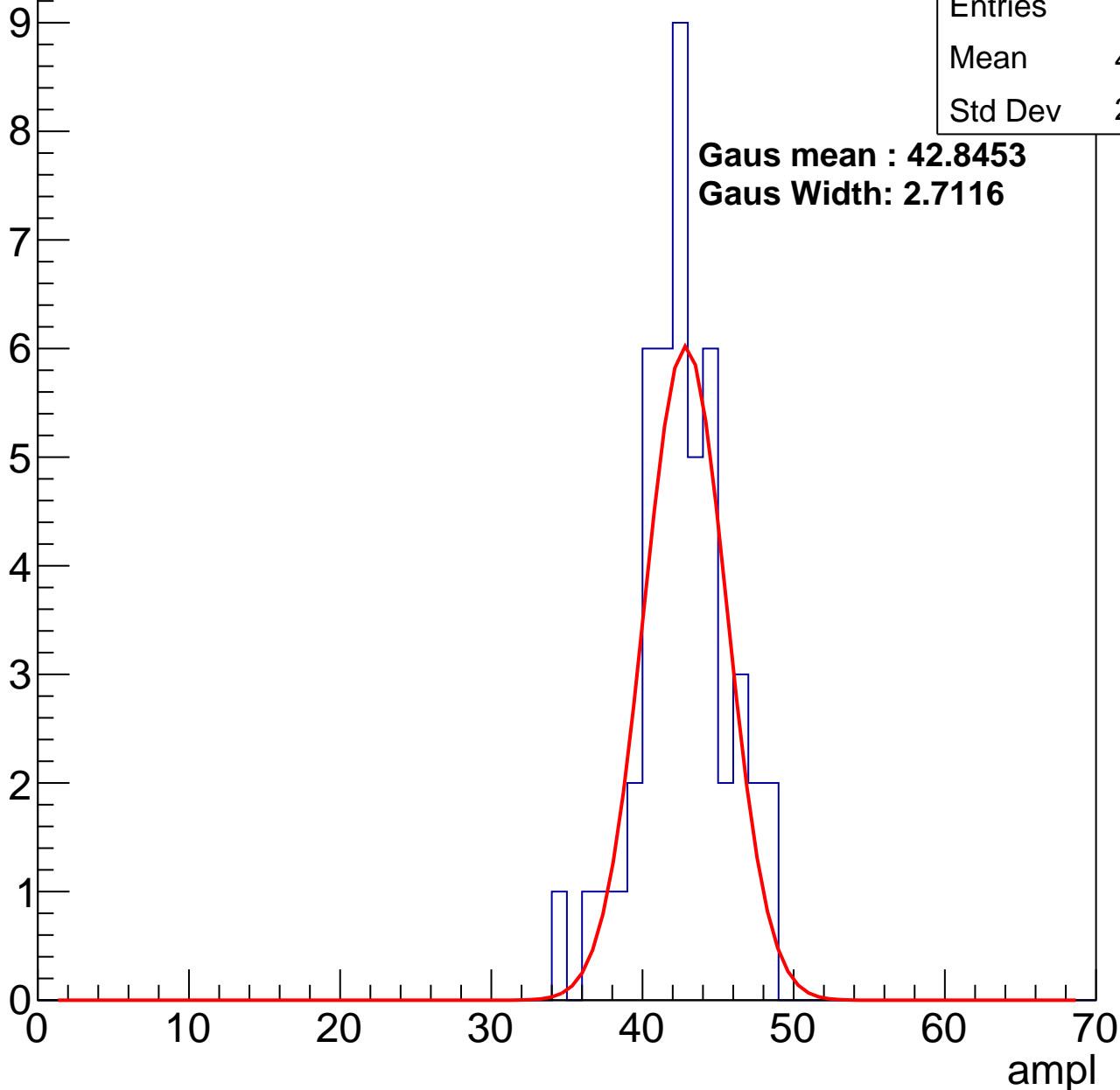
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	42.21
Std Dev	2.931

**Gaus mean : 42.8453**

**Gaus Width: 2.7116**

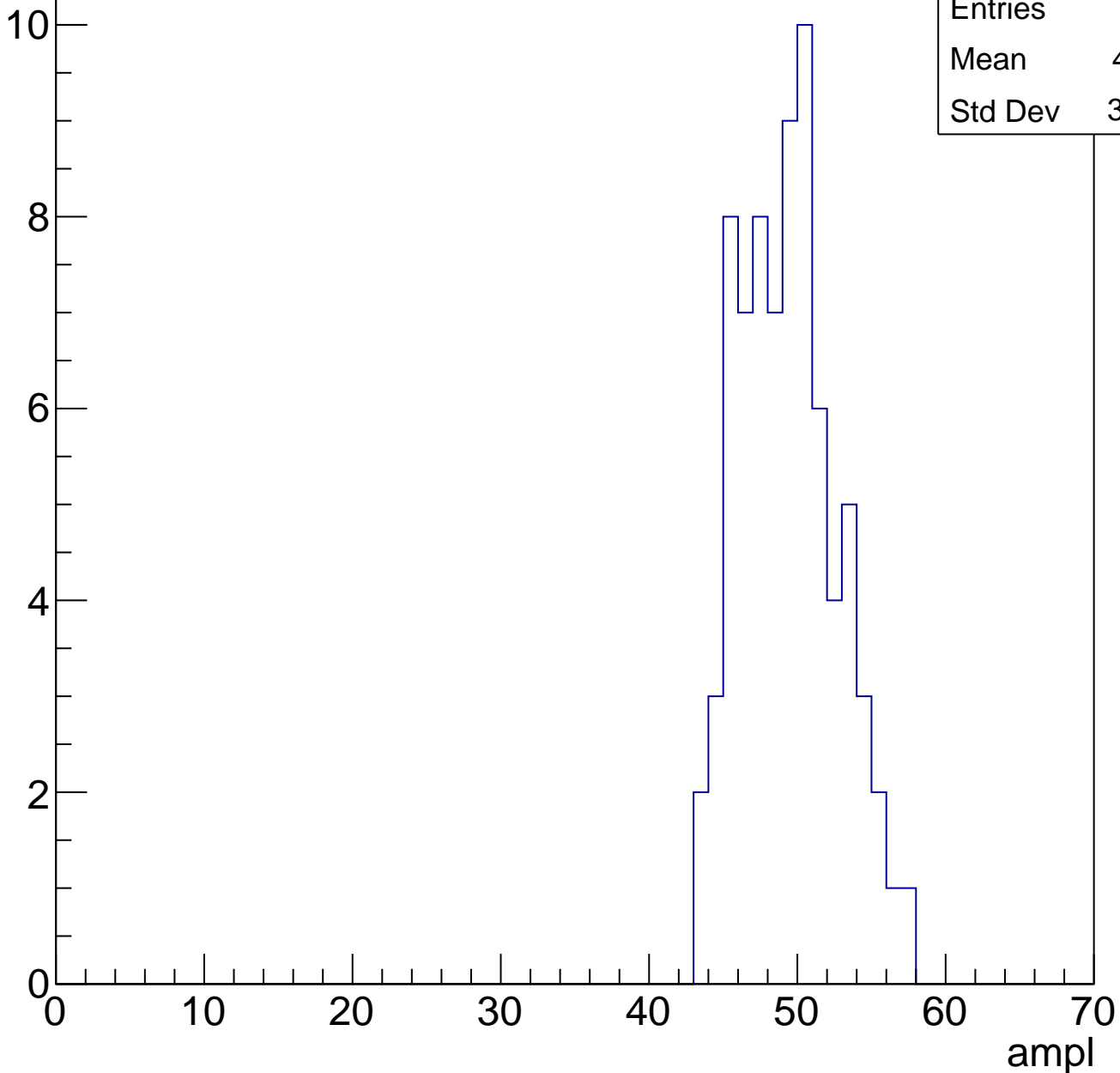


# B1L000S, U2-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	48.91
Std Dev	3.217

Entry

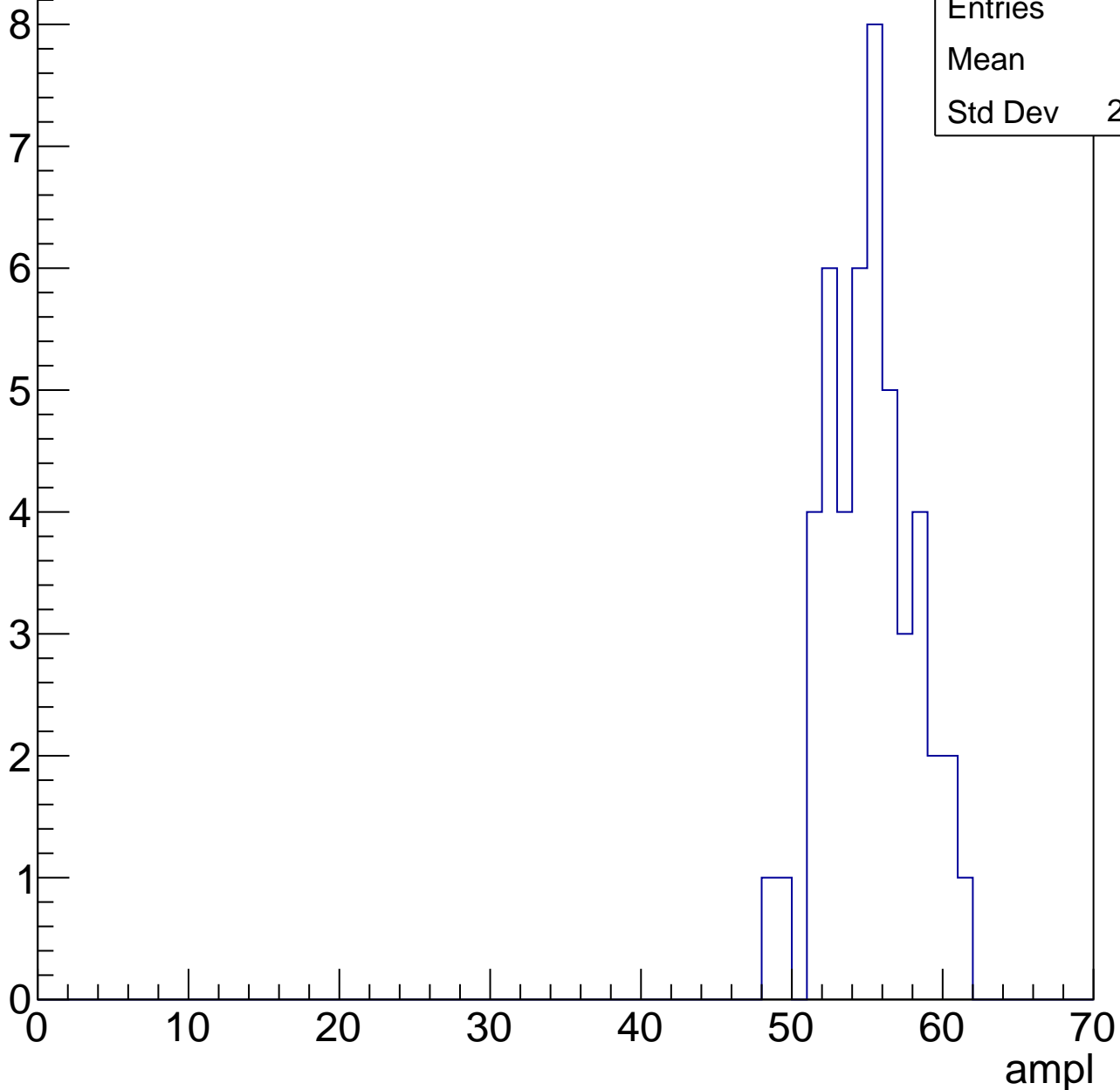


# B1L000S, U2-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	54.7
Std Dev	2.887

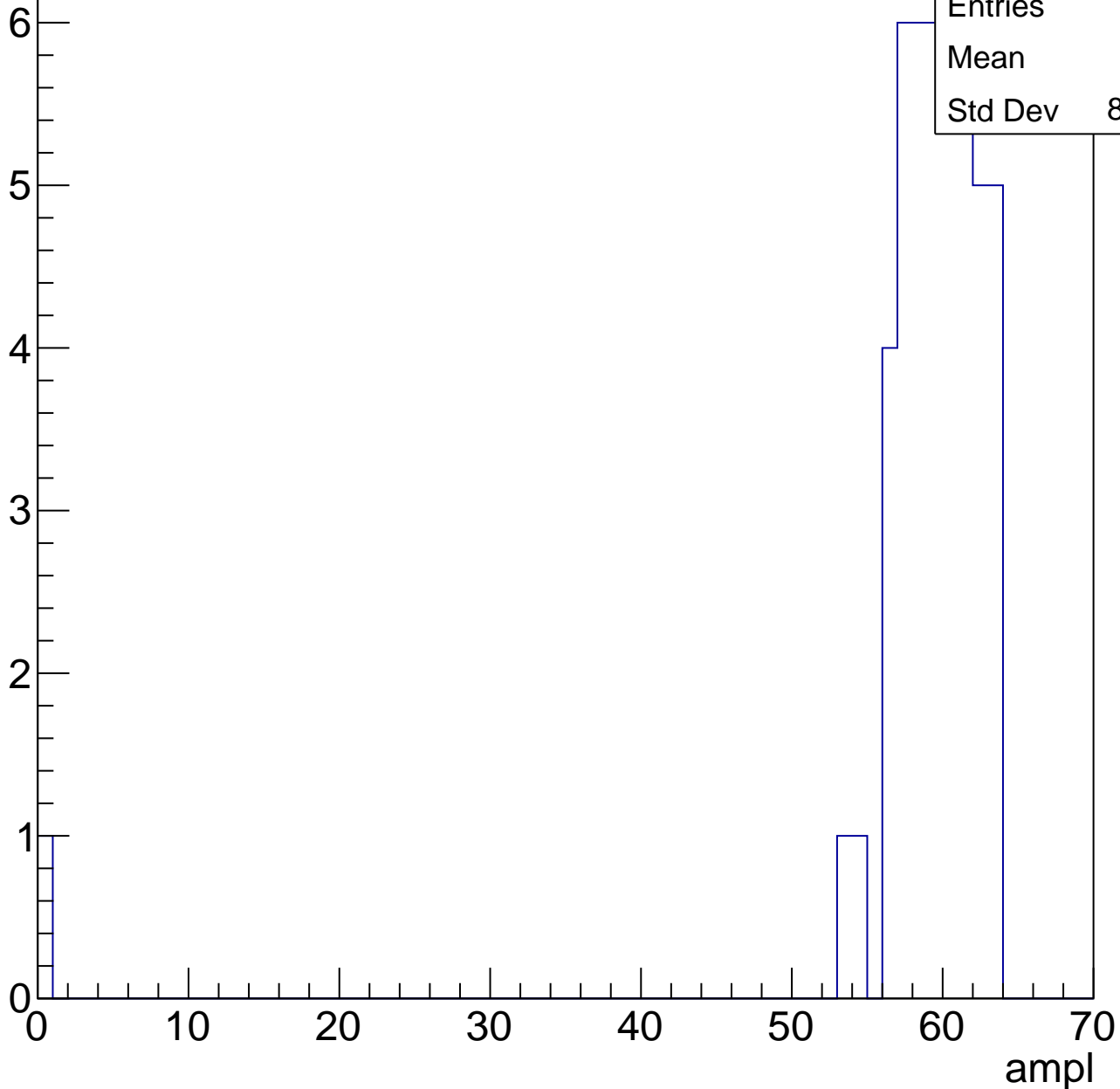


# B1L000S, U2-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	58
Std Dev	8.892

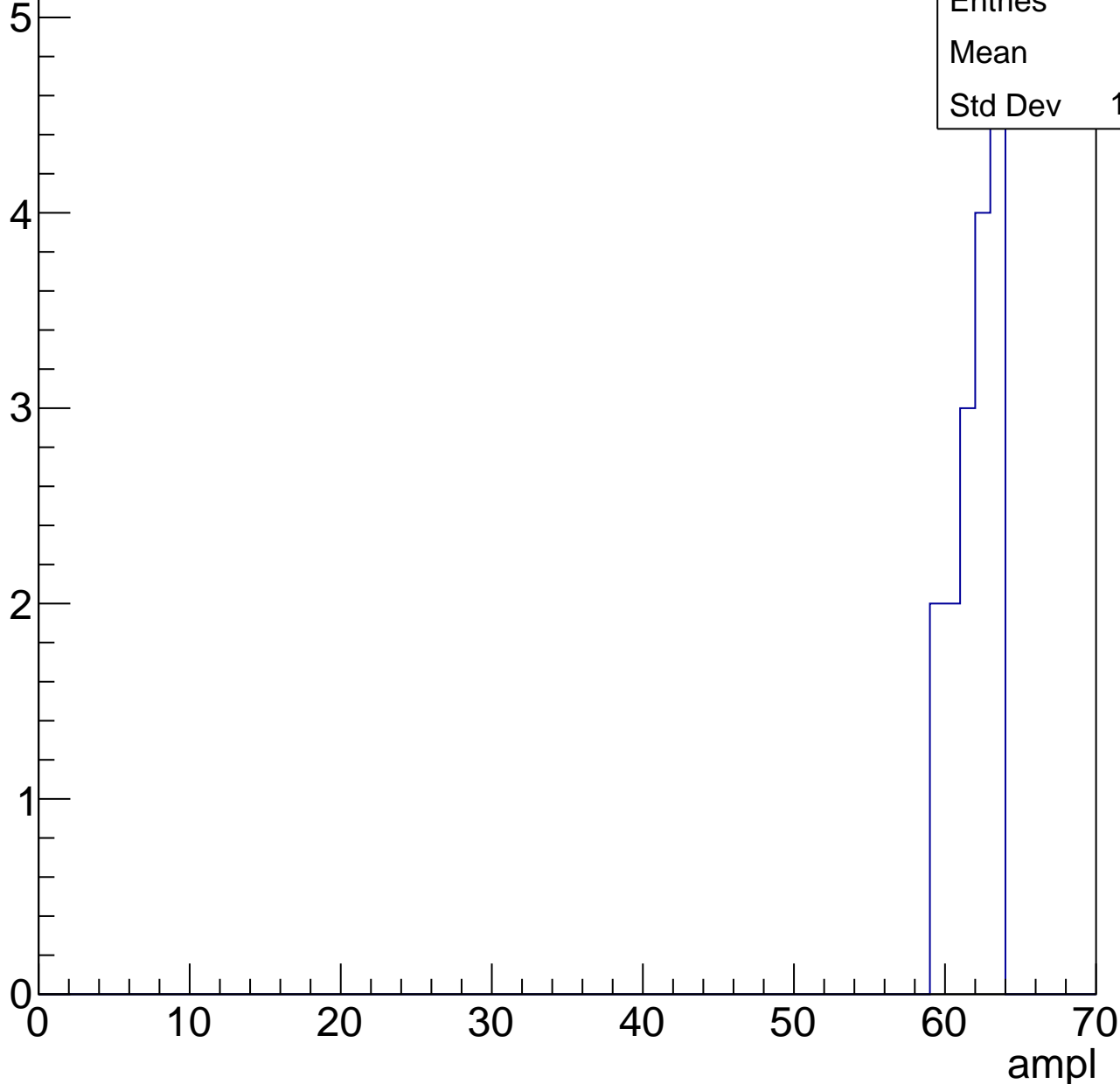


# B1L000S, U2-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.5
Std Dev	1.369





# B1L000S, U2-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch18, adc0

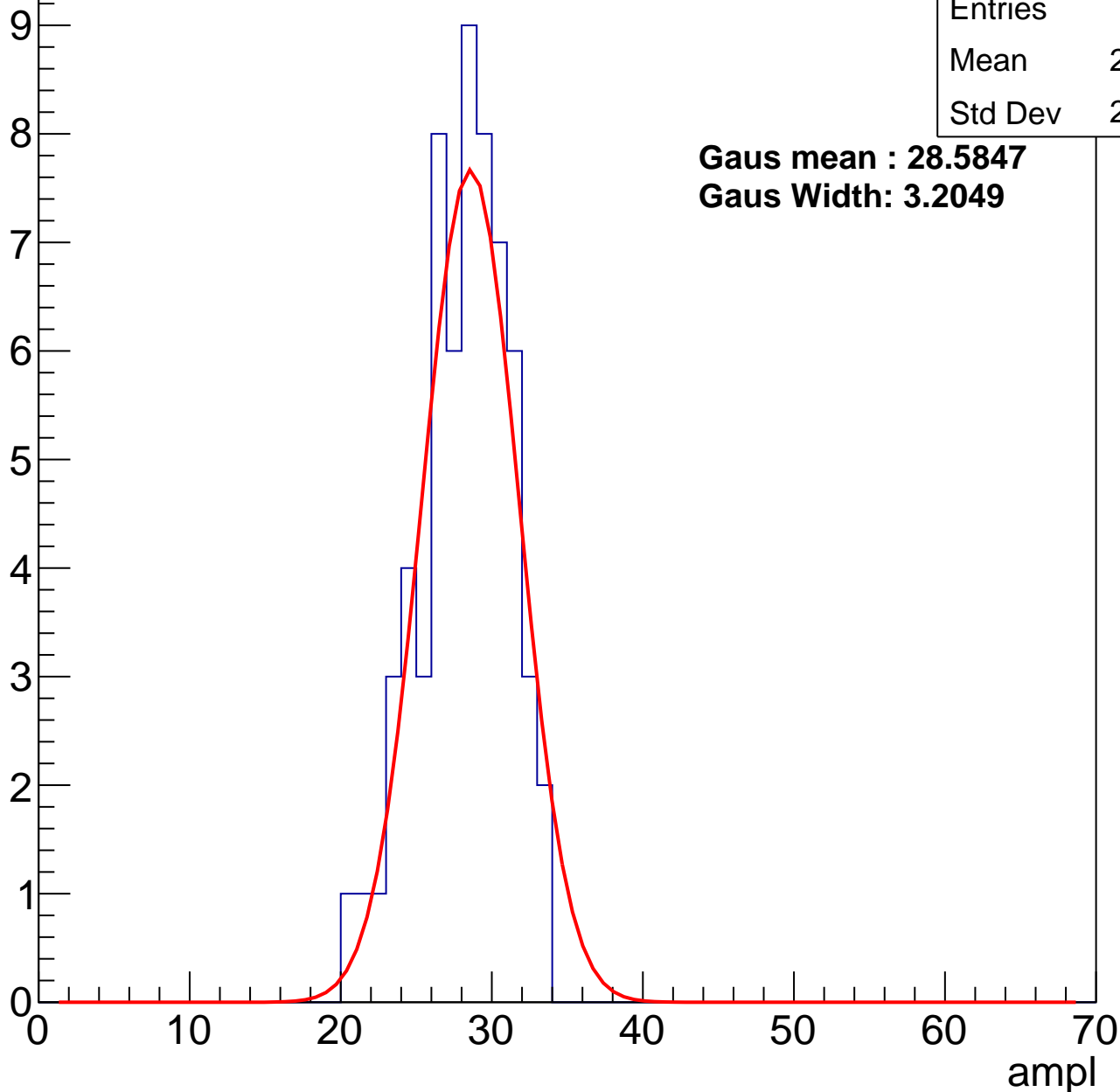
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	27.66
Std Dev	2.934

**Gaus mean : 28.5847**

**Gaus Width: 3.2049**



# B1L000S, U2-ch18, adc1

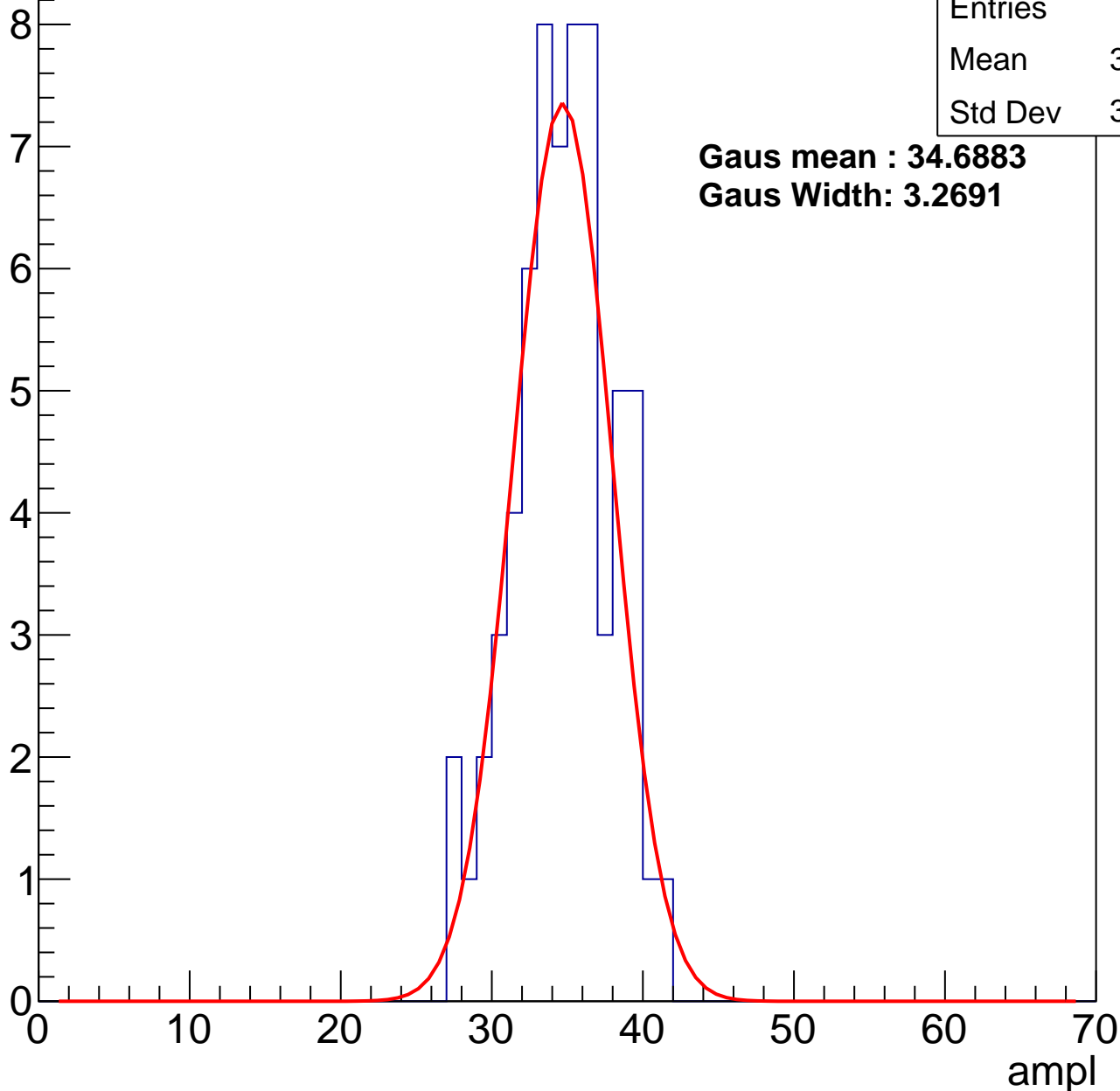
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.27
Std Dev	3.193

**Gaus mean : 34.6883**

**Gaus Width: 3.2691**



# B1L000S, U2-ch18, adc2

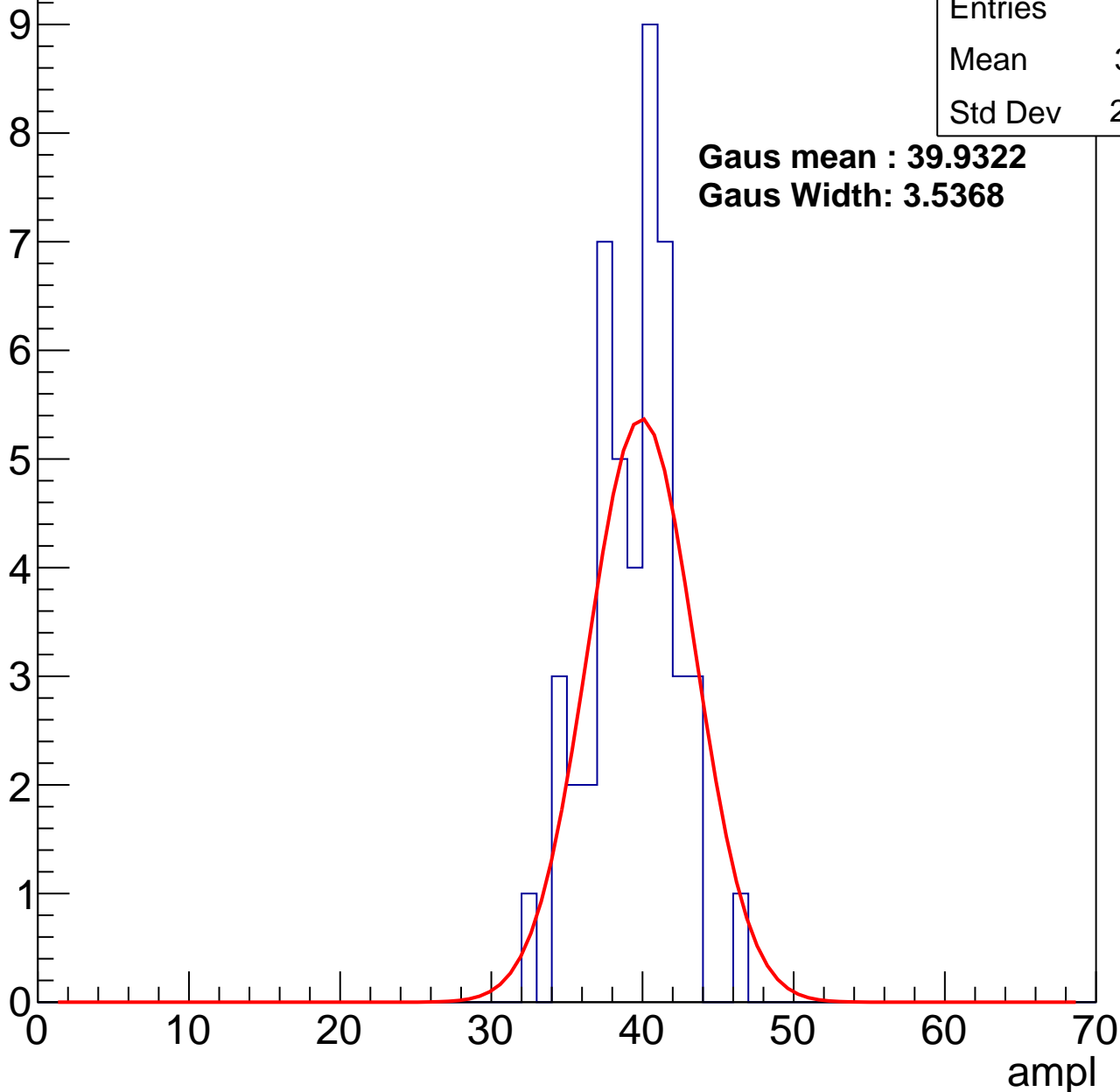
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	38.91
Std Dev	2.797

**Gaus mean : 39.9322**

**Gaus Width: 3.5368**

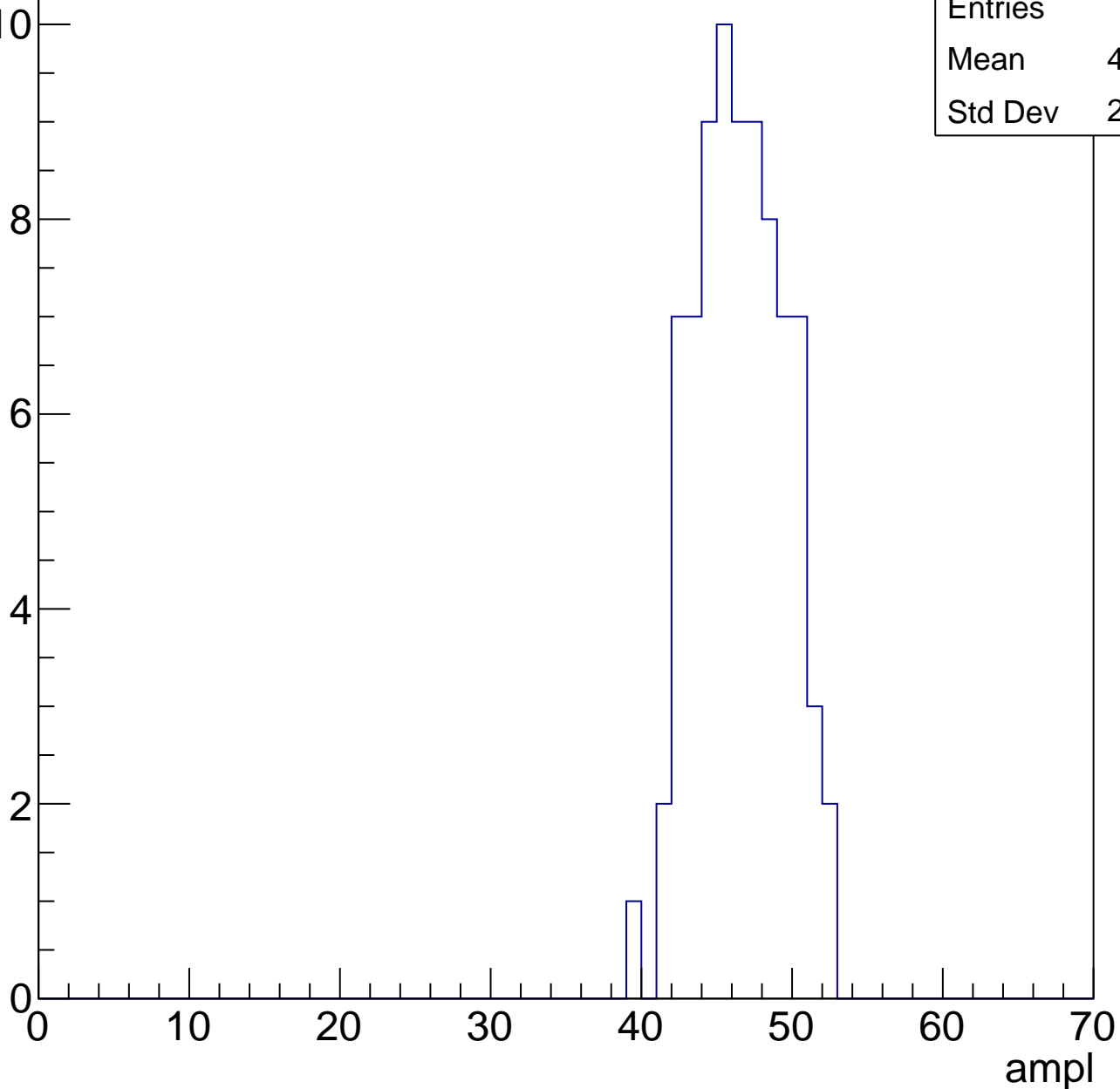


# B1L000S, U2-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	46.09
Std Dev	2.903

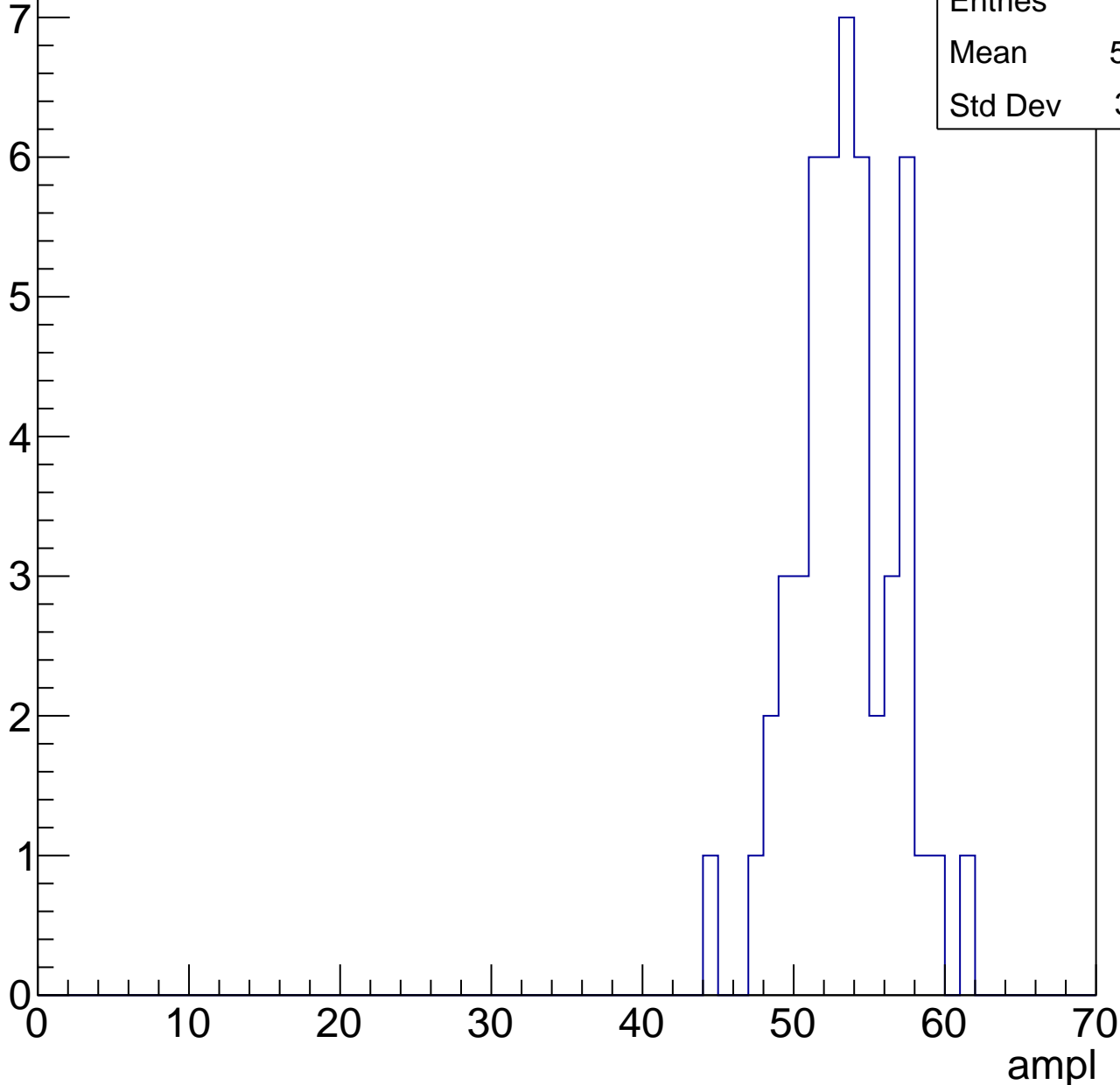


# B1L000S, U2-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

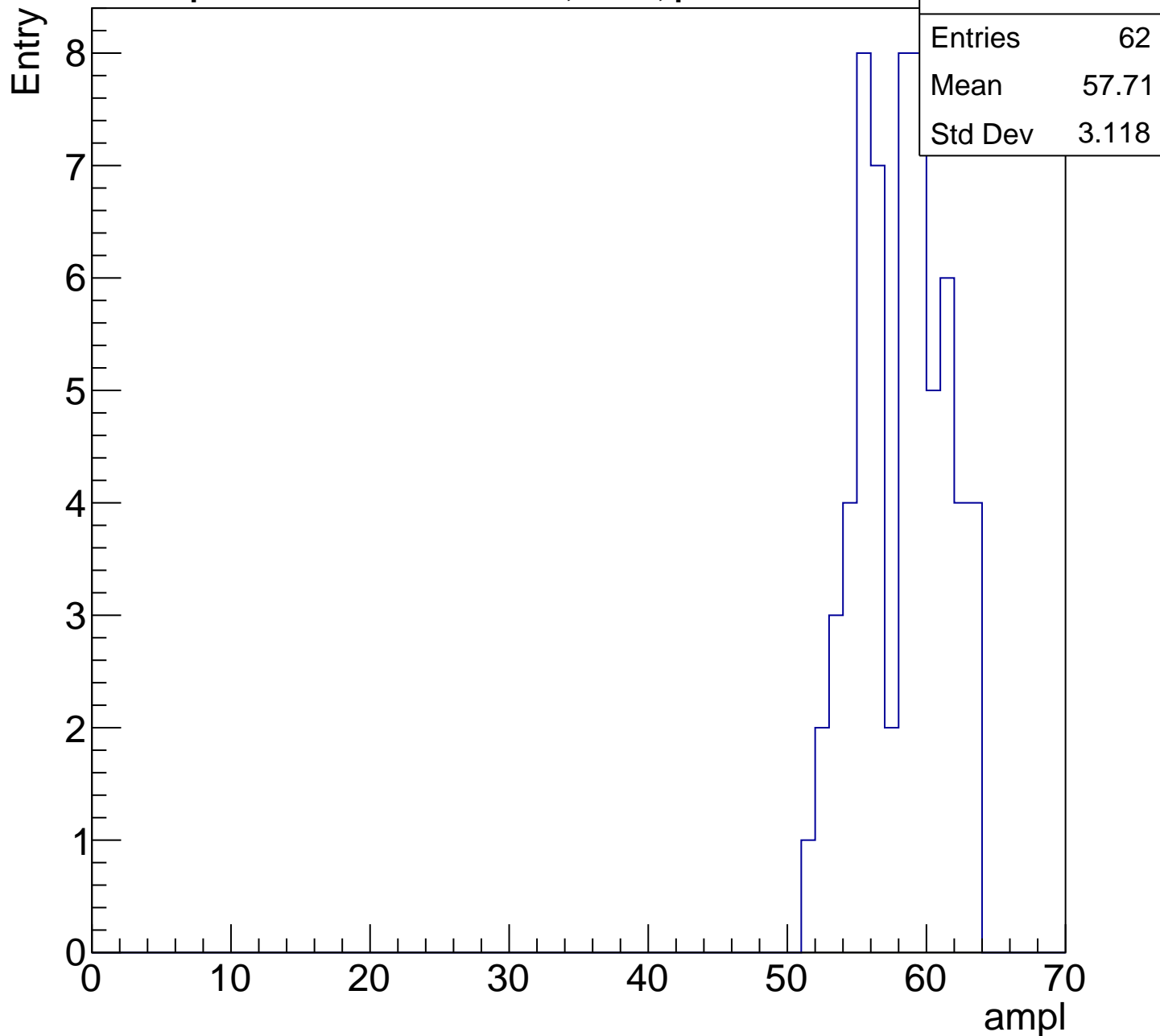
Entry

Entries	49
Mean	52.96
Std Dev	3.301



# B1L000S, U2-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

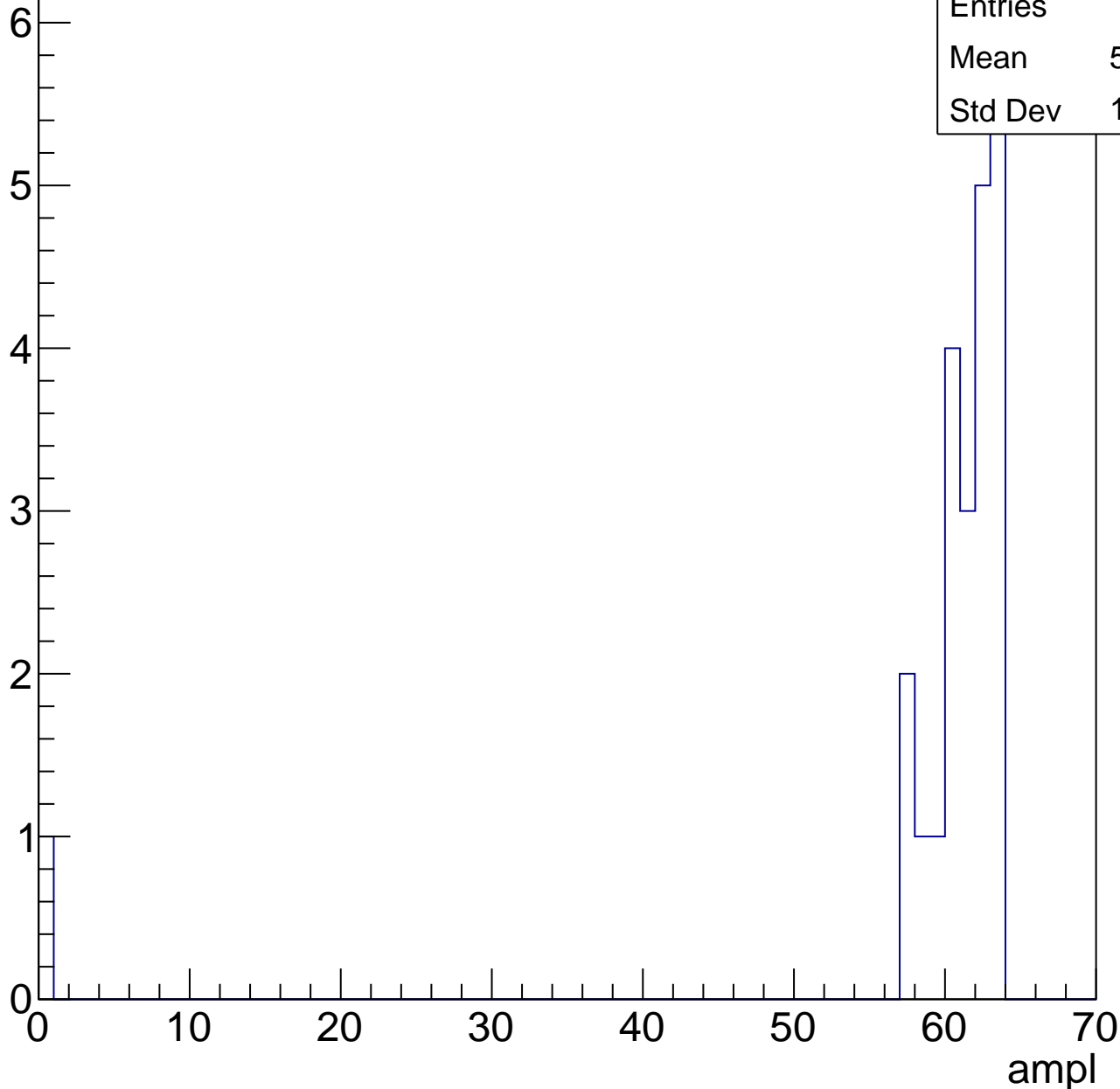


# B1L000S, U2-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	58.35
Std Dev	12.58

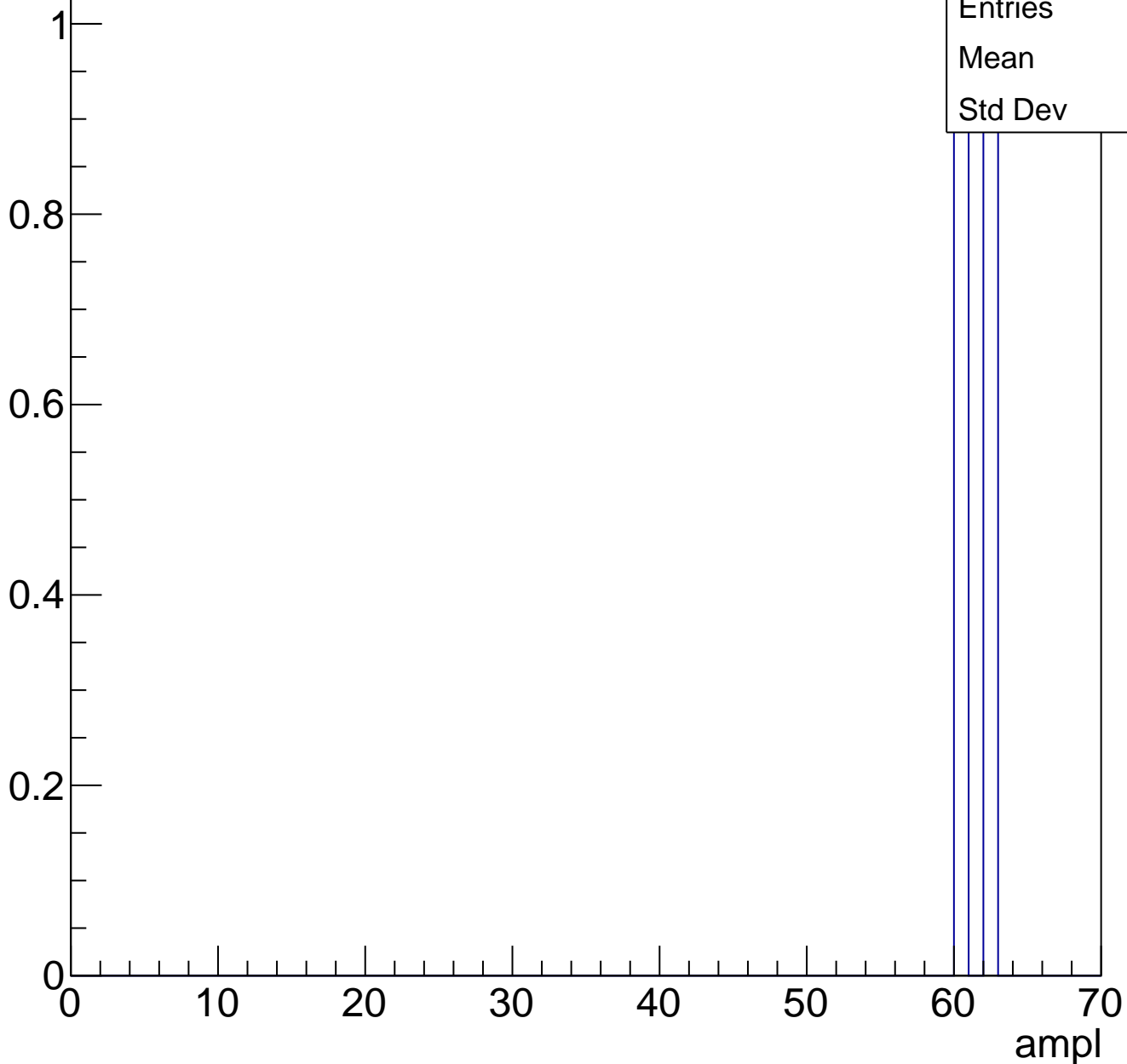




# B1L000S, U2-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch19, adc0

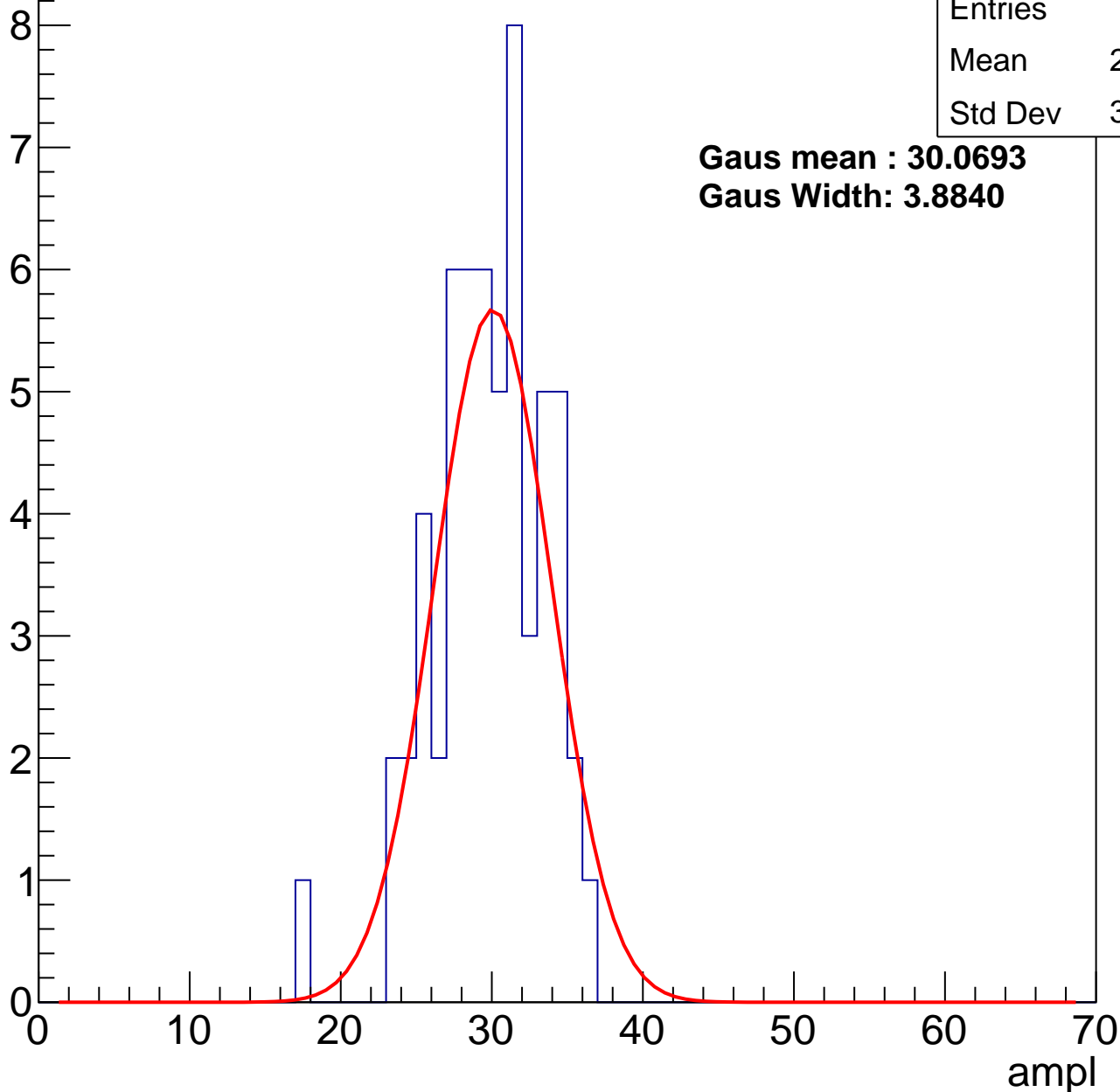
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	29.34
Std Dev	3.622

**Gaus mean : 30.0693**

**Gaus Width: 3.8840**



# B1L000S, U2-ch19, adc1

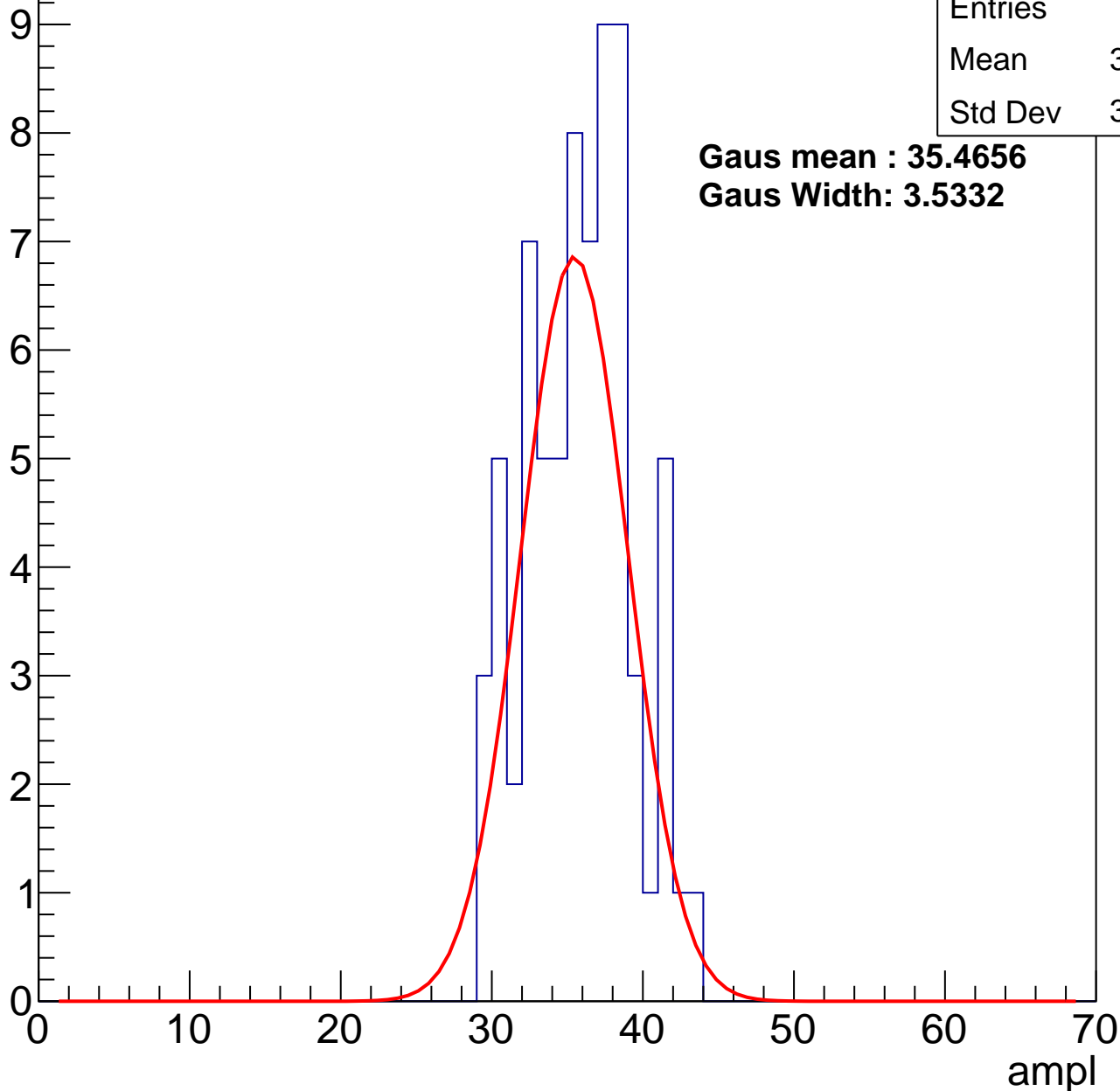
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.38
Std Dev	3.425

**Gaus mean : 35.4656**

**Gaus Width: 3.5332**



# B1L000S, U2-ch19, adc2

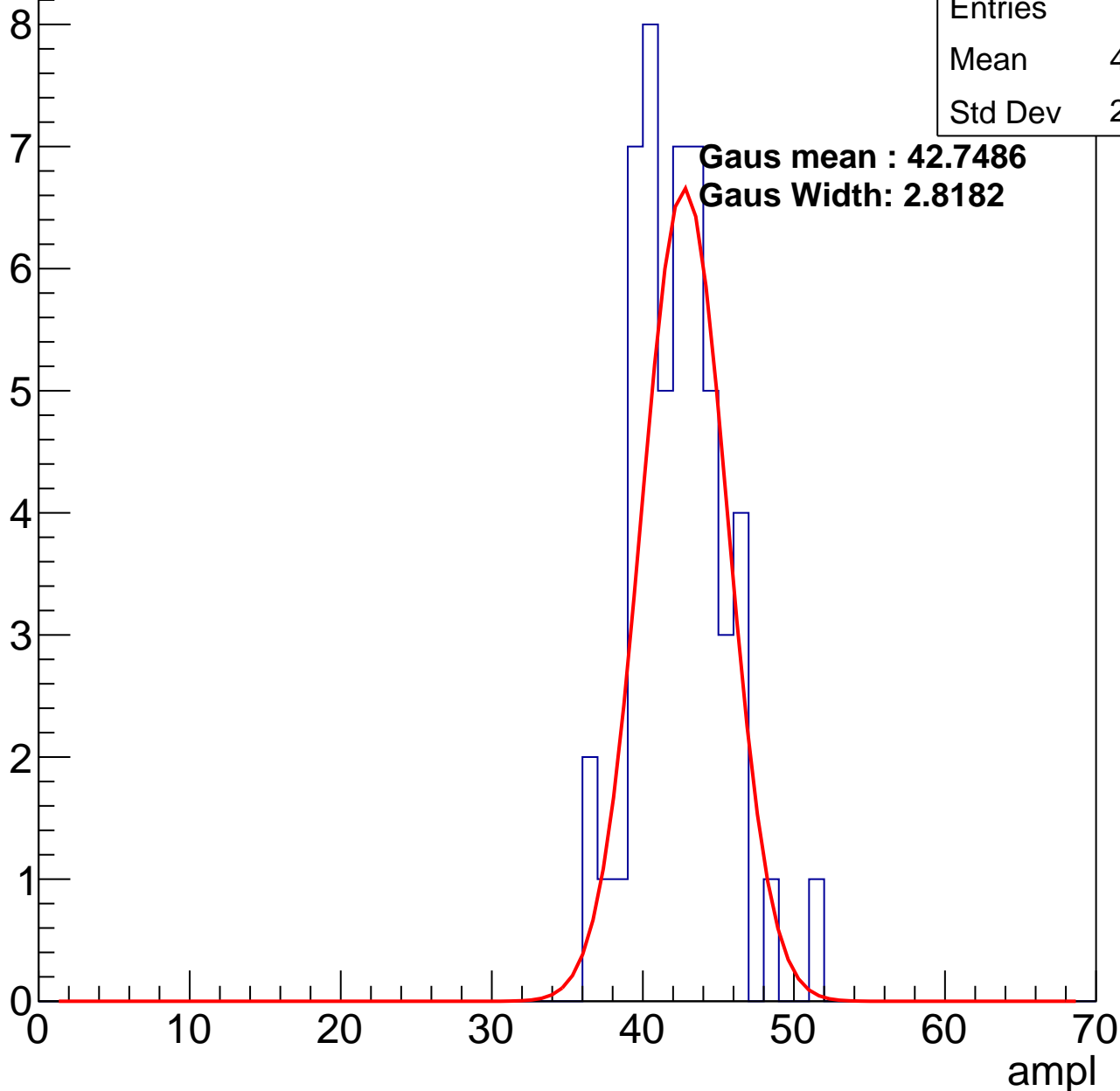
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	41.88
Std Dev	2.933

**Gaus mean : 42.7486**

**Gaus Width: 2.8182**



# B1L000S, U2-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	47.99
Std Dev	3.16

Entry

10

8

6

4

2

0

0

10

20

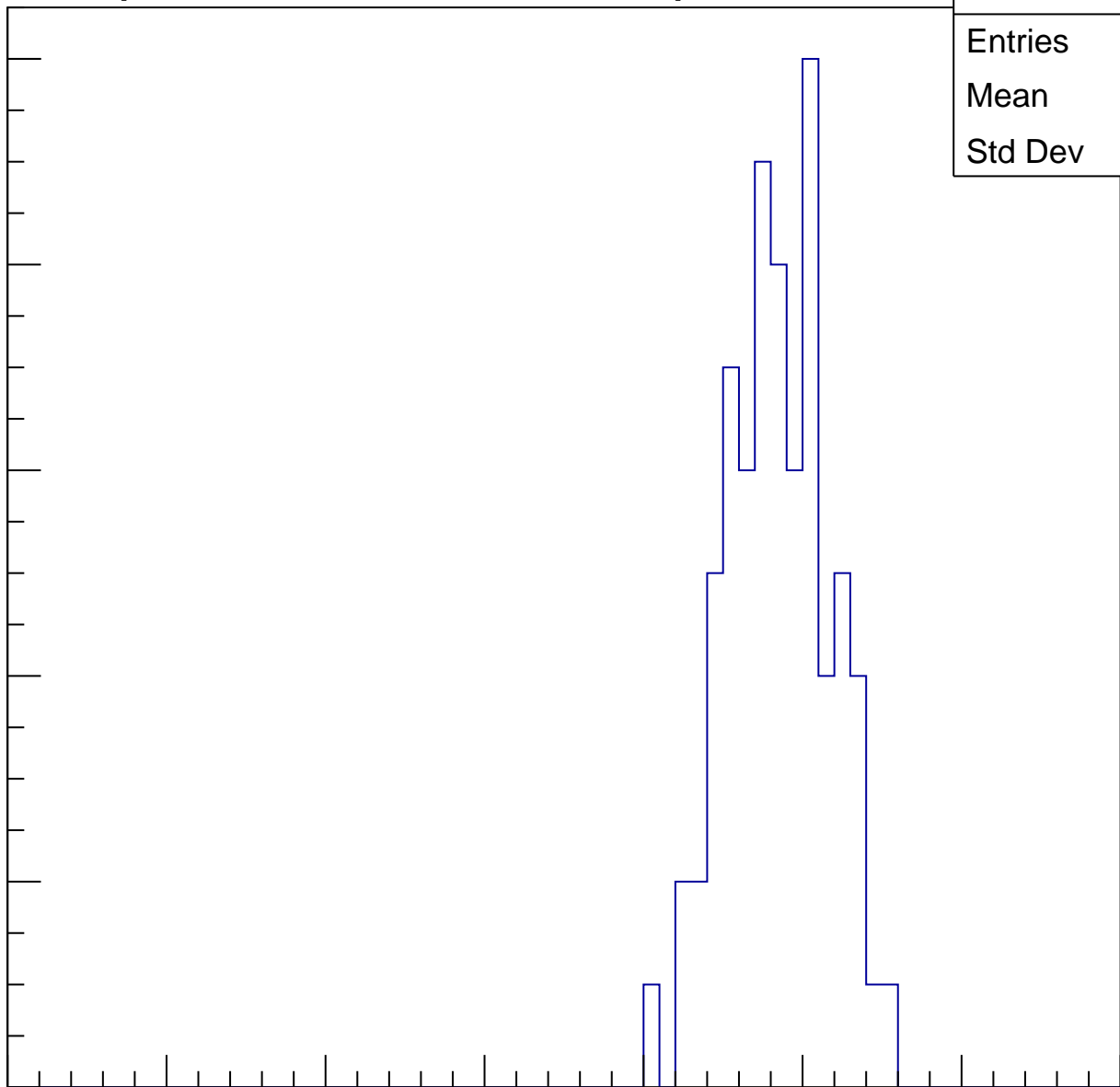
30

40

50

60

ampl

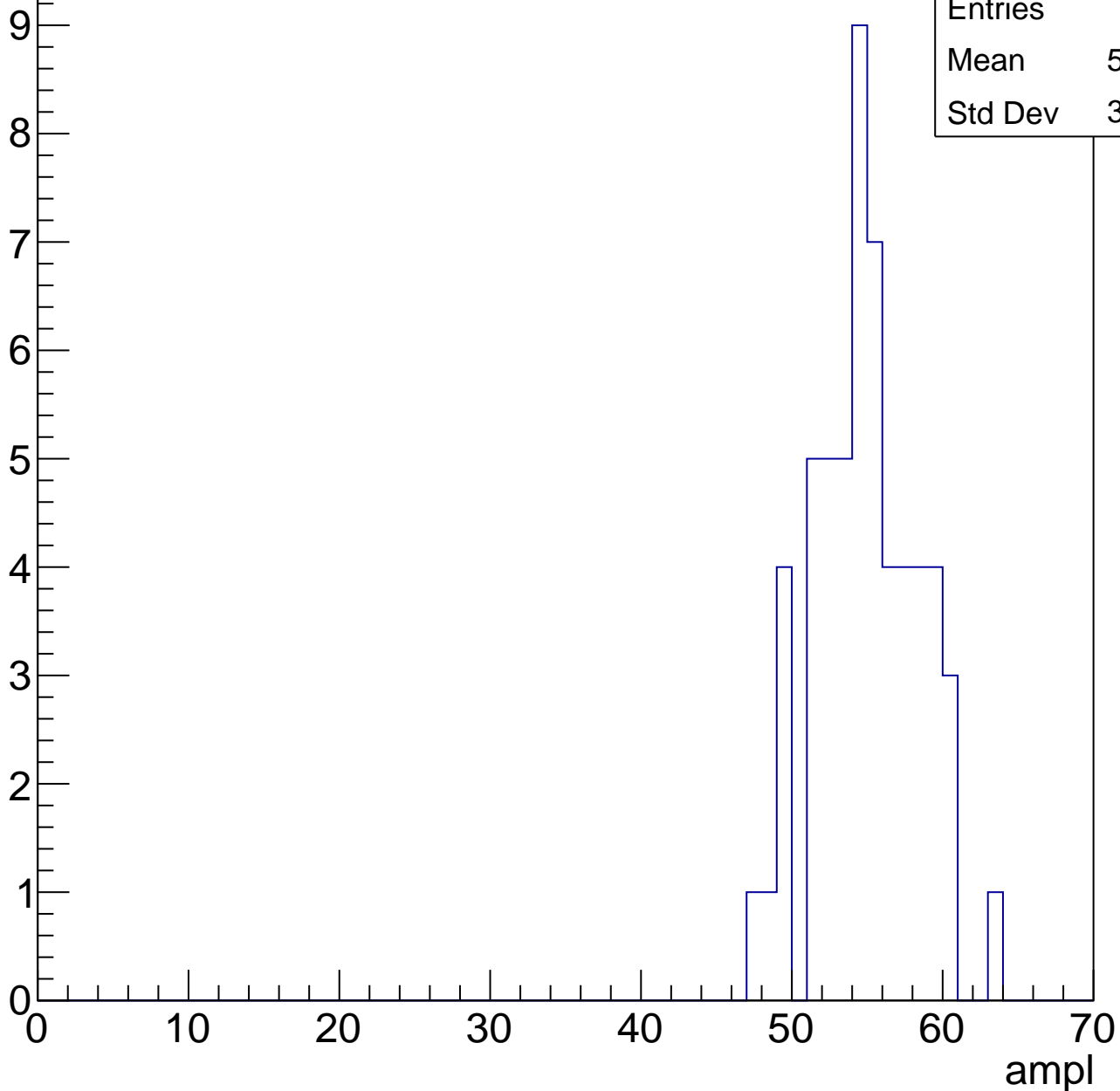


# B1L000S, U2-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

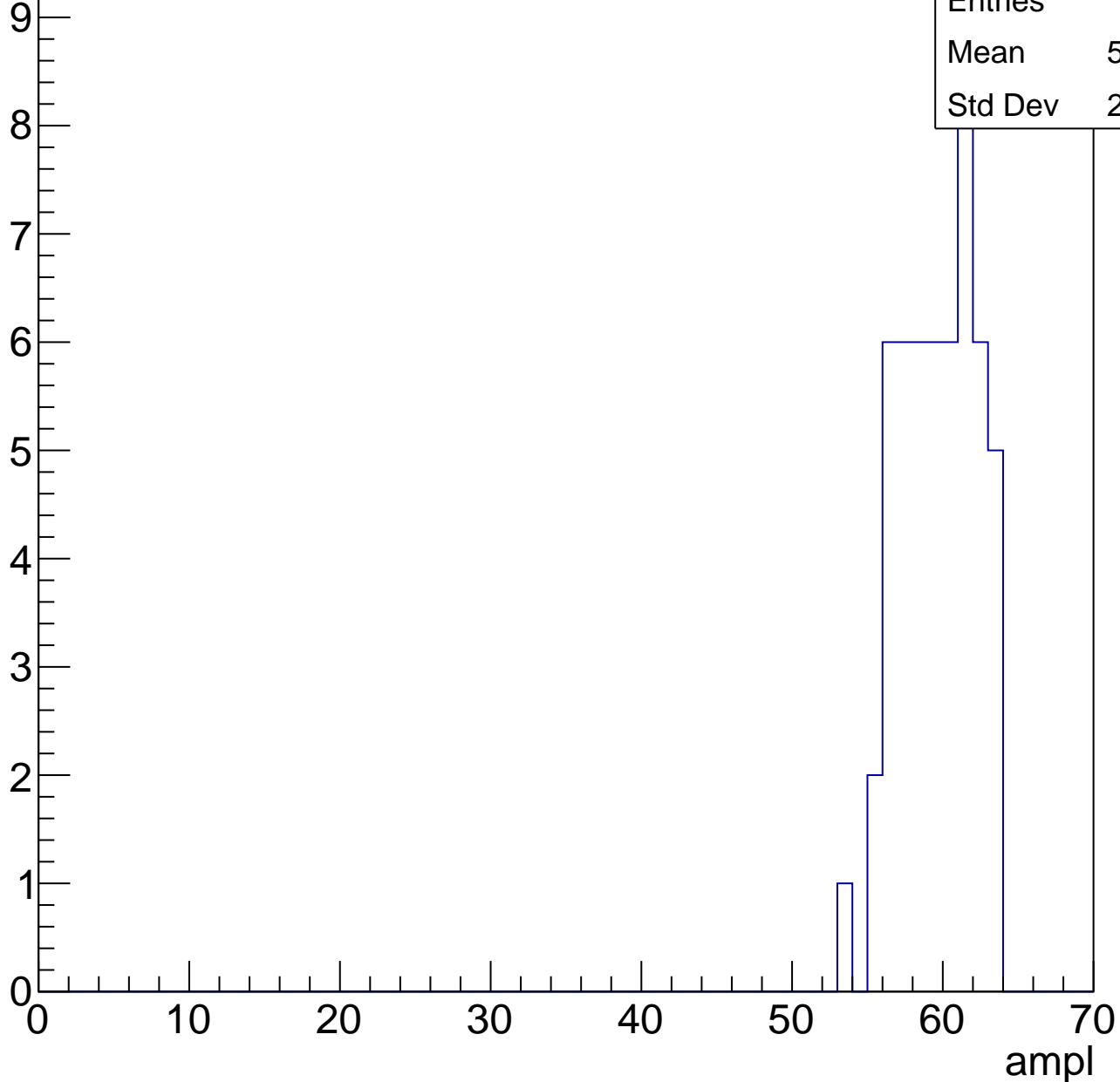
Entries	57
Mean	54.47
Std Dev	3.388



# B1L000S, U2-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

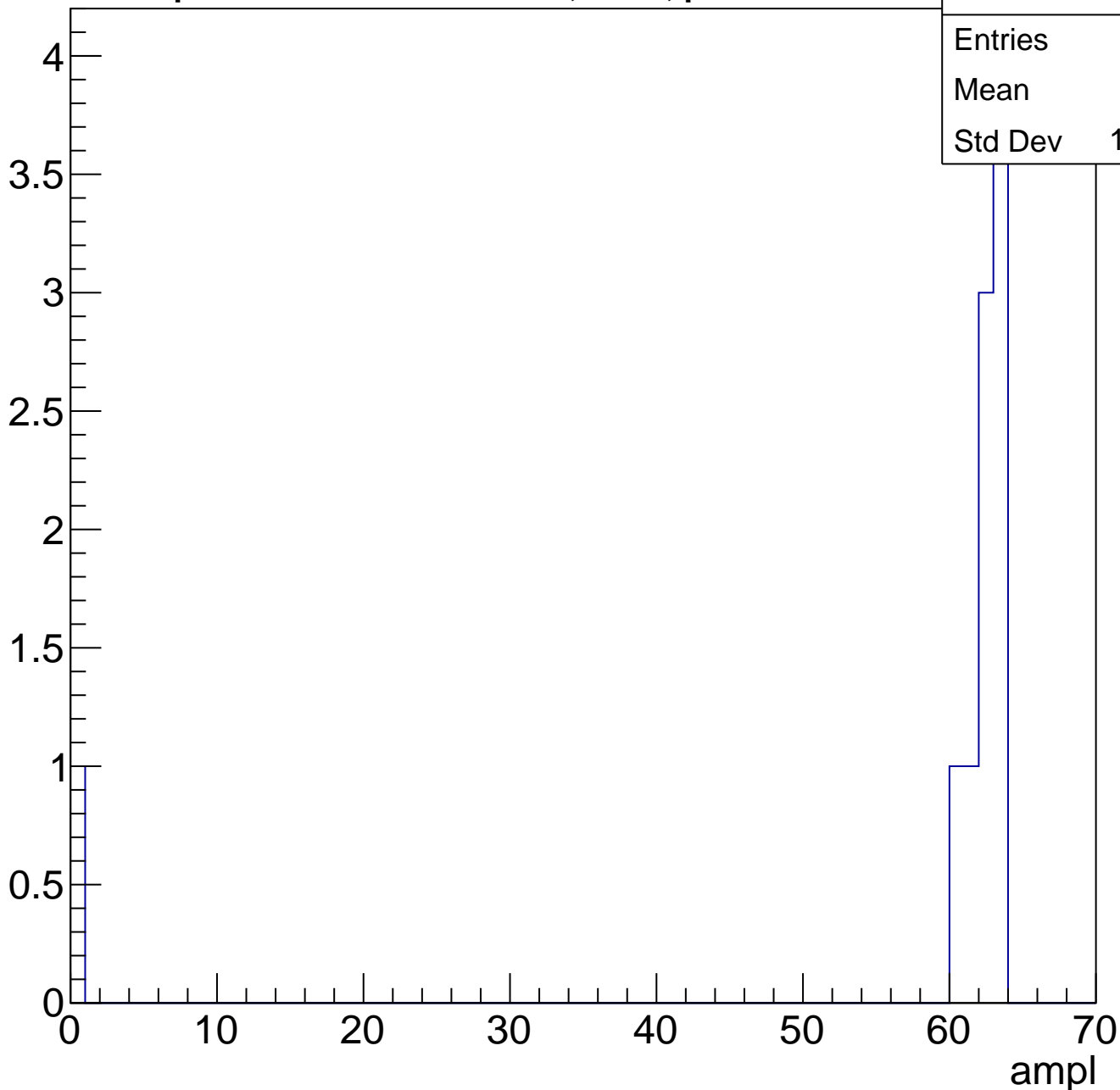
Entry



# B1L000S, U2-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

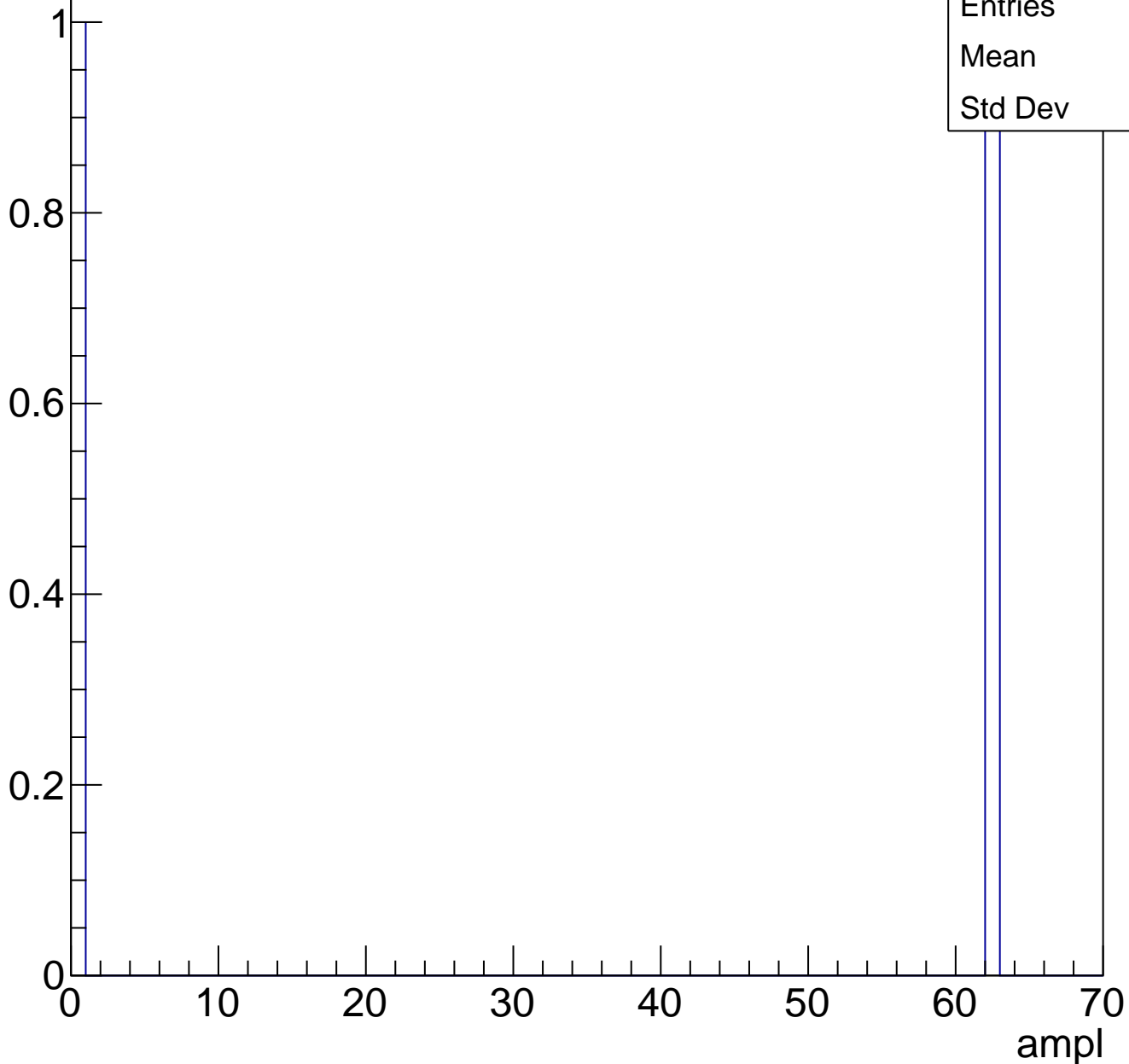




# B1L000S, U2-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch20, adc0

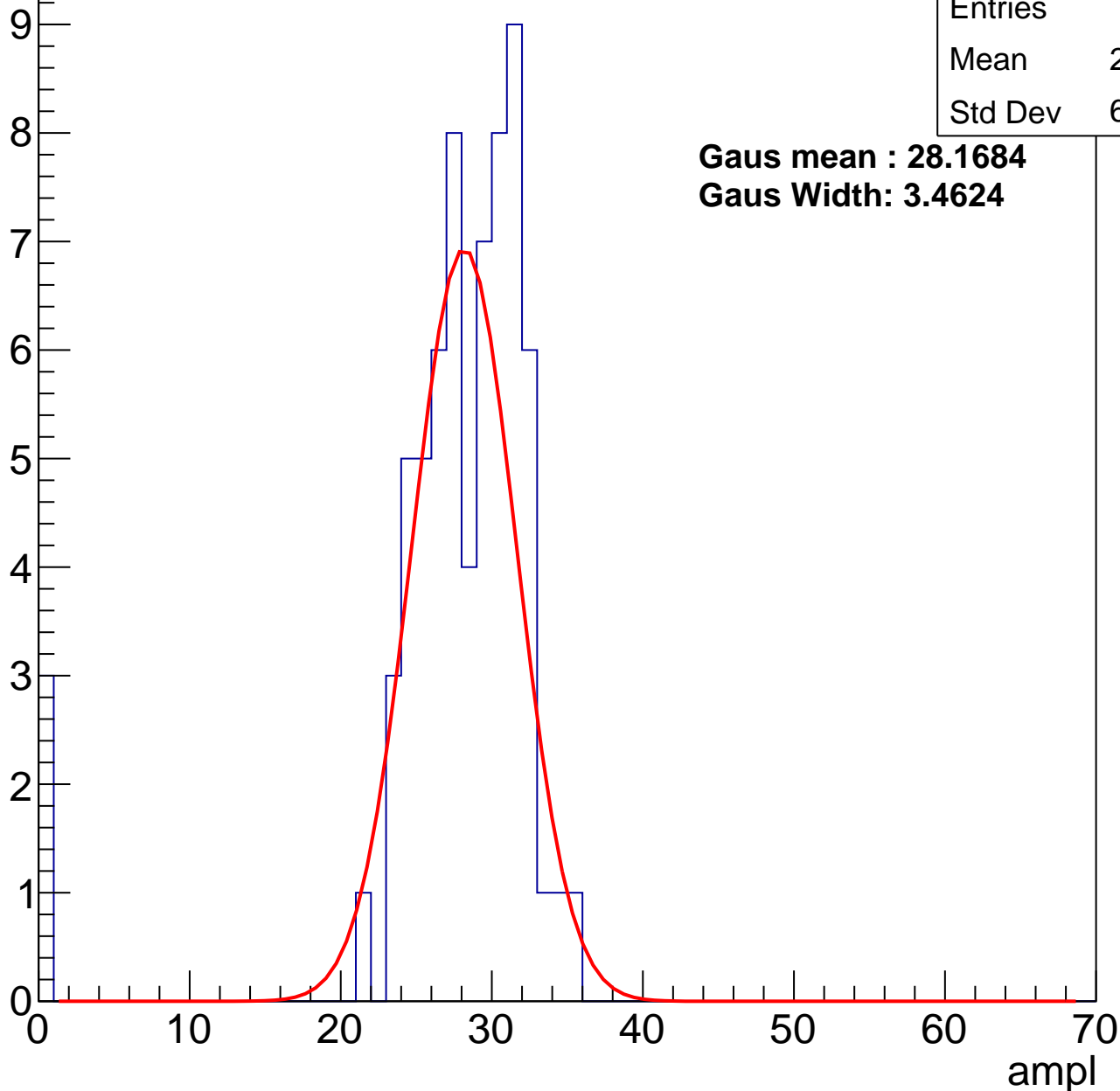
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	26.99
Std Dev	6.518

**Gaus mean : 28.1684**

**Gaus Width: 3.4624**



# B1L000S, U2-ch20, adc1

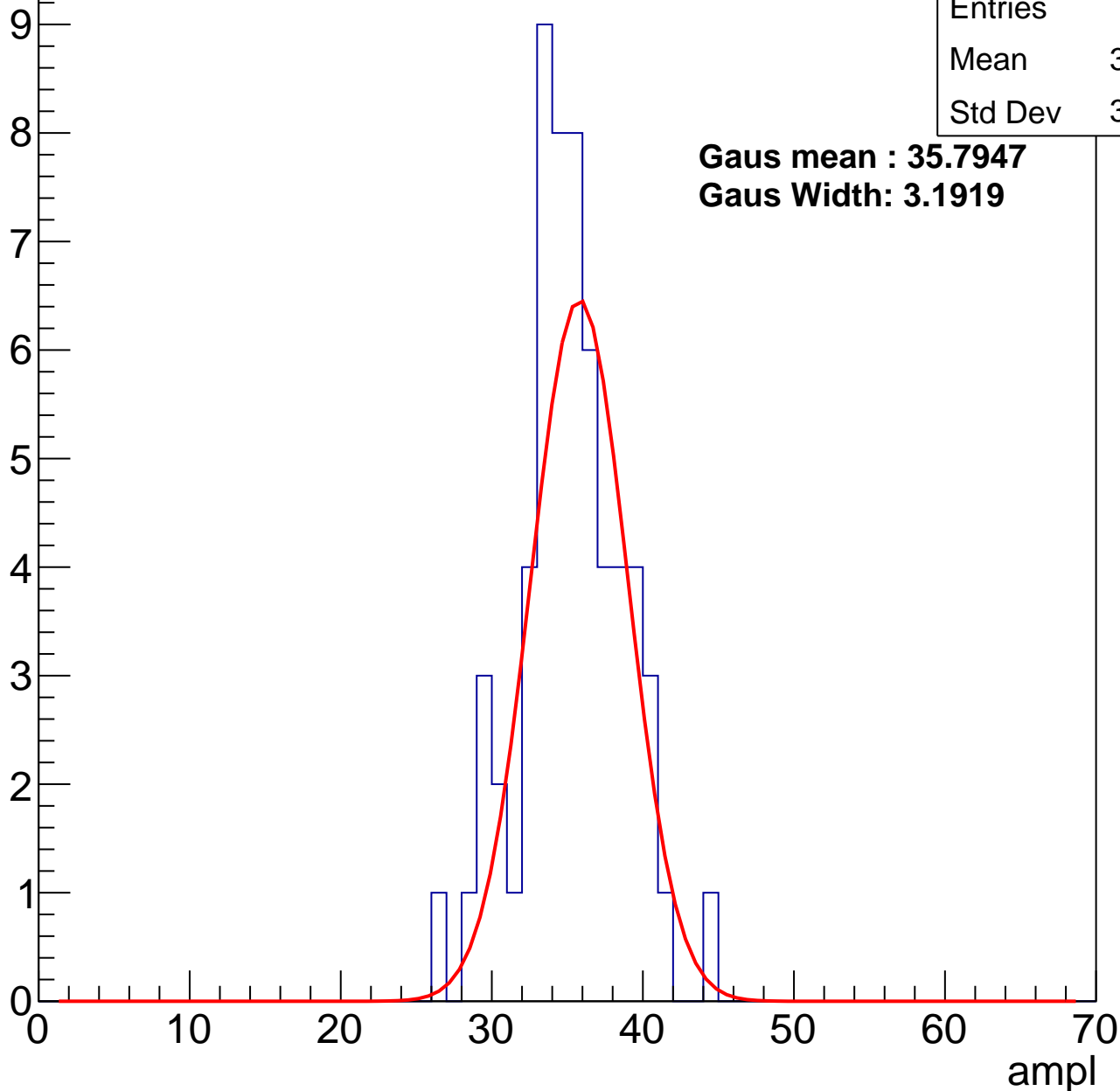
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	34.77
Std Dev	3.403

**Gaus mean : 35.7947**

**Gaus Width: 3.1919**



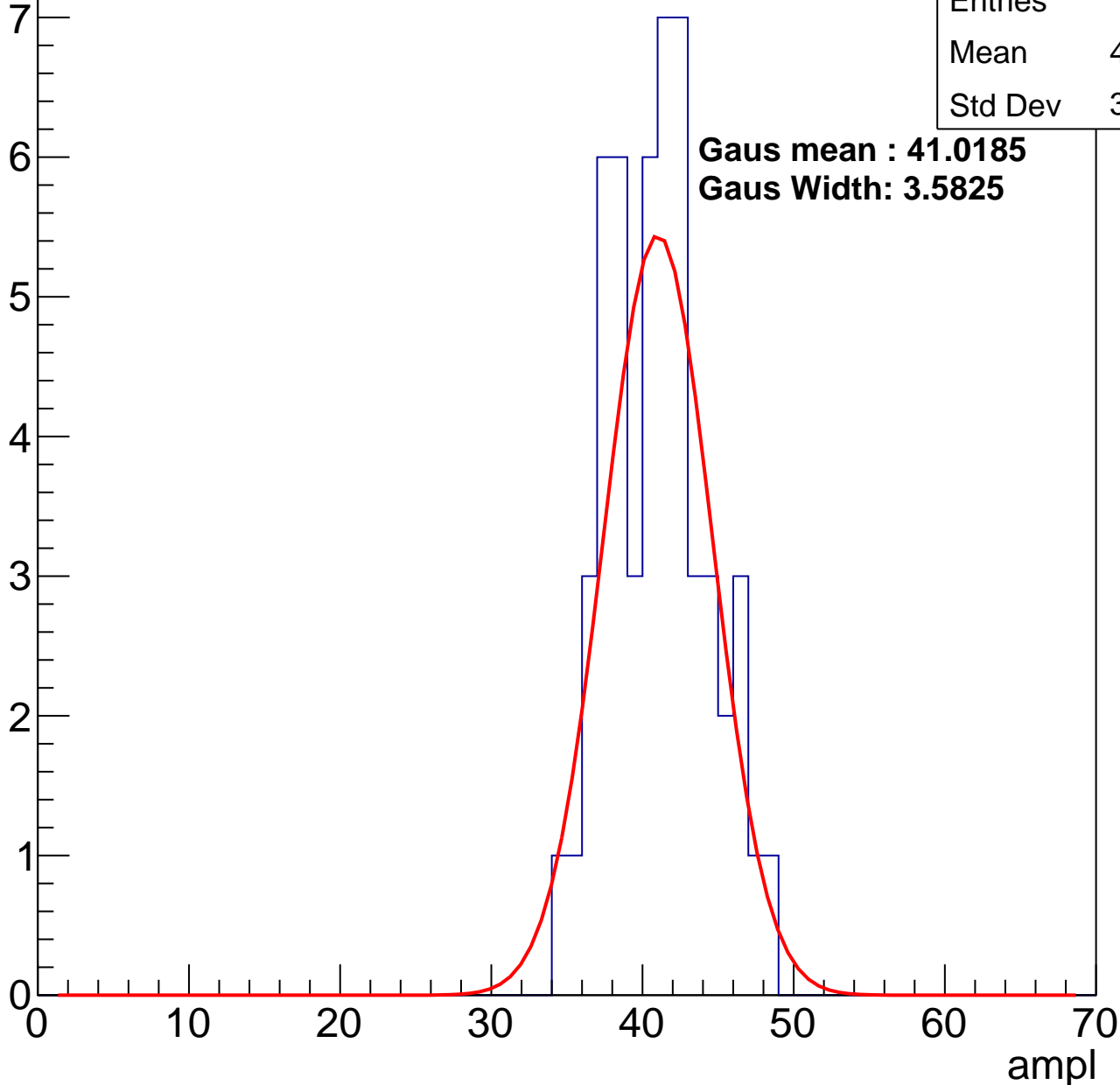
# B1L000S, U2-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	40.55
Std Dev	3.236

**Gaus mean : 41.0185**  
**Gaus Width: 3.5825**

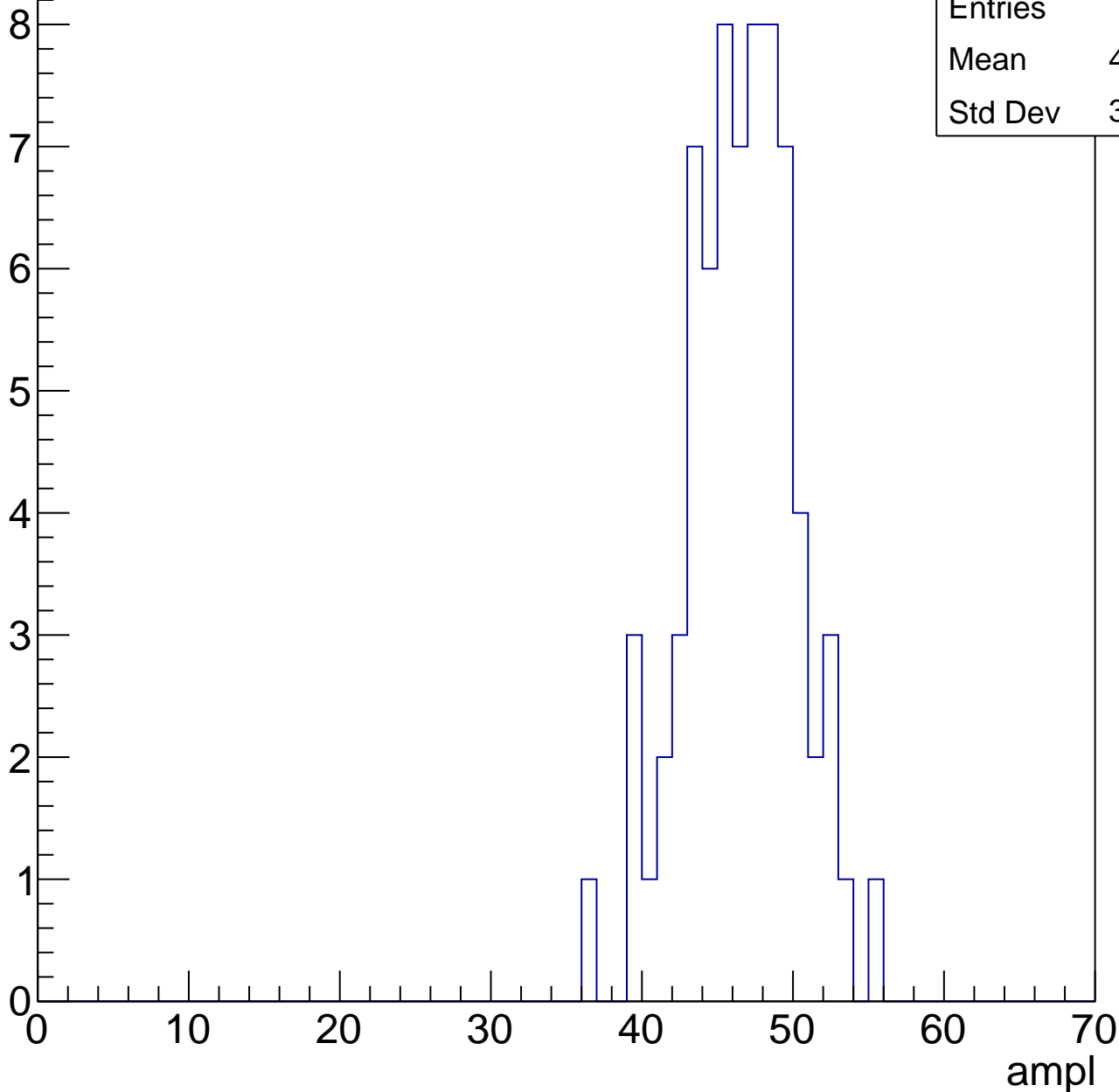


# B1L000S, U2-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

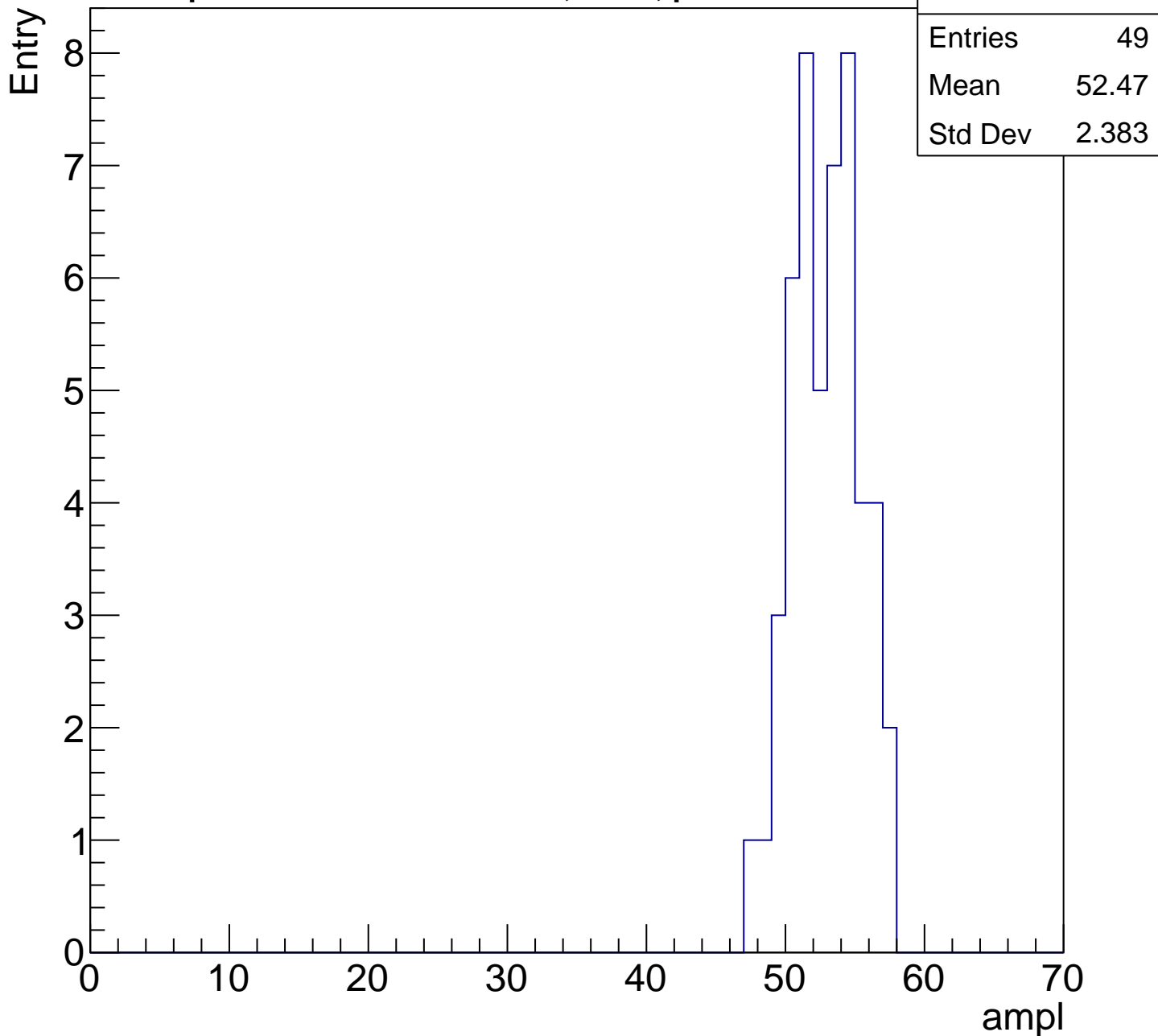
Entry

Entries	72
Mean	46.07
Std Dev	3.595



# B1L000S, U2-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 53

Mean 57.53

Std Dev 2.469

8

6

4

2

0

0

10

20

30

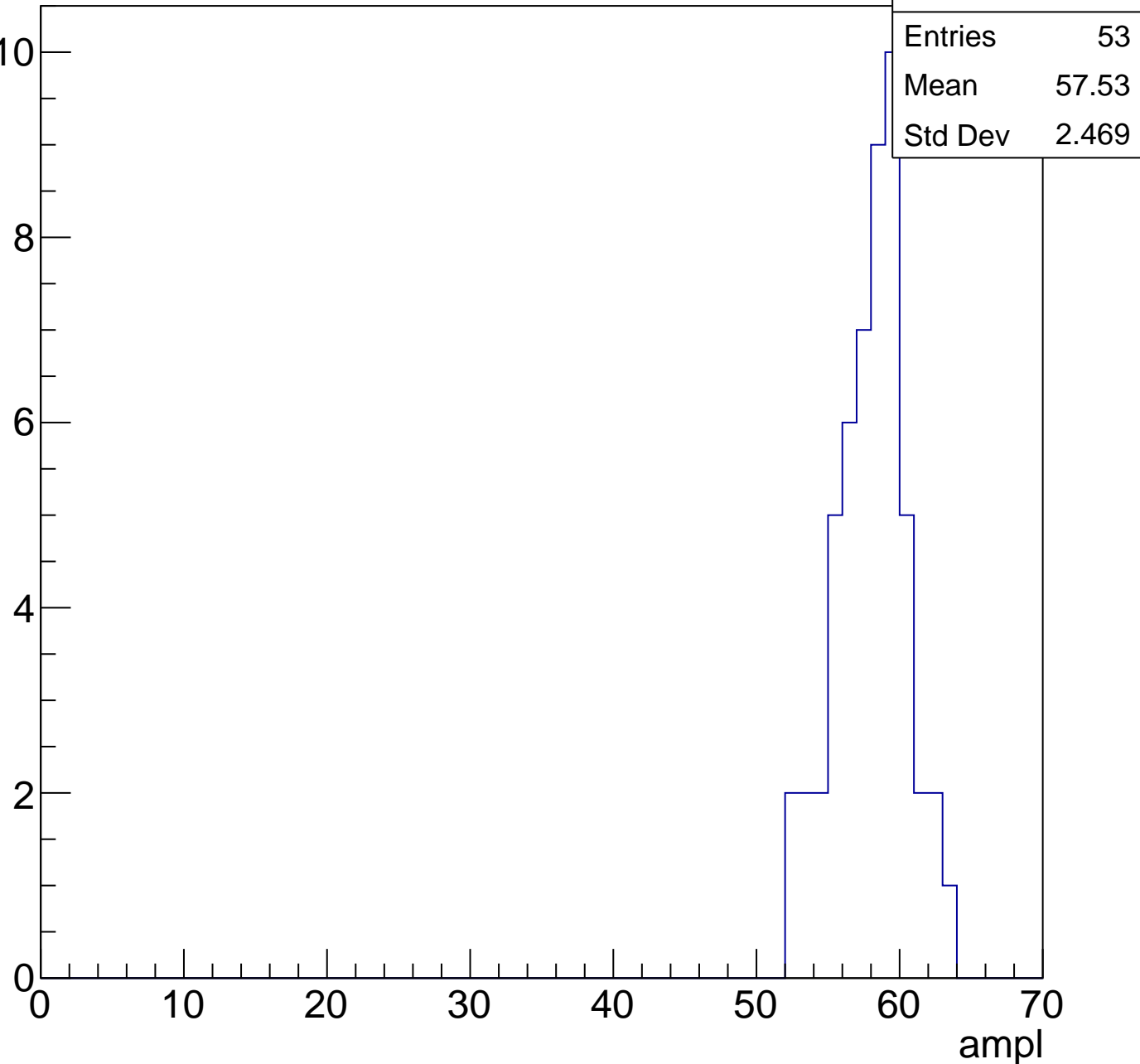
40

50

60

70

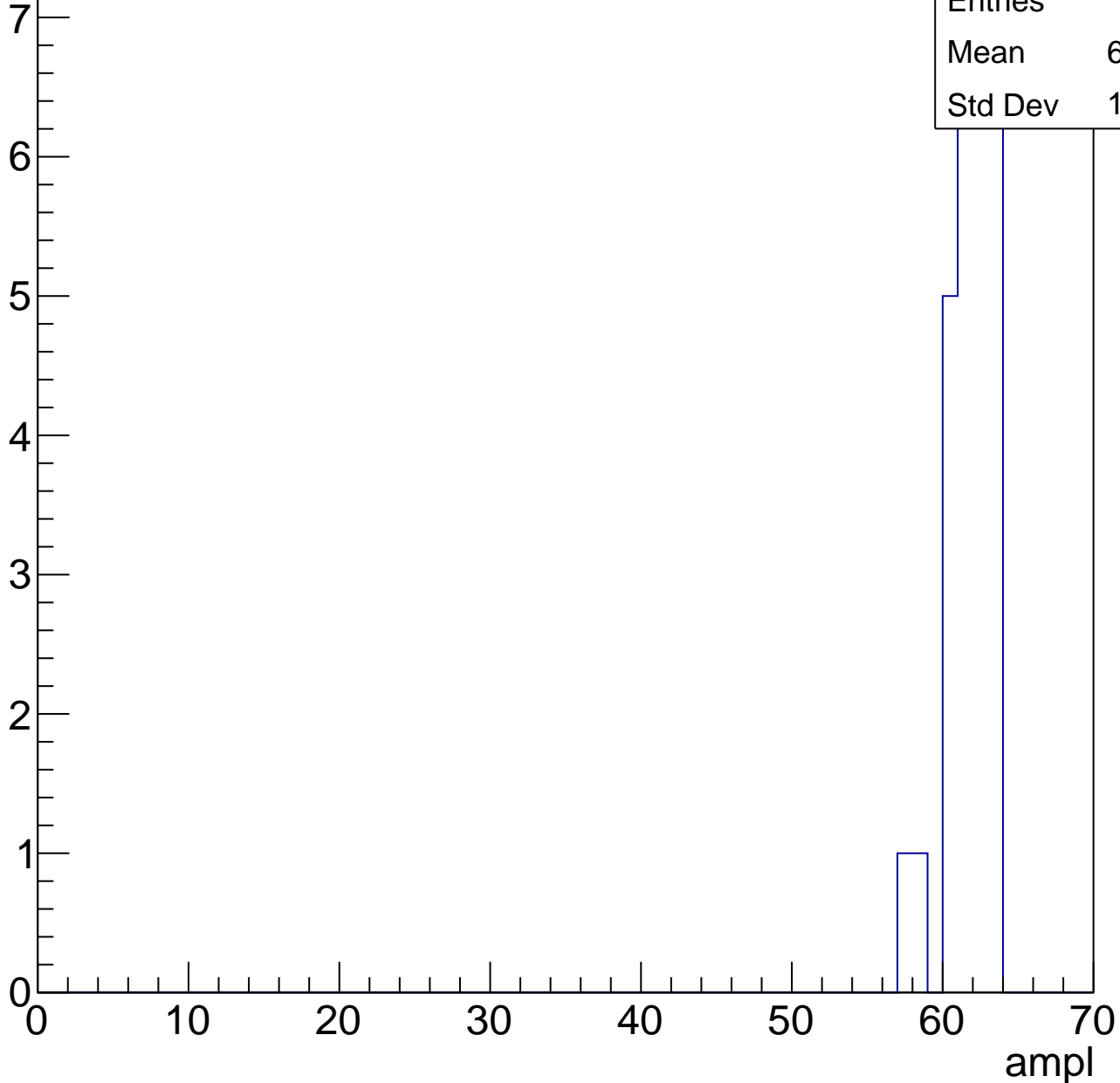
ampl



# B1L000S, U2-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

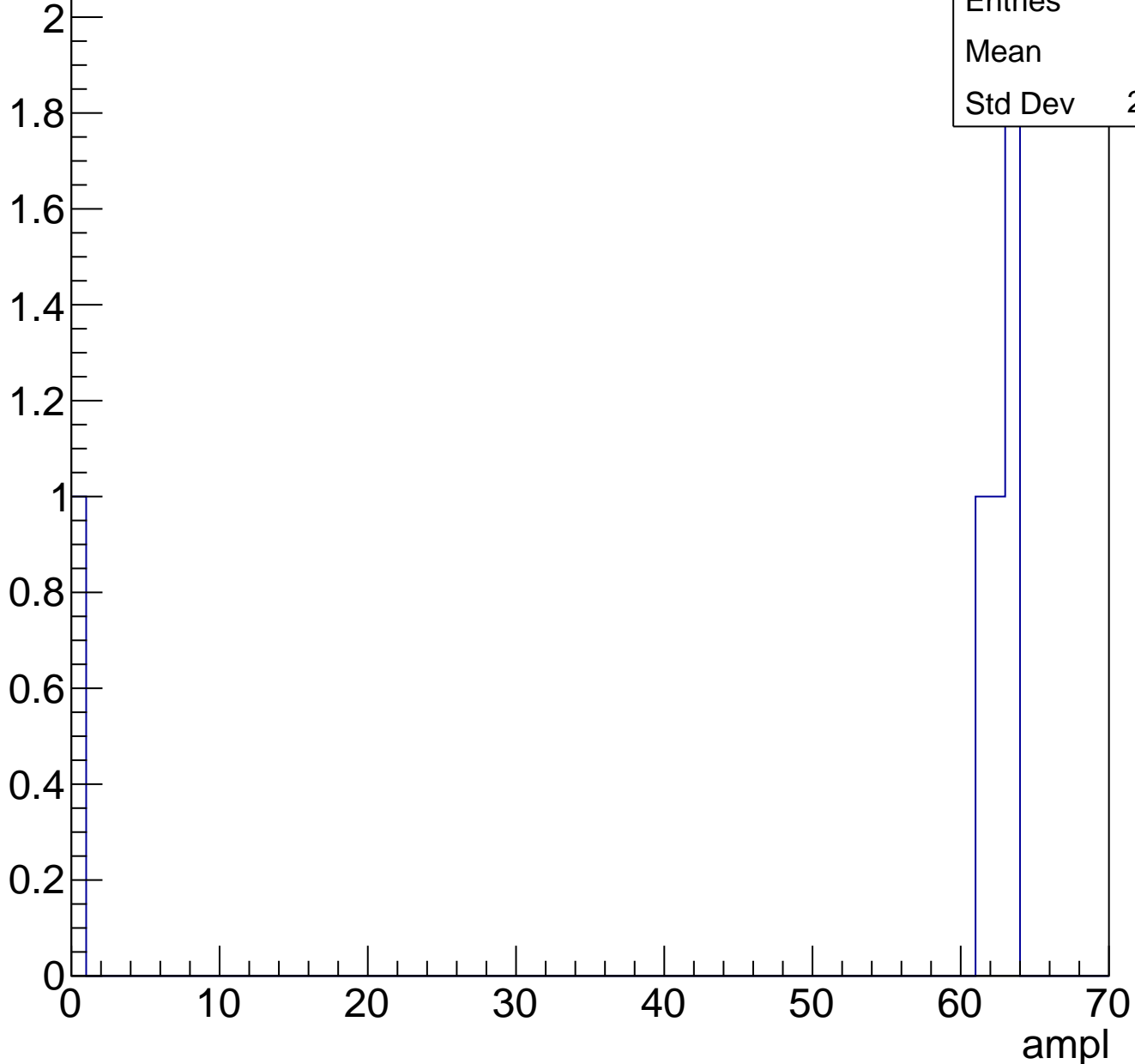




# B1L000S, U2-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	49.8
Std Dev	24.91

# B1L000S, U2-ch21, adc0

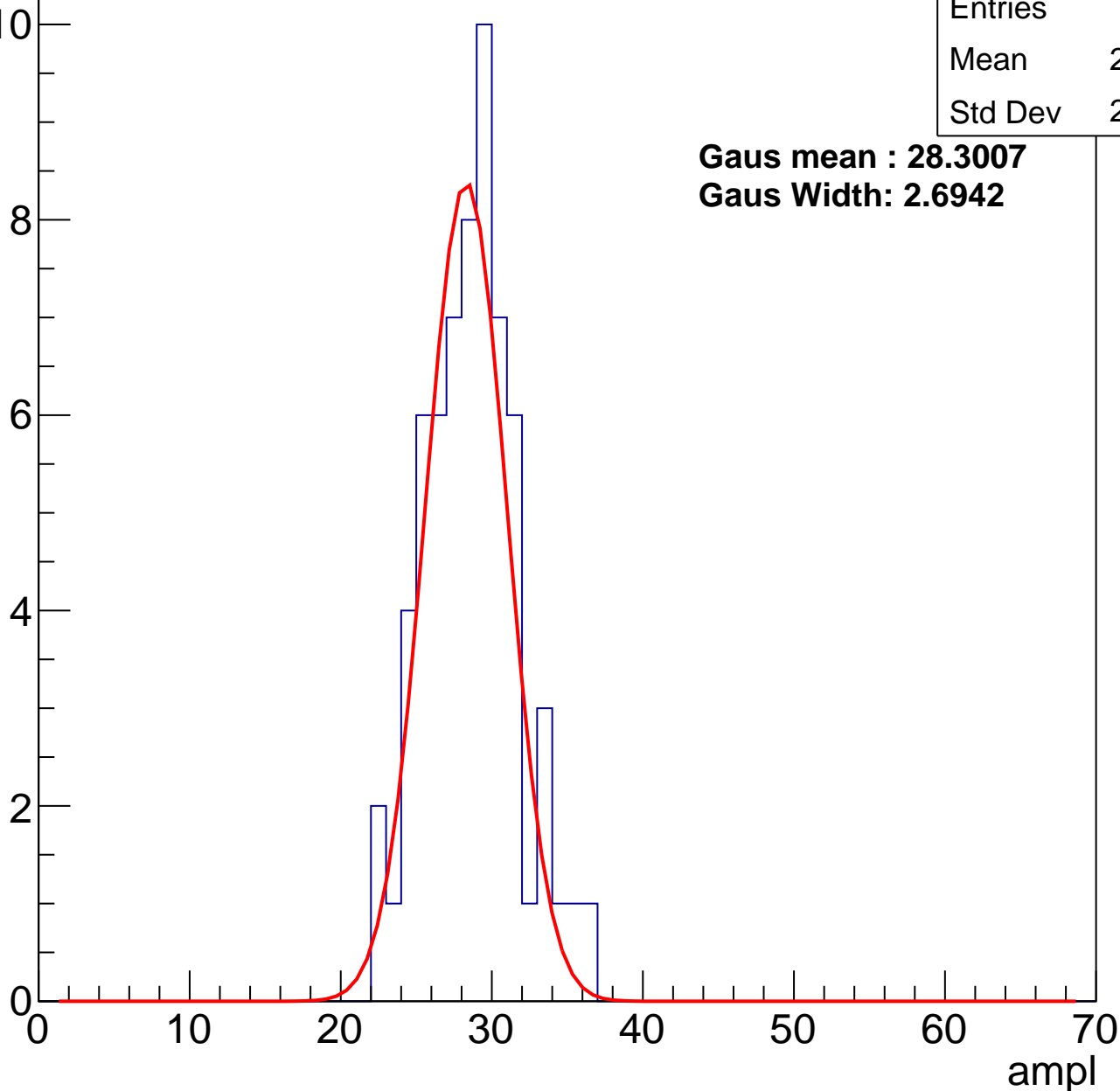
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.19
Std Dev	2.999

**Gaus mean : 28.3007**

**Gaus Width: 2.6942**



# B1L000S, U2-ch21, adc1

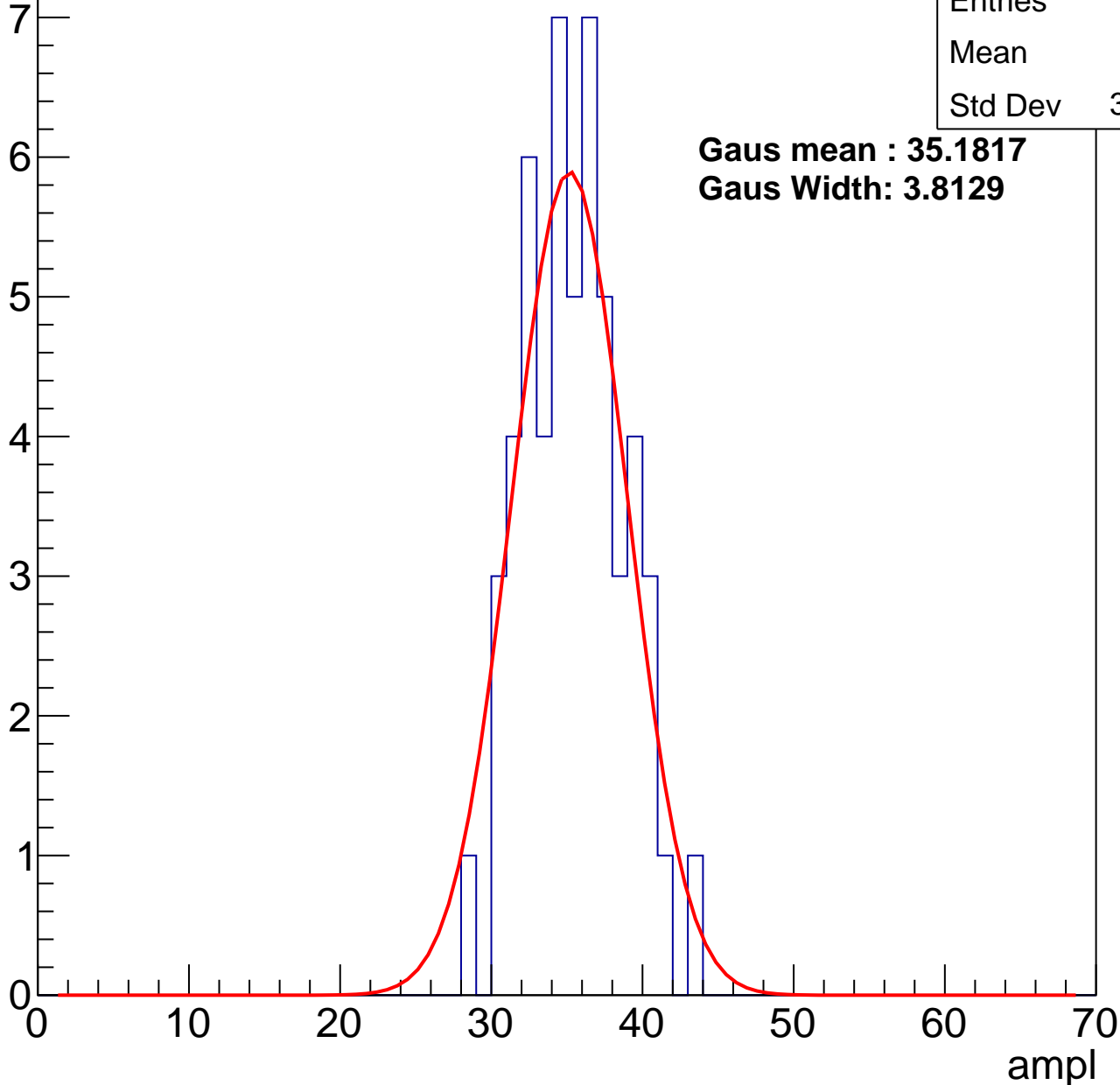
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	35
Std Dev	3.215

**Gaus mean : 35.1817**

**Gaus Width: 3.8129**



# B1L000S, U2-ch21, adc2

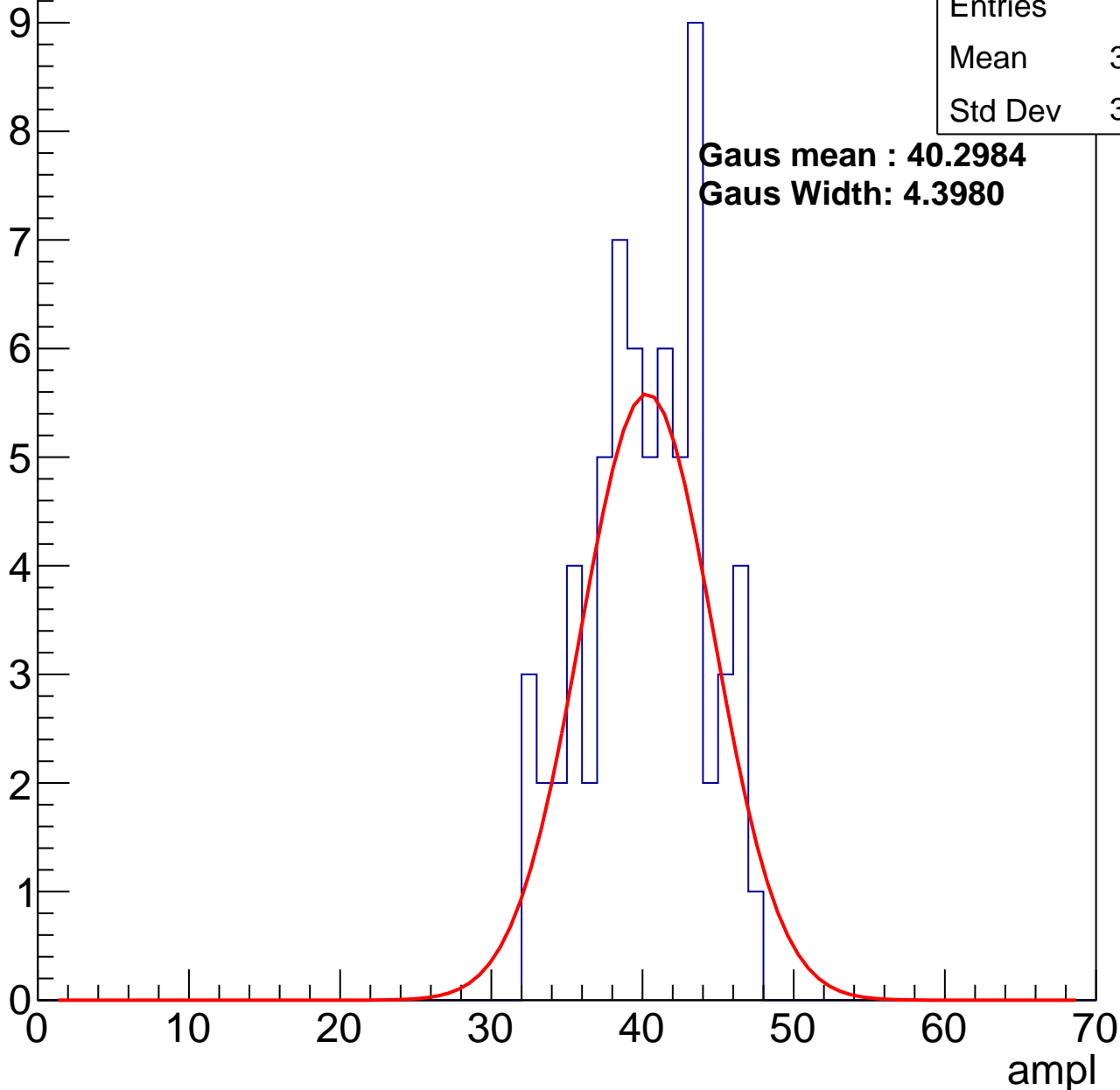
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	39.76
Std Dev	3.846

**Gaus mean : 40.2984**

**Gaus Width: 4.3980**

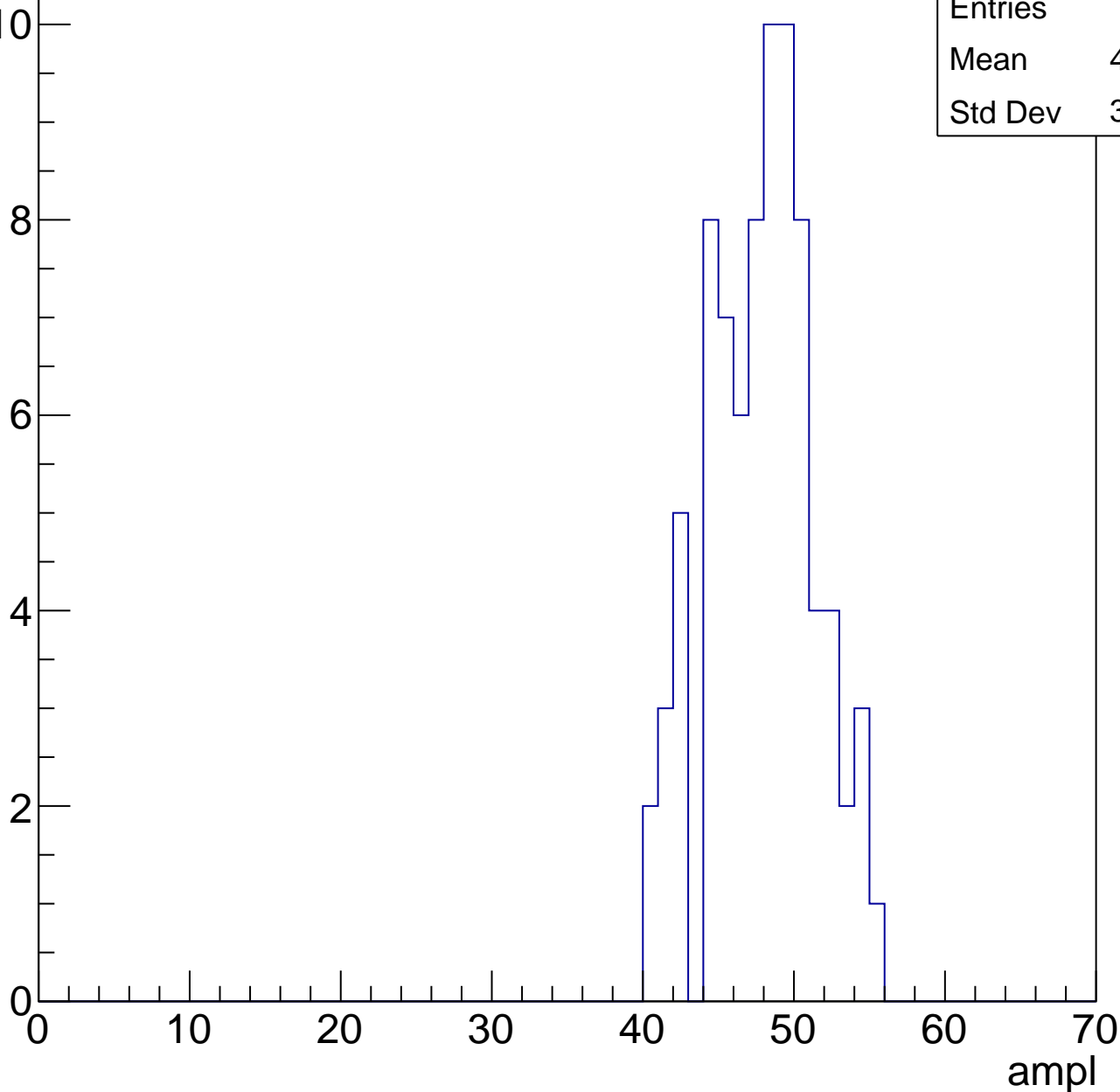


# B1L000S, U2-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	47.37
Std Dev	3.505

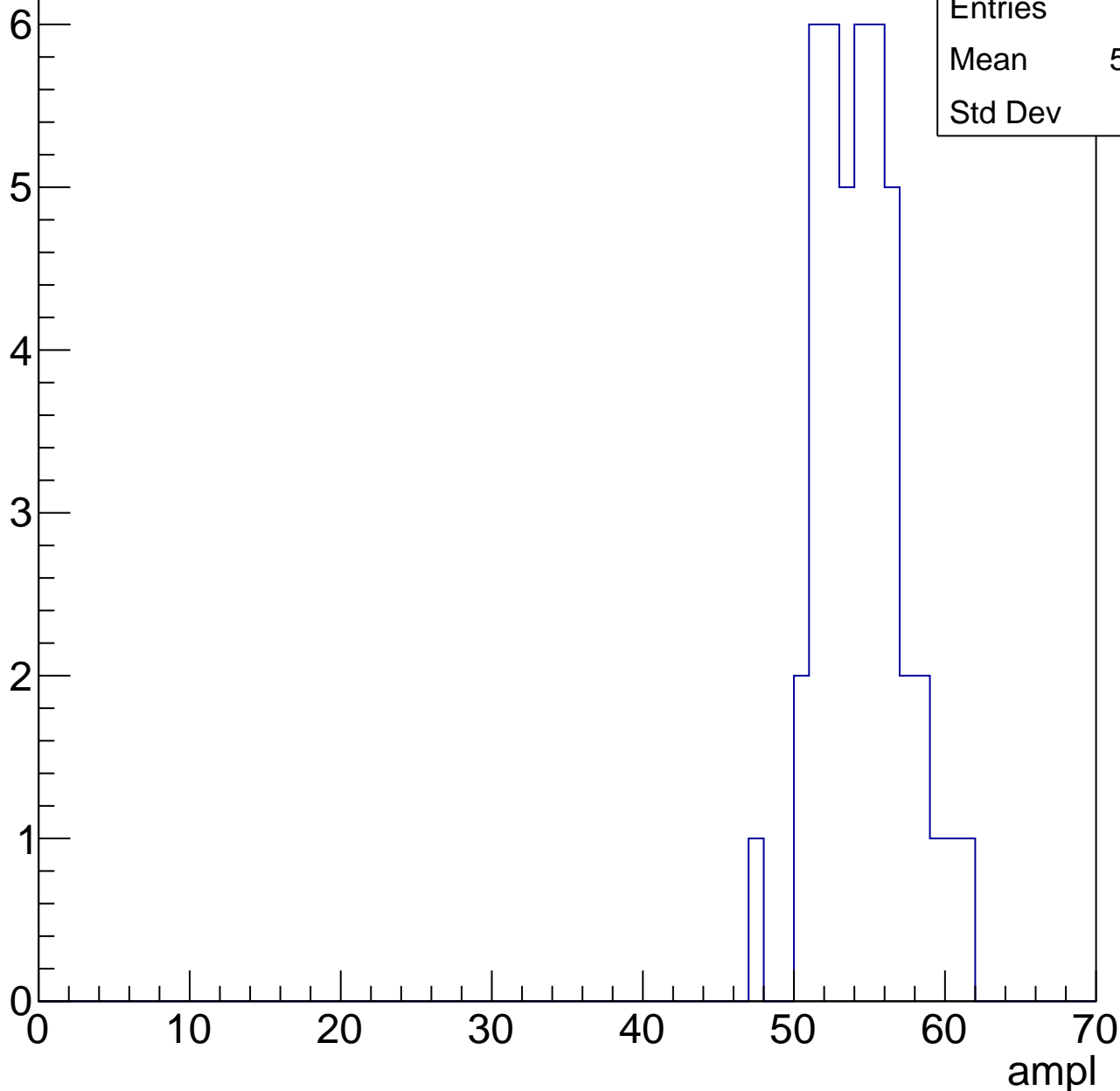


# B1L000S, U2-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	53.95
Std Dev	2.82



# B1L000S, U2-ch21, adc5

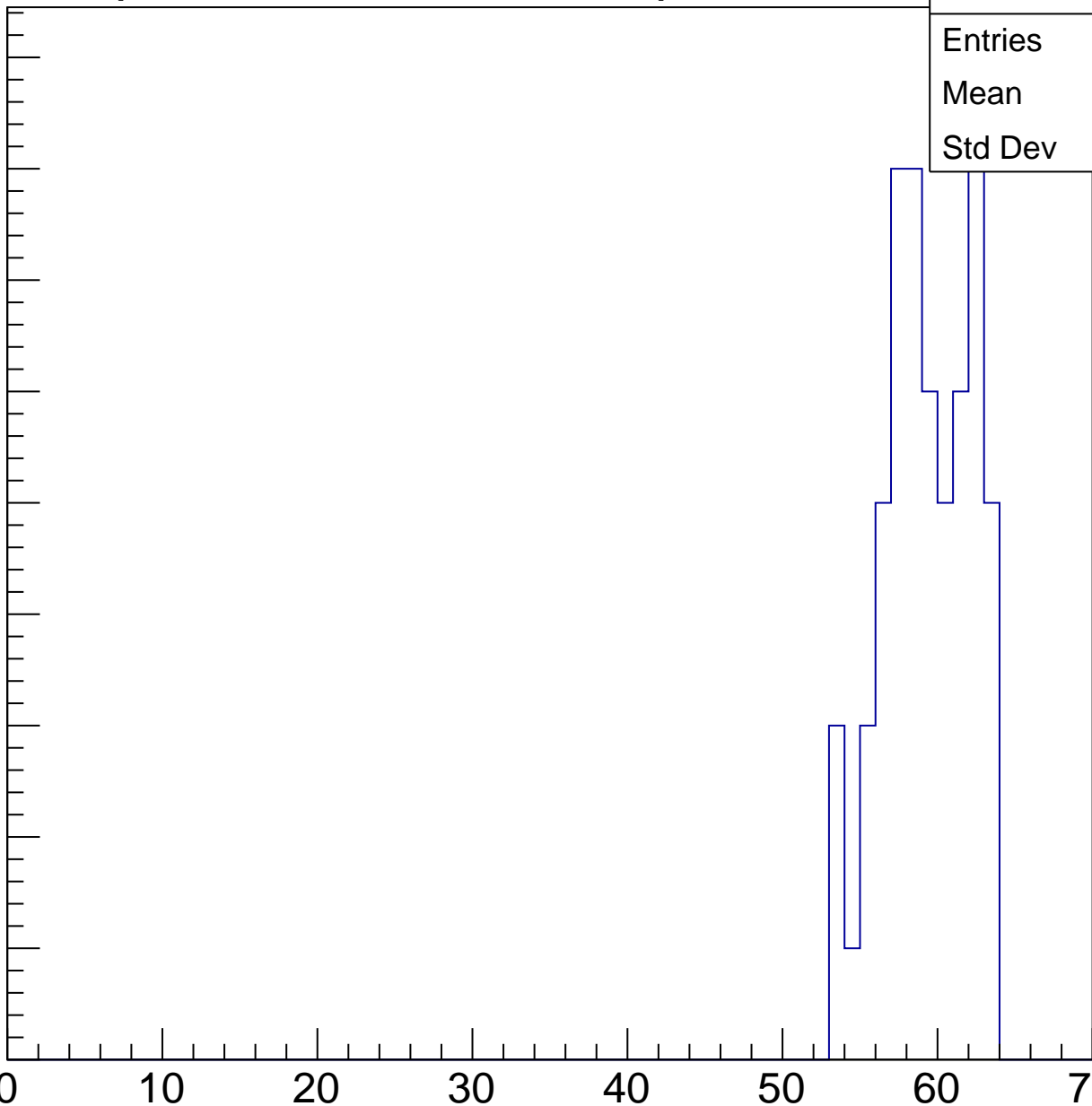
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	58.83
Std Dev	2.775

ampl

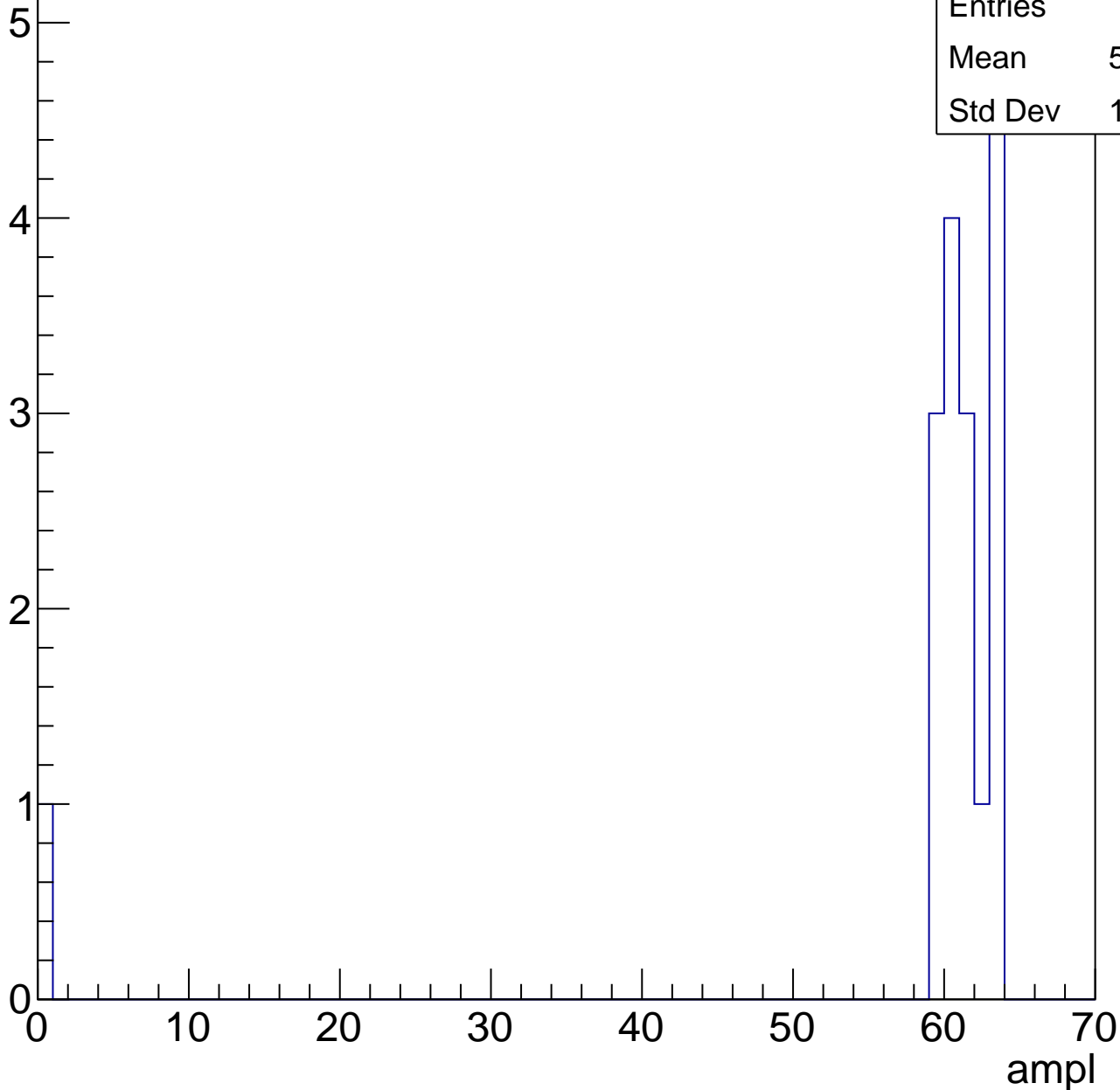


# B1L000S, U2-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.47
Std Dev	14.44

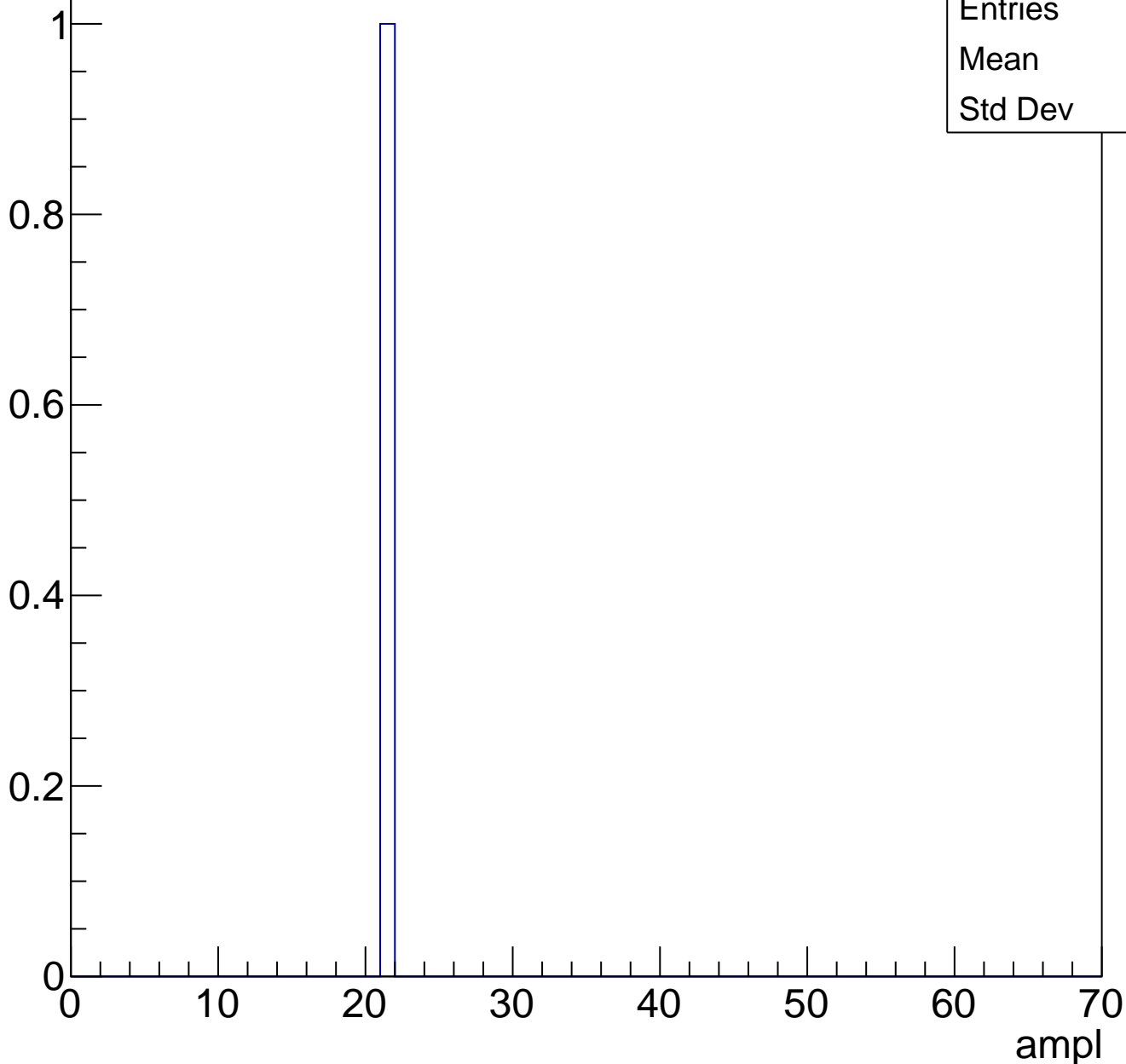




# B1L000S, U2-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch22, adc0

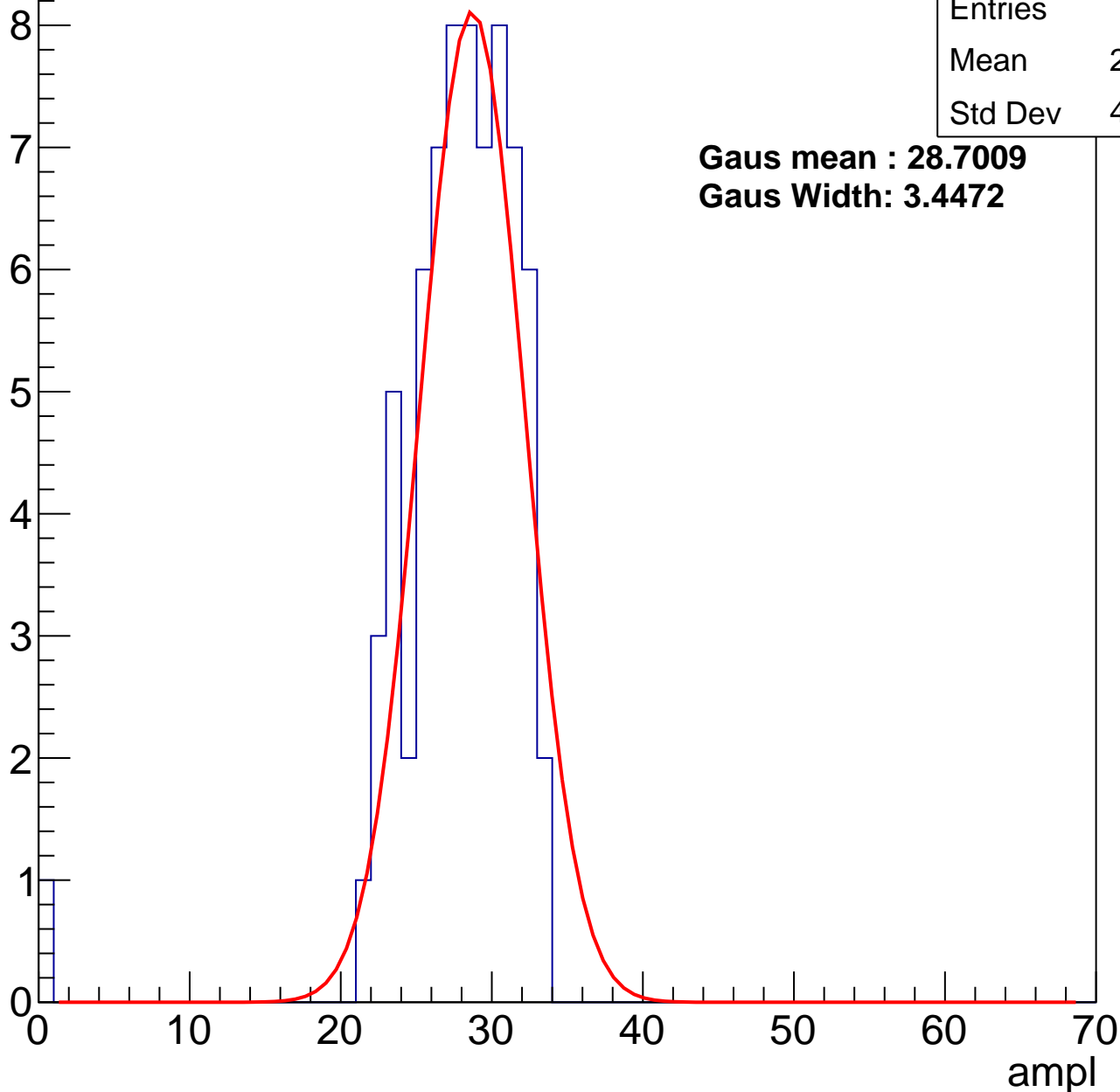
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	27.32
Std Dev	4.446

**Gaus mean : 28.7009**

**Gaus Width: 3.4472**



# B1L000S, U2-ch22, adc1

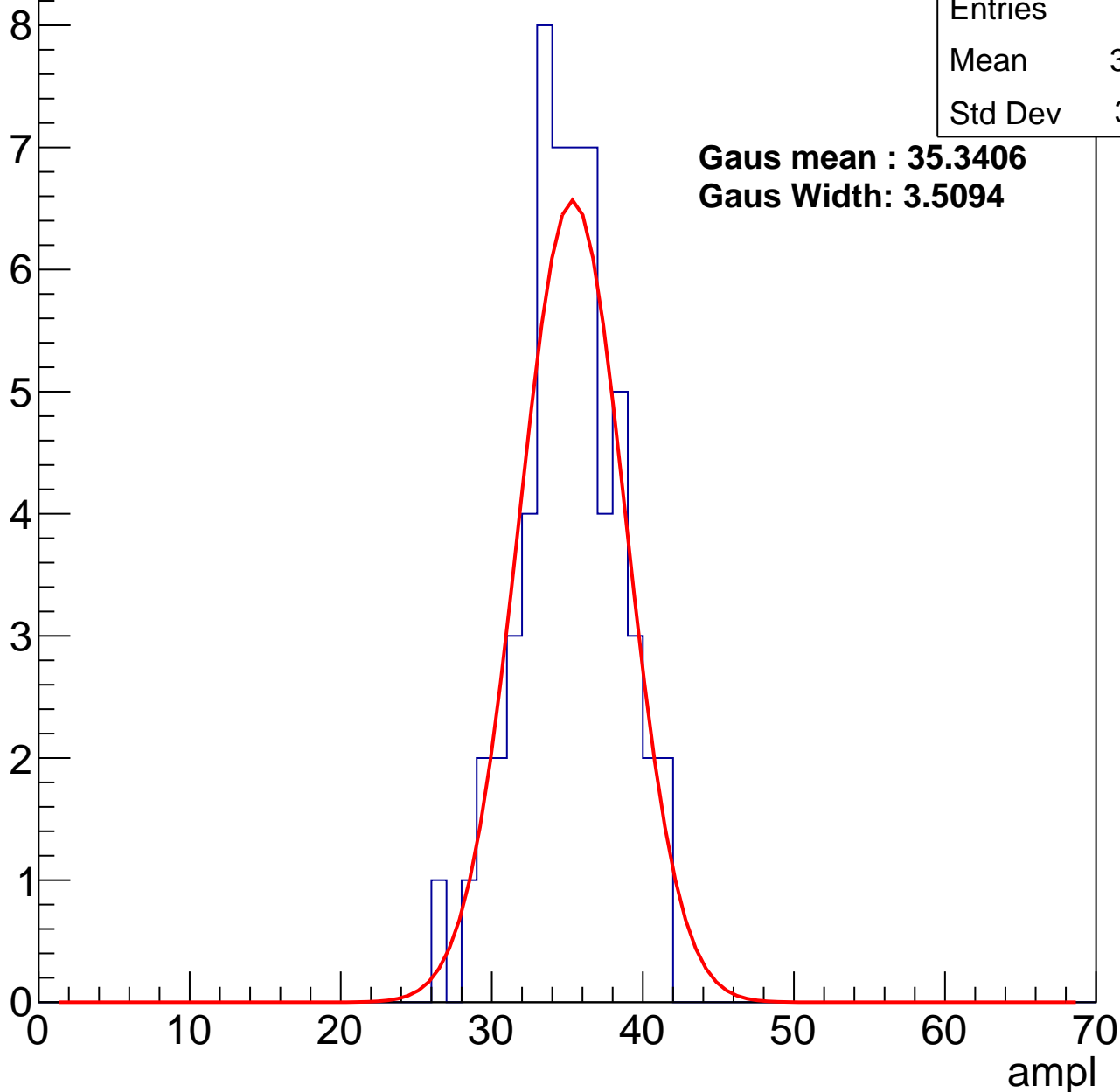
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.64
Std Dev	3.231

**Gaus mean : 35.3406**

**Gaus Width: 3.5094**



# B1L000S, U2-ch22, adc2

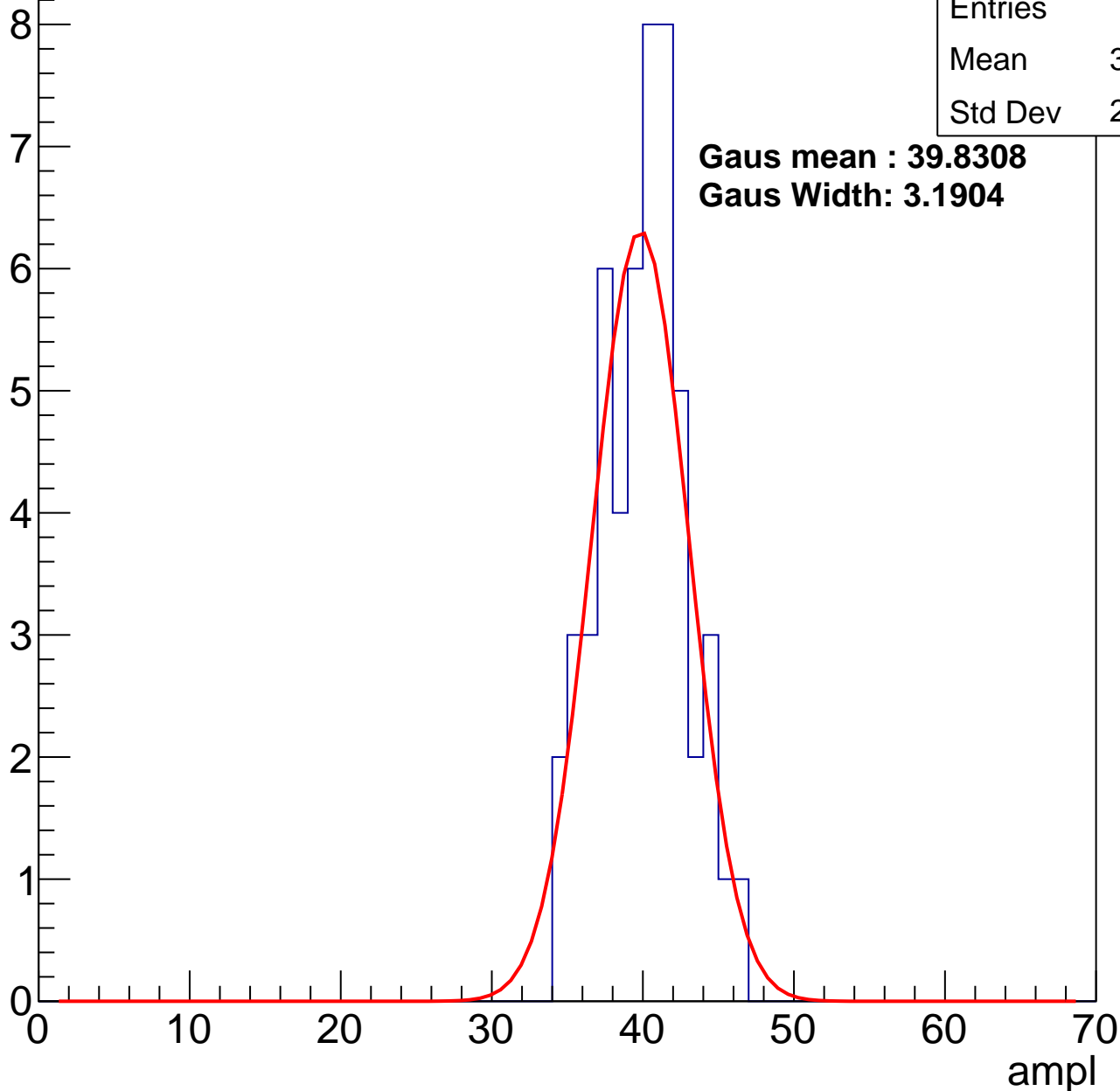
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	39.54
Std Dev	2.825

**Gaus mean : 39.8308**

**Gaus Width: 3.1904**

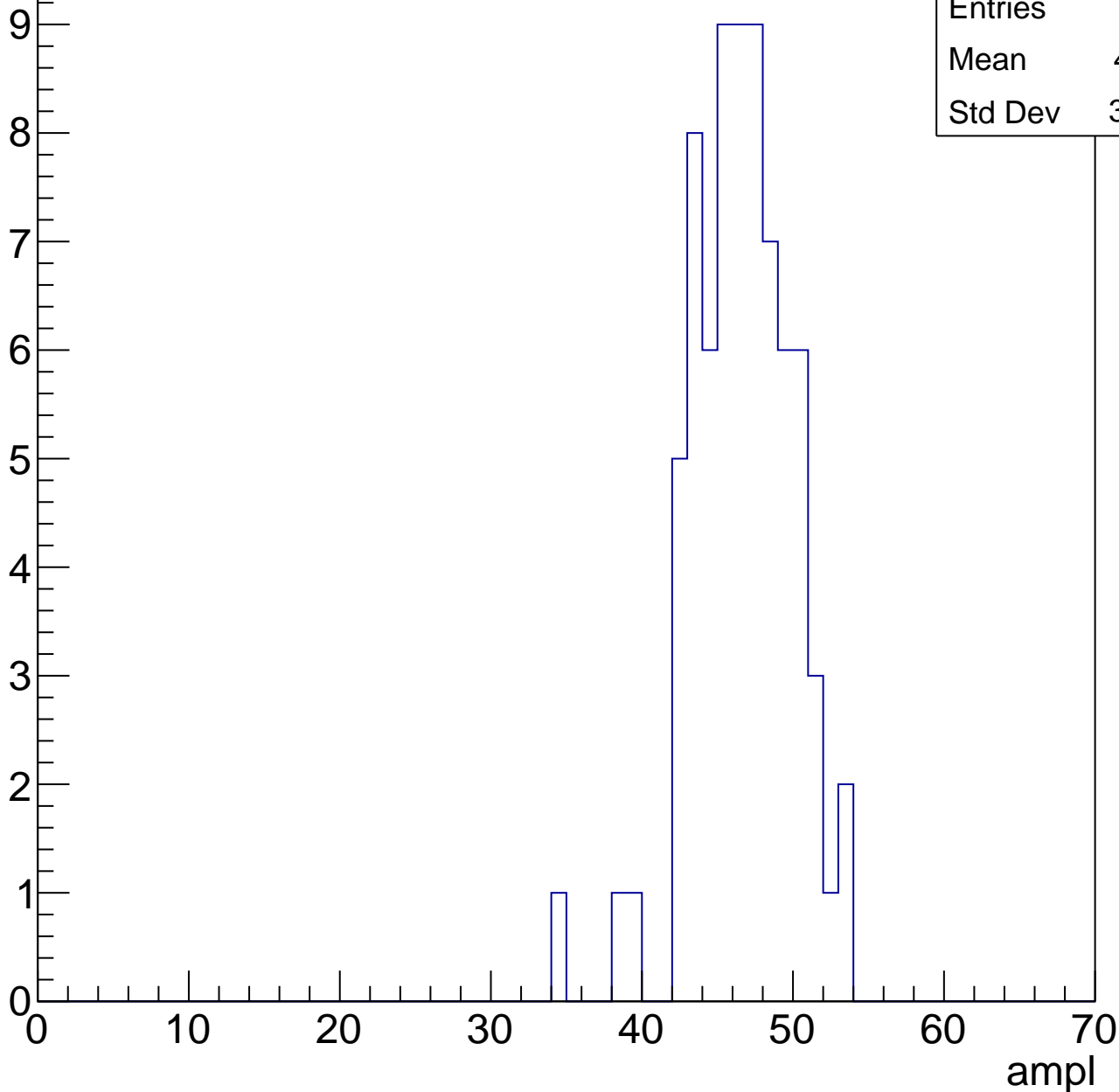


# B1L000S, U2-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	46.11
Std Dev	3.363

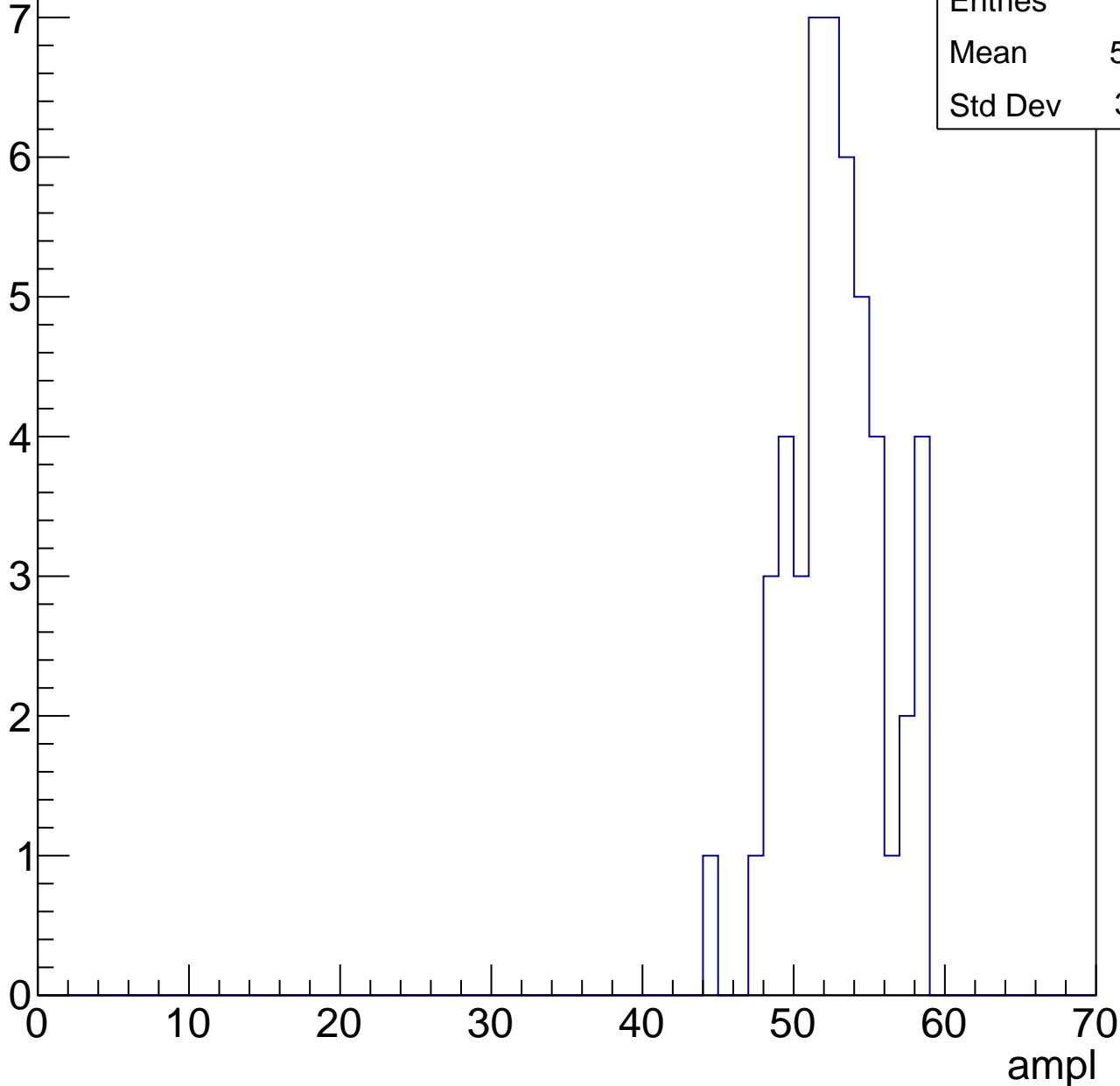


# B1L000S, U2-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

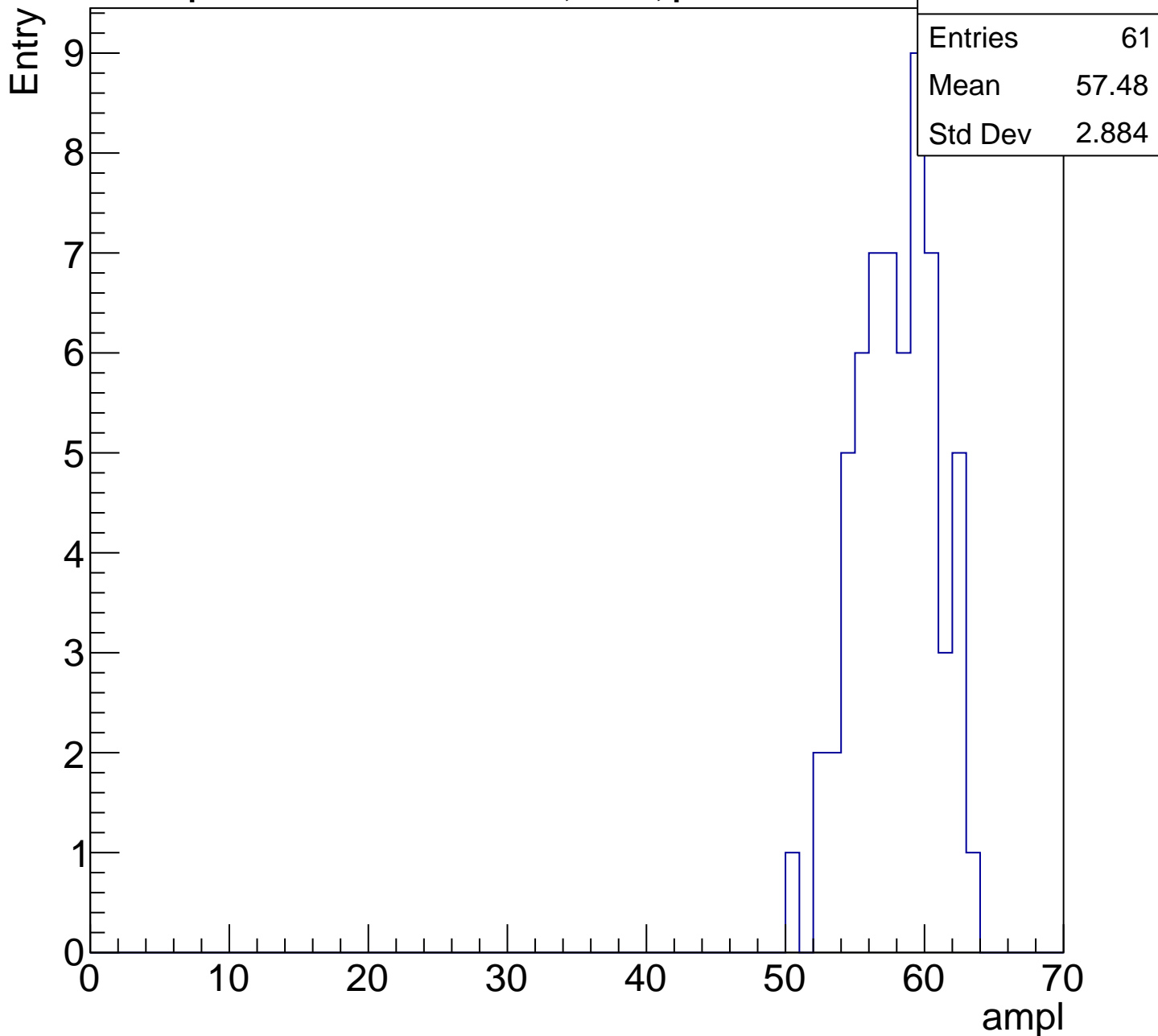
Entry

Entries	48
Mean	52.33
Std Dev	3.091



# B1L000S, U2-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

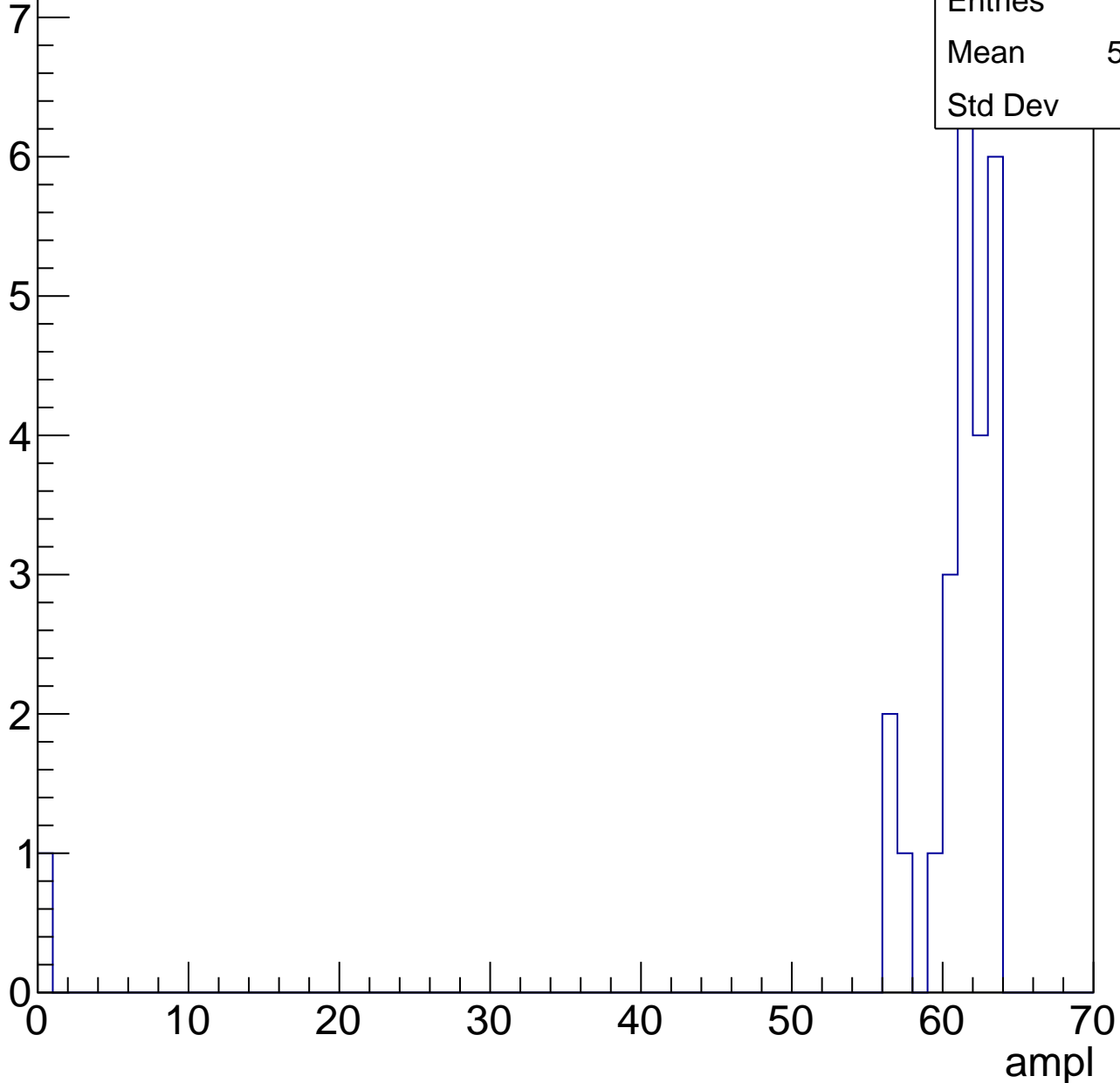


# B1L000S, U2-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.44
Std Dev	12.1

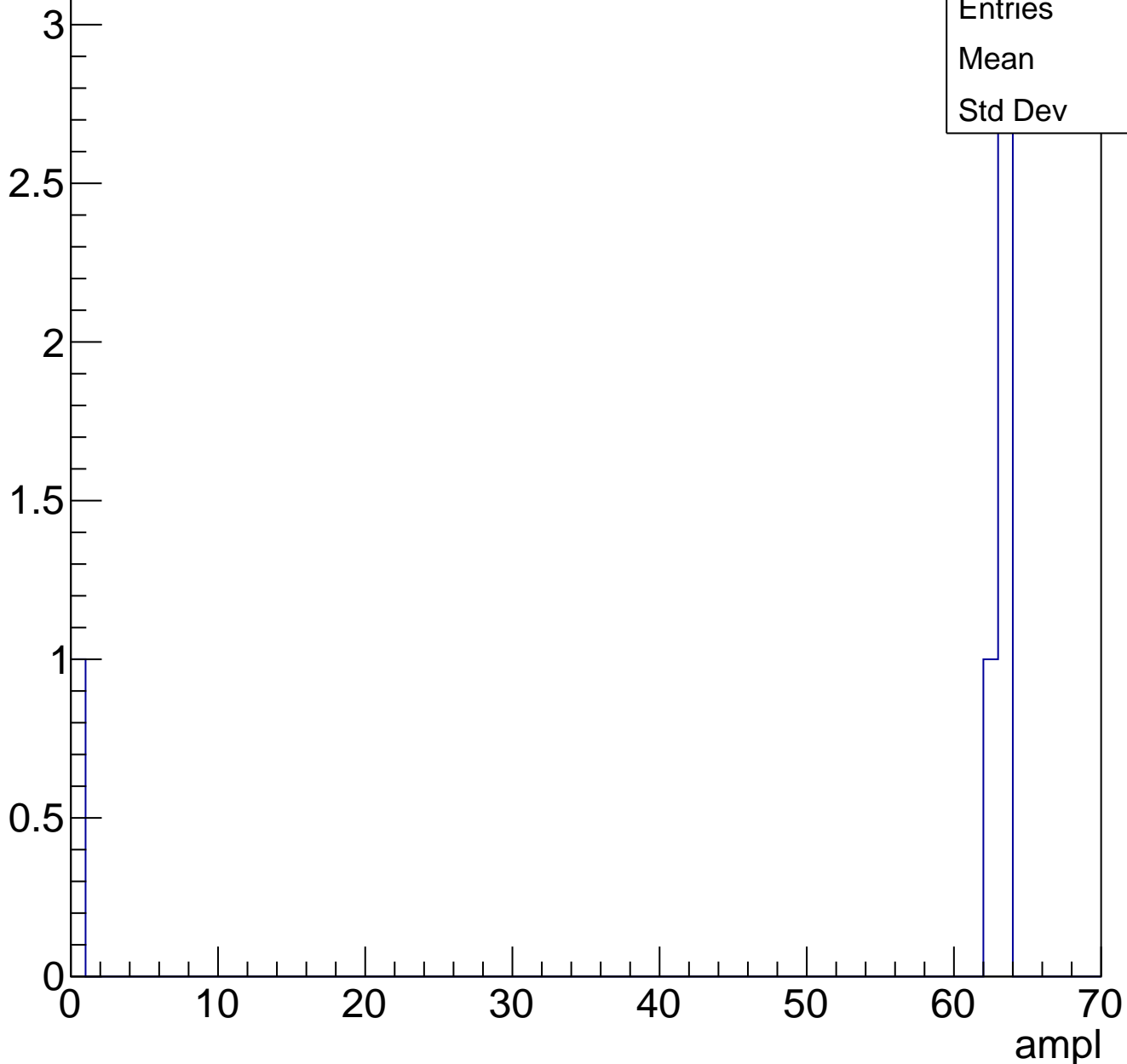




# B1L000S, U2-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	50.2
Std Dev	25.1

# B1L000S, U2-ch23, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	29.51
Std Dev	4.437

**Gaus mean : 30.4352**

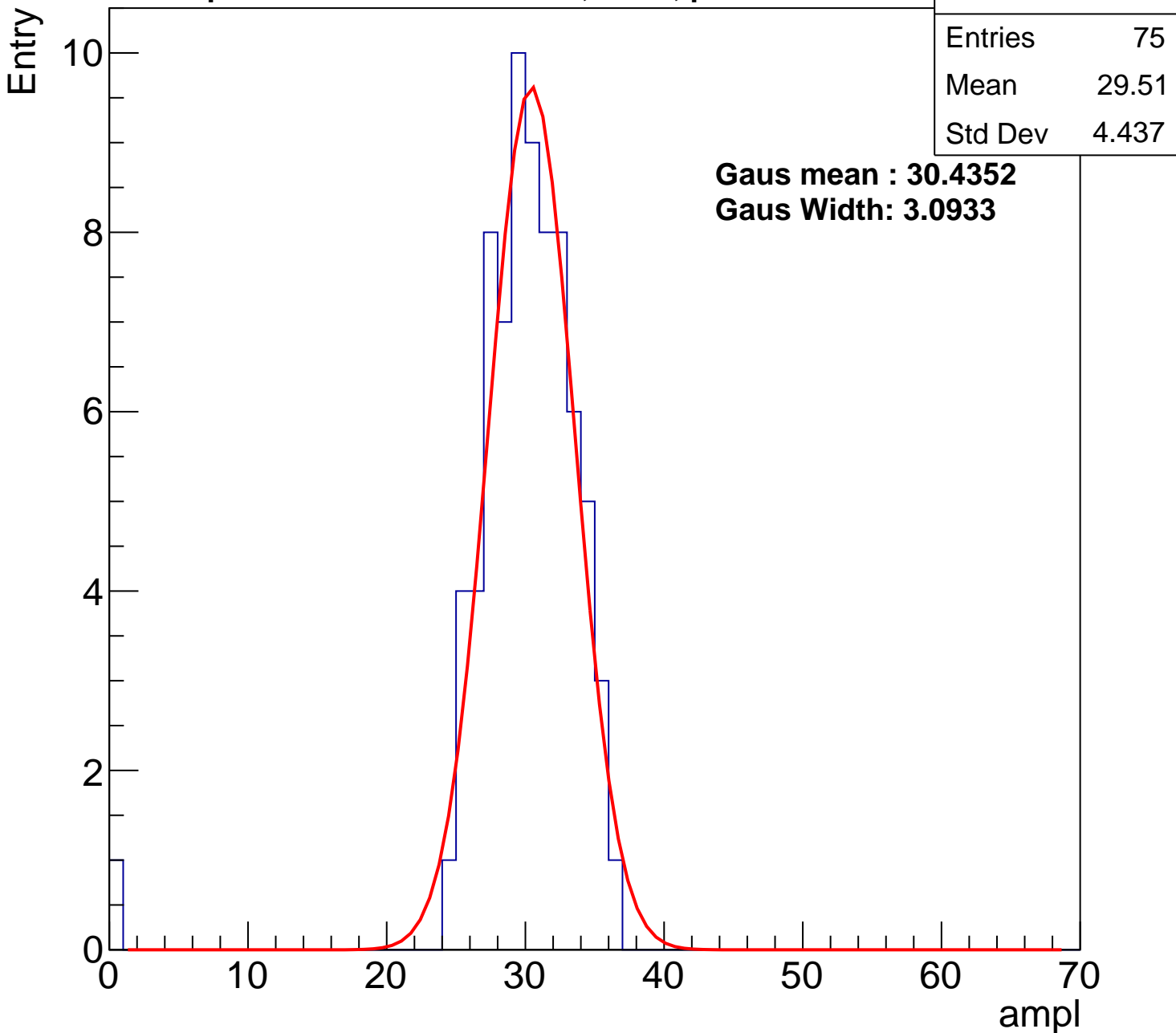
**Gaus Width: 3.0933**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch23, adc1

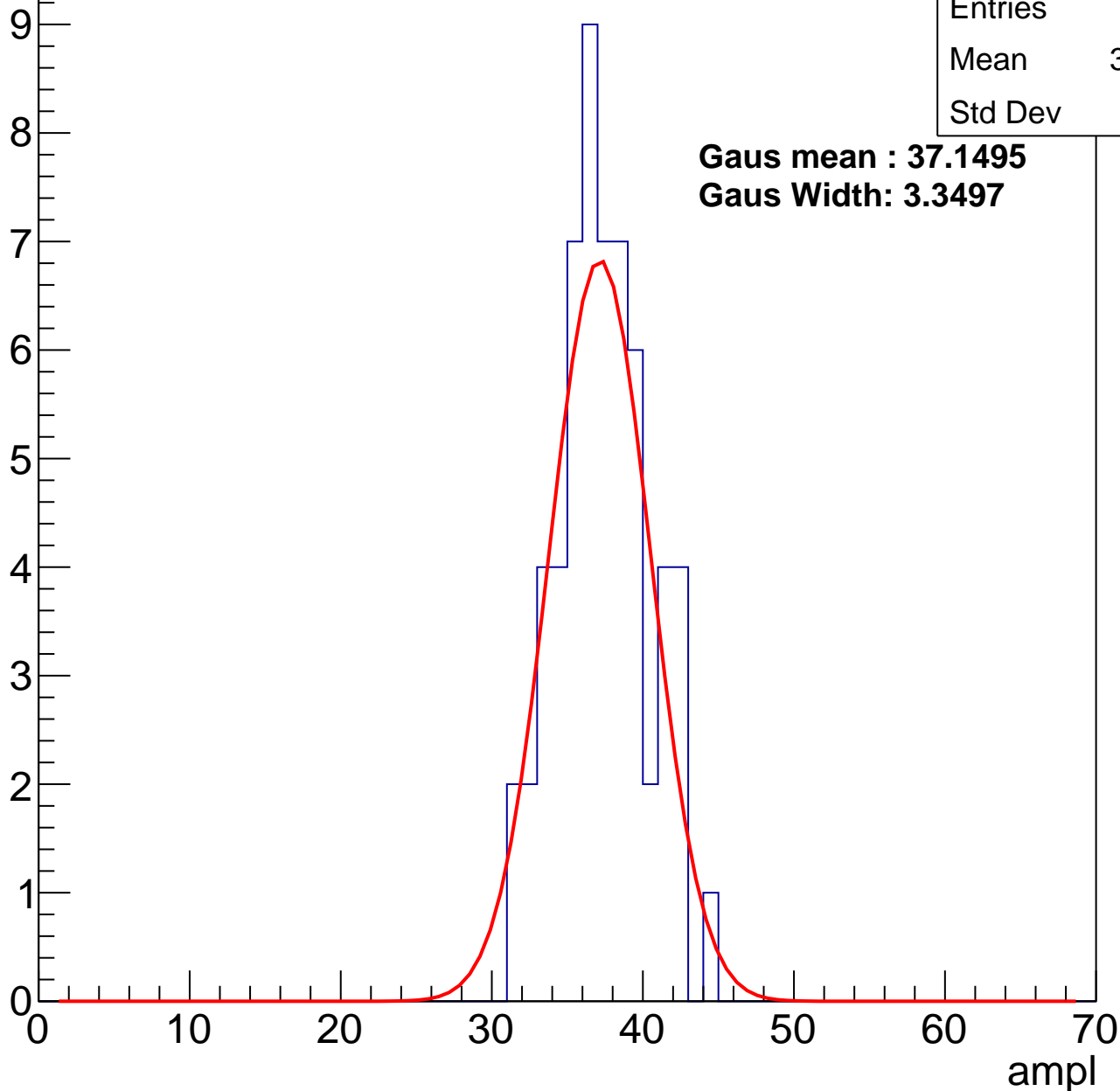
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	36.92
Std Dev	2.97

**Gaus mean : 37.1495**

**Gaus Width: 3.3497**

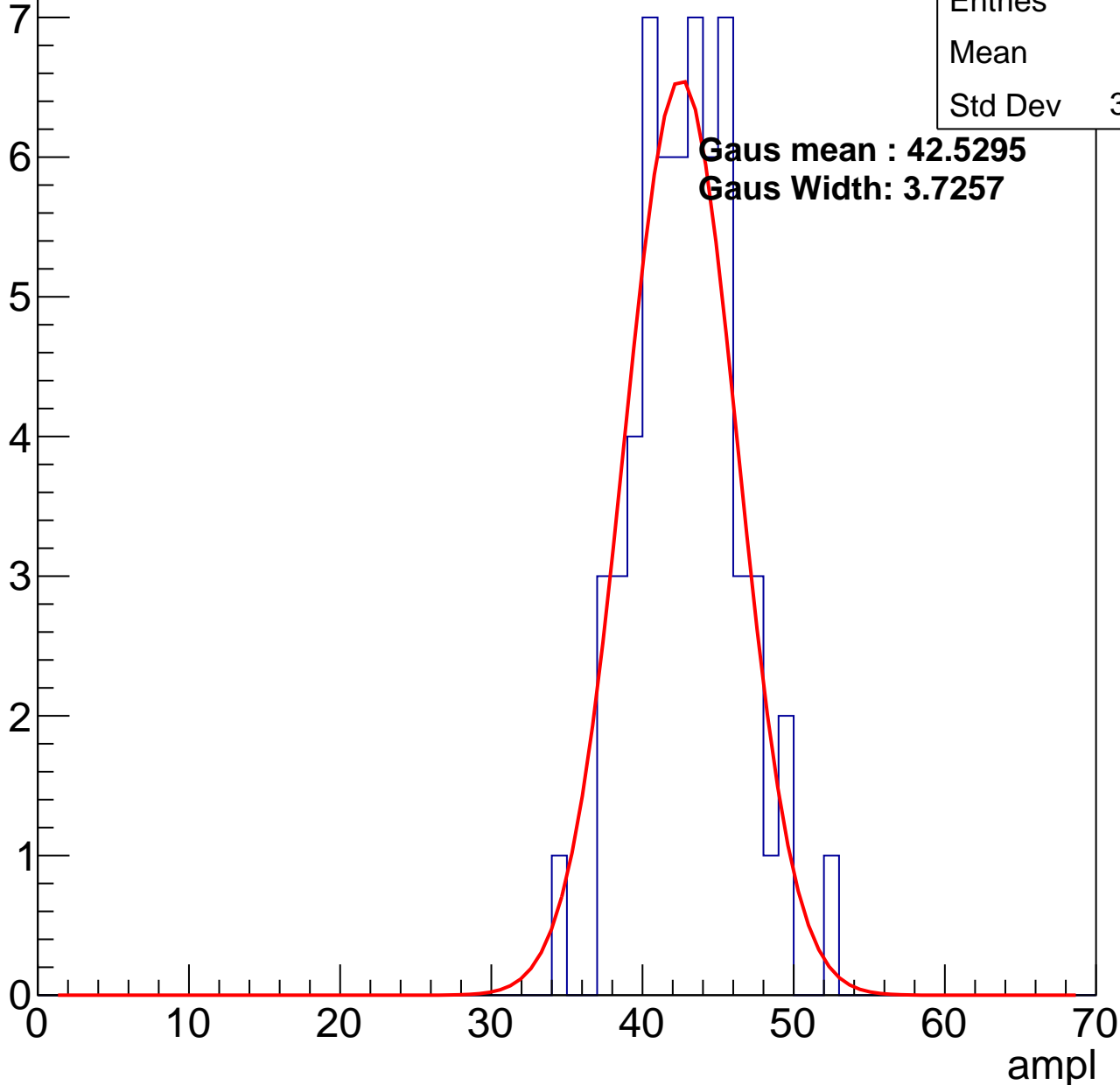


# B1L000S, U2-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.5
Std Dev	3.399

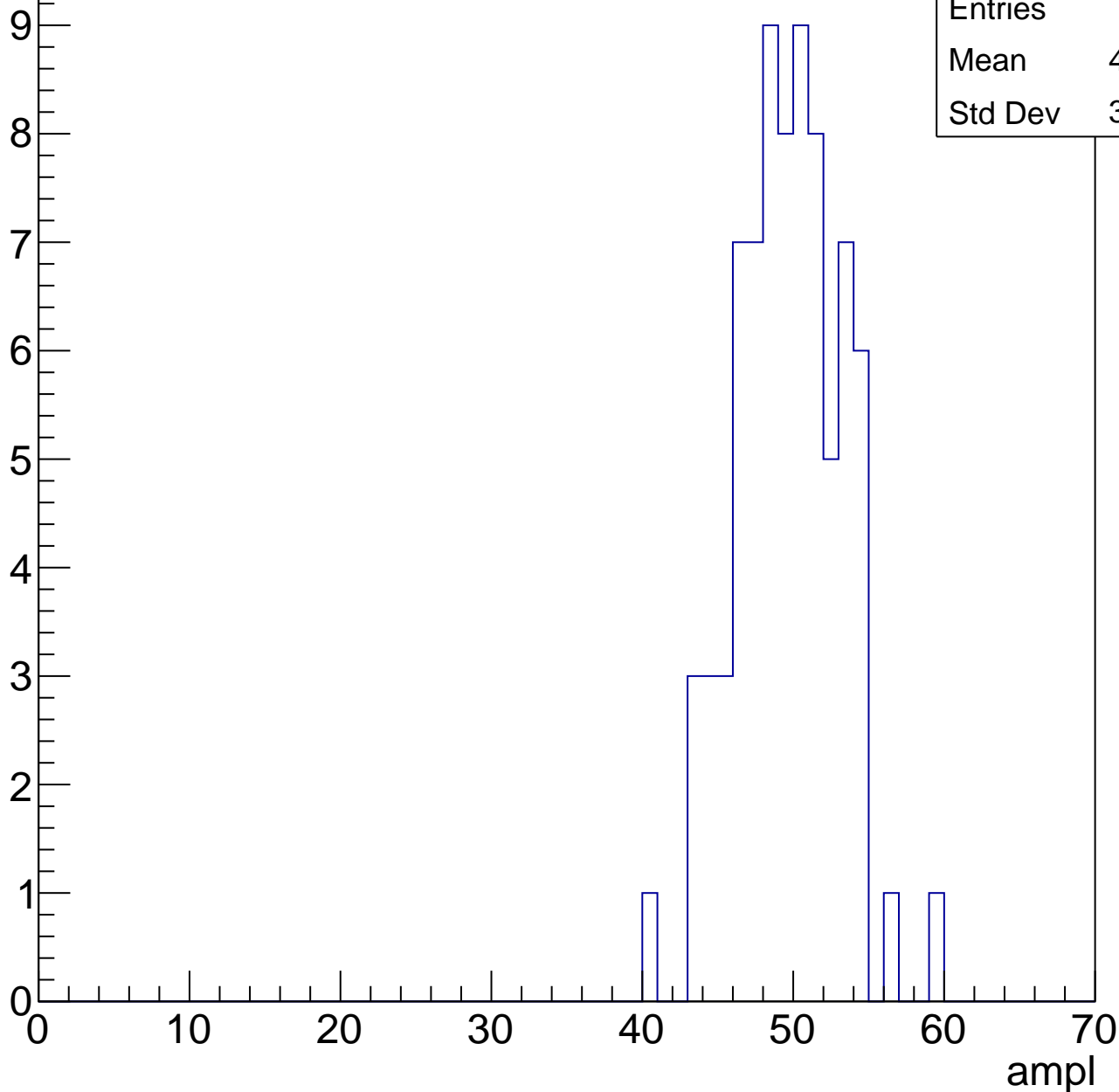


# B1L000S, U2-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	49.22
Std Dev	3.407

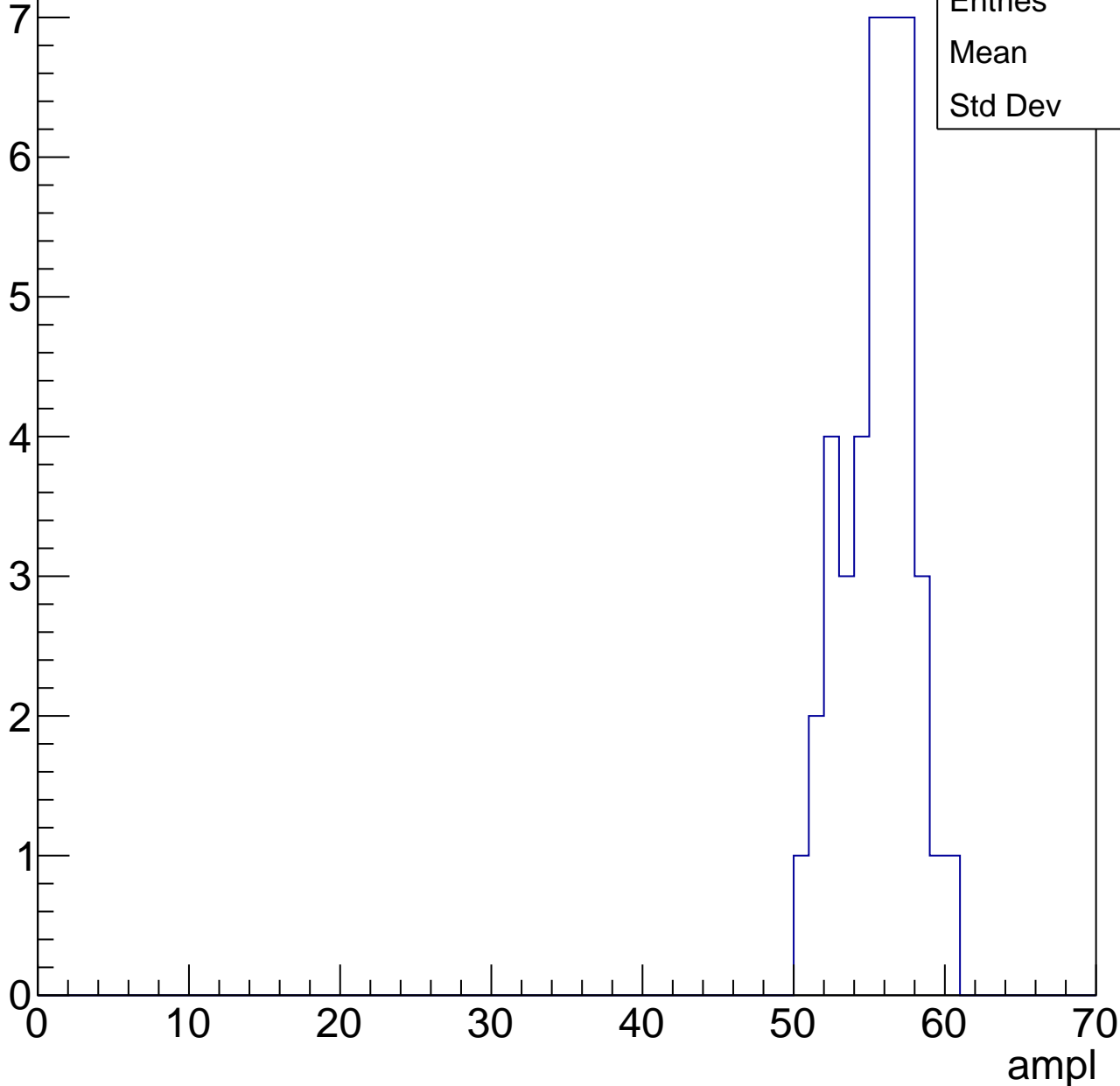


# B1L000S, U2-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

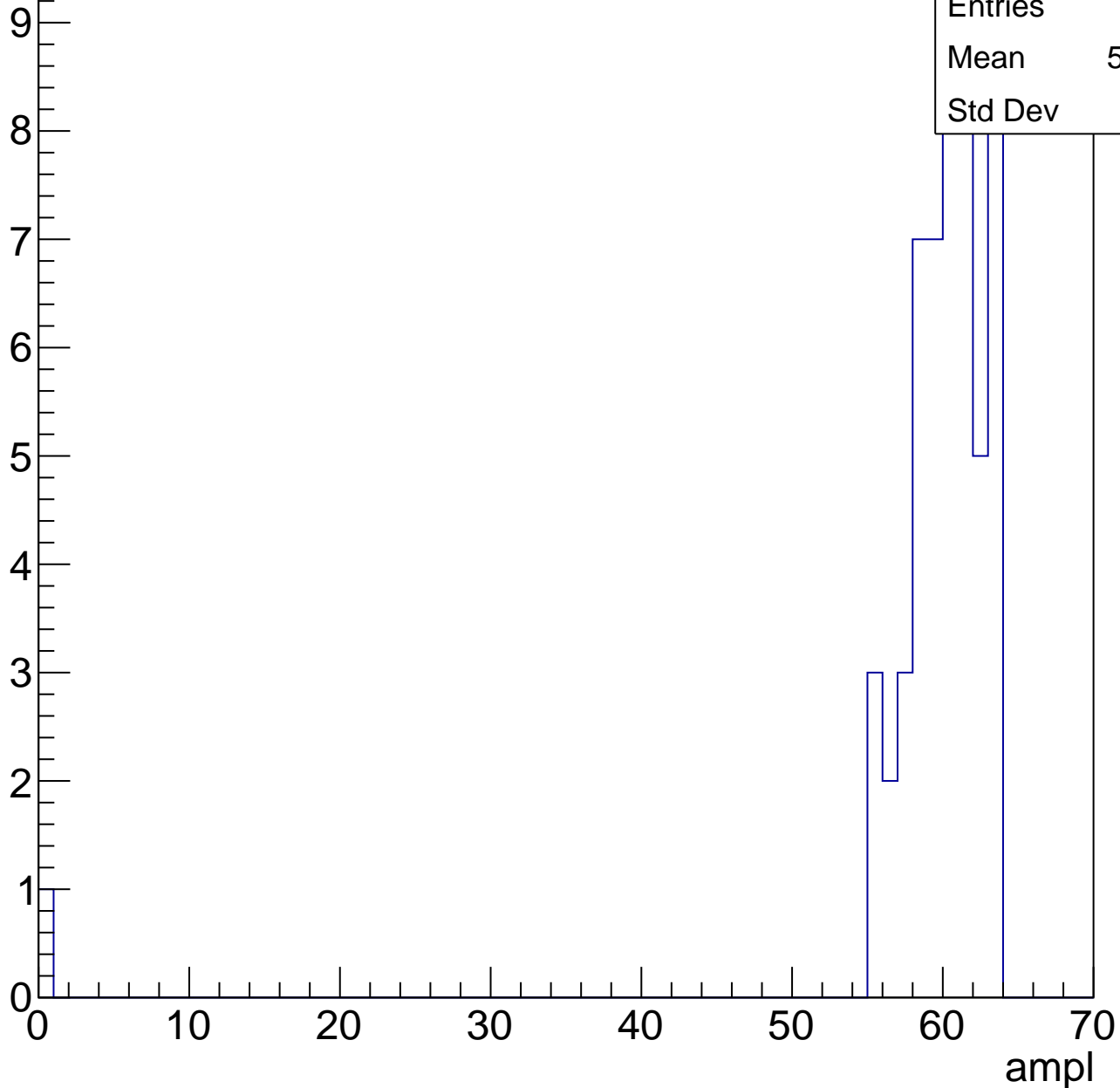
Entries	40
Mean	55.1
Std Dev	2.3



# B1L000S, U2-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

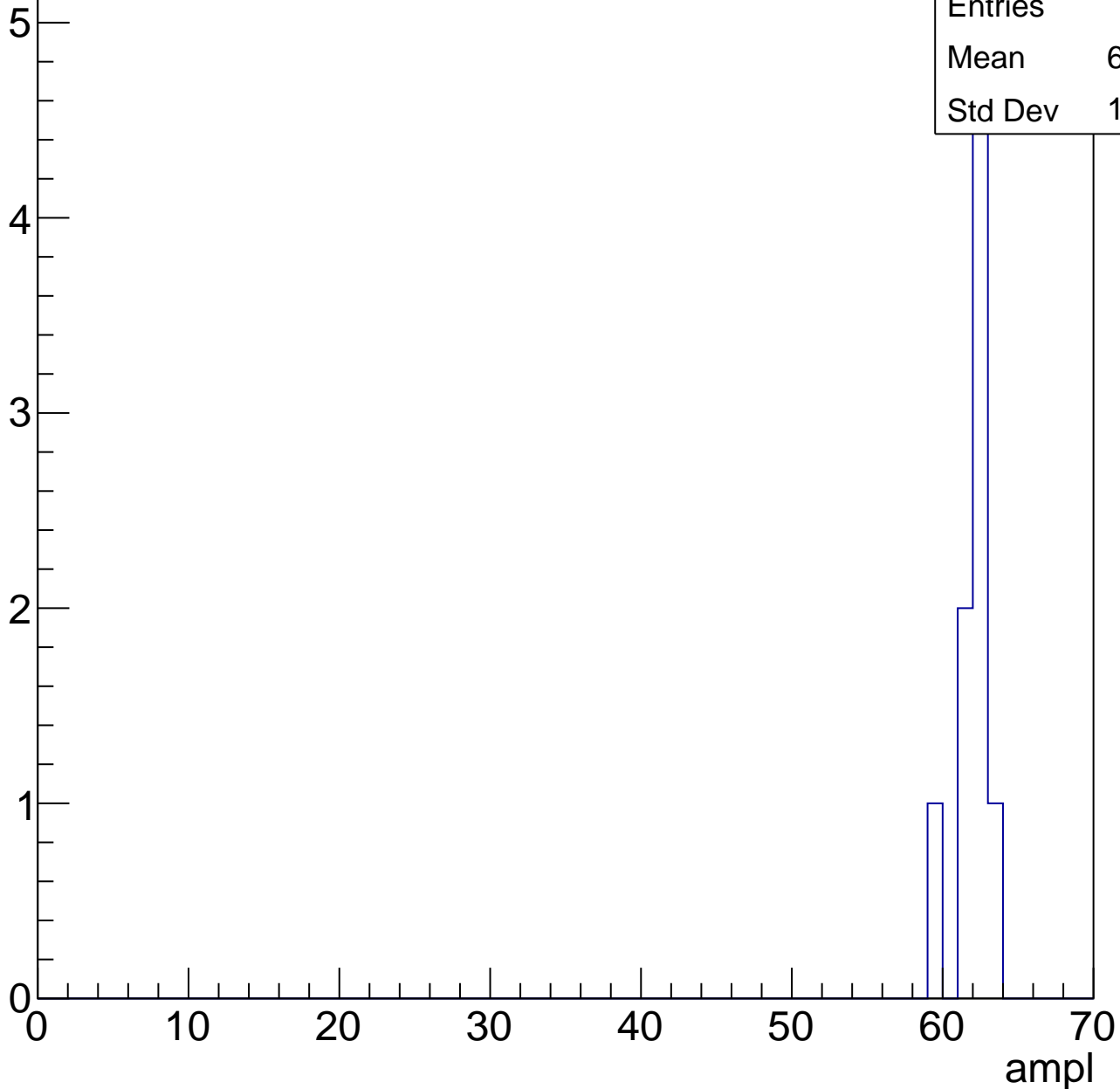


# B1L000S, U2-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	9
Mean	61.56
Std Dev	1.066





# B1L000S, U2-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch24, adc0

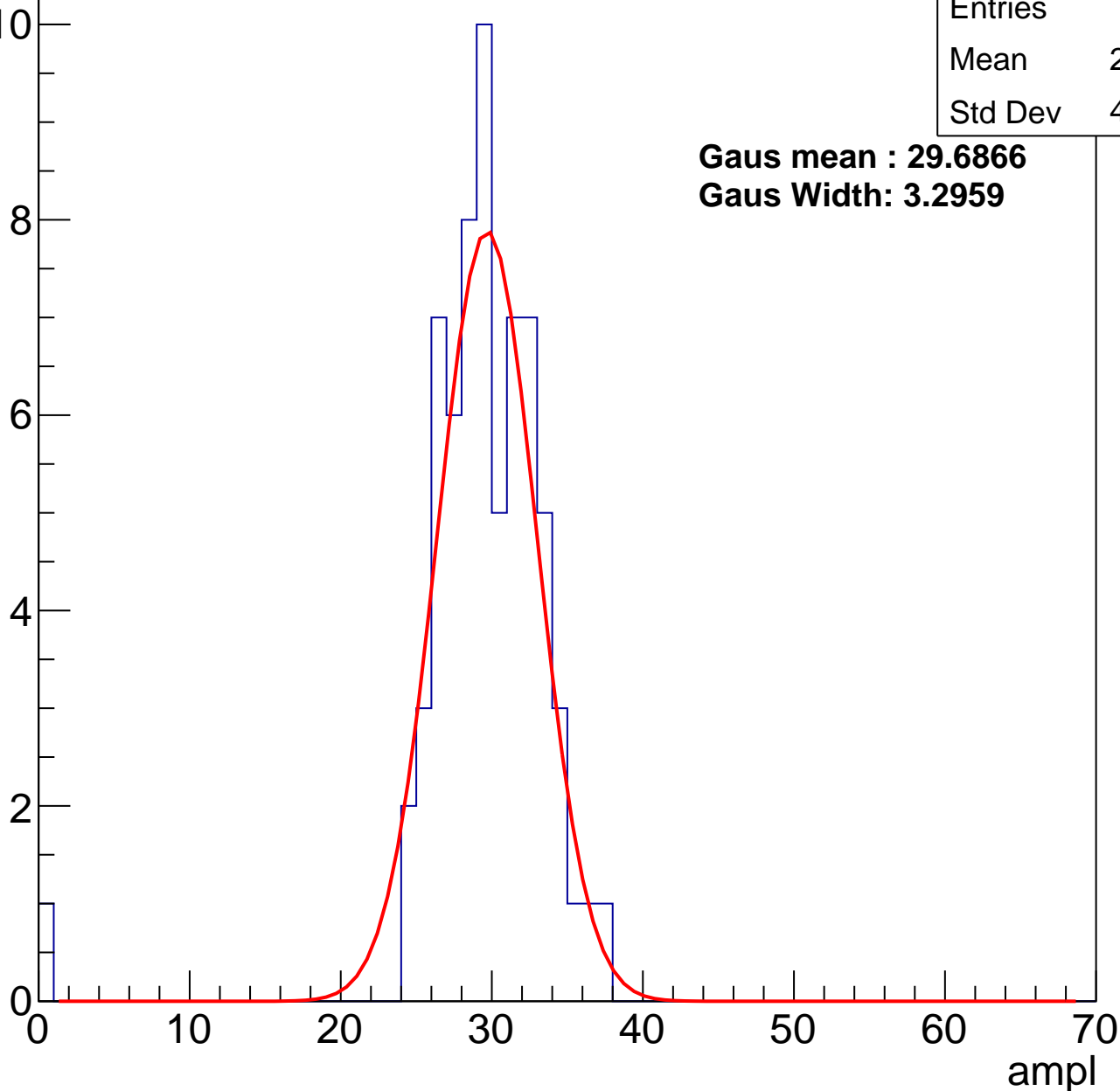
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.06
Std Dev	4.626

**Gaus mean : 29.6866**

**Gaus Width: 3.2959**



# B1L000S, U2-ch24, adc1

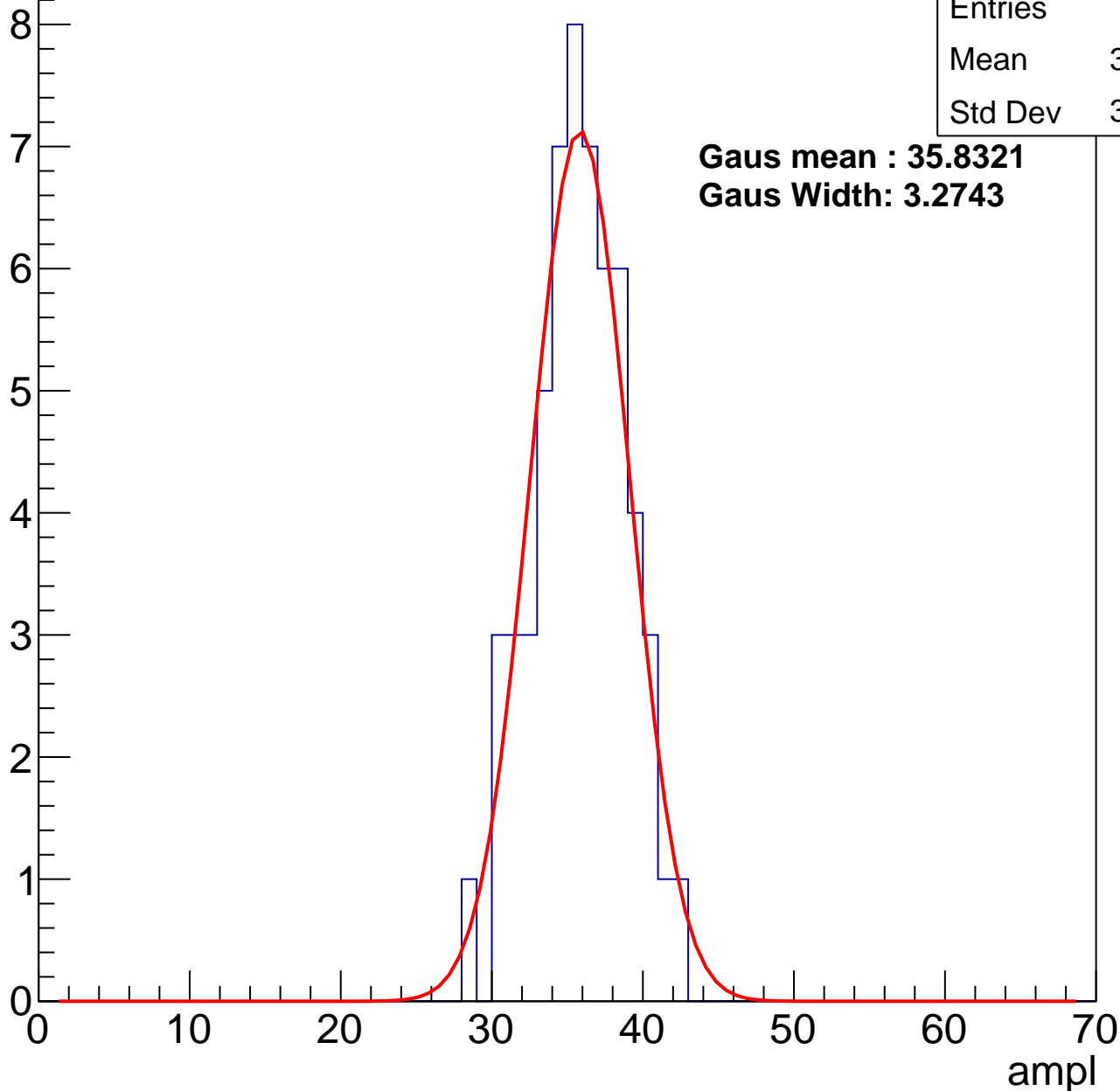
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.36
Std Dev	3.015

**Gaus mean : 35.8321**

**Gaus Width: 3.2743**



# B1L000S, U2-ch24, adc2

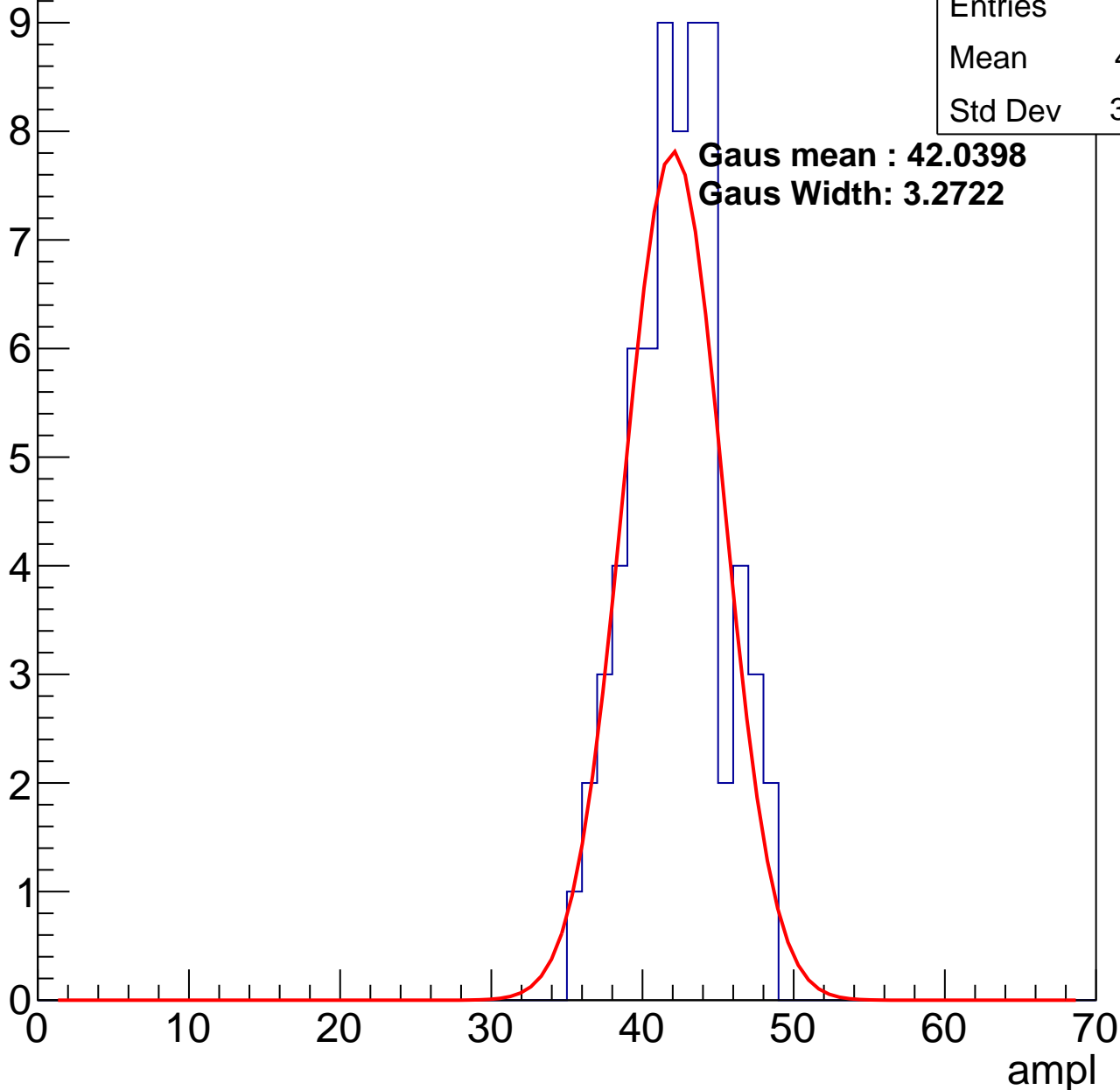
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	41.81
Std Dev	3.016

**Gaus mean : 42.0398**

**Gaus Width: 3.2722**

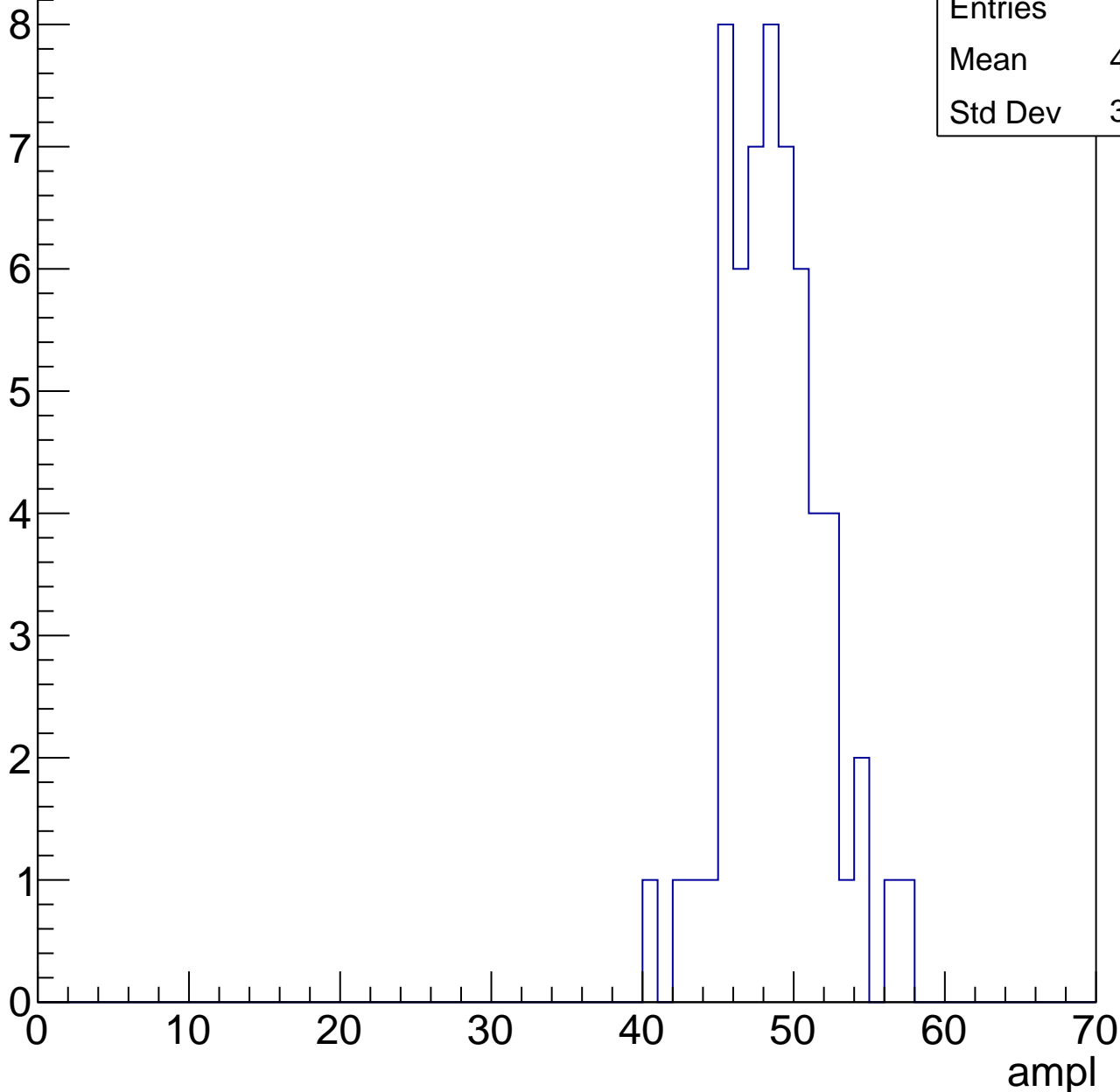


# B1L000S, U2-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

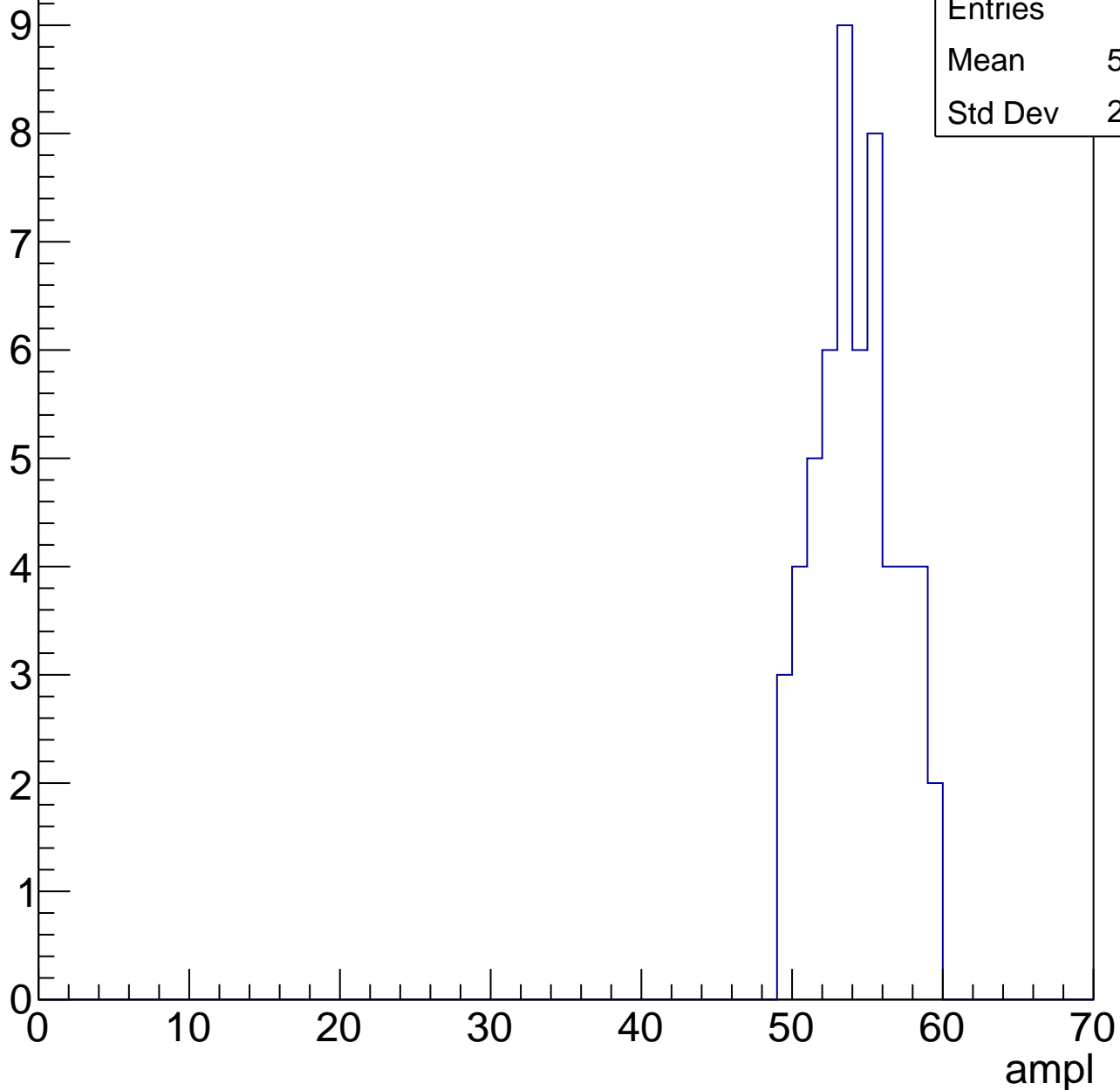
Entries	59
Mean	48.25
Std Dev	3.224



# B1L000S, U2-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

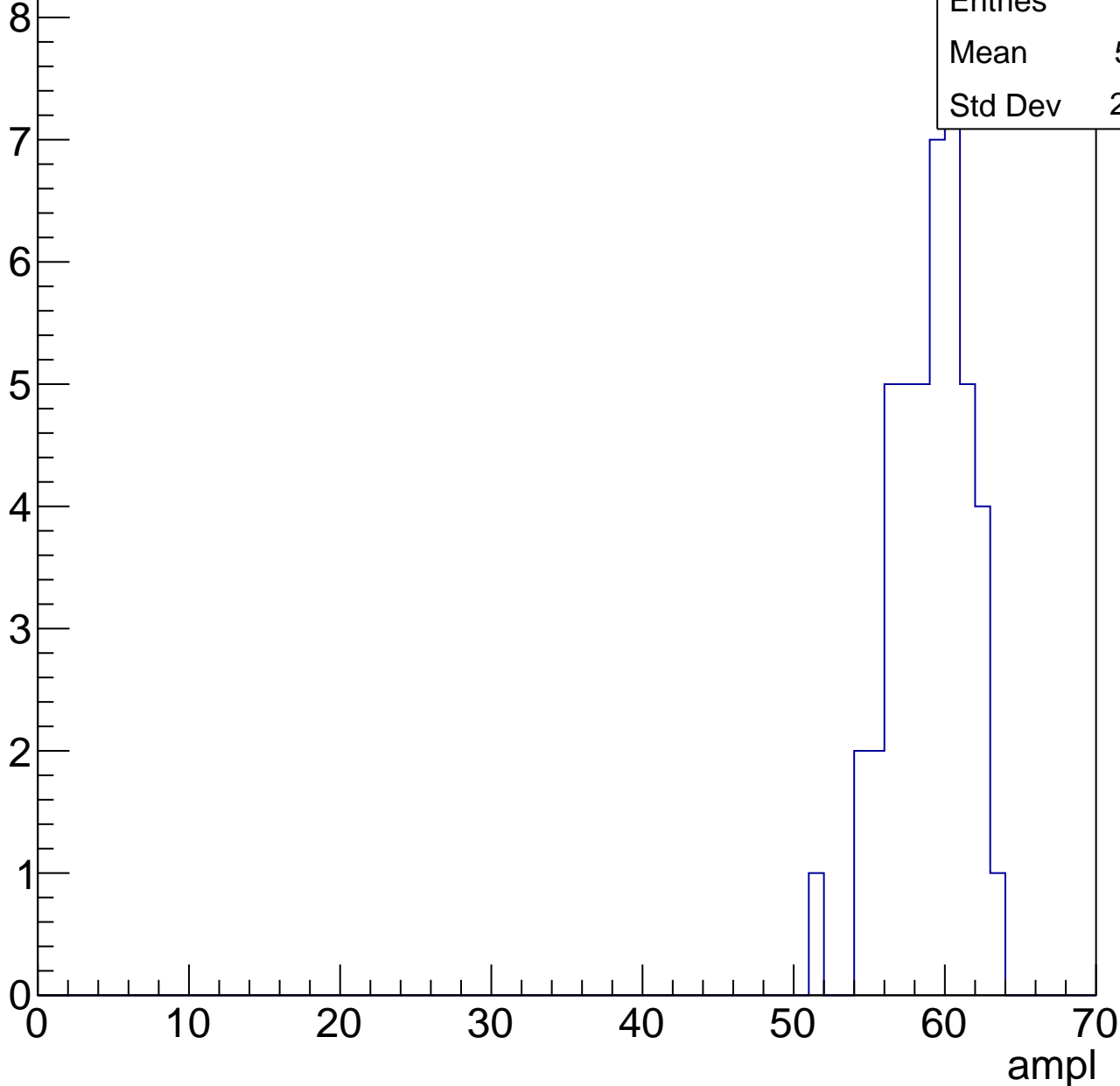


# B1L000S, U2-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	58.51
Std Dev	2.518

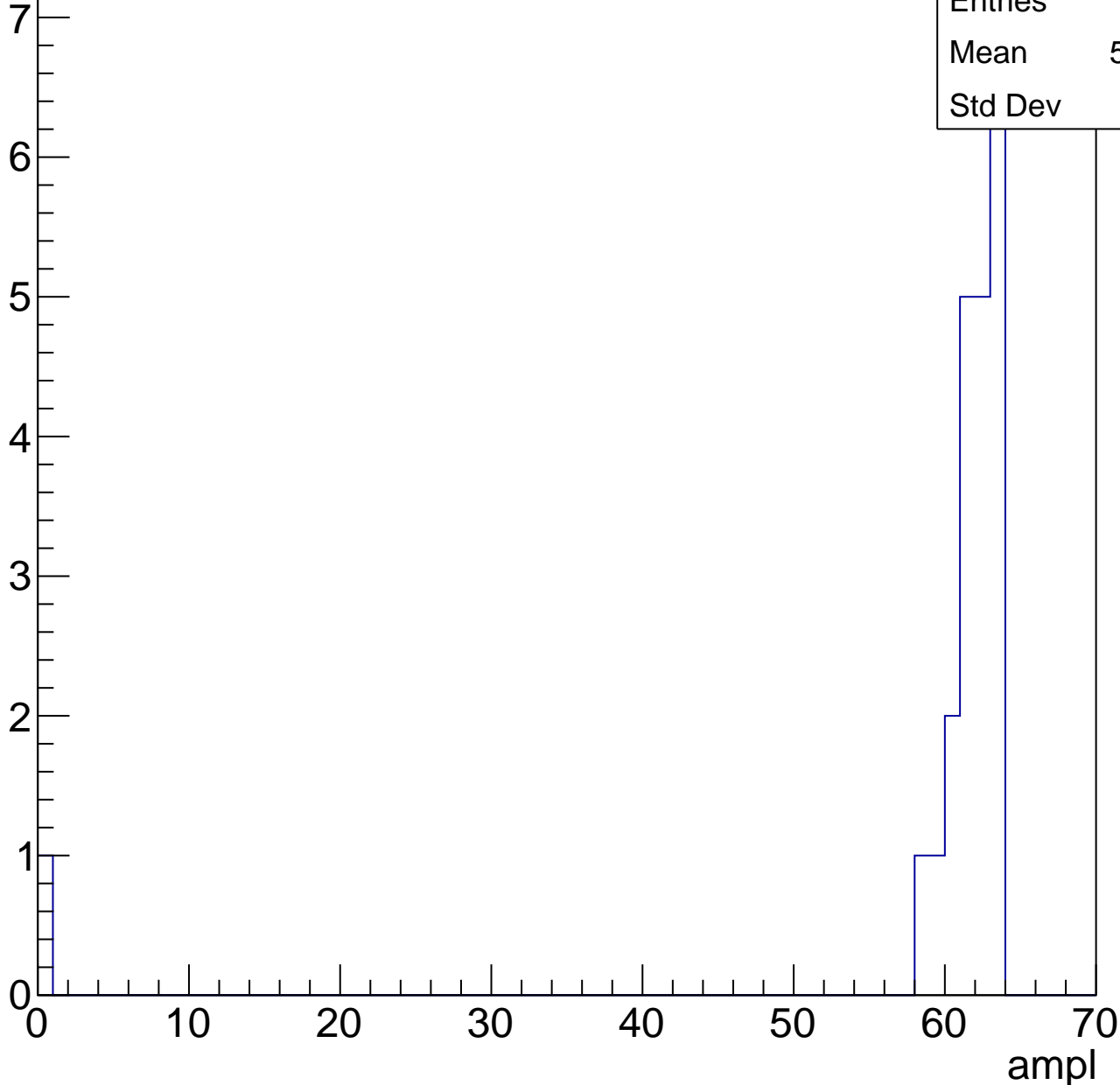


# B1L000S, U2-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.77
Std Dev	12.9

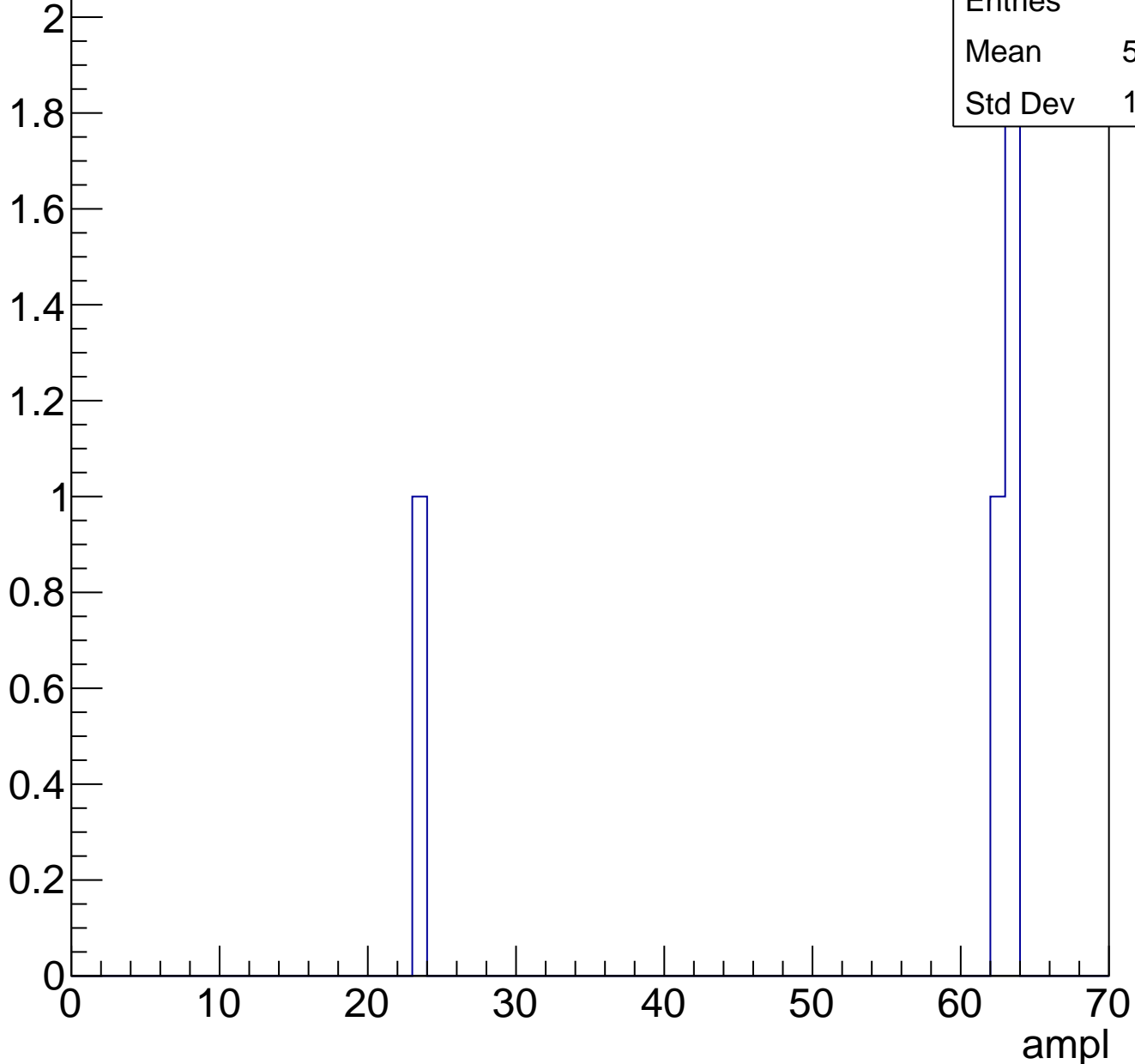




# B1L000S, U2-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	52.75
Std Dev	17.18

# B1L000S, U2-ch25, adc0

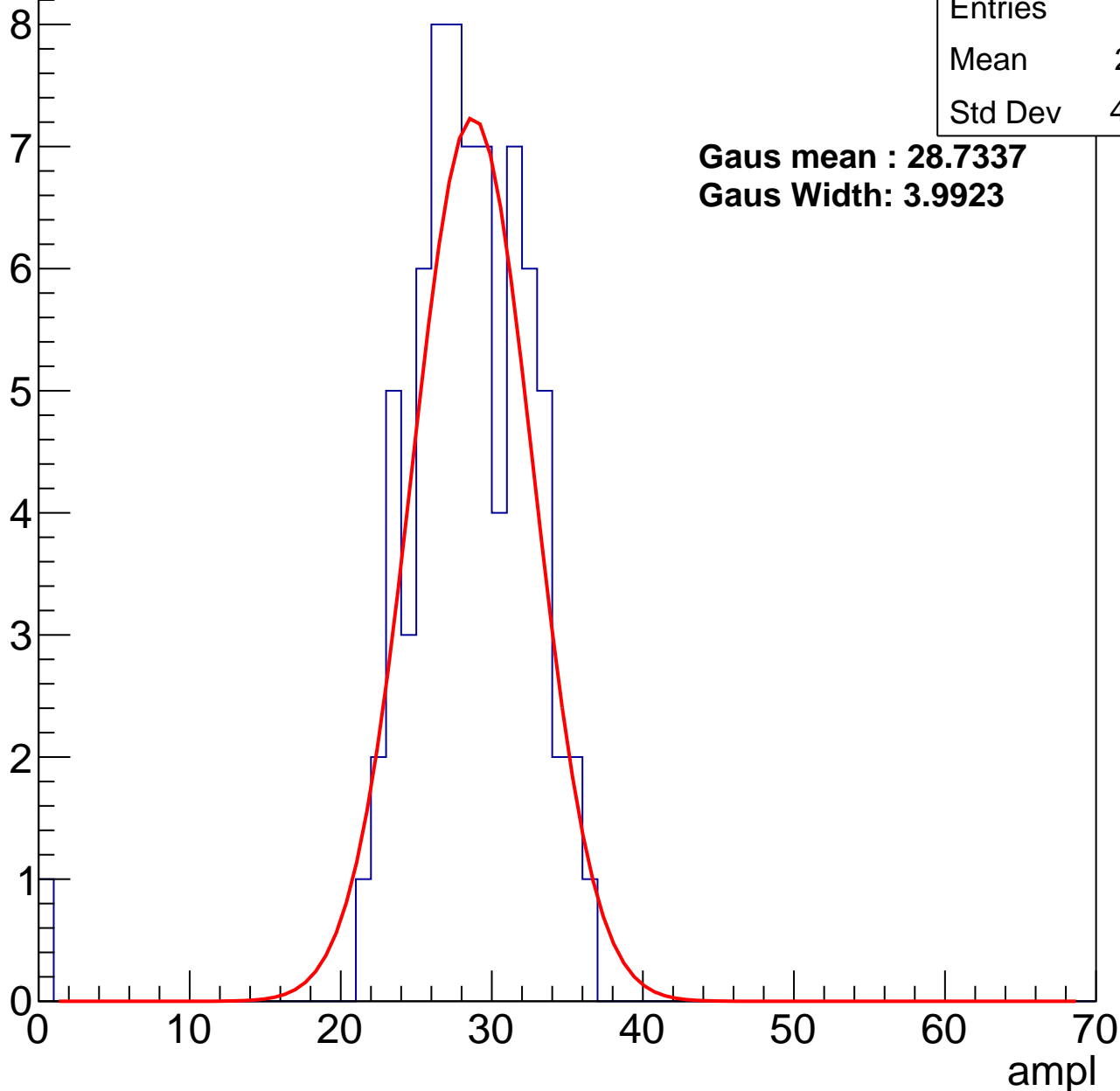
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	27.91
Std Dev	4.778

**Gaus mean : 28.7337**

**Gaus Width: 3.9923**



# B1L000S, U2-ch25, adc1

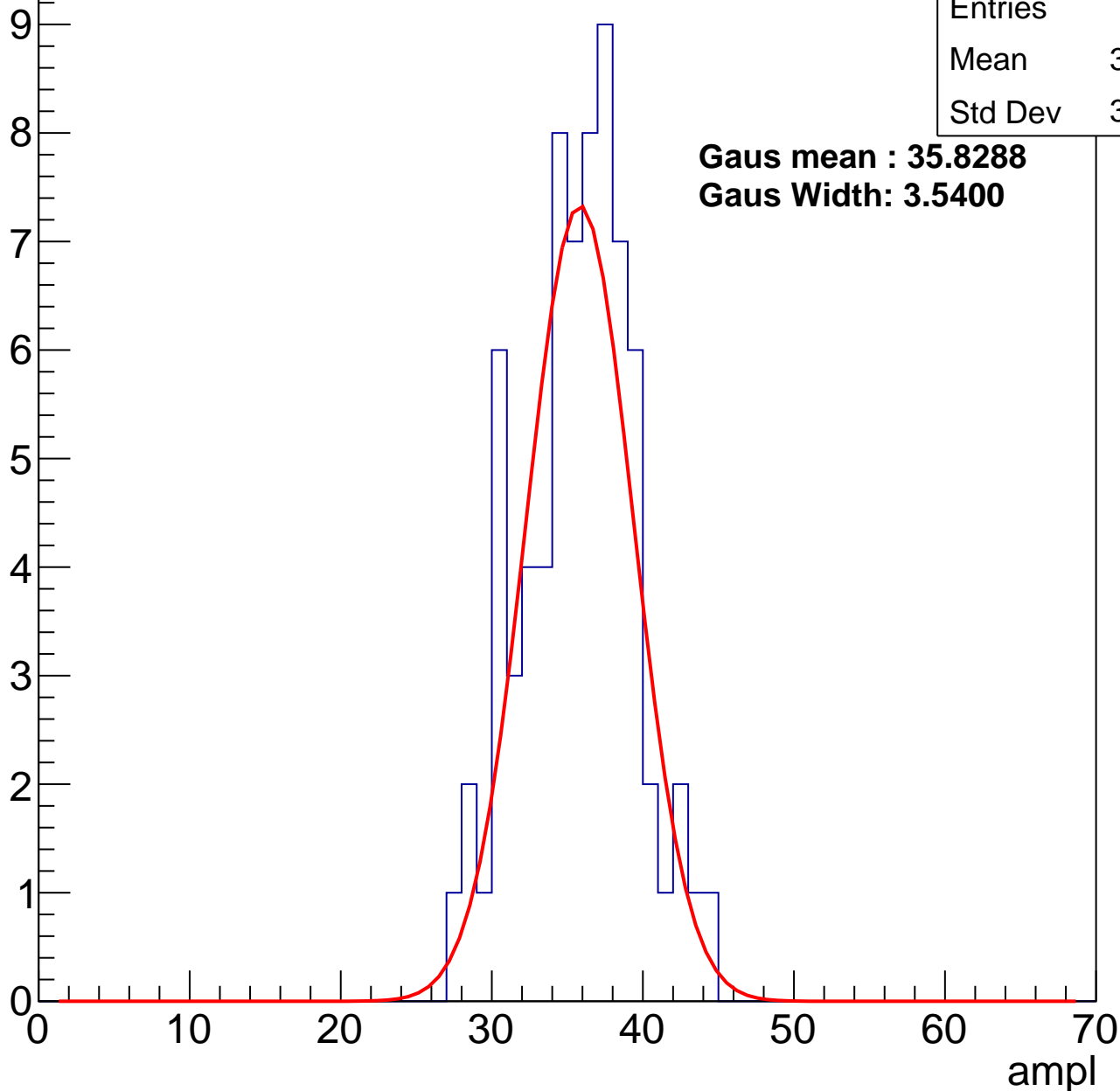
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	35.27
Std Dev	3.669

**Gaus mean : 35.8288**

**Gaus Width: 3.5400**



# B1L000S, U2-ch25, adc2

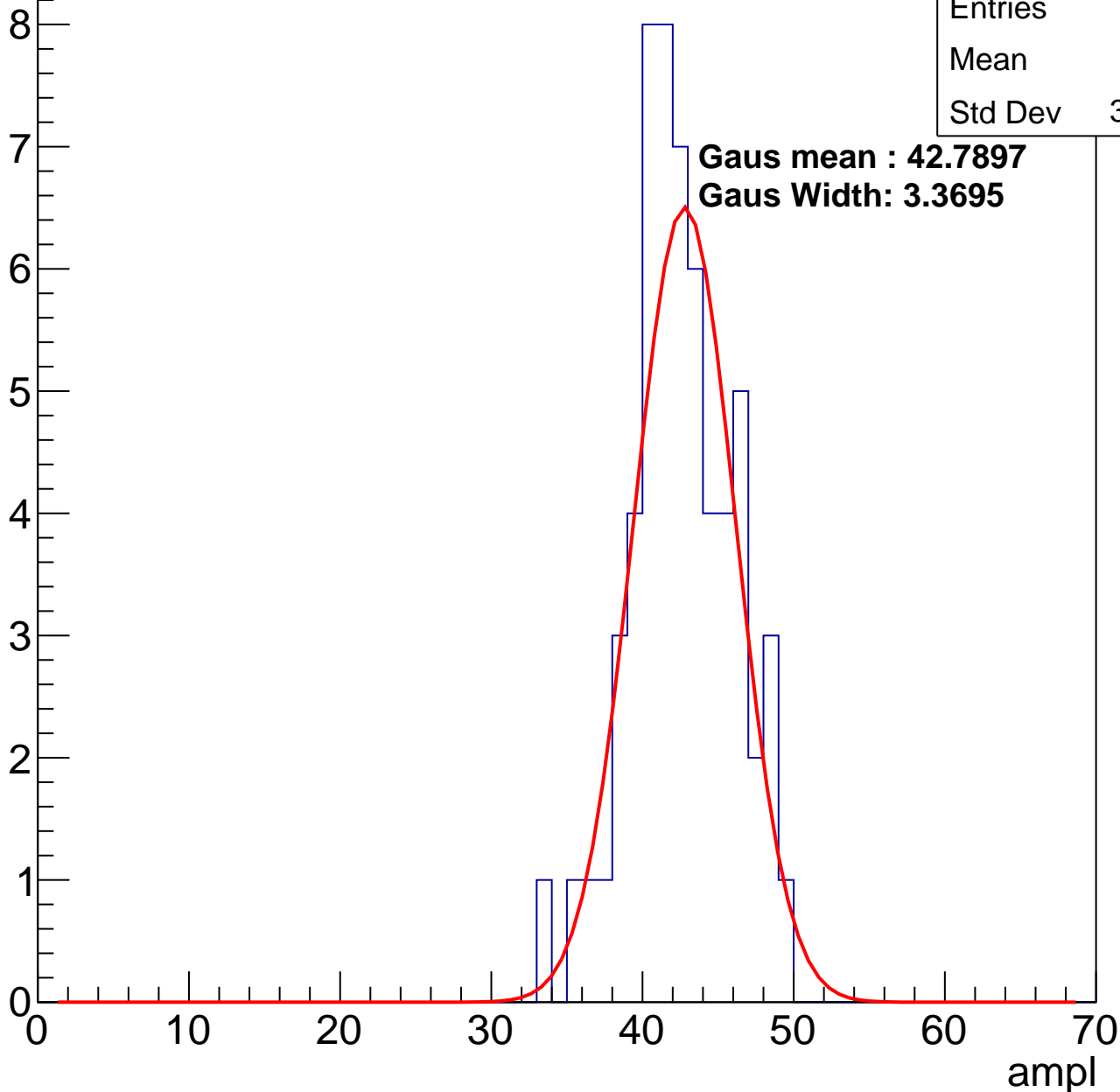
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.1
Std Dev	3.343

**Gaus mean : 42.7897**

**Gaus Width: 3.3695**

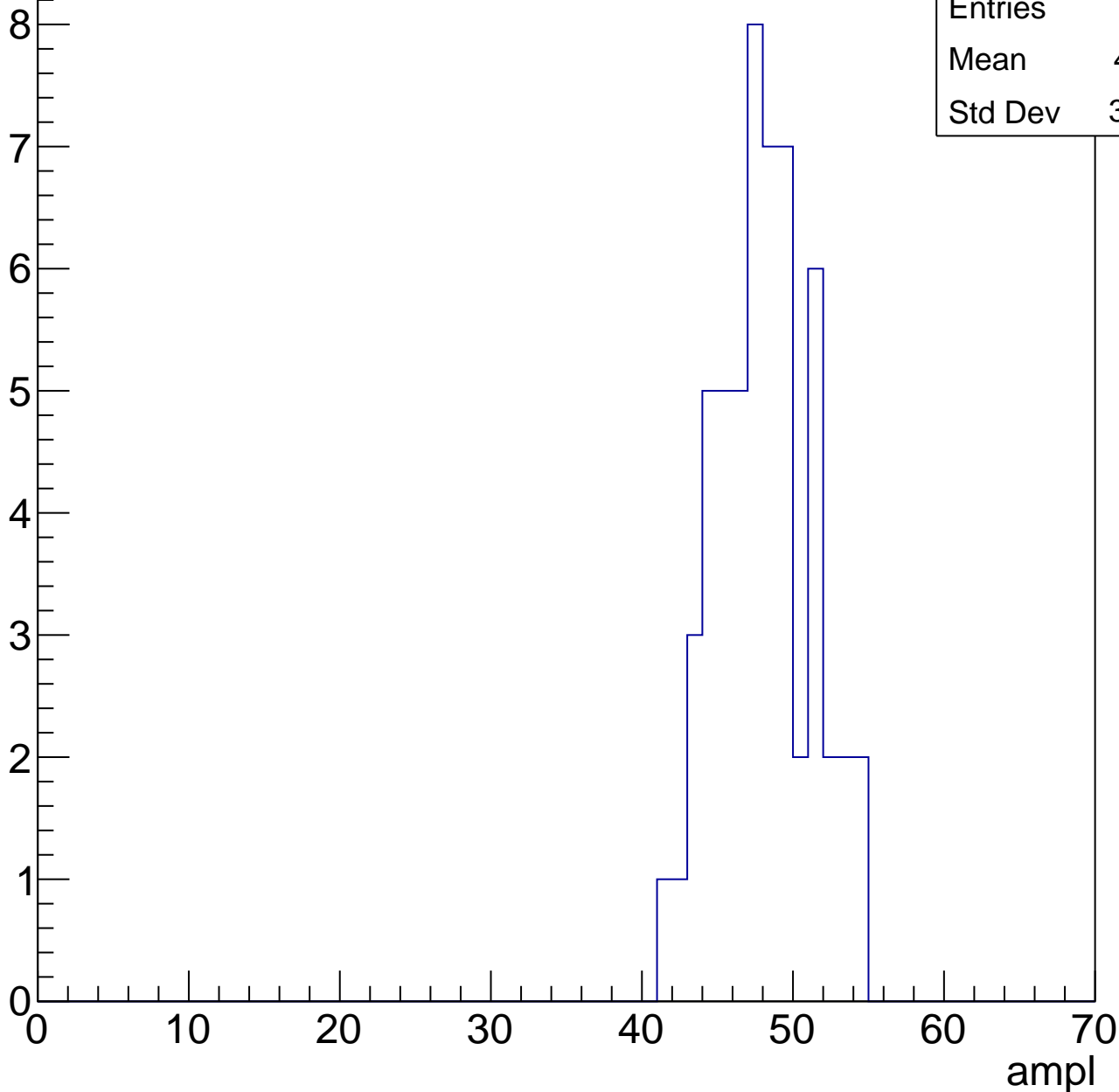


# B1L000S, U2-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

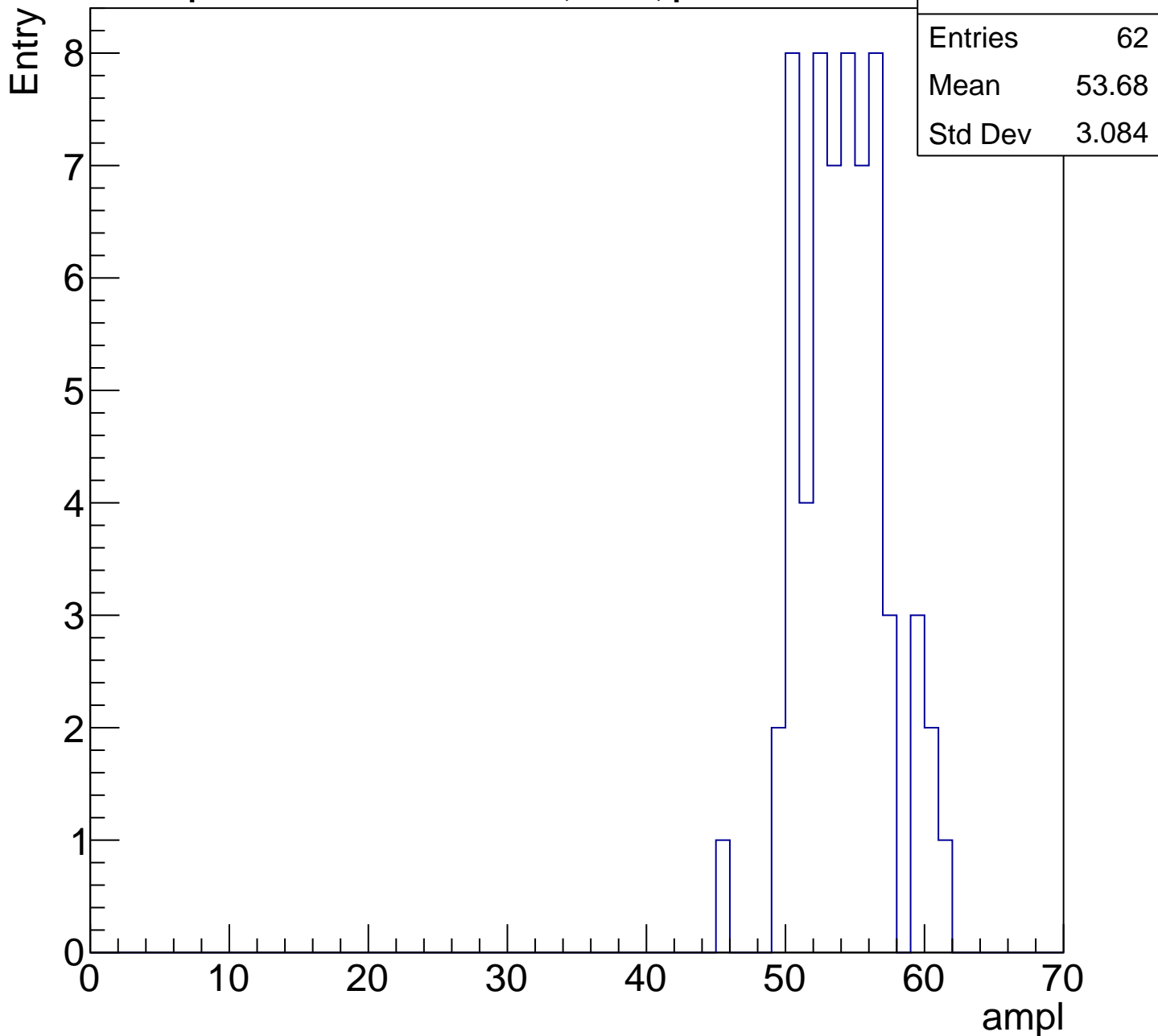
Entry

Entries	56
Mean	47.61
Std Dev	3.069



# B1L000S, U2-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch25, adc5

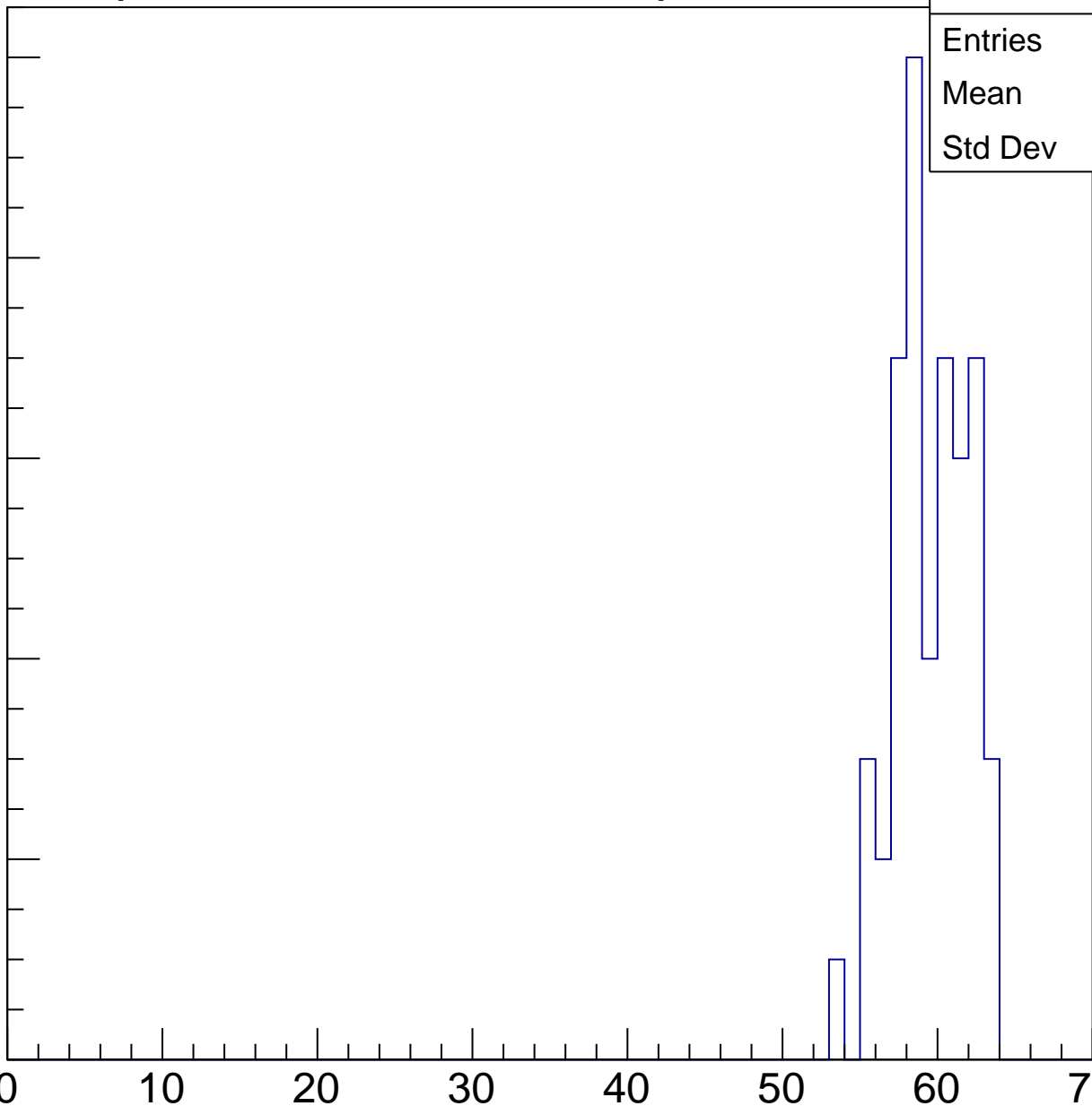
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10  
8  
6  
4  
2  
0

Entries	50
Mean	59.08
Std Dev	2.374

ampl

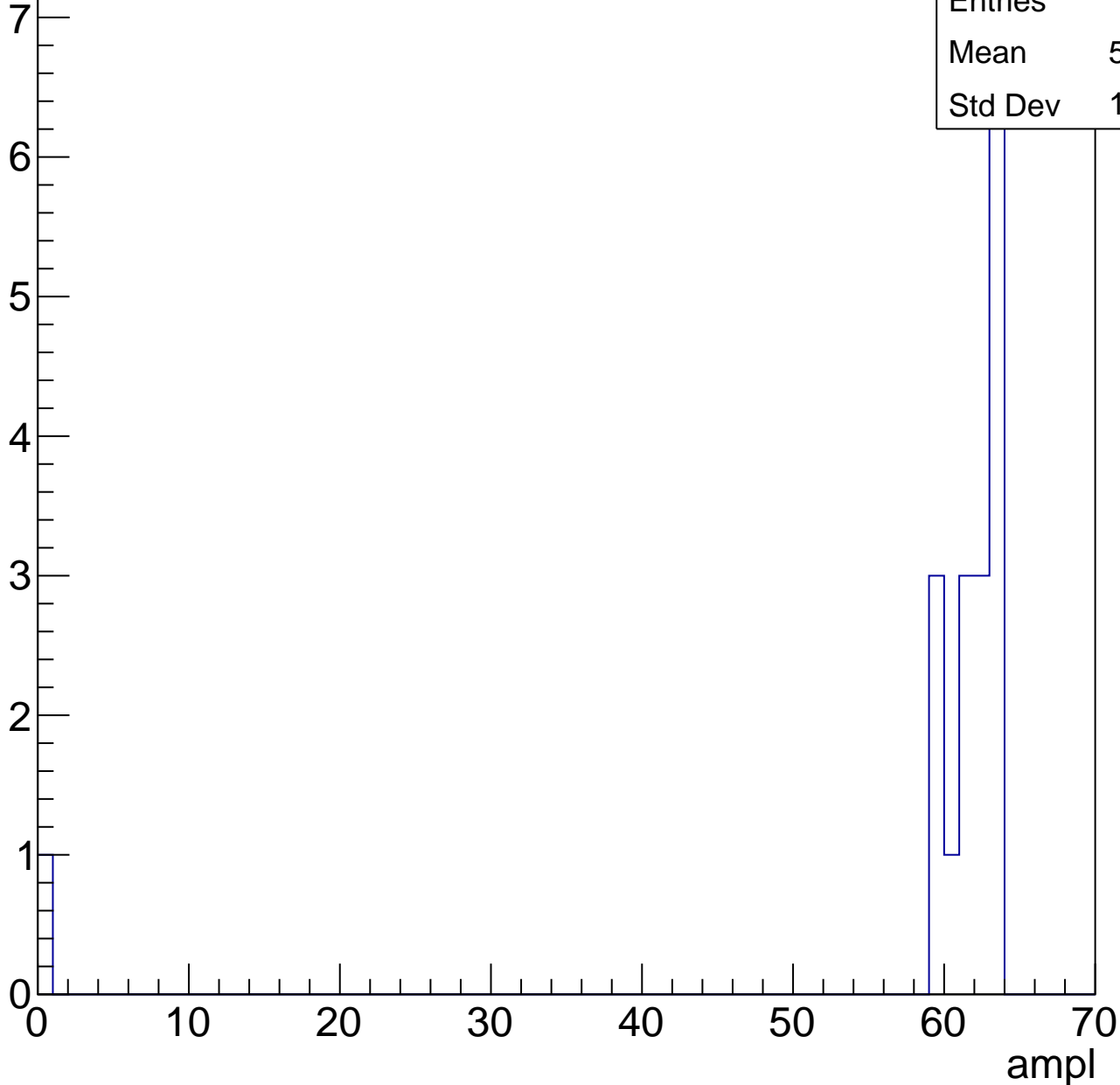


# B1L000S, U2-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.17
Std Dev	14.18





# B1L000S, U2-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B1L000S, U2-ch26, adc0

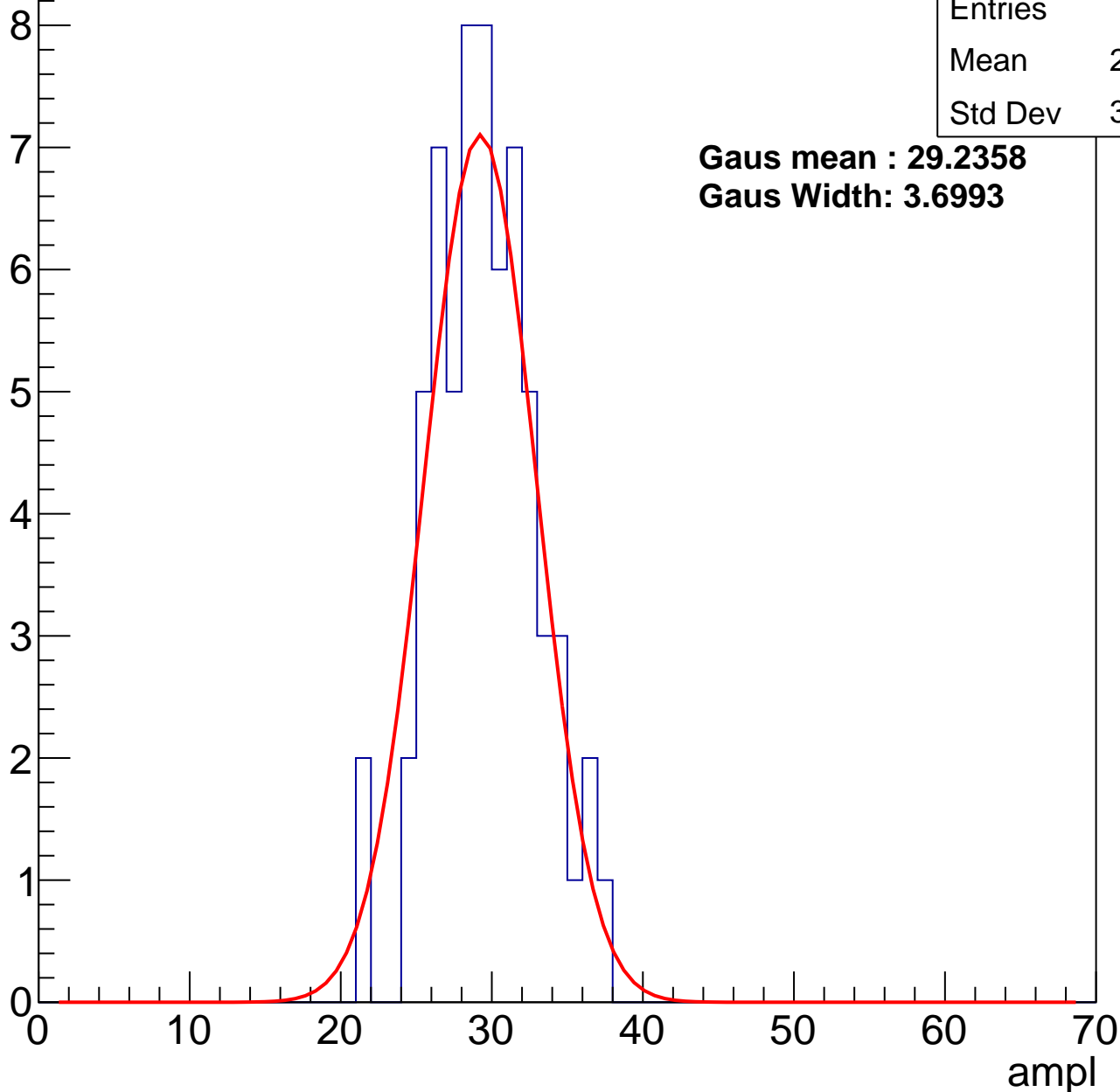
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	29.08
Std Dev	3.394

**Gaus mean : 29.2358**

**Gaus Width: 3.6993**



# B1L000S, U2-ch26, adc1

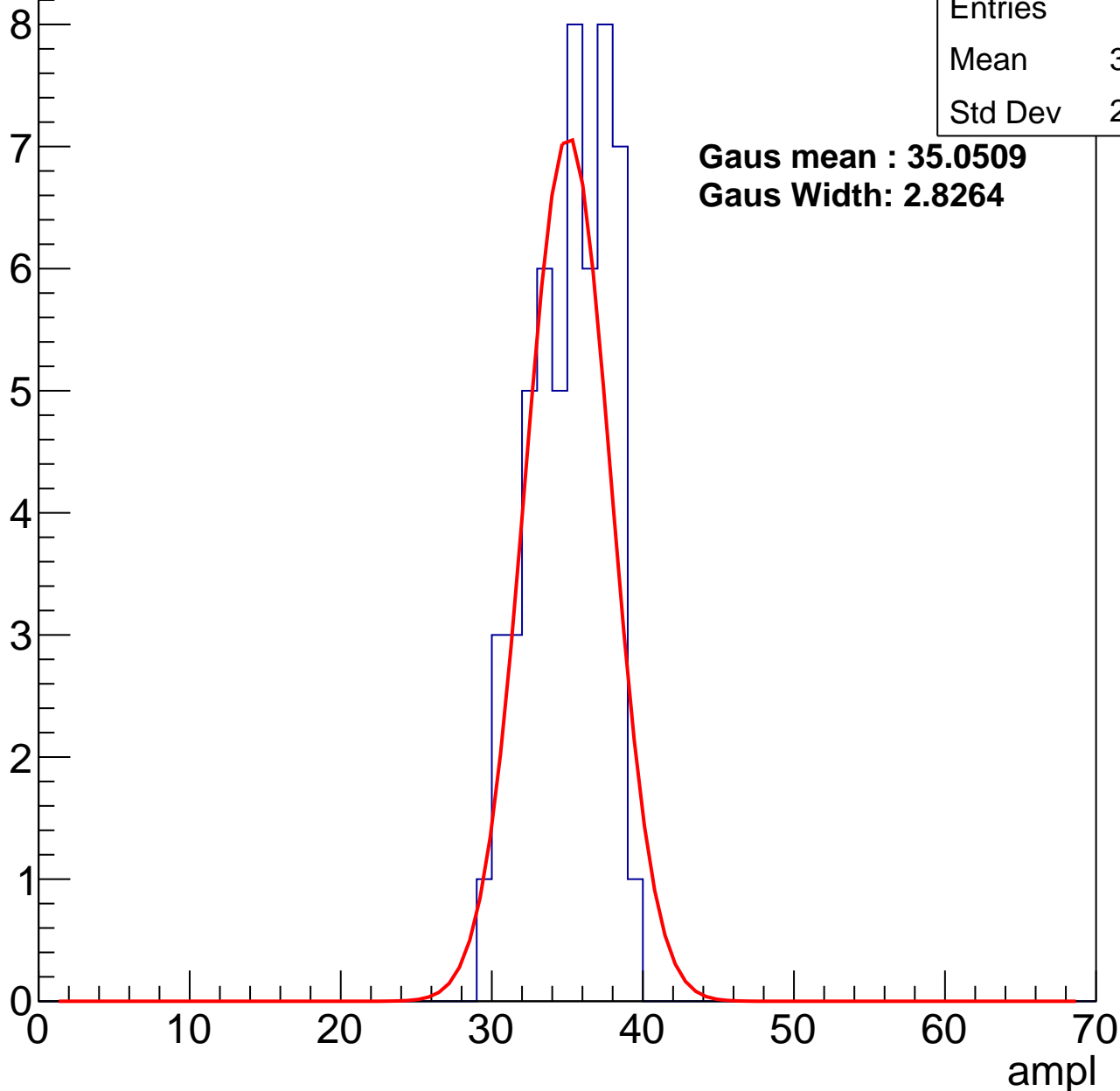
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	34.66
Std Dev	2.547

**Gaus mean : 35.0509**

**Gaus Width: 2.8264**



# B1L000S, U2-ch26, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	40.73
Std Dev	2.956

**Gaus mean : 41.1653**  
**Gaus Width: 1.8378**

Entry

10

8

6

4

2

0

0

10

20

30

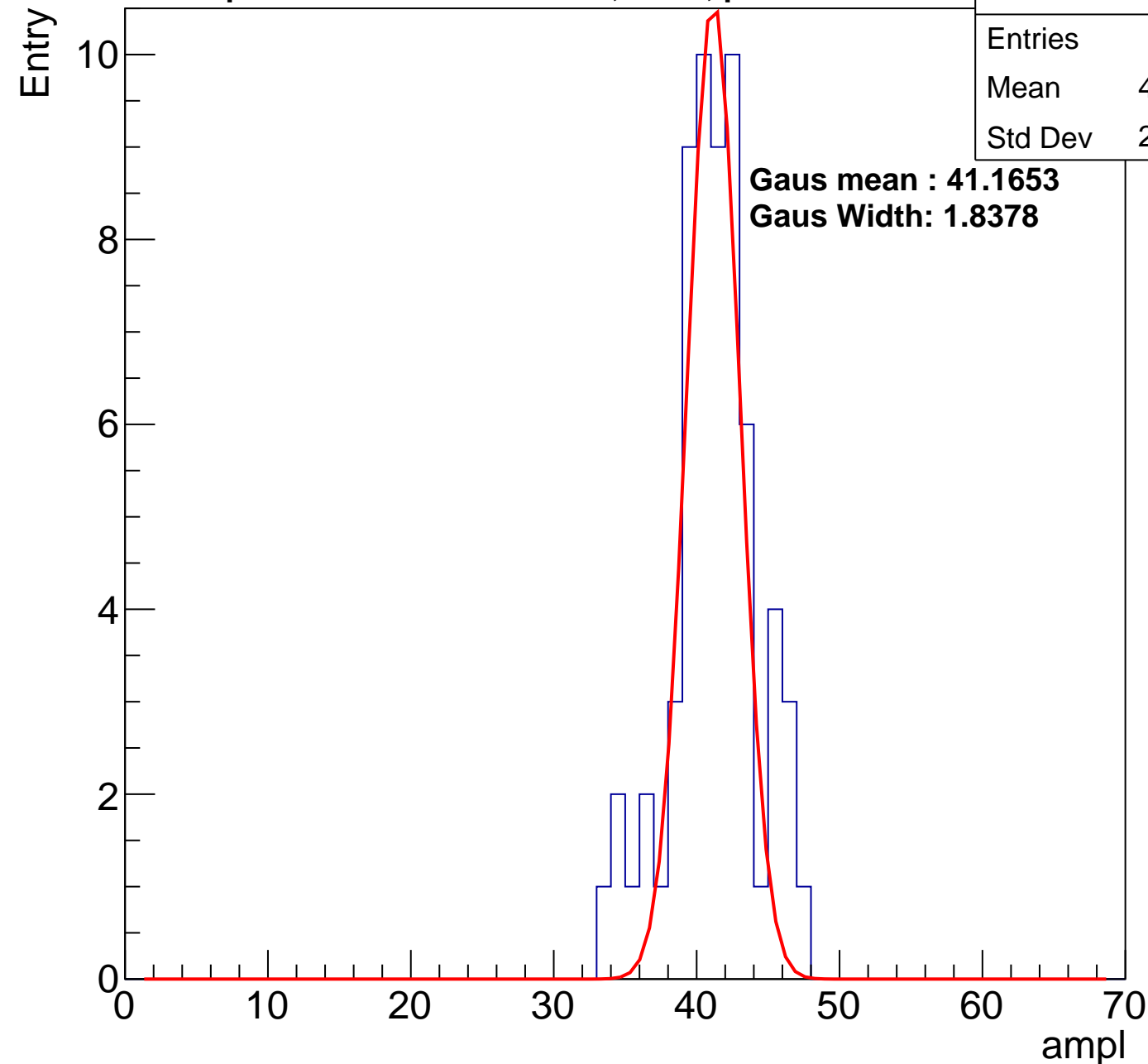
40

50

60

70

ampl

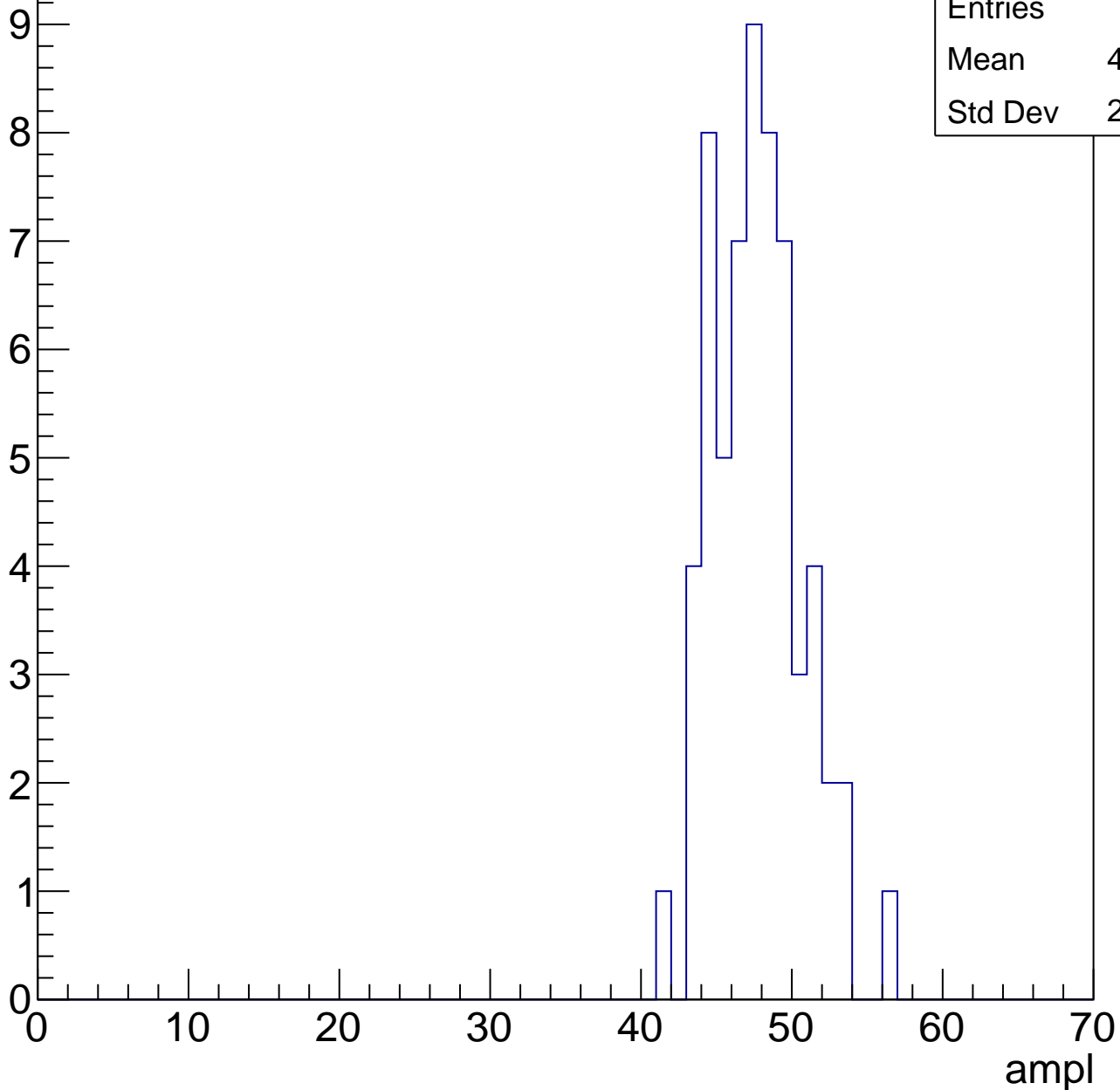


# B1L000S, U2-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	47.25
Std Dev	2.935

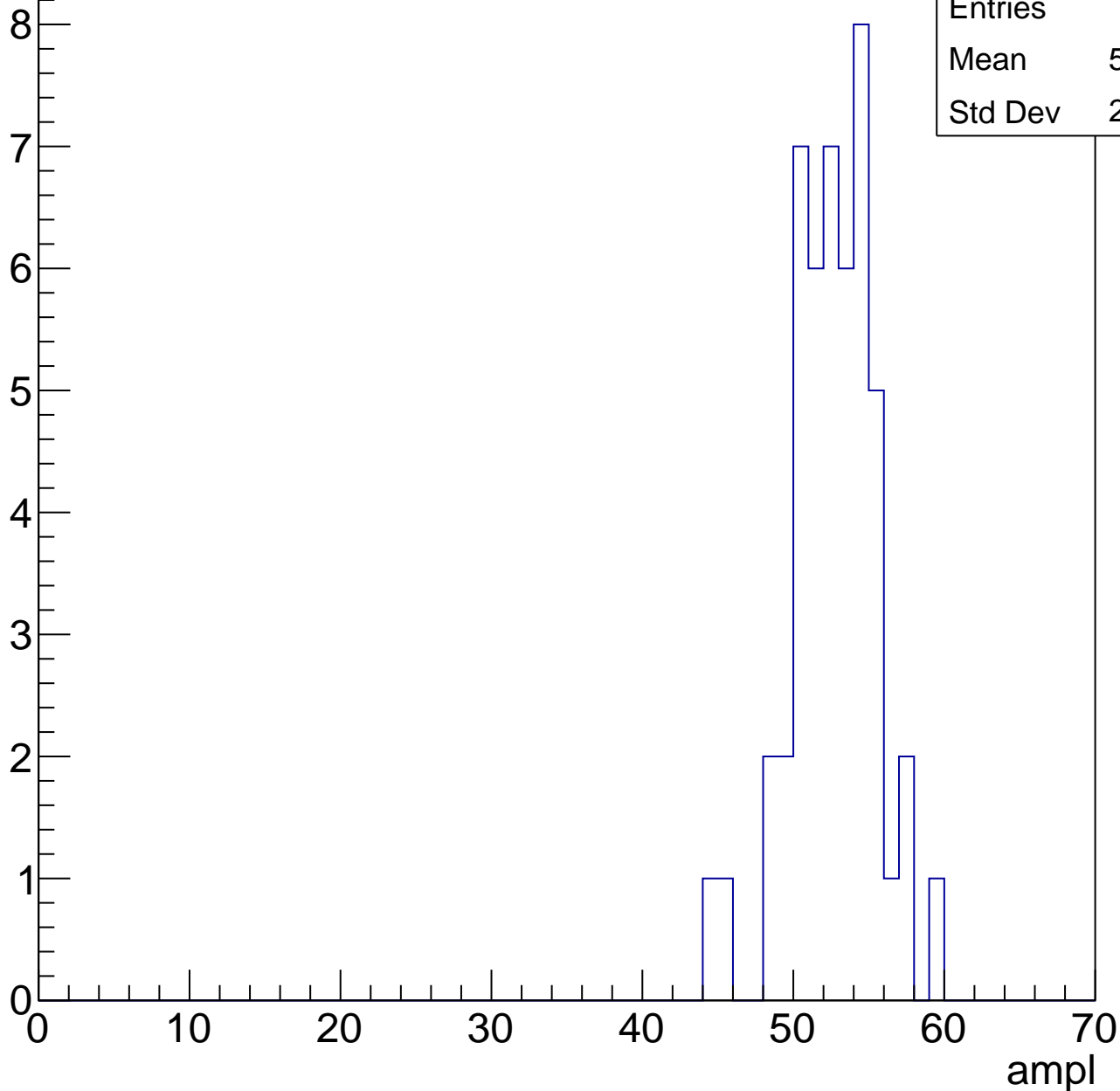


# B1L000S, U2-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	52.18
Std Dev	2.833



# B1L000S, U2-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

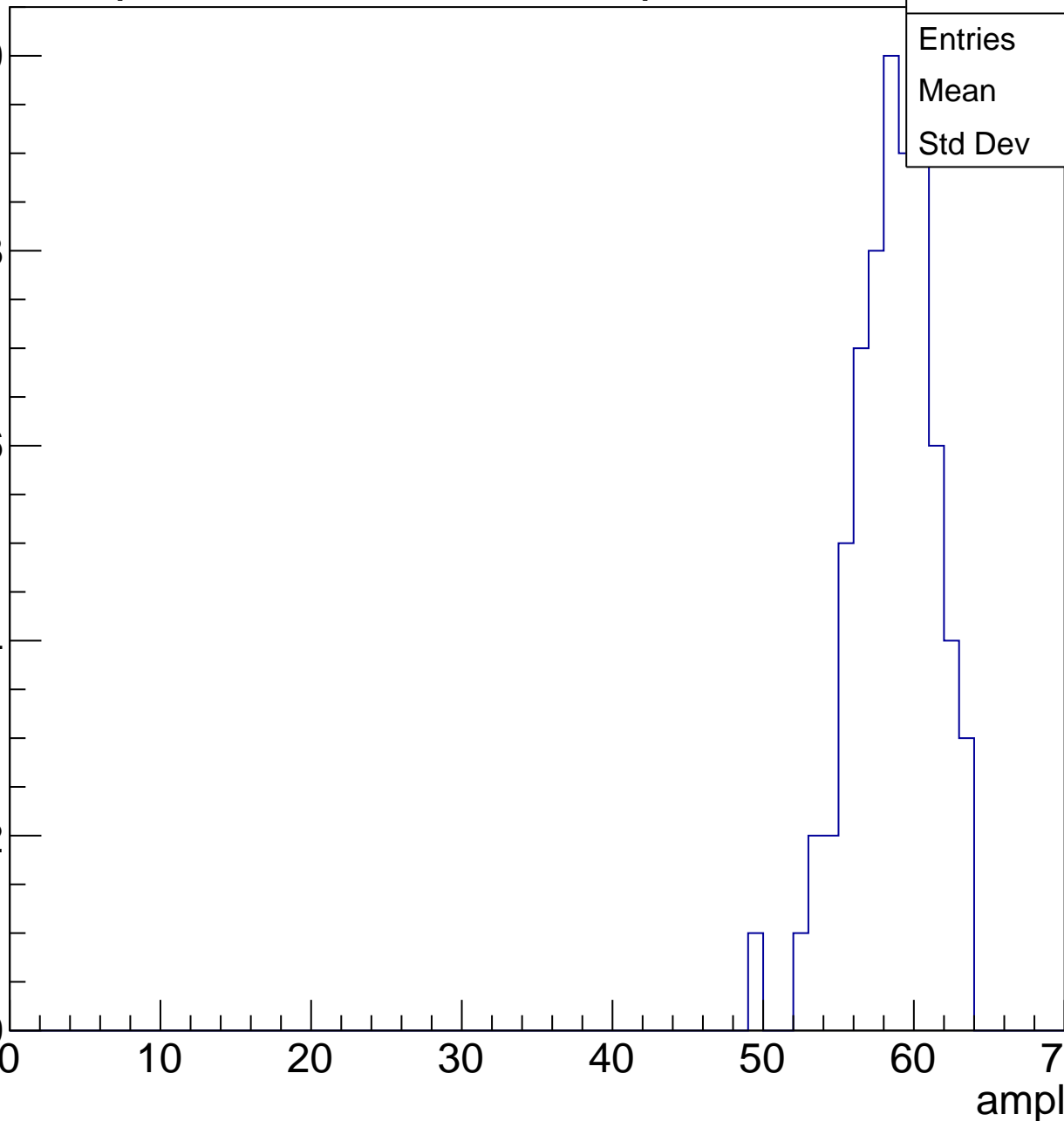
50

60

70

ampl

Entries	67
Mean	58.09
Std Dev	2.779

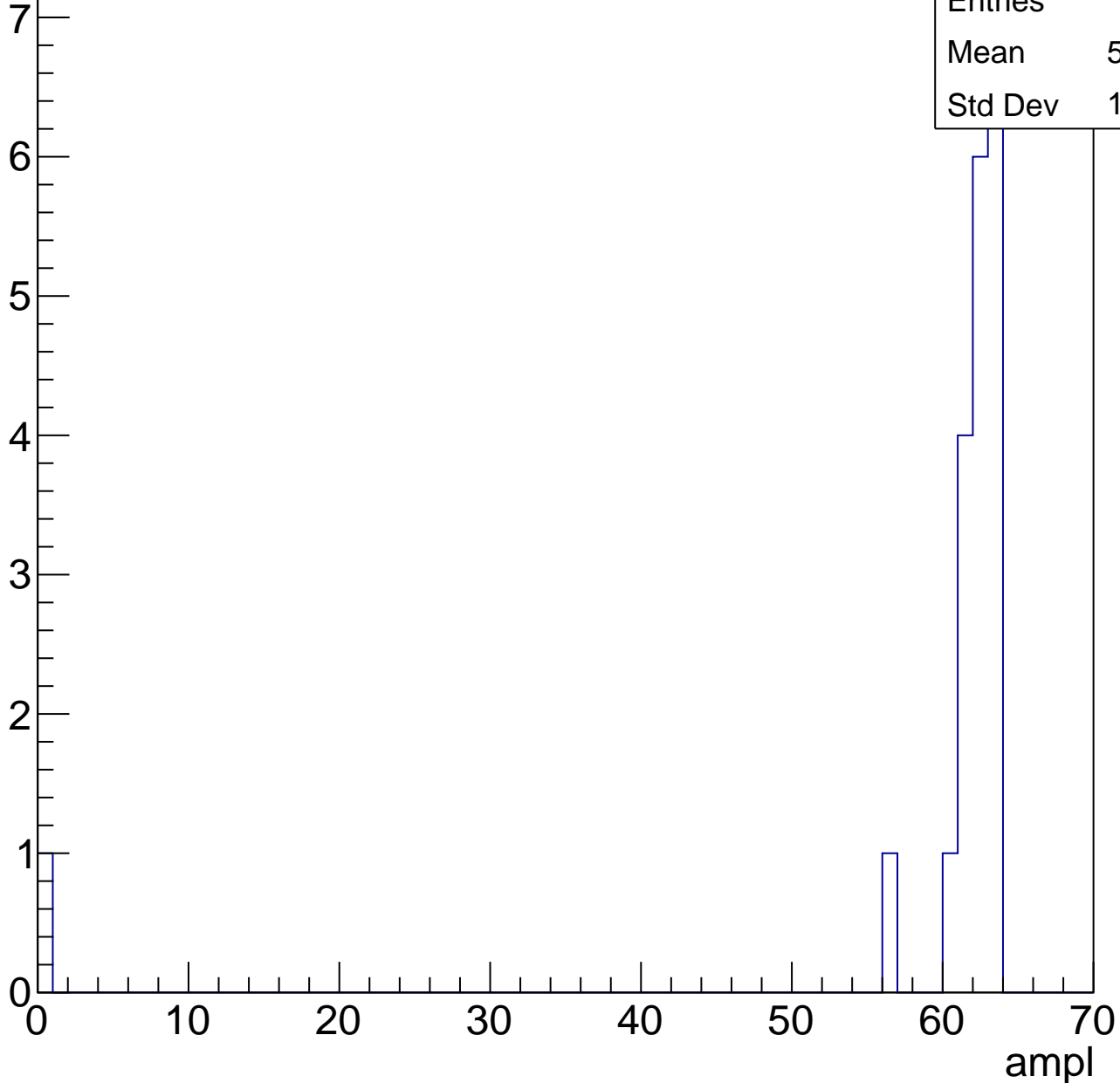


# B1L000S, U2-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.65
Std Dev	13.55





# B1L000S, U2-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U2-ch27, adc0

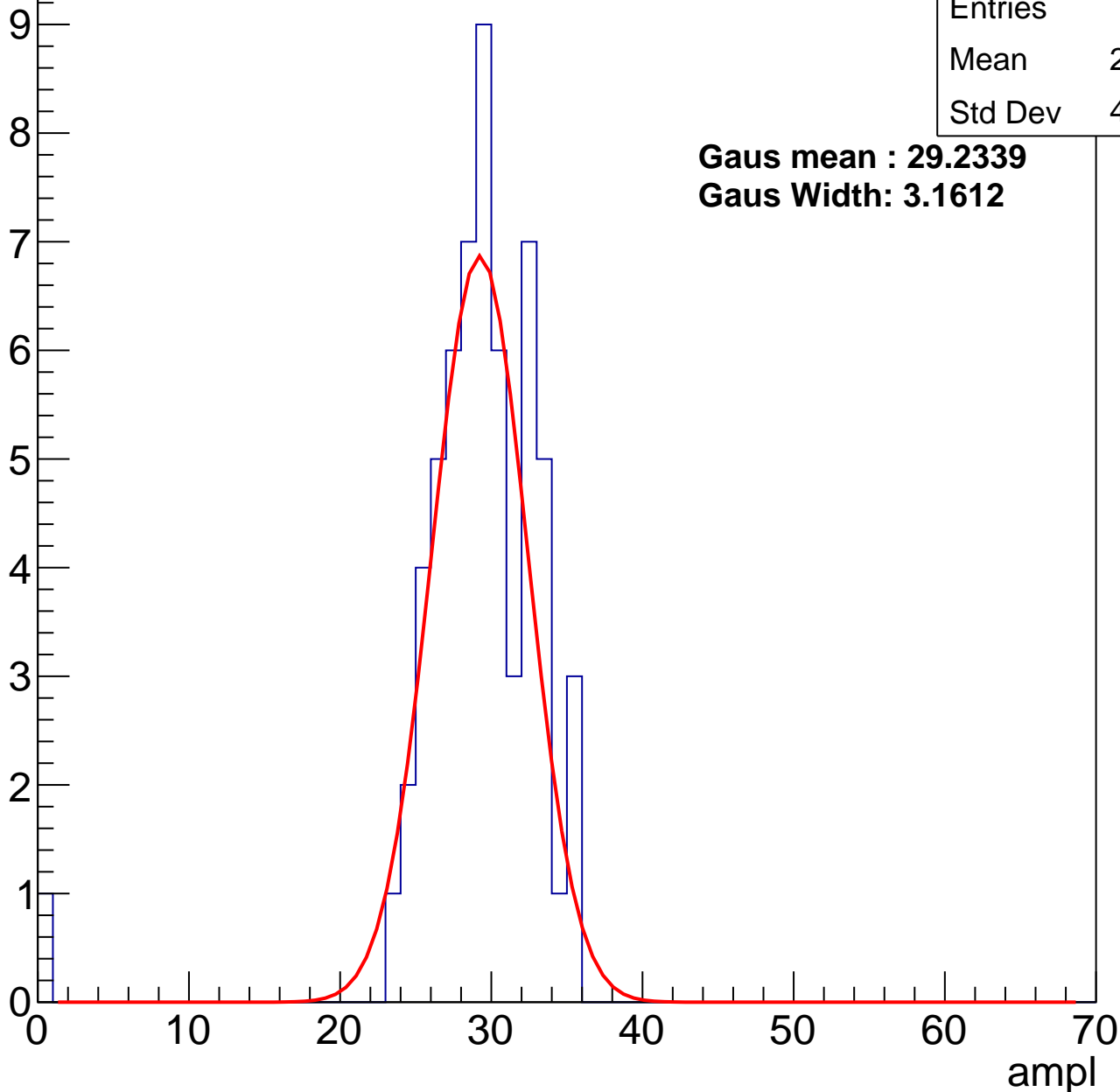
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	28.68
Std Dev	4.752

**Gaus mean : 29.2339**

**Gaus Width: 3.1612**



# B1L000S, U2-ch27, adc1

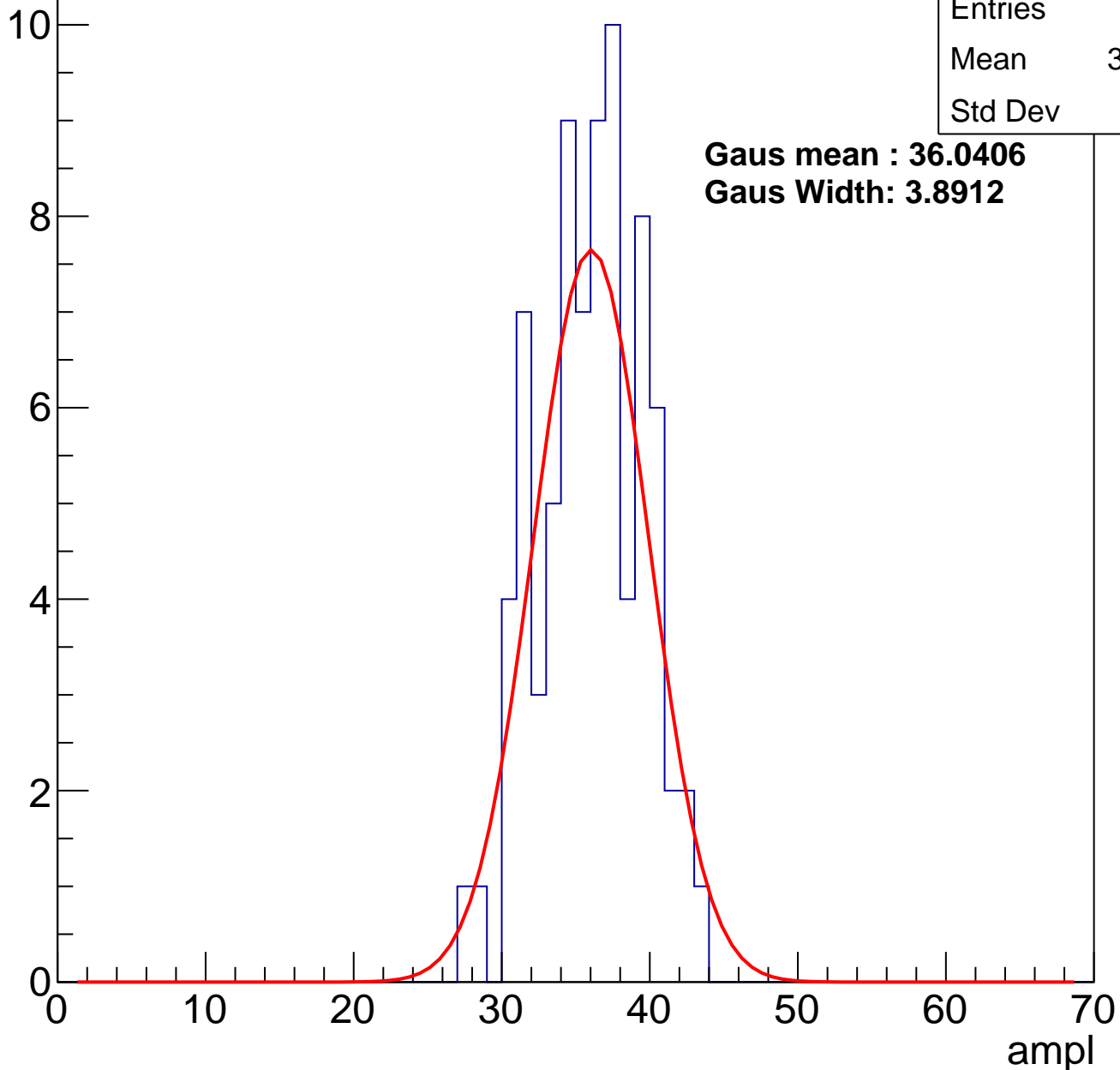
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	35.58
Std Dev	3.47

**Gaus mean : 36.0406**

**Gaus Width: 3.8912**

Entry



# B1L000S, U2-ch27, adc2

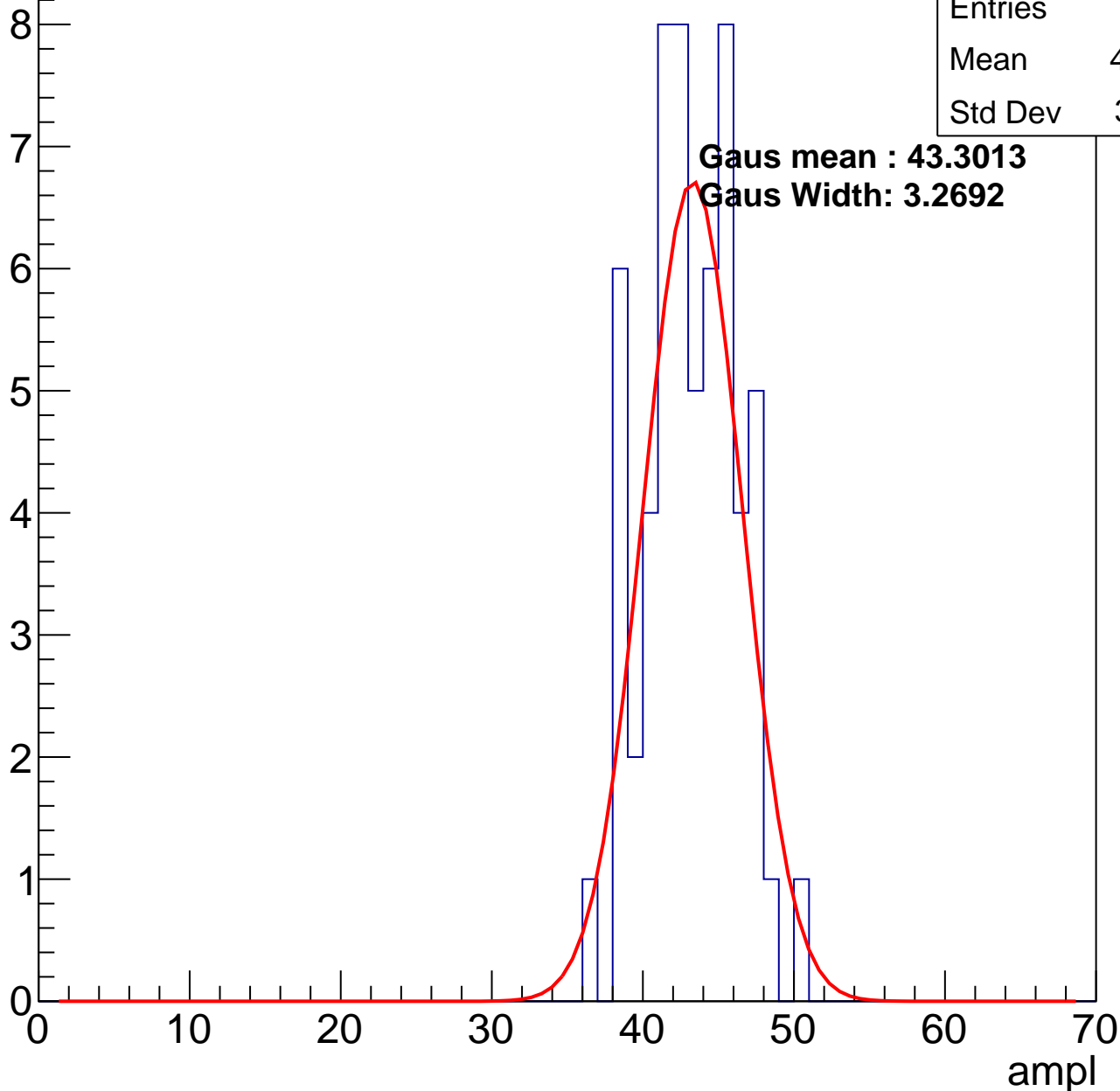
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.75
Std Dev	3.001

**Gaus mean : 43.3013**

**Gaus Width: 3.2692**

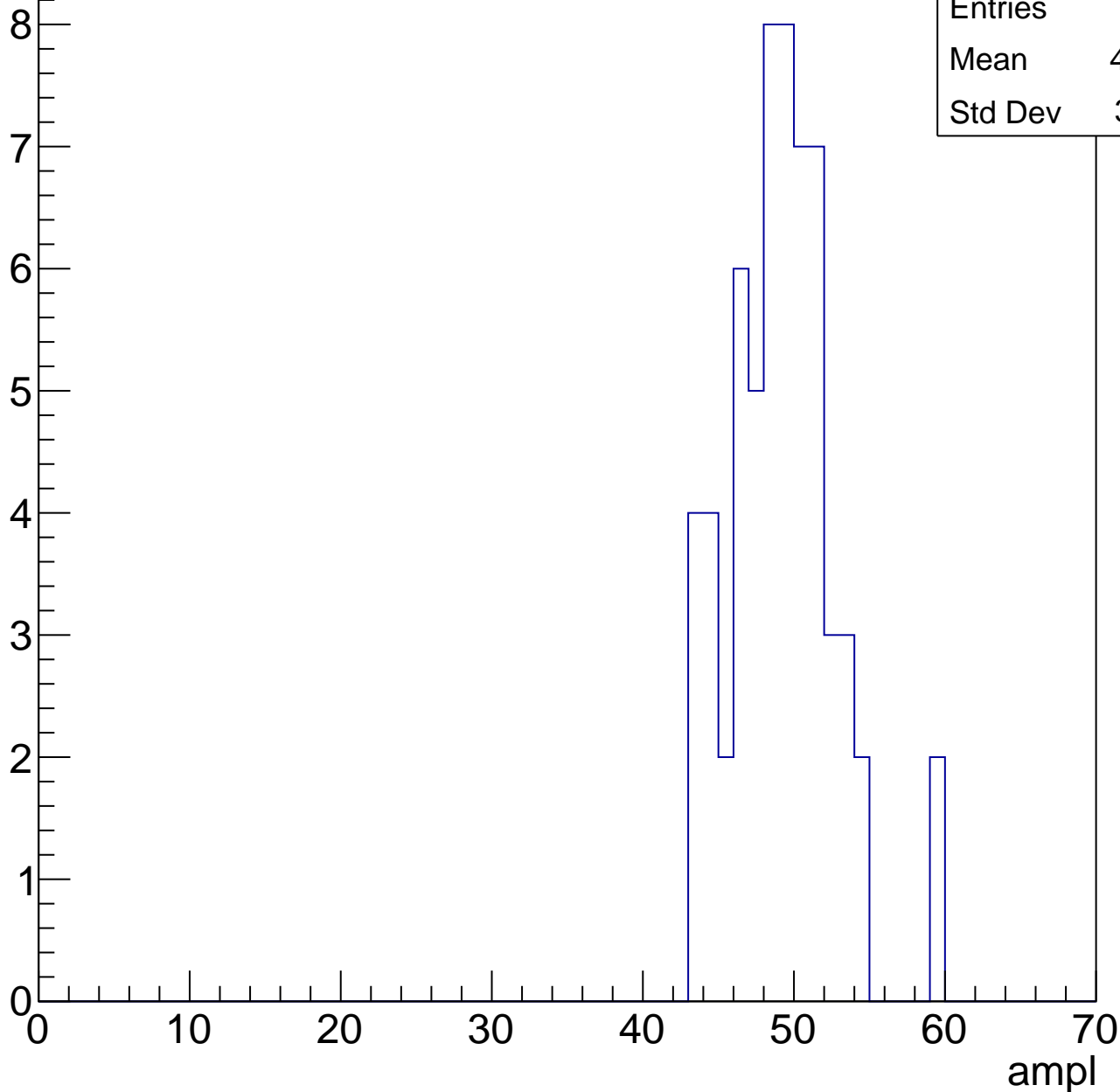


# B1L000S, U2-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	48.74
Std Dev	3.421

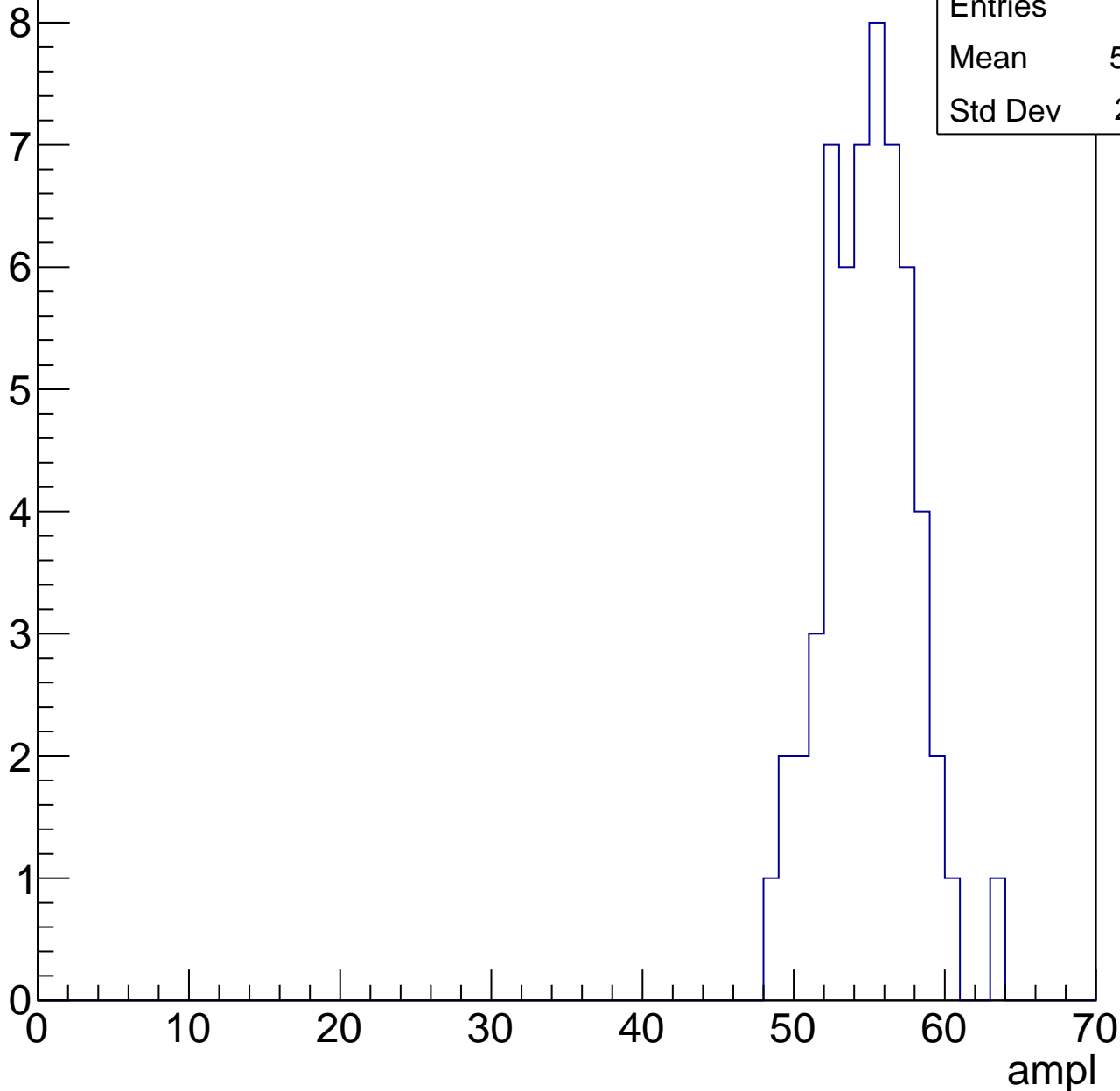


# B1L000S, U2-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	54.49
Std Dev	2.921

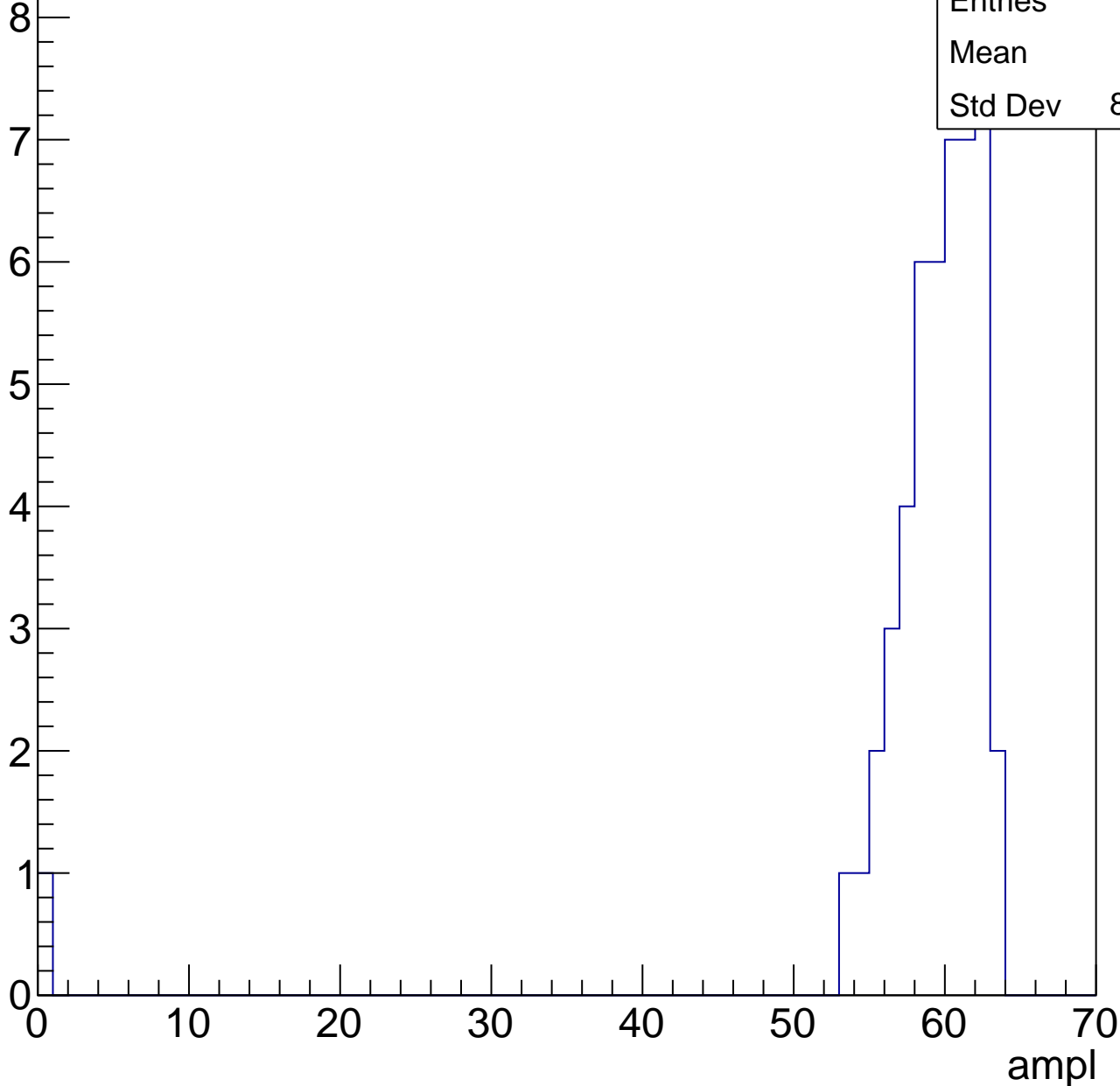


# B1L000S, U2-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

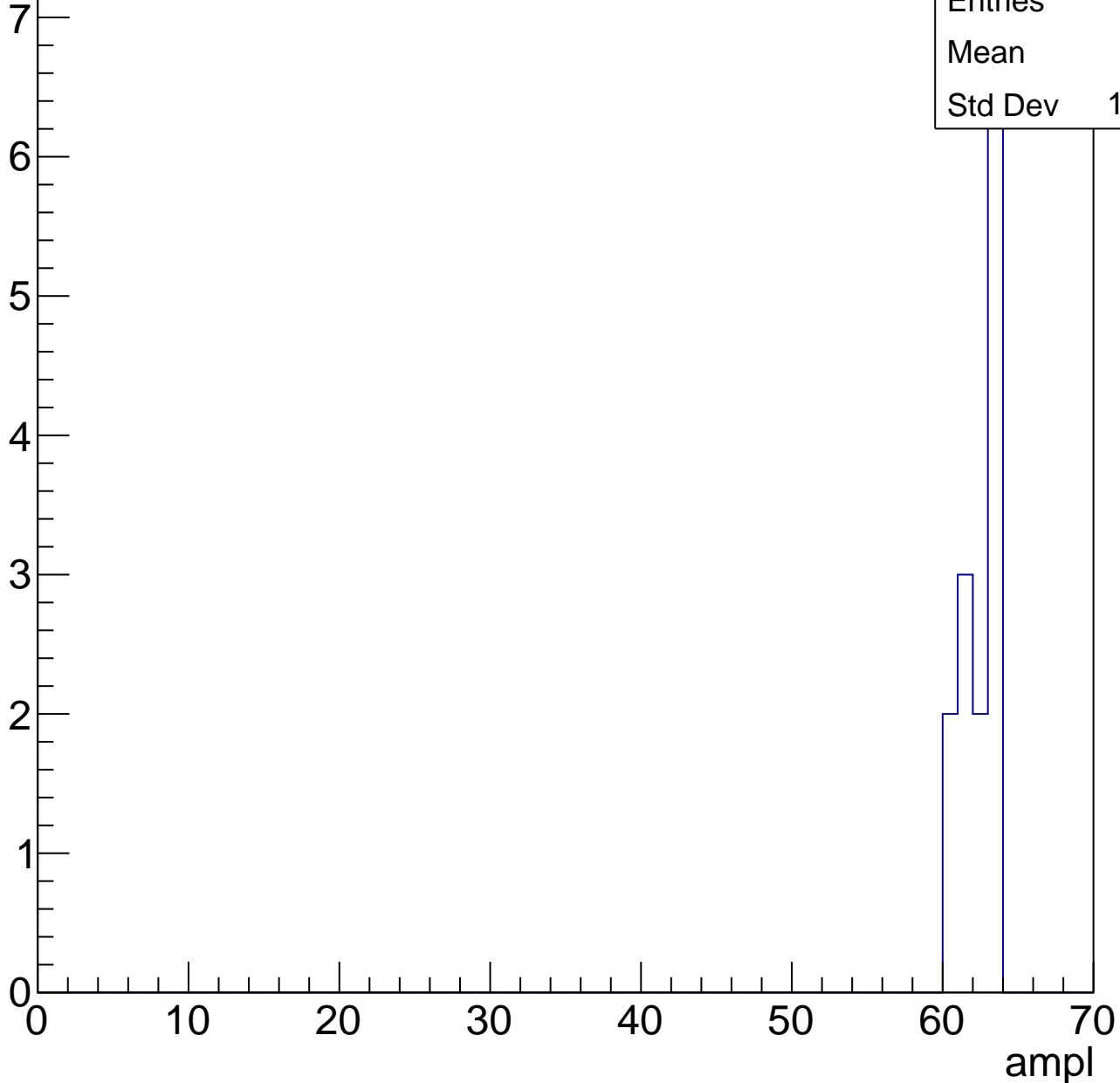
Entries	48
Mean	58
Std Dev	8.796



# B1L000S, U2-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



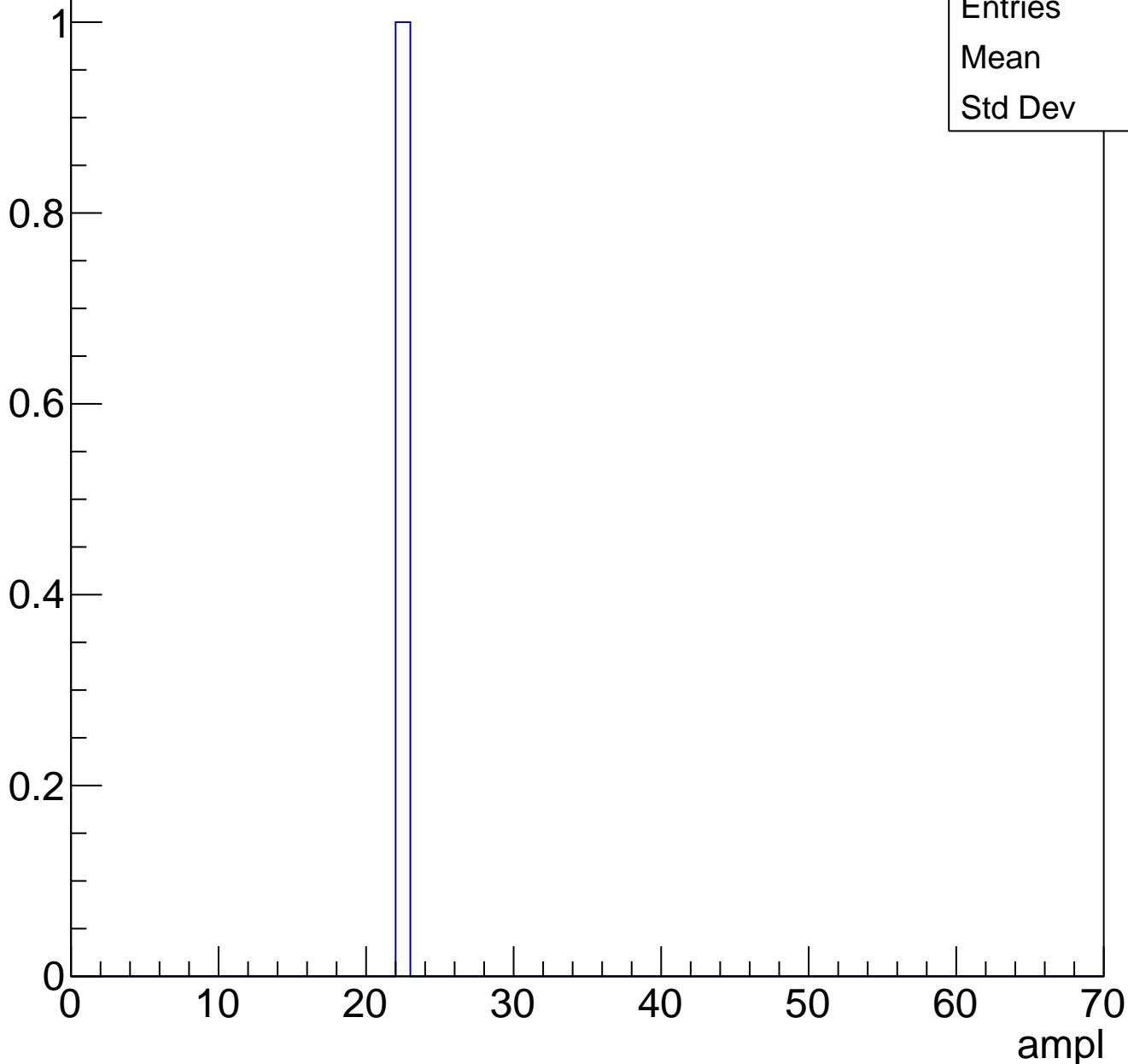
Entries	14
Mean	62
Std Dev	1.134



# B1L000S, U2-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch28, adc0

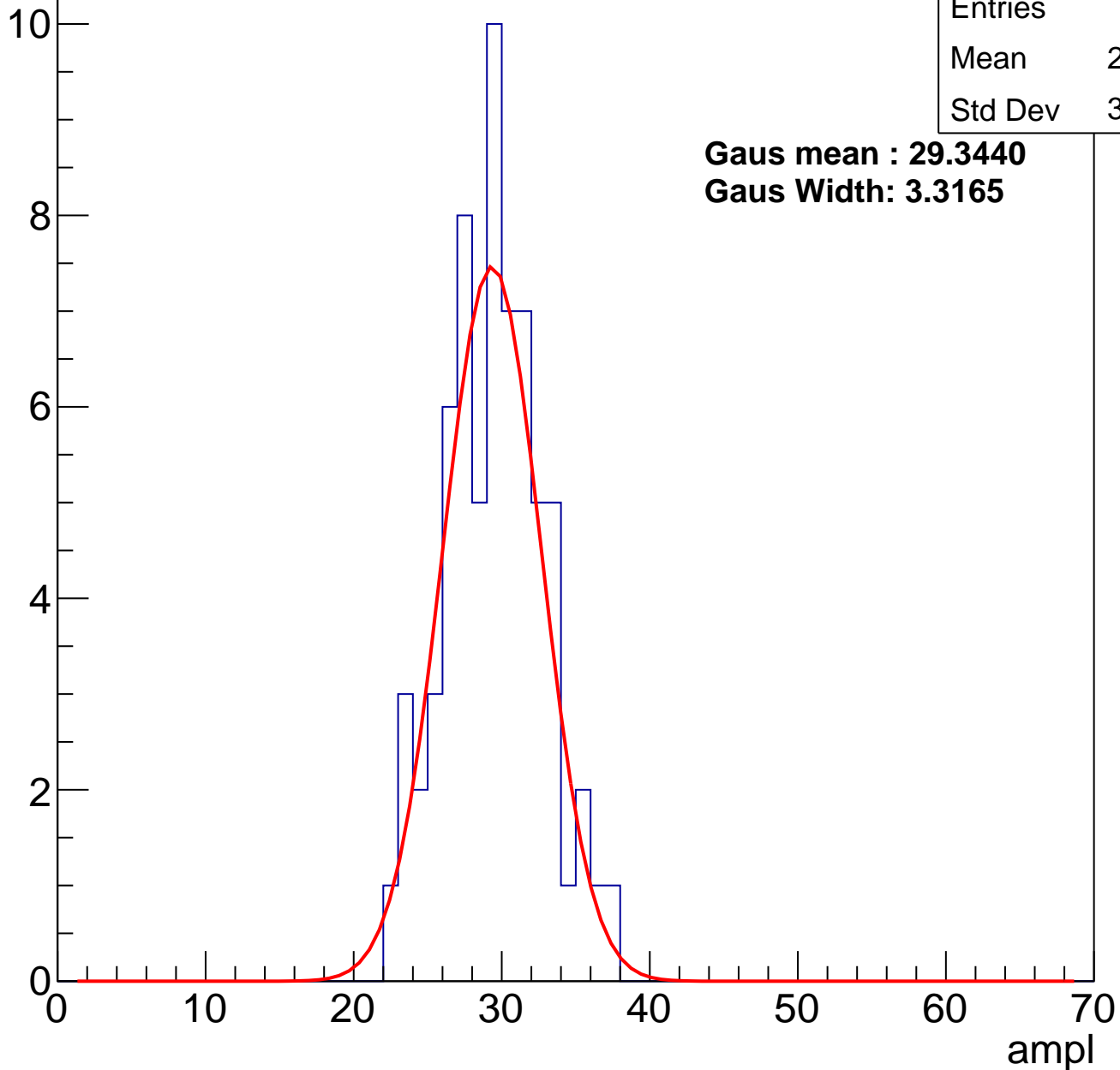
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	29.03
Std Dev	3.269

**Gaus mean : 29.3440**

**Gaus Width: 3.3165**

Entry



# B1L000S, U2-ch28, adc1

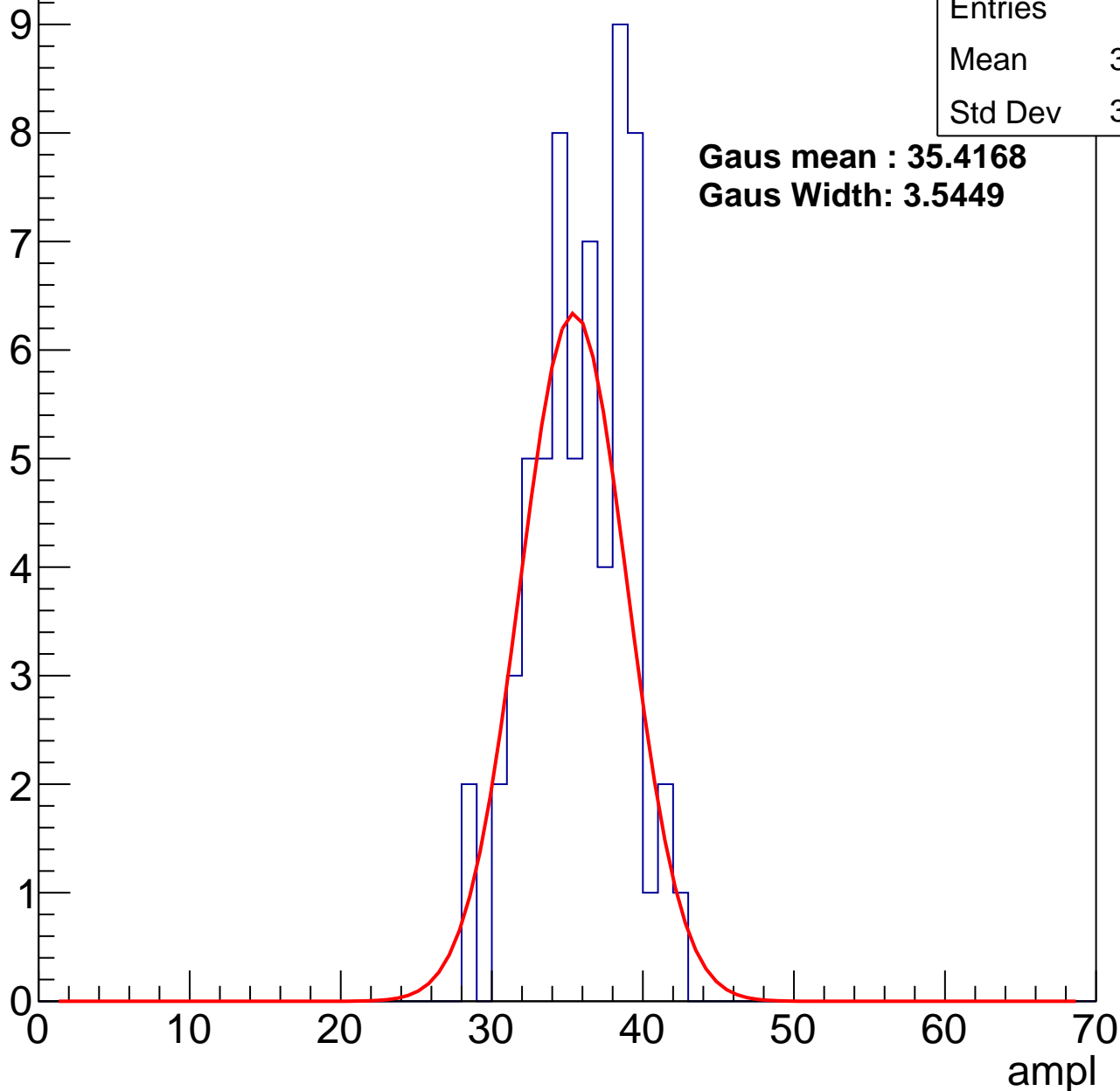
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.47
Std Dev	3.196

**Gaus mean : 35.4168**

**Gaus Width: 3.5449**



# B1L000S, U2-ch28, adc2

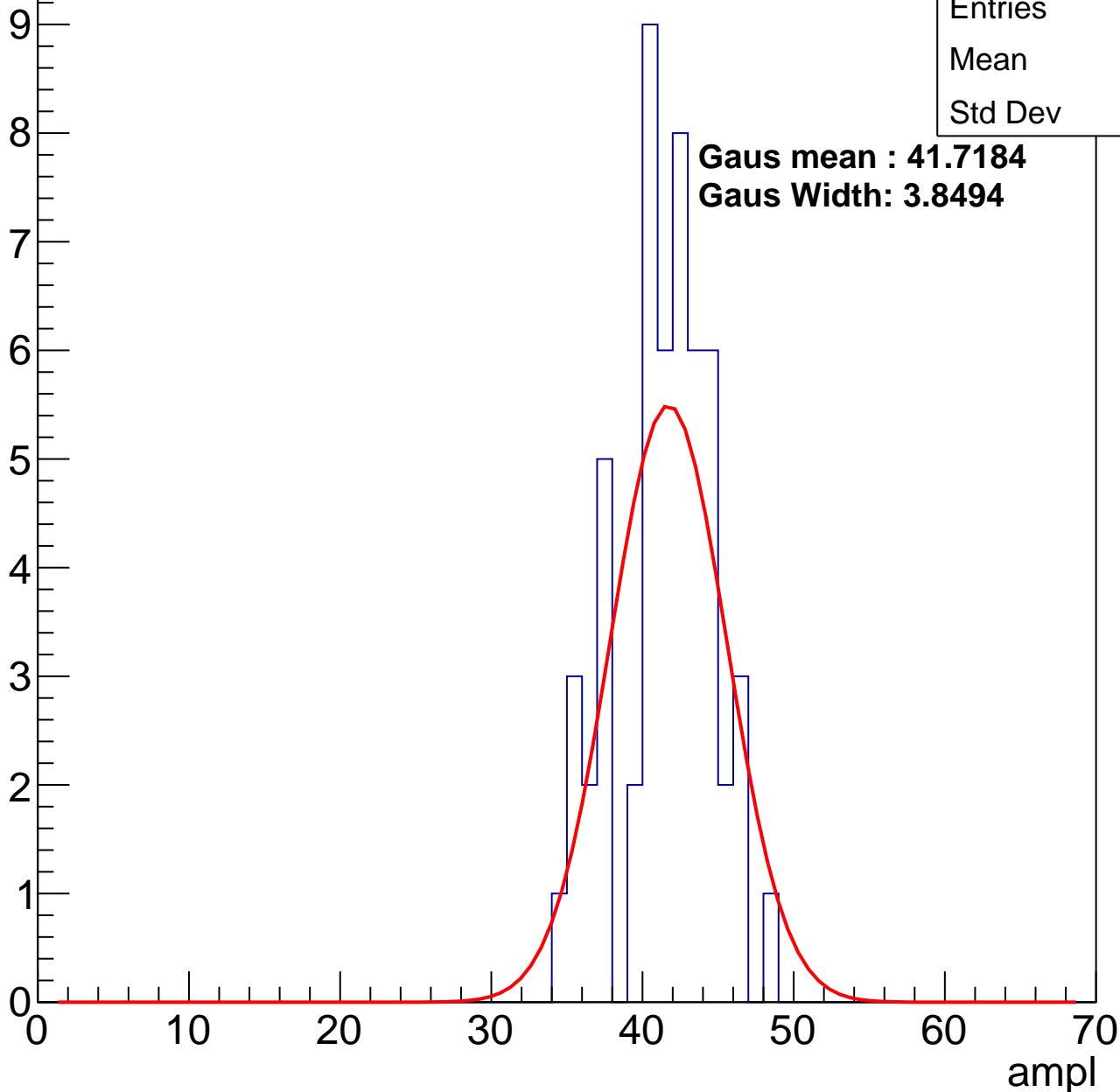
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41
Std Dev	3.18

**Gaus mean : 41.7184**

**Gaus Width: 3.8494**

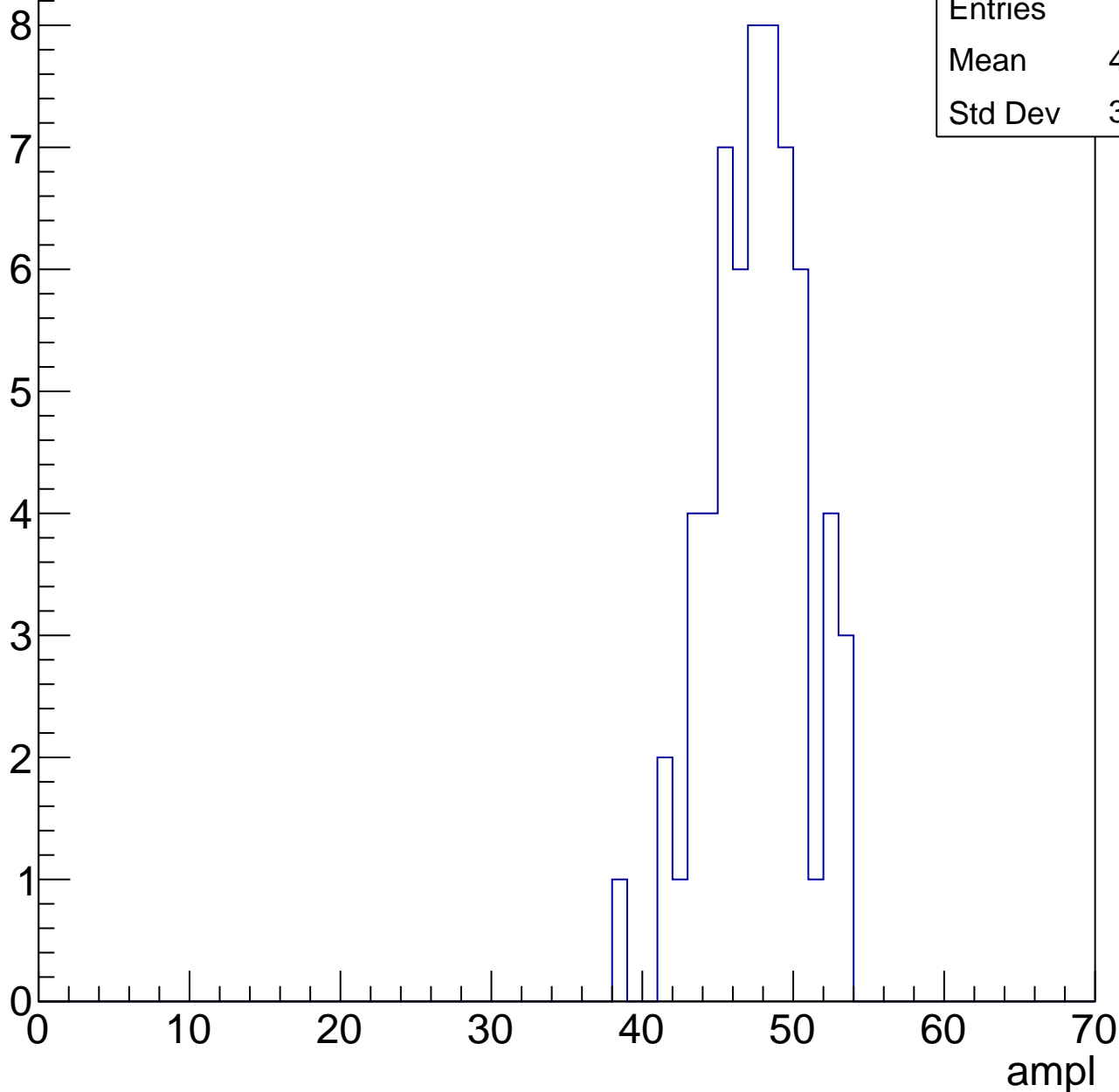


# B1L000S, U2-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	47.13
Std Dev	3.175

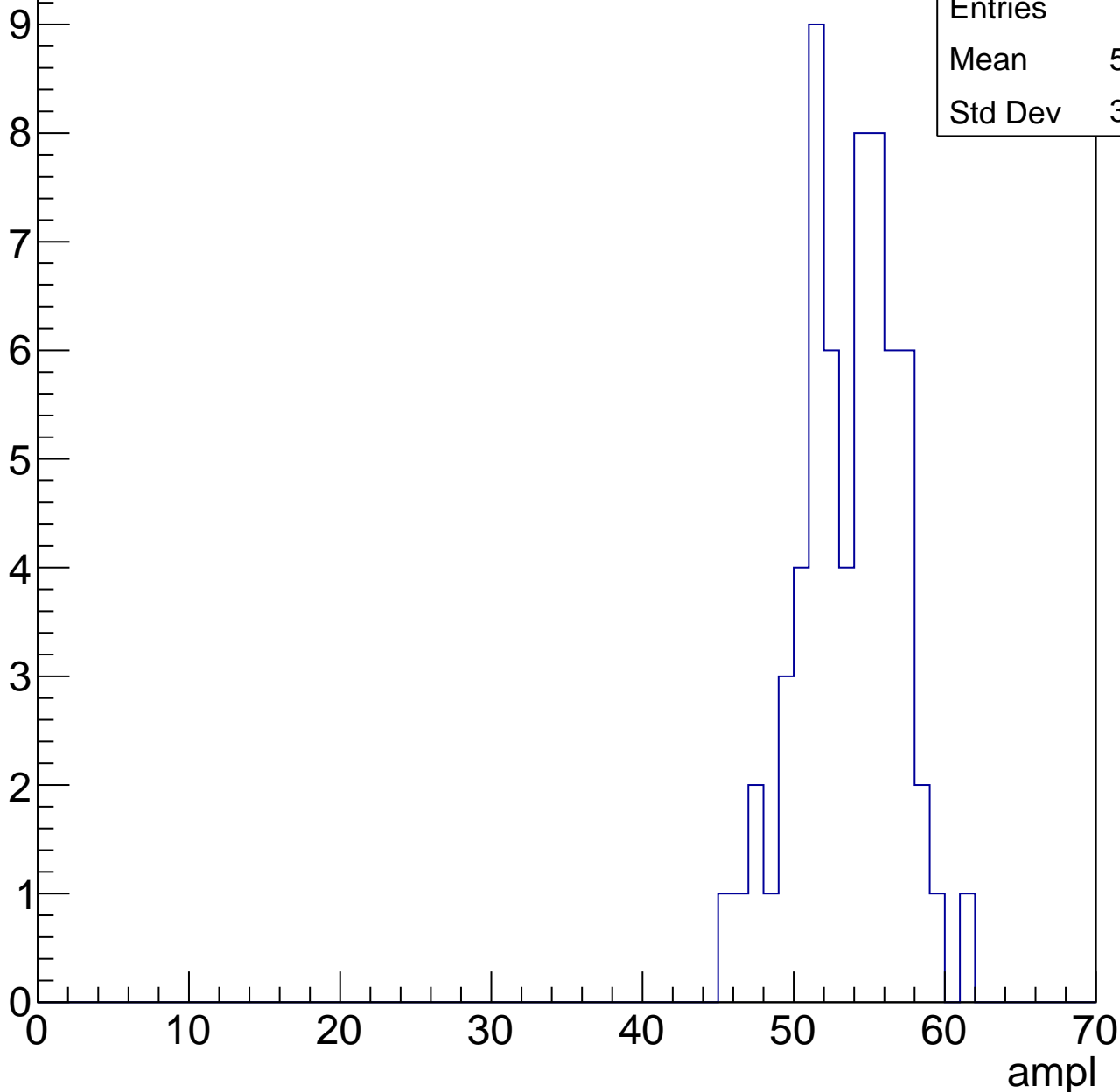


# B1L000S, U2-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	53.16
Std Dev	3.272

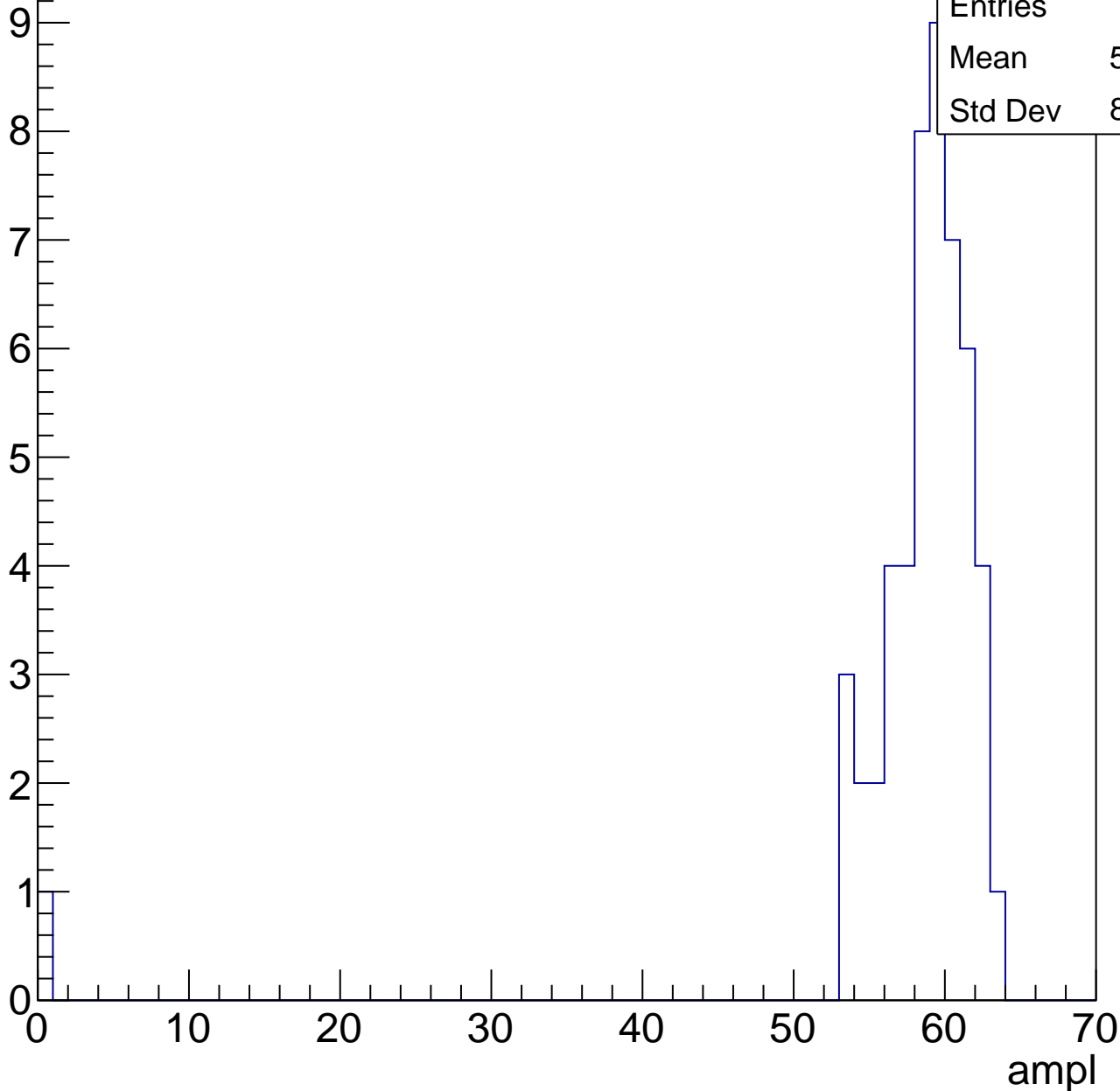


# B1L000S, U2-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

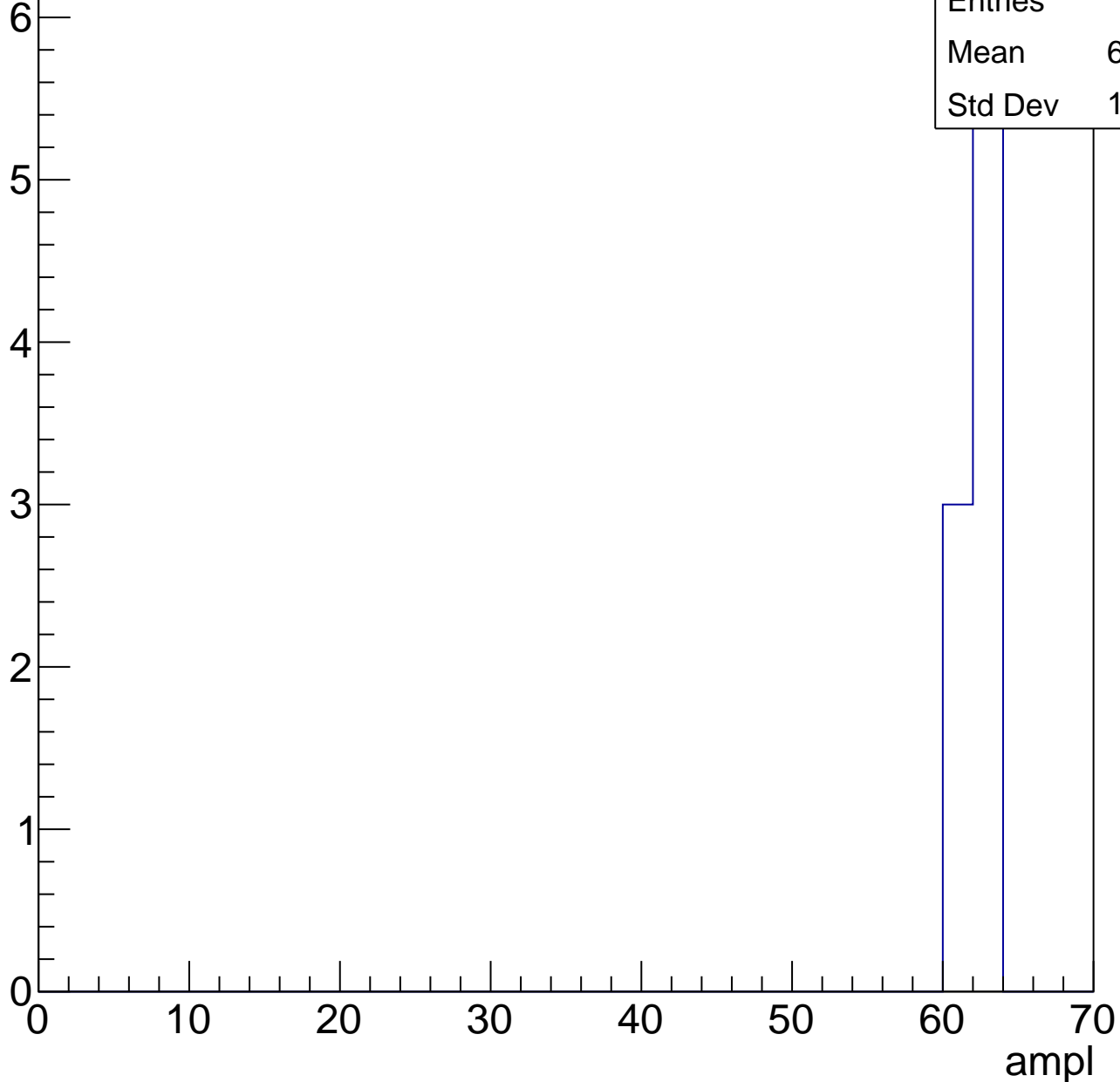
Entries	51
Mean	57.27
Std Dev	8.474



# B1L000S, U2-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



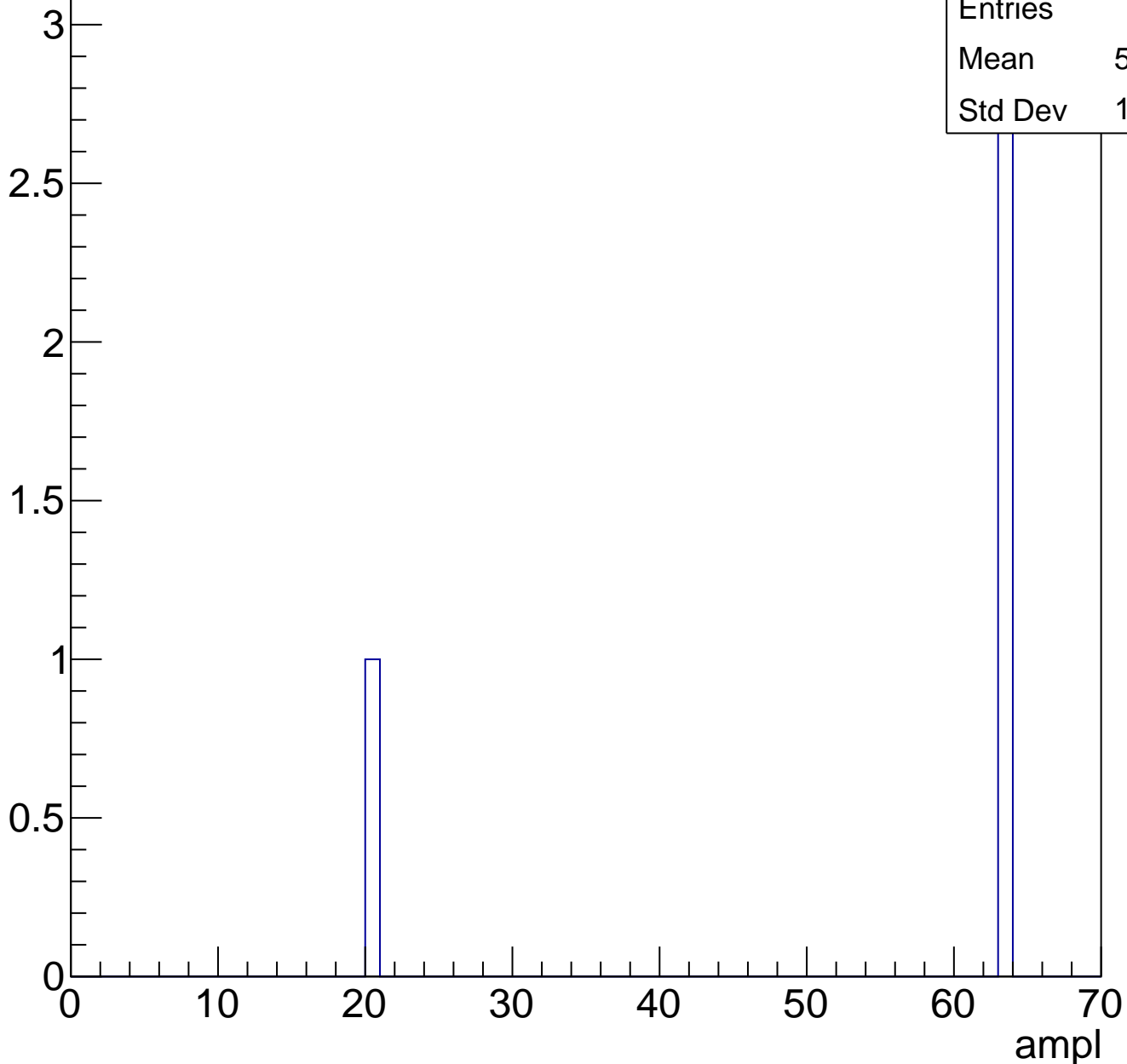
Entries	18
Mean	61.83
Std Dev	1.067



# B1L000S, U2-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch29, adc0

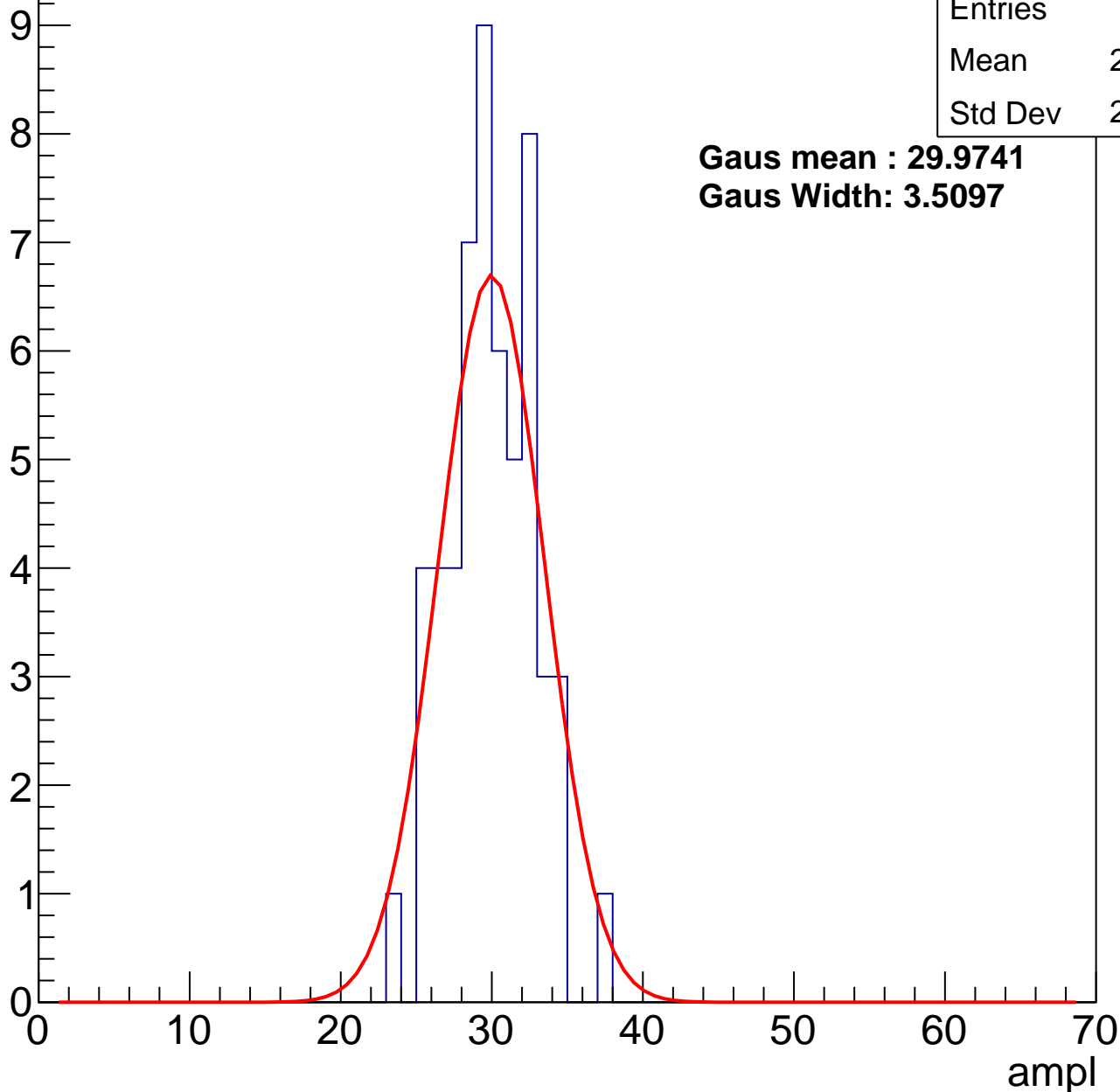
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	29.47
Std Dev	2.802

**Gaus mean : 29.9741**

**Gaus Width: 3.5097**



# B1L000S, U2-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.9
Std Dev	3.072

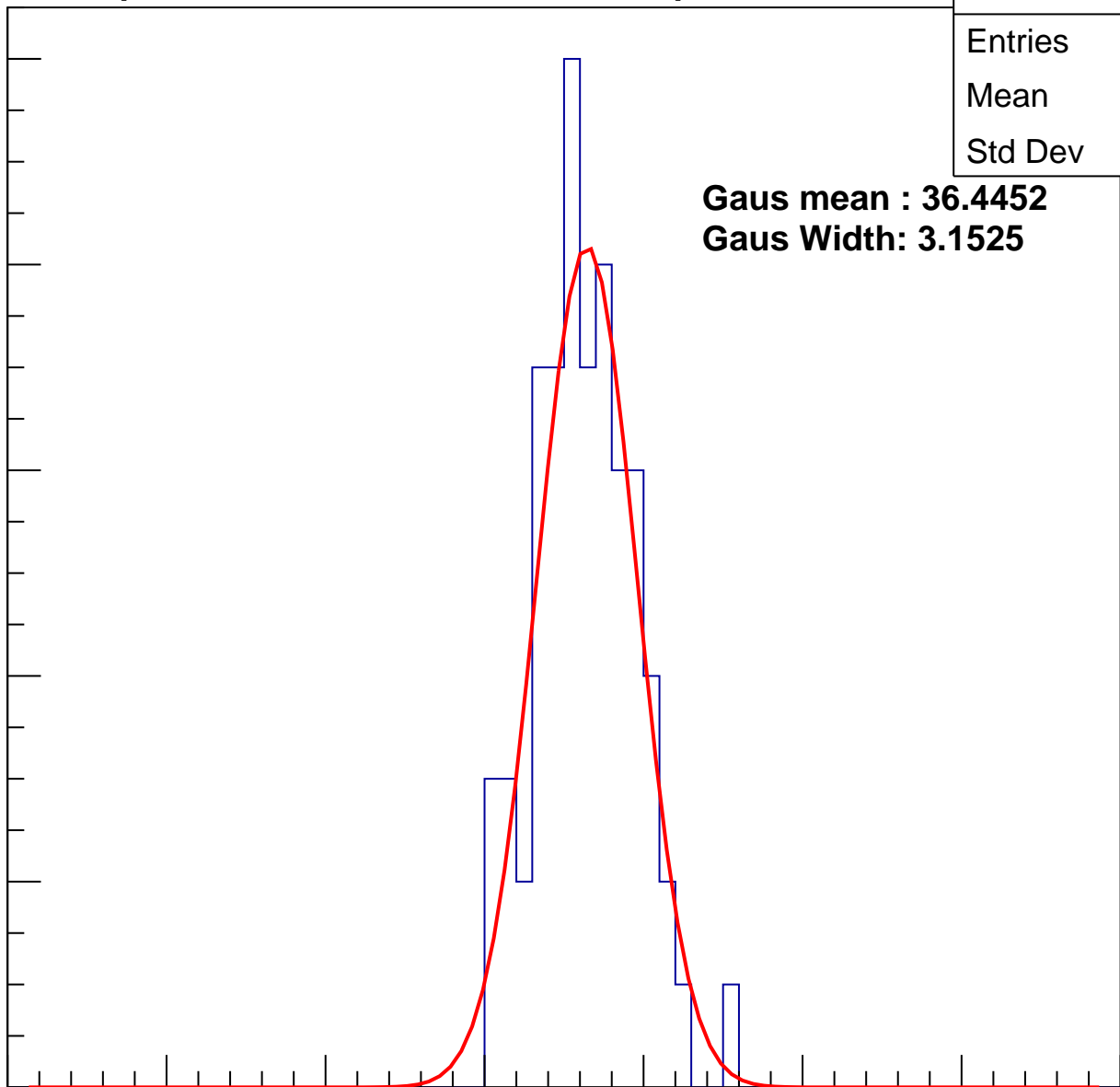
**Gaus mean : 36.4452**

**Gaus Width: 3.1525**

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch29, adc2

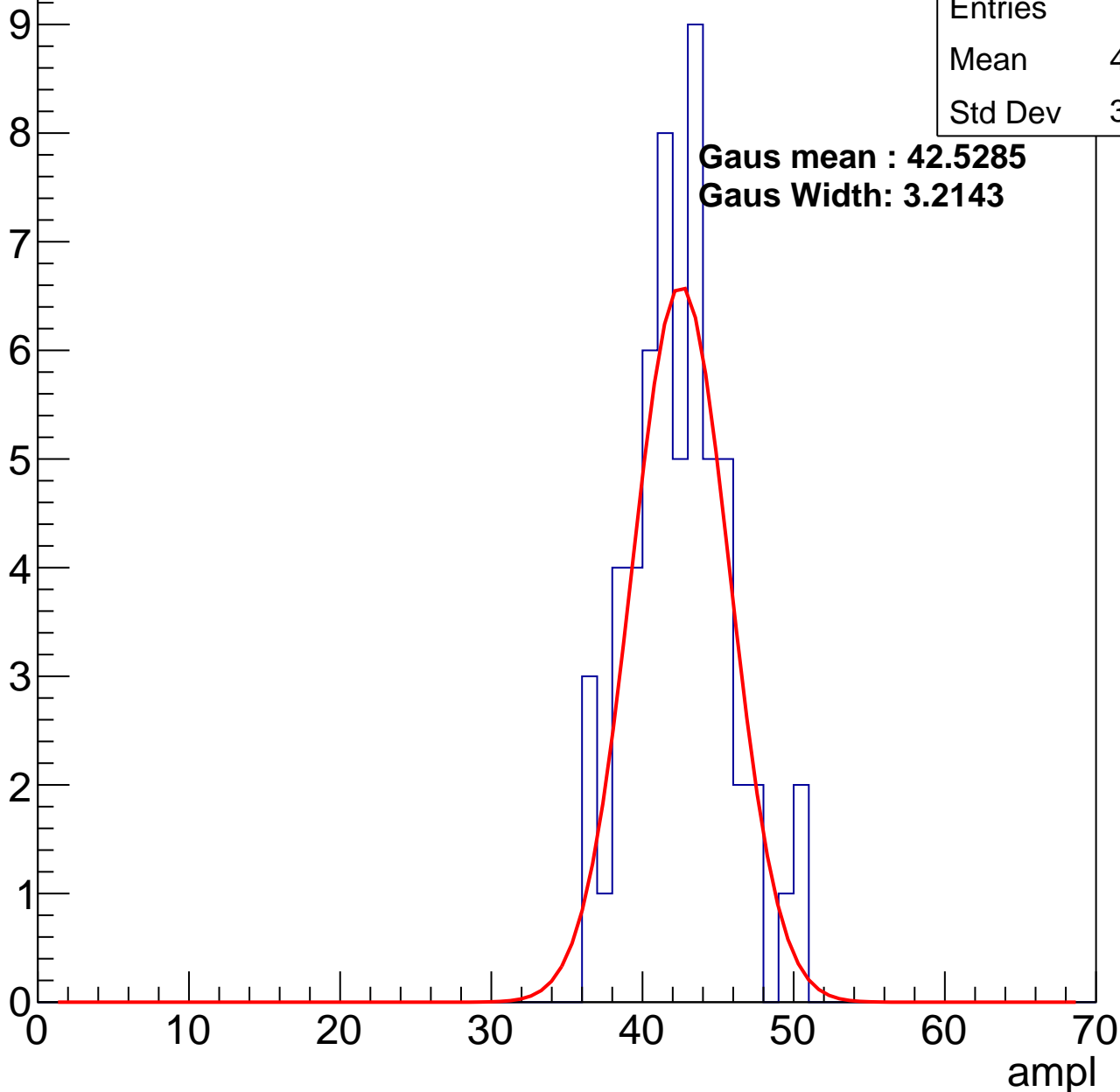
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	42.07
Std Dev	3.238

**Gaus mean : 42.5285**

**Gaus Width: 3.2143**



# B1L000S, U2-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

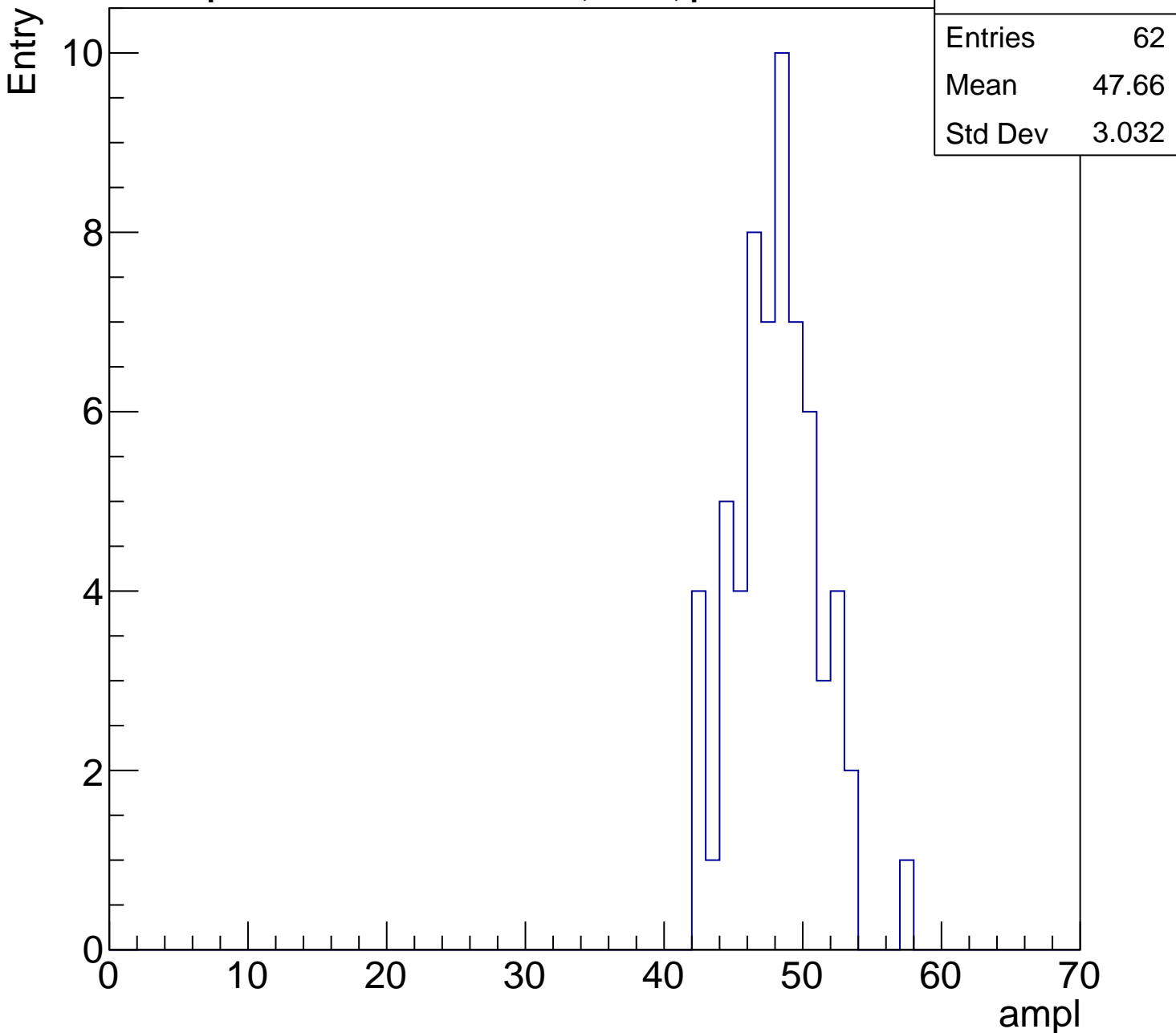
Entries	62
Mean	47.66
Std Dev	3.032

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

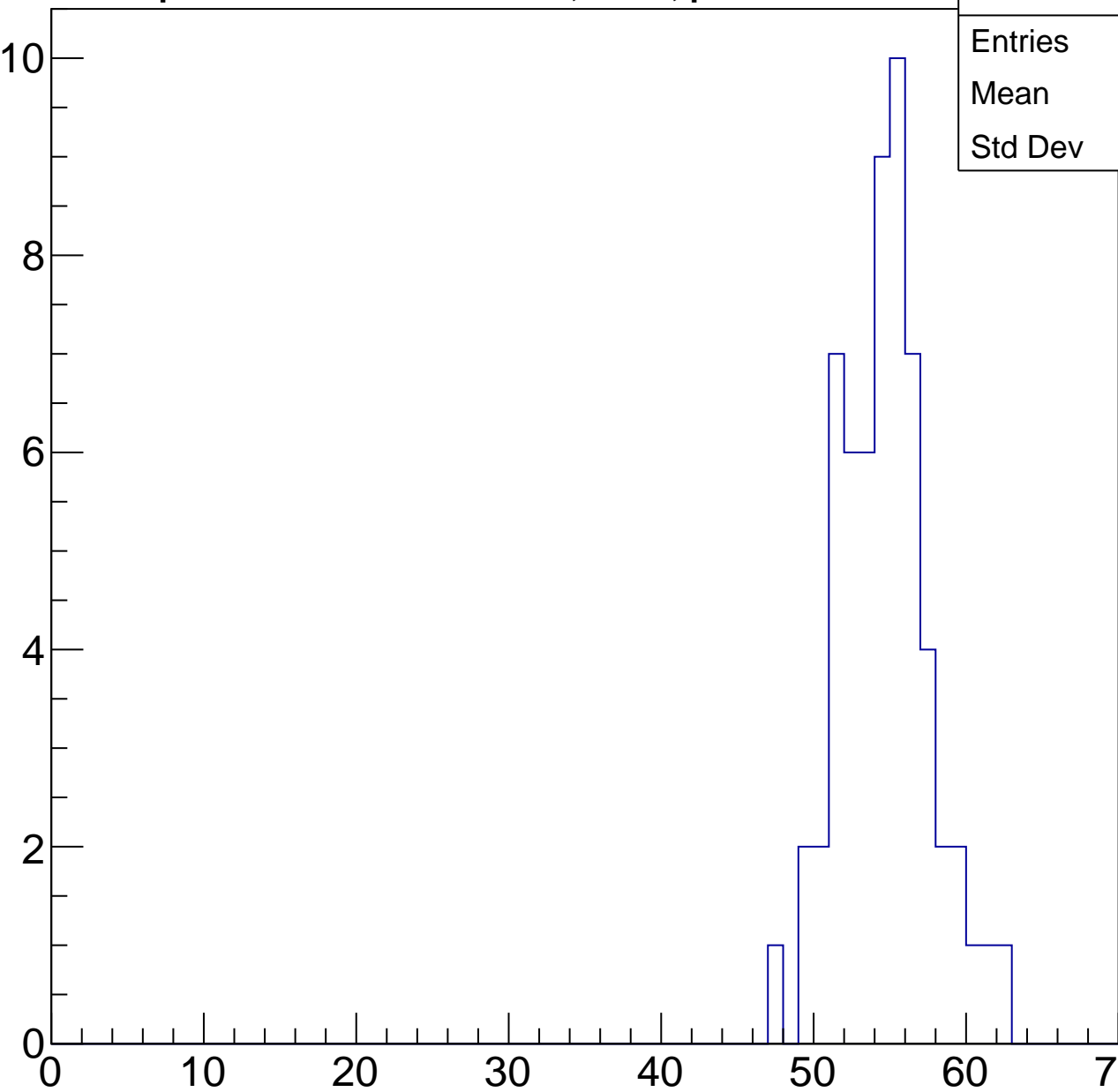
Entries	61
Mean	54.18
Std Dev	2.939

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

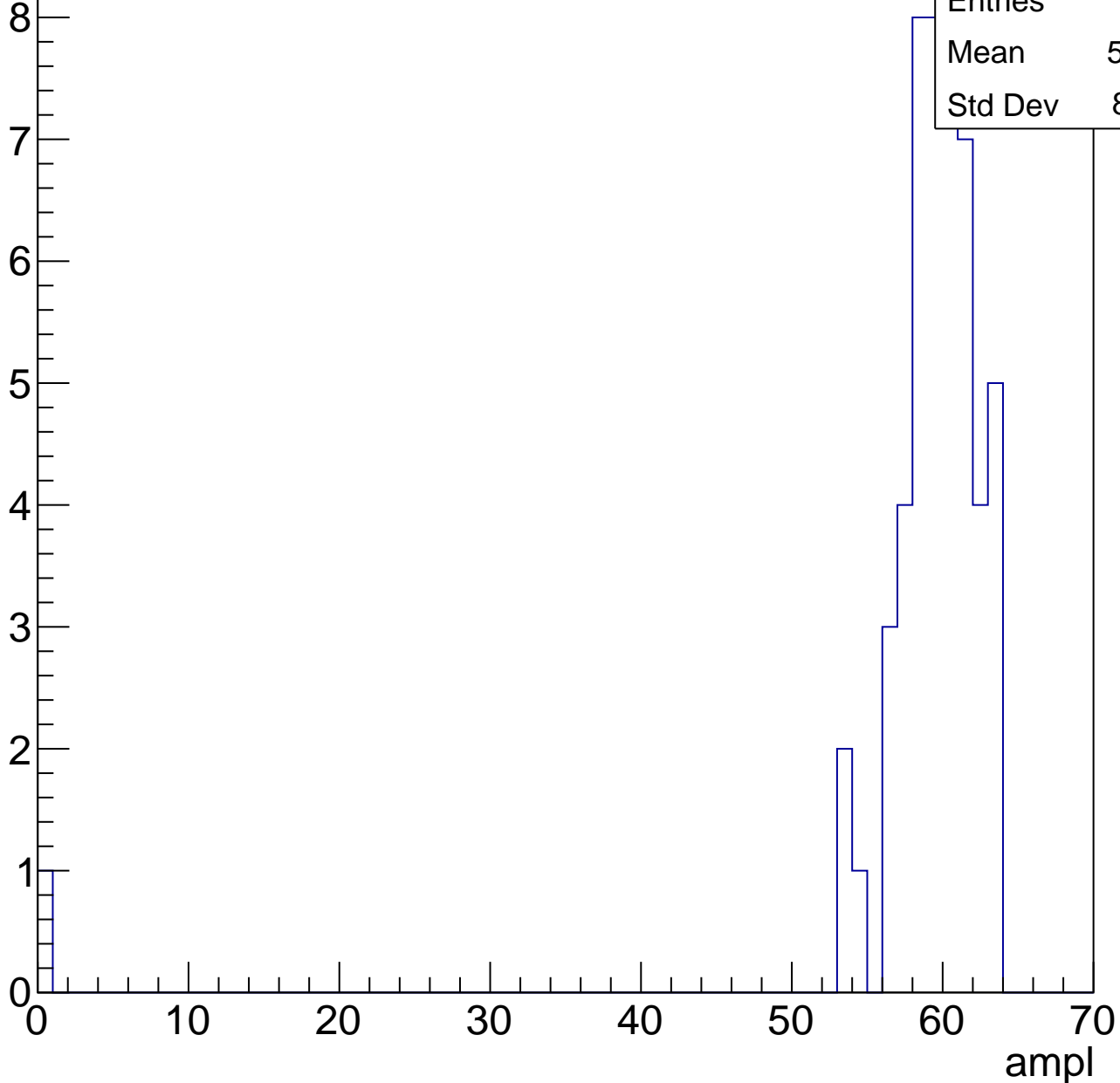


# B1L000S, U2-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	58.08
Std Dev	8.561

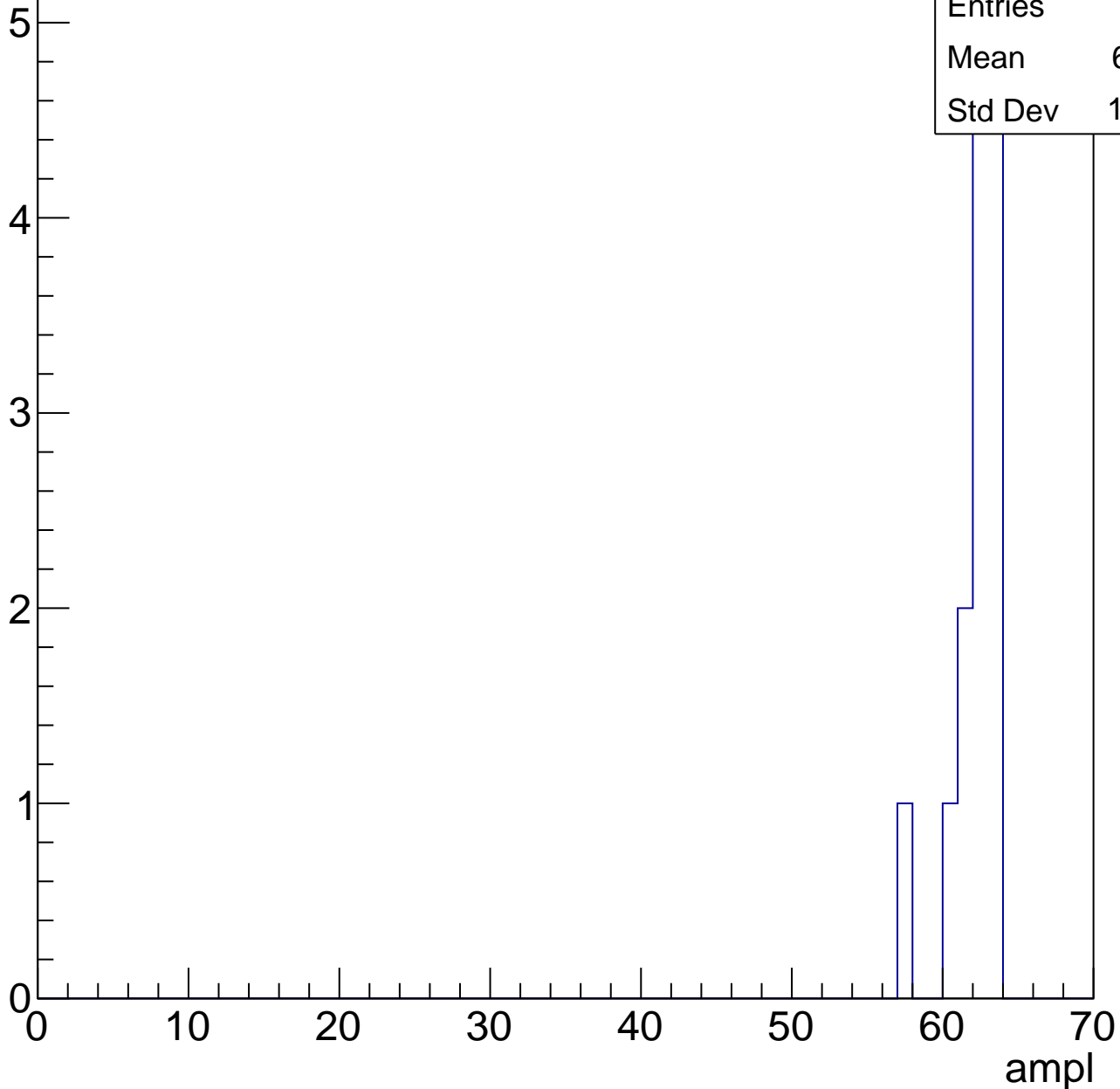


# B1L000S, U2-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.71
Std Dev	1.578





# B1L000S, U2-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch30, adc0

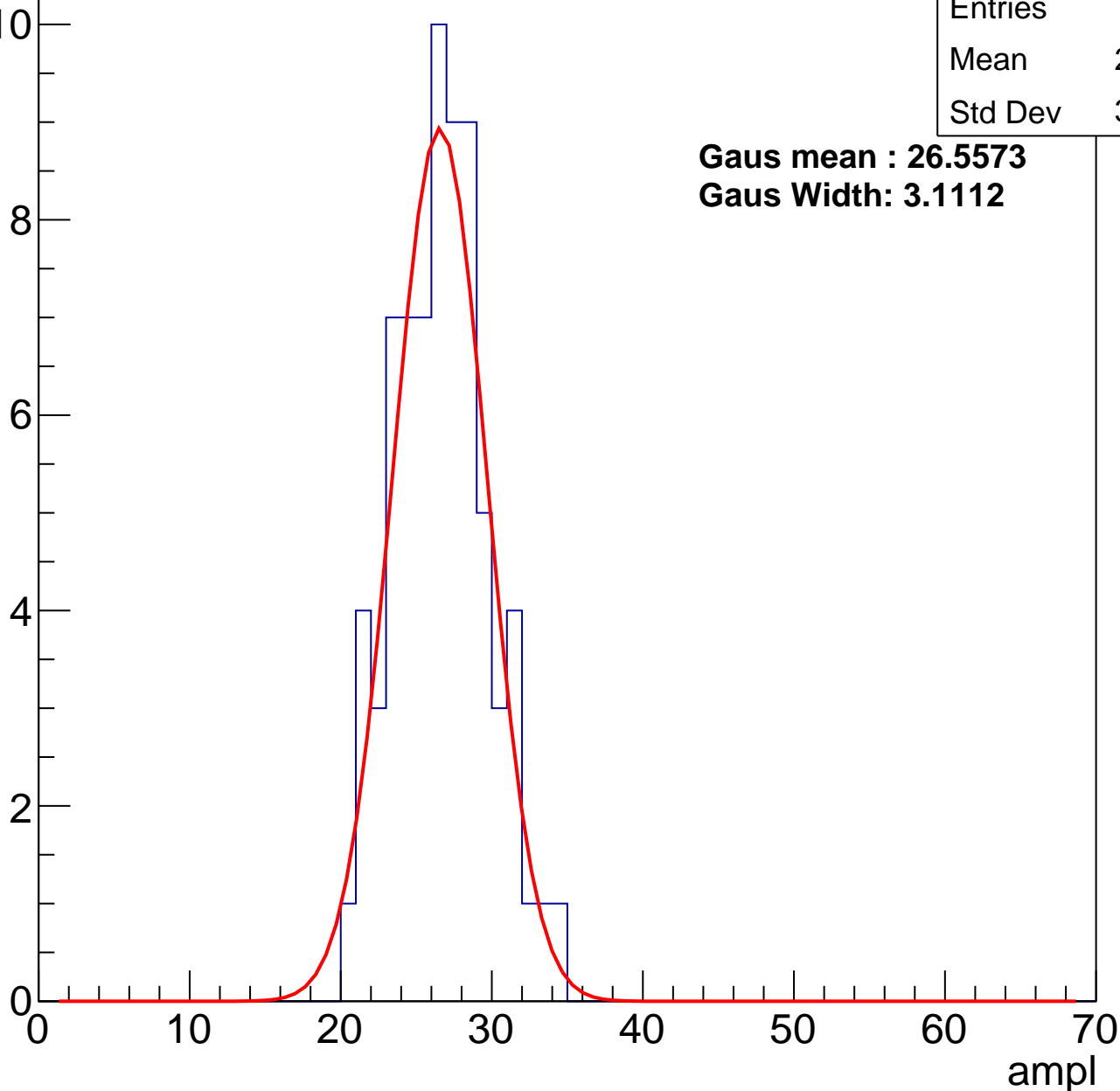
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	26.21
Std Dev	3.041

**Gaus mean : 26.5573**

**Gaus Width: 3.1112**



# B1L000S, U2-ch30, adc1

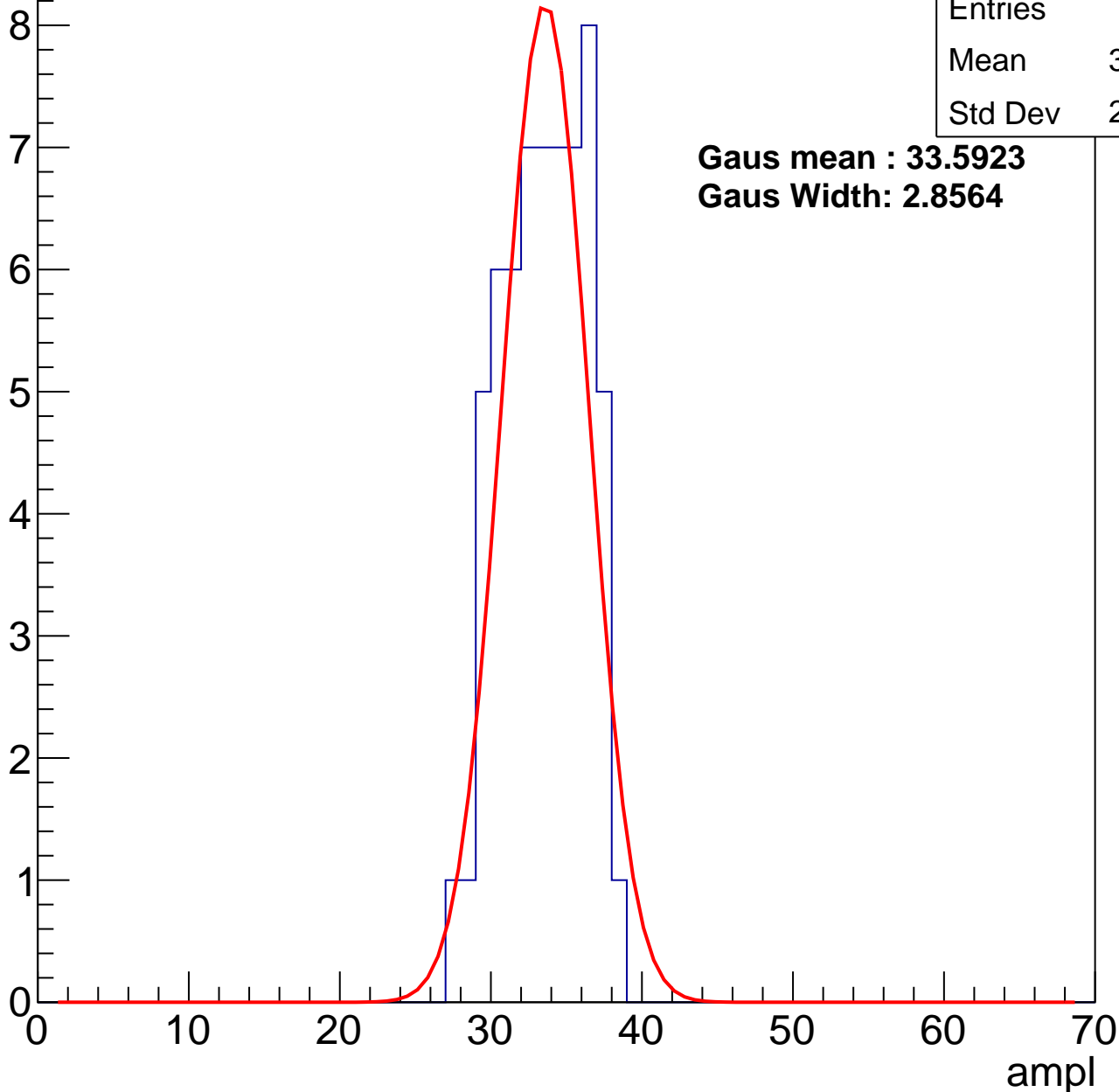
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	33.03
Std Dev	2.679

**Gaus mean : 33.5923**

**Gaus Width: 2.8564**



# B1L000S, U2-ch30, adc2

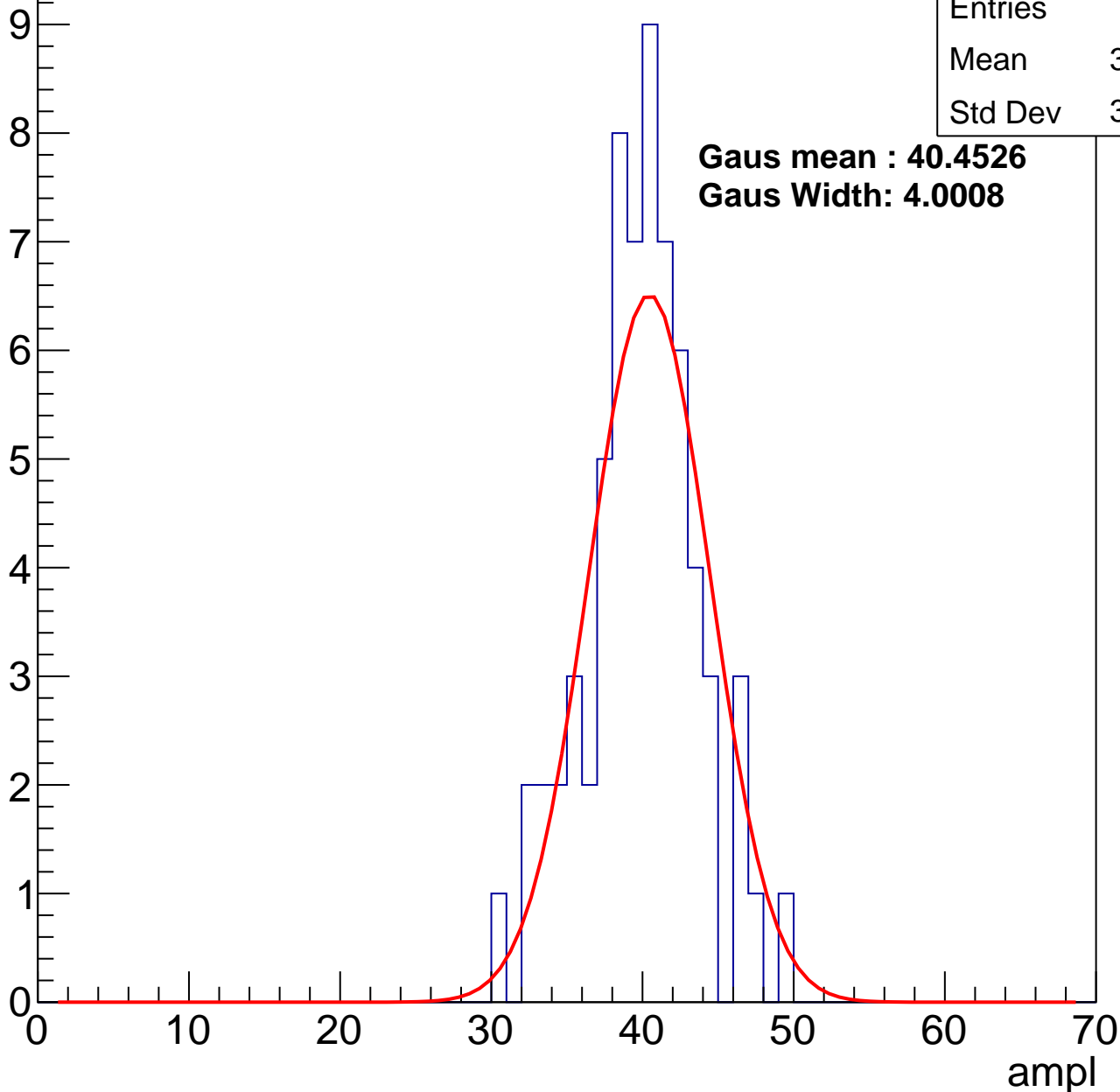
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	39.45
Std Dev	3.738

**Gaus mean : 40.4526**

**Gaus Width: 4.0008**



# B1L000S, U2-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

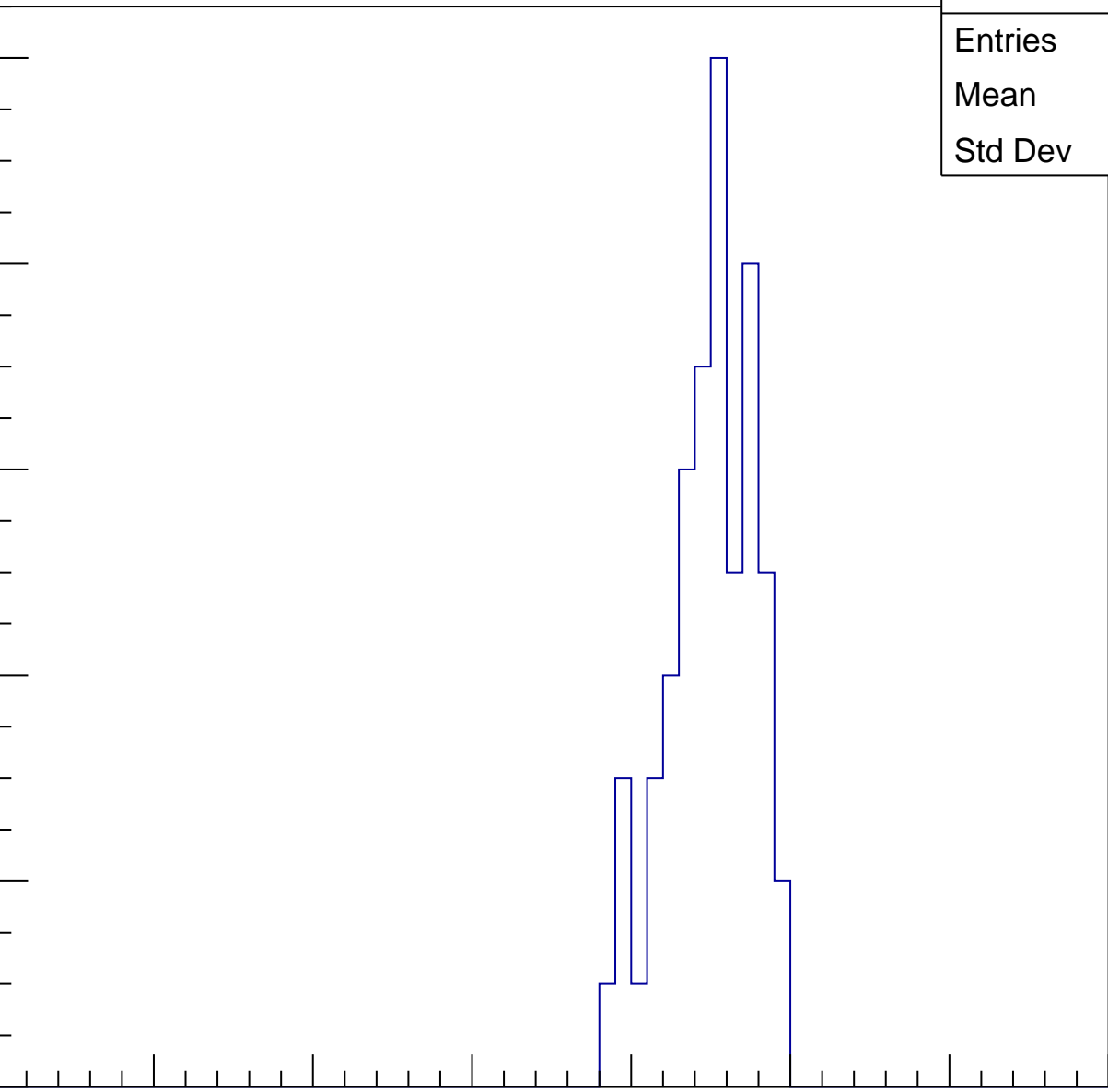
Entries	55
Mean	44.47
Std Dev	2.682

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

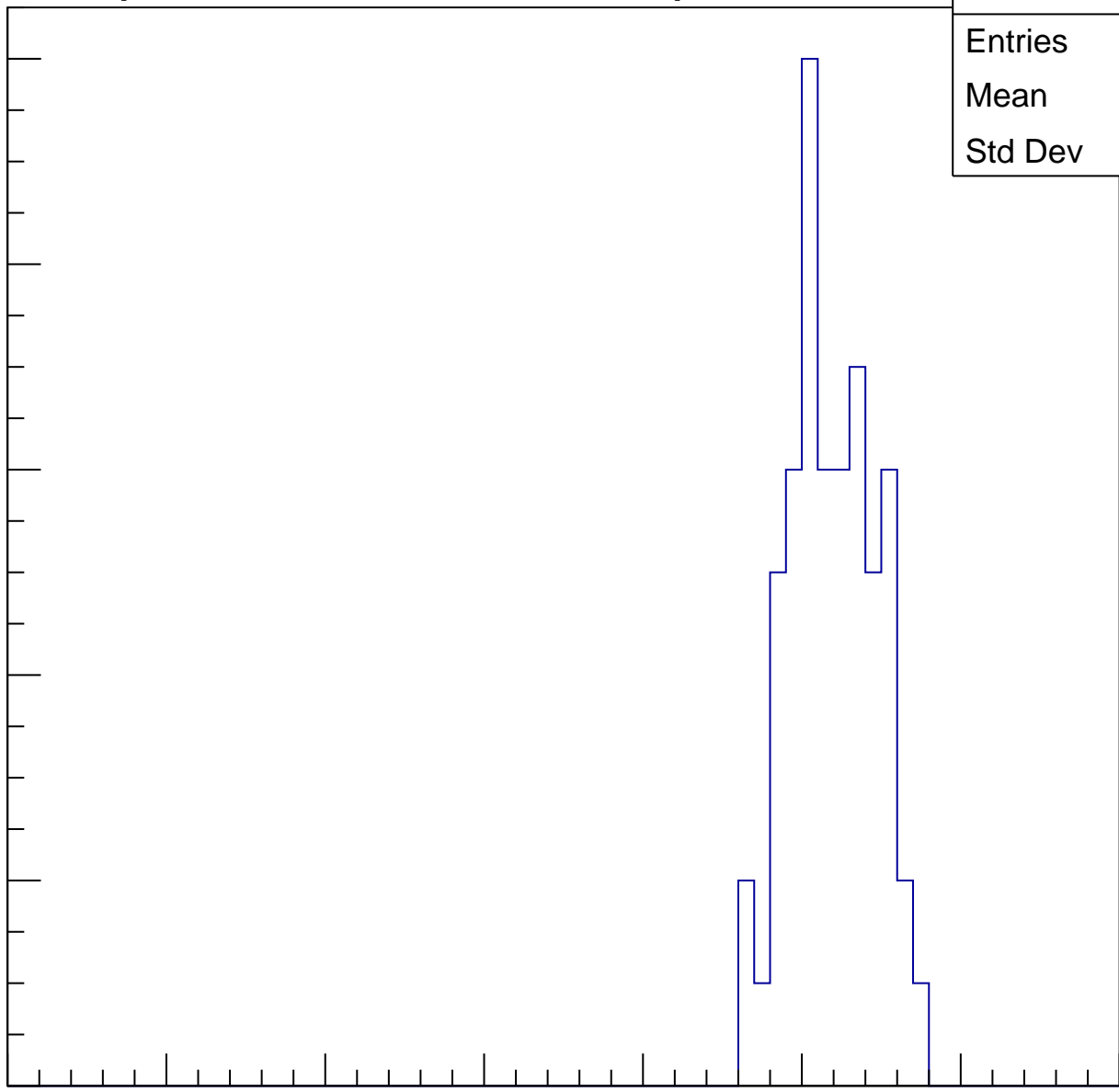
Entries	57
Mean	51.42
Std Dev	2.635

Entry

10  
8  
6  
4  
2  
0

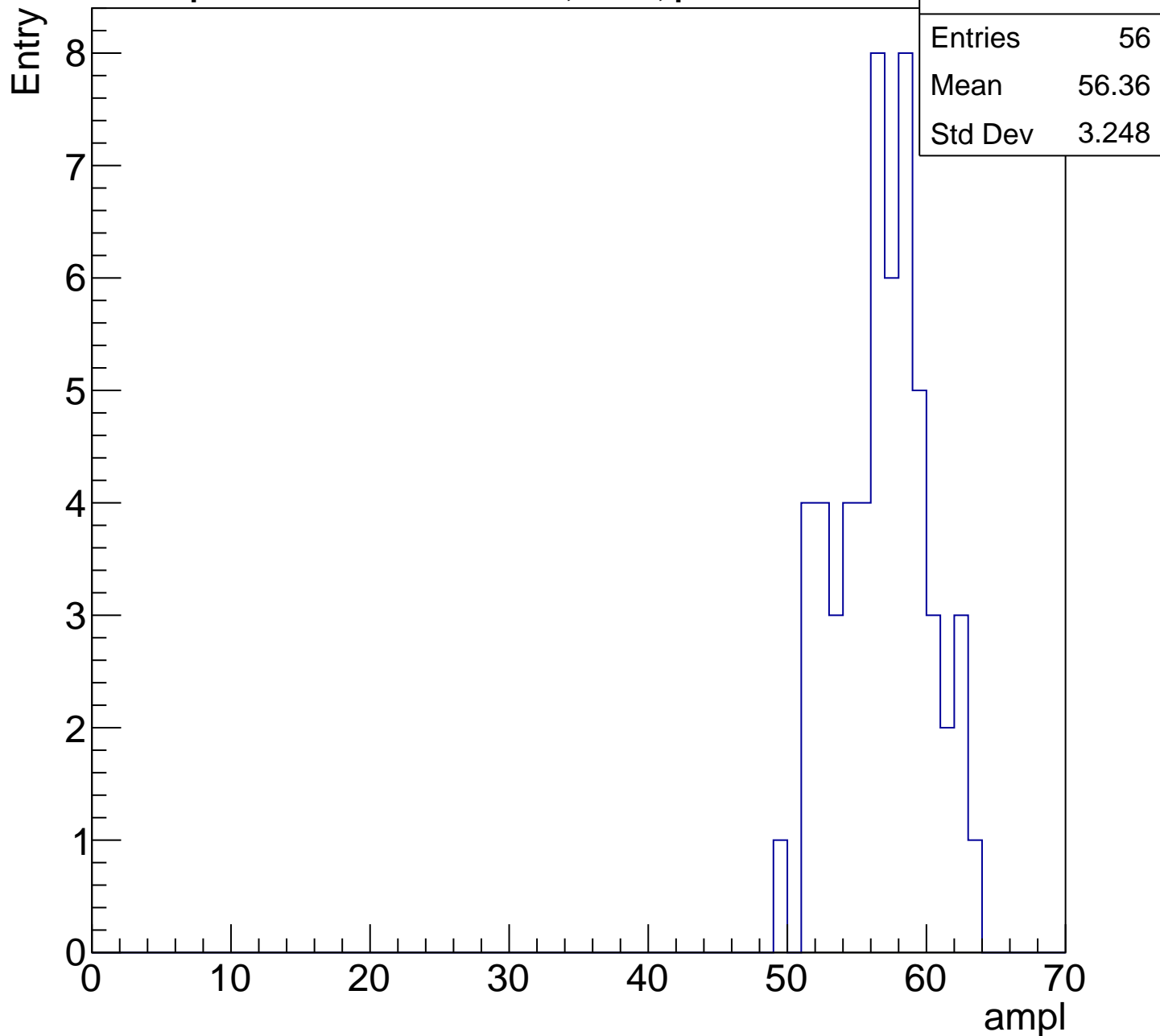
0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

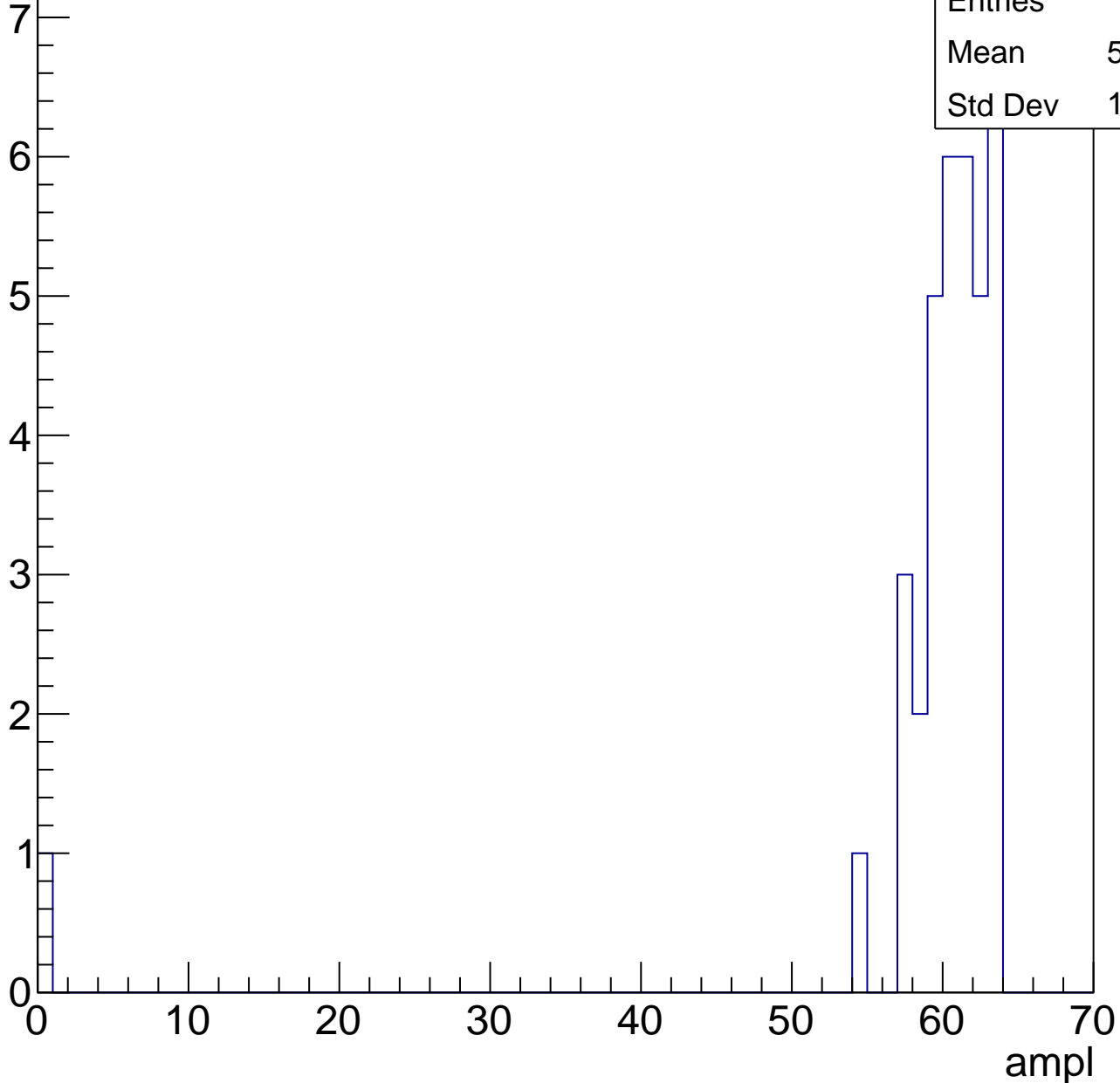


# B1L000S, U2-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	36
Mean	58.69
Std Dev	10.14





# B1L000S, U2-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

0

10

20

30

40

50

60

70

ampl

Entries	8
Mean	56.38
Std Dev	14.16

# B1L000S, U2-ch31, adc0

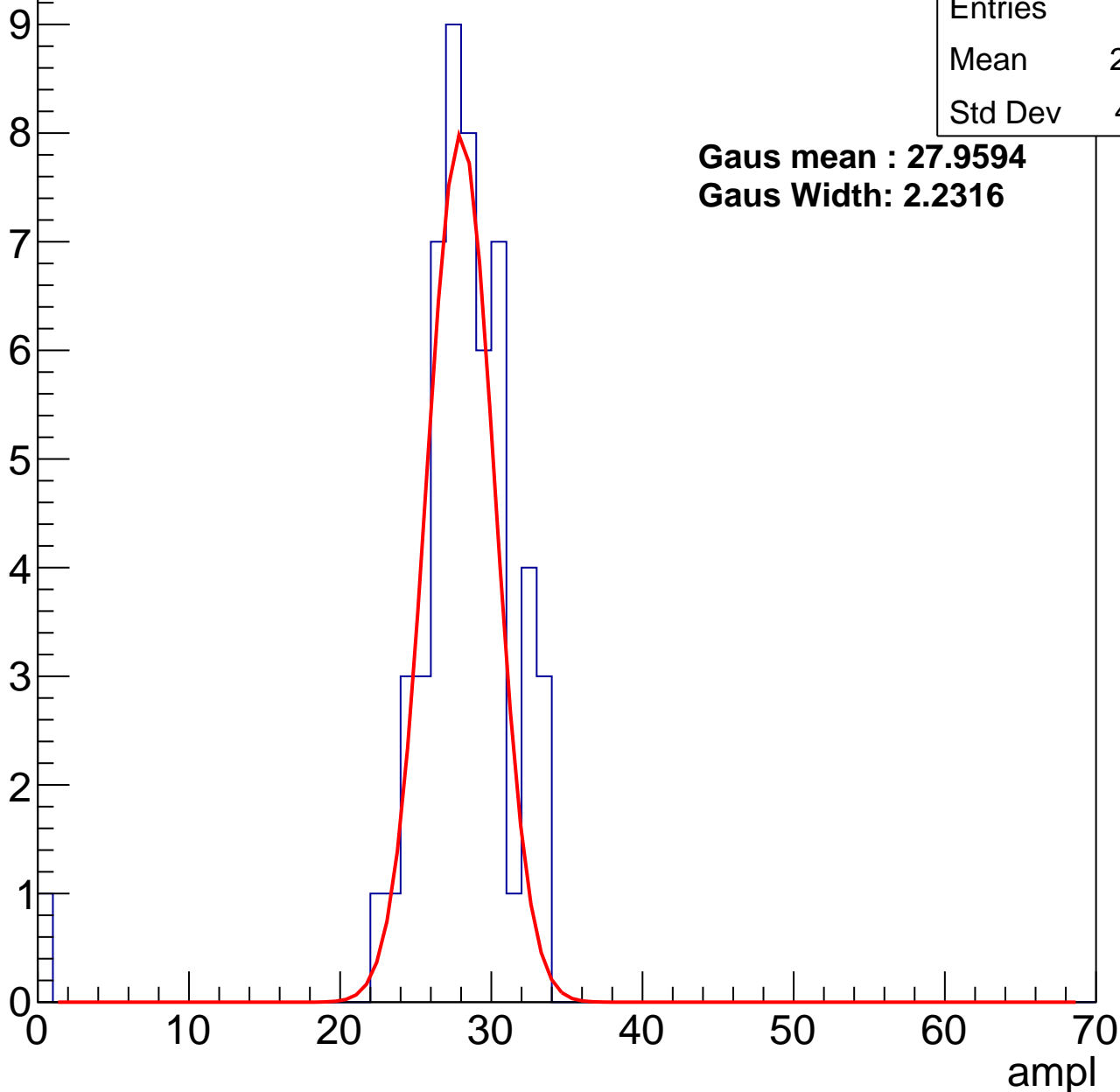
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	27.46
Std Dev	4.561

**Gaus mean : 27.9594**

**Gaus Width: 2.2316**



# B1L000S, U2-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.21
Std Dev	3.219

**Gaus mean : 34.4004**

**Gaus Width: 3.6269**

10

8

6

4

2

0

0

10

20

30

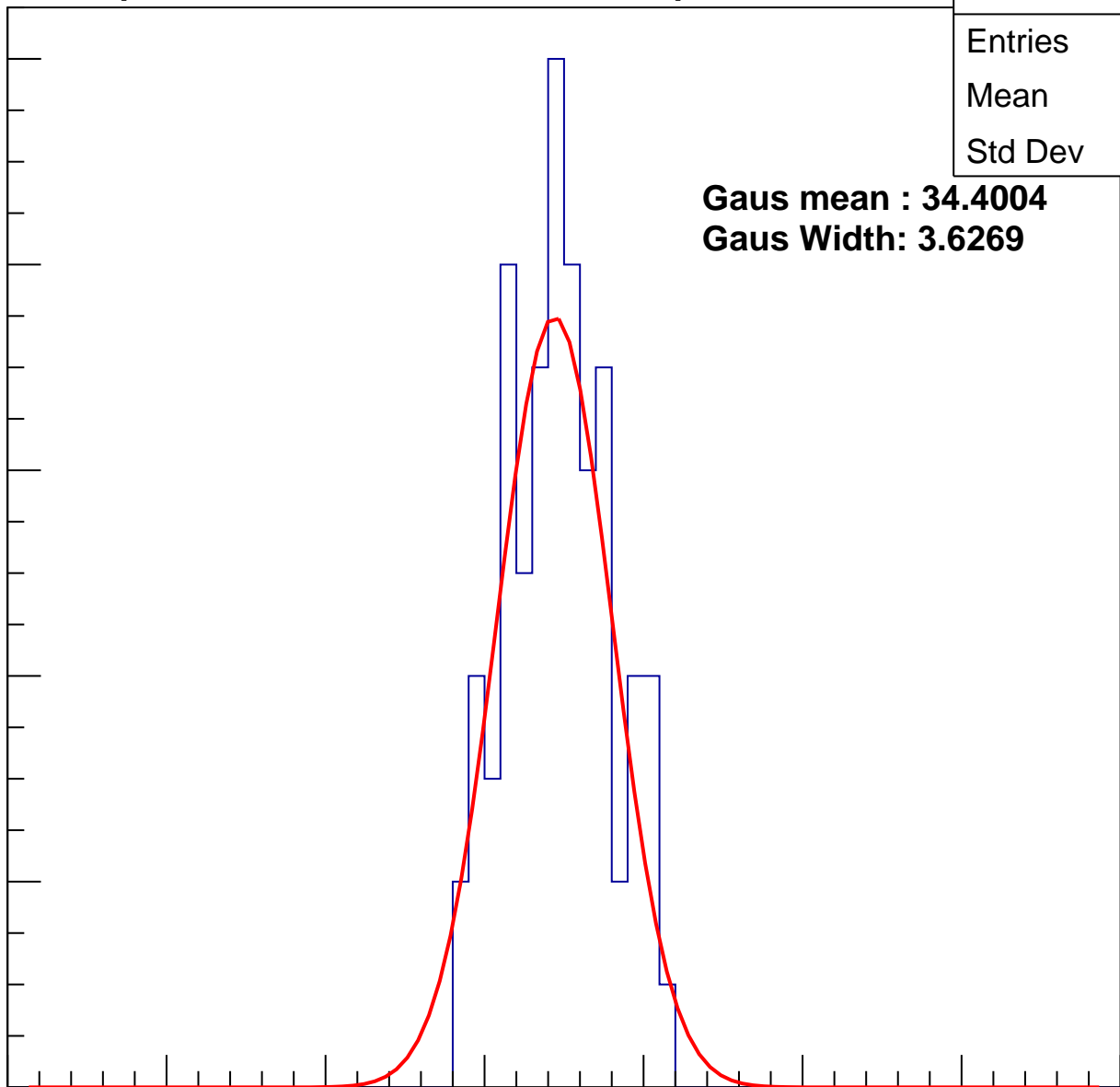
40

50

60

70

ampl



# B1L000S, U2-ch31, adc2

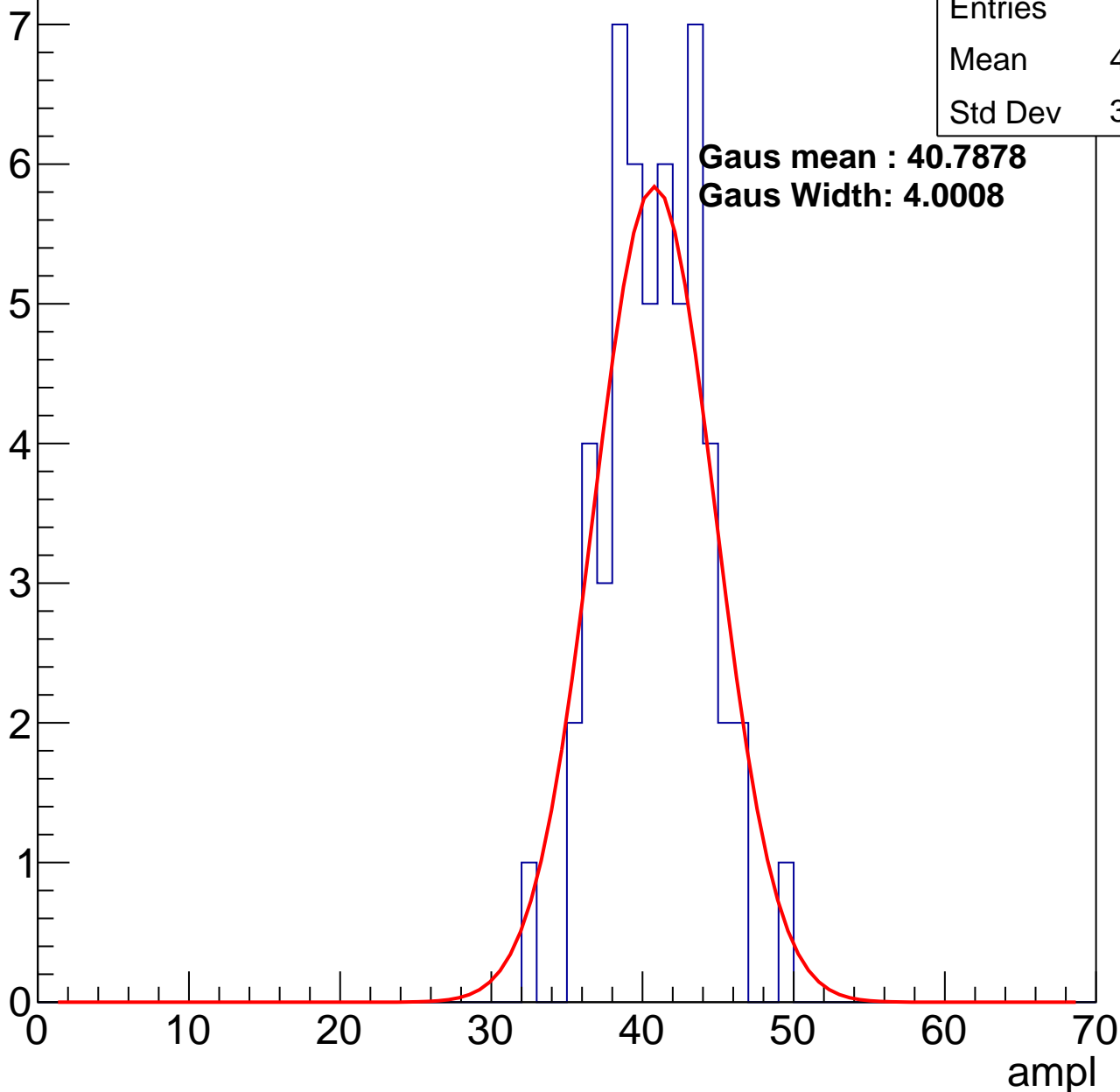
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	40.38
Std Dev	3.267

**Gaus mean : 40.7878**

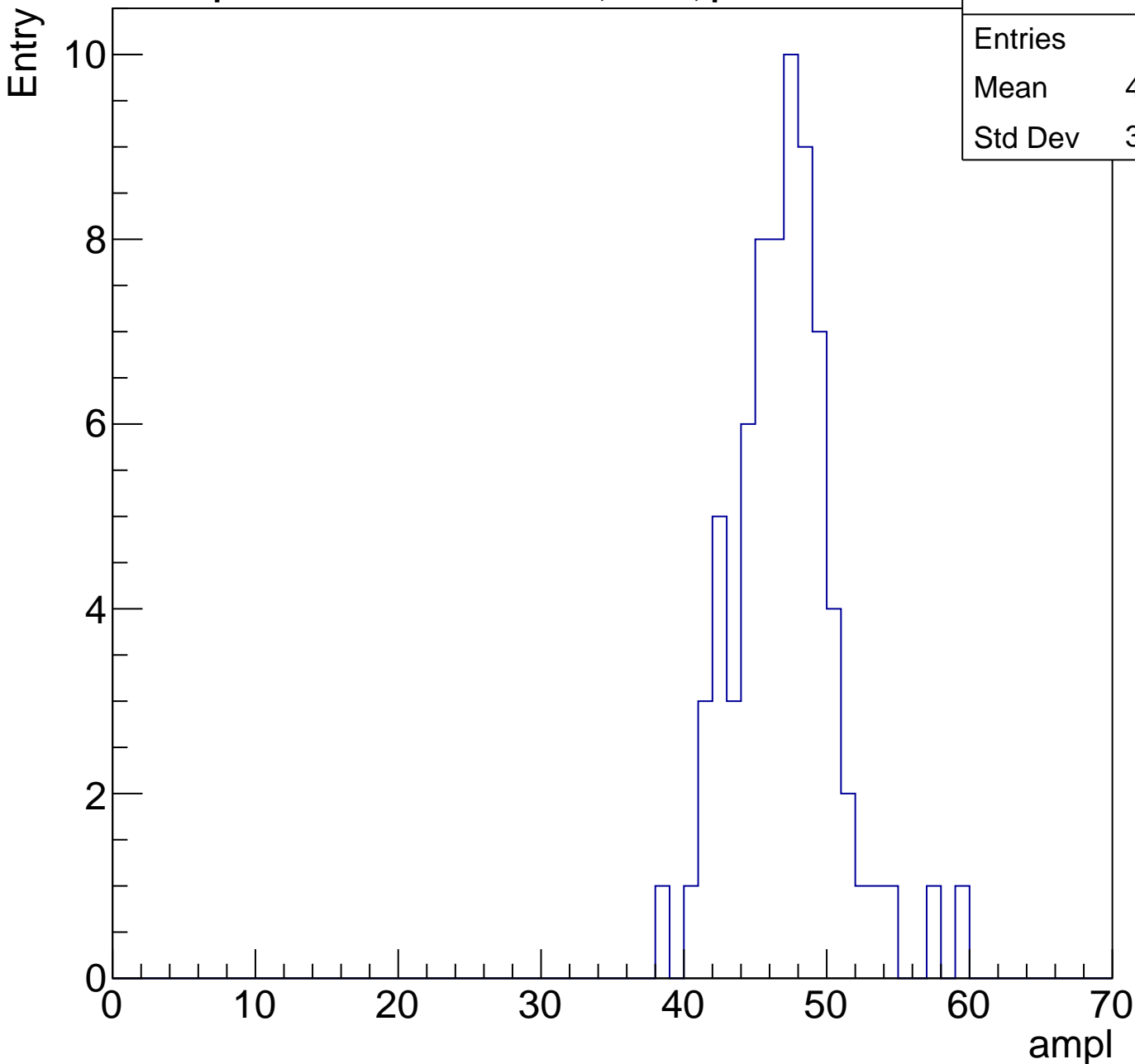
**Gaus Width: 4.0008**



# B1L000S, U2-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

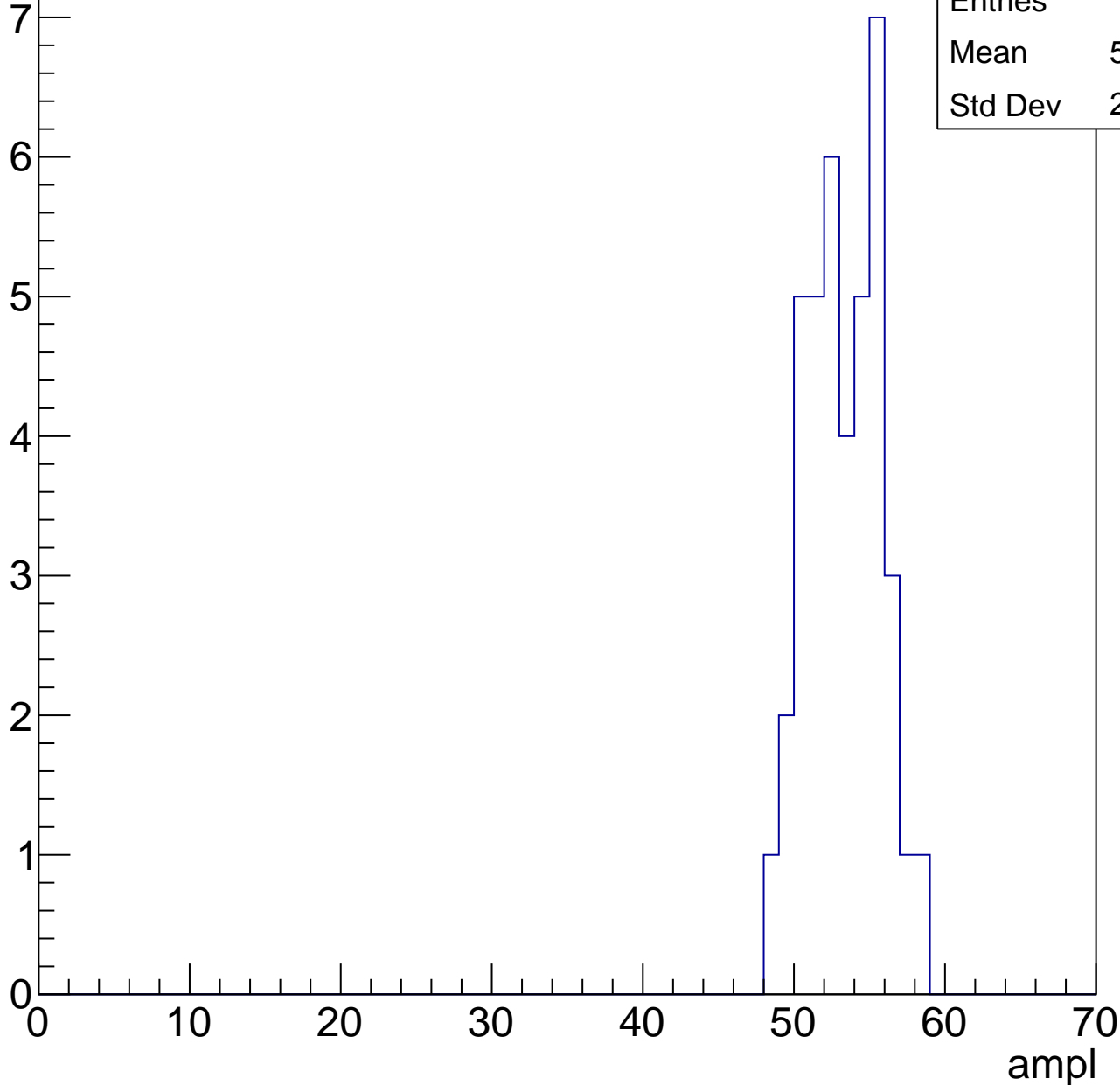
Entries	72
Mean	46.58
Std Dev	3.635



# B1L000S, U2-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

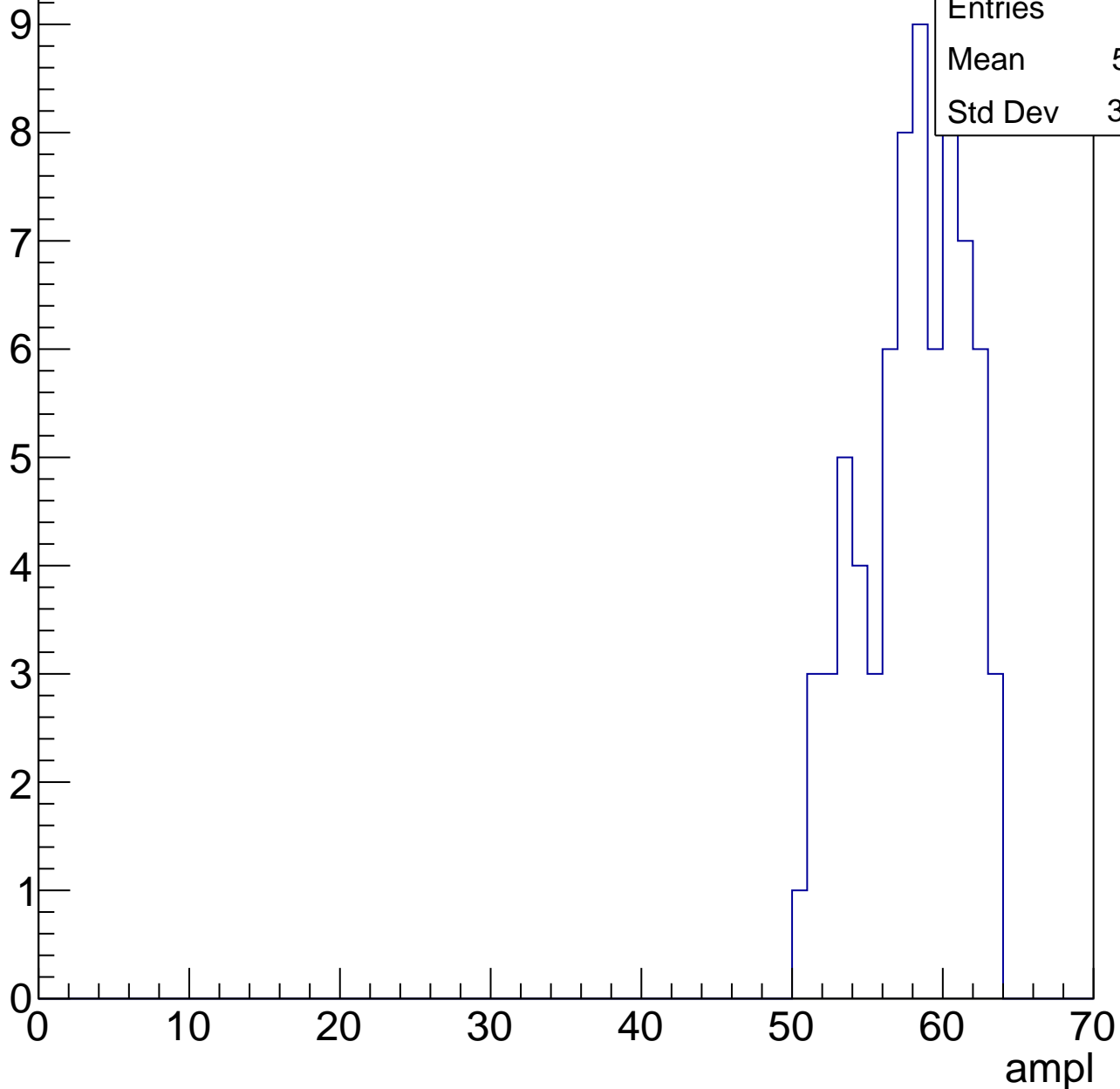
Entry



# B1L000S, U2-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



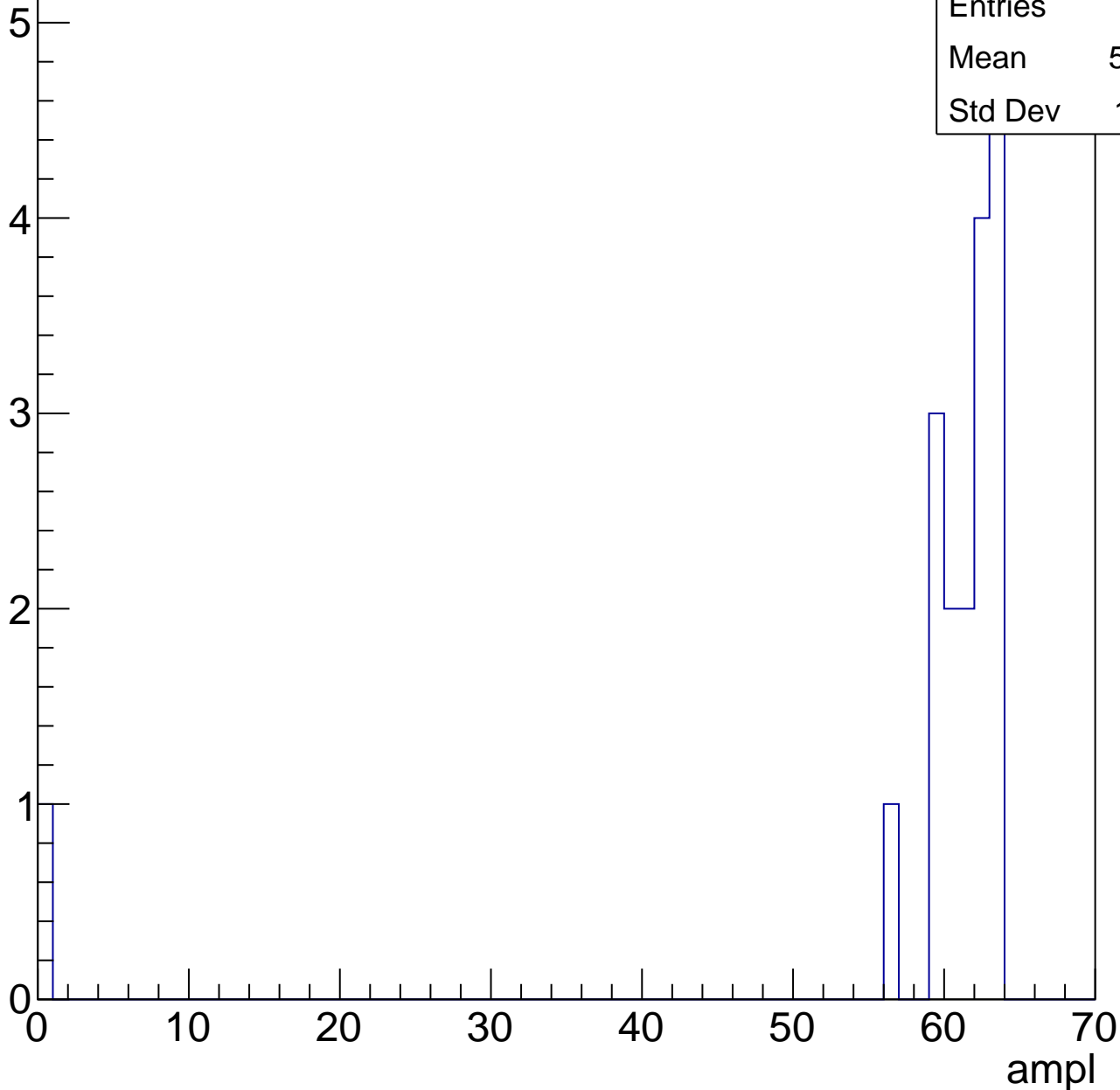
Entries	72
Mean	57.51
Std Dev	3.379

# B1L000S, U2-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	57.67
Std Dev	14.11

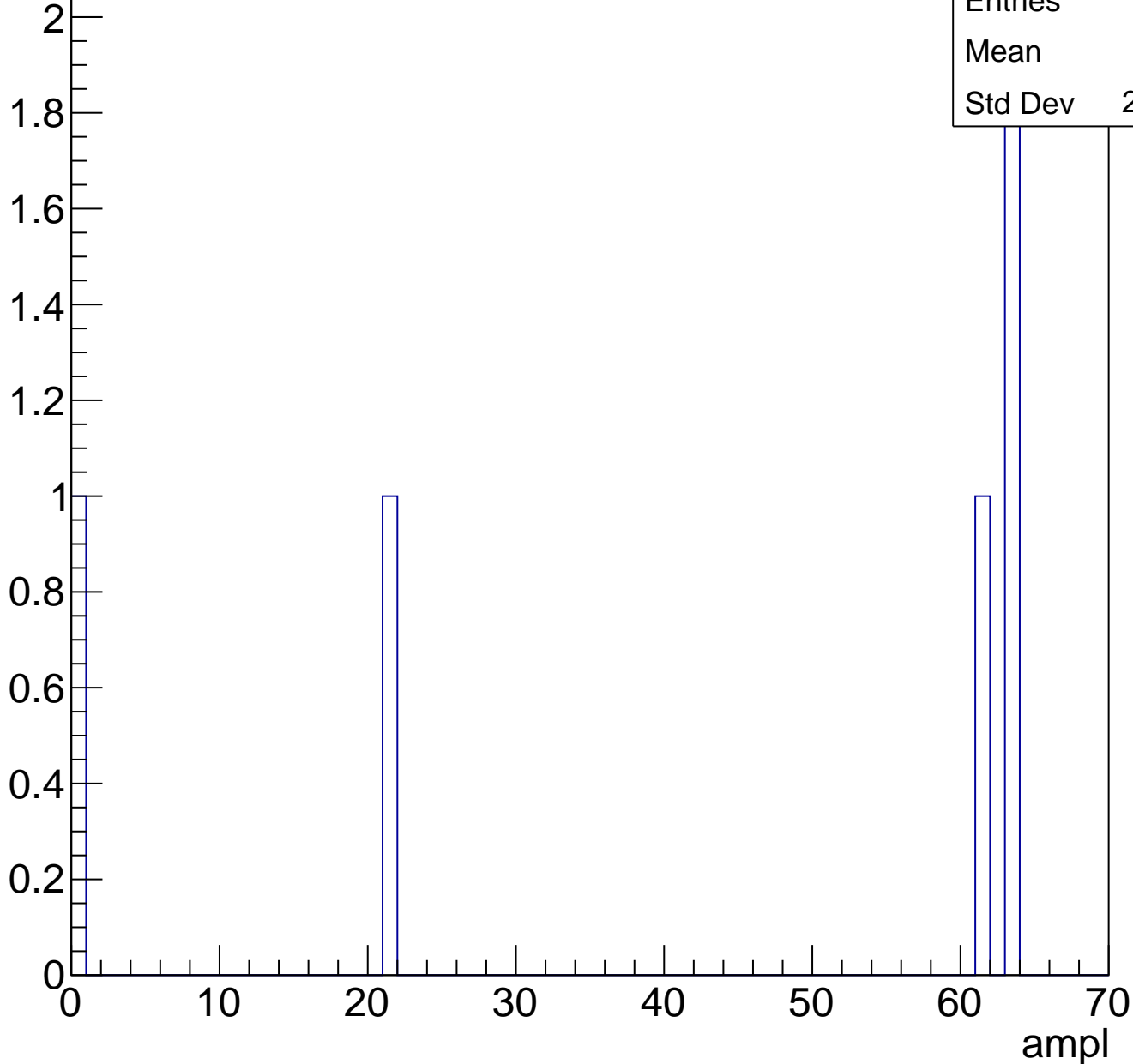




# B1L000S, U2-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch32, adc0

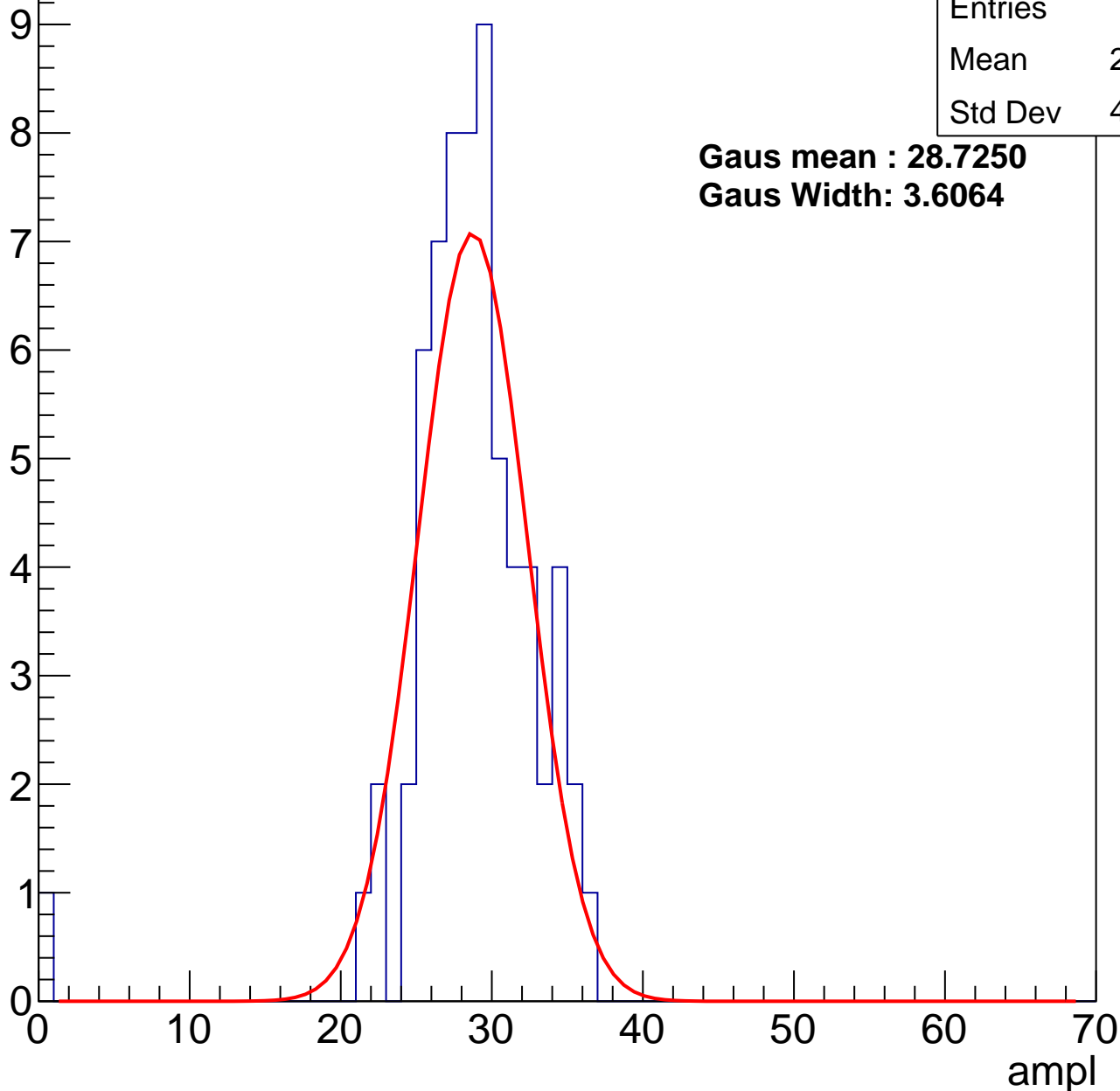
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	28.12
Std Dev	4.785

**Gaus mean : 28.7250**

**Gaus Width: 3.6064**



# B1L000S, U2-ch32, adc1

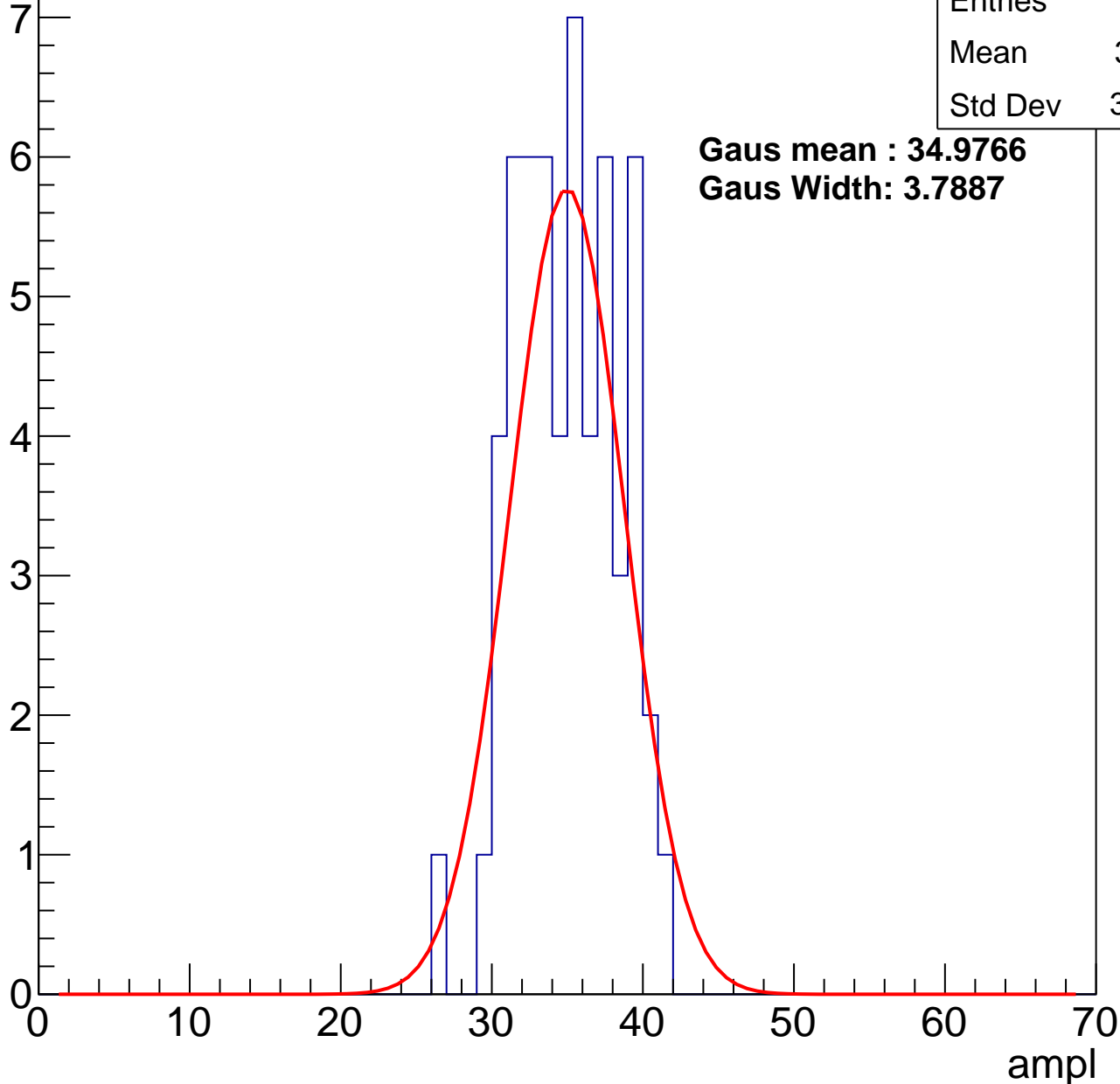
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	34.51
Std Dev	3.293

**Gaus mean : 34.9766**

**Gaus Width: 3.7887**



# B1L000S, U2-ch32, adc2

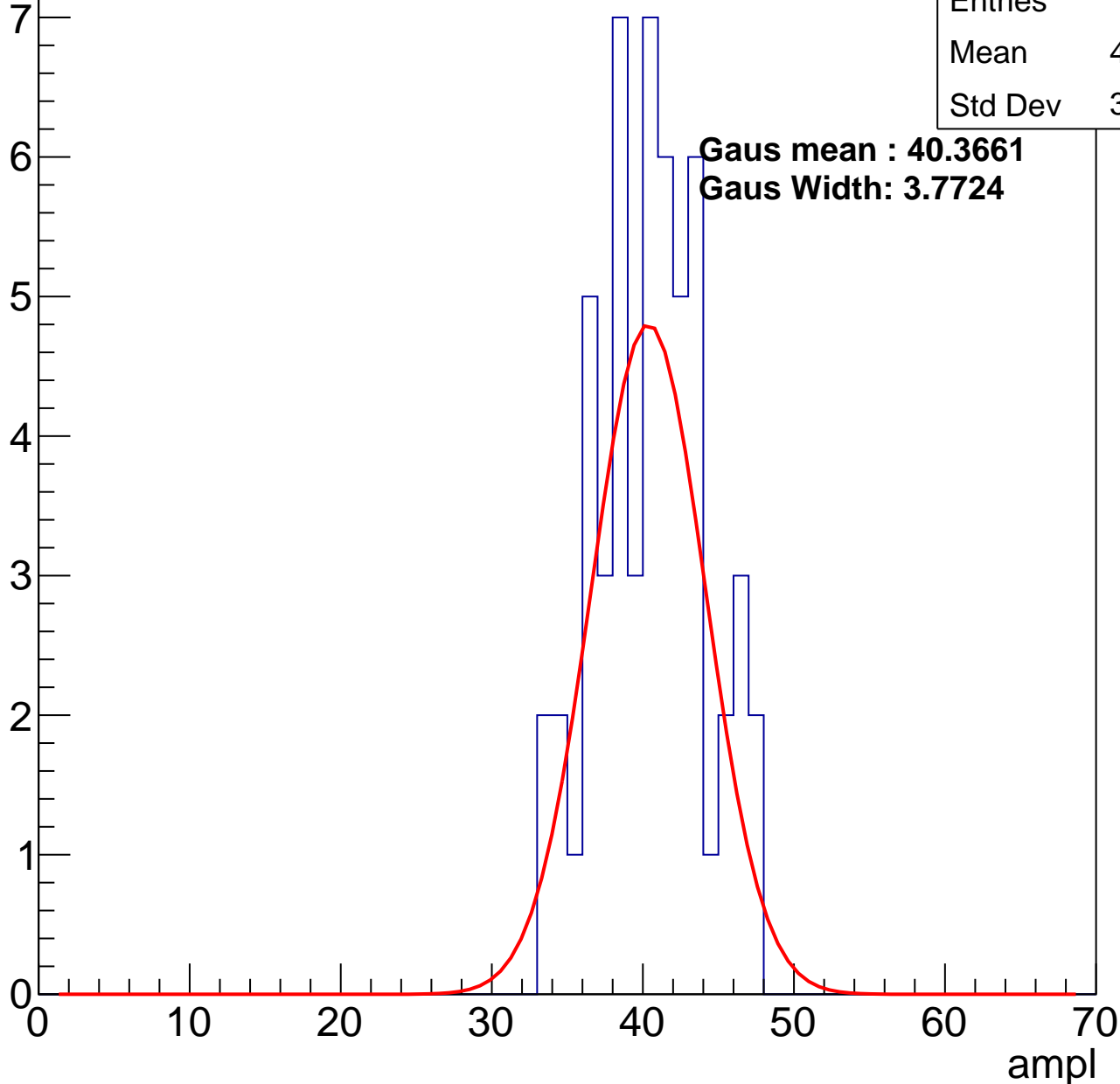
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	40.05
Std Dev	3.529

**Gaus mean : 40.3661**

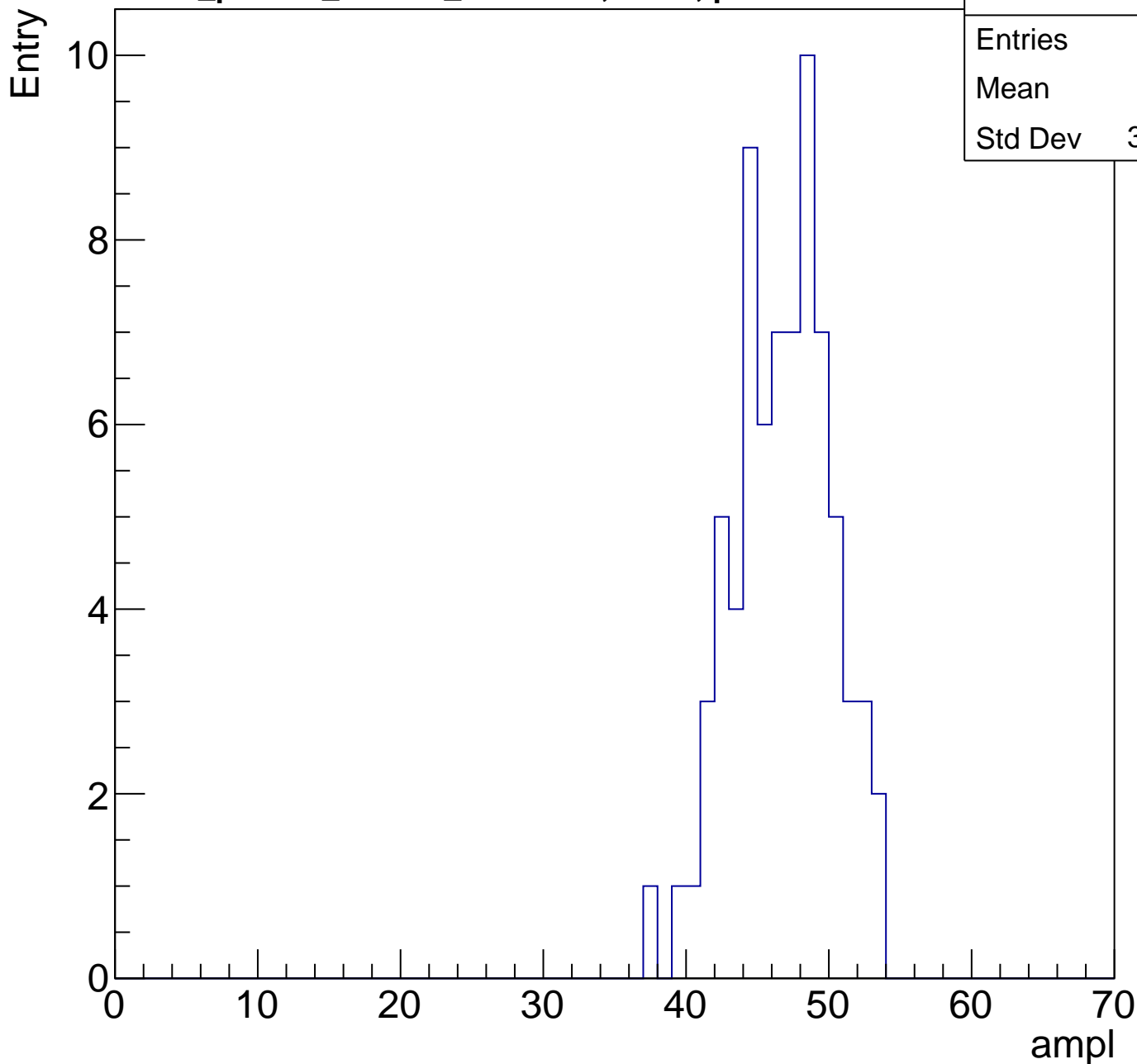
**Gaus Width: 3.7724**



# B1L000S, U2-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	46.3
Std Dev	3.416

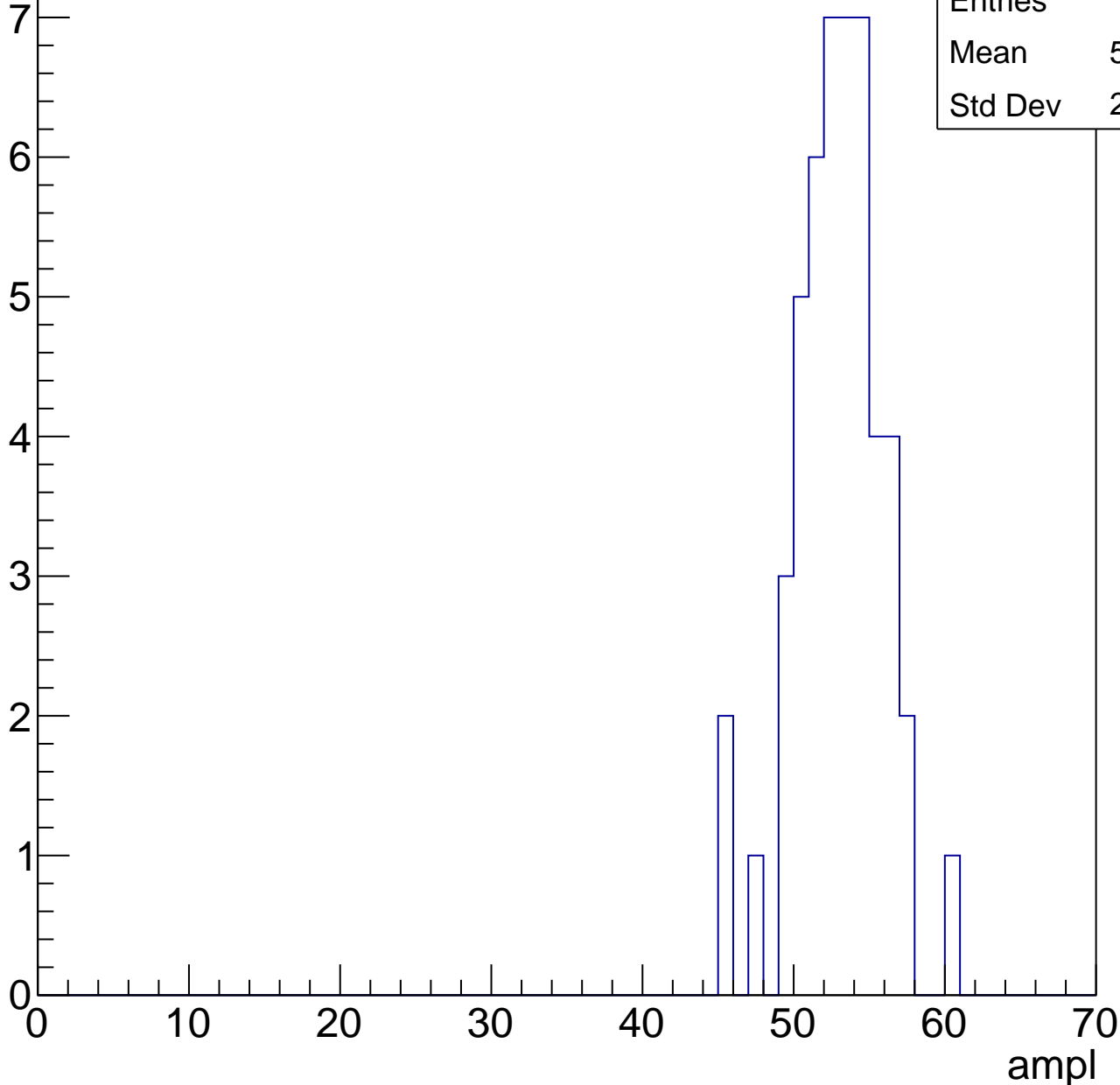


# B1L000S, U2-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	52.47
Std Dev	2.907

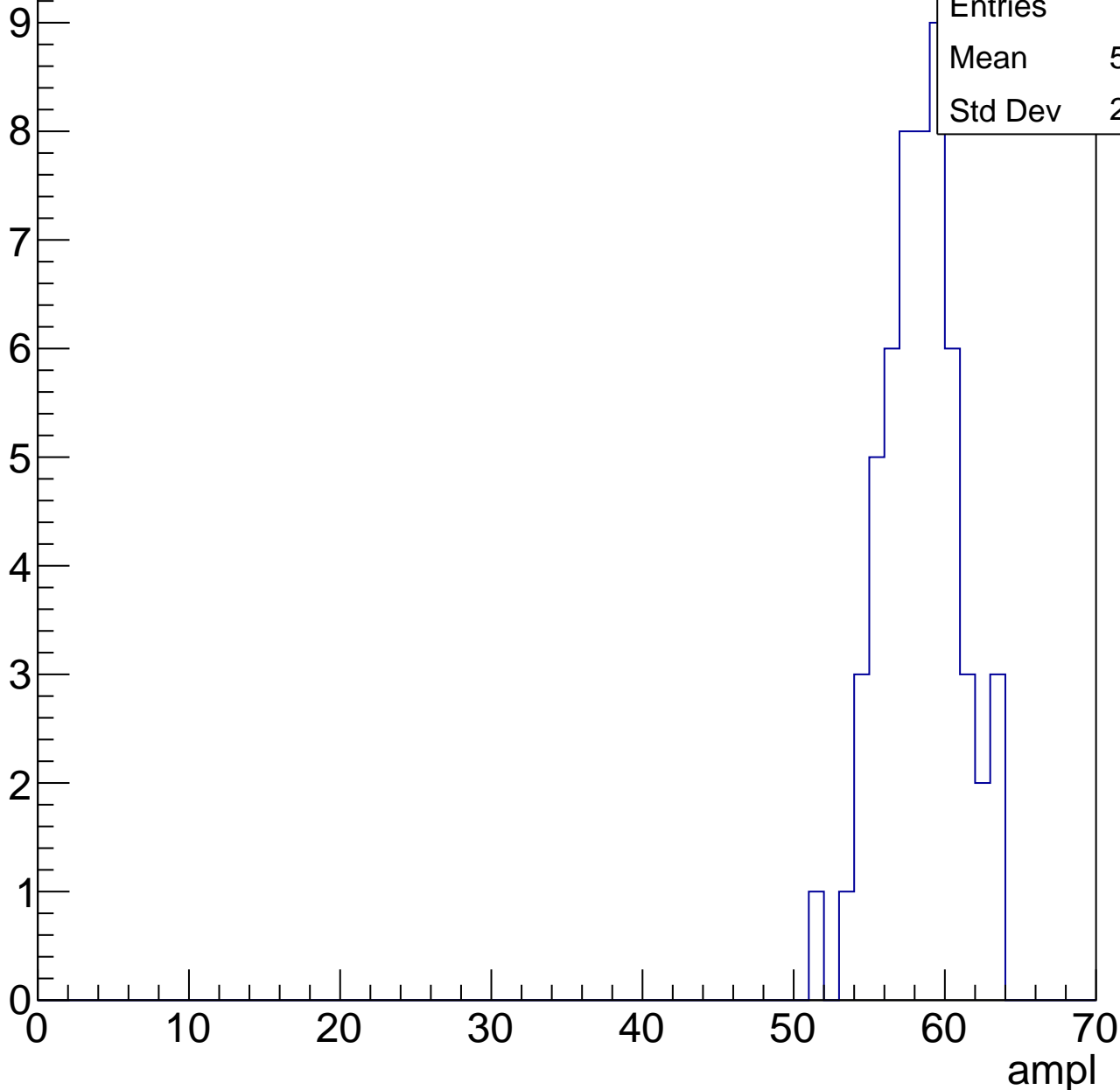


# B1L000S, U2-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	57.89
Std Dev	2.577

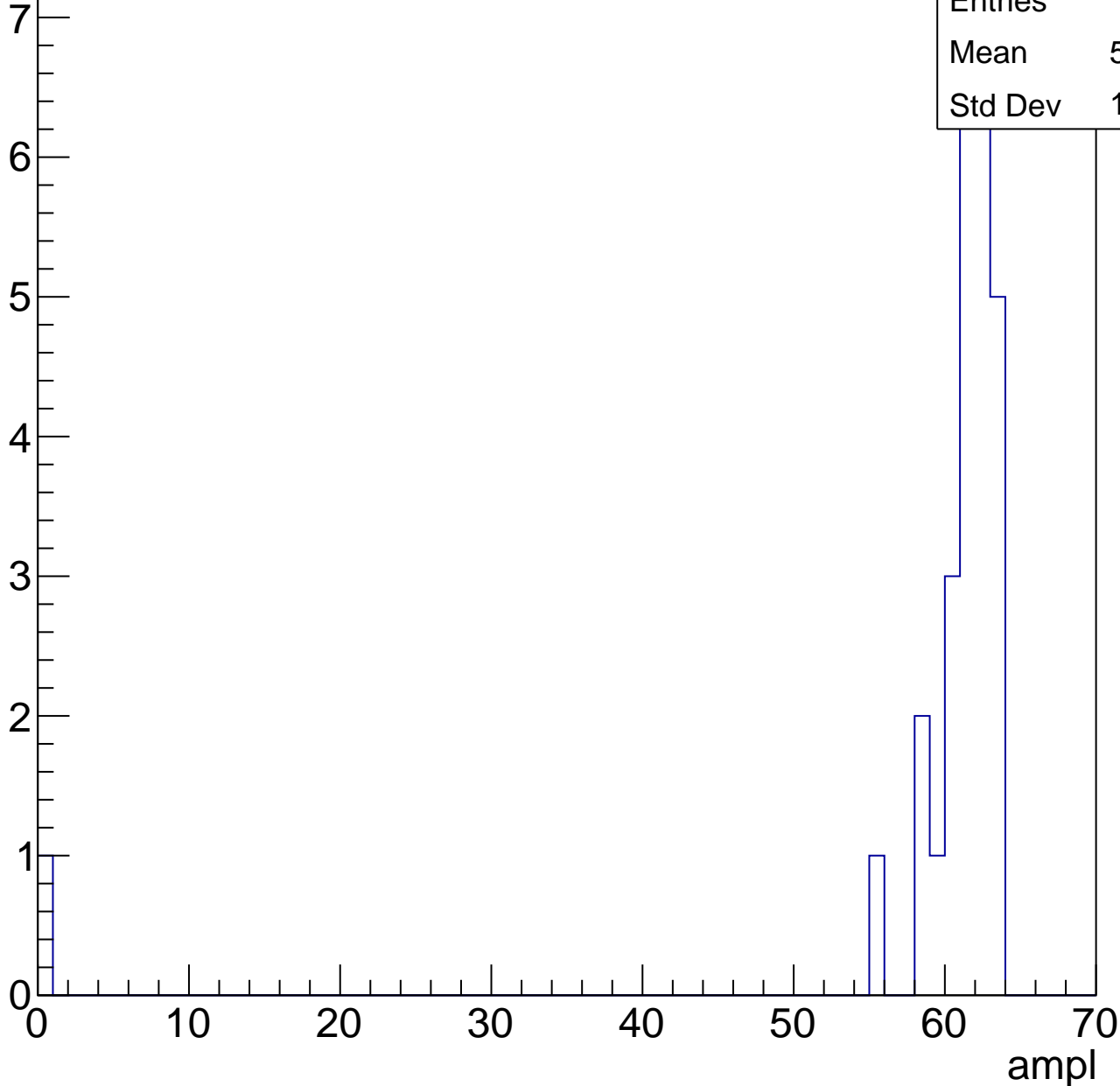


# B1L000S, U2-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	58.74
Std Dev	11.66

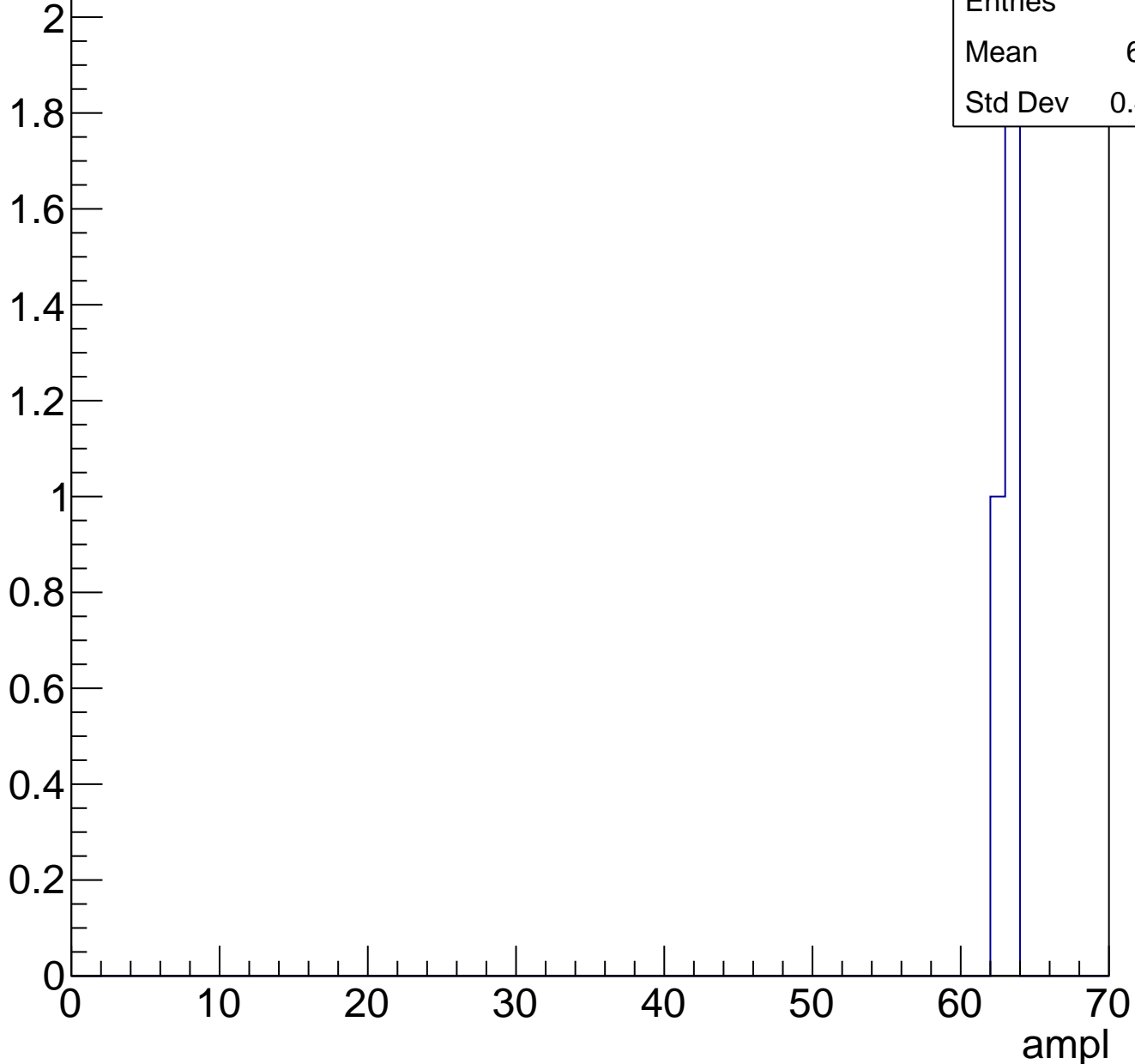




# B1L000S, U2-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch33, adc0

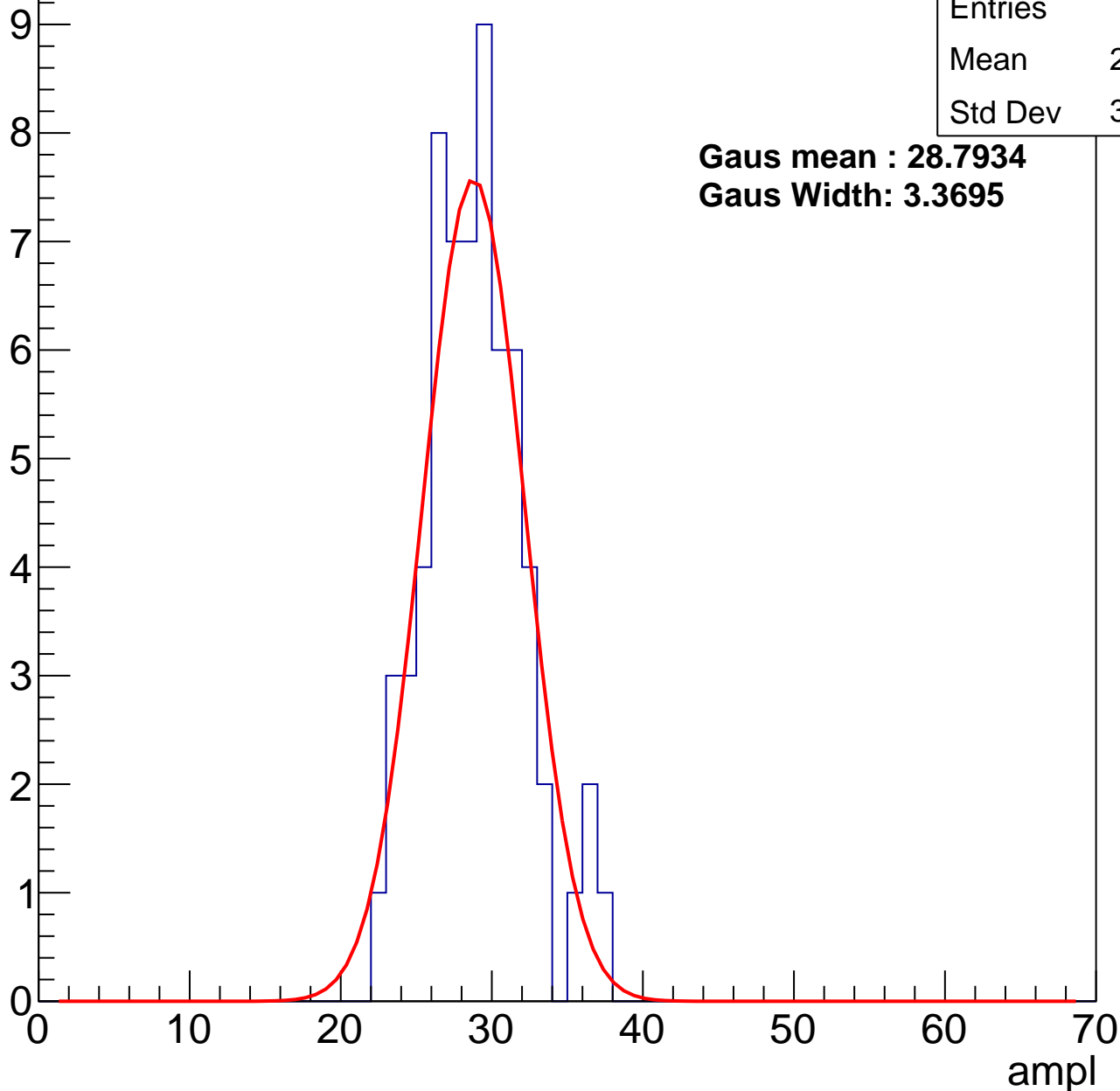
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.45
Std Dev	3.259

**Gaus mean : 28.7934**

**Gaus Width: 3.3695**



# B1L000S, U2-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	35.19
Std Dev	3.45

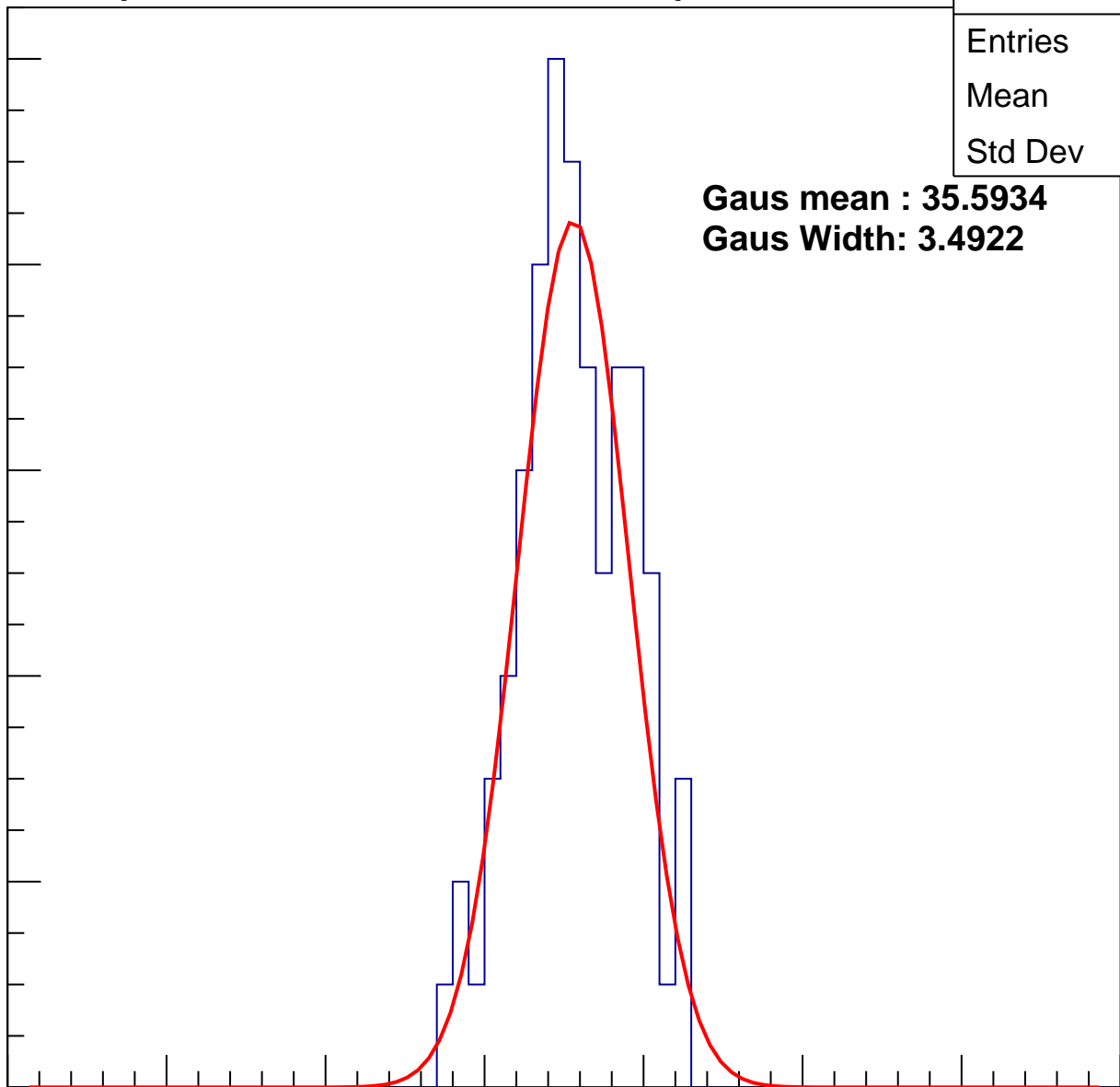
**Gaus mean : 35.5934**

**Gaus Width: 3.4922**

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch33, adc2

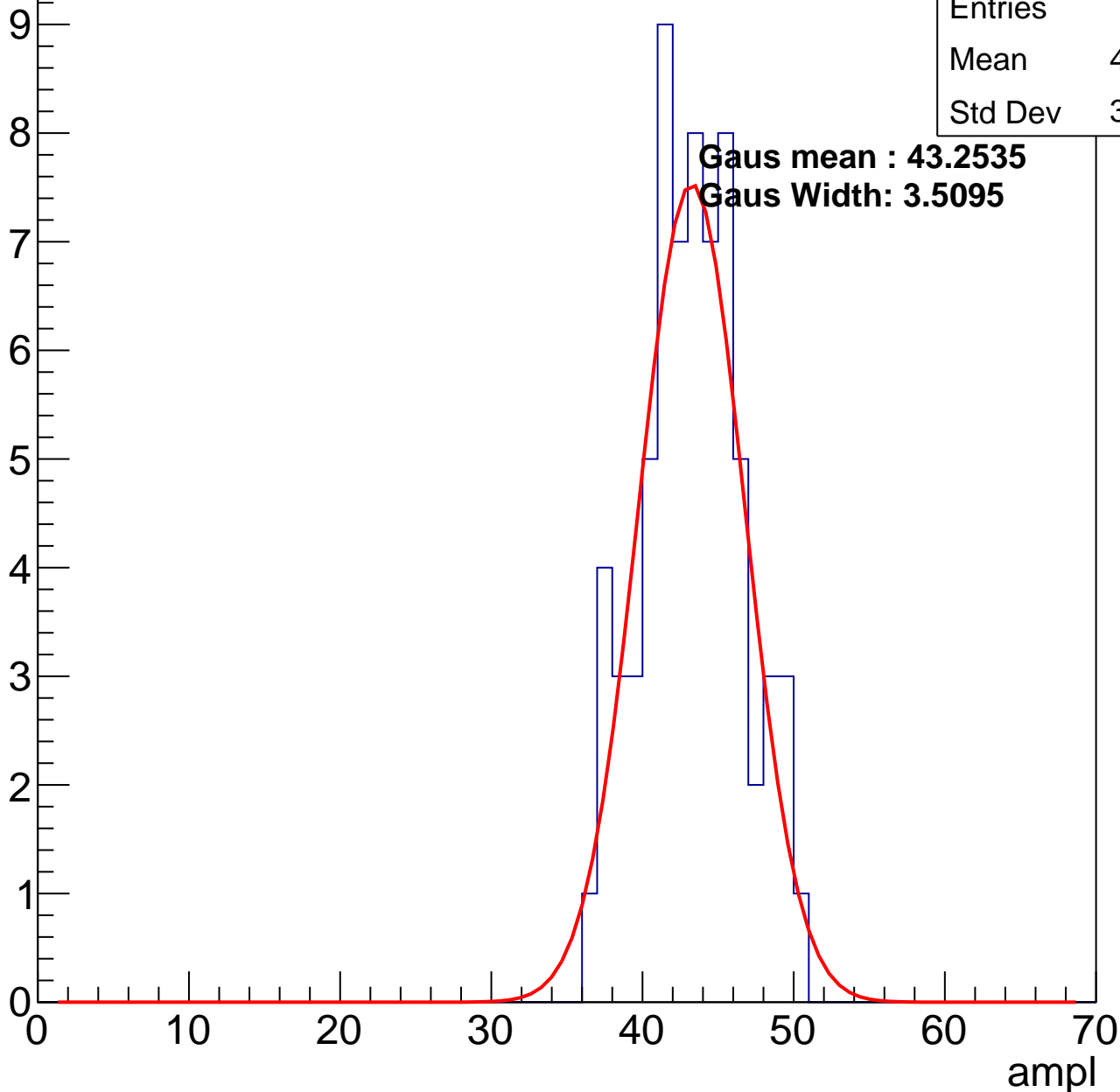
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	42.83
Std Dev	3.297

**Gaus mean : 43.2535**

**Gaus Width: 3.5095**

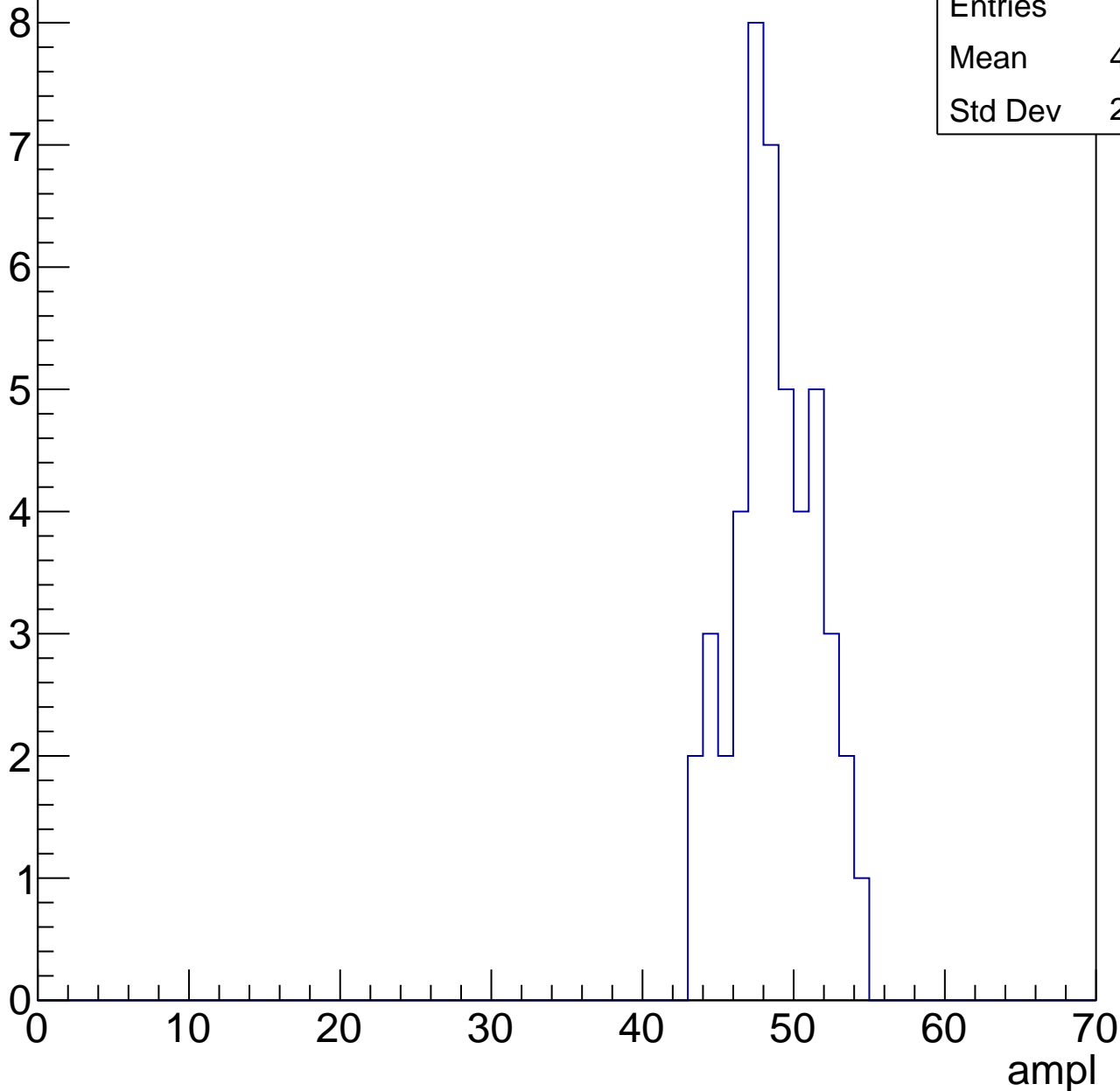


# B1L000S, U2-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	48.26
Std Dev	2.706

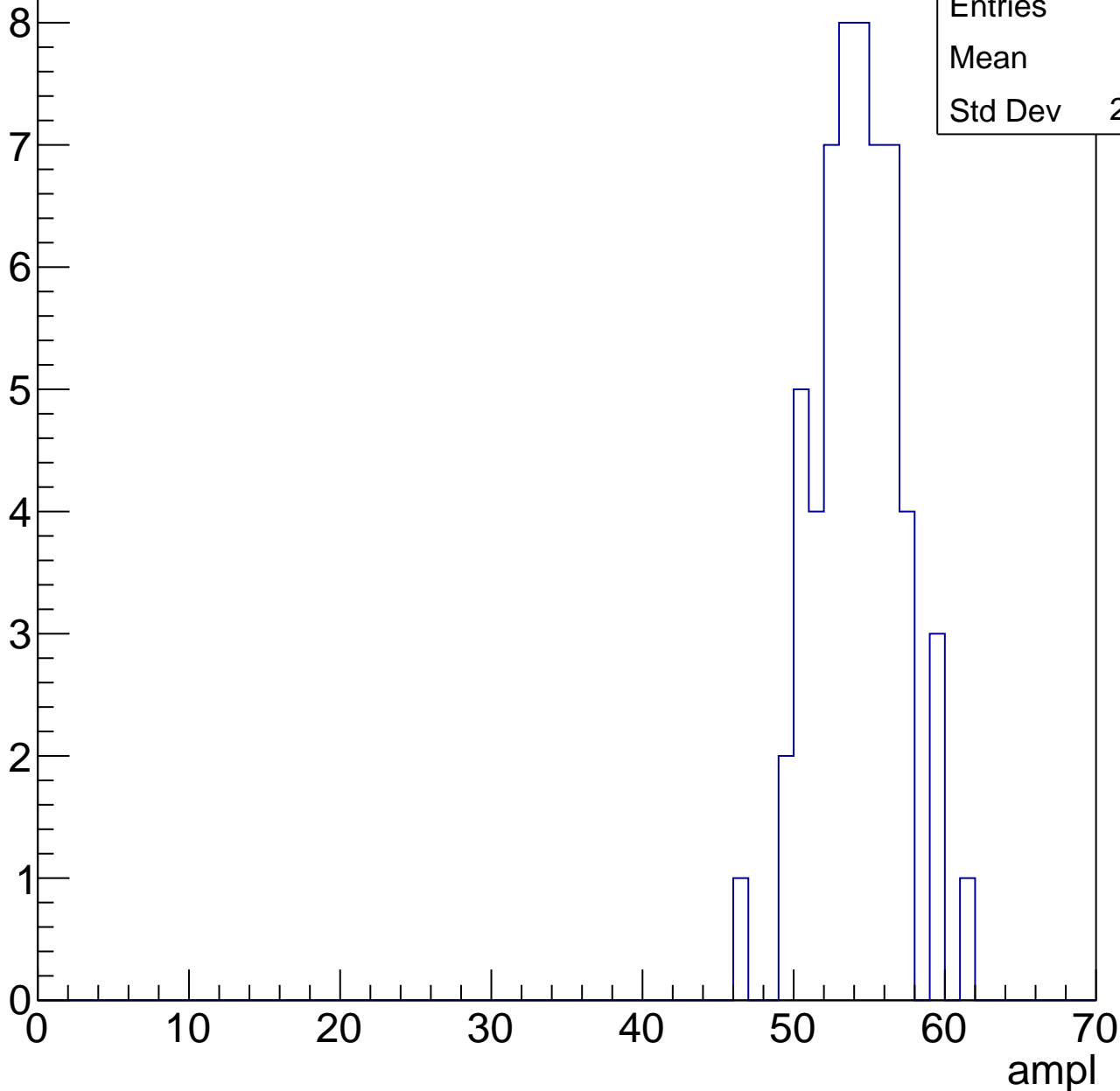


# B1L000S, U2-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	53.7
Std Dev	2.828



# B1L000S, U2-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	59.05
Std Dev	2.545

Entry

10

8

6

4

2

0

0

10

20

30

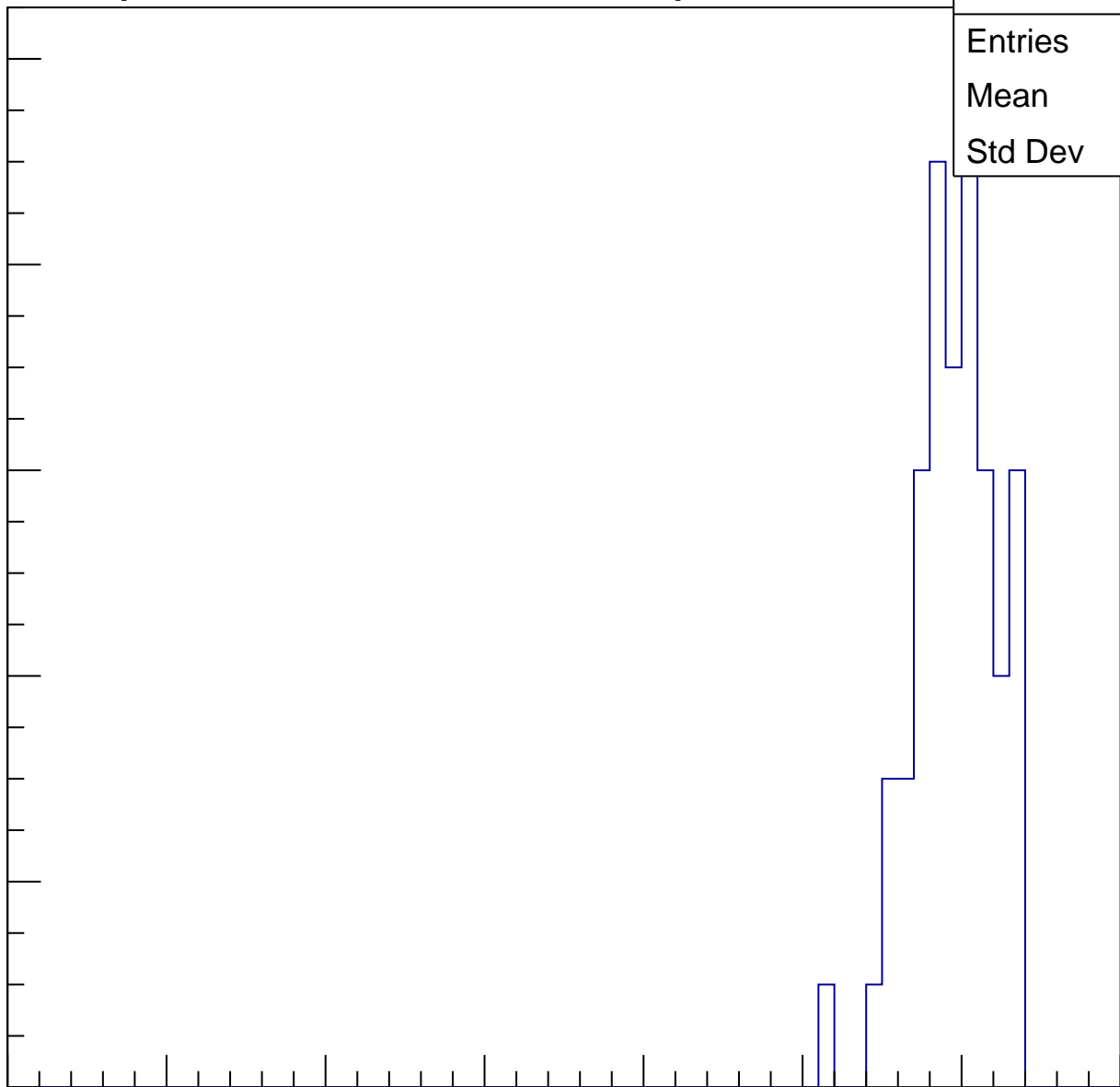
40

50

60

70

ampl

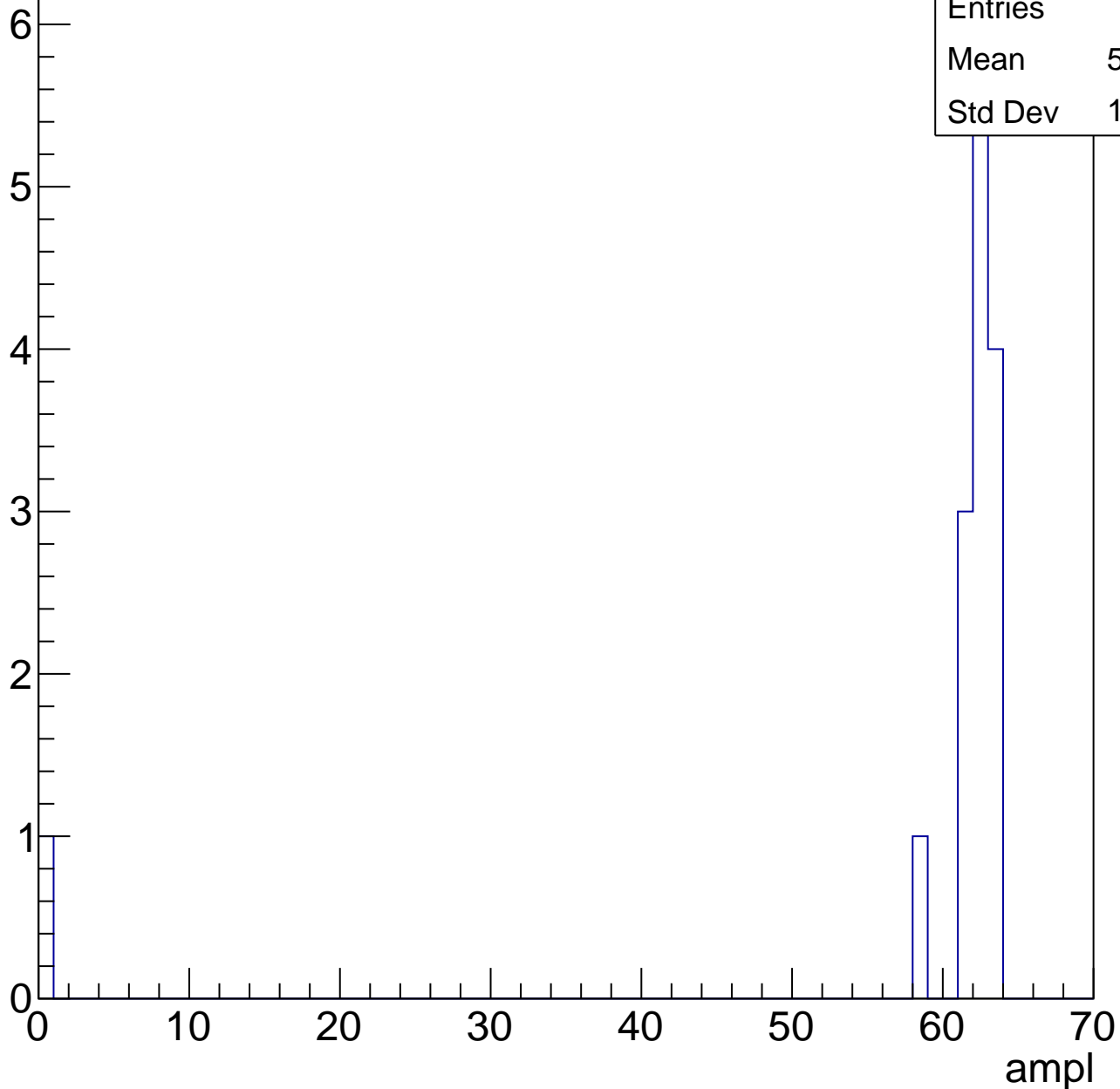


# B1L000S, U2-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.67
Std Dev	15.46





# B1L000S, U2-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U2-ch34, adc0

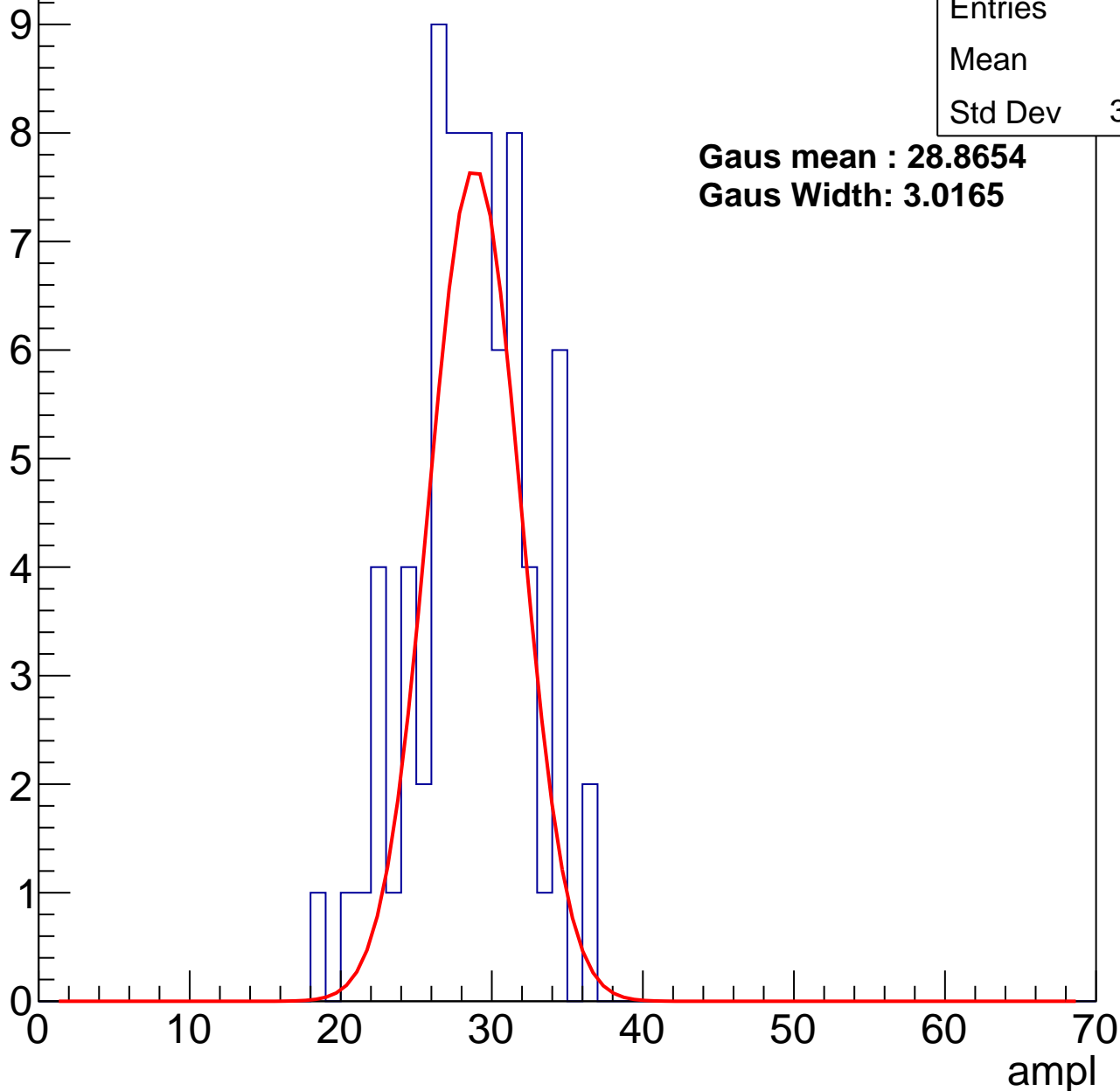
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	28.2
Std Dev	3.752

**Gaus mean : 28.8654**

**Gaus Width: 3.0165**



# B1L000S, U2-ch34, adc1

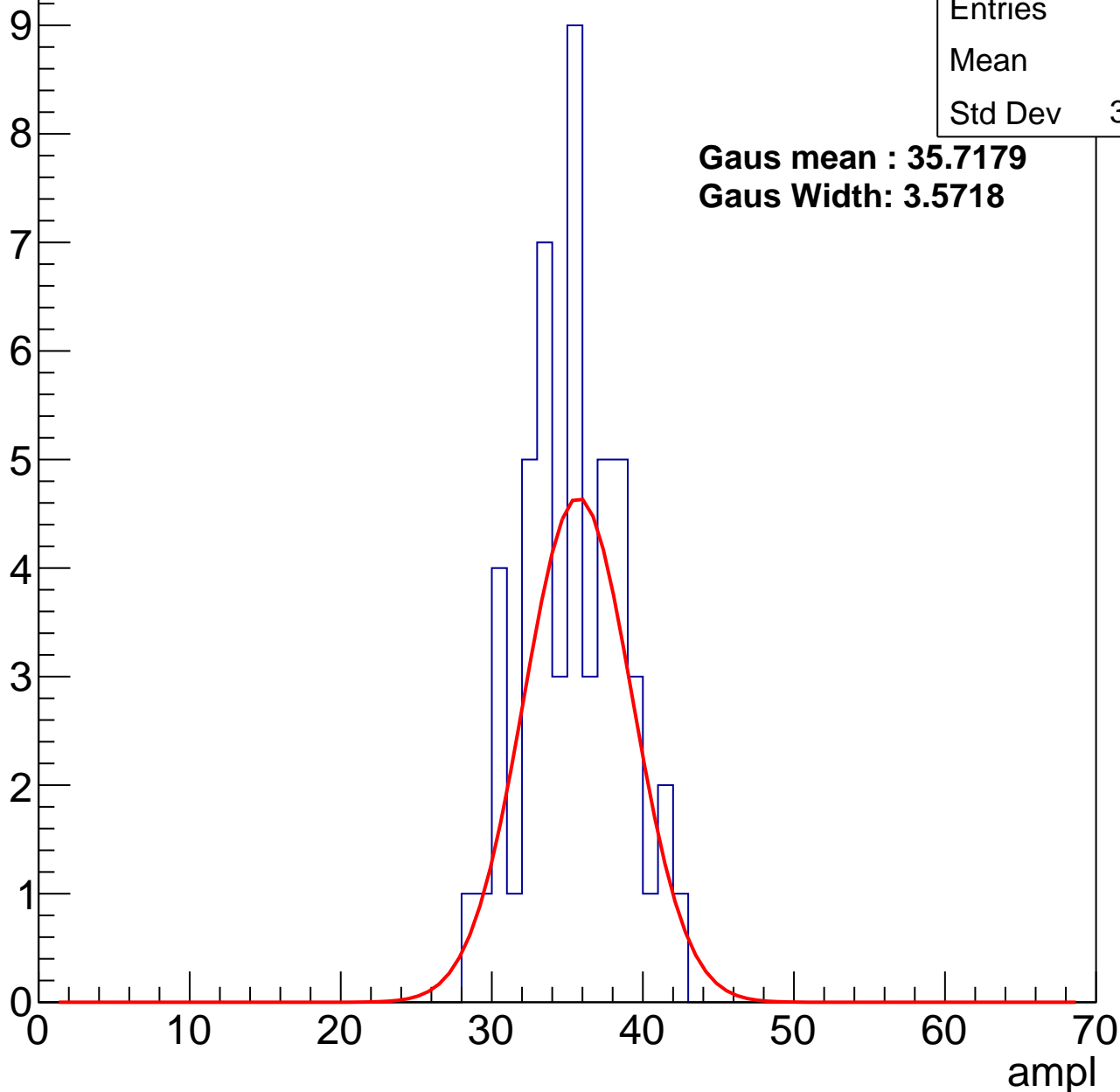
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	34.9
Std Dev	3.249

**Gaus mean : 35.7179**

**Gaus Width: 3.5718**



# B1L000S, U2-ch34, adc2

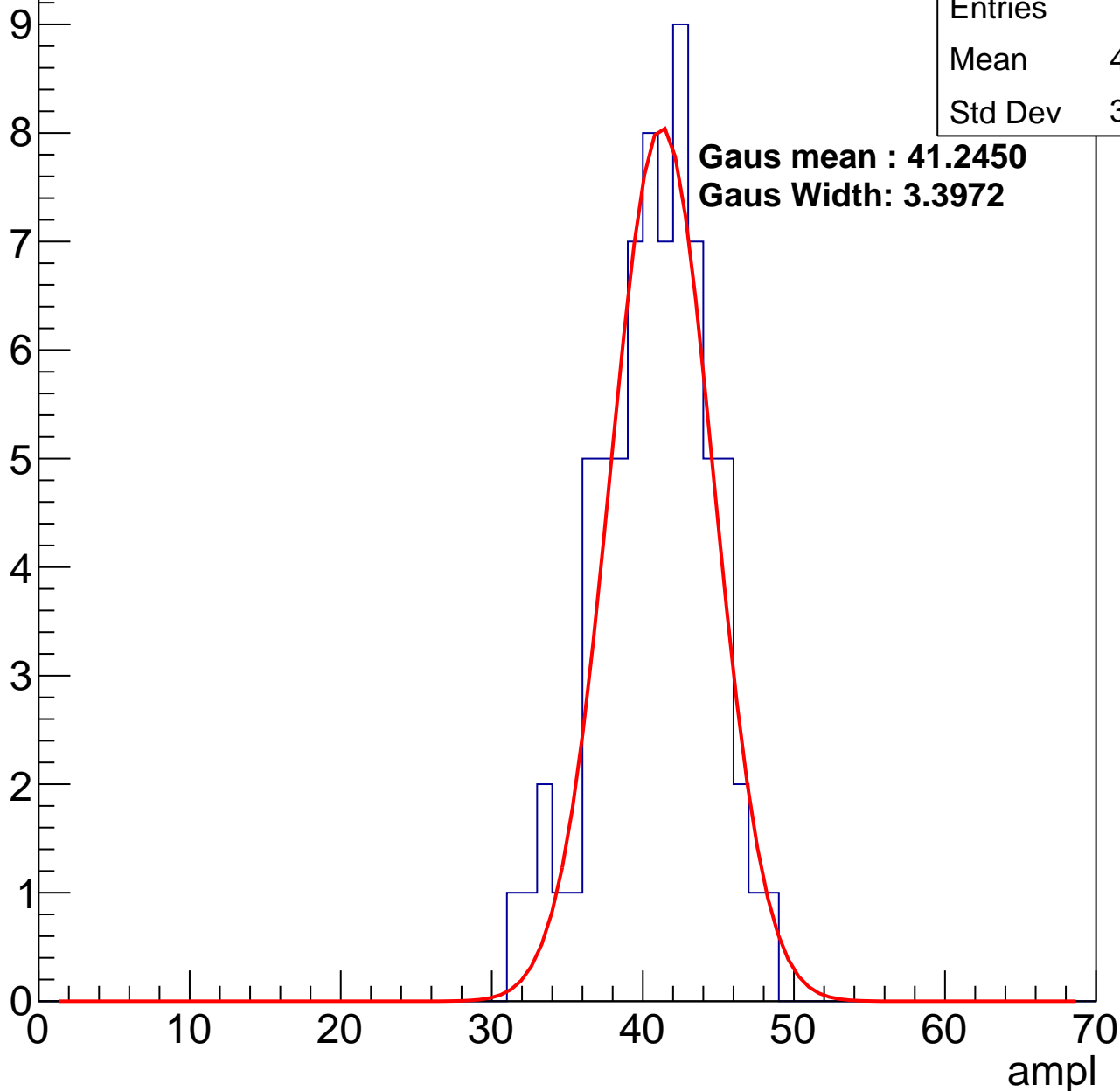
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	40.33
Std Dev	3.596

**Gaus mean : 41.2450**

**Gaus Width: 3.3972**

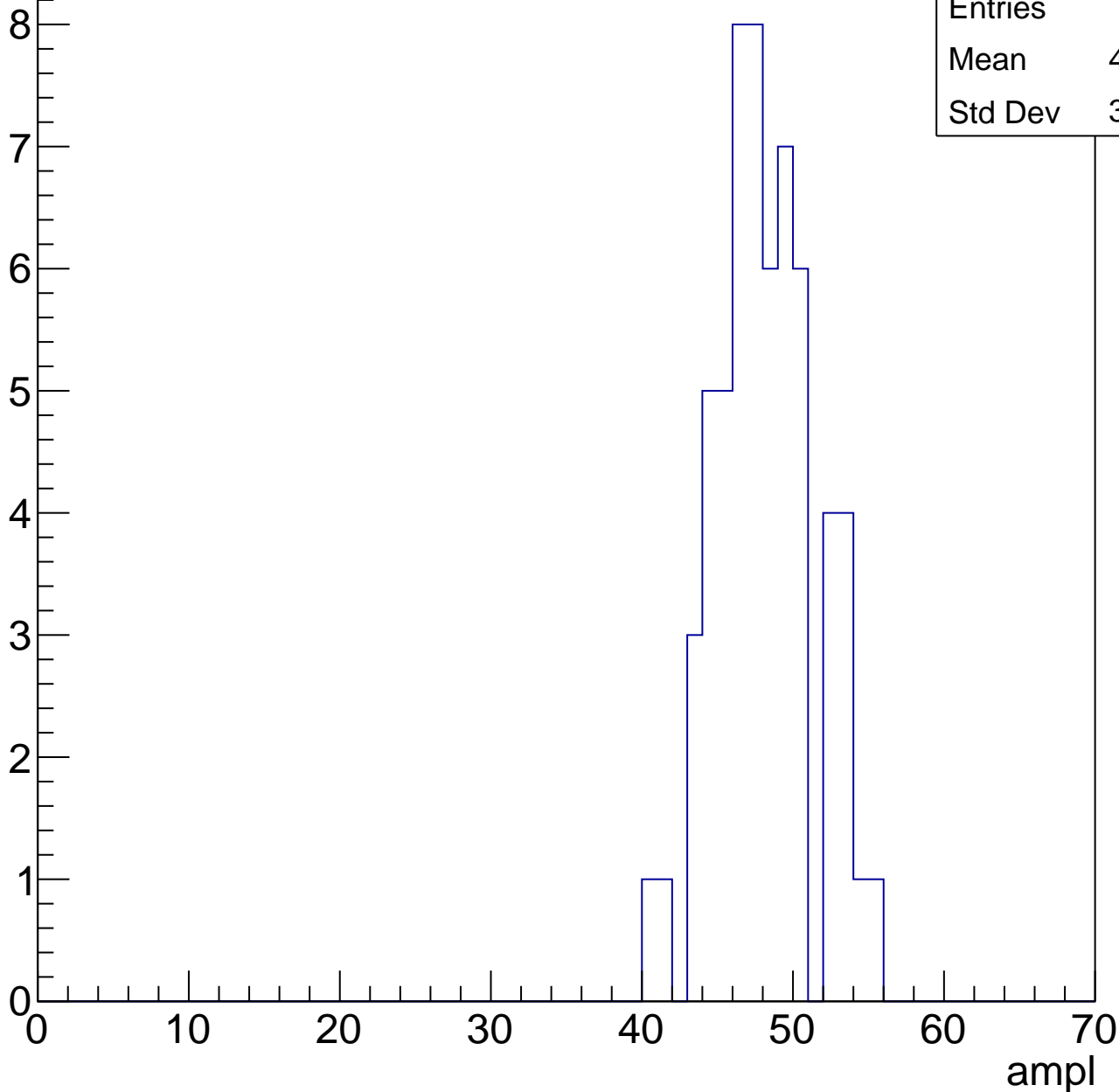


# B1L000S, U2-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	47.65
Std Dev	3.229

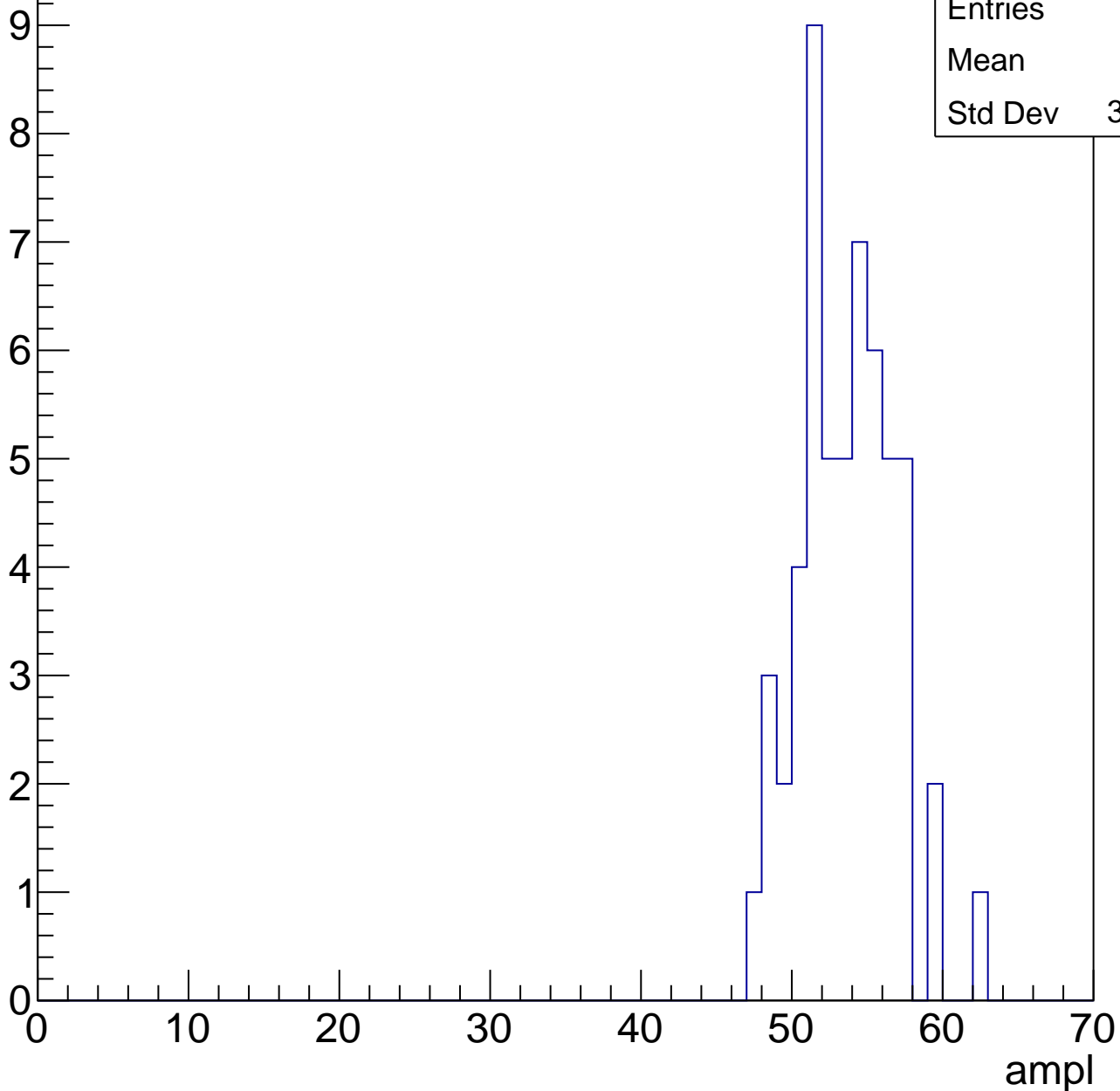


# B1L000S, U2-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

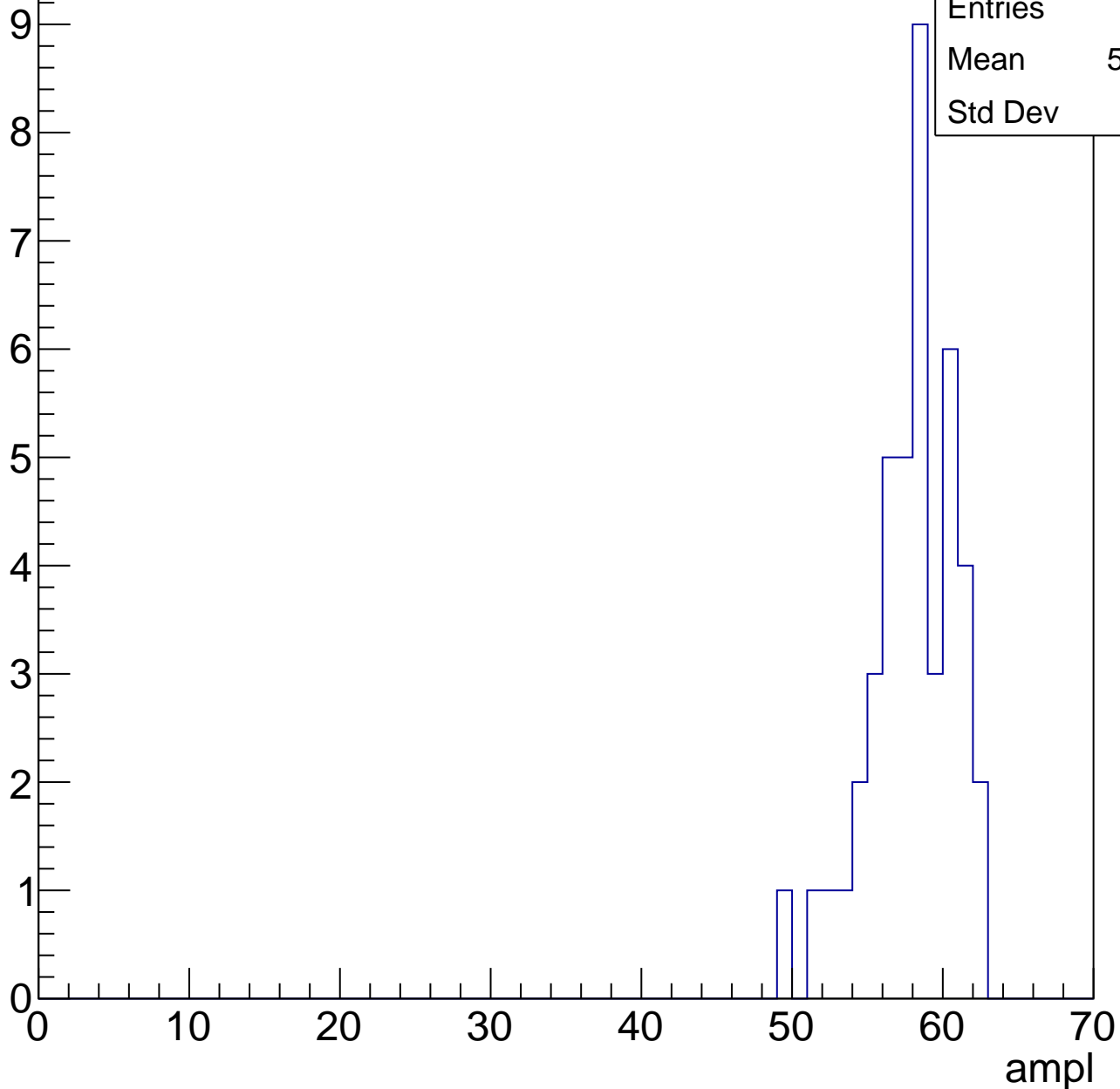
Entries	55
Mean	53.2
Std Dev	3.095



# B1L000S, U2-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

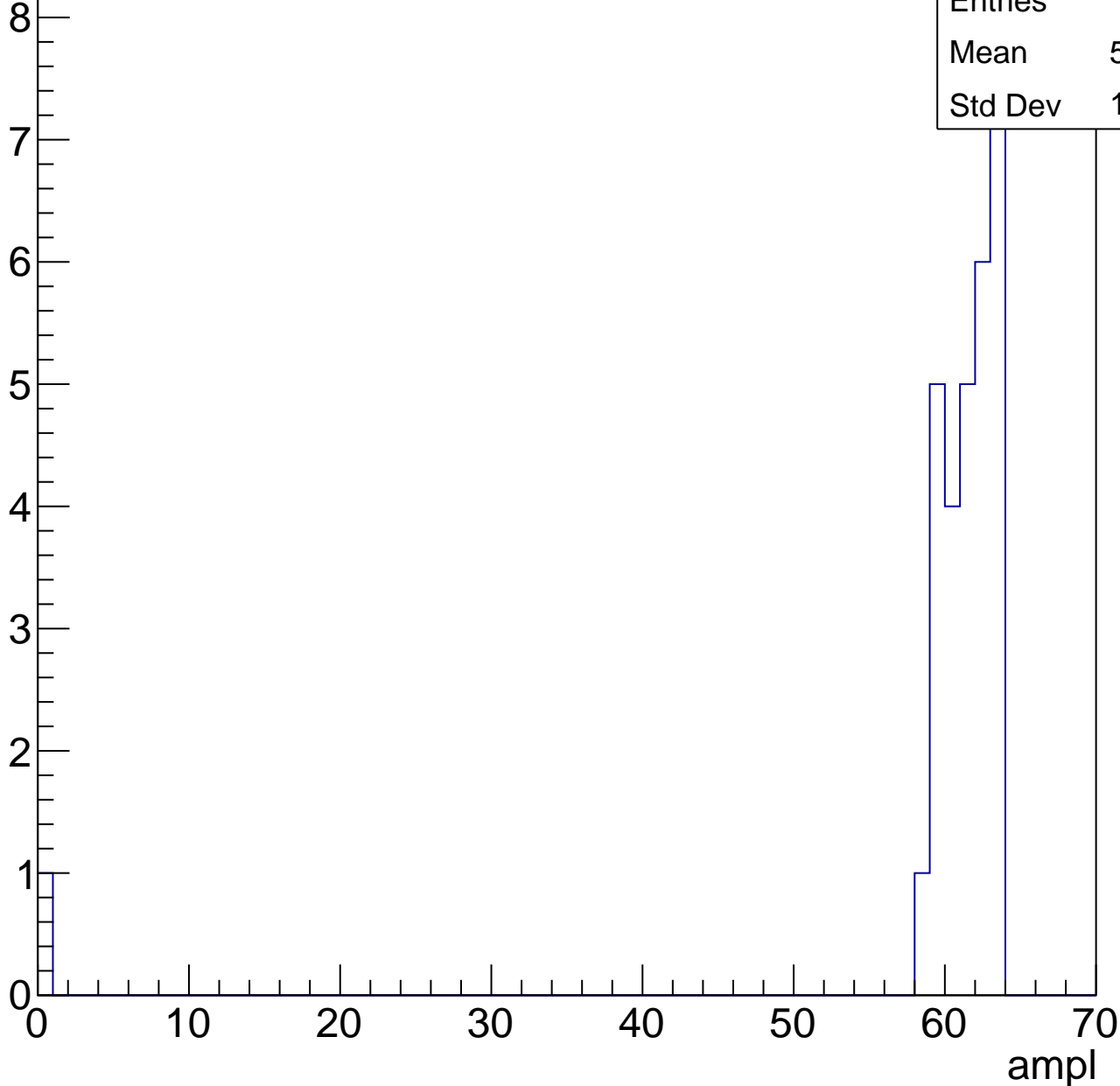


# B1L000S, U2-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	59.13
Std Dev	11.09

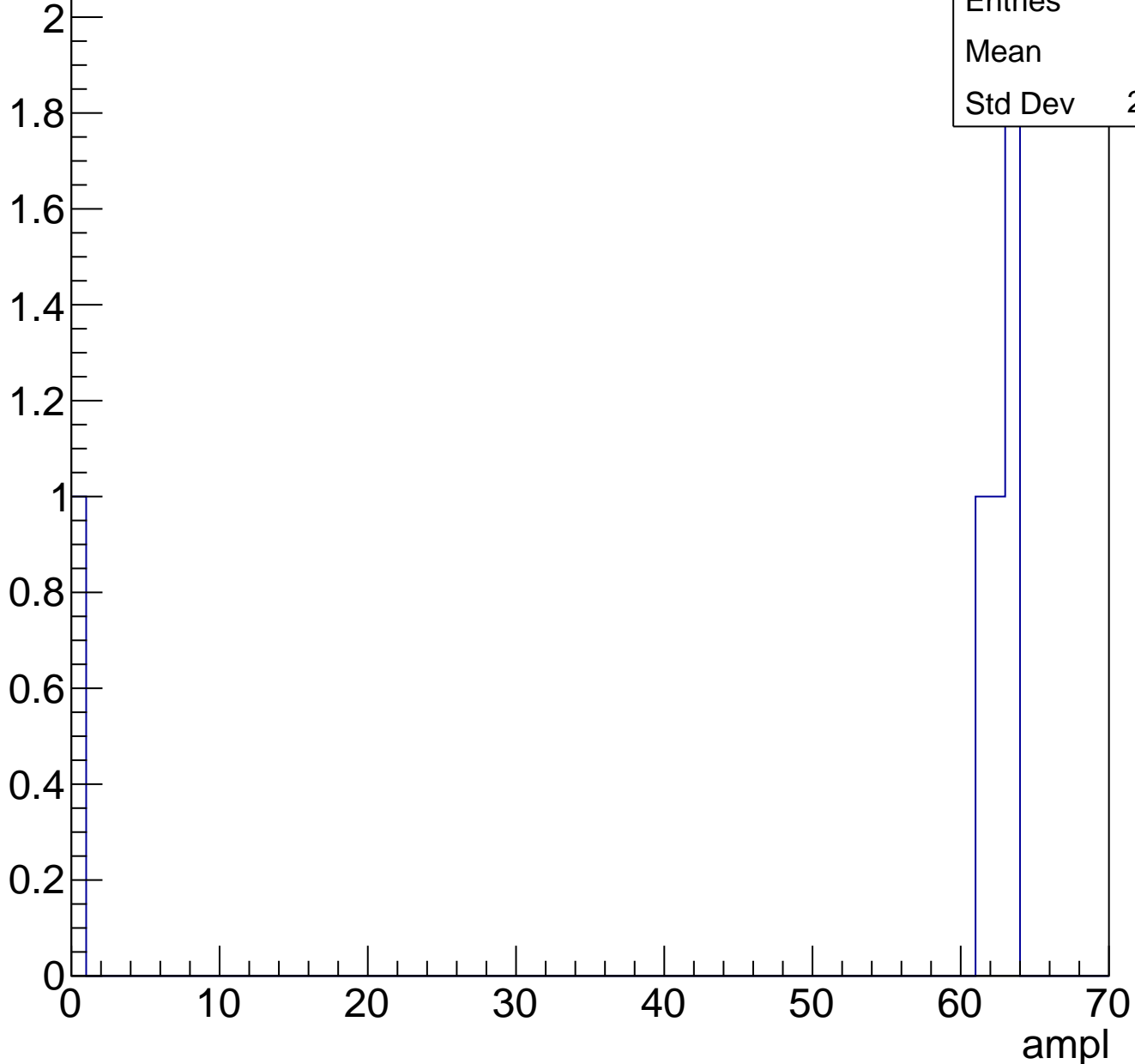




# B1L000S, U2-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch35, adc0

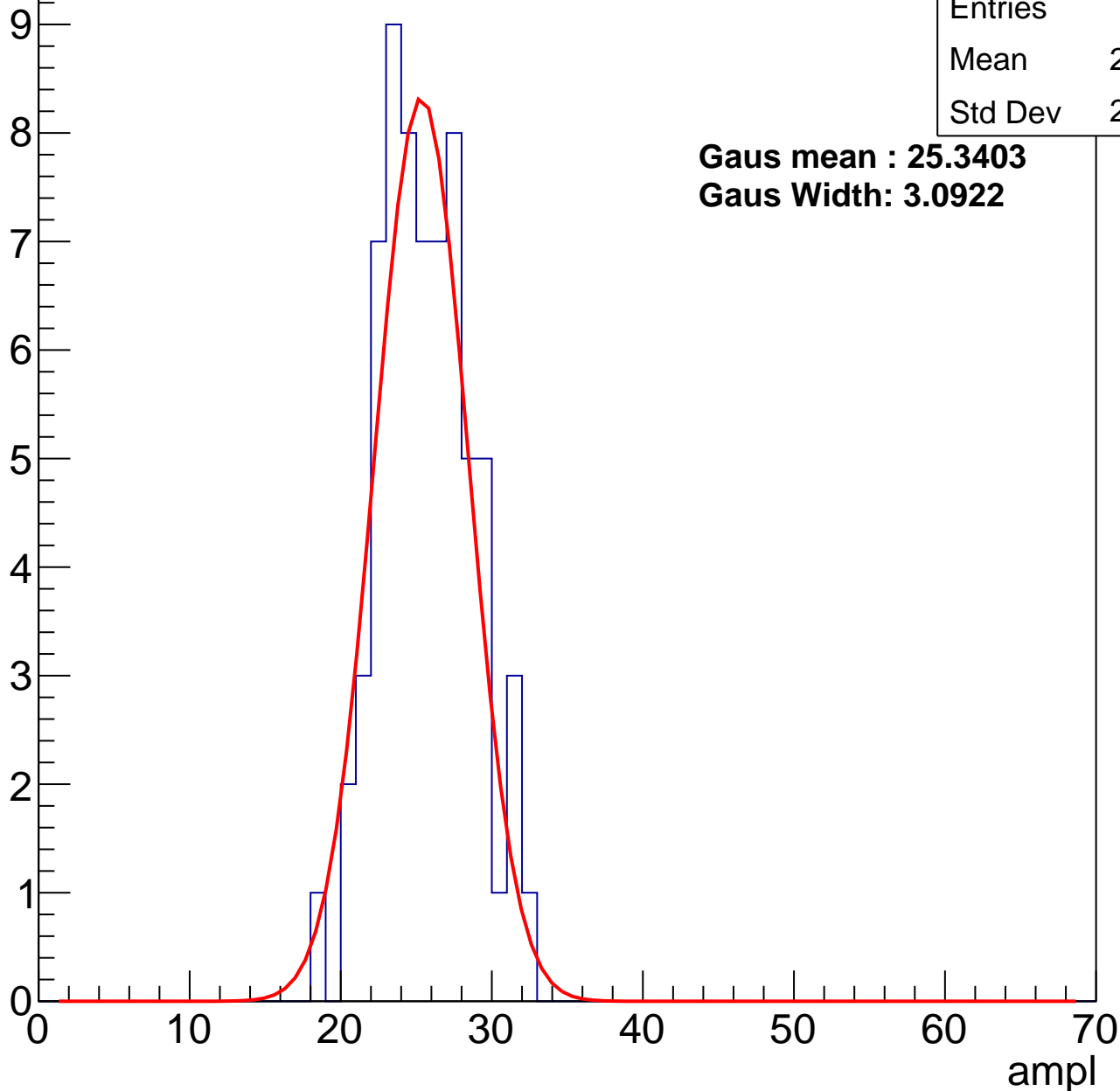
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	25.18
Std Dev	2.987

**Gaus mean : 25.3403**

**Gaus Width: 3.0922**



# B1L000S, U2-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	87
Mean	32.46
Std Dev	3.584

**Gaus mean : 33.1618**

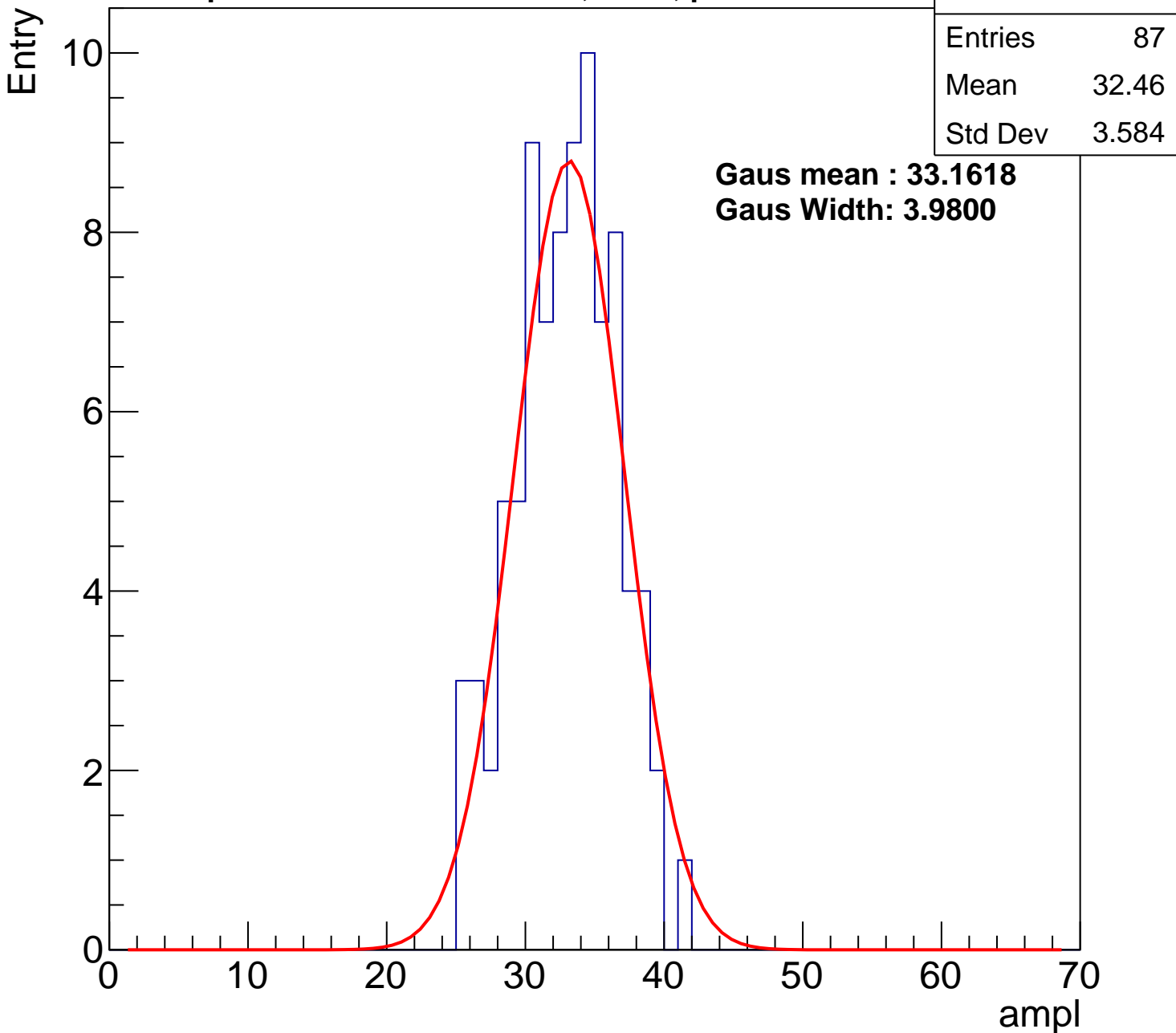
**Gaus Width: 3.9800**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch35, adc2

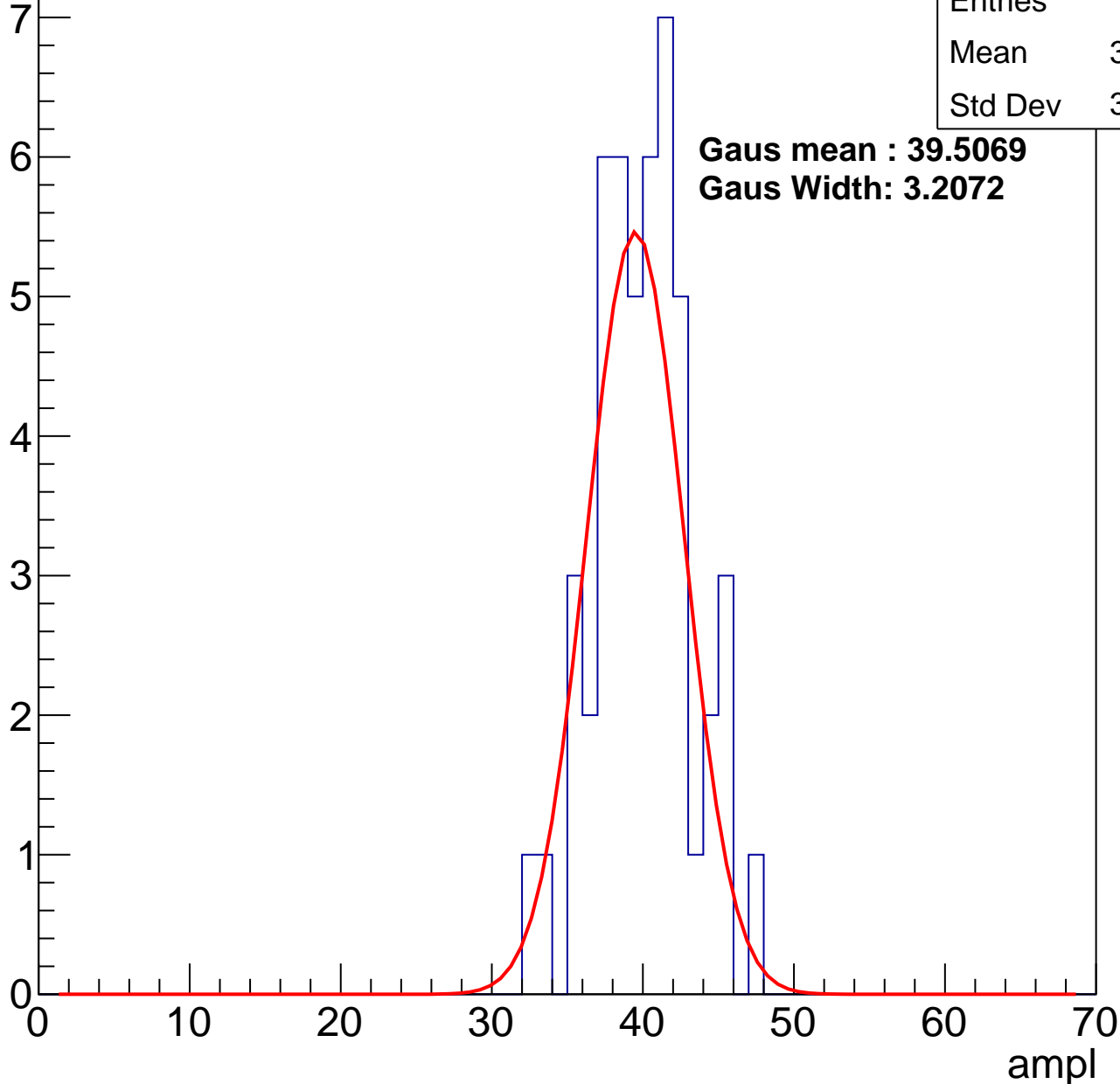
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	39.53
Std Dev	3.144

**Gaus mean : 39.5069**

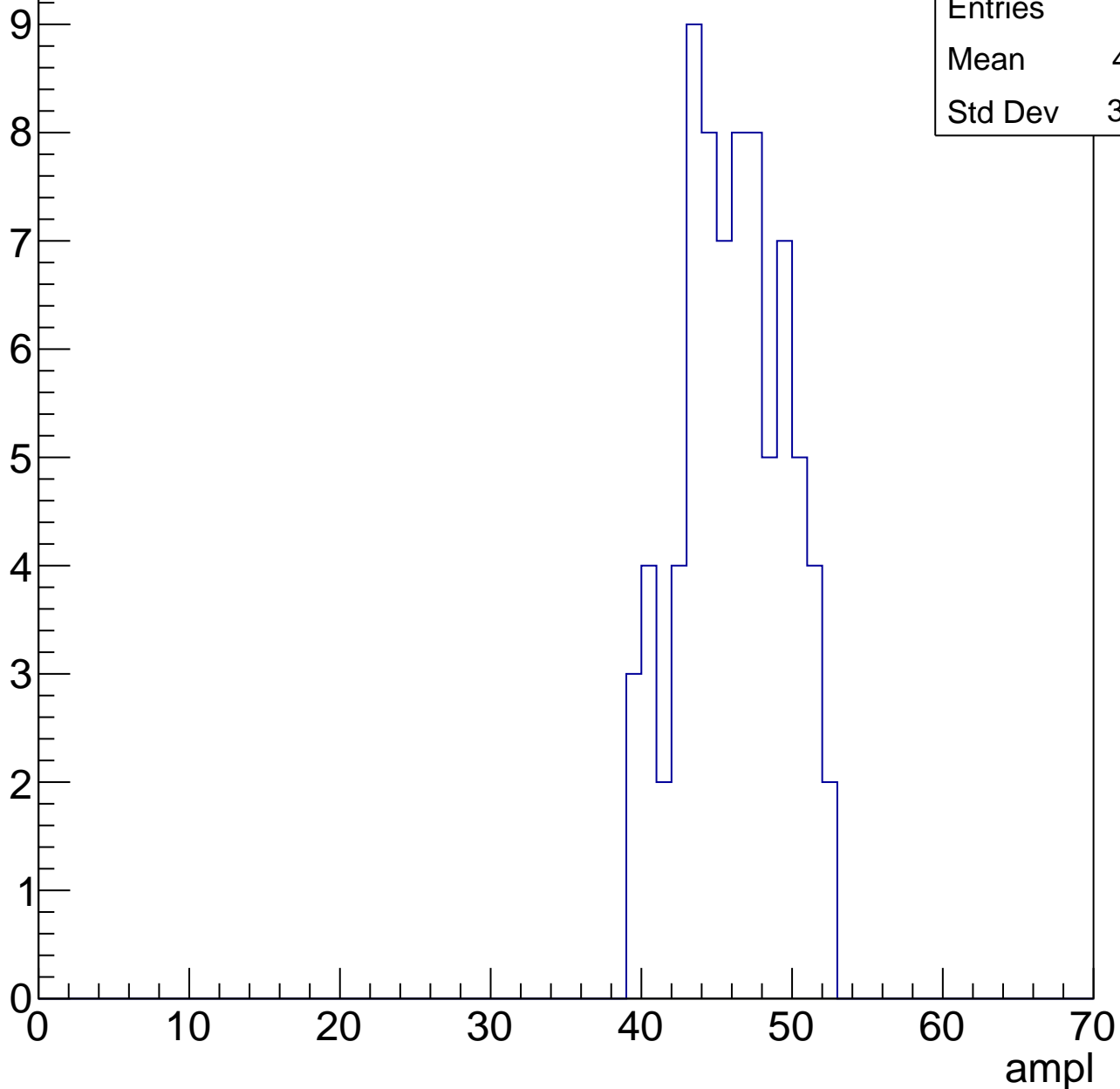
**Gaus Width: 3.2072**



# B1L000S, U2-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

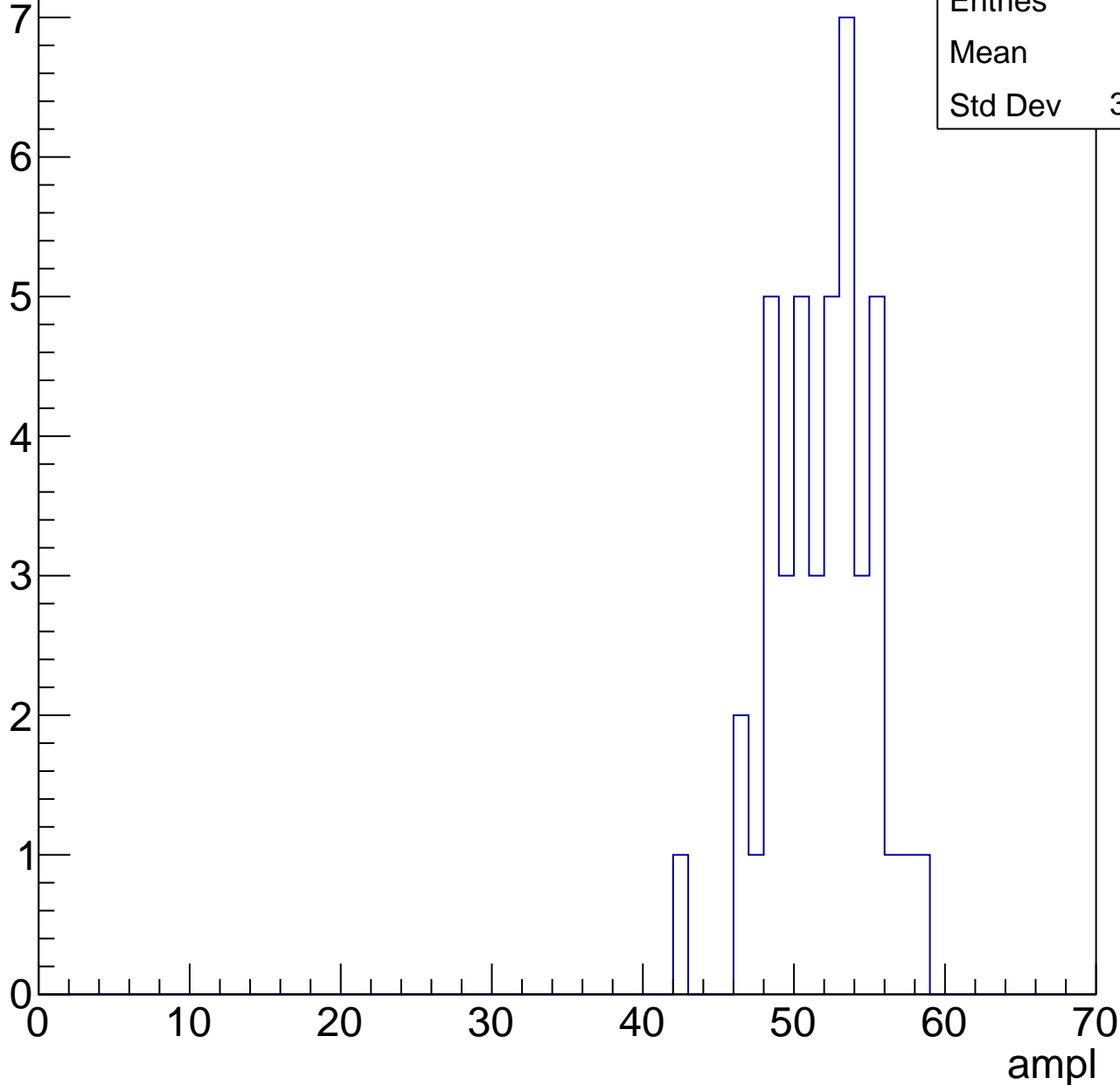


Entries	76
Mean	45.61
Std Dev	3.356

# B1L000S, U2-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

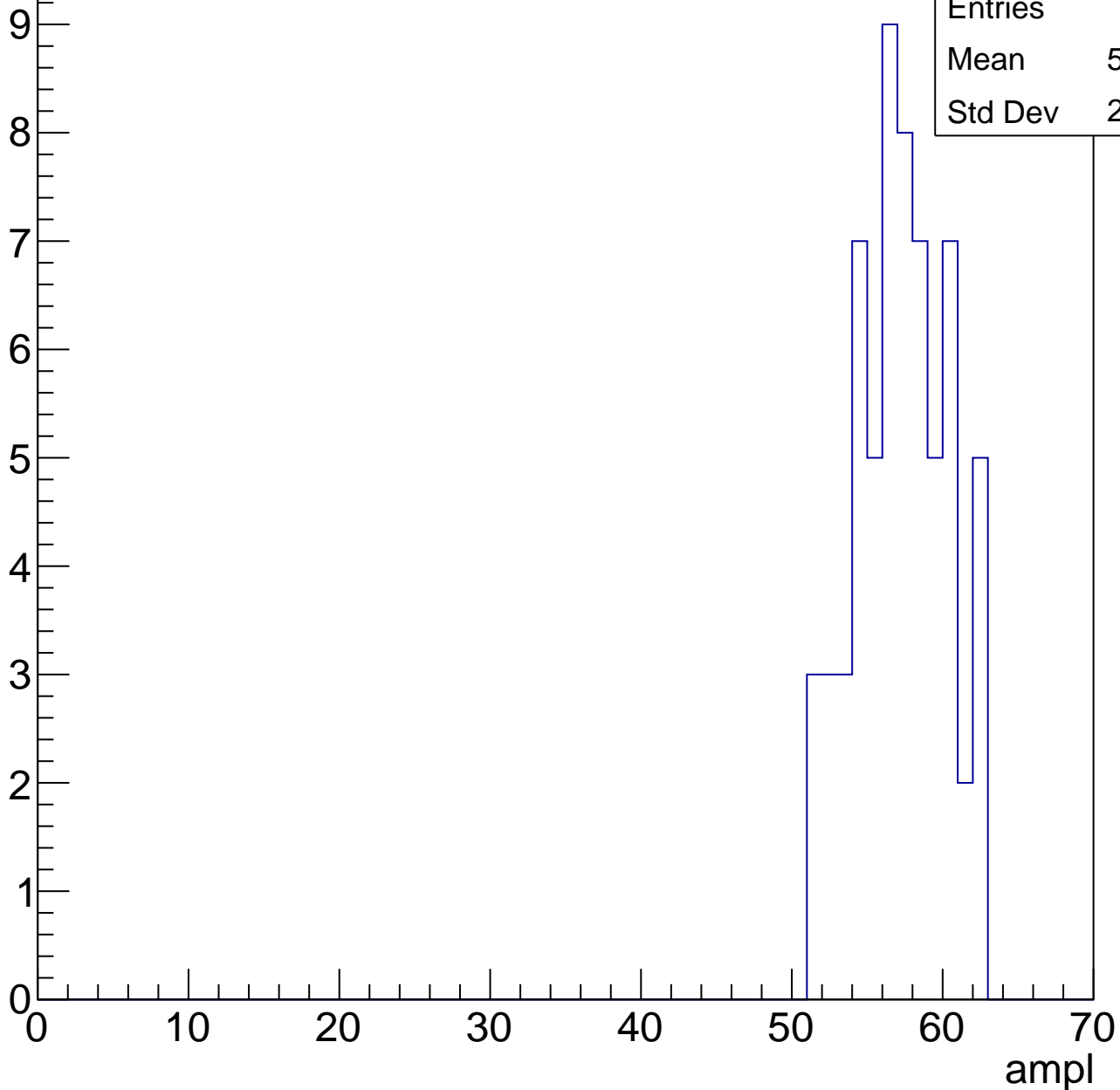


# B1L000S, U2-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

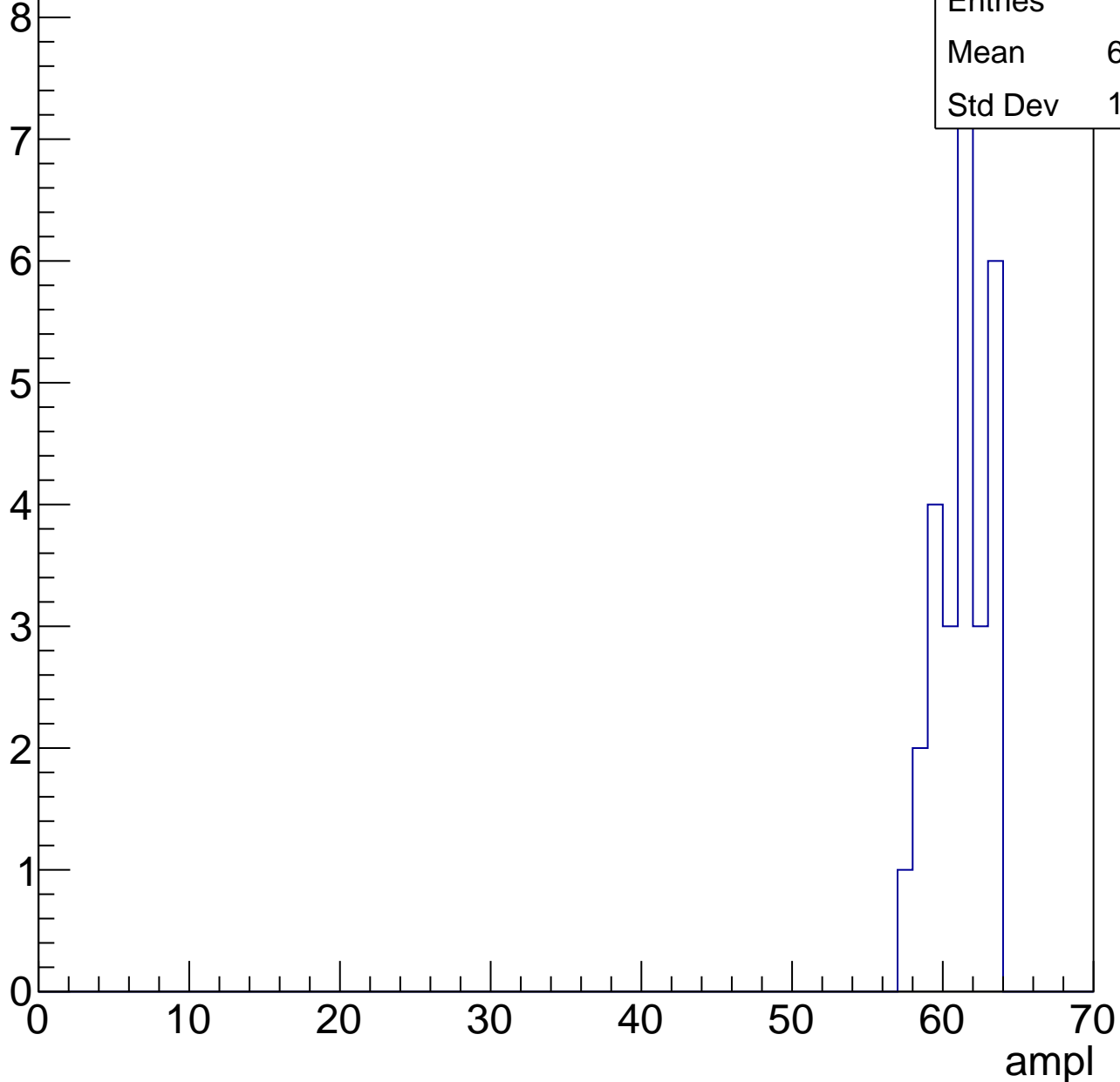
Entries	64
Mean	56.78
Std Dev	2.976



# B1L000S, U2-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



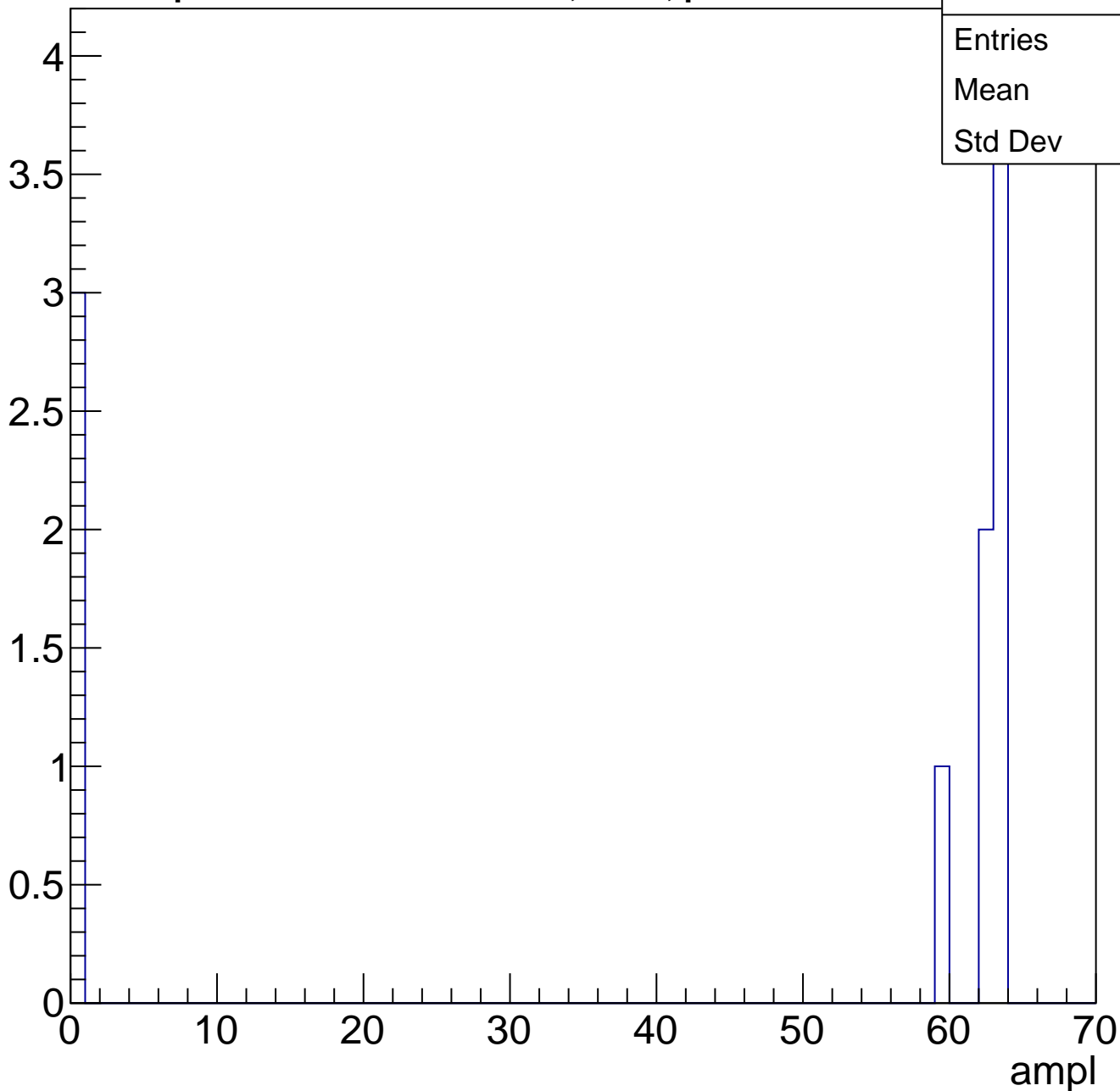
Entries	27
Mean	60.78
Std Dev	1.707



# B1L000S, U2-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch36, adc0

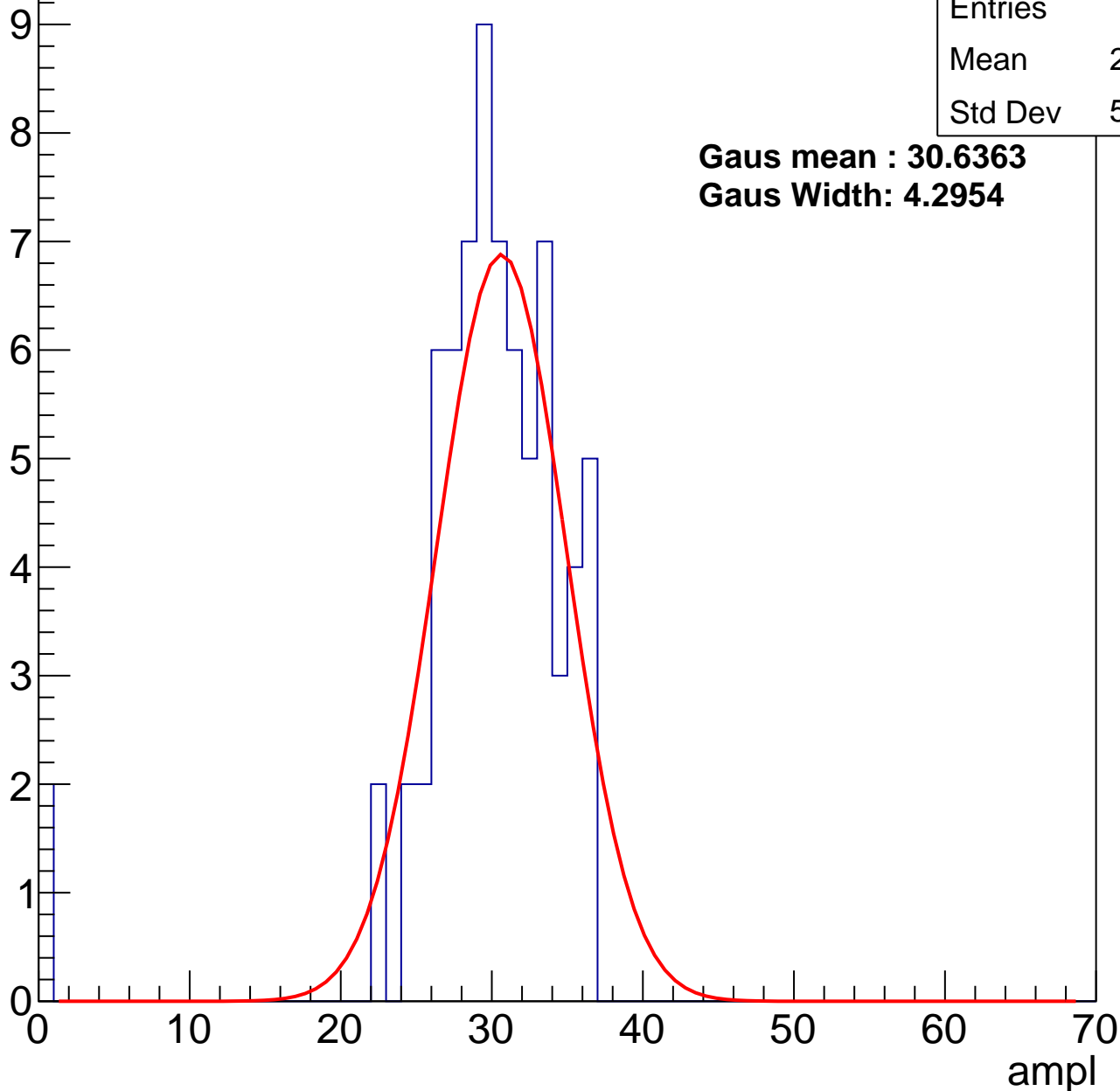
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	29.12
Std Dev	5.966

**Gaus mean : 30.6363**

**Gaus Width: 4.2954**



# B1L000S, U2-ch36, adc1

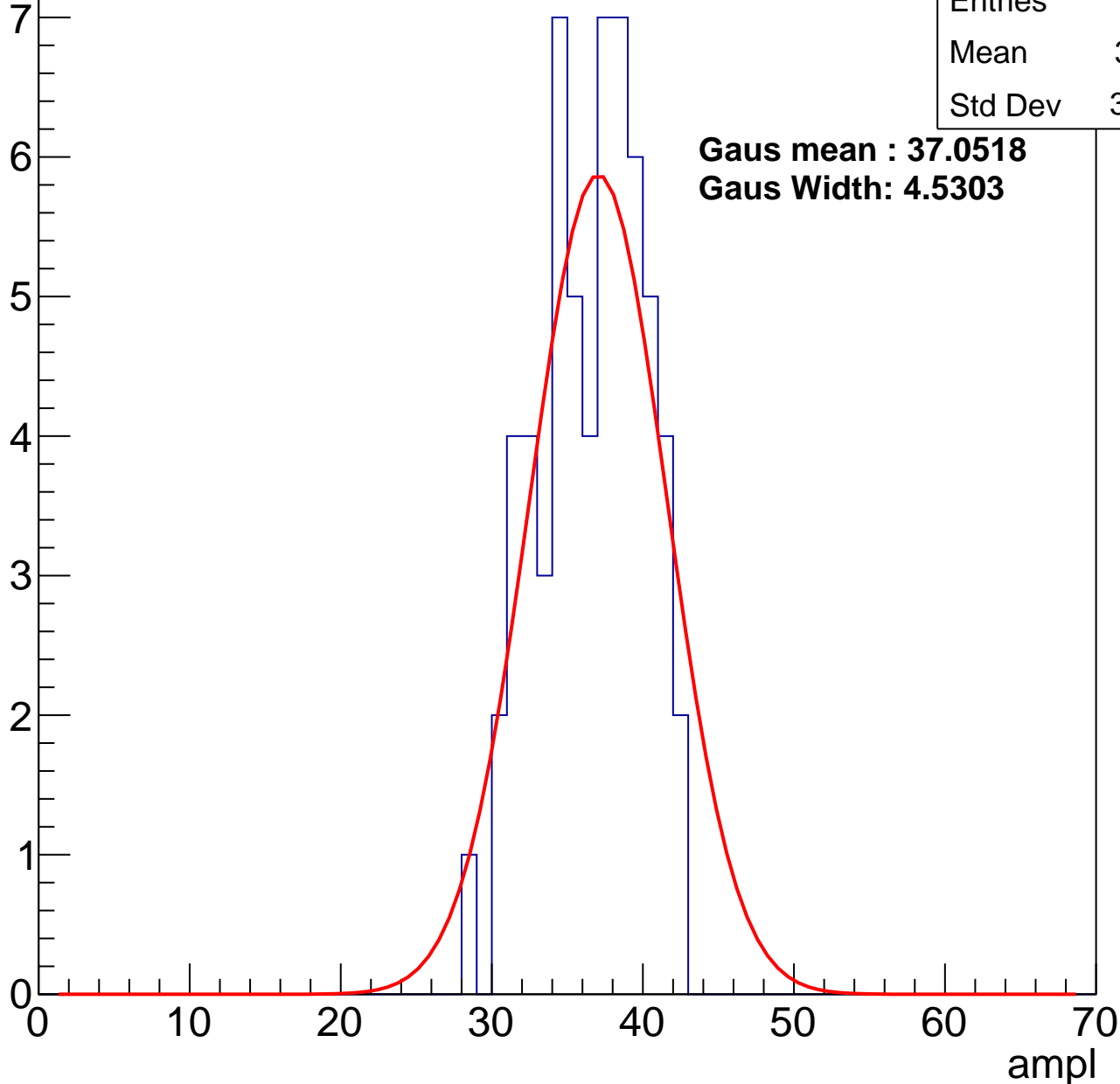
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	36.11
Std Dev	3.388

**Gaus mean : 37.0518**

**Gaus Width: 4.5303**



# B1L000S, U2-ch36, adc2

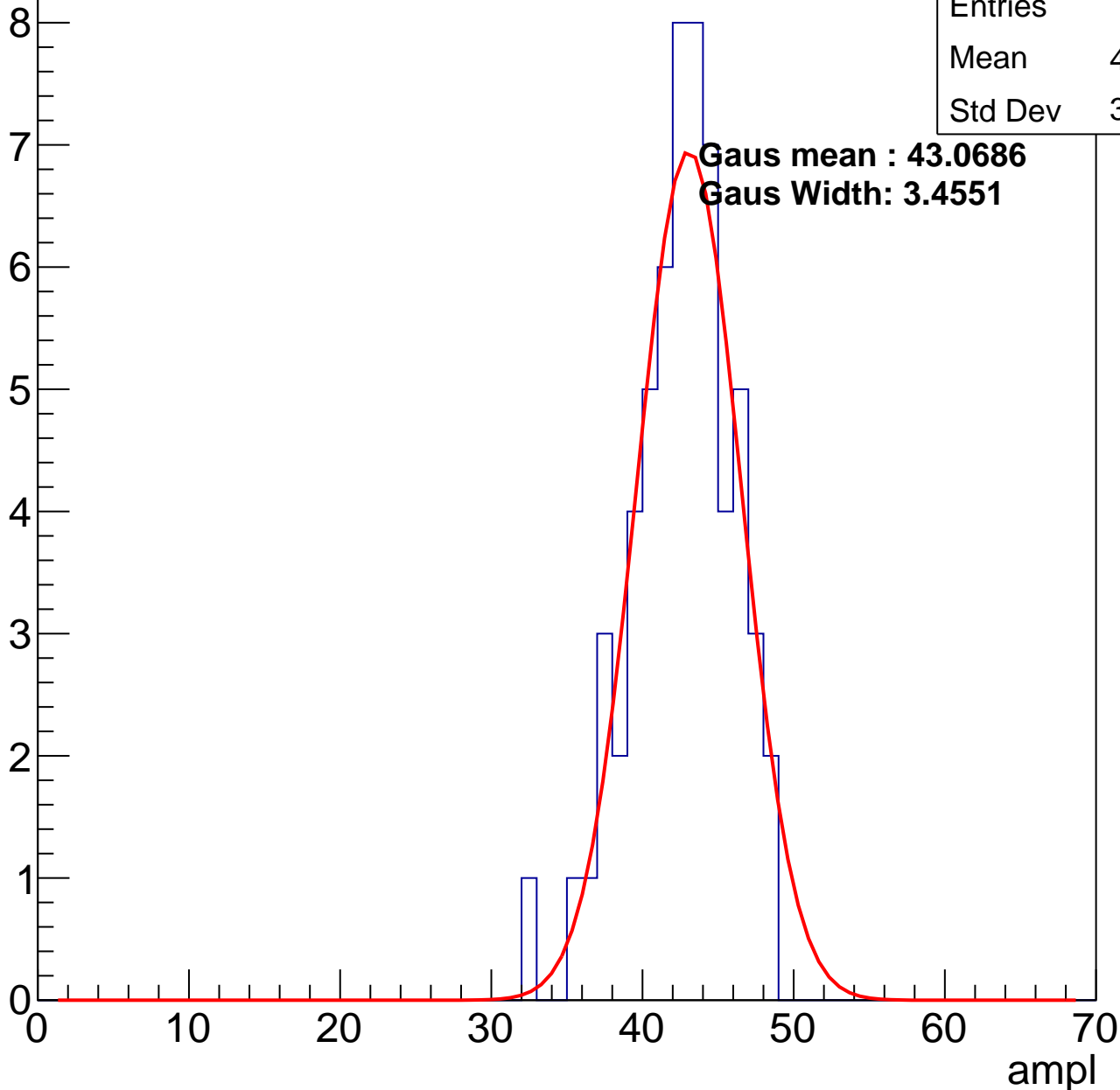
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.12
Std Dev	3.297

**Gaus mean : 43.0686**

**Gaus Width: 3.4551**

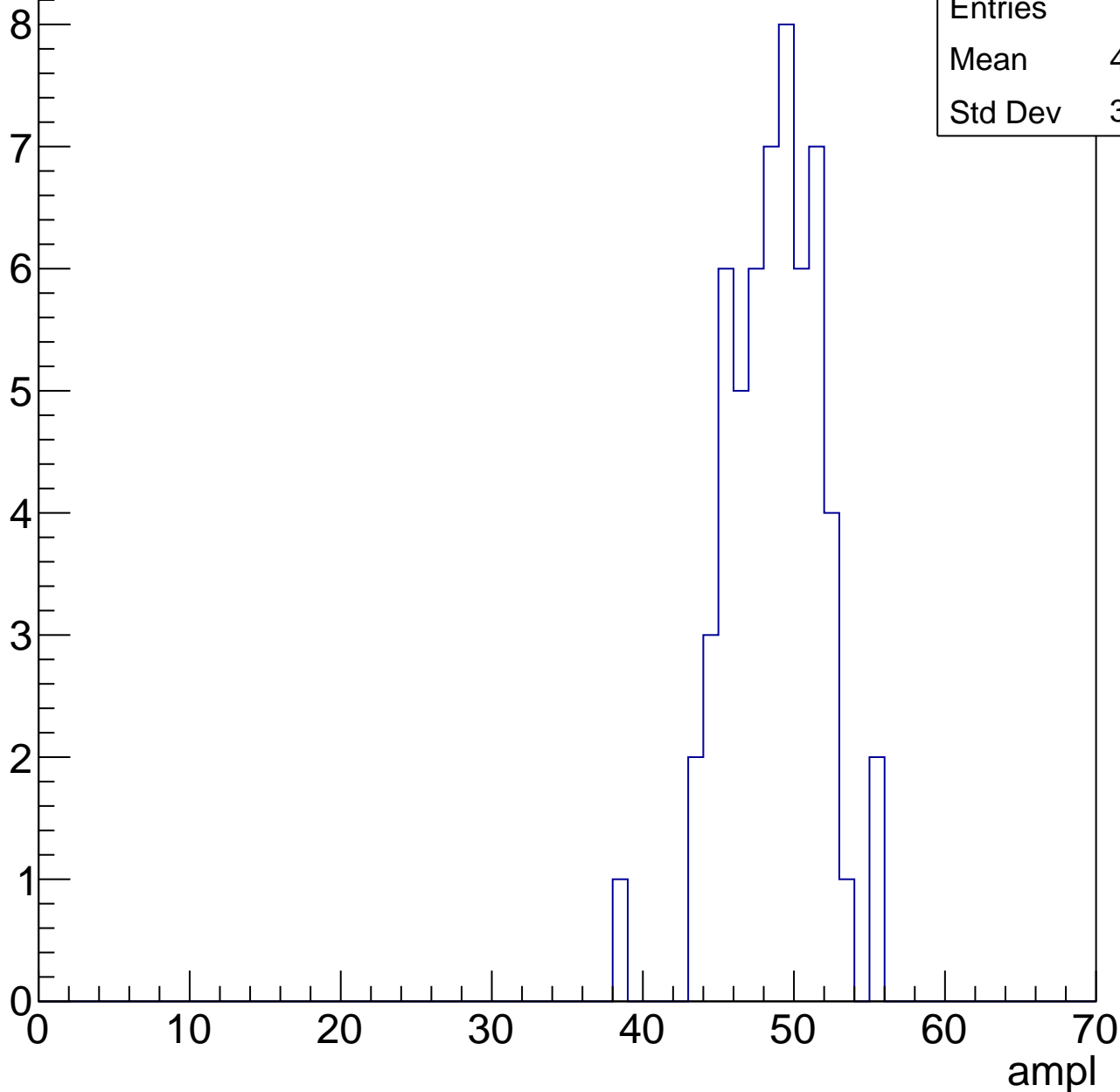


# B1L000S, U2-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	48.17
Std Dev	3.102

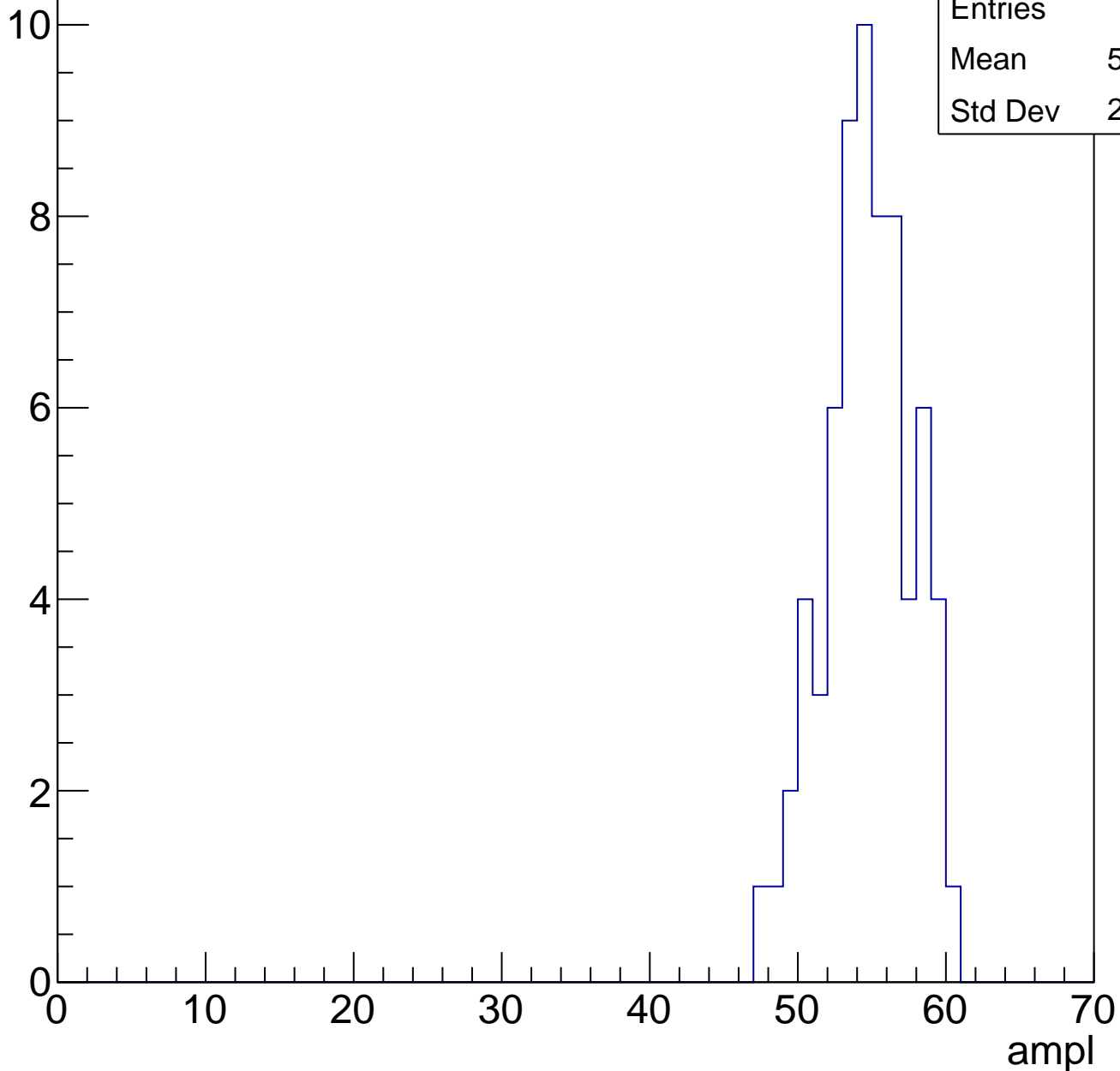


# B1L000S, U2-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	54.25
Std Dev	2.898

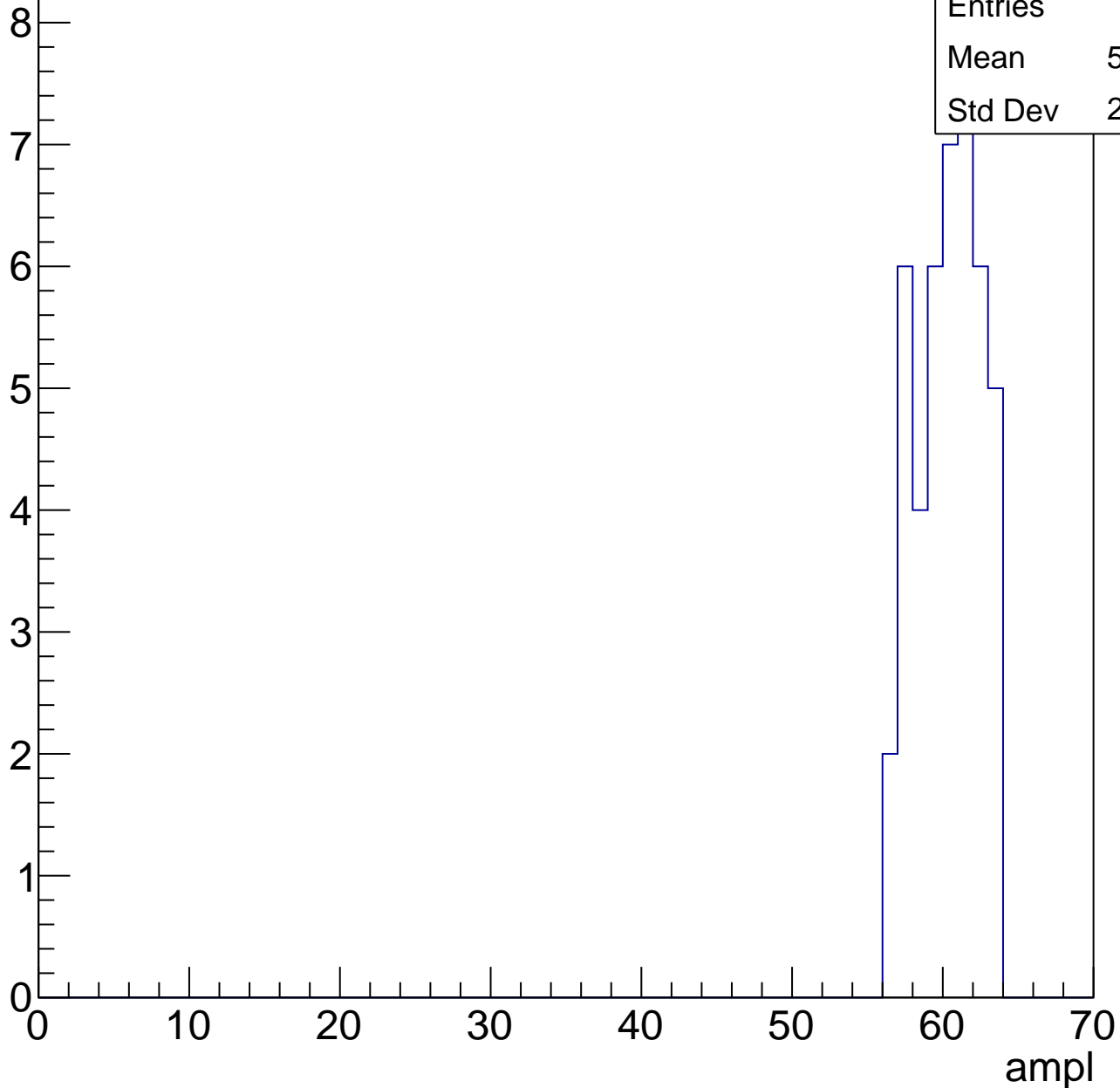
Entry



# B1L000S, U2-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

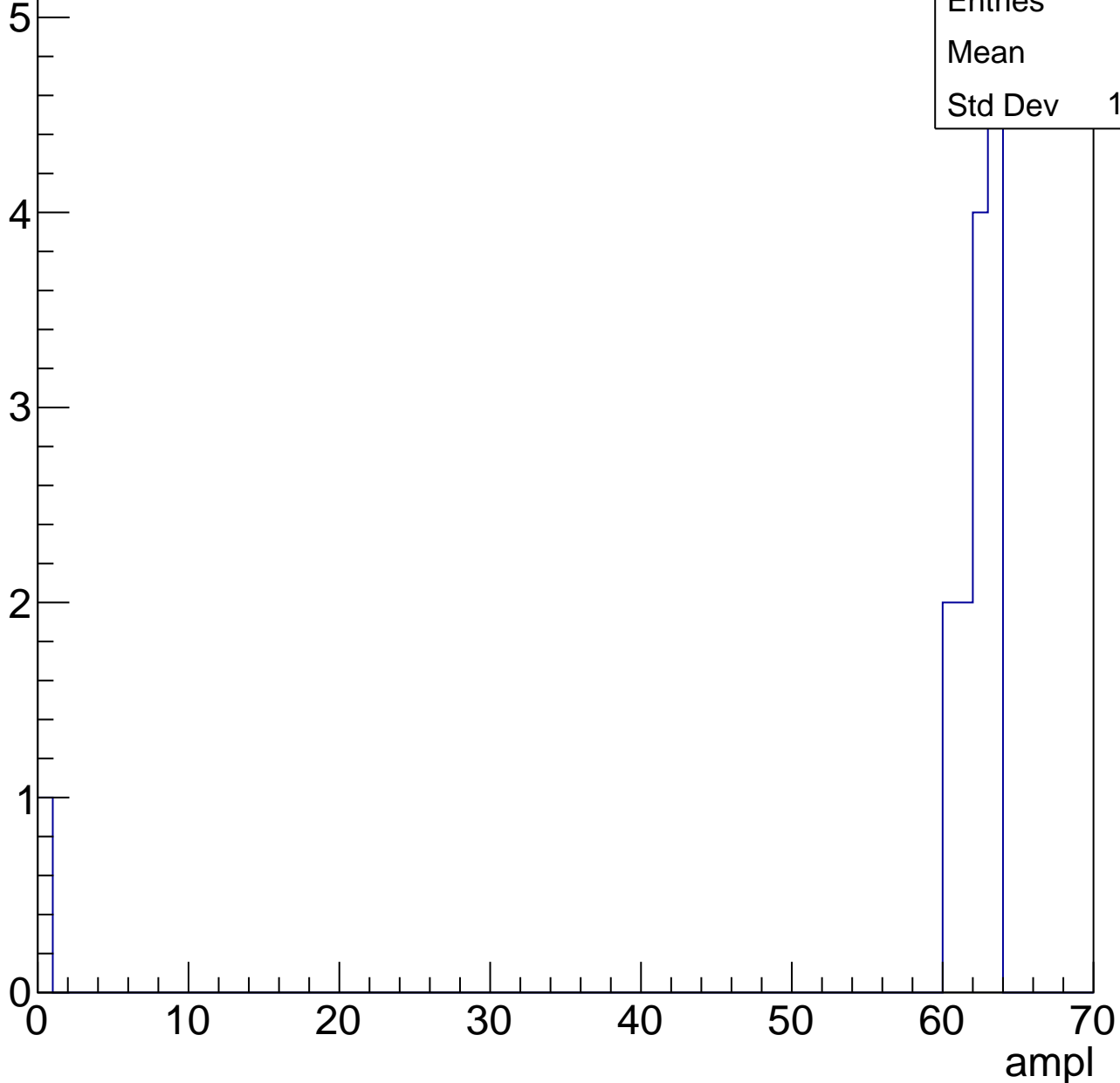


# B1L000S, U2-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.5
Std Dev	15.98

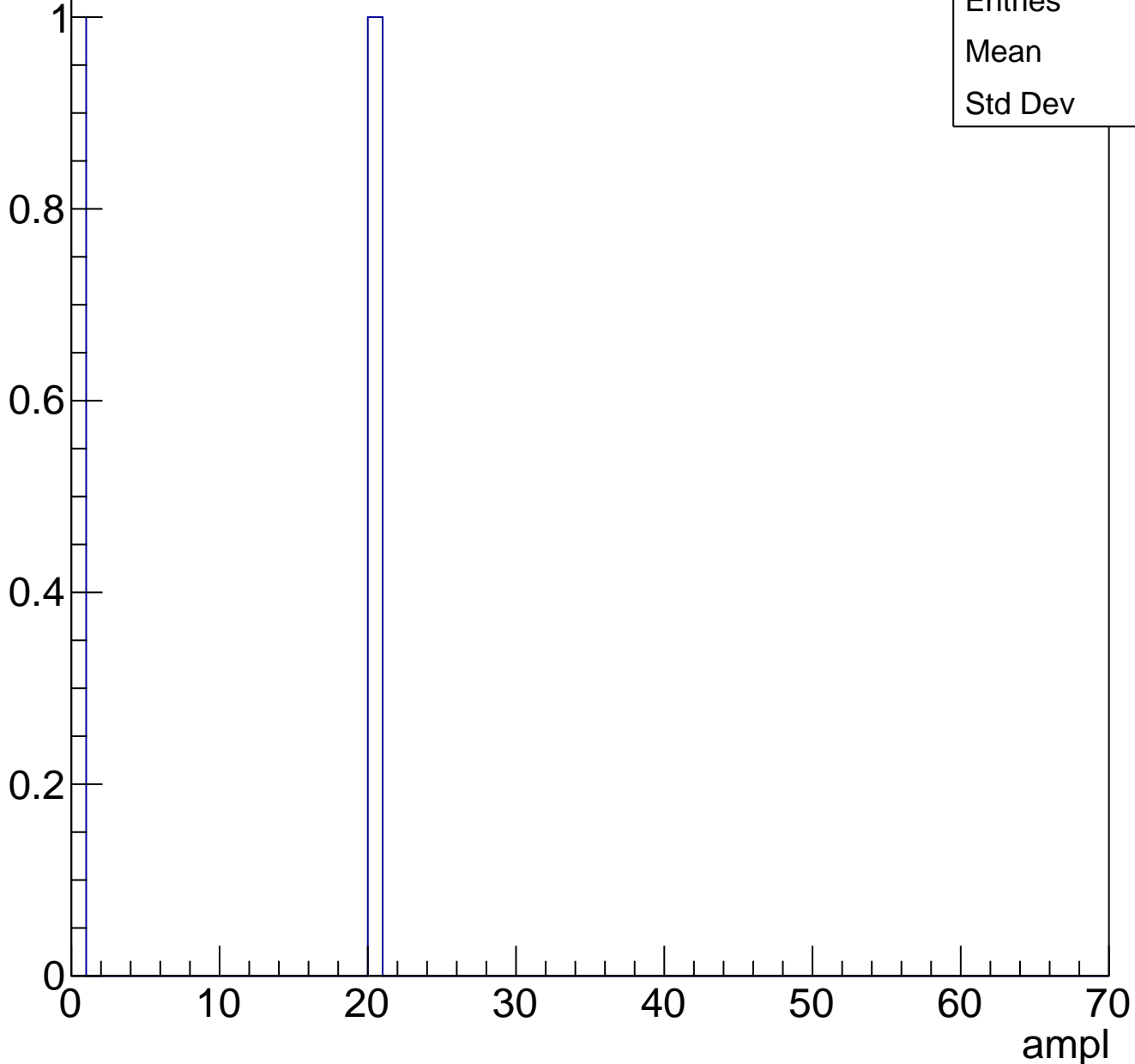




# B1L000S, U2-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L000S, U2-ch37, adc0

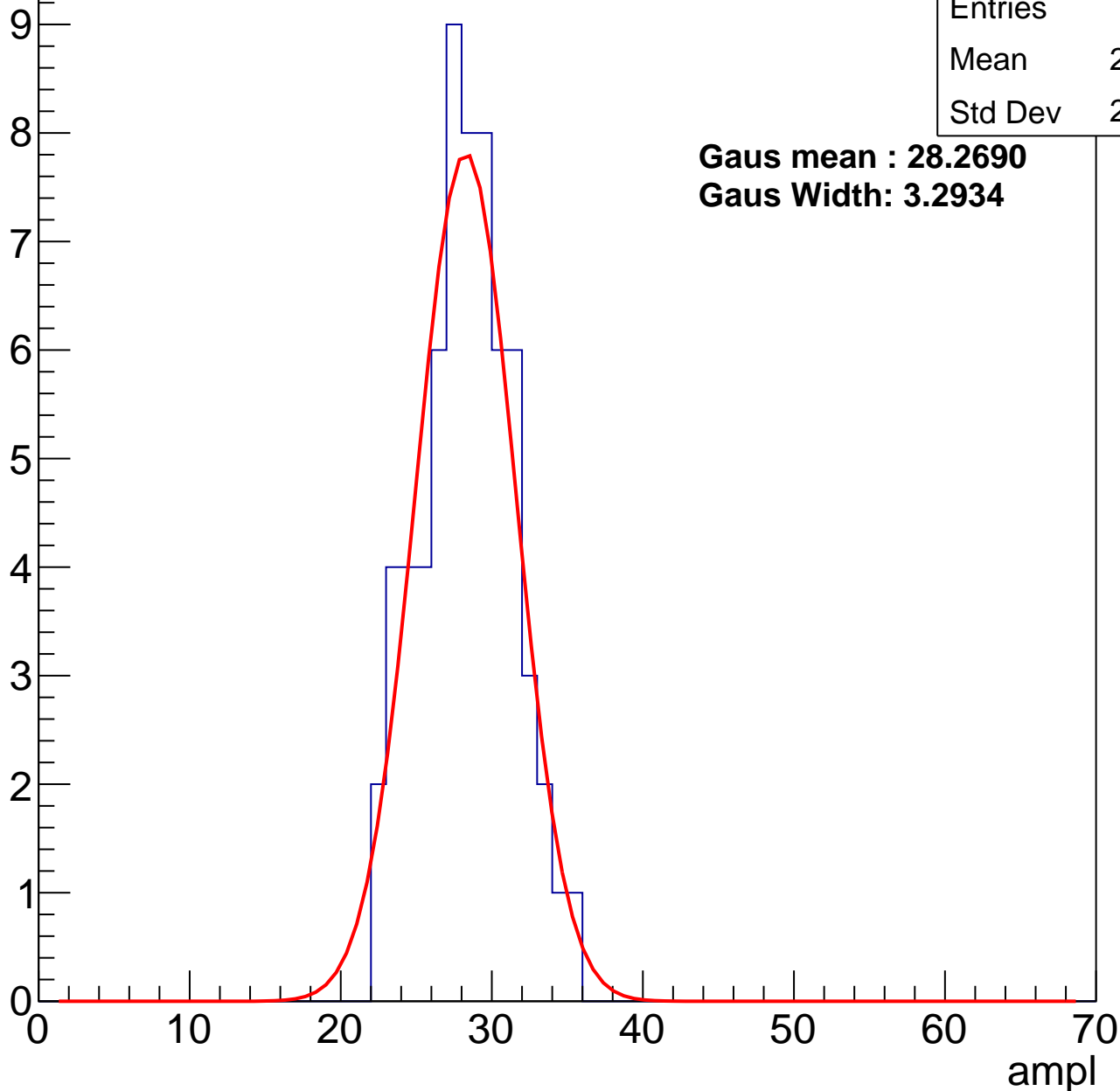
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	27.88
Std Dev	2.992

**Gaus mean : 28.2690**

**Gaus Width: 3.2934**



# B1L000S, U2-ch37, adc1

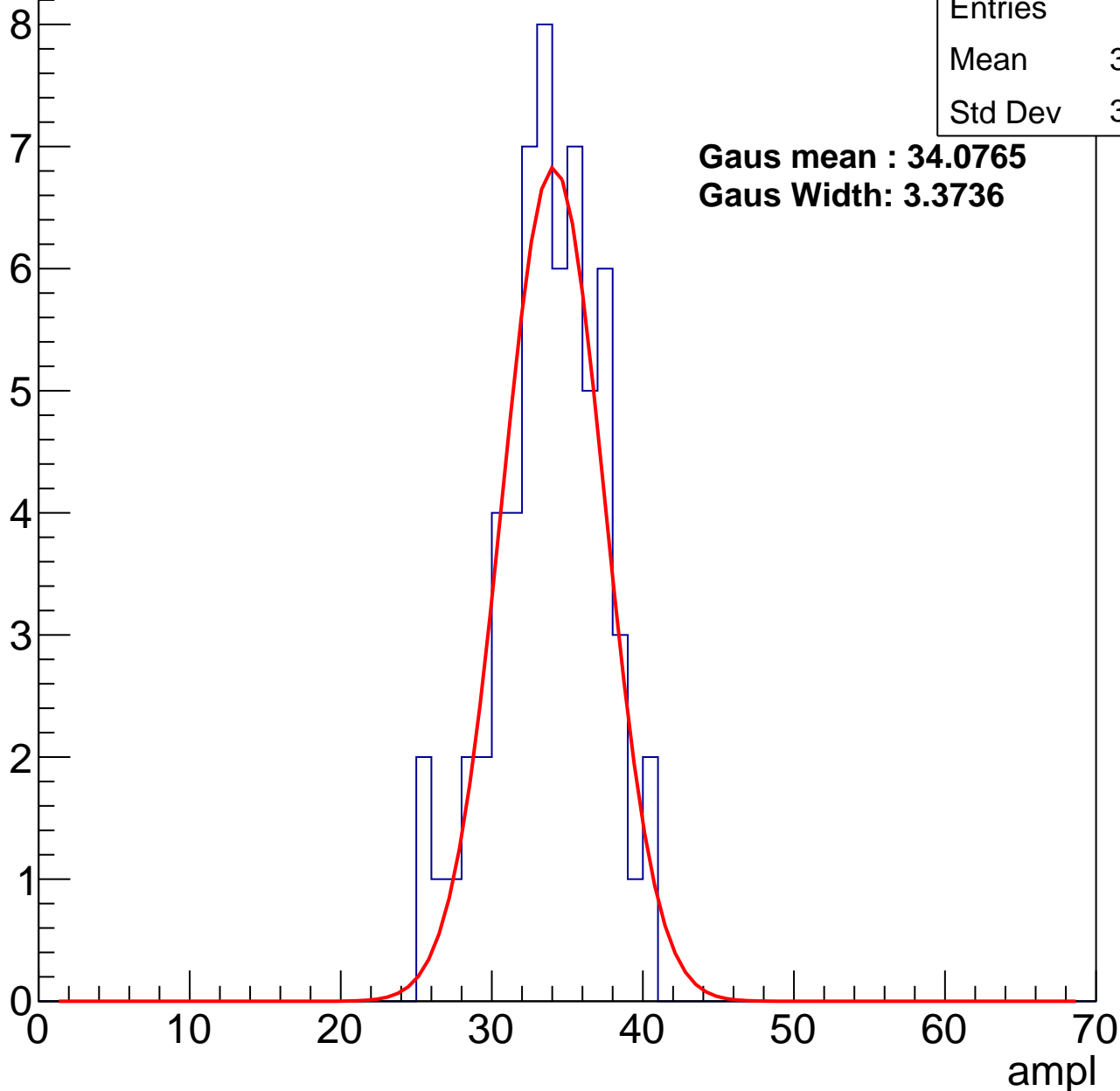
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	33.33
Std Dev	3.463

**Gaus mean : 34.0765**

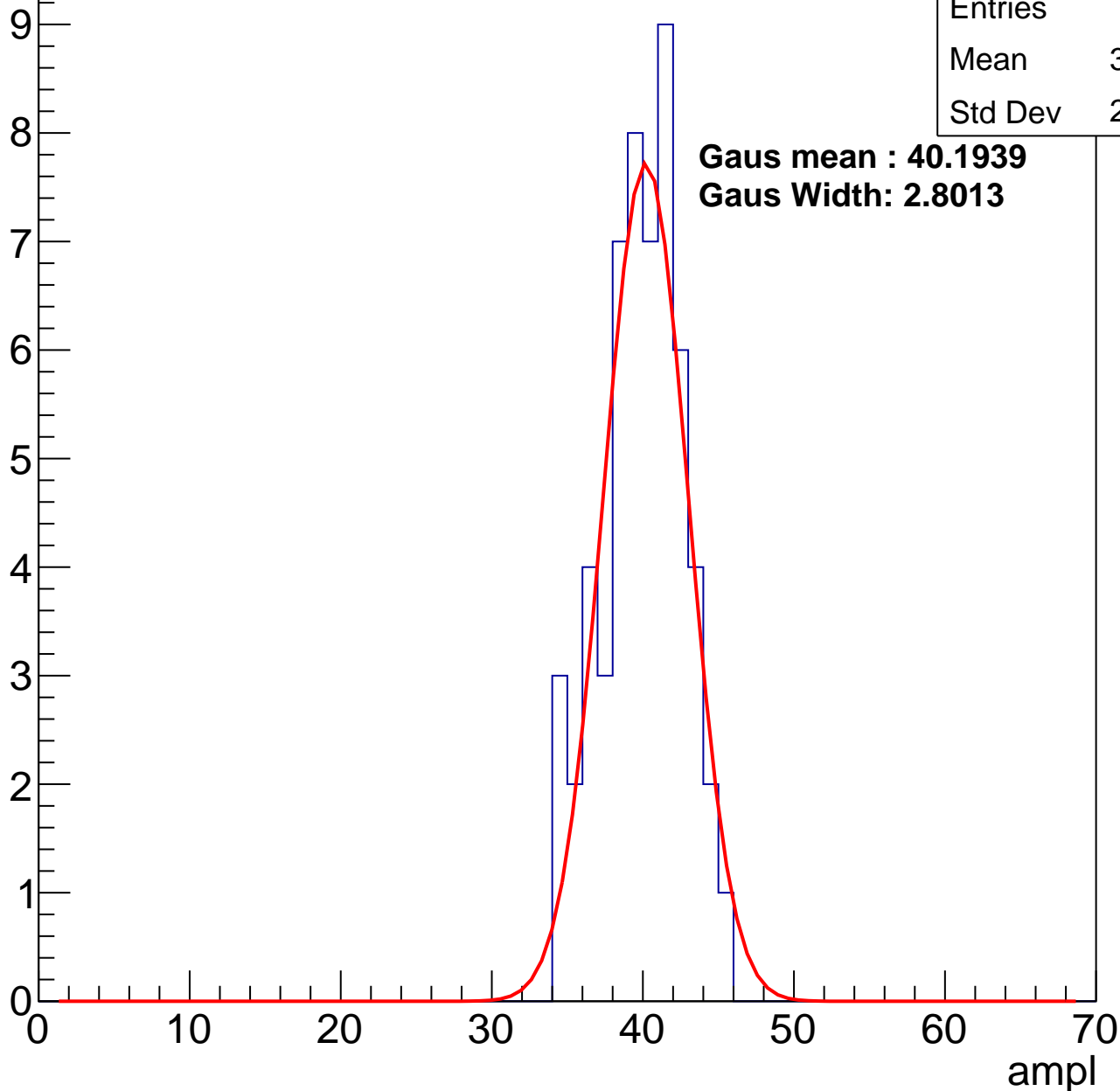
**Gaus Width: 3.3736**



# B1L000S, U2-ch37, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



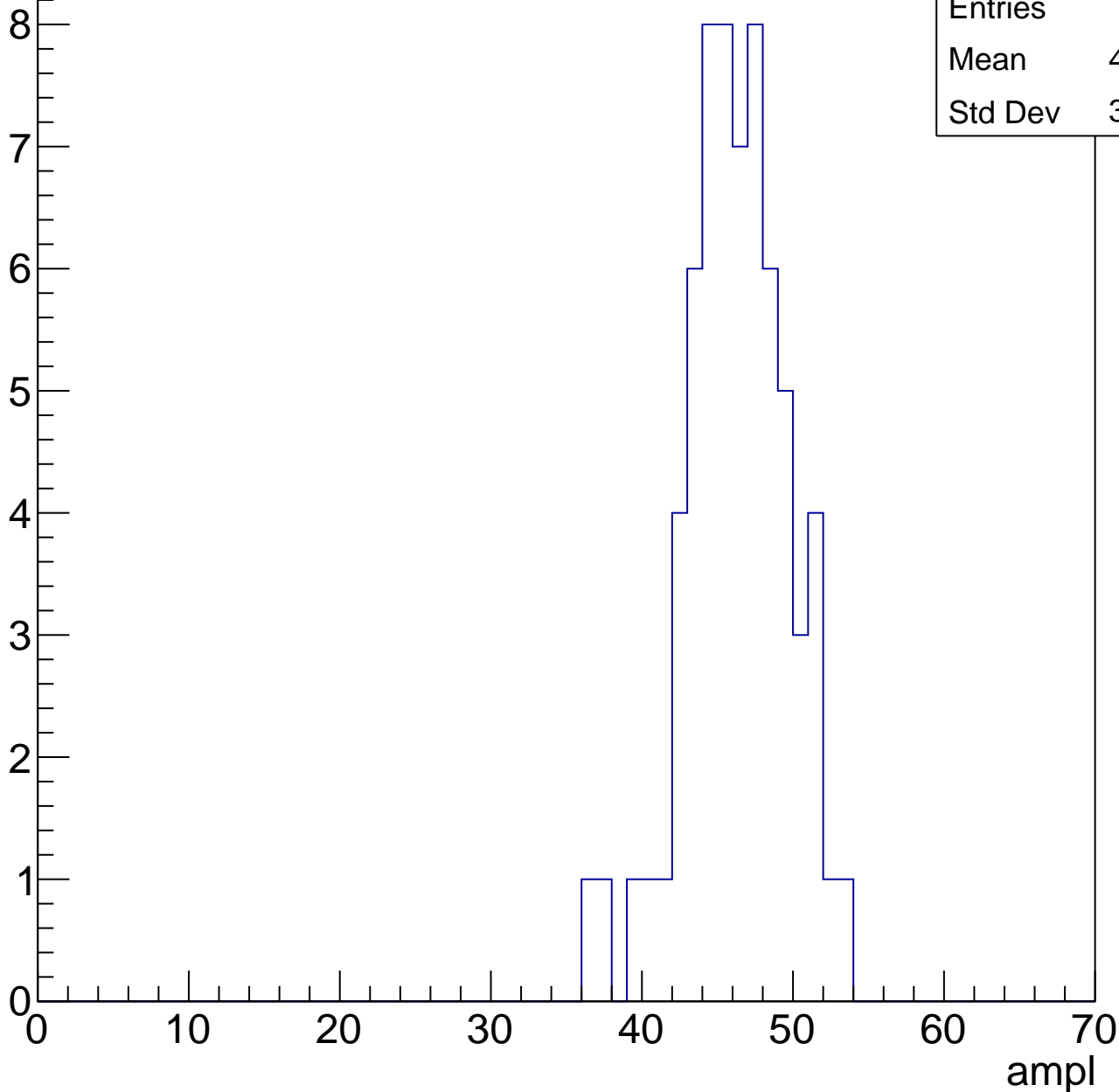
Entries	56
Mean	39.48
Std Dev	2.659

# B1L000S, U2-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	45.77
Std Dev	3.384

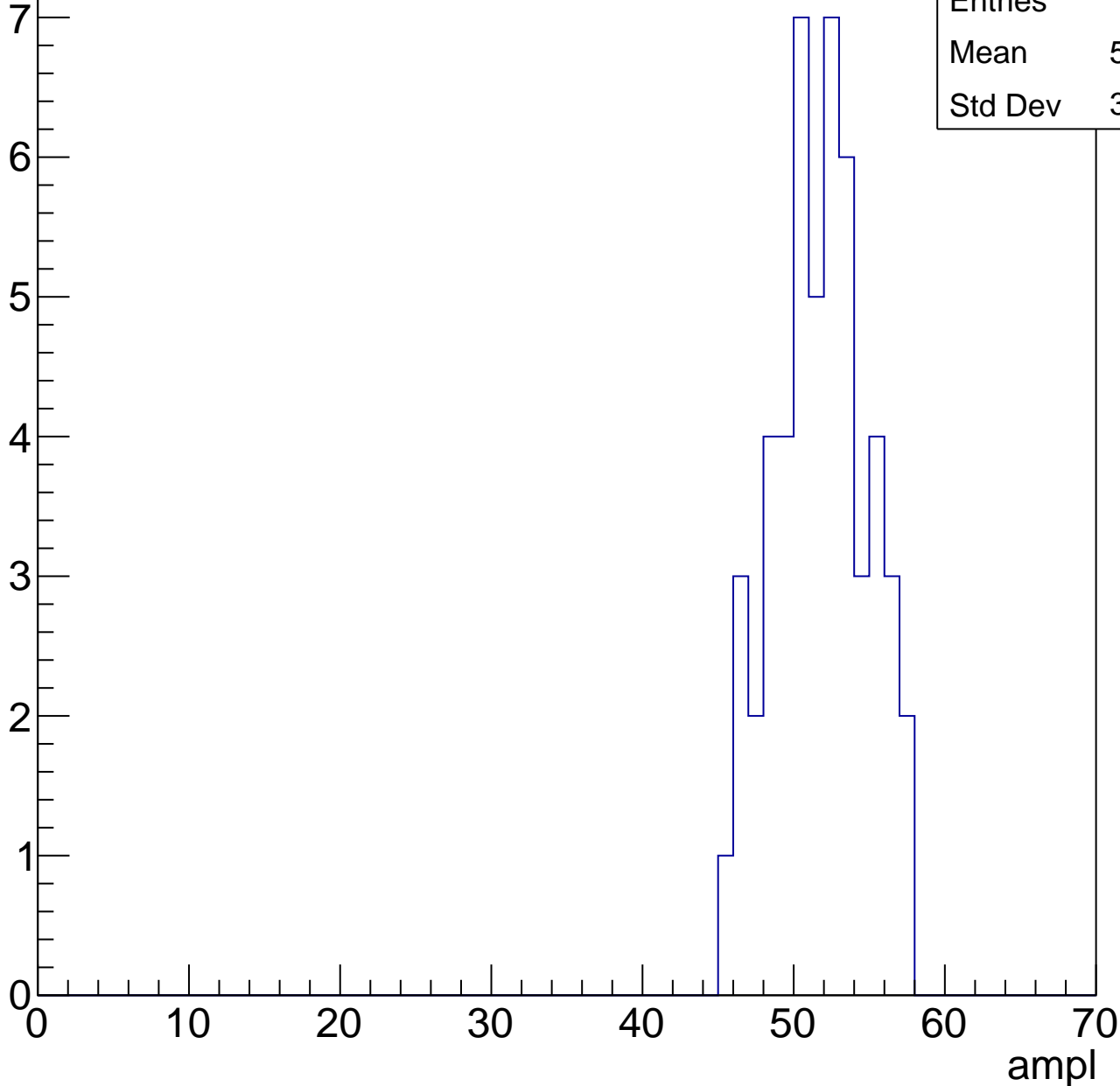


# B1L000S, U2-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	51.29
Std Dev	3.025



# B1L000S, U2-ch37, adc5

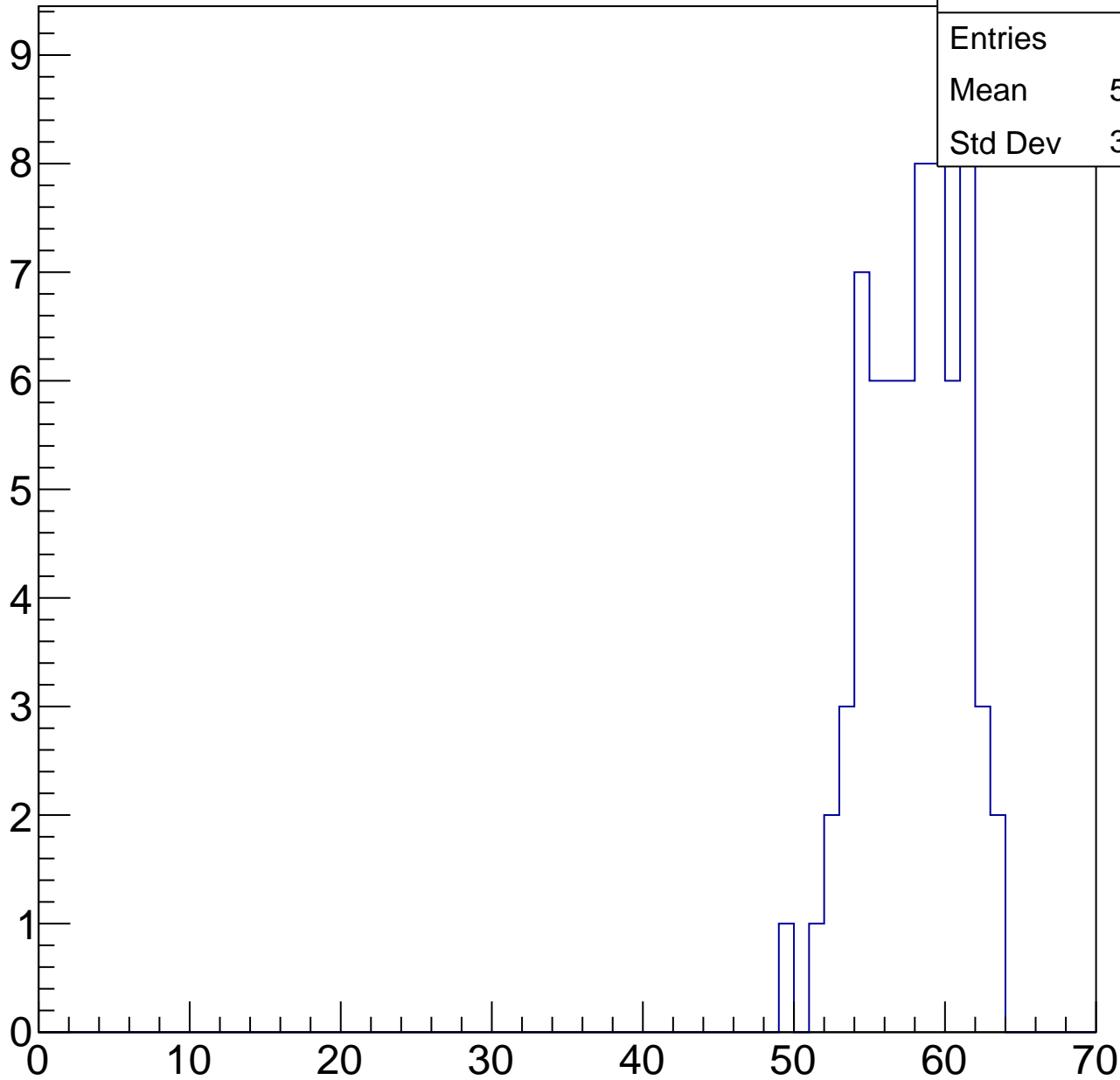
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	68
Mean	57.44
Std Dev	3.122

ampl

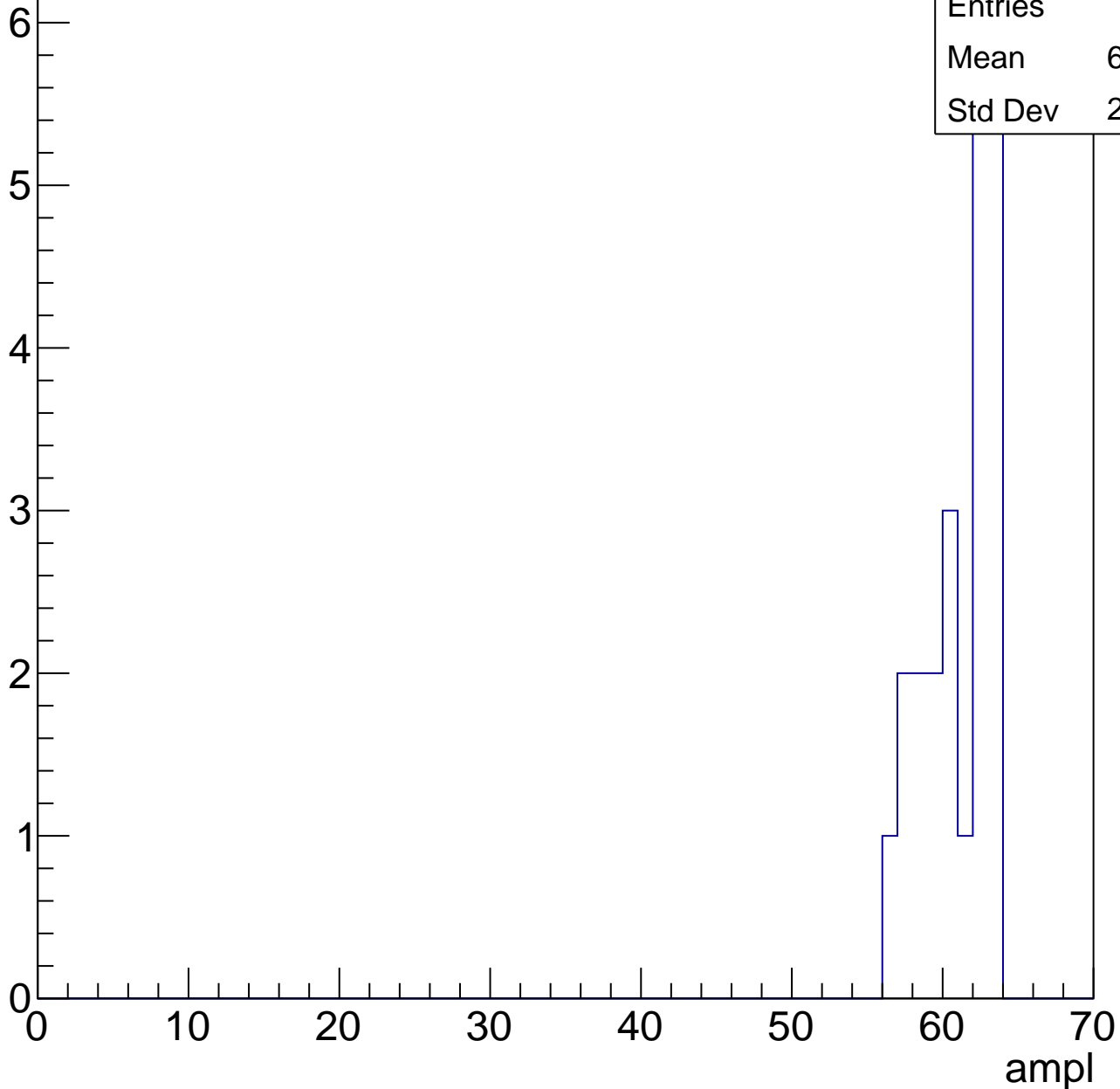


# B1L000S, U2-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	60.65
Std Dev	2.219

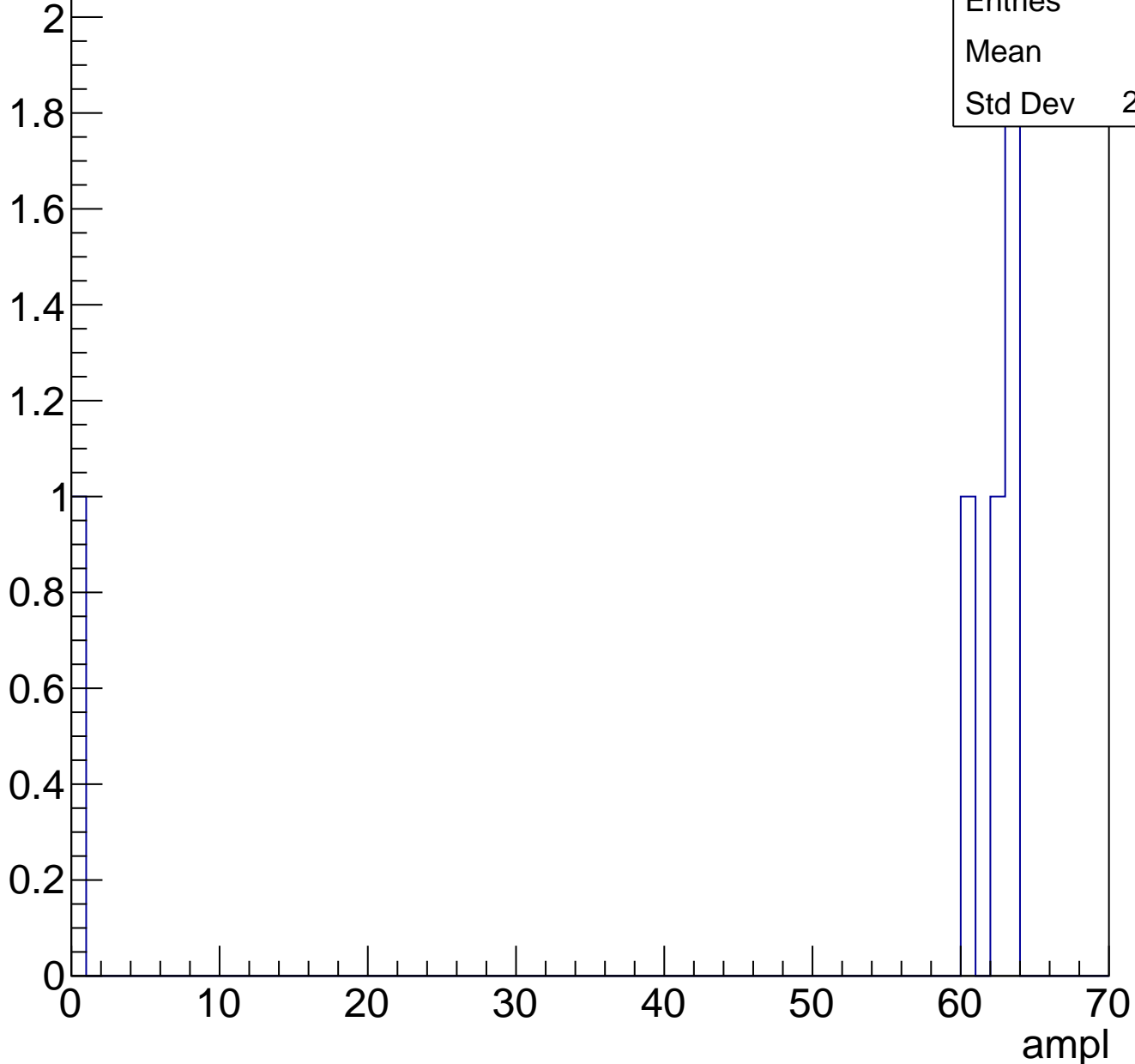




# B1L000S, U2-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch38, adc0

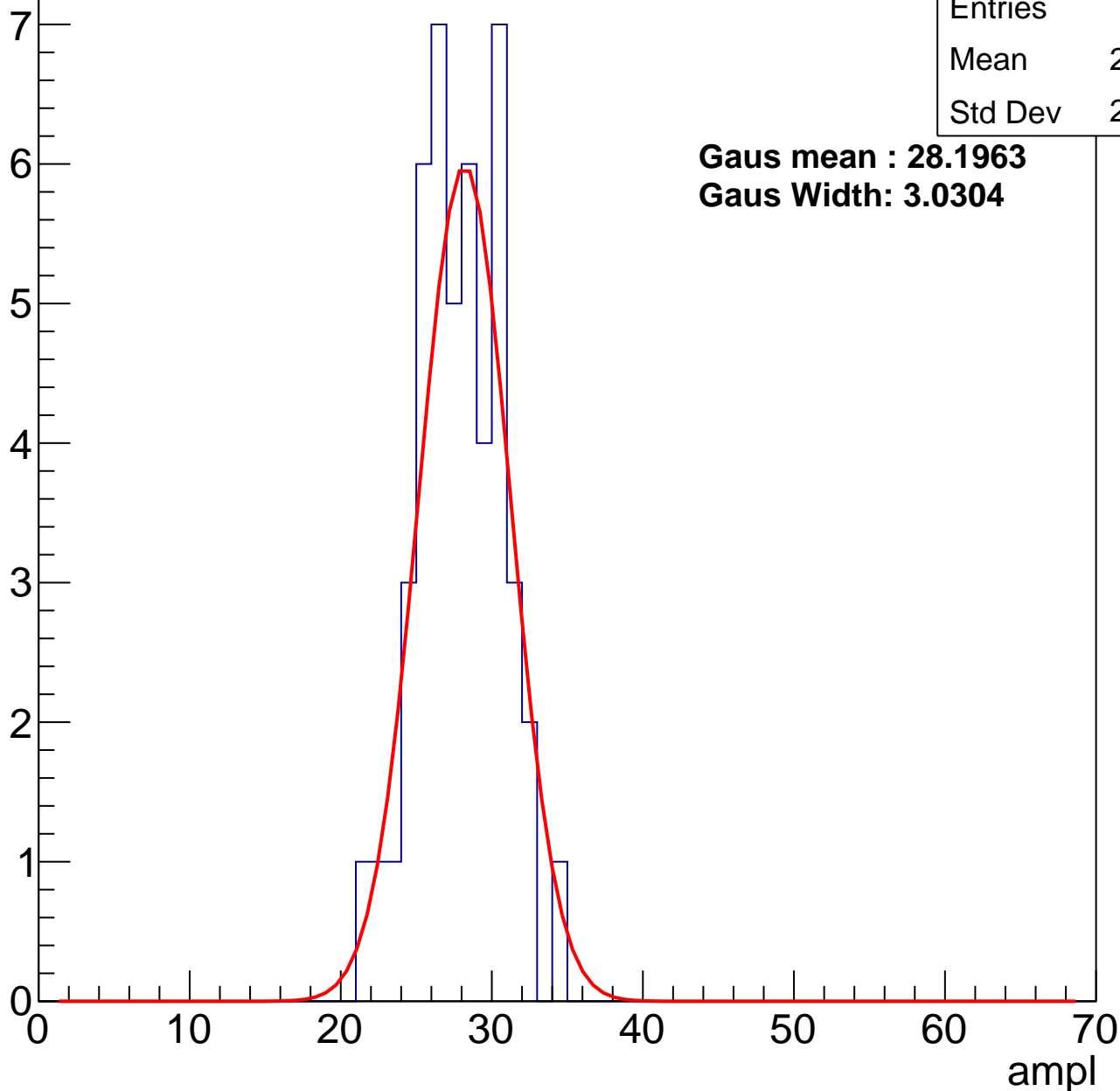
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	27.45
Std Dev	2.758

**Gaus mean : 28.1963**

**Gaus Width: 3.0304**



# B1L000S, U2-ch38, adc1

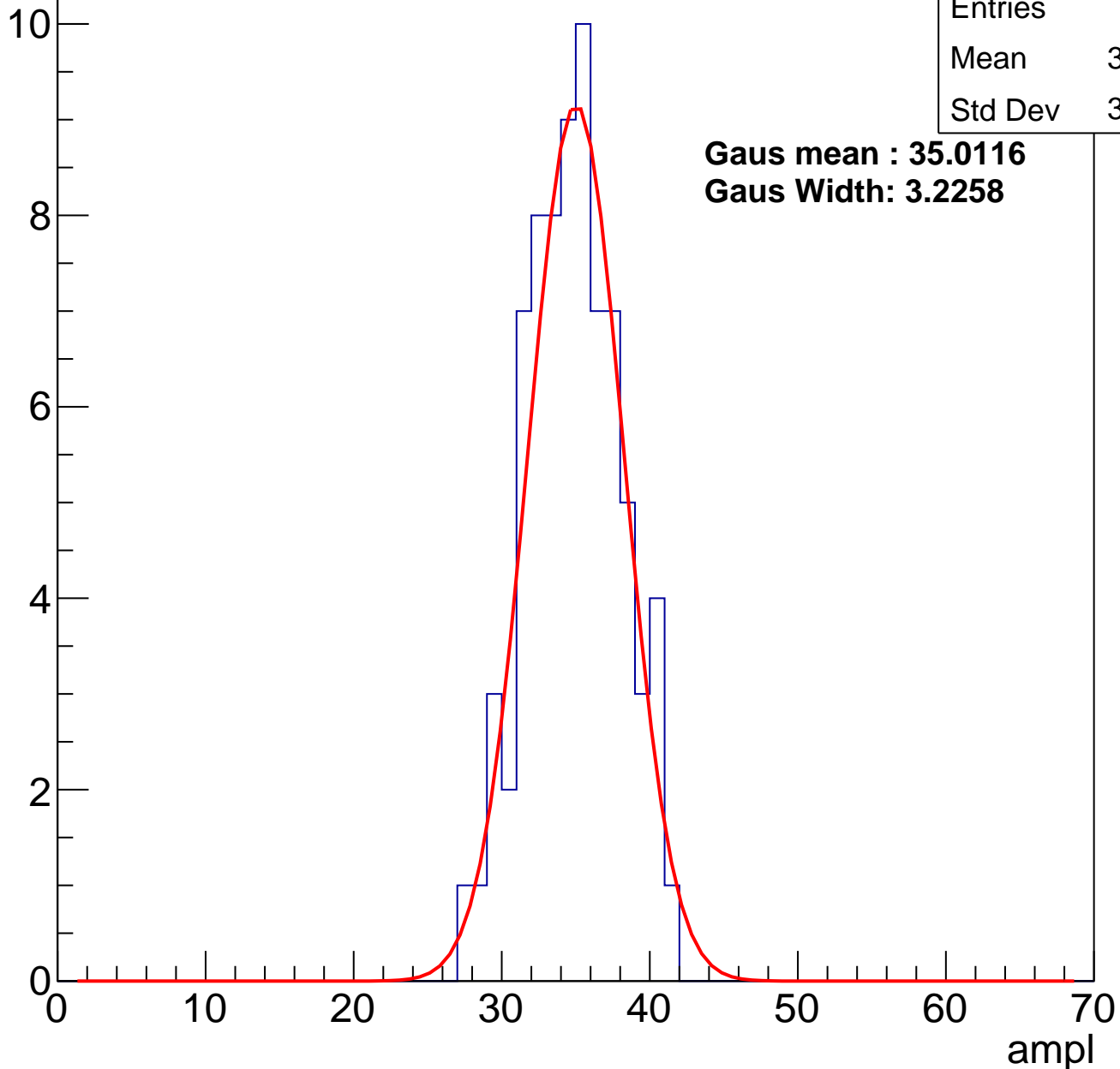
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	34.39
Std Dev	3.104

**Gaus mean : 35.0116**

**Gaus Width: 3.2258**

Entry



# B1L000S, U2-ch38, adc2

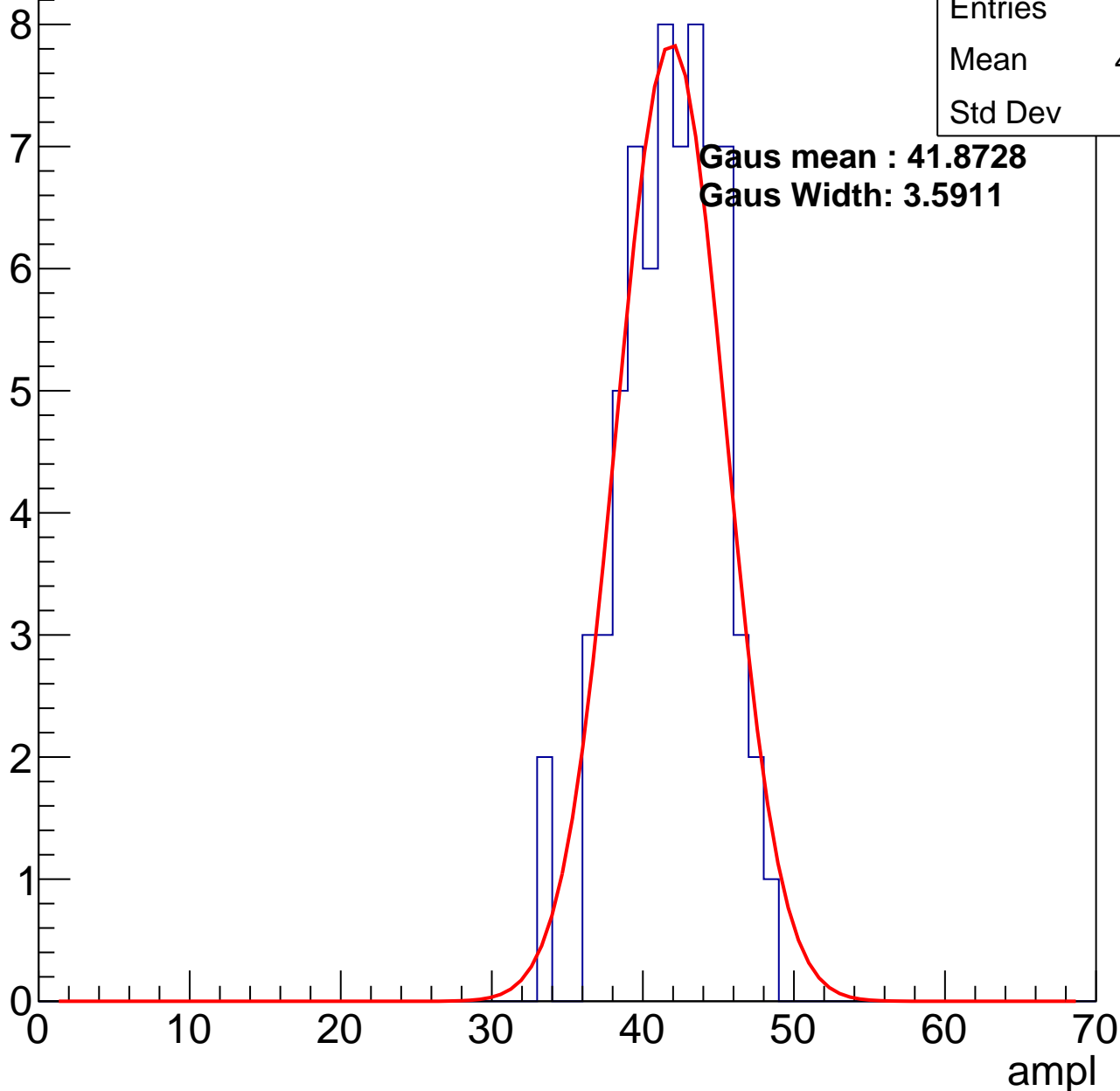
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	41.41
Std Dev	3.25

**Gaus mean : 41.8728**

**Gaus Width: 3.5911**

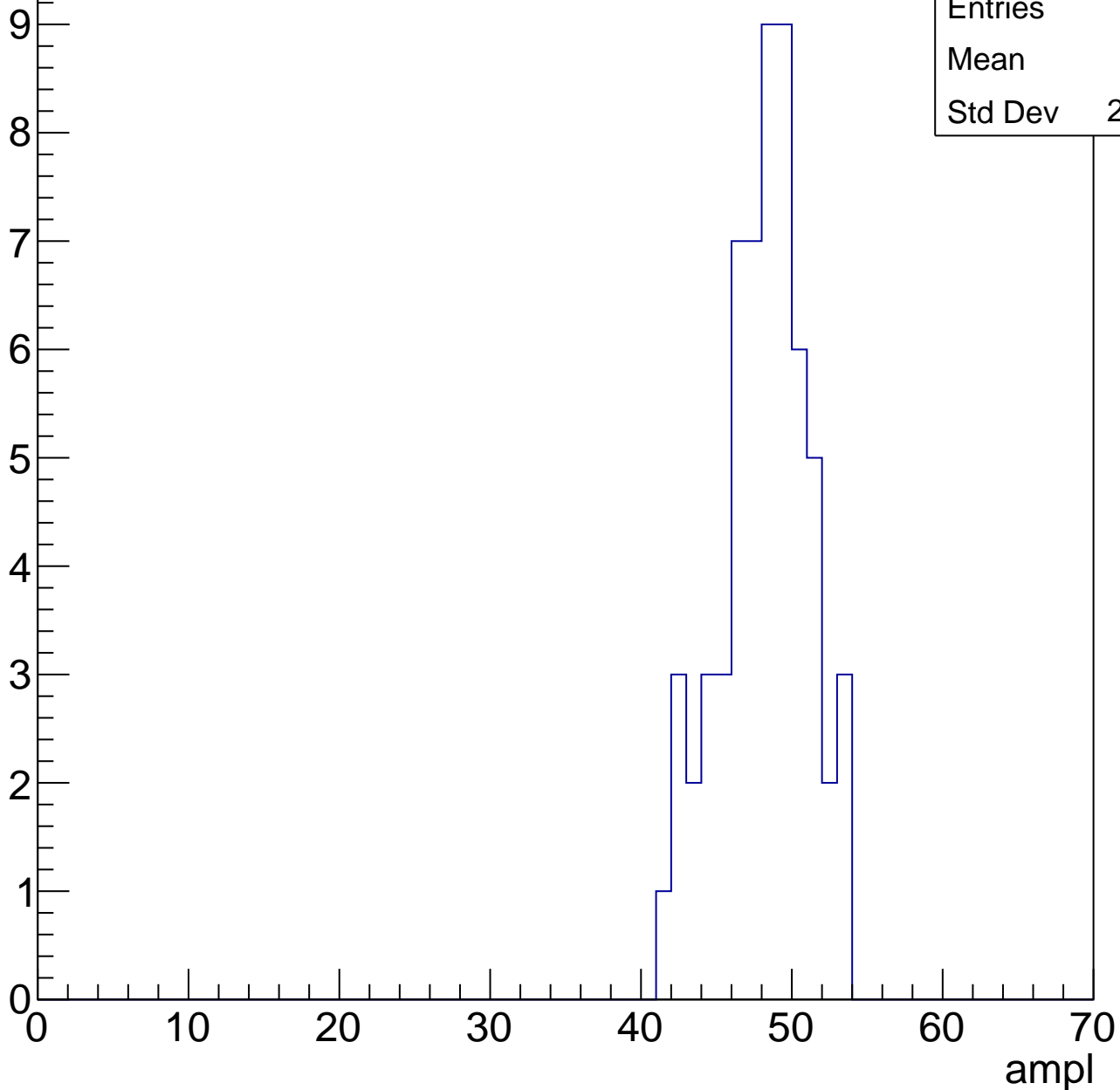


# B1L000S, U2-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

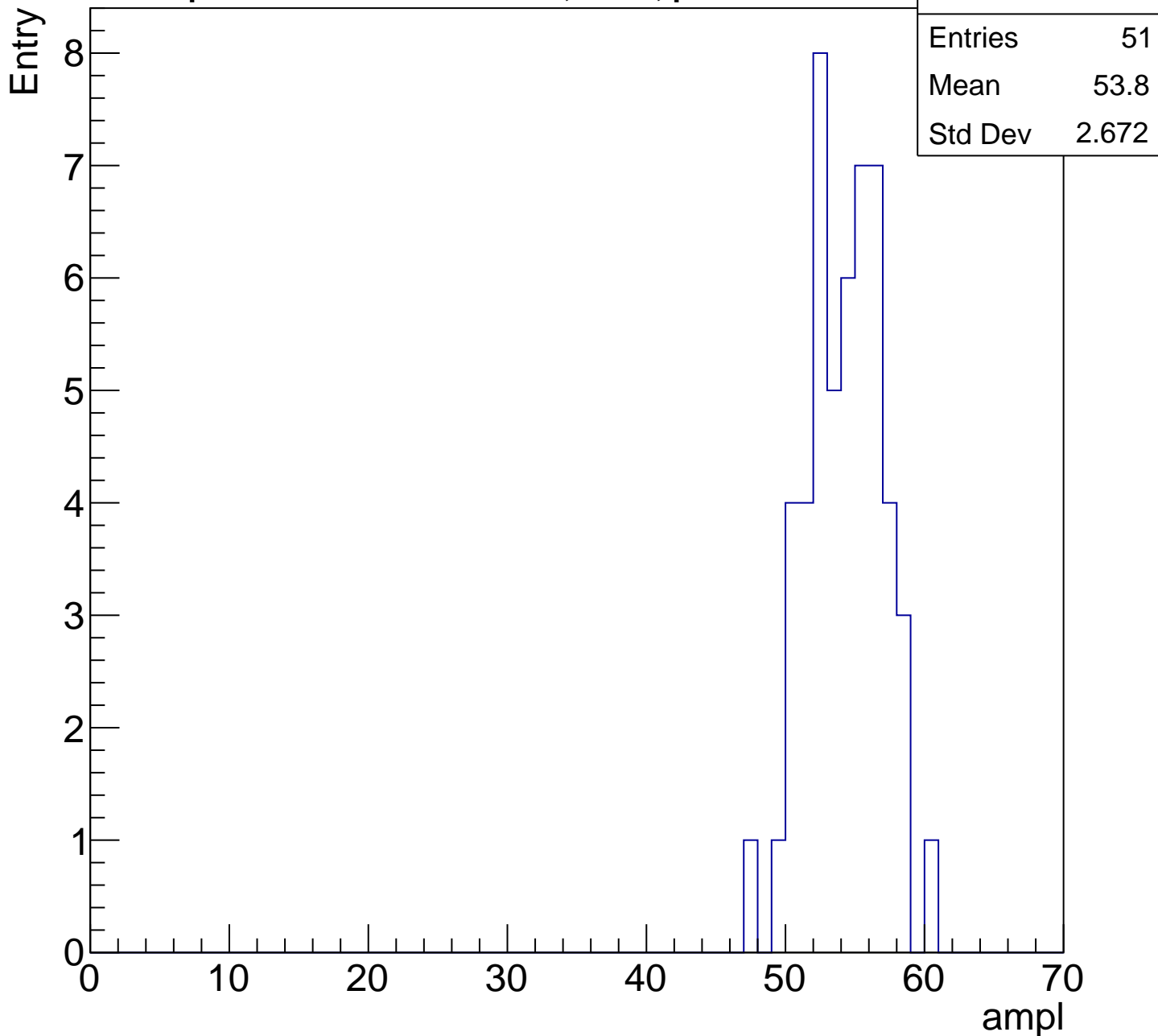
Entry

Entries	60
Mean	47.7
Std Dev	2.877



# B1L000S, U2-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

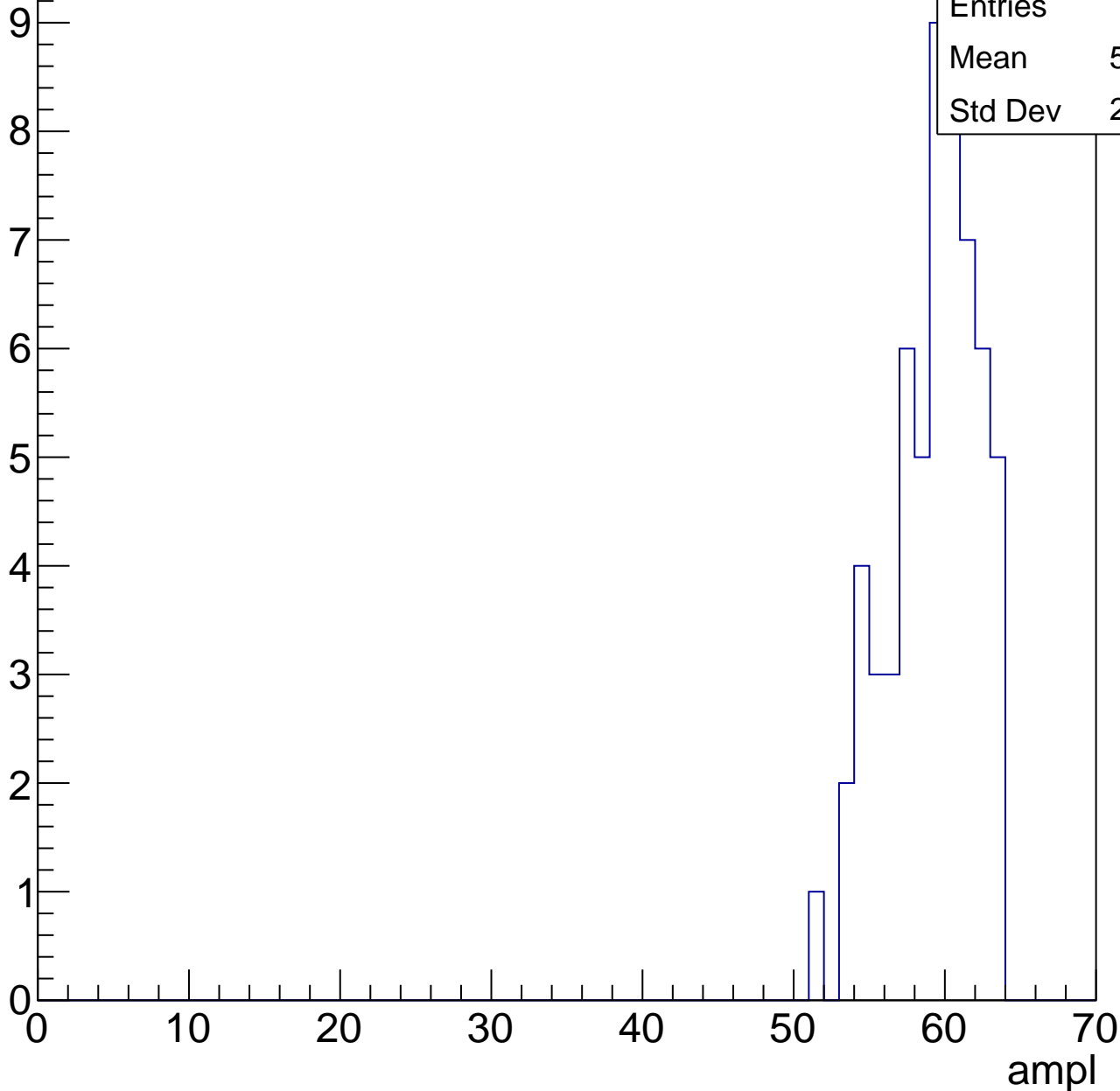


# B1L000S, U2-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

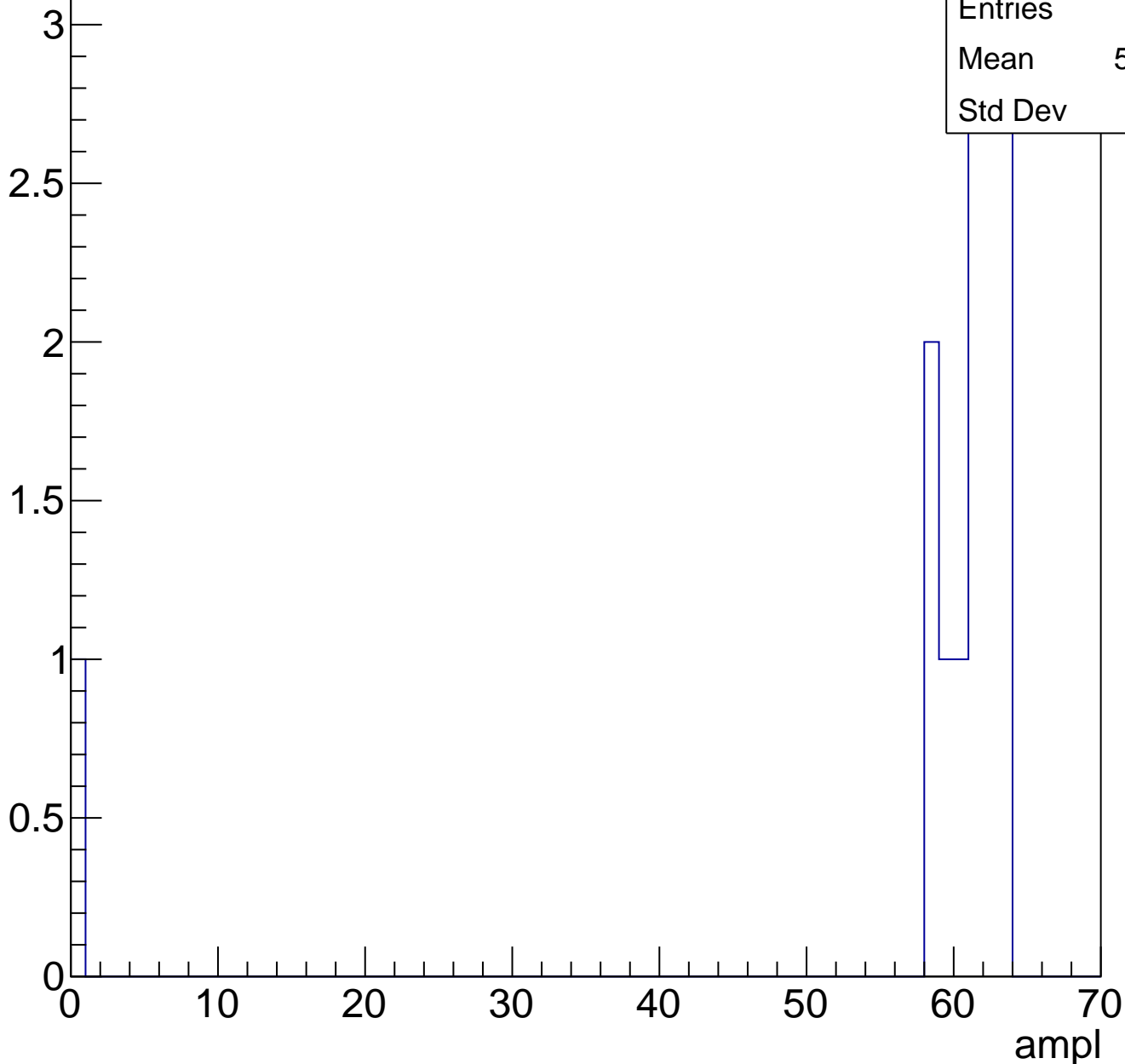
Entries	59
Mean	58.69
Std Dev	2.924



# B1L000S, U2-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch39, adc0

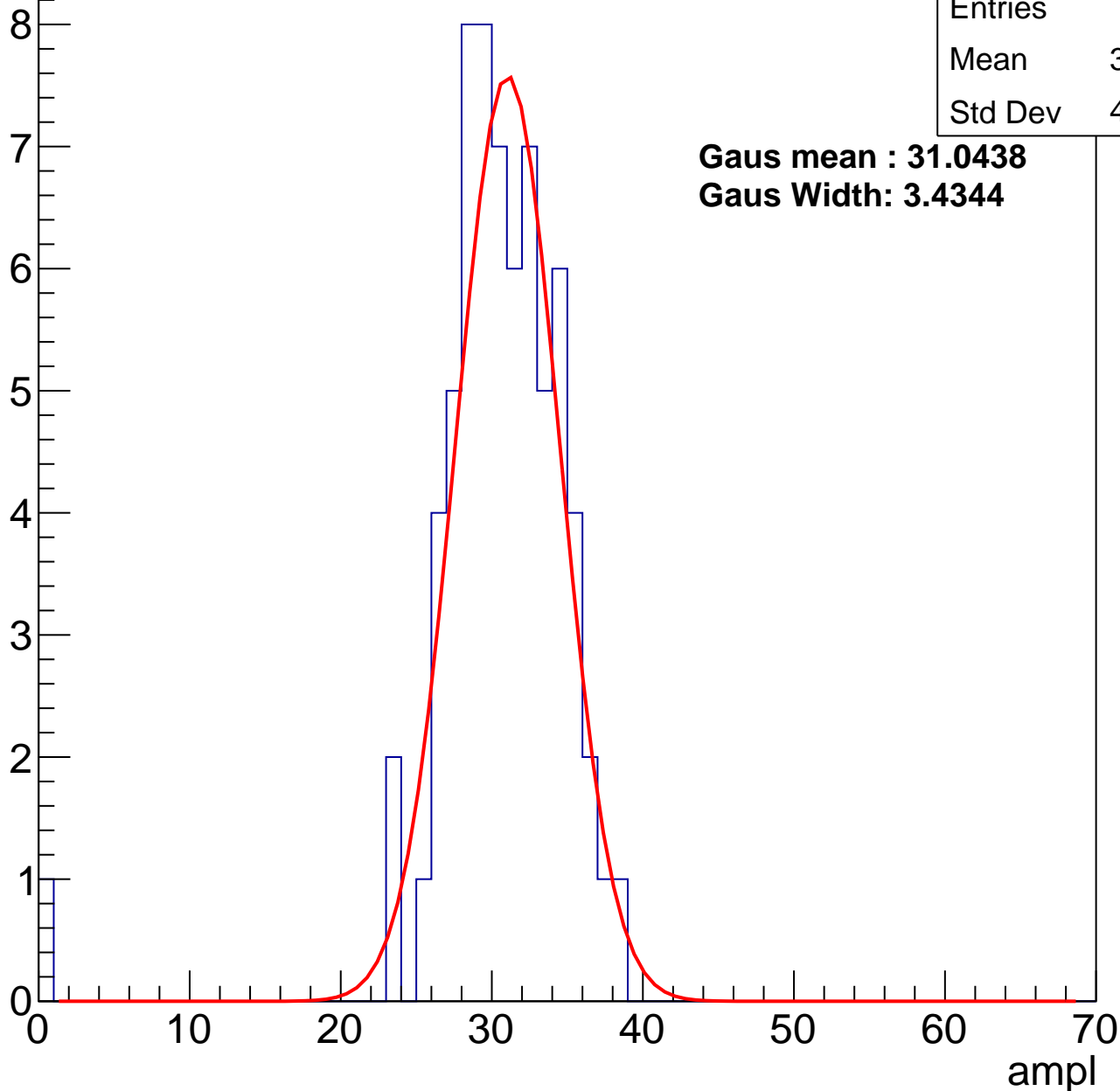
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	30.03
Std Dev	4.899

**Gaus mean : 31.0438**

**Gaus Width: 3.4344**



# B1L000S, U2-ch39, adc1

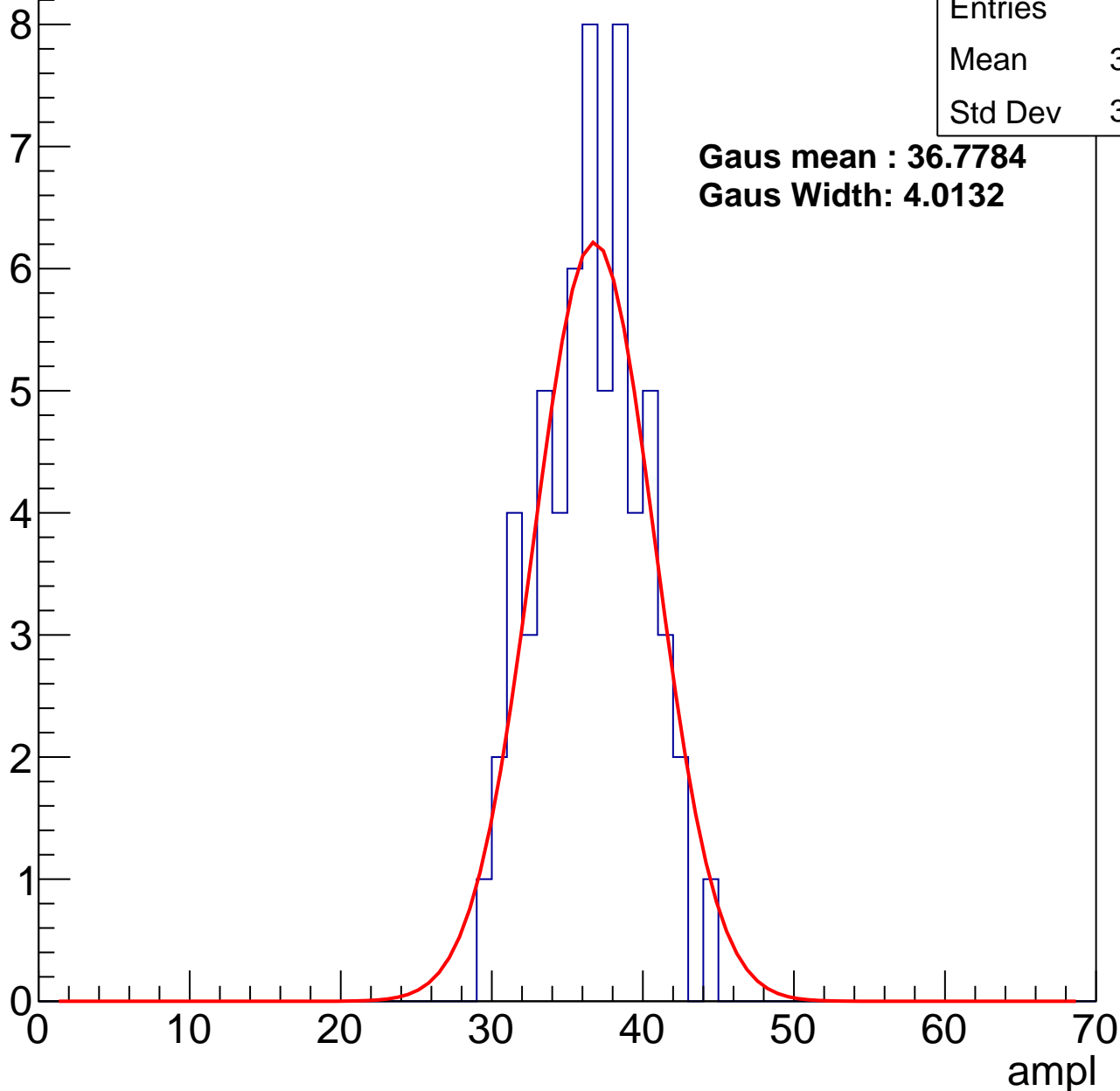
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	36.13
Std Dev	3.385

**Gaus mean : 36.7784**

**Gaus Width: 4.0132**



# B1L000S, U2-ch39, adc2

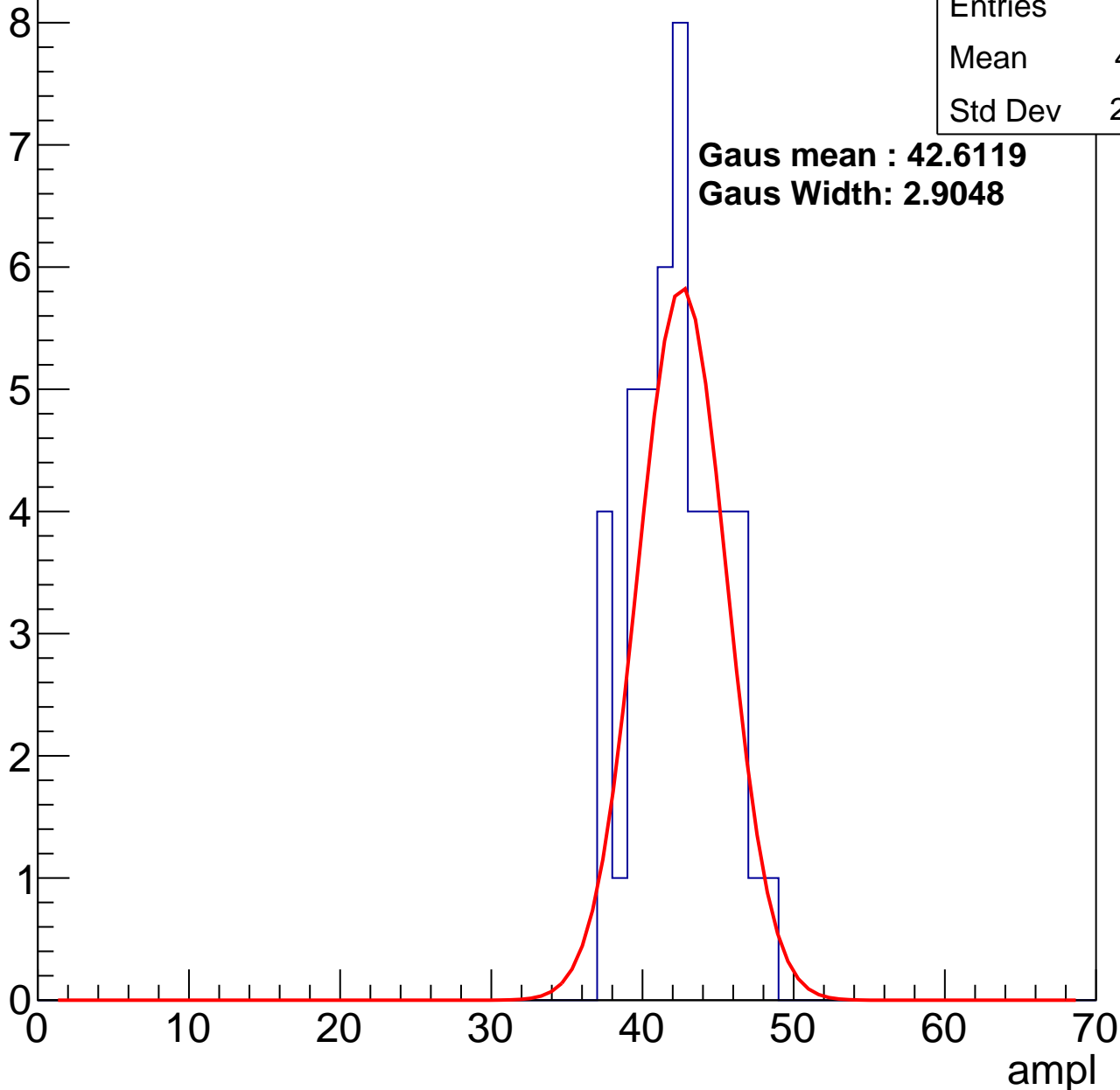
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	41.91
Std Dev	2.797

**Gaus mean : 42.6119**

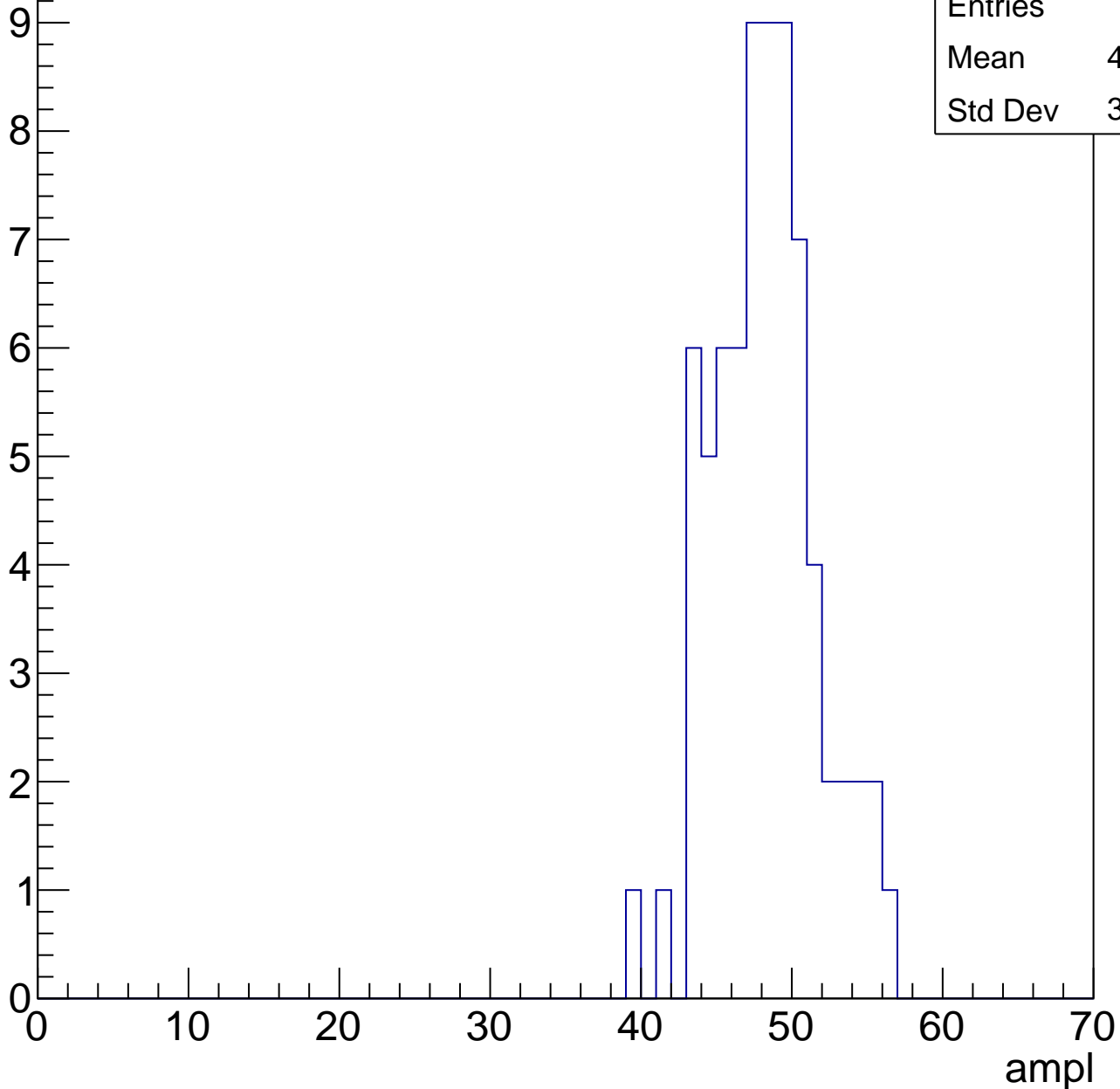
**Gaus Width: 2.9048**



# B1L000S, U2-ch39, adc3

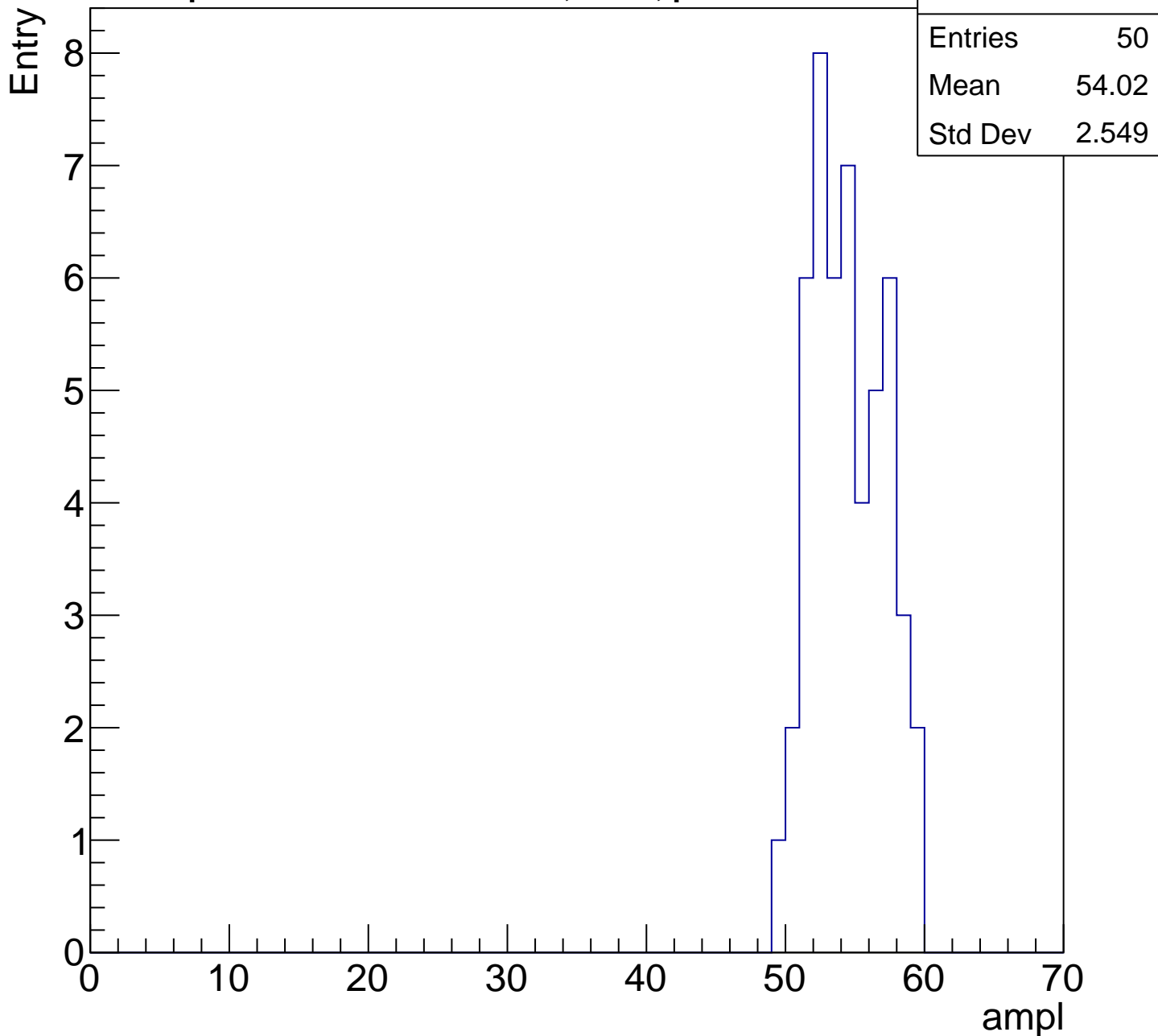
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch39, adc4

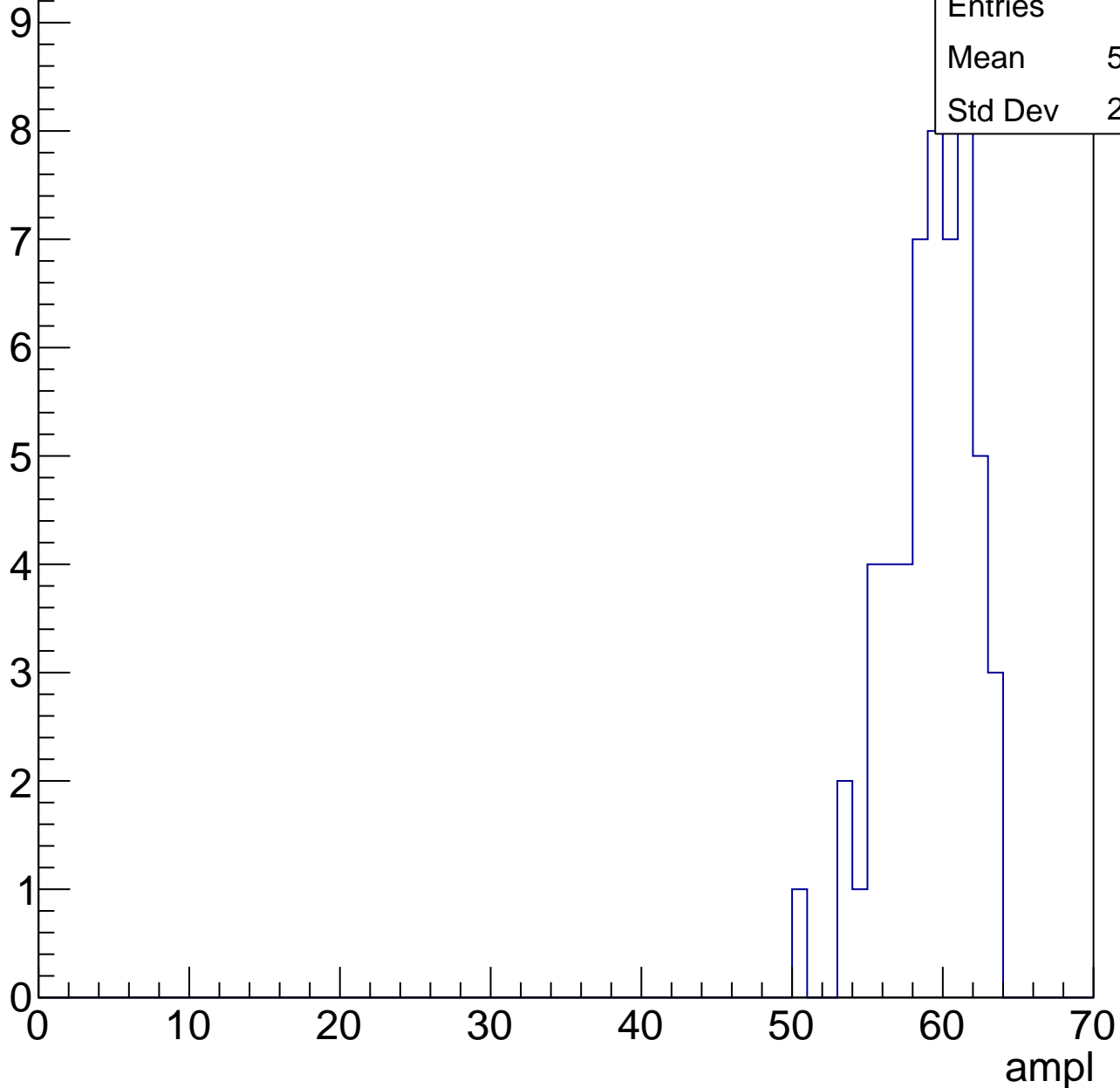
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

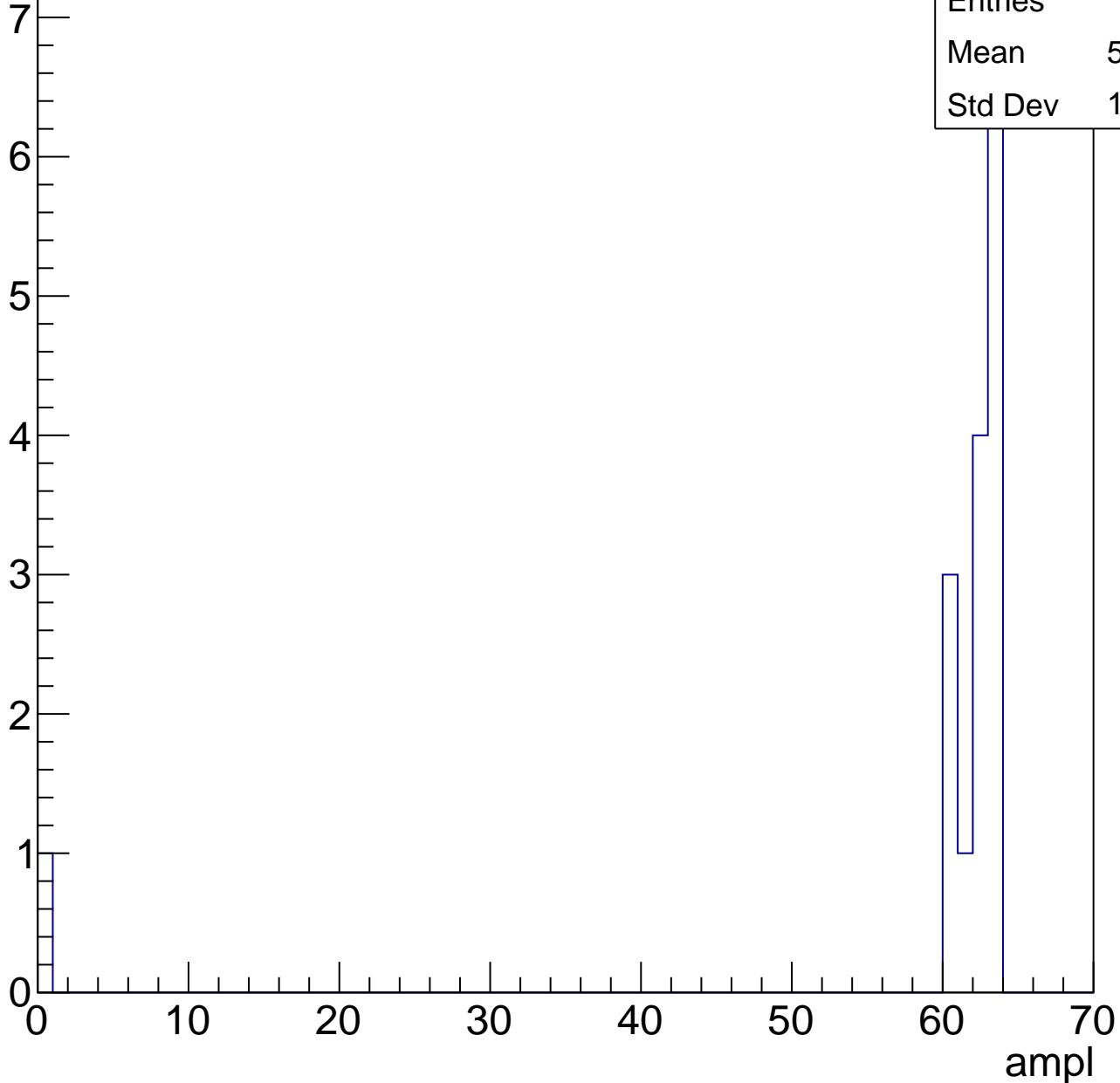


# B1L000S, U2-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	58.12
Std Dev	15.05





# B1L000S, U2-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch40, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	27.05
Std Dev	6.674

**Gaus mean : 28.9092**

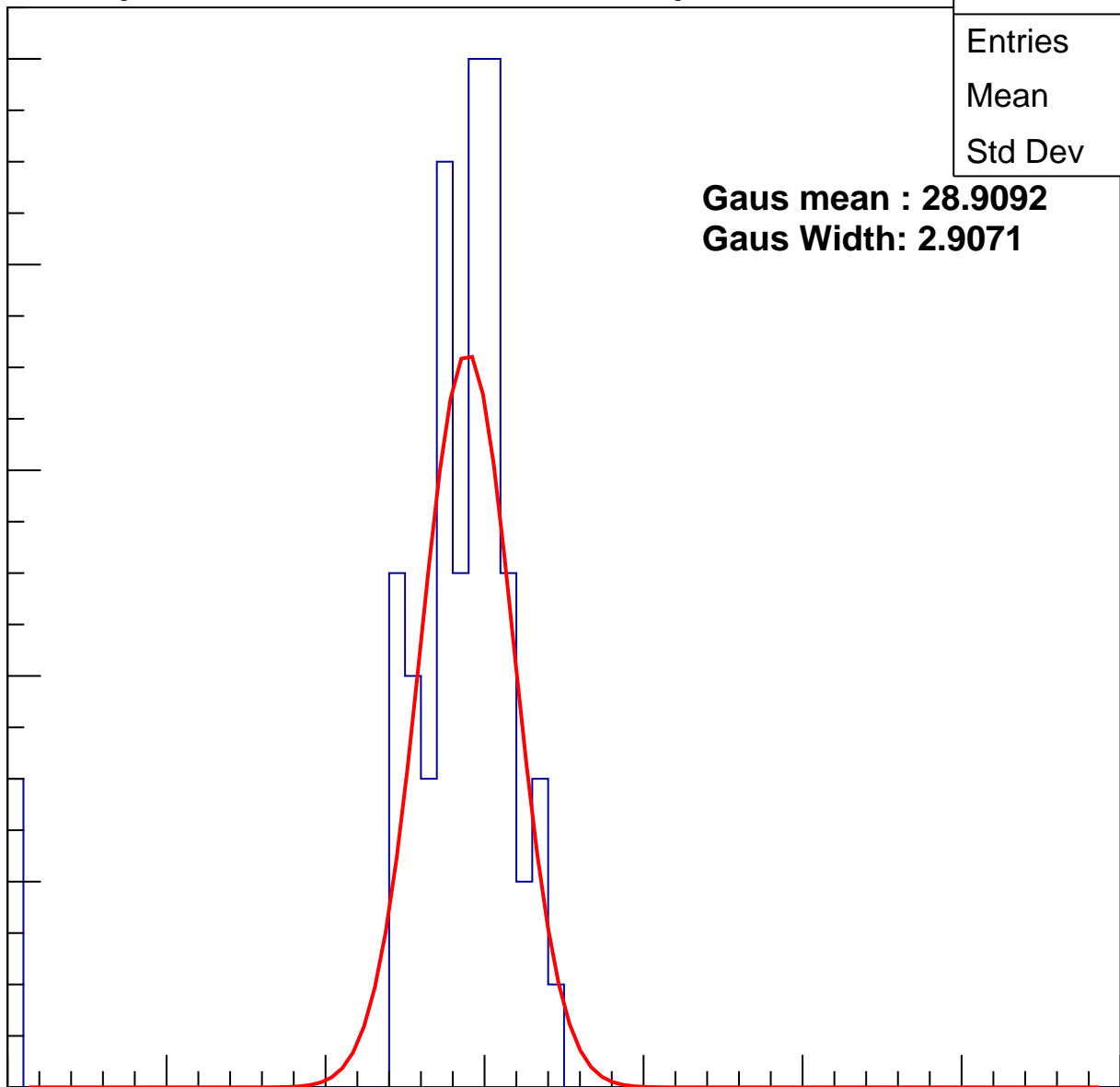
**Gaus Width: 2.9071**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch40, adc1

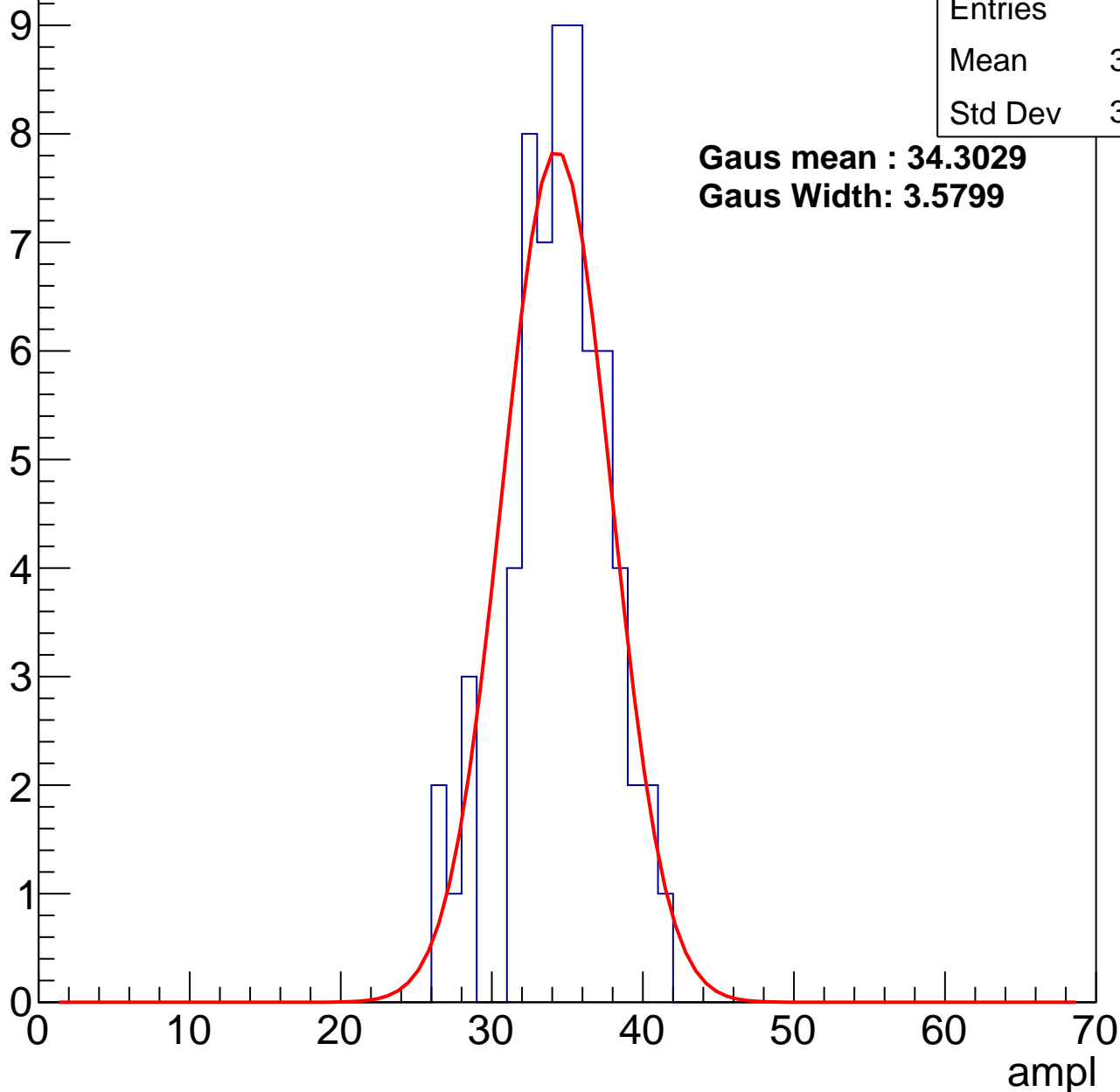
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.12
Std Dev	3.262

**Gaus mean : 34.3029**

**Gaus Width: 3.5799**



# B1L000S, U2-ch40, adc2

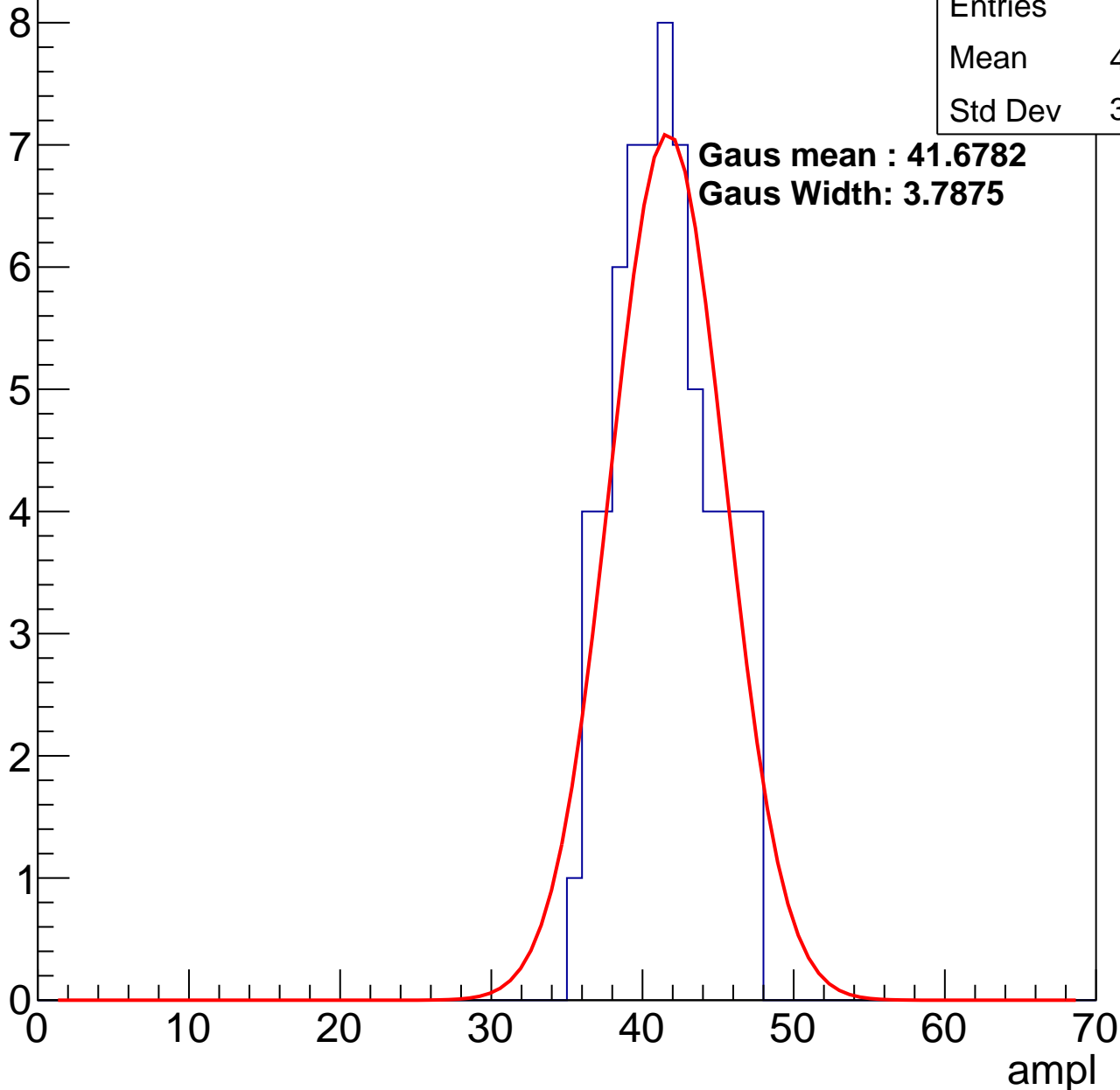
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.12
Std Dev	3.184

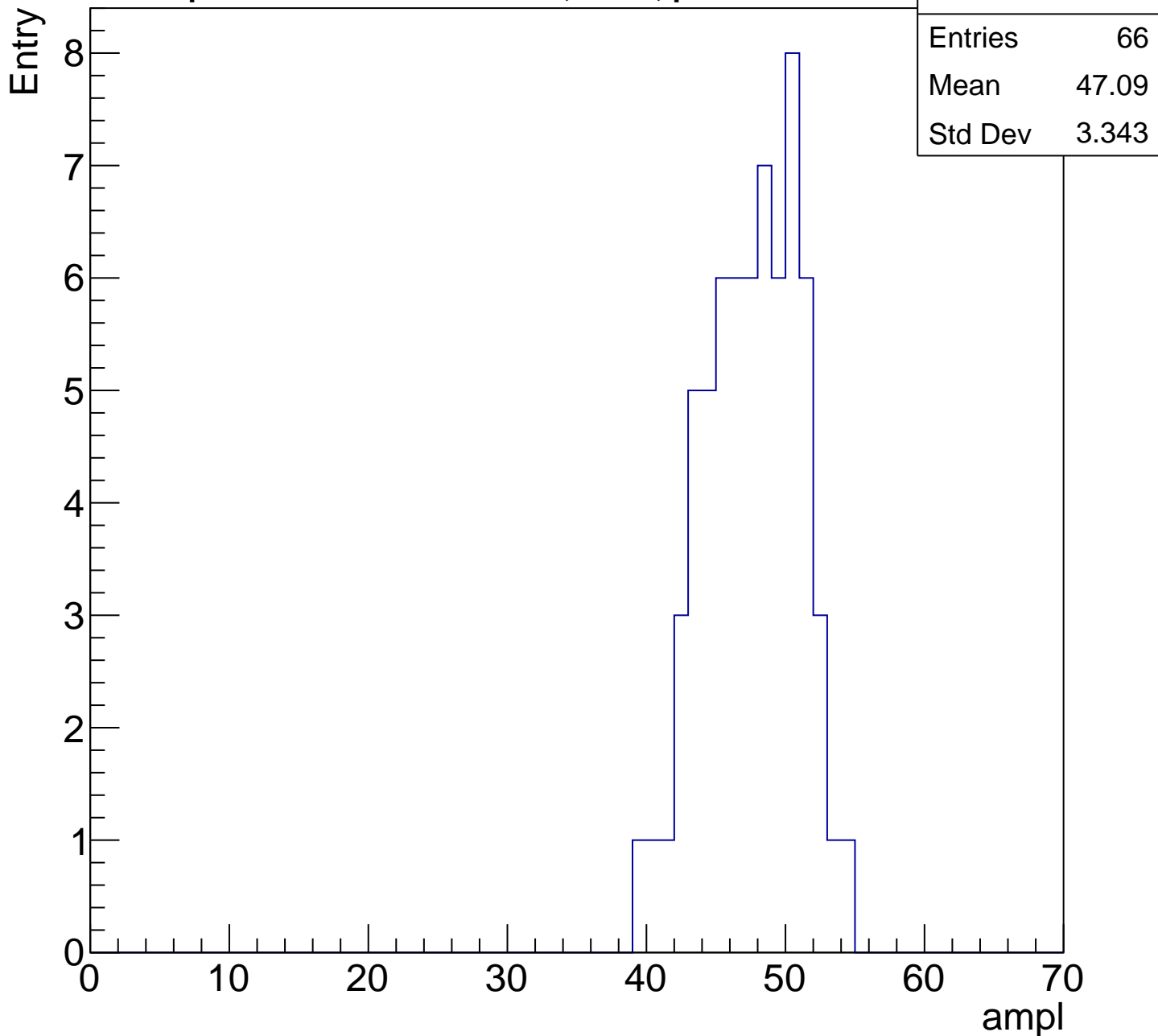
**Gaus mean : 41.6782**

**Gaus Width: 3.7875**



# B1L000S, U2-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

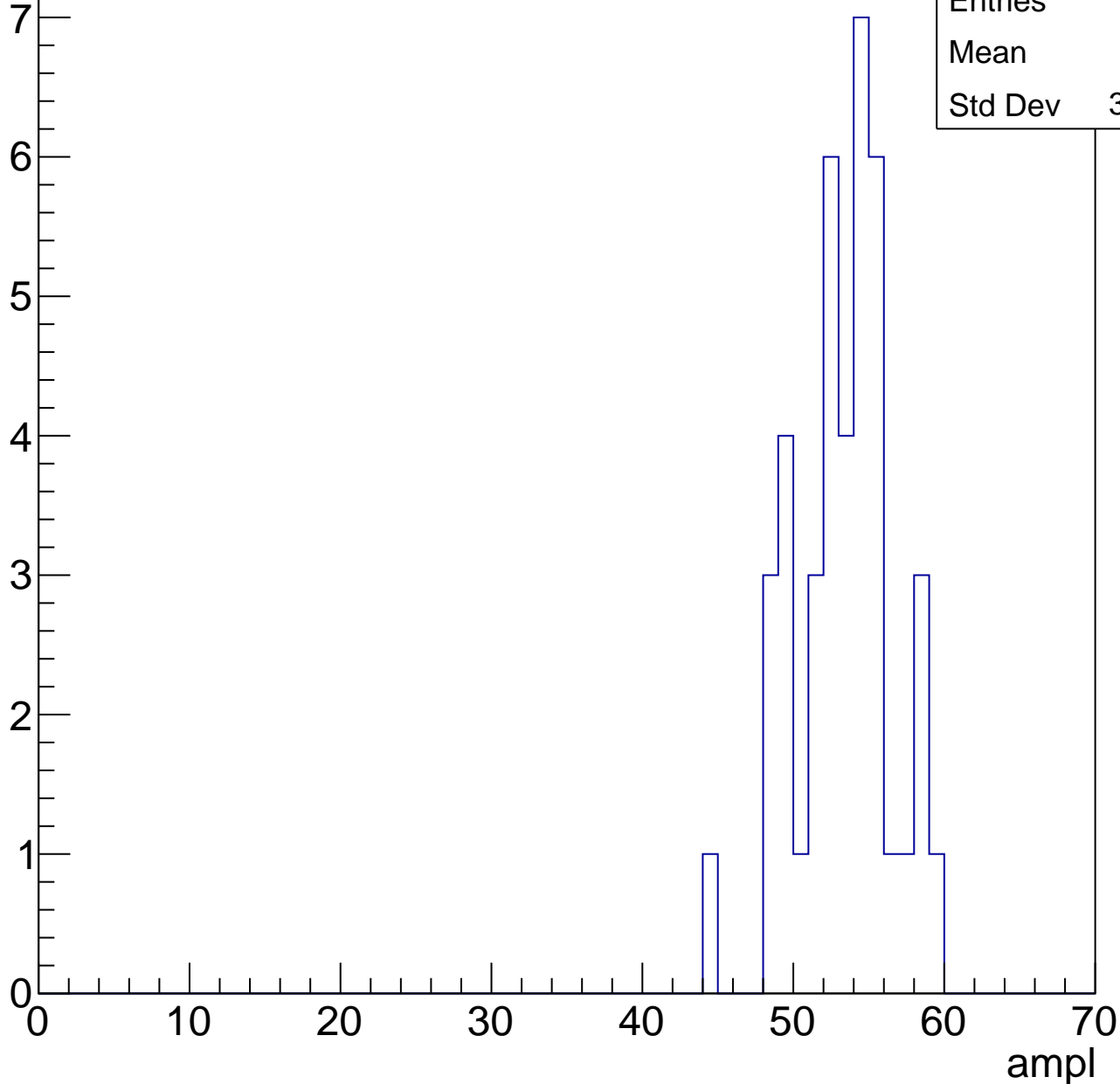


# B1L000S, U2-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

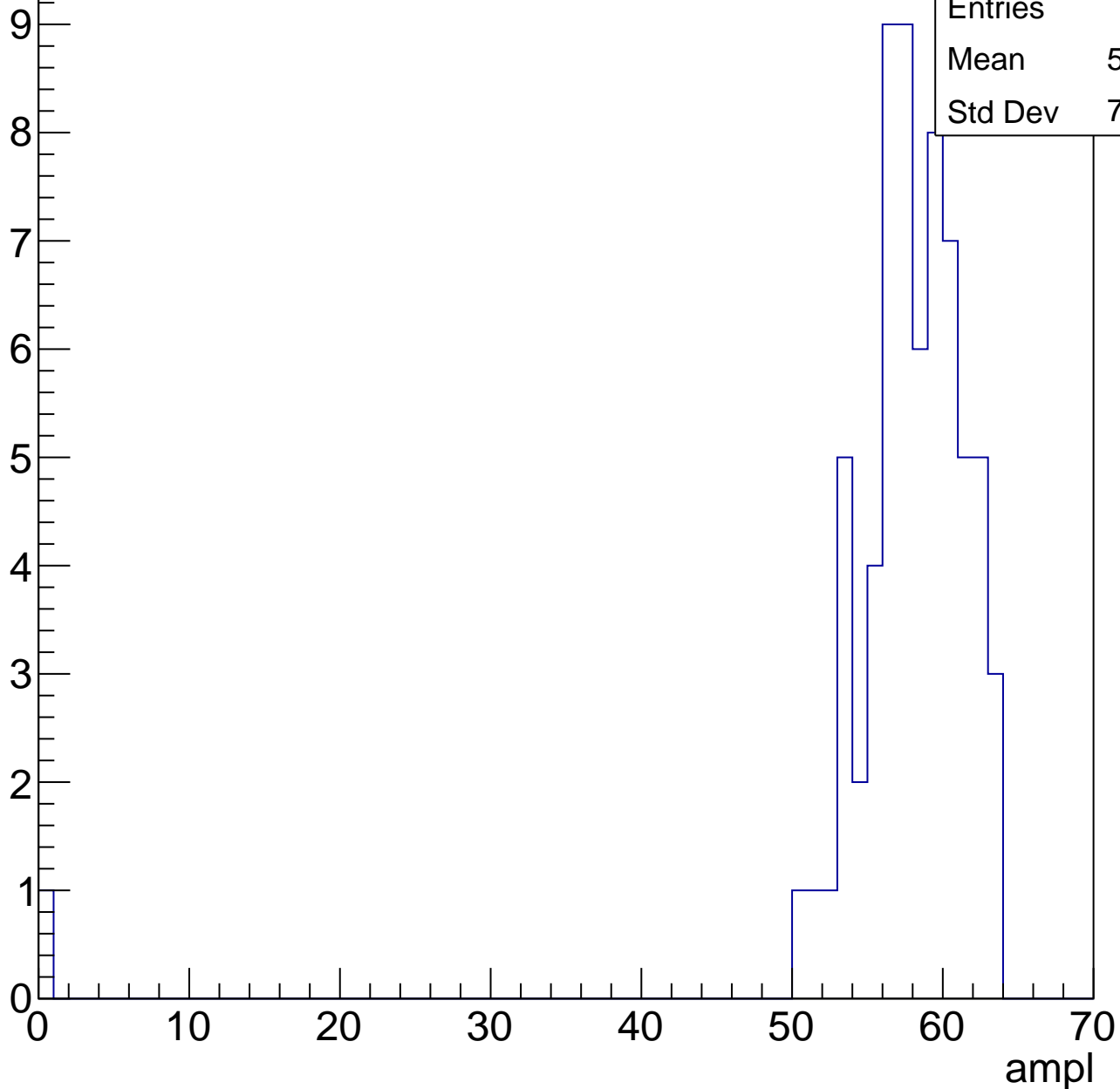
Entries	41
Mean	52.8
Std Dev	3.172



# B1L000S, U2-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

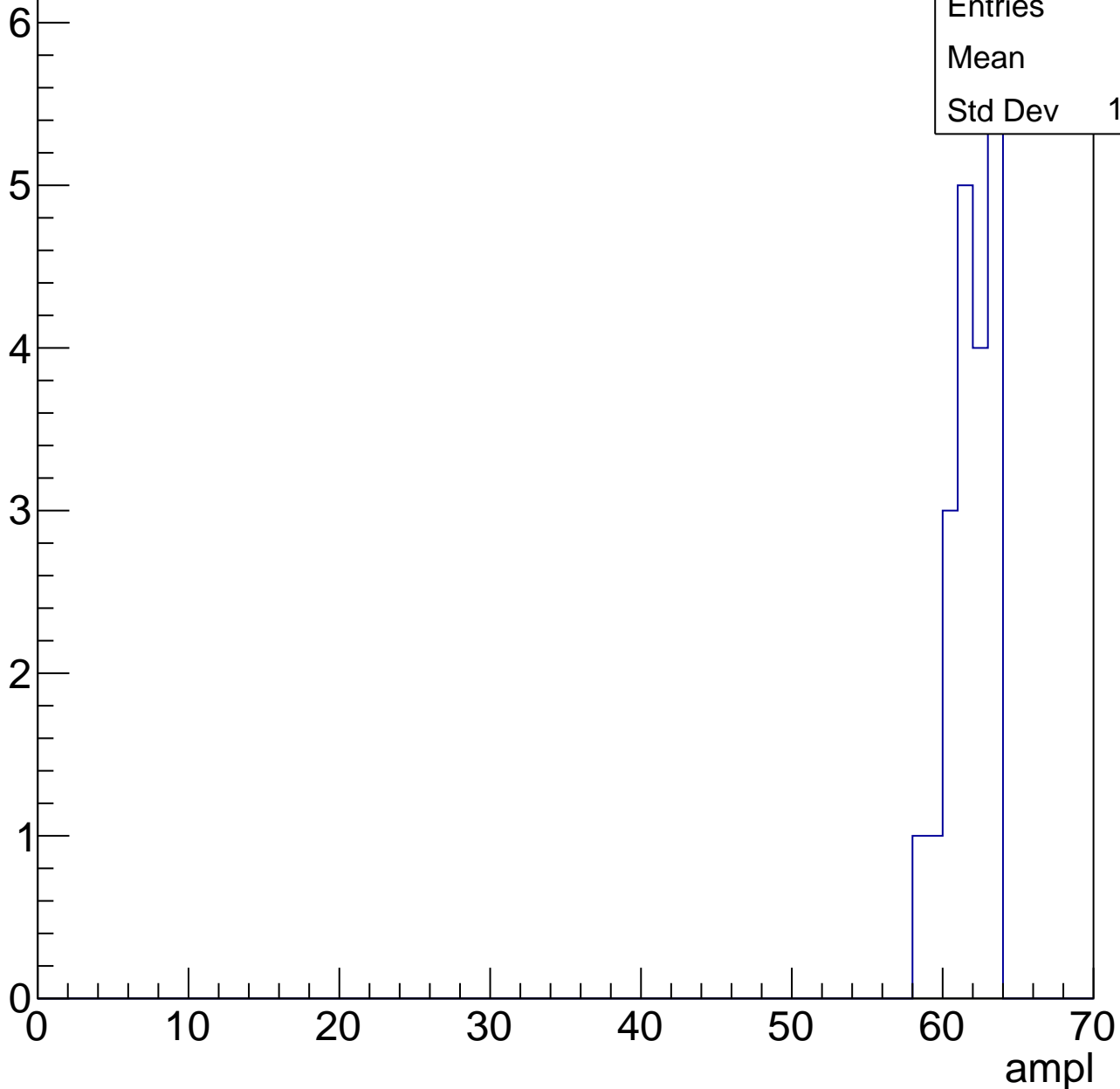


# B1L000S, U2-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	61.4
Std Dev	1.428





# B1L000S, U2-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch41, adc0

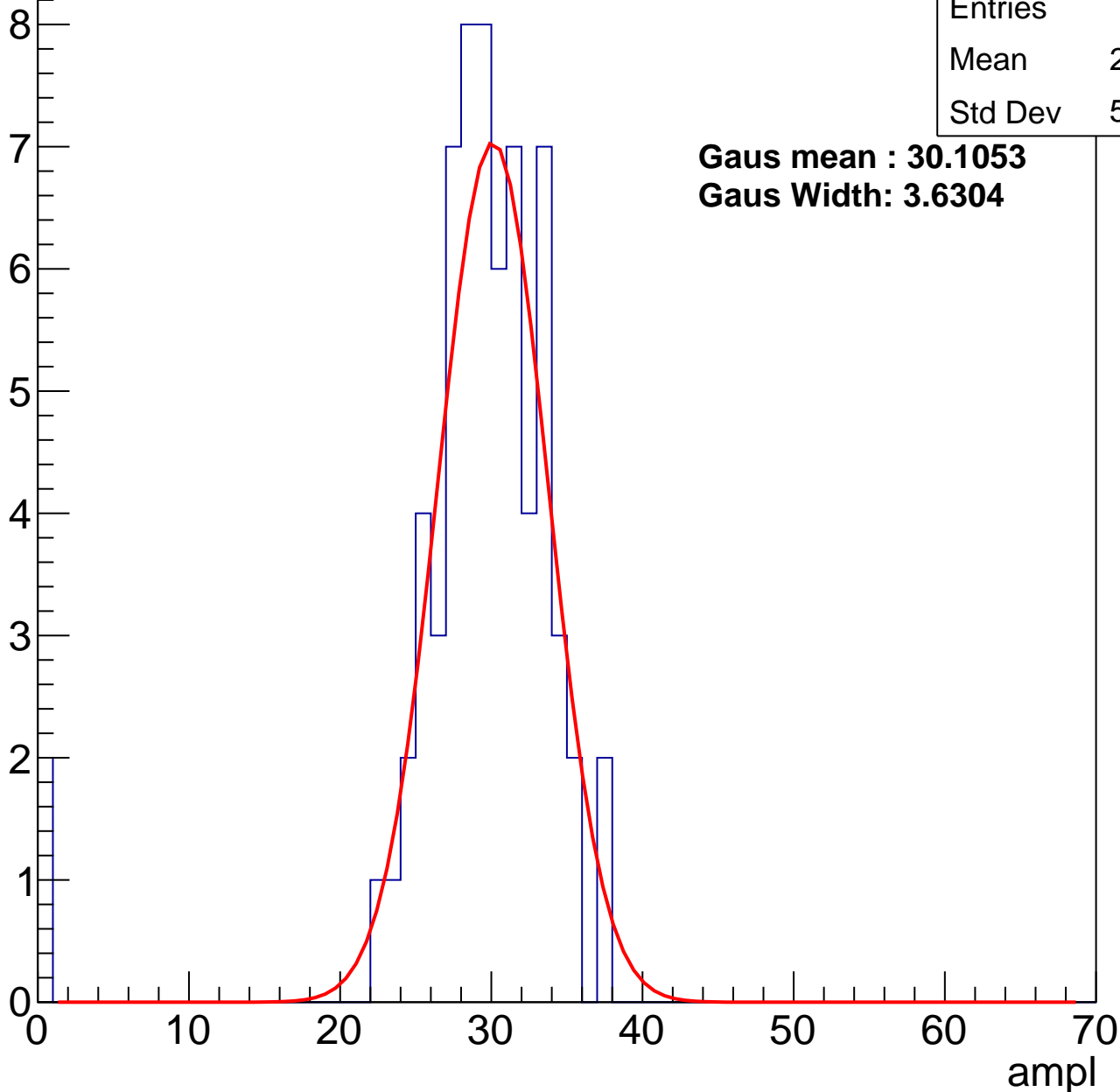
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.63
Std Dev	5.975

**Gaus mean : 30.1053**

**Gaus Width: 3.6304**



# B1L000S, U2-ch41, adc1

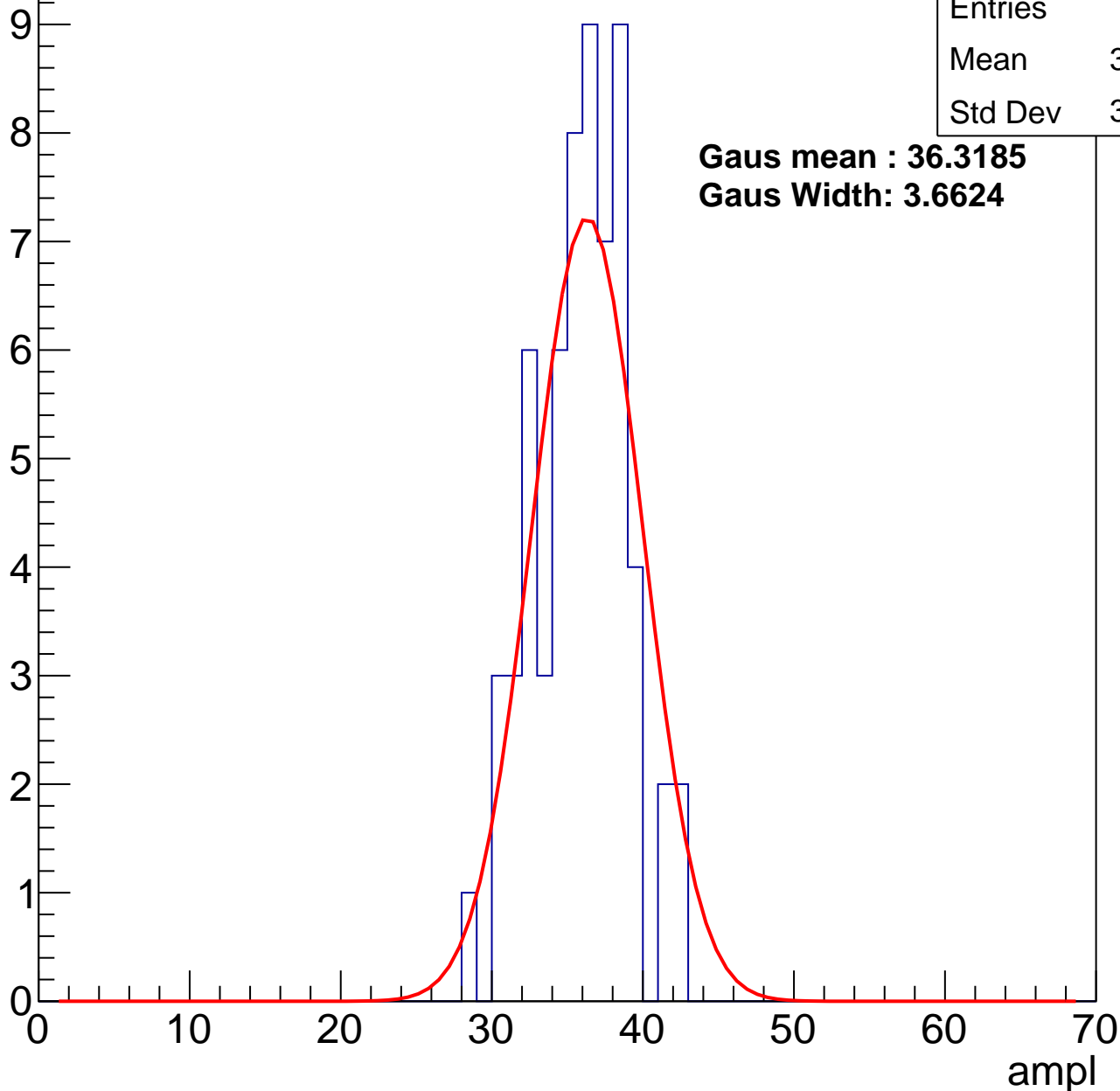
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.44
Std Dev	3.043

**Gaus mean : 36.3185**

**Gaus Width: 3.6624**



# B1L000S, U2-ch41, adc2

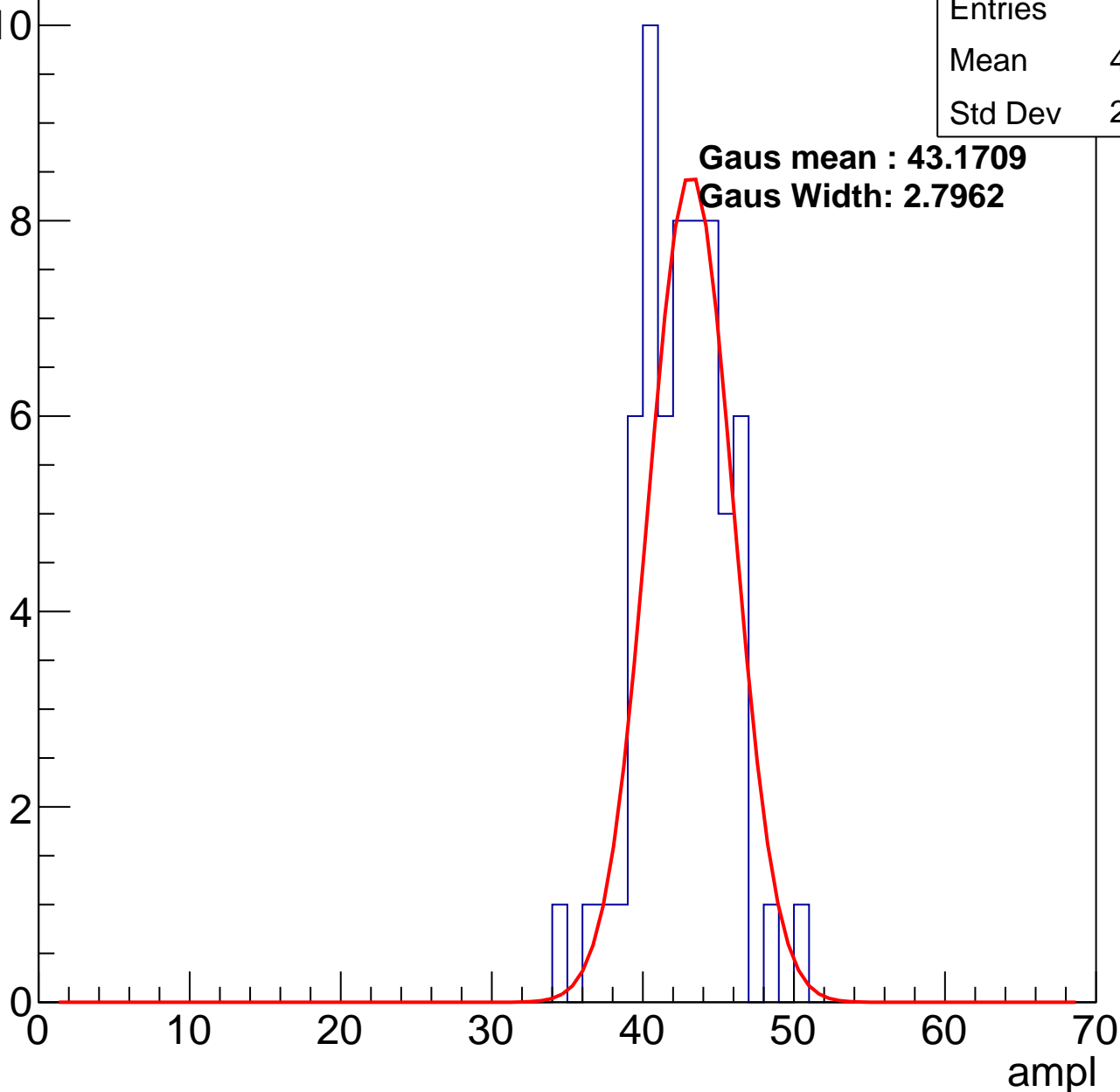
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	42.16
Std Dev	2.874

**Gaus mean : 43.1709**

**Gaus Width: 2.7962**

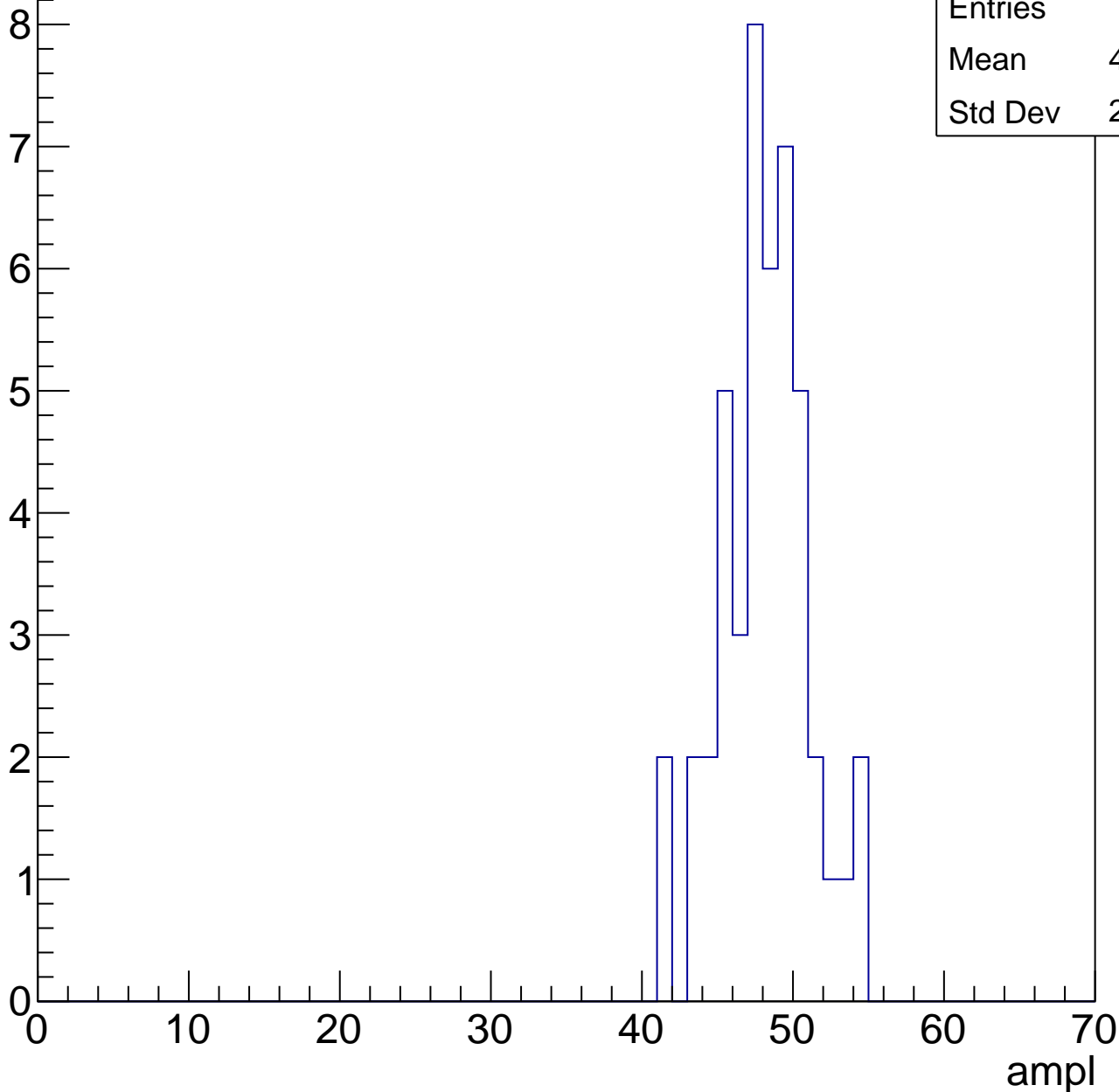


# B1L000S, U2-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	47.63
Std Dev	2.937

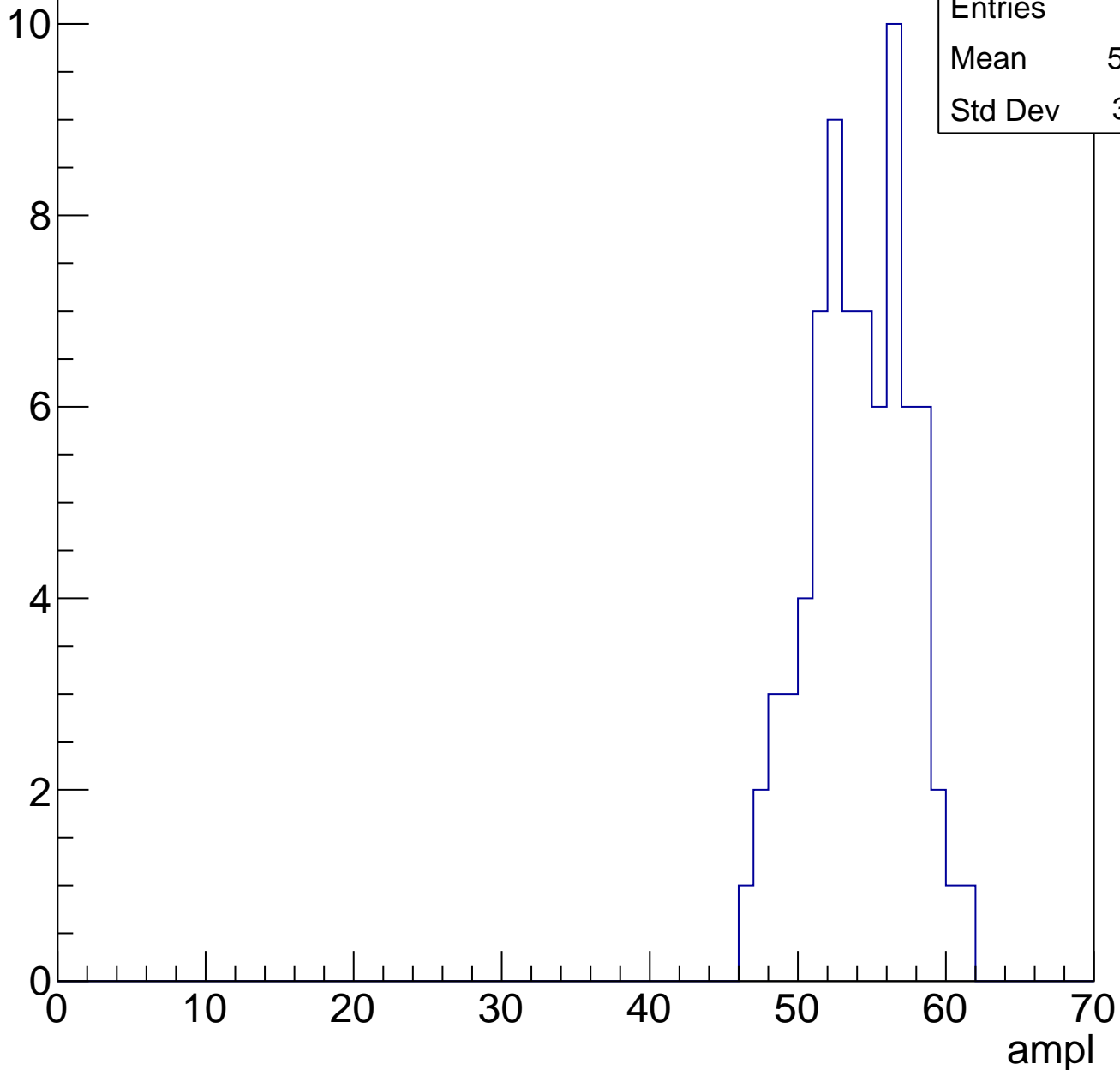


# B1L000S, U2-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	53.65
Std Dev	3.341

Entry

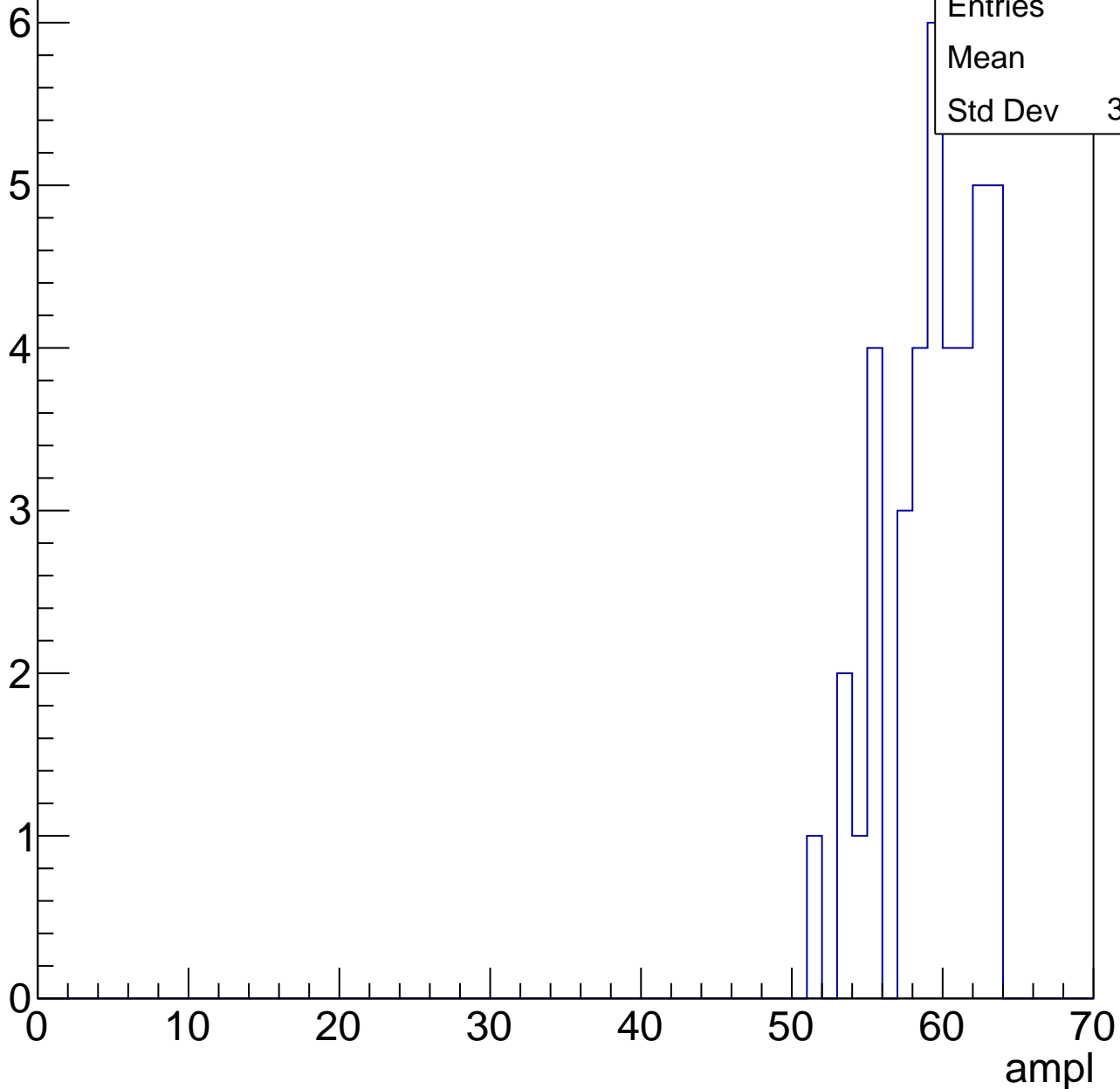


# B1L000S, U2-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	58.9
Std Dev	3.144

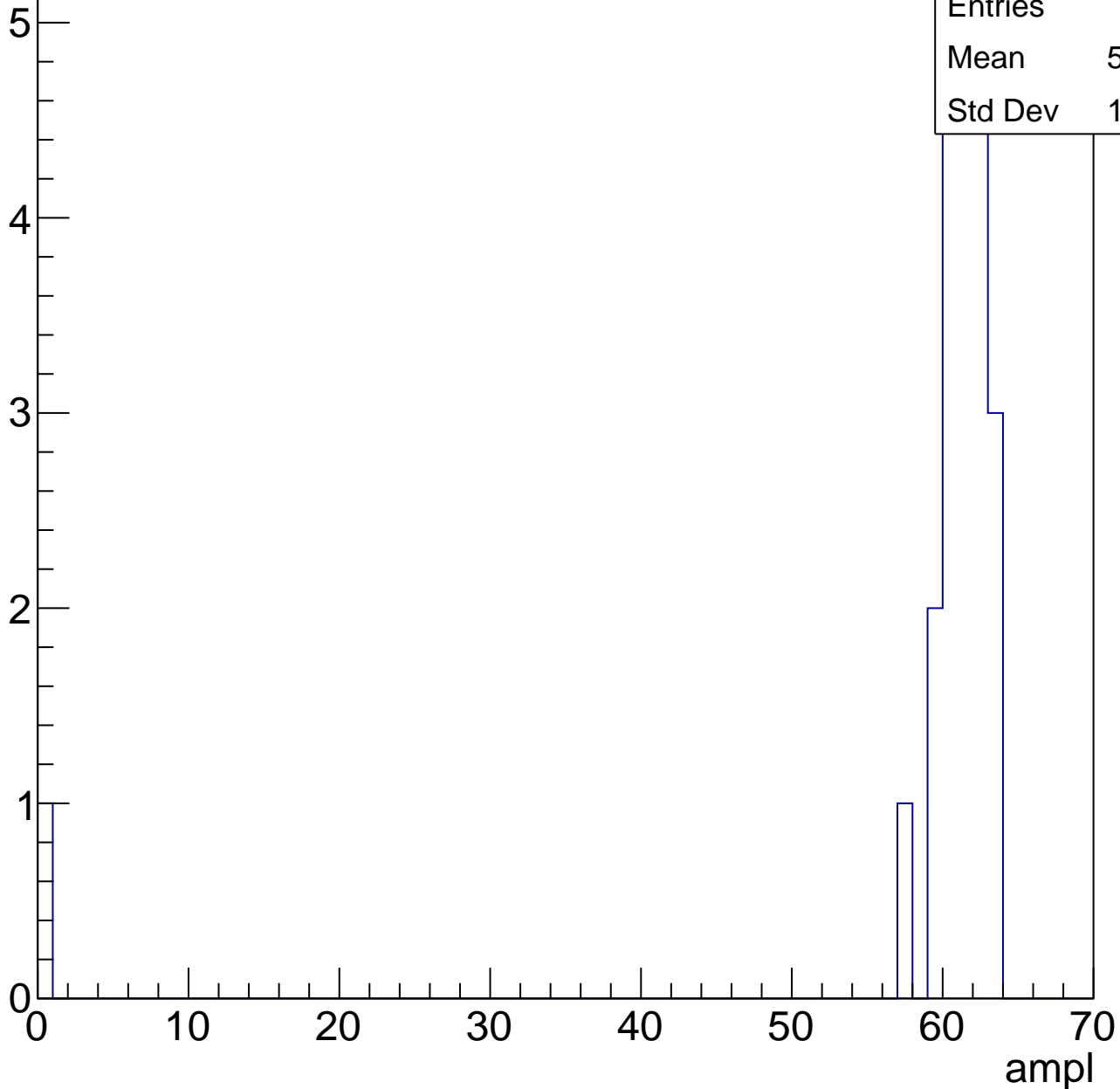


# B1L000S, U2-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.14
Std Dev	12.77

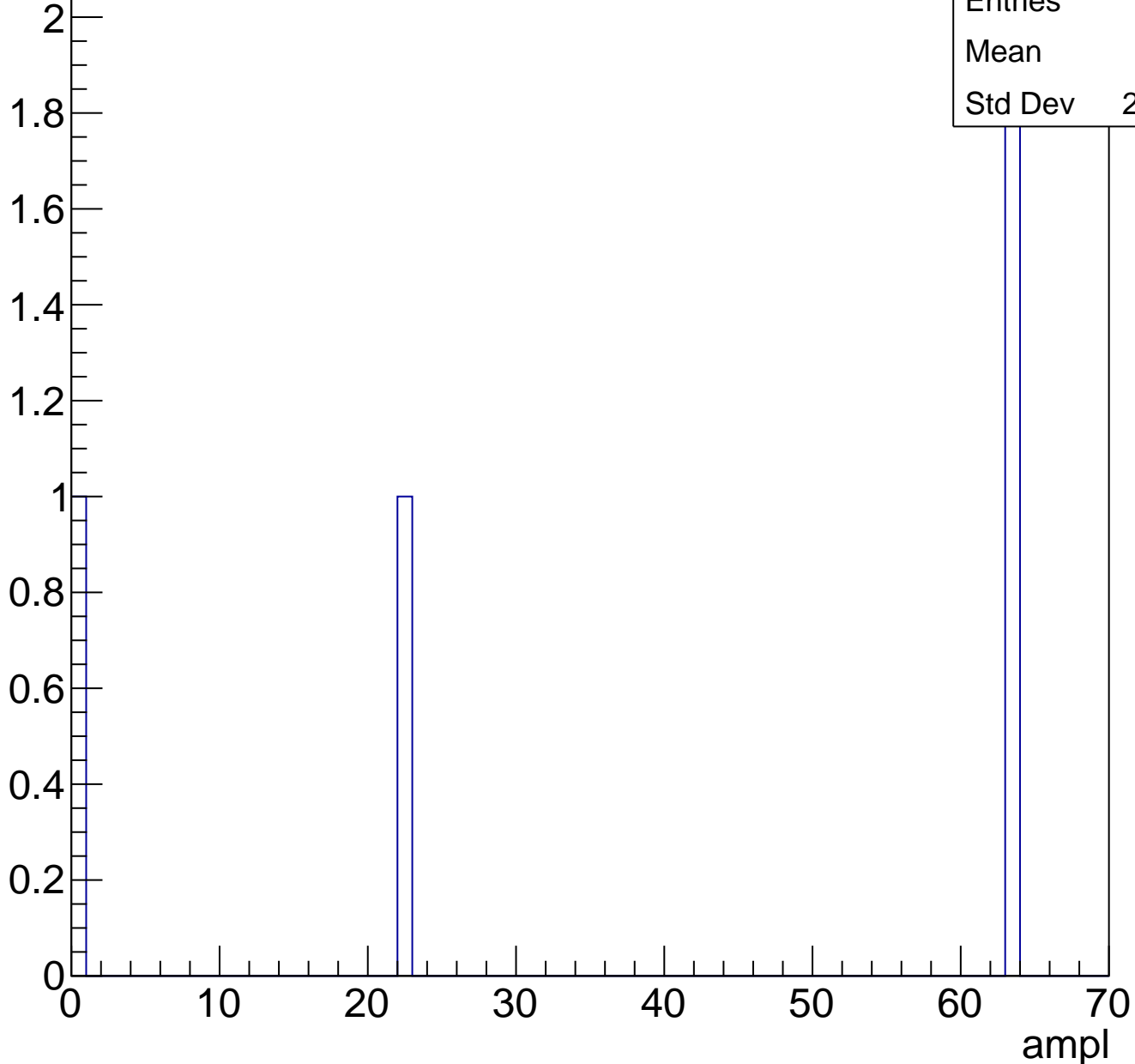




# B1L000S, U2-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	37
Std Dev	27.14

# B1L000S, U2-ch42, adc0

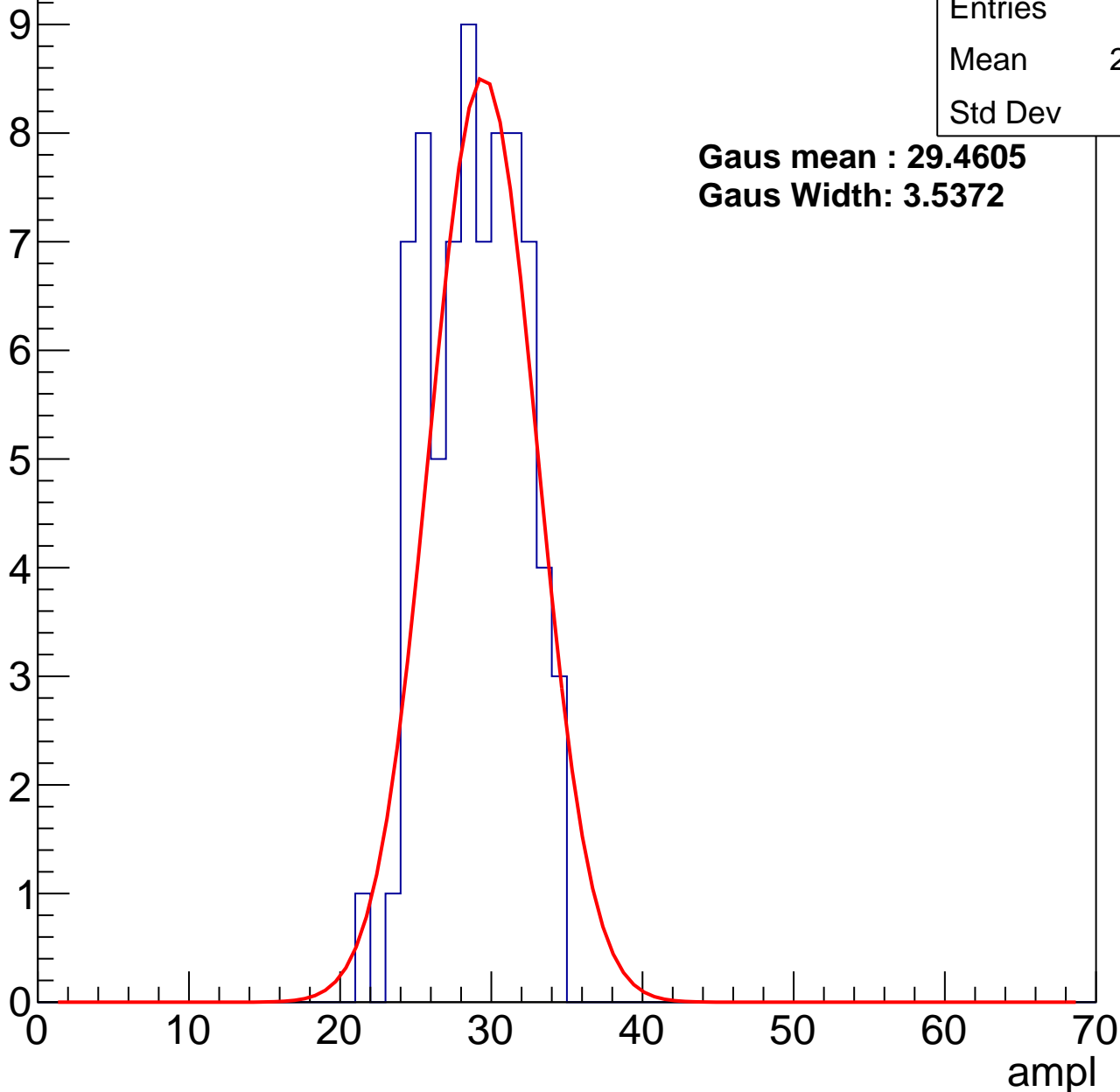
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	28.43
Std Dev	3.06

**Gaus mean : 29.4605**

**Gaus Width: 3.5372**



# B1L000S, U2-ch42, adc1

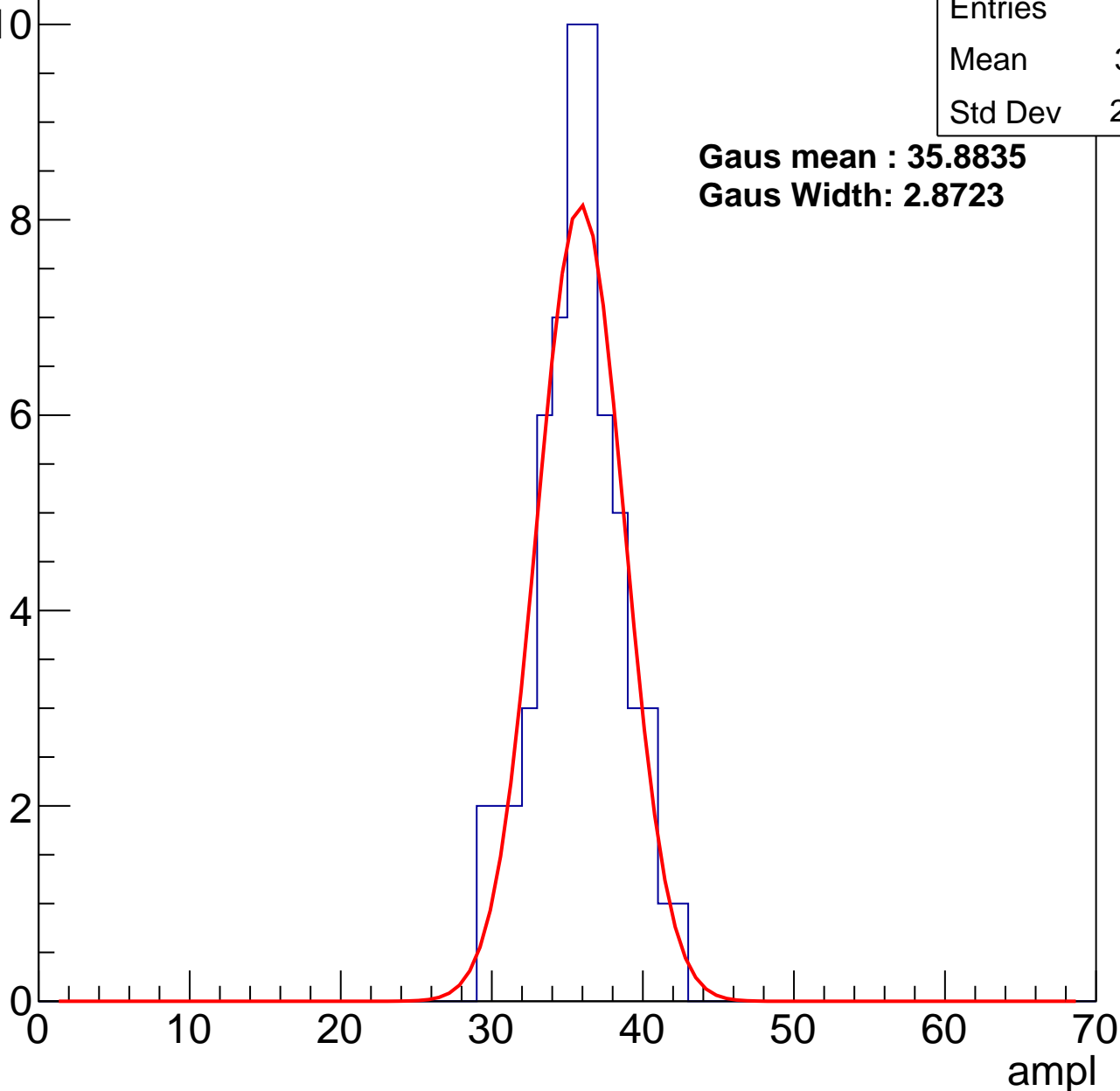
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.31
Std Dev	2.843

**Gaus mean : 35.8835**

**Gaus Width: 2.8723**



# B1L000S, U2-ch42, adc2

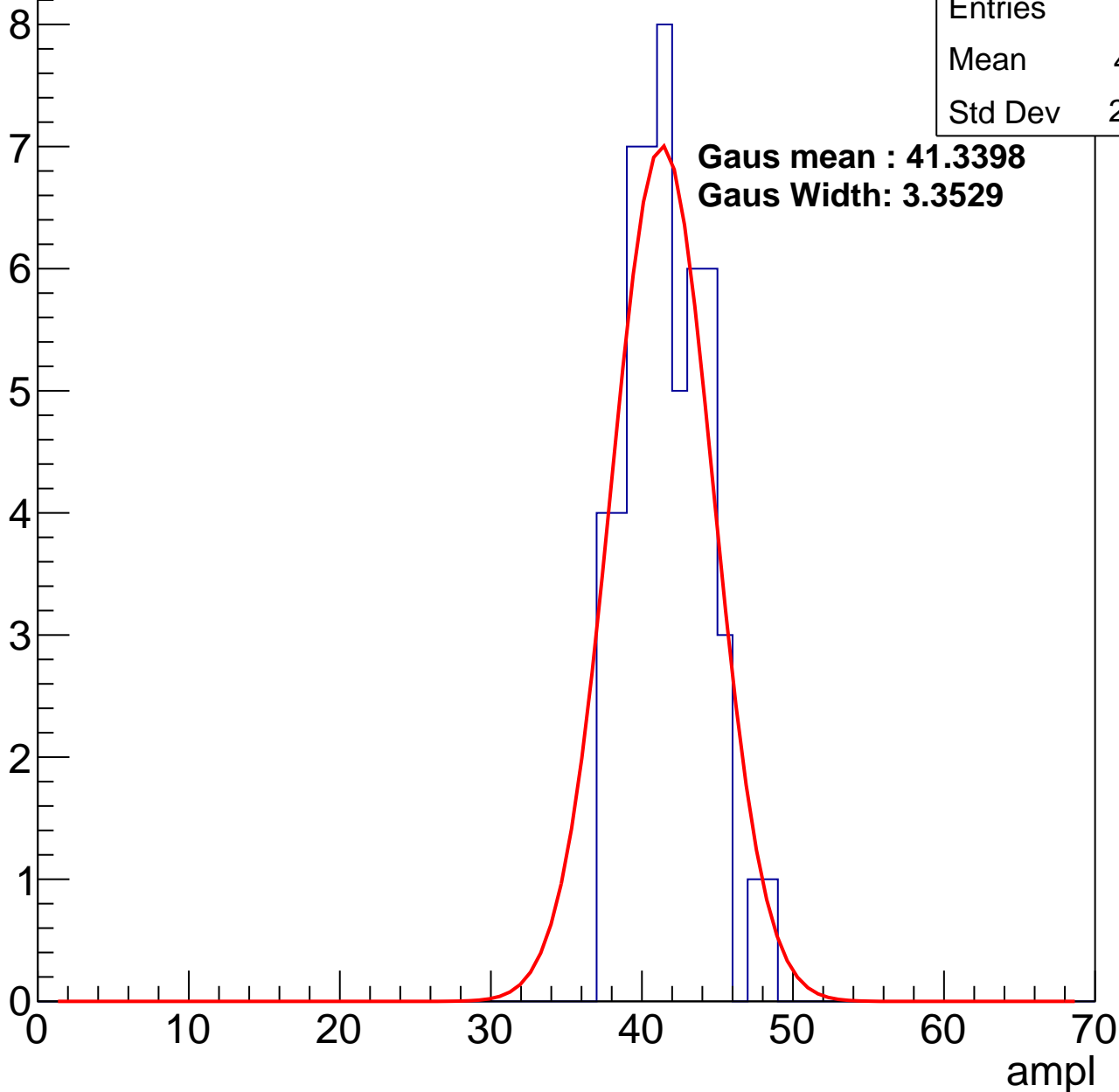
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	41.21
Std Dev	2.589

**Gaus mean : 41.3398**

**Gaus Width: 3.3529**

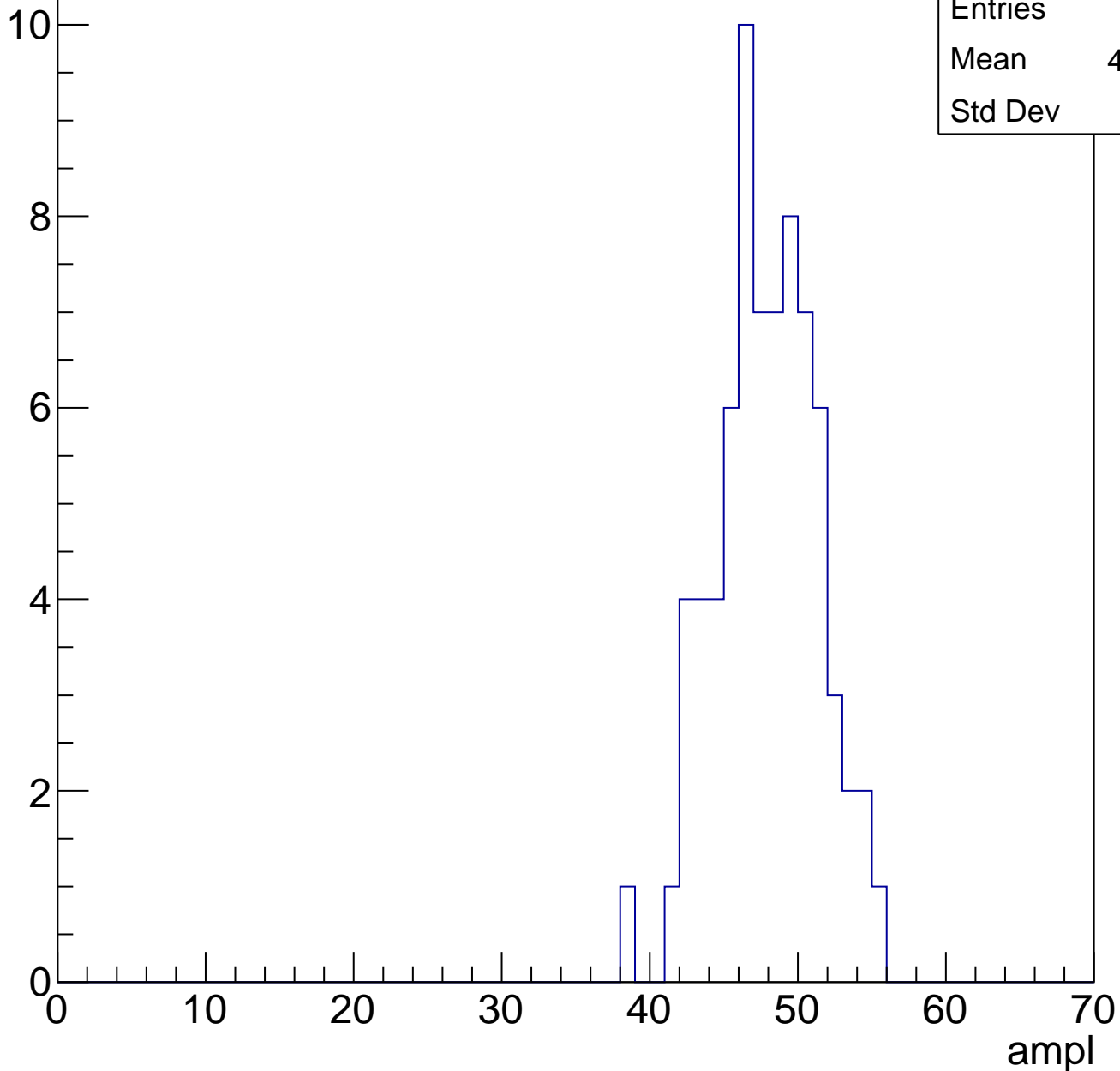


# B1L000S, U2-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

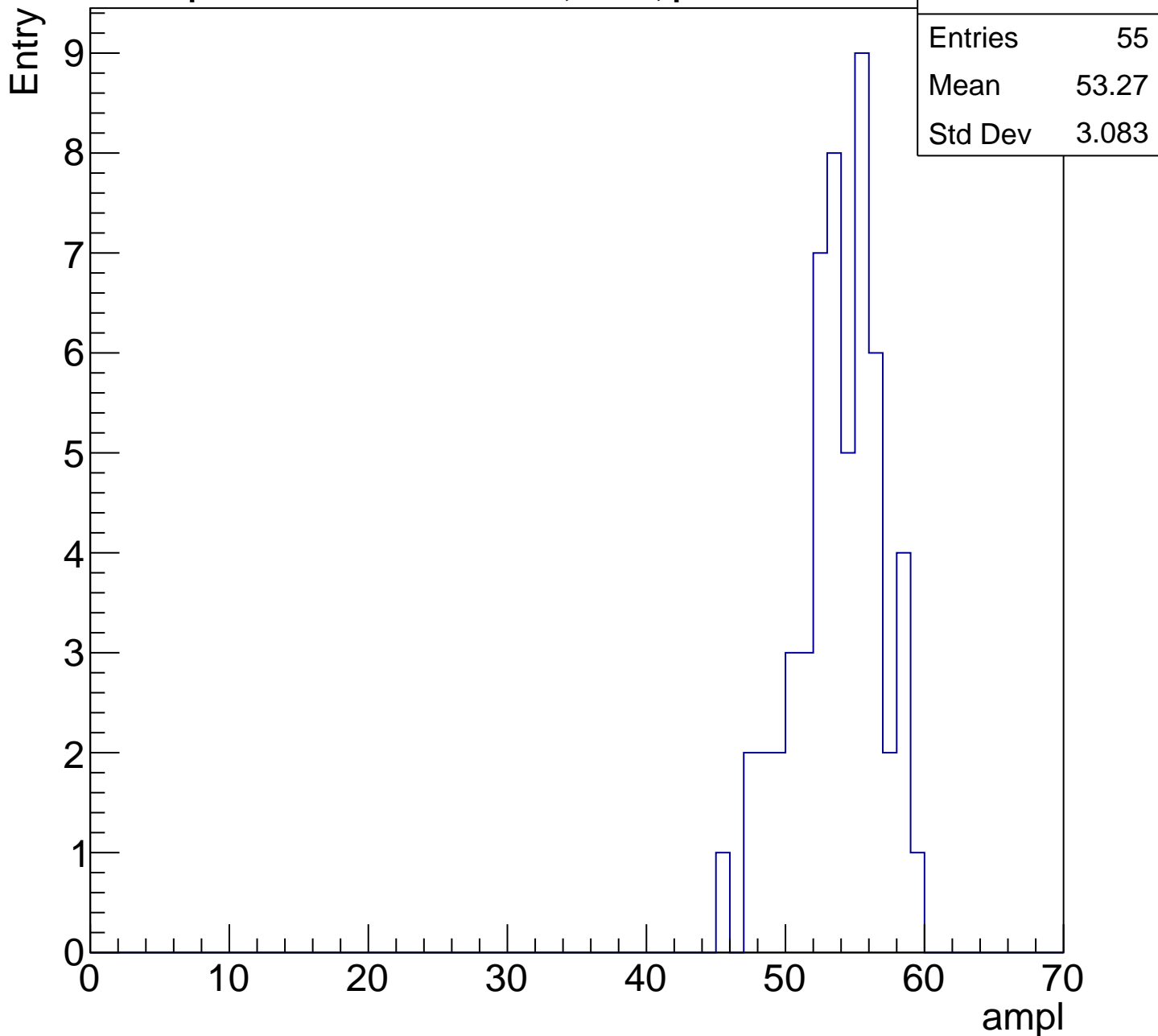
Entries	73
Mean	47.44
Std Dev	3.4

Entry



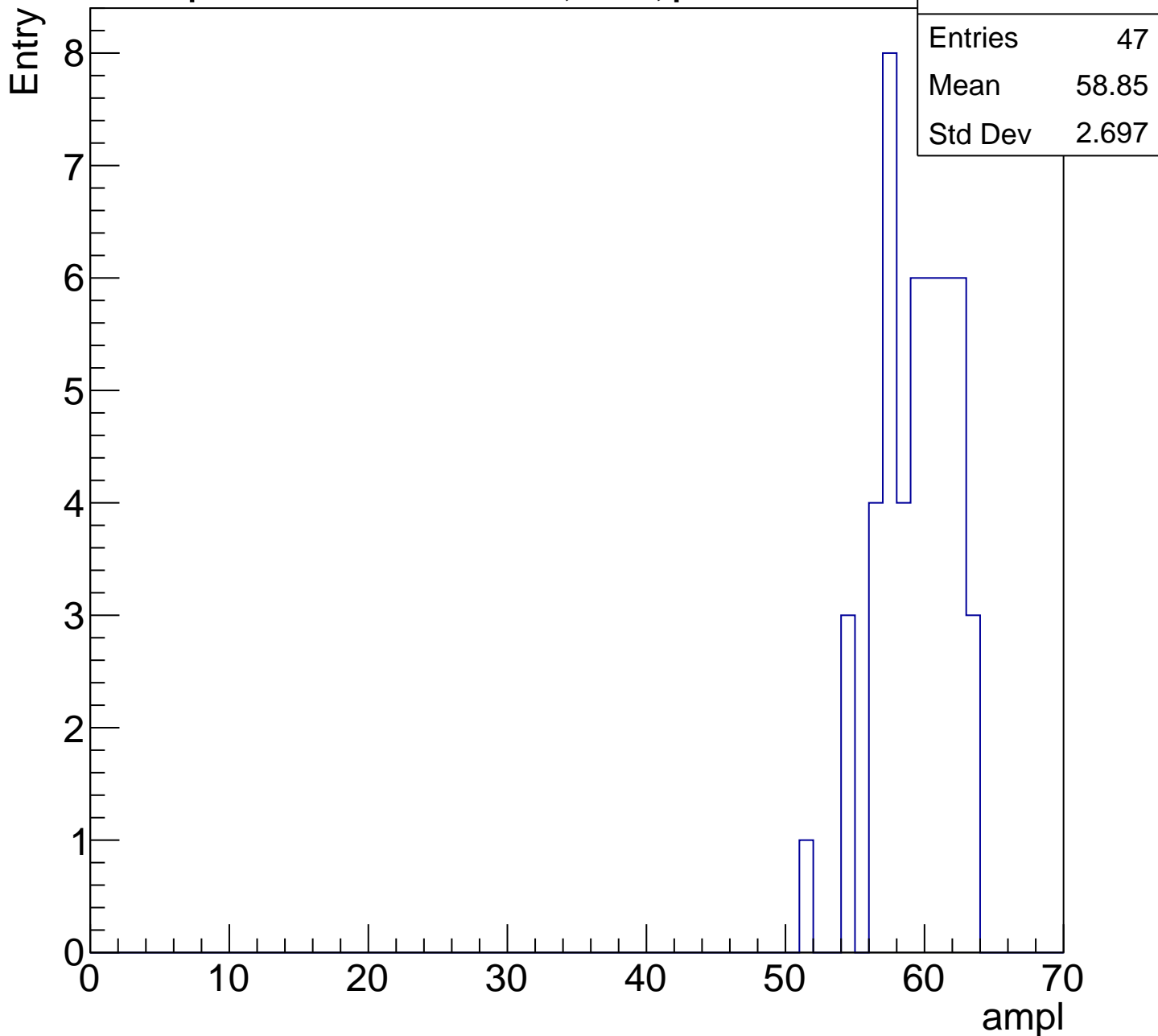
# B1L000S, U2-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

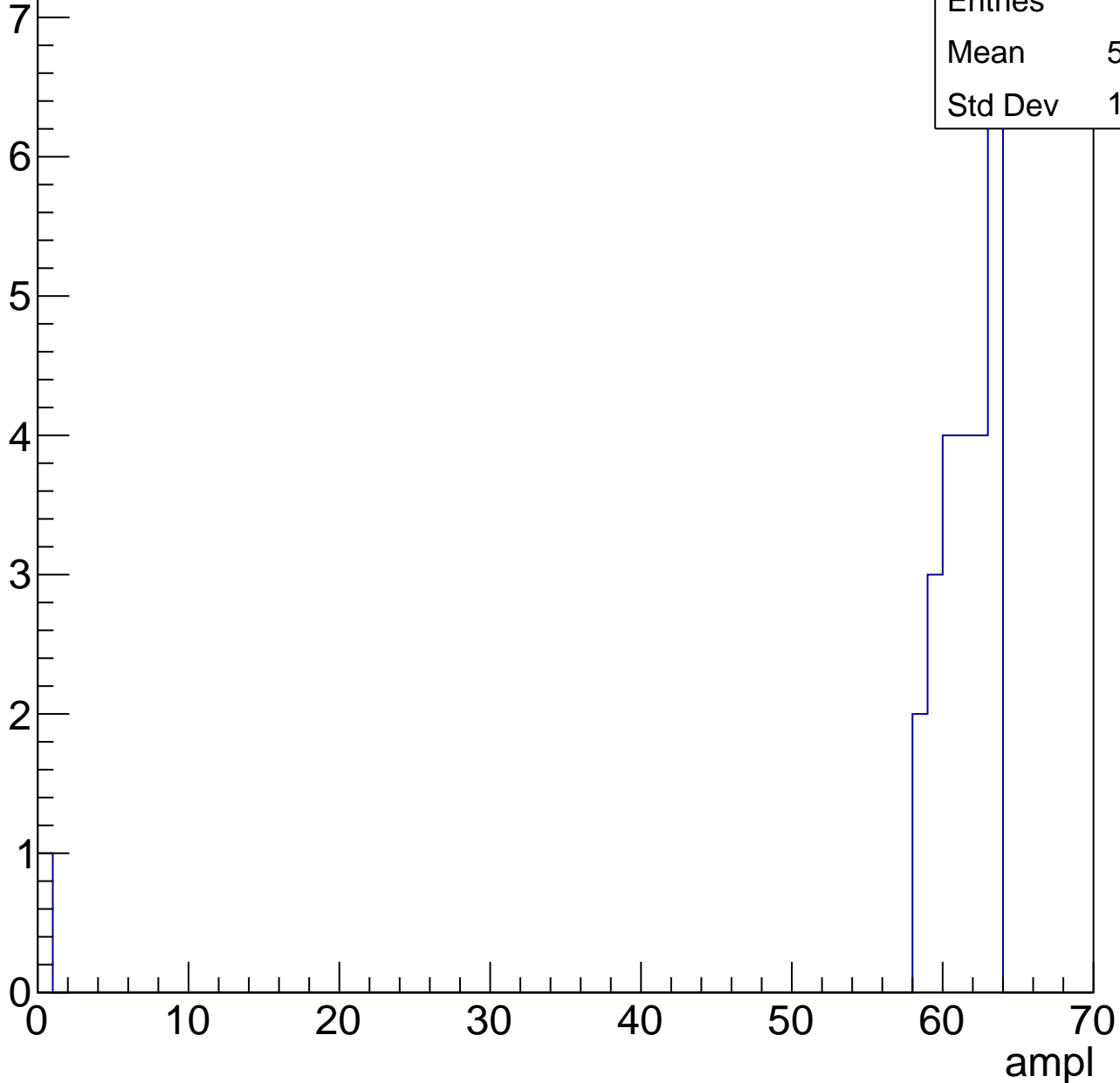


# B1L000S, U2-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.64
Std Dev	12.08





# B1L000S, U2-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch43, adc0

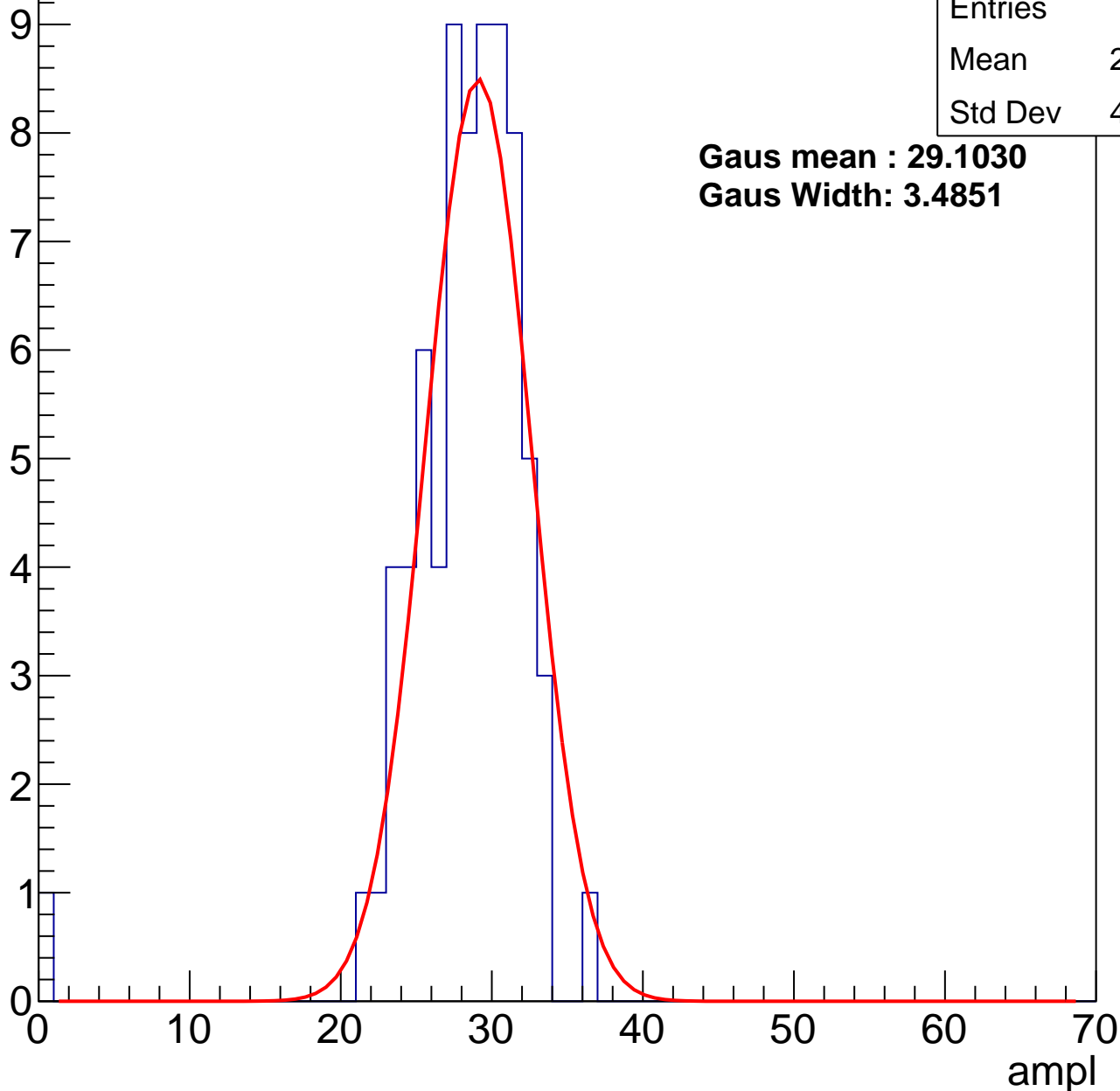
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	27.75
Std Dev	4.447

**Gaus mean : 29.1030**

**Gaus Width: 3.4851**



# B1L000S, U2-ch43, adc1

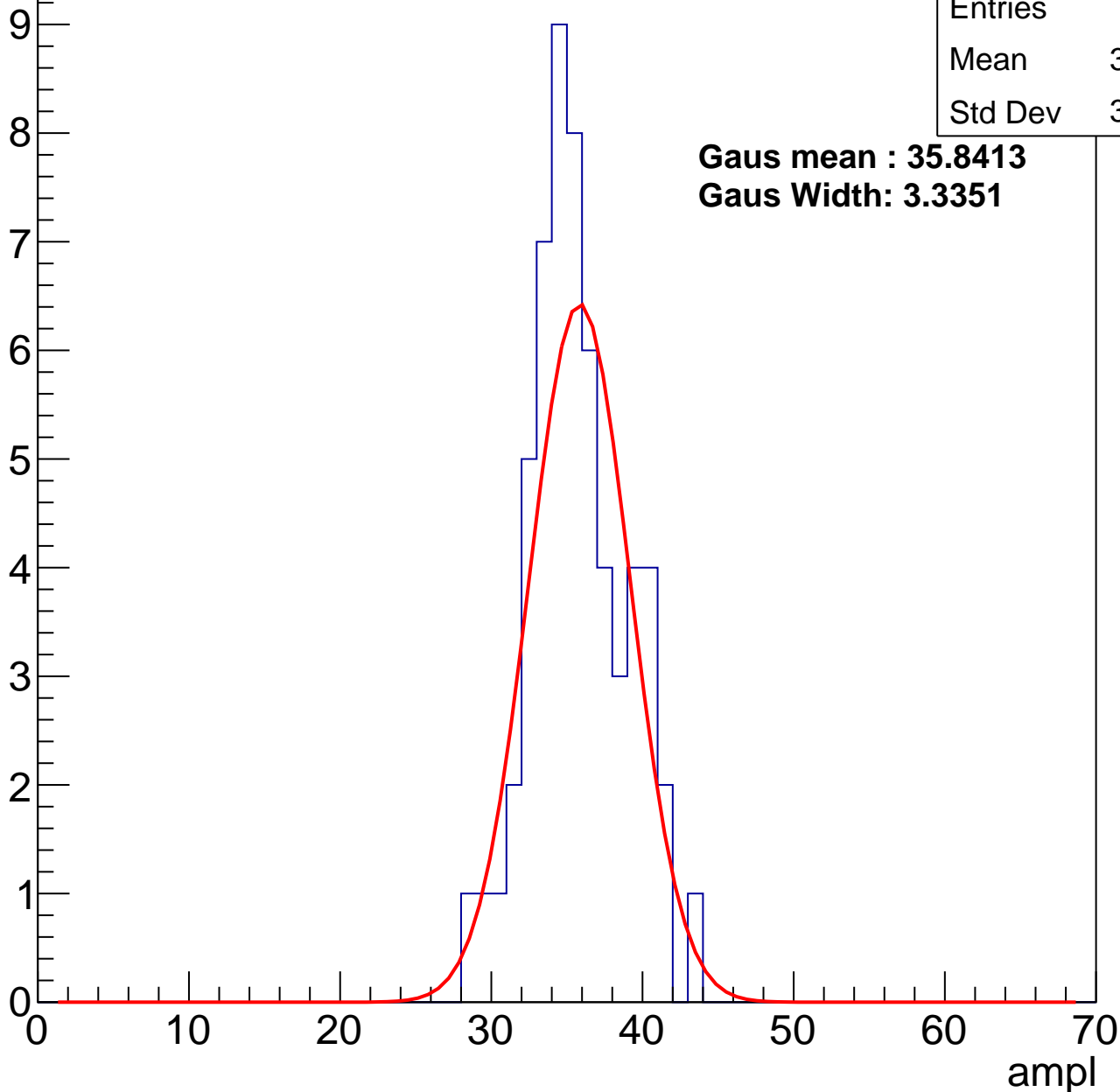
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.26
Std Dev	3.132

**Gaus mean : 35.8413**

**Gaus Width: 3.3351**



# B1L000S, U2-ch43, adc2

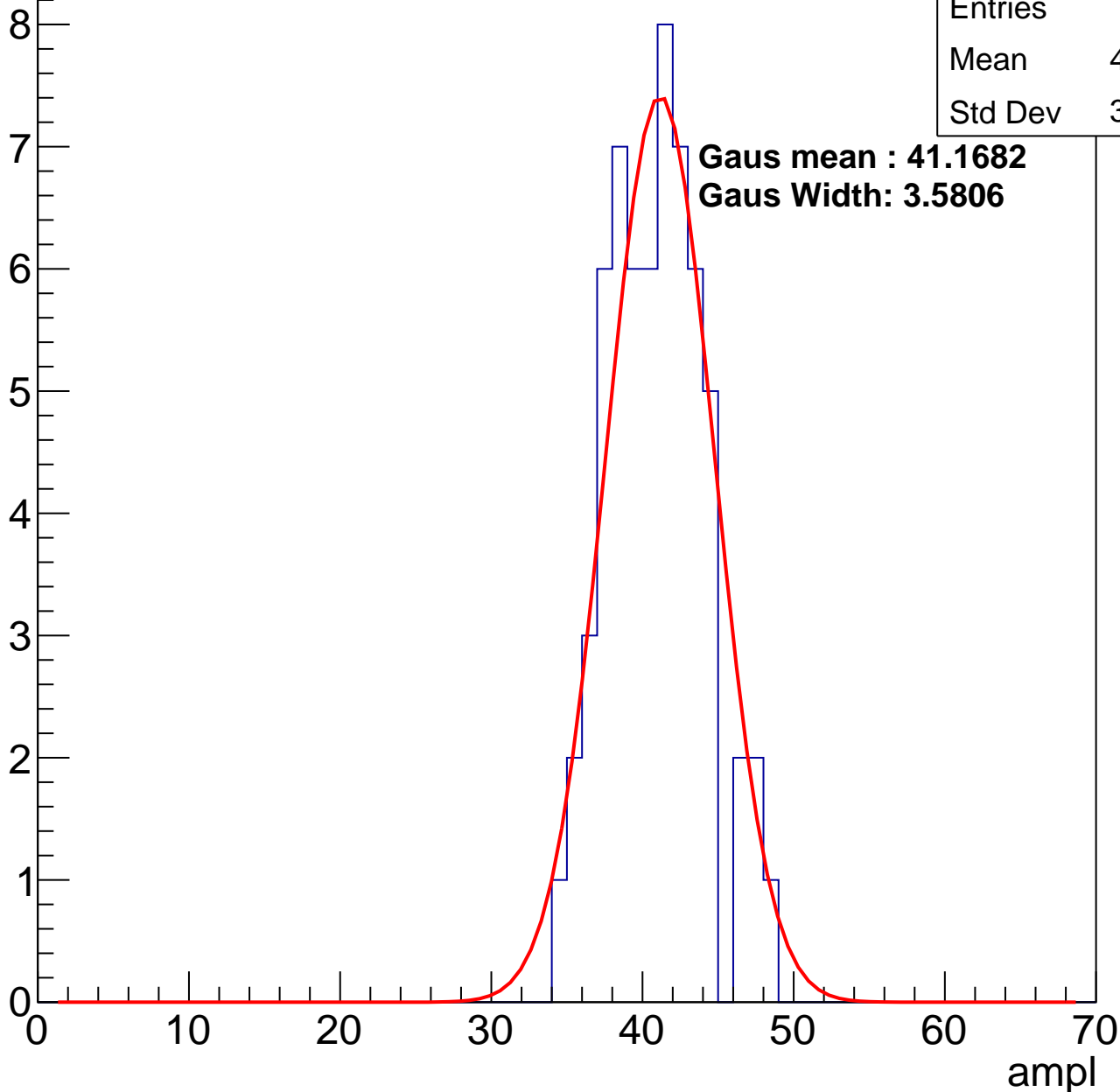
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.45
Std Dev	3.145

**Gaus mean : 41.1682**

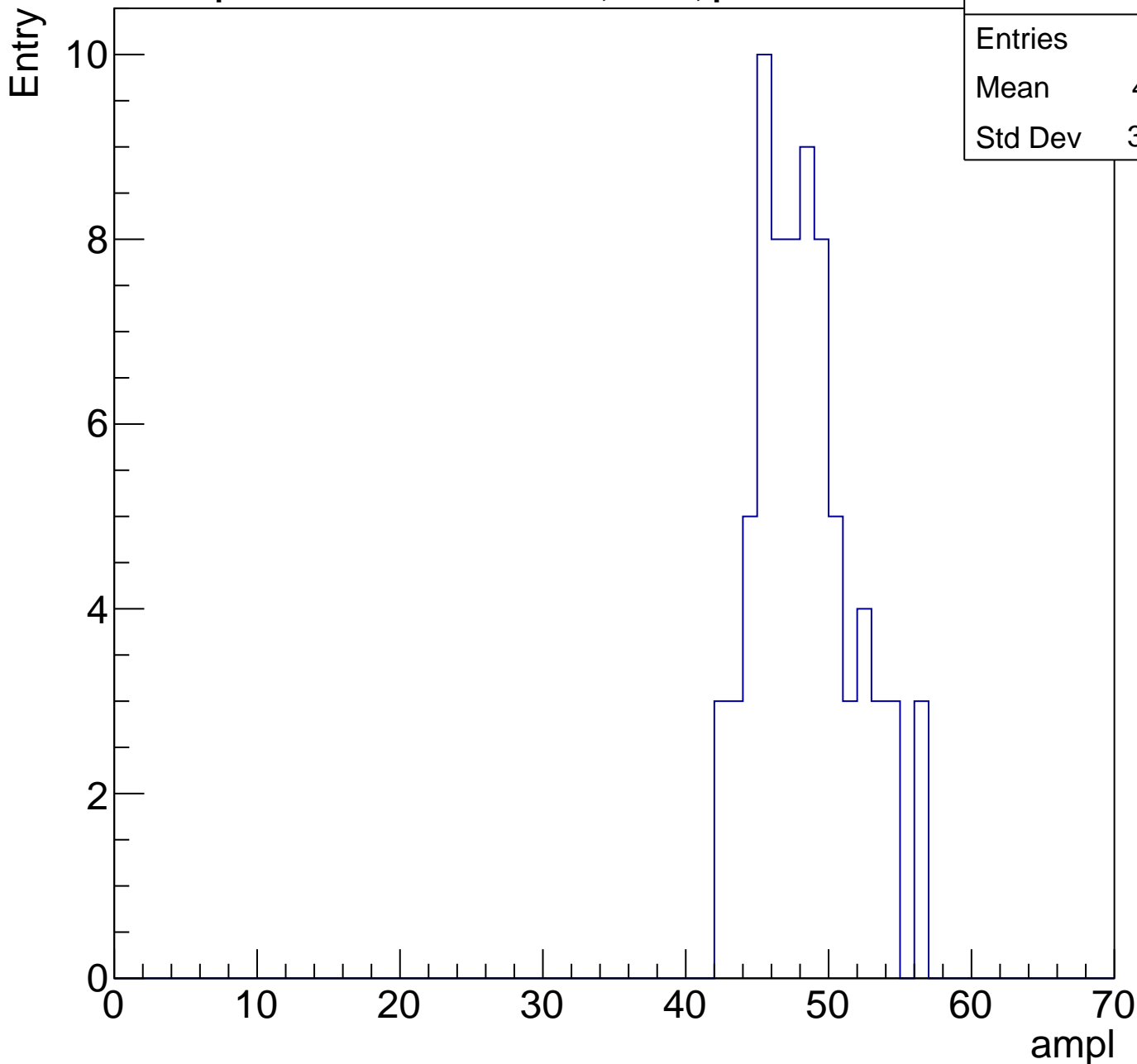
**Gaus Width: 3.5806**



# B1L000S, U2-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	47.91
Std Dev	3.438

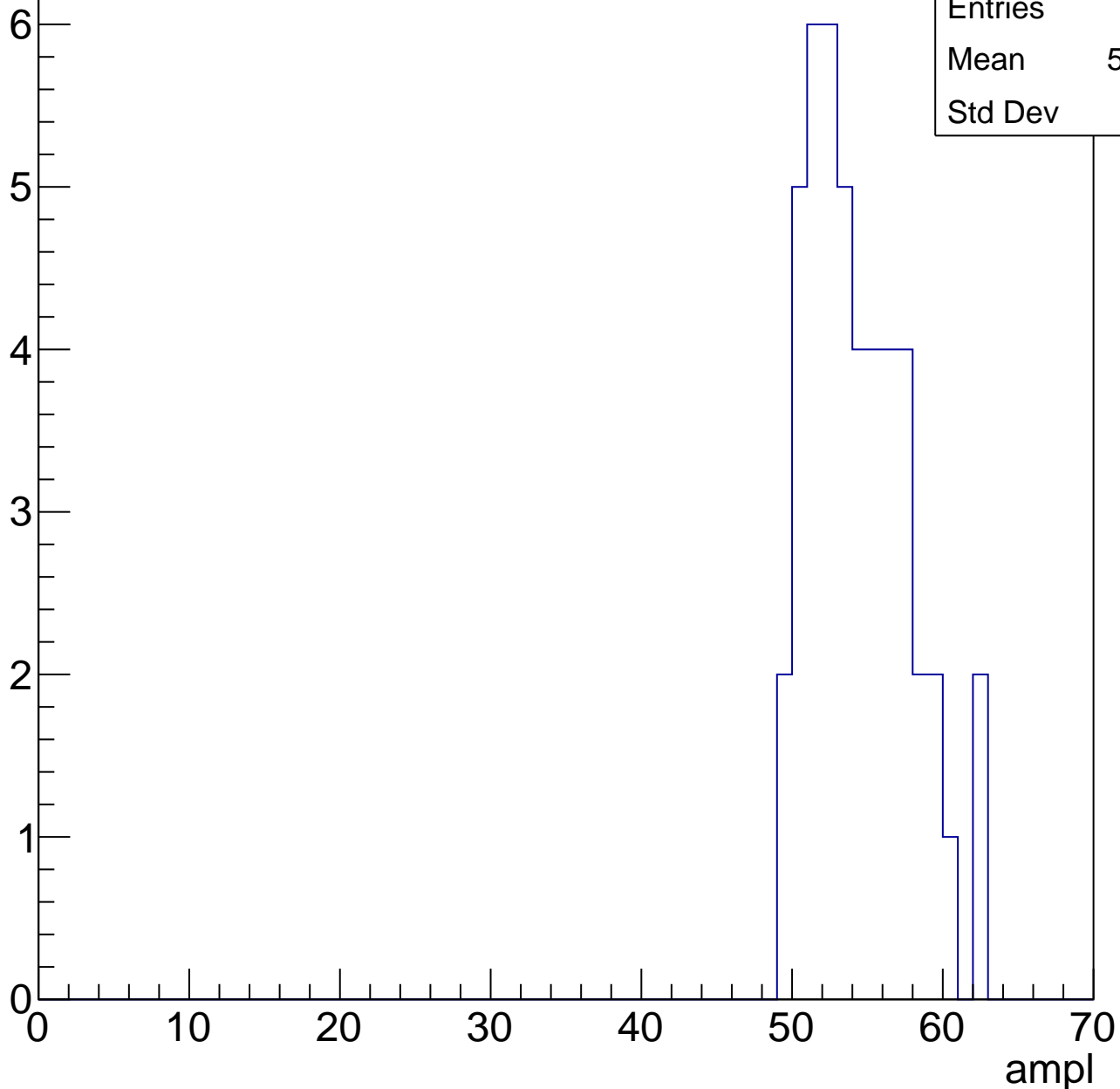


# B1L000S, U2-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

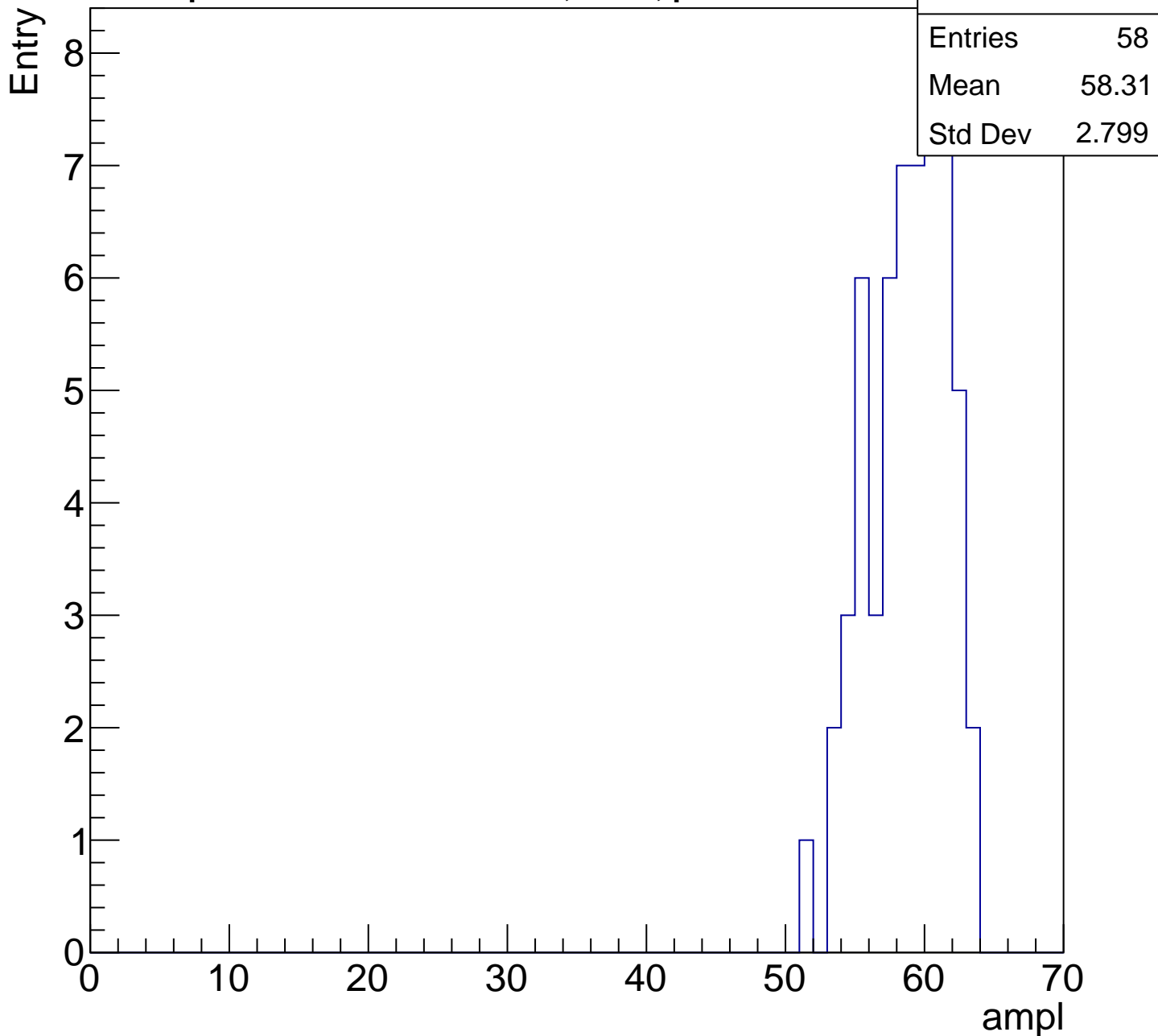
Entry

Entries	47
Mean	53.98
Std Dev	3.31



# B1L000S, U2-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

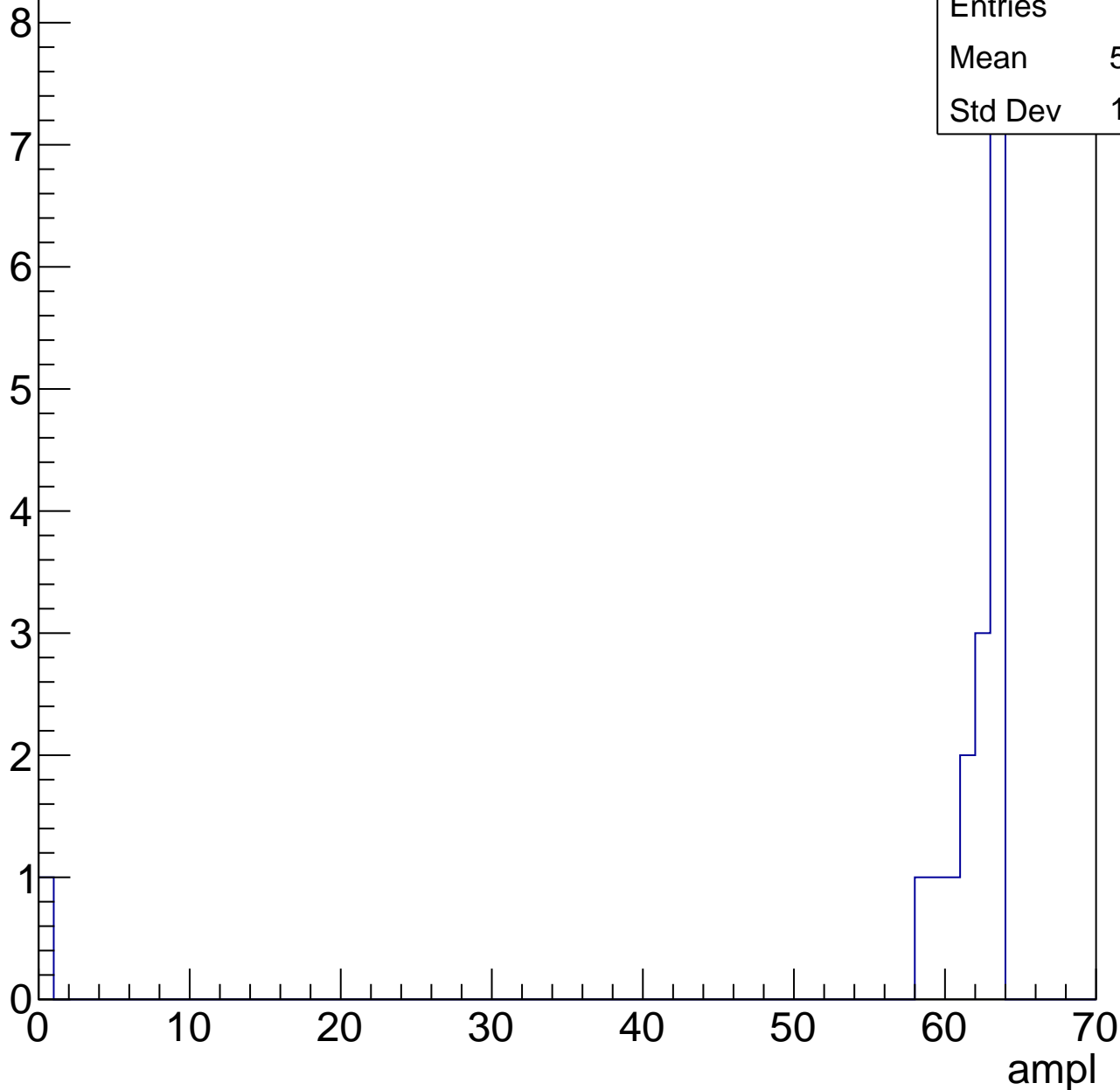


# B1L000S, U2-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.18
Std Dev	14.62





# B1L000S, U2-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U2-ch44, adc0

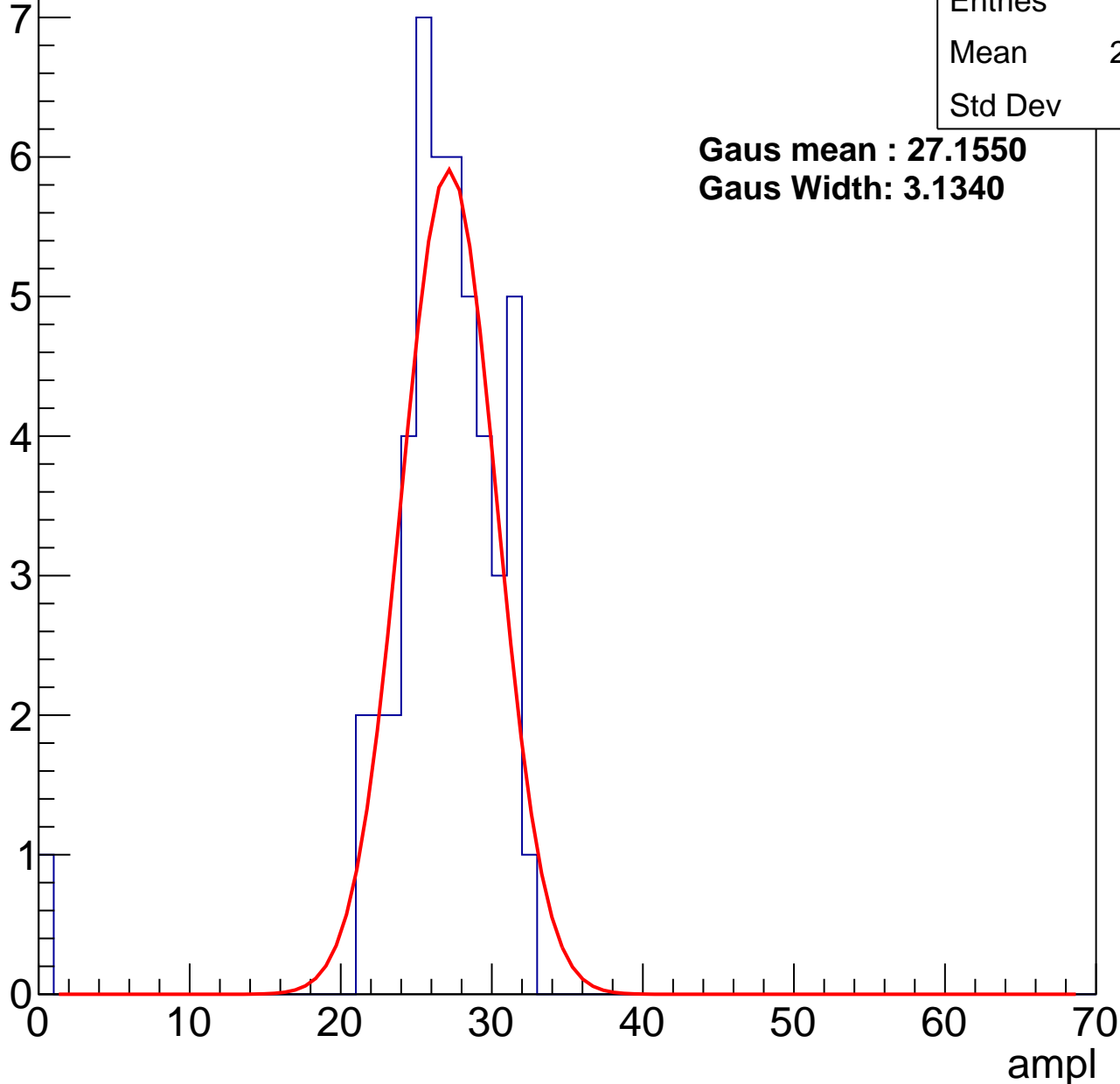
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	26.12
Std Dev	4.72

**Gaus mean : 27.1550**

**Gaus Width: 3.1340**



# B1L000S, U2-ch44, adc1

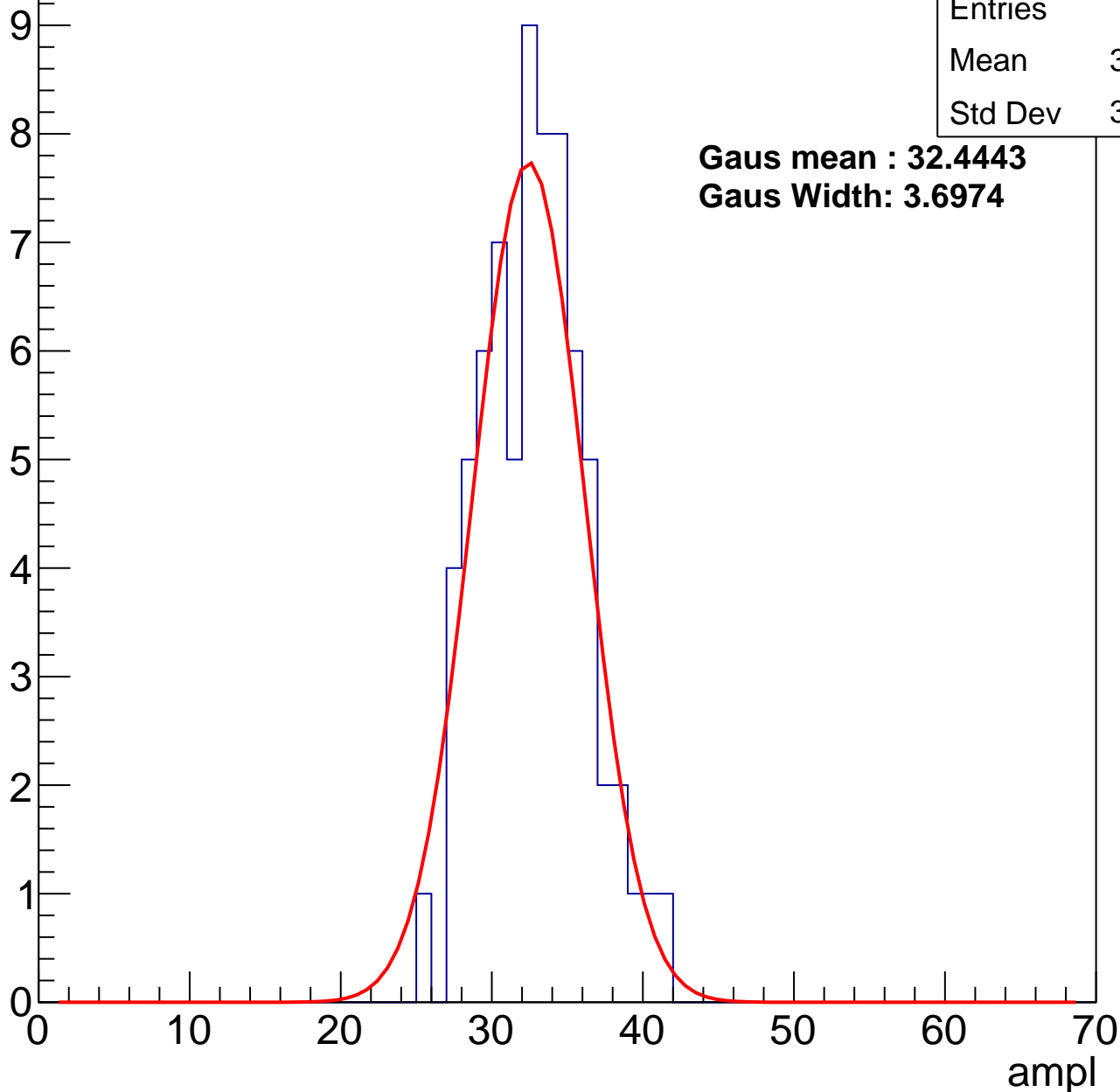
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	32.34
Std Dev	3.352

**Gaus mean : 32.4443**

**Gaus Width: 3.6974**



# B1L000S, U2-ch44, adc2

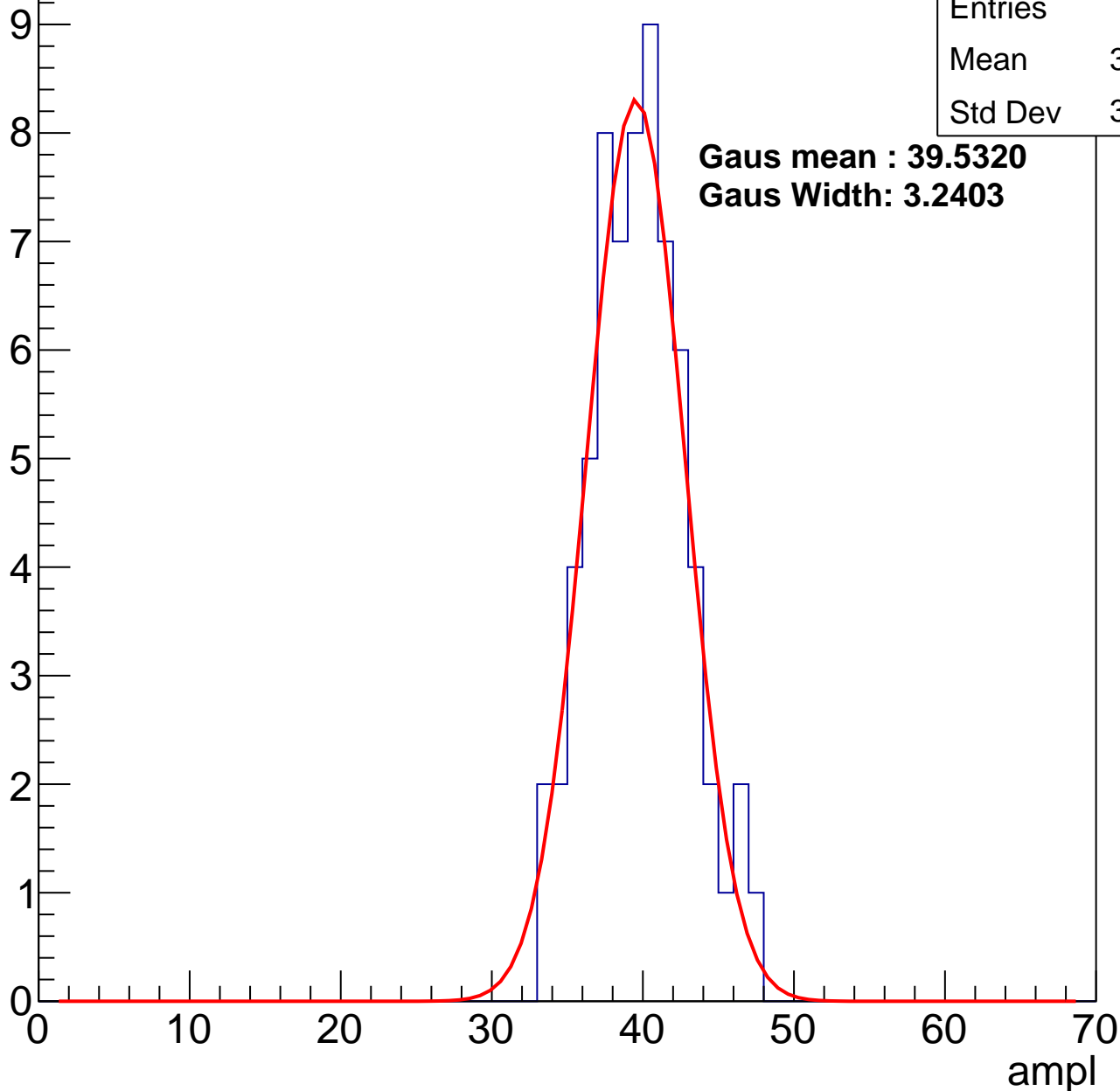
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	39.28
Std Dev	3.134

**Gaus mean : 39.5320**

**Gaus Width: 3.2403**

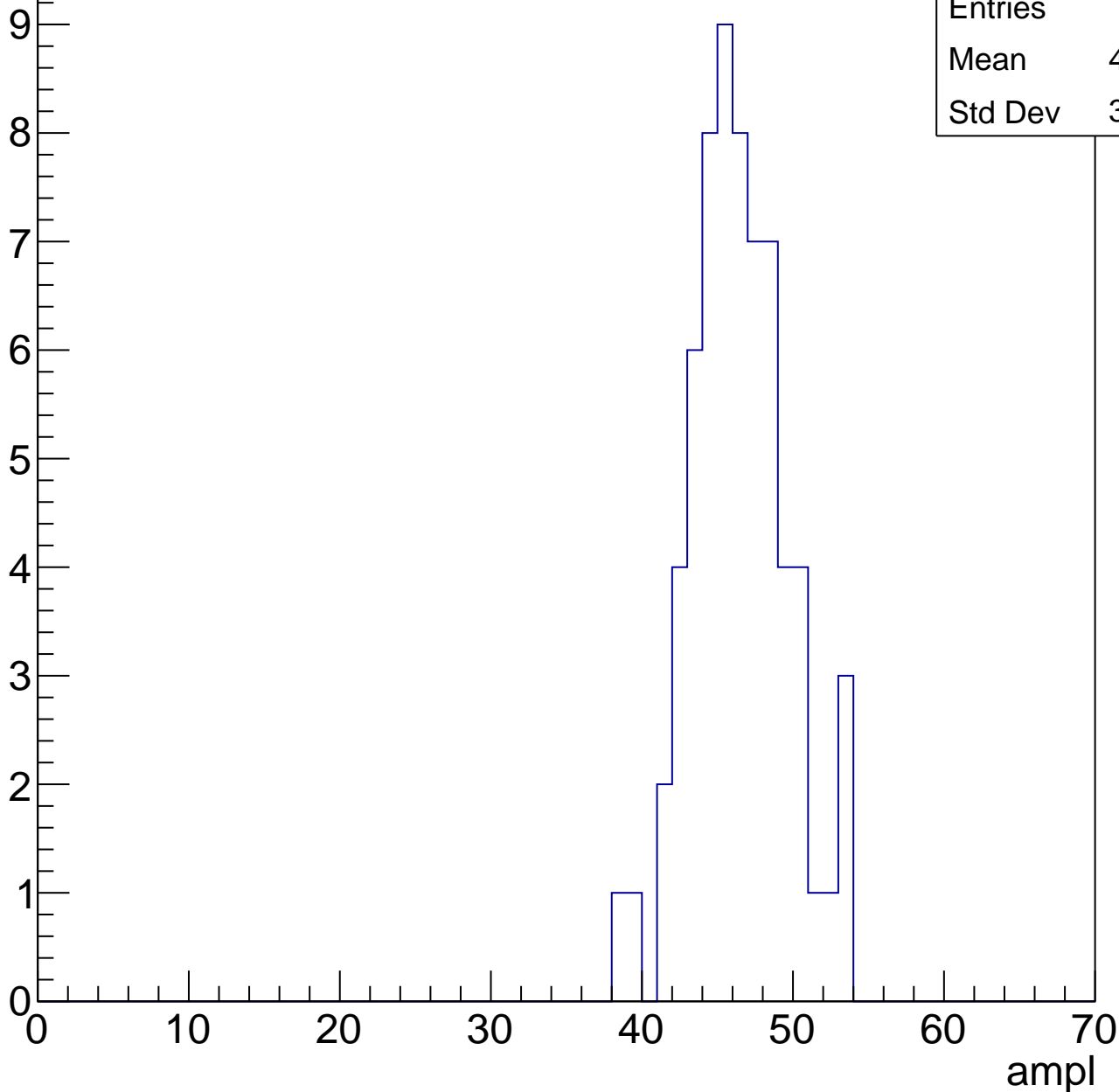


# B1L000S, U2-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	45.95
Std Dev	3.174

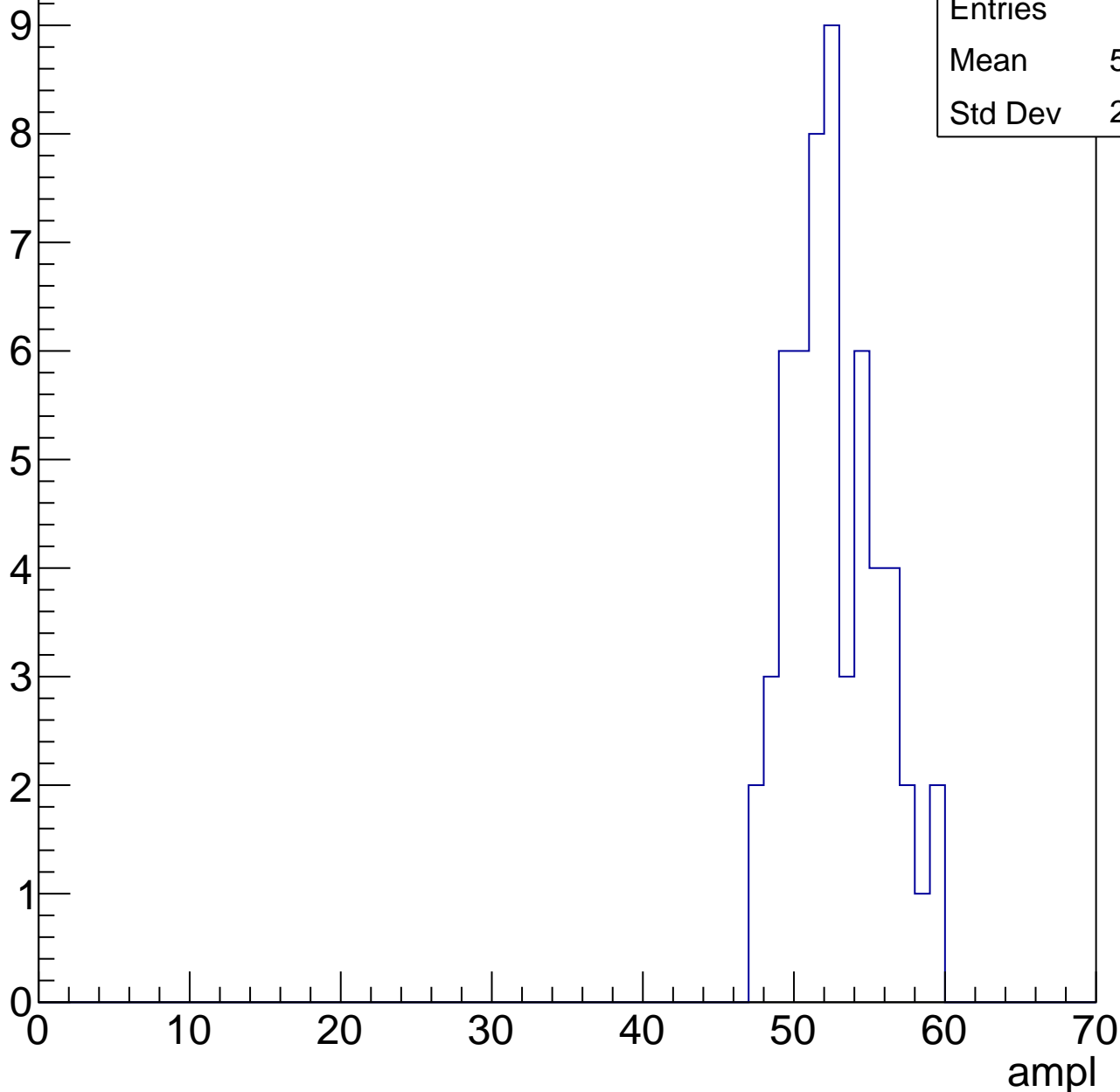


# B1L000S, U2-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	52.23
Std Dev	2.964

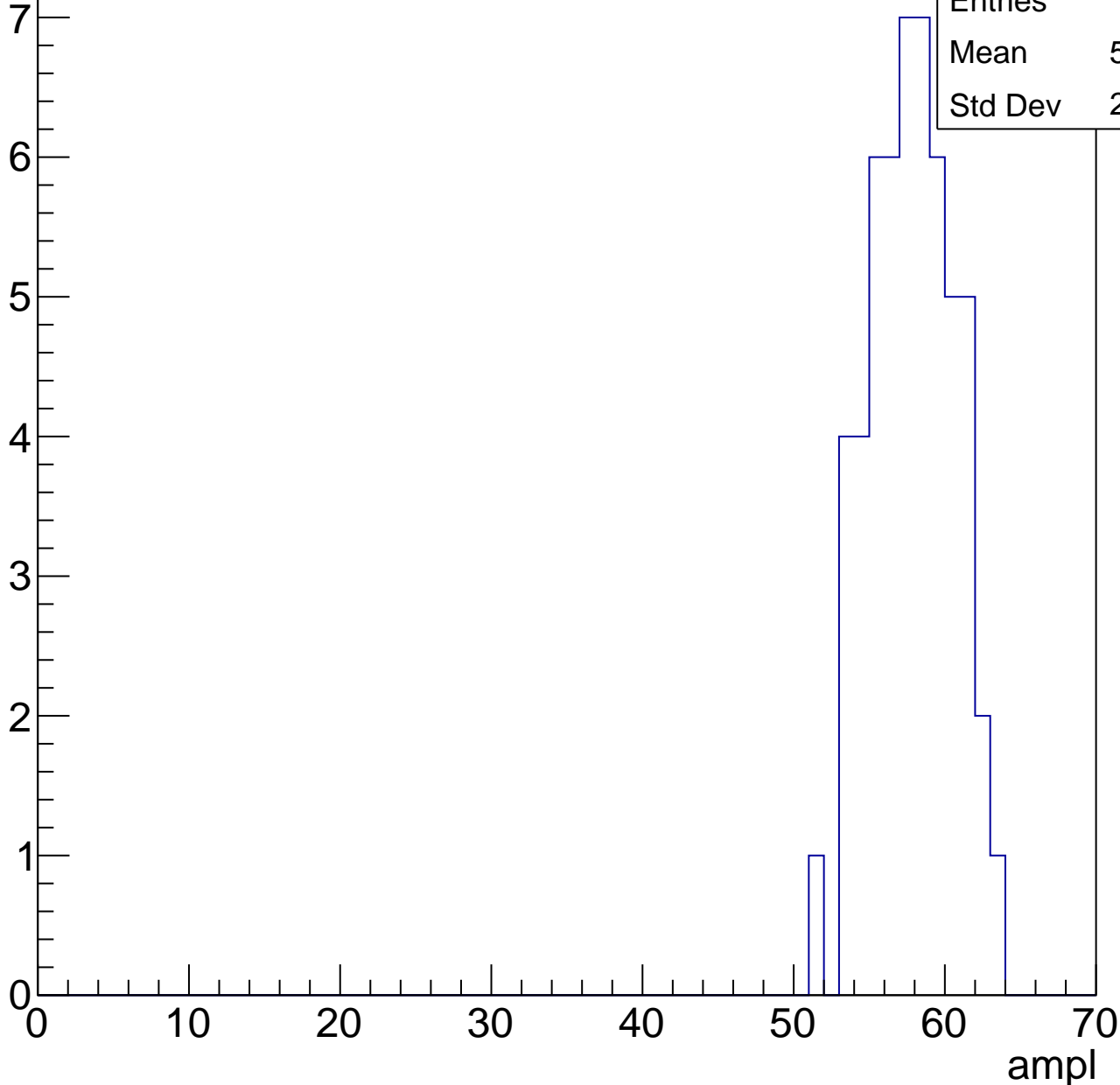


# B1L000S, U2-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	57.33
Std Dev	2.728

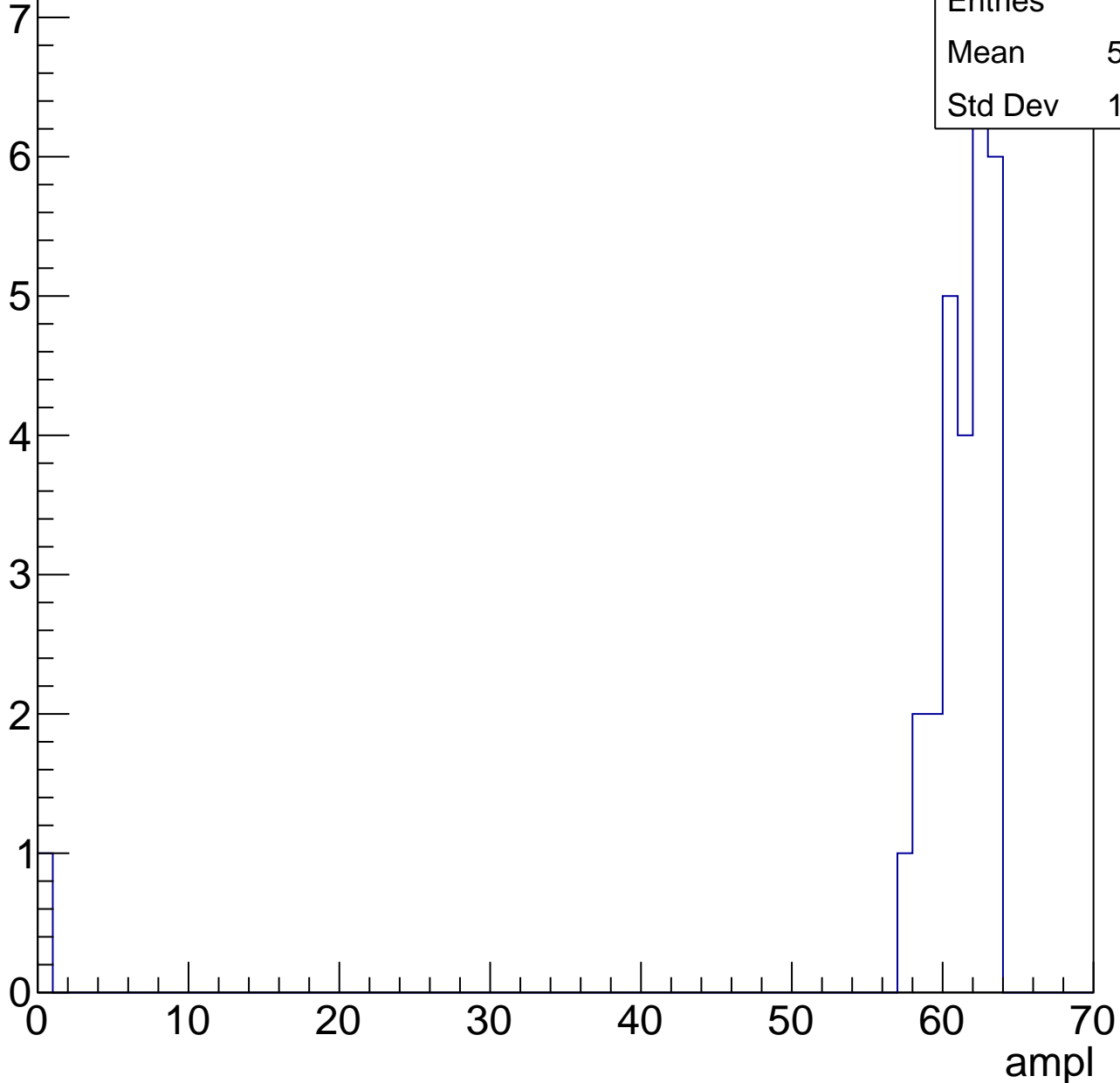


# B1L000S, U2-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.82
Std Dev	11.44

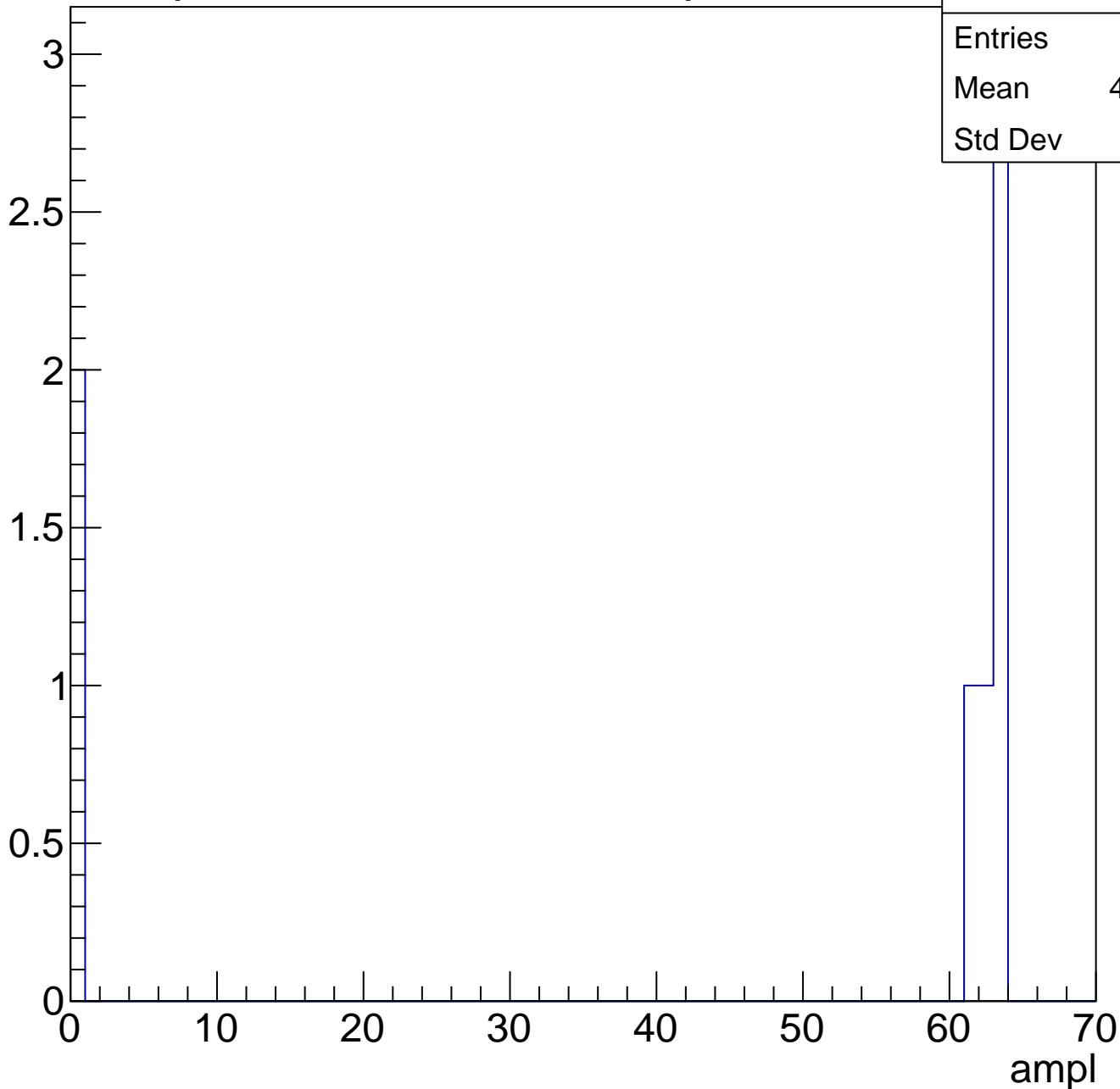




# B1L000S, U2-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch45, adc0

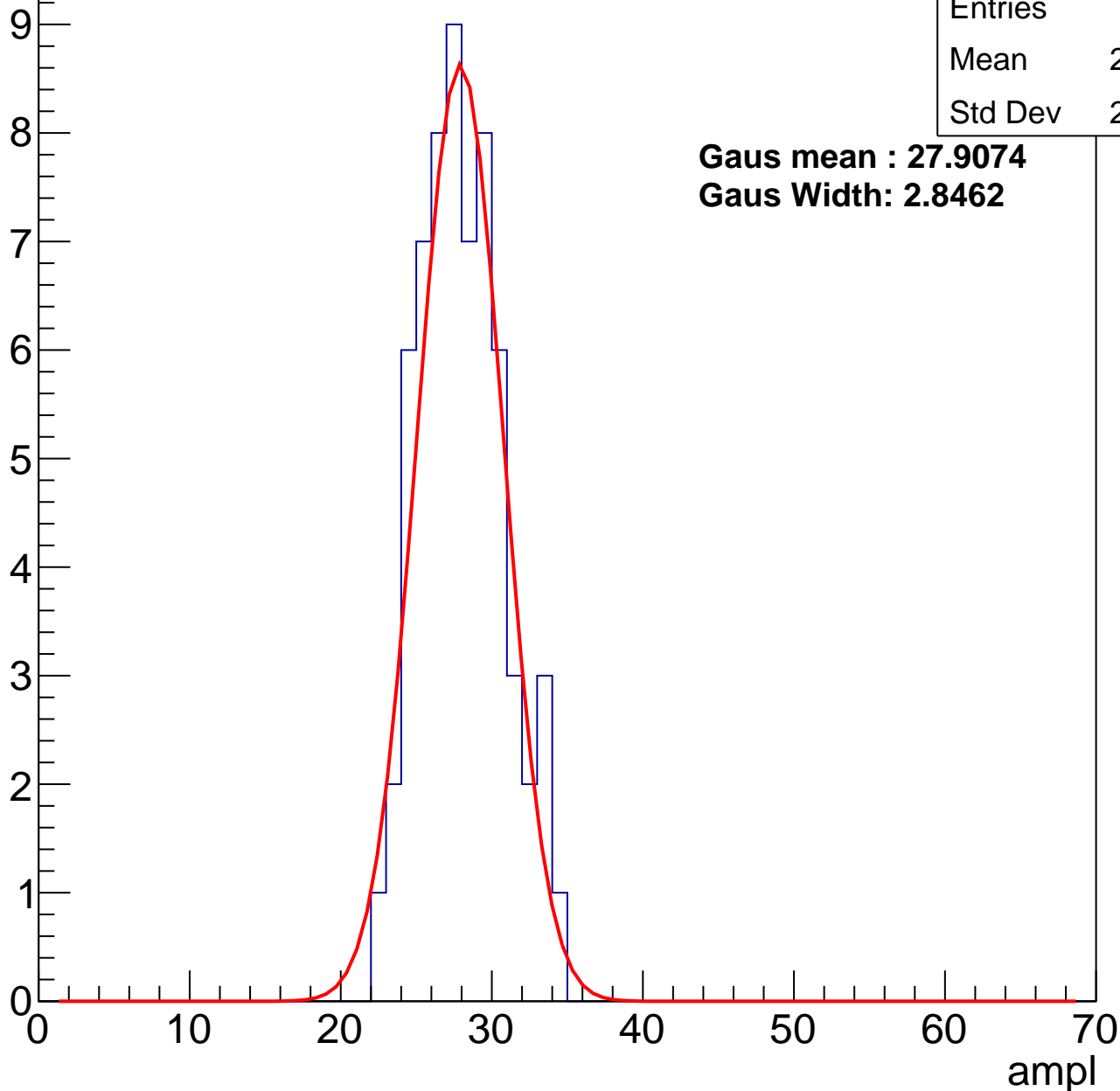
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	27.56
Std Dev	2.747

**Gaus mean : 27.9074**

**Gaus Width: 2.8462**



# B1L000S, U2-ch45, adc1

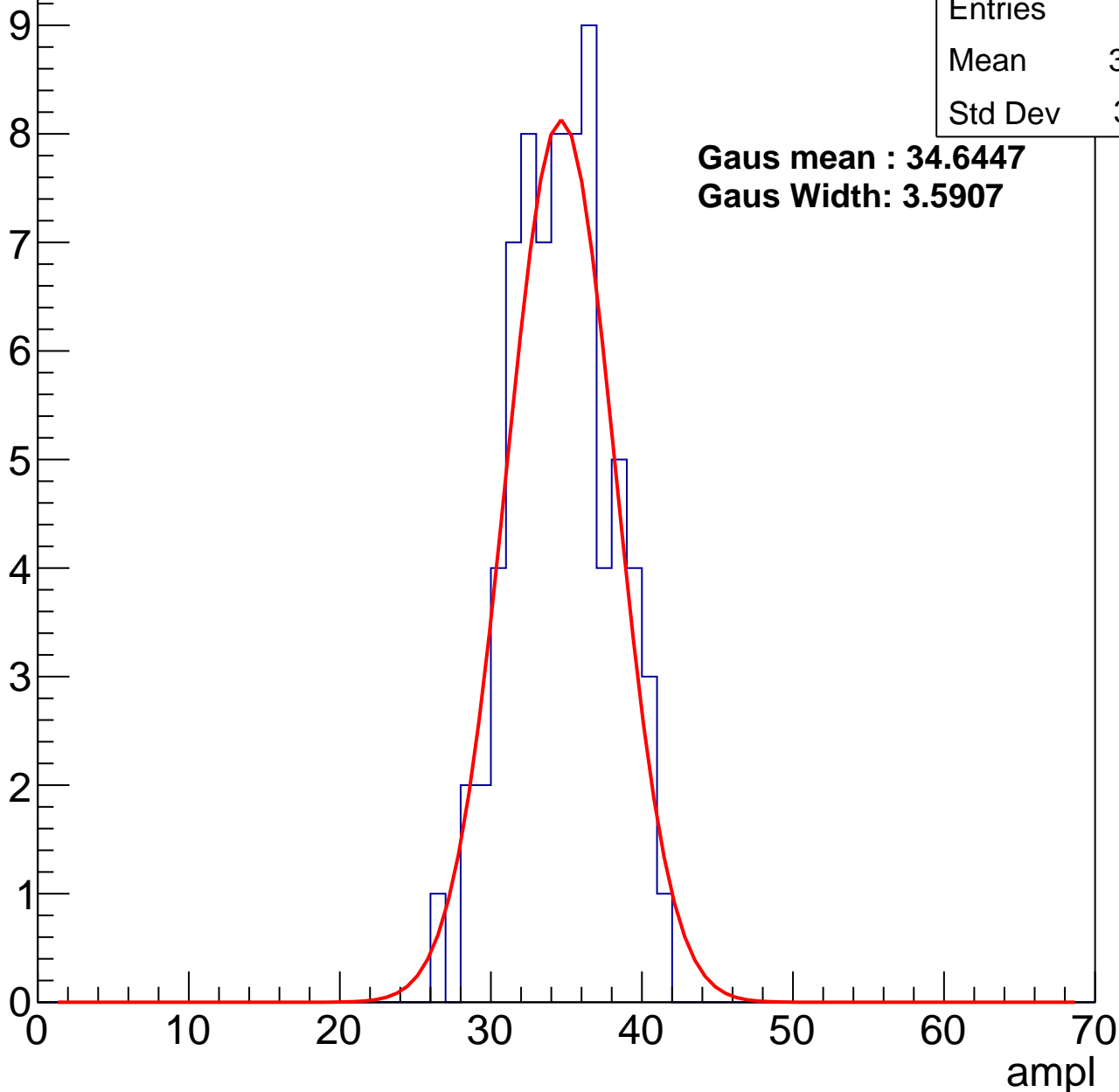
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	34.18
Std Dev	3.241

**Gaus mean : 34.6447**

**Gaus Width: 3.5907**



# B1L000S, U2-ch45, adc2

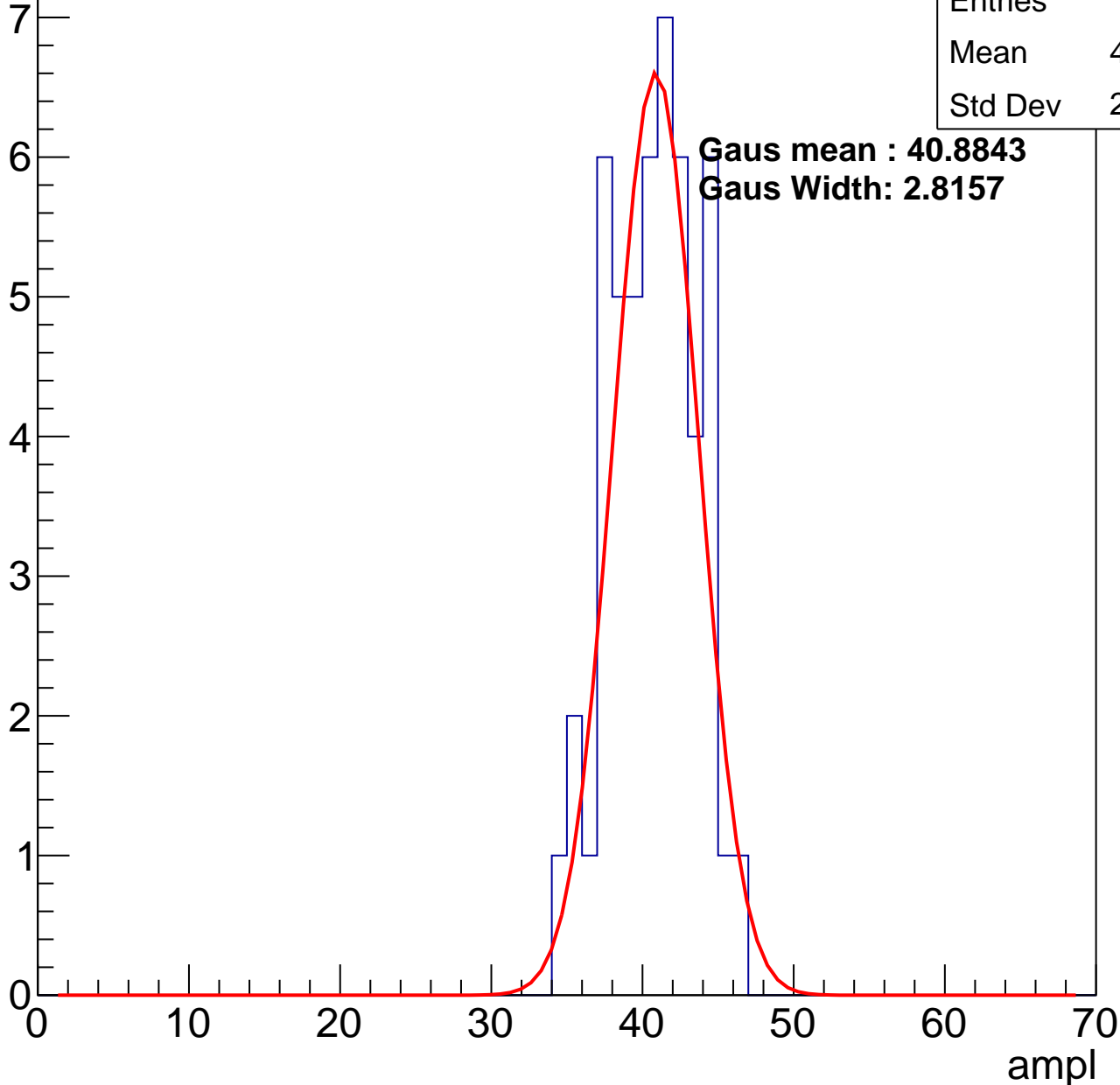
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	40.25
Std Dev	2.806

**Gaus mean : 40.8843**

**Gaus Width: 2.8157**



# B1L000S, U2-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

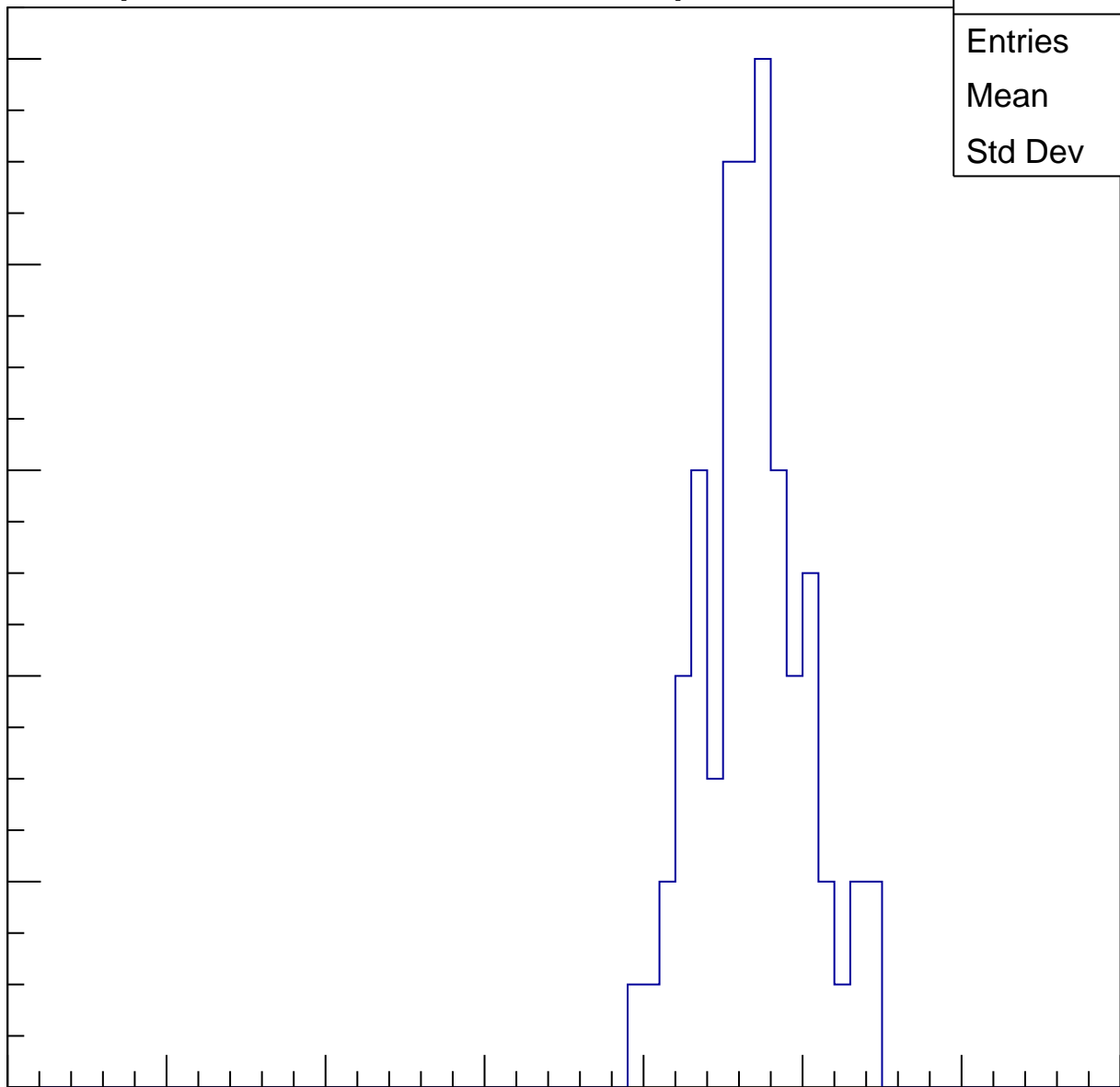
Entries	67
Mean	46.42
Std Dev	3.288

Entry

10  
8  
6  
4  
2  
0

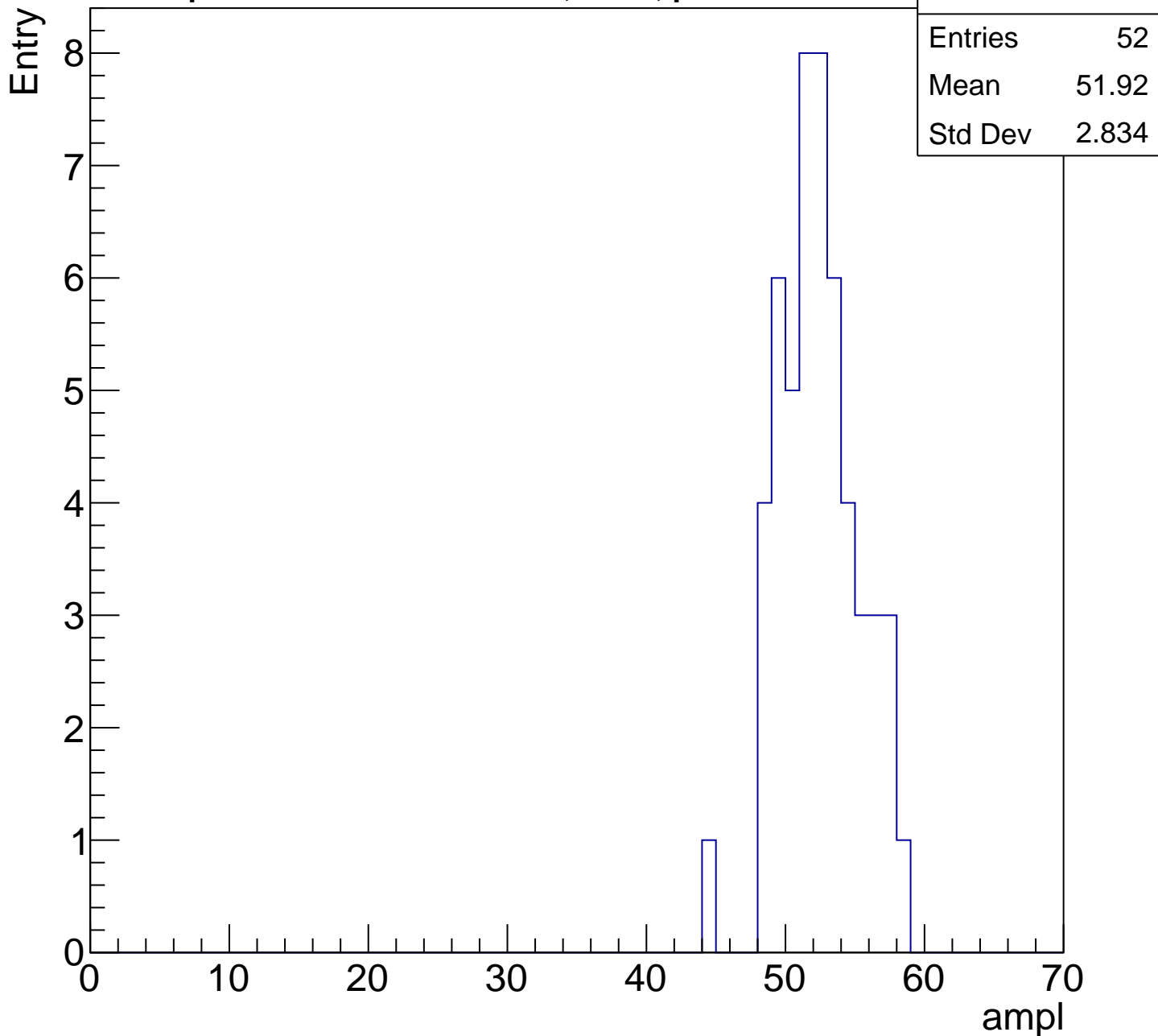
0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

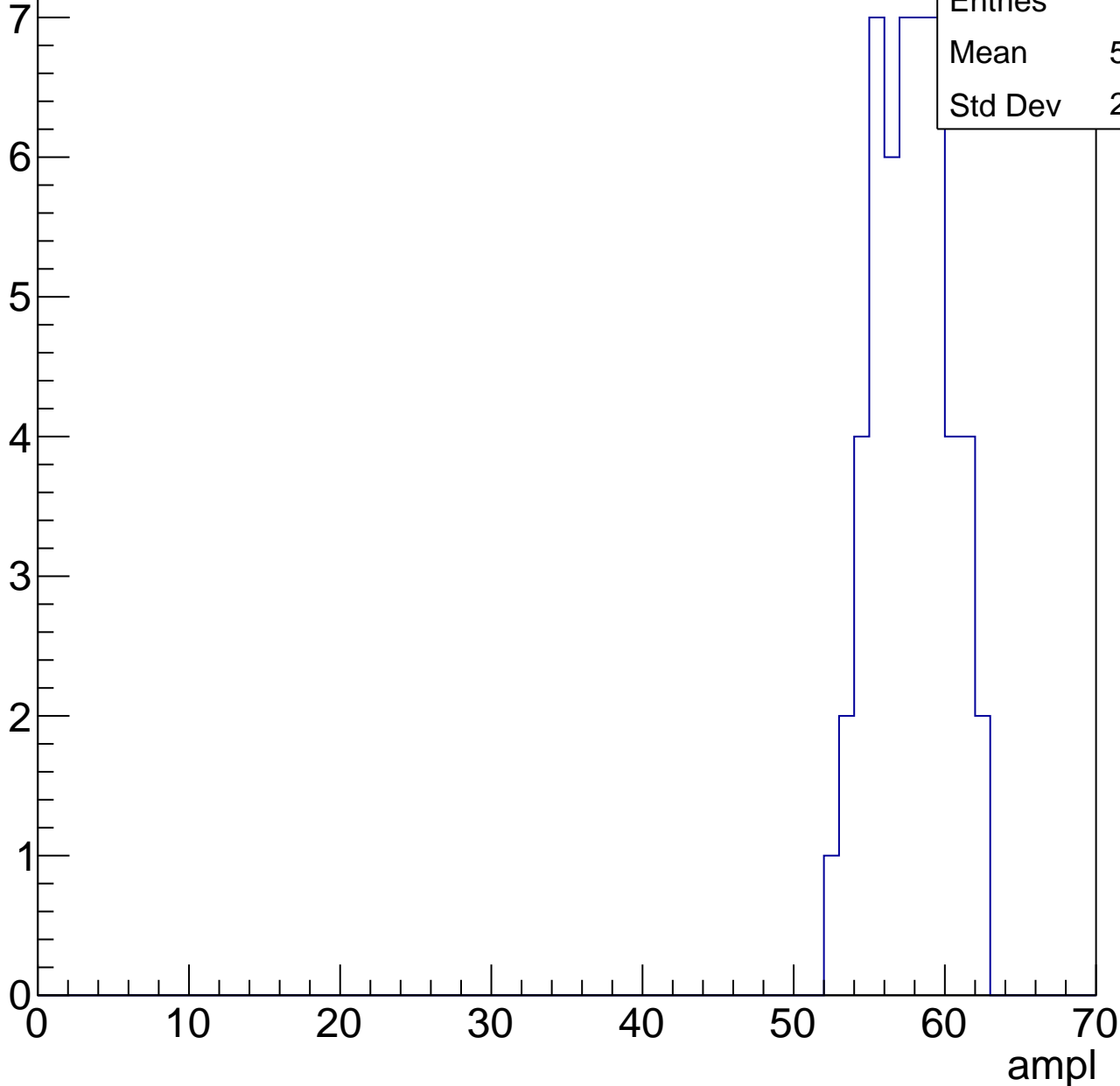


# B1L000S, U2-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	57.27
Std Dev	2.458

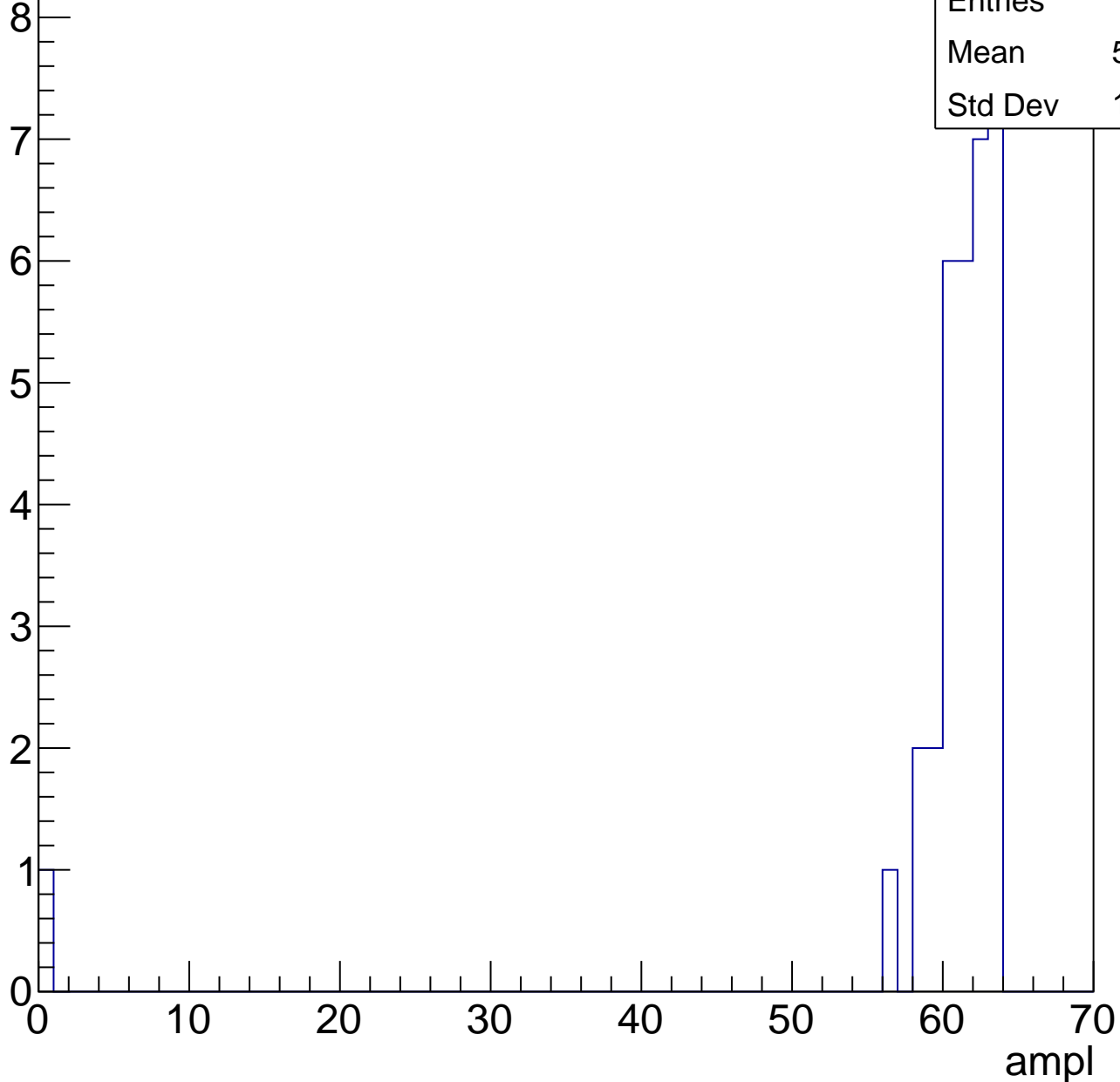


# B1L000S, U2-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	33
Mean	59.21
Std Dev	10.61

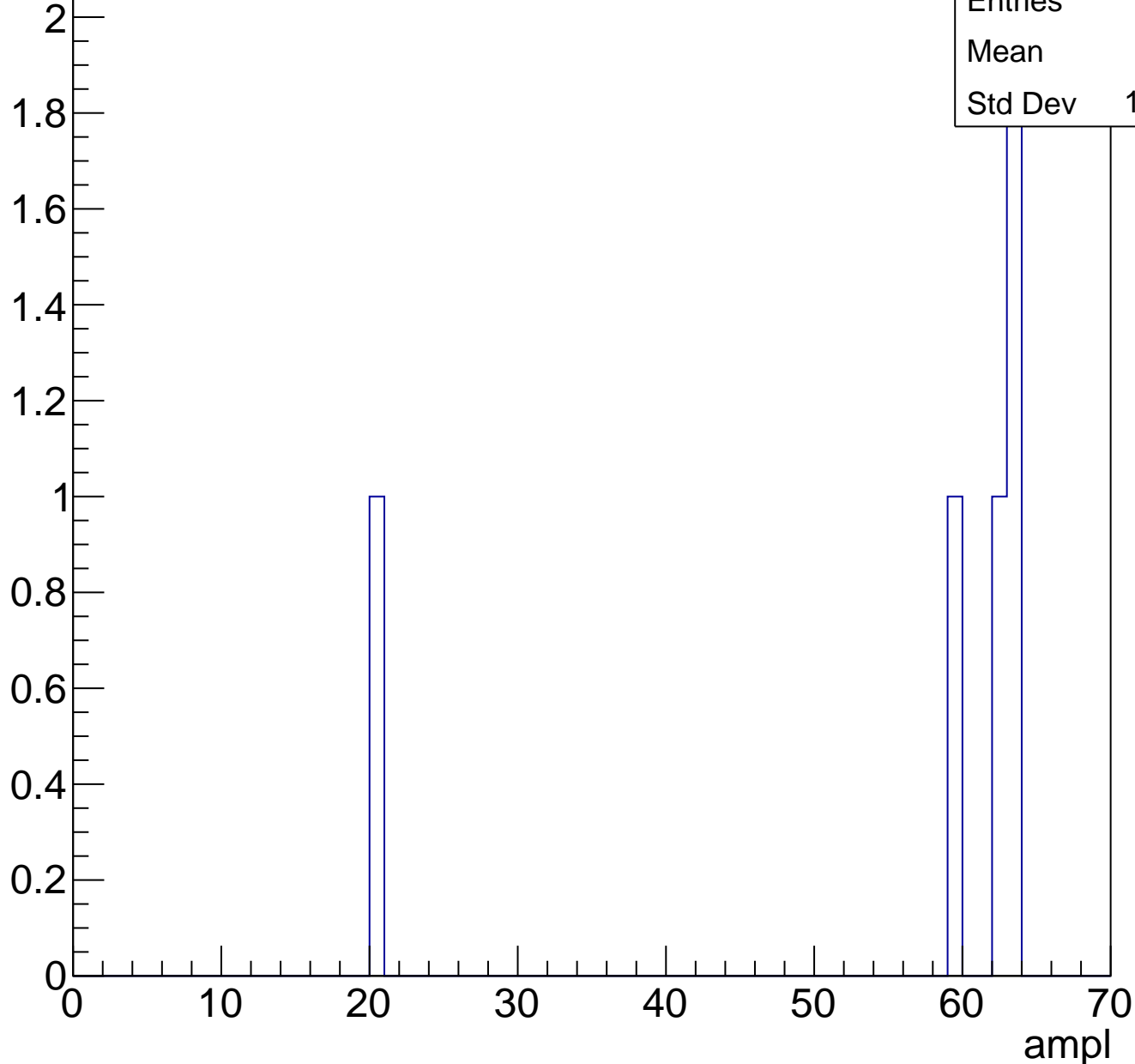




# B1L000S, U2-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch46, adc0

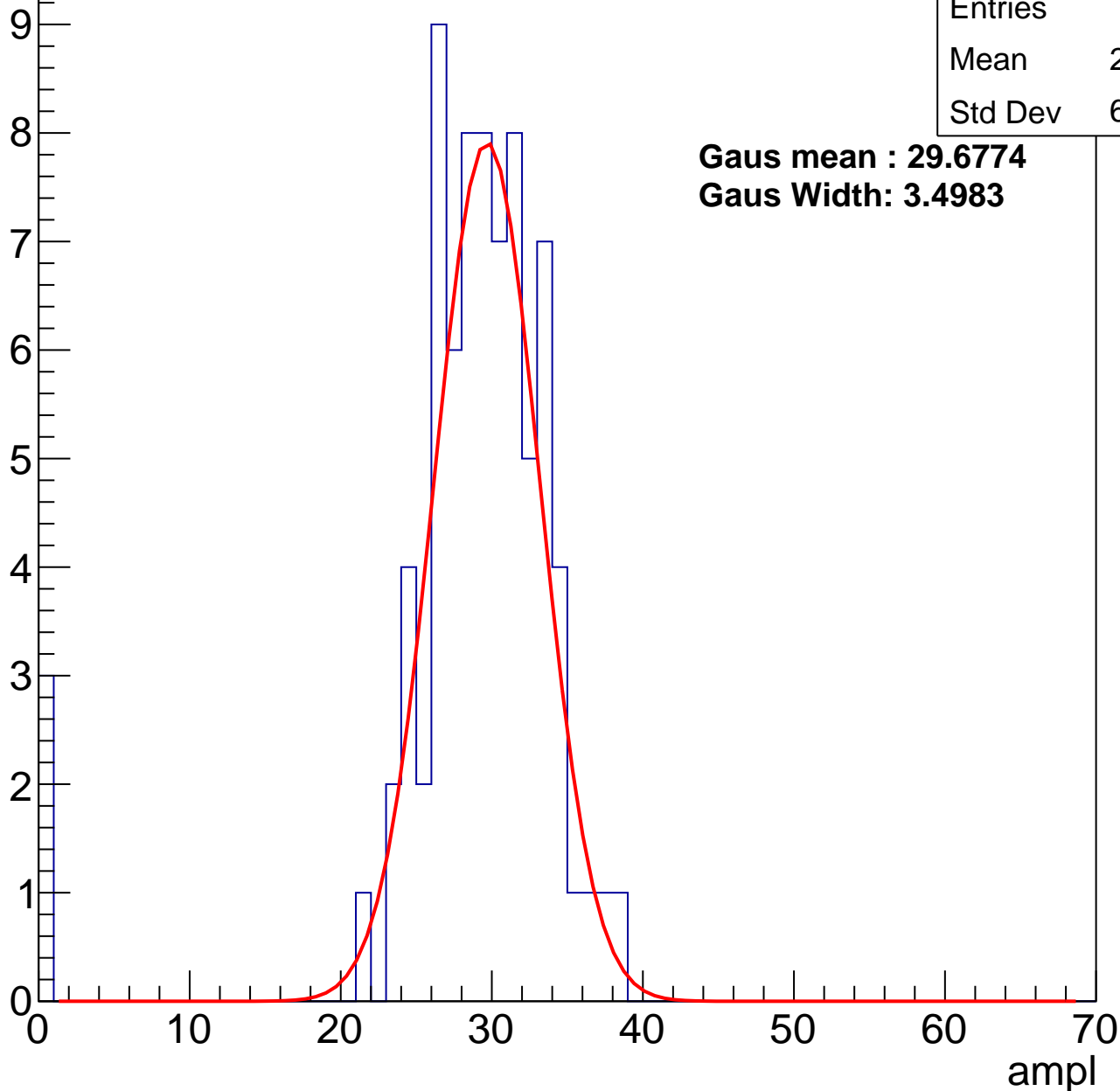
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	28.15
Std Dev	6.577

**Gaus mean : 29.6774**

**Gaus Width: 3.4983**



# B1L000S, U2-ch46, adc1

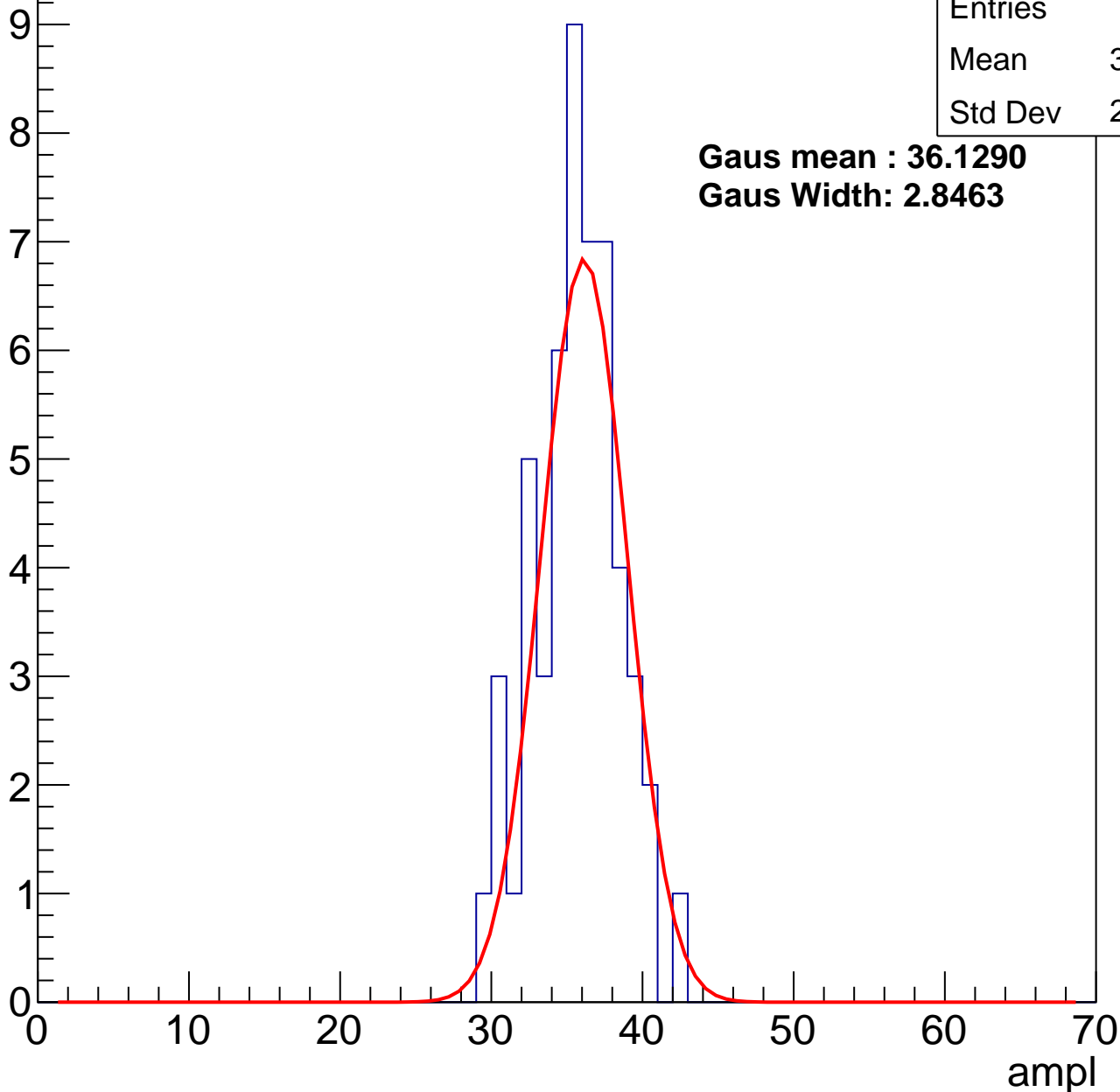
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	35.19
Std Dev	2.794

**Gaus mean : 36.1290**

**Gaus Width: 2.8463**



# B1L000S, U2-ch46, adc2

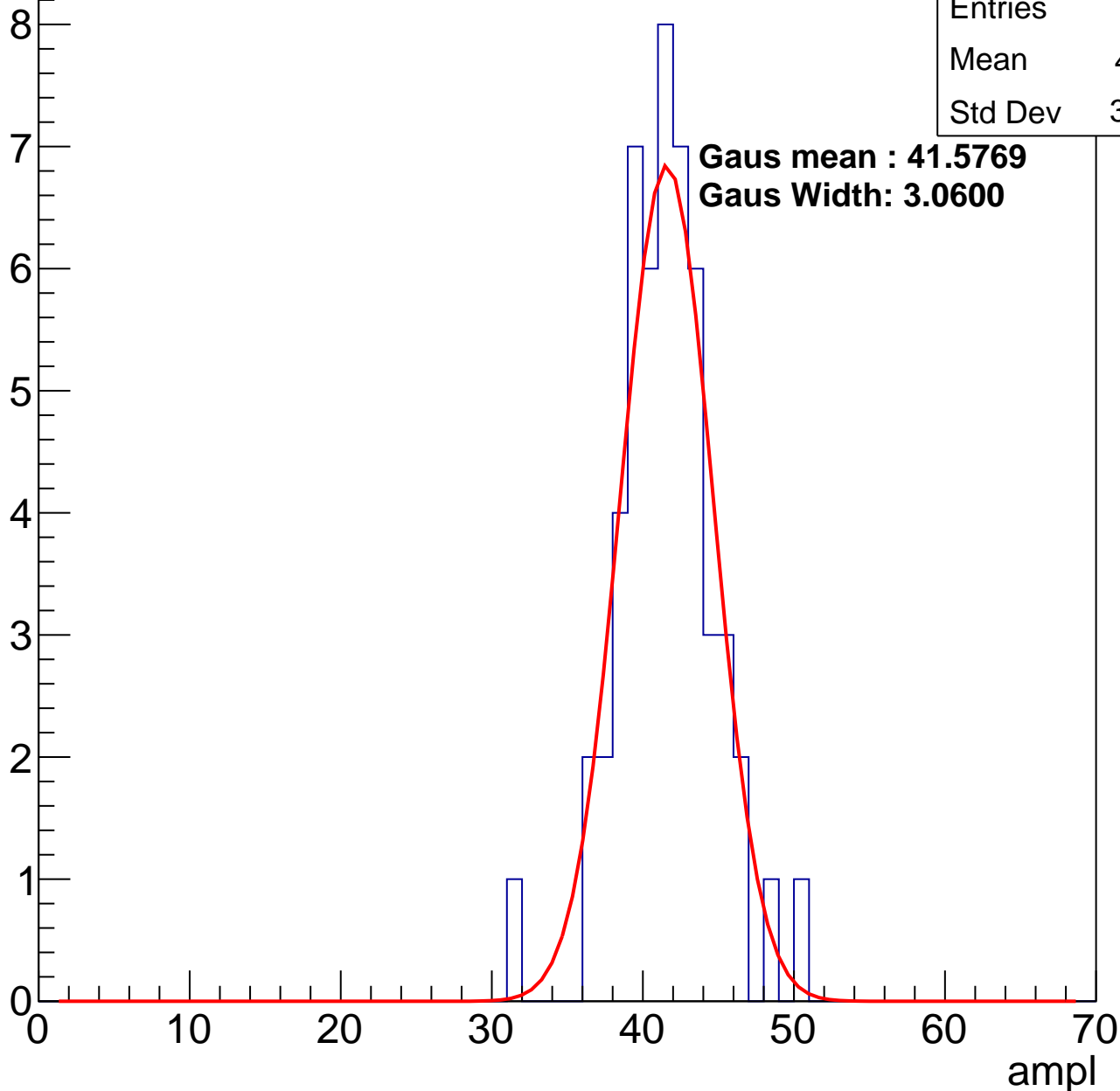
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	41.11
Std Dev	3.184

**Gaus mean : 41.5769**

**Gaus Width: 3.0600**

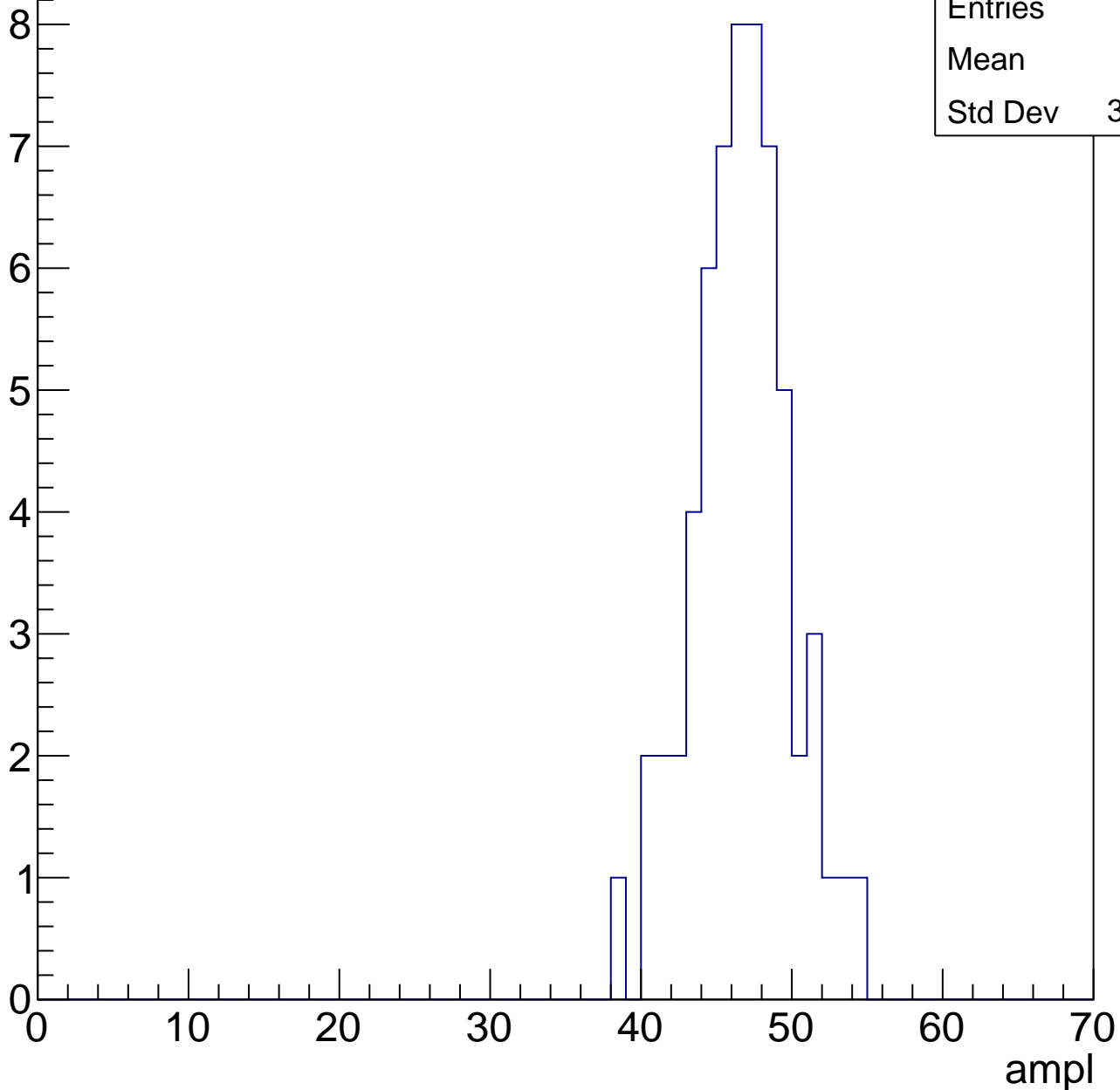


# B1L000S, U2-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	46.2
Std Dev	3.214

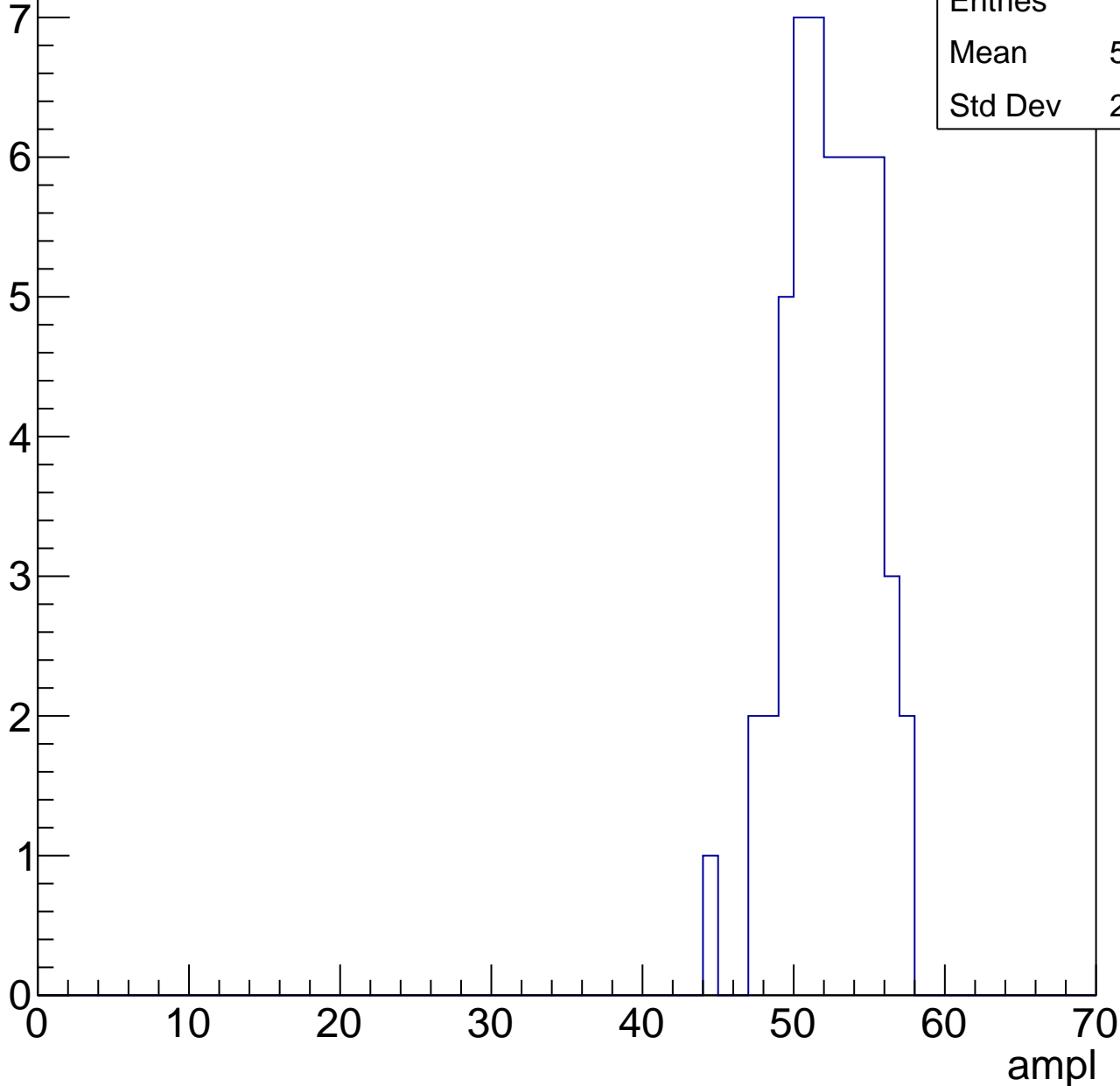


# B1L000S, U2-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

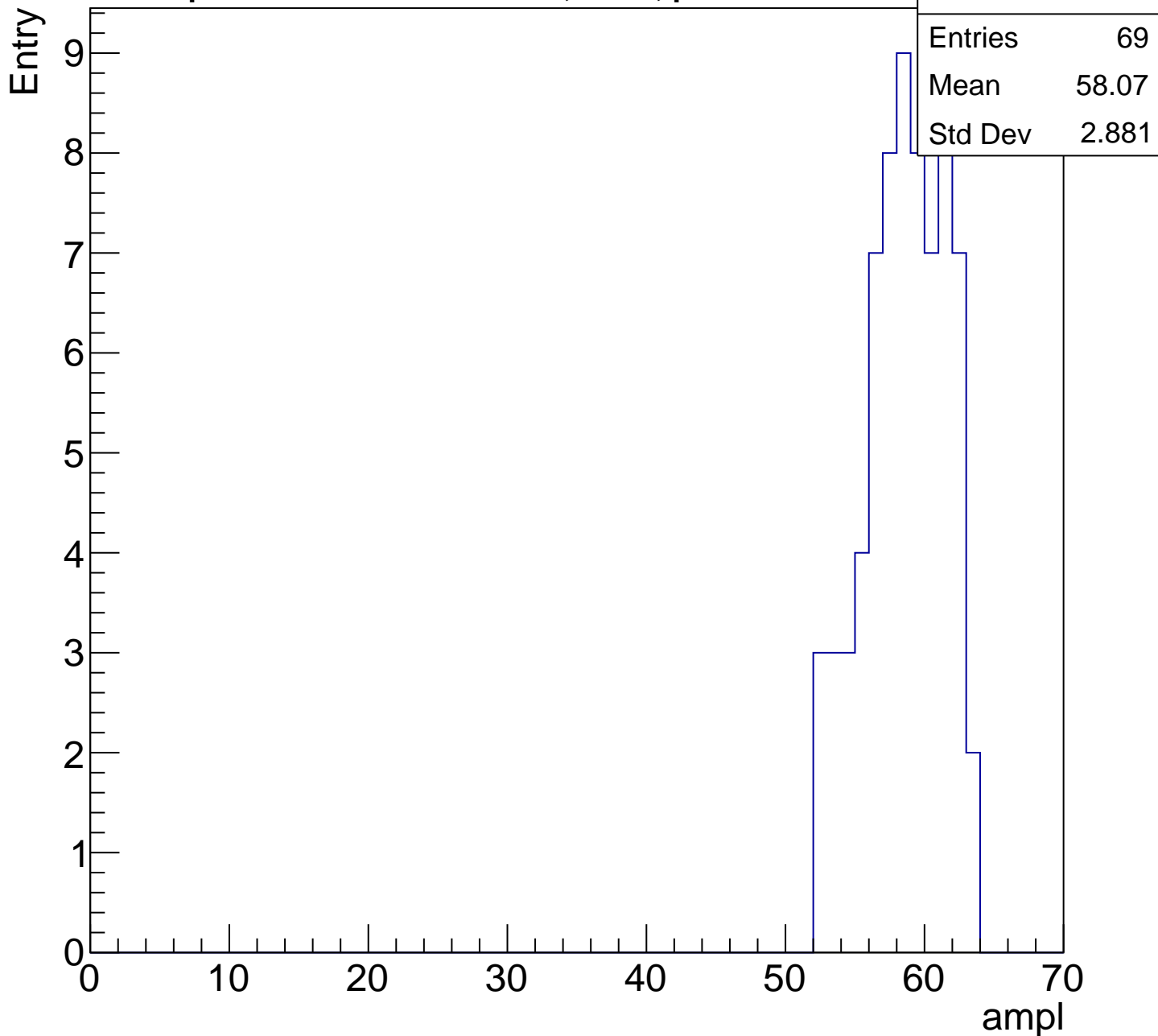
Entry

Entries	53
Mean	51.92
Std Dev	2.774



# B1L000S, U2-ch46, adc5

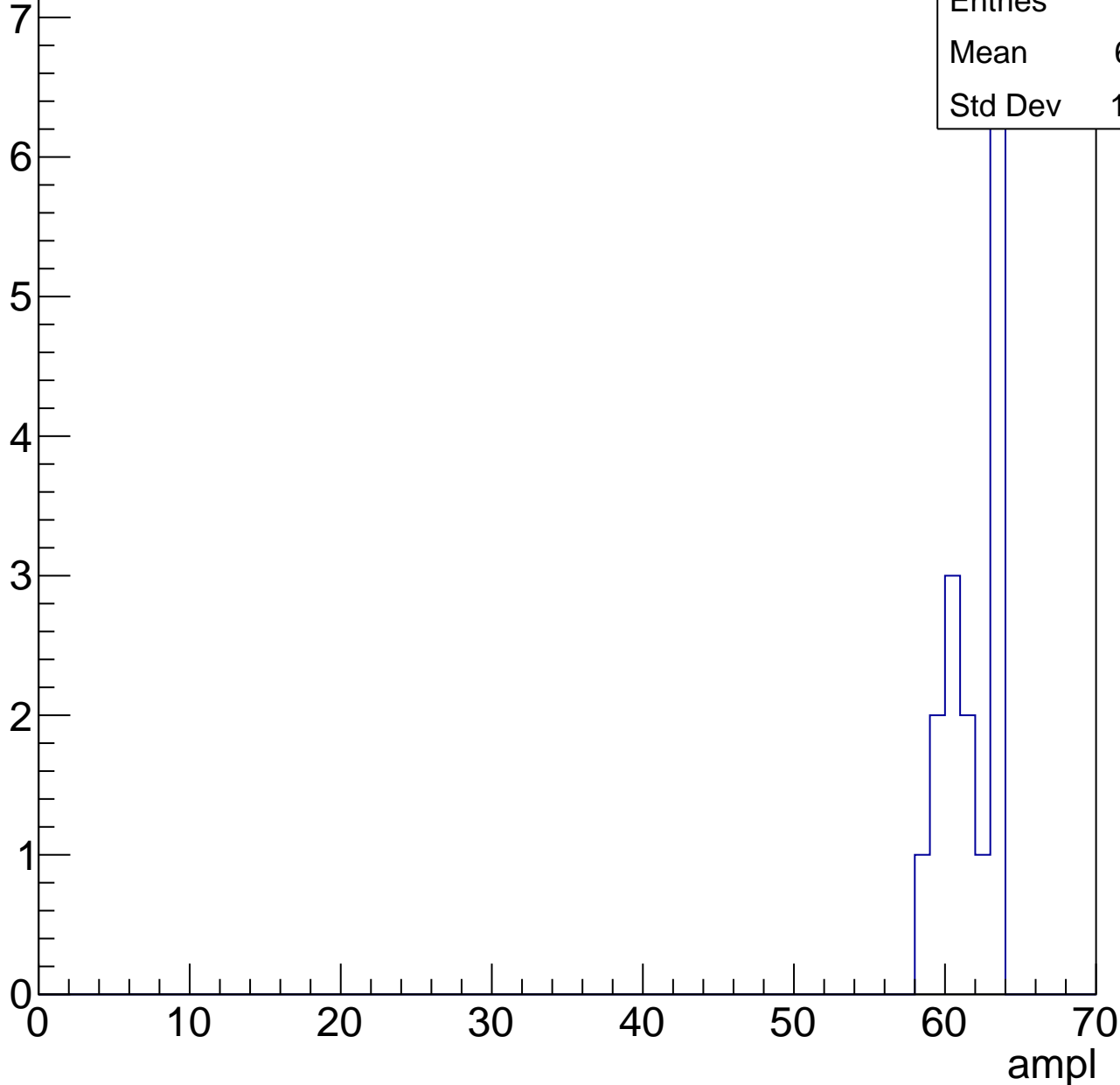
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

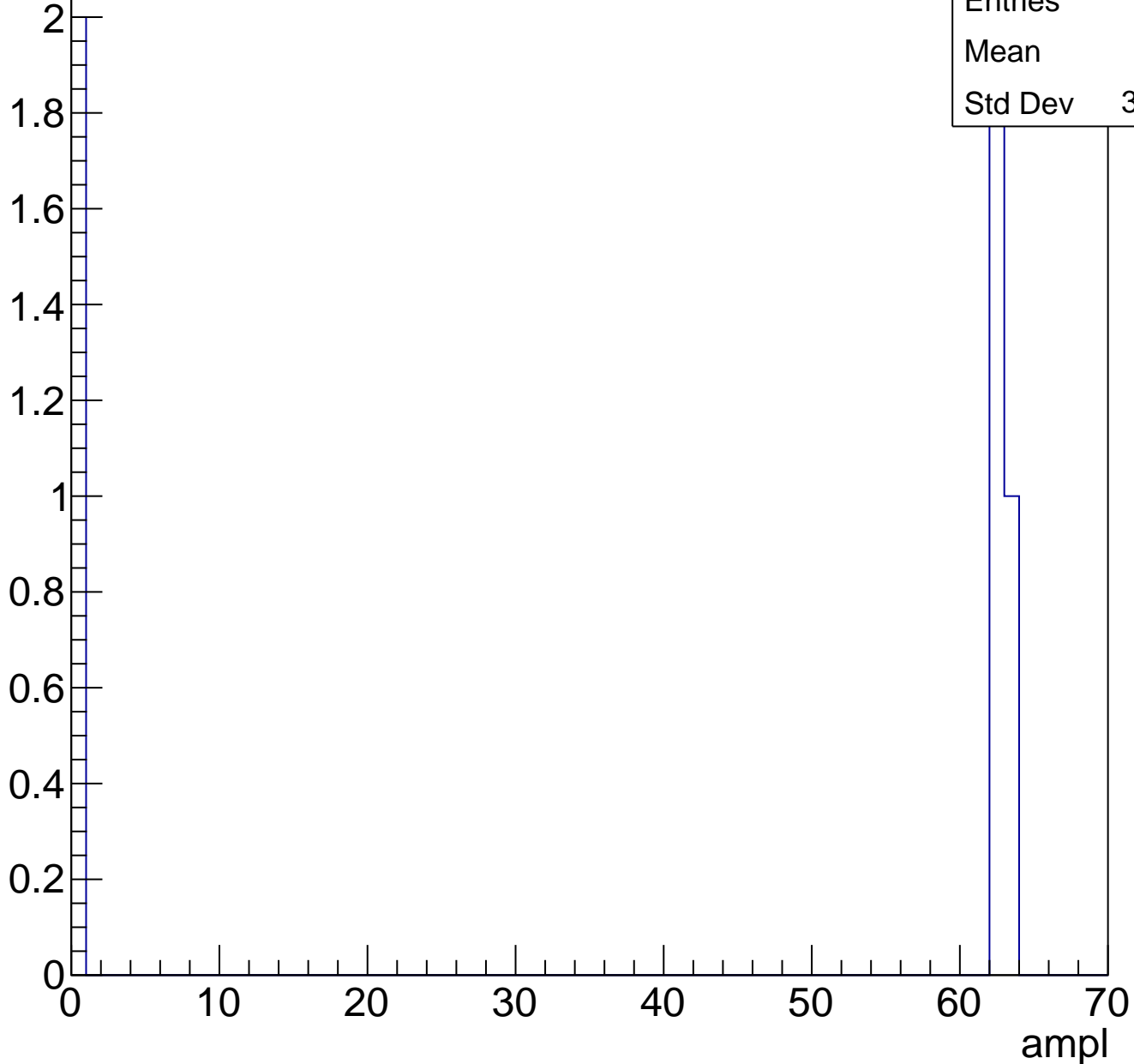




# B1L000S, U2-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch47, adc0

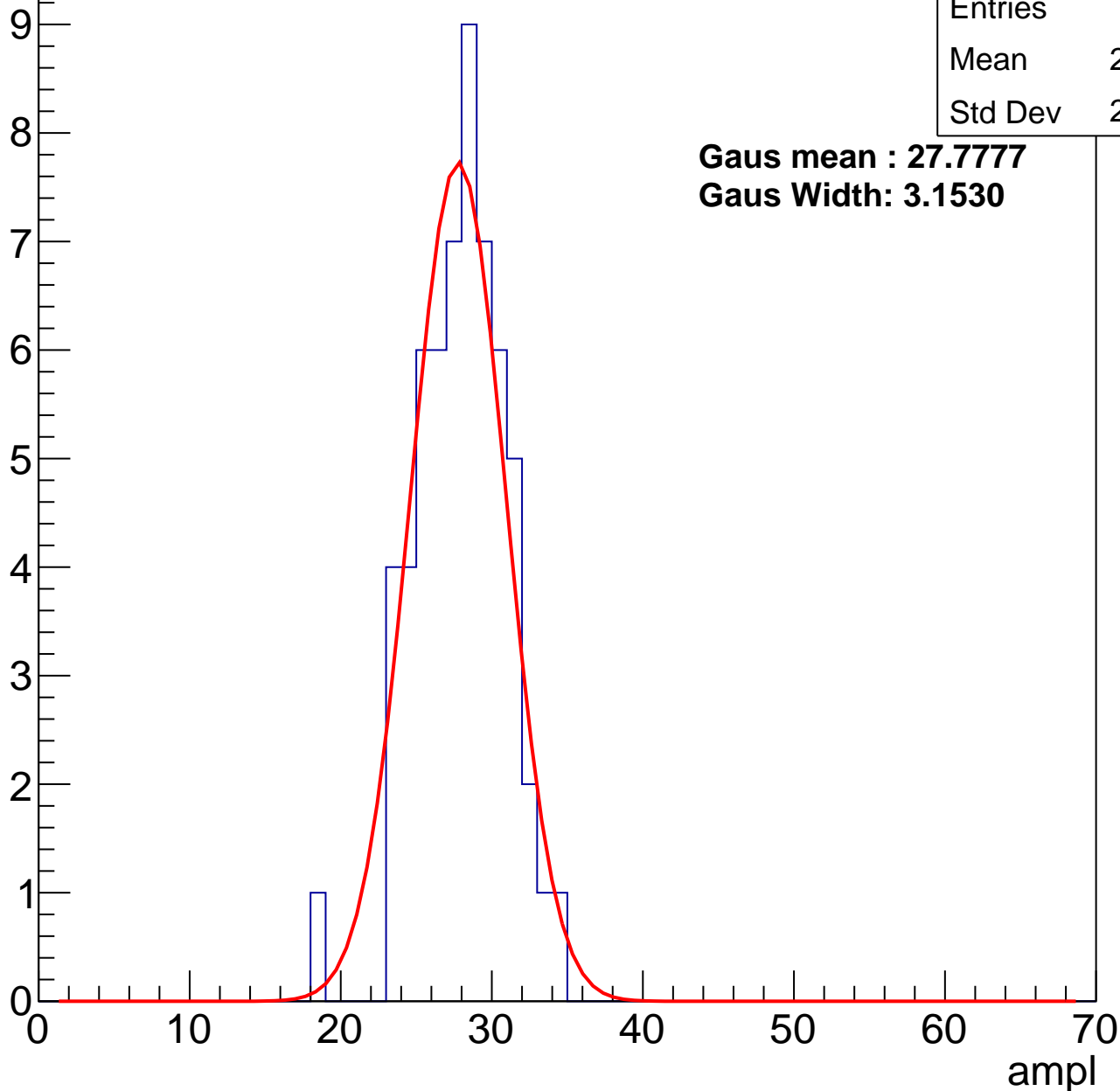
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.49
Std Dev	2.919

**Gaus mean : 27.7777**

**Gaus Width: 3.1530**



# B1L000S, U2-ch47, adc1

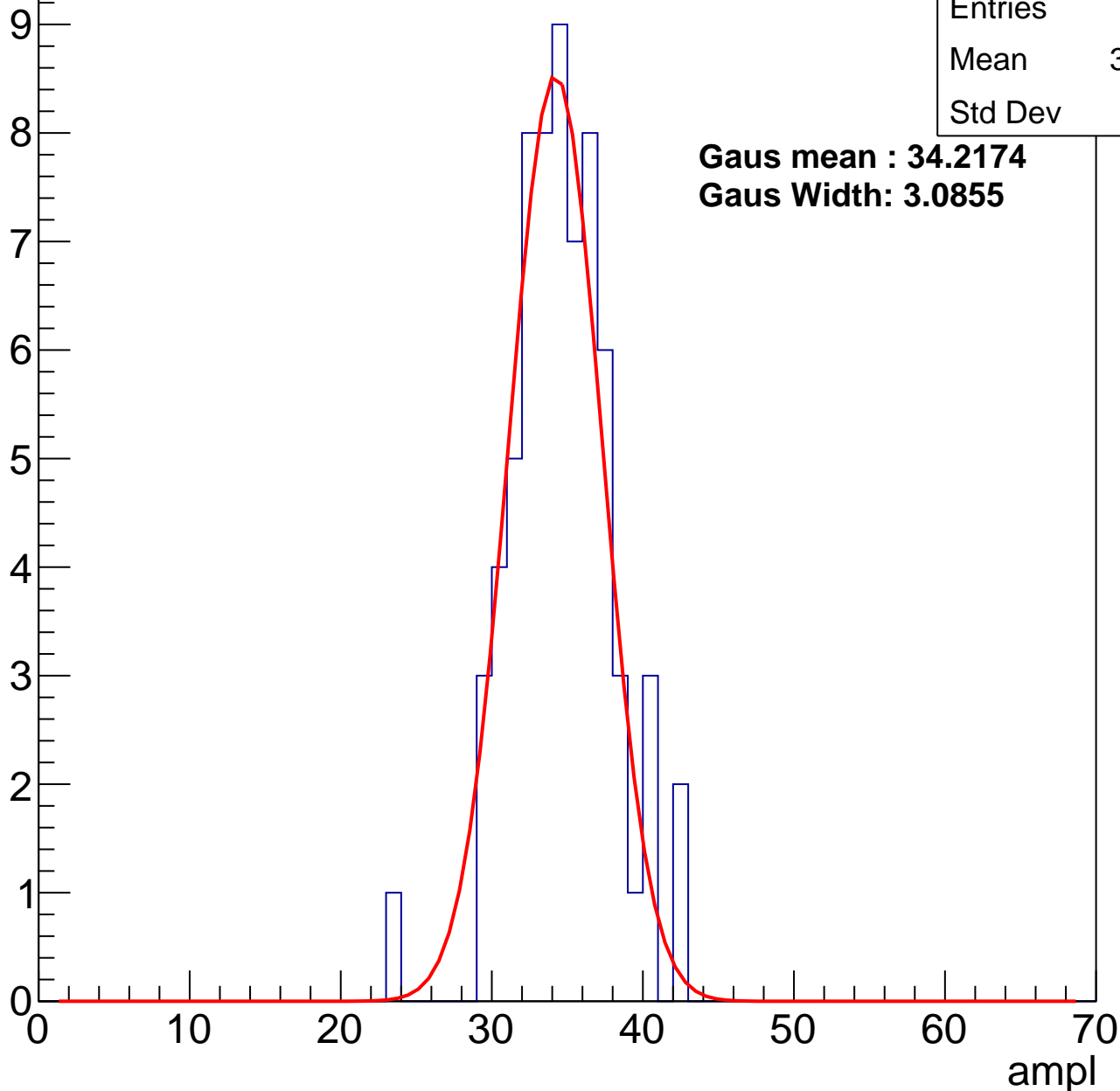
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	34.16
Std Dev	3.31

**Gaus mean : 34.2174**

**Gaus Width: 3.0855**



# B1L000S, U2-ch47, adc2

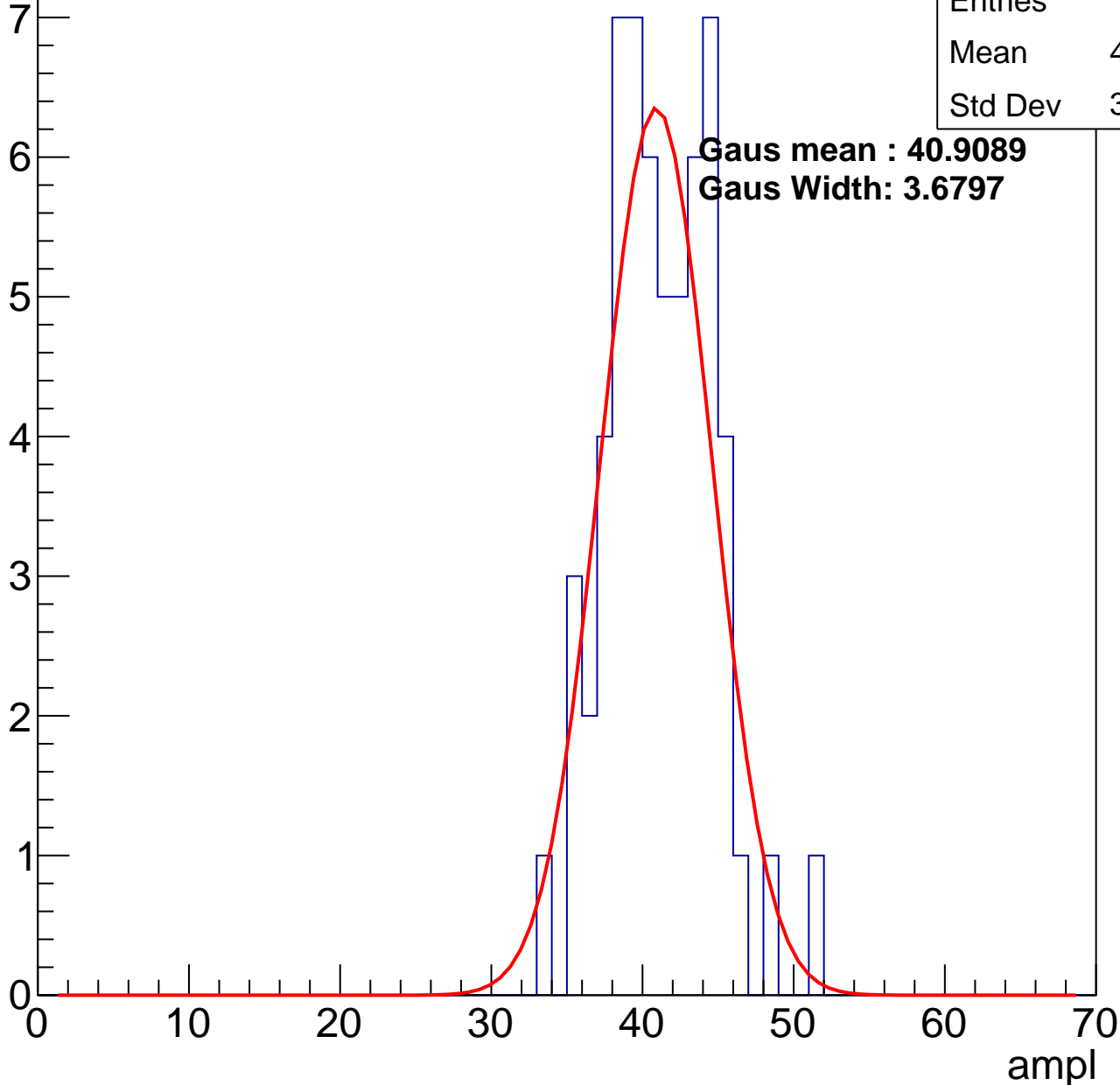
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	40.72
Std Dev	3.436

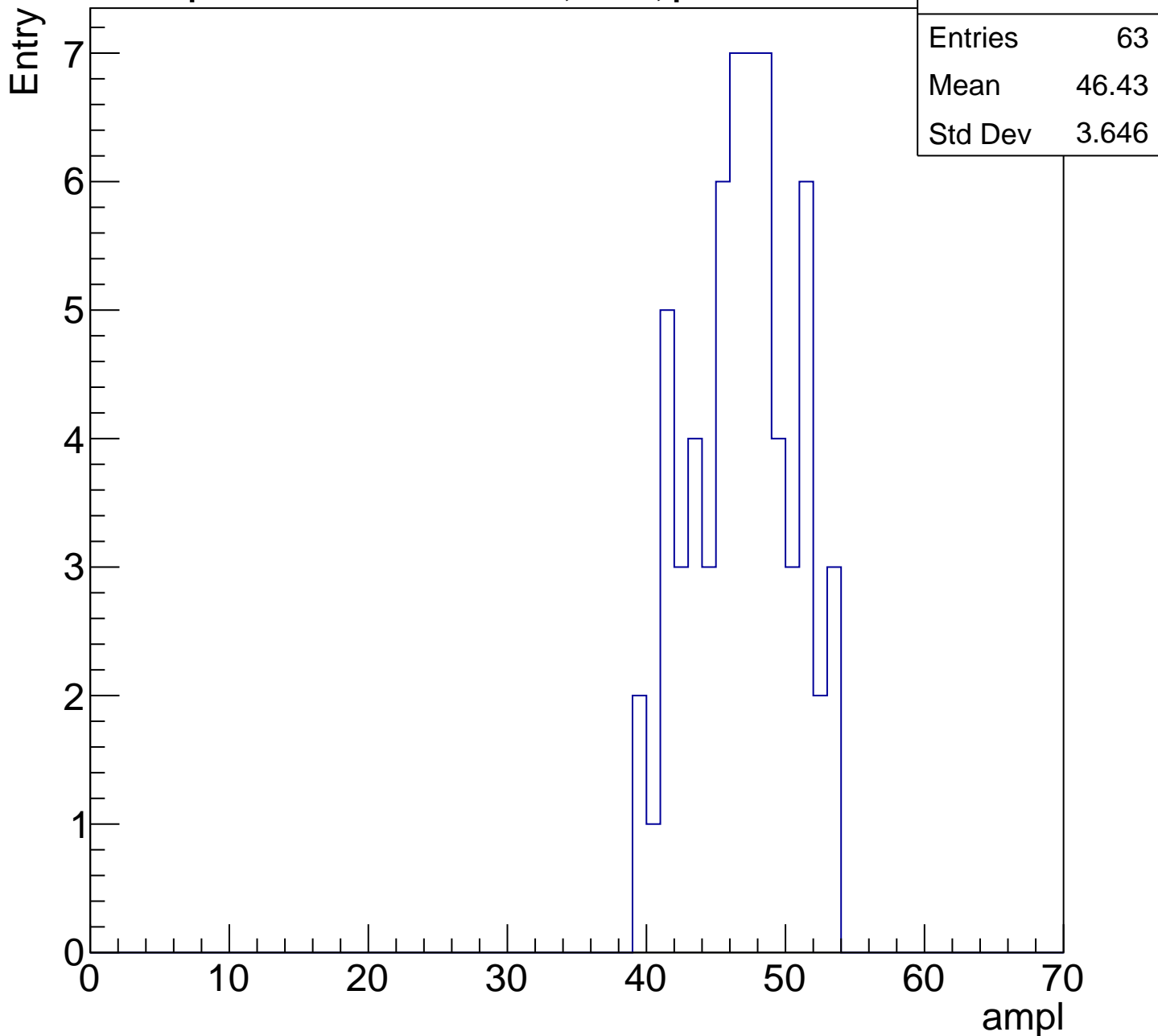
**Gaus mean : 40.9089**

**Gaus Width: 3.6797**



# B1L000S, U2-ch47, adc3

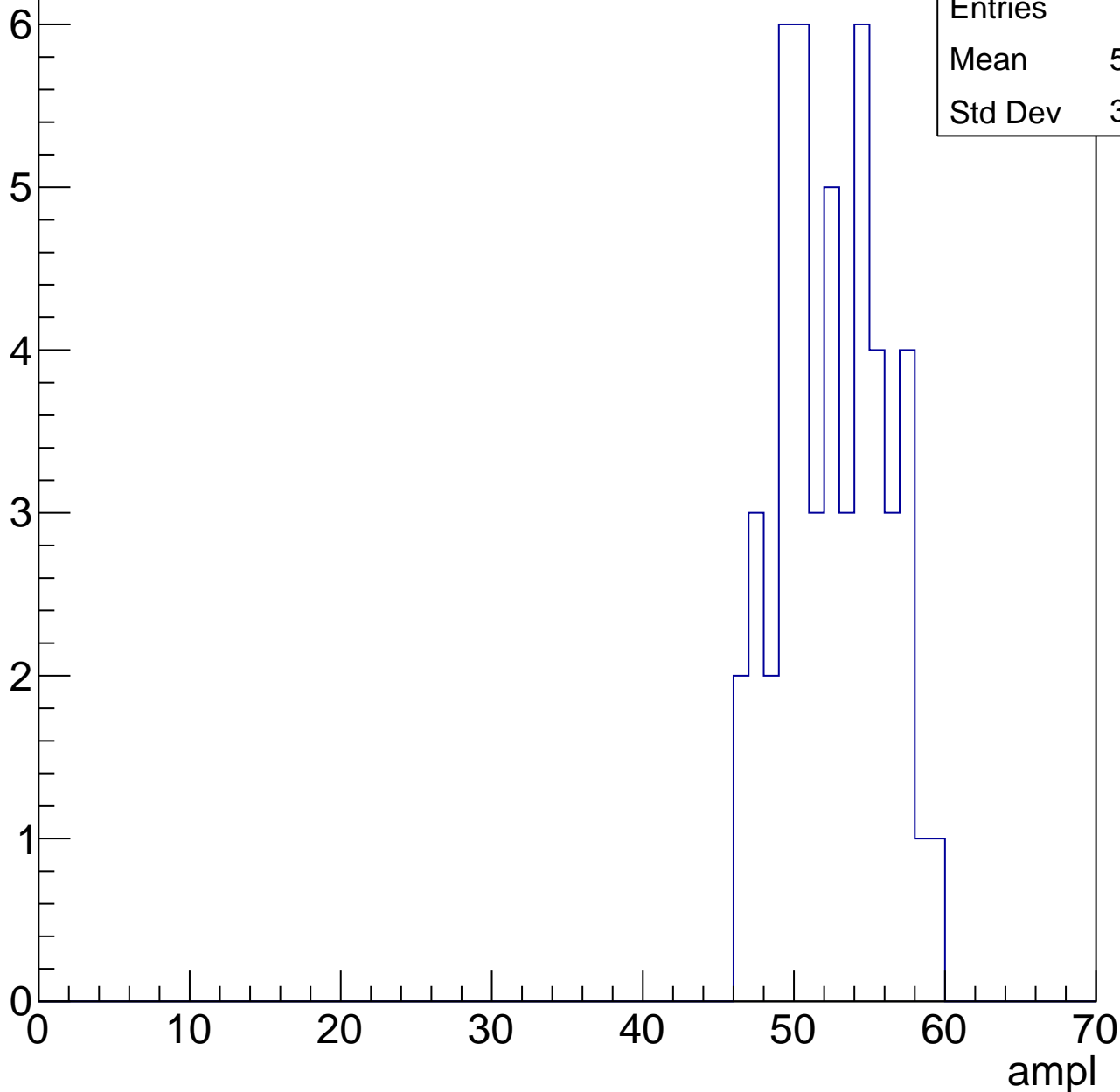
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

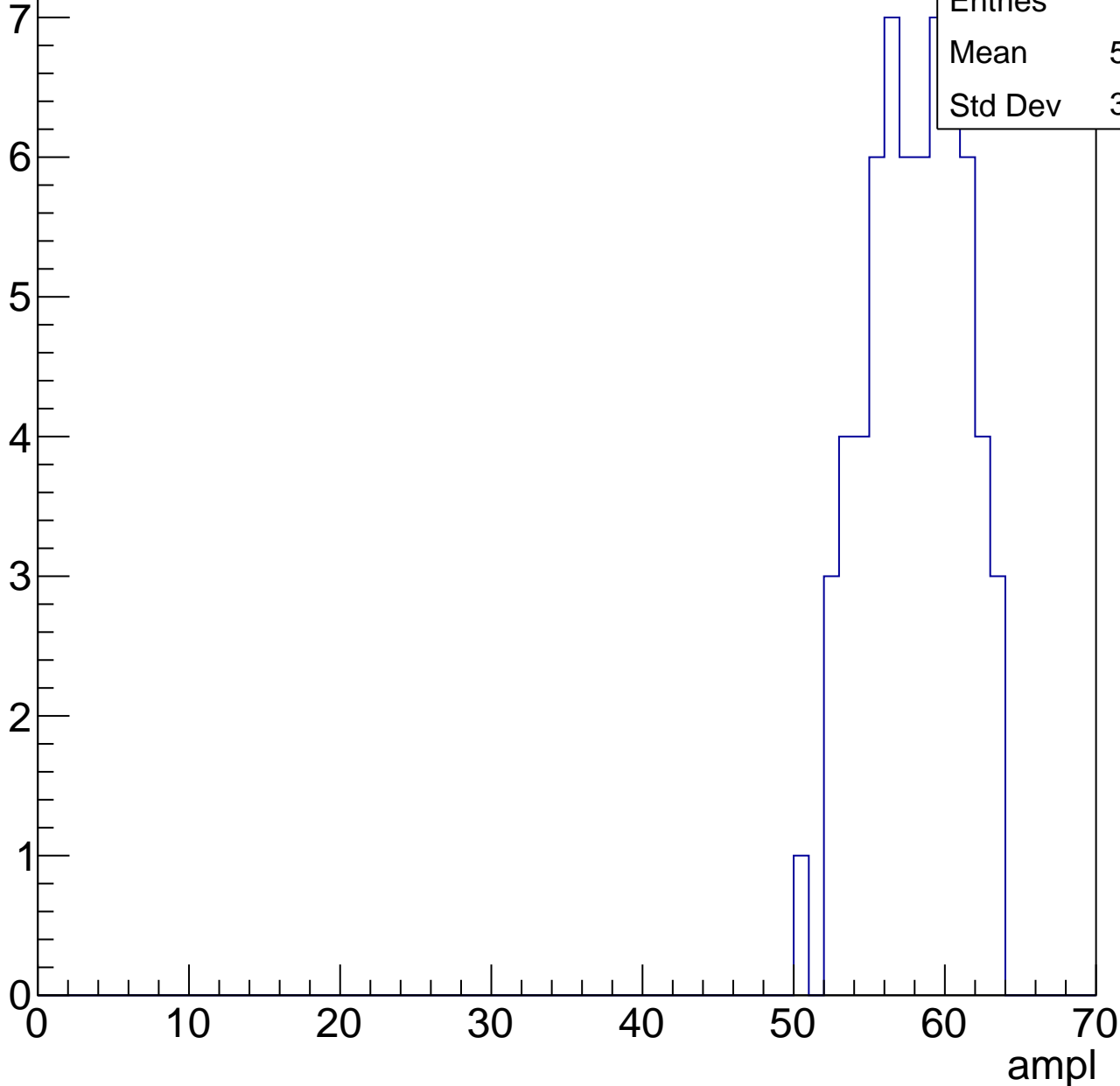


# B1L000S, U2-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	57.53
Std Dev	3.157

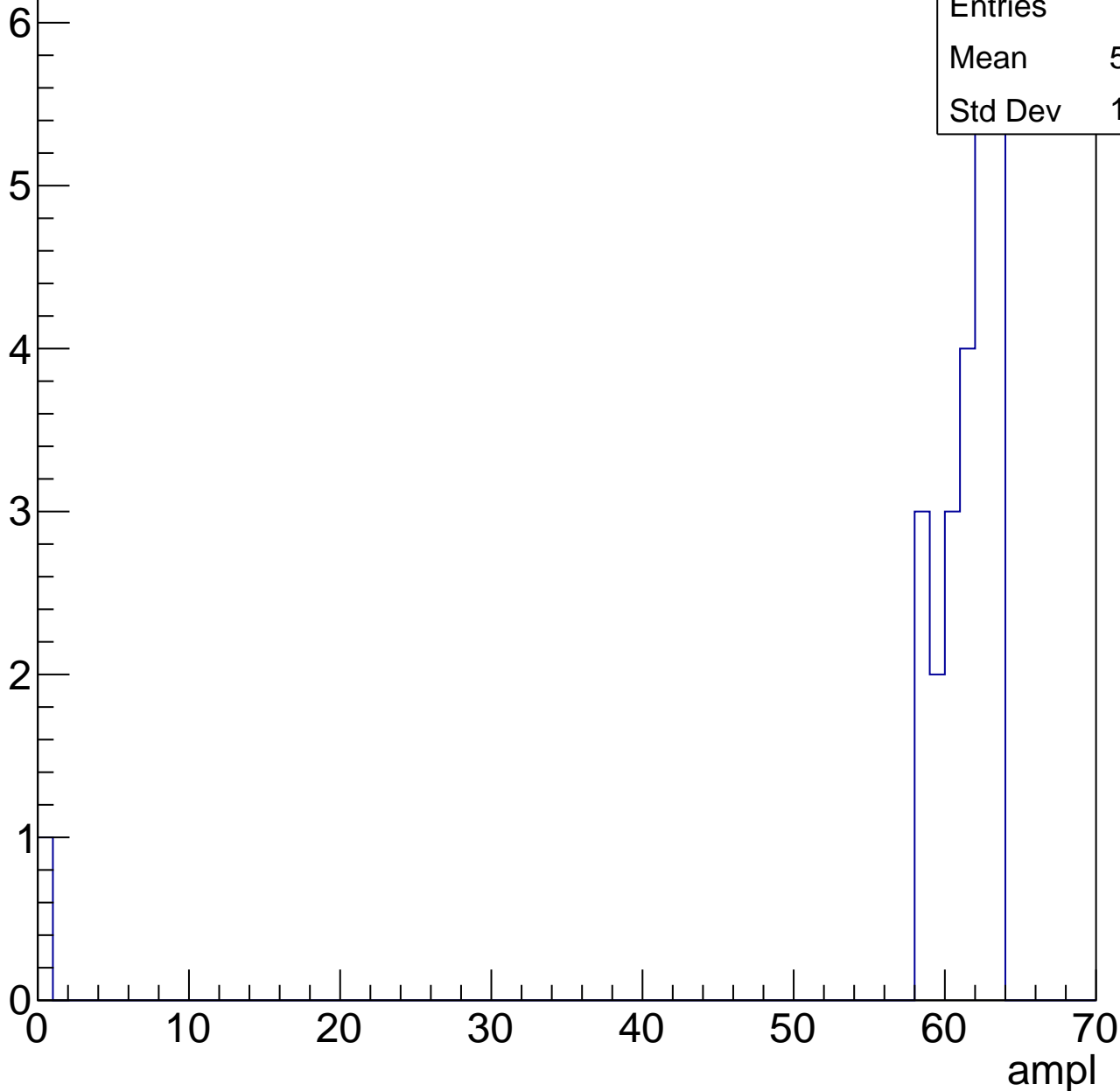


# B1L000S, U2-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.64
Std Dev	12.08

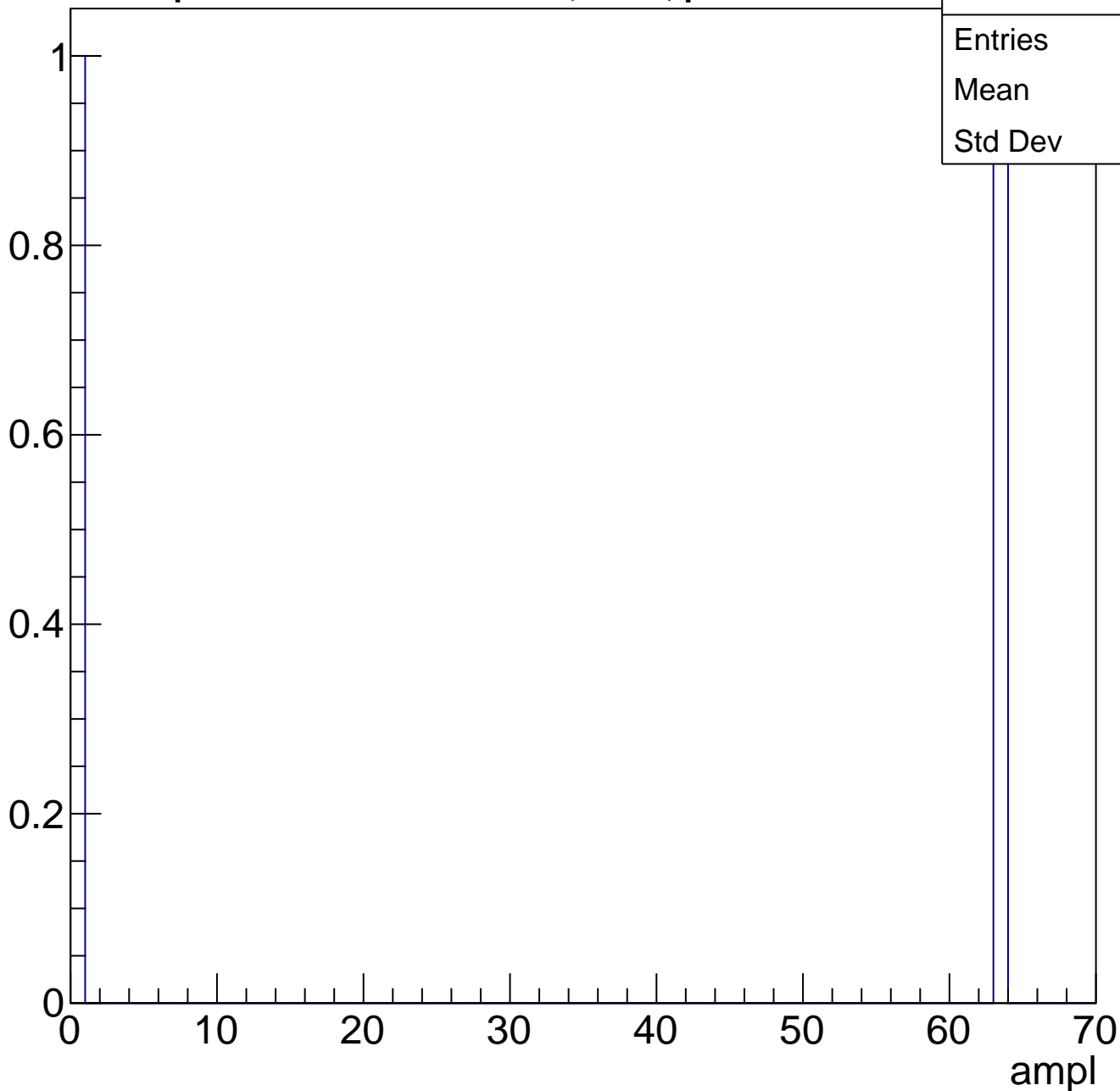




# B1L000S, U2-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch48, adc0

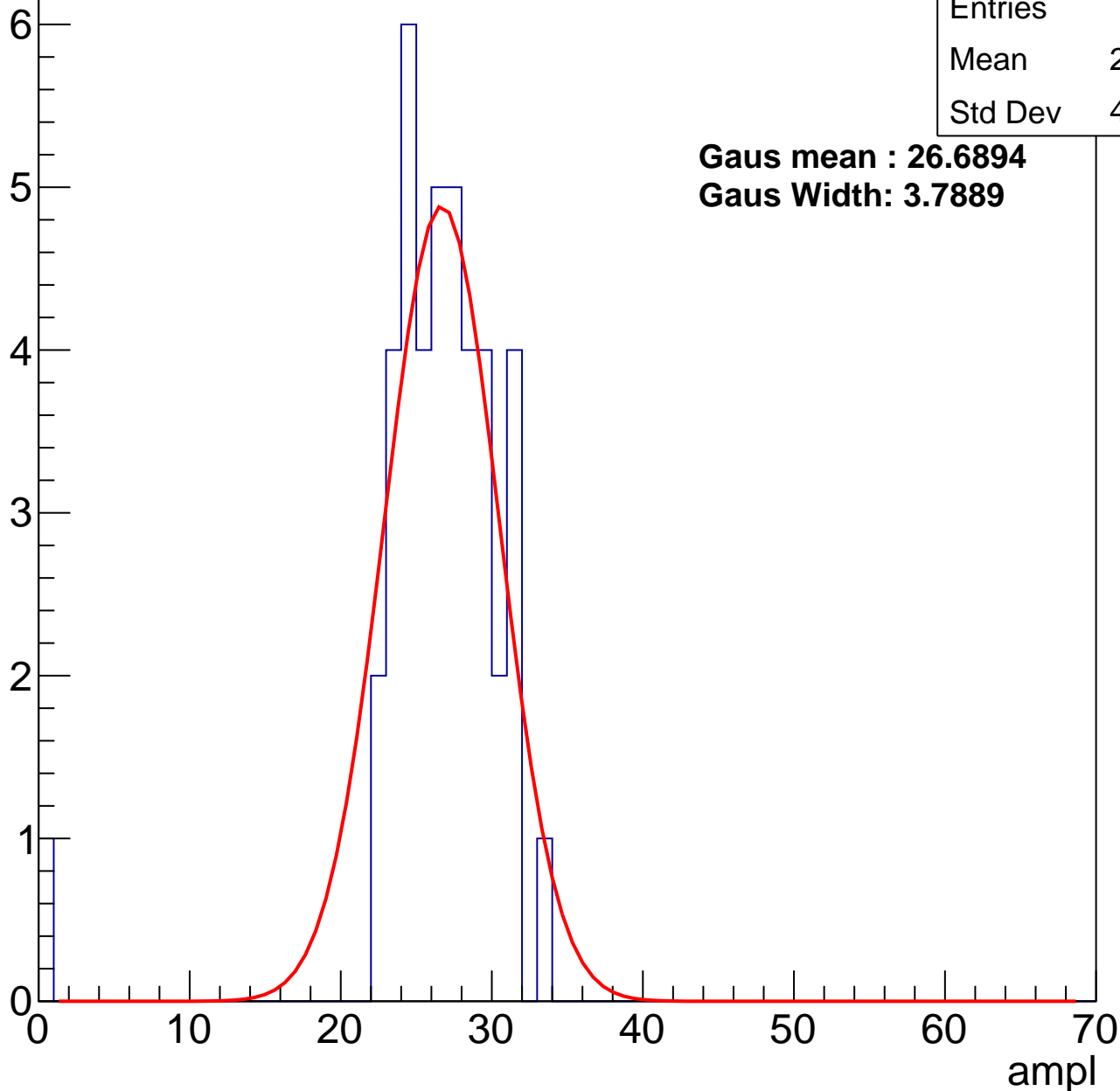
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	25.95
Std Dev	4.904

**Gaus mean : 26.6894**

**Gaus Width: 3.7889**



# B1L000S, U2-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	33.01
Std Dev	3.23

**Gaus mean : 33.5338**

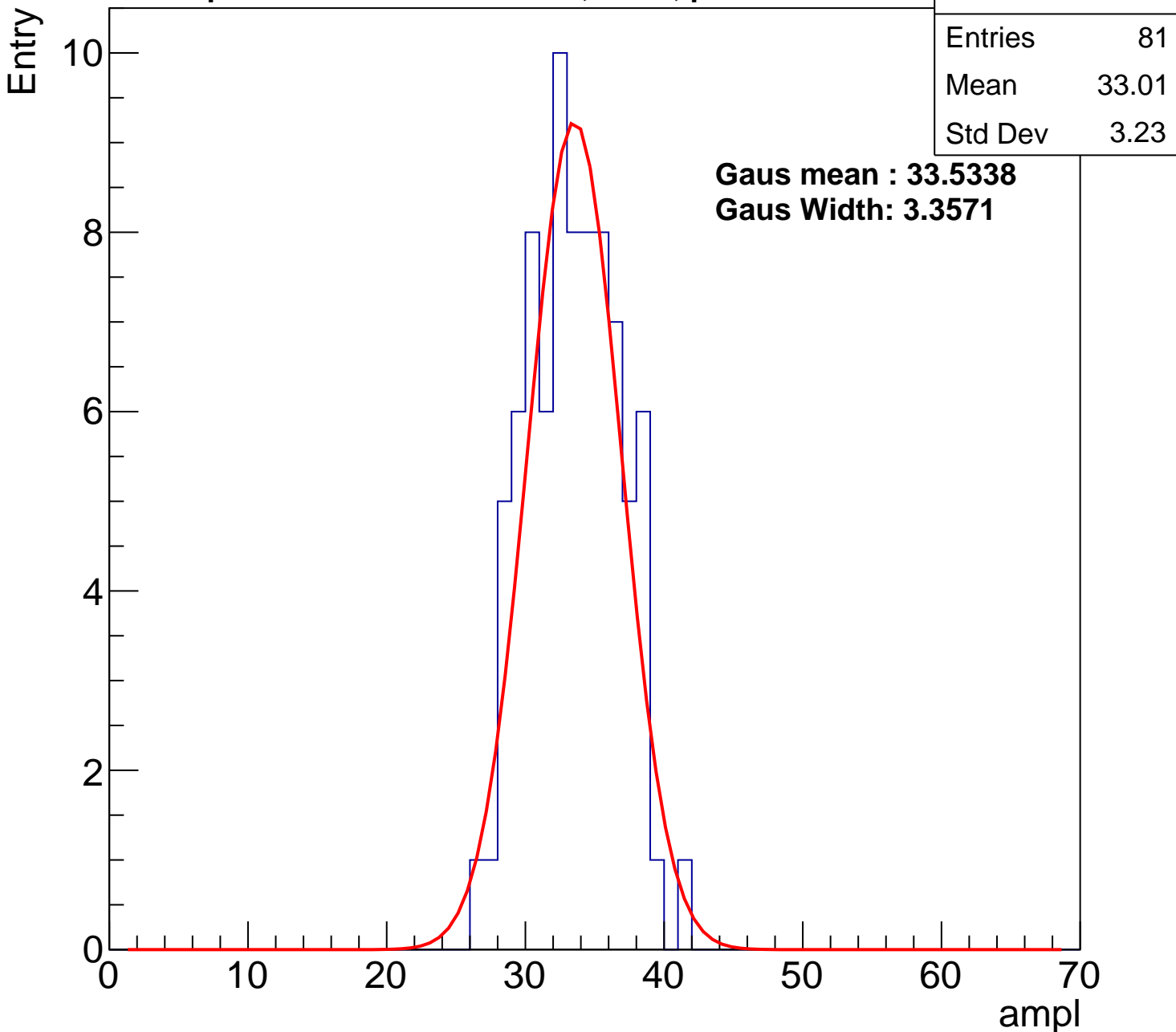
**Gaus Width: 3.3571**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch48, adc2

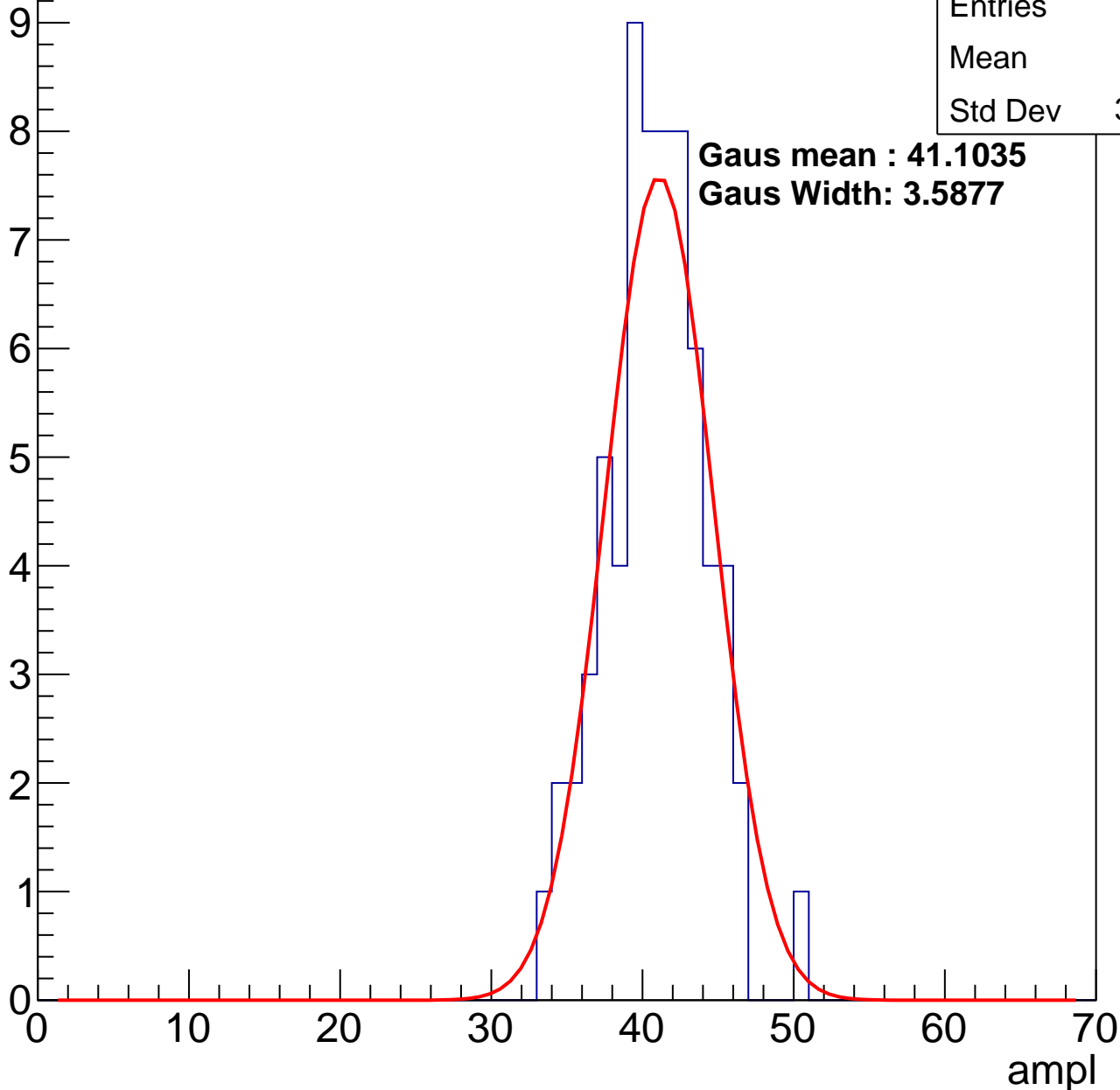
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	40.4
Std Dev	3.251

**Gaus mean : 41.1035**

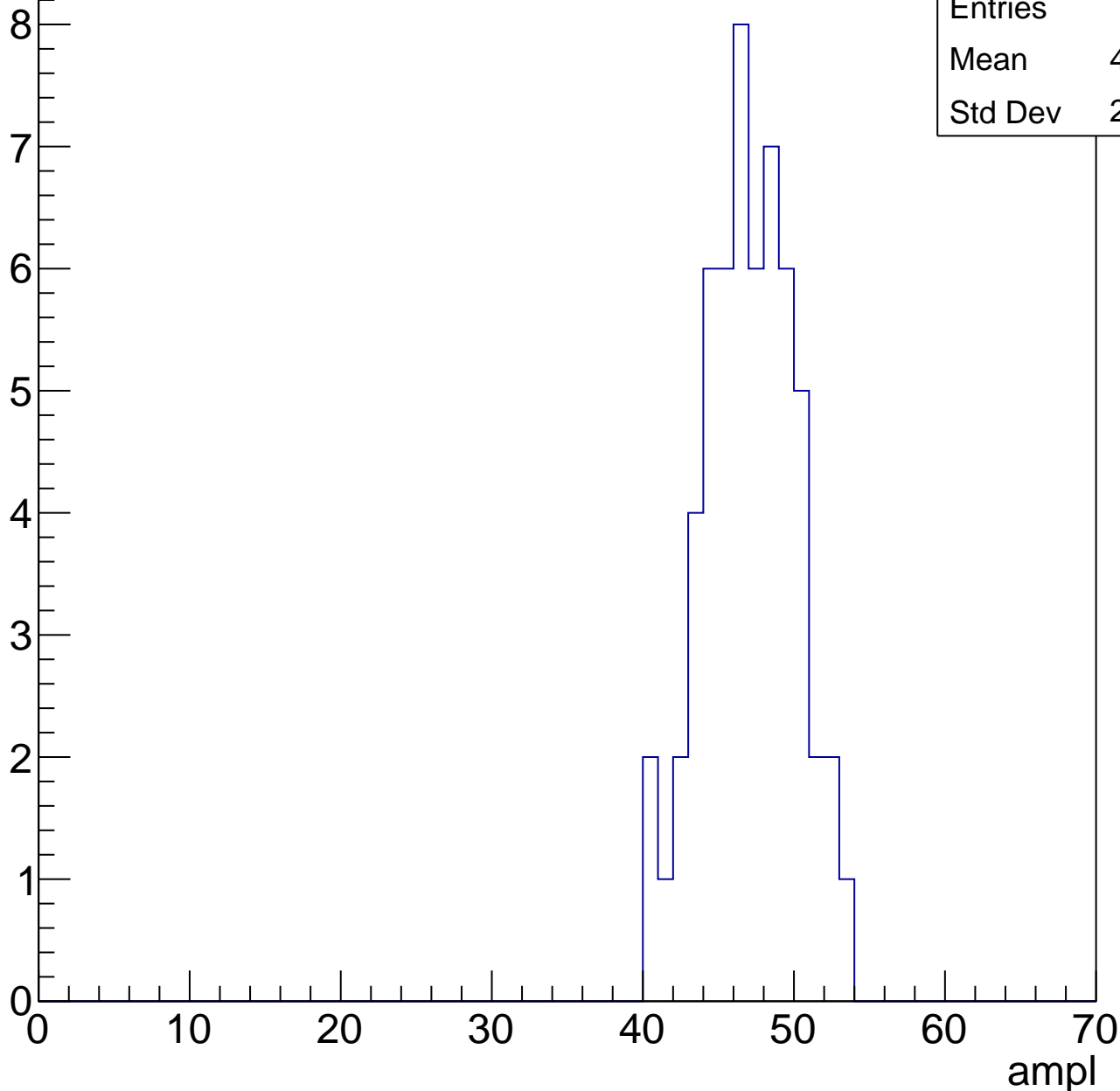
**Gaus Width: 3.5877**



# B1L000S, U2-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



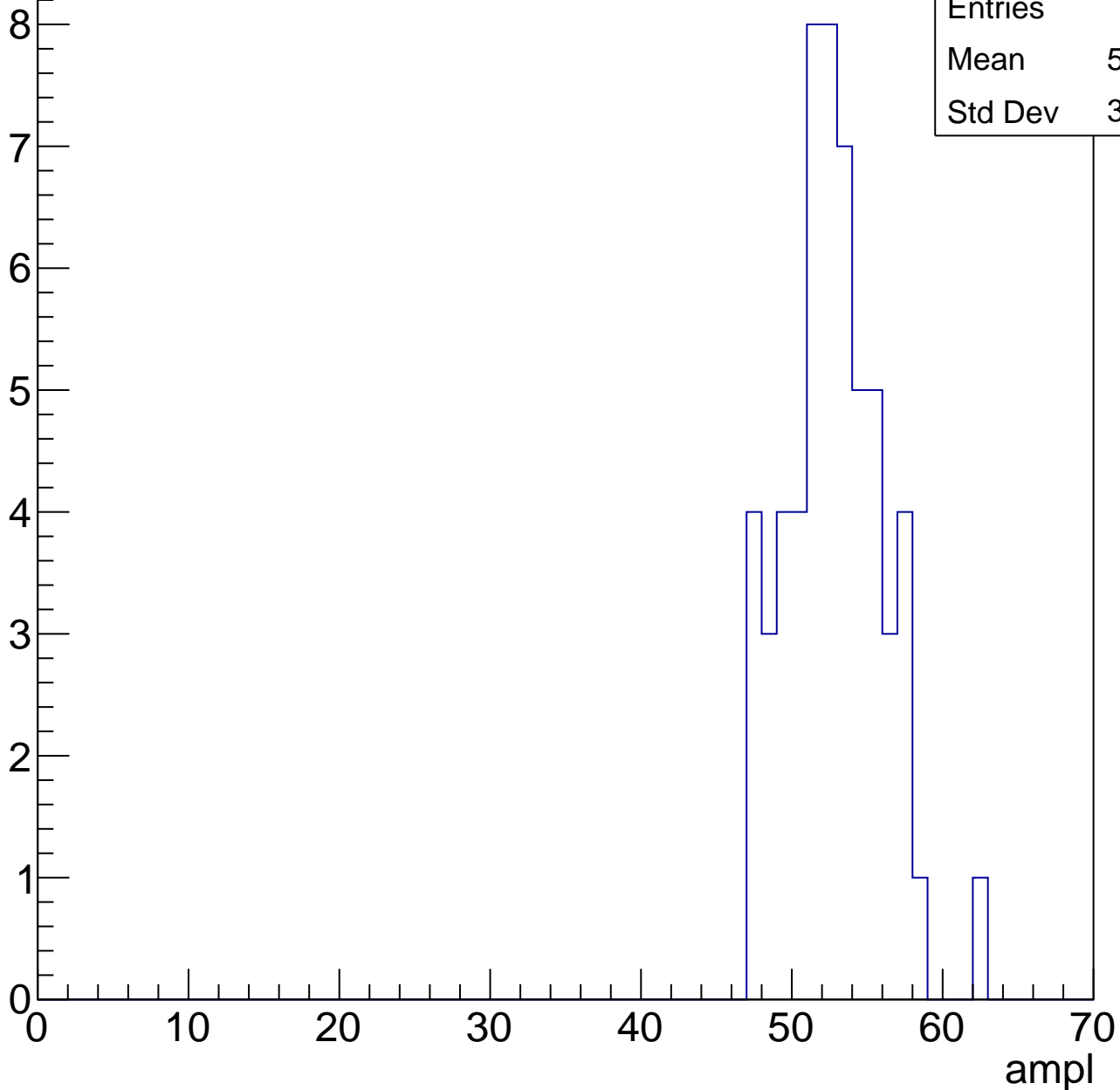
Entries	58
Mean	46.55
Std Dev	2.984

# B1L000S, U2-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

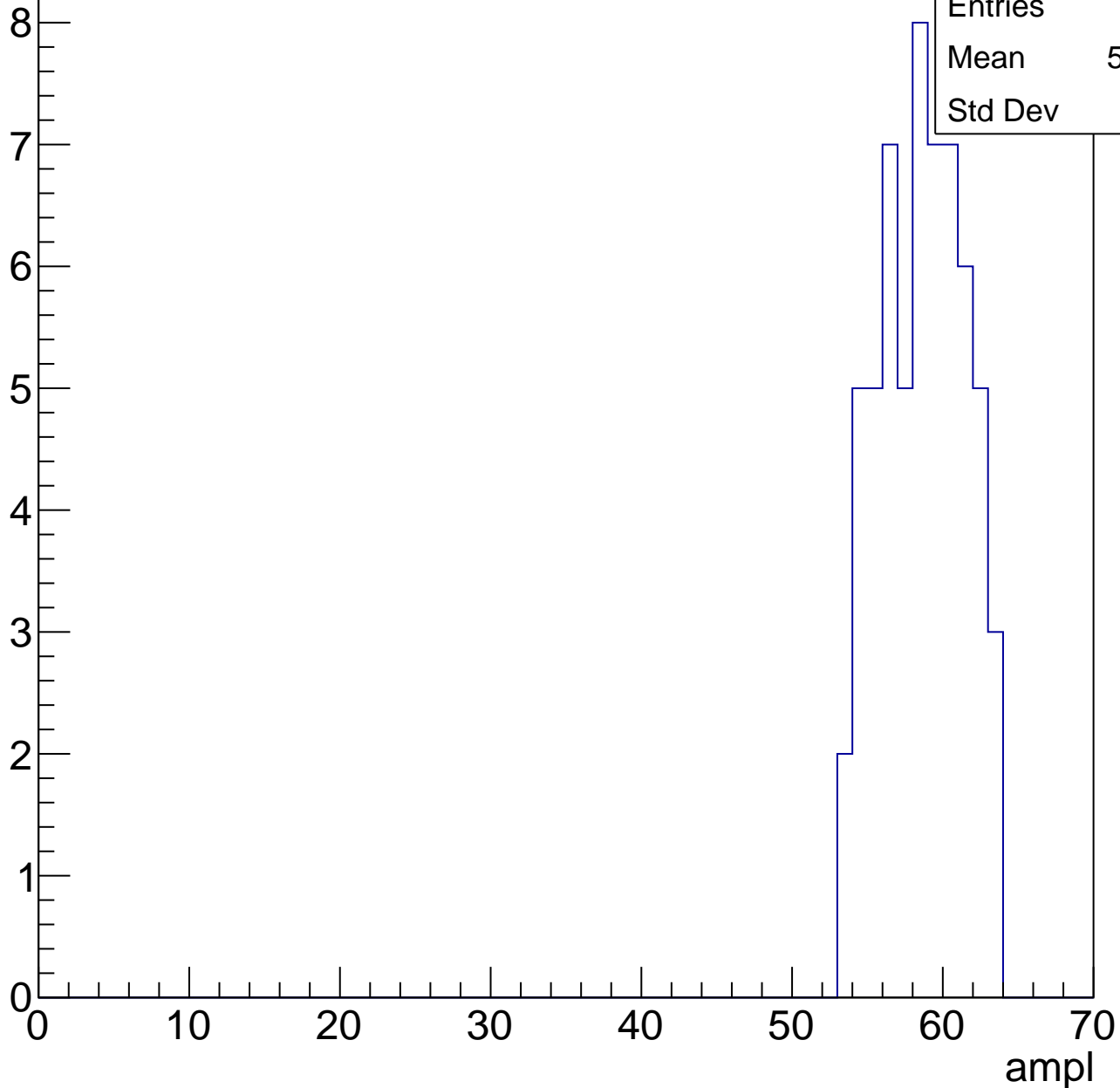
Entries	57
Mean	52.35
Std Dev	3.126



# B1L000S, U2-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

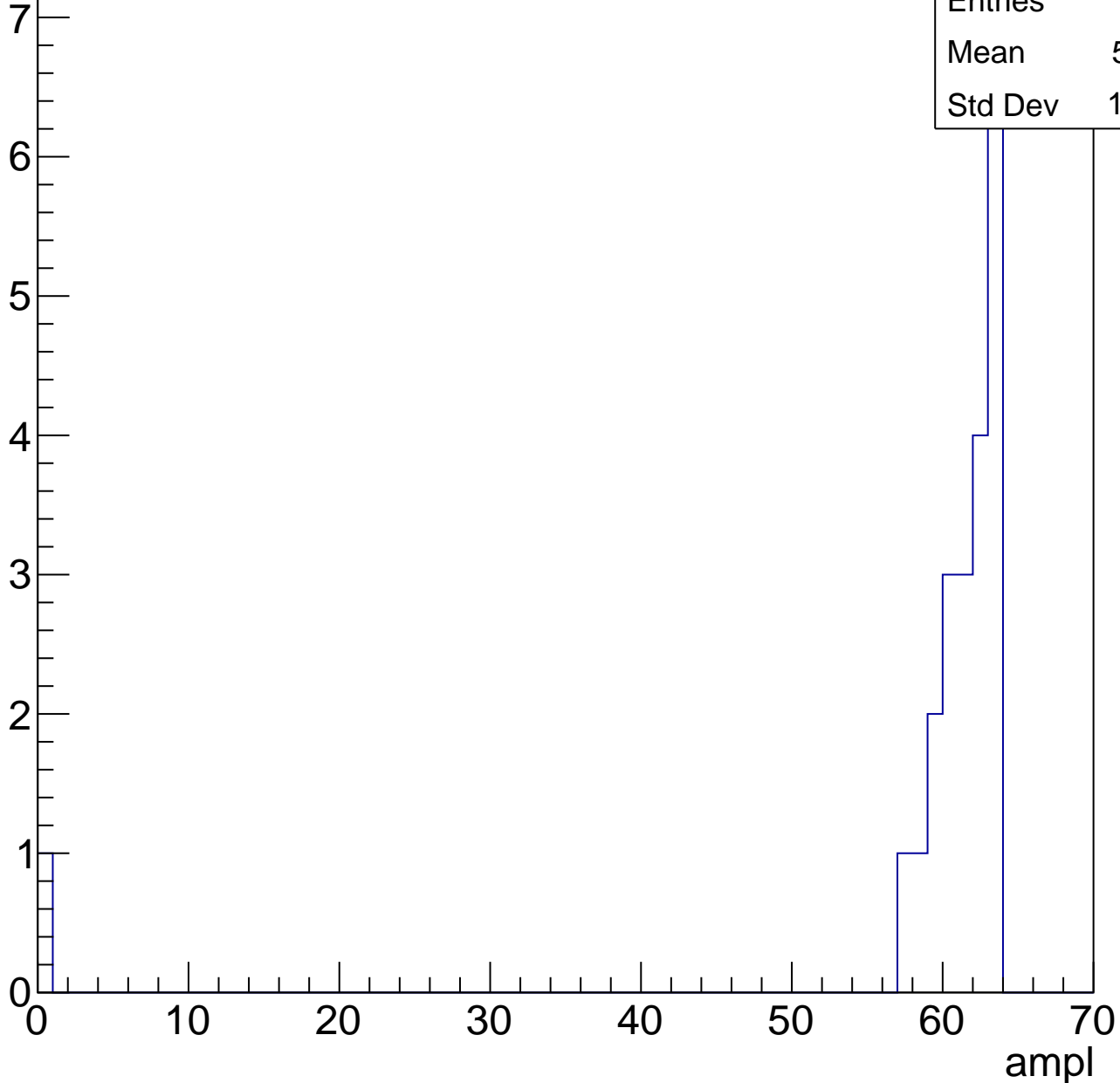


# B1L000S, U2-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.41
Std Dev	12.87

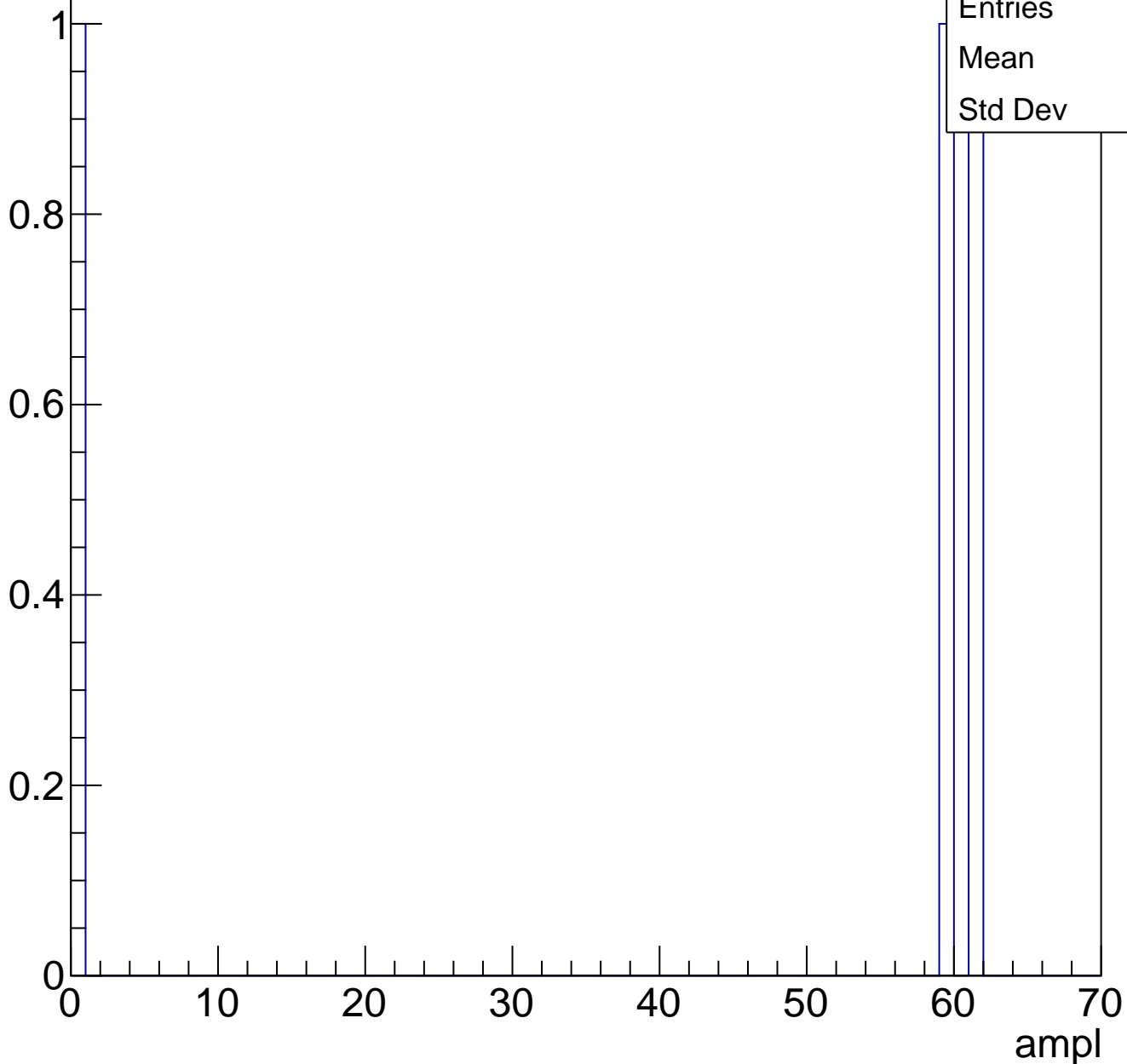




# B1L000S, U2-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch49, adc0

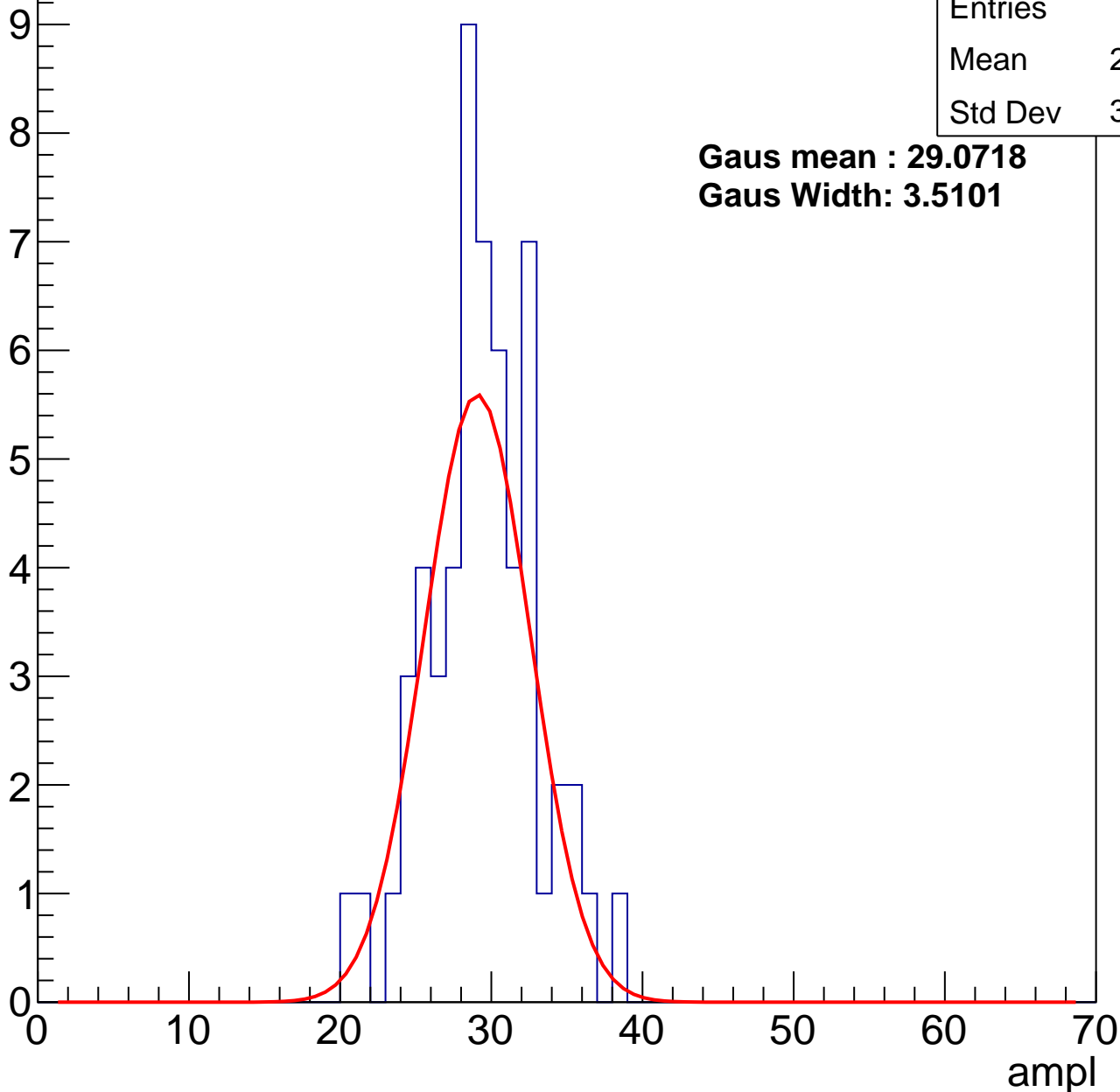
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28.95
Std Dev	3.566

**Gaus mean : 29.0718**

**Gaus Width: 3.5101**



# B1L000S, U2-ch49, adc1

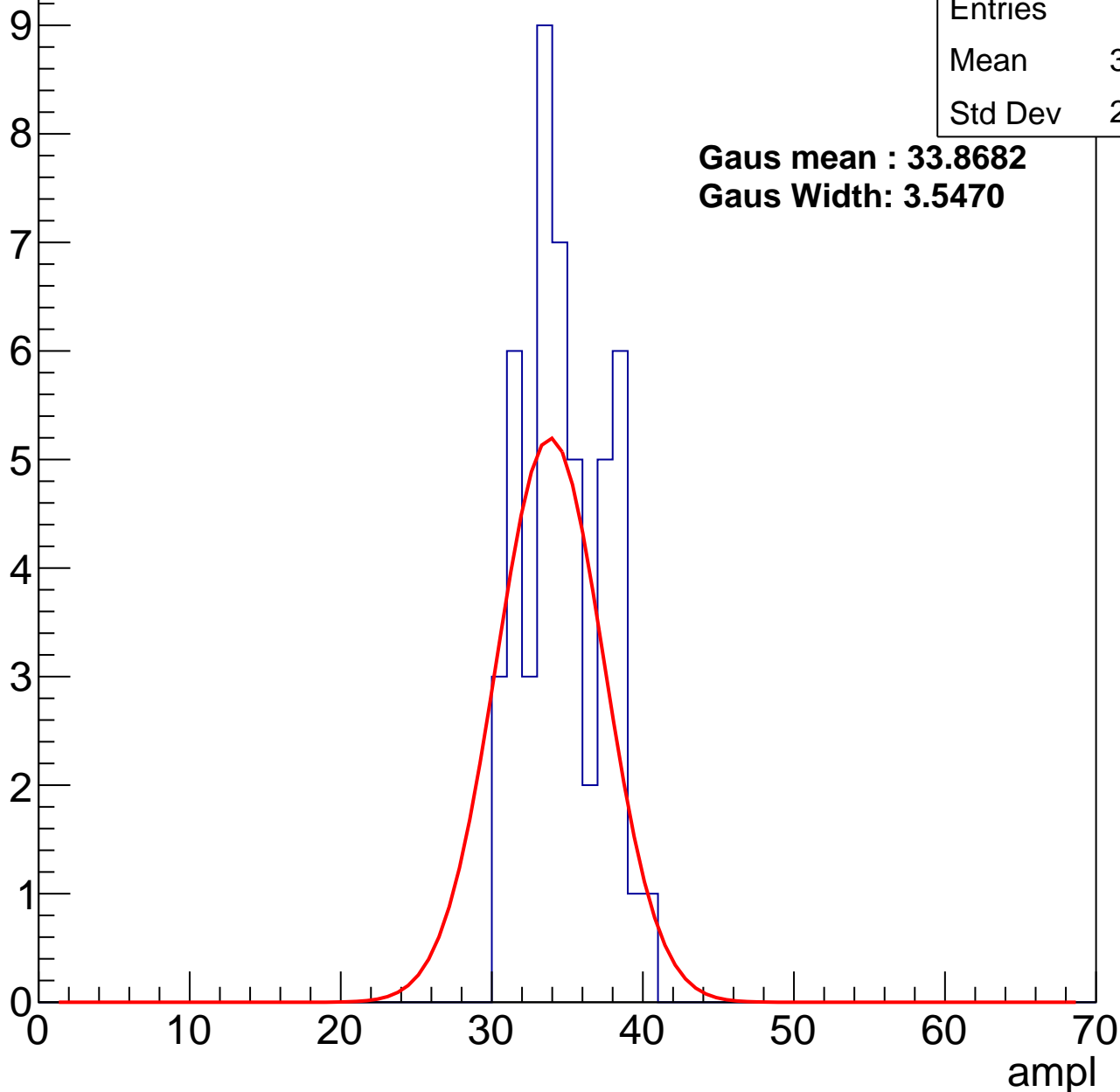
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	34.29
Std Dev	2.638

**Gaus mean : 33.8682**

**Gaus Width: 3.5470**



# B1L000S, U2-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	40.61
Std Dev	3.358

**Gaus mean : 40.7999**

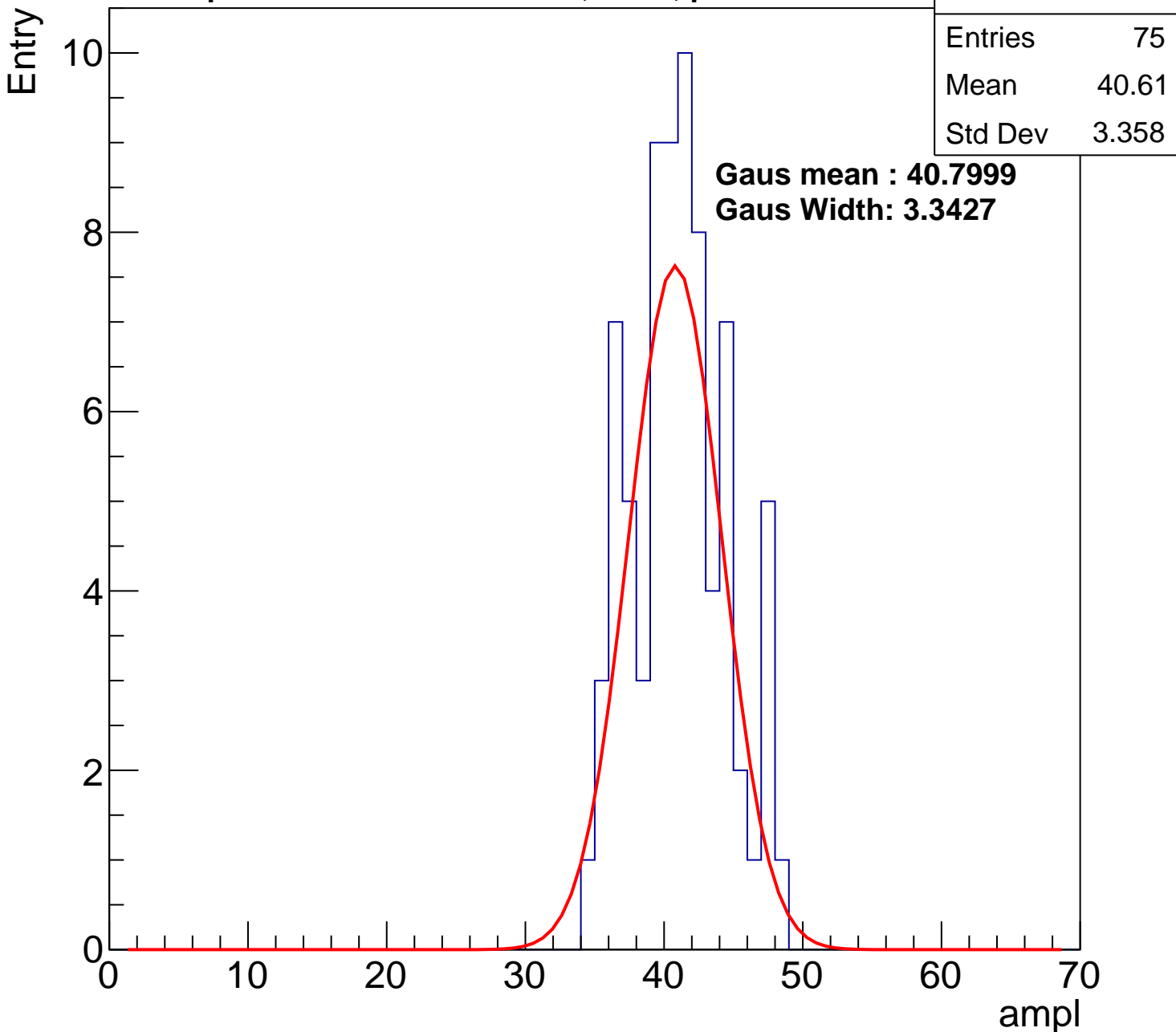
**Gaus Width: 3.3427**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

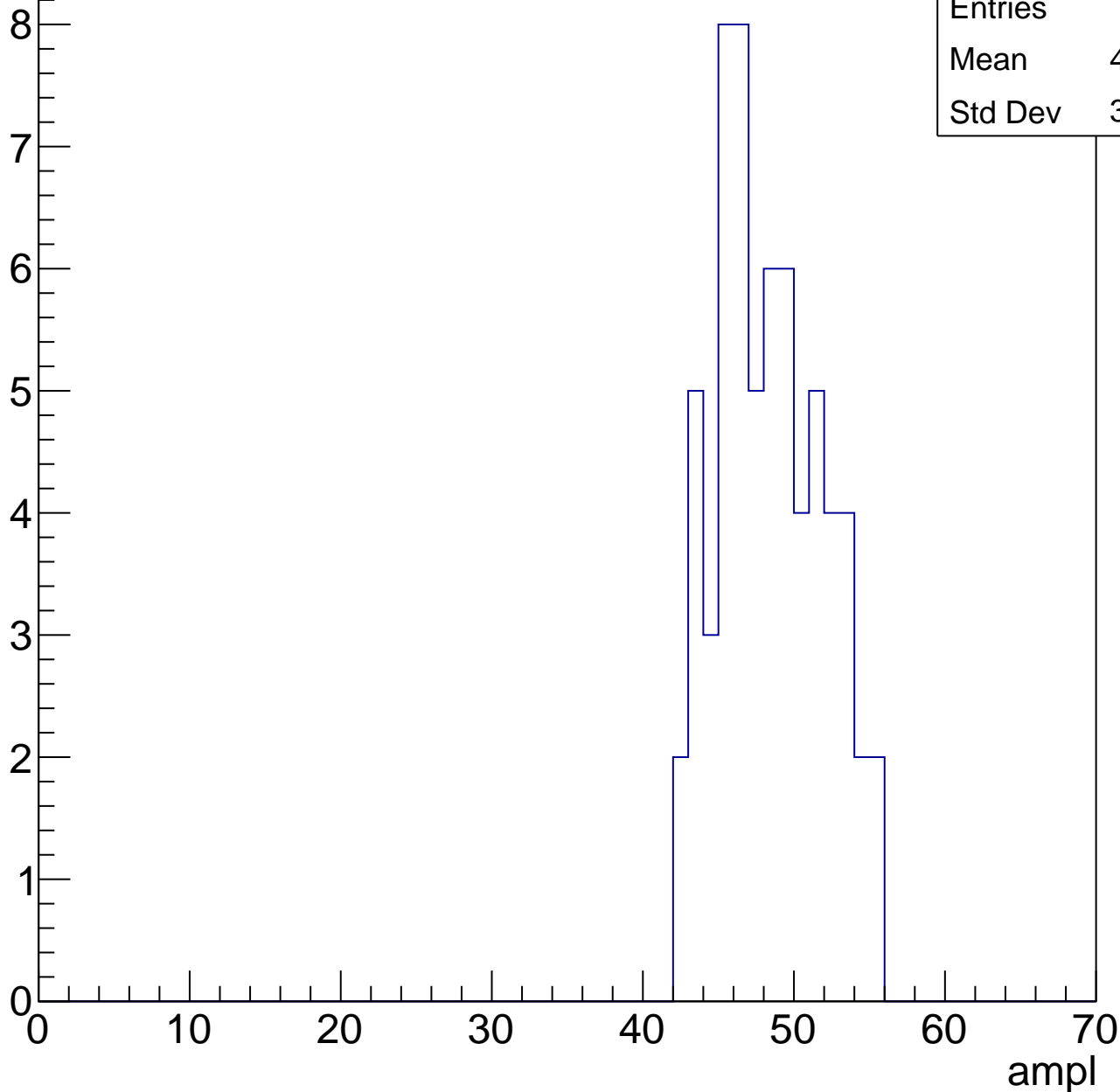


# B1L000S, U2-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	47.95
Std Dev	3.434

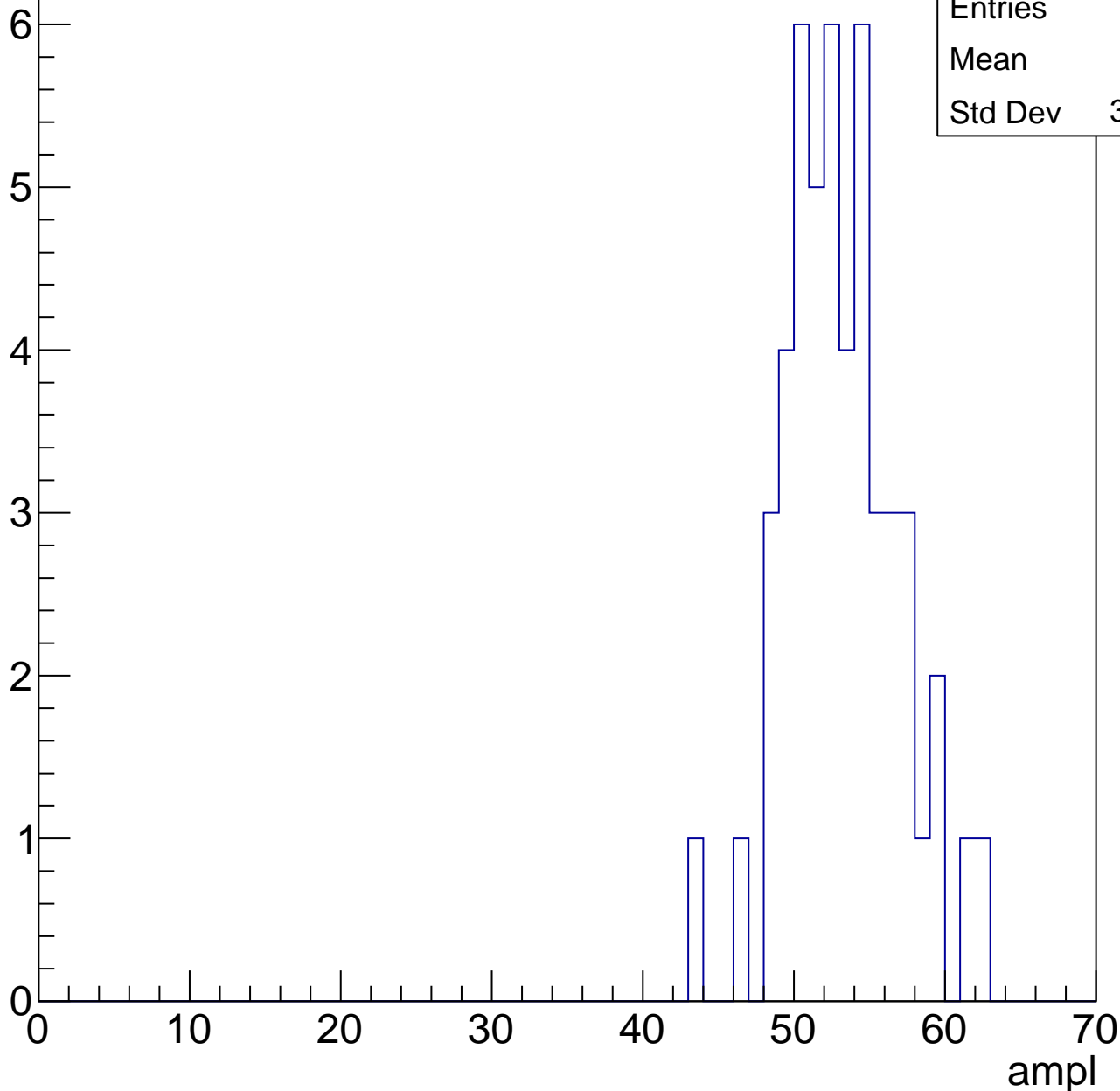


# B1L000S, U2-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	52.7
Std Dev	3.738

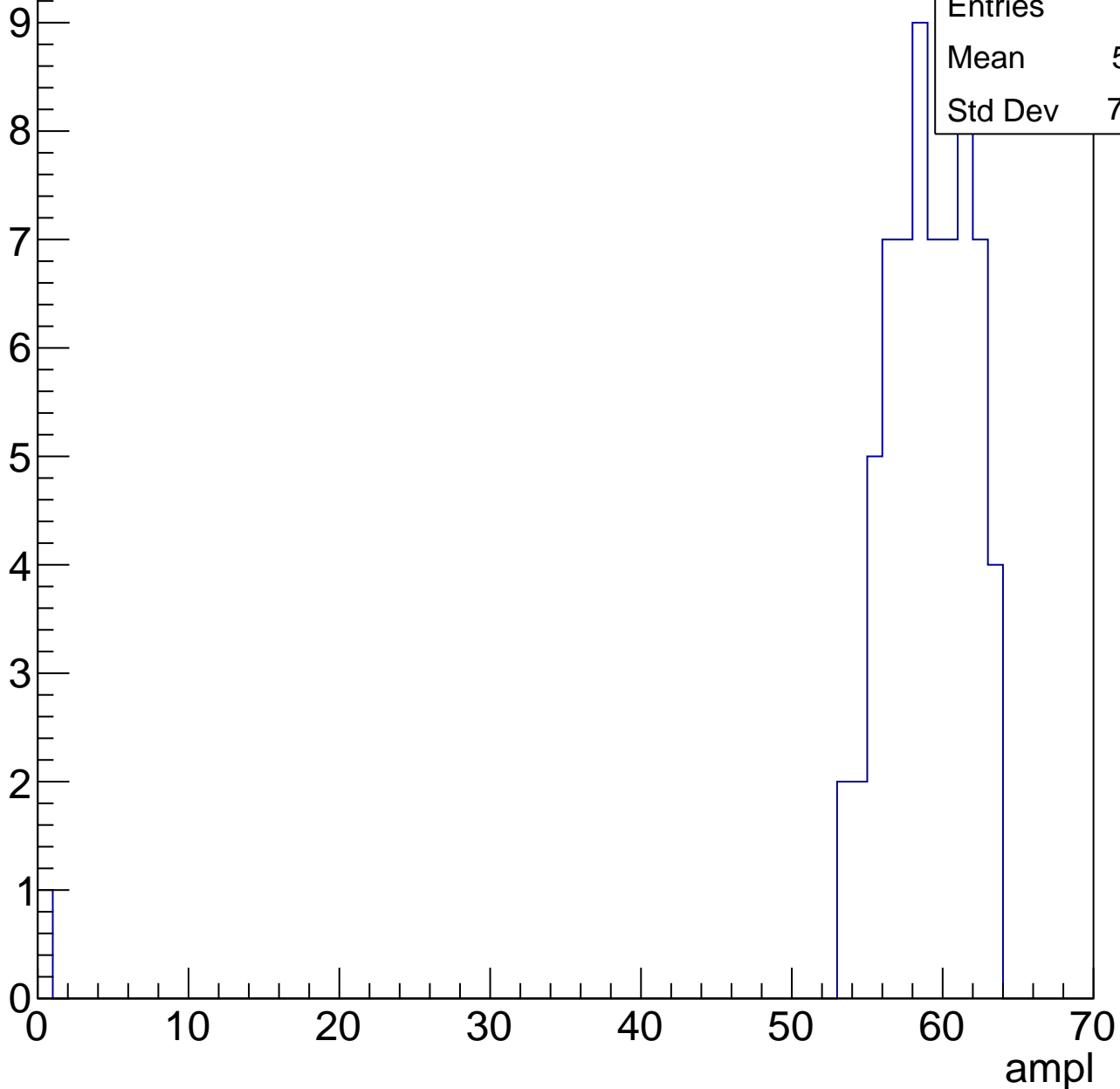


# B1L000S, U2-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

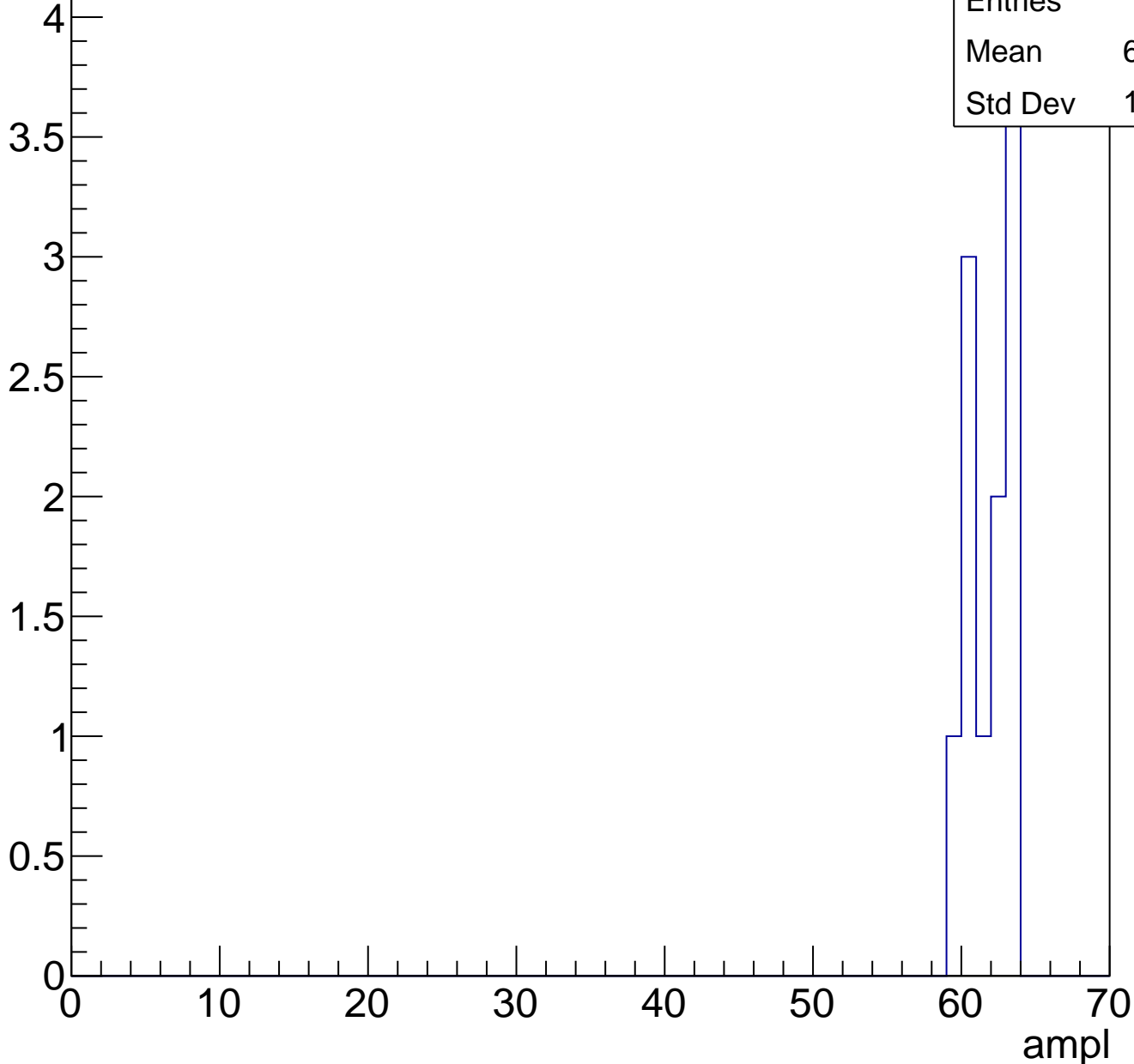
Entries	66
Mean	57.71
Std Dev	7.627



# B1L000S, U2-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch50, adc0

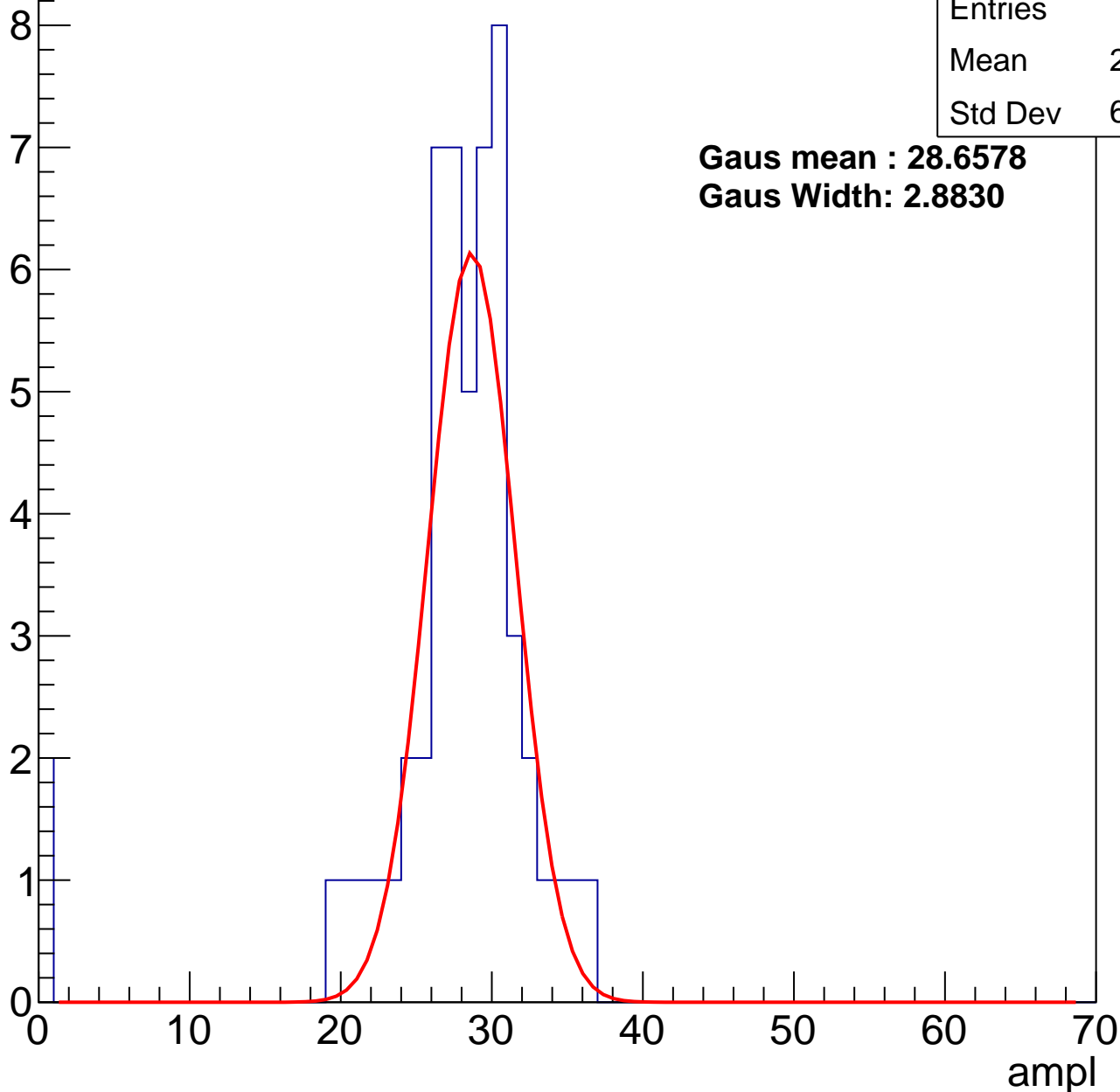
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	26.89
Std Dev	6.256

**Gaus mean : 28.6578**

**Gaus Width: 2.8830**



# B1L000S, U2-ch50, adc1

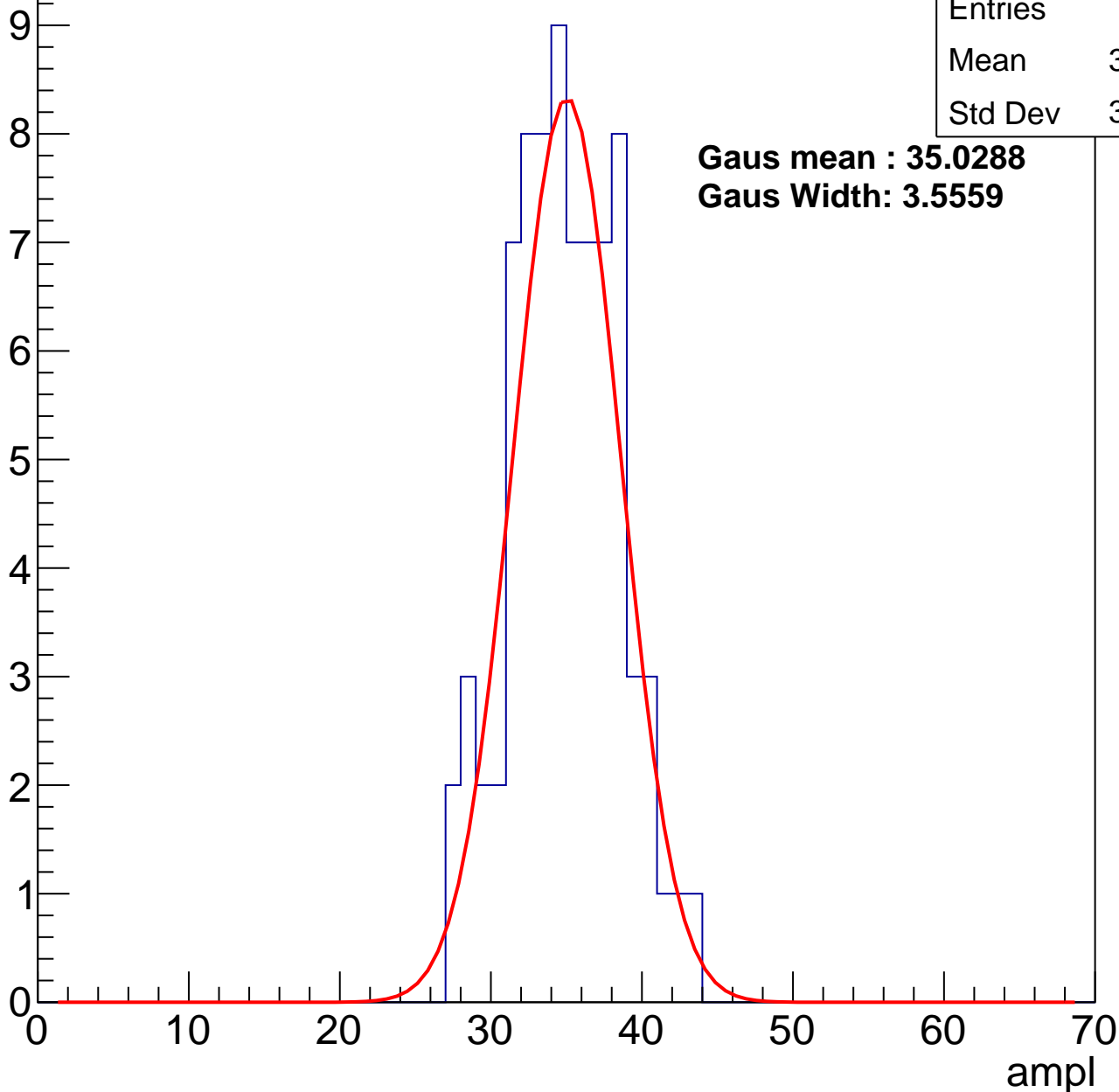
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	34.46
Std Dev	3.518

**Gaus mean : 35.0288**

**Gaus Width: 3.5559**



# B1L000S, U2-ch50, adc2

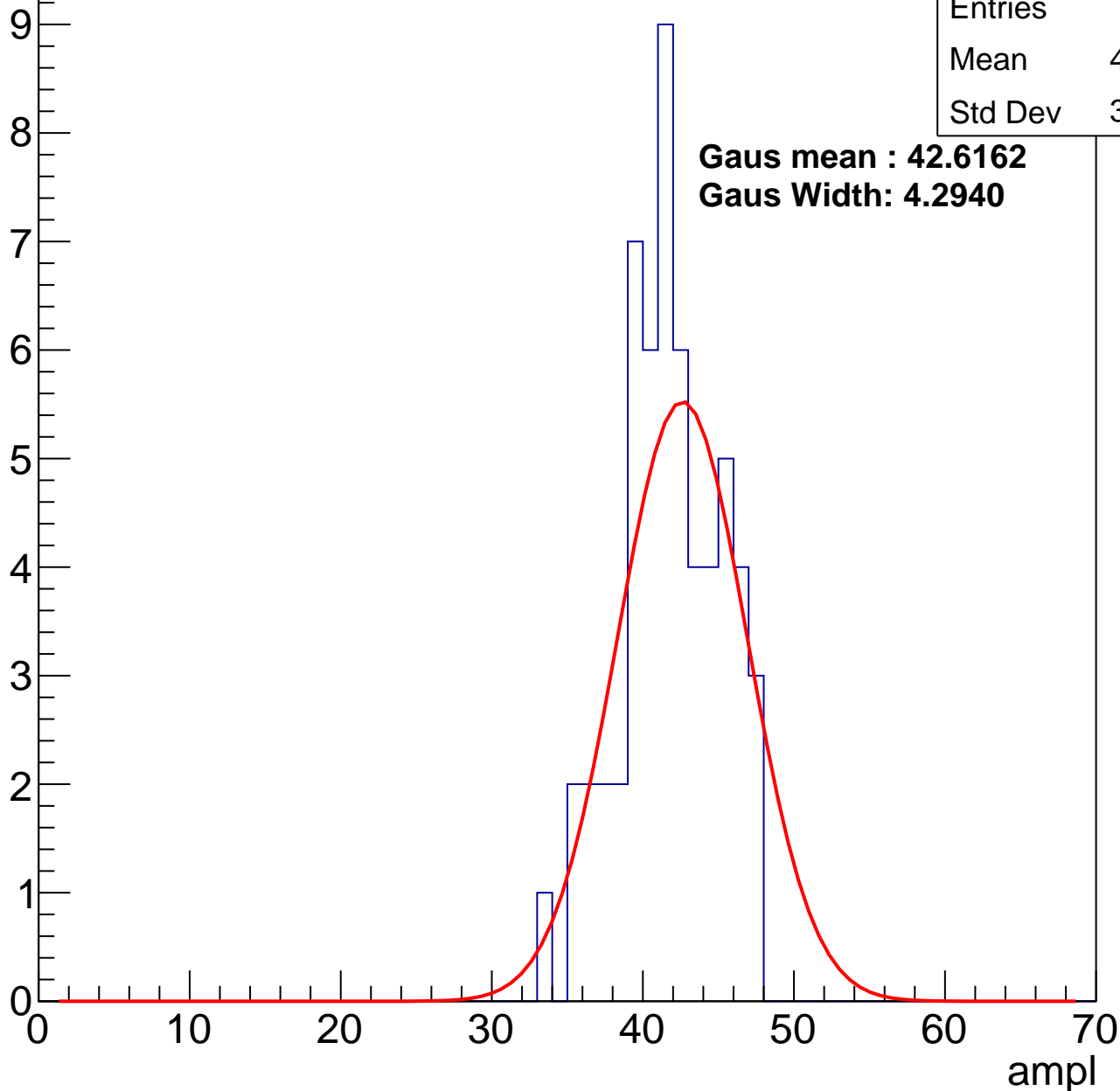
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.35
Std Dev	3.269

**Gaus mean : 42.6162**

**Gaus Width: 4.2940**

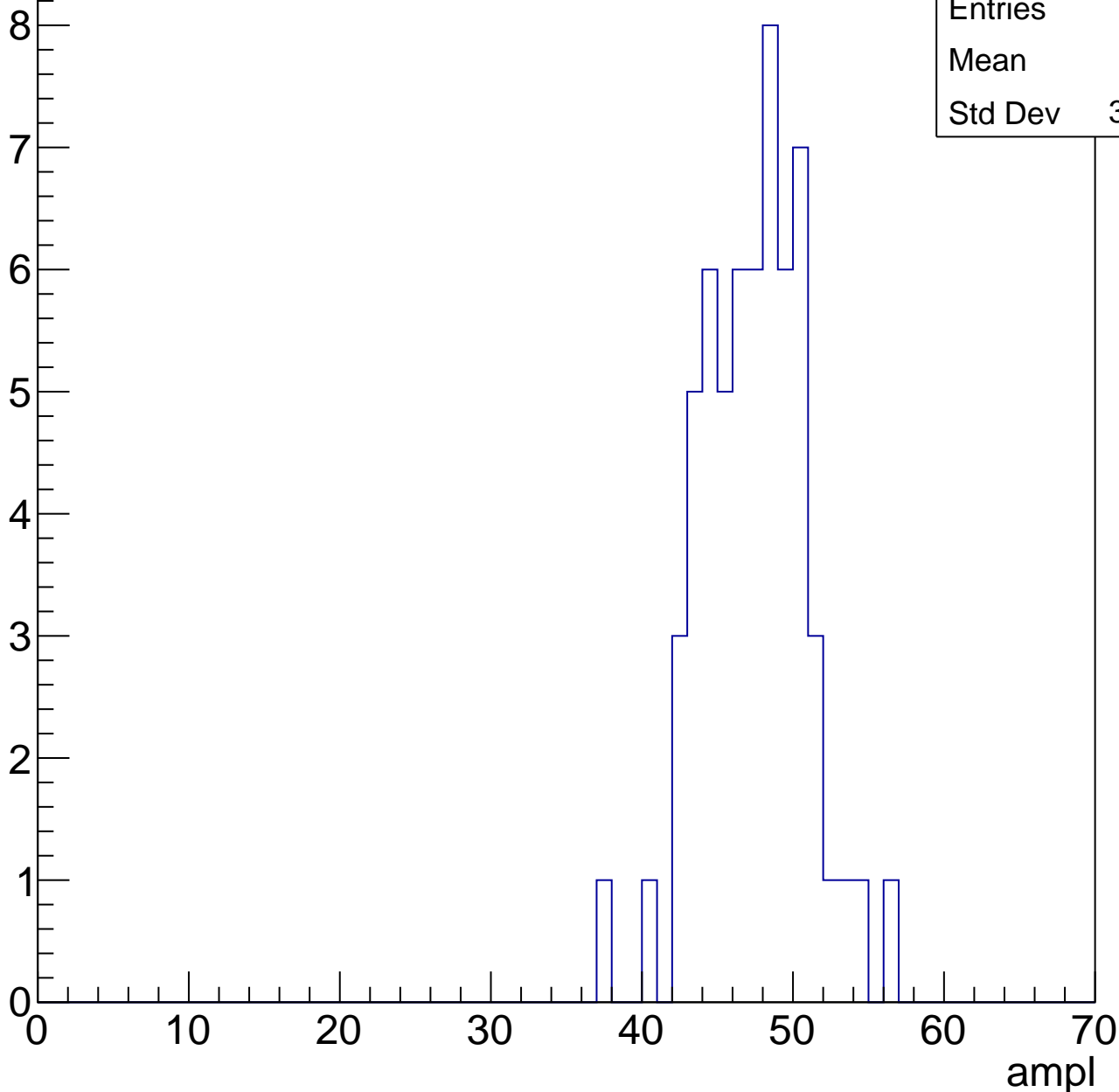


# B1L000S, U2-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	46.9
Std Dev	3.429

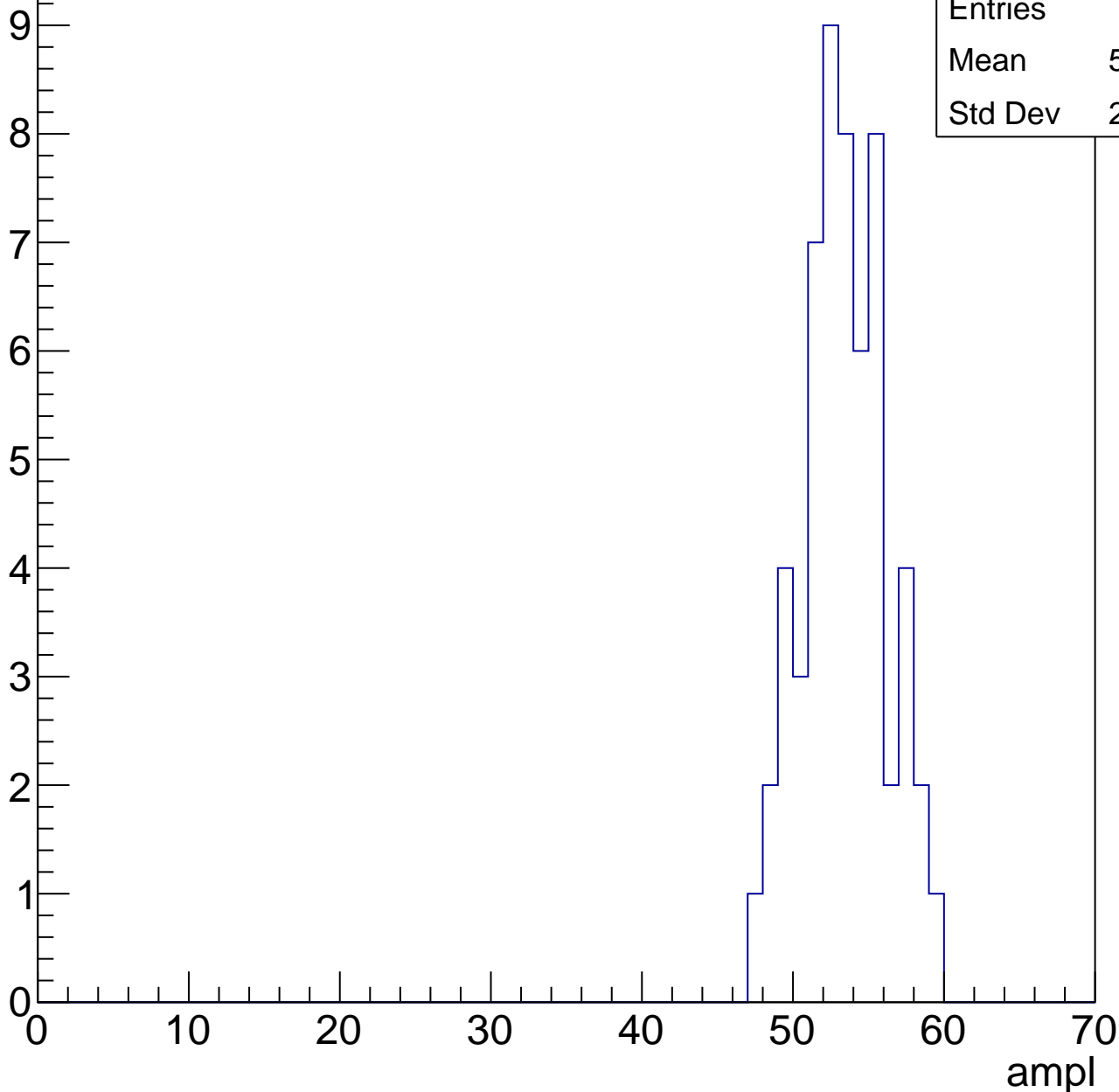


# B1L000S, U2-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	52.93
Std Dev	2.714



# B1L000S, U2-ch50, adc5

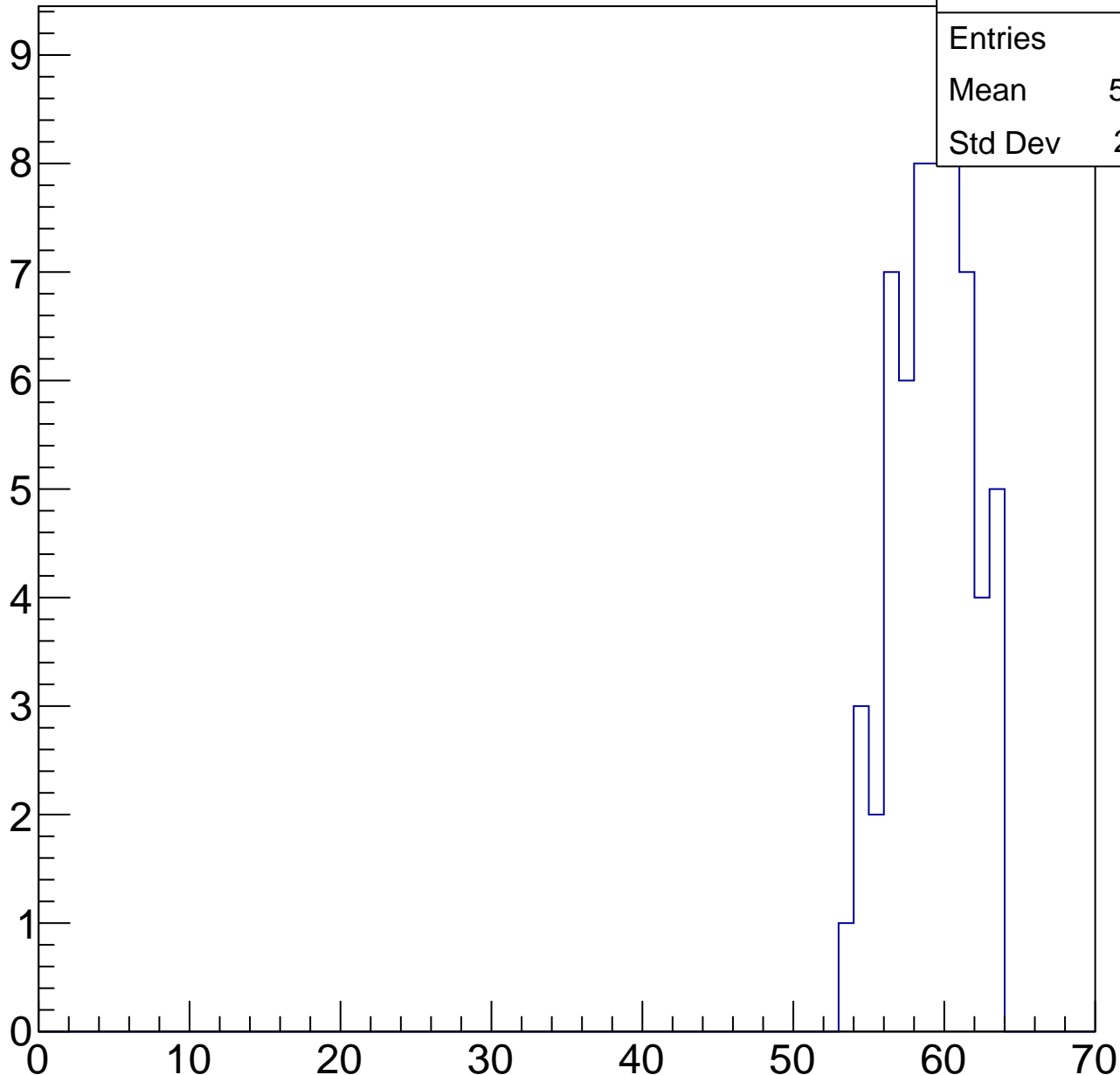
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	58.75
Std Dev	2.541

ampl

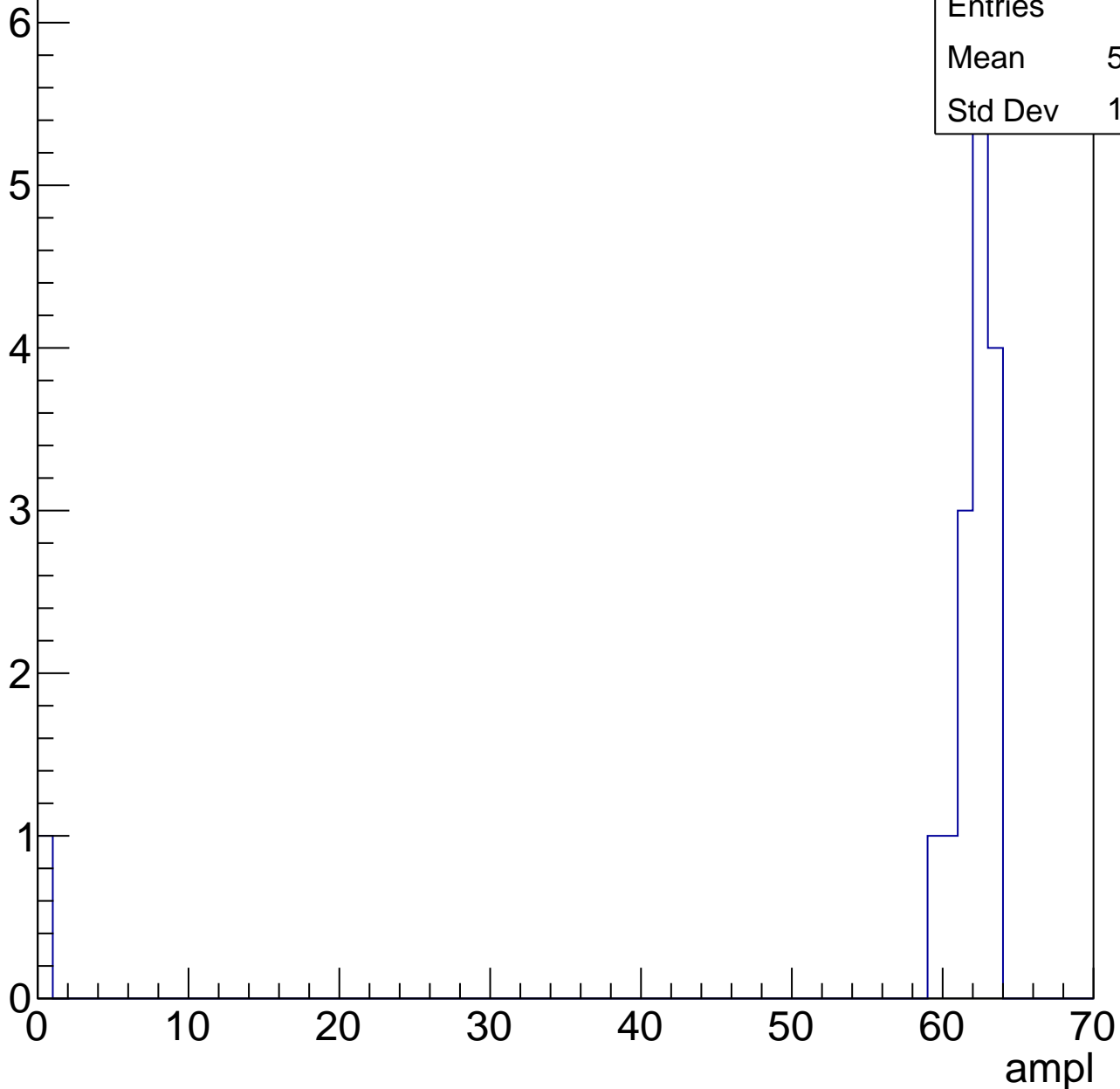


# B1L000S, U2-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.88
Std Dev	14.98





# B1L000S, U2-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U2-ch51, adc0

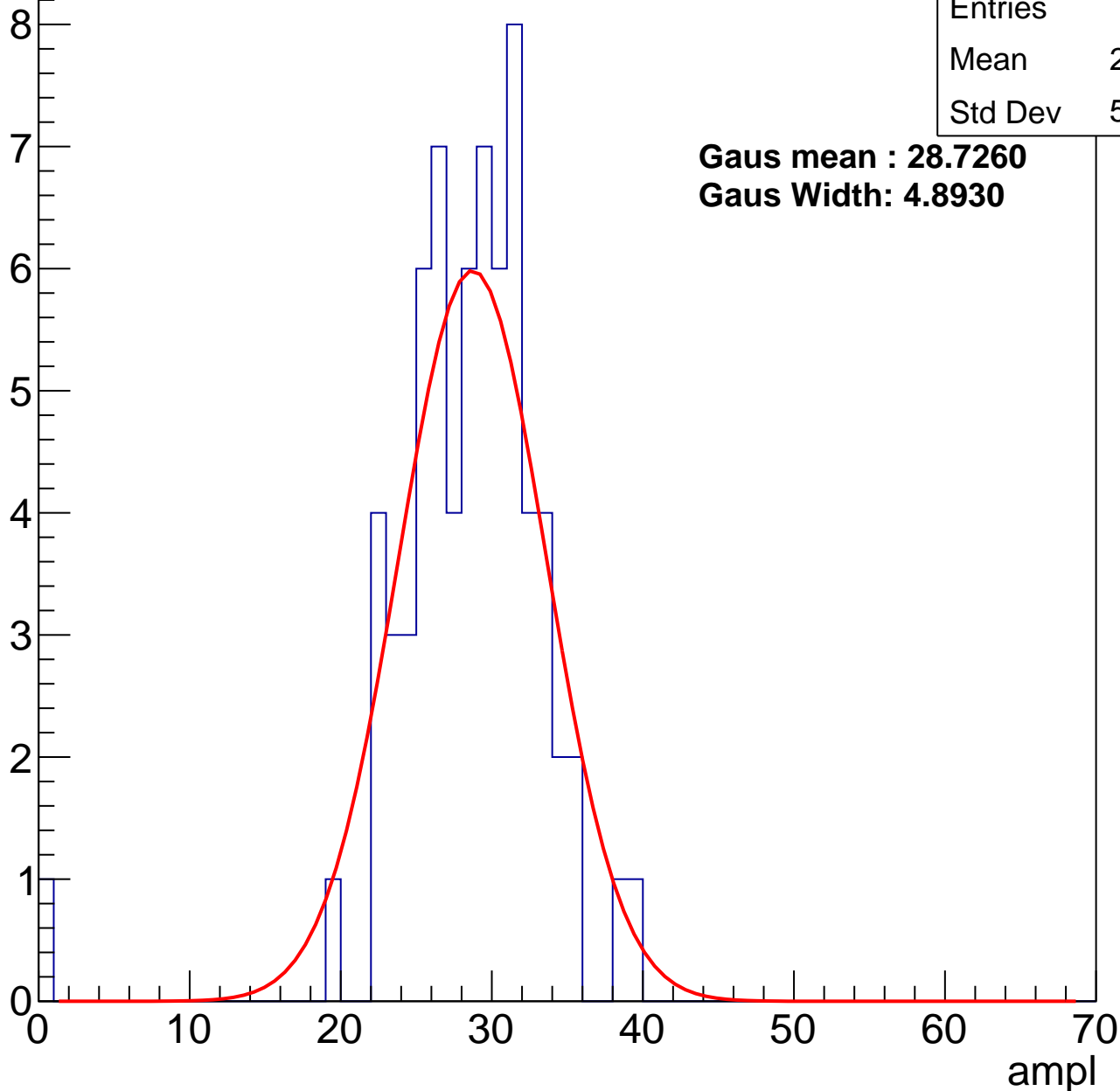
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	28.03
Std Dev	5.177

**Gaus mean : 28.7260**

**Gaus Width: 4.8930**



# B1L000S, U2-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	35.51
Std Dev	3.481

**Gaus mean : 36.6795**

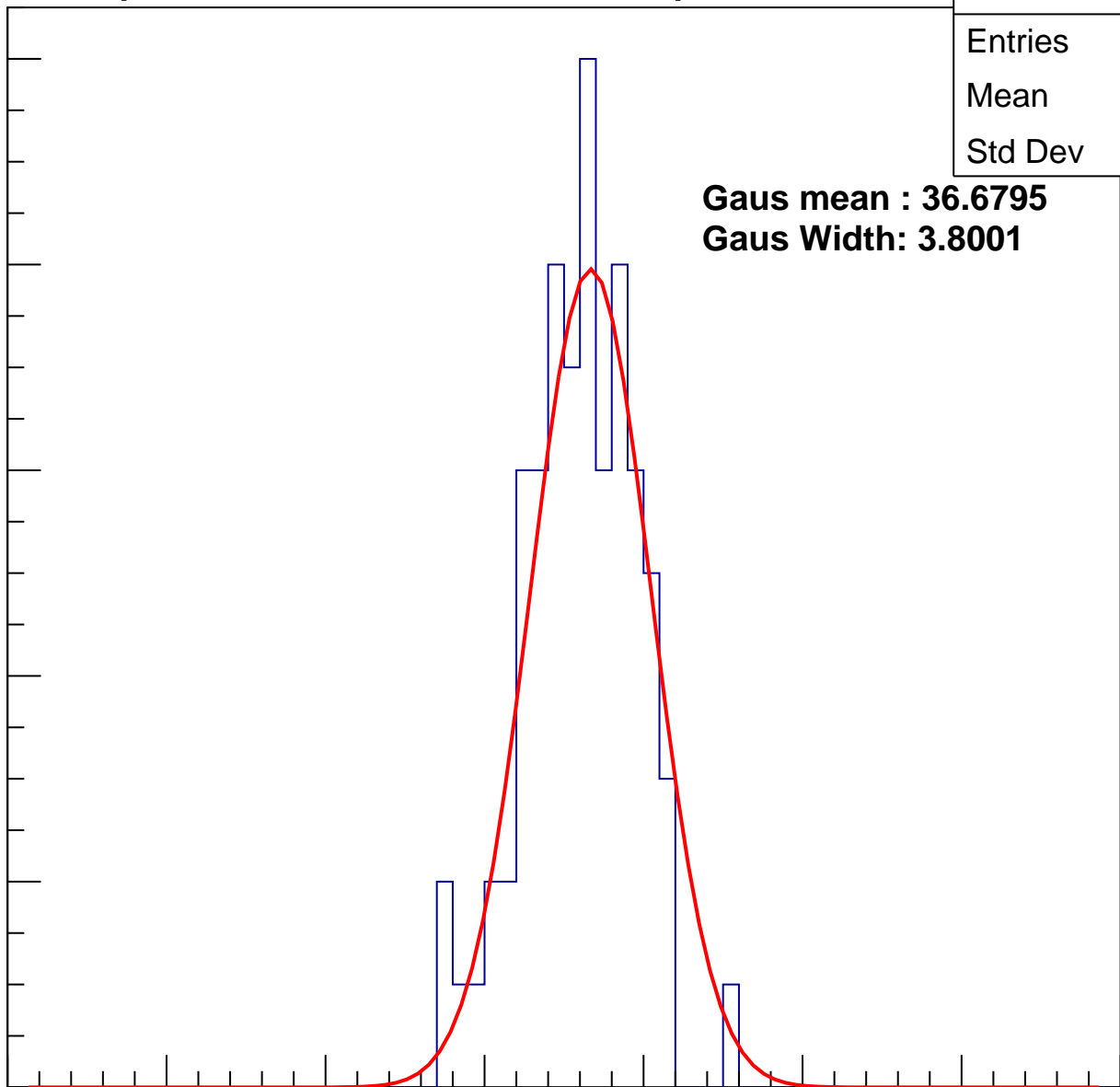
**Gaus Width: 3.8001**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch51, adc2

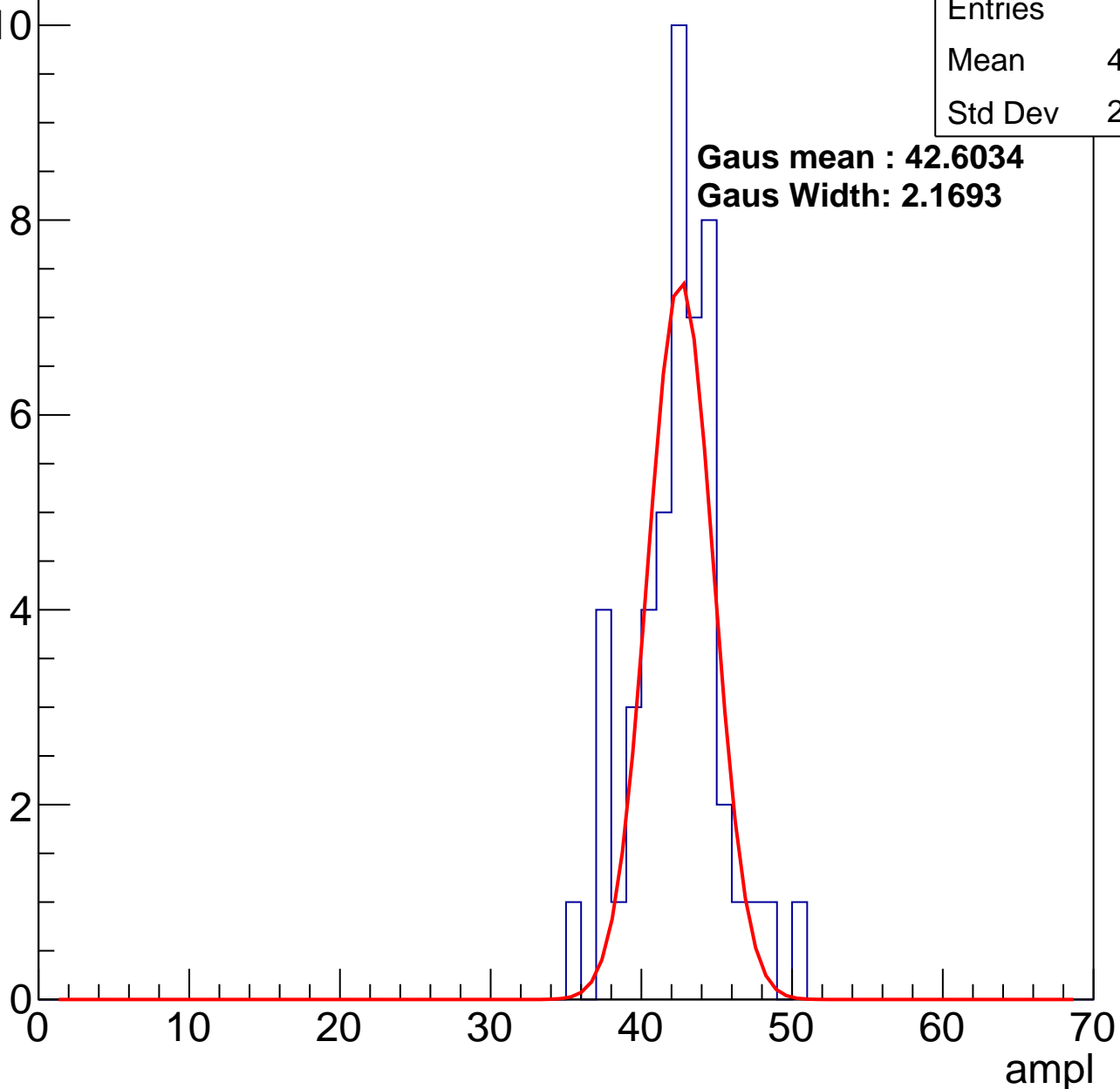
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	41.98
Std Dev	2.896

**Gaus mean : 42.6034**

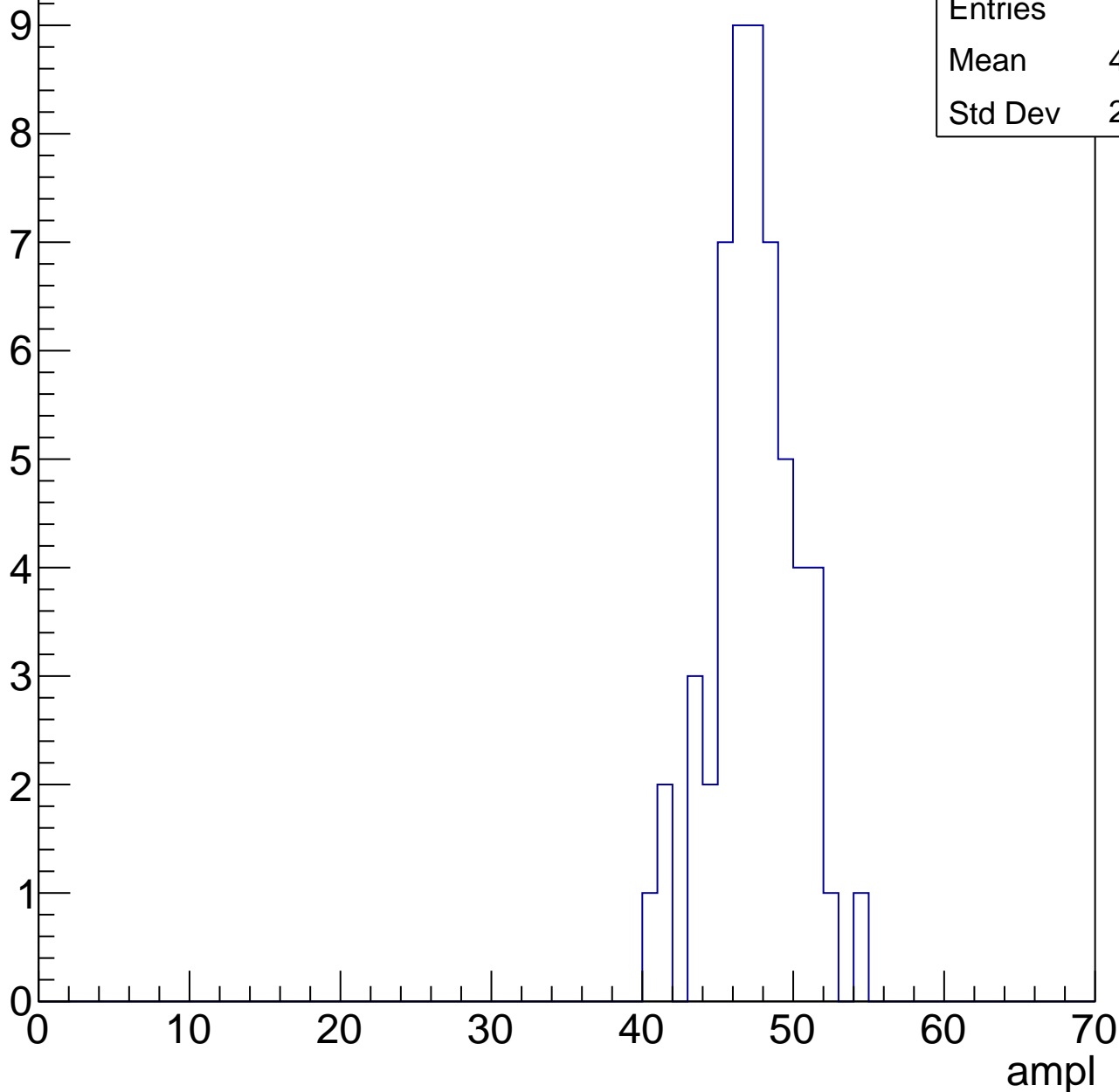
**Gaus Width: 2.1693**



# B1L000S, U2-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

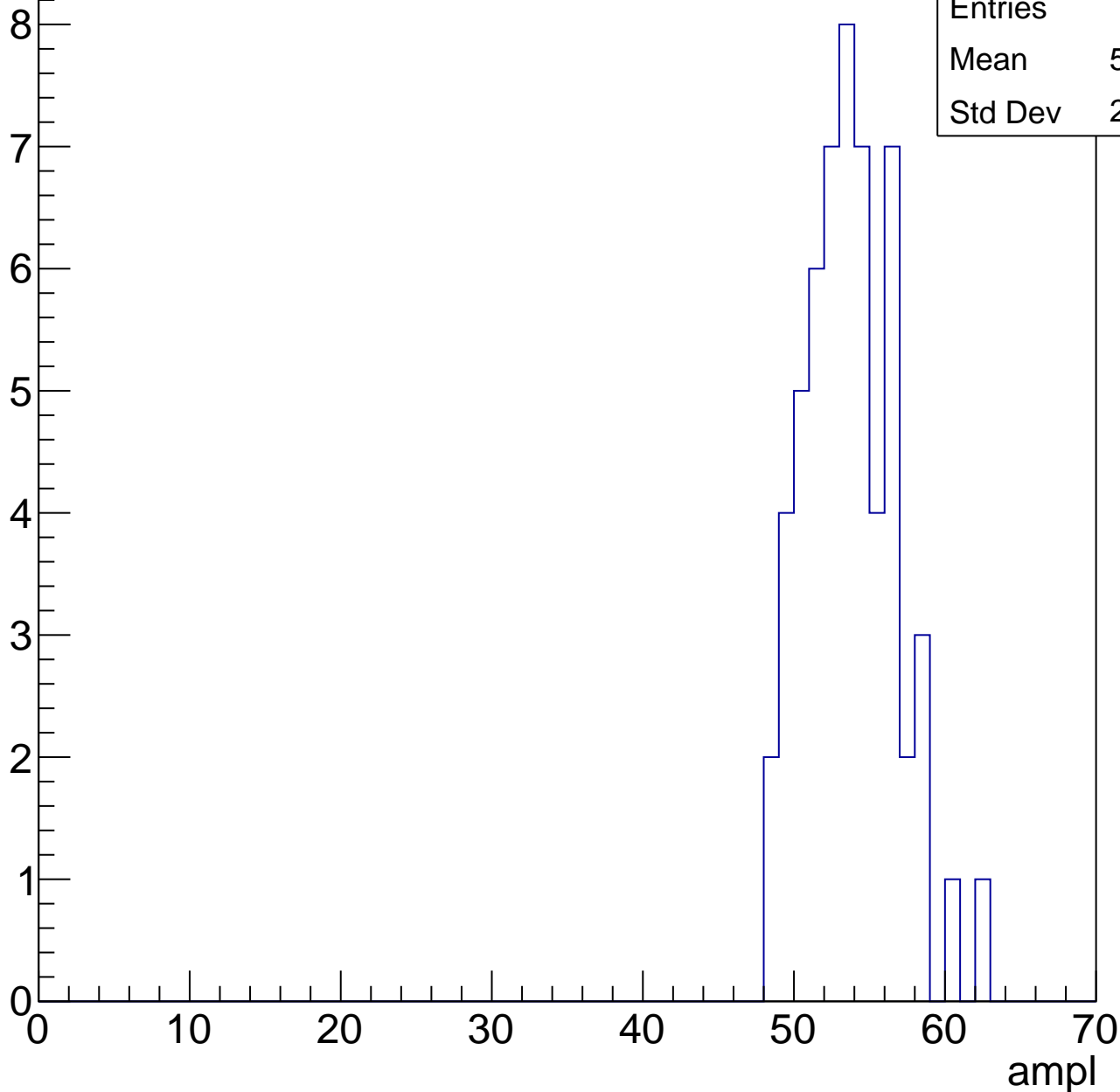


# B1L000S, U2-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

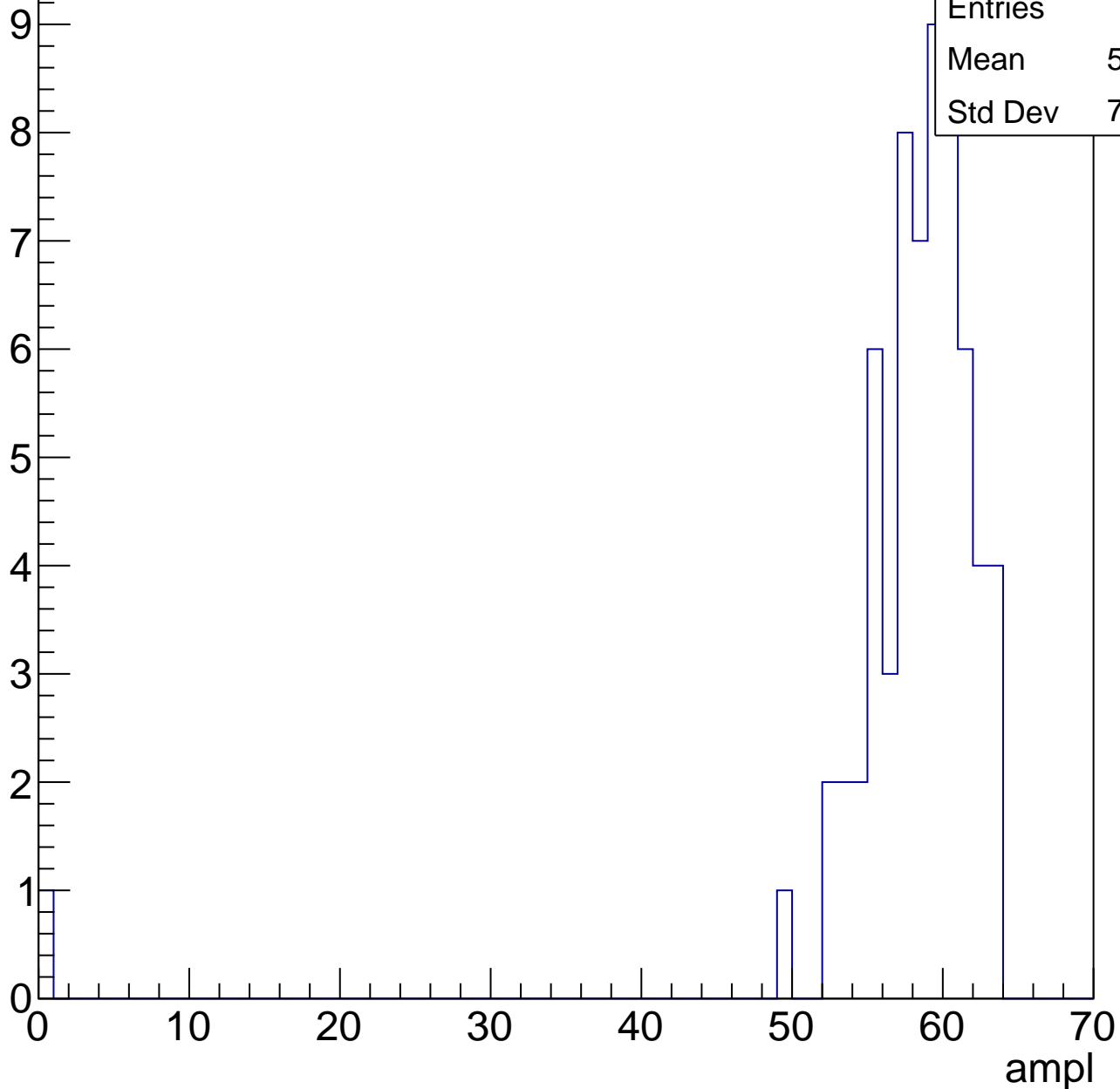
Entries	57
Mean	53.26
Std Dev	2.988



# B1L000S, U2-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

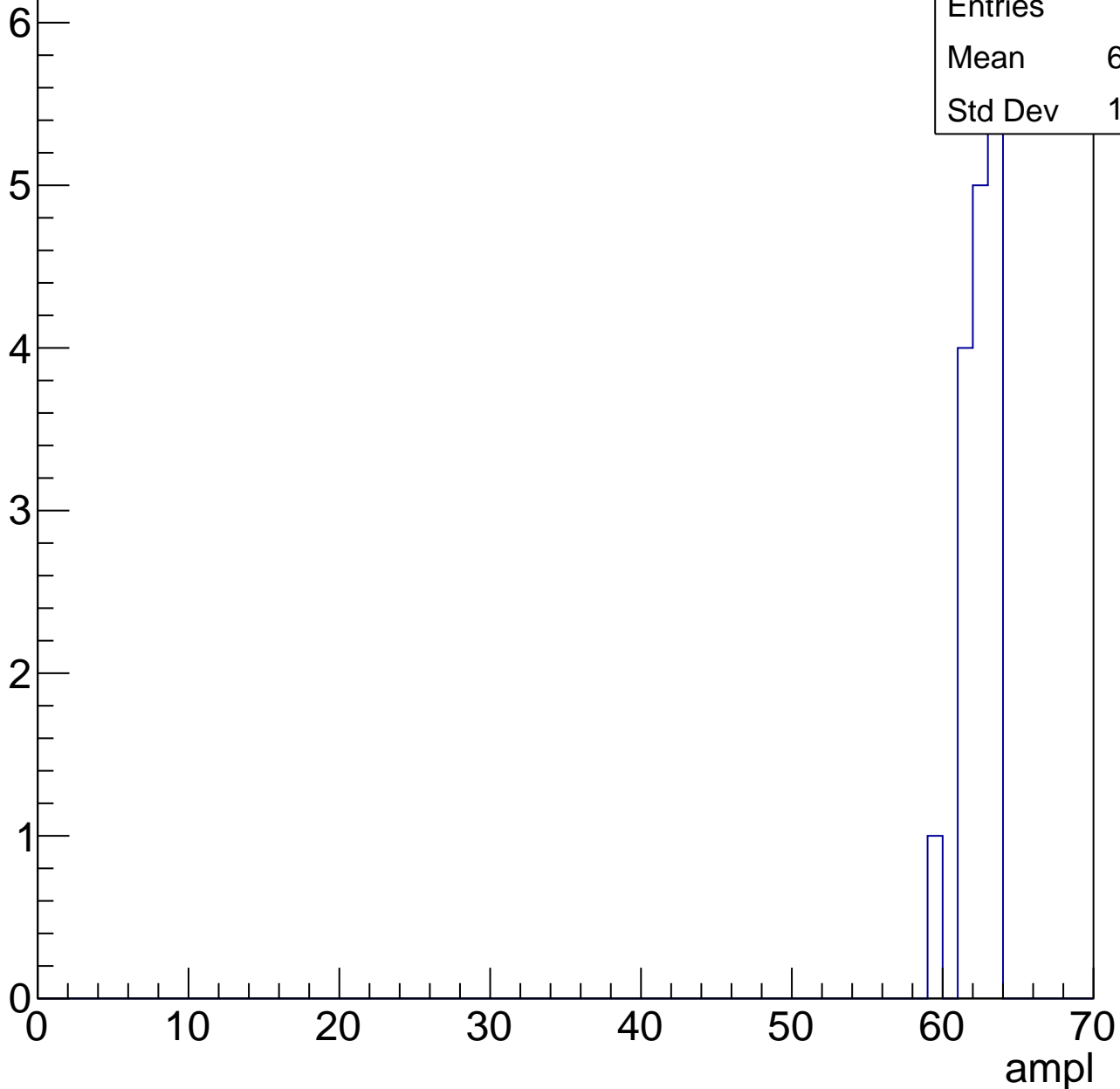


# B1L000S, U2-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.94
Std Dev	1.088





# B1L000S, U2-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U2-ch52, adc0

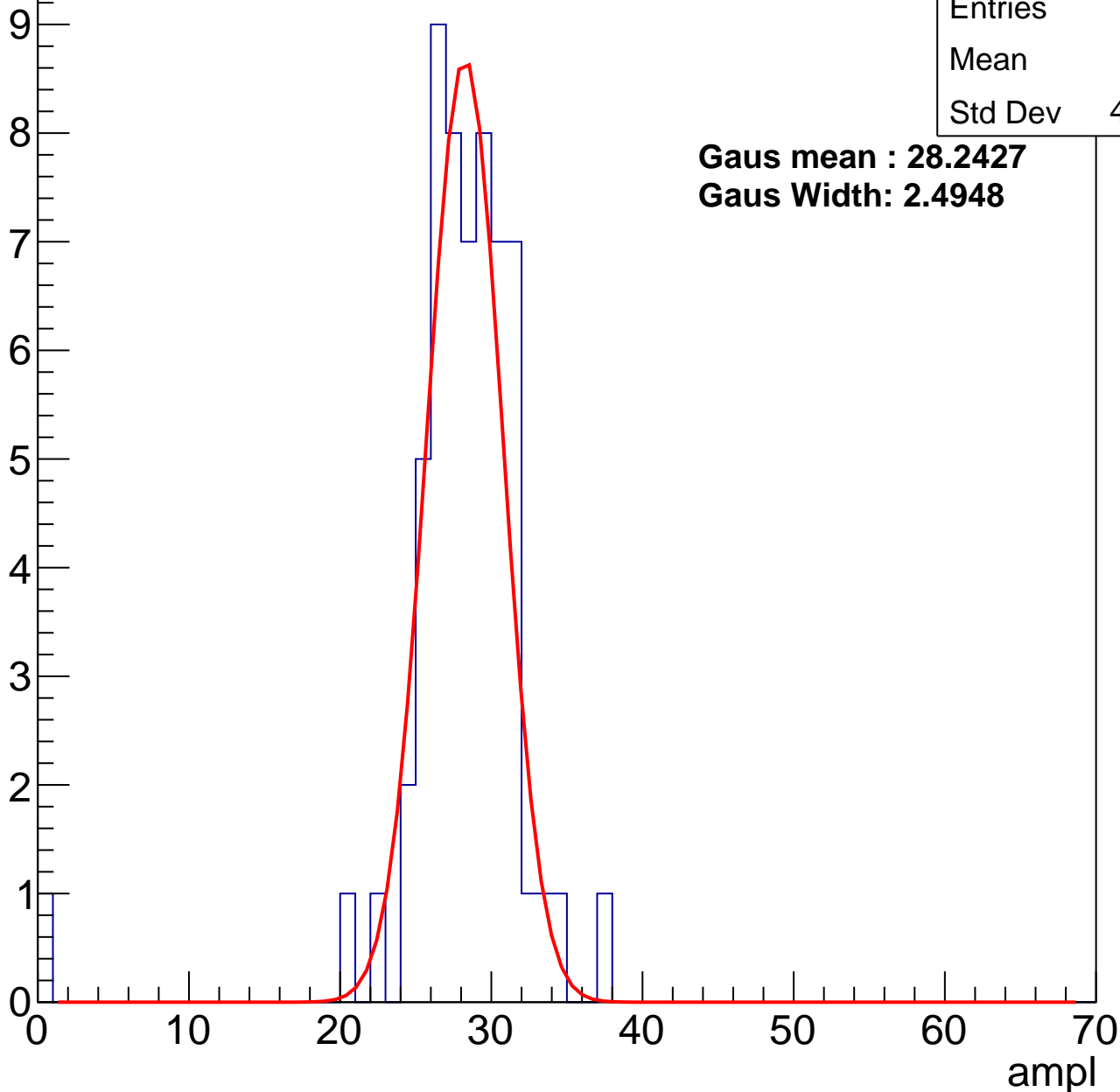
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.6
Std Dev	4.569

**Gaus mean : 28.2427**

**Gaus Width: 2.4948**



# B1L000S, U2-ch52, adc1

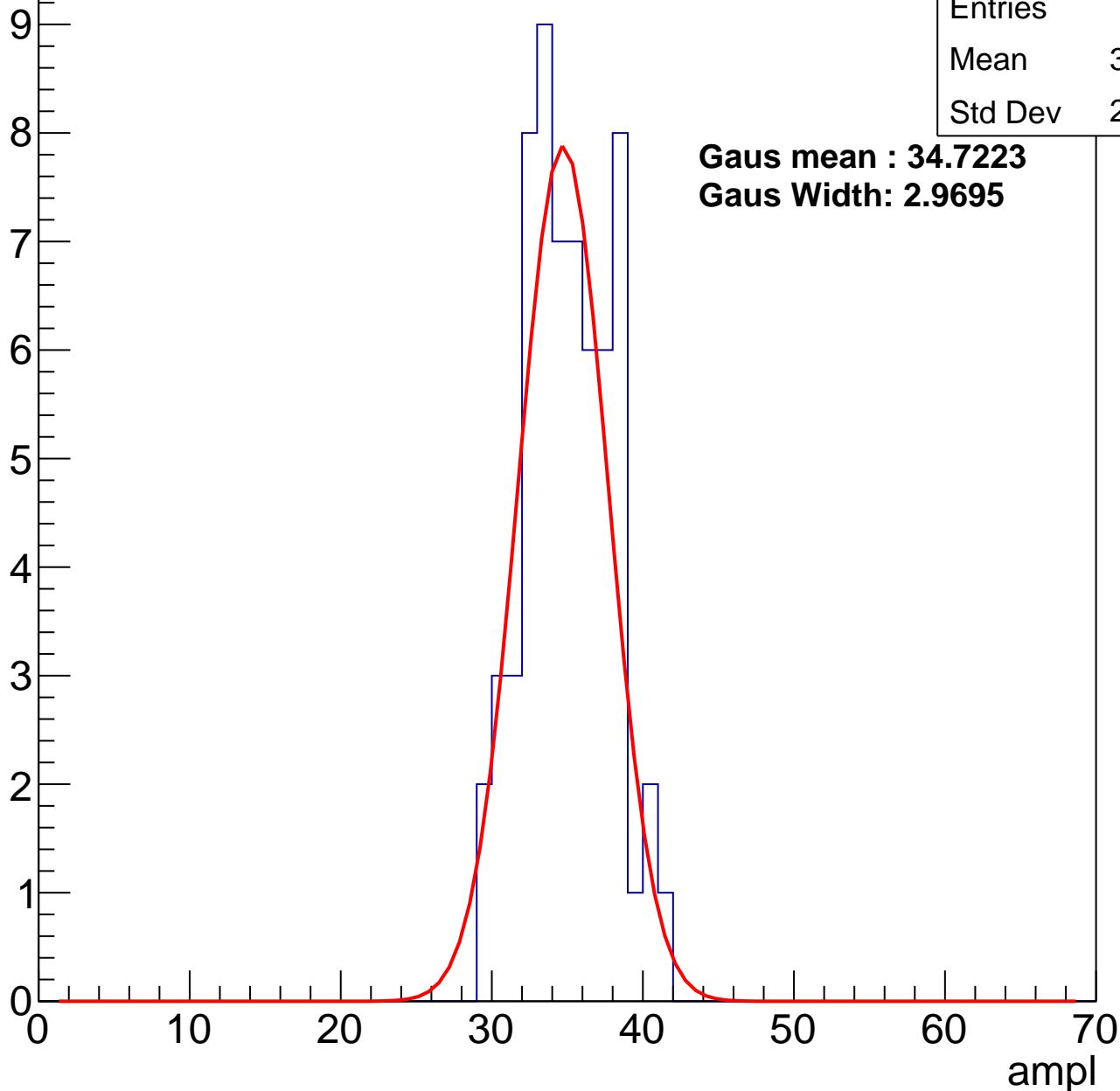
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	34.59
Std Dev	2.826

**Gaus mean : 34.7223**

**Gaus Width: 2.9695**



# B1L000S, U2-ch52, adc2

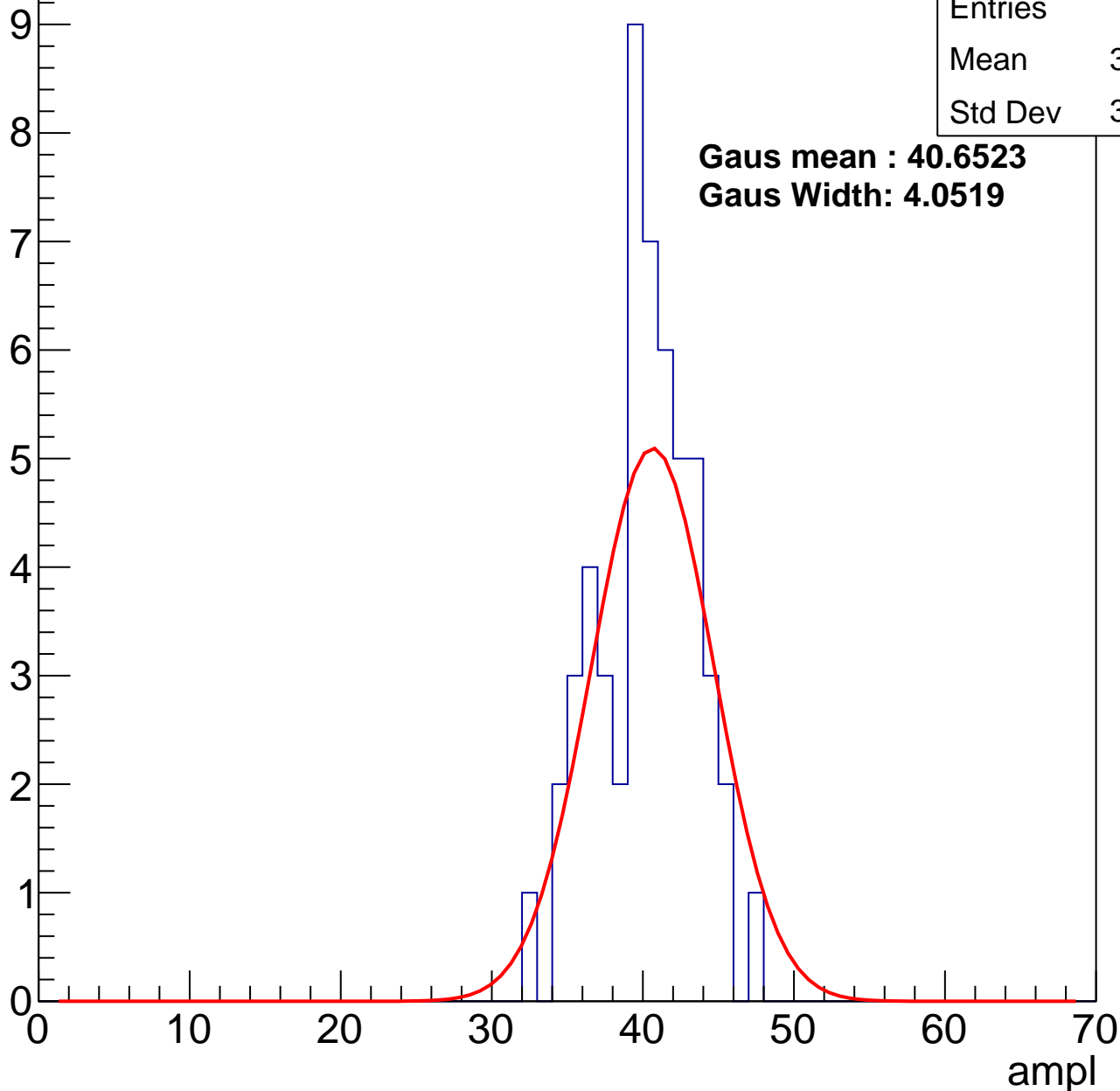
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	39.75
Std Dev	3.174

**Gaus mean : 40.6523**

**Gaus Width: 4.0519**



# B1L000S, U2-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

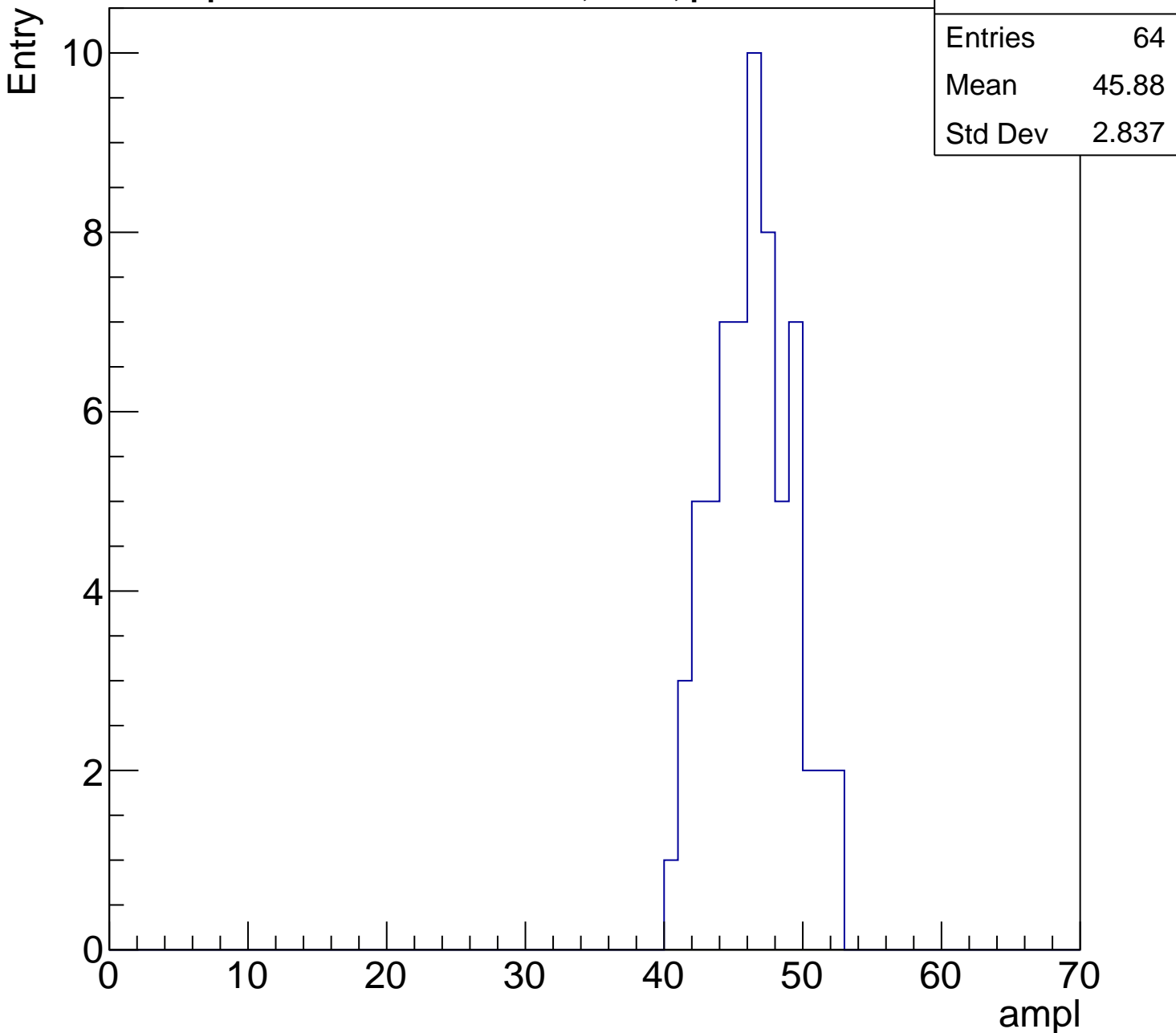
Entries	64
Mean	45.88
Std Dev	2.837

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

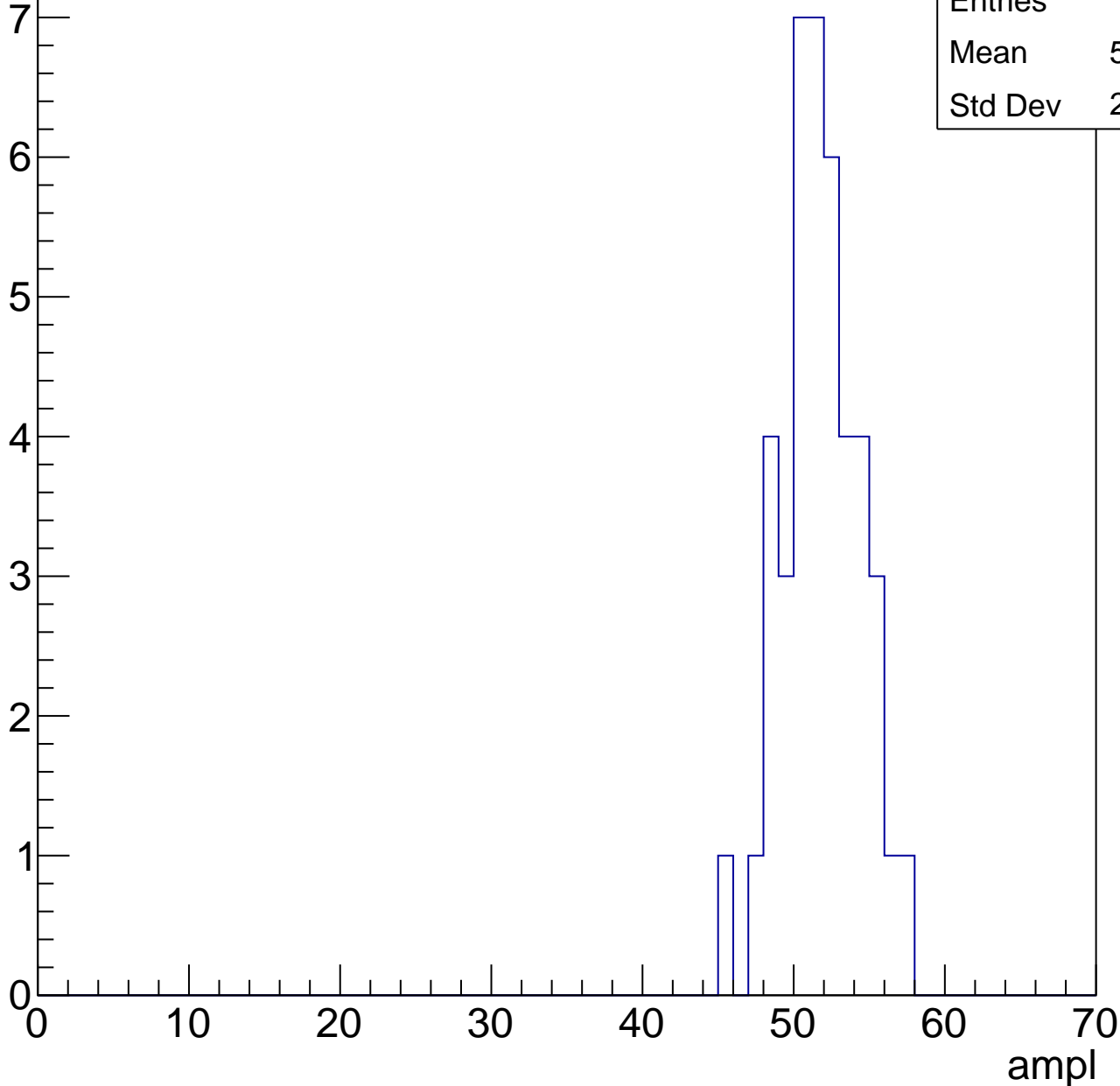


# B1L000S, U2-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

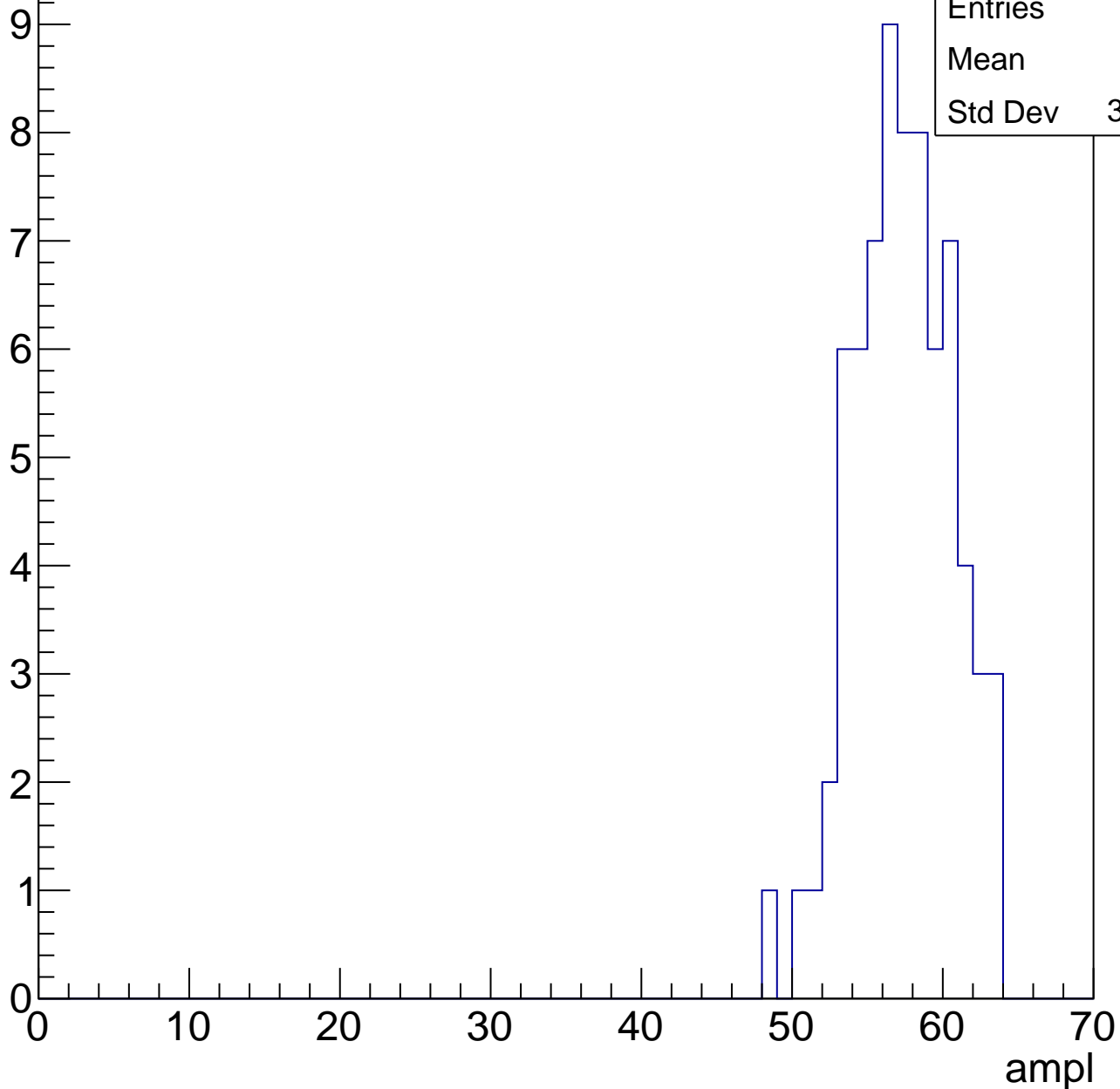
Entries	42
Mean	51.33
Std Dev	2.532



# B1L000S, U2-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

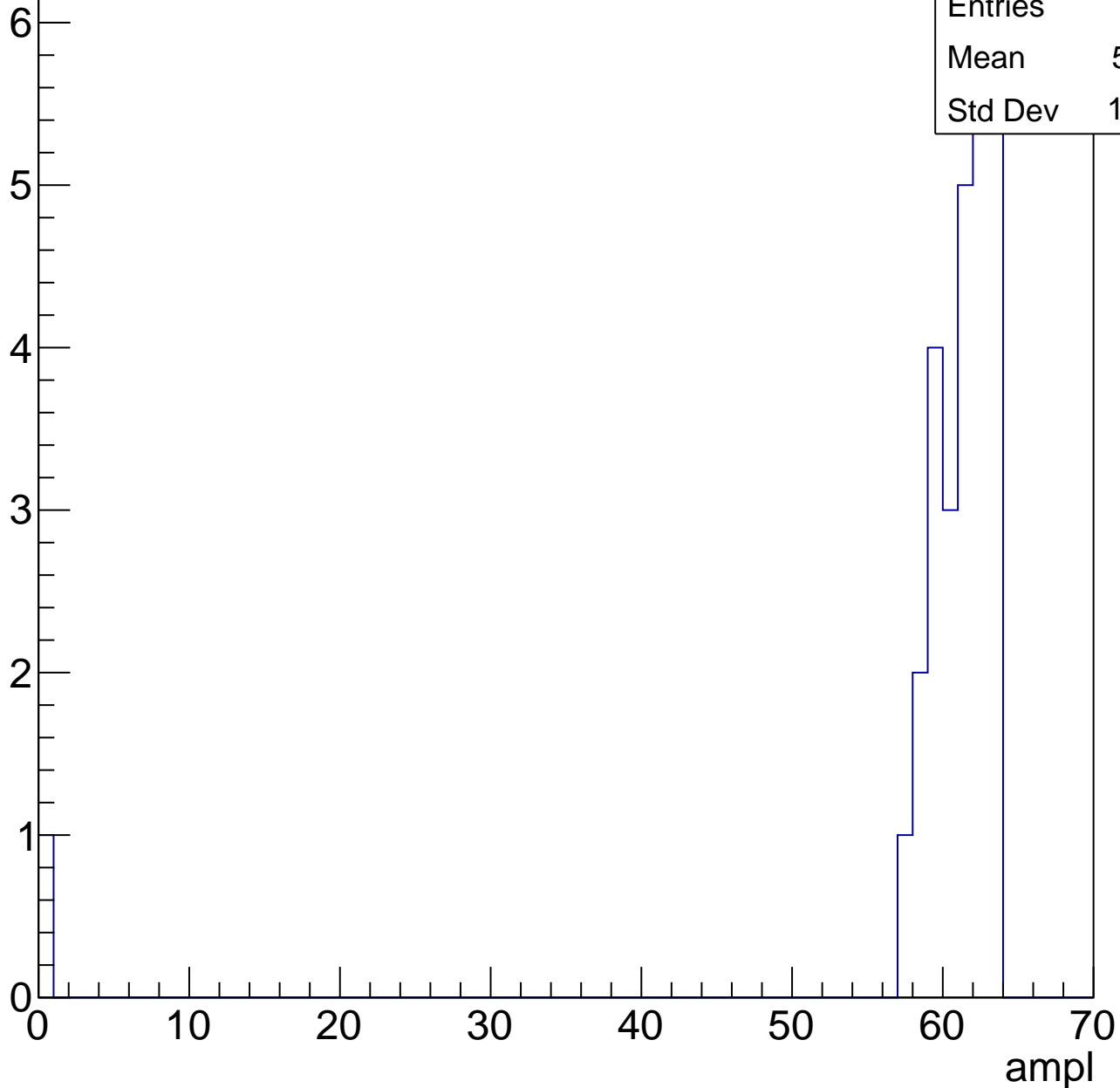


# B1L000S, U2-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.71
Std Dev	11.43

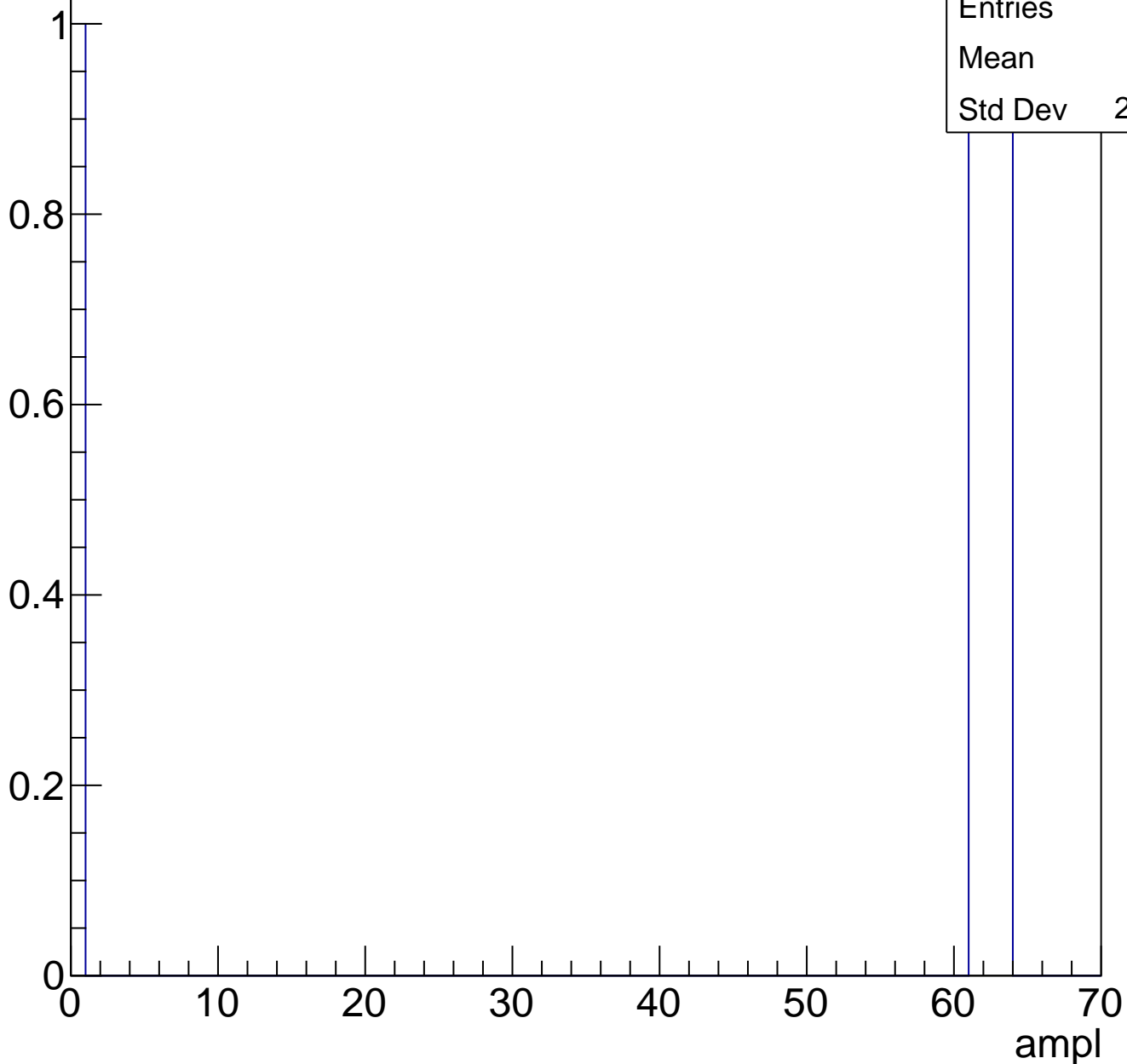




# B1L000S, U2-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch53, adc0

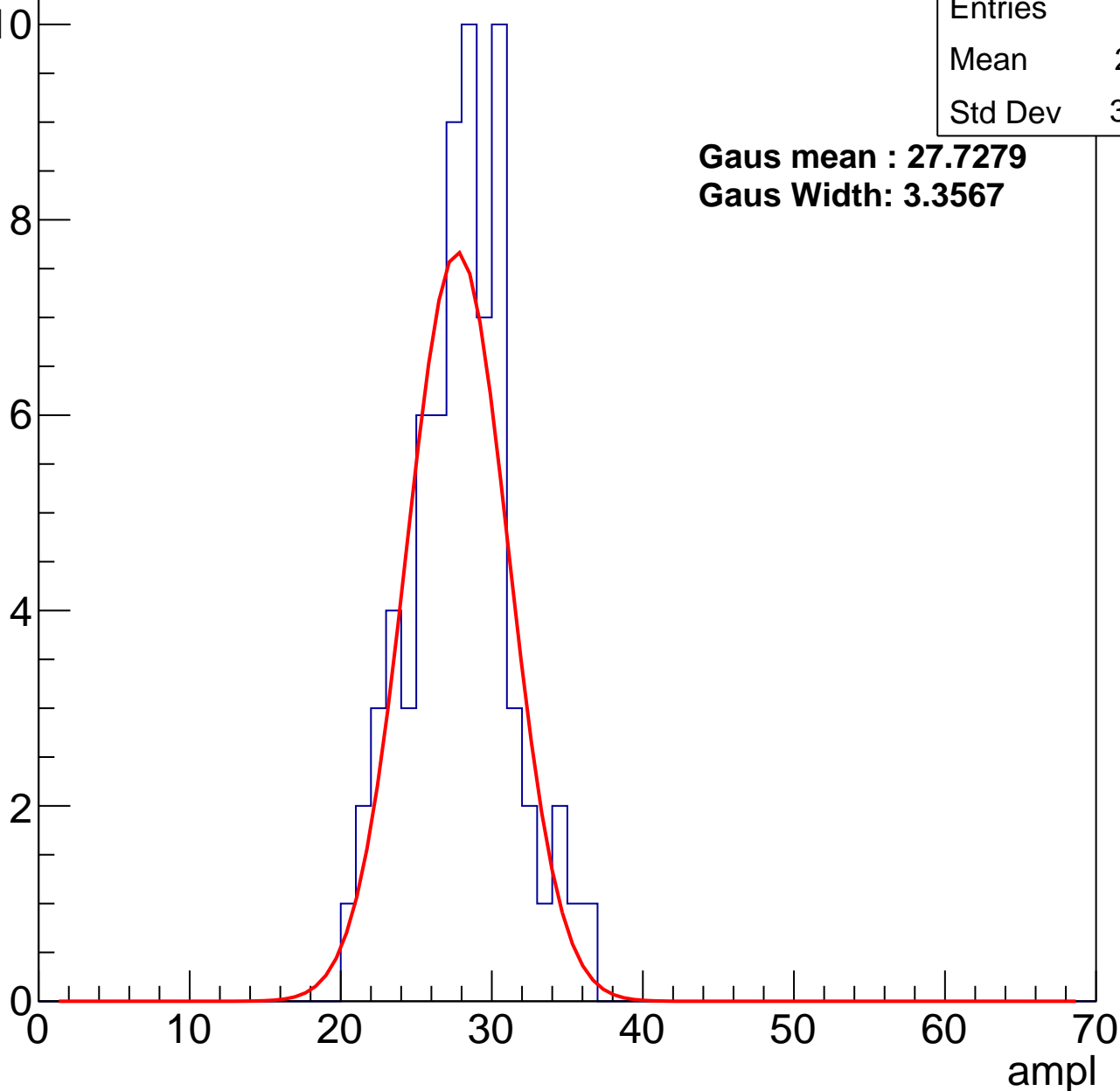
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	27.51
Std Dev	3.365

**Gaus mean : 27.7279**

**Gaus Width: 3.3567**



# B1L000S, U2-ch53, adc1

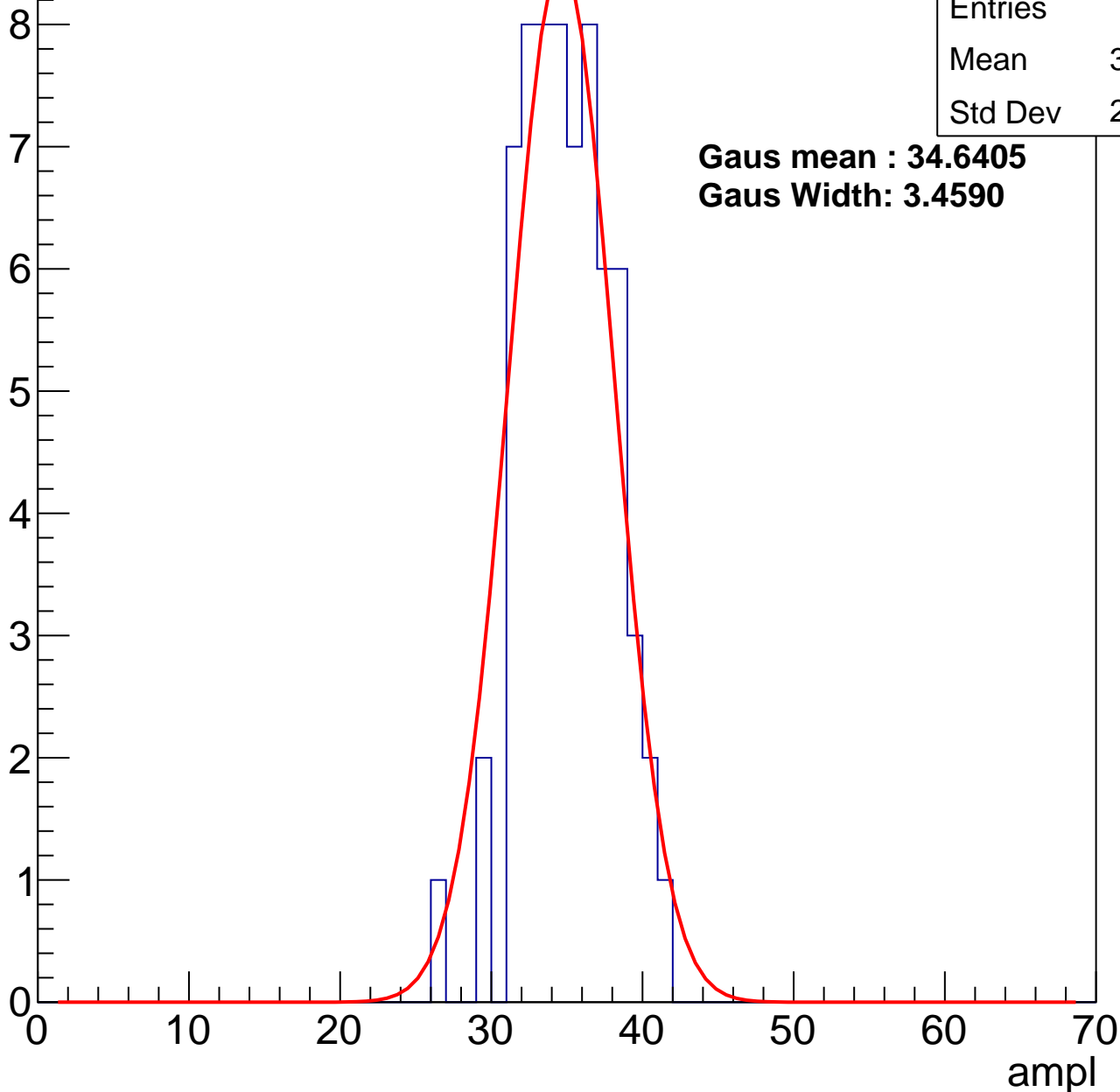
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.54
Std Dev	2.949

**Gaus mean : 34.6405**

**Gaus Width: 3.4590**



# B1L000S, U2-ch53, adc2

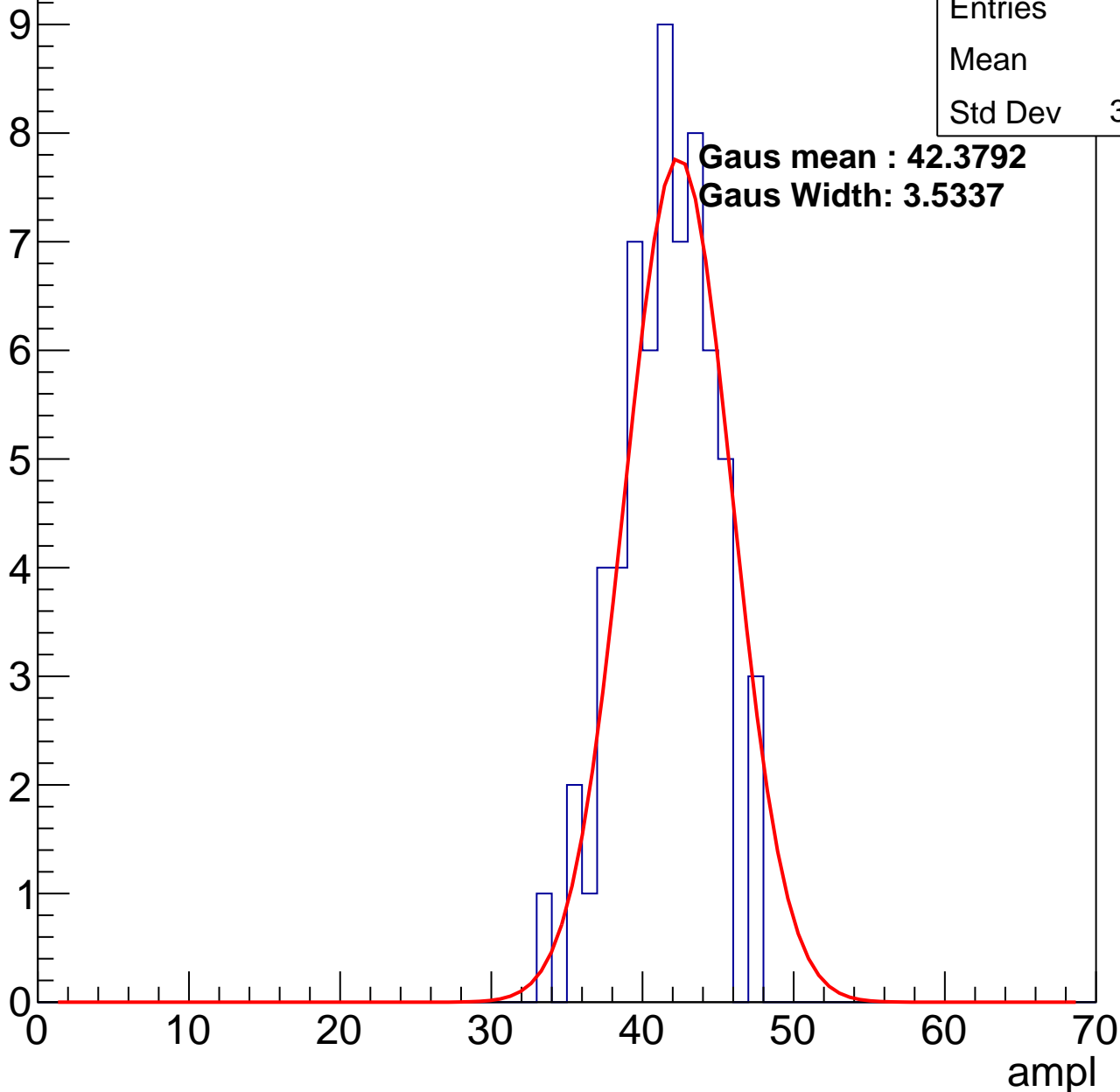
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.1
Std Dev	3.022

**Gaus mean : 42.3792**

**Gaus Width: 3.5337**



# B1L000S, U2-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

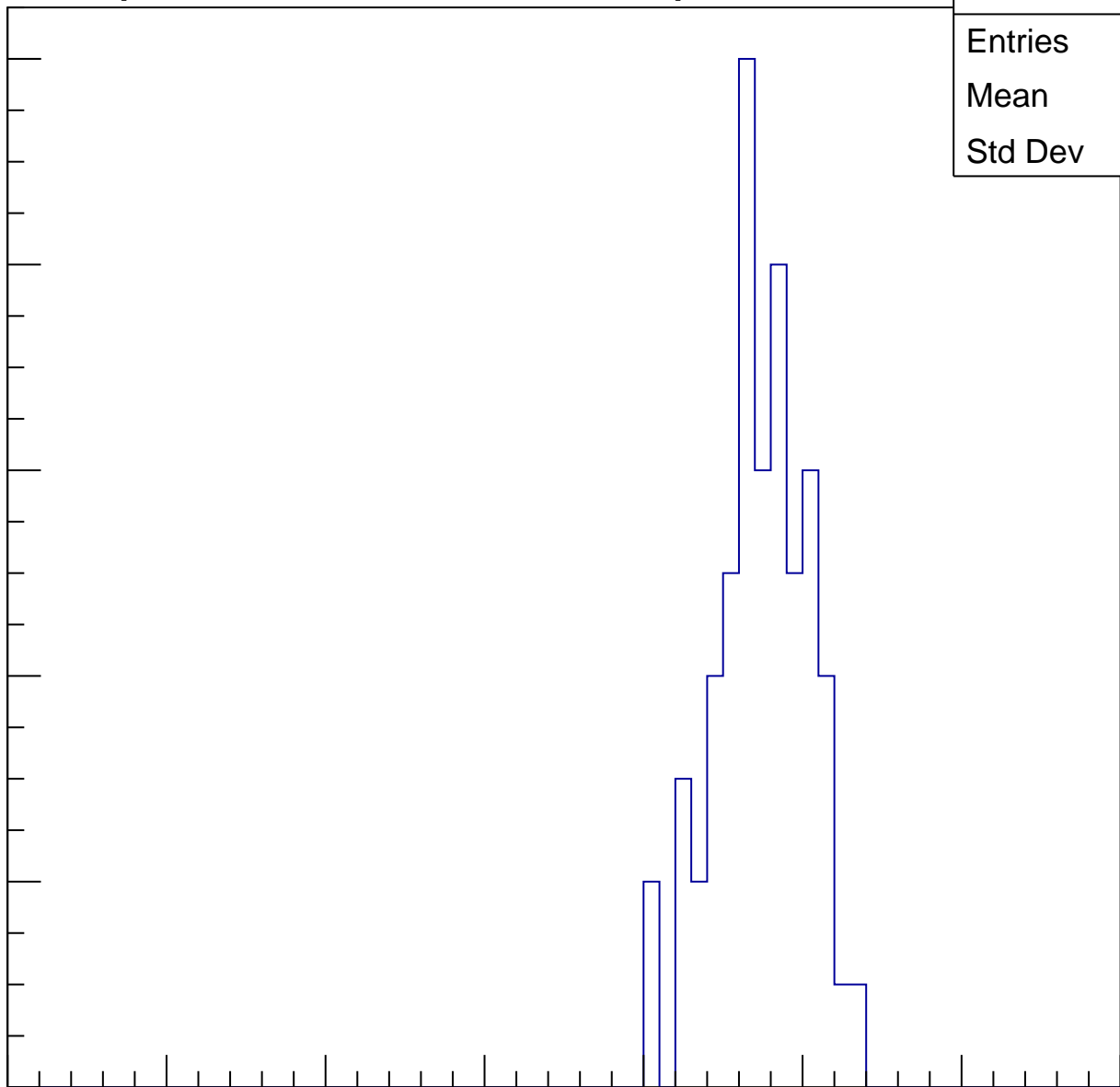
Entries	57
Mean	46.89
Std Dev	2.894

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

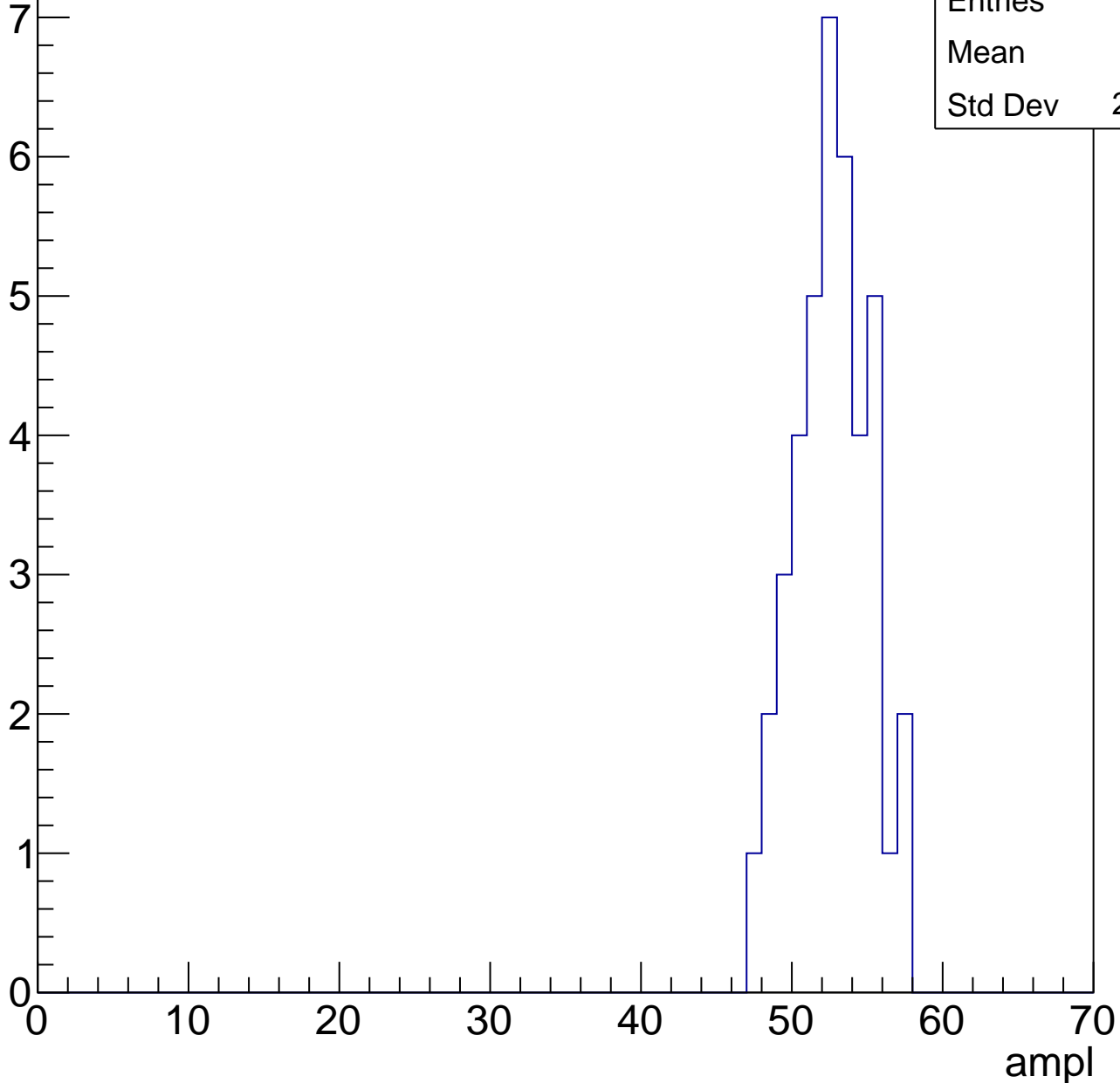


# B1L000S, U2-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	52.2
Std Dev	2.431



# B1L000S, U2-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	56.91
Std Dev	7.18

Entry

10

8

6

4

2

0

0

10

20

30

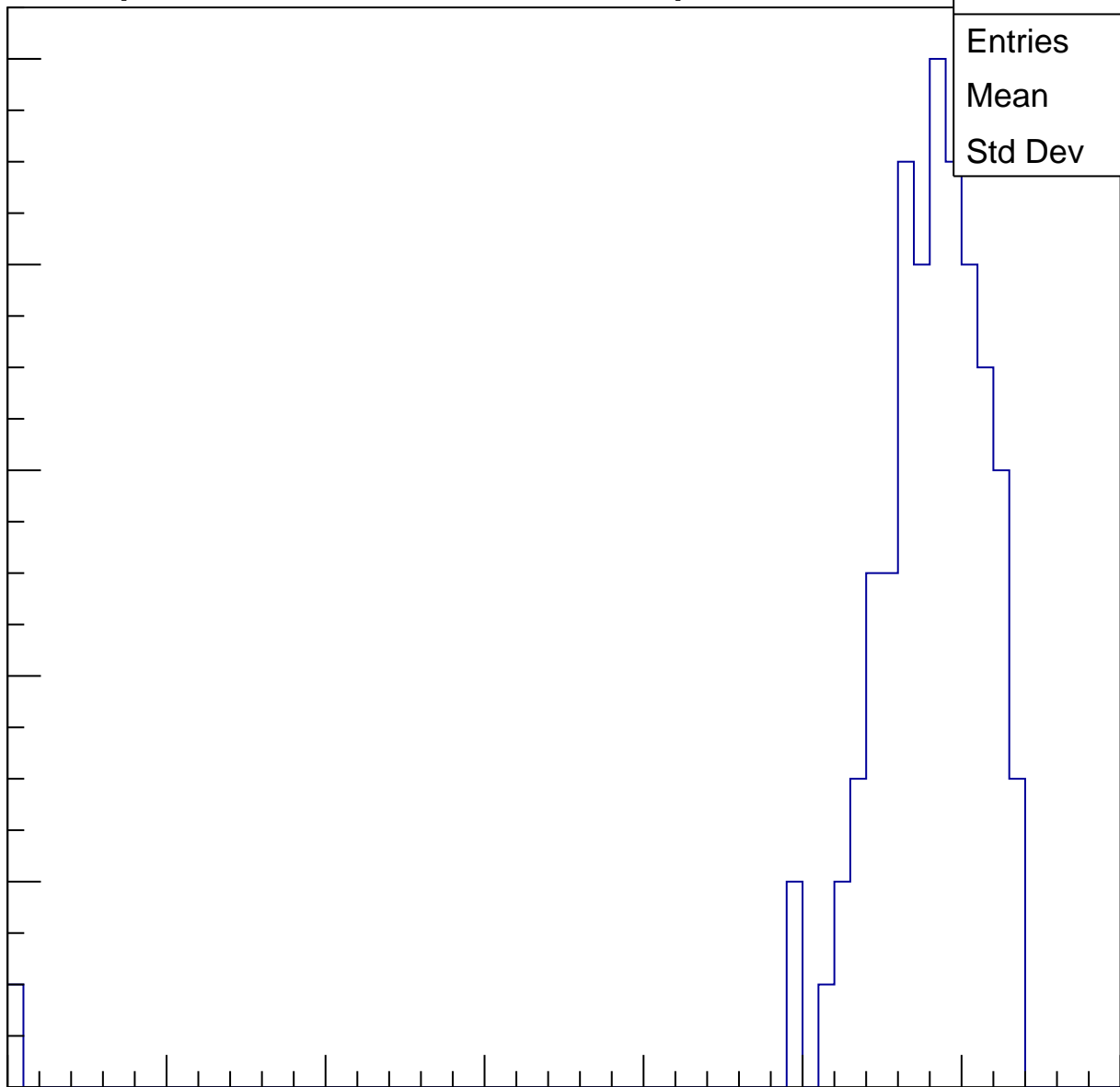
40

50

60

70

ampl

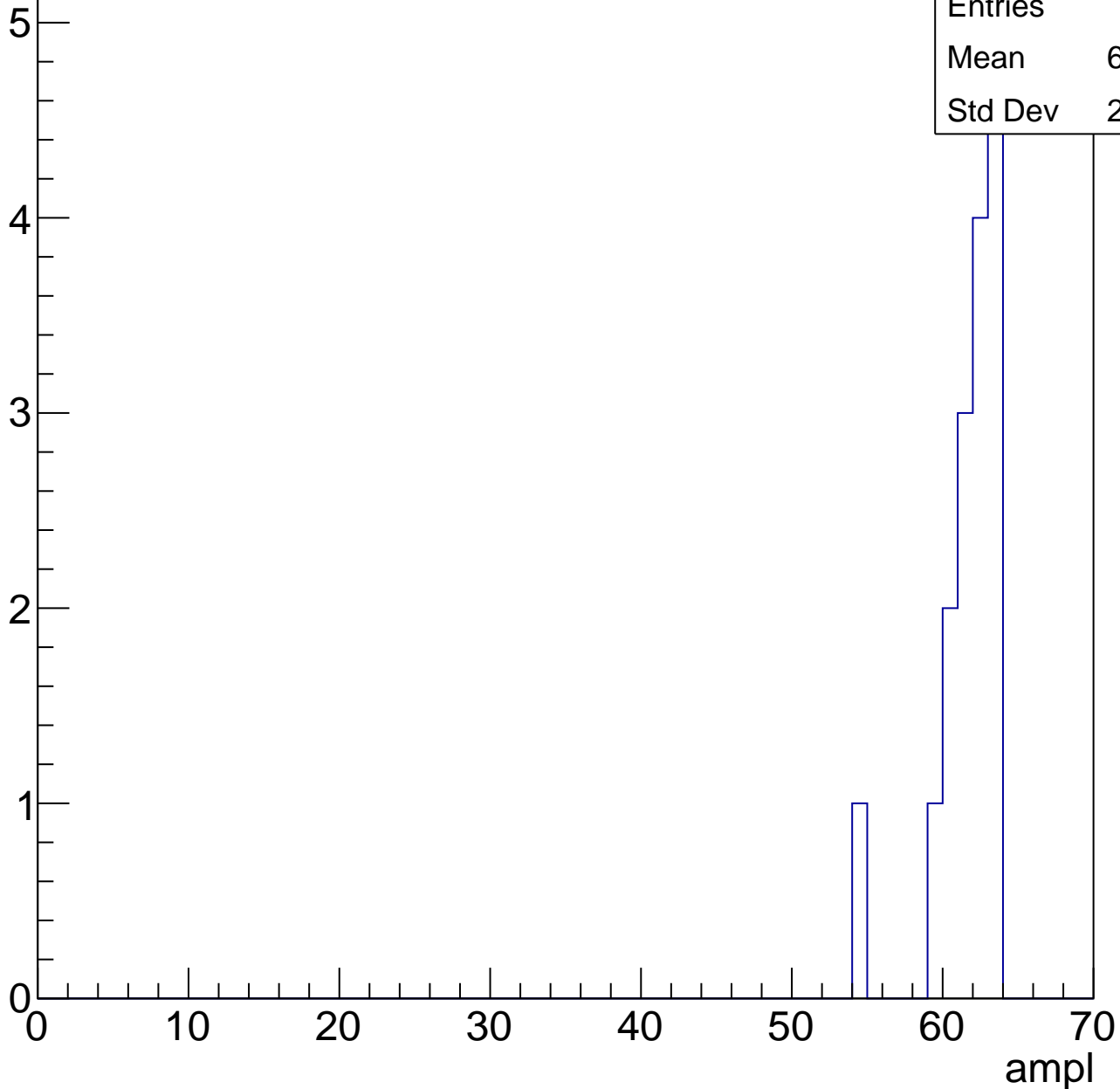


# B1L000S, U2-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.19
Std Dev	2.214





# B1L000S, U2-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch54, adc0

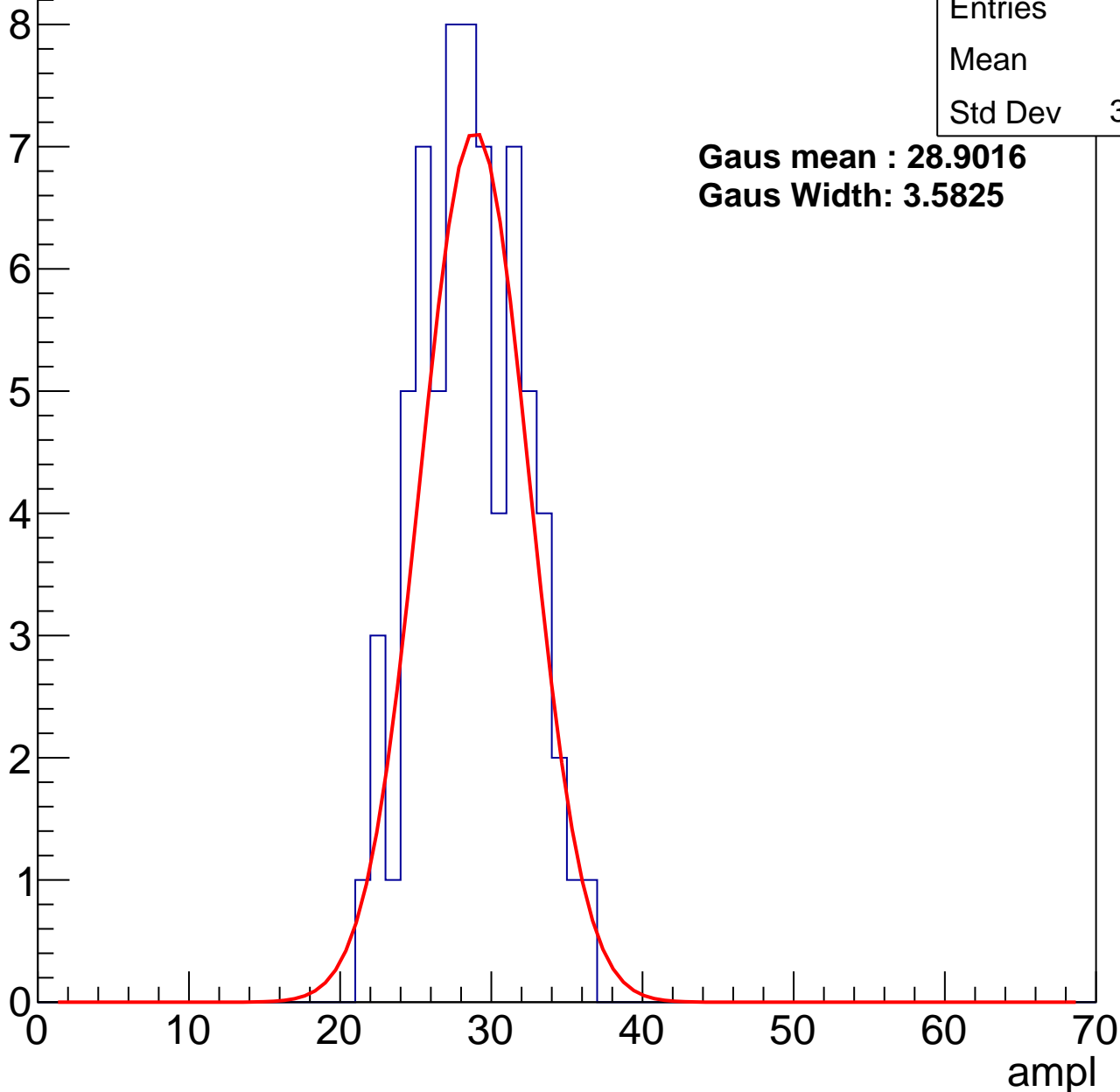
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.2
Std Dev	3.407

**Gaus mean : 28.9016**

**Gaus Width: 3.5825**



# B1L000S, U2-ch54, adc1

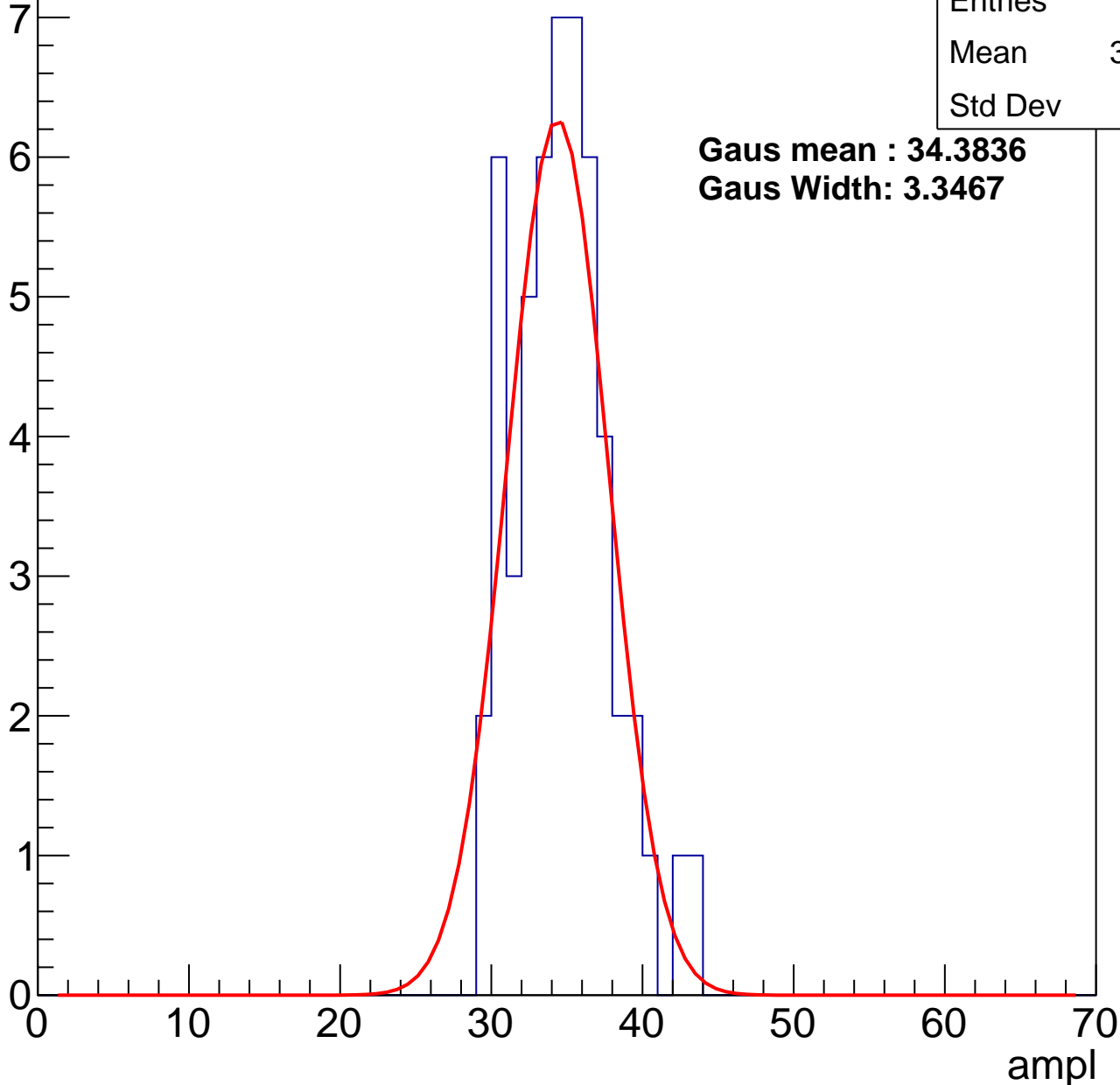
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	34.25
Std Dev	3.15

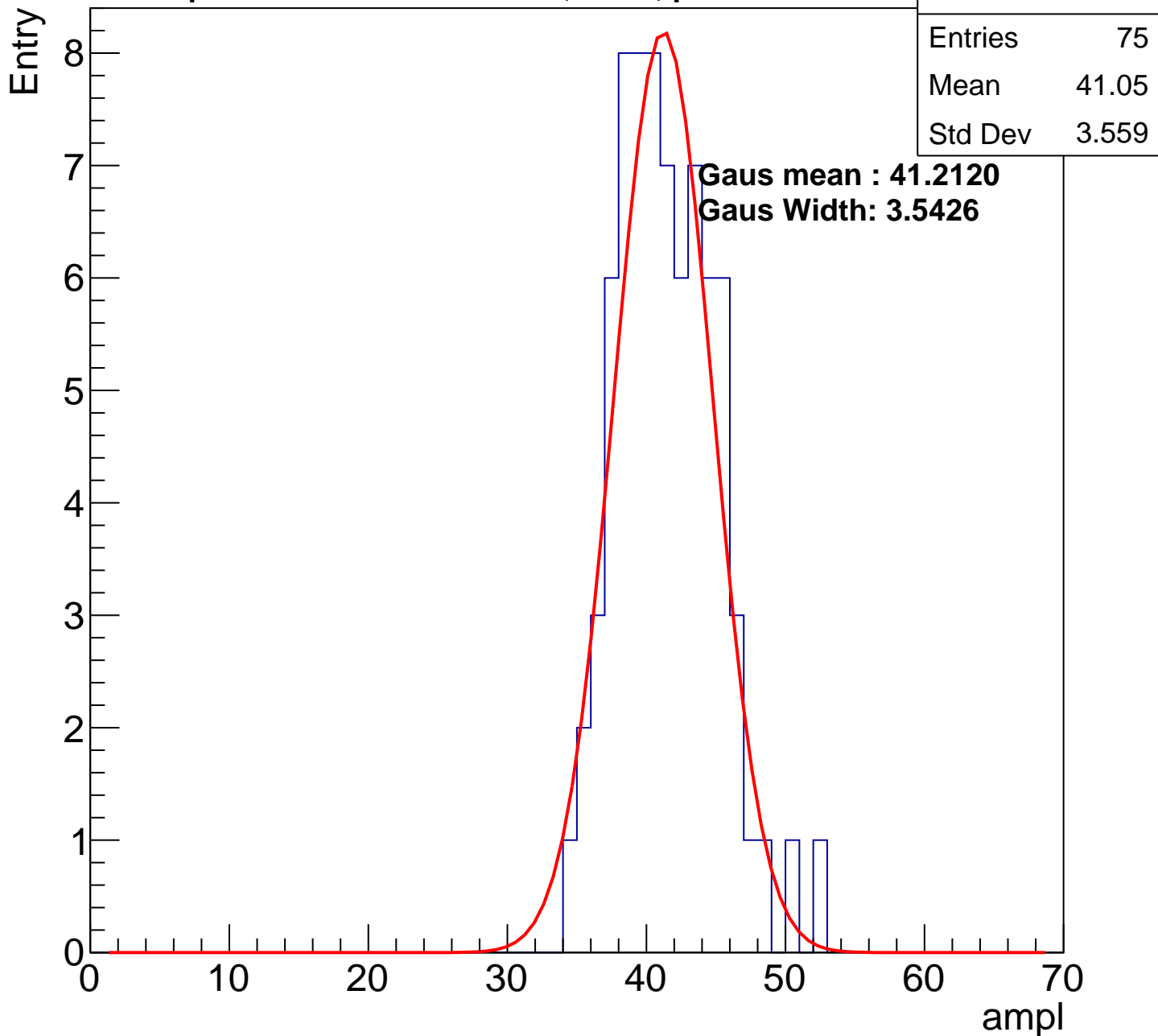
**Gaus mean : 34.3836**

**Gaus Width: 3.3467**



# B1L000S, U2-ch54, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

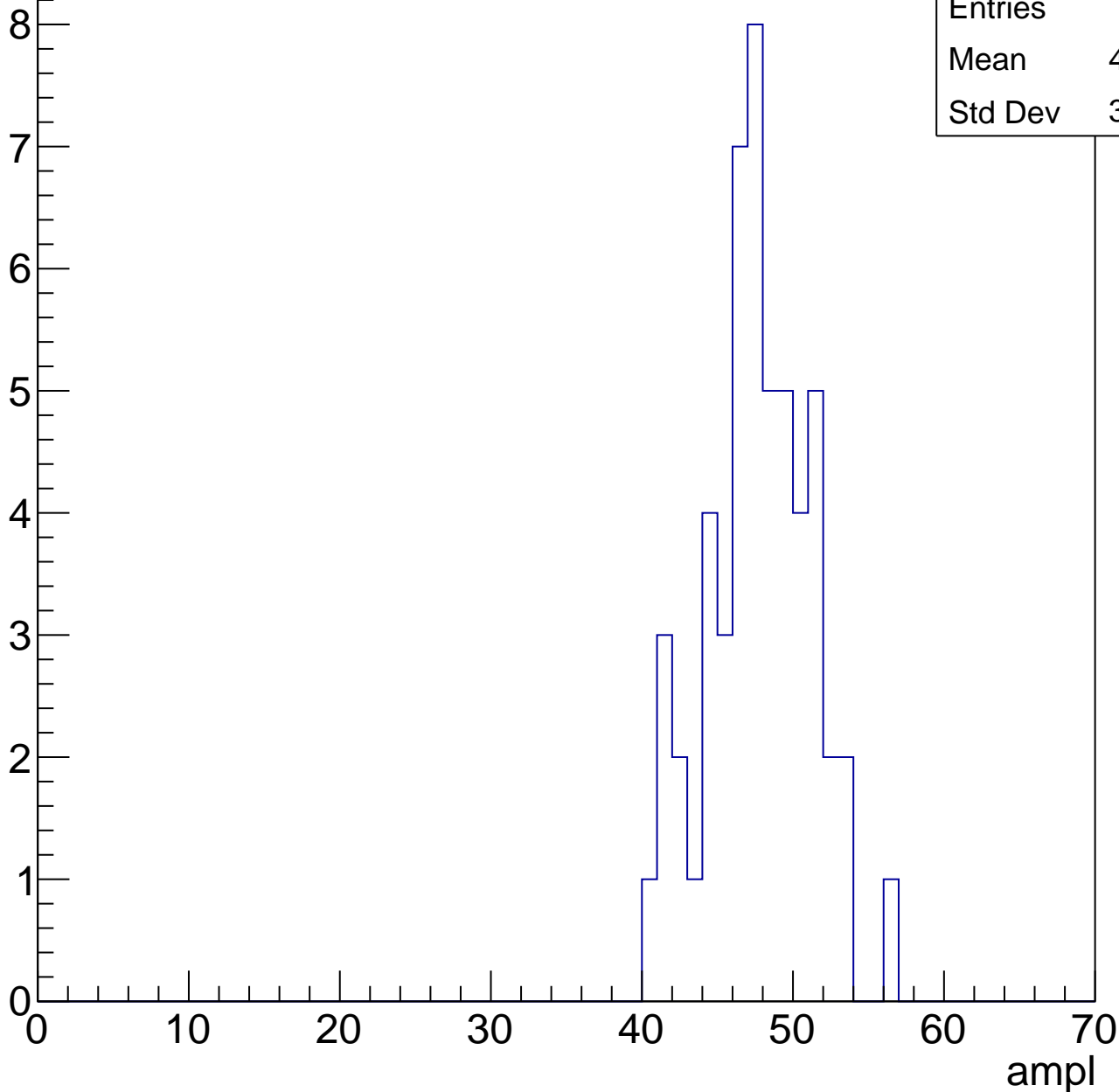


# B1L000S, U2-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

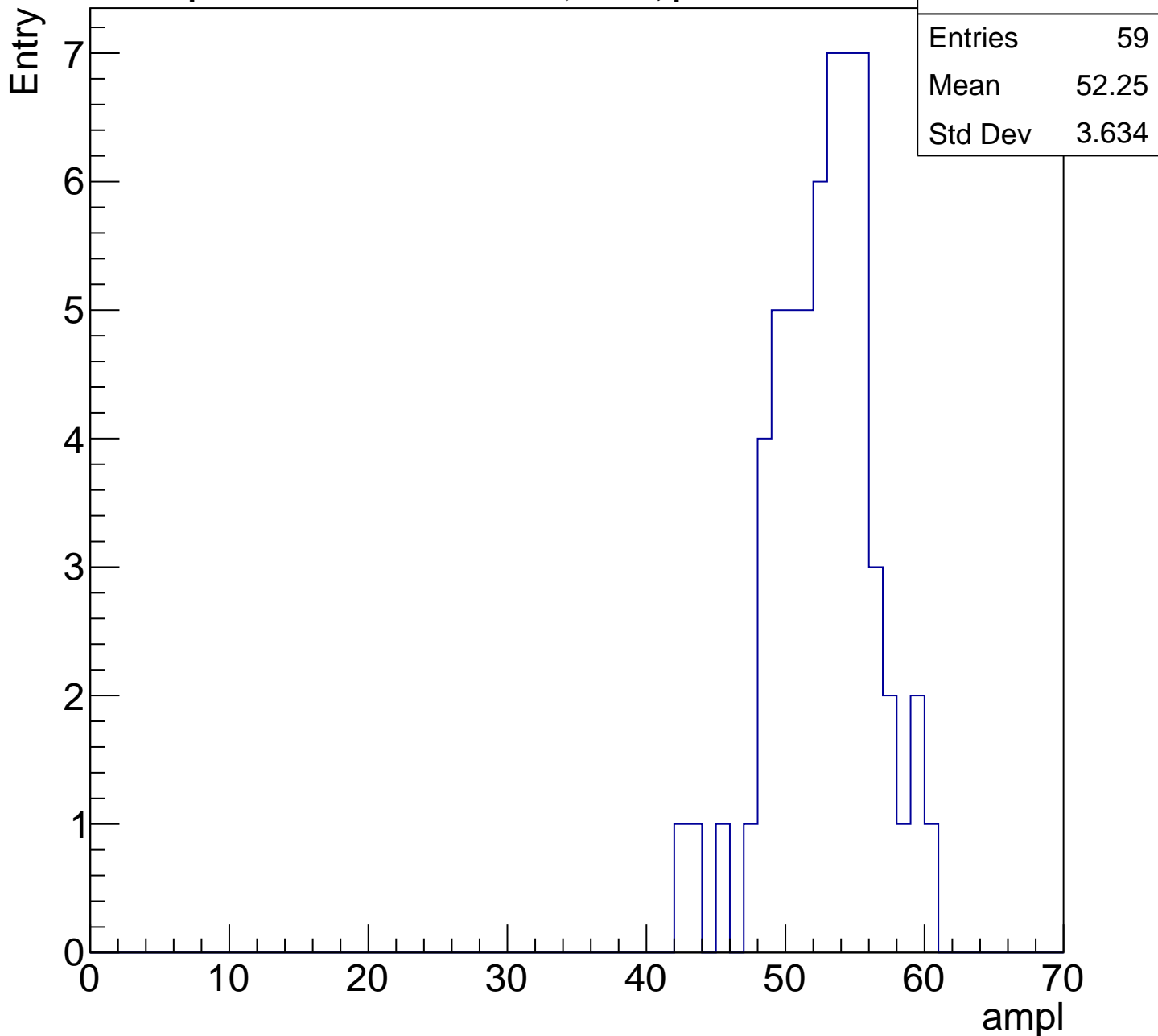
Entry

Entries	53
Mean	47.26
Std Dev	3.416



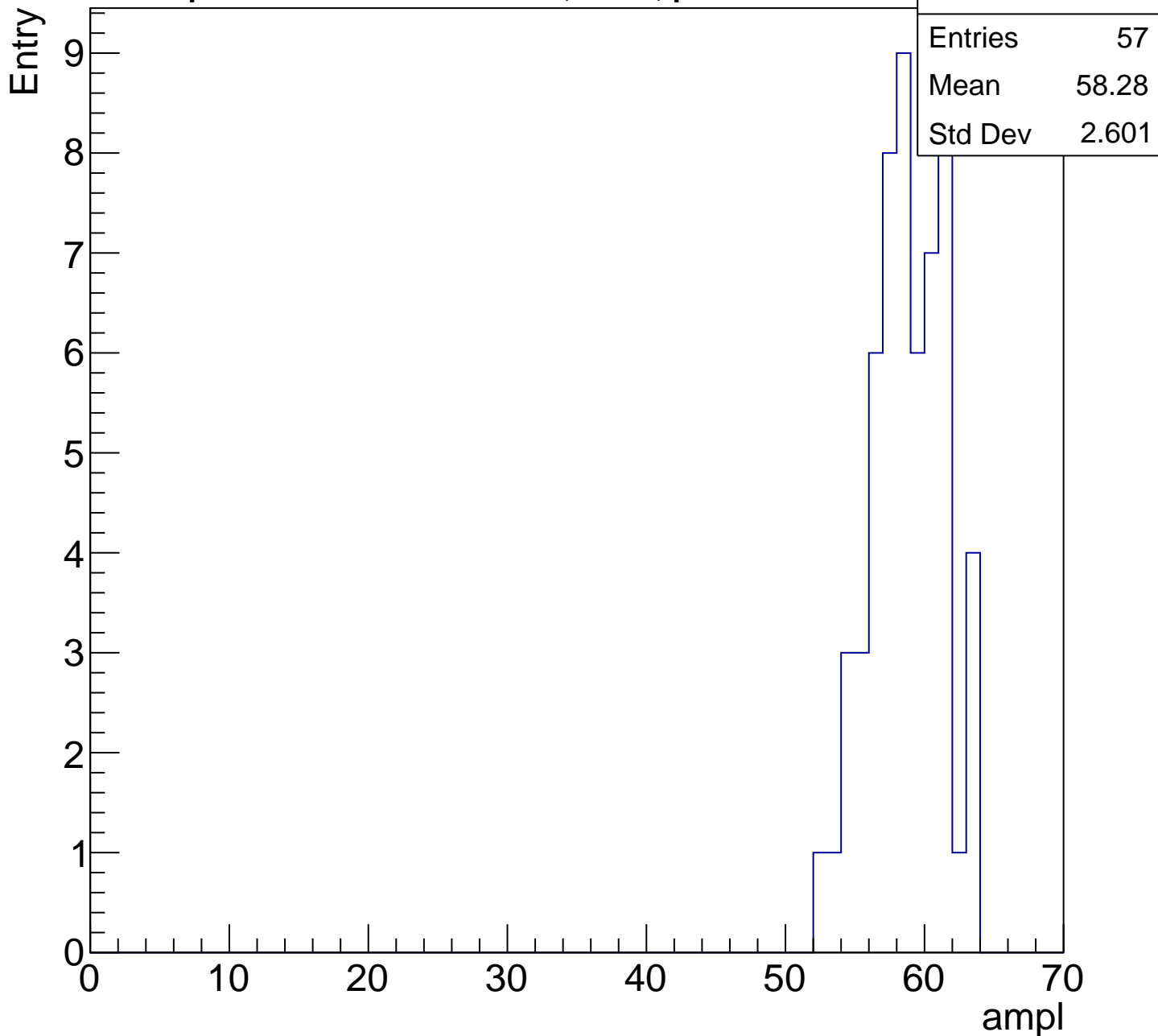
# B1L000S, U2-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

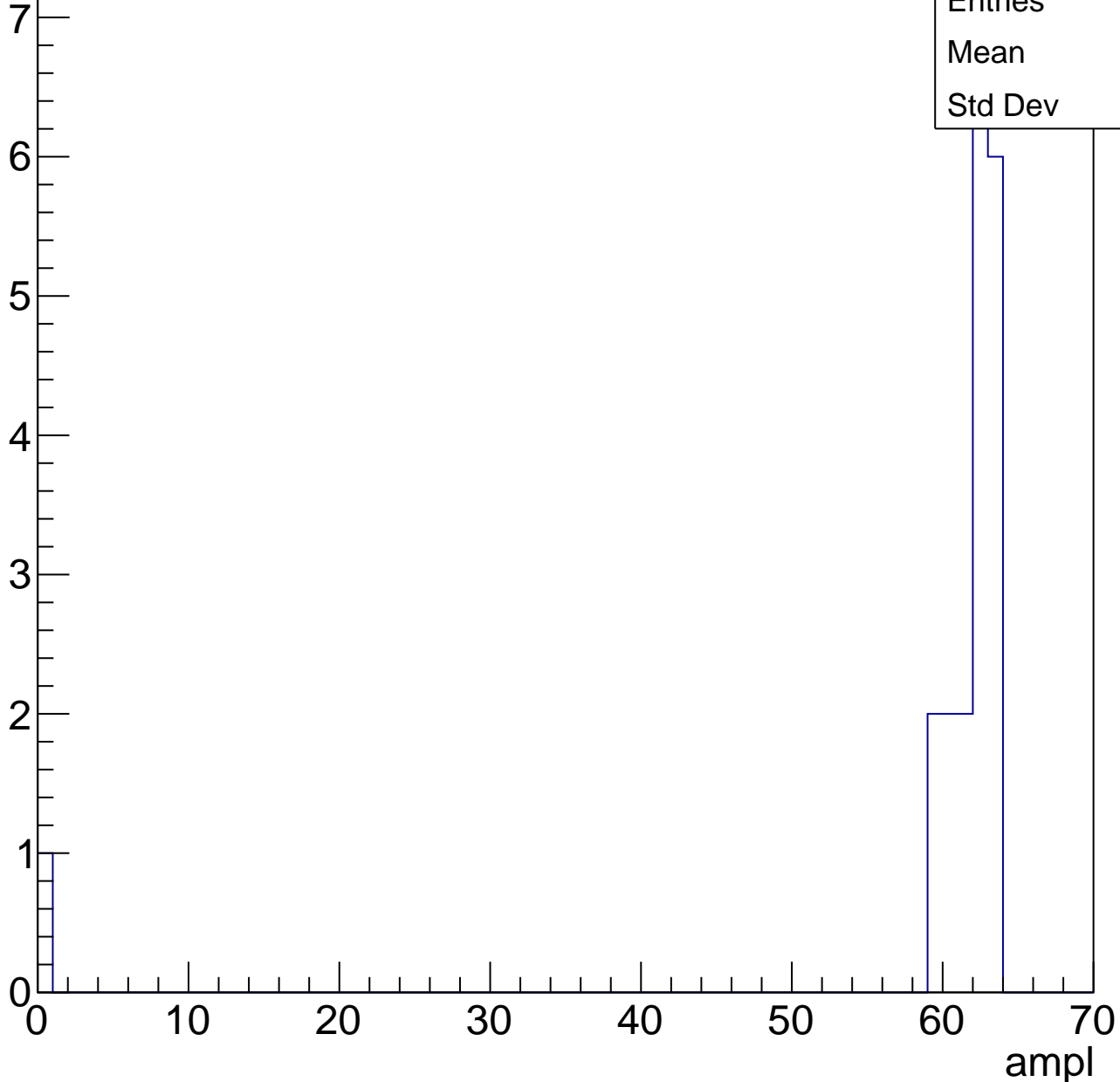


# B1L000S, U2-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.6
Std Dev	13.5

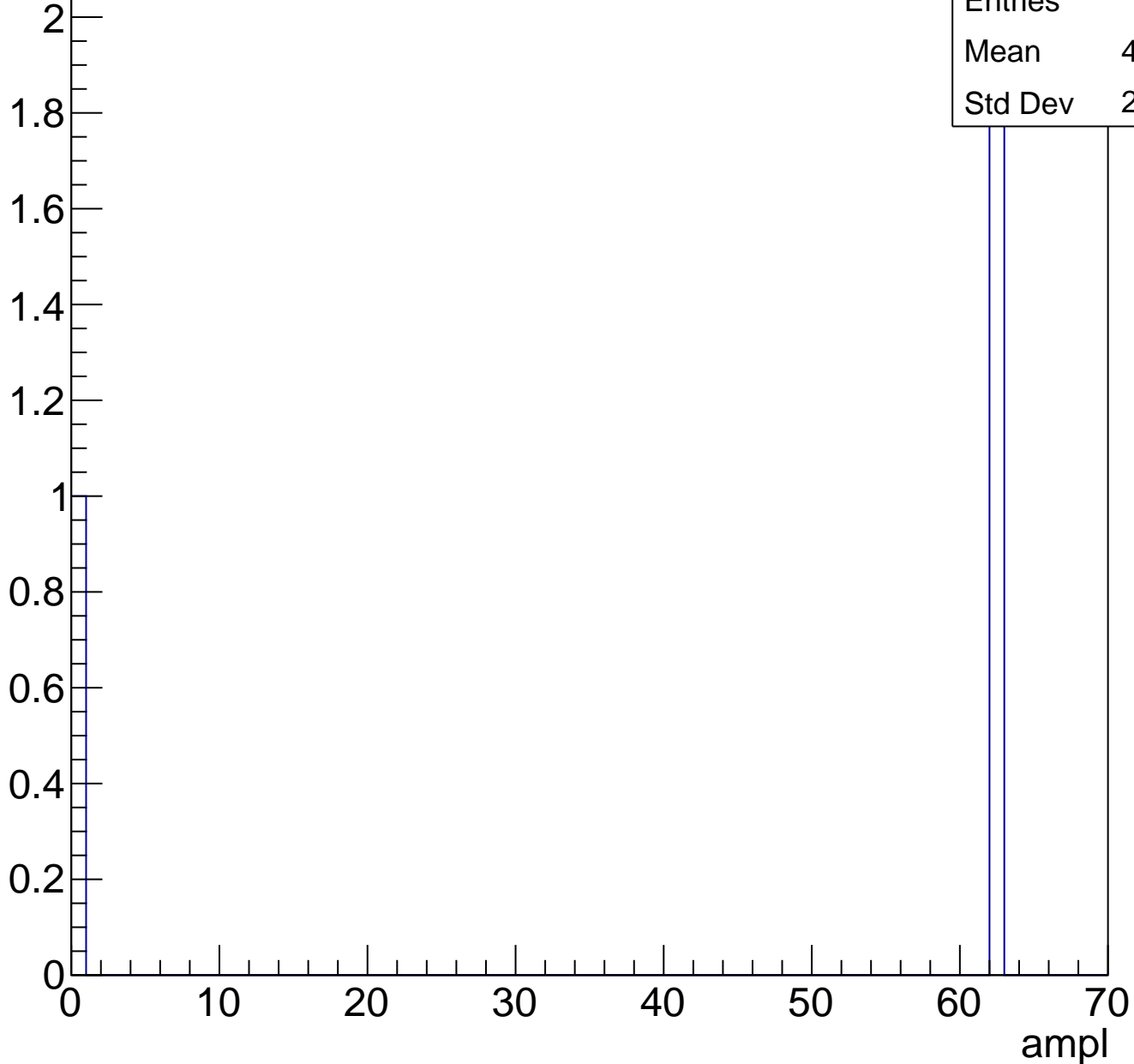




# B1L000S, U2-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch55, adc0

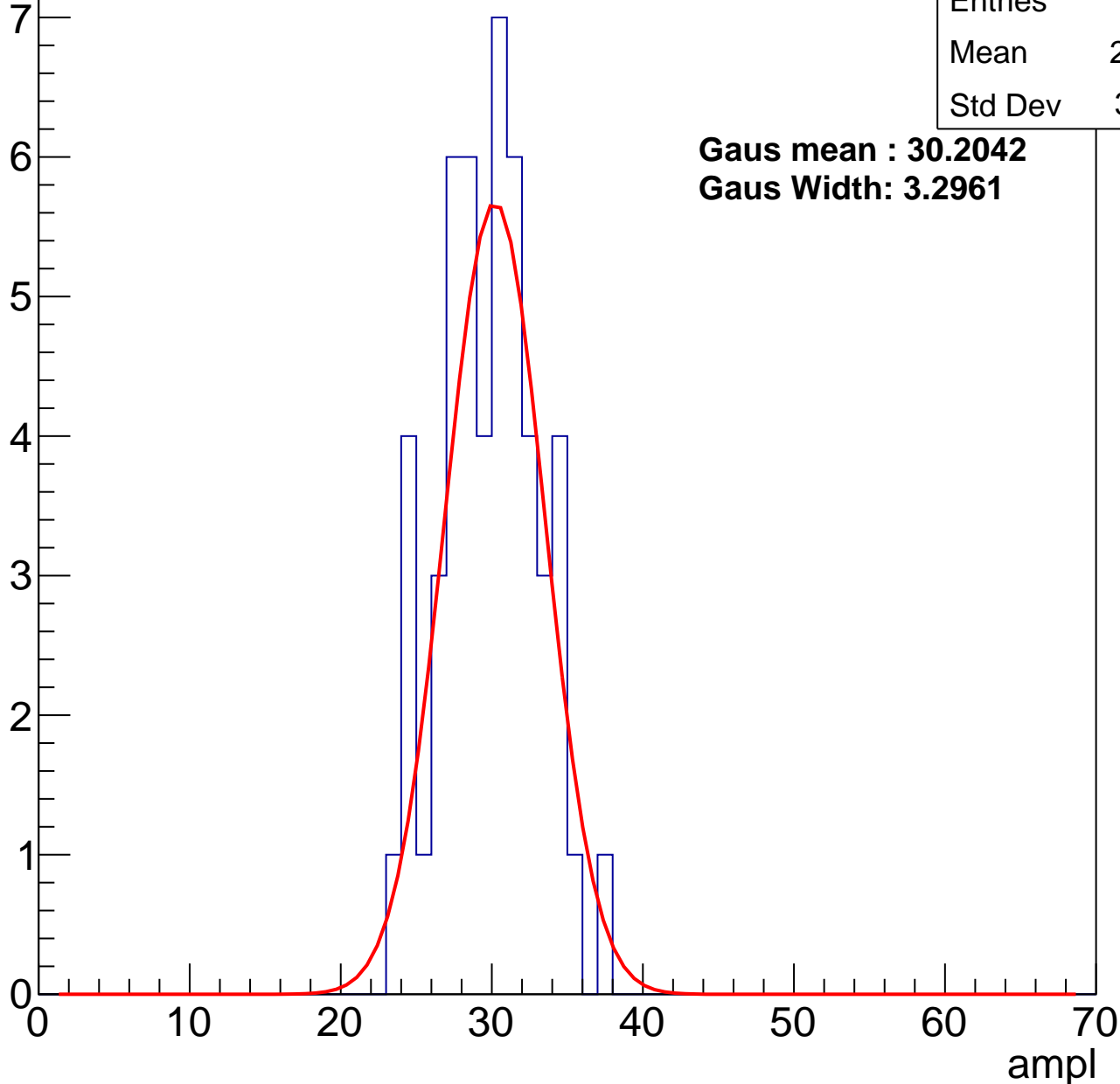
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	29.39
Std Dev	3.181

**Gaus mean : 30.2042**

**Gaus Width: 3.2961**



# B1L000S, U2-ch55, adc1

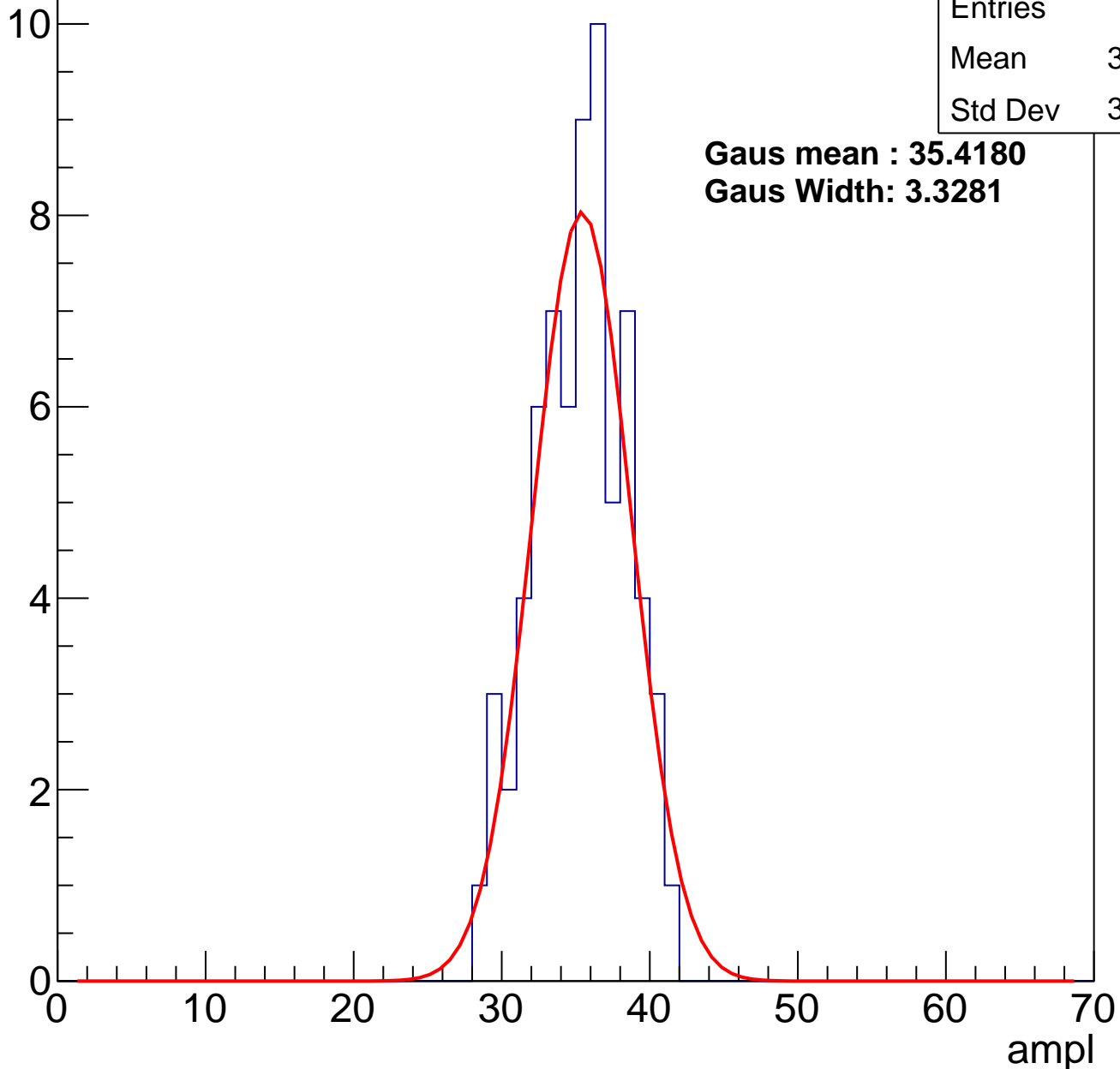
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	34.84
Std Dev	3.032

**Gaus mean : 35.4180**

**Gaus Width: 3.3281**

Entry



# B1L000S, U2-ch55, adc2

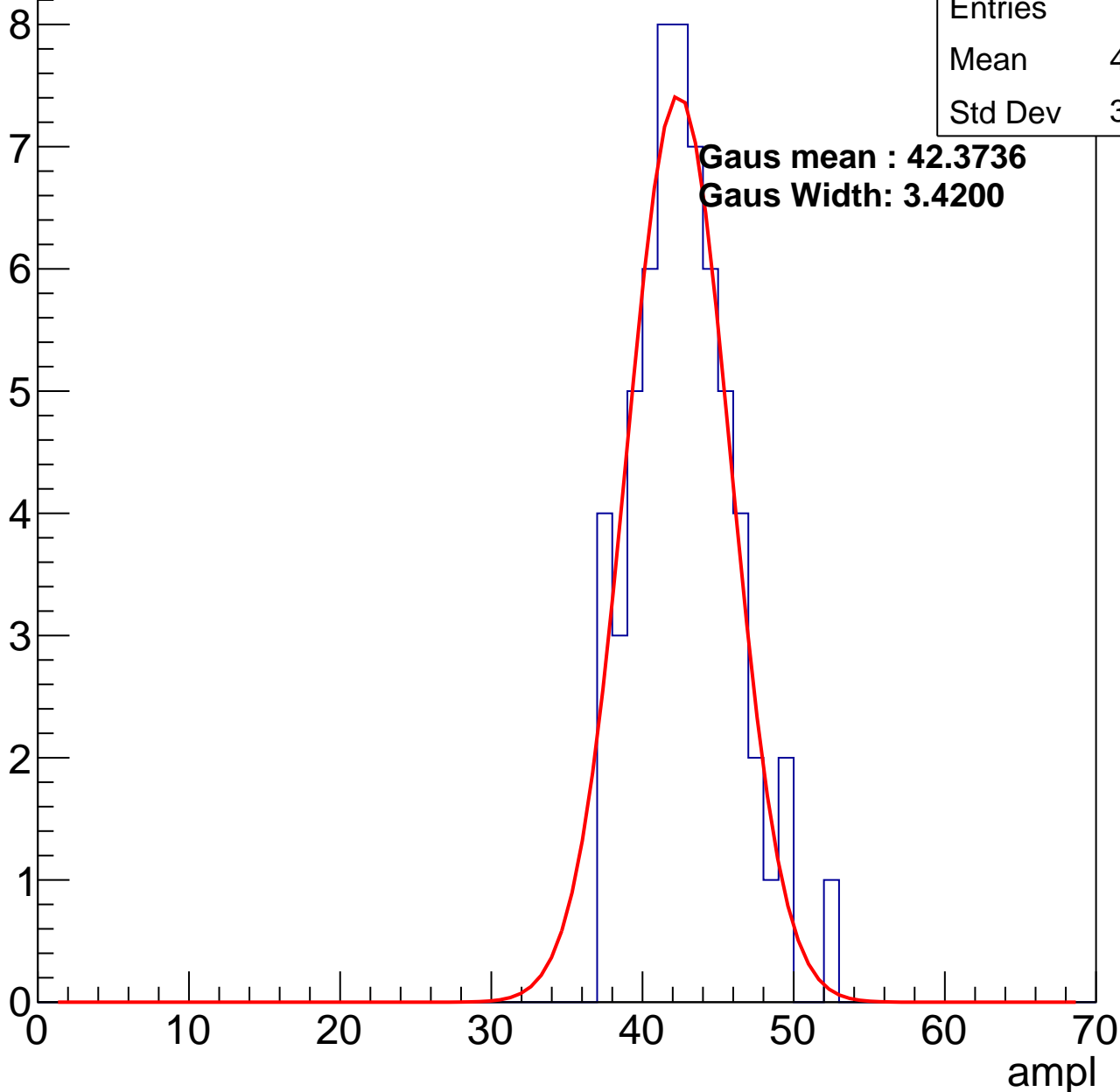
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.37
Std Dev	3.214

**Gaus mean : 42.3736**

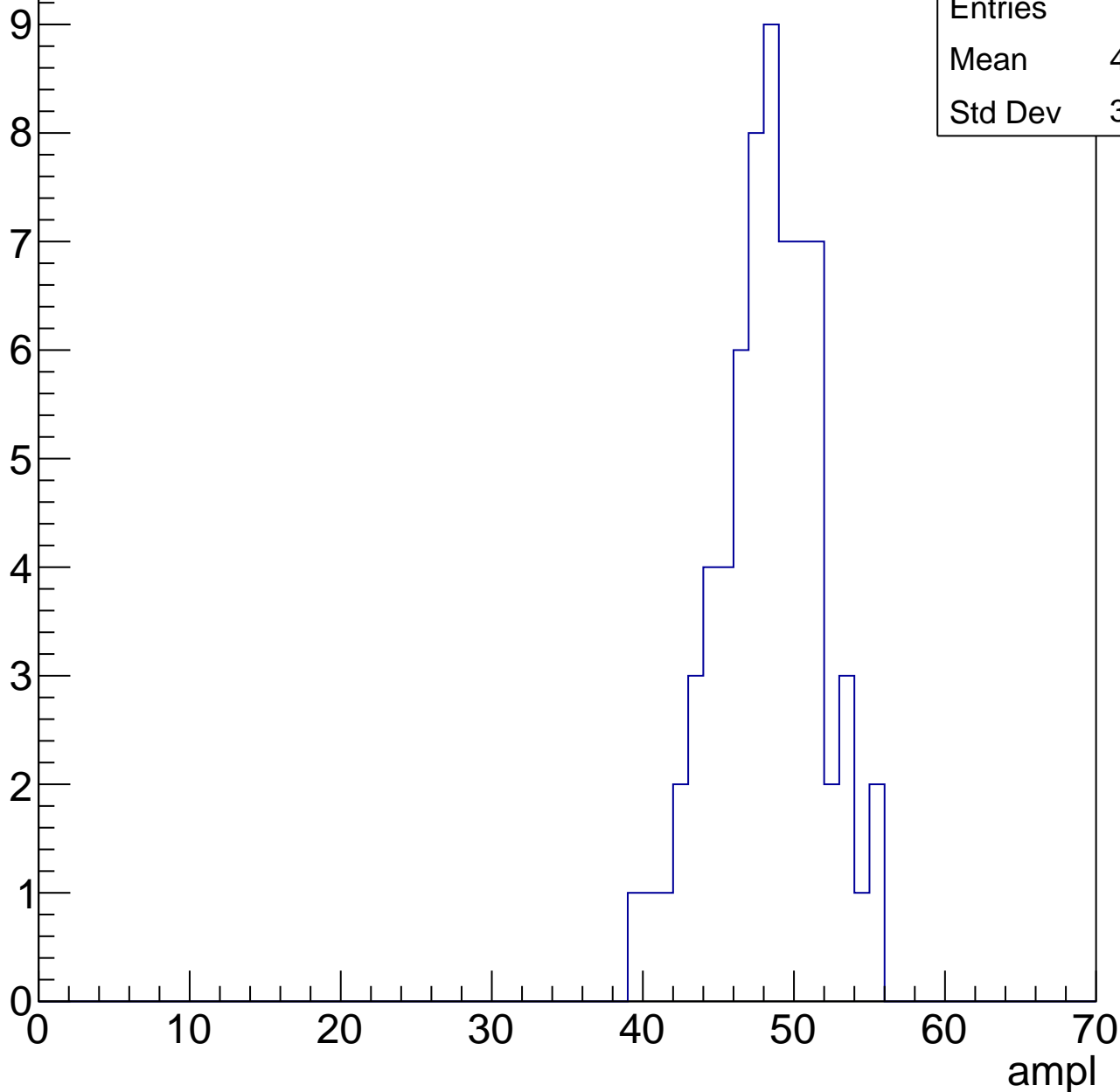
**Gaus Width: 3.4200**



# B1L000S, U2-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

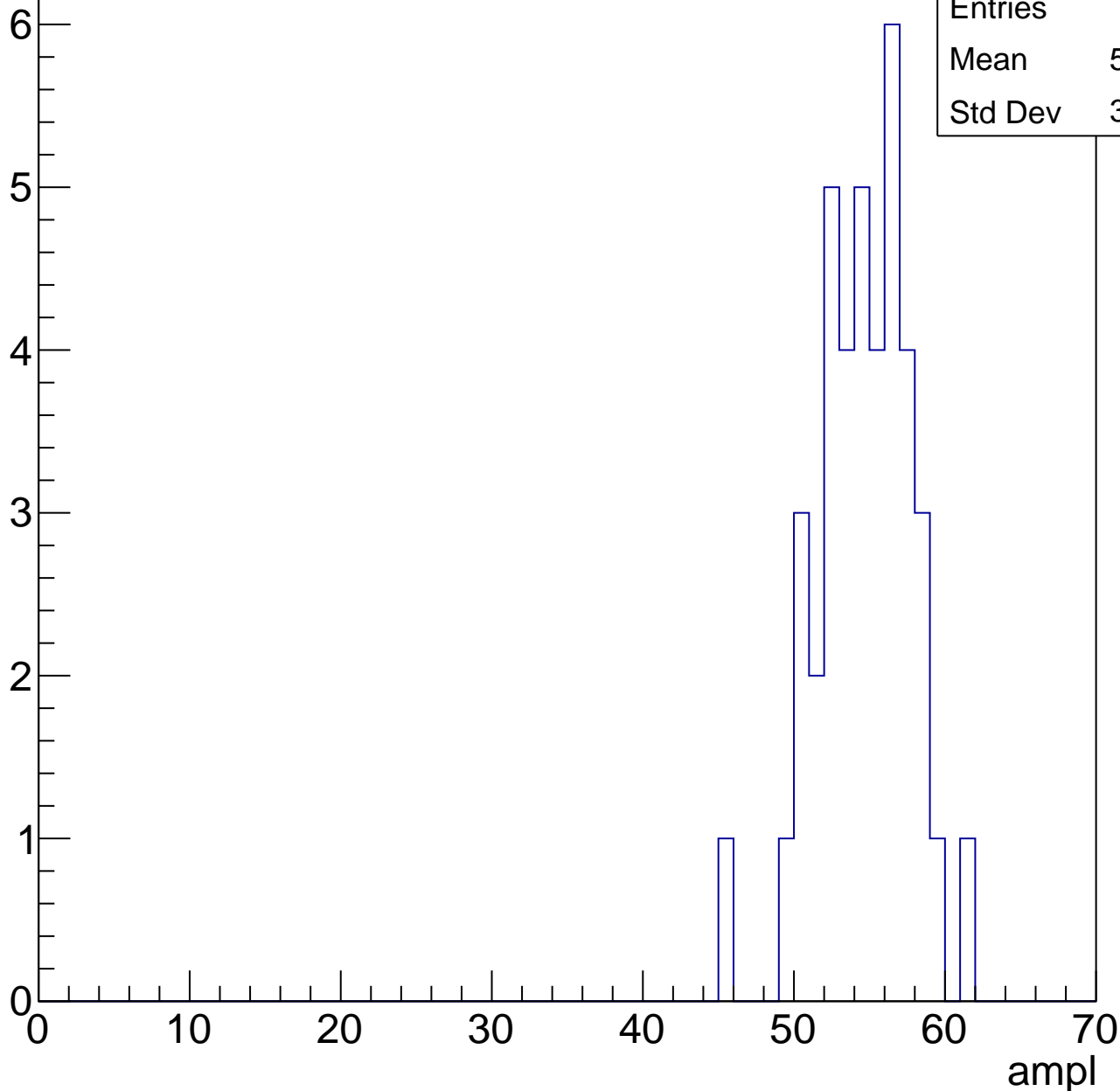


# B1L000S, U2-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

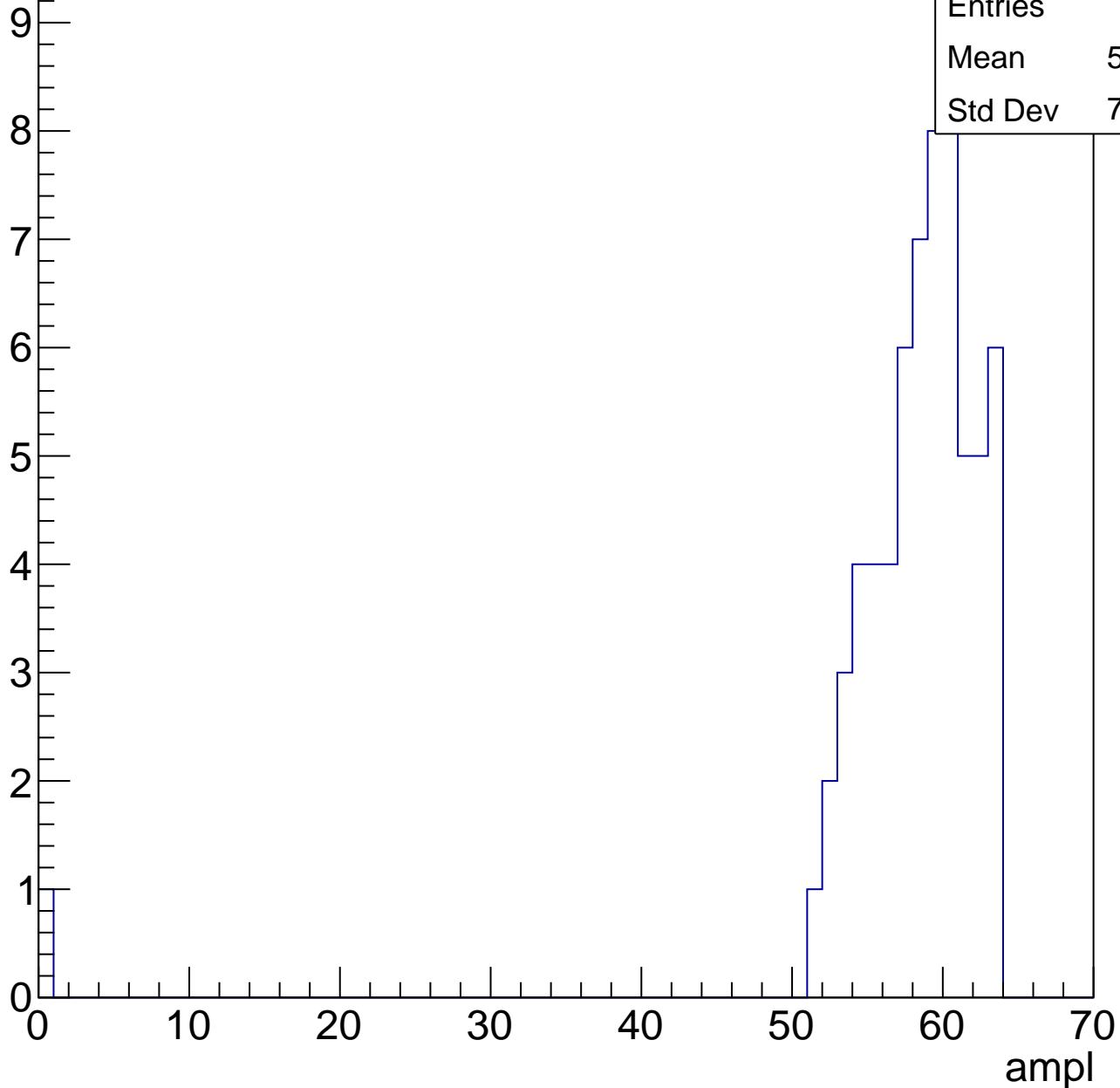
Entries	40
Mean	54.15
Std Dev	3.087



# B1L000S, U2-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

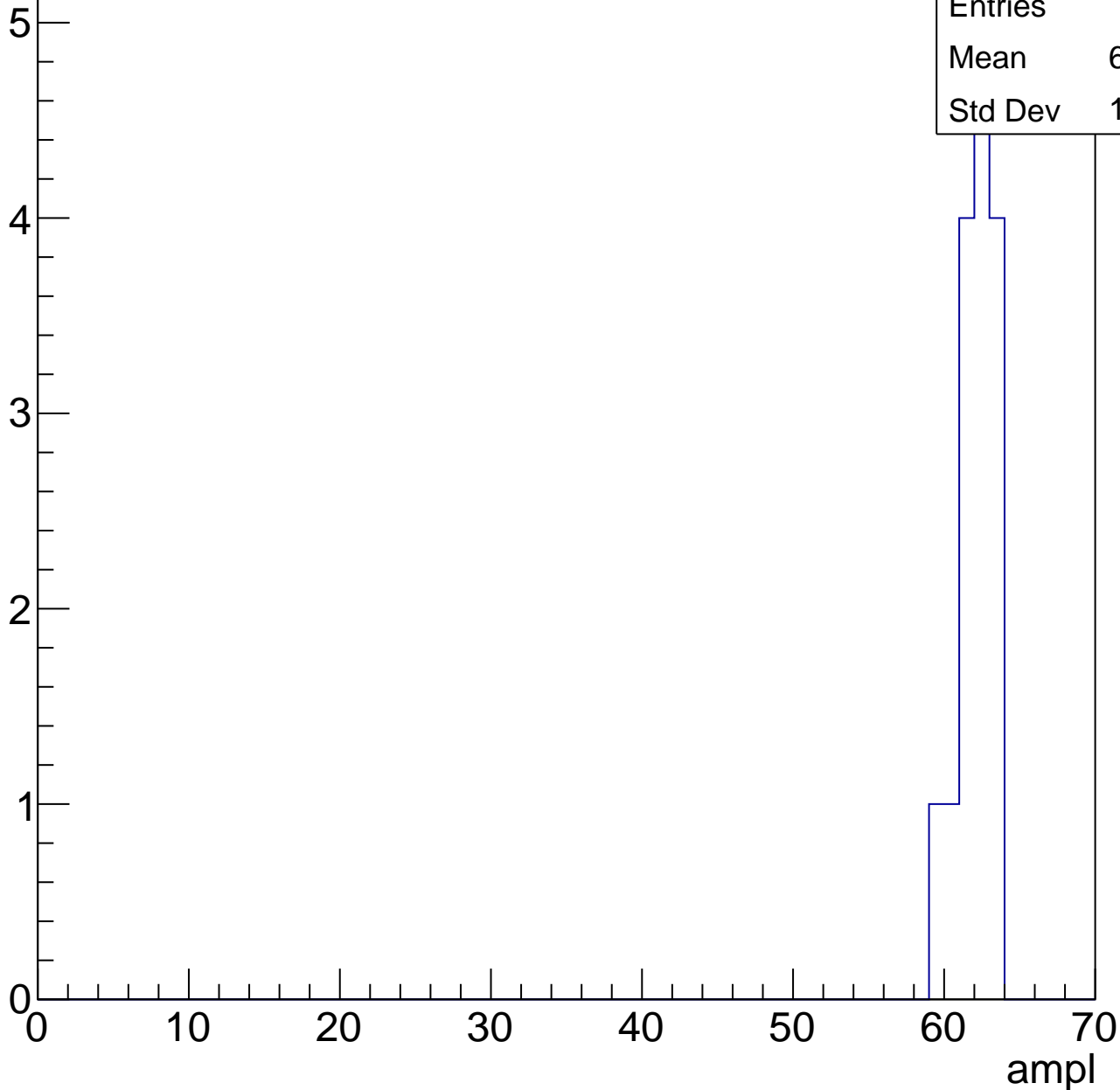


# B1L000S, U2-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.67
Std Dev	1.135





# B1L000S, U2-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U2-ch56, adc0

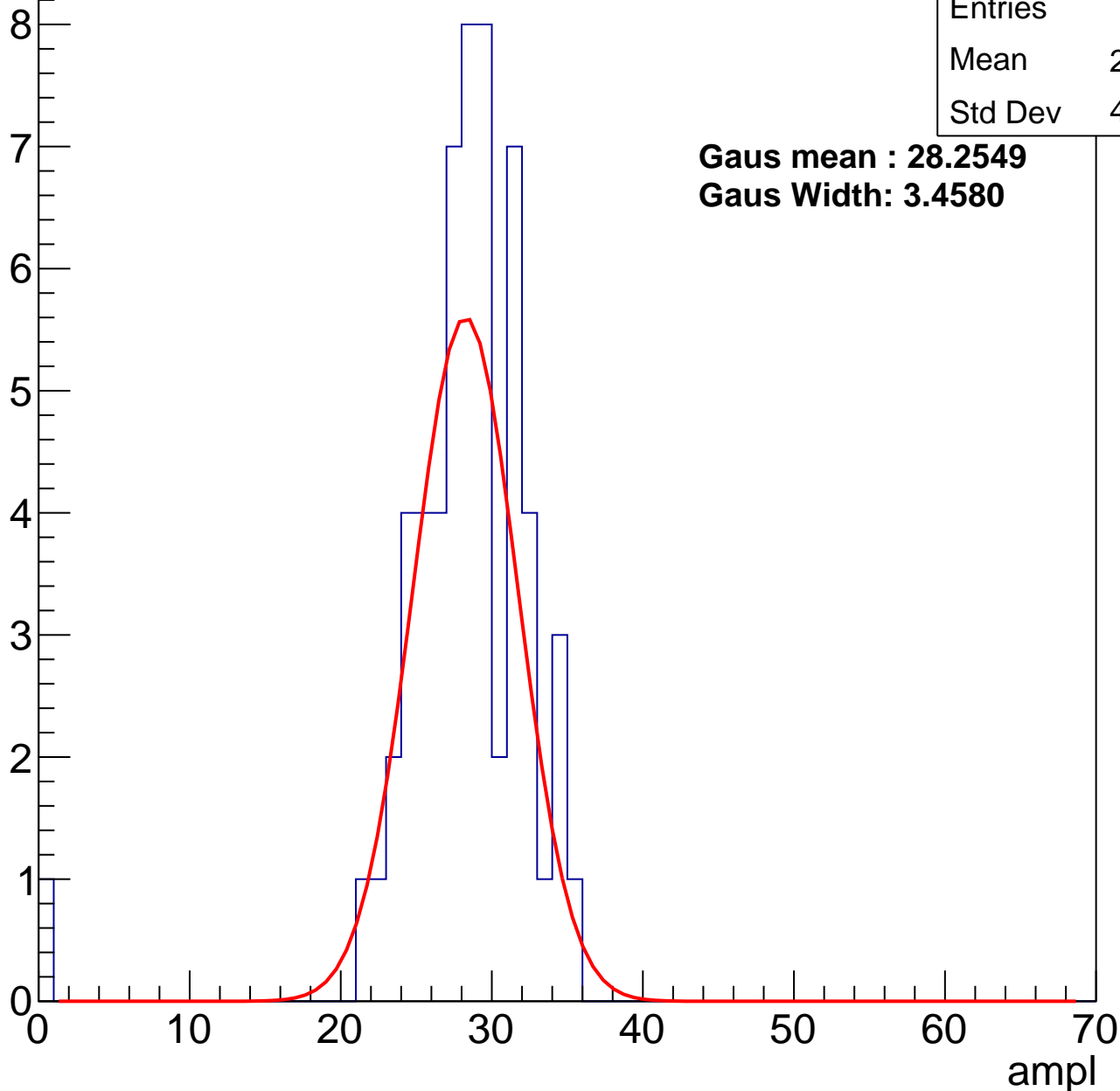
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	27.74
Std Dev	4.848

**Gaus mean : 28.2549**

**Gaus Width: 3.4580**



# B1L000S, U2-ch56, adc1

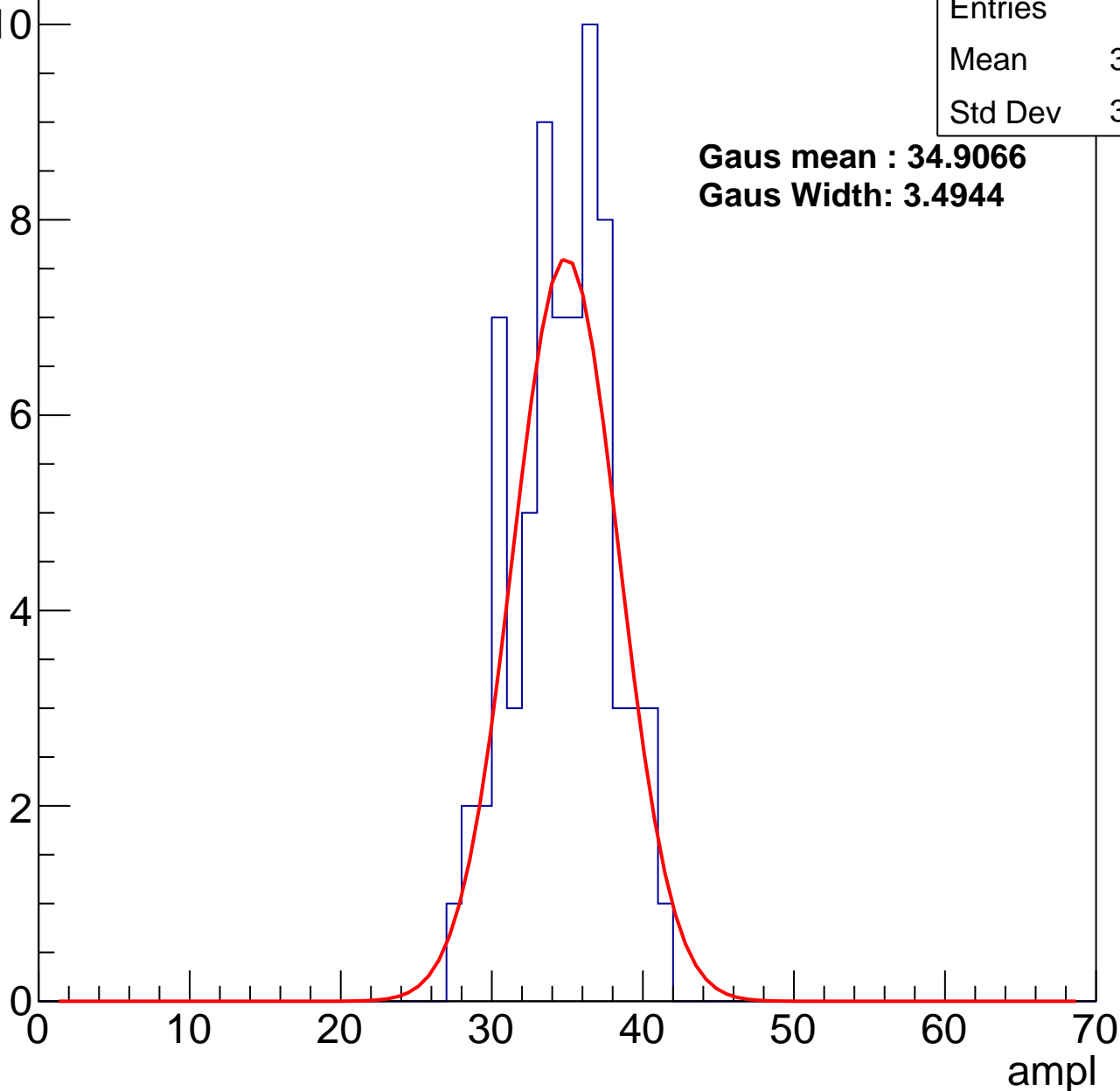
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.25
Std Dev	3.214

**Gaus mean : 34.9066**

**Gaus Width: 3.4944**



# B1L000S, U2-ch56, adc2

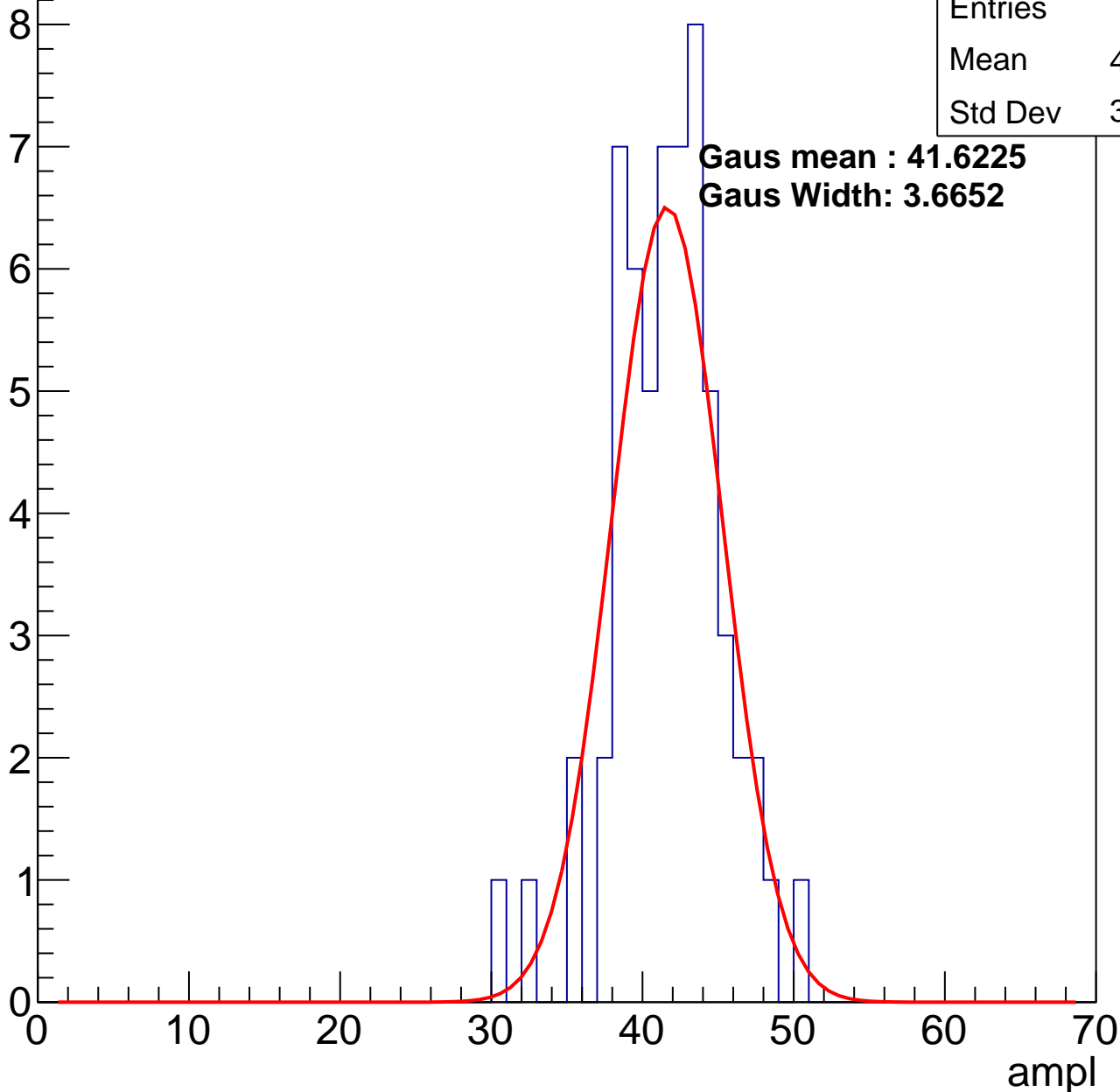
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.17
Std Dev	3.606

**Gaus mean : 41.6225**

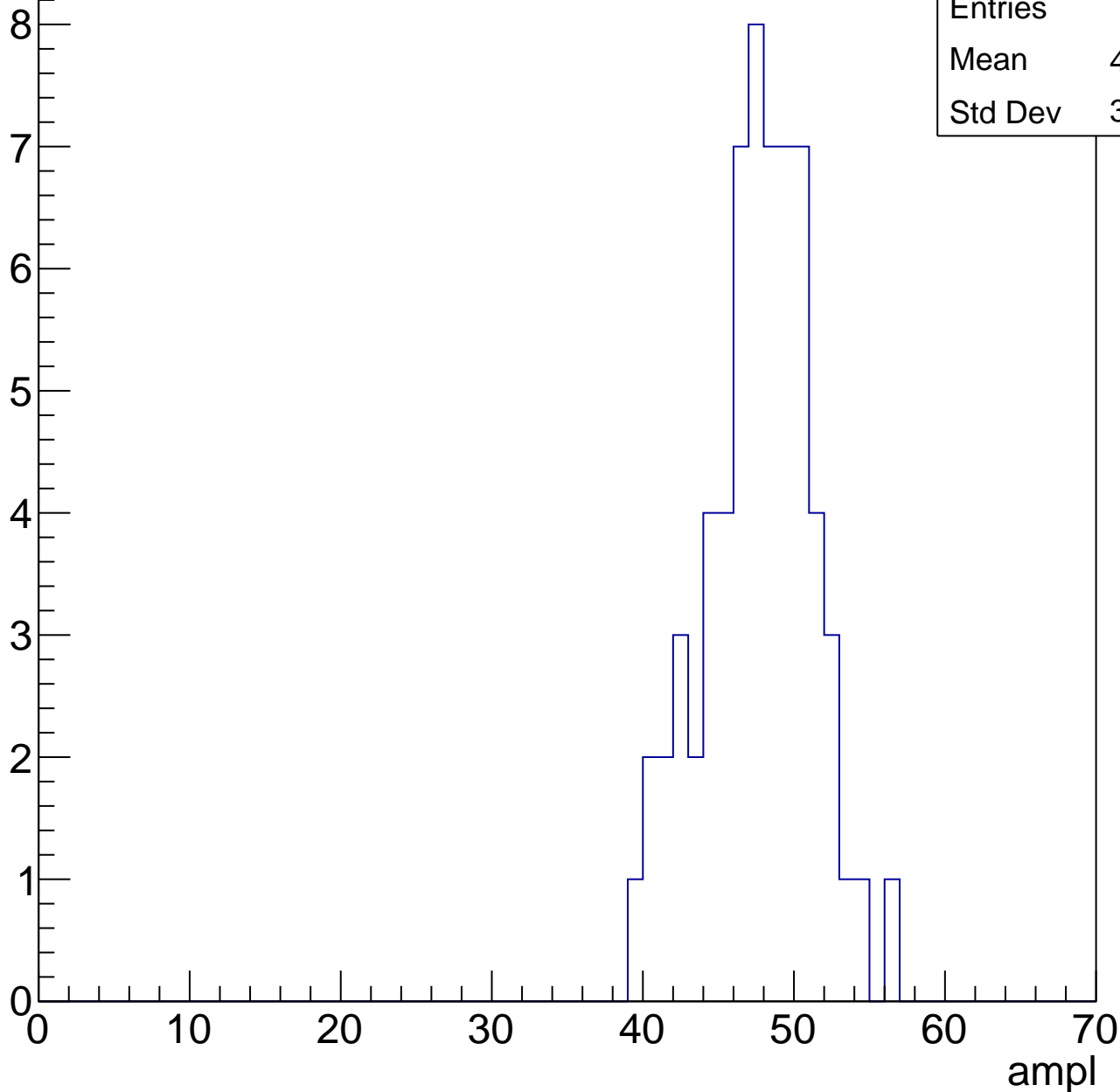
**Gaus Width: 3.6652**



# B1L000S, U2-ch56, adc3

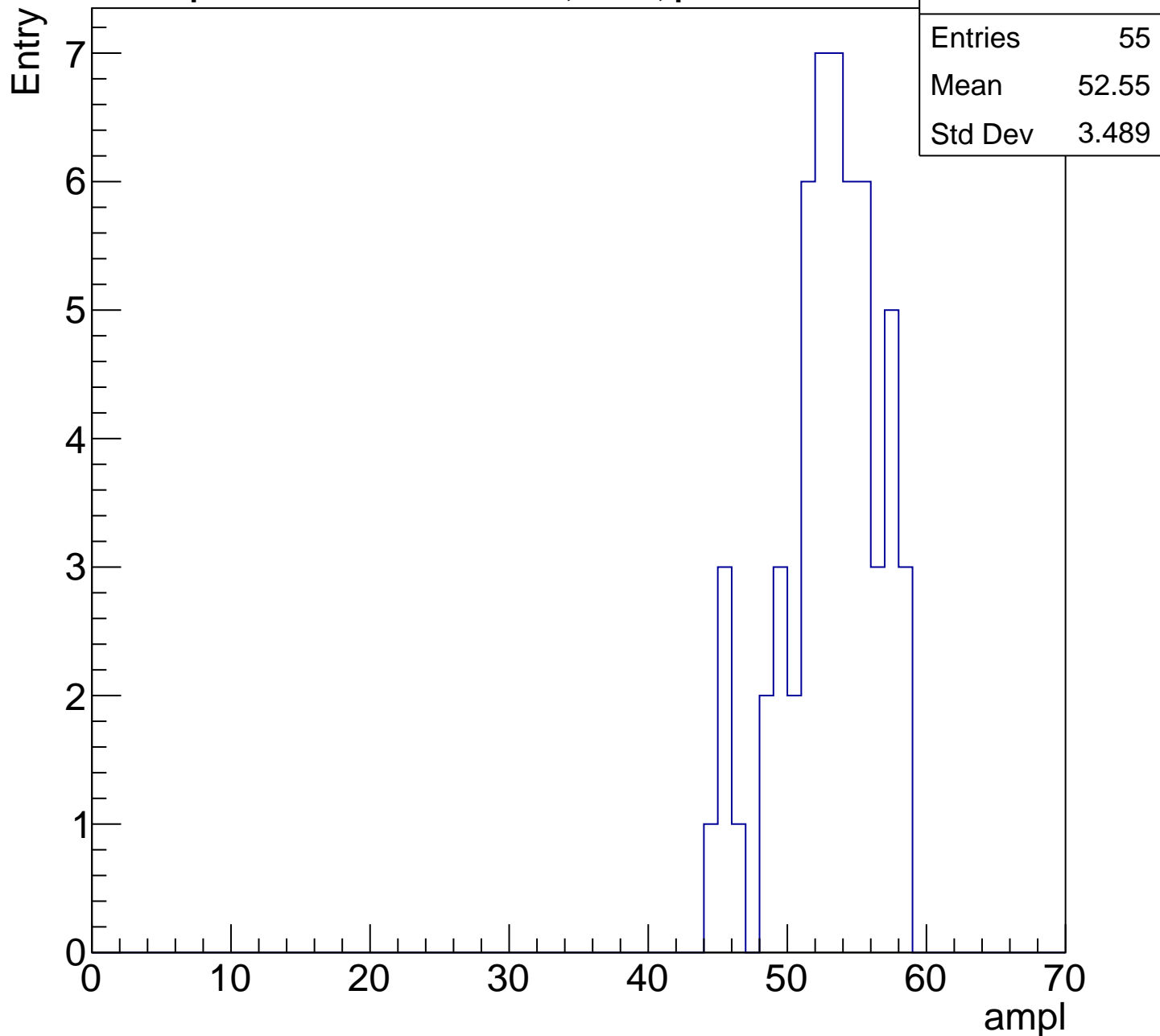
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch56, adc5

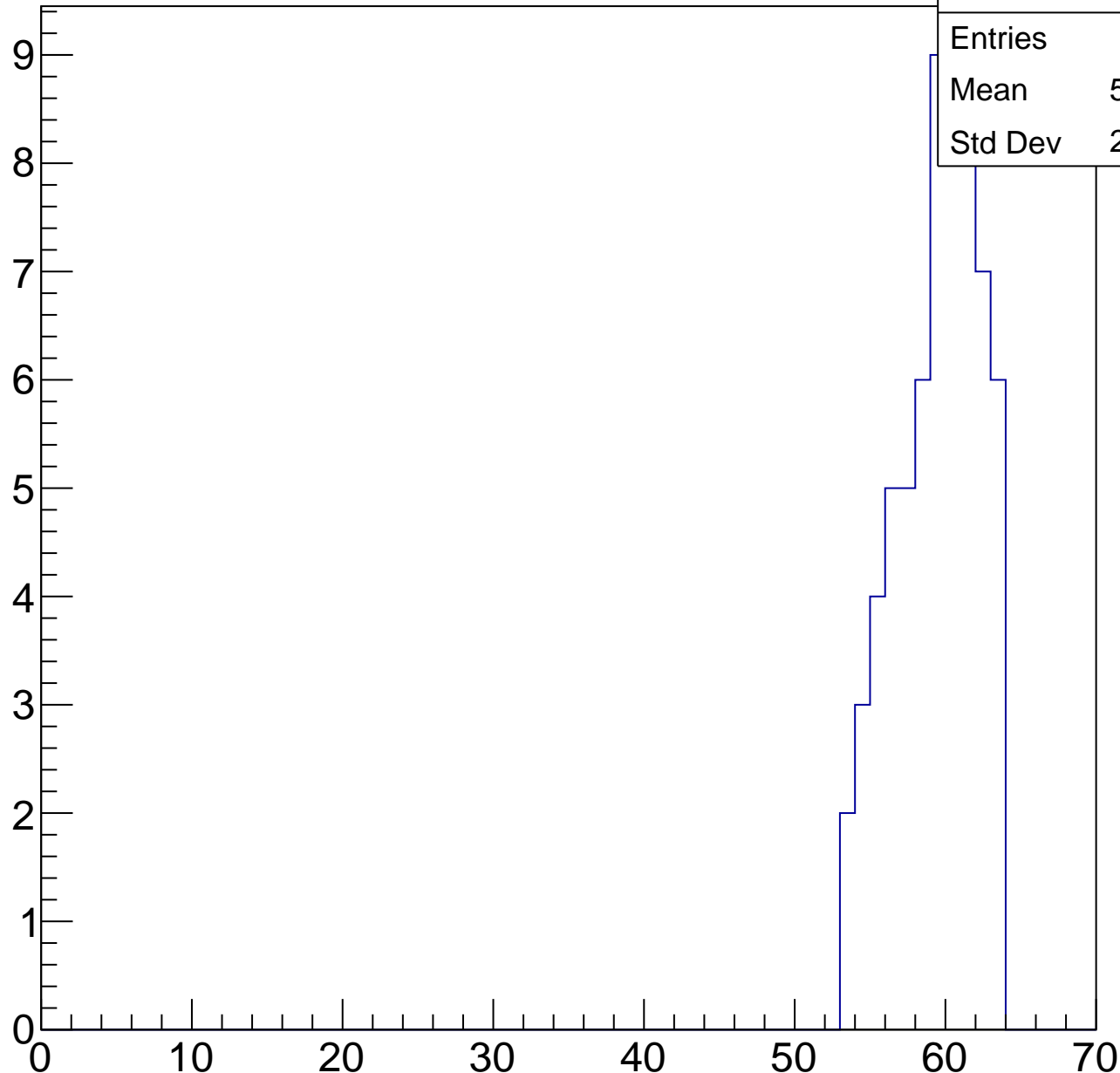
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	58.92
Std Dev	2.762

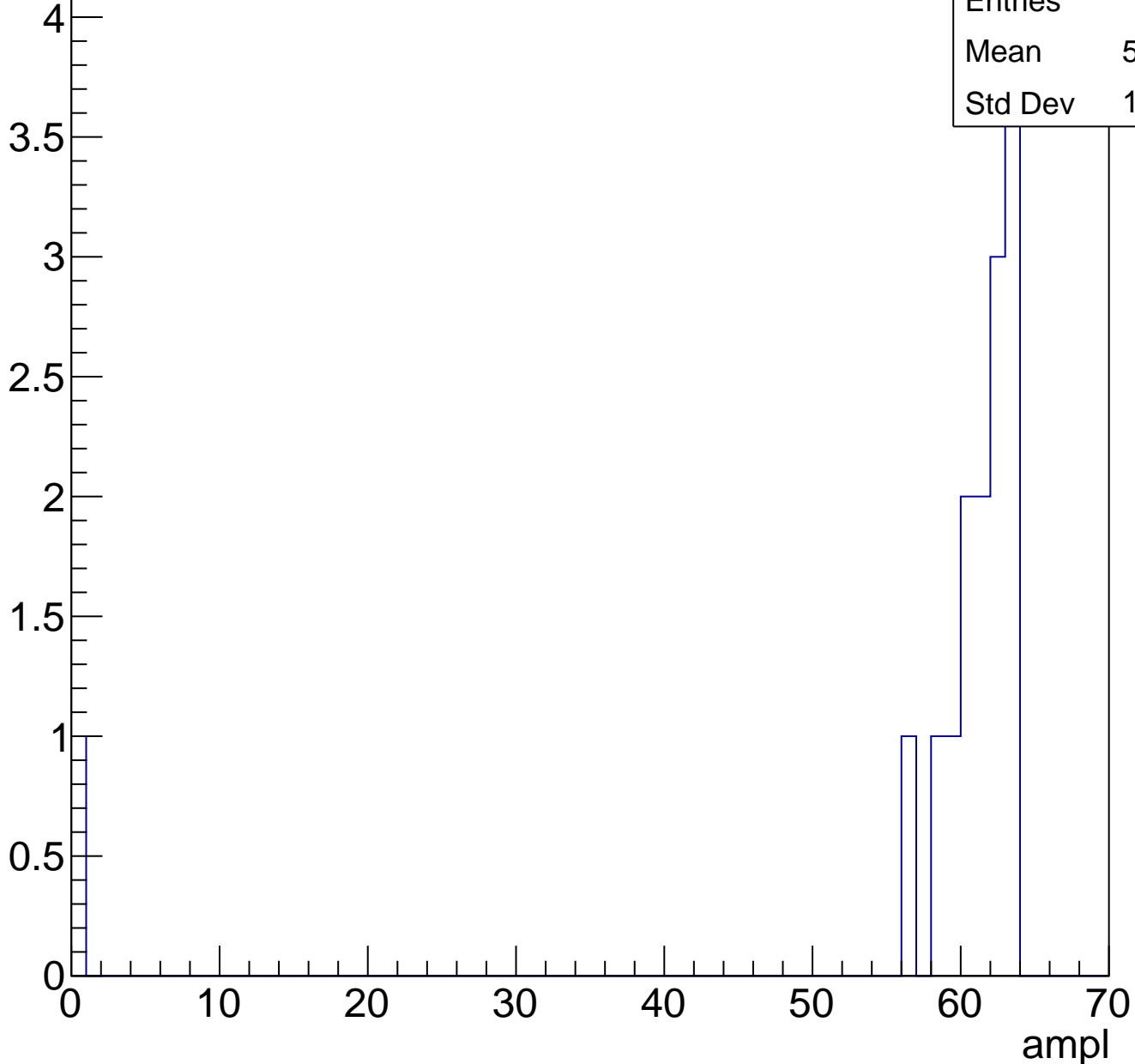
ampl



# B1L000S, U2-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	15
Mean	56.87
Std Dev	15.33



# B1L000S, U2-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U2-ch57, adc0

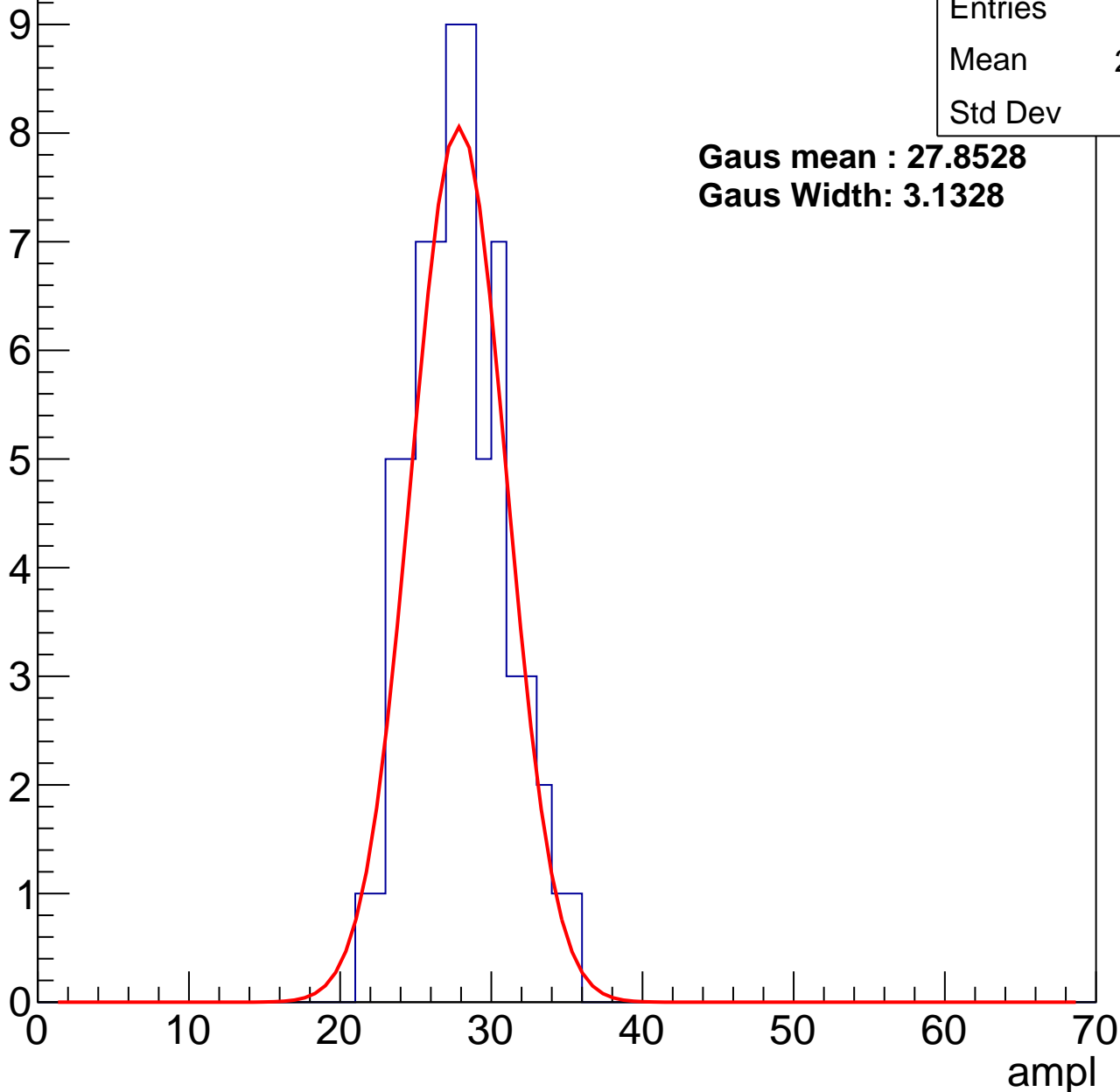
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	27.41
Std Dev	3.04

**Gaus mean : 27.8528**

**Gaus Width: 3.1328**



# B1L000S, U2-ch57, adc1

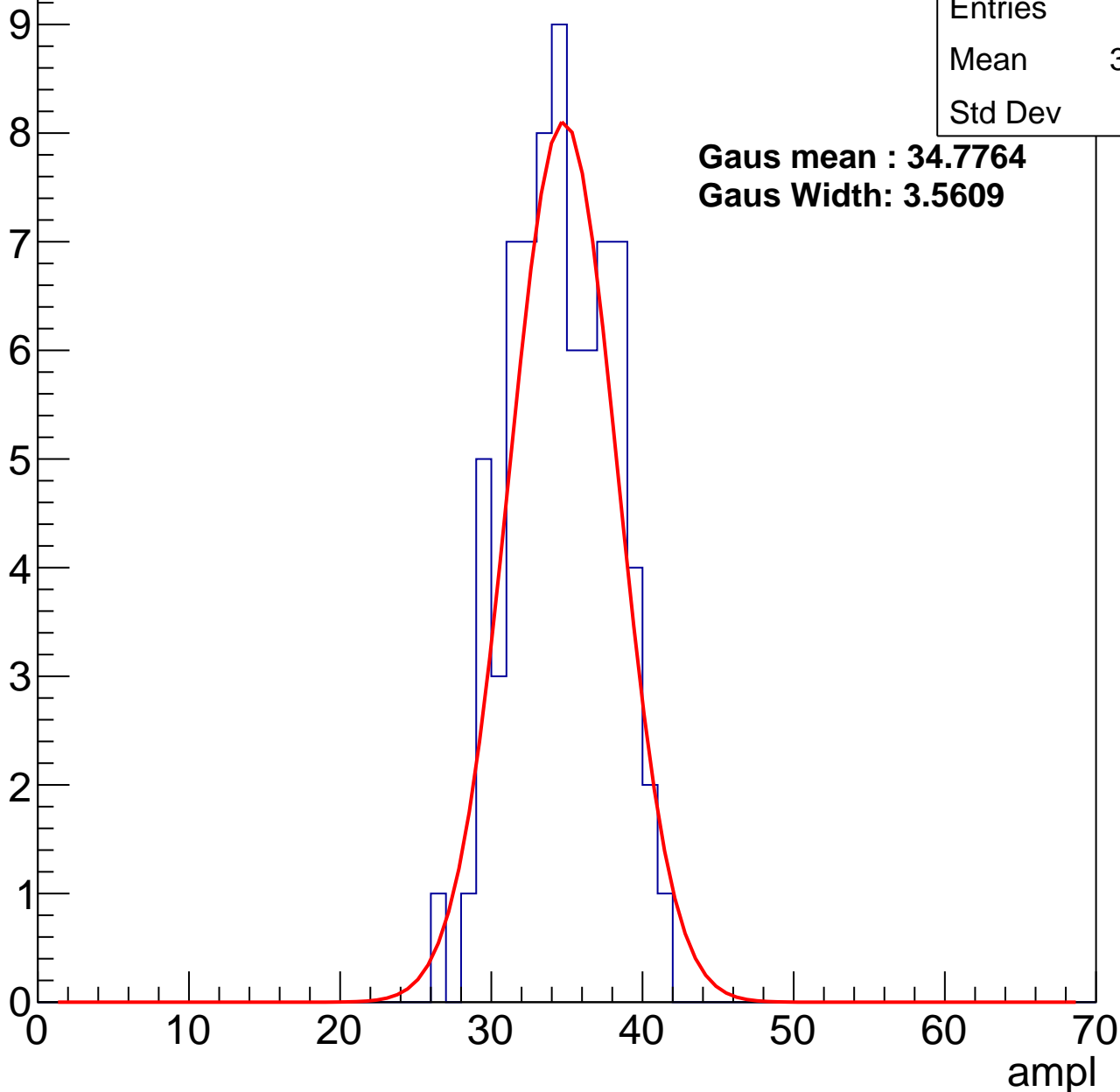
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	34.16
Std Dev	3.28

**Gaus mean : 34.7764**

**Gaus Width: 3.5609**



# B1L000S, U2-ch57, adc2

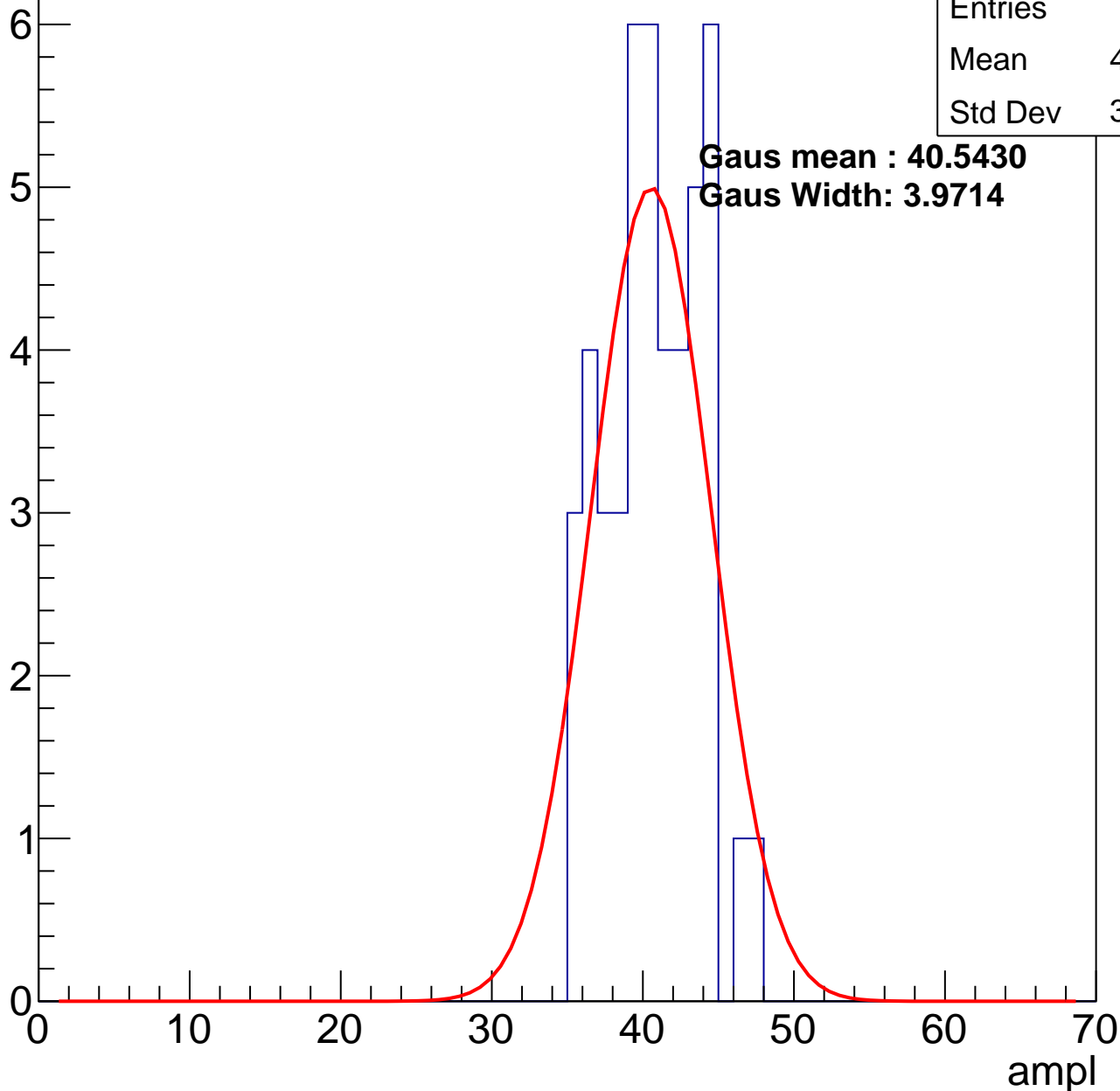
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	40.26
Std Dev	3.046

**Gaus mean : 40.5430**

**Gaus Width: 3.9714**

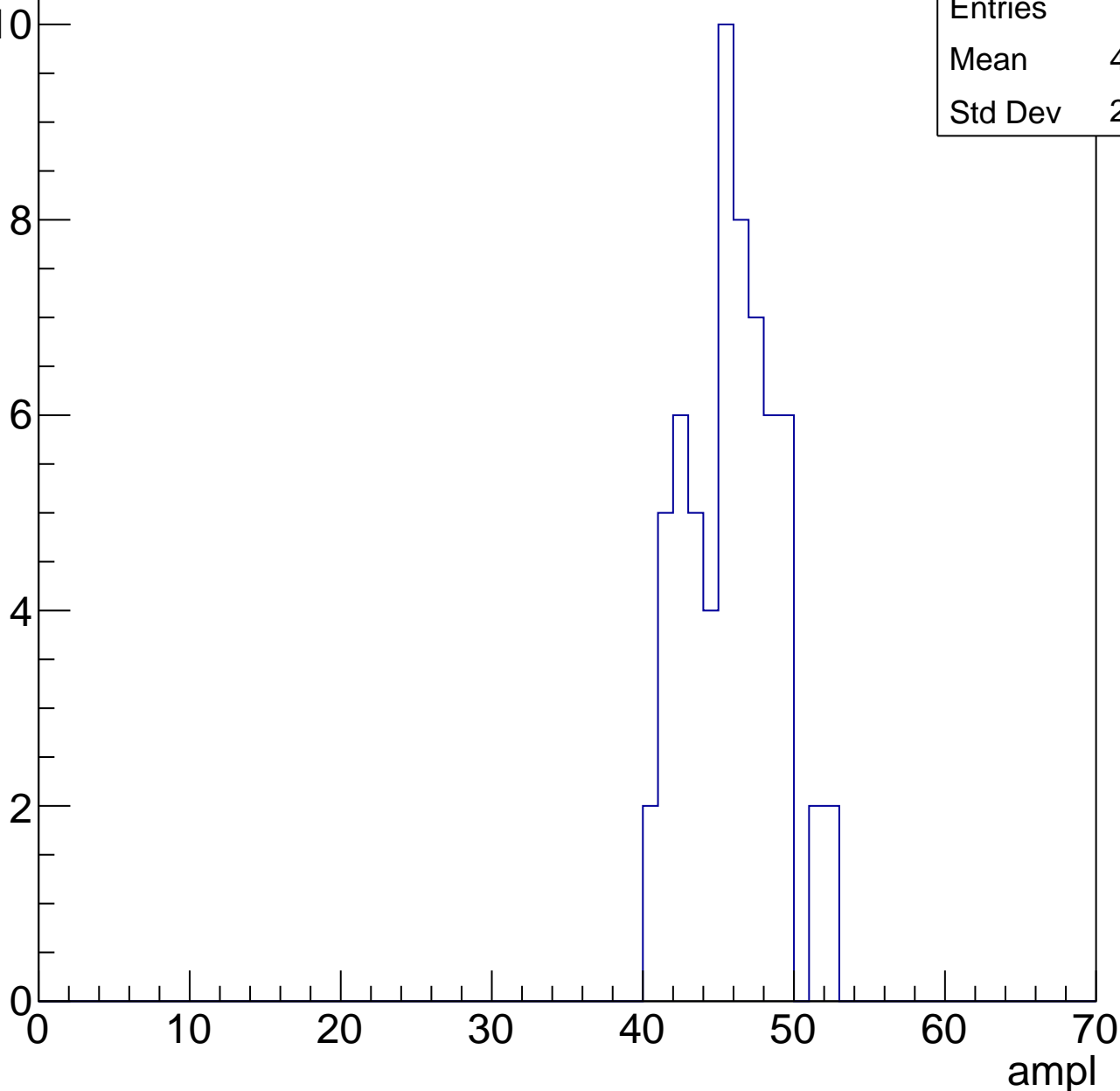


# B1L000S, U2-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

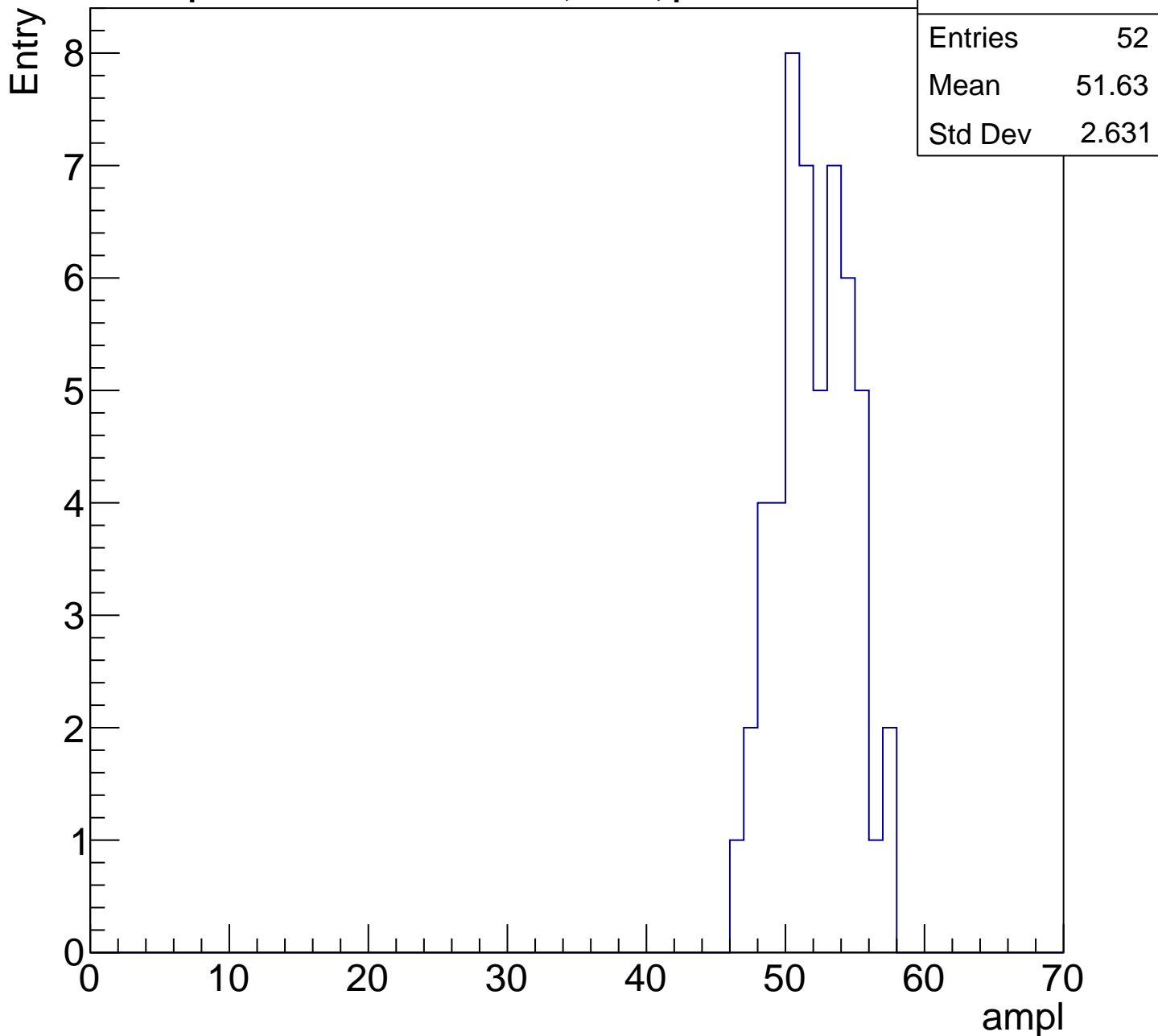
Entry

Entries	63
Mean	45.44
Std Dev	2.959



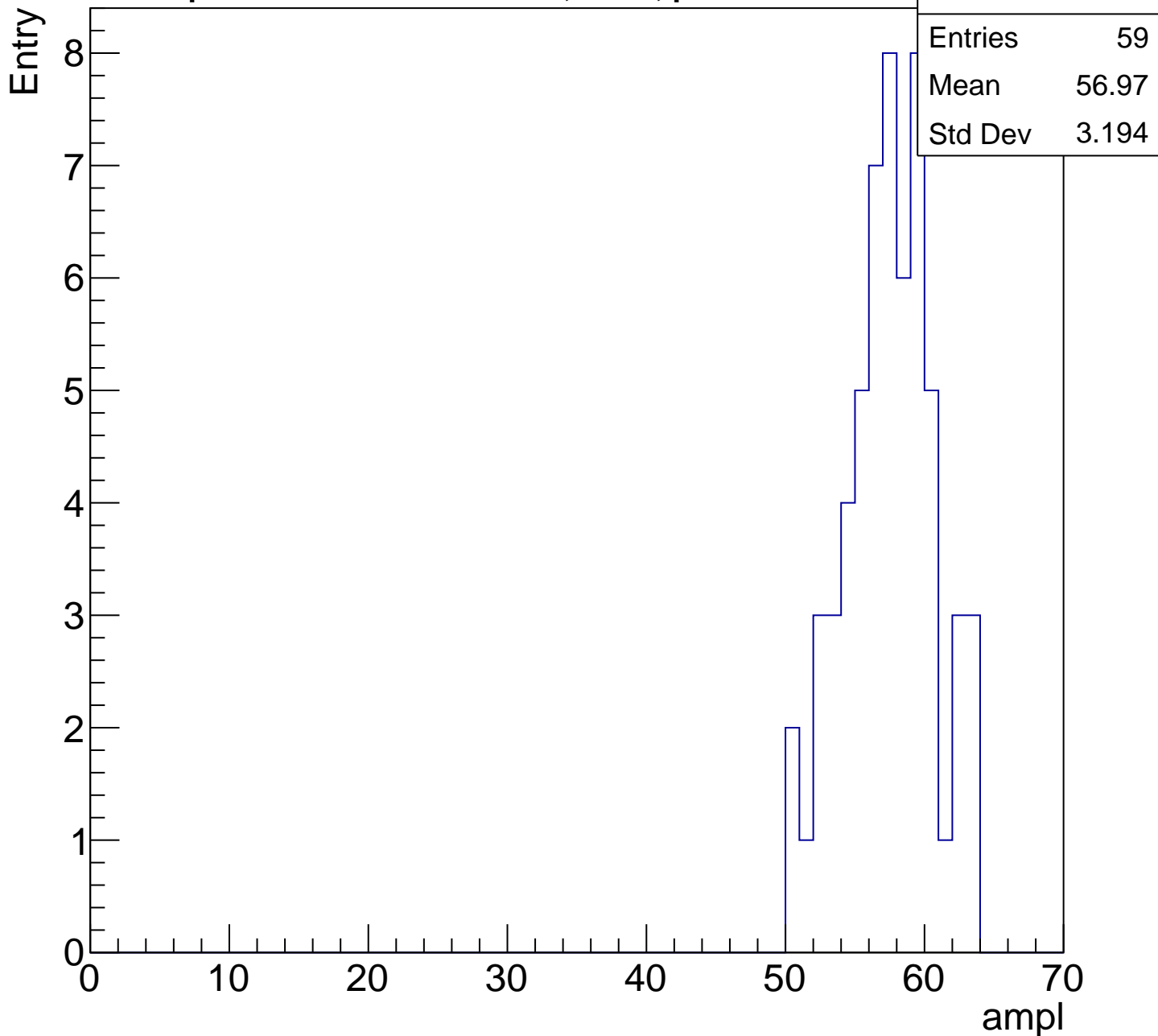
# B1L000S, U2-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

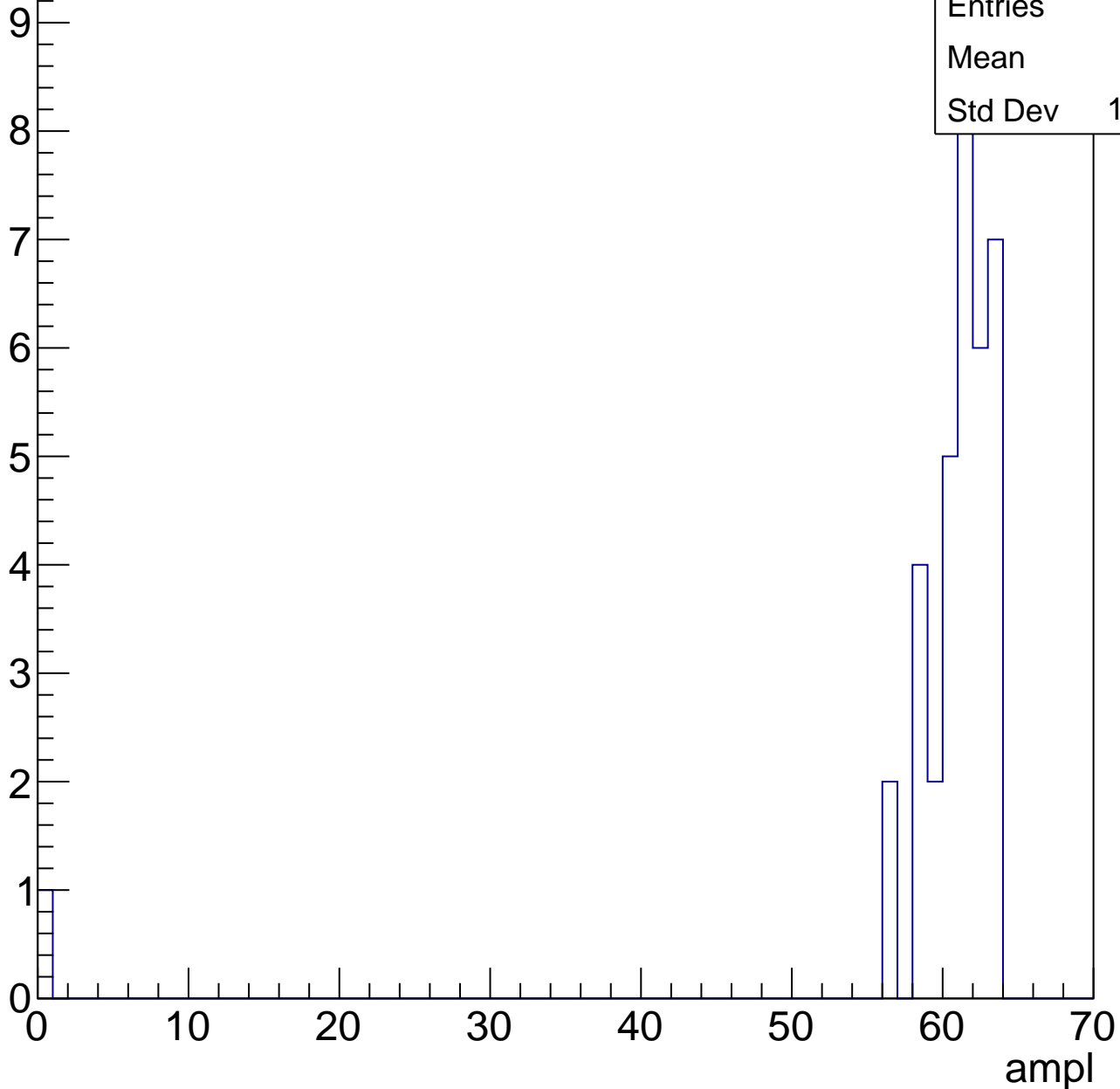


# B1L000S, U2-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	36
Mean	59
Std Dev	10.15

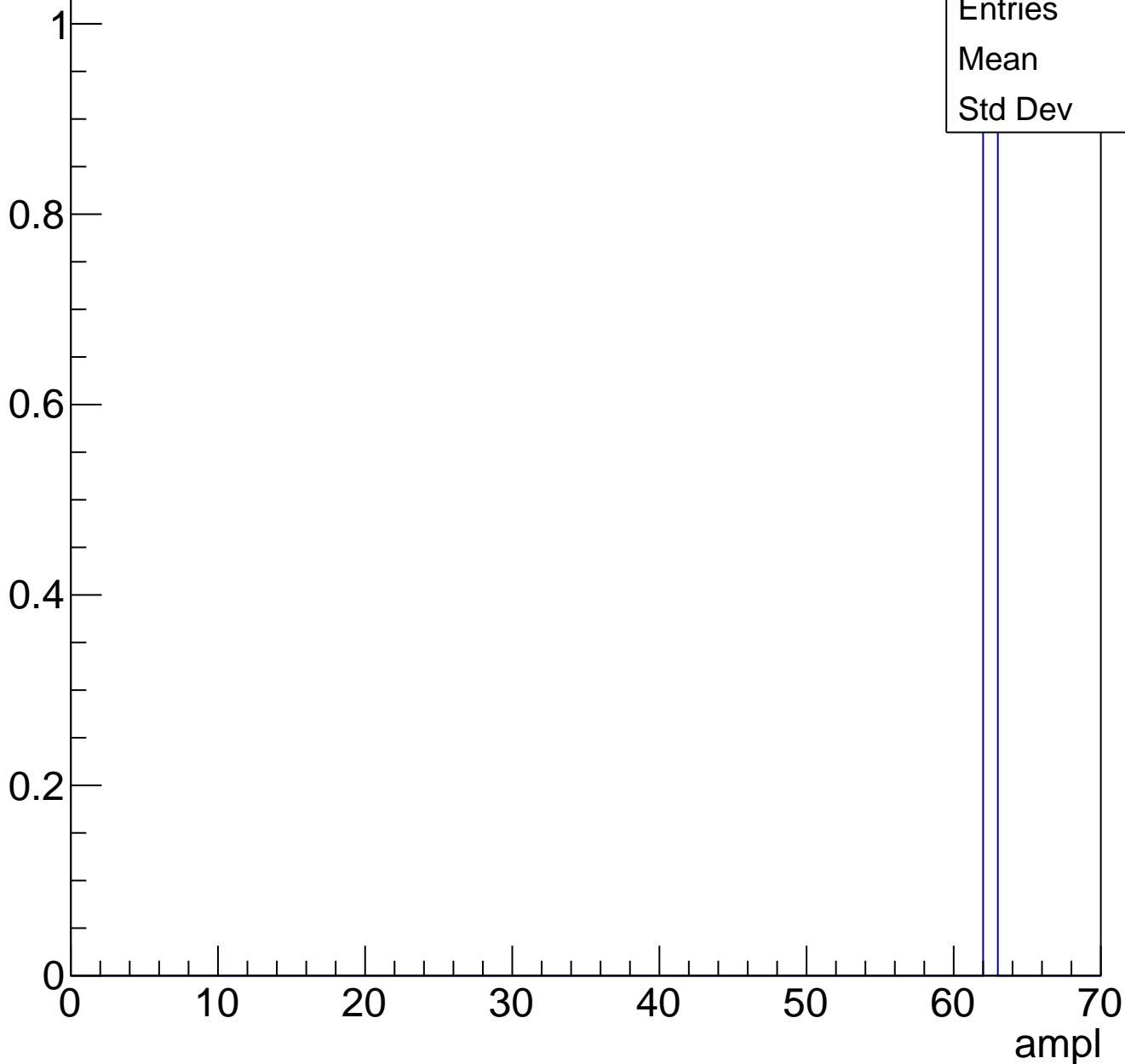




# B1L000S, U2-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch58, adc0

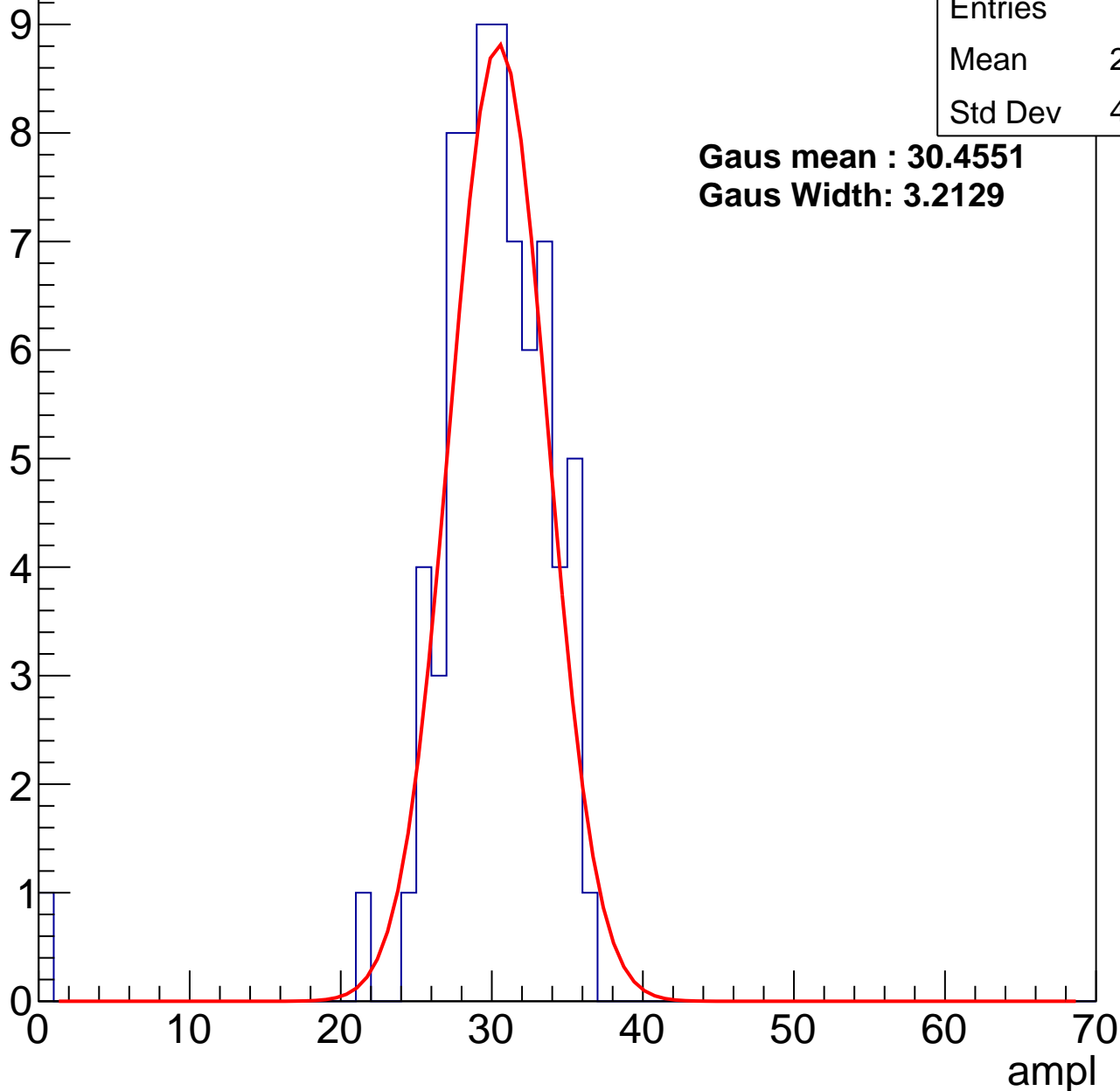
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	29.47
Std Dev	4.616

**Gaus mean : 30.4551**

**Gaus Width: 3.2129**



# B1L000S, U2-ch58, adc1

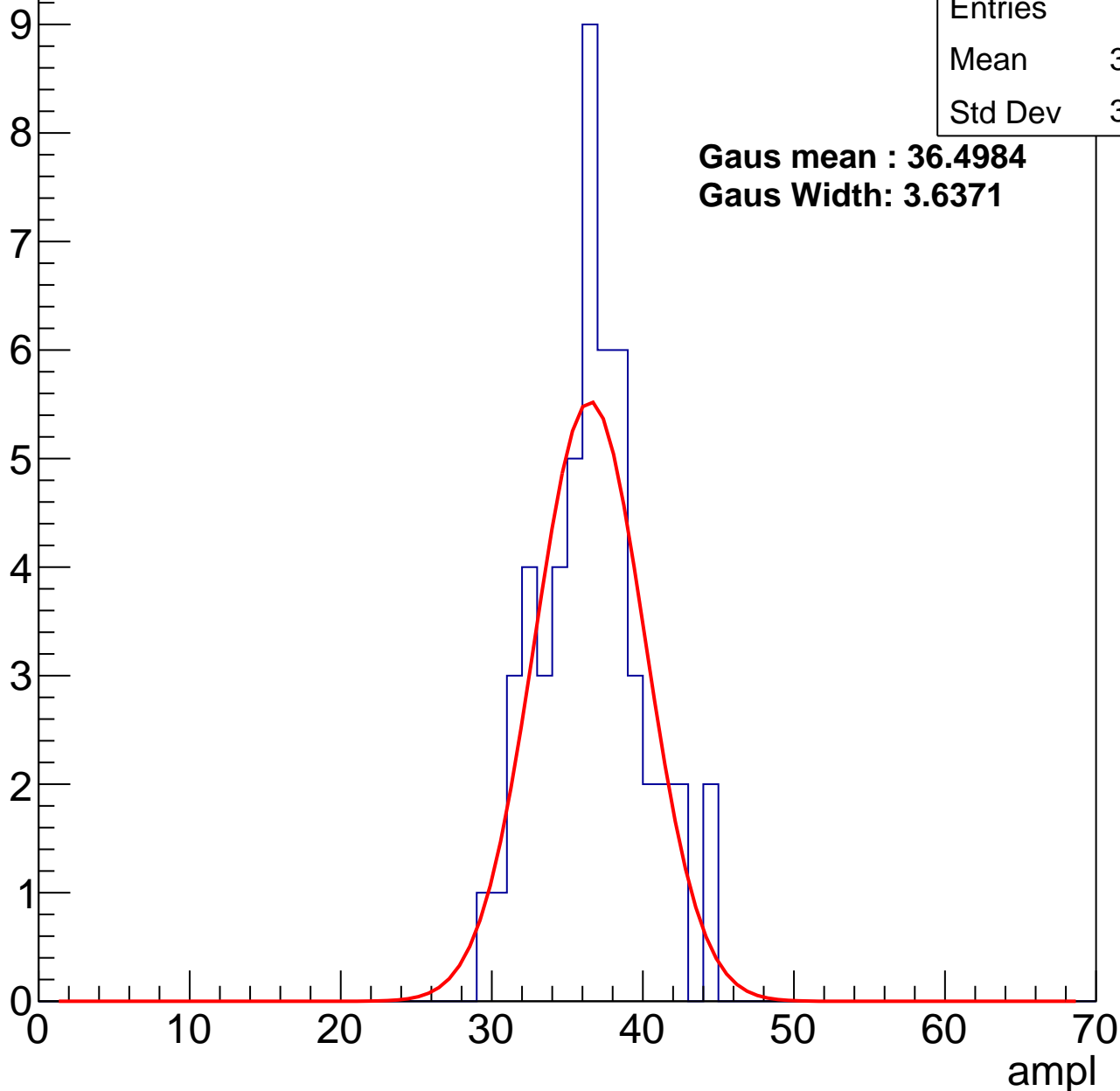
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	36.13
Std Dev	3.393

**Gaus mean : 36.4984**

**Gaus Width: 3.6371**



# B1L000S, U2-ch58, adc2

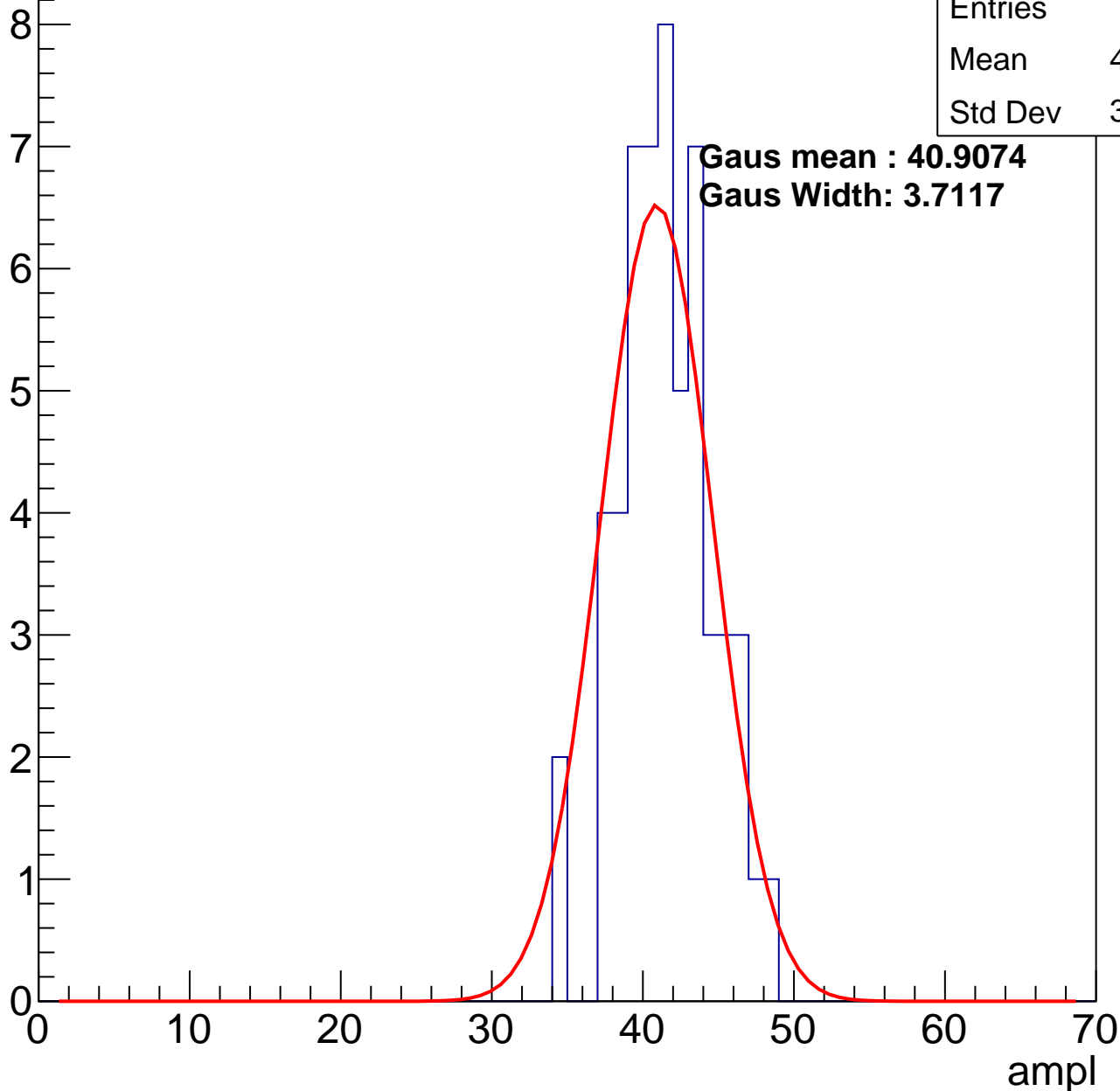
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.09
Std Dev	3.017

**Gaus mean : 40.9074**

**Gaus Width: 3.7117**

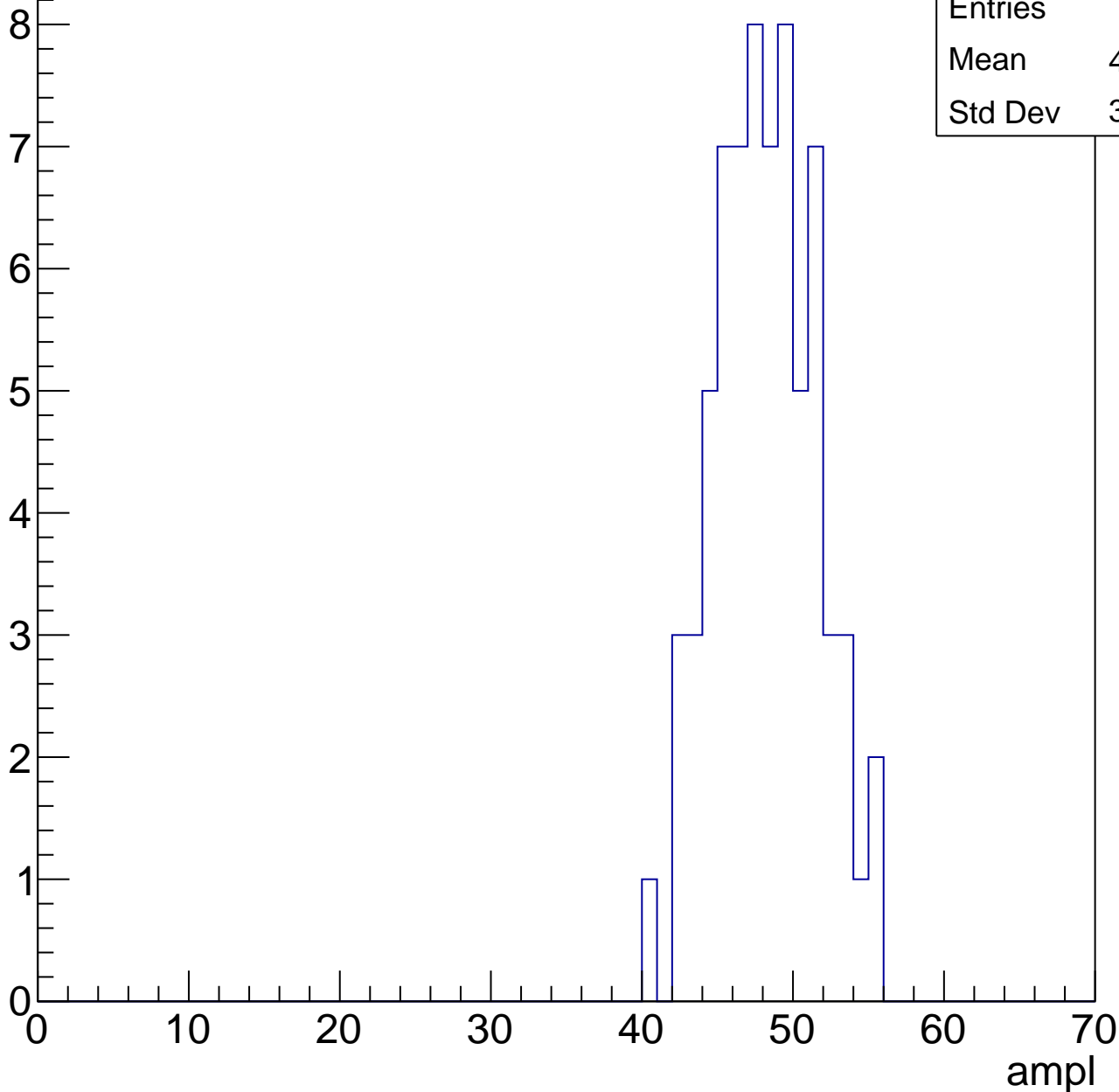


# B1L000S, U2-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	47.74
Std Dev	3.315

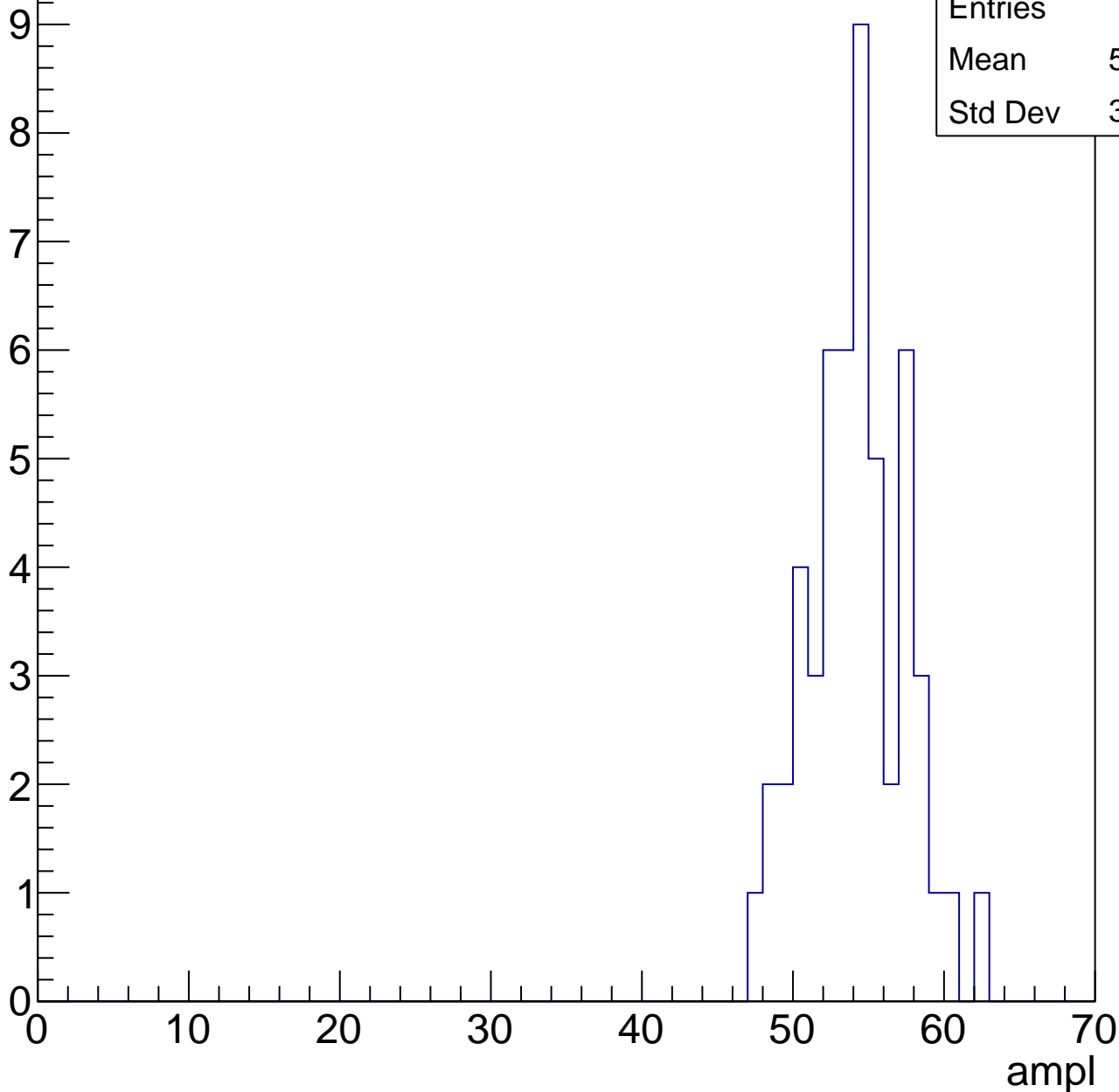


# B1L000S, U2-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

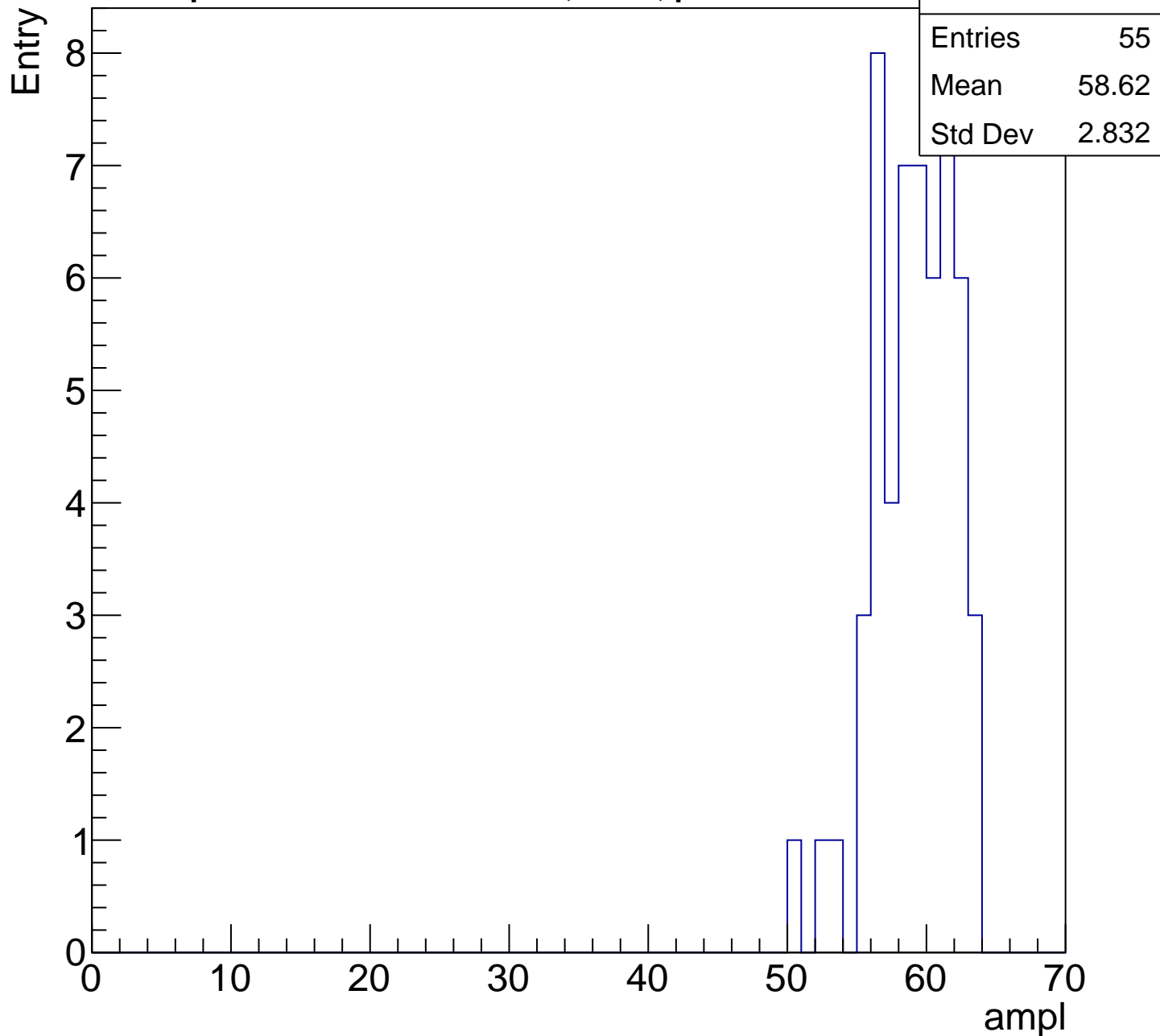
Entry

Entries	52
Mean	53.73
Std Dev	3.187



# B1L000S, U2-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

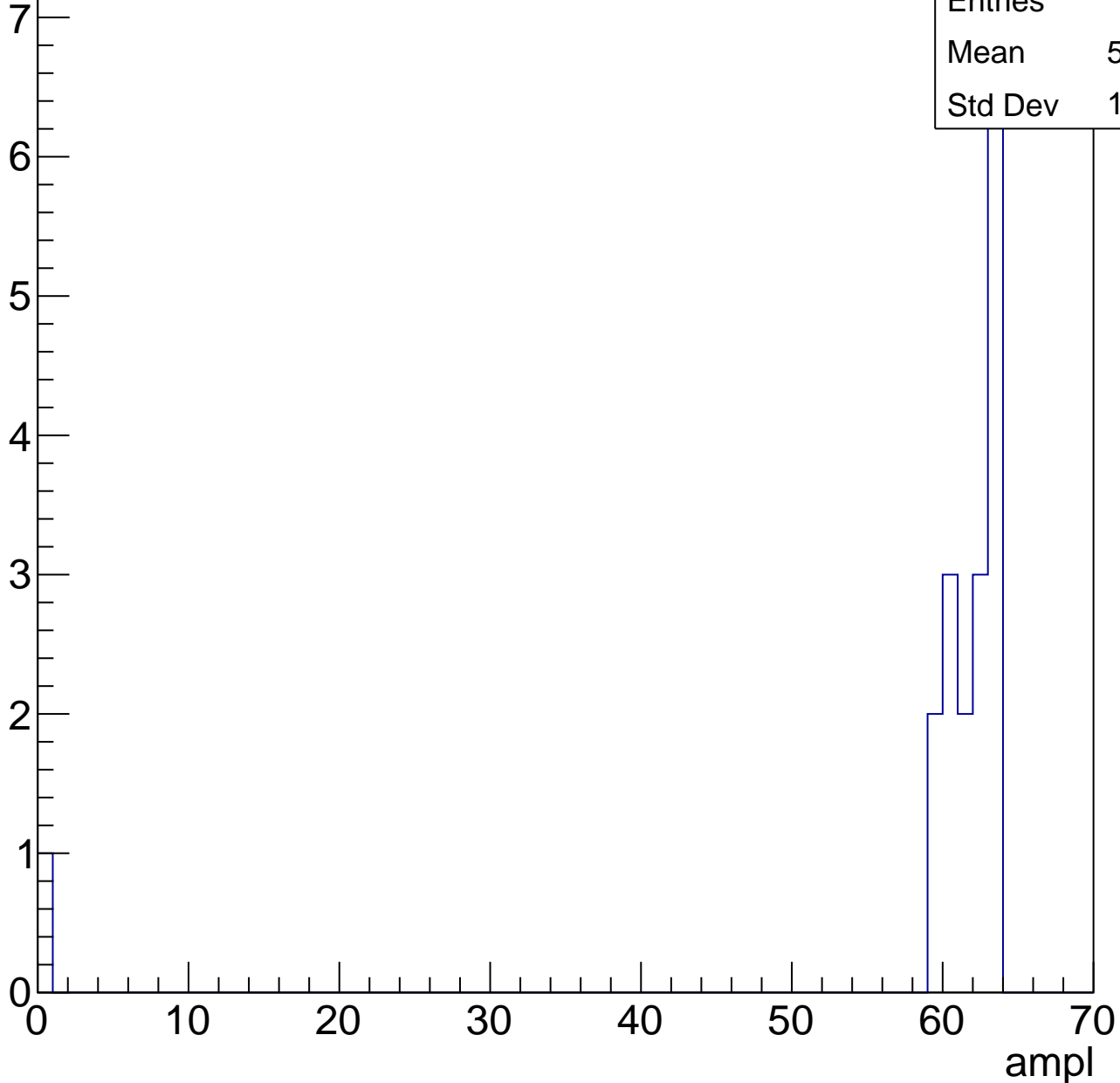


# B1L000S, U2-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.17
Std Dev	14.18





# B1L000S, U2-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch59, adc0

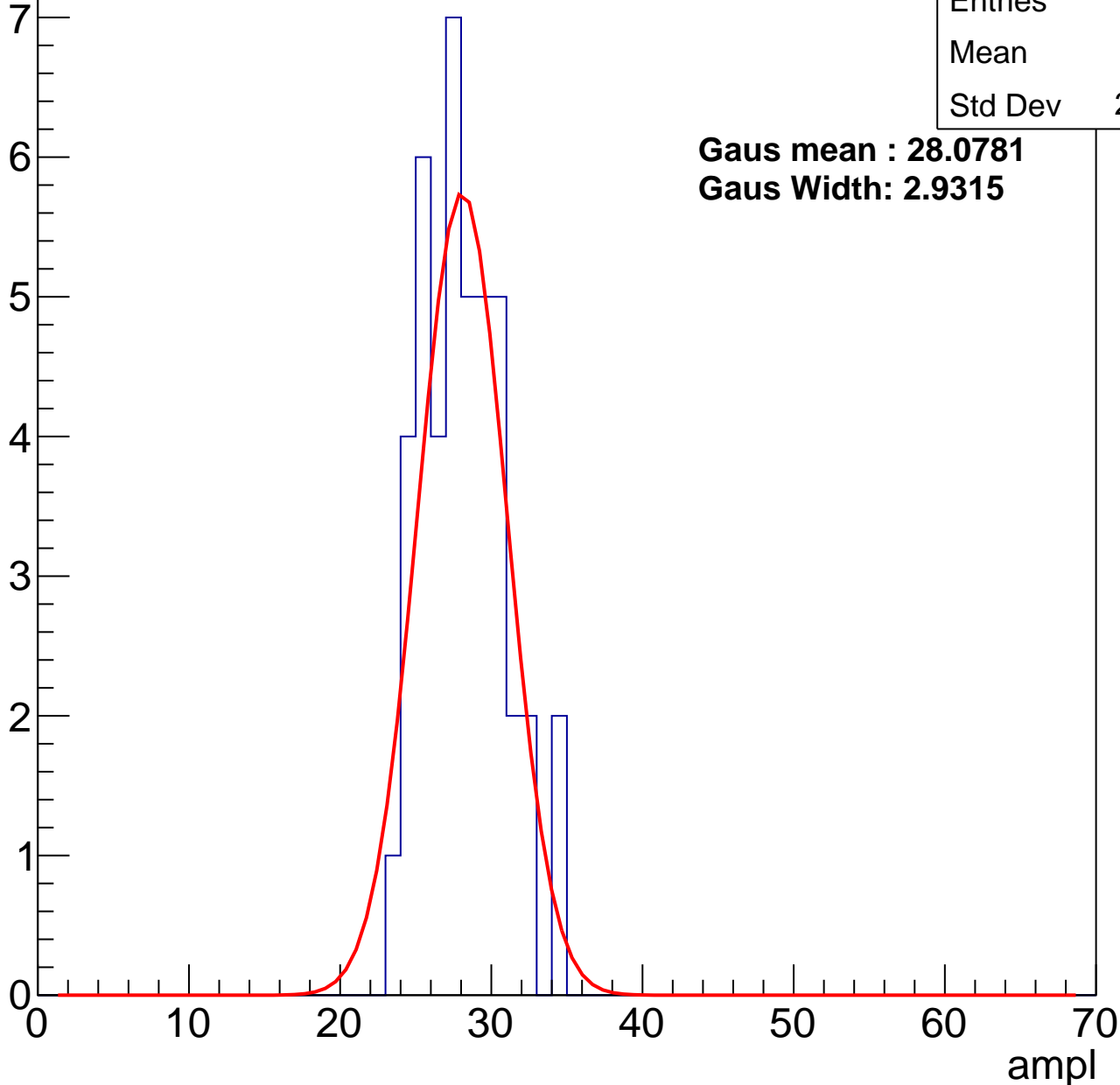
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	27.7
Std Dev	2.681

**Gaus mean : 28.0781**

**Gaus Width: 2.9315**



# B1L000S, U2-ch59, adc1

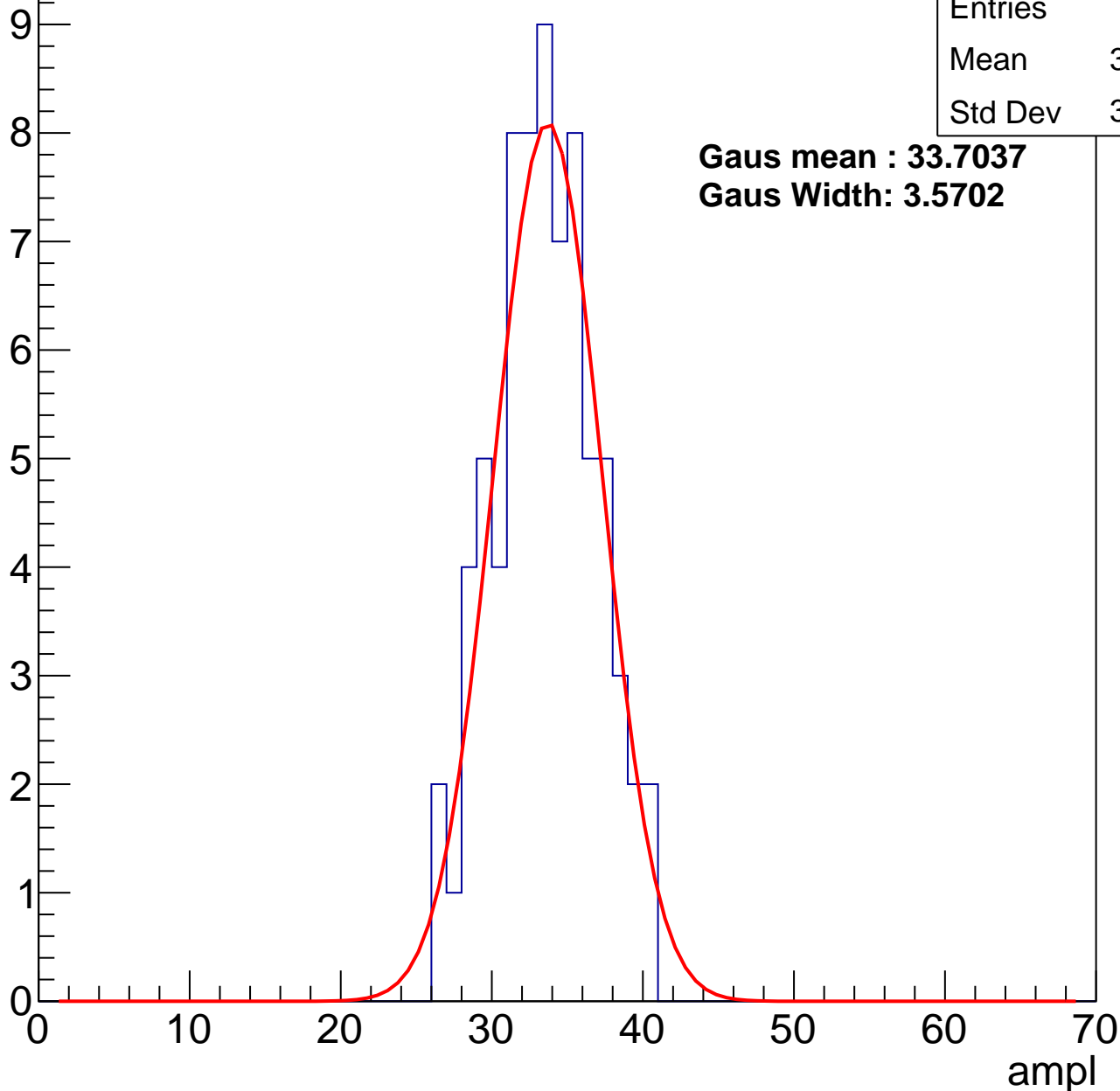
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	33.04
Std Dev	3.308

**Gaus mean : 33.7037**

**Gaus Width: 3.5702**



# B1L000S, U2-ch59, adc2

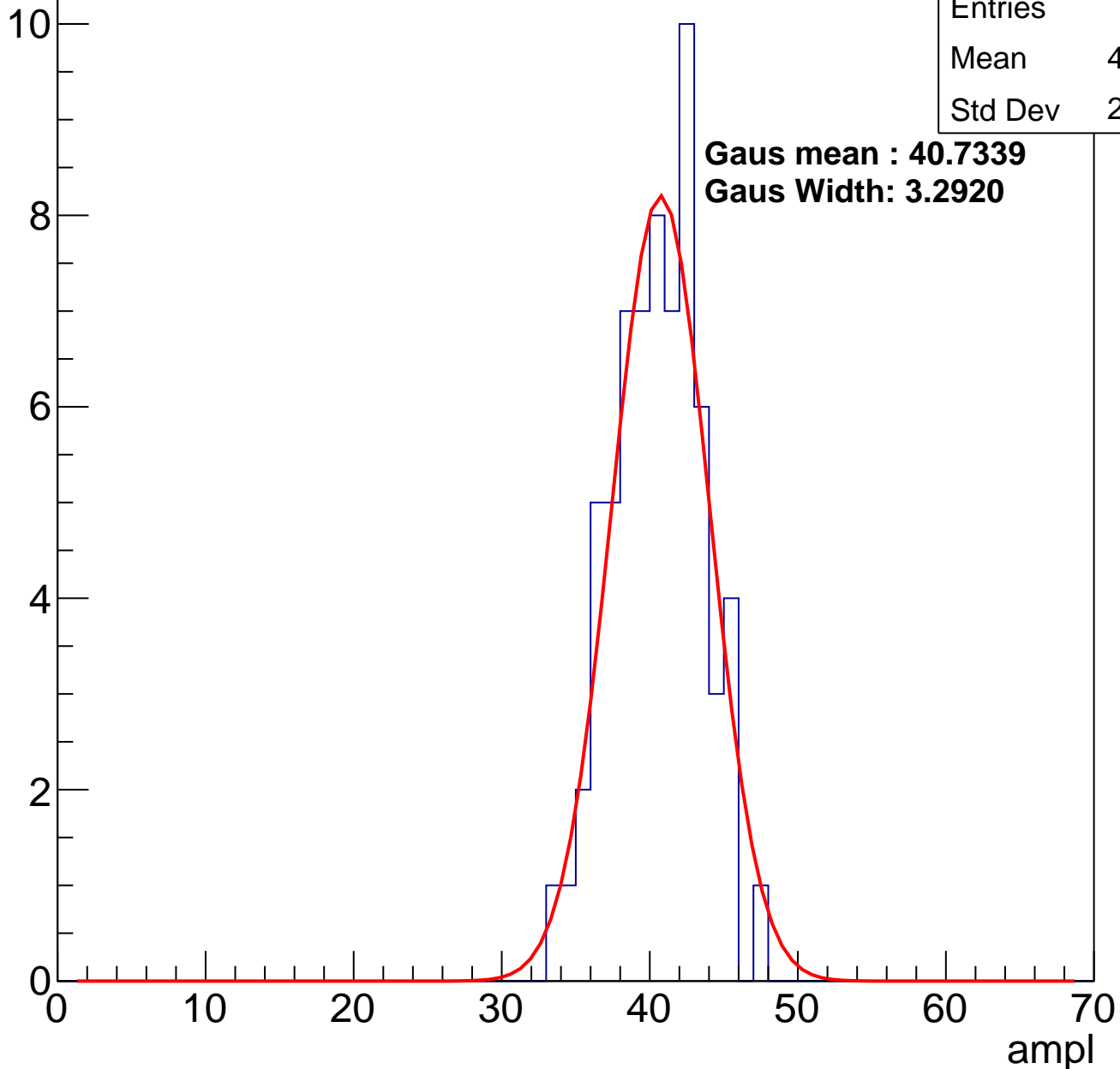
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	40.07
Std Dev	2.974

**Gaus mean : 40.7339**

**Gaus Width: 3.2920**

Entry

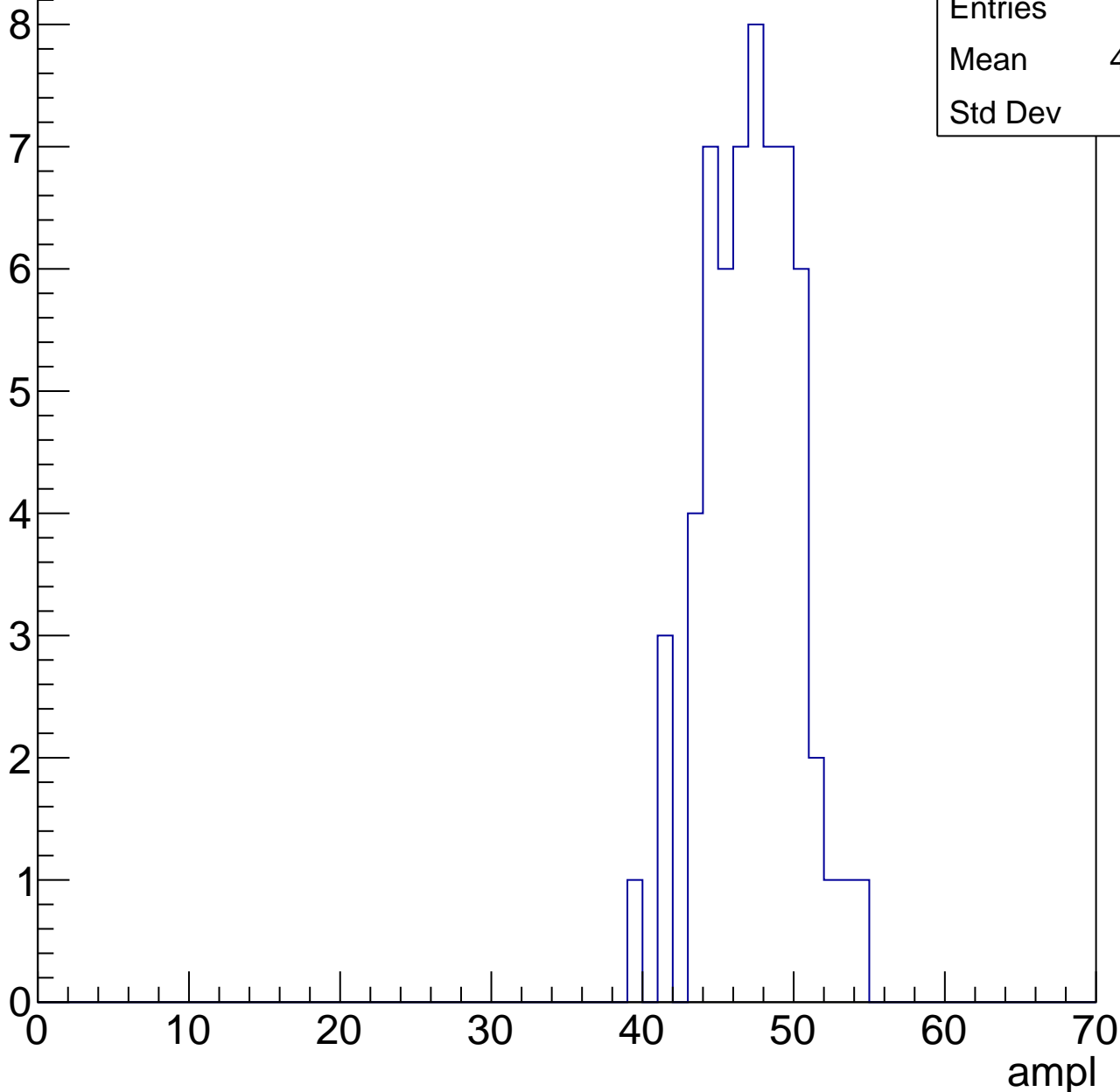


# B1L000S, U2-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

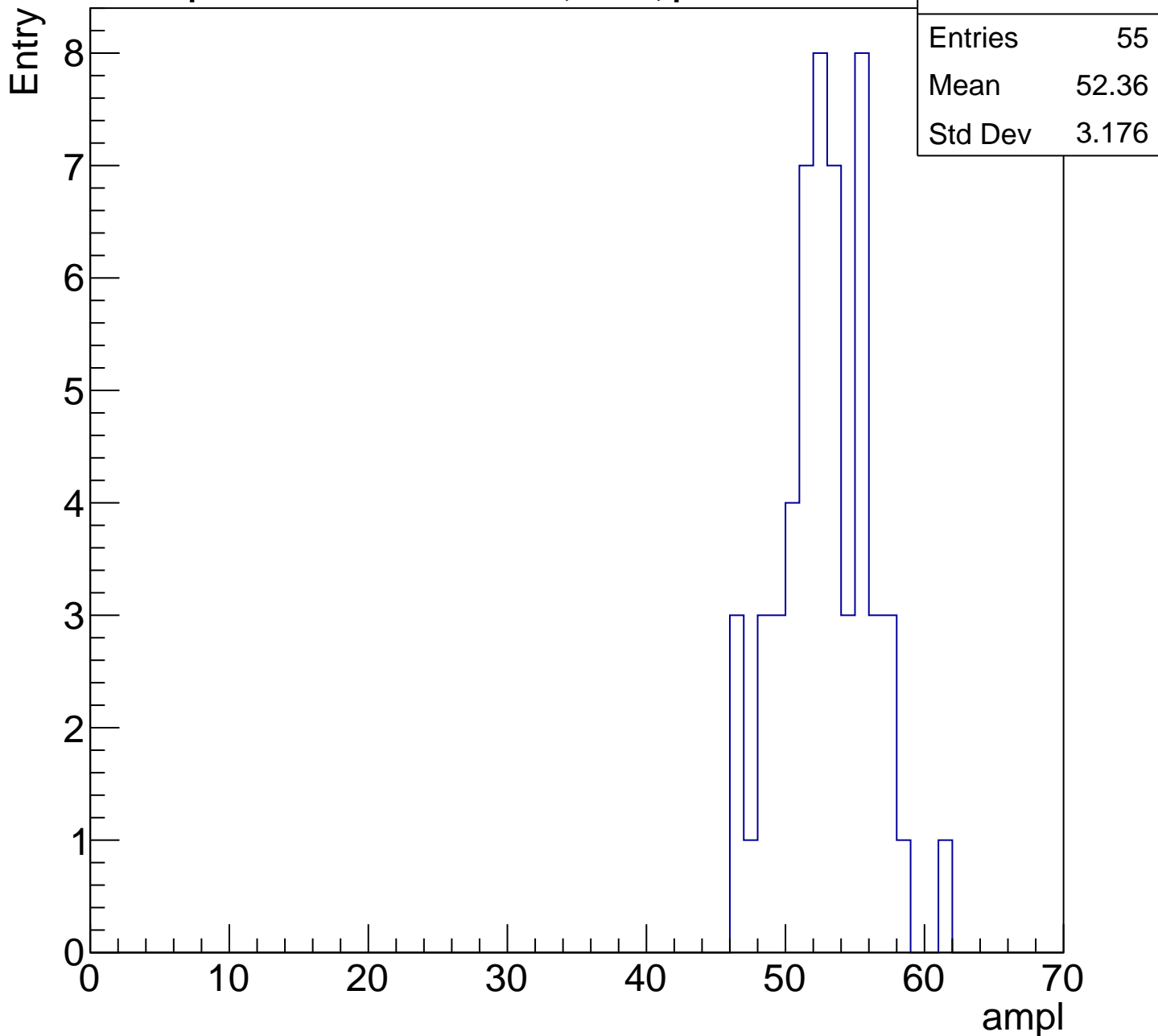
Entry

Entries	61
Mean	46.72
Std Dev	3.02



# B1L000S, U2-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

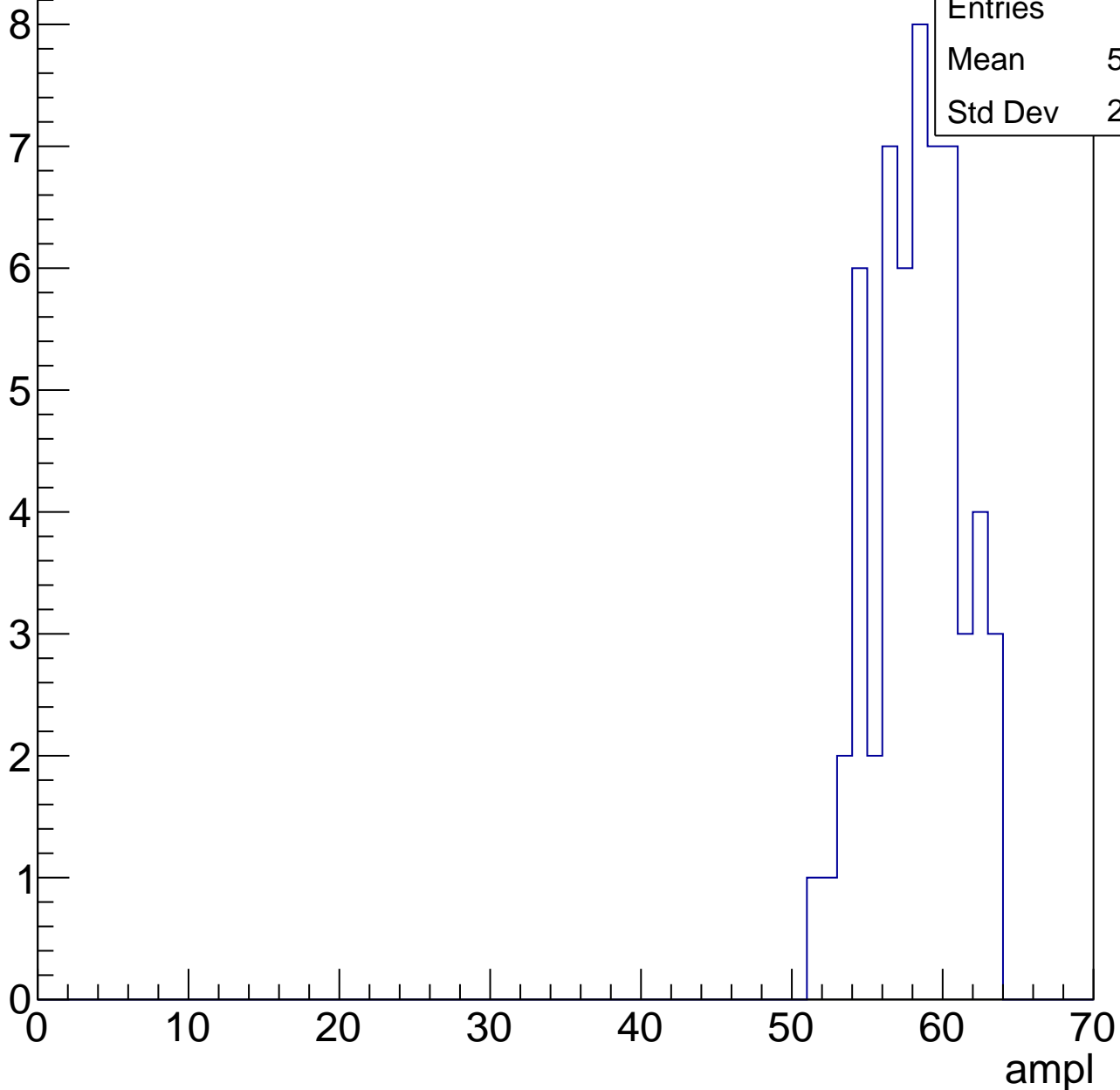


# B1L000S, U2-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

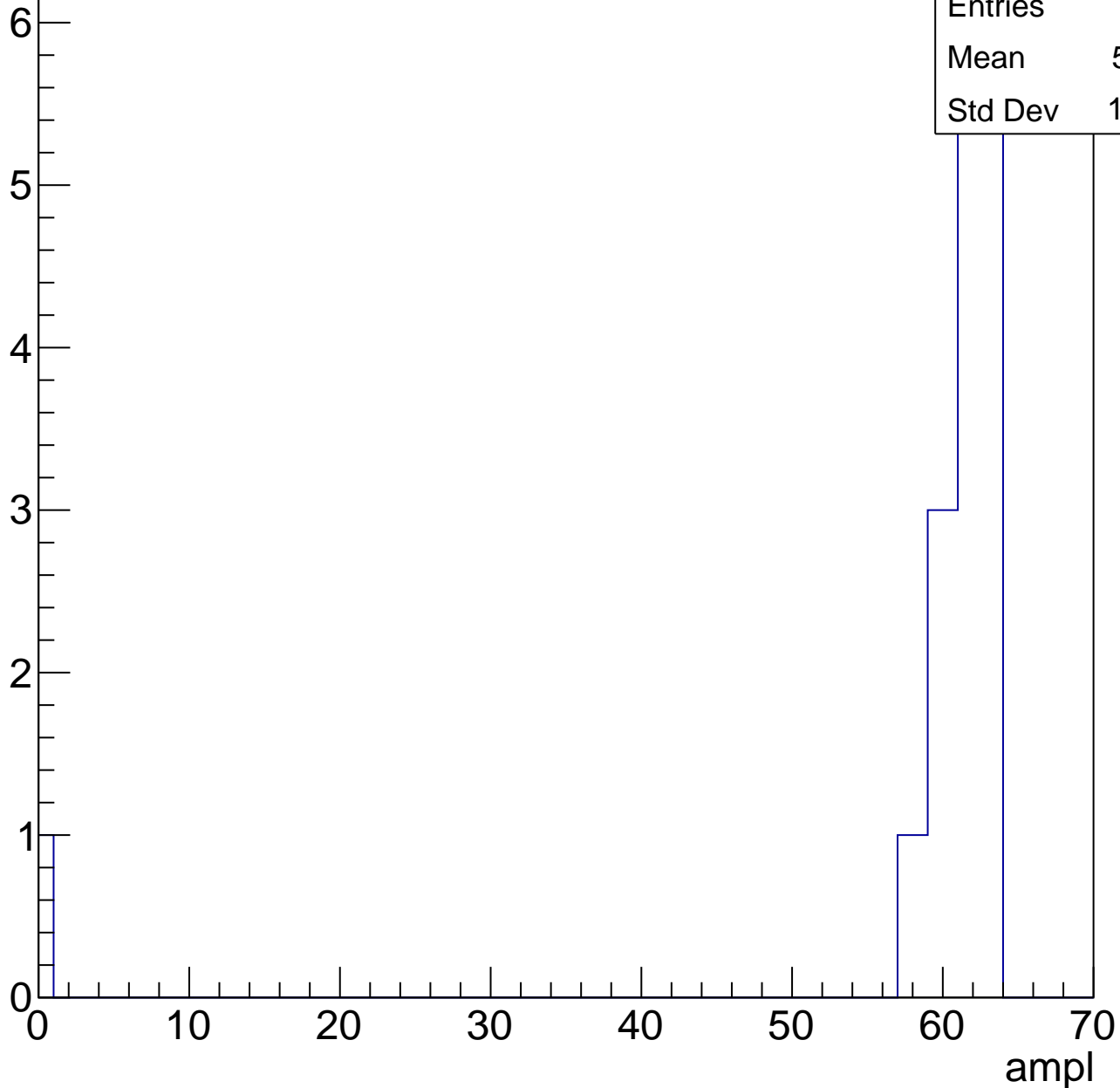
Entries	57
Mean	57.79
Std Dev	2.906



# B1L000S, U2-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch60, adc0

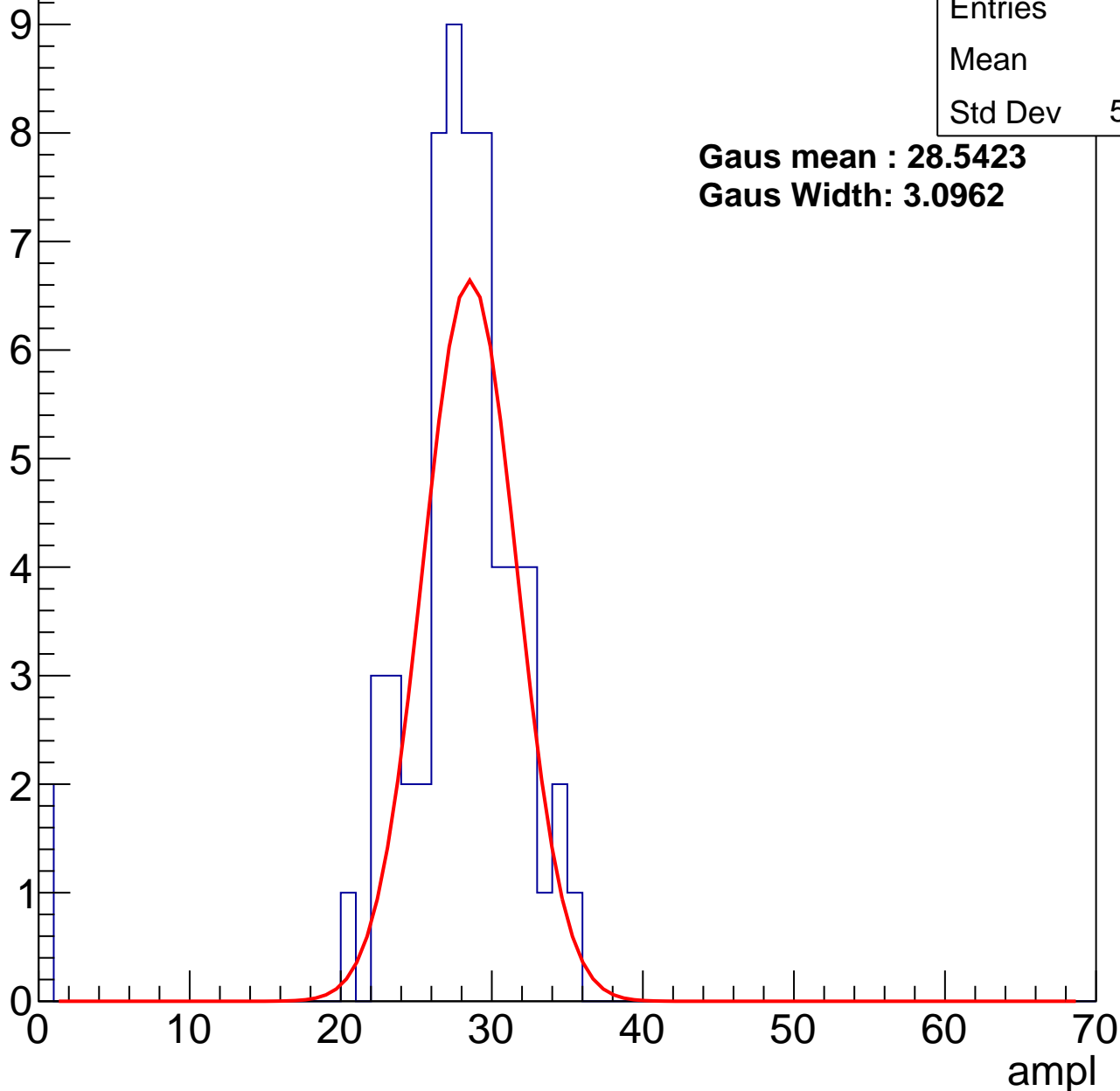
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	26.9
Std Dev	5.822

**Gaus mean : 28.5423**

**Gaus Width: 3.0962**



# B1L000S, U2-ch60, adc1

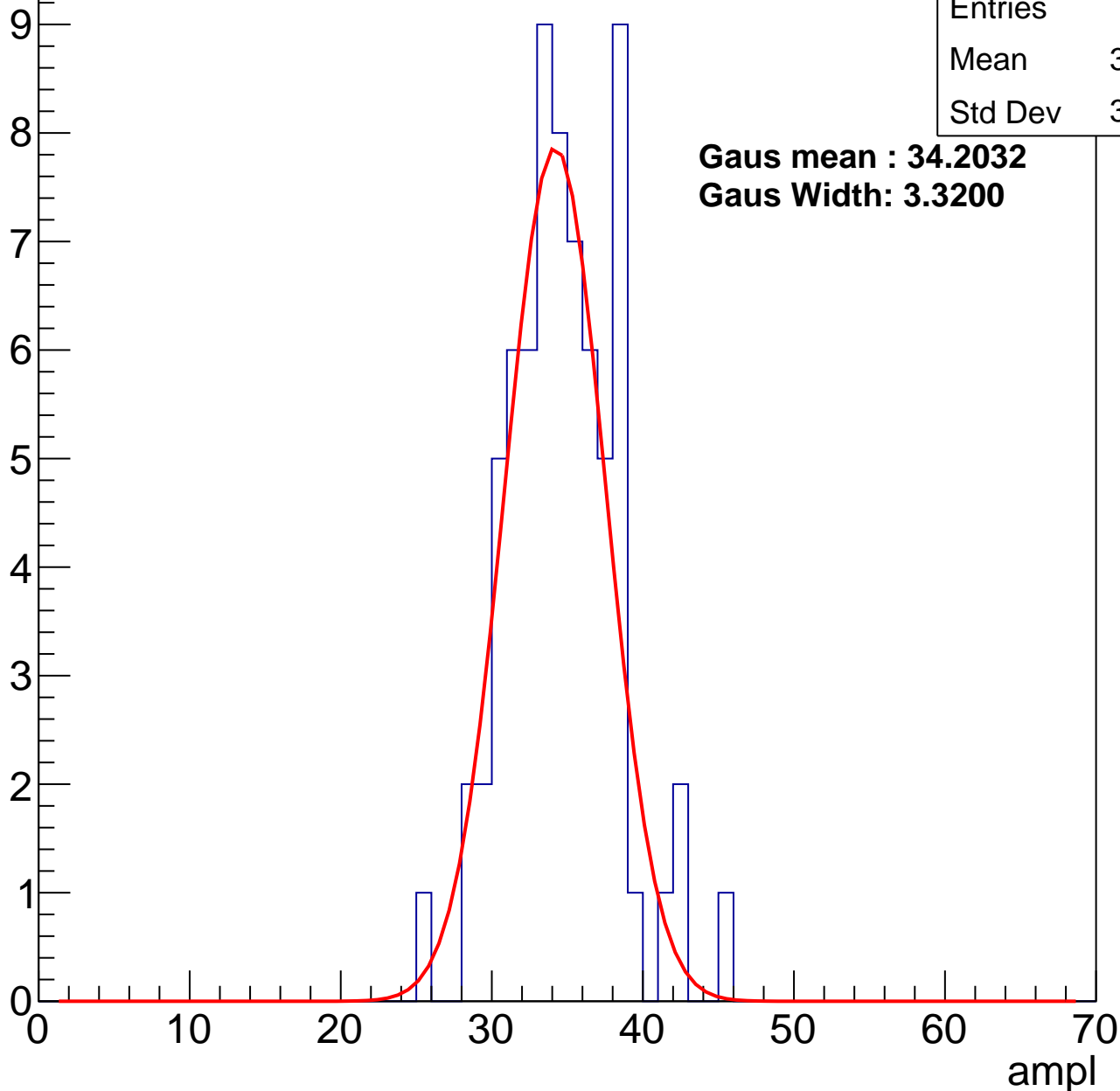
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.27
Std Dev	3.576

**Gaus mean : 34.2032**

**Gaus Width: 3.3200**



# B1L000S, U2-ch60, adc2

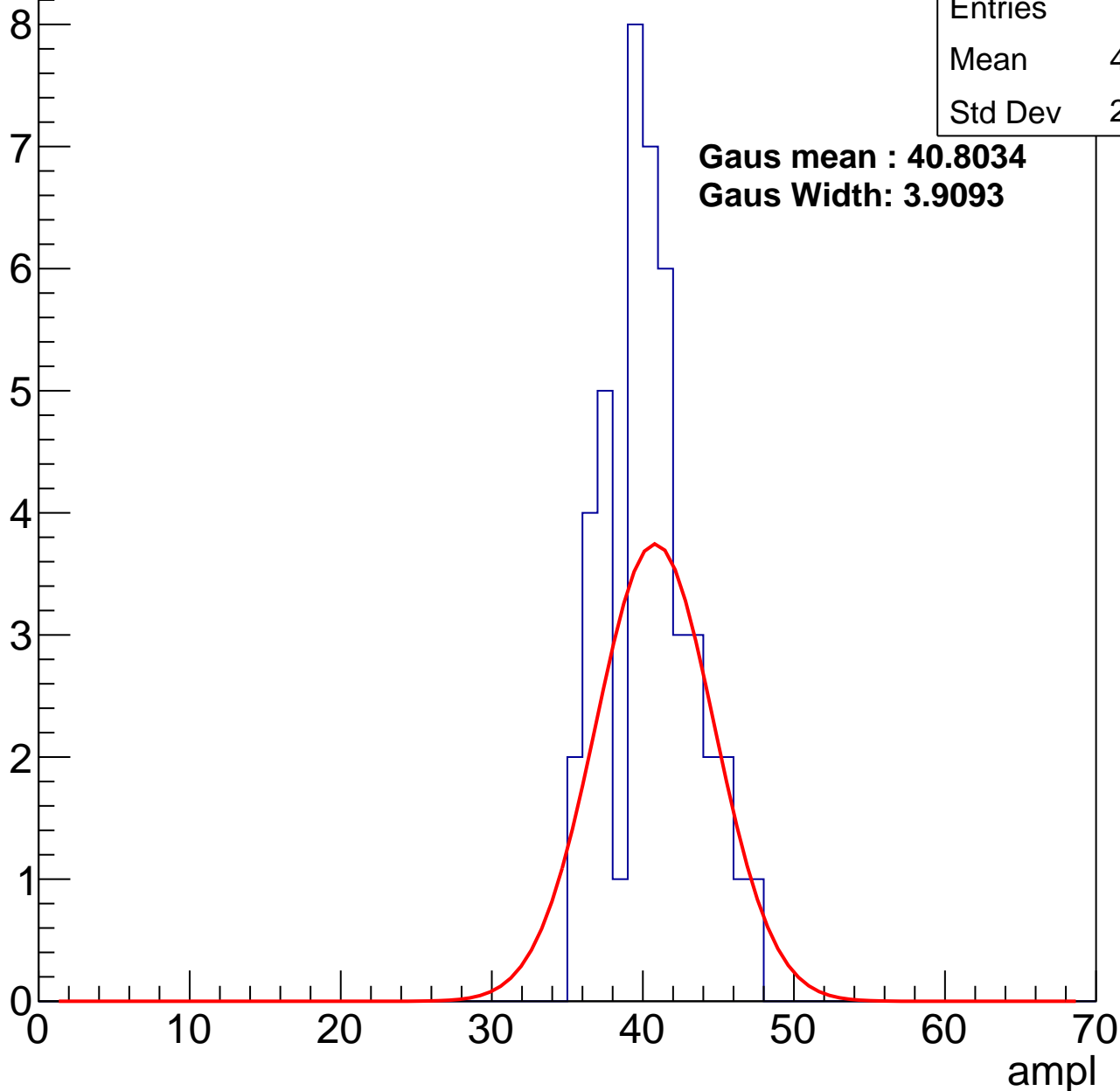
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	40.02
Std Dev	2.917

**Gaus mean : 40.8034**

**Gaus Width: 3.9093**

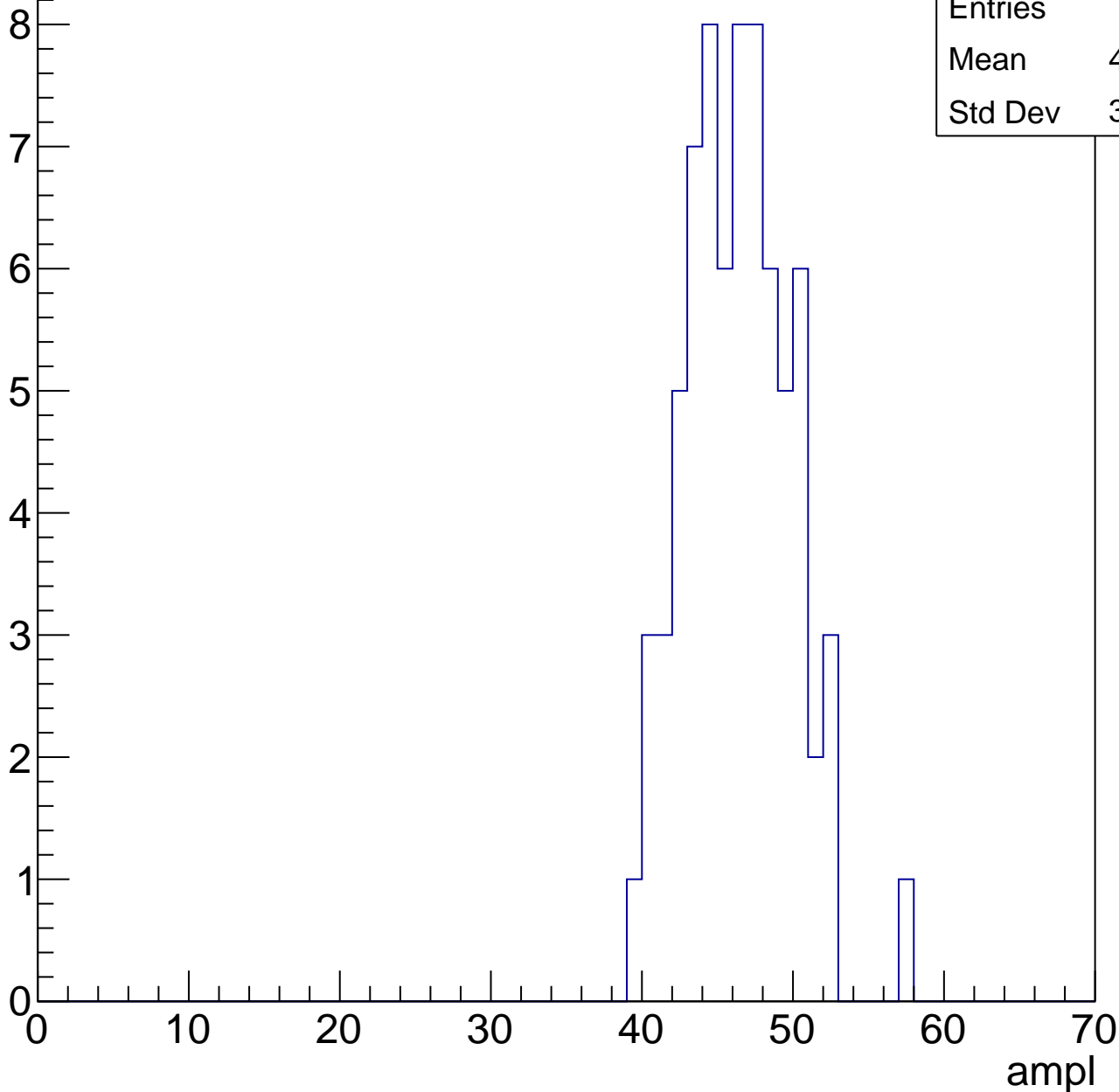


# B1L000S, U2-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	45.93
Std Dev	3.465

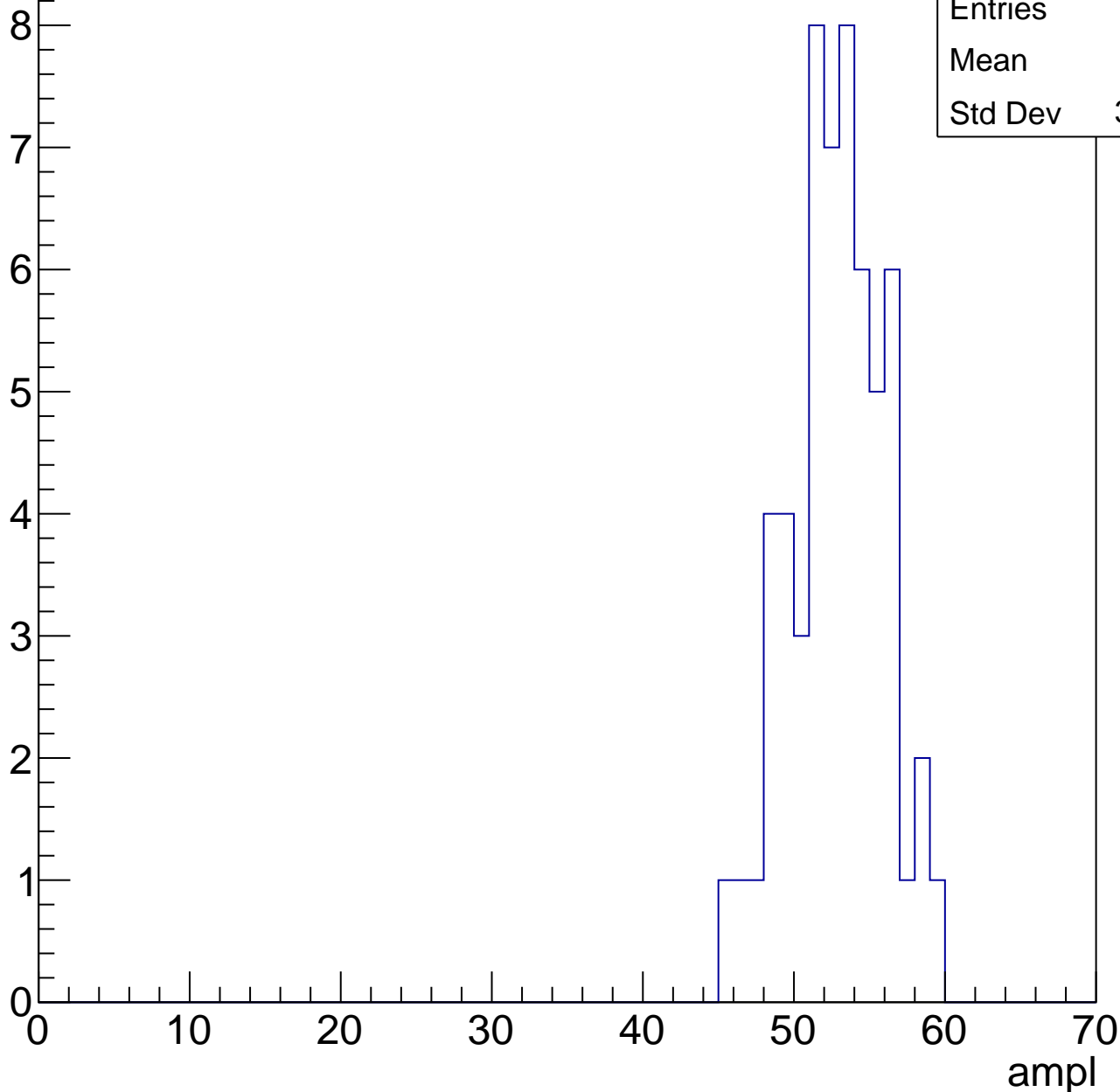


# B1L000S, U2-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

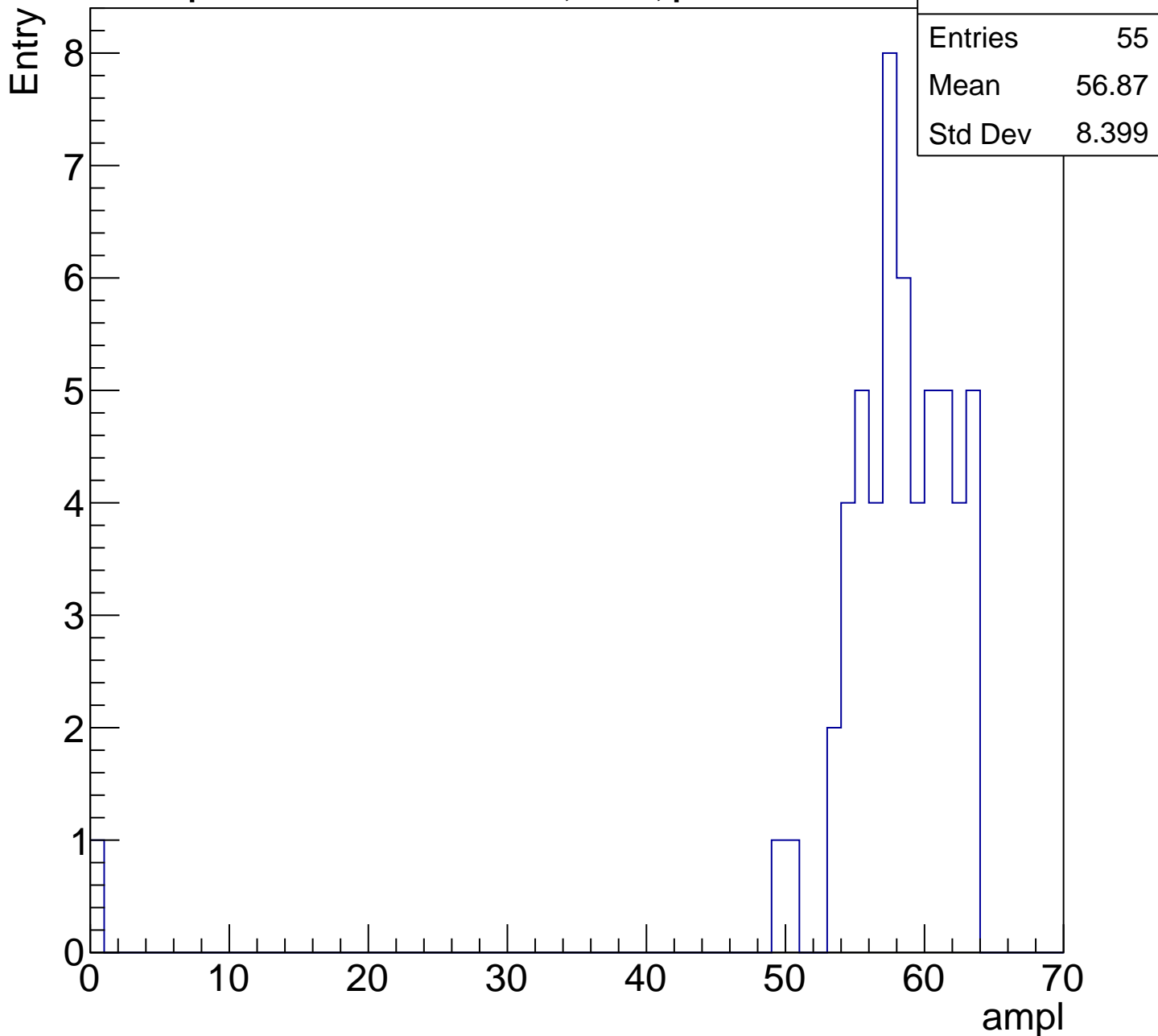
Entry

Entries	58
Mean	52.4
Std Dev	3.051



# B1L000S, U2-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

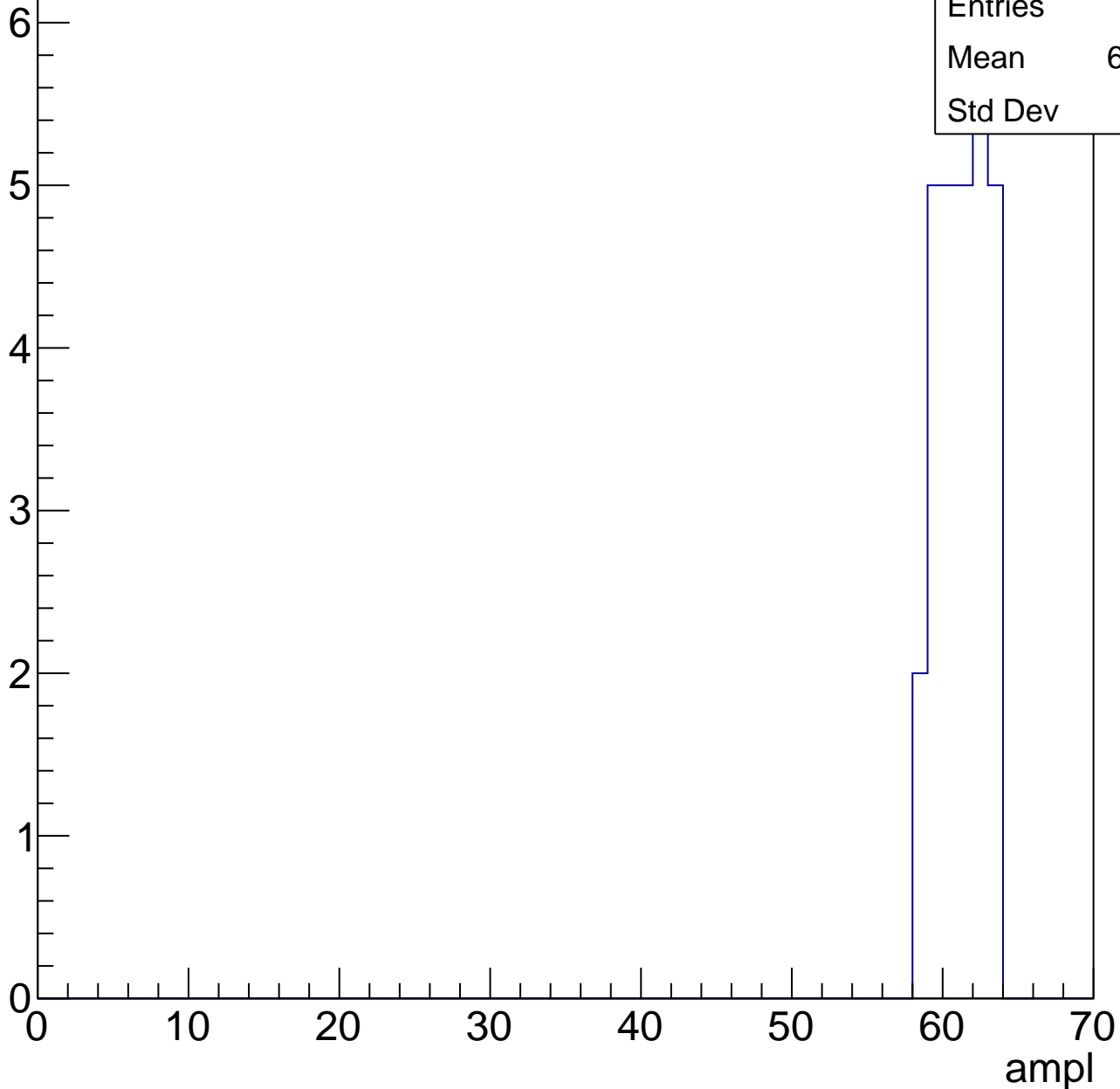


# B1L000S, U2-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	60.82
Std Dev	1.56





# B1L000S, U2-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U2-ch61, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	82
Mean	29.18
Std Dev	3.52

**Gaus mean : 29.5842**

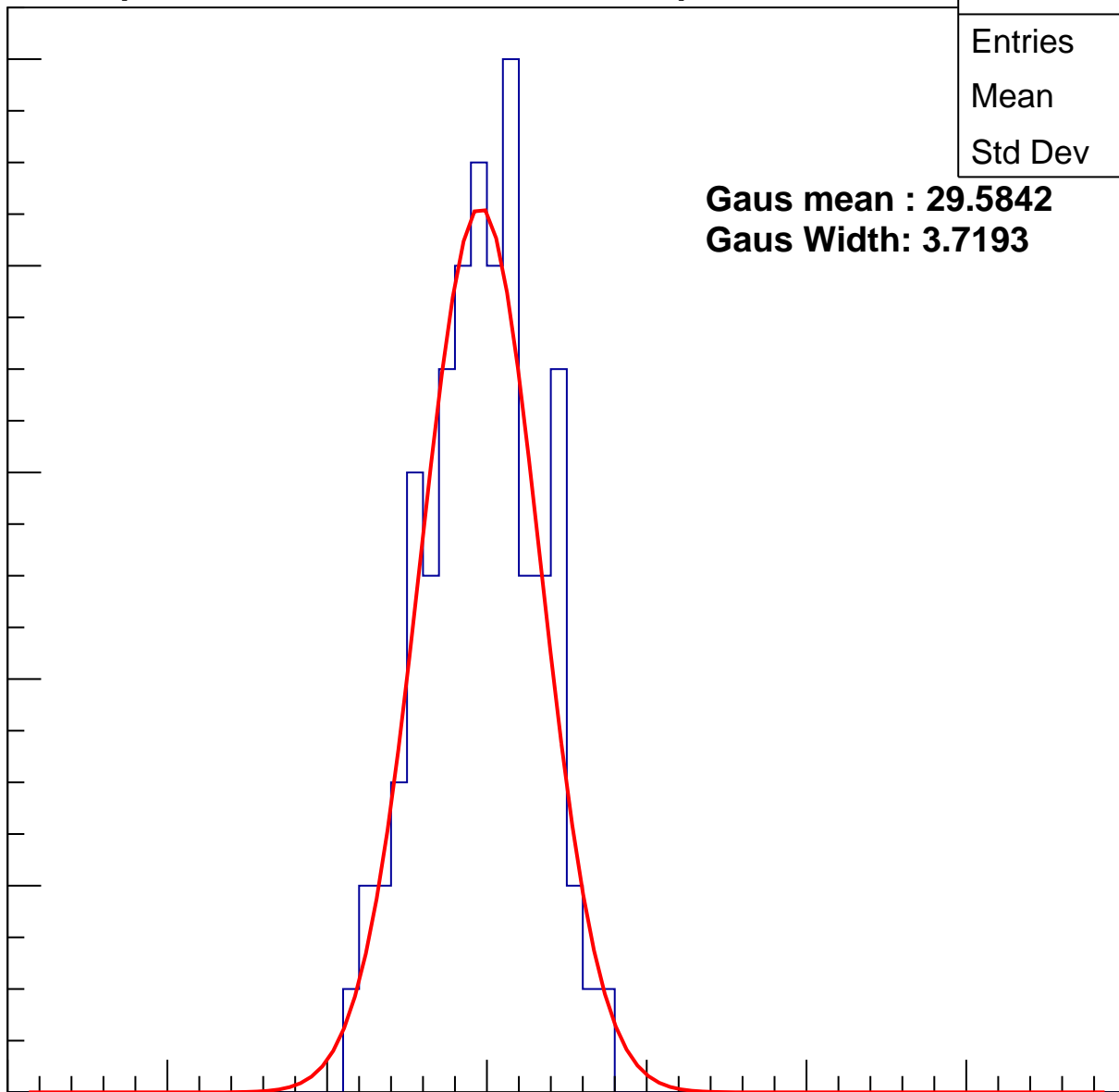
**Gaus Width: 3.7193**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch61, adc1

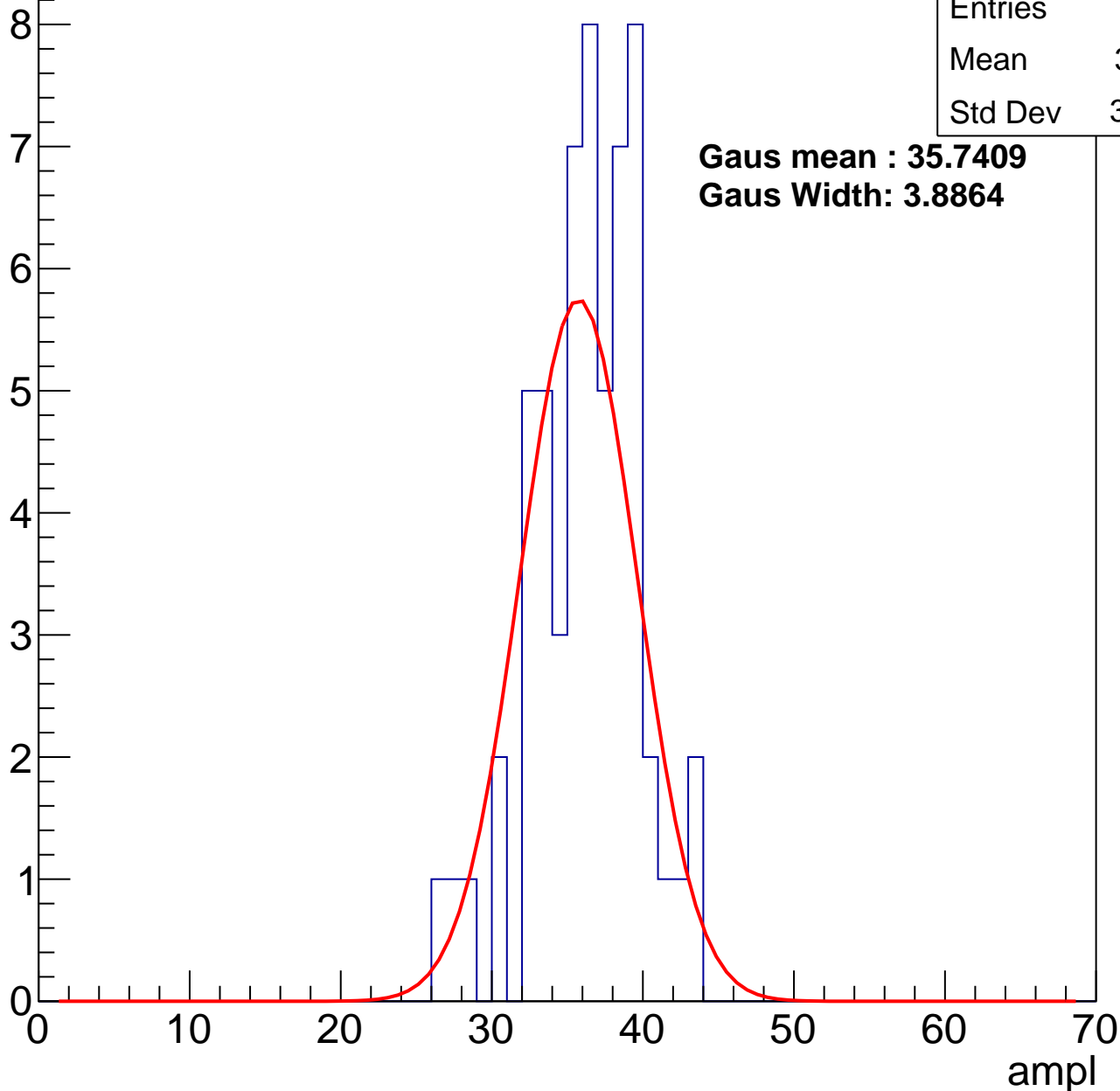
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	35.81
Std Dev	3.577

**Gaus mean : 35.7409**

**Gaus Width: 3.8864**



# B1L000S, U2-ch61, adc2

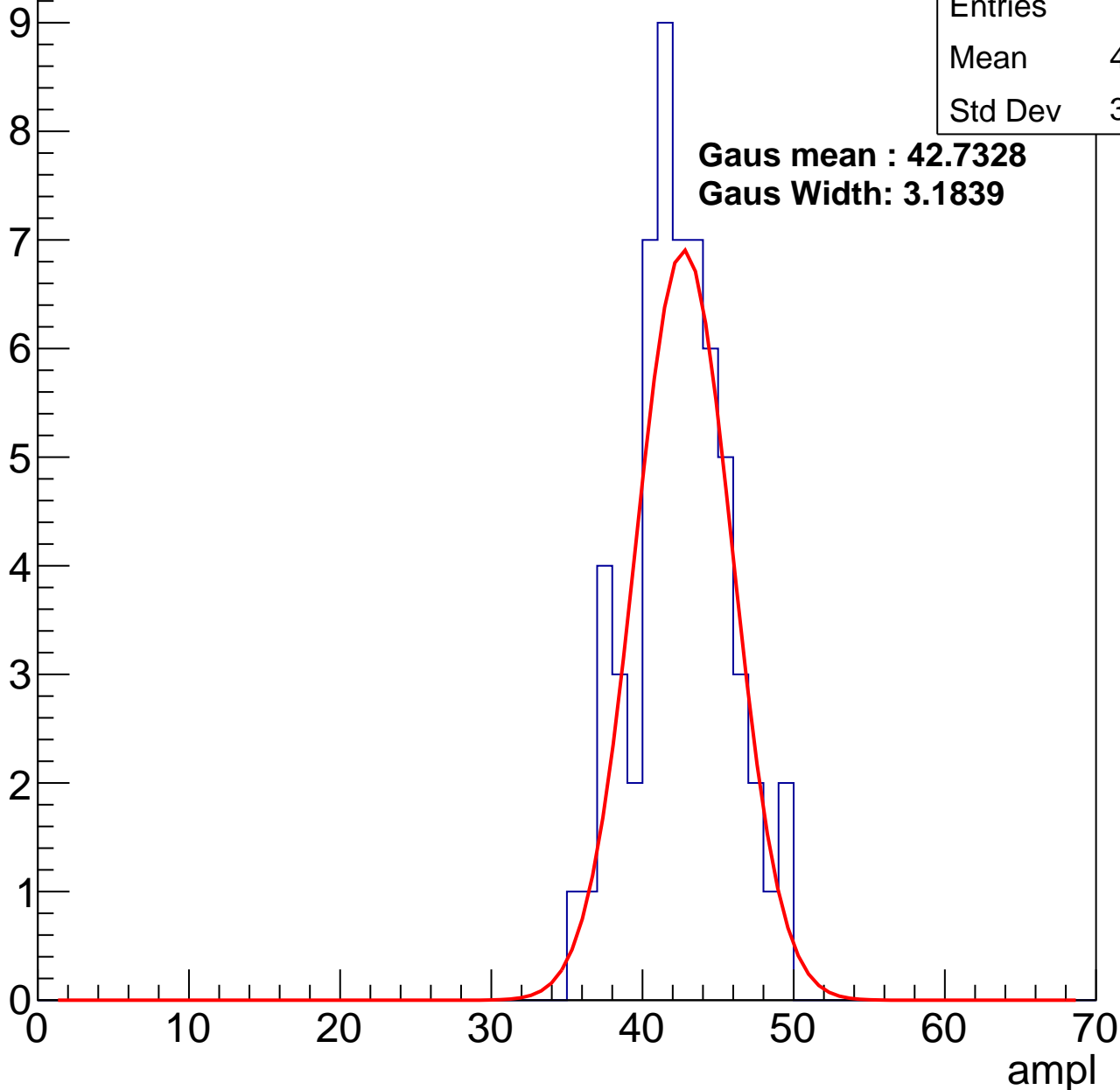
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.03
Std Dev	3.152

**Gaus mean : 42.7328**

**Gaus Width: 3.1839**

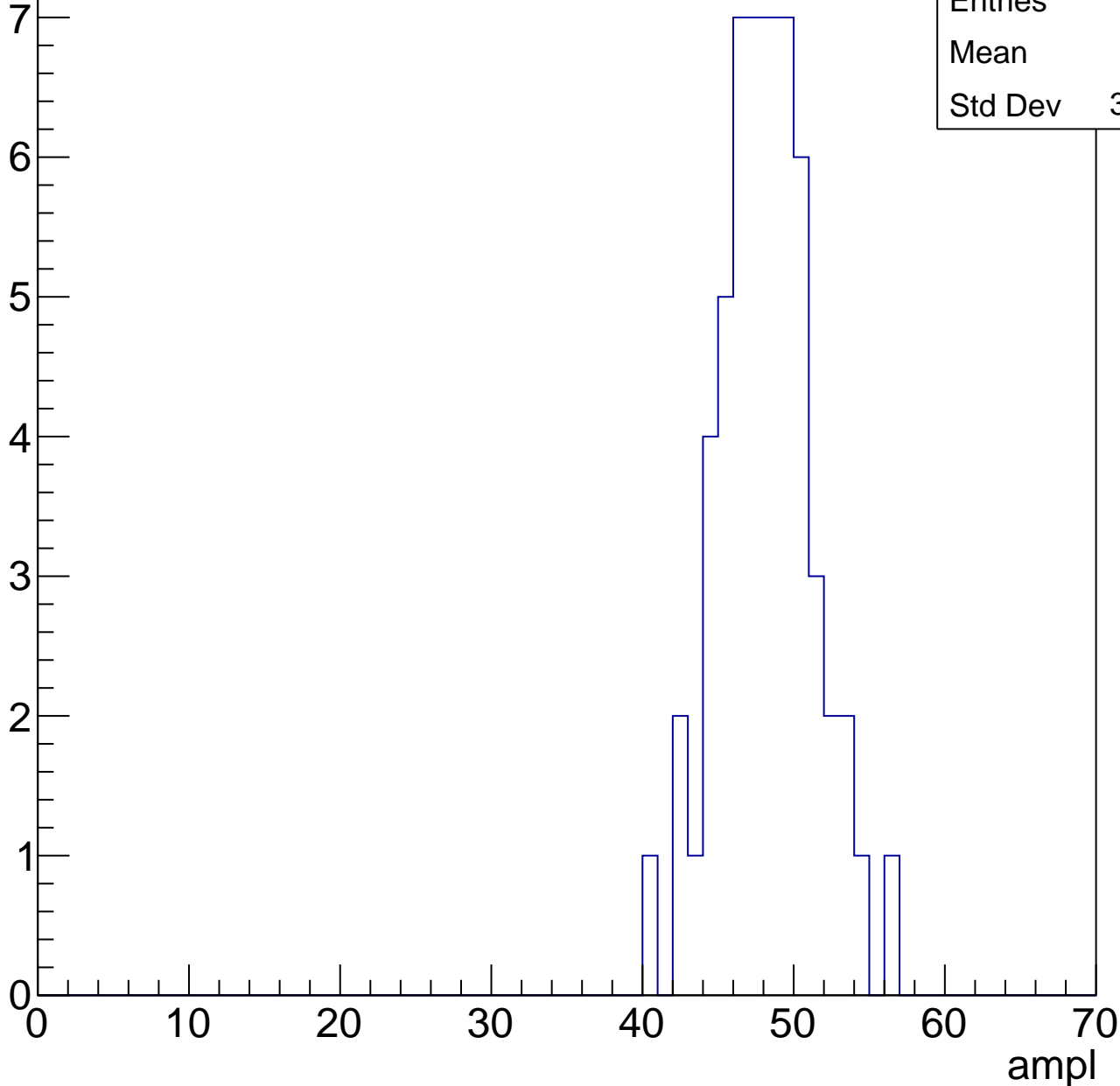


# B1L000S, U2-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	47.7
Std Dev	3.105

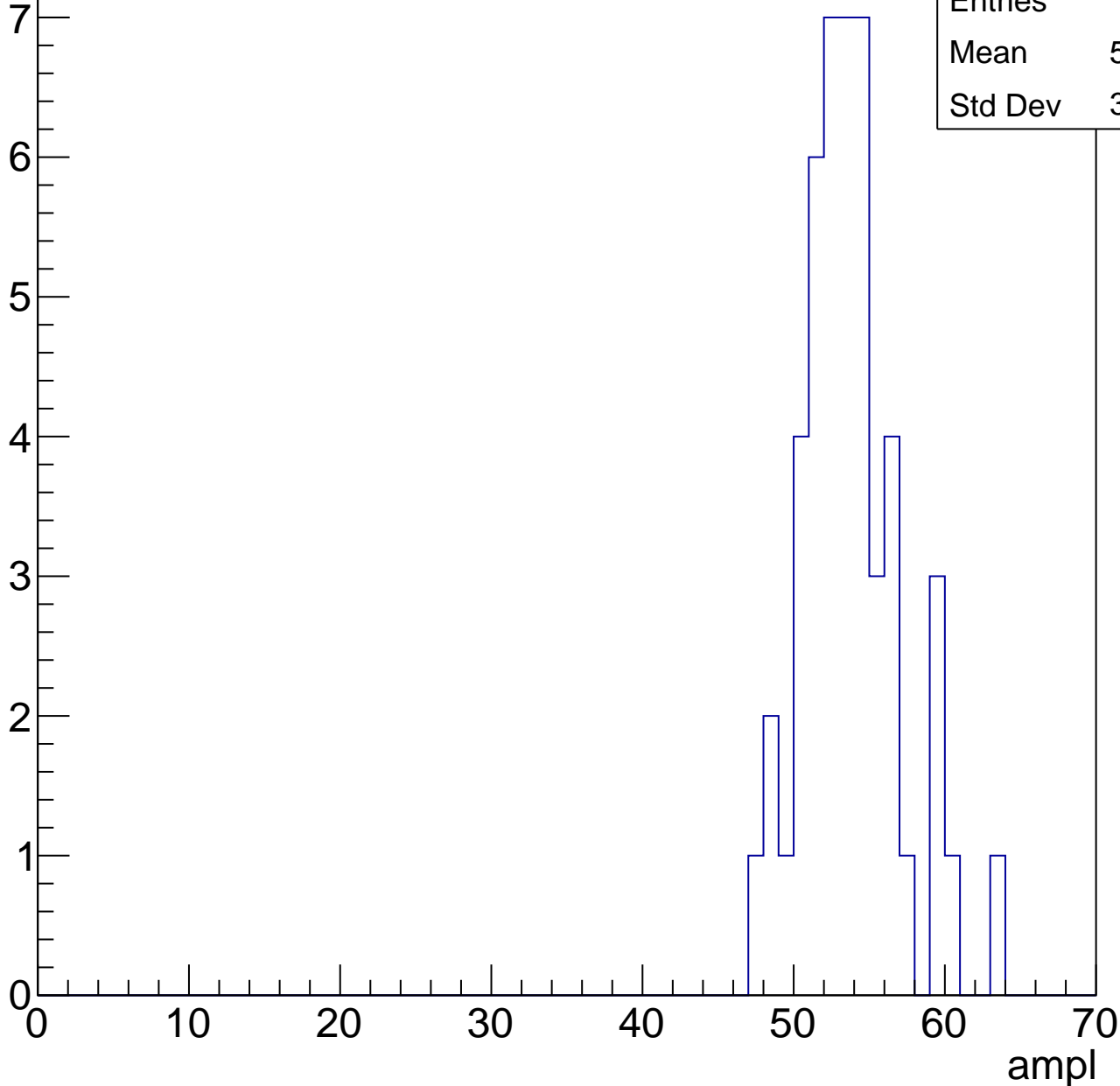


# B1L000S, U2-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

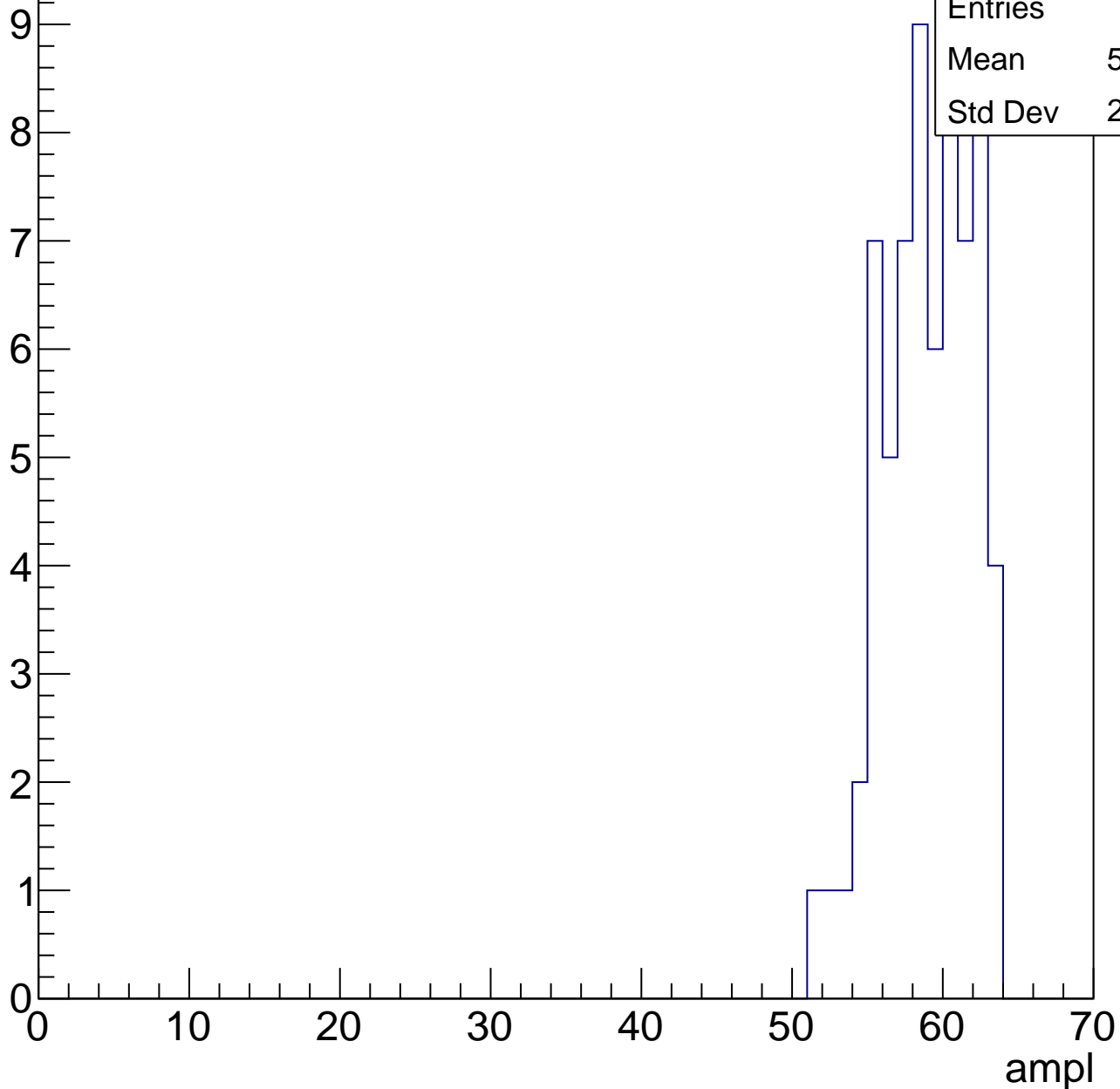
Entries	48
Mean	53.27
Std Dev	3.206



# B1L000S, U2-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

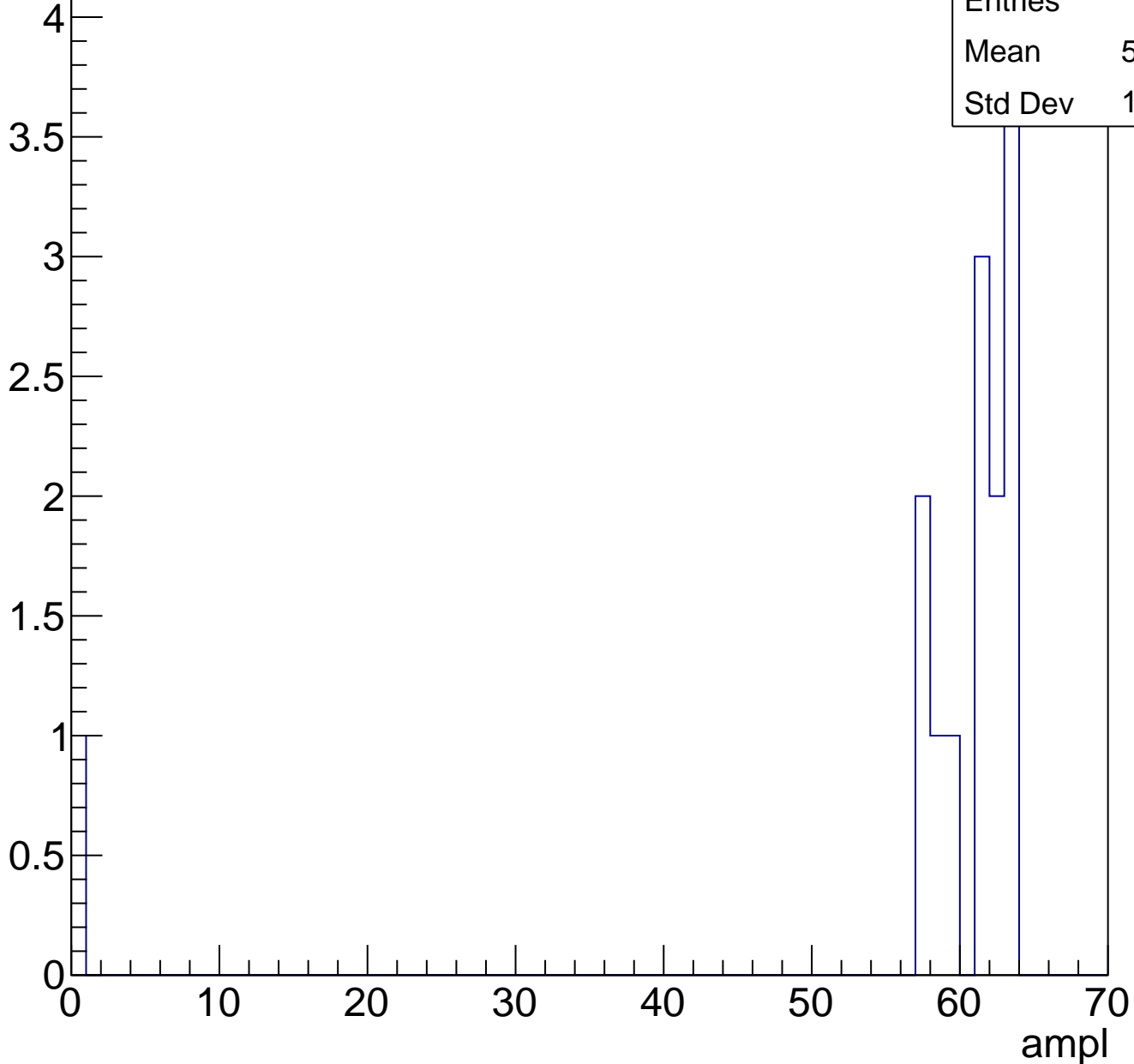
Entry



# B1L000S, U2-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

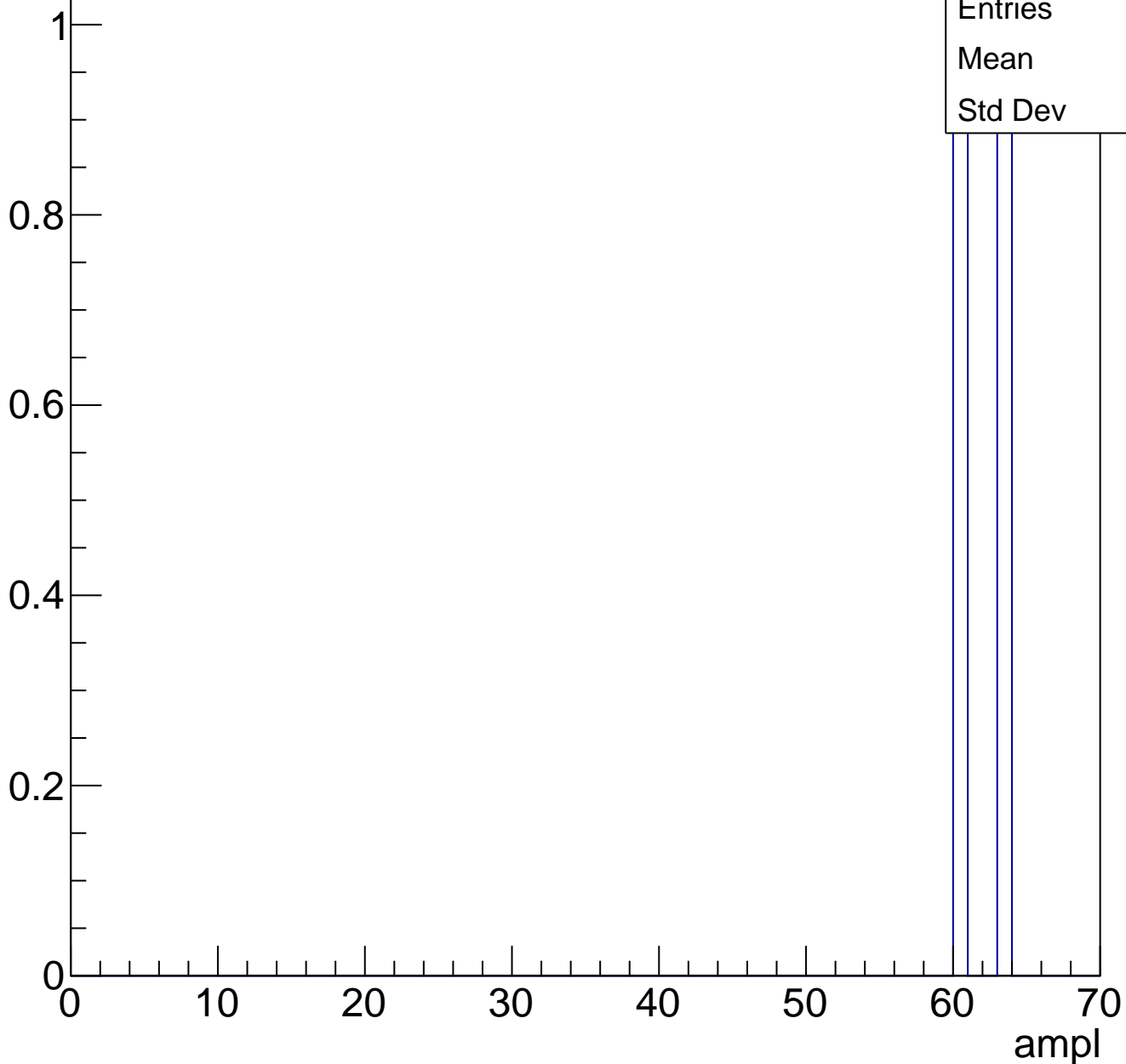




# B1L000S, U2-ch61, adc7

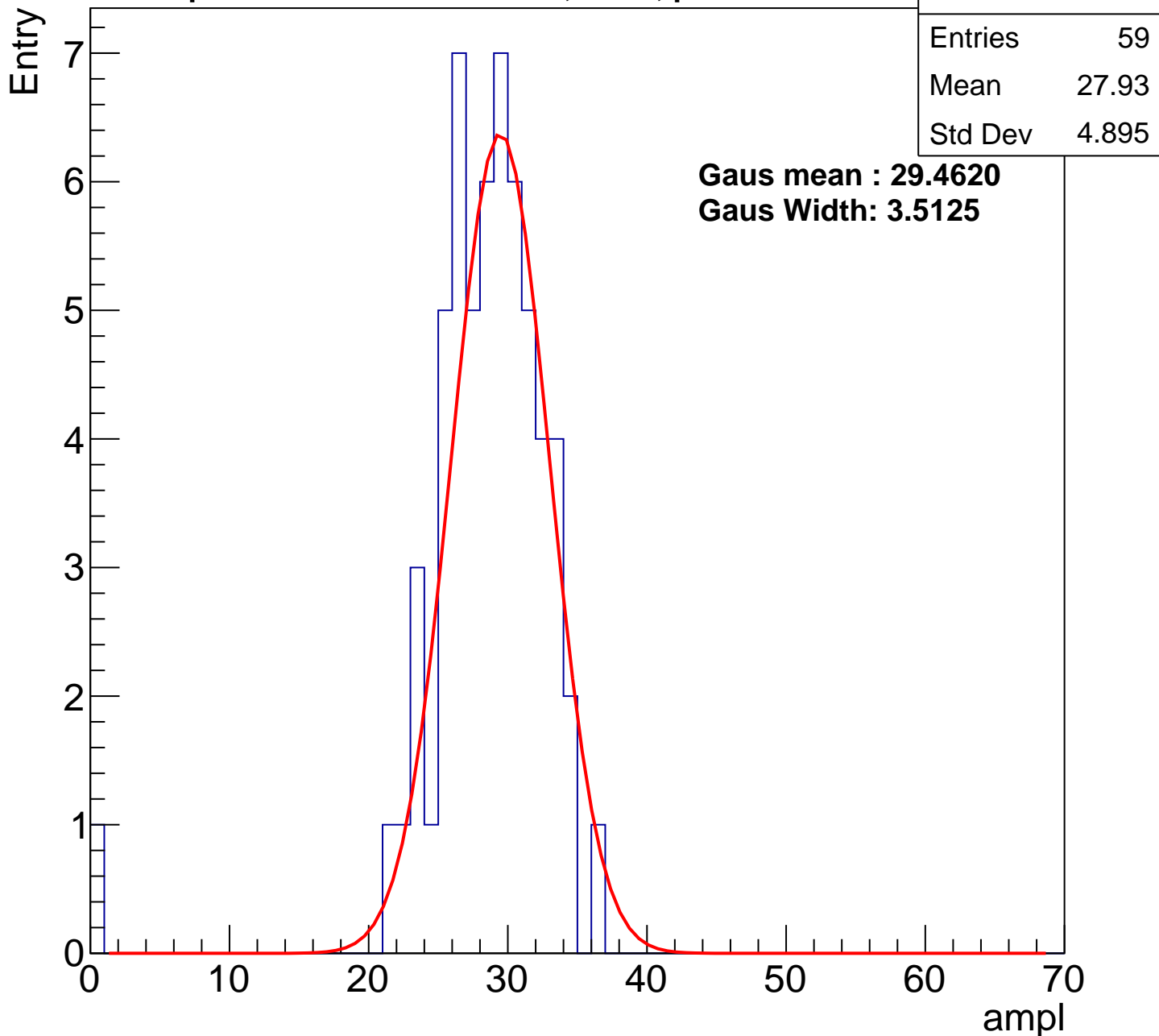
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch62, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch62, adc1

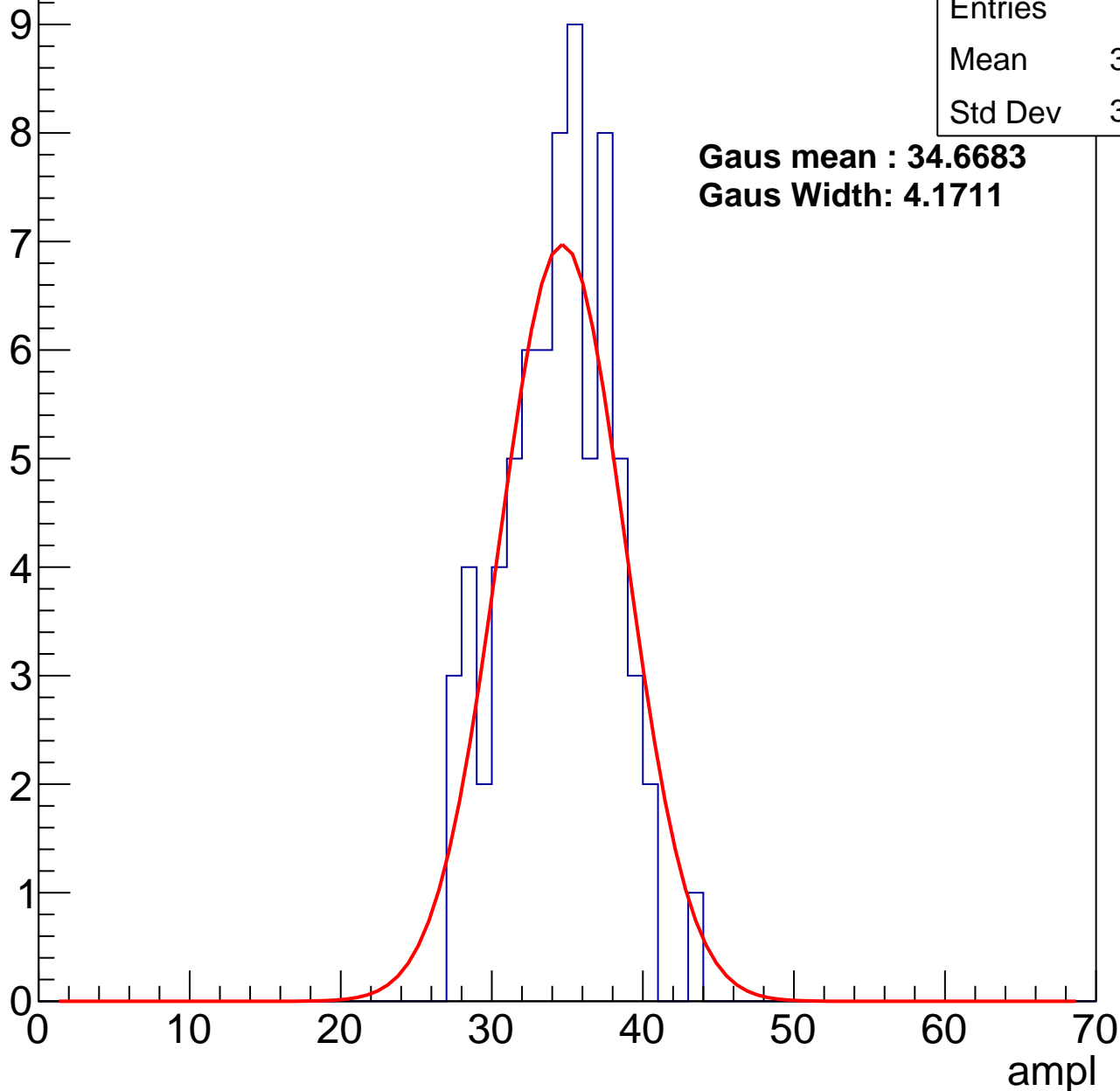
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	33.93
Std Dev	3.538

**Gaus mean : 34.6683**

**Gaus Width: 4.1711**



# B1L000S, U2-ch62, adc2

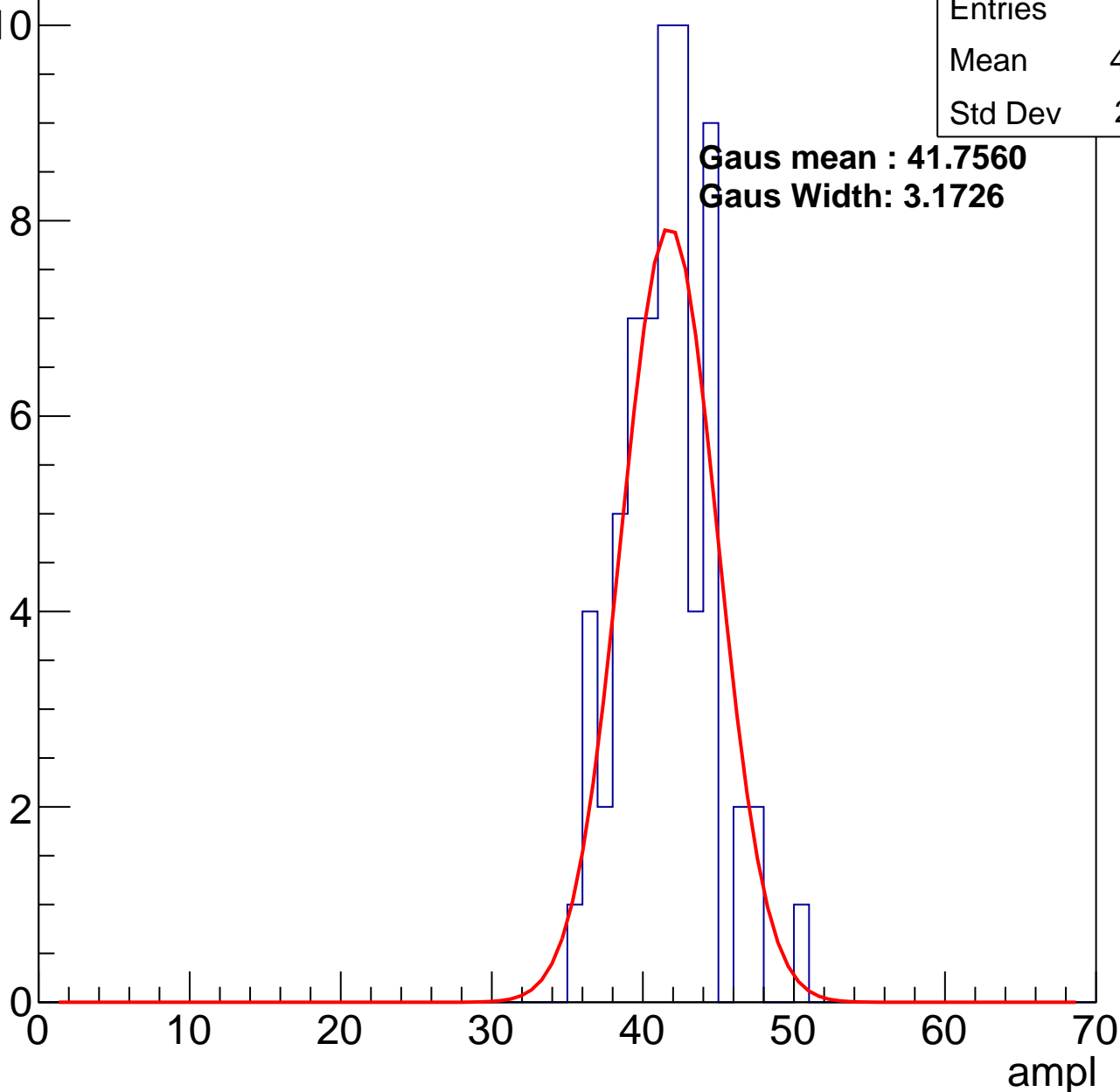
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.09
Std Dev	2.951

**Gaus mean : 41.7560**

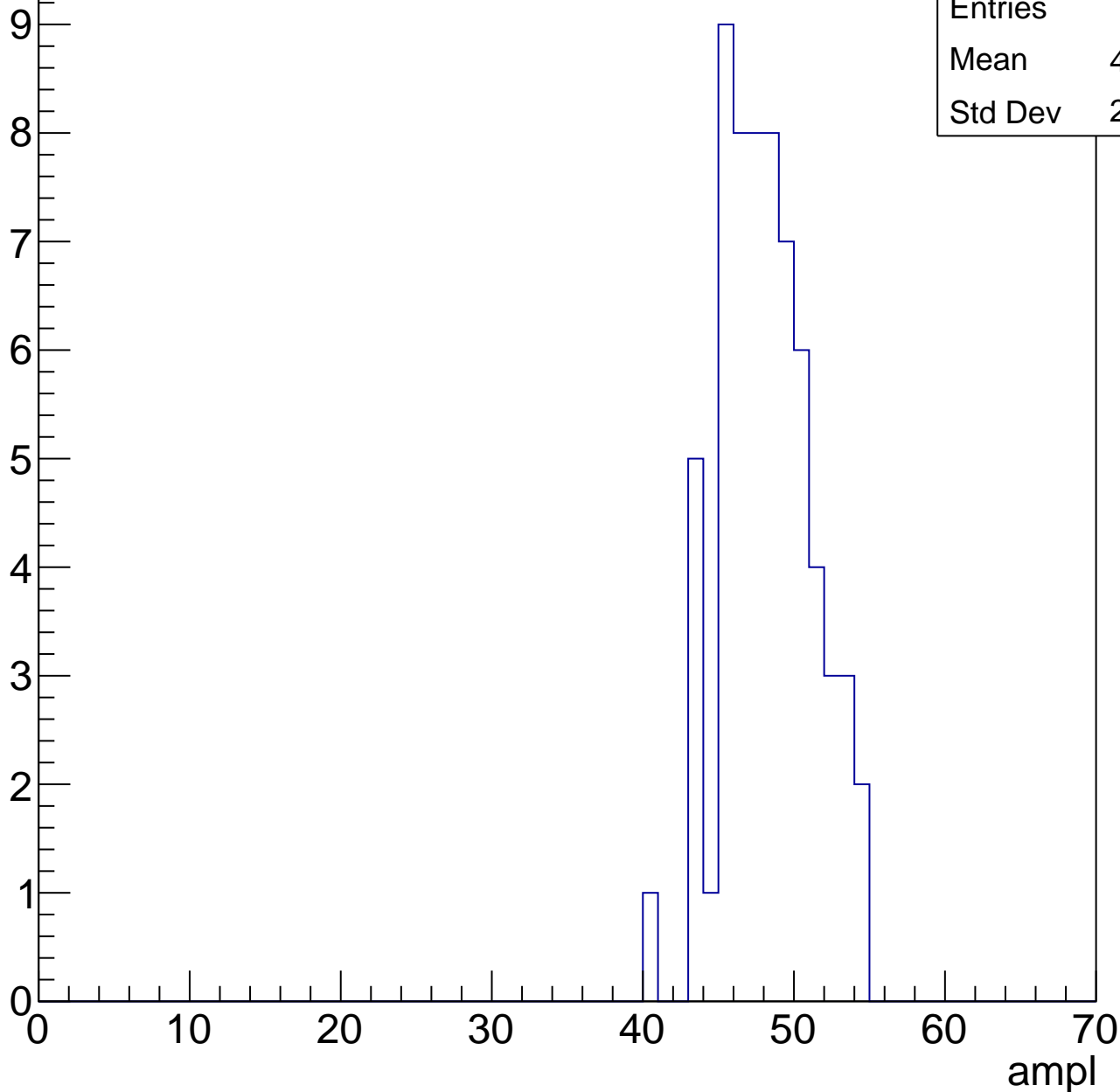
**Gaus Width: 3.1726**



# B1L000S, U2-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

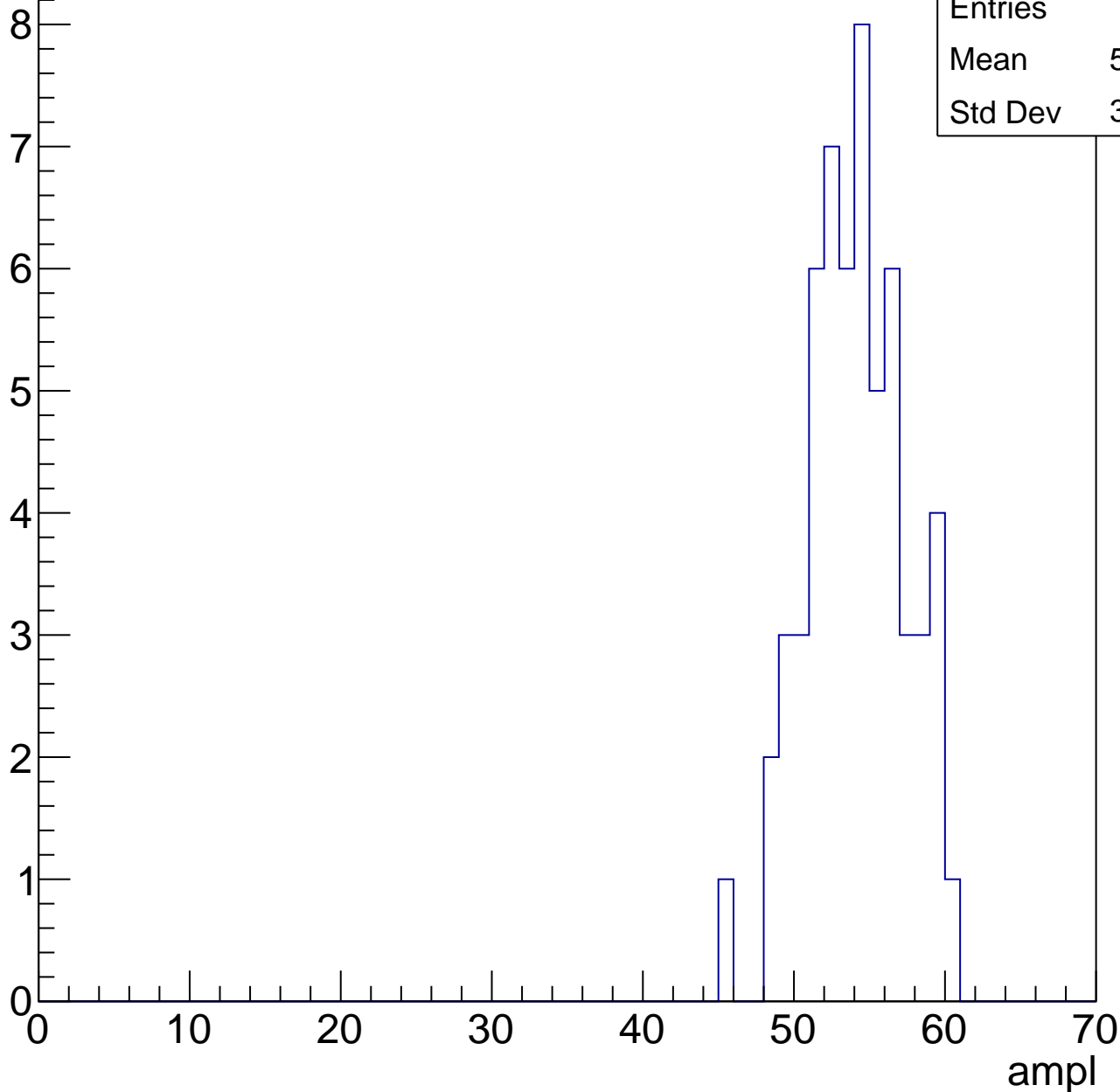


# B1L000S, U2-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

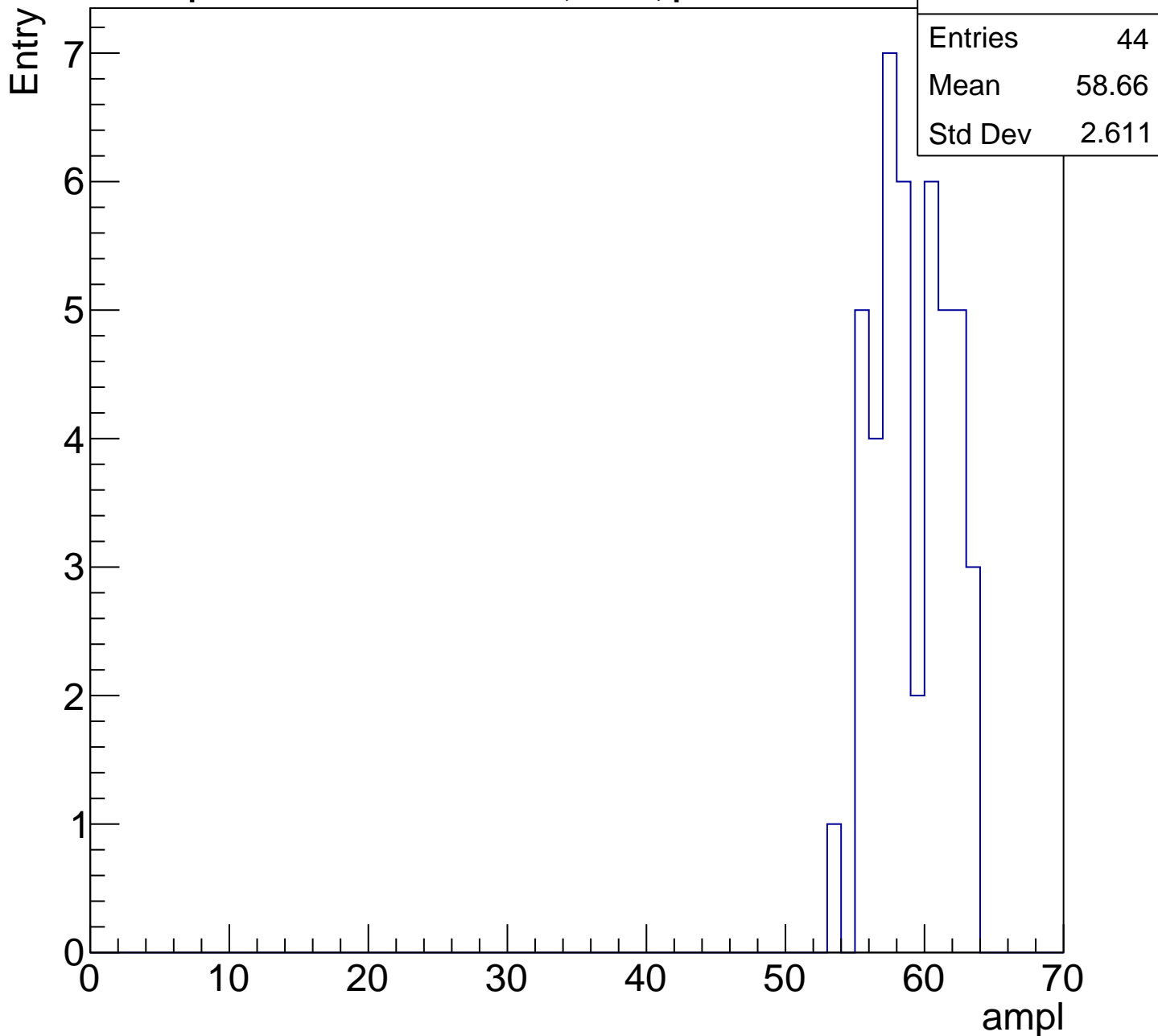
Entry

Entries	58
Mean	53.62
Std Dev	3.205



# B1L000S, U2-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

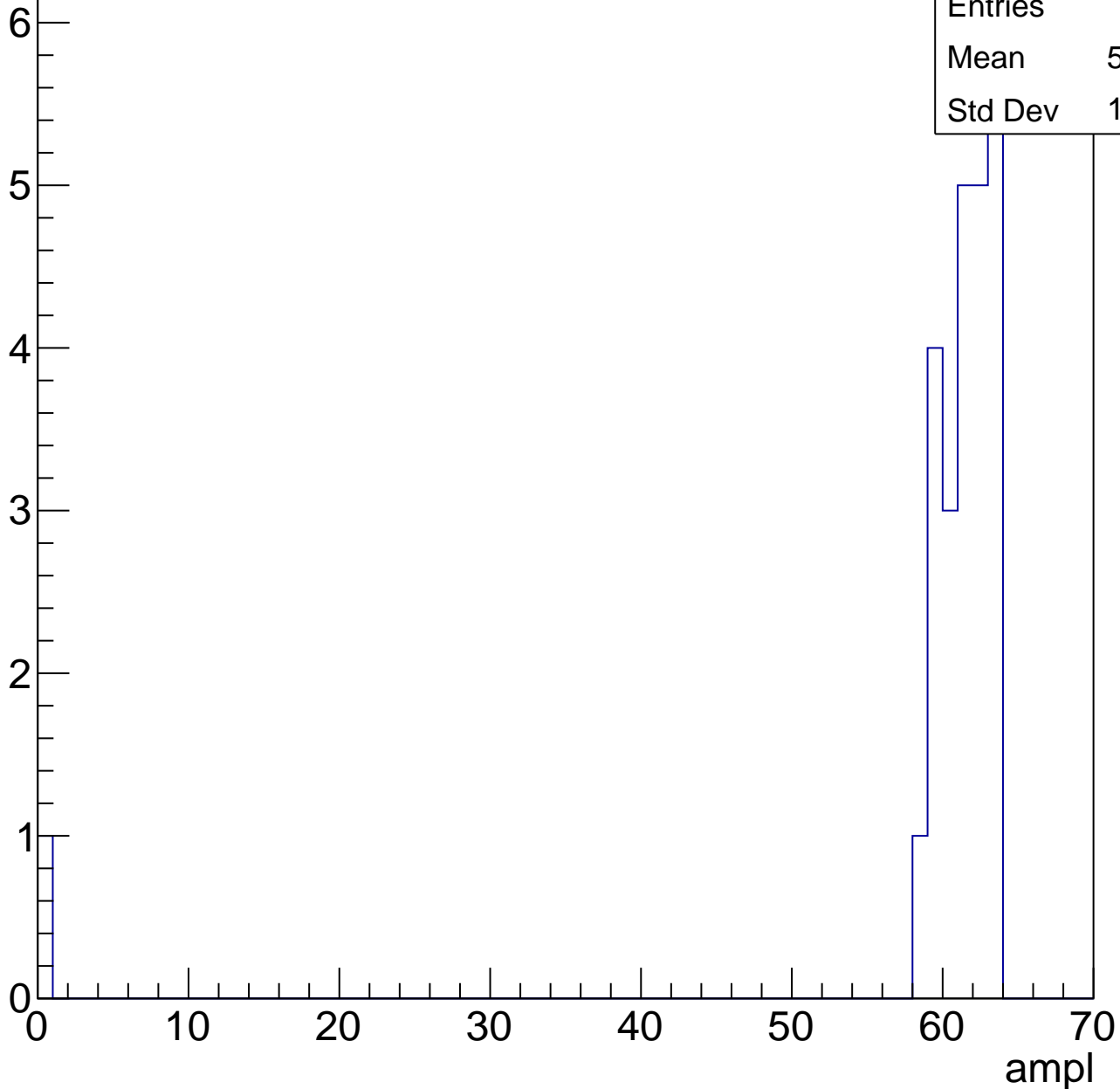


# B1L000S, U2-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.68
Std Dev	12.07





# B1L000S, U2-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch63, adc0

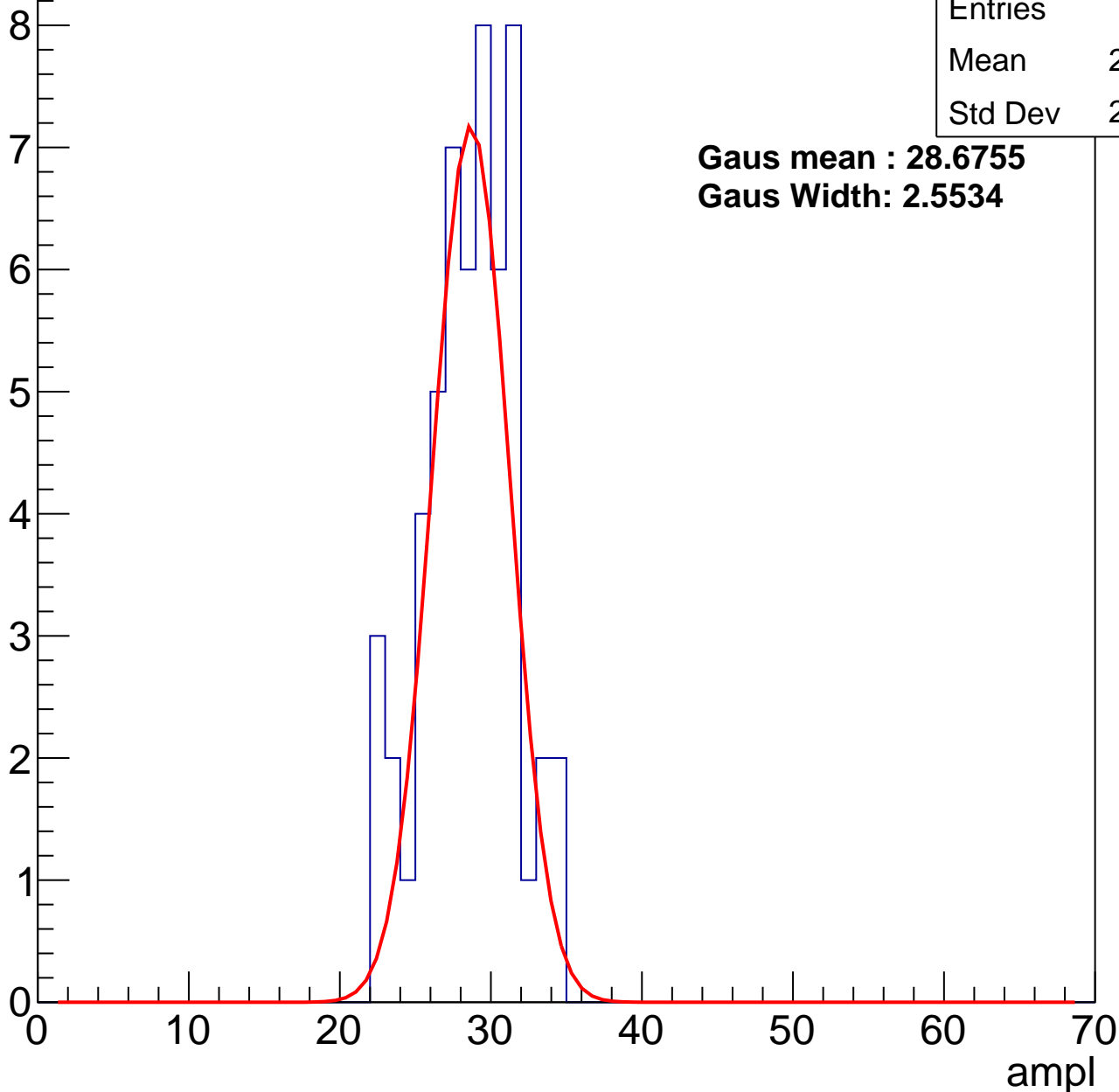
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	28.16
Std Dev	2.947

**Gaus mean : 28.6755**

**Gaus Width: 2.5534**



# B1L000S, U2-ch63, adc1

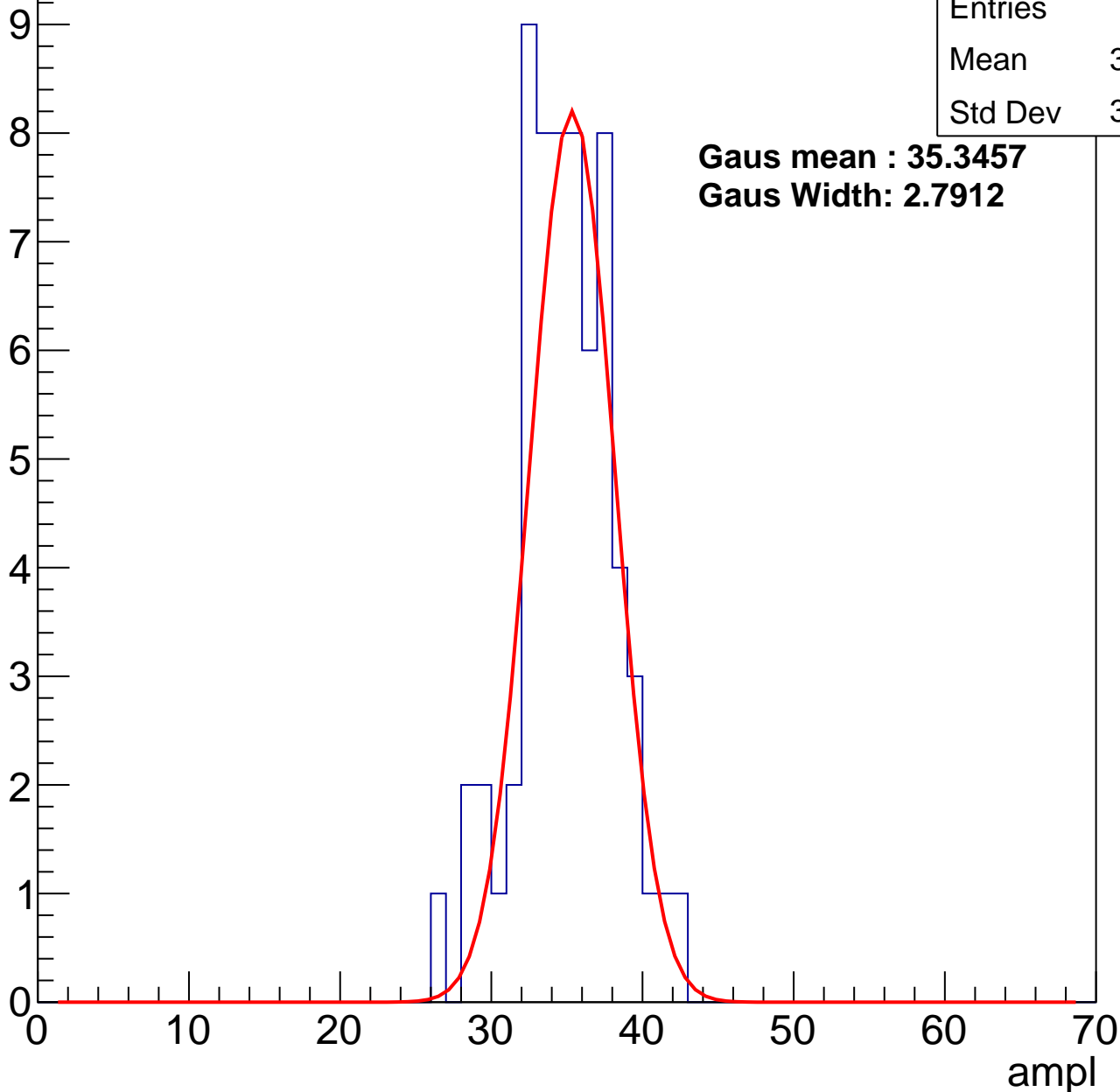
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	34.46
Std Dev	3.143

**Gaus mean : 35.3457**

**Gaus Width: 2.7912**



# B1L000S, U2-ch63, adc2

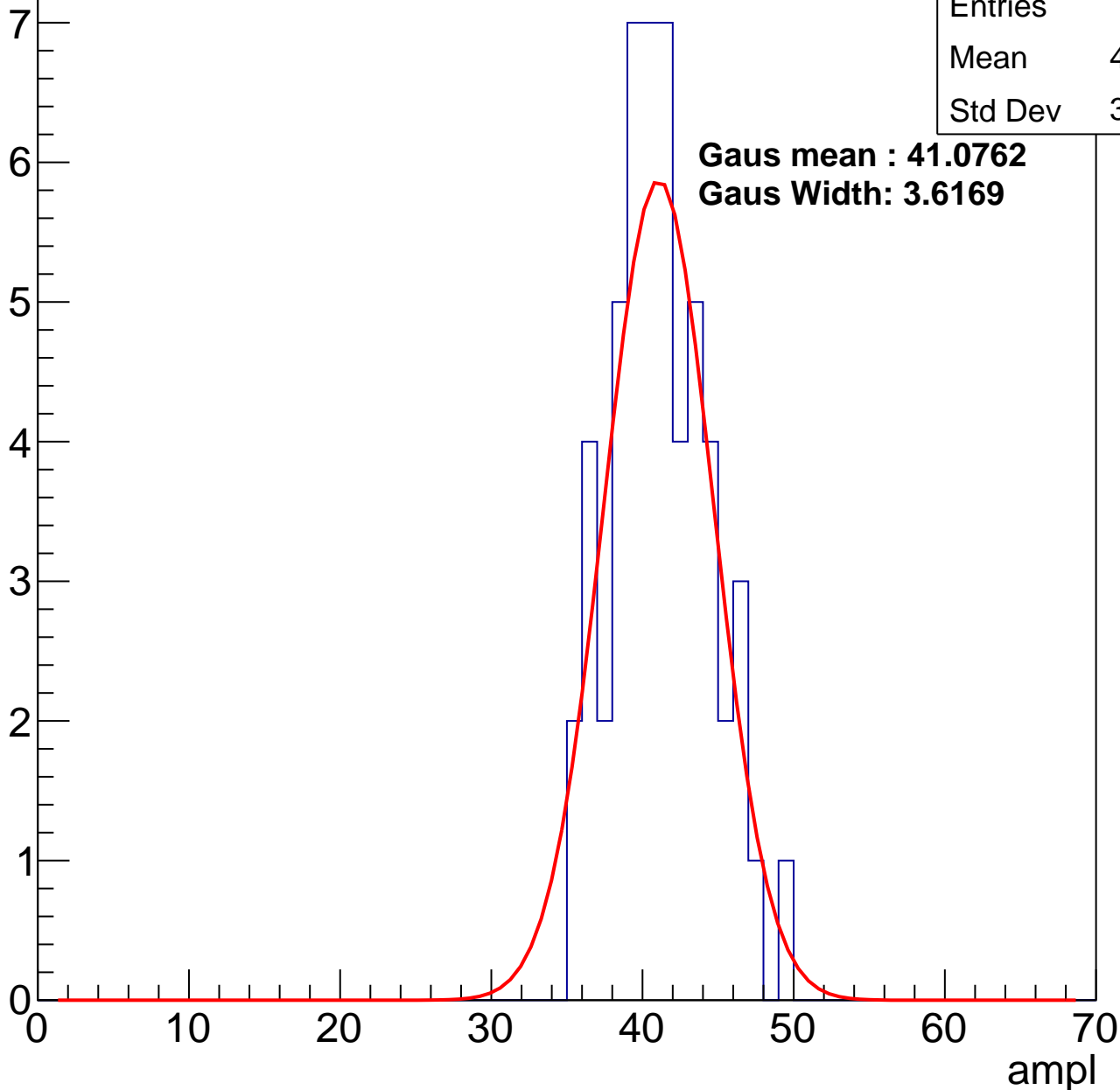
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	40.76
Std Dev	3.185

**Gaus mean : 41.0762**

**Gaus Width: 3.6169**



# B1L000S, U2-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

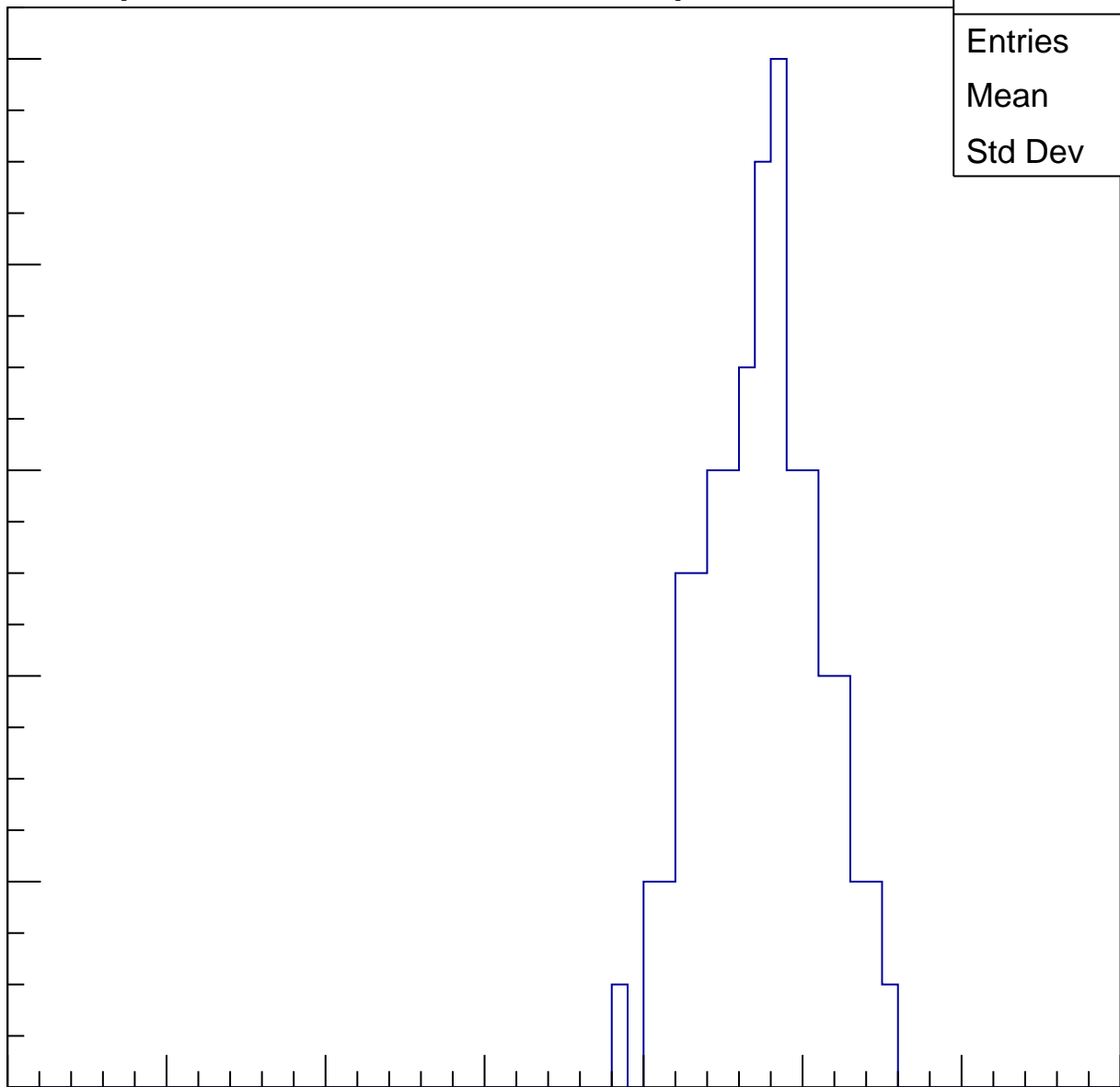
Entries	78
Mean	46.91
Std Dev	3.627

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

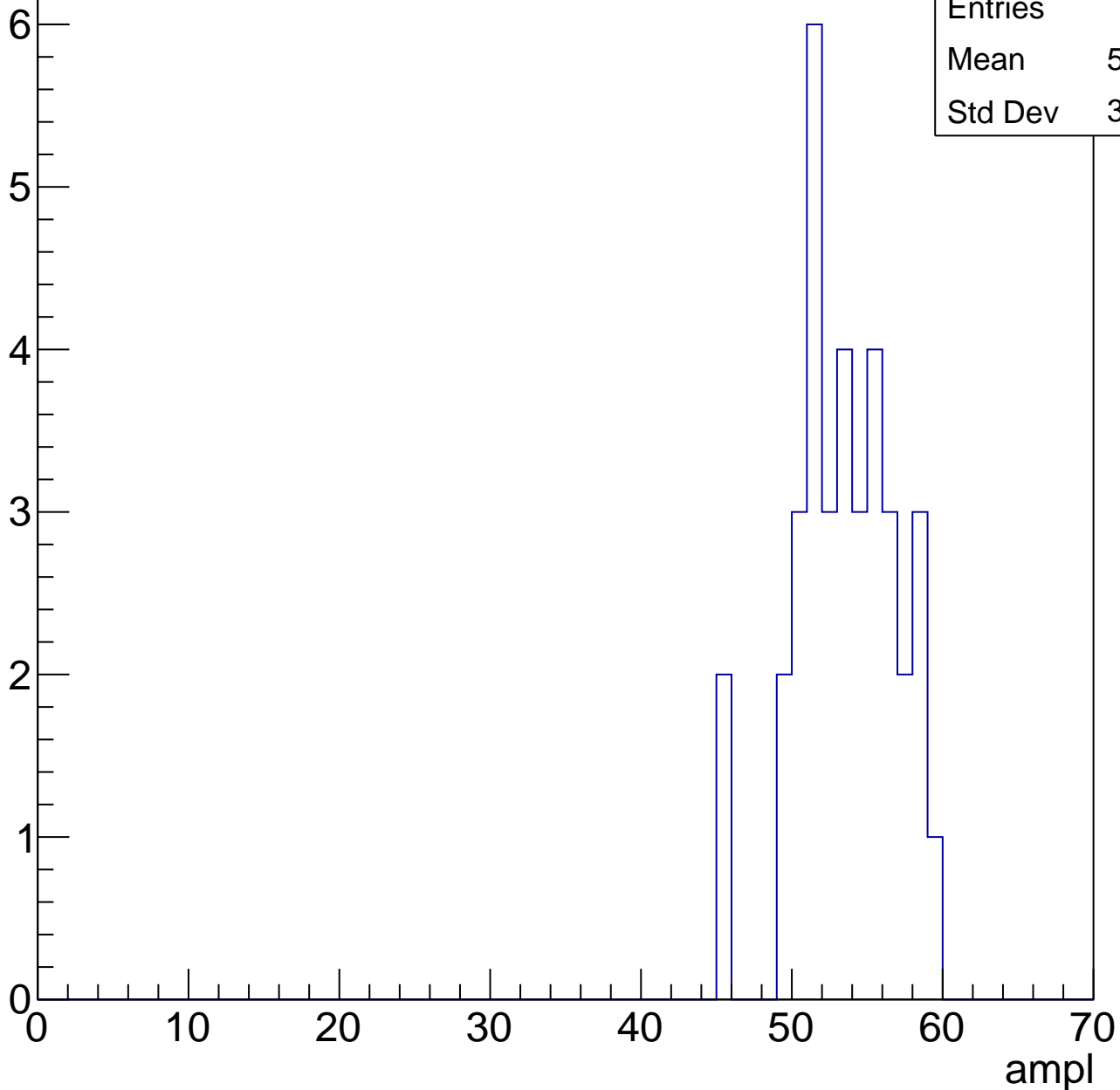


# B1L000S, U2-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

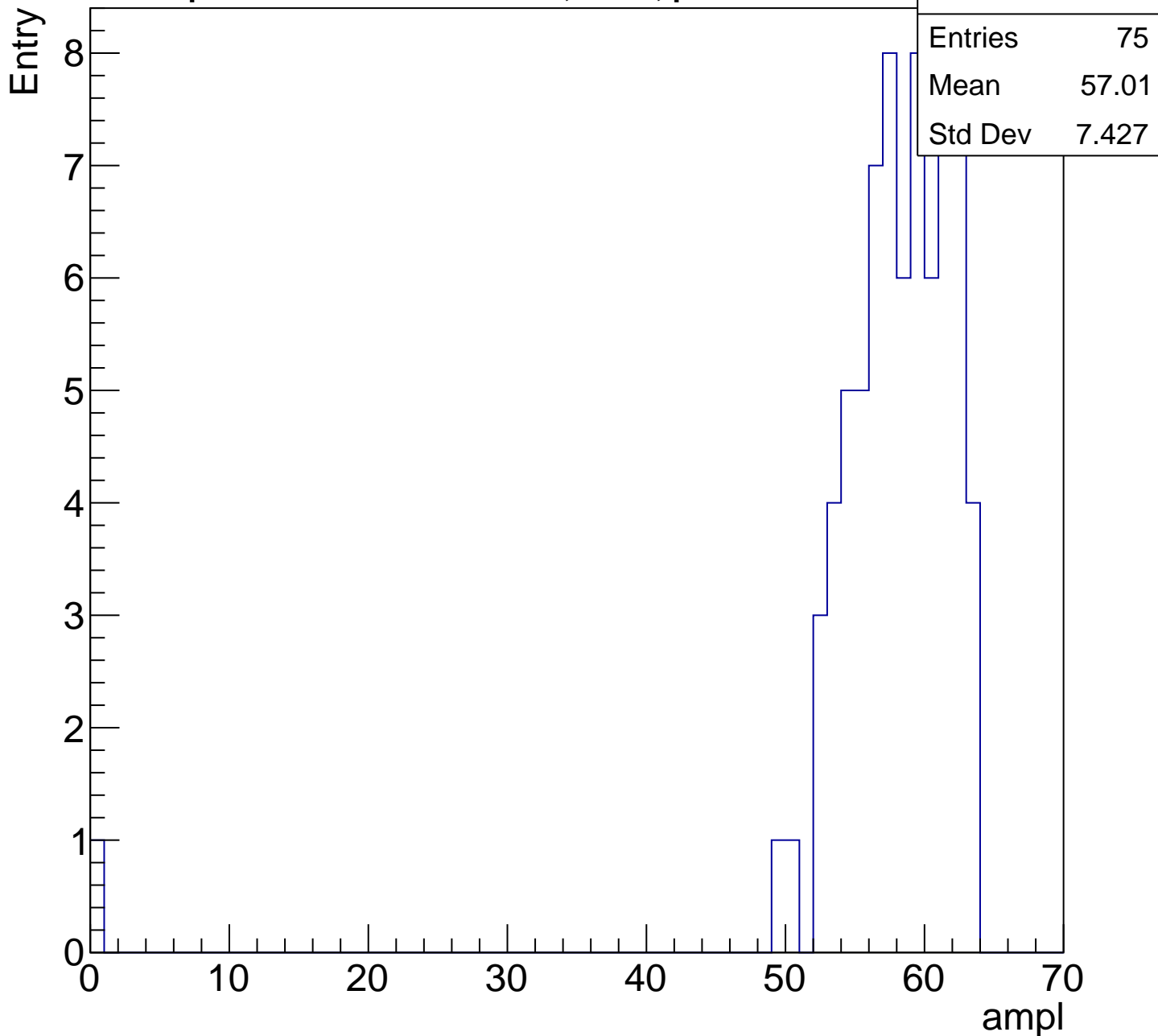
Entry

Entries	36
Mean	53.03
Std Dev	3.346



# B1L000S, U2-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

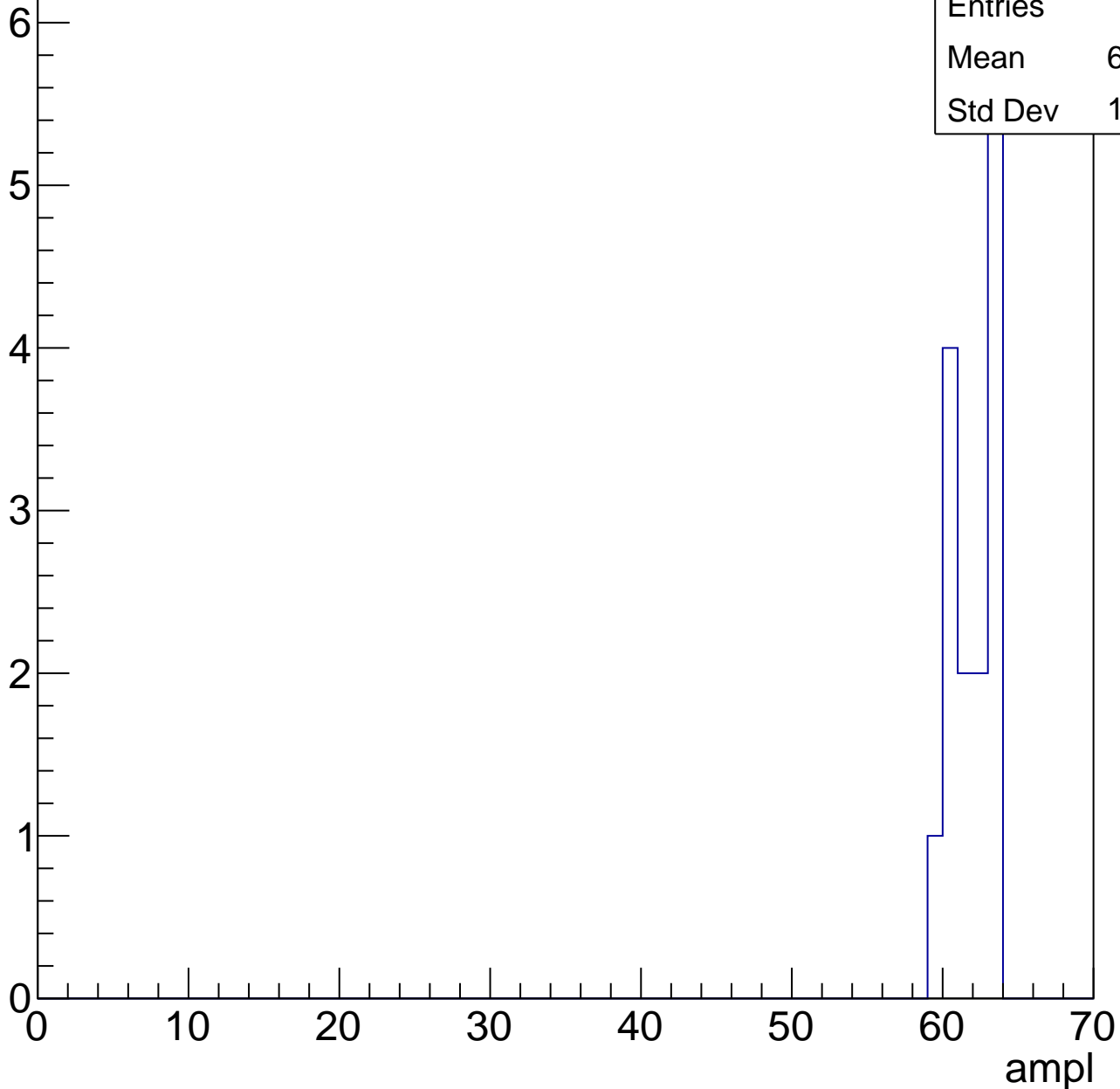


# B1L000S, U2-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.53
Std Dev	1.408

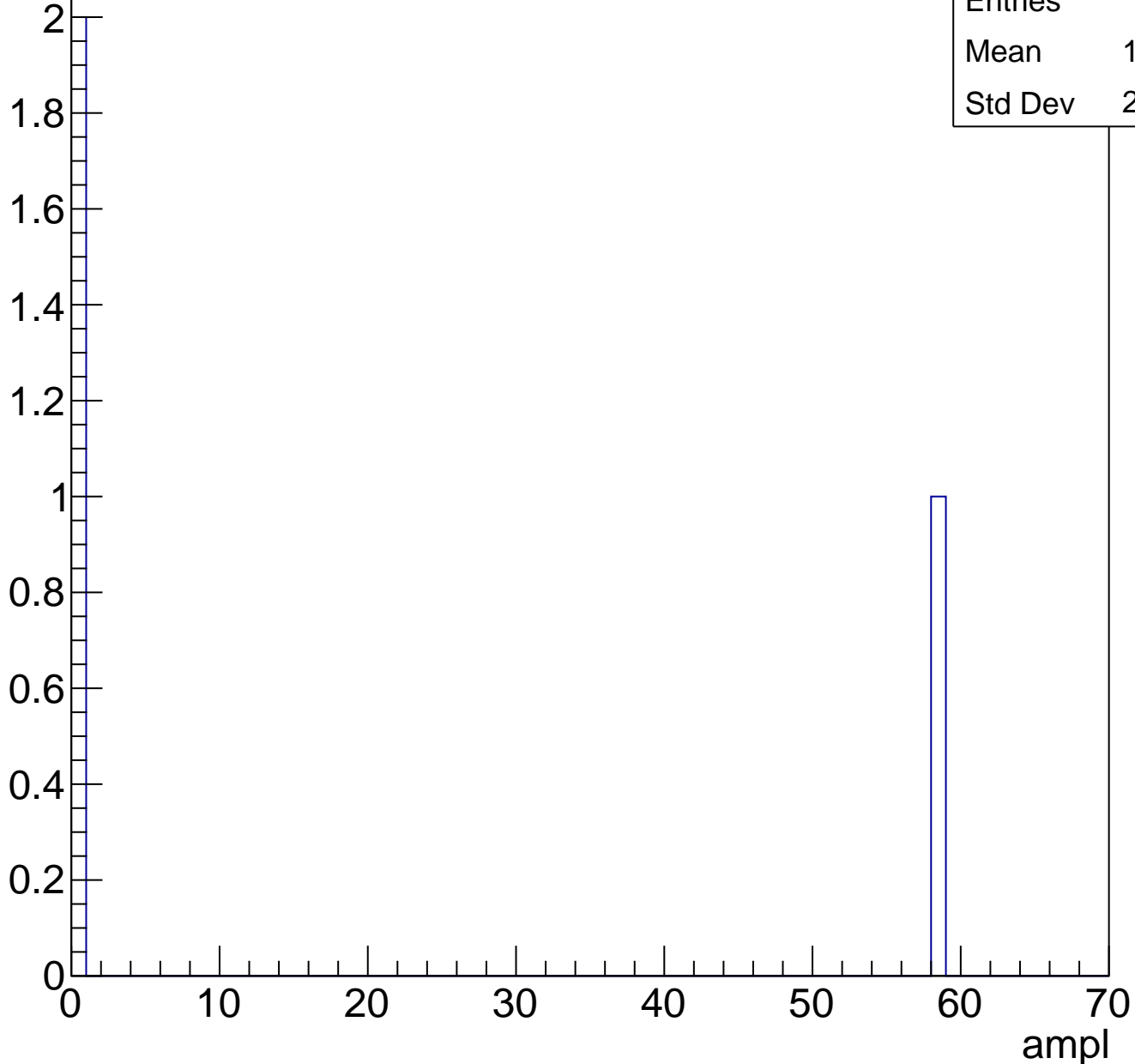




# B1L000S, U2-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	19.33
Std Dev	27.34

# B1L000S, U2-ch64, adc0

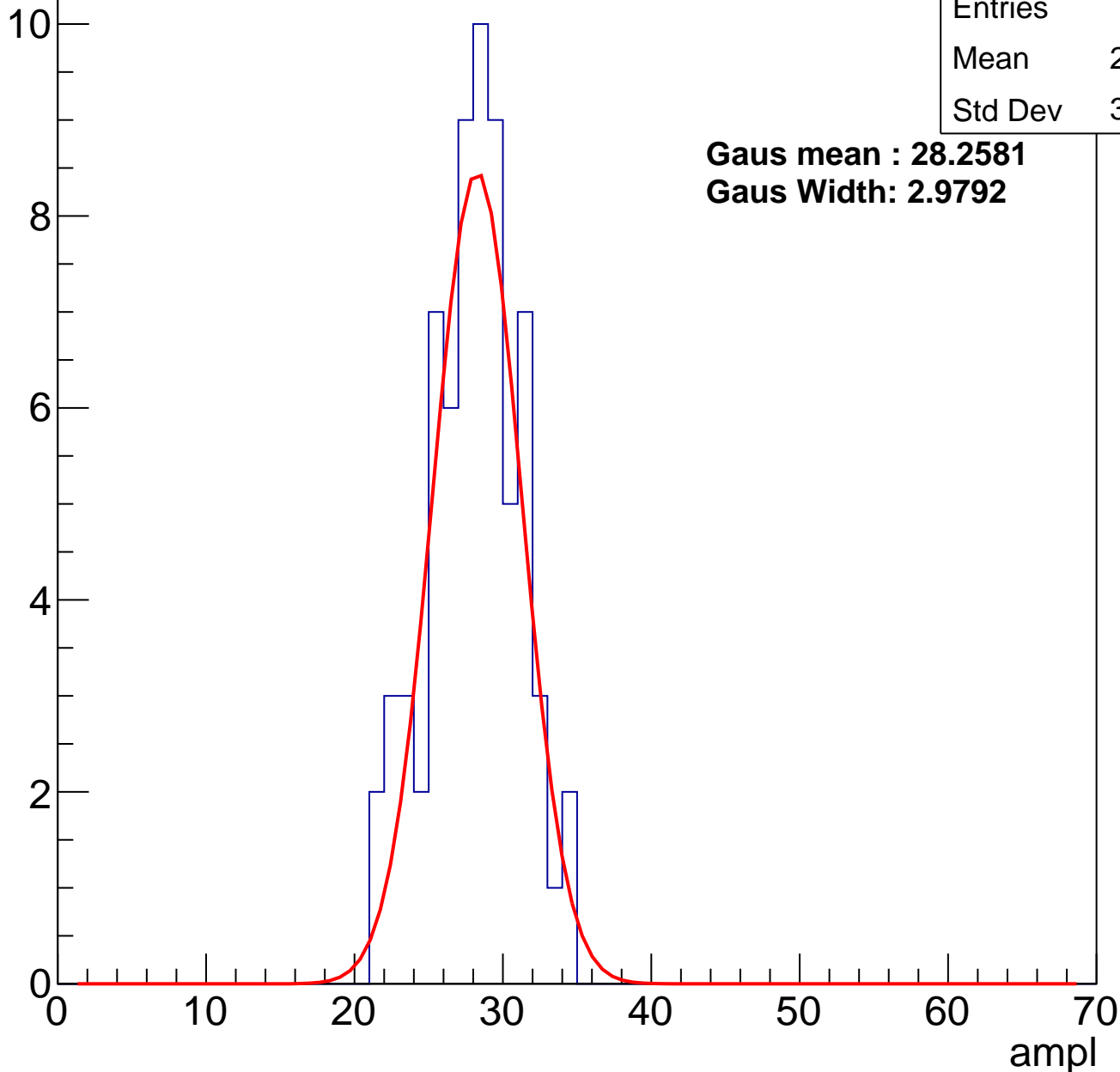
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	27.59
Std Dev	3.033

**Gaus mean : 28.2581**

**Gaus Width: 2.9792**

Entry



# B1L000S, U2-ch64, adc1

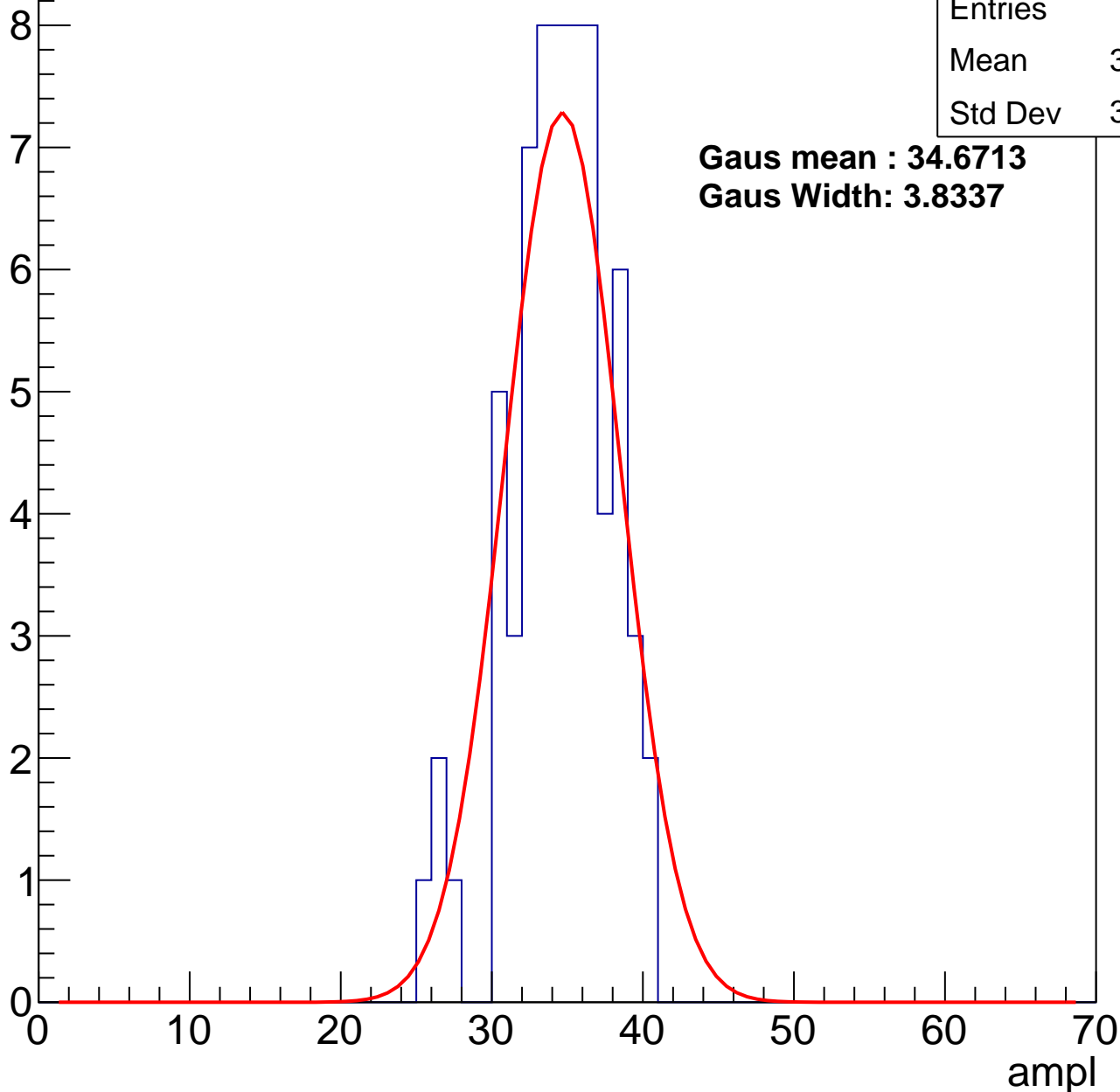
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.06
Std Dev	3.302

**Gaus mean : 34.6713**

**Gaus Width: 3.8337**



# B1L000S, U2-ch64, adc2

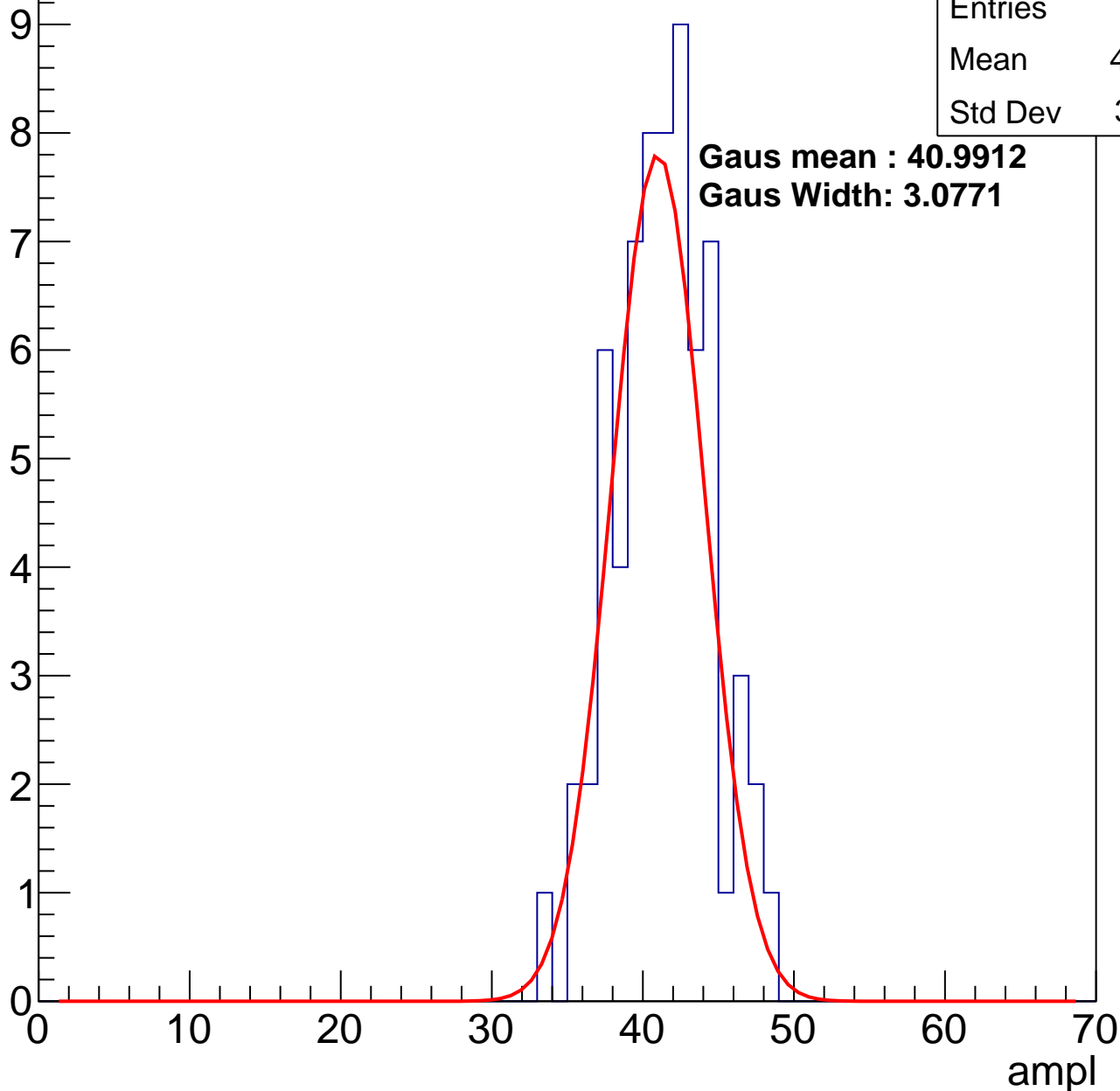
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	40.88
Std Dev	3.141

**Gaus mean : 40.9912**

**Gaus Width: 3.0771**



# B1L000S, U2-ch64, adc3

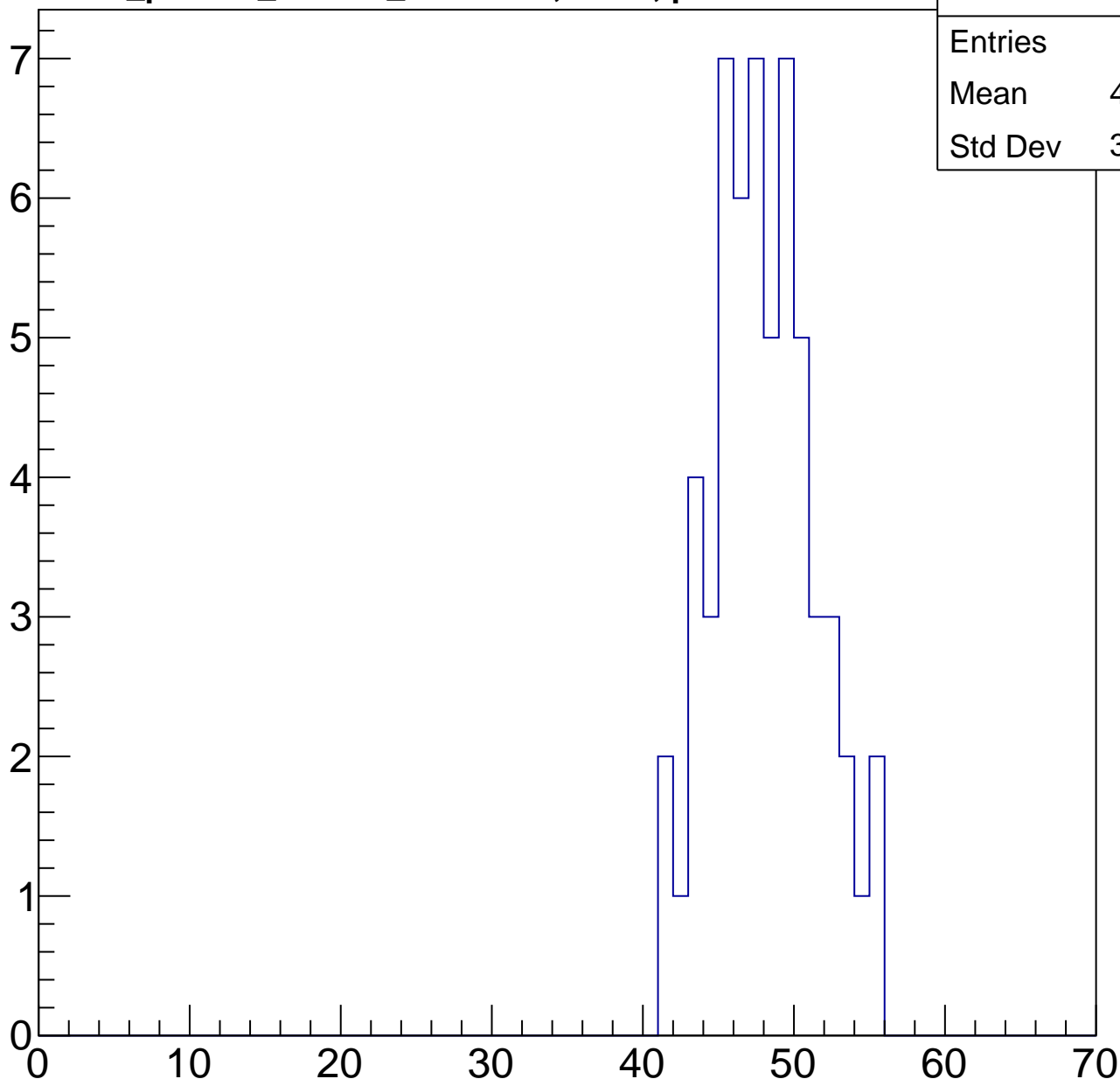
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	47.59
Std Dev	3.353

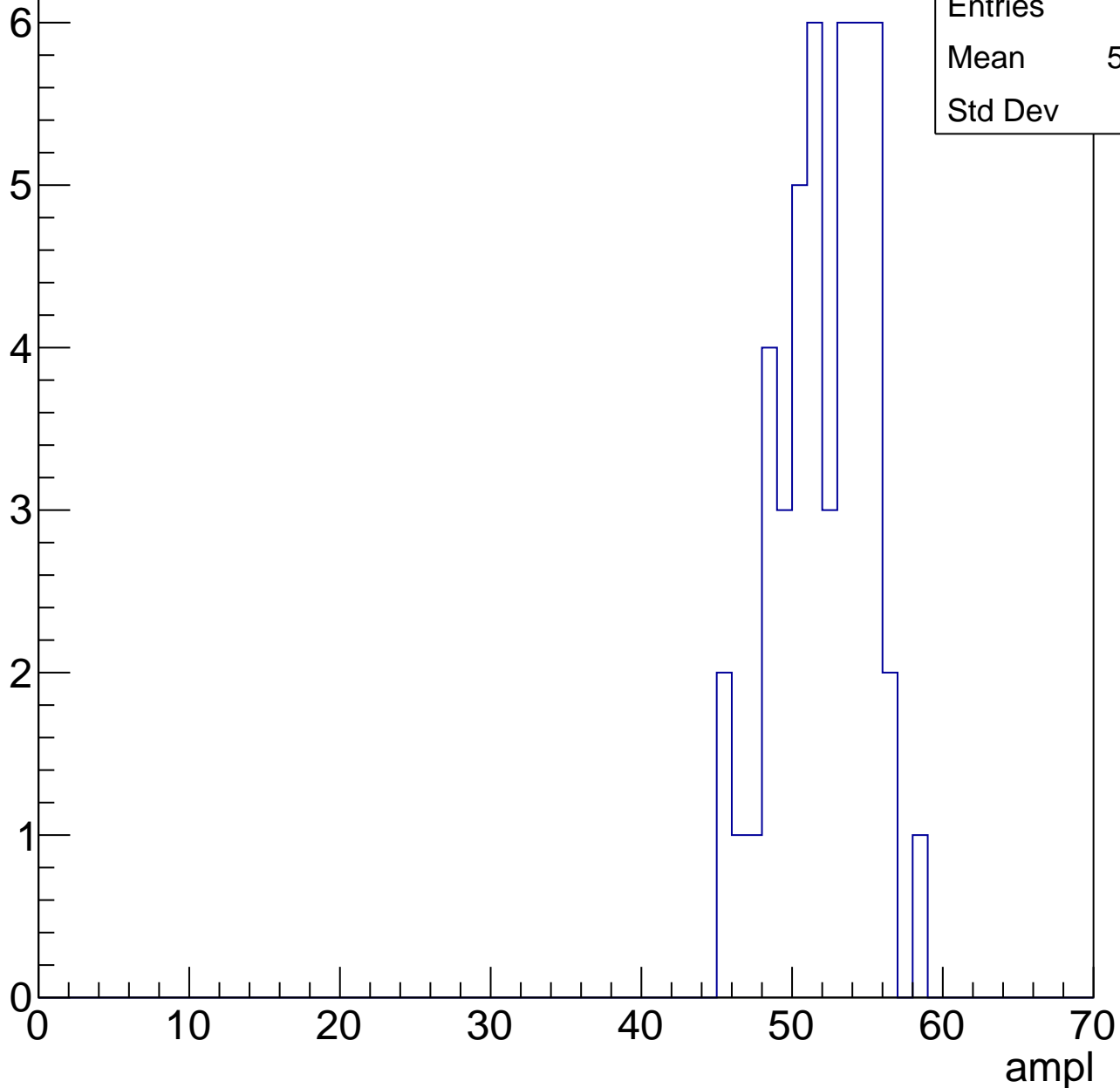
ampl



# B1L000S, U2-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch64, adc5

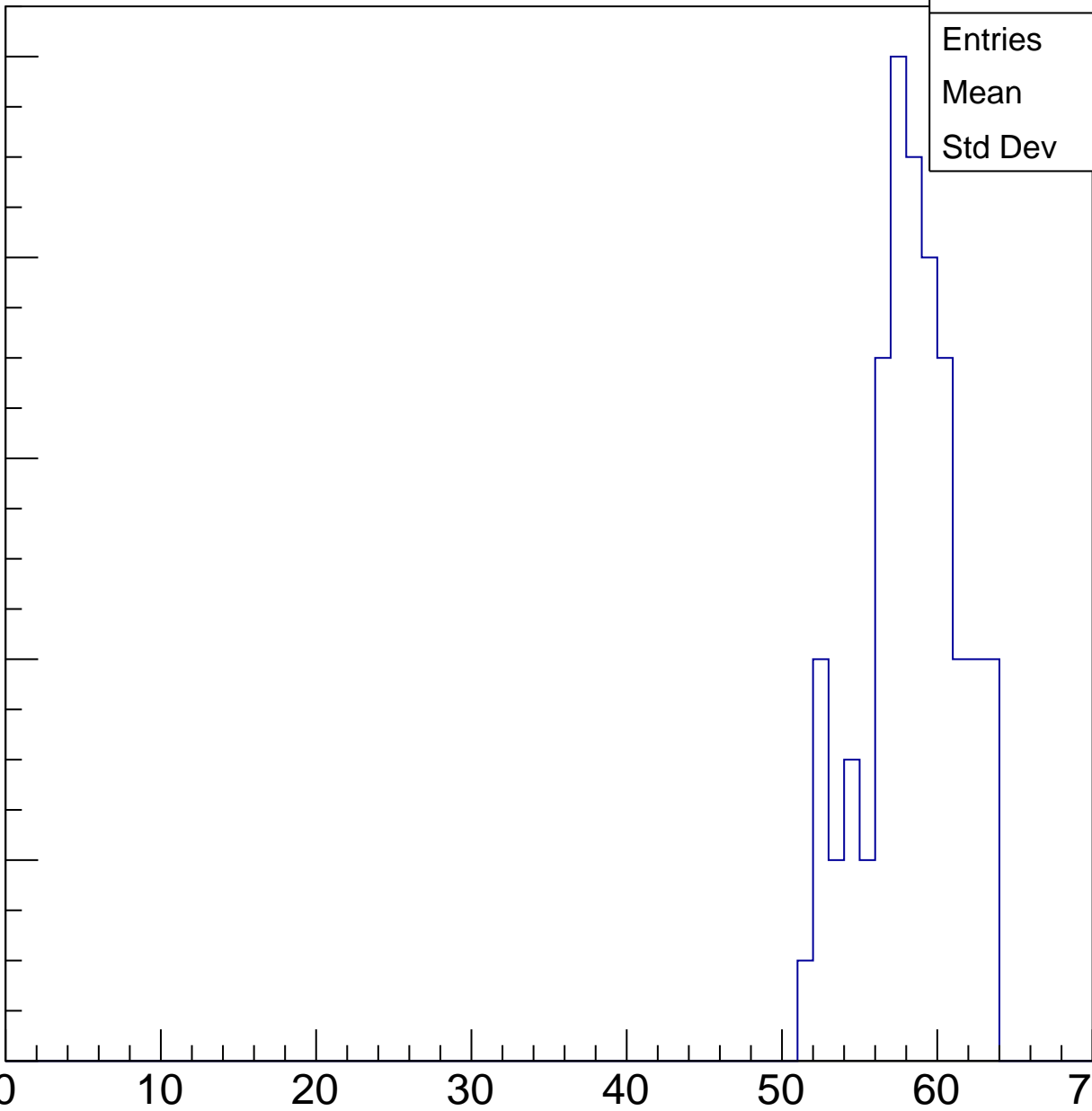
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10  
8  
6  
4  
2  
0

Entries	65
Mean	57.8
Std Dev	2.988

ampl

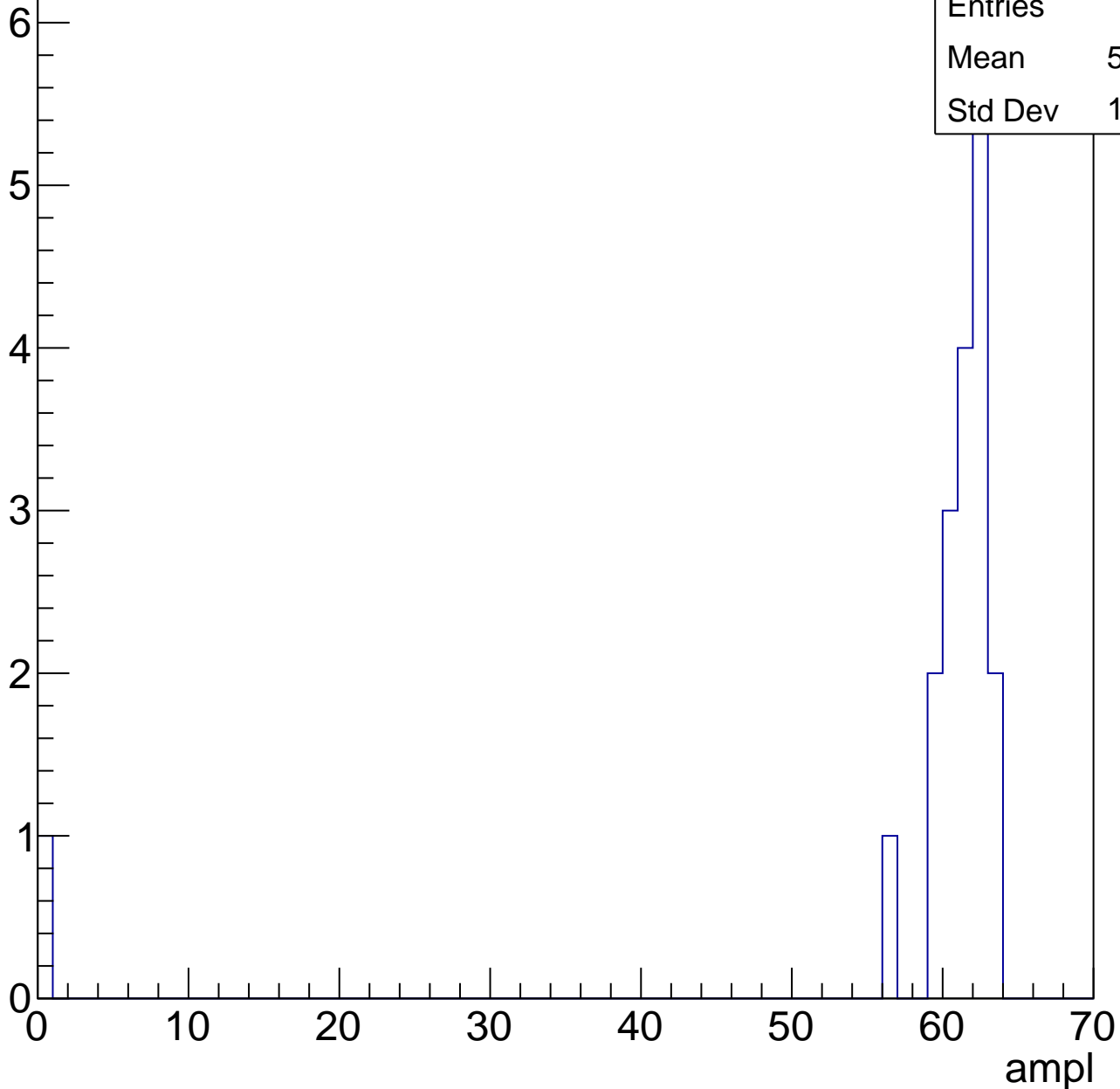


# B1L000S, U2-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	57.68
Std Dev	13.69

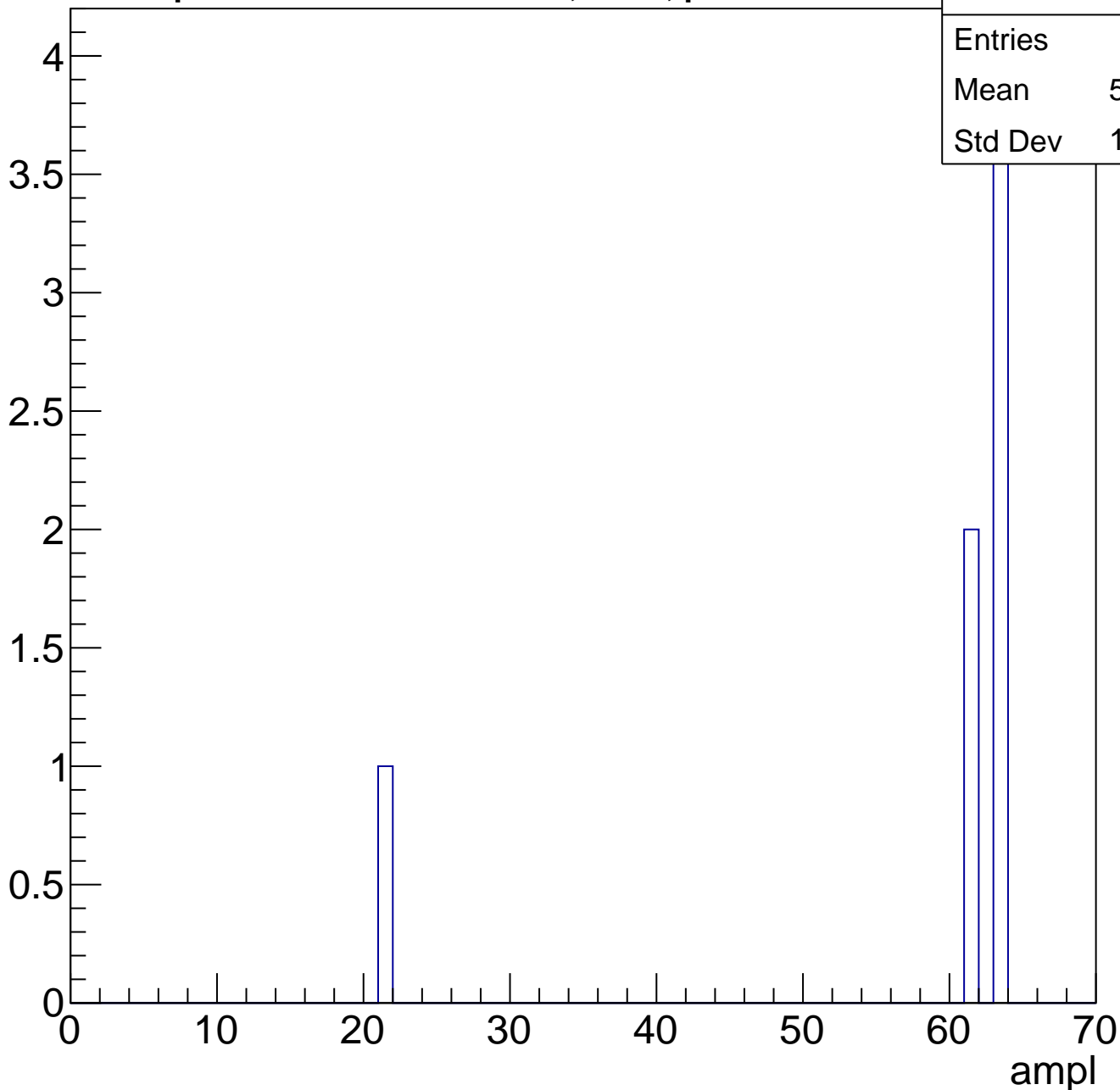




# B1L000S, U2-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	7
Mean	56.43
Std Dev	14.49

# B1L000S, U2-ch65, adc0

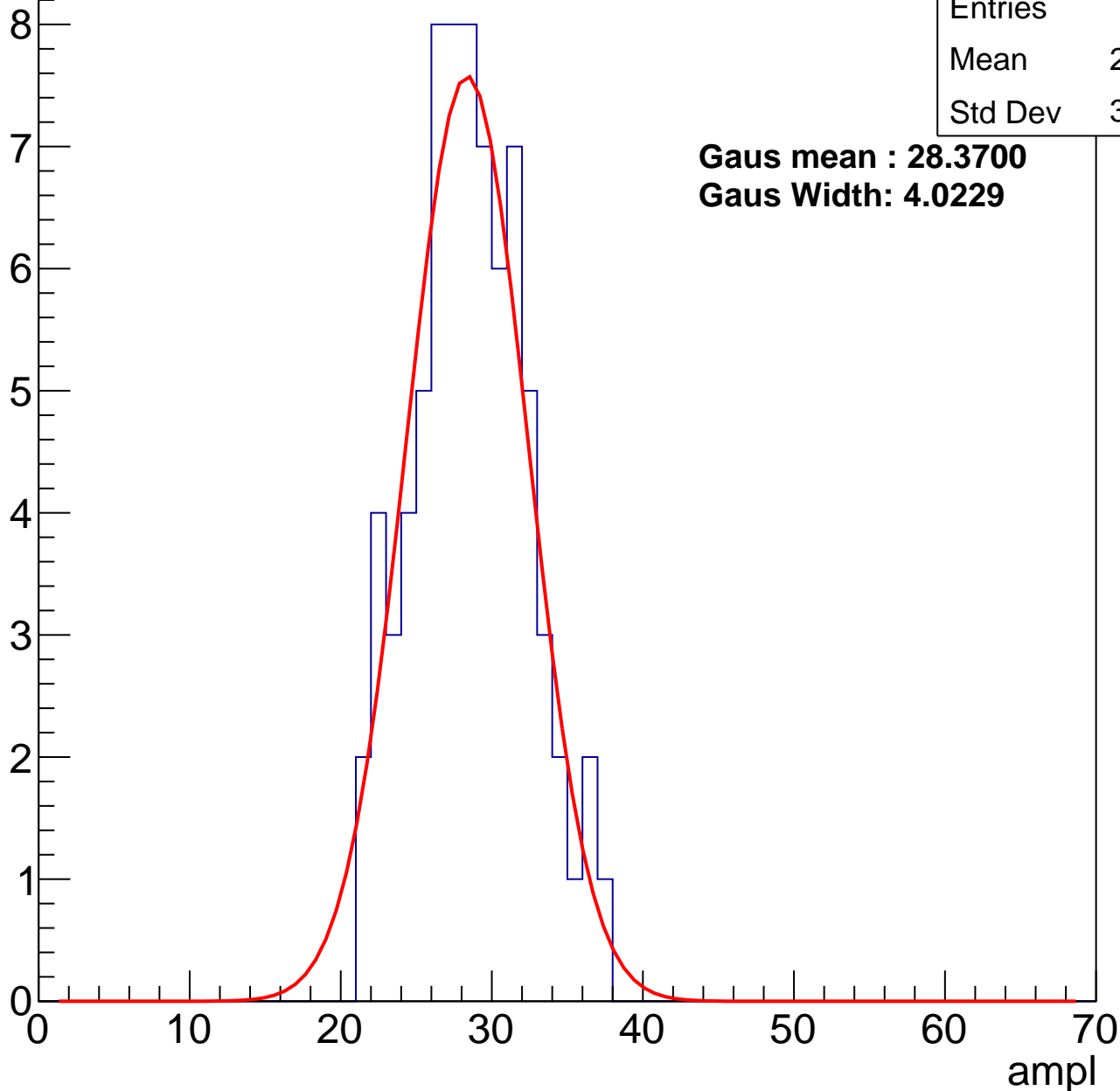
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	28.14
Std Dev	3.705

**Gaus mean : 28.3700**

**Gaus Width: 4.0229**



# B1L000S, U2-ch65, adc1

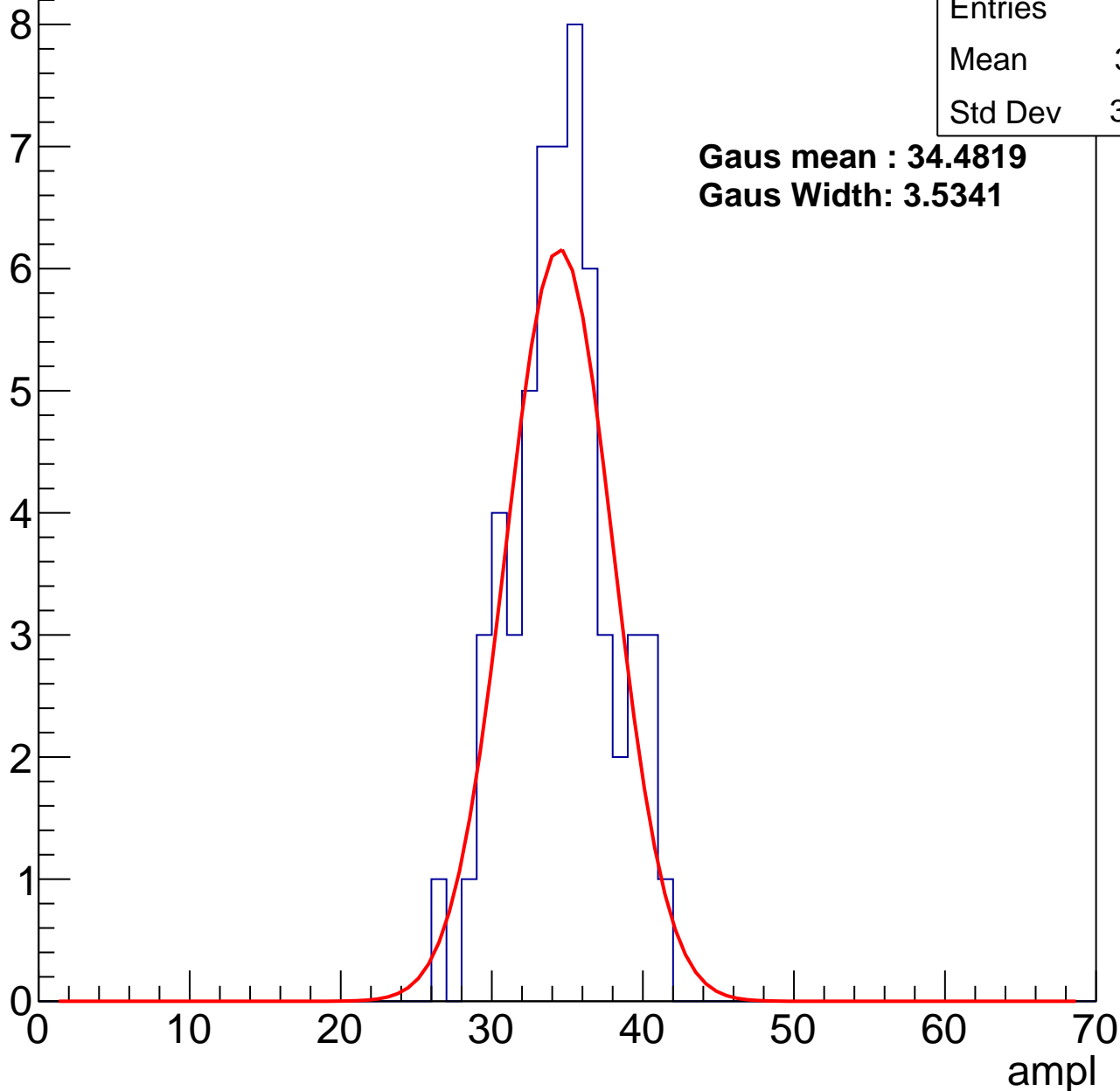
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	34.11
Std Dev	3.286

**Gaus mean : 34.4819**

**Gaus Width: 3.5341**



# B1L000S, U2-ch65, adc2

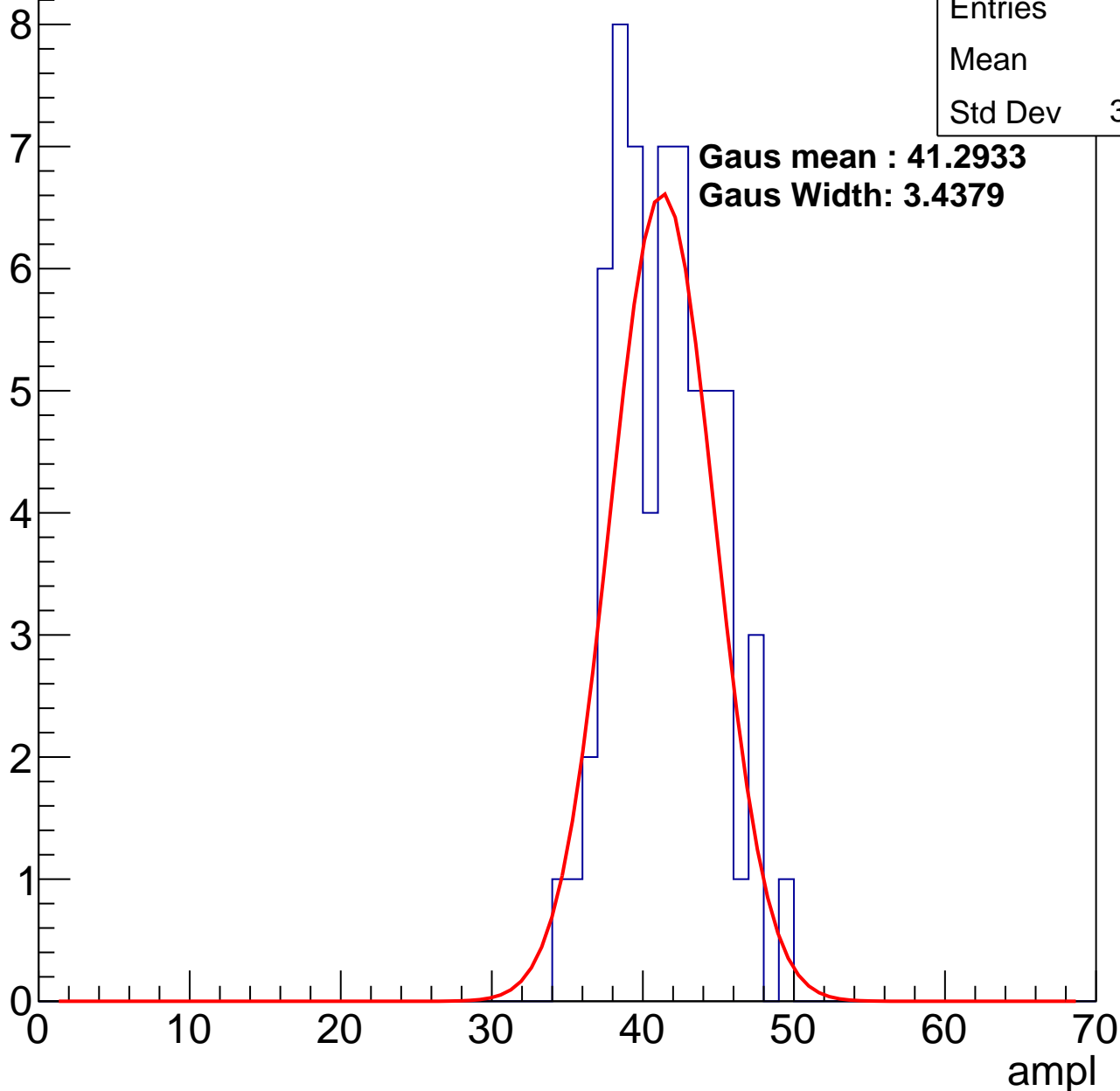
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	40.9
Std Dev	3.294

**Gaus mean : 41.2933**

**Gaus Width: 3.4379**

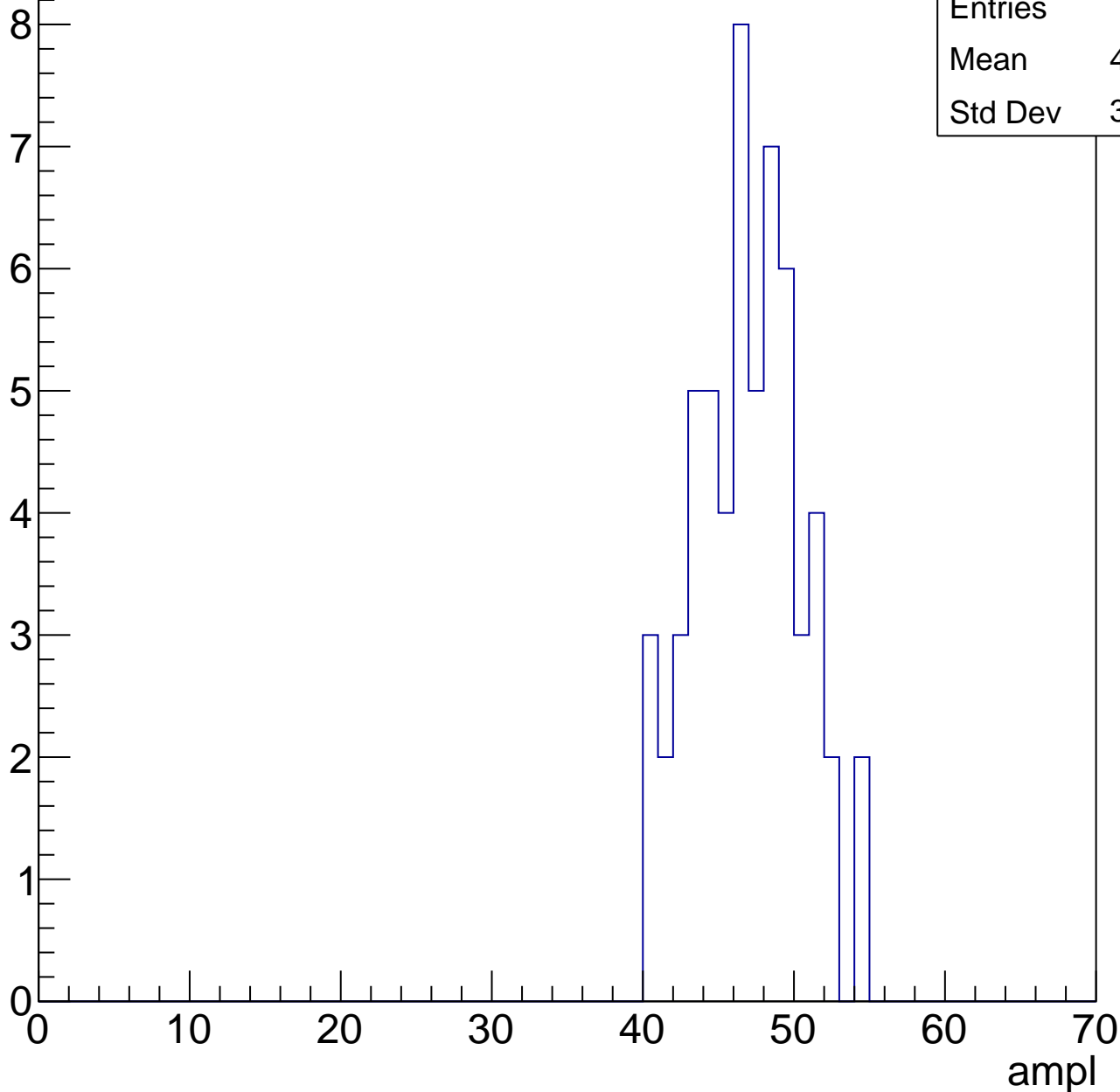


# B1L000S, U2-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

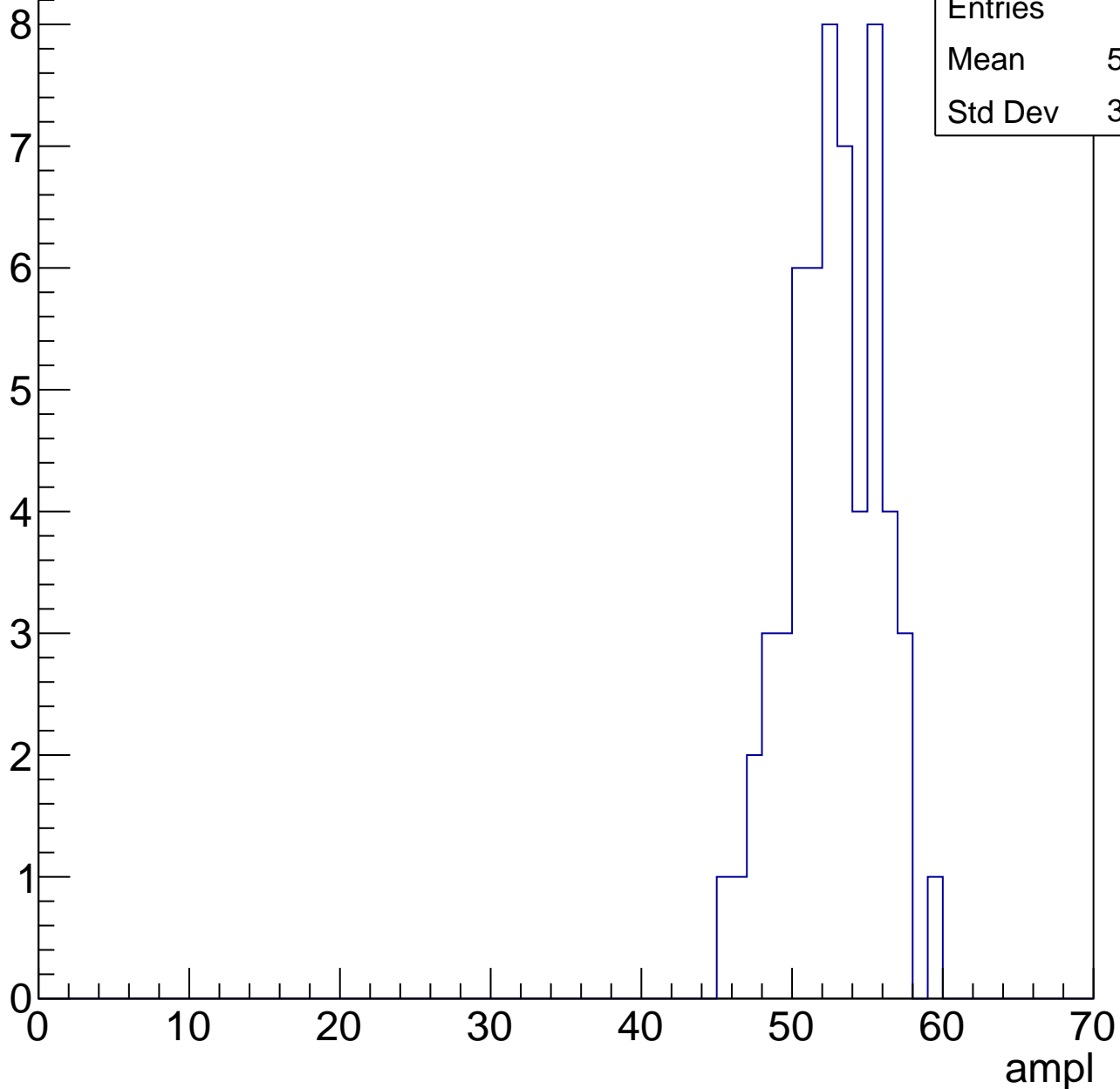
Entries	59
Mean	46.47
Std Dev	3.436



# B1L000S, U2-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

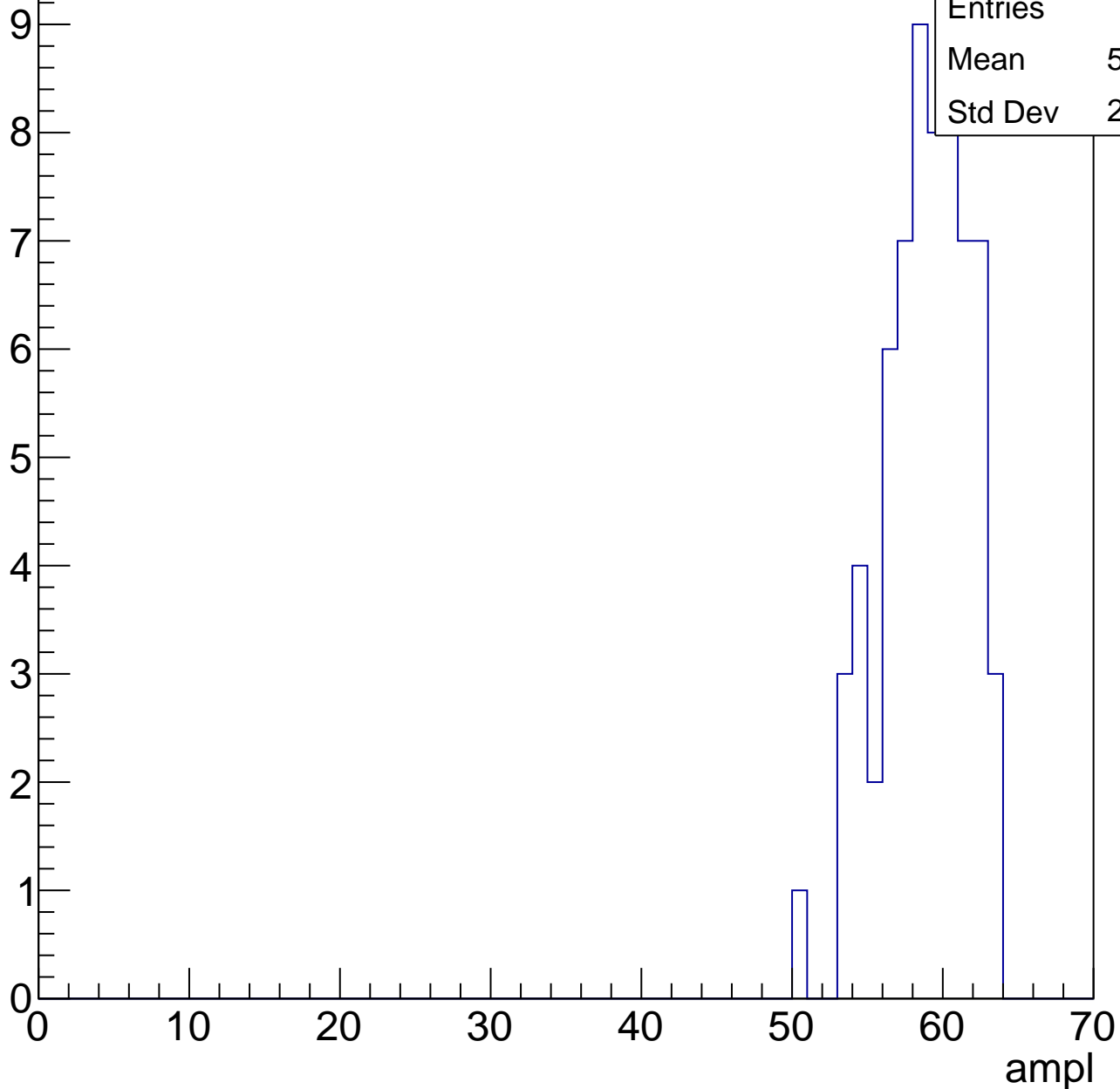
Entry



# B1L000S, U2-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

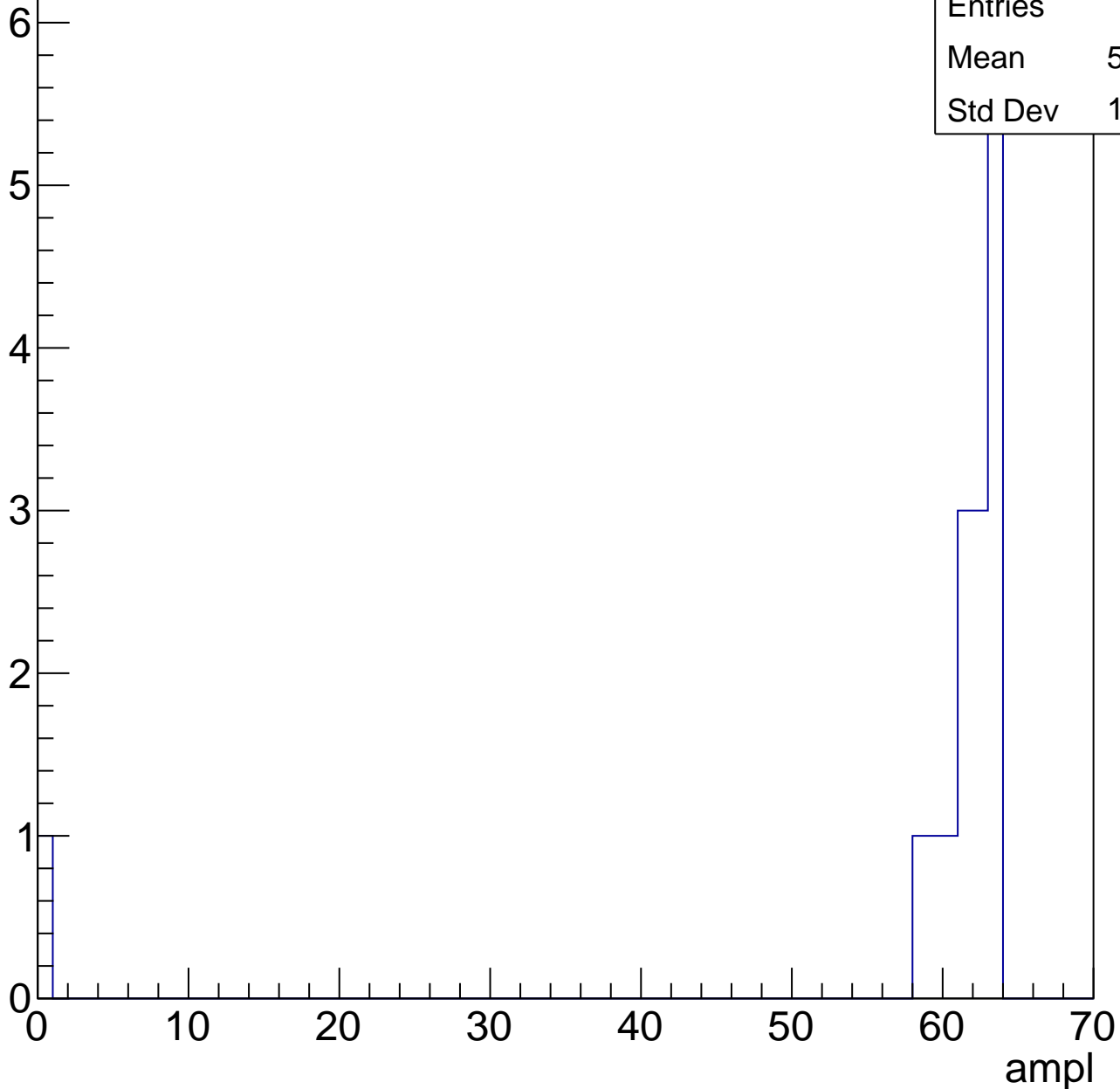


# B1L000S, U2-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.75
Std Dev	14.99





# B1L000S, U2-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch66, adc0

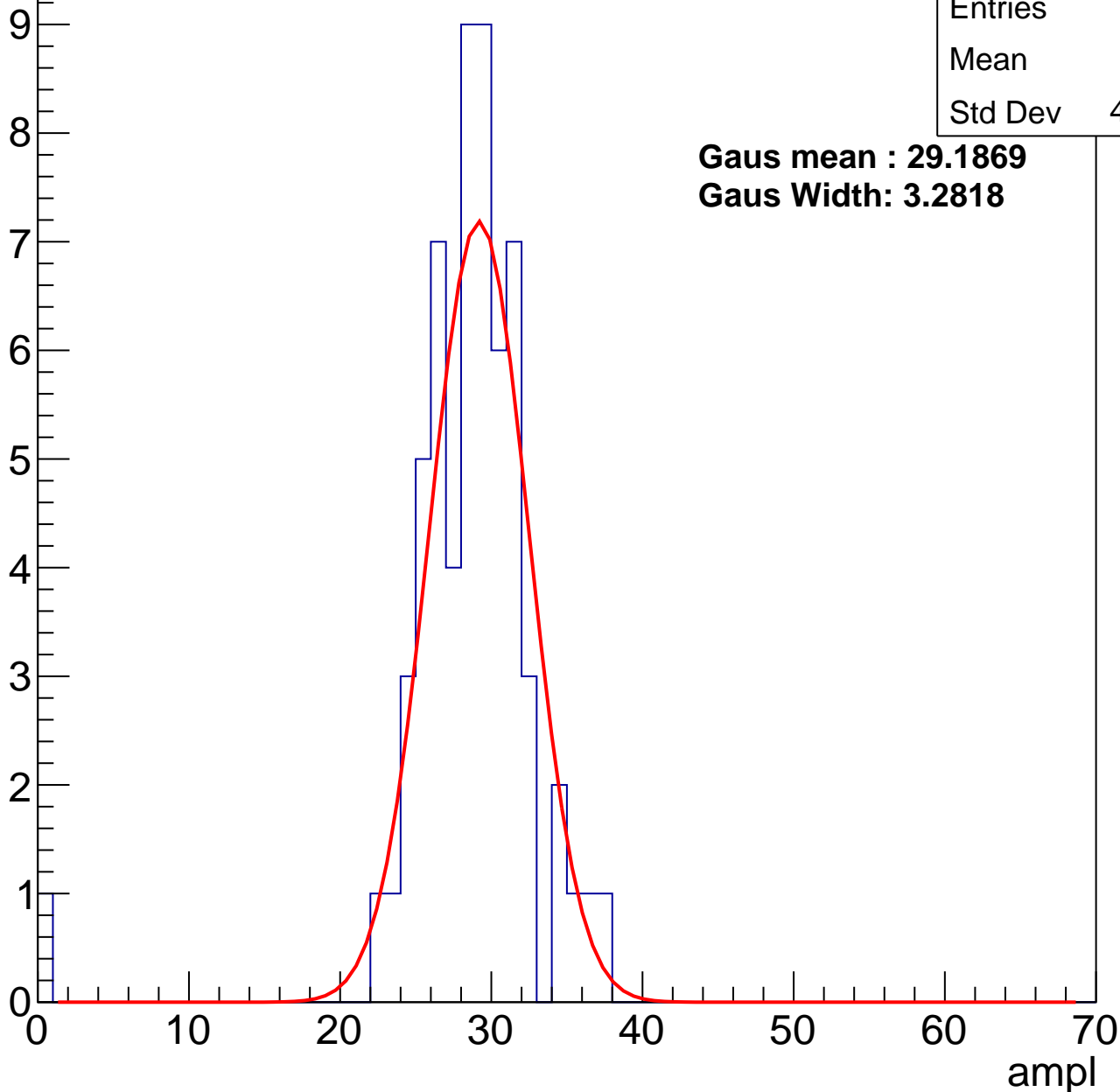
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.1
Std Dev	4.755

**Gaus mean : 29.1869**

**Gaus Width: 3.2818**



# B1L000S, U2-ch66, adc1

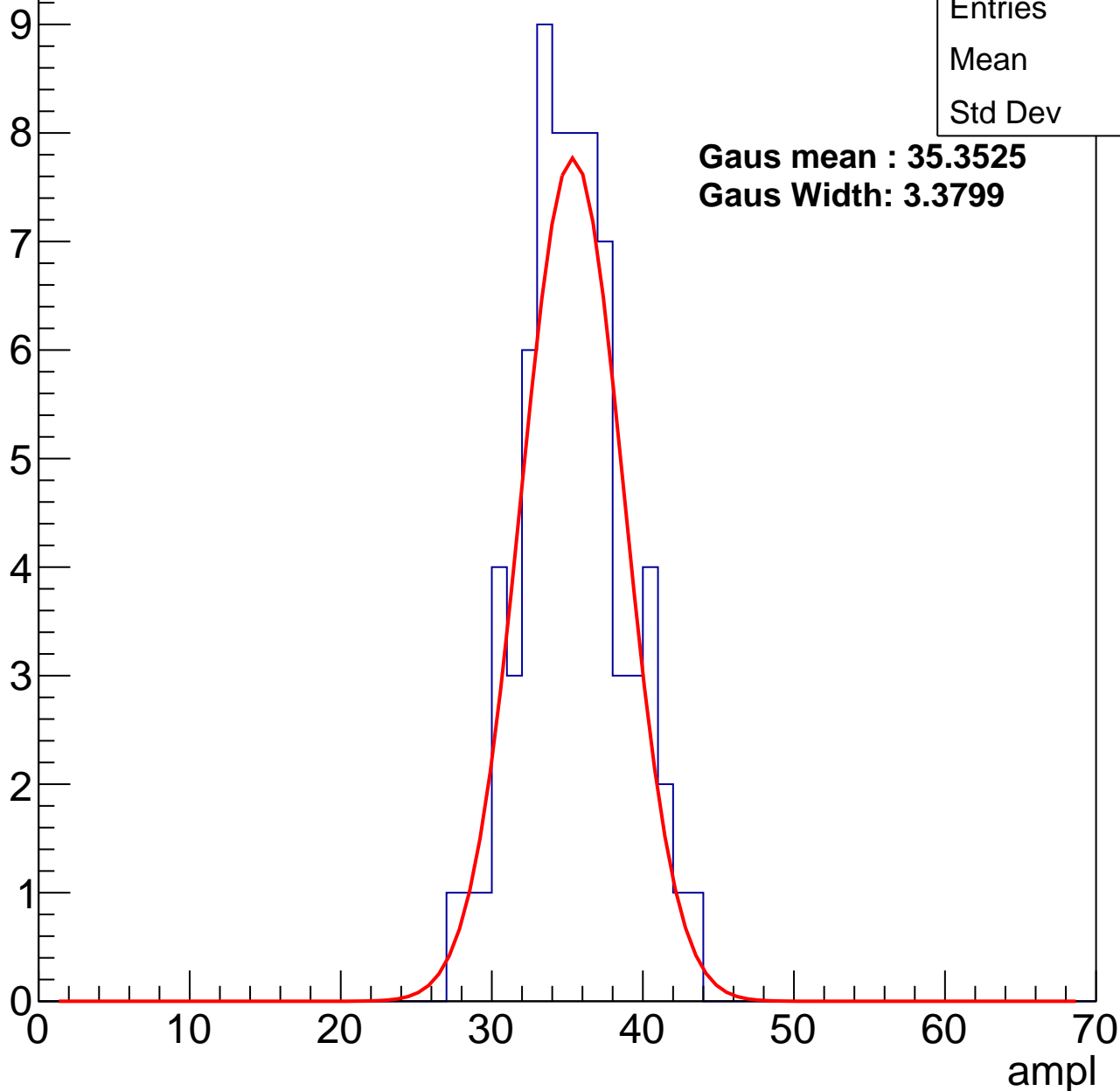
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	34.9
Std Dev	3.36

**Gaus mean : 35.3525**

**Gaus Width: 3.3799**

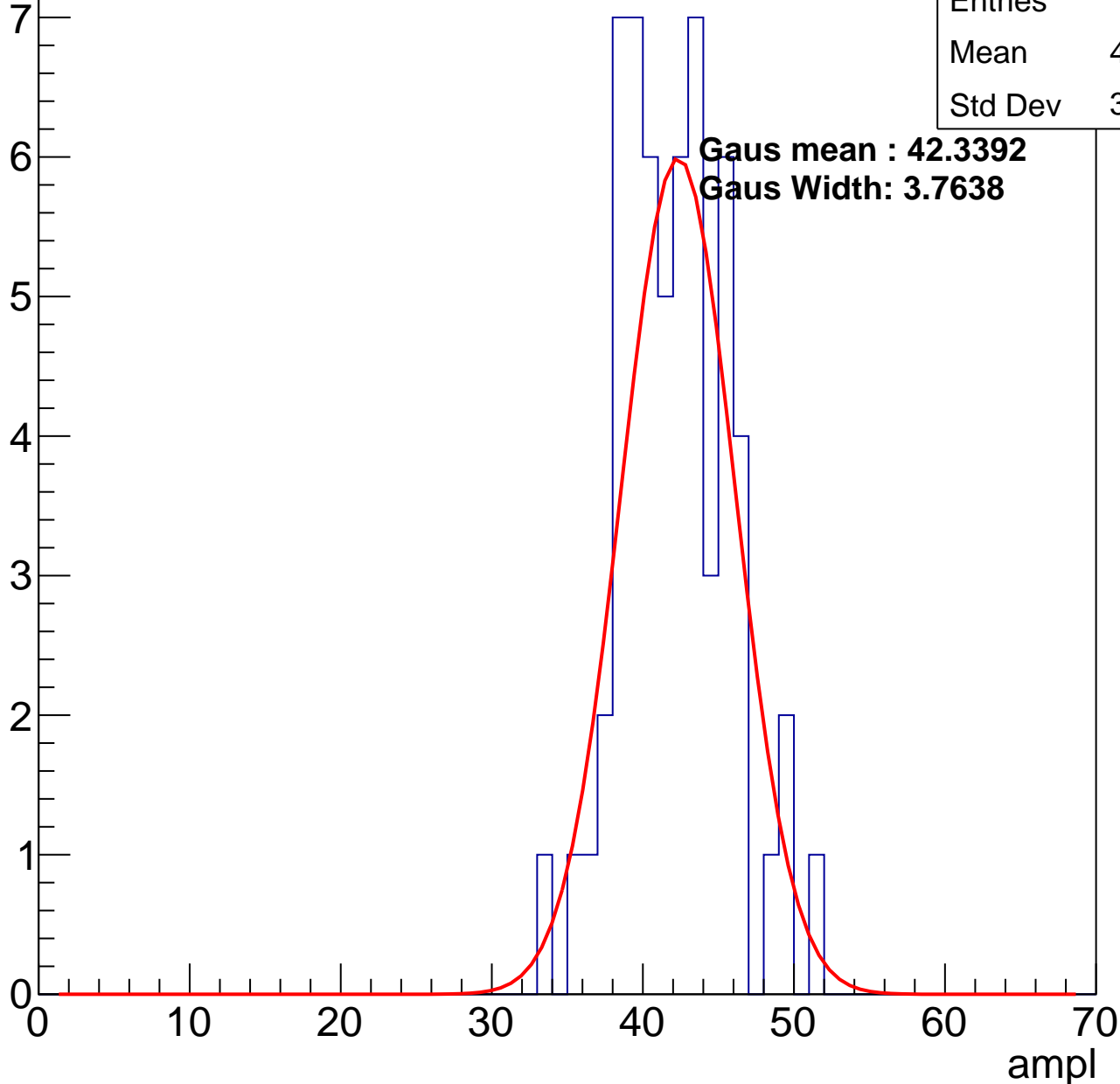


# B1L000S, U2-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.63
Std Dev	3.568



# B1L000S, U2-ch66, adc3

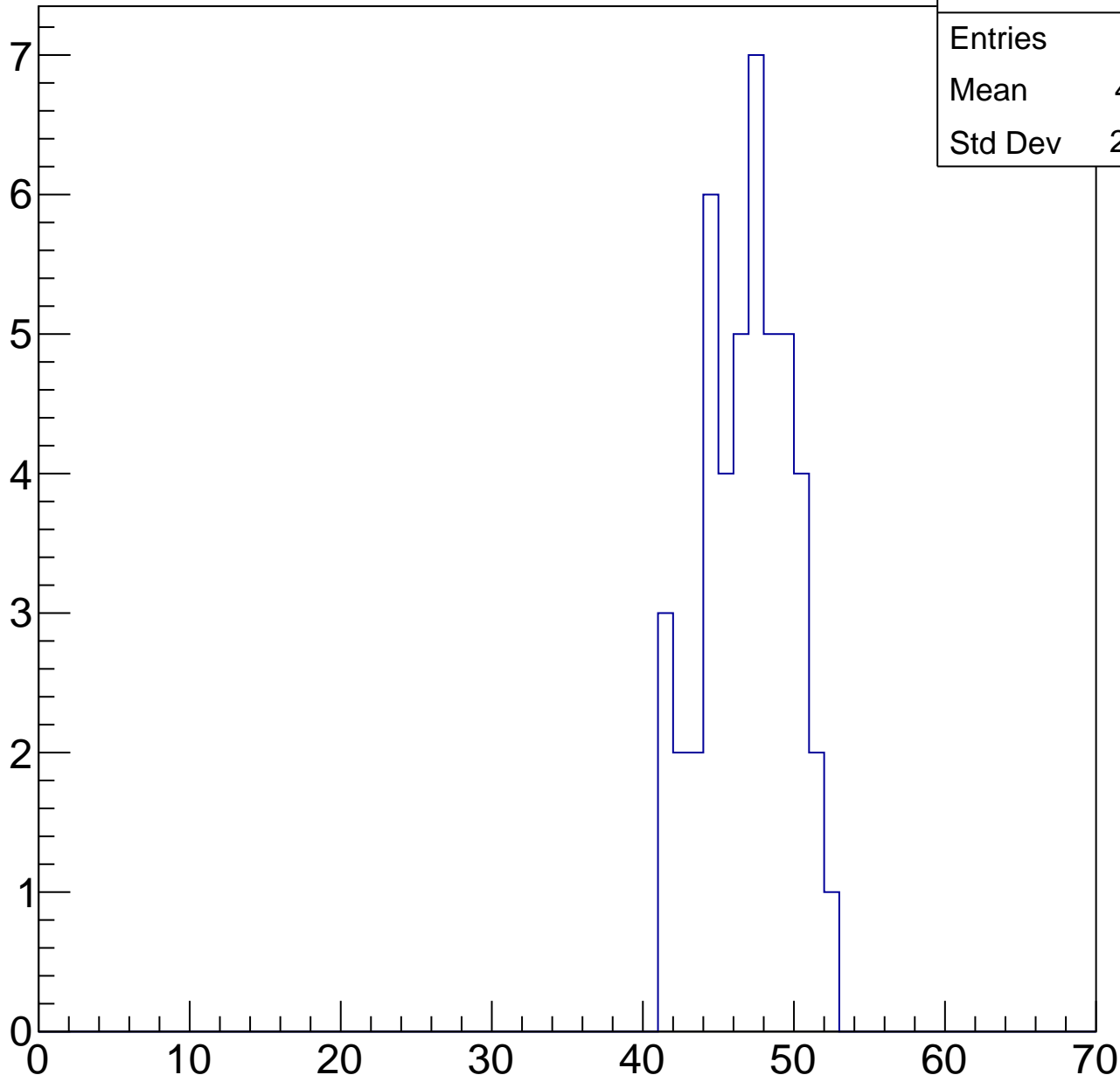
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

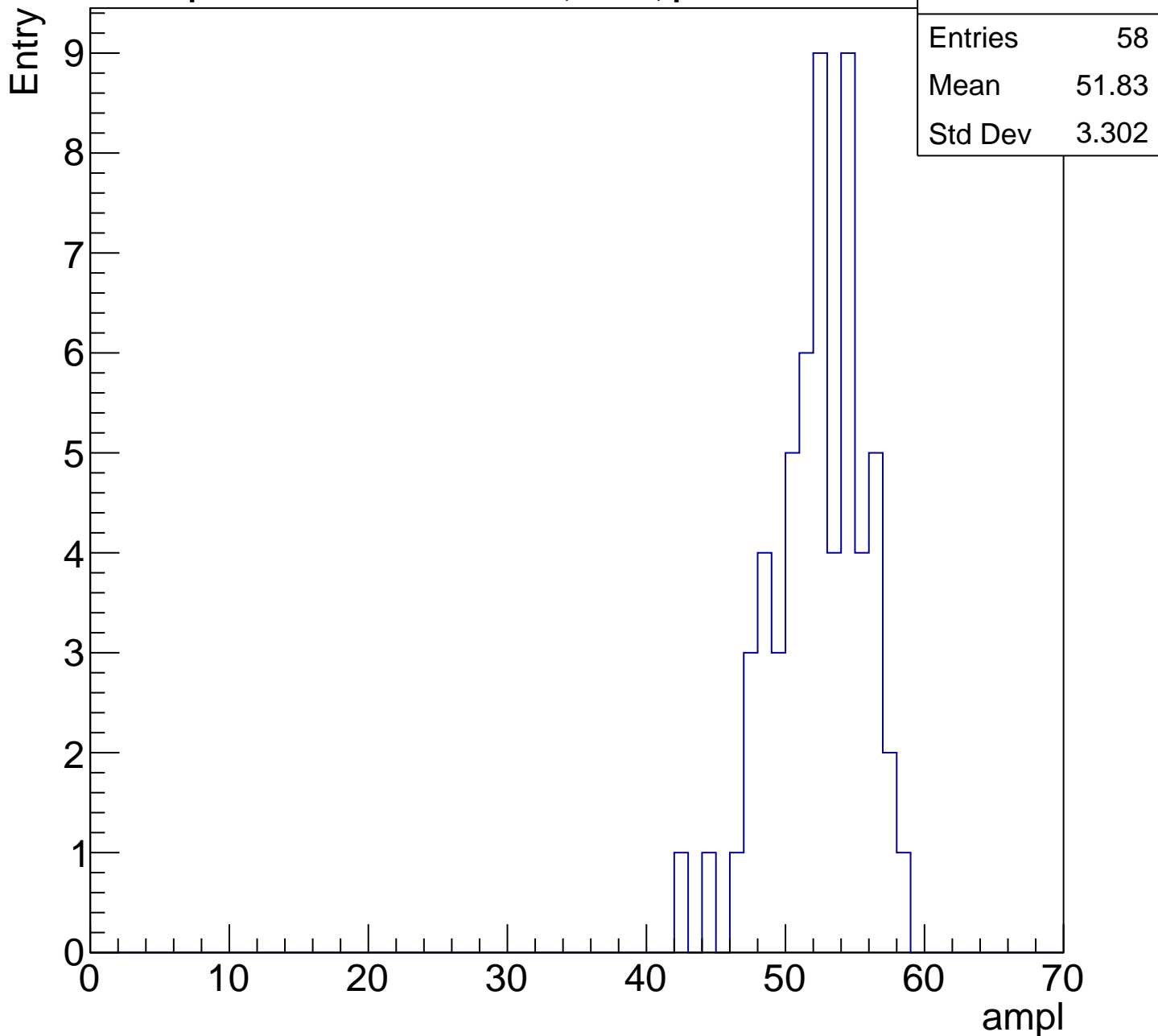
Entries	46
Mean	46.41
Std Dev	2.825

ampl



# B1L000S, U2-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

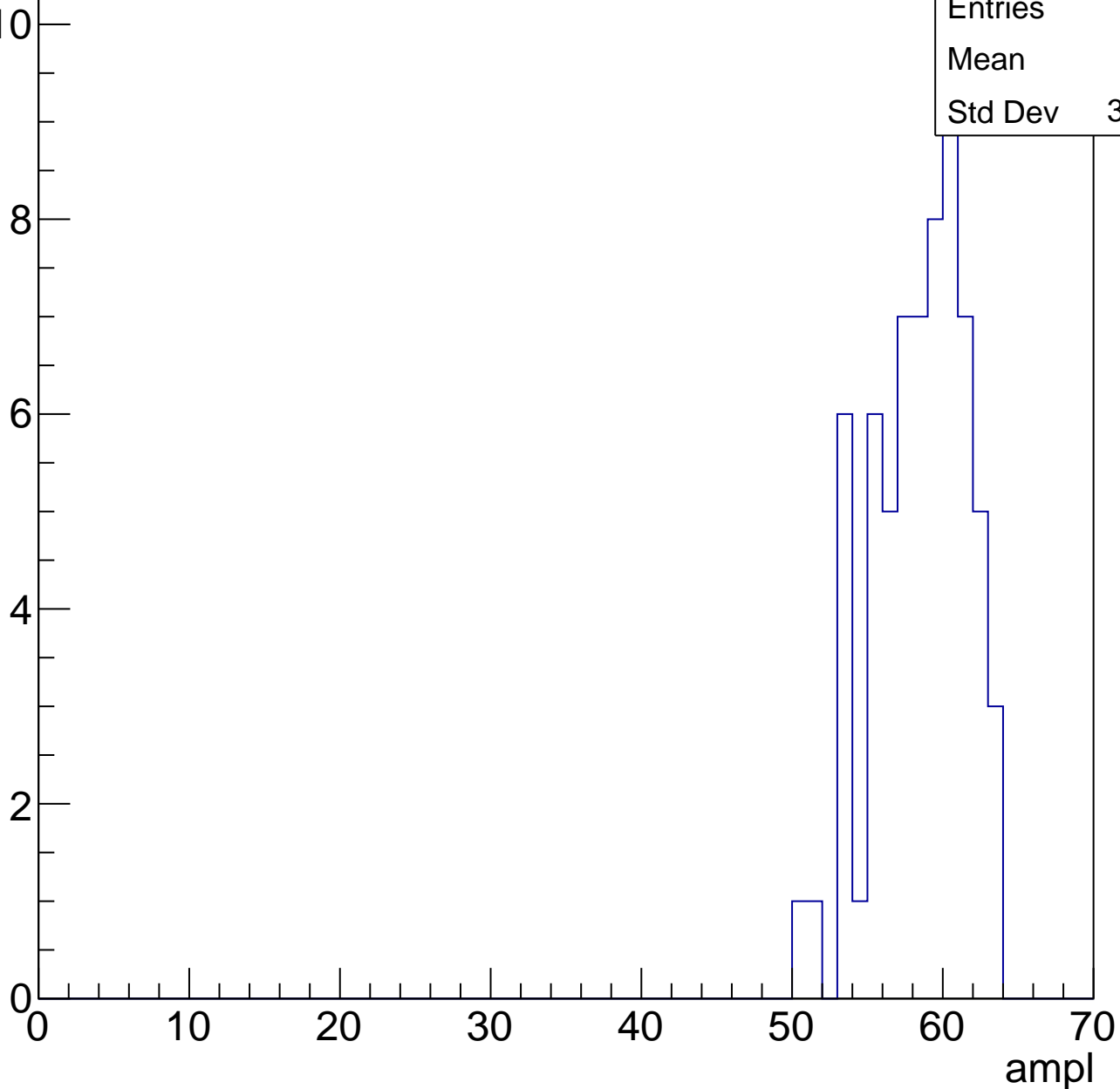


# B1L000S, U2-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	58
Std Dev	3.057

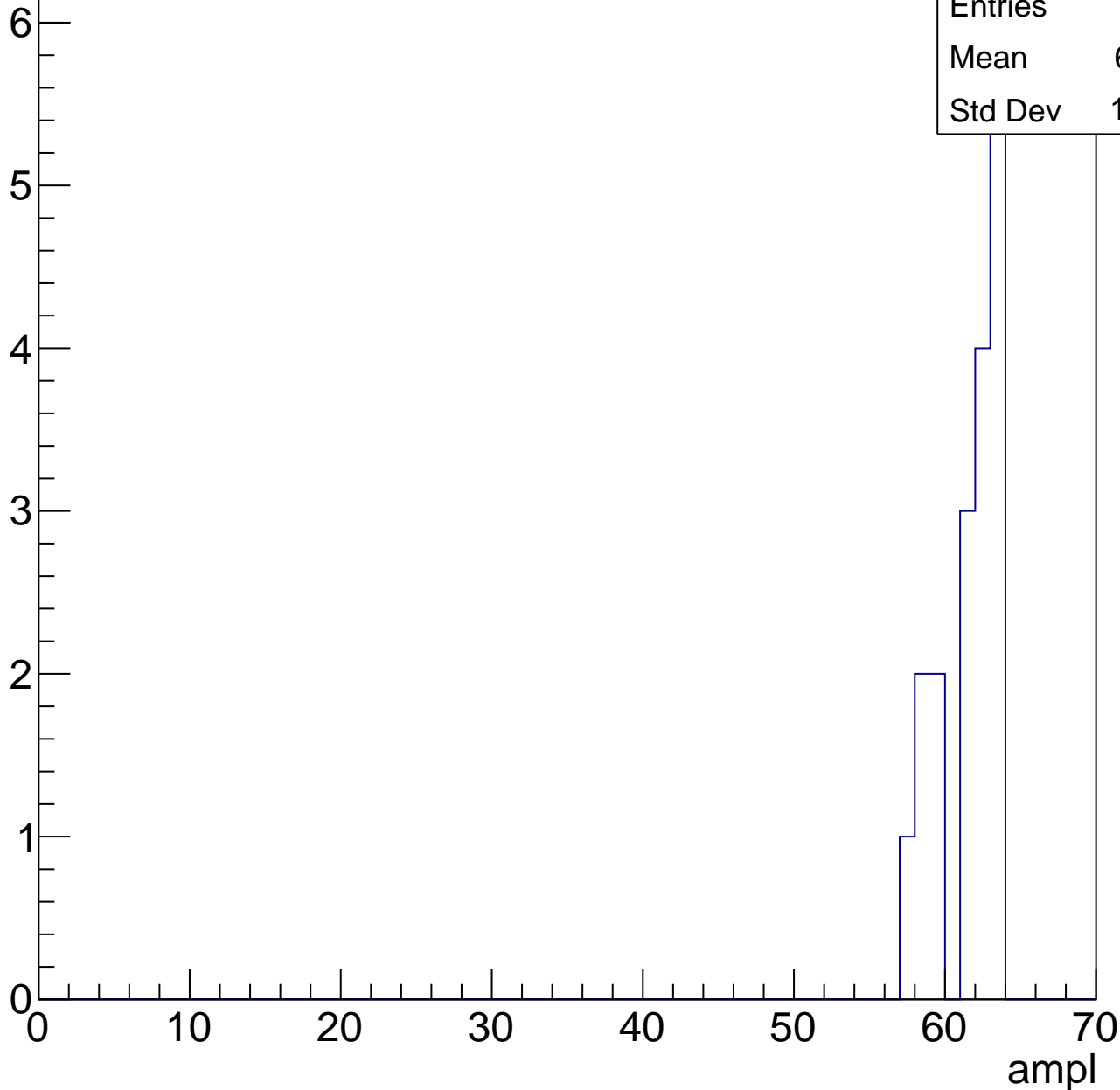


# B1L000S, U2-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.11
Std Dev	1.969





# B1L000S, U2-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	31.25
Std Dev	31.25

# B1L000S, U2-ch67, adc0

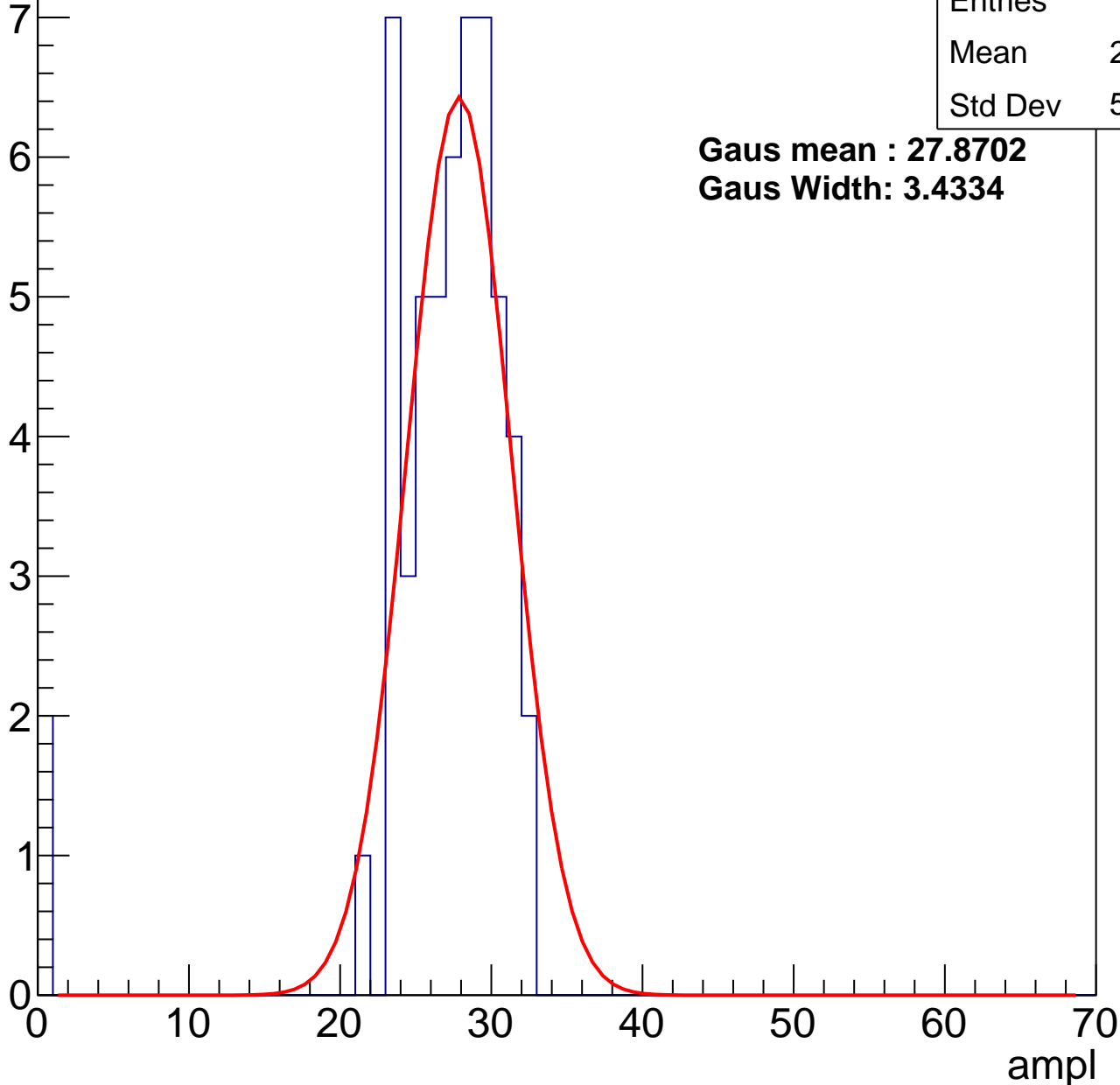
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	26.07
Std Dev	5.783

**Gaus mean : 27.8702**

**Gaus Width: 3.4334**



# B1L000S, U2-ch67, adc1

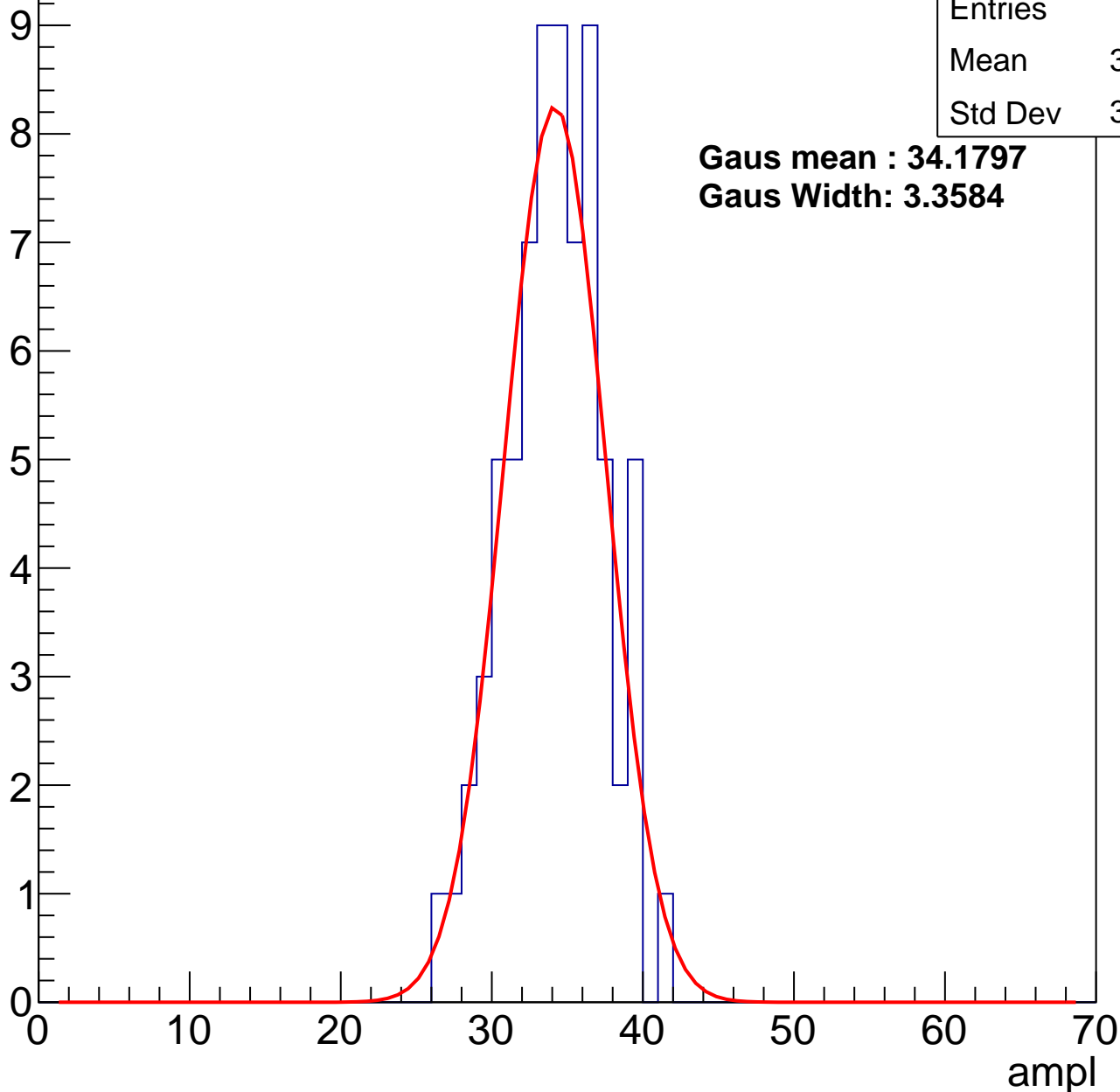
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	33.72
Std Dev	3.163

**Gaus mean : 34.1797**

**Gaus Width: 3.3584**



# B1L000S, U2-ch67, adc2

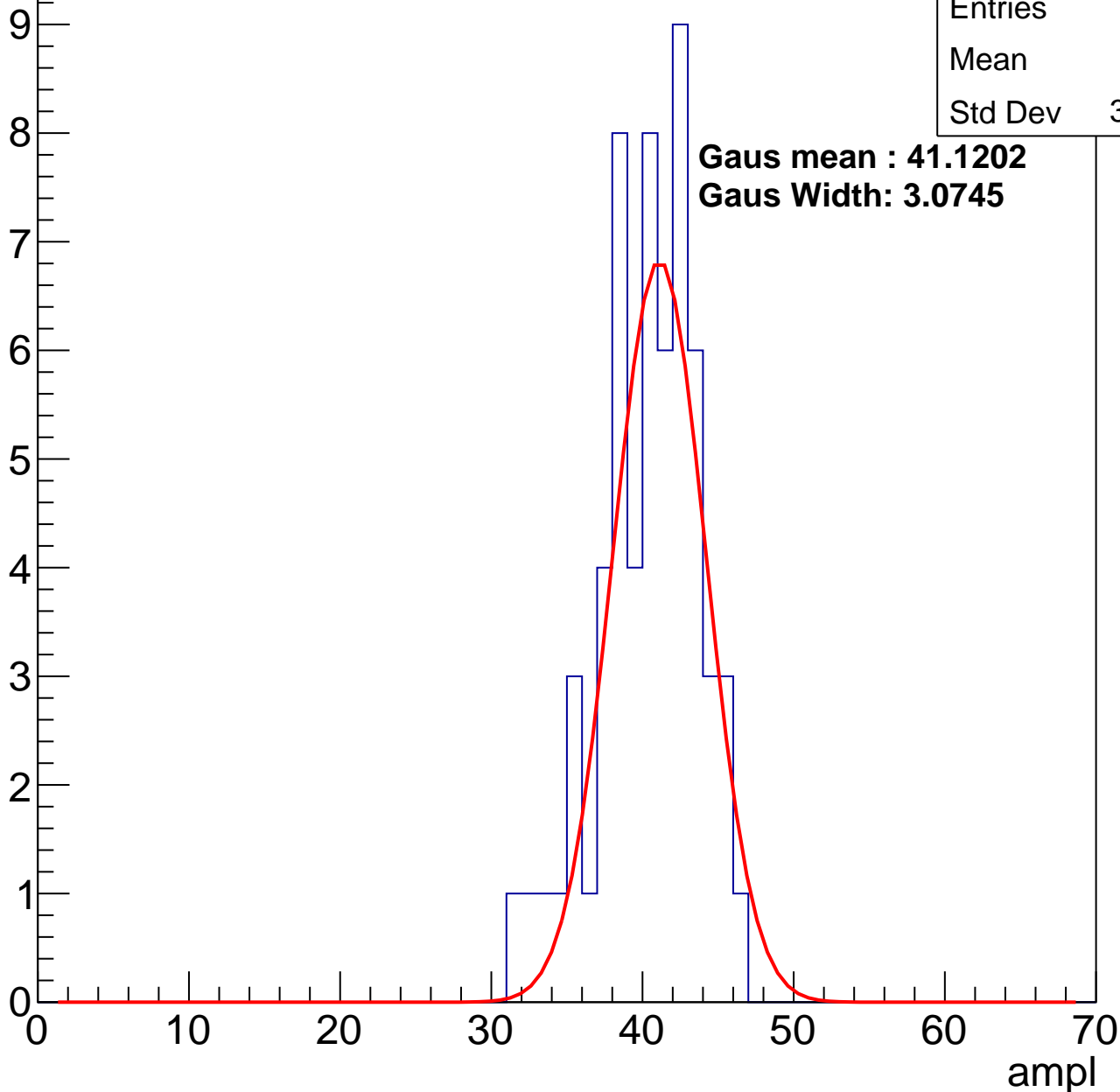
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	39.9
Std Dev	3.285

**Gaus mean : 41.1202**

**Gaus Width: 3.0745**

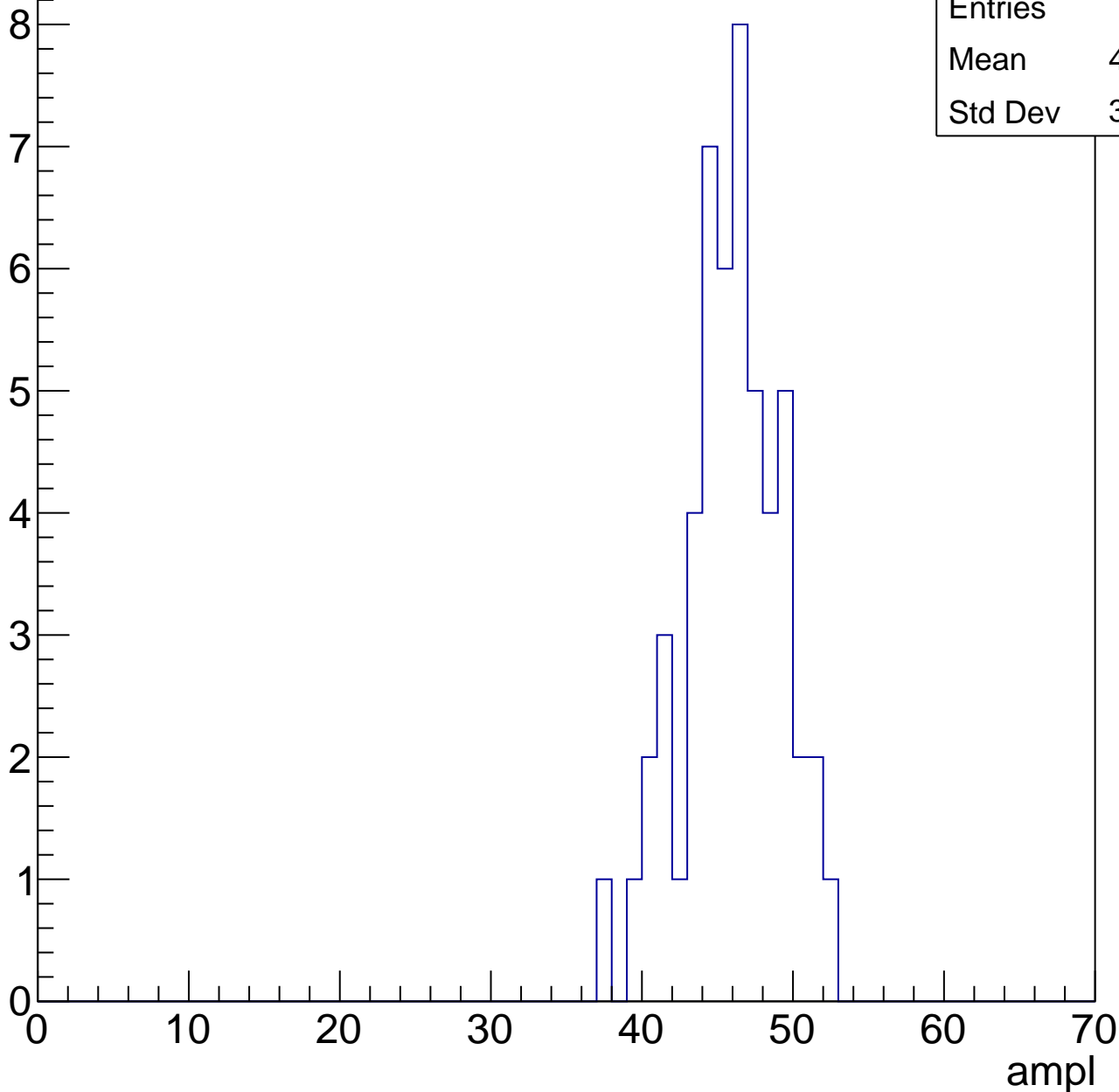


# B1L000S, U2-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	45.48
Std Dev	3.202



# B1L000S, U2-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

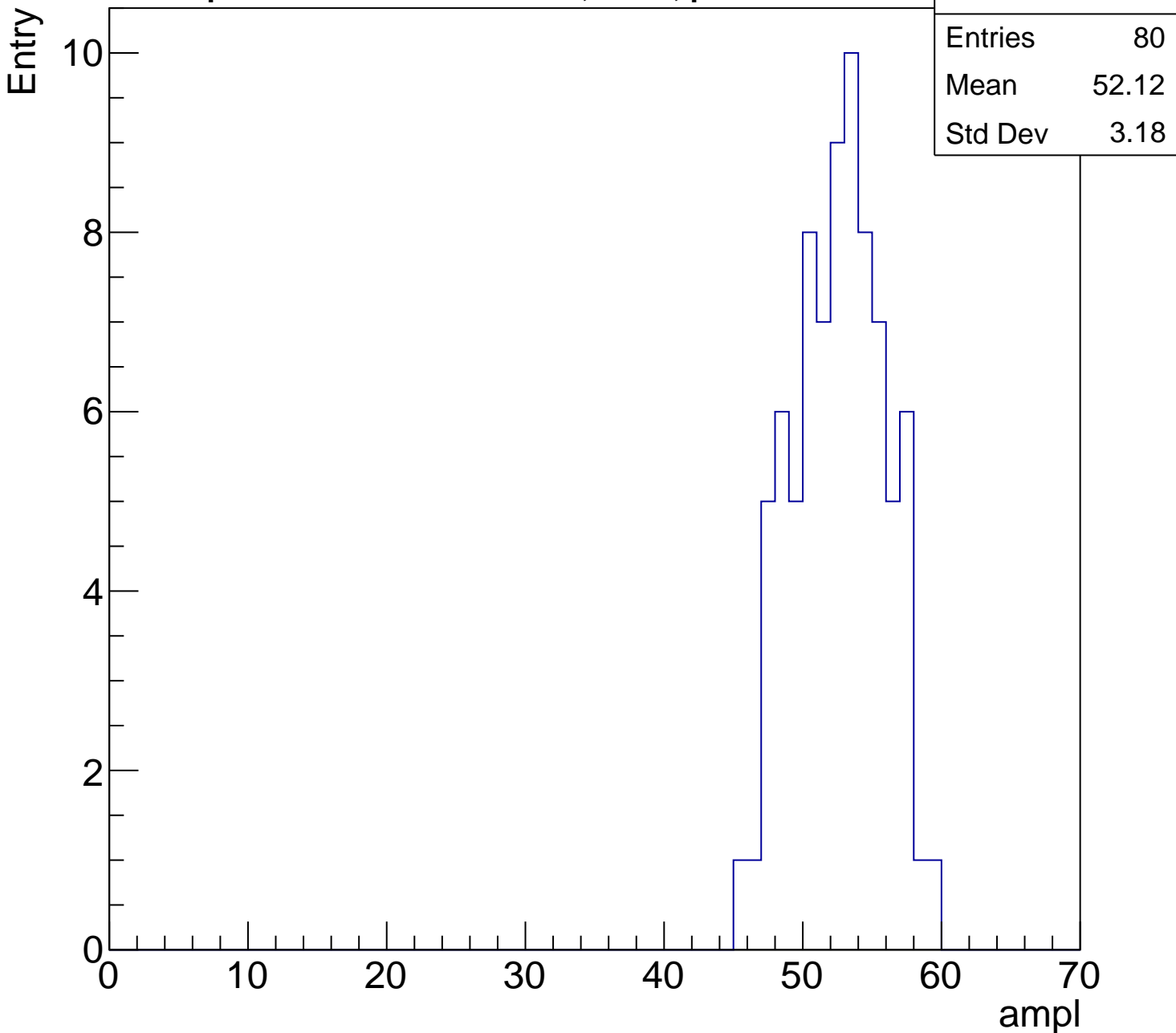
Entries	80
Mean	52.12
Std Dev	3.18

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

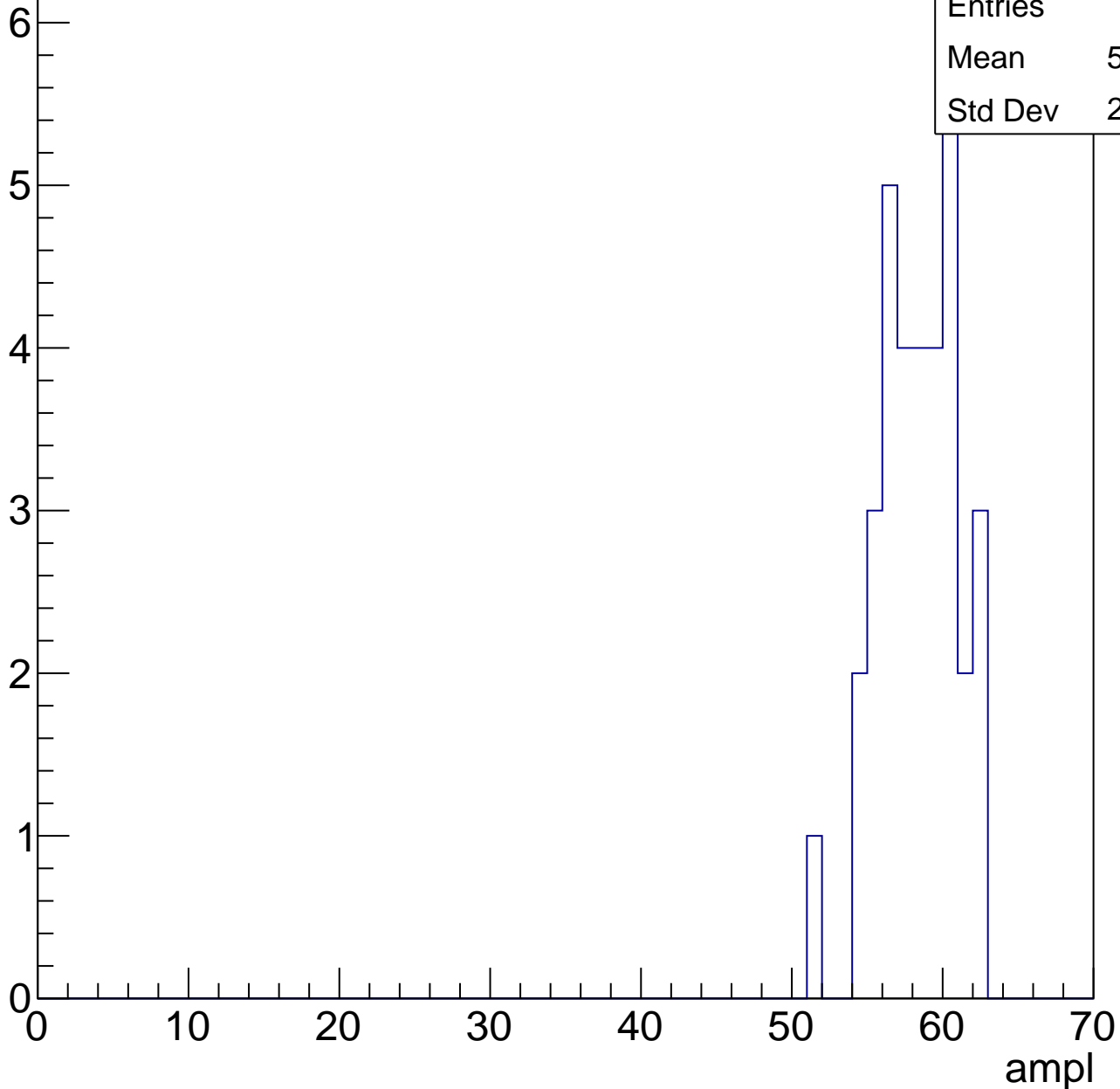


# B1L000S, U2-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

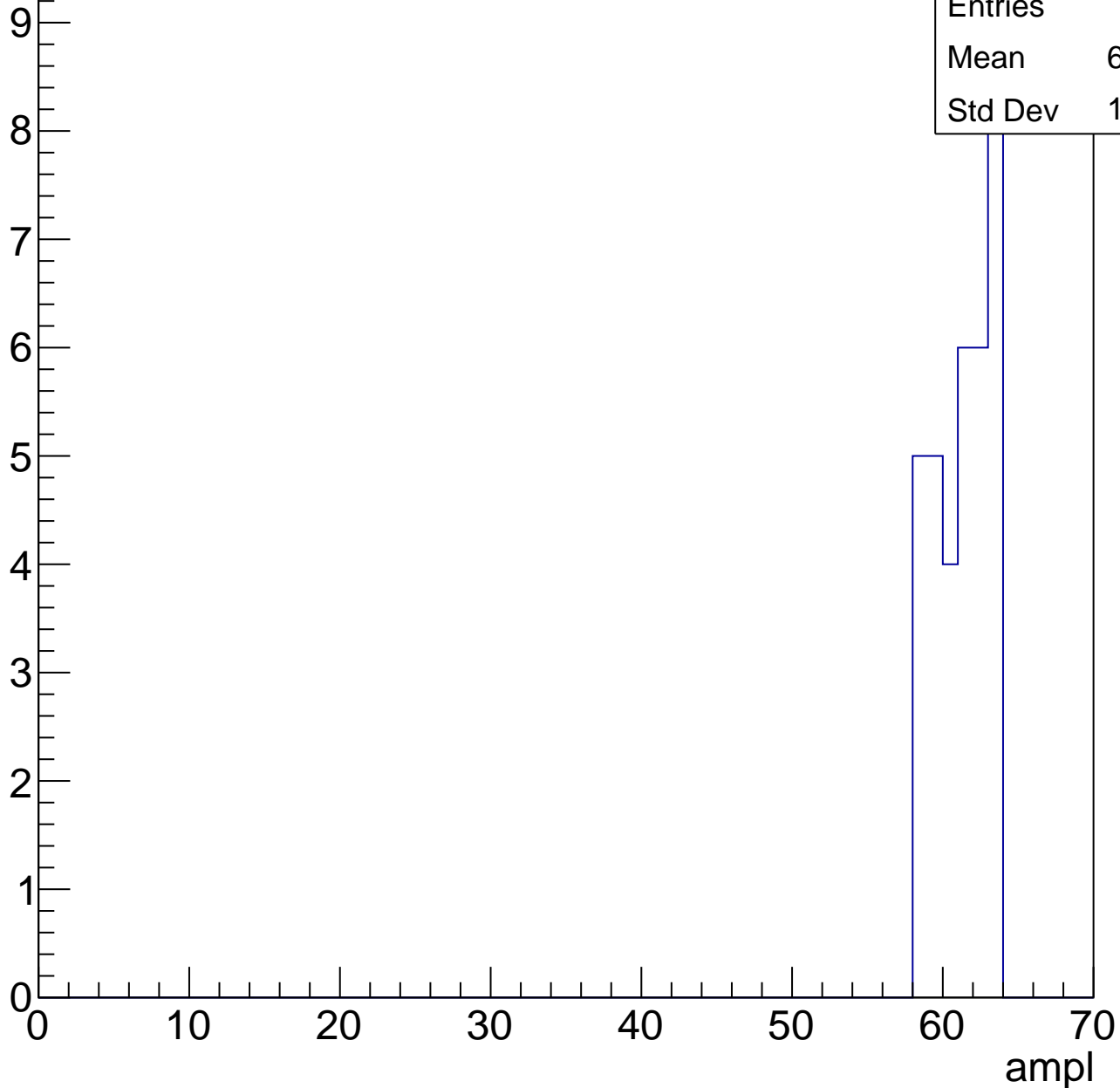
Entries	34
Mean	57.88
Std Dev	2.576



# B1L000S, U2-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



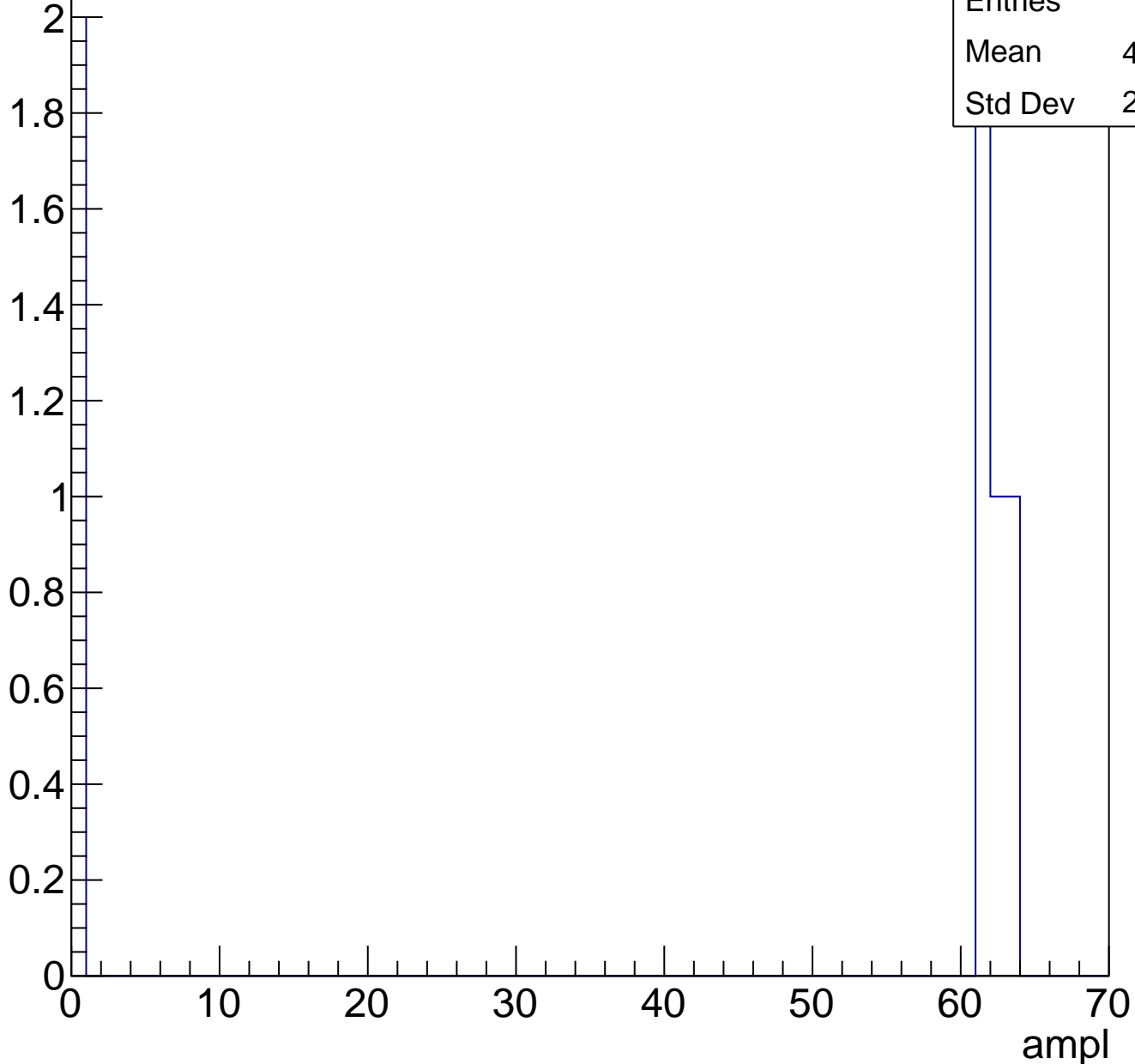
Entries	35
Mean	60.86
Std Dev	1.775



# B1L000S, U2-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch68, adc0

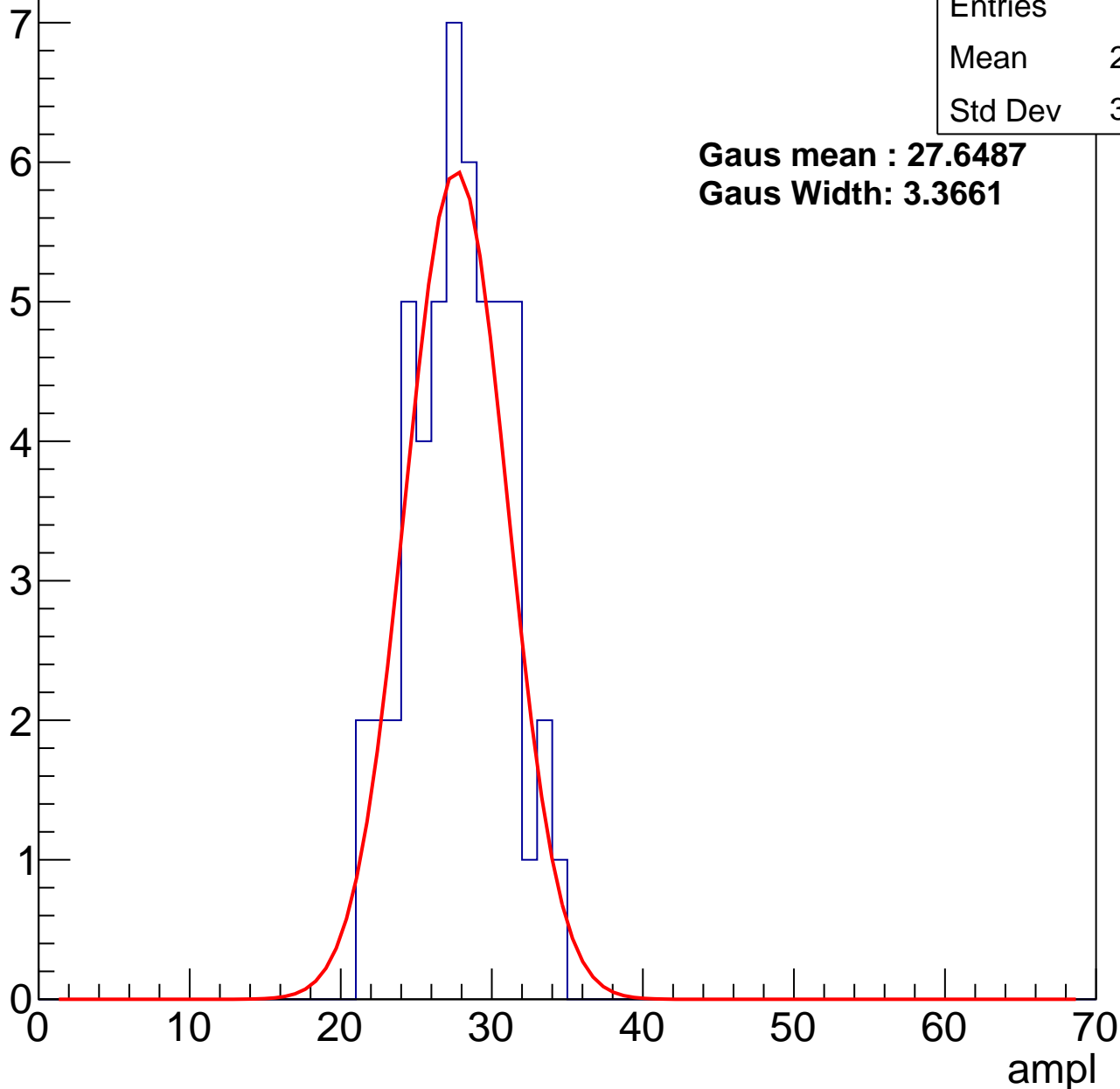
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	27.33
Std Dev	3.136

**Gaus mean : 27.6487**

**Gaus Width: 3.3661**



# B1L000S, U2-ch68, adc1

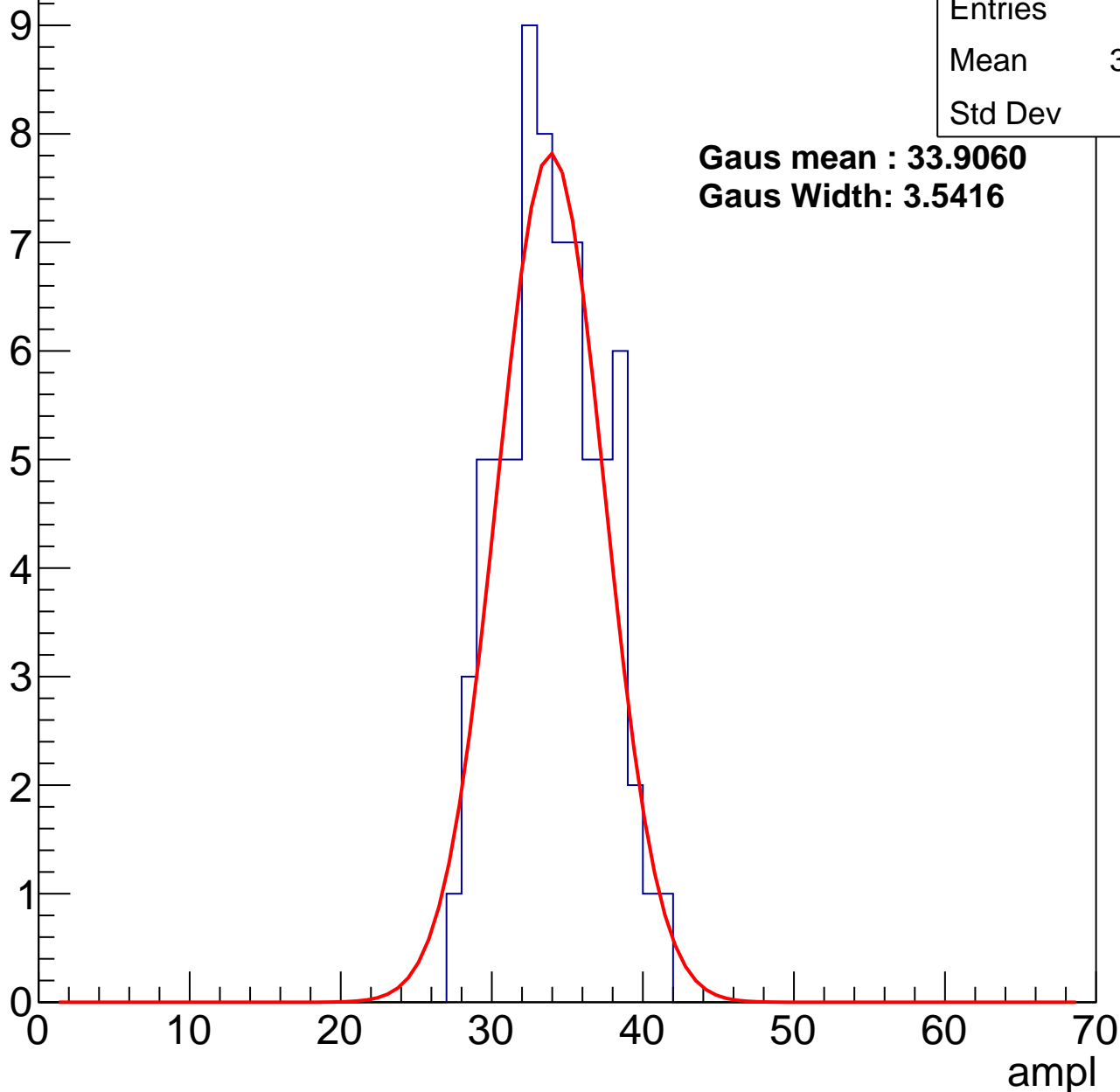
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	33.54
Std Dev	3.25

**Gaus mean : 33.9060**

**Gaus Width: 3.5416**



# B1L000S, U2-ch68, adc2

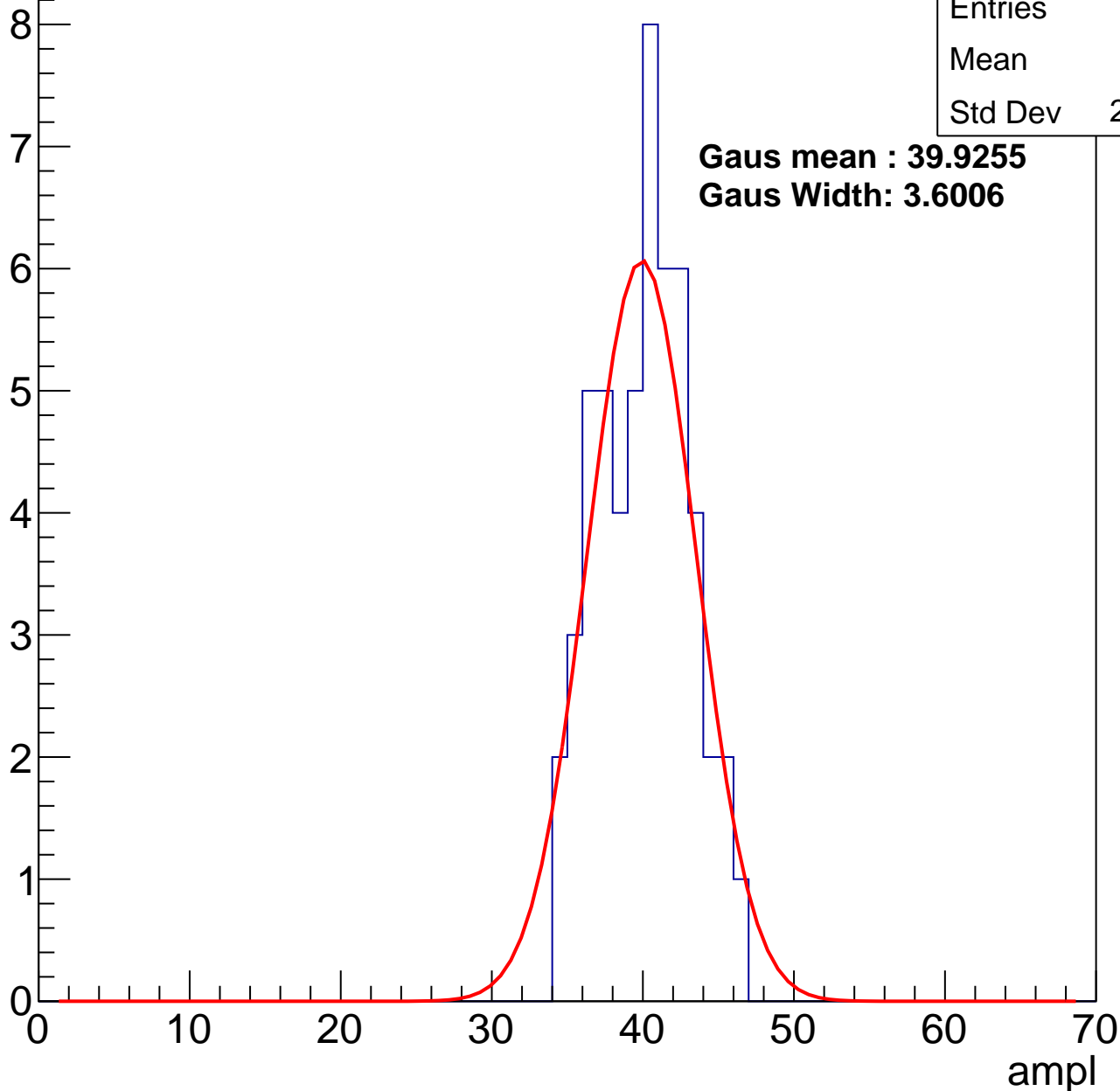
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	39.6
Std Dev	2.974

**Gaus mean : 39.9255**

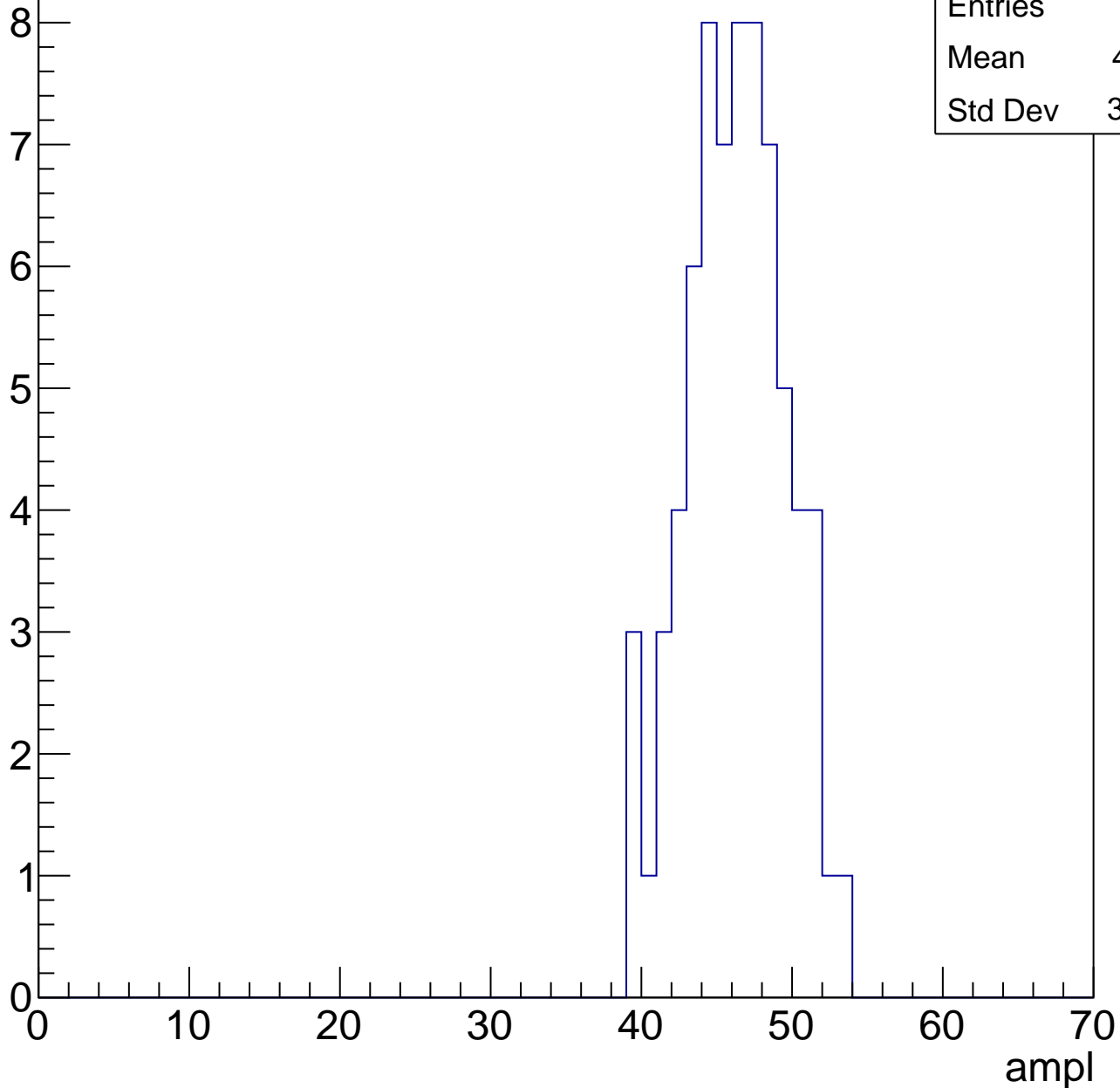
**Gaus Width: 3.6006**



# B1L000S, U2-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

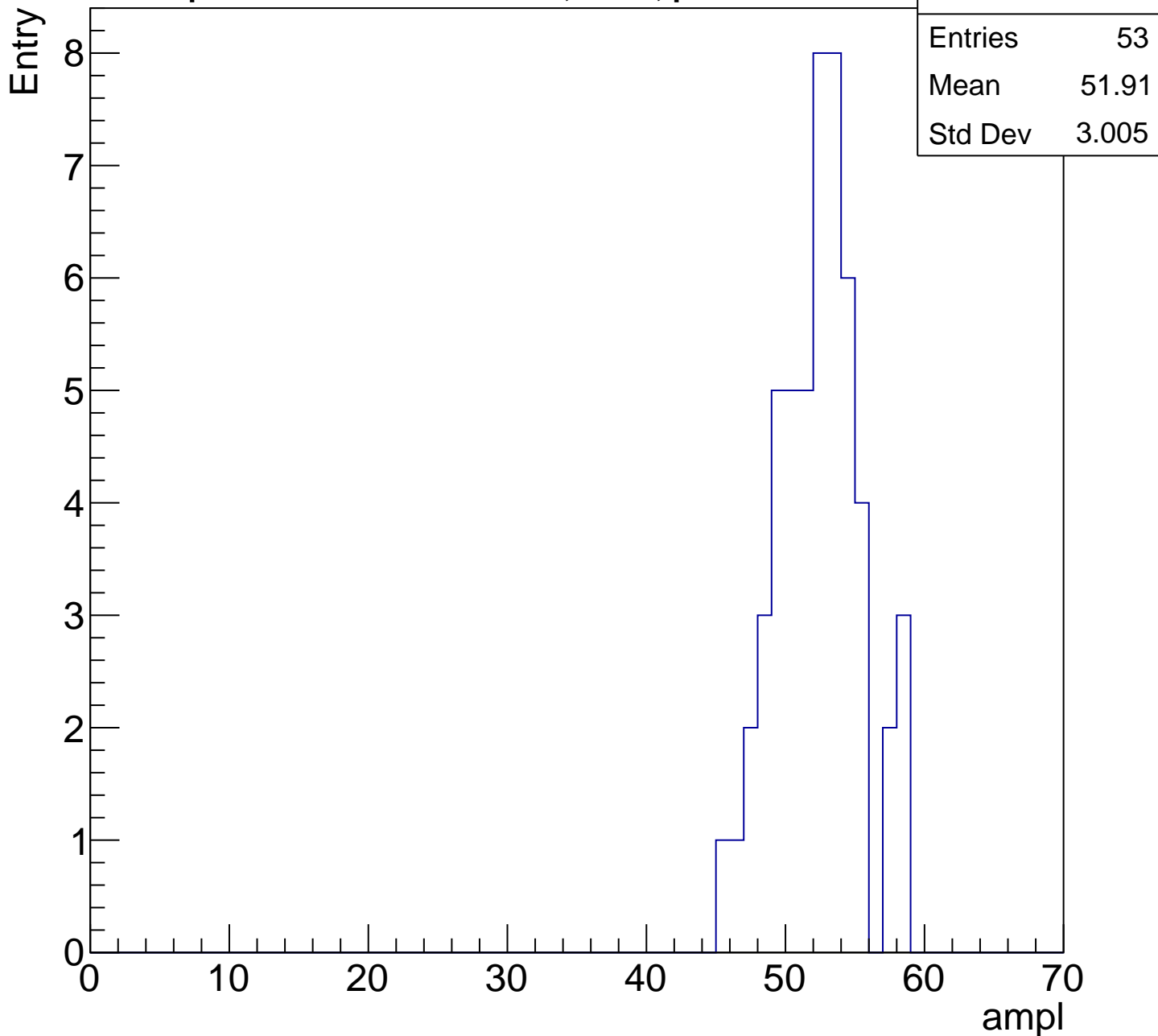
Entry



Entries	70
Mean	45.81
Std Dev	3.257

# B1L000S, U2-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

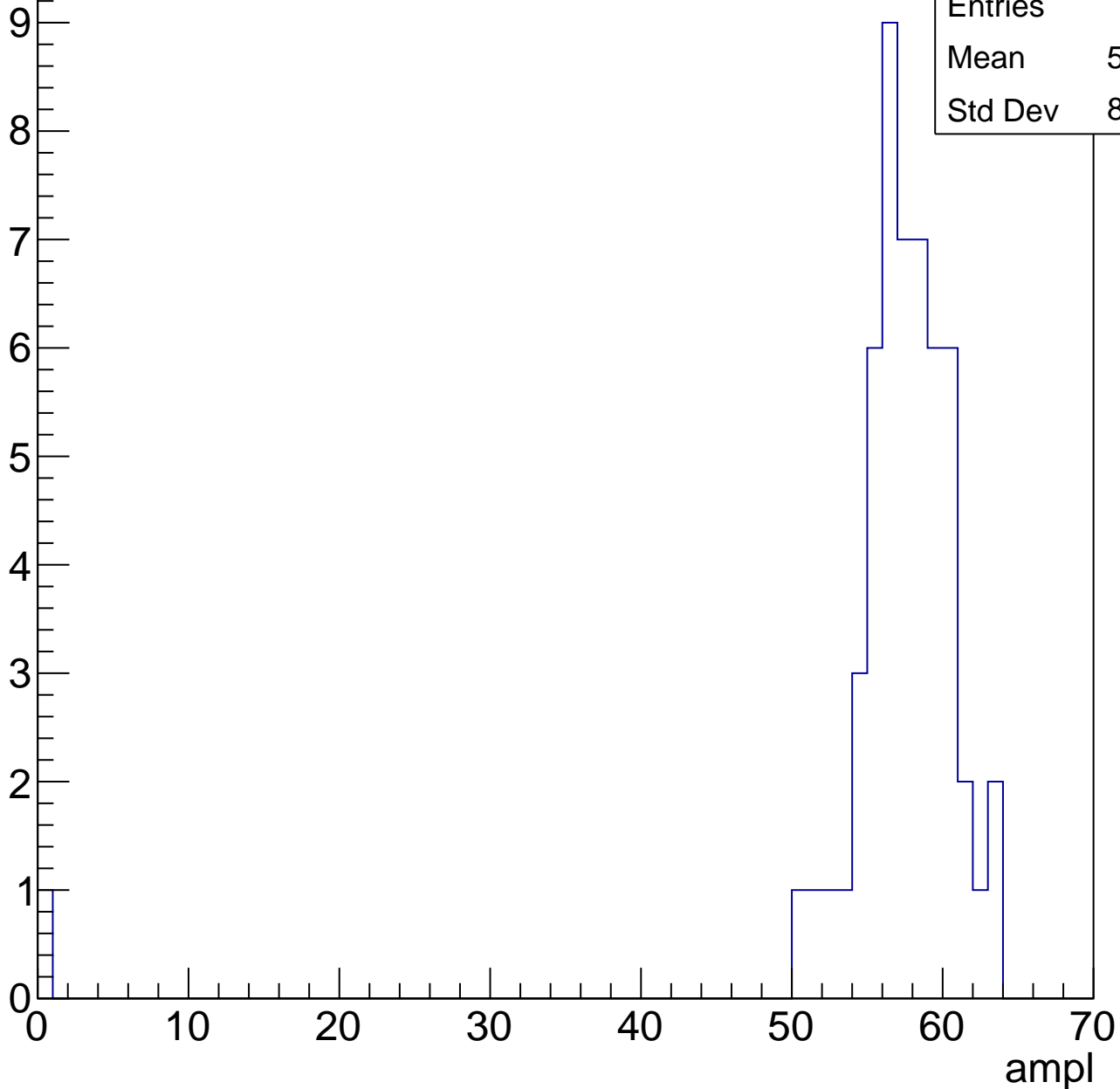


# B1L000S, U2-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	56.13
Std Dev	8.174

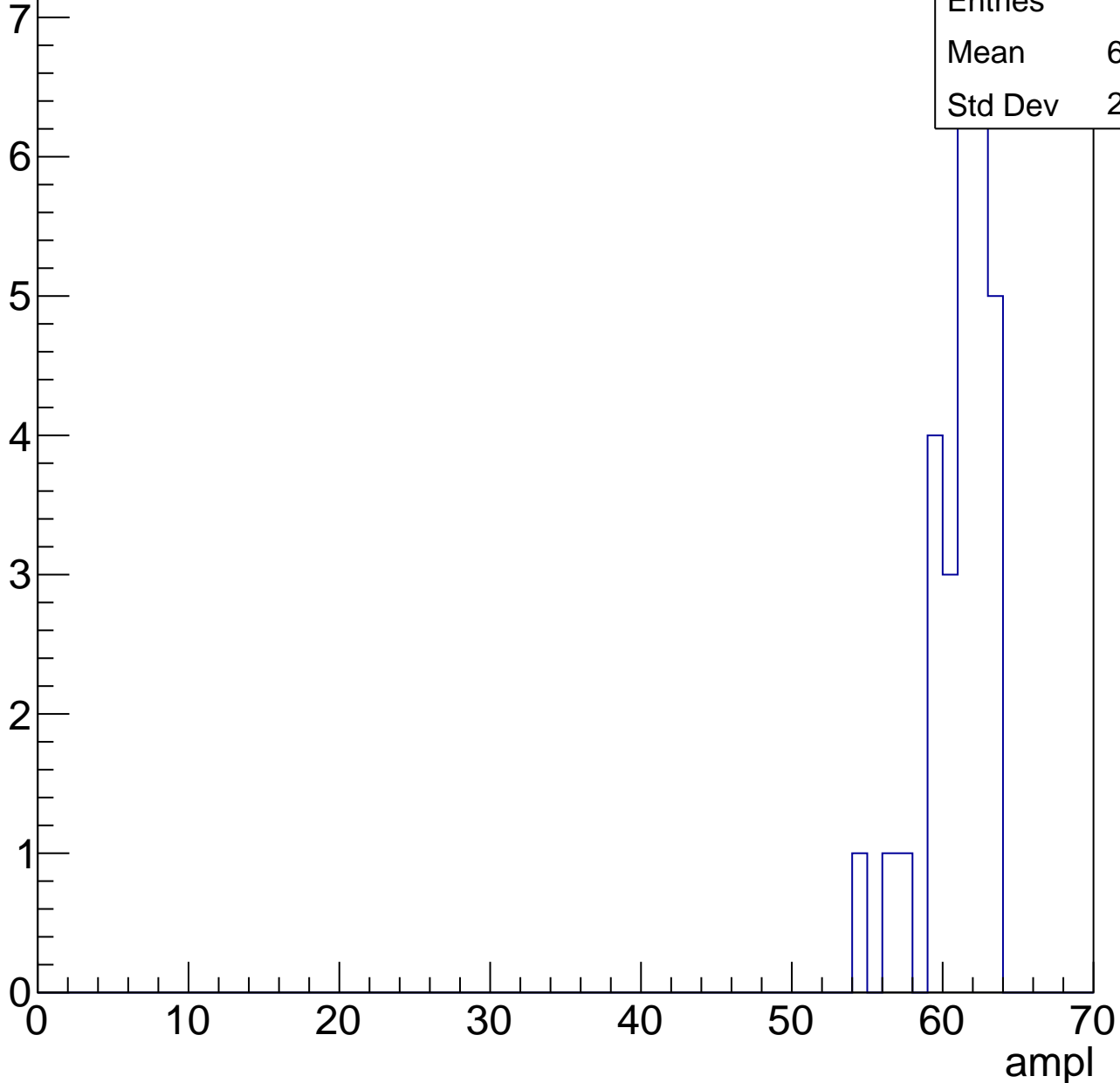


# B1L000S, U2-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	60.66
Std Dev	2.138

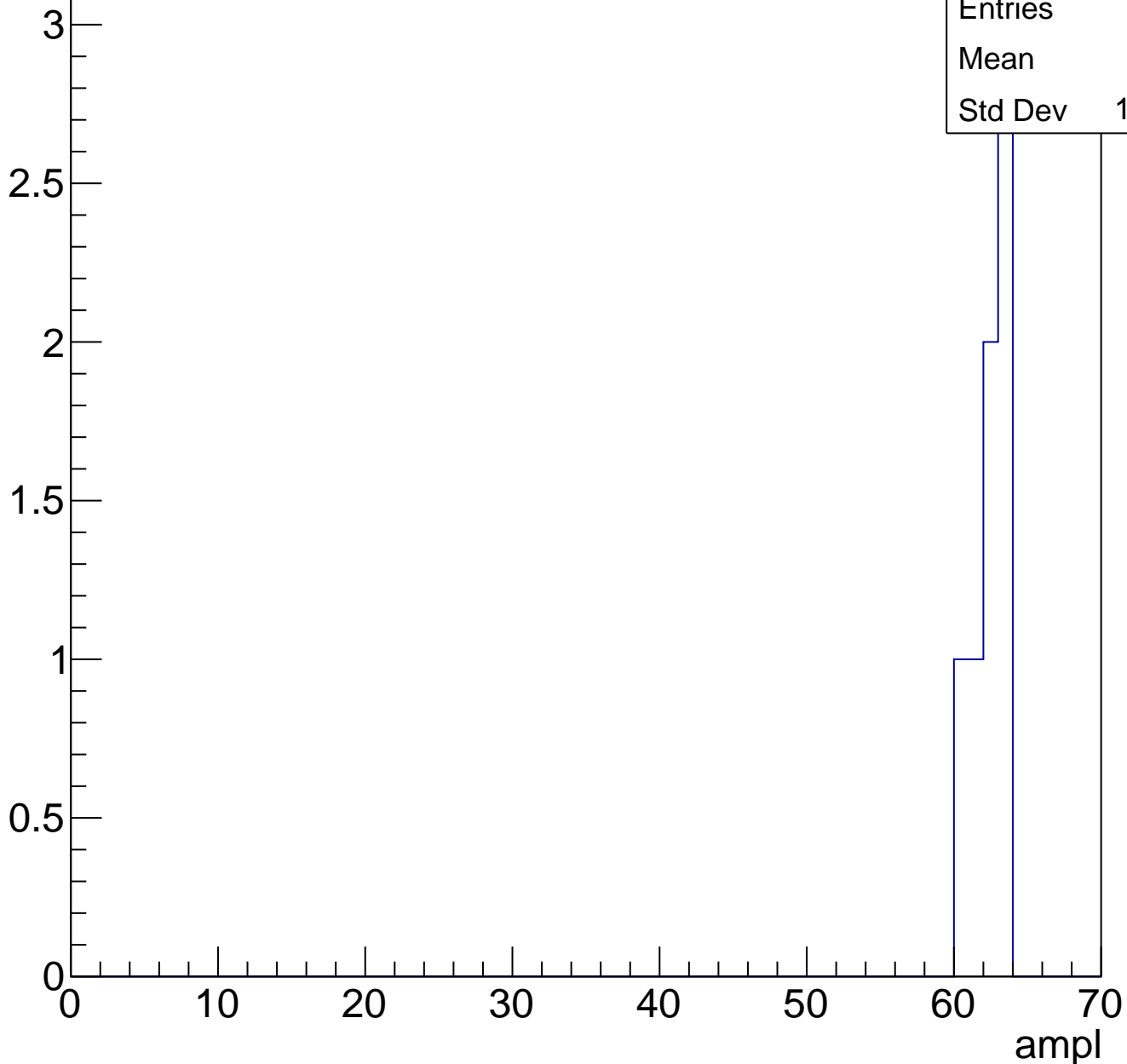




# B1L000S, U2-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch69, adc0

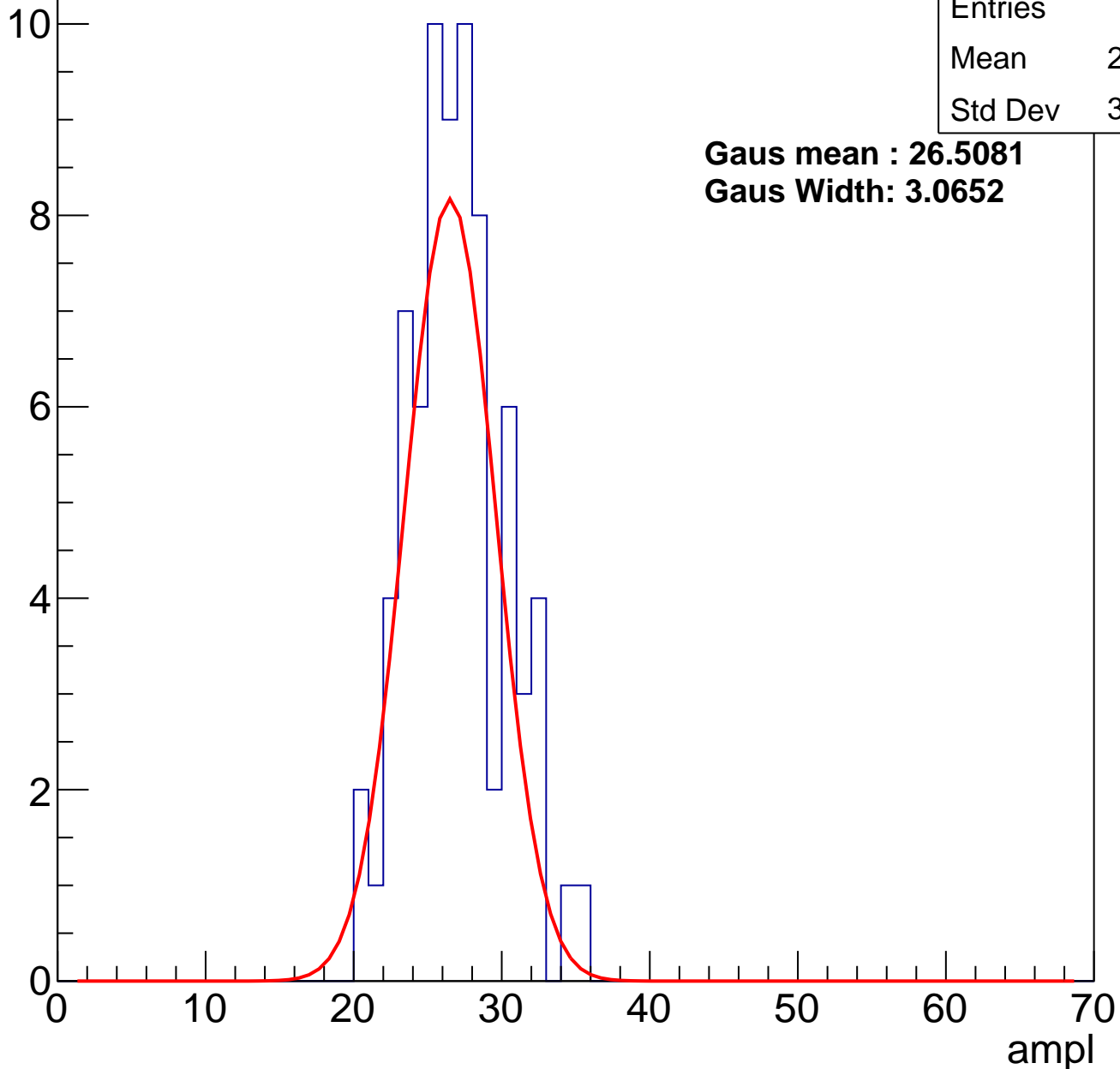
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	26.49
Std Dev	3.206

**Gaus mean : 26.5081**

**Gaus Width: 3.0652**

Entry



# B1L000S, U2-ch69, adc1

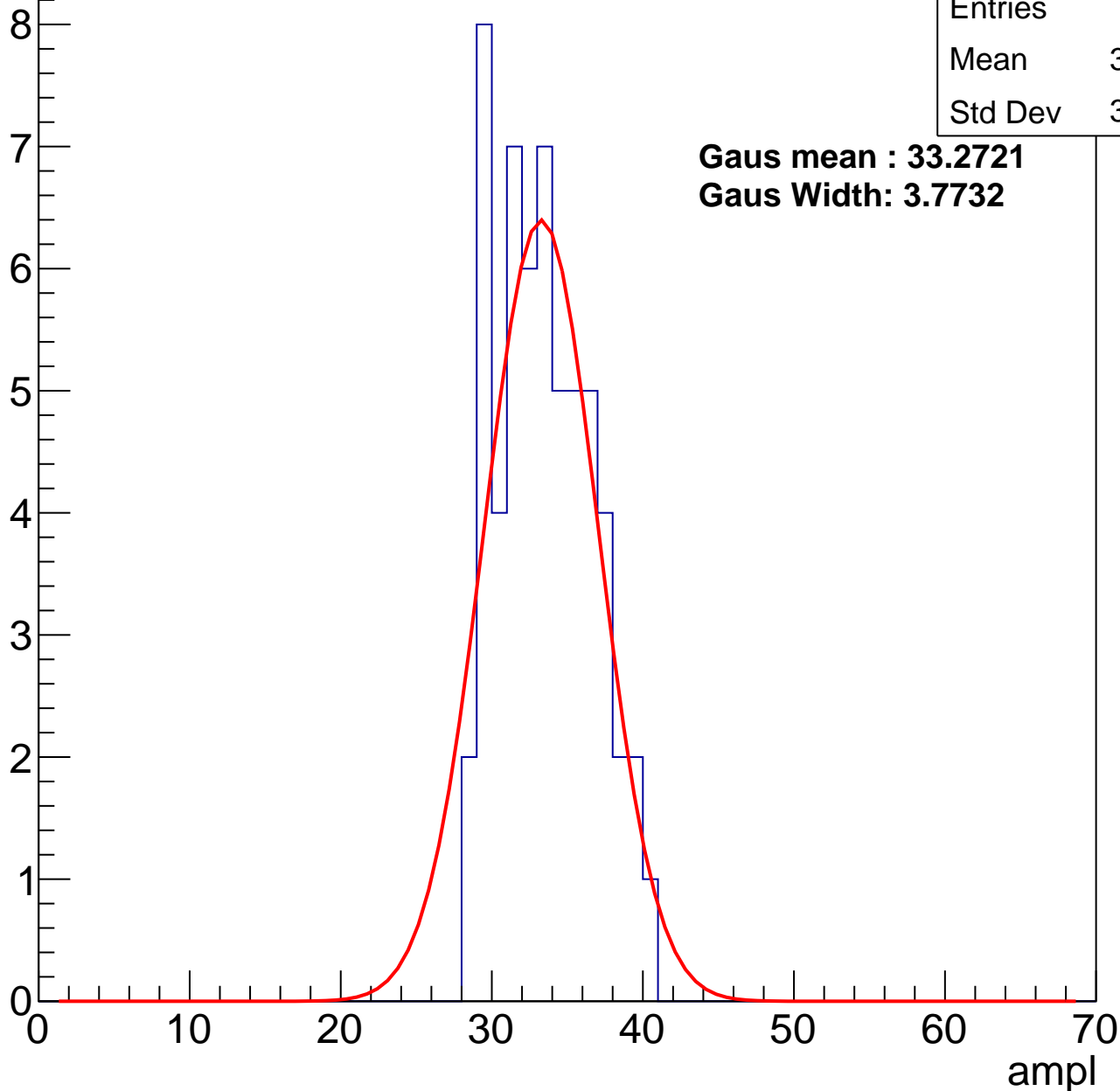
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	33.02
Std Dev	3.088

**Gaus mean : 33.2721**

**Gaus Width: 3.7732**



# B1L000S, U2-ch69, adc2

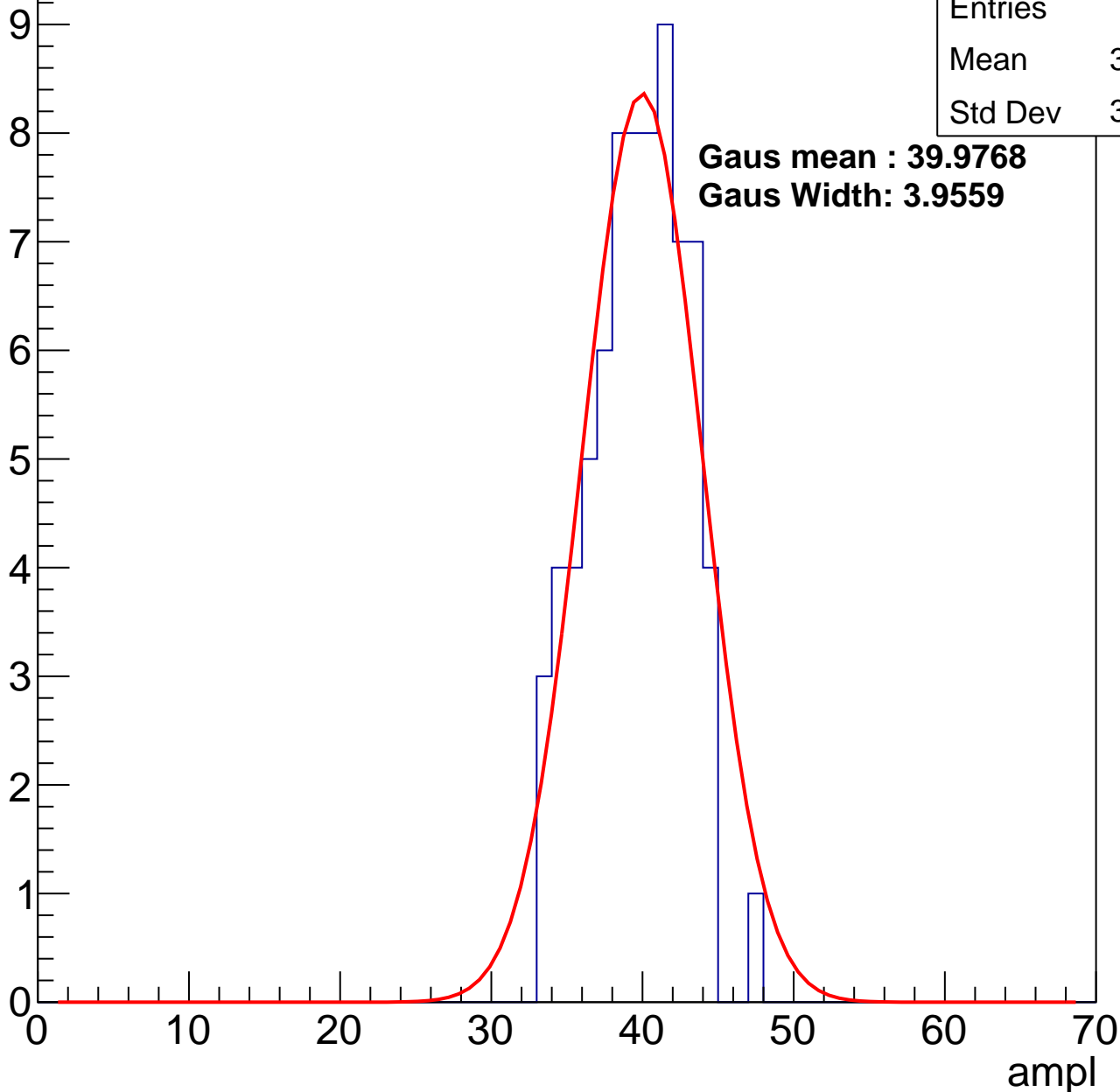
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	39.19
Std Dev	3.139

**Gaus mean : 39.9768**

**Gaus Width: 3.9559**

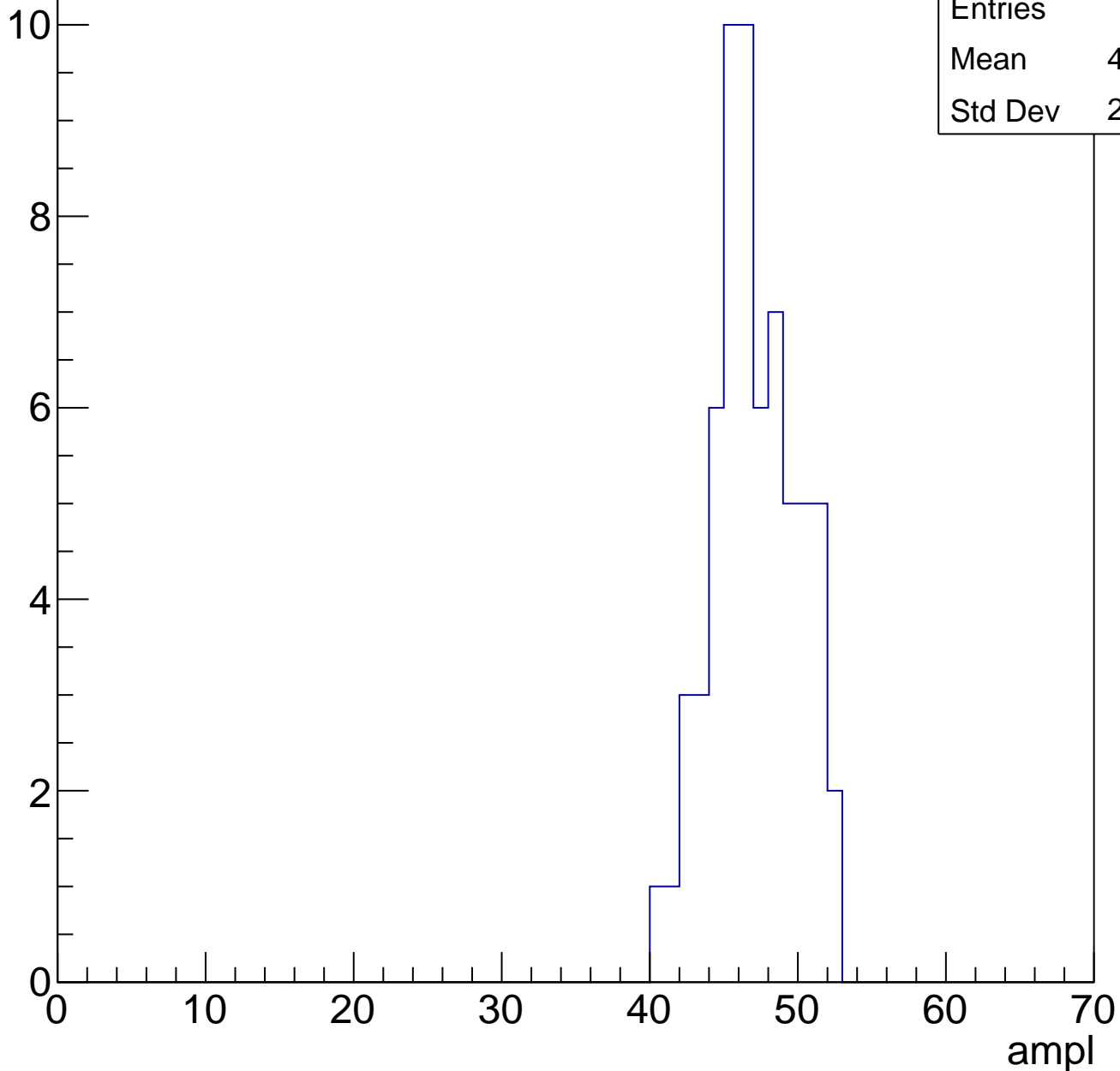


# B1L000S, U2-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	46.59
Std Dev	2.805

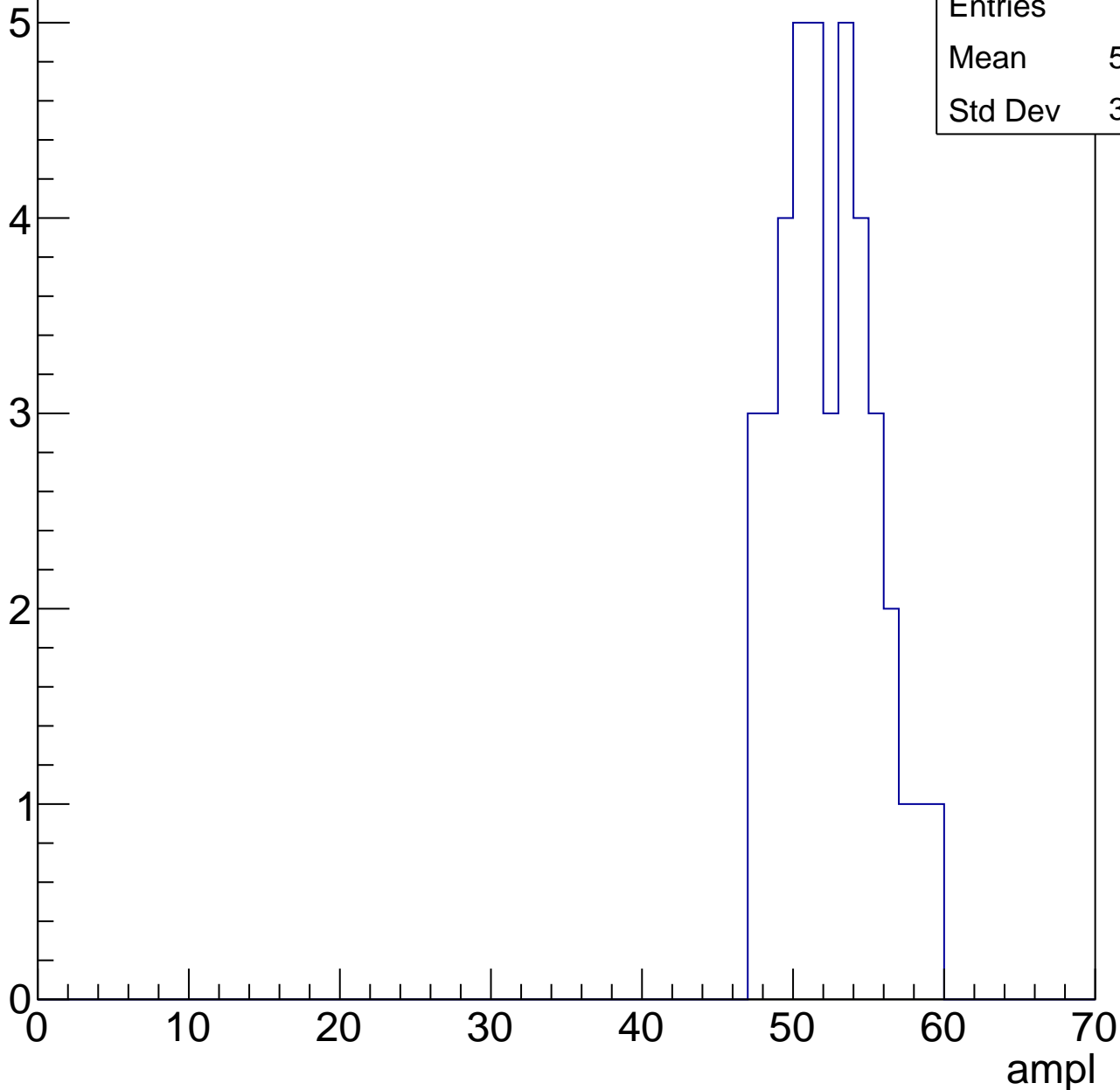


# B1L000S, U2-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

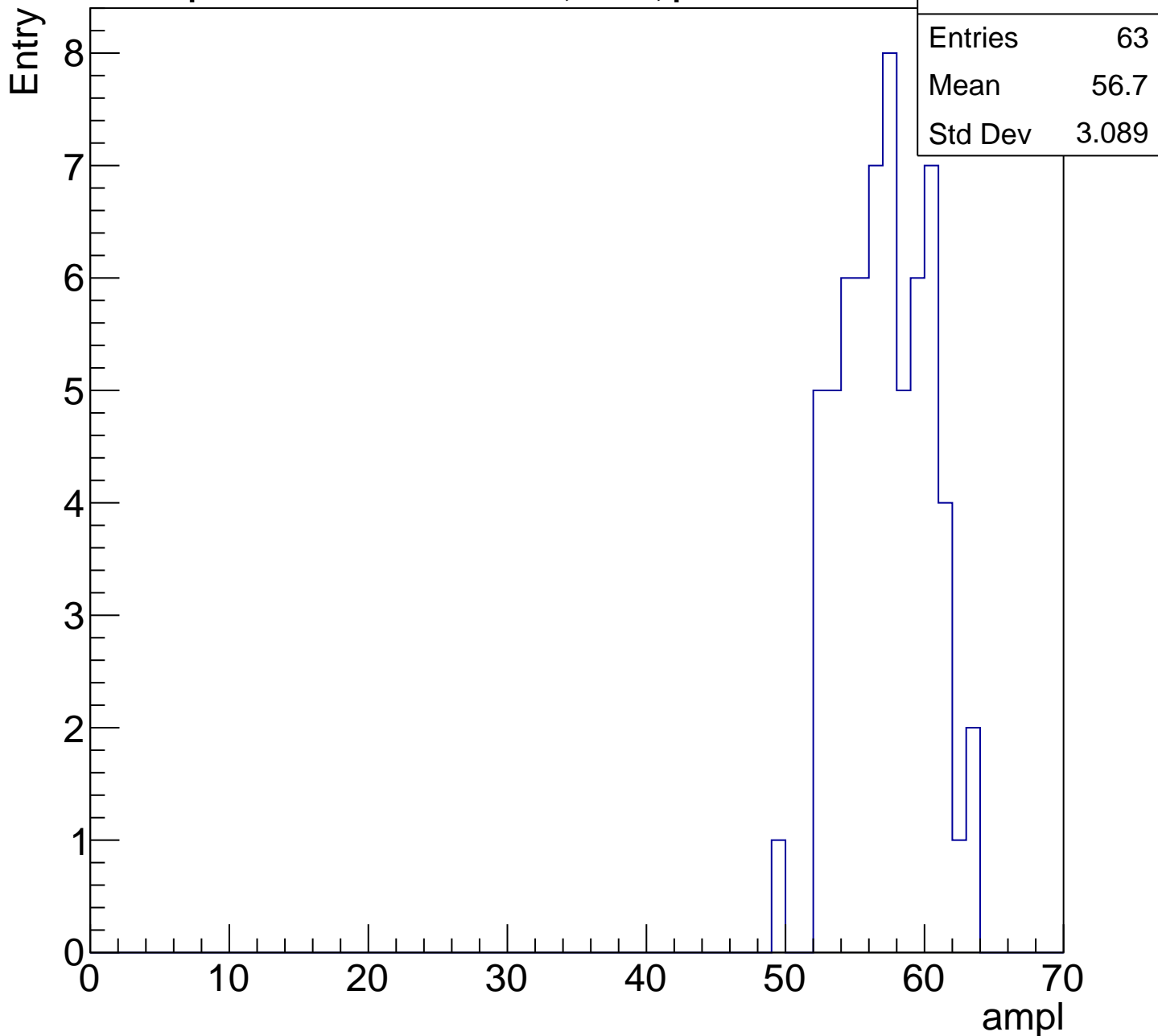
Entry

Entries	40
Mean	51.85
Std Dev	3.054



# B1L000S, U2-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

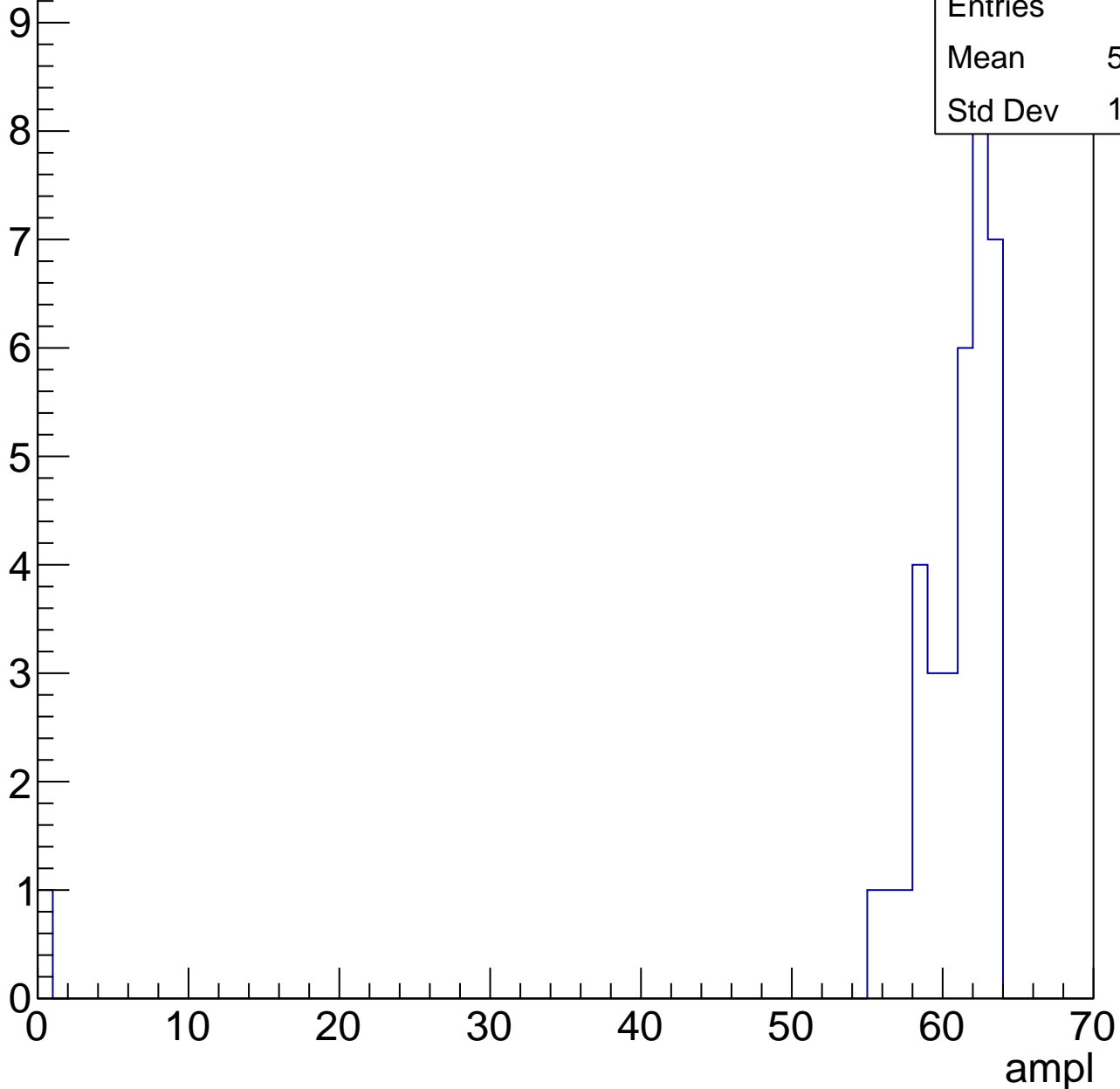


# B1L000S, U2-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	36
Mean	58.94
Std Dev	10.18

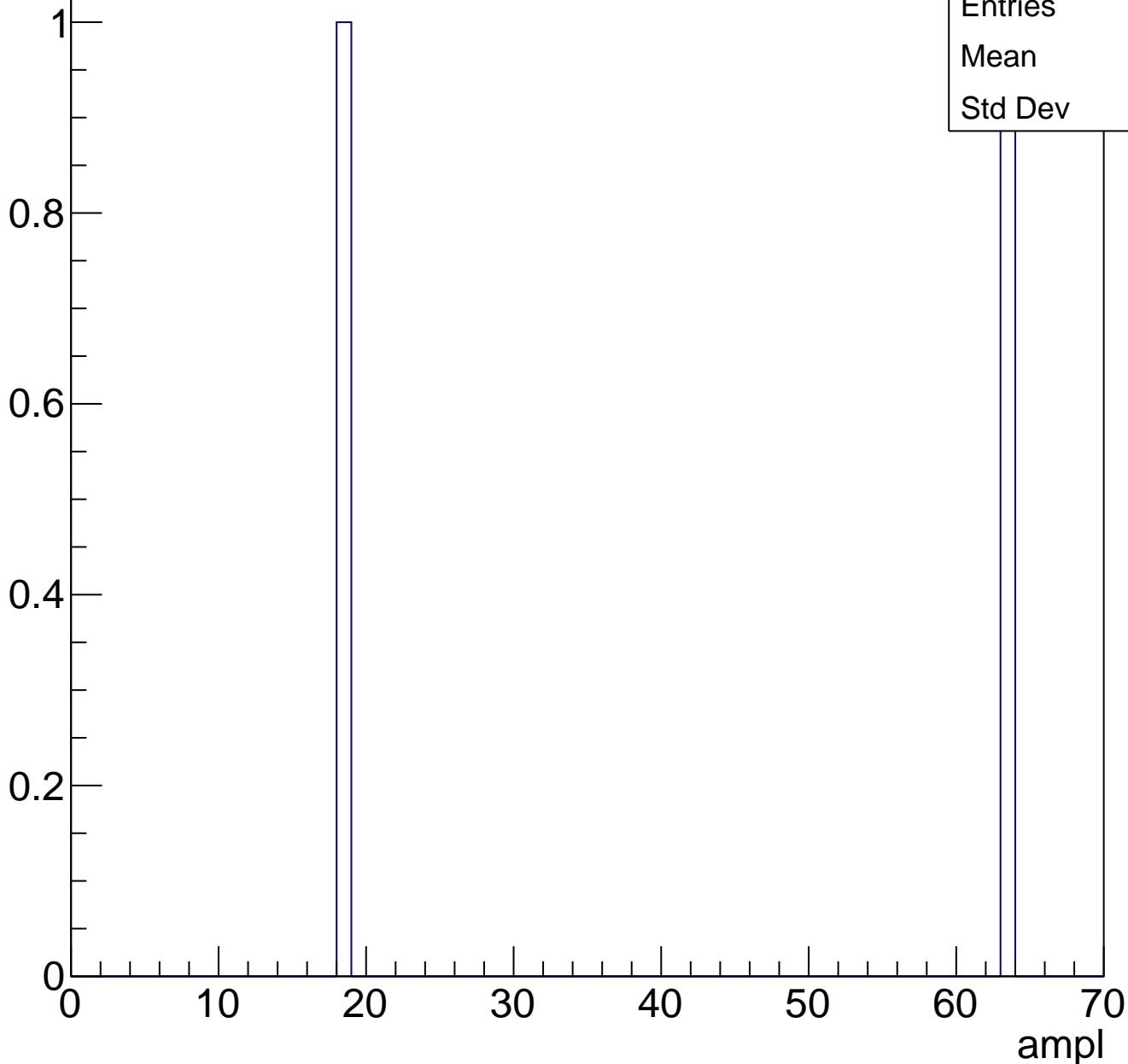




# B1L000S, U2-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch70, adc0

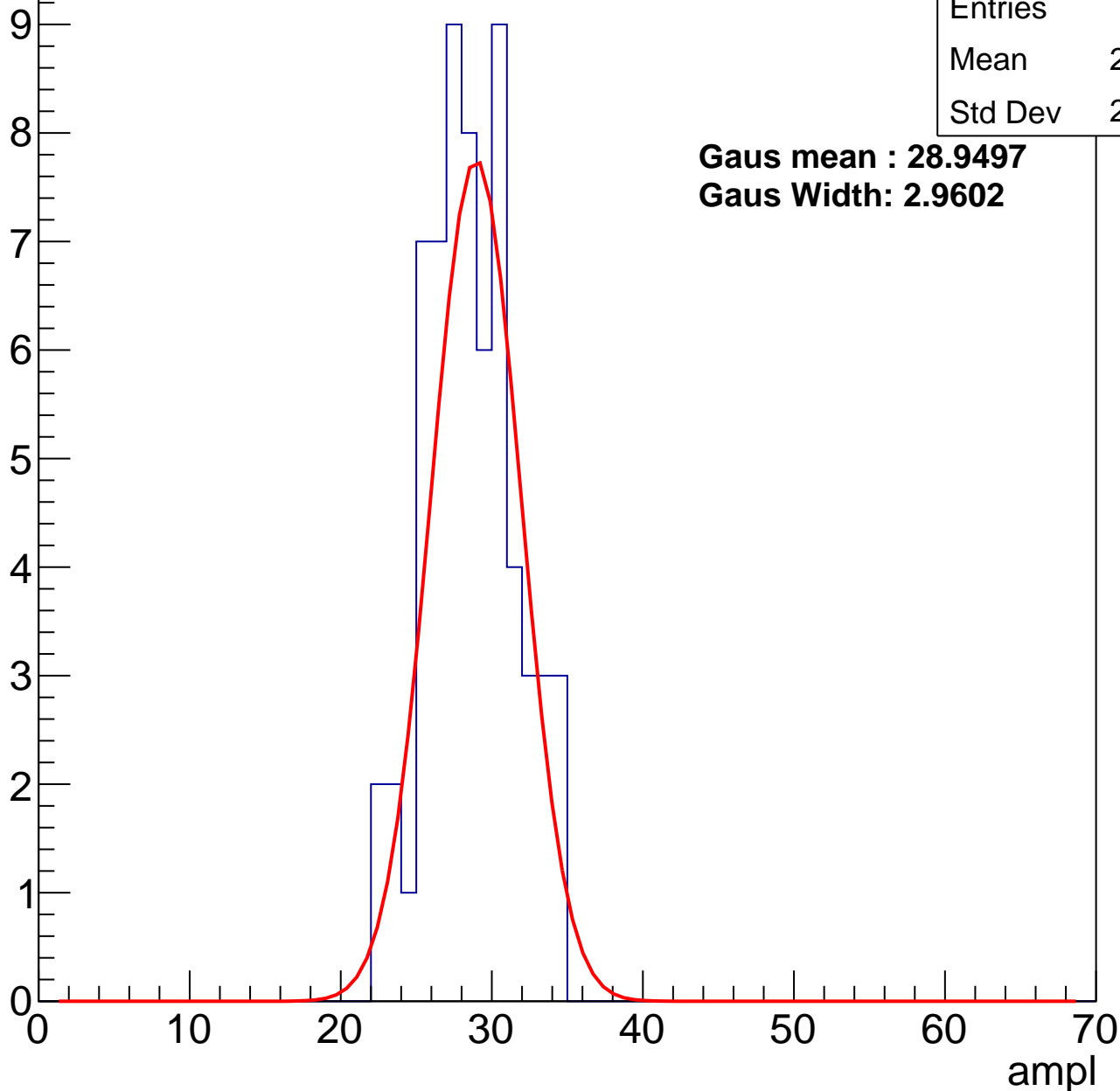
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.17
Std Dev	2.918

**Gaus mean : 28.9497**

**Gaus Width: 2.9602**



# B1L000S, U2-ch70, adc1

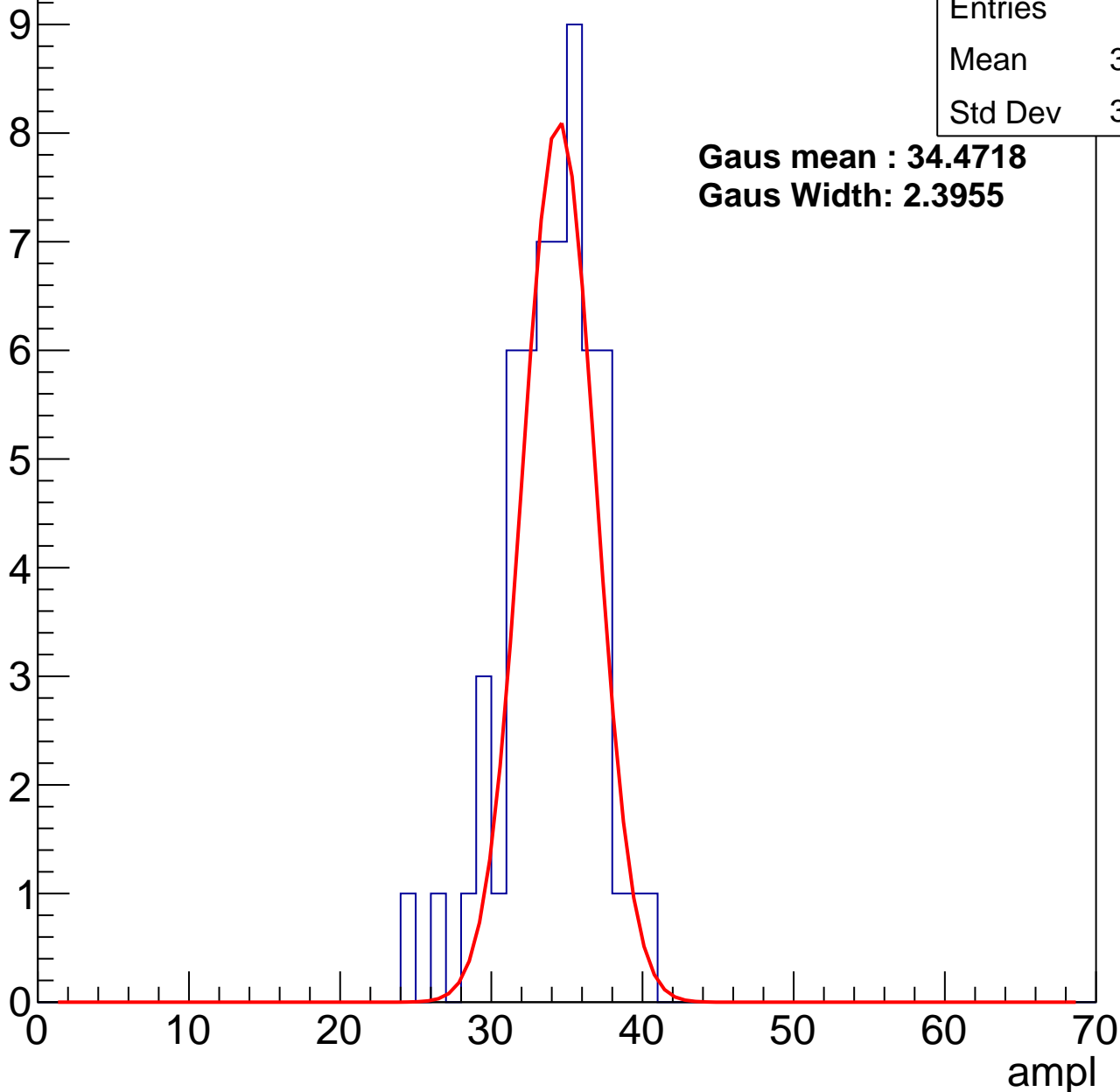
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	33.54
Std Dev	3.044

**Gaus mean : 34.4718**

**Gaus Width: 2.3955**



# B1L000S, U2-ch70, adc2

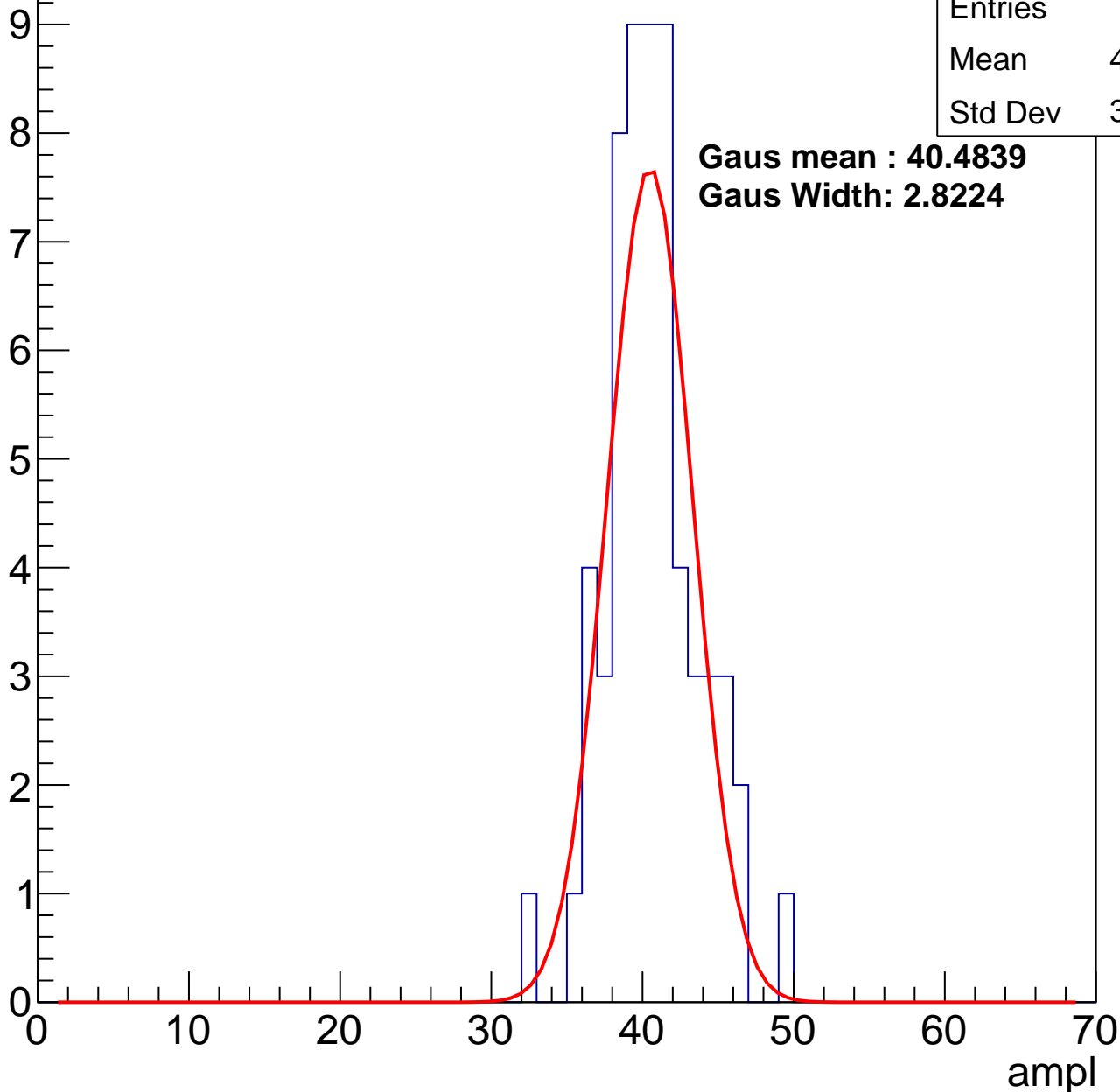
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	40.18
Std Dev	3.019

**Gaus mean : 40.4839**

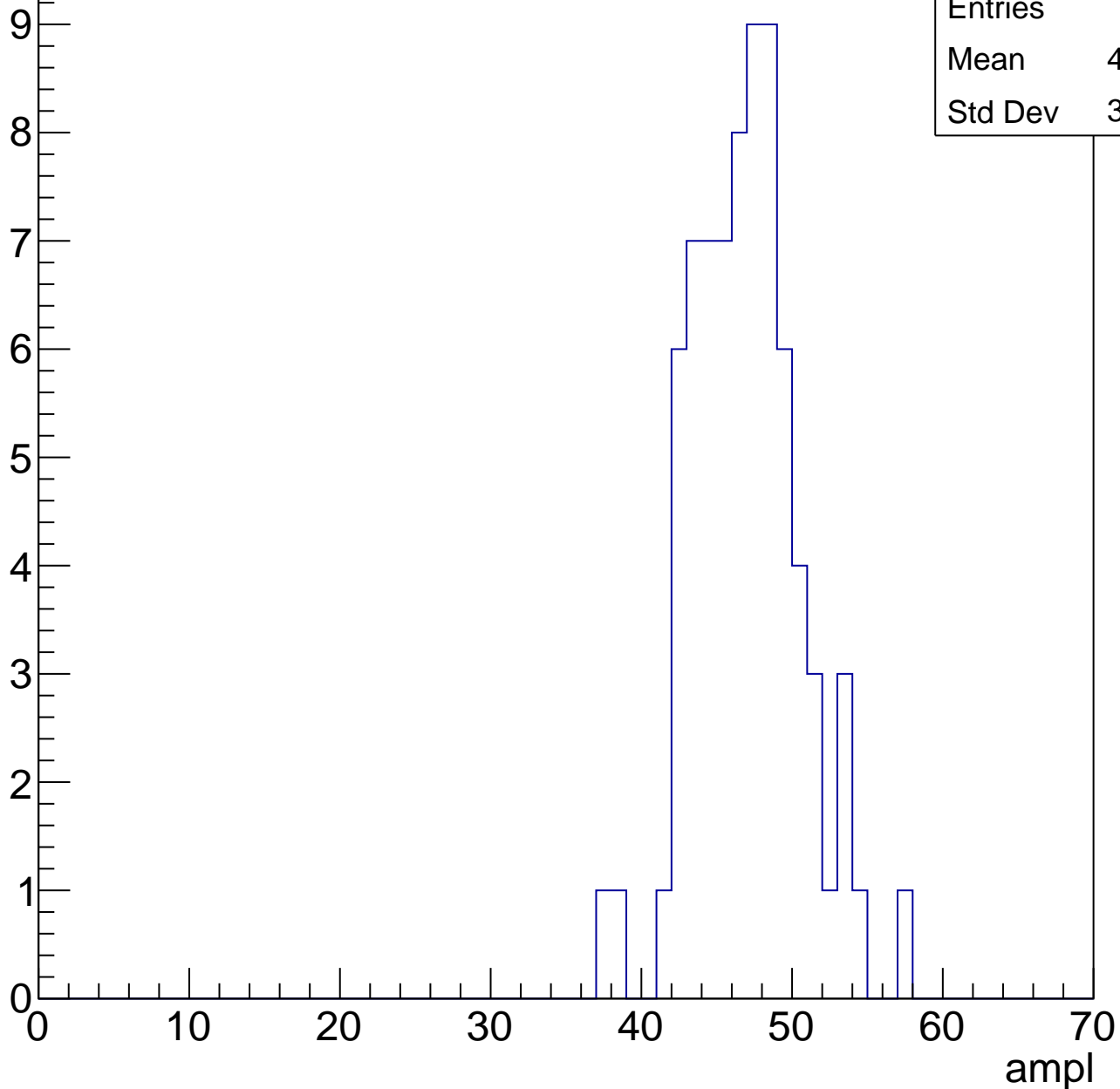
**Gaus Width: 2.8224**



# B1L000S, U2-ch70, adc3

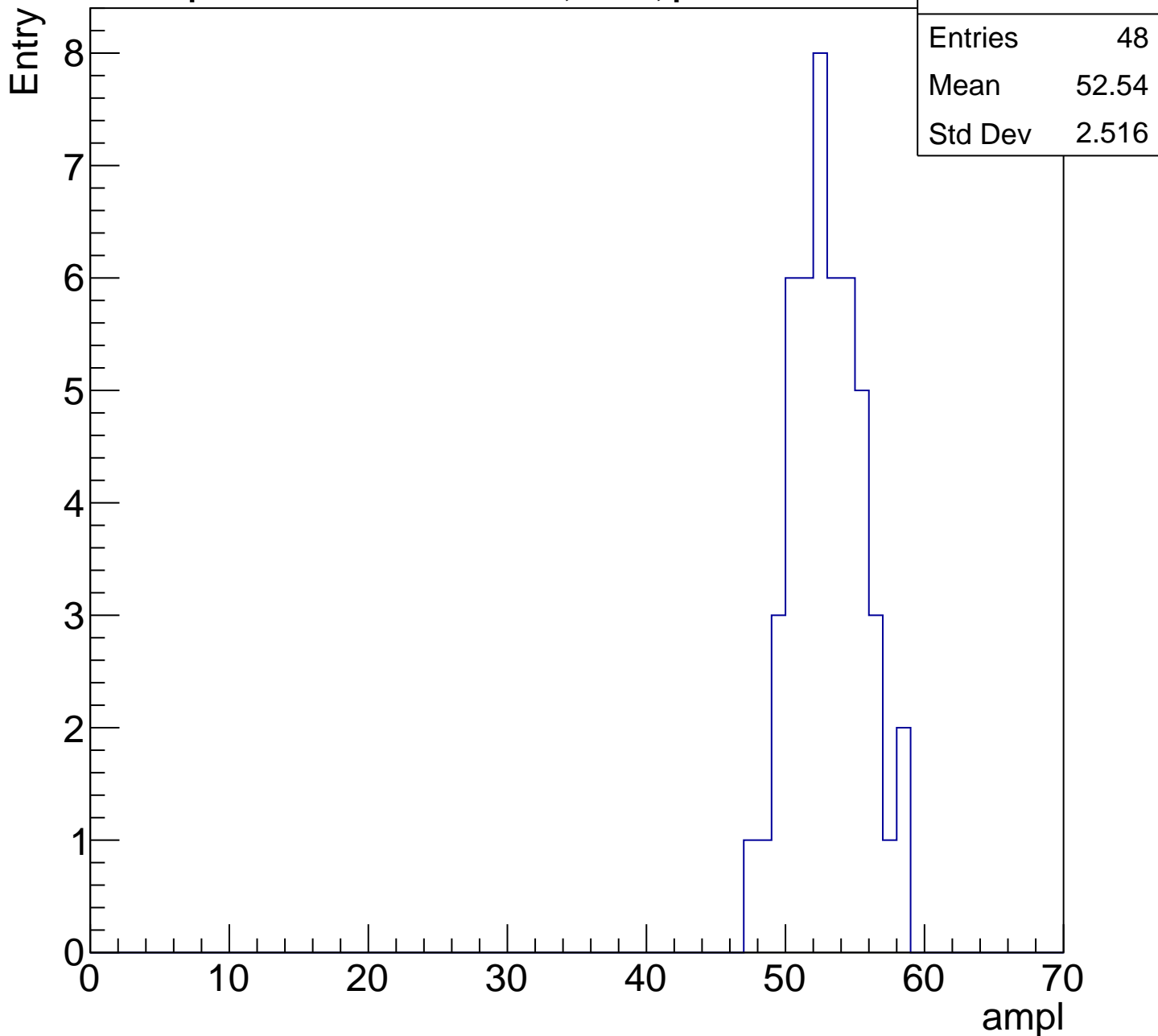
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



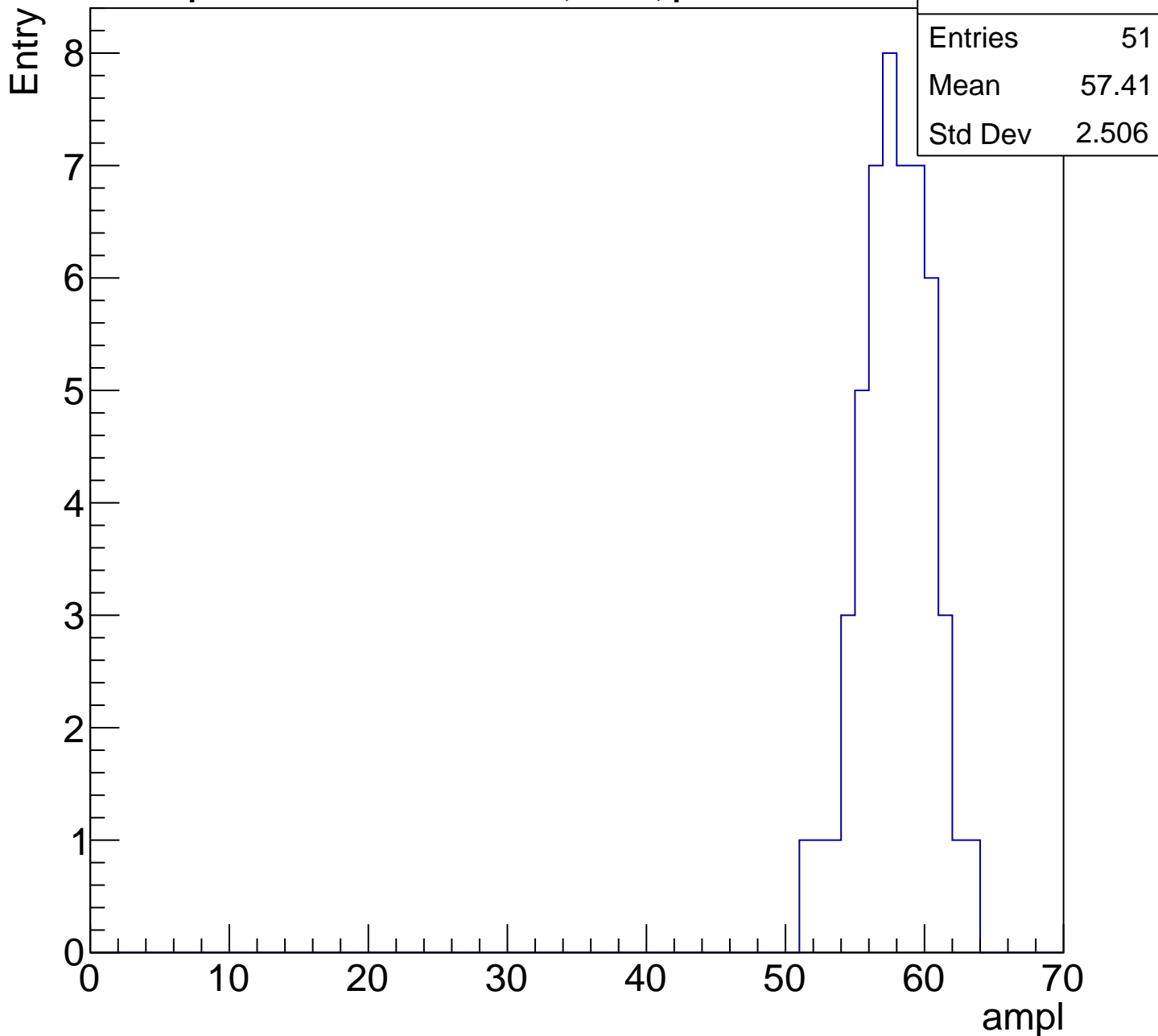
# B1L000S, U2-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

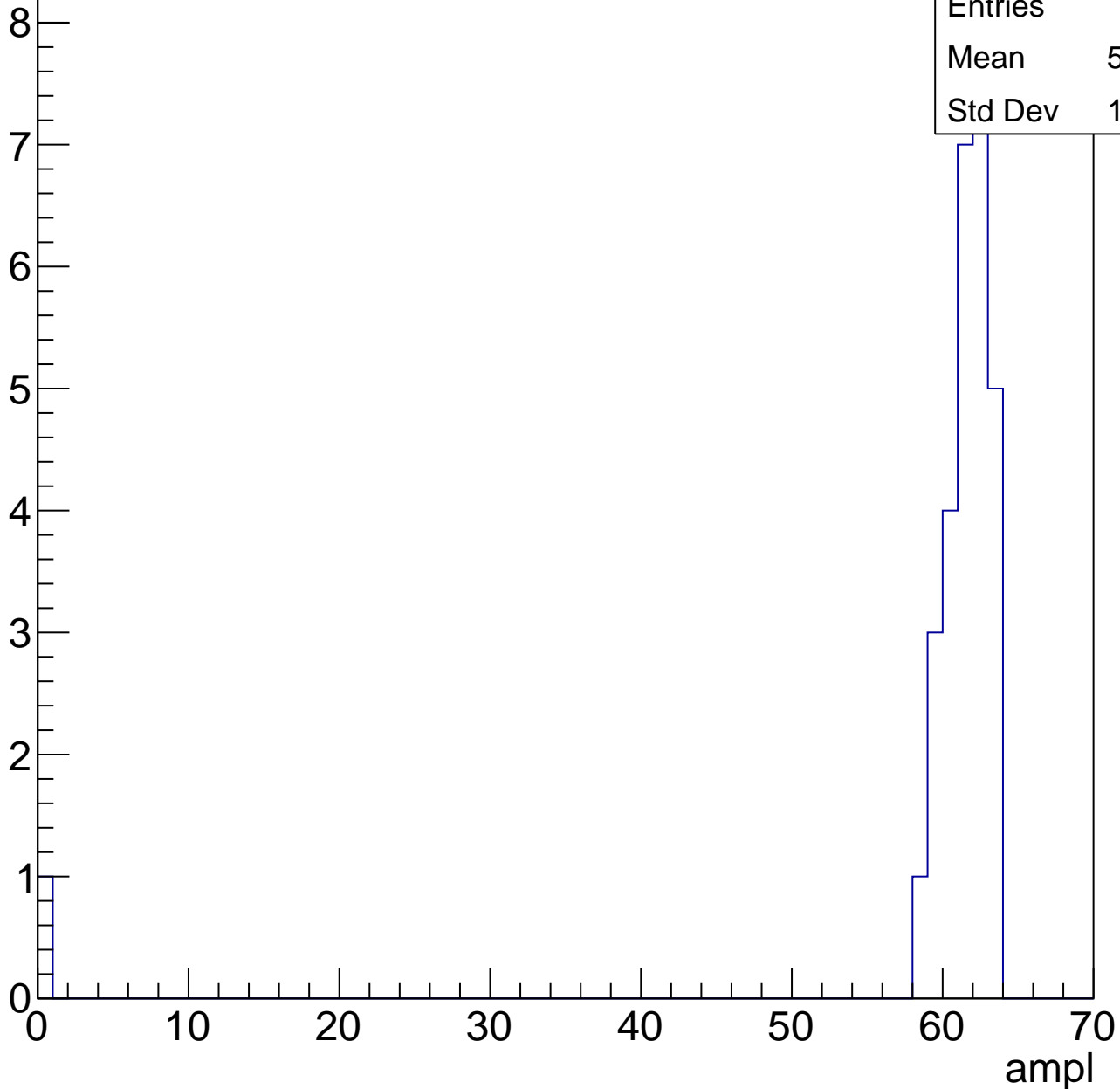


# B1L000S, U2-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	59.07
Std Dev	11.24

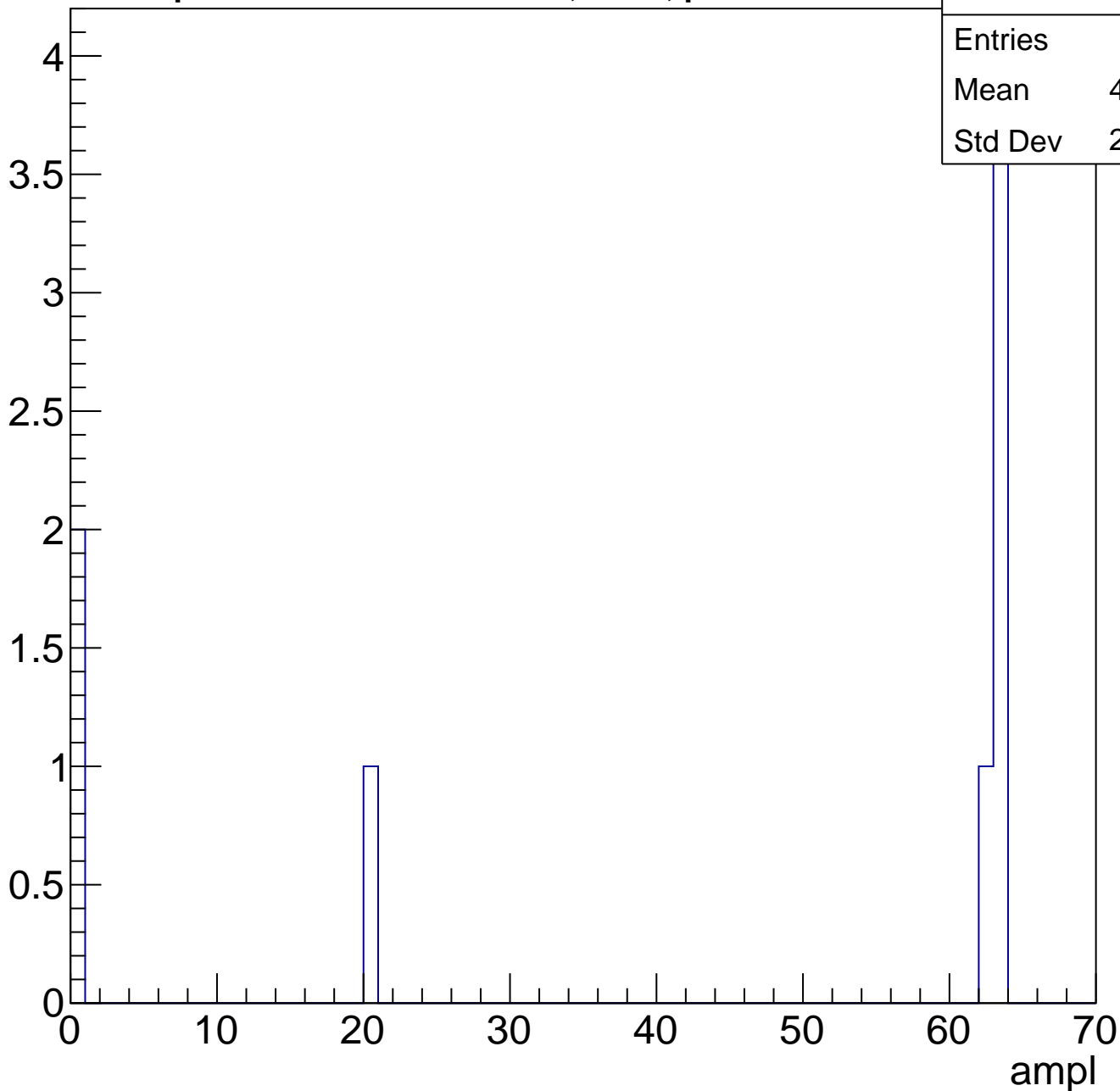




# B1L000S, U2-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch71, adc0

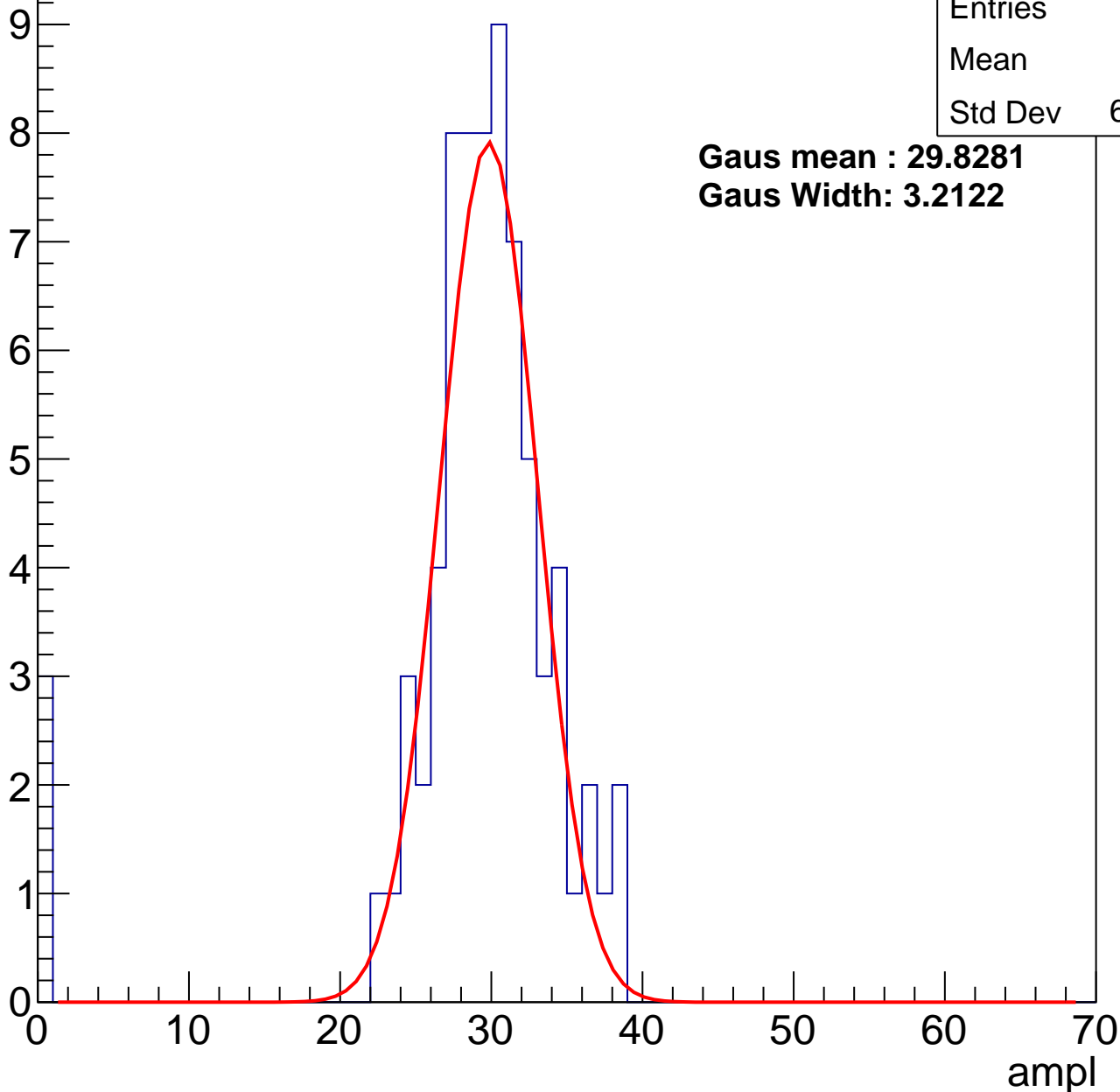
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	28.4
Std Dev	6.827

**Gaus mean : 29.8281**

**Gaus Width: 3.2122**



# B1L000S, U2-ch71, adc1

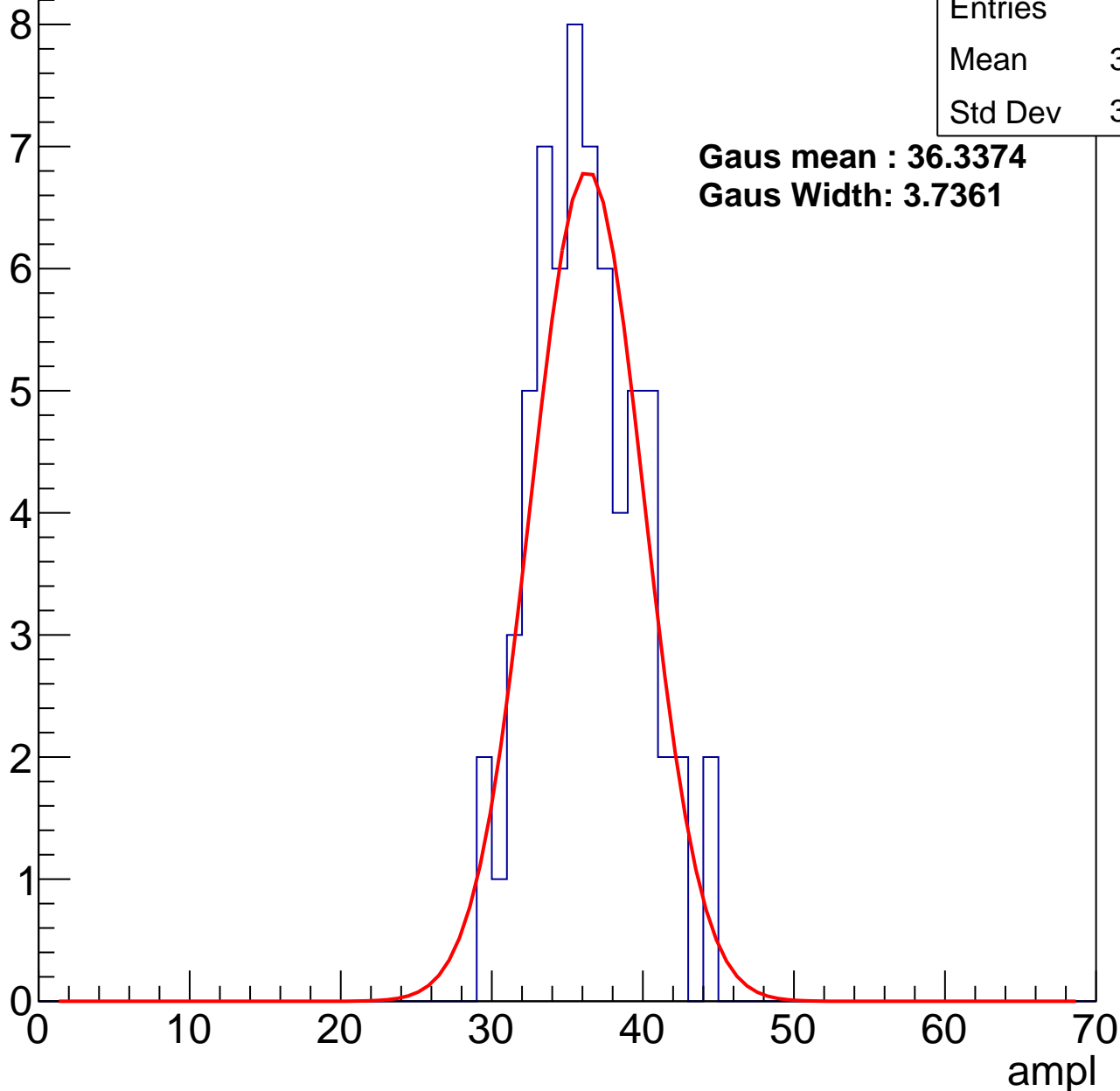
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.86
Std Dev	3.464

**Gaus mean : 36.3374**

**Gaus Width: 3.7361**



# B1L000S, U2-ch71, adc2

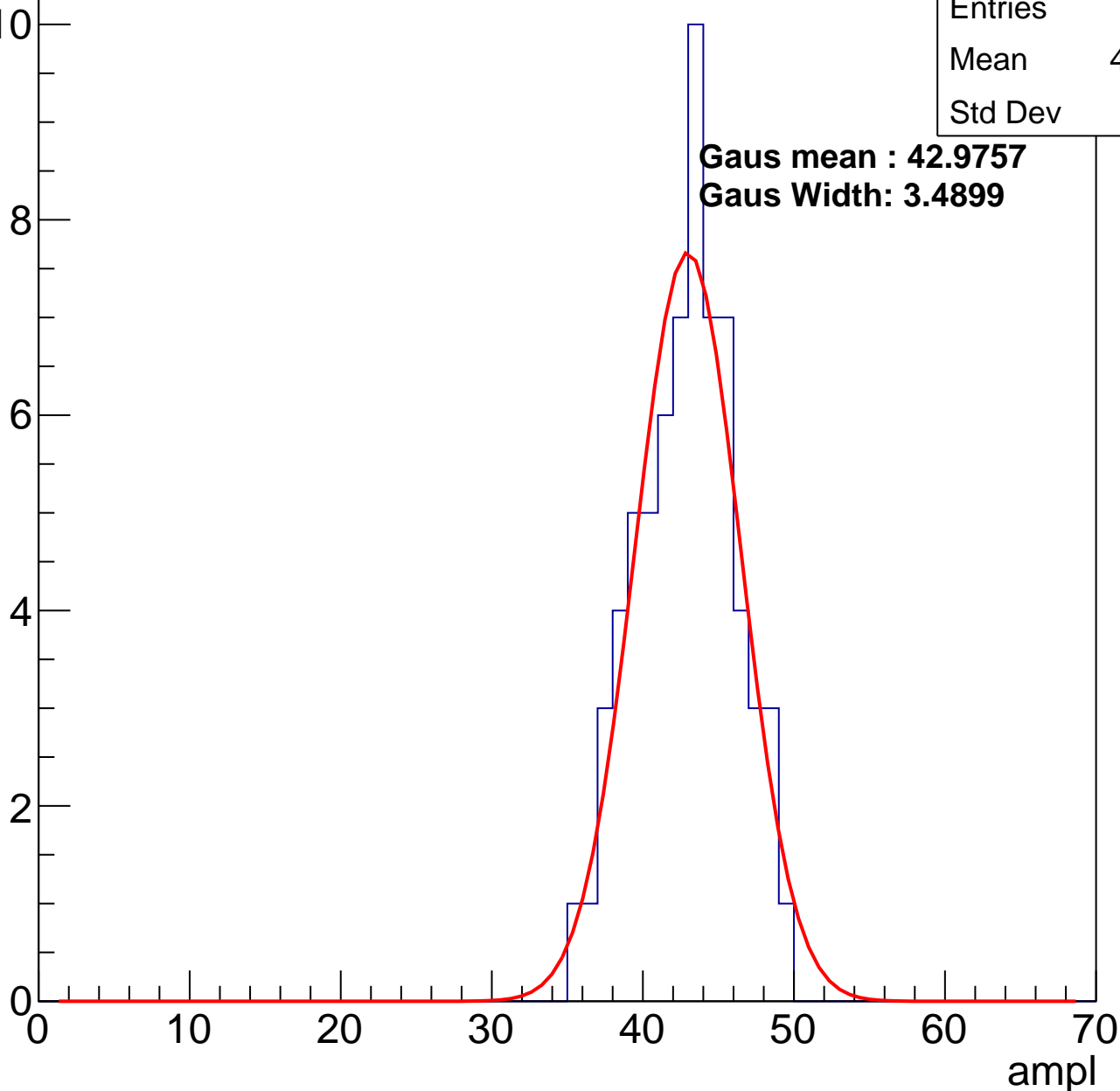
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.39
Std Dev	3.19

**Gaus mean : 42.9757**

**Gaus Width: 3.4899**

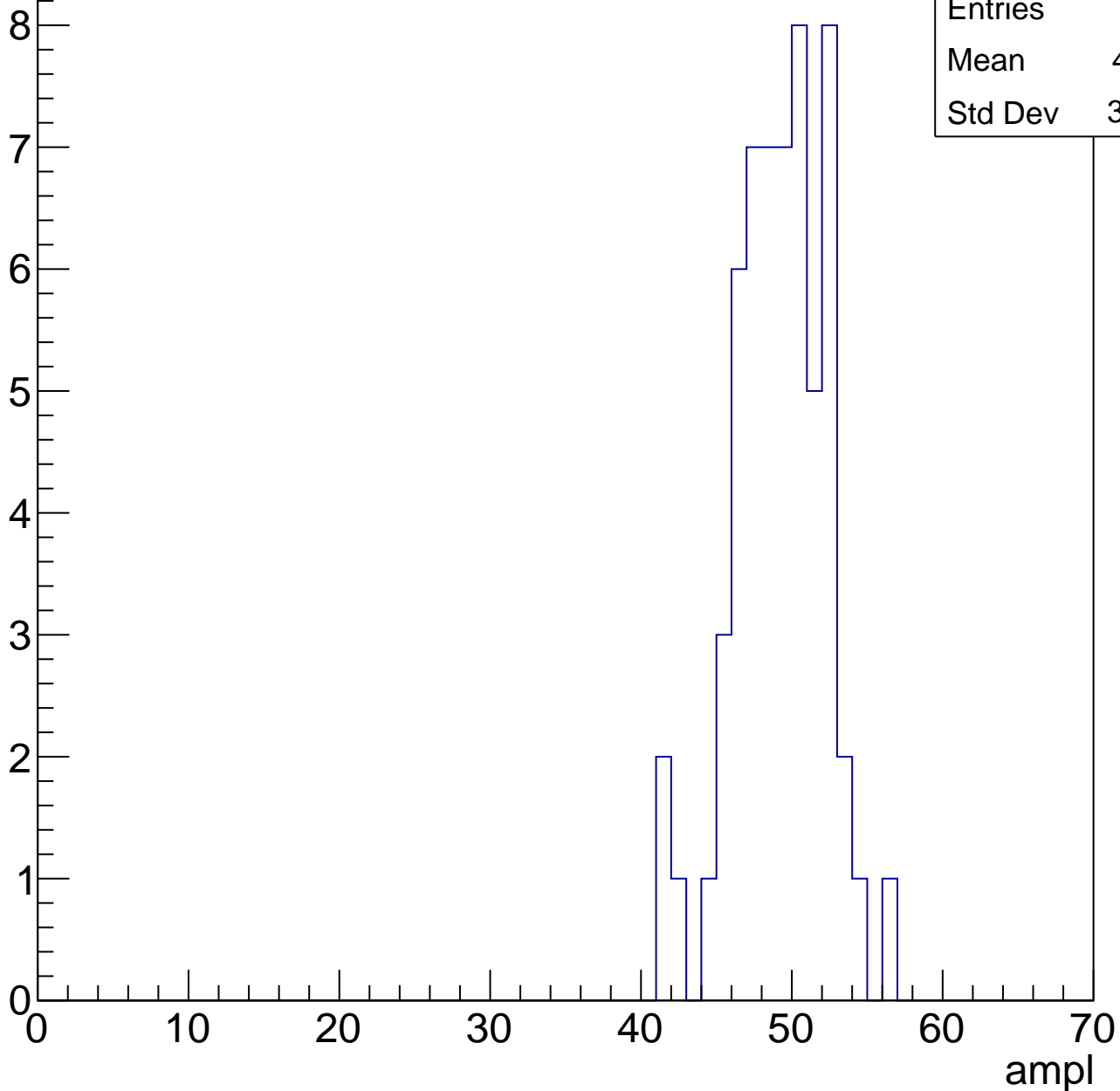


# B1L000S, U2-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	48.71
Std Dev	3.026



# B1L000S, U2-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

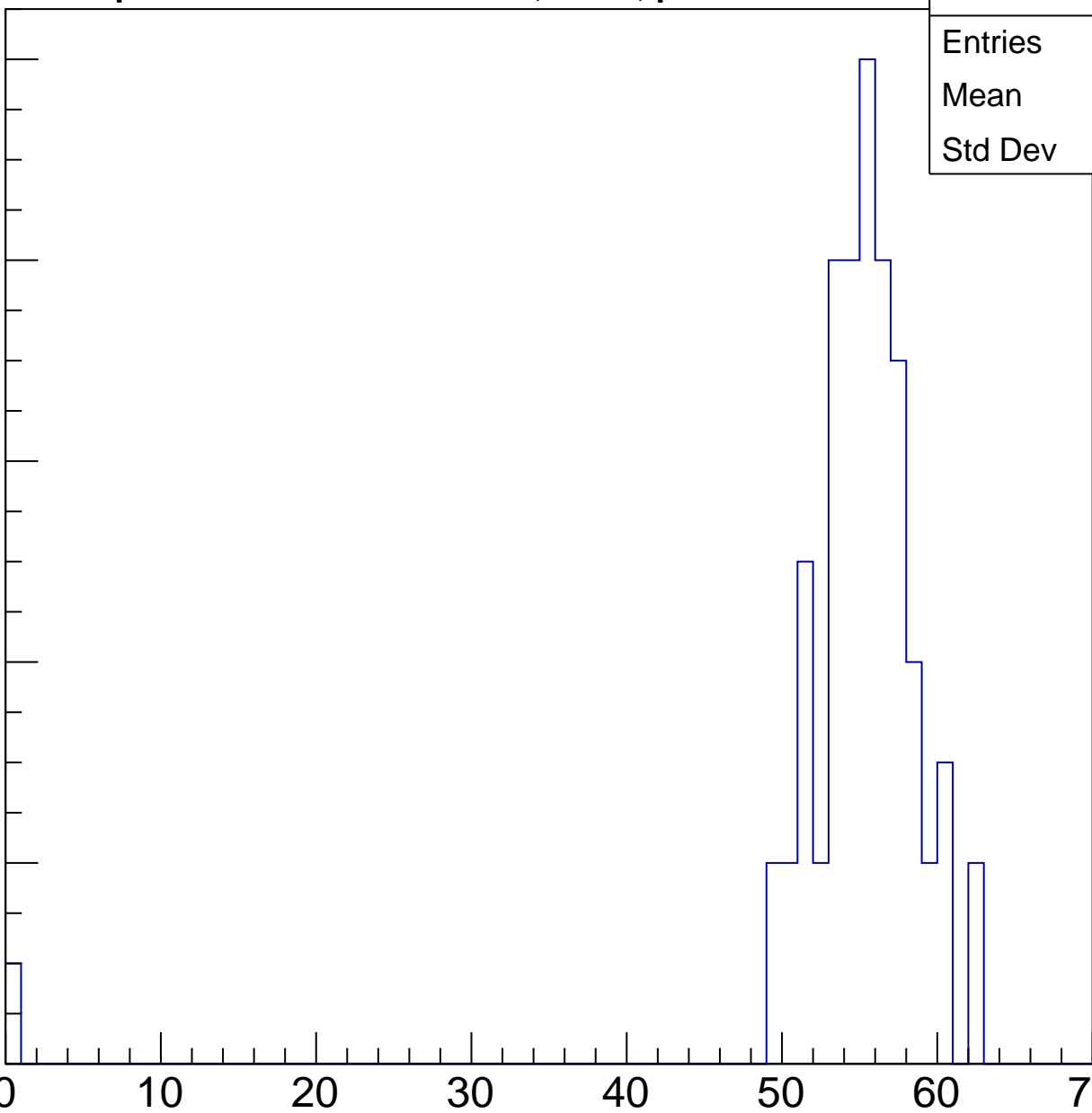
Entries	64
Mean	54.12
Std Dev	7.409

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

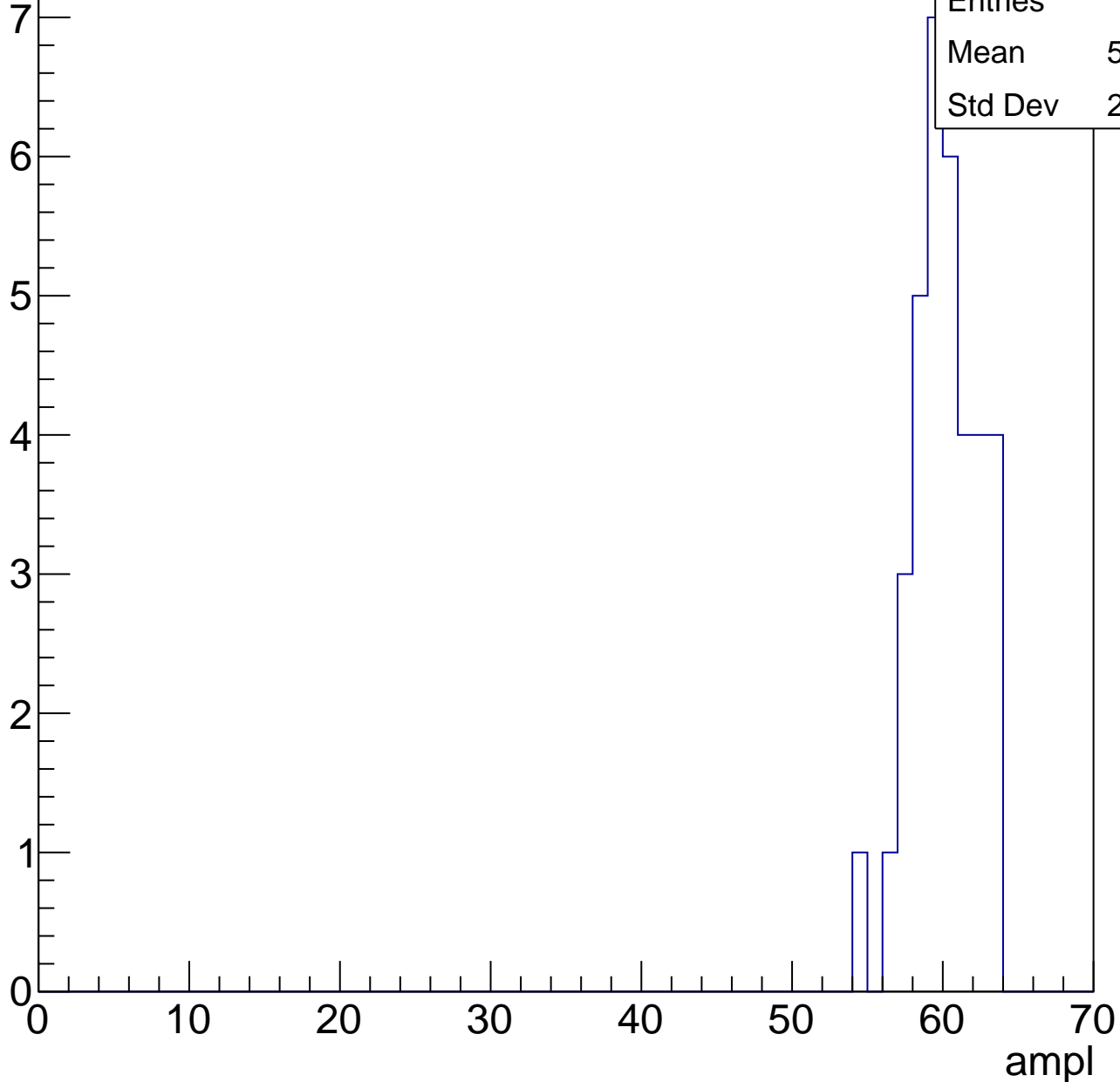
ampl



# B1L000S, U2-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

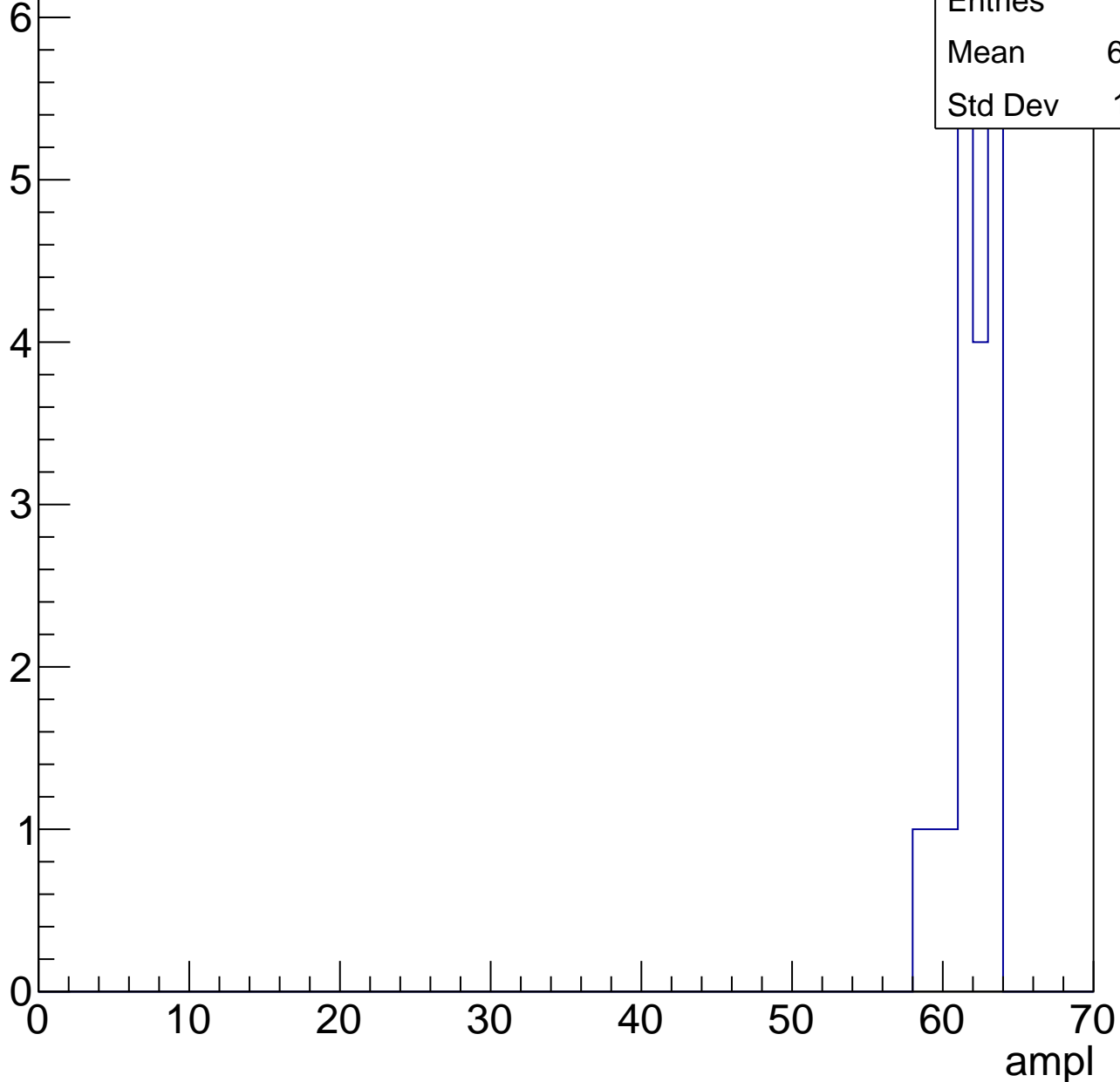
Entry



# B1L000S, U2-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U2-ch72, adc0

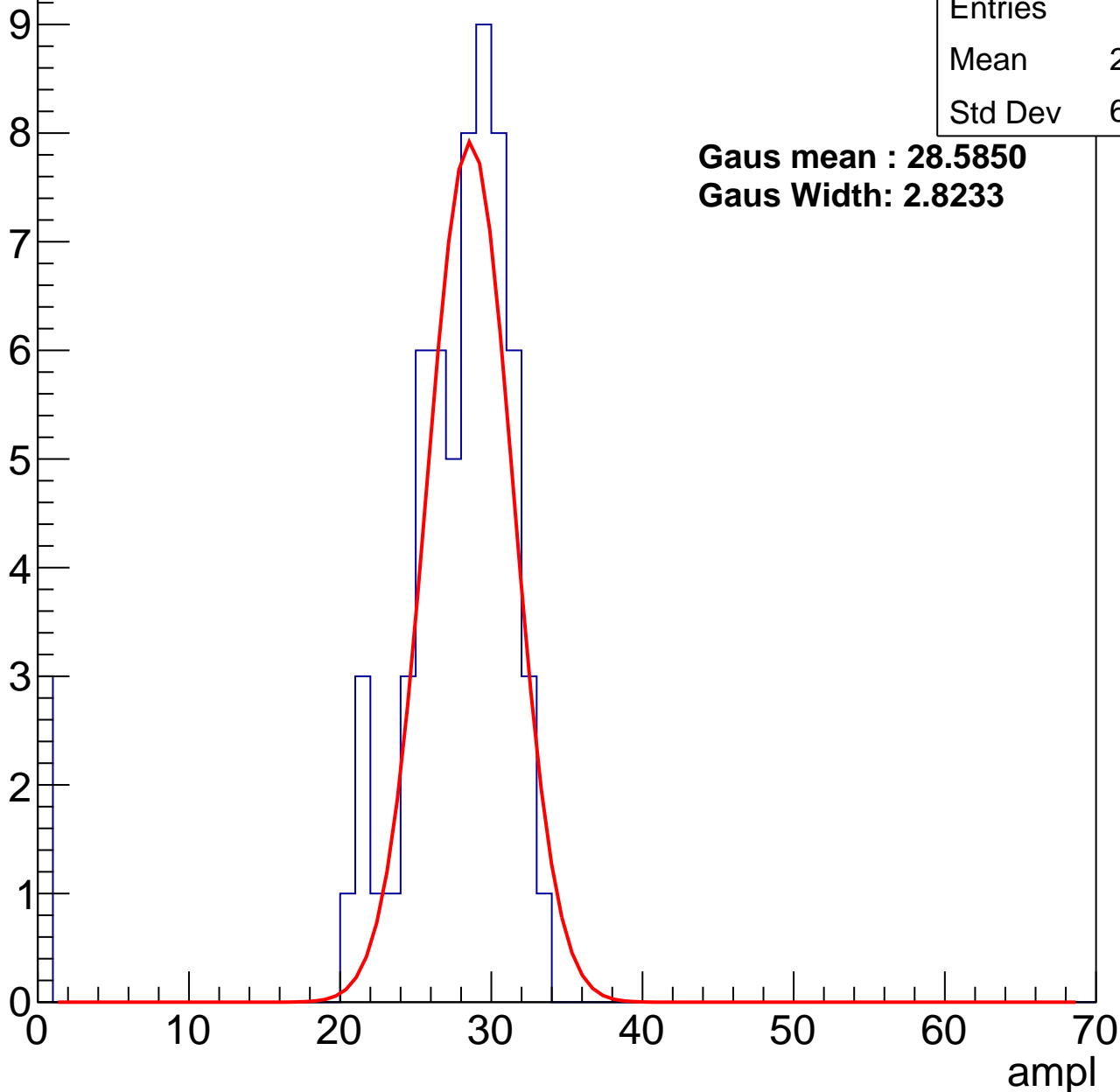
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	26.27
Std Dev	6.532

**Gaus mean : 28.5850**

**Gaus Width: 2.8233**



# B1L000S, U2-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	34.35
Std Dev	2.668

**Gaus mean : 34.9443**

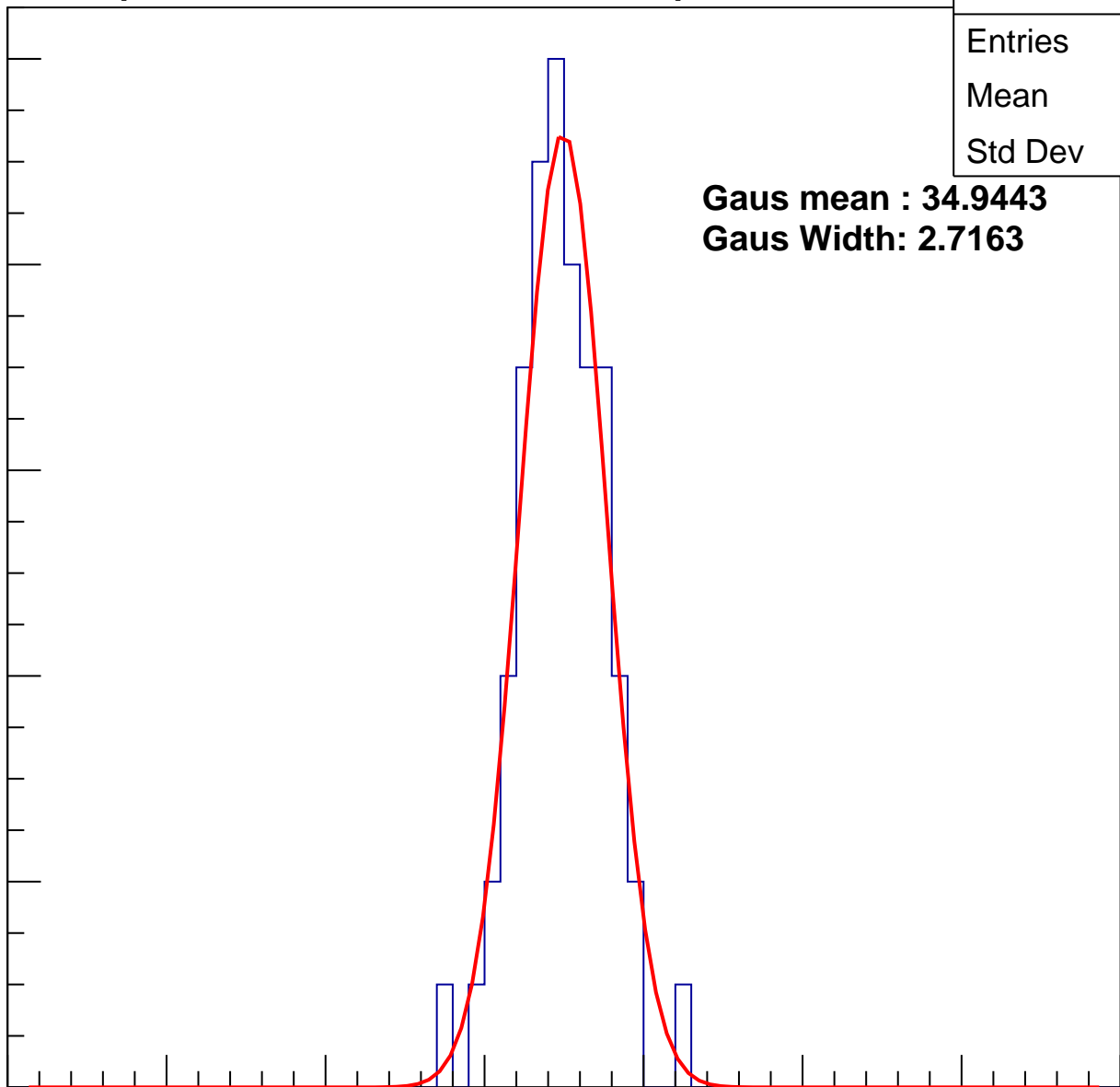
**Gaus Width: 2.7163**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch72, adc2

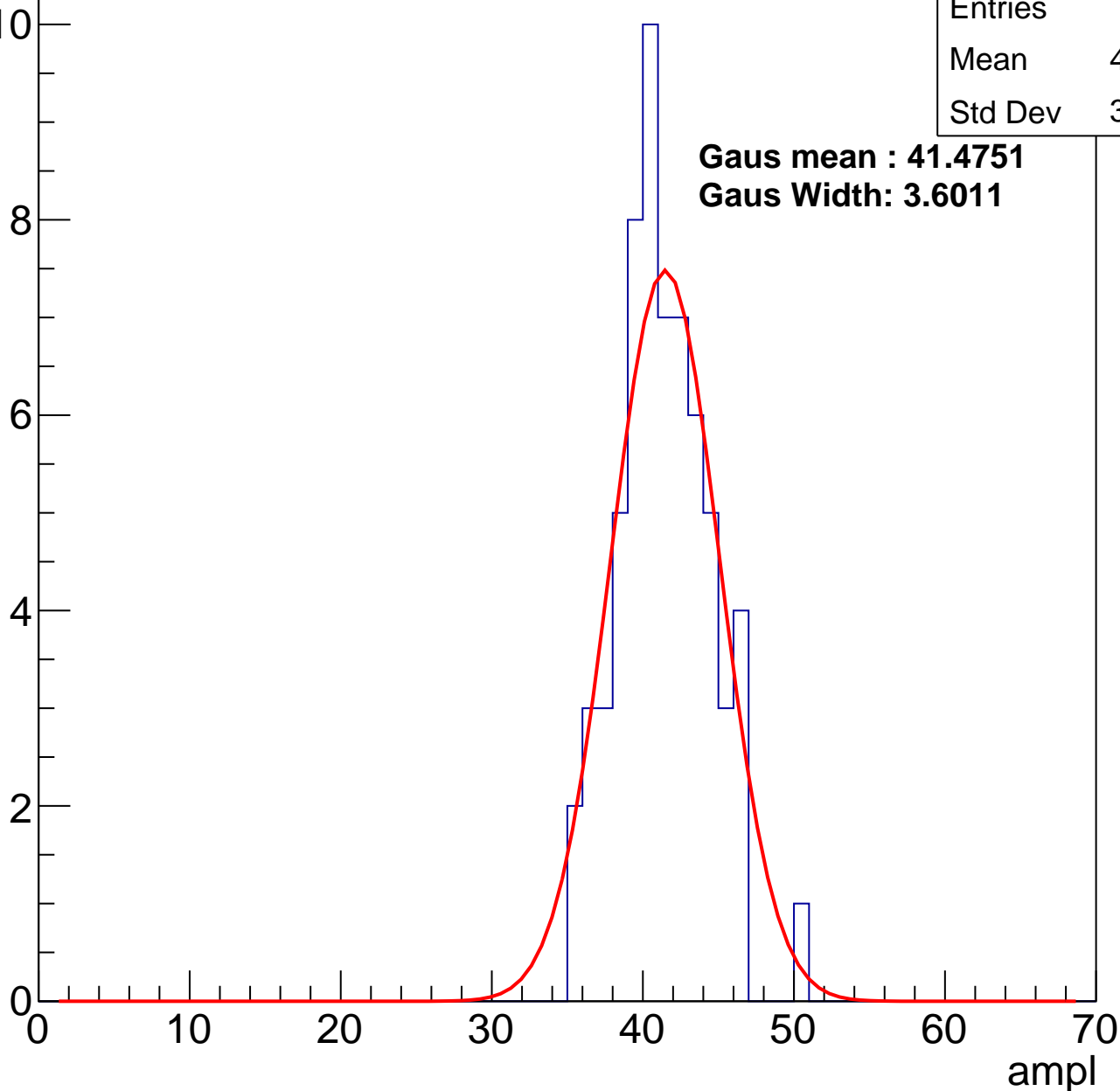
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	40.92
Std Dev	3.028

**Gaus mean : 41.4751**

**Gaus Width: 3.6011**

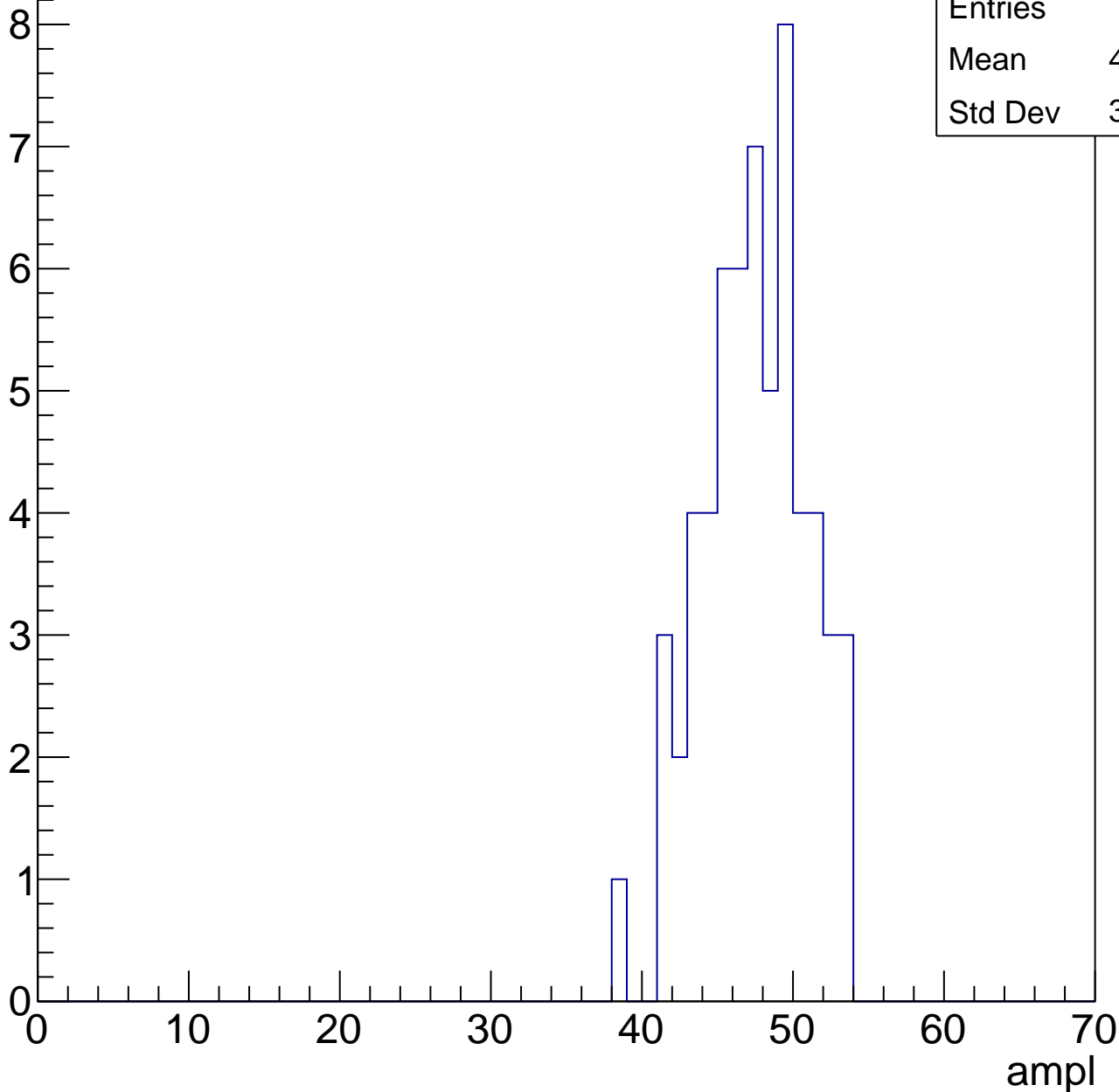


# B1L000S, U2-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

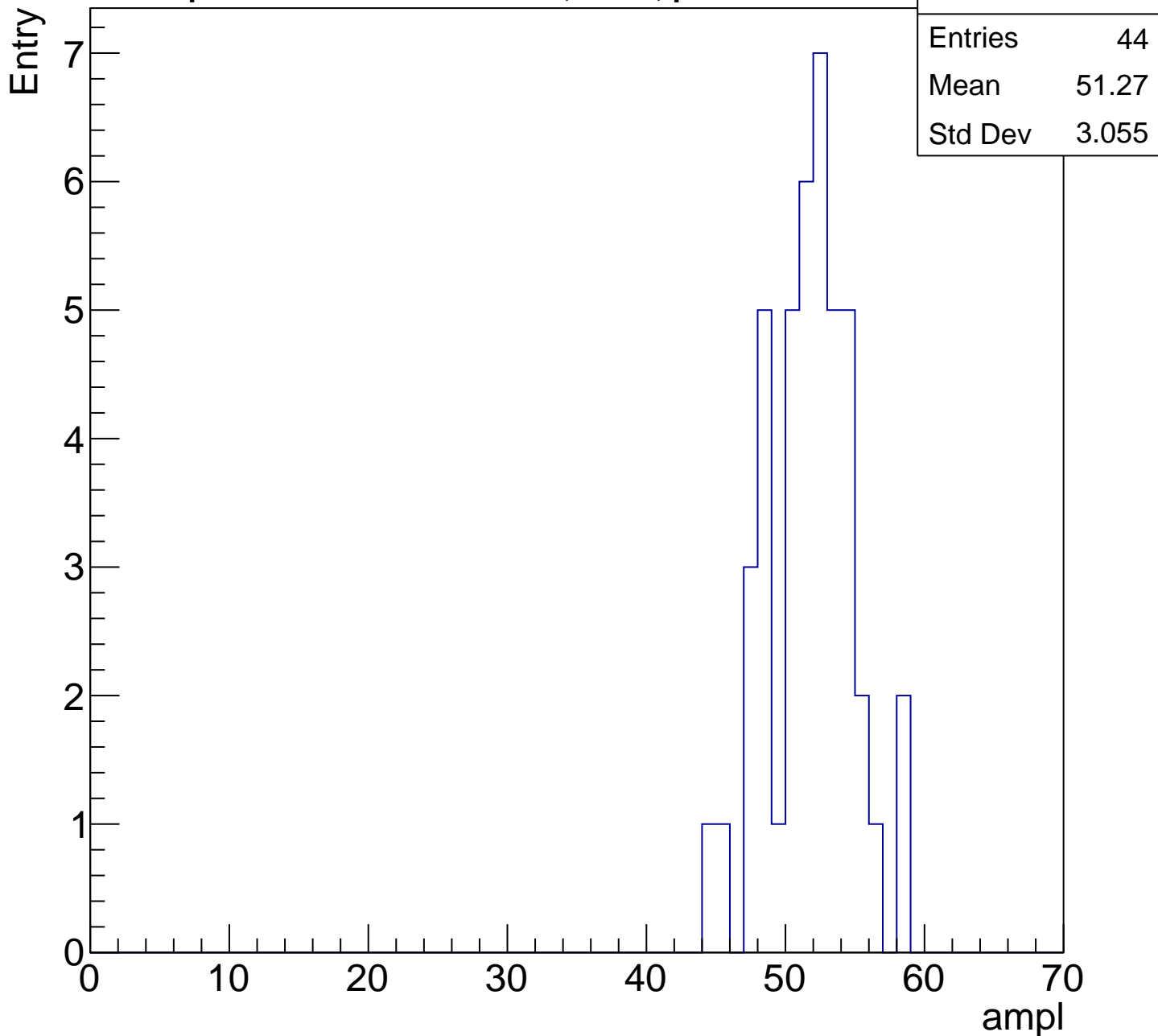
Entry

Entries	60
Mean	46.98
Std Dev	3.389



# B1L000S, U2-ch72, adc4

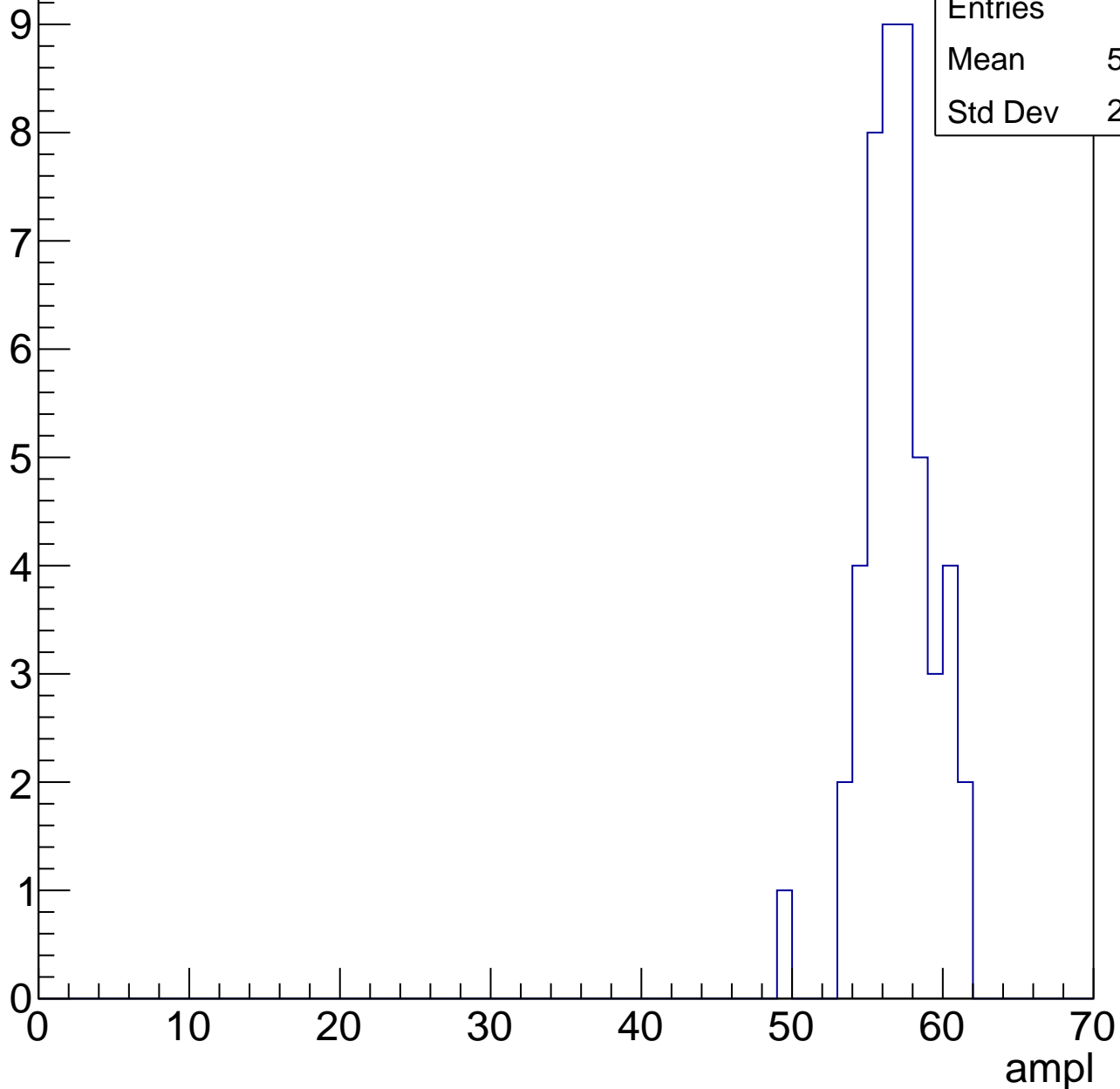
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

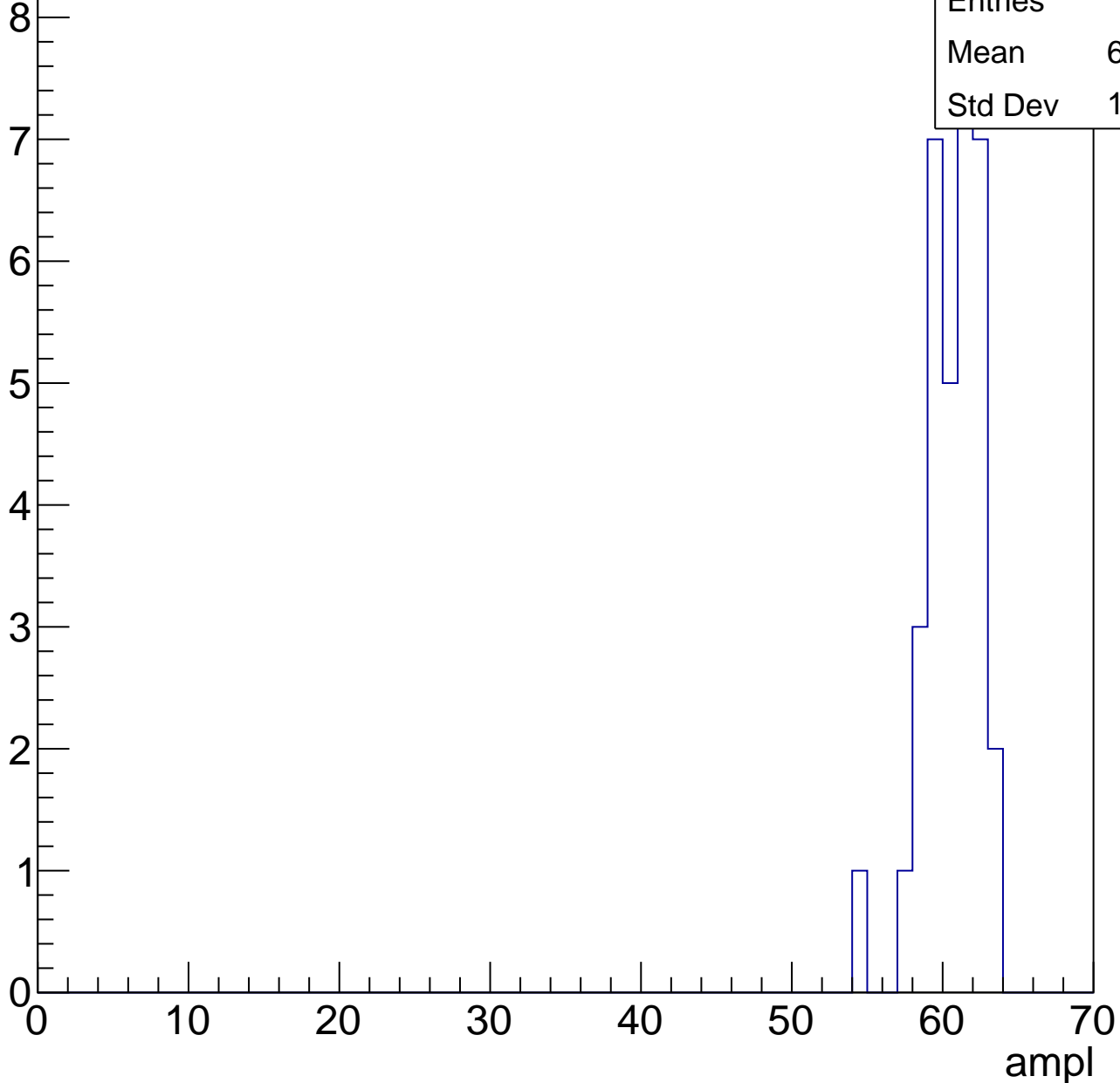


# B1L000S, U2-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	60.18
Std Dev	1.855



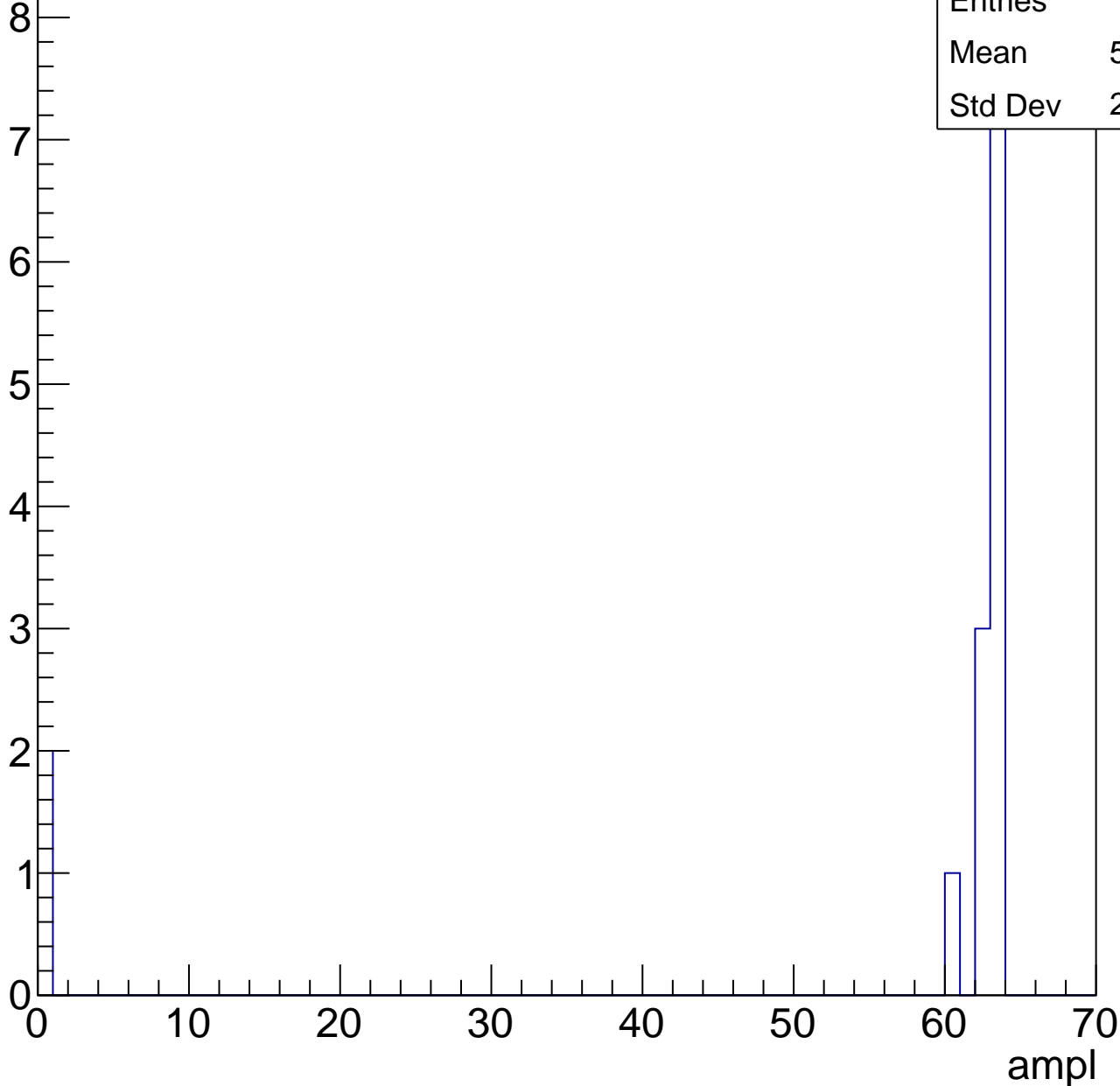


# B1L000S, U2-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	53.57
Std Dev	21.89



# B1L000S, U2-ch73, adc0

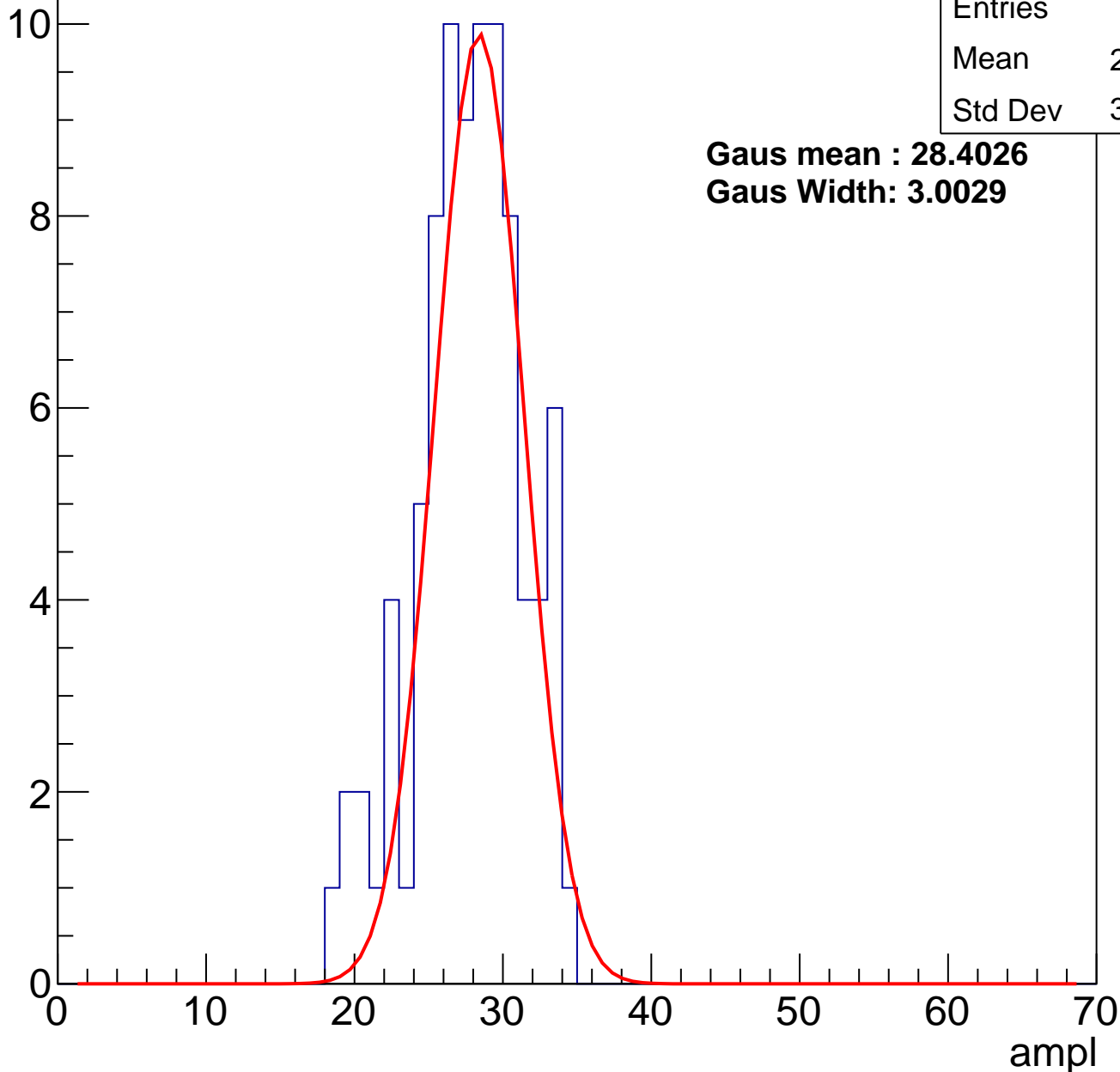
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	86
Mean	27.27
Std Dev	3.562

**Gaus mean : 28.4026**

**Gaus Width: 3.0029**

Entry



# B1L000S, U2-ch73, adc1

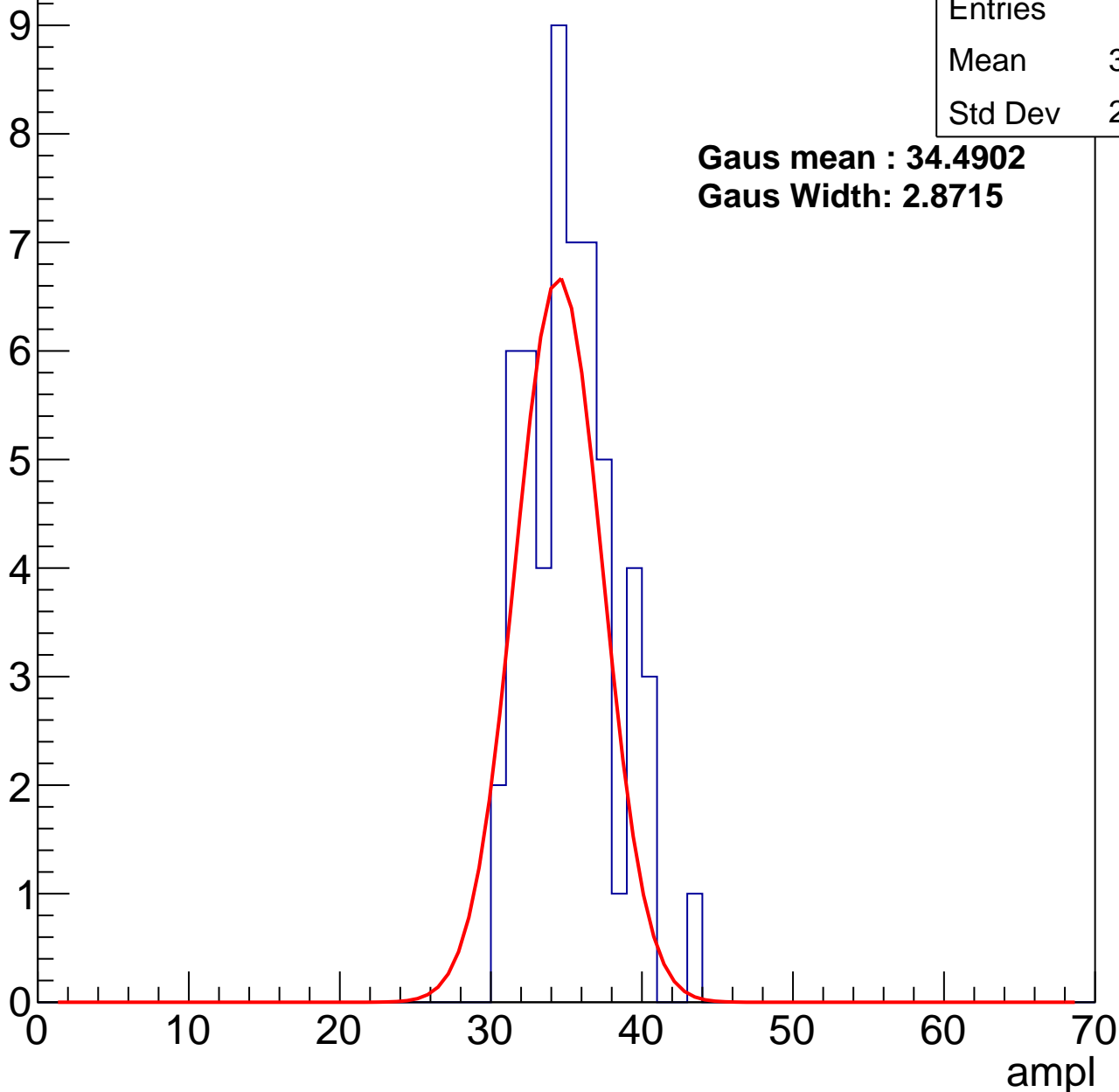
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34.82
Std Dev	2.899

**Gaus mean : 34.4902**

**Gaus Width: 2.8715**



# B1L000S, U2-ch73, adc2

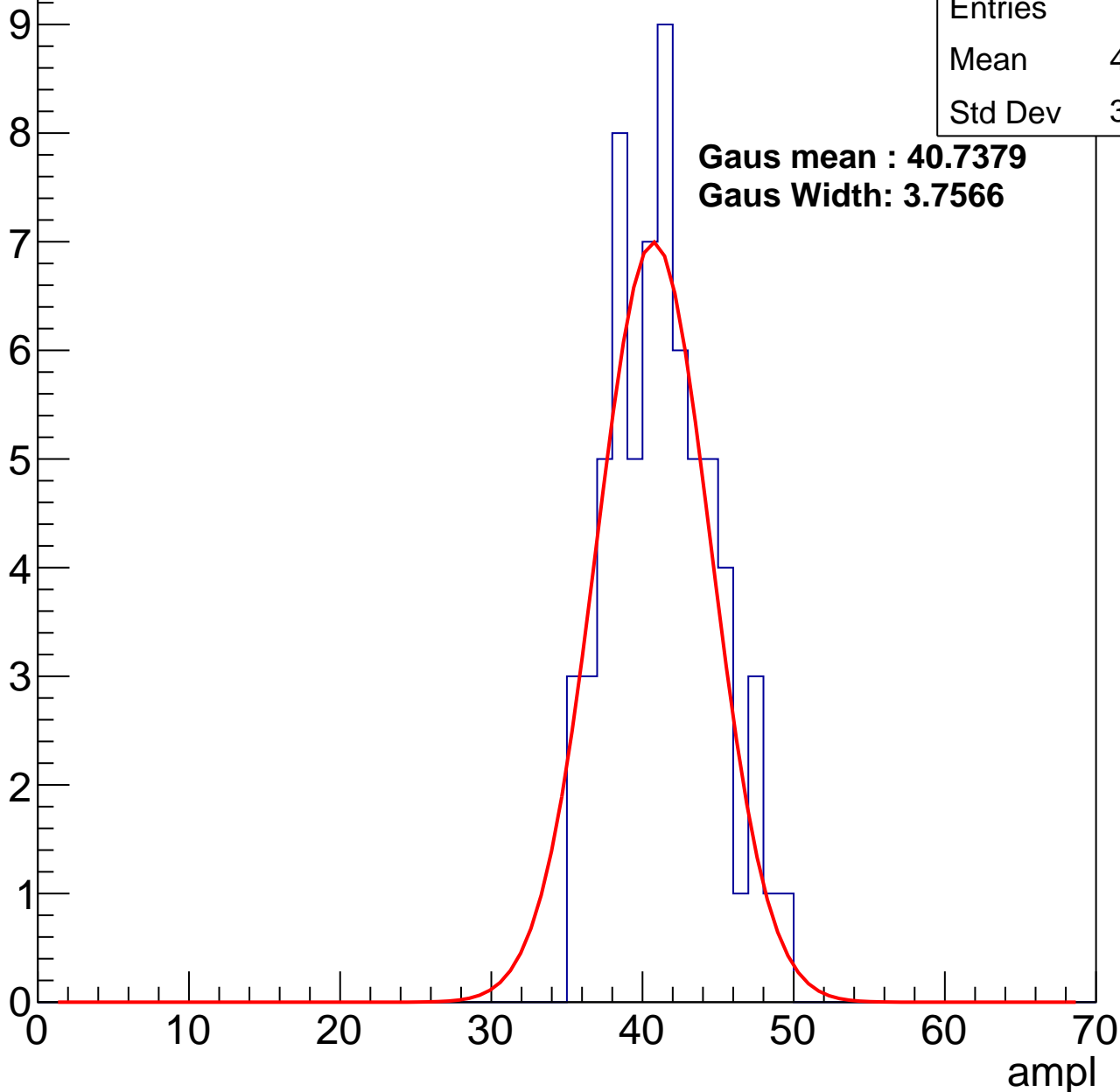
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	40.86
Std Dev	3.352

**Gaus mean : 40.7379**

**Gaus Width: 3.7566**

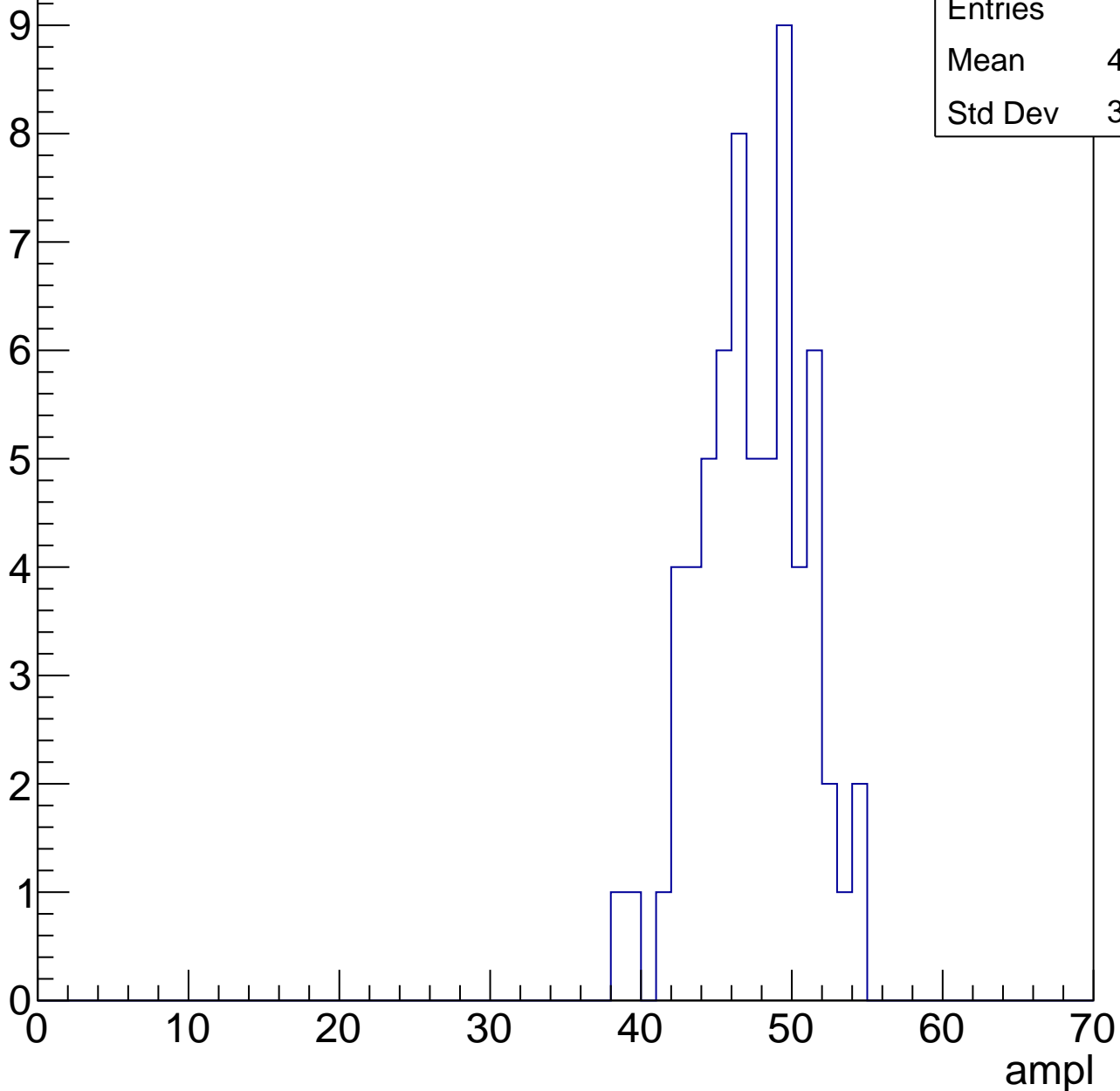


# B1L000S, U2-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	46.92
Std Dev	3.488

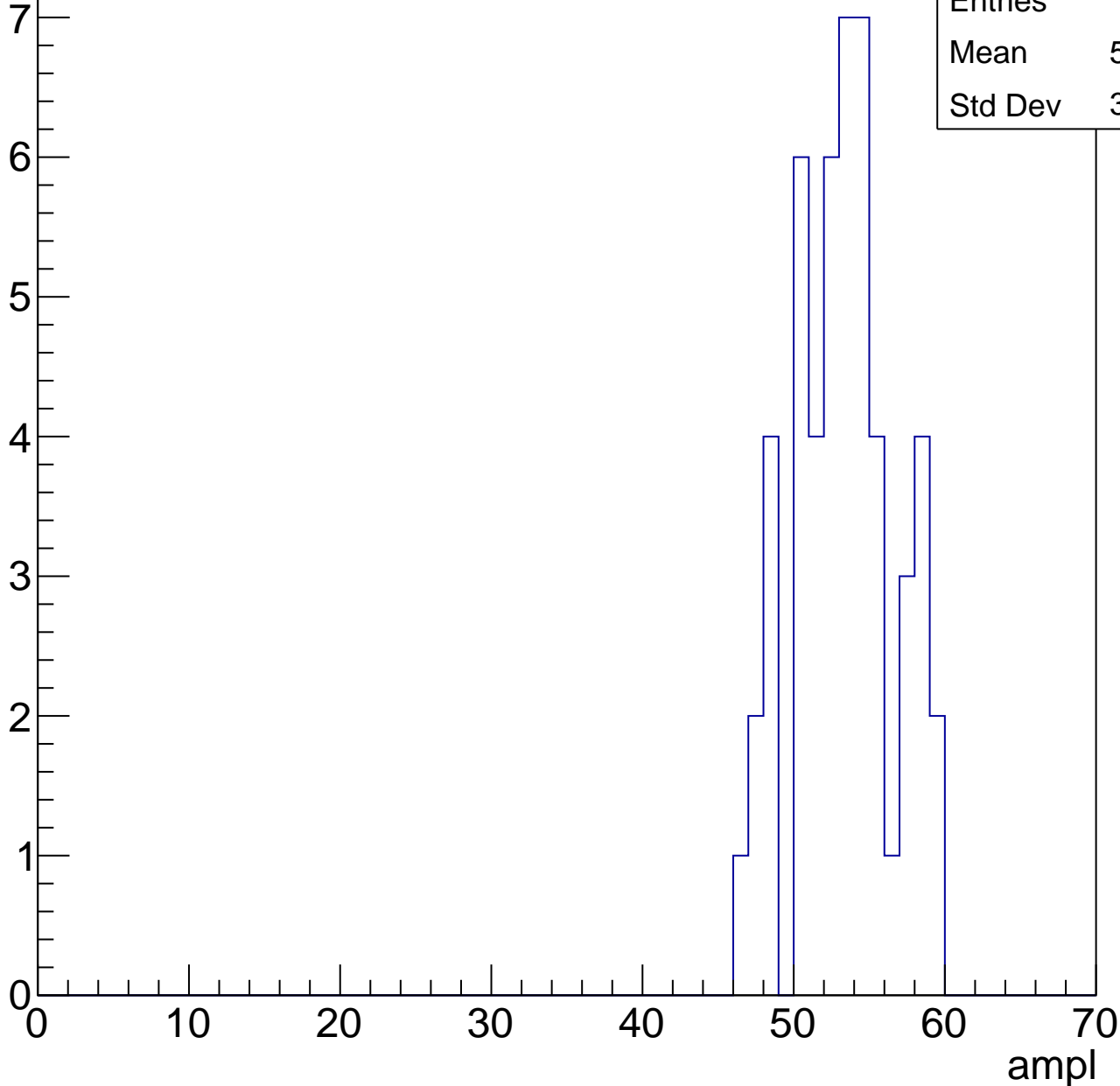


# B1L000S, U2-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

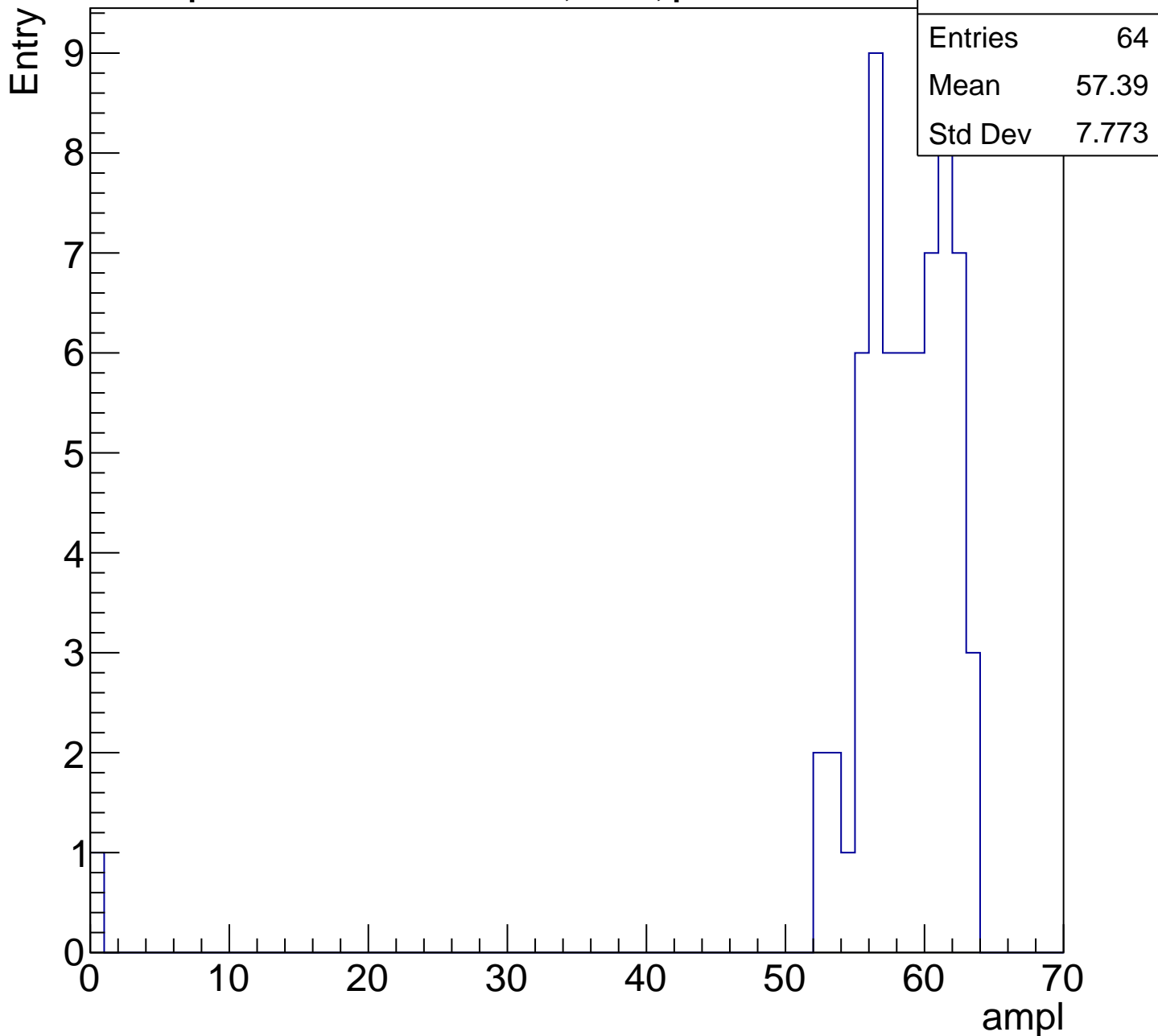
Entry

Entries	51
Mean	52.82
Std Dev	3.276



# B1L000S, U2-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

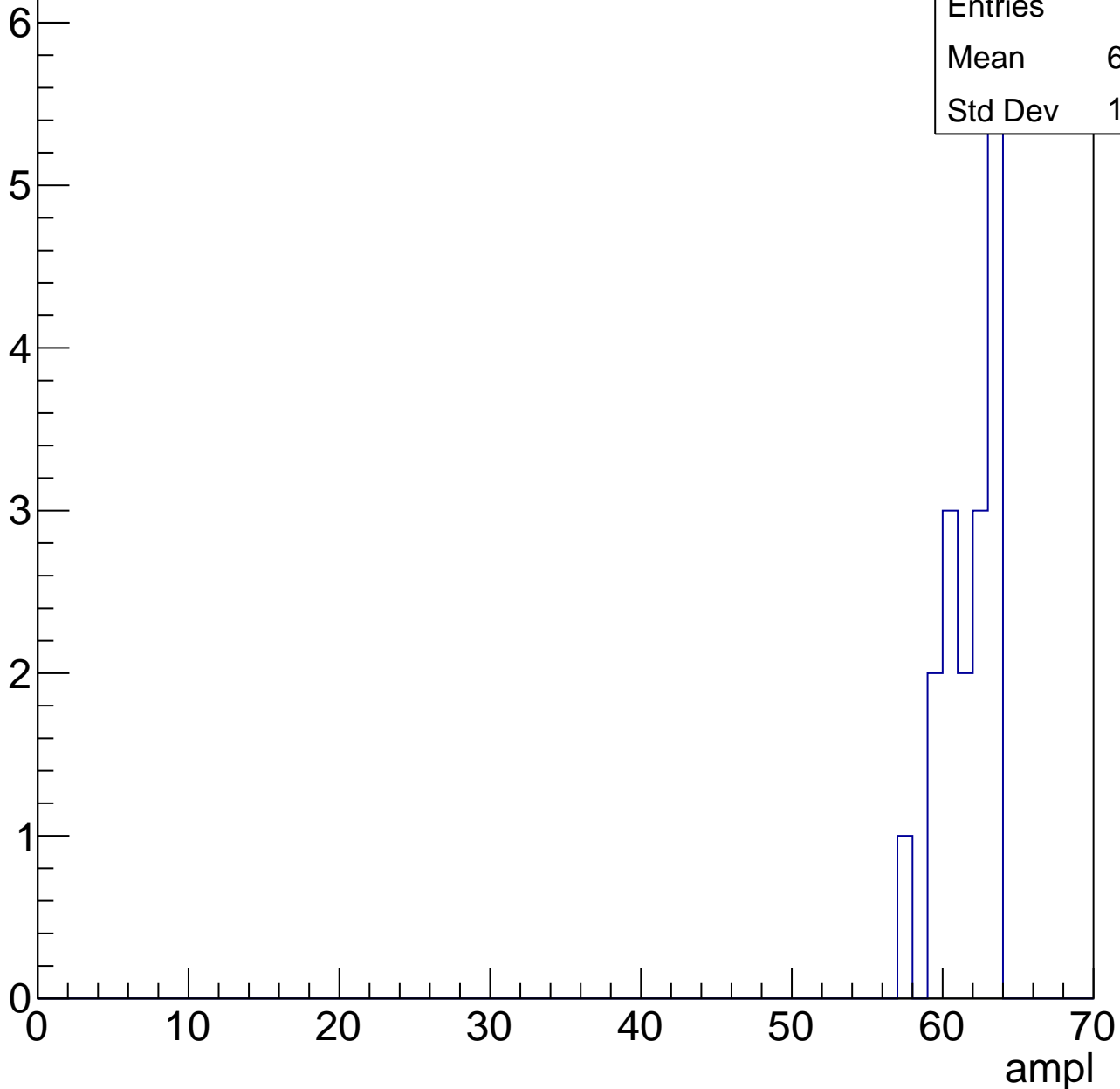


# B1L000S, U2-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	61.24
Std Dev	1.767





# B1L000S, U2-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch74, adc0

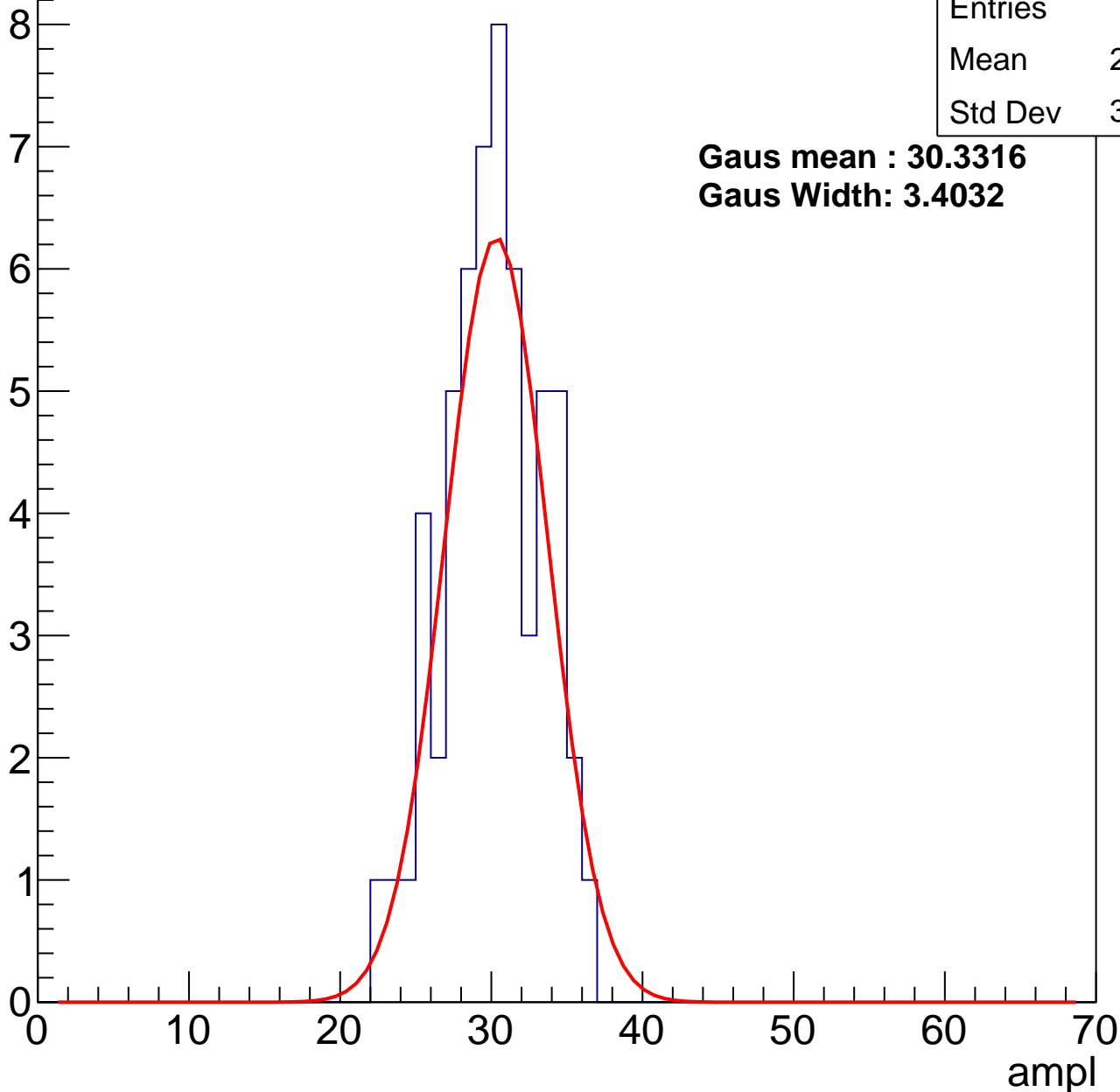
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	29.65
Std Dev	3.187

**Gaus mean : 30.3316**

**Gaus Width: 3.4032**



# B1L000S, U2-ch74, adc1

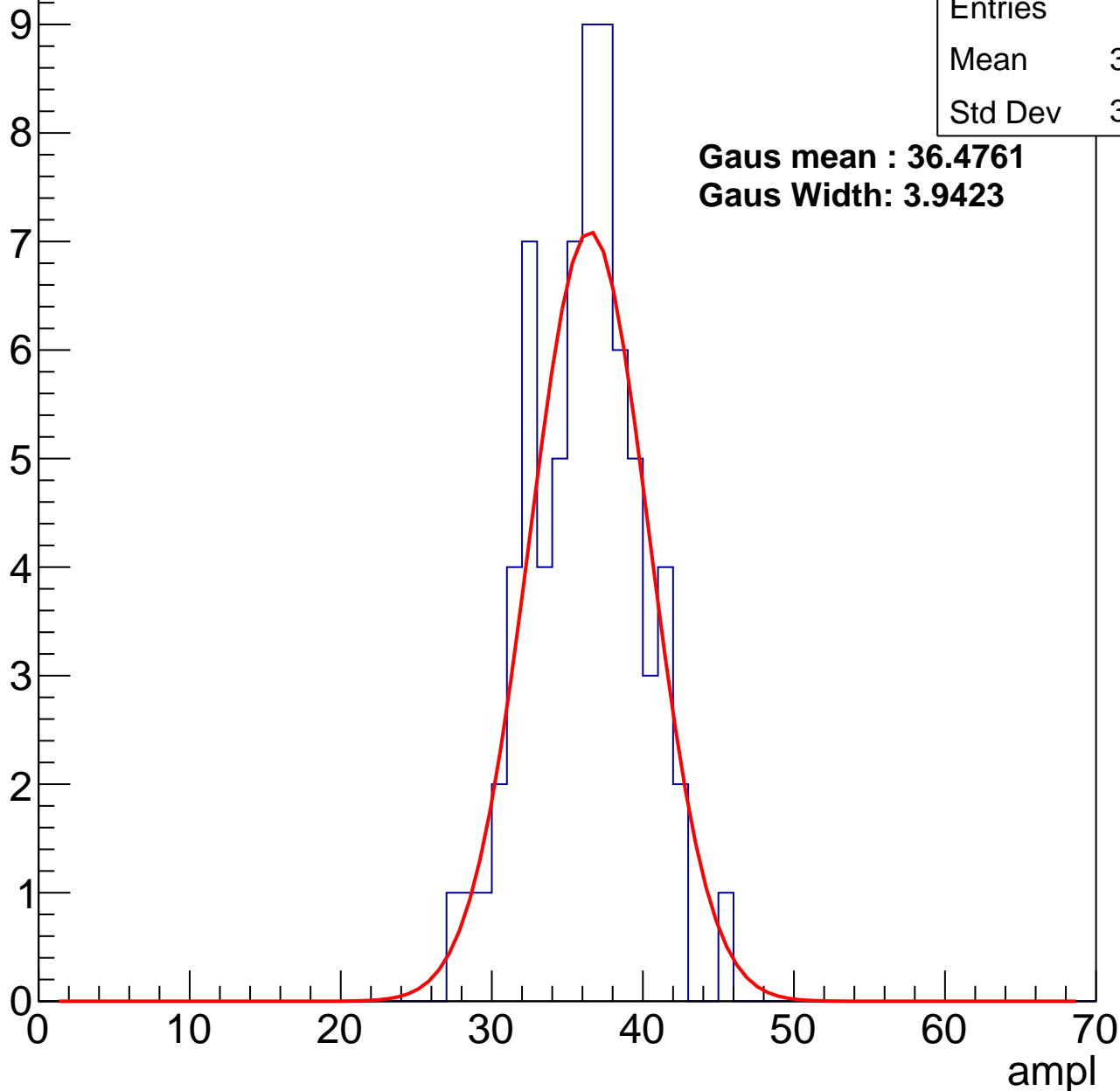
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.66
Std Dev	3.584

**Gaus mean : 36.4761**

**Gaus Width: 3.9423**



# B1L000S, U2-ch74, adc2

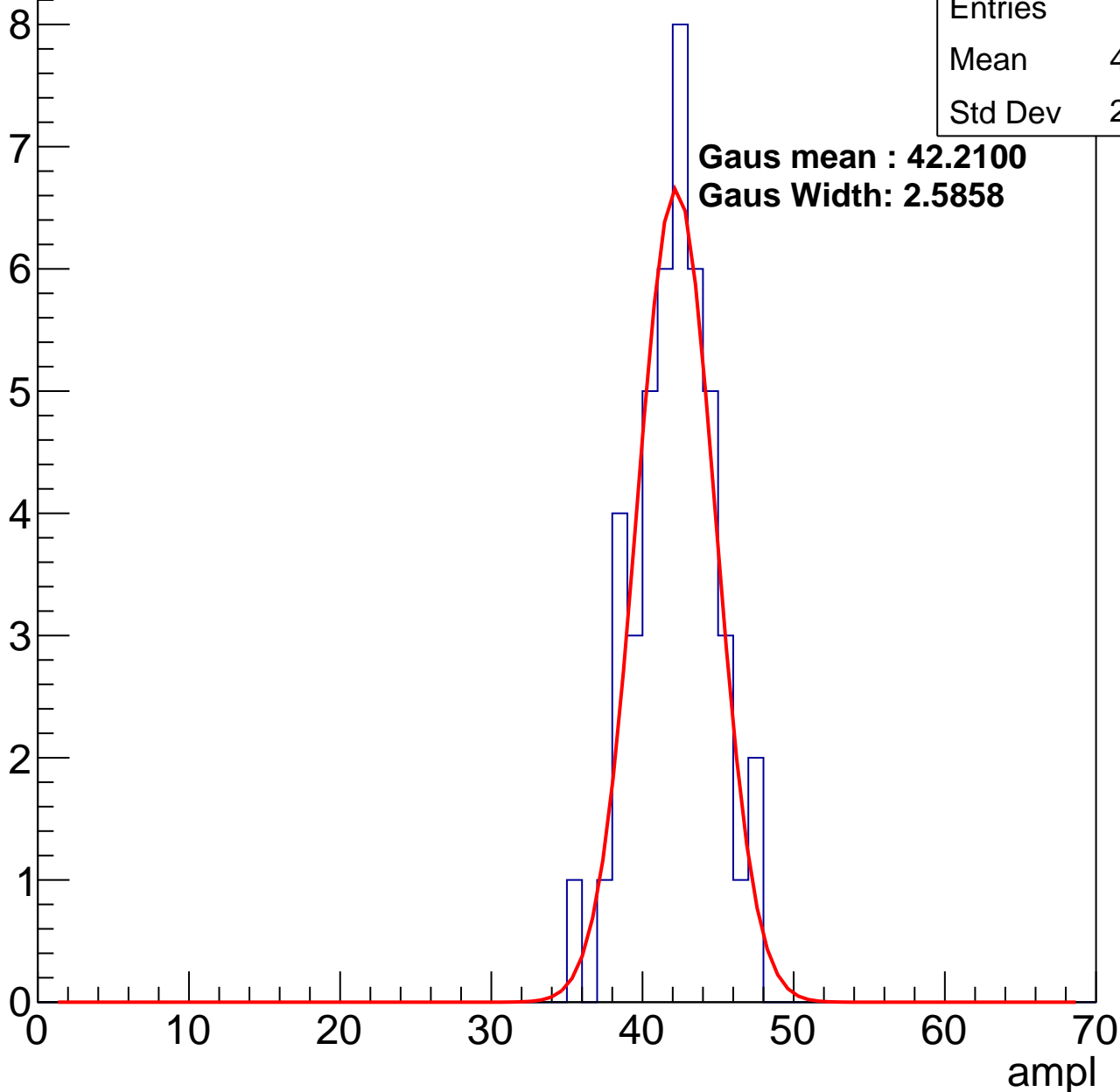
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	41.69
Std Dev	2.606

**Gaus mean : 42.2100**

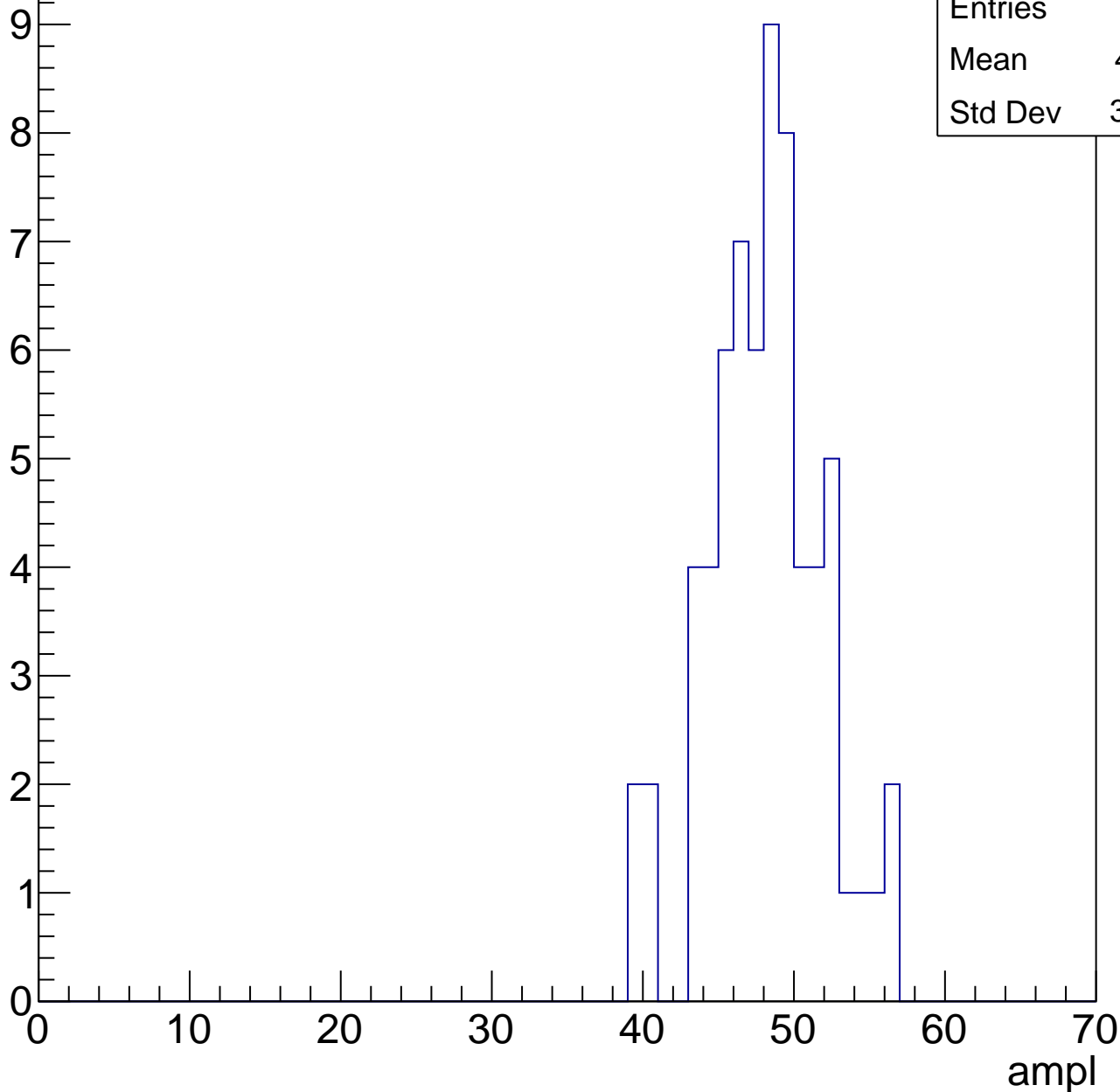
**Gaus Width: 2.5858**



# B1L000S, U2-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

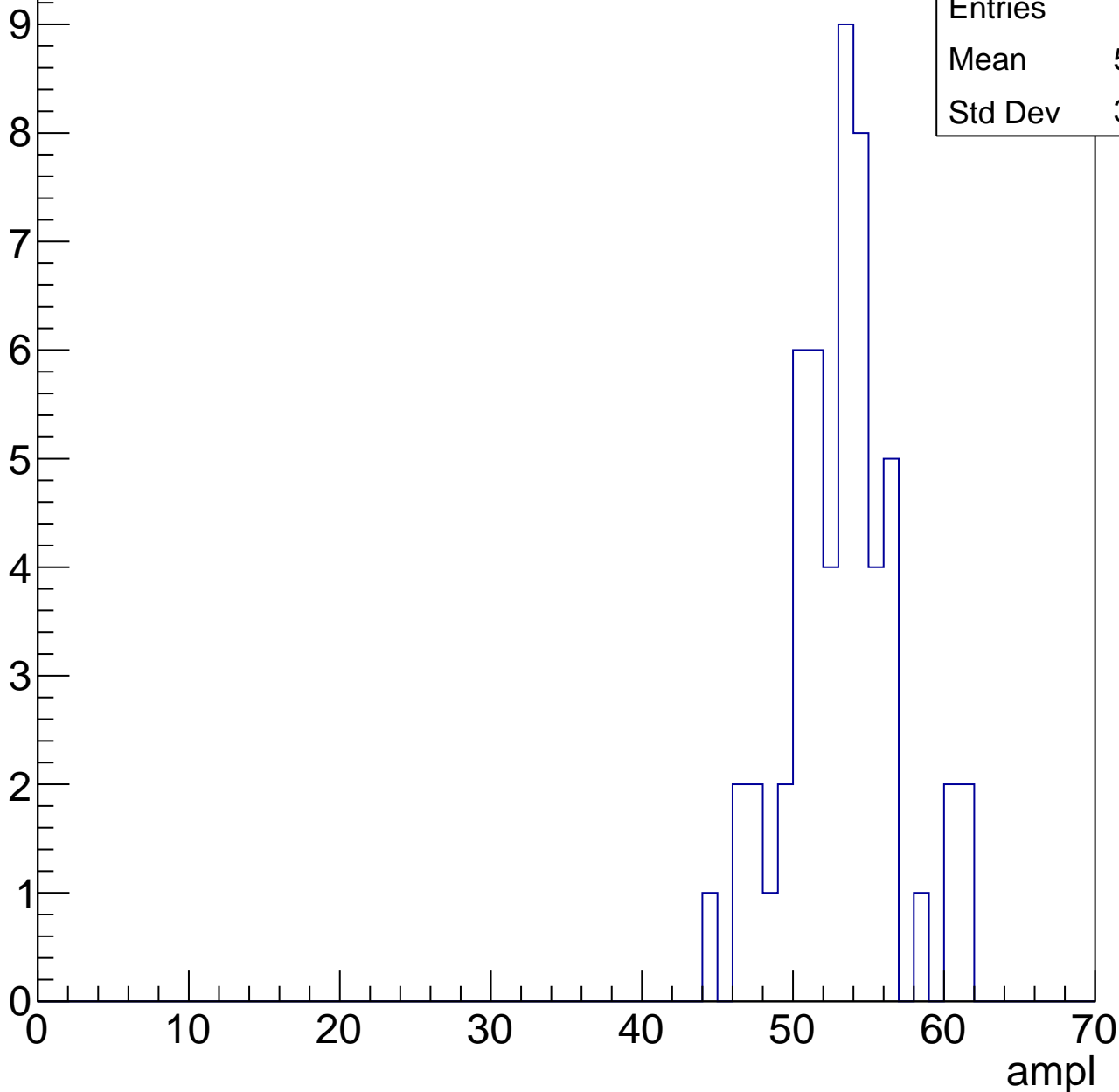


# B1L000S, U2-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	52.71
Std Dev	3.581

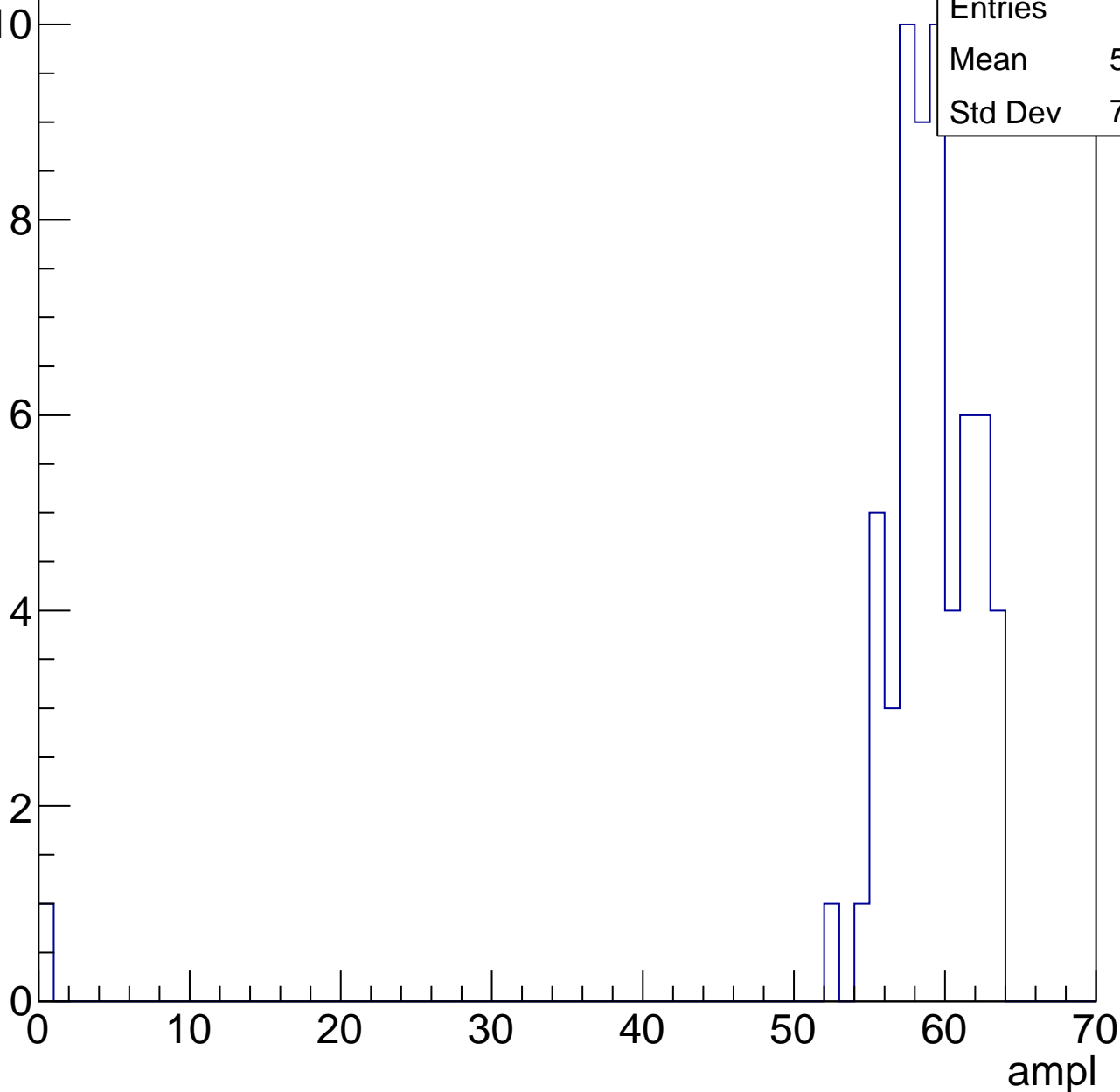


# B1L000S, U2-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

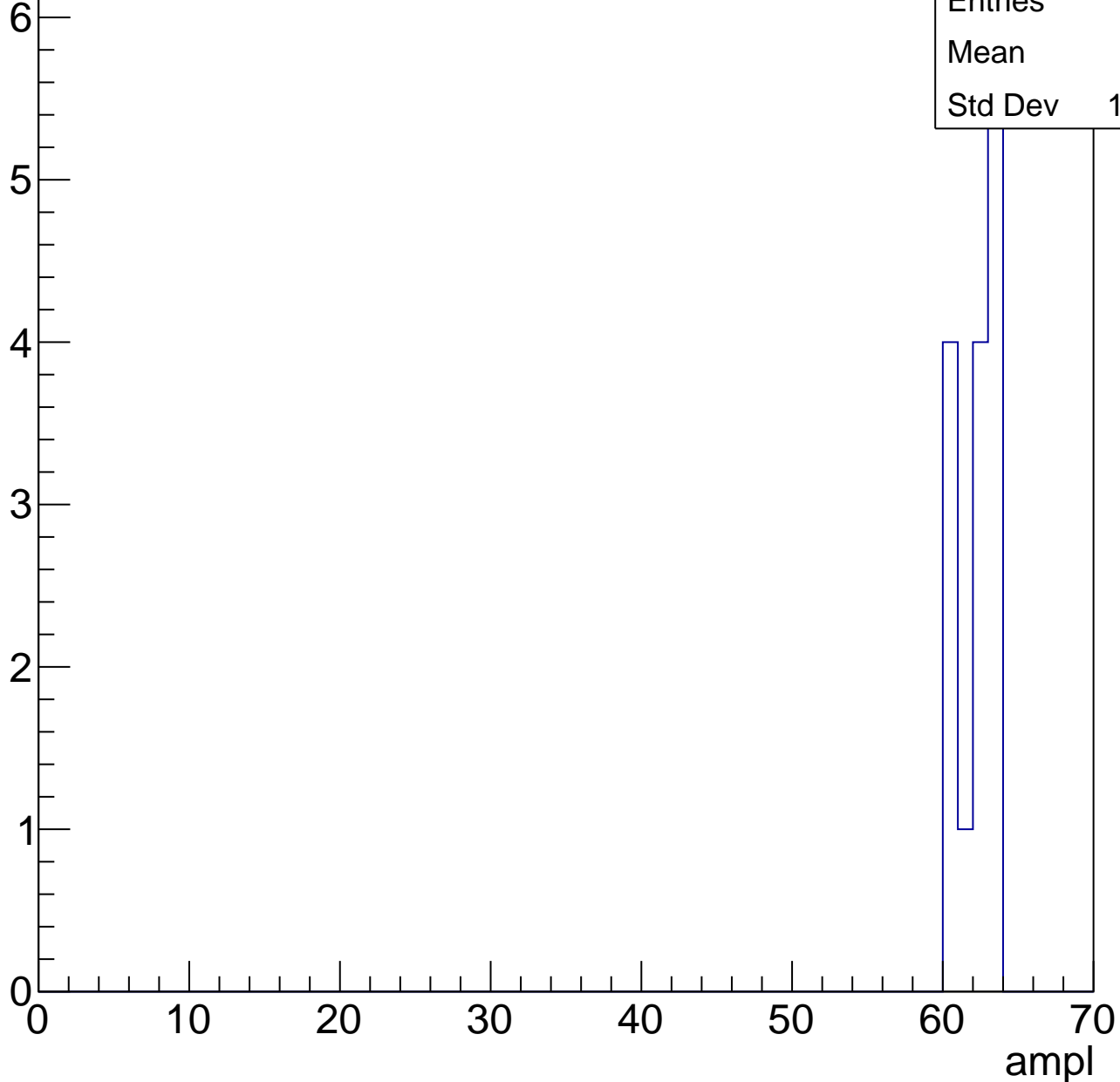
Entries	60
Mean	57.68
Std Dev	7.909



# B1L000S, U2-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch75, adc0

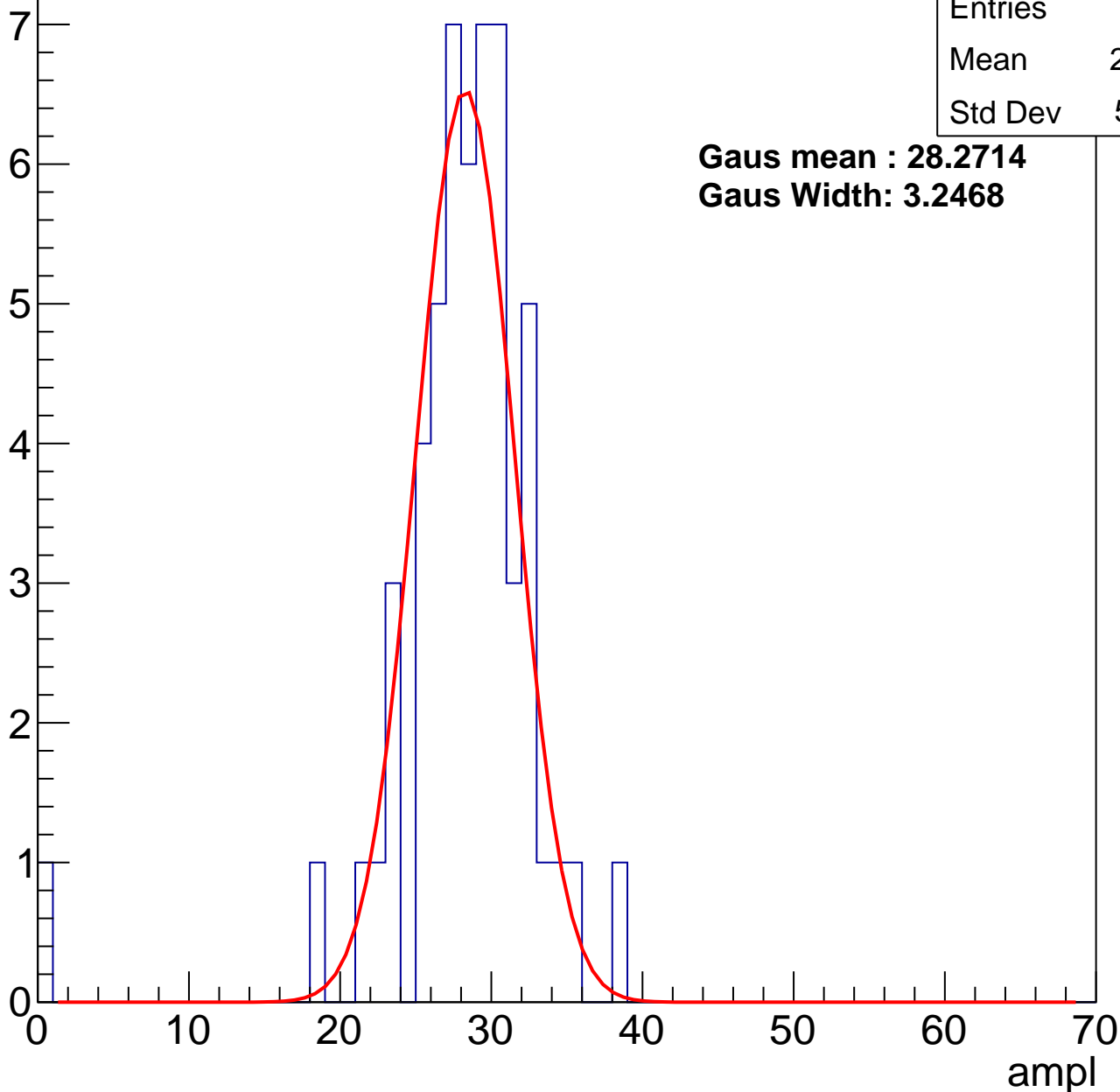
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	27.69
Std Dev	5.141

**Gaus mean : 28.2714**

**Gaus Width: 3.2468**



# B1L000S, U2-ch75, adc1

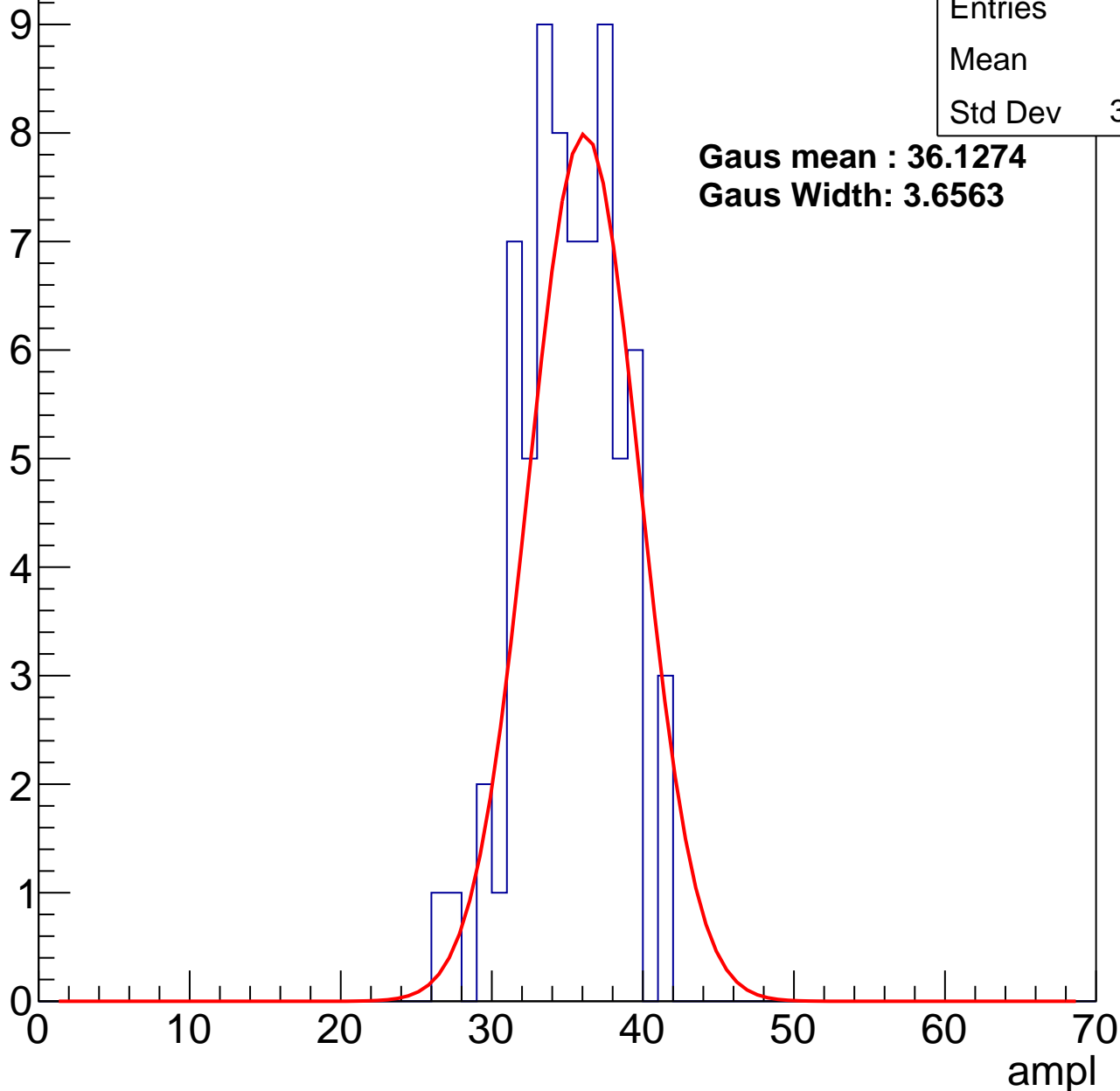
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.7
Std Dev	3.204

**Gaus mean : 36.1274**

**Gaus Width: 3.6563**



# B1L000S, U2-ch75, adc2

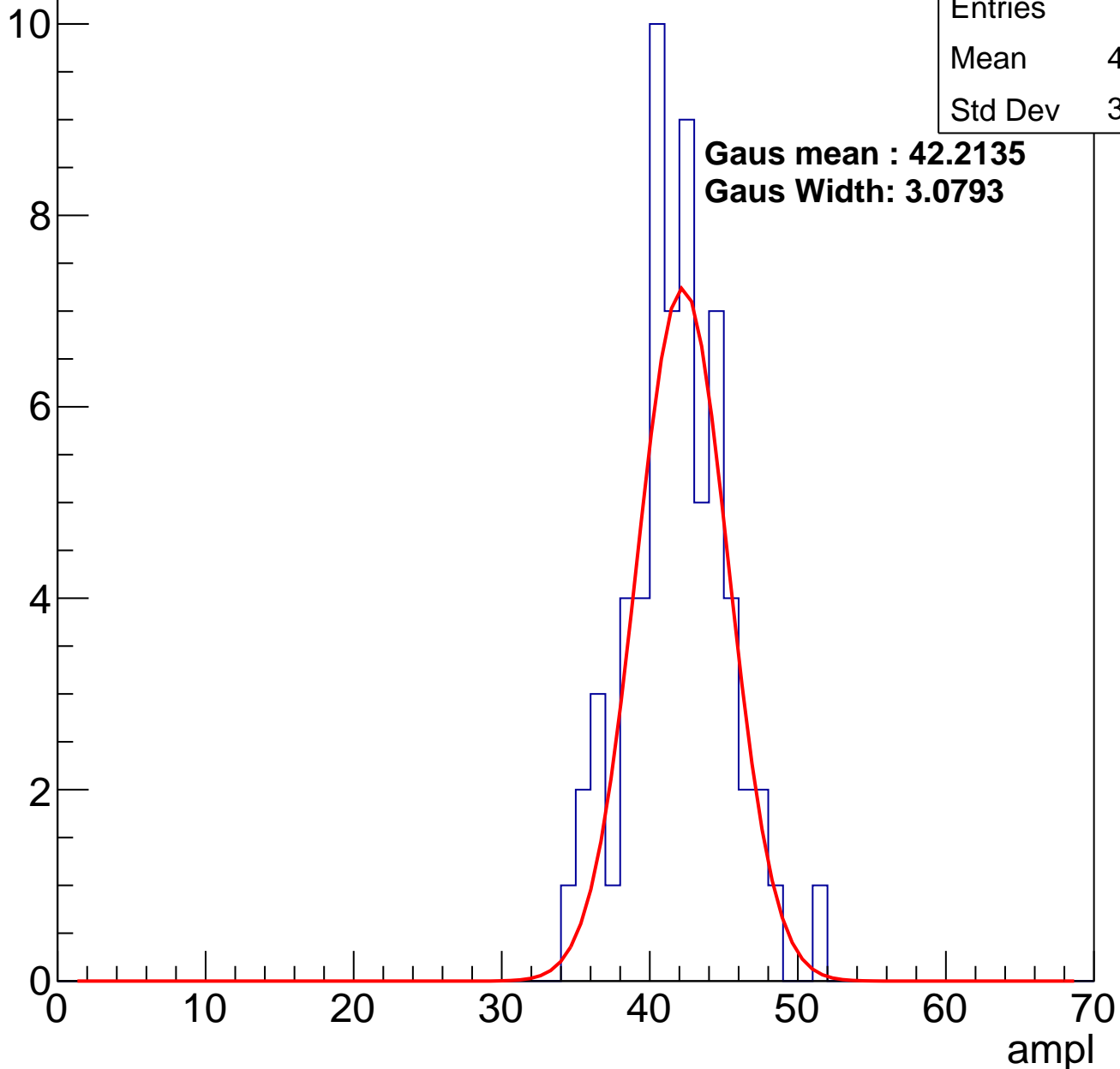
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	41.43
Std Dev	3.308

**Gaus mean : 42.2135**

**Gaus Width: 3.0793**

Entry

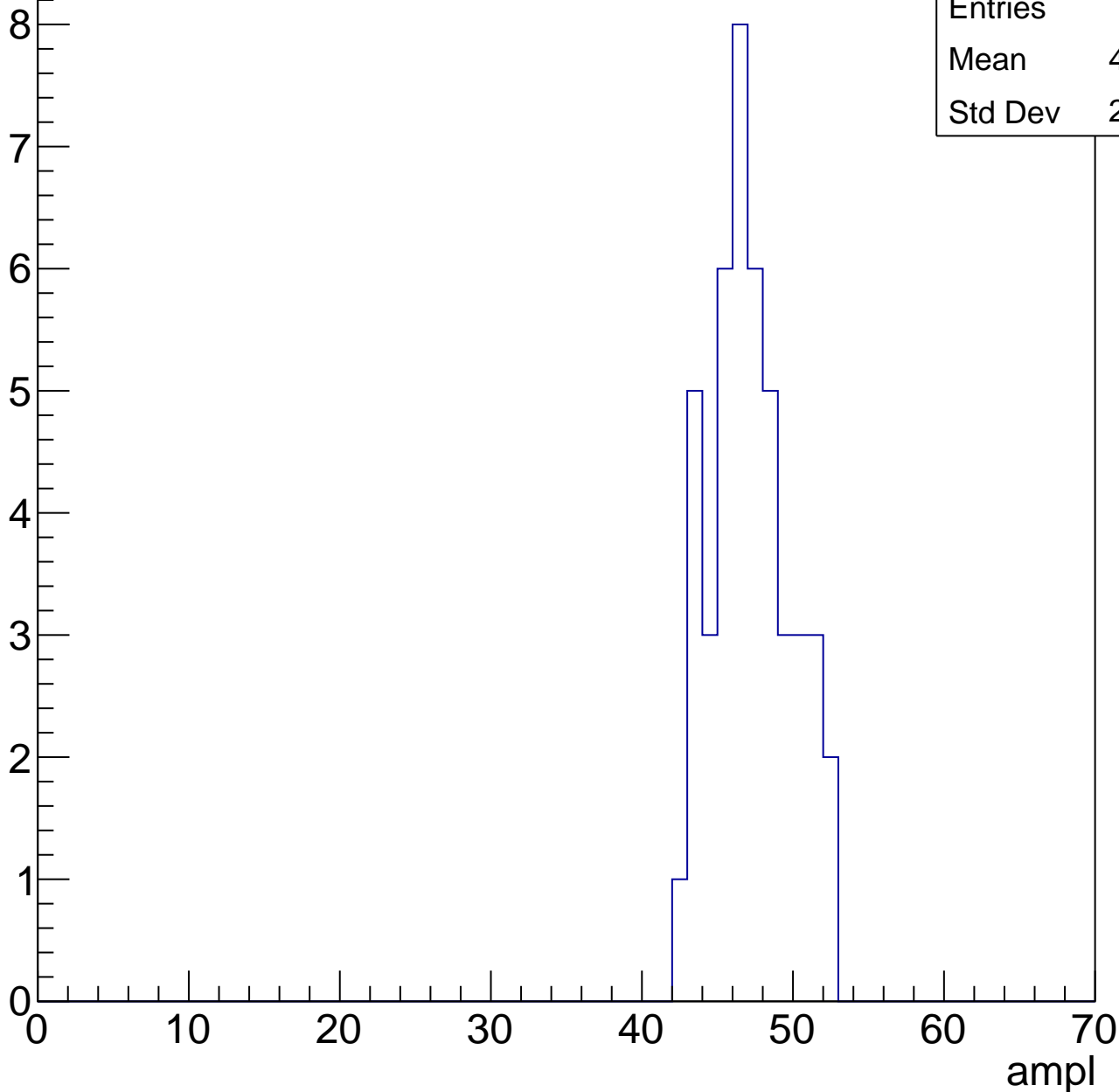


# B1L000S, U2-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

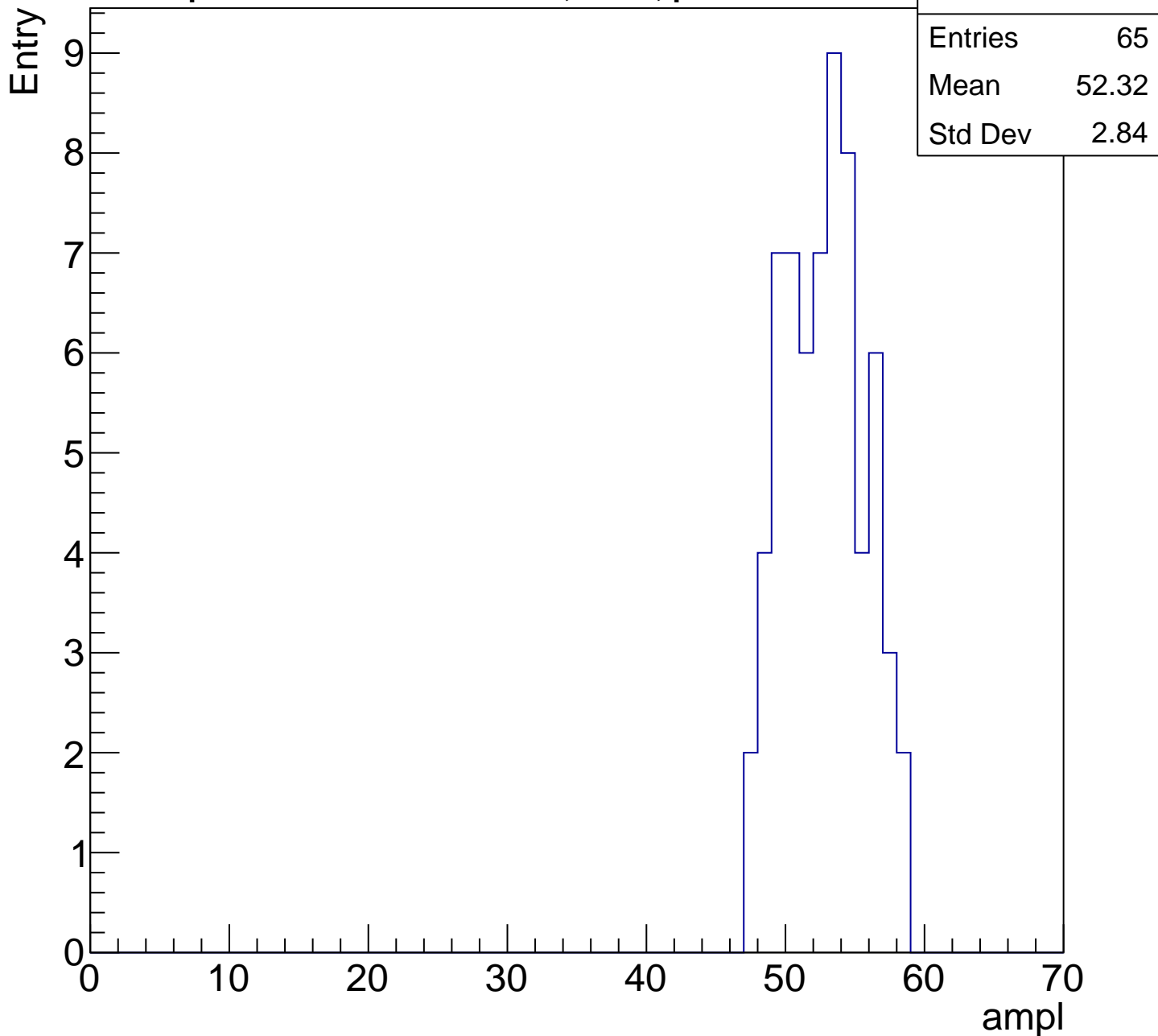
Entry

Entries	45
Mean	46.73
Std Dev	2.594



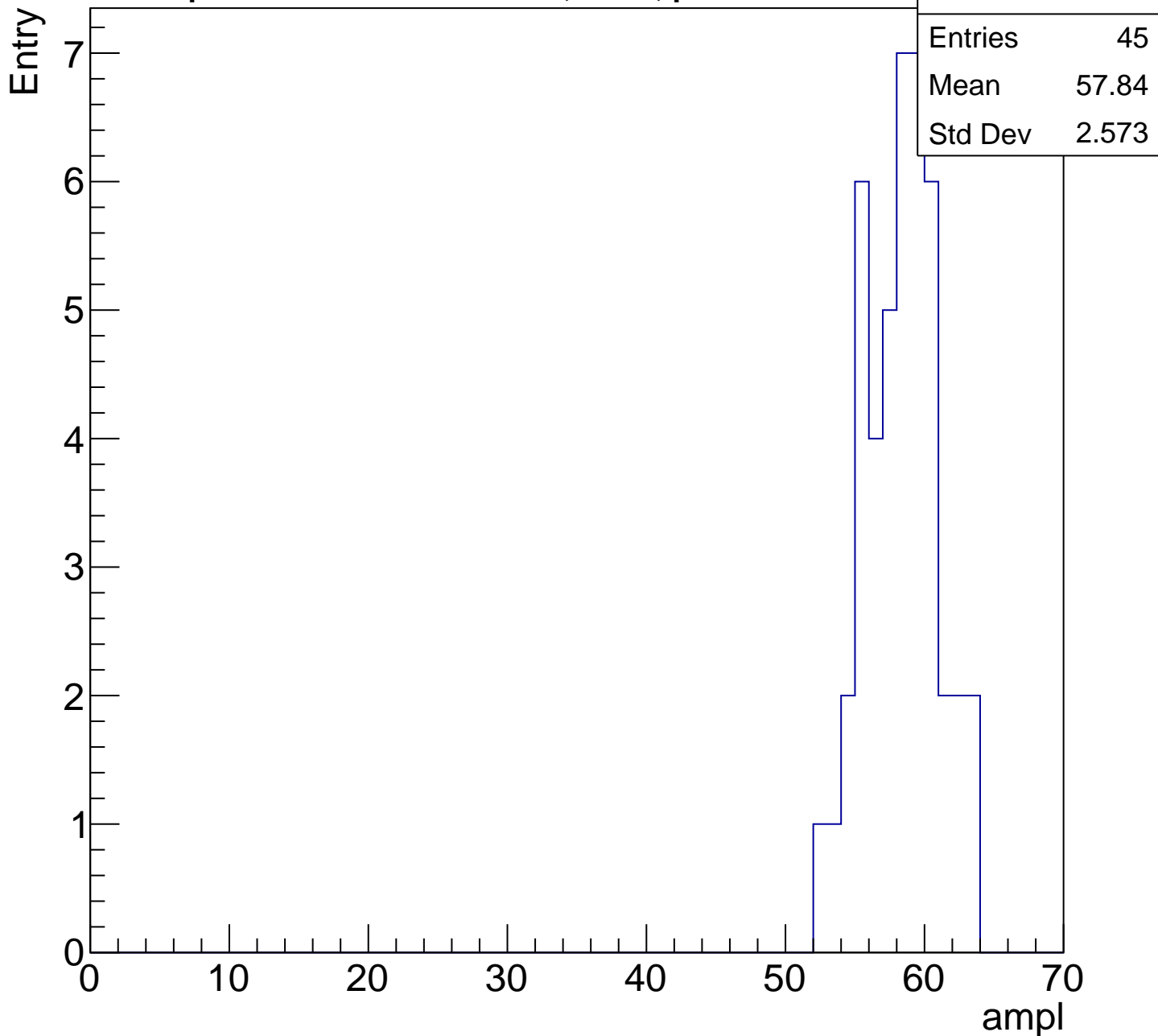
# B1L000S, U2-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

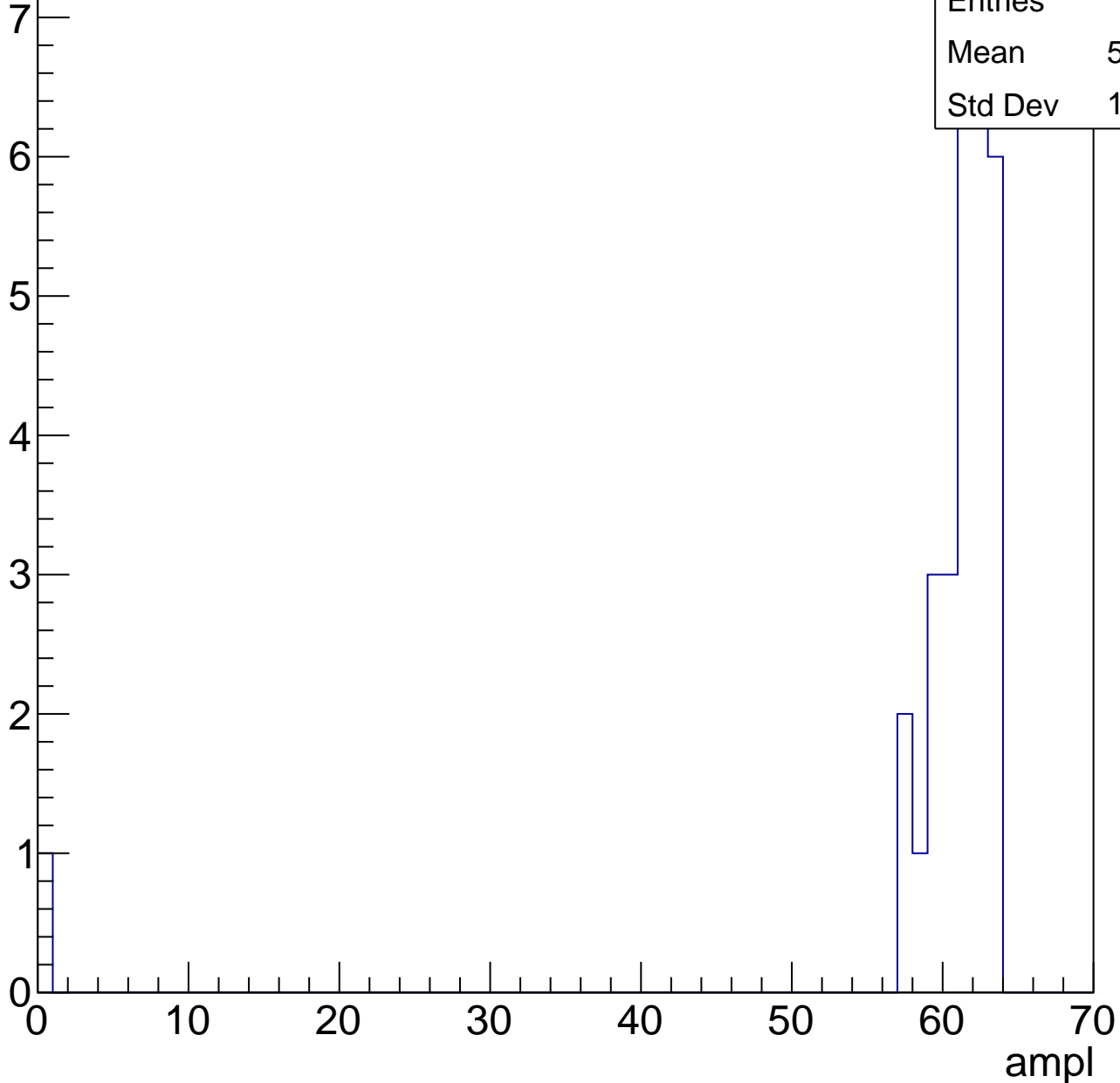


# B1L000S, U2-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	58.93
Std Dev	11.08

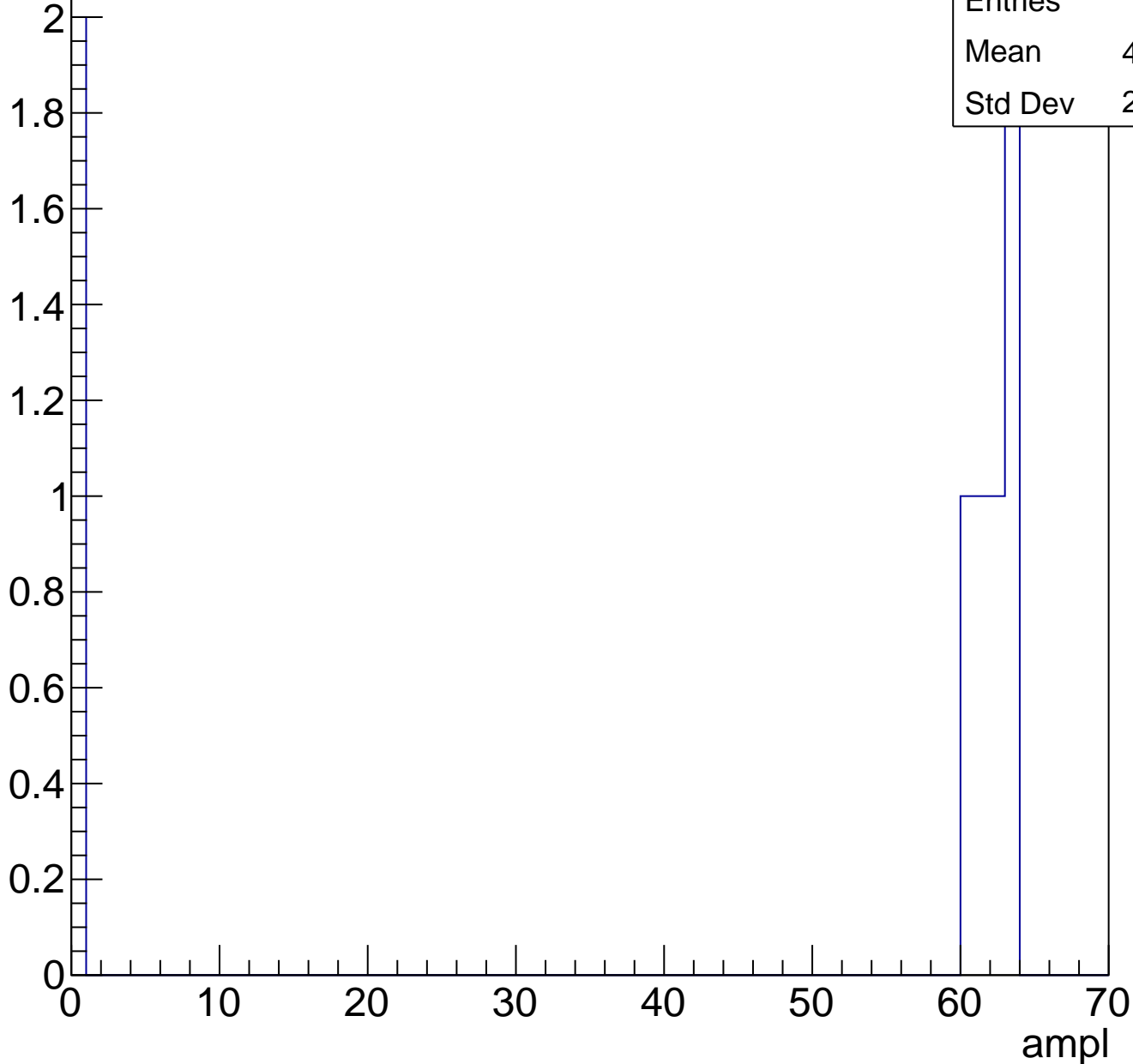




# B1L000S, U2-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch76, adc0

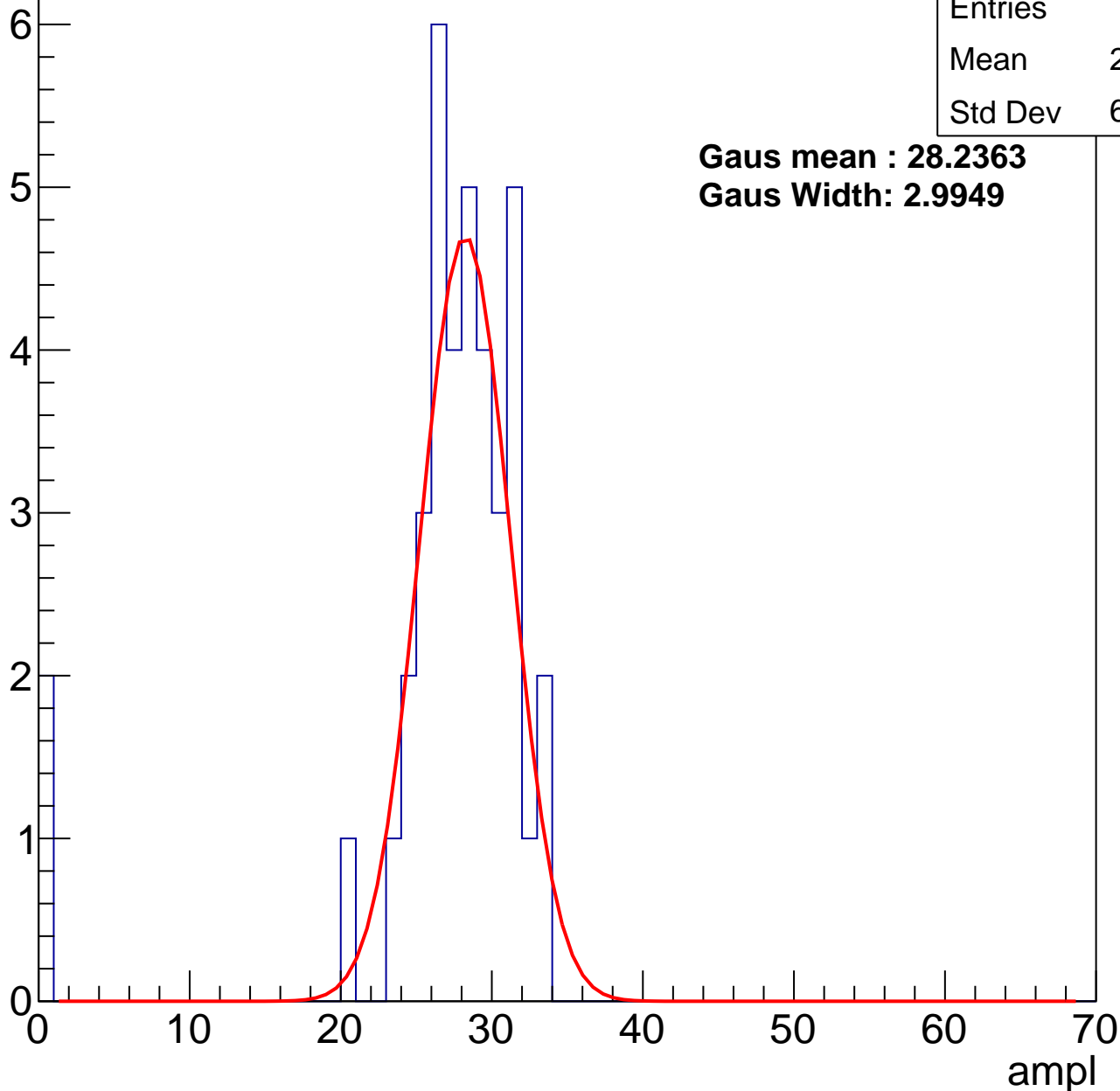
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	26.38
Std Dev	6.735

**Gaus mean : 28.2363**

**Gaus Width: 2.9949**



# B1L000S, U2-ch76, adc1

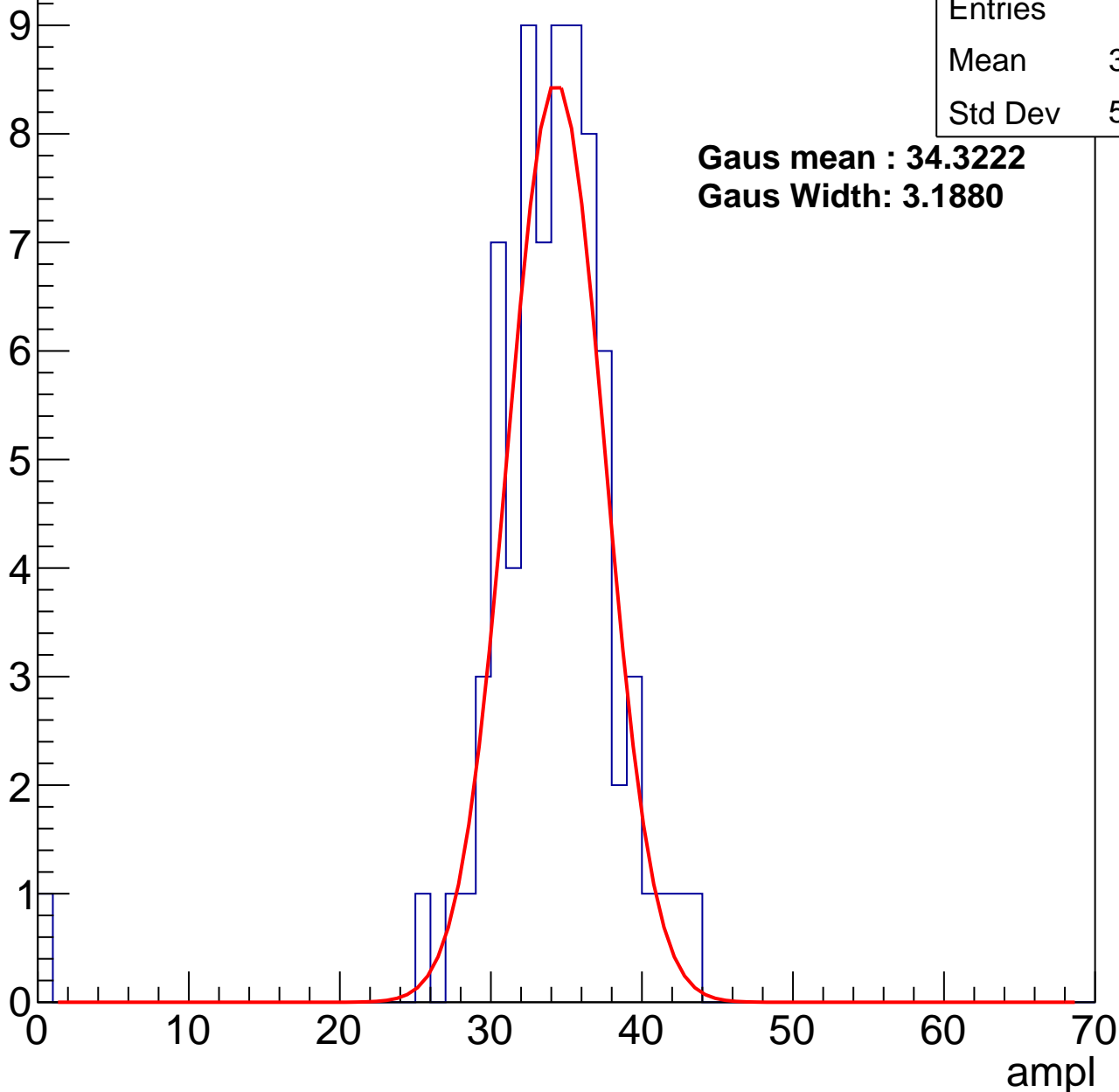
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	33.47
Std Dev	5.162

**Gaus mean : 34.3222**

**Gaus Width: 3.1880**



# B1L000S, U2-ch76, adc2

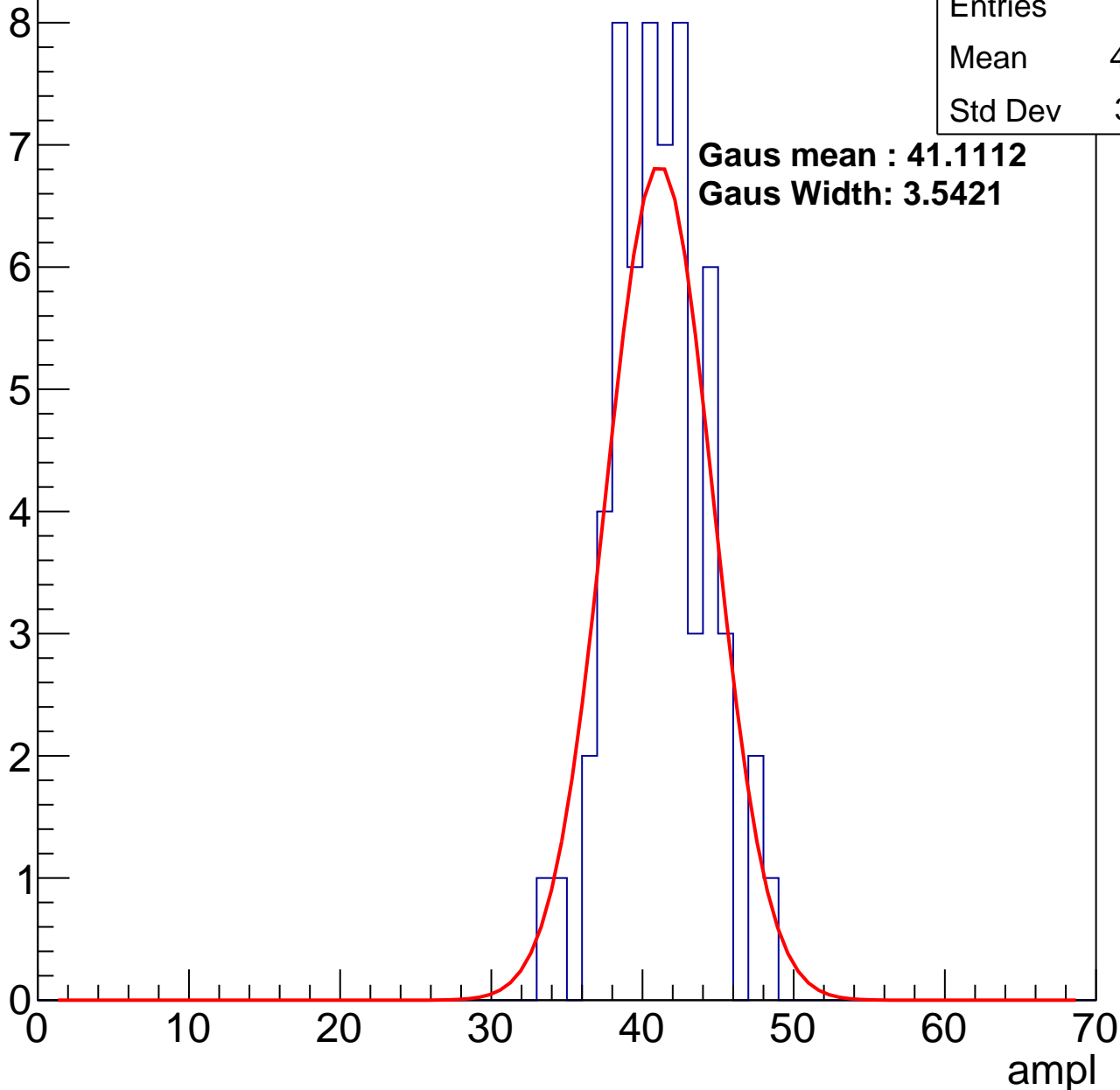
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	40.63
Std Dev	3.071

**Gaus mean : 41.1112**

**Gaus Width: 3.5421**



# B1L000S, U2-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

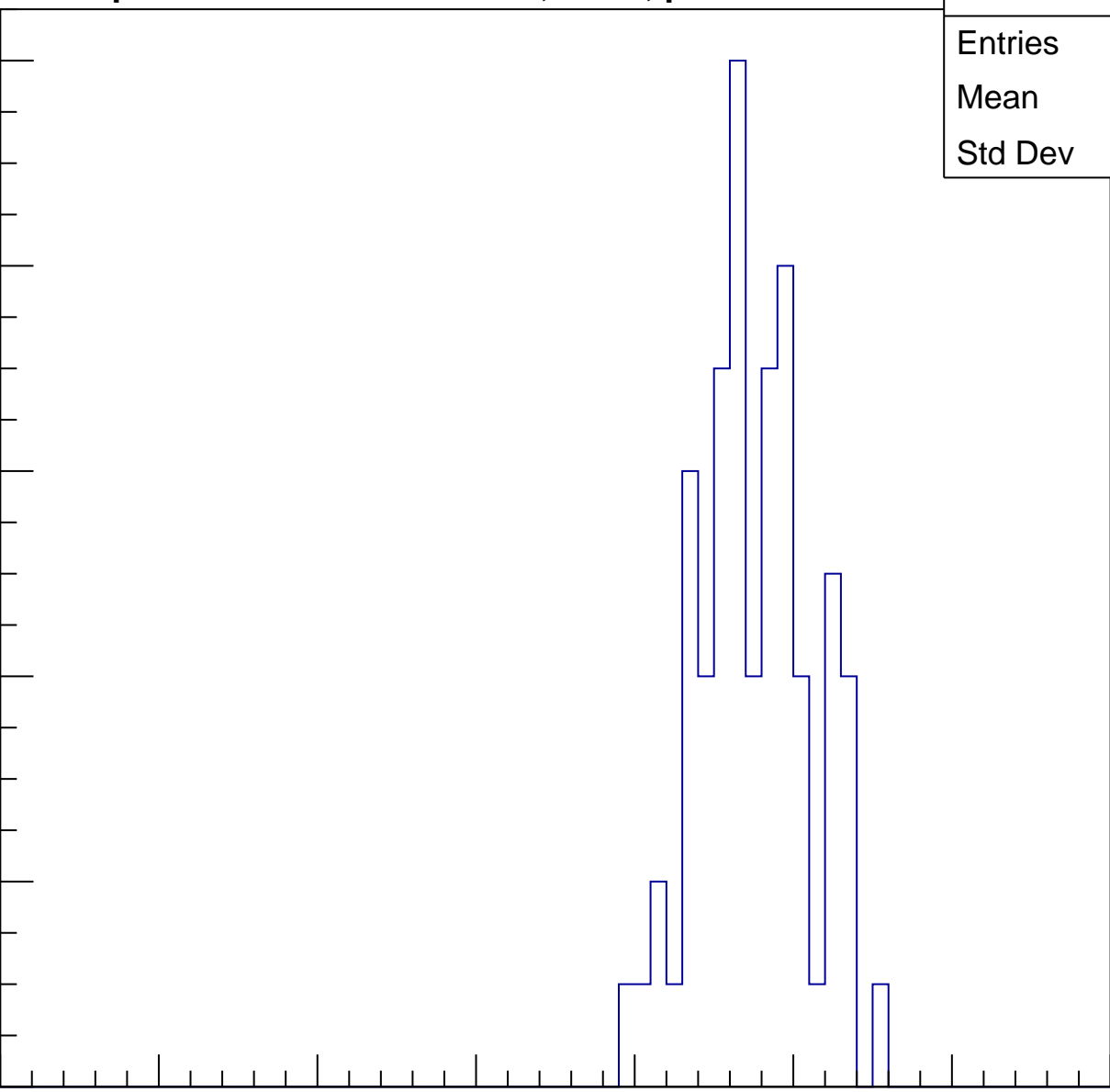
Entries	66
Mean	47.06
Std Dev	3.49

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

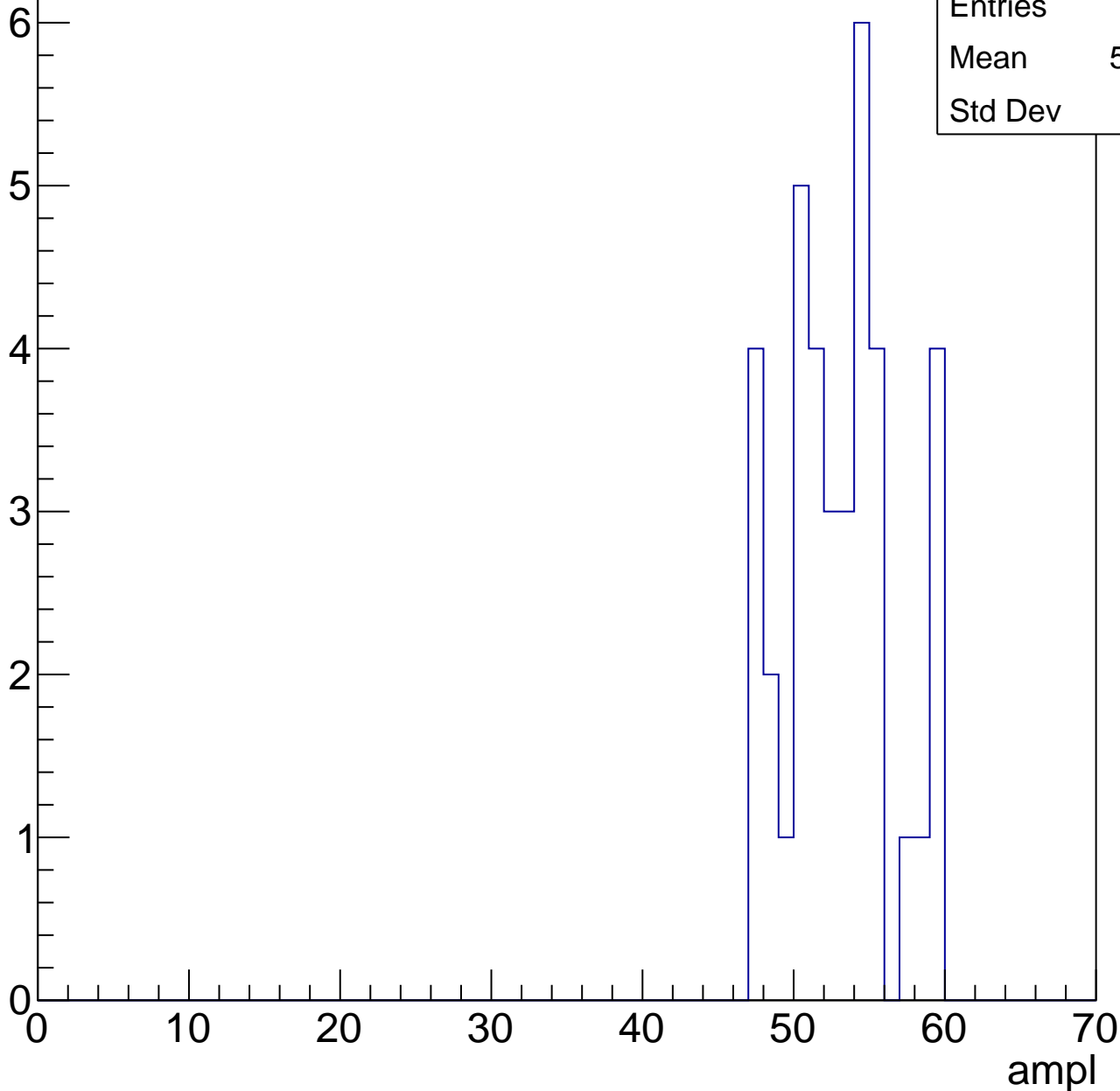


# B1L000S, U2-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	38
Mean	52.55
Std Dev	3.53

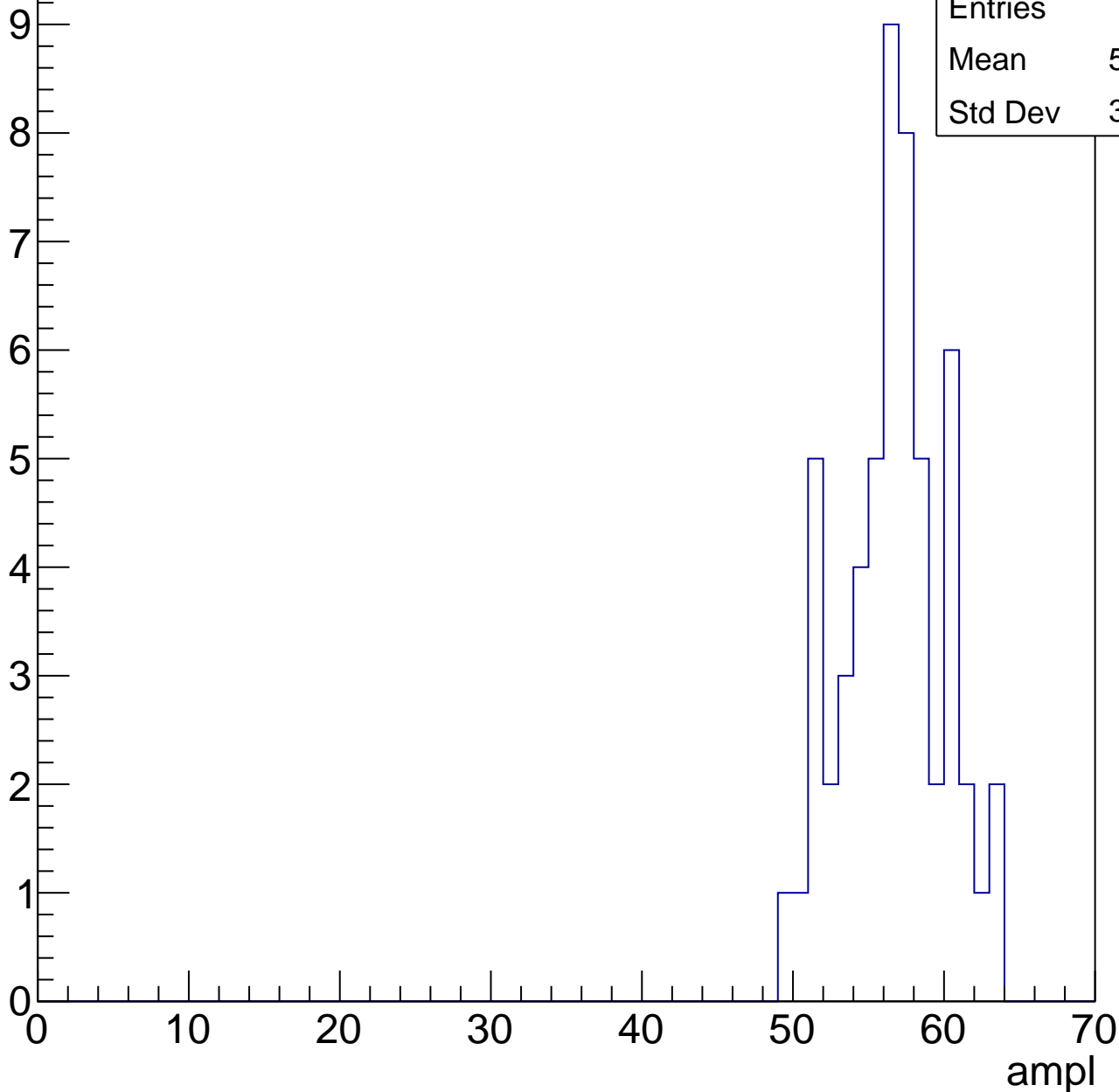


# B1L000S, U2-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	56.18
Std Dev	3.312

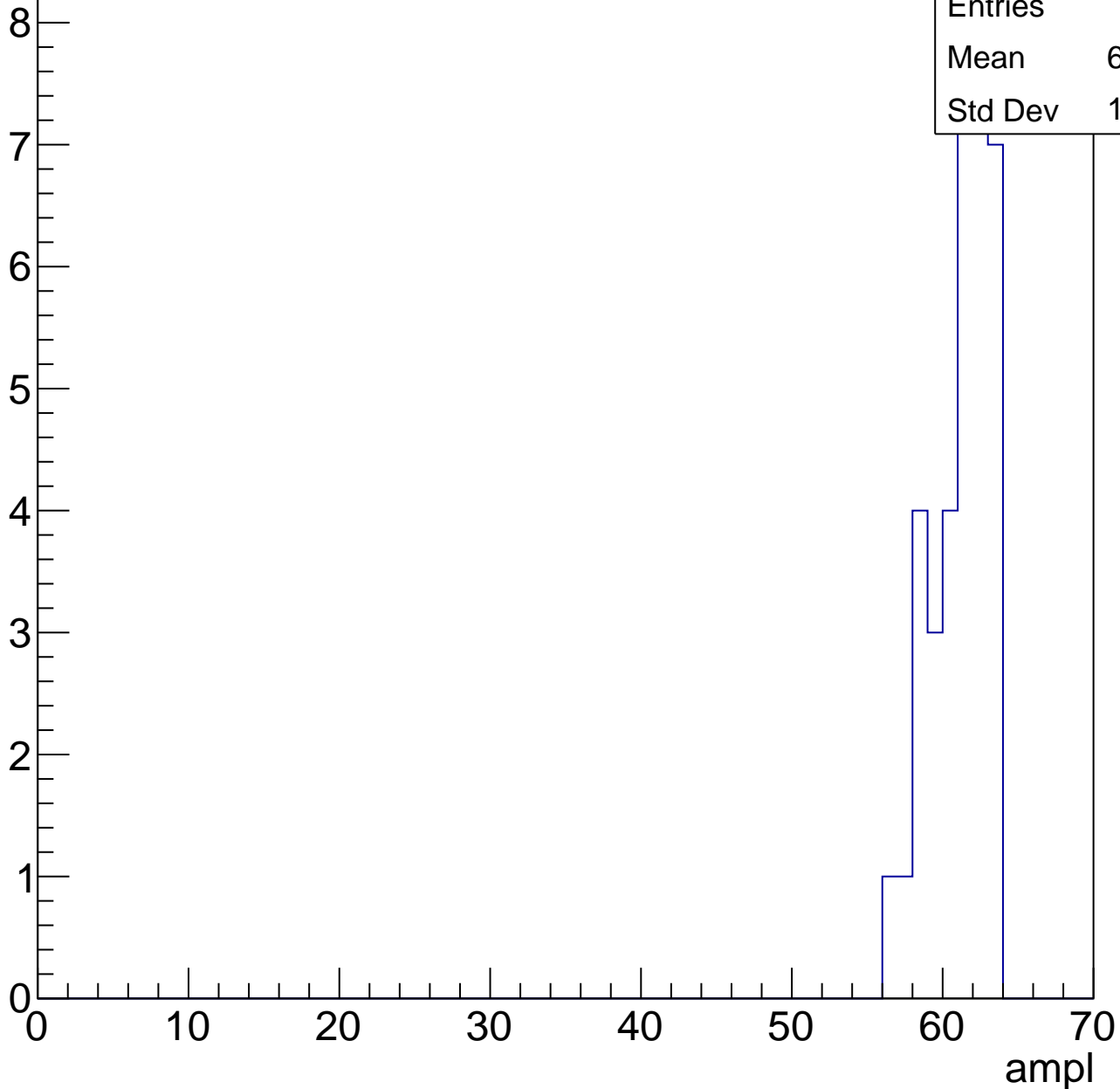


# B1L000S, U2-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	36
Mean	60.75
Std Dev	1.876

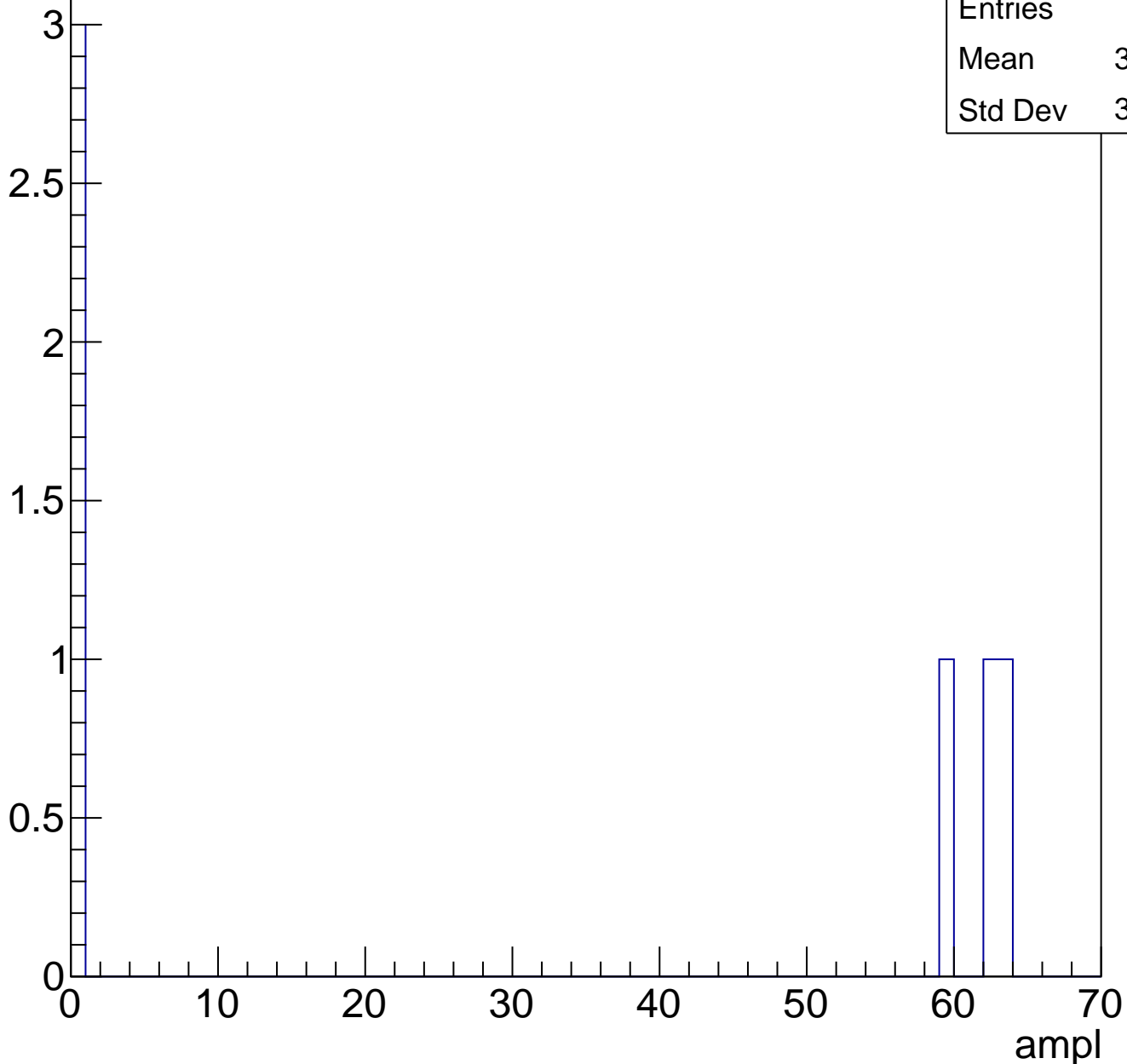




# B1L000S, U2-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch77, adc0

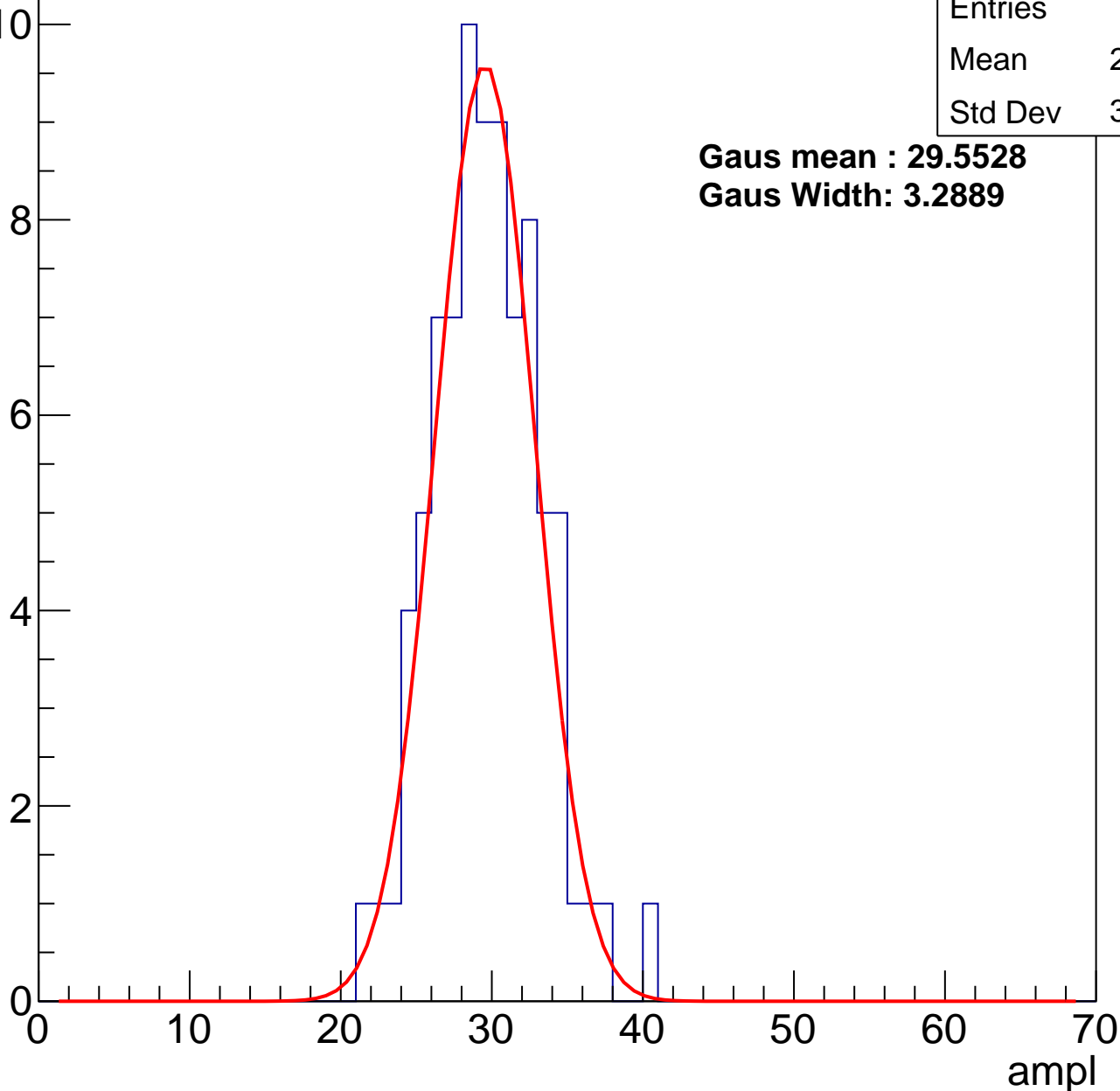
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	83
Mean	29.22
Std Dev	3.489

**Gaus mean : 29.5528**

**Gaus Width: 3.2889**



# B1L000S, U2-ch77, adc1

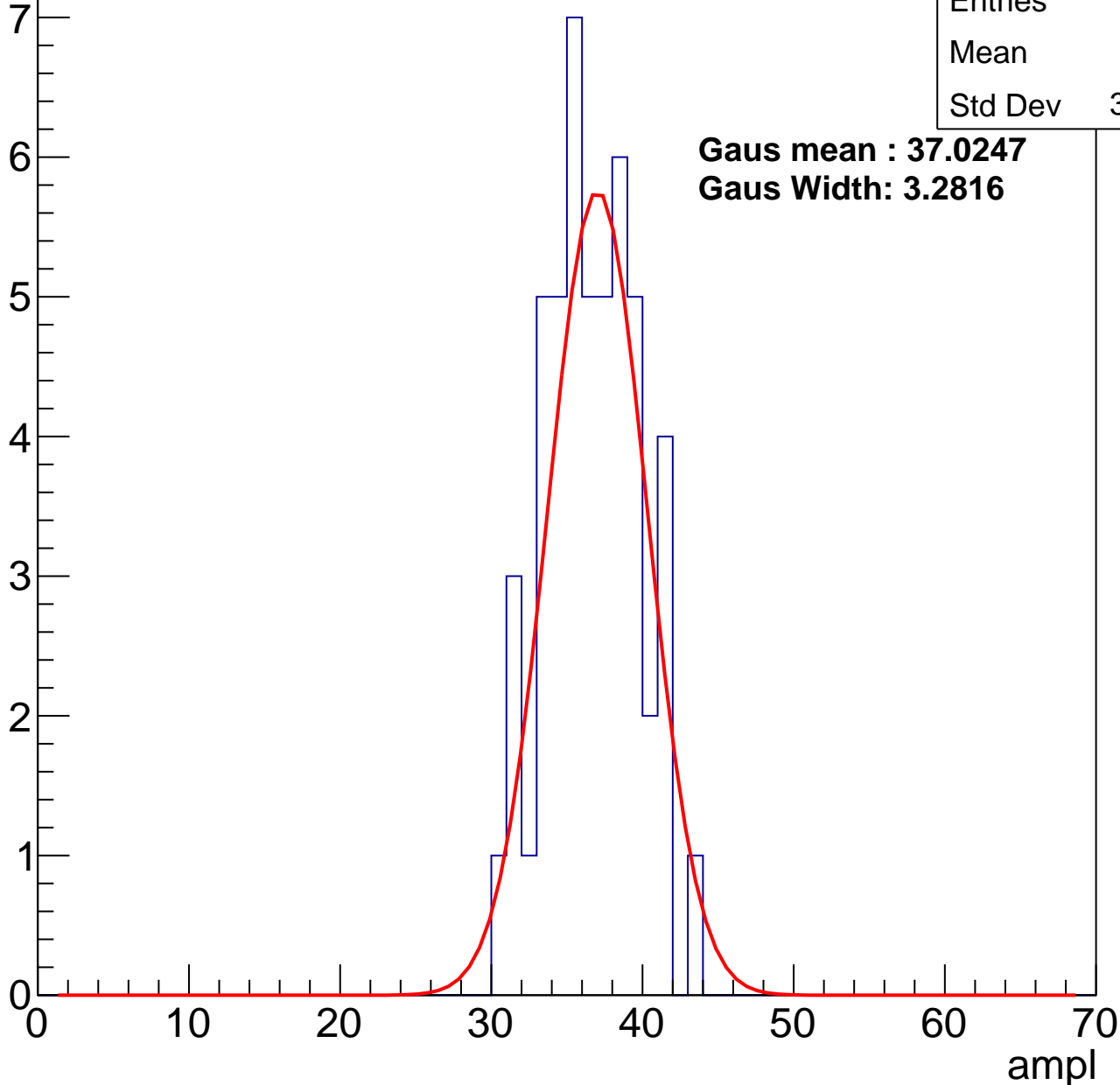
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	36.2
Std Dev	3.007

**Gaus mean : 37.0247**

**Gaus Width: 3.2816**



# B1L000S, U2-ch77, adc2

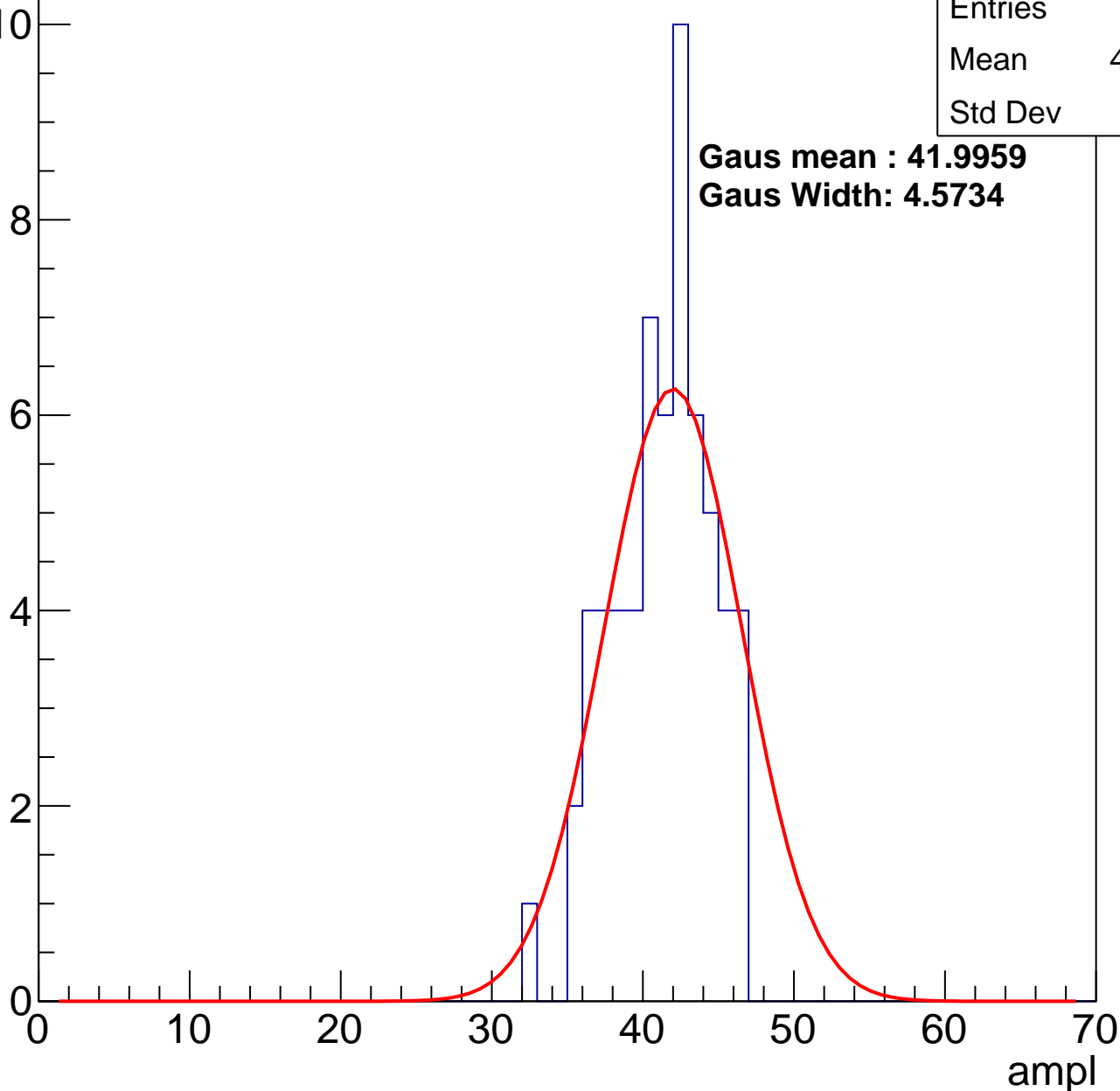
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	40.82
Std Dev	3.18

**Gaus mean : 41.9959**

**Gaus Width: 4.5734**

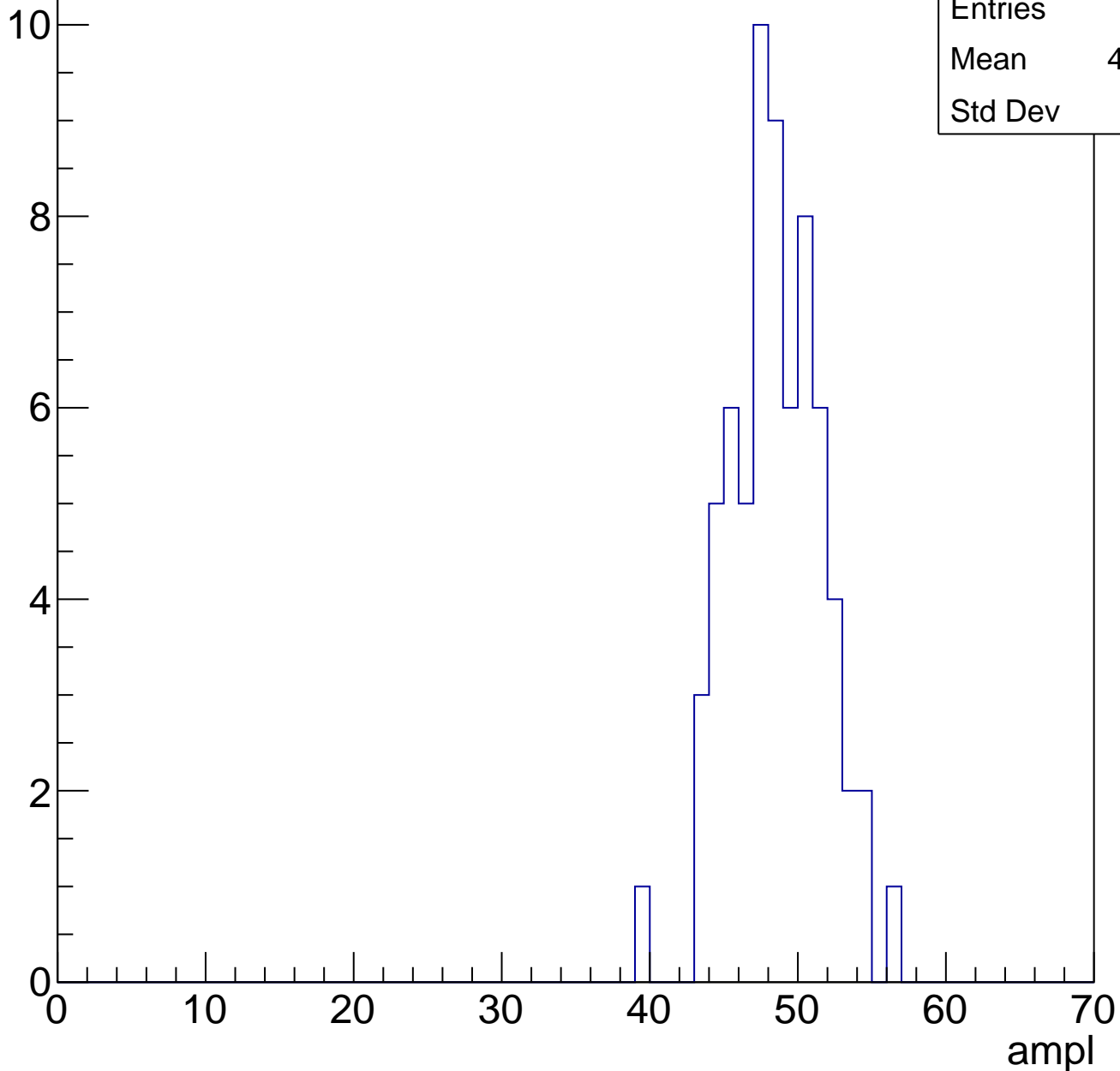


# B1L000S, U2-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

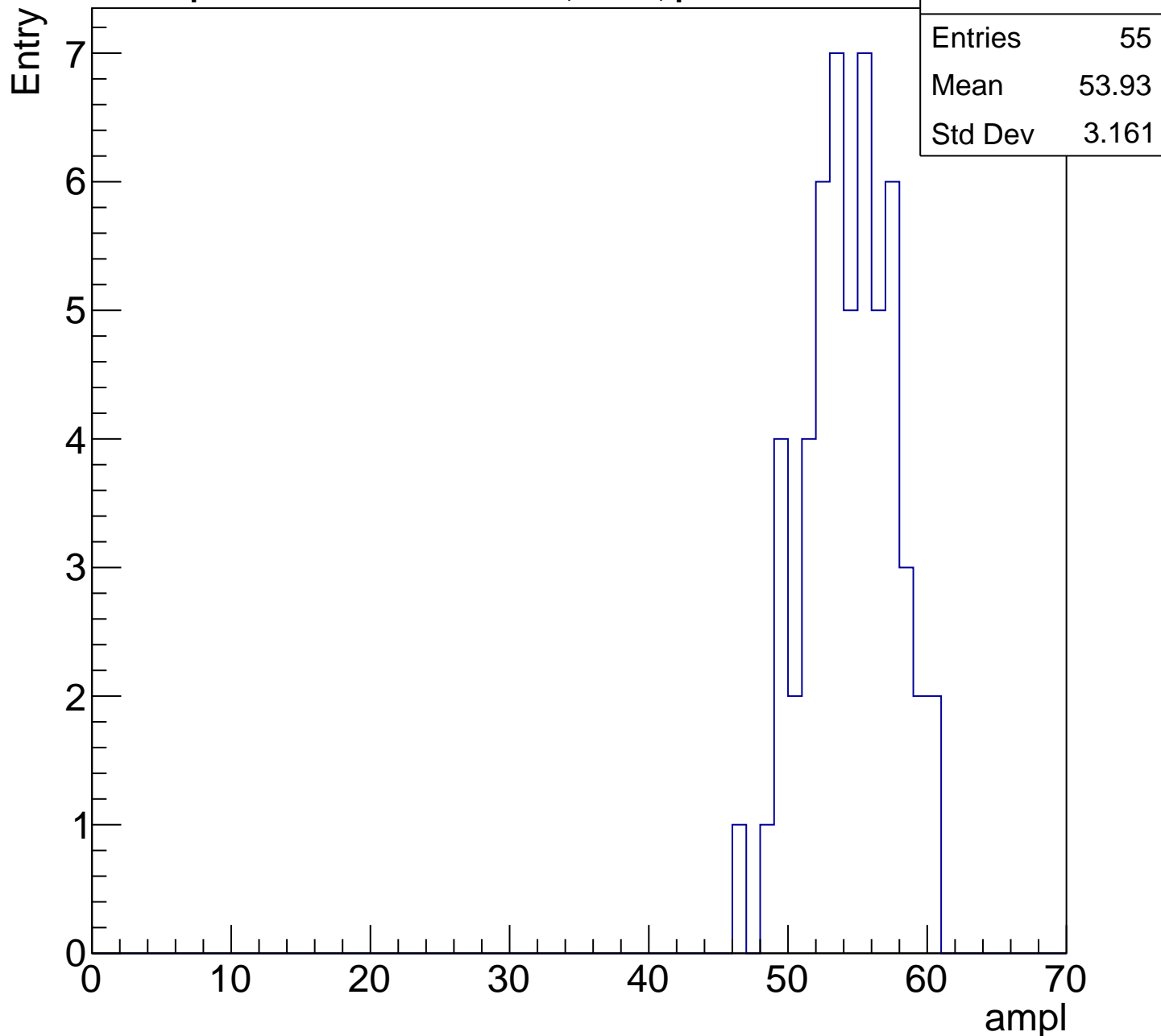
Entries	68
Mean	48.06
Std Dev	3.12

Entry



# B1L000S, U2-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

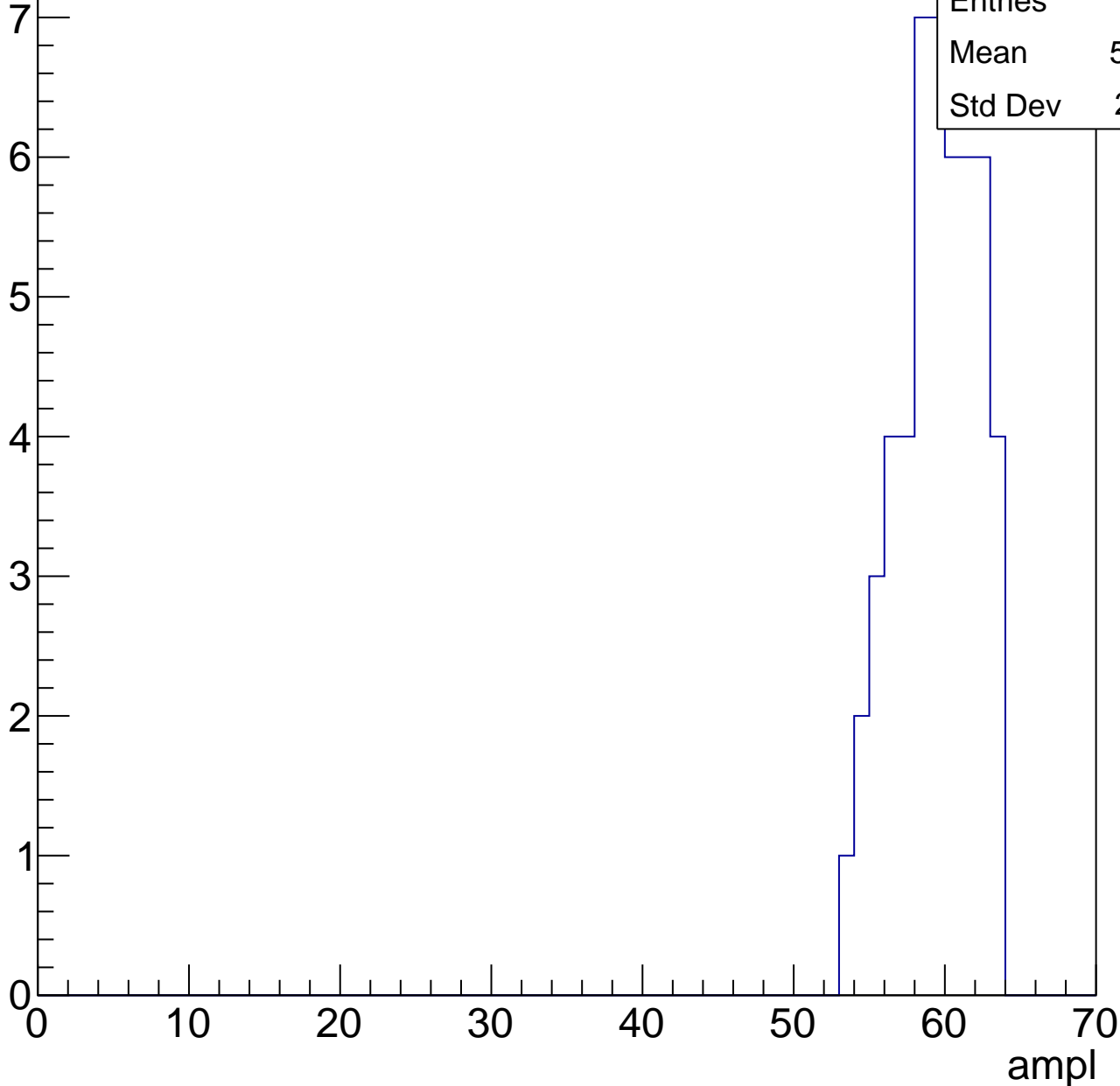


# B1L000S, U2-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.94
Std Dev	2.611

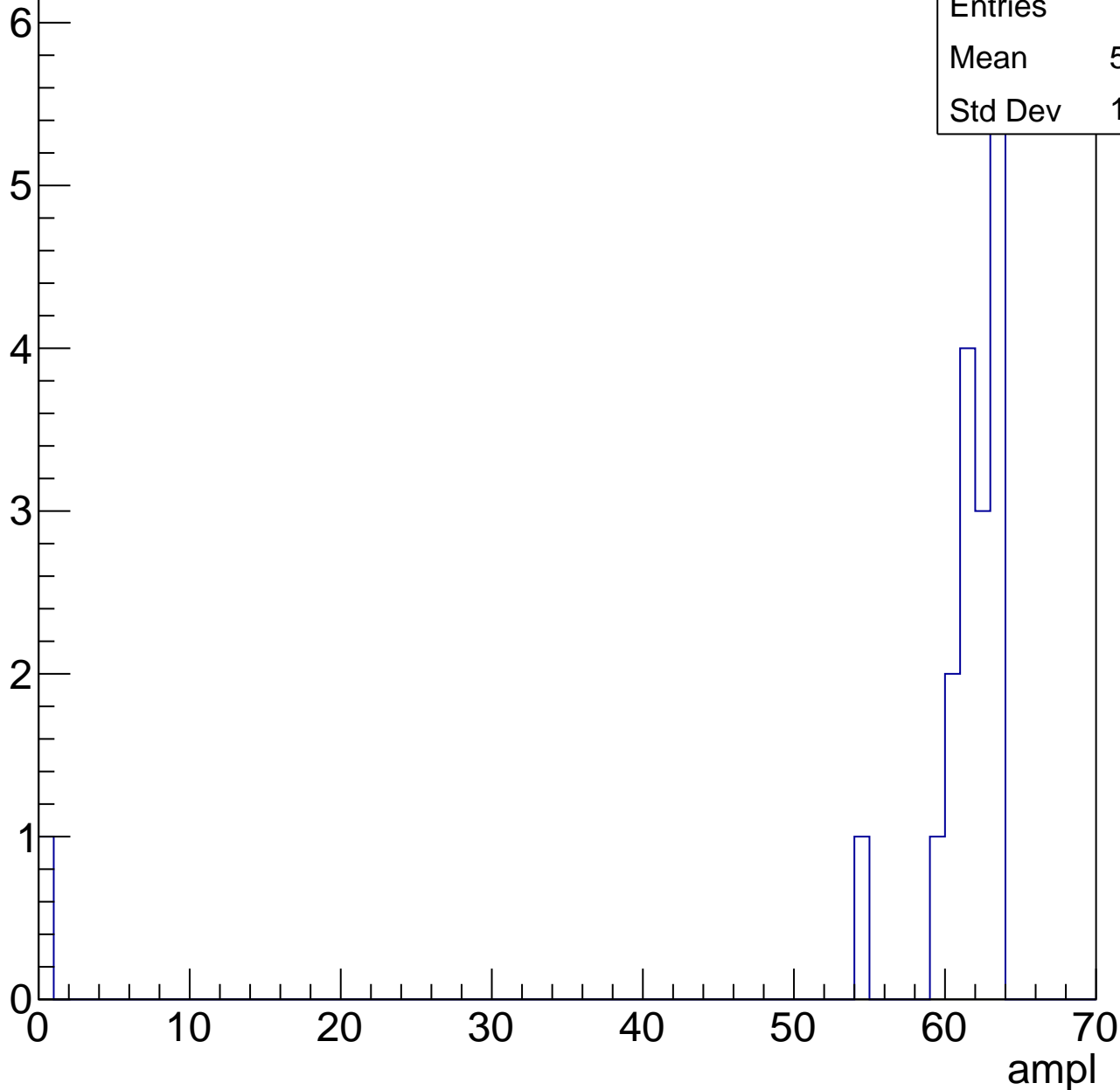


# B1L000S, U2-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	57.83
Std Dev	14.19





# B1L000S, U2-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch78, adc0

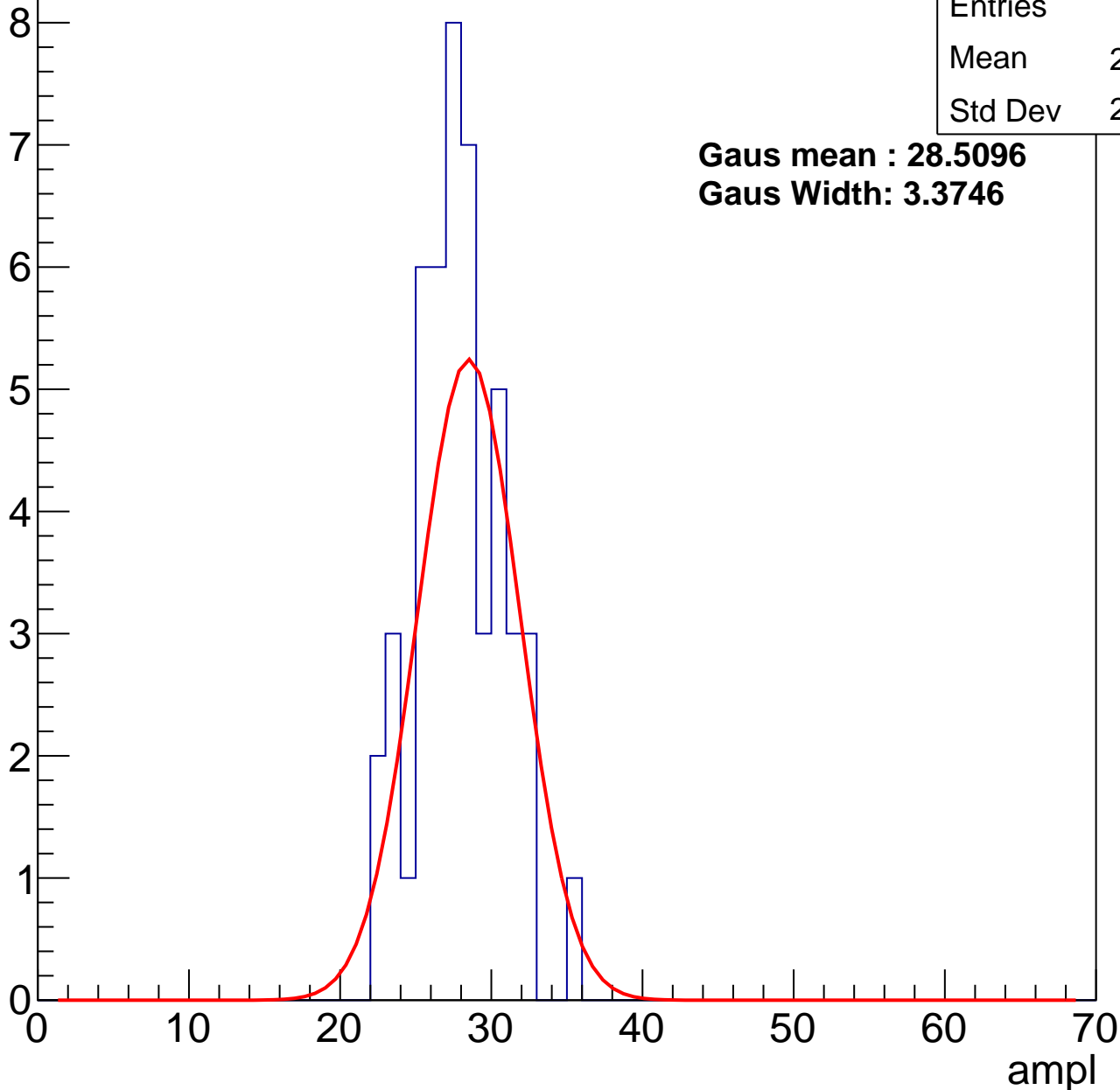
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	27.42
Std Dev	2.812

**Gaus mean : 28.5096**

**Gaus Width: 3.3746**



# B1L000S, U2-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	32.82
Std Dev	3.529

**Gaus mean : 33.3860**

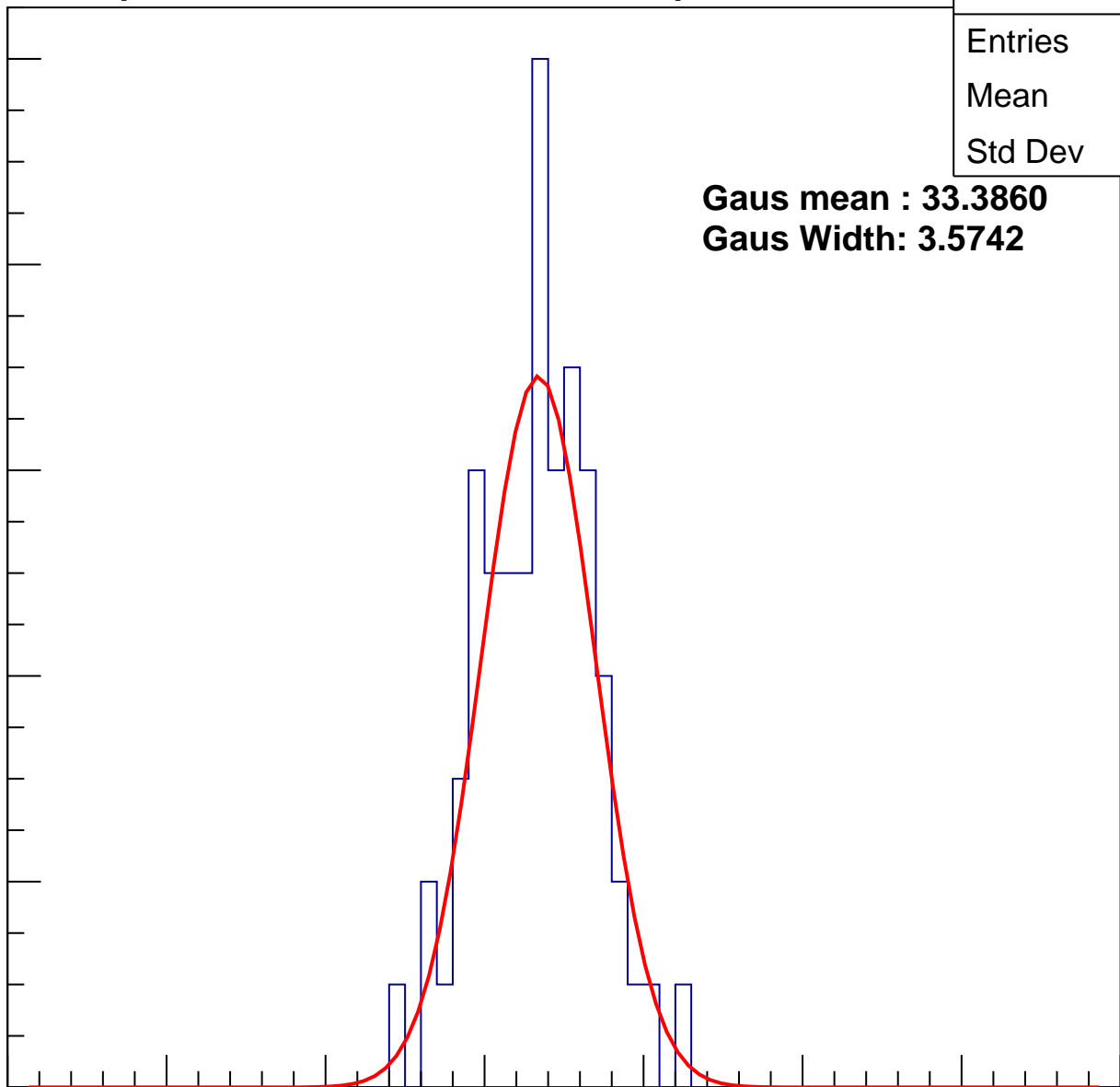
**Gaus Width: 3.5742**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch78, adc2

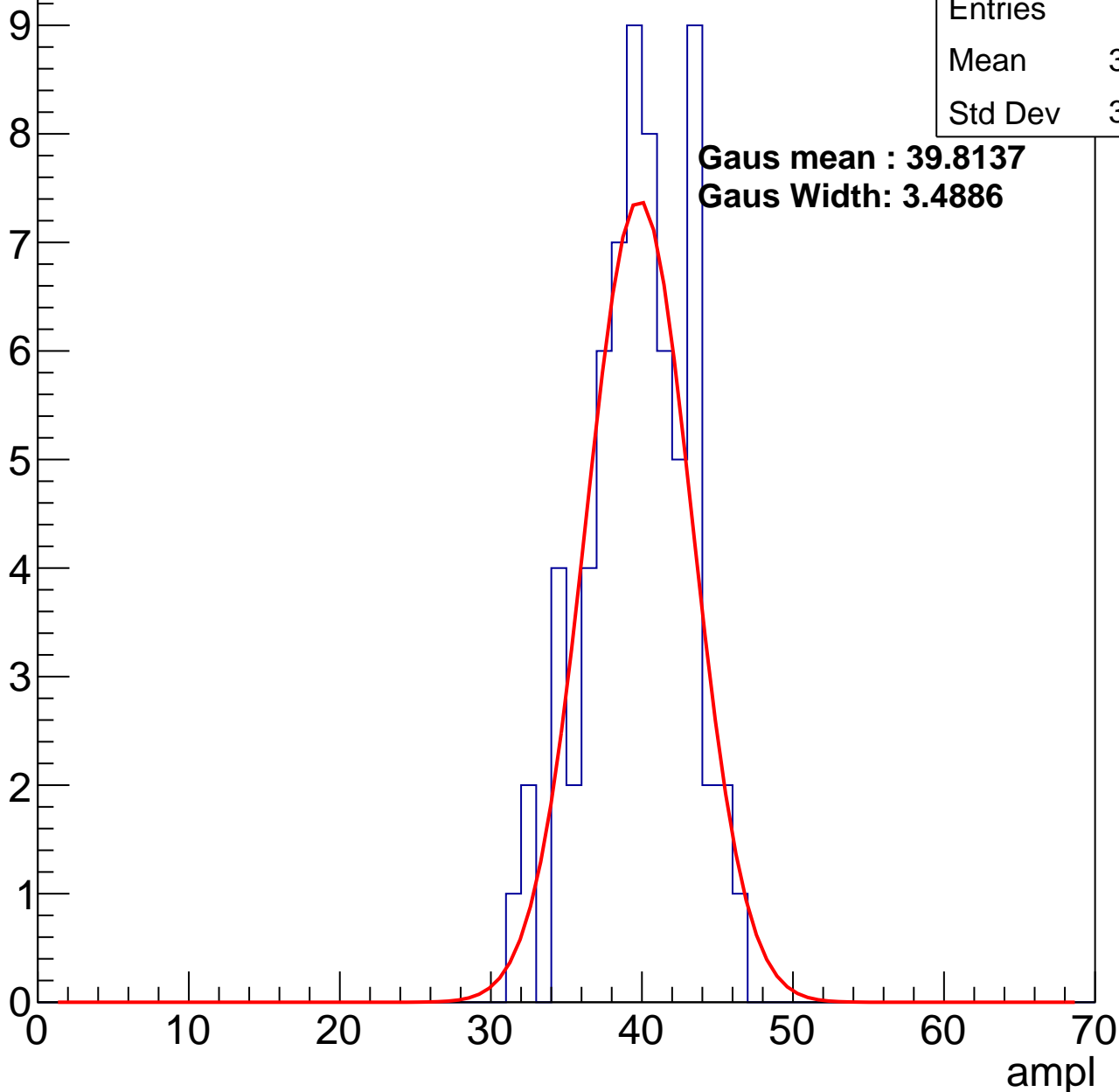
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	39.28
Std Dev	3.316

**Gaus mean : 39.8137**

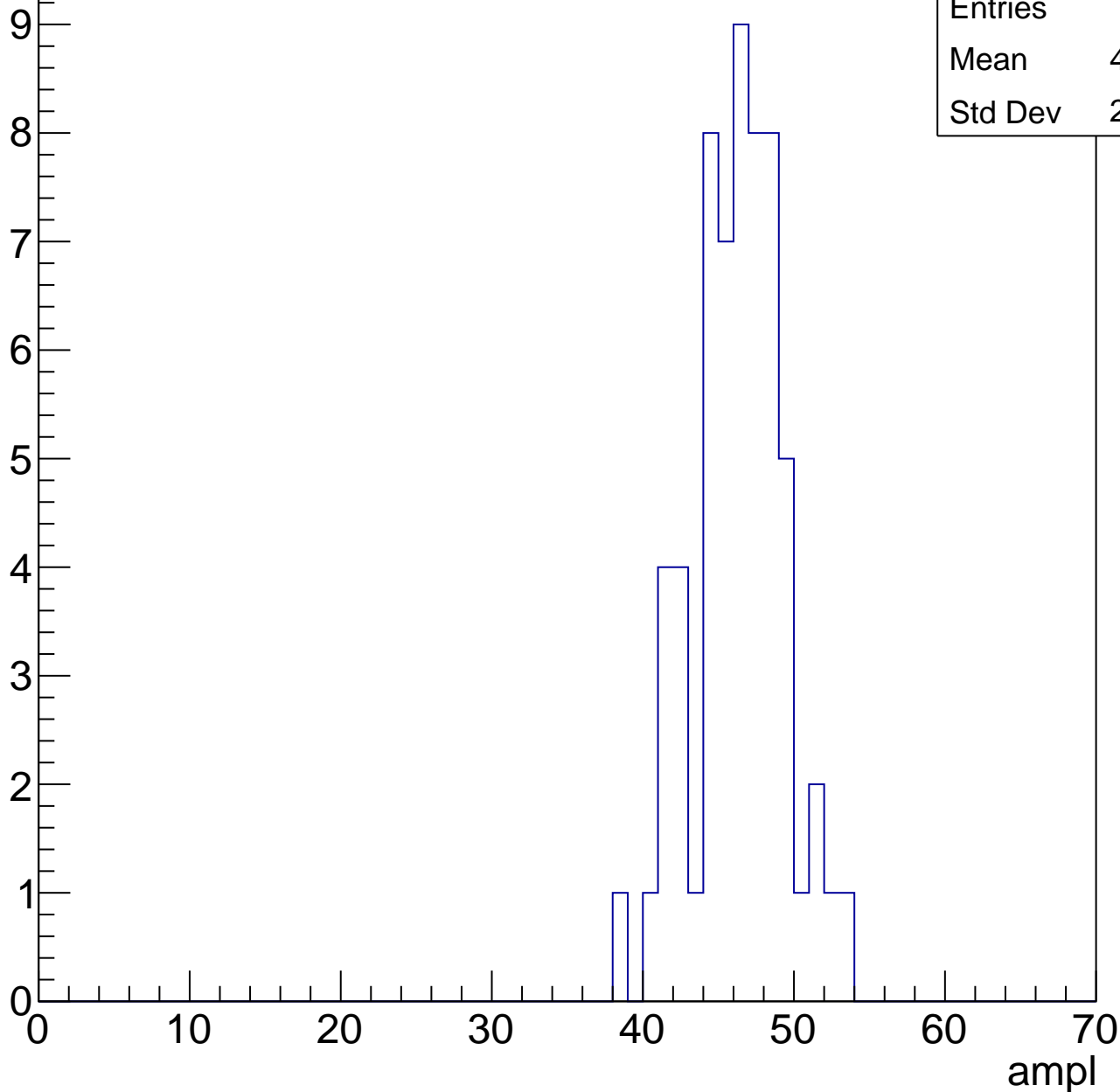
**Gaus Width: 3.4886**



# B1L000S, U2-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

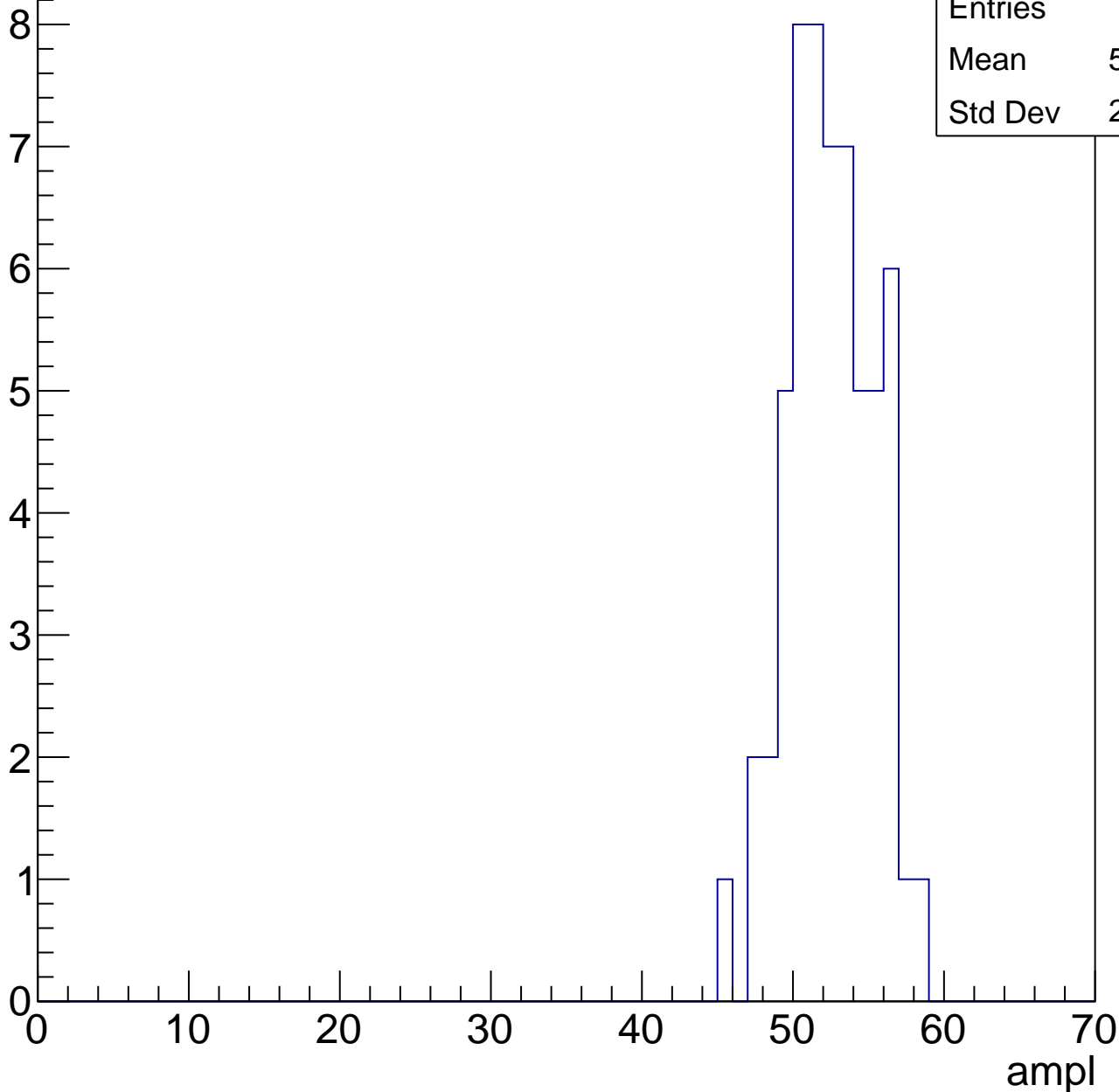


# B1L000S, U2-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

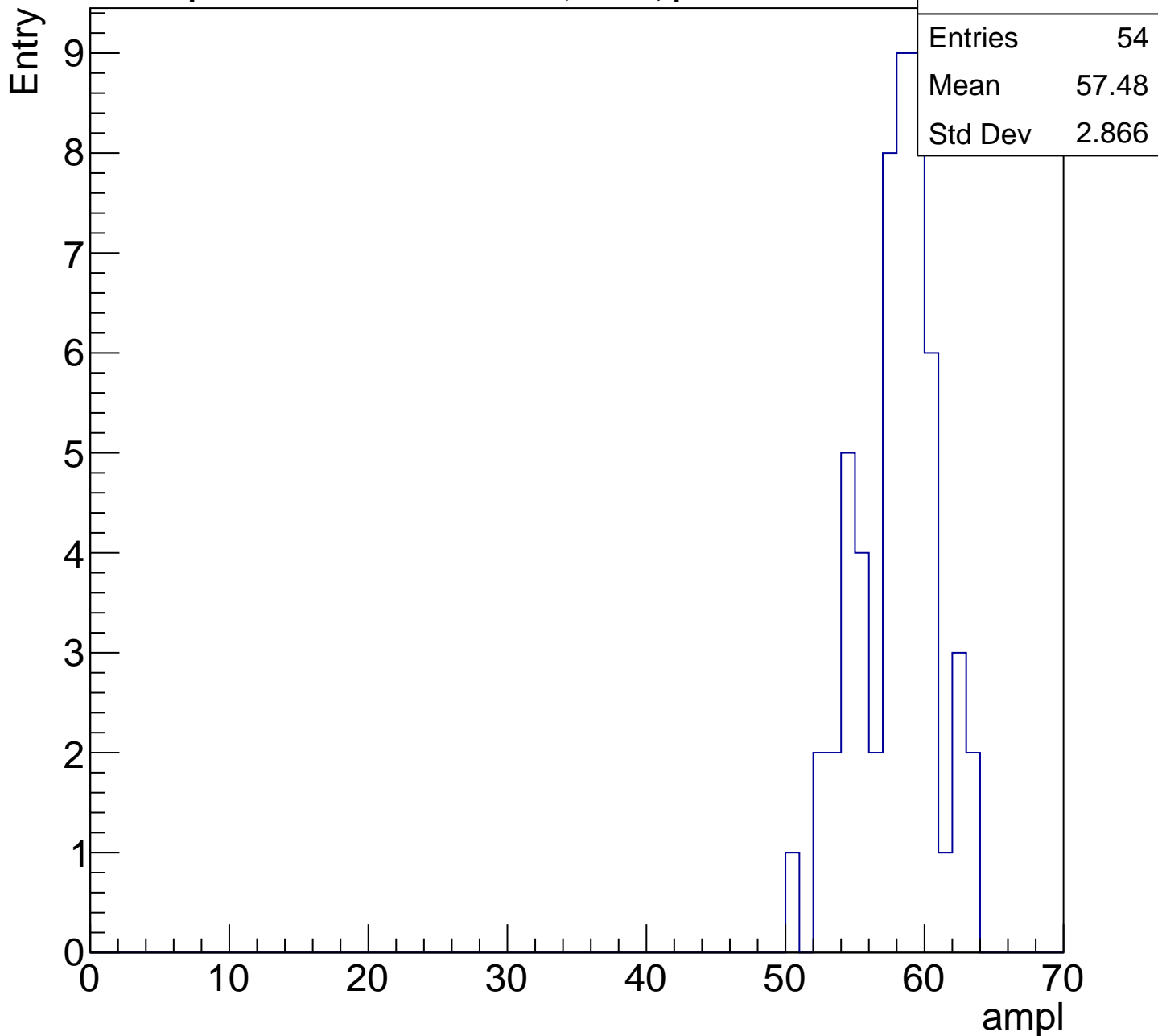
Entry

Entries	58
Mean	52.05
Std Dev	2.769



# B1L000S, U2-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

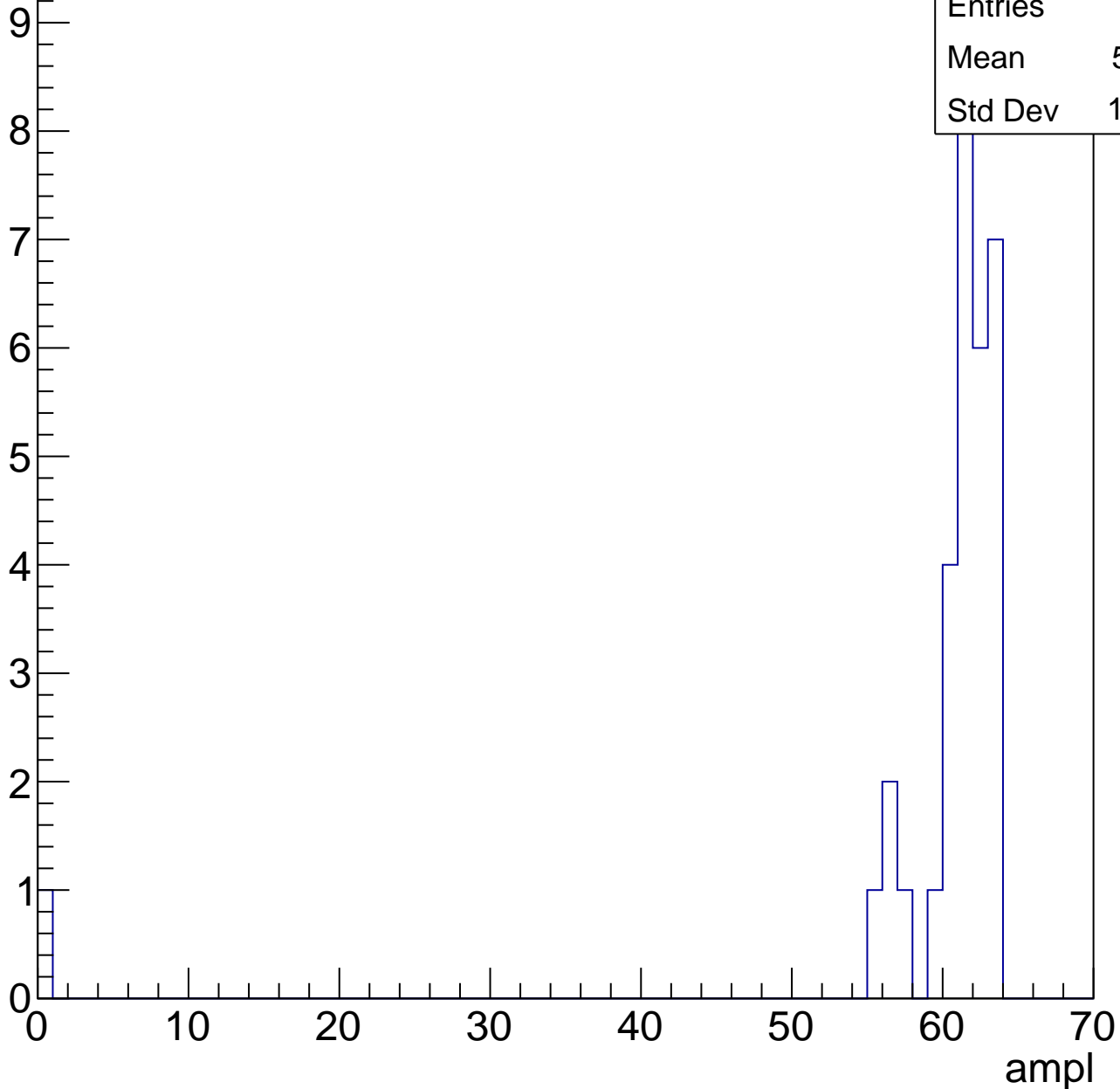


# B1L000S, U2-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	58.91
Std Dev	10.79





# B1L000S, U2-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch79, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	28.25
Std Dev	4.874

**Gaus mean : 29.5153**

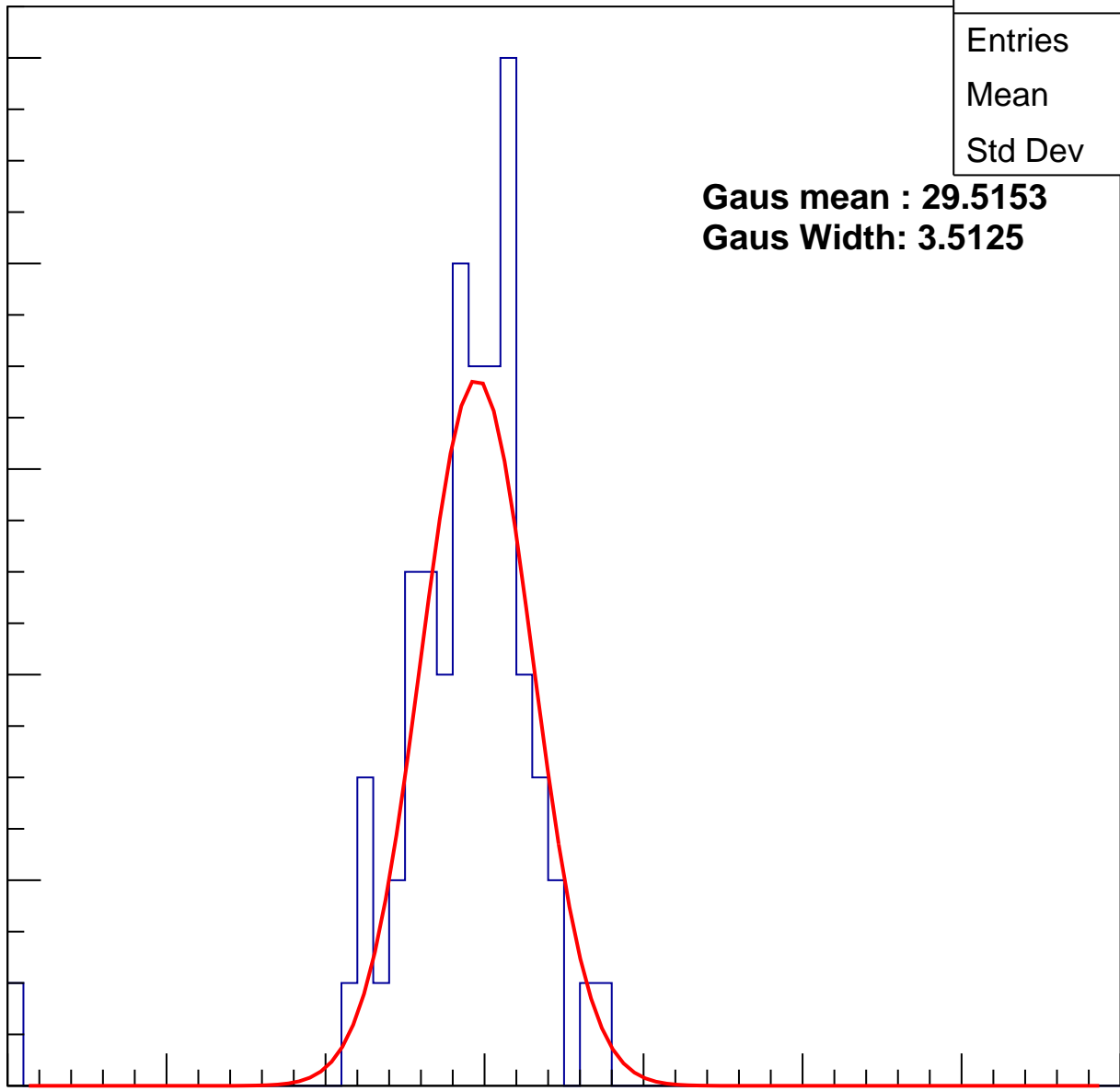
**Gaus Width: 3.5125**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch79, adc1

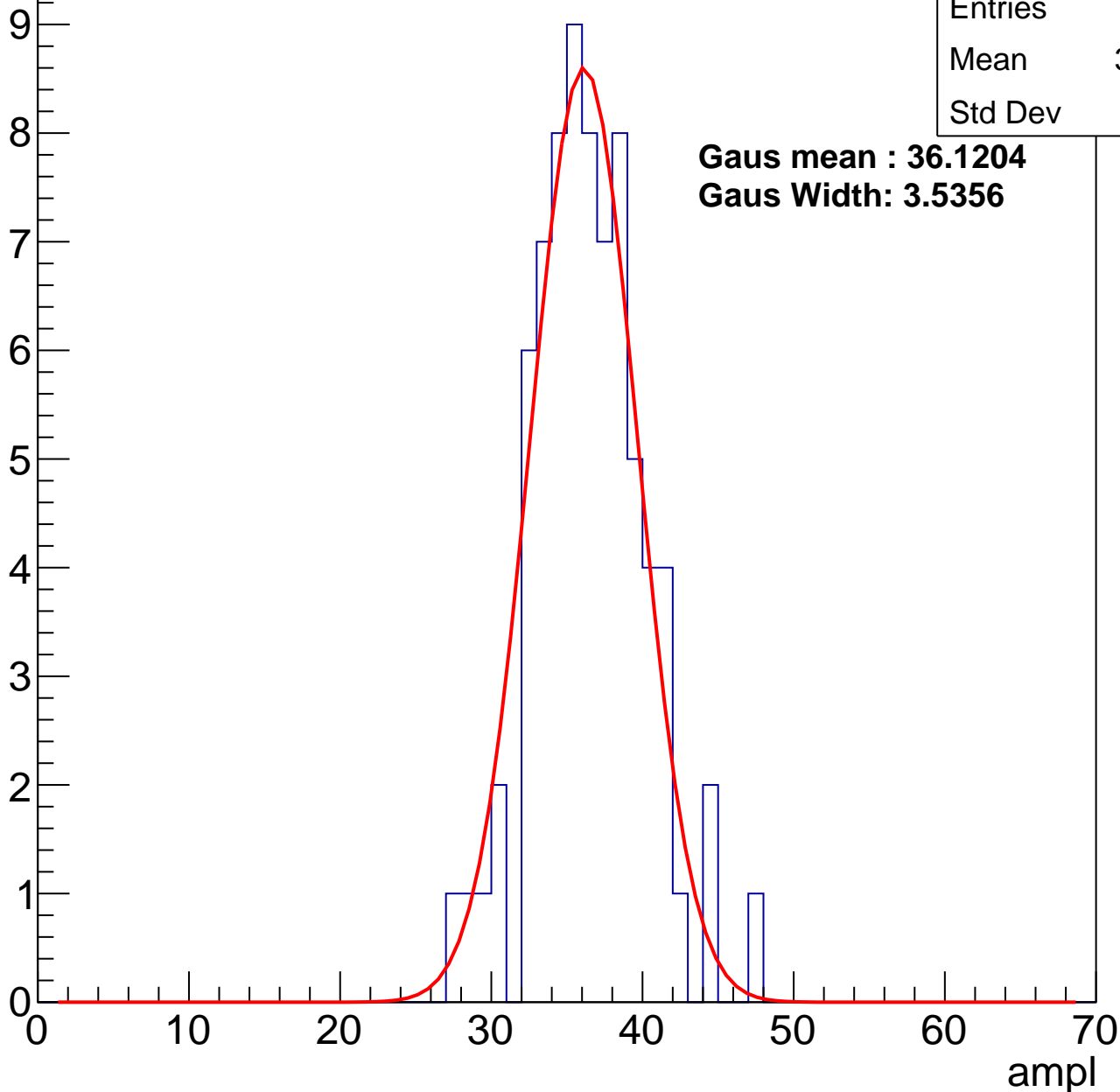
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	36.01
Std Dev	3.65

**Gaus mean : 36.1204**

**Gaus Width: 3.5356**



# B1L000S, U2-ch79, adc2

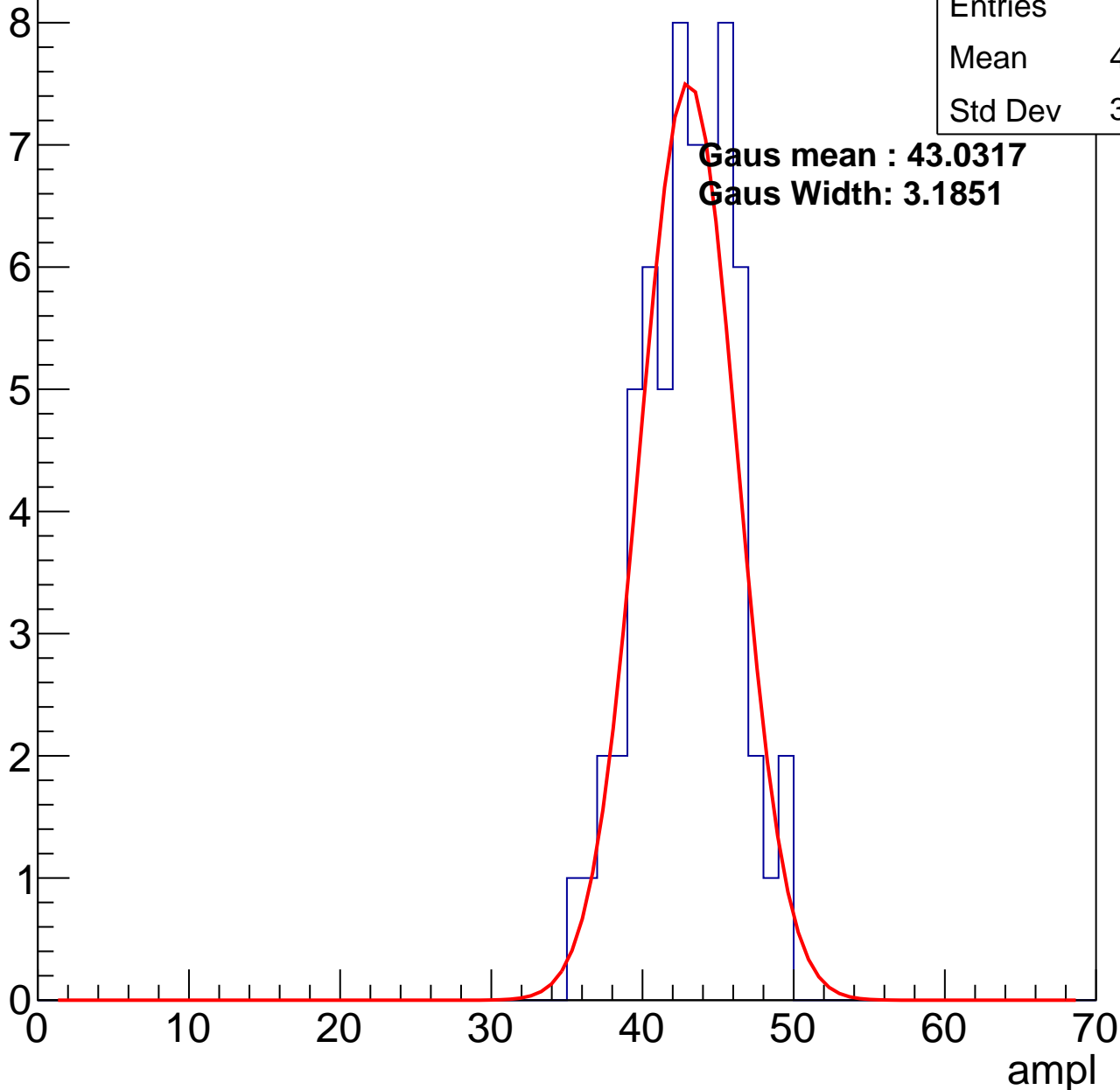
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	42.57
Std Dev	3.105

**Gaus mean : 43.0317**

**Gaus Width: 3.1851**

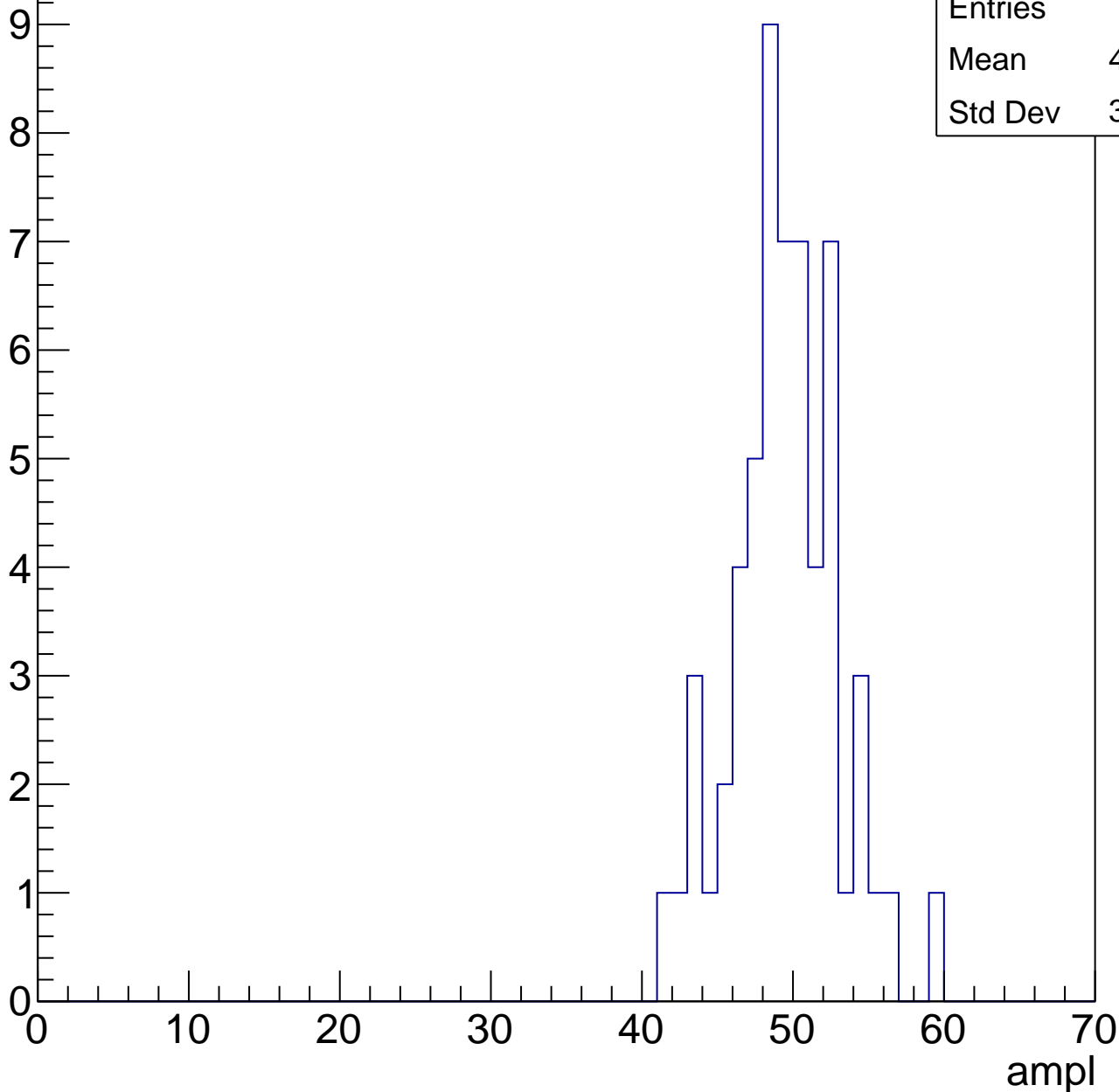


# B1L000S, U2-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	49.02
Std Dev	3.486

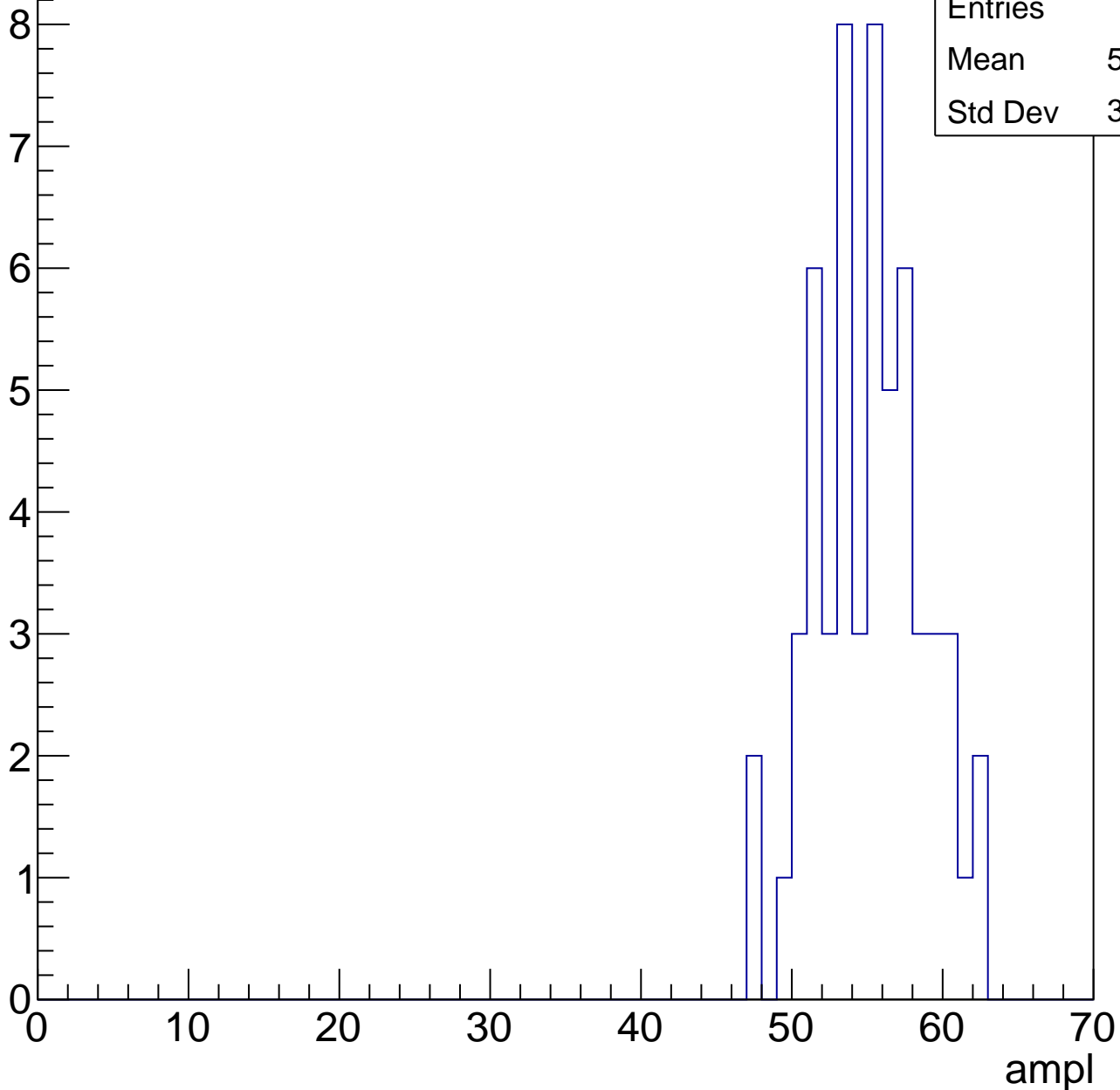


# B1L000S, U2-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

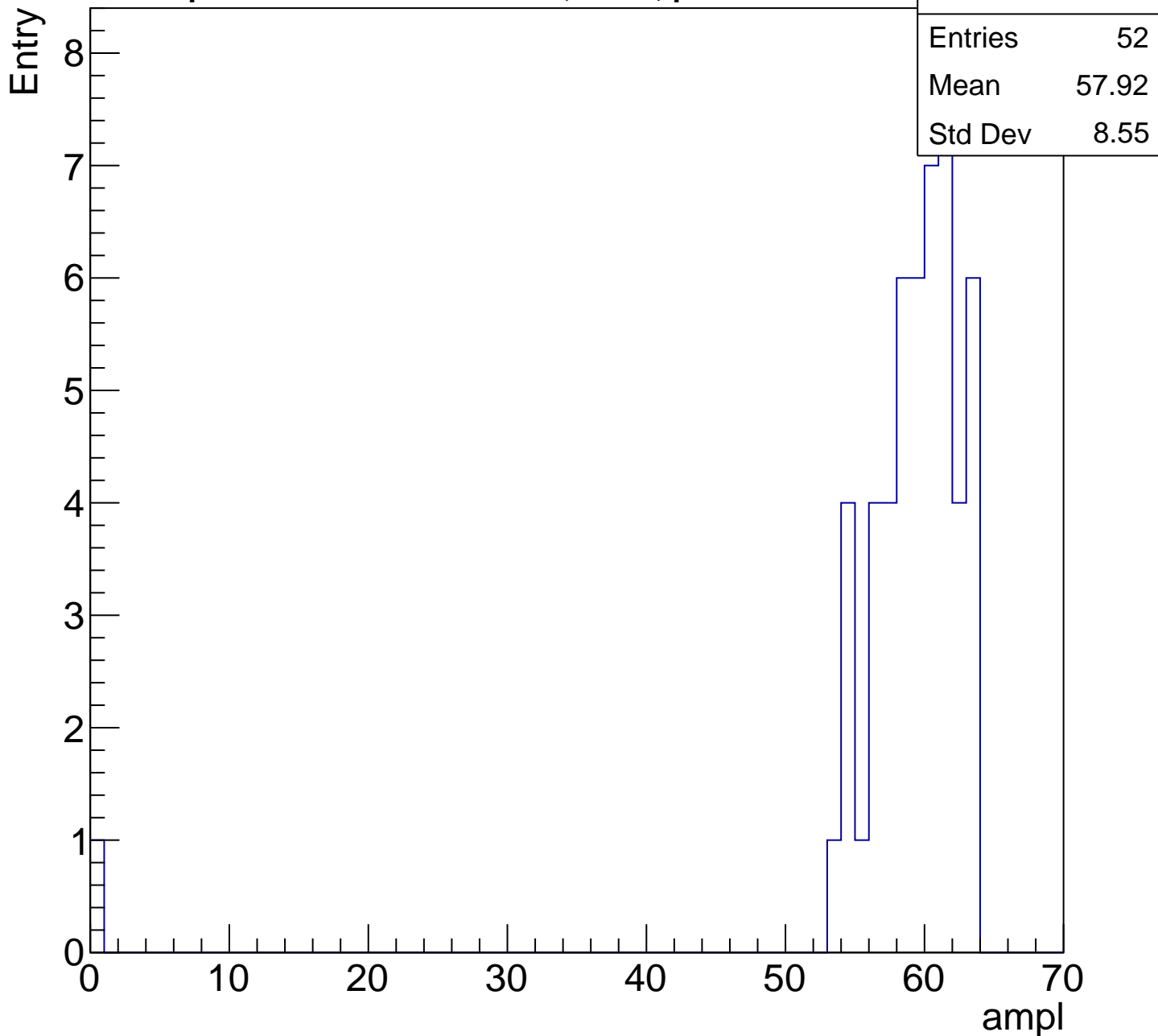
Entry

Entries	57
Mean	54.72
Std Dev	3.518



# B1L000S, U2-ch79, adc5

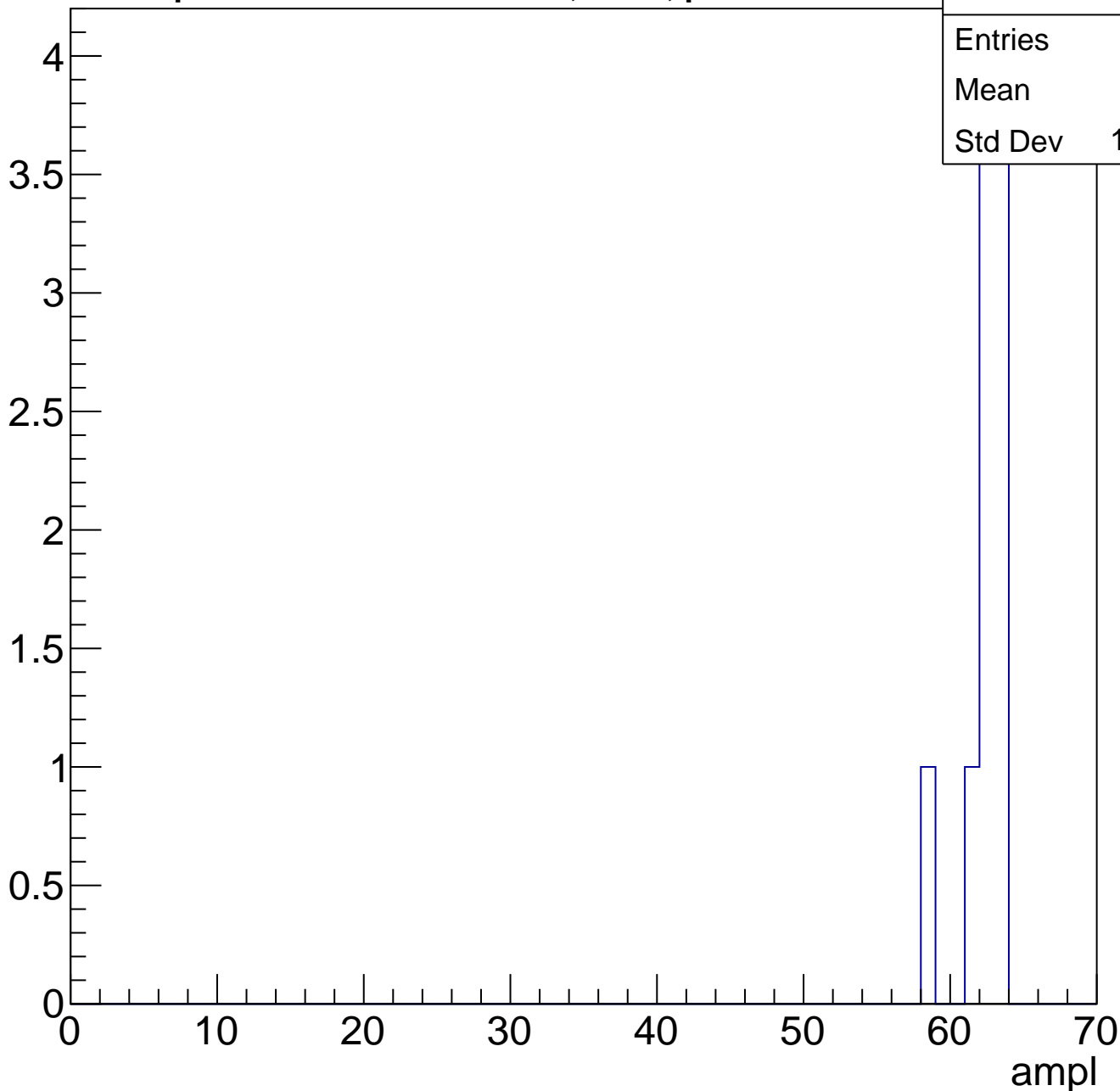
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	10
Mean	61.9
Std Dev	1.446



# B1L000S, U2-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch80, adc0

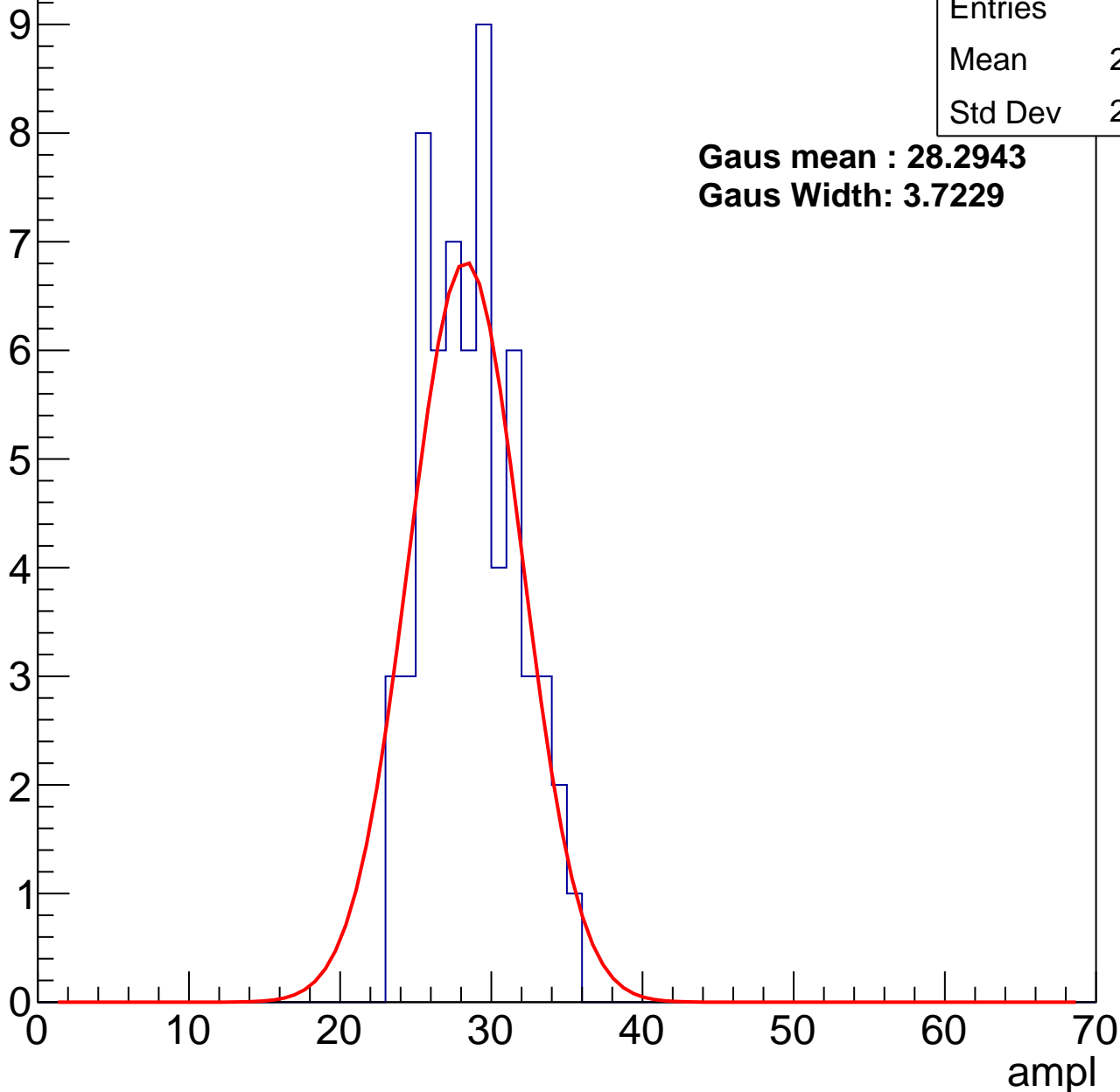
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.18
Std Dev	2.995

**Gaus mean : 28.2943**

**Gaus Width: 3.7229**



# B1L000S, U2-ch80, adc1

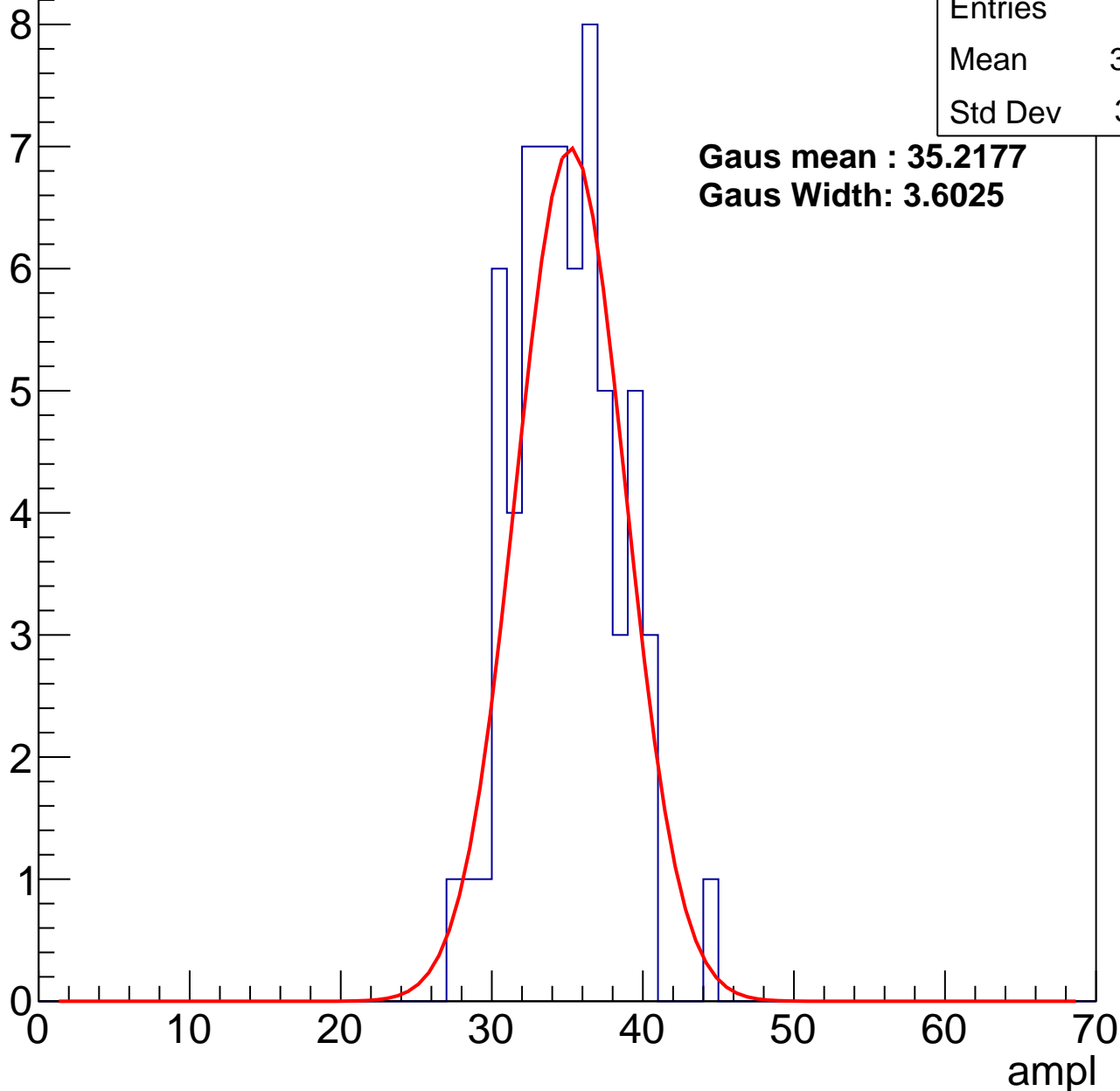
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	34.42
Std Dev	3.351

**Gaus mean : 35.2177**

**Gaus Width: 3.6025**



# B1L000S, U2-ch80, adc2

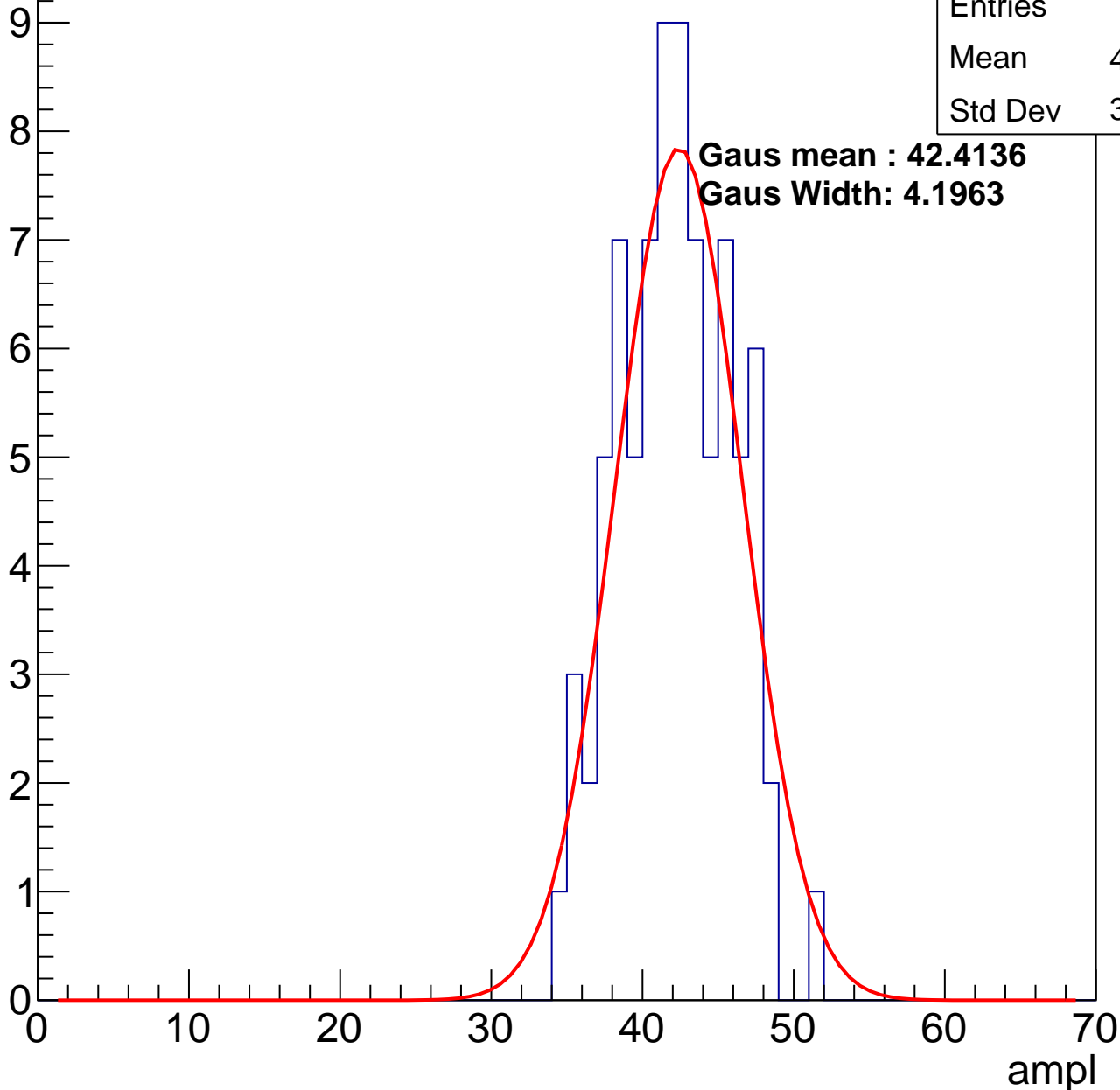
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	41.72
Std Dev	3.632

**Gaus mean : 42.4136**

**Gaus Width: 4.1963**

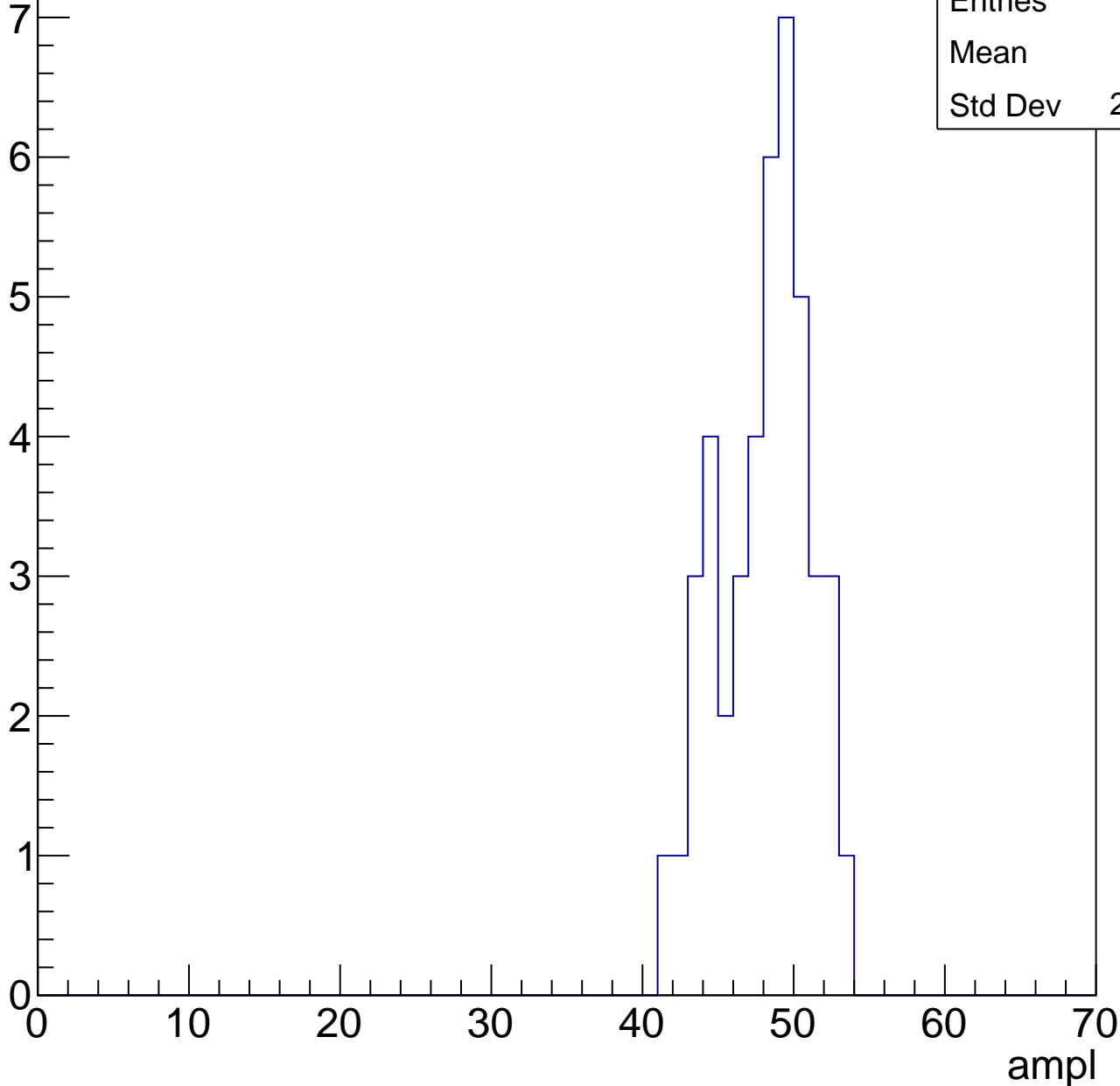


# B1L000S, U2-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	47.6
Std Dev	2.966

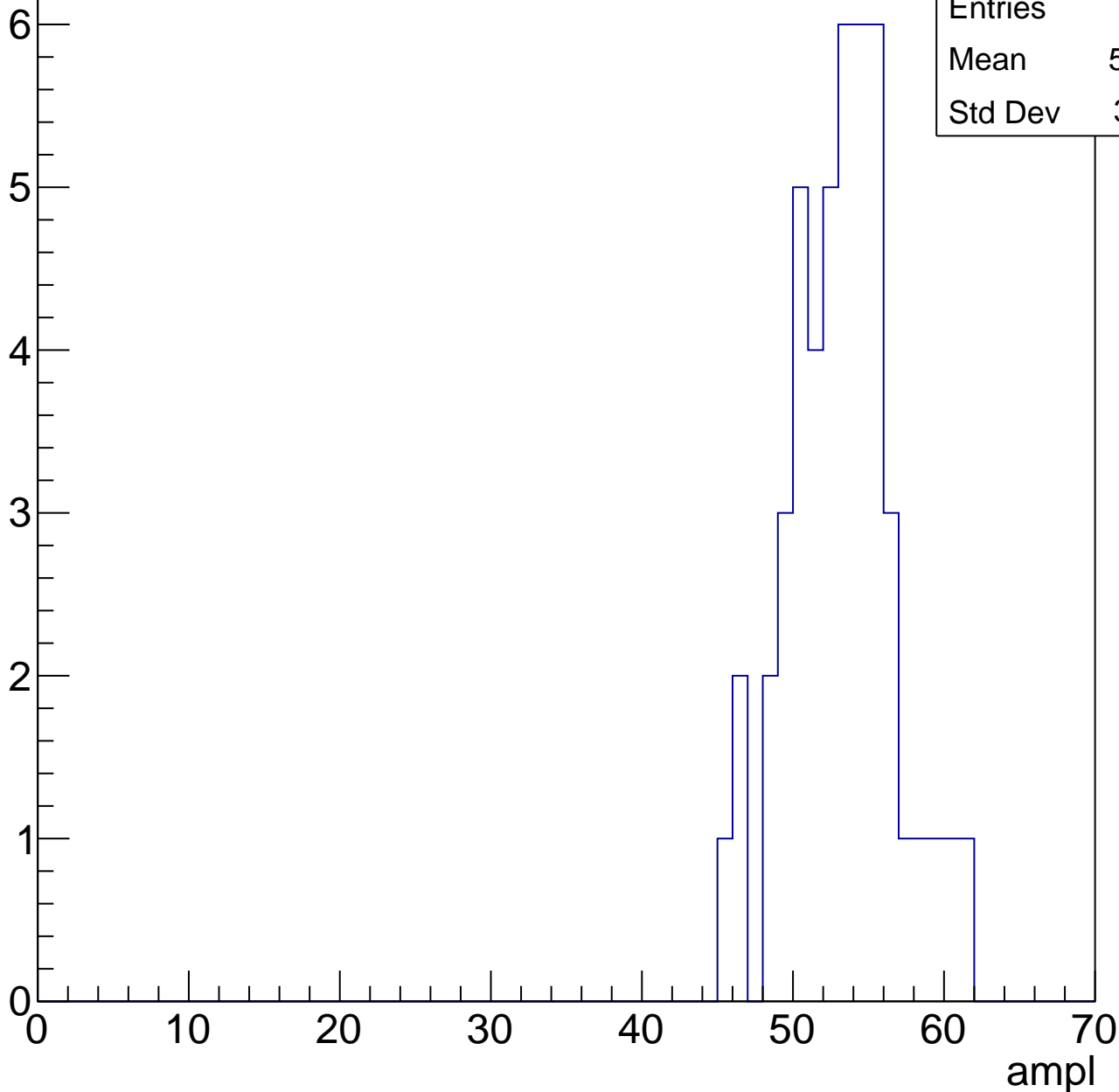


# B1L000S, U2-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

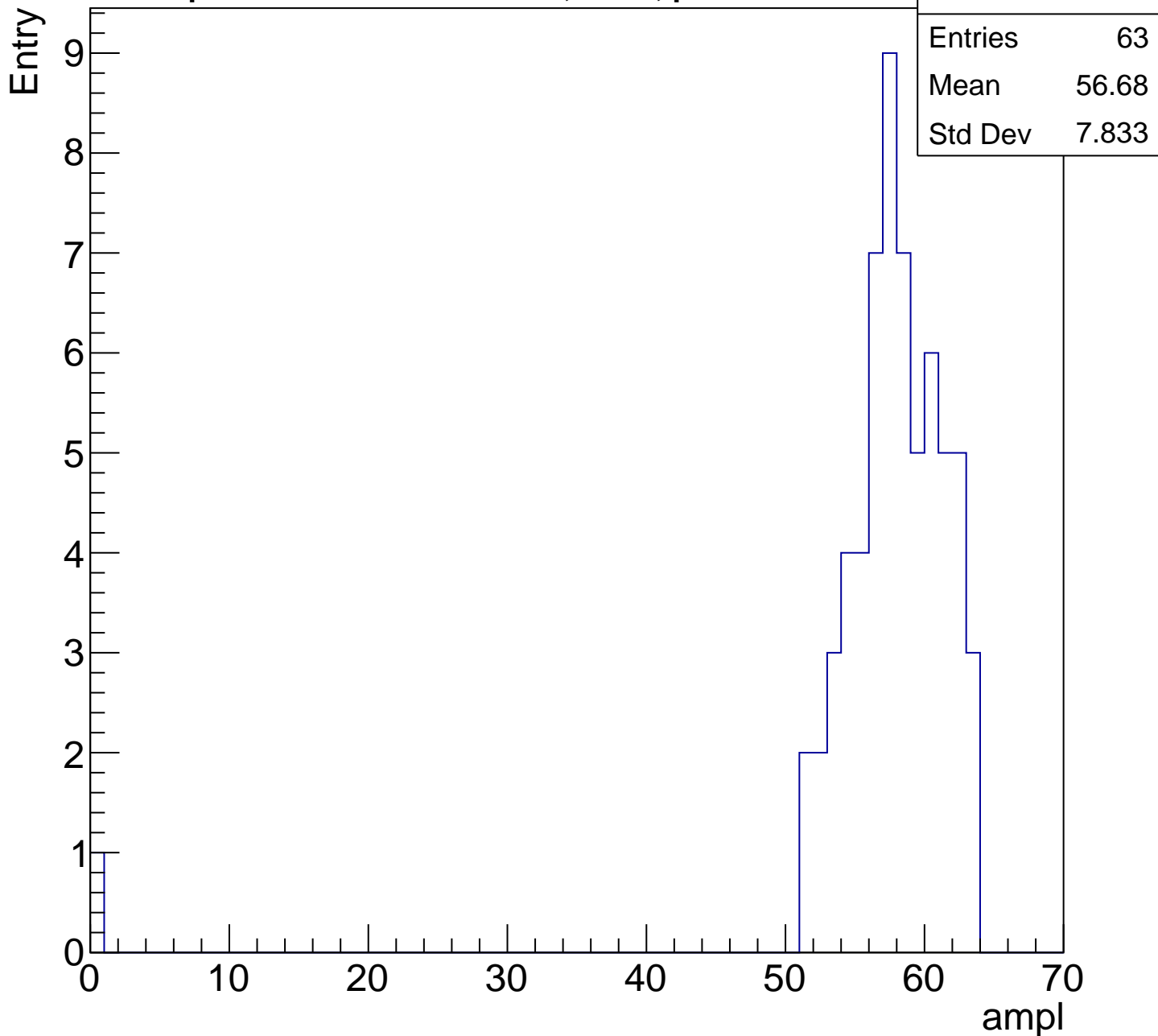
Entry

Entries	48
Mean	52.69
Std Dev	3.441



# B1L000S, U2-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

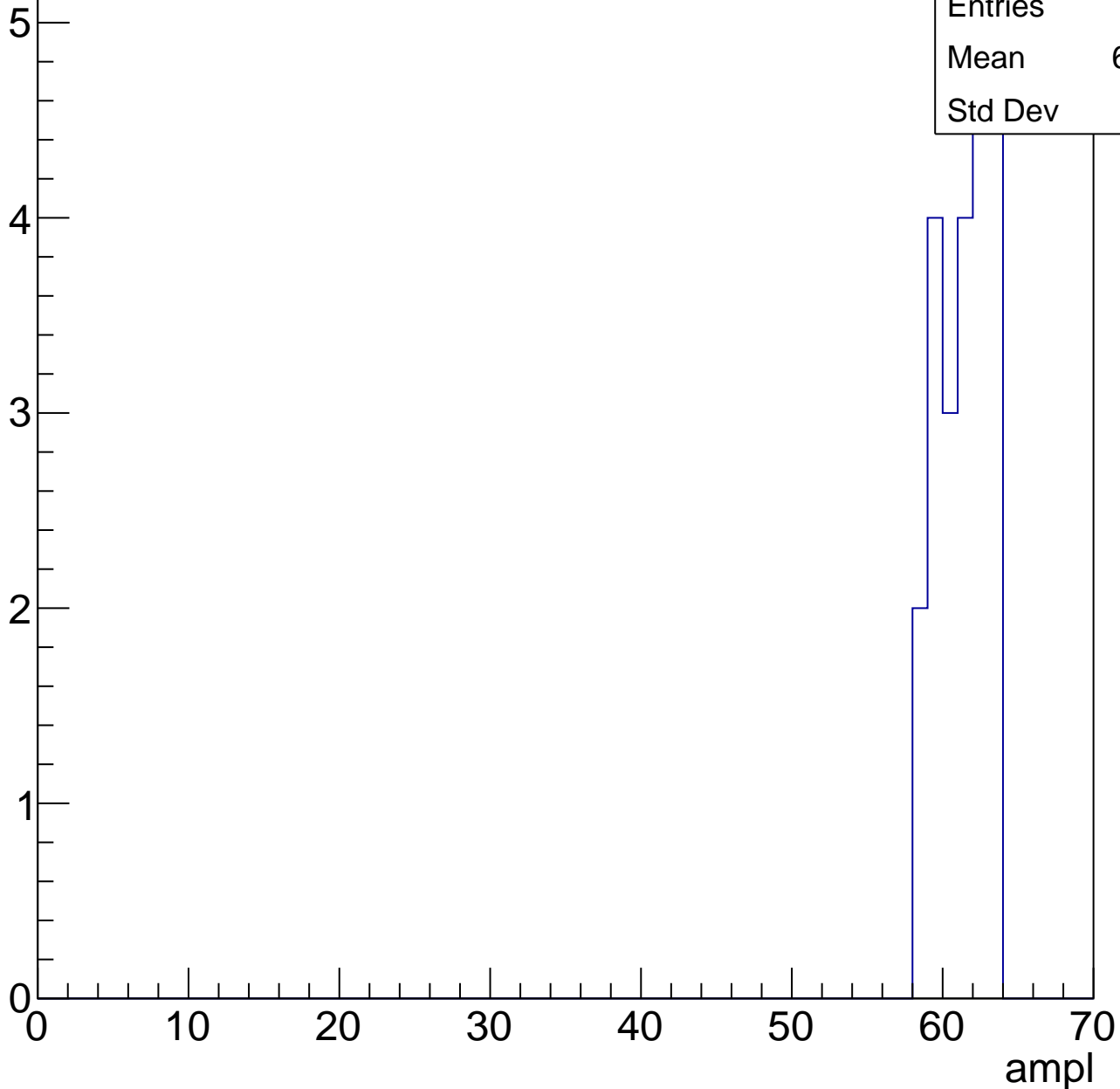


# B1L000S, U2-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	60.91
Std Dev	1.64





# B1L000S, U2-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch81, adc0

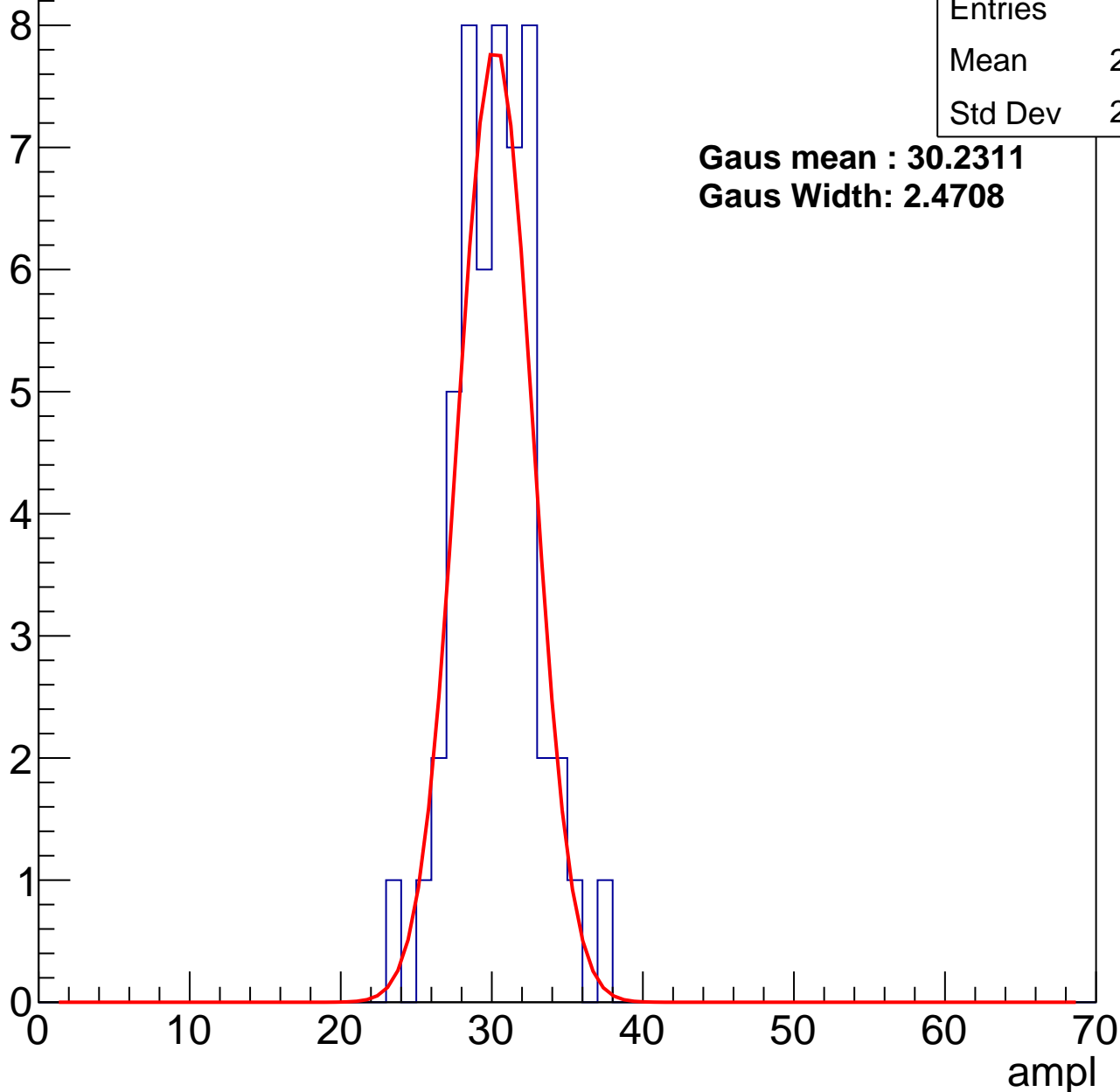
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	29.85
Std Dev	2.597

**Gaus mean : 30.2311**

**Gaus Width: 2.4708**



# B1L000S, U2-ch81, adc1

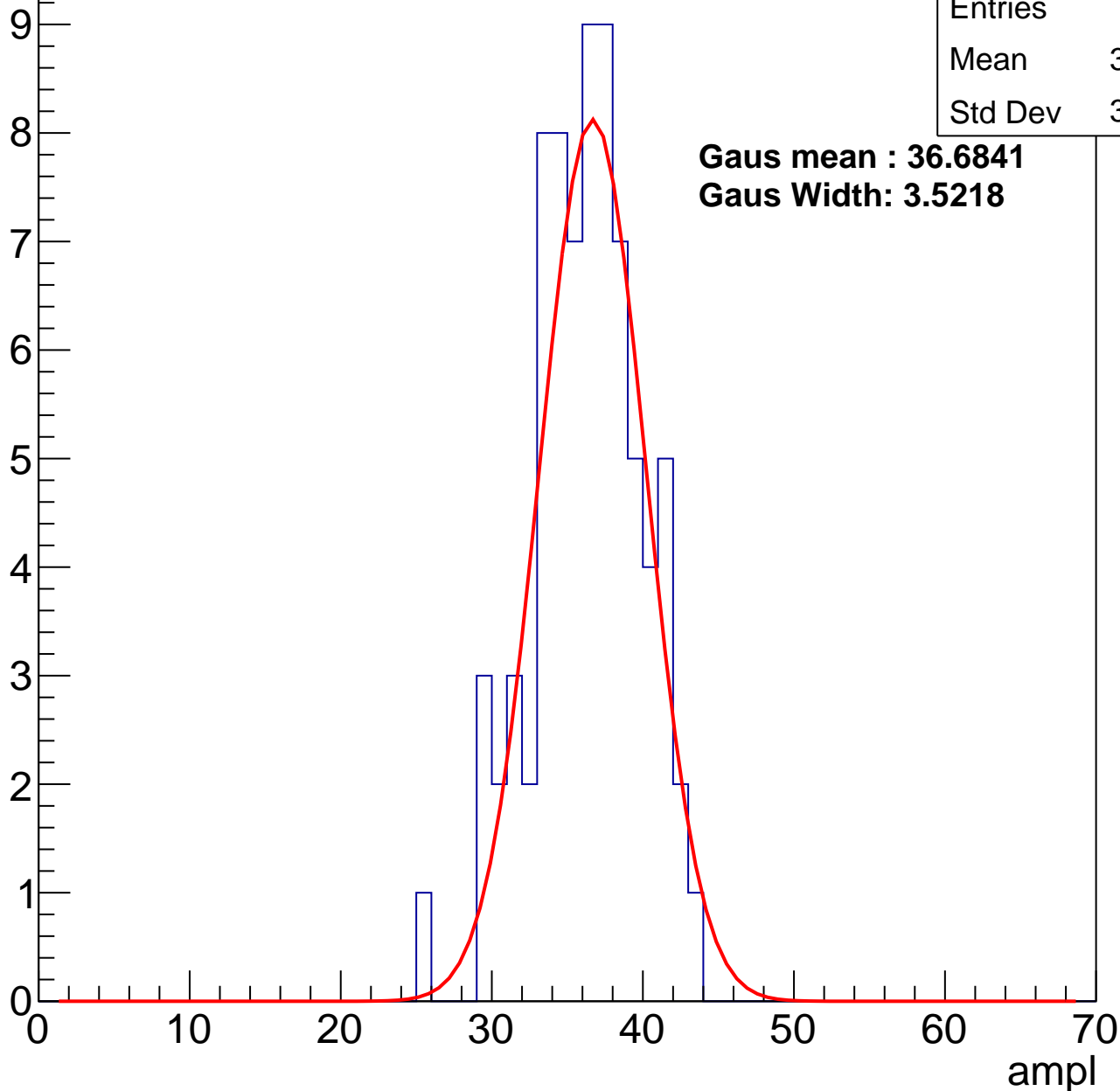
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	35.79
Std Dev	3.529

**Gaus mean : 36.6841**

**Gaus Width: 3.5218**



# B1L000S, U2-ch81, adc2

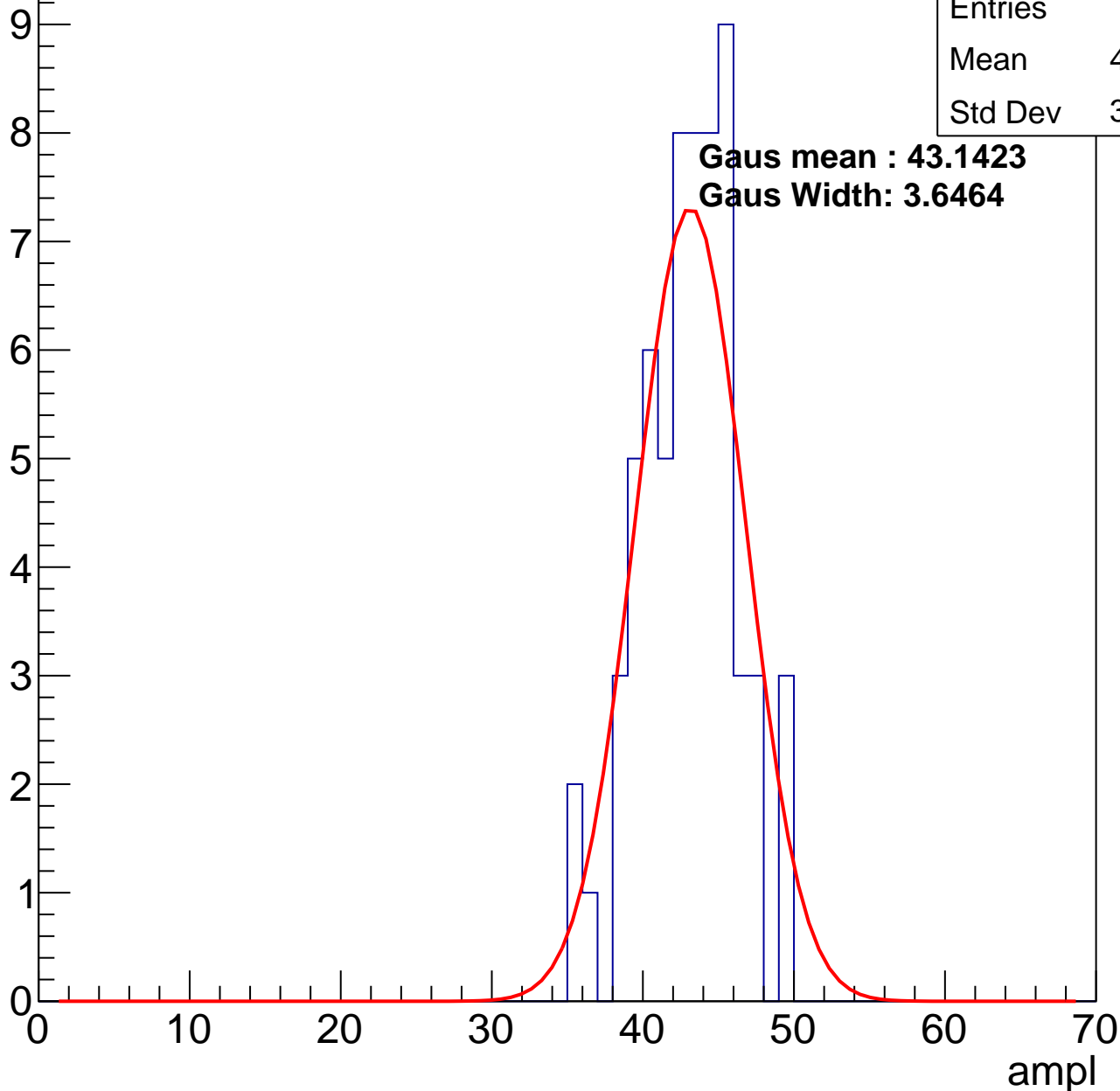
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	42.55
Std Dev	3.132

**Gaus mean : 43.1423**

**Gaus Width: 3.6464**

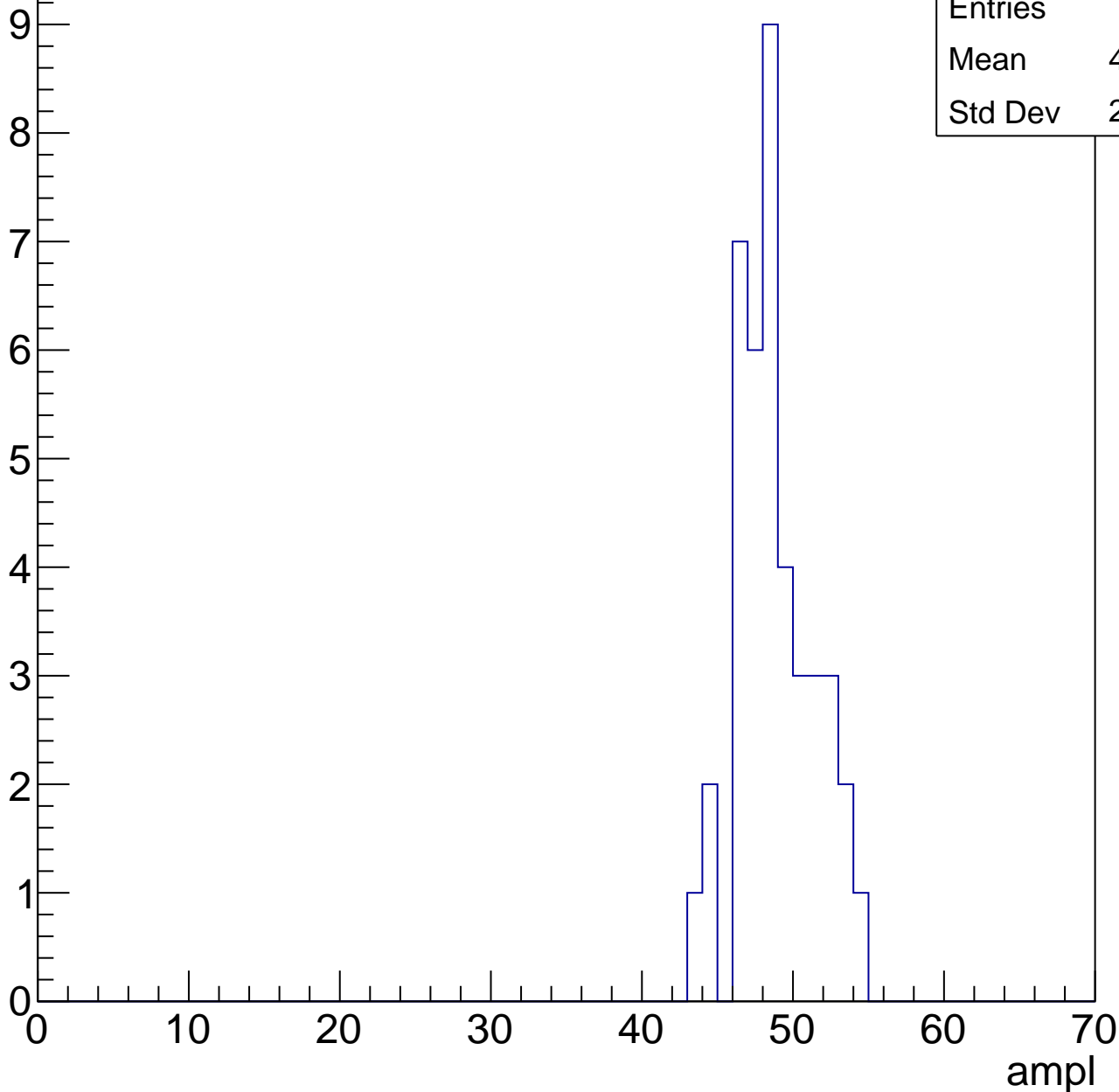


# B1L000S, U2-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	41
Mean	48.34
Std Dev	2.534

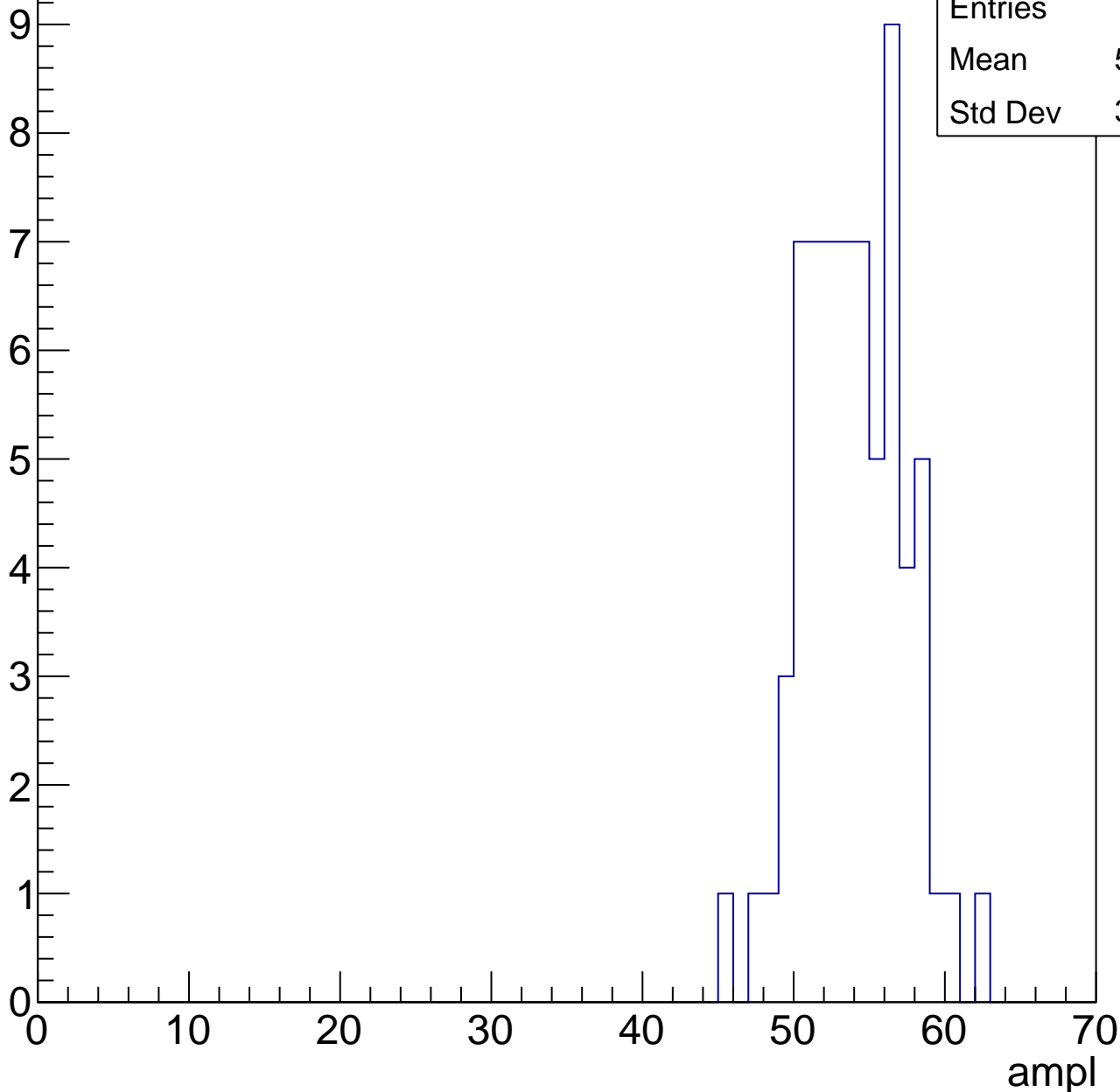


# B1L000S, U2-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	53.51
Std Dev	3.271

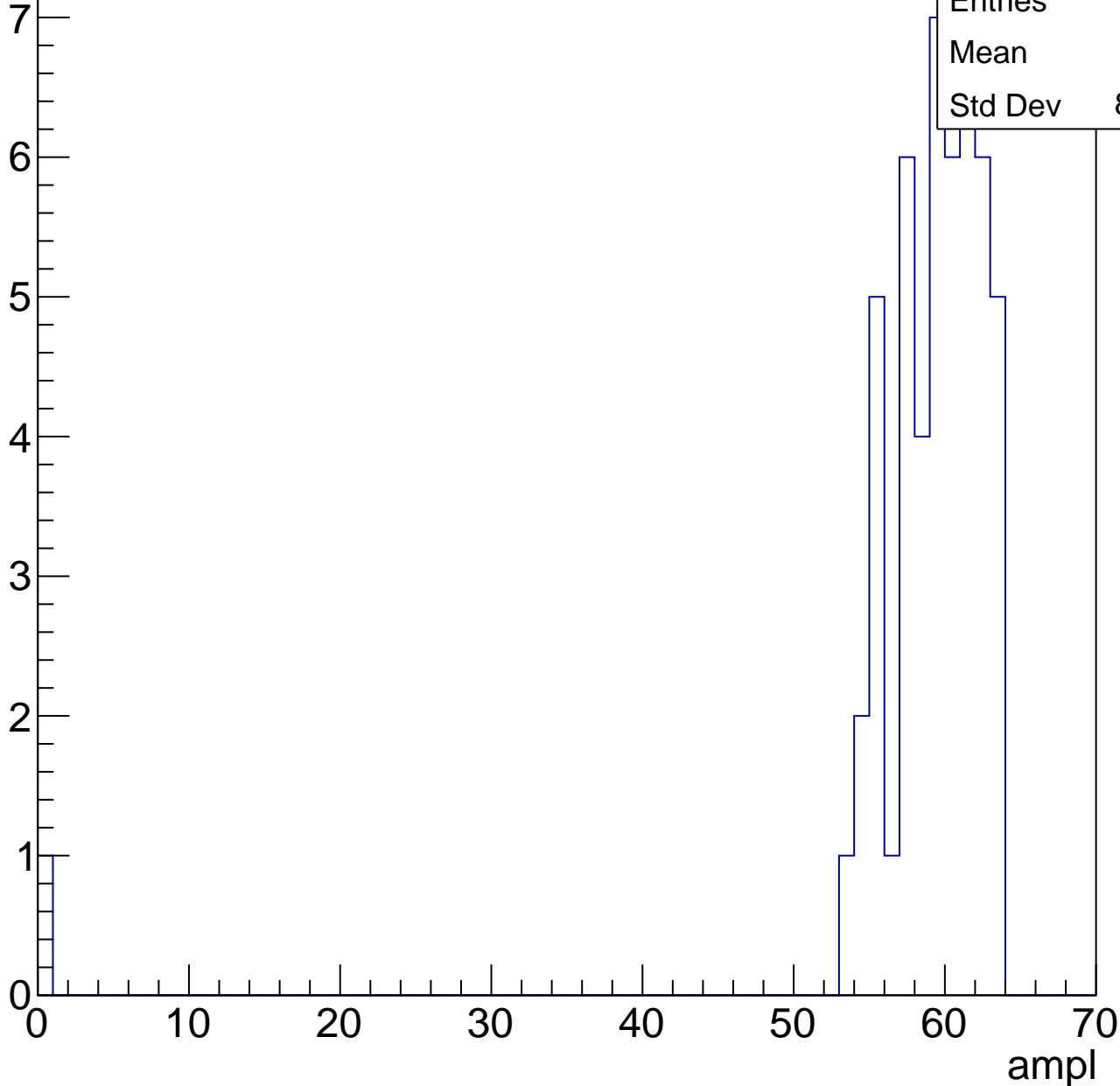


# B1L000S, U2-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	57.9
Std Dev	8.621



# B1L000S, U2-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

3

2.5

2

1.5

1

0.5

0

Entries

15

Mean

60.93

Std Dev

1.526

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch81, adc7

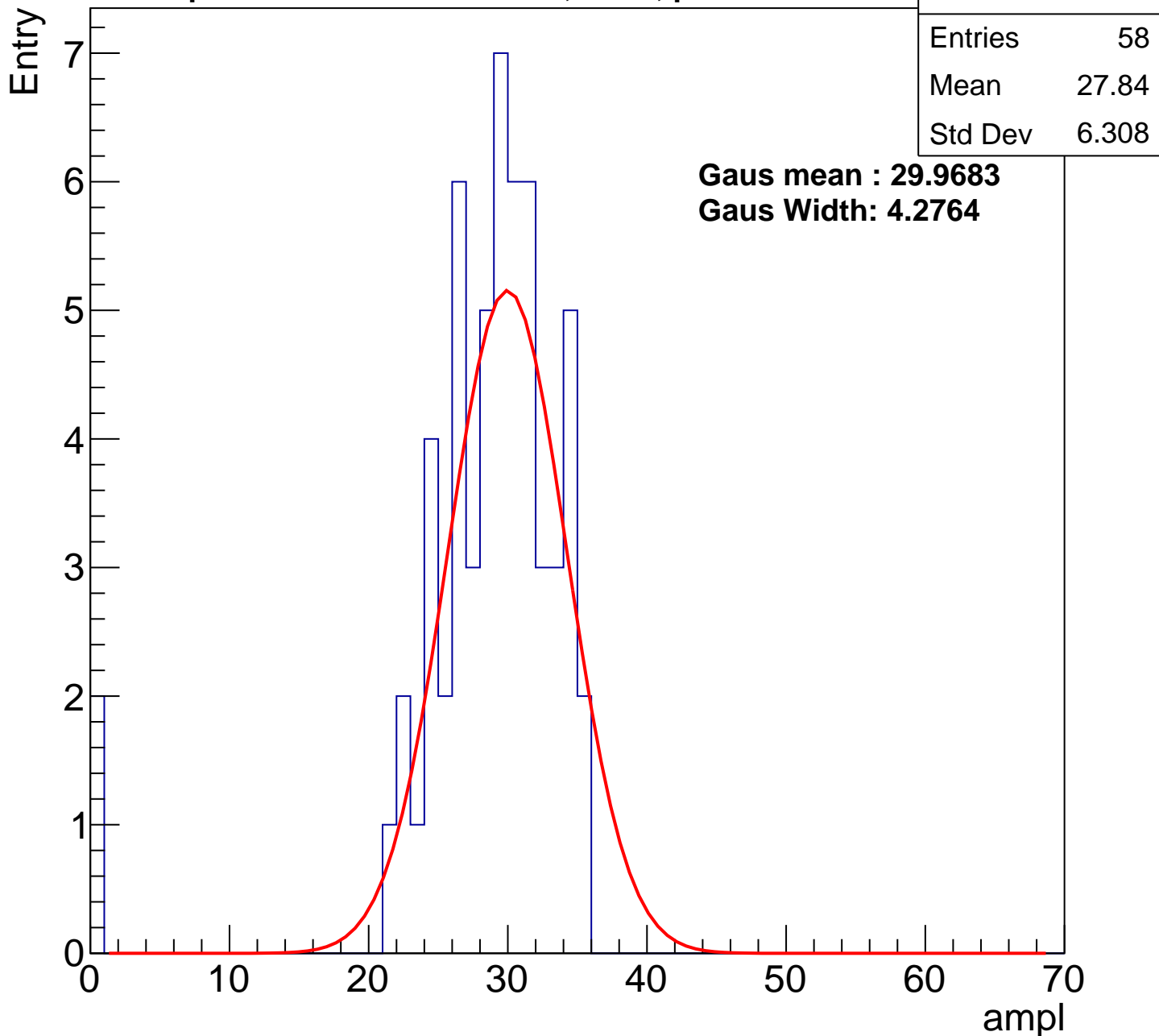
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch82, adc1

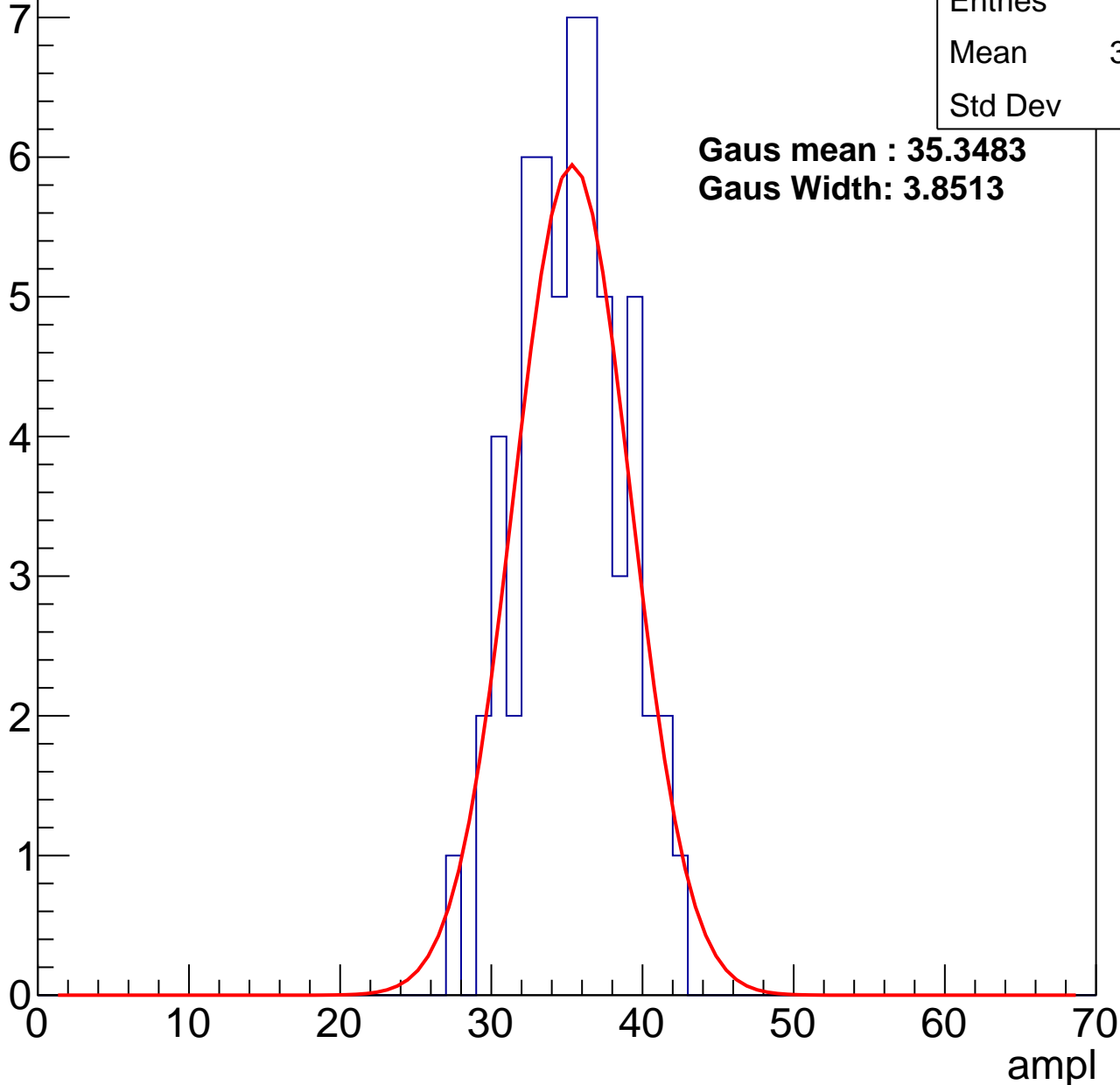
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.86
Std Dev	3.36

**Gaus mean : 35.3483**

**Gaus Width: 3.8513**



# B1L000S, U2-ch82, adc2

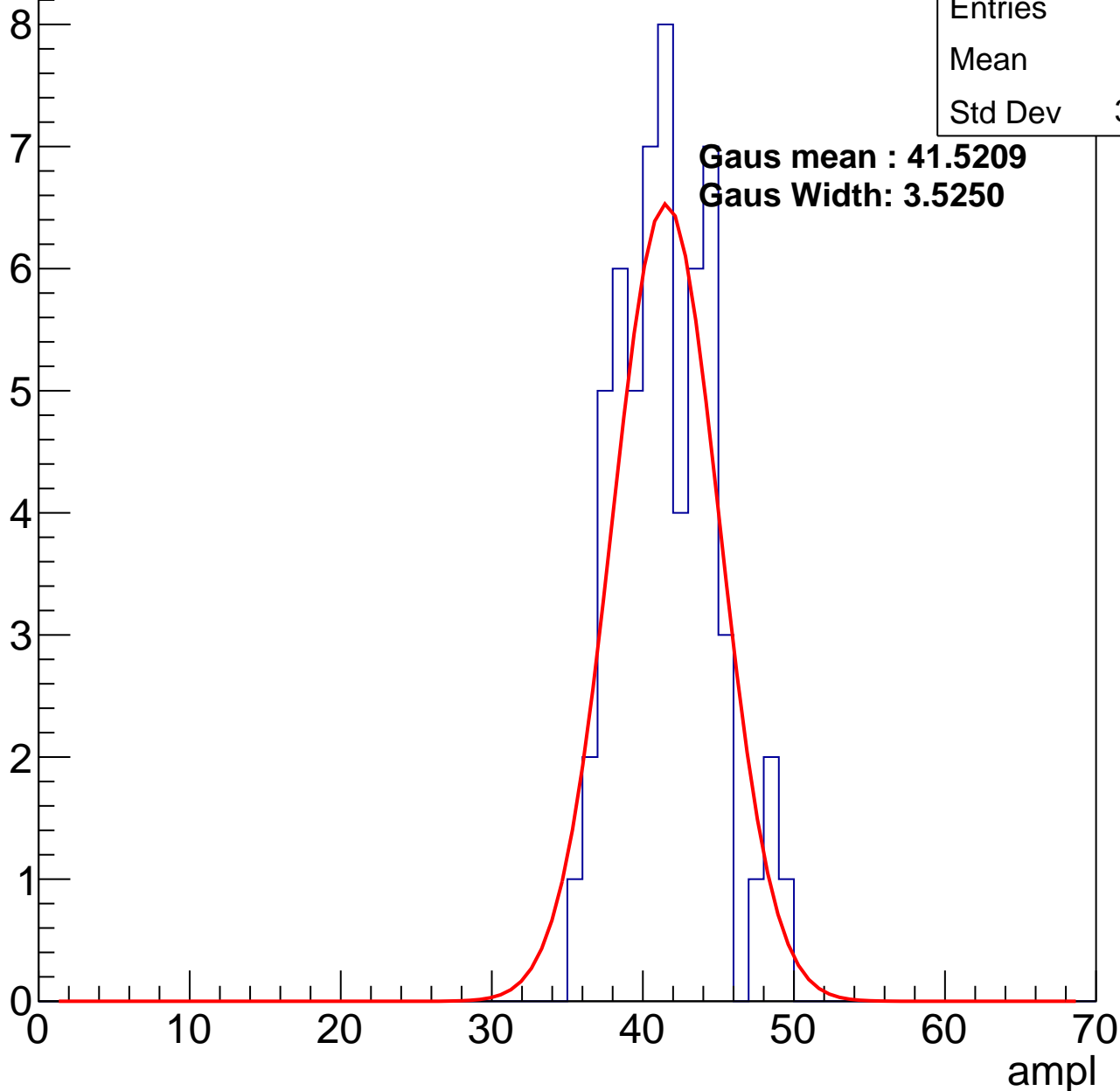
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.1
Std Dev	3.171

**Gaus mean : 41.5209**

**Gaus Width: 3.5250**

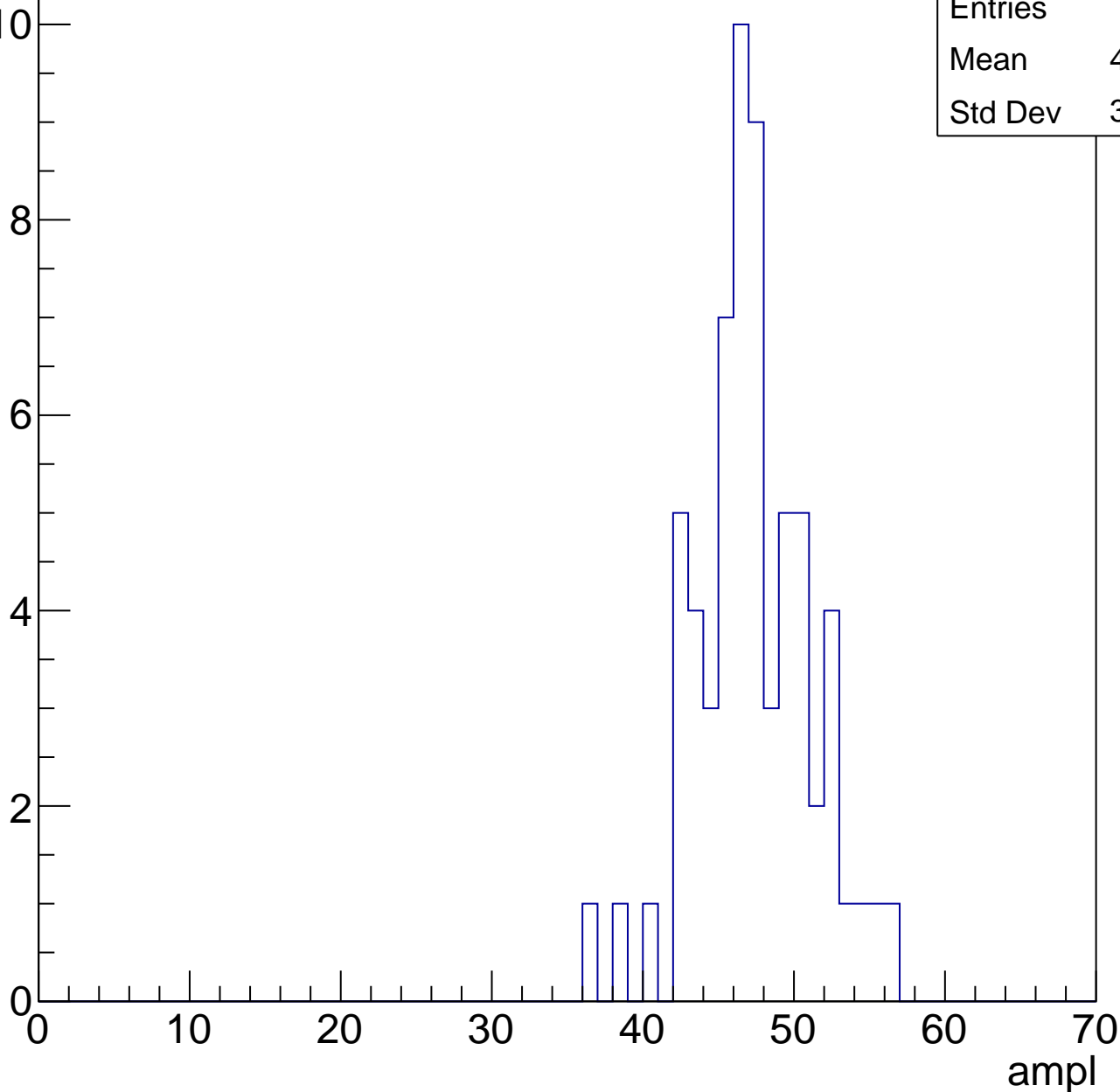


# B1L000S, U2-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

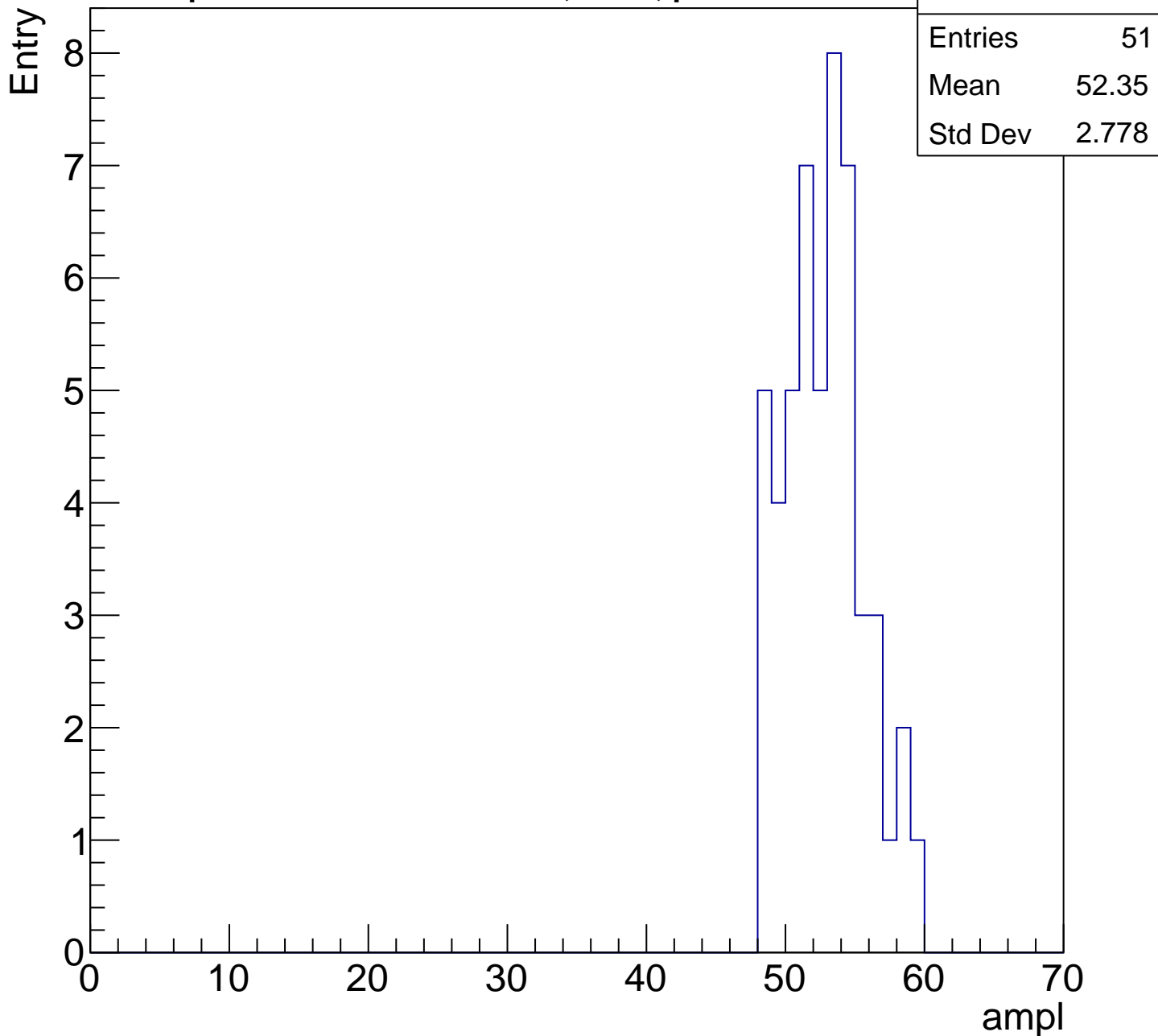
Entry

Entries	64
Mean	46.77
Std Dev	3.815



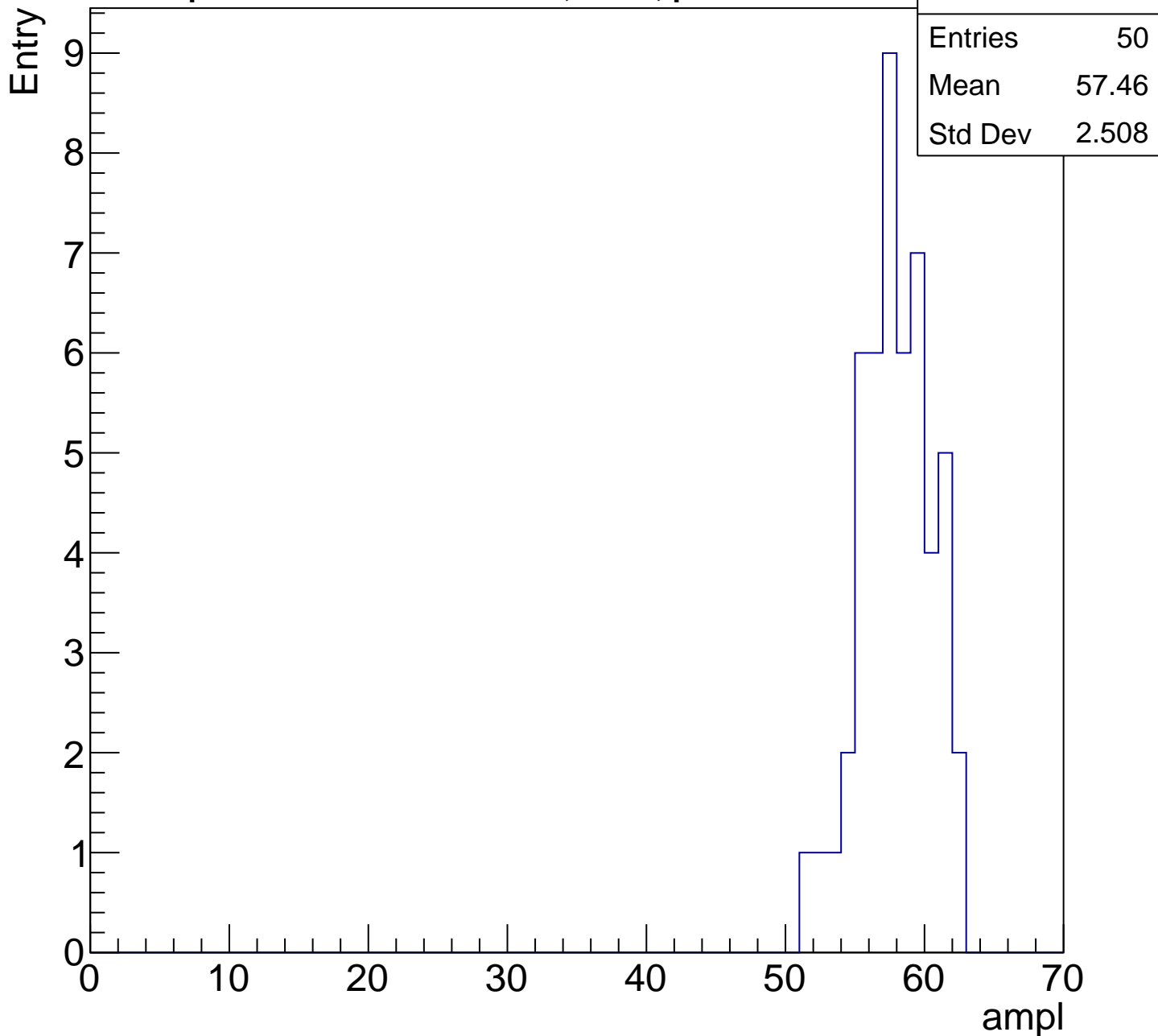
# B1L000S, U2-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

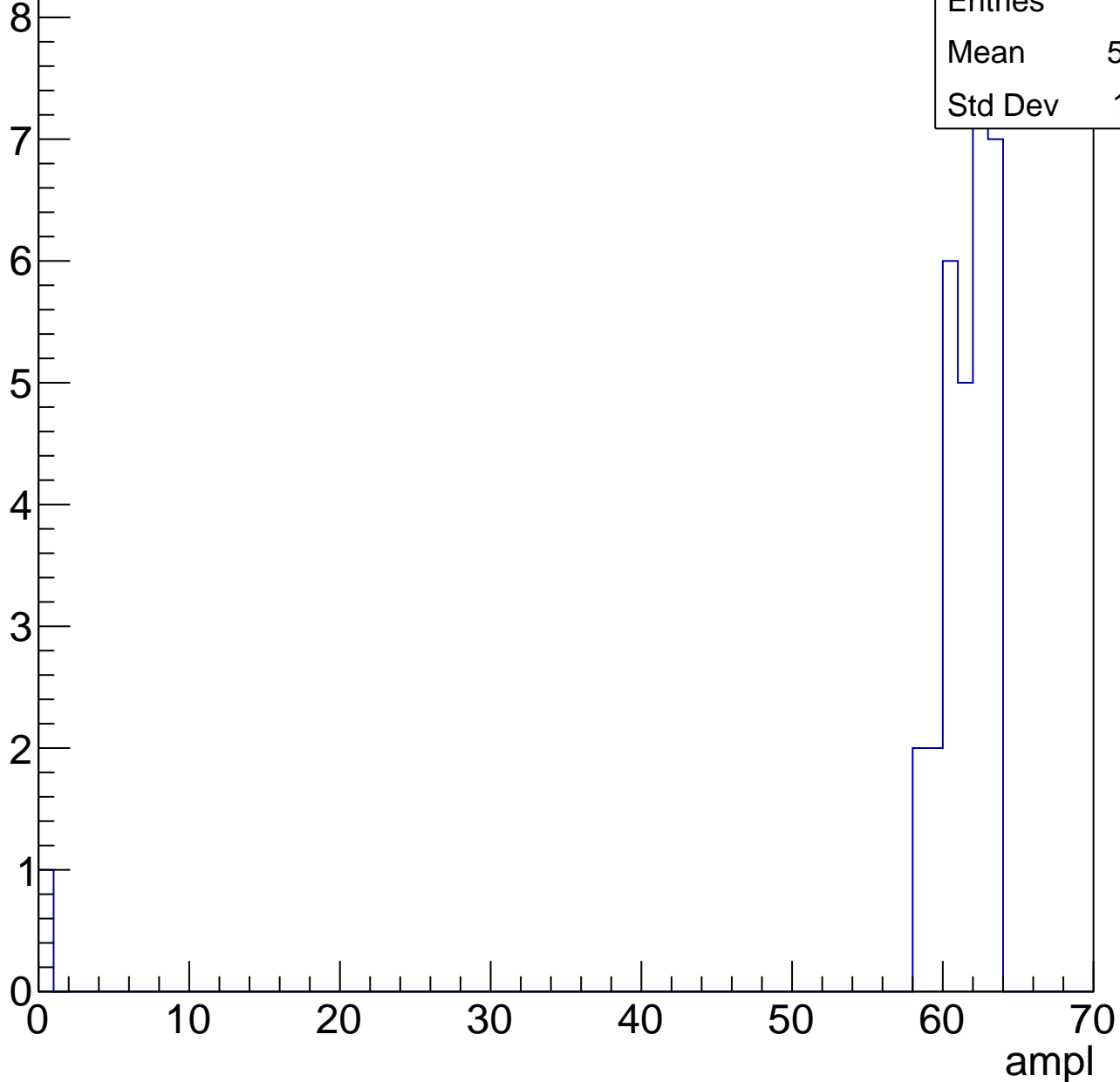


# B1L000S, U2-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	31
Mean	59.23
Std Dev	10.91

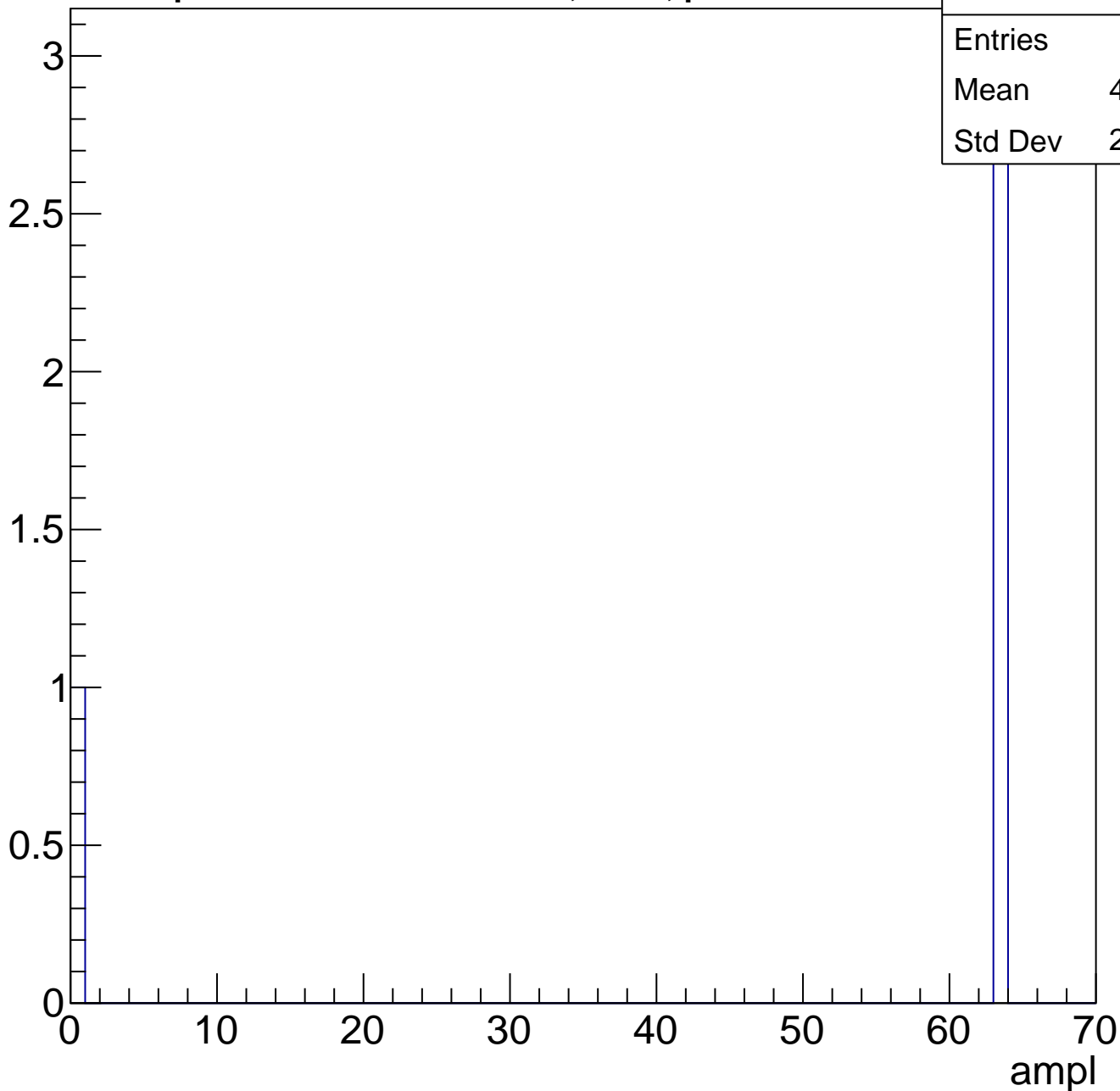




# B1L000S, U2-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch83, adc0

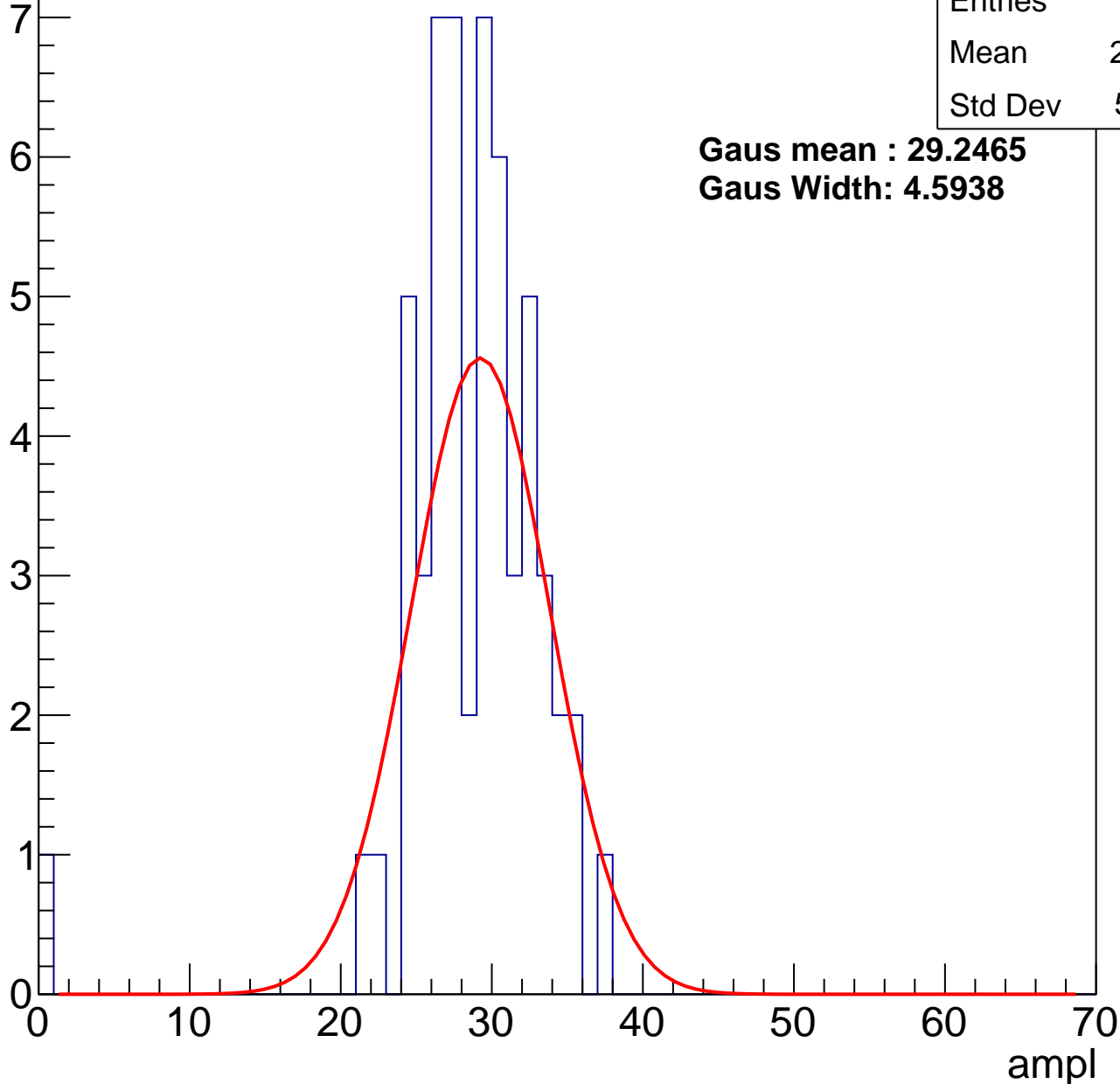
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	28.12
Std Dev	5.131

**Gaus mean : 29.2465**

**Gaus Width: 4.5938**



# B1L000S, U2-ch83, adc1

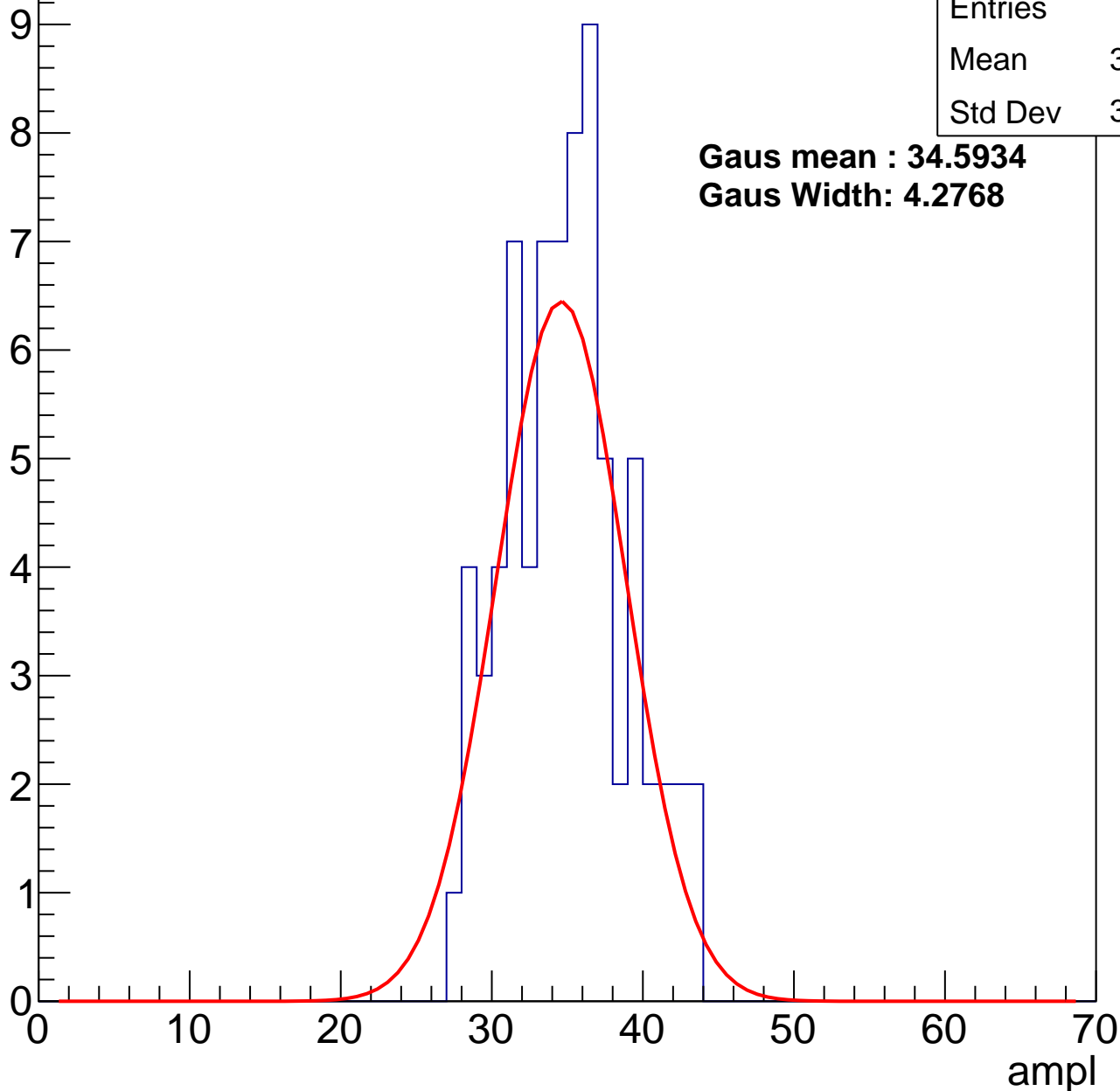
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	34.49
Std Dev	3.867

**Gaus mean : 34.5934**

**Gaus Width: 4.2768**



# B1L000S, U2-ch83, adc2

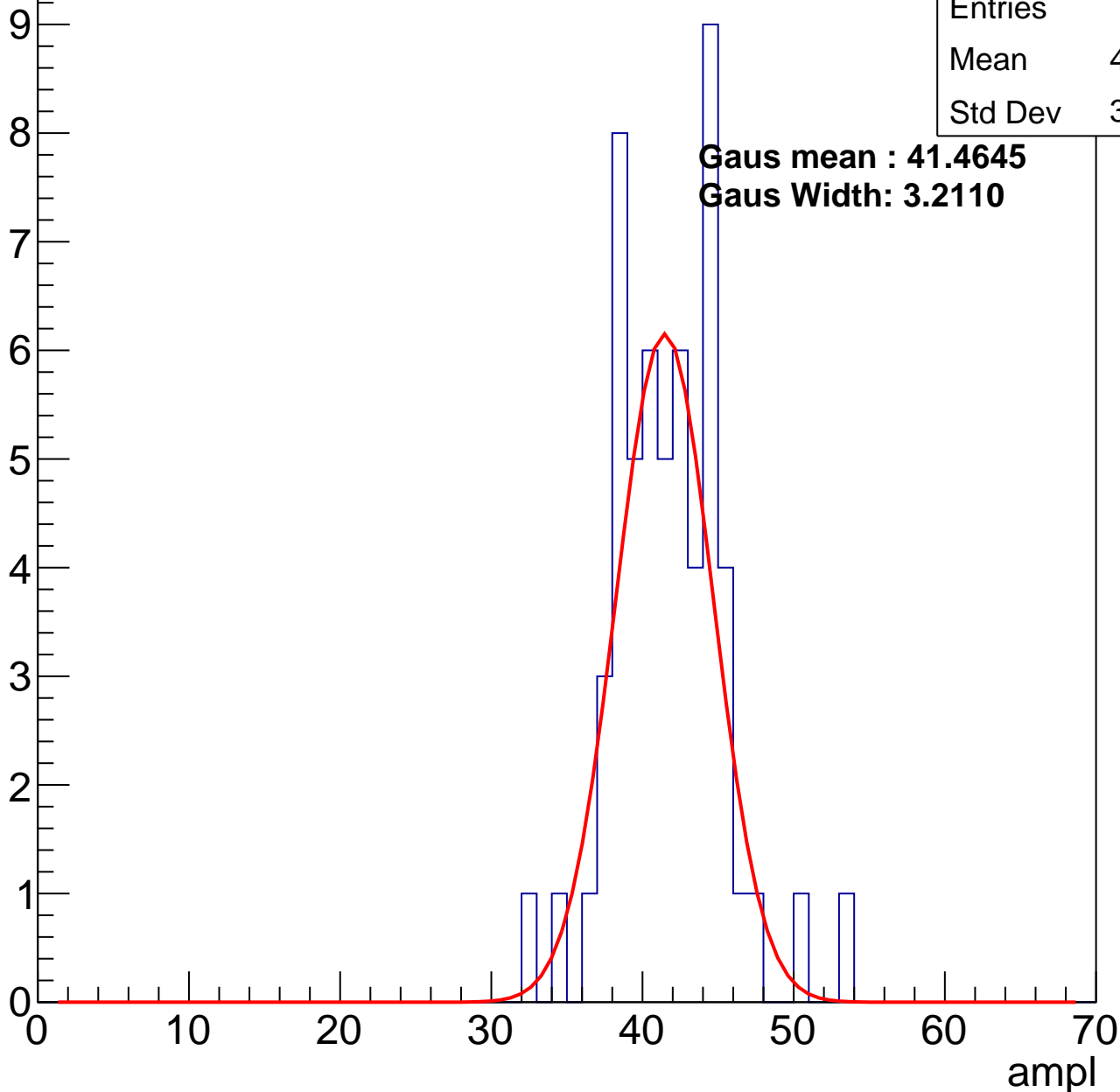
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.28
Std Dev	3.626

**Gaus mean : 41.4645**

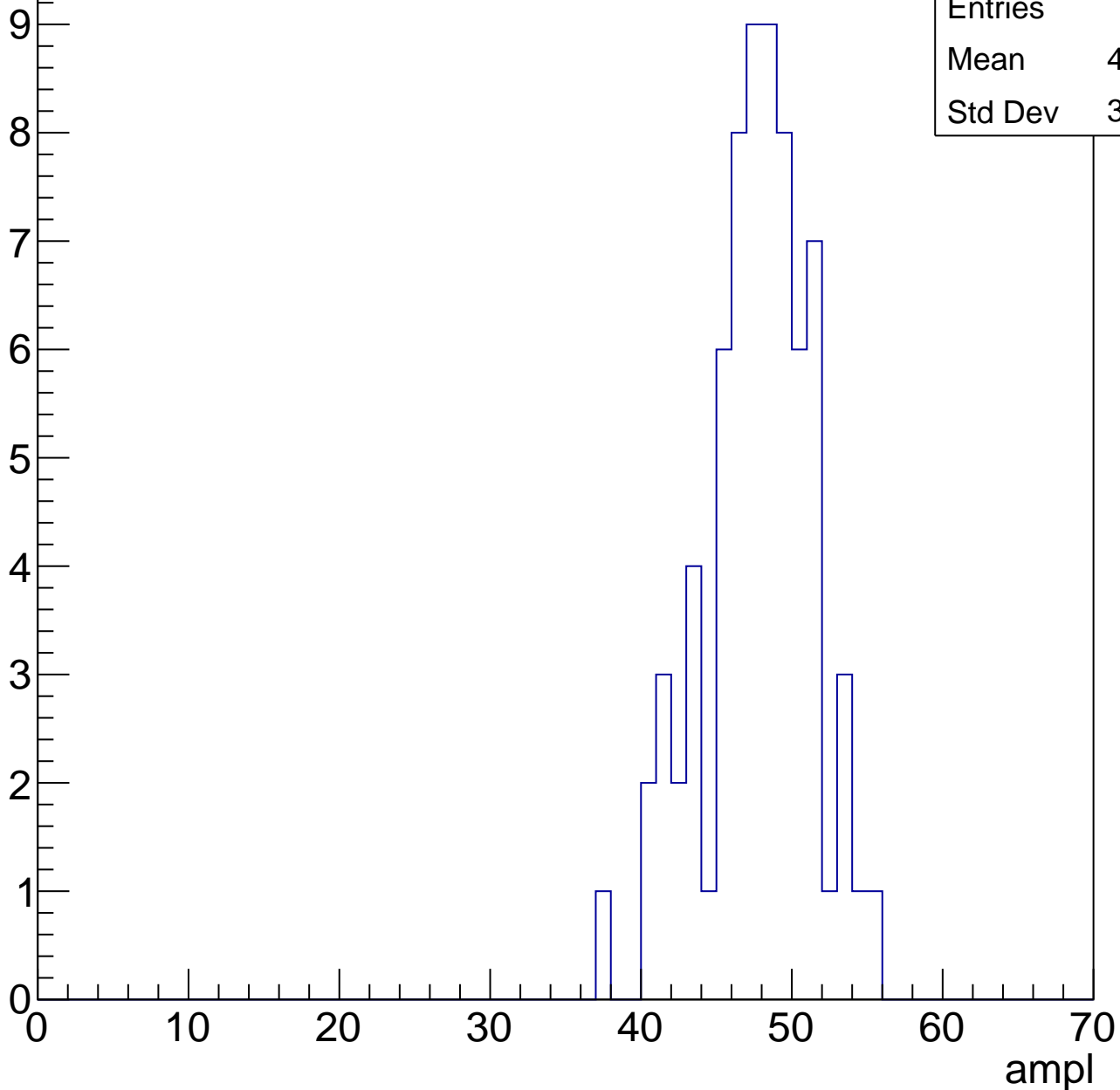
**Gaus Width: 3.2110**



# B1L000S, U2-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

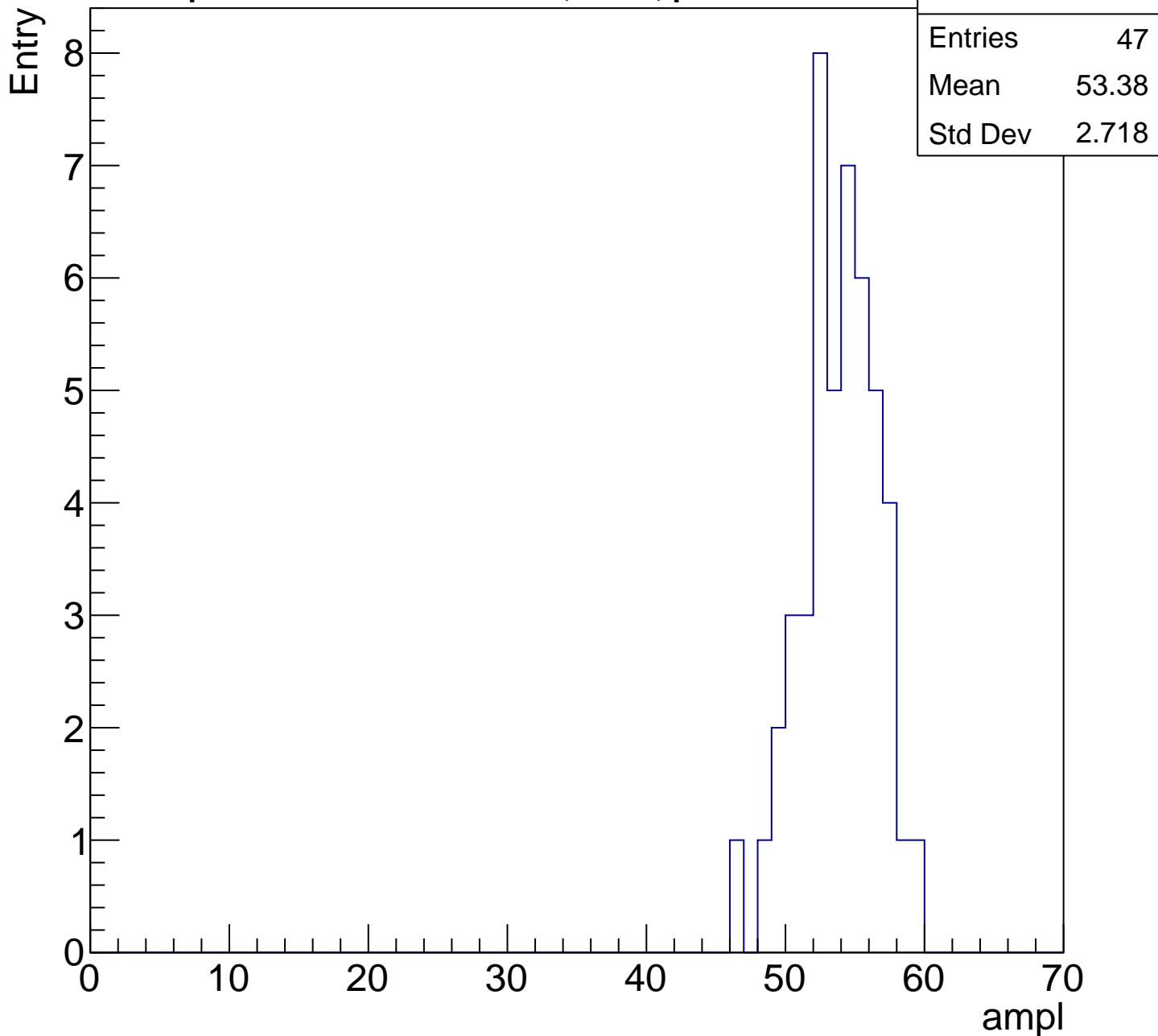
Entry



Entries	72
Mean	47.25
Std Dev	3.554

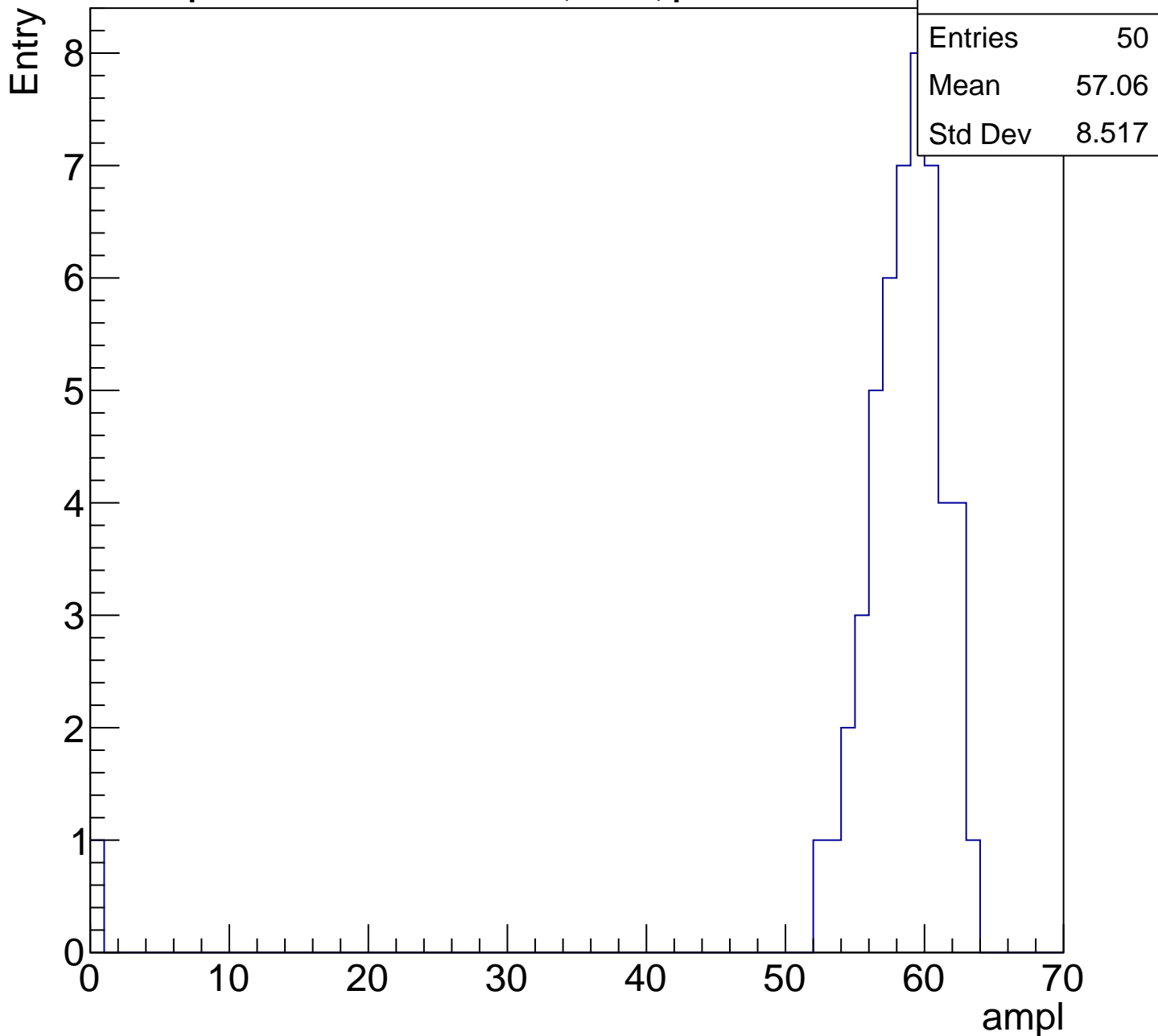
# B1L000S, U2-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

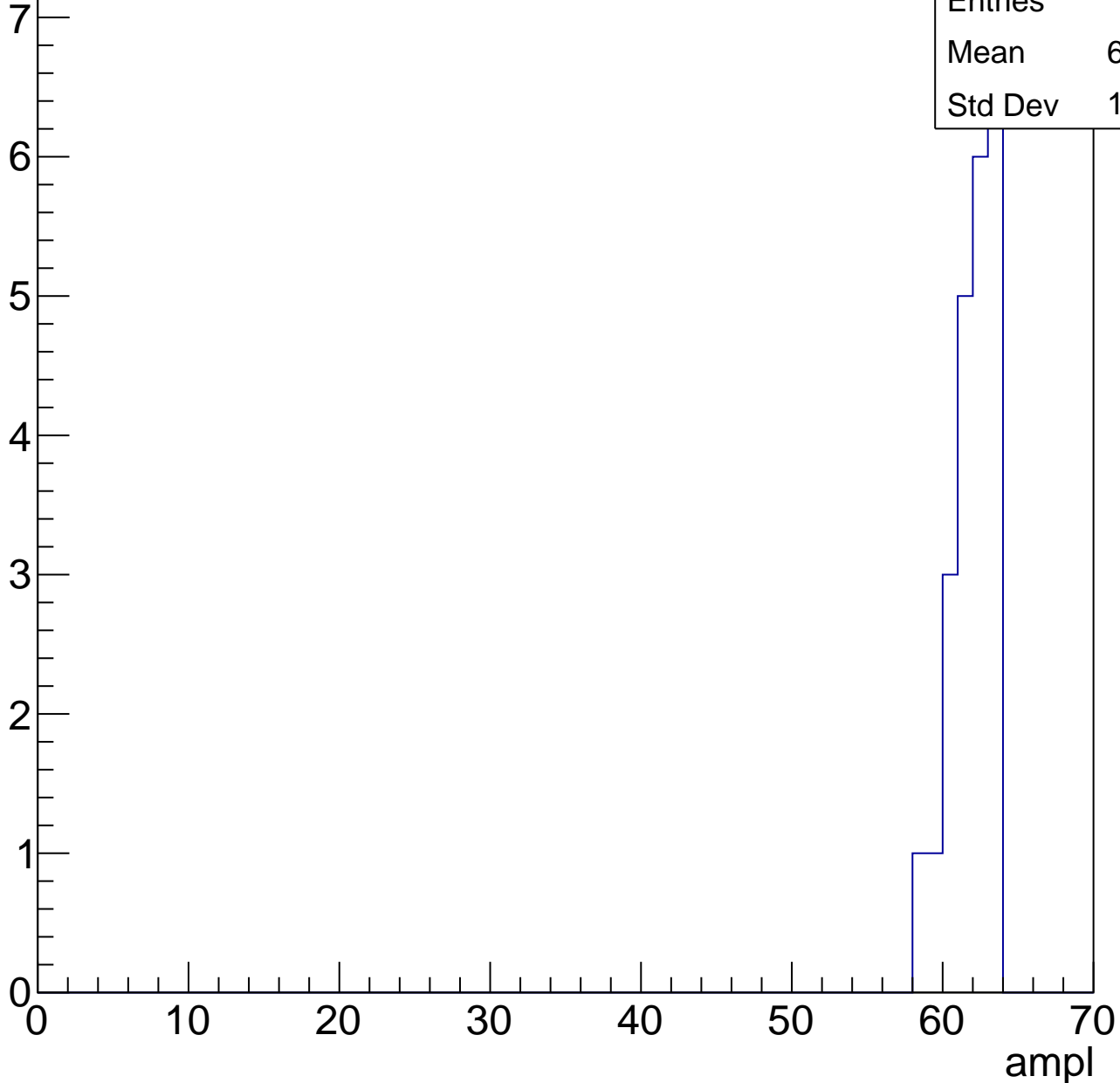


# B1L000S, U2-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	61.52
Std Dev	1.379

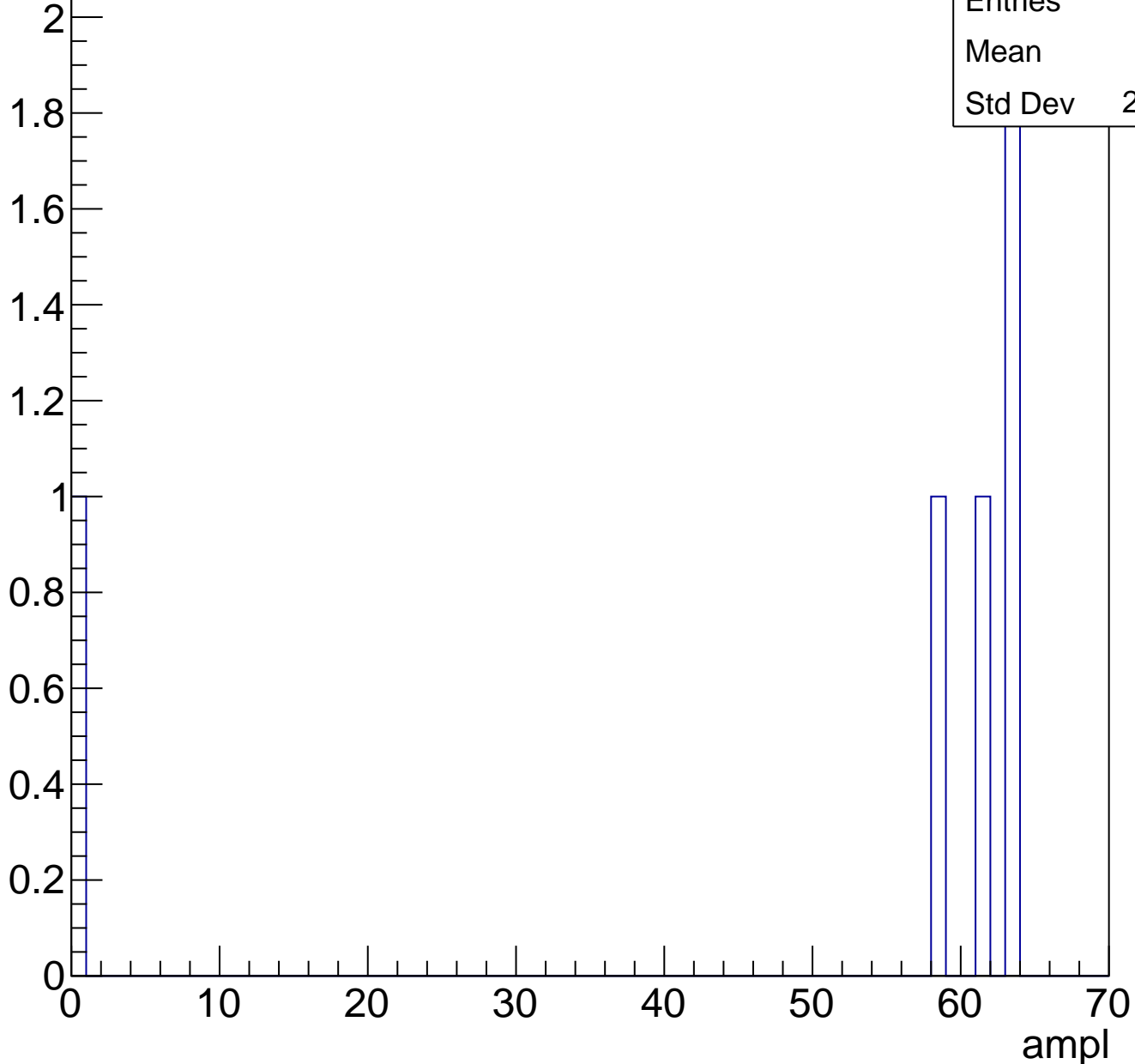




# B1L000S, U2-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch84, adc0

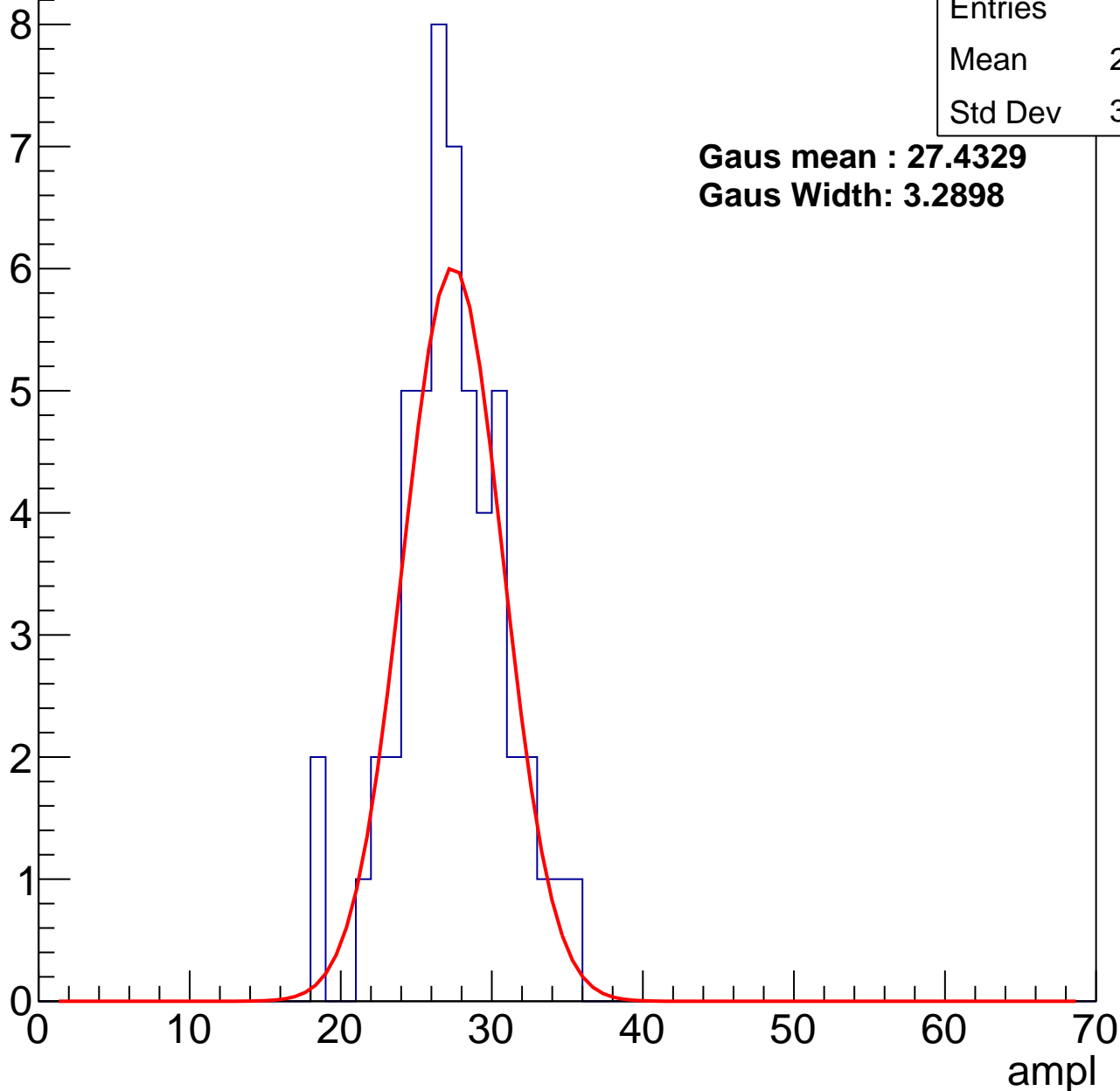
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	26.85
Std Dev	3.499

**Gaus mean : 27.4329**

**Gaus Width: 3.2898**



# B1L000S, U2-ch84, adc1

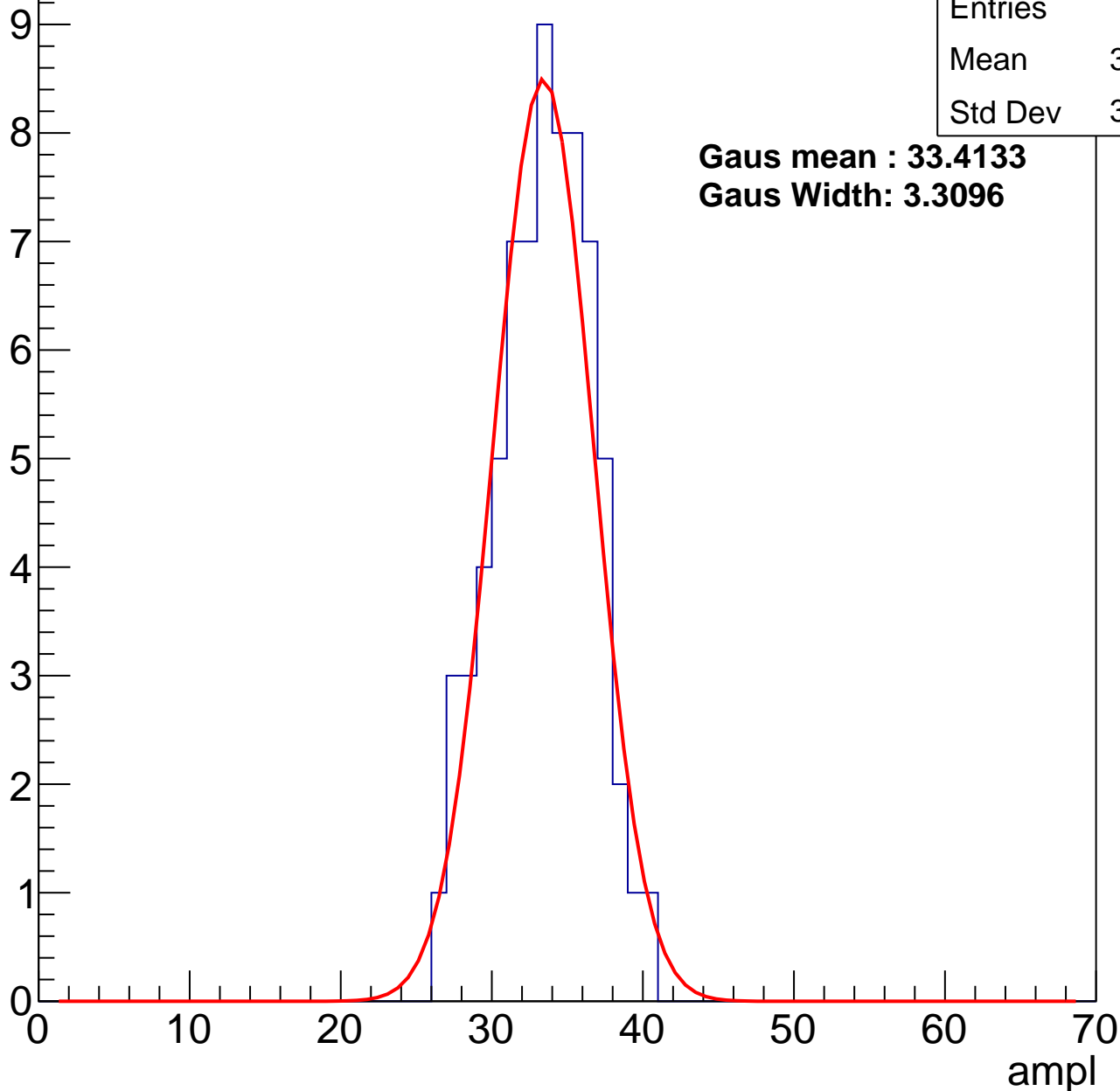
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	32.94
Std Dev	3.126

**Gaus mean : 33.4133**

**Gaus Width: 3.3096**



# B1L000S, U2-ch84, adc2

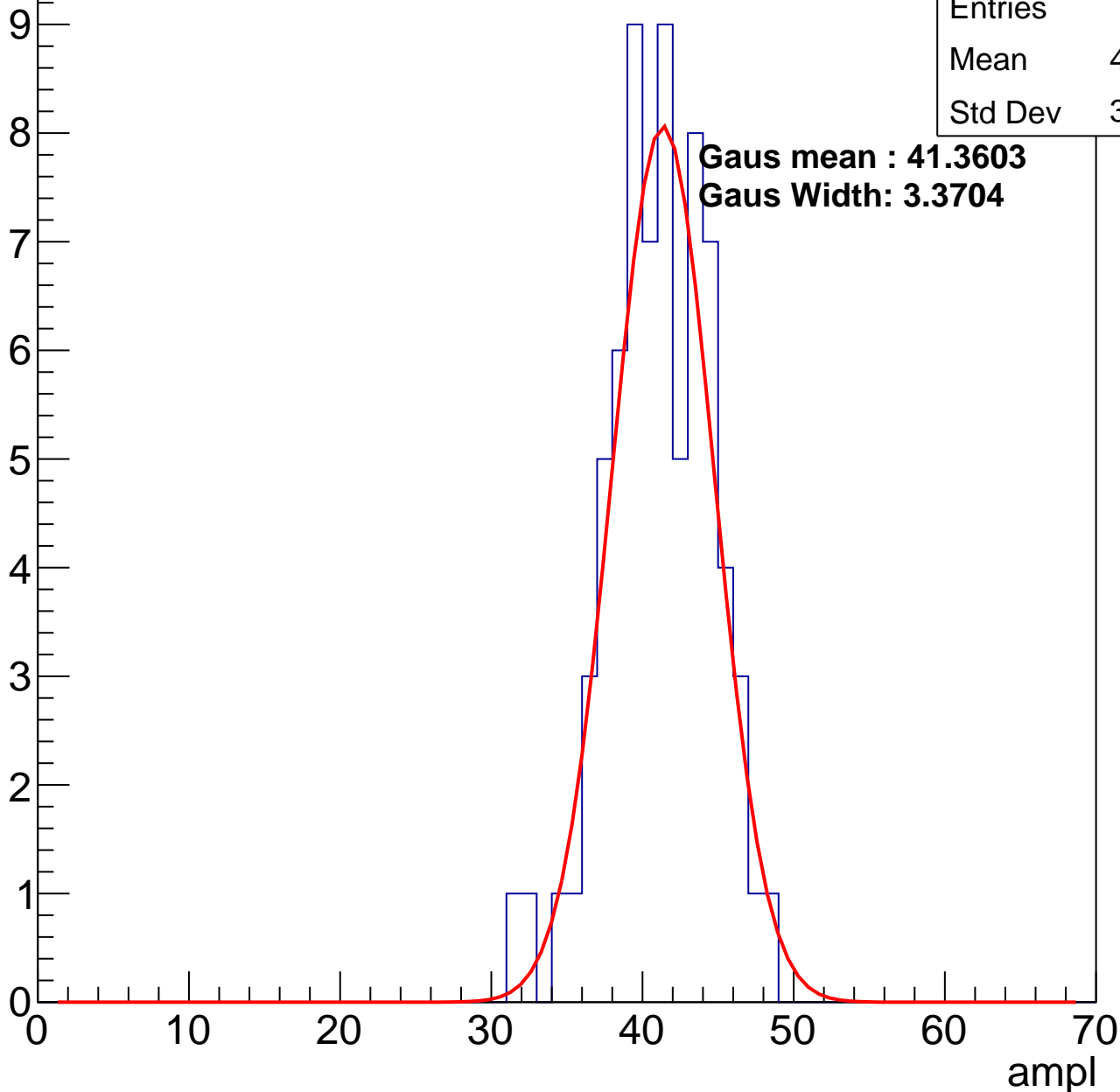
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	40.67
Std Dev	3.403

**Gaus mean : 41.3603**

**Gaus Width: 3.3704**

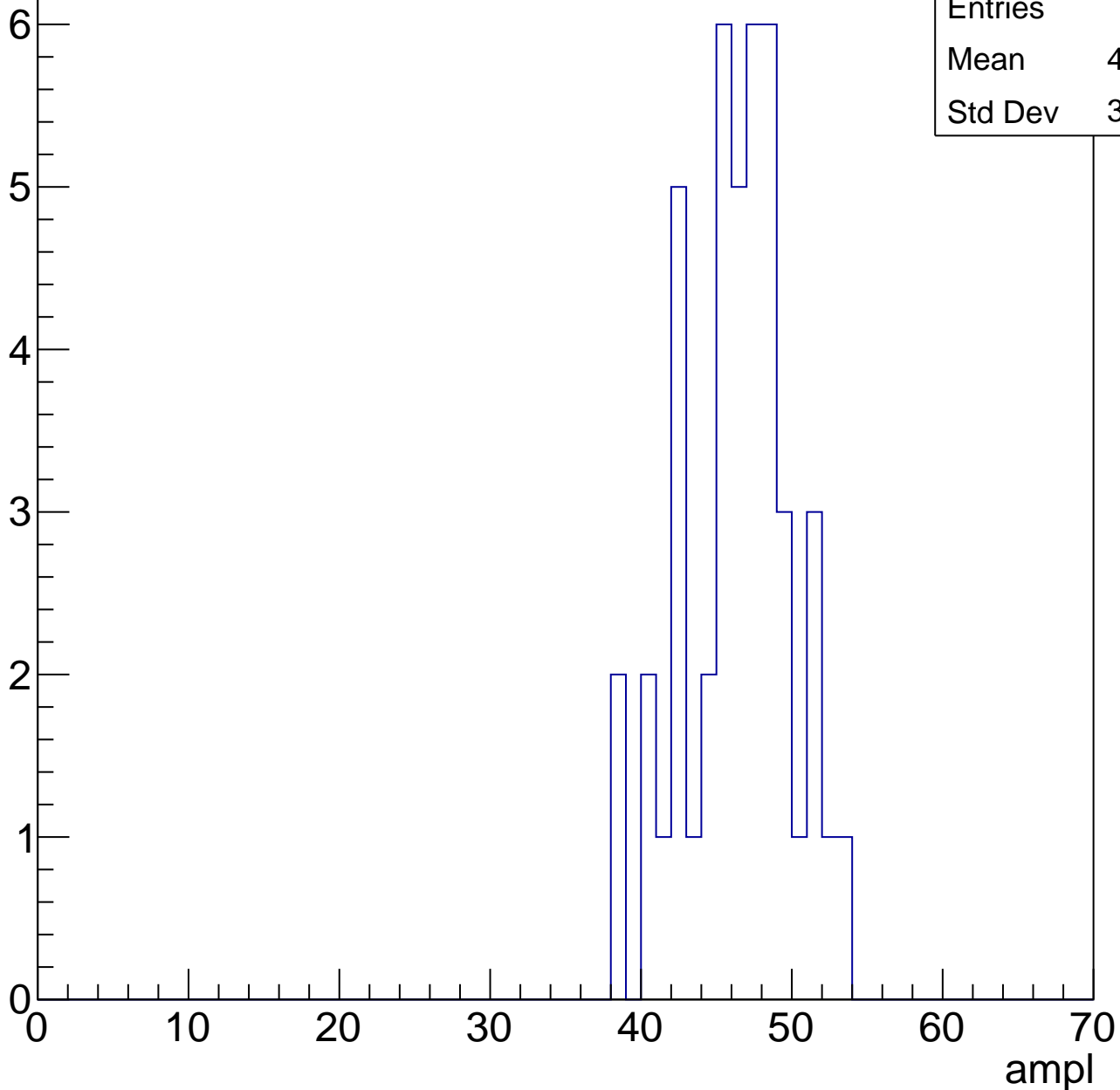


# B1L000S, U2-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	45.84
Std Dev	3.527

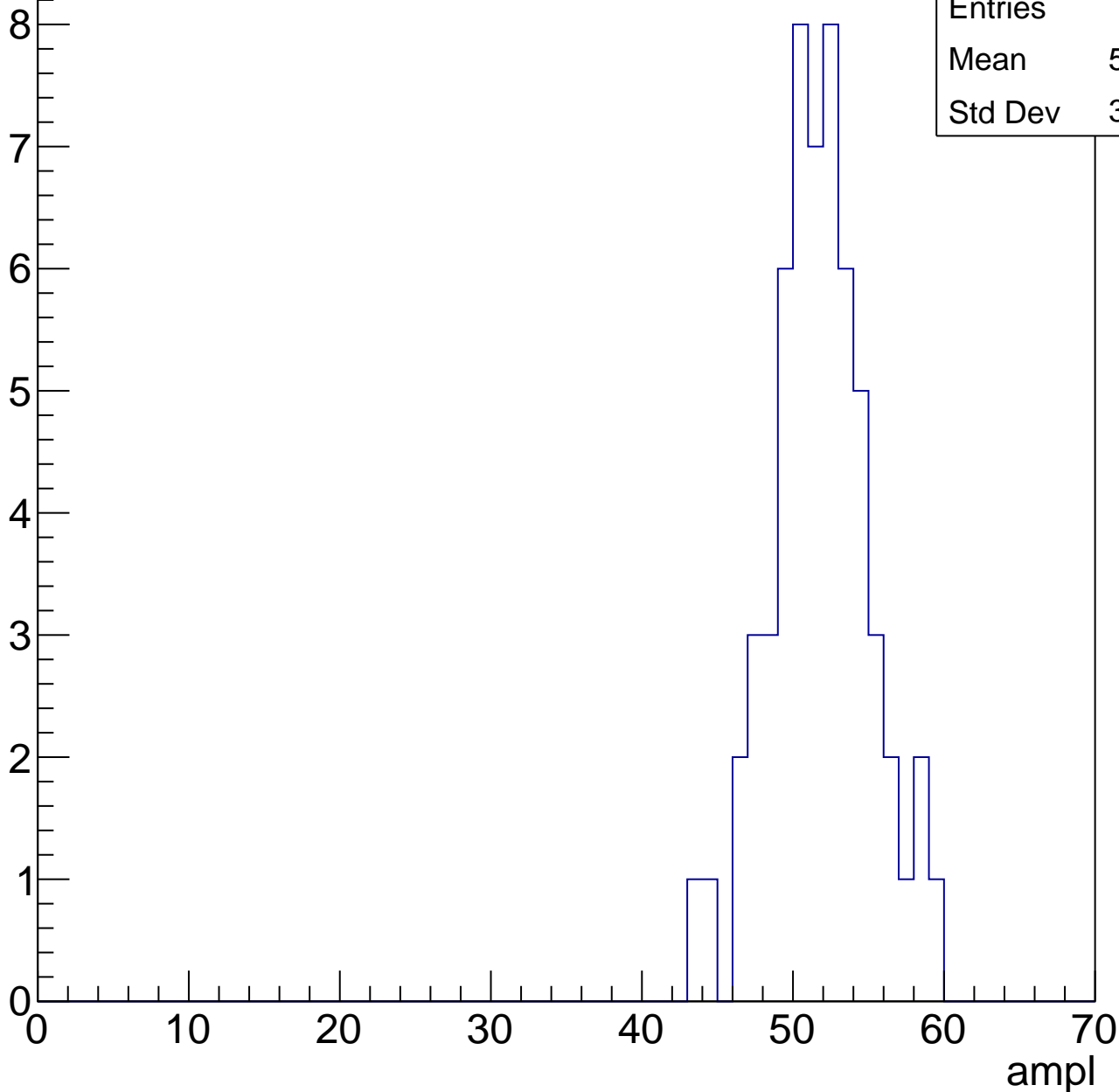


# B1L000S, U2-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

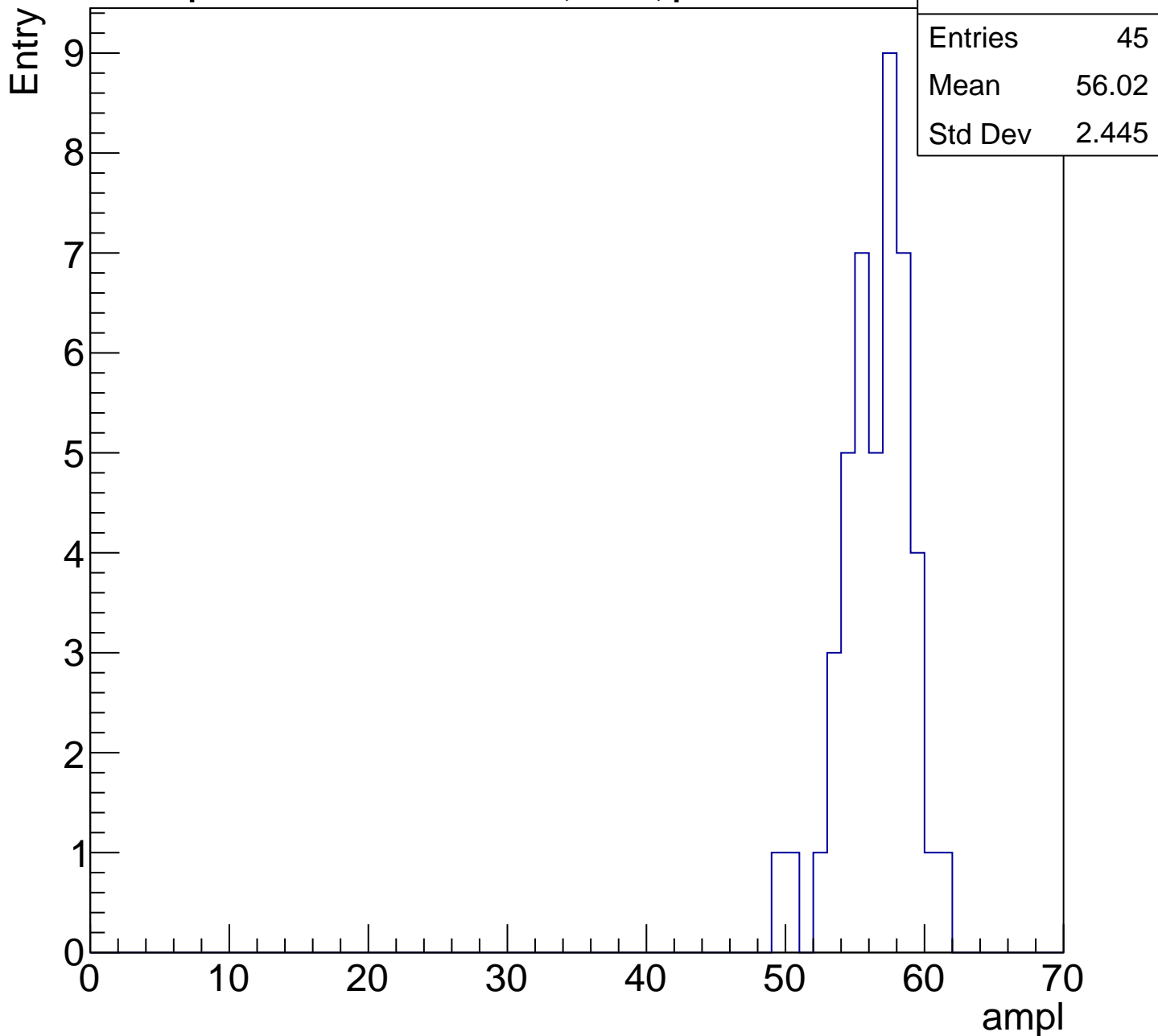
Entry

Entries	59
Mean	51.32
Std Dev	3.286



# B1L000S, U2-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

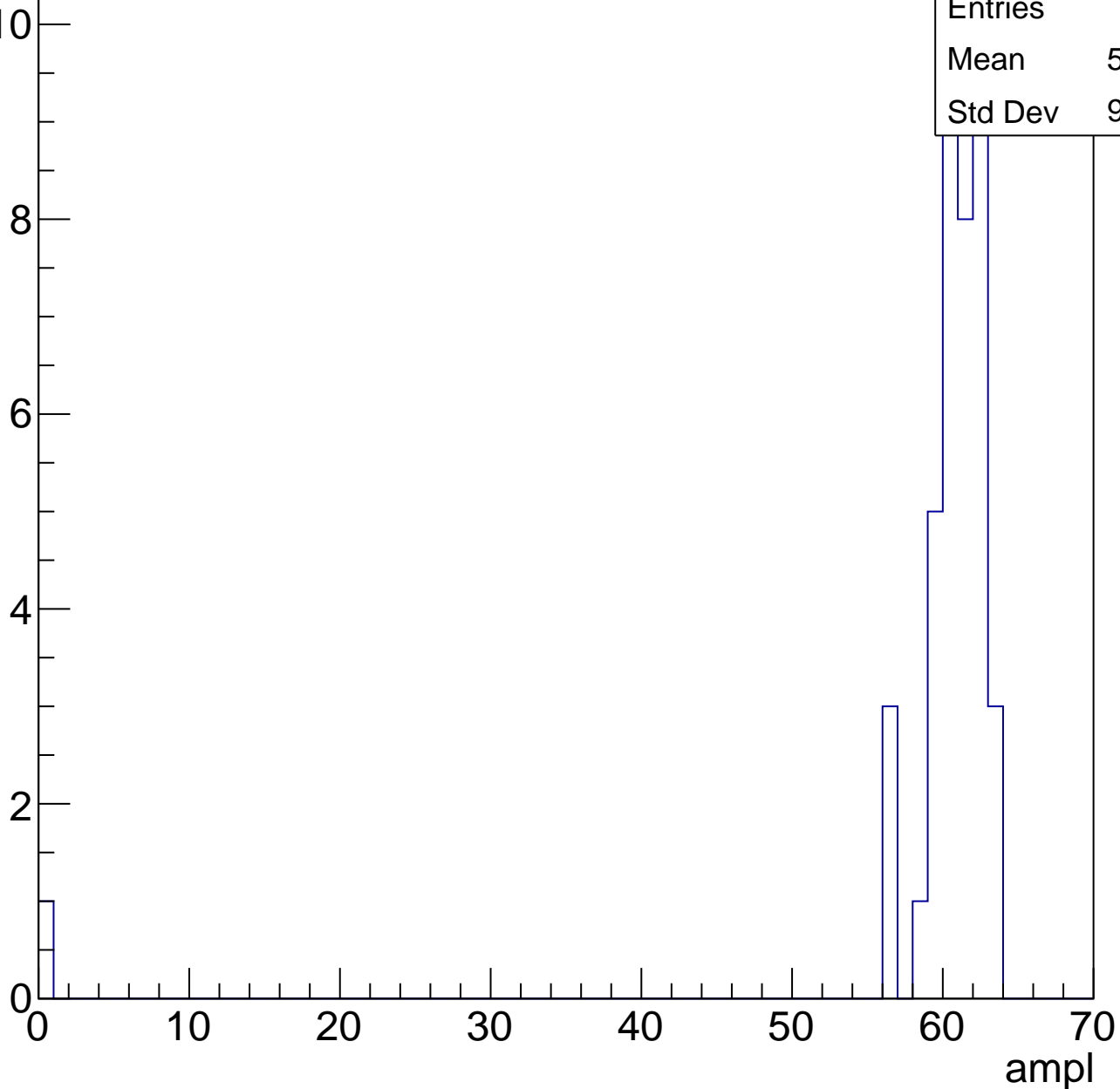


# B1L000S, U2-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	58.95
Std Dev	9.602



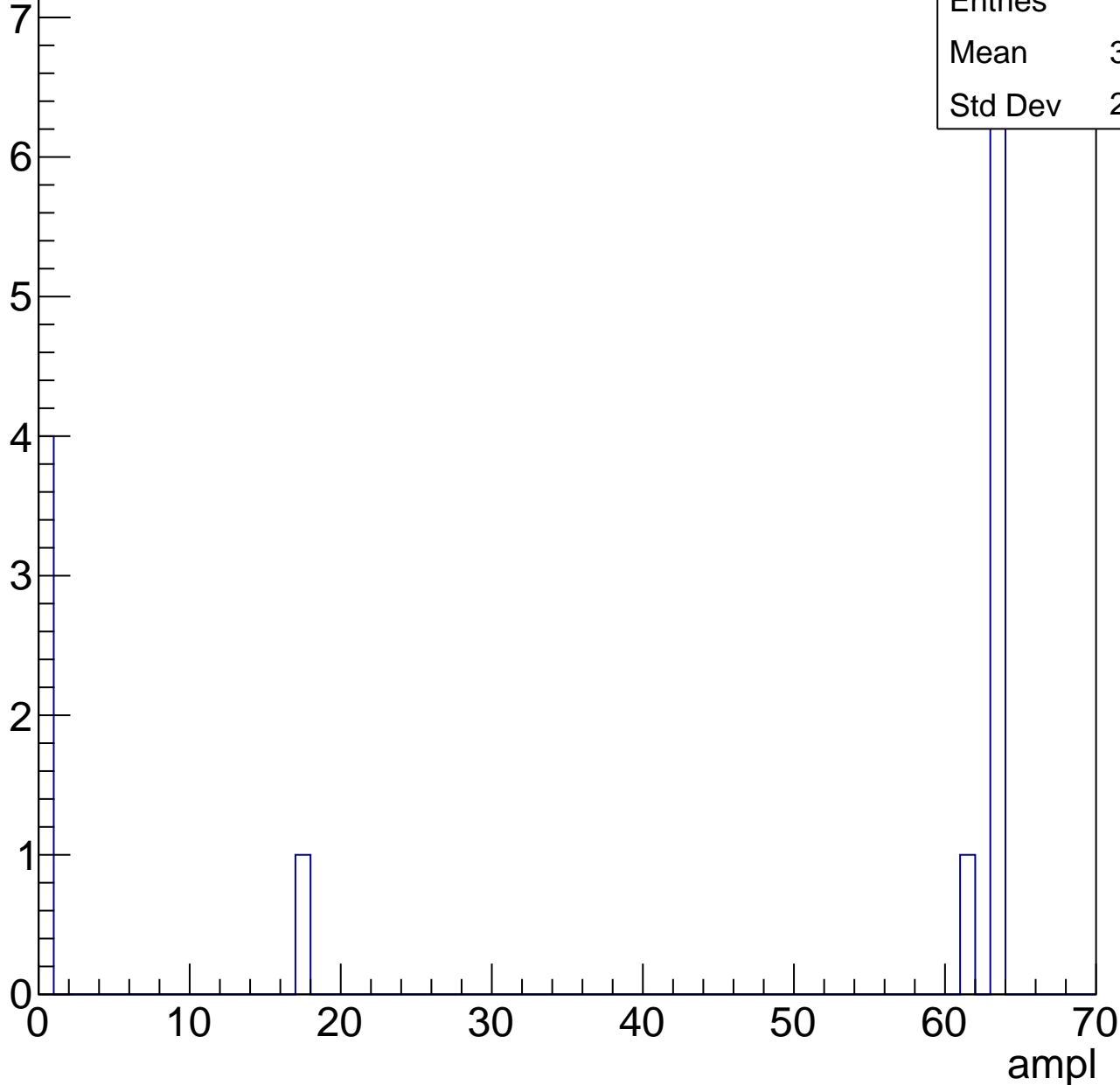


# B1L000S, U2-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	39.92
Std Dev	29.18



# B1L000S, U2-ch85, adc0

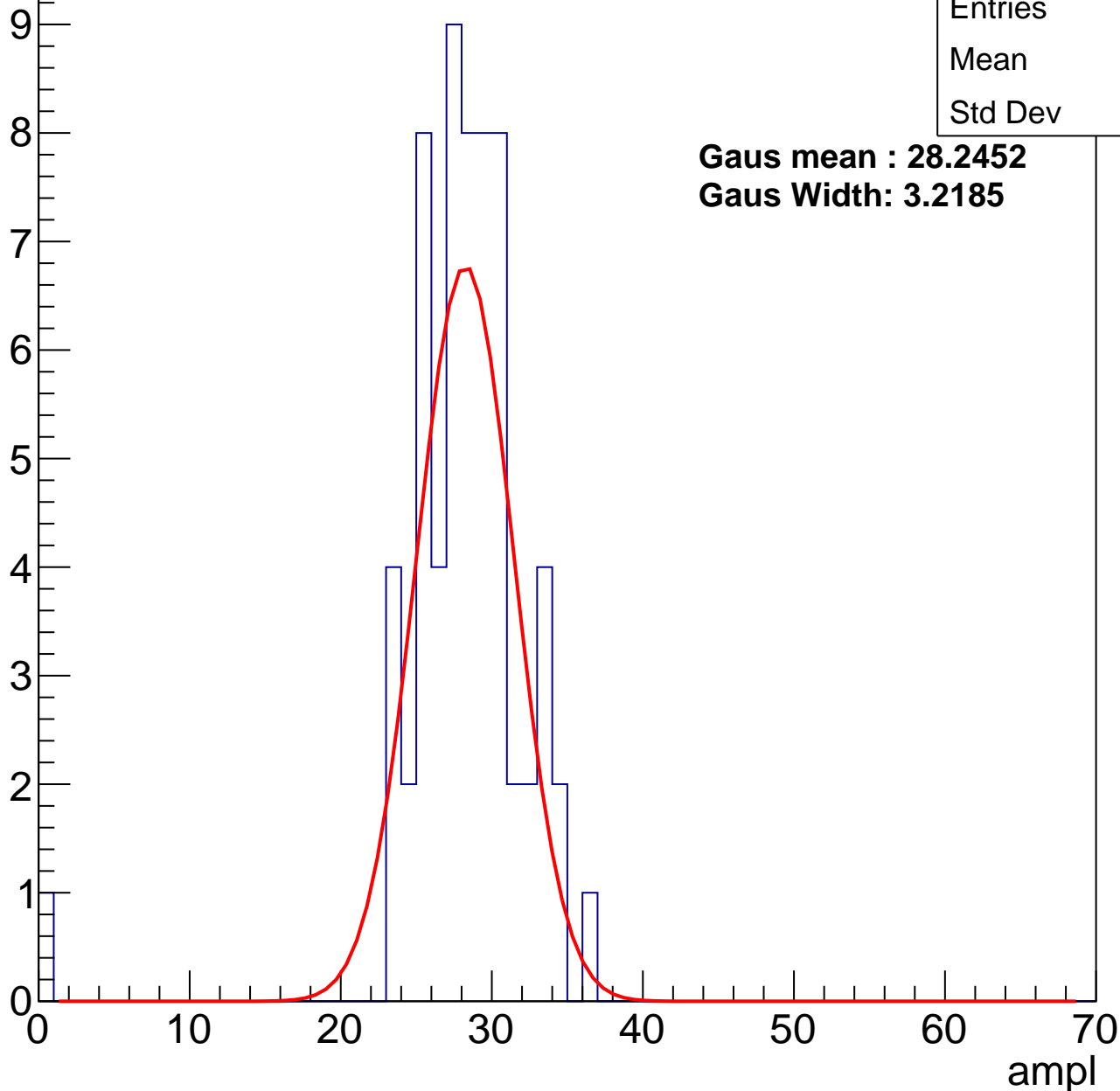
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	27.7
Std Dev	4.6

**Gaus mean : 28.2452**

**Gaus Width: 3.2185**



# B1L000S, U2-ch85, adc1

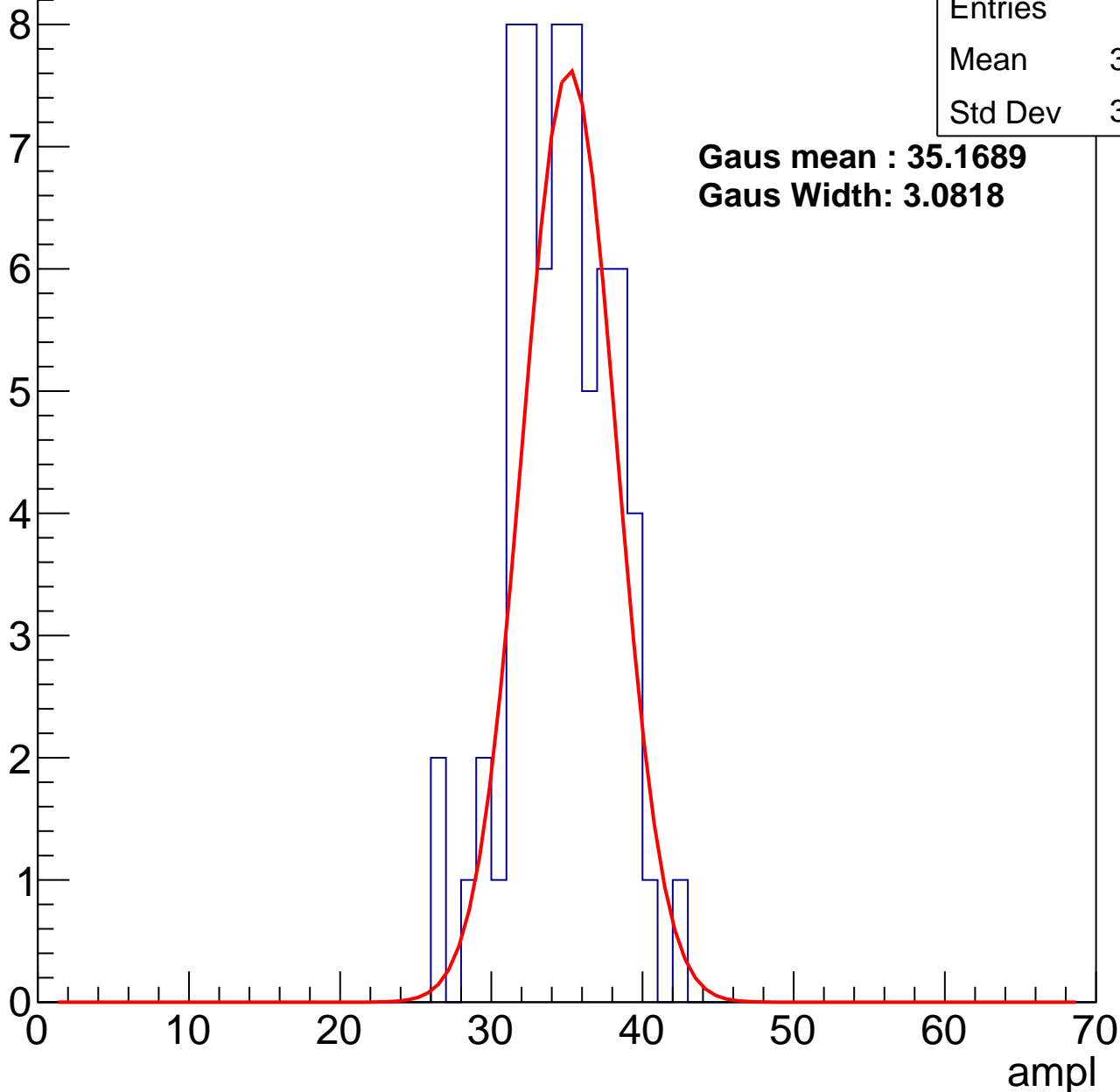
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.18
Std Dev	3.269

**Gaus mean : 35.1689**

**Gaus Width: 3.0818**



# B1L000S, U2-ch85, adc2

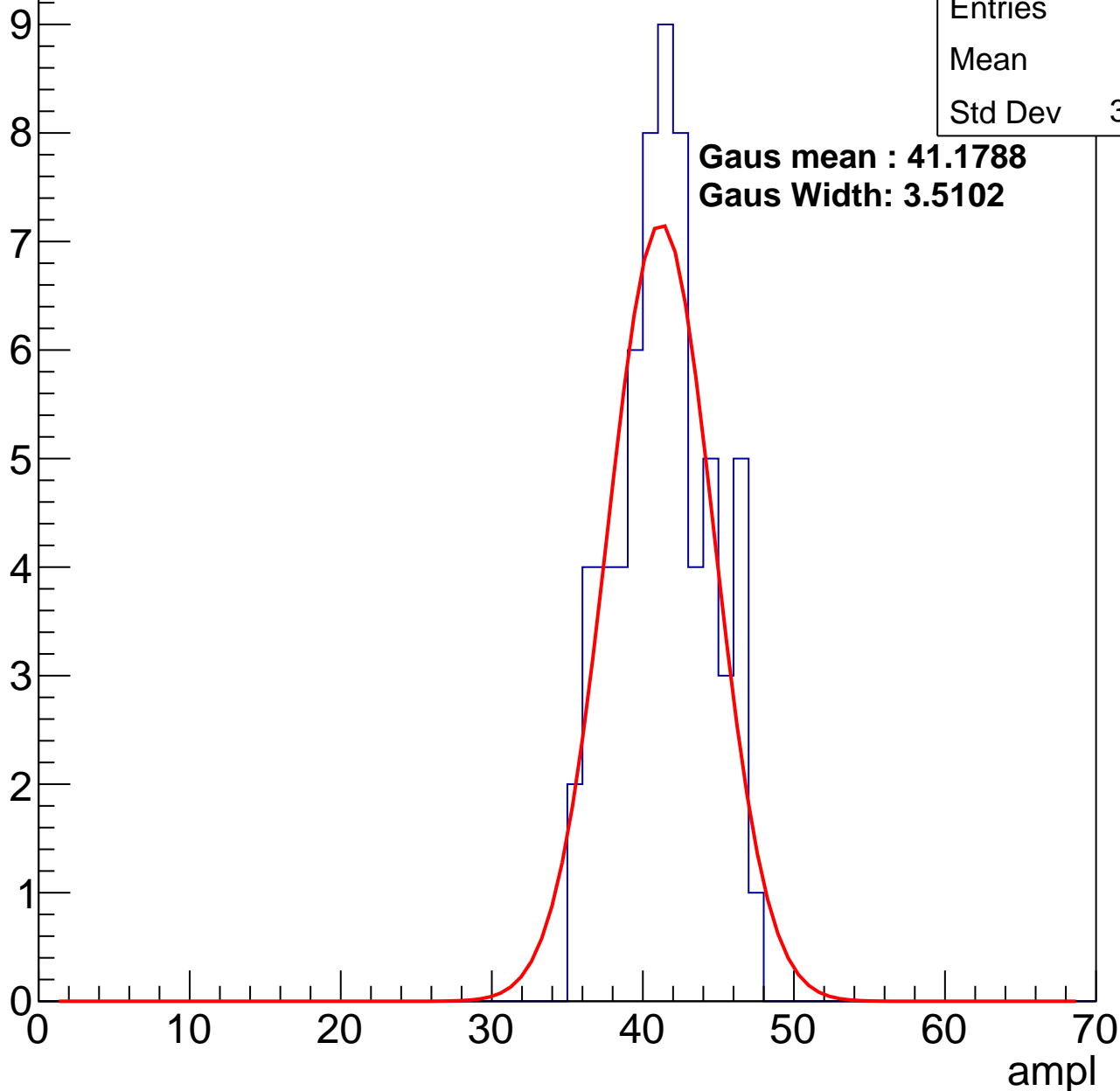
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	40.9
Std Dev	3.038

**Gaus mean : 41.1788**

**Gaus Width: 3.5102**

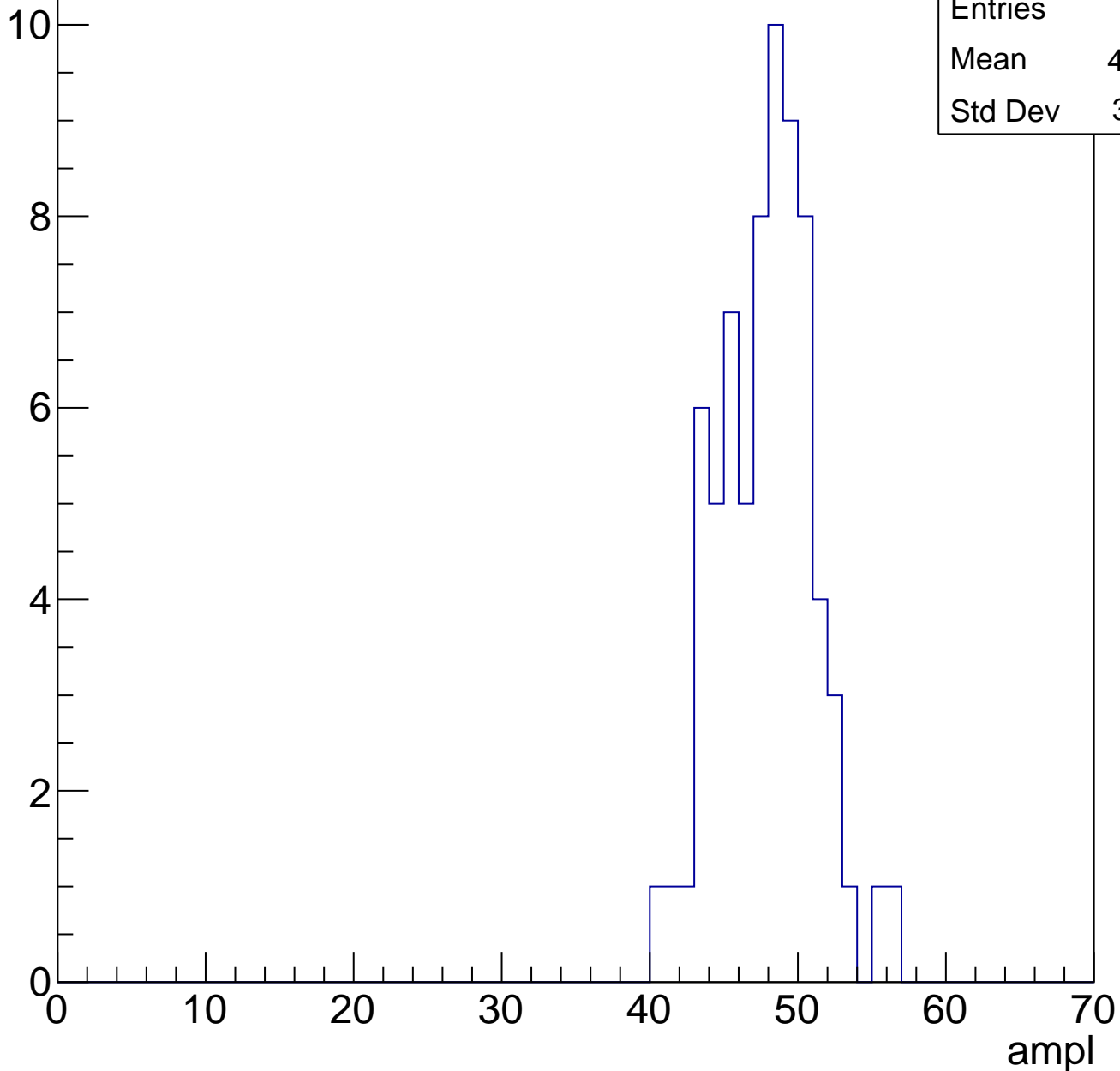


# B1L000S, U2-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	47.42
Std Dev	3.161

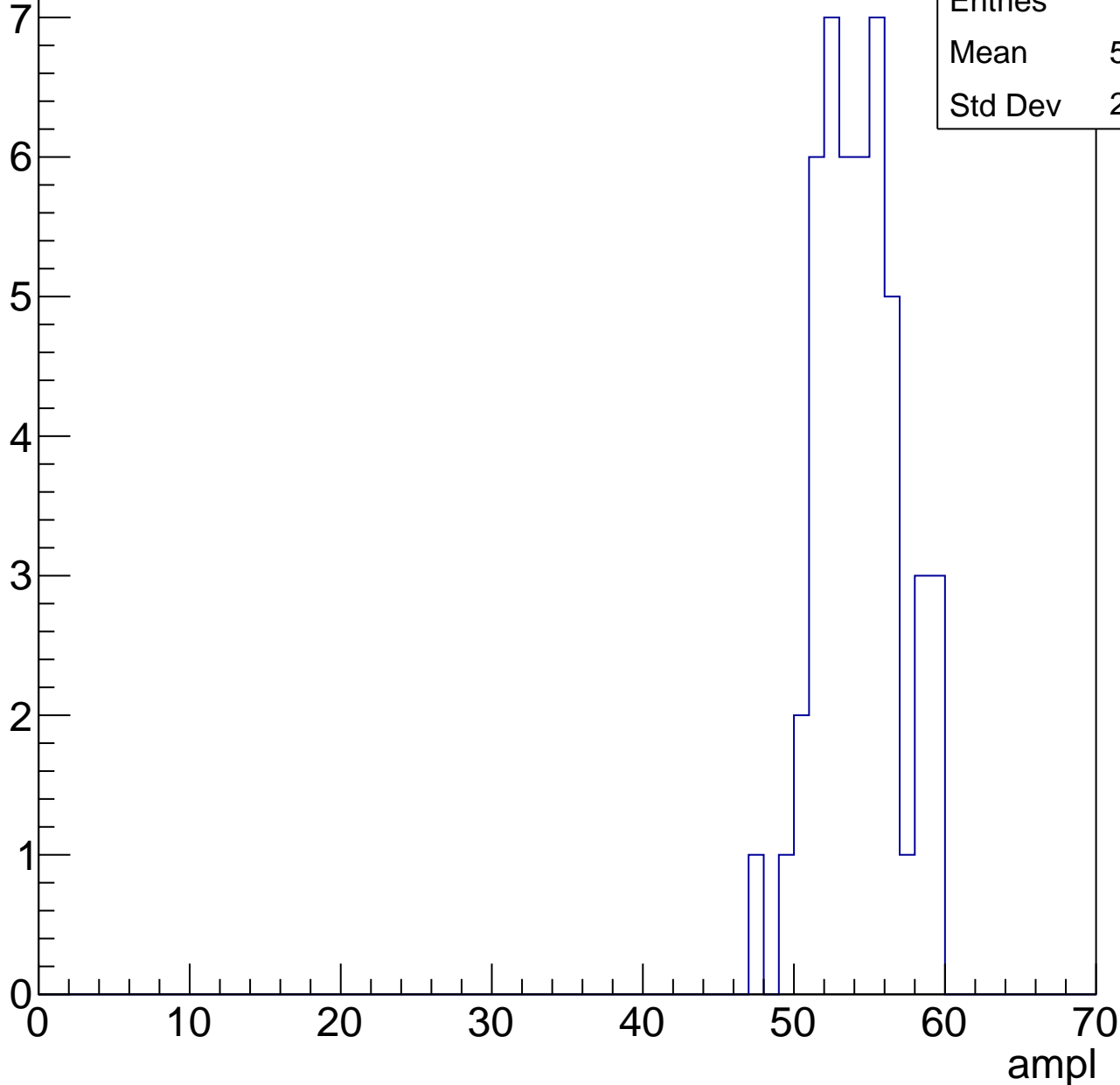
Entry



# B1L000S, U2-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	48
Mean	53.77
Std Dev	2.702

# B1L000S, U2-ch85, adc5

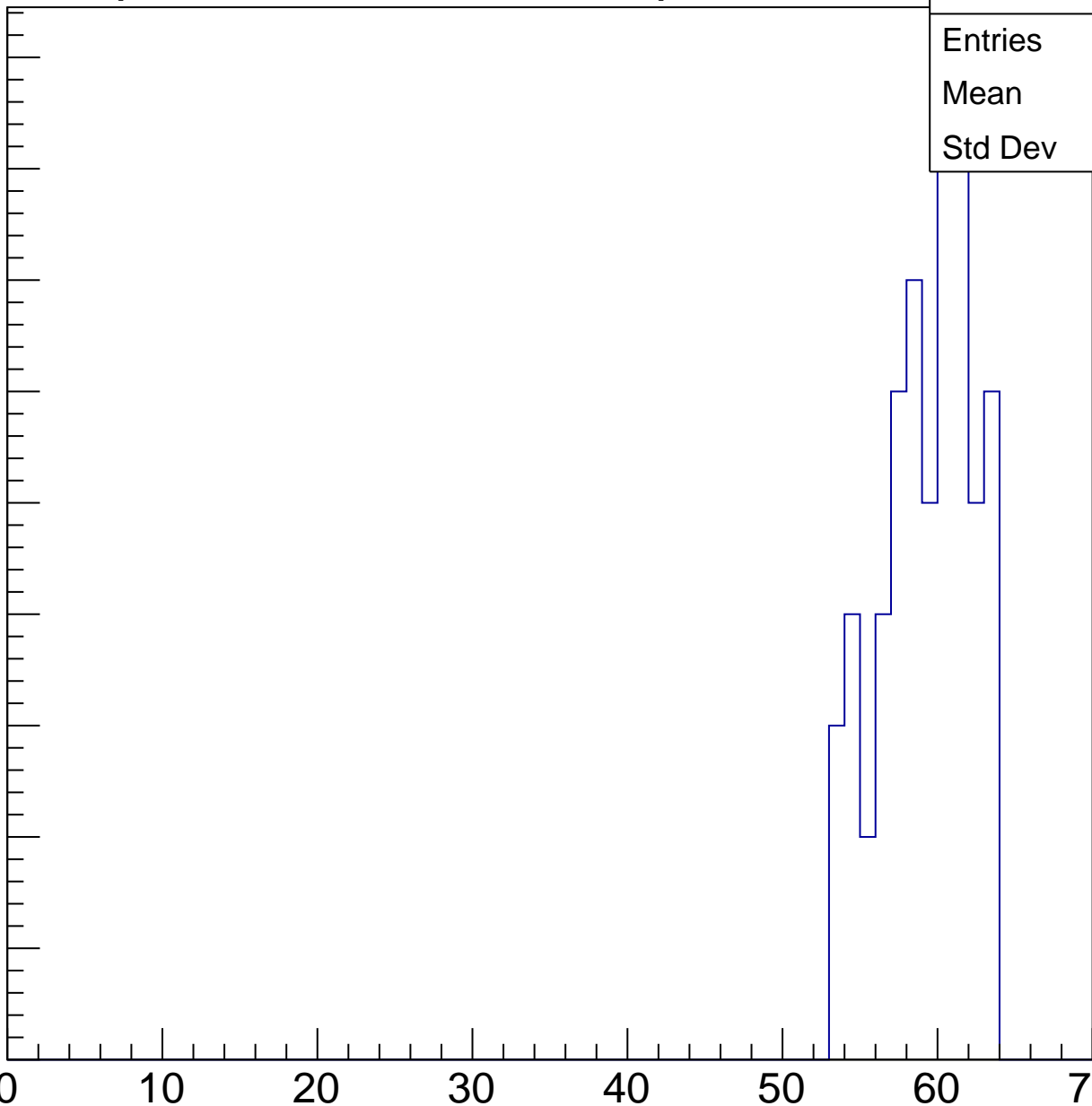
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	58.78
Std Dev	2.87

ampl

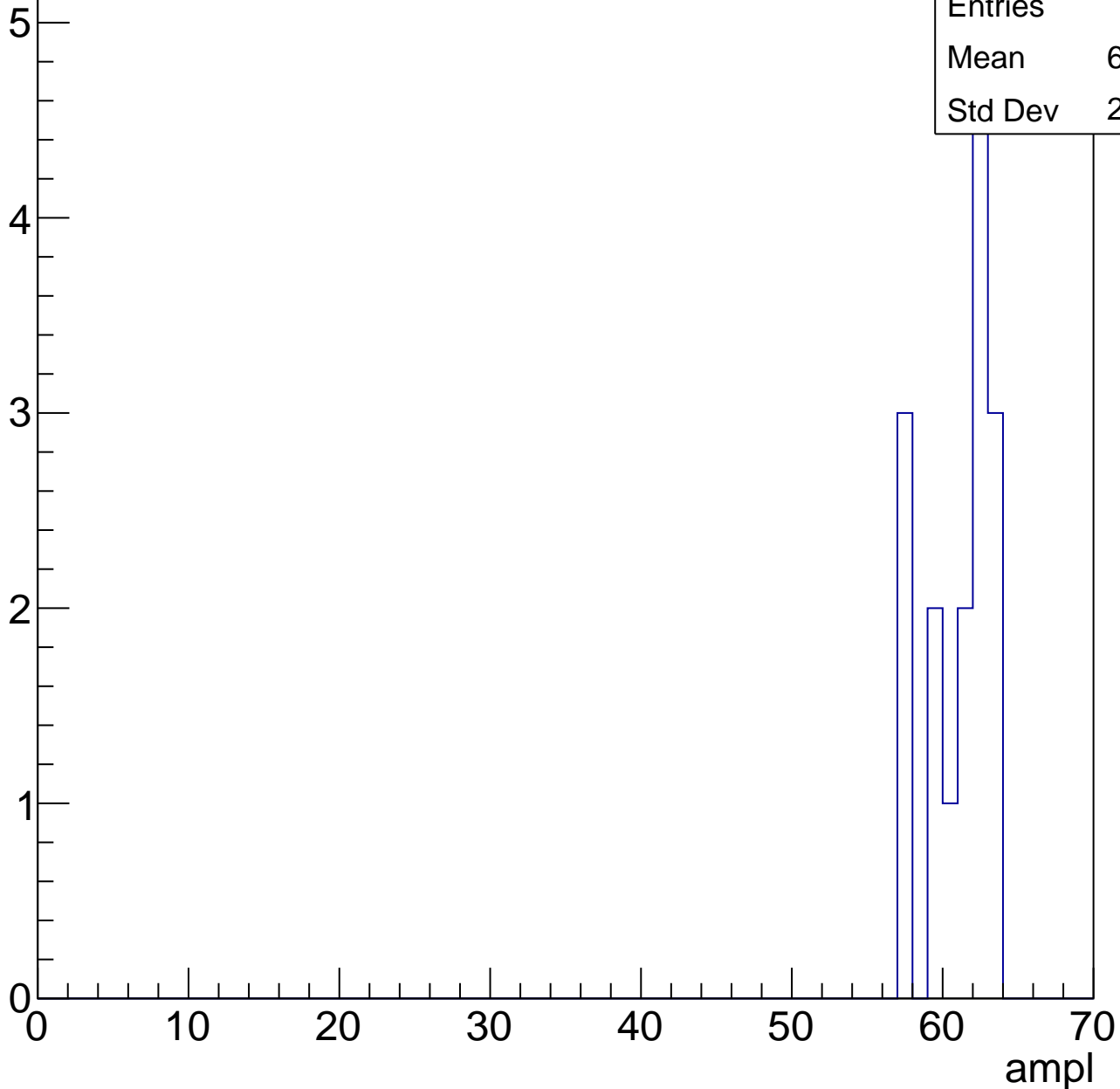


# B1L000S, U2-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	60.62
Std Dev	2.118





# B1L000S, U2-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch86, adc0

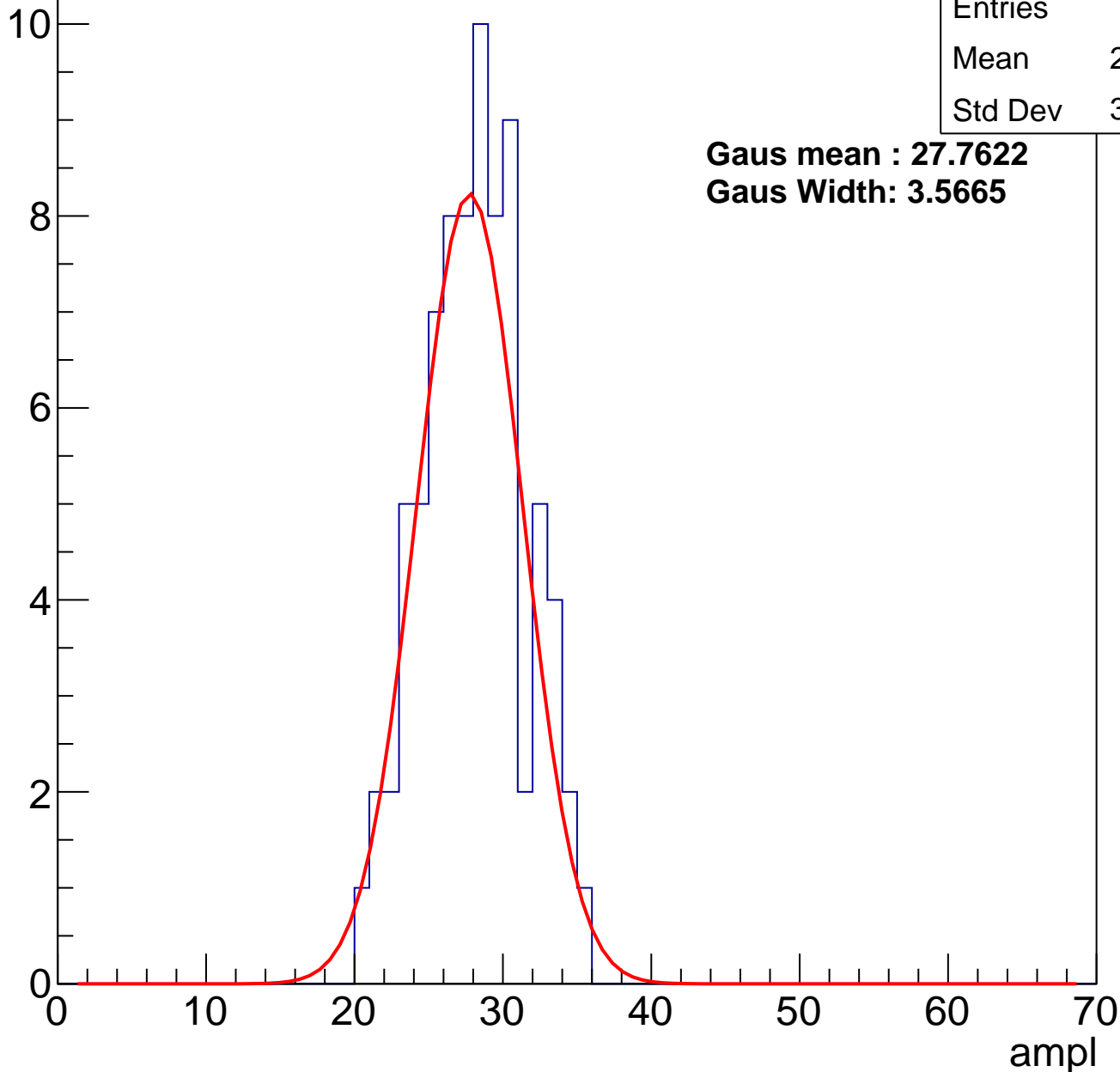
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	27.58
Std Dev	3.359

**Gaus mean : 27.7622**

**Gaus Width: 3.5665**

Entry



# B1L000S, U2-ch86, adc1

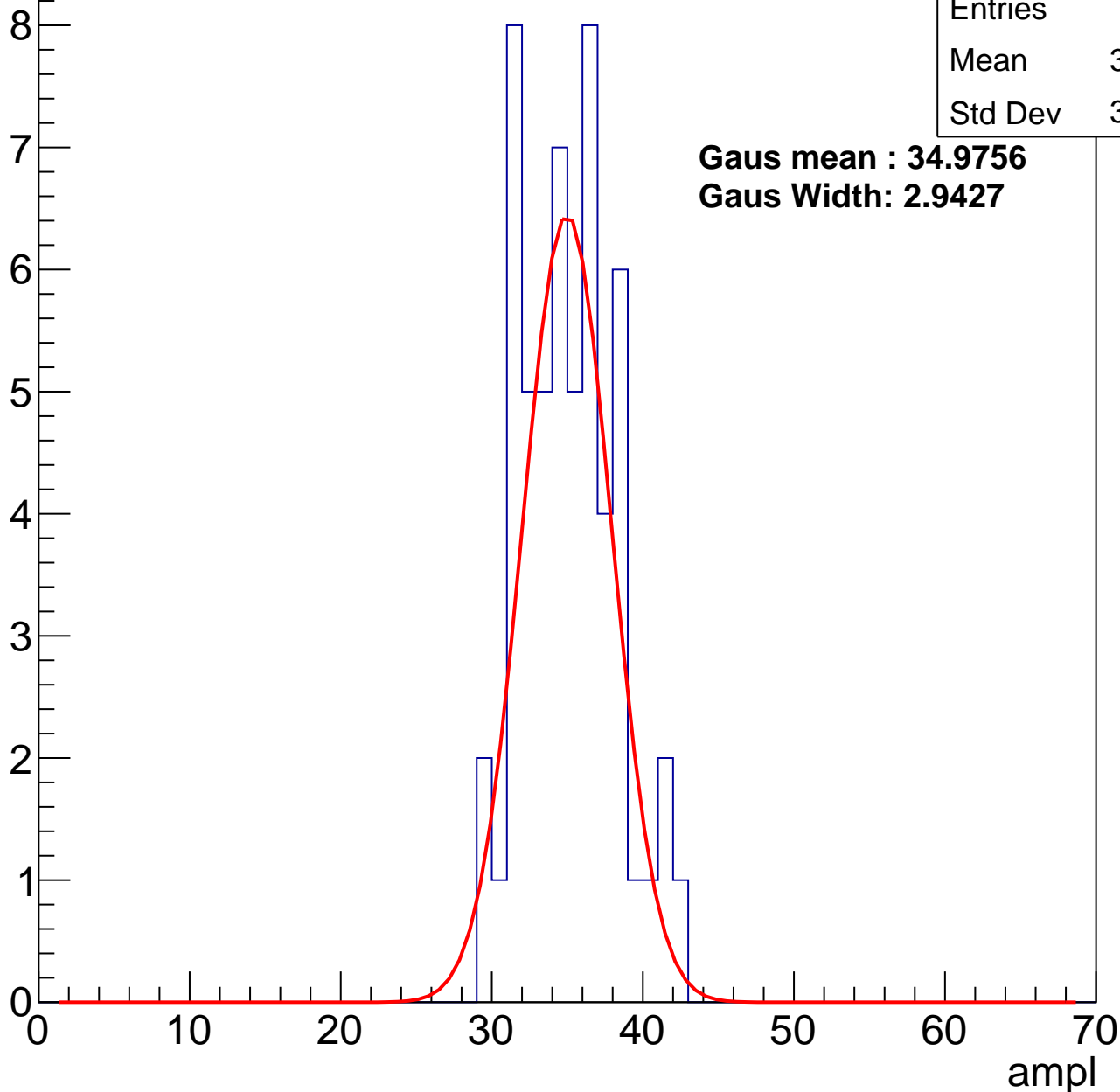
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	34.66
Std Dev	3.078

**Gaus mean : 34.9756**

**Gaus Width: 2.9427**



# B1L000S, U2-ch86, adc2

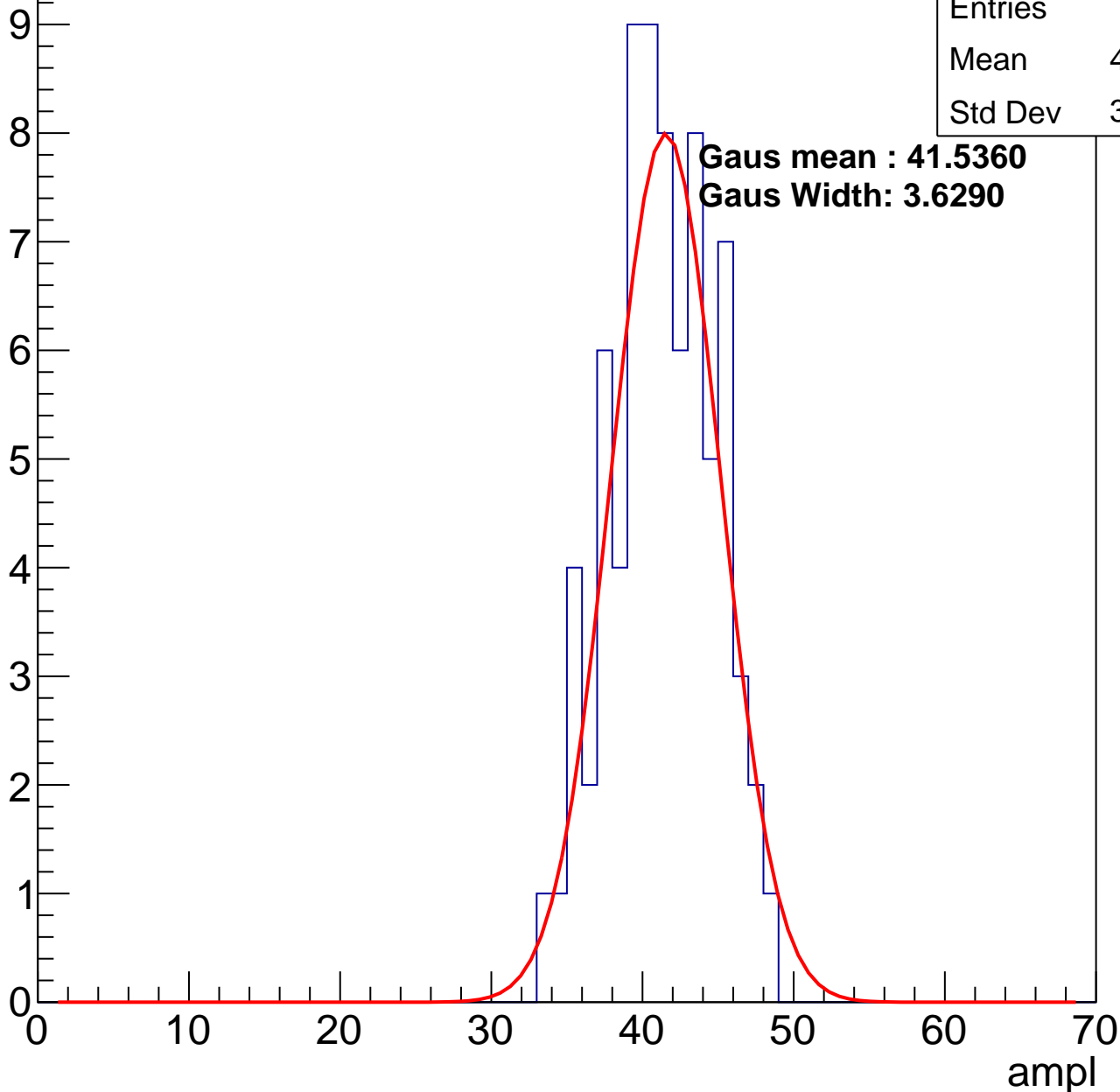
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	40.83
Std Dev	3.389

**Gaus mean : 41.5360**

**Gaus Width: 3.6290**

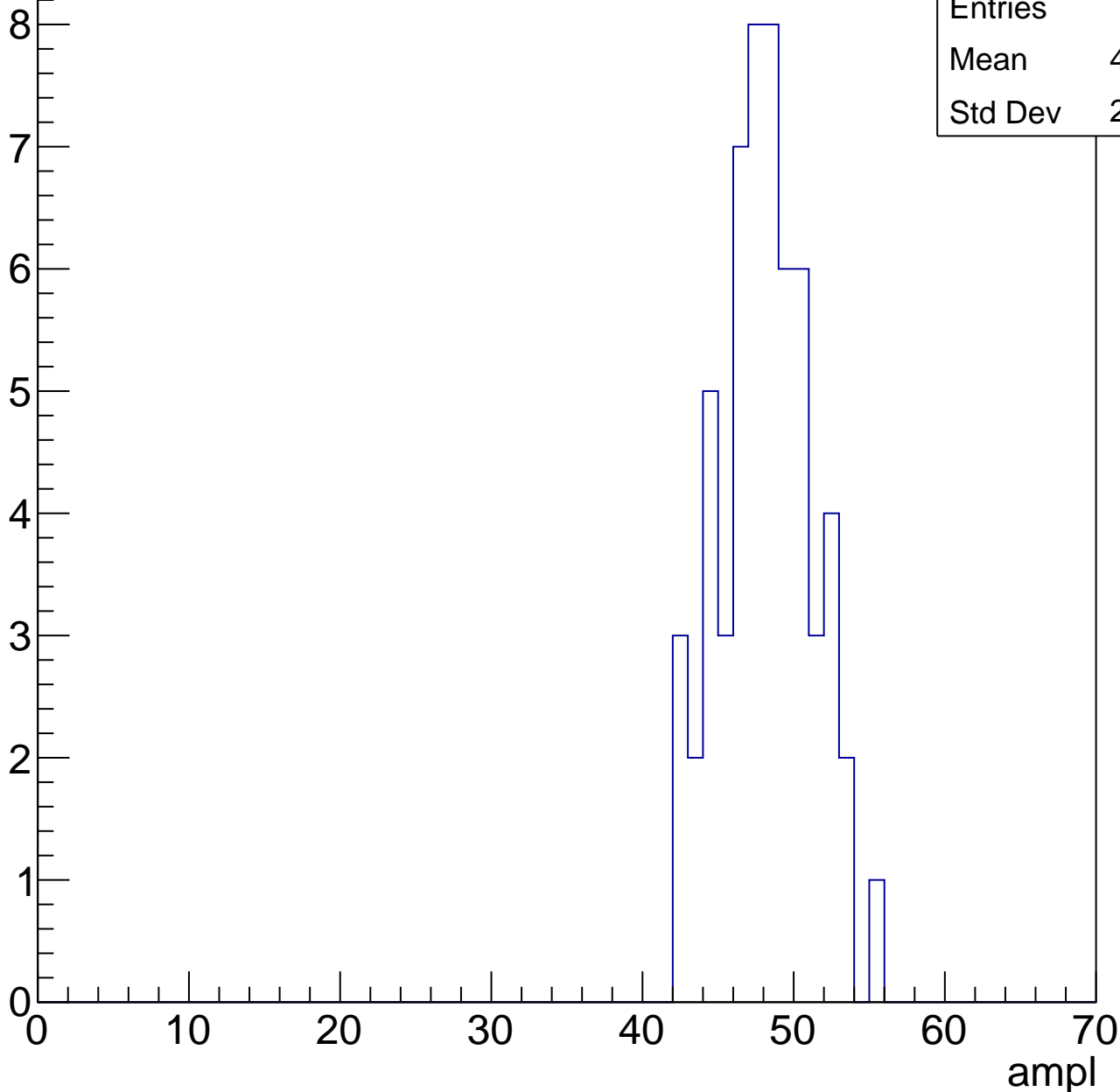


# B1L000S, U2-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

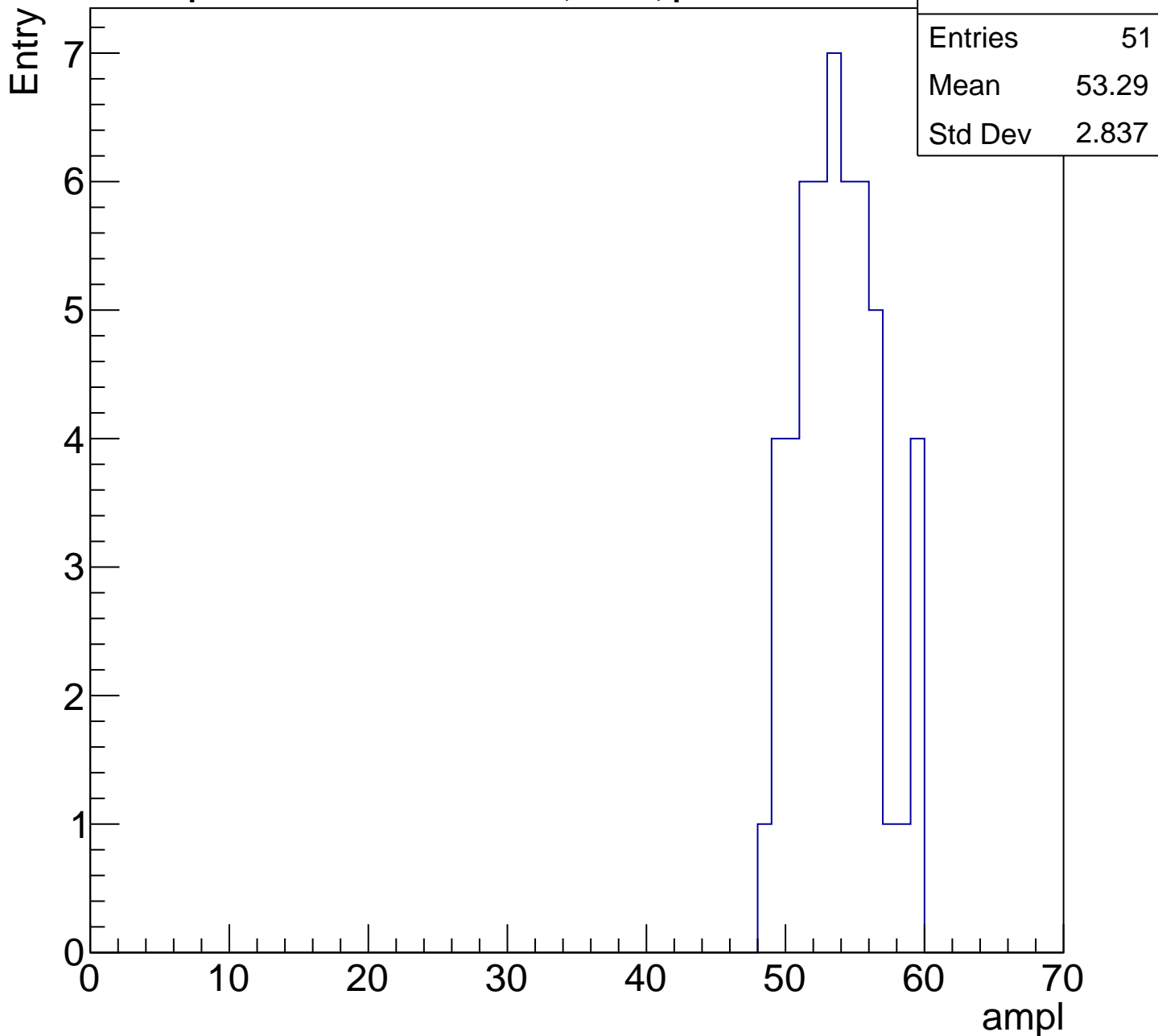
Entry

Entries	58
Mean	47.67
Std Dev	2.979



# B1L000S, U2-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

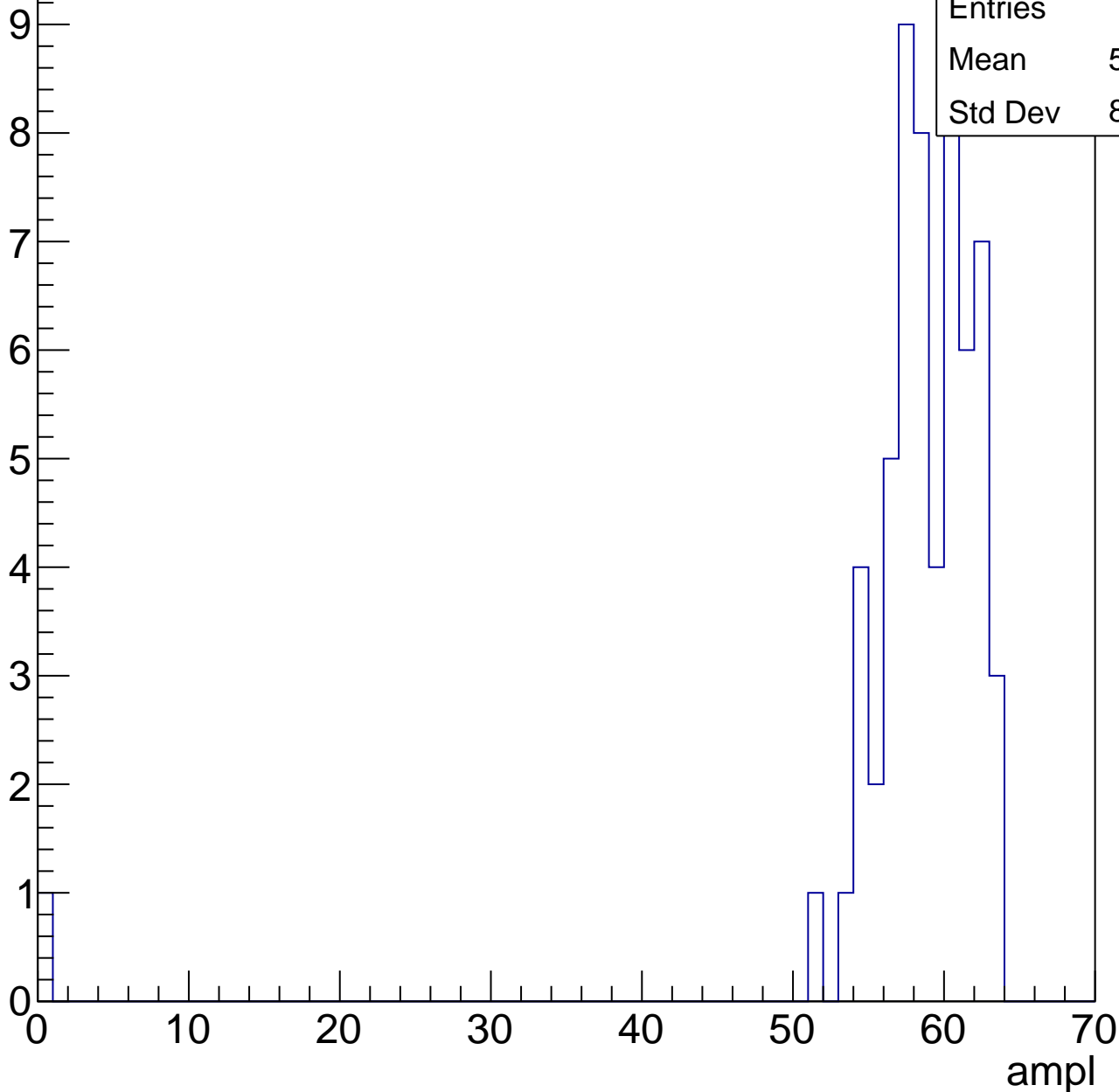


# B1L000S, U2-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.49
Std Dev	8.037

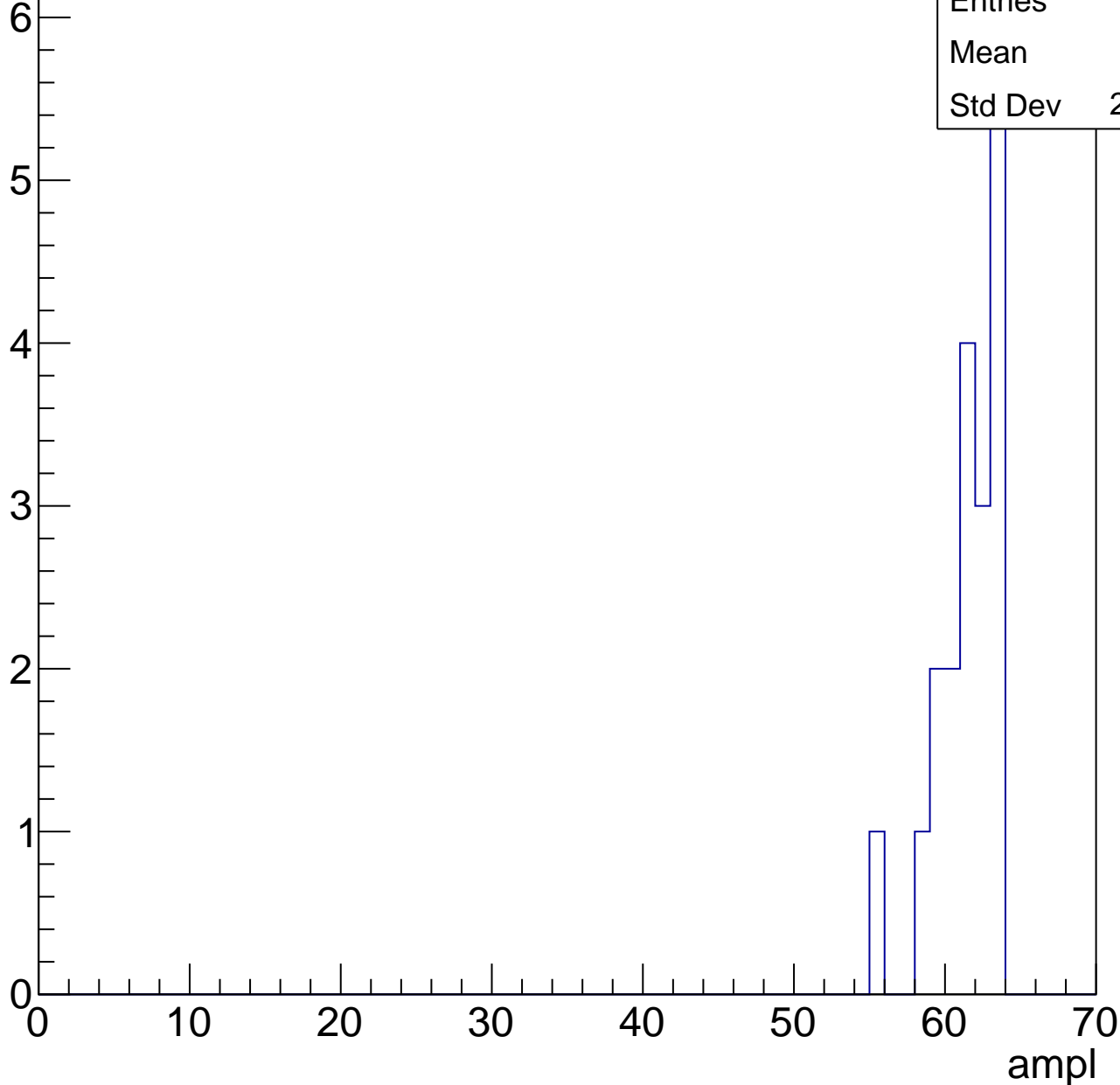


# B1L000S, U2-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	61
Std Dev	2.077





# B1L000S, U2-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch87, adc0

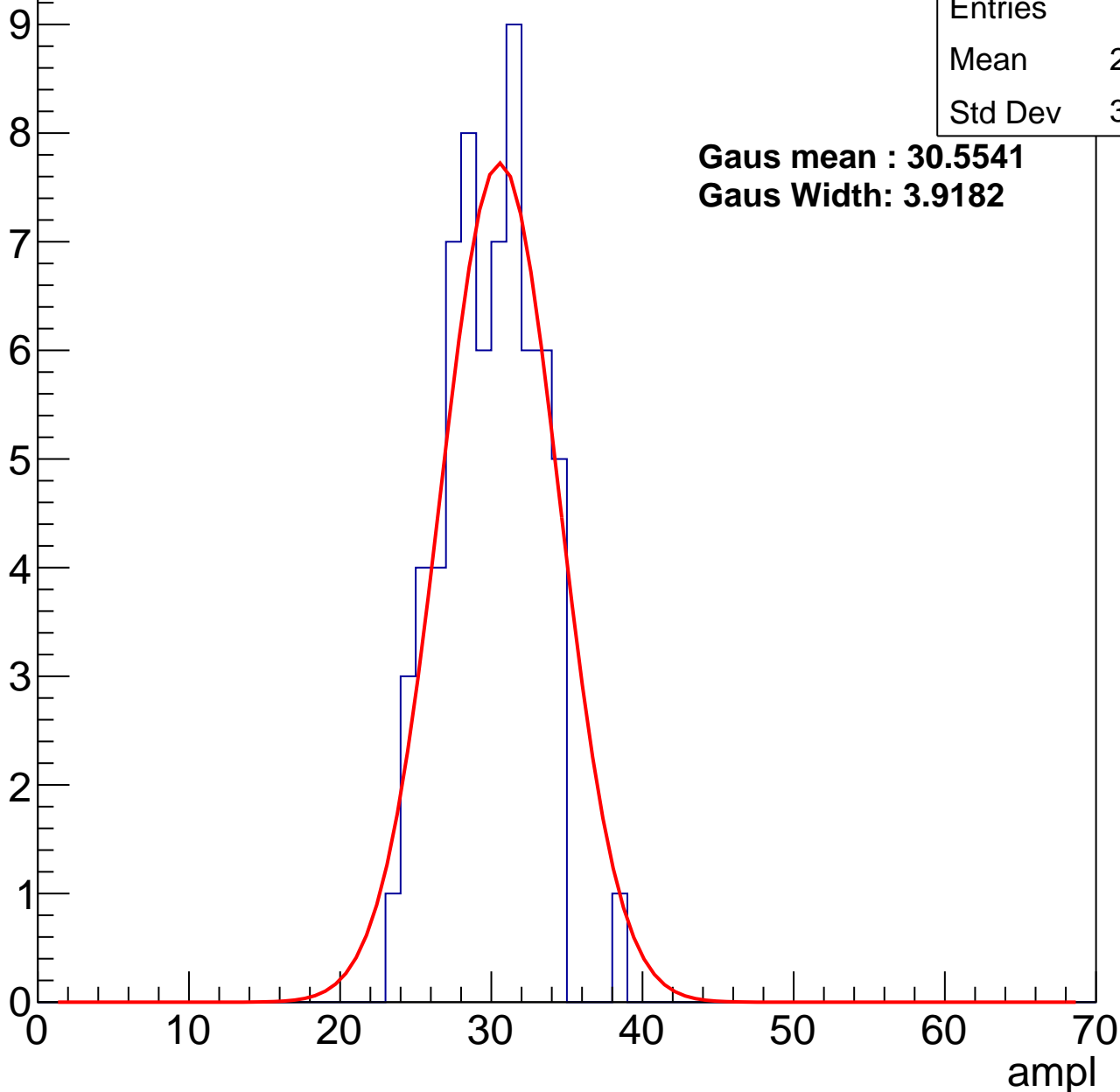
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.45
Std Dev	3.073

**Gaus mean : 30.5541**

**Gaus Width: 3.9182**



# B1L000S, U2-ch87, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	51
Mean	34.16
Std Dev	3.398

**Gaus mean : 35.9332**

**Gaus Width: 2.7113**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B1L000S, U2-ch87, adc2

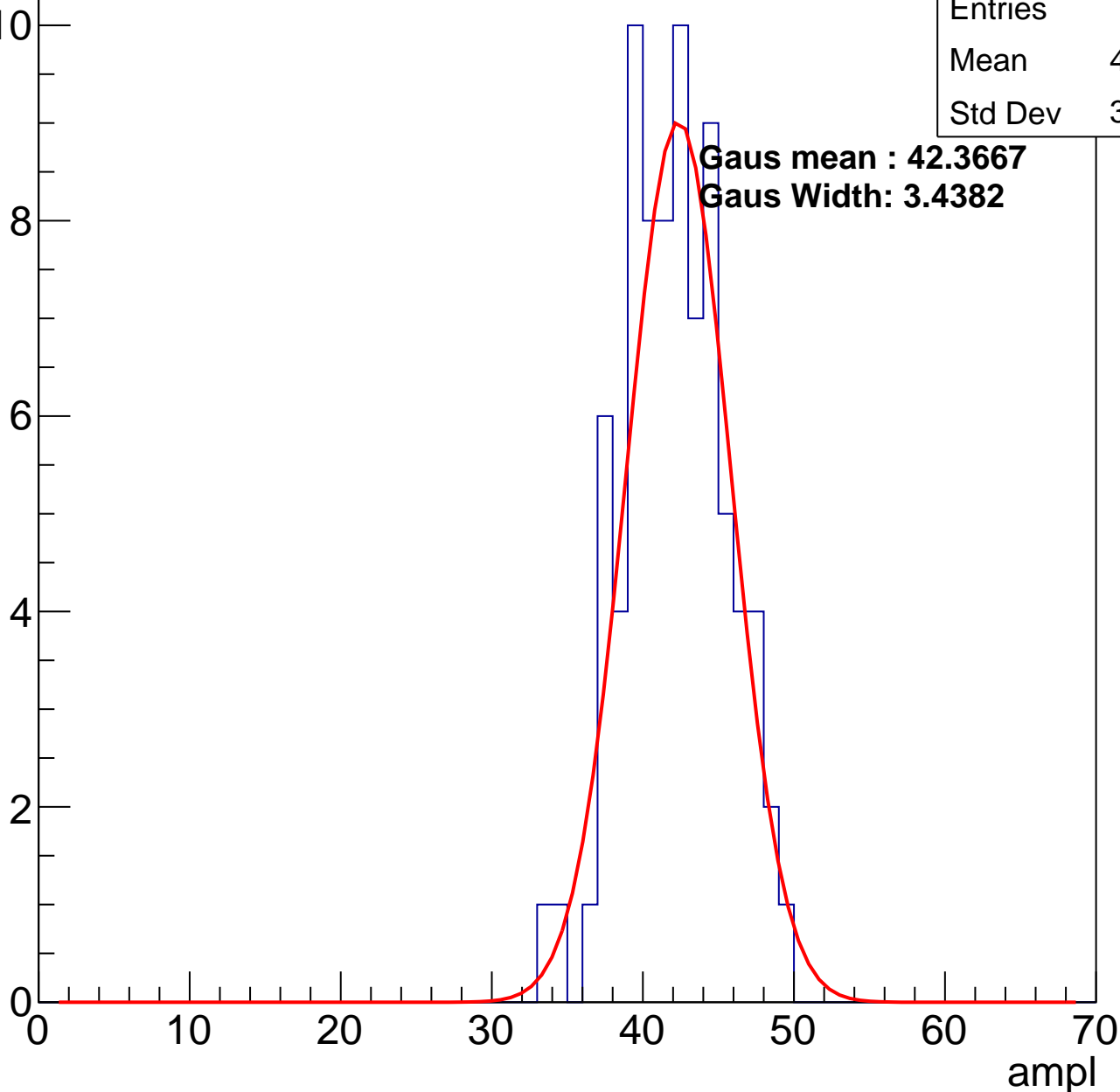
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	41.65
Std Dev	3.312

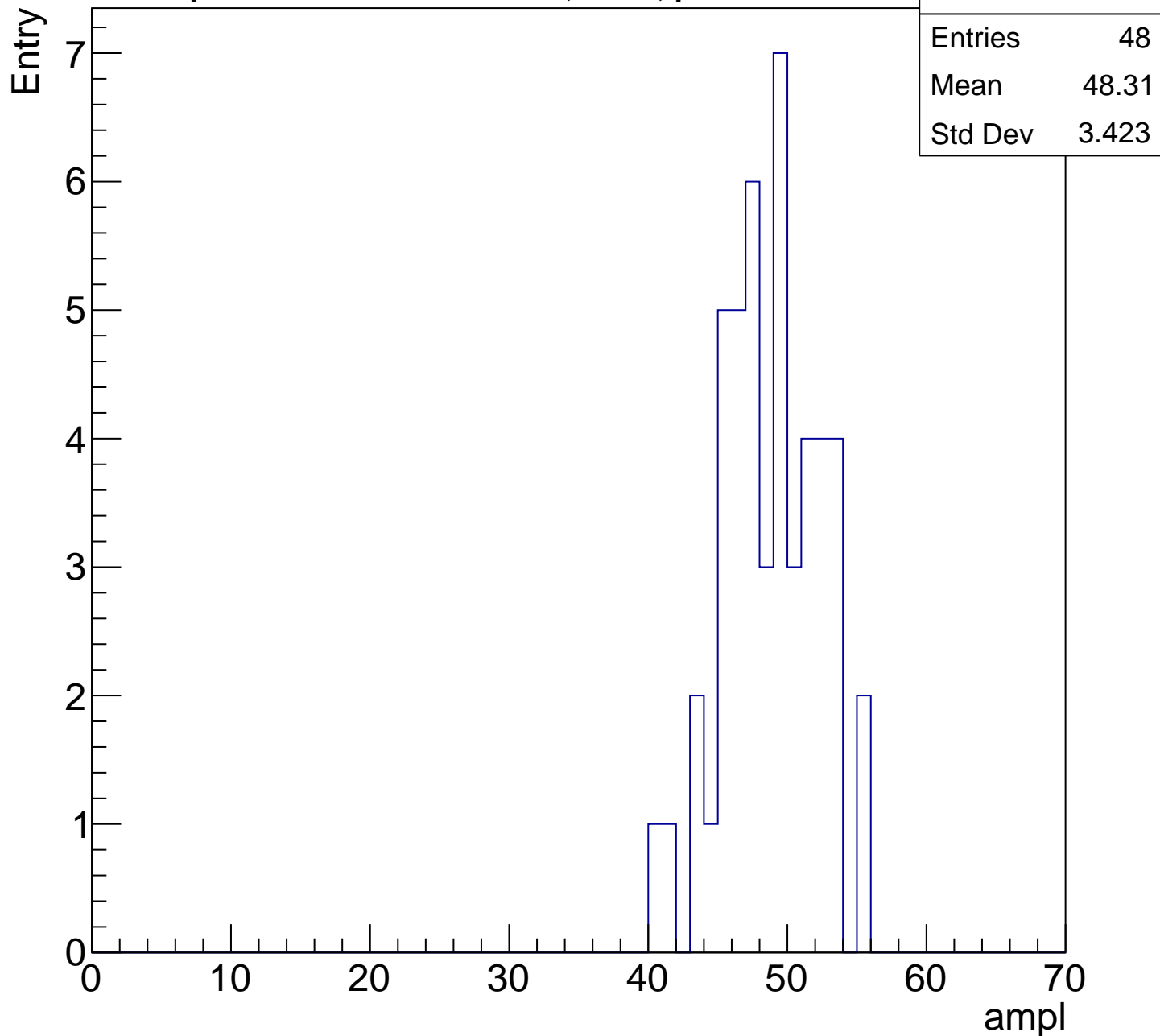
**Gaus mean : 42.3667**

**Gaus Width: 3.4382**



# B1L000S, U2-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

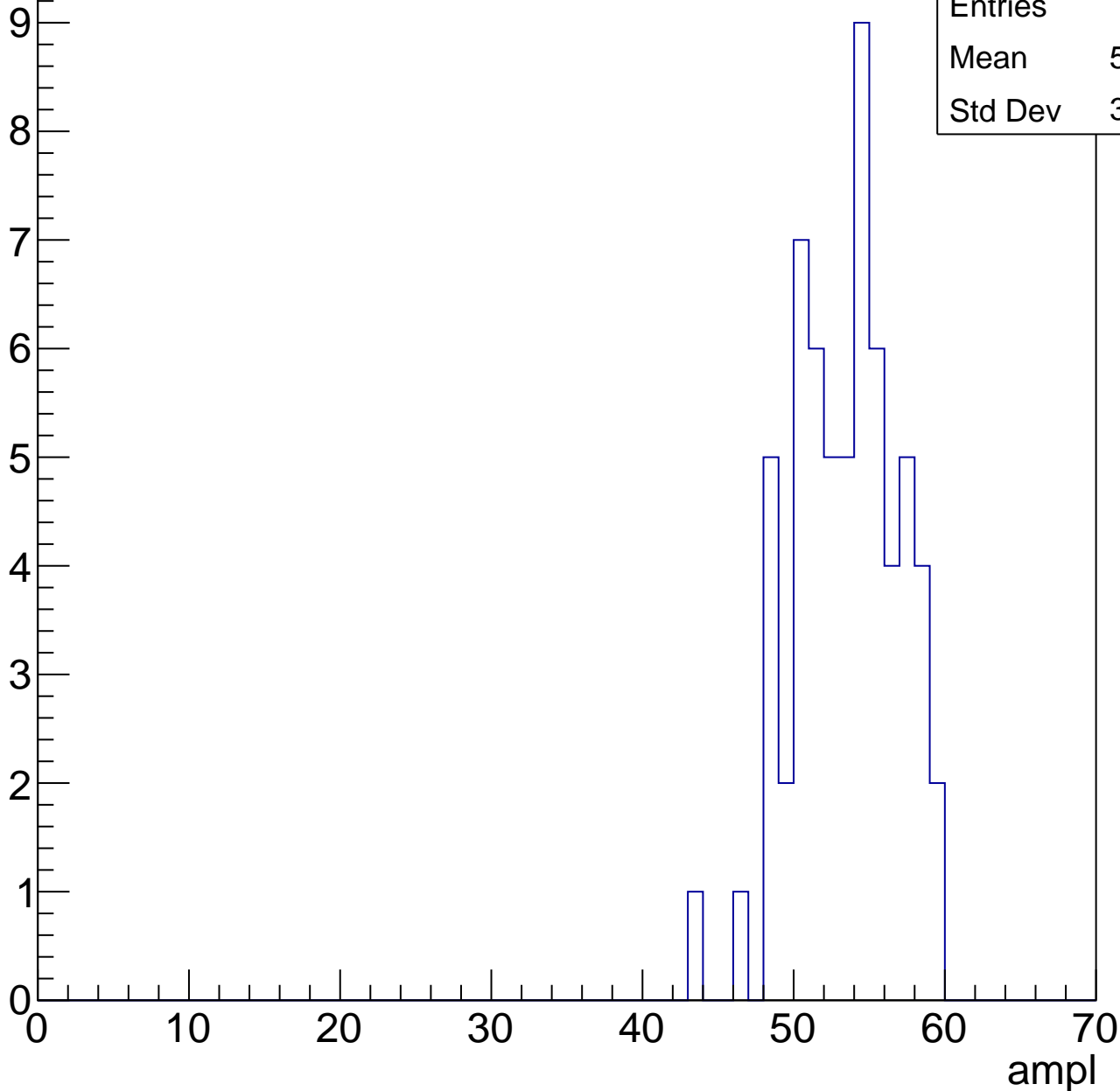


# B1L000S, U2-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	52.95
Std Dev	3.405

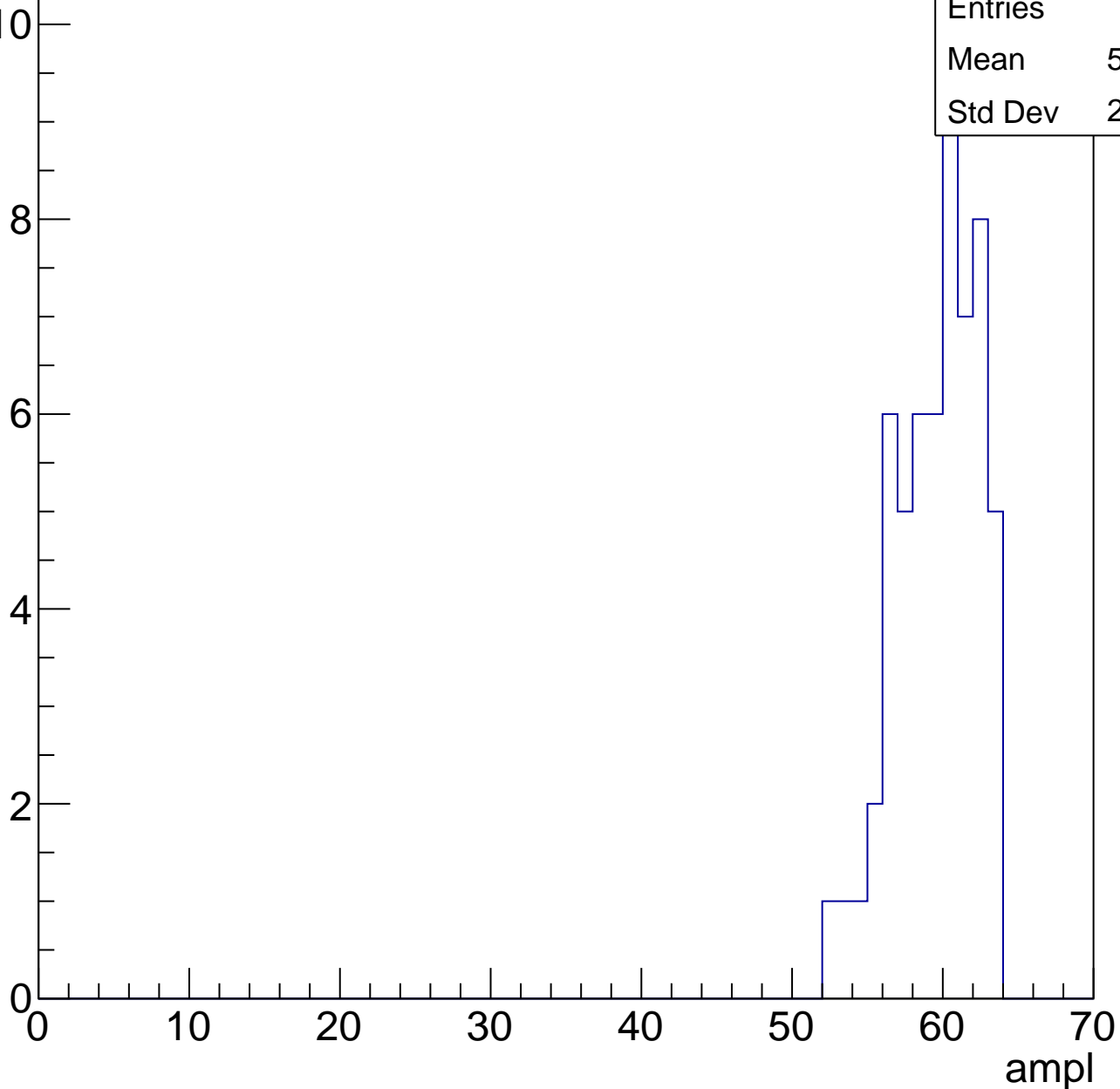


# B1L000S, U2-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	59.14
Std Dev	2.662

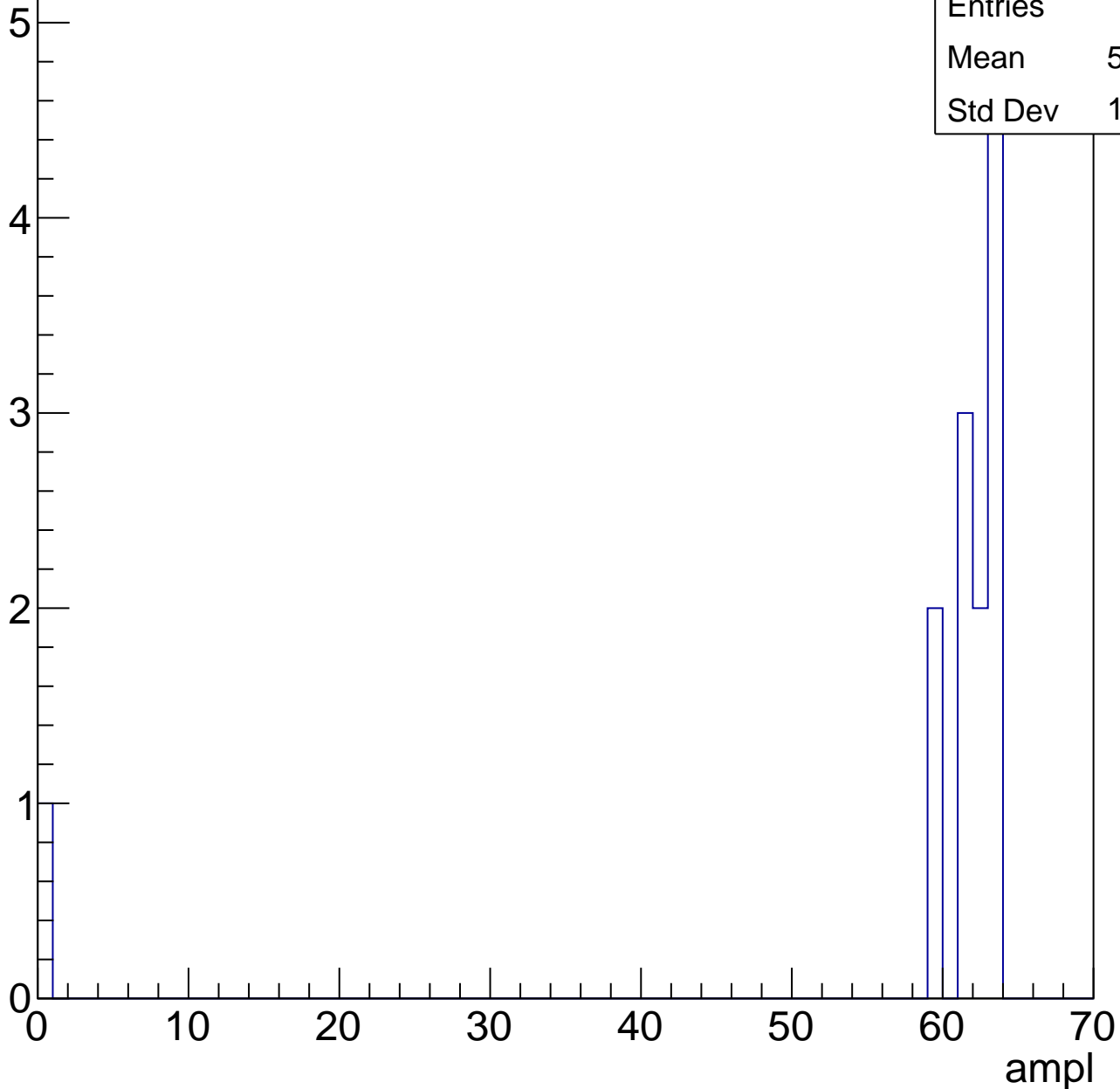


# B1L000S, U2-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	56.92
Std Dev	16.49





# B1L000S, U2-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L000S, U2-ch88, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	27.48
Std Dev	3.368

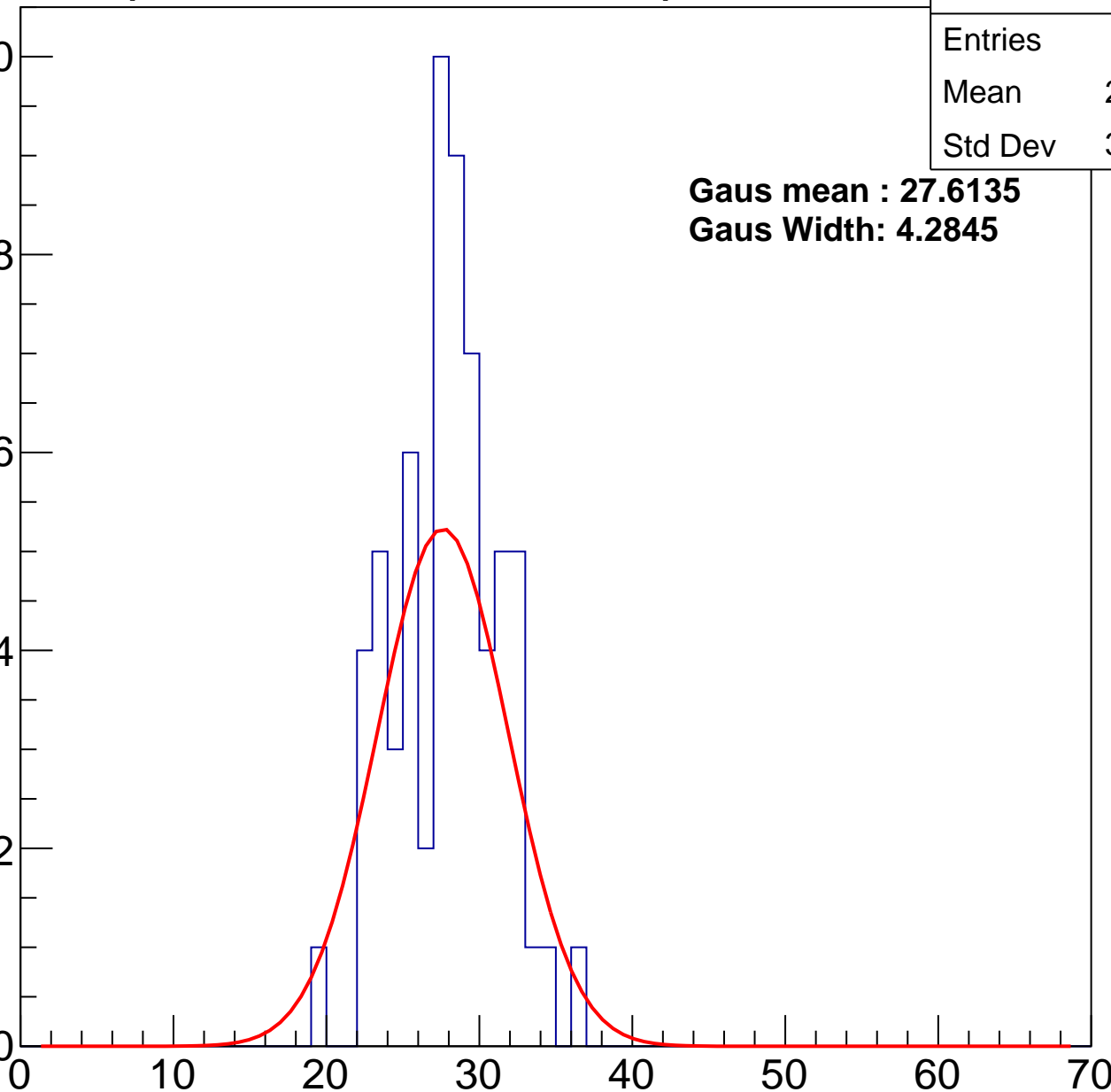
**Gaus mean : 27.6135**

**Gaus Width: 4.2845**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L000S, U2-ch88, adc1

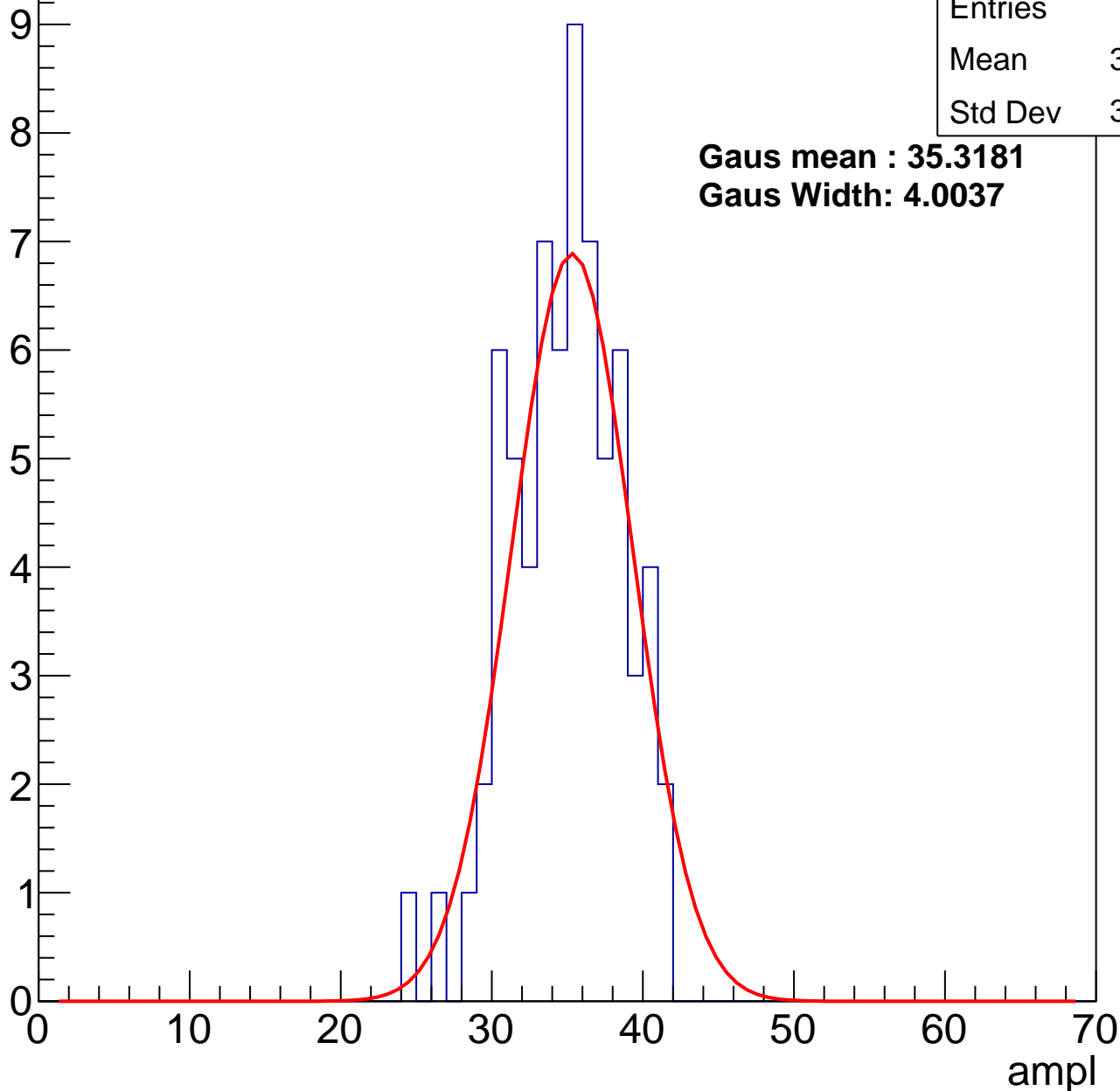
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.39
Std Dev	3.608

**Gaus mean : 35.3181**

**Gaus Width: 4.0037**



# B1L000S, U2-ch88, adc2

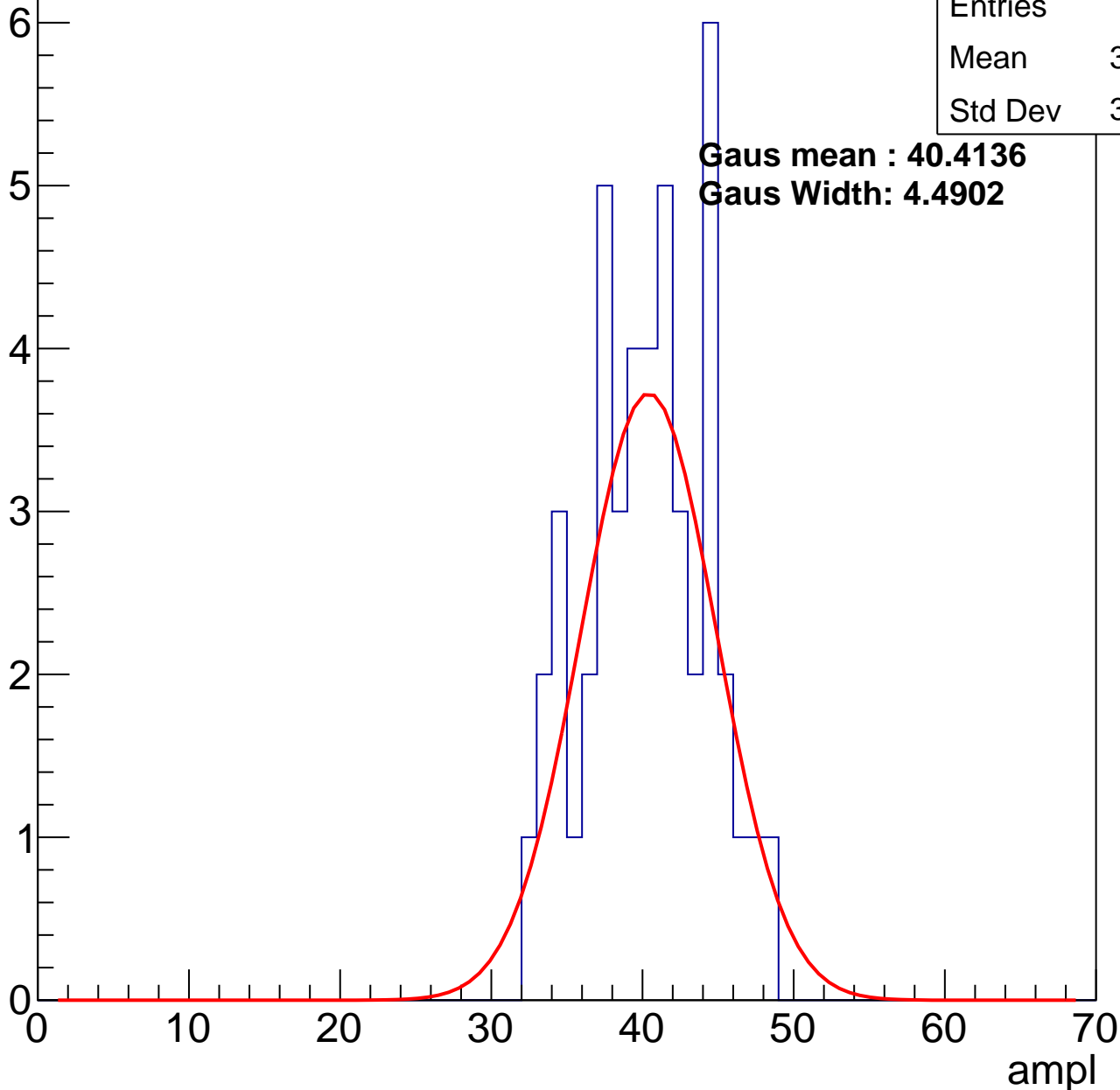
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	39.87
Std Dev	3.949

**Gaus mean : 40.4136**

**Gaus Width: 4.4902**

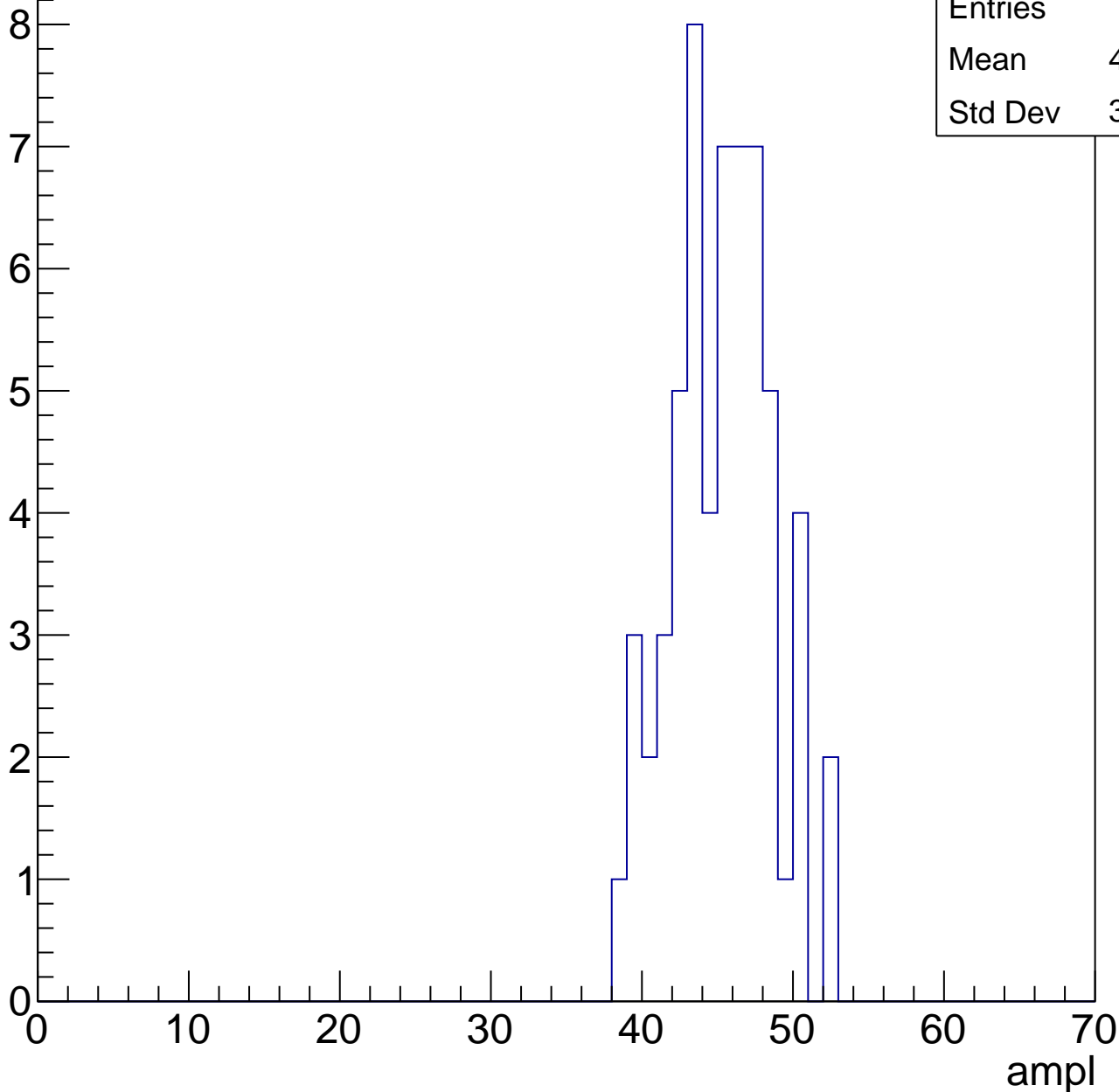


# B1L000S, U2-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	44.86
Std Dev	3.265



# B1L000S, U2-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	51.09
Std Dev	3.21

Entry

10

8

6

4

2

0

0

10

20

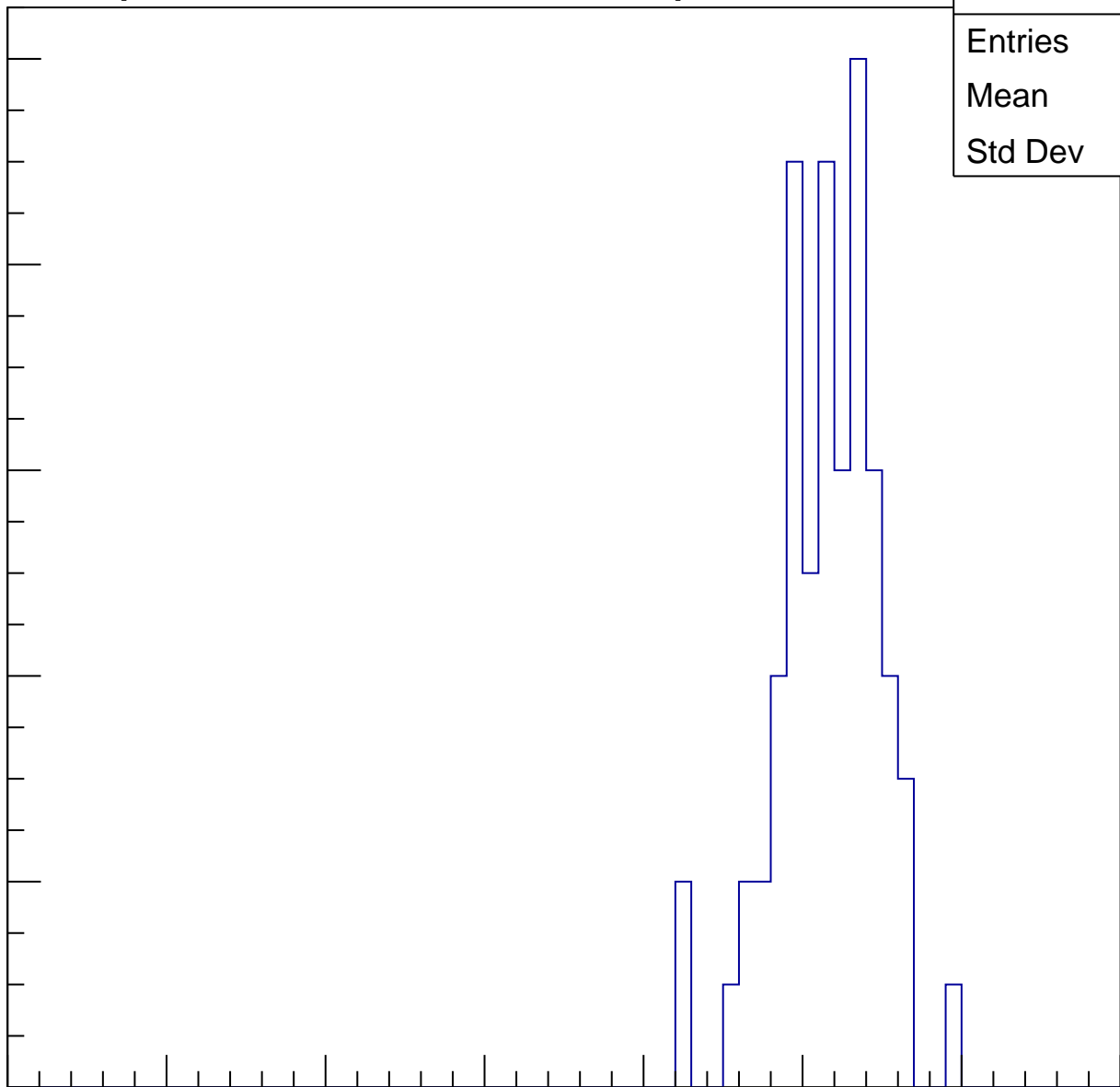
30

40

50

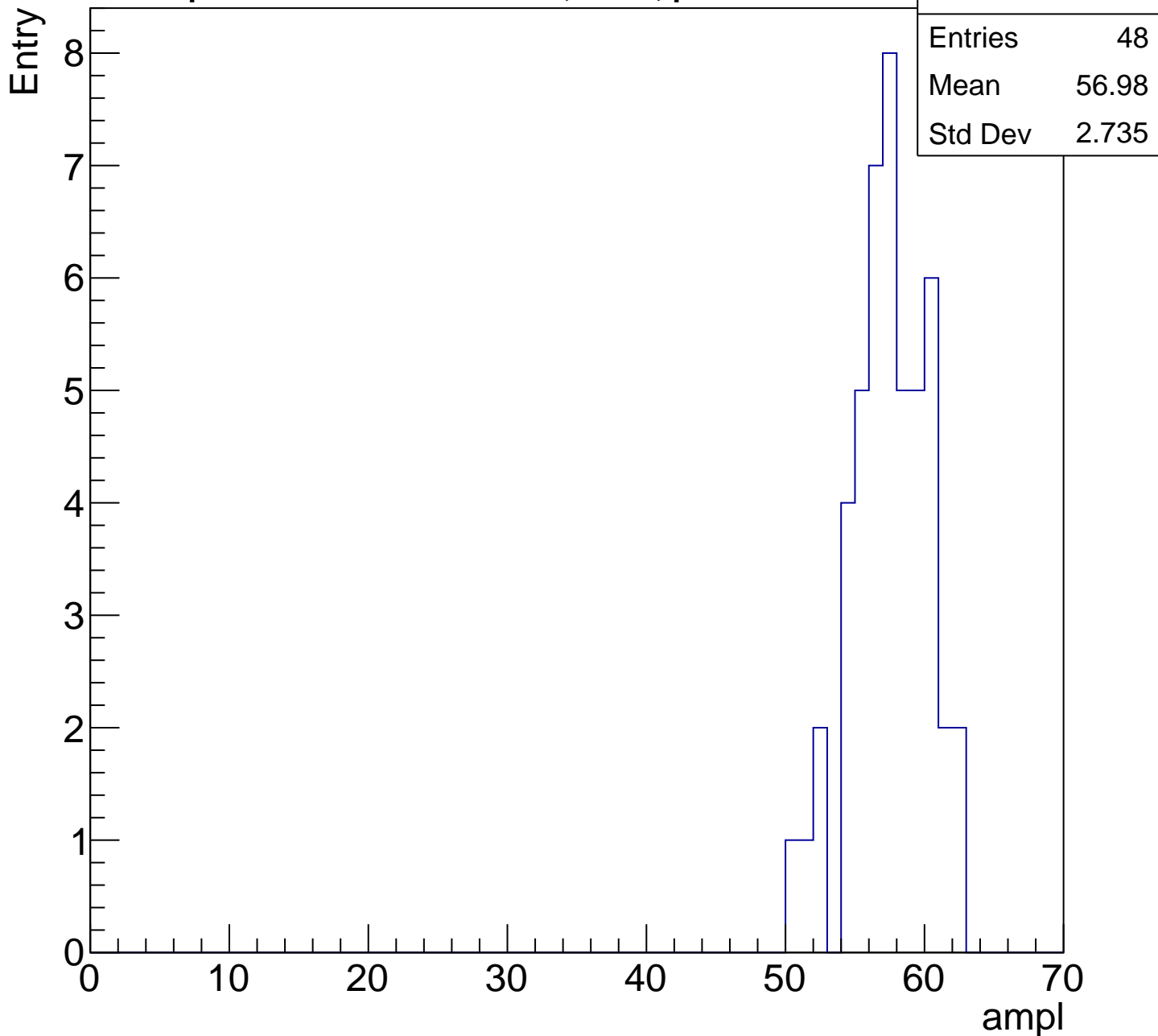
60

ampl



# B1L000S, U2-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

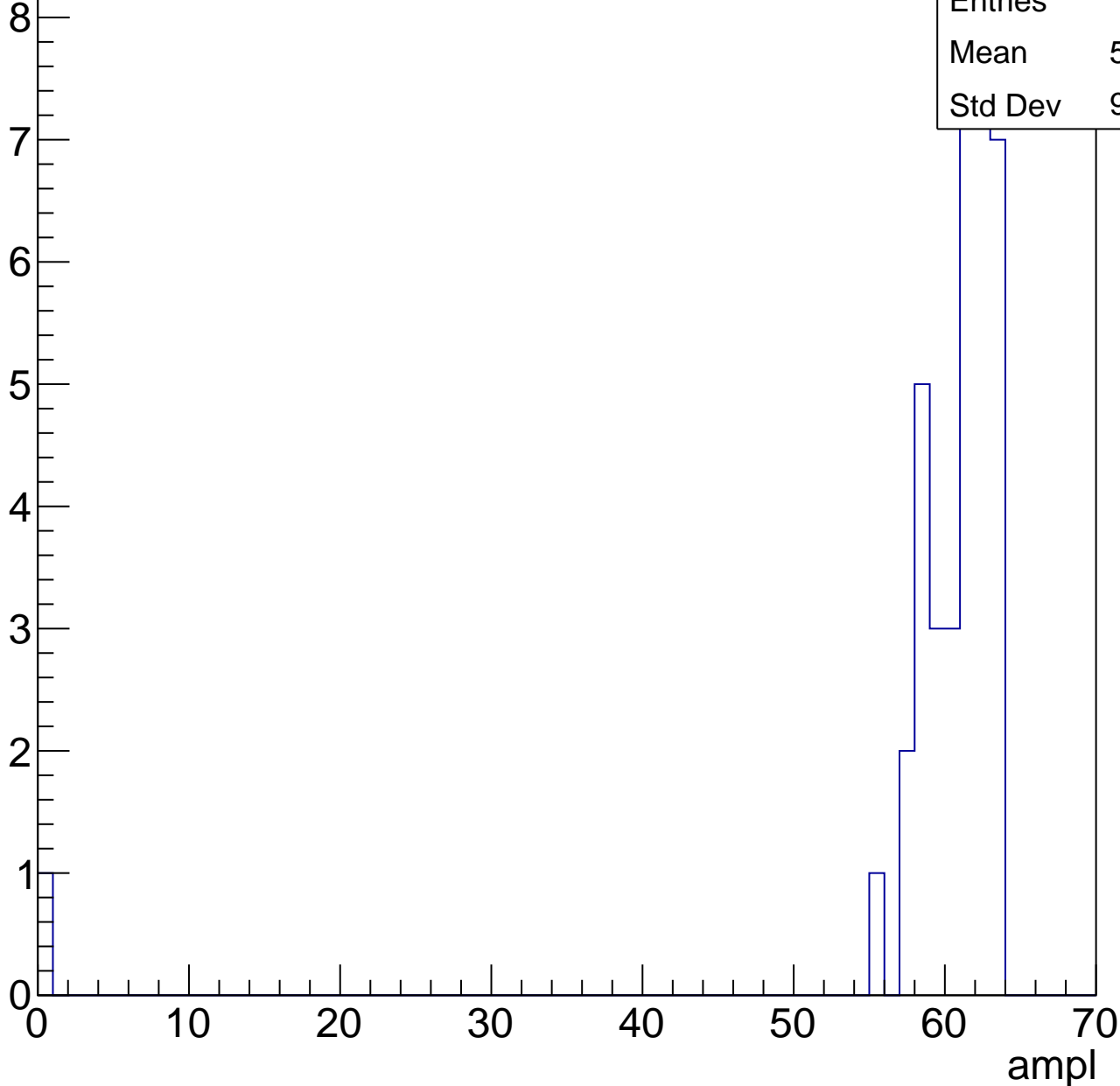


# B1L000S, U2-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	38
Mean	58.97
Std Dev	9.906

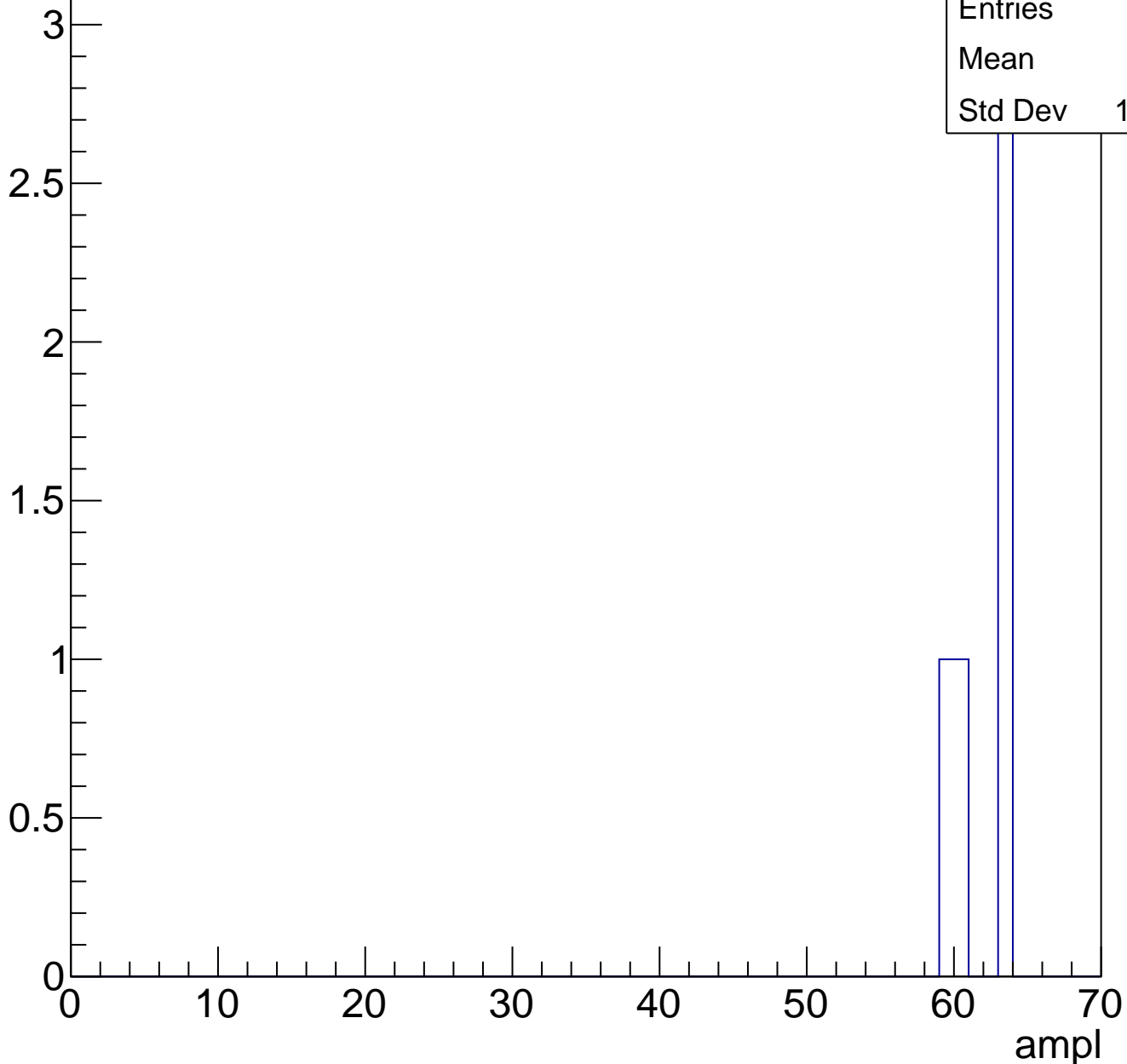




# B1L000S, U2-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch89, adc0

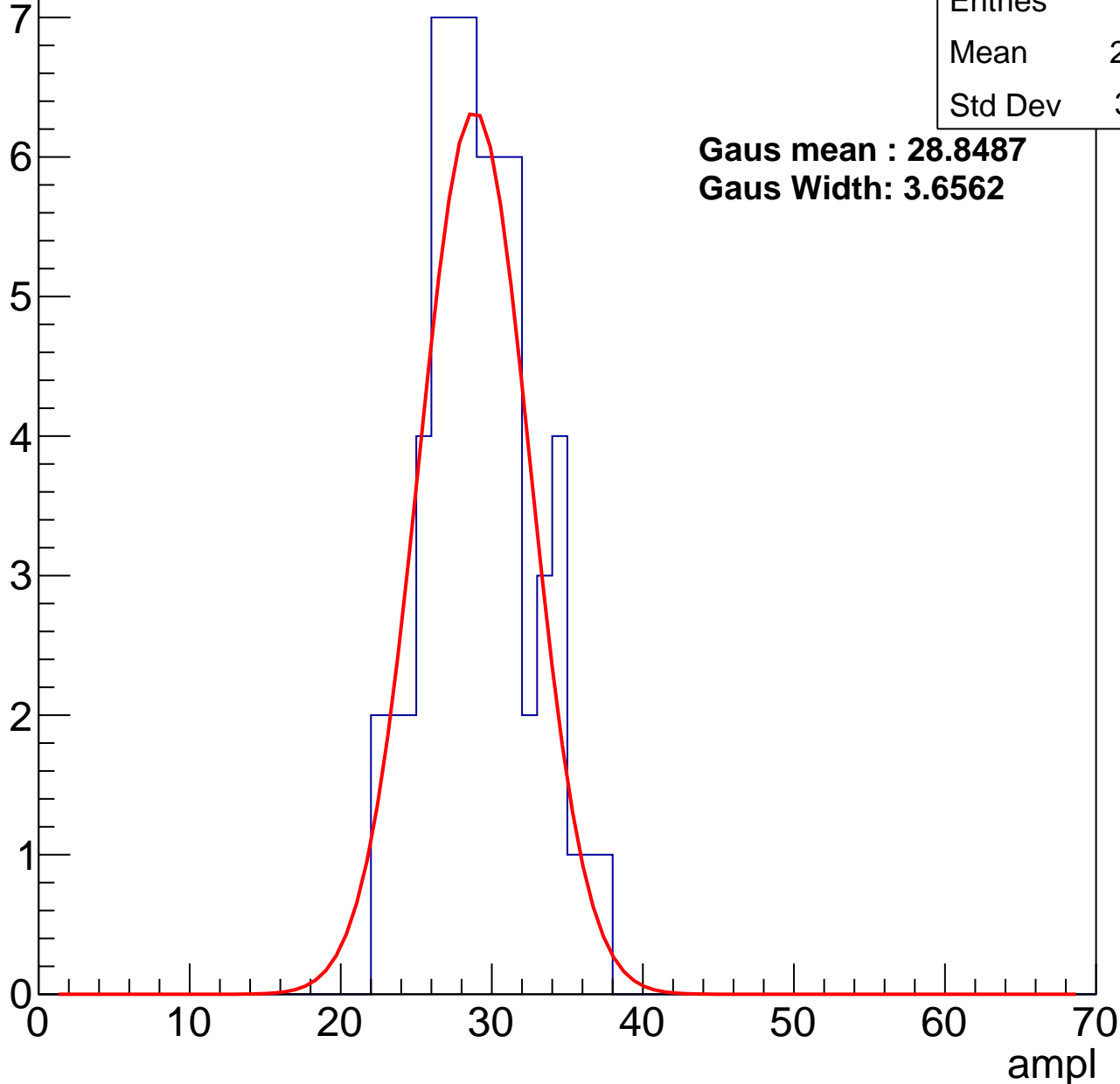
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.72
Std Dev	3.441

**Gaus mean : 28.8487**

**Gaus Width: 3.6562**



# B1L000S, U2-ch89, adc1

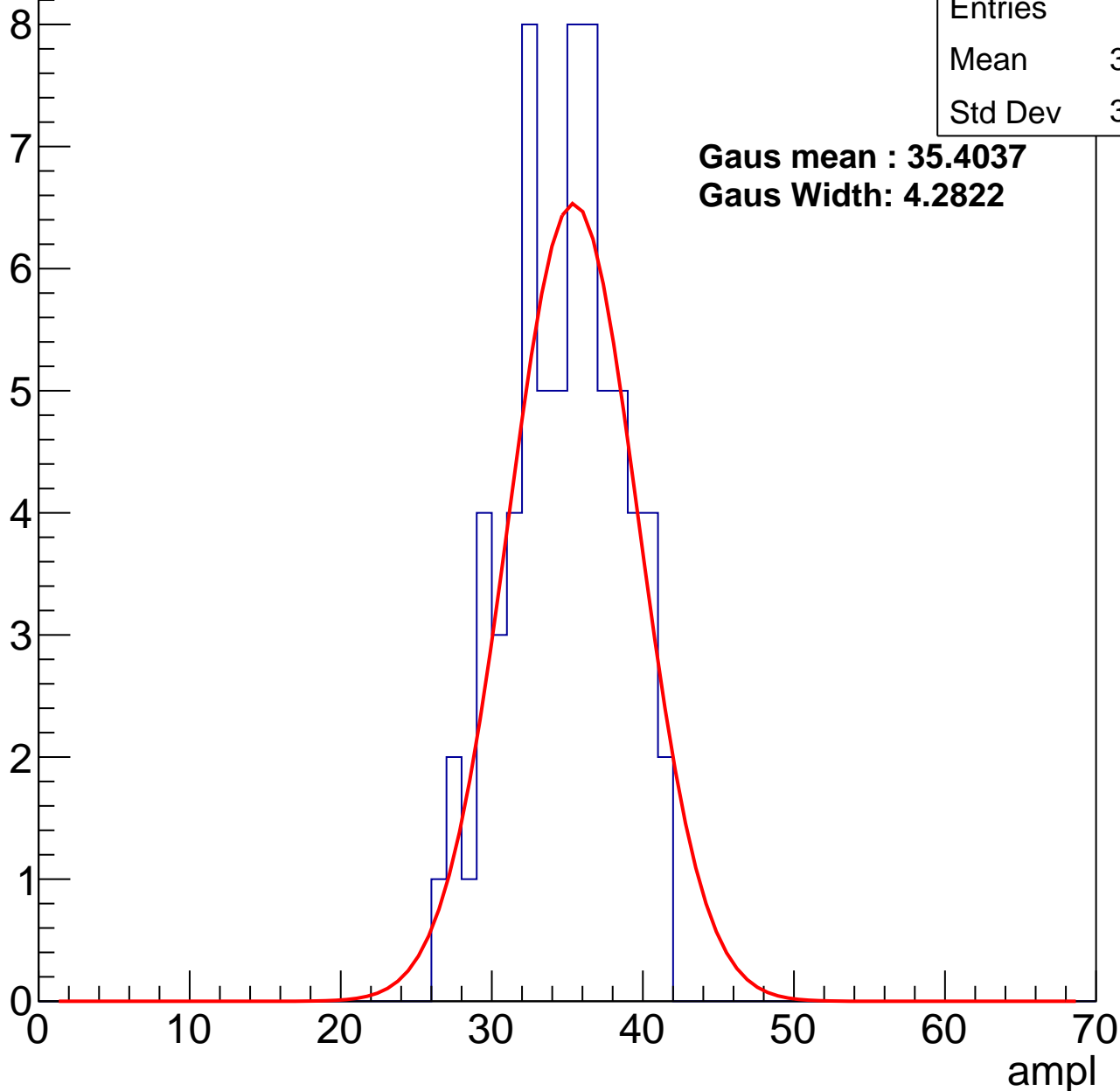
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.35
Std Dev	3.647

**Gaus mean : 35.4037**

**Gaus Width: 4.2822**



# B1L000S, U2-ch89, adc2

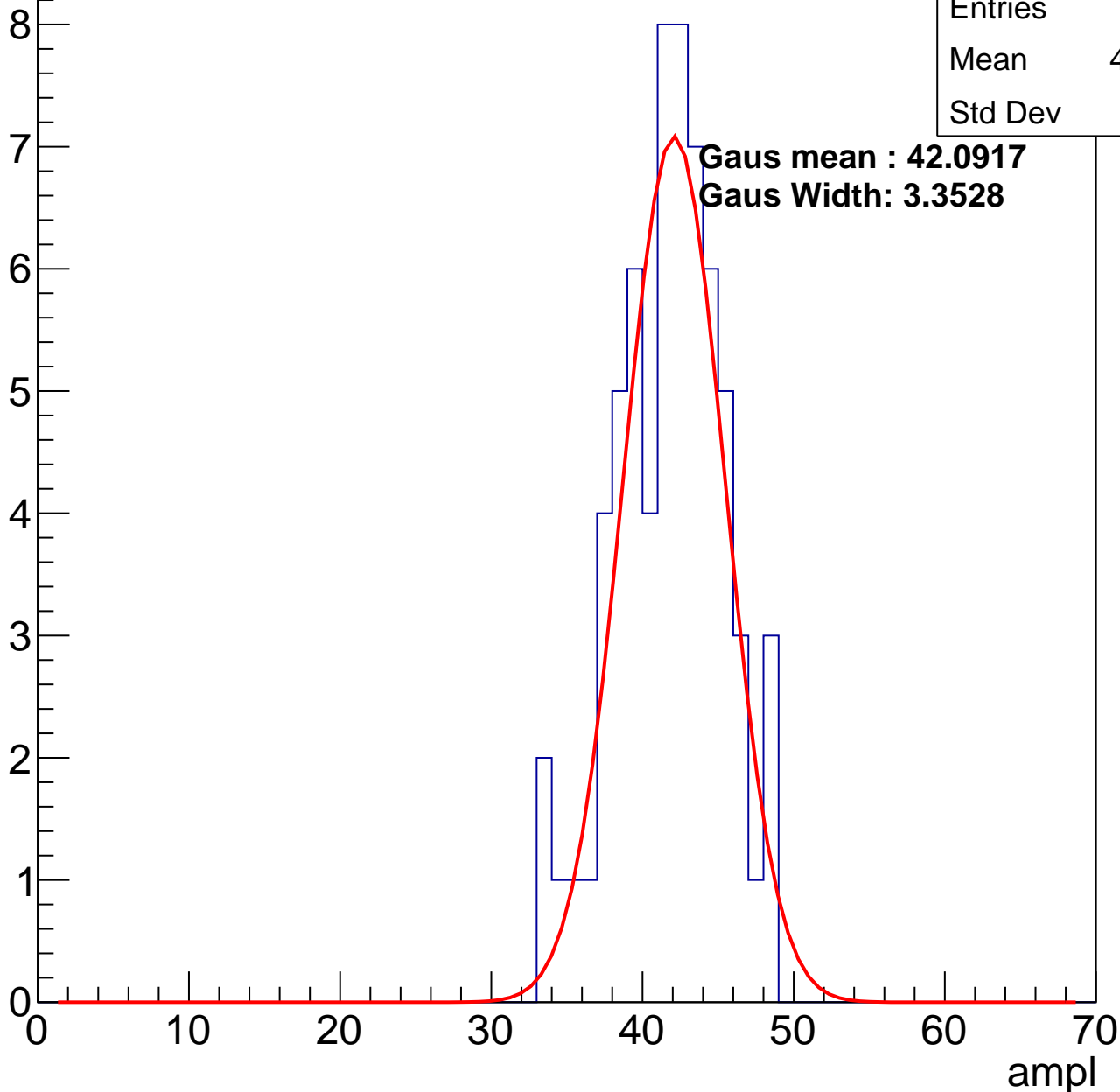
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.32
Std Dev	3.5

**Gaus mean : 42.0917**

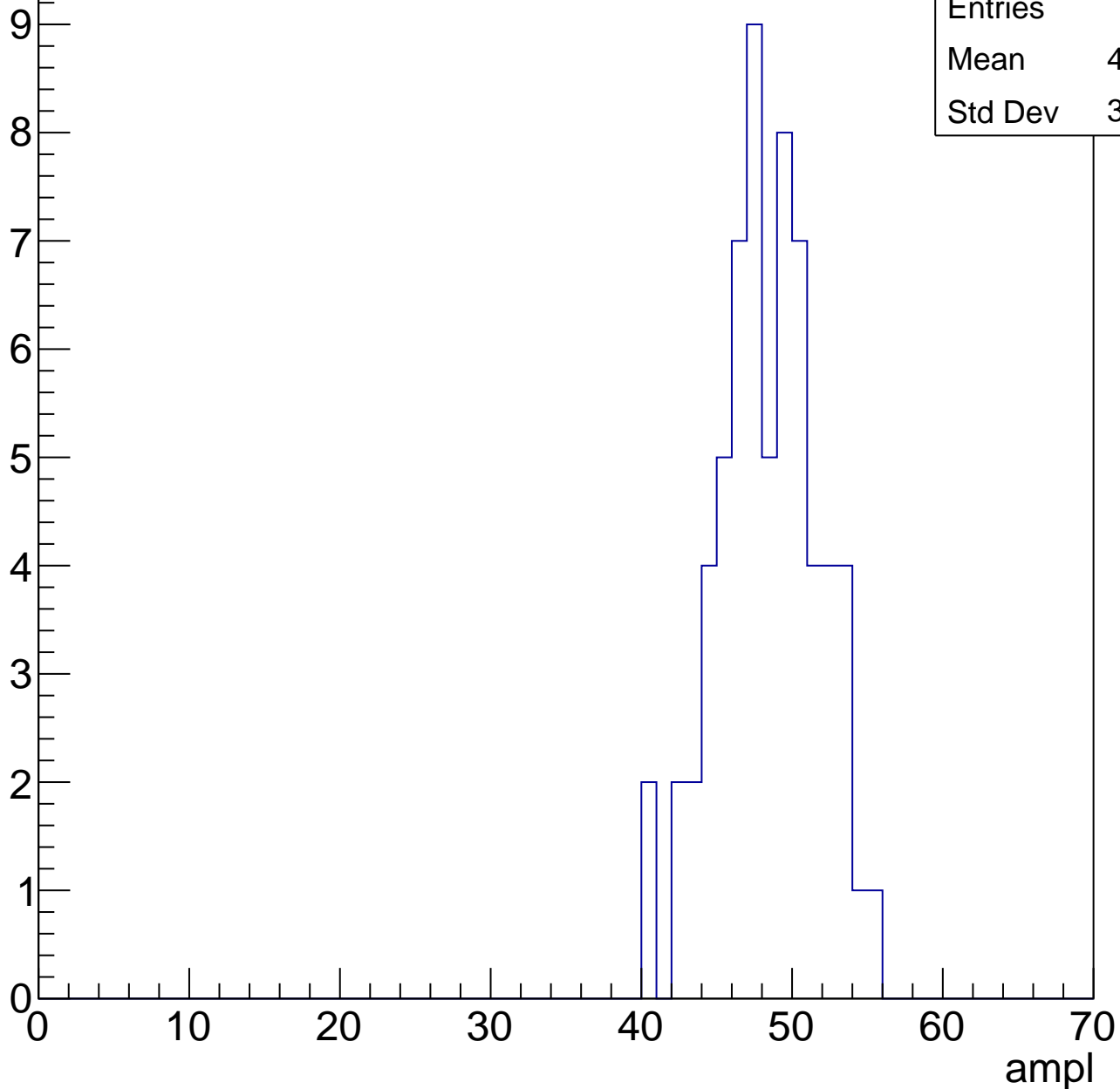
**Gaus Width: 3.3528**



# B1L000S, U2-ch89, adc3

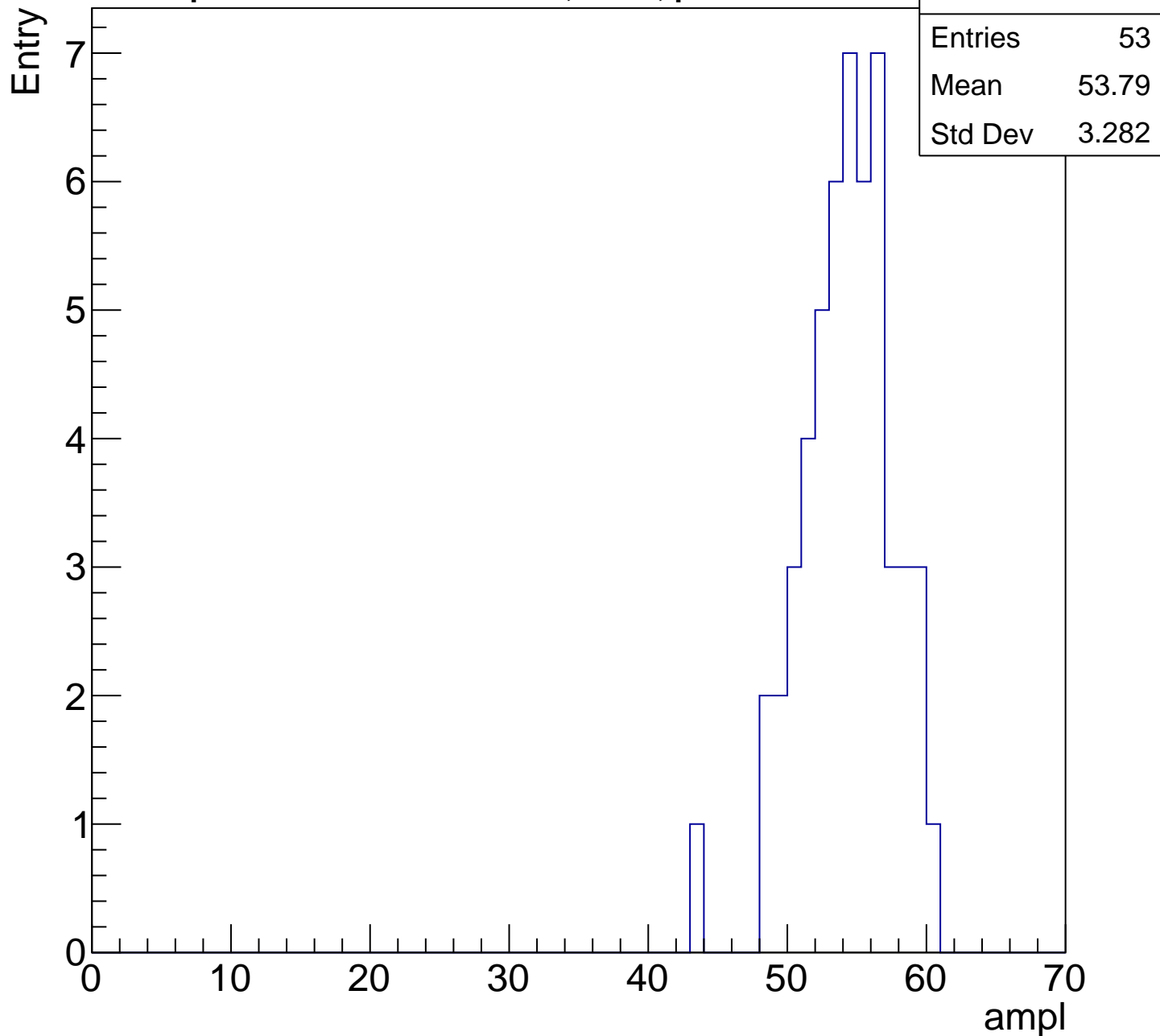
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch89, adc5

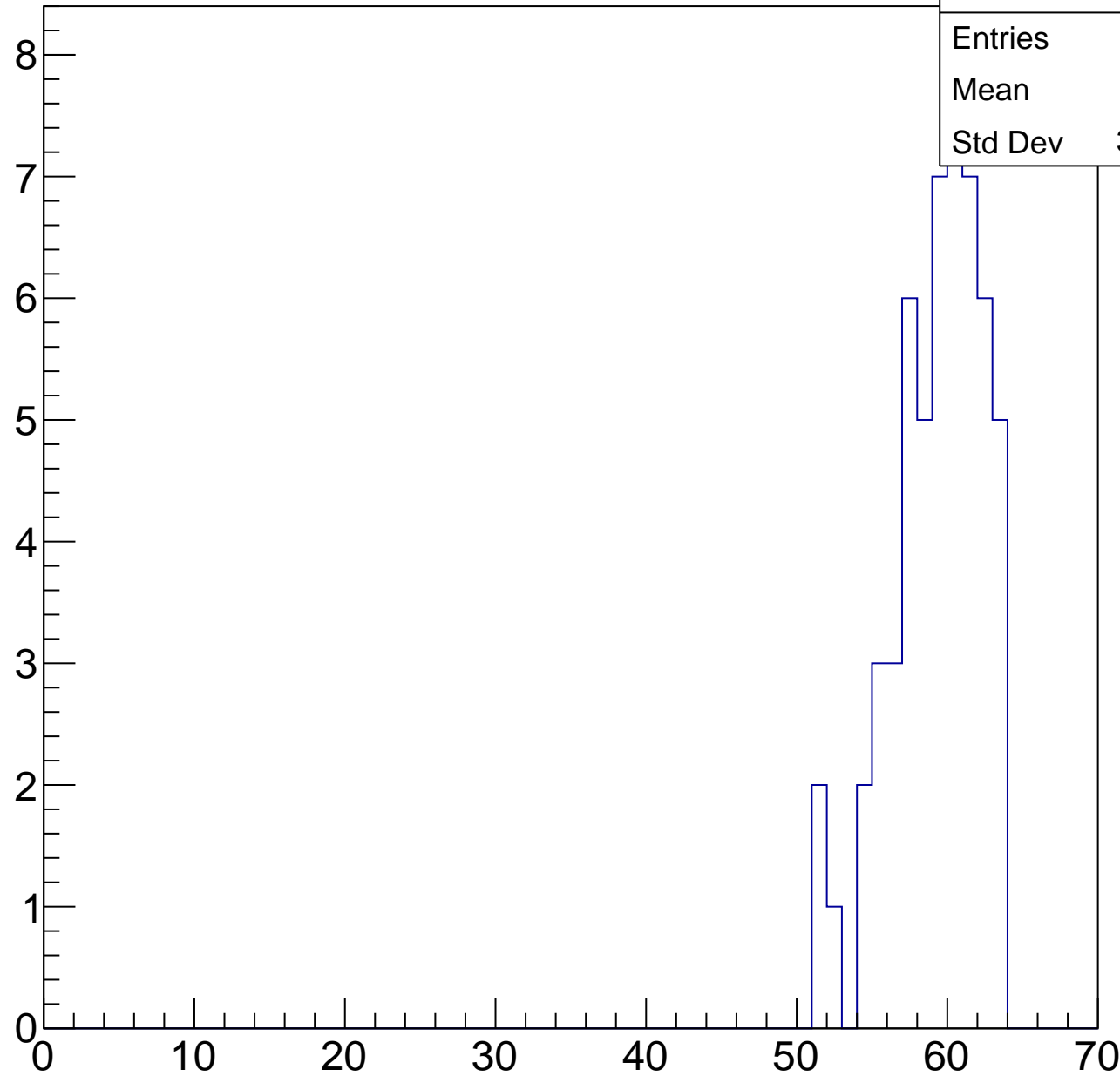
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	58.8
Std Dev	3.011

ampl

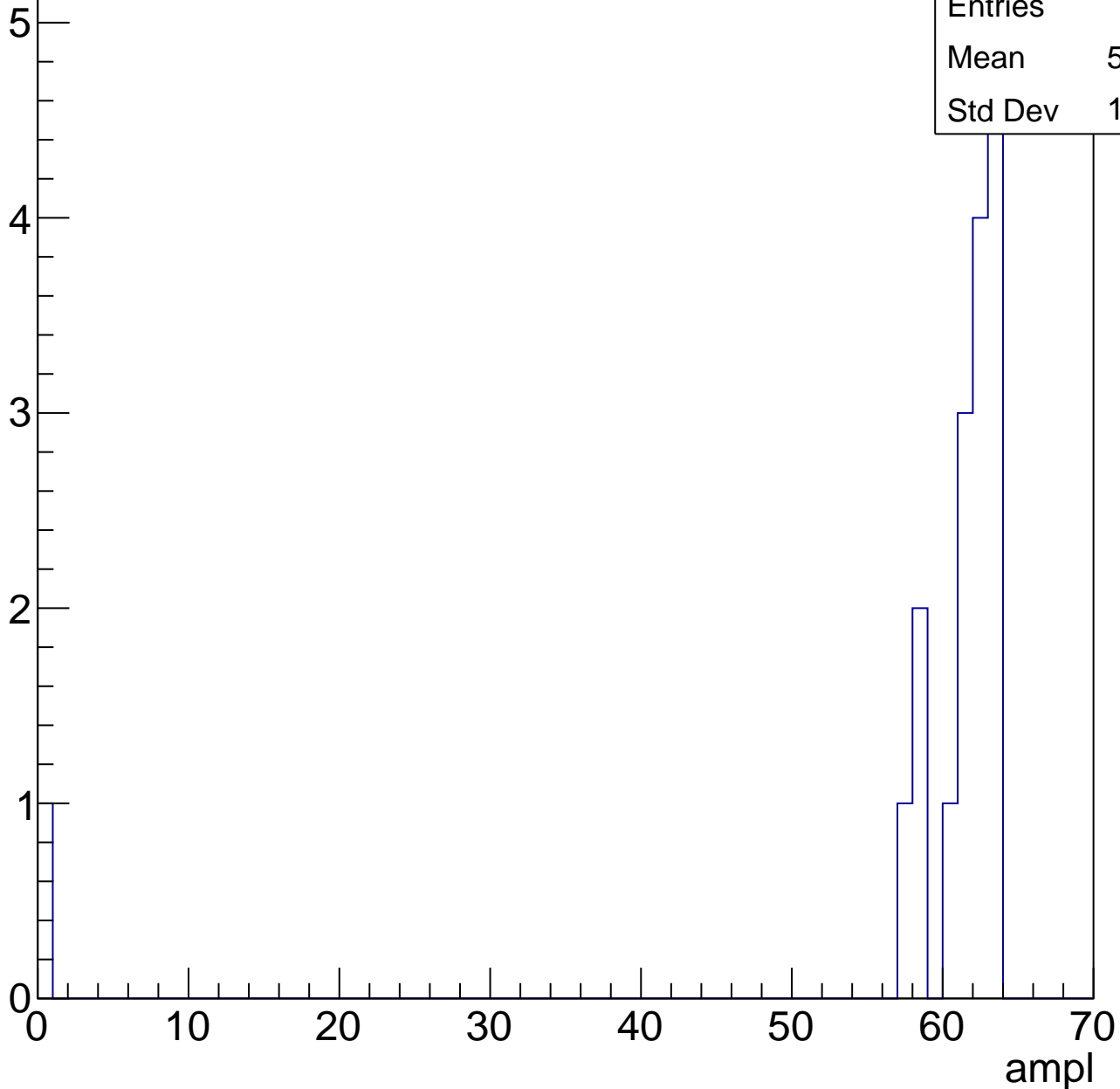


# B1L000S, U2-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.59
Std Dev	14.52





# B1L000S, U2-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U2-ch90, adc0

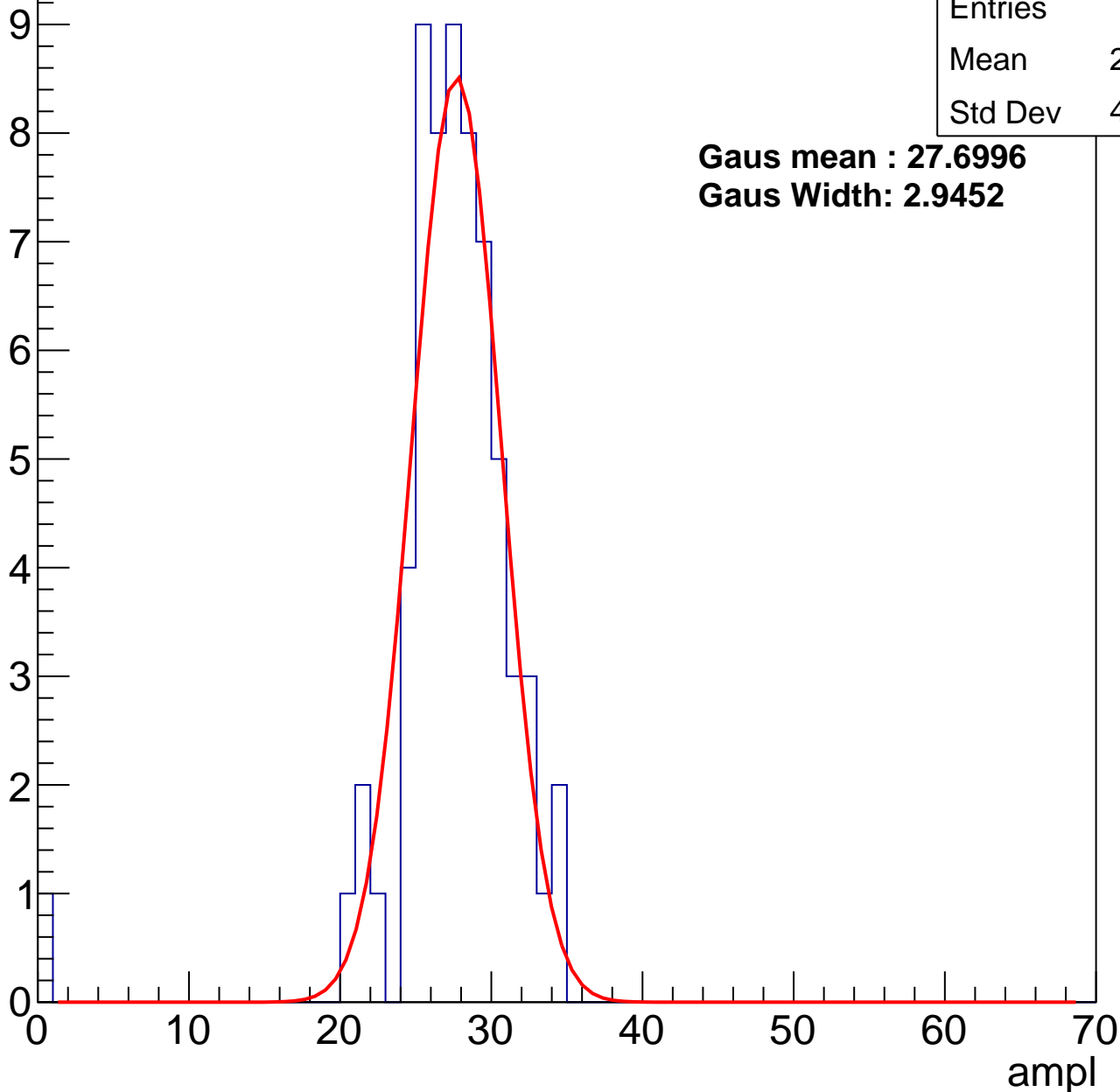
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	26.92
Std Dev	4.494

**Gaus mean : 27.6996**

**Gaus Width: 2.9452**



# B1L000S, U2-ch90, adc1

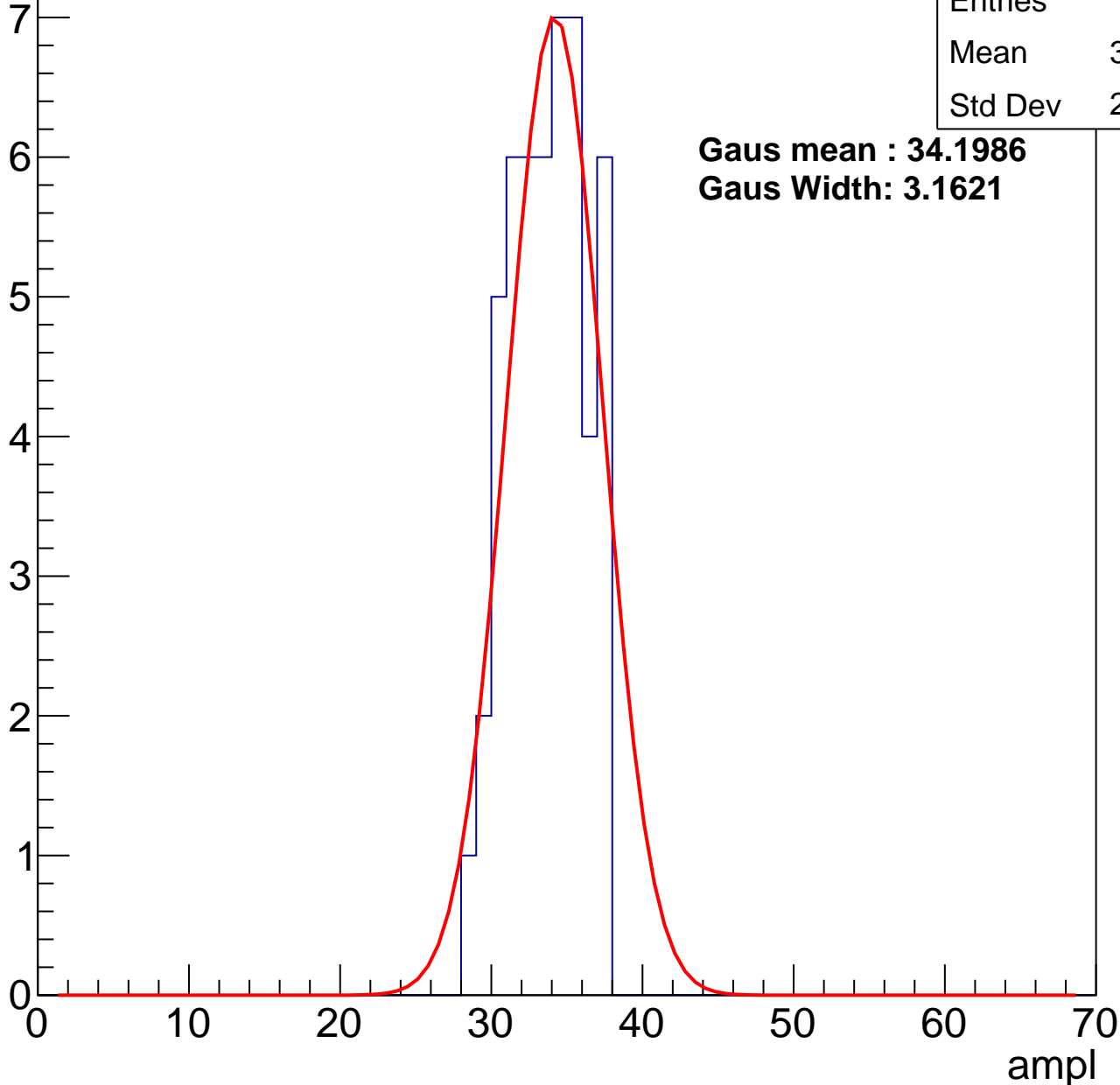
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	33.22
Std Dev	2.435

**Gaus mean : 34.1986**

**Gaus Width: 3.1621**



# B1L000S, U2-ch90, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	38.53
Std Dev	3.106

**Gaus mean : 38.8354**

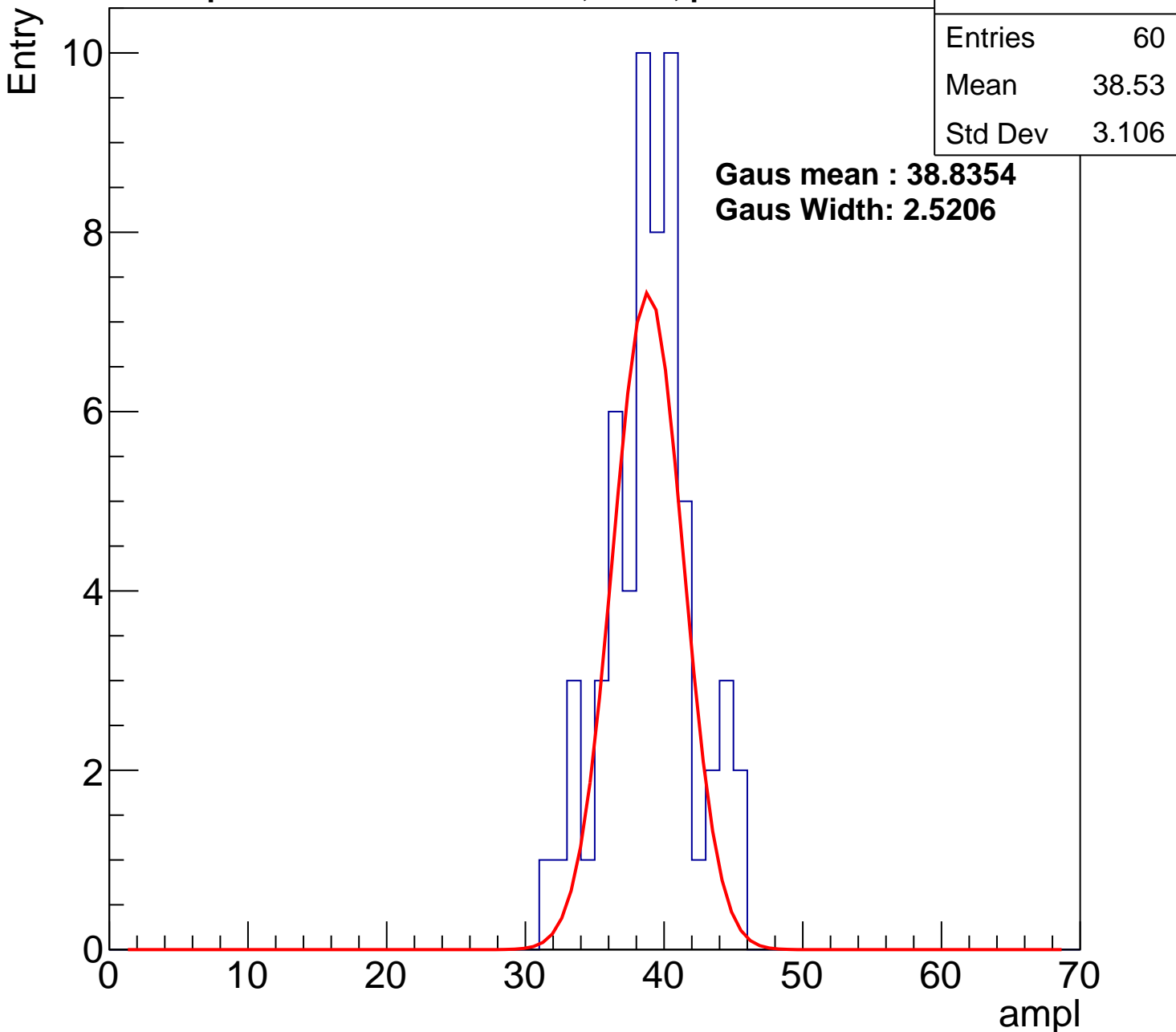
**Gaus Width: 2.5206**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

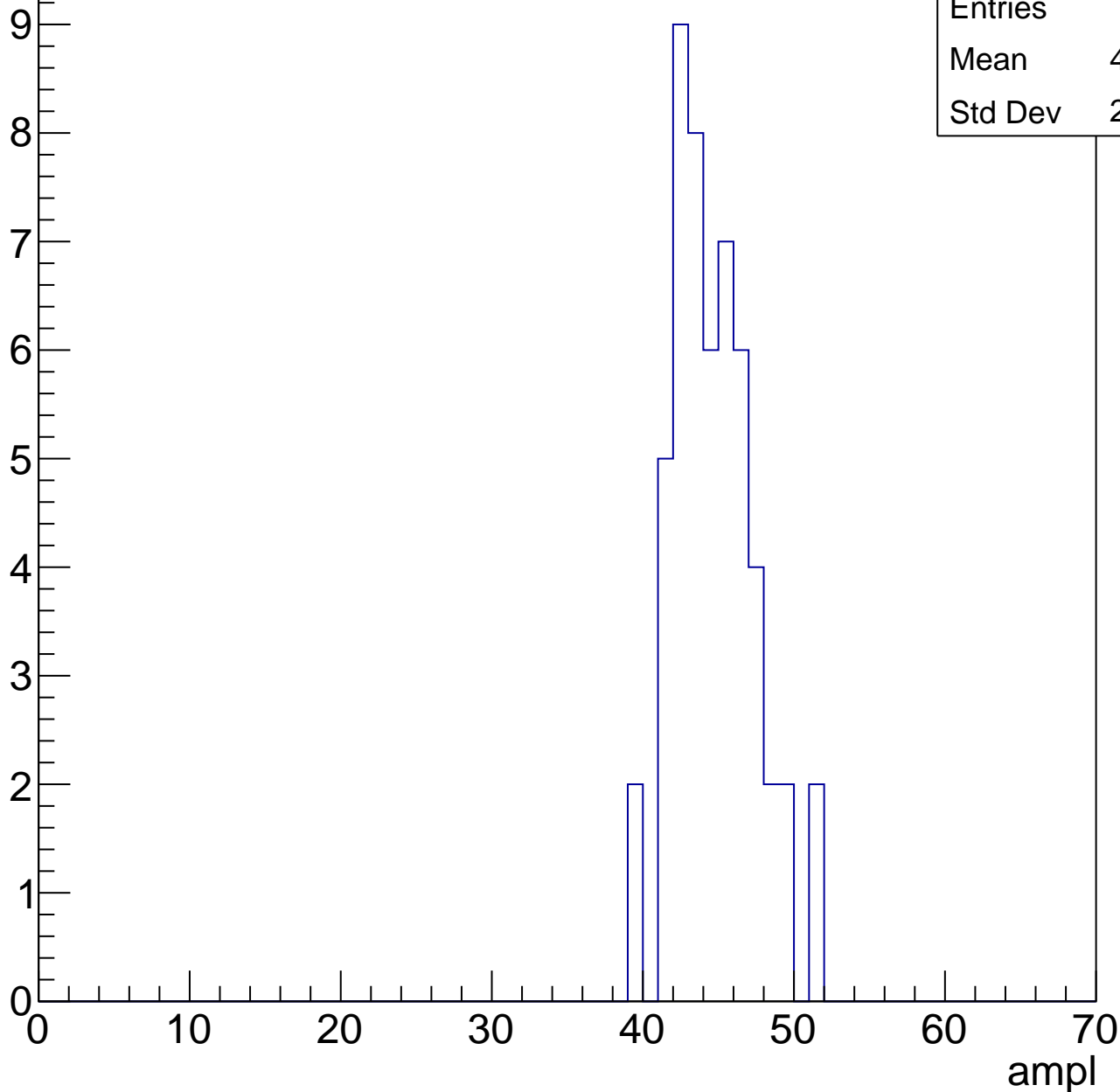


# B1L000S, U2-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	44.23
Std Dev	2.689

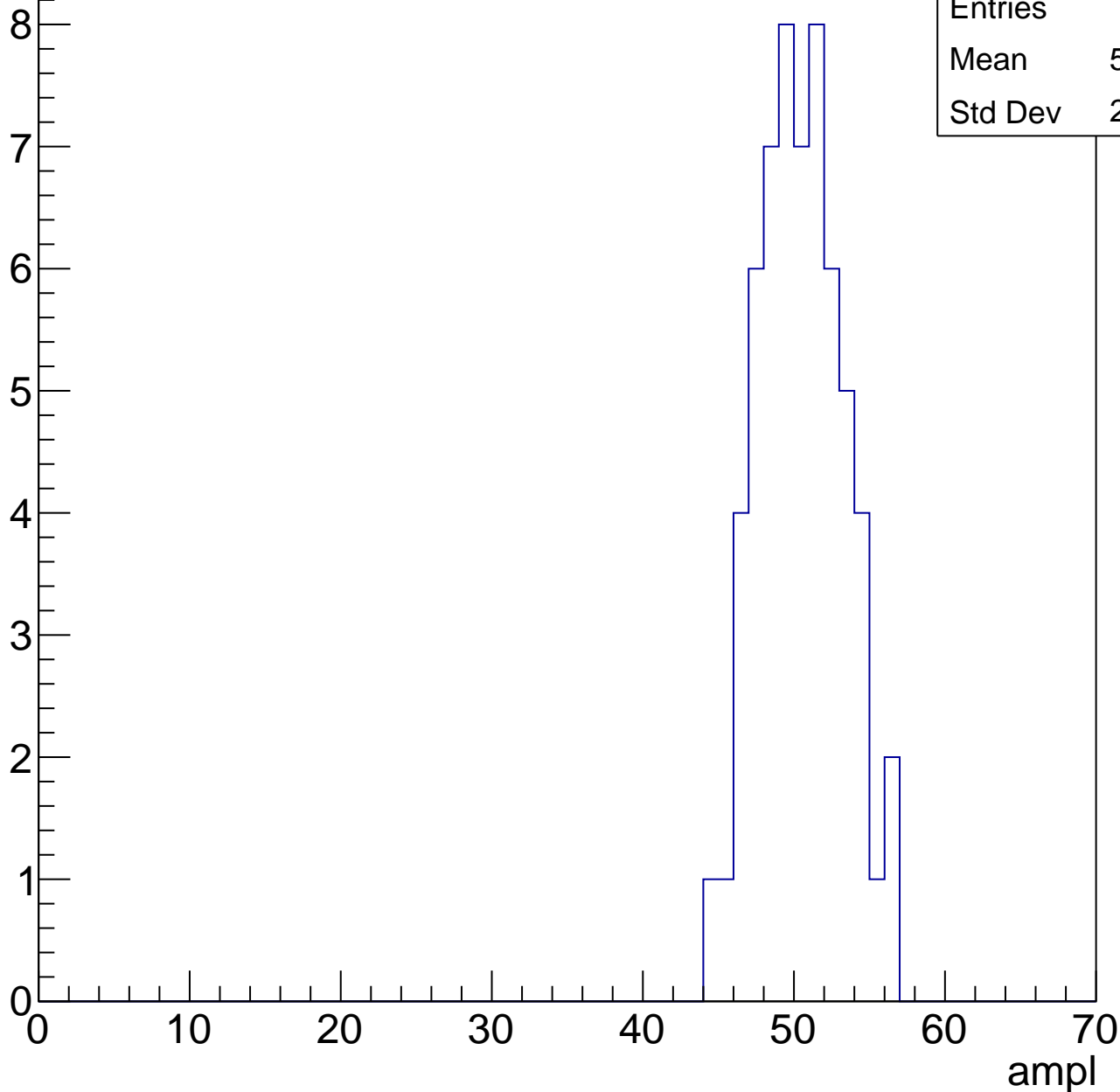


# B1L000S, U2-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	50.02
Std Dev	2.748

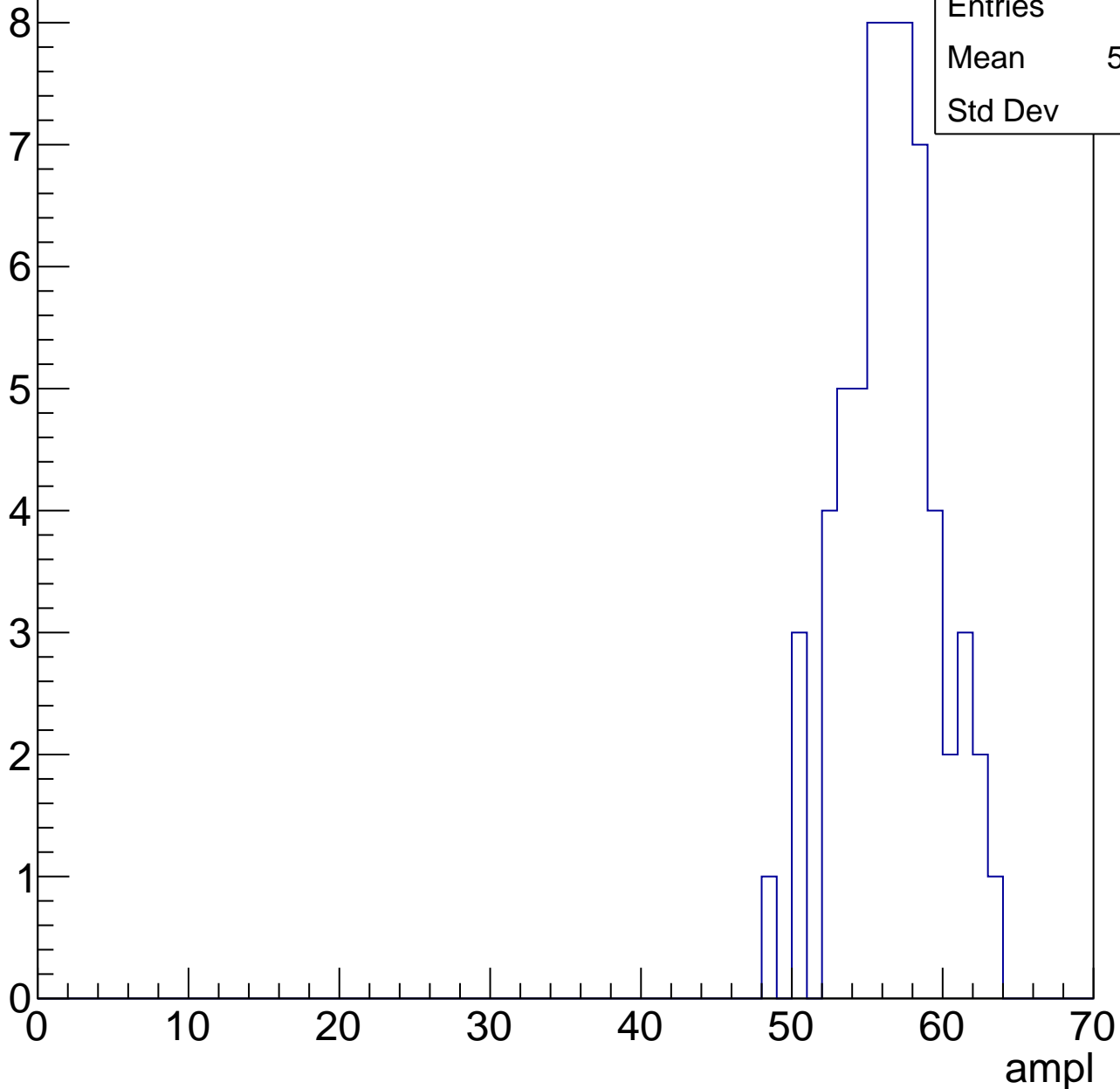


# B1L000S, U2-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

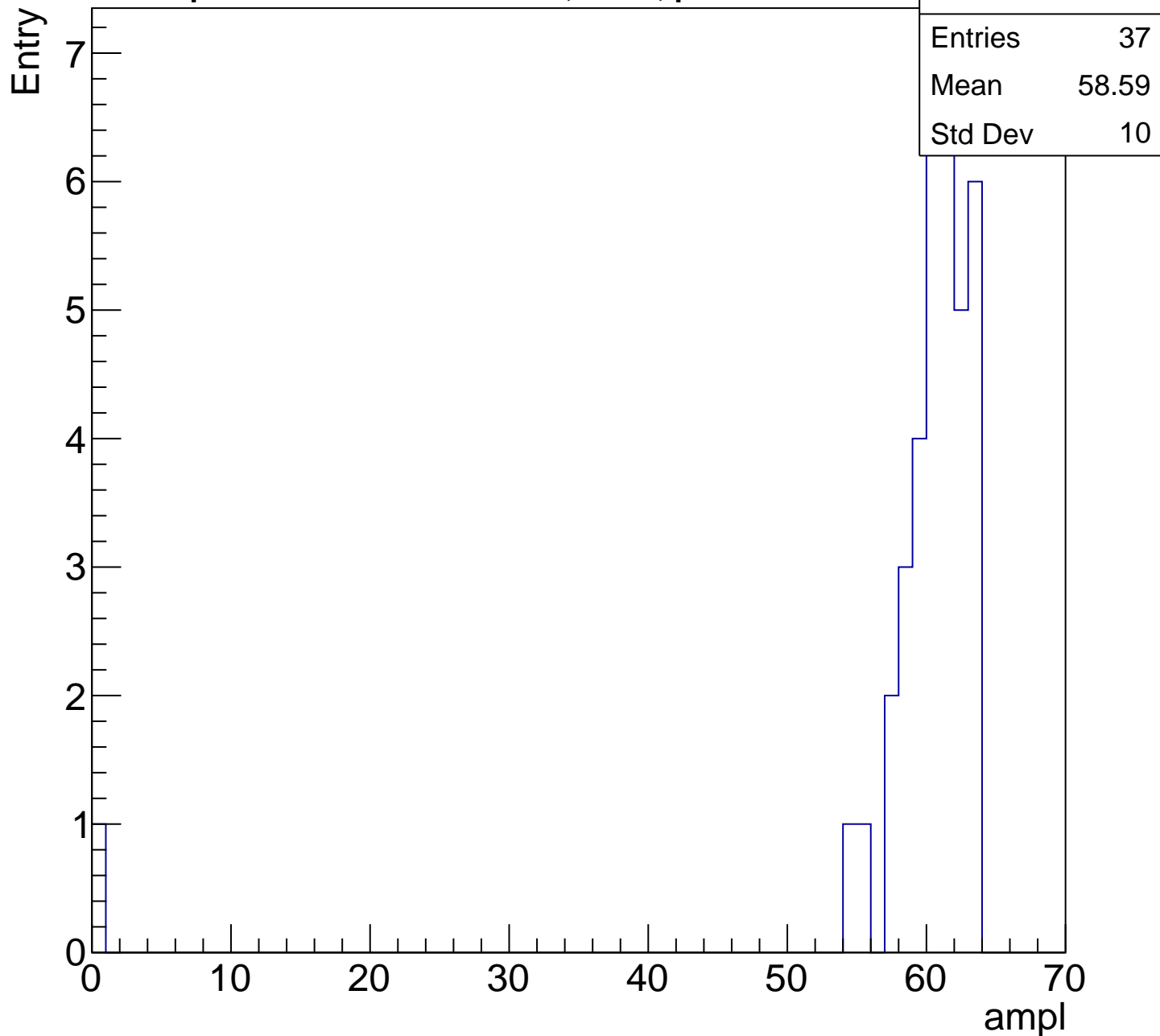
Entry

Entries	61
Mean	56.02
Std Dev	3.16



# B1L000S, U2-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

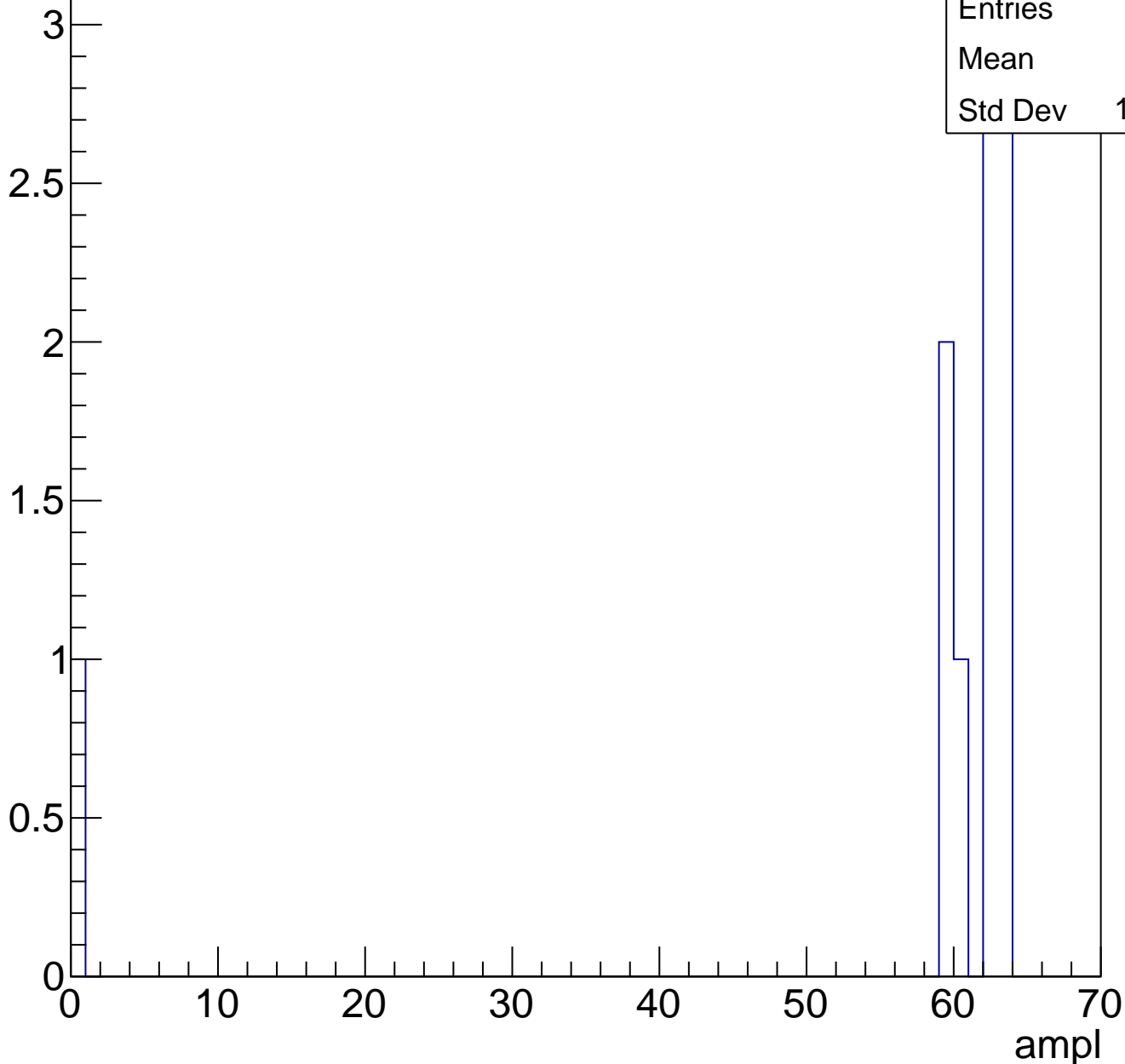




# B1L000S, U2-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch91, adc0

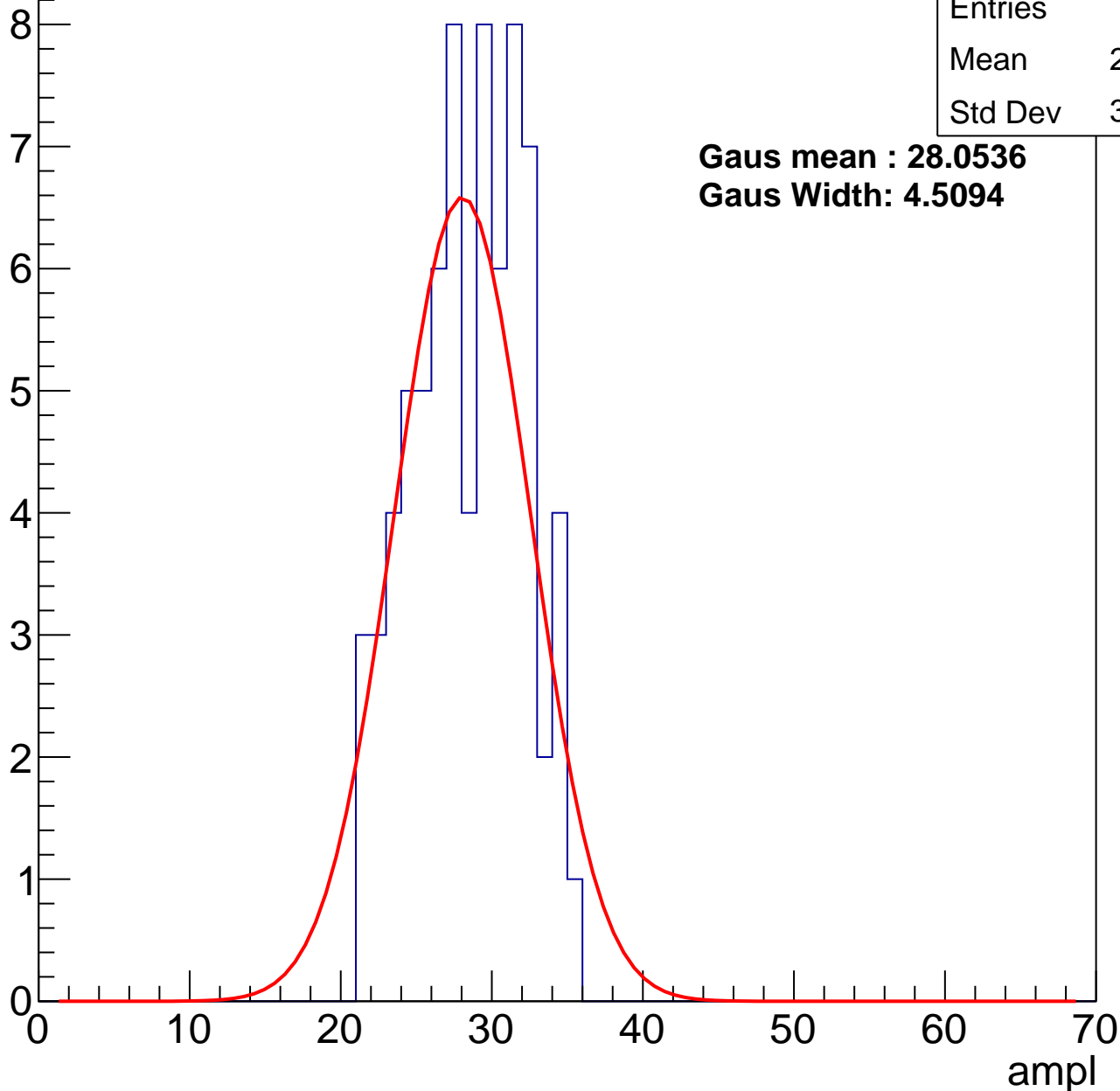
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	27.99
Std Dev	3.622

**Gaus mean : 28.0536**

**Gaus Width: 4.5094**



# B1L000S, U2-ch91, adc1

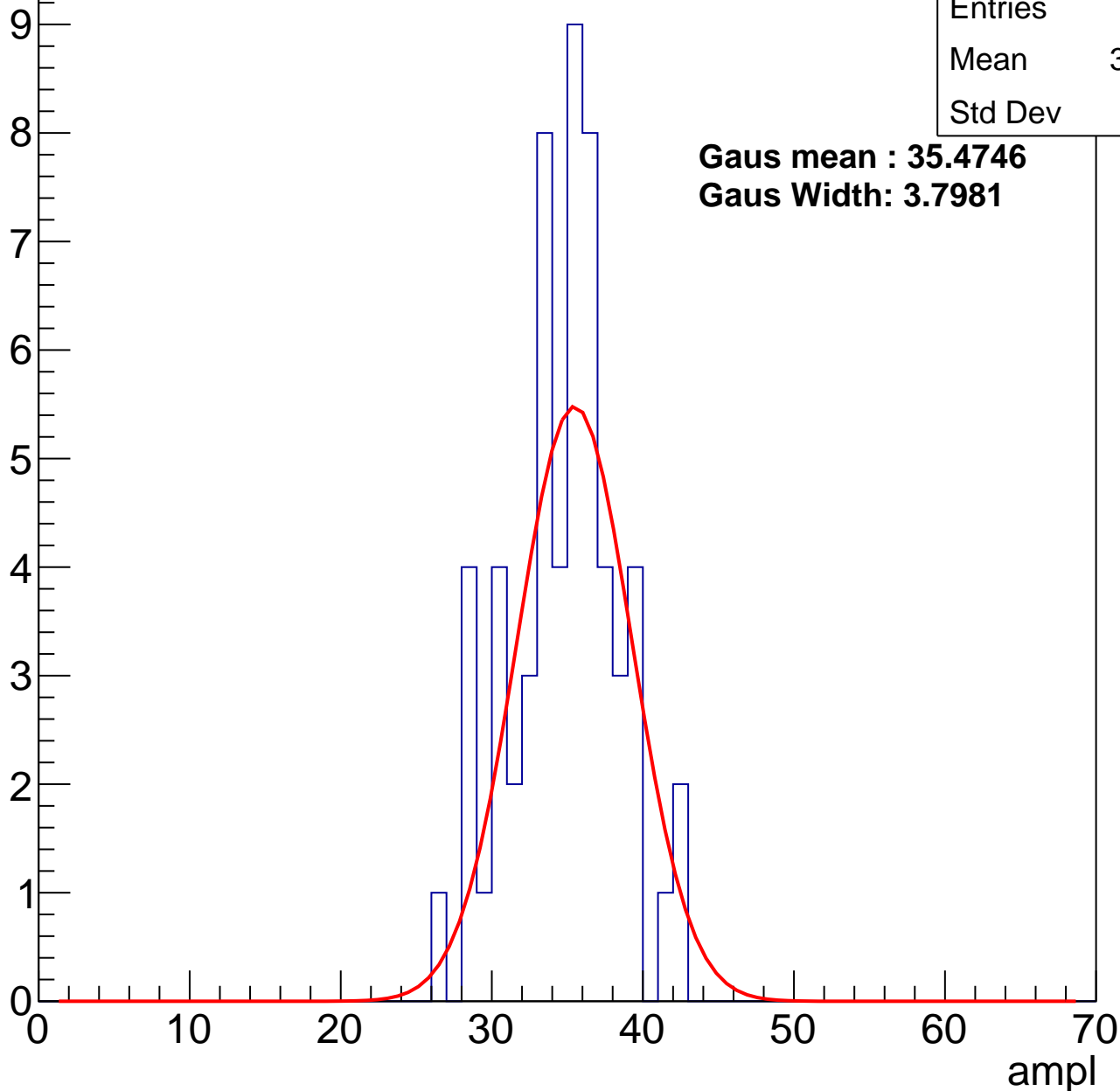
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.33
Std Dev	3.55

**Gaus mean : 35.4746**

**Gaus Width: 3.7981**



# B1L000S, U2-ch91, adc2

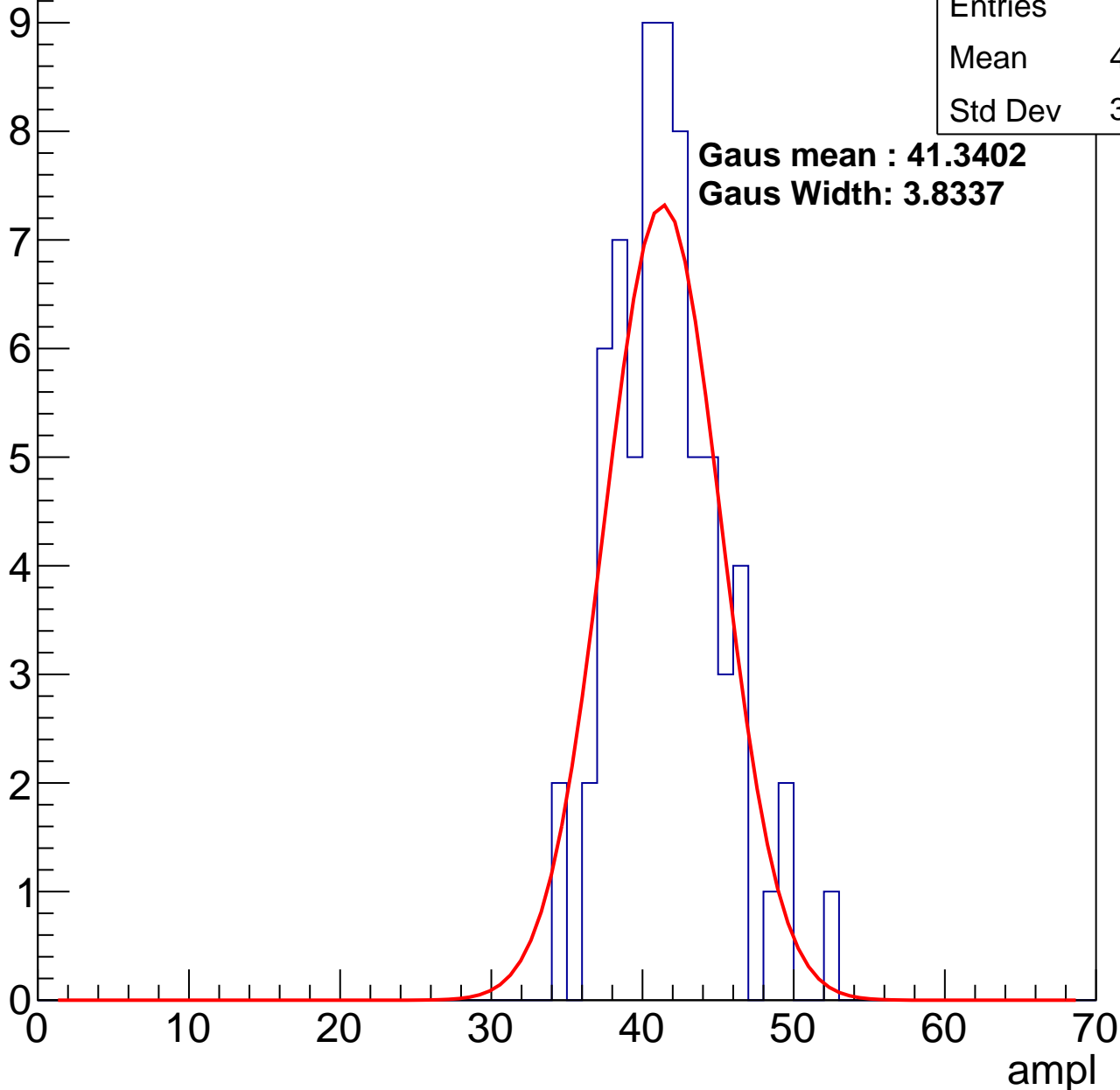
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	41.16
Std Dev	3.517

**Gaus mean : 41.3402**

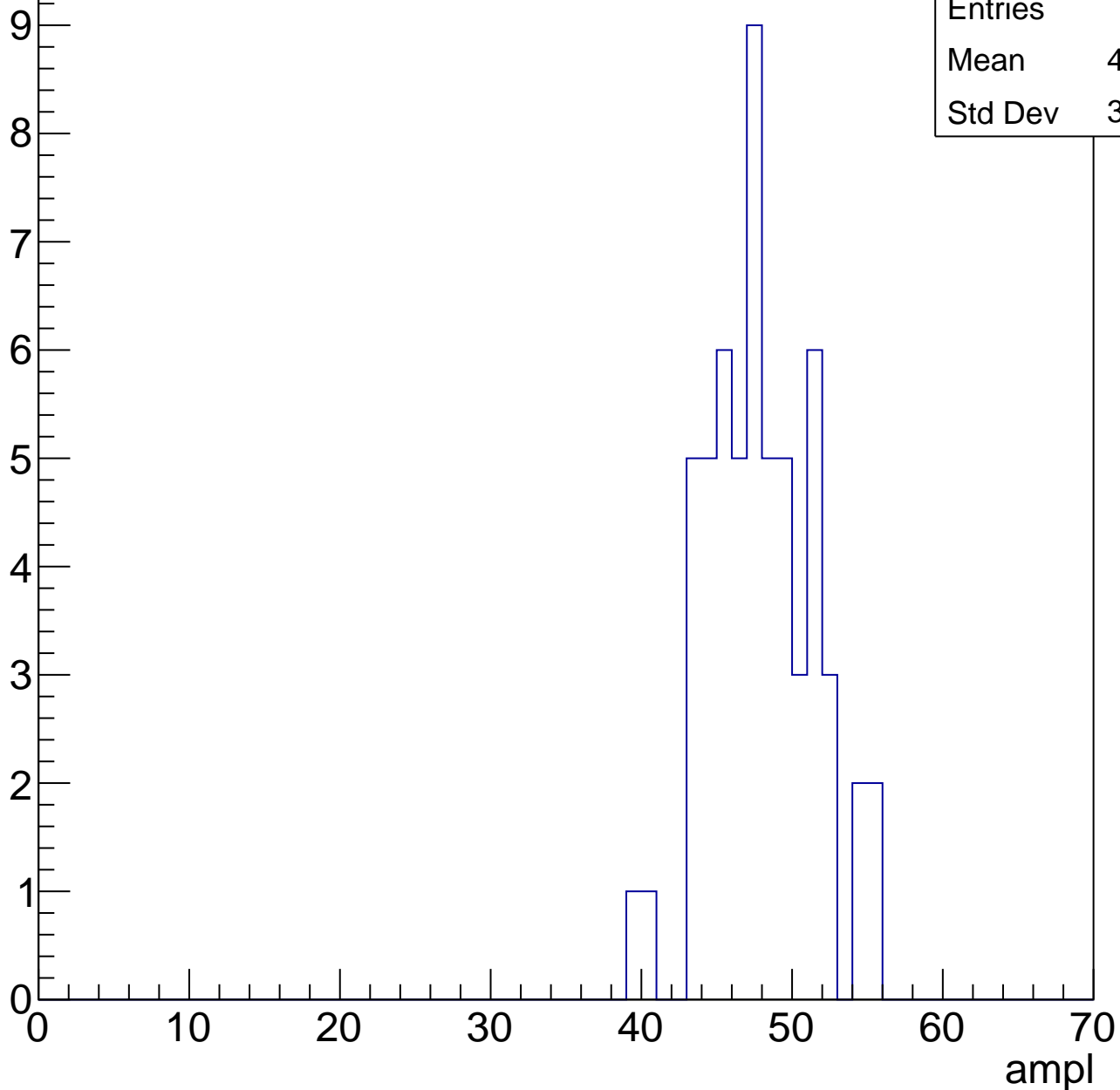
**Gaus Width: 3.8337**



# B1L000S, U2-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

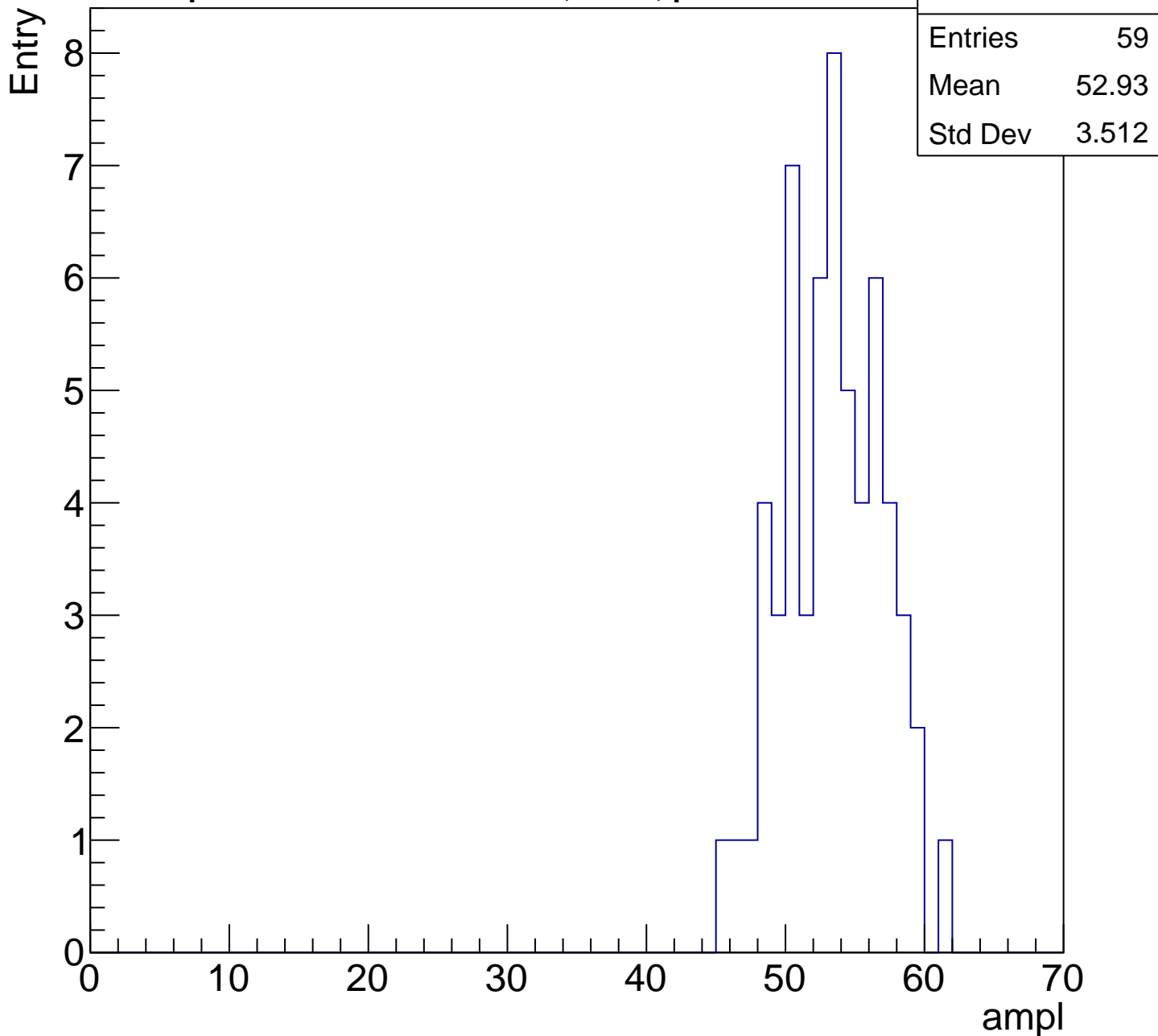
Entry



Entries	58
Mean	47.45
Std Dev	3.485

# B1L000S, U2-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

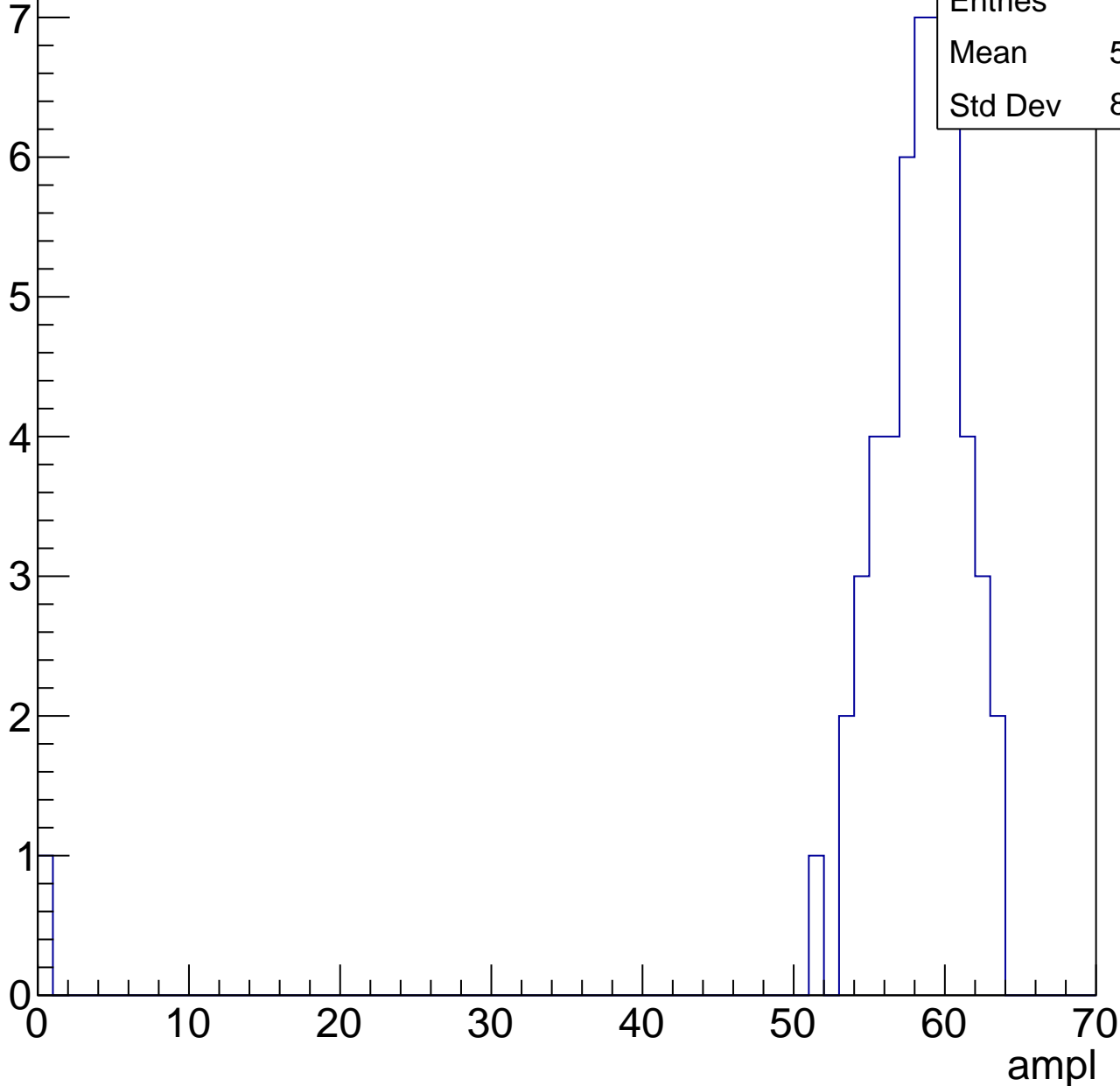


# B1L000S, U2-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

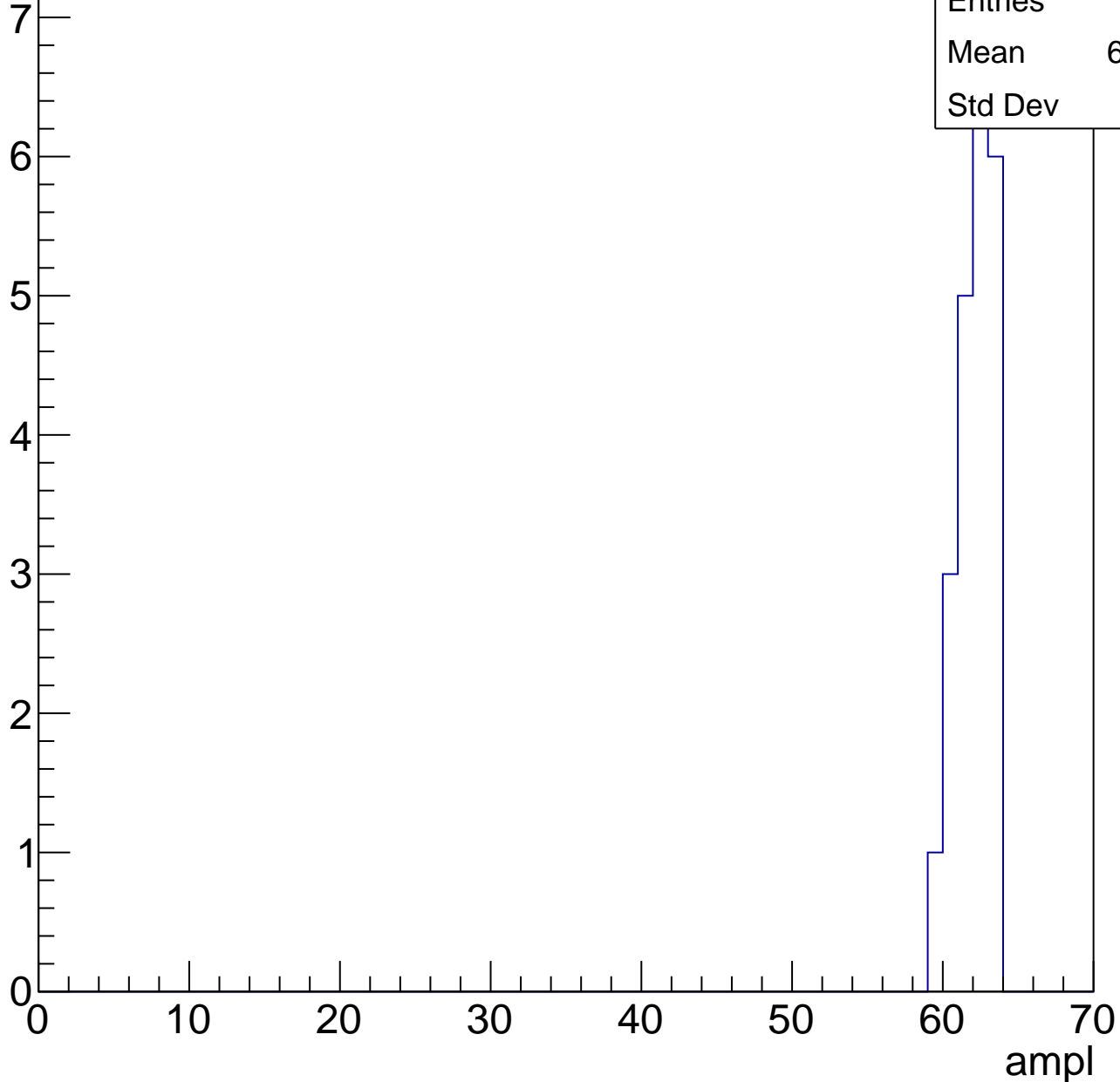
Entries	51
Mean	56.86
Std Dev	8.485



# B1L000S, U2-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

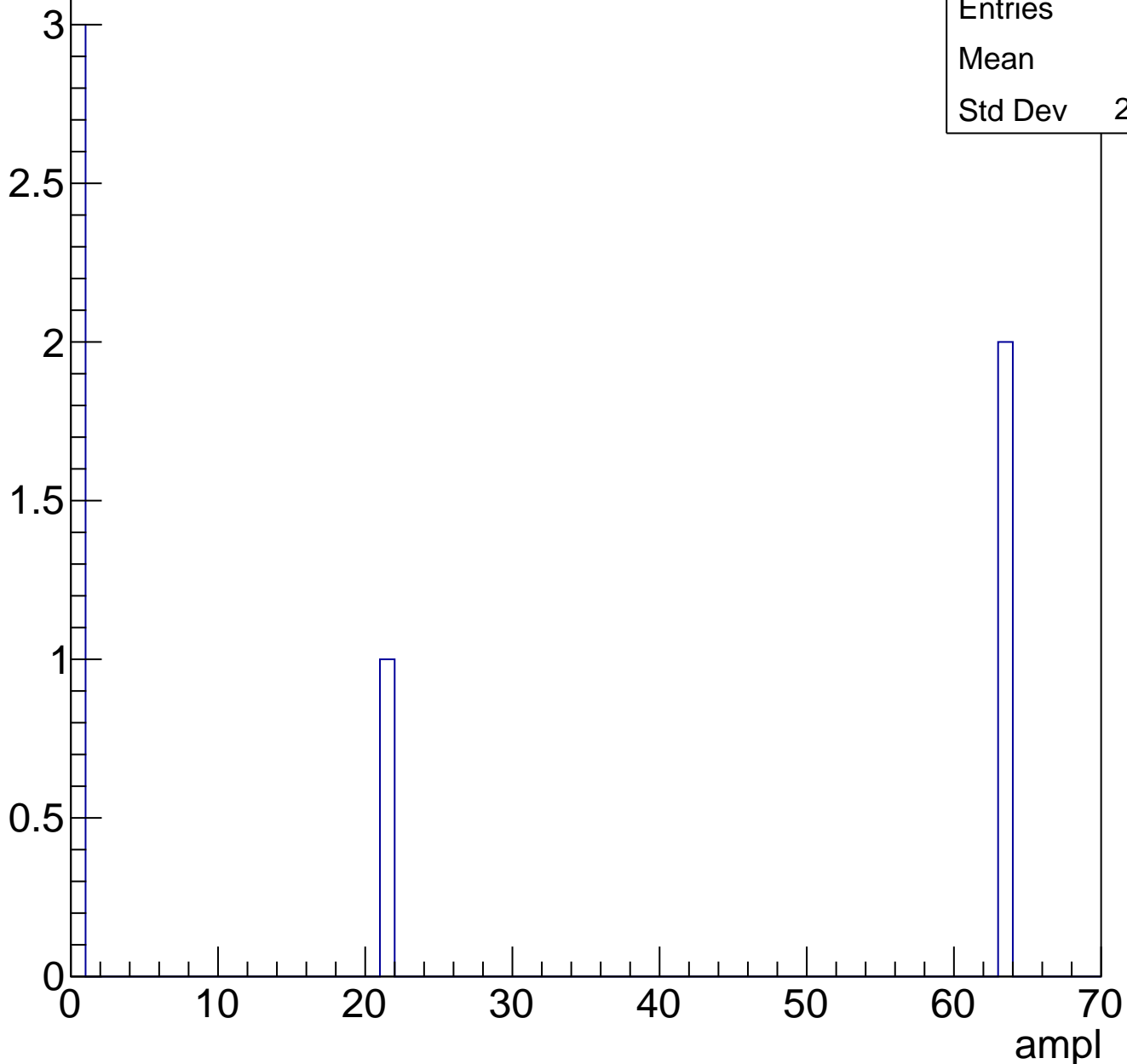




# B1L000S, U2-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	6
Mean	24.5
Std Dev	28.22

# B1L000S, U2-ch92, adc0

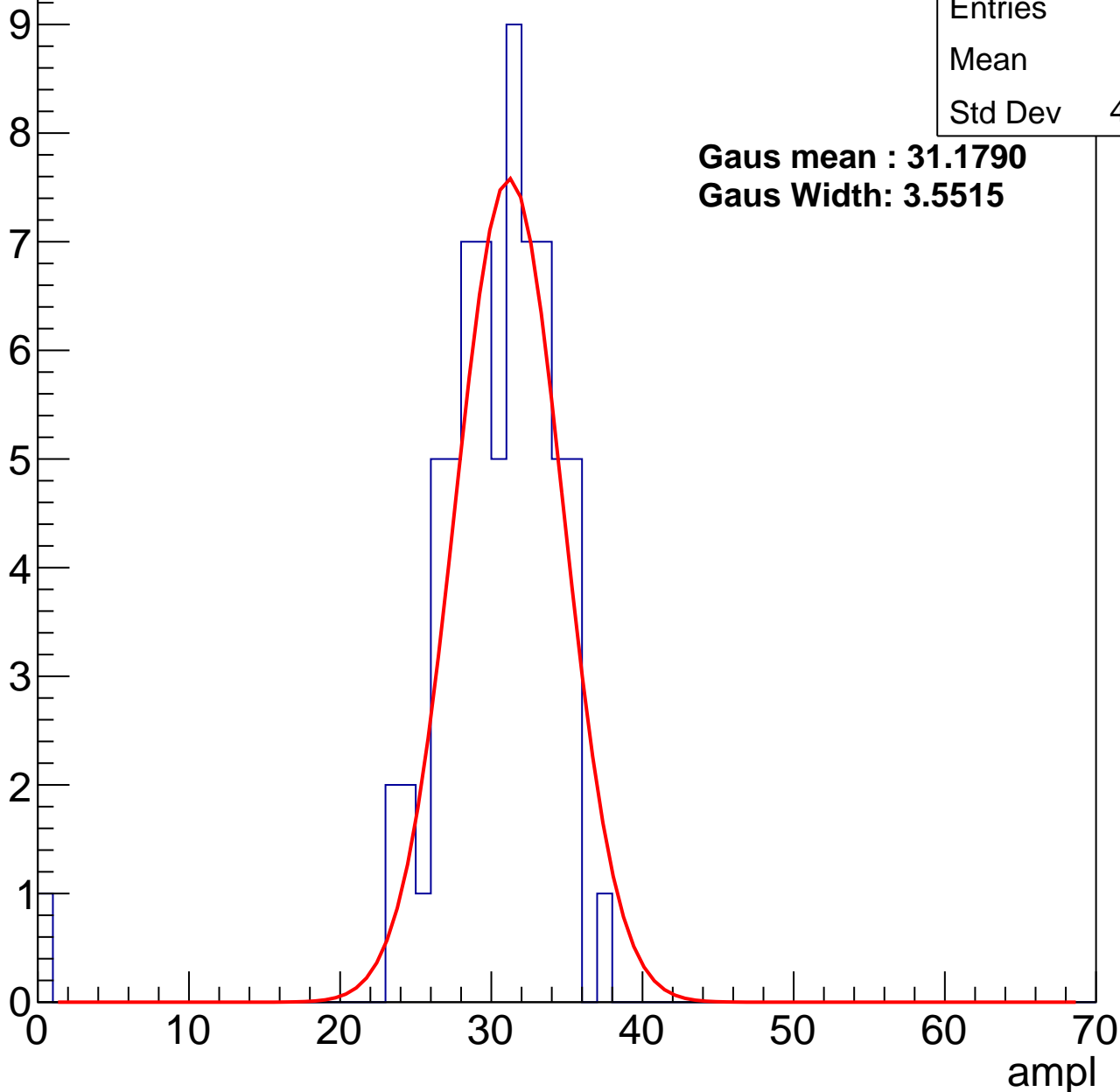
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.7
Std Dev	4.819

**Gaus mean : 31.1790**

**Gaus Width: 3.5515**



# B1L000S, U2-ch92, adc1

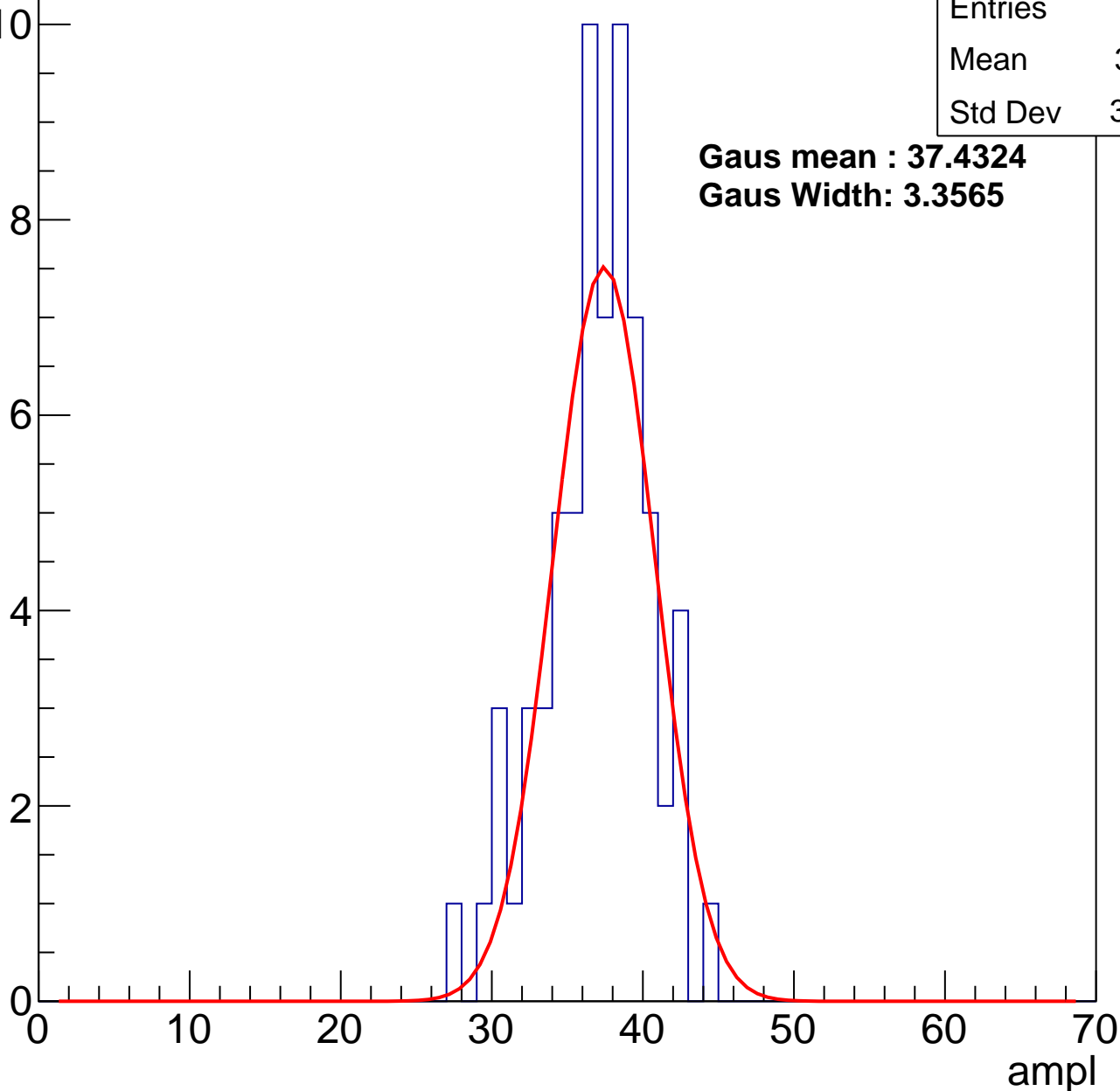
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	36.51
Std Dev	3.415

**Gaus mean : 37.4324**

**Gaus Width: 3.3565**



# B1L000S, U2-ch92, adc2

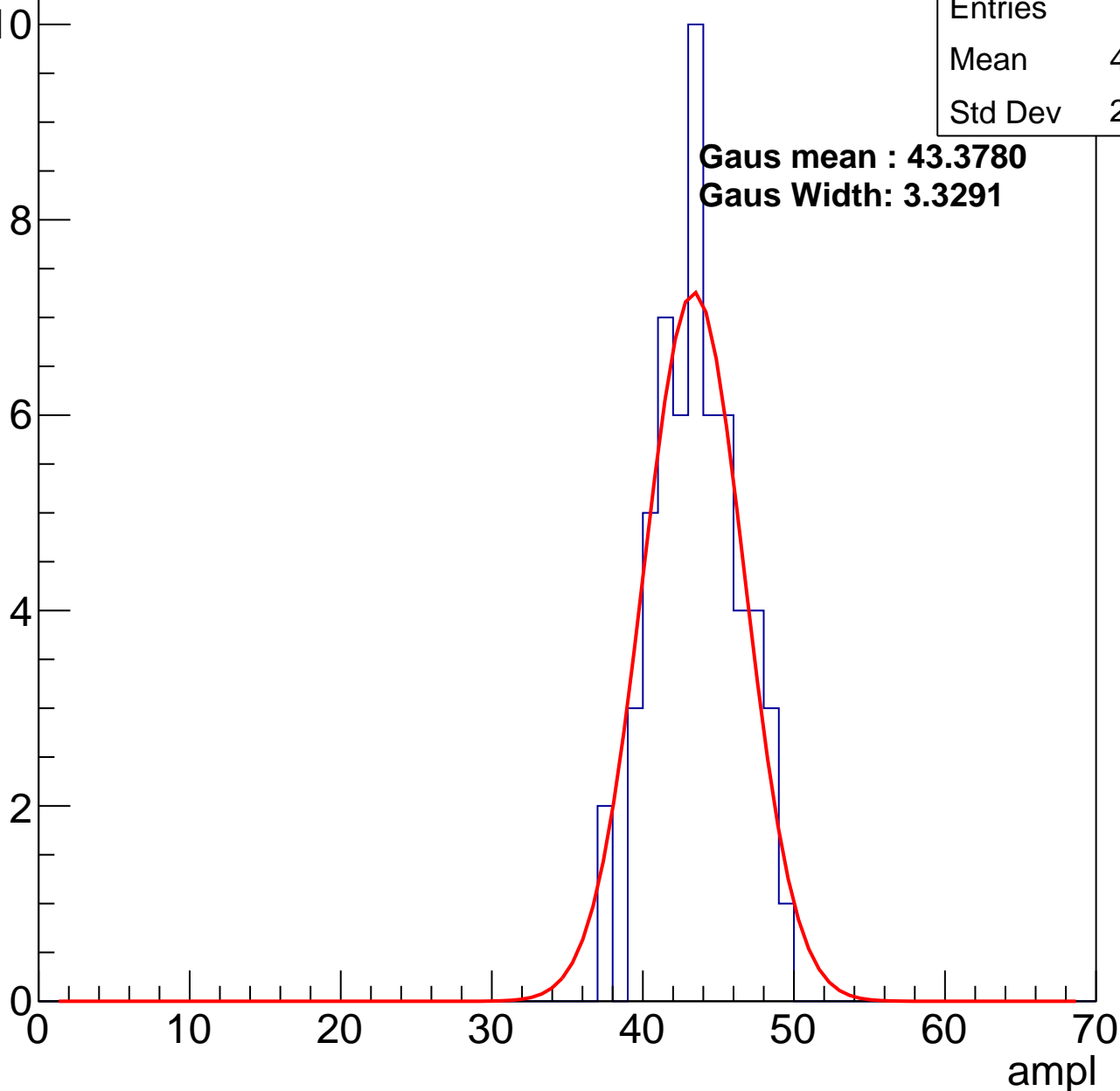
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	43.14
Std Dev	2.775

**Gaus mean : 43.3780**

**Gaus Width: 3.3291**

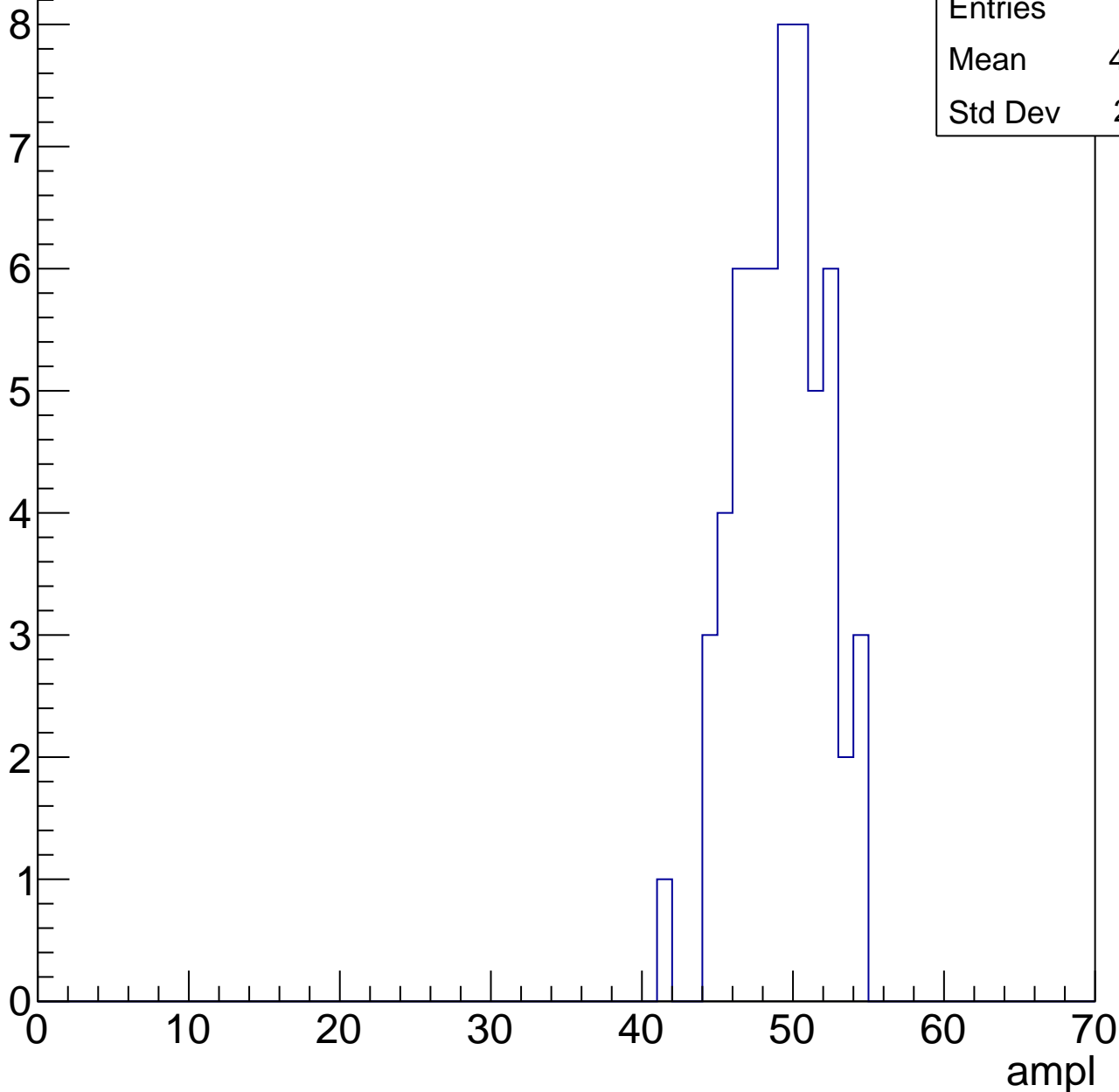


# B1L000S, U2-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	48.72
Std Dev	2.851

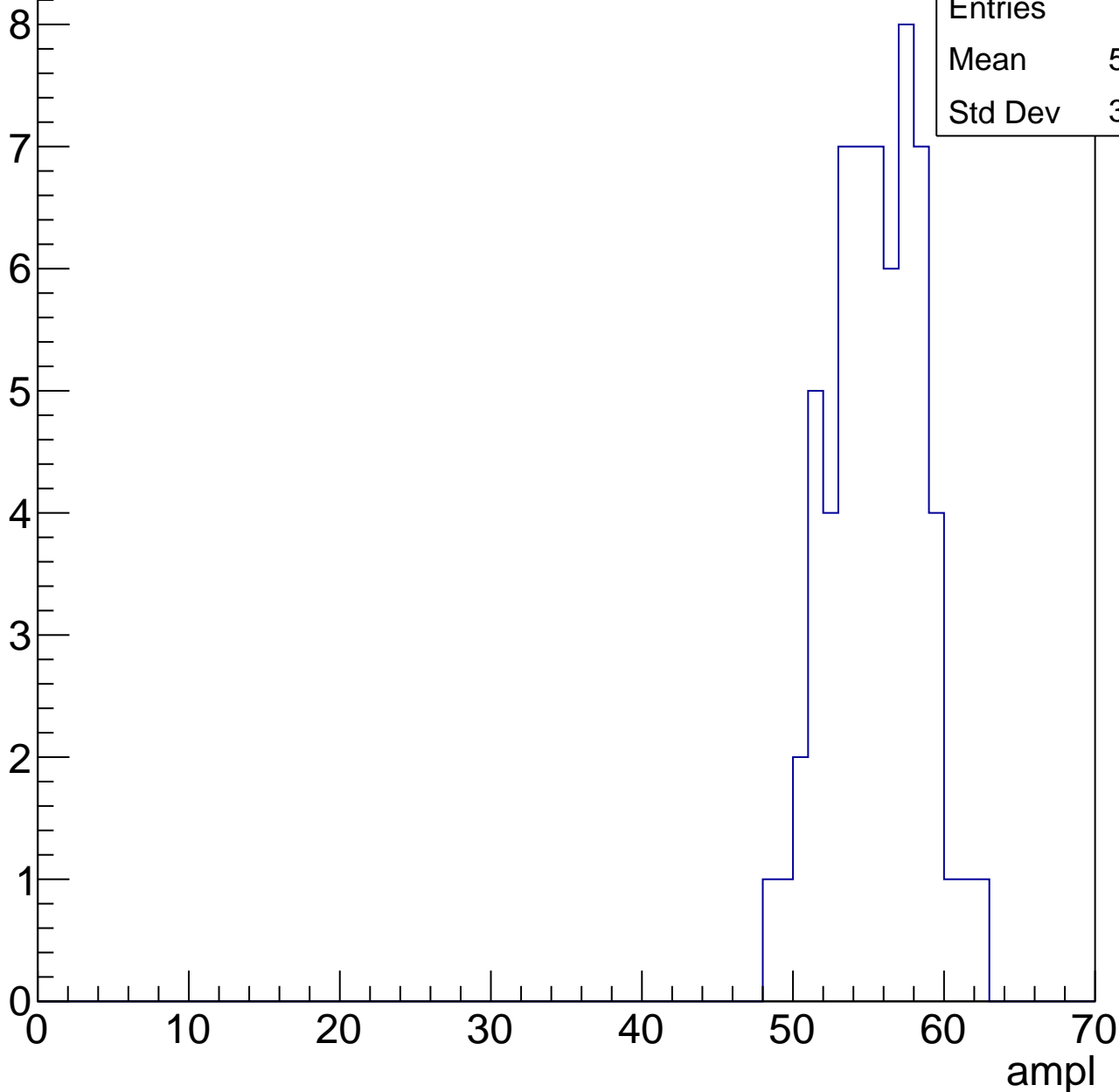


# B1L000S, U2-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	55.02
Std Dev	3.008

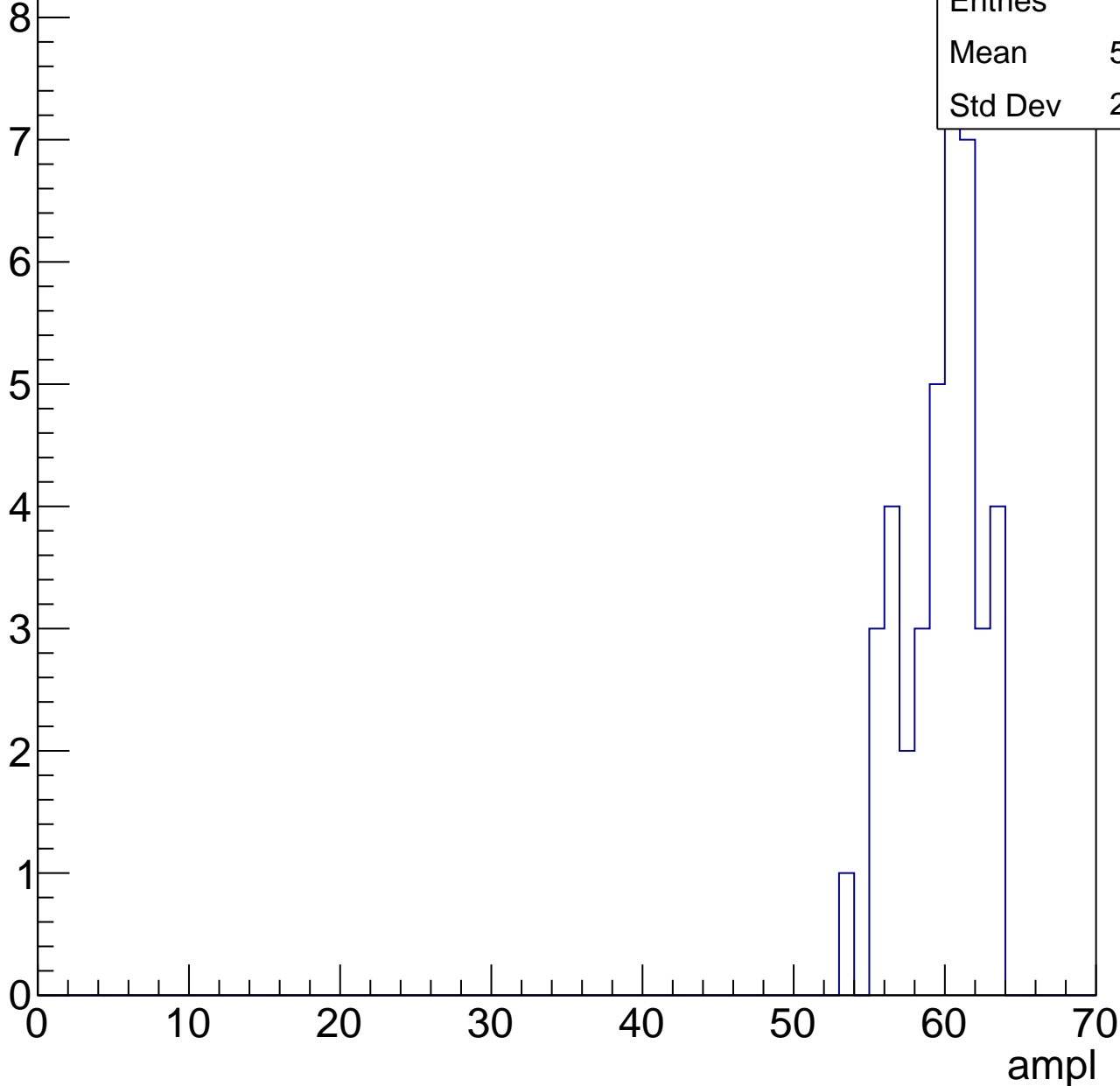


# B1L000S, U2-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	59.25
Std Dev	2.527

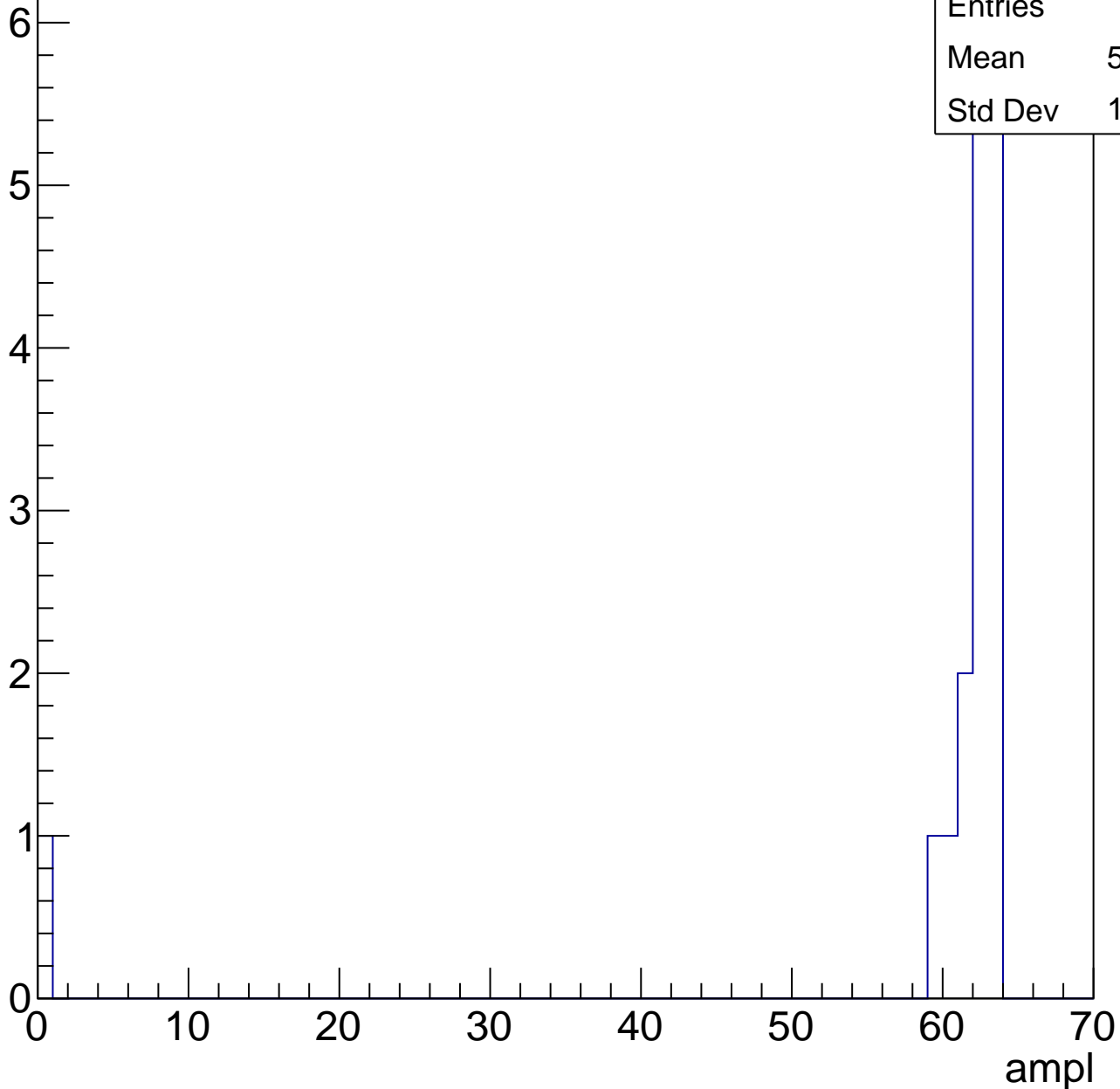


# B1L000S, U2-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.29
Std Dev	14.62





# B1L000S, U2-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U2-ch93, adc0

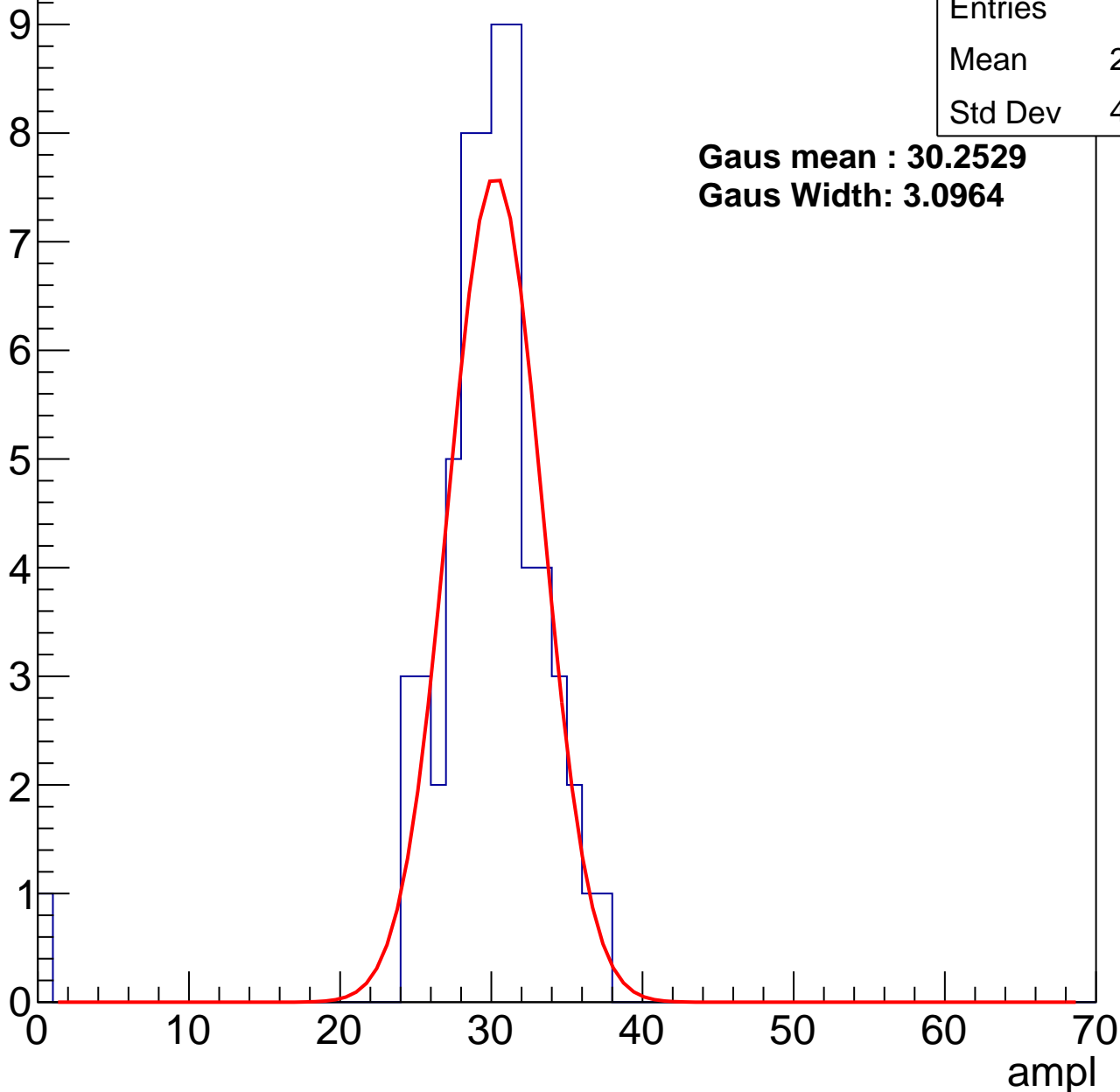
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	29.27
Std Dev	4.732

**Gaus mean : 30.2529**

**Gaus Width: 3.0964**



# B1L000S, U2-ch93, adc1

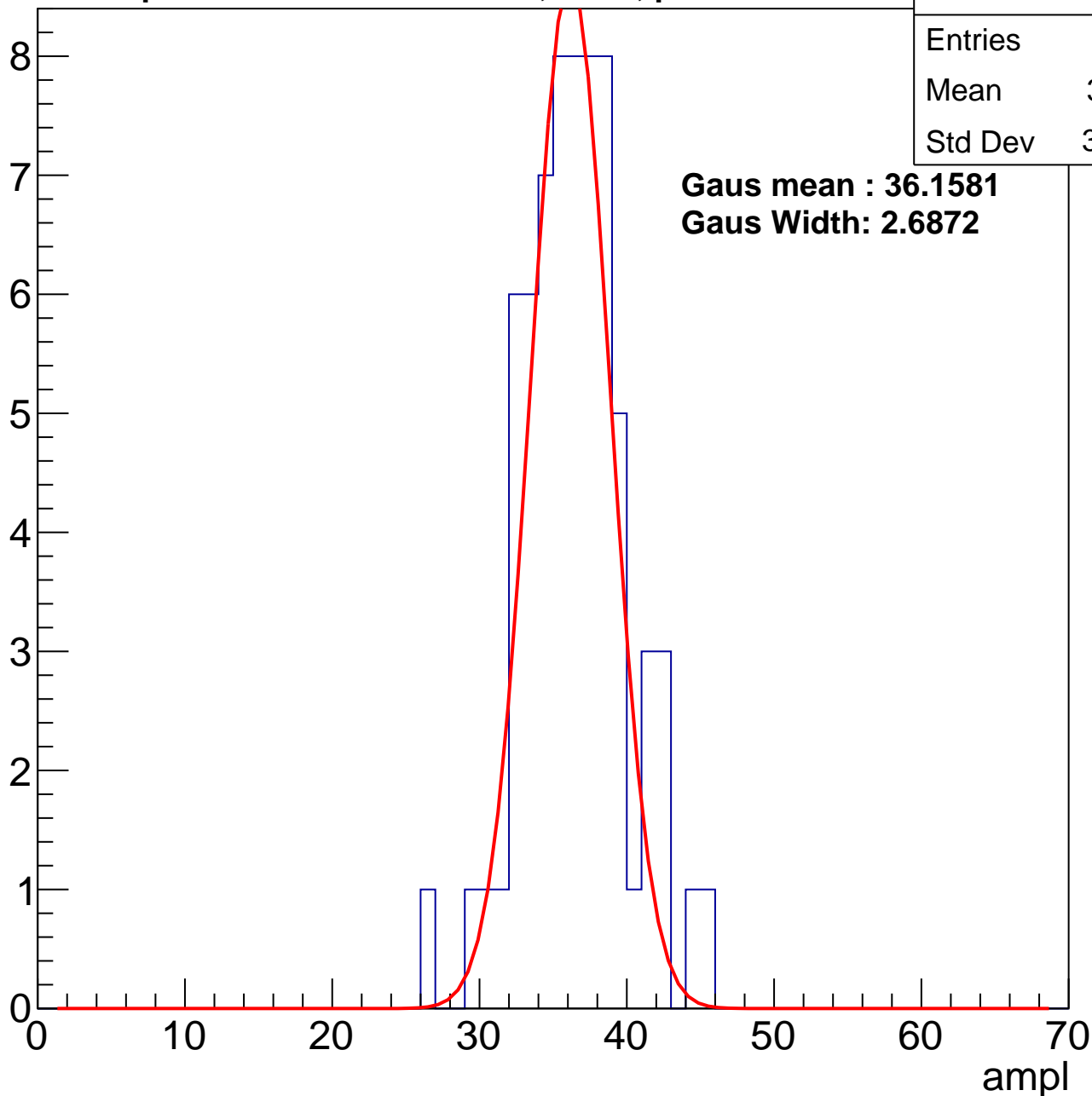
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	36.01
Std Dev	3.458

**Gaus mean : 36.1581**

**Gaus Width: 2.6872**



# B1L000S, U2-ch93, adc2

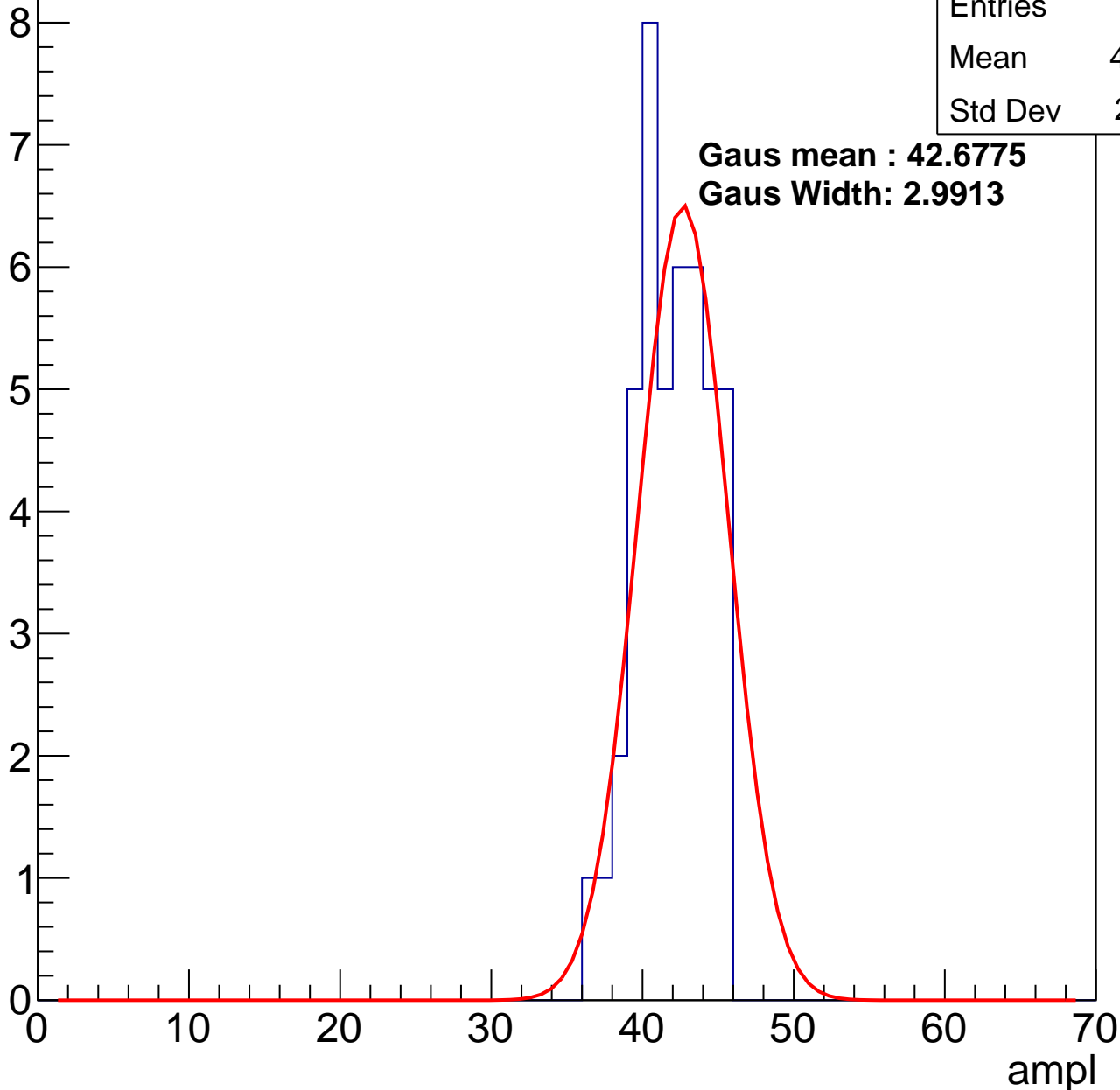
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	41.45
Std Dev	2.301

**Gaus mean : 42.6775**

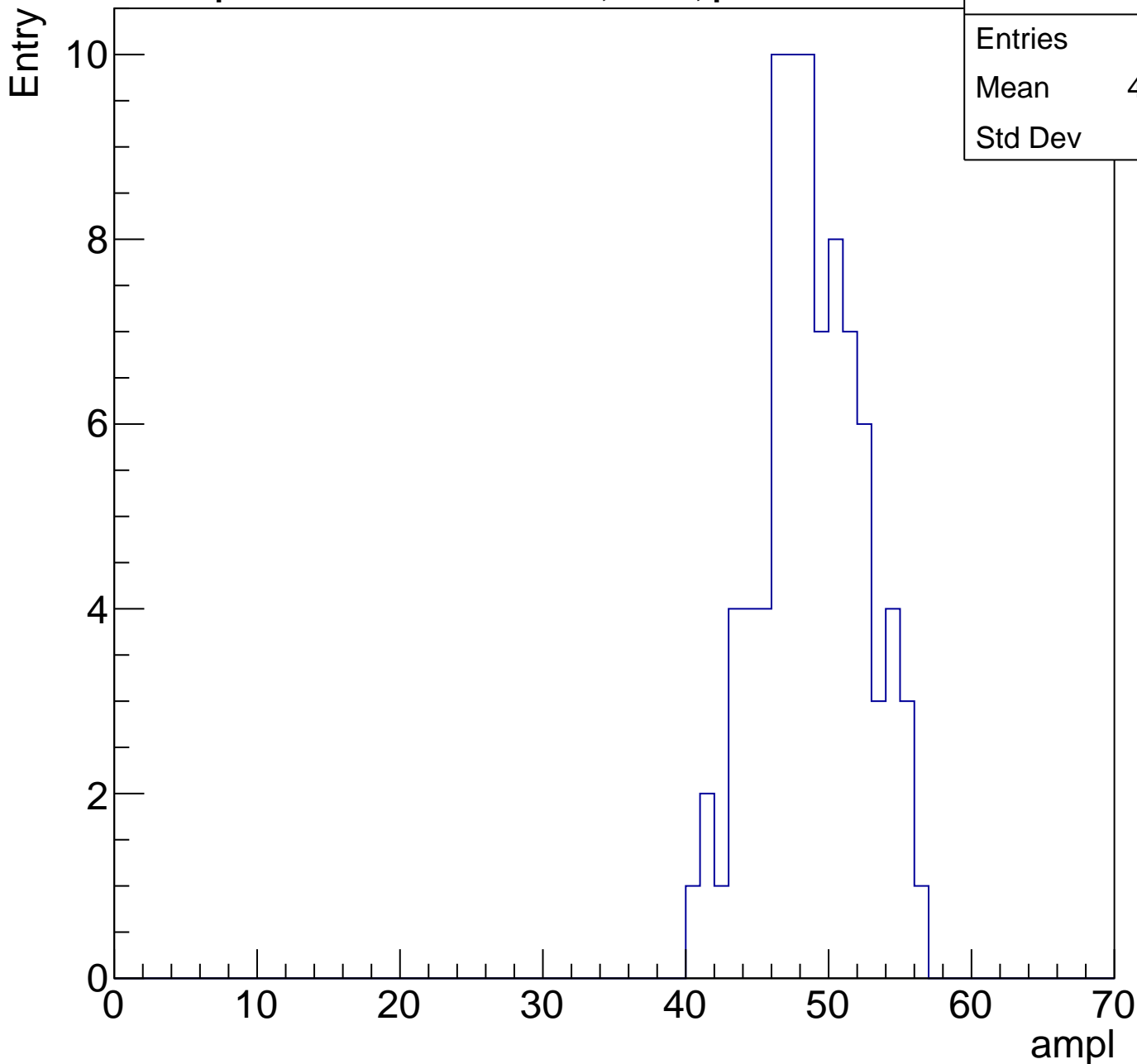
**Gaus Width: 2.9913**



# B1L000S, U2-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	85
Mean	48.35
Std Dev	3.54

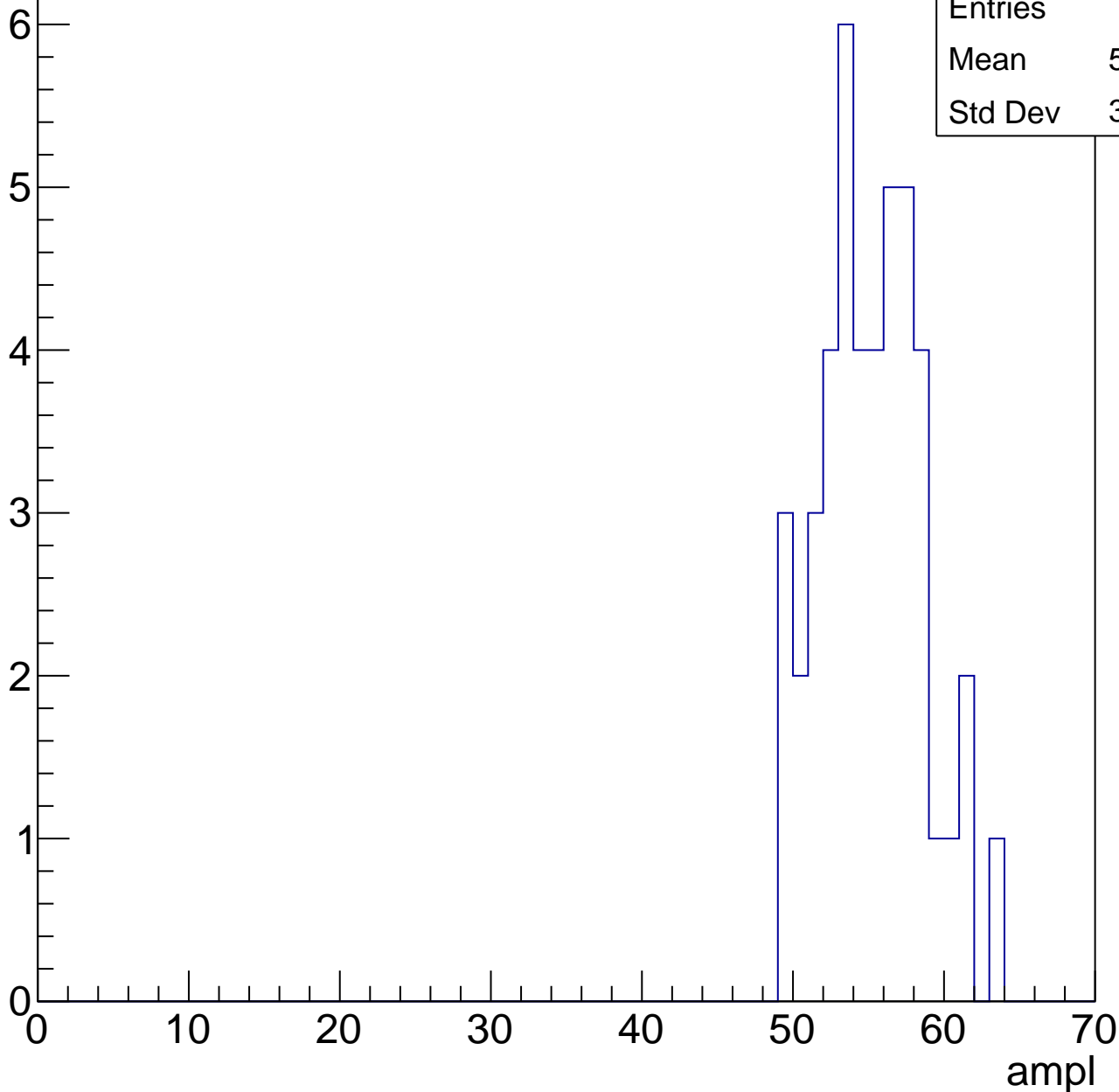


# B1L000S, U2-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

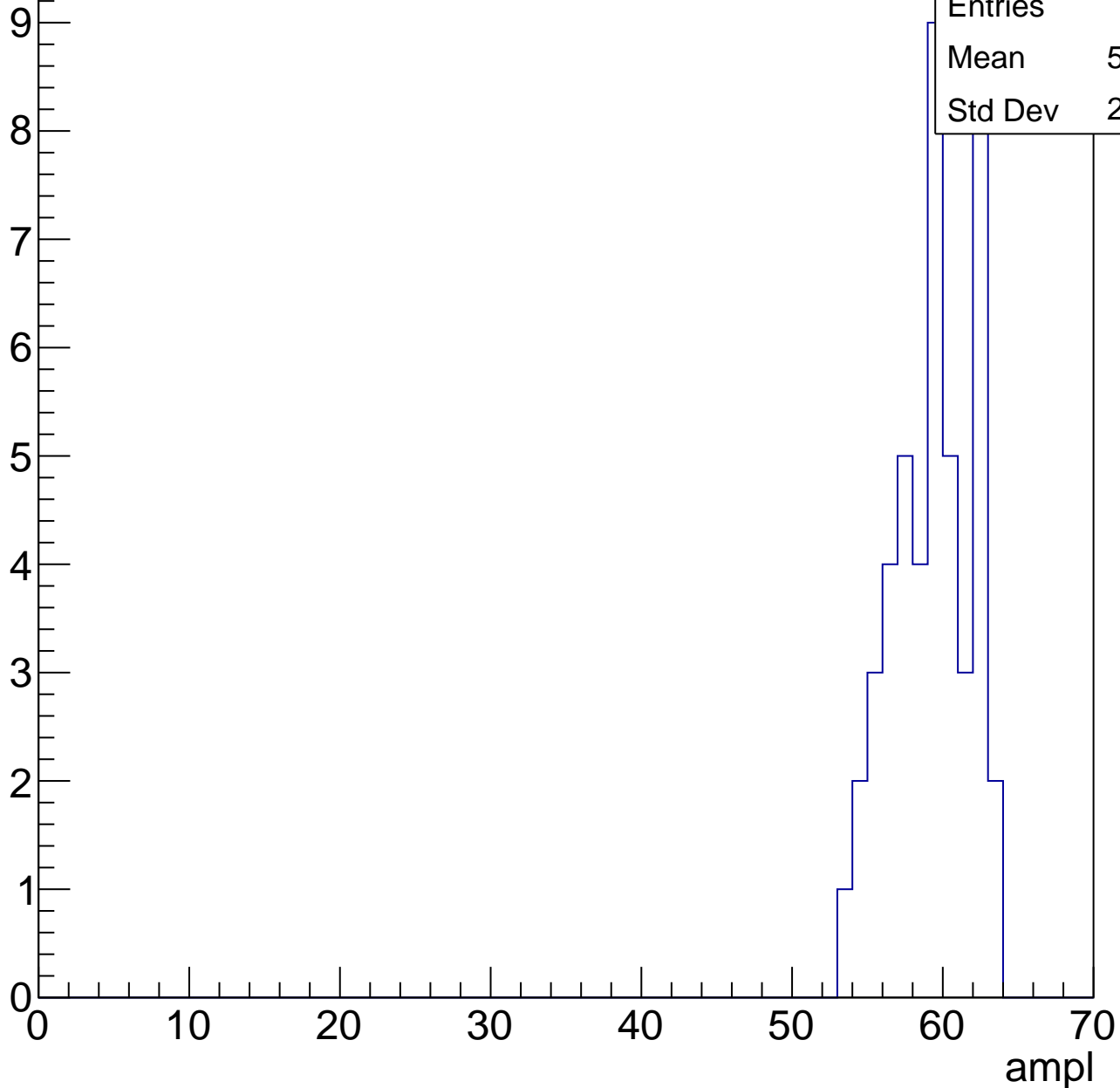
Entries	45
Mean	54.73
Std Dev	3.349



# B1L000S, U2-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

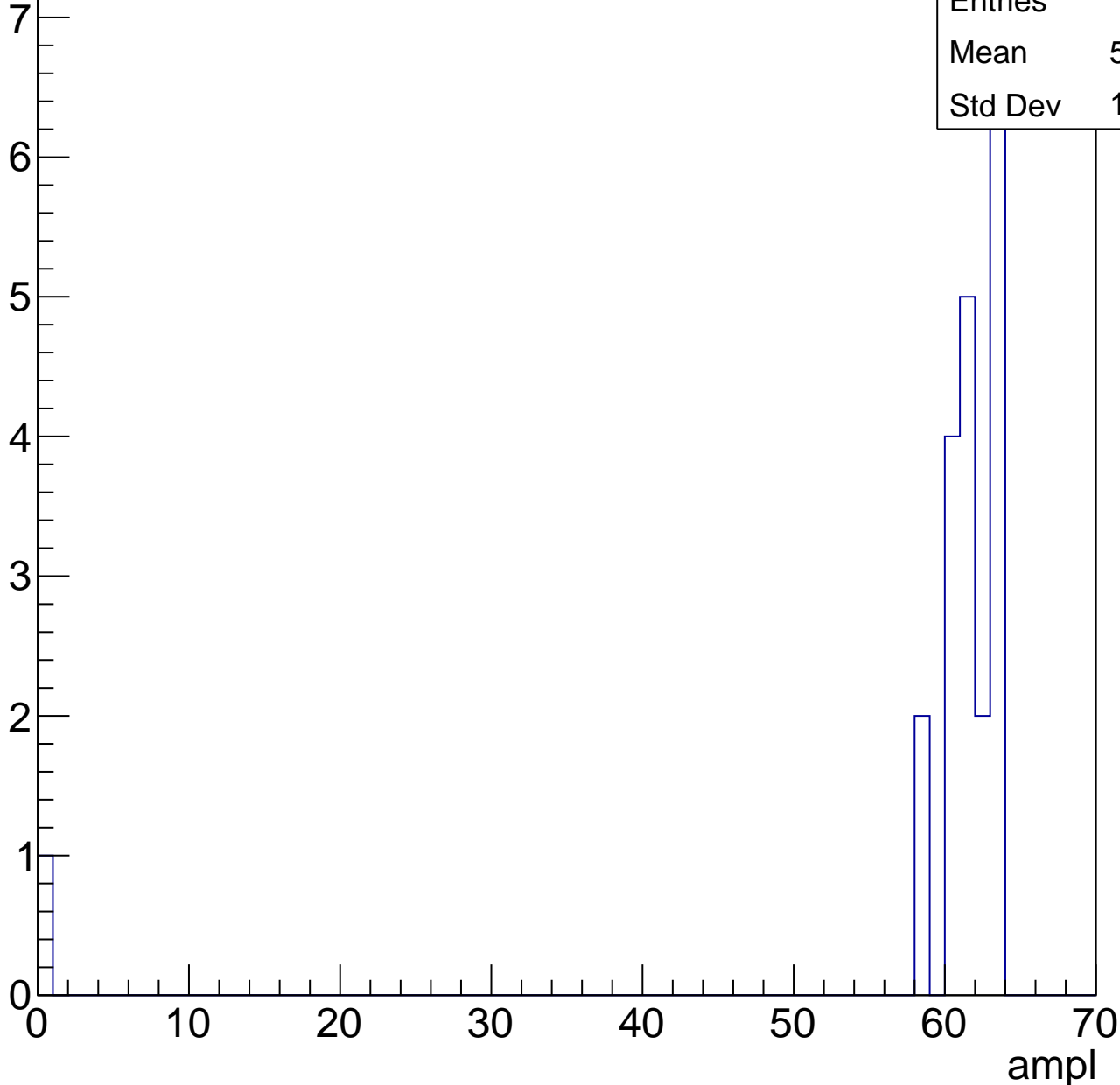


# B1L000S, U2-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.38
Std Dev	13.15





# B1L000S, U2-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch94, adc0

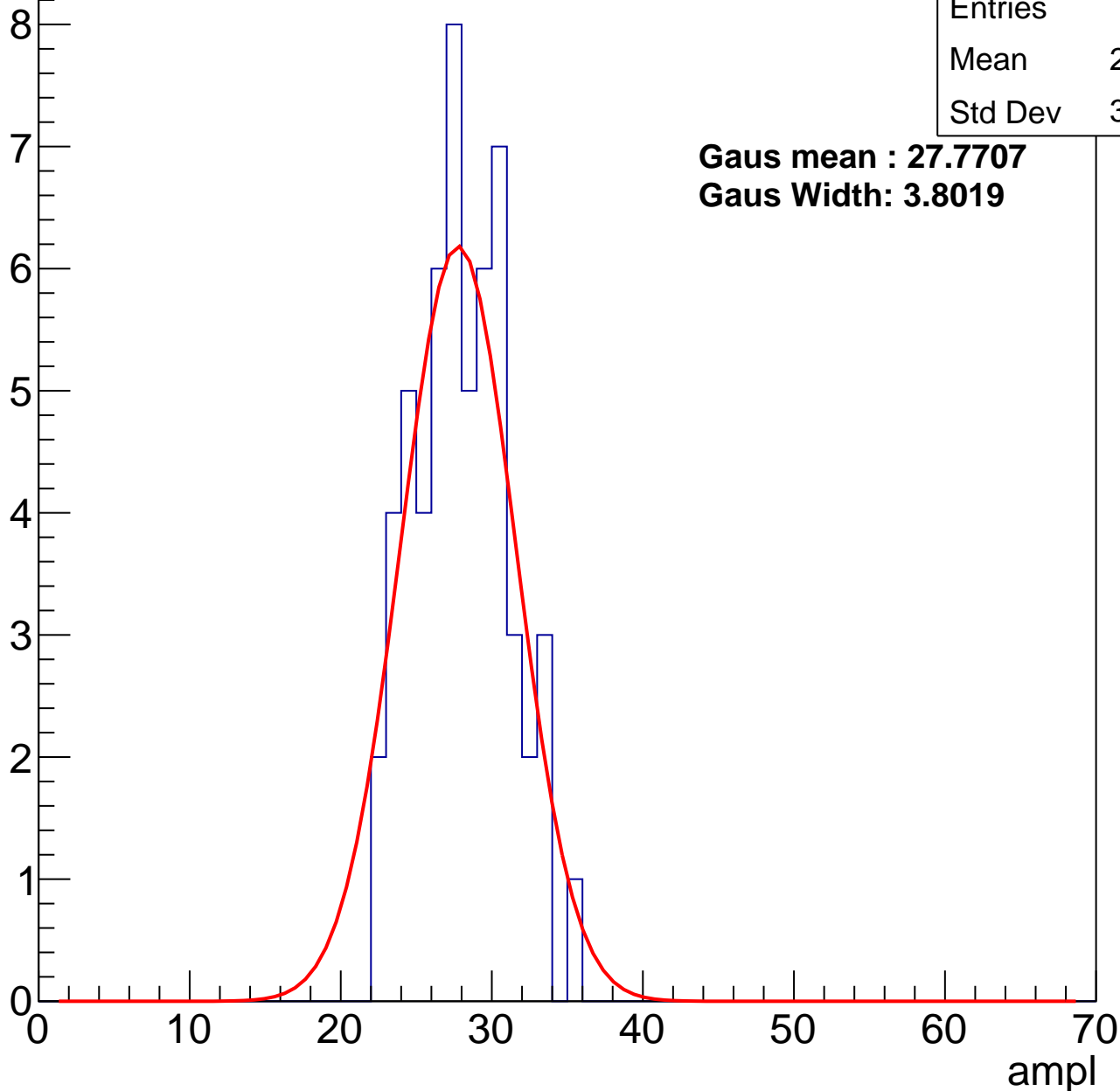
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	27.55
Std Dev	3.064

**Gaus mean : 27.7707**

**Gaus Width: 3.8019**



# B1L000S, U2-ch94, adc1

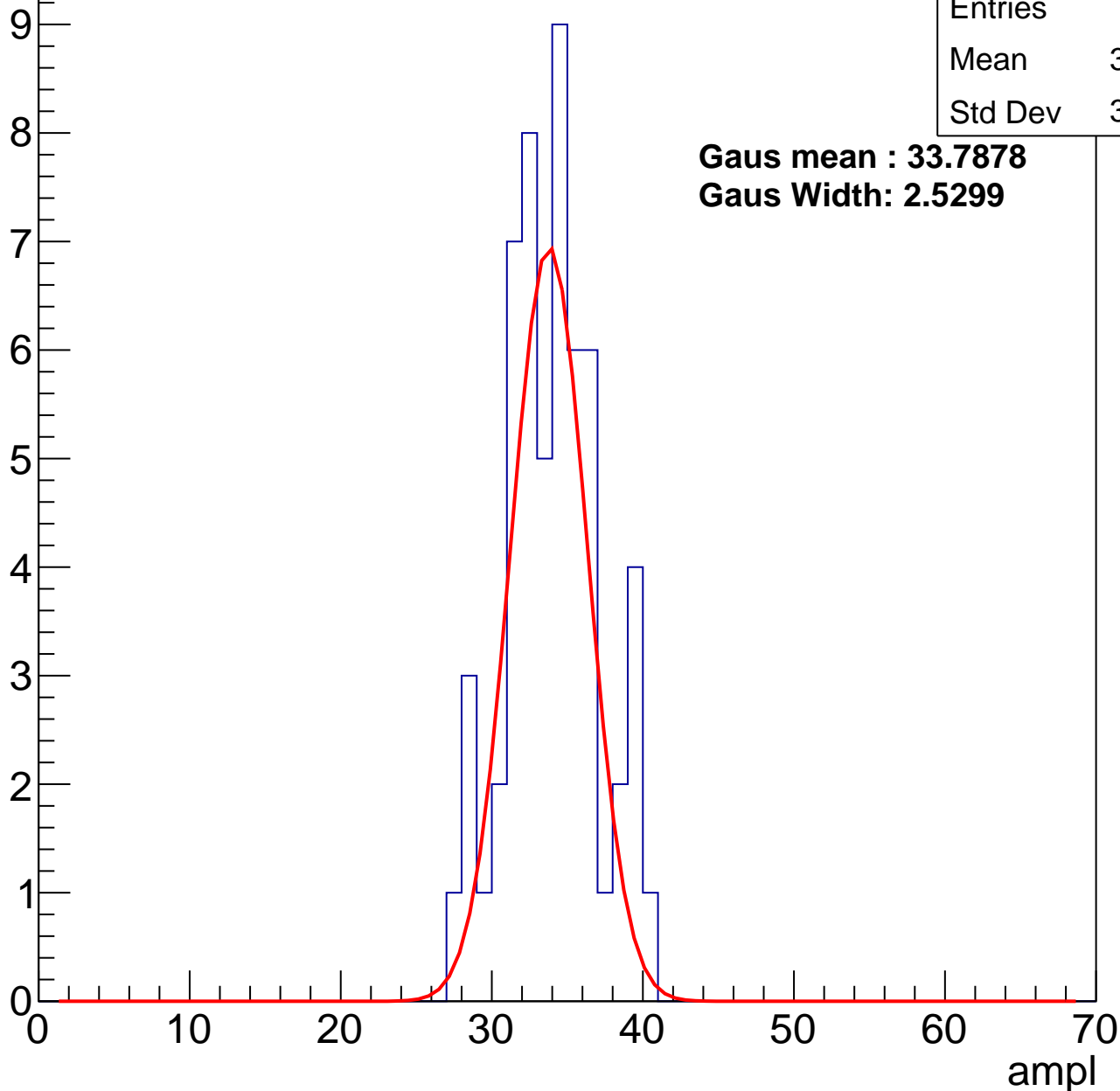
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	33.55
Std Dev	3.017

**Gaus mean : 33.7878**

**Gaus Width: 2.5299**



# B1L000S, U2-ch94, adc2

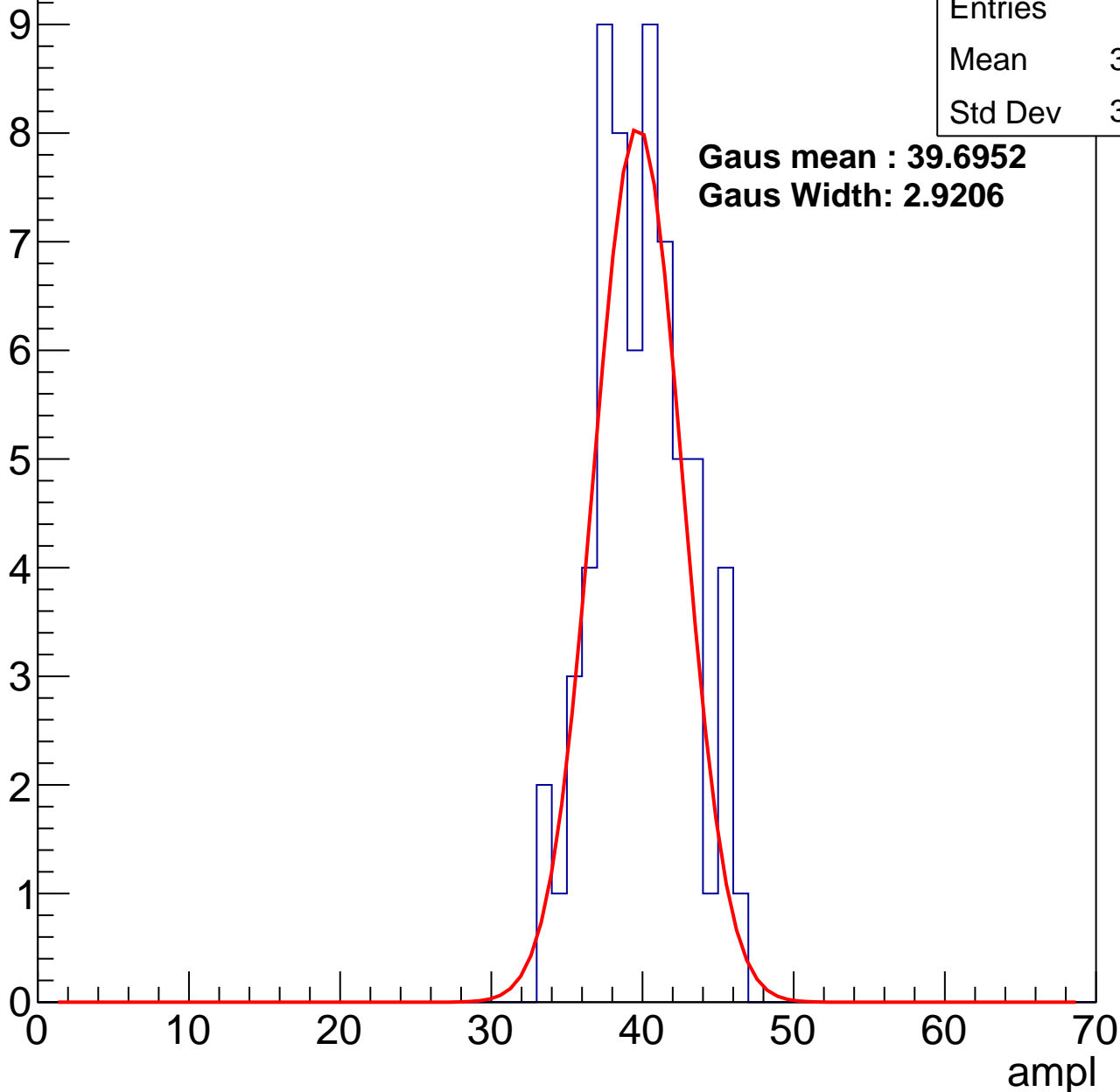
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	39.42
Std Dev	3.022

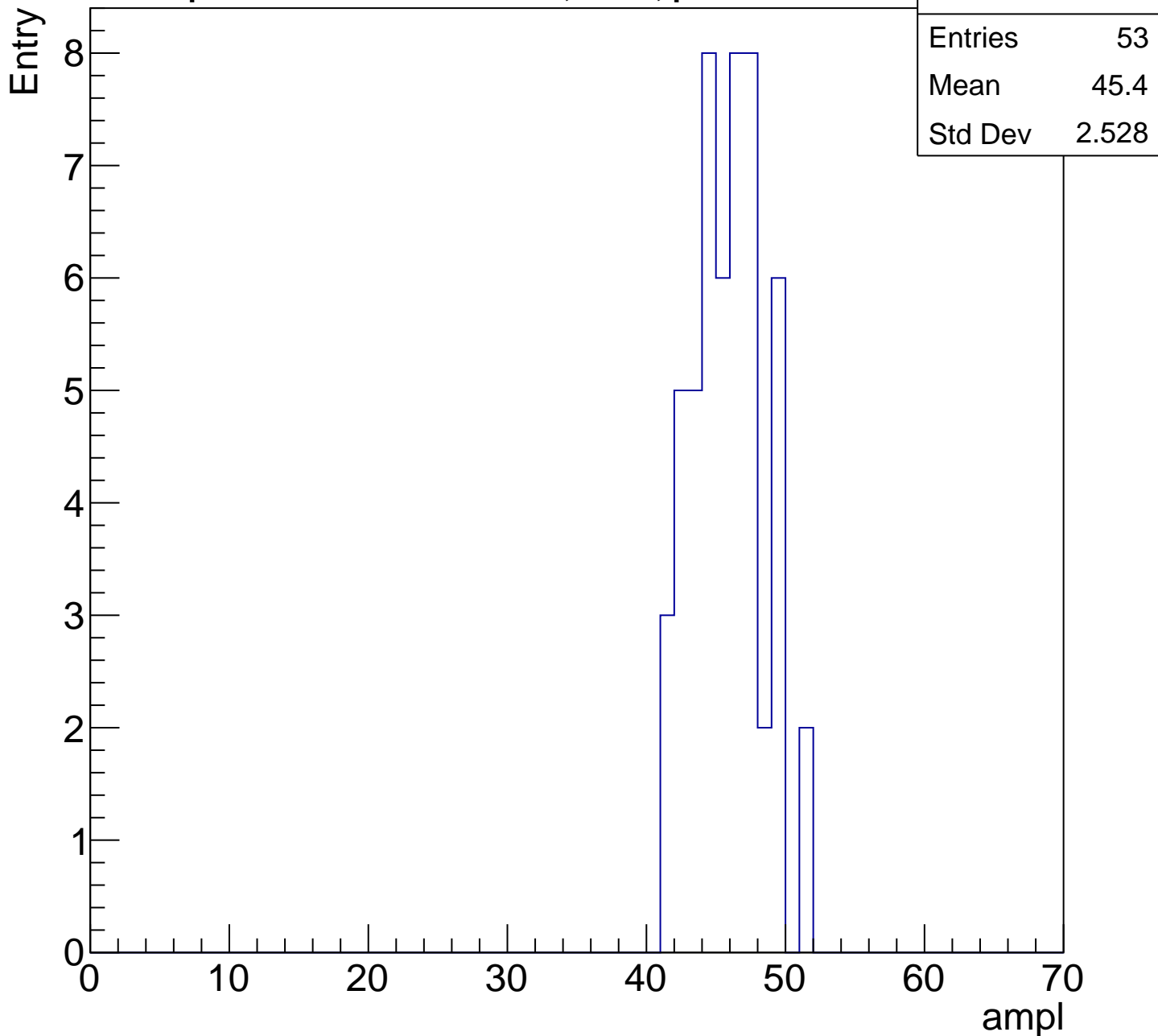
**Gaus mean : 39.6952**

**Gaus Width: 2.9206**



# B1L000S, U2-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

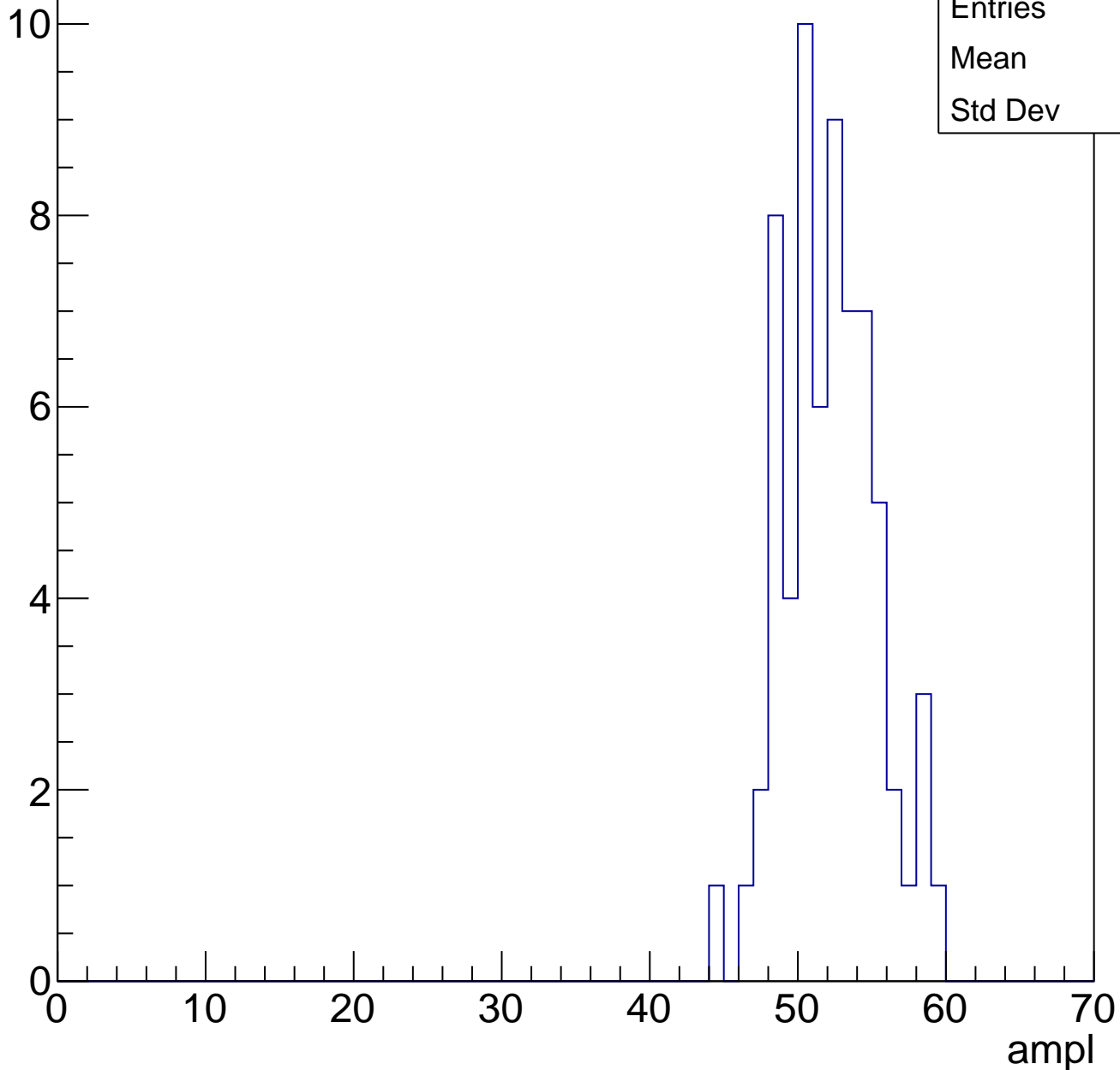


# B1L000S, U2-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	51.7
Std Dev	3.11

Entry

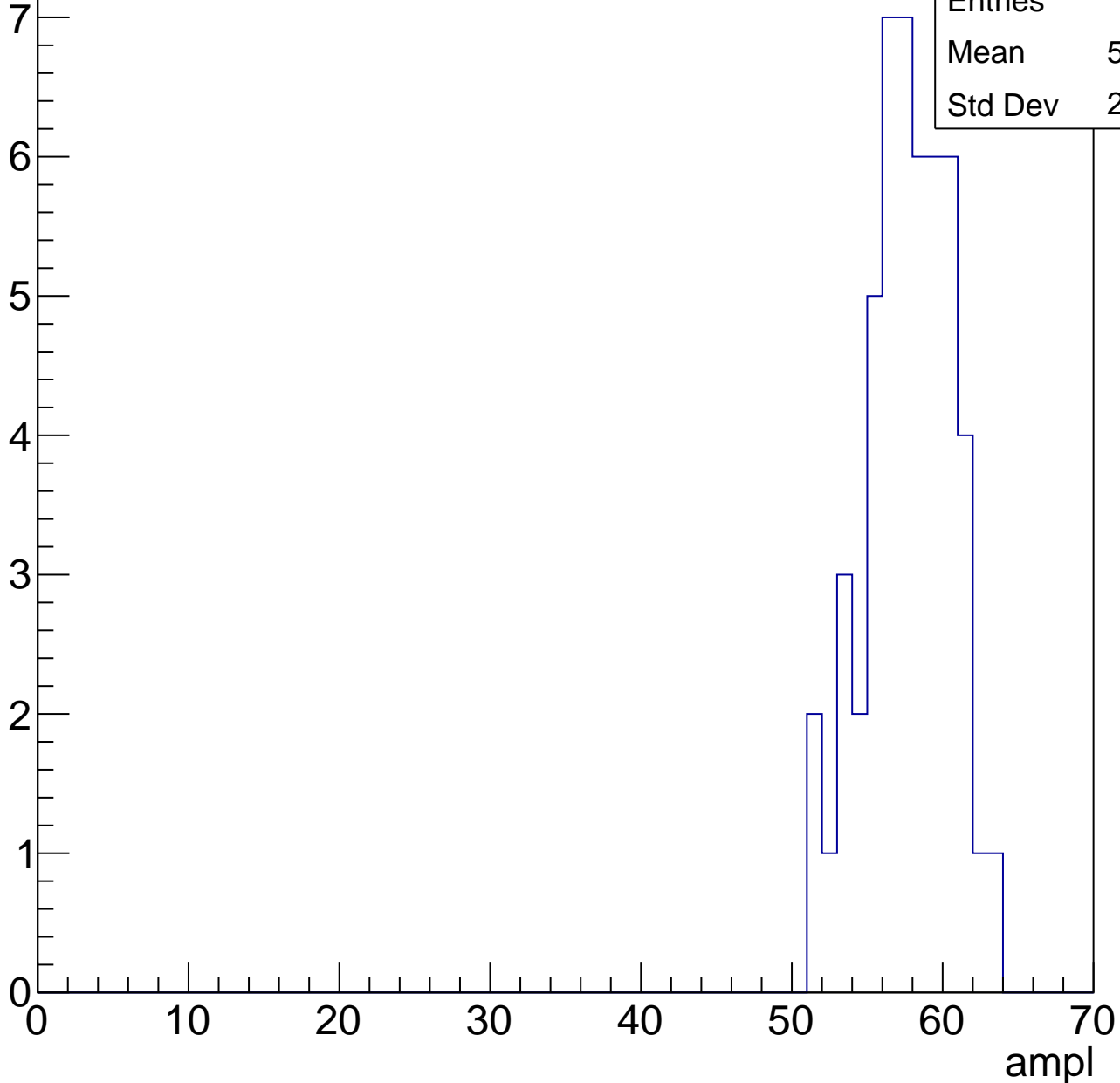


# B1L000S, U2-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	57.22
Std Dev	2.789

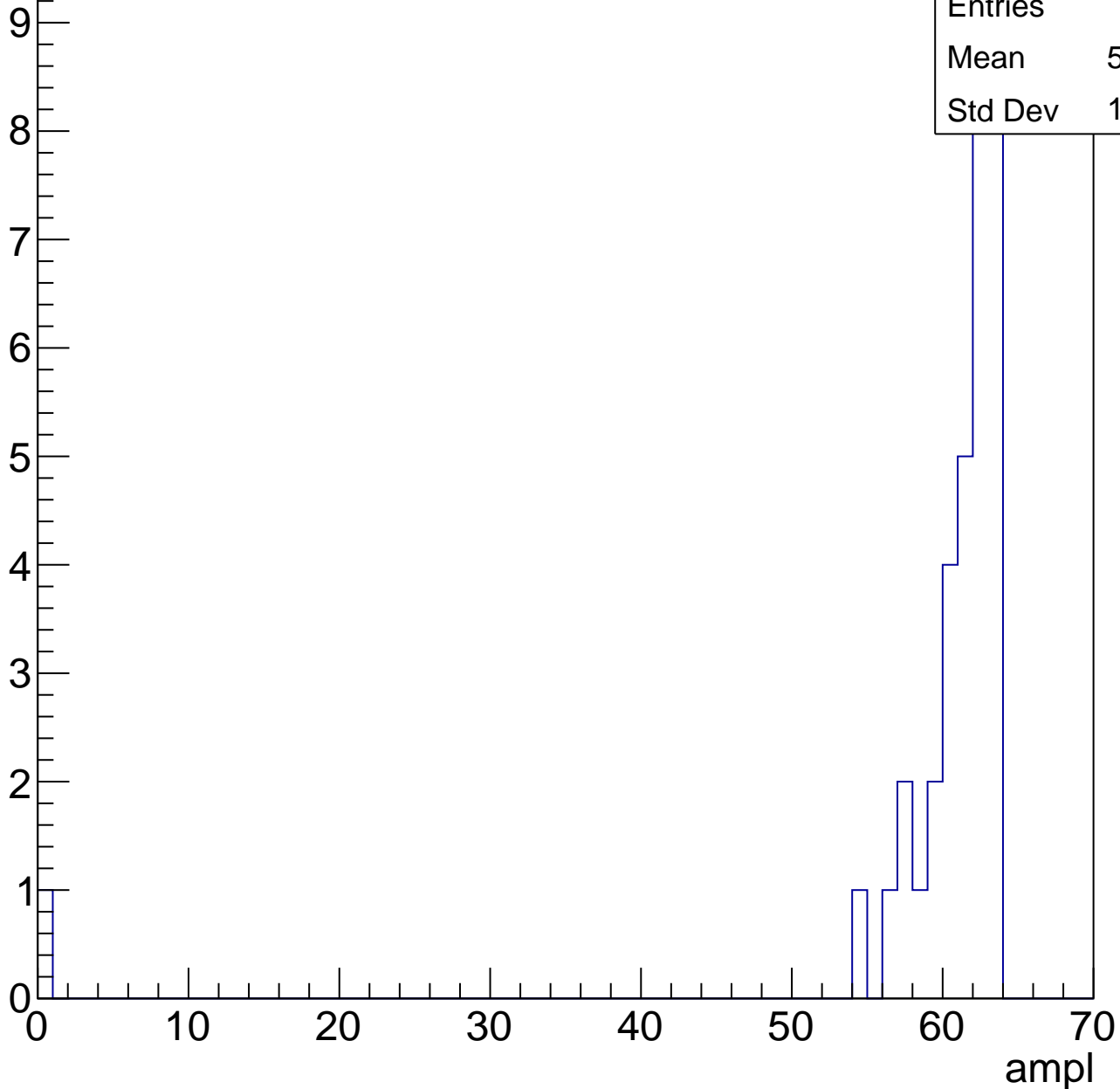


# B1L000S, U2-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	59.06
Std Dev	10.52

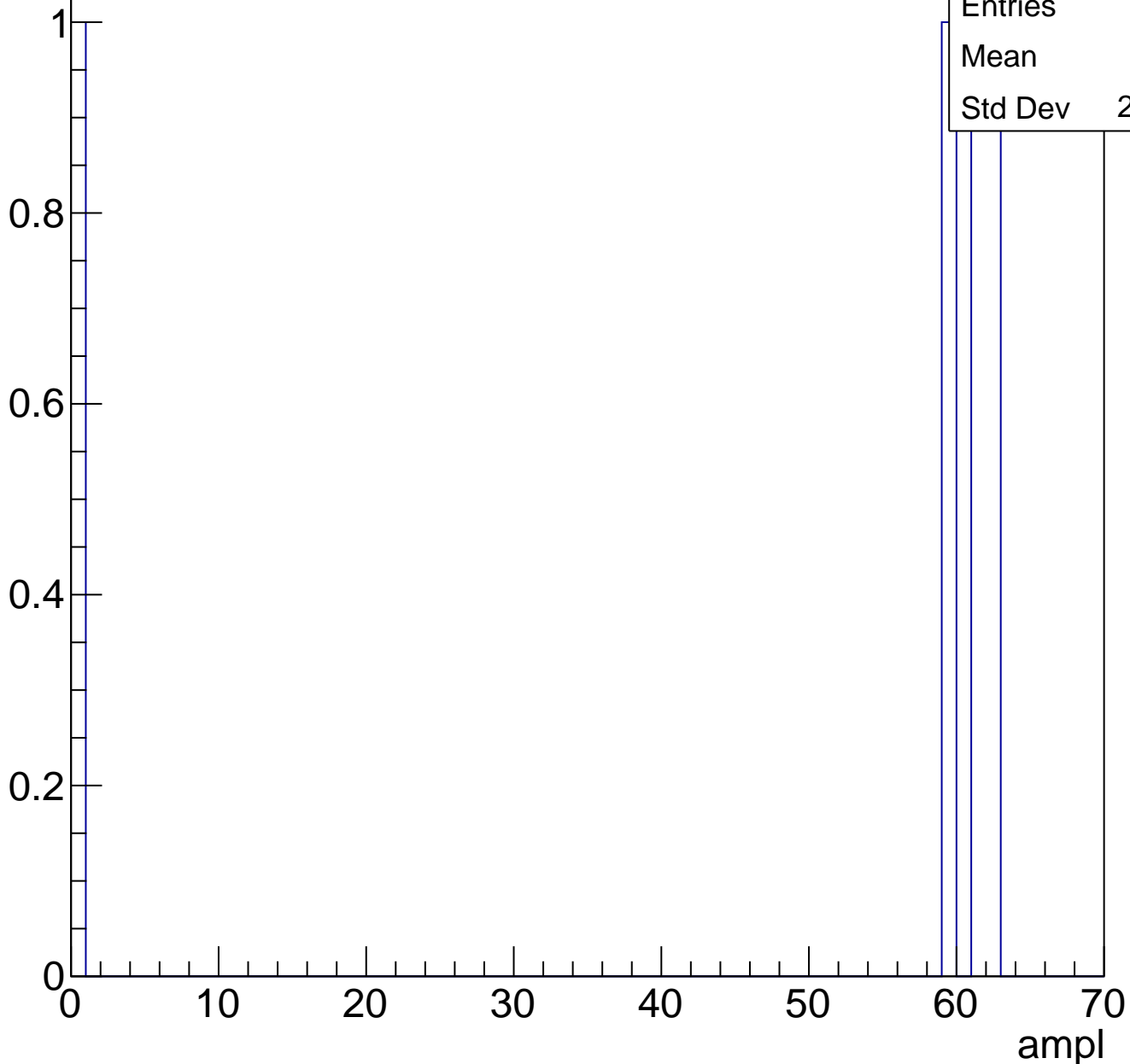




# B1L000S, U2-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch95, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	27.83
Std Dev	3.693

**Gaus mean : 28.0570**

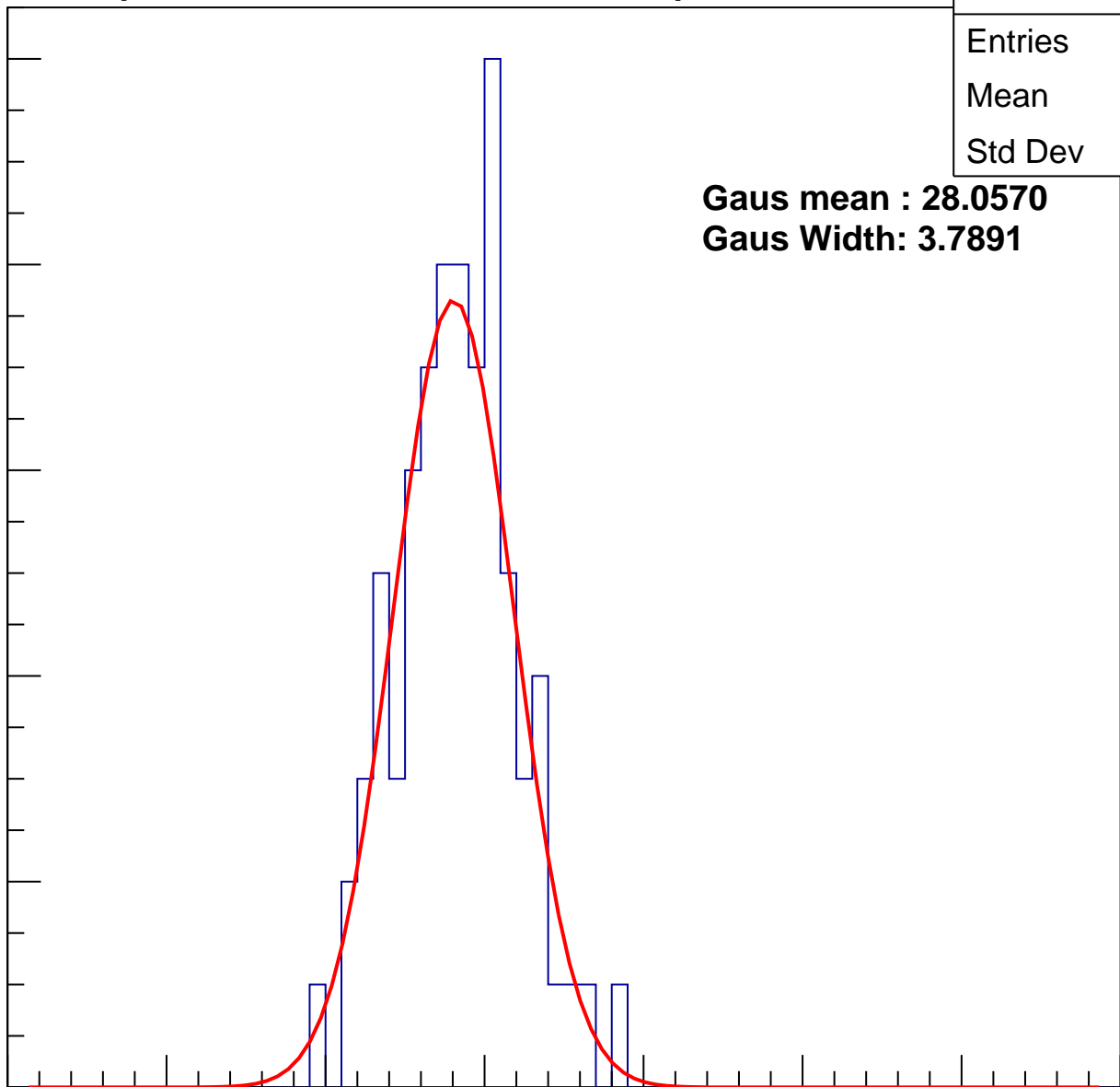
**Gaus Width: 3.7891**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch95, adc1

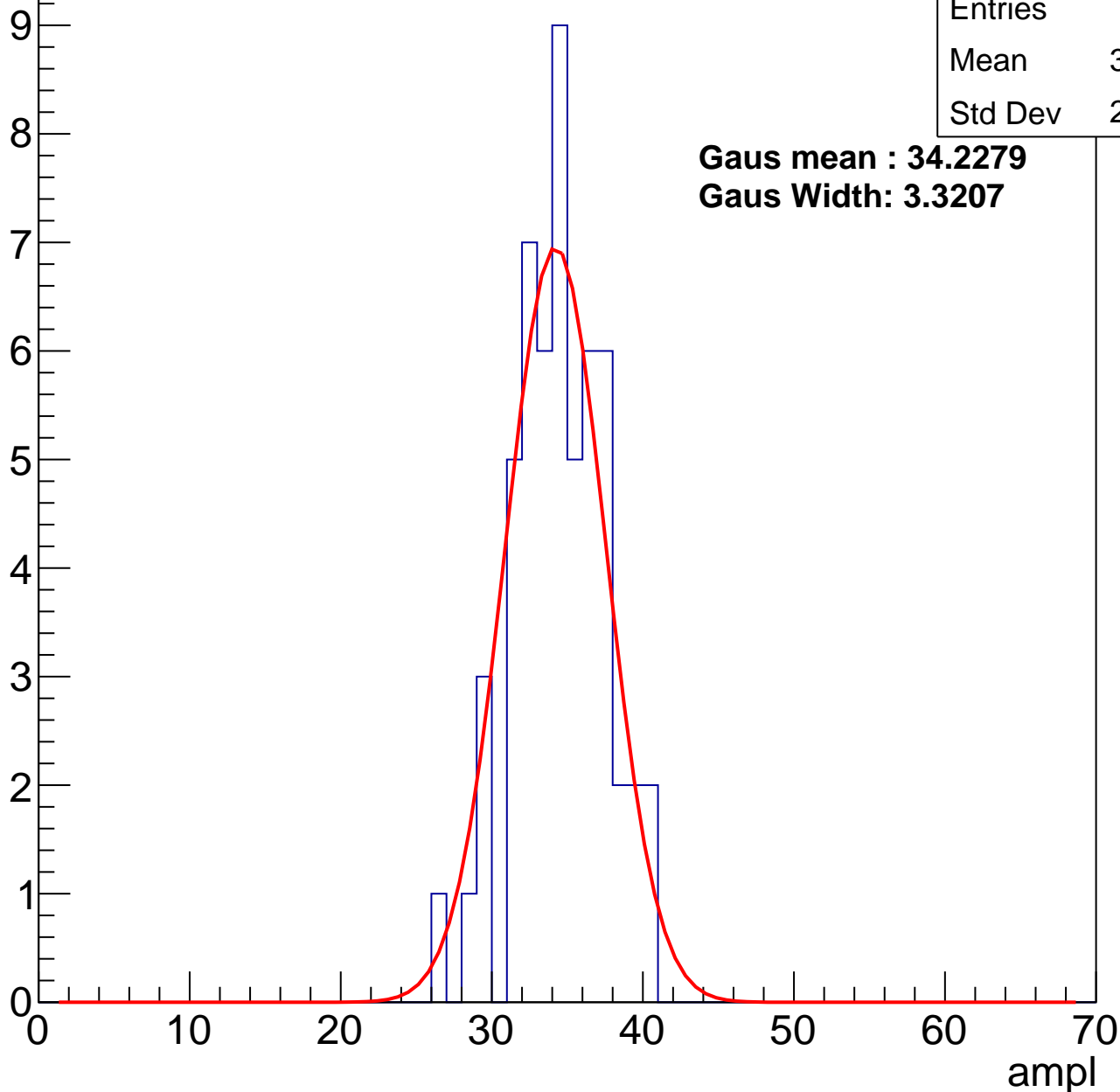
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34.02
Std Dev	2.988

**Gaus mean : 34.2279**

**Gaus Width: 3.3207**



# B1L000S, U2-ch95, adc2

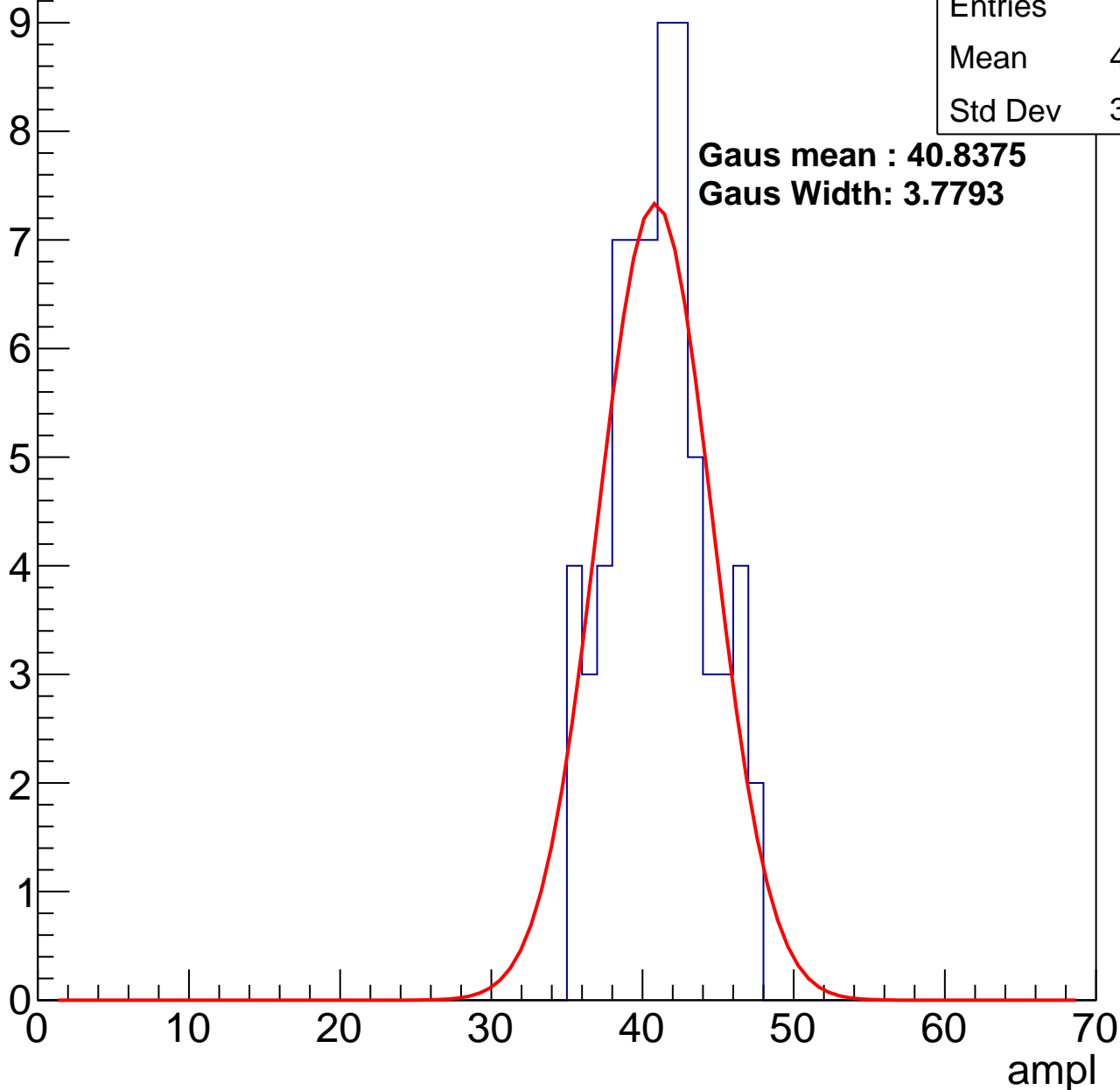
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	40.63
Std Dev	3.109

**Gaus mean : 40.8375**

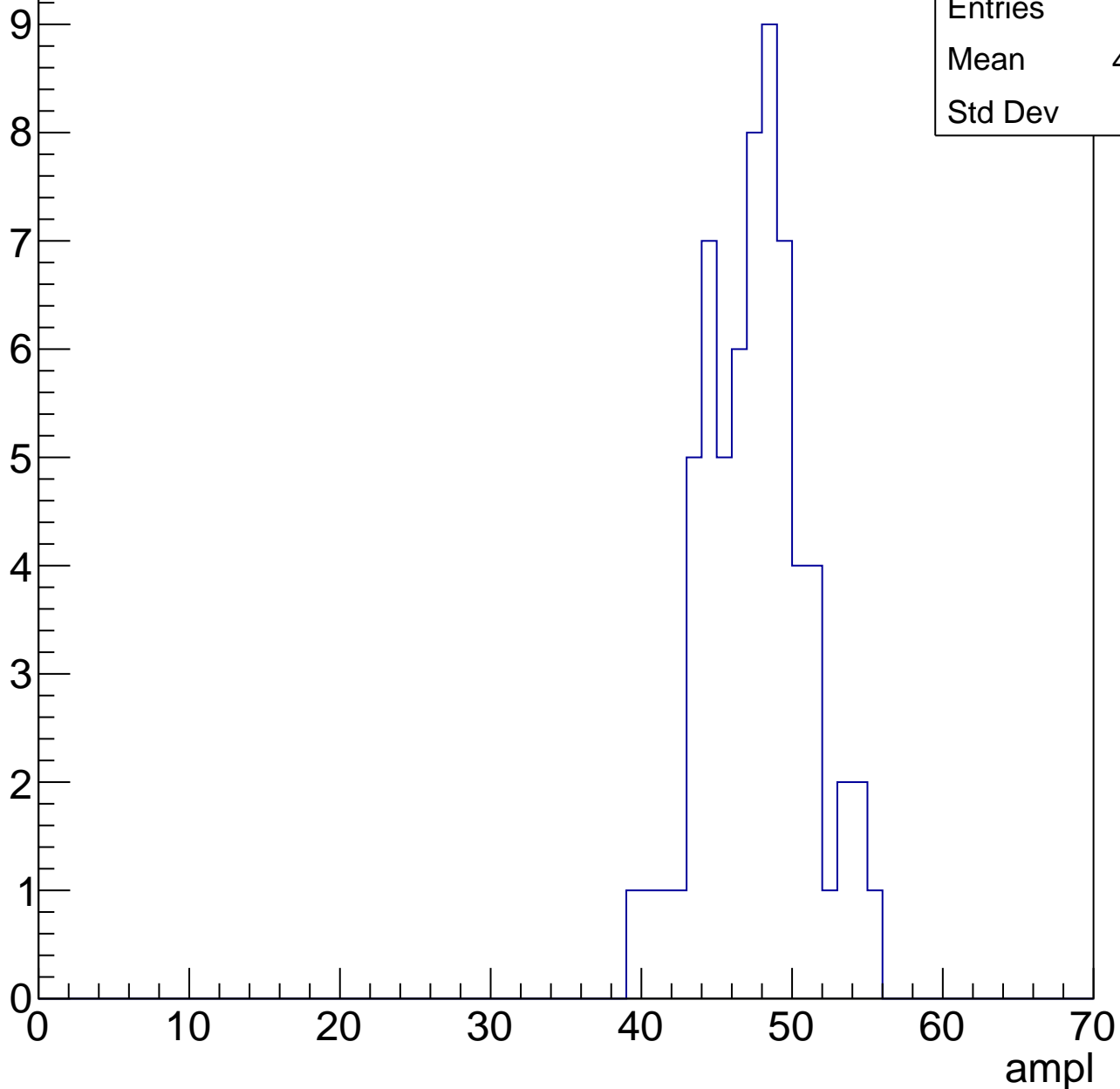
**Gaus Width: 3.7793**



# B1L000S, U2-ch95, adc3

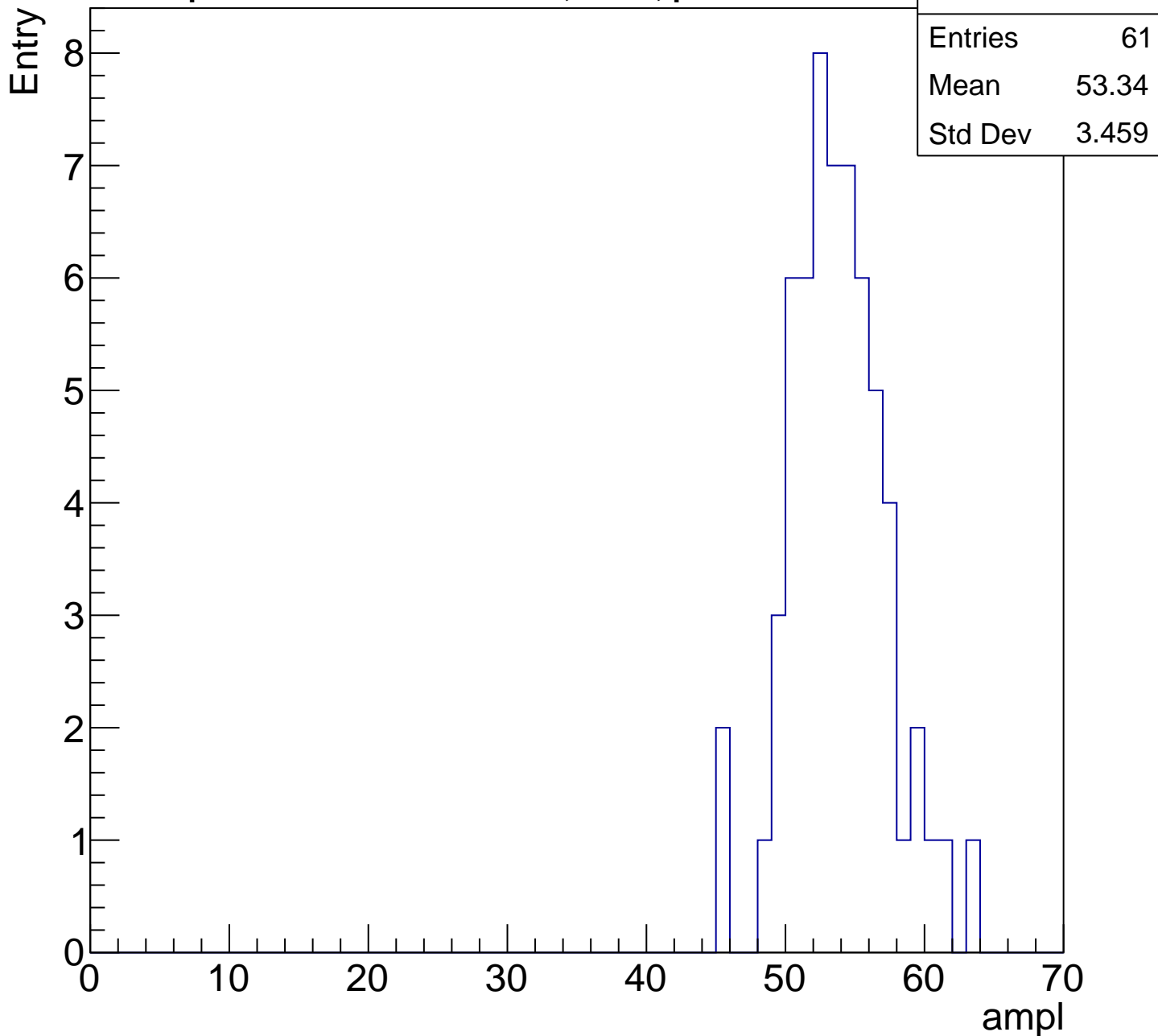
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch95, adc4

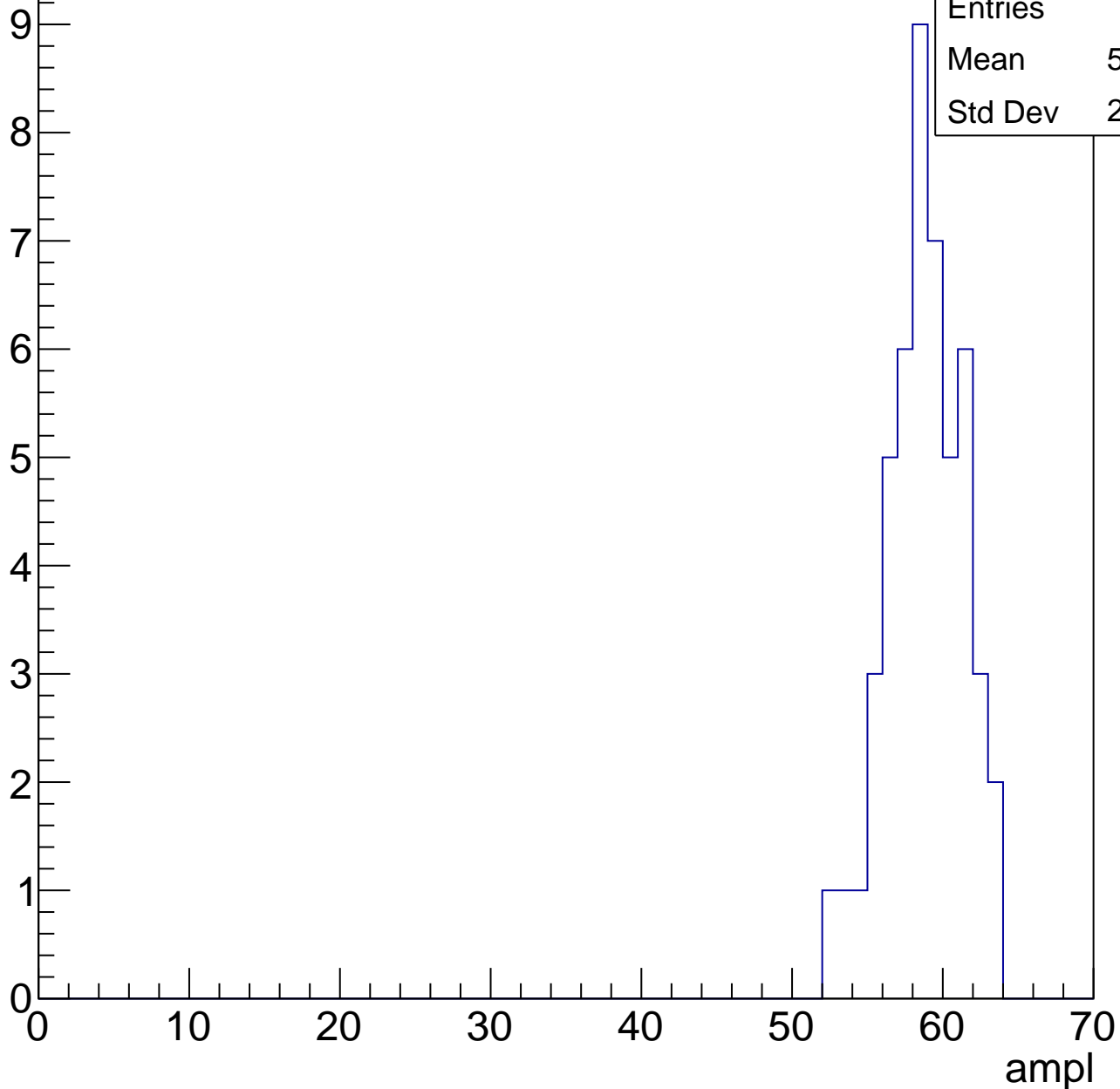
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U2-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

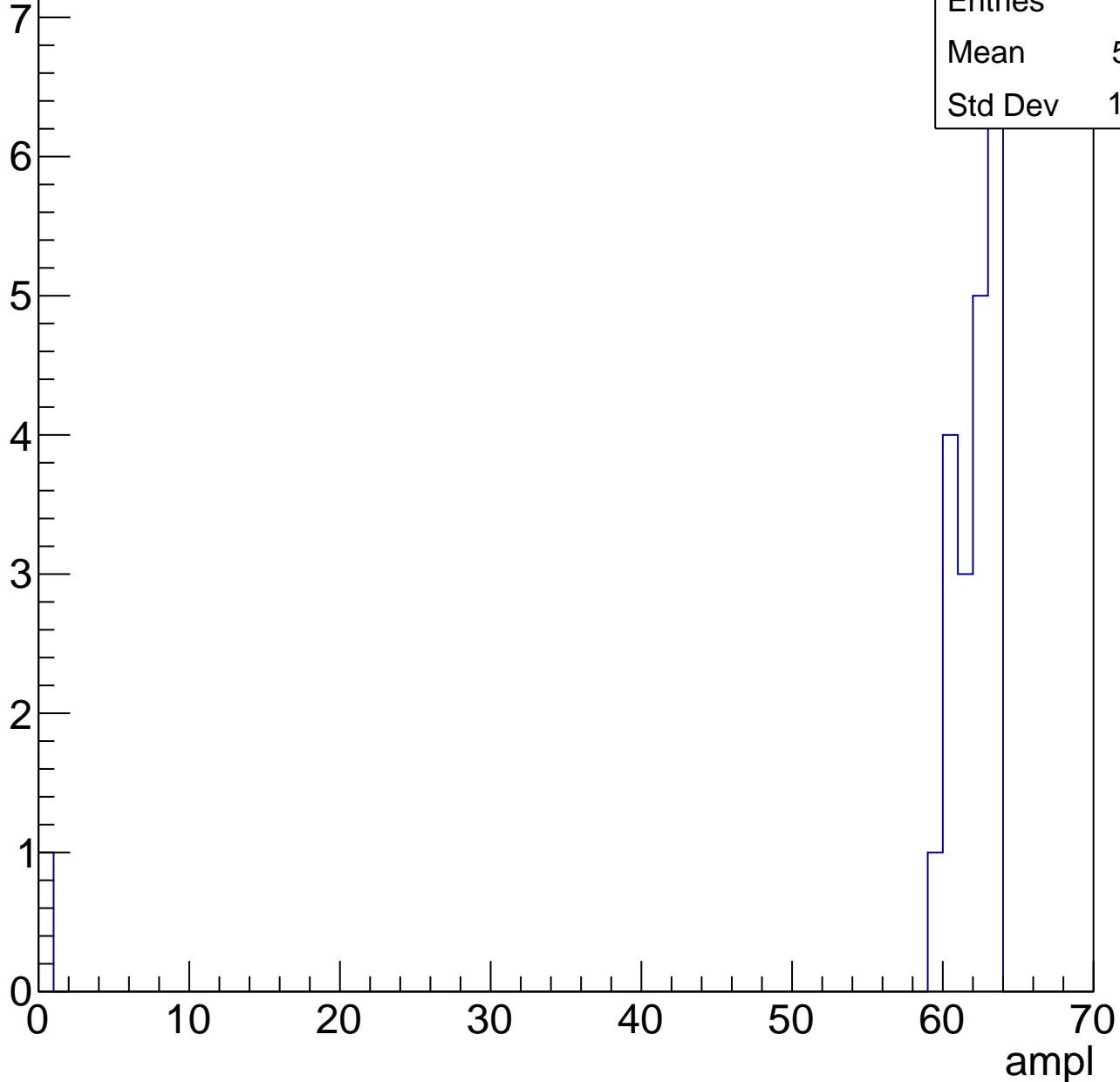


# B1L000S, U2-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.71
Std Dev	13.19

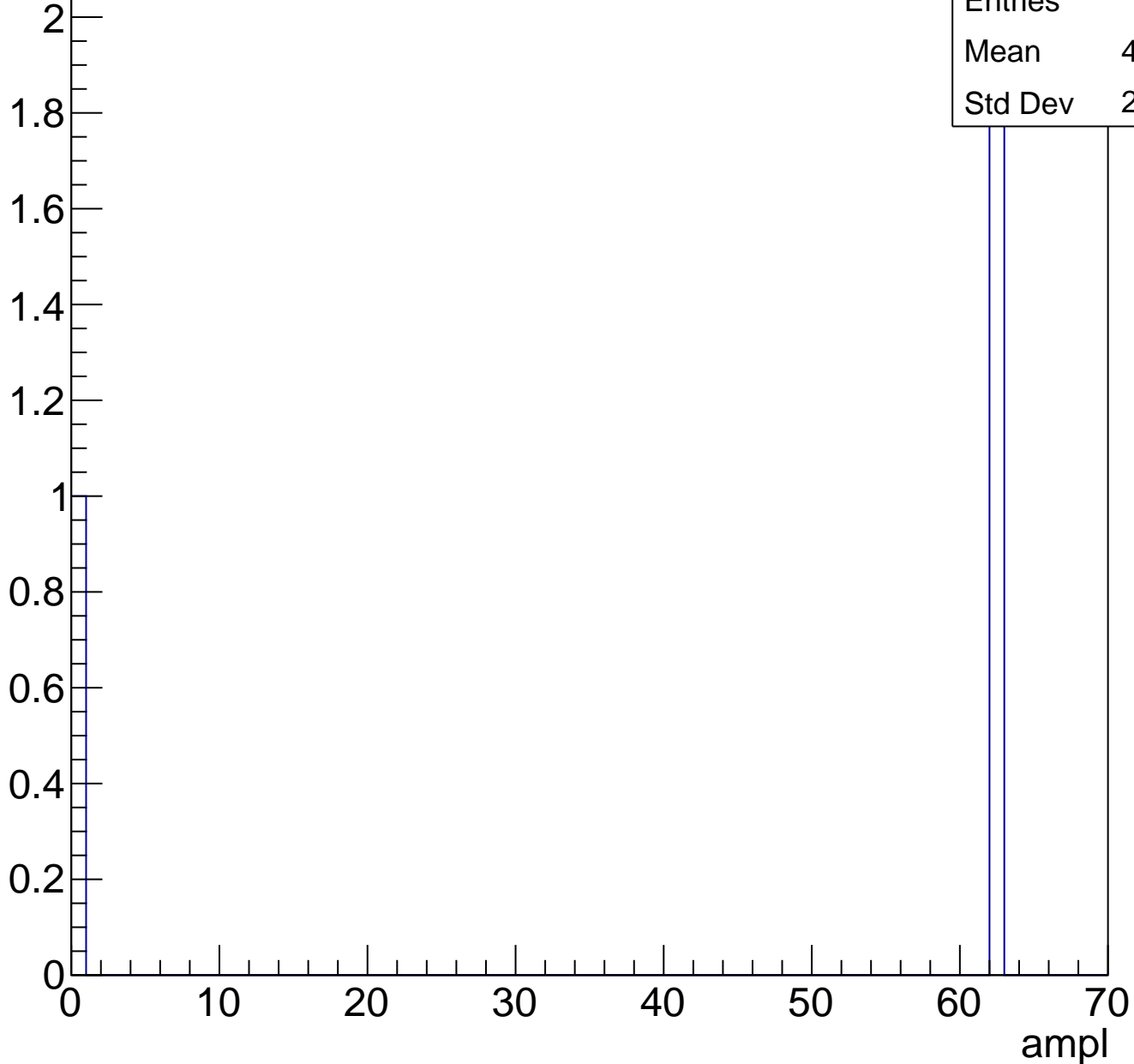




# B1L000S, U2-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch96, adc0

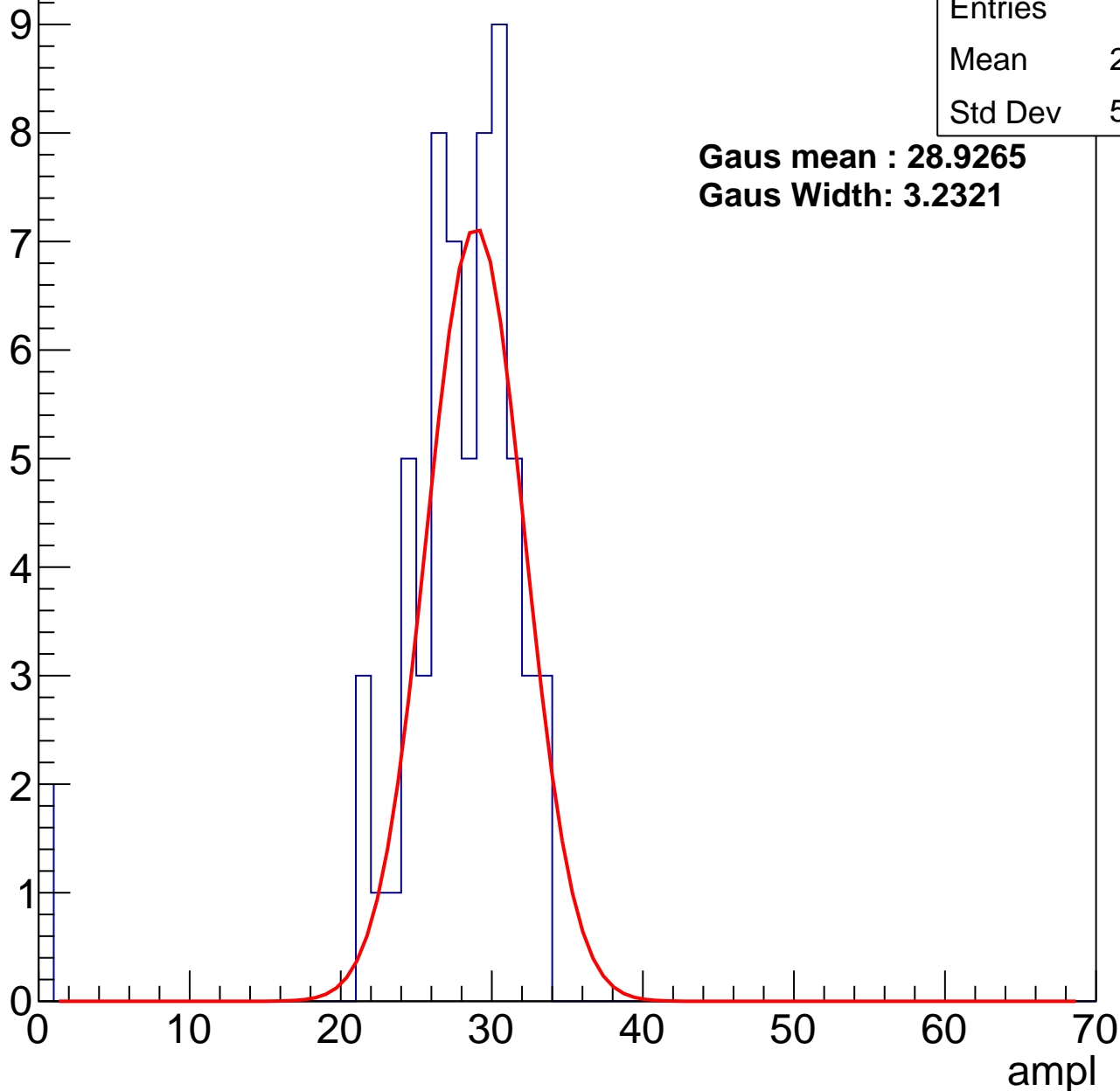
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	26.86
Std Dev	5.707

**Gaus mean : 28.9265**

**Gaus Width: 3.2321**



# B1L000S, U2-ch96, adc1

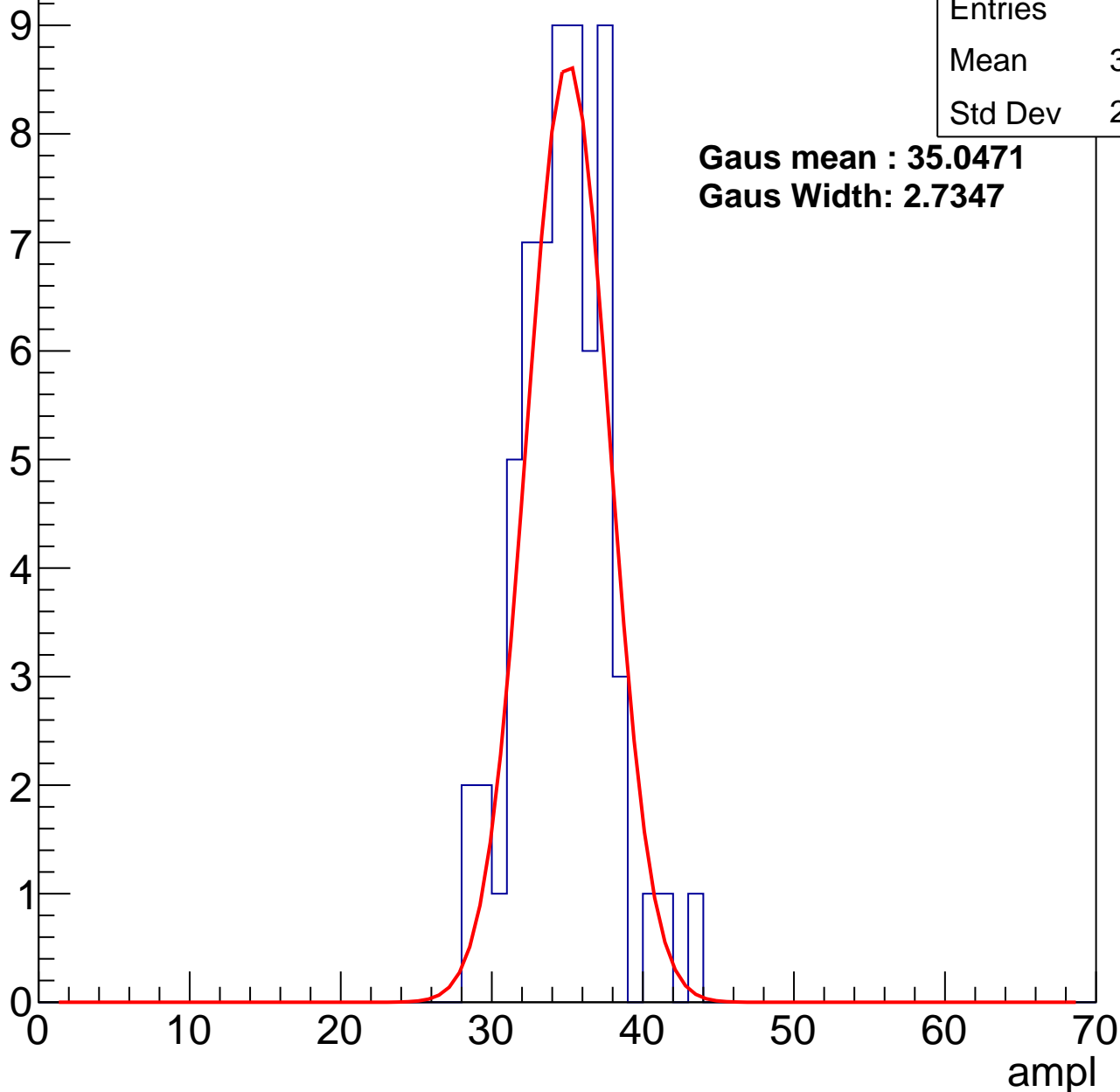
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	34.32
Std Dev	2.927

**Gaus mean : 35.0471**

**Gaus Width: 2.7347**



# B1L000S, U2-ch96, adc2

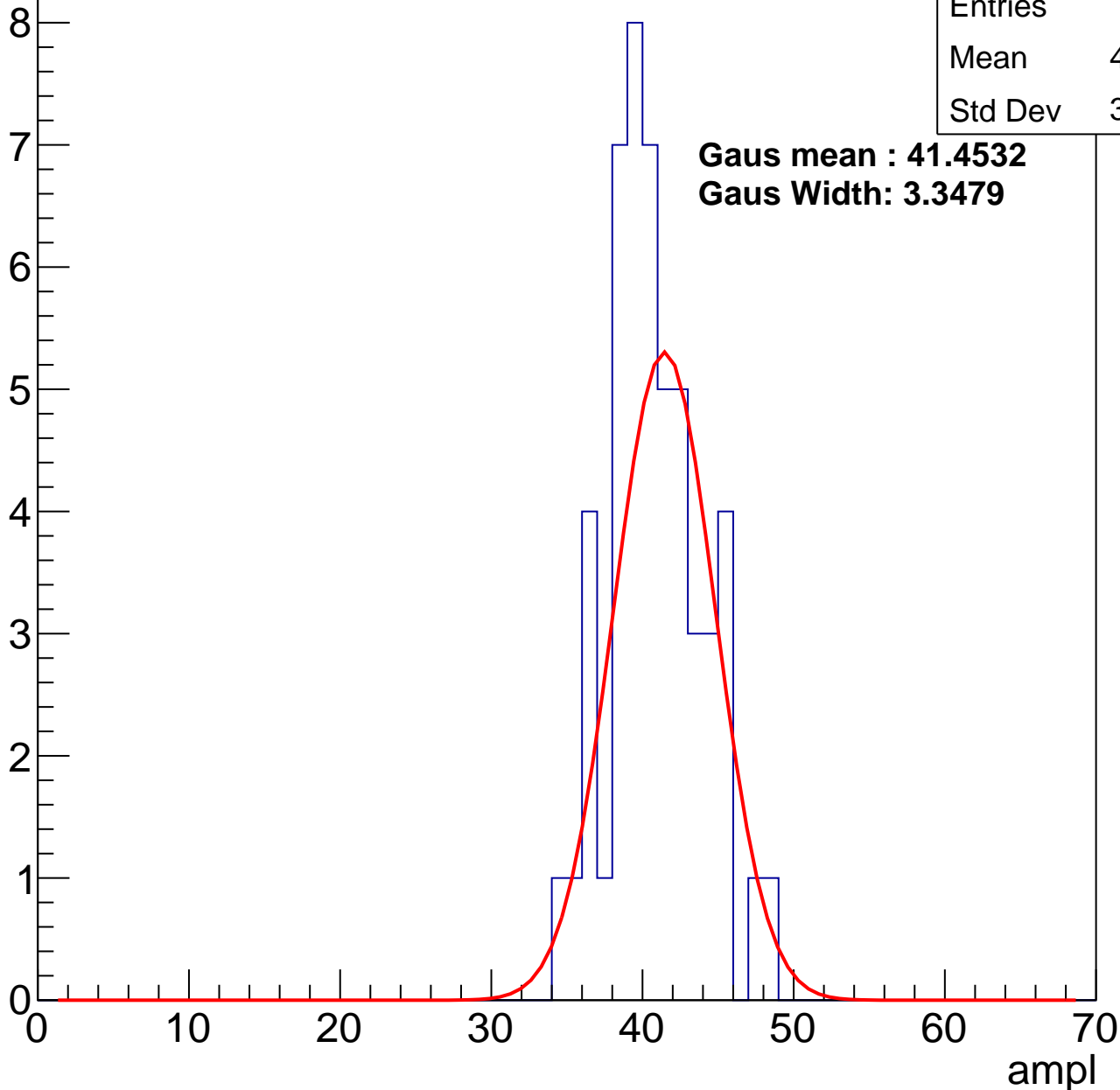
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	40.37
Std Dev	3.055

**Gaus mean : 41.4532**

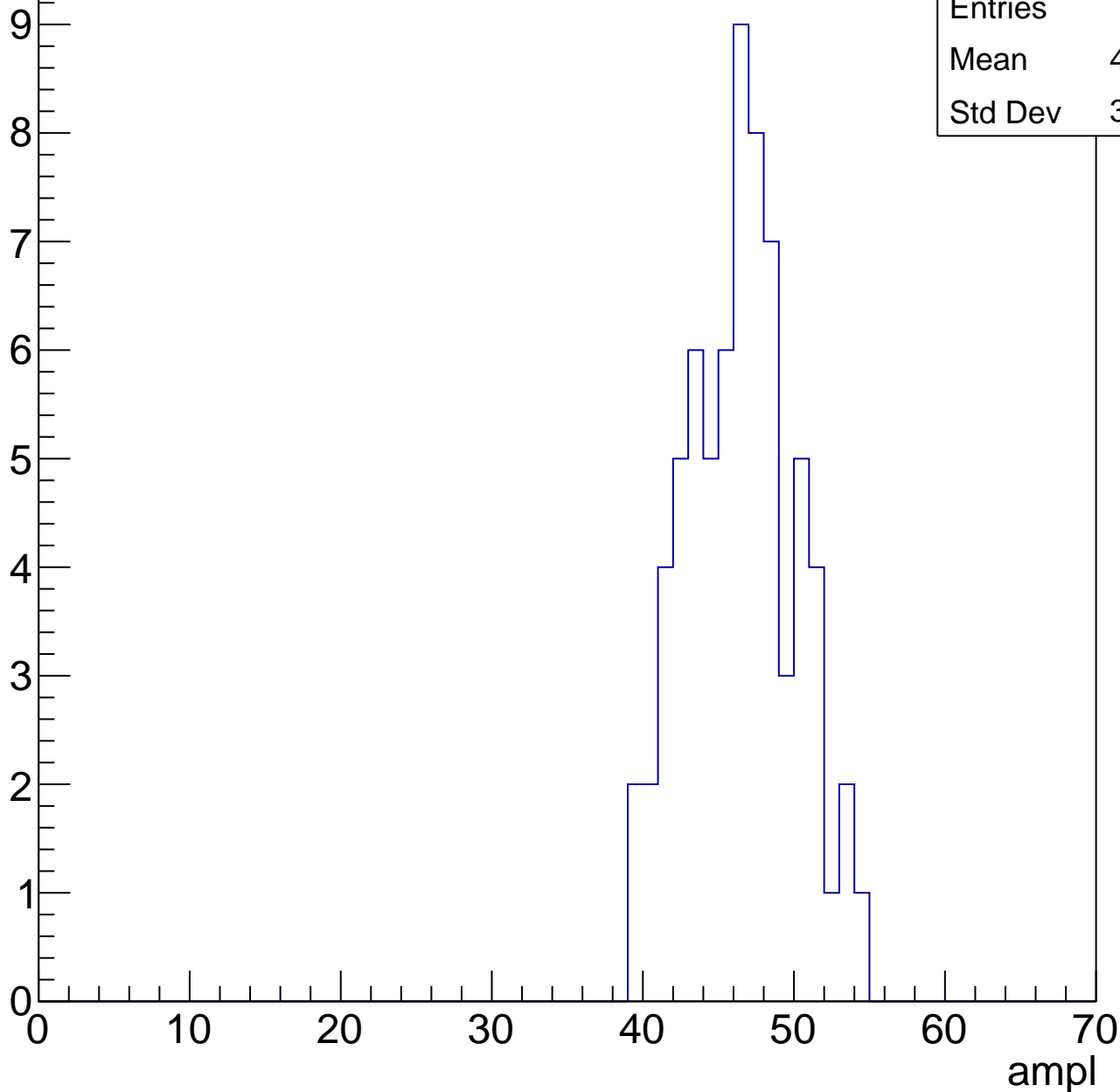
**Gaus Width: 3.3479**



# B1L000S, U2-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

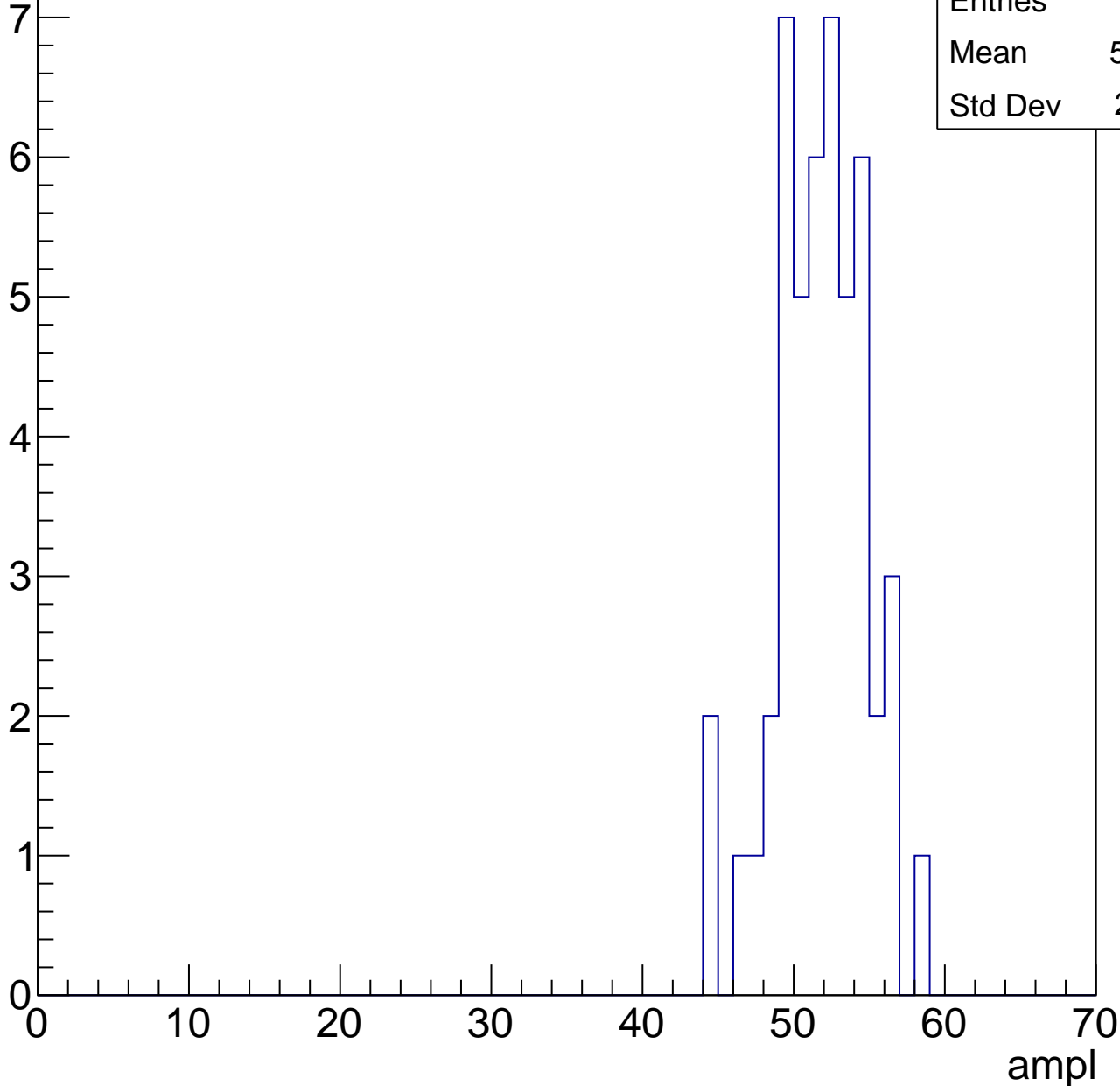


# B1L000S, U2-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	51.35
Std Dev	2.961



# B1L000S, U2-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

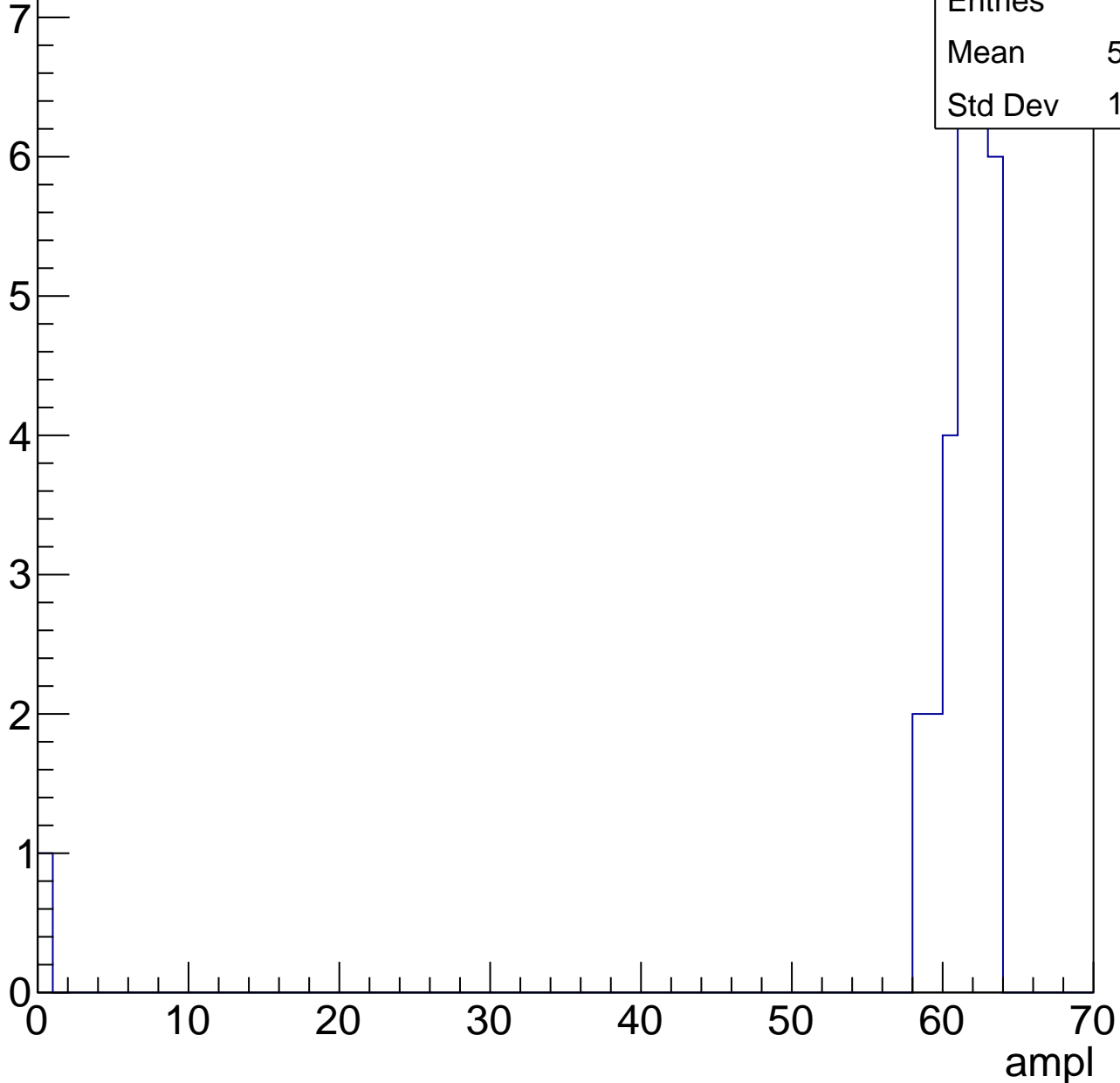
Entries	63
Mean	57.57
Std Dev	2.816

# B1L000S, U2-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	59.07
Std Dev	11.26

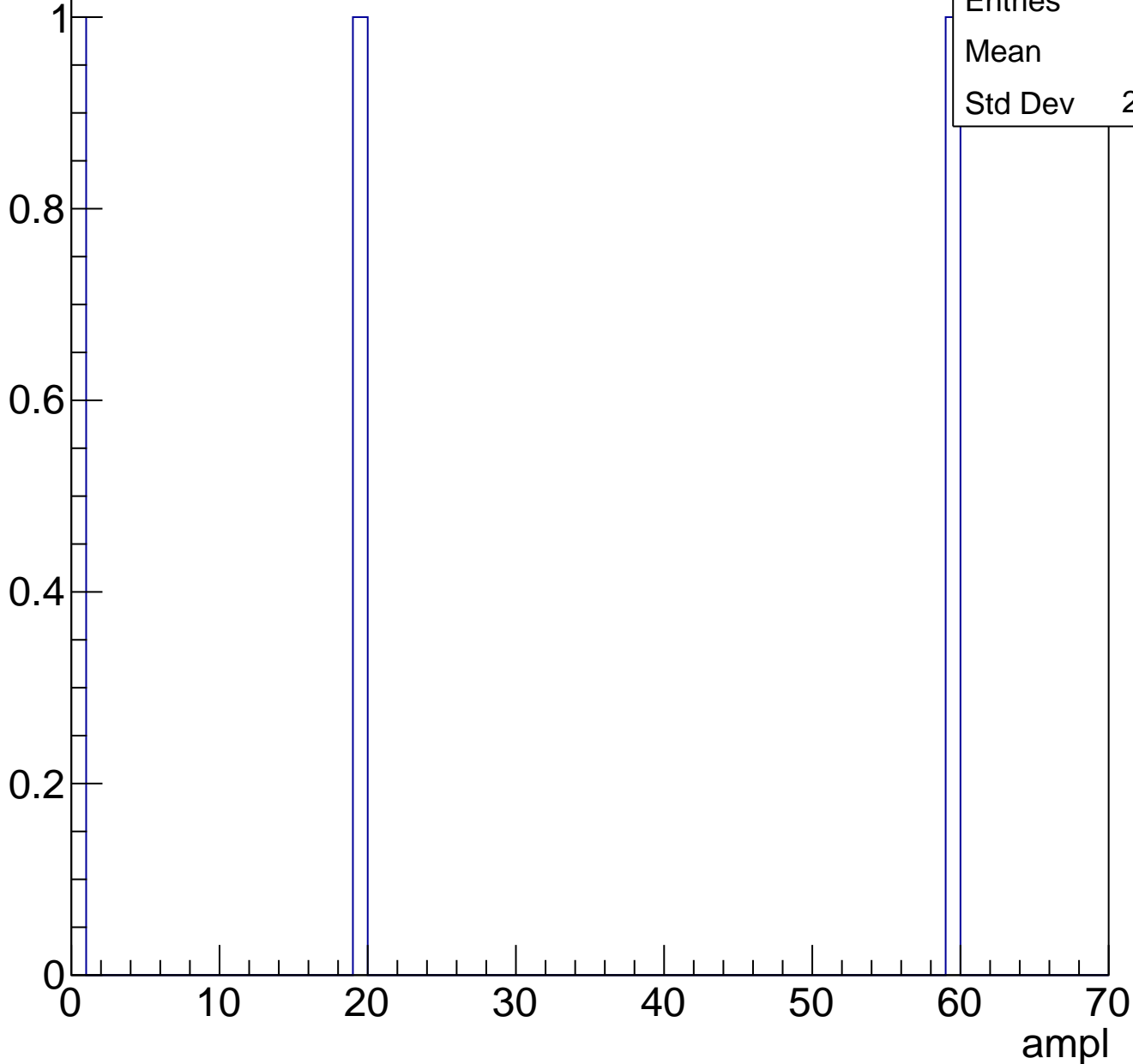




# B1L000S, U2-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch97, adc0

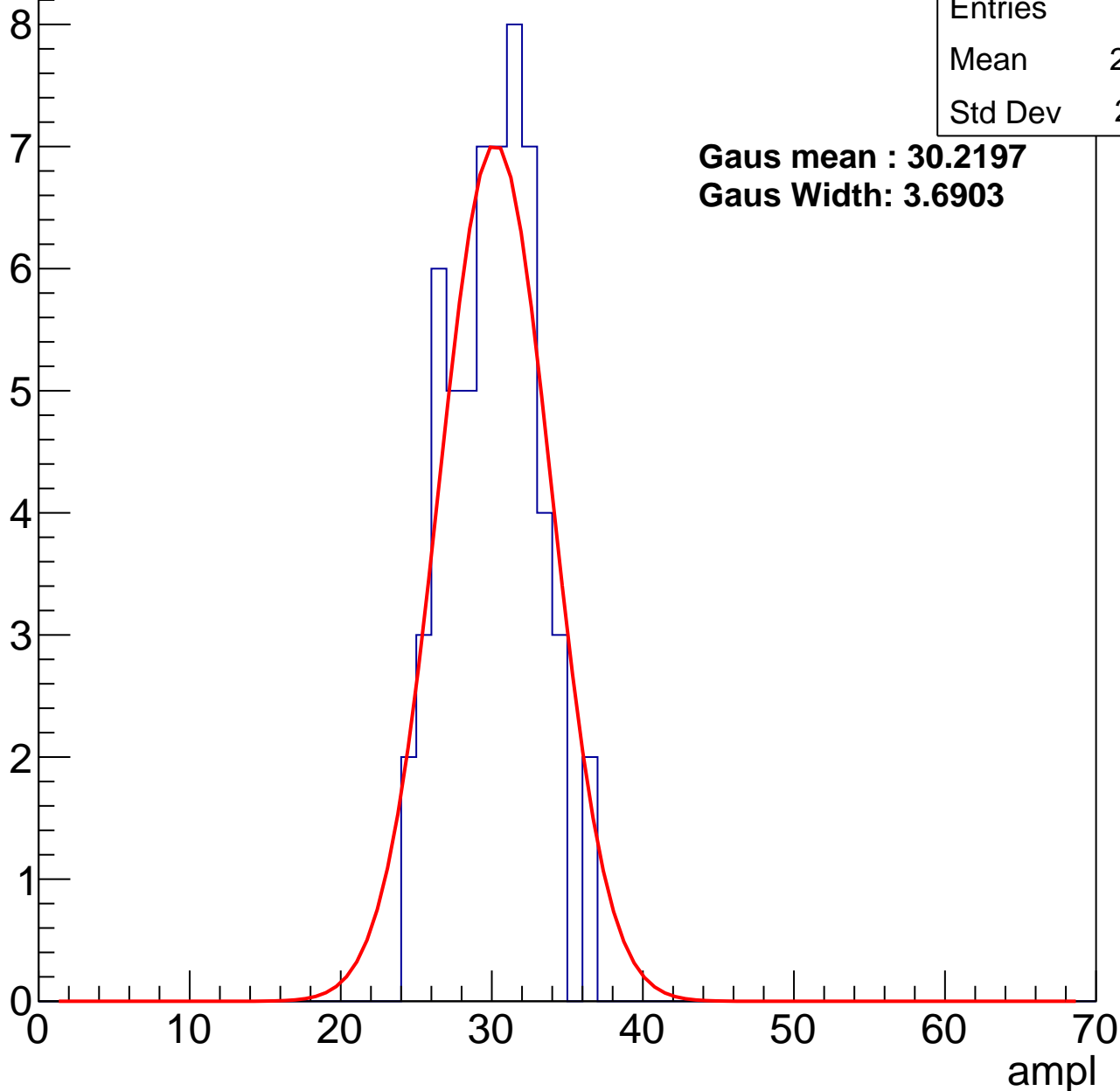
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	29.58
Std Dev	2.901

**Gaus mean : 30.2197**

**Gaus Width: 3.6903**



# B1L000S, U2-ch97, adc1

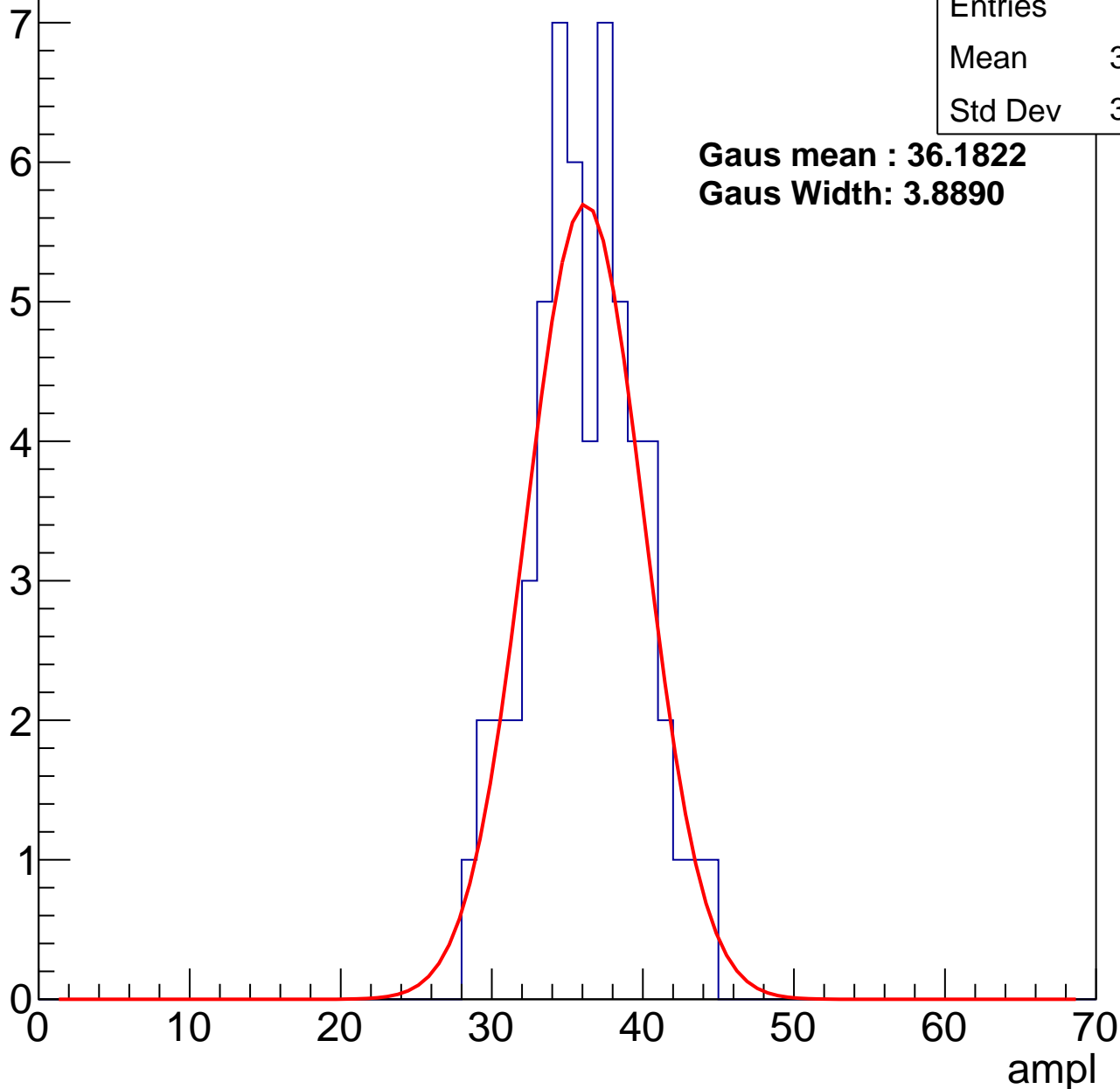
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.74
Std Dev	3.586

**Gaus mean : 36.1822**

**Gaus Width: 3.8890**



# B1L000S, U2-ch97, adc2

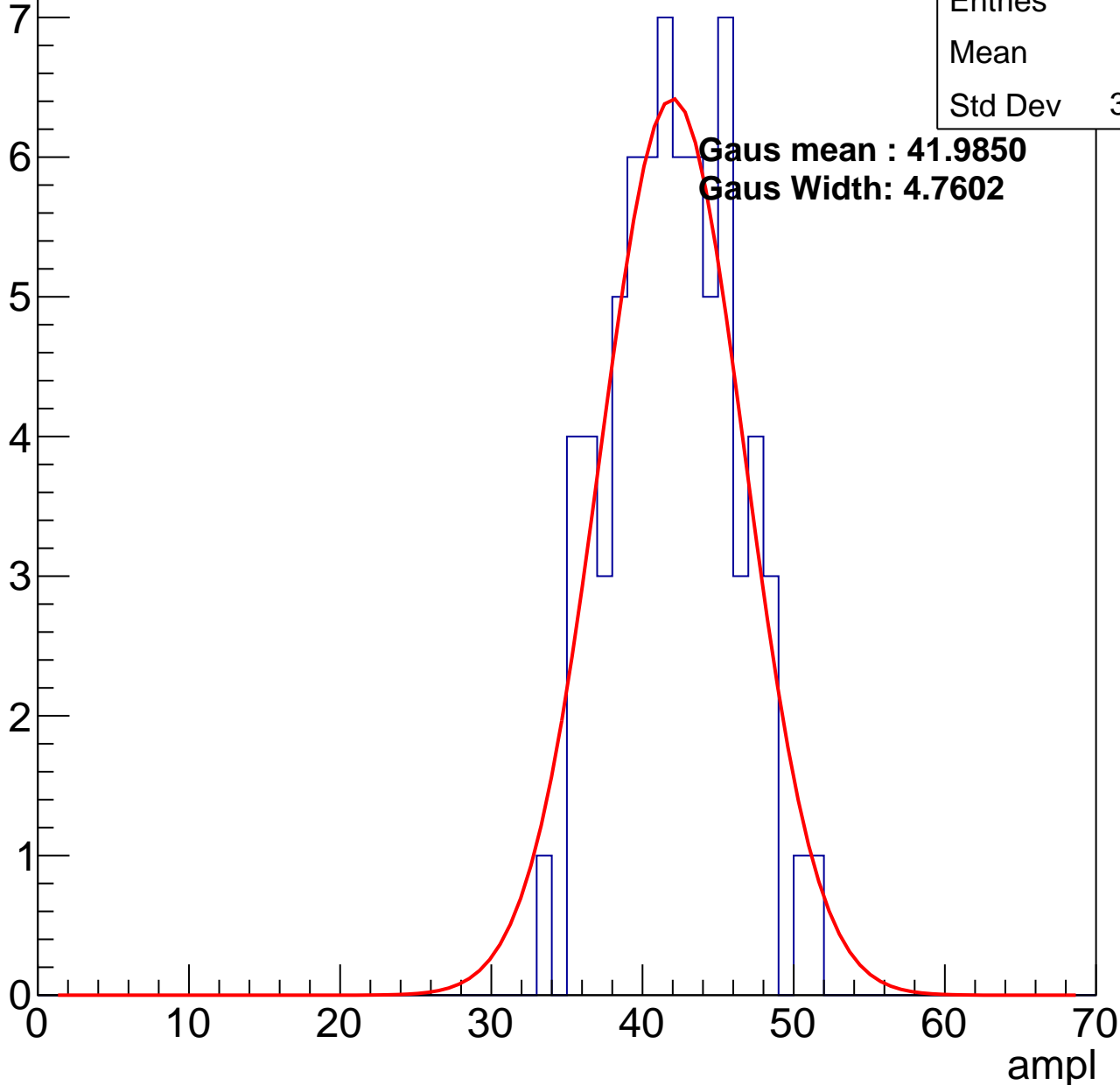
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	41.6
Std Dev	3.978

**Gaus mean : 41.9850**

**Gaus Width: 4.7602**

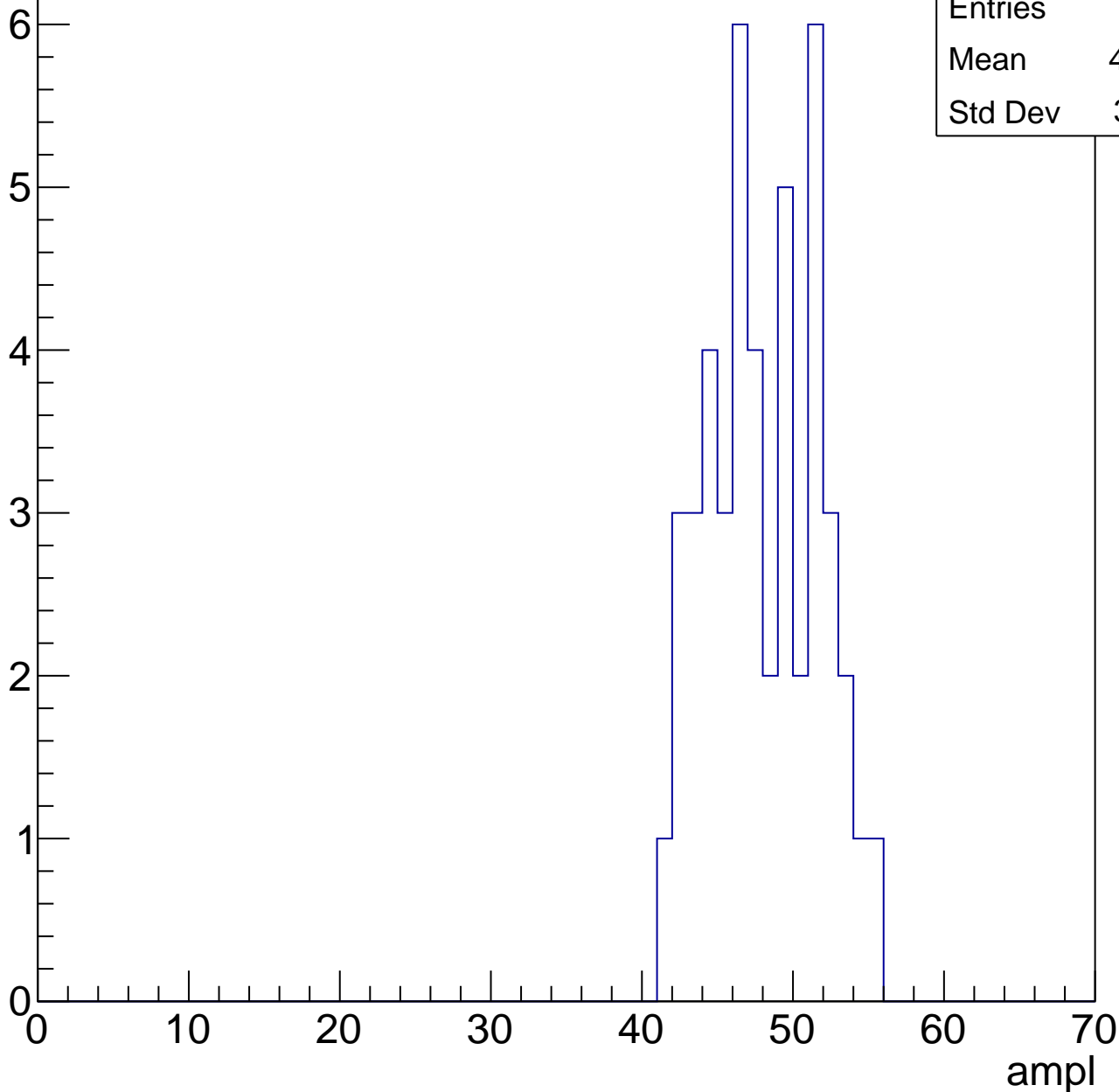


# B1L000S, U2-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	47.59
Std Dev	3.591

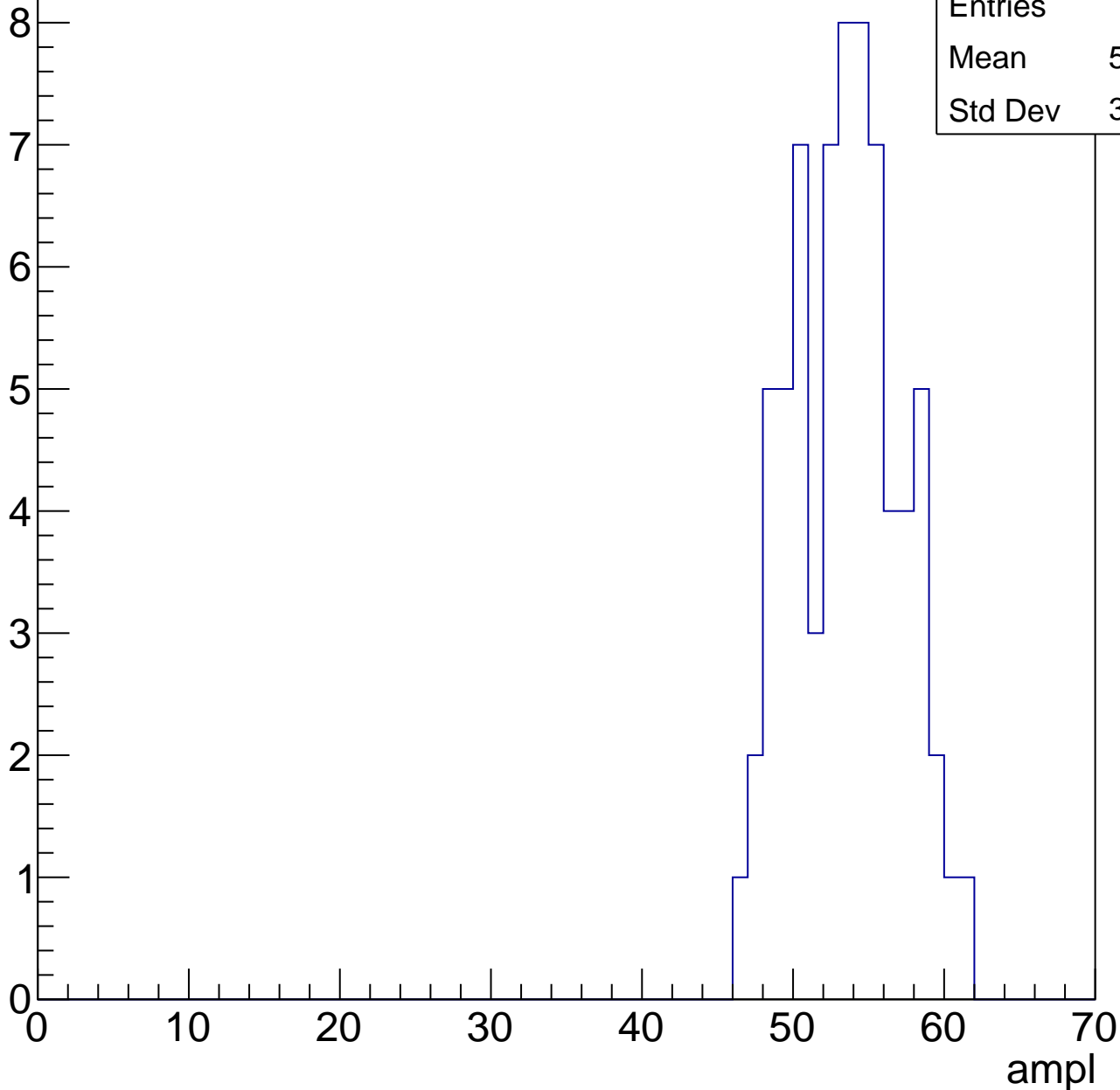


# B1L000S, U2-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

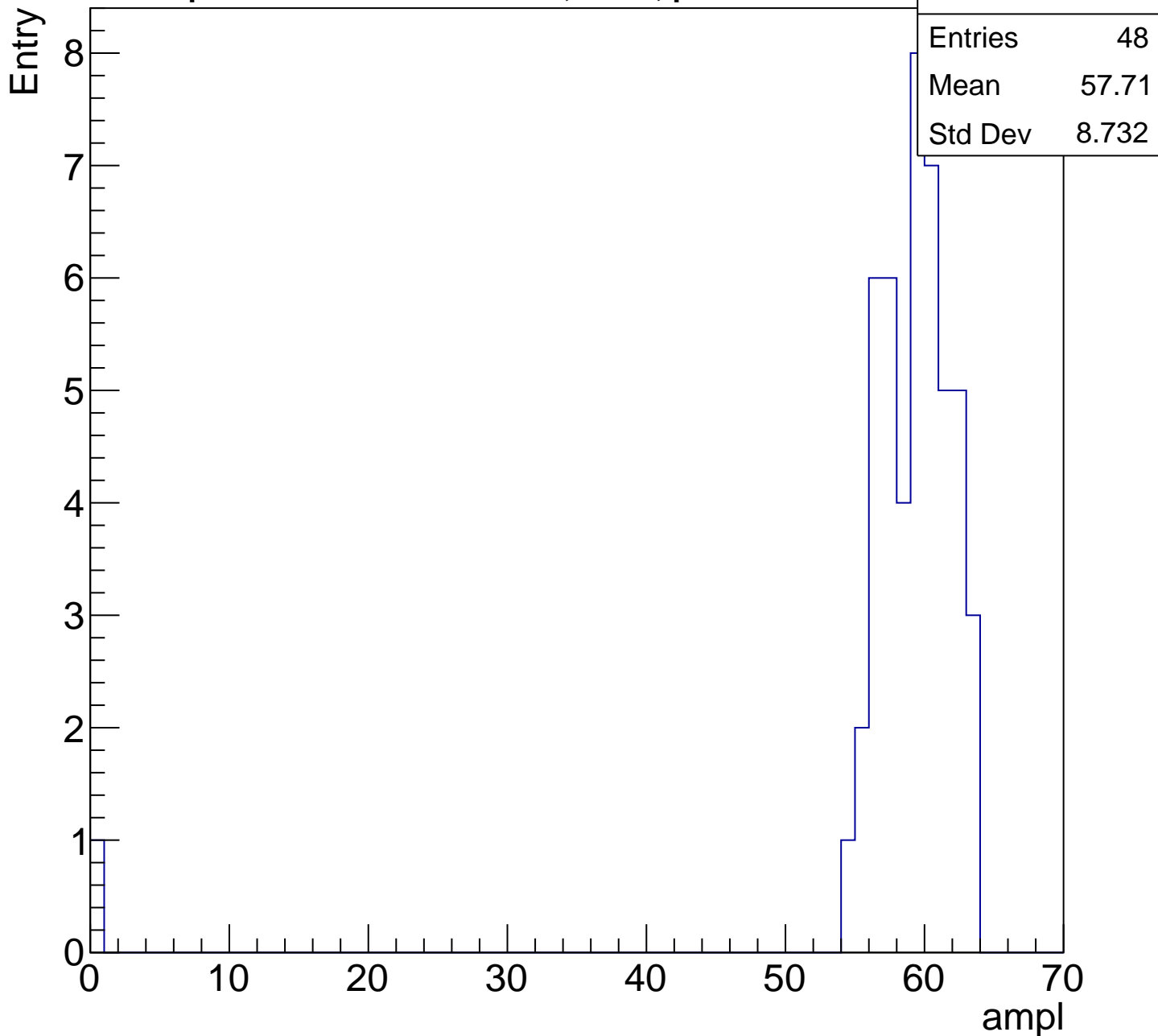
Entry

Entries	70
Mean	53.06
Std Dev	3.492



# B1L000S, U2-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

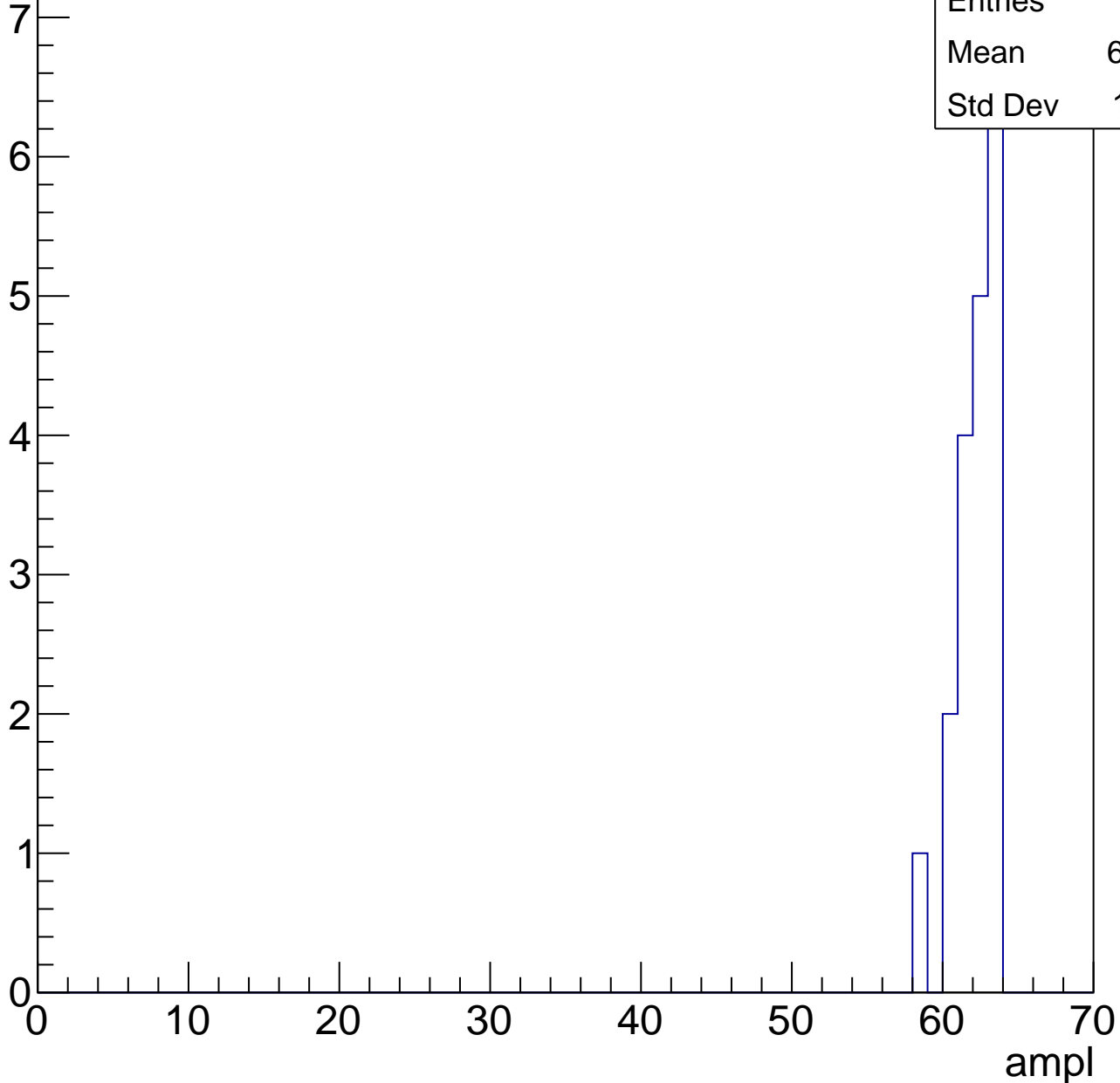


# B1L000S, U2-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	61.74
Std Dev	1.331





# B1L000S, U2-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch98, adc0

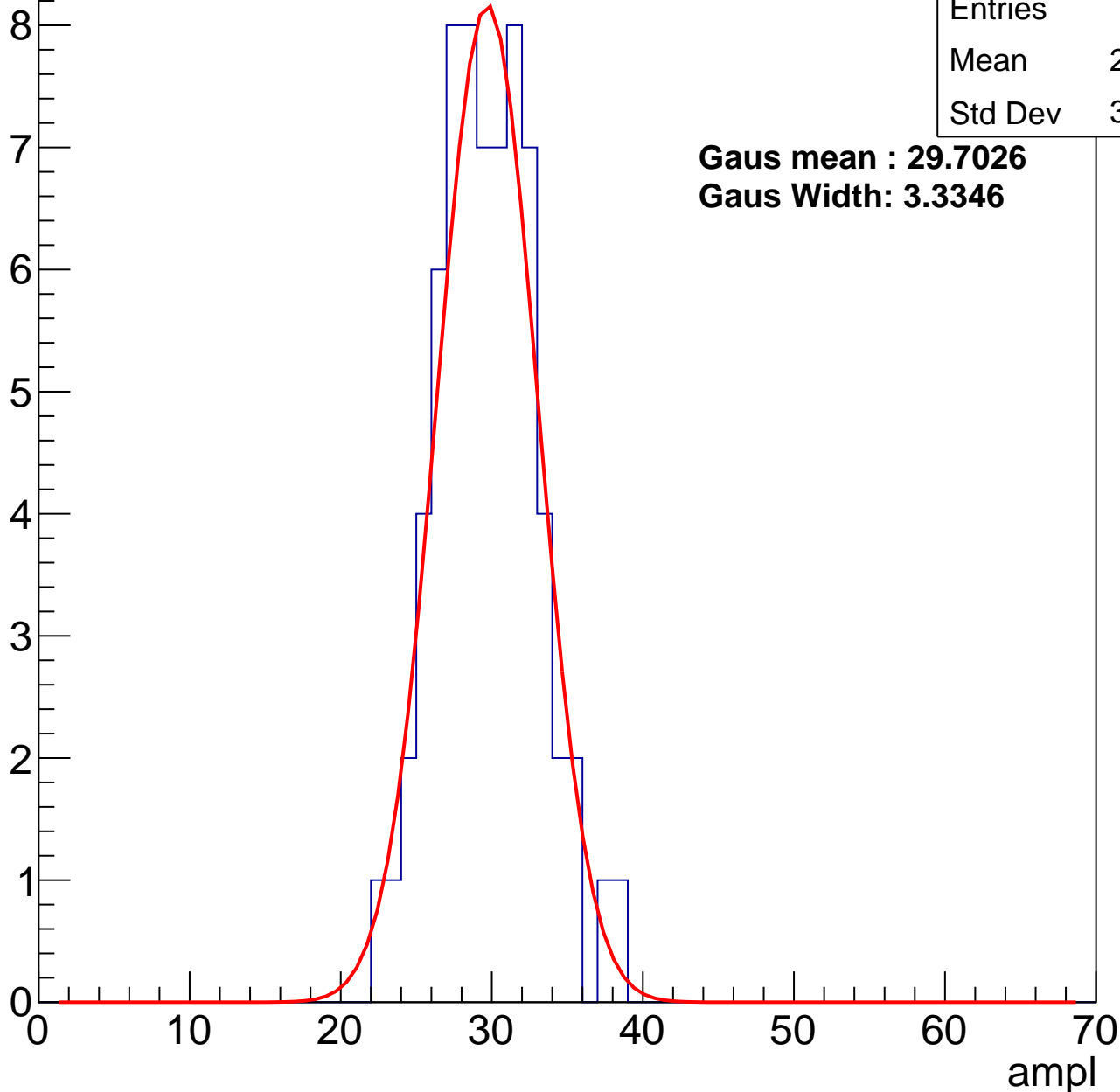
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.26
Std Dev	3.233

**Gaus mean : 29.7026**

**Gaus Width: 3.3346**



# B1L000S, U2-ch98, adc1

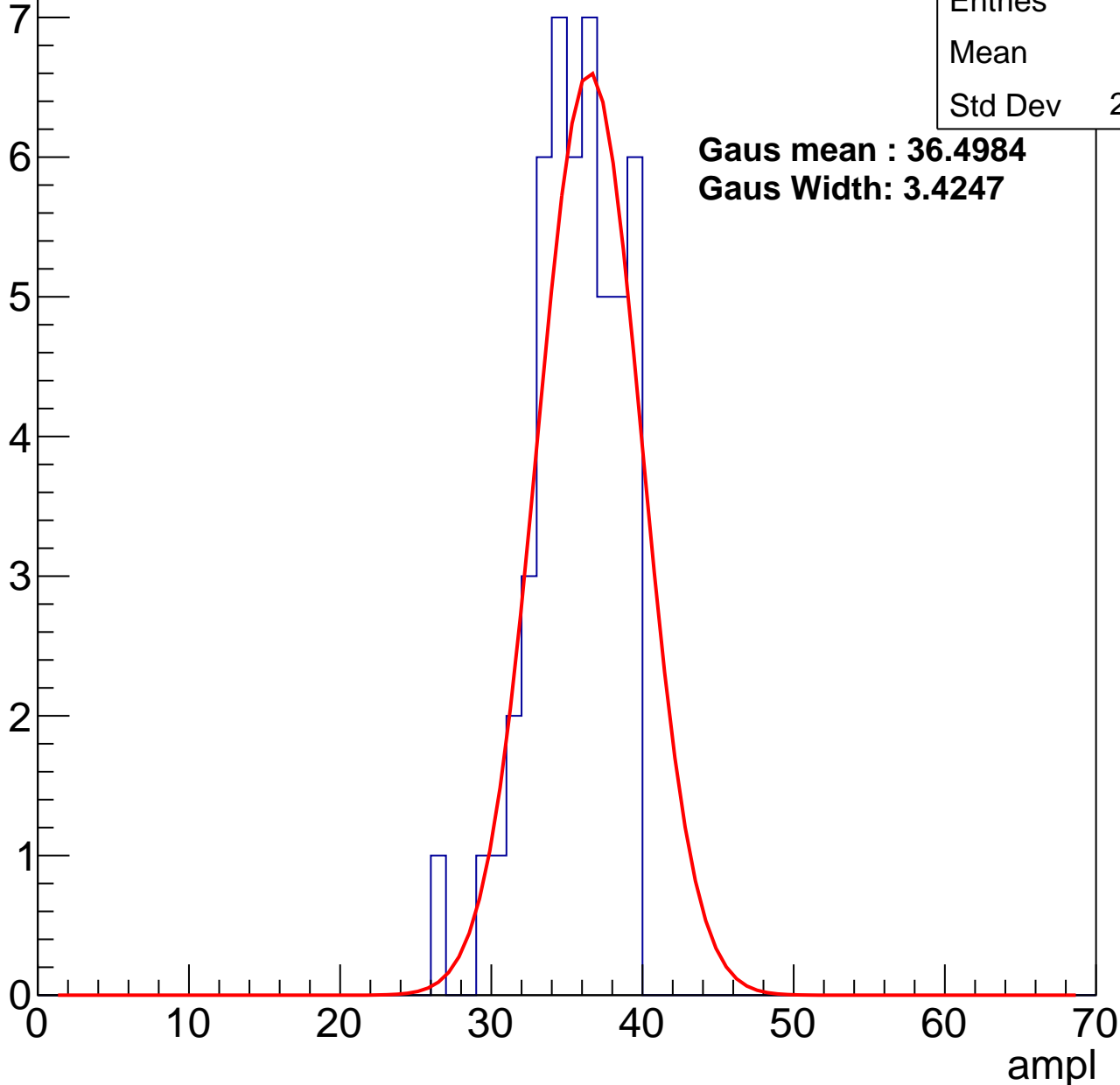
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	35
Std Dev	2.828

**Gaus mean : 36.4984**

**Gaus Width: 3.4247**



# B1L000S, U2-ch98, adc2

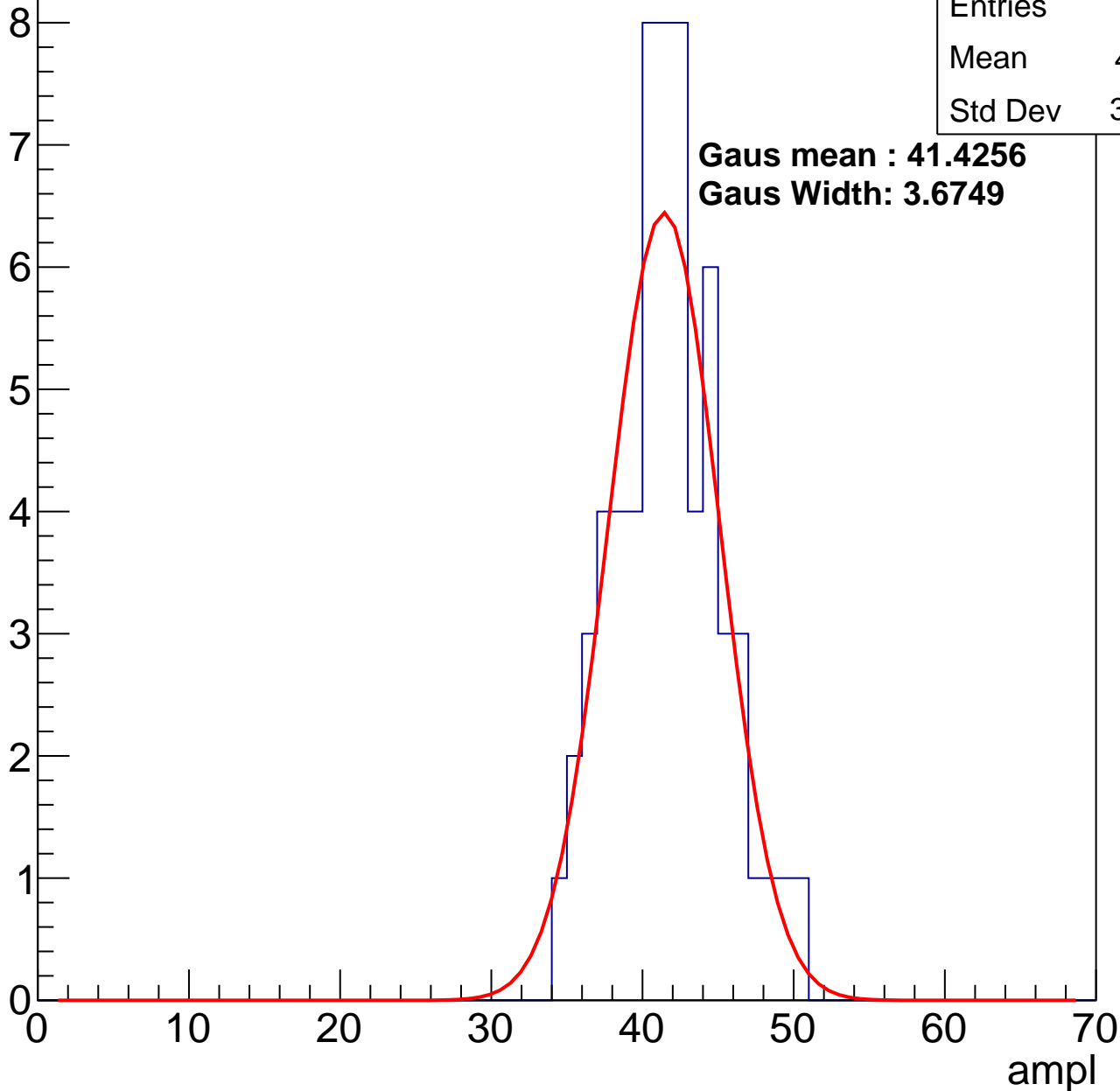
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.21
Std Dev	3.474

**Gaus mean : 41.4256**

**Gaus Width: 3.6749**



# B1L000S, U2-ch98, adc3

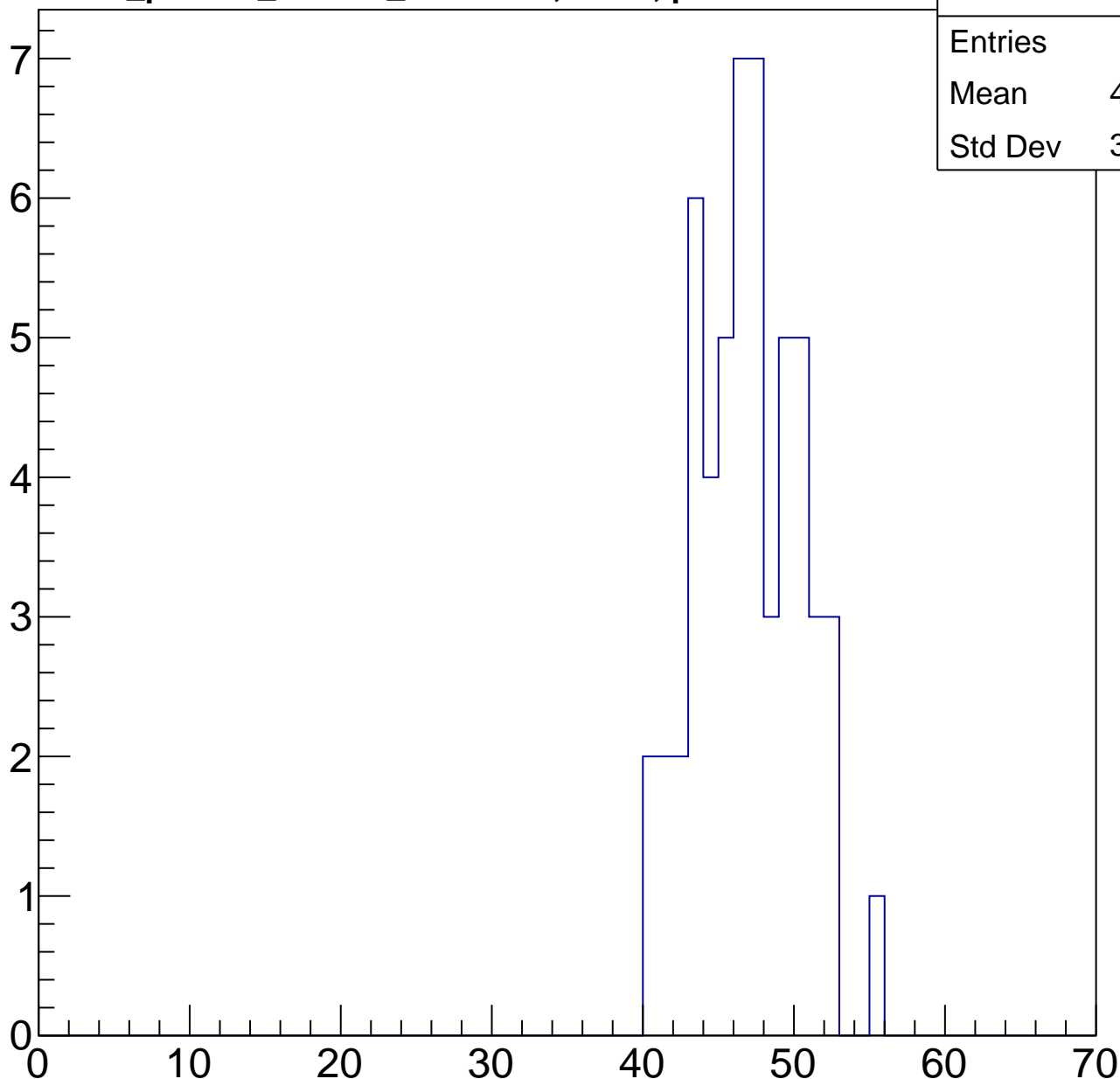
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	46.53
Std Dev	3.362

ampl

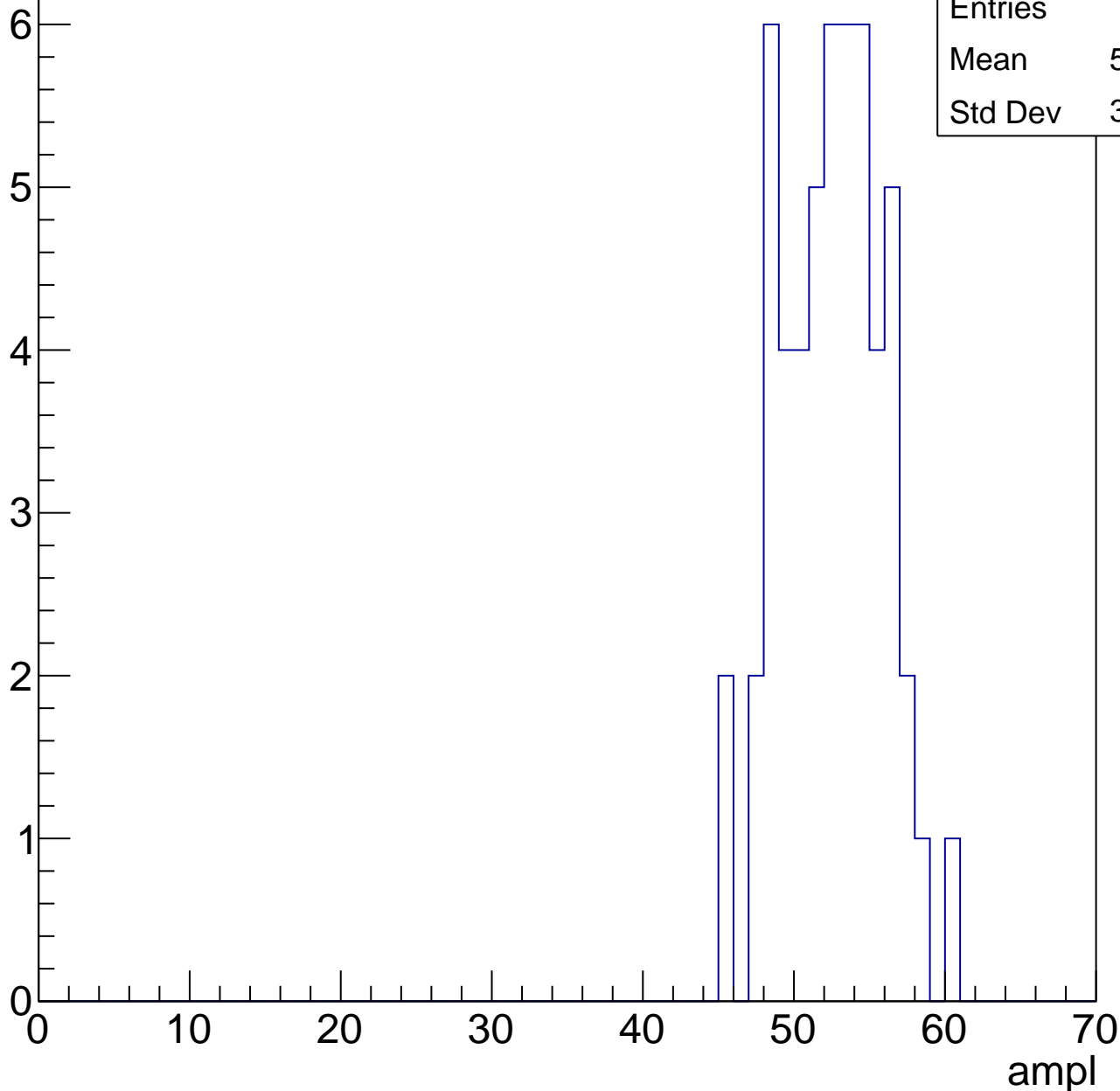


# B1L000S, U2-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	52.02
Std Dev	3.325



# B1L000S, U2-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	58.34
Std Dev	3.126

Entry

10

8

6

4

2

0

0

10

20

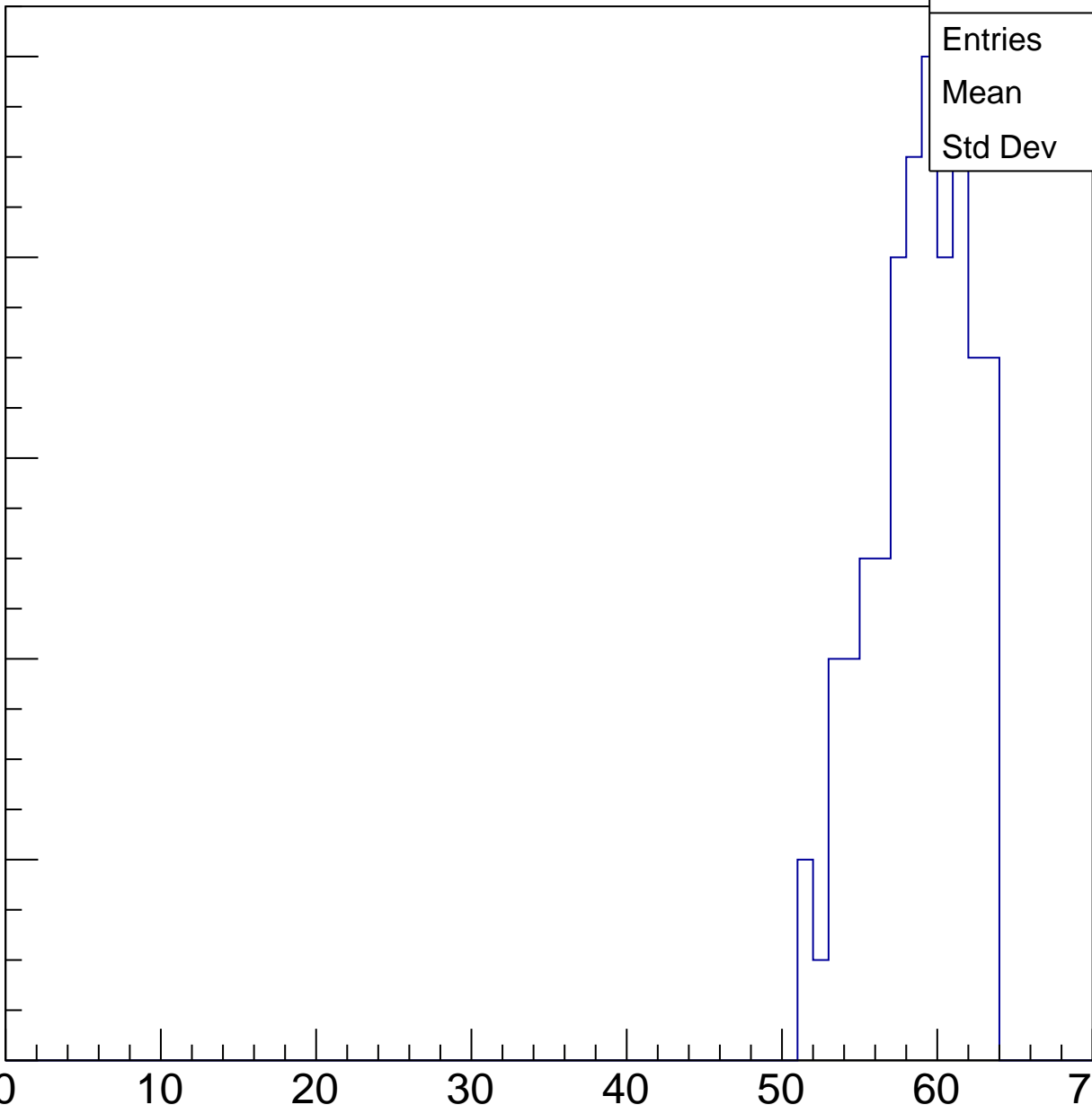
30

40

50

60

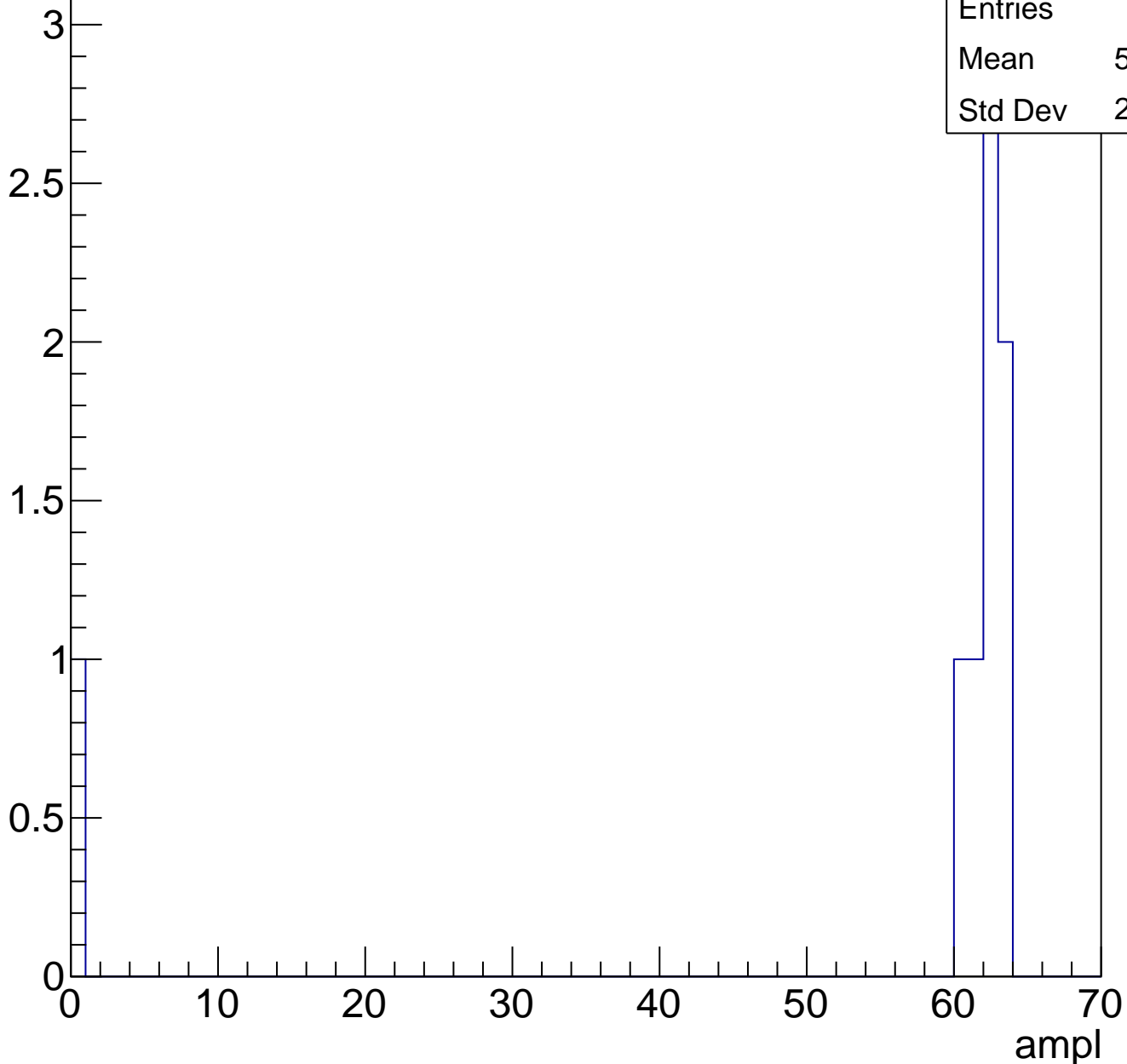
ampl



# B1L000S, U2-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch99, adc0

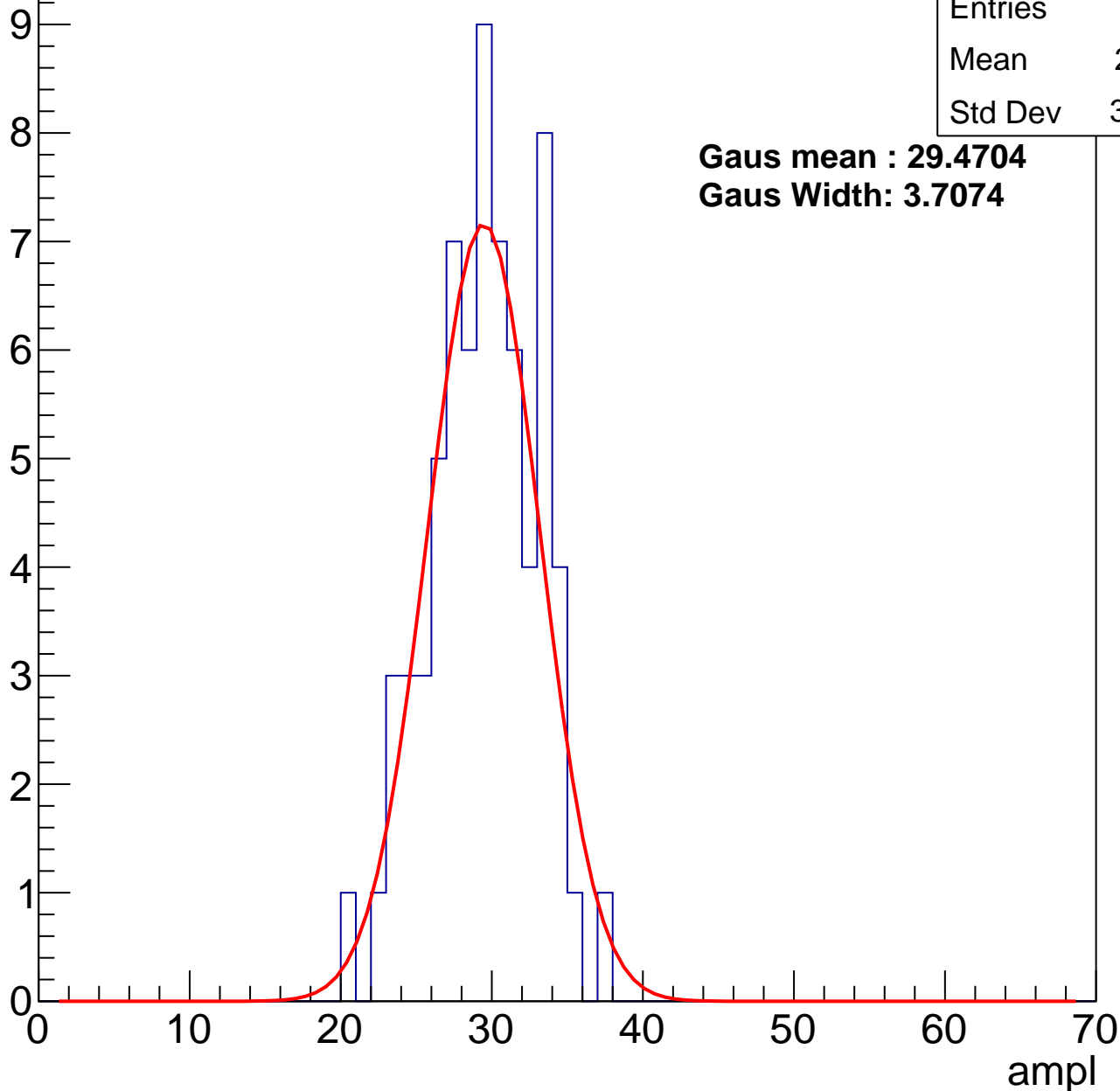
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.01
Std Dev	3.479

**Gaus mean : 29.4704**

**Gaus Width: 3.7074**



# B1L000S, U2-ch99, adc1

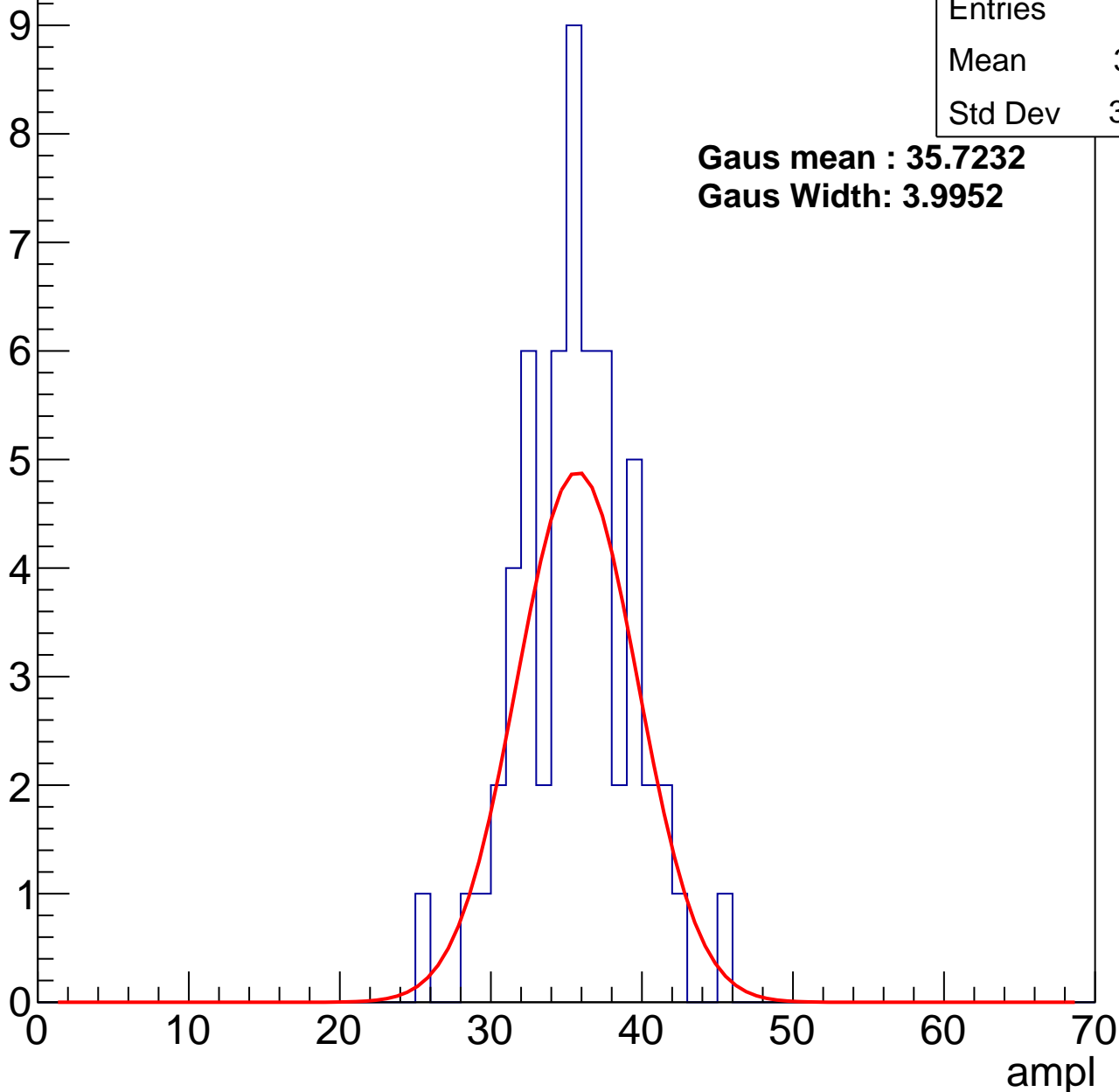
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.11
Std Dev	3.664

**Gaus mean : 35.7232**

**Gaus Width: 3.9952**



# B1L000S, U2-ch99, adc2

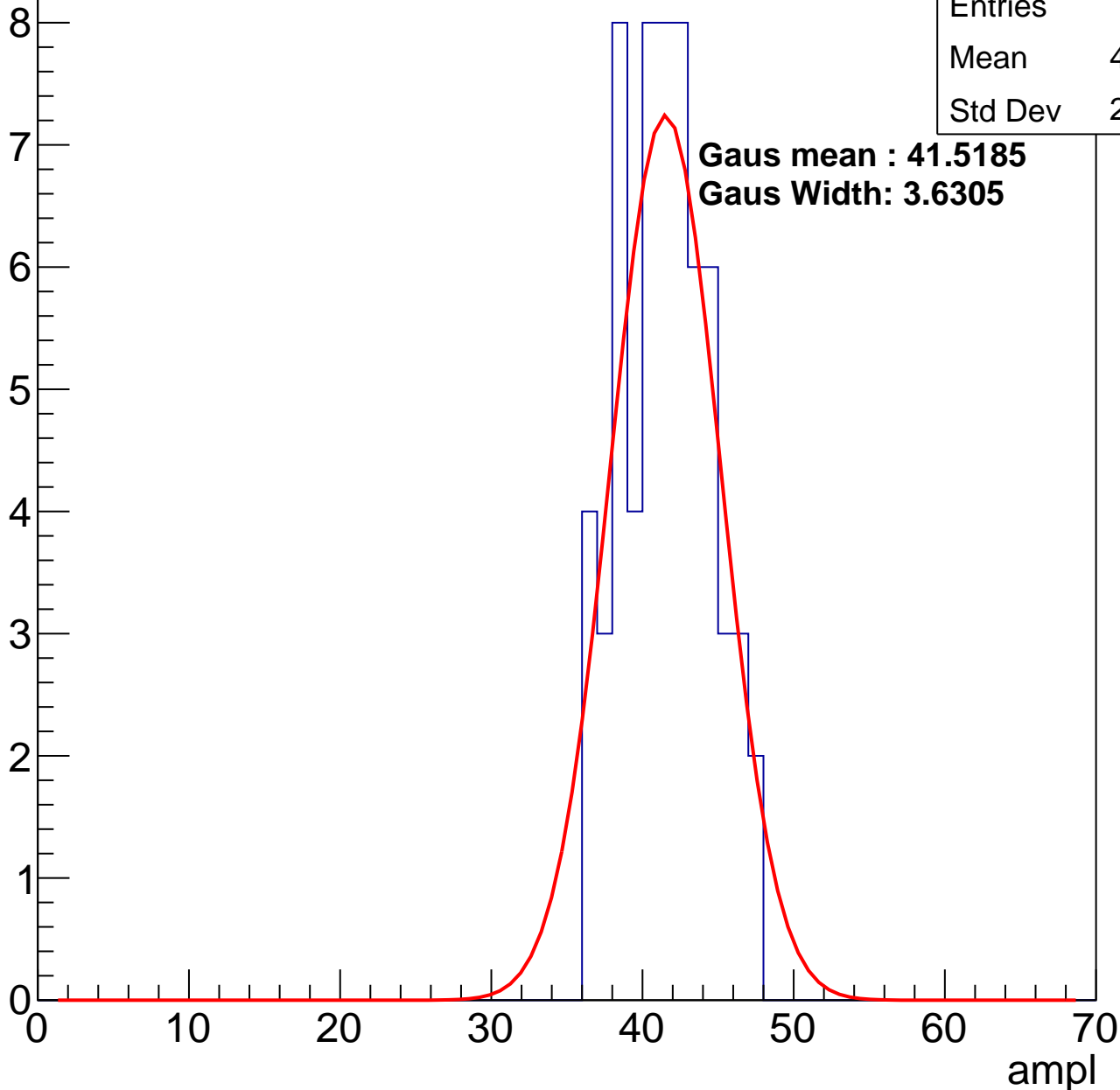
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.08
Std Dev	2.886

**Gaus mean : 41.5185**

**Gaus Width: 3.6305**

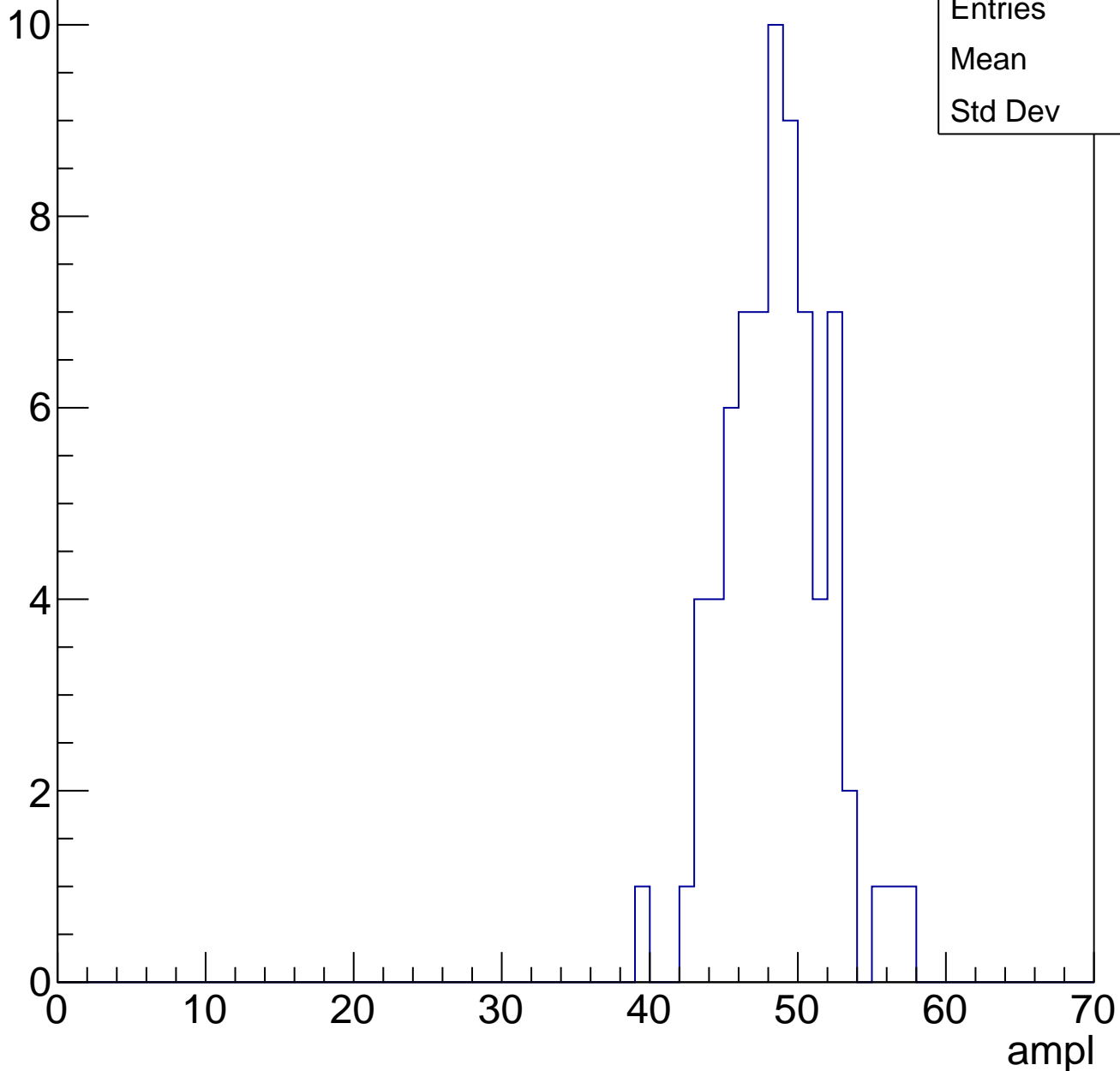


# B1L000S, U2-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	48.1
Std Dev	3.33

Entry

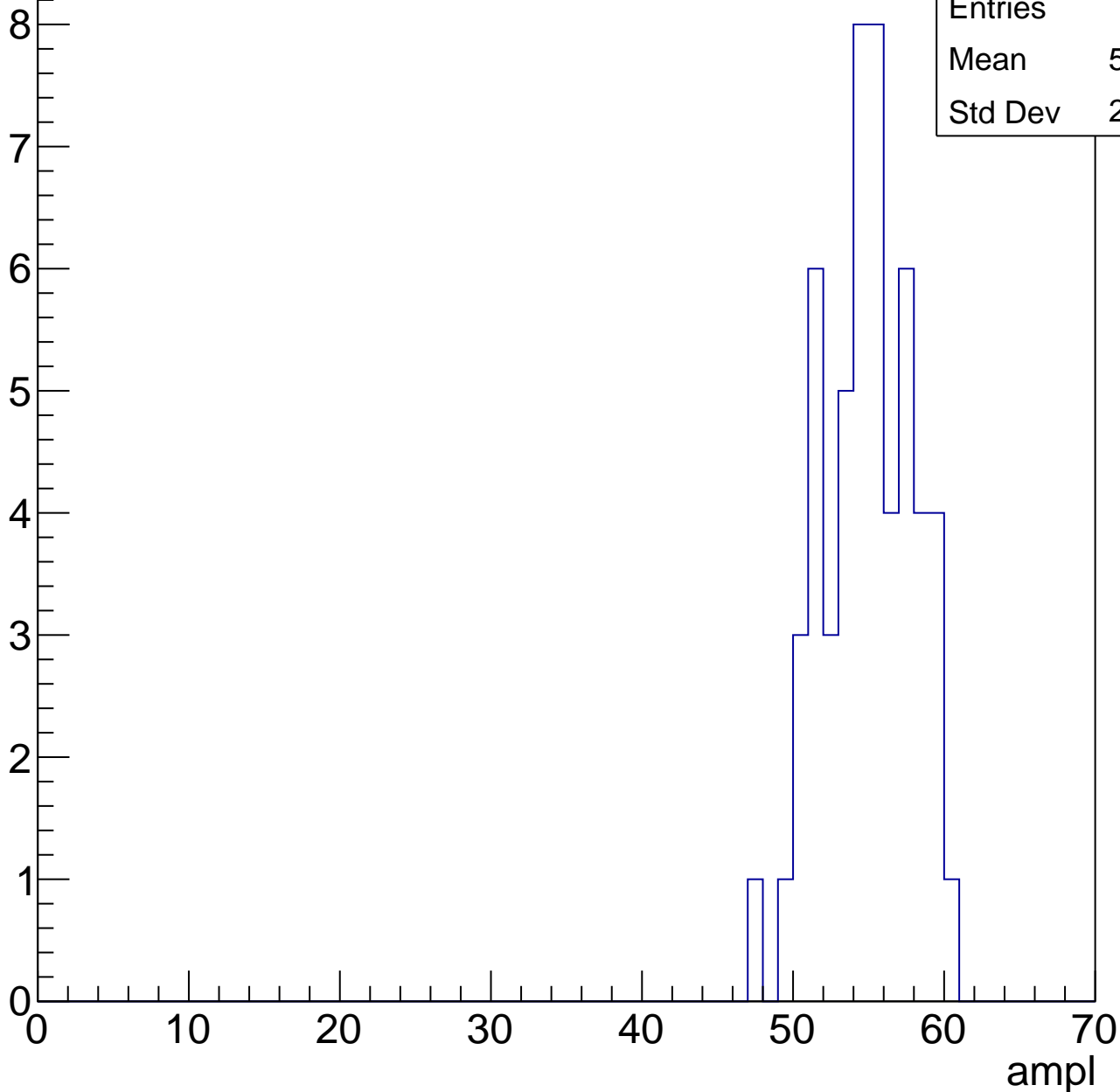


# B1L000S, U2-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	54.43
Std Dev	2.922



# B1L000S, U2-ch99, adc5

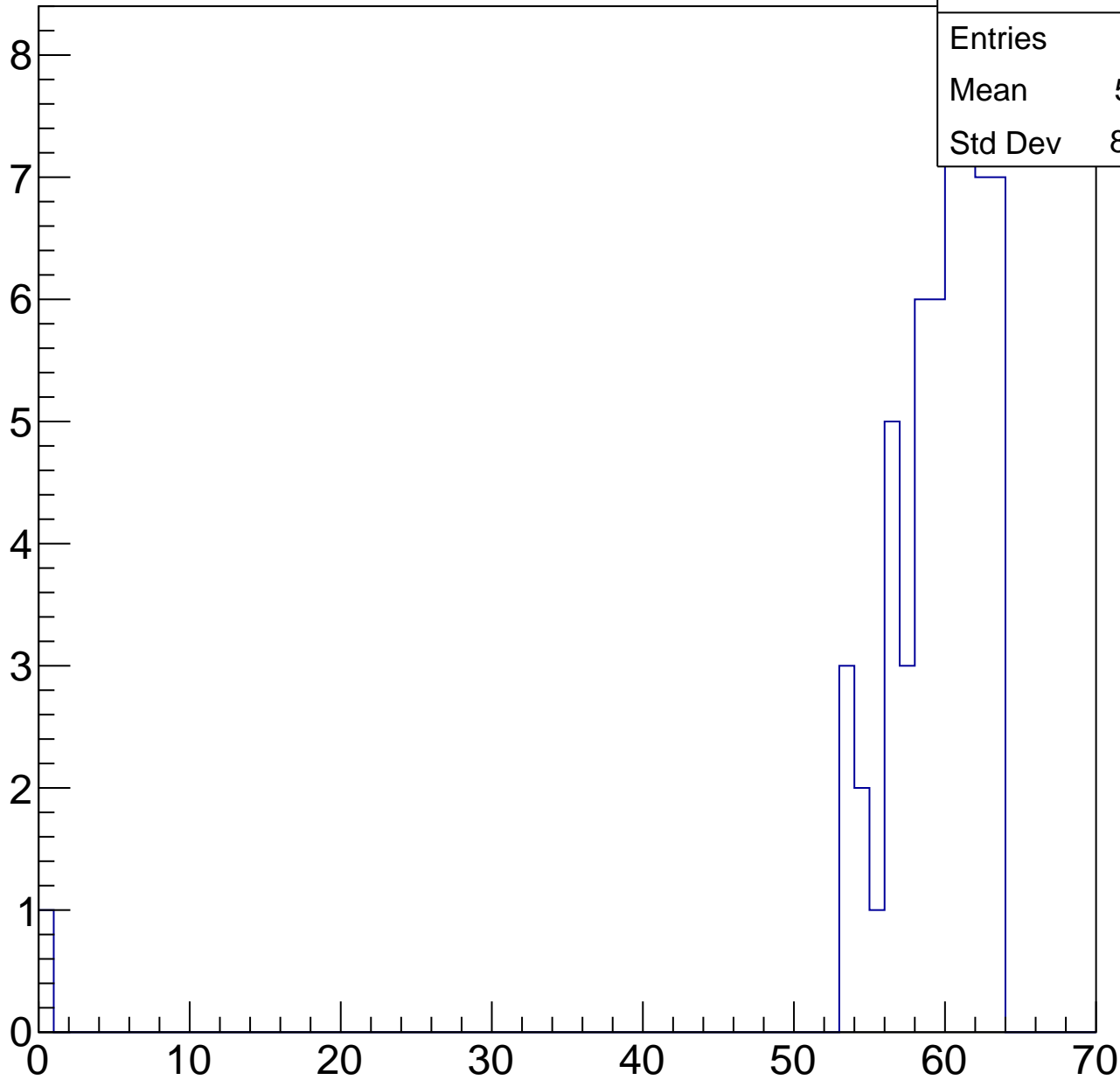
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	57
Mean	58.21
Std Dev	8.269

ampl



# B1L000S, U2-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

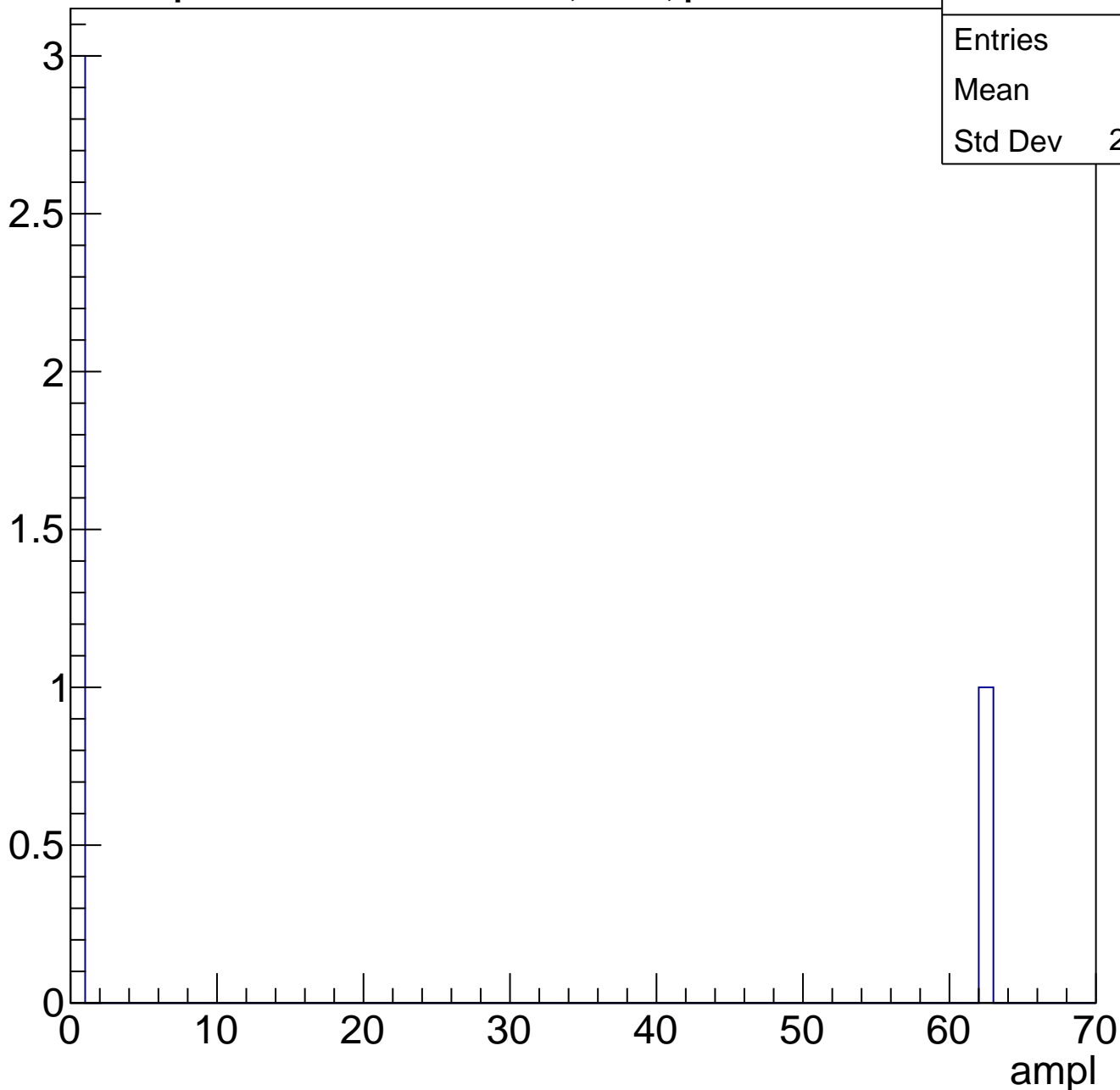




# B1L000S, U2-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch100, adc0

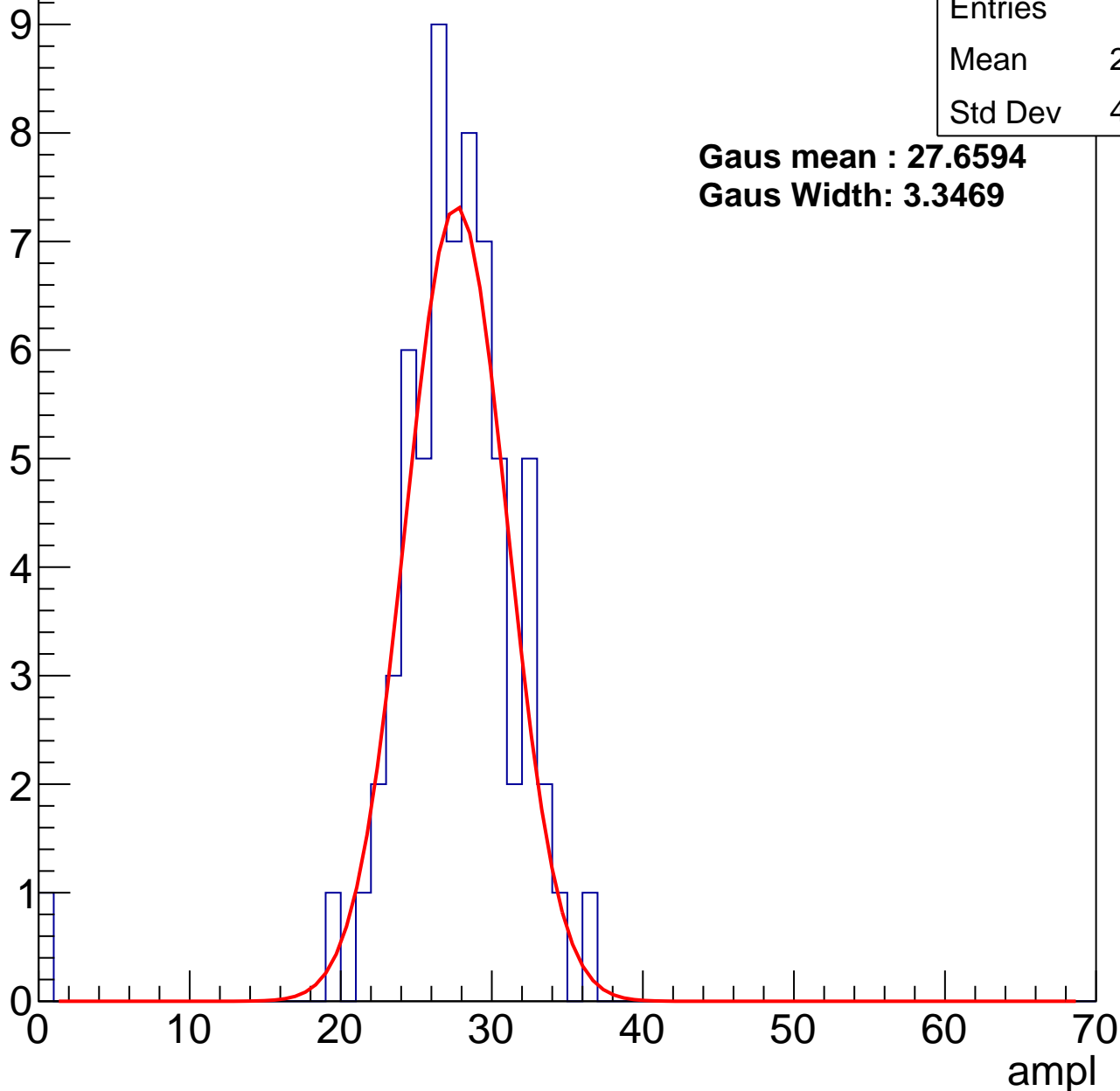
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	26.97
Std Dev	4.687

**Gaus mean : 27.6594**

**Gaus Width: 3.3469**



# B1L000S, U2-ch100, adc1

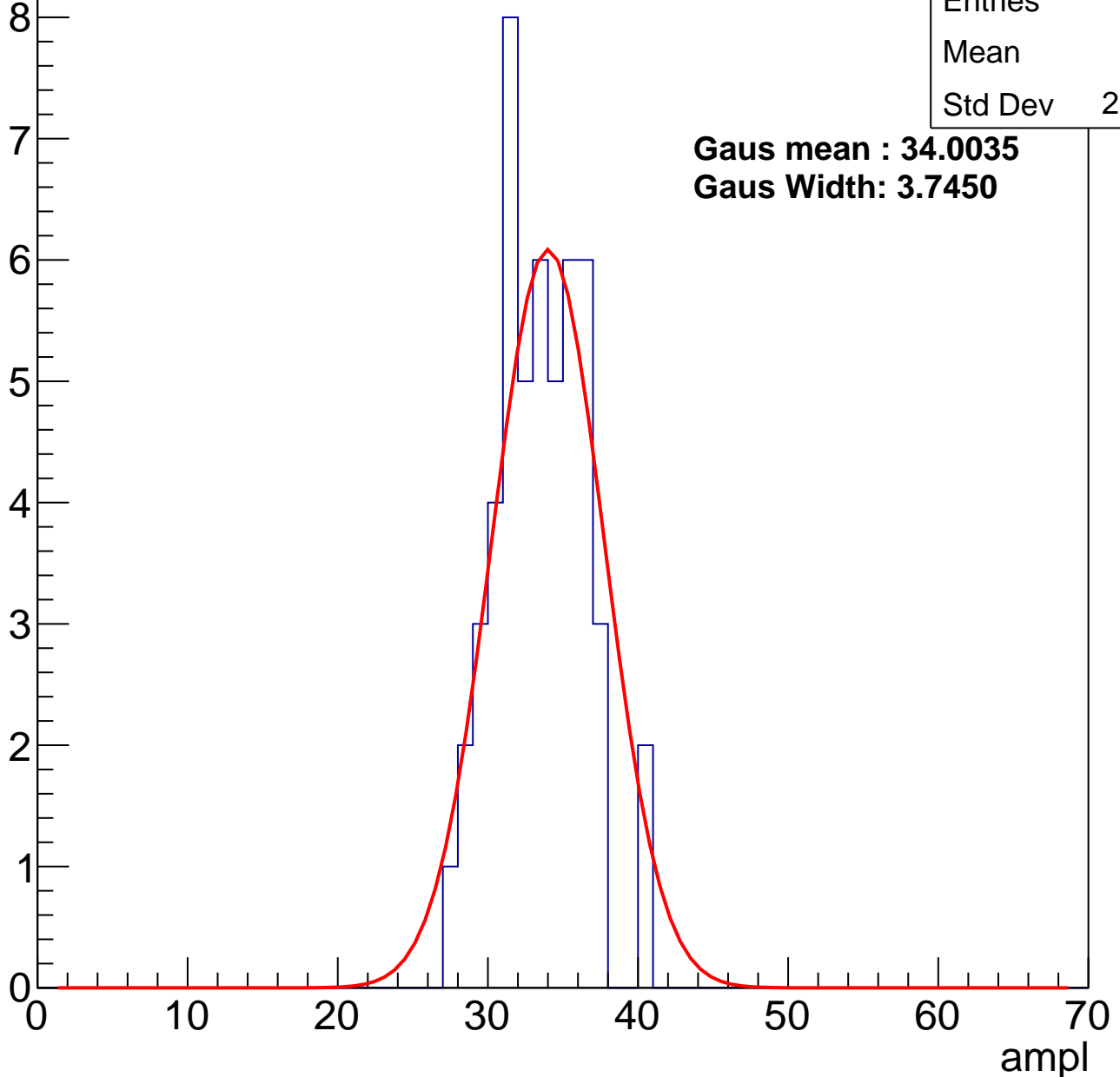
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	33
Std Dev	2.924

**Gaus mean : 34.0035**

**Gaus Width: 3.7450**



# B1L000S, U2-ch100, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	39.03
Std Dev	3.363

**Gaus mean : 39.5296**

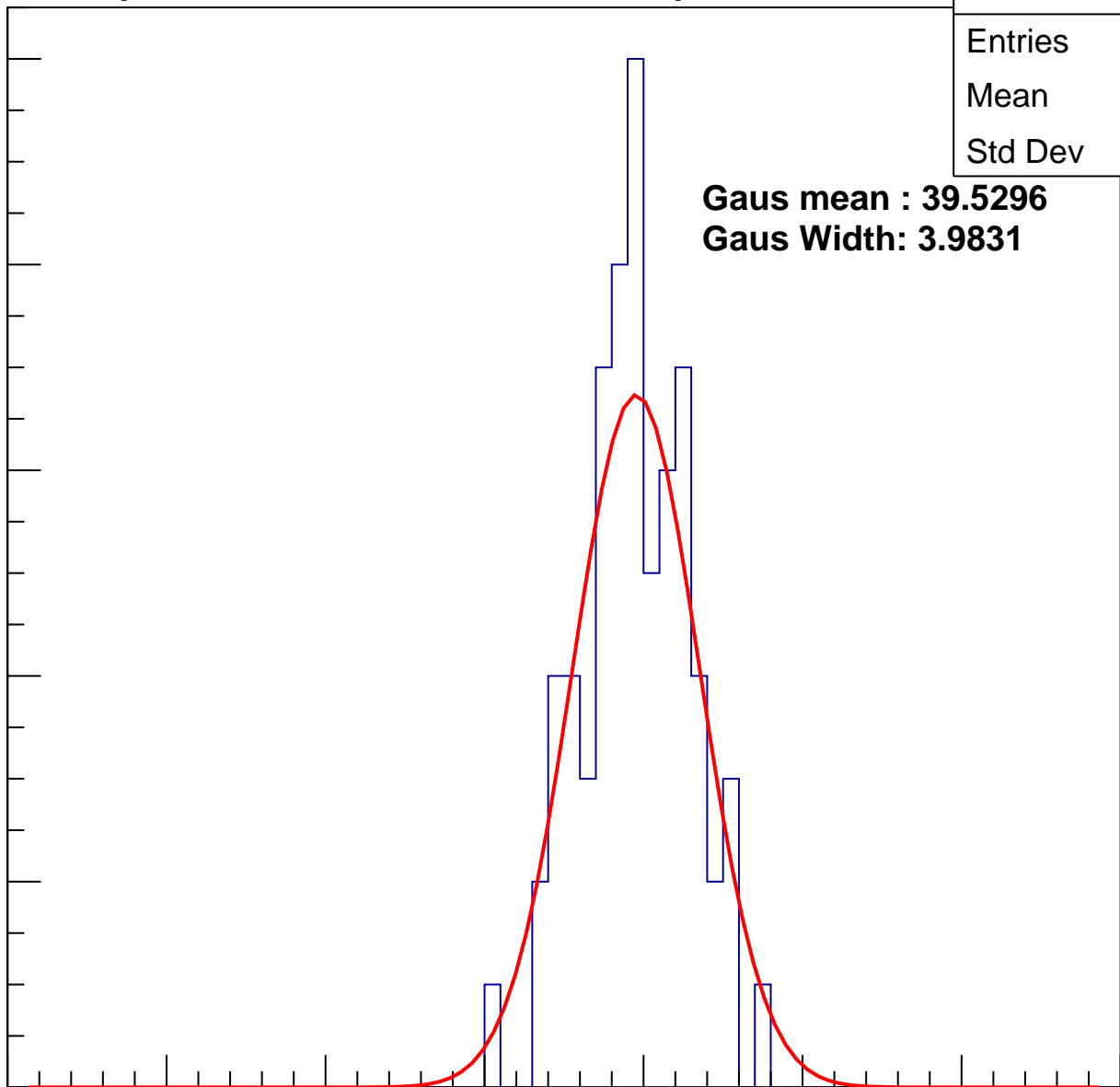
**Gaus Width: 3.9831**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

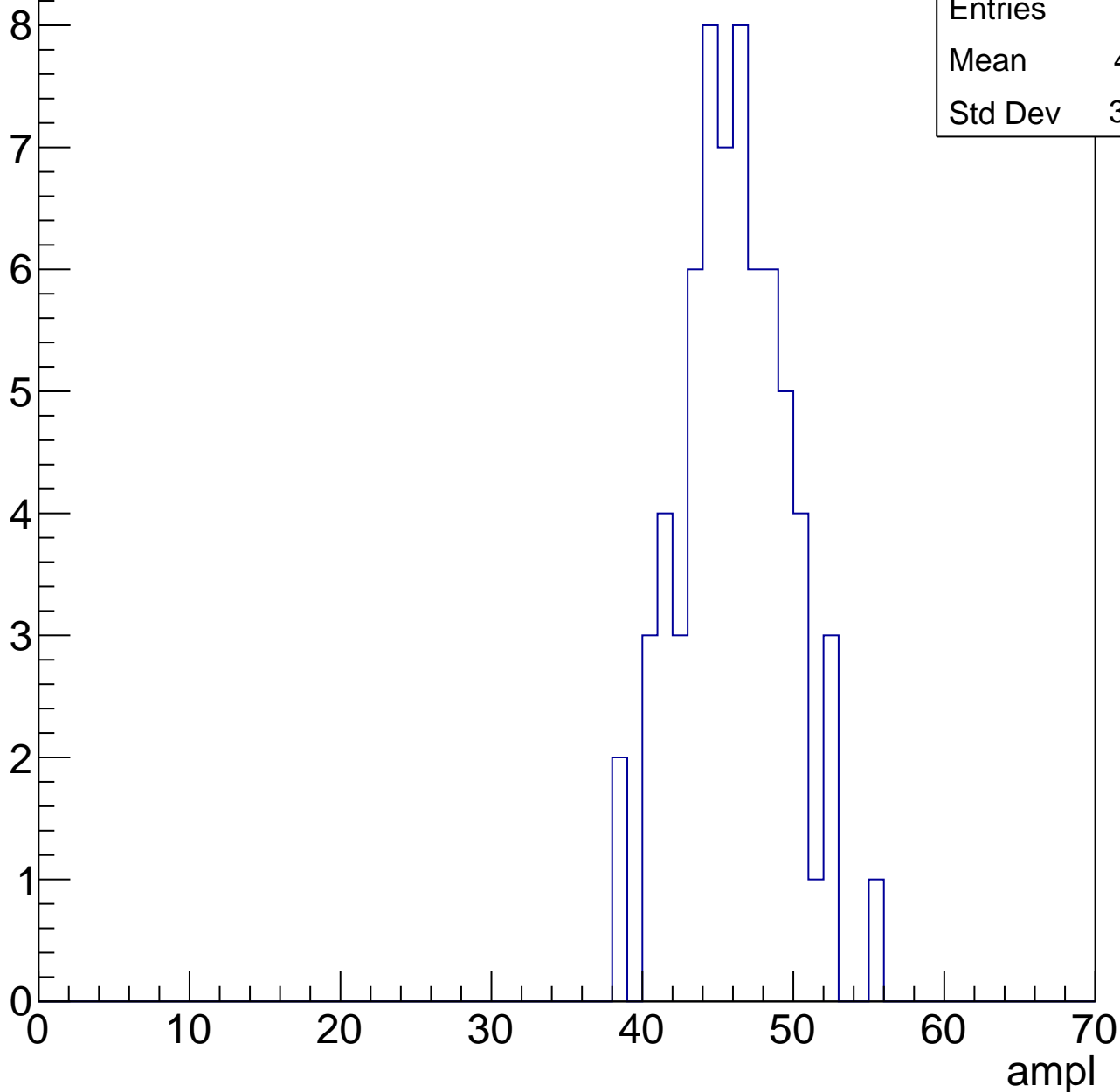


# B1L000S, U2-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	45.61
Std Dev	3.498

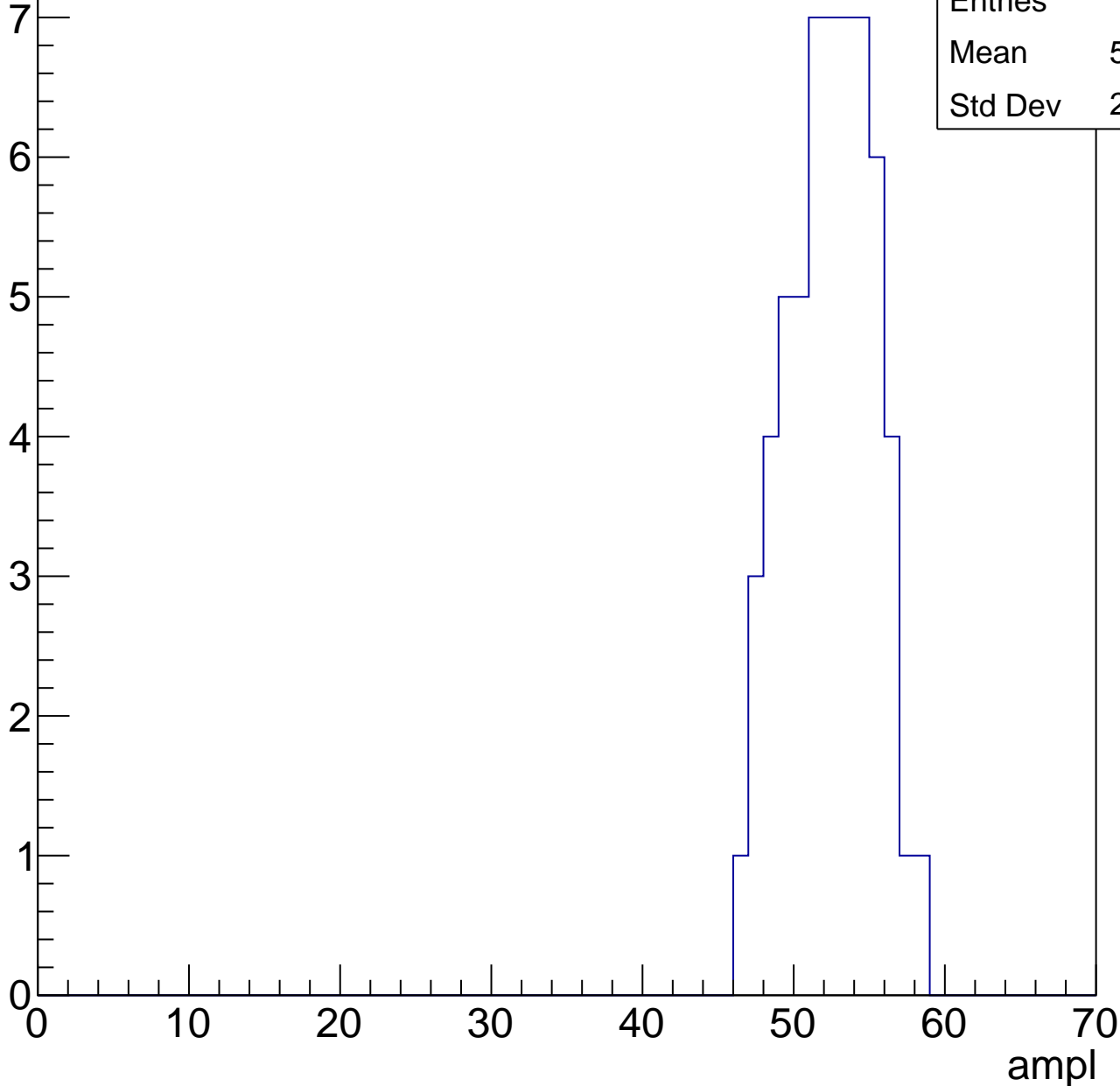


# B1L000S, U2-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	51.95
Std Dev	2.819

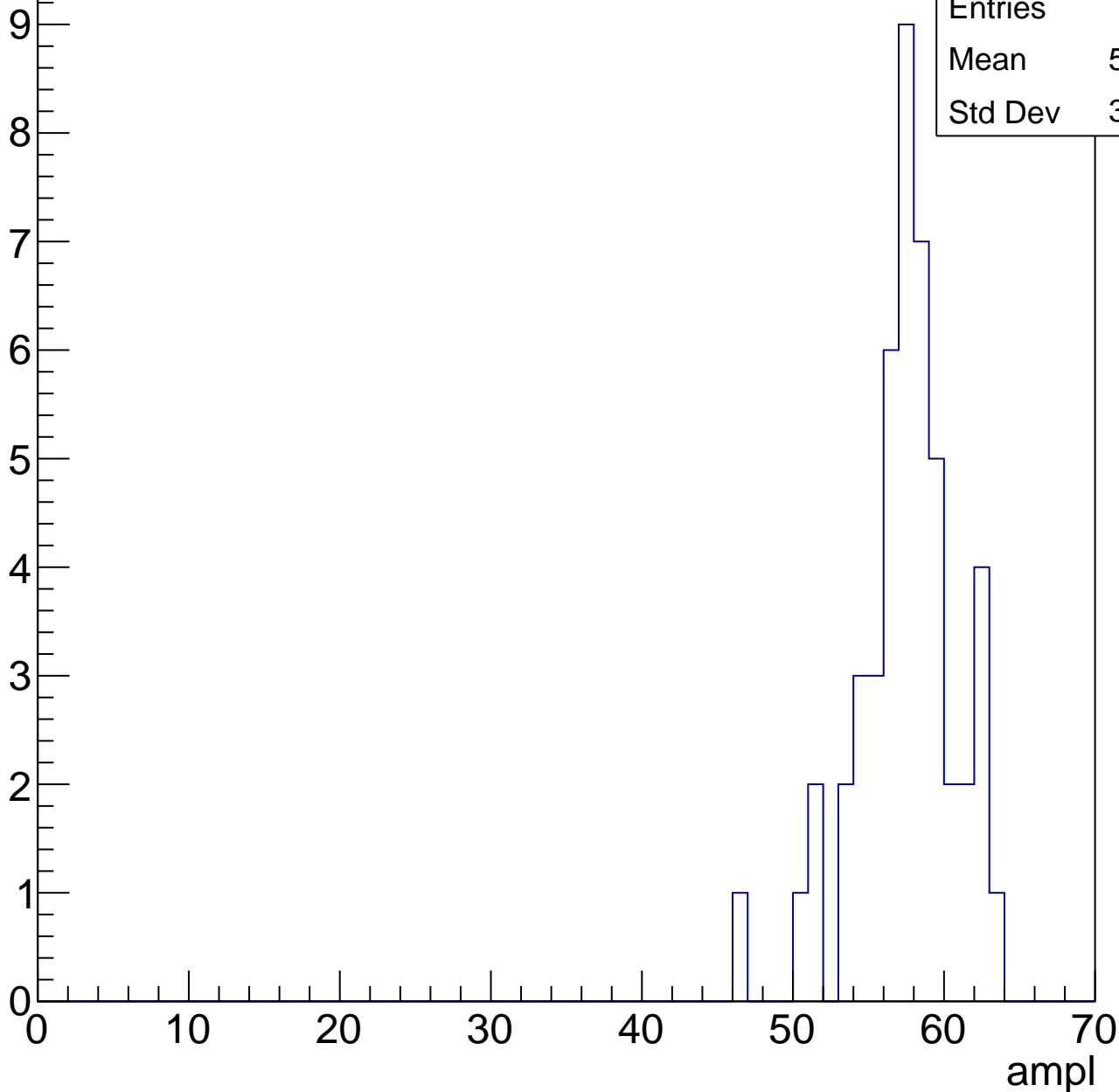


# B1L000S, U2-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

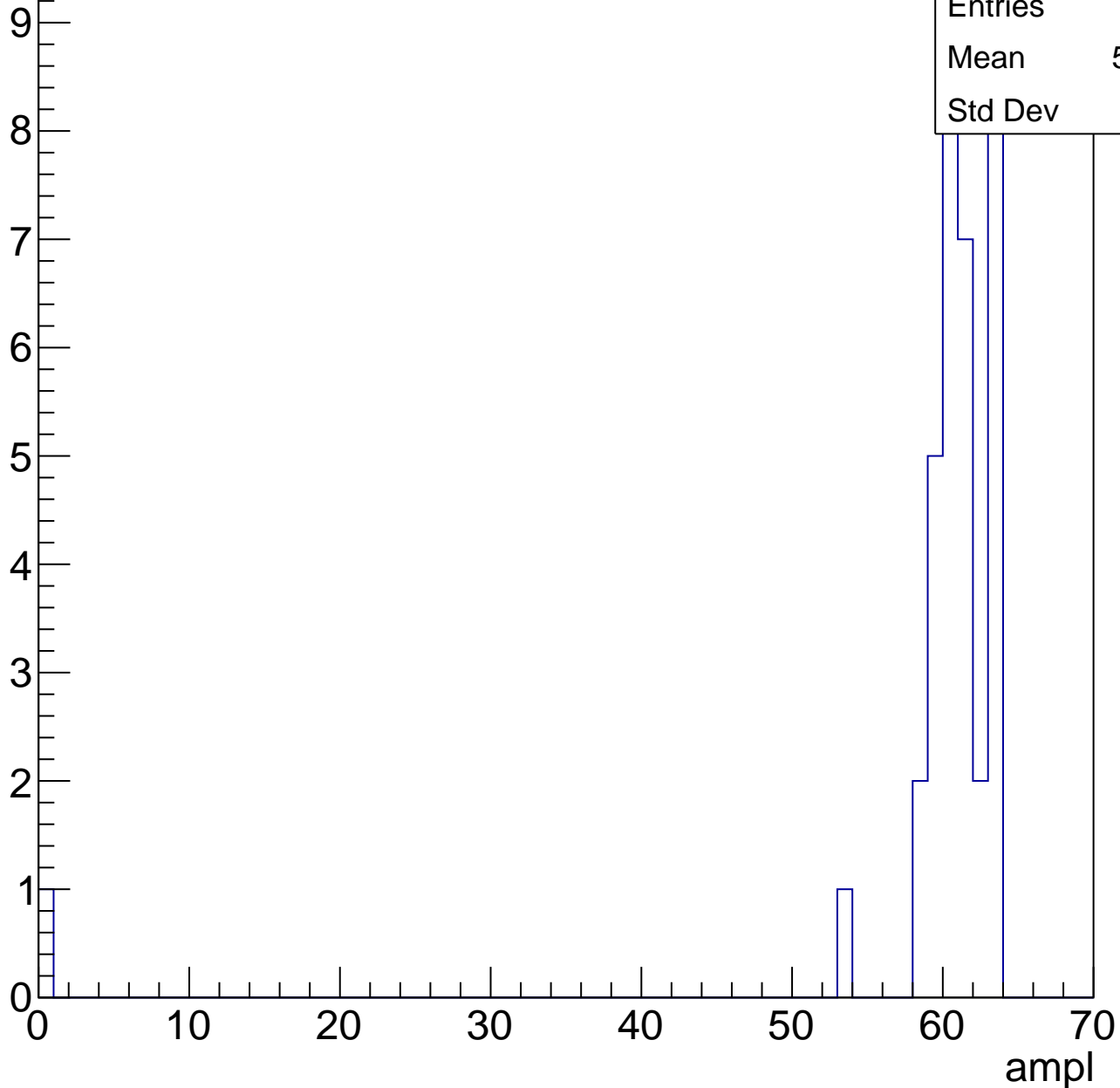
Entries	48
Mean	56.96
Std Dev	3.329



# B1L000S, U2-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

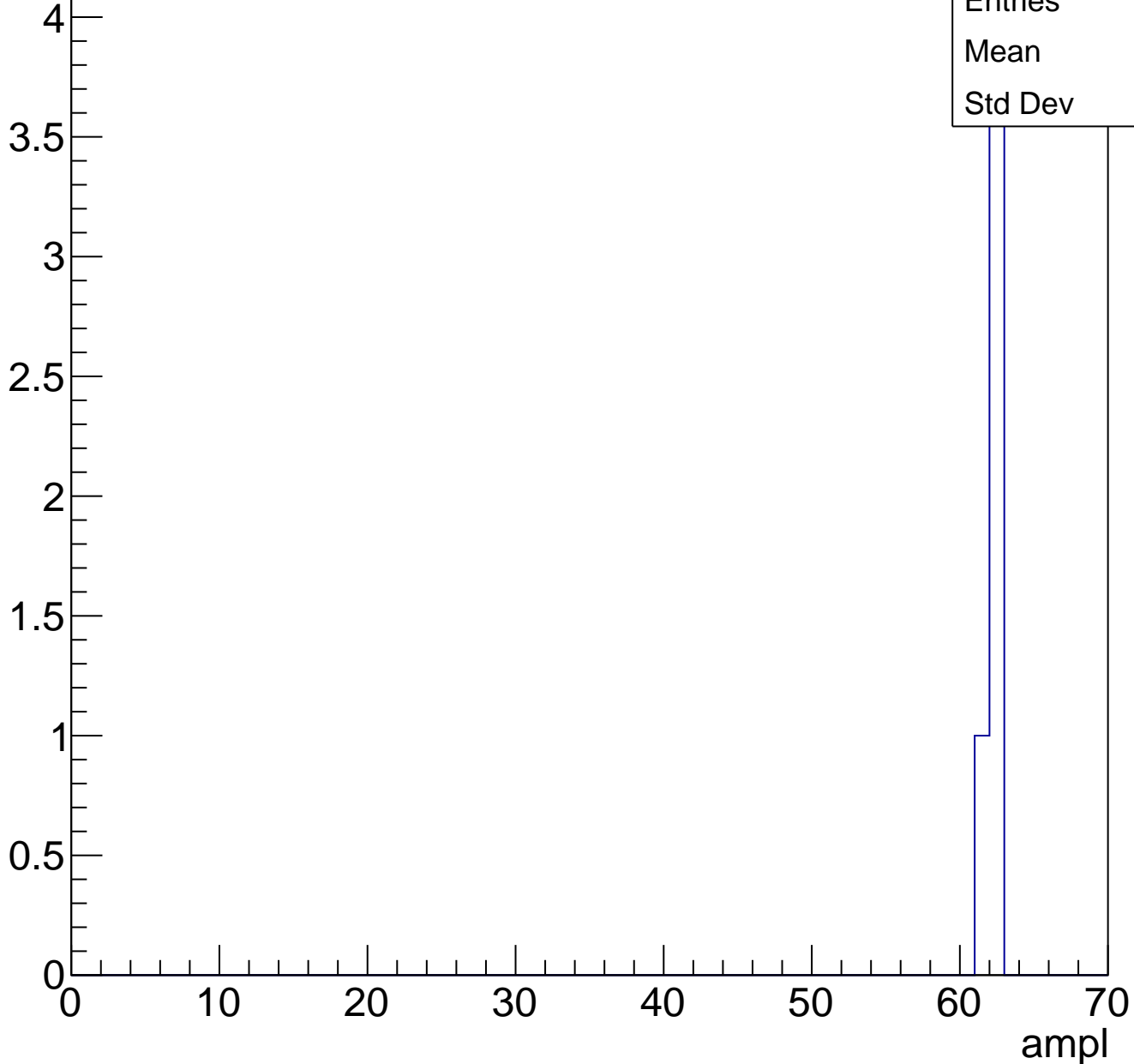




# B1L000S, U2-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch101, adc0

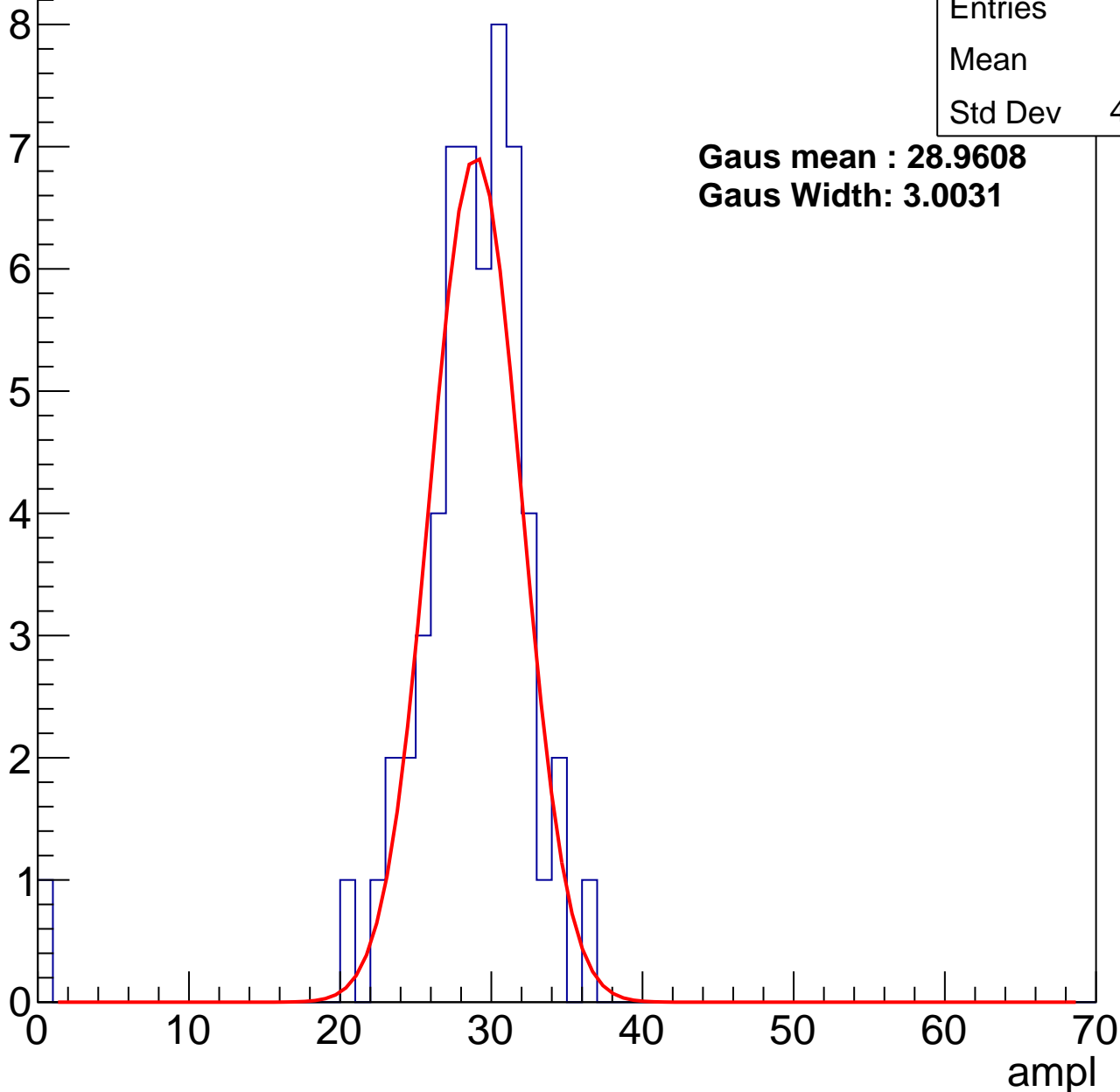
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28
Std Dev	4.852

**Gaus mean : 28.9608**

**Gaus Width: 3.0031**



# B1L000S, U2-ch101, adc1

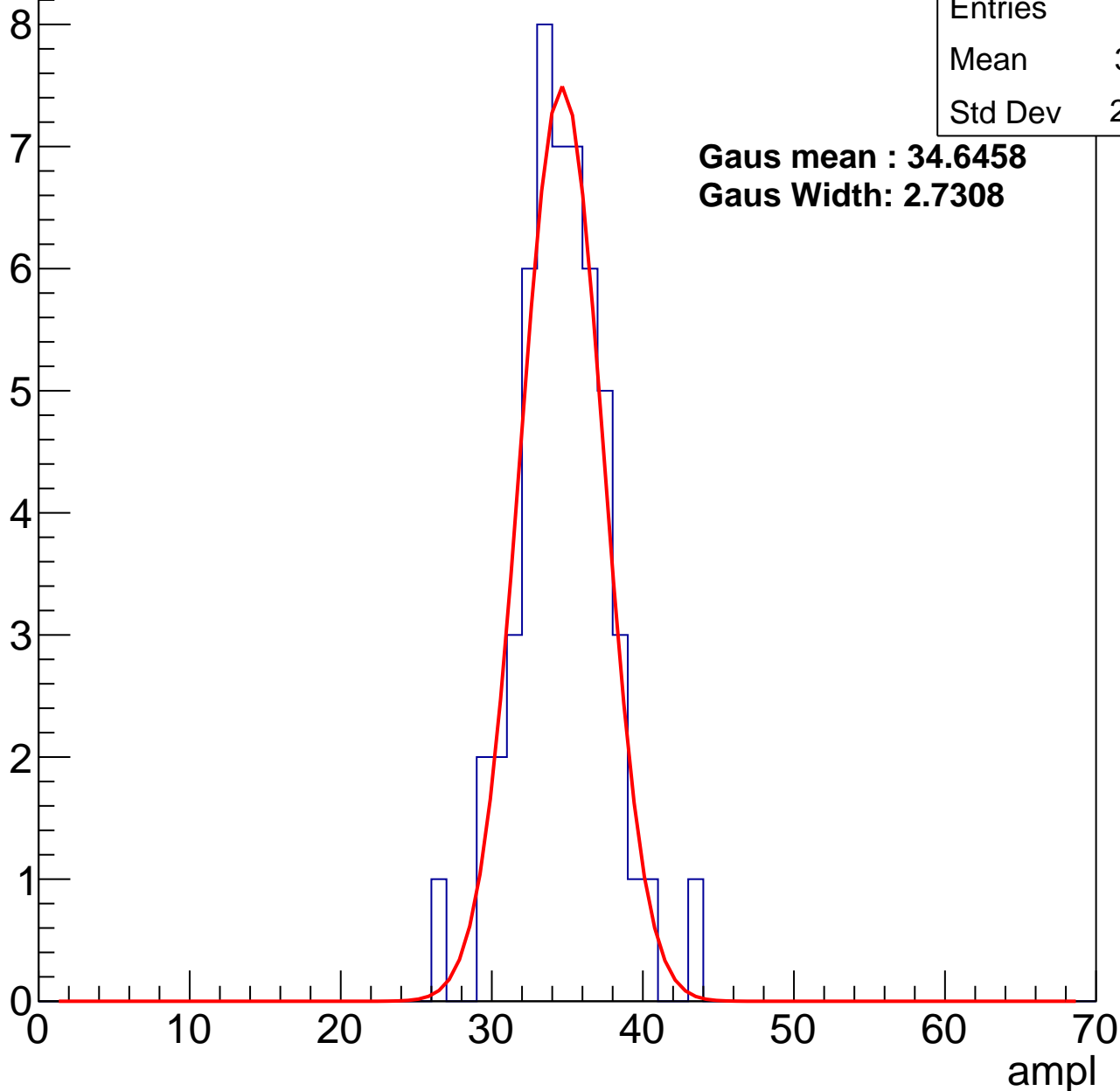
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	34.21
Std Dev	2.974

**Gaus mean : 34.6458**

**Gaus Width: 2.7308**



# B1L000S, U2-ch101, adc2

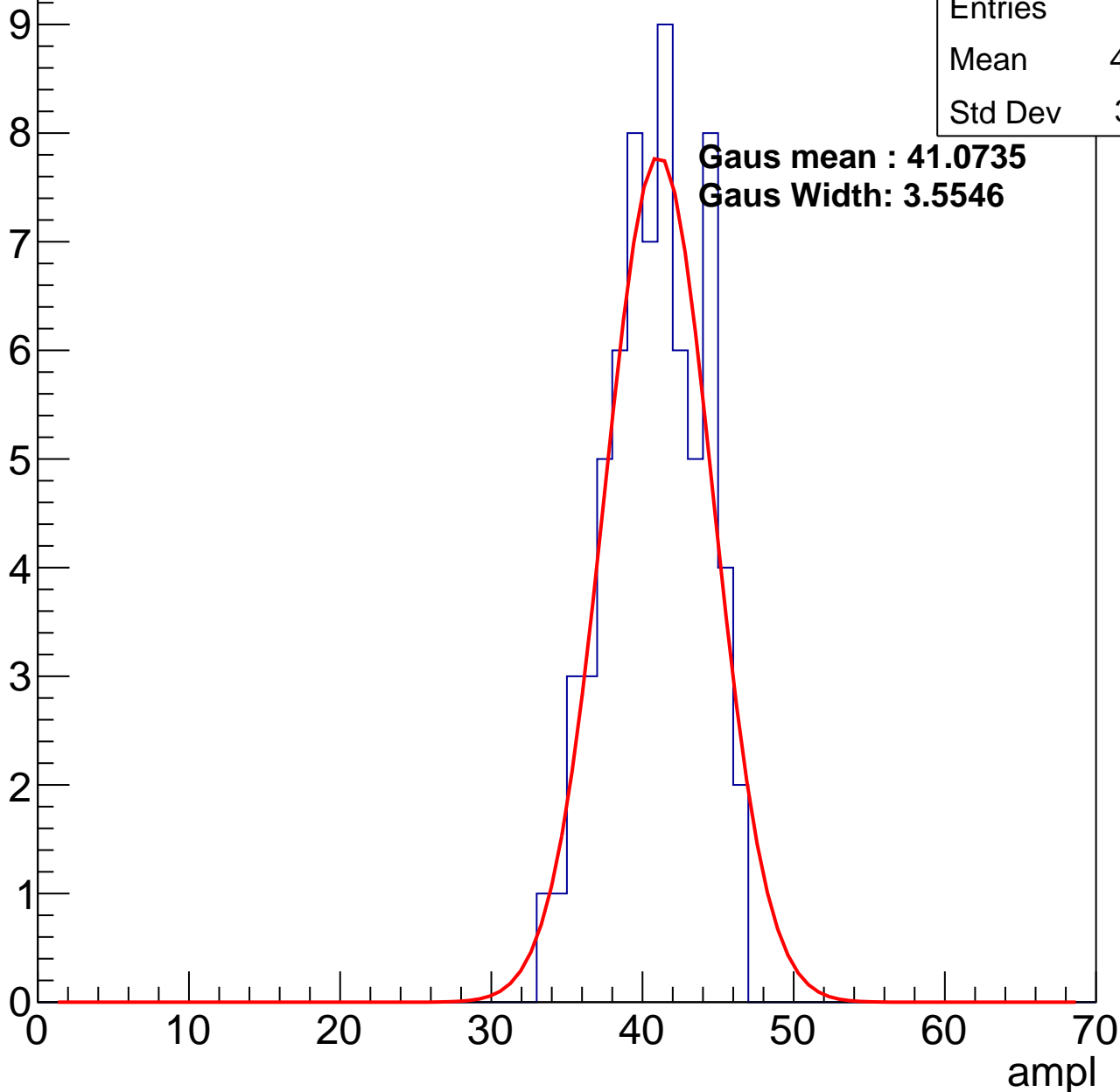
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	40.37
Std Dev	3.101

**Gaus mean : 41.0735**

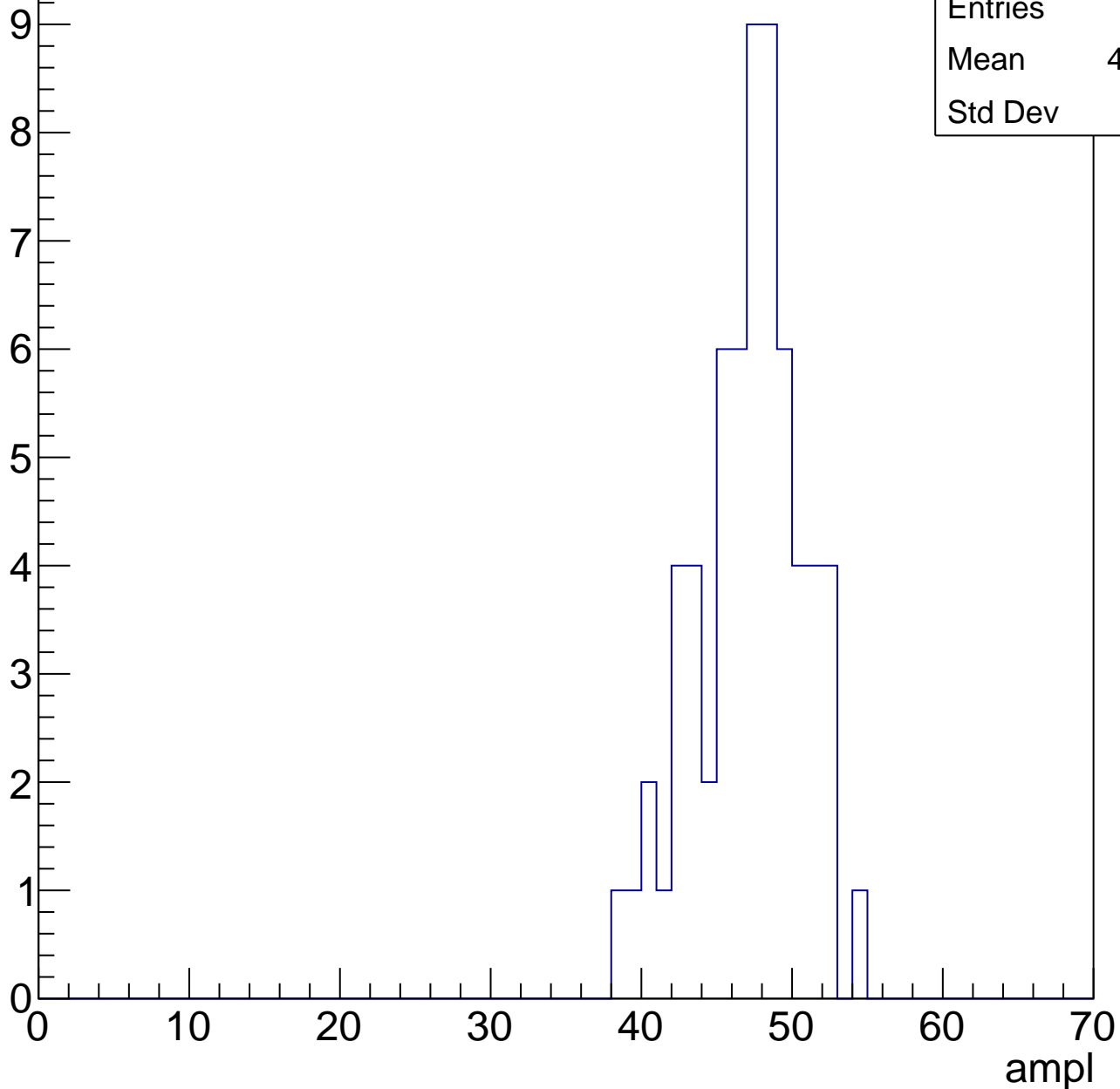
**Gaus Width: 3.5546**



# B1L000S, U2-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



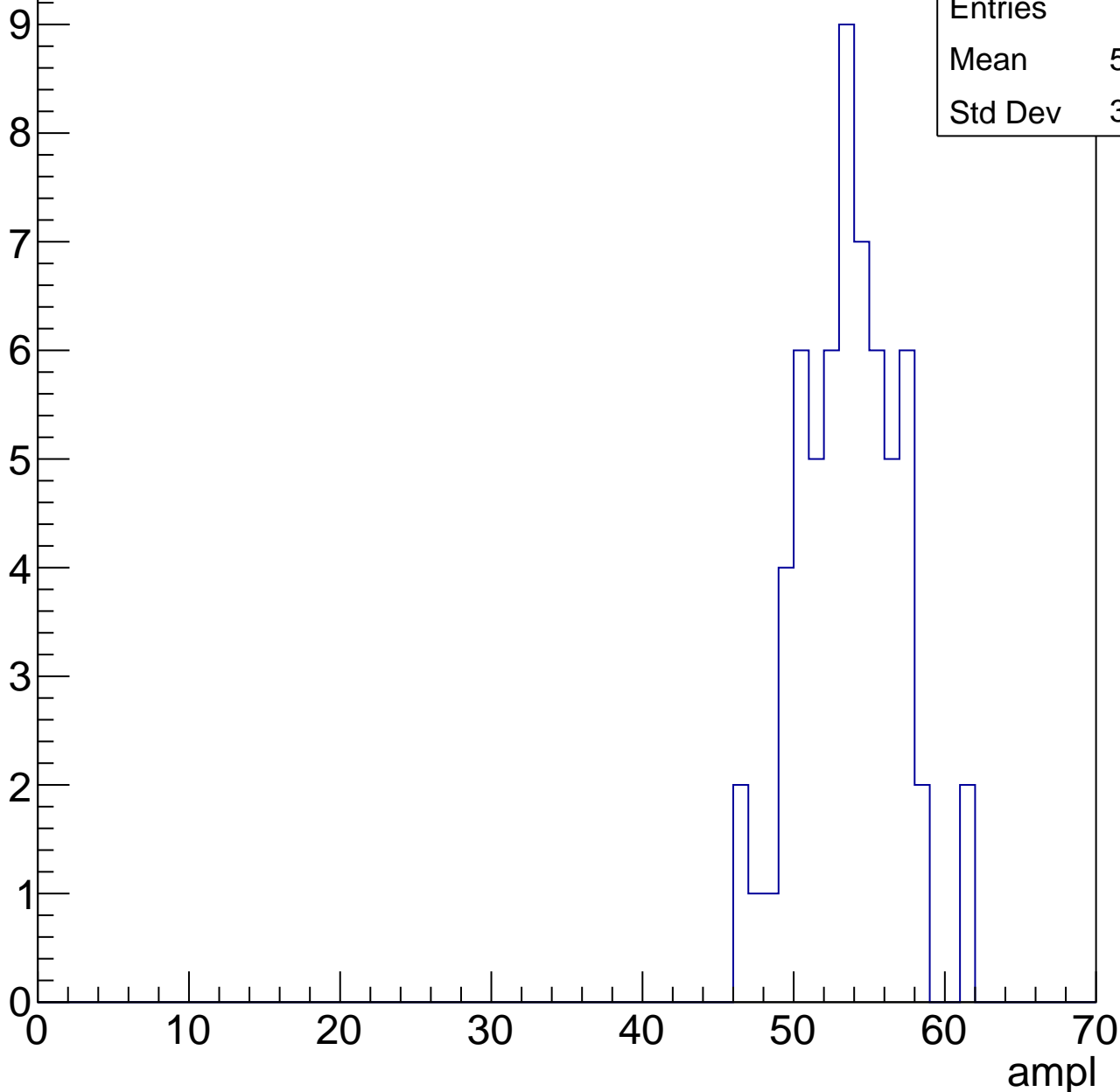
Entries	64
Mean	46.67
Std Dev	3.46

# B1L000S, U2-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

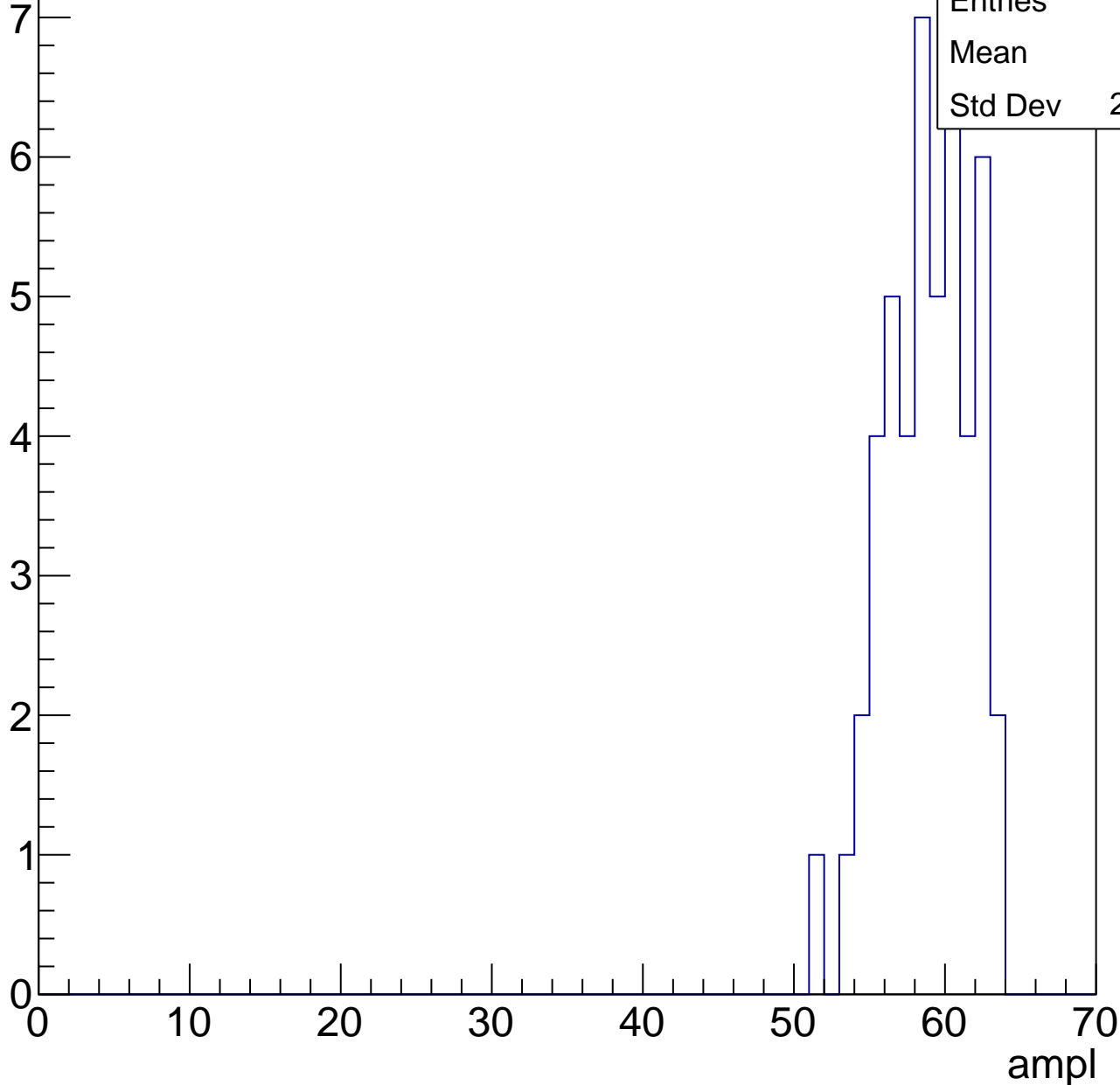
Entries	62
Mean	53.15
Std Dev	3.242



# B1L000S, U2-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

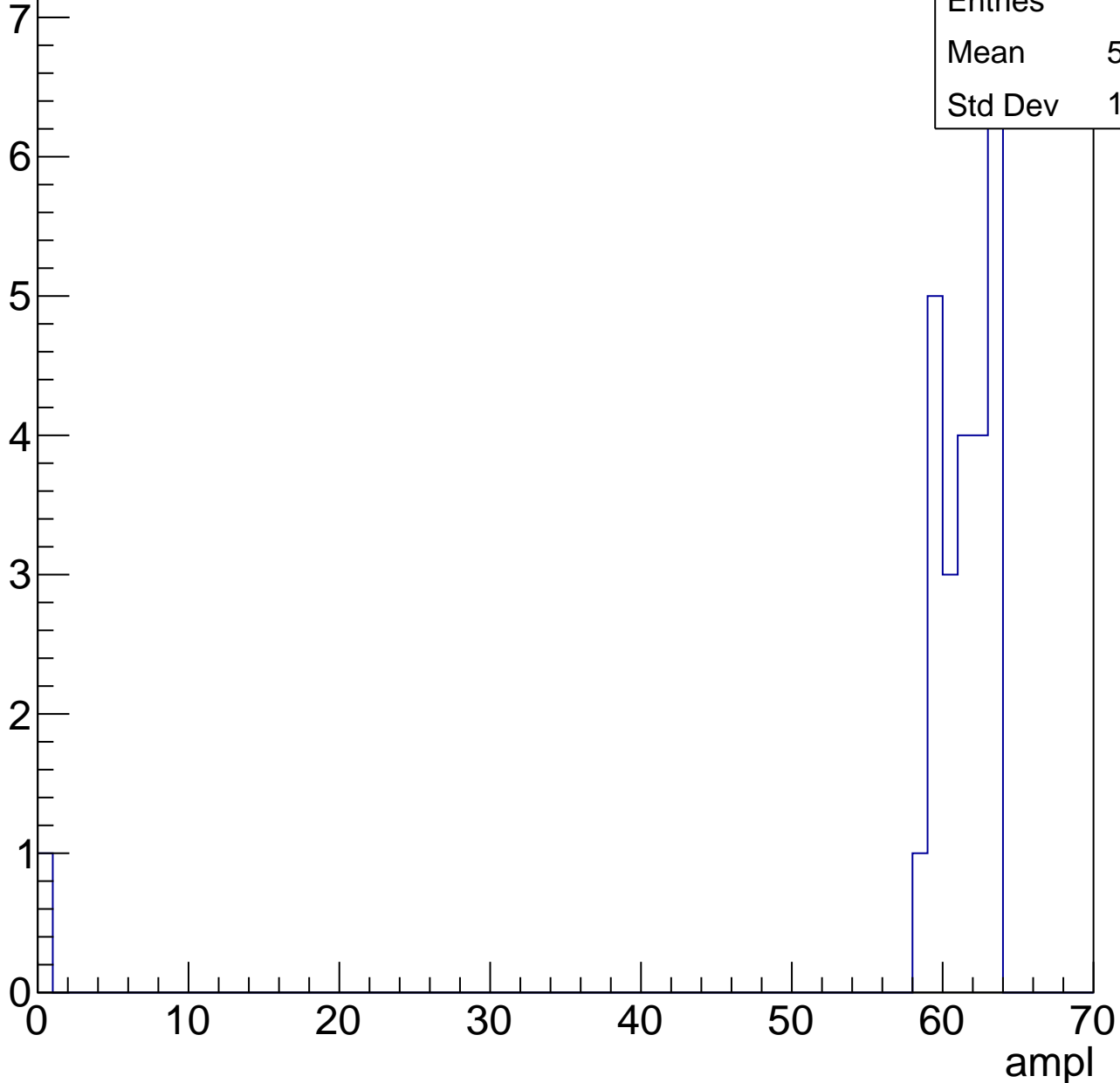


# B1L000S, U2-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.64
Std Dev	12.08





# B1L000S, U2-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch102, adc0

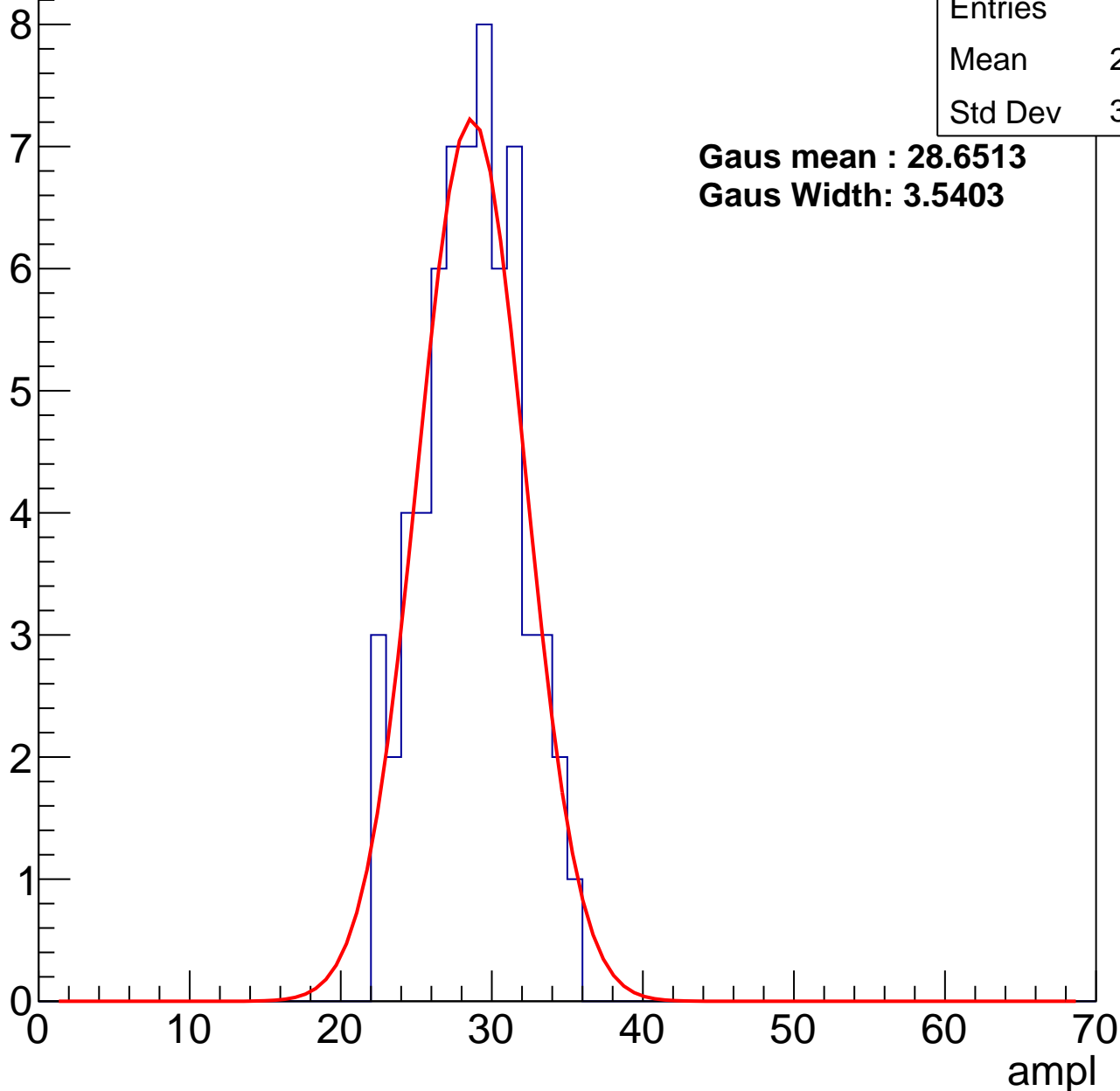
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	28.19
Std Dev	3.152

**Gaus mean : 28.6513**

**Gaus Width: 3.5403**



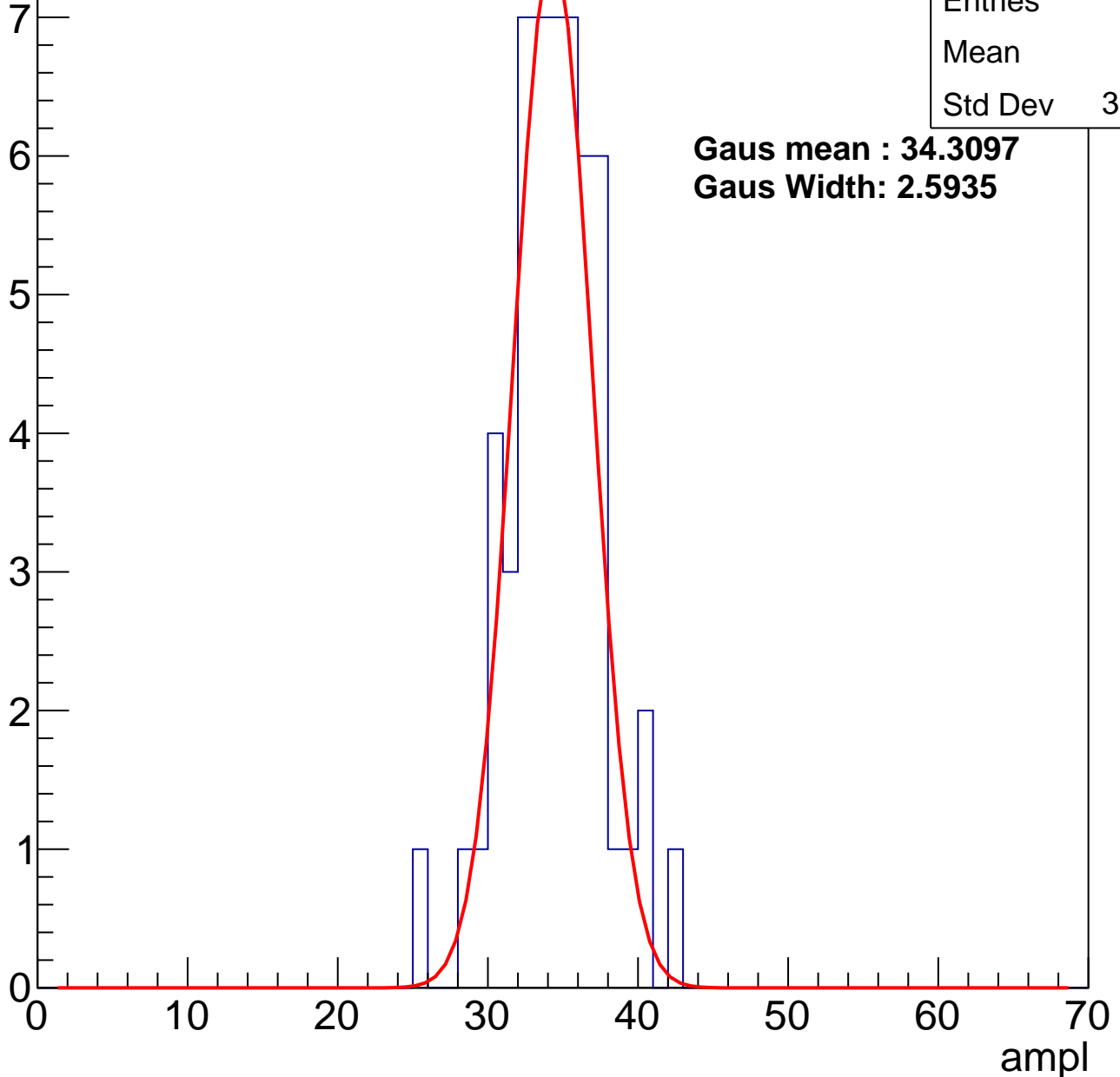
# B1L000S, U2-ch102, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34
Std Dev	3.104

**Gaus mean : 34.3097**  
**Gaus Width: 2.5935**



# B1L000S, U2-ch102, adc2

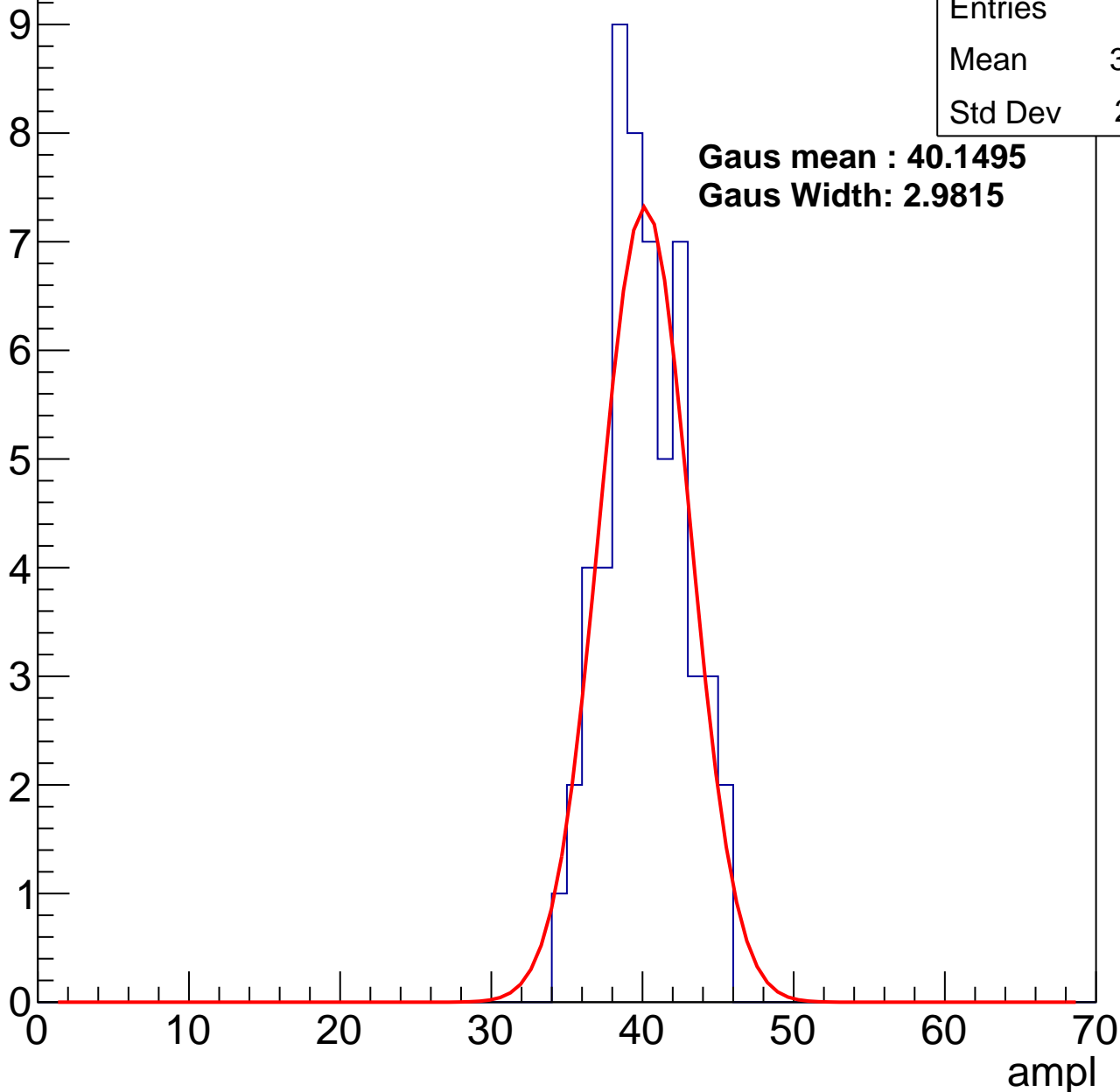
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	39.64
Std Dev	2.631

**Gaus mean : 40.1495**

**Gaus Width: 2.9815**

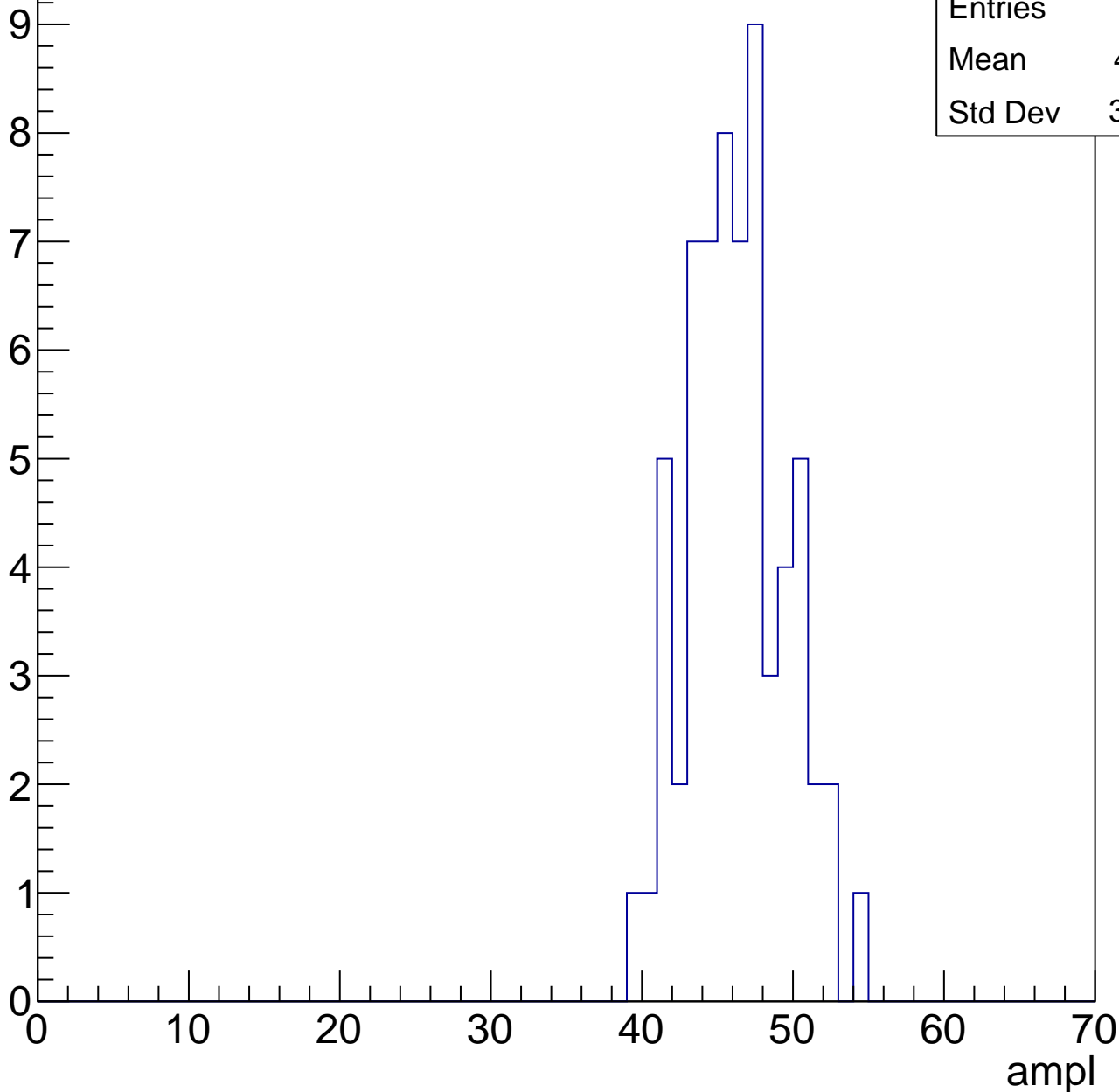


# B1L000S, U2-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	45.81
Std Dev	3.216

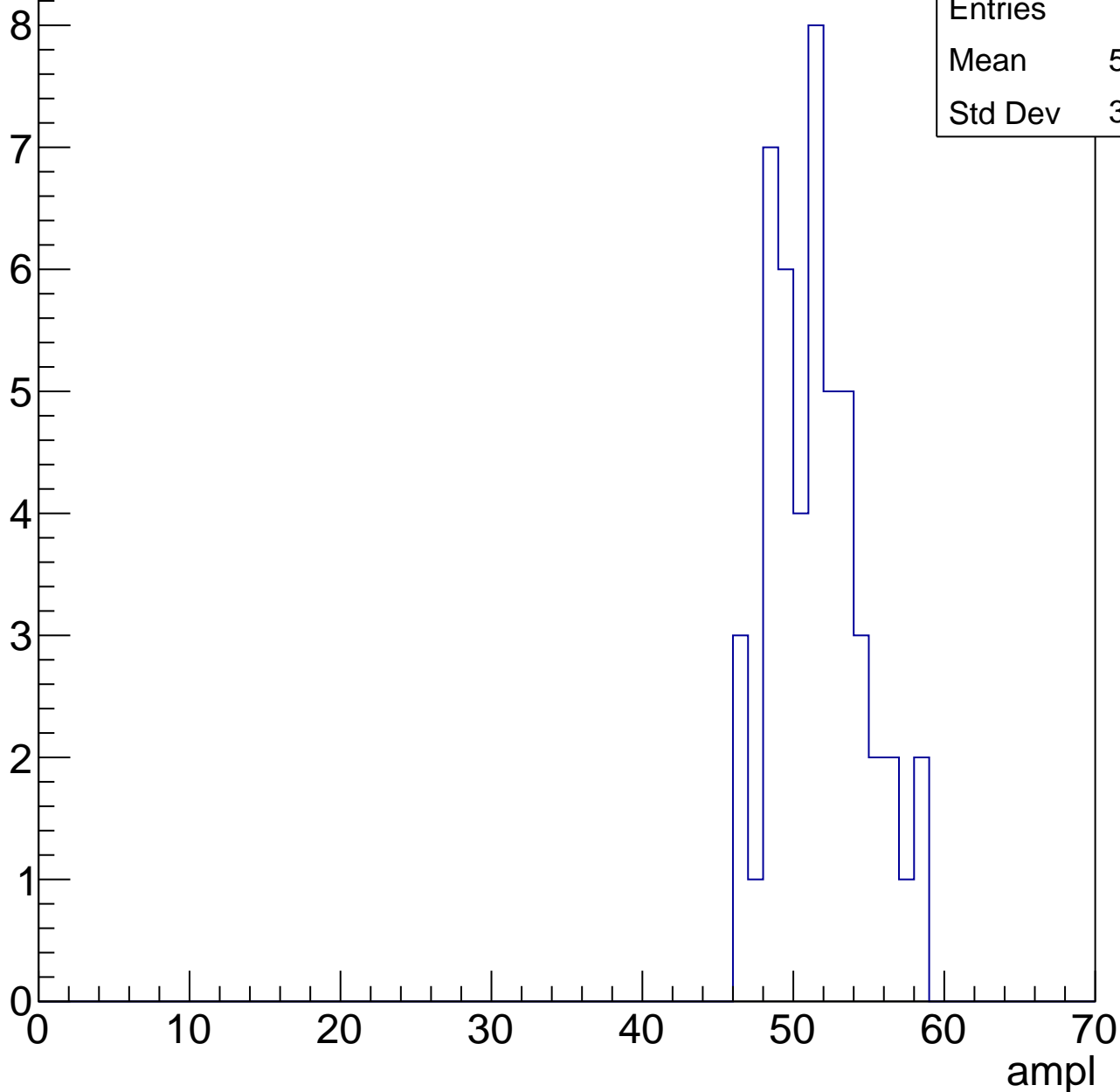


# B1L000S, U2-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	51.12
Std Dev	3.028

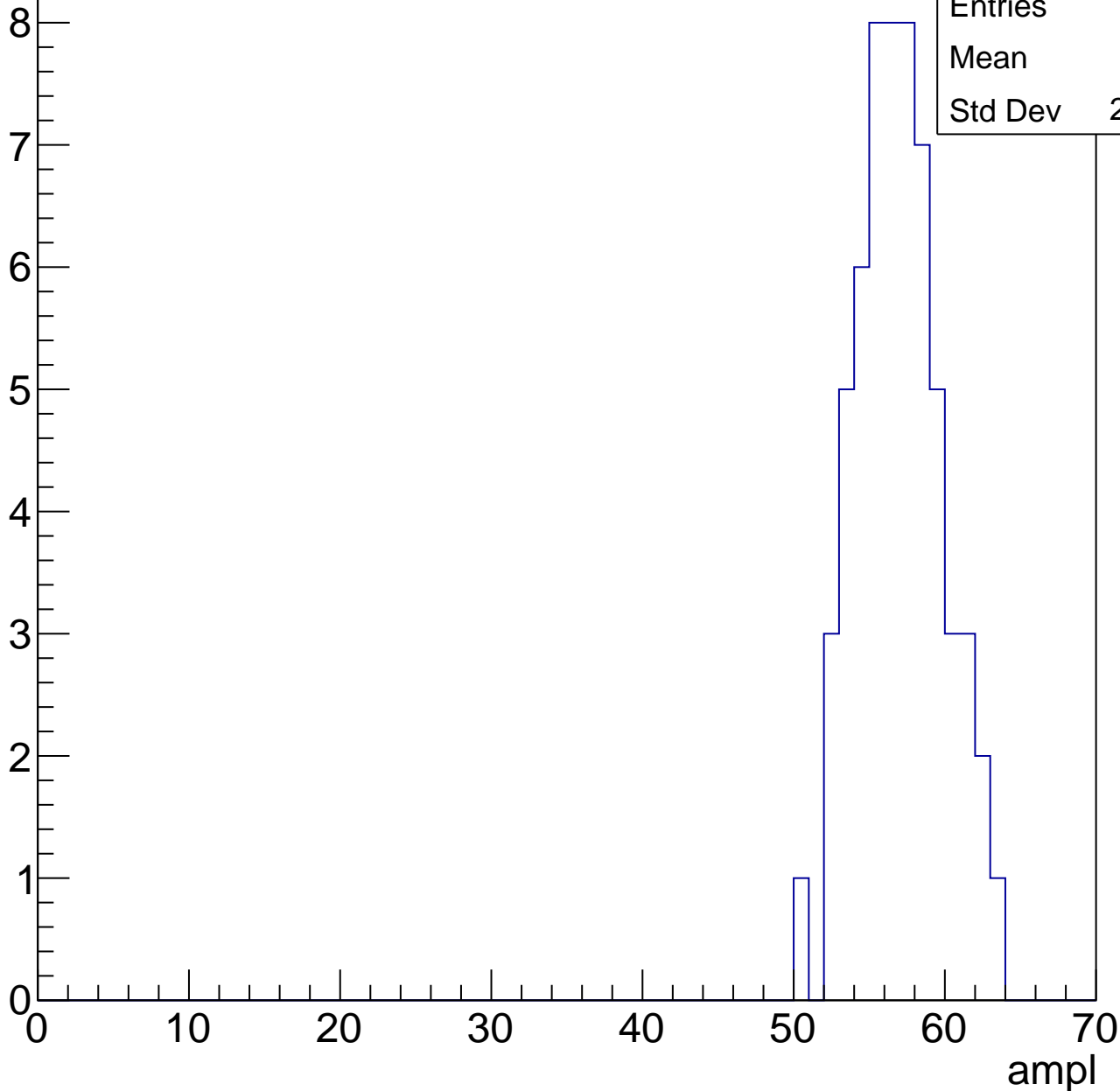


# B1L000S, U2-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

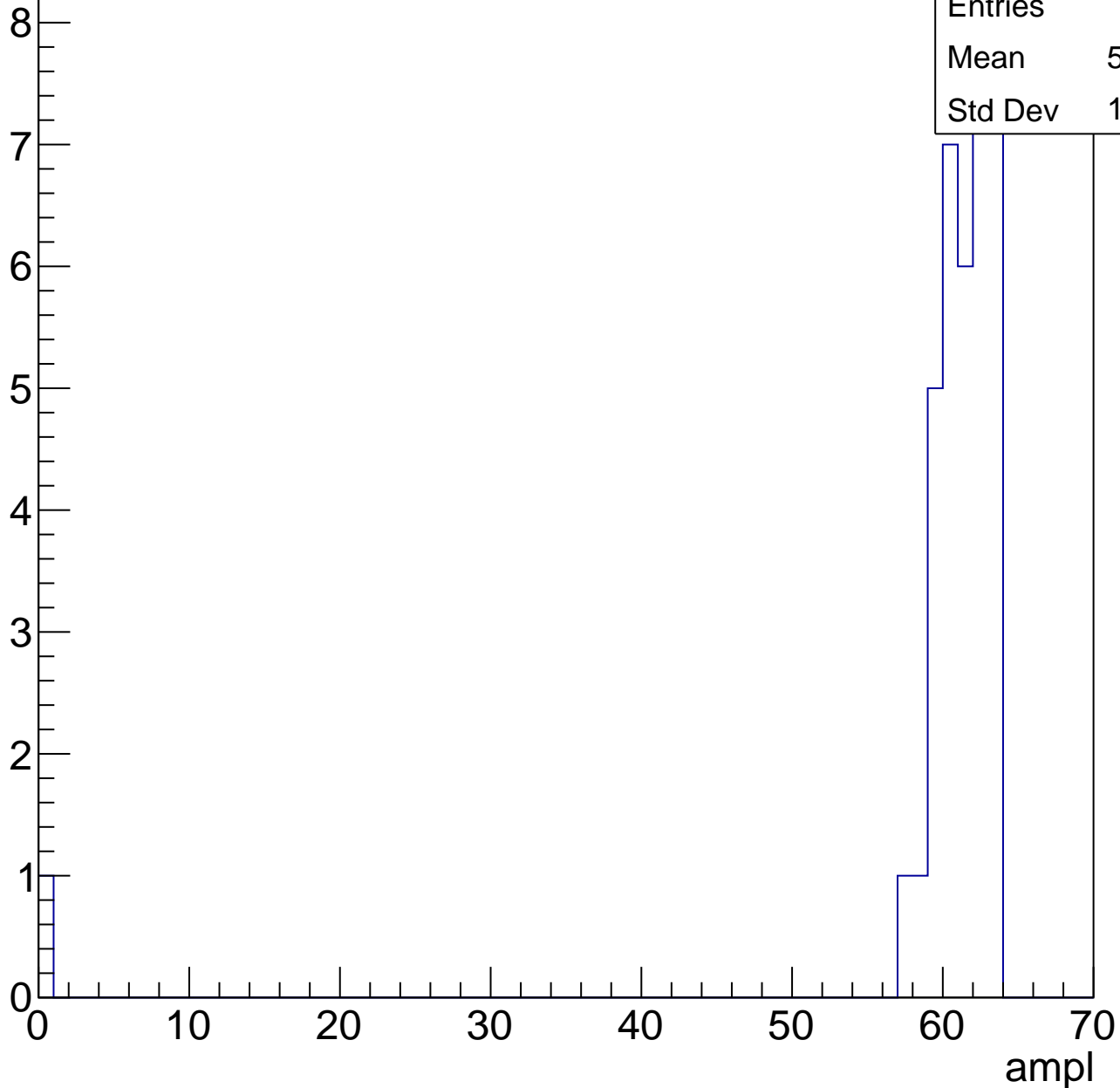
Entries	60
Mean	56.5
Std Dev	2.802



# B1L000S, U2-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

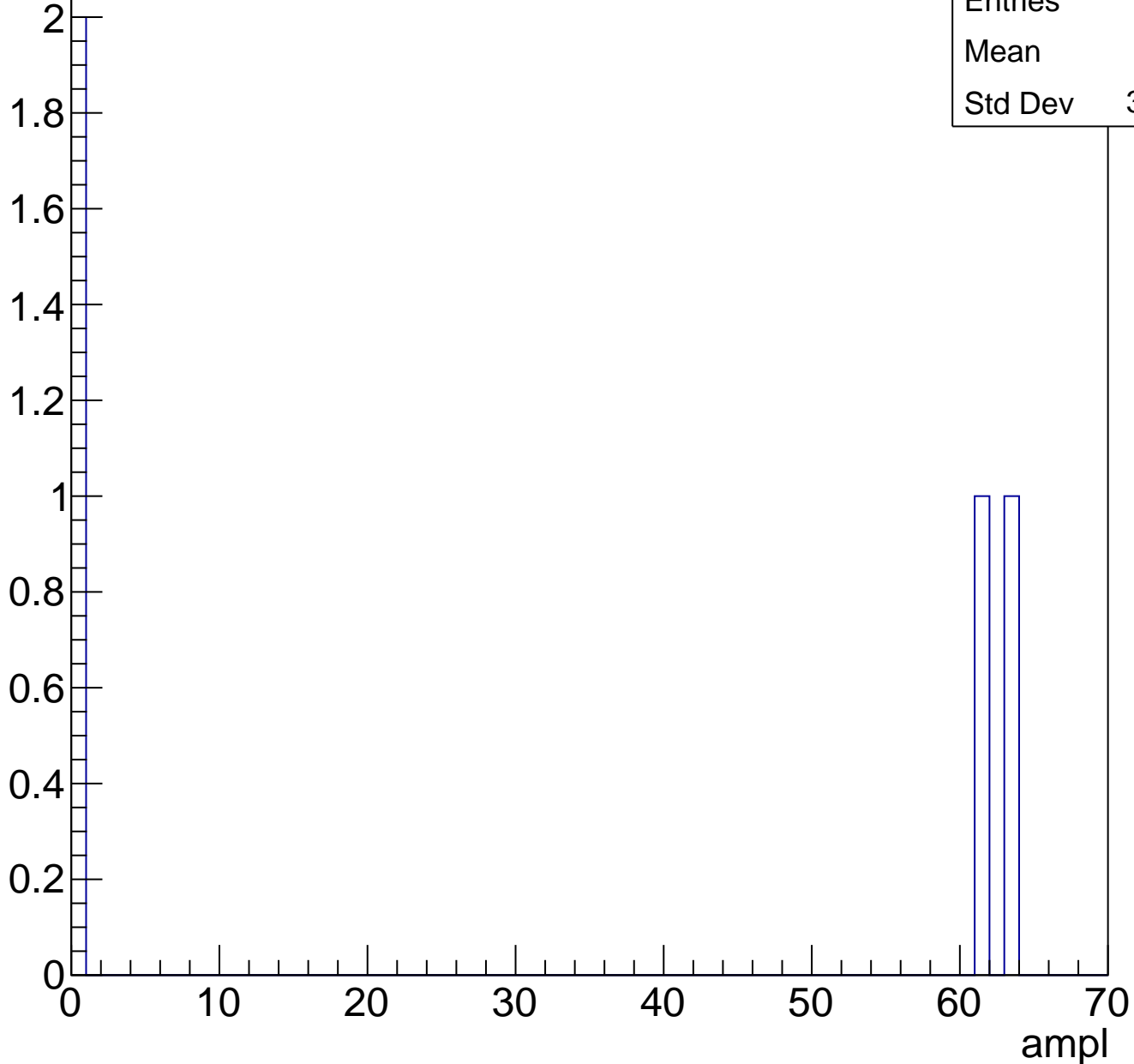




# B1L000S, U2-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	31
Std Dev	31.01

# B1L000S, U2-ch103, adc0

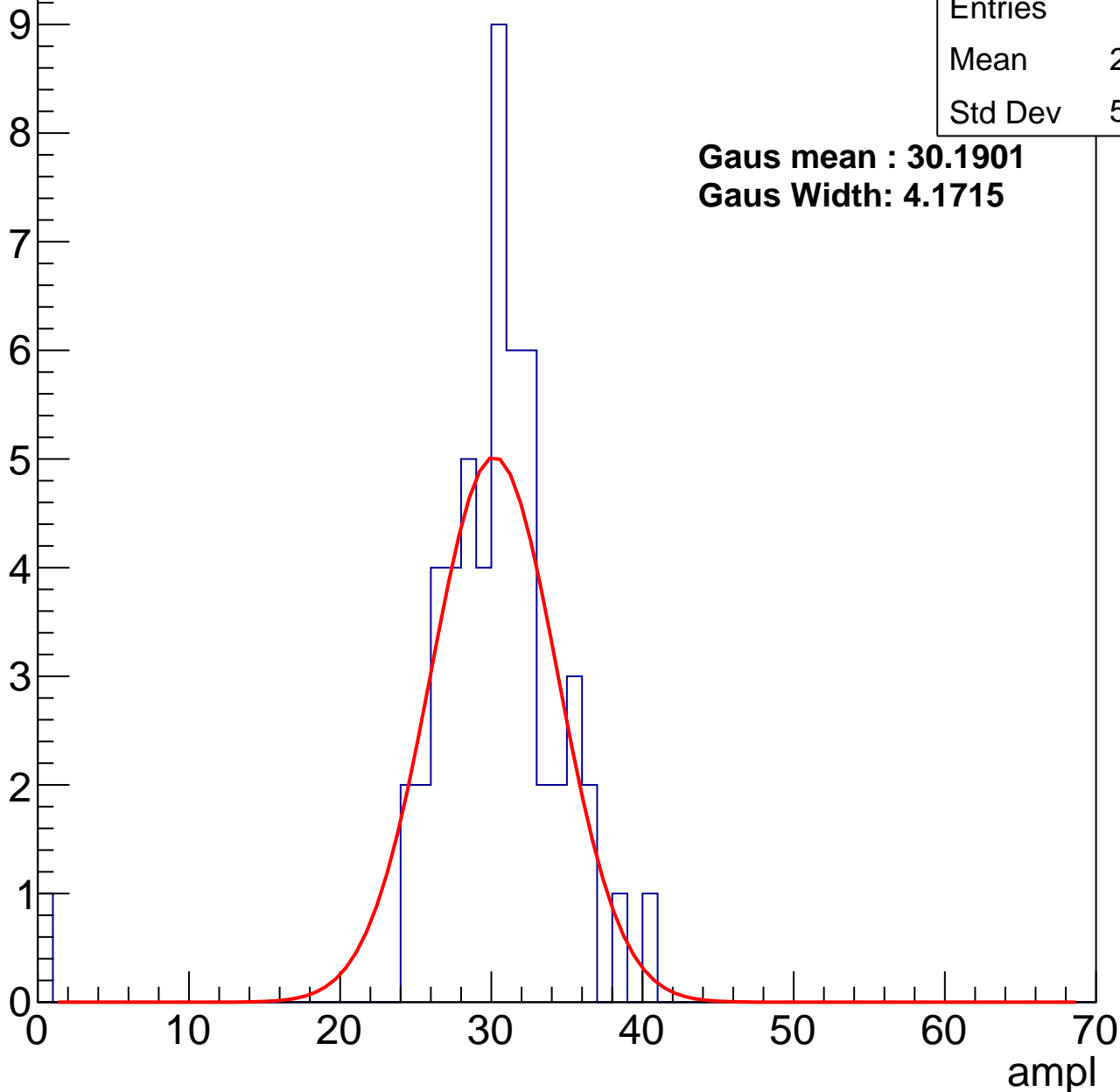
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	29.69
Std Dev	5.326

**Gaus mean : 30.1901**

**Gaus Width: 4.1715**



# B1L000S, U2-ch103, adc1

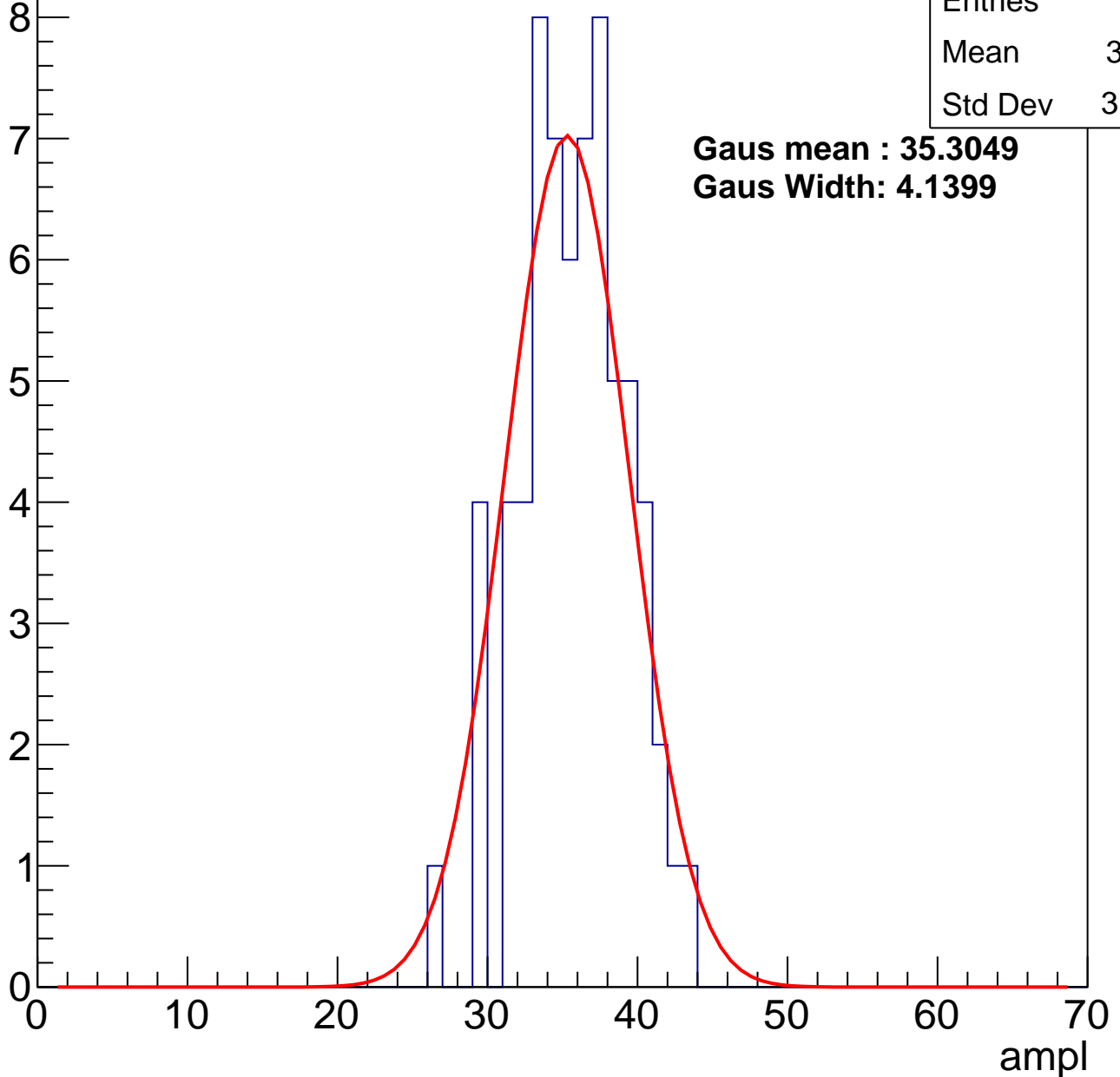
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.31
Std Dev	3.469

**Gaus mean : 35.3049**

**Gaus Width: 4.1399**



# B1L000S, U2-ch103, adc2

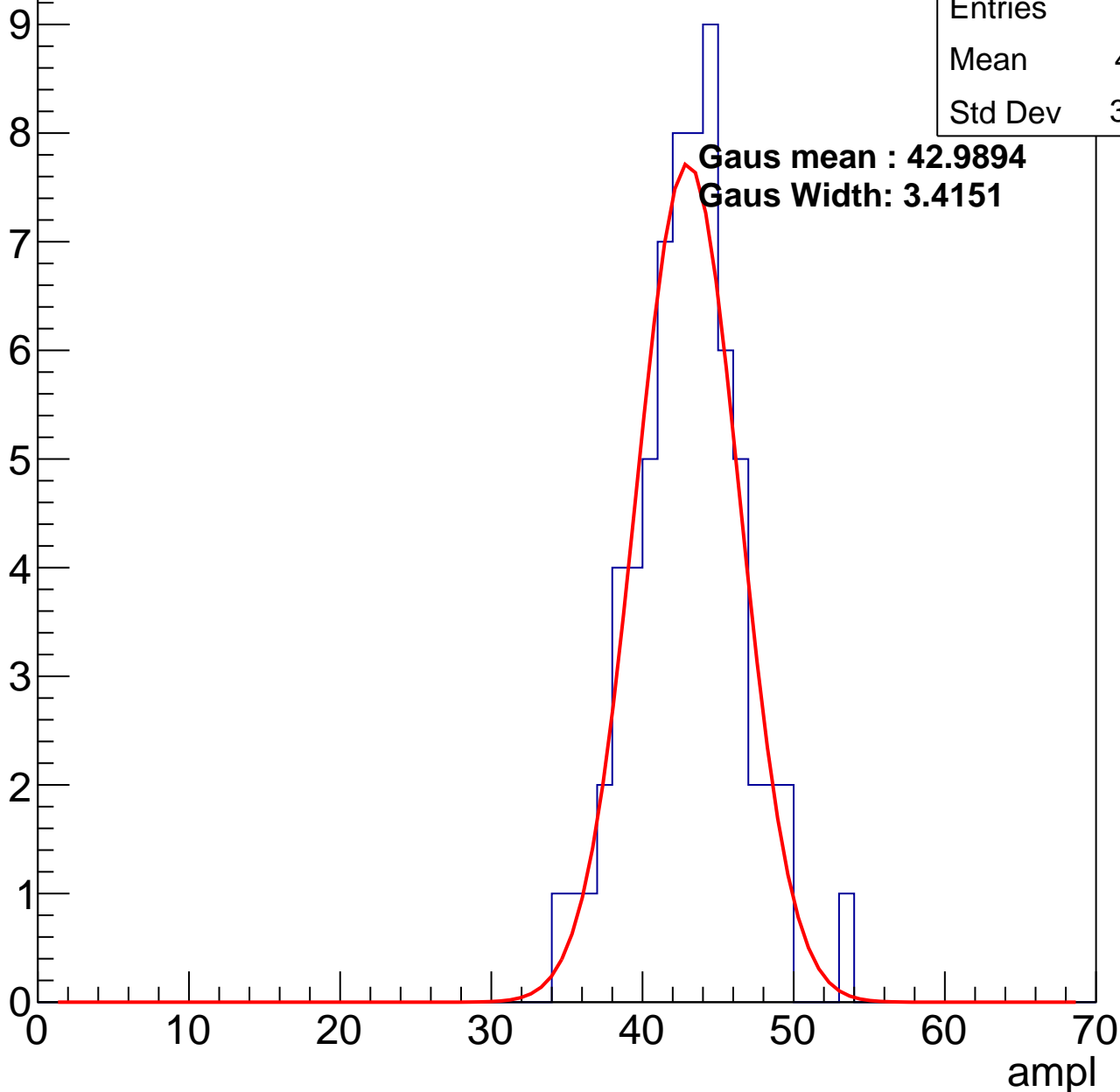
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	42.51
Std Dev	3.487

**Gaus mean : 42.9894**

**Gaus Width: 3.4151**

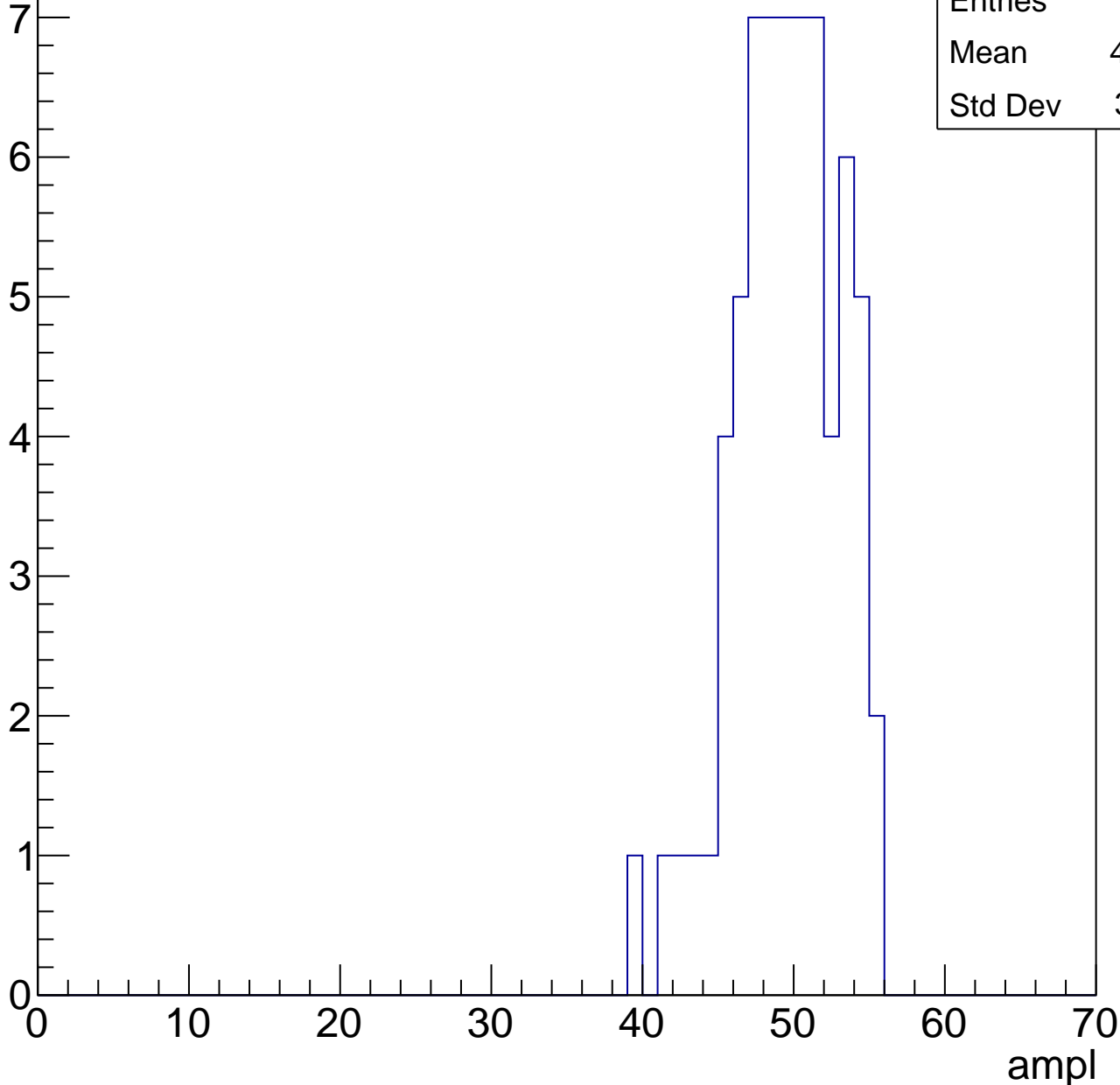


# B1L000S, U2-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	49.09
Std Dev	3.441

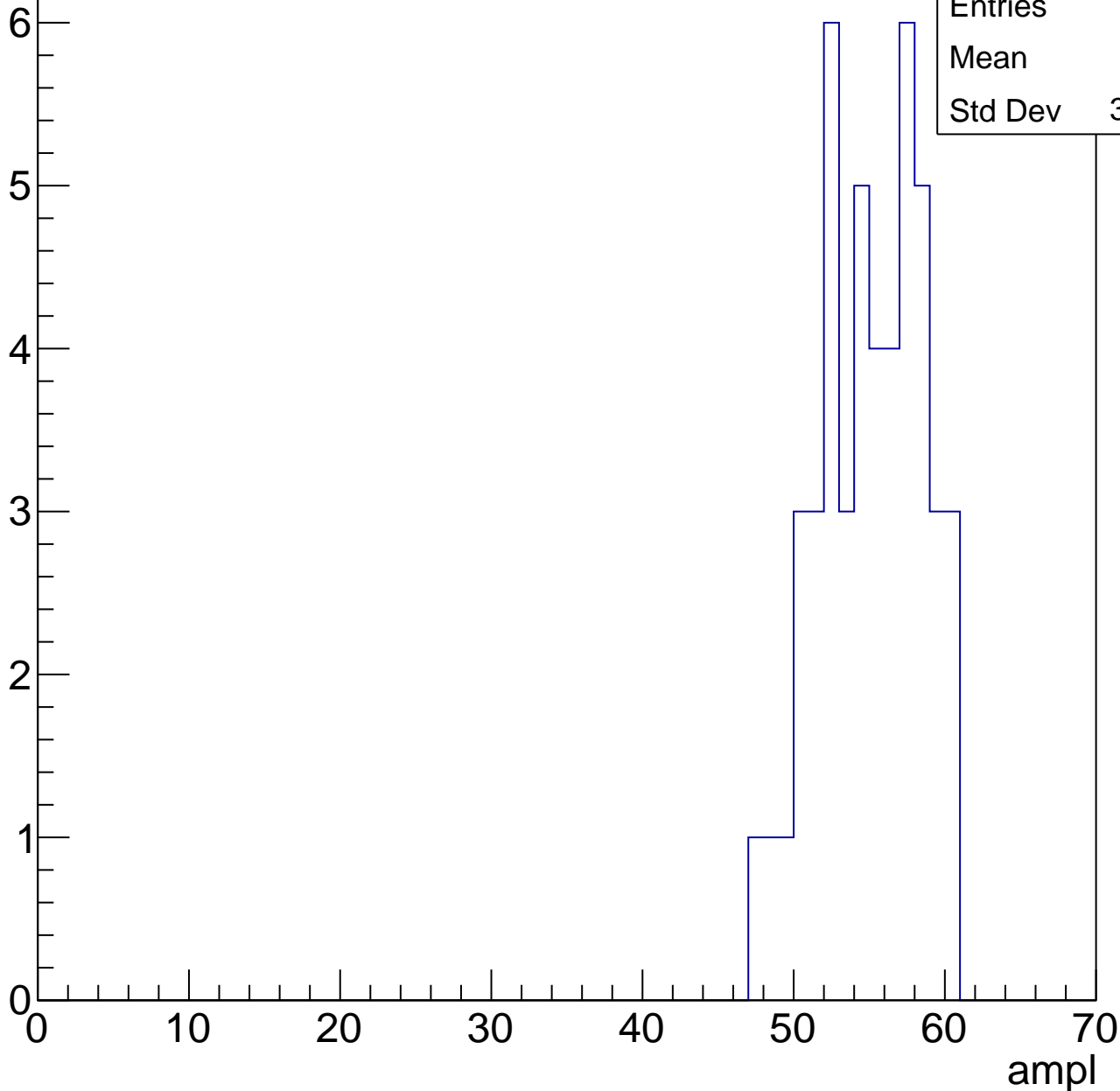


# B1L000S, U2-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	54.6
Std Dev	3.328

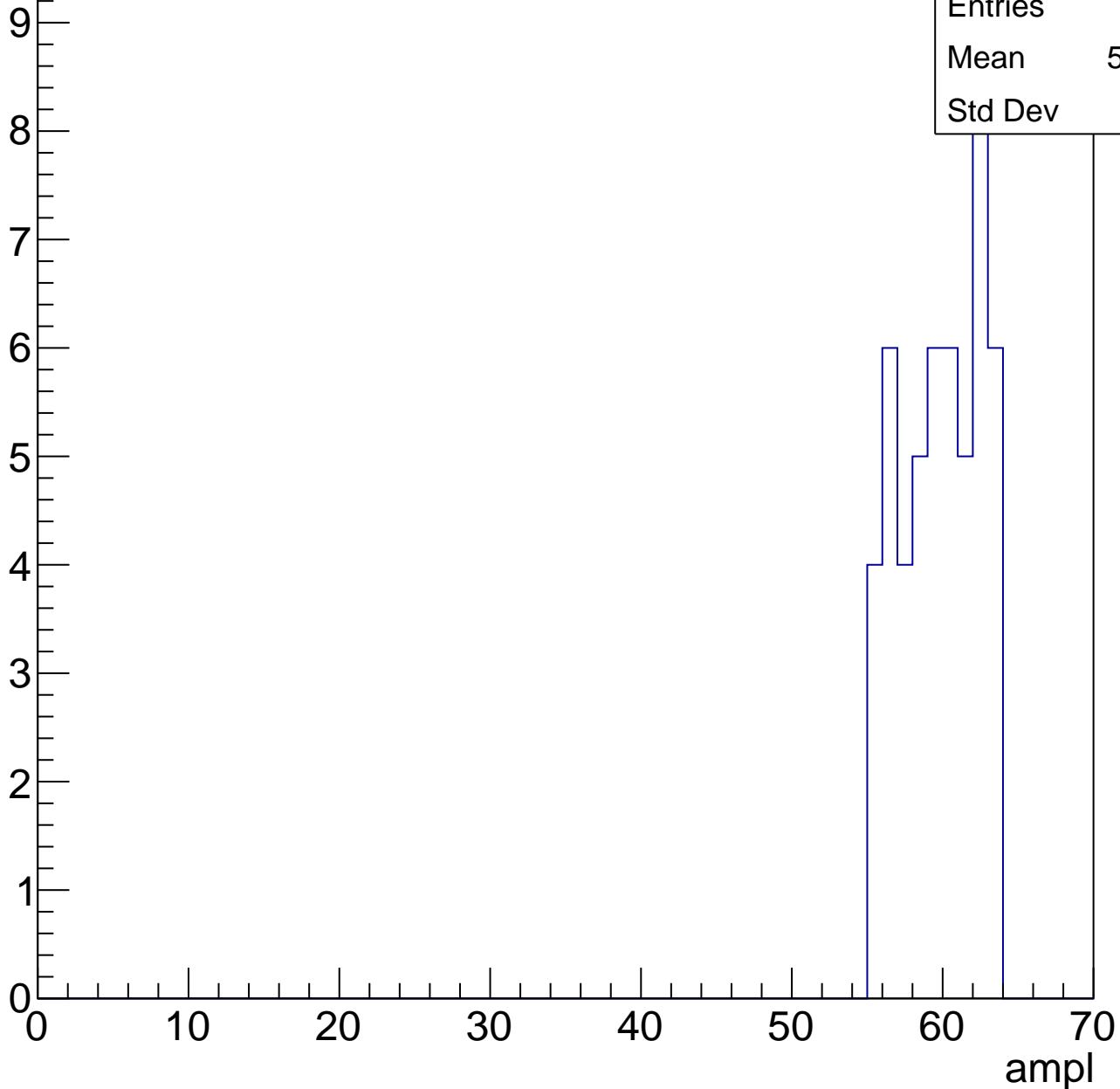


# B1L000S, U2-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	59.39
Std Dev	2.56

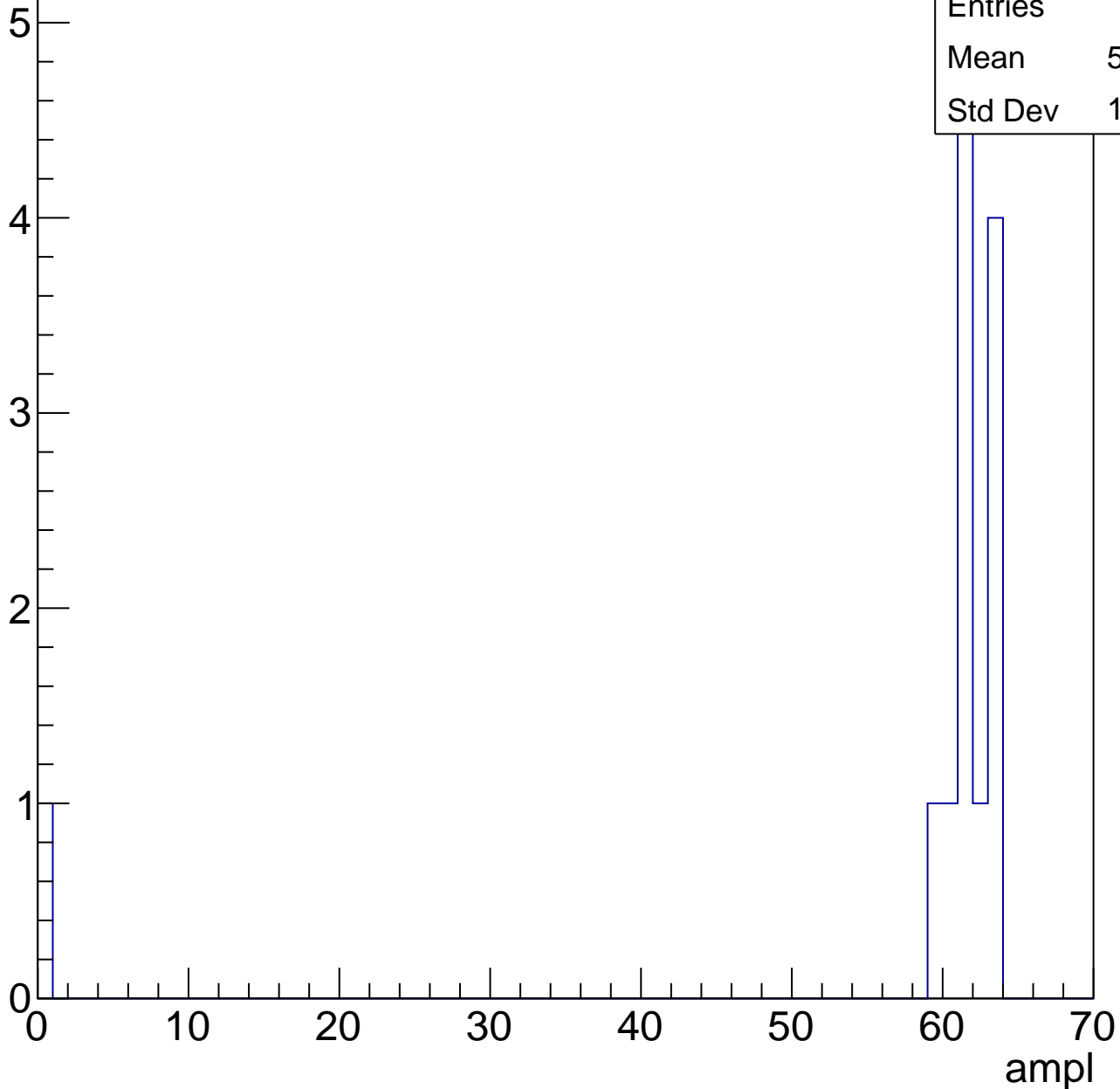


# B1L000S, U2-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	56.77
Std Dev	16.43





# B1L000S, U2-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch104, adc0

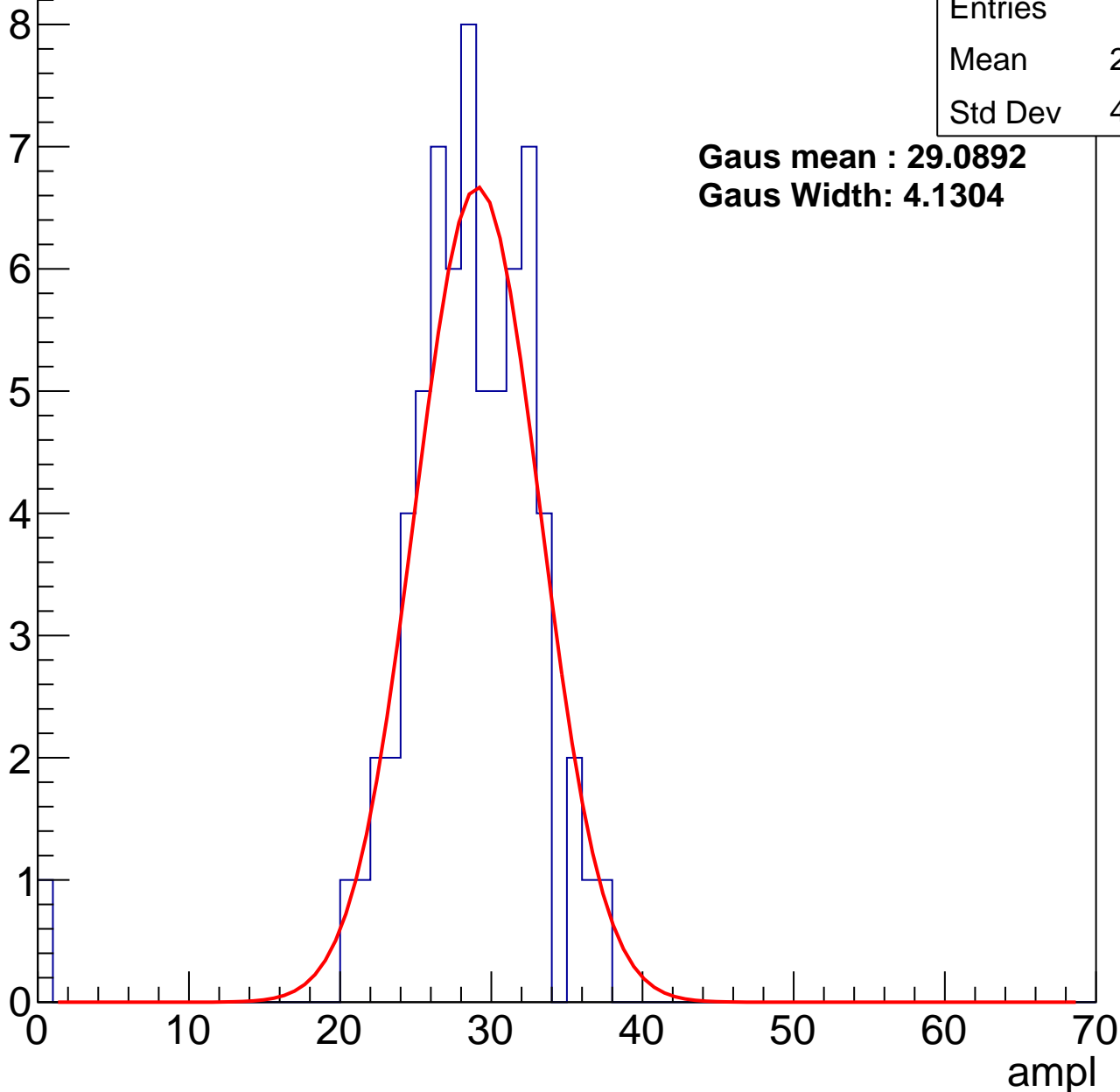
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	27.94
Std Dev	4.994

**Gaus mean : 29.0892**

**Gaus Width: 4.1304**



# B1L000S, U2-ch104, adc1

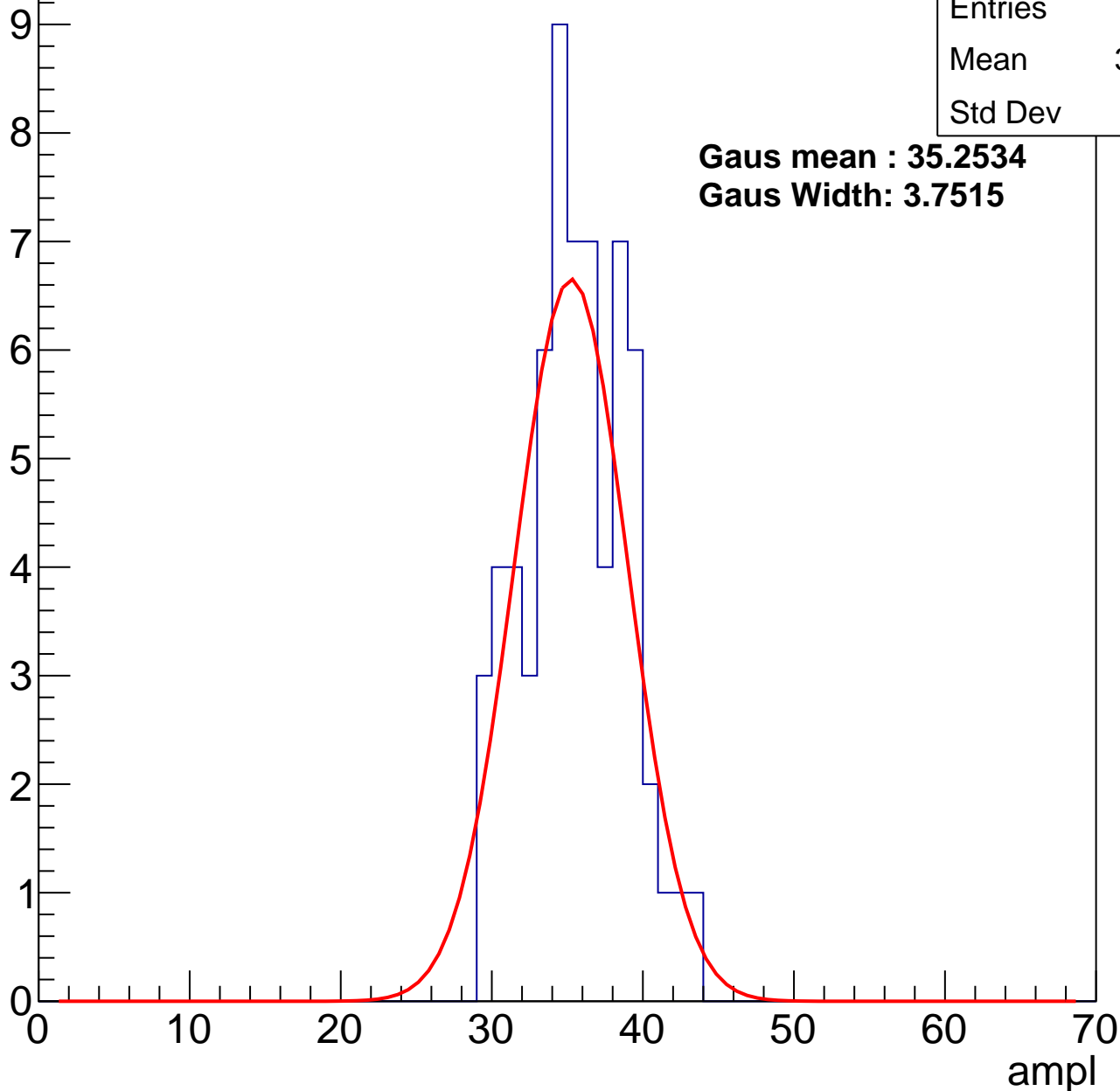
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.11
Std Dev	3.31

**Gaus mean : 35.2534**

**Gaus Width: 3.7515**



# B1L000S, U2-ch104, adc2

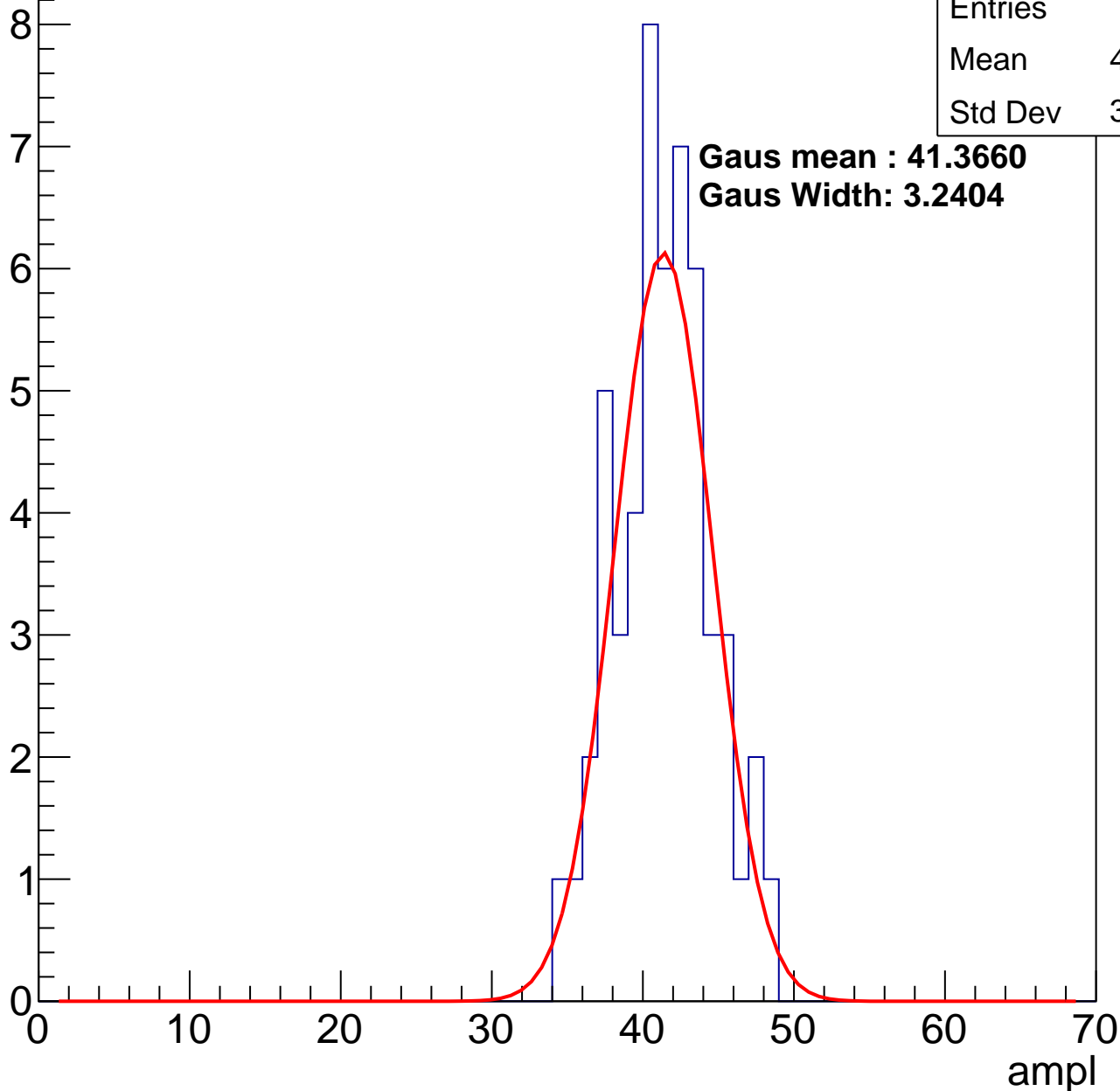
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	40.92
Std Dev	3.125

**Gaus mean : 41.3660**

**Gaus Width: 3.2404**

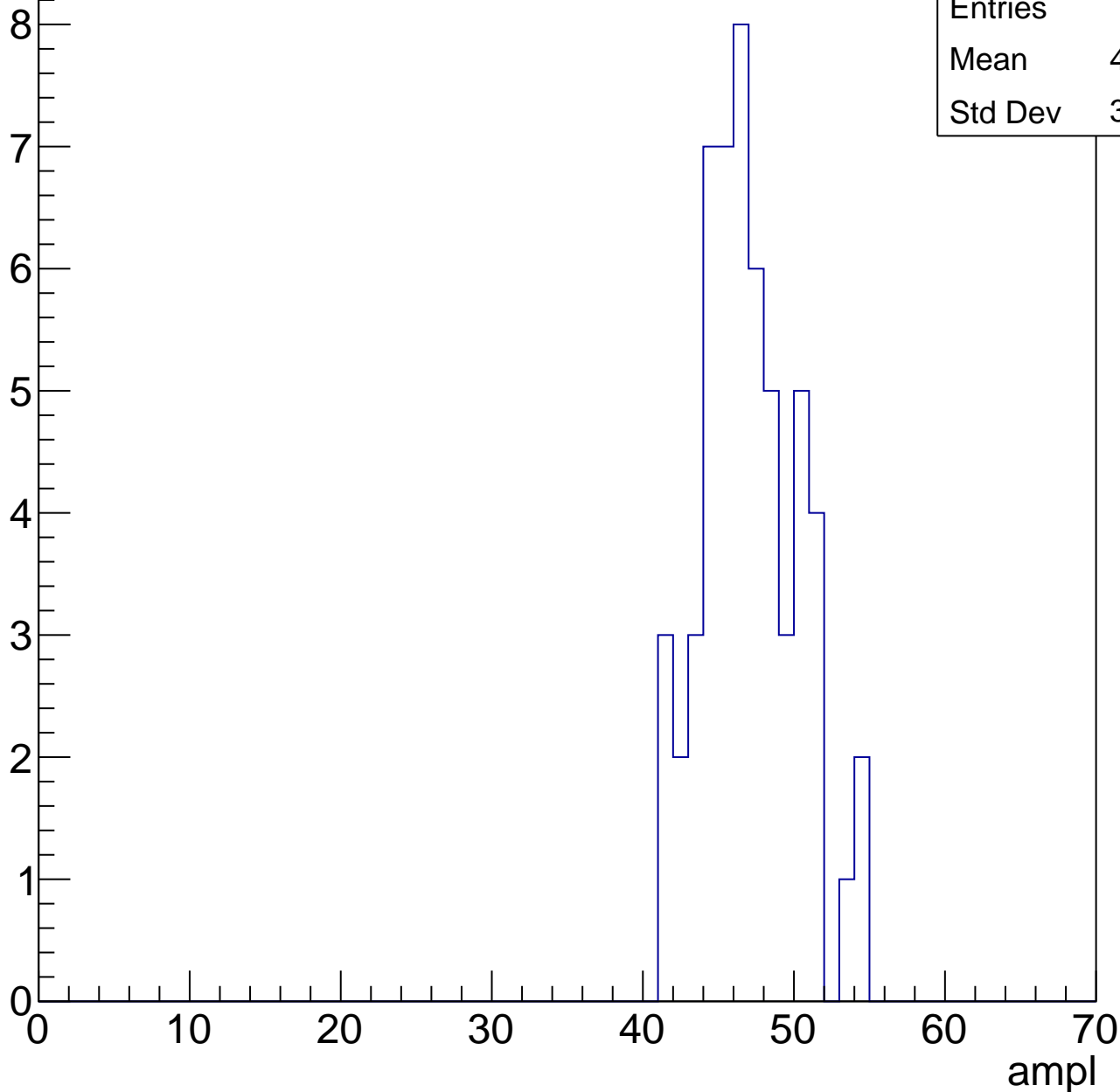


# B1L000S, U2-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	46.62
Std Dev	3.154

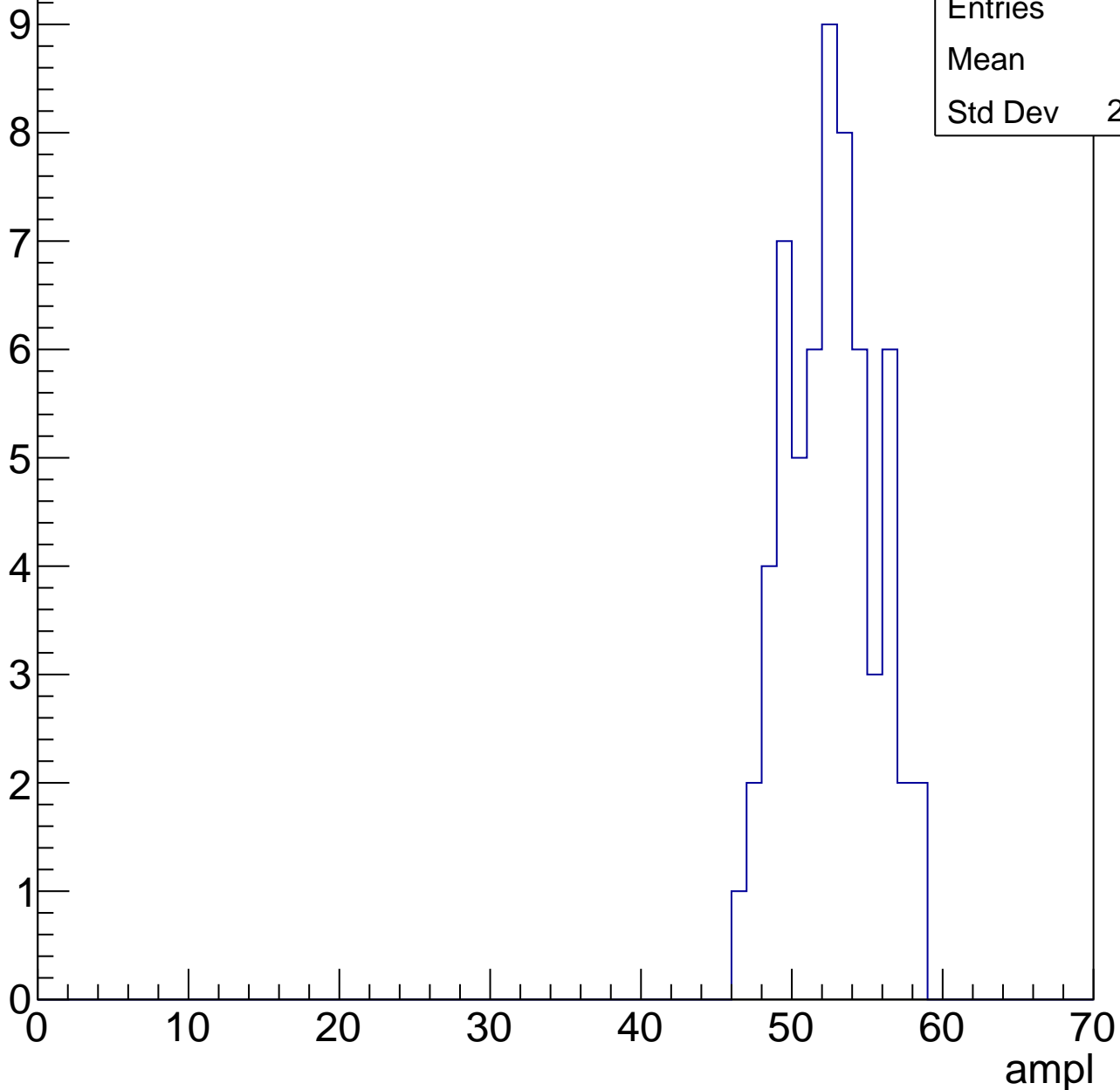


# B1L000S, U2-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	52.1
Std Dev	2.907

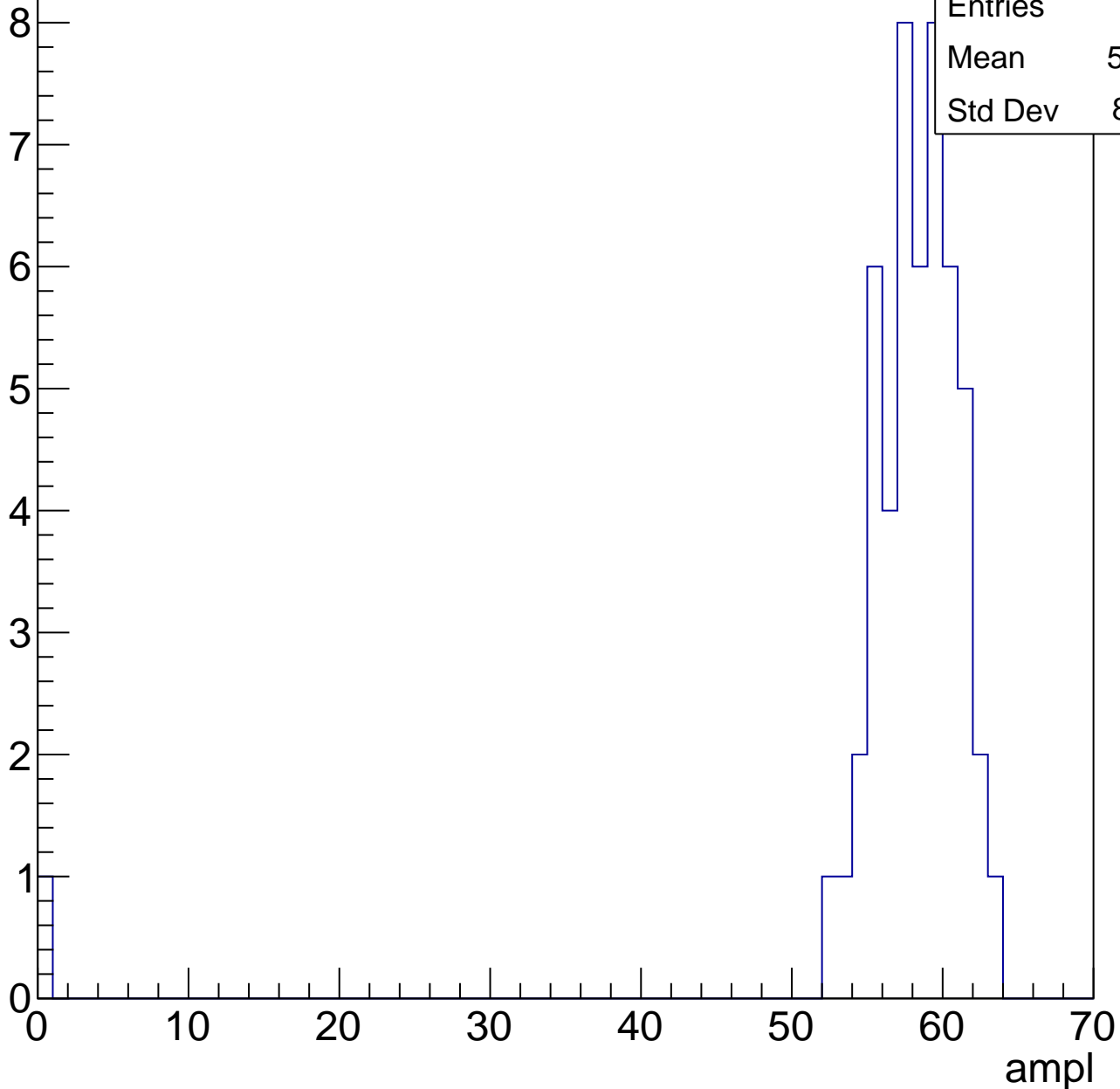


# B1L000S, U2-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	56.76
Std Dev	8.391

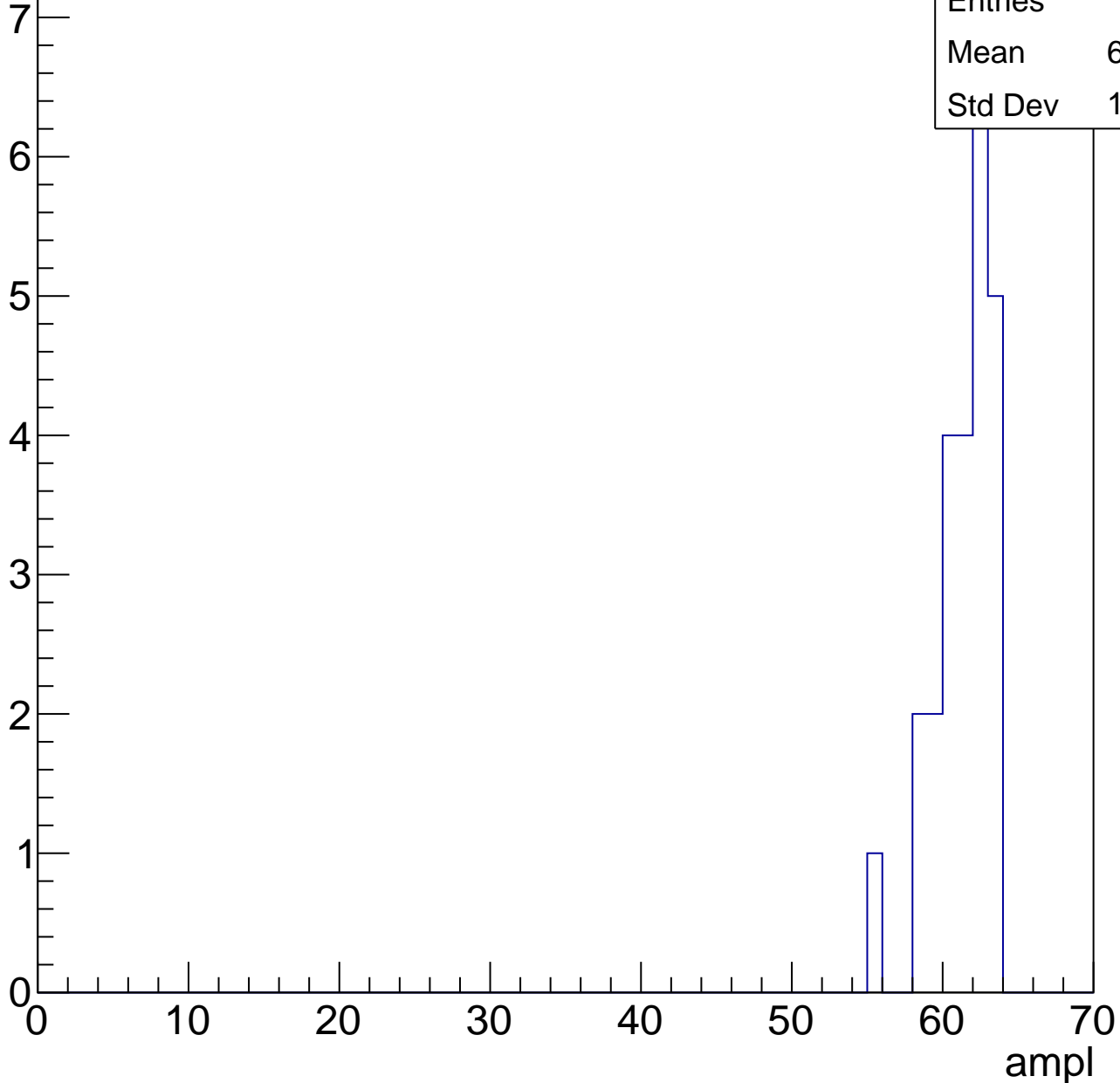


# B1L000S, U2-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	60.88
Std Dev	1.925

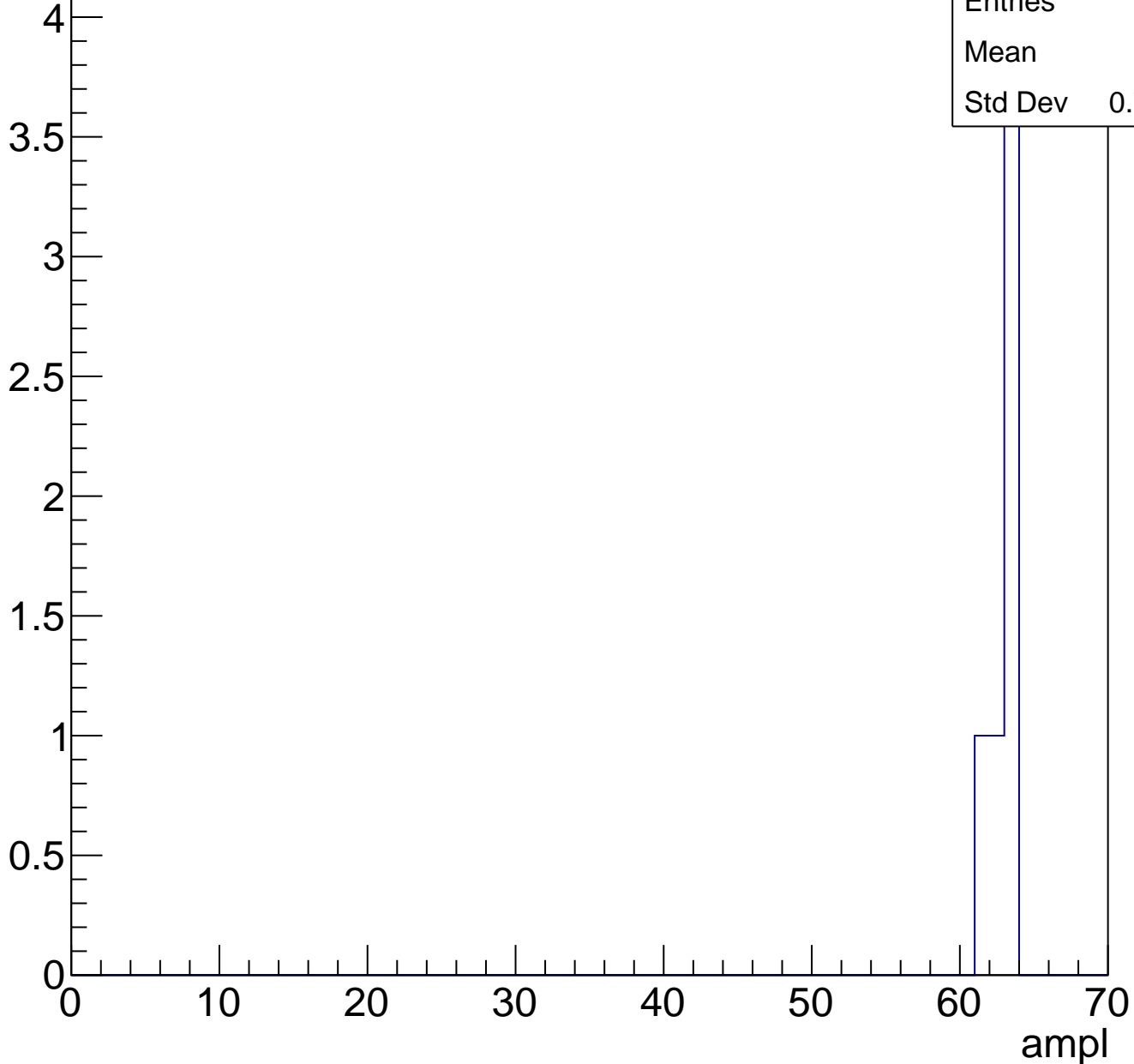




# B1L000S, U2-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch105, adc0

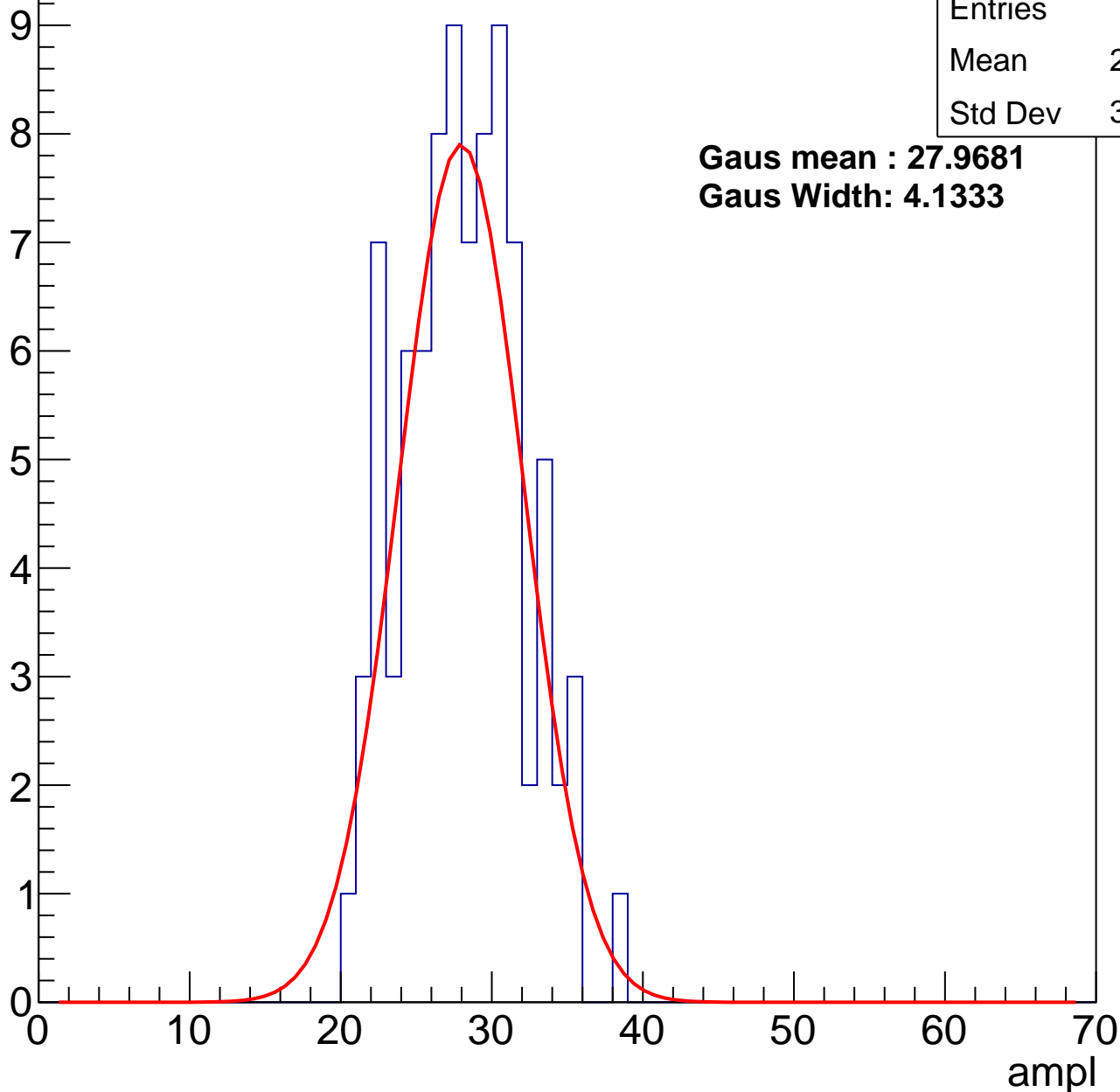
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	87
Mean	27.66
Std Dev	3.868

**Gaus mean : 27.9681**

**Gaus Width: 4.1333**



# B1L000S, U2-ch105, adc1

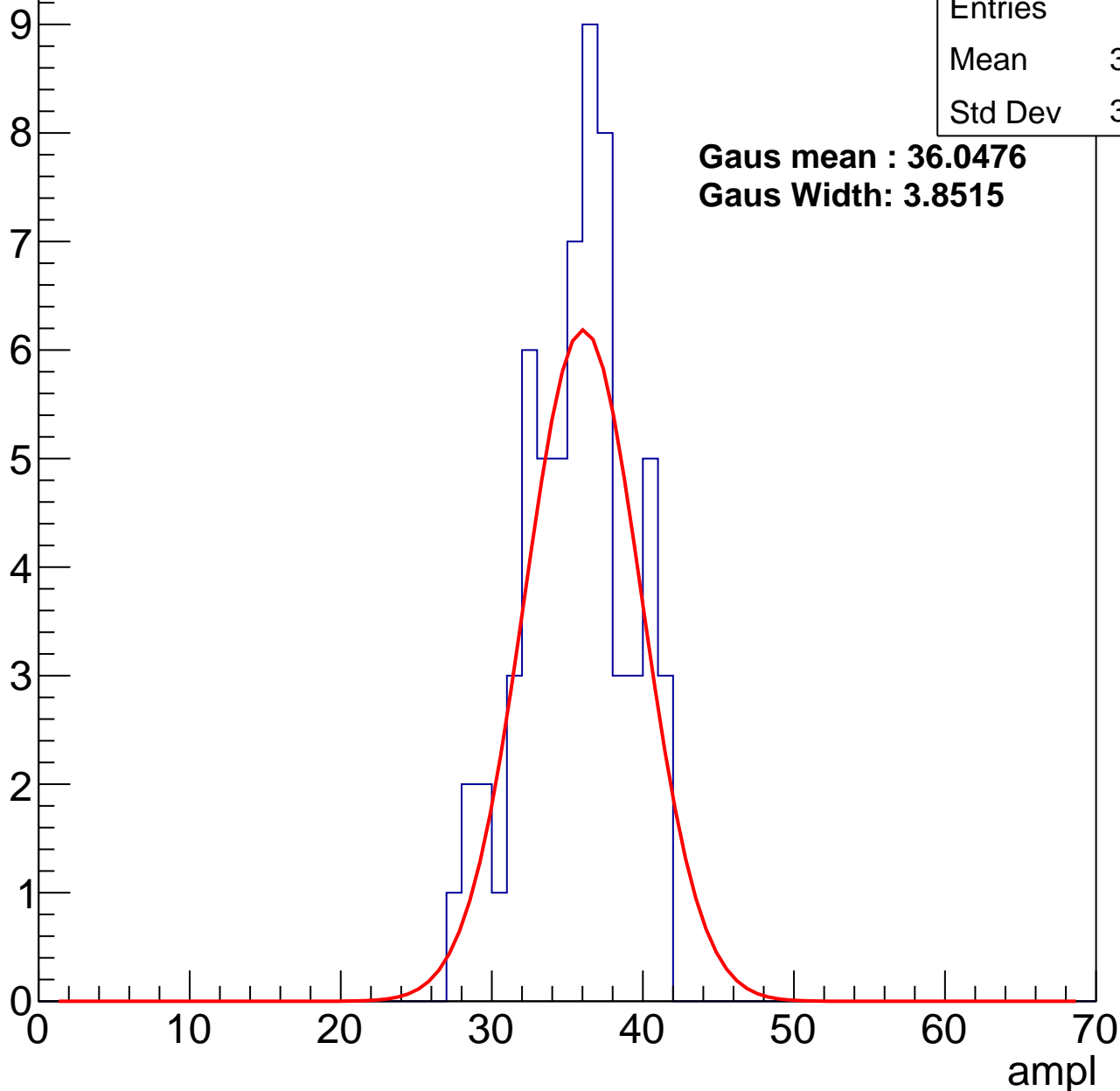
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.08
Std Dev	3.415

**Gaus mean : 36.0476**

**Gaus Width: 3.8515**



# B1L000S, U2-ch105, adc2

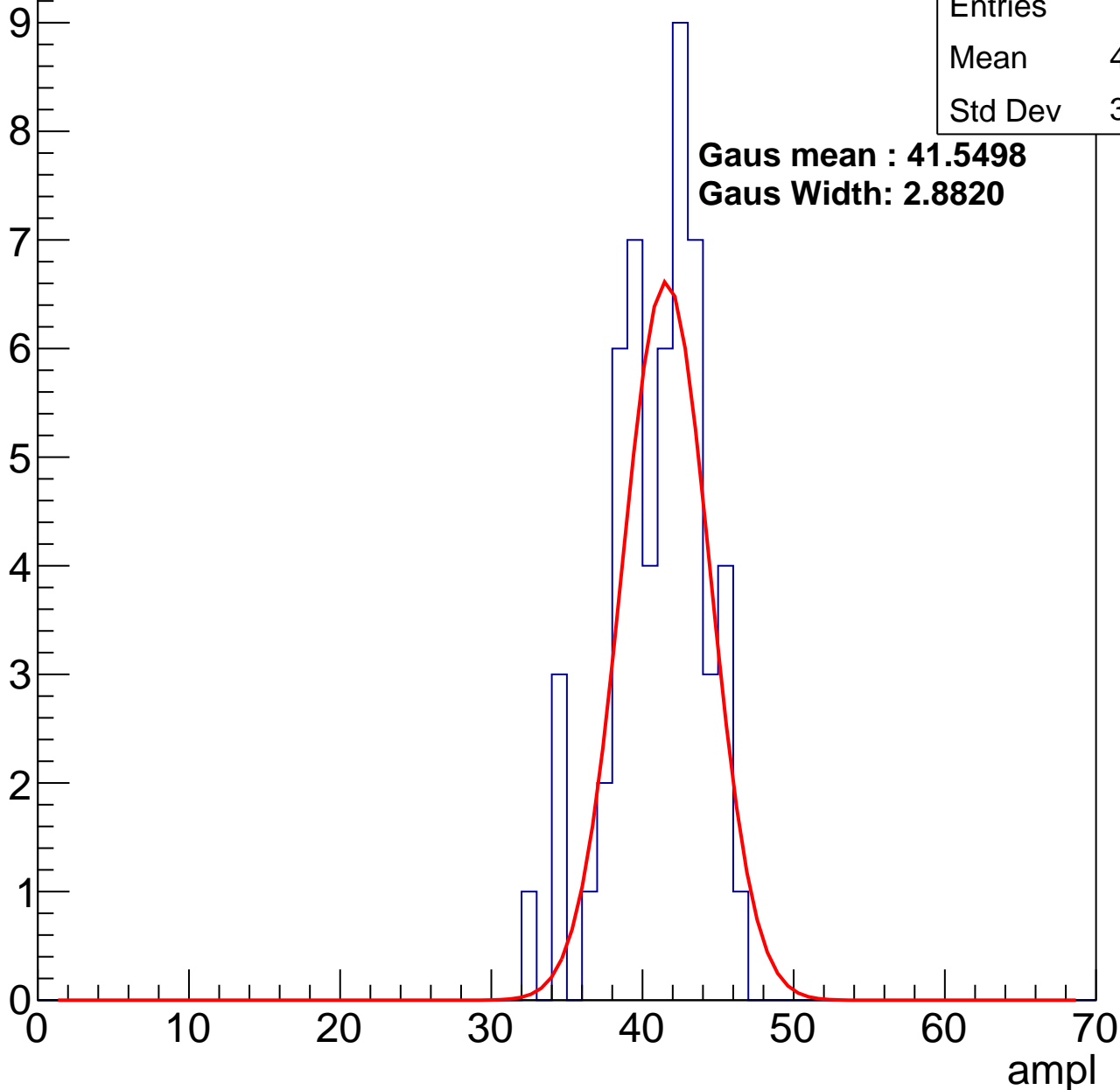
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	40.52
Std Dev	3.078

**Gaus mean : 41.5498**

**Gaus Width: 2.8820**

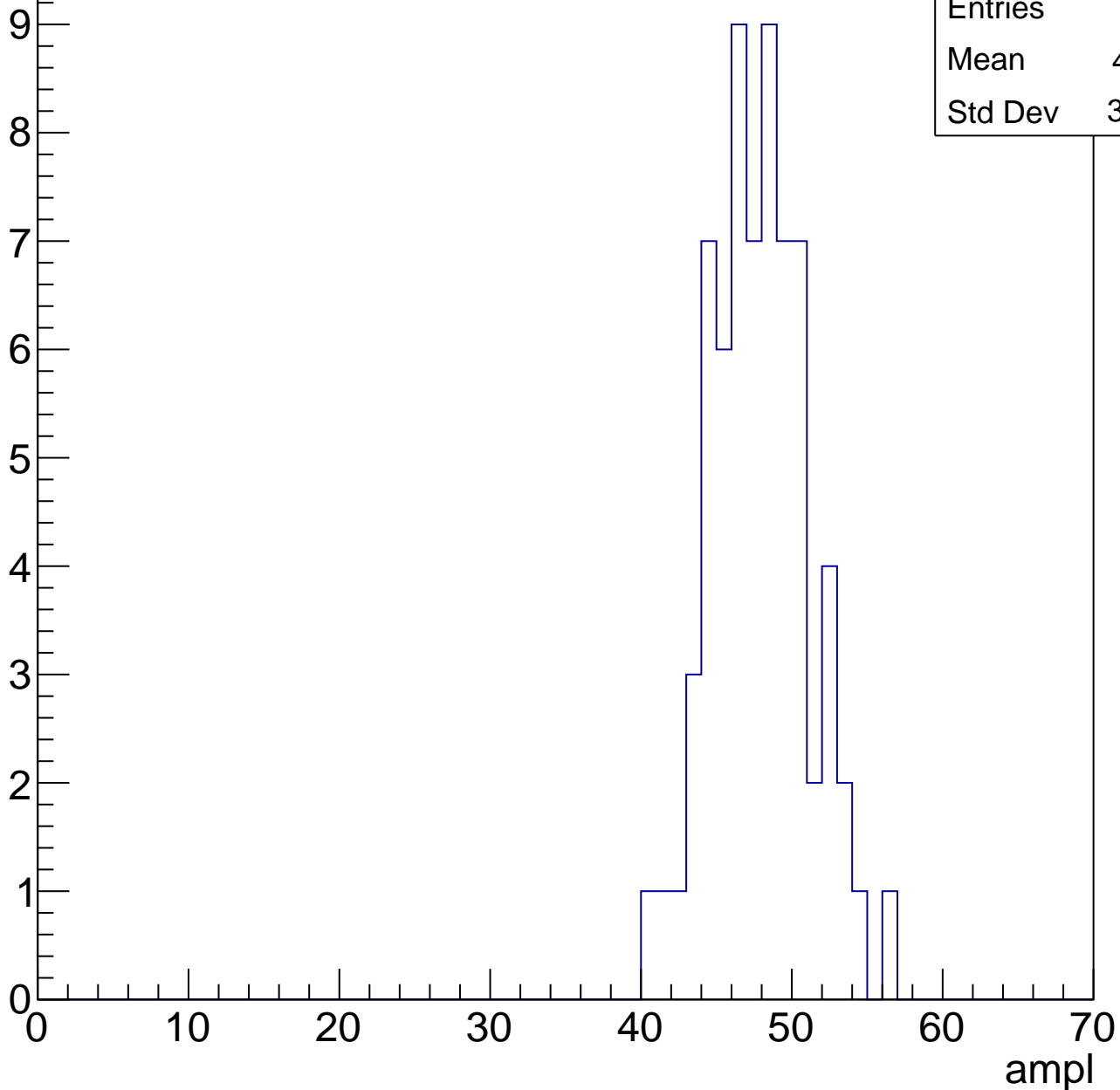


# B1L000S, U2-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	47.41
Std Dev	3.154

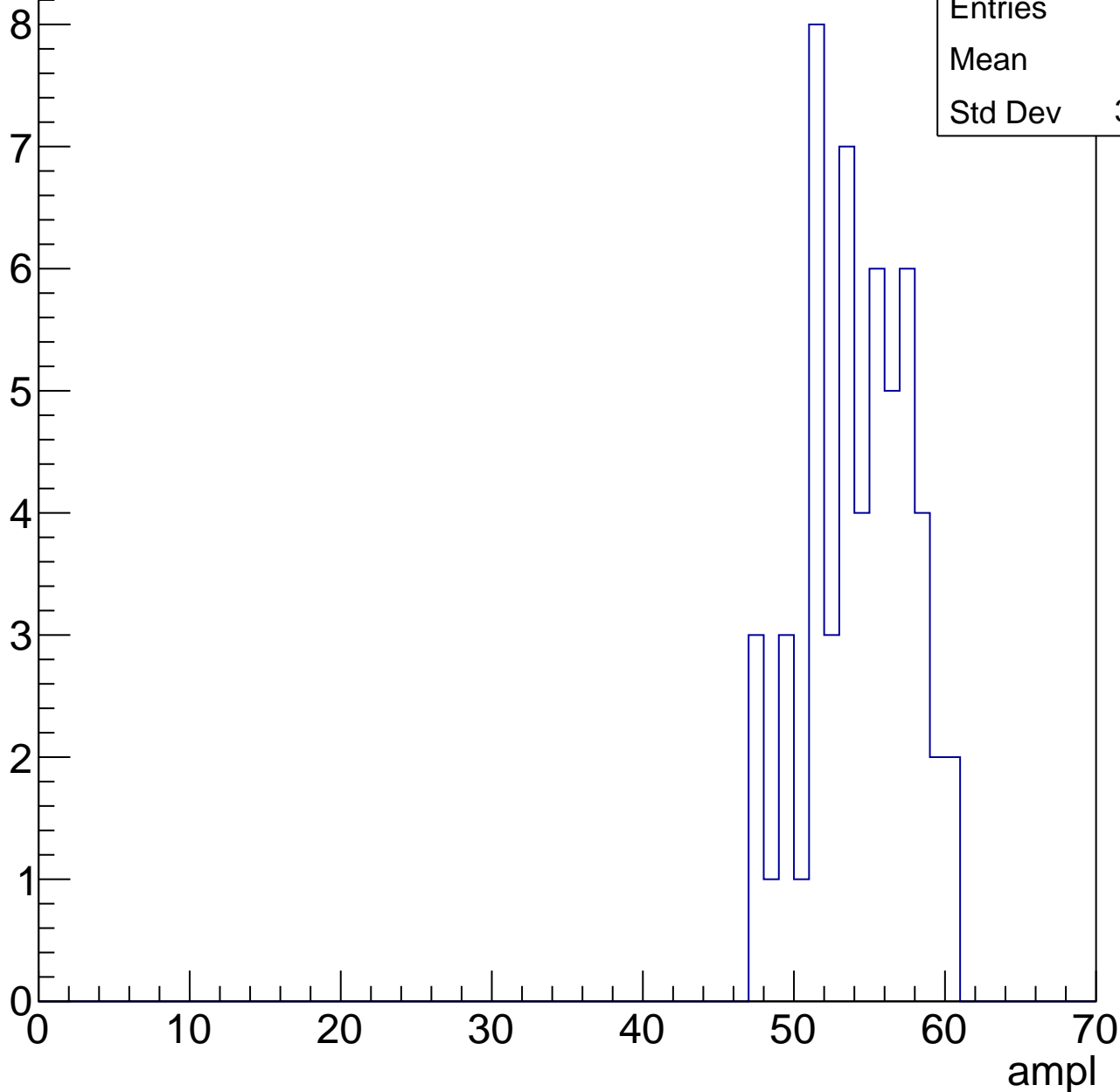


# B1L000S, U2-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	53.8
Std Dev	3.381

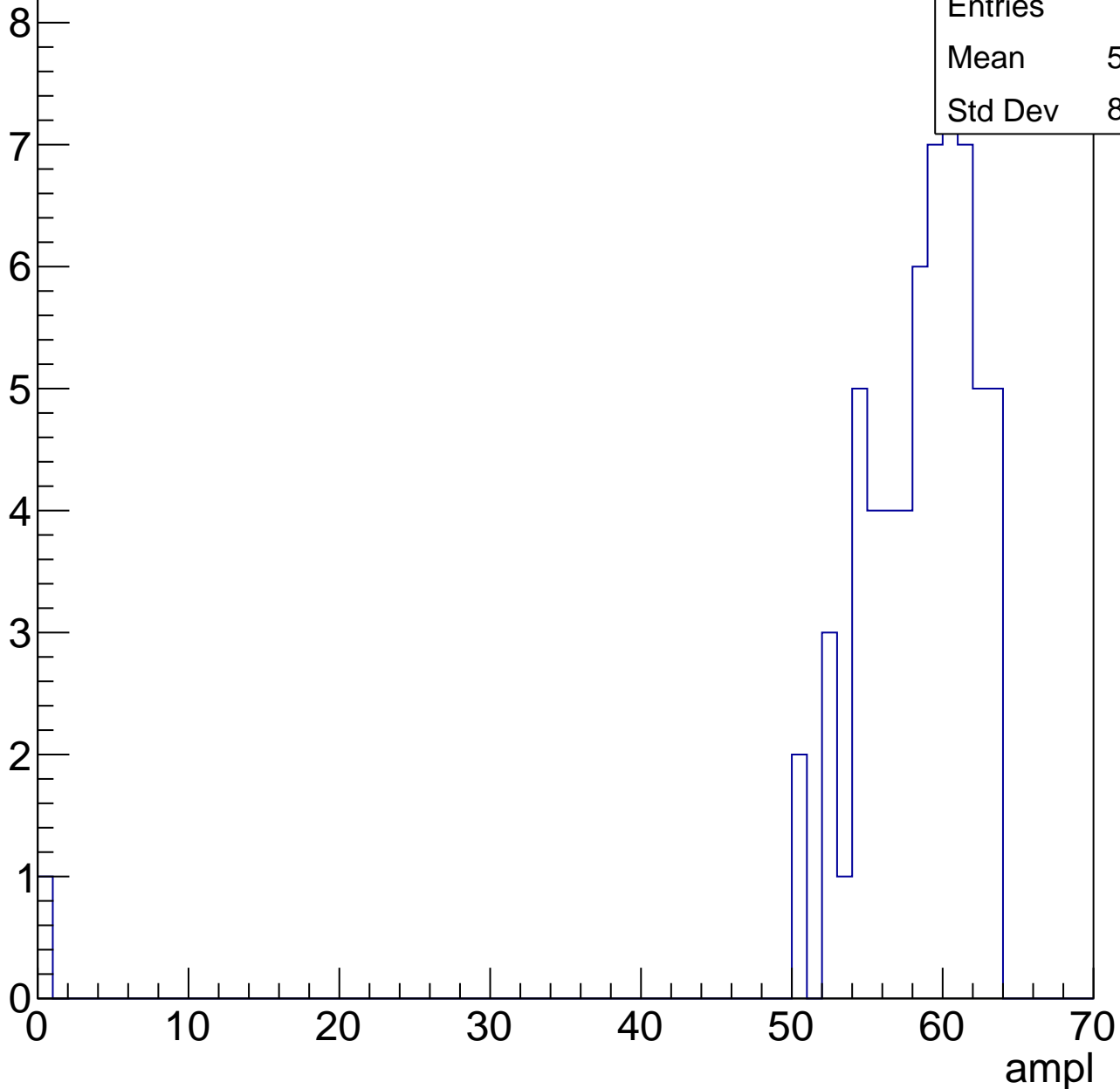


# B1L000S, U2-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	57.16
Std Dev	8.057

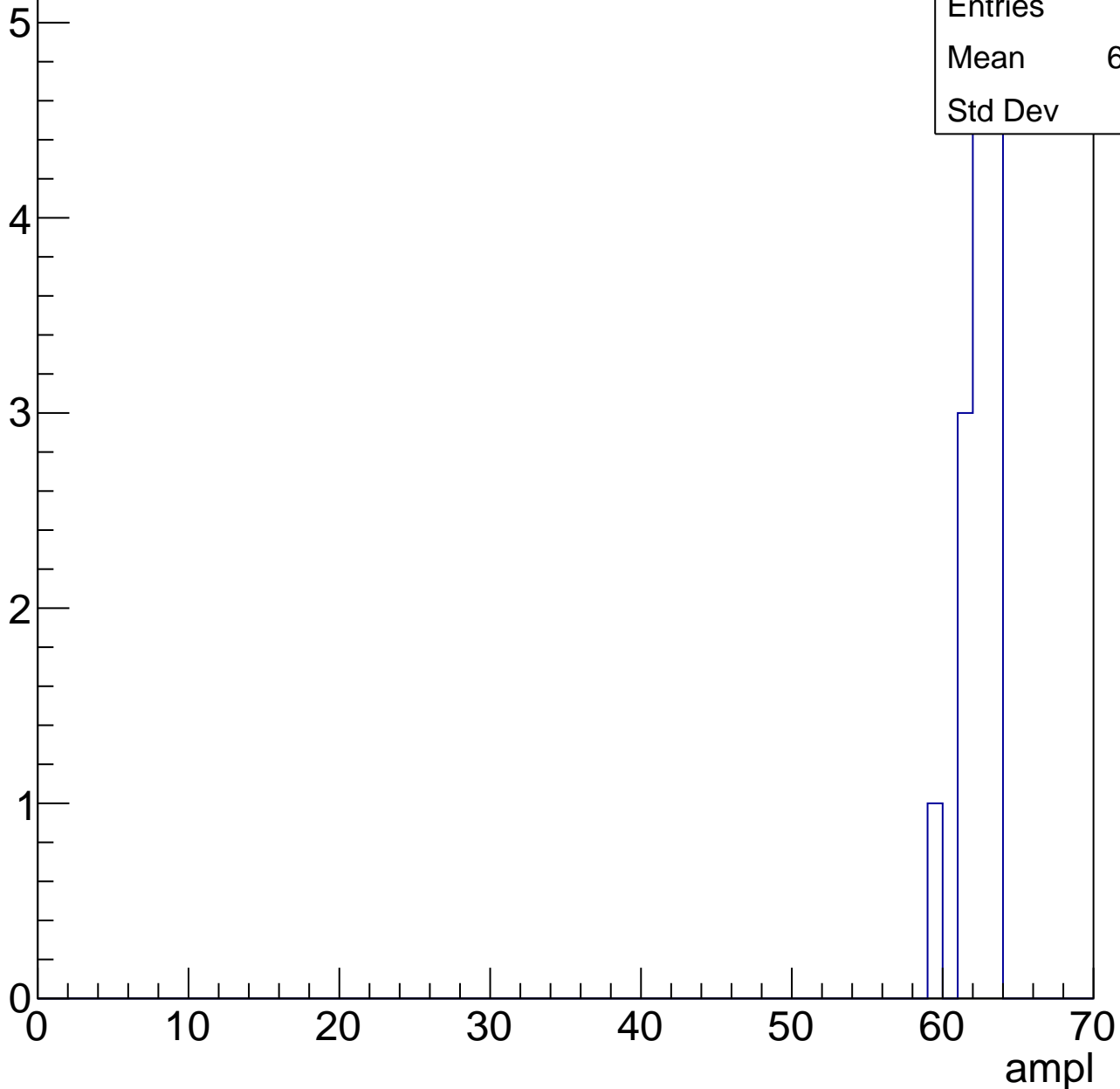


# B1L000S, U2-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.93
Std Dev	1.1





# B1L000S, U2-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U2-ch106, adc0

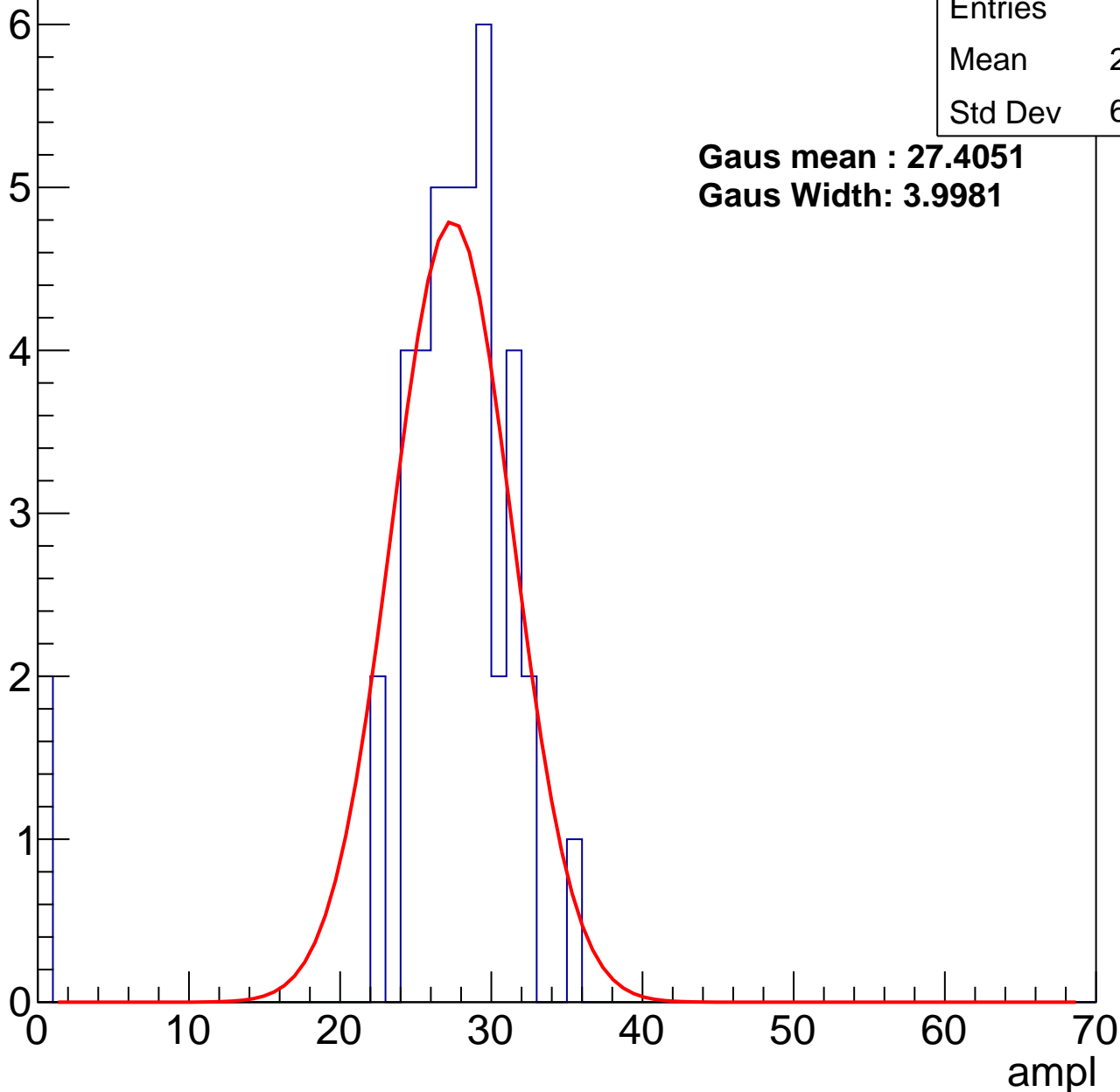
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	26.24
Std Dev	6.484

**Gaus mean : 27.4051**

**Gaus Width: 3.9981**



# B1L000S, U2-ch106, adc1

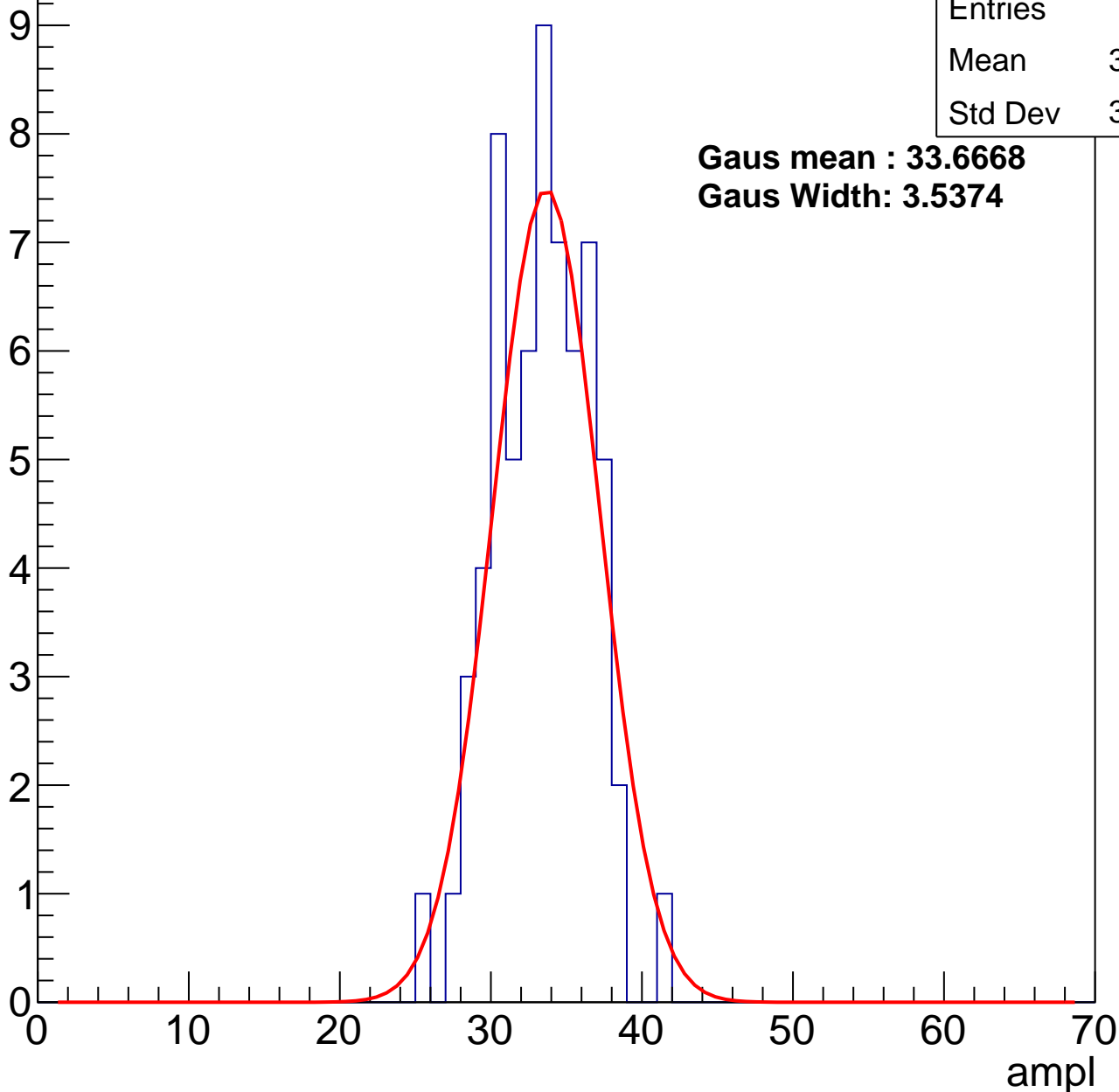
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	32.89
Std Dev	3.099

**Gaus mean : 33.6668**

**Gaus Width: 3.5374**



# B1L000S, U2-ch106, adc2

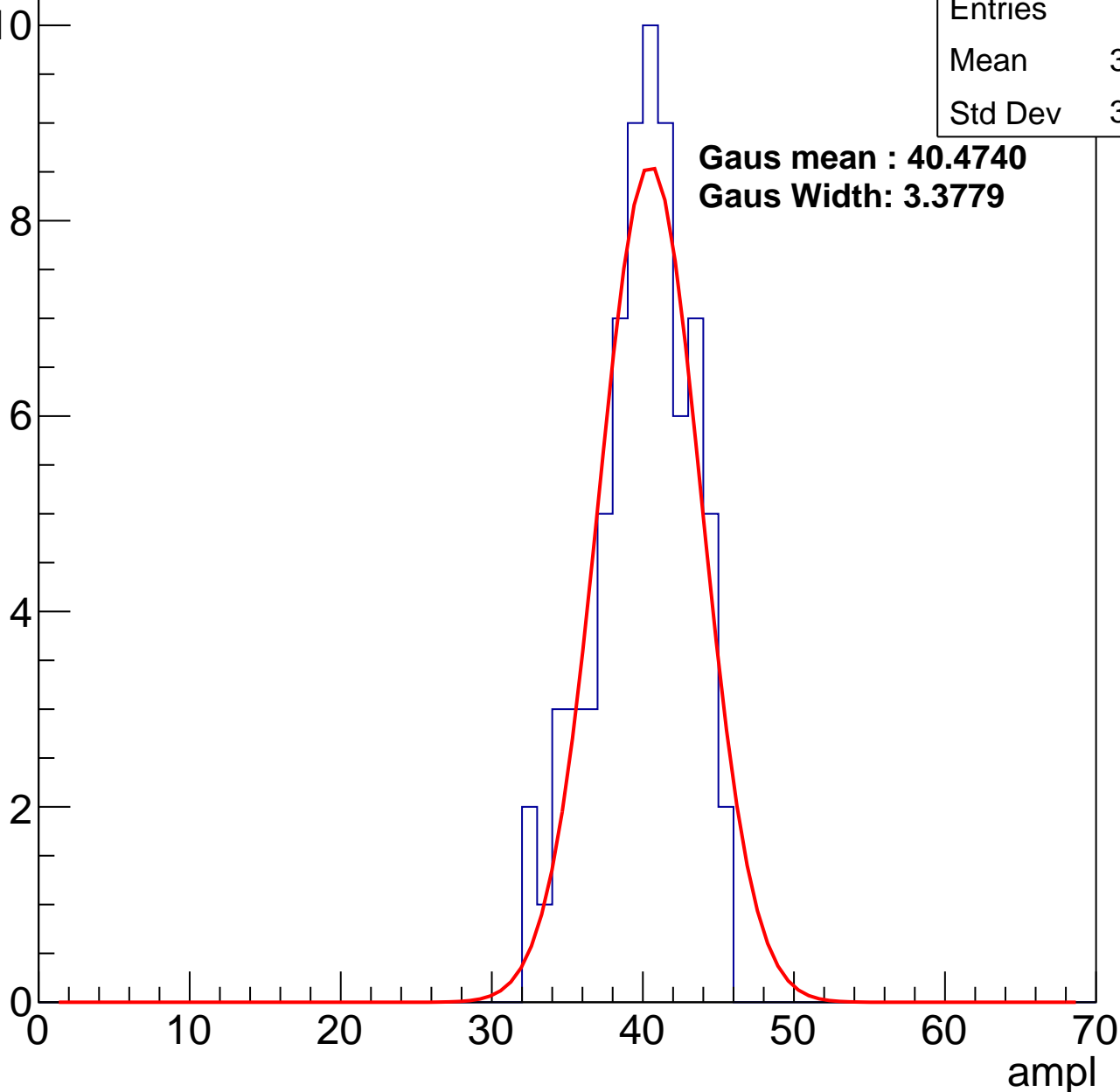
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	39.53
Std Dev	3.118

**Gaus mean : 40.4740**

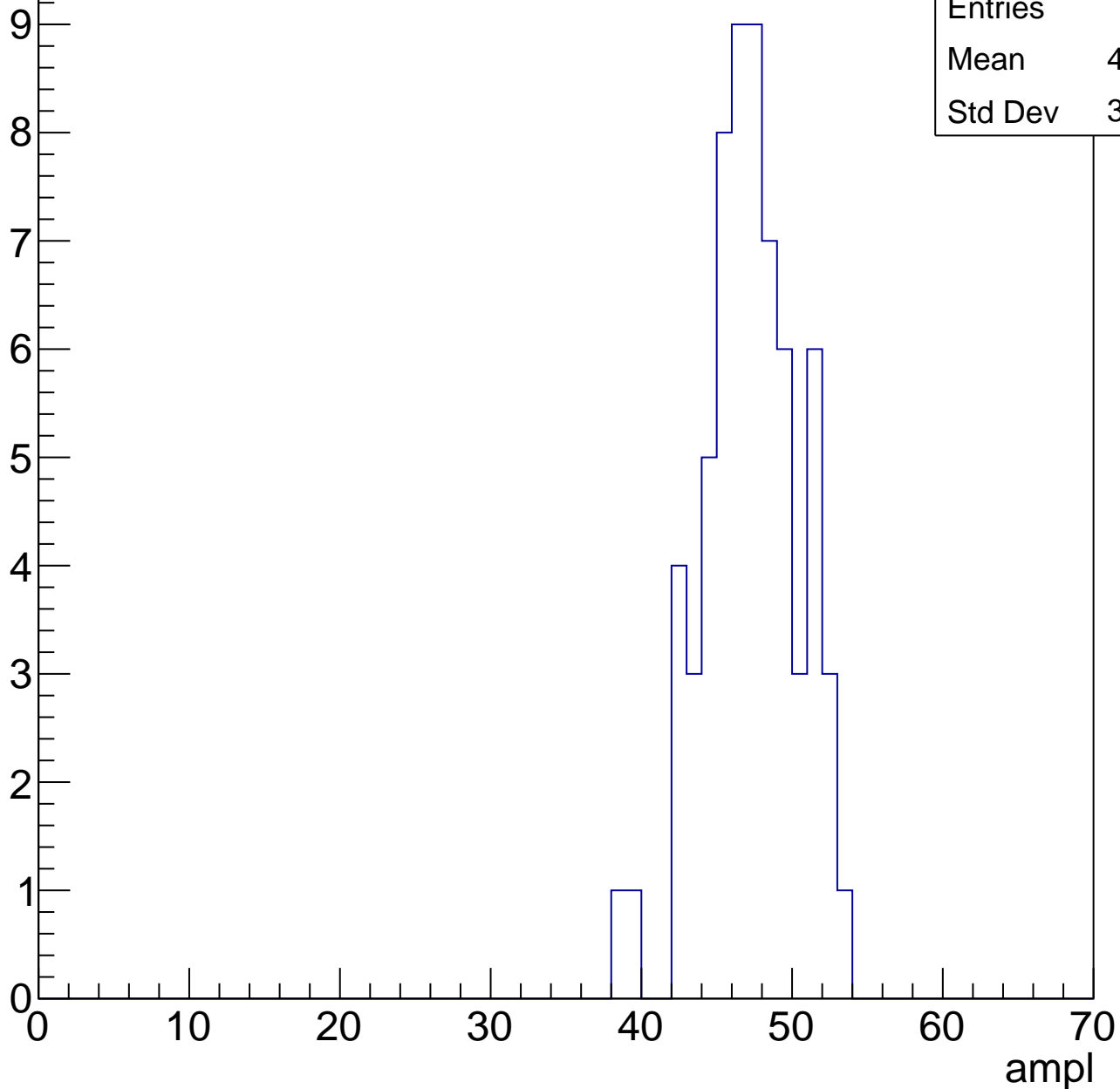
**Gaus Width: 3.3779**



# B1L000S, U2-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

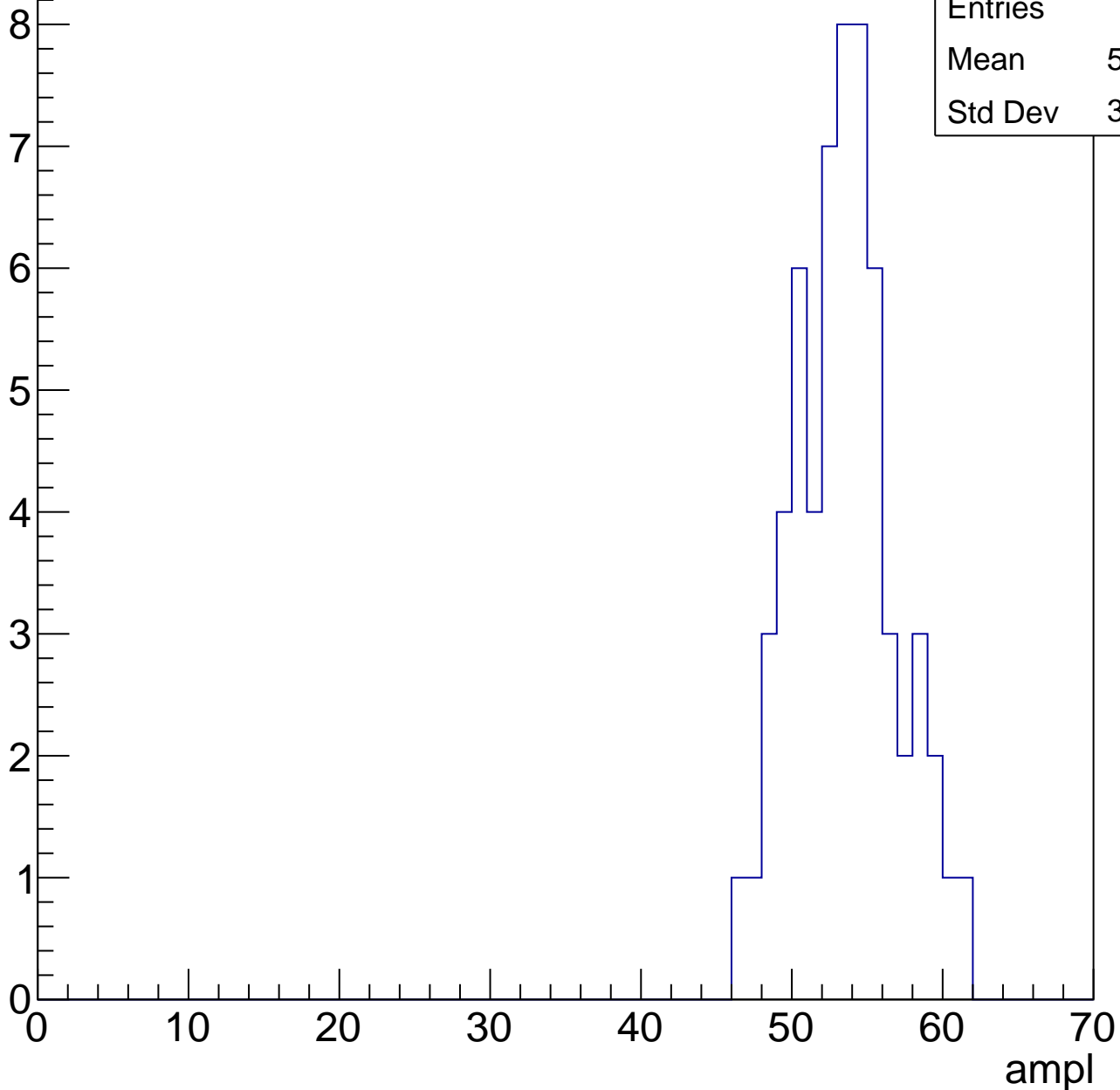


# B1L000S, U2-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	53.03
Std Dev	3.296

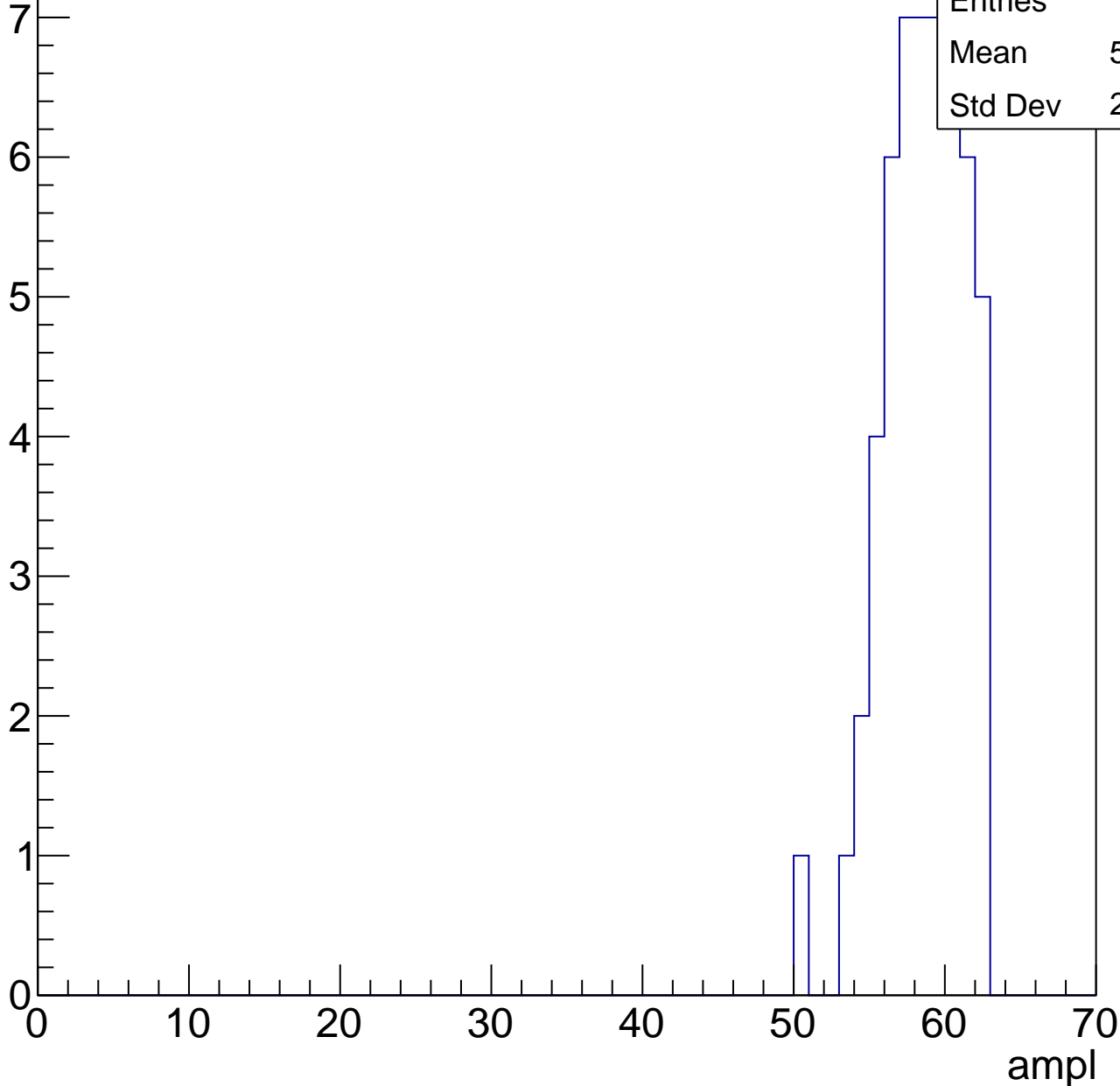


# B1L000S, U2-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	58.13
Std Dev	2.592

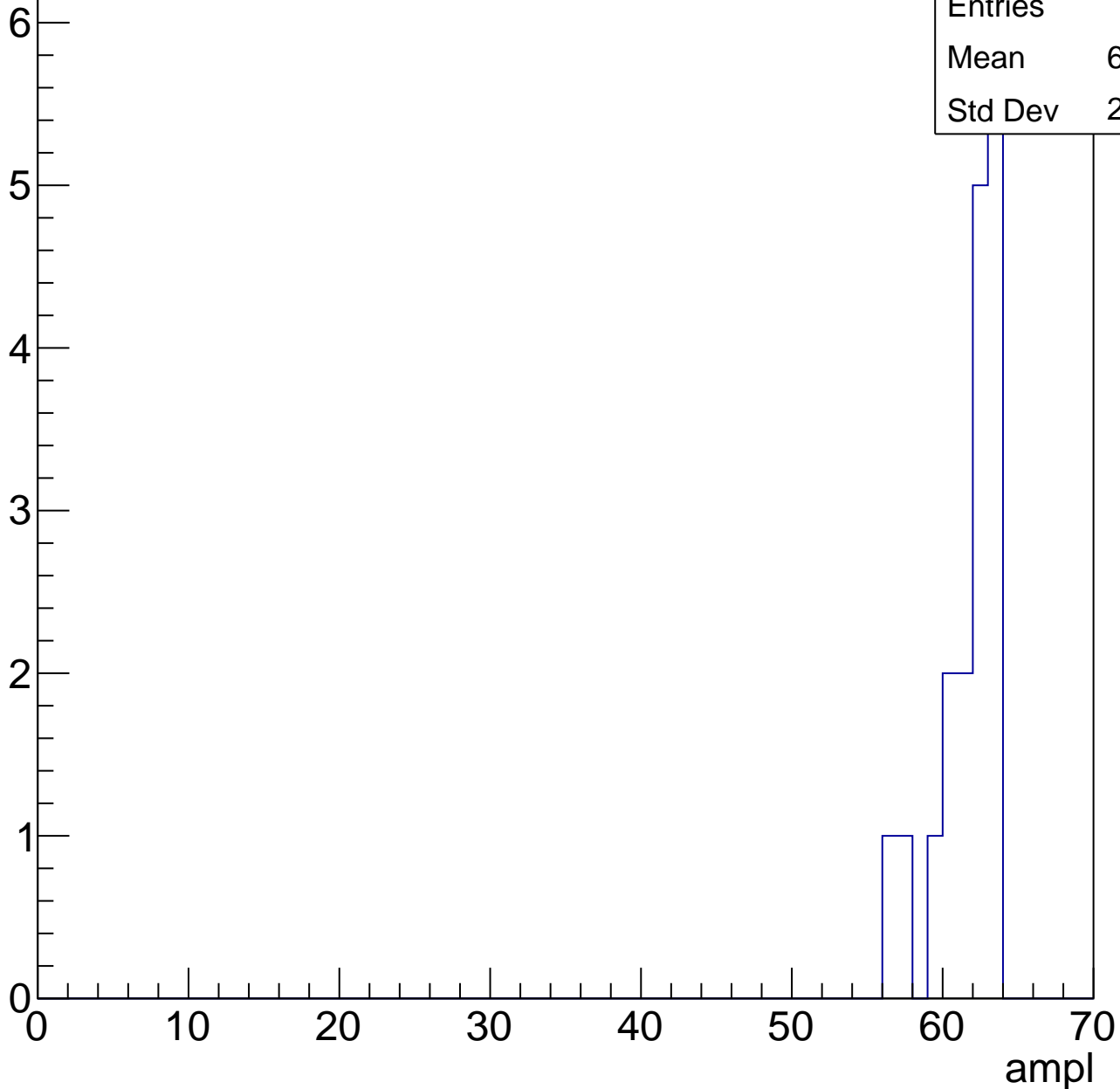


# B1L000S, U2-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.22
Std Dev	2.043

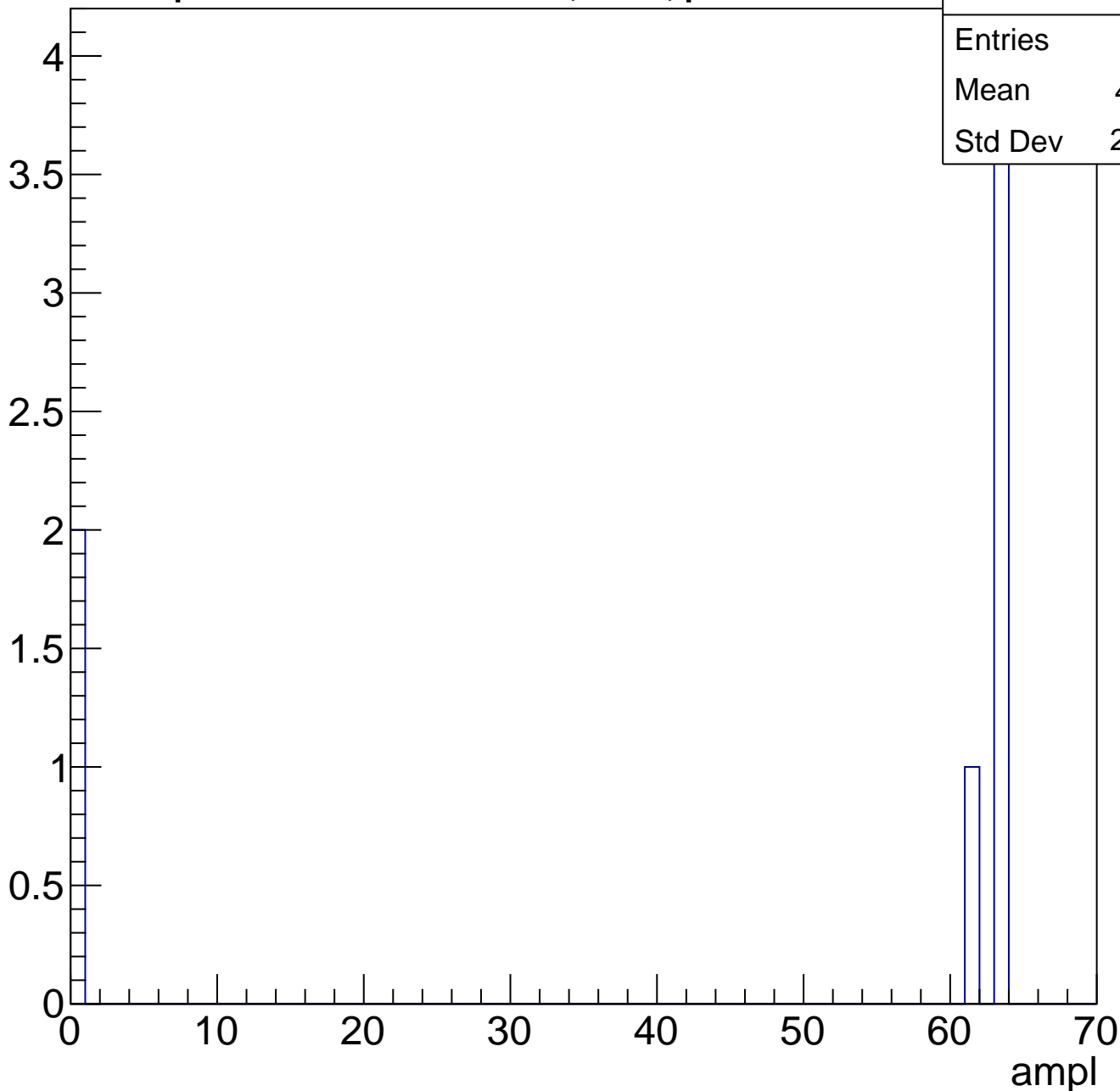




# B1L000S, U2-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	7
Mean	44.71
Std Dev	28.29

# B1L000S, U2-ch107, adc0

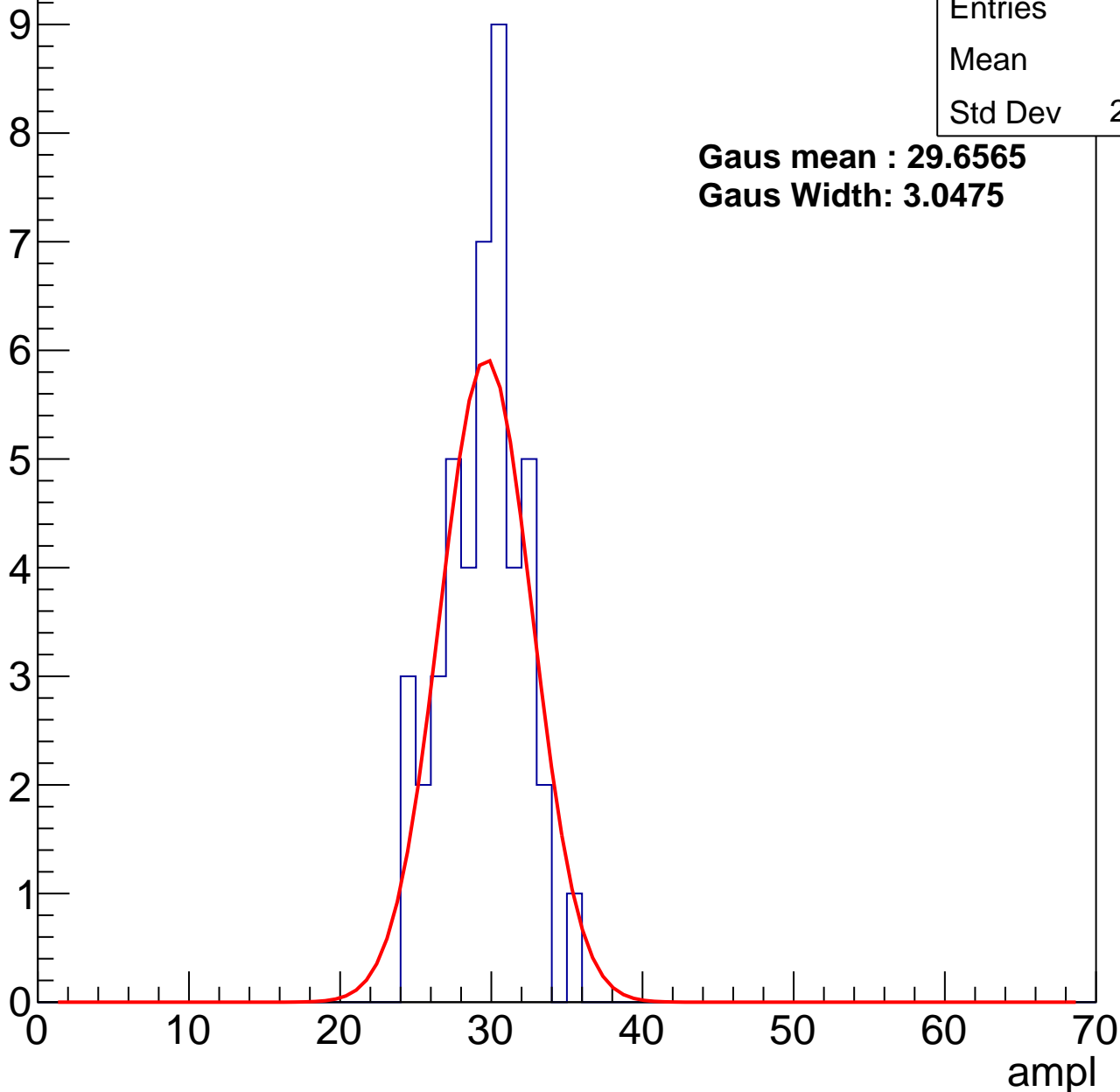
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	29
Std Dev	2.565

**Gaus mean : 29.6565**

**Gaus Width: 3.0475**



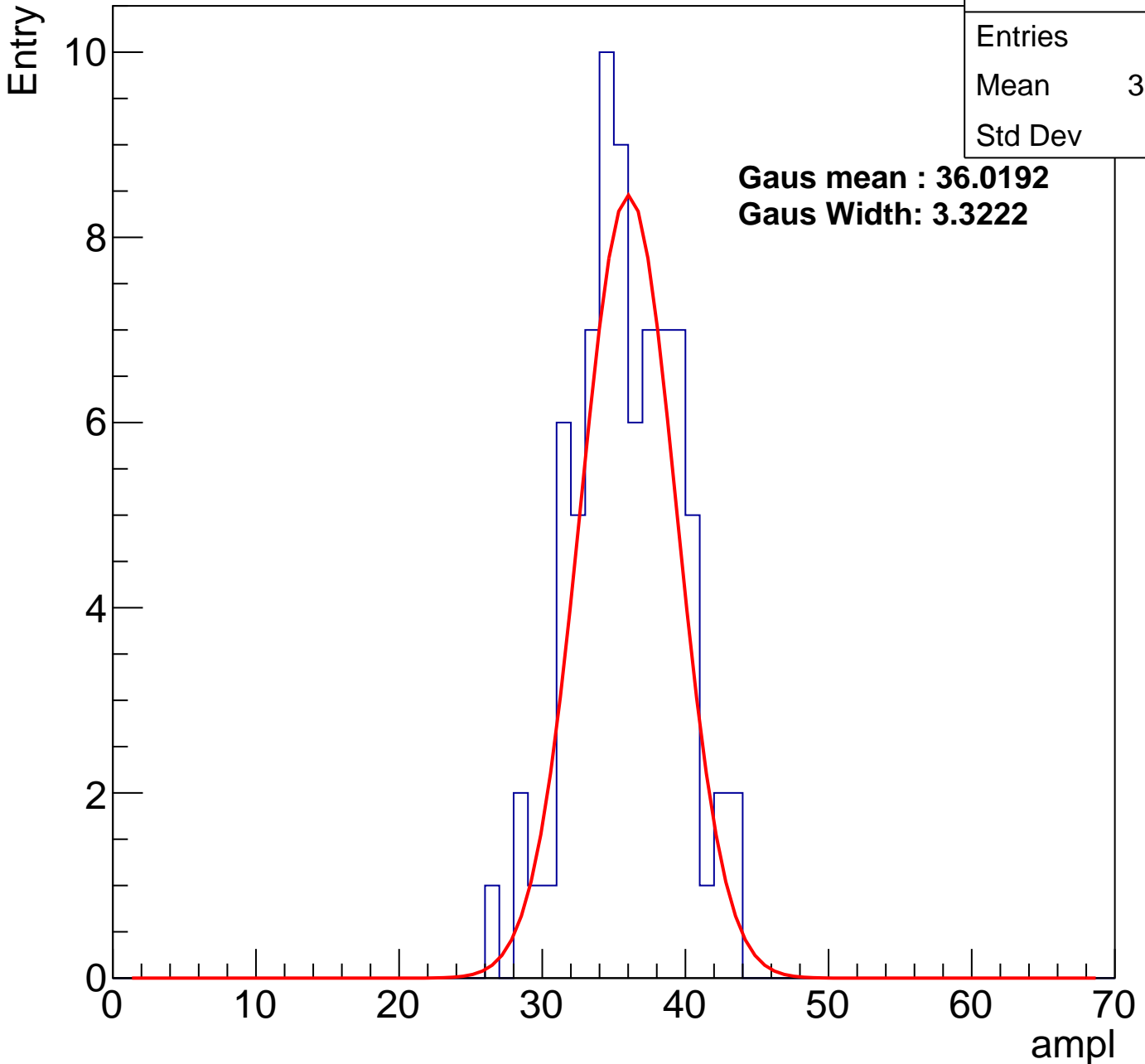
# B1L000S, U2-ch107, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	35.42
Std Dev	3.56

**Gaus mean : 36.0192**

**Gaus Width: 3.3222**



# B1L000S, U2-ch107, adc2

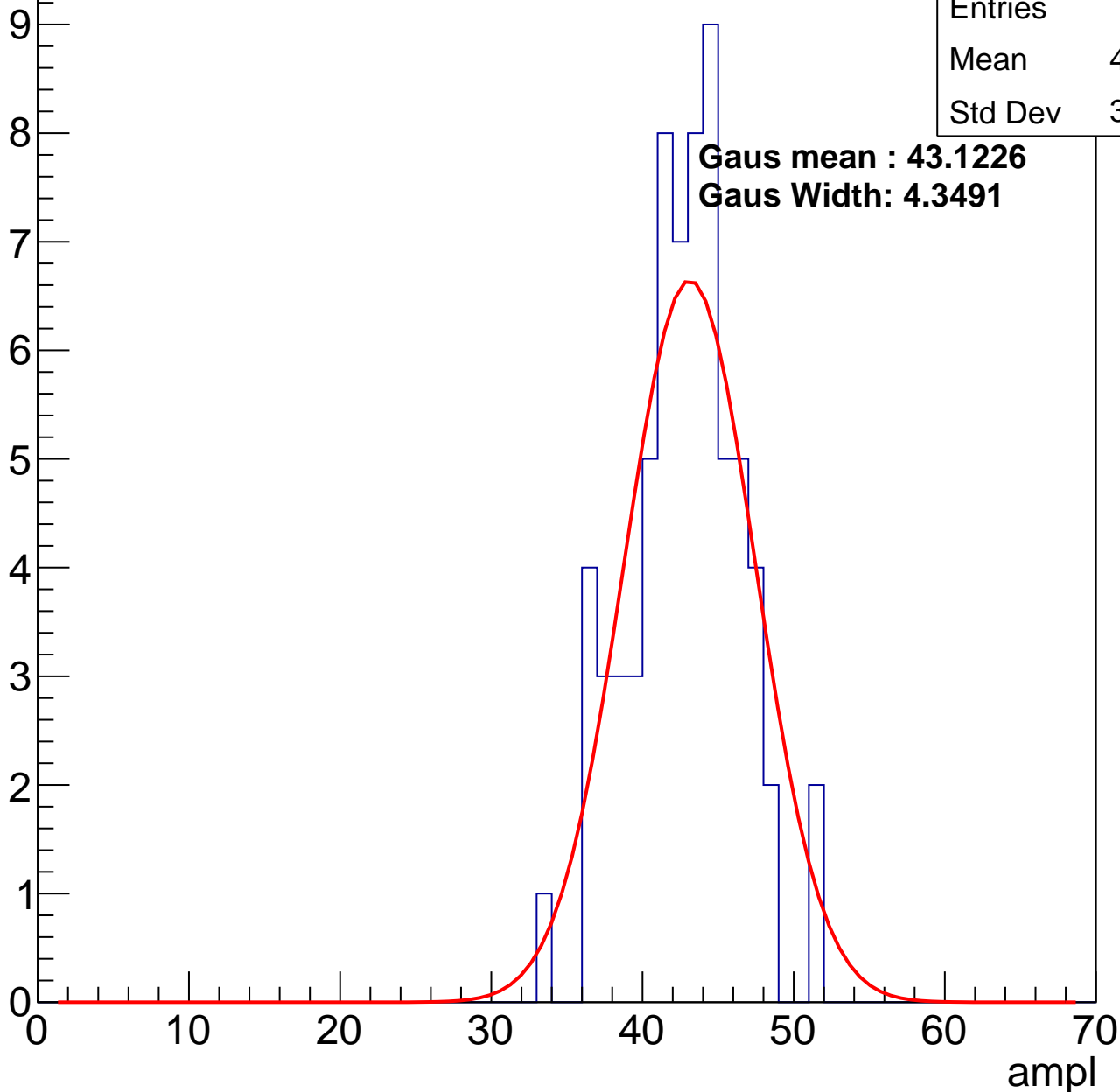
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	42.35
Std Dev	3.607

**Gaus mean : 43.1226**

**Gaus Width: 4.3491**

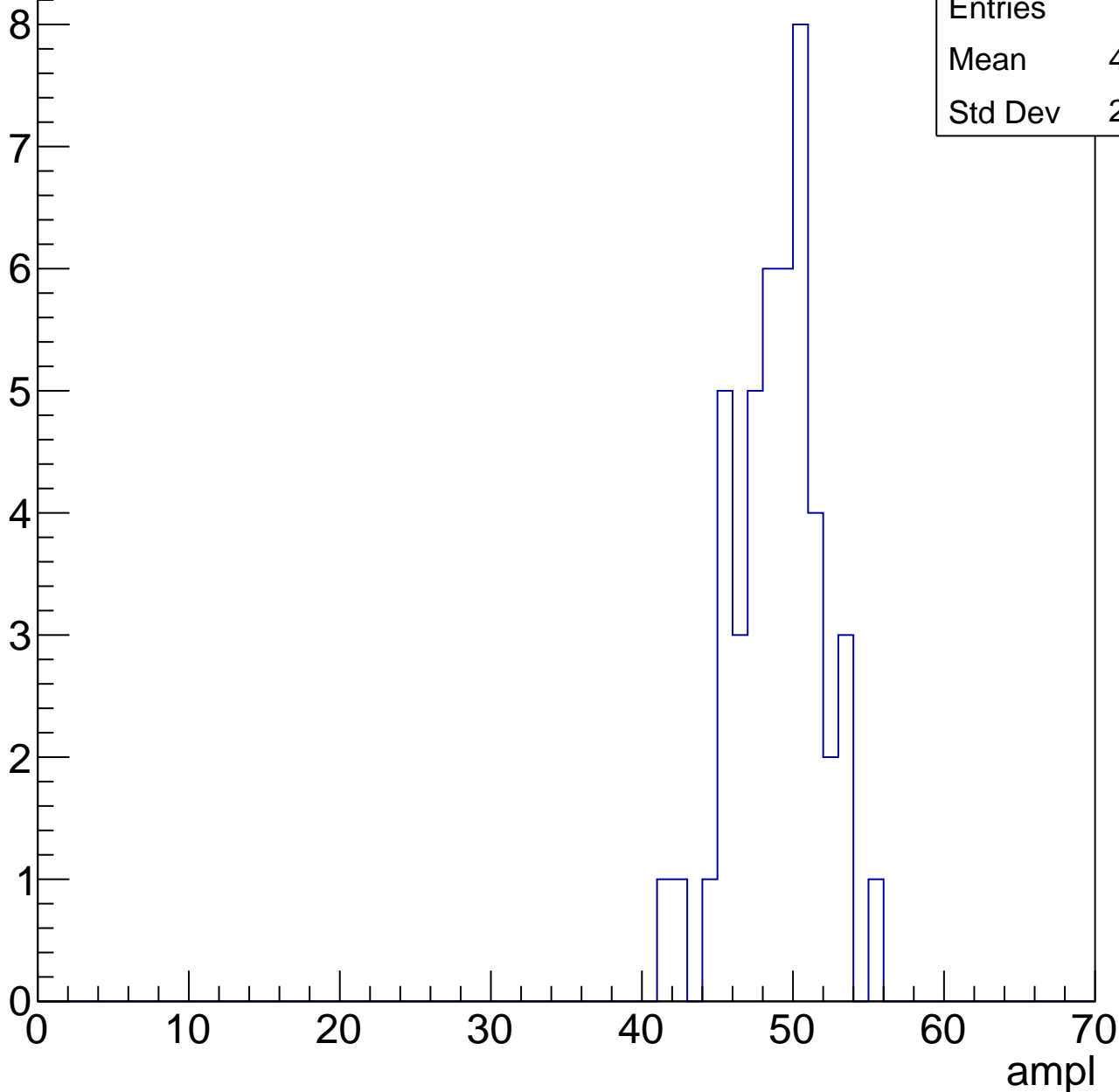


# B1L000S, U2-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

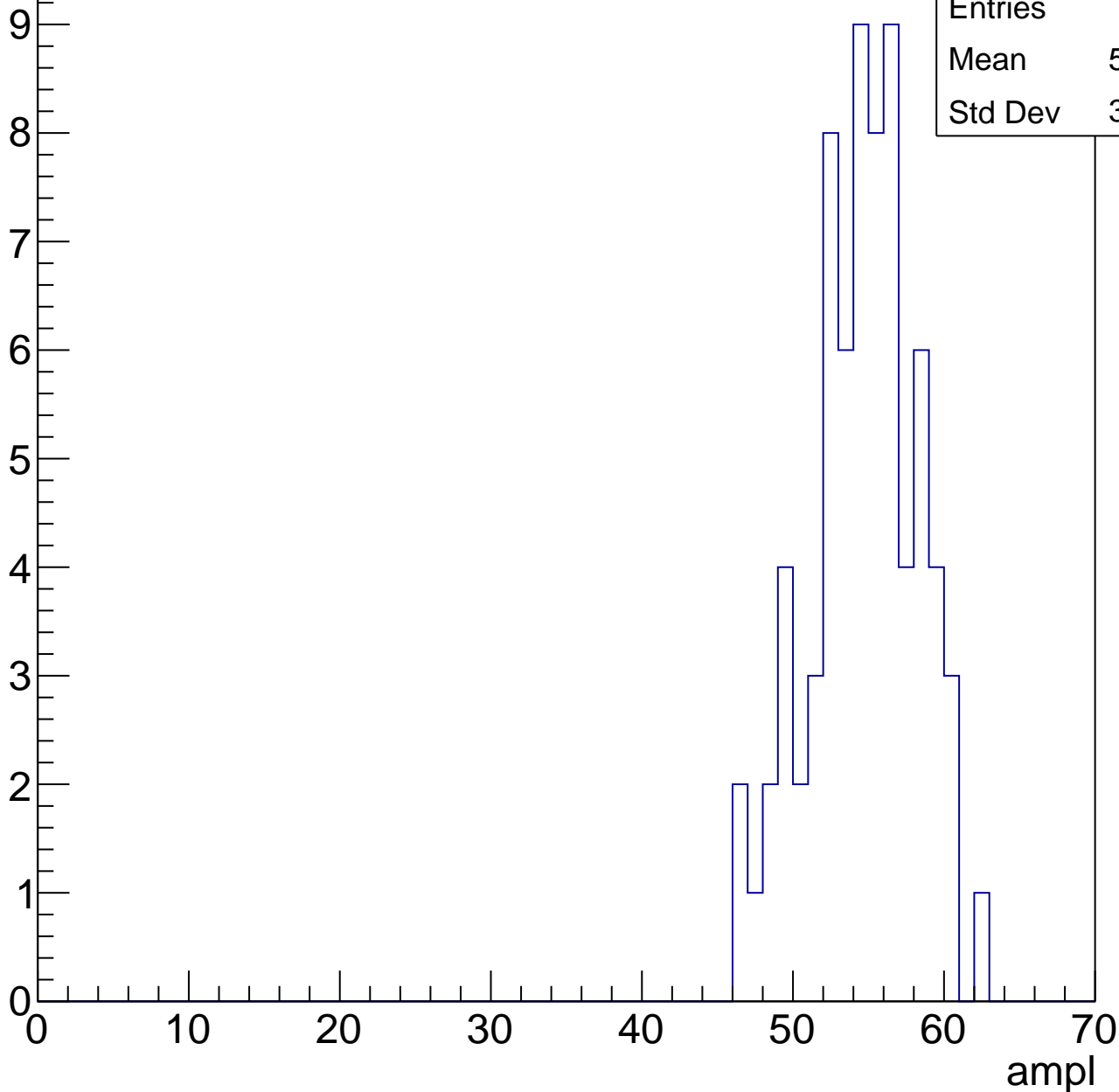
Entries	46
Mean	48.46
Std Dev	2.887



# B1L000S, U2-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 44

Mean 59.55

Std Dev 2.824

ampl

0

10

20

30

40

50

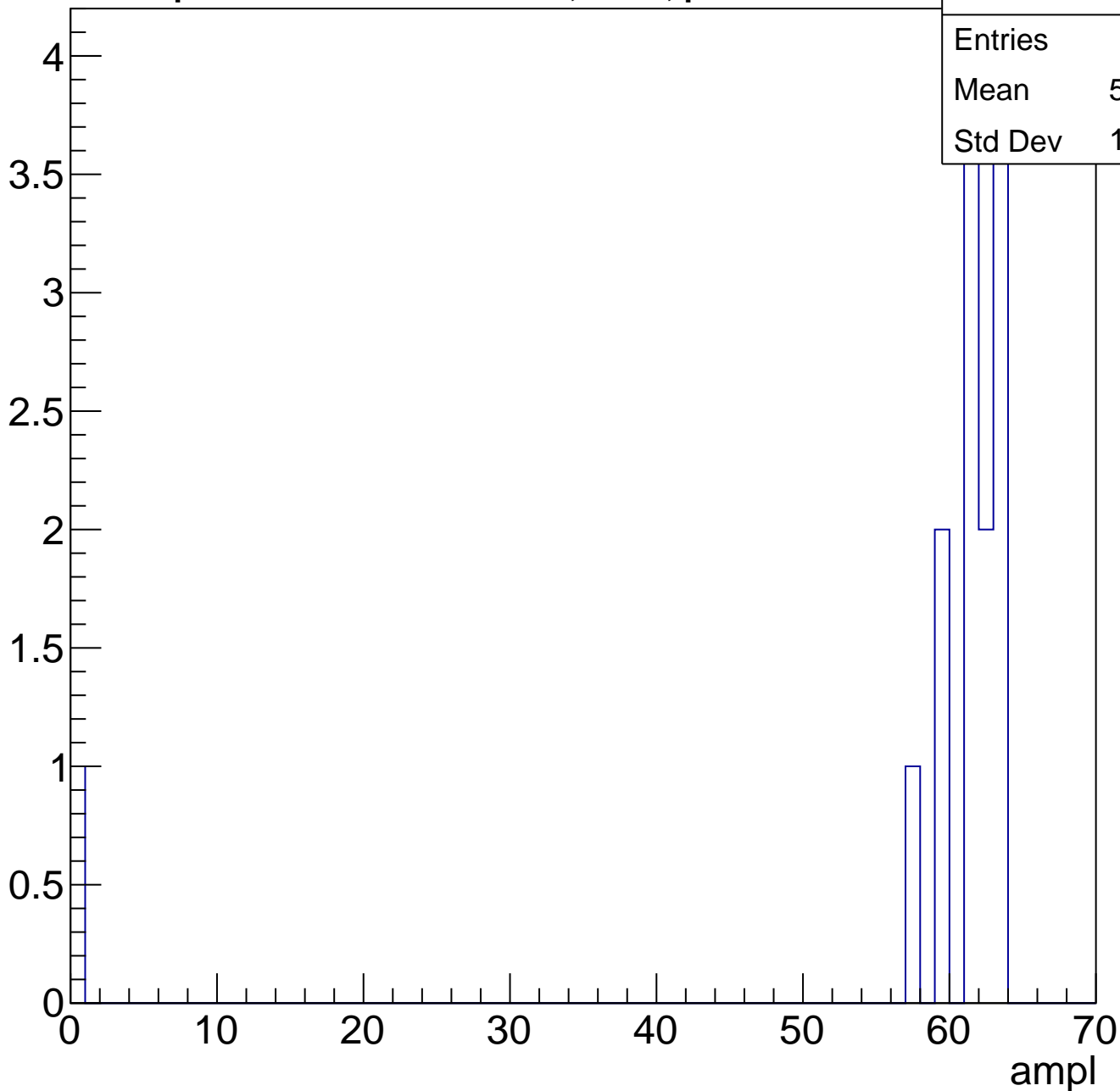
60

70

# B1L000S, U2-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



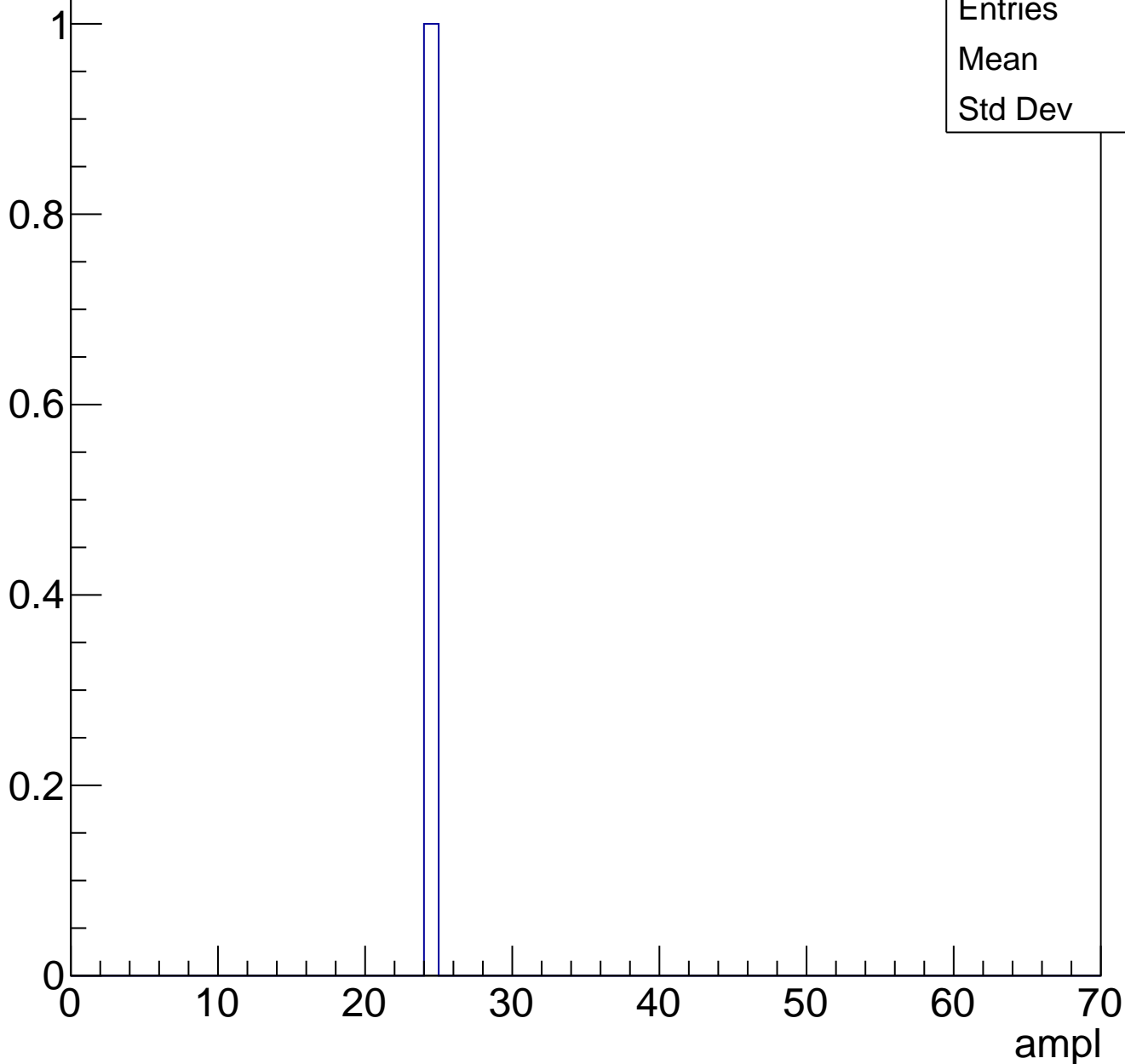
Entries	14
Mean	56.79
Std Dev	15.84



# B1L000S, U2-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	24
Std Dev	0

# B1L000S, U2-ch108, adc0

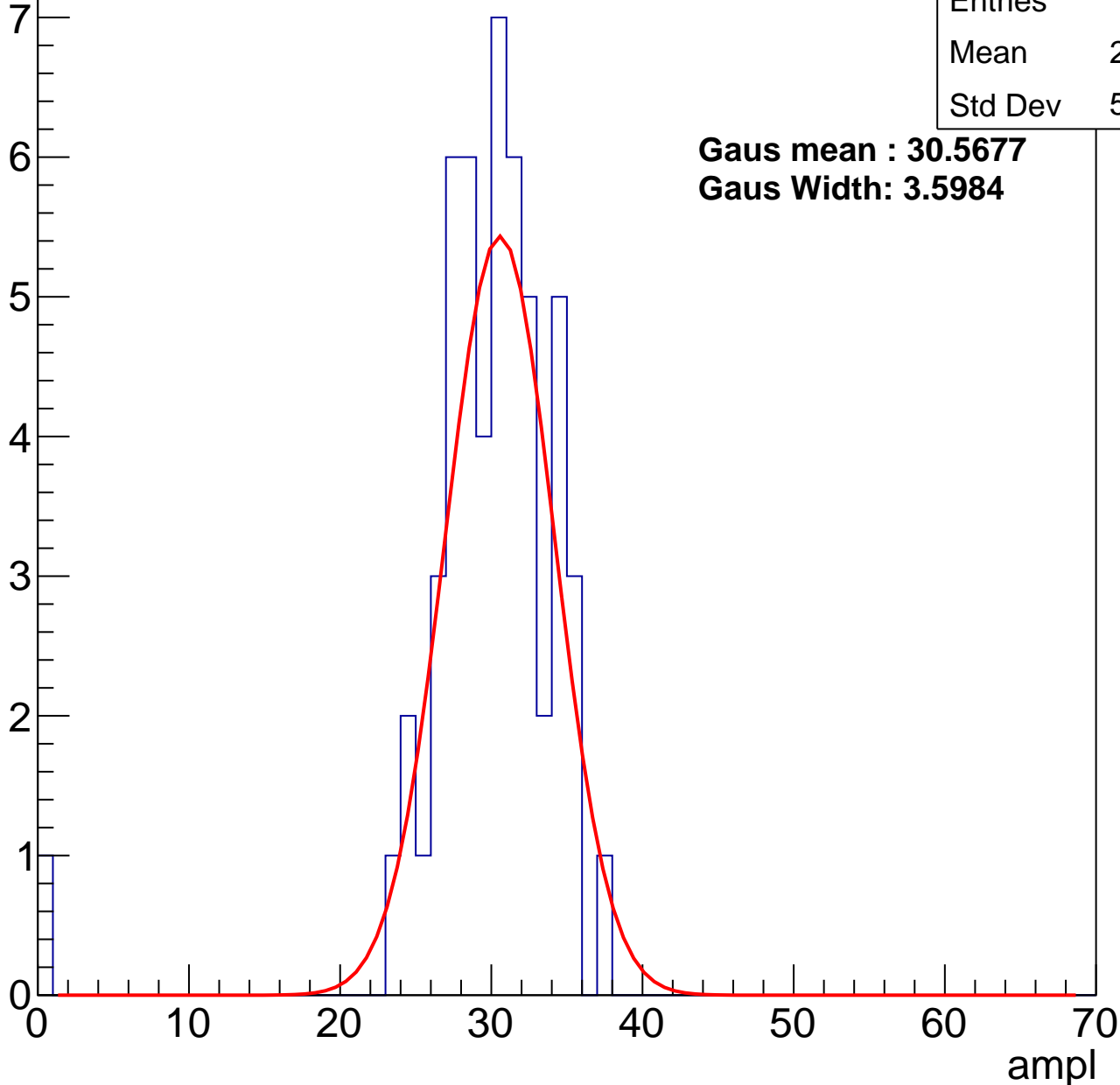
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	29.32
Std Dev	5.139

**Gaus mean : 30.5677**

**Gaus Width: 3.5984**



# B1L000S, U2-ch108, adc1

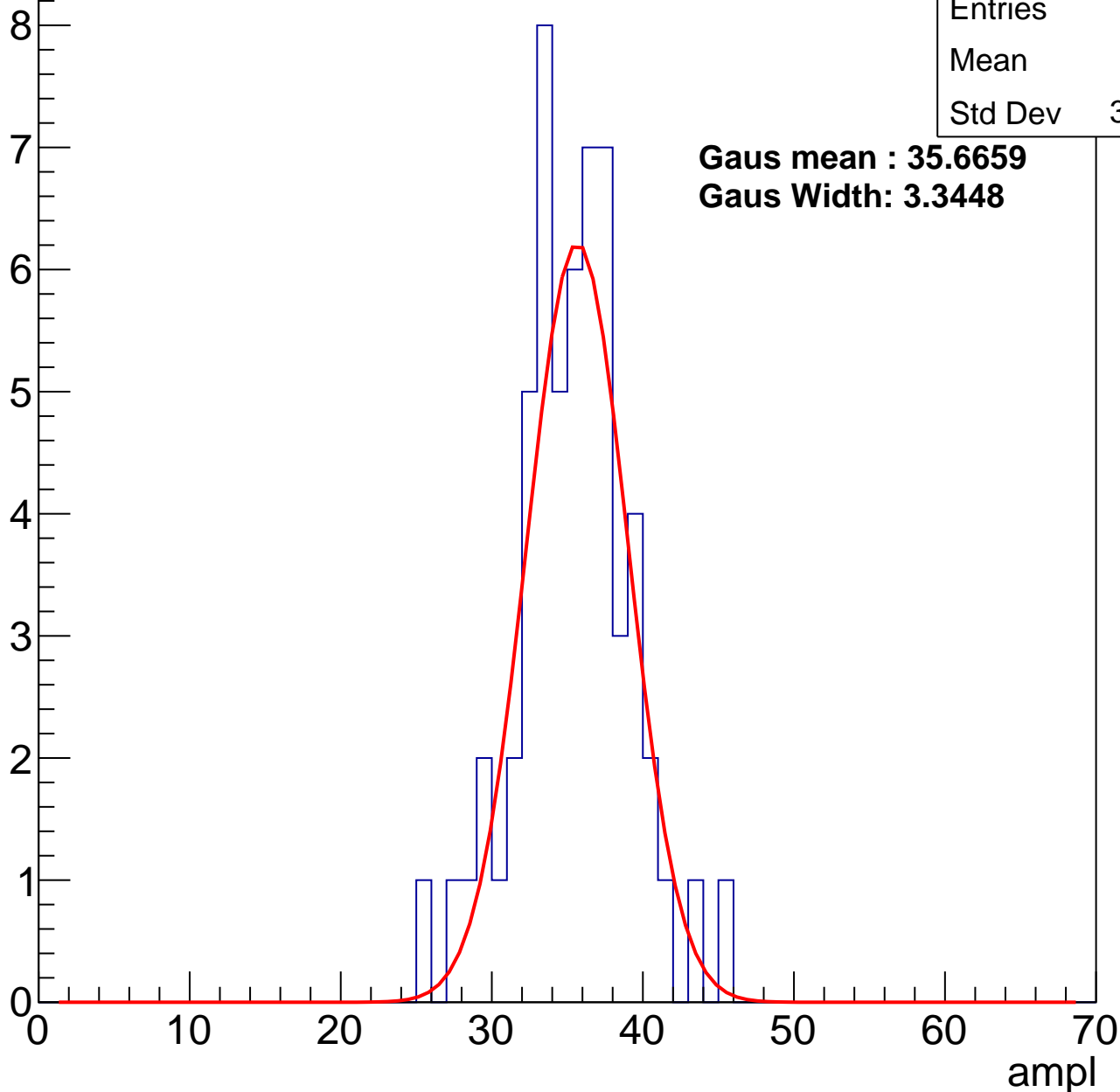
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.9
Std Dev	3.712

**Gaus mean : 35.6659**

**Gaus Width: 3.3448**



# B1L000S, U2-ch108, adc2

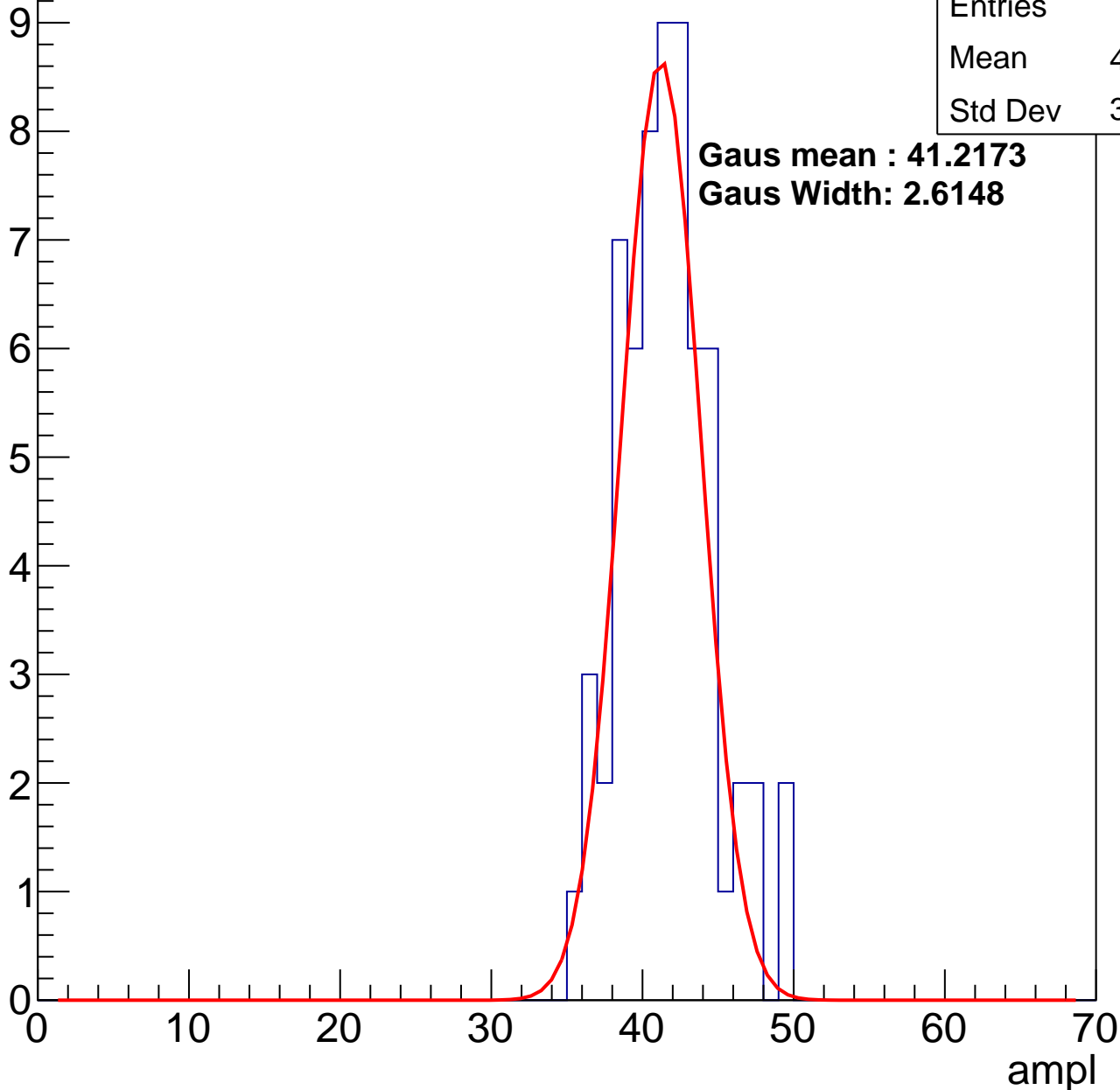
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.17
Std Dev	3.034

**Gaus mean : 41.2173**

**Gaus Width: 2.6148**

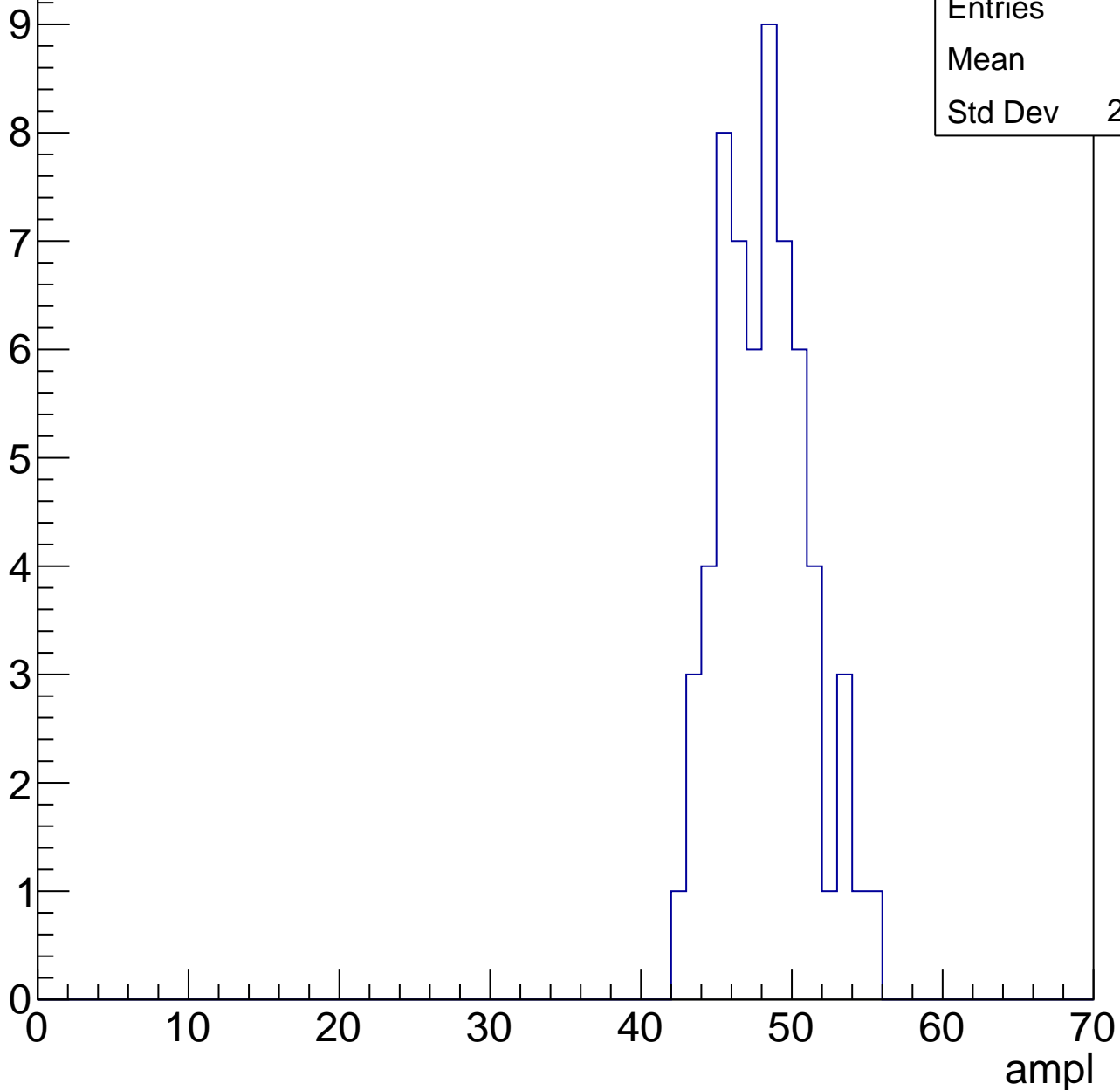


# B1L000S, U2-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	47.7
Std Dev	2.916

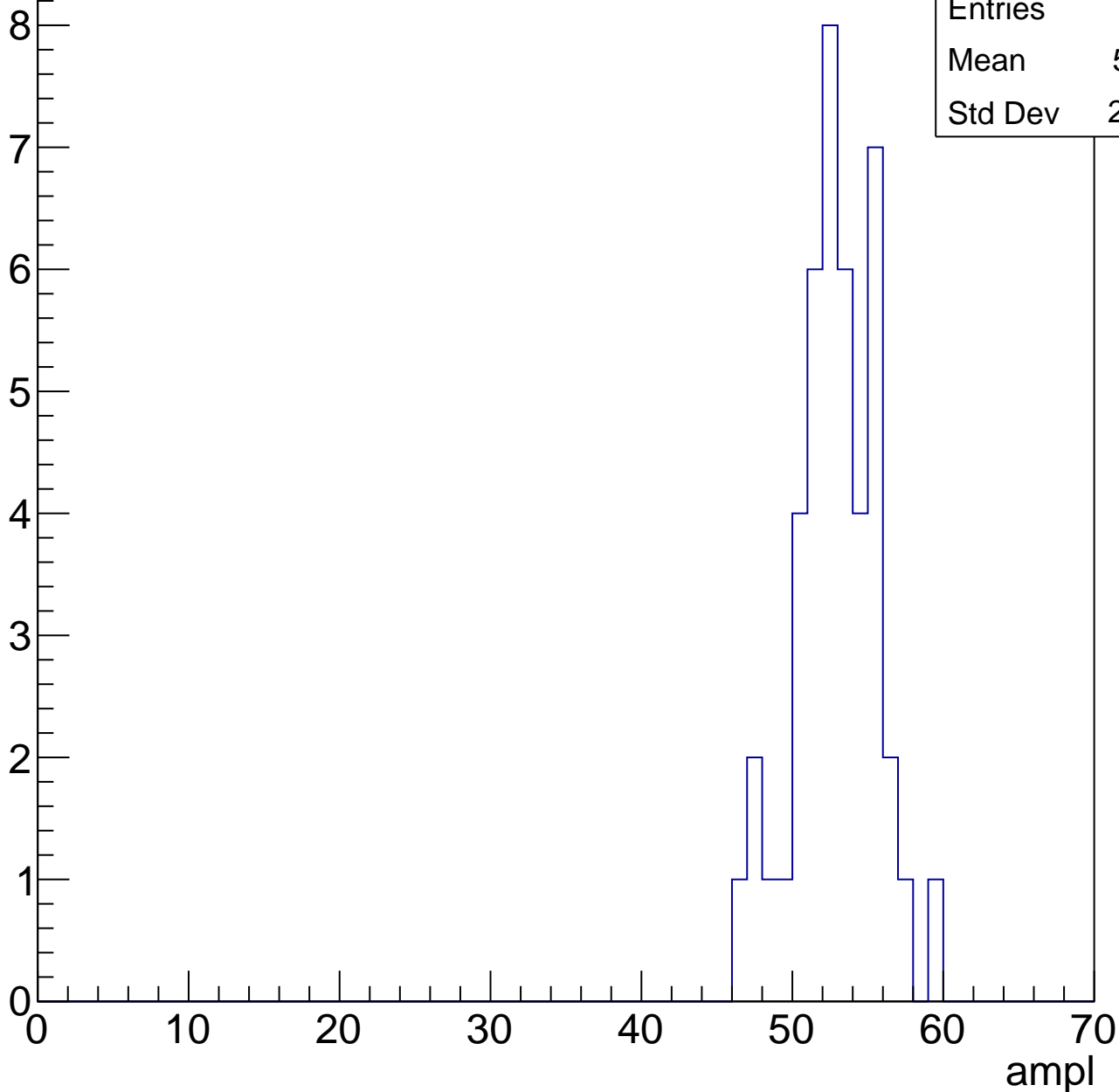


# B1L000S, U2-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	52.41
Std Dev	2.683



# B1L000S, U2-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

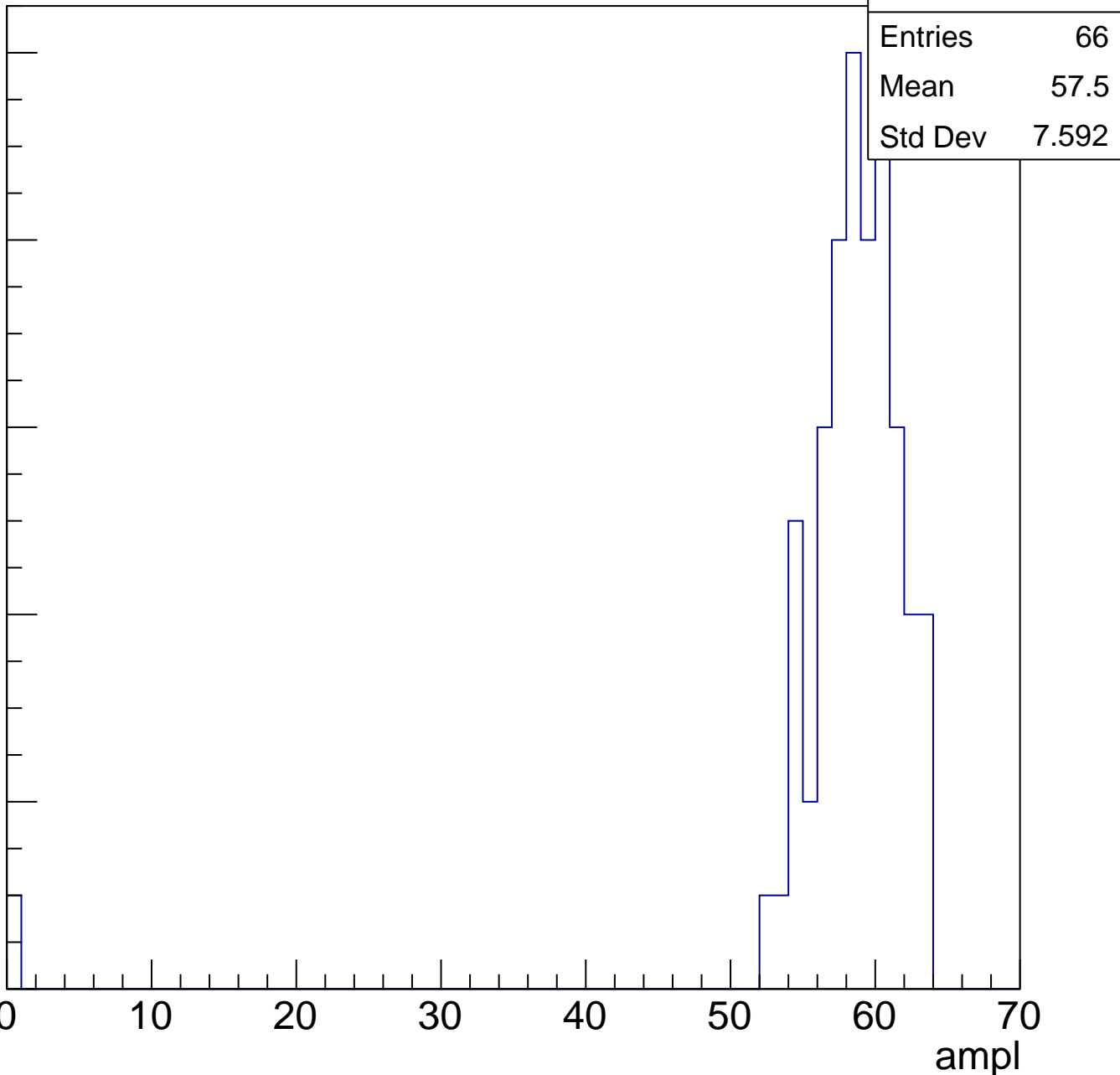
40

50

60

ampl

Entries	66
Mean	57.5
Std Dev	7.592

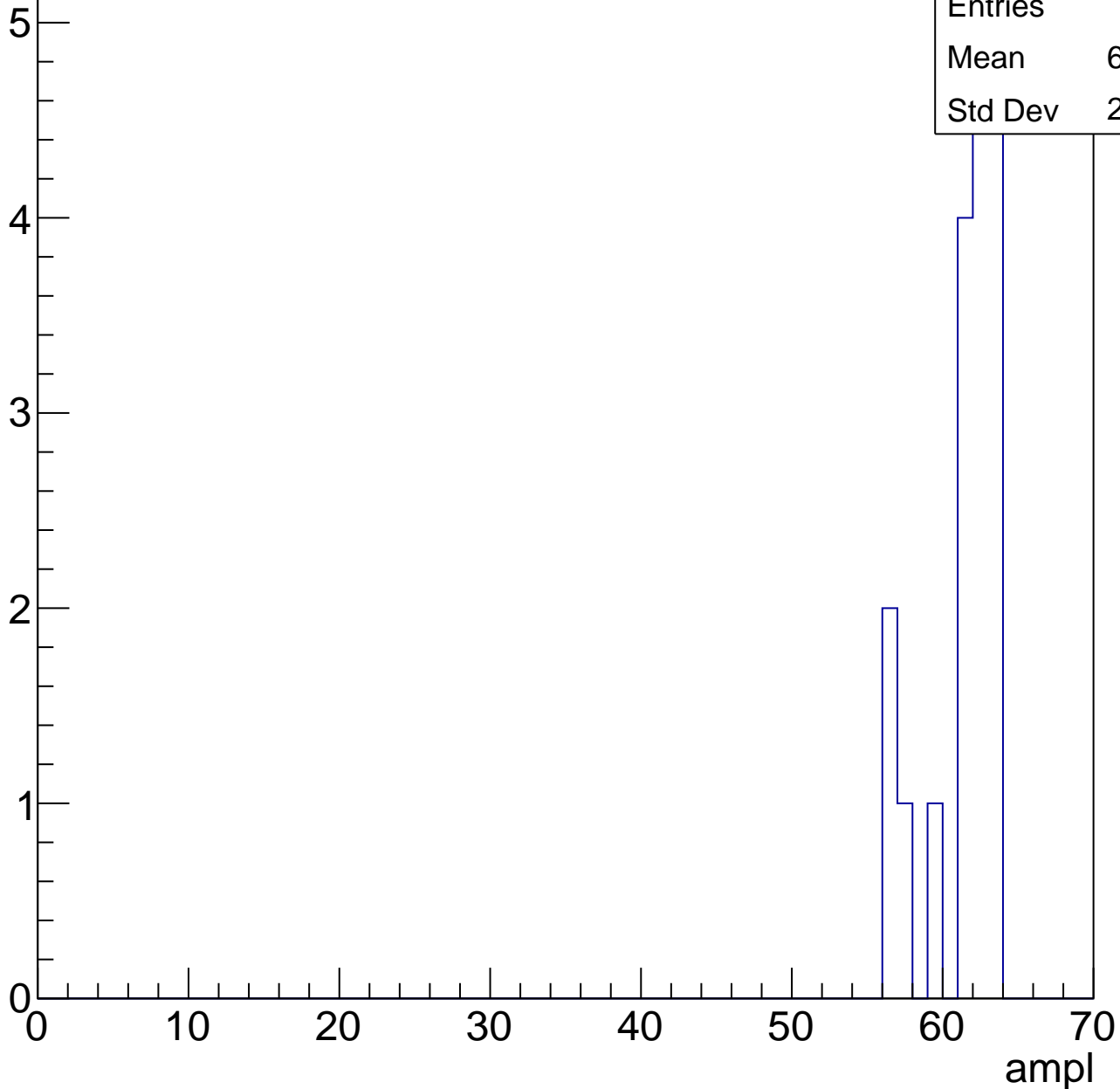


# B1L000S, U2-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	60.94
Std Dev	2.297

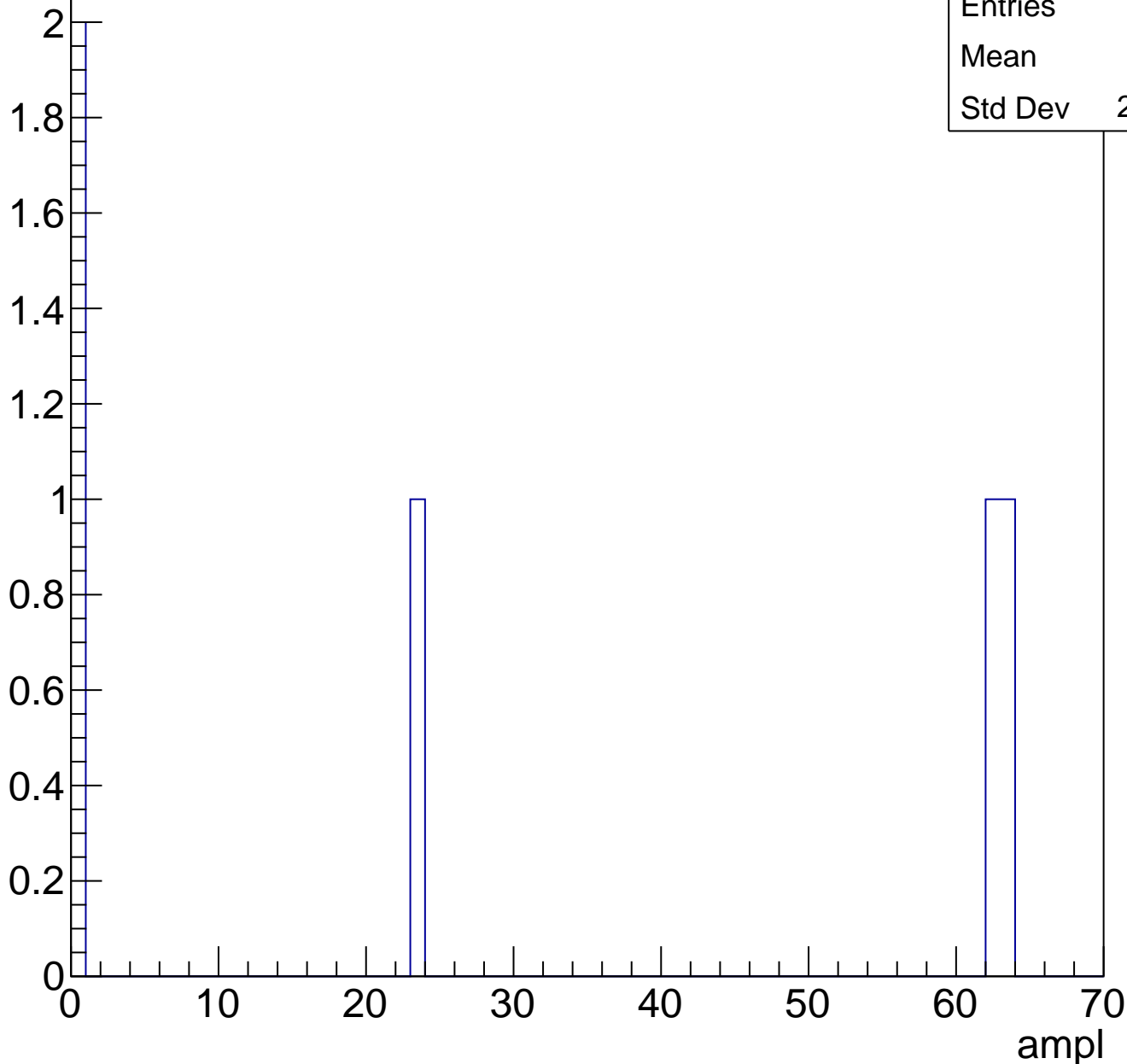




# B1L000S, U2-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	29.6
Std Dev	28.15

# B1L000S, U2-ch109, adc0

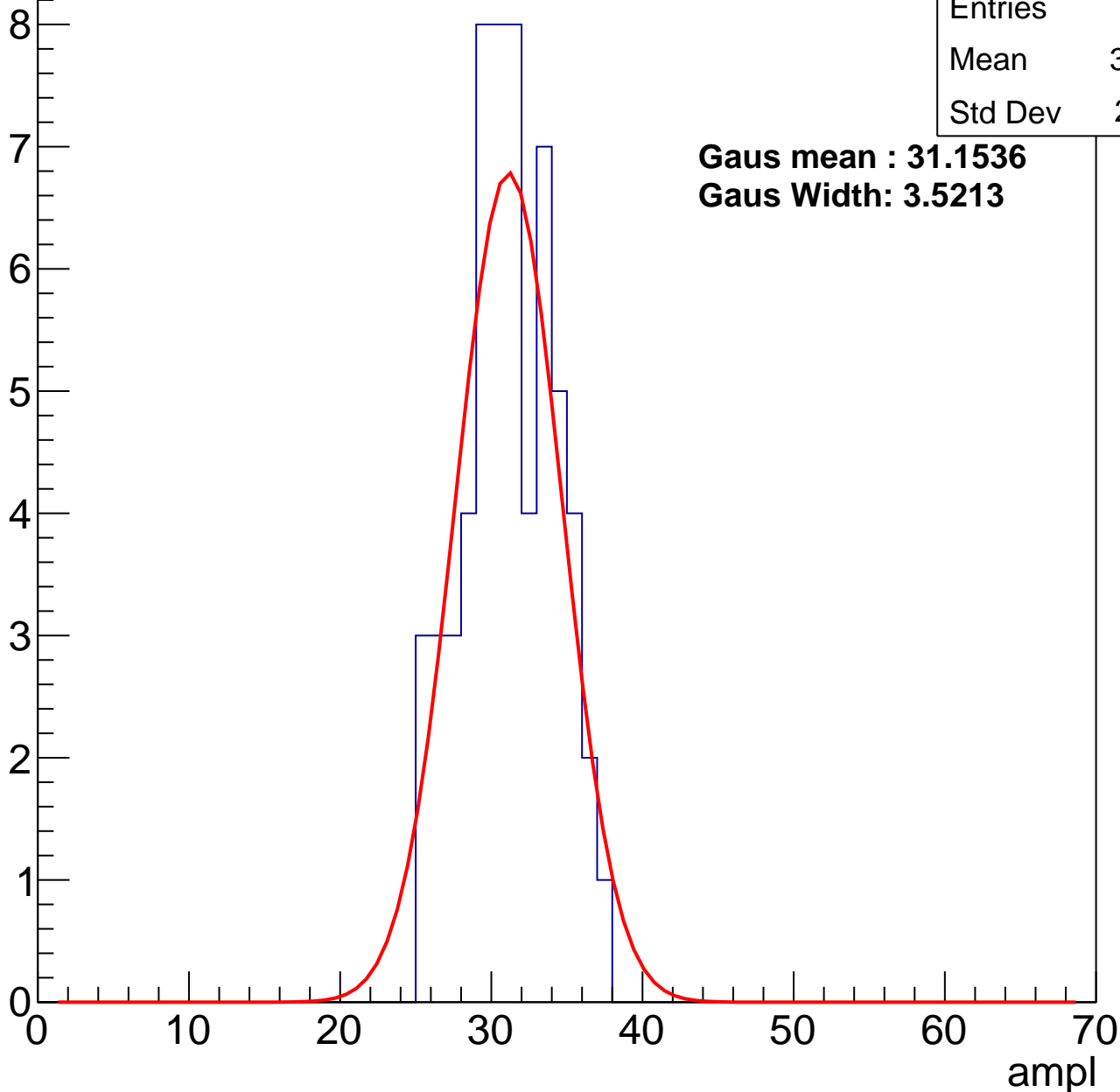
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	30.73
Std Dev	2.971

**Gaus mean : 31.1536**

**Gaus Width: 3.5213**



# B1L000S, U2-ch109, adc1

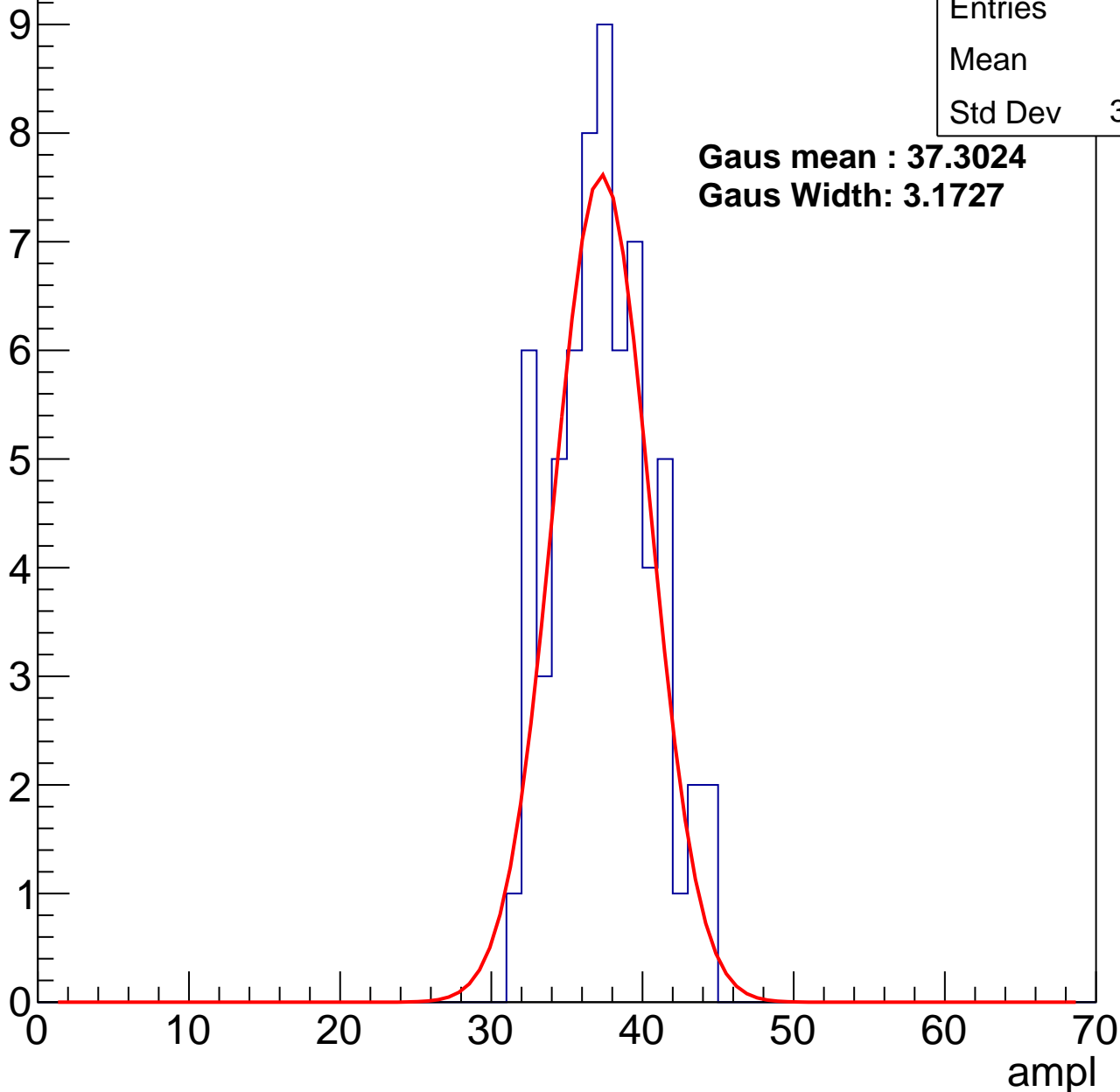
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	37
Std Dev	3.177

**Gaus mean : 37.3024**

**Gaus Width: 3.1727**



# B1L000S, U2-ch109, adc2

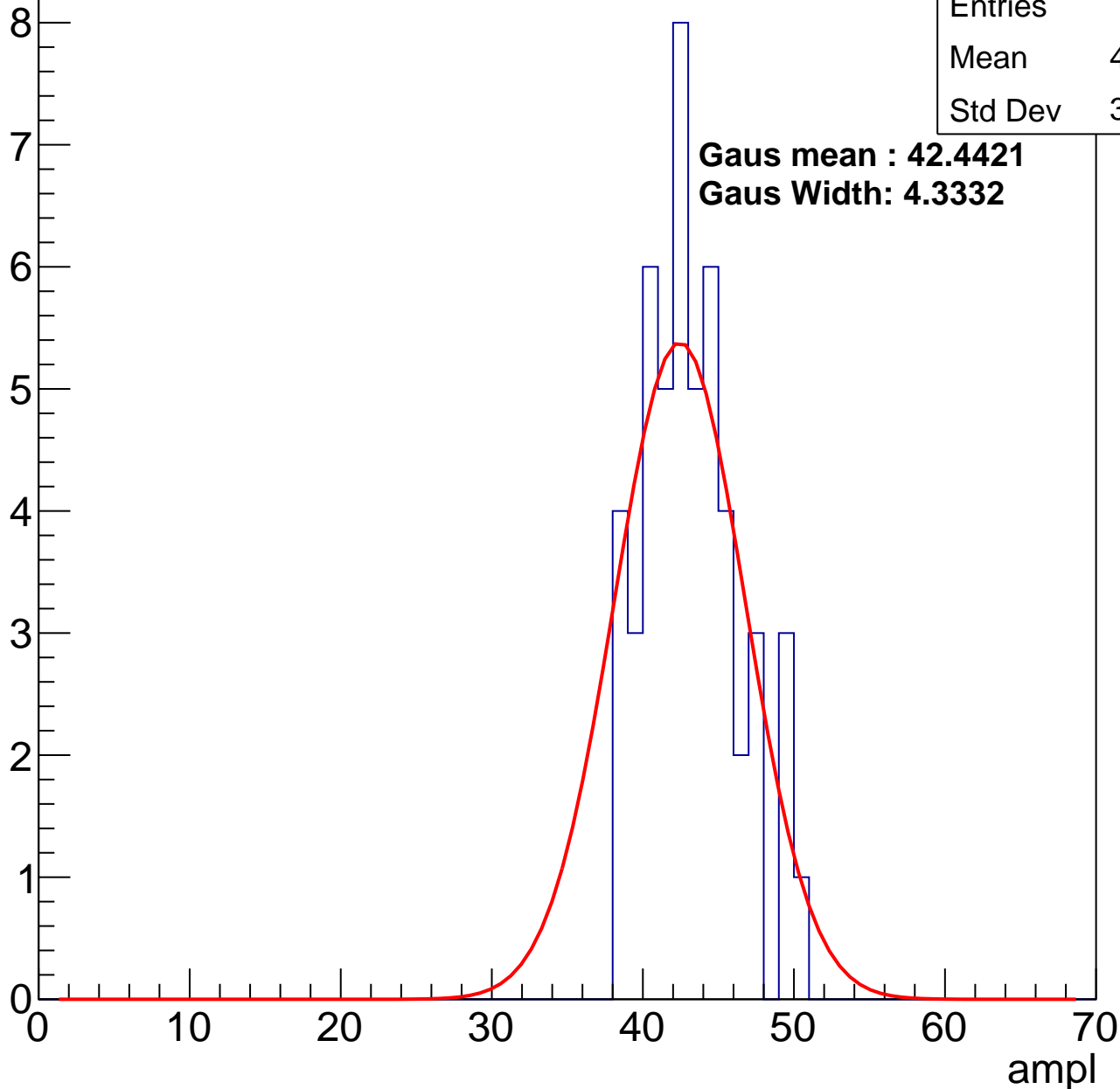
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	42.78
Std Dev	3.074

**Gaus mean : 42.4421**

**Gaus Width: 4.3332**

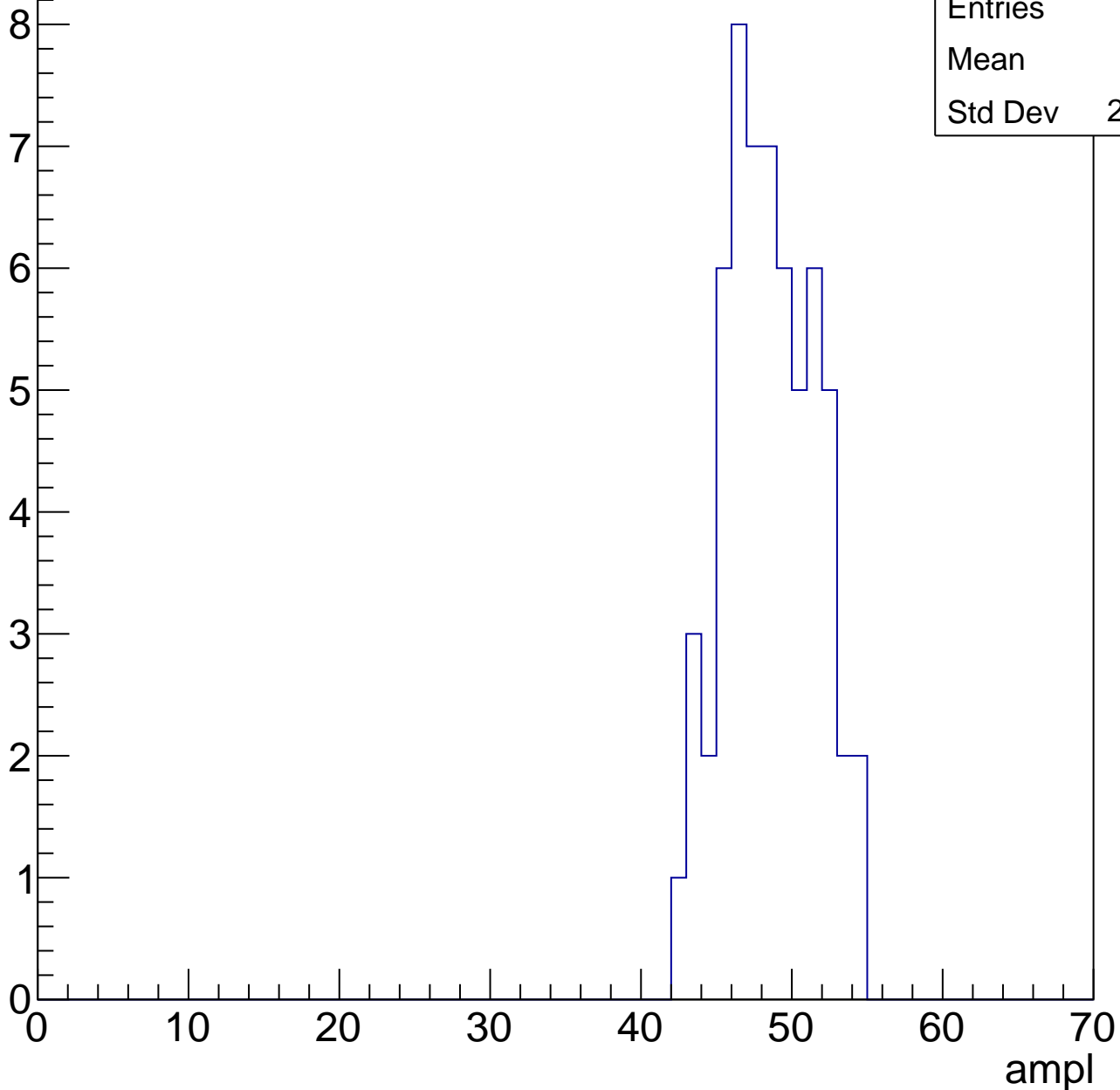


# B1L000S, U2-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	48.1
Std Dev	2.937

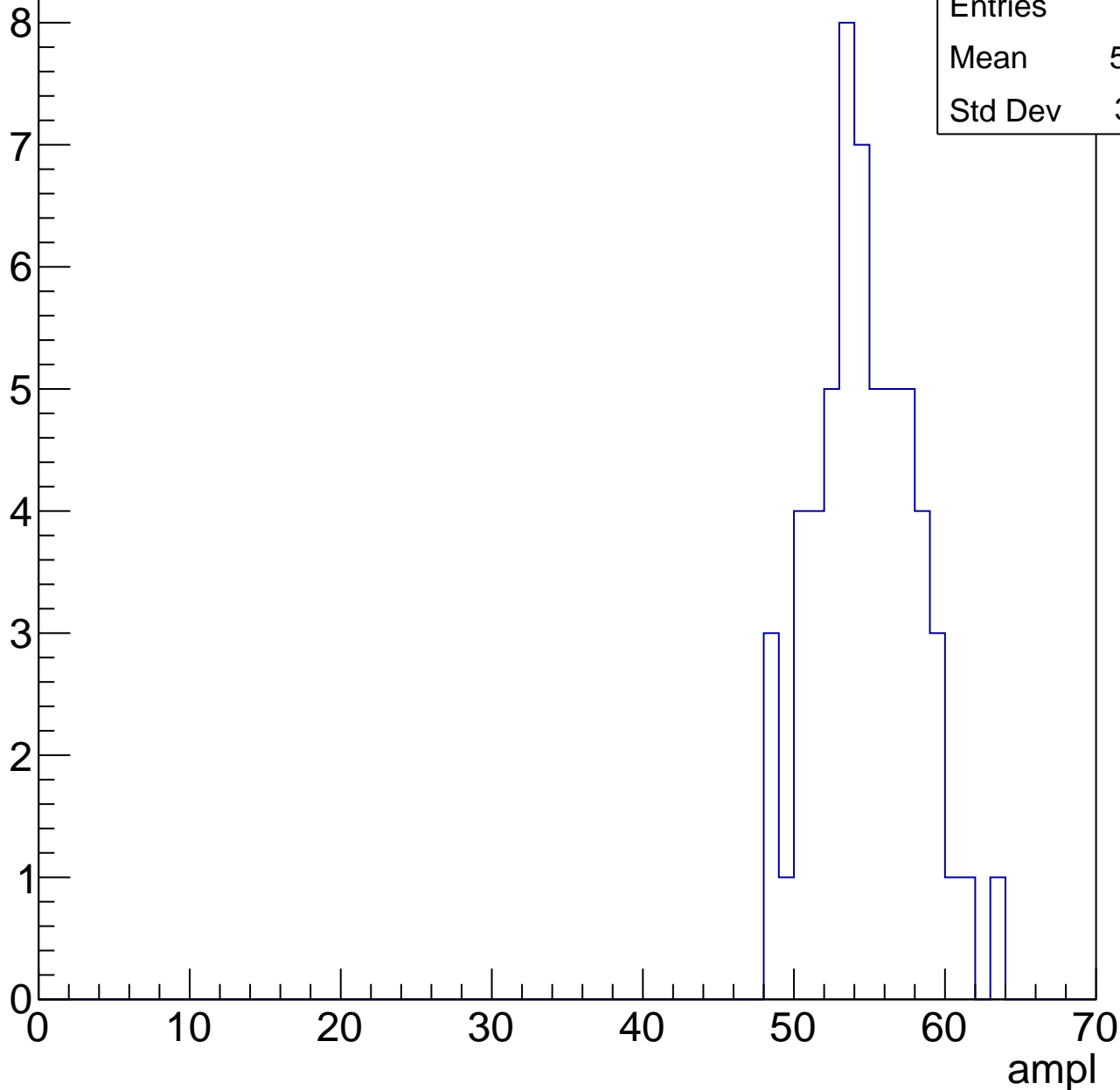


# B1L000S, U2-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	54.25
Std Dev	3.331

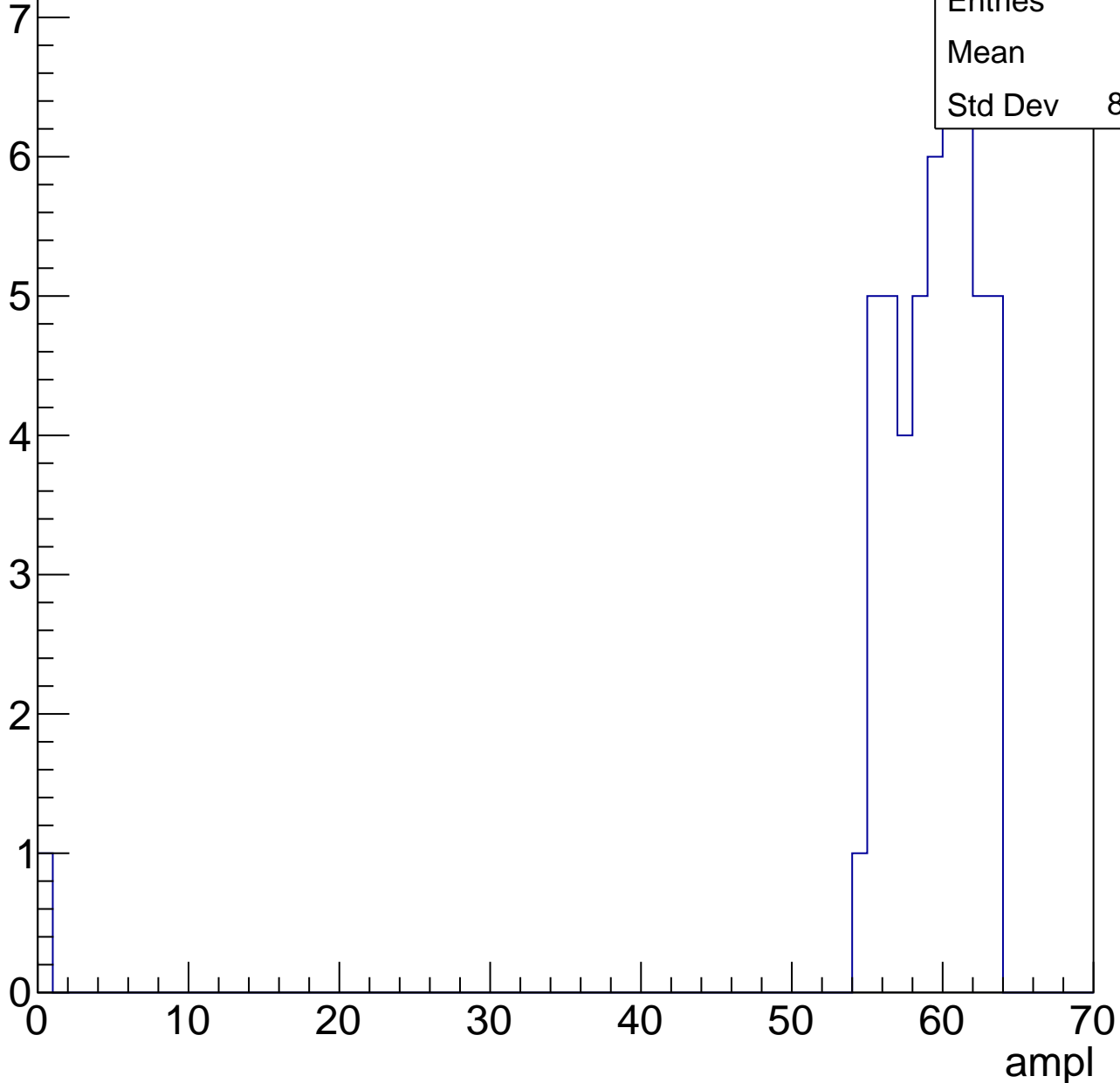


# B1L000S, U2-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	57.9
Std Dev	8.576

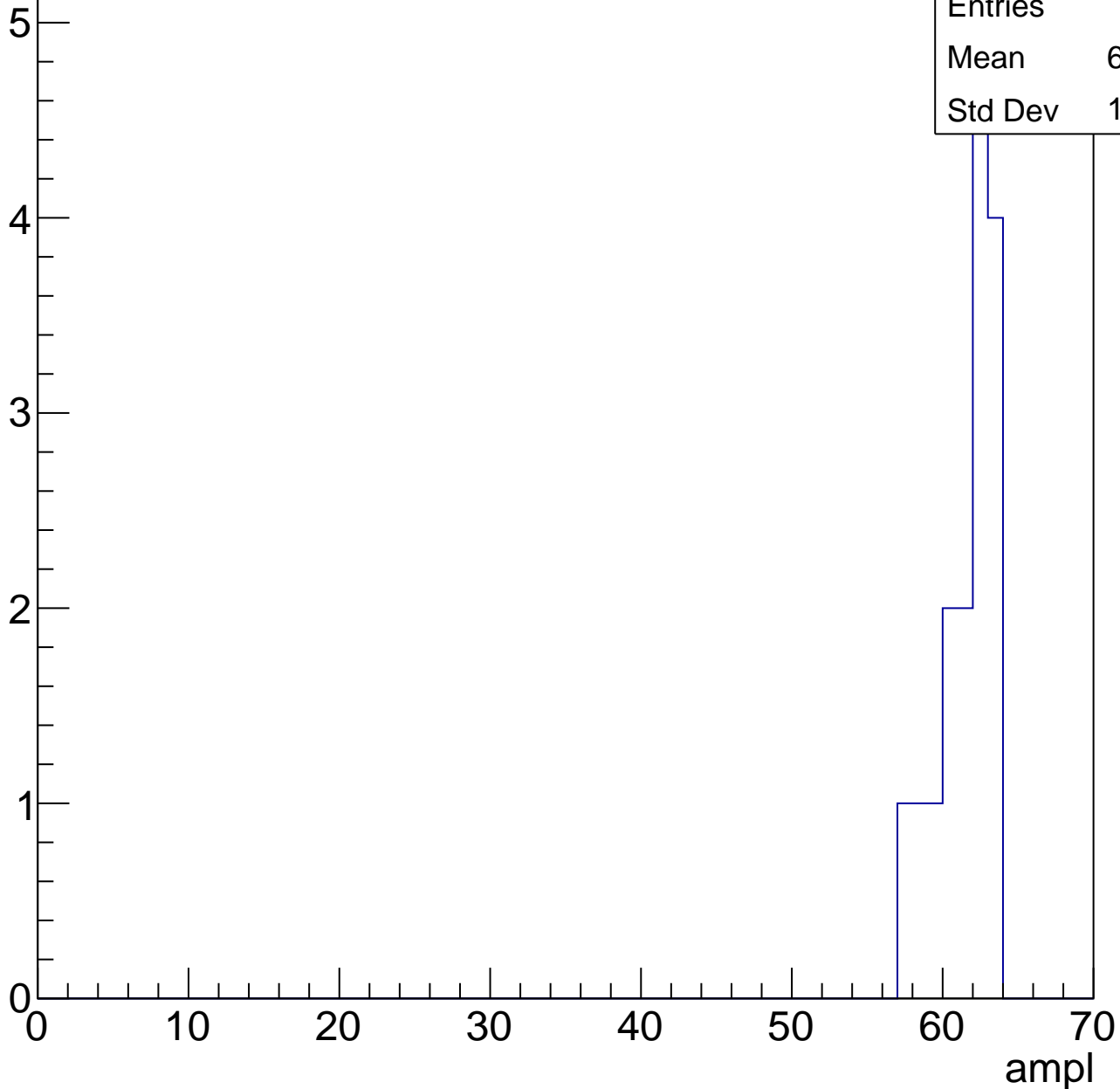


# B1L000S, U2-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.12
Std Dev	1.798





# B1L000S, U2-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch110, adc0

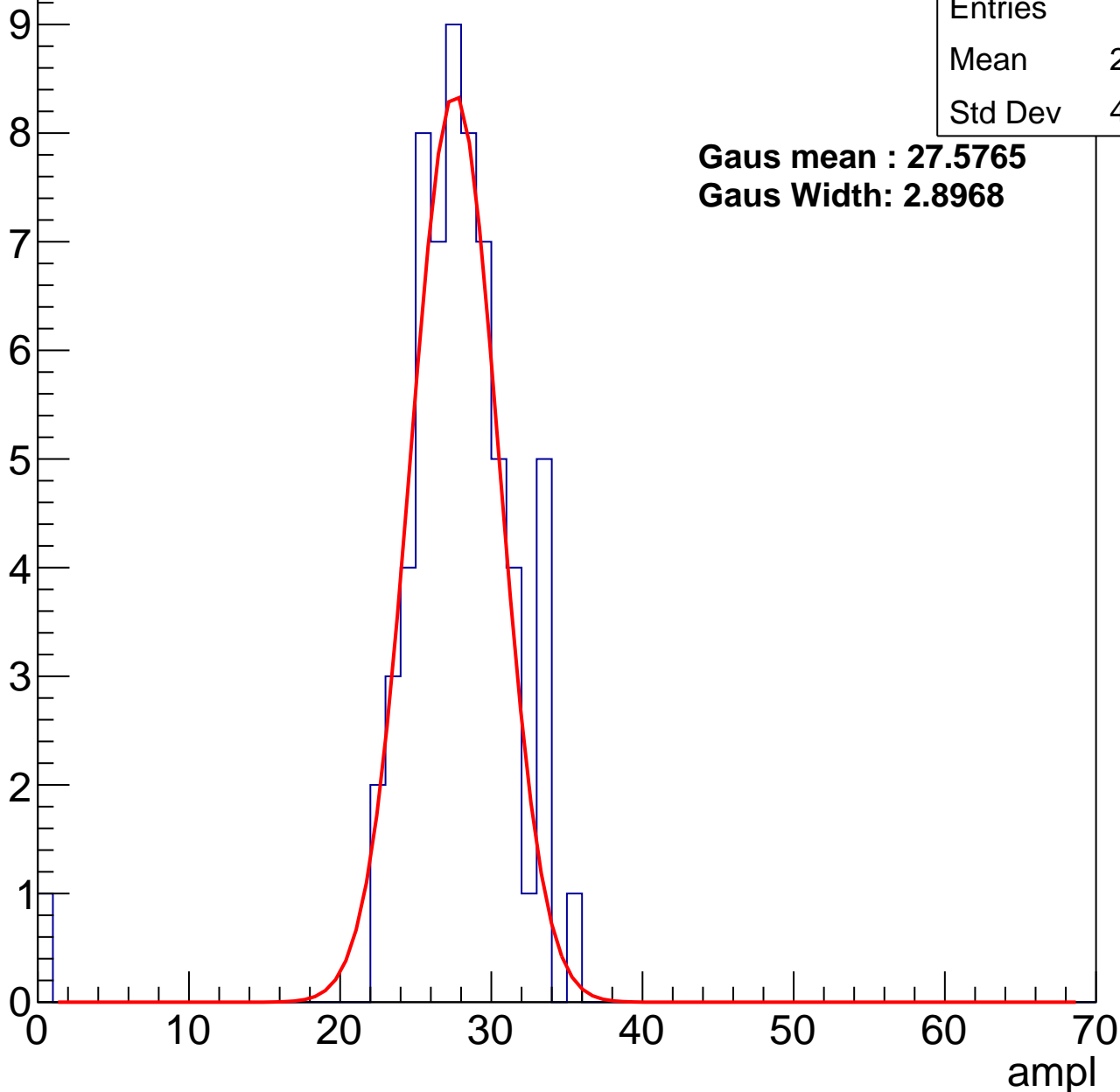
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	27.18
Std Dev	4.496

**Gaus mean : 27.5765**

**Gaus Width: 2.8968**



# B1L000S, U2-ch110, adc1

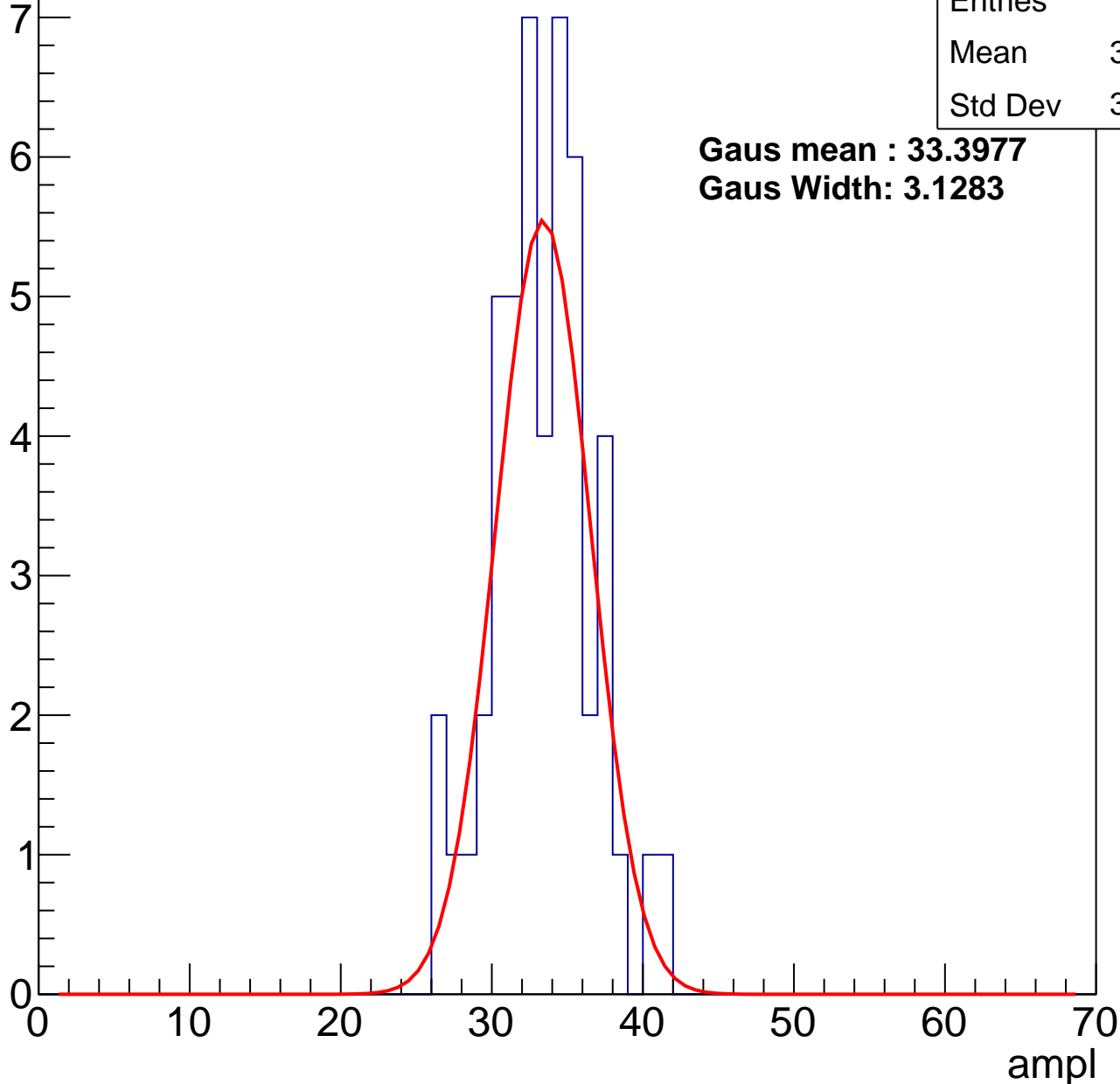
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	32.92
Std Dev	3.238

**Gaus mean : 33.3977**

**Gaus Width: 3.1283**



# B1L000S, U2-ch110, adc2

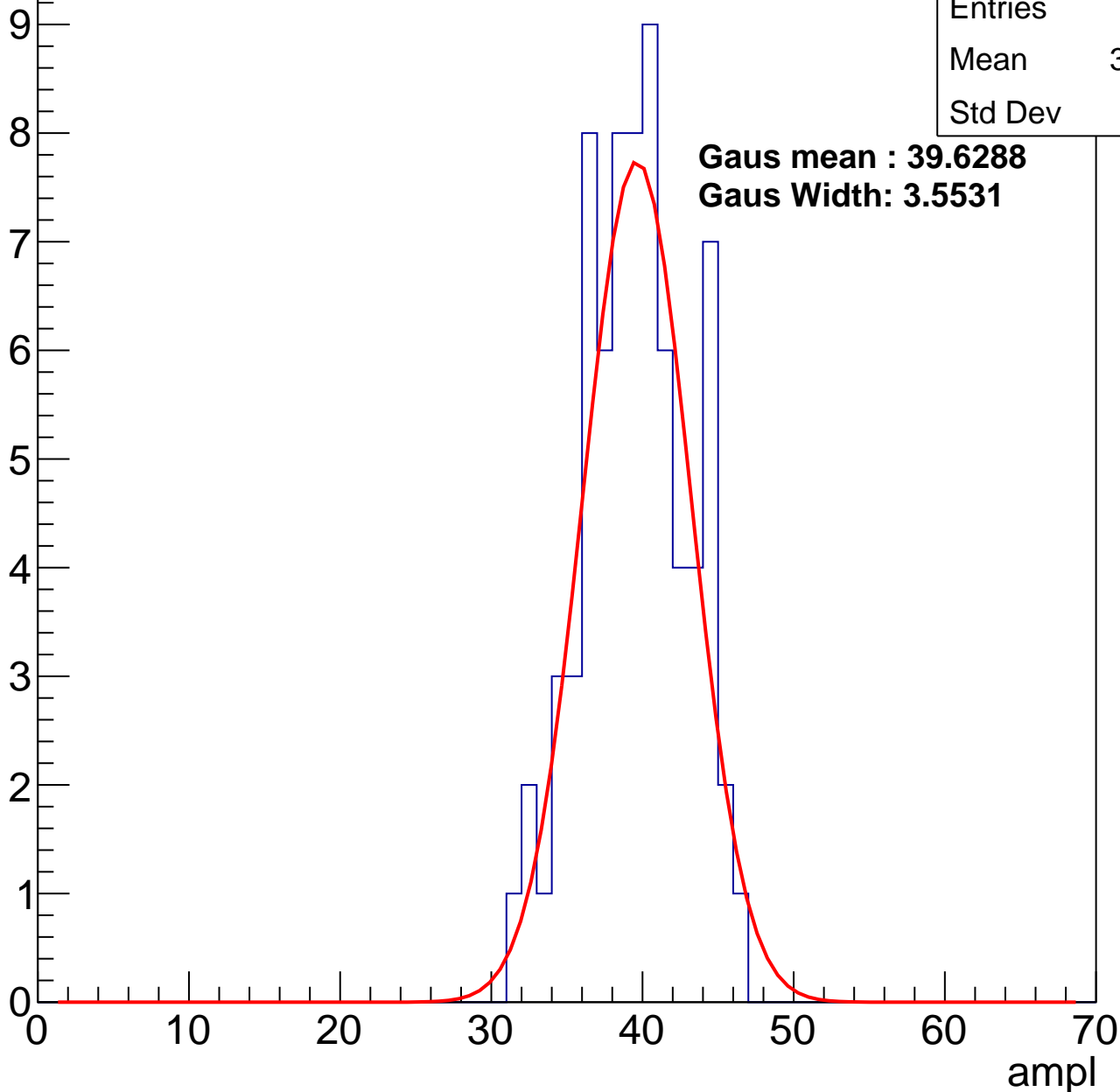
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	39.05
Std Dev	3.42

**Gaus mean : 39.6288**

**Gaus Width: 3.5531**

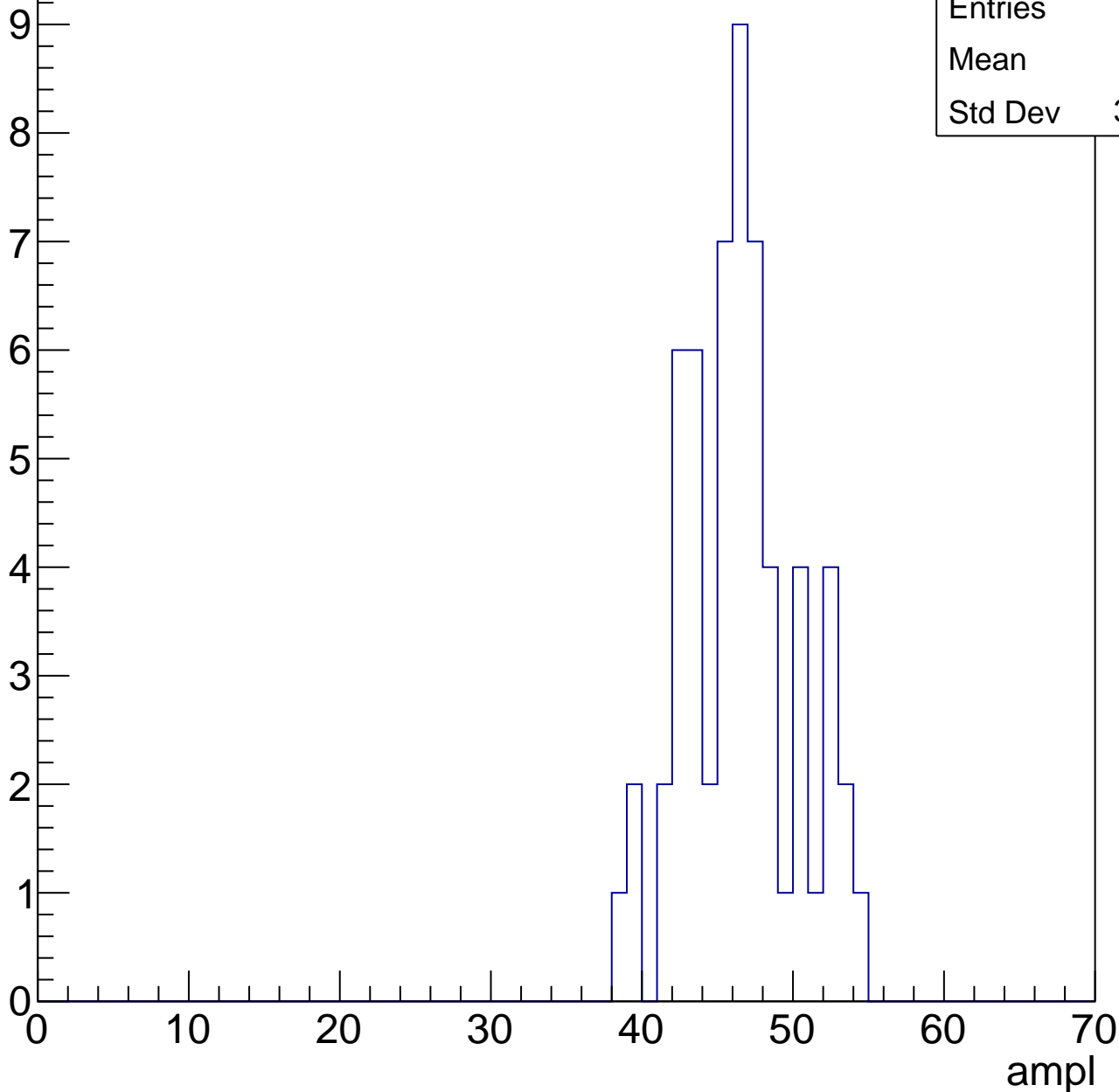


# B1L000S, U2-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	46
Std Dev	3.691

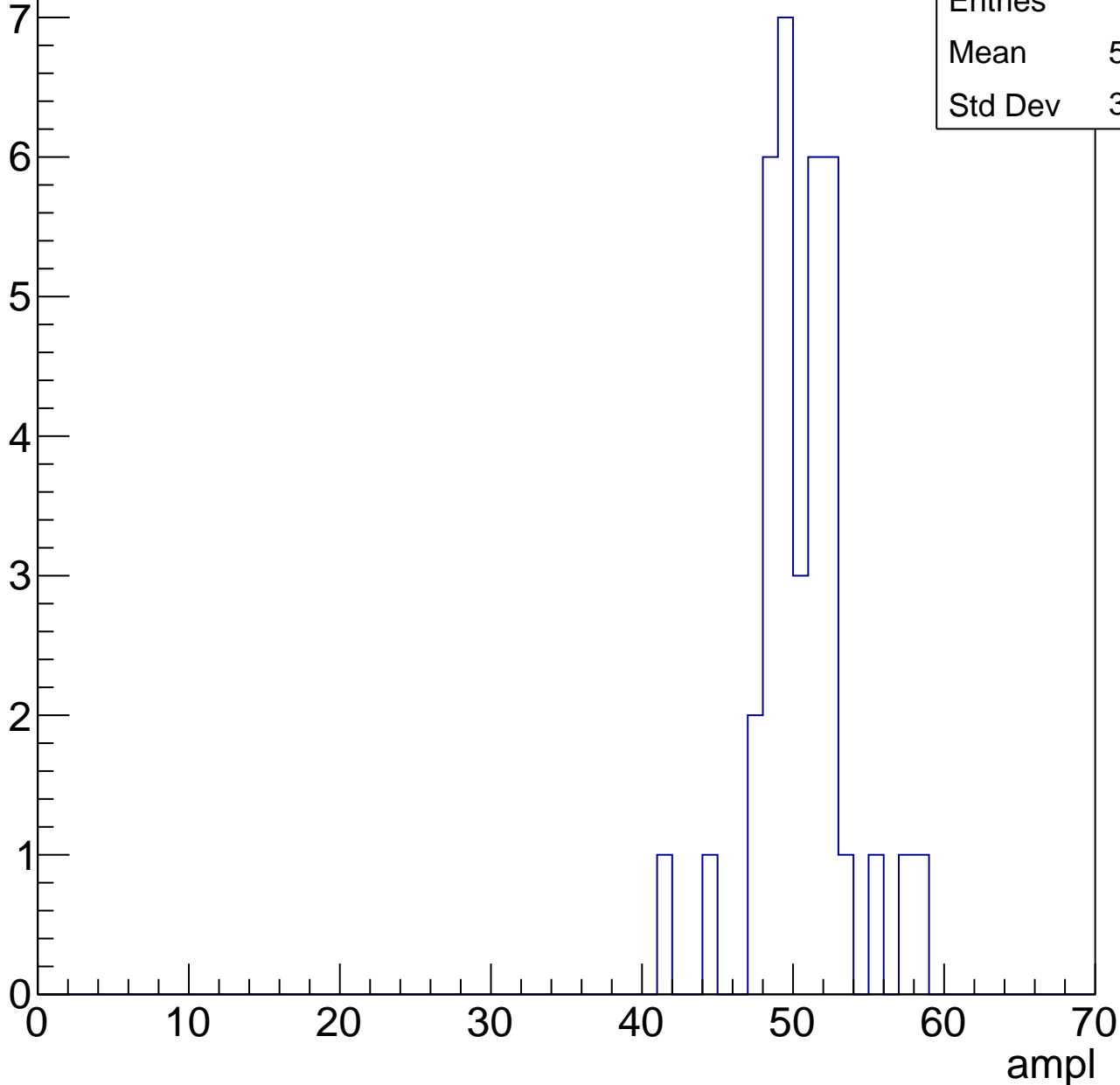


# B1L000S, U2-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

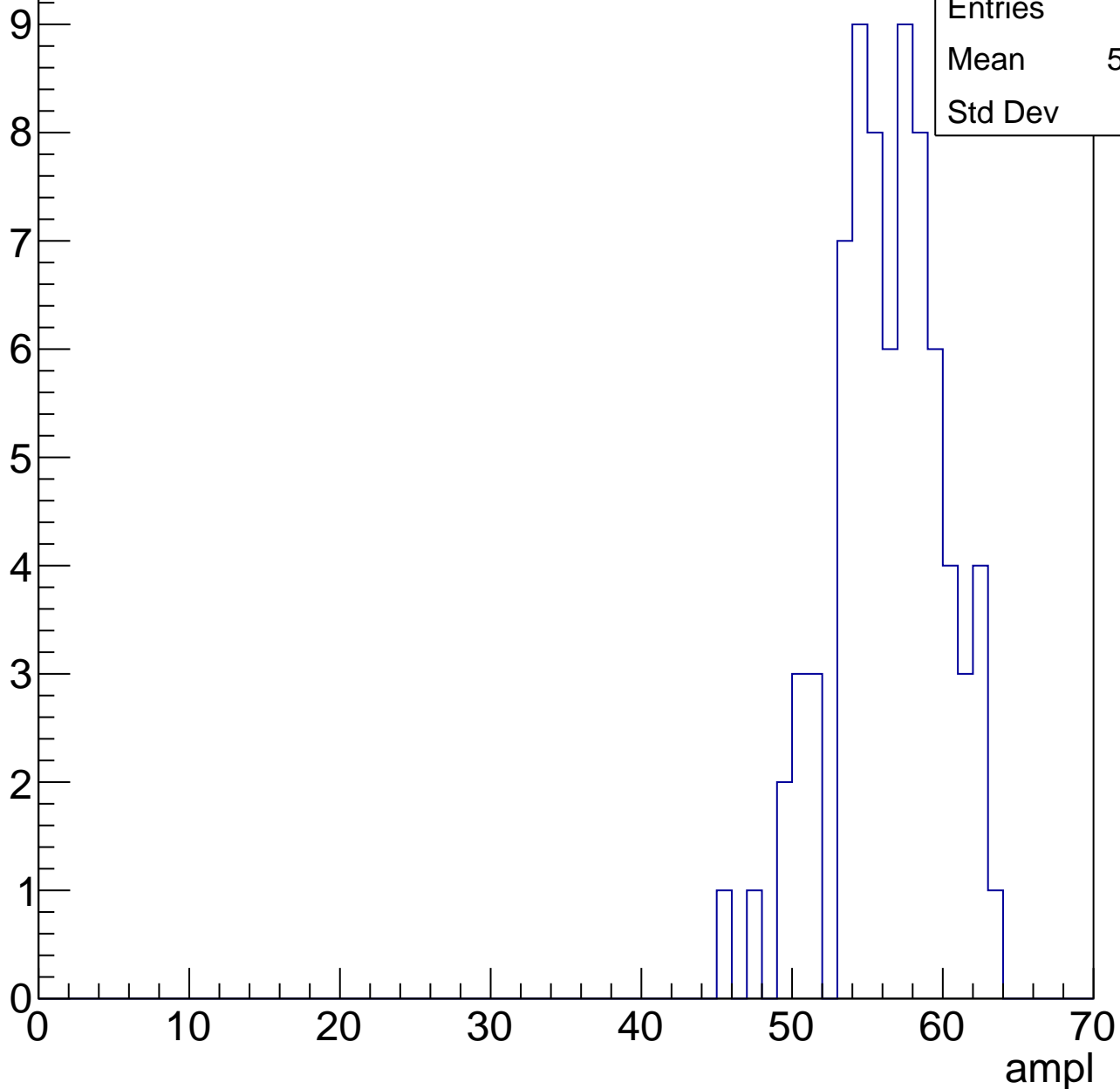
Entries	36
Mean	50.03
Std Dev	3.087



# B1L000S, U2-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



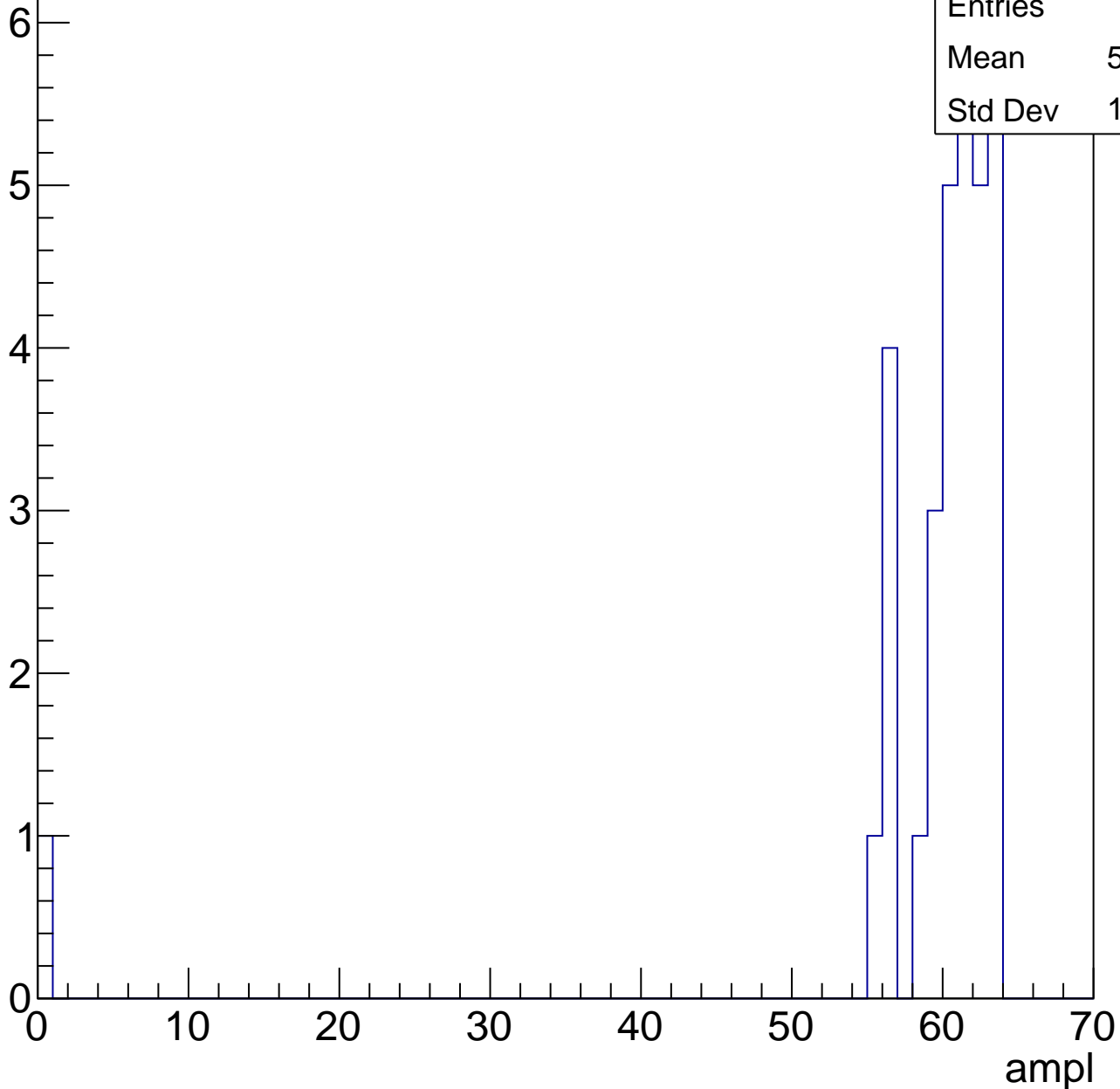
Entries	75
Mean	55.88
Std Dev	3.68

# B1L000S, U2-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	58.38
Std Dev	10.74

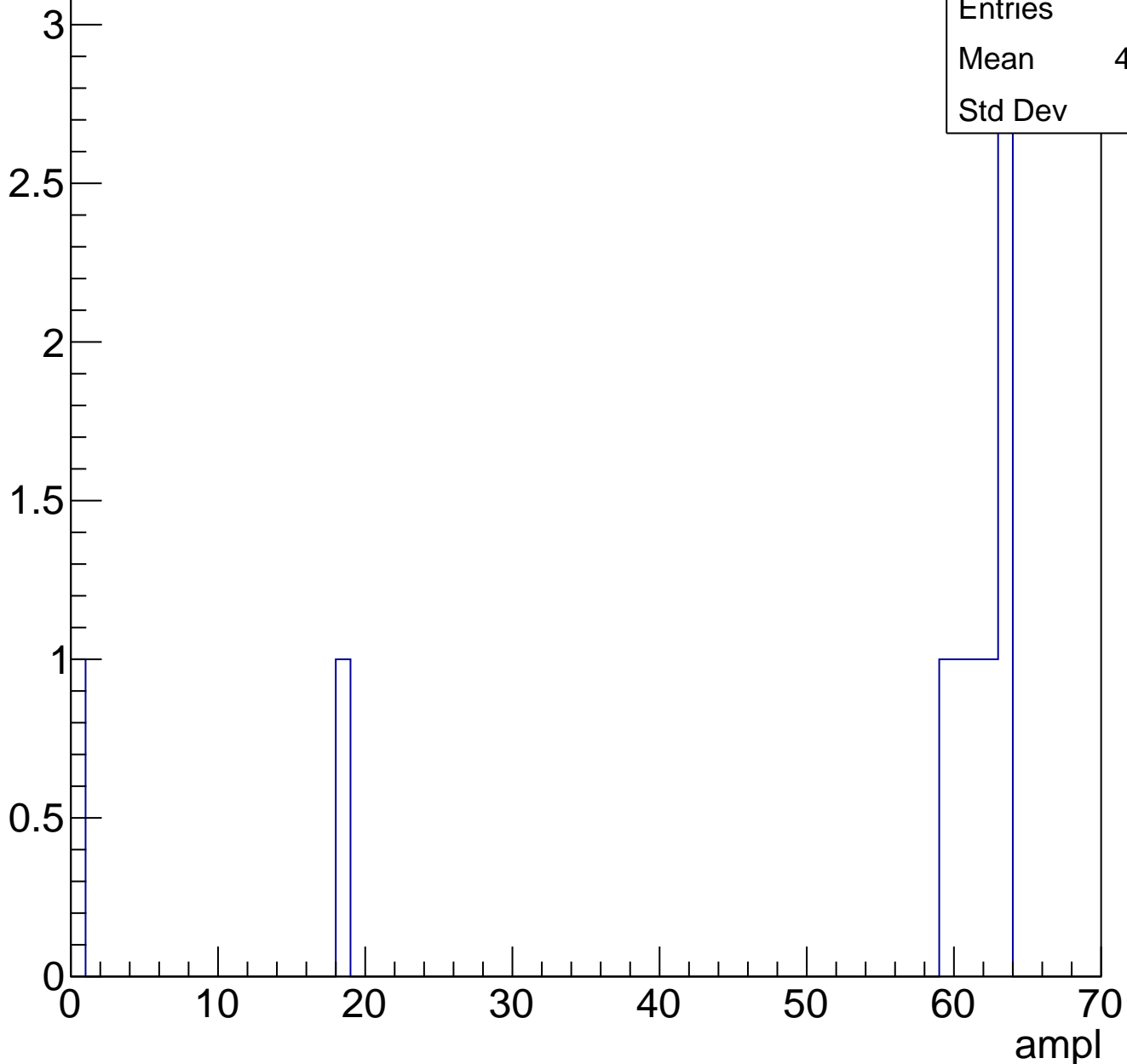




# B1L000S, U2-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch111, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	89
Mean	27.54
Std Dev	4.722

**Gaus mean : 28.2440**

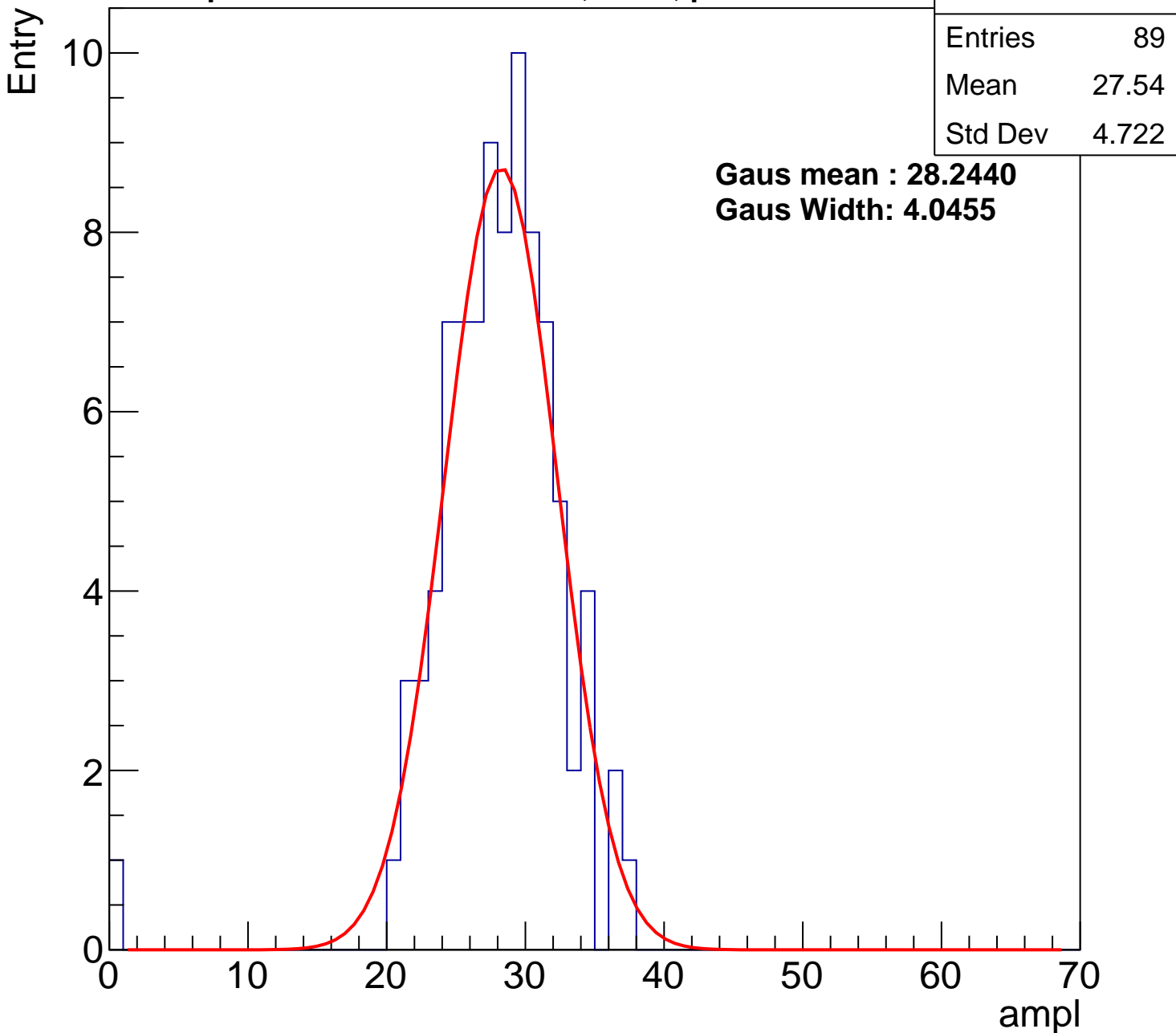
**Gaus Width: 4.0455**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch111, adc1

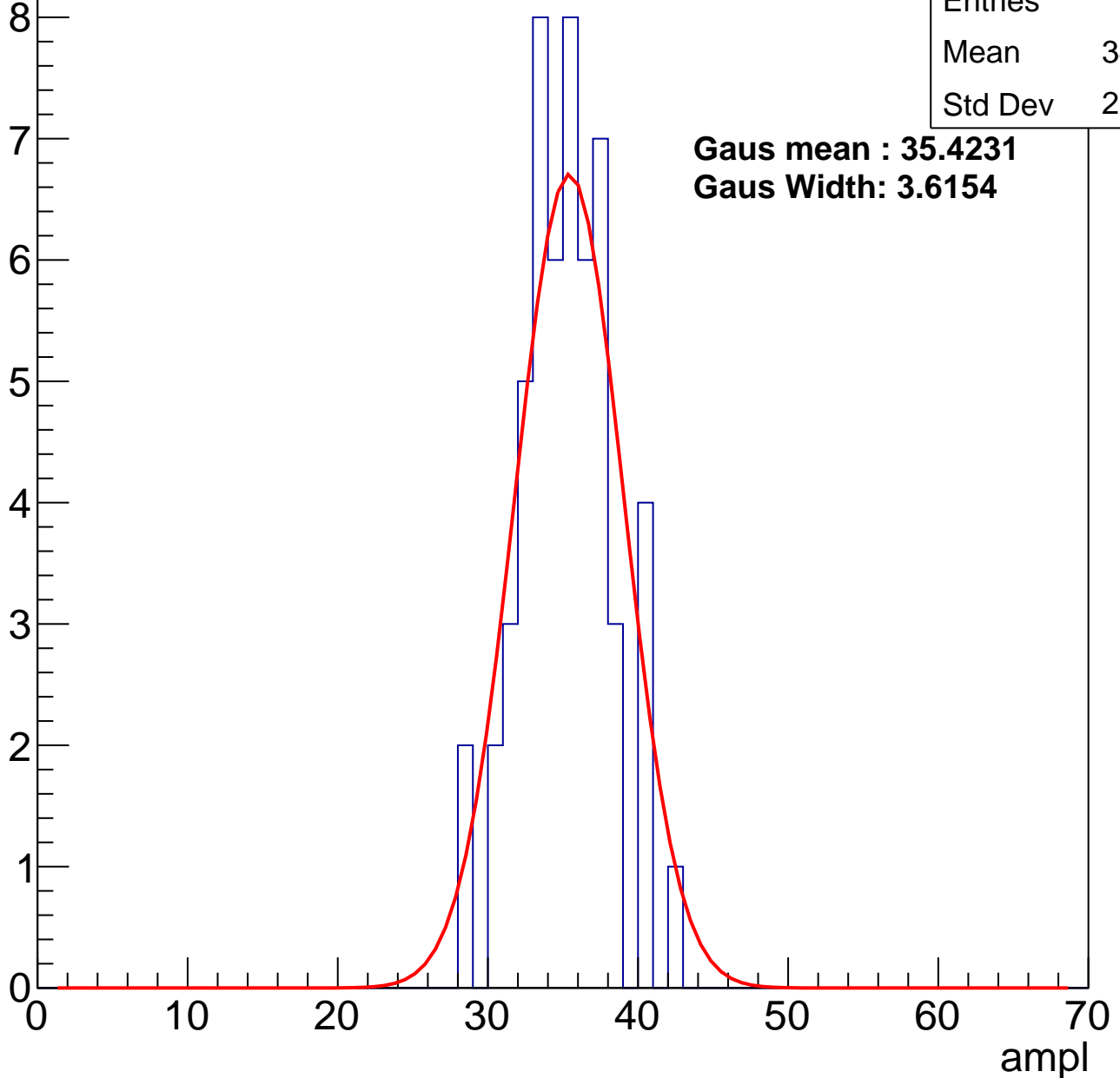
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34.69
Std Dev	2.966

**Gaus mean : 35.4231**

**Gaus Width: 3.6154**



# B1L000S, U2-ch111, adc2

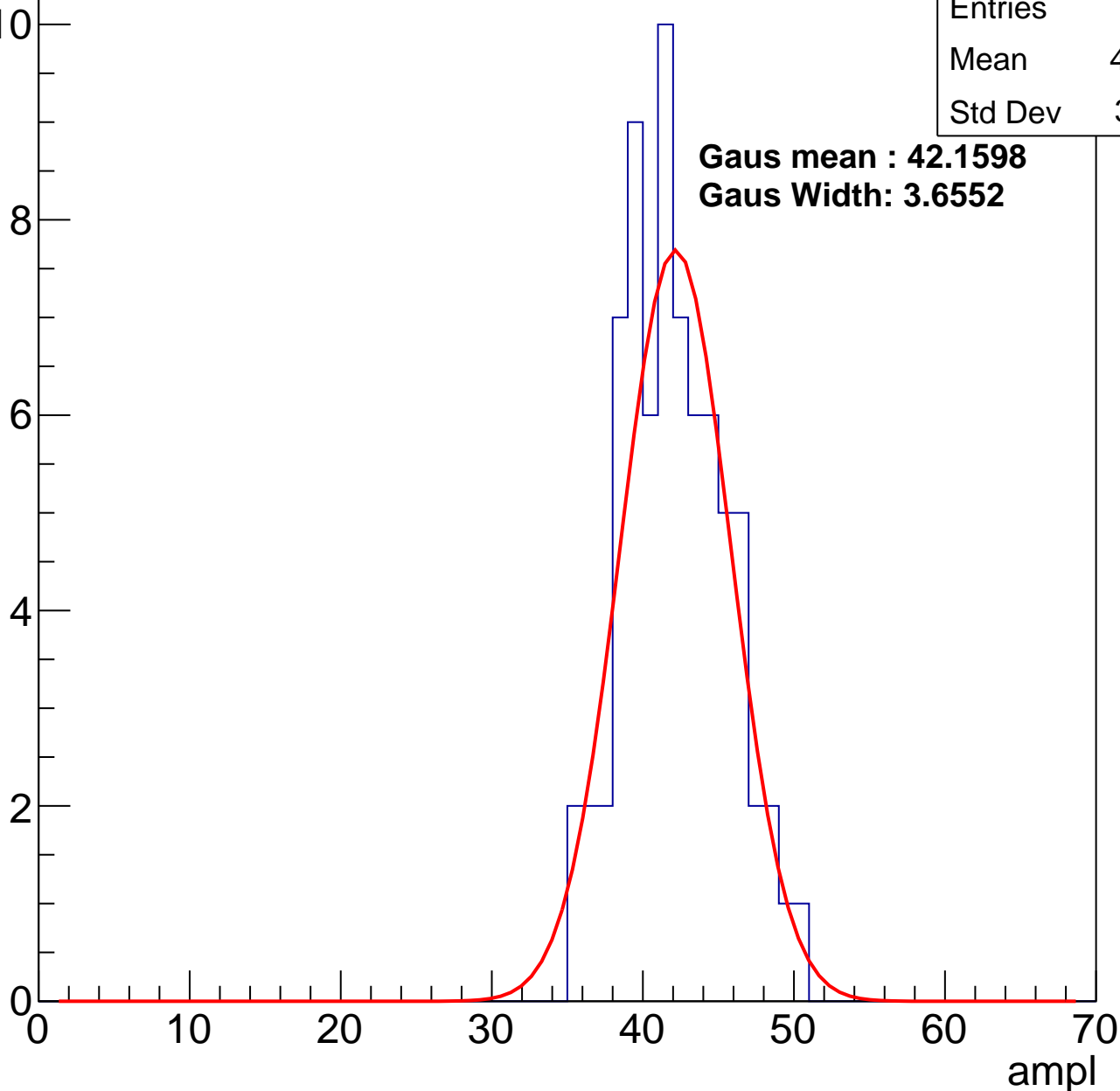
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	41.68
Std Dev	3.371

**Gaus mean : 42.1598**

**Gaus Width: 3.6552**

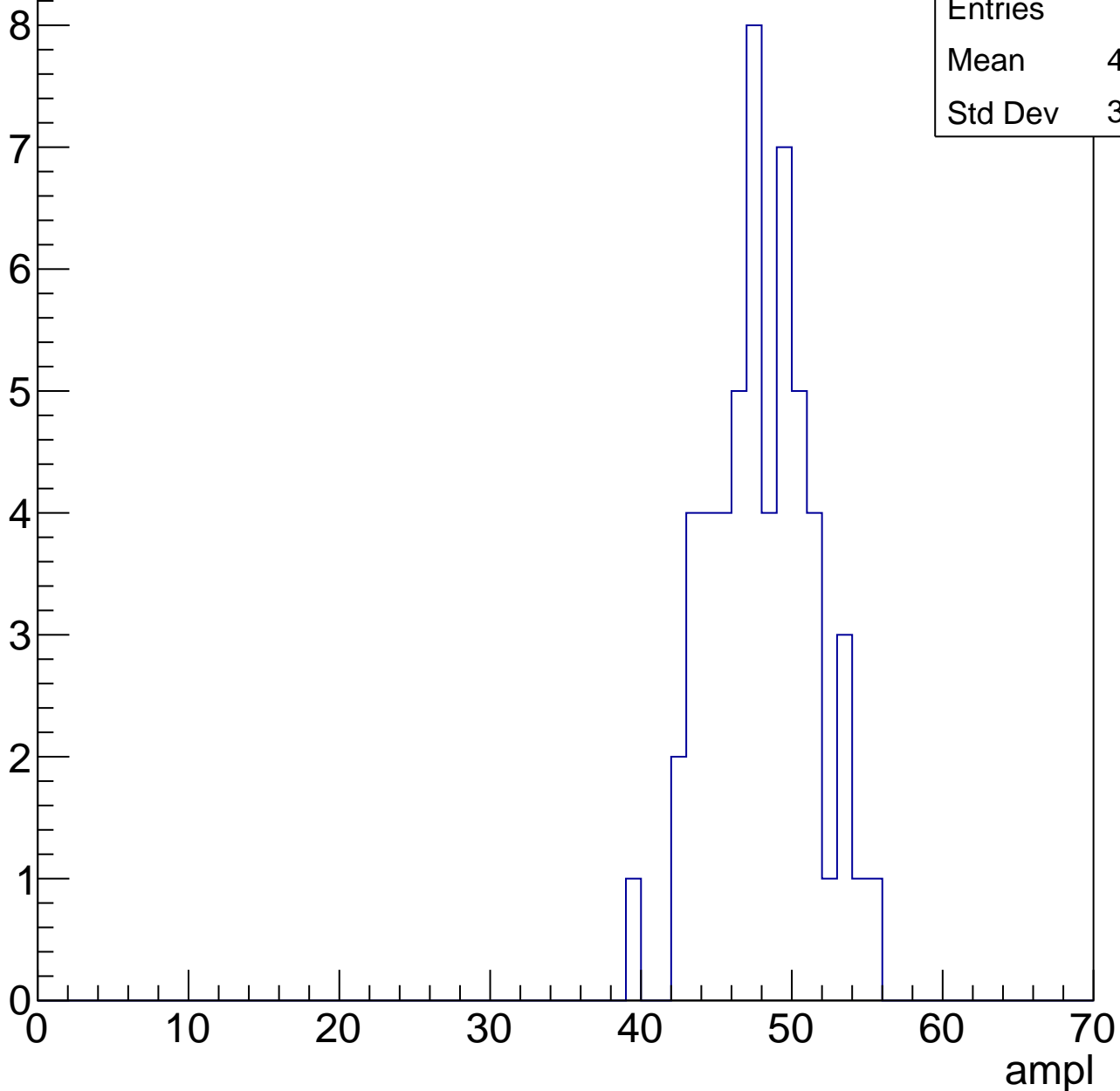


# B1L000S, U2-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	47.52
Std Dev	3.354

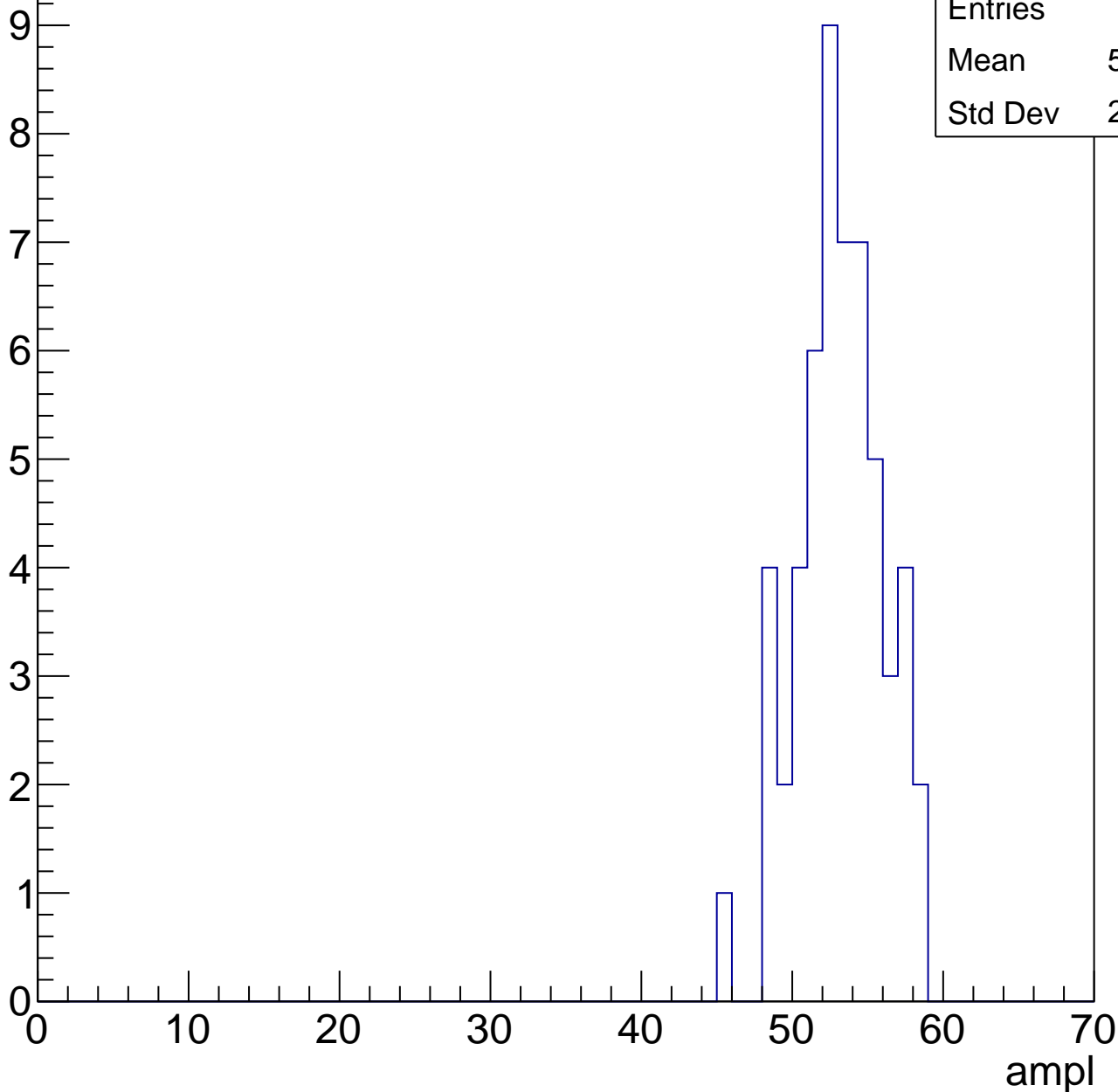


# B1L000S, U2-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

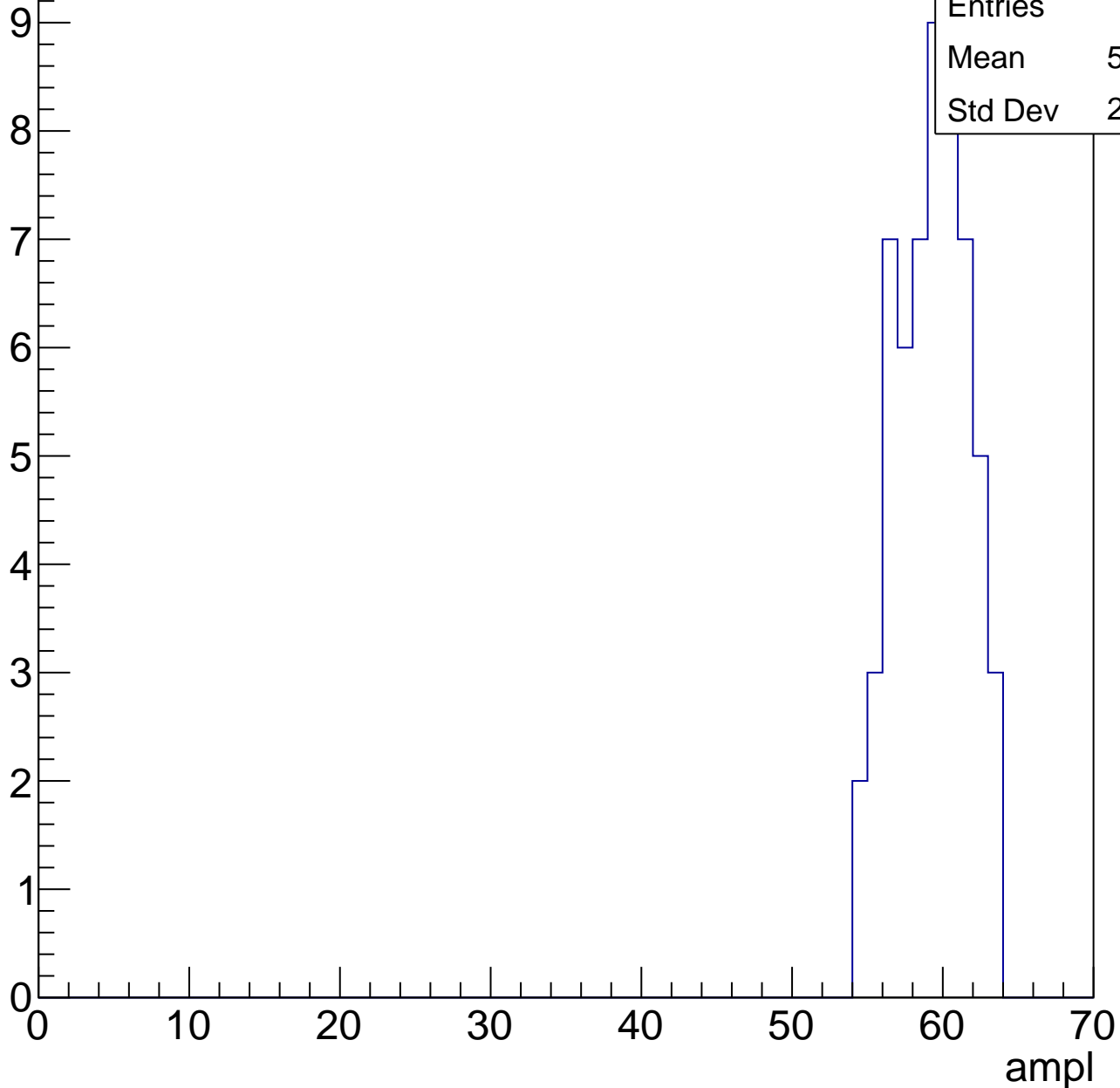
Entries	54
Mean	52.69
Std Dev	2.814



# B1L000S, U2-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

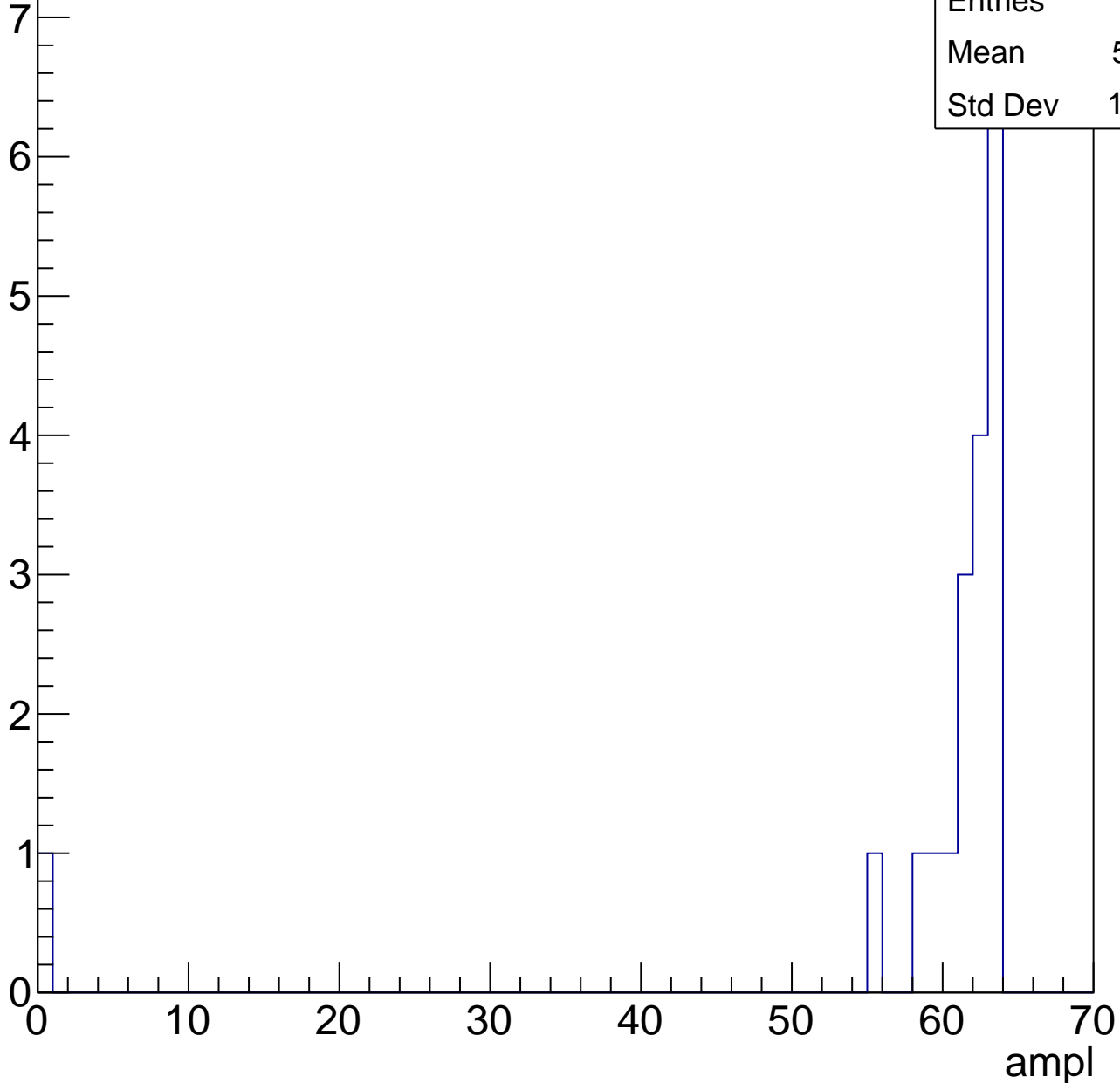


# B1L000S, U2-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.11
Std Dev	13.85





# B1L000S, U2-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch112, adc0

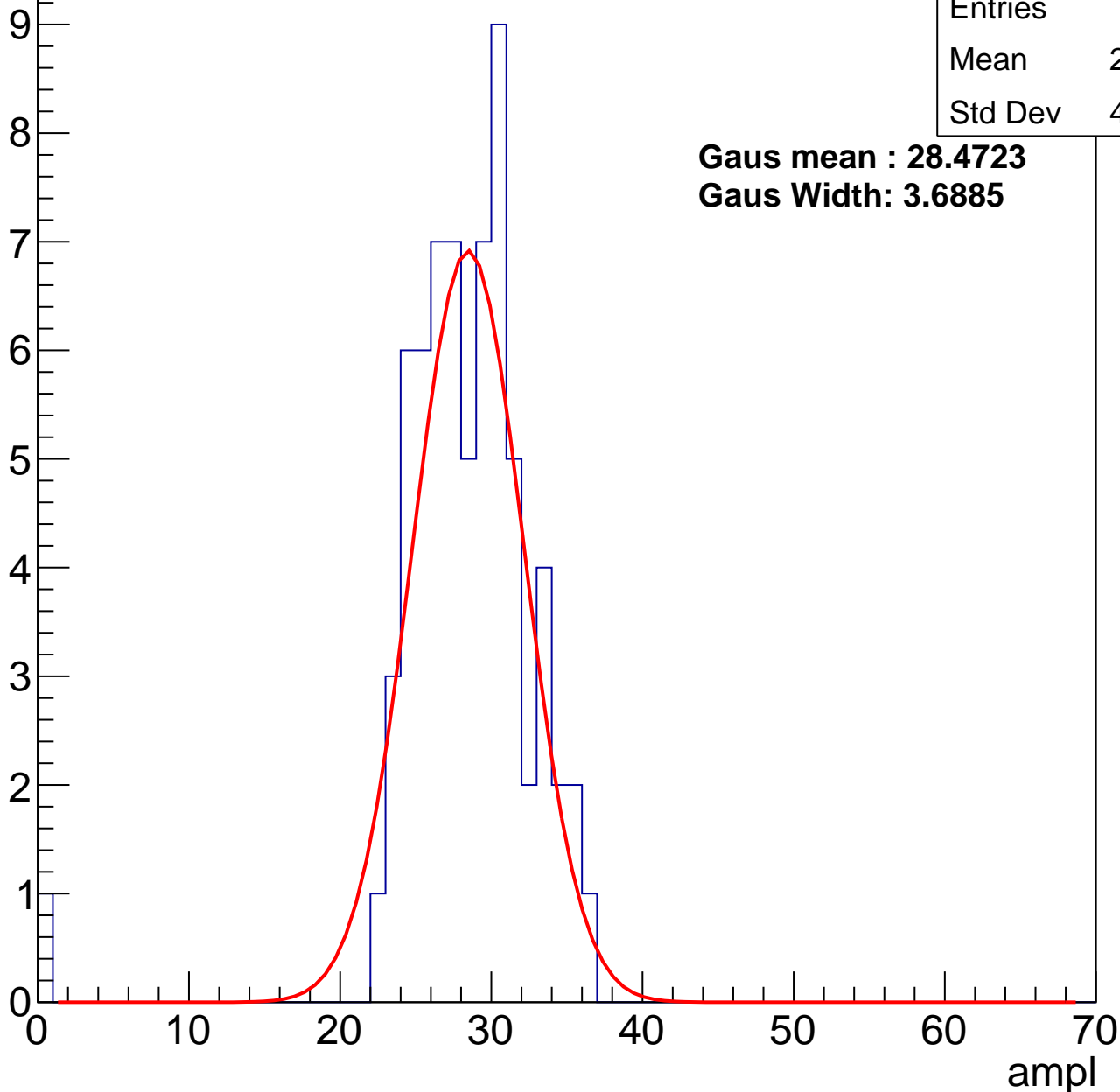
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	27.85
Std Dev	4.747

**Gaus mean : 28.4723**

**Gaus Width: 3.6885**



# B1L000S, U2-ch112, adc1

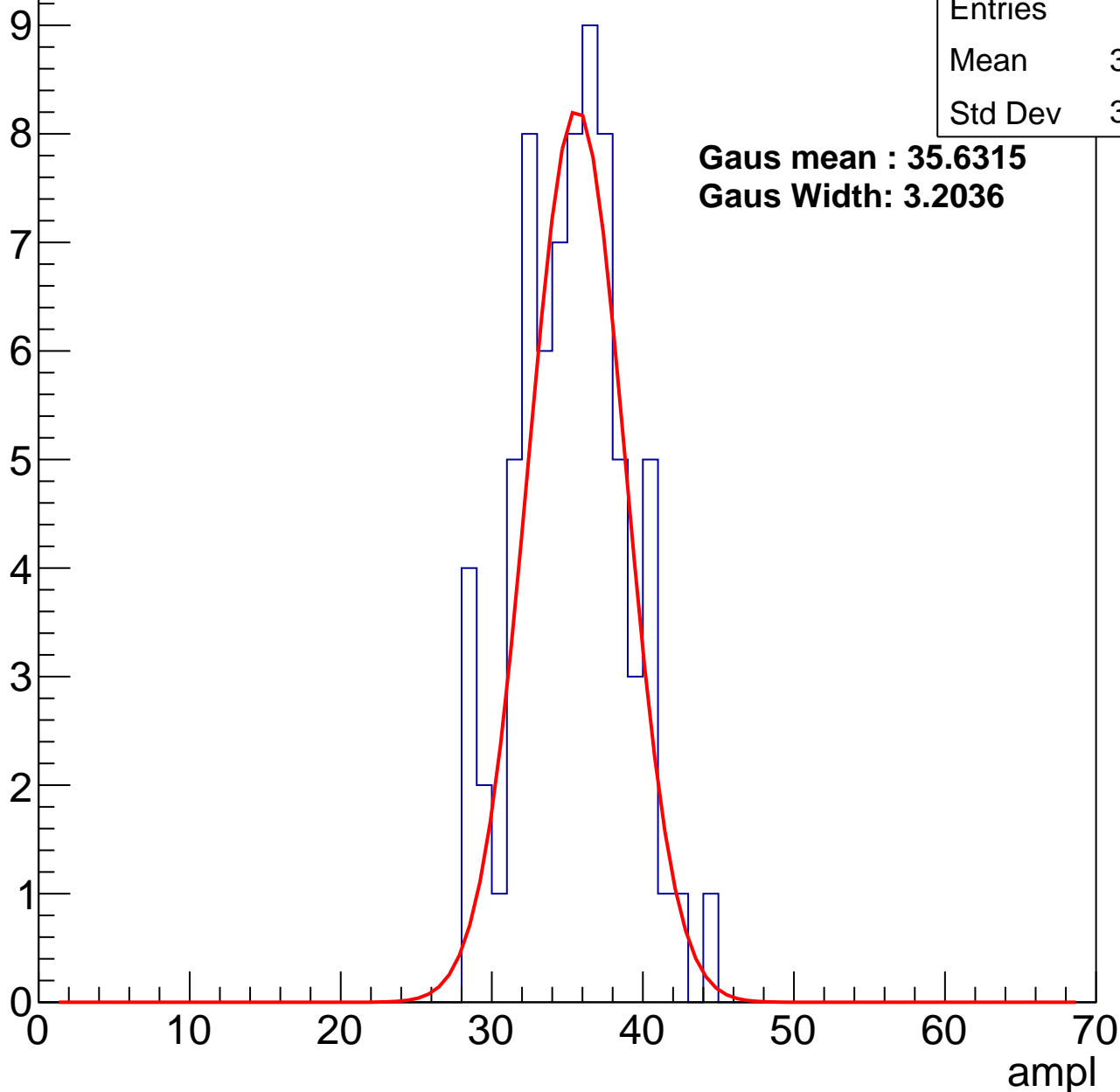
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	34.88
Std Dev	3.487

**Gaus mean : 35.6315**

**Gaus Width: 3.2036**



# B1L000S, U2-ch112, adc2

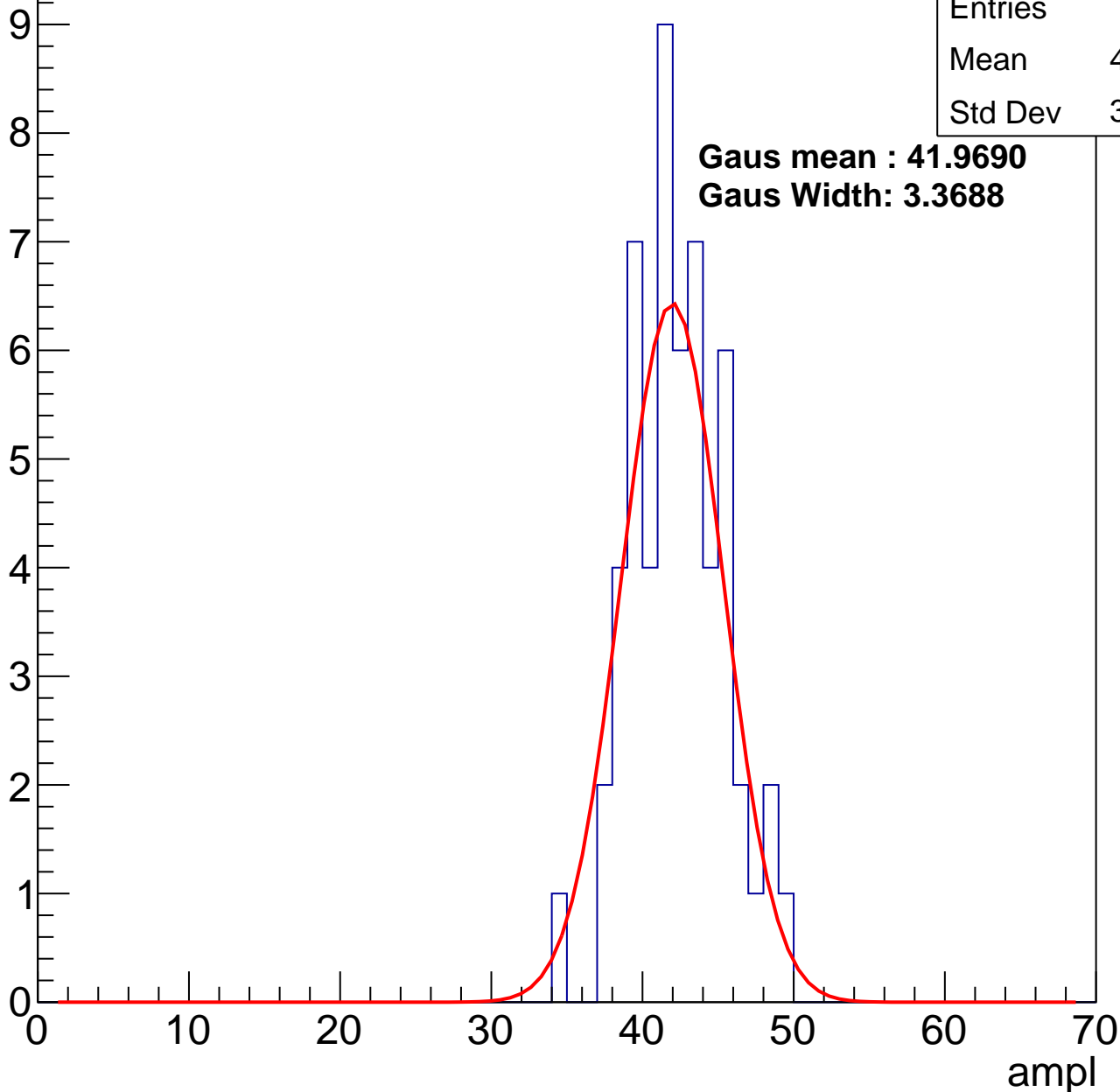
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.88
Std Dev	3.048

**Gaus mean : 41.9690**

**Gaus Width: 3.3688**

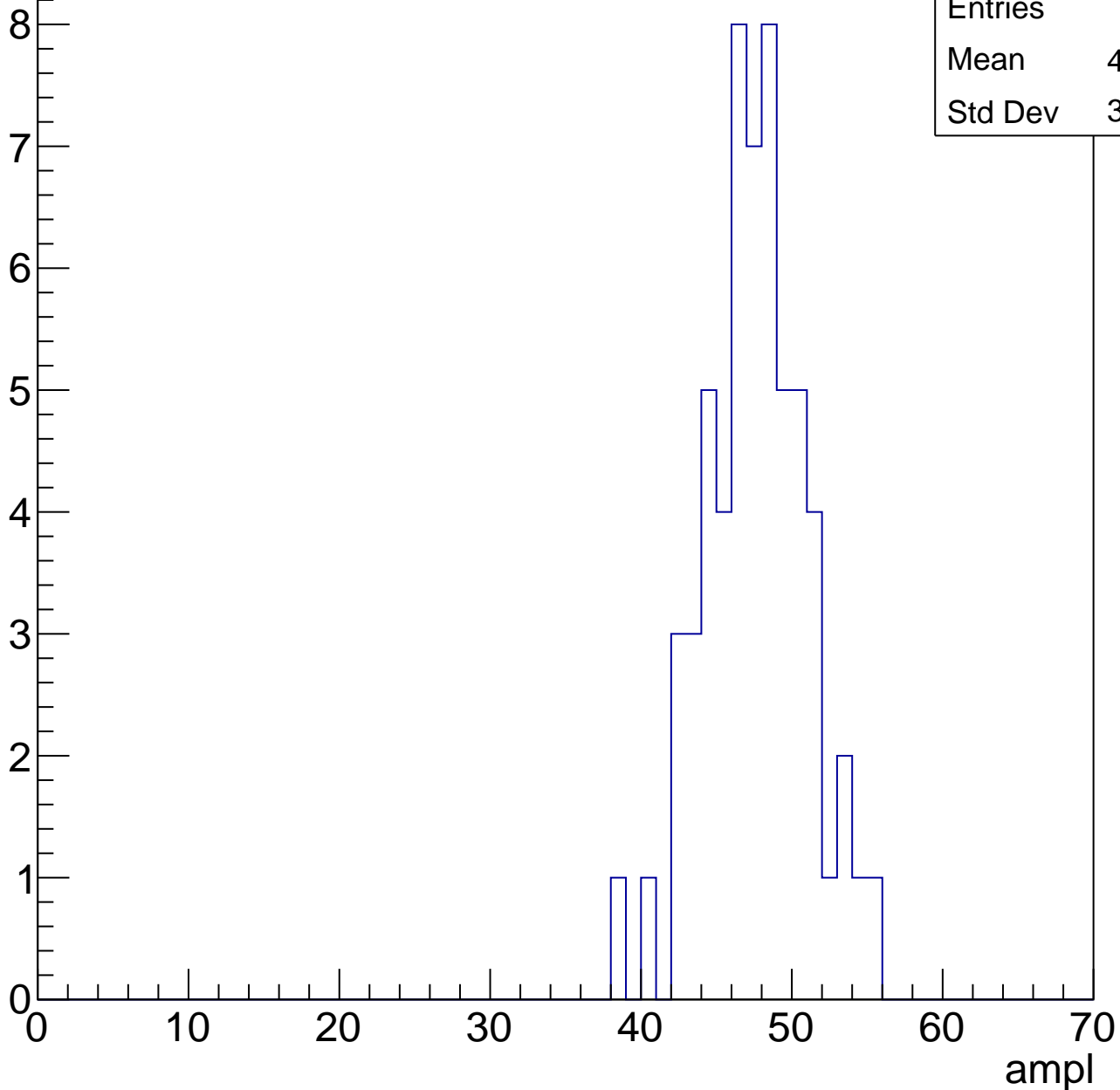


# B1L000S, U2-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.12
Std Dev	3.365

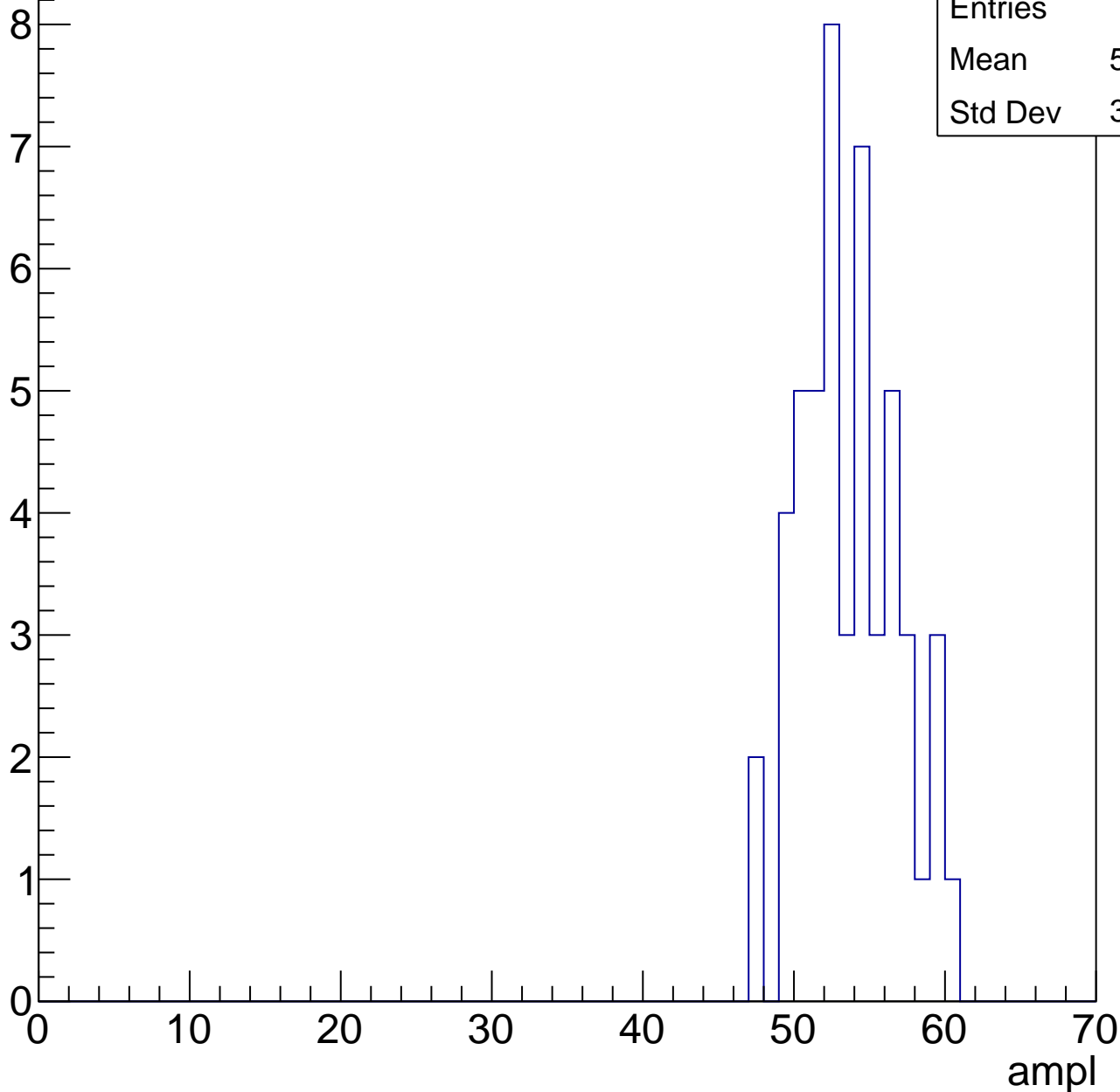


# B1L000S, U2-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	53.18
Std Dev	3.167

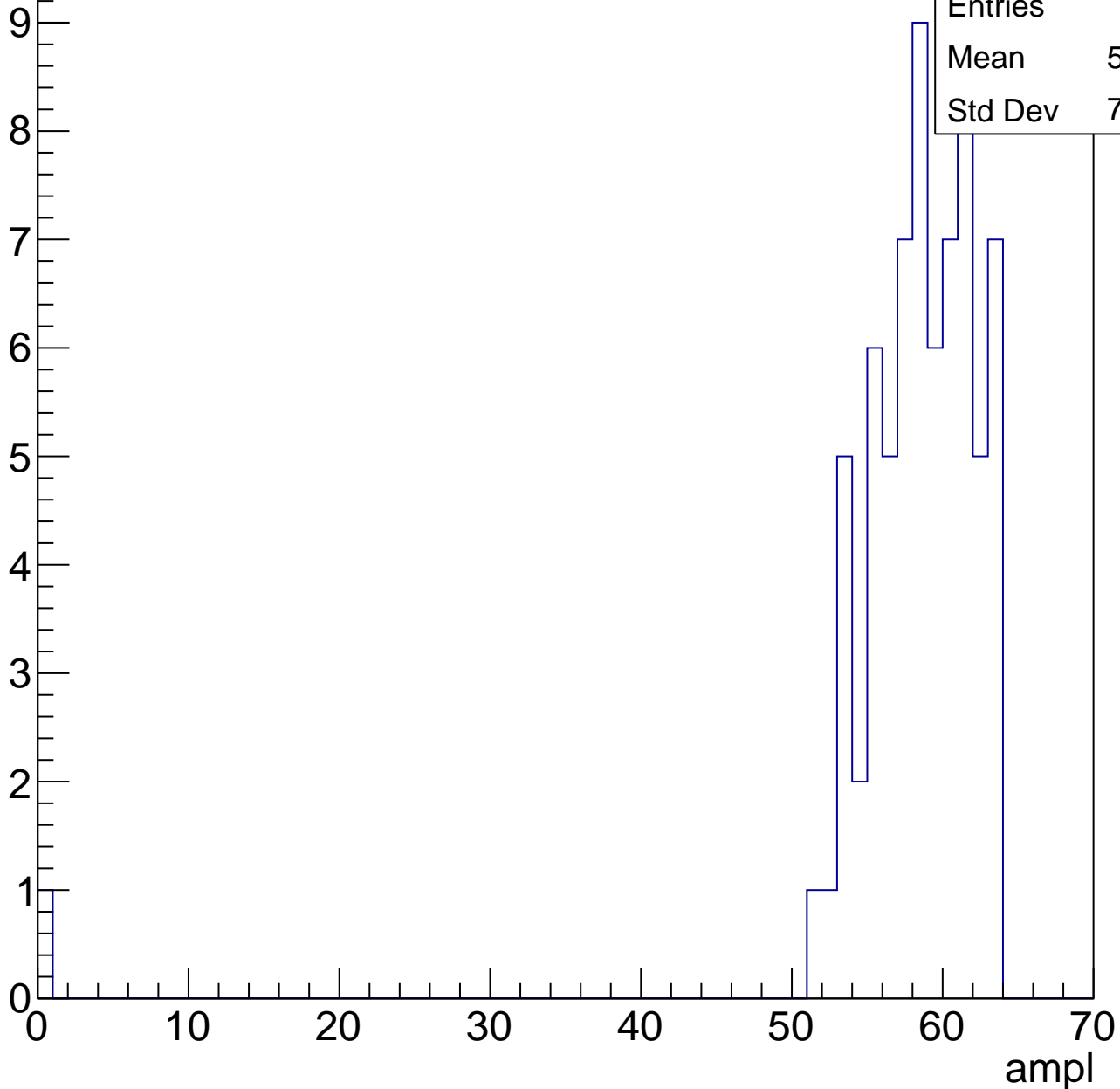


# B1L000S, U2-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	57.43
Std Dev	7.583

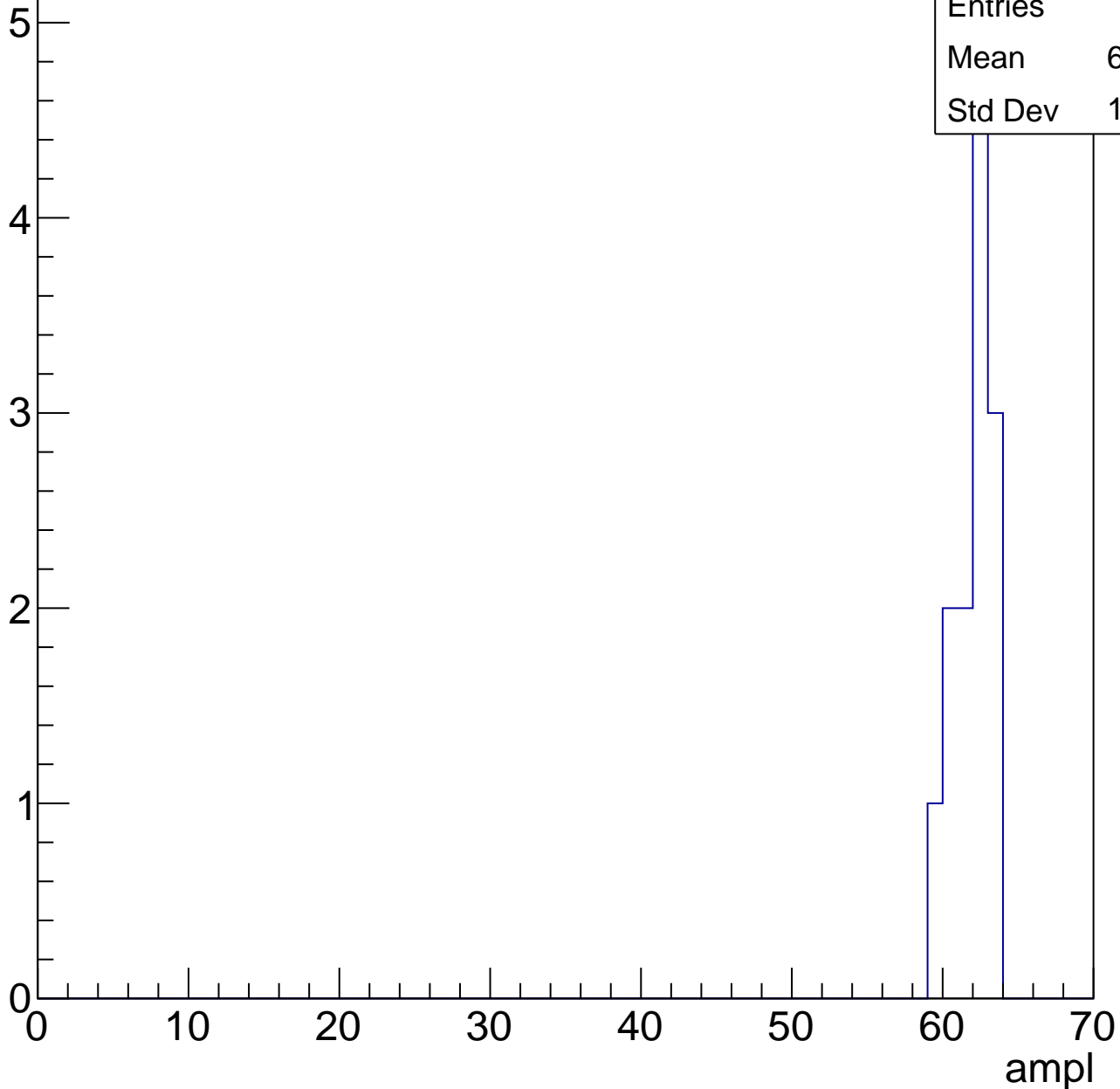


# B1L000S, U2-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	61.54
Std Dev	1.216





# B1L000S, U2-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch113, adc0

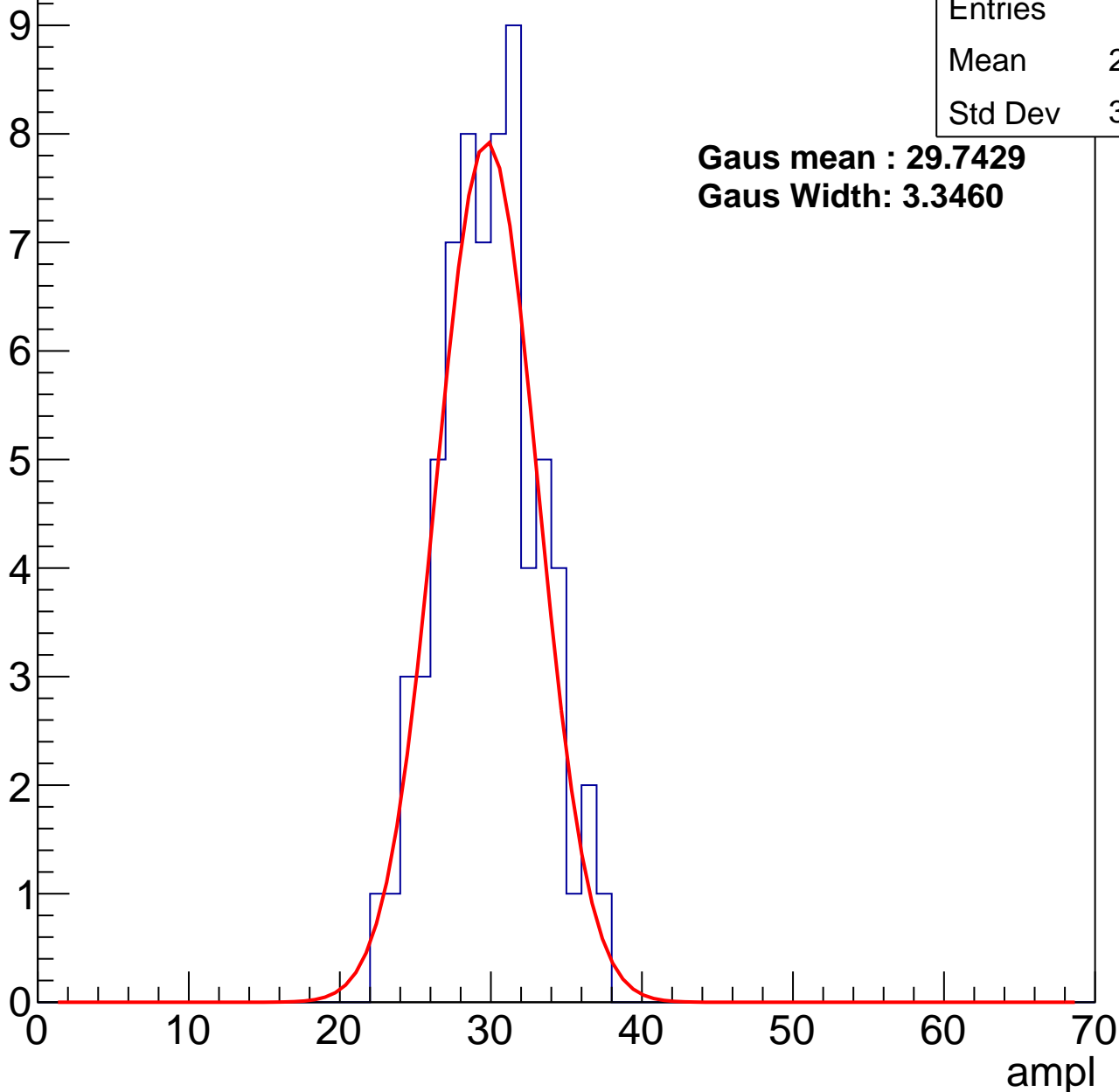
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.42
Std Dev	3.263

**Gaus mean : 29.7429**

**Gaus Width: 3.3460**



# B1L000S, U2-ch113, adc1

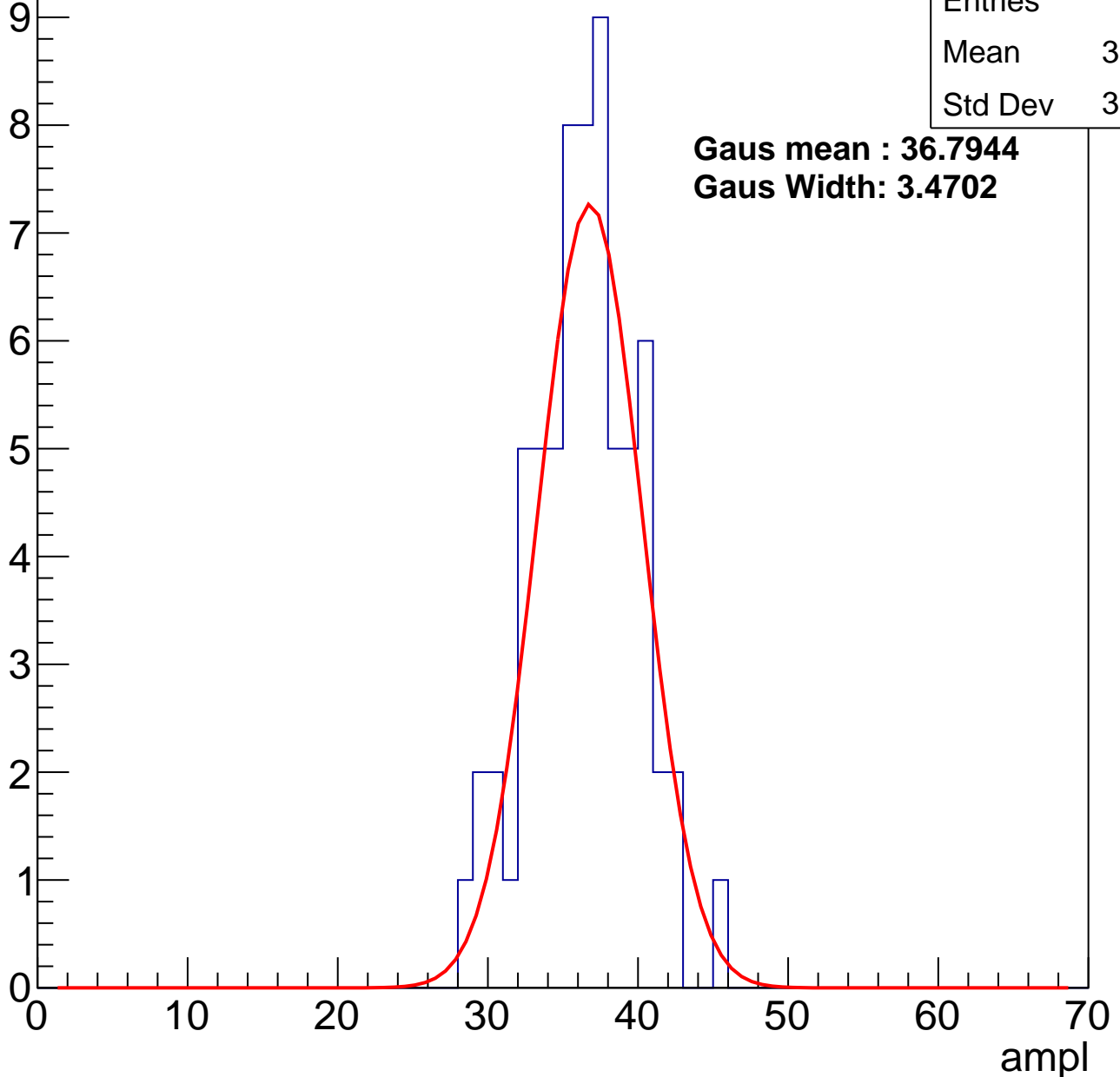
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.96
Std Dev	3.423

**Gaus mean : 36.7944**

**Gaus Width: 3.4702**



# B1L000S, U2-ch113, adc2

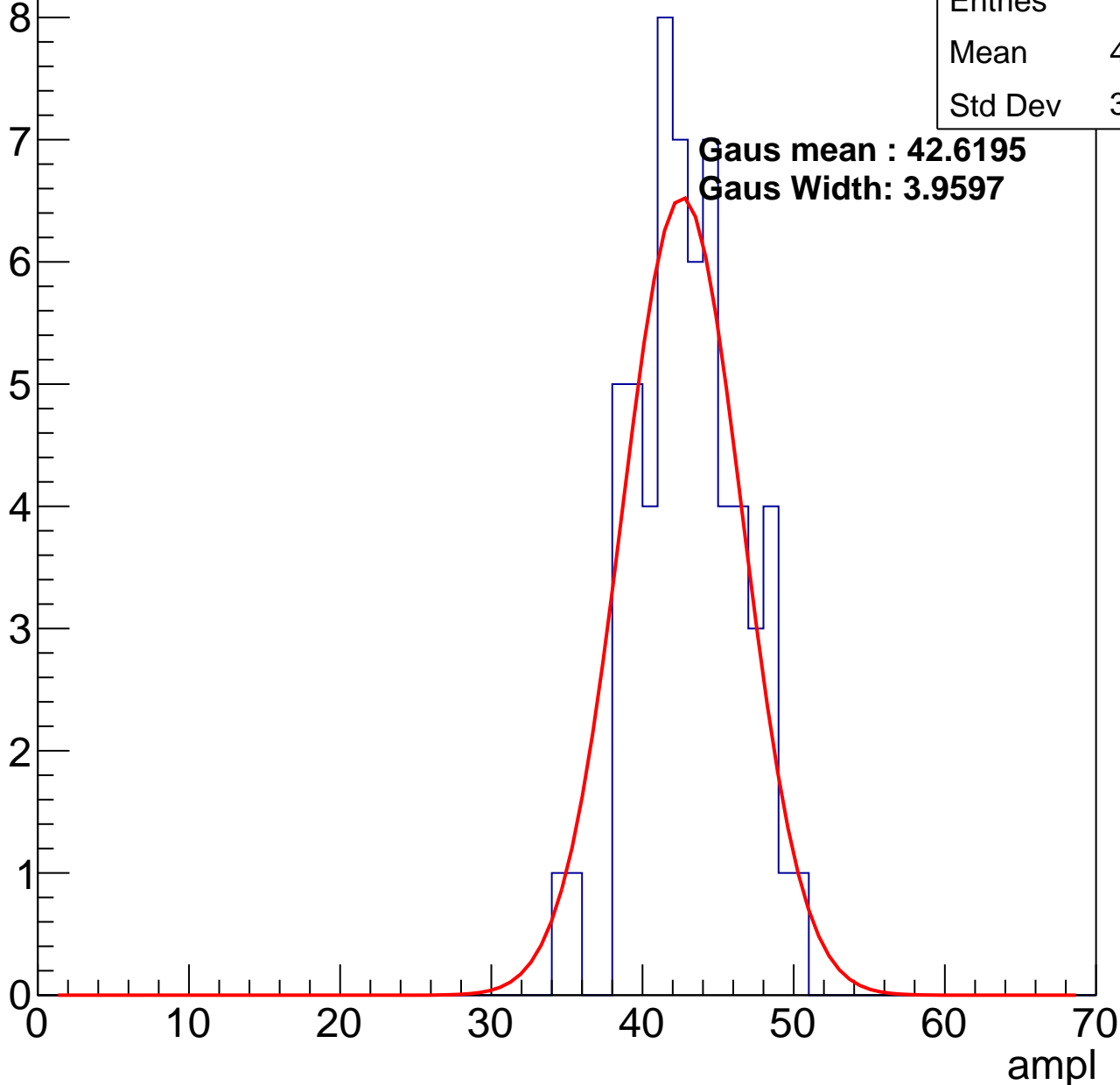
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.59
Std Dev	3.404

**Gaus mean : 42.6195**

**Gaus Width: 3.9597**

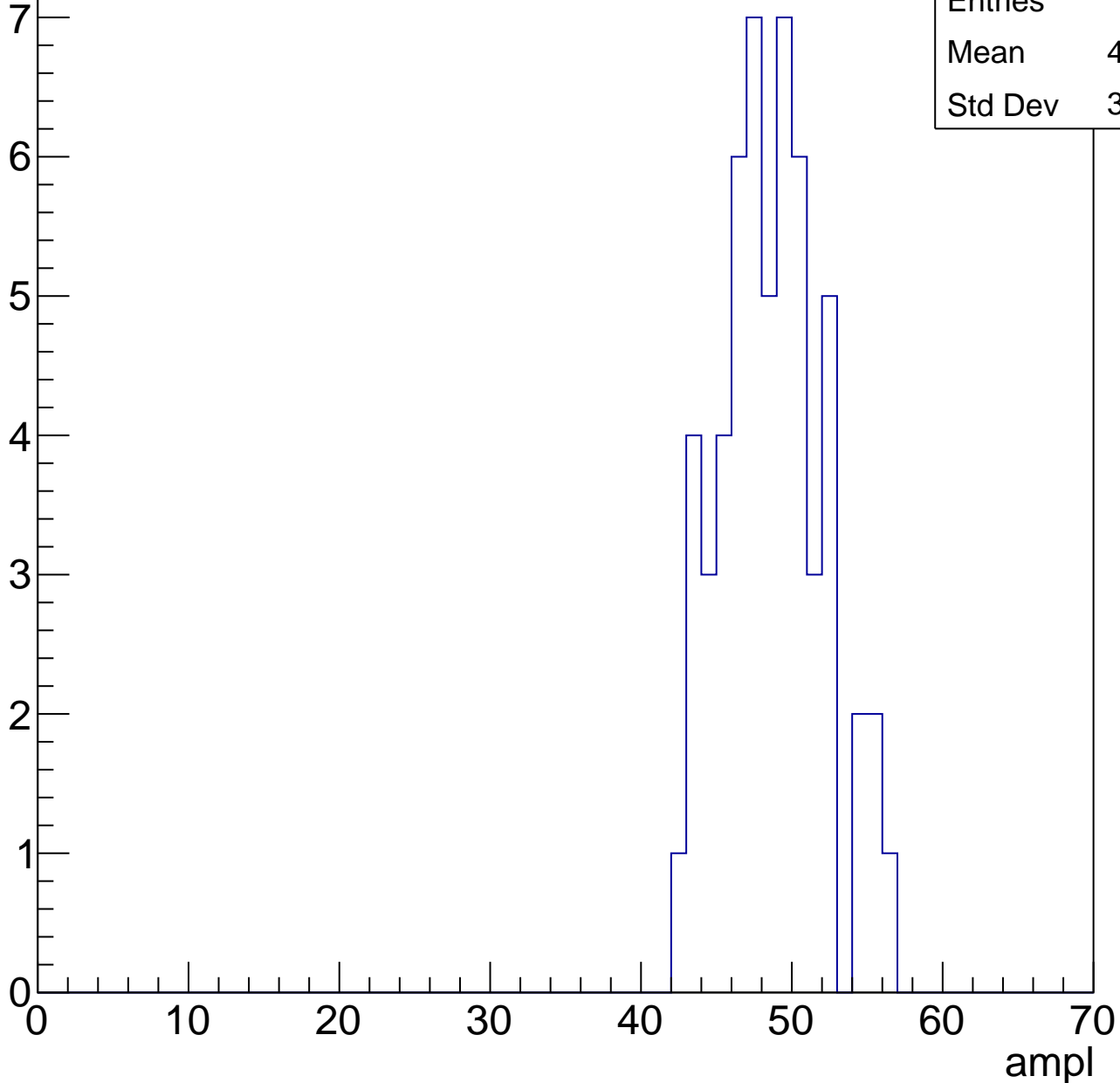


# B1L000S, U2-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	48.23
Std Dev	3.327

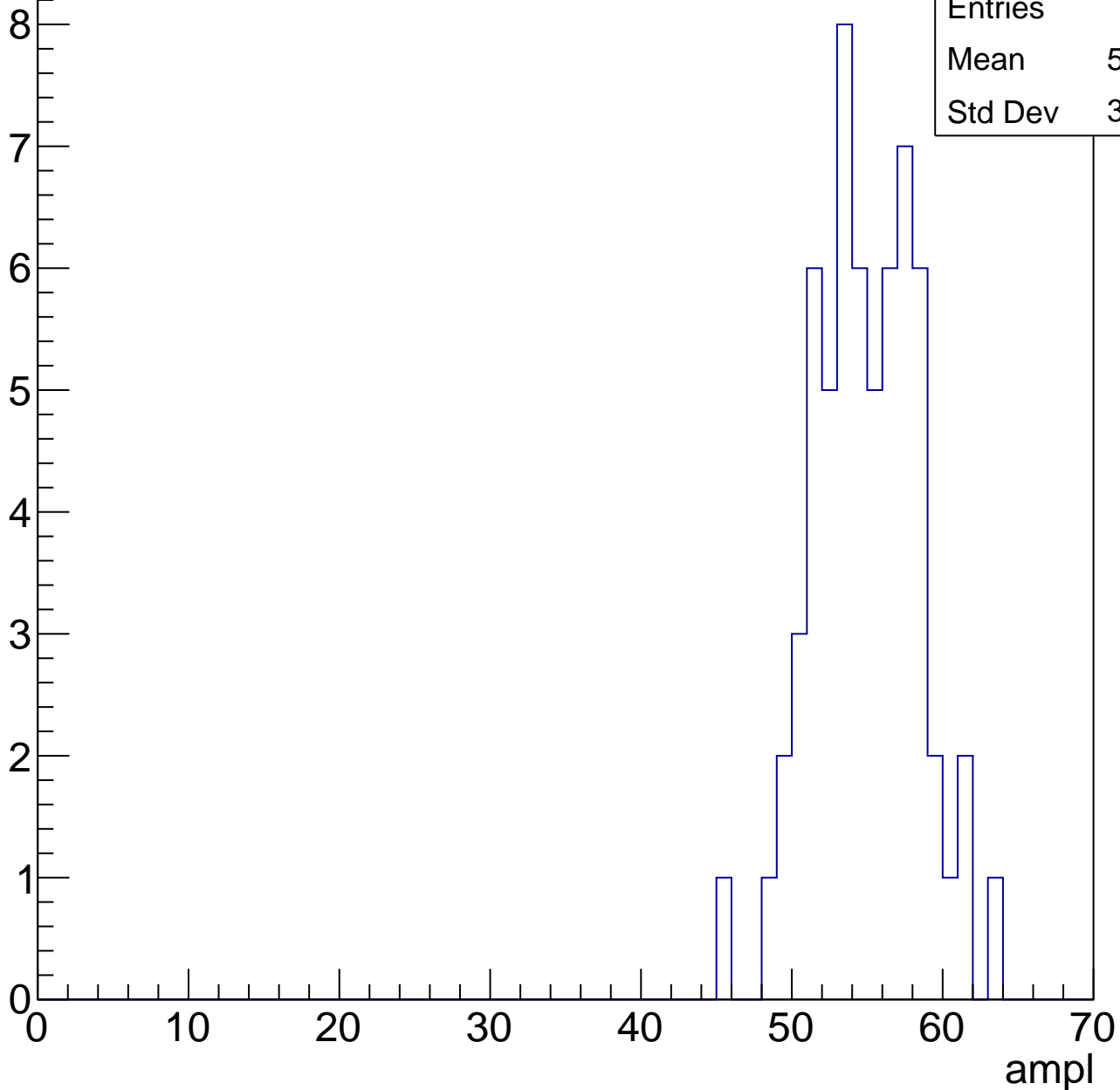


# B1L000S, U2-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

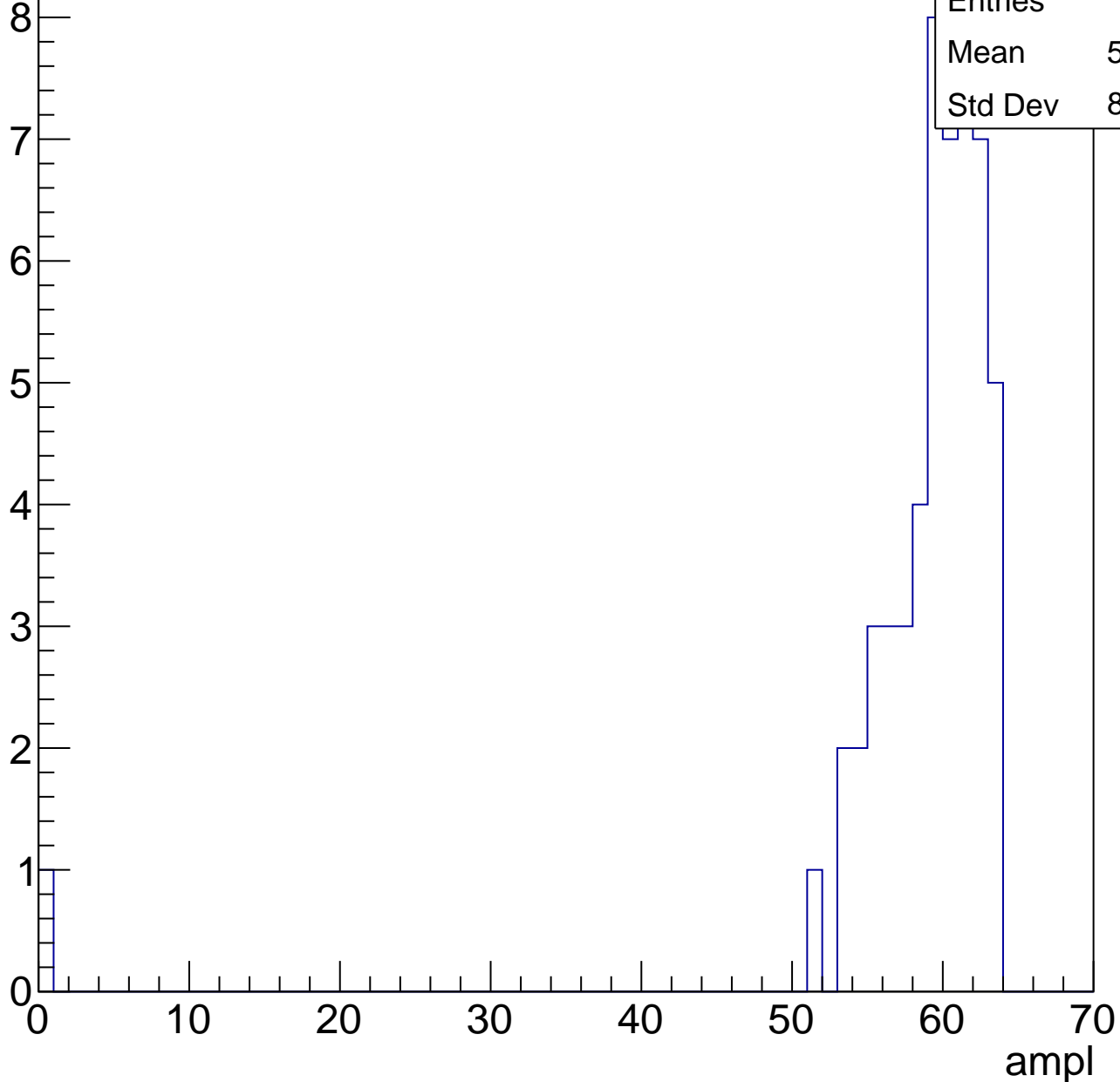
Entries	62
Mean	54.45
Std Dev	3.449



# B1L000S, U2-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

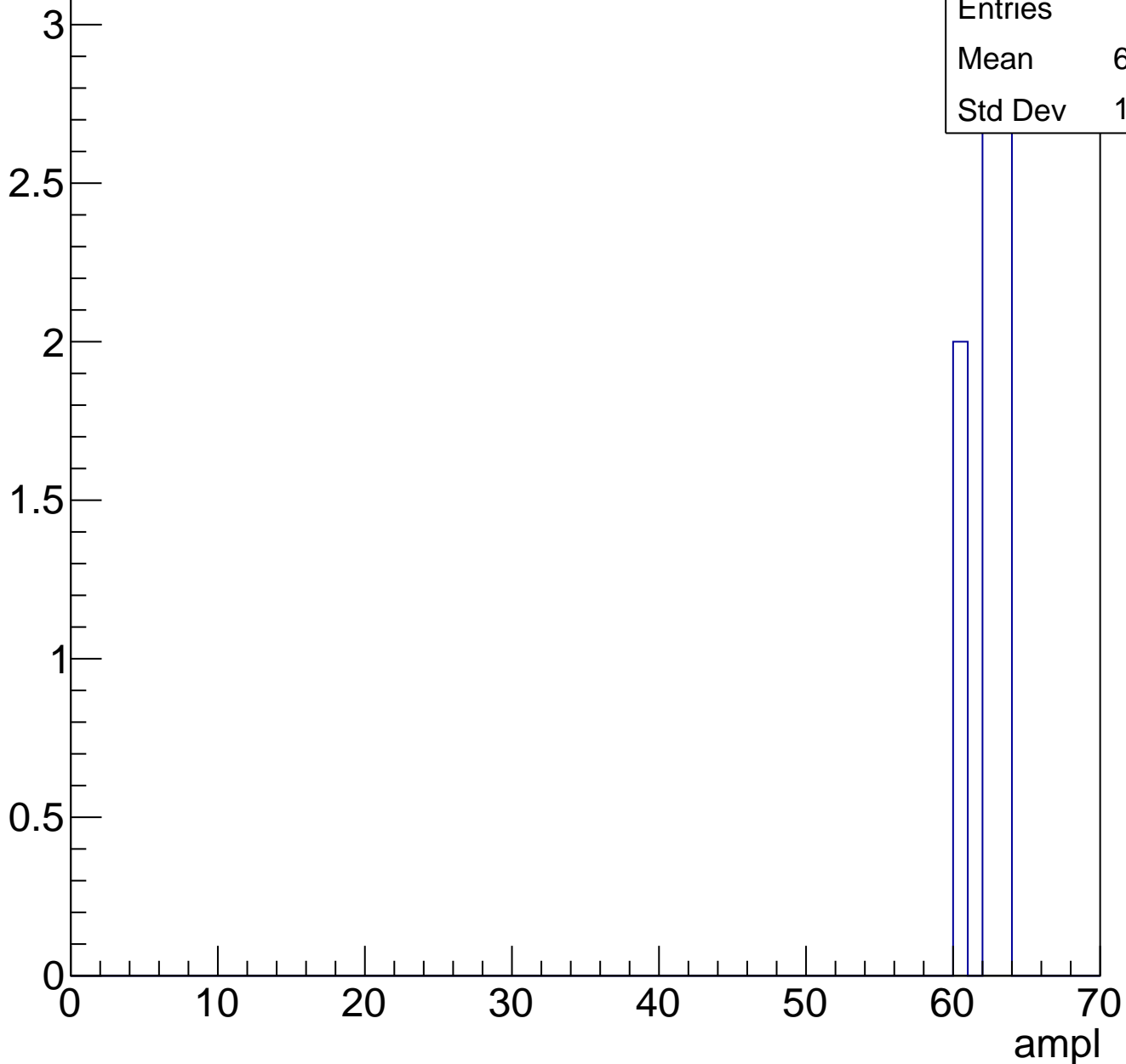
Entry



# B1L000S, U2-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L000S, U2-ch114, adc0

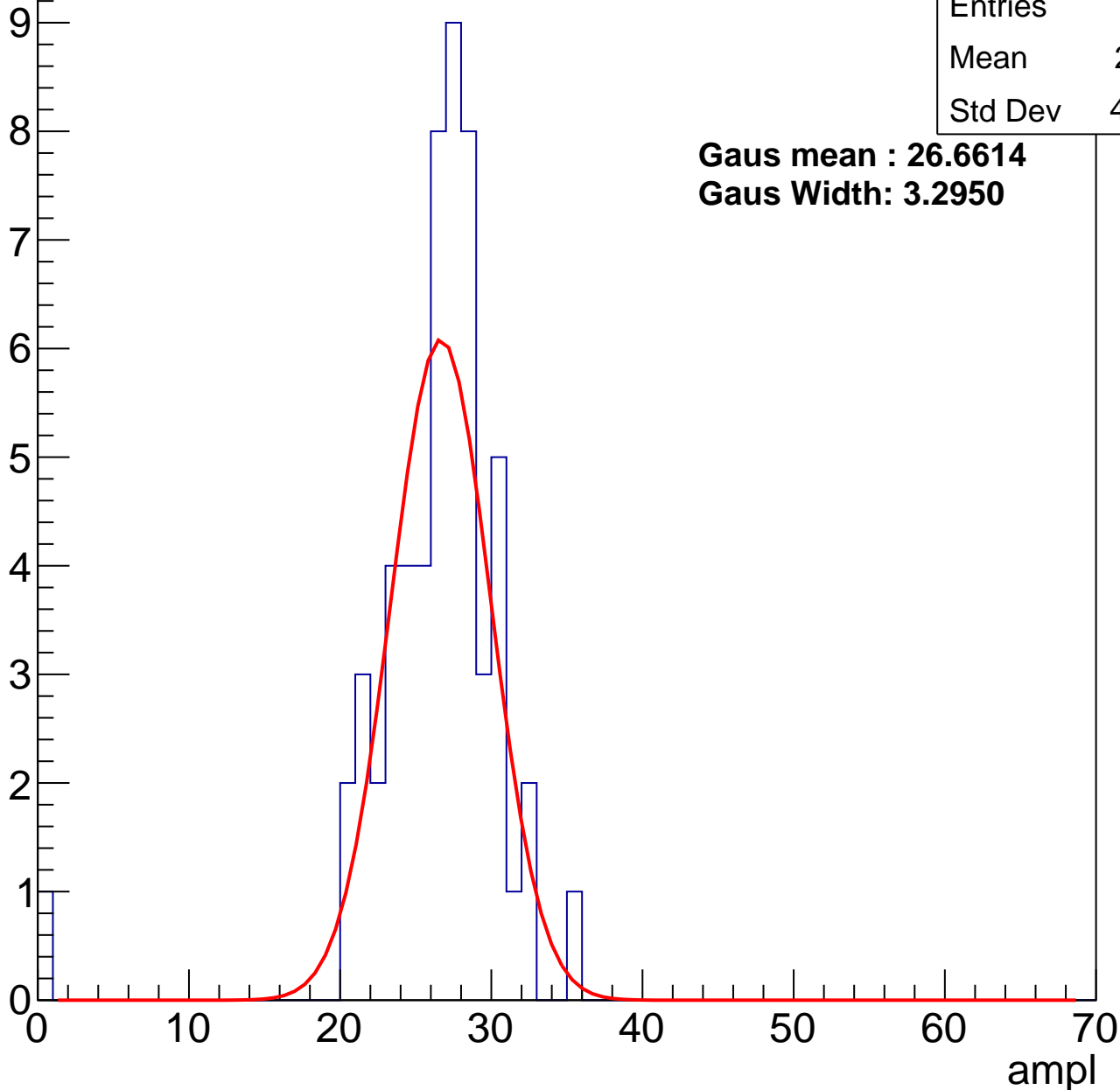
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	25.91
Std Dev	4.658

**Gaus mean : 26.6614**

**Gaus Width: 3.2950**



# B1L000S, U2-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	32.63
Std Dev	3.19

**Gaus mean : 32.7068**

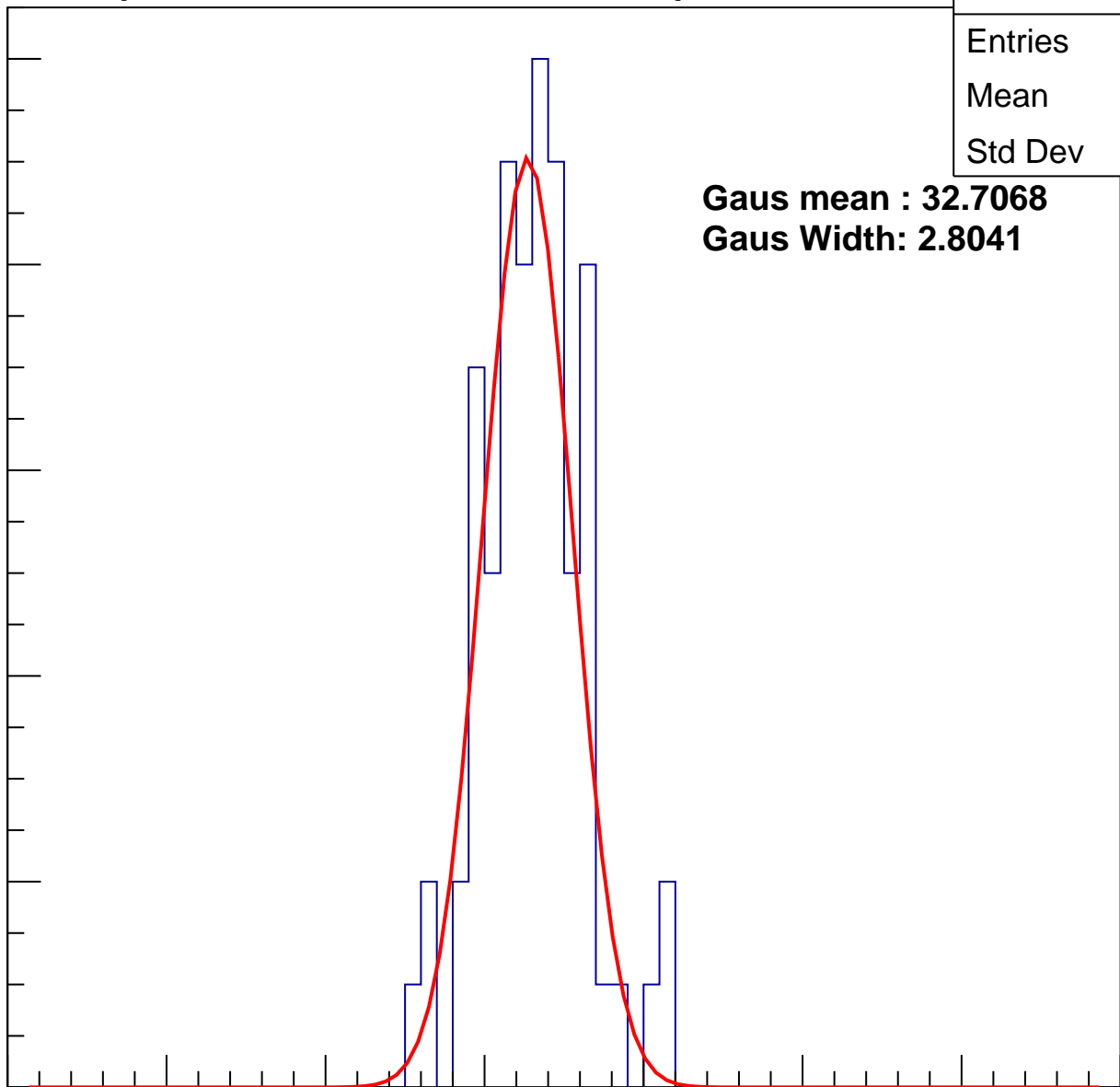
**Gaus Width: 2.8041**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch114, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	39.45
Std Dev	2.866

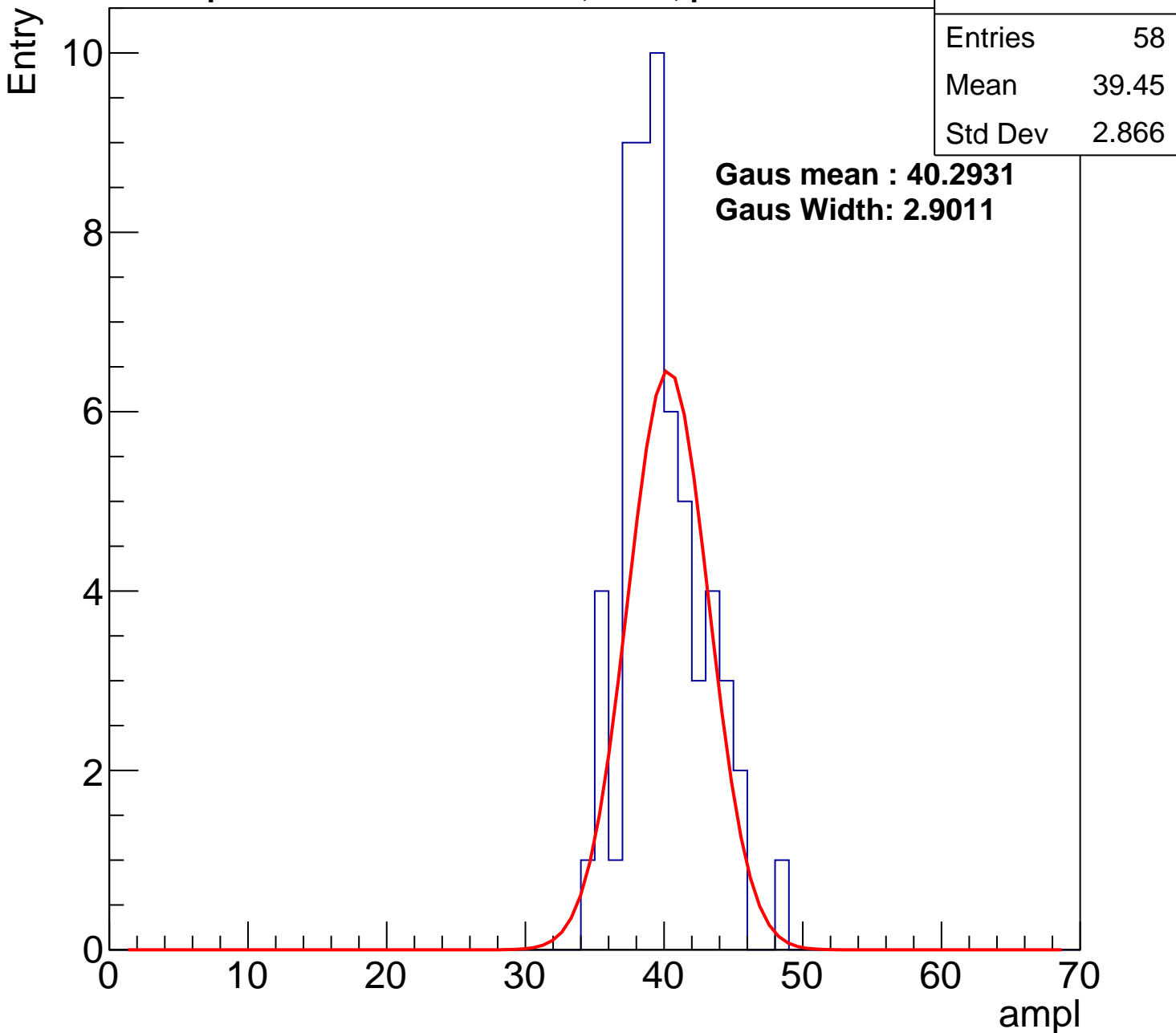
**Gaus mean : 40.2931**  
**Gaus Width: 2.9011**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

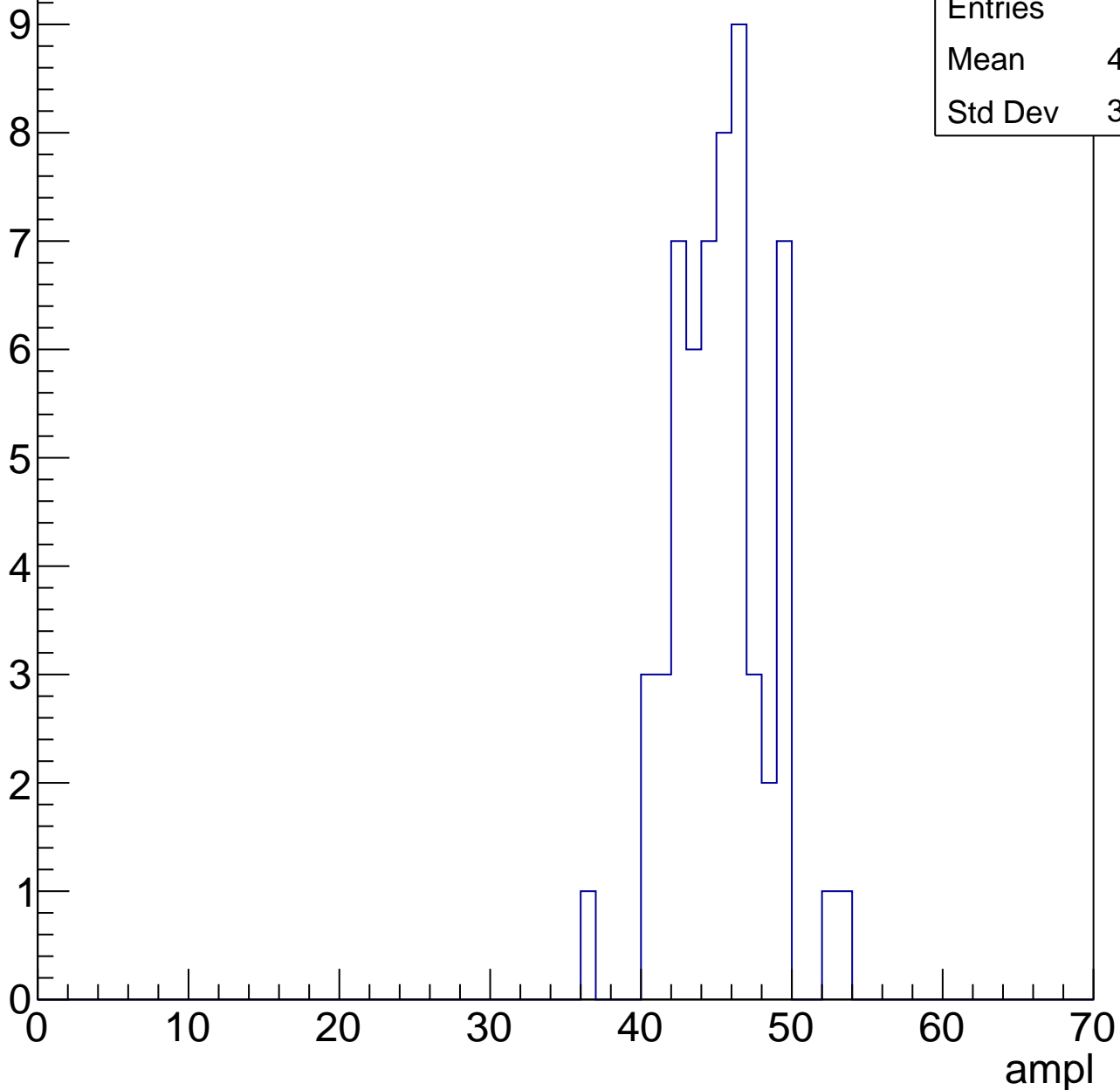


# B1L000S, U2-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	44.79
Std Dev	3.106

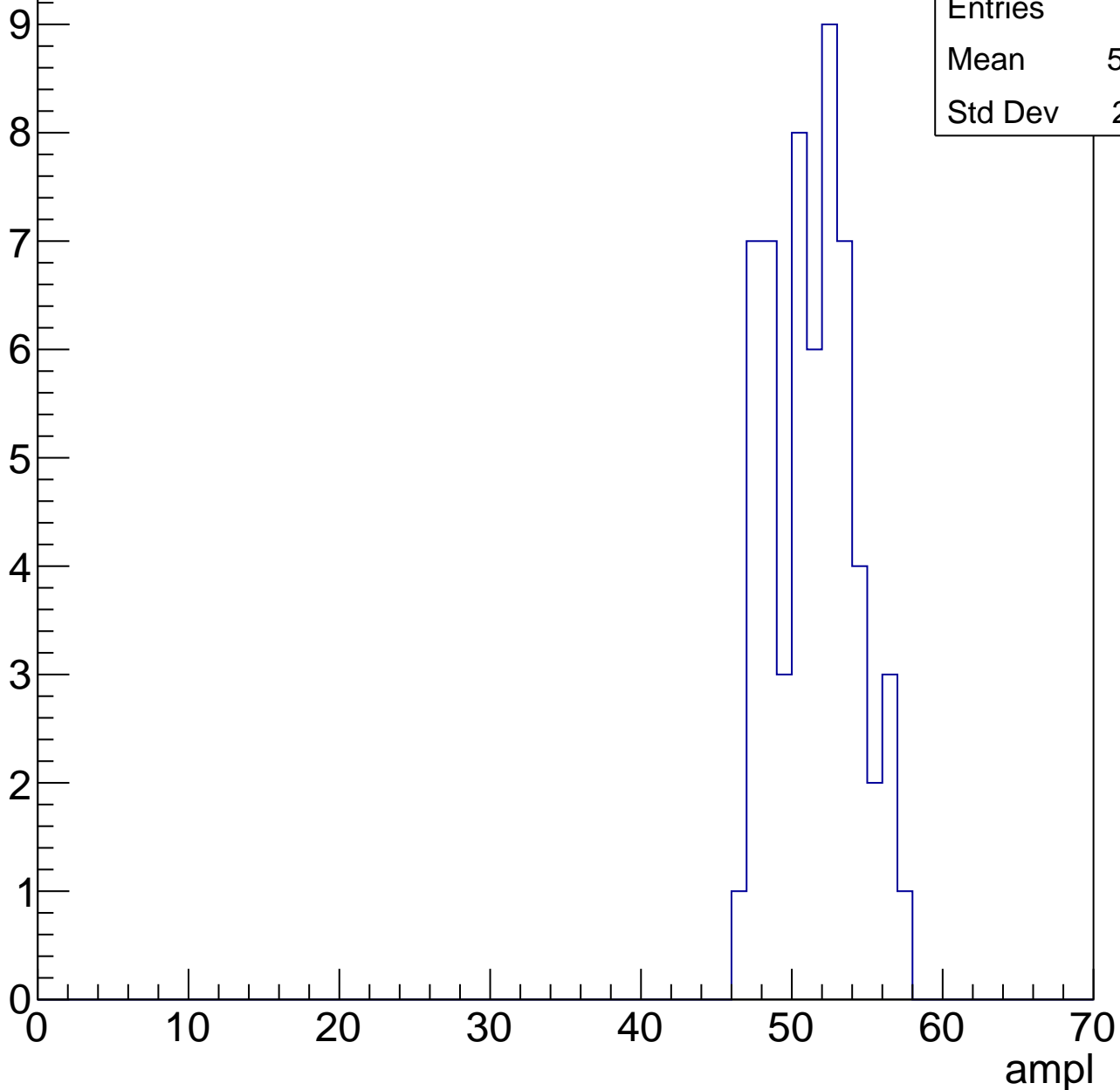


# B1L000S, U2-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

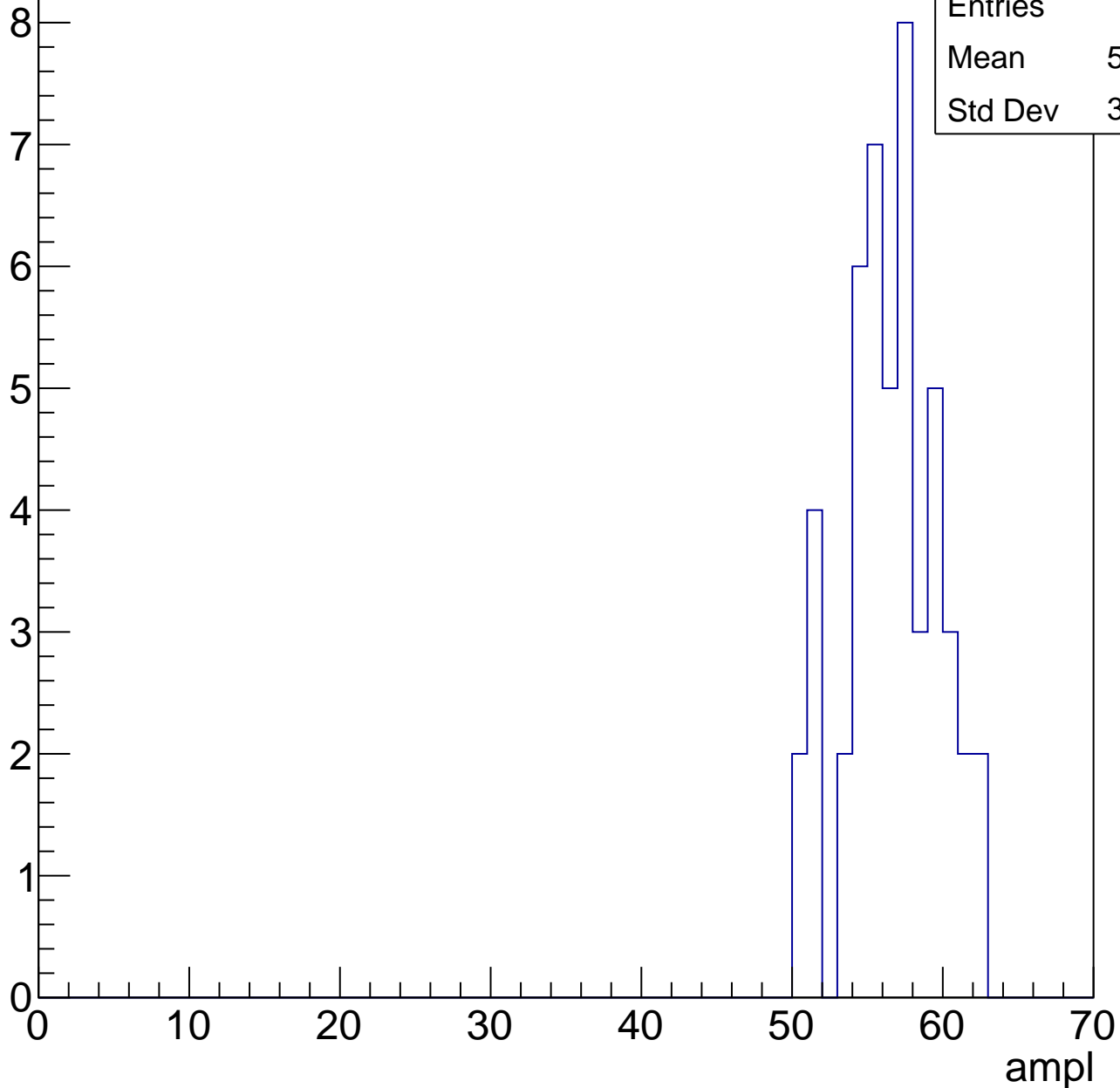
Entries	58
Mean	50.93
Std Dev	2.741



# B1L000S, U2-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	49
Mean	56.12
Std Dev	3.048

# B1L000S, U2-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 40

Mean 58.42

Std Dev 9.584

ampl

0

10

20

30

40

50

60

70

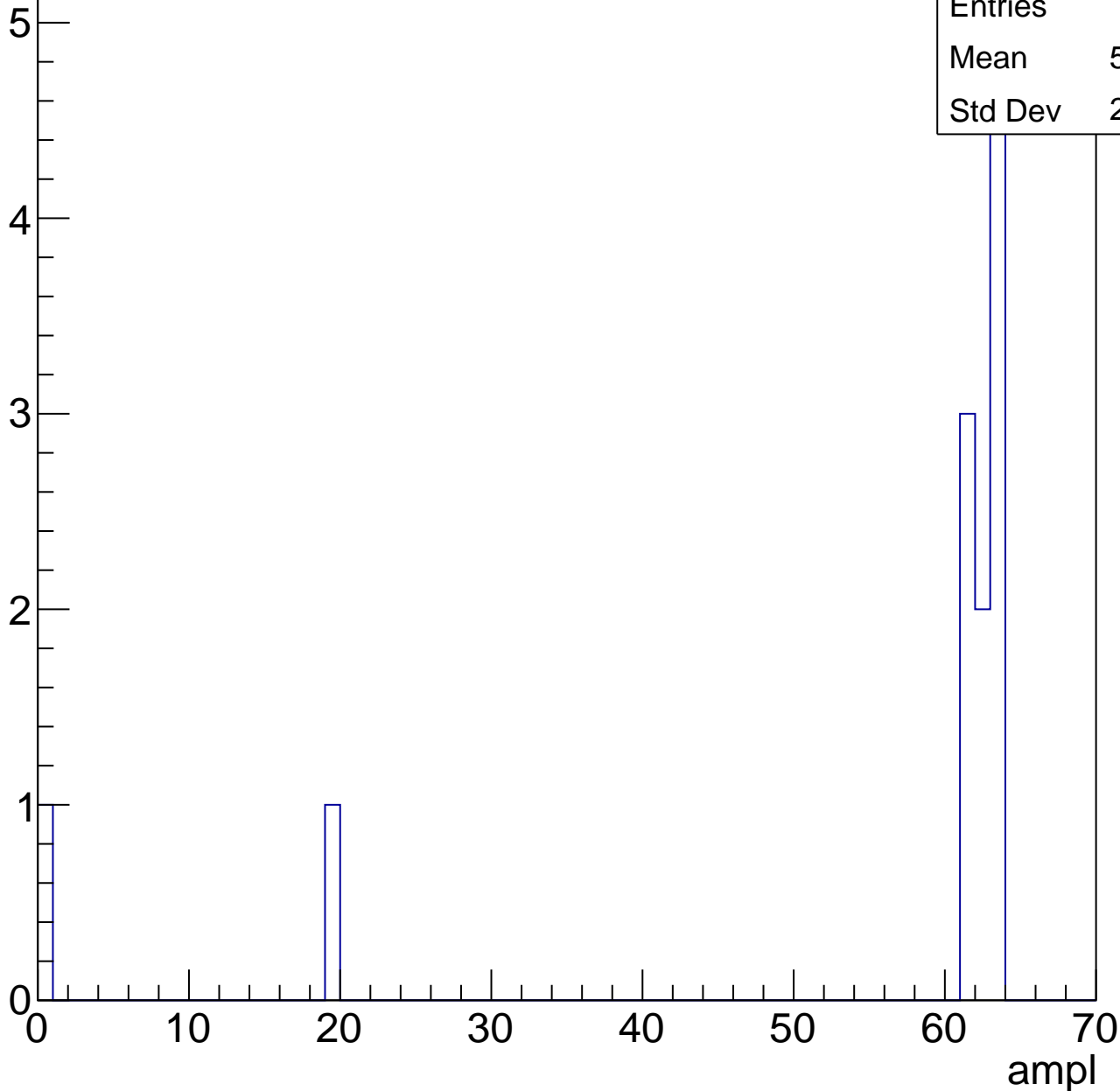


# B1L000S, U2-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	53.42
Std Dev	20.04



# B1L000S, U2-ch115, adc0

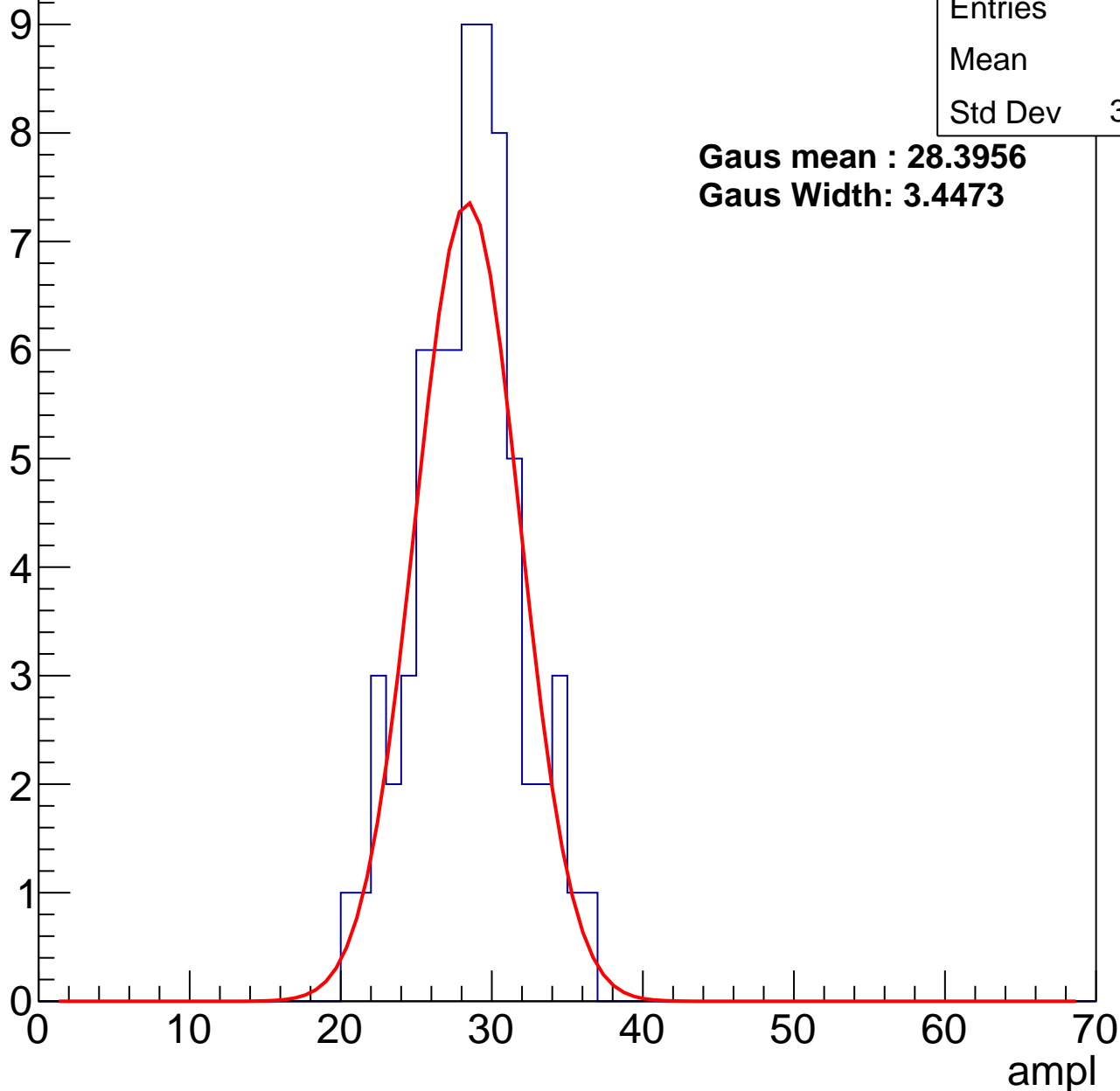
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28
Std Dev	3.413

**Gaus mean : 28.3956**

**Gaus Width: 3.4473**



# B1L000S, U2-ch115, adc1

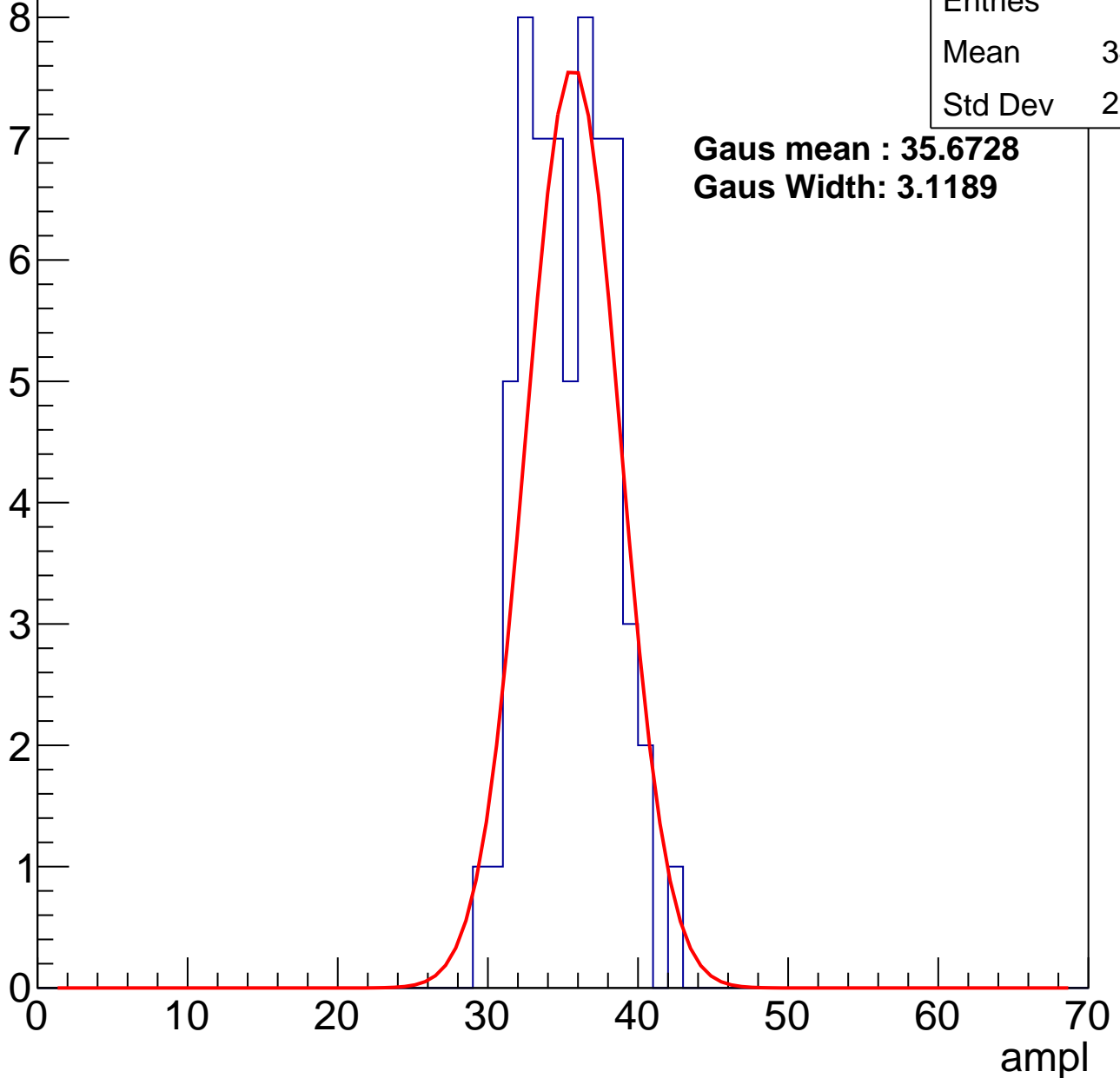
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	34.94
Std Dev	2.822

**Gaus mean : 35.6728**

**Gaus Width: 3.1189**



# B1L000S, U2-ch115, adc2

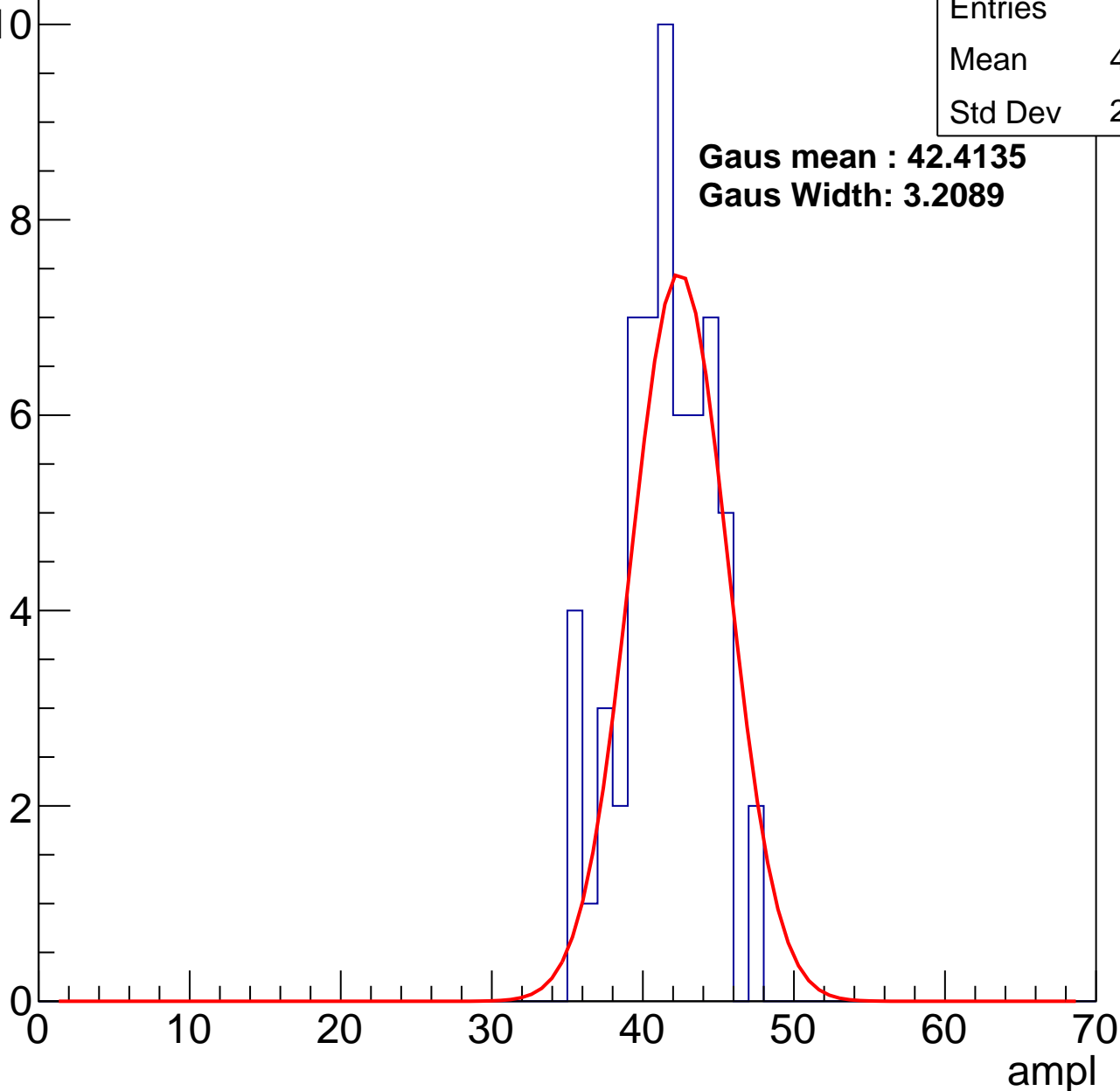
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.05
Std Dev	2.929

**Gaus mean : 42.4135**

**Gaus Width: 3.2089**

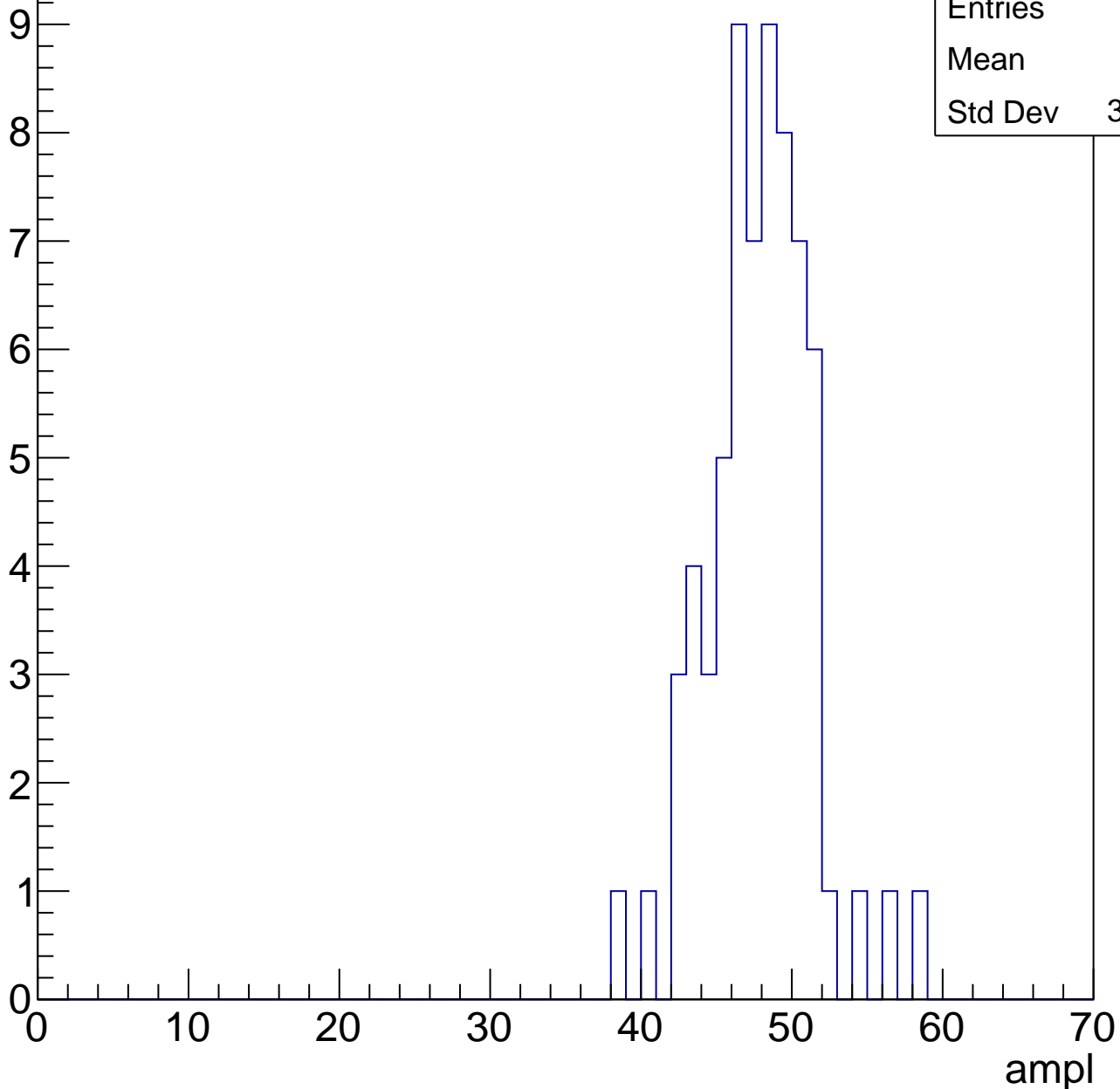


# B1L000S, U2-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	47.4
Std Dev	3.434

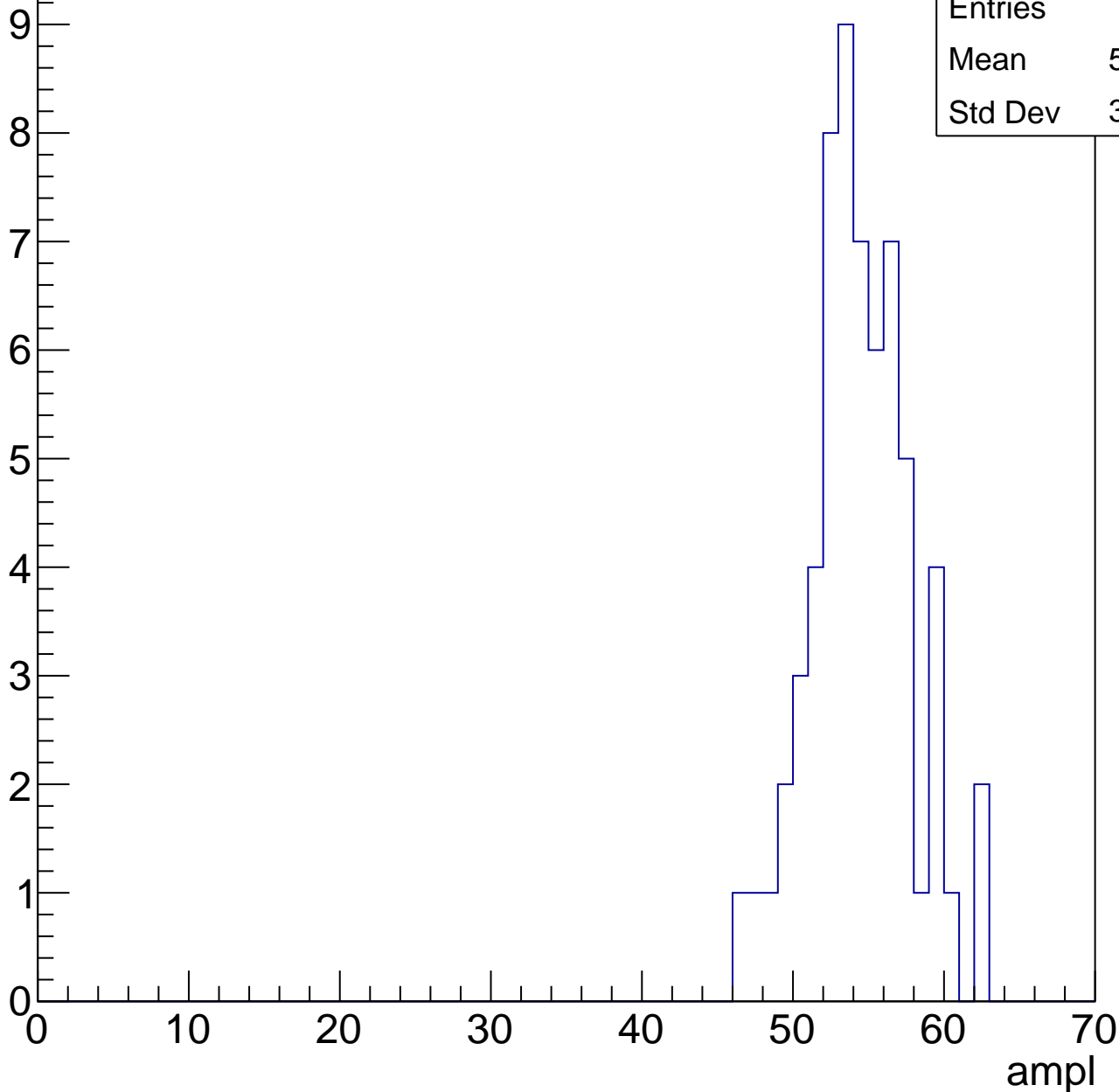


# B1L000S, U2-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

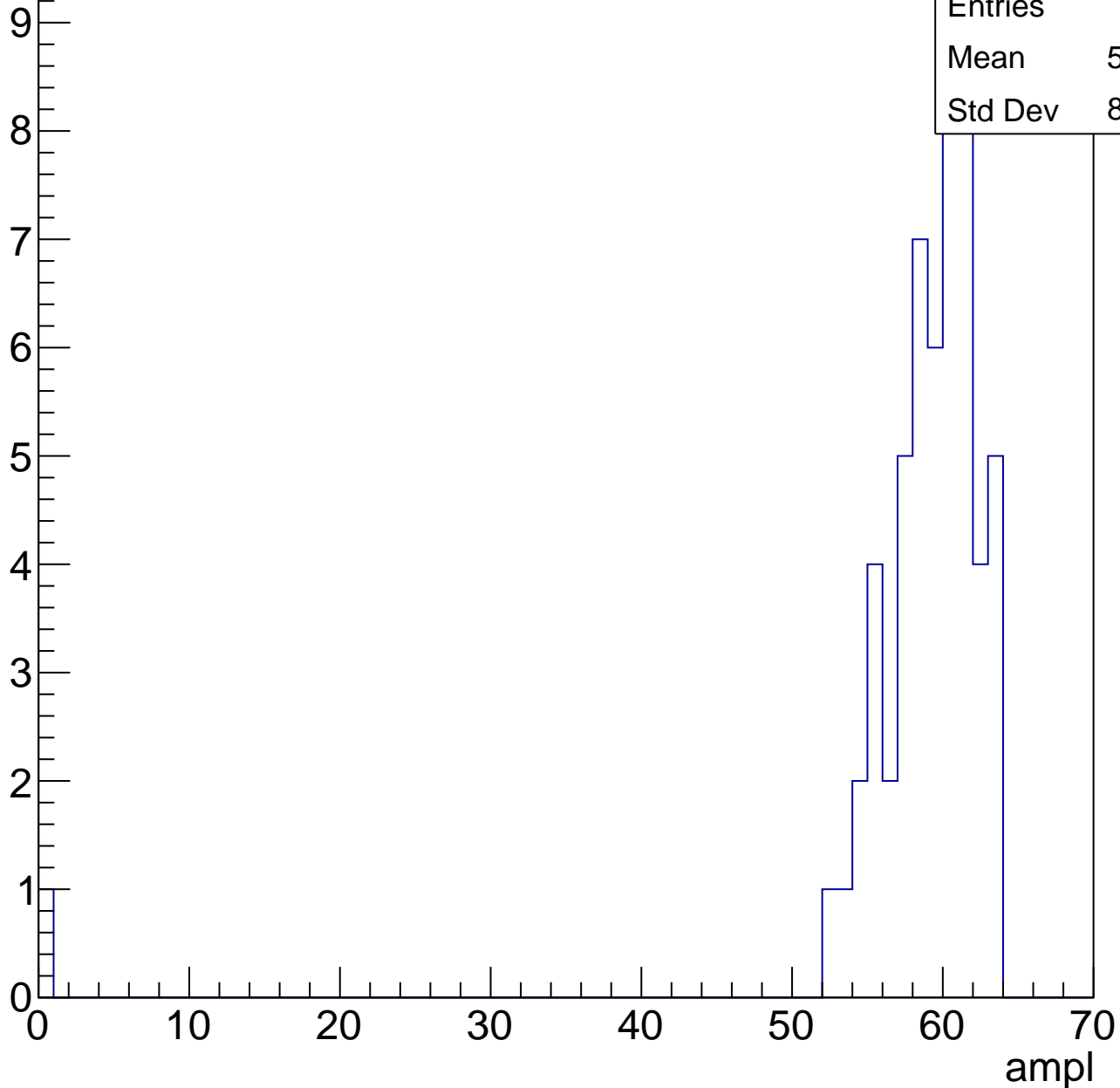
Entries	62
Mean	54.02
Std Dev	3.319



# B1L000S, U2-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

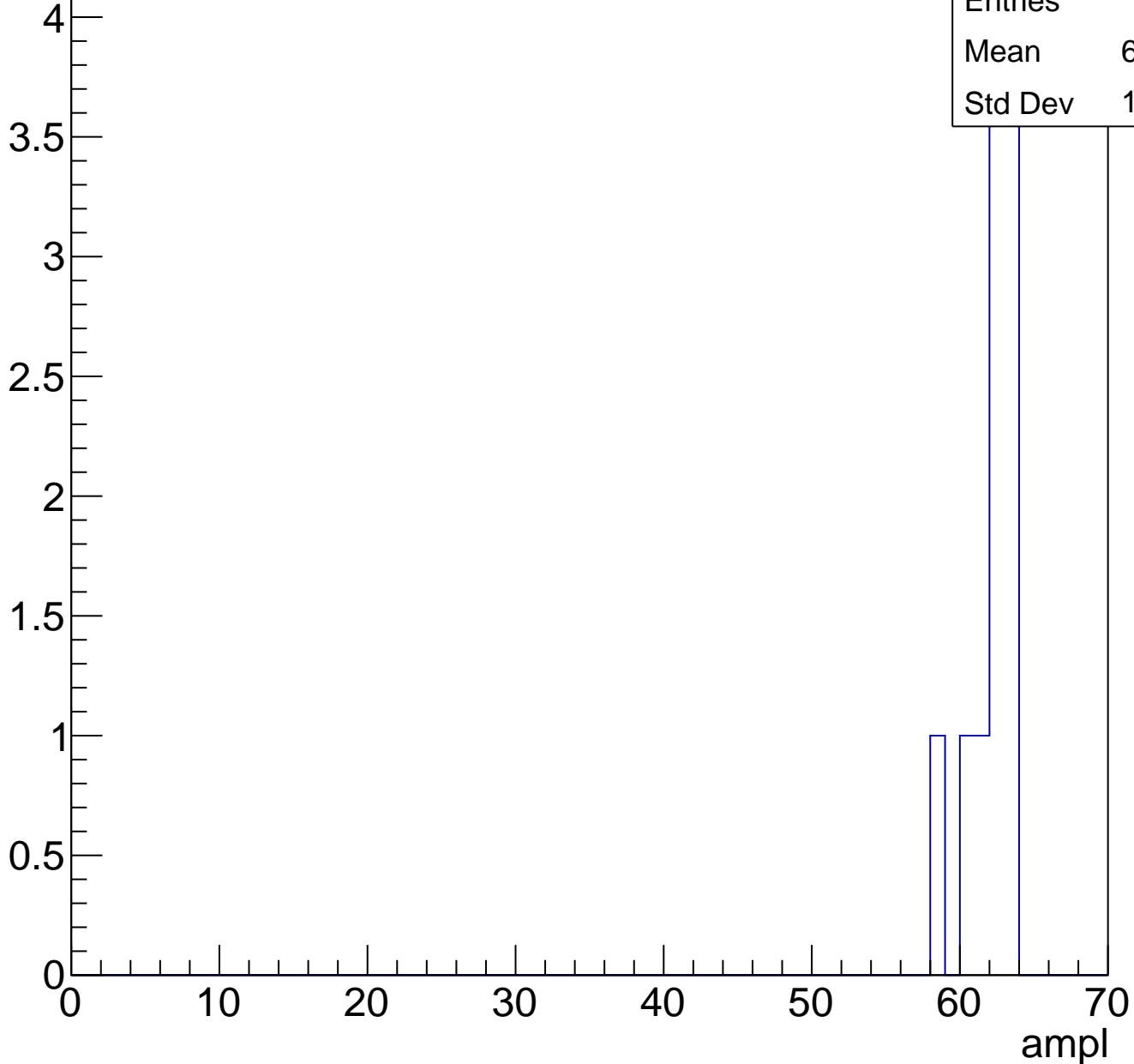
Entry



# B1L000S, U2-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U2-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L000S, U2-ch116, adc0

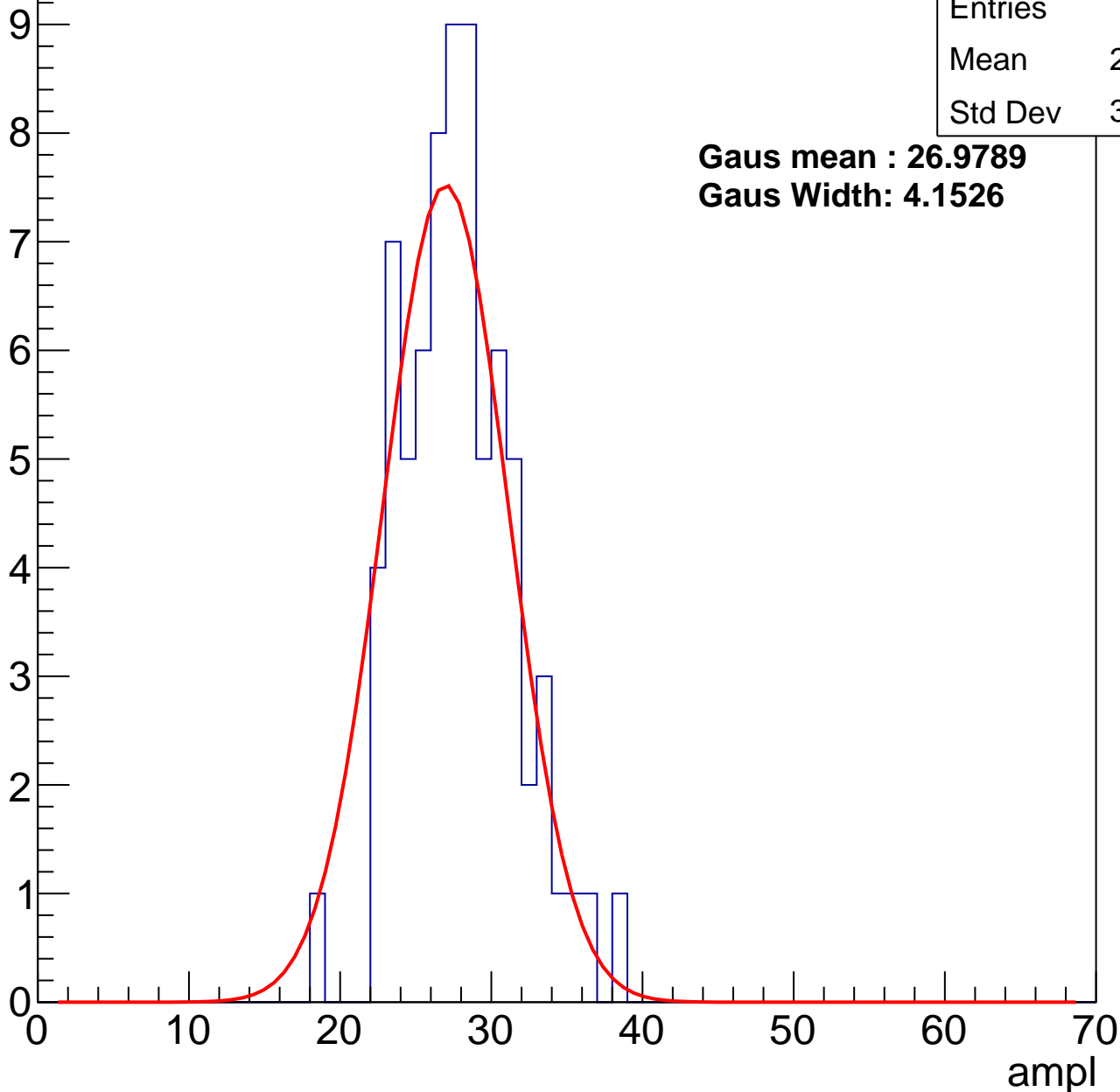
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	27.38
Std Dev	3.668

**Gaus mean : 26.9789**

**Gaus Width: 4.1526**



# B1L000S, U2-ch116, adc1

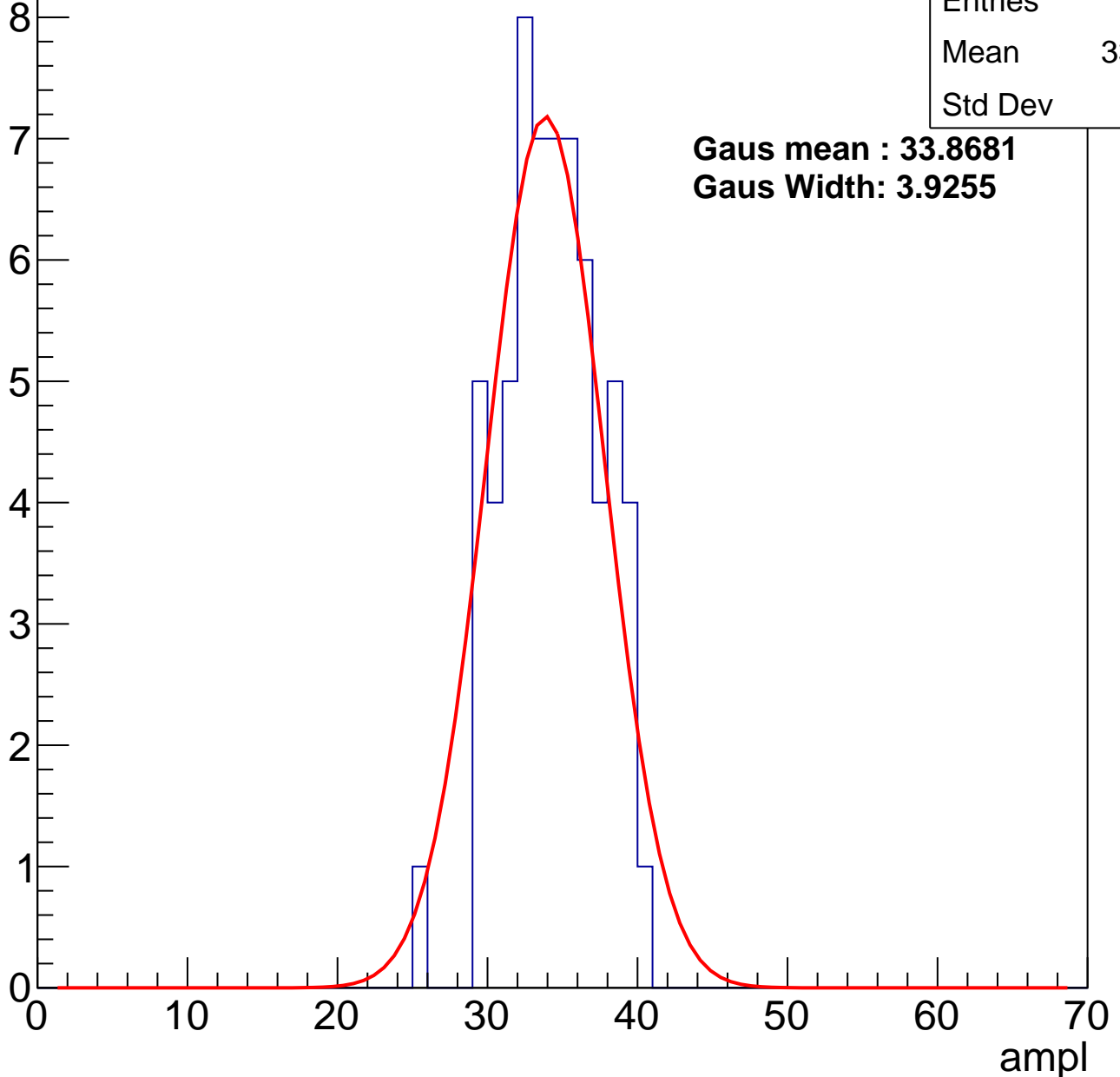
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	33.83
Std Dev	3.15

**Gaus mean : 33.8681**

**Gaus Width: 3.9255**



# B1L000S, U2-ch116, adc2

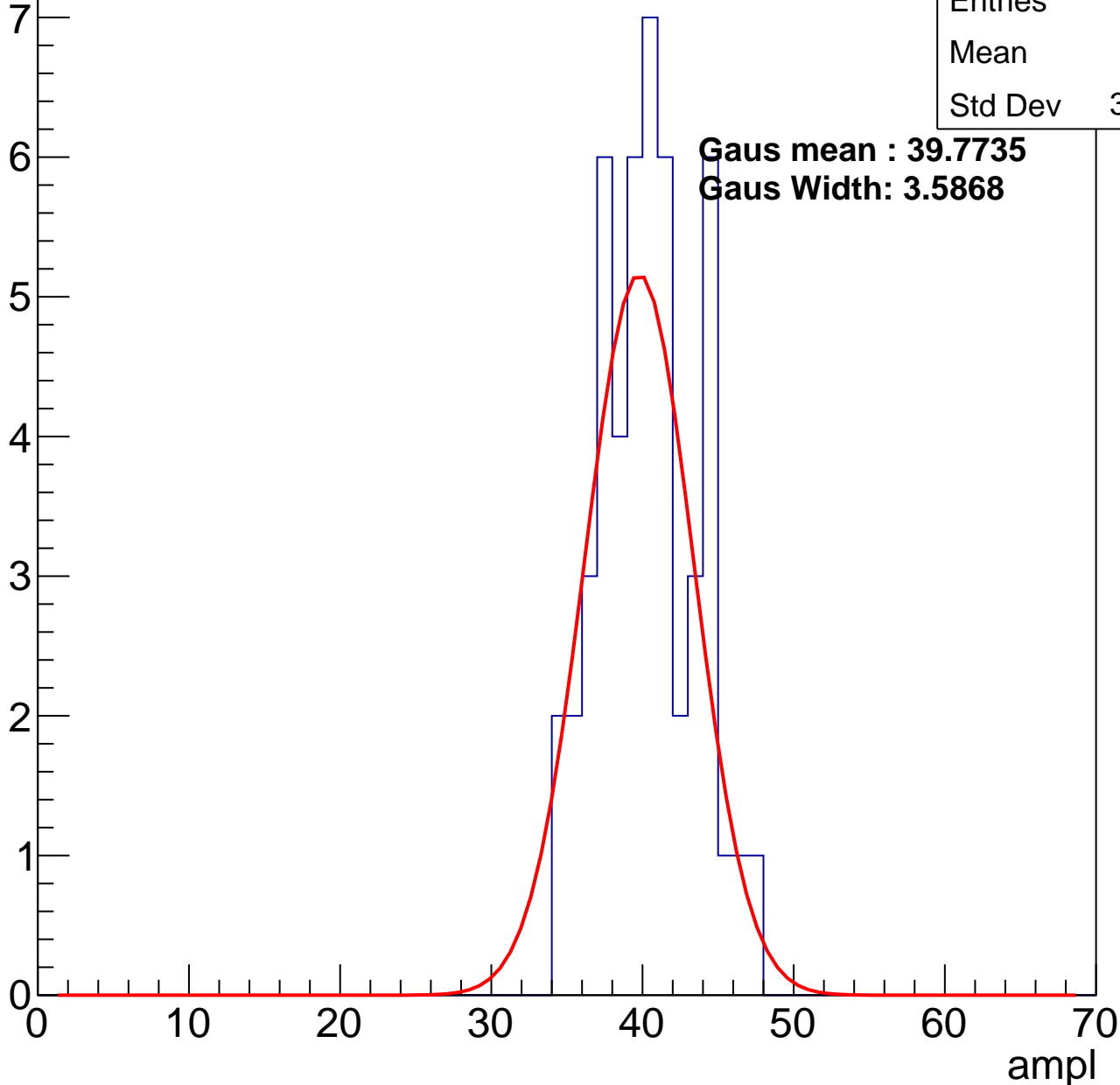
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	39.9
Std Dev	3.138

**Gaus mean : 39.7735**

**Gaus Width: 3.5868**

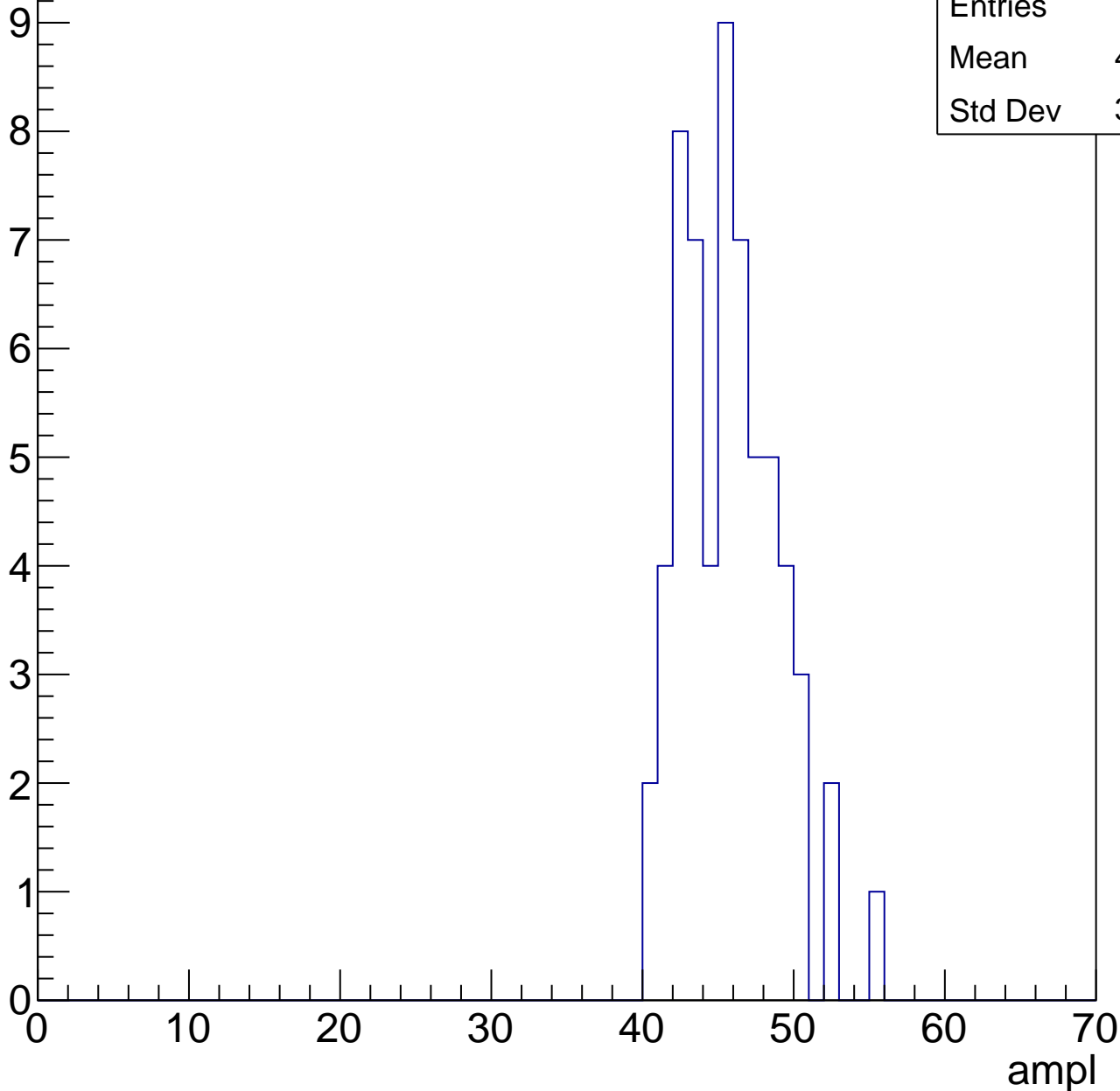


# B1L000S, U2-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	45.31
Std Dev	3.191

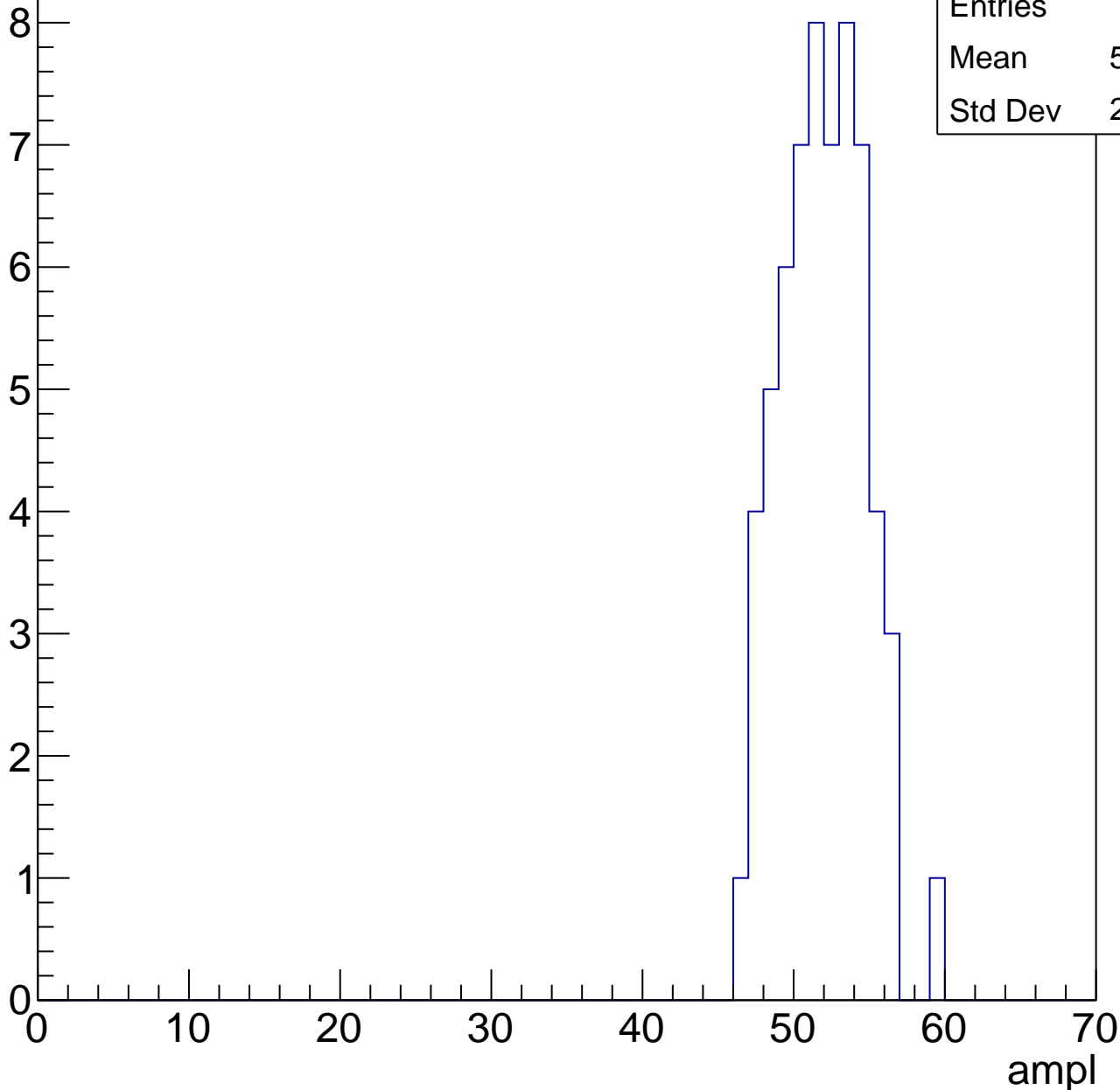


# B1L000S, U2-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

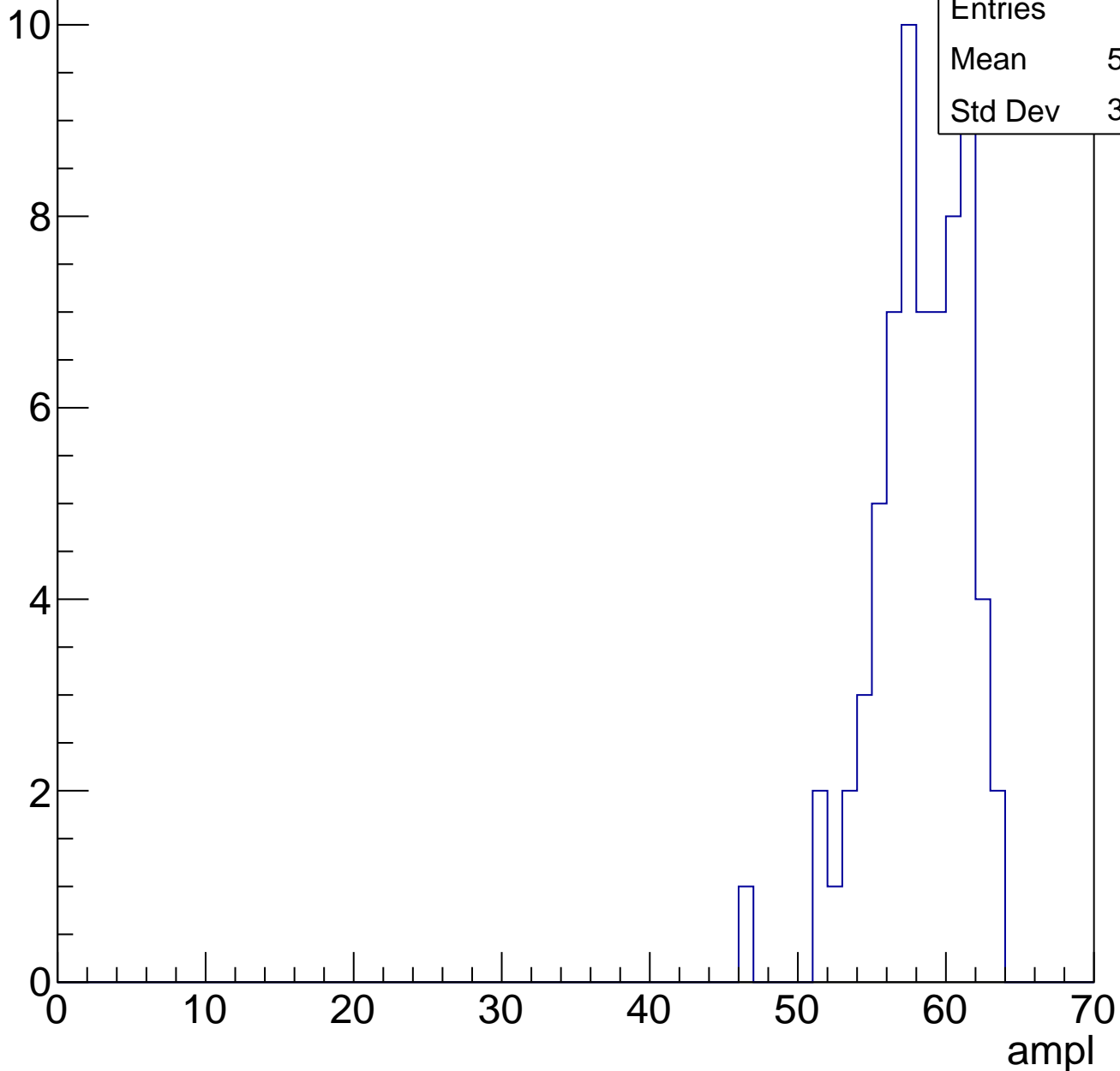
Entries	61
Mean	51.46
Std Dev	2.738



# B1L000S, U2-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

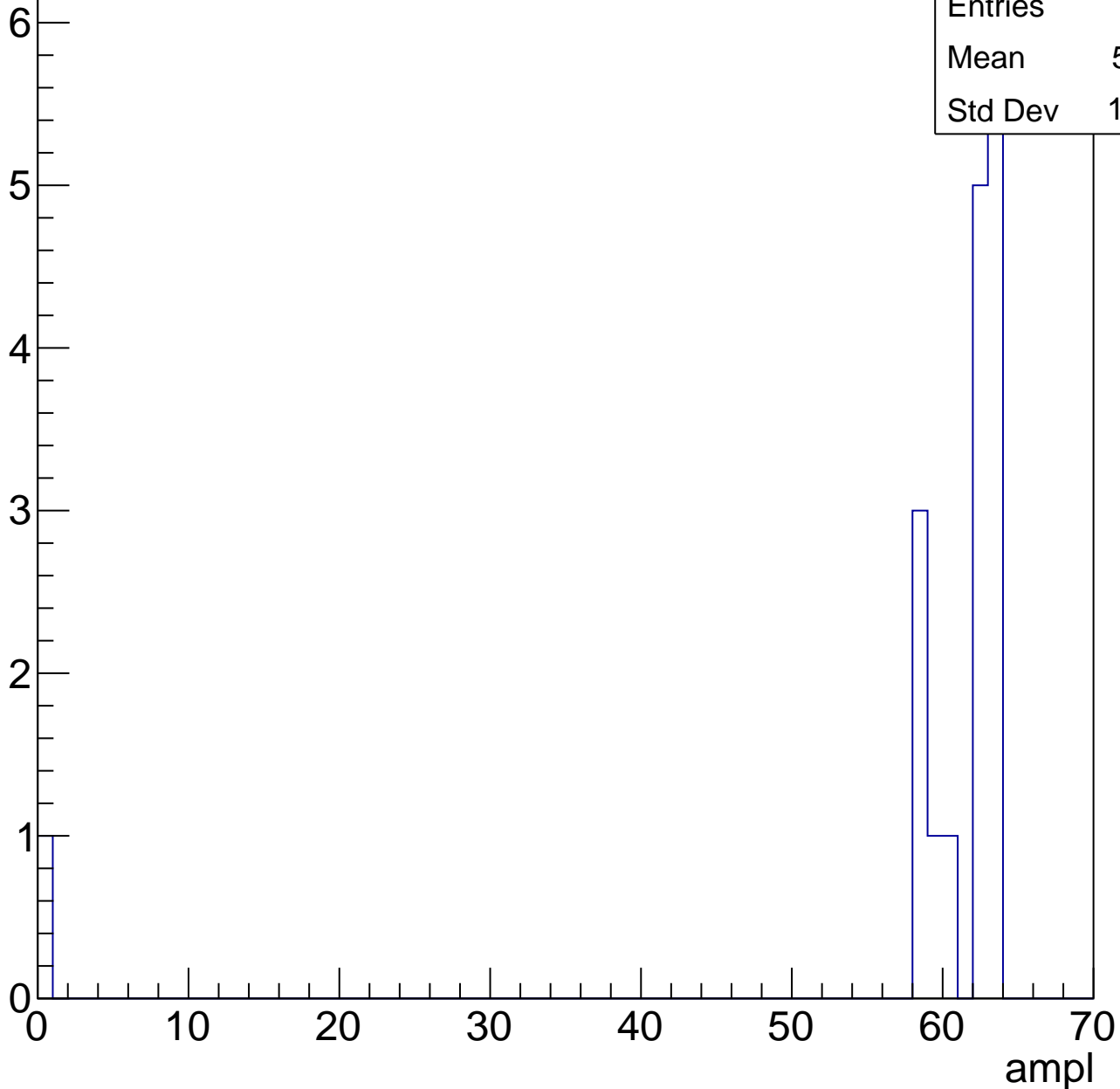


# B1L000S, U2-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.71
Std Dev	14.55





# B1L000S, U2-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

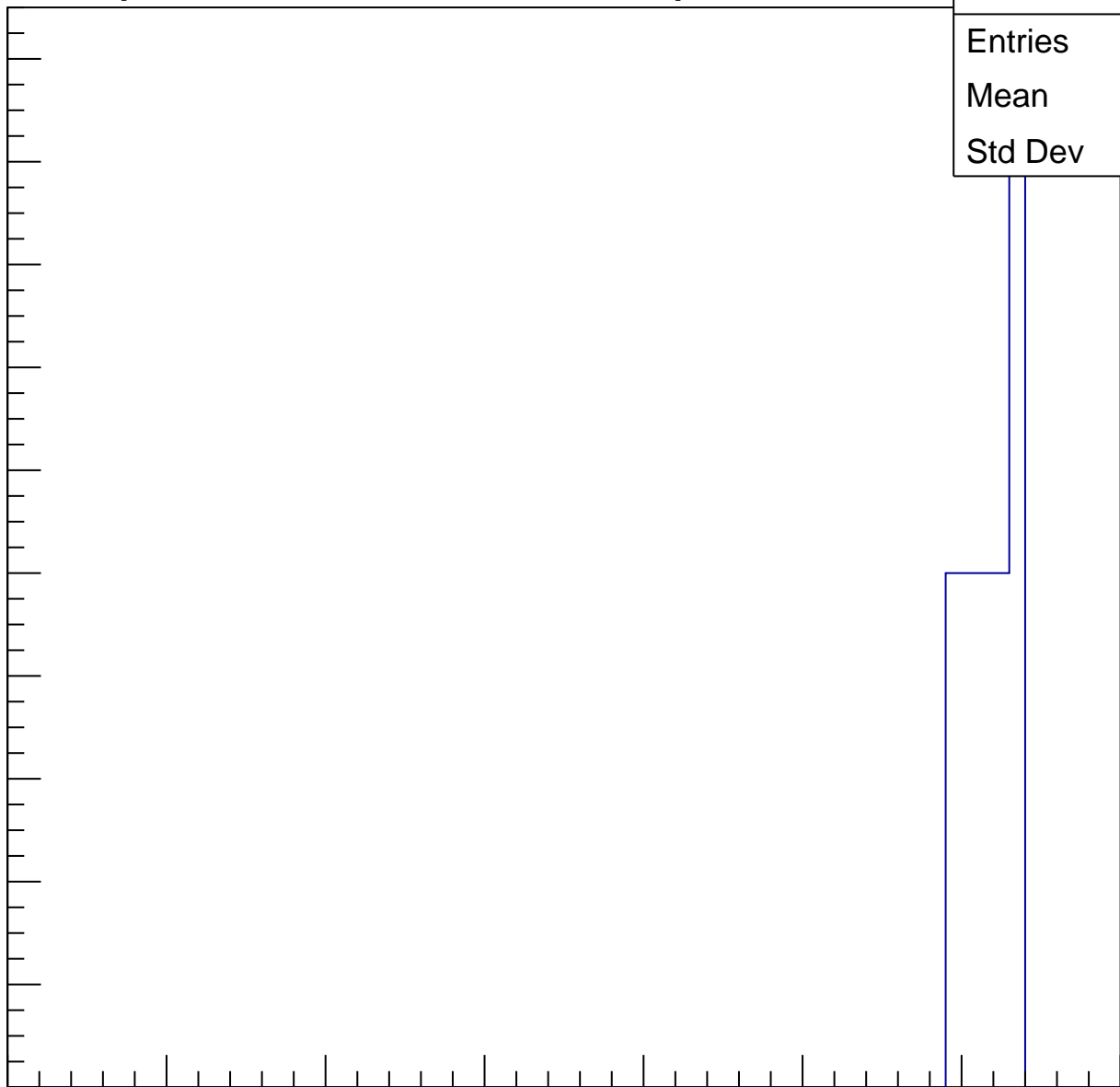
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.33
Std Dev	1.491

0 10 20 30 40 50 60 70

ampl



# B1L000S, U2-ch117, adc0

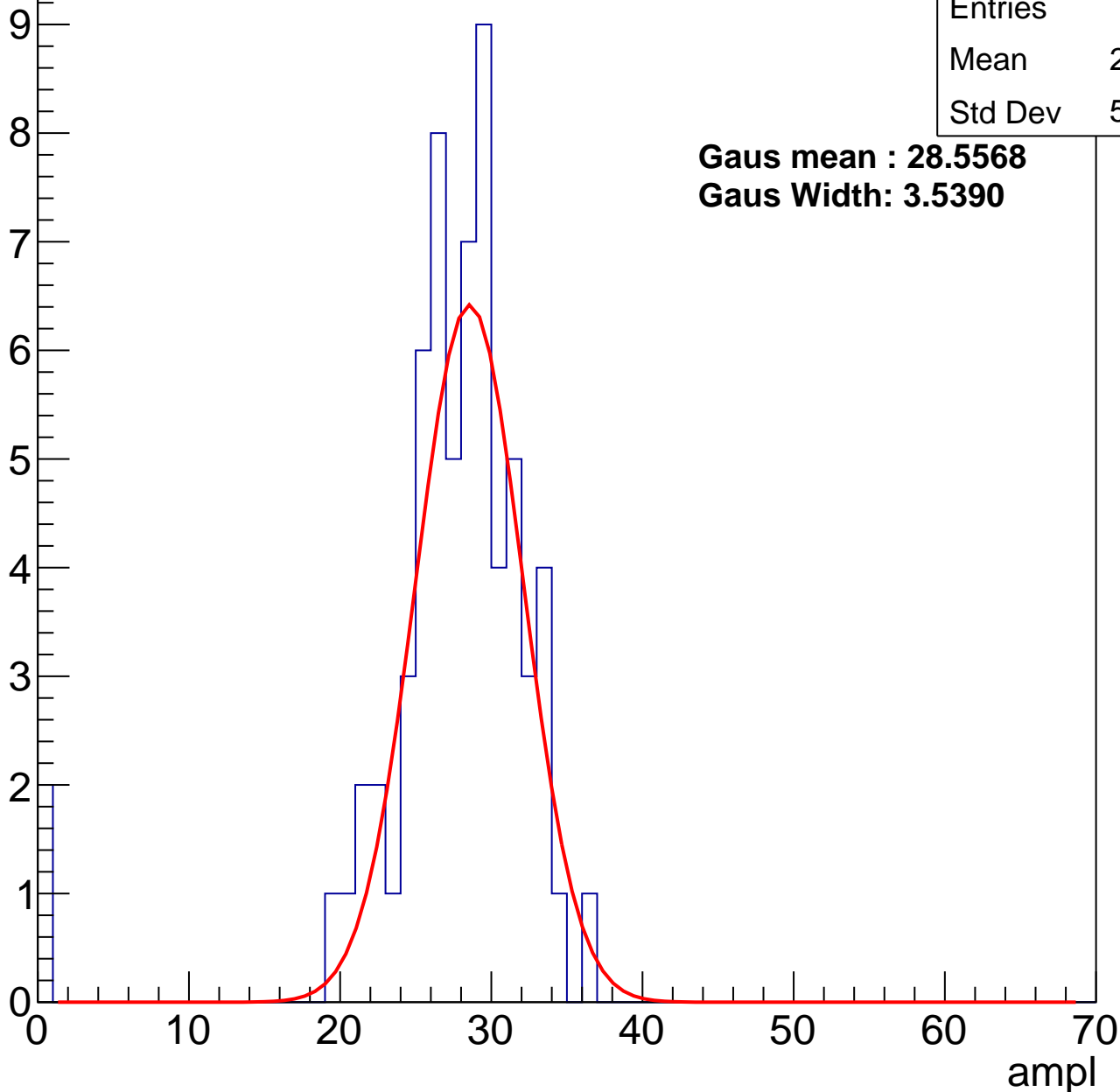
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	26.82
Std Dev	5.914

**Gaus mean : 28.5568**

**Gaus Width: 3.5390**



# B1L000S, U2-ch117, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	34.28
Std Dev	3.22

**Gaus mean : 34.6861**

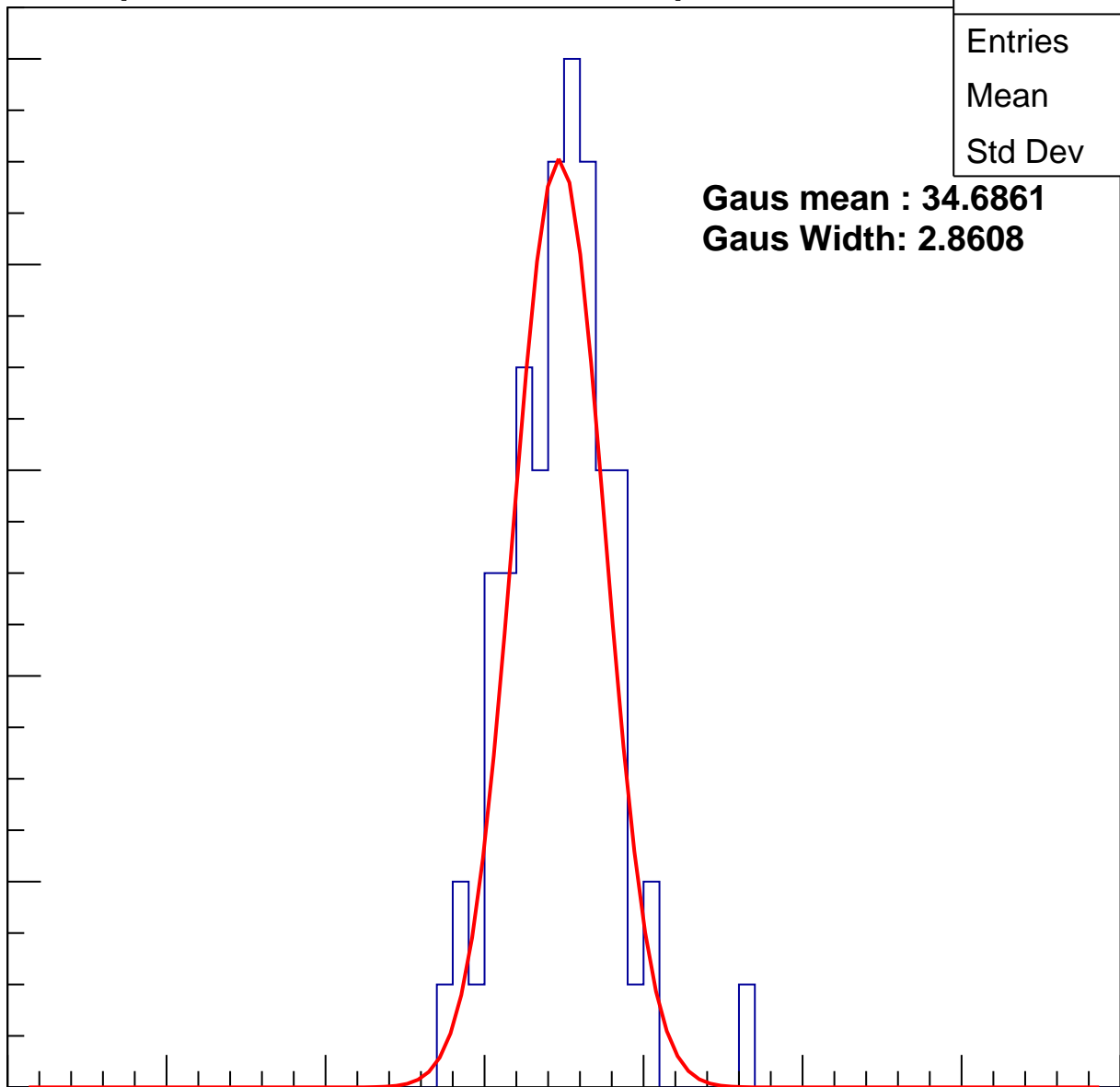
**Gaus Width: 2.8608**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch117, adc2

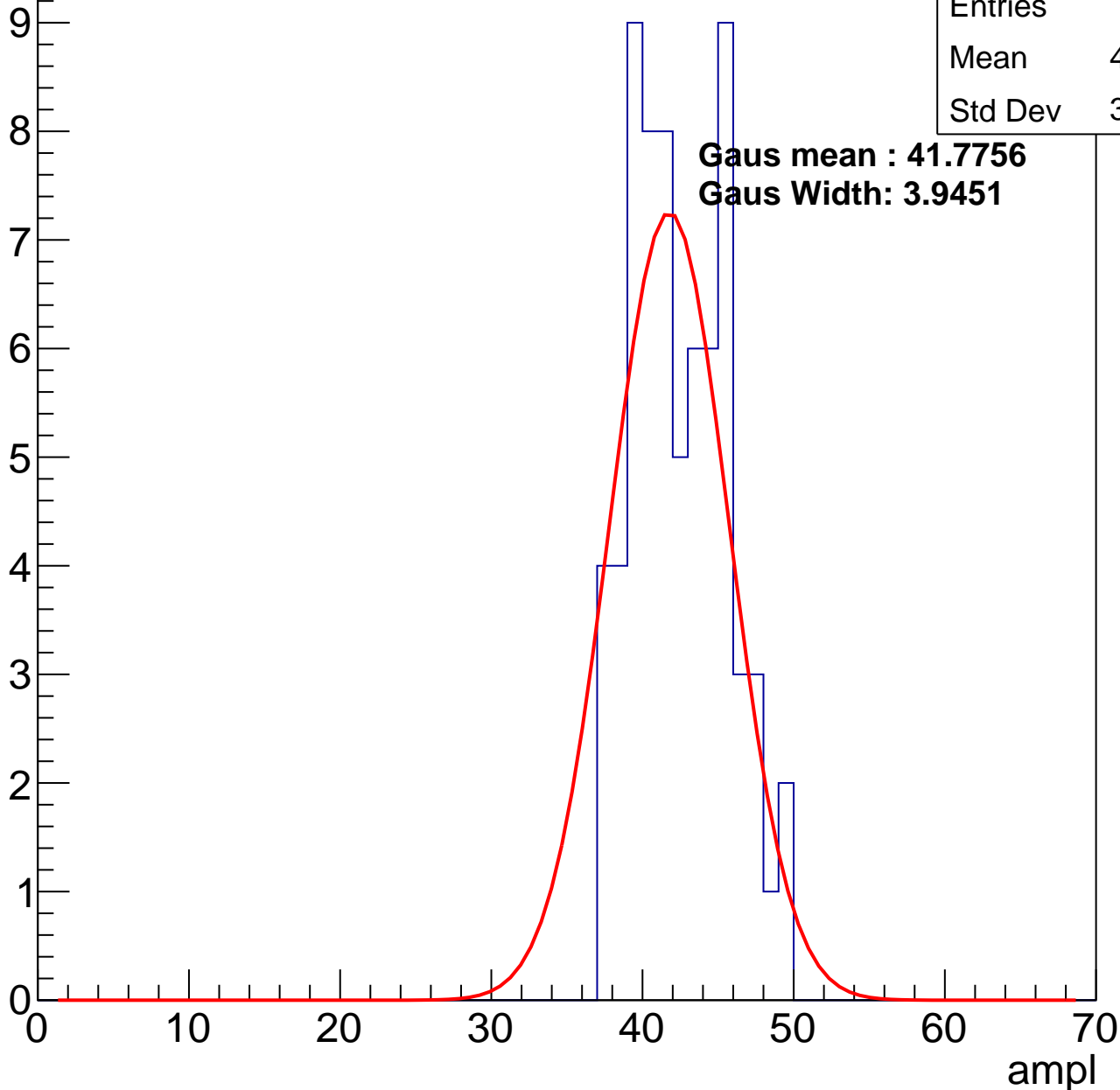
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	42.07
Std Dev	3.098

**Gaus mean : 41.7756**

**Gaus Width: 3.9451**

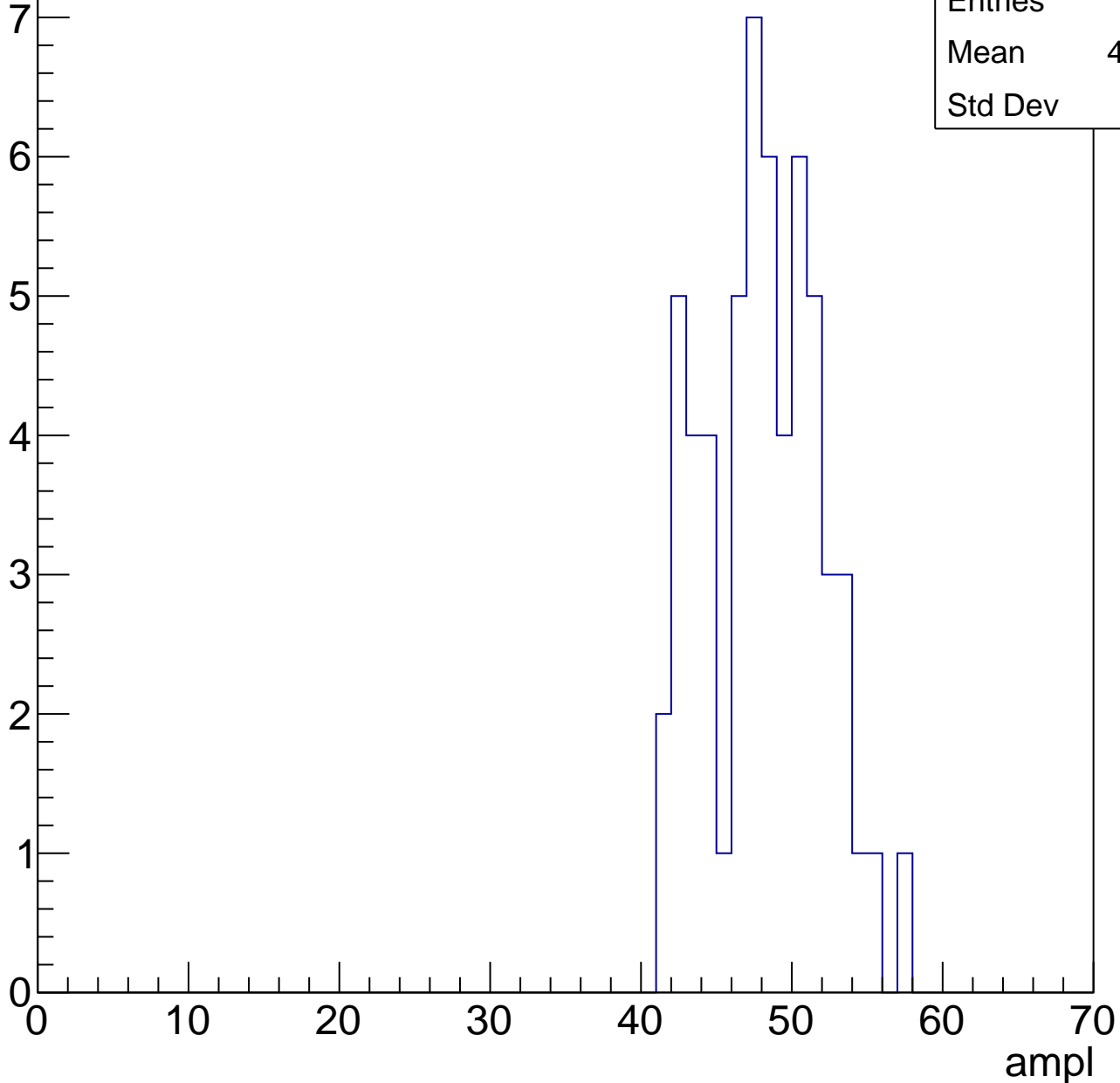


# B1L000S, U2-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	47.66
Std Dev	3.79

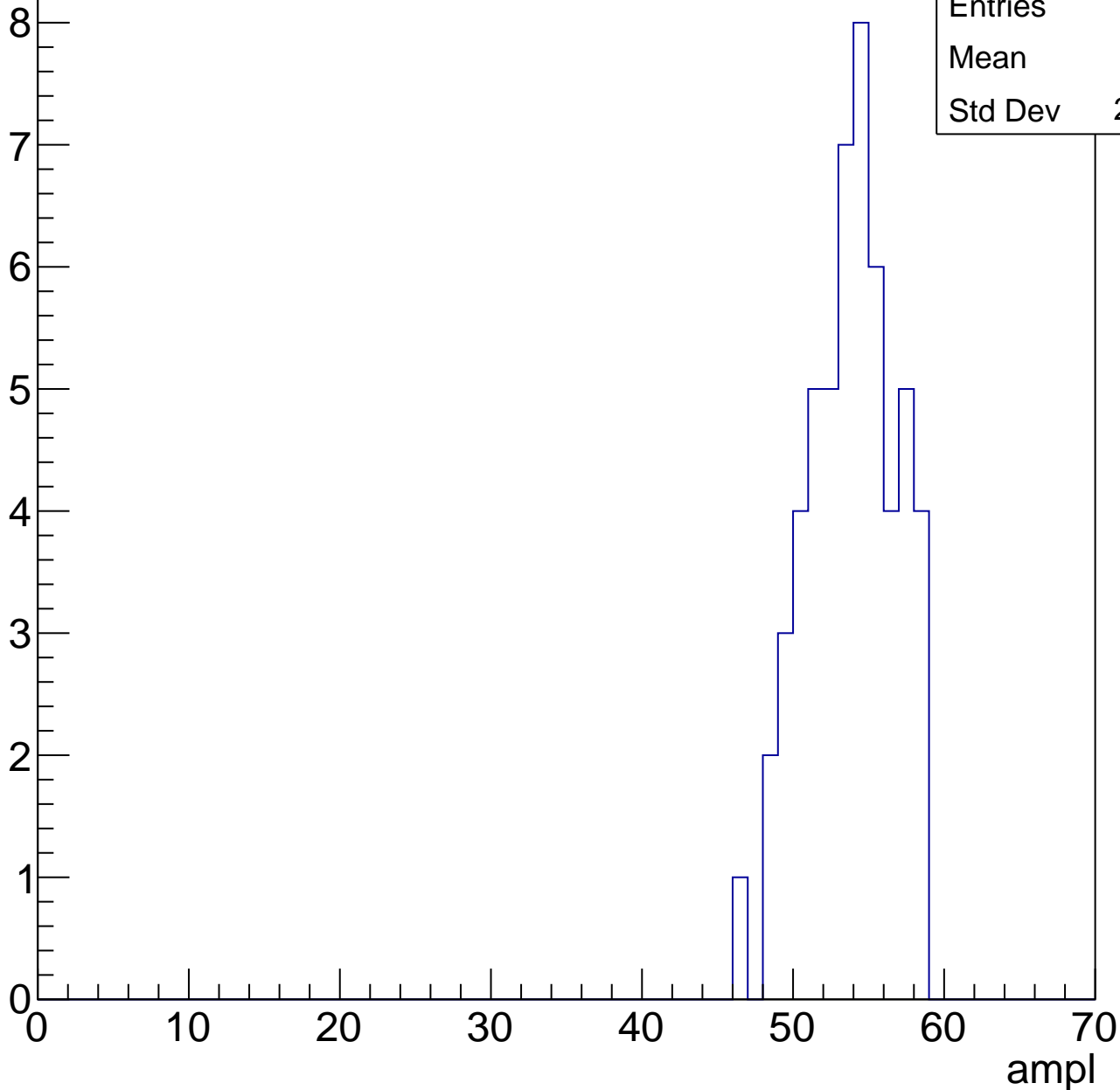


# B1L000S, U2-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	53.3
Std Dev	2.891



# B1L000S, U2-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	50
Mean	58.06
Std Dev	3.19

Entry

10

8

6

4

2

0

0

10

20

30

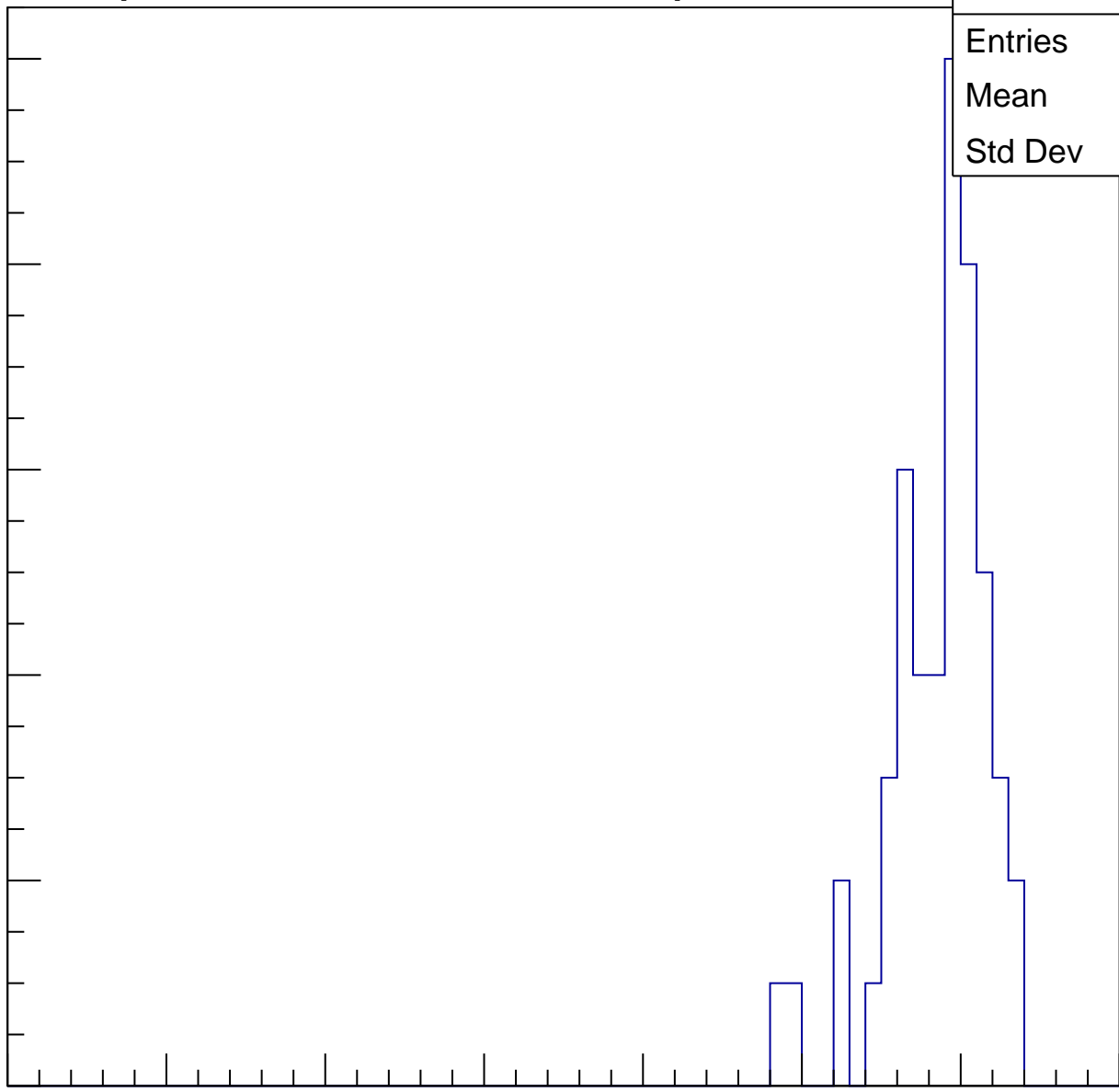
40

50

60

70

ampl

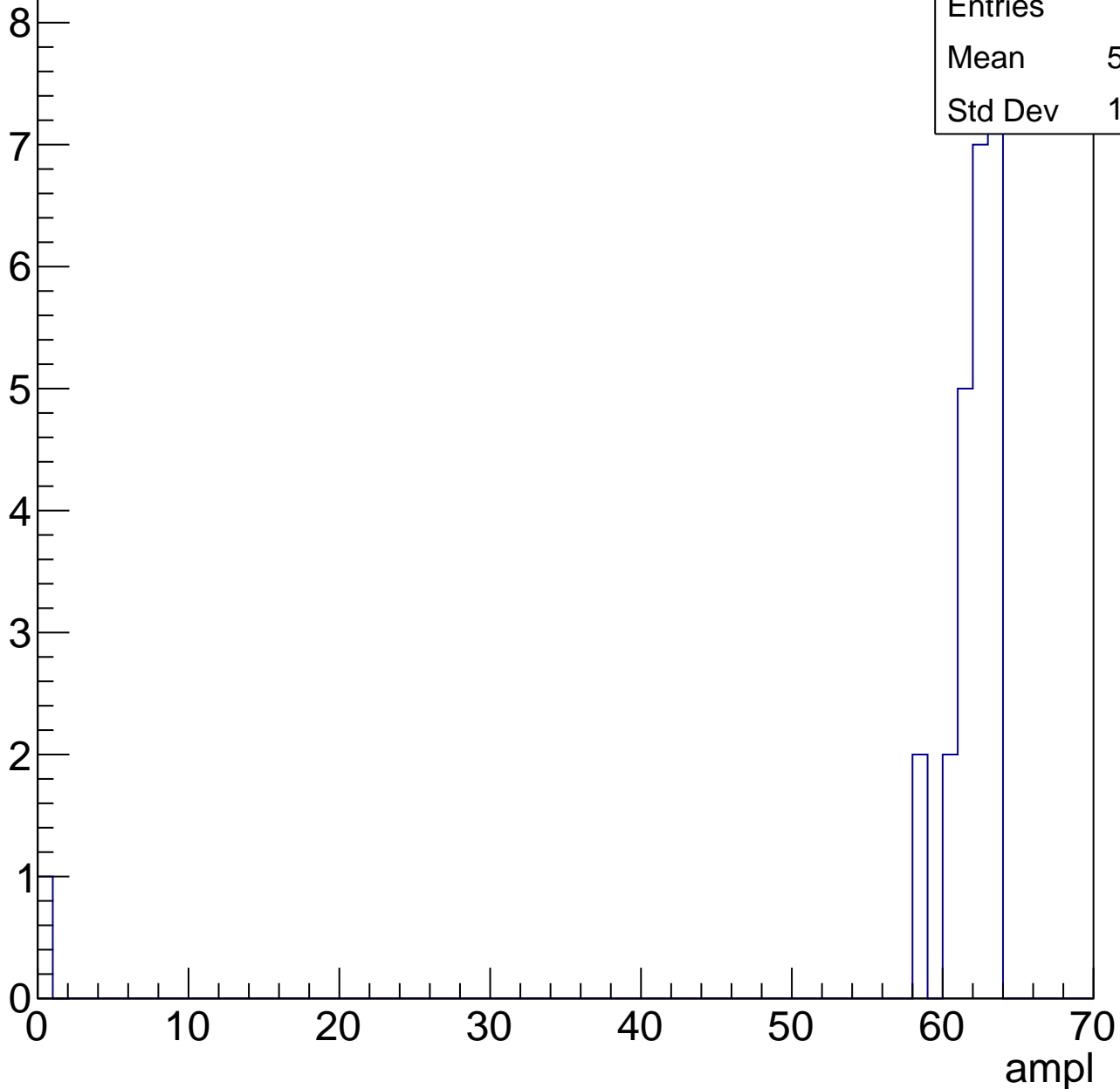


# B1L000S, U2-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	59.16
Std Dev	12.16





# B1L000S, U2-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U2-ch118, adc0

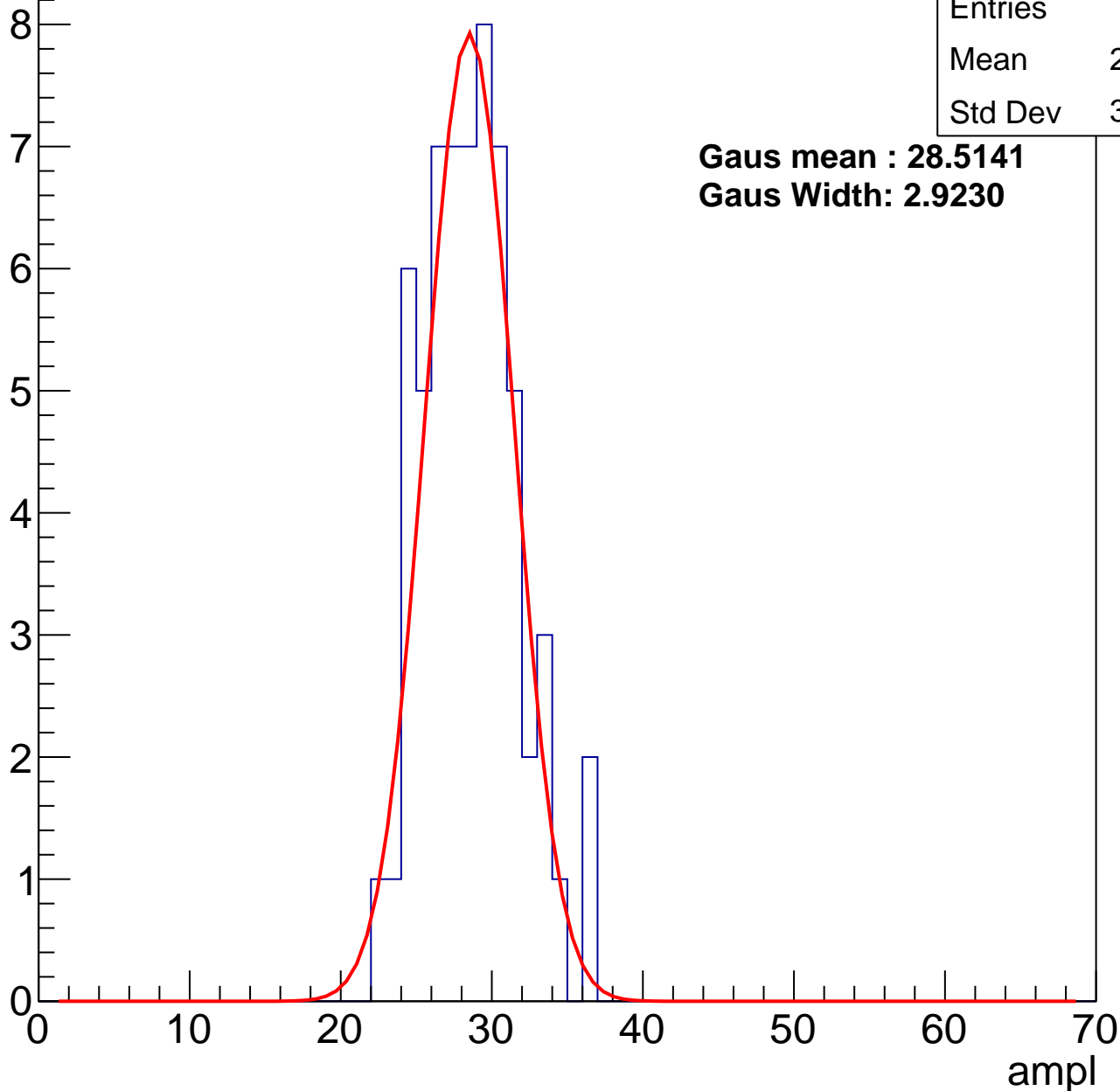
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.18
Std Dev	3.077

**Gaus mean : 28.5141**

**Gaus Width: 2.9230**



# B1L000S, U2-ch118, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	34.68
Std Dev	2.971

**Gaus mean : 35.5806**

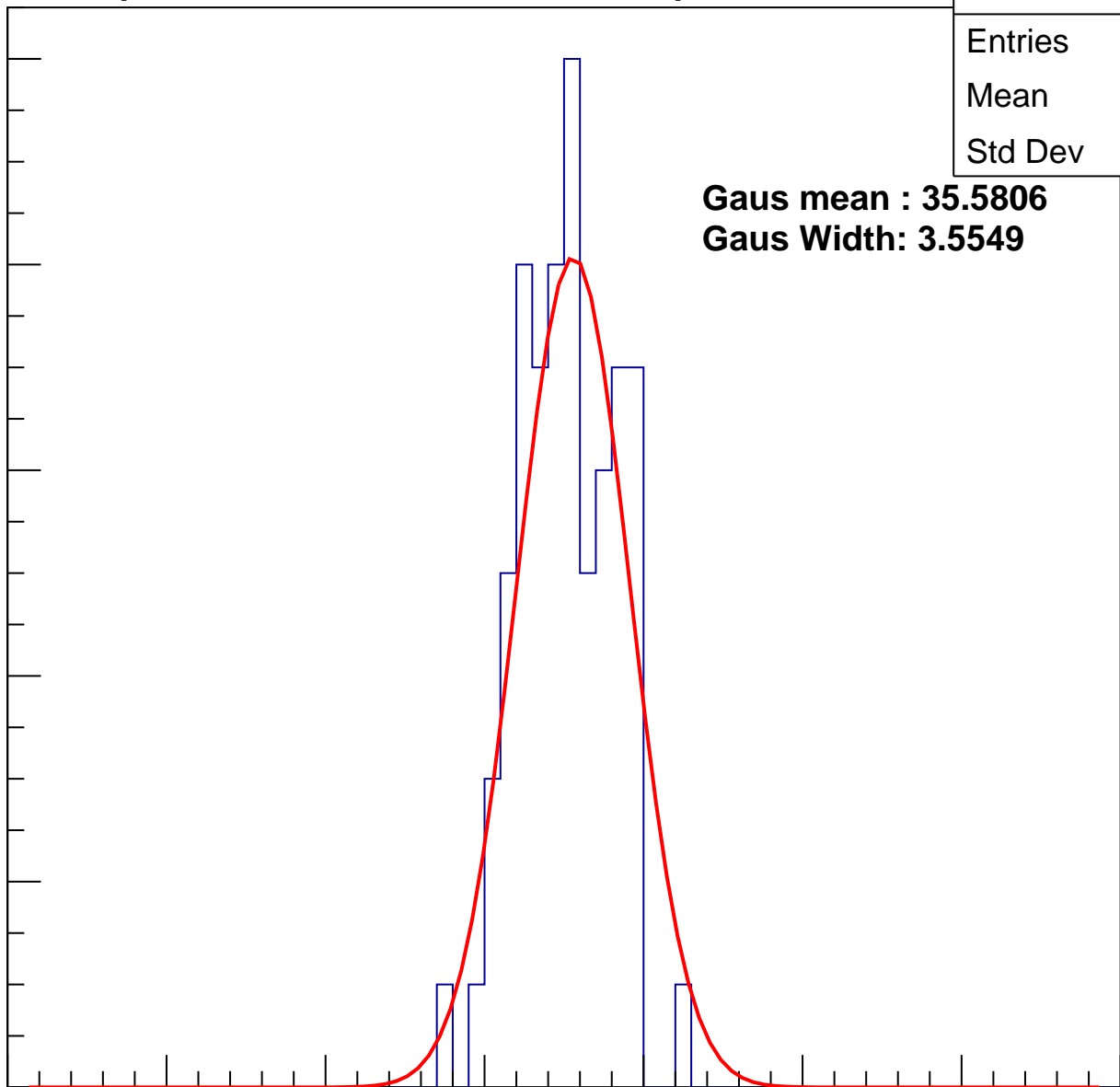
**Gaus Width: 3.5549**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch118, adc2

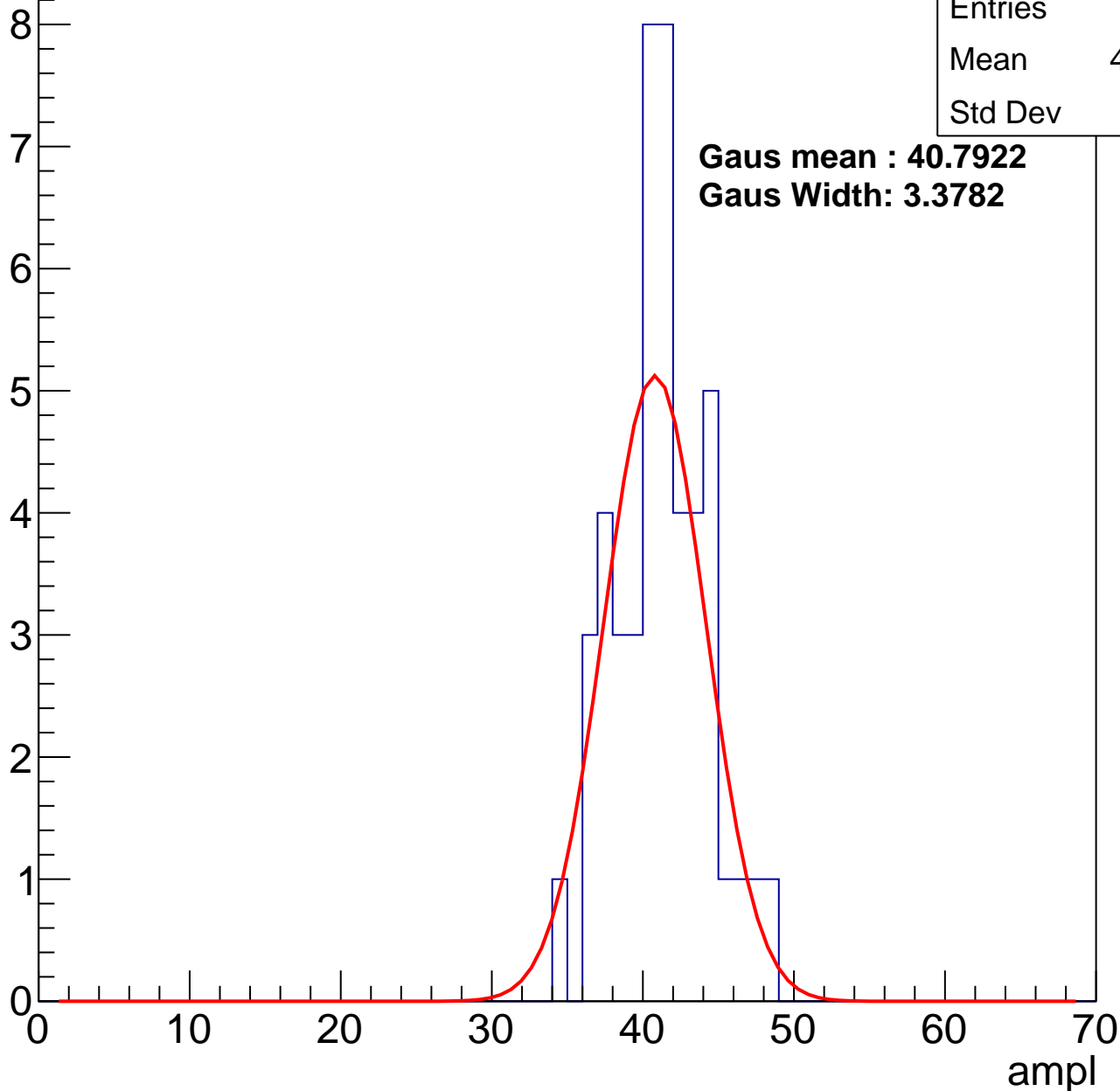
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	40.74
Std Dev	3

**Gaus mean : 40.7922**

**Gaus Width: 3.3782**

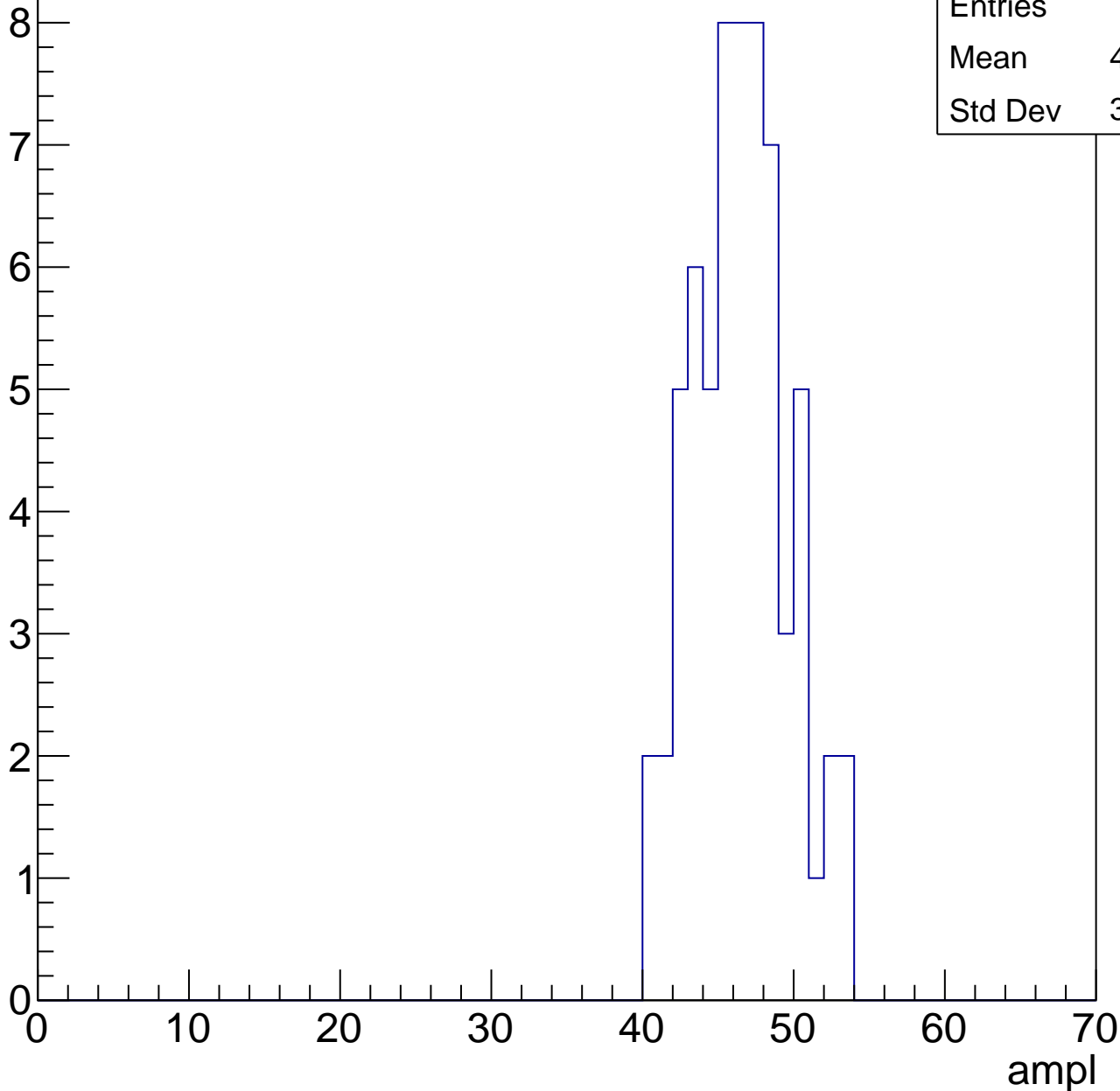


# B1L000S, U2-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	46.06
Std Dev	3.117

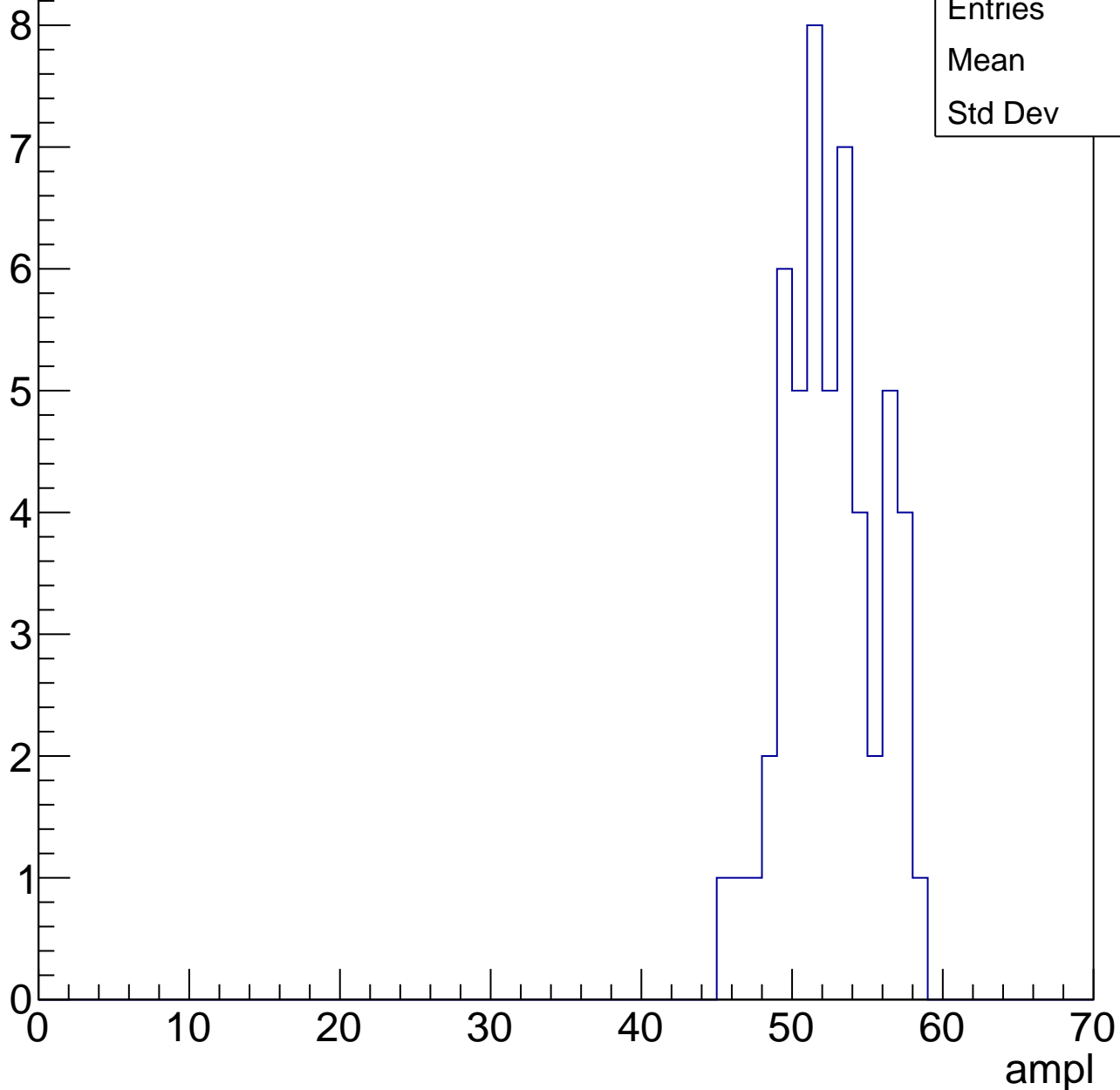


# B1L000S, U2-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	52.1
Std Dev	3.04

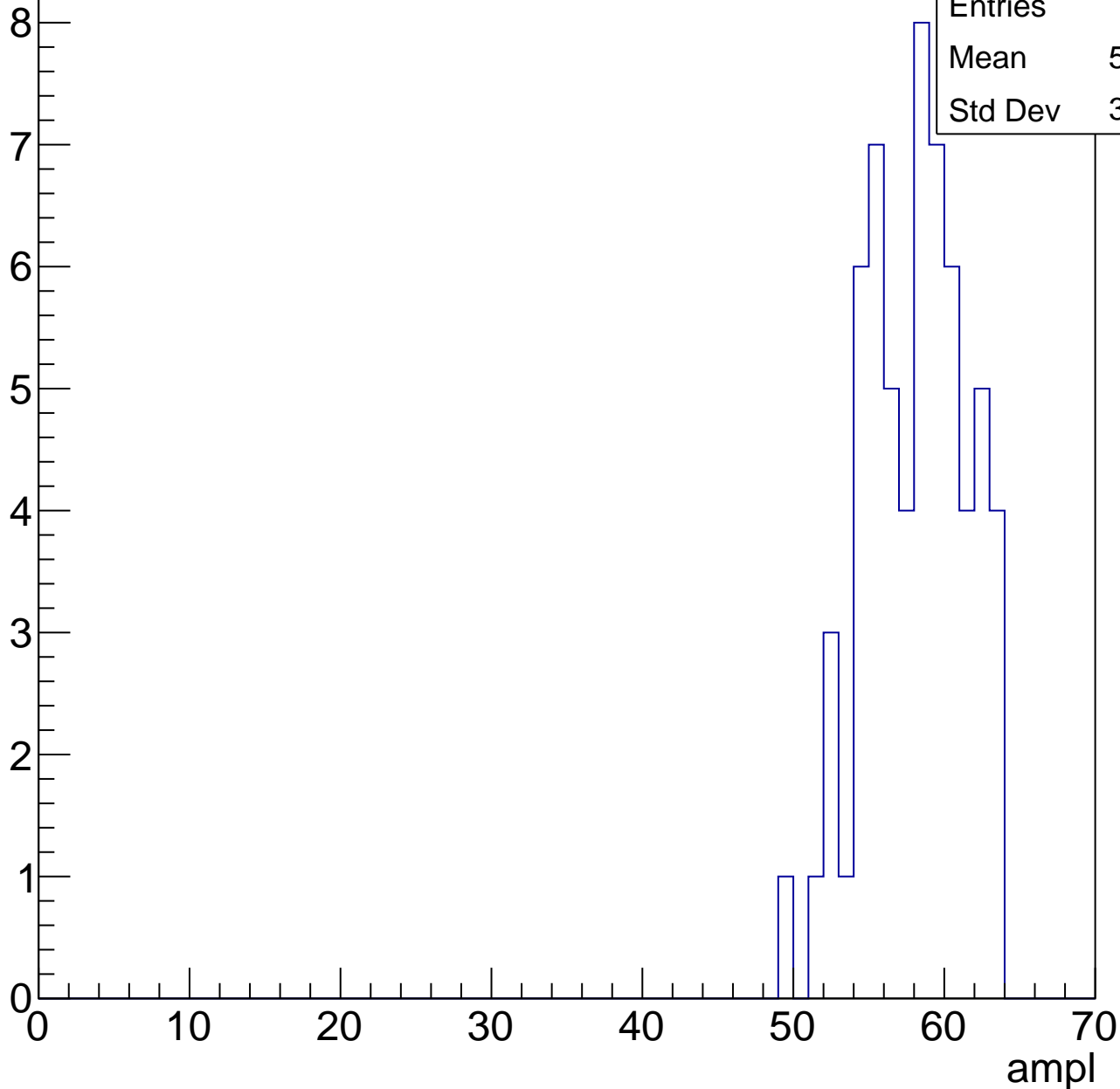


# B1L000S, U2-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	57.56
Std Dev	3.315

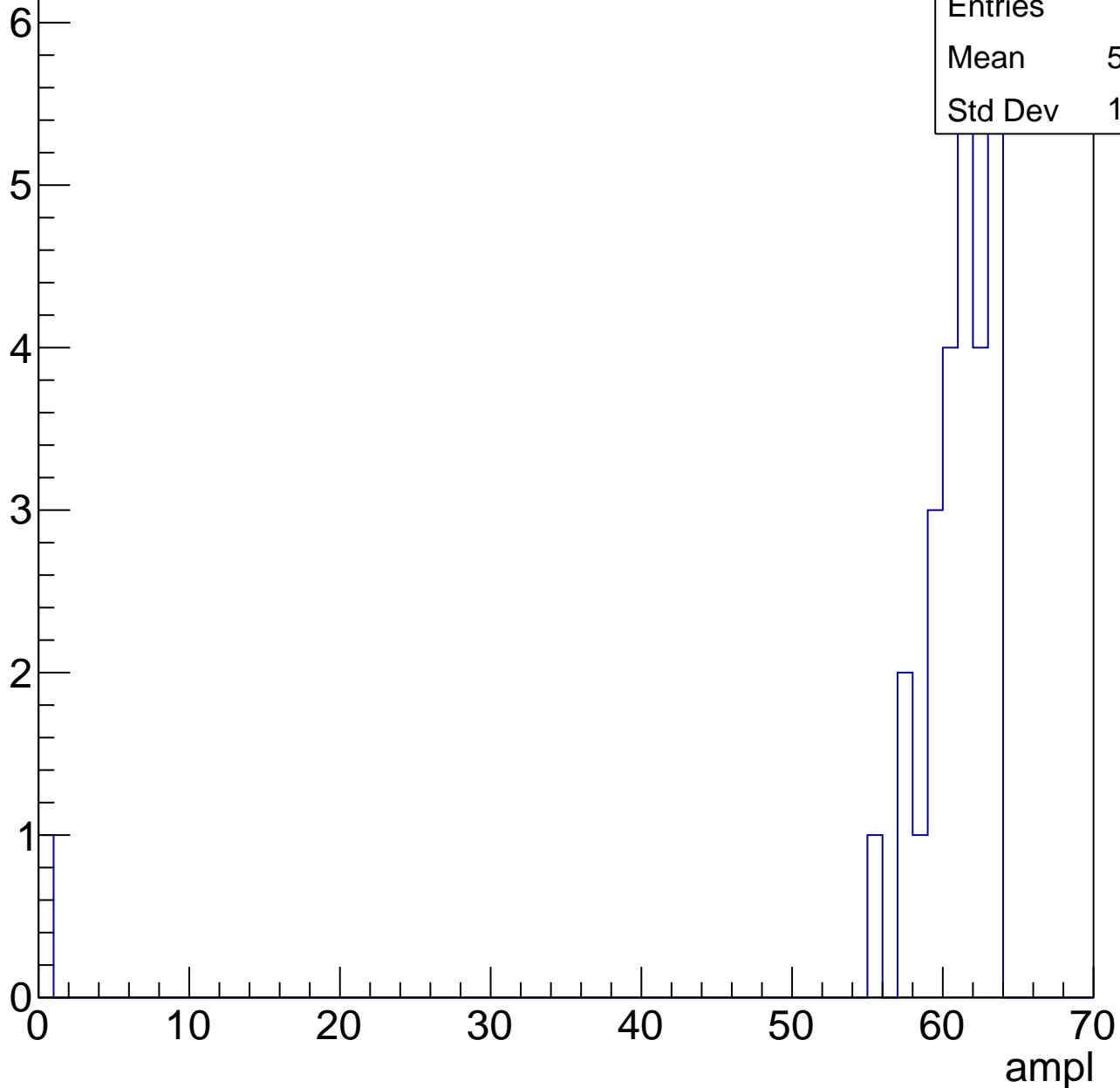


# B1L000S, U2-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.43
Std Dev	11.43

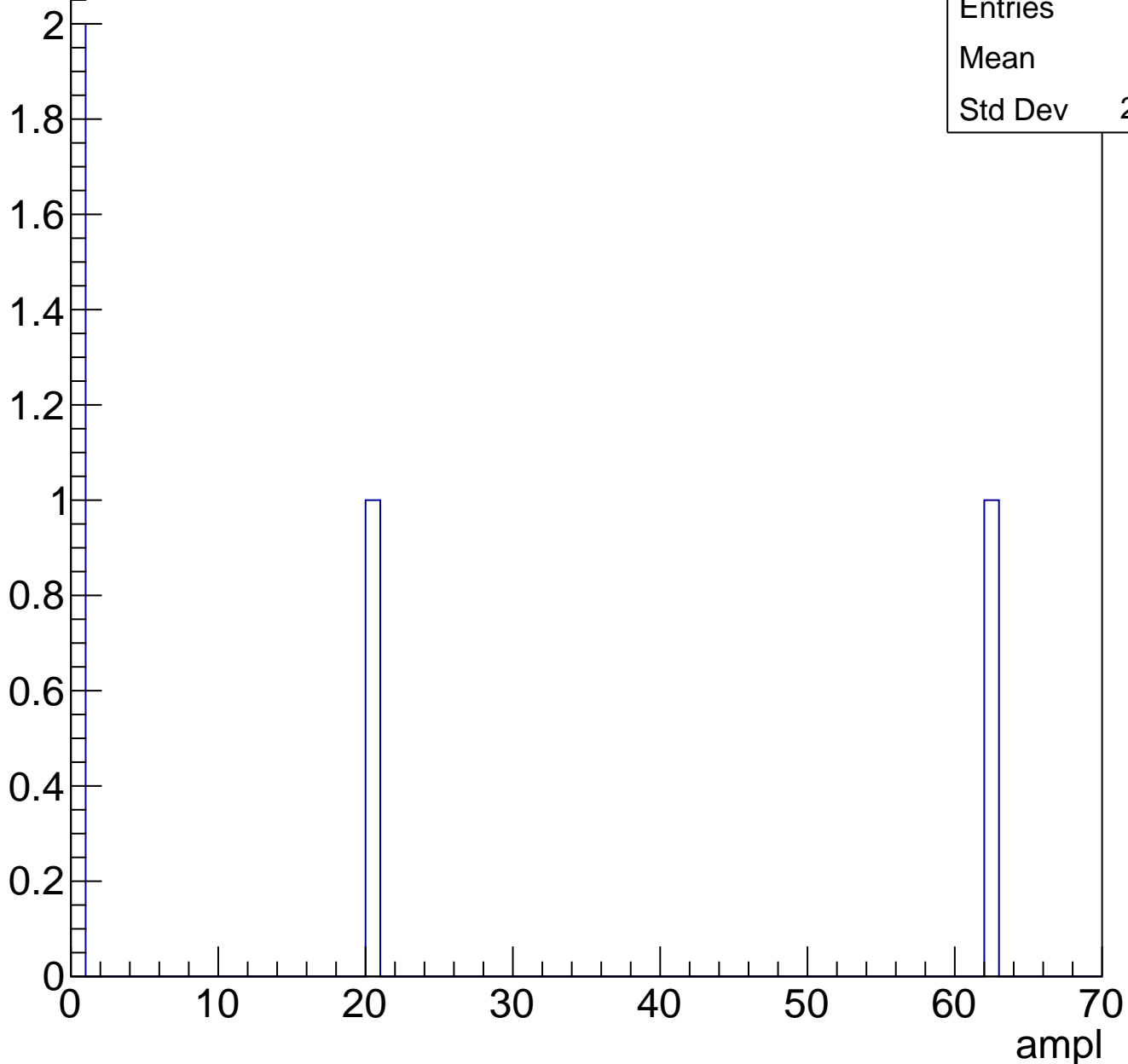




# B1L000S, U2-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	20.5
Std Dev	25.31

# B1L000S, U2-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	28.66
Std Dev	3.496

**Gaus mean : 29.1304**

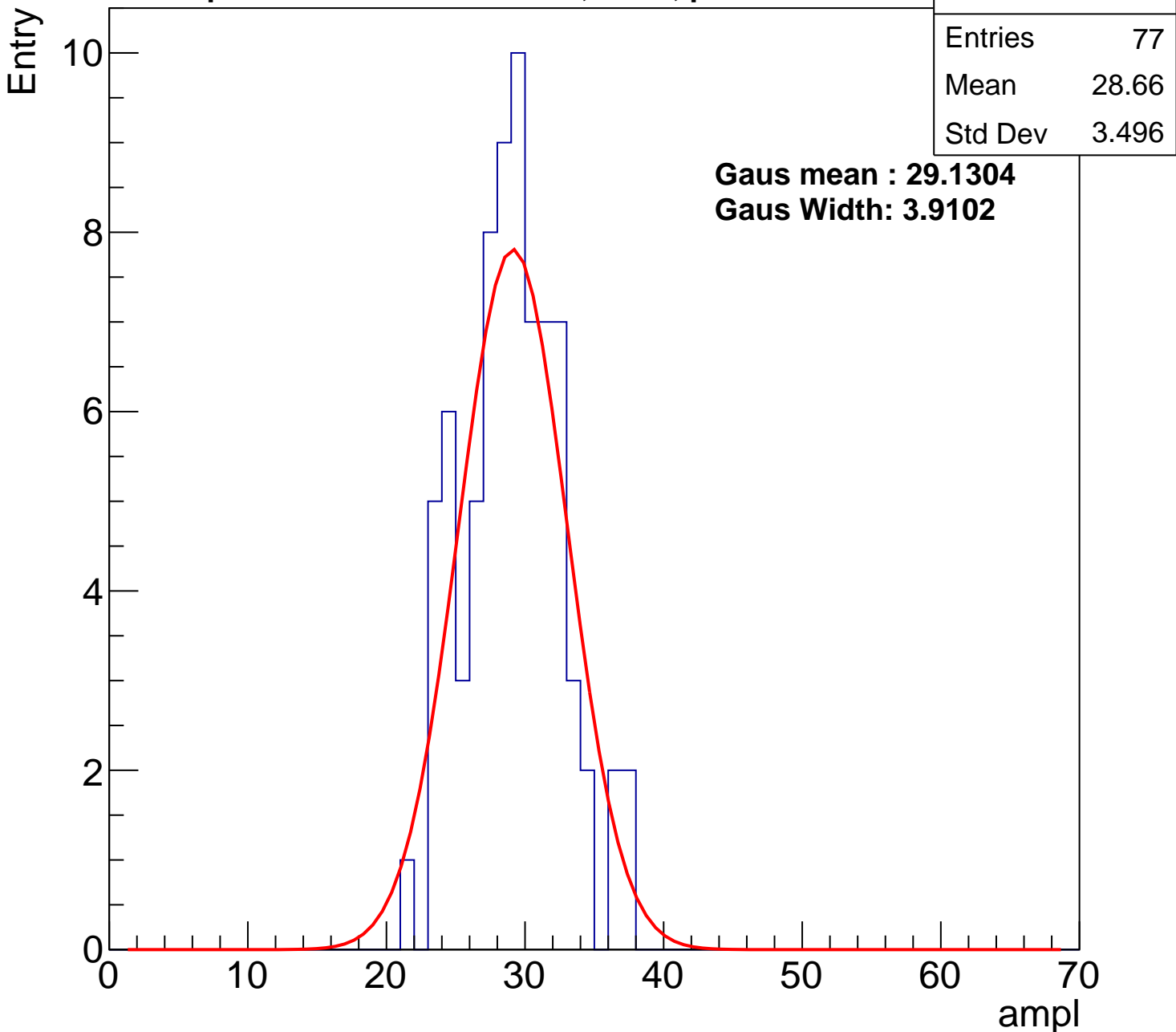
**Gaus Width: 3.9102**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

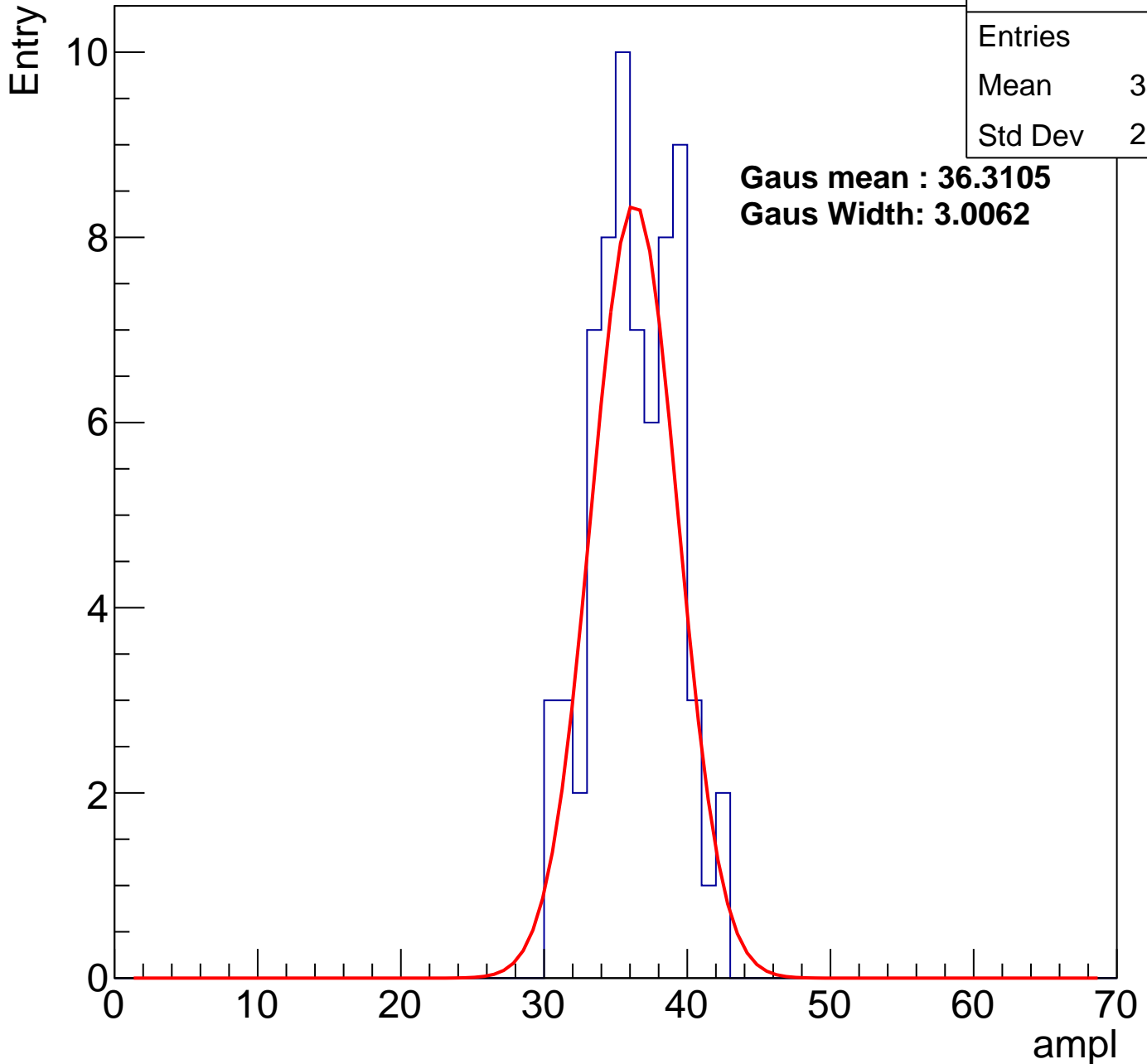


# B1L000S, U2-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	35.86
Std Dev	2.906

**Gaus mean : 36.3105**  
**Gaus Width: 3.0062**



# B1L000S, U2-ch119, adc2

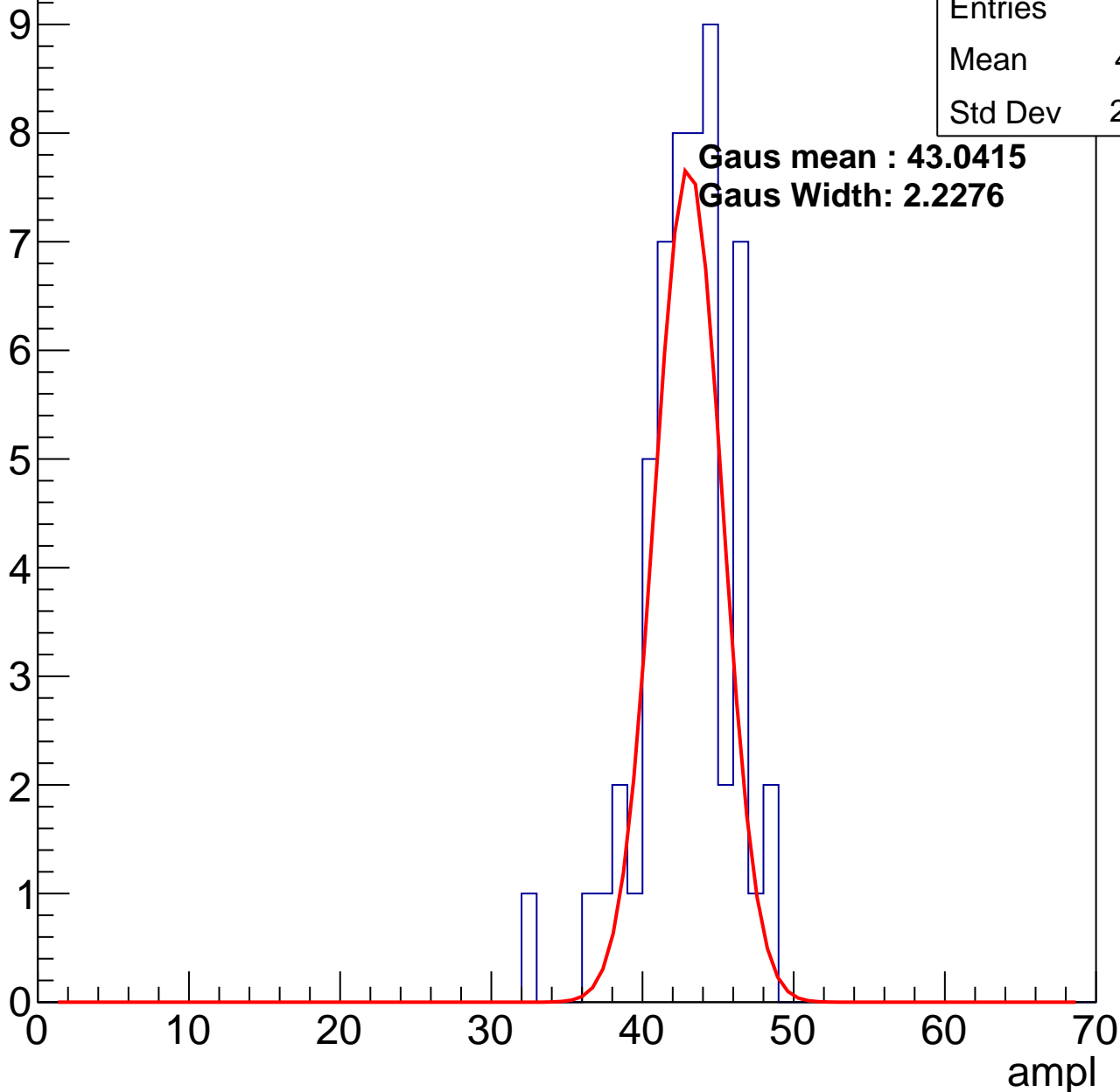
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	42.51
Std Dev	2.972

**Gaus mean : 43.0415**

**Gaus Width: 2.2276**

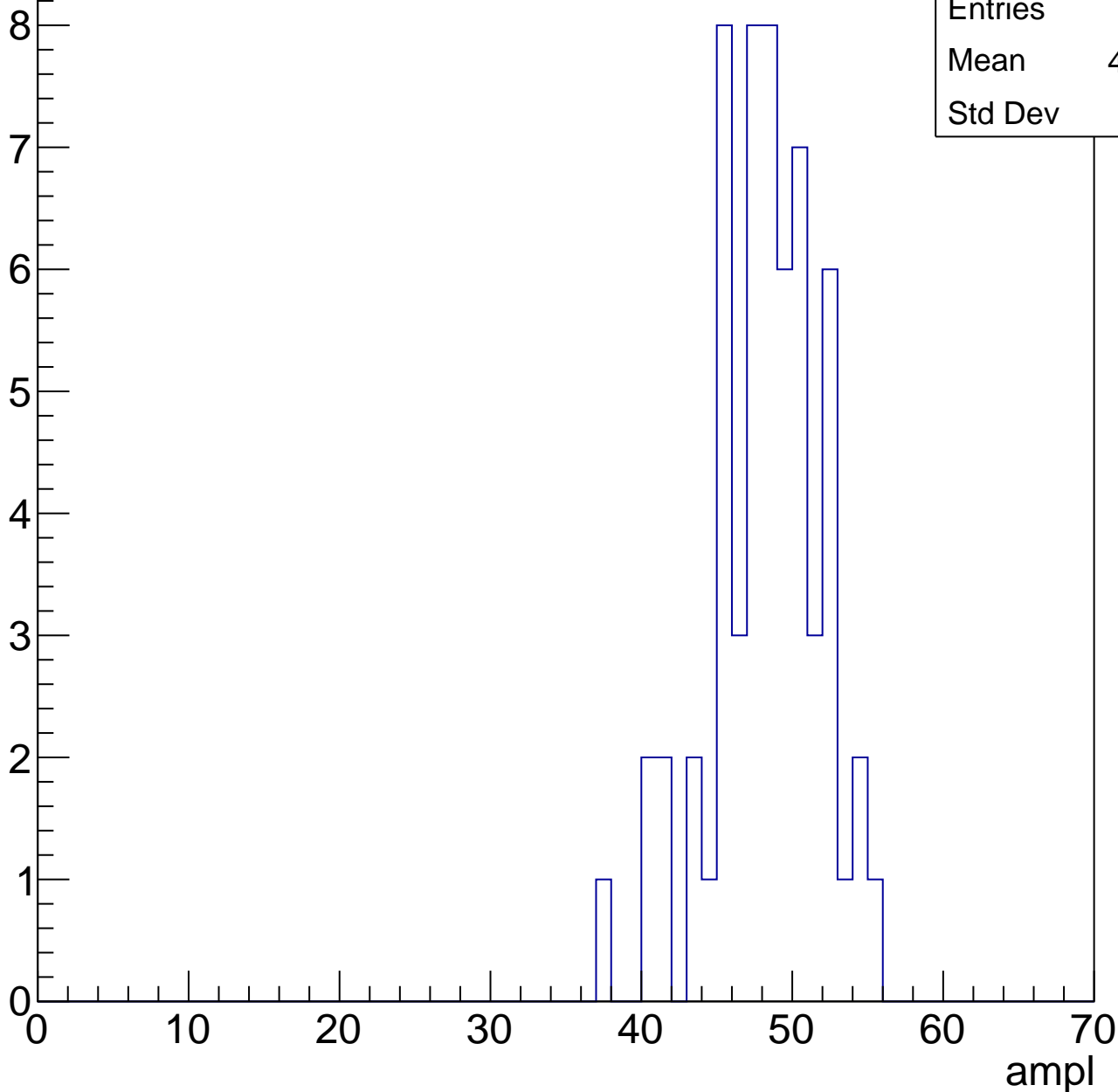


# B1L000S, U2-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	47.74
Std Dev	3.63

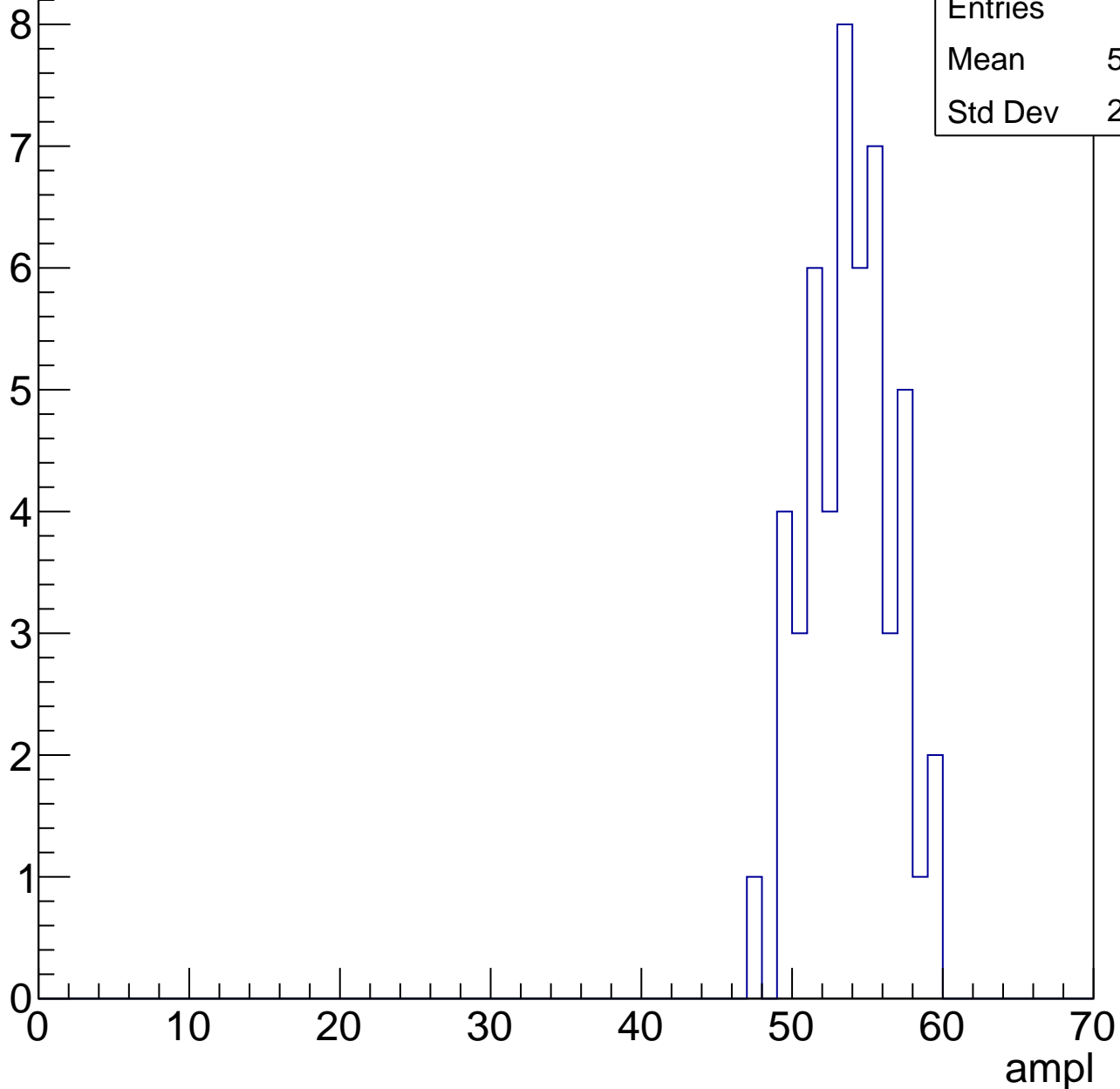


# B1L000S, U2-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	53.38
Std Dev	2.778

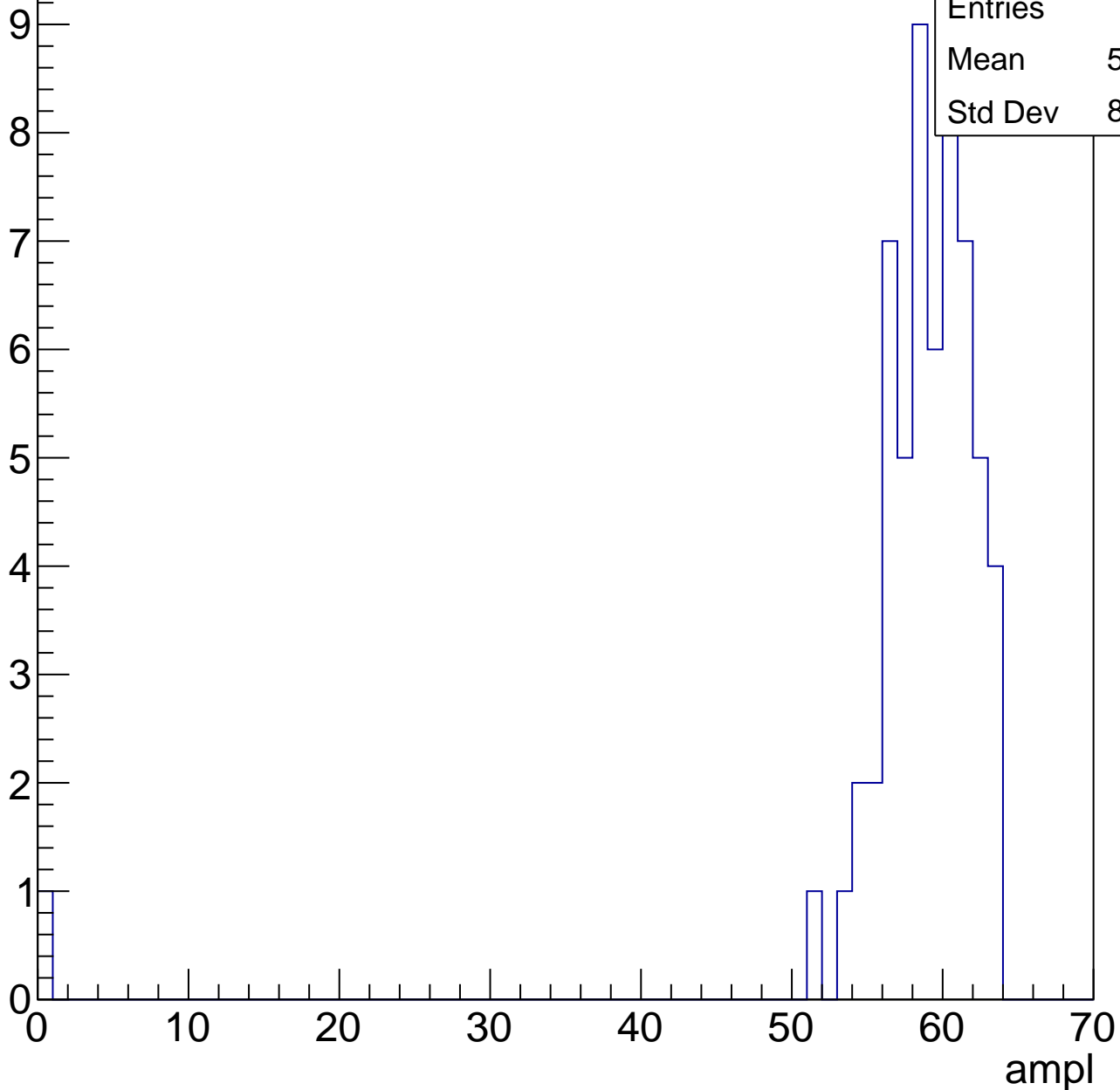


# B1L000S, U2-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.69
Std Dev	8.026

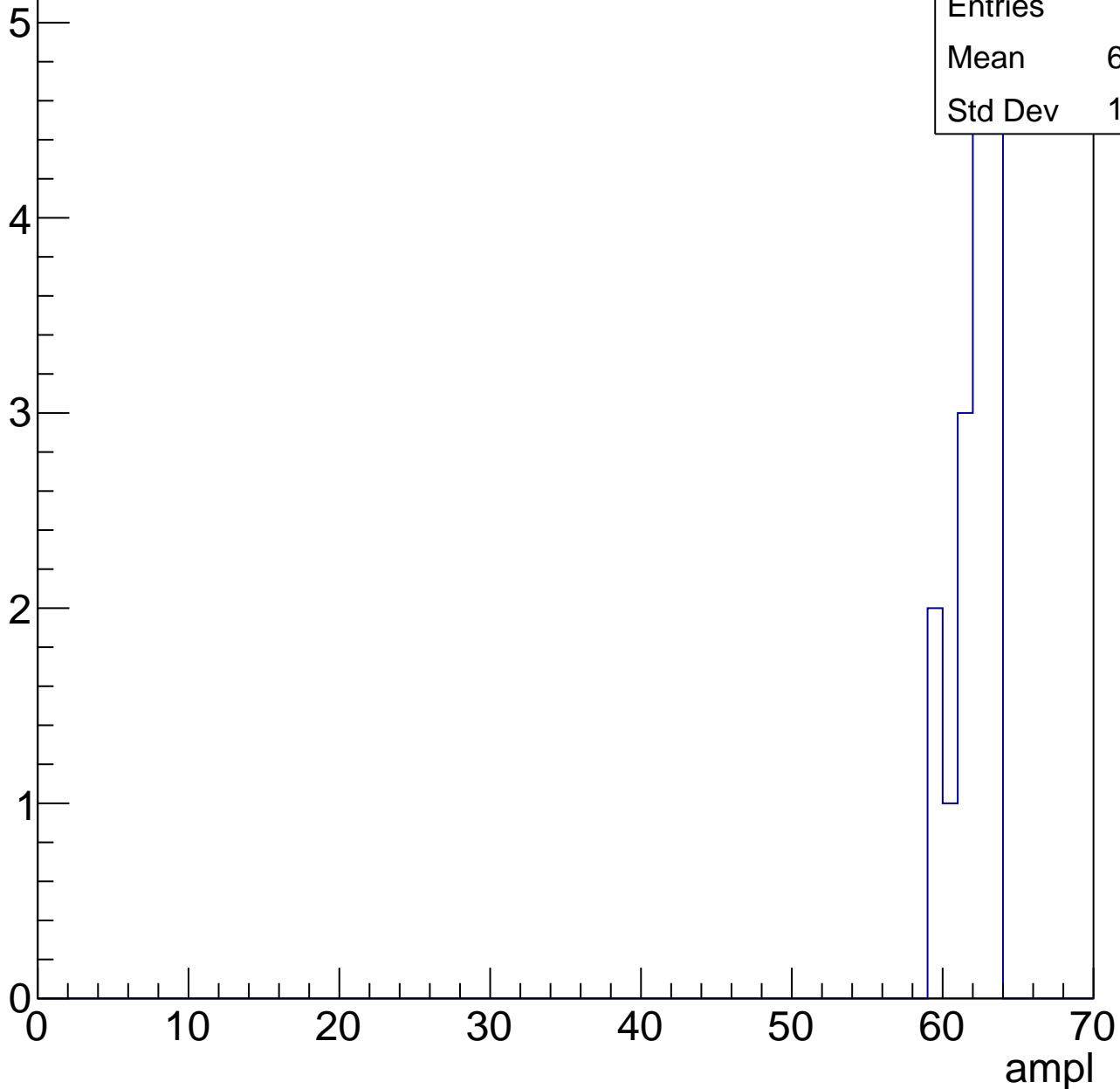


# B1L000S, U2-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.62
Std Dev	1.317





# B1L000S, U2-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch120, adc0

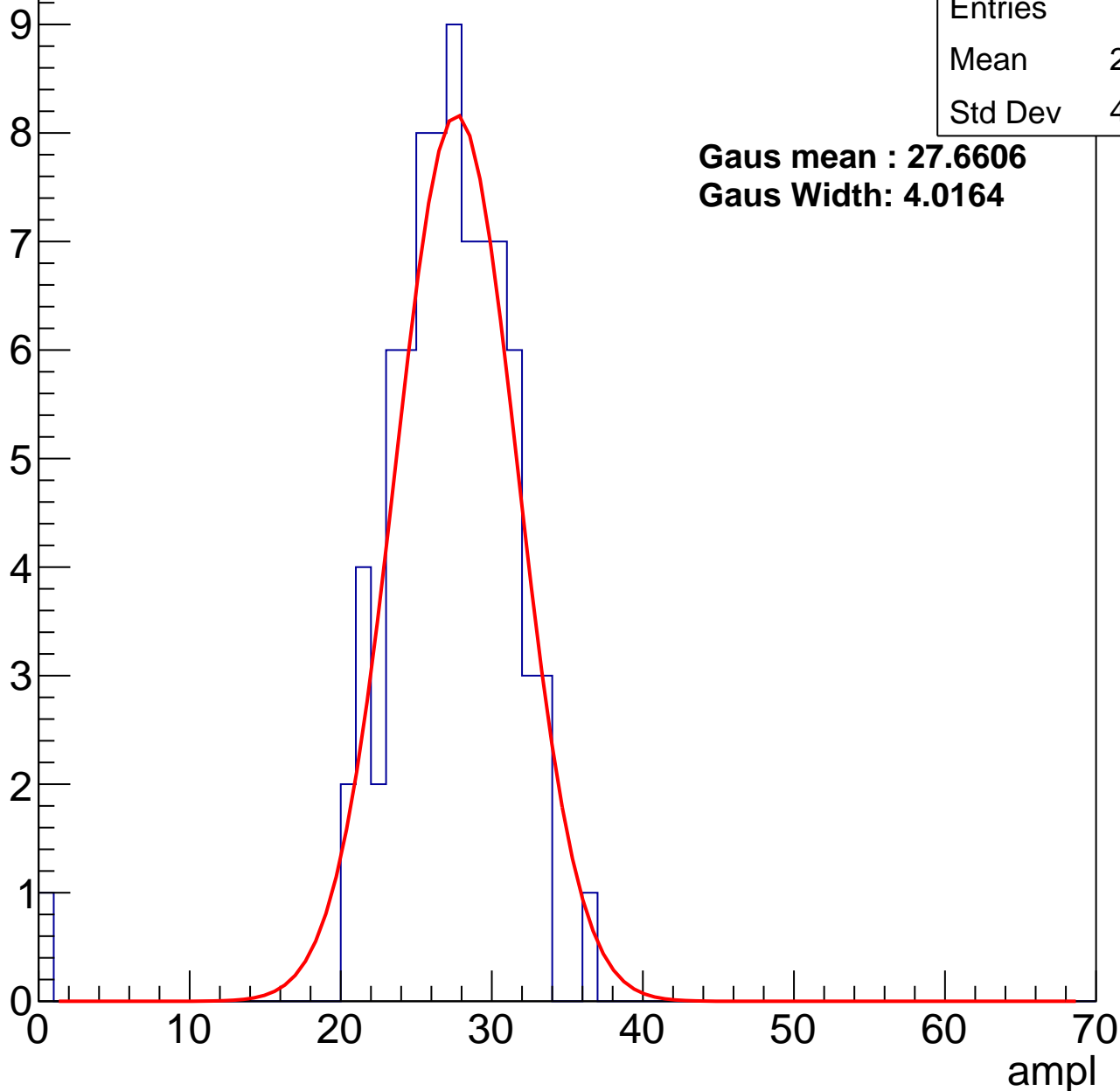
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	26.59
Std Dev	4.549

**Gaus mean : 27.6606**

**Gaus Width: 4.0164**



# B1L000S, U2-ch120, adc1

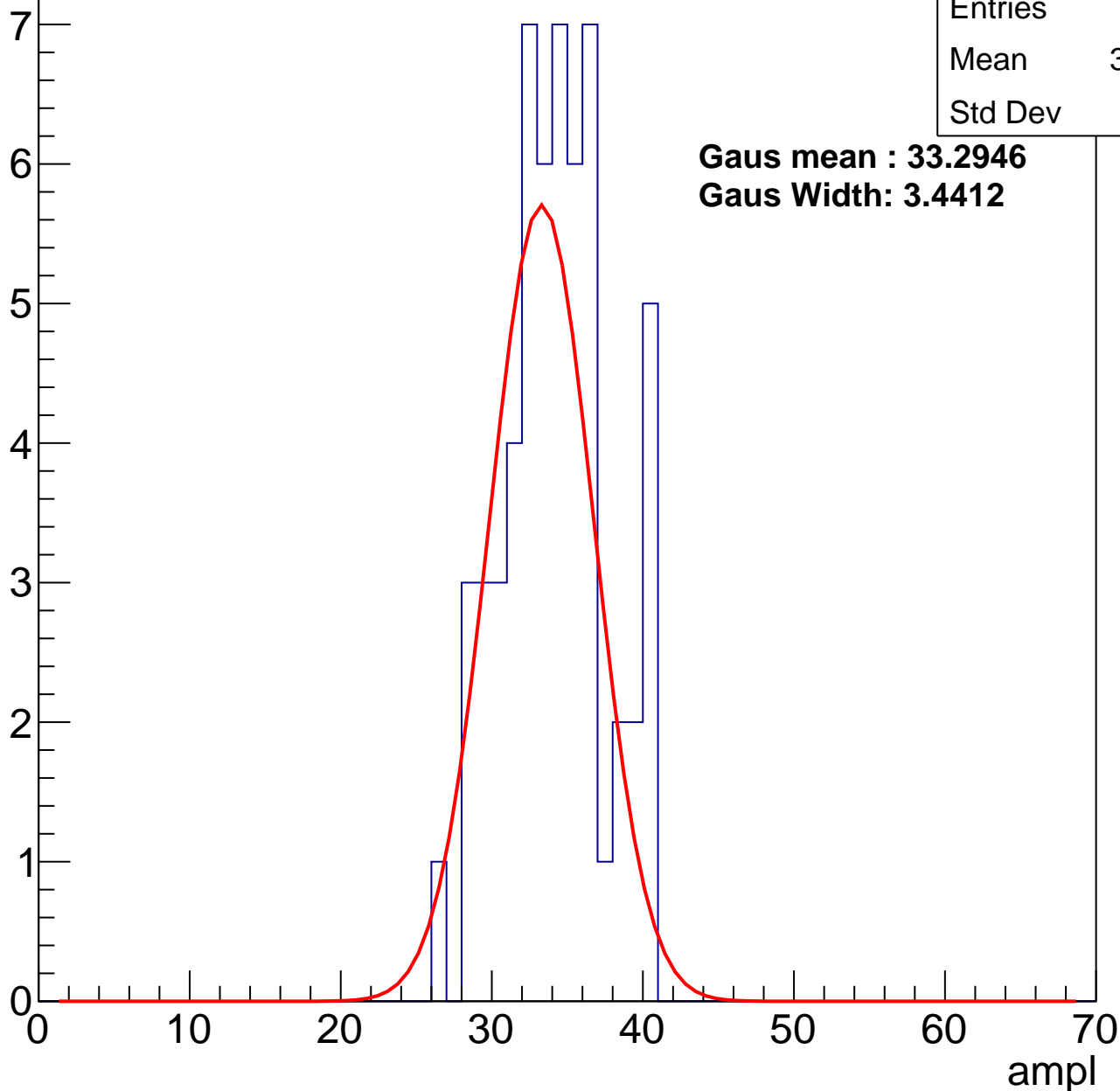
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	33.75
Std Dev	3.42

**Gaus mean : 33.2946**

**Gaus Width: 3.4412**



# B1L000S, U2-ch120, adc2

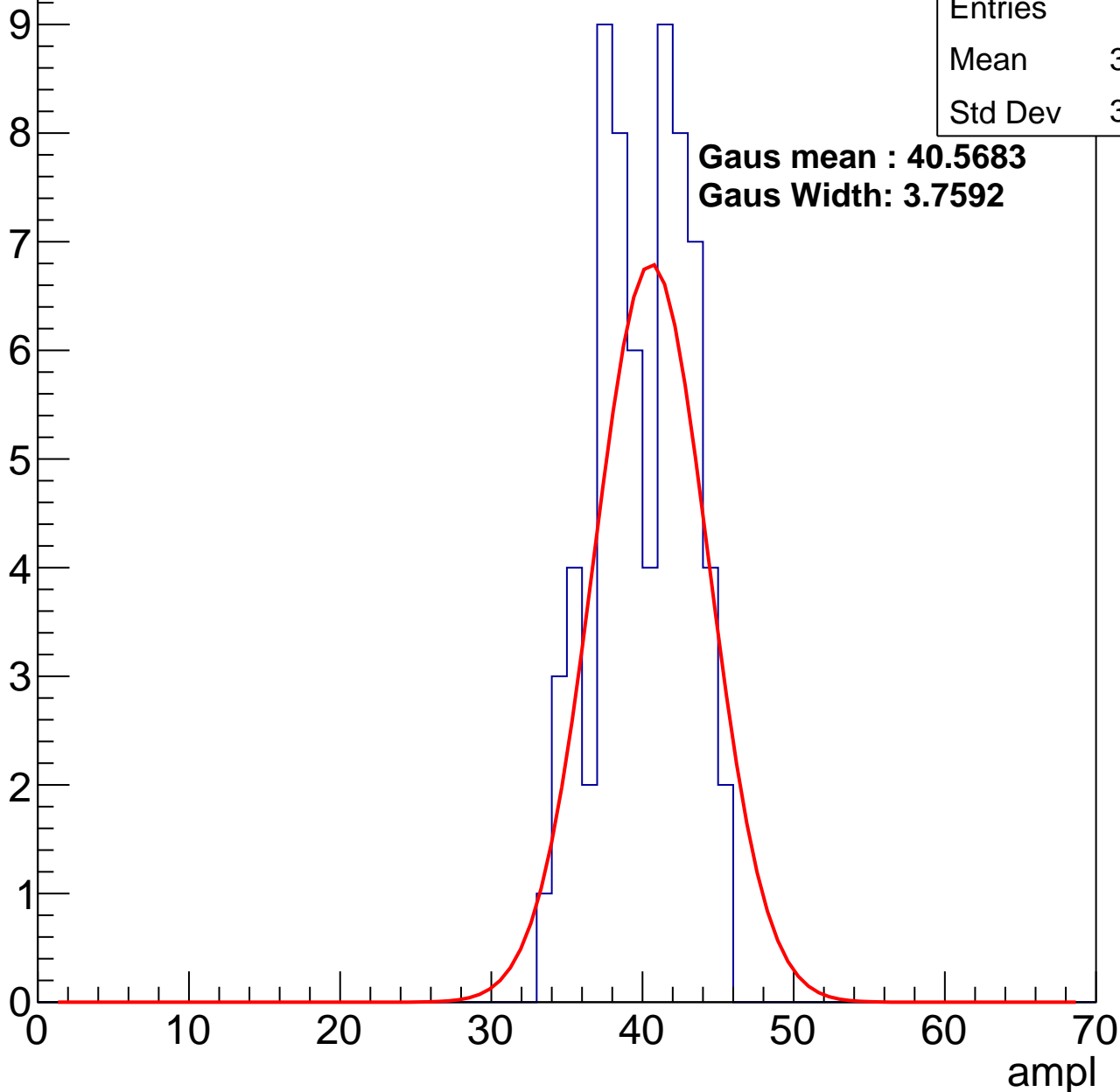
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	39.55
Std Dev	3.024

**Gaus mean : 40.5683**

**Gaus Width: 3.7592**

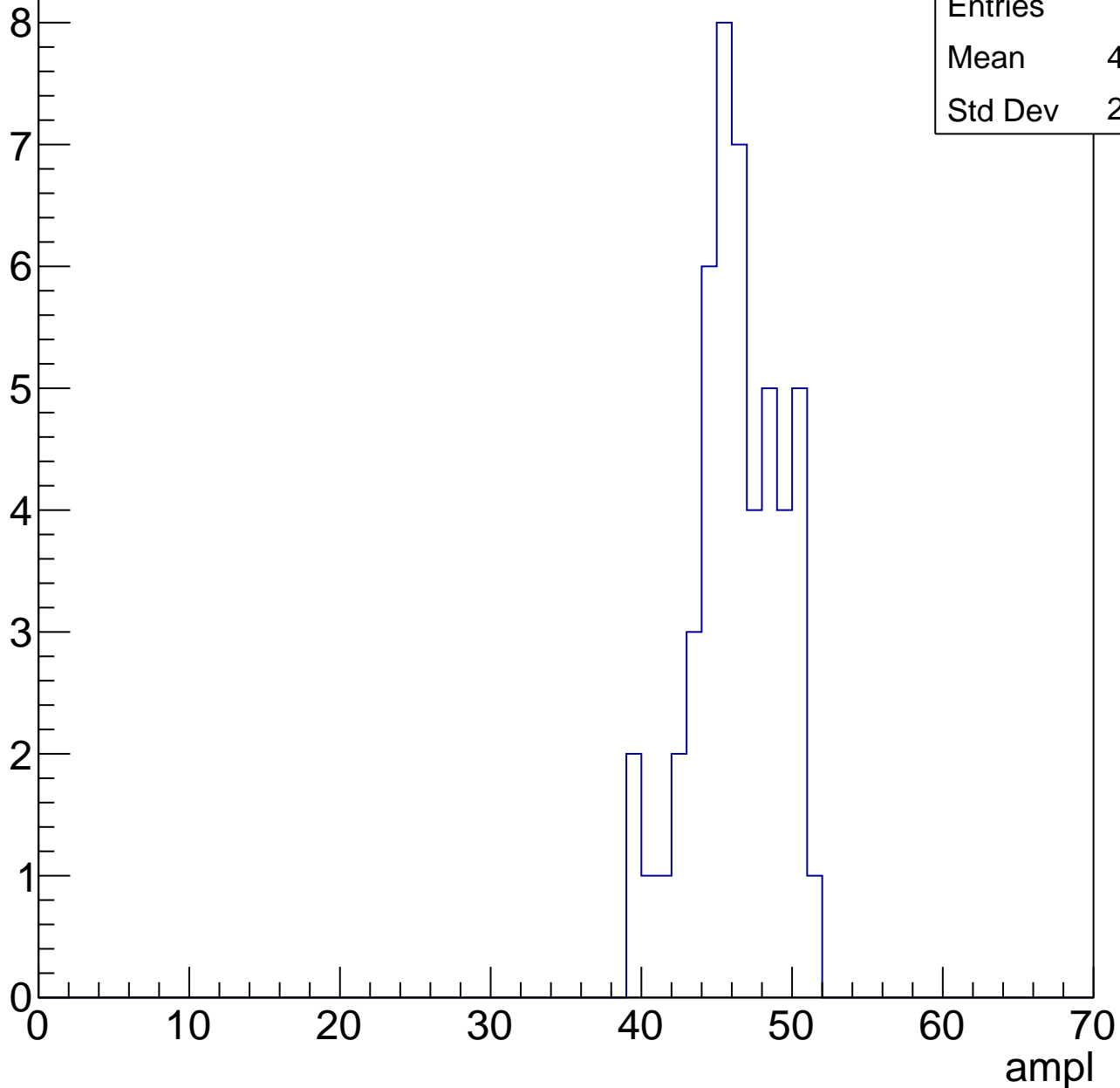


# B1L000S, U2-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	45.78
Std Dev	2.902

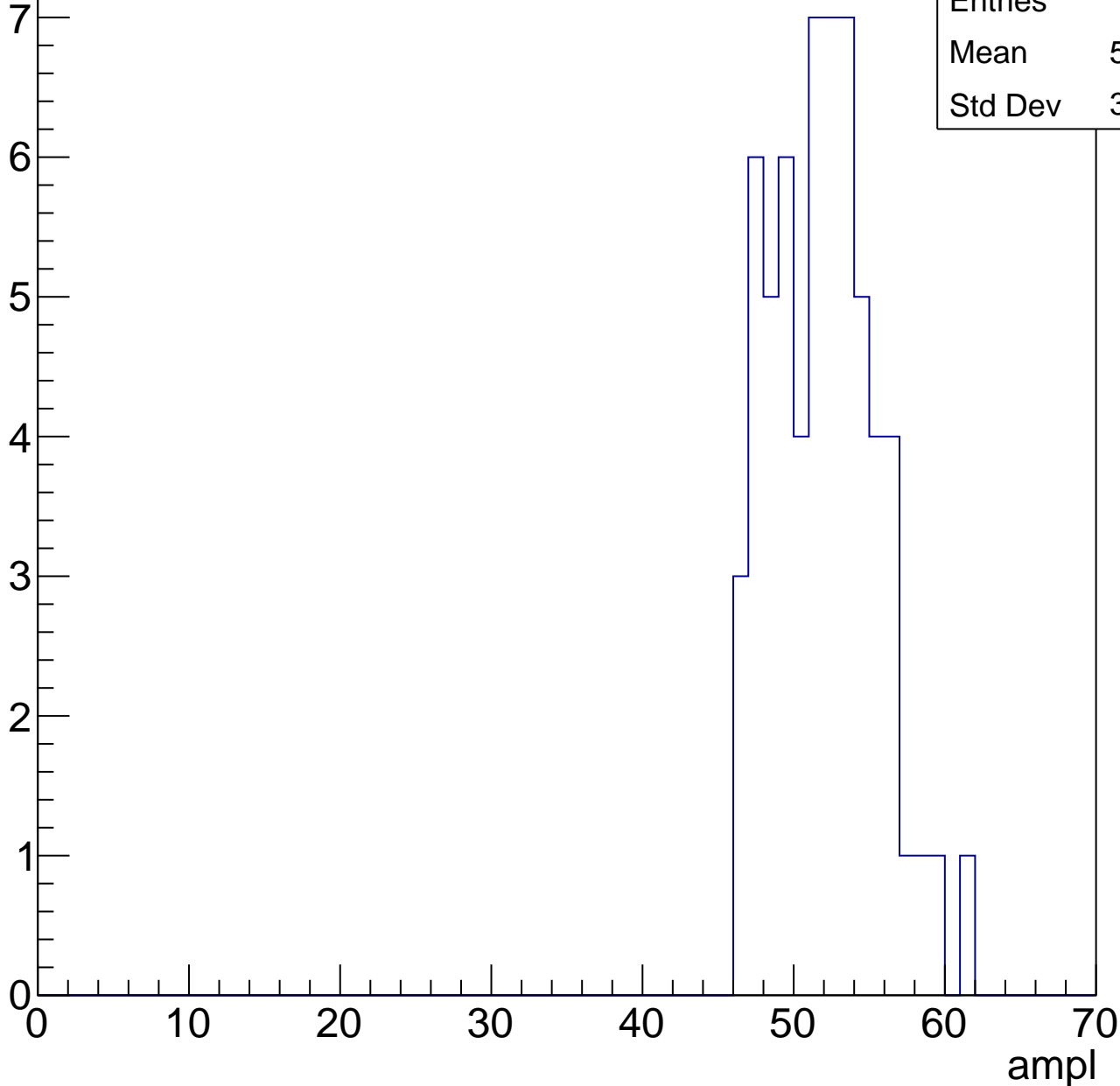


# B1L000S, U2-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

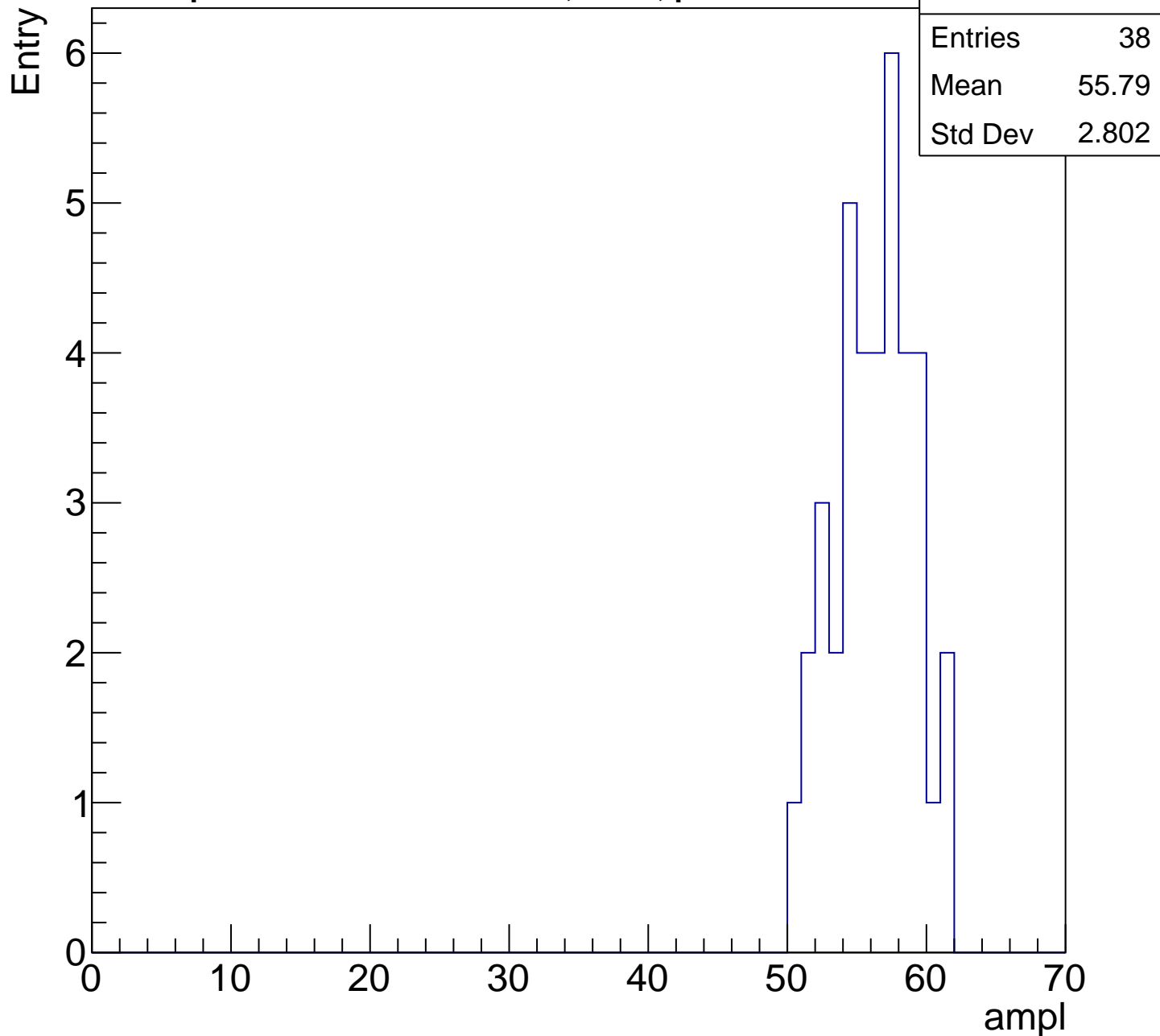
Entry

Entries	62
Mean	51.53
Std Dev	3.406



# B1L000S, U2-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

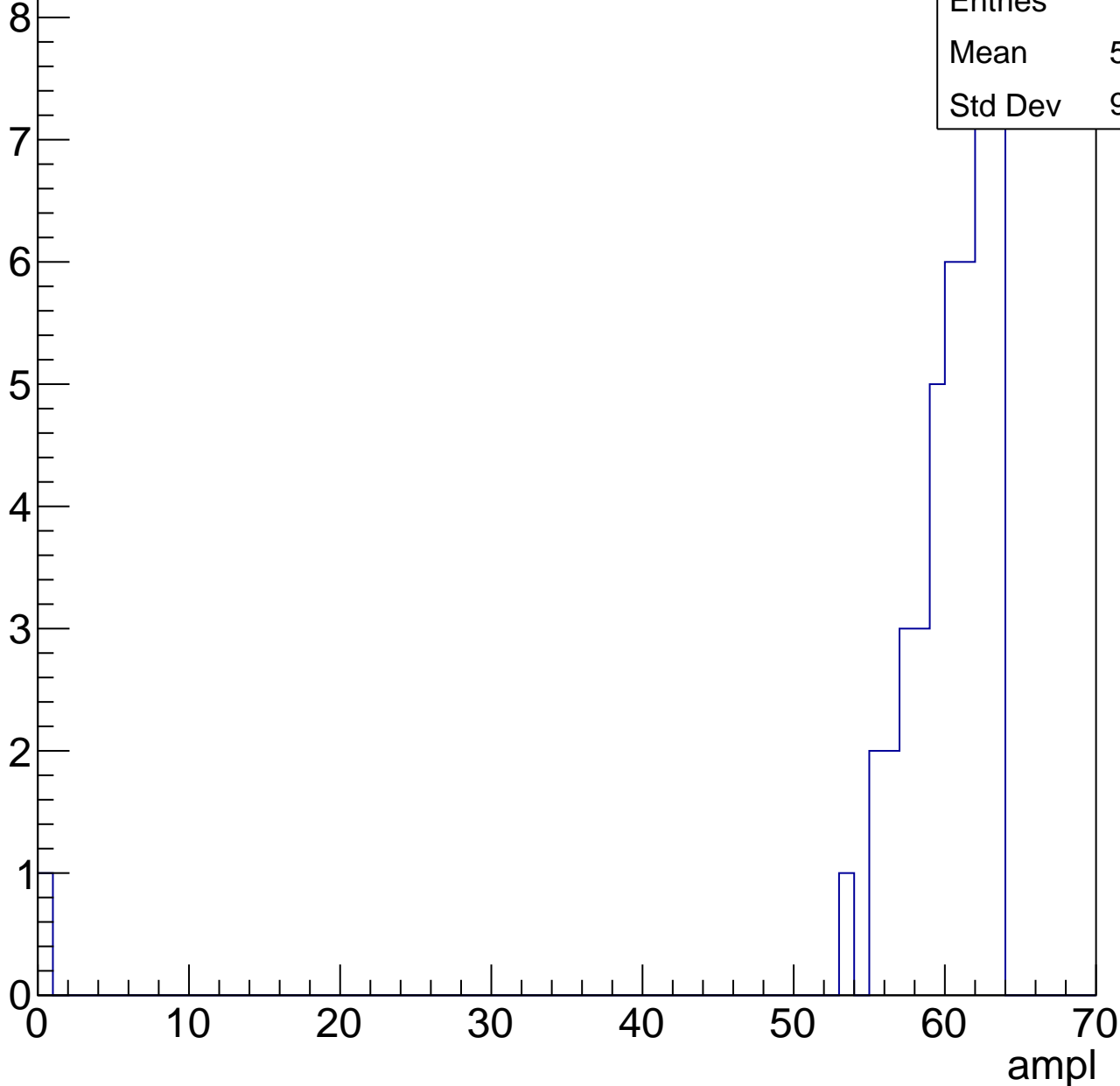


# B1L000S, U2-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	58.69
Std Dev	9.199

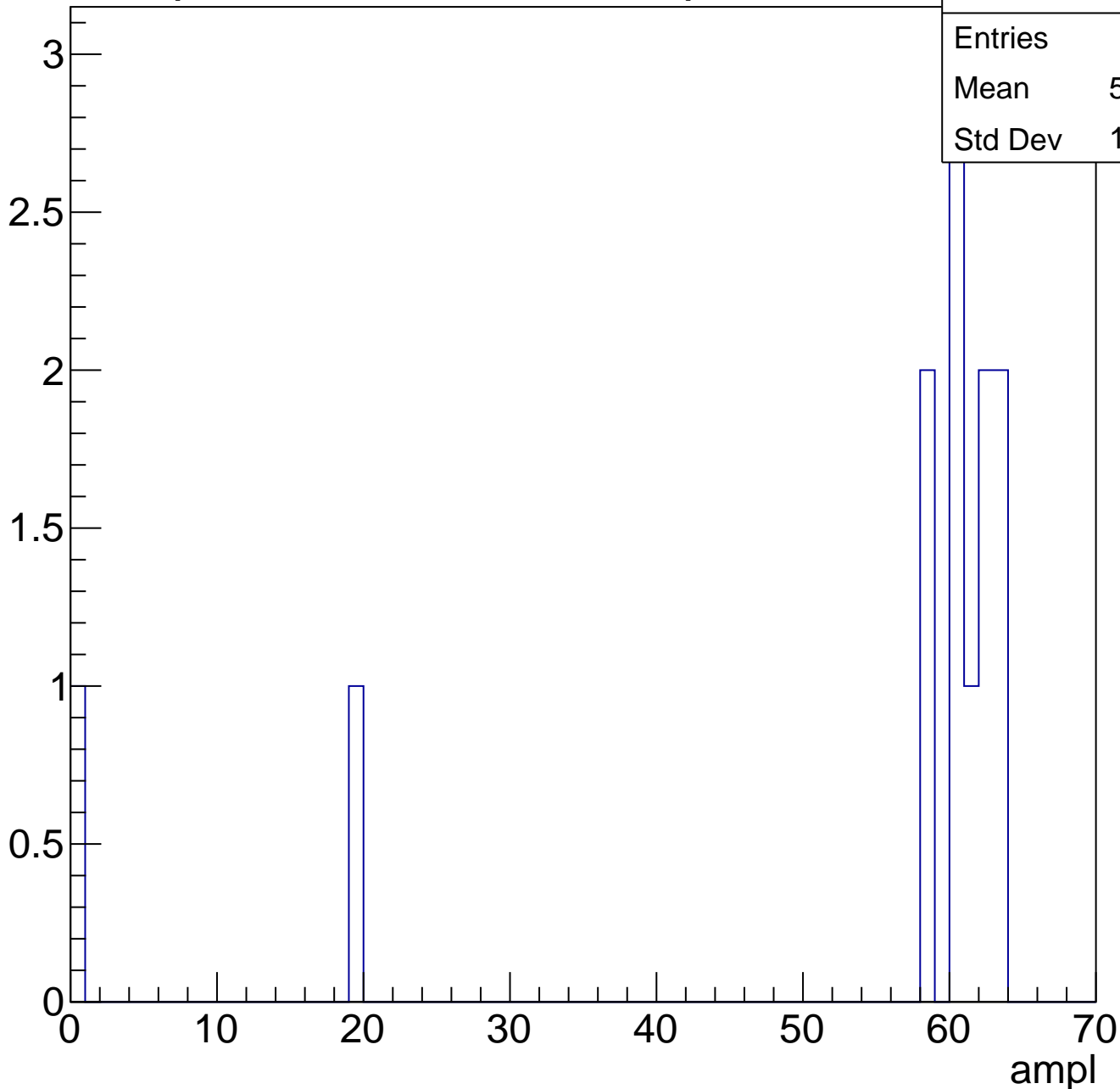




# B1L000S, U2-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch121, adc0

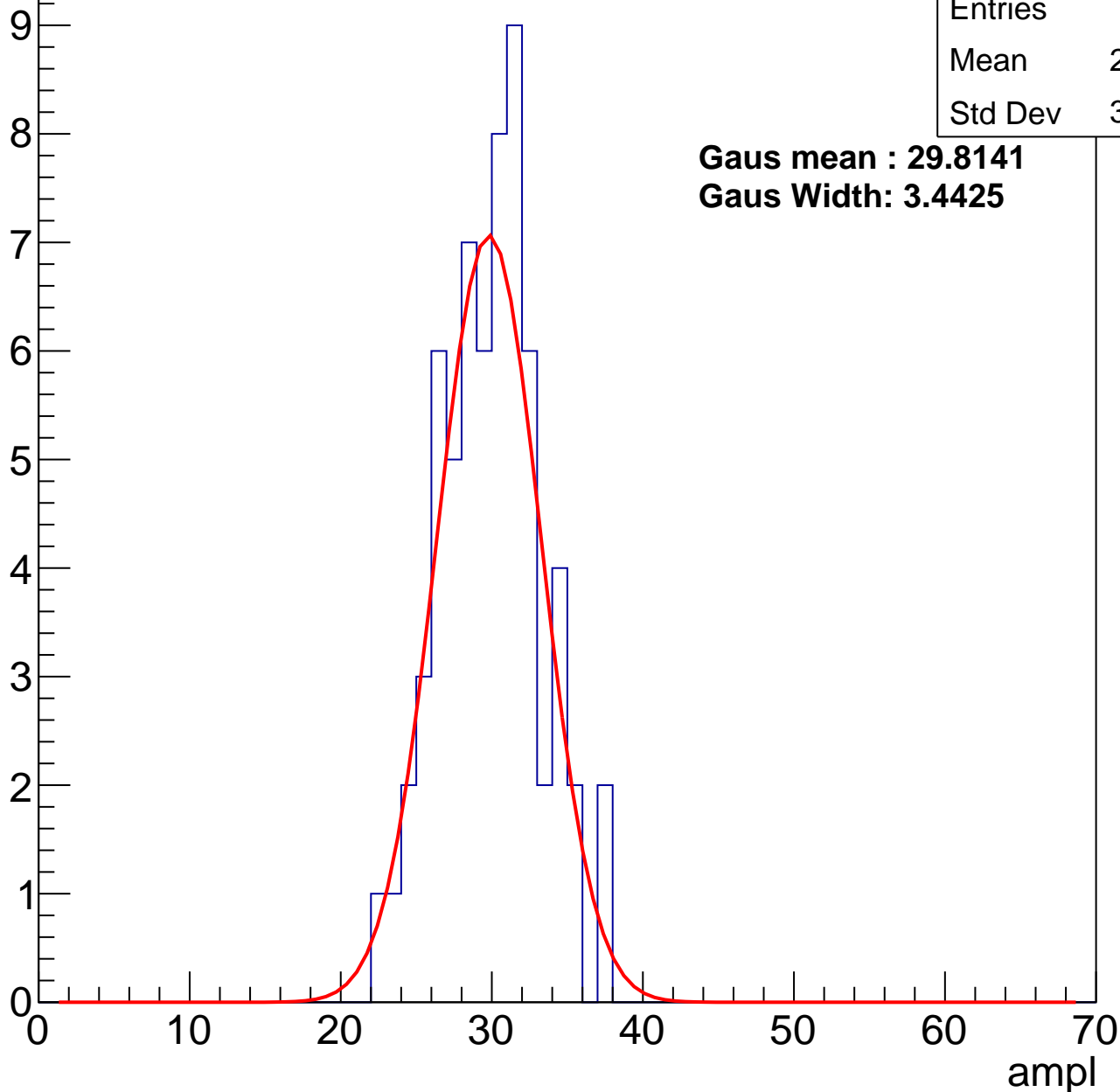
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.47
Std Dev	3.259

**Gaus mean : 29.8141**

**Gaus Width: 3.4425**



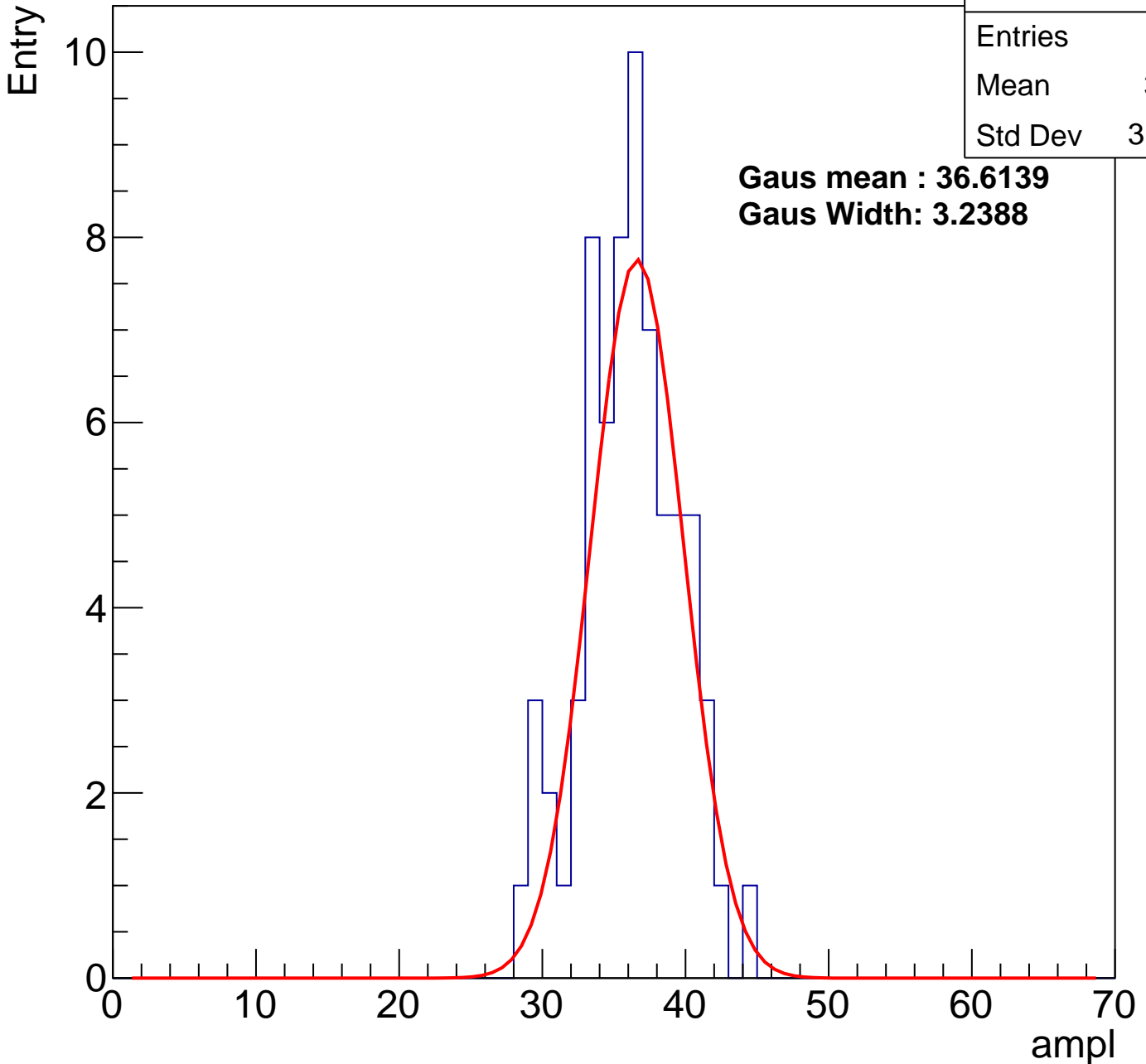
# B1L000S, U2-ch121, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	35.7
Std Dev	3.364

**Gaus mean : 36.6139**

**Gaus Width: 3.2388**



# B1L000S, U2-ch121, adc2

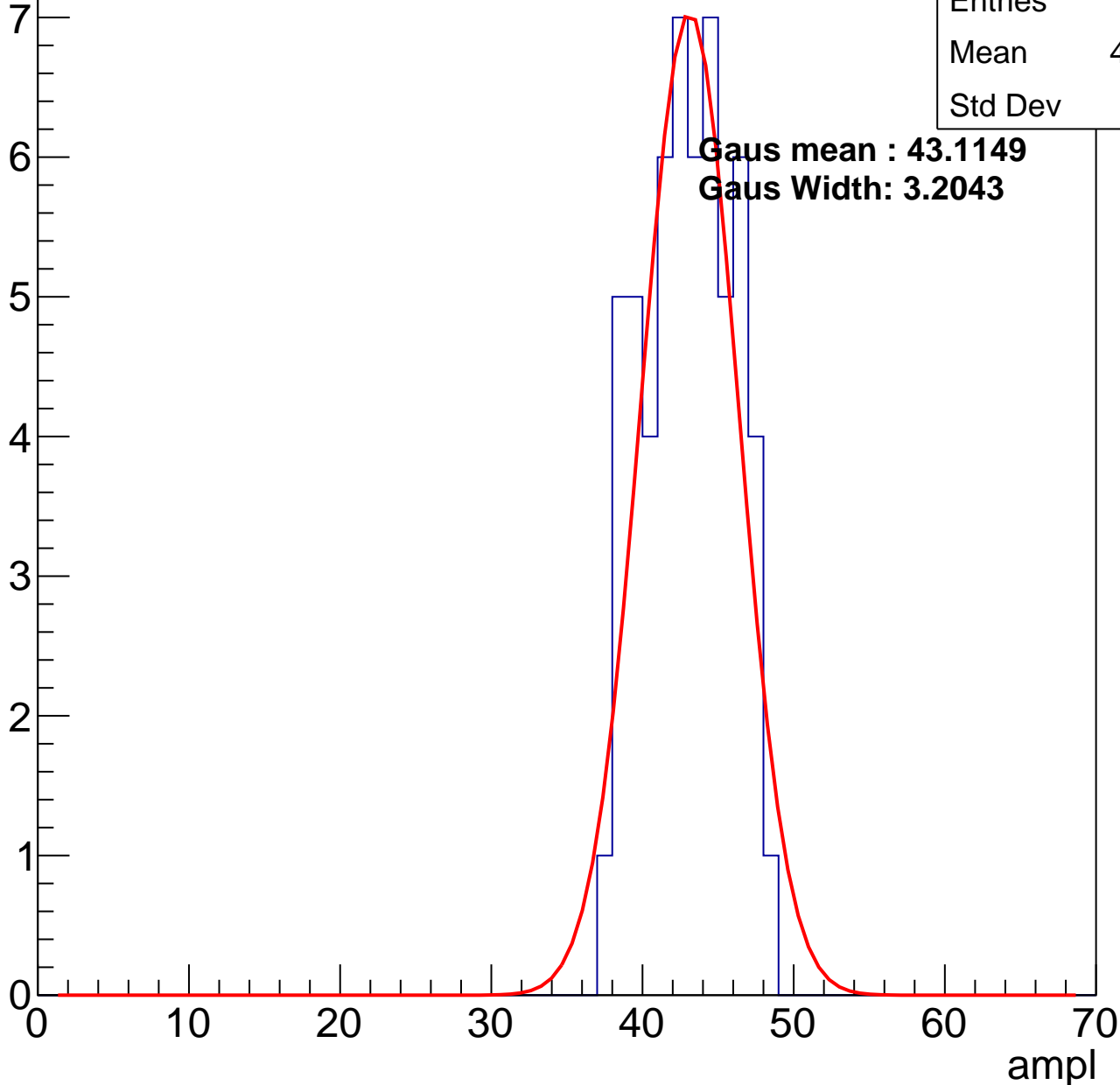
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	42.54
Std Dev	2.86

**Gaus mean : 43.1149**

**Gaus Width: 3.2043**

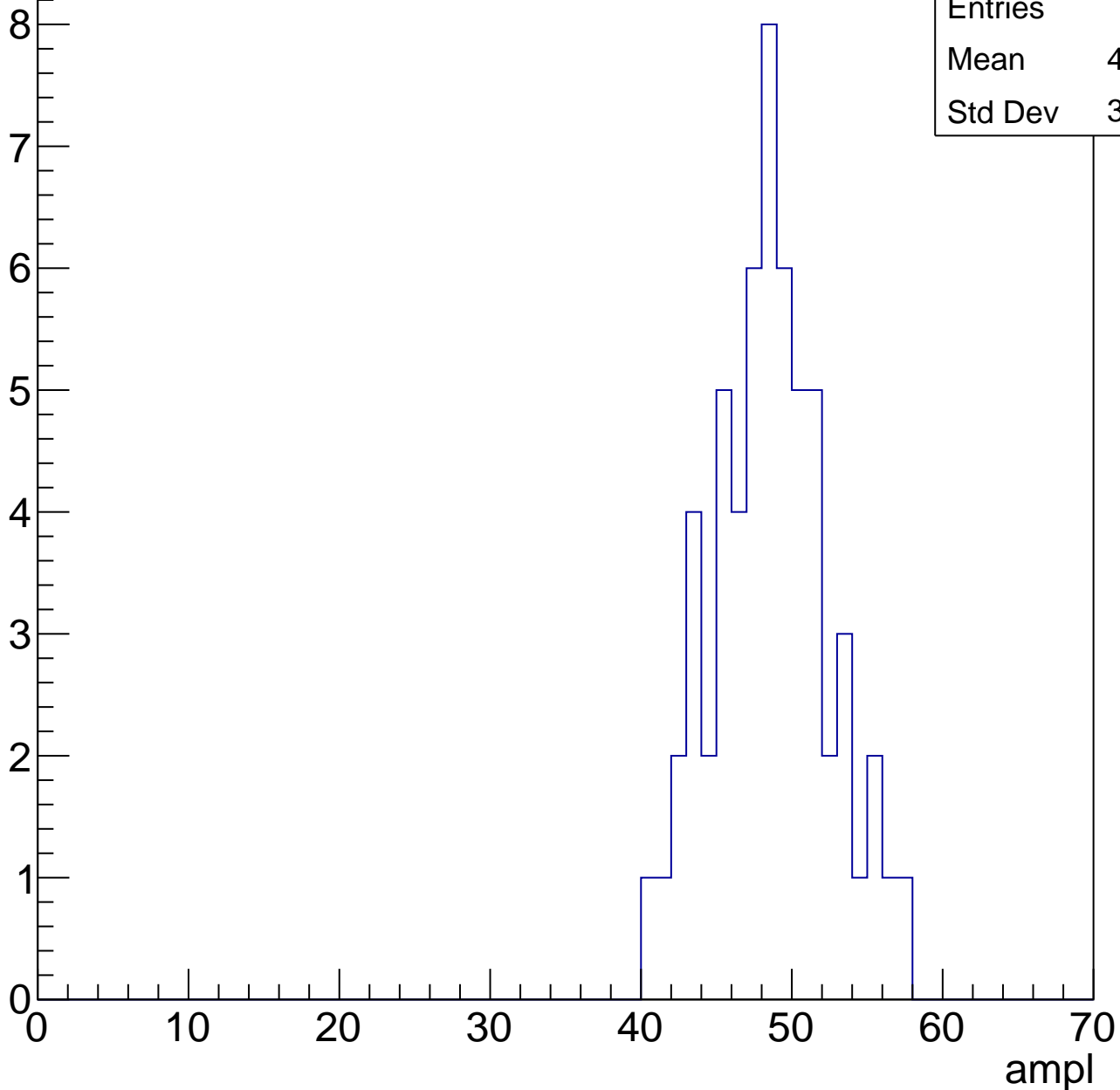


# B1L000S, U2-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	48.12
Std Dev	3.774

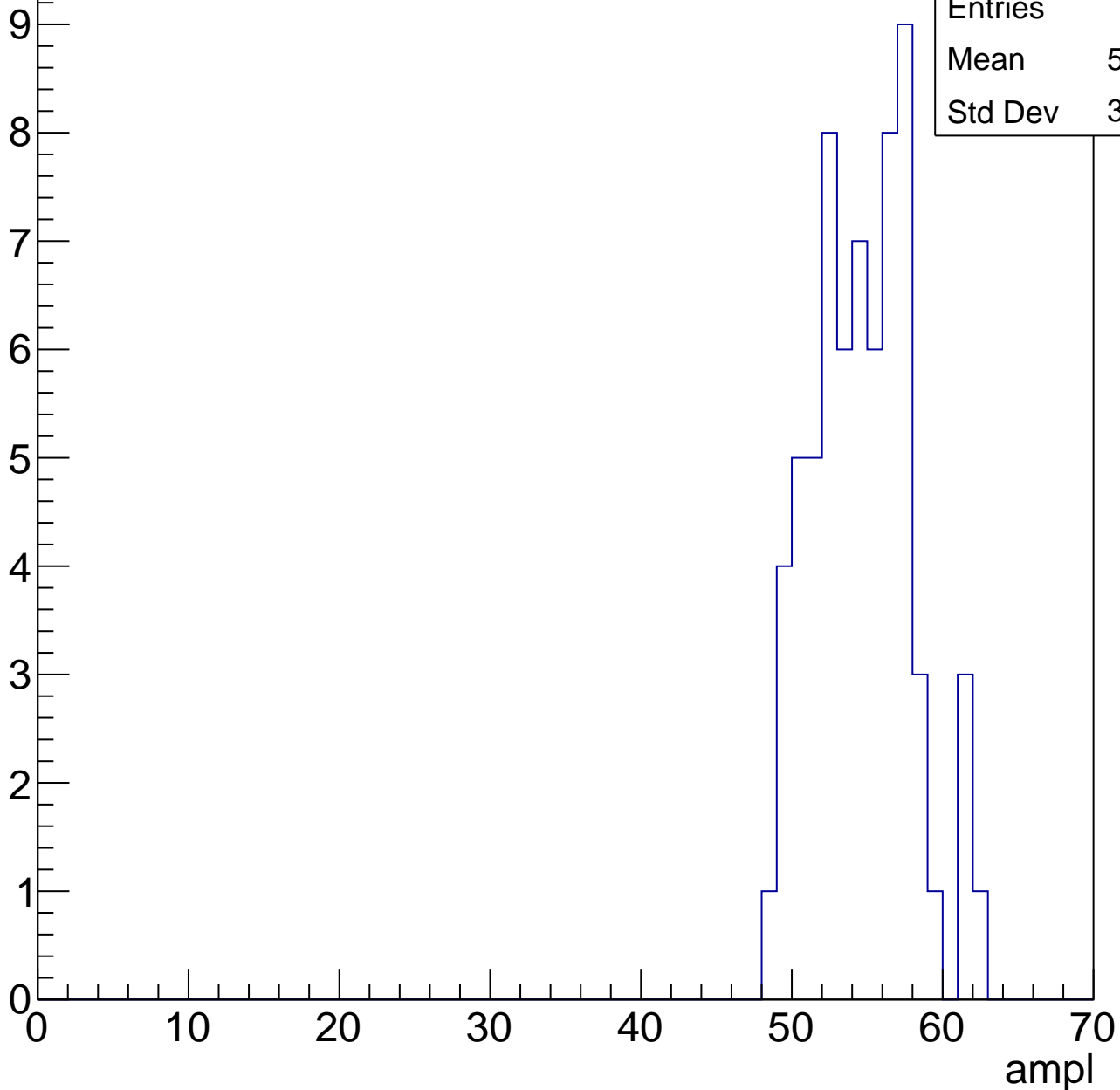


# B1L000S, U2-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

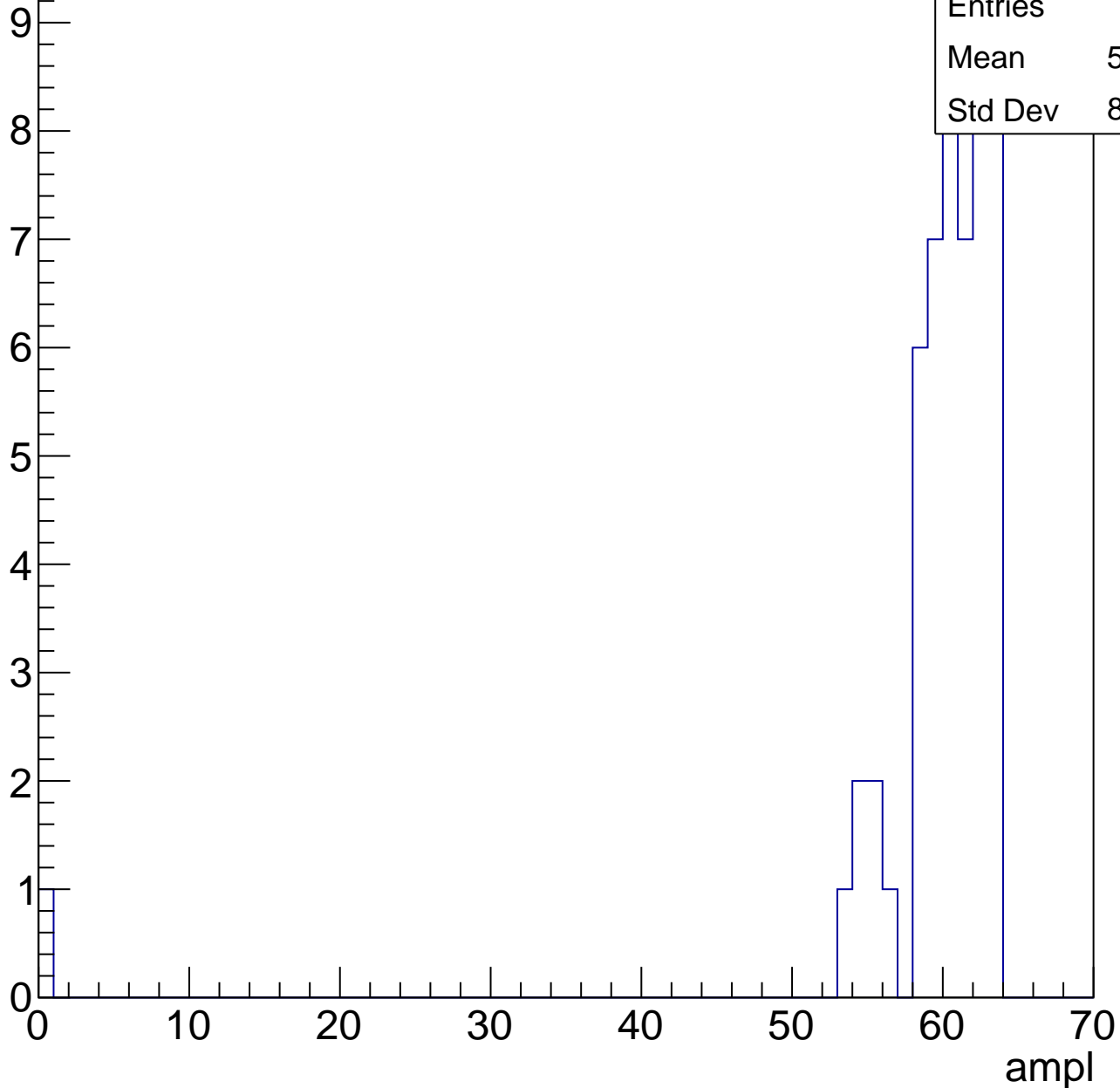
Entries	67
Mean	54.18
Std Dev	3.232



# B1L000S, U2-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

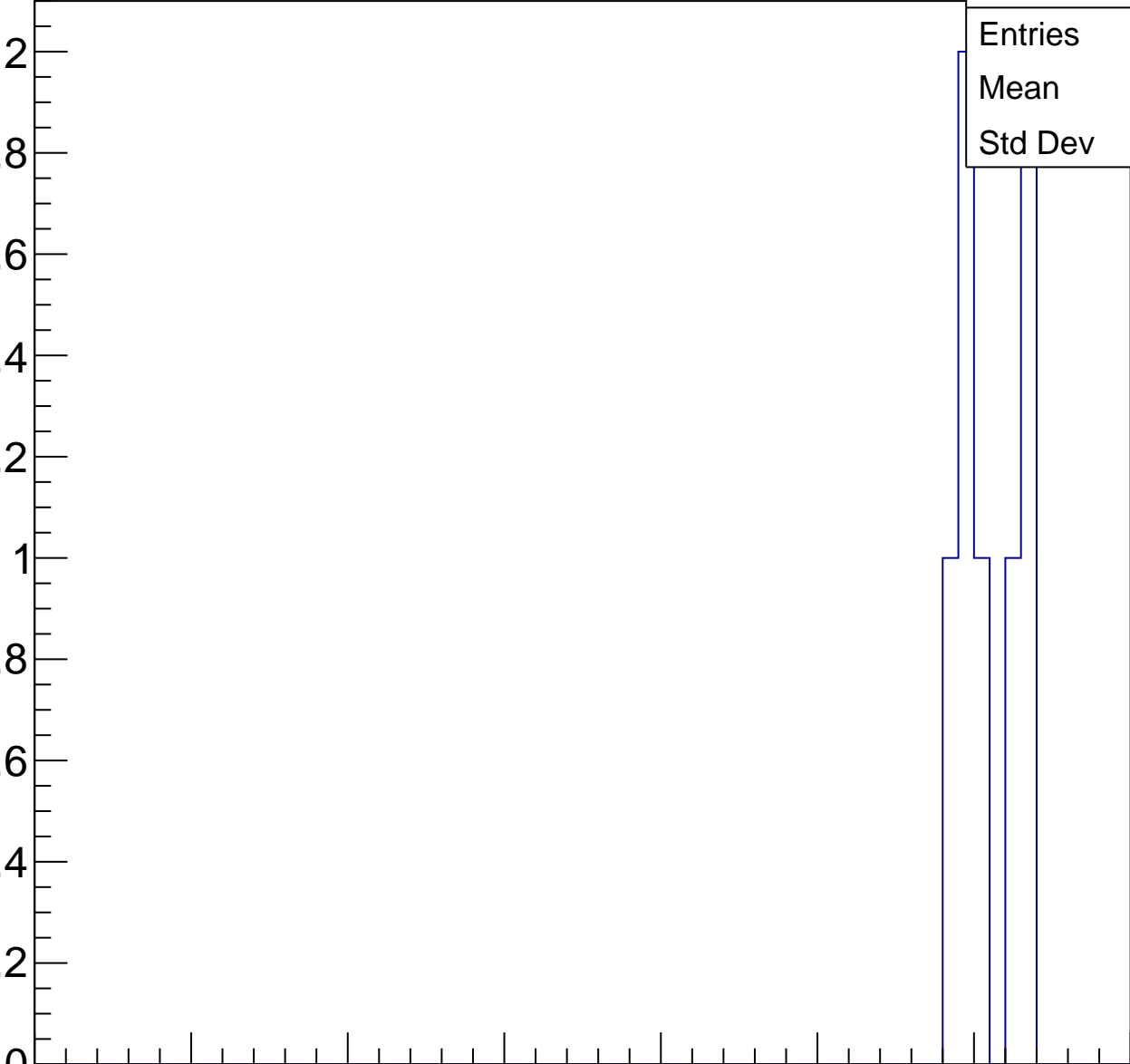
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	60.57
Std Dev	1.917

ampl

0 10 20 30 40 50 60 70





# B1L000S, U2-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U2-ch122, adc0

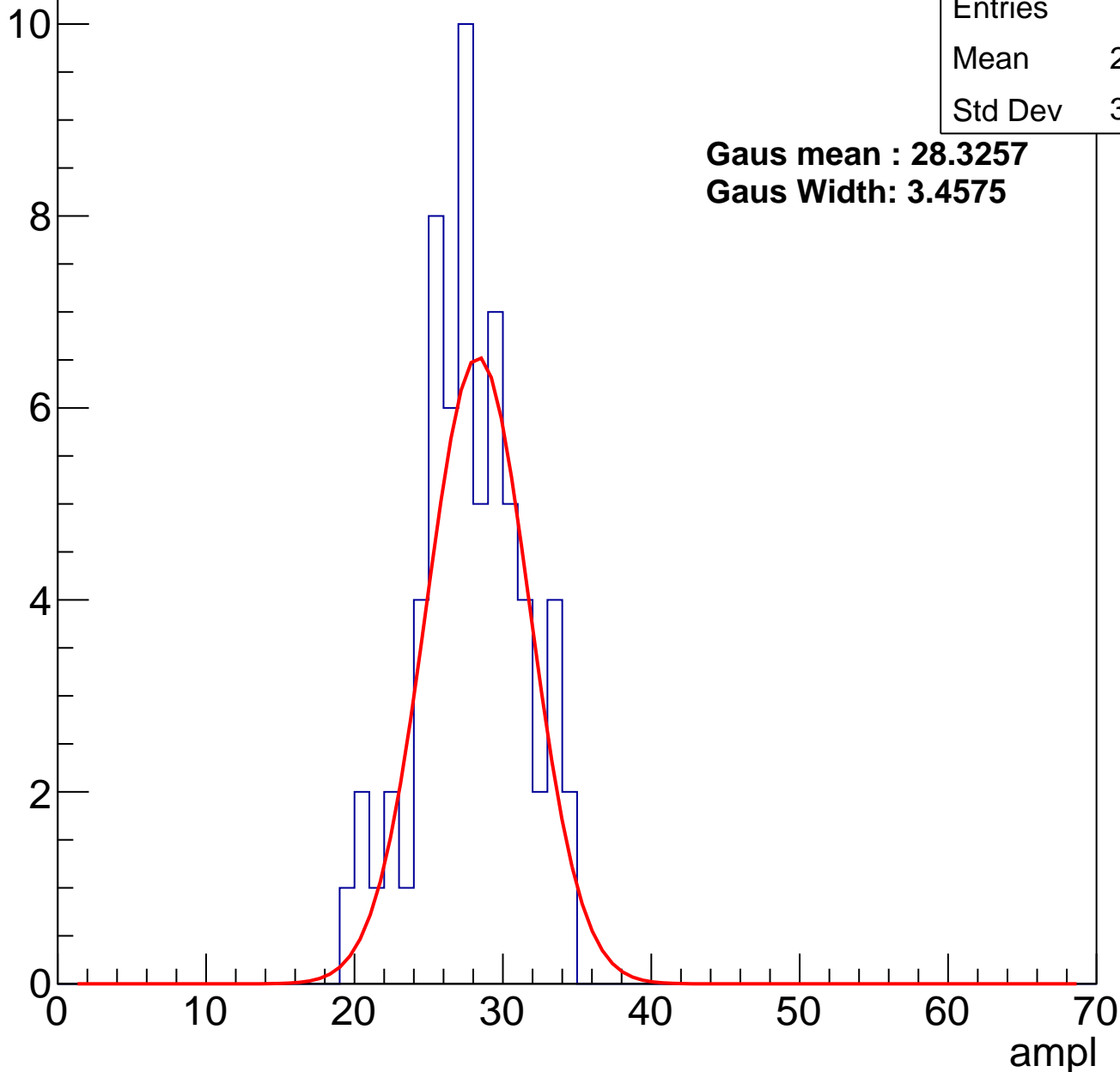
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	27.34
Std Dev	3.456

**Gaus mean : 28.3257**

**Gaus Width: 3.4575**

Entry



# B1L000S, U2-ch122, adc1

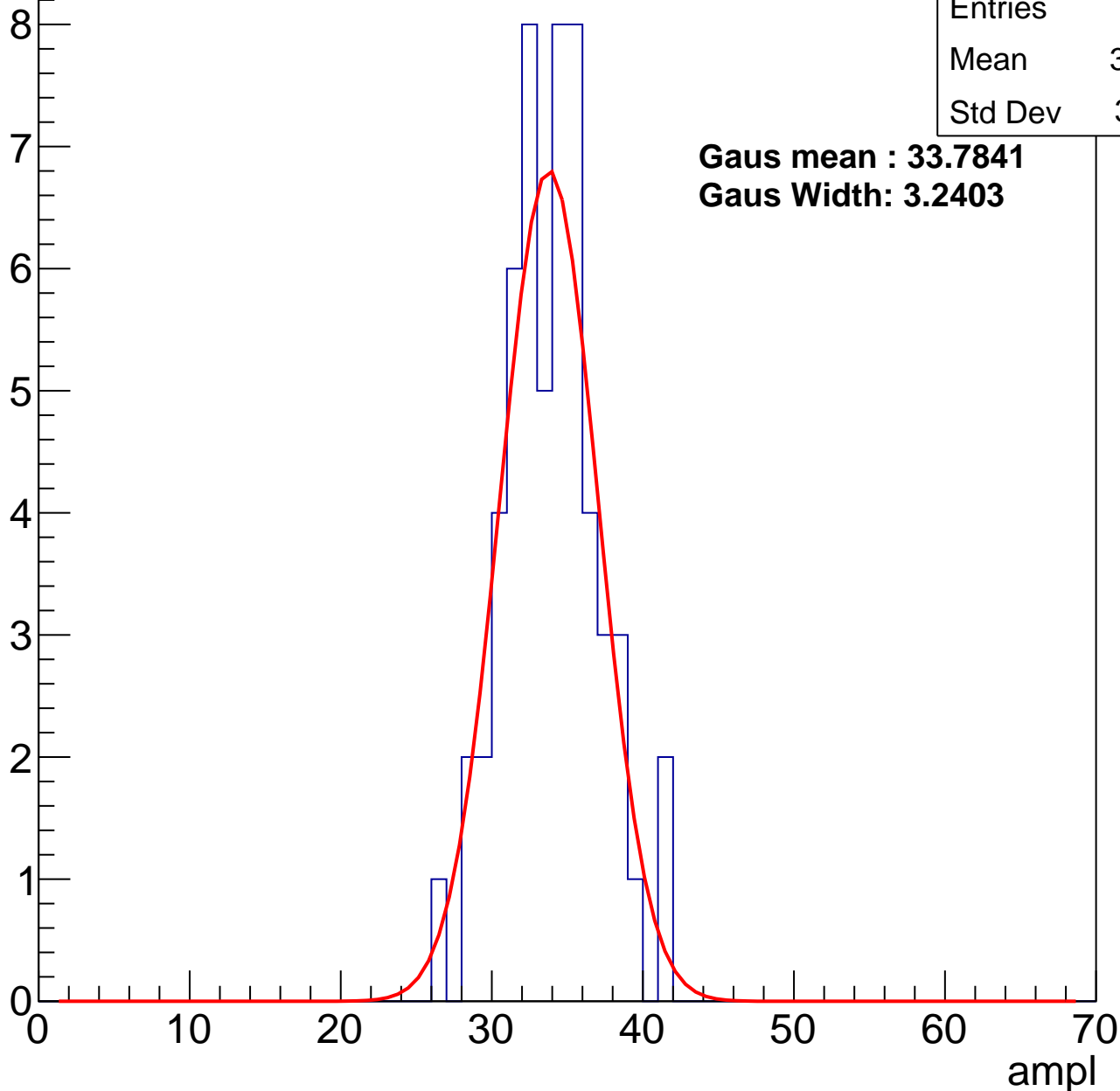
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	33.49
Std Dev	3.101

**Gaus mean : 33.7841**

**Gaus Width: 3.2403**



# B1L000S, U2-ch122, adc2

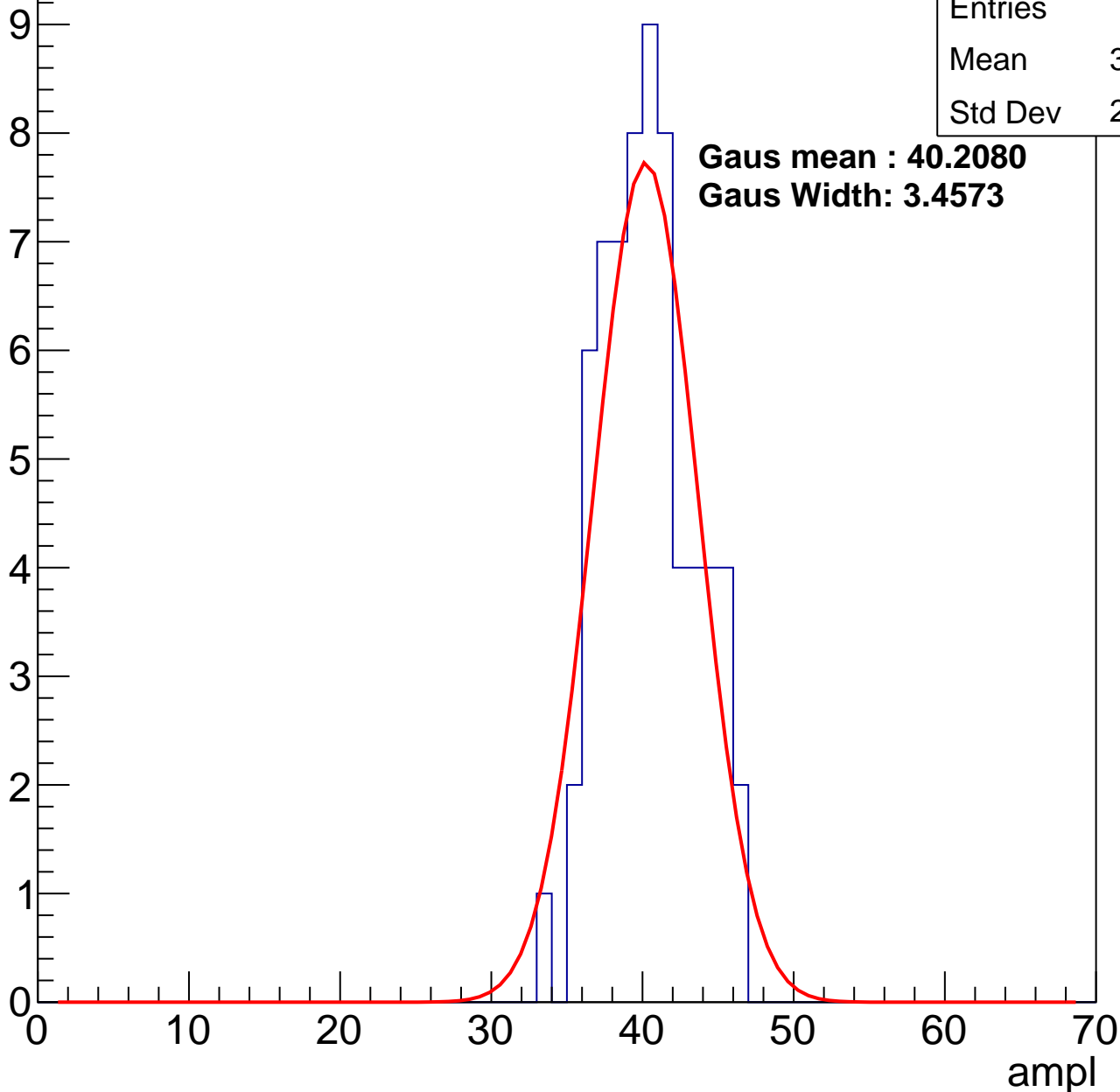
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	39.88
Std Dev	2.987

**Gaus mean : 40.2080**

**Gaus Width: 3.4573**

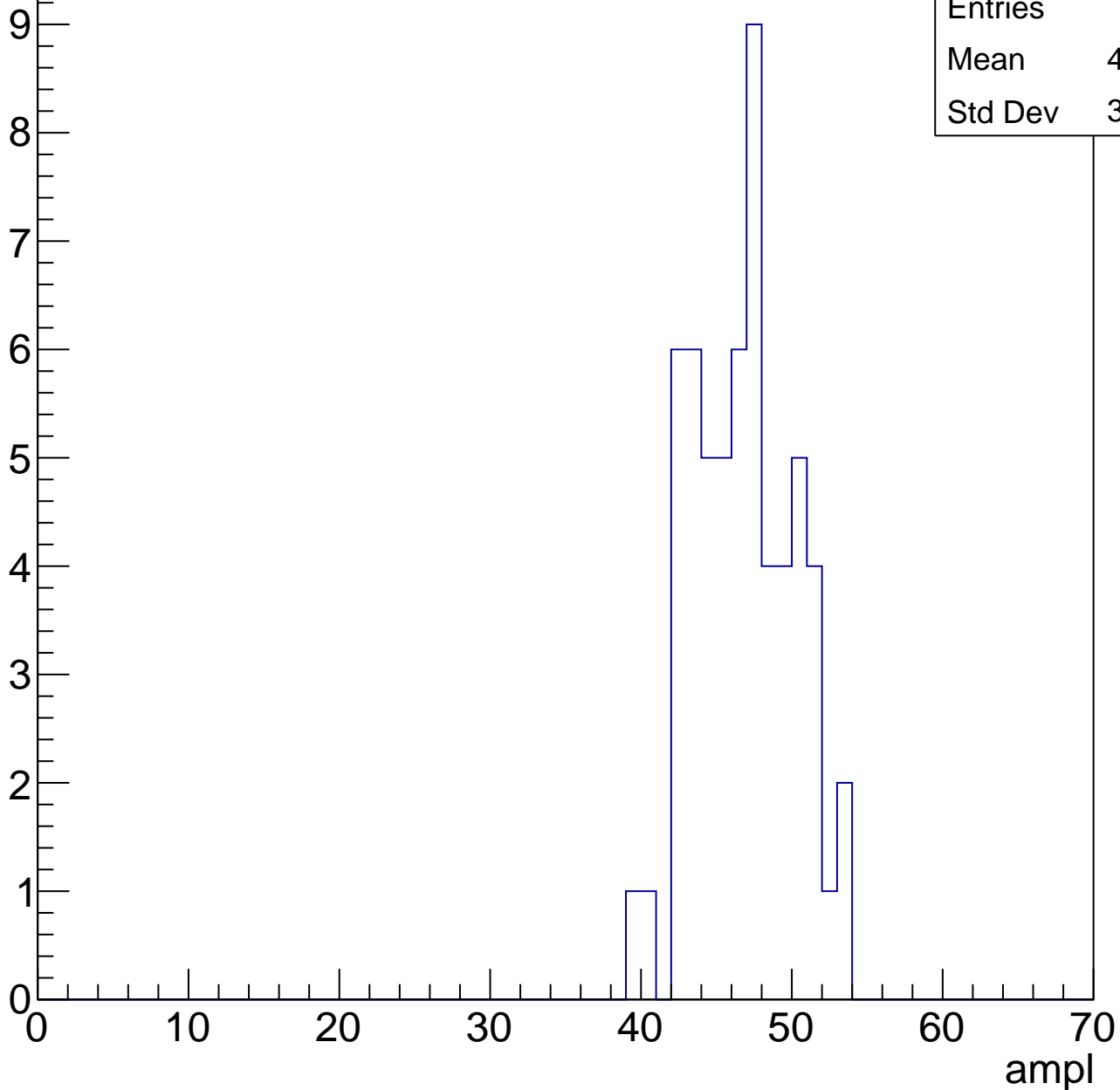


# B1L000S, U2-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	46.32
Std Dev	3.265

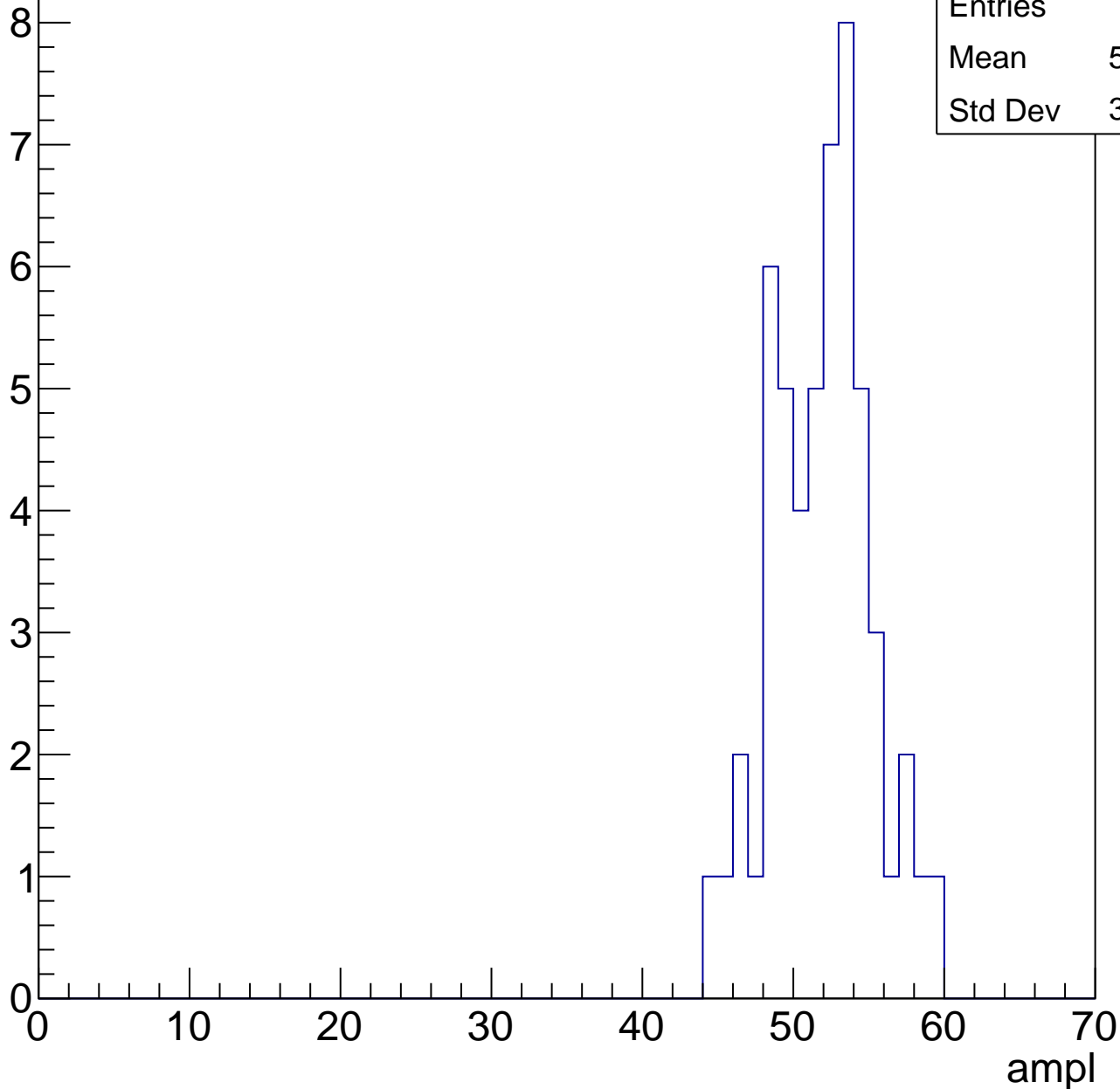


# B1L000S, U2-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

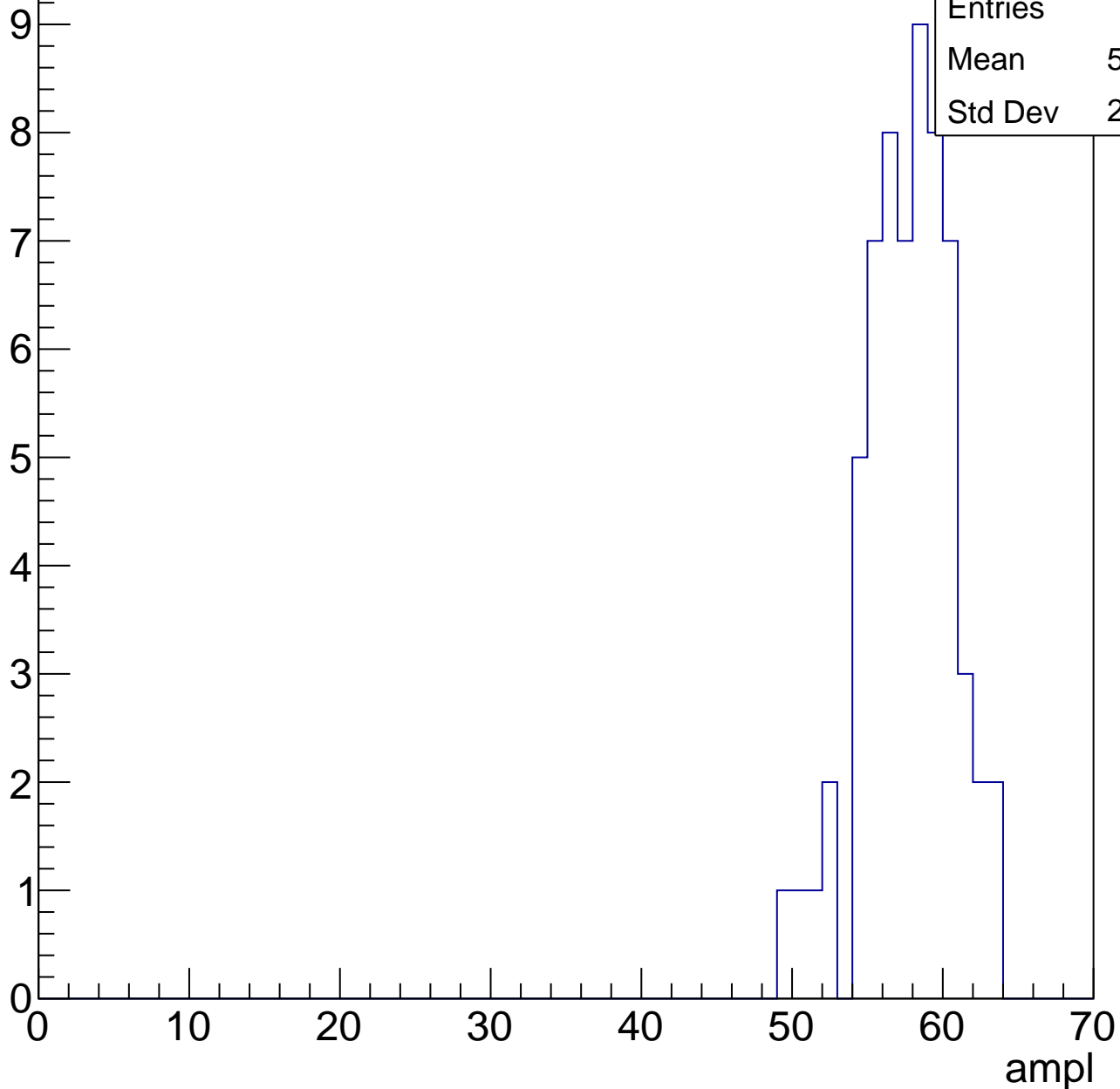
Entries	53
Mean	51.43
Std Dev	3.253



# B1L000S, U2-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



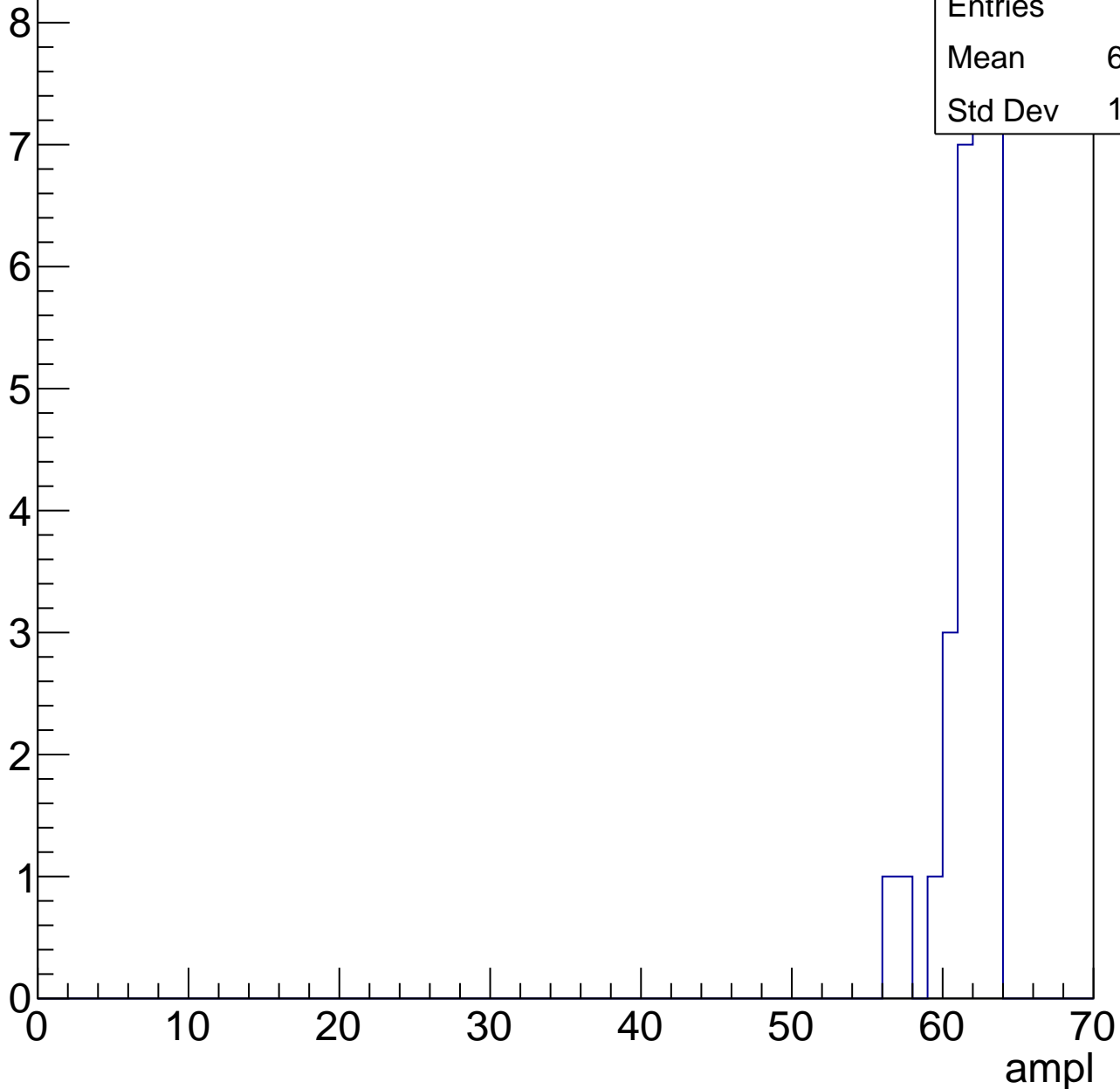
Entries	63
Mean	57.19
Std Dev	2.959

# B1L000S, U2-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	61.34
Std Dev	1.708





# B1L000S, U2-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U2-ch123, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	29.62
Std Dev	3.221

**Gaus mean : 29.8390**

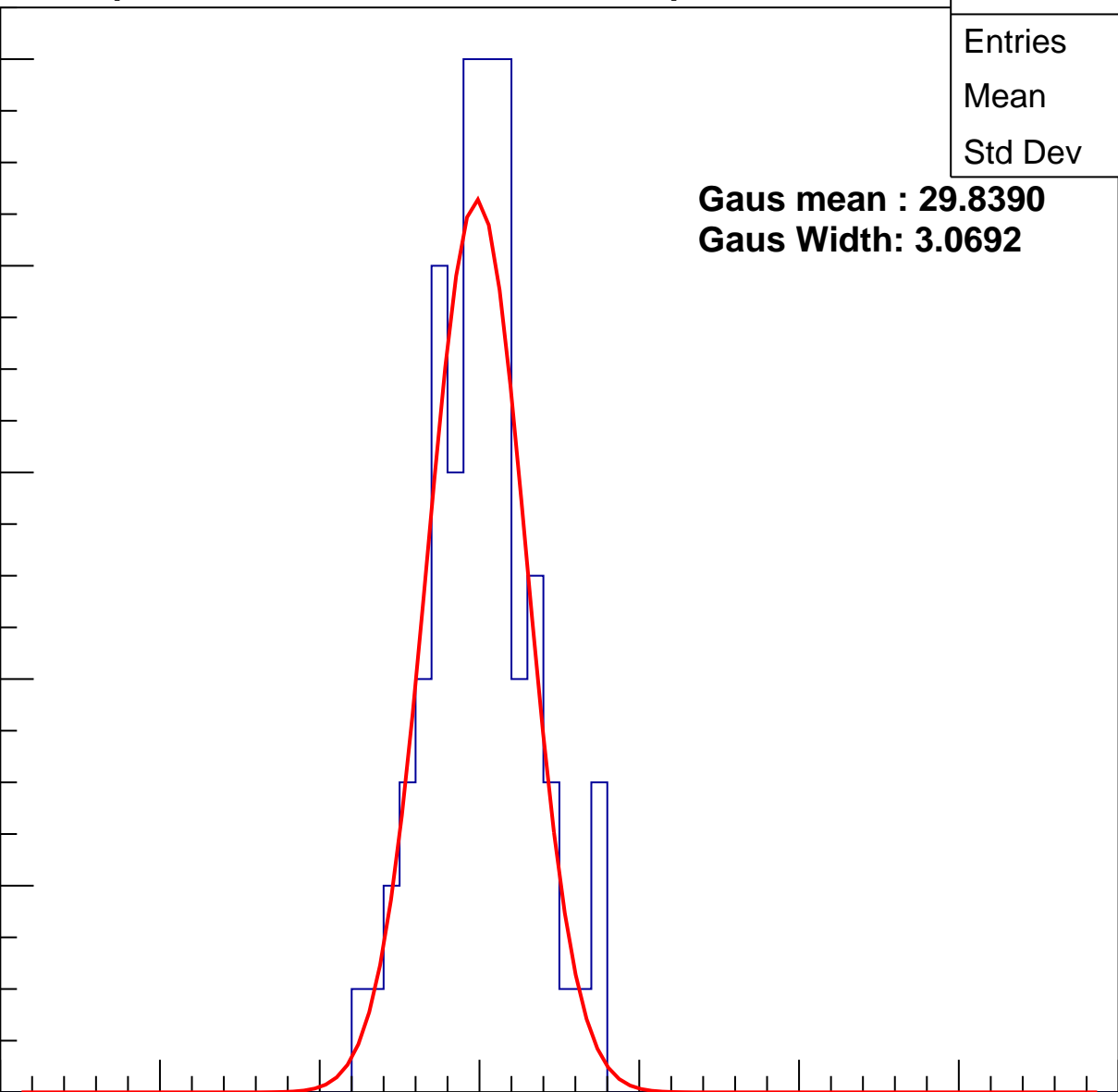
**Gaus Width: 3.0692**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U2-ch123, adc1

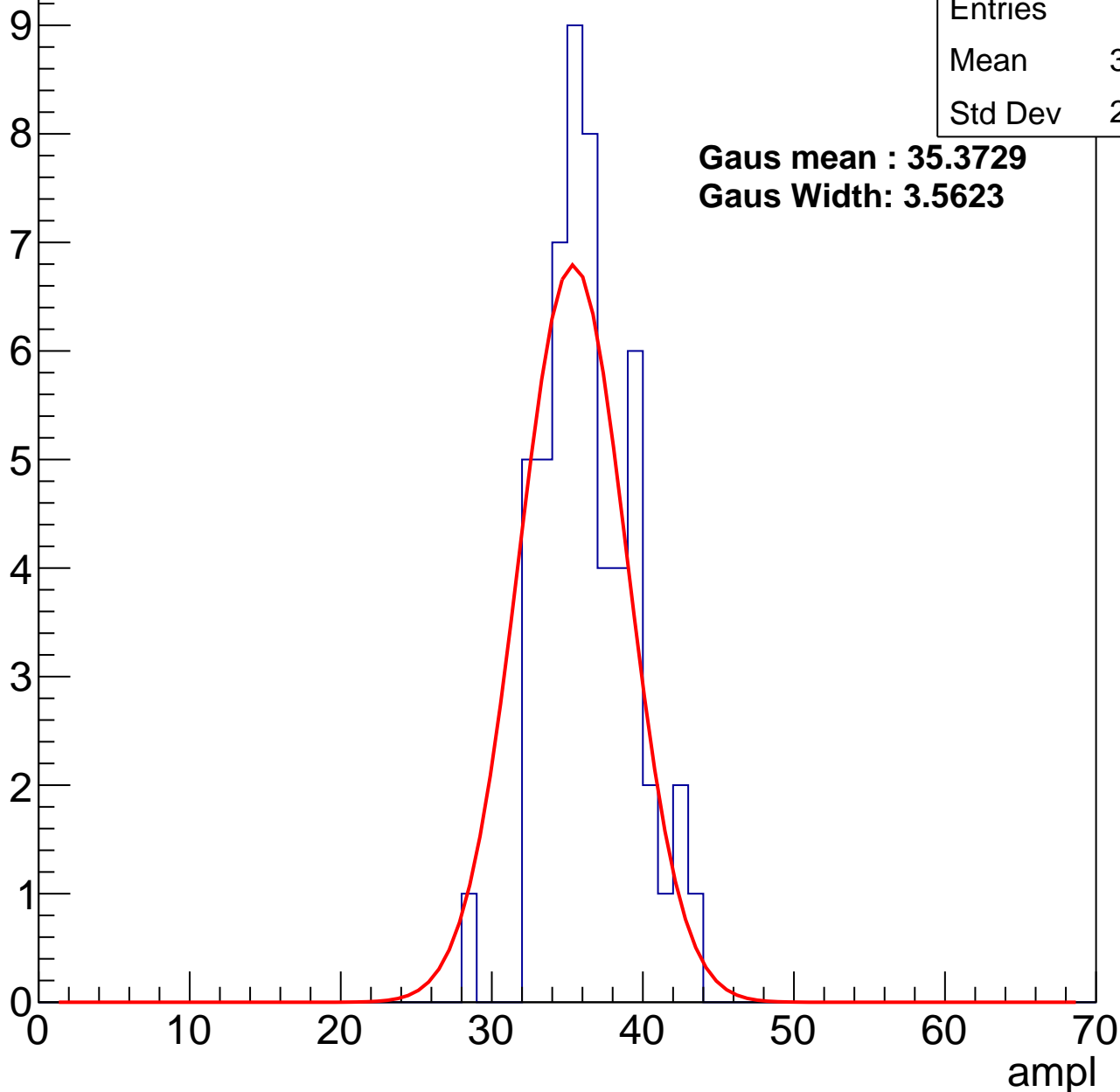
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	35.93
Std Dev	2.947

**Gaus mean : 35.3729**

**Gaus Width: 3.5623**



# B1L000S, U2-ch123, adc2

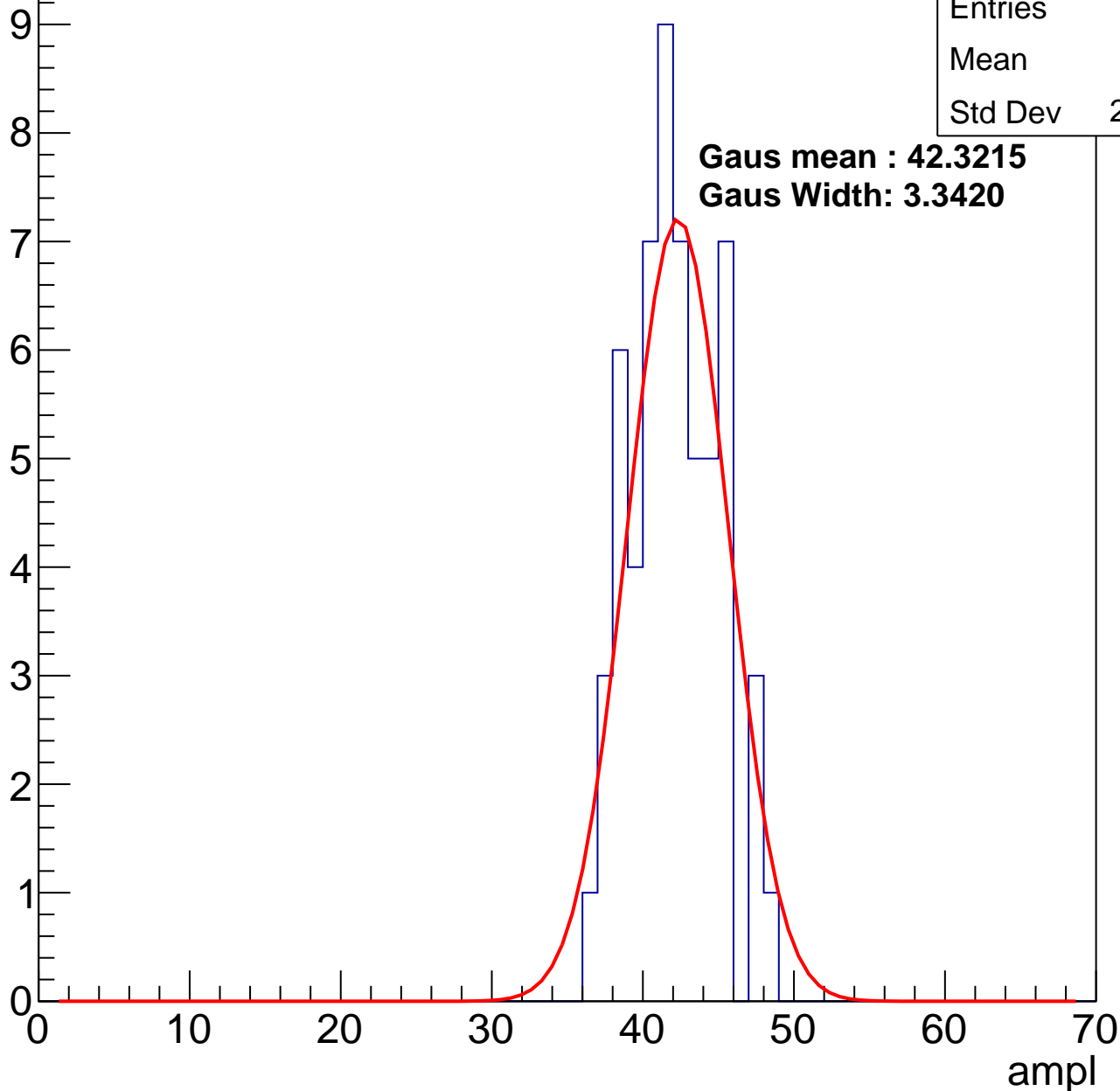
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.6
Std Dev	2.846

**Gaus mean : 42.3215**

**Gaus Width: 3.3420**

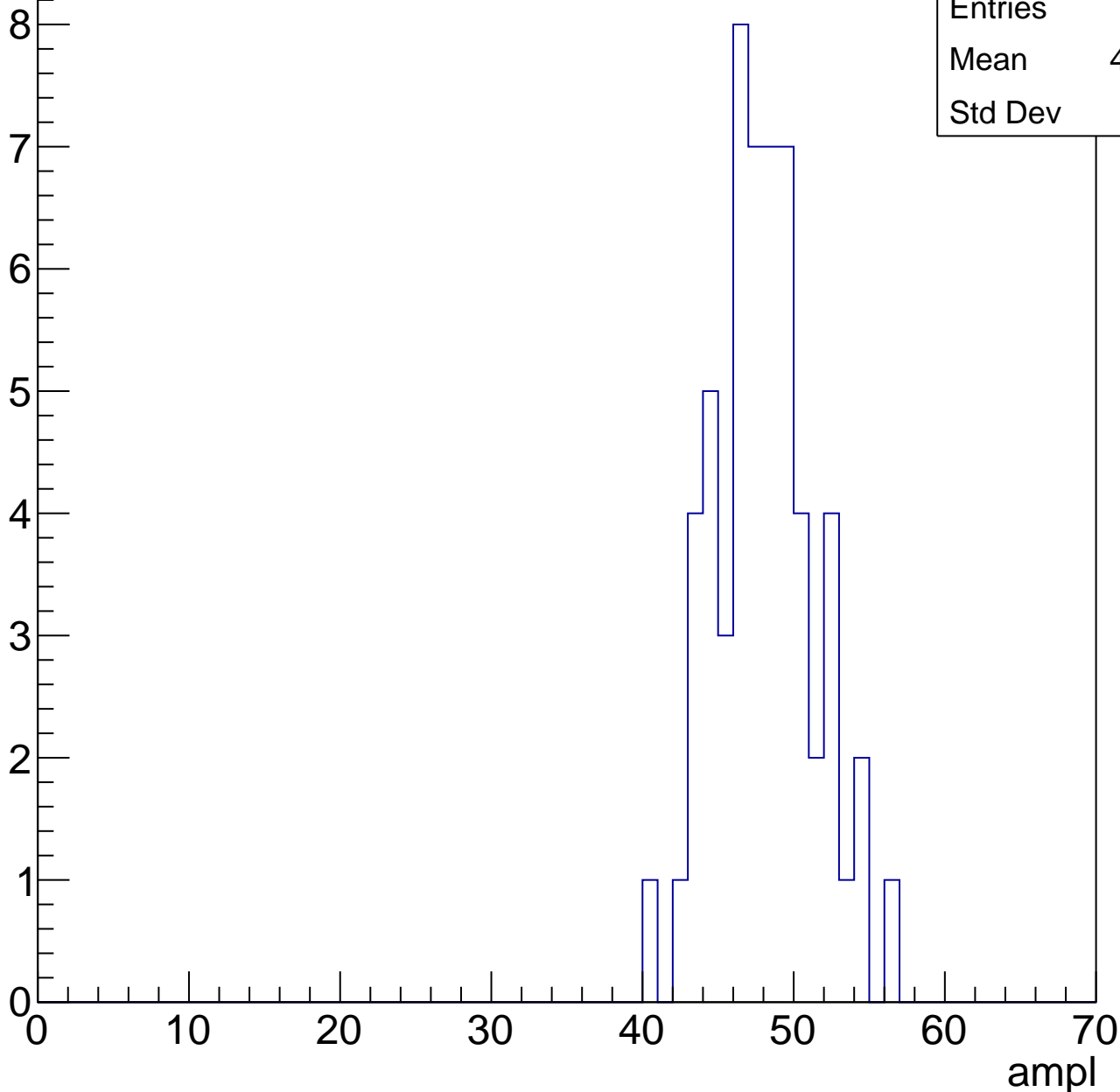


# B1L000S, U2-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	47.58
Std Dev	3.25

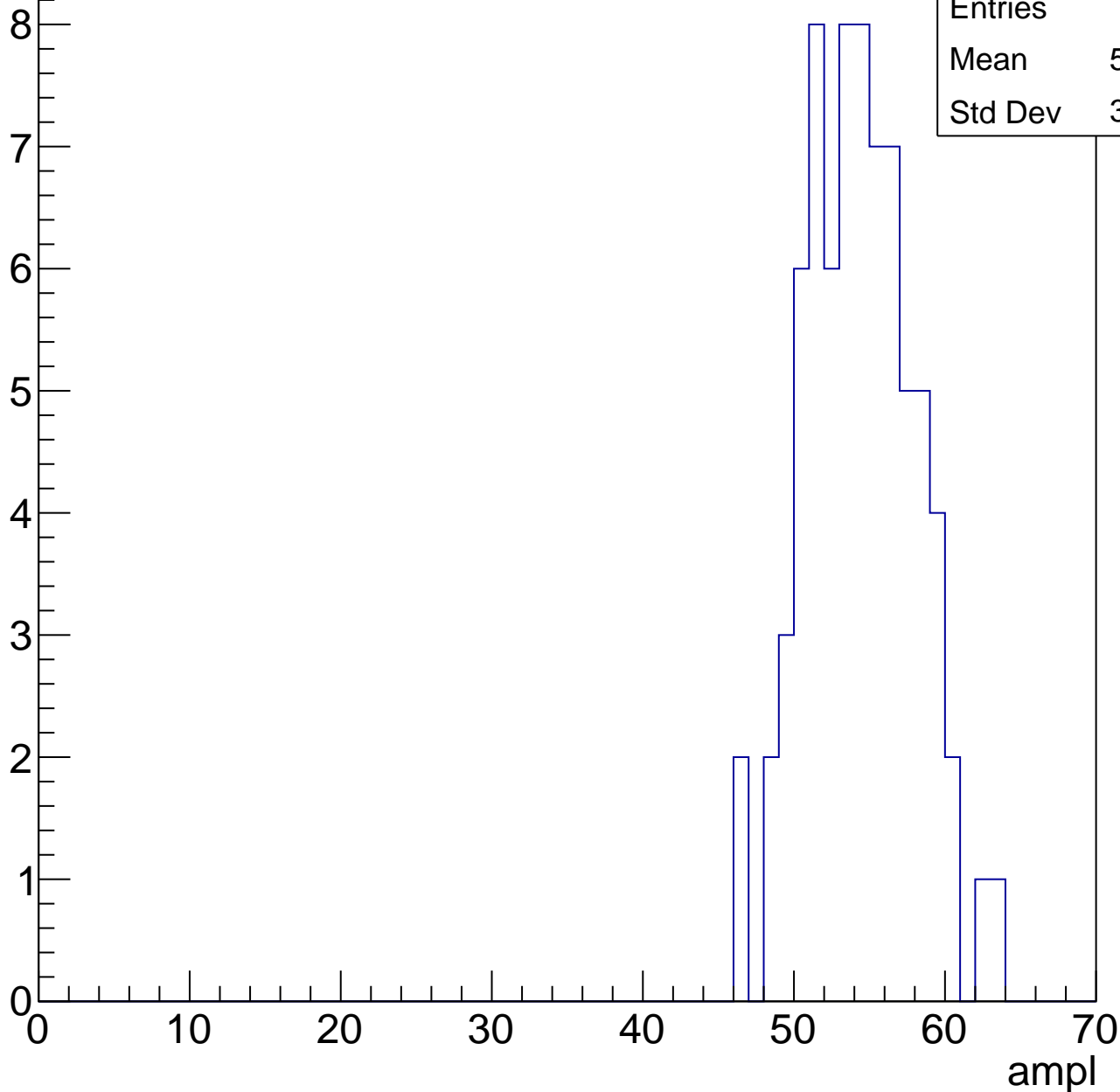


# B1L000S, U2-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	53.92
Std Dev	3.562

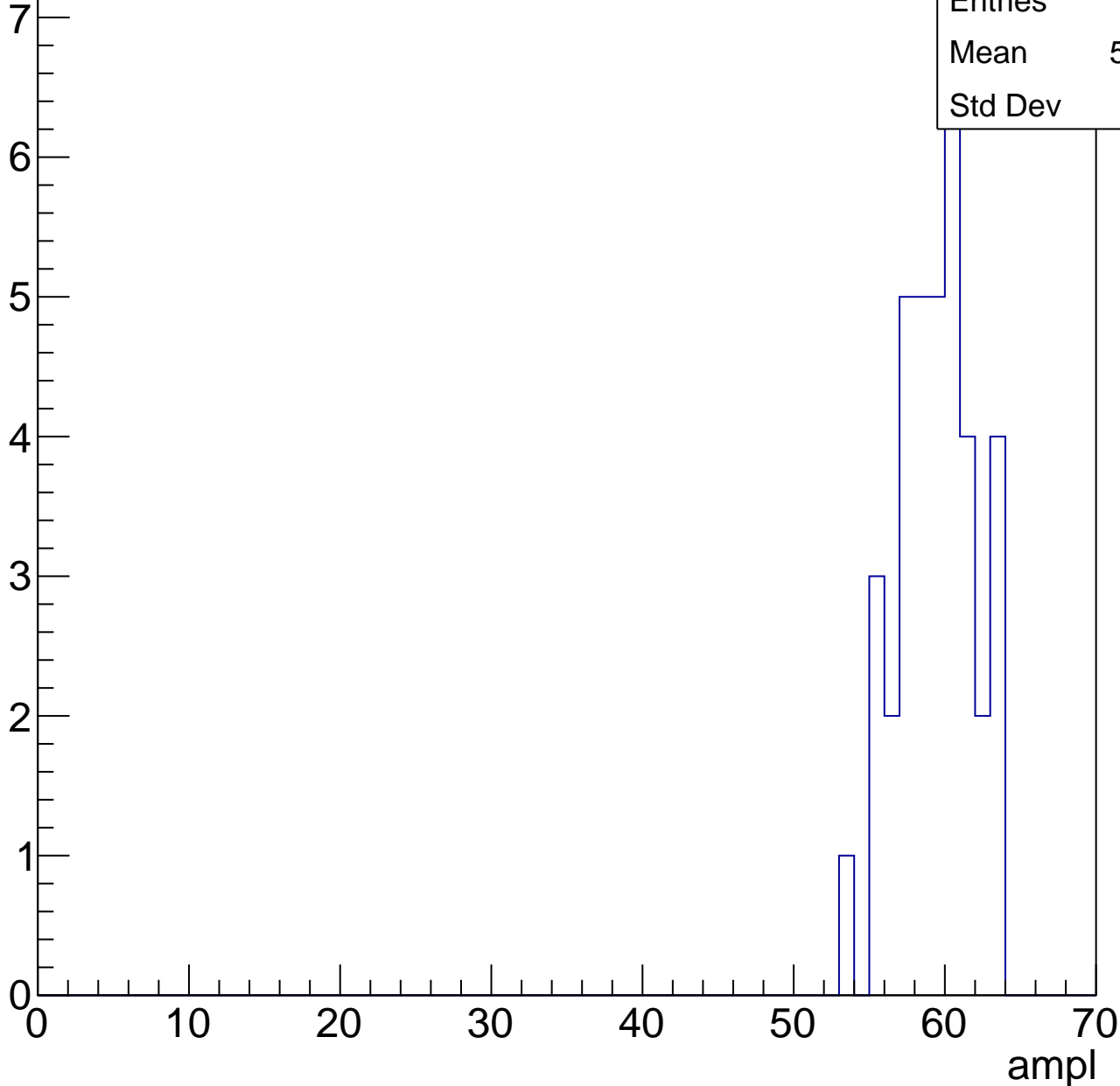


# B1L000S, U2-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	38
Mean	58.95
Std Dev	2.47

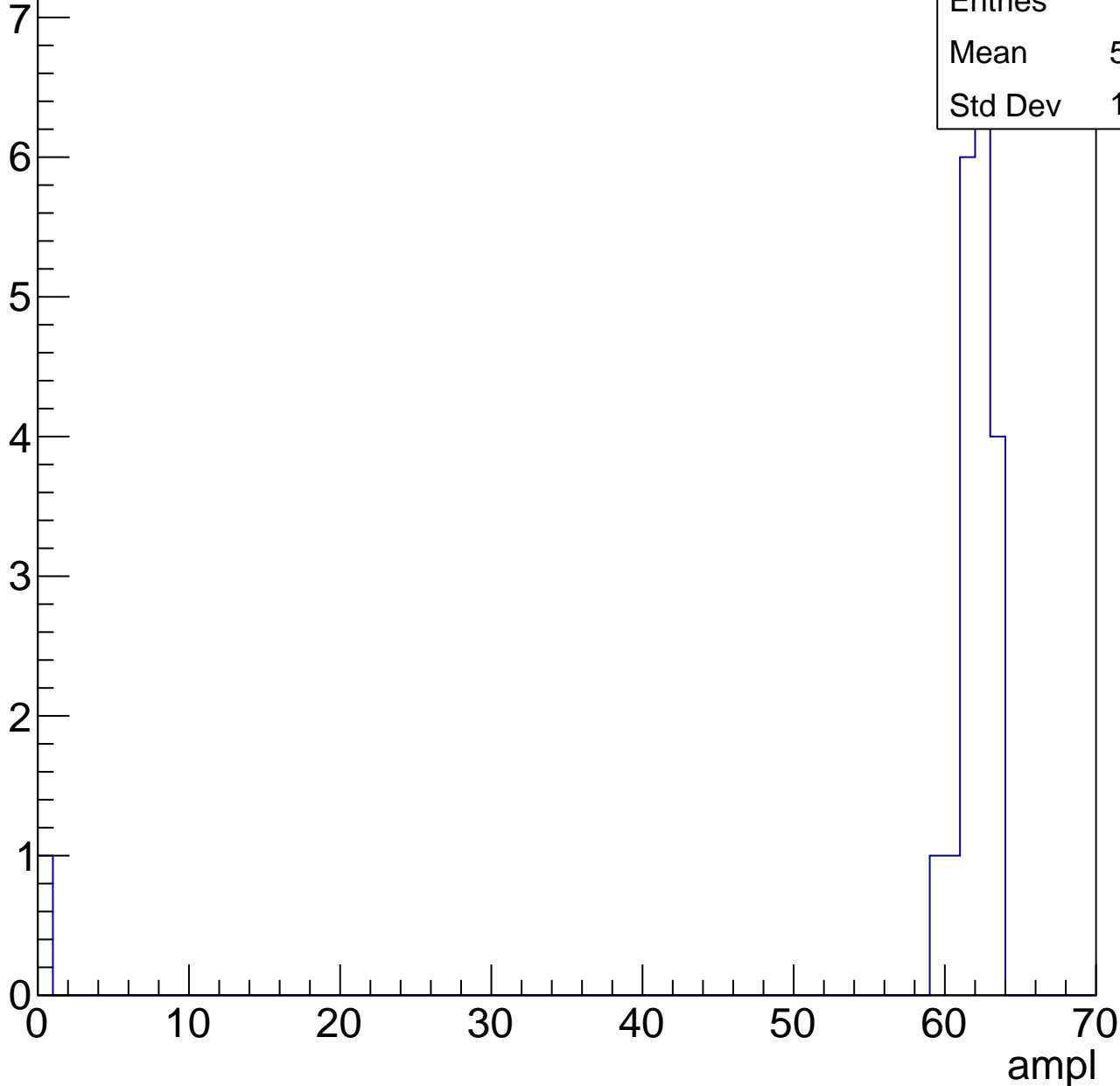


# B1L000S, U2-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.55
Std Dev	13.47





# B1L000S, U2-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch124, adc0

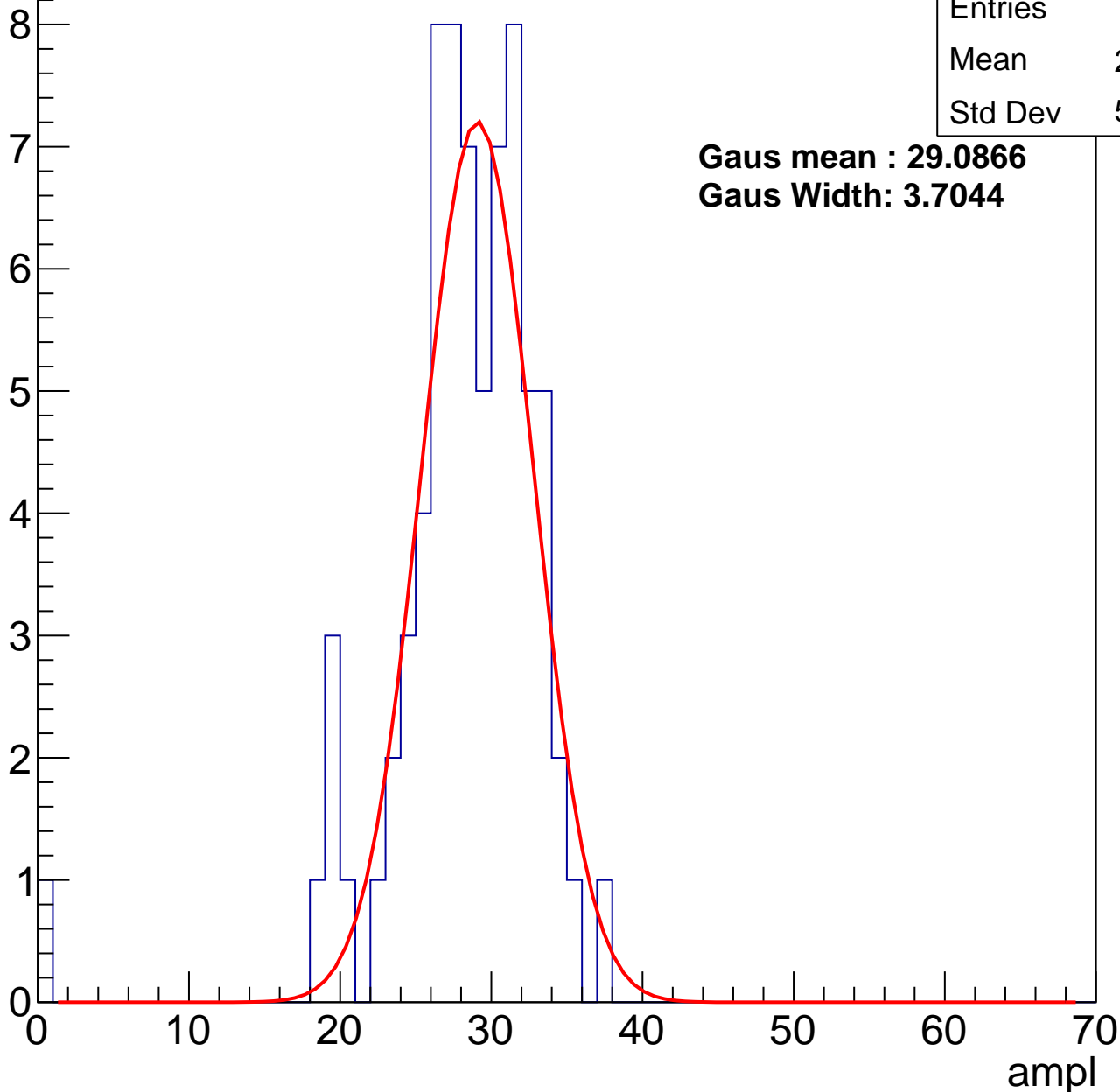
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	27.71
Std Dev	5.111

**Gaus mean : 29.0866**

**Gaus Width: 3.7044**



# B1L000S, U2-ch124, adc1

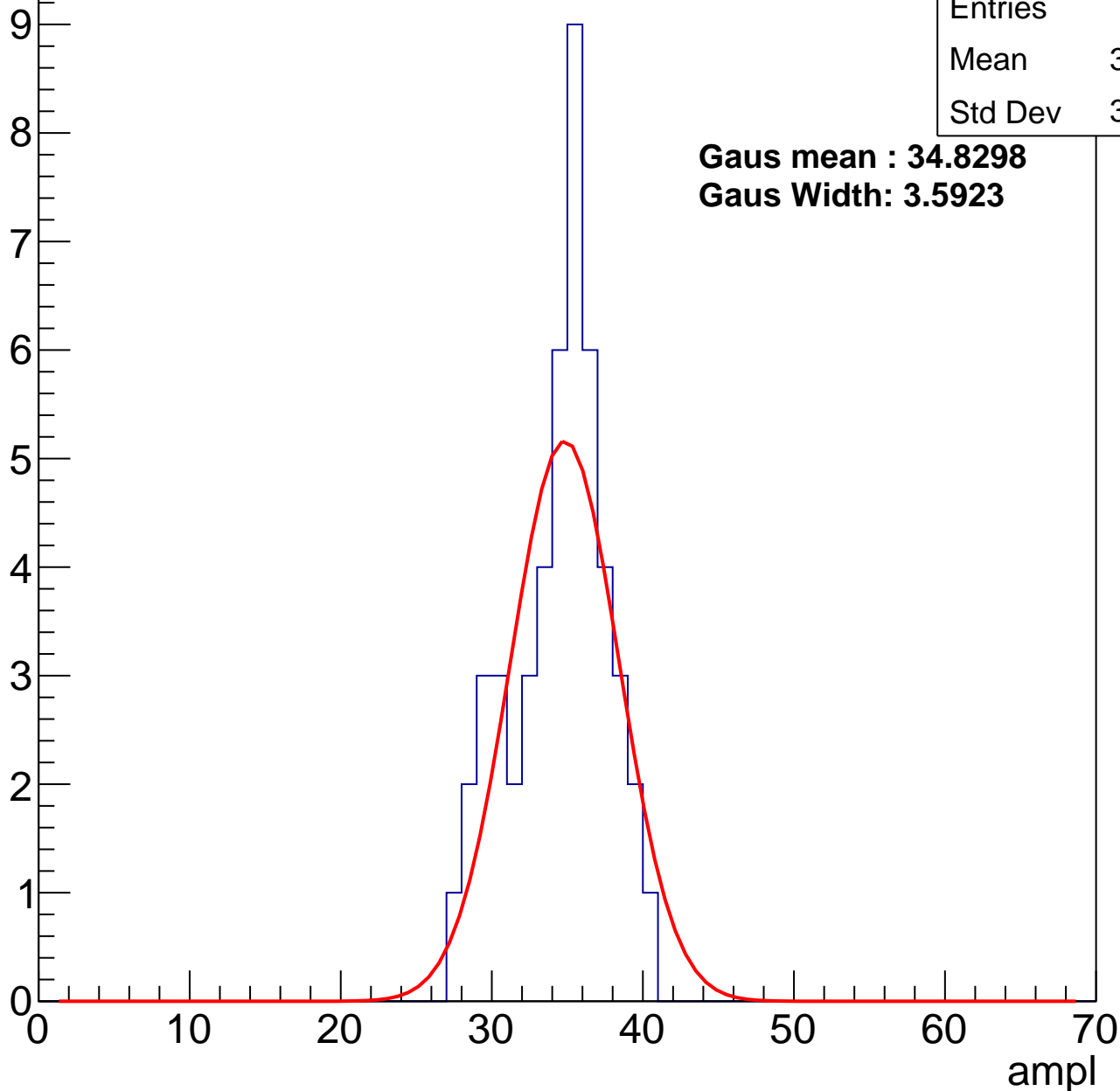
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	33.98
Std Dev	3.133

**Gaus mean : 34.8298**

**Gaus Width: 3.5923**



# B1L000S, U2-ch124, adc2

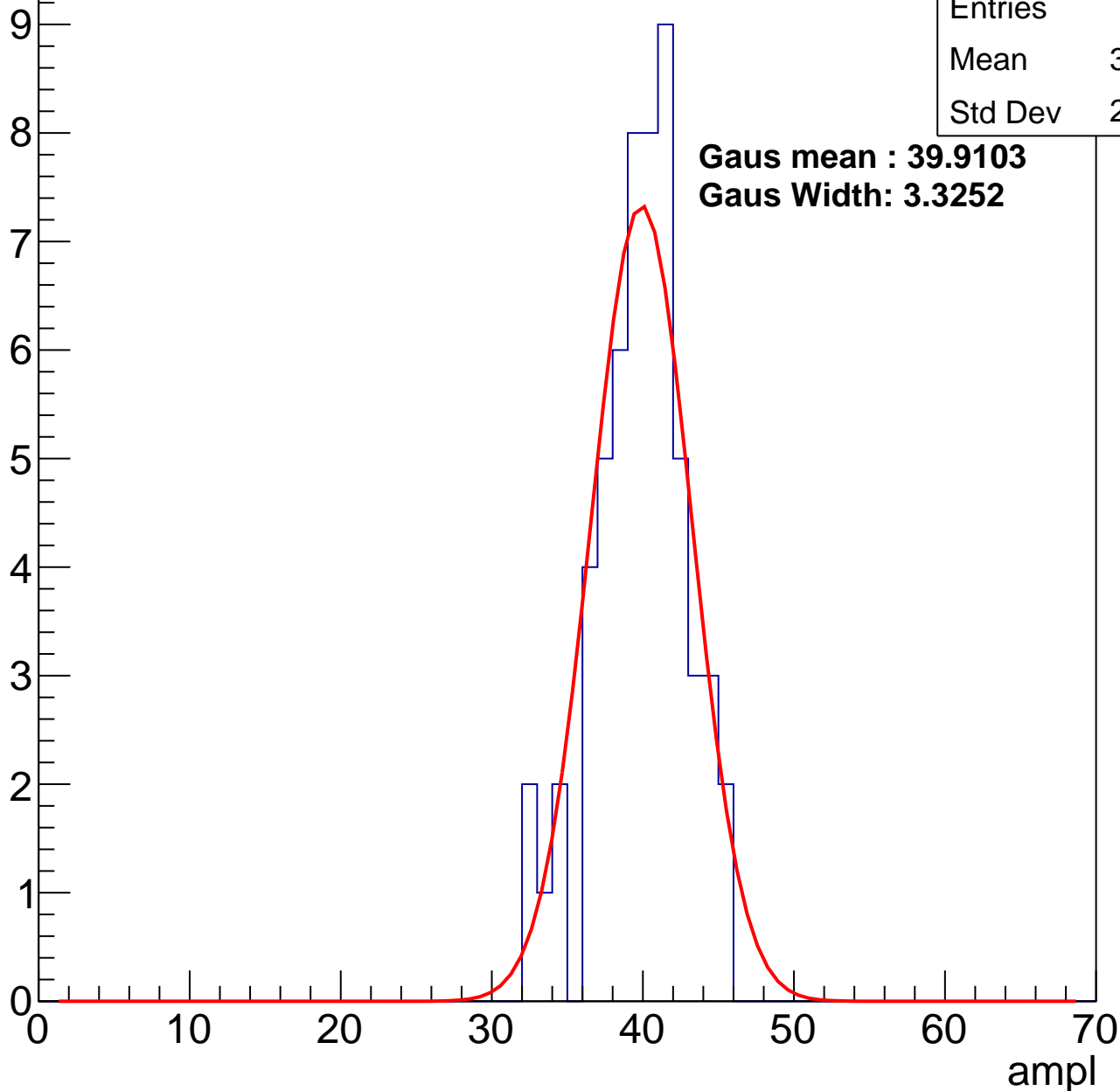
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	39.38
Std Dev	2.999

**Gaus mean : 39.9103**

**Gaus Width: 3.3252**

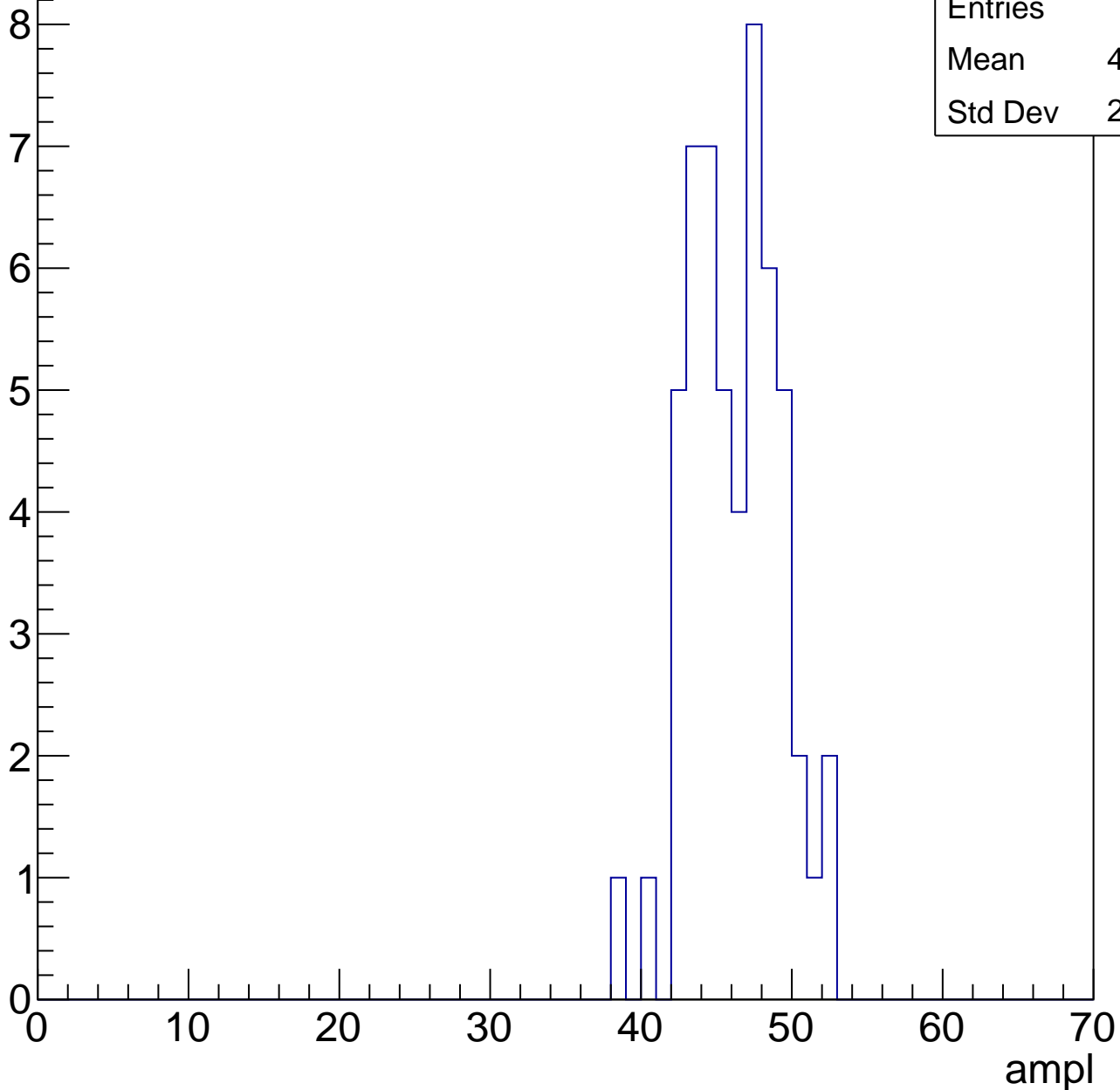


# B1L000S, U2-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	45.74
Std Dev	2.976

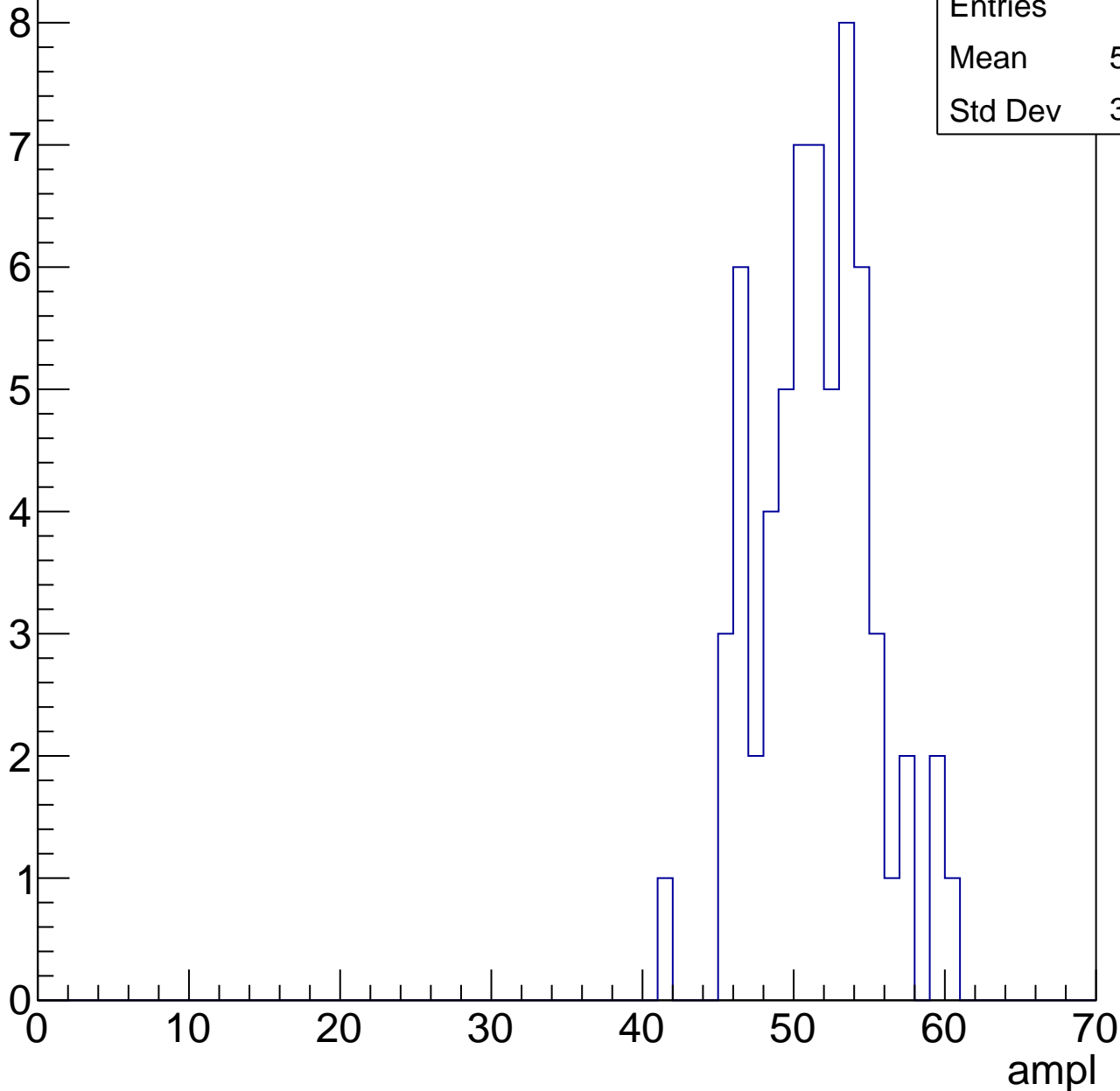


# B1L000S, U2-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

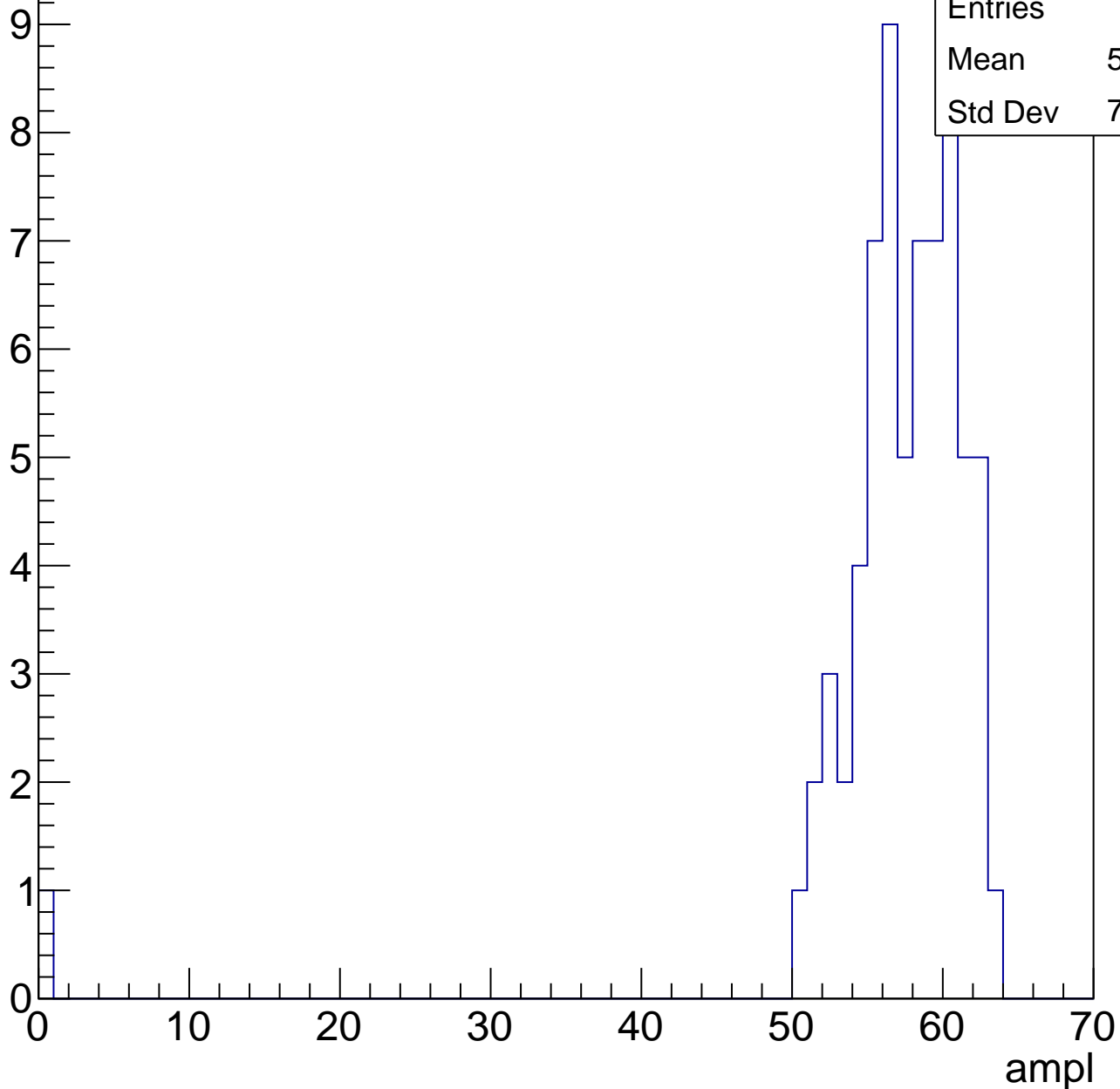
Entries	63
Mean	50.97
Std Dev	3.775



# B1L000S, U2-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

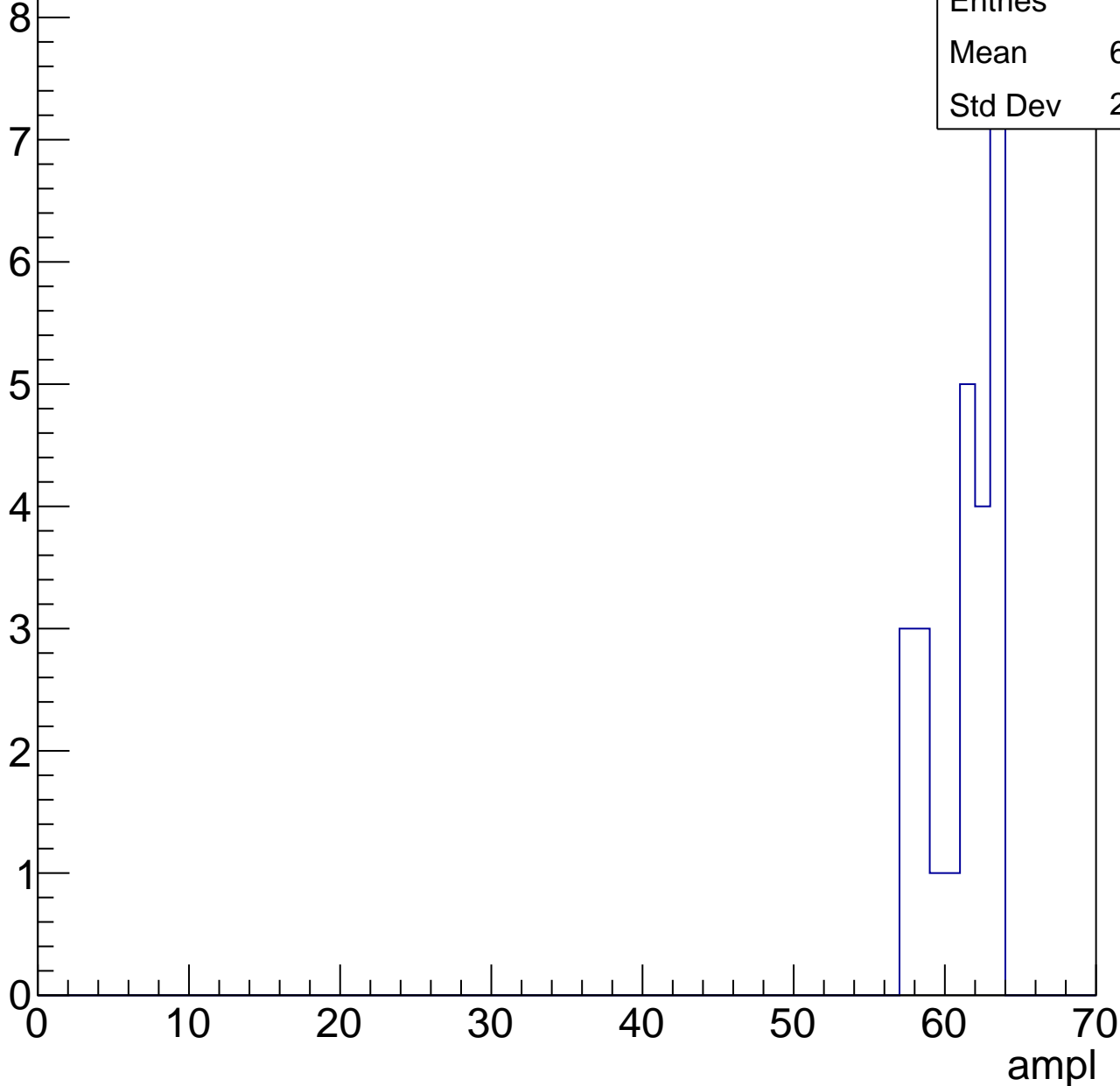


# B1L000S, U2-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	60.84
Std Dev	2.148





# B1L000S, U2-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch125, adc0

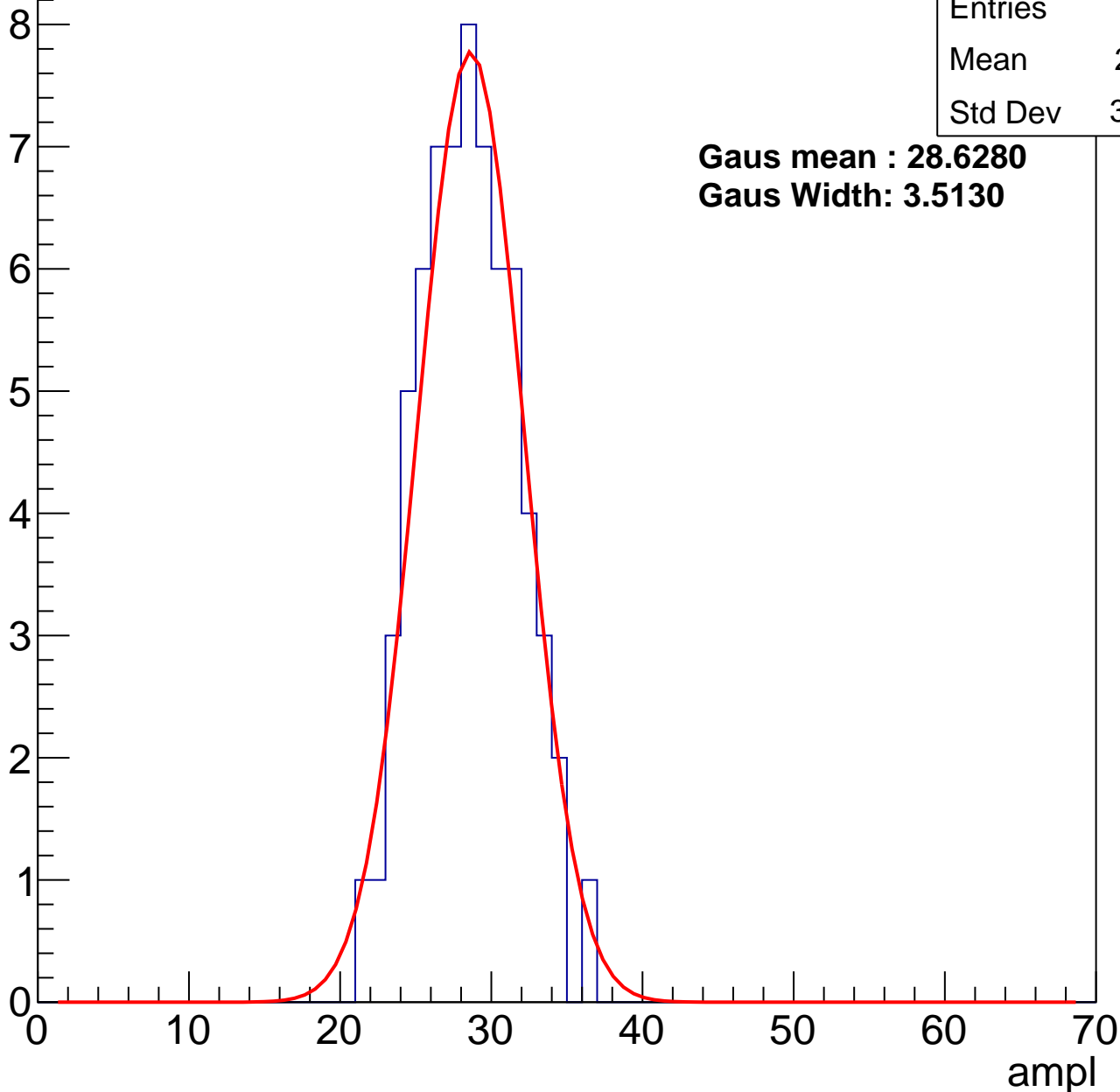
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.01
Std Dev	3.207

**Gaus mean : 28.6280**

**Gaus Width: 3.5130**



# B1L000S, U2-ch125, adc1

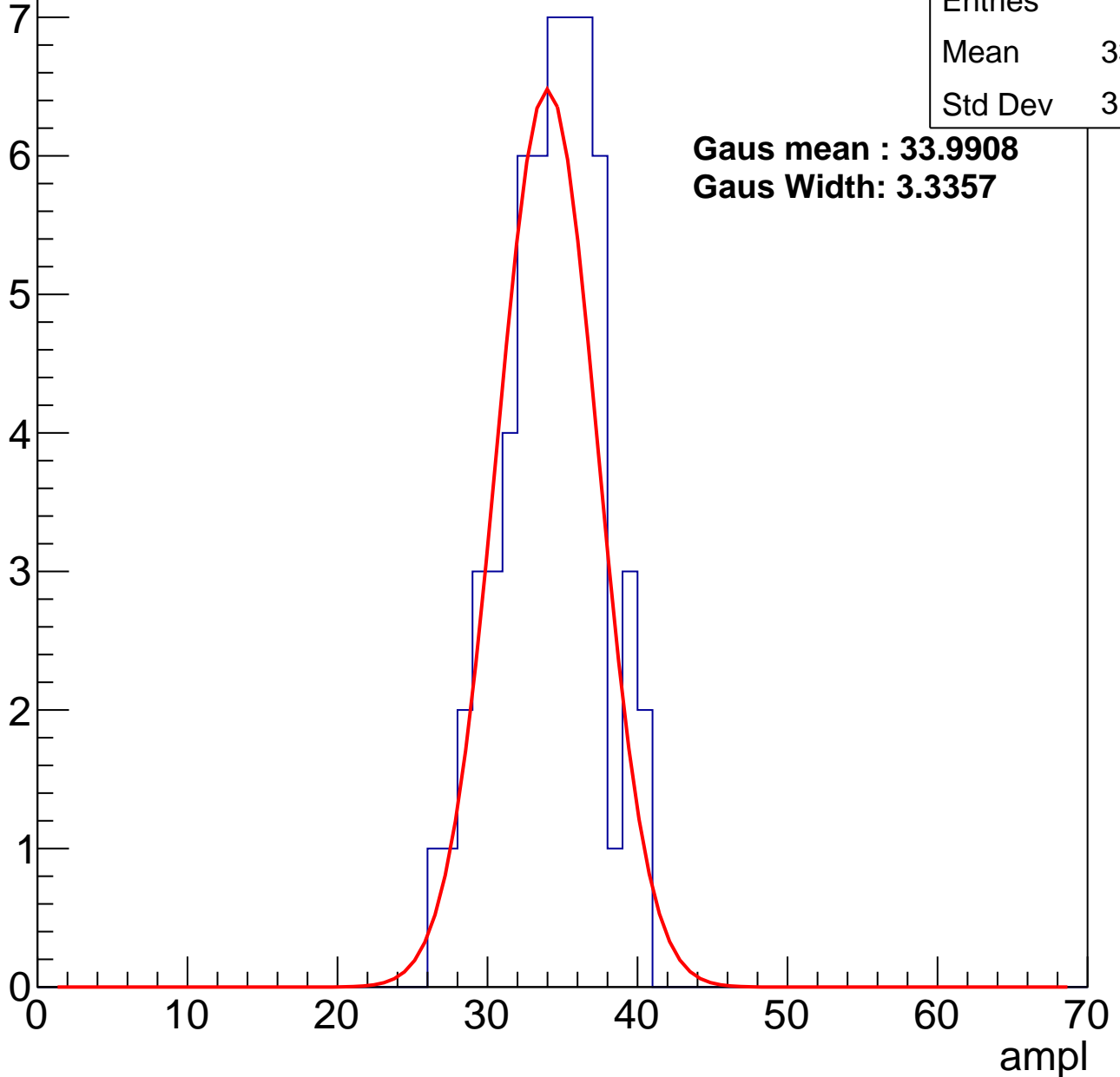
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	33.76
Std Dev	3.249

**Gaus mean : 33.9908**

**Gaus Width: 3.3357**



# B1L000S, U2-ch125, adc2

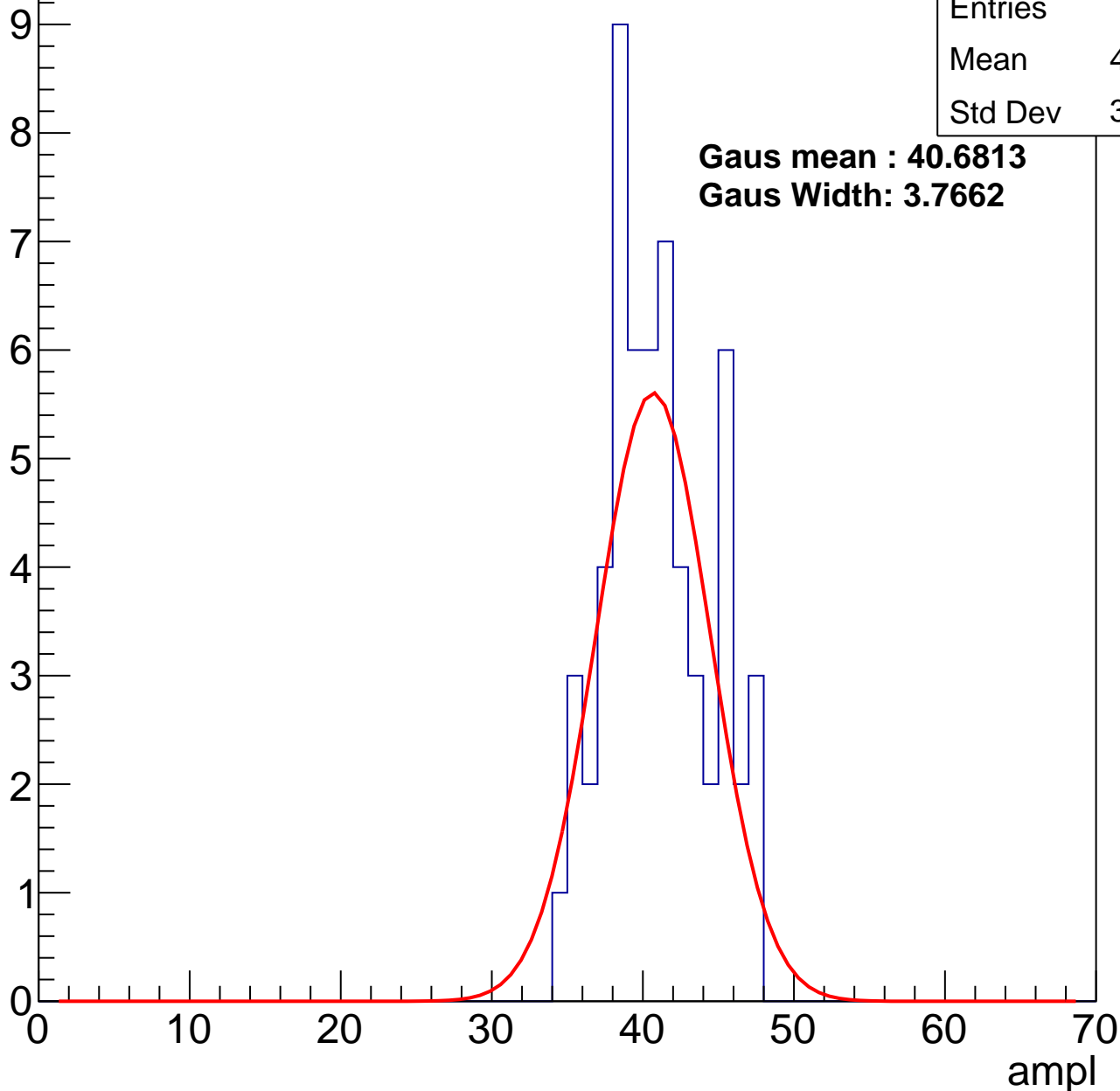
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	40.52
Std Dev	3.364

**Gaus mean : 40.6813**

**Gaus Width: 3.7662**

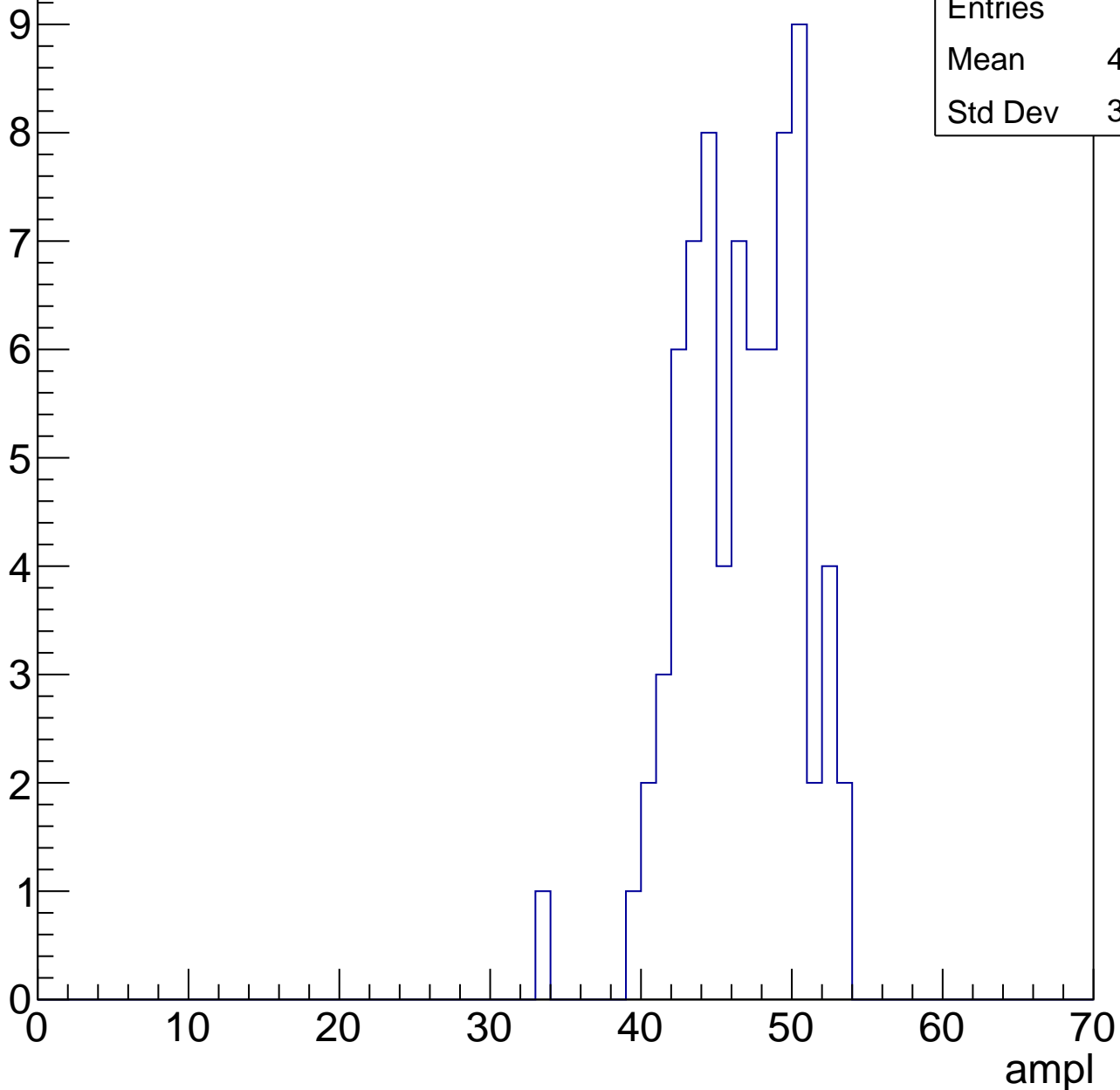


# B1L000S, U2-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	46.18
Std Dev	3.797

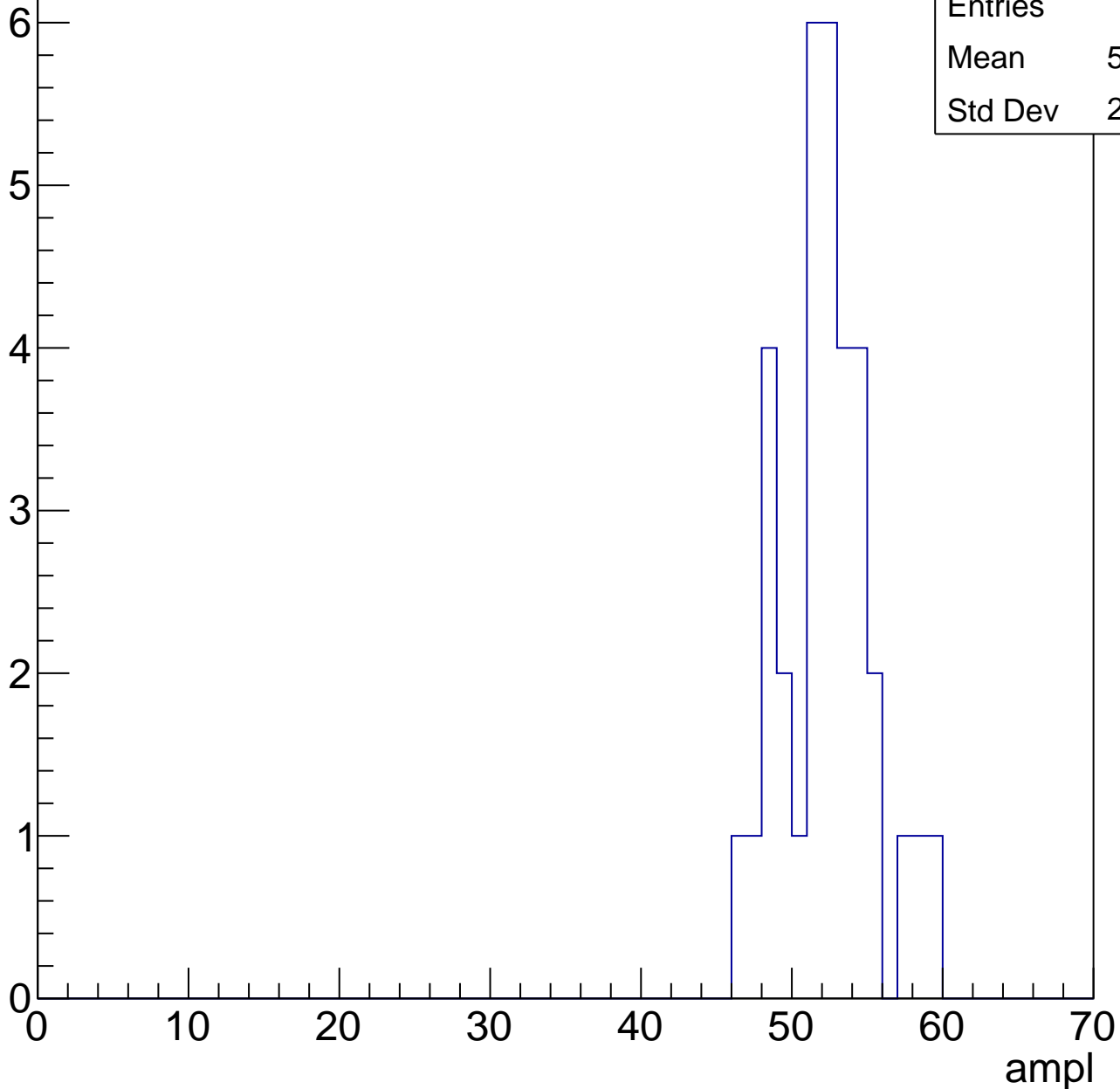


# B1L000S, U2-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	51.85
Std Dev	2.972

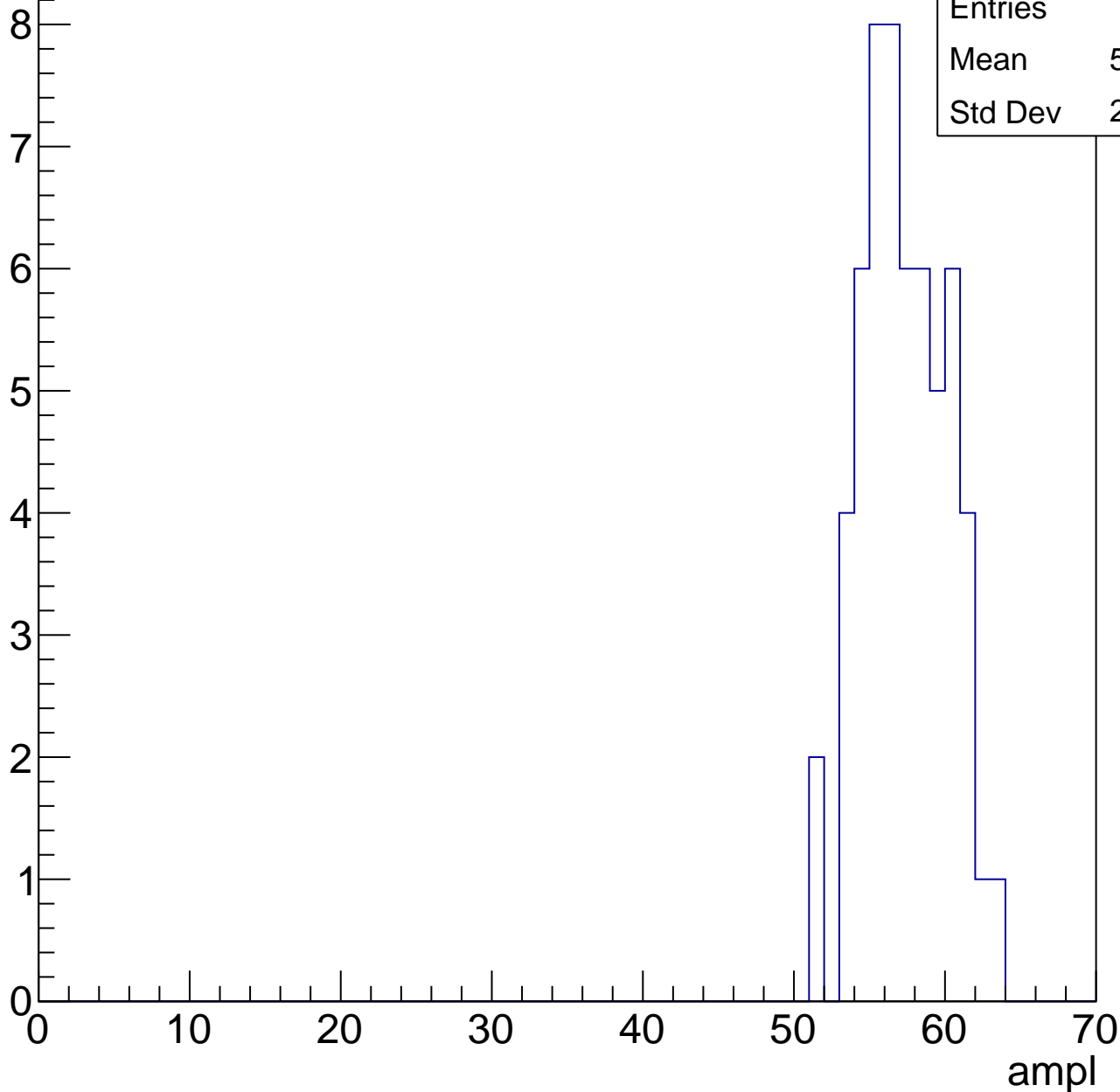


# B1L000S, U2-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	56.84
Std Dev	2.758

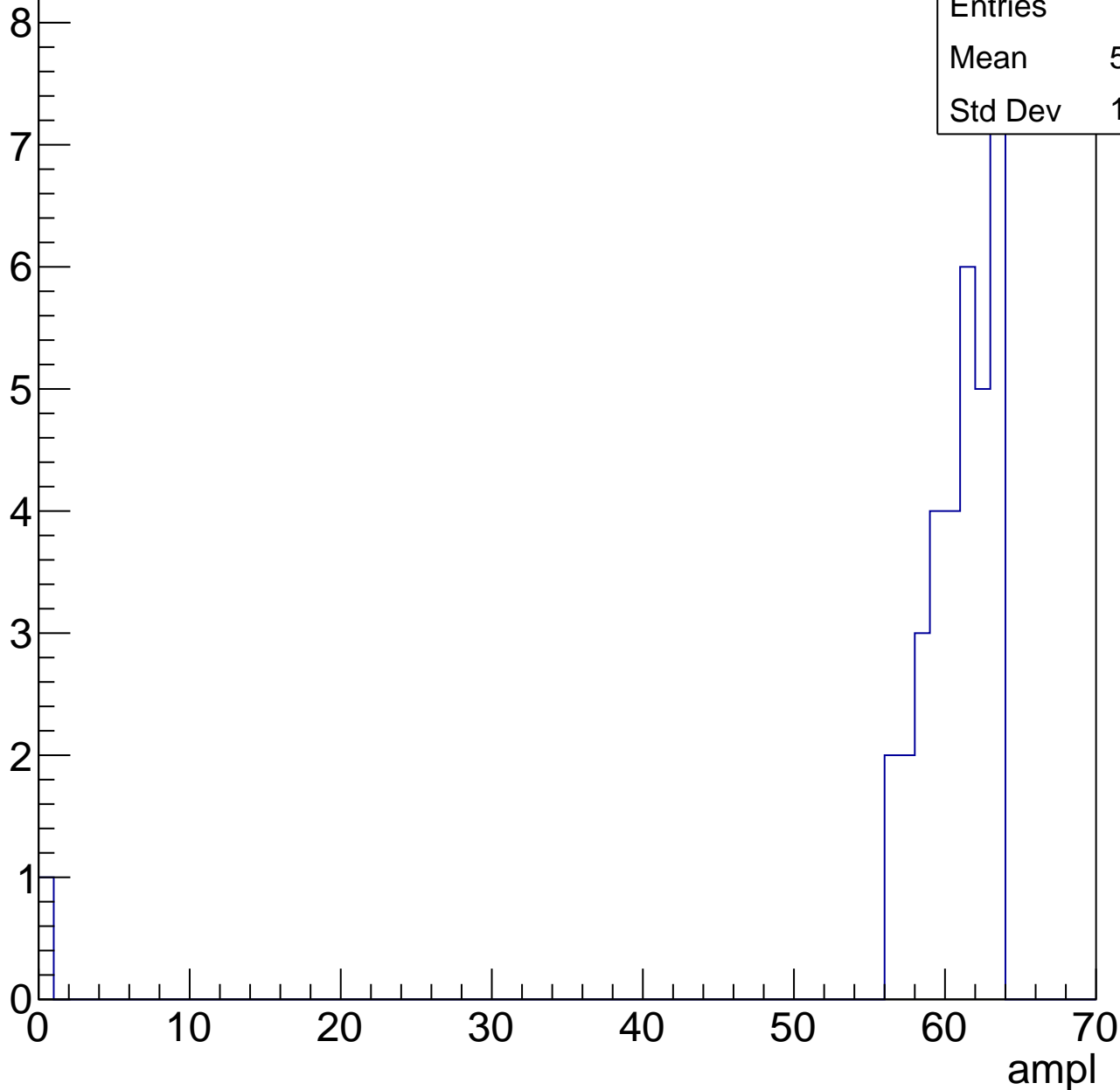


# B1L000S, U2-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	35
Mean	58.74
Std Dev	10.29

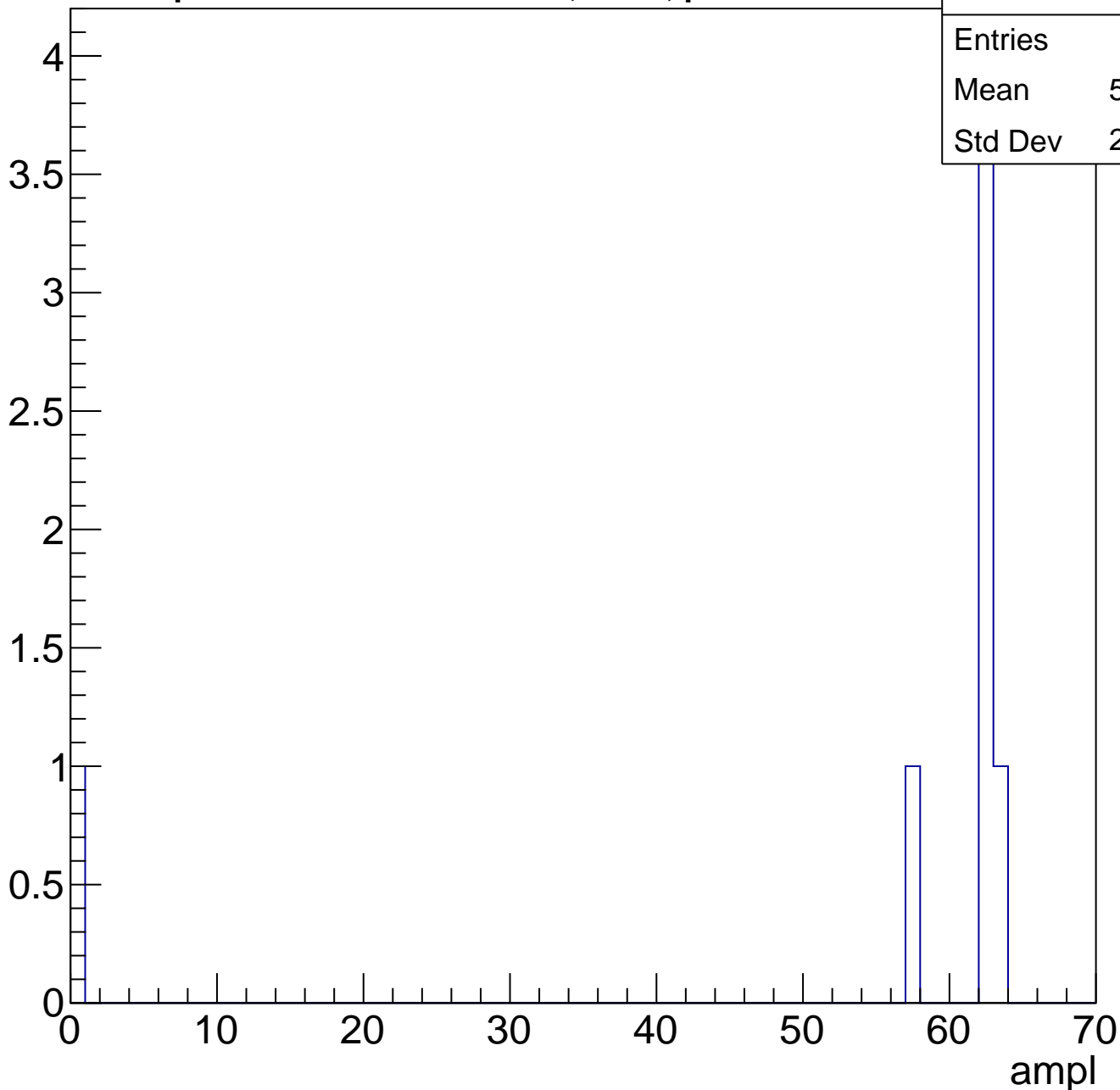




# B1L000S, U2-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch126, adc0

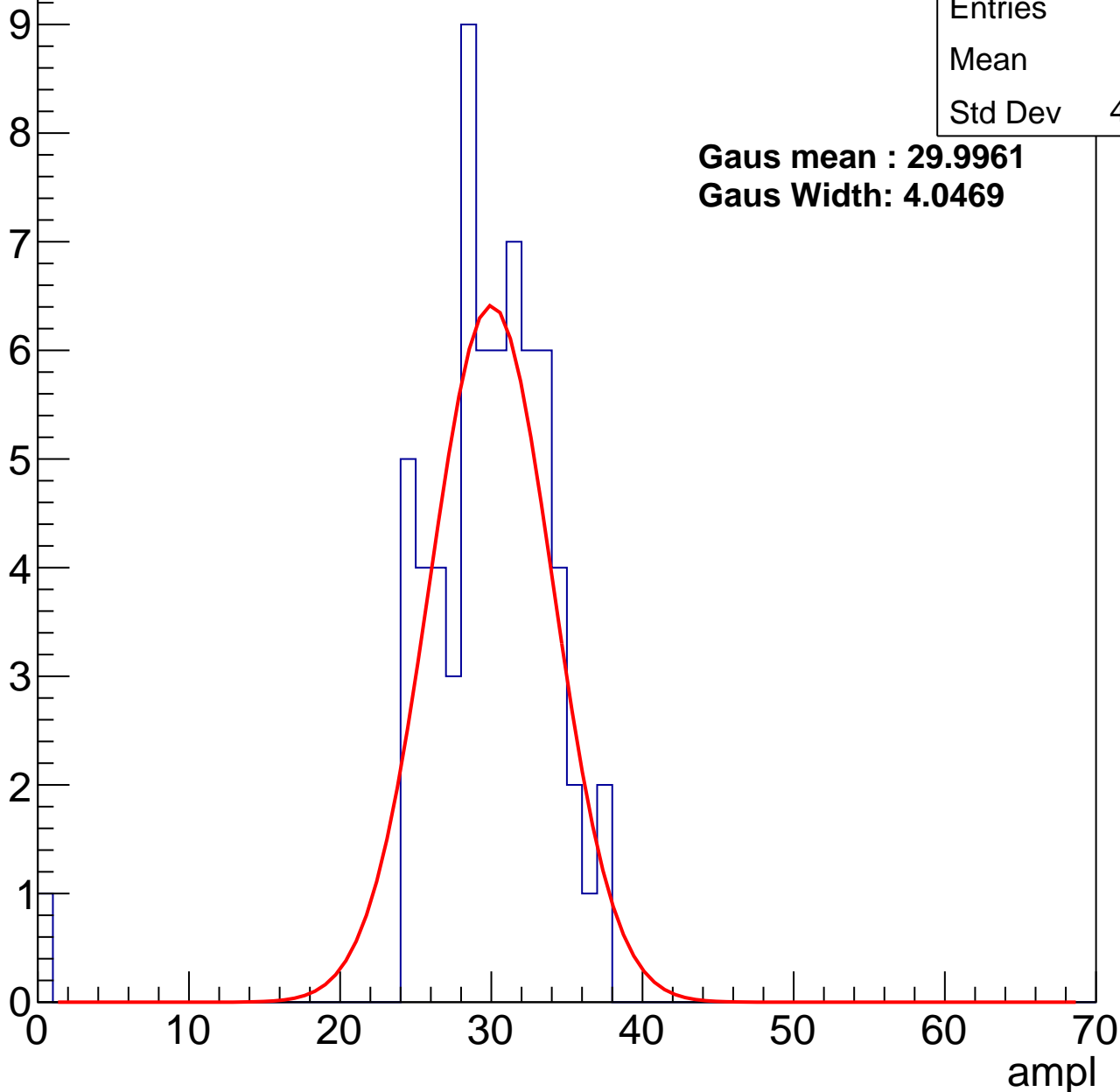
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	29.3
Std Dev	4.945

**Gaus mean : 29.9961**

**Gaus Width: 4.0469**



# B1L000S, U2-ch126, adc1

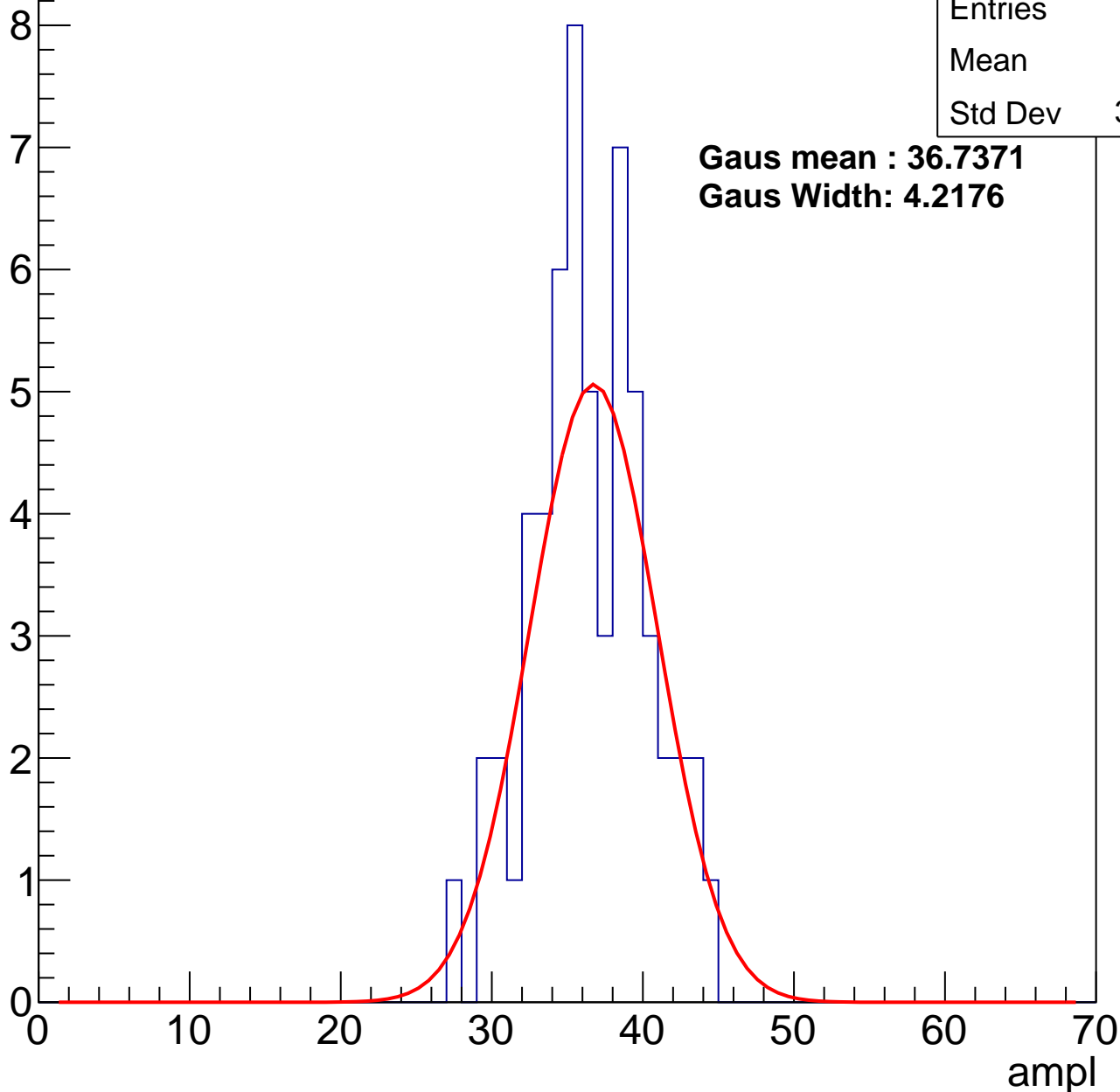
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	36
Std Dev	3.751

**Gaus mean : 36.7371**

**Gaus Width: 4.2176**



# B1L000S, U2-ch126, adc2

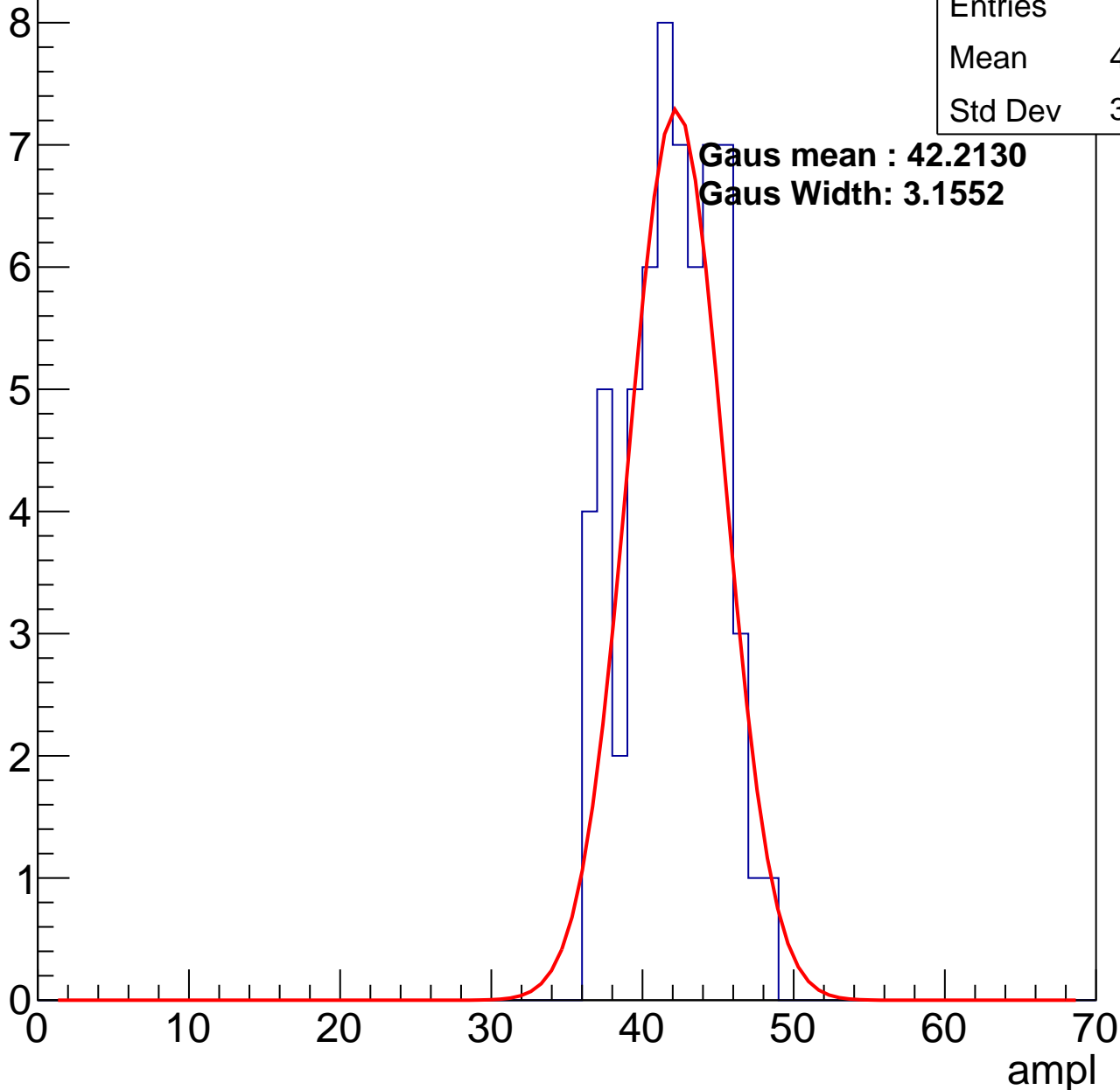
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.55
Std Dev	3.036

**Gaus mean : 42.2130**

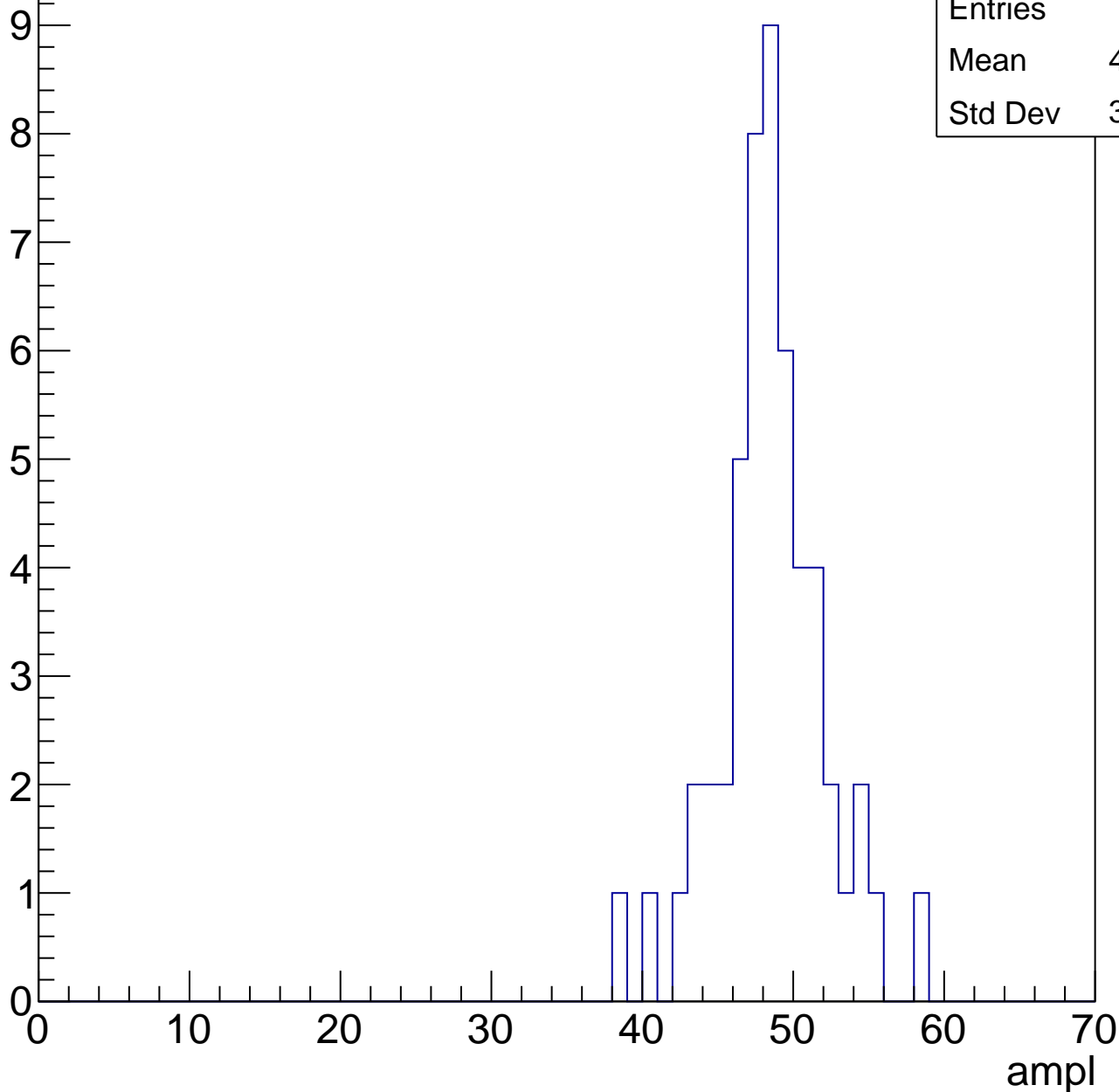
**Gaus Width: 3.1552**



# B1L000S, U2-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



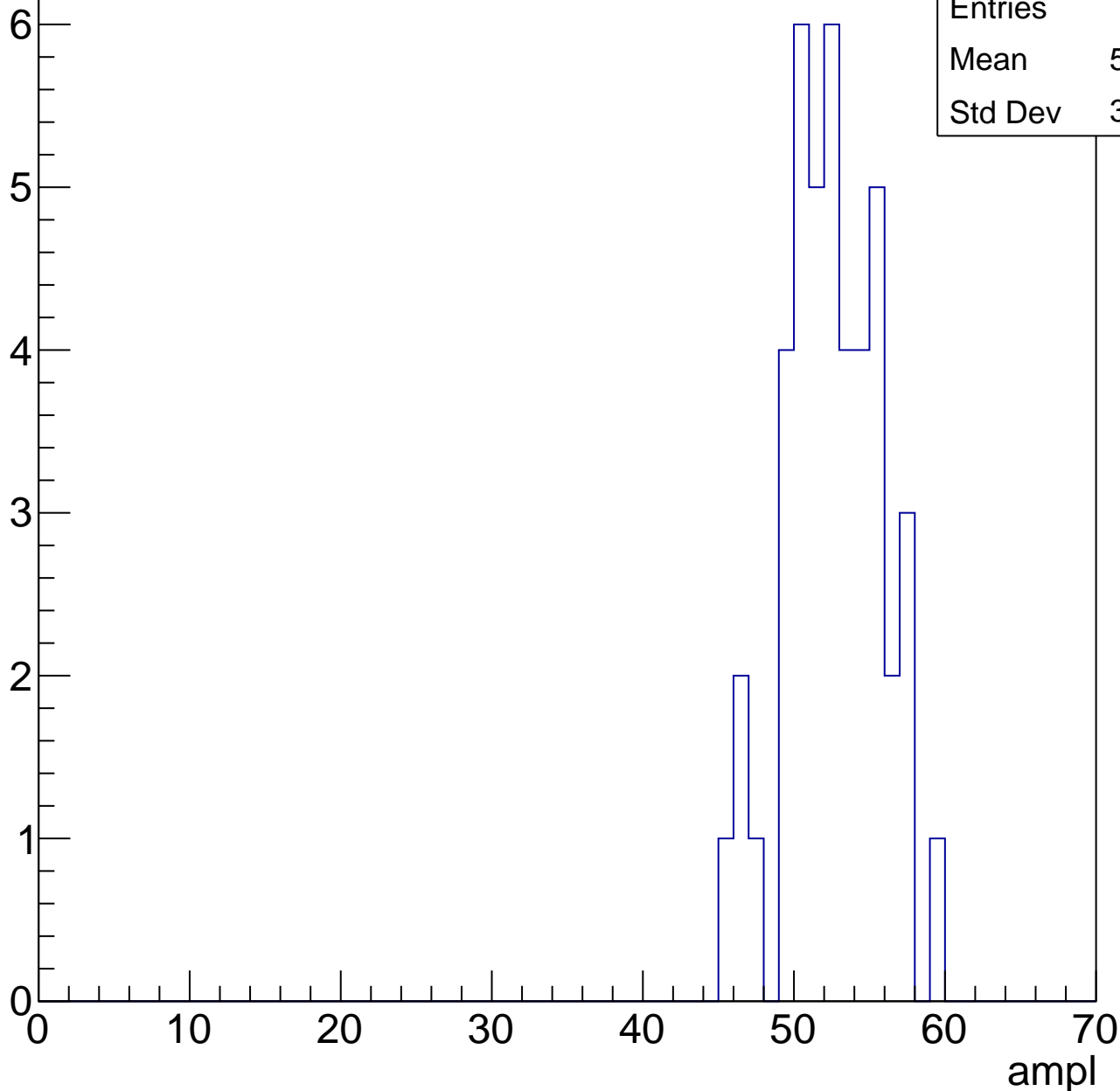
Entries	52
Mean	48.04
Std Dev	3.573

# B1L000S, U2-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

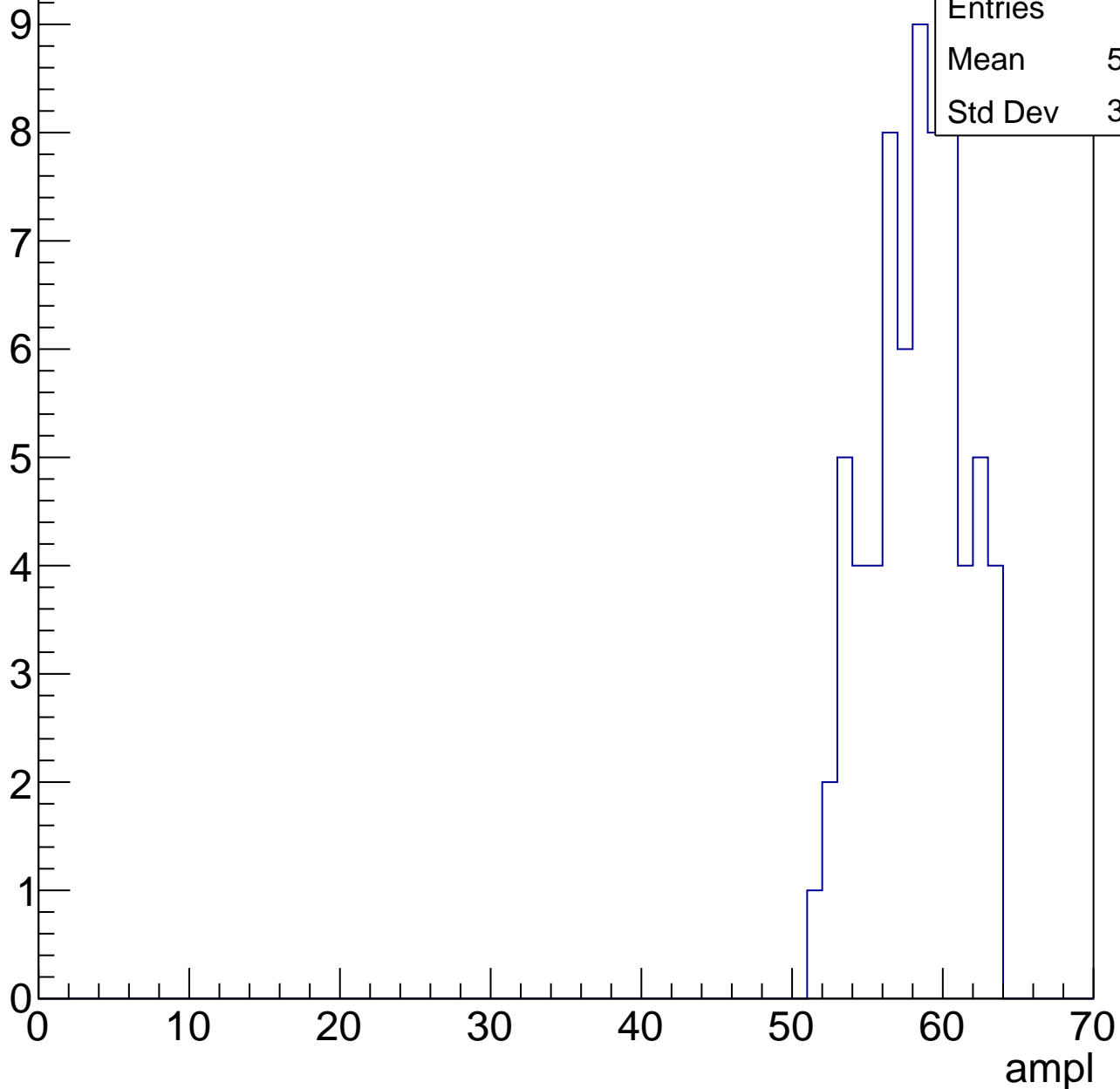
Entries	44
Mean	52.09
Std Dev	3.132



# B1L000S, U2-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

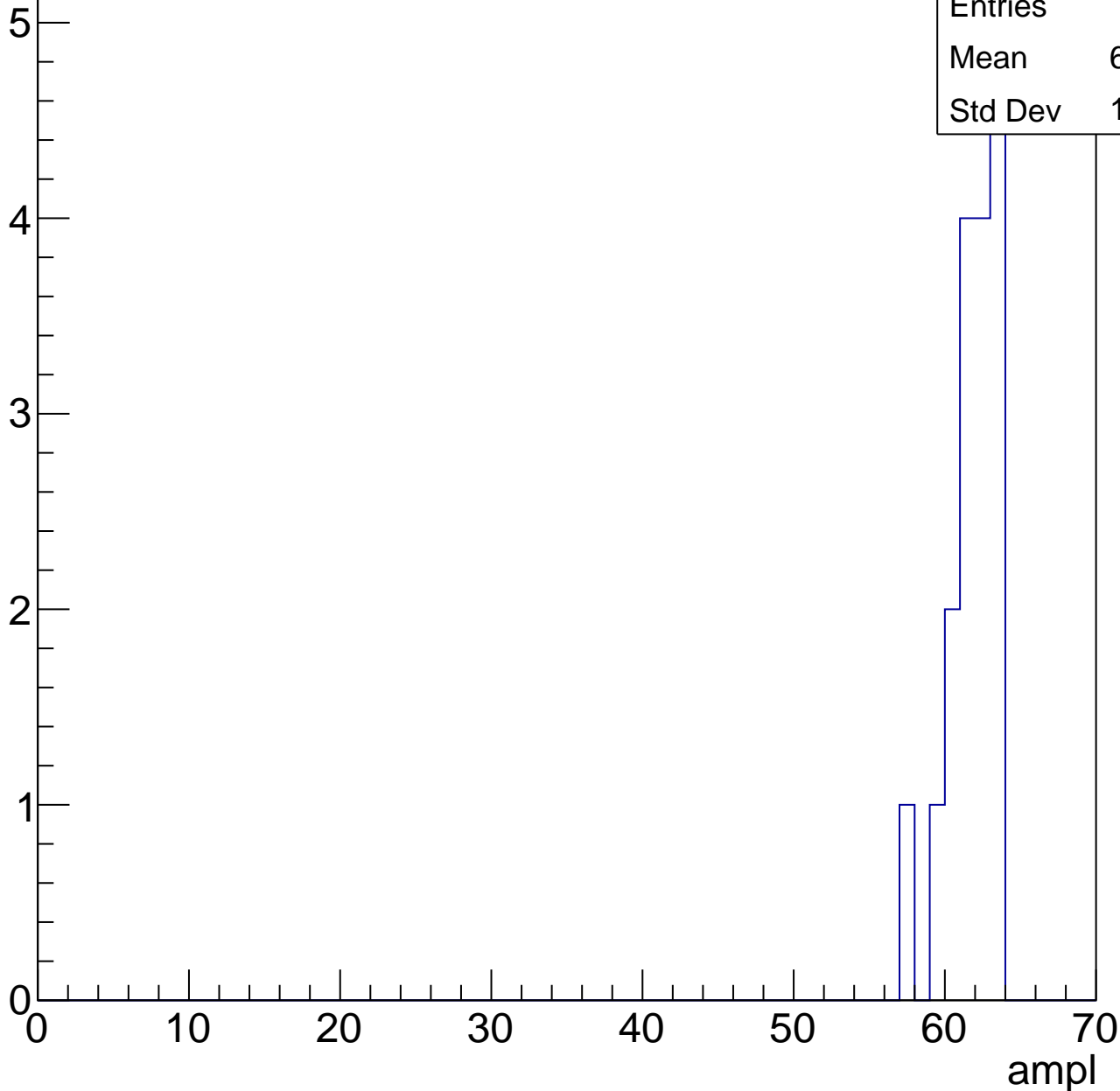


# B1L000S, U2-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	61.35
Std Dev	1.607

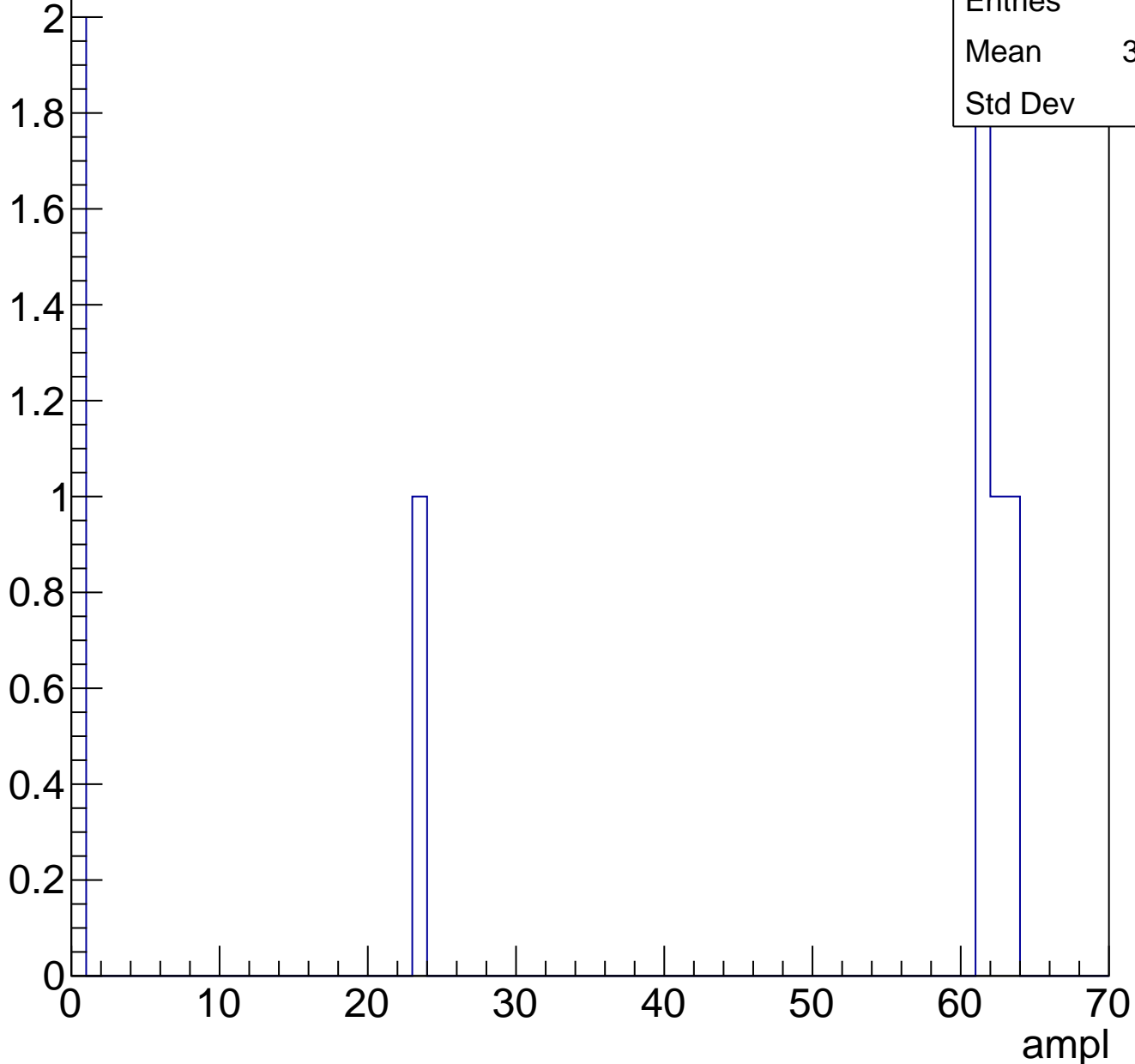




# B1L000S, U2-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	7
Mean	38.57
Std Dev	27.7

# B1L000S, U2-ch127, adc0

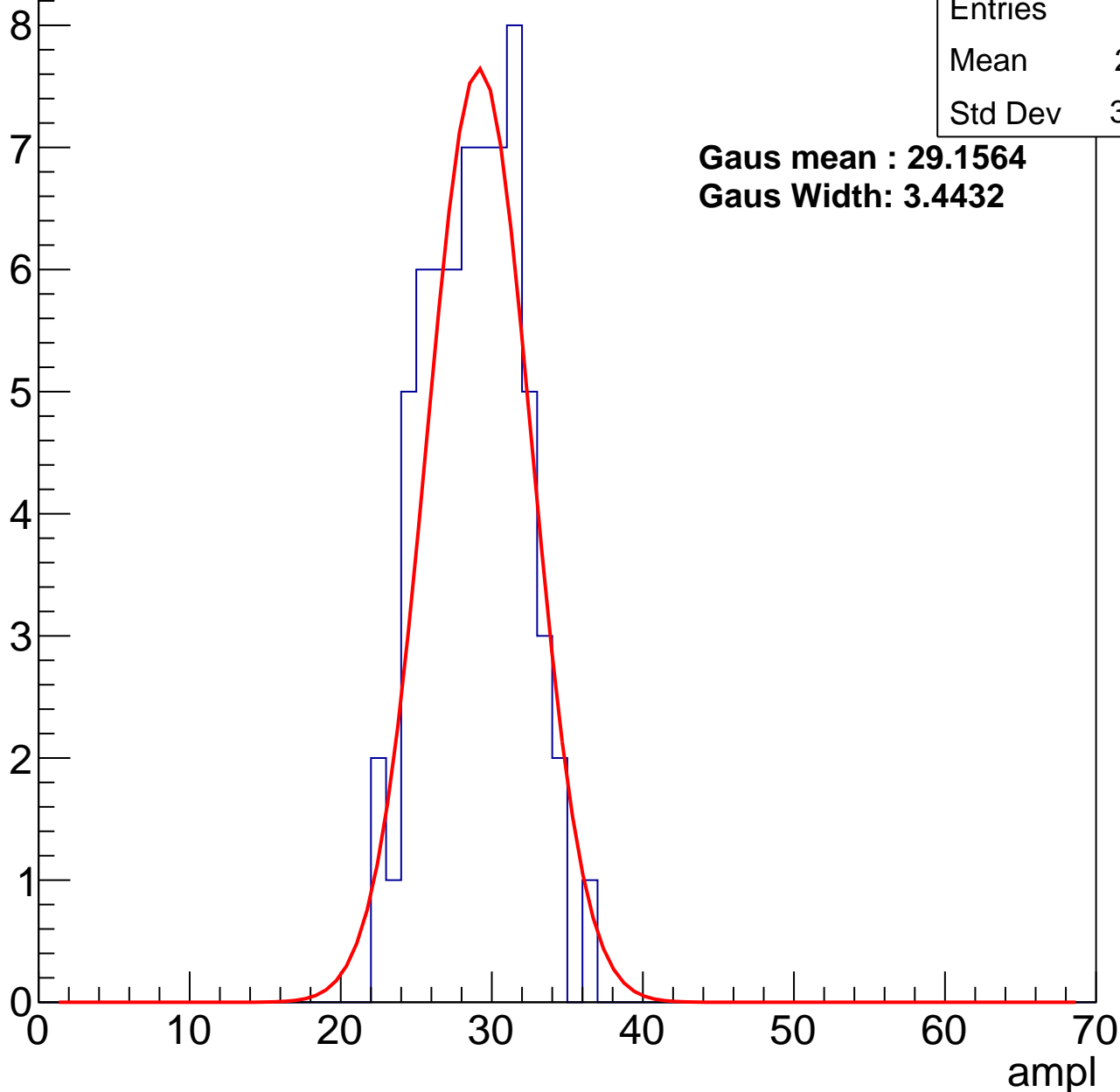
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	28.41
Std Dev	3.133

**Gaus mean : 29.1564**

**Gaus Width: 3.4432**



# B1L000S, U2-ch127, adc1

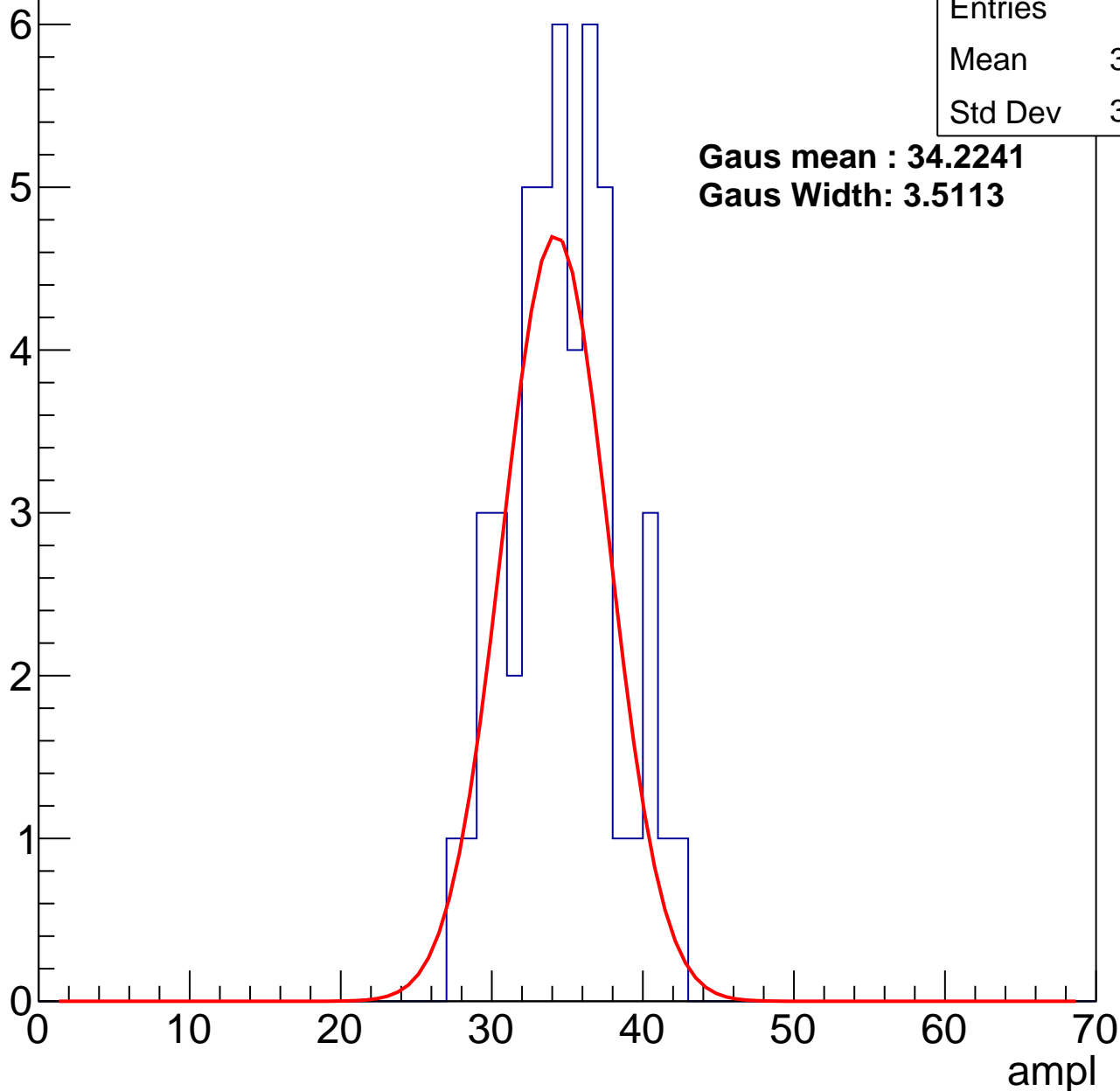
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	34.25
Std Dev	3.485

**Gaus mean : 34.2241**

**Gaus Width: 3.5113**



# B1L000S, U2-ch127, adc2

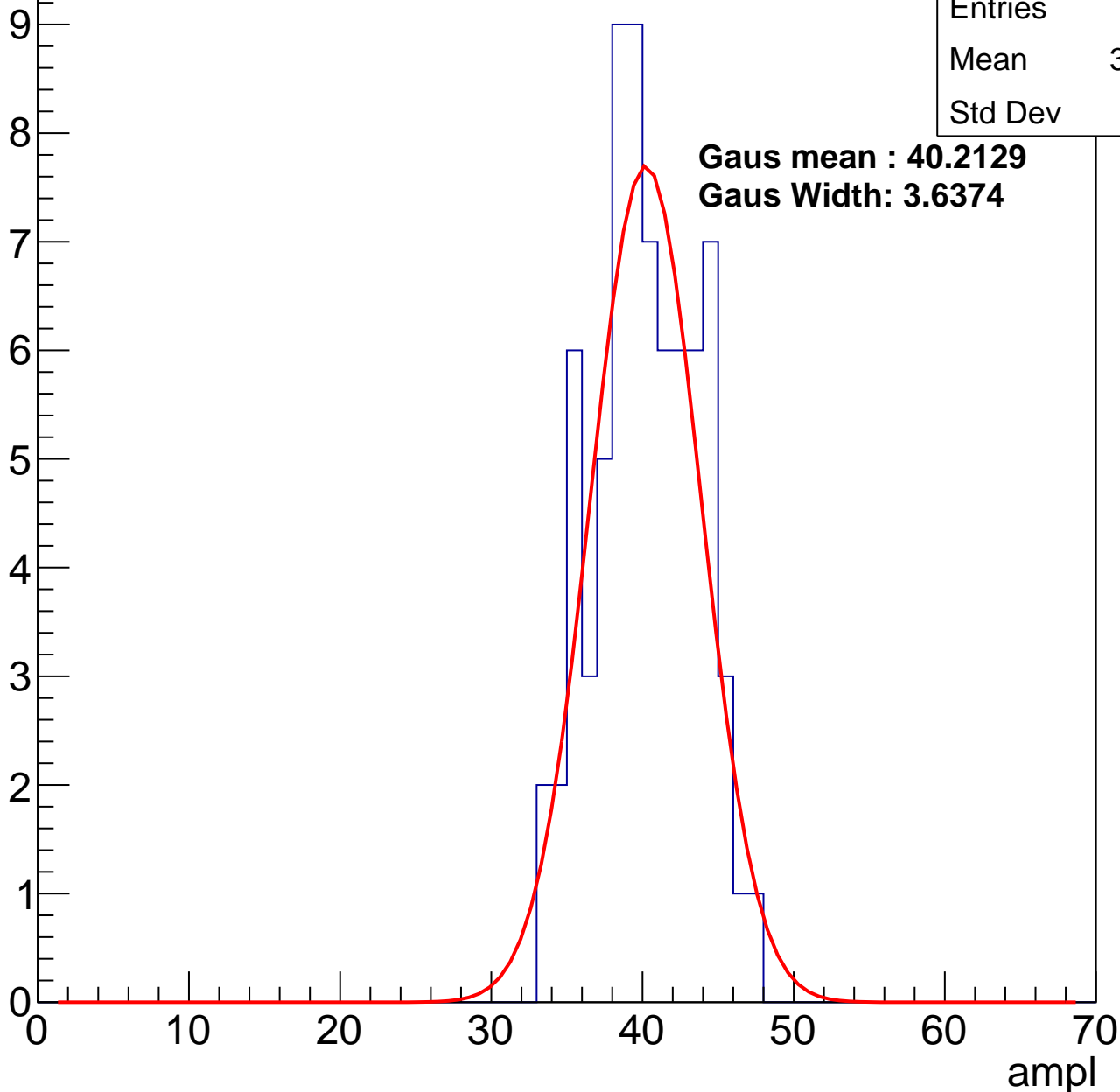
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	39.75
Std Dev	3.33

**Gaus mean : 40.2129**

**Gaus Width: 3.6374**

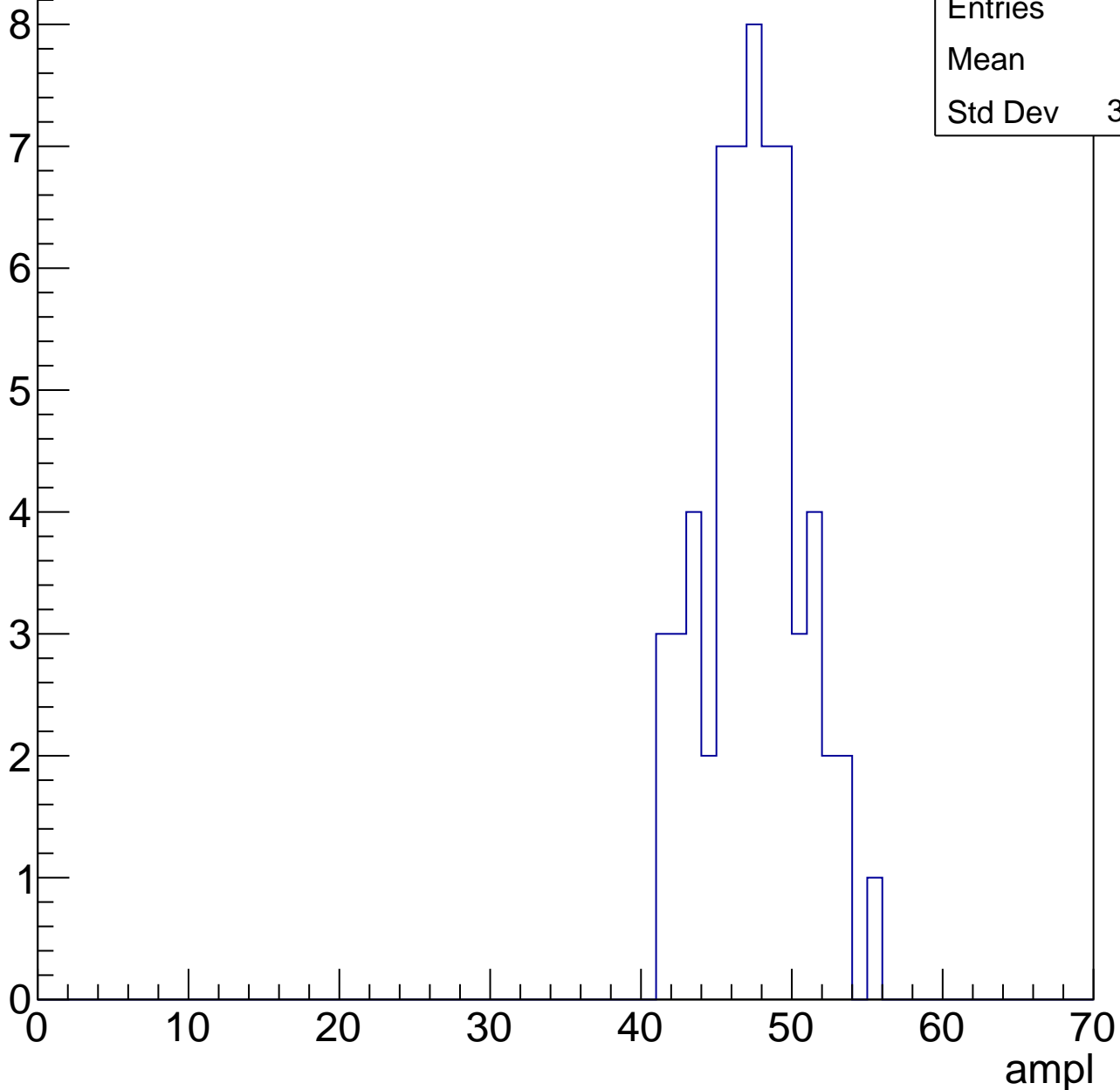


# B1L000S, U2-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	47
Std Dev	3.194

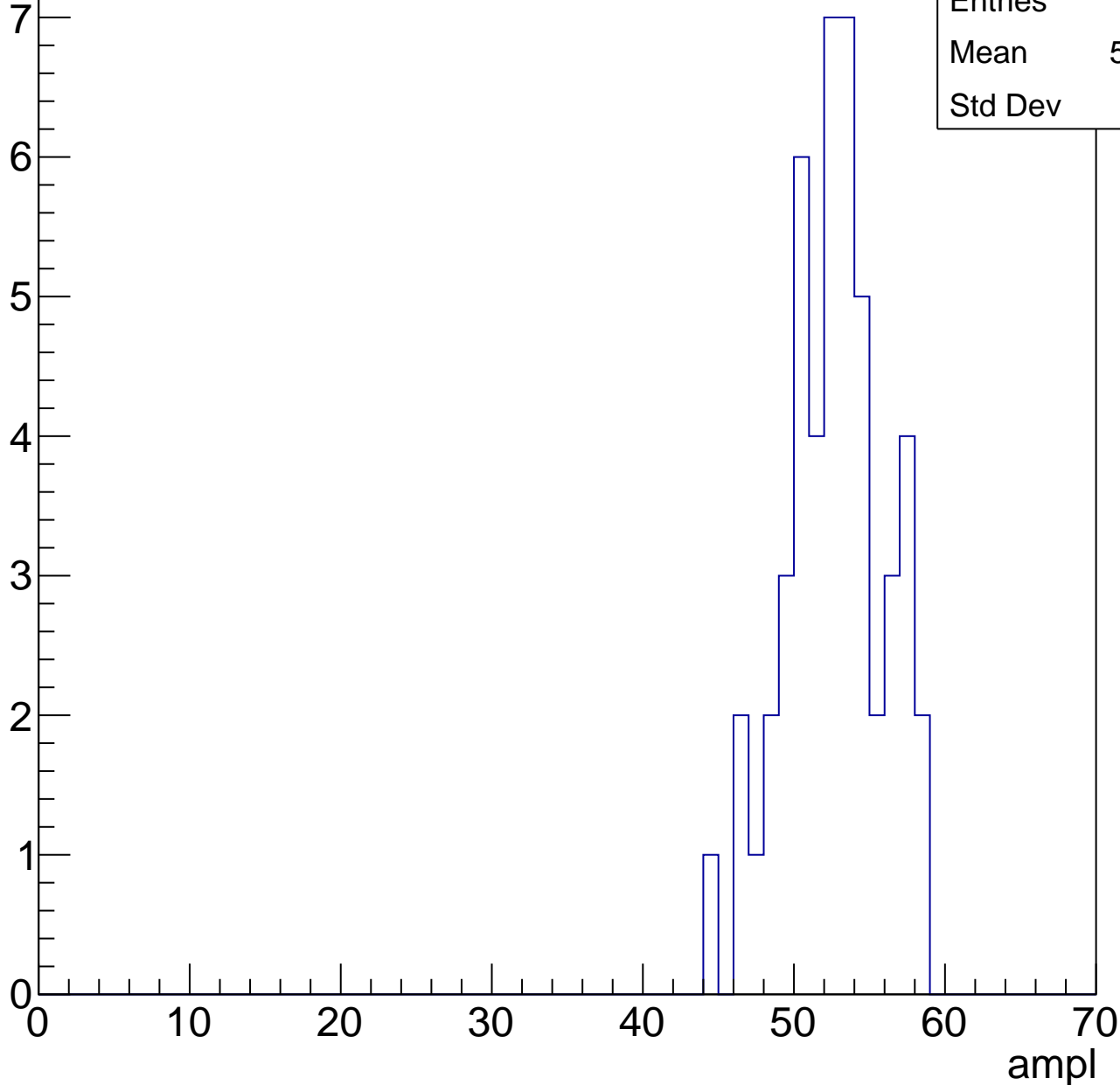


# B1L000S, U2-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	52.18
Std Dev	3.23

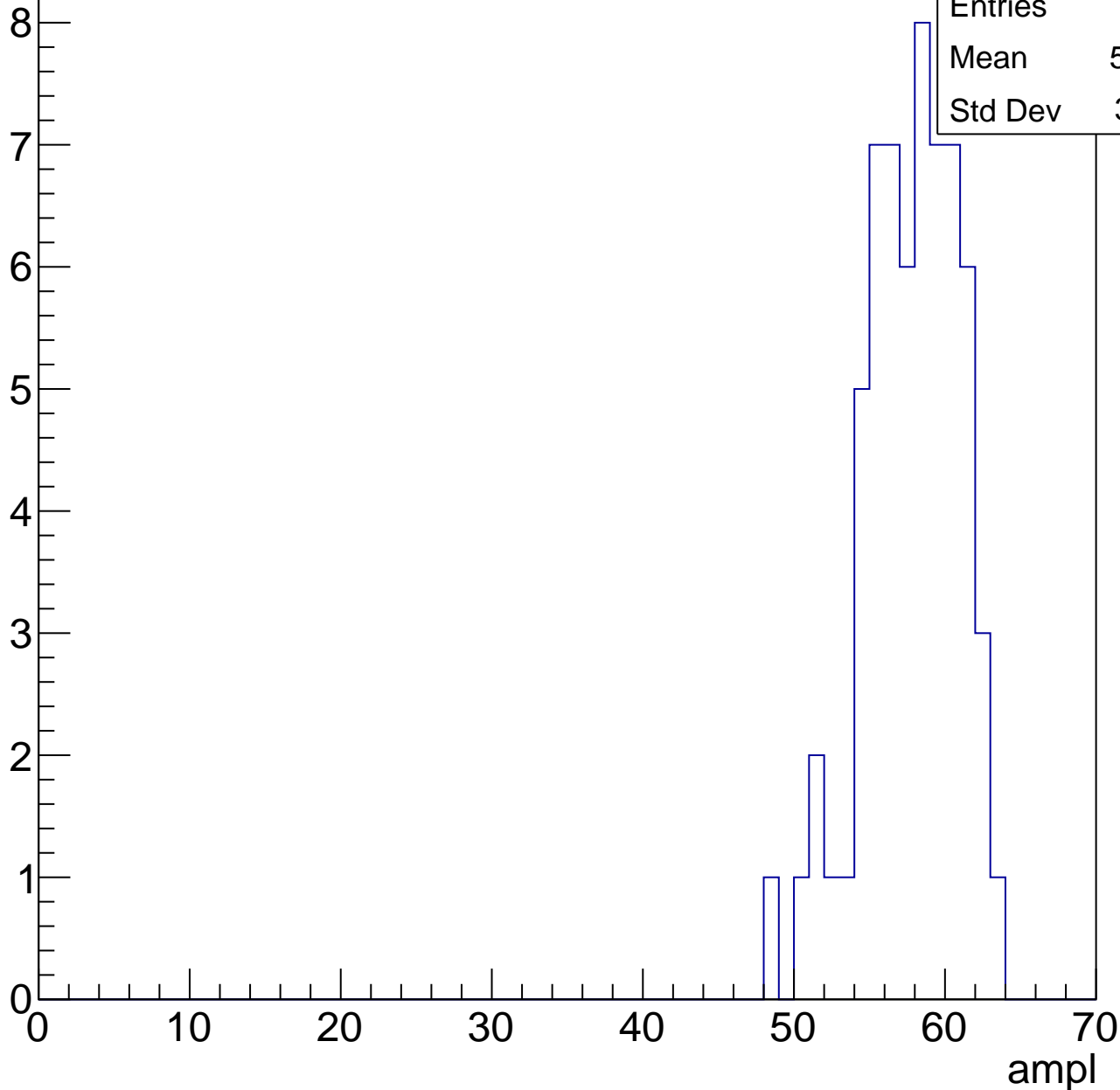


# B1L000S, U2-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	57.24
Std Dev	3.151

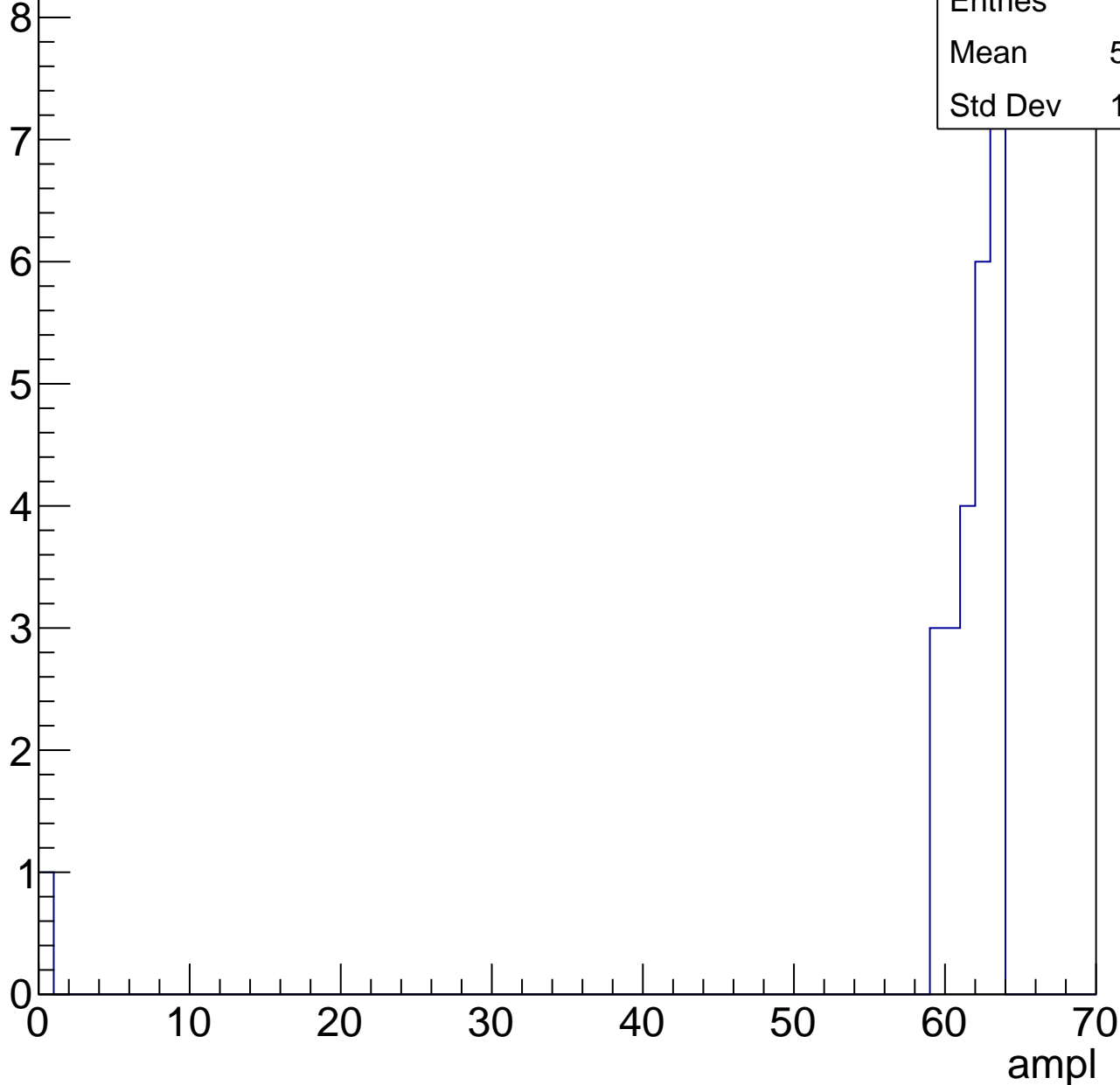


# B1L000S, U2-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	59.08
Std Dev	12.14

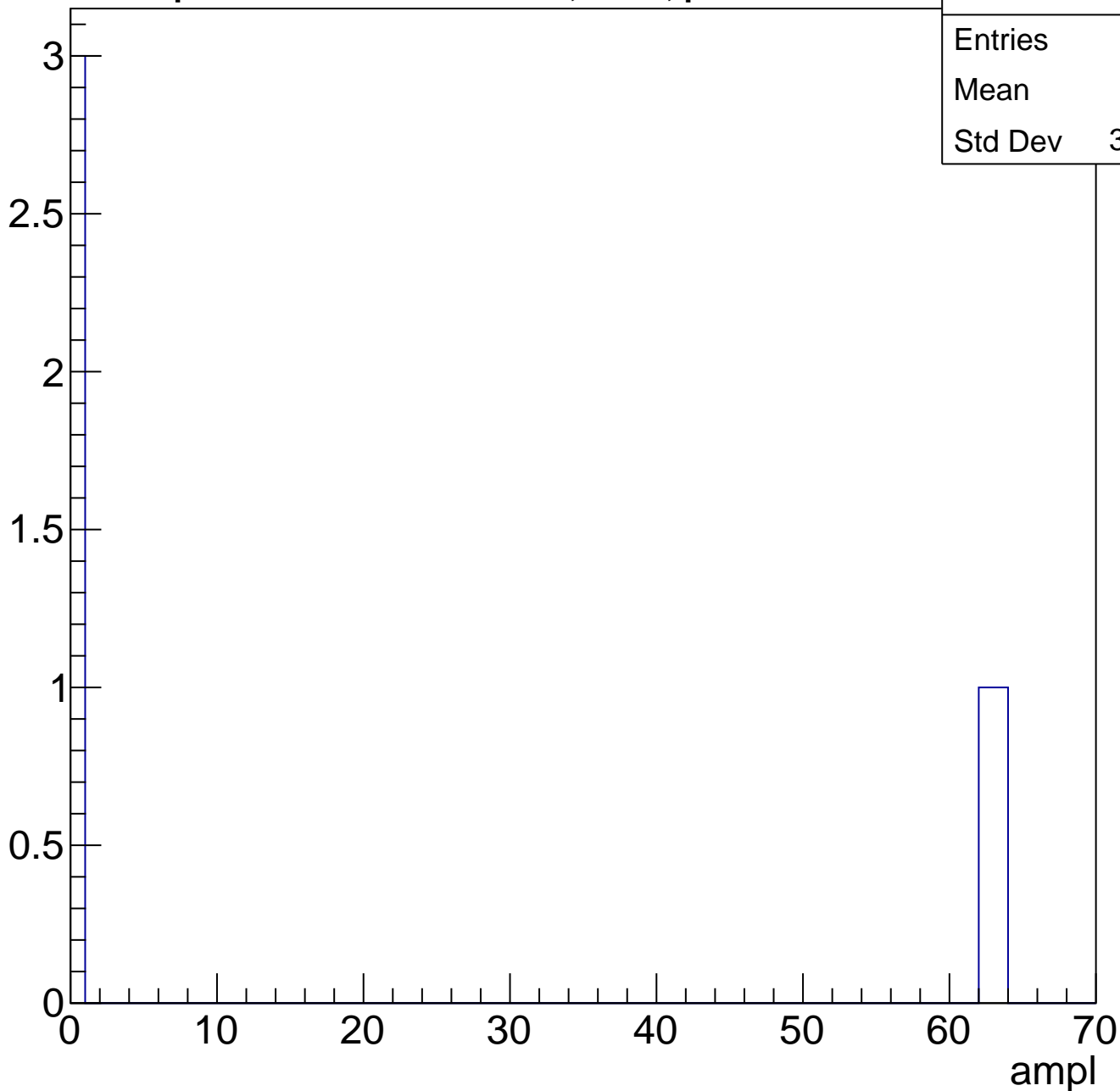




# B1L000S, U2-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U2-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

