

B1L103S, U13-ch0, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	22.6
Std Dev	11.31

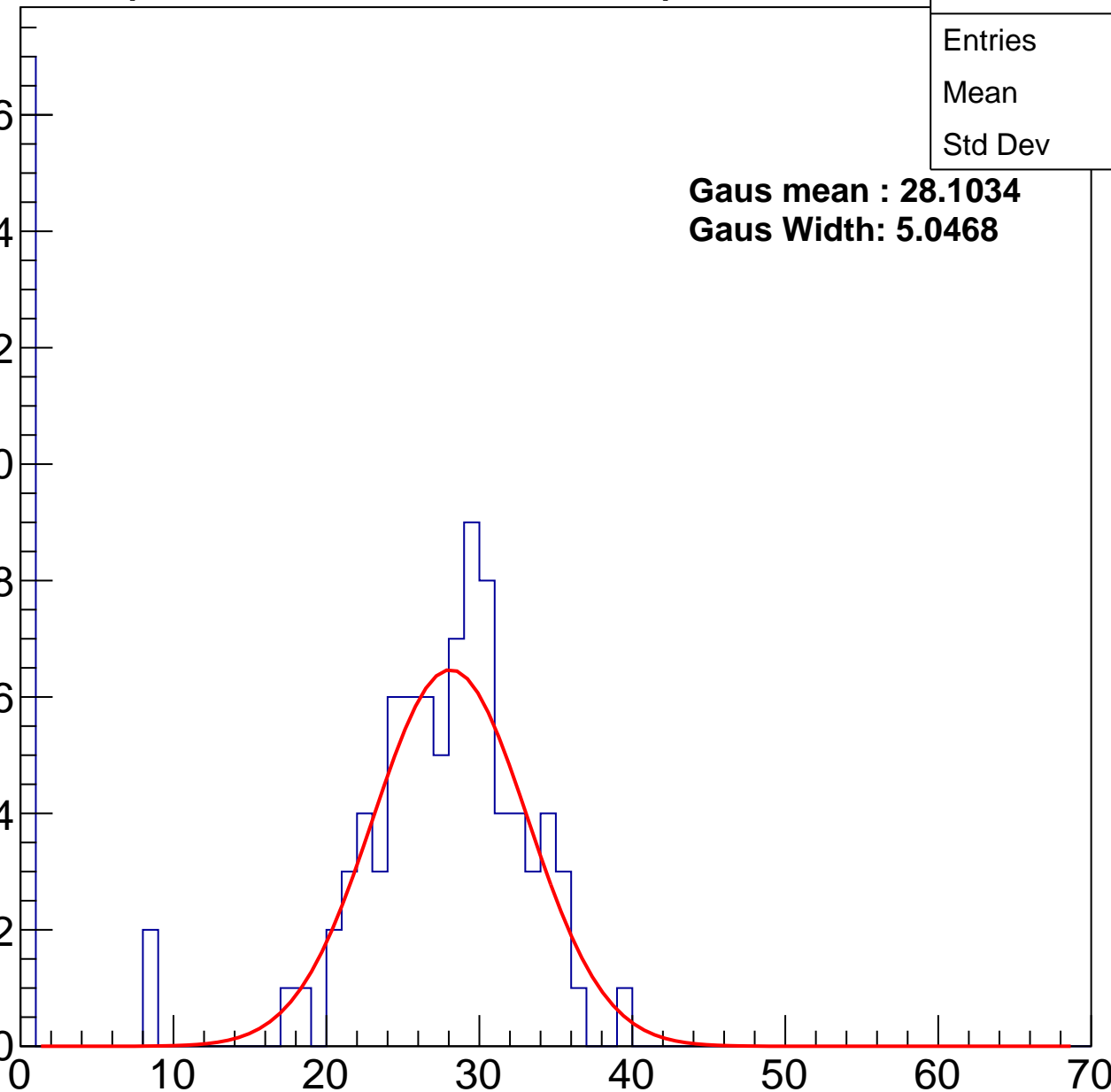
Gaus mean : 28.1034

Gaus Width: 5.0468

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch0, adc1

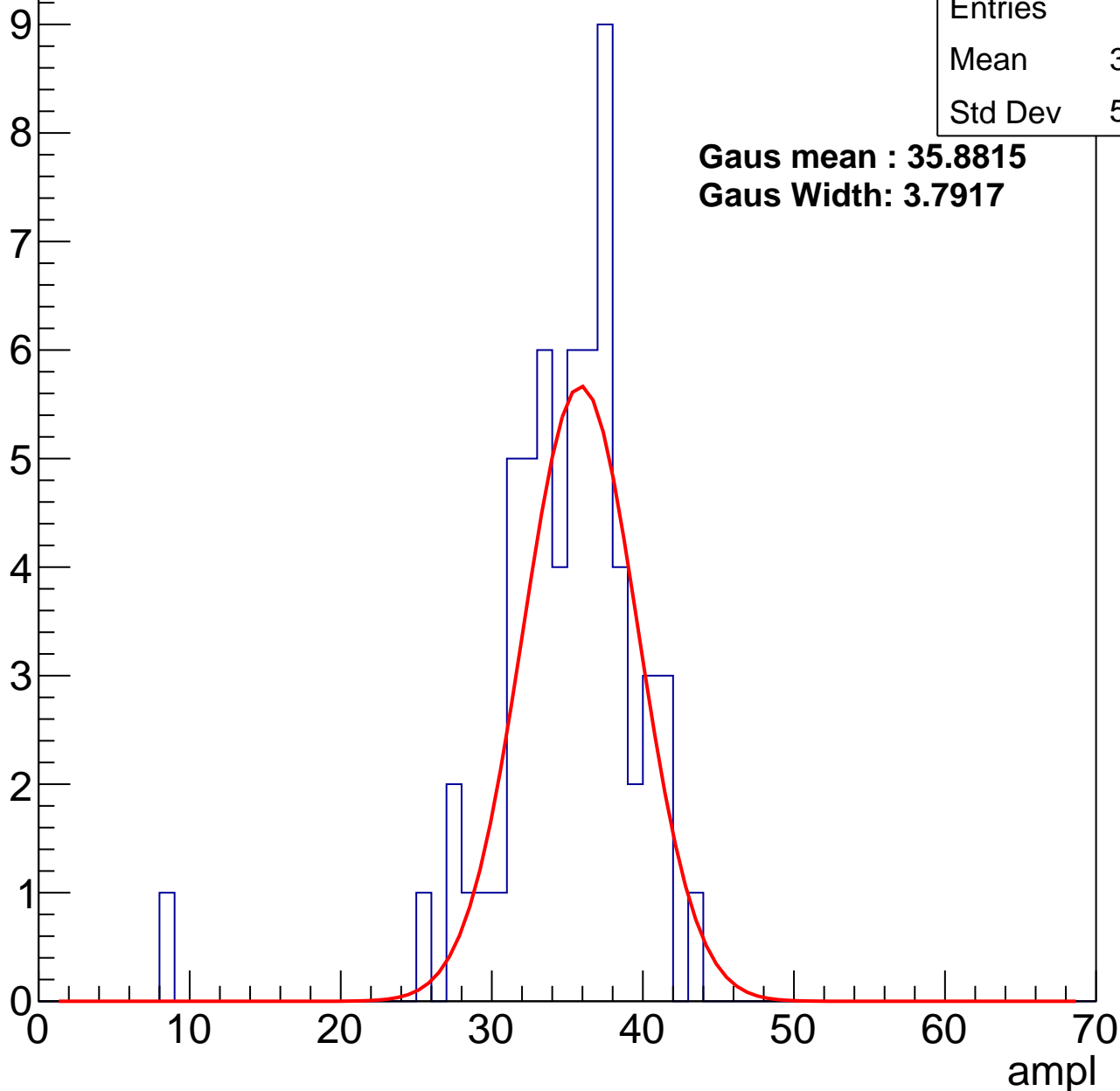
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.39
Std Dev	5.048

Gaus mean : 35.8815

Gaus Width: 3.7917



B1L103S, U13-ch0, adc2

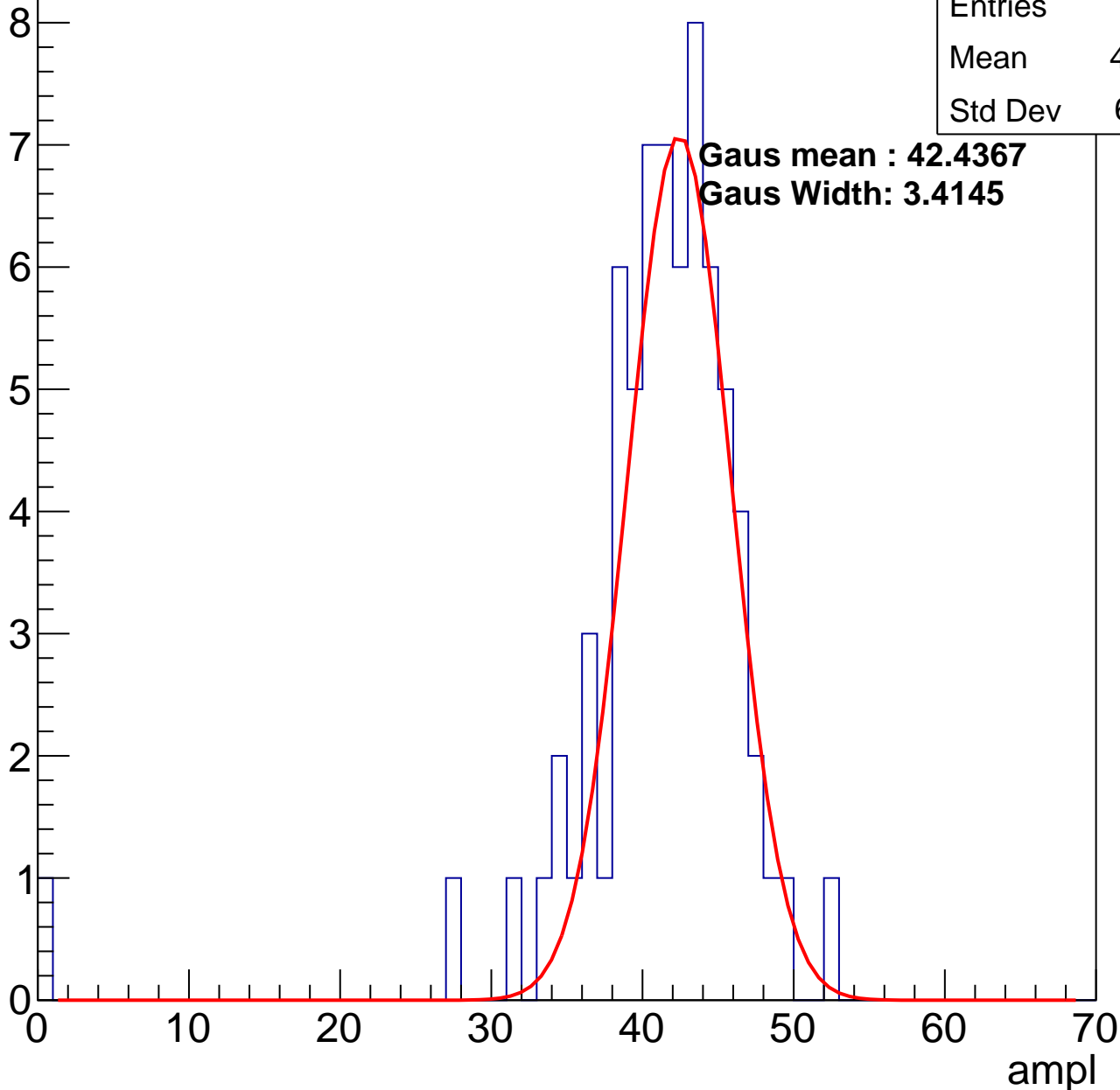
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	40.59
Std Dev	6.451

Gaus mean : 42.4367

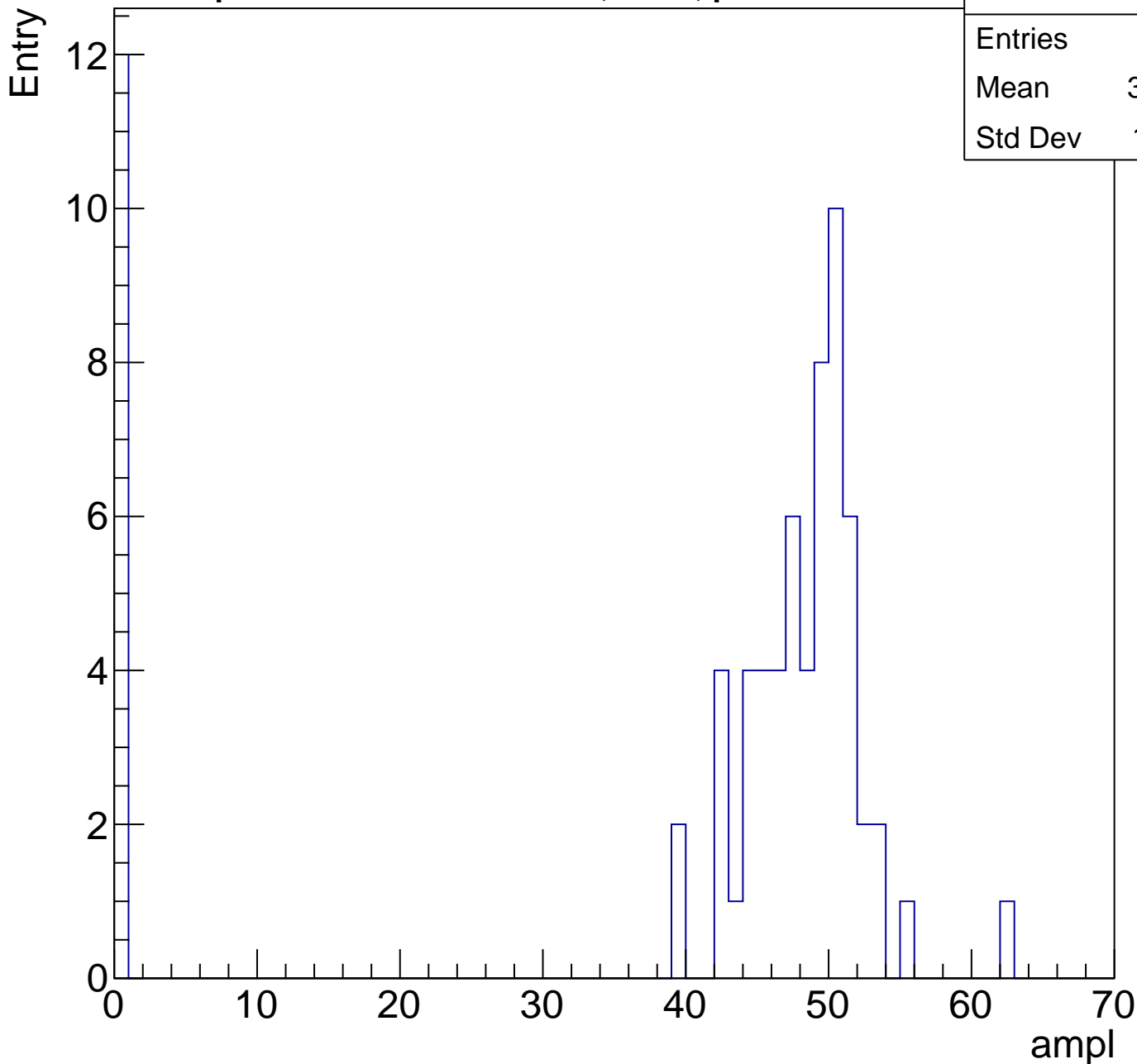
Gaus Width: 3.4145



B1L103S, U13-ch0, adc3

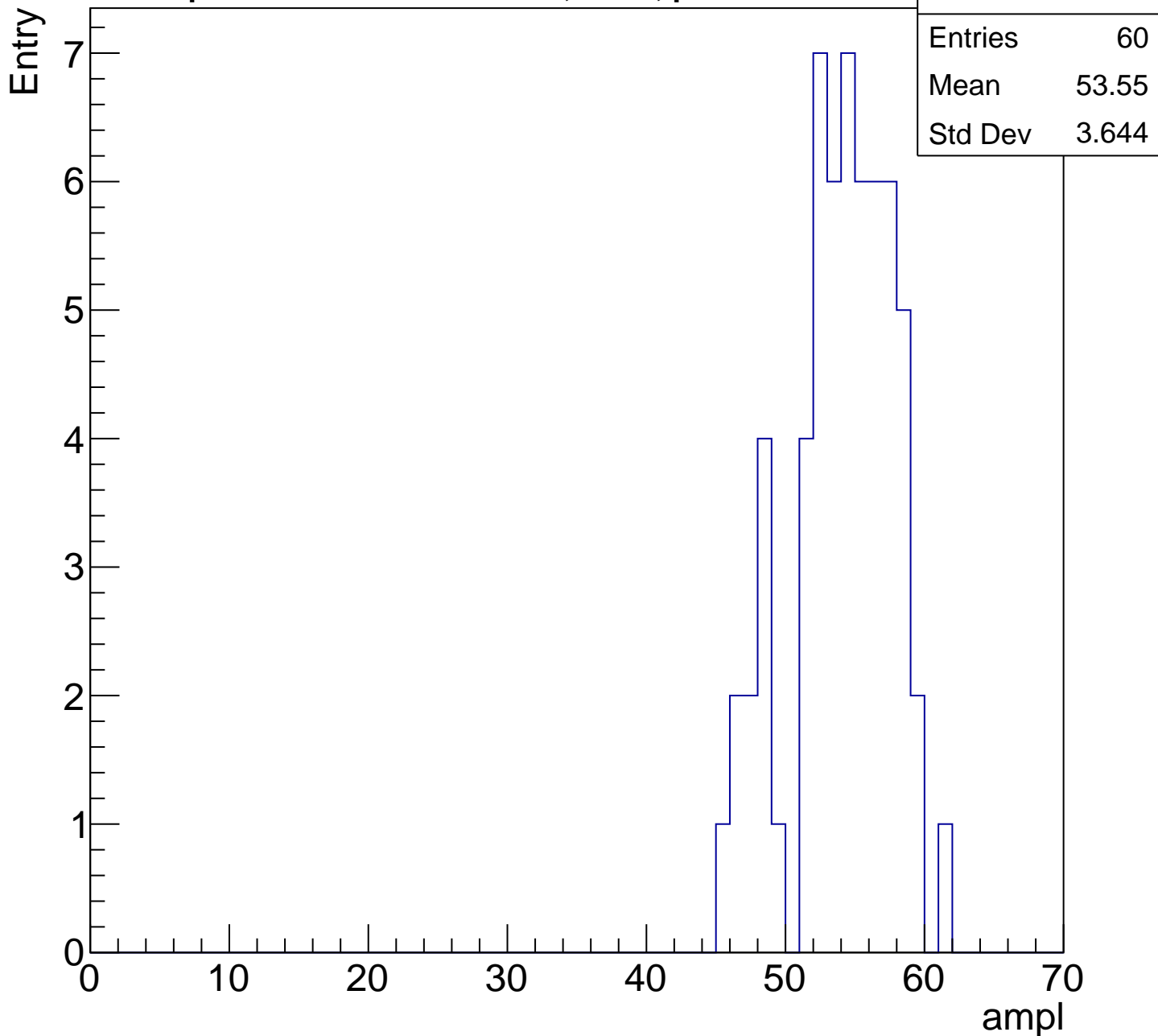
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	39.83
Std Dev	18.31



B1L103S, U13-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

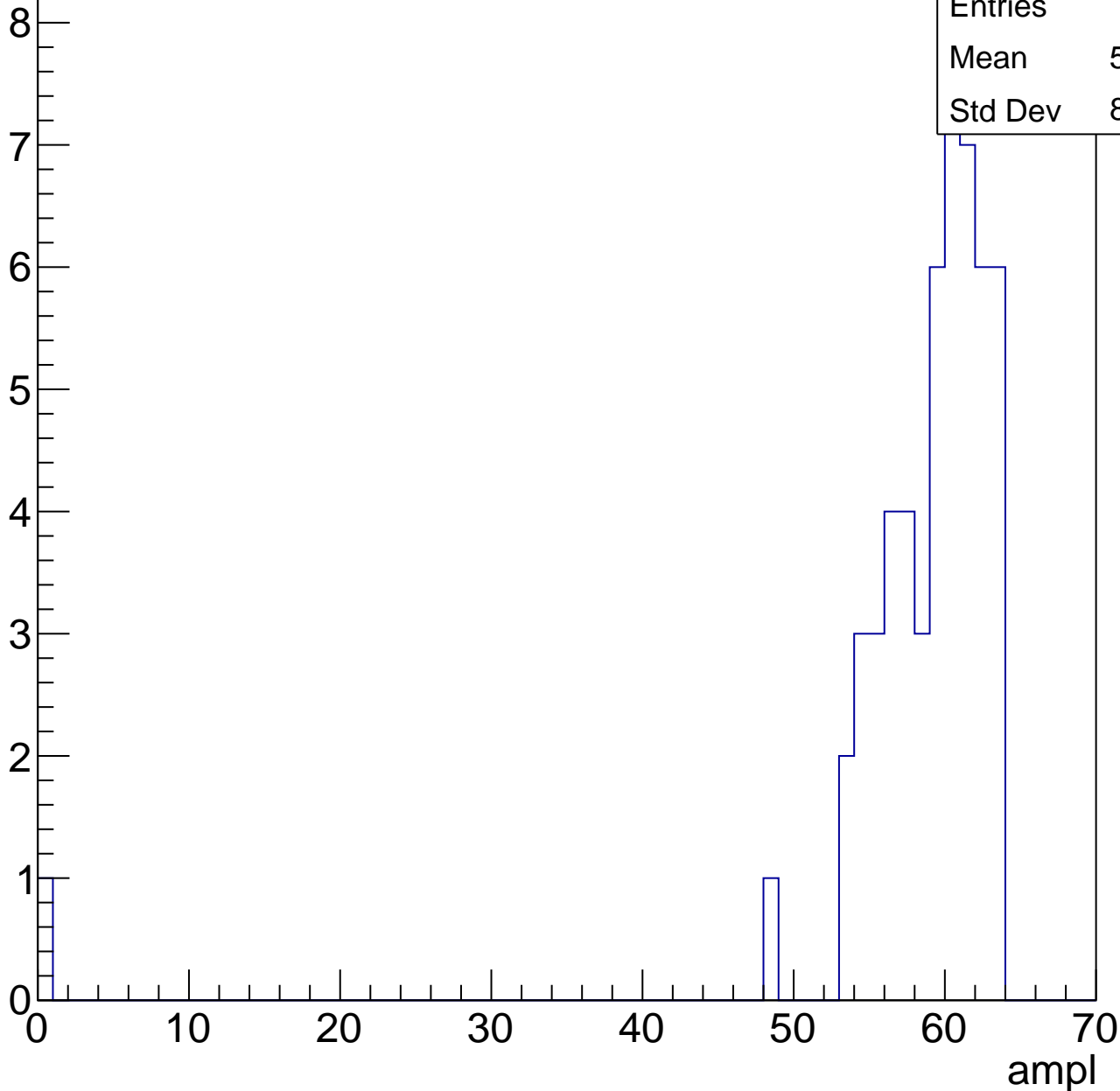


B1L103S, U13-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

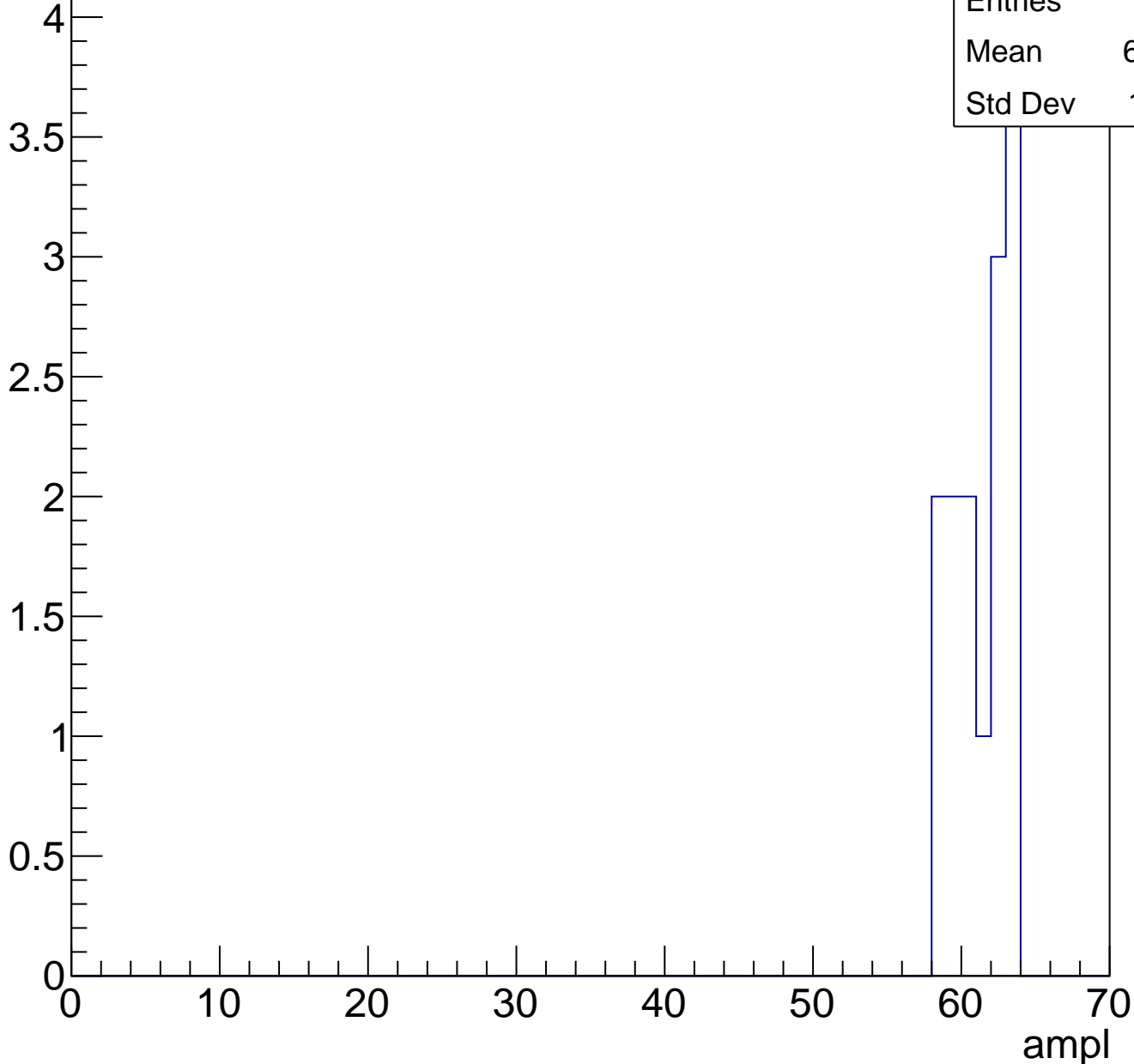
Entries	54
Mean	57.74
Std Dev	8.555



B1L103S, U13-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

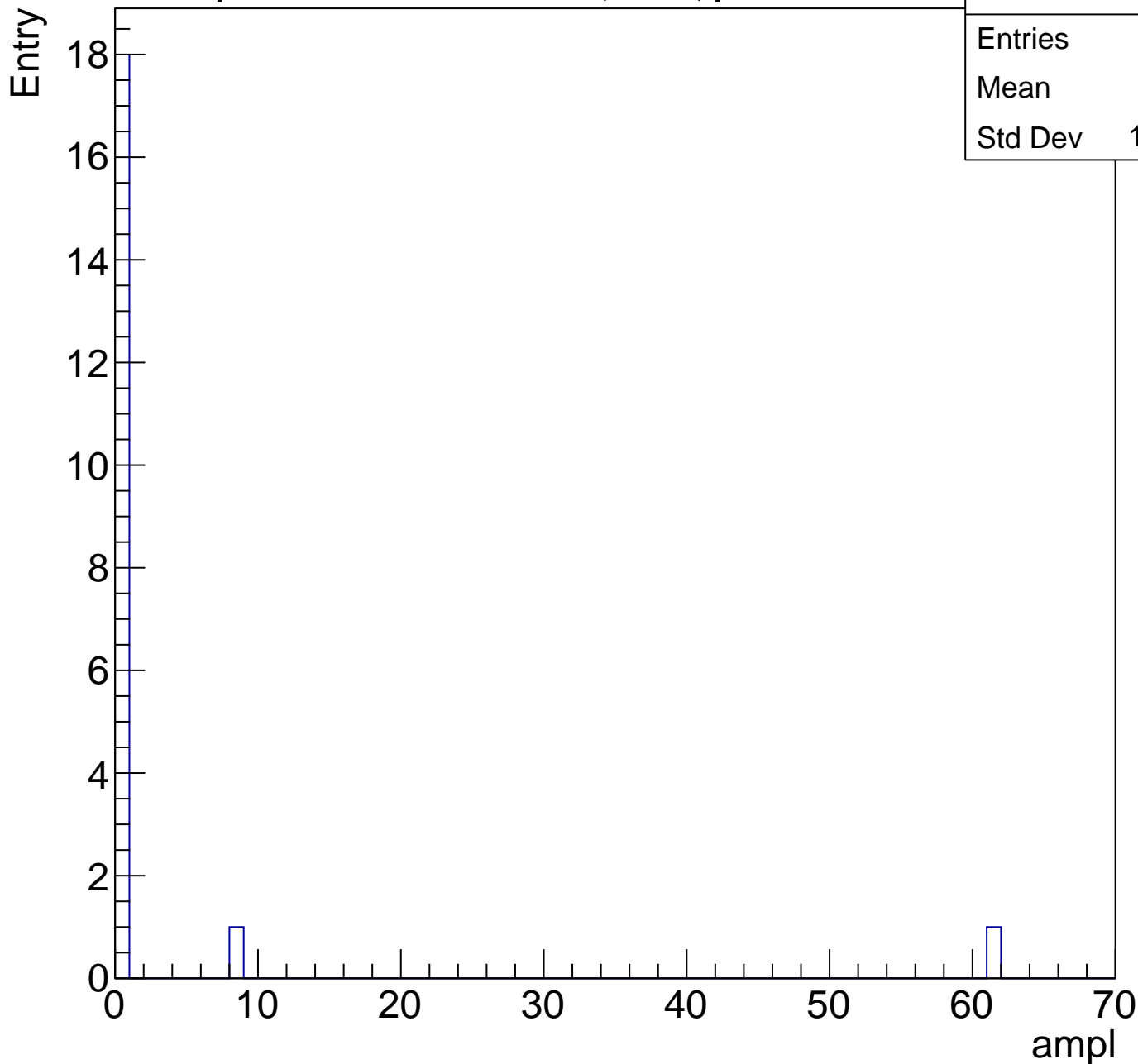
Entry



B1L103S, U13-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.45
Std Dev	13.32



B1L103S, U13-ch1, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	28.96
Std Dev	9.043

Gaus mean : 31.1433

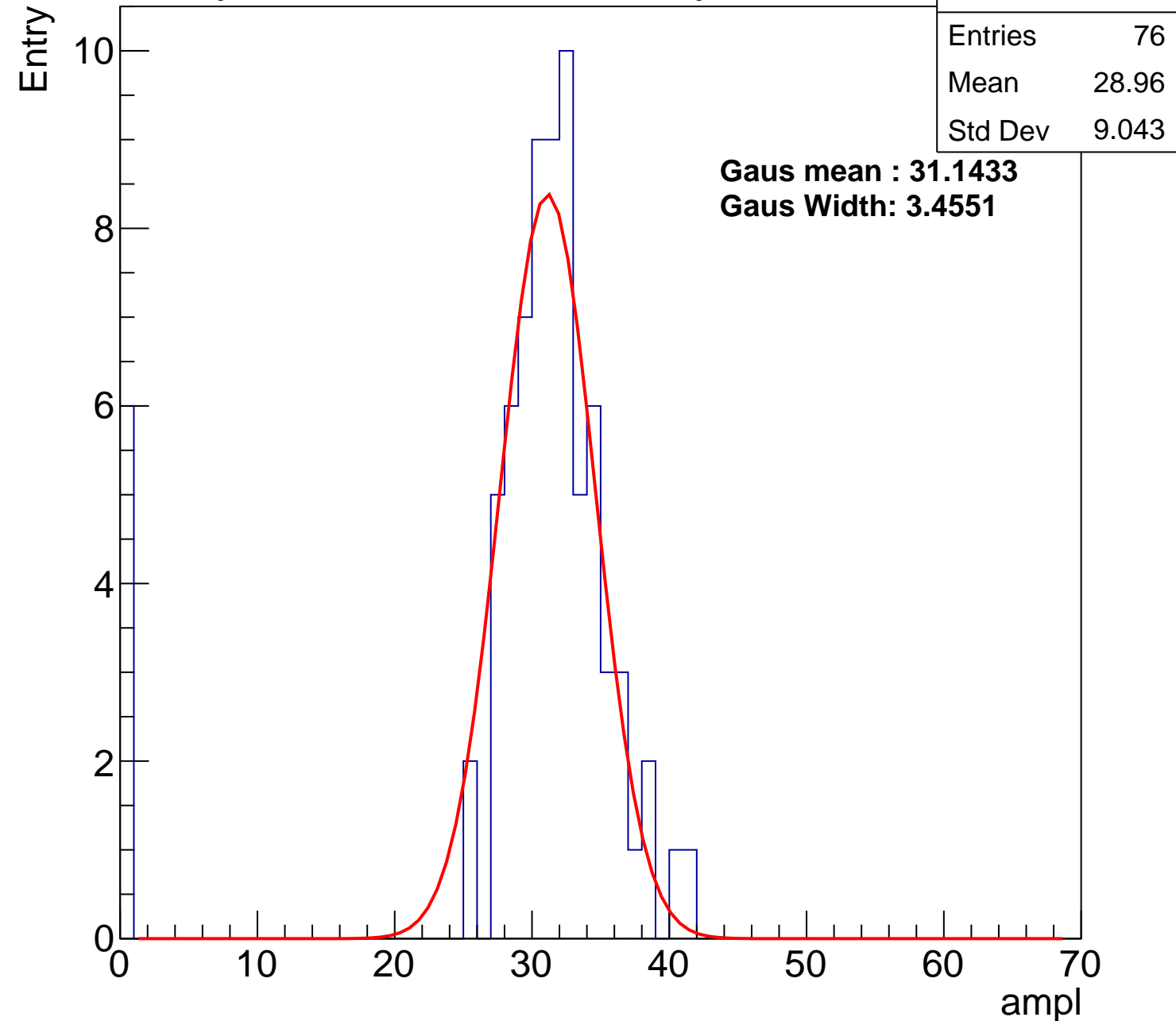
Gaus Width: 3.4551

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



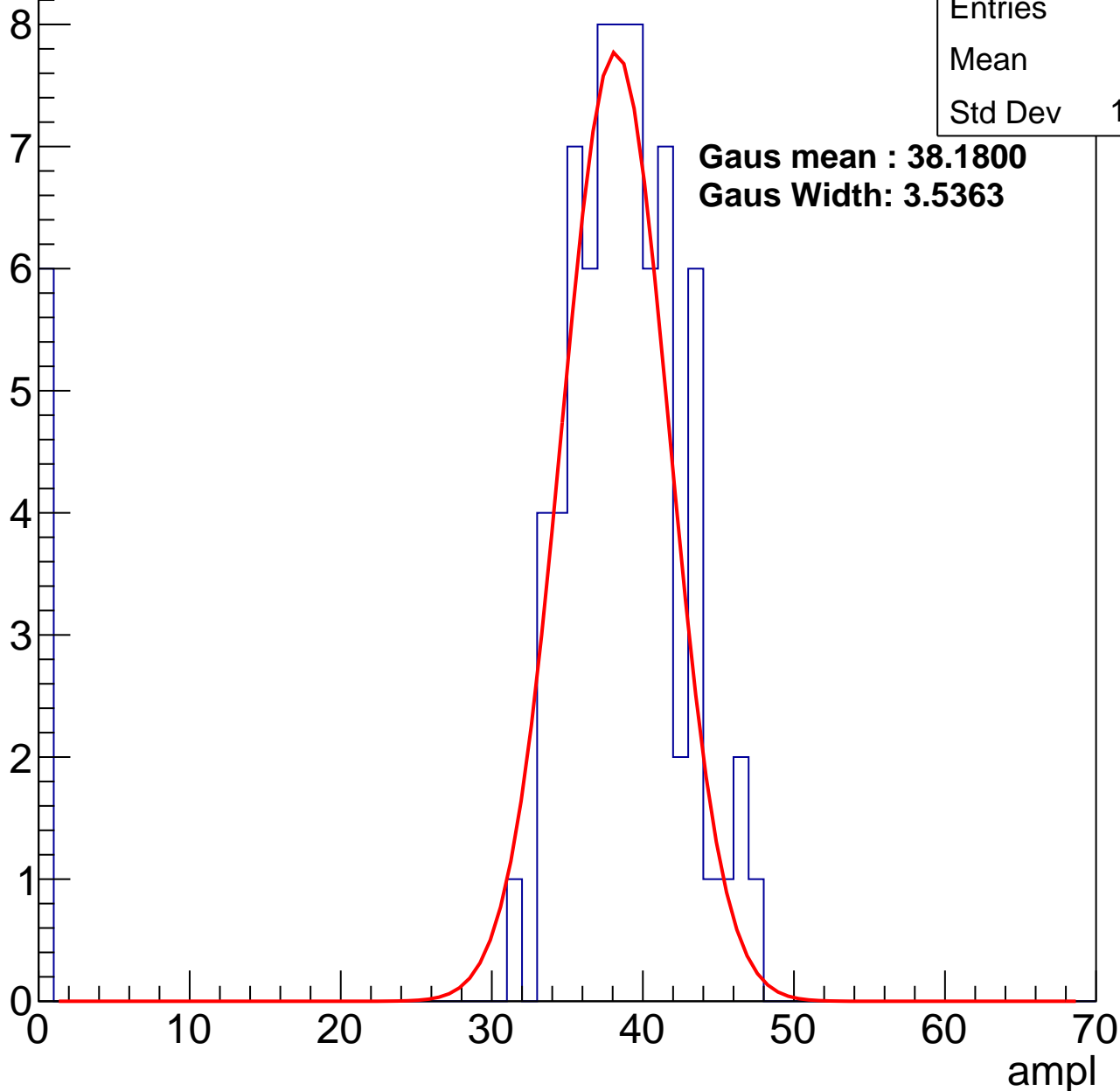
B1L103S, U13-ch1, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	35.5
Std Dev	10.77

Gaus mean : 38.1800
Gaus Width: 3.5363



B1L103S, U13-ch1, adc2

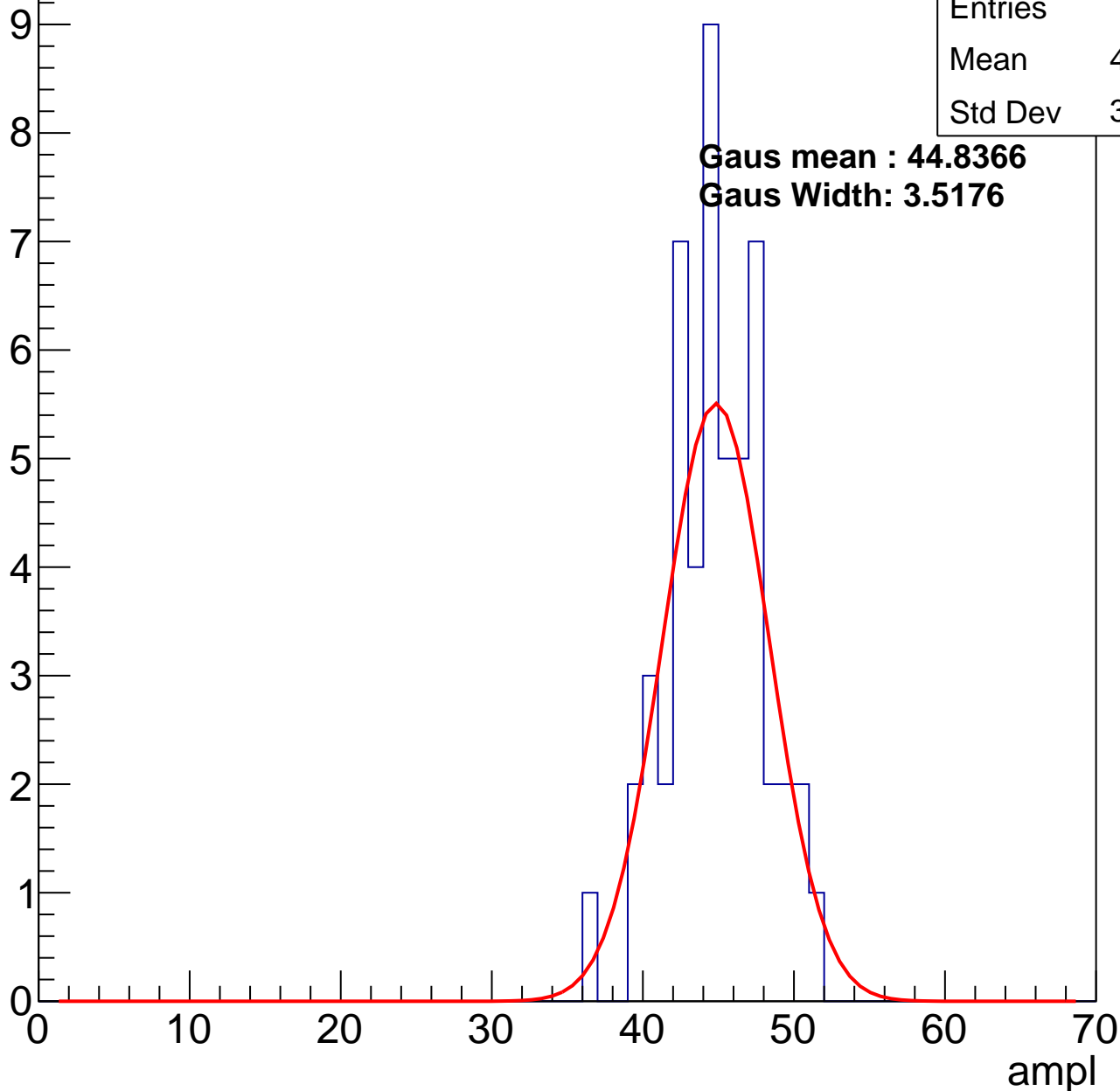
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	44.37
Std Dev	3.089

Gaus mean : 44.8366

Gaus Width: 3.5176

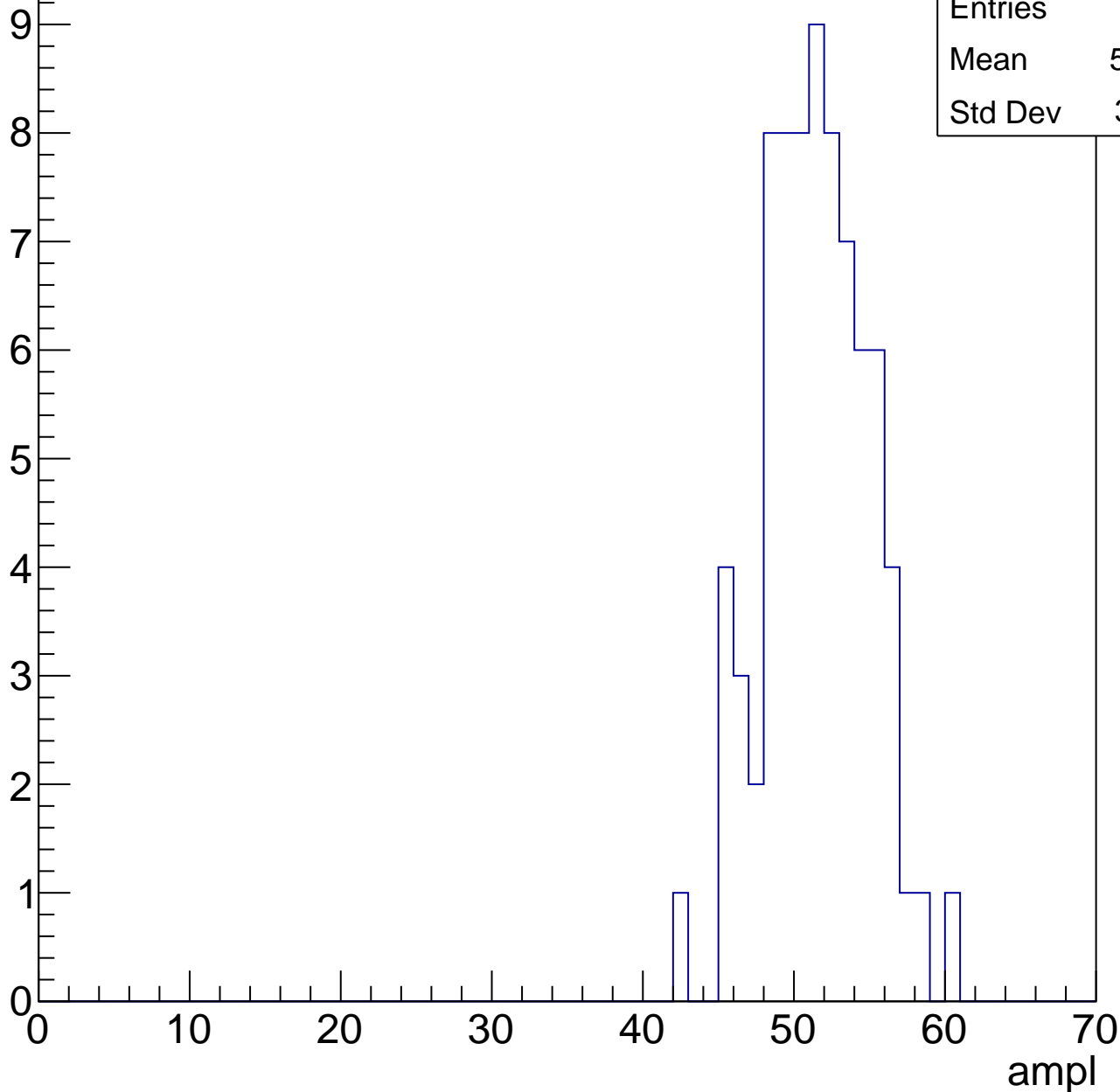


B1L103S, U13-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

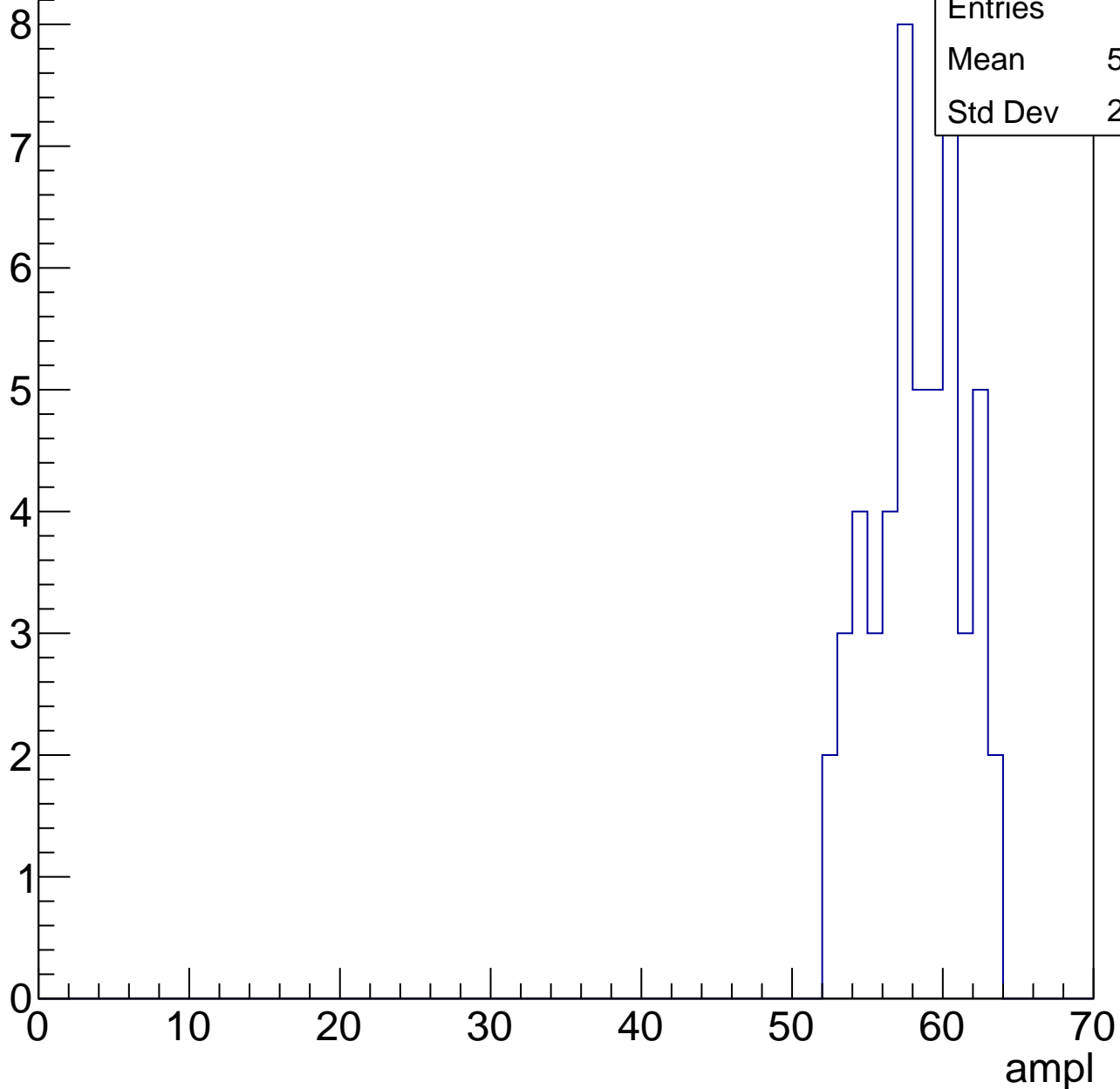
Entries	77
Mean	51.03
Std Dev	3.411



B1L103S, U13-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

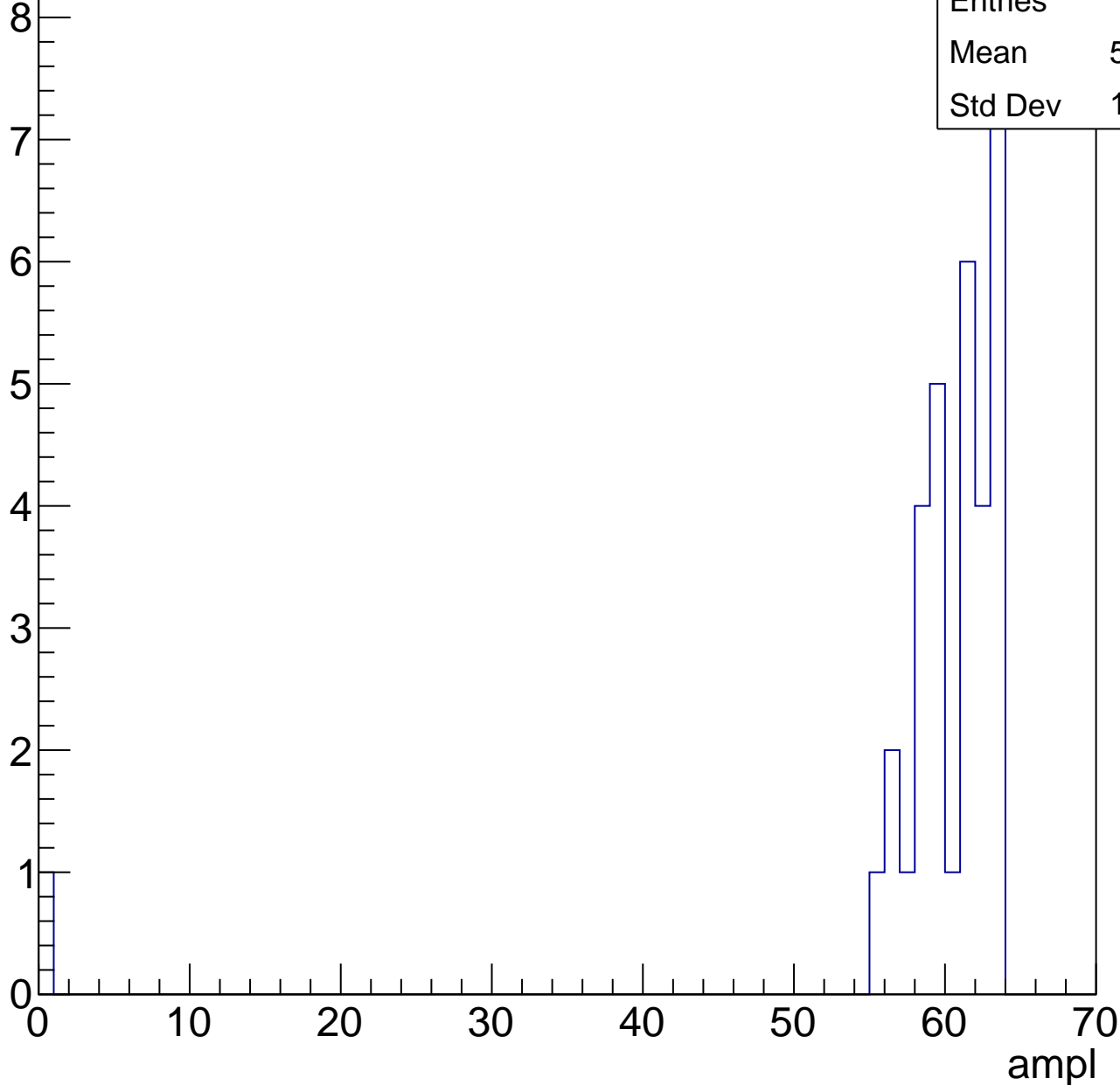


B1L103S, U13-ch1, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.45
Std Dev	10.59



B1L103S, U13-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

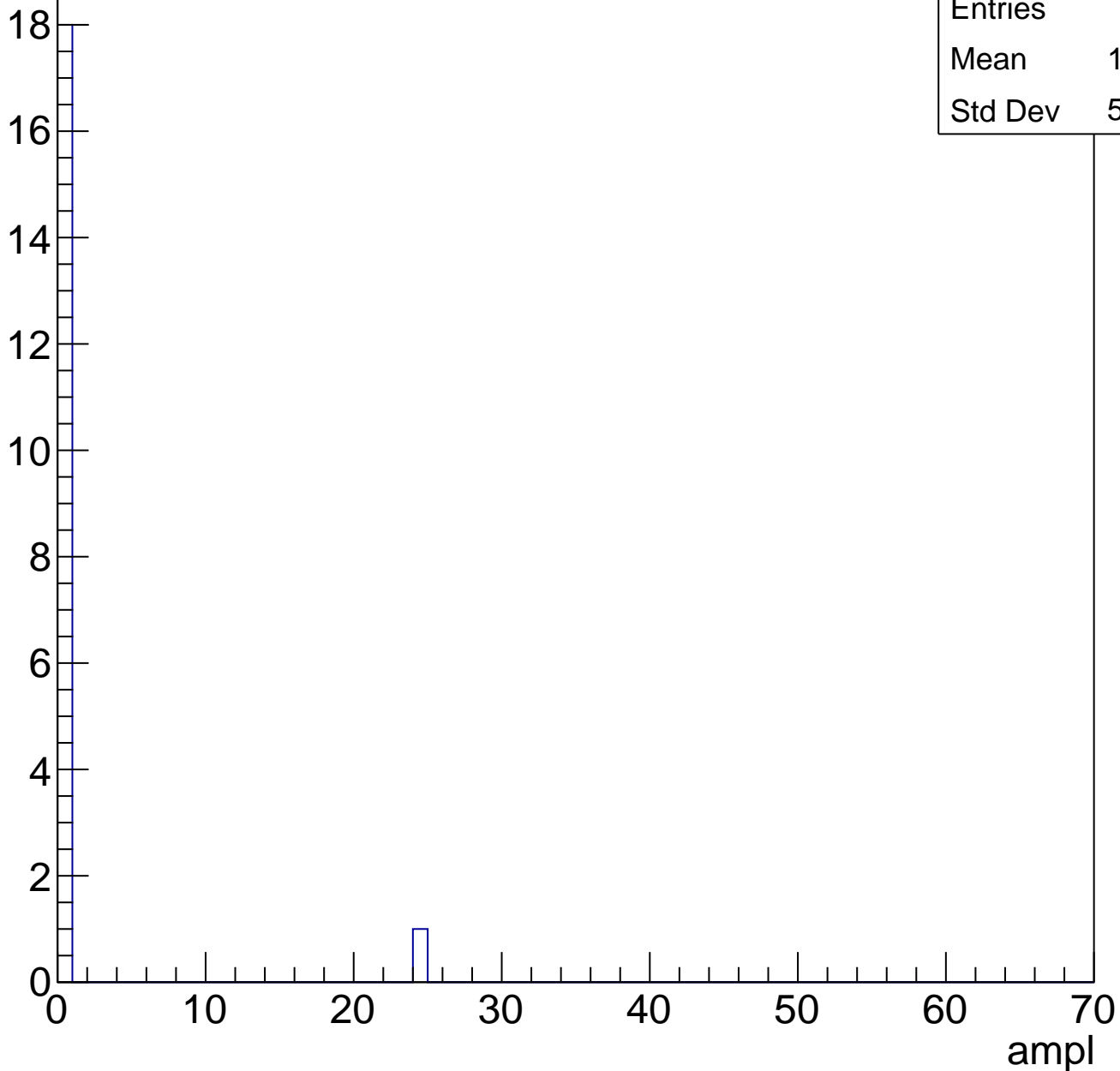


B1L103S, U13-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.263
Std Dev	5.359

Entry



B1L103S, U13-ch2, adc0

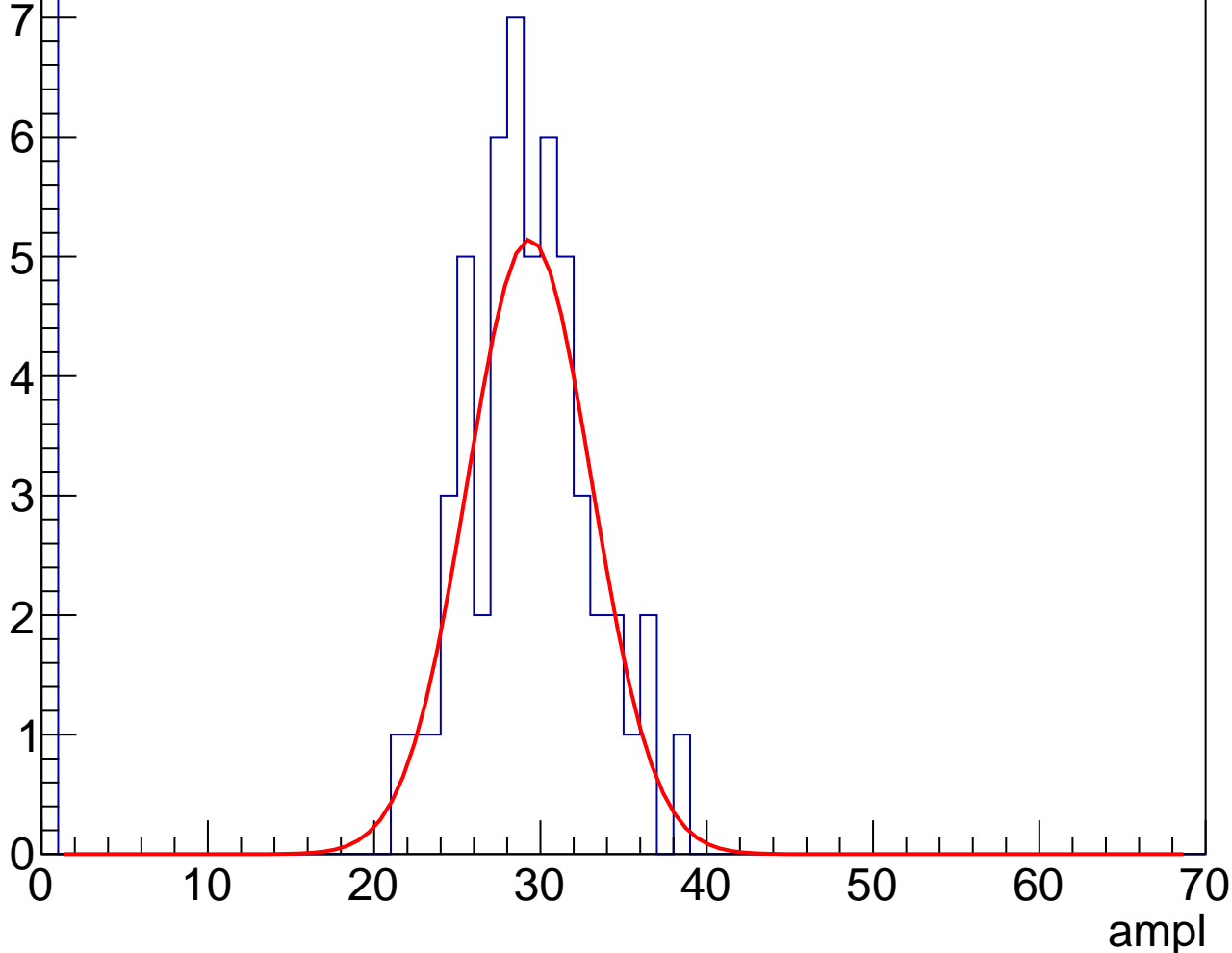
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	24.65
Std Dev	10.7

Gaus mean : 29.3452

Gaus Width: 3.7385



B1L103S, U13-ch2, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	30.85
Std Dev	11.8

Gaus mean : 35.3030

Gaus Width: 3.4781

Entry

10

8

6

4

2

0

0

10

20

30

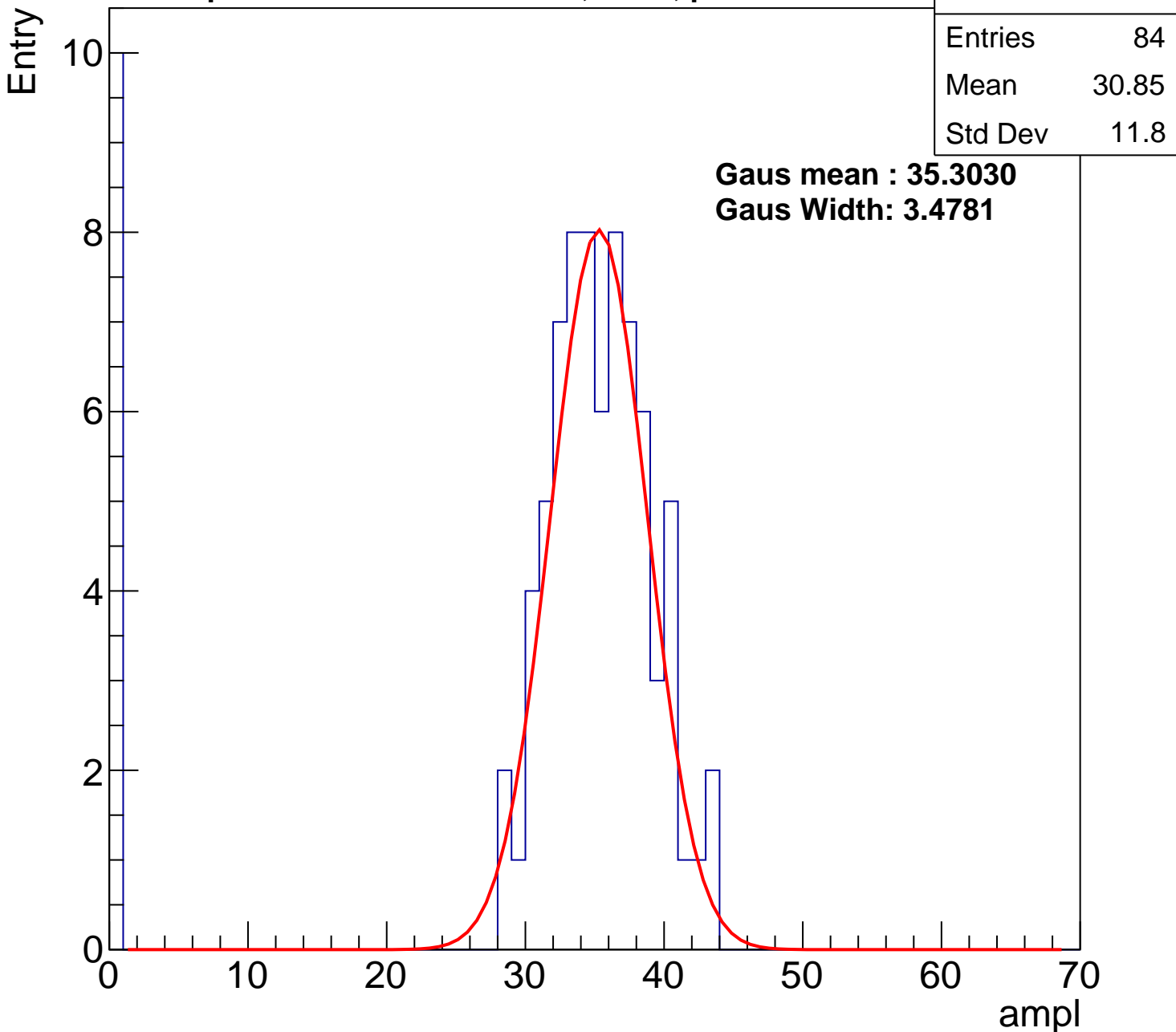
40

50

60

70

ampl



B1L103S, U13-ch2, adc2

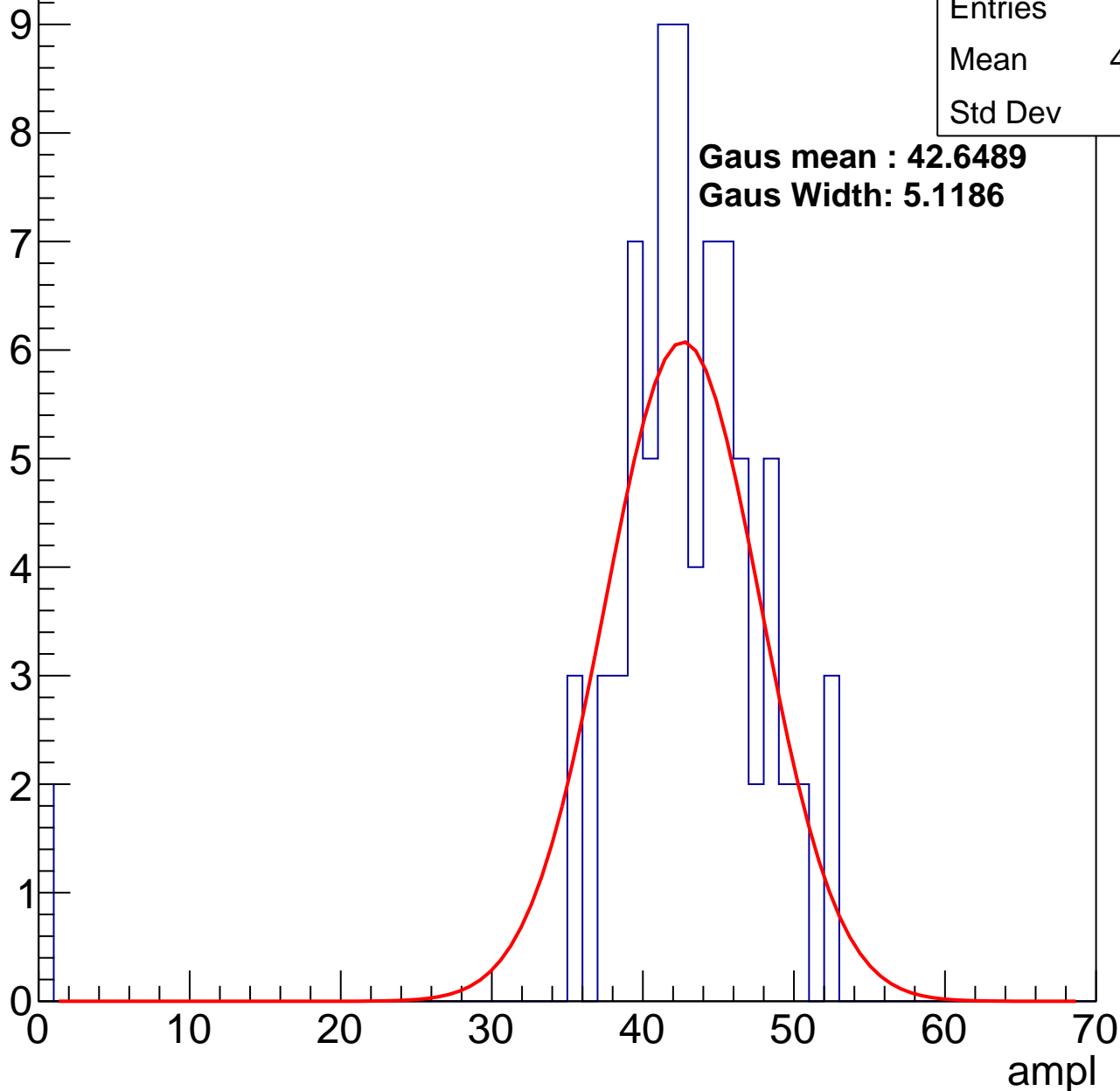
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	41.83
Std Dev	7.86

Gaus mean : 42.6489

Gaus Width: 5.1186

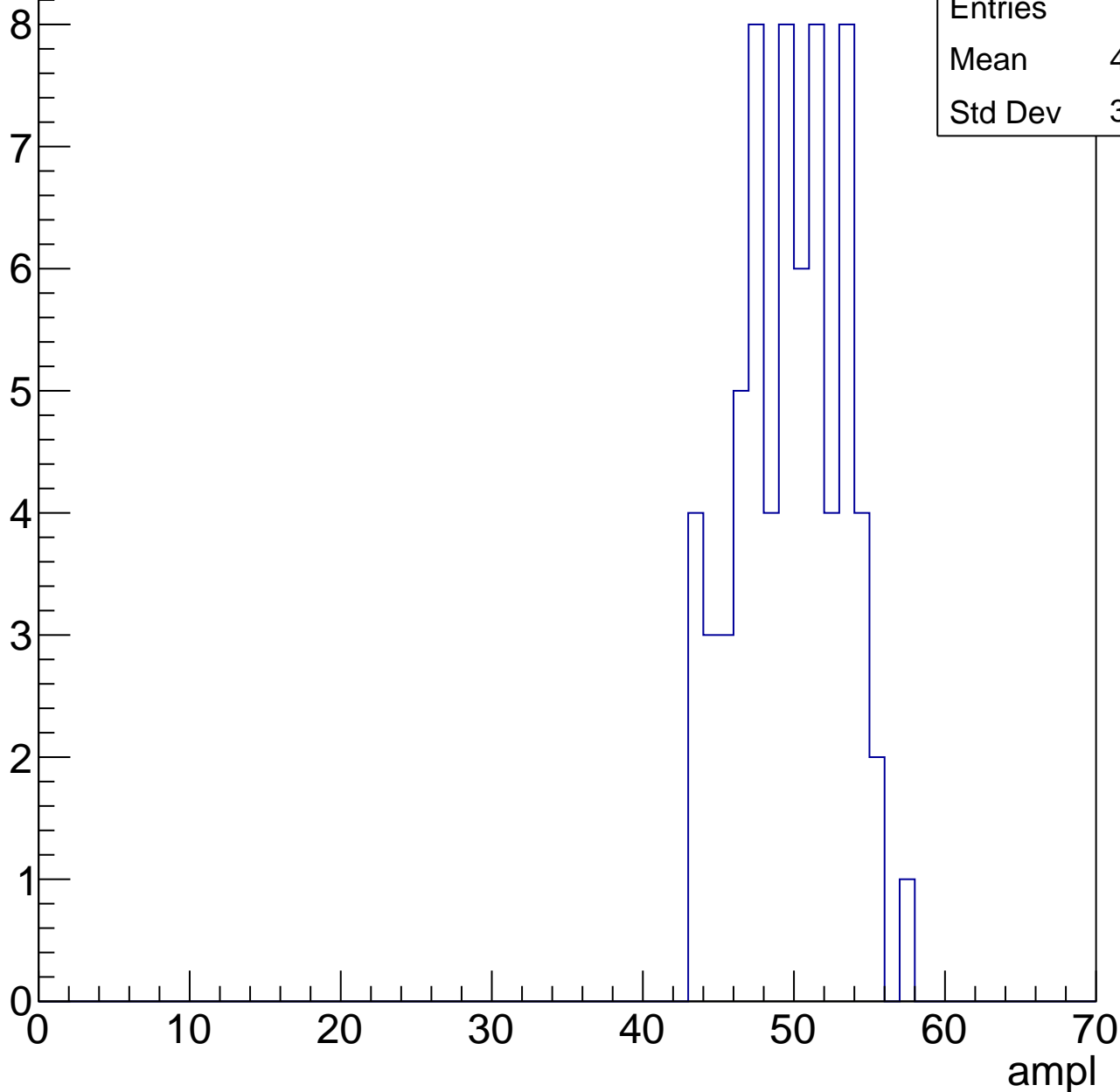


B1L103S, U13-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	49.29
Std Dev	3.387

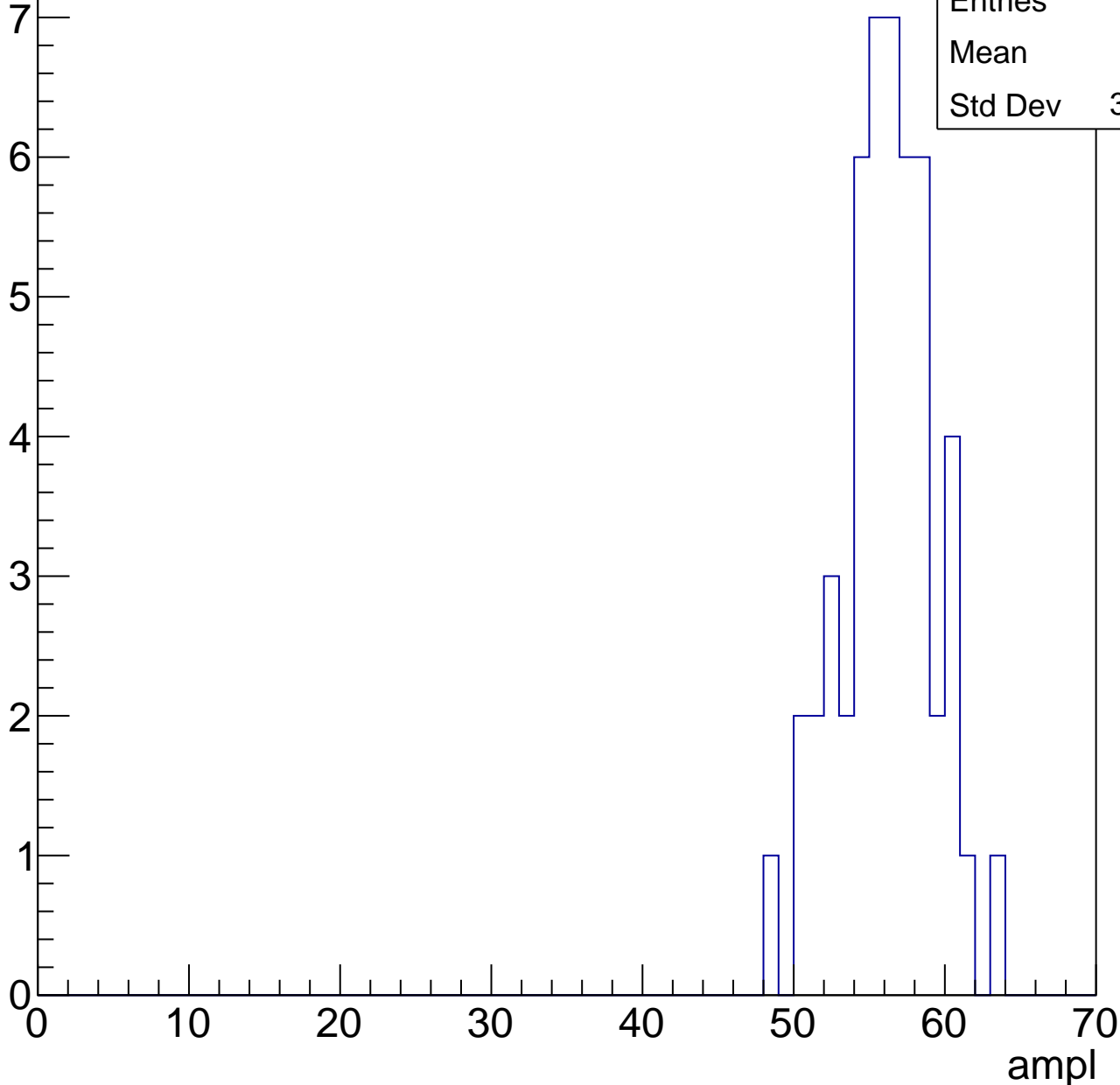


B1L103S, U13-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.7
Std Dev	3.048

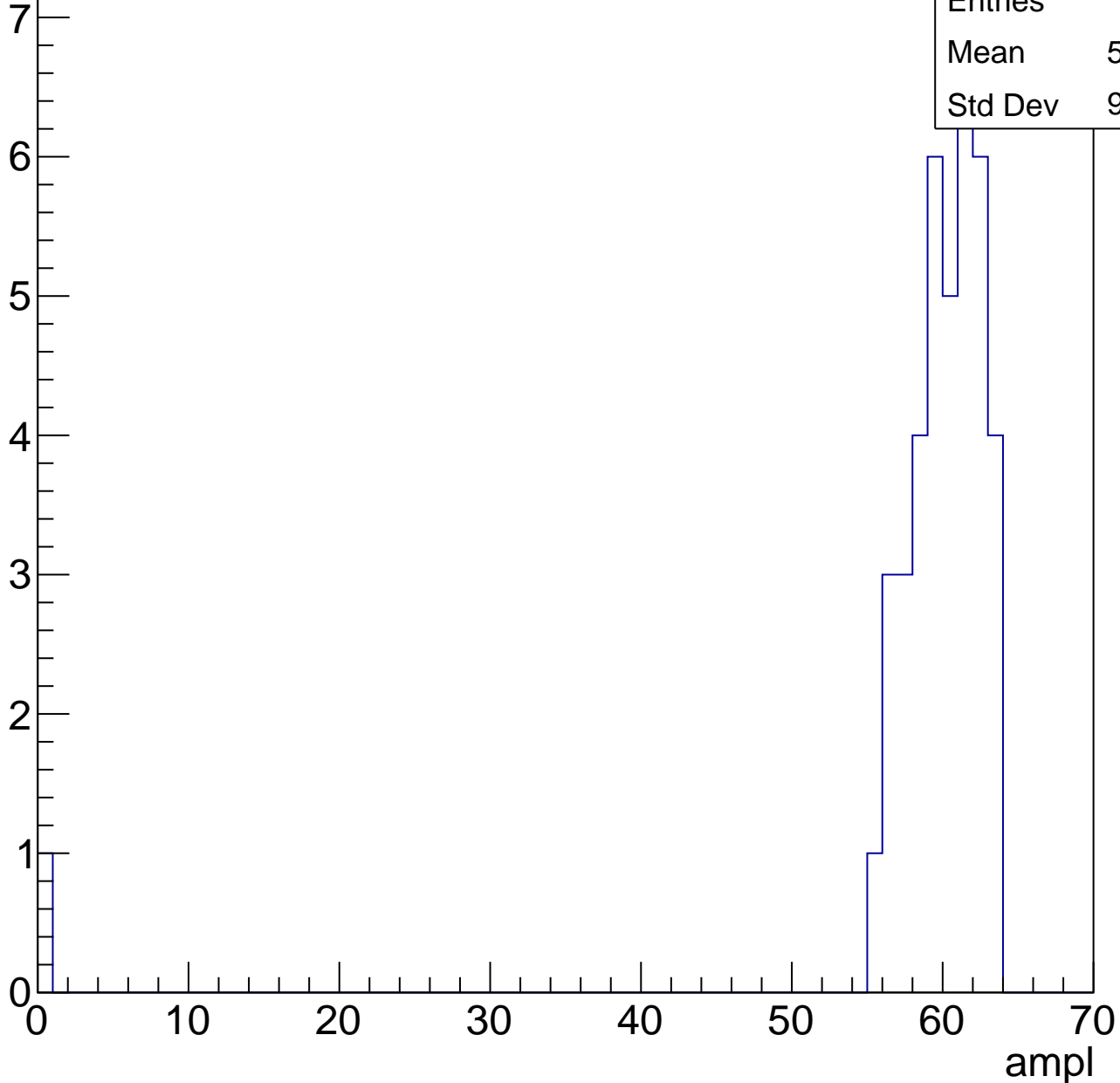


B1L103S, U13-ch2, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

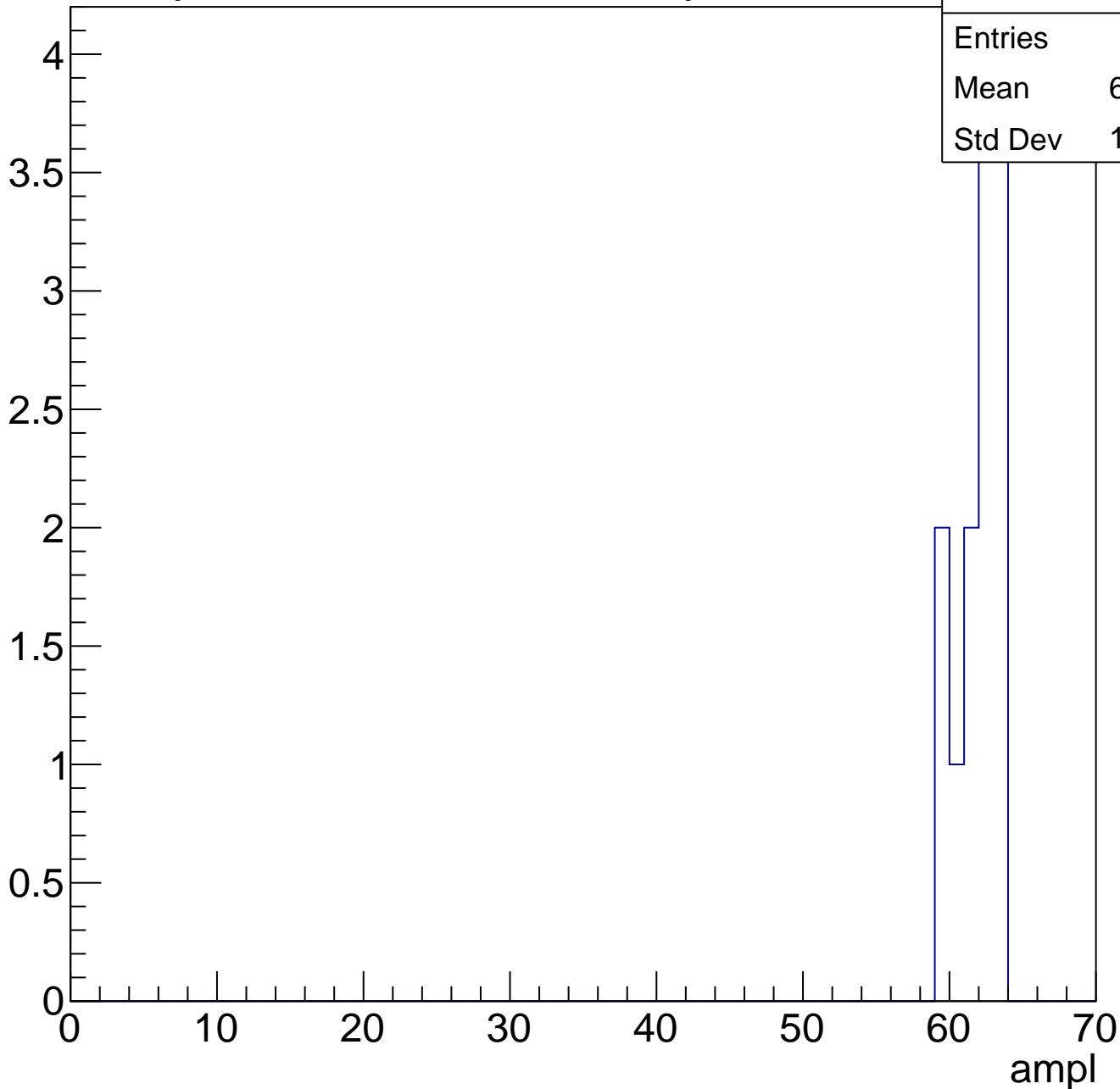
Entries	40
Mean	58.27
Std Dev	9.579



B1L103S, U13-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

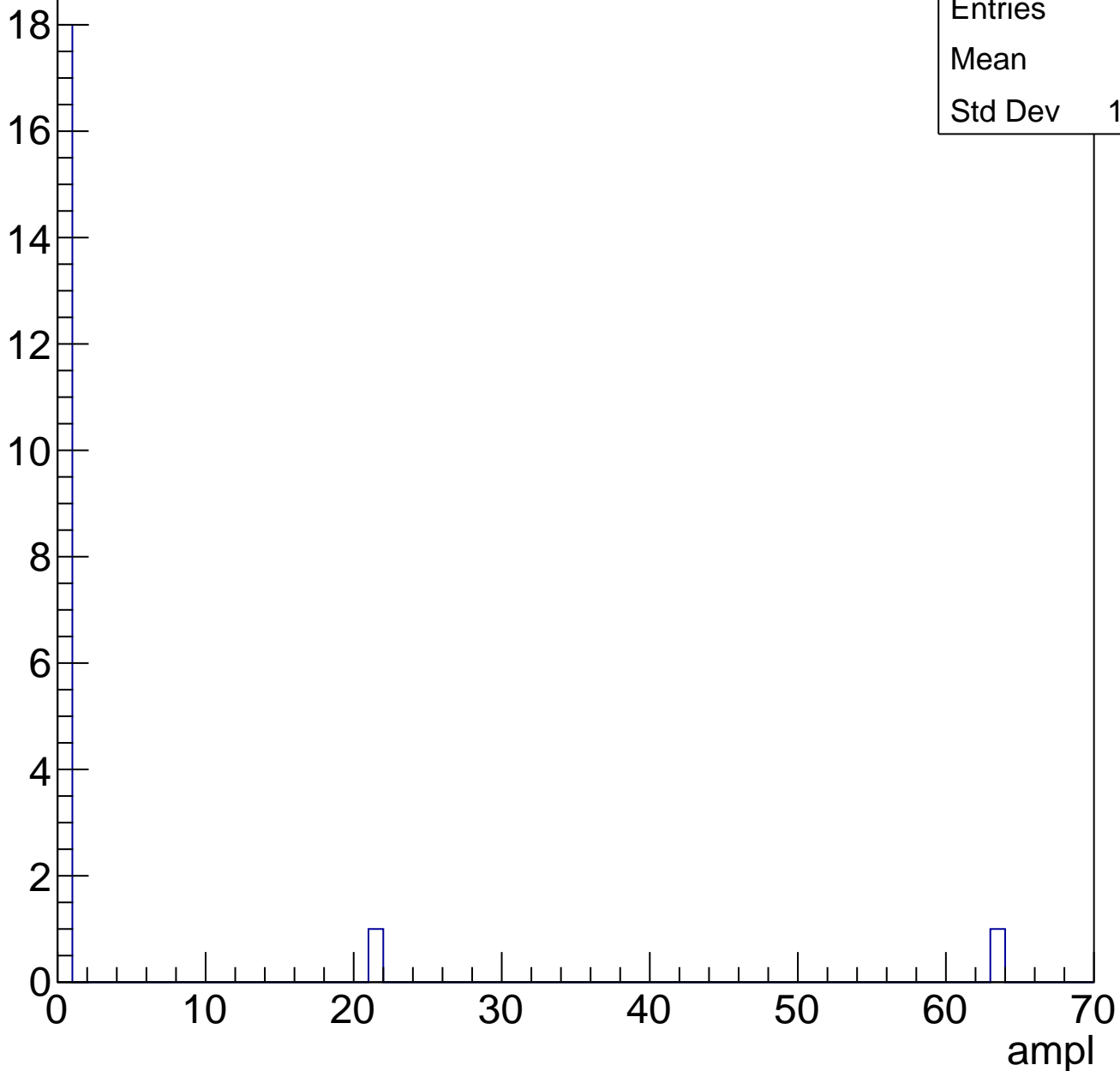


B1L103S, U13-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.2
Std Dev	14.24

Entry



B1L103S, U13-ch3, adc0

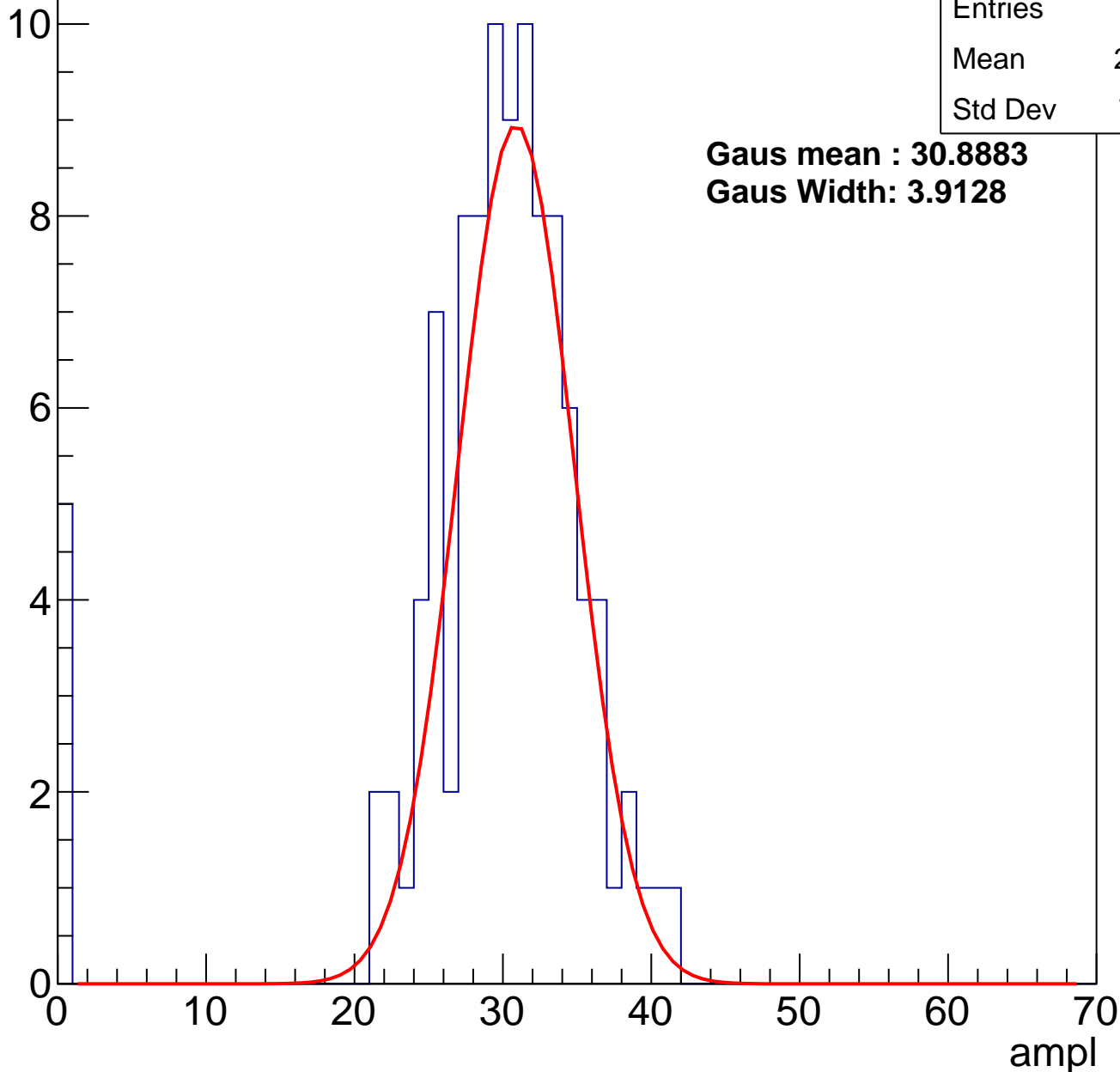
calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	28.68
Std Dev	7.631

Gaus mean : 30.8883

Gaus Width: 3.9128

Entry



B1L103S, U13-ch3, adc1

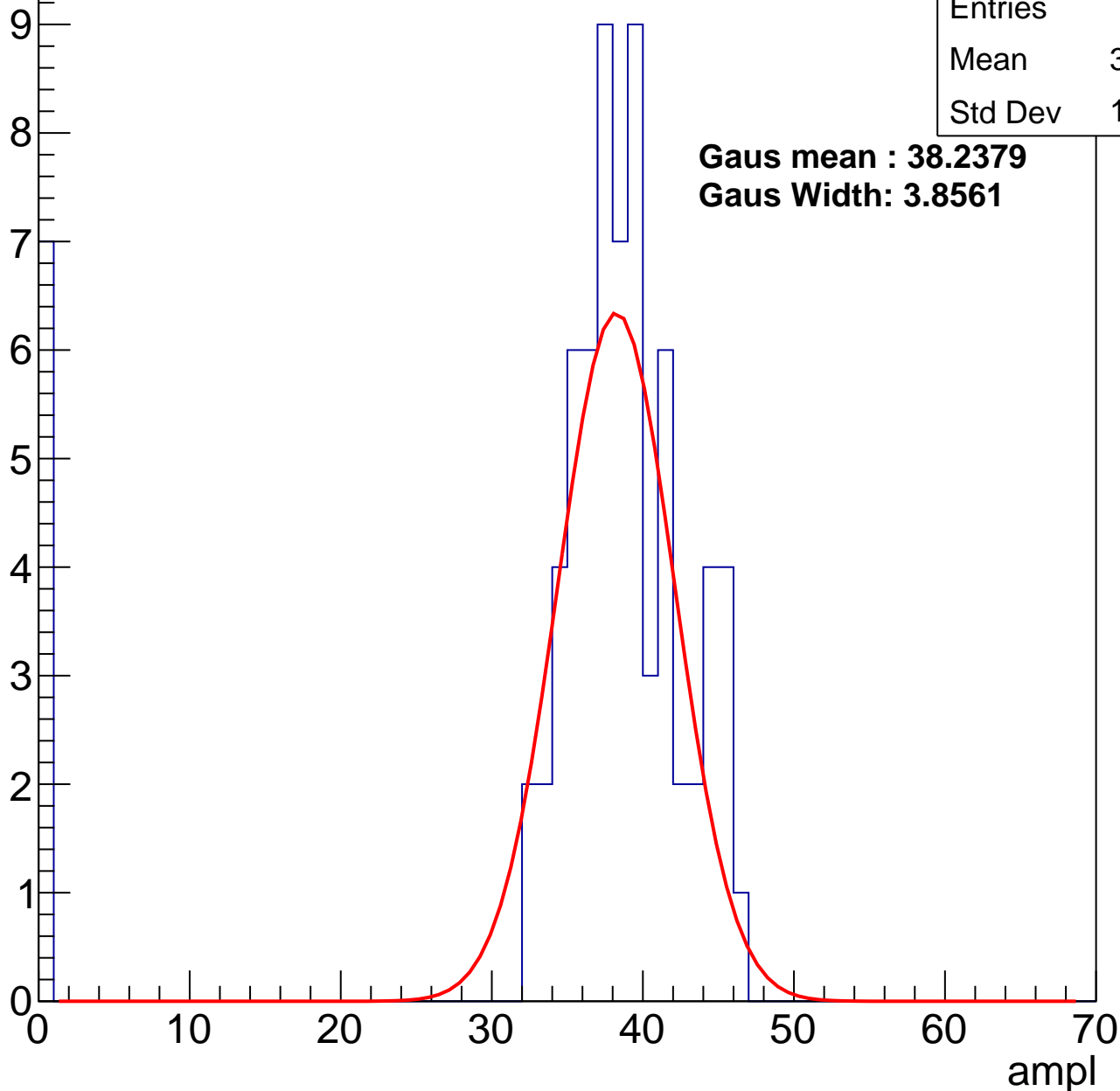
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	34.86
Std Dev	11.75

Gaus mean : 38.2379

Gaus Width: 3.8561



B1L103S, U13-ch3, adc2

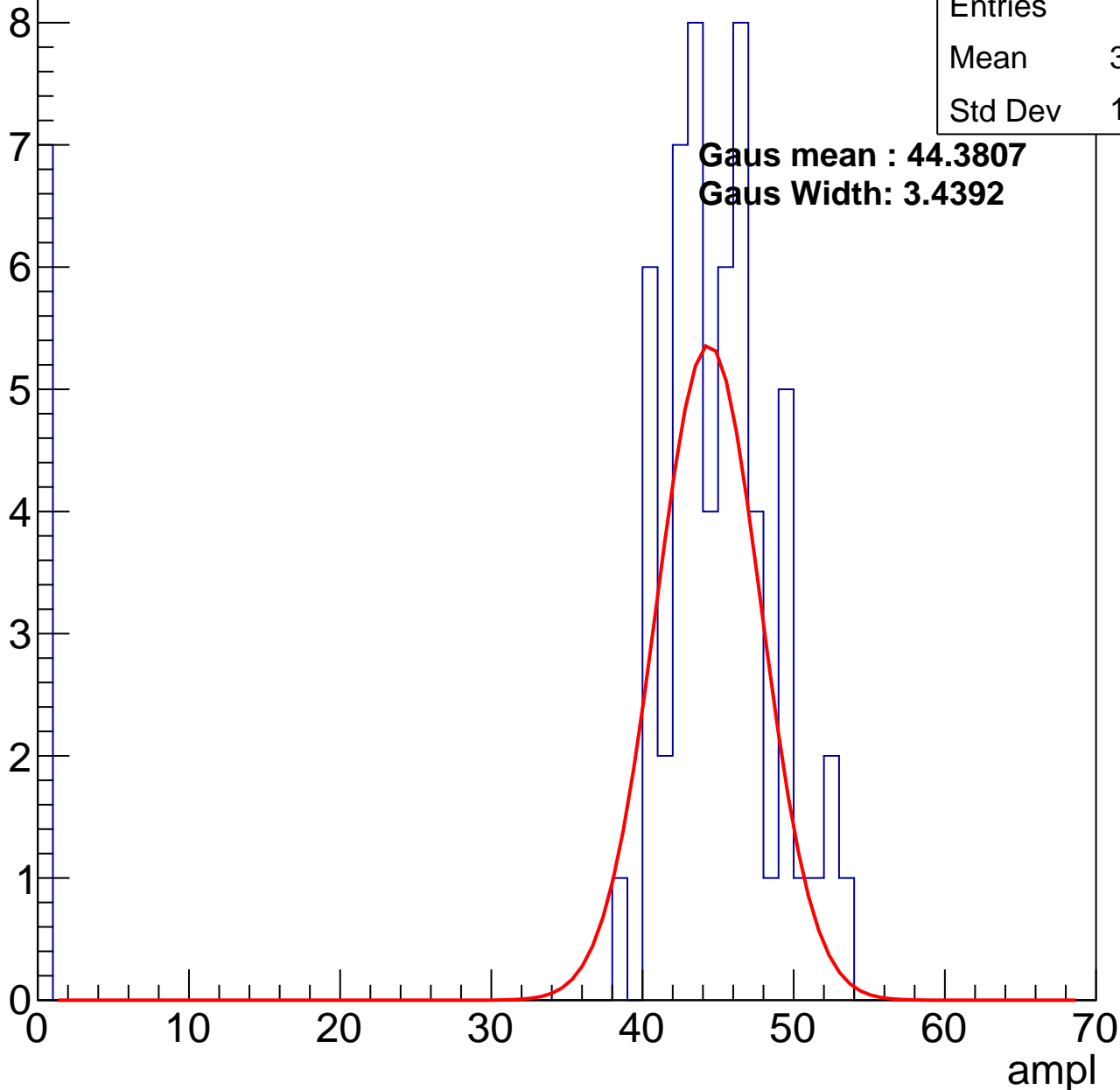
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	39.86
Std Dev	14.33

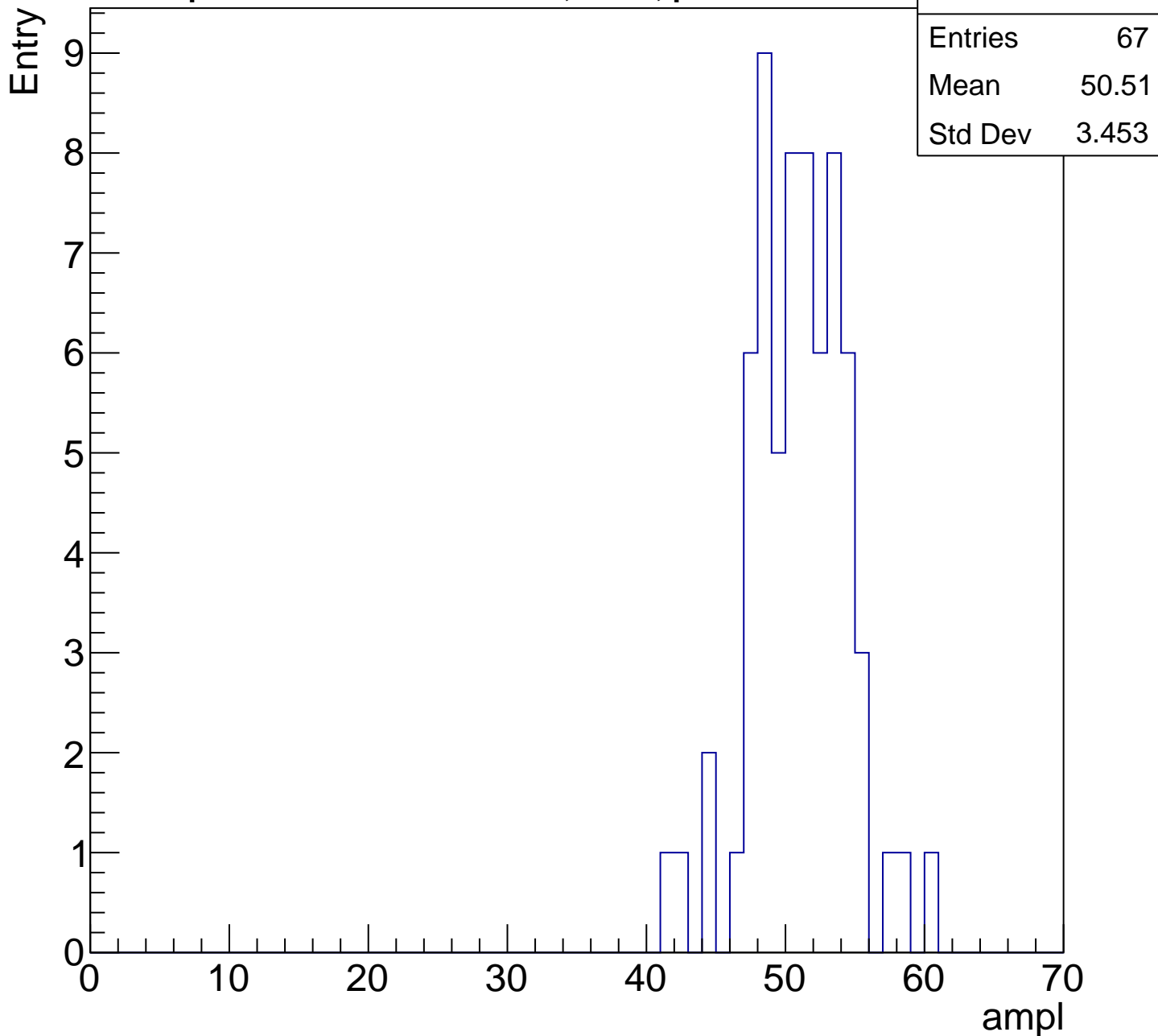
Gaus mean : 44.3807

Gaus Width: 3.4392



B1L103S, U13-ch3, adc3

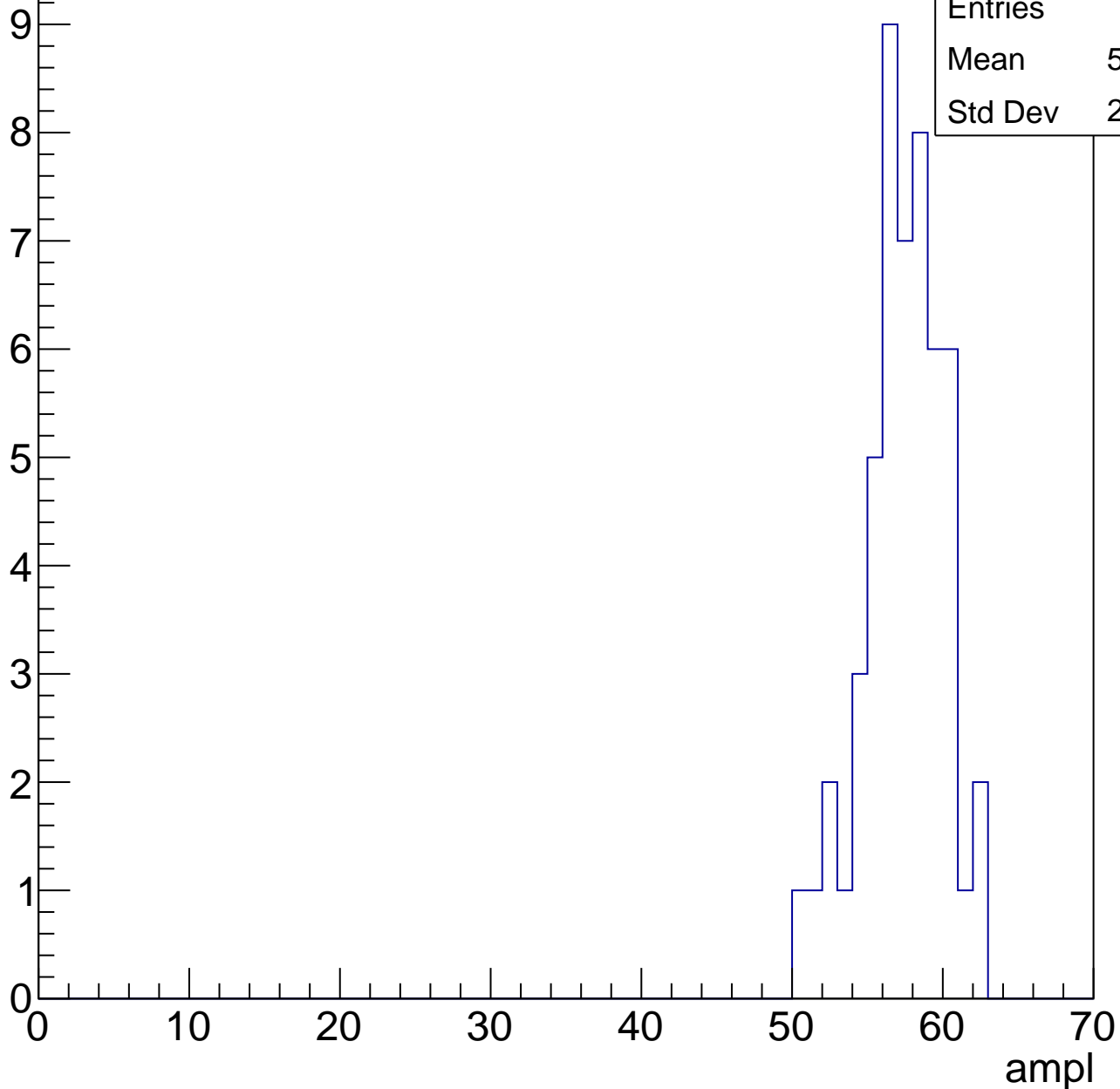
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U13-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



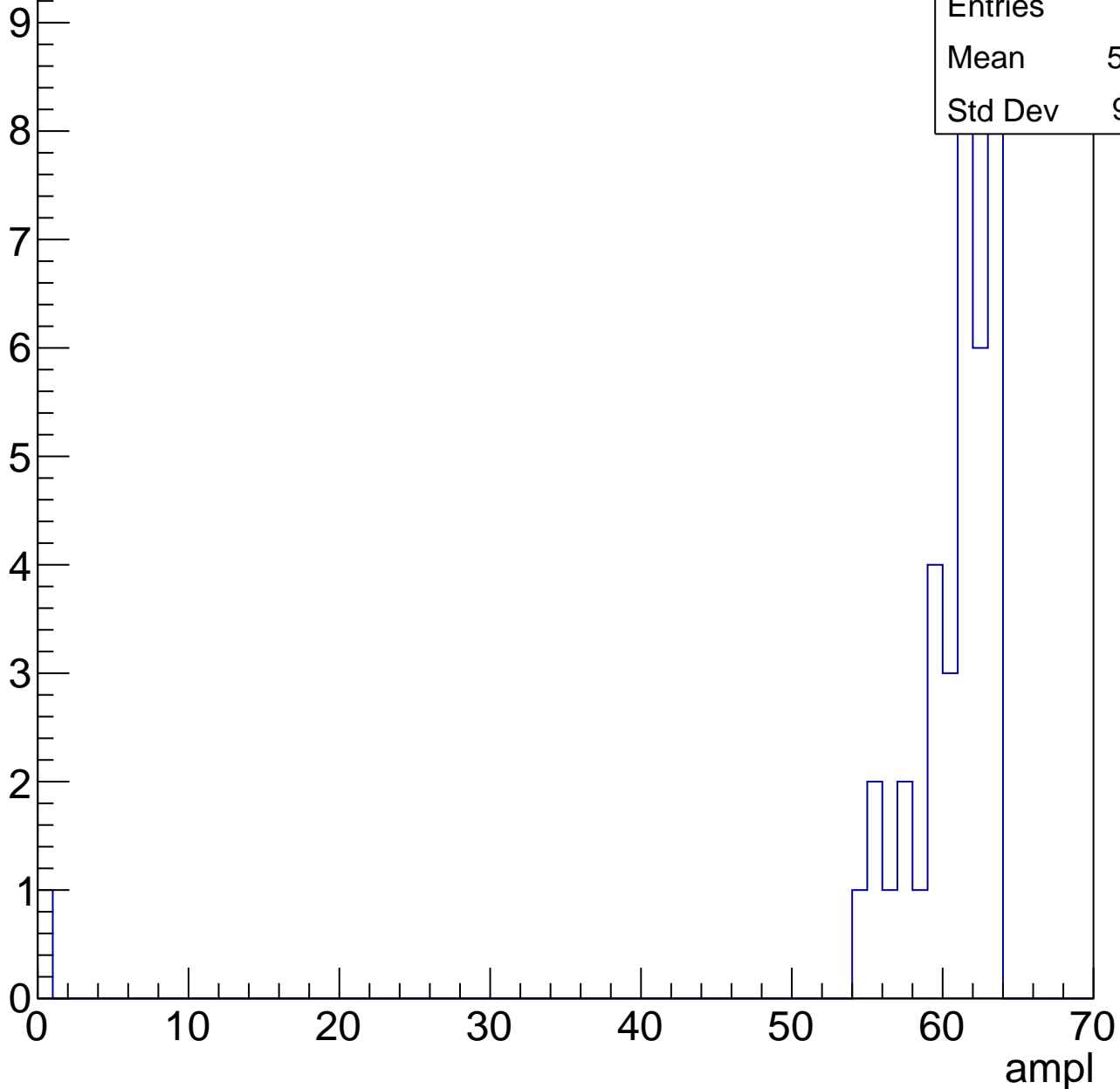
Entries	52
Mean	56.94
Std Dev	2.627

B1L103S, U13-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	58.87
Std Dev	9.861



B1L103S, U13-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch4, adc0

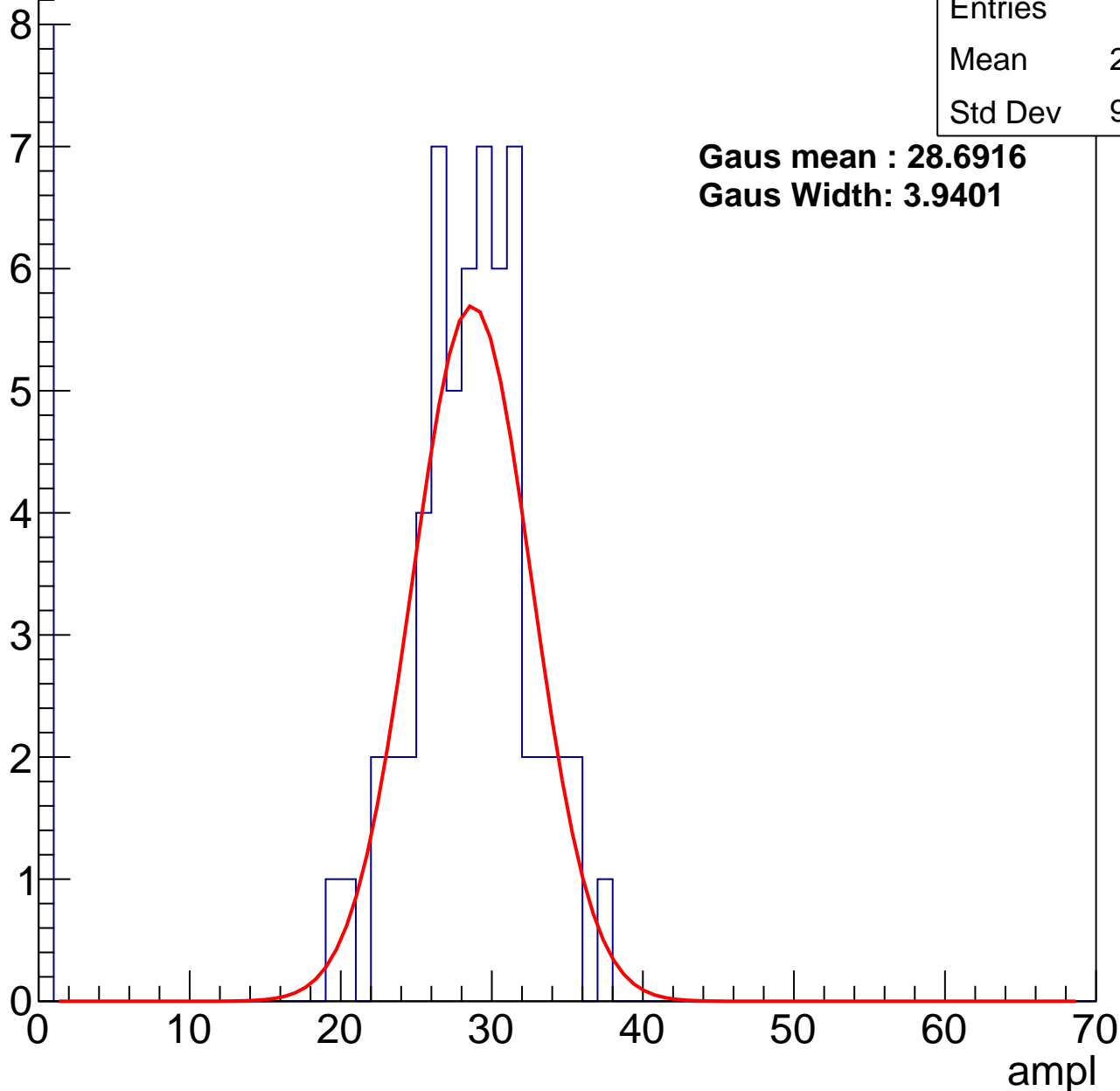
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	24.88
Std Dev	9.785

Gaus mean : 28.6916

Gaus Width: 3.9401



B1L103S, U13-ch4, adc1

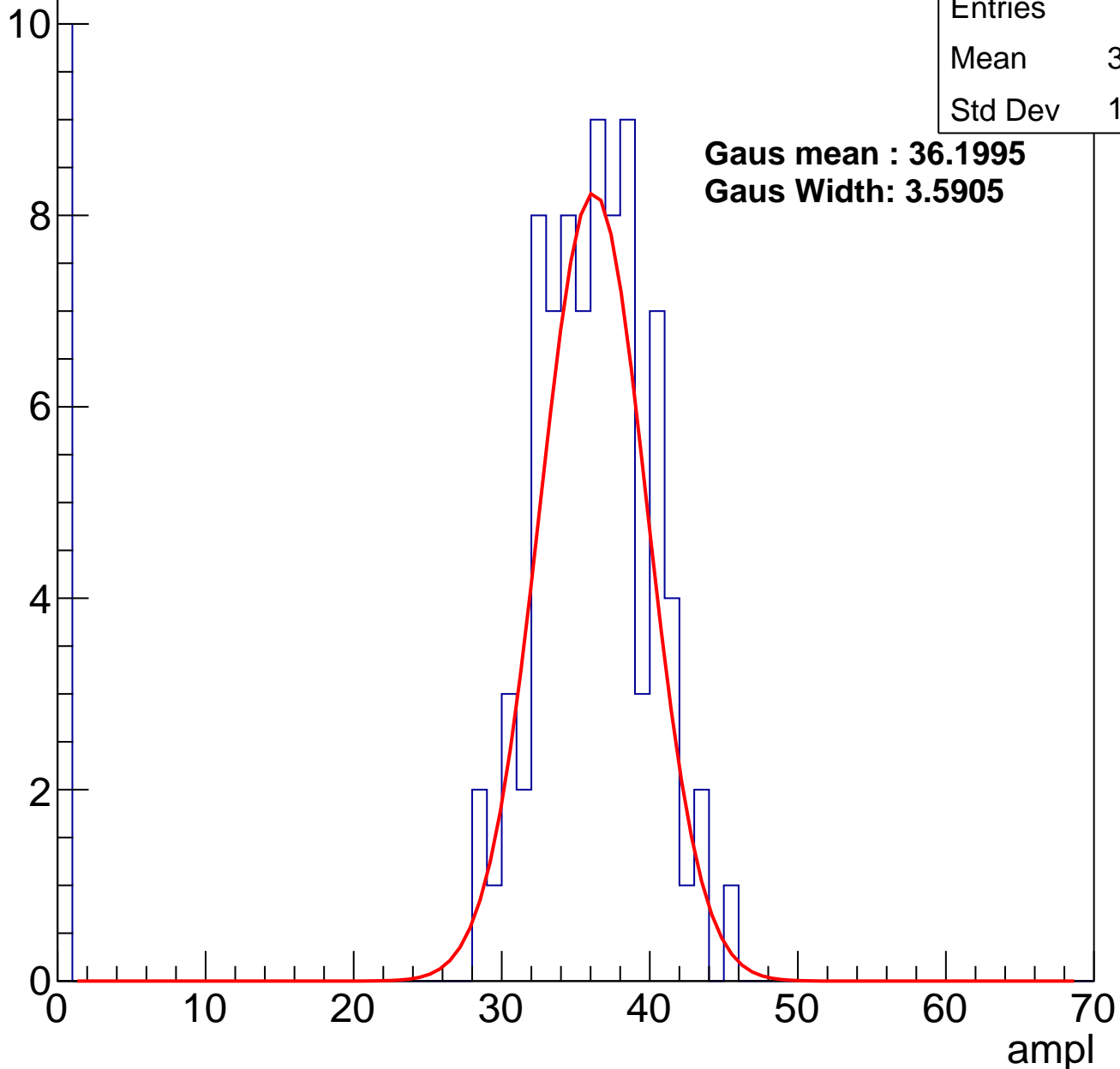
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	31.92
Std Dev	11.65

Gaus mean : 36.1995

Gaus Width: 3.5905

Entry



B1L103S, U13-ch4, adc2

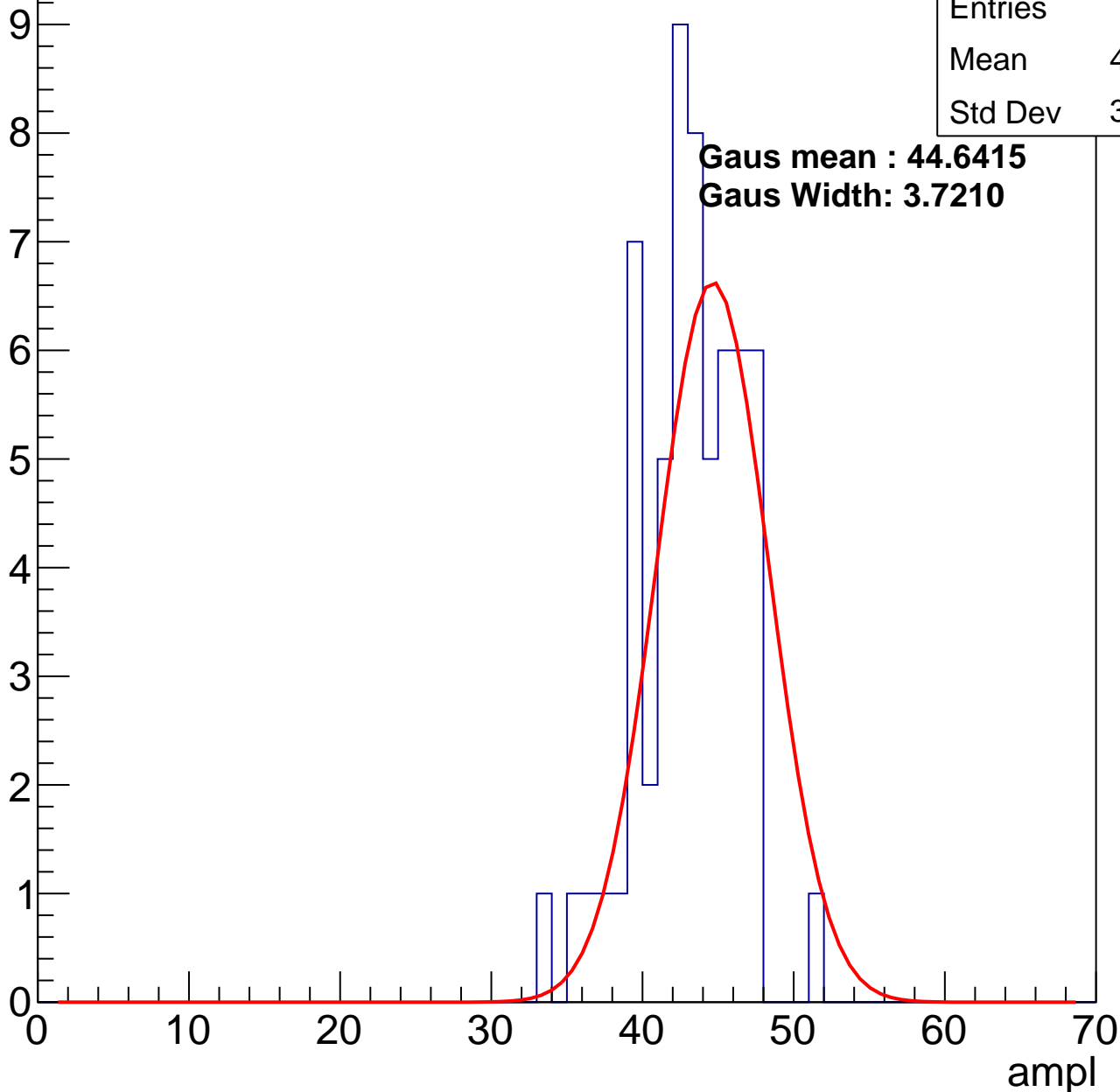
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	42.63
Std Dev	3.336

Gaus mean : 44.6415

Gaus Width: 3.7210



B1L103S, U13-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

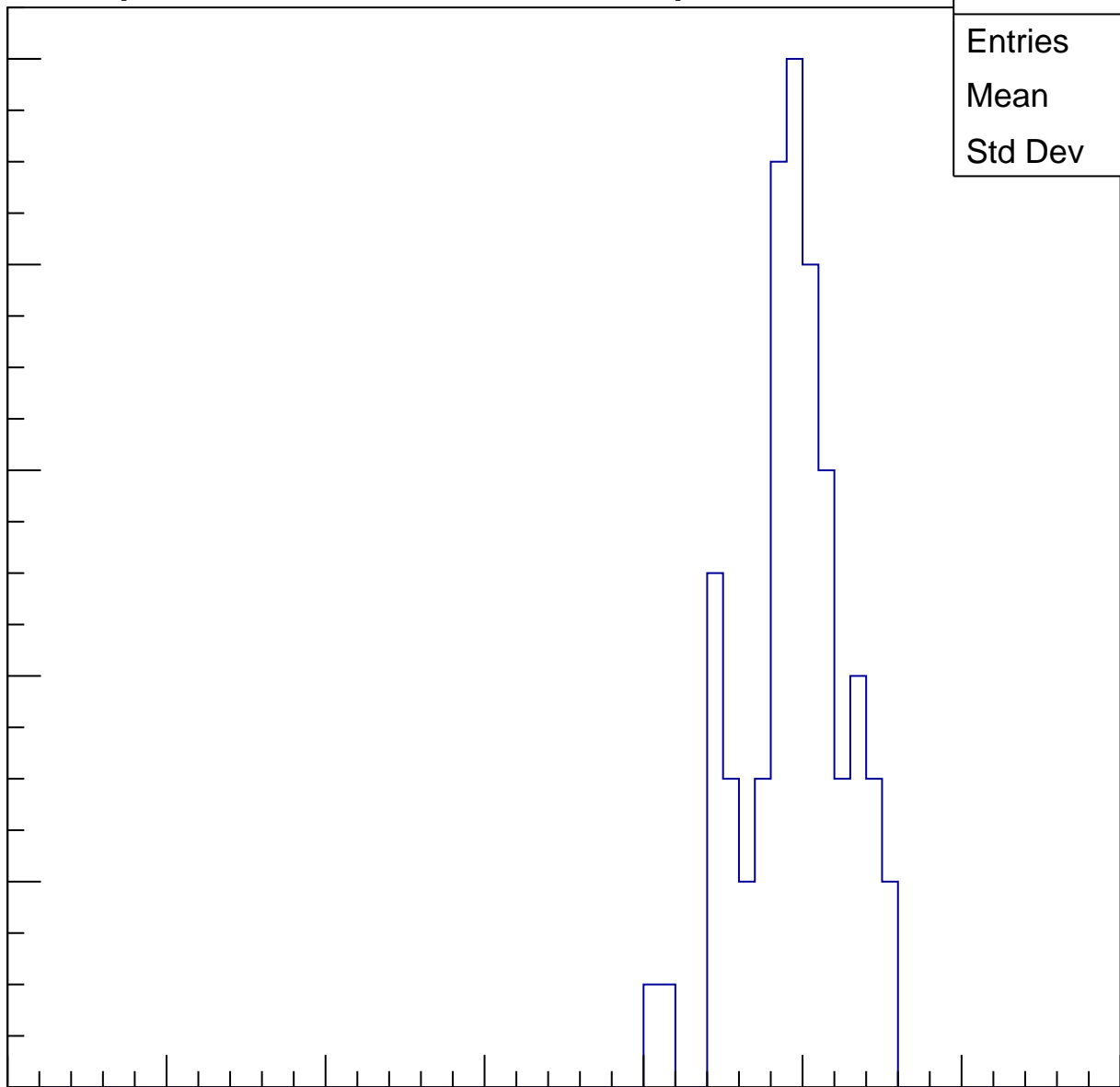
Entries	60
Mean	48.95
Std Dev	3.232

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

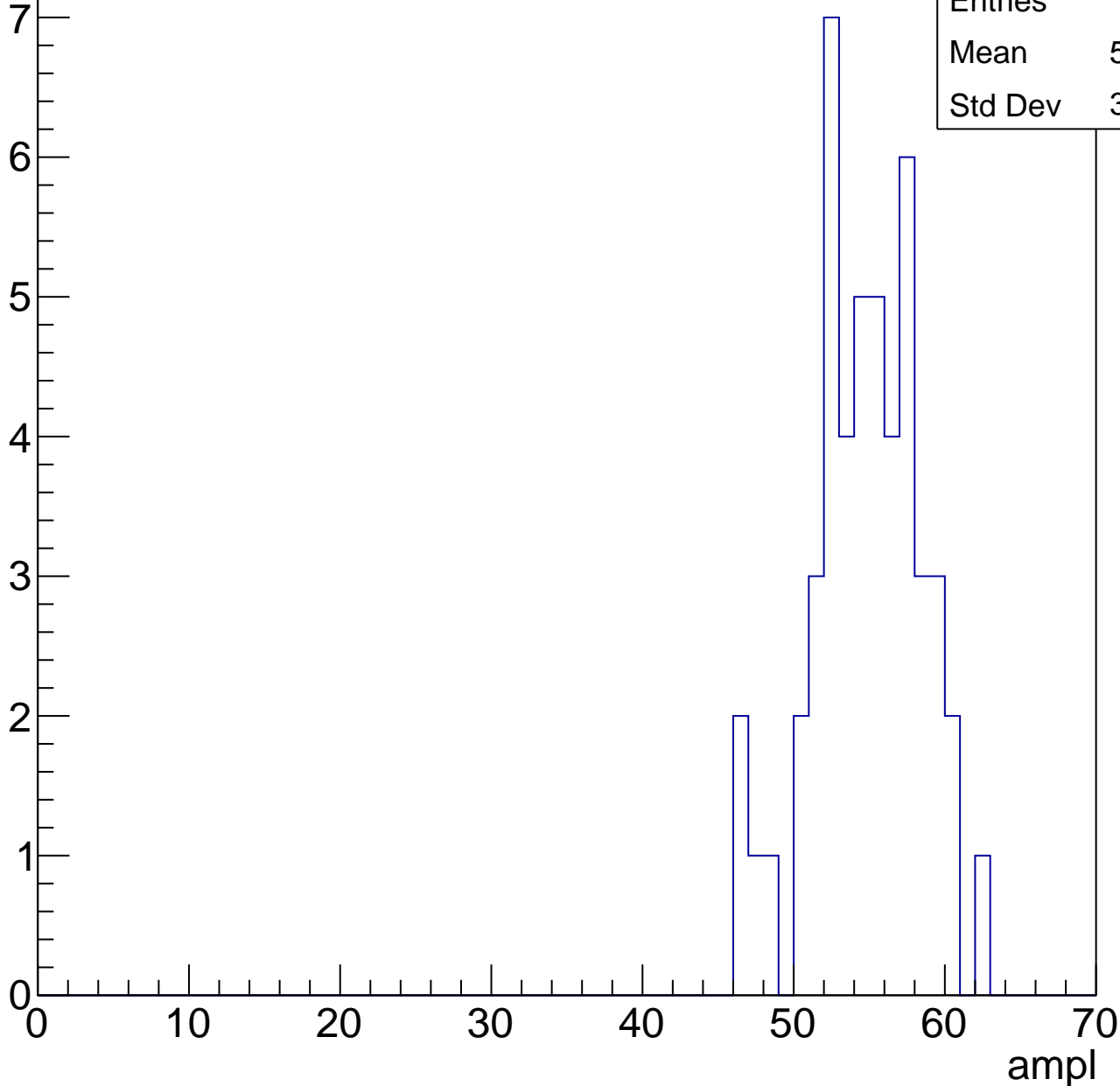


B1L103S, U13-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	54.29
Std Dev	3.597

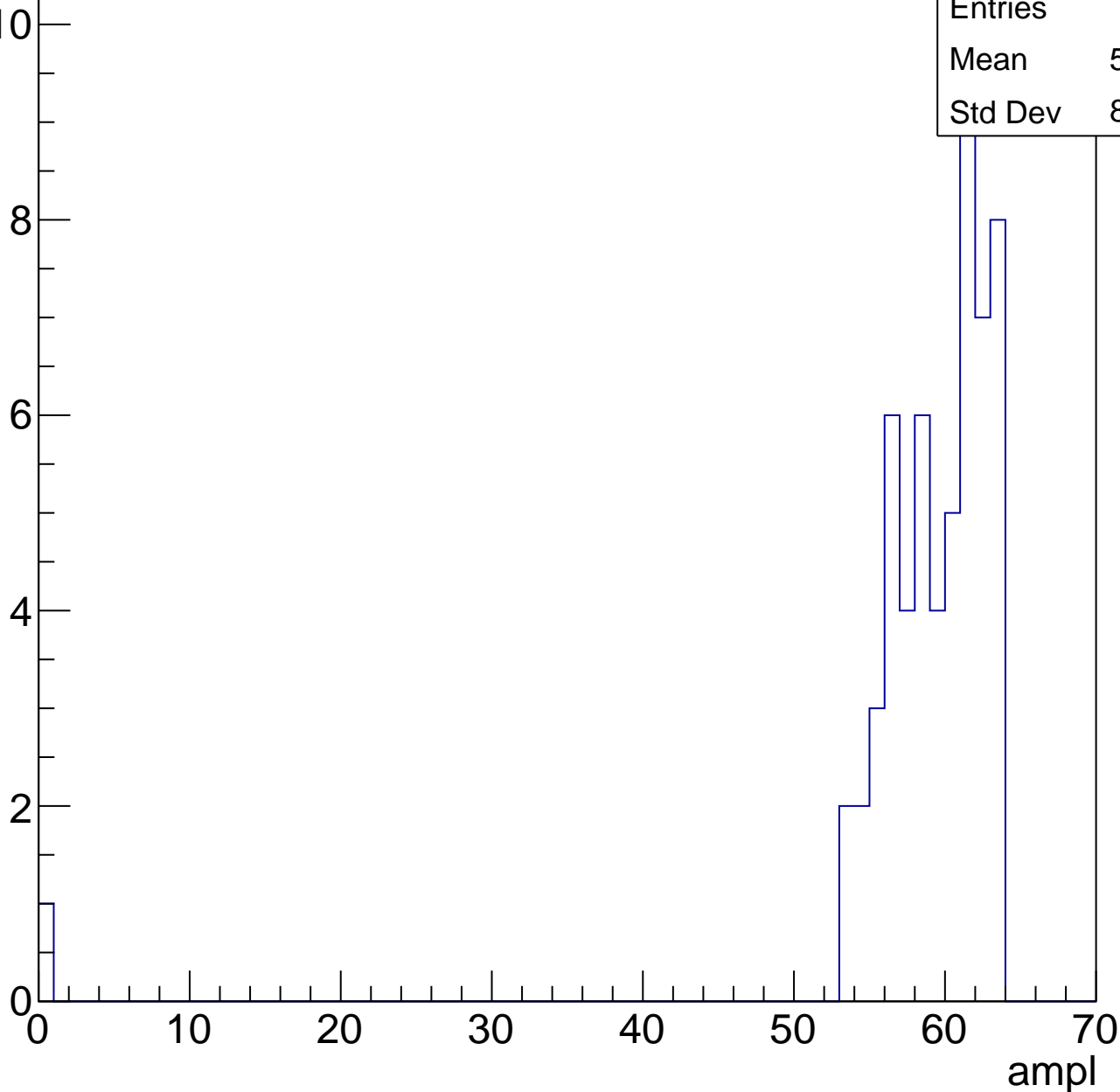


B1L103S, U13-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	58.19
Std Dev	8.226



B1L103S, U13-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	60.45
Std Dev	1.671

B1L103S, U13-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch5, adc0

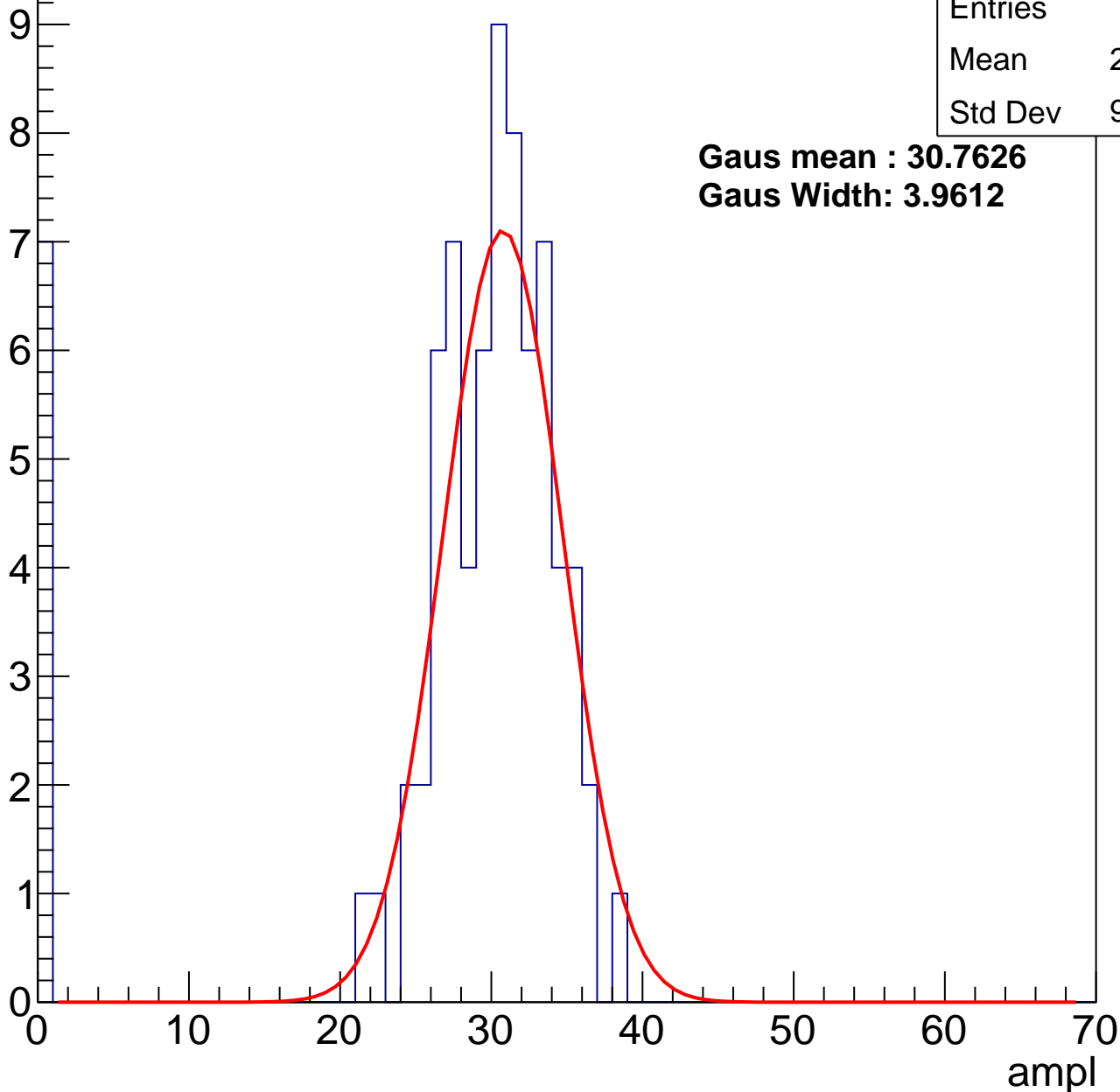
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	27.26
Std Dev	9.233

Gaus mean : 30.7626

Gaus Width: 3.9612



B1L103S, U13-ch5, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	35.32
Std Dev	10.46

Gaus mean : 38.8354

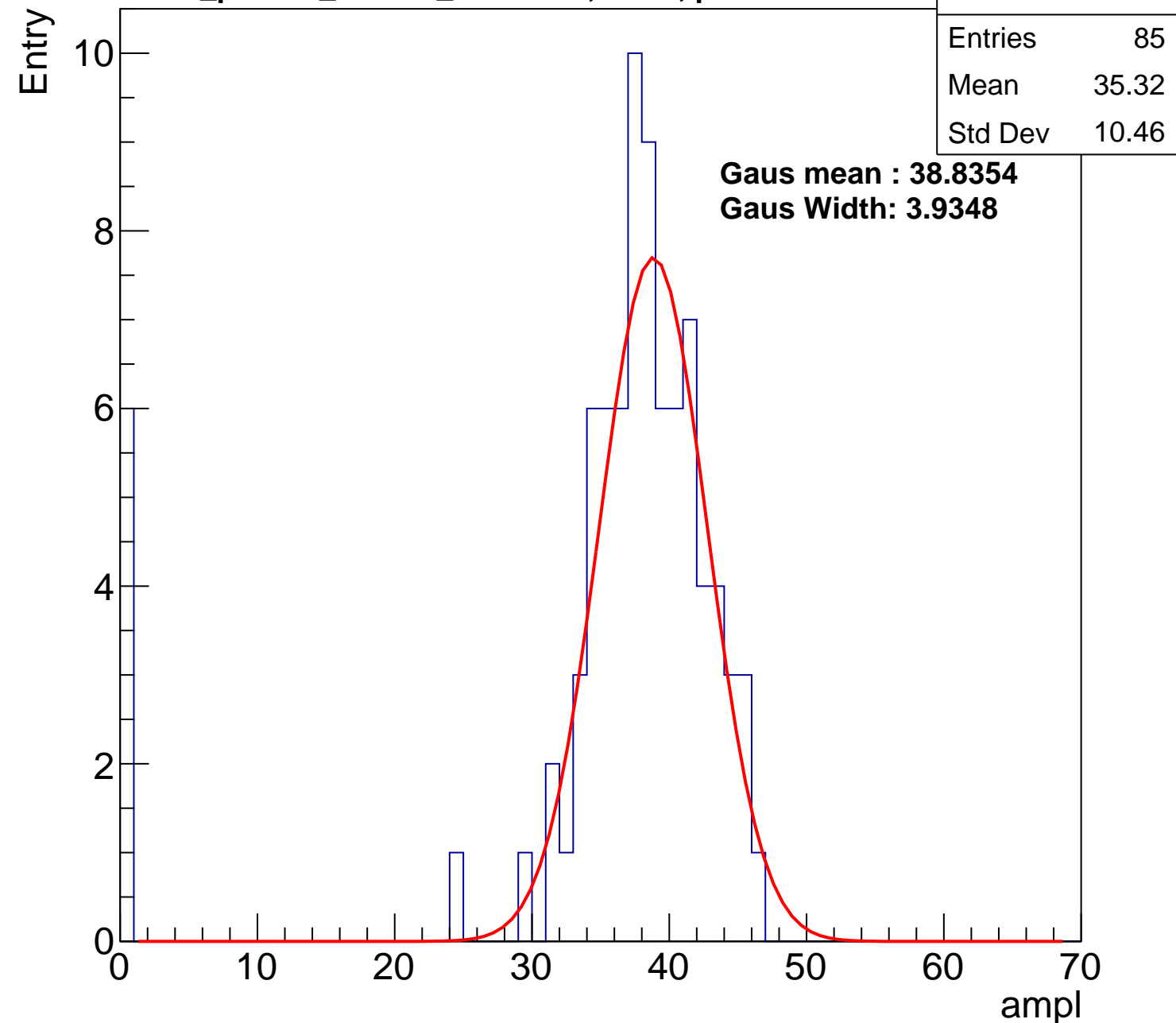
Gaus Width: 3.9348

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch5, adc2

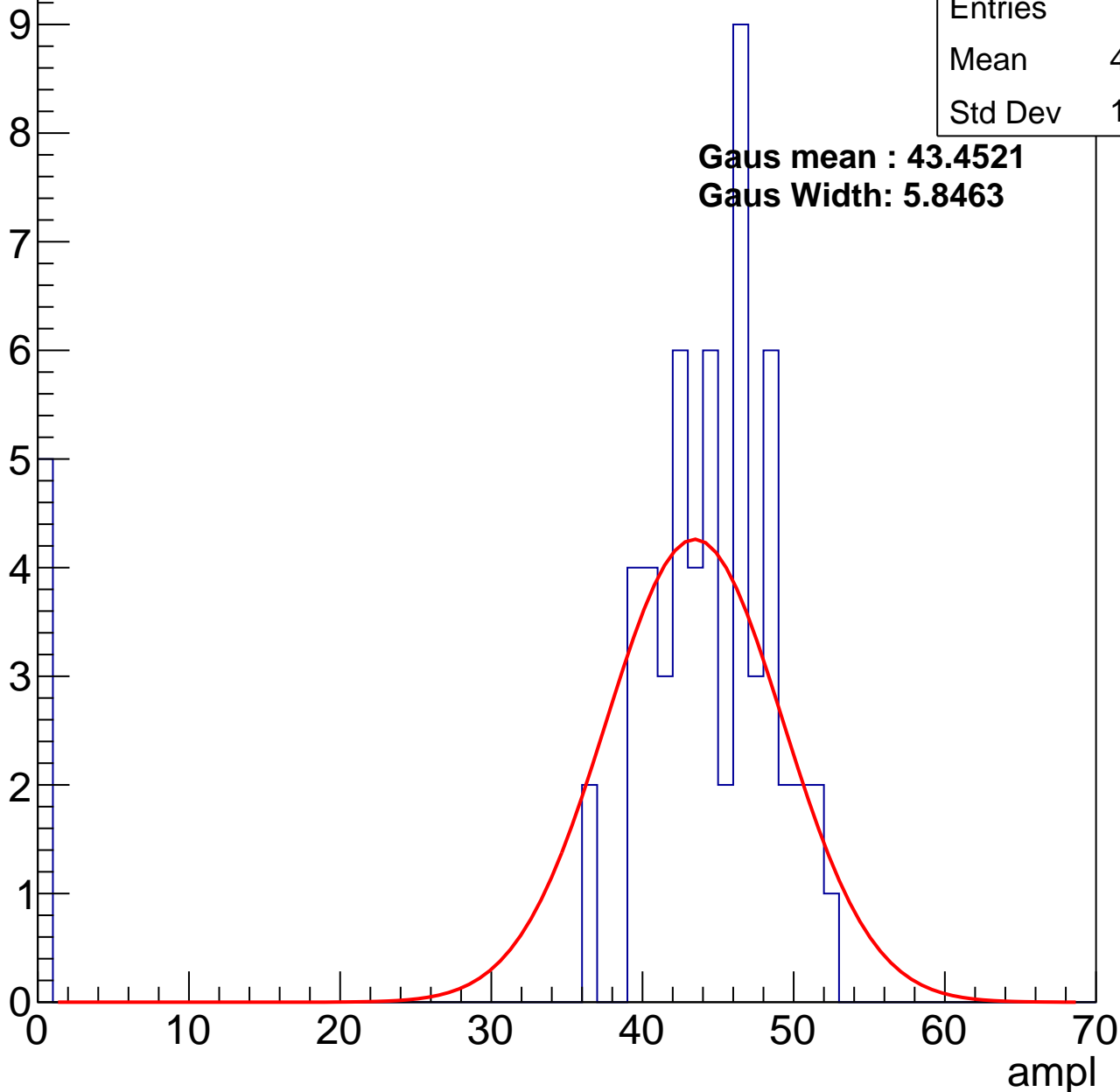
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	40.72
Std Dev	12.69

Gaus mean : 43.4521

Gaus Width: 5.8463

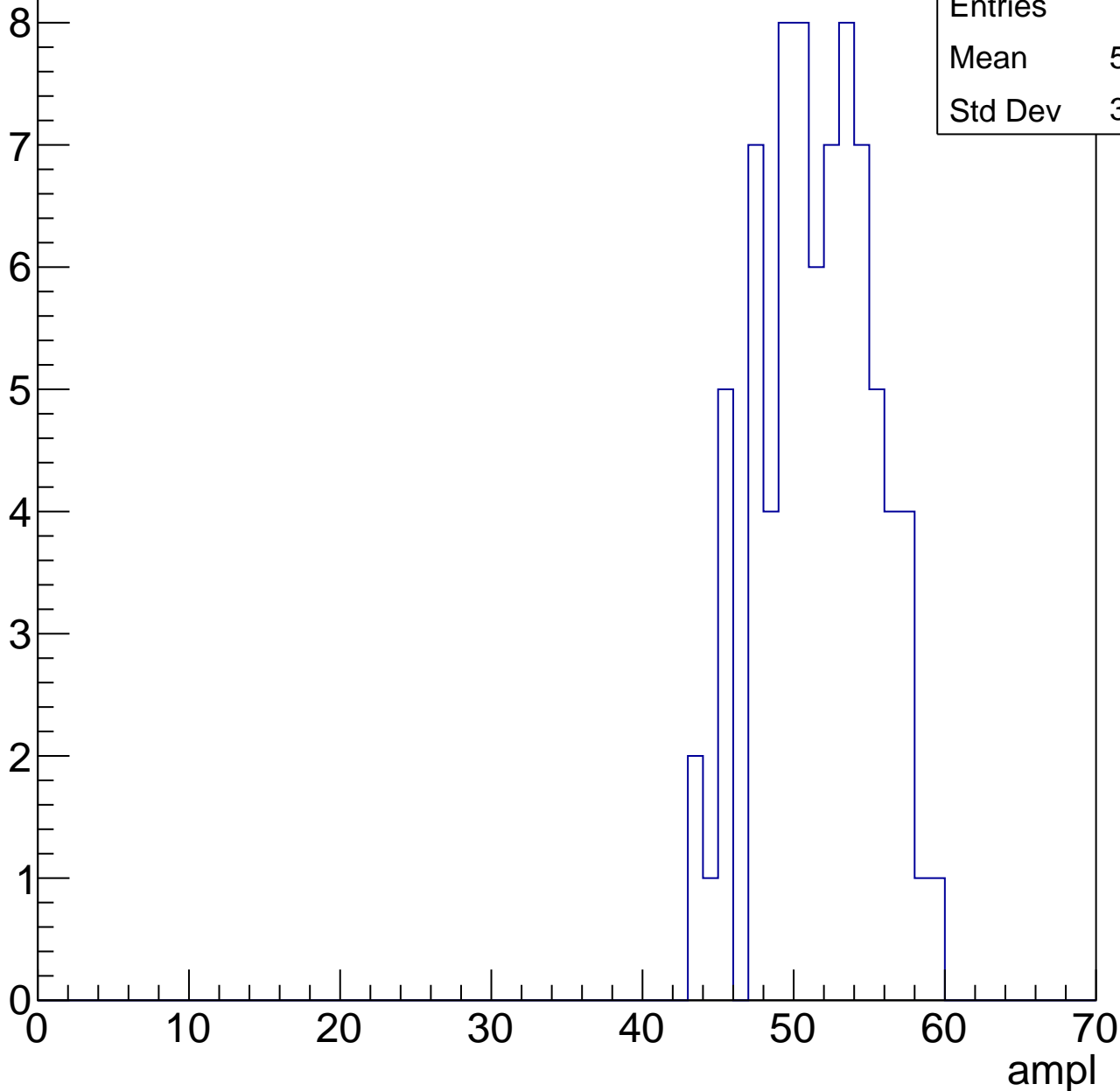


B1L103S, U13-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	51.08
Std Dev	3.727



B1L103S, U13-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 57.47

Std Dev 3.051

ampl

0

10

20

30

40

50

60

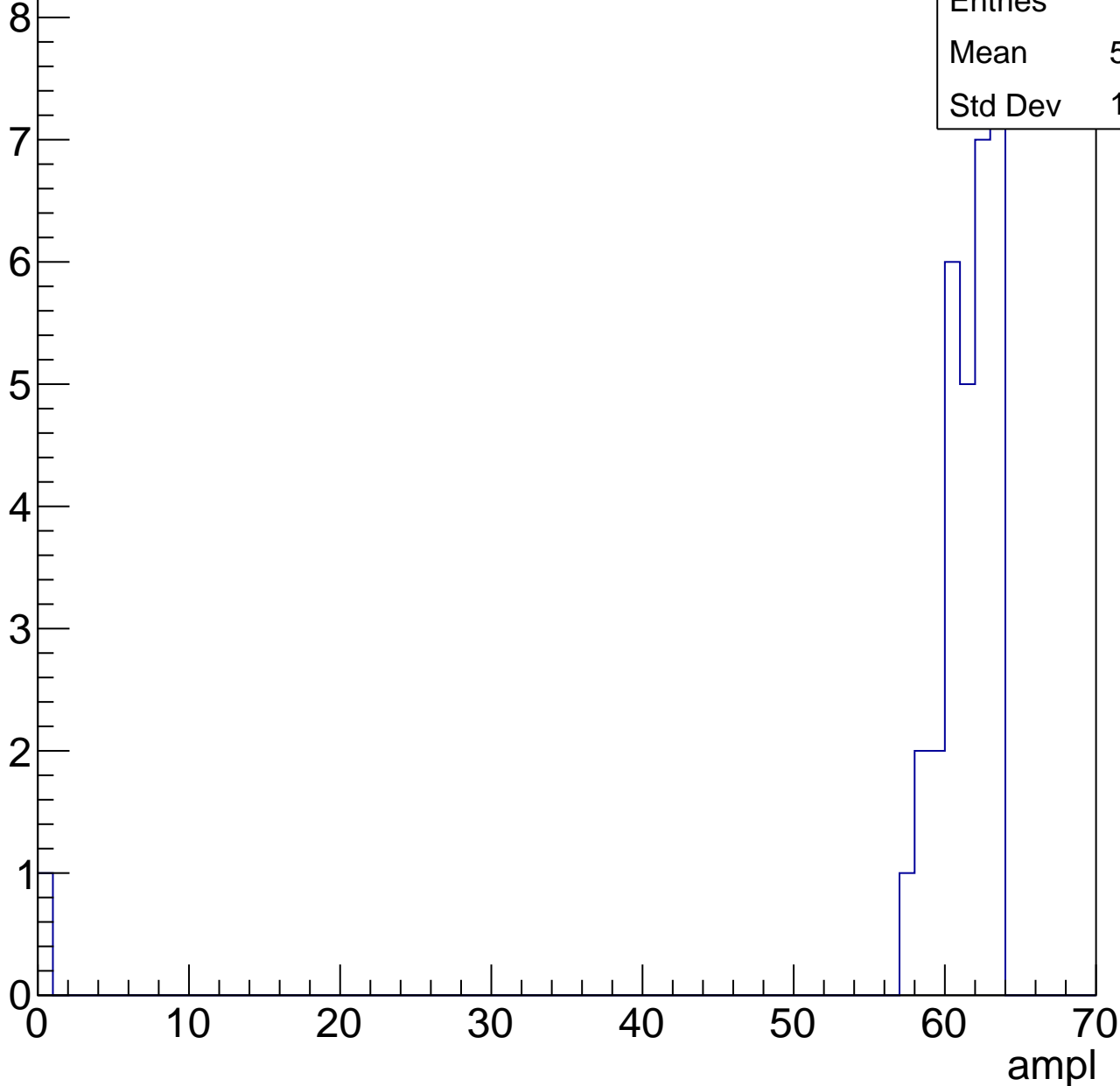
70

B1L103S, U13-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	59.19
Std Dev	10.76



B1L103S, U13-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch6, adc0

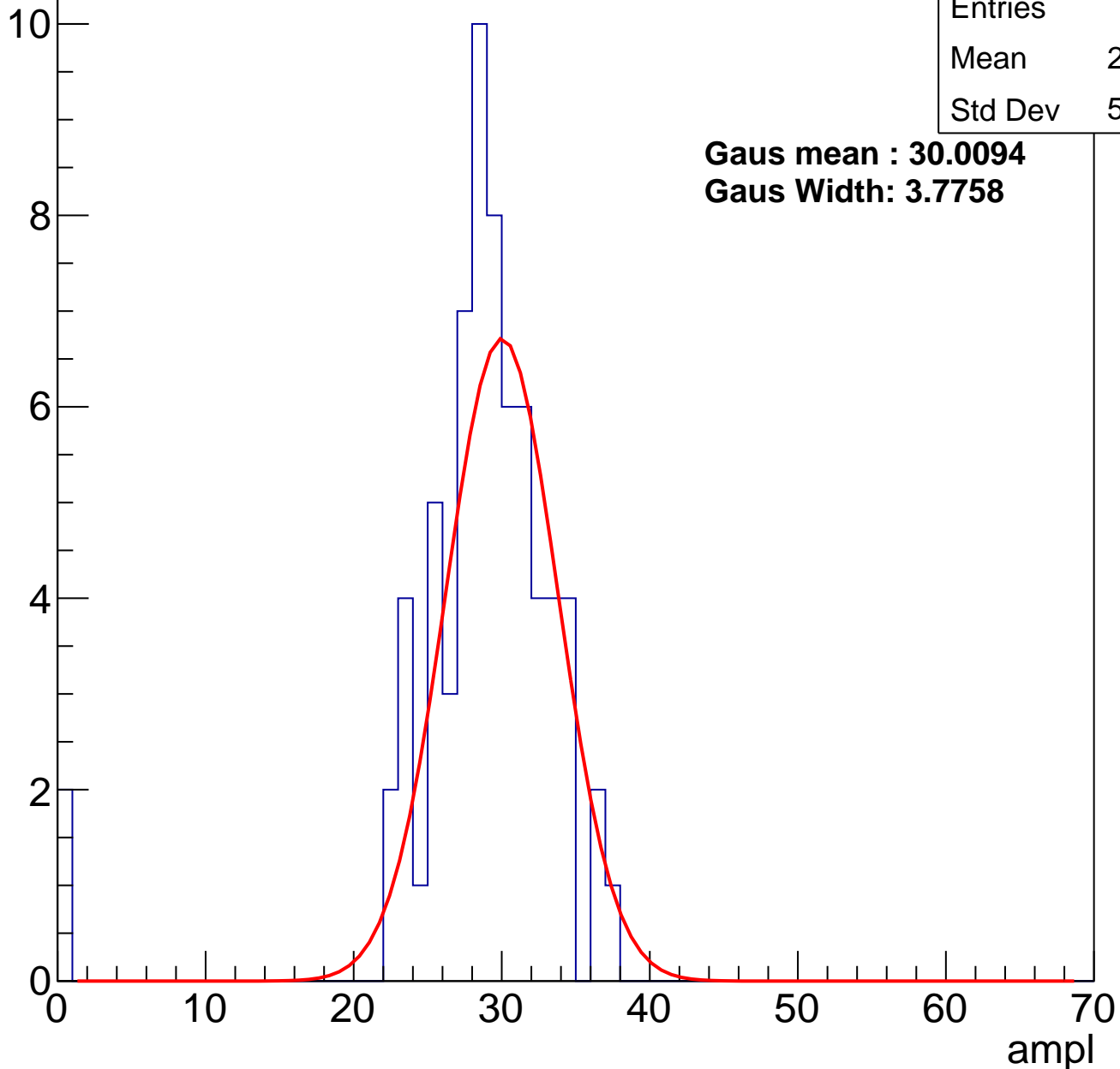
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	28.04
Std Dev	5.926

Gaus mean : 30.0094

Gaus Width: 3.7758

Entry



B1L103S, U13-ch6, adc1

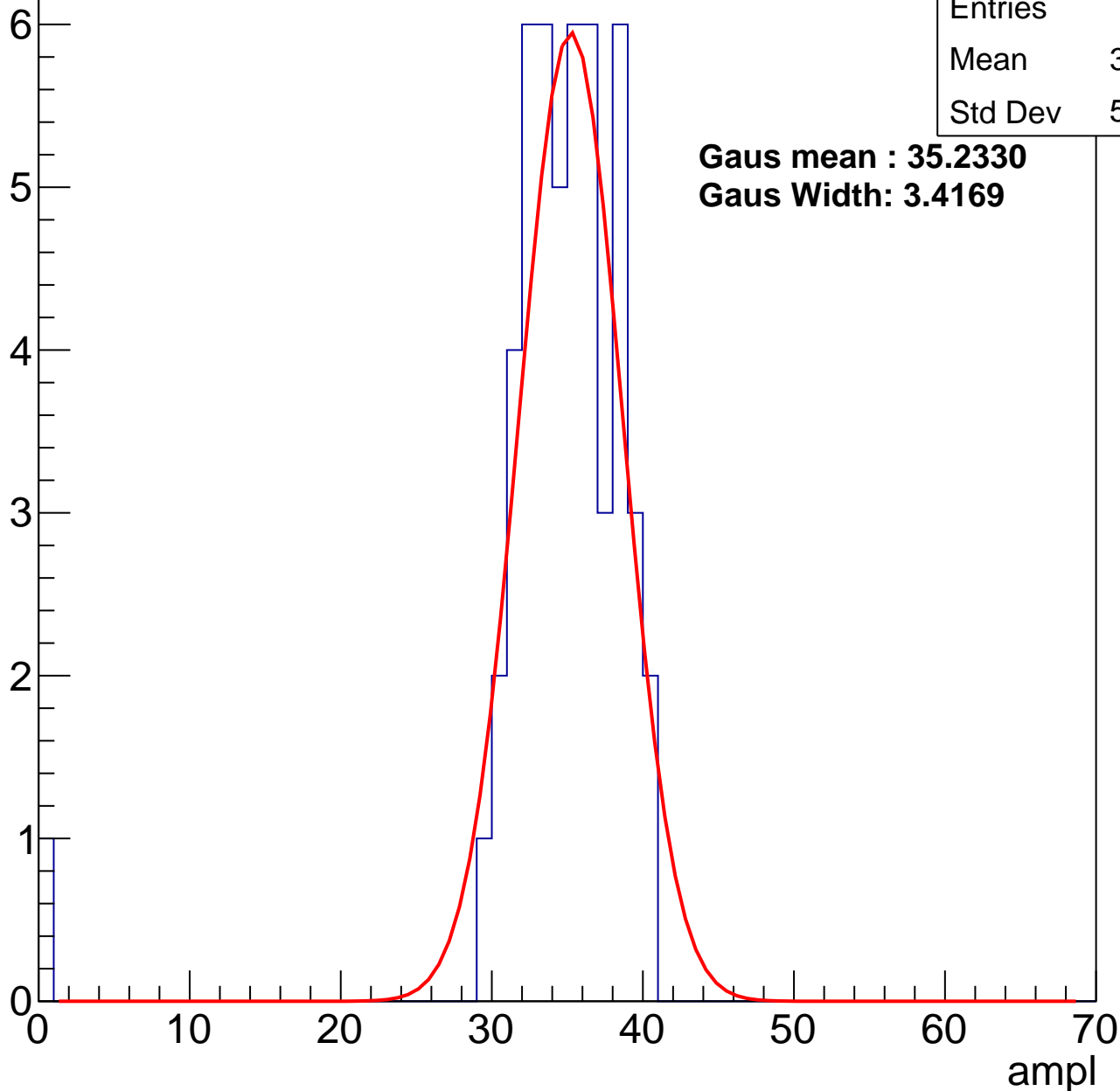
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	34.02
Std Dev	5.564

Gaus mean : 35.2330

Gaus Width: 3.4169



B1L103S, U13-ch6, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	38.35
Std Dev	10.64

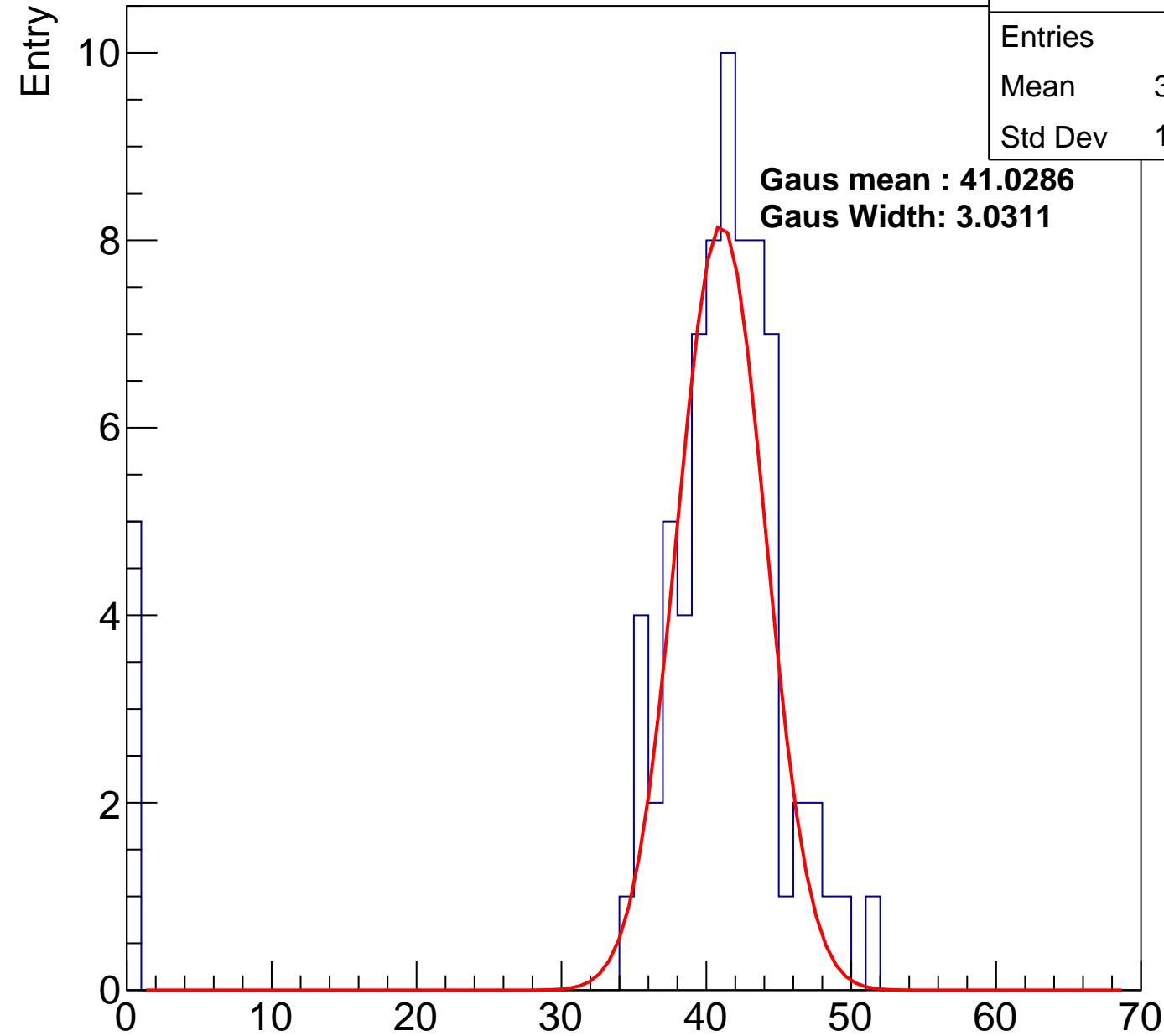
Gaus mean : 41.0286

Gaus Width: 3.0311

Entry

10
8
6
4
2
0

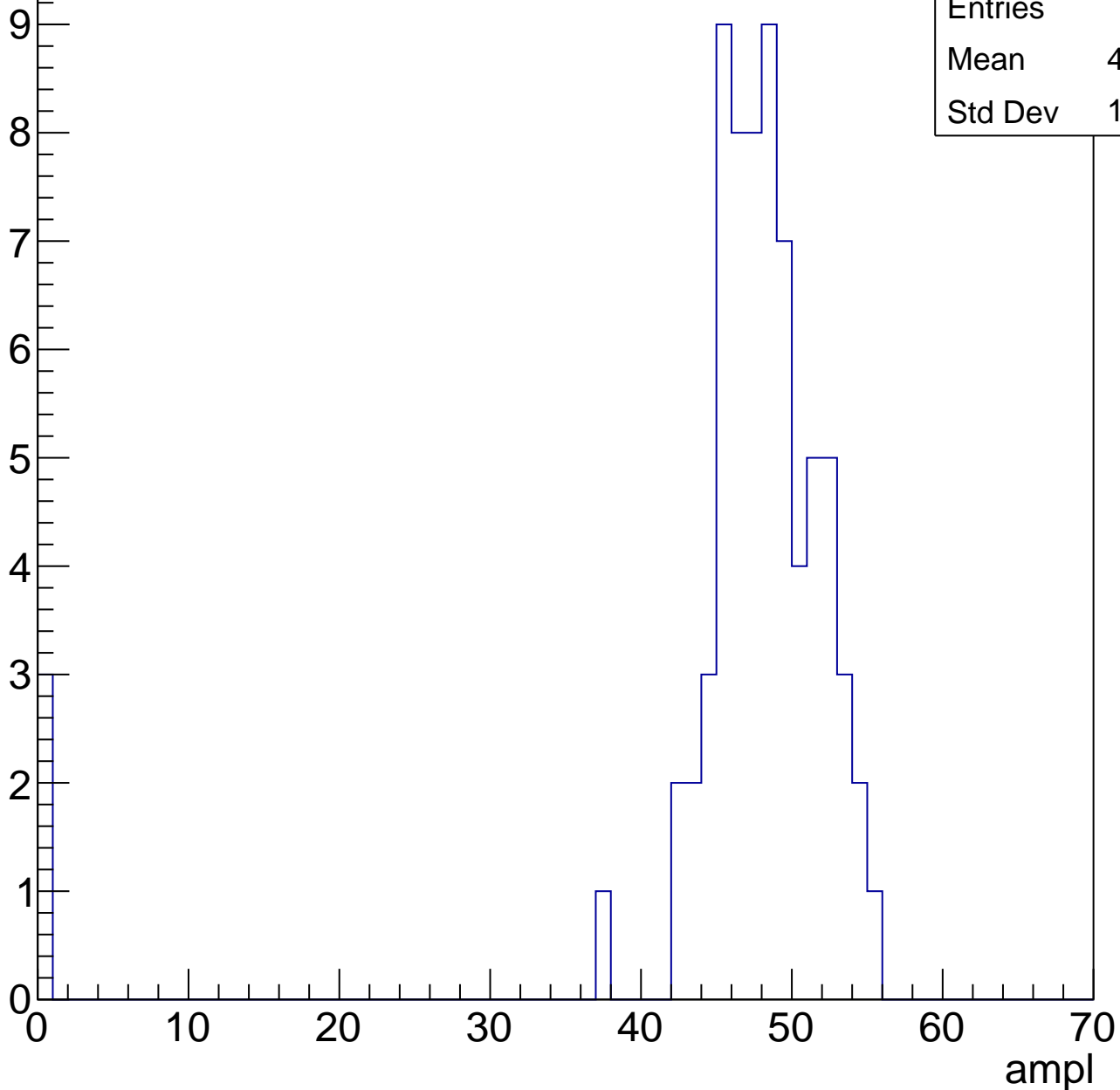
ampl



B1L103S, U13-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

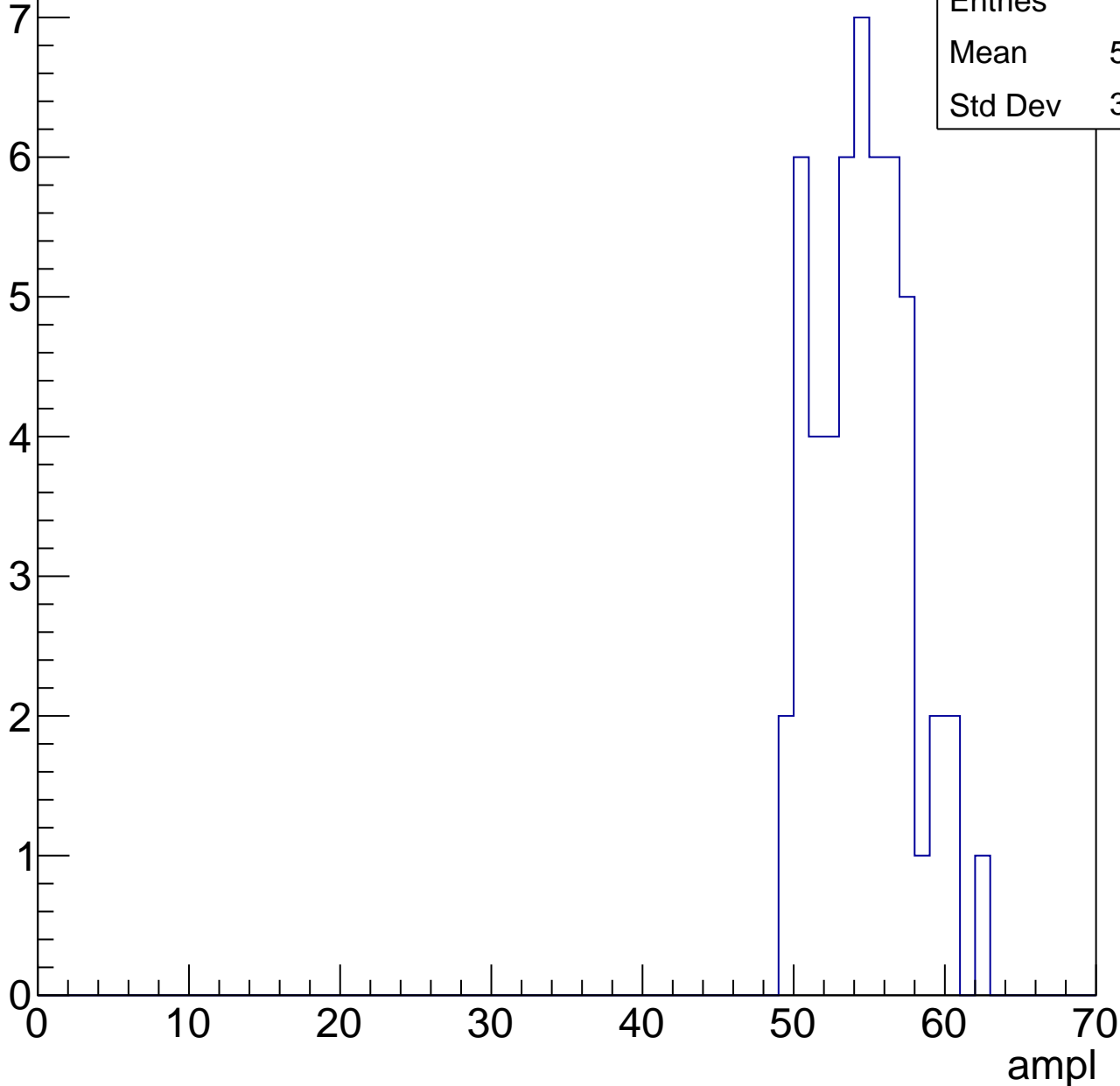


B1L103S, U13-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	54.13
Std Dev	3.038



B1L103S, U13-ch6, adc5

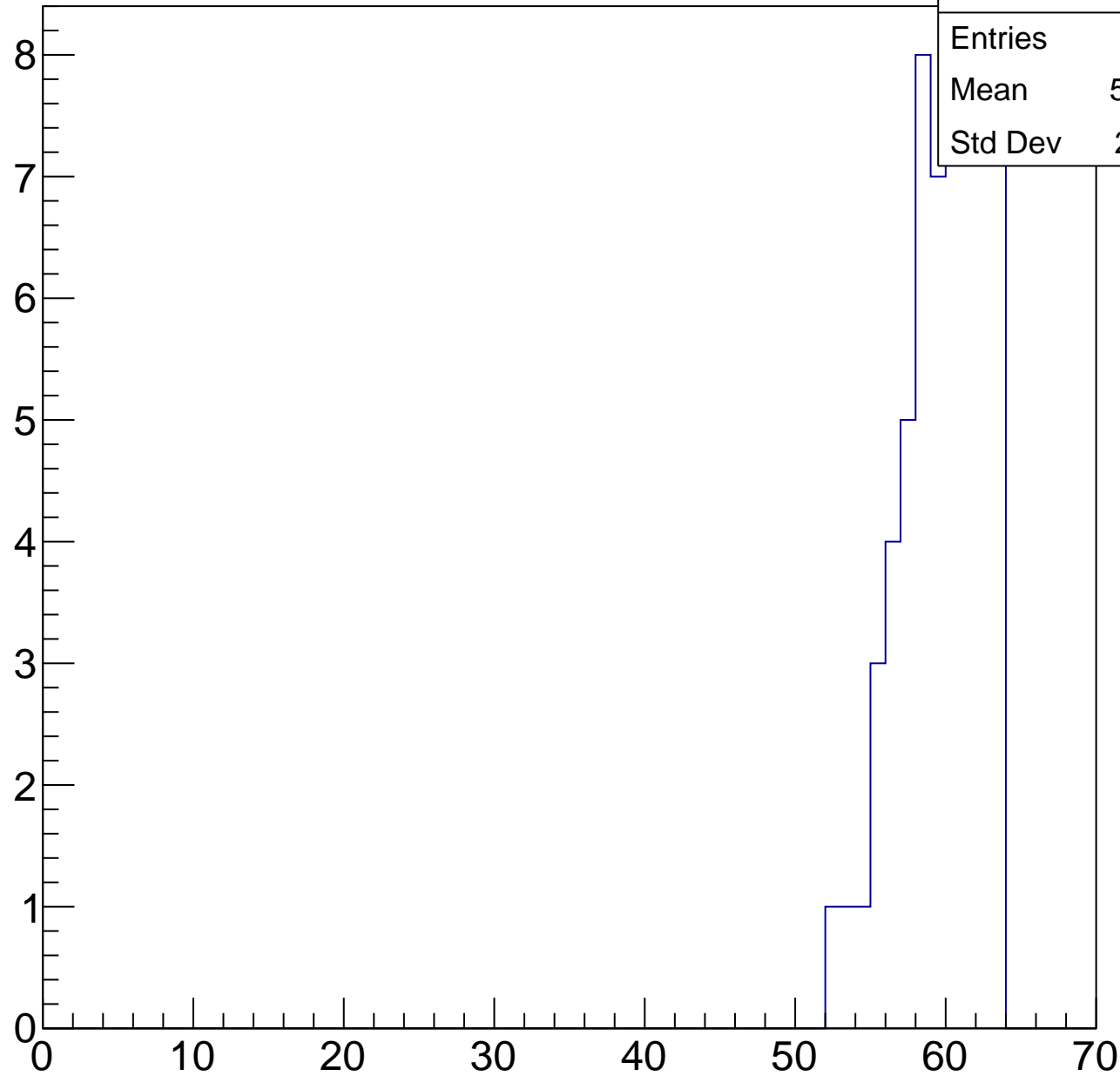
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	62
Mean	59.32
Std Dev	2.711

ampl



B1L103S, U13-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

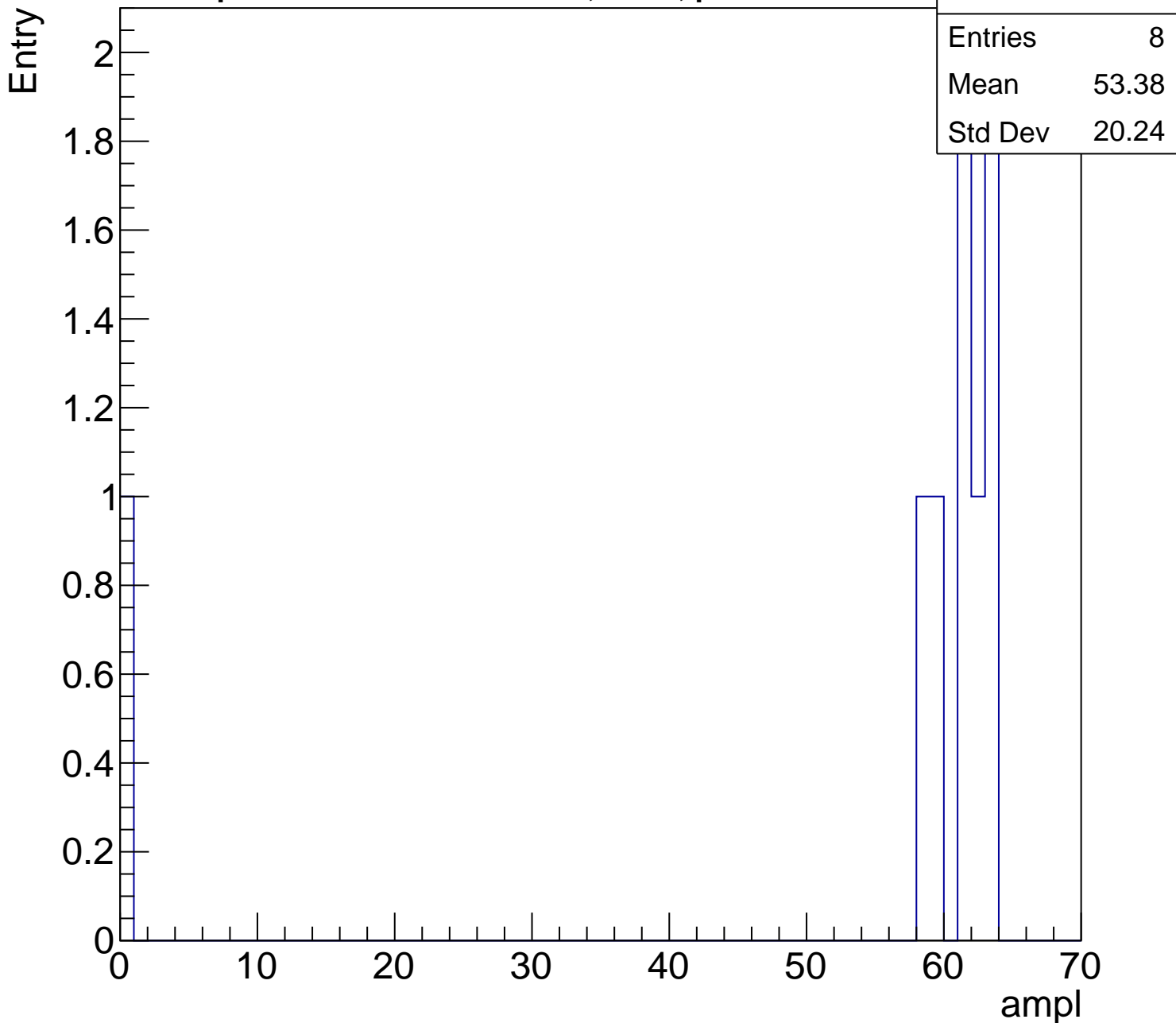
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.38
Std Dev	20.24

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U13-ch7, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	25.62
Std Dev	6.977

Gaus mean : 27.2158

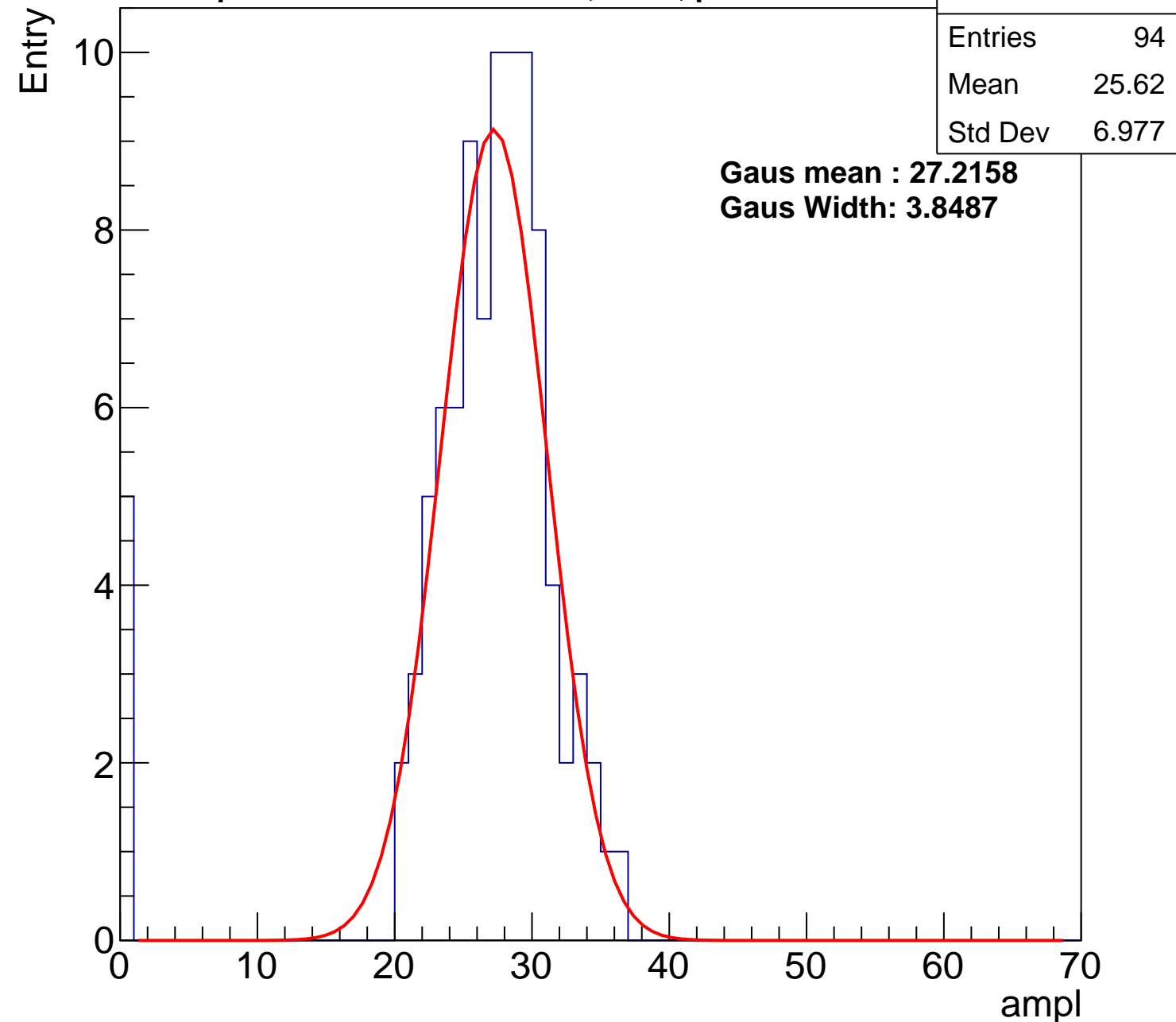
Gaus Width: 3.8487

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch7, adc1

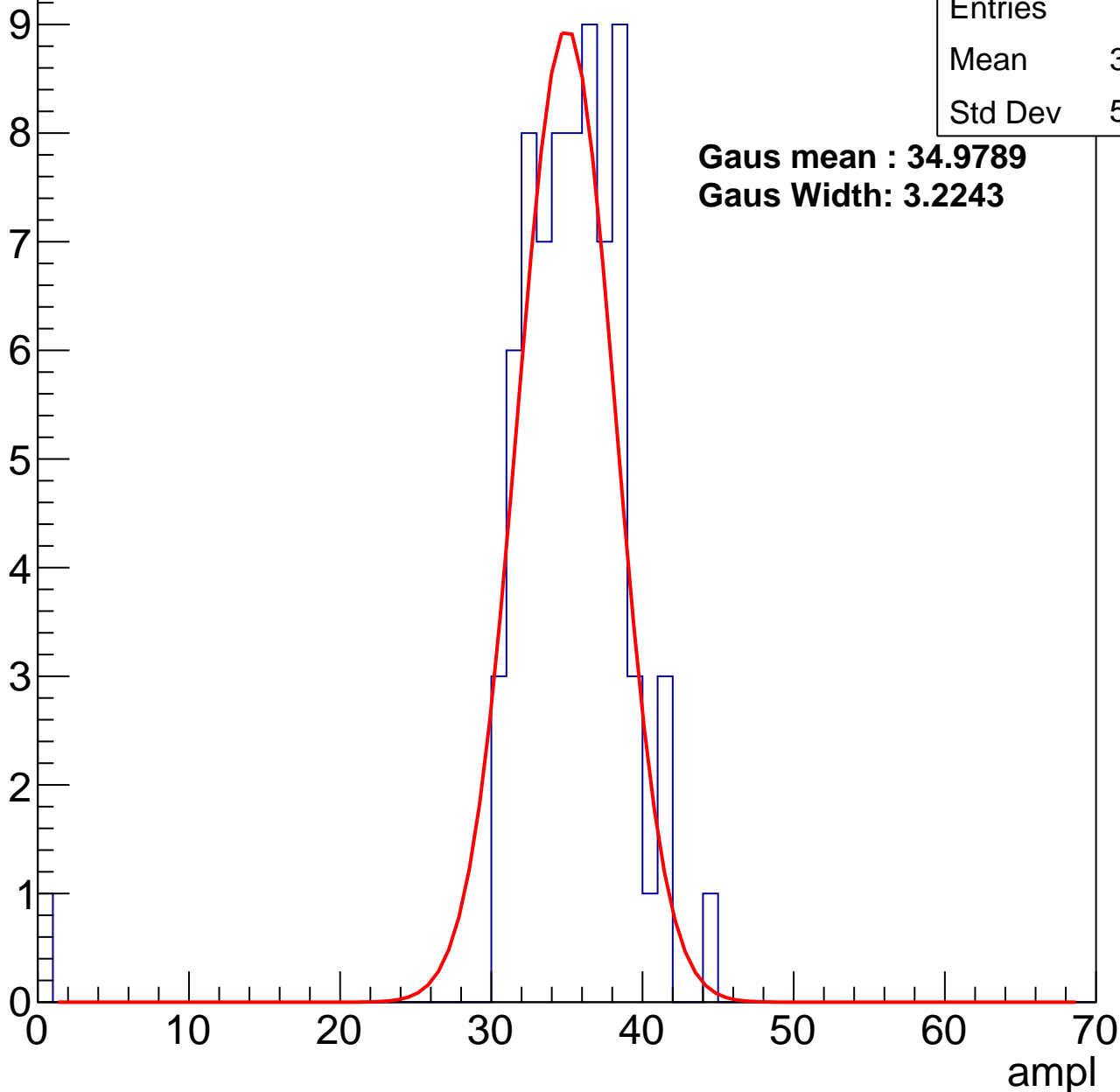
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	34.65
Std Dev	5.033

Gaus mean : 34.9789

Gaus Width: 3.2243



B1L103S, U13-ch7, adc2

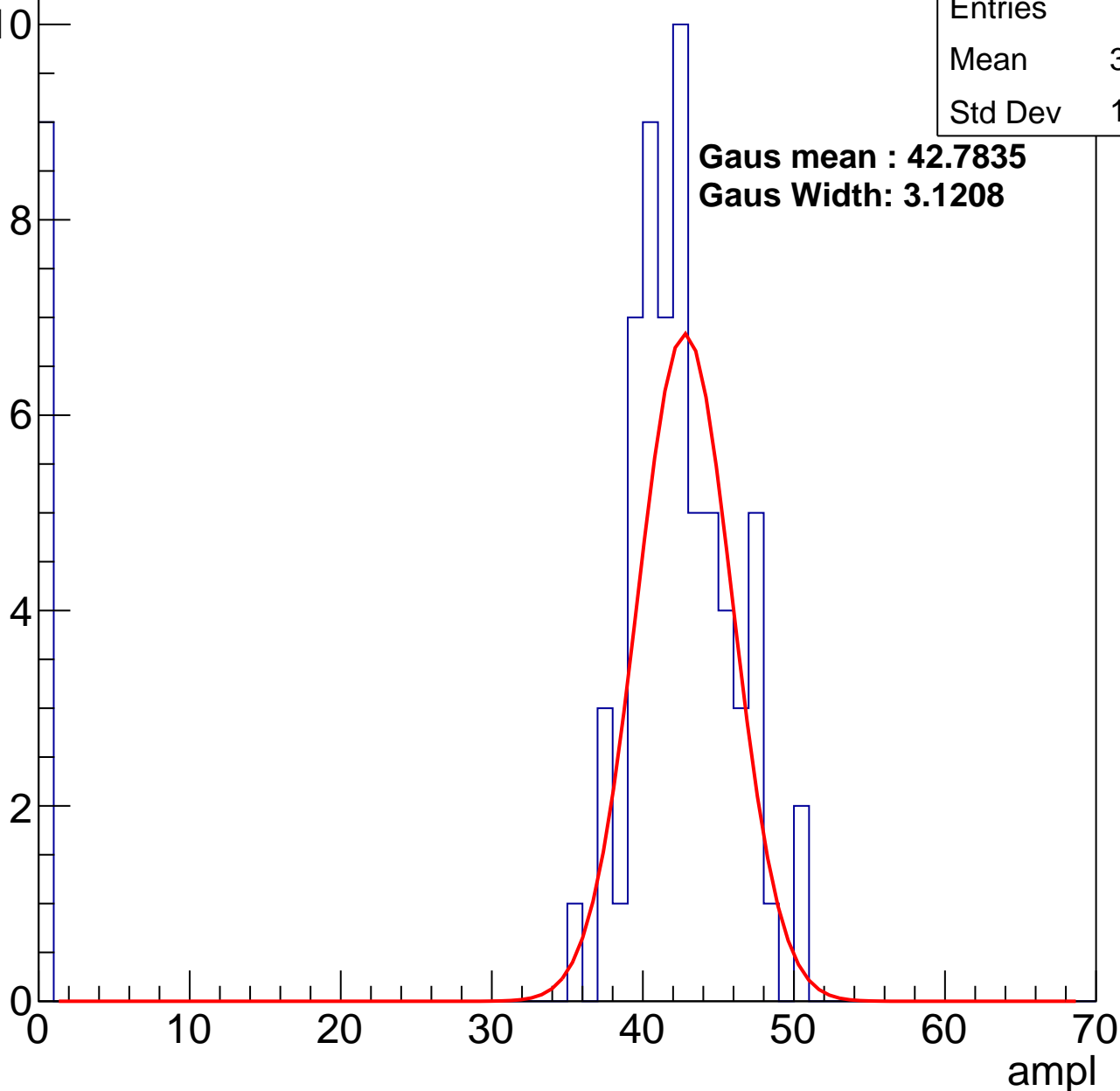
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	36.94
Std Dev	14.28

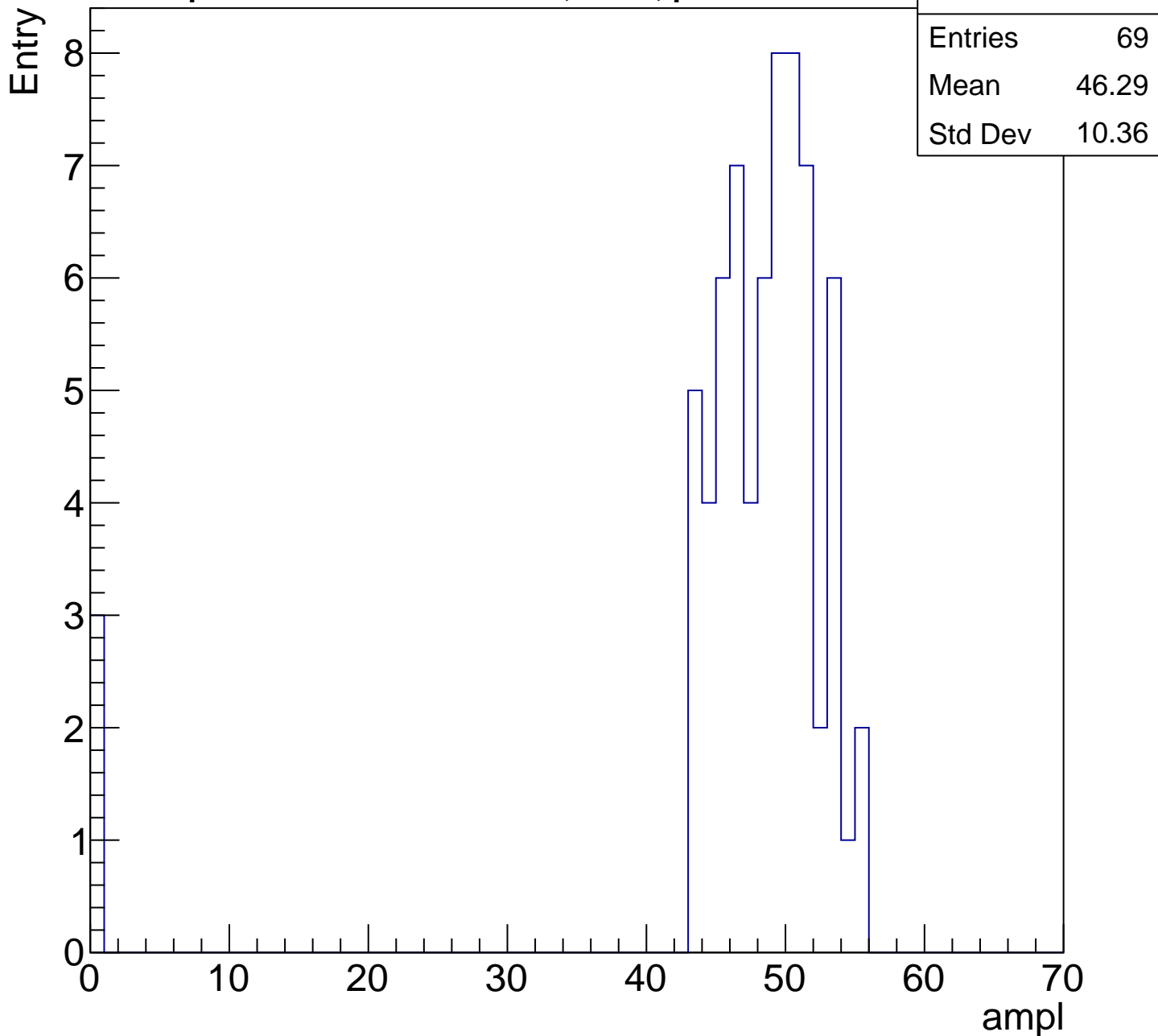
Gaus mean : 42.7835

Gaus Width: 3.1208



B1L103S, U13-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

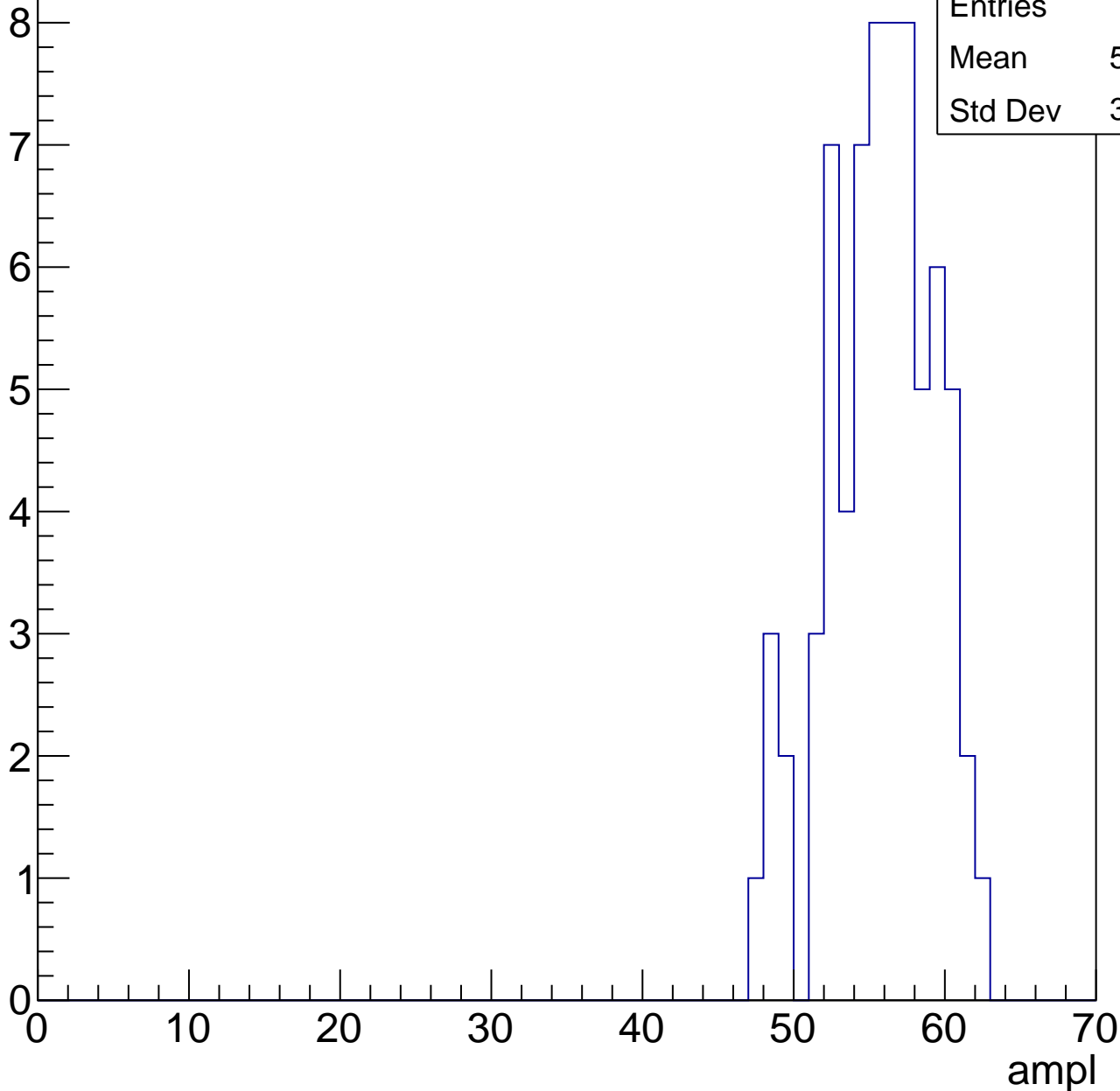


B1L103S, U13-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	55.26
Std Dev	3.463

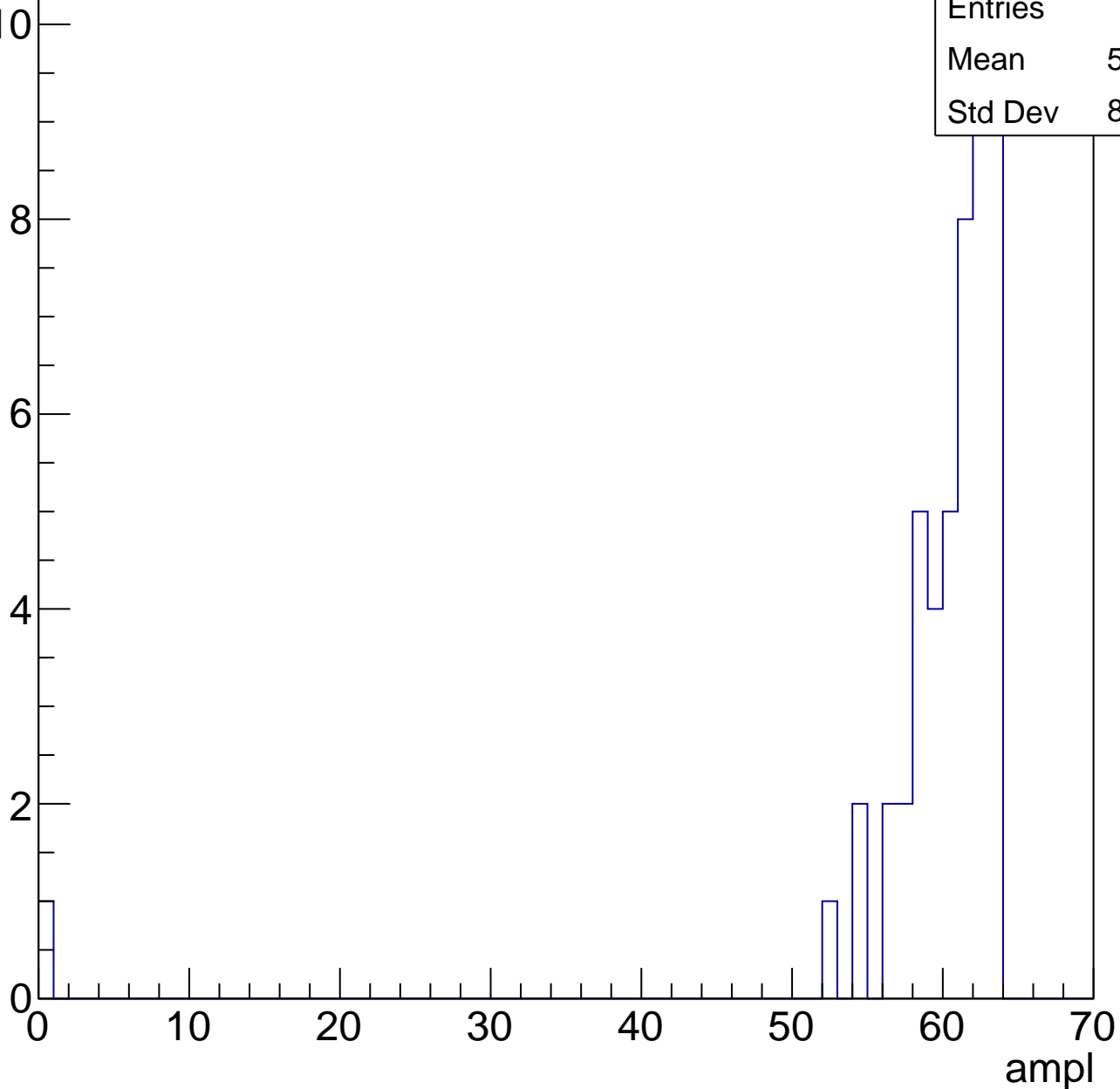


B1L103S, U13-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.94
Std Dev	8.909



B1L103S, U13-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch7, adc7

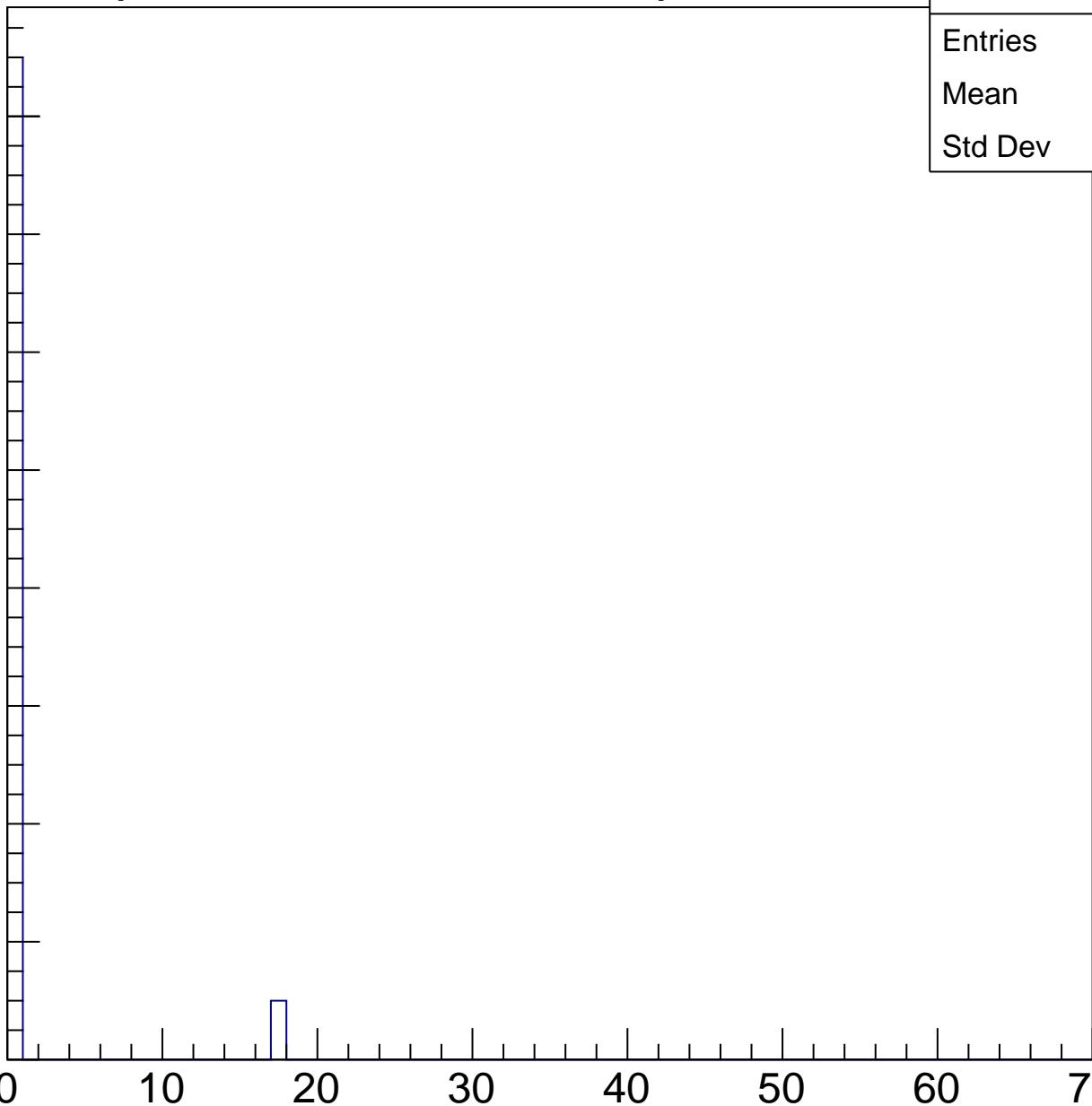
calib_packv5_041523_1651.root, FC#0, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	18
Mean	0.9444
Std Dev	3.894

ampl



B1L103S, U13-ch8, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	25.48
Std Dev	9.948

Gaus mean : 29.2121

Gaus Width: 3.8227

Entry

10

8

6

4

2

0

0

10

20

30

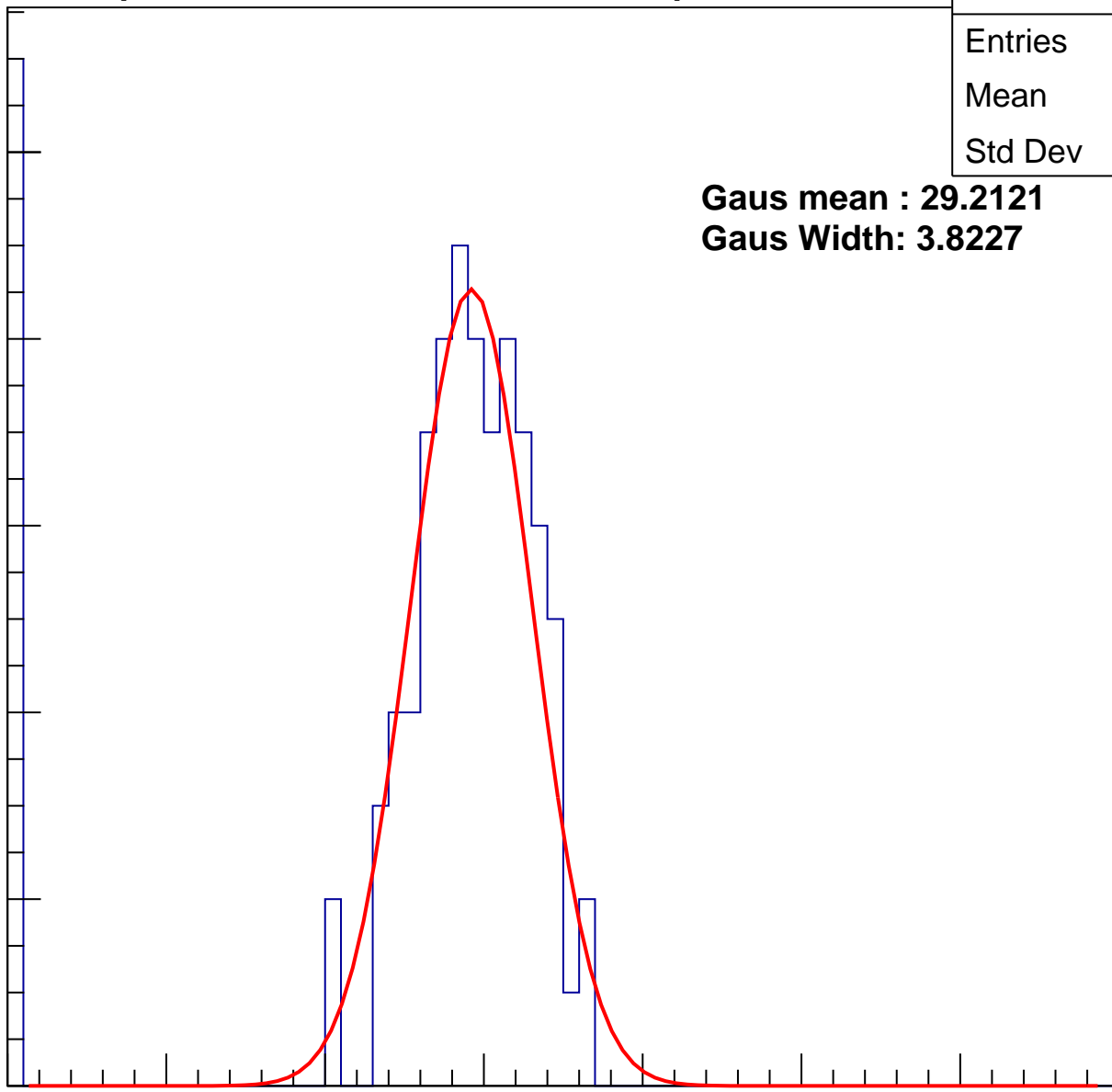
40

50

60

70

ampl



B1L103S, U13-ch8, adc1

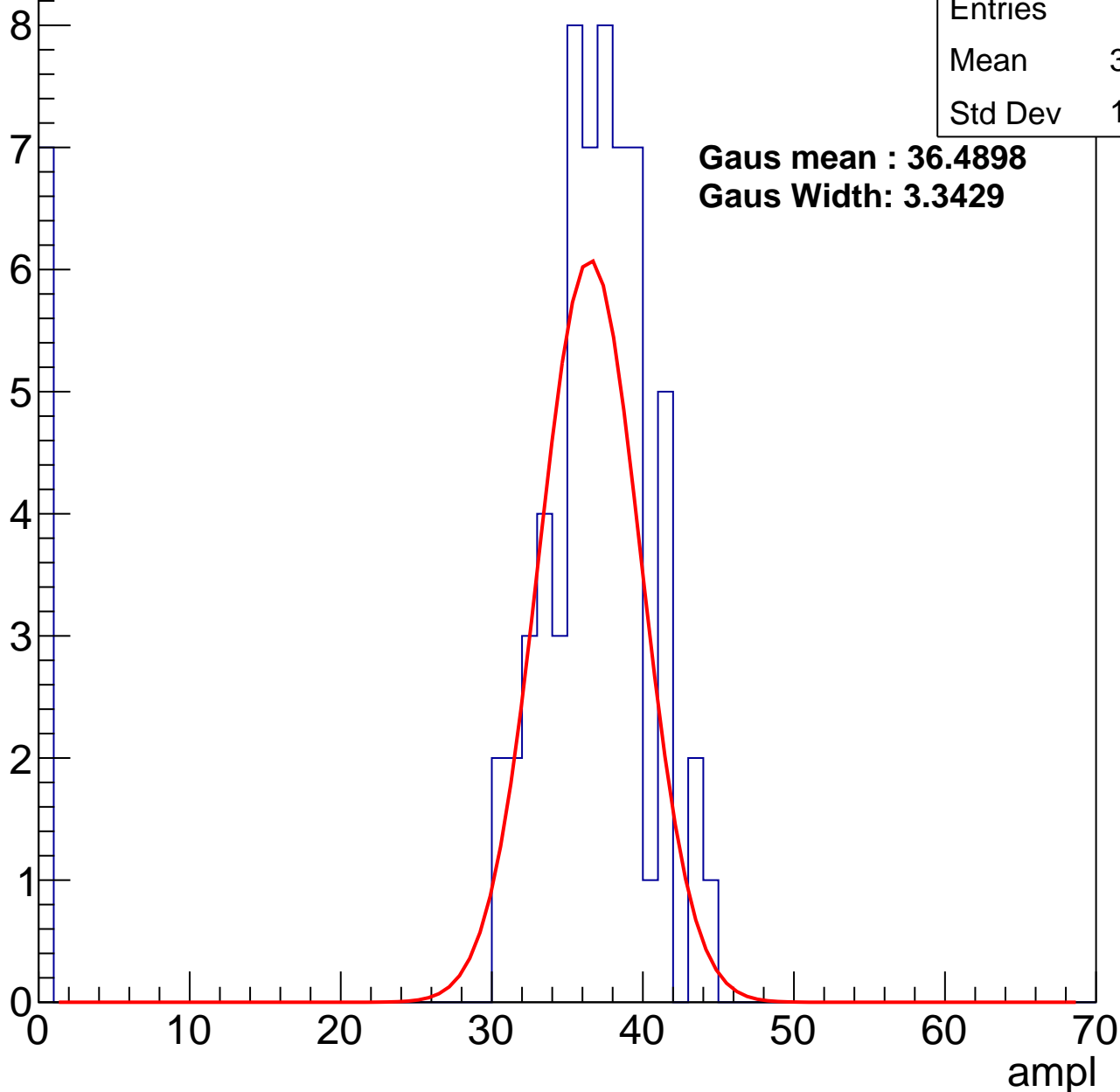
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.75
Std Dev	11.58

Gaus mean : 36.4898

Gaus Width: 3.3429



B1L103S, U13-ch8, adc2

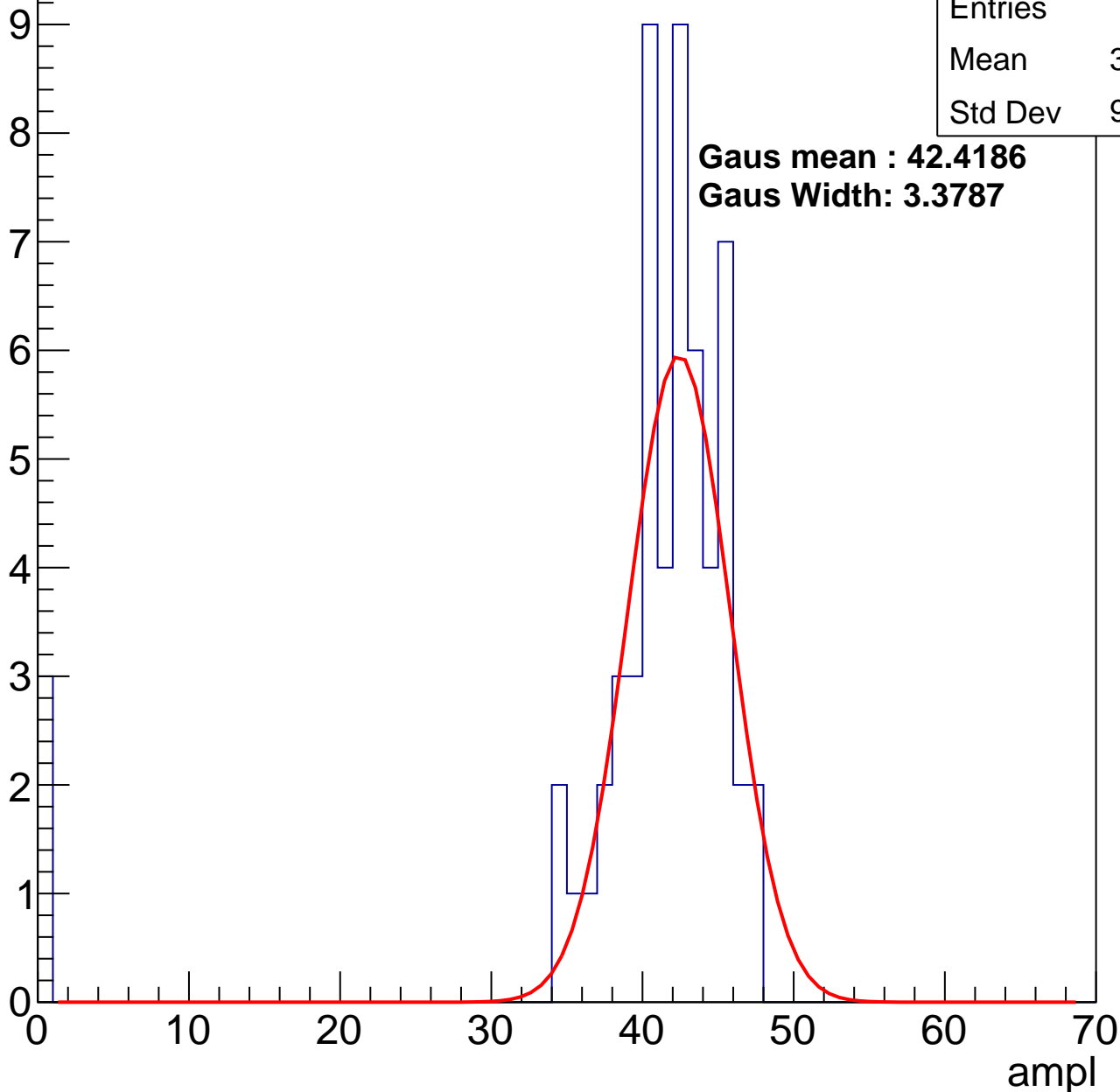
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	39.33
Std Dev	9.669

Gaus mean : 42.4186

Gaus Width: 3.3787



B1L103S, U13-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	49.36
Std Dev	3.649

Entry

10

8

6

4

2

0

0

10

20

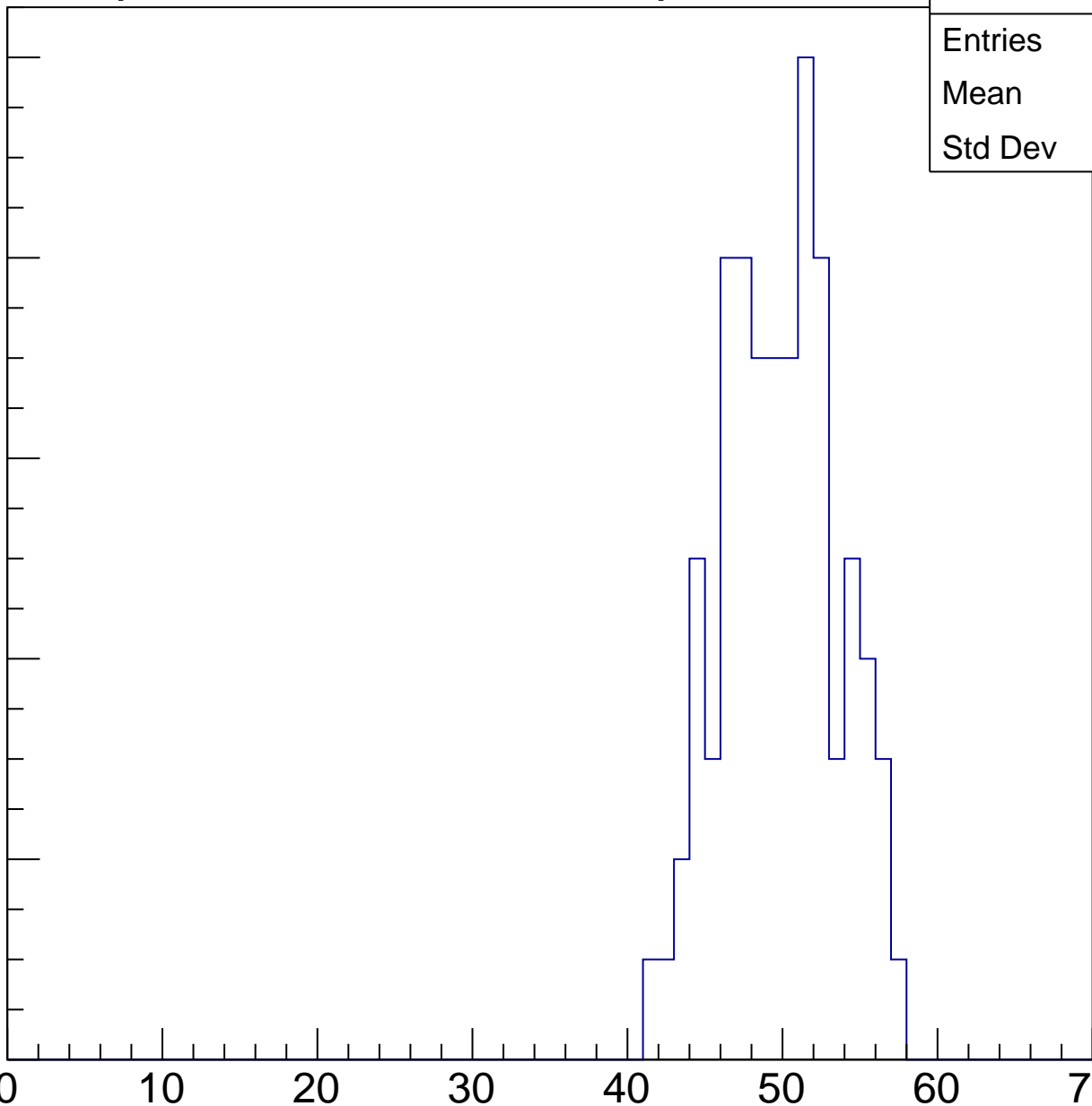
30

40

50

60

ampl

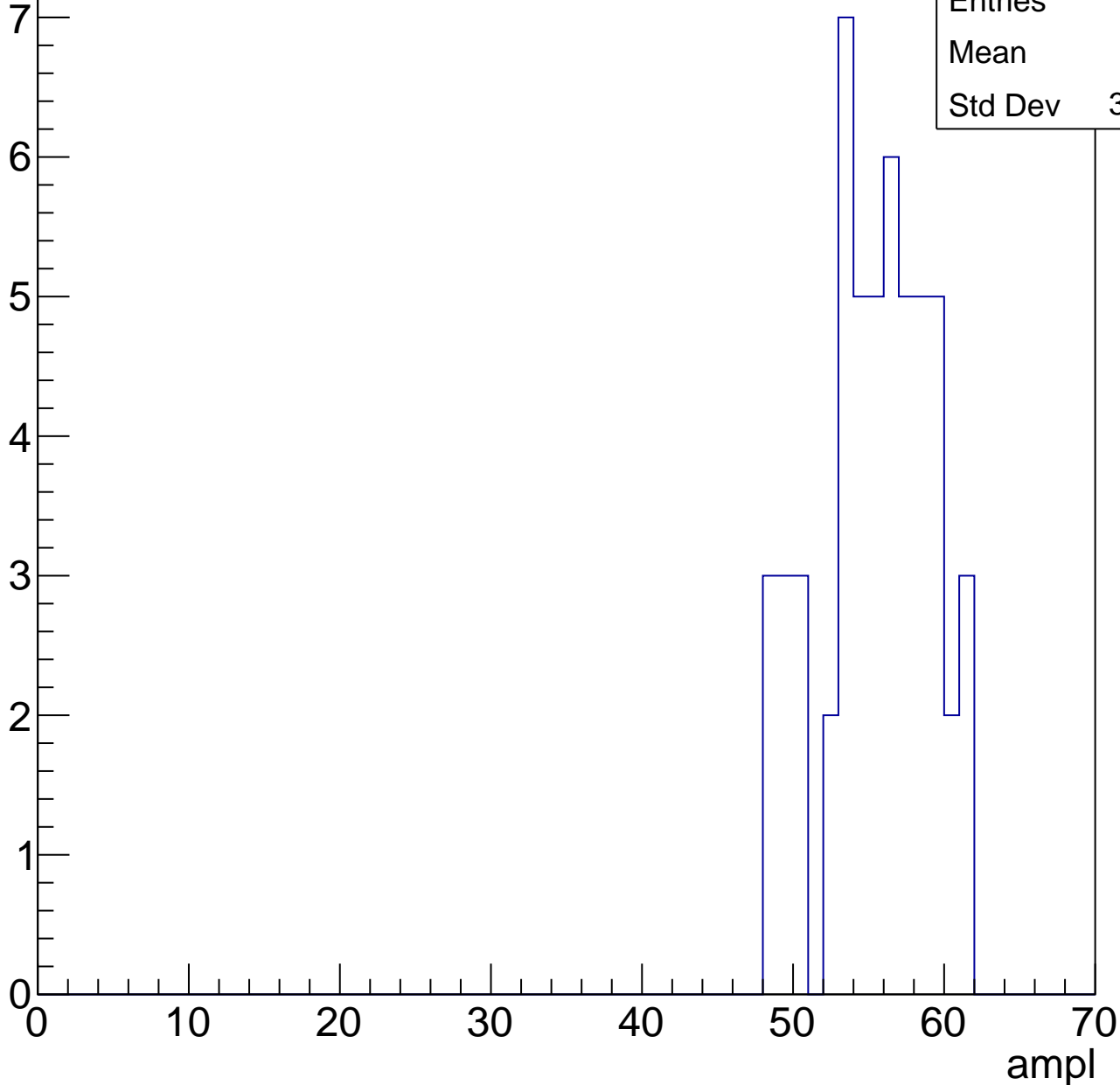


B1L103S, U13-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

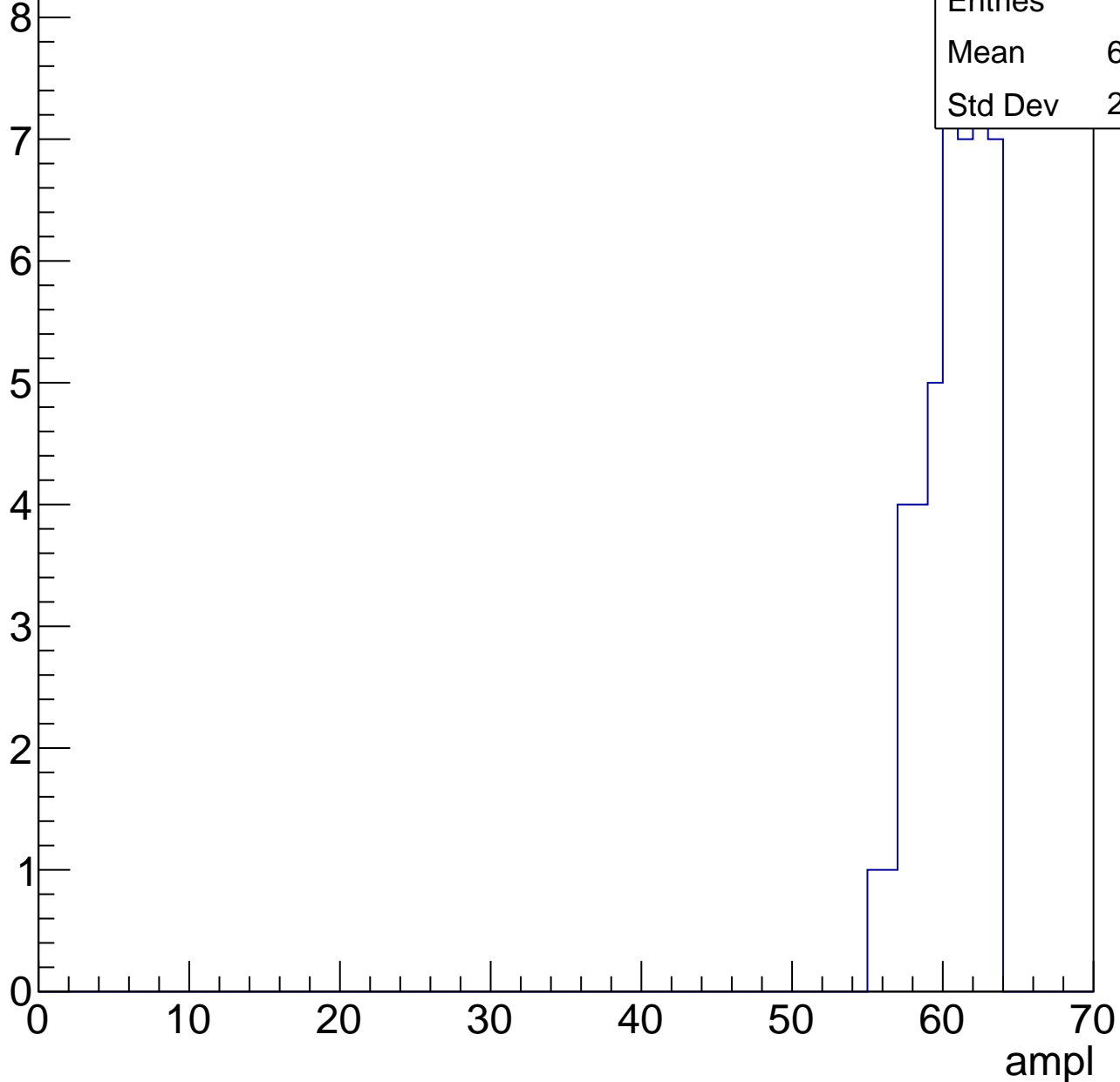
Entries	54
Mean	55
Std Dev	3.575



B1L103S, U13-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

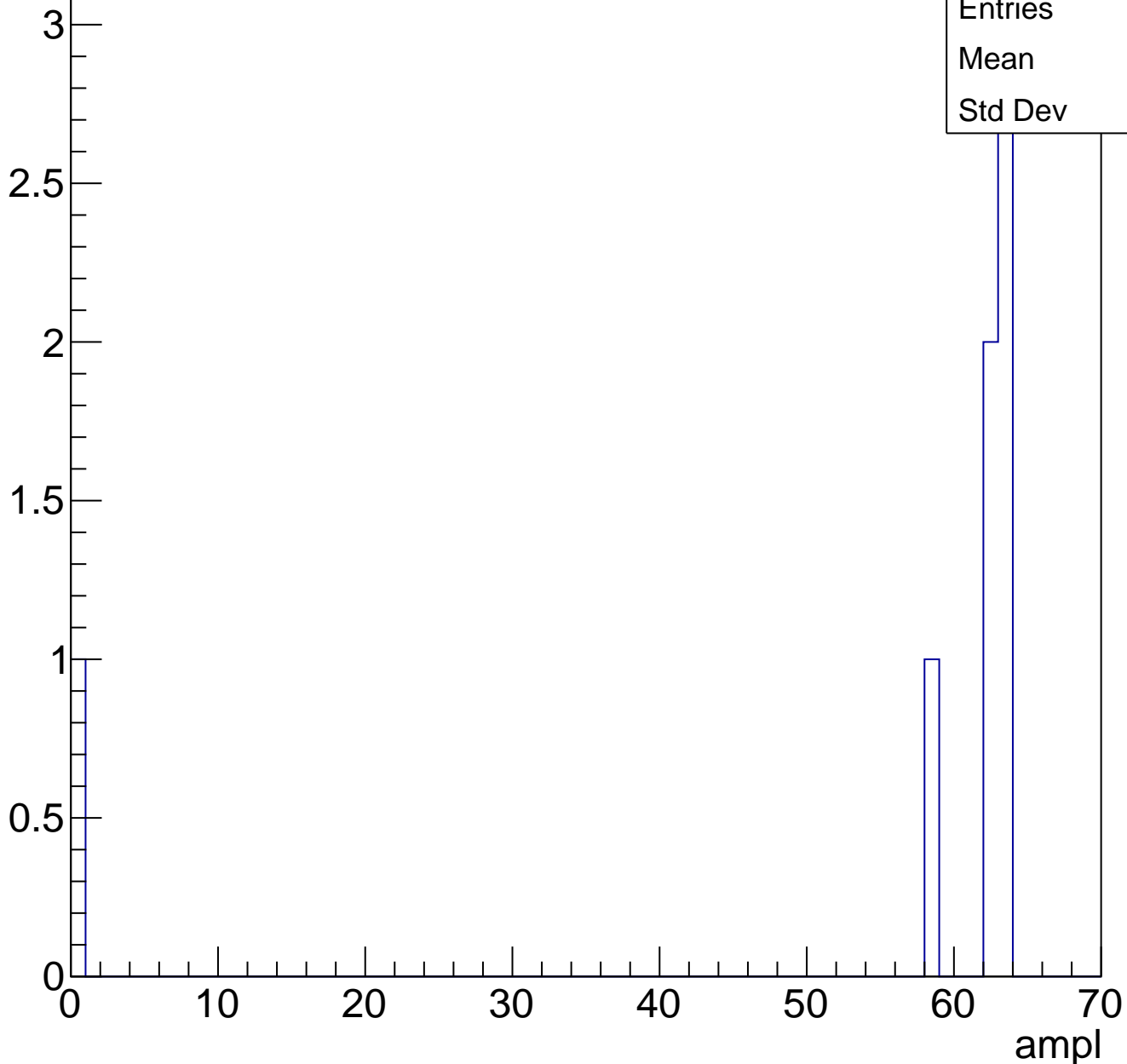


Entries	45
Mean	60.22
Std Dev	2.096

B1L103S, U13-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch9, adc0

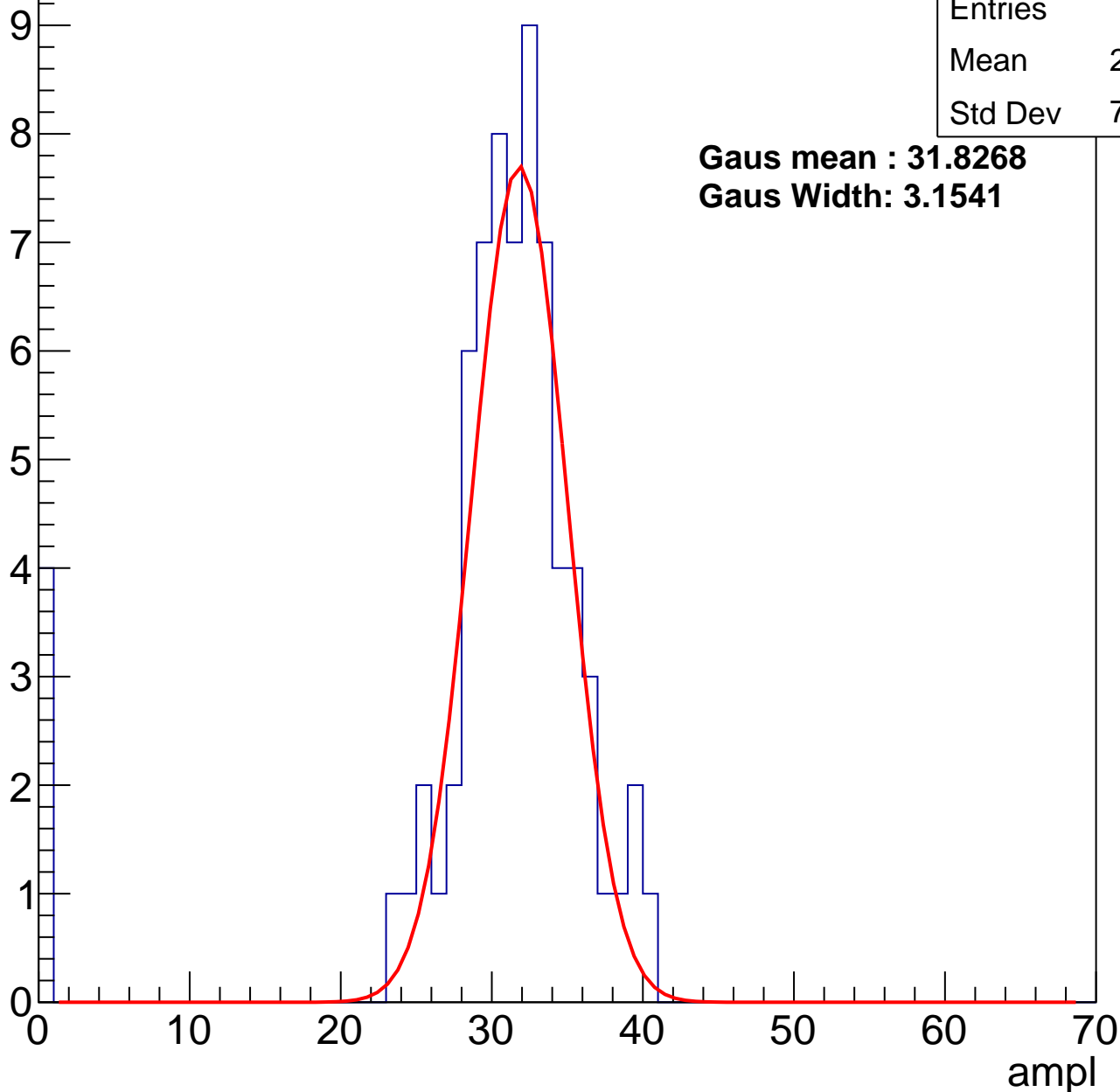
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	29.59
Std Dev	7.997

Gaus mean : 31.8268

Gaus Width: 3.1541



B1L103S, U13-ch9, adc1

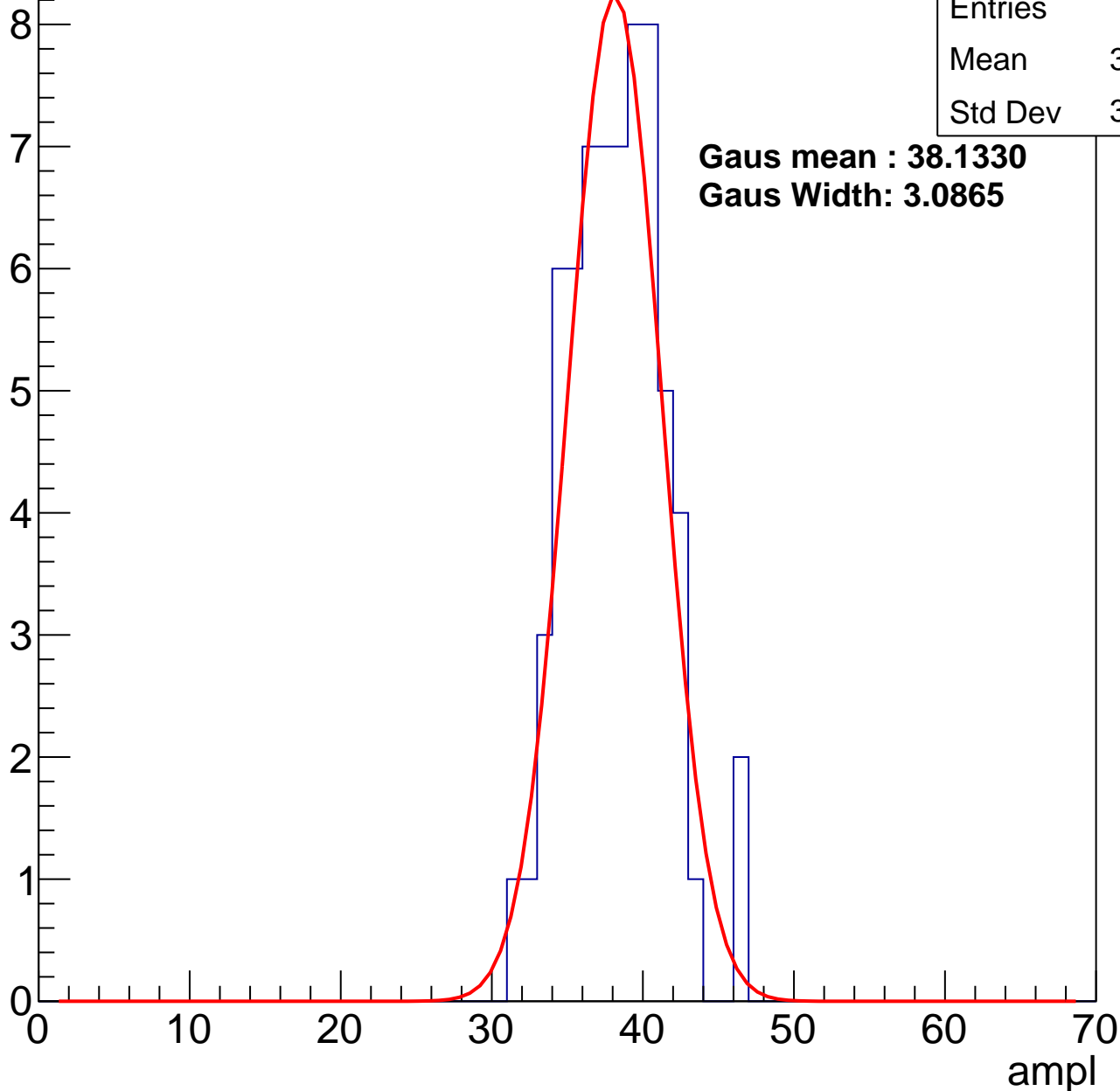
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	37.77
Std Dev	3.123

Gaus mean : 38.1330

Gaus Width: 3.0865



B1L103S, U13-ch9, adc2

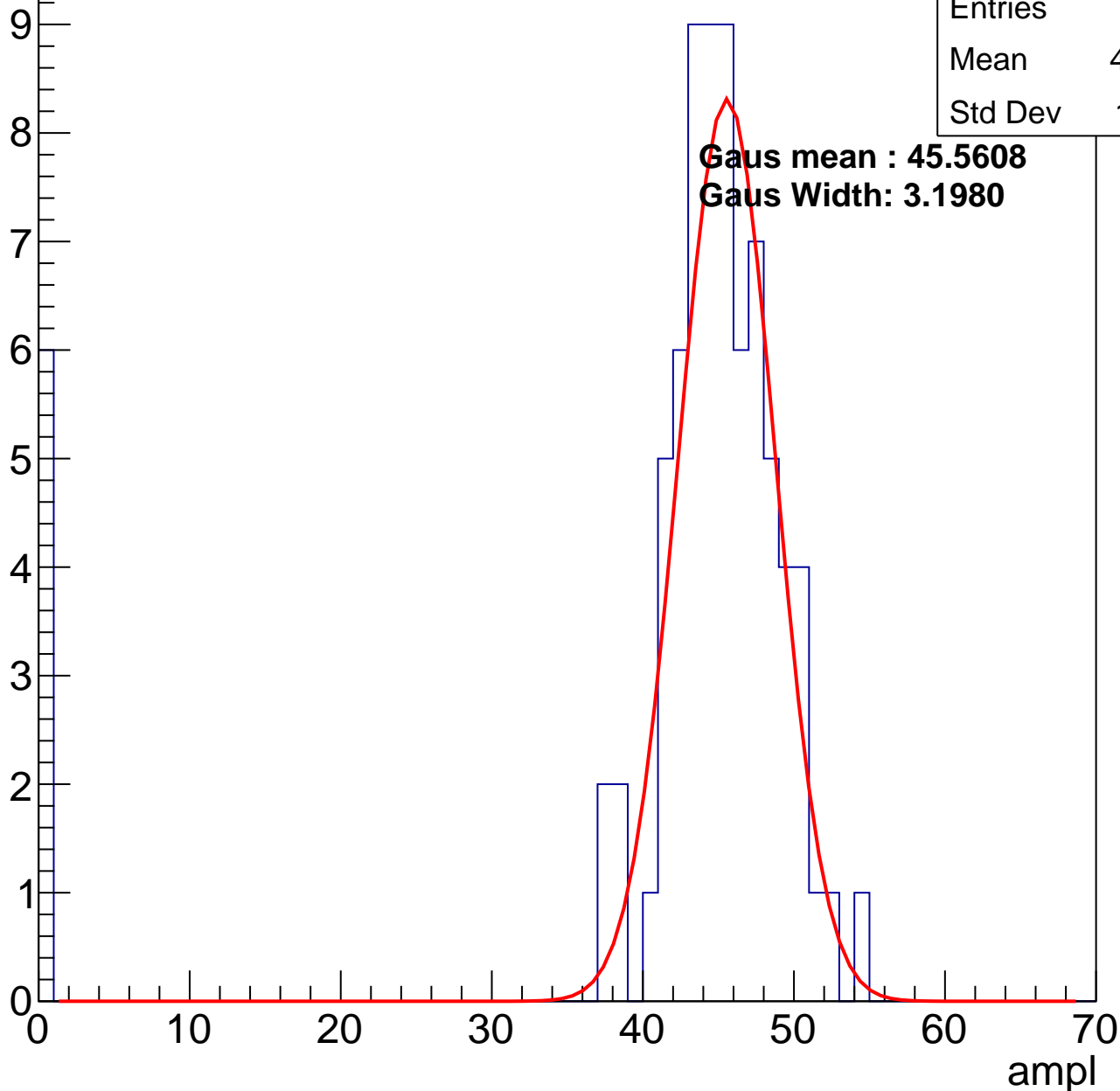
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	41.45
Std Dev	12.41

Gaus mean : 45.5608

Gaus Width: 3.1980

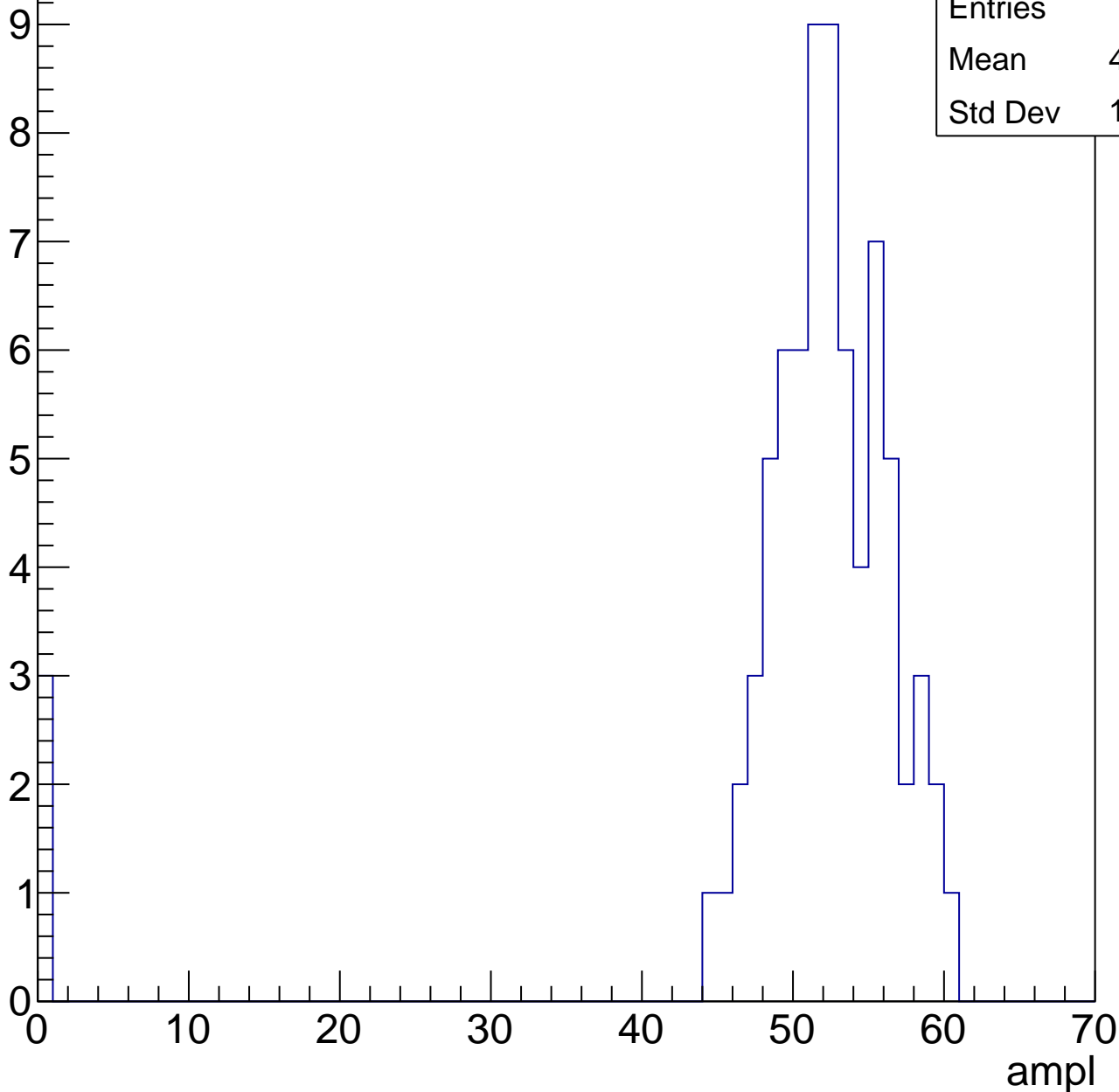


B1L103S, U13-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

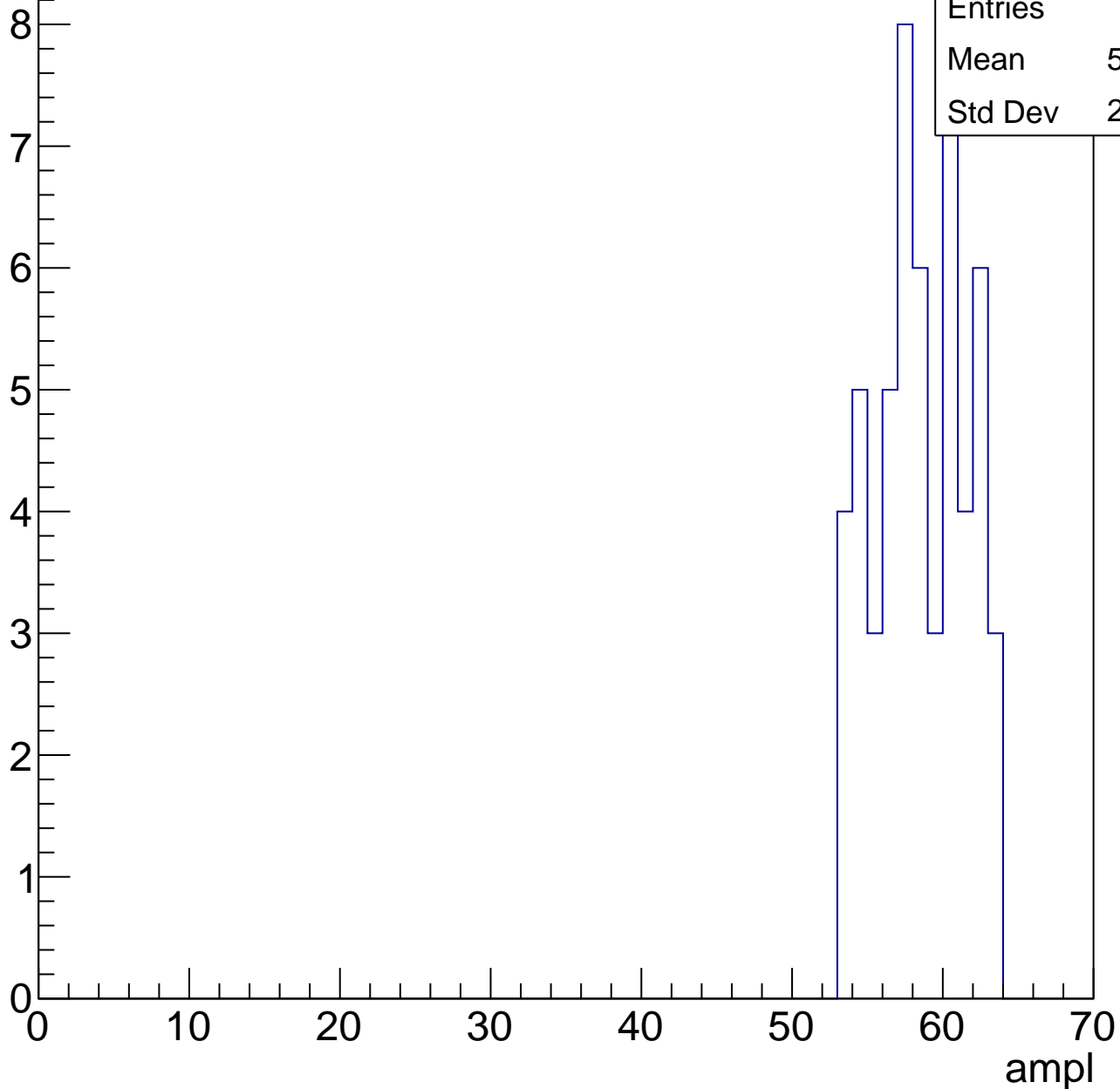
Entries	75
Mean	49.97
Std Dev	10.78



B1L103S, U13-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



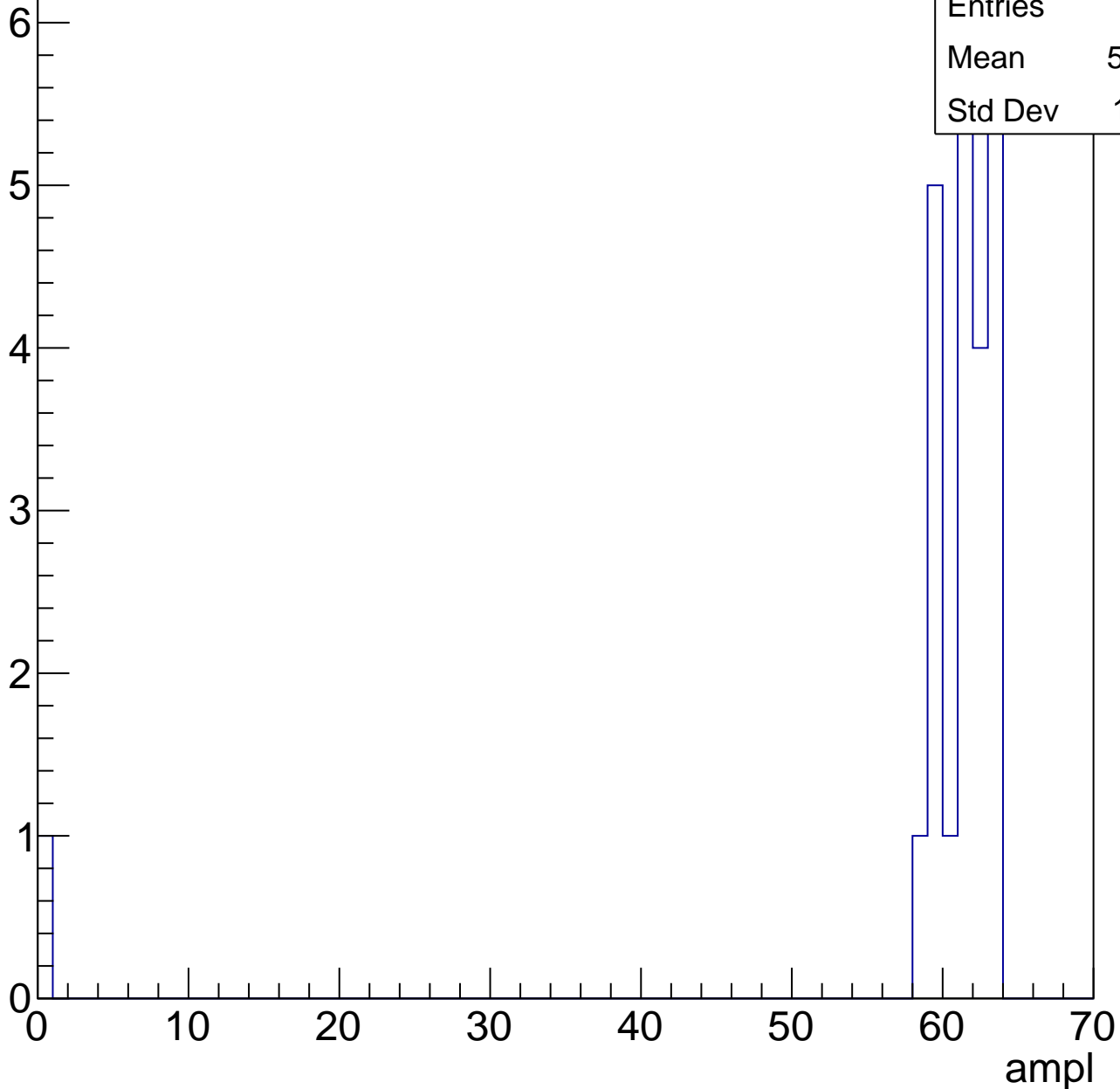
Entries	55
Mean	58.05
Std Dev	2.944

B1L103S, U13-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.54
Std Dev	12.31



B1L103S, U13-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

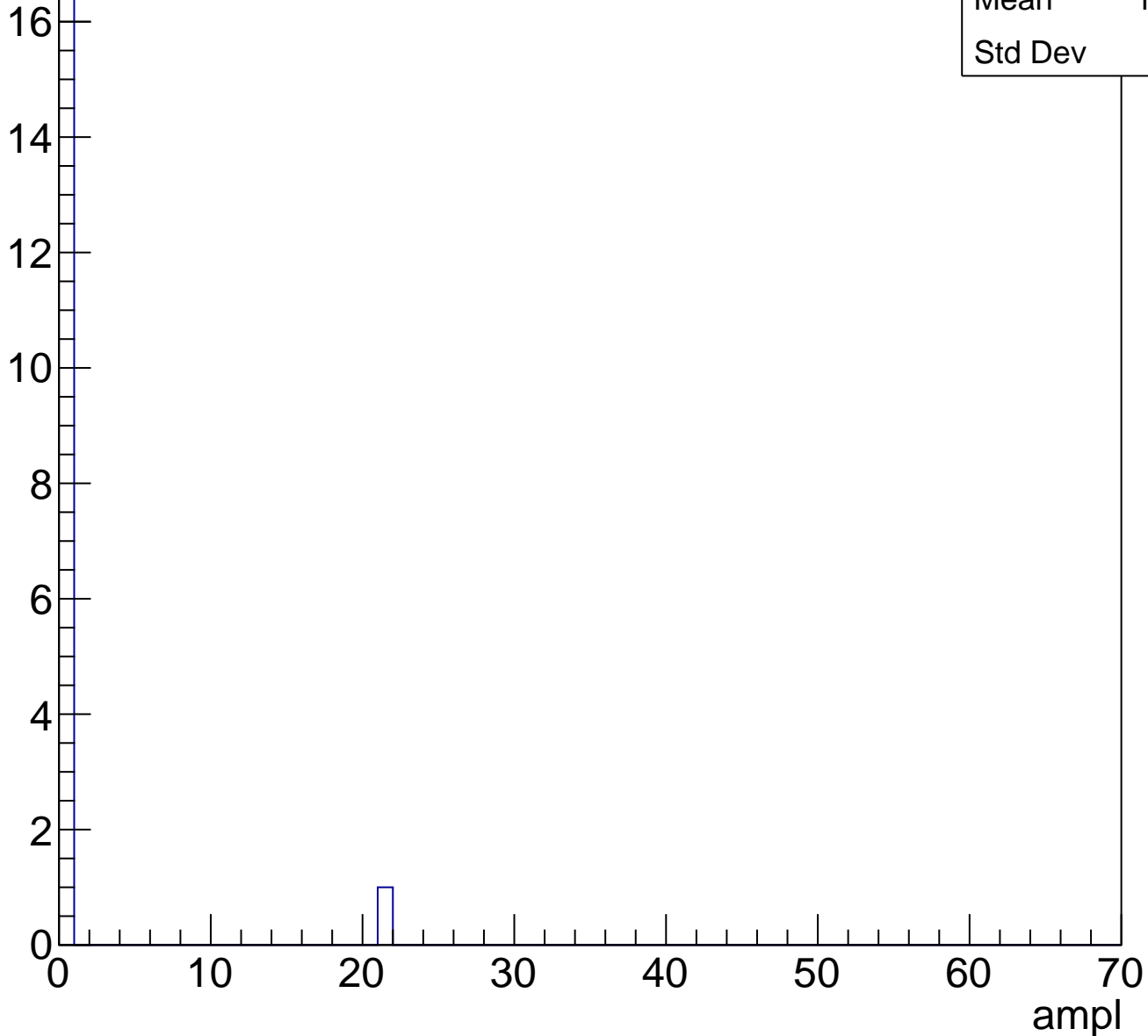
ampl

B1L103S, U13-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.167
Std Dev	4.81

Entry



B1L103S, U13-ch10, adc0

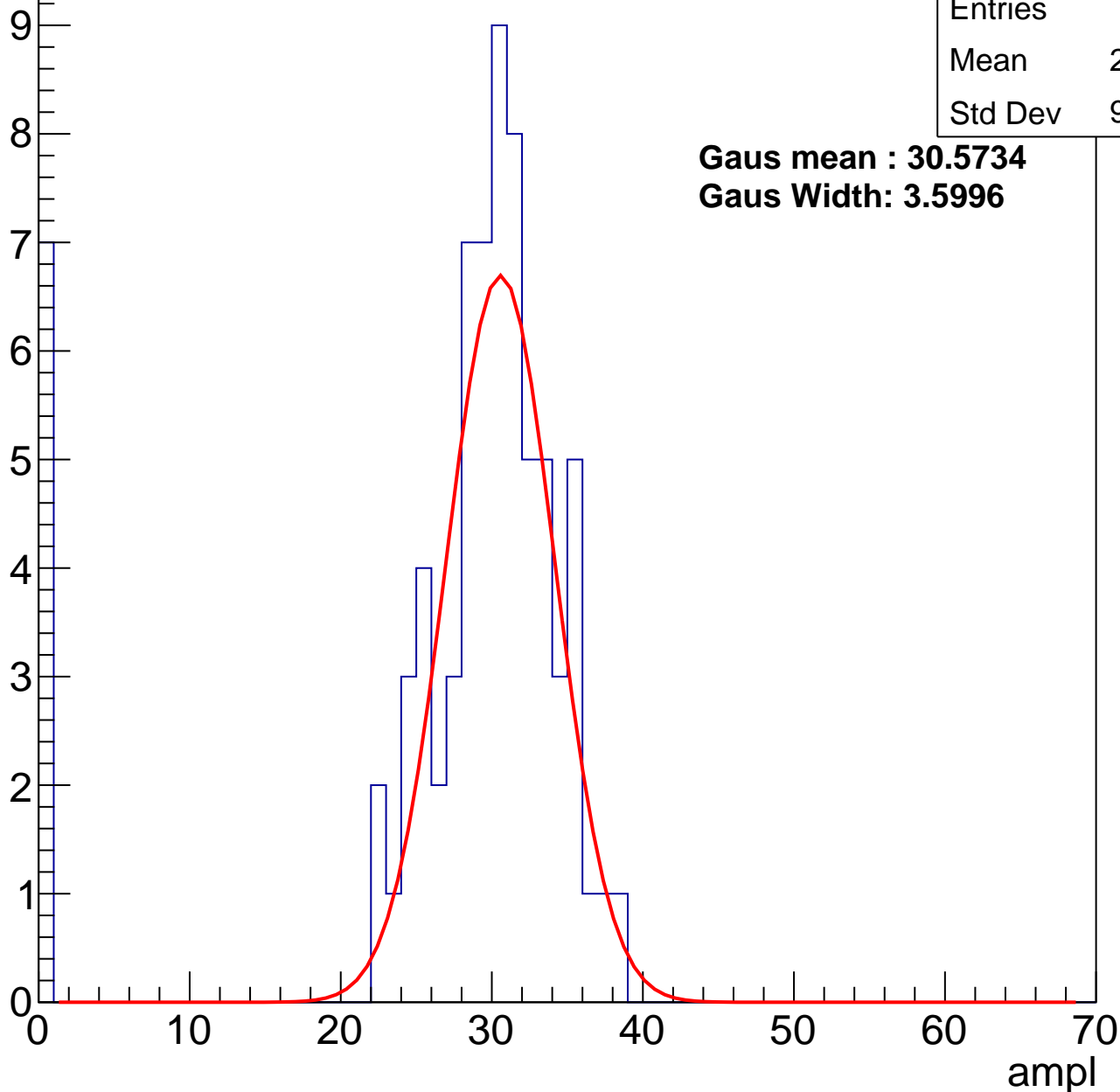
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	27.05
Std Dev	9.395

Gaus mean : 30.5734

Gaus Width: 3.5996



B1L103S, U13-ch10, adc1

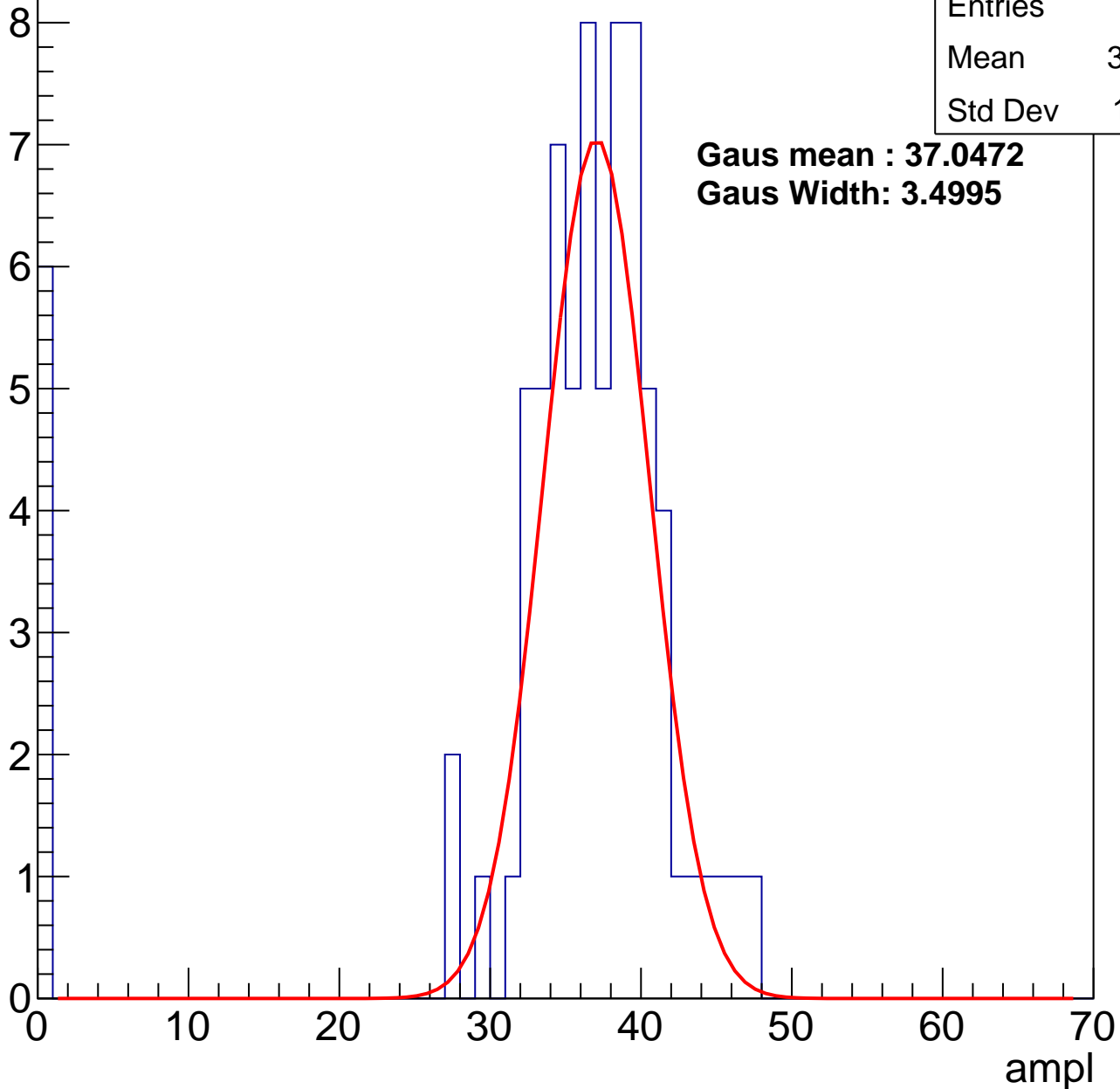
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	33.84
Std Dev	10.61

Gaus mean : 37.0472

Gaus Width: 3.4995



B1L103S, U13-ch10, adc2

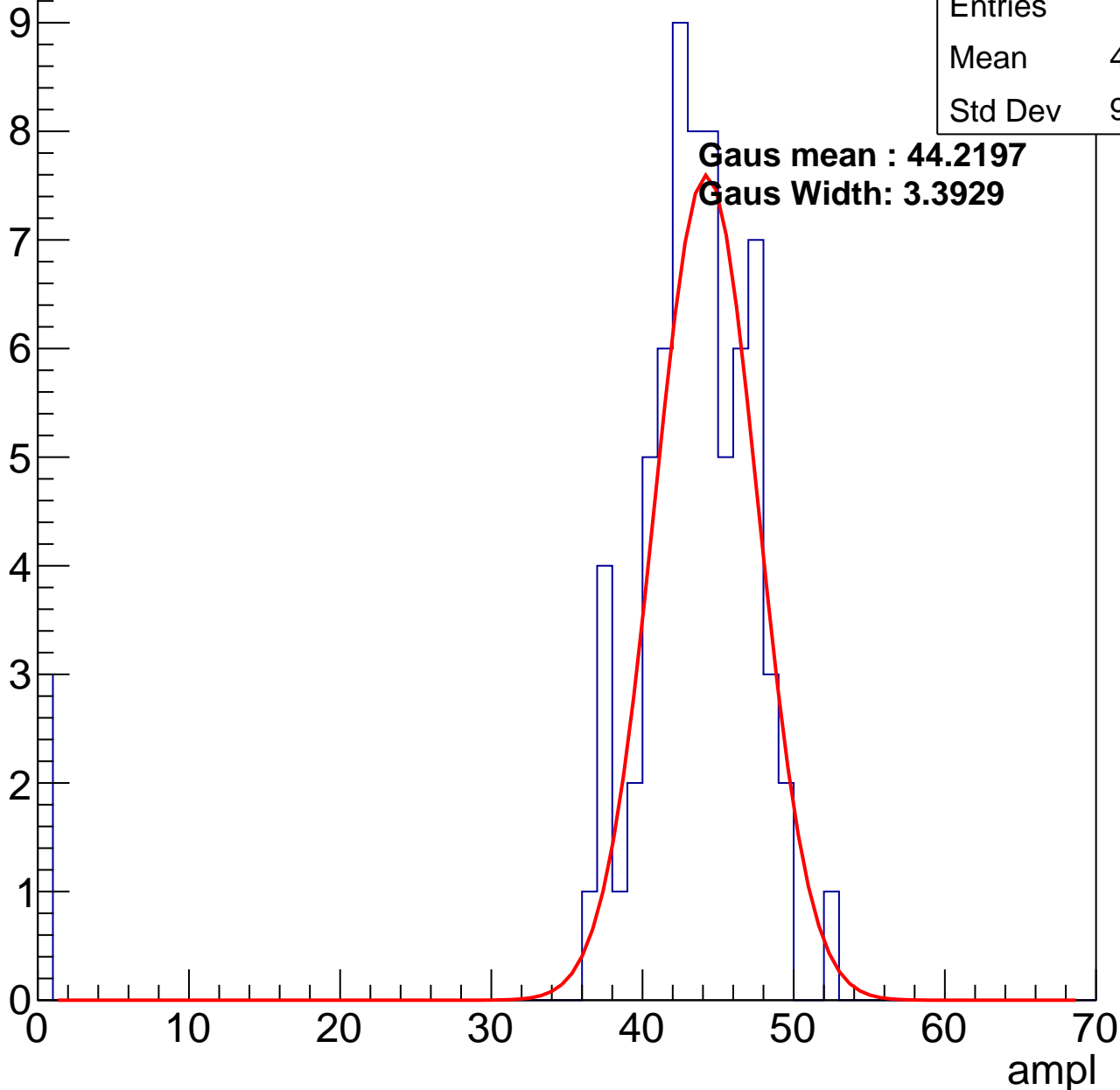
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	41.46
Std Dev	9.292

Gaus mean : 44.2197

Gaus Width: 3.3929

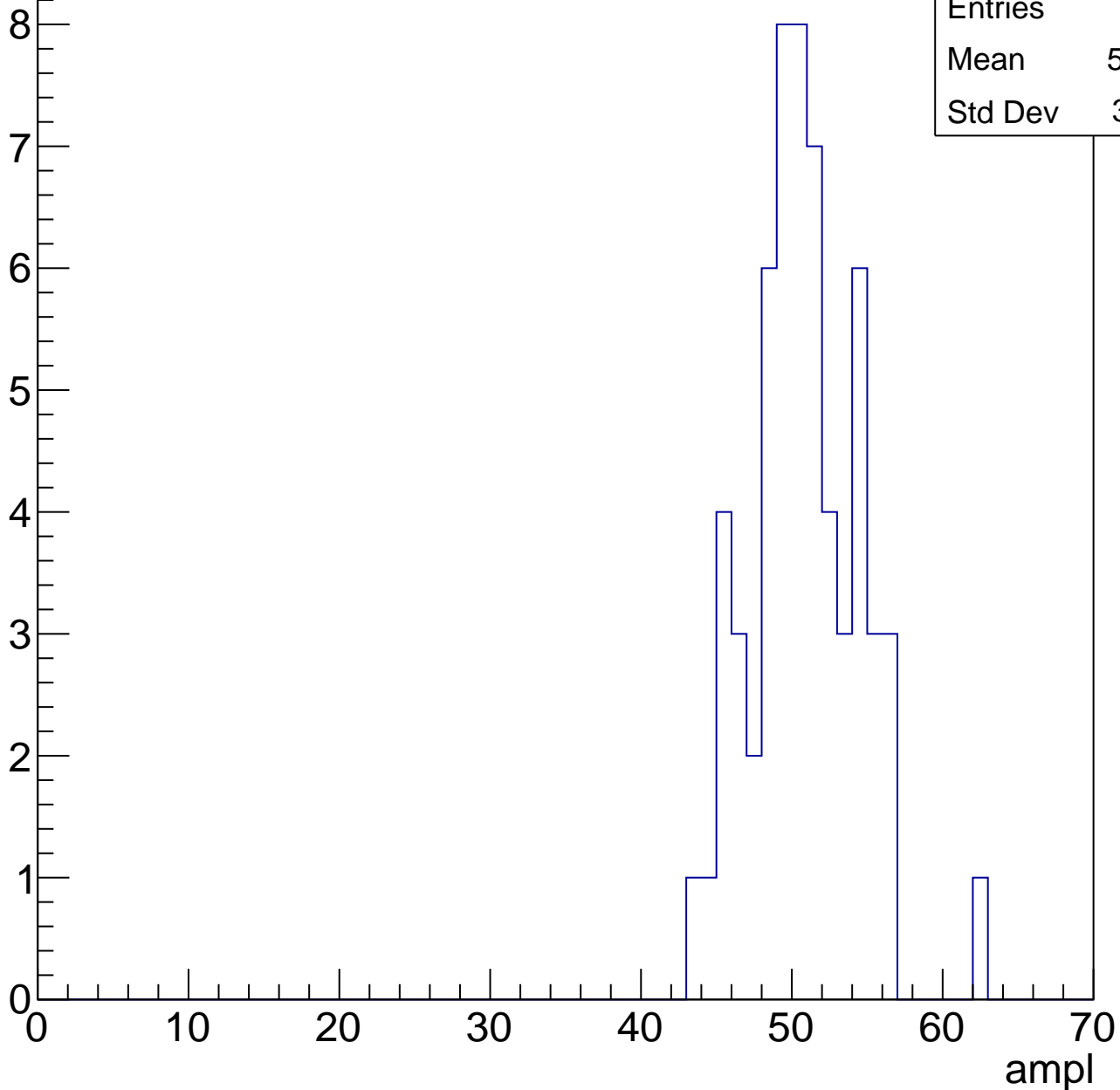


B1L103S, U13-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	50.37
Std Dev	3.531

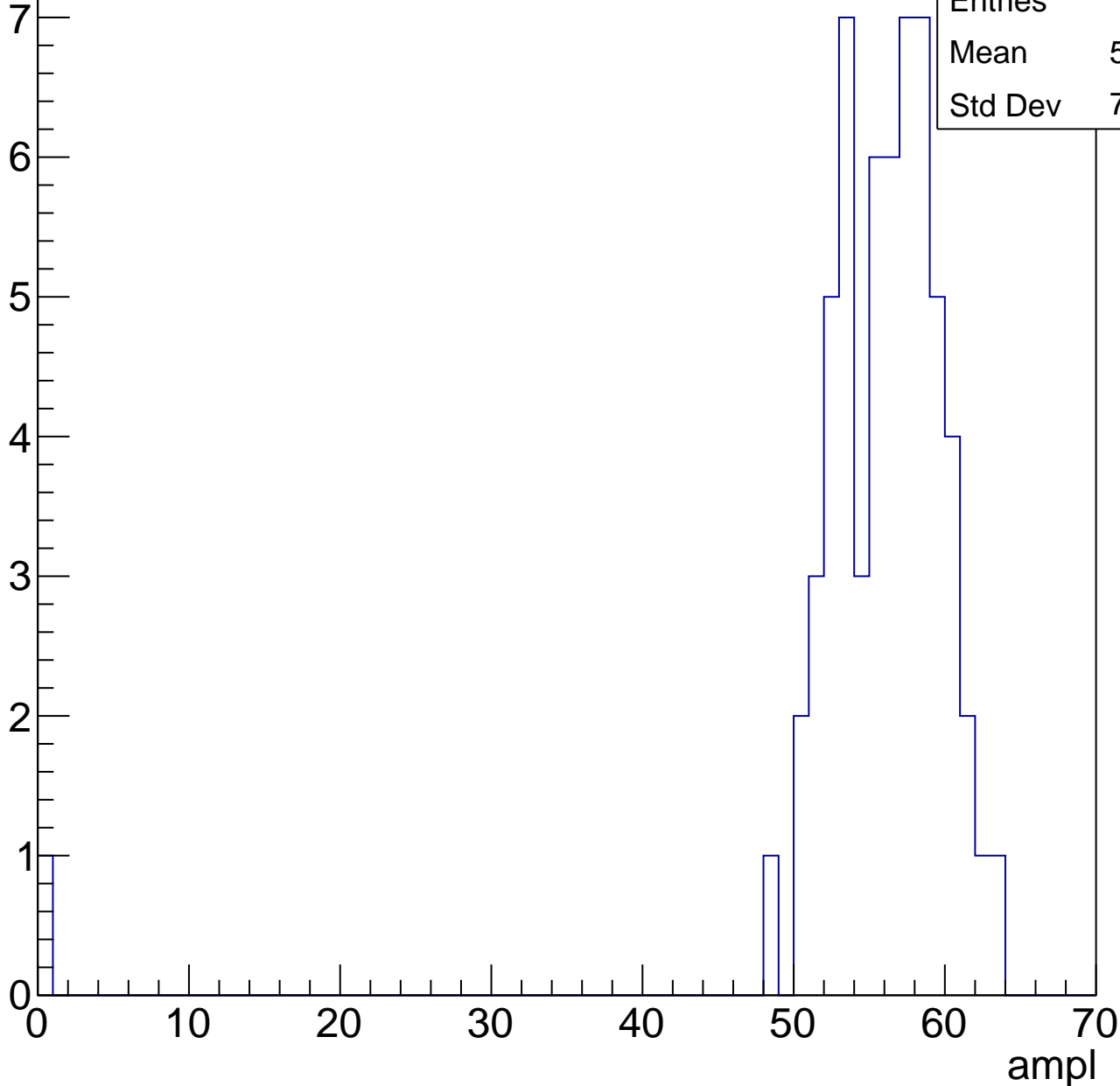


B1L103S, U13-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

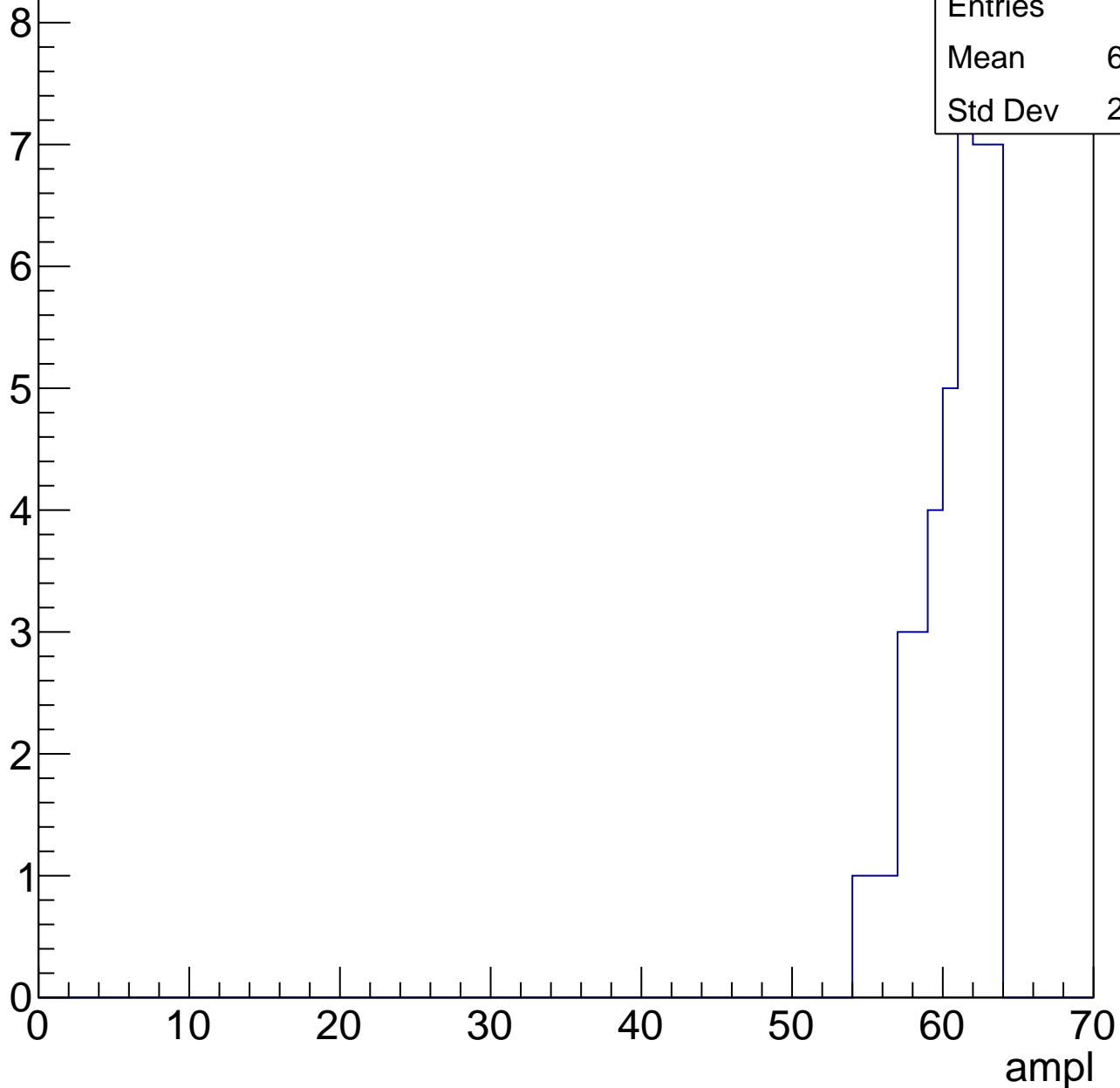
Entries	61
Mean	54.87
Std Dev	7.798



B1L103S, U13-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

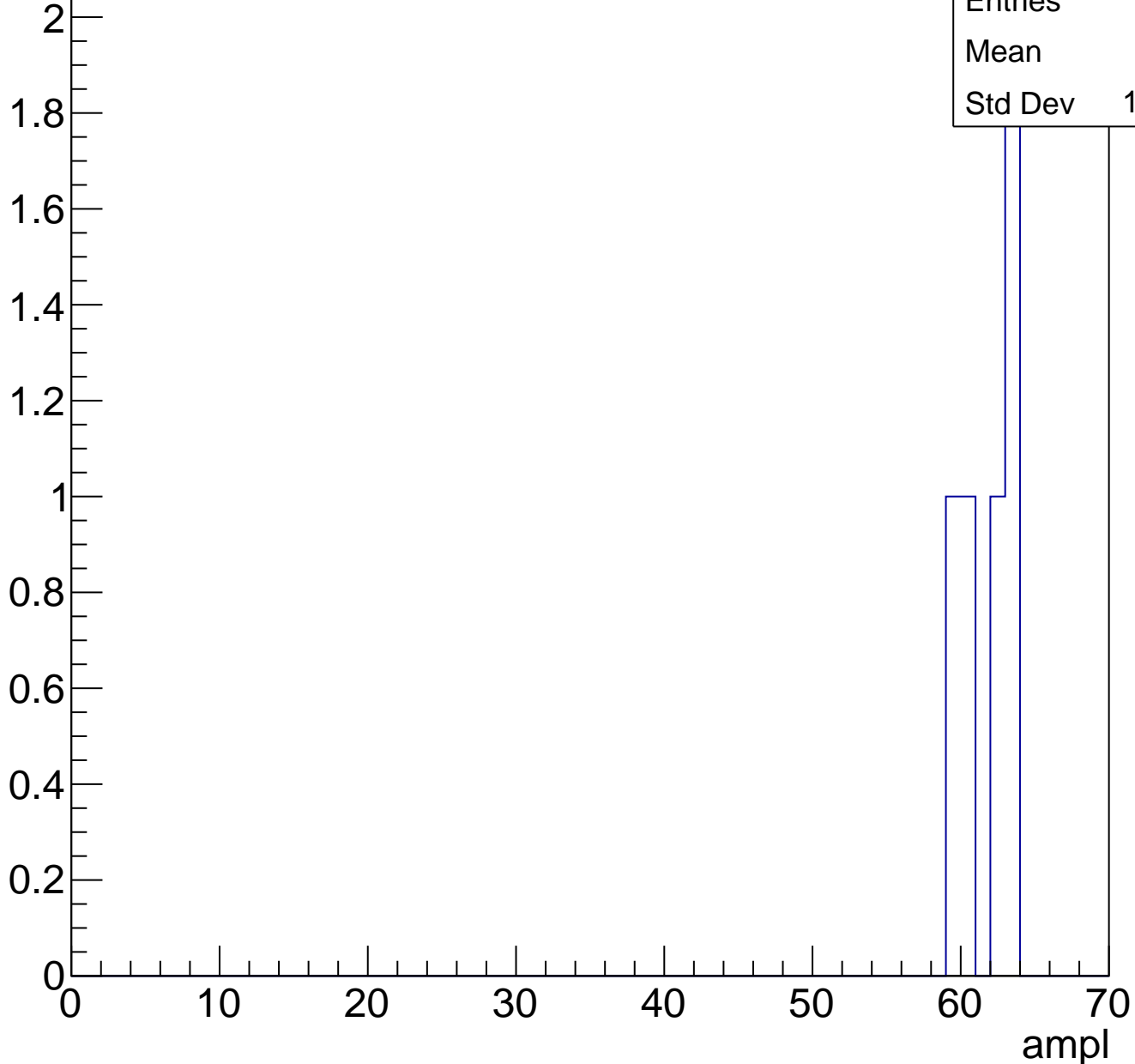
Entry



B1L103S, U13-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

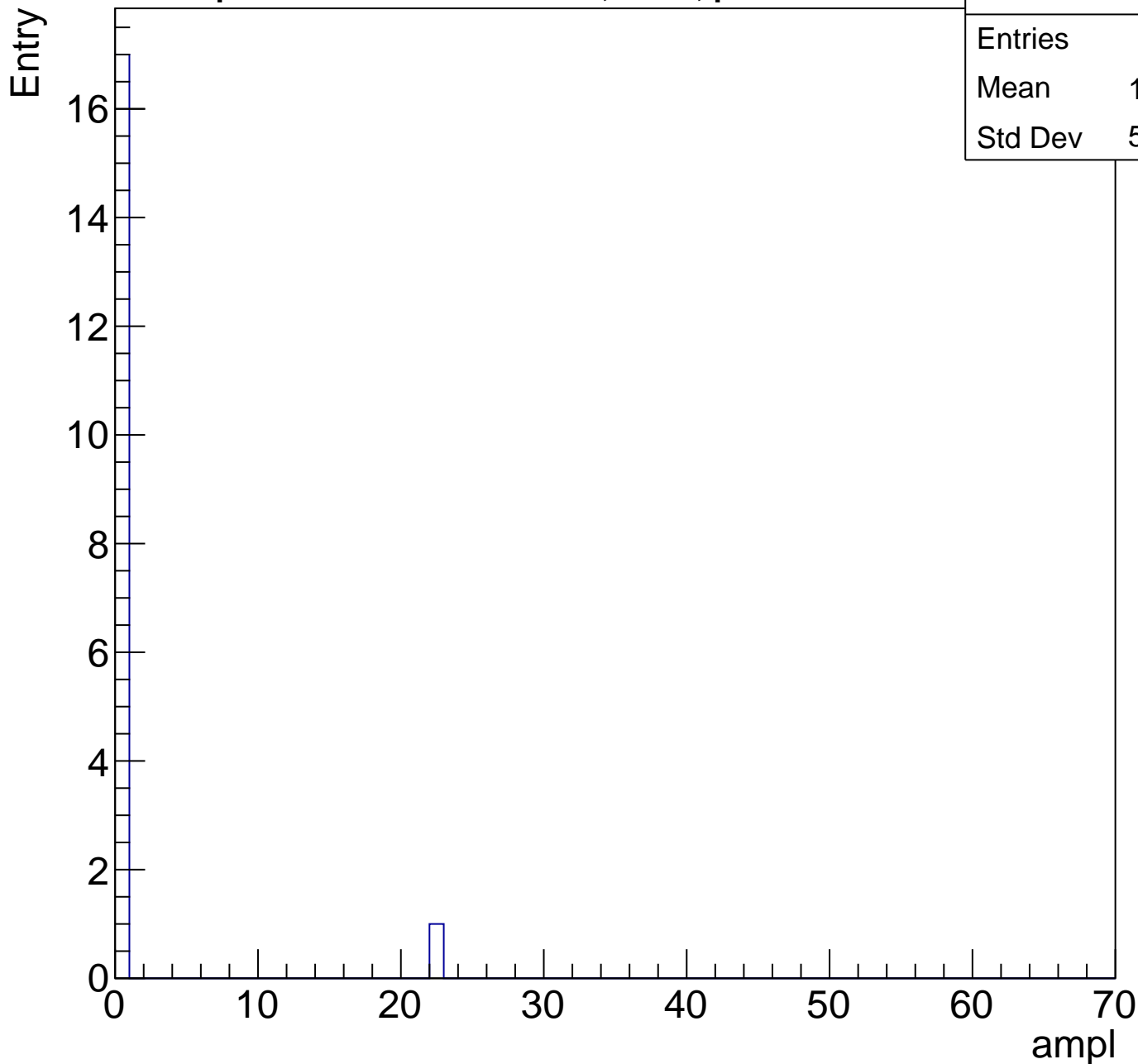
Entry



B1L103S, U13-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.222
Std Dev	5.039



B1L103S, U13-ch11, adc0

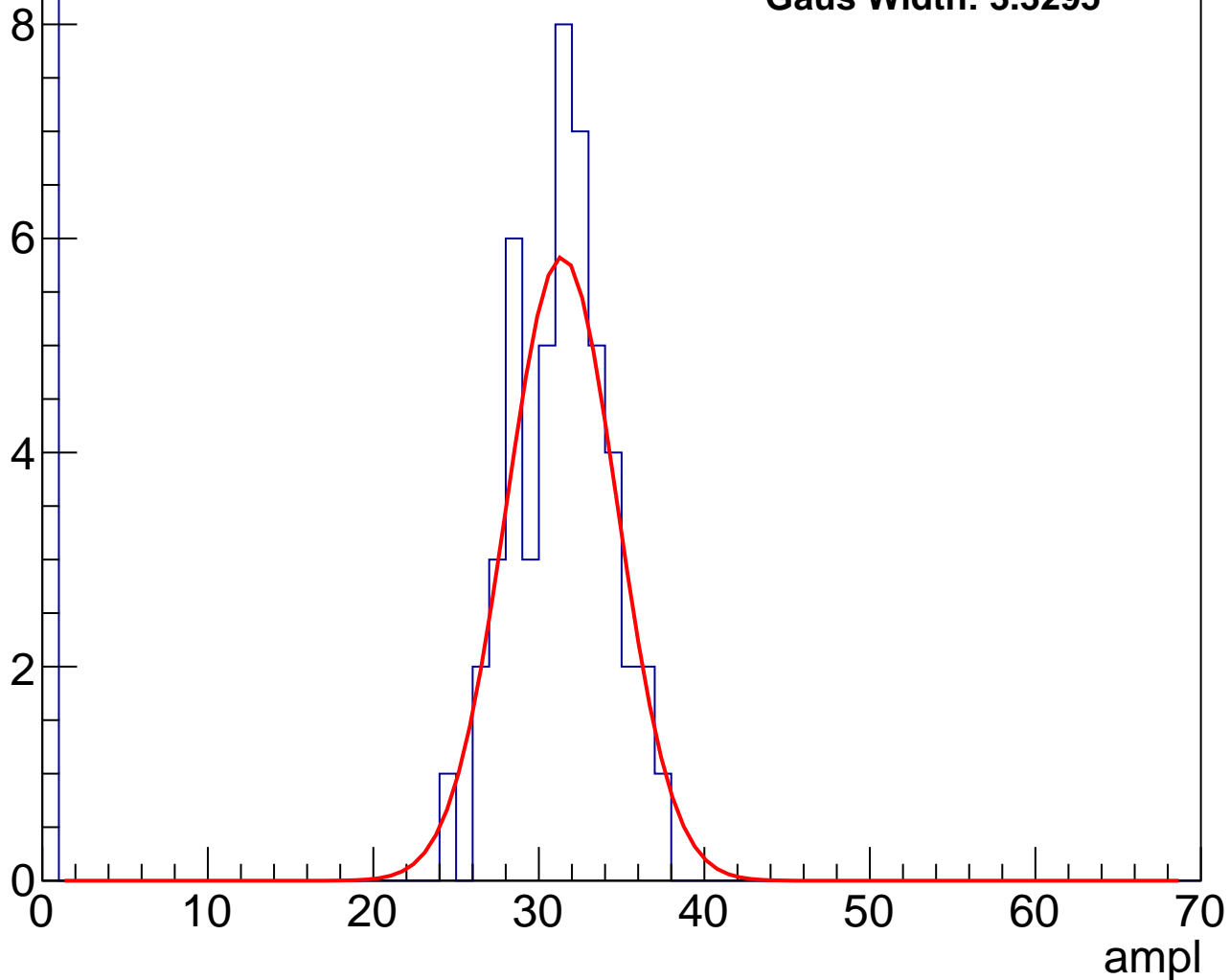
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	25.66
Std Dev	11.88

Gaus mean : 31.3931

Gaus Width: 3.3295



B1L103S, U13-ch11, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	31.47
Std Dev	13.07

Gaus mean : 37.0011

Gaus Width: 4.0361

Entry

10

8

6

4

2

0

0

10

20

30

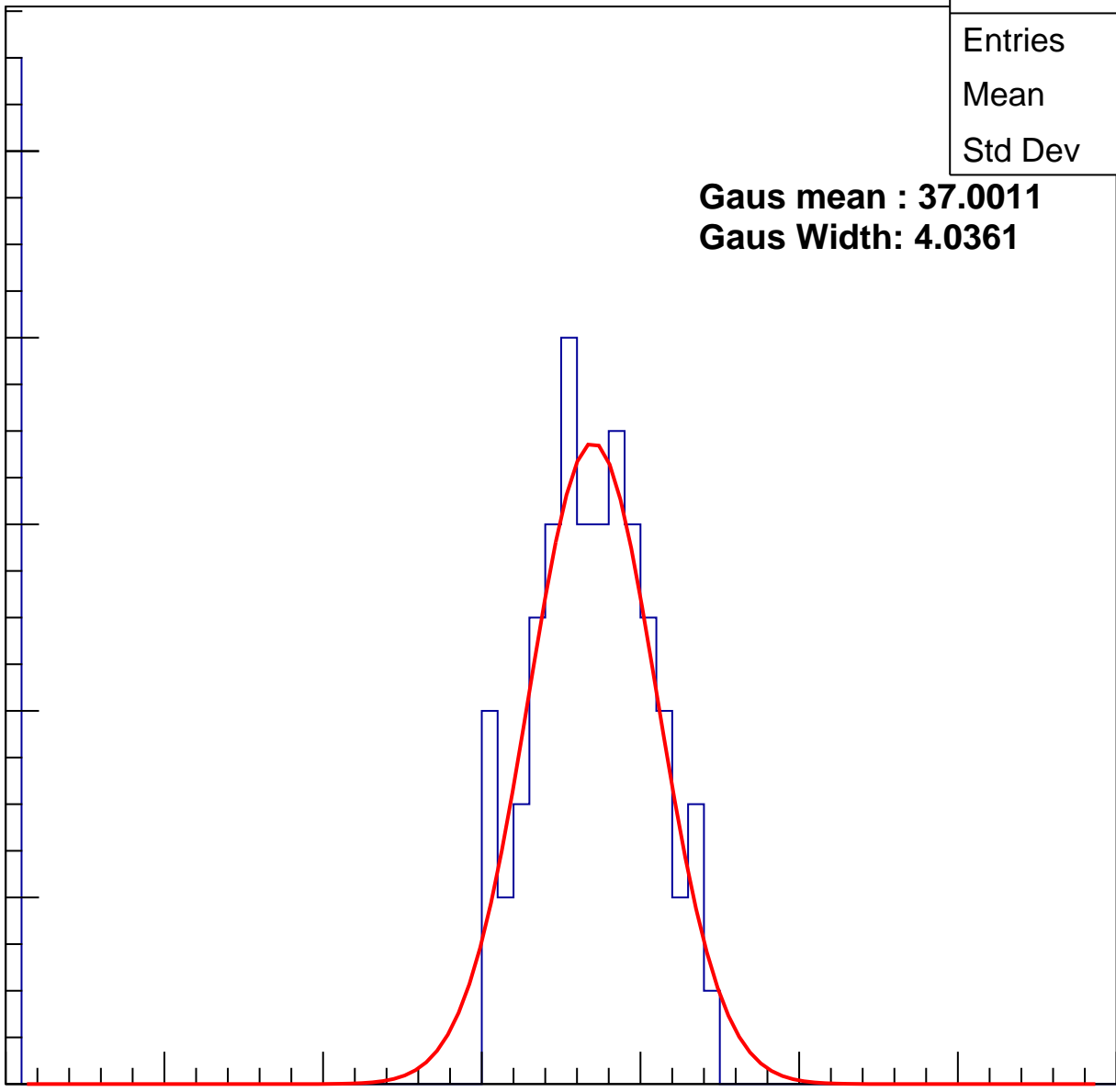
40

50

60

70

ampl



B1L103S, U13-ch11, adc2

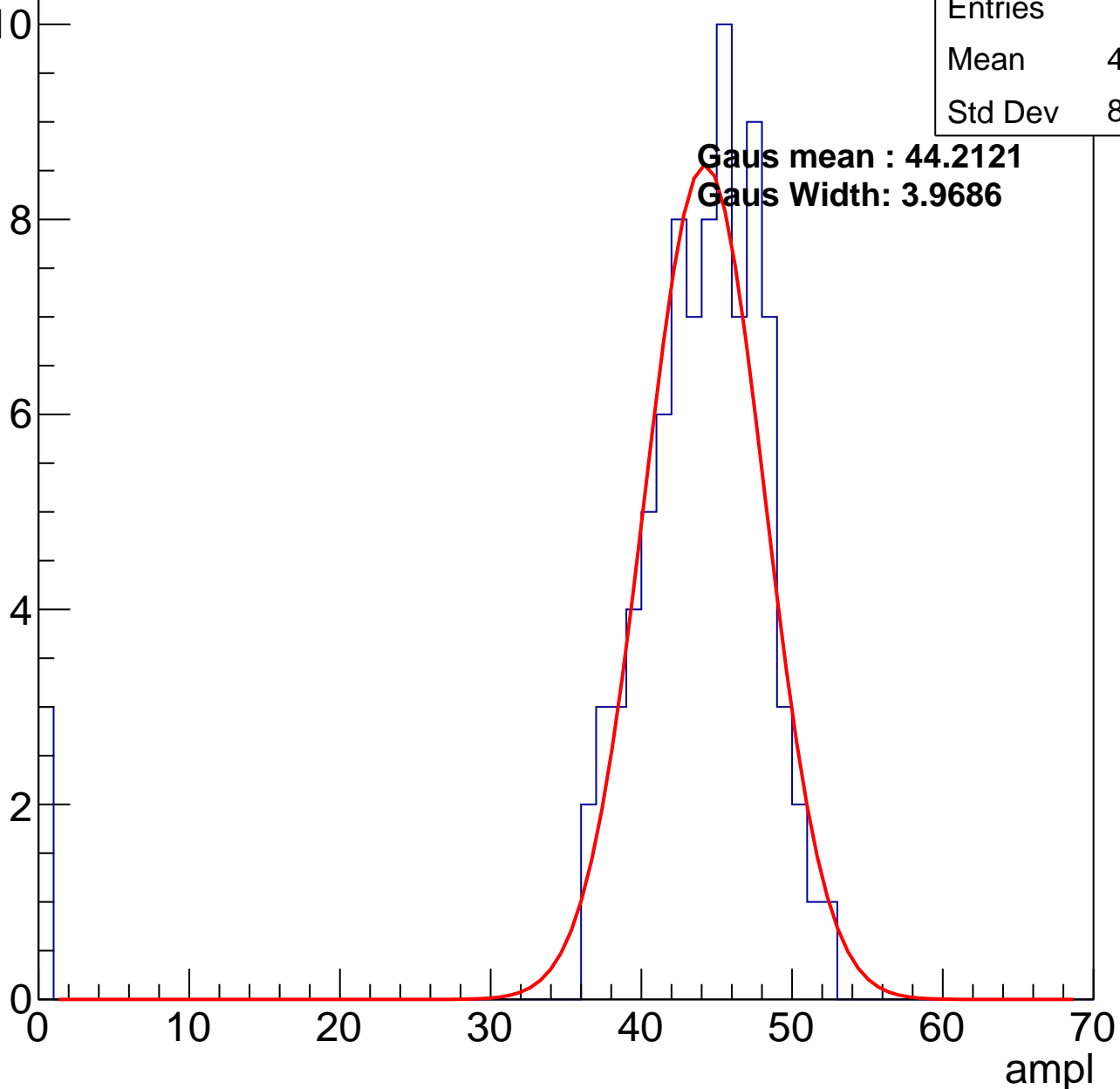
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	42.35
Std Dev	8.684

Gaus mean : 44.2121

Gaus Width: 3.9686

Entry

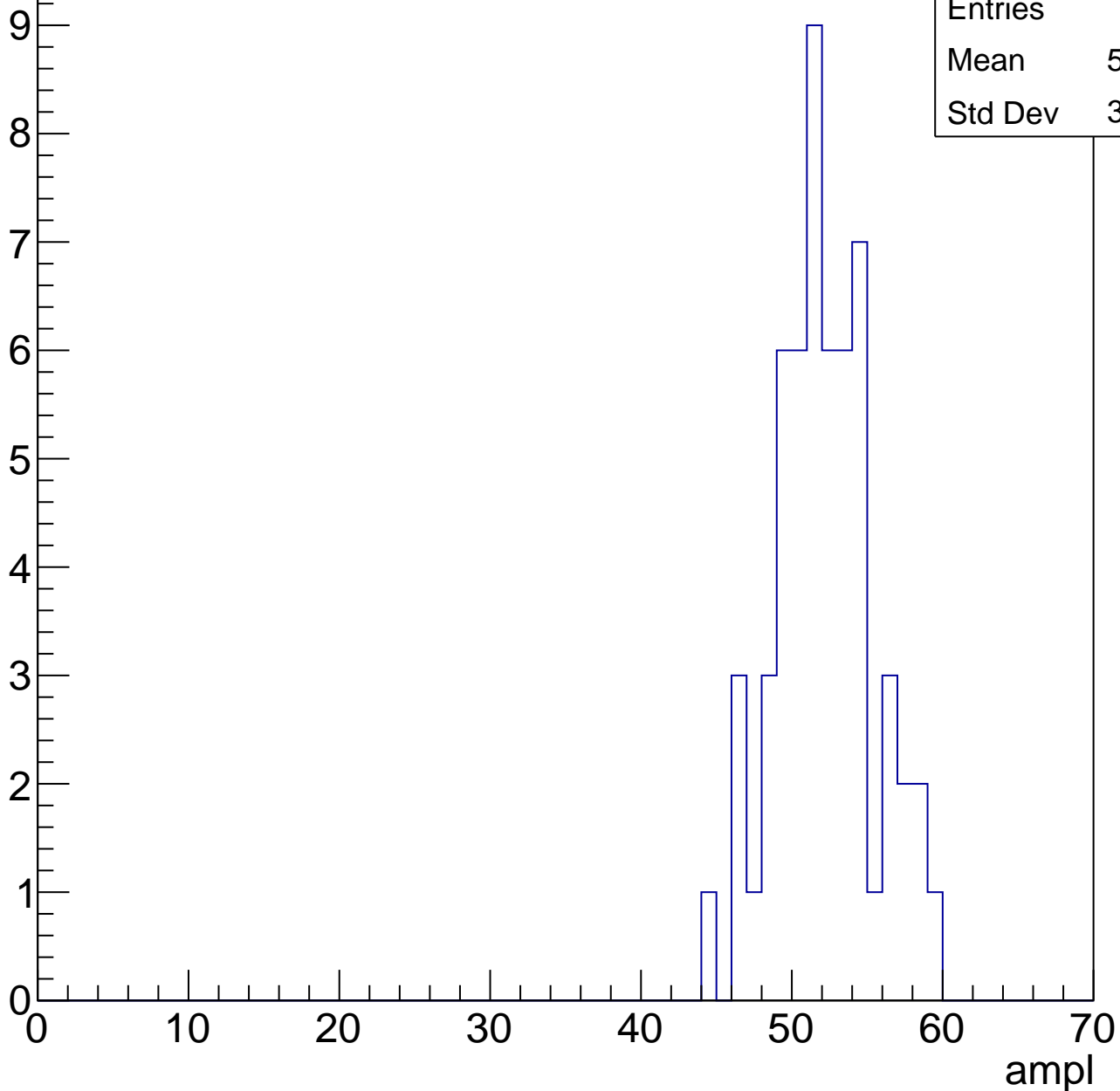


B1L103S, U13-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

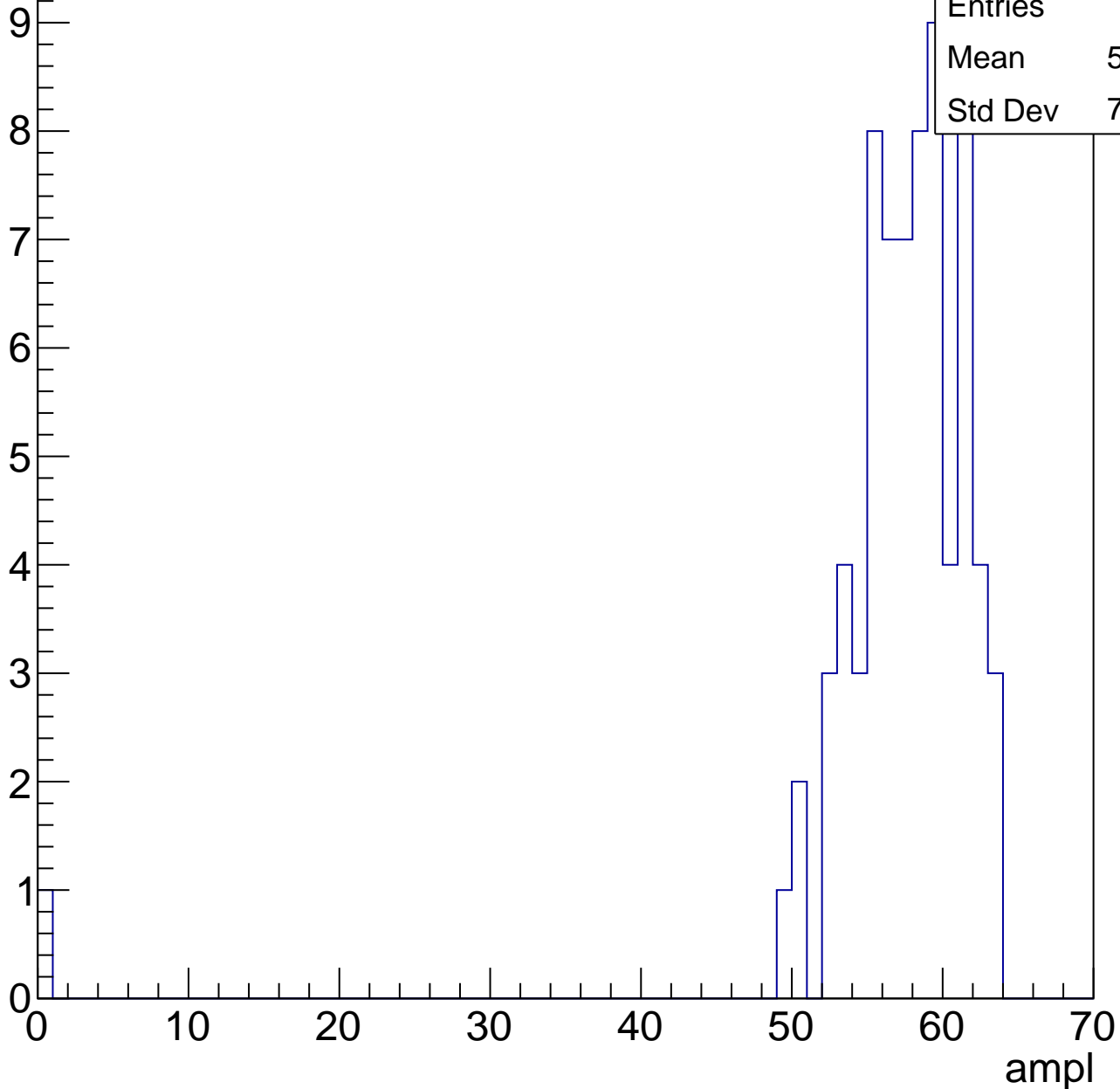
Entries	57
Mean	51.68
Std Dev	3.213



B1L103S, U13-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

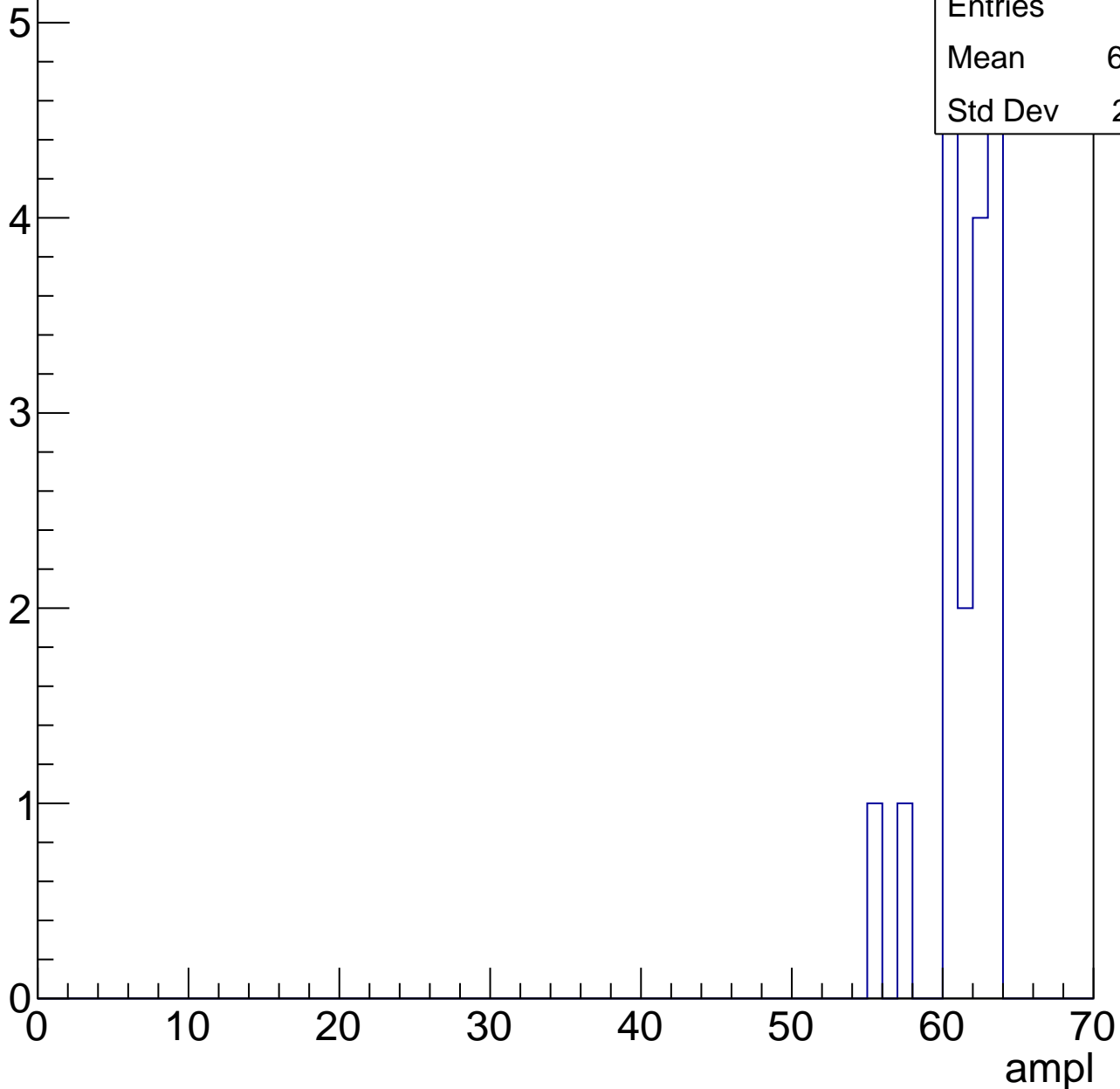


B1L103S, U13-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

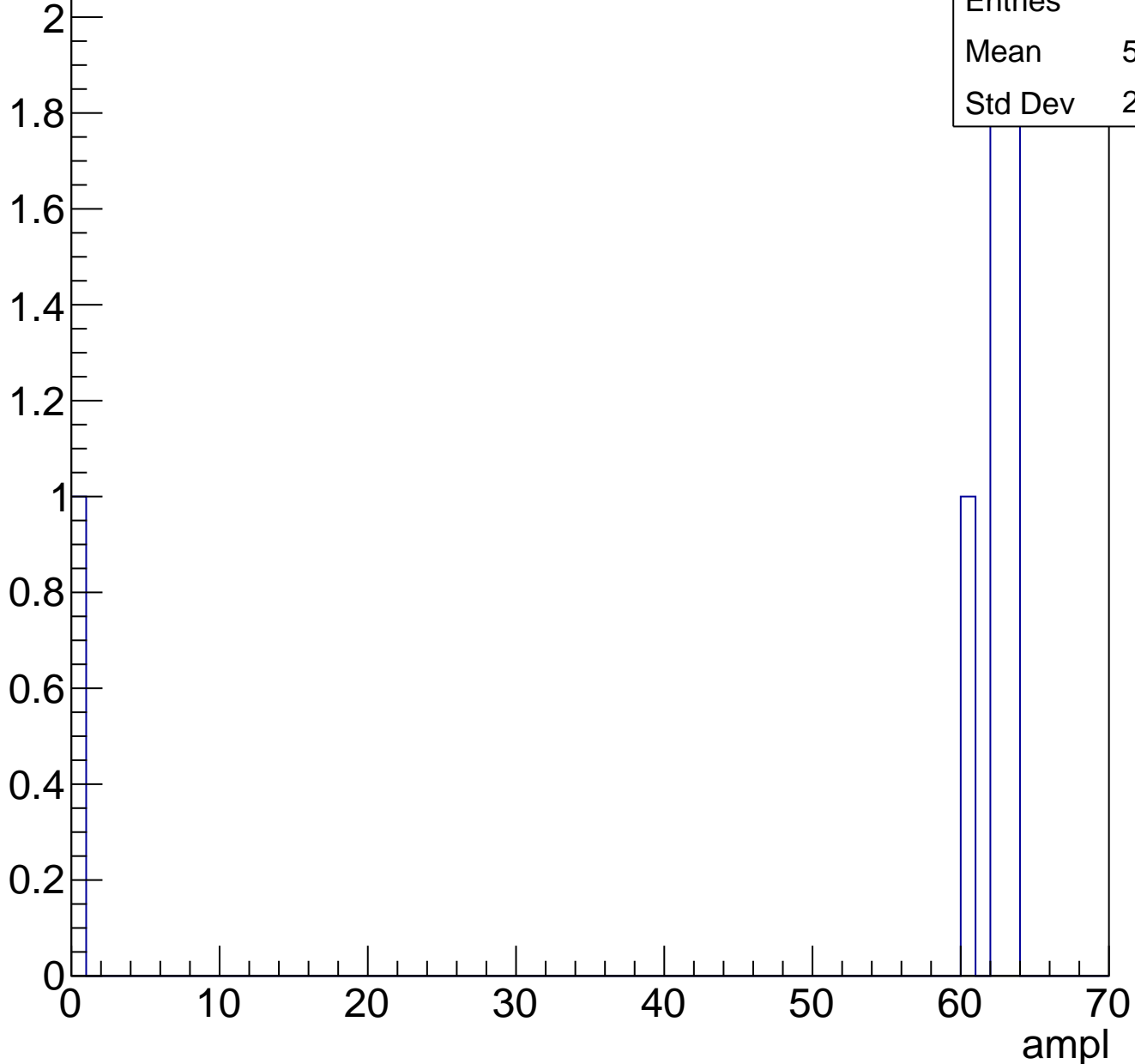
Entries	18
Mean	60.94
Std Dev	2.121



B1L103S, U13-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136

Entry



B1L103S, U13-ch12, adc0

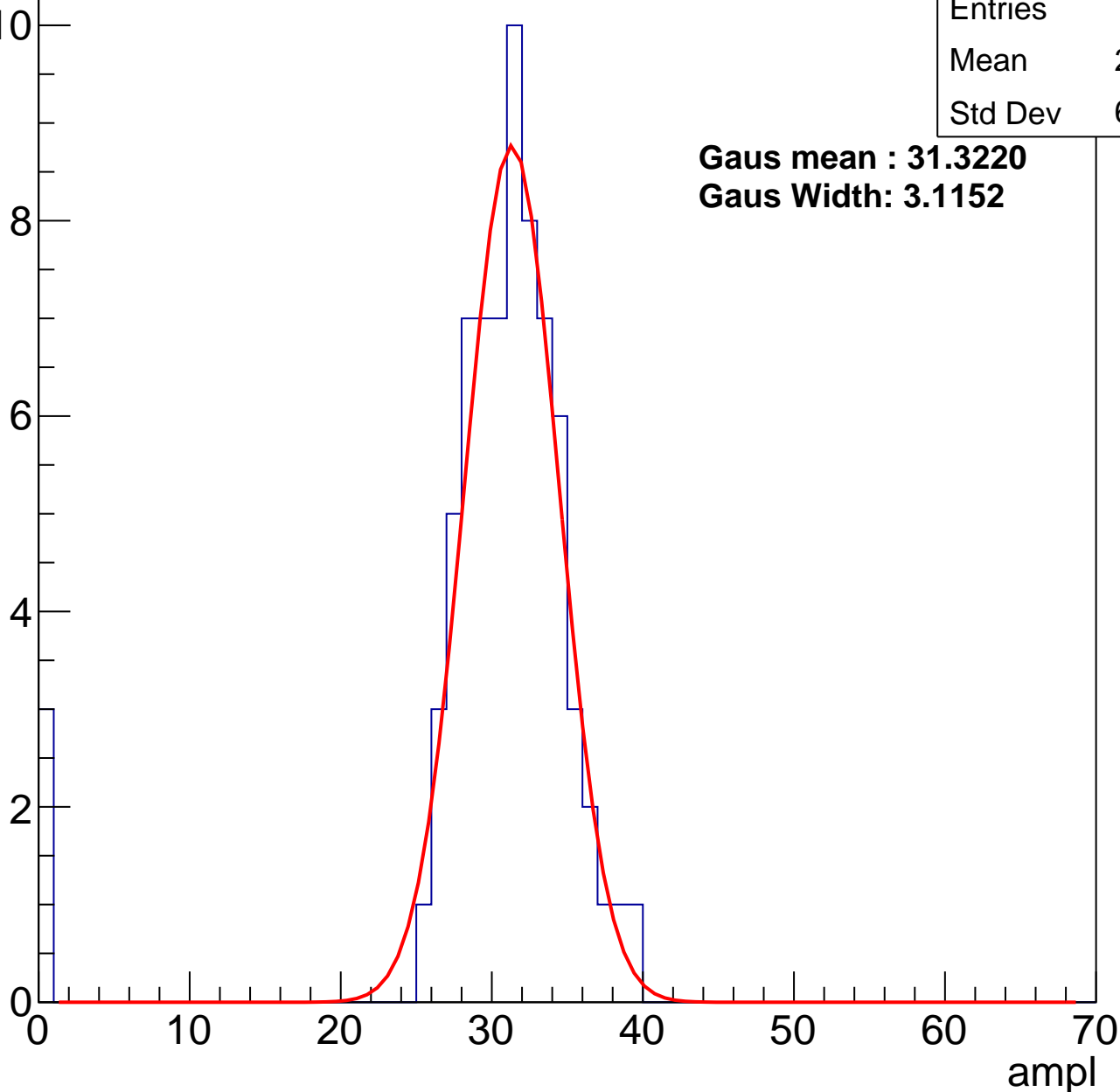
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	29.71
Std Dev	6.861

Gaus mean : 31.3220

Gaus Width: 3.1152



B1L103S, U13-ch12, adc1

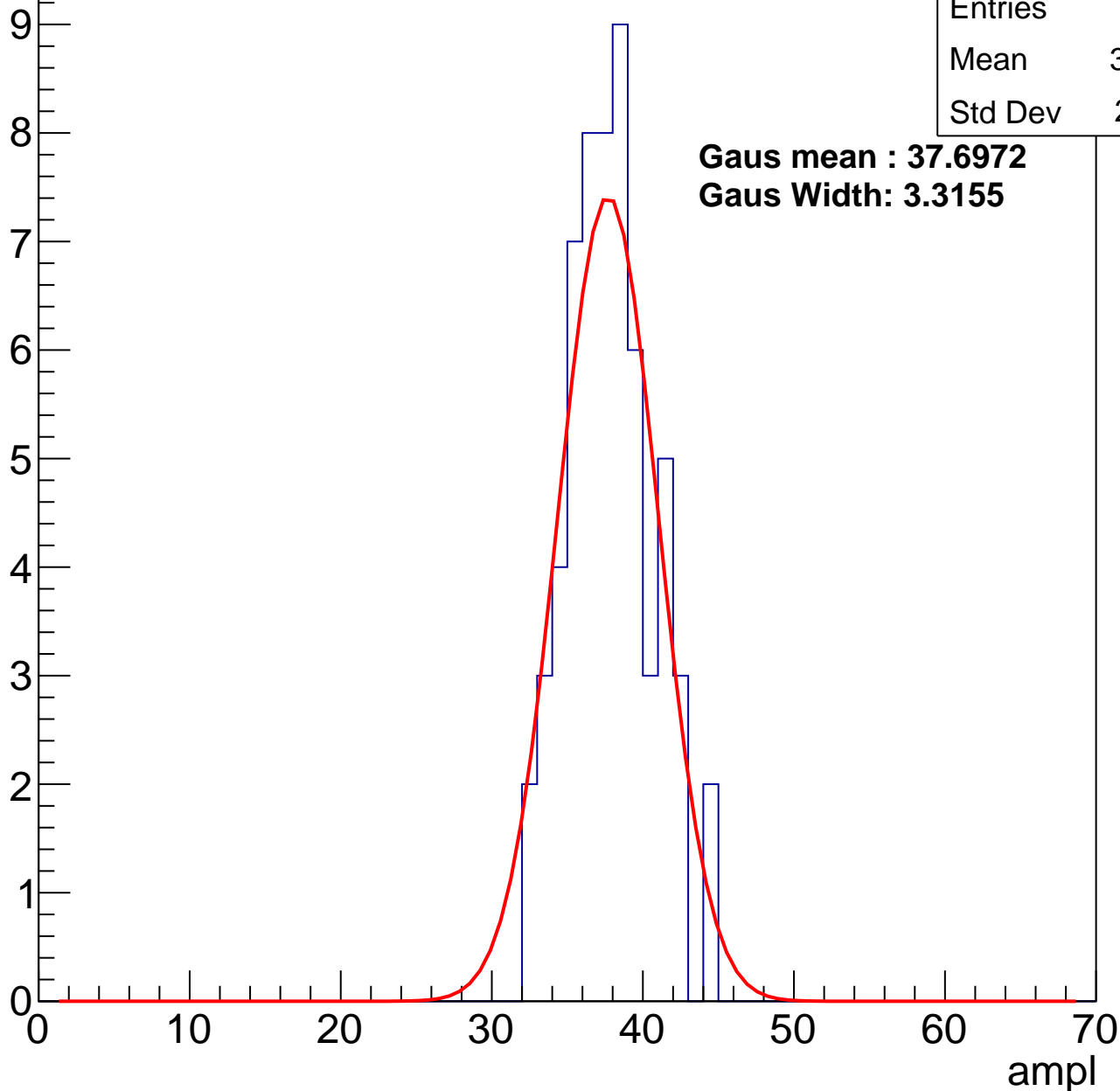
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	37.38
Std Dev	2.811

Gaus mean : 37.6972

Gaus Width: 3.3155



B1L103S, U13-ch12, adc2

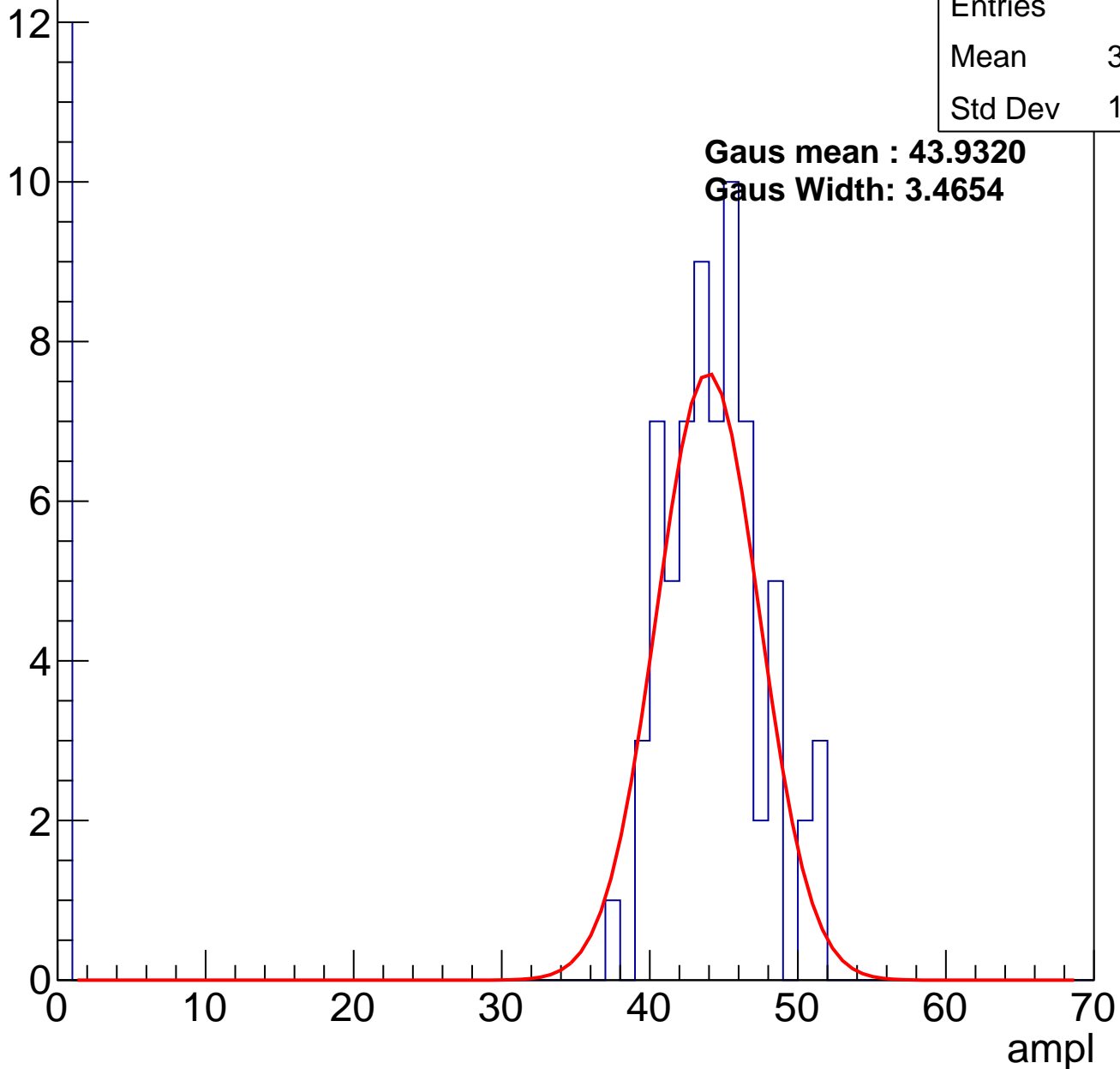
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	37.34
Std Dev	15.95

Gaus mean : 43.9320

Gaus Width: 3.4654

Entry

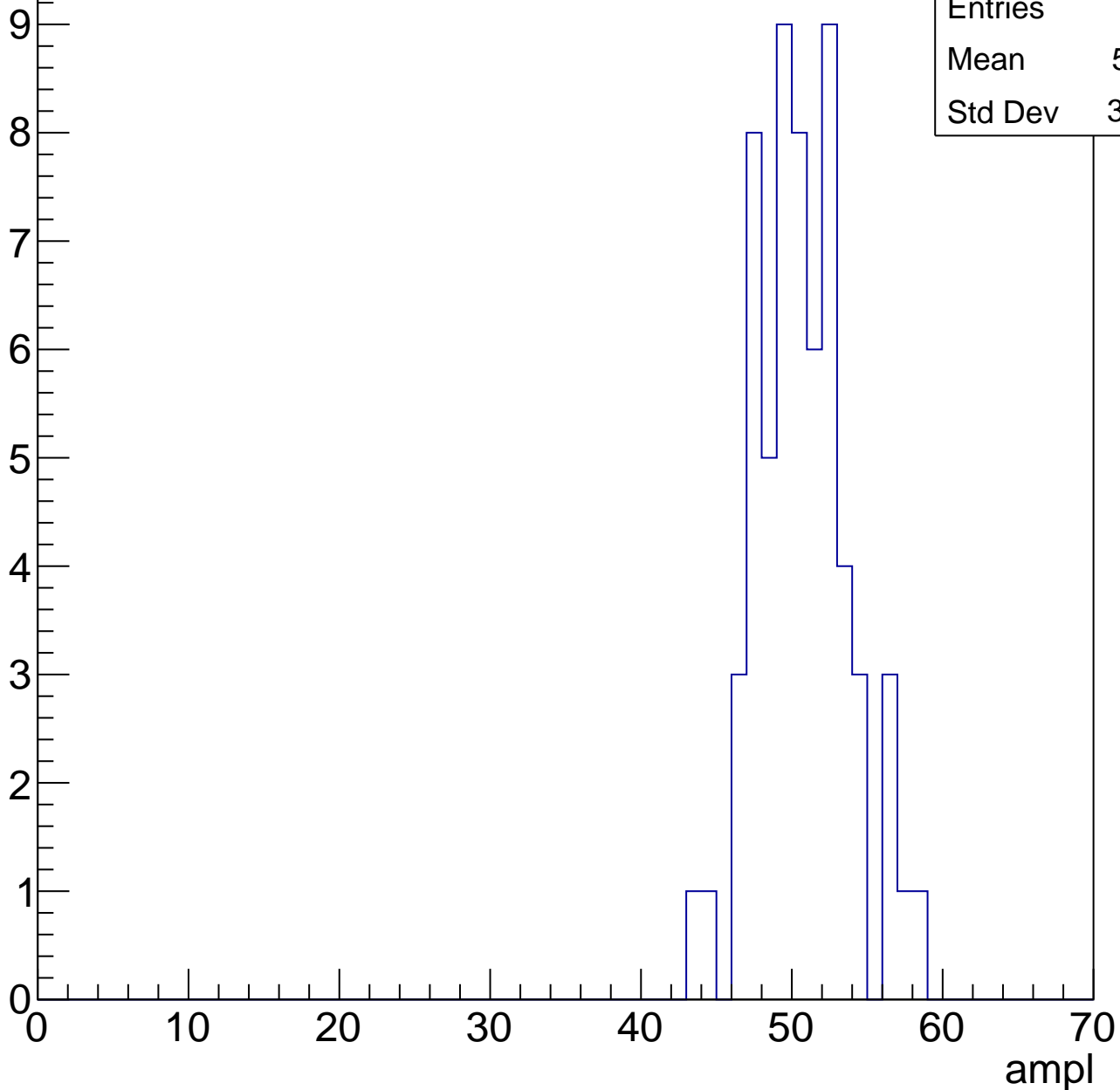


B1L103S, U13-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	50.21
Std Dev	3.054

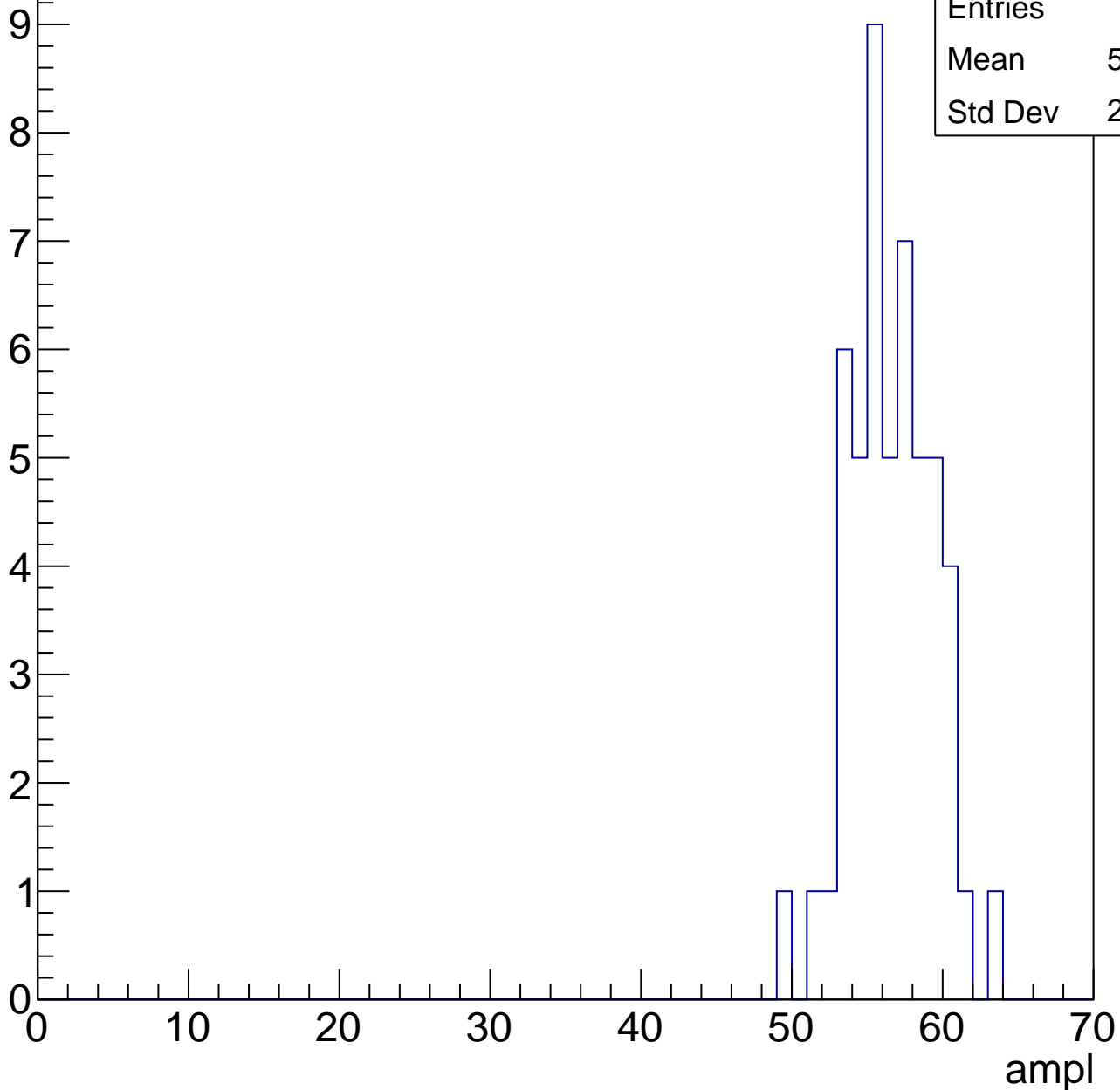


B1L103S, U13-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

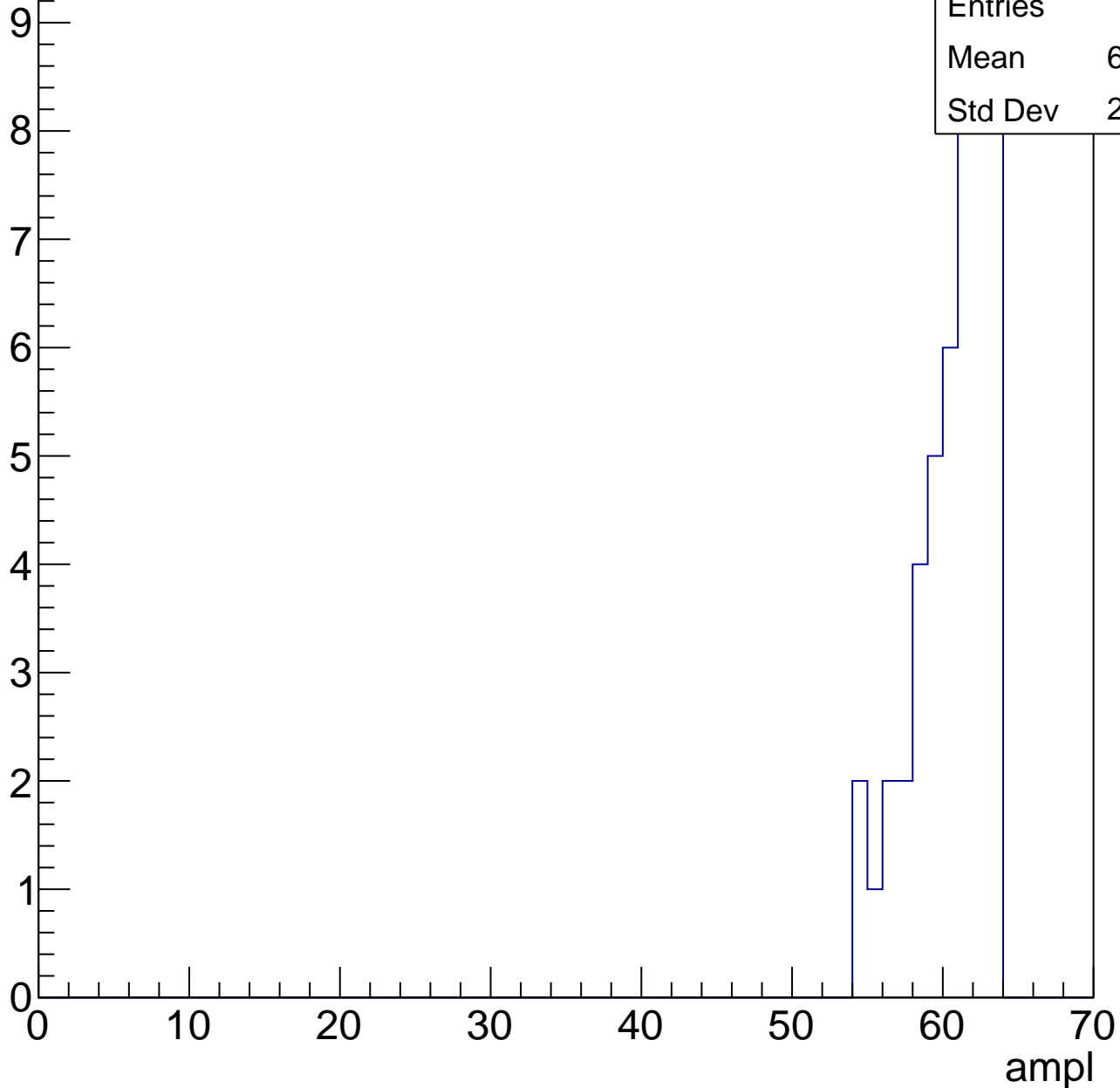
Entries	51
Mean	56.14
Std Dev	2.737



B1L103S, U13-ch12, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	48
Mean	60.15
Std Dev	2.432

B1L103S, U13-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch13, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	24.99
Std Dev	10.79

Gaus mean : 29.5968

Gaus Width: 3.3637

Entry

10

8

6

4

2

0

0

10

20

30

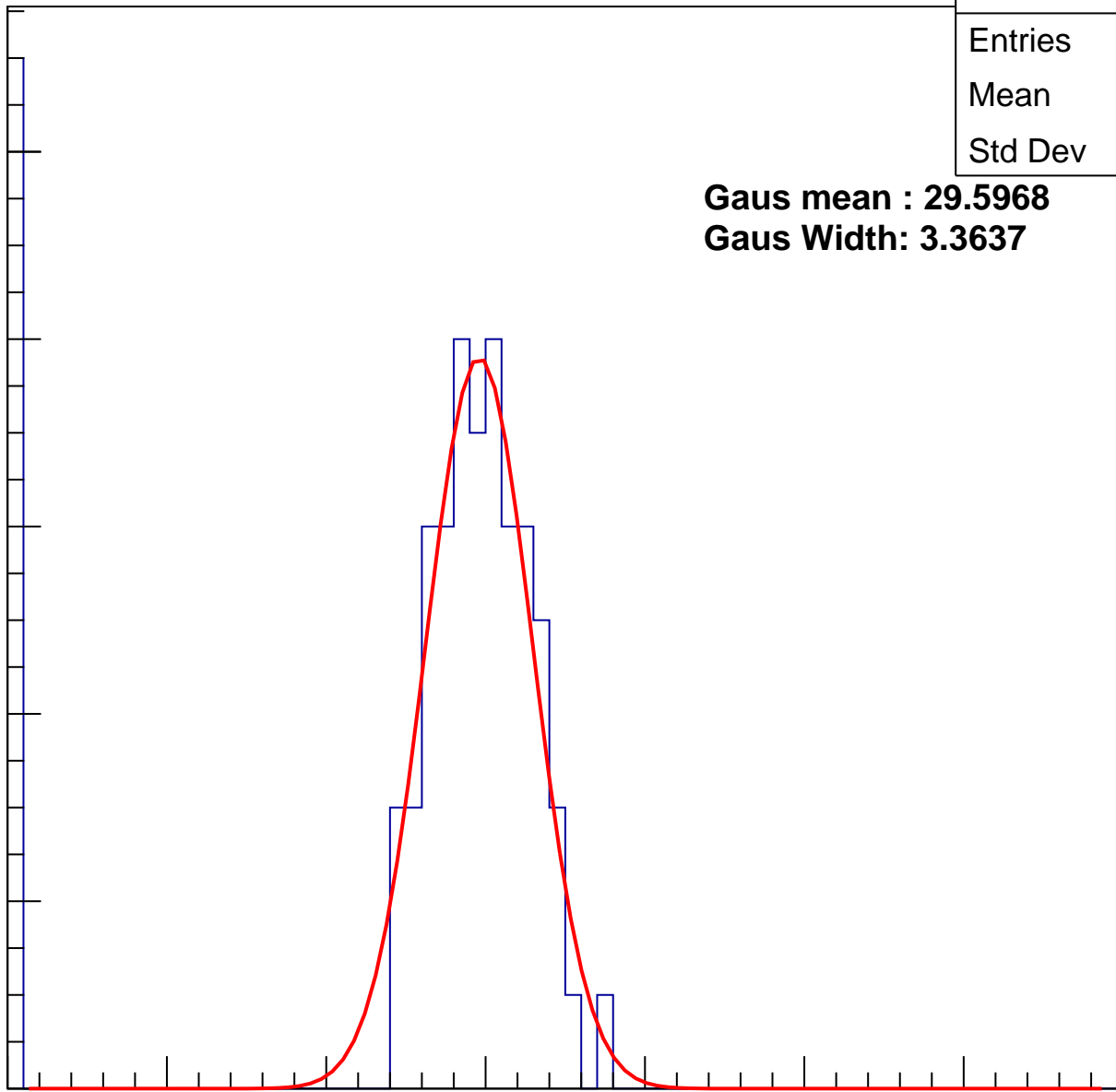
40

50

60

70

ampl



B1L103S, U13-ch13, adc1

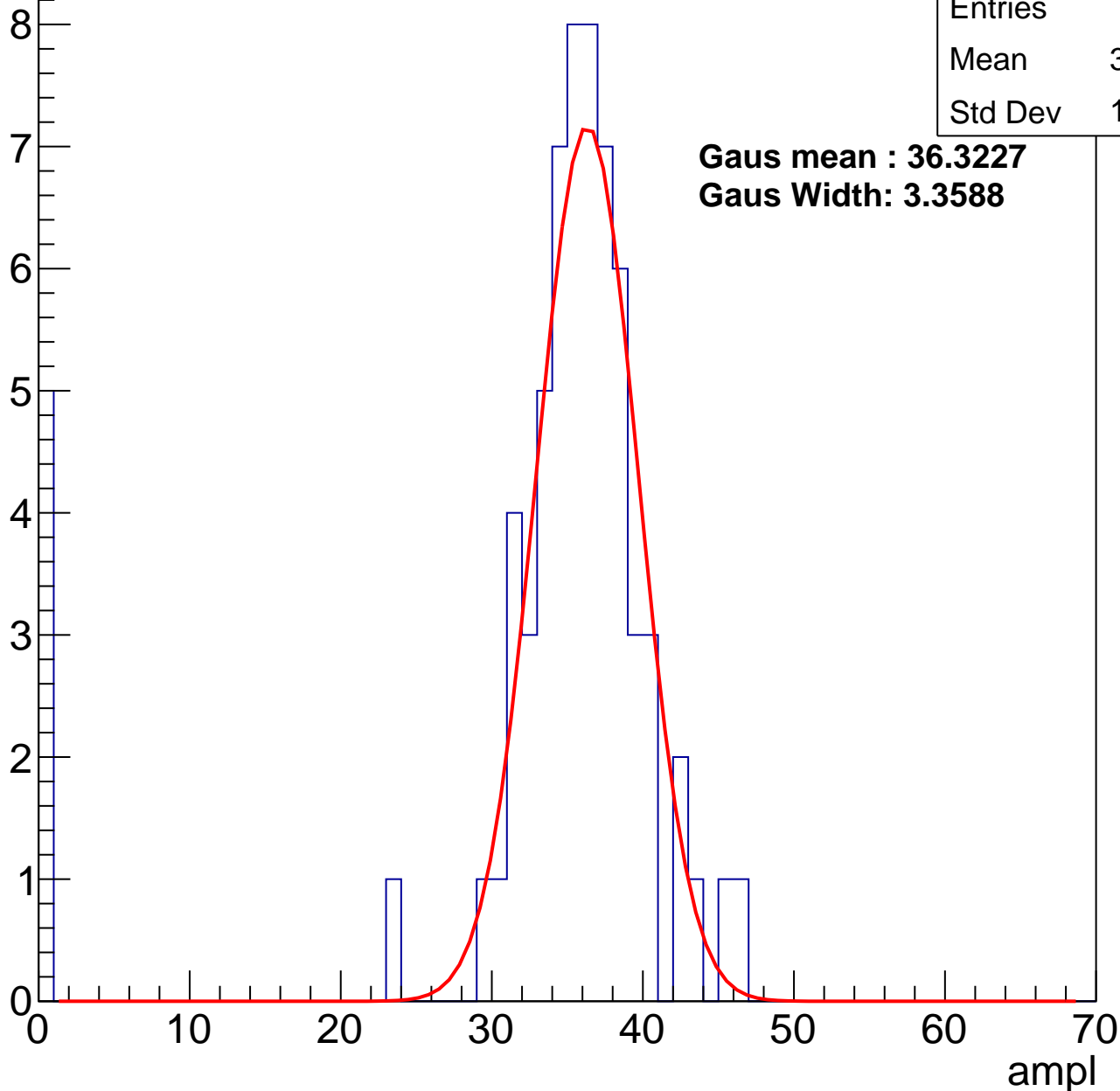
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.06
Std Dev	10.07

Gaus mean : 36.3227

Gaus Width: 3.3588



B1L103S, U13-ch13, adc2

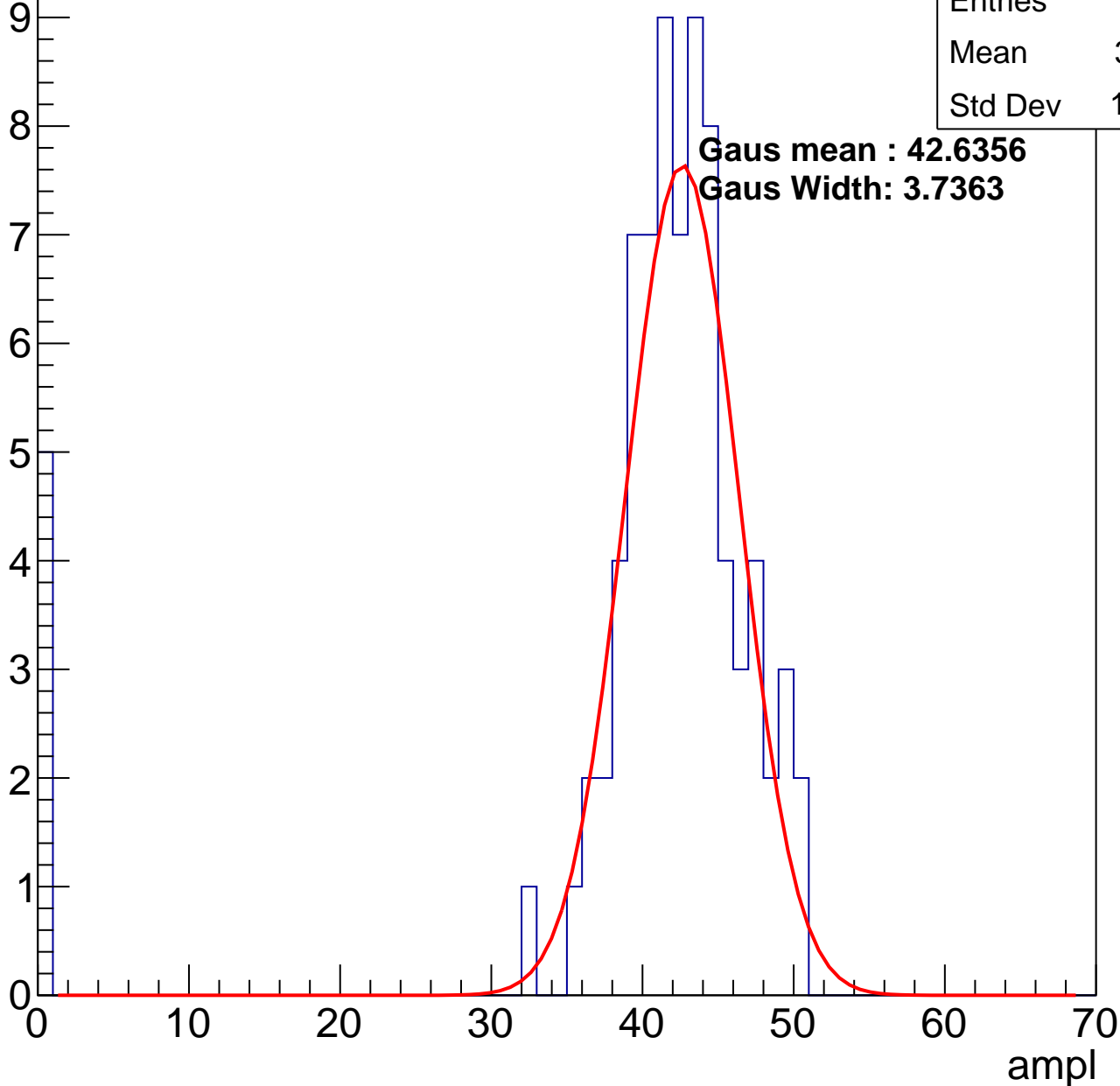
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	39.61
Std Dev	10.82

Gaus mean : 42.6356

Gaus Width: 3.7363

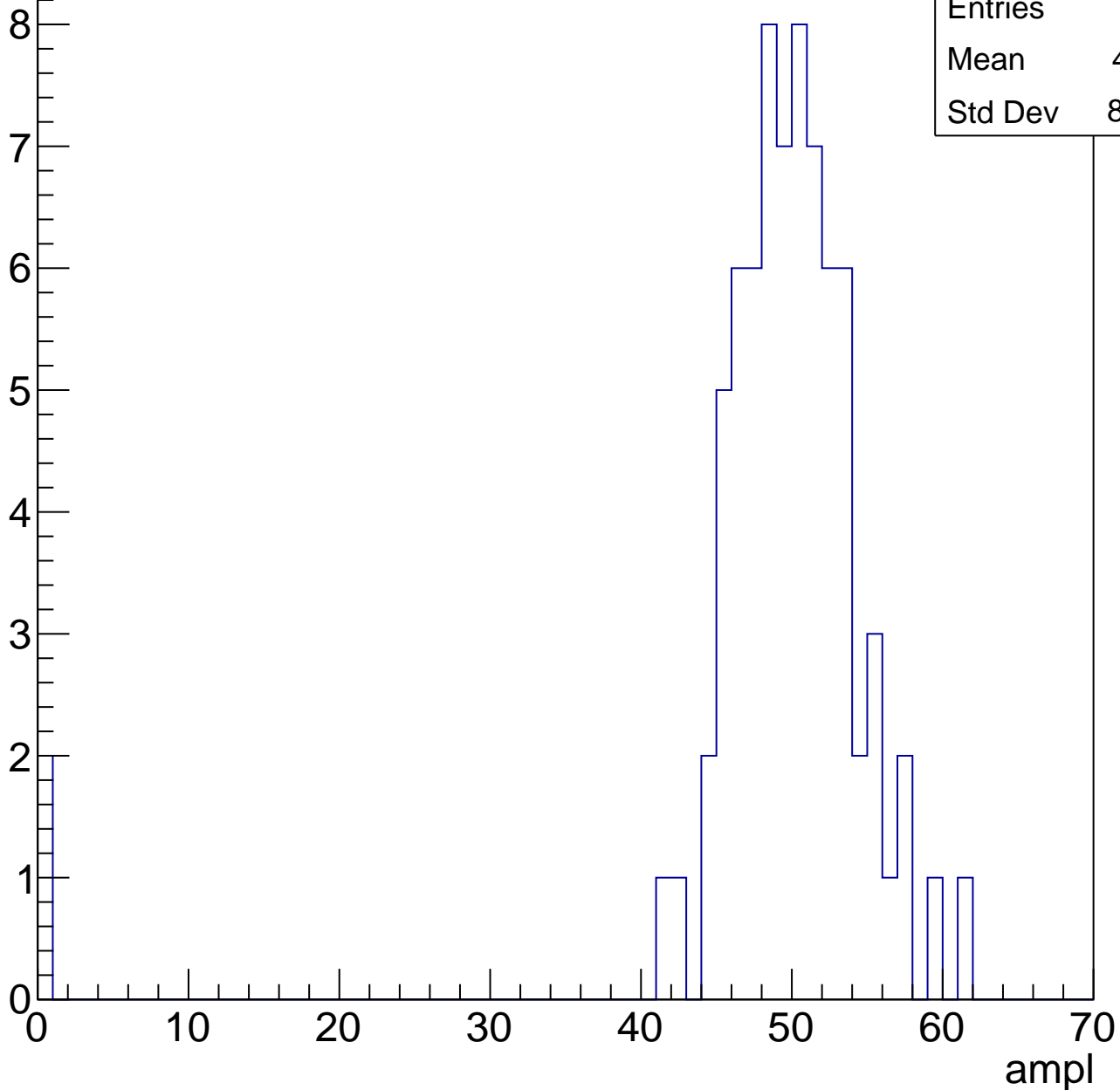


B1L103S, U13-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

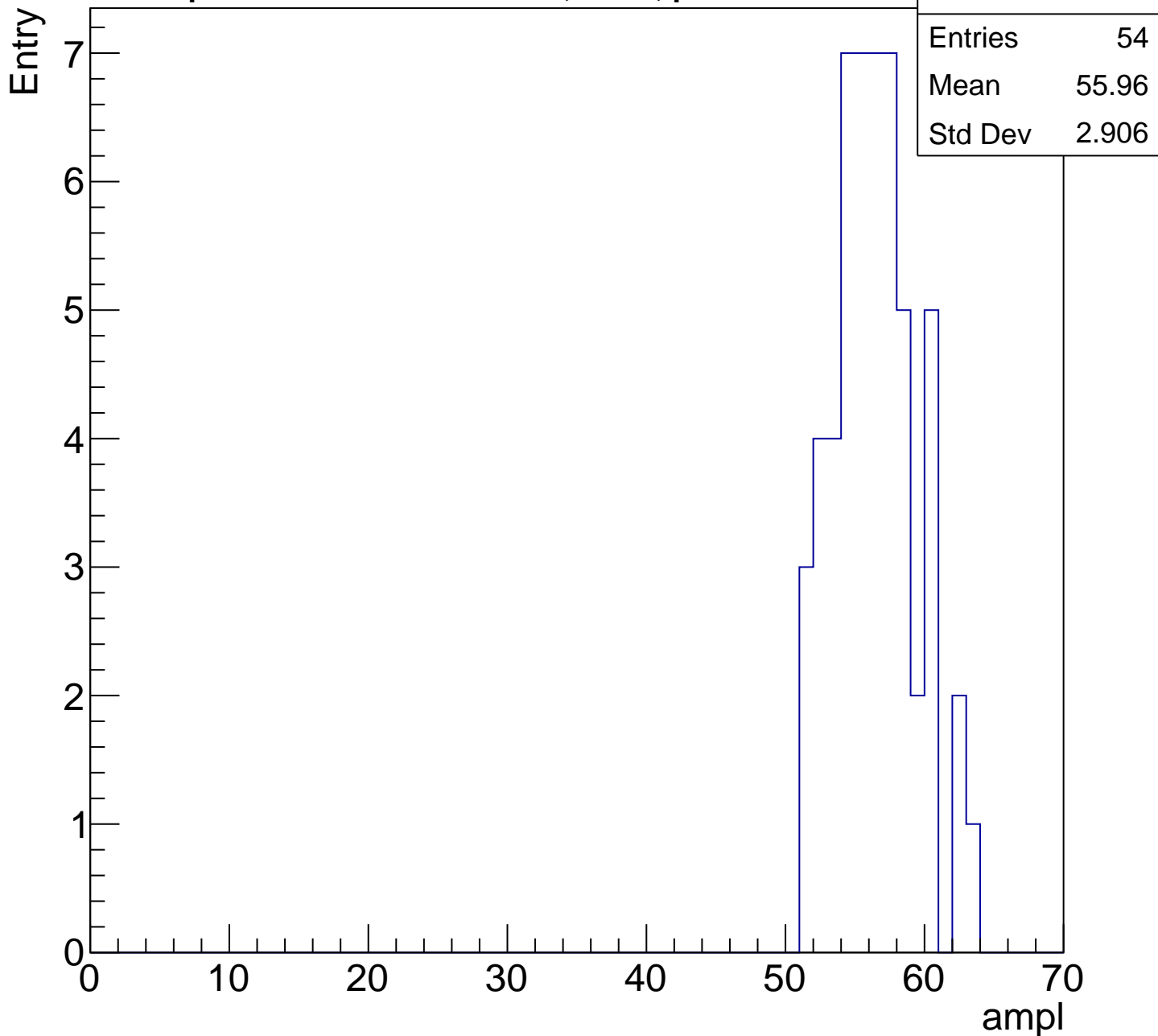
Entry

Entries	75
Mean	48.41
Std Dev	8.847



B1L103S, U13-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U13-ch13, adc5

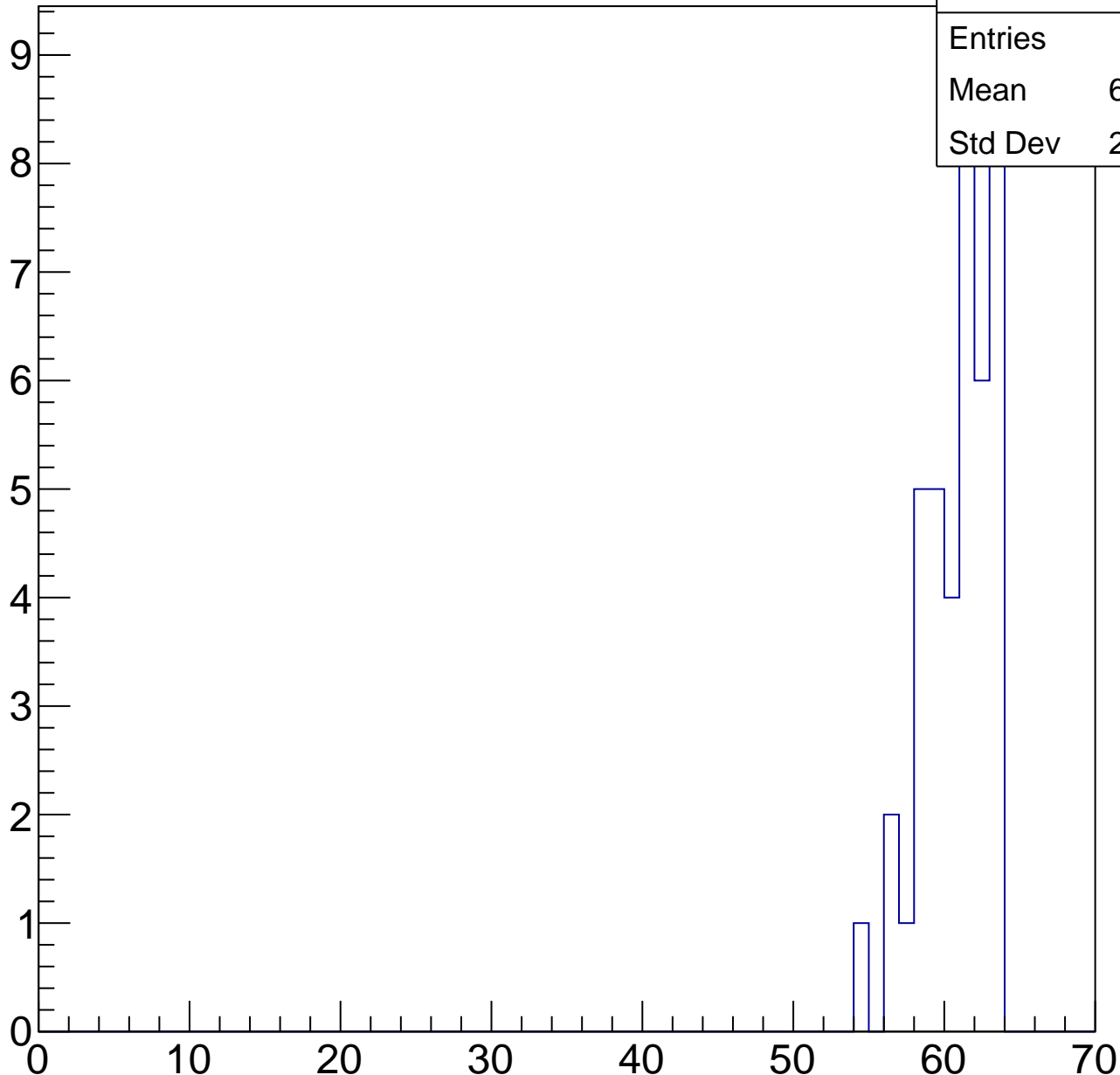
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.32
Std Dev	2.224

ampl



B1L103S, U13-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

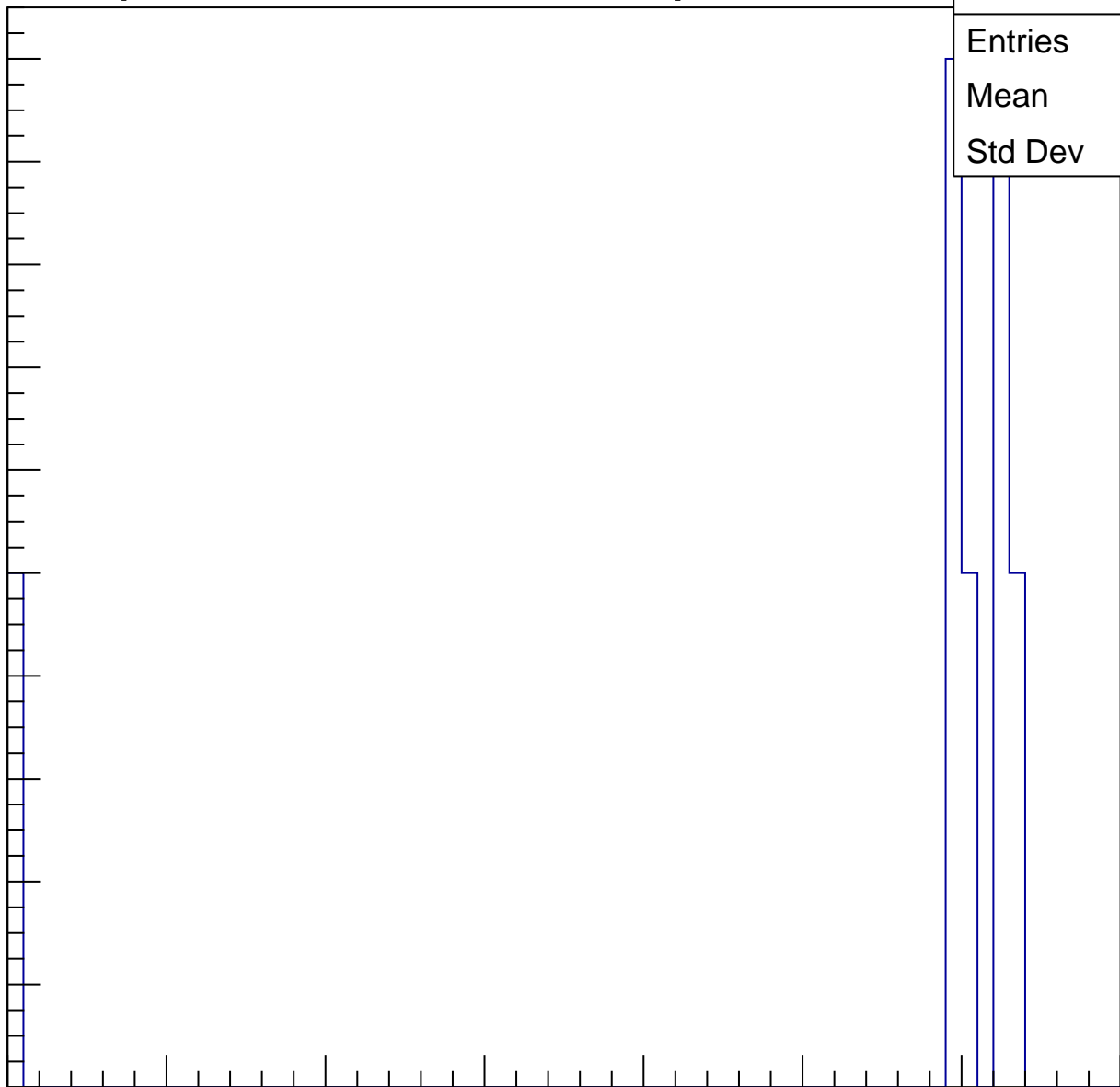
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.14
Std Dev	21.34

0 10 20 30 40 50 60 70

ampl

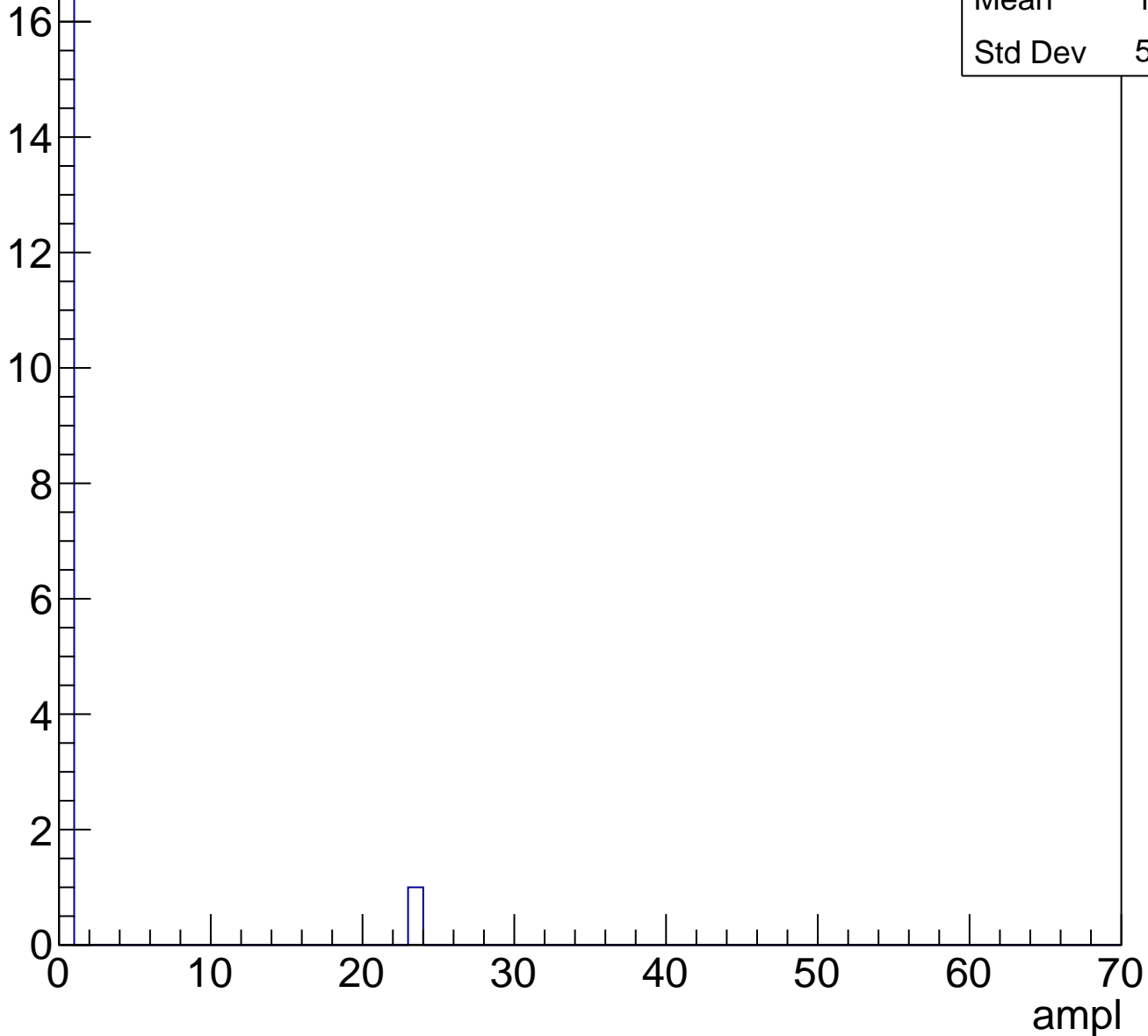


B1L103S, U13-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.278
Std Dev	5.268

Entry



B1L103S, U13-ch14, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	27.21
Std Dev	7.752

Gaus mean : 29.3513

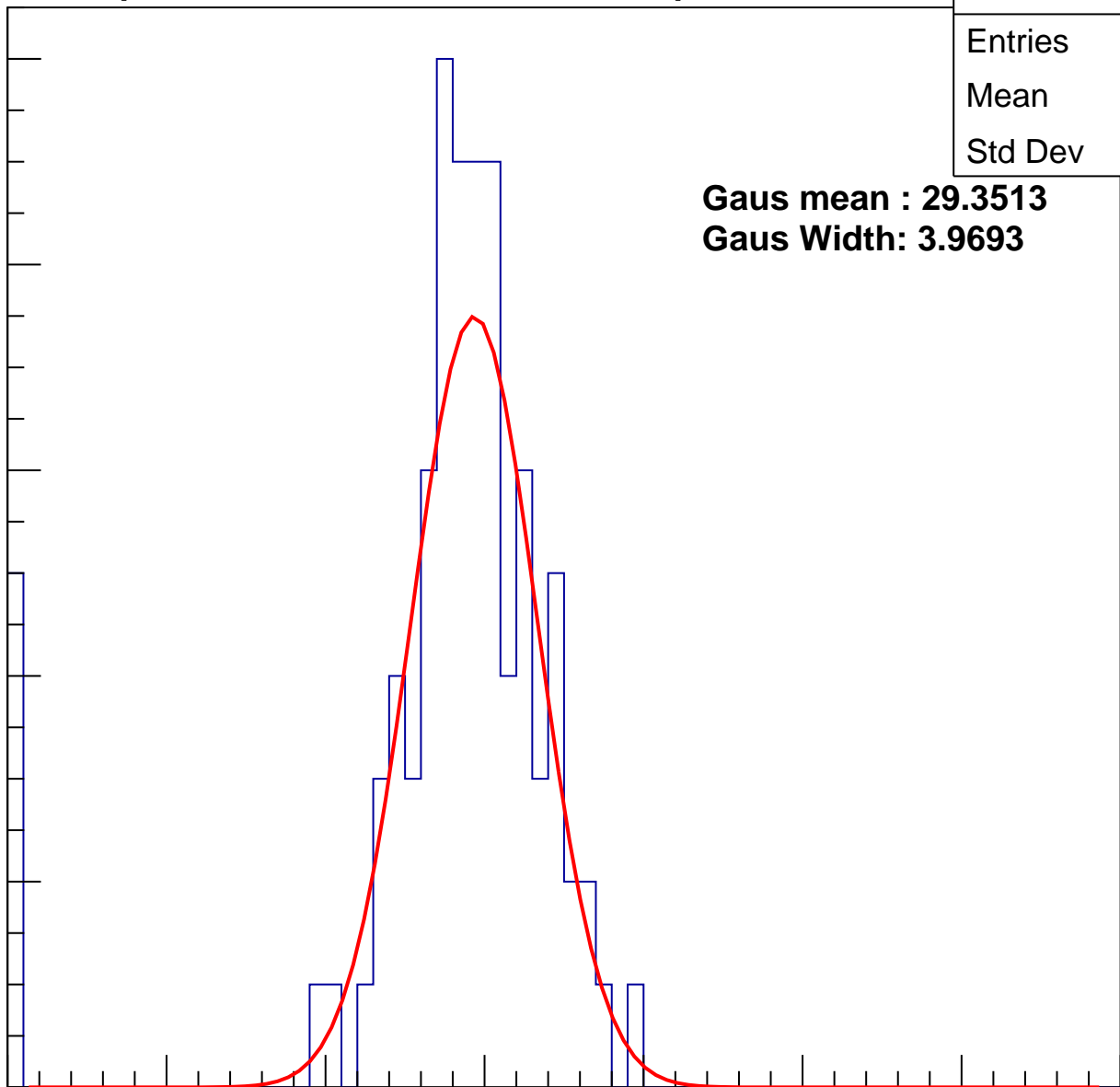
Gaus Width: 3.9693

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch14, adc1

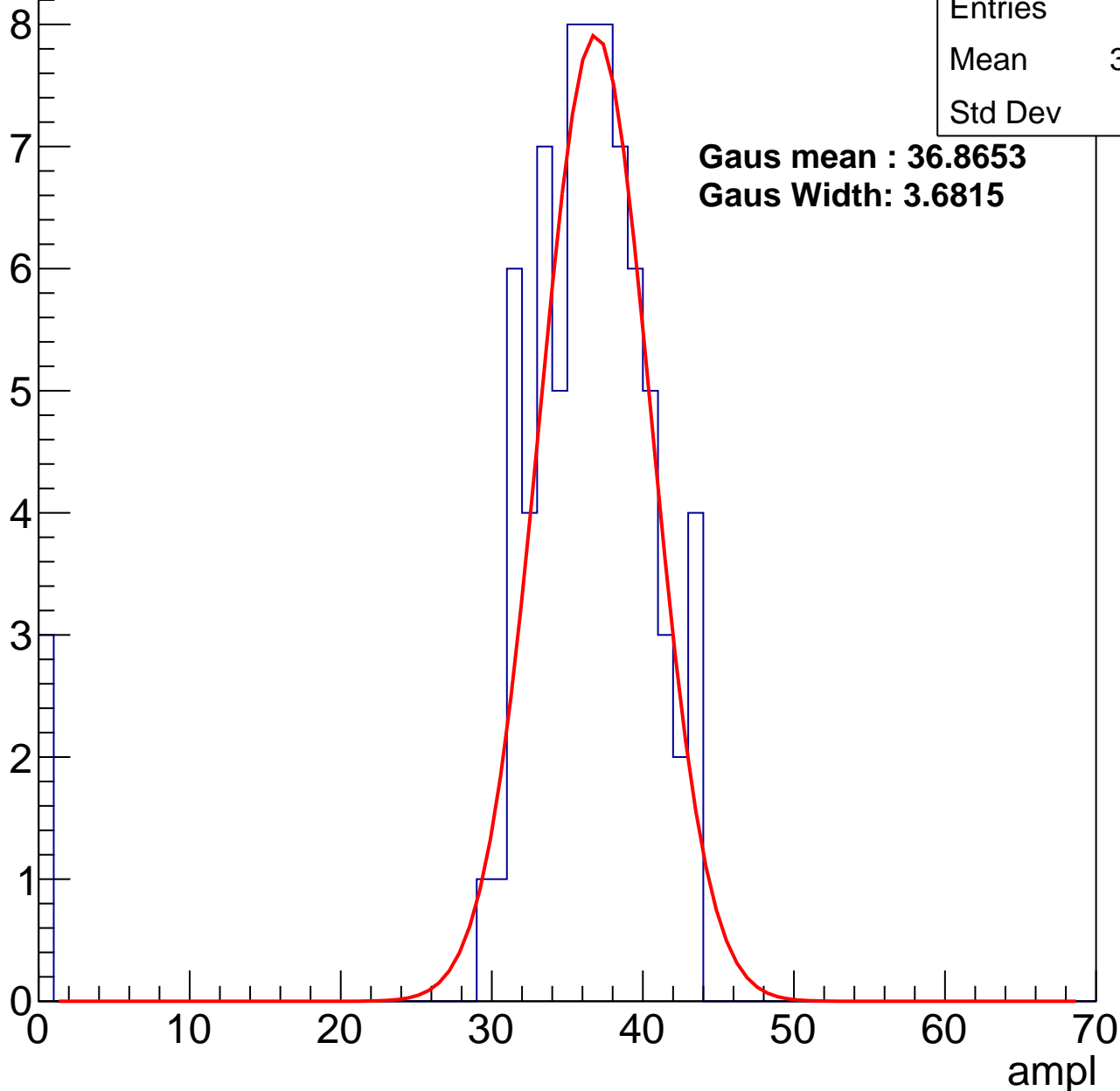
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.83
Std Dev	7.75

Gaus mean : 36.8653

Gaus Width: 3.6815



B1L103S, U13-ch14, adc2

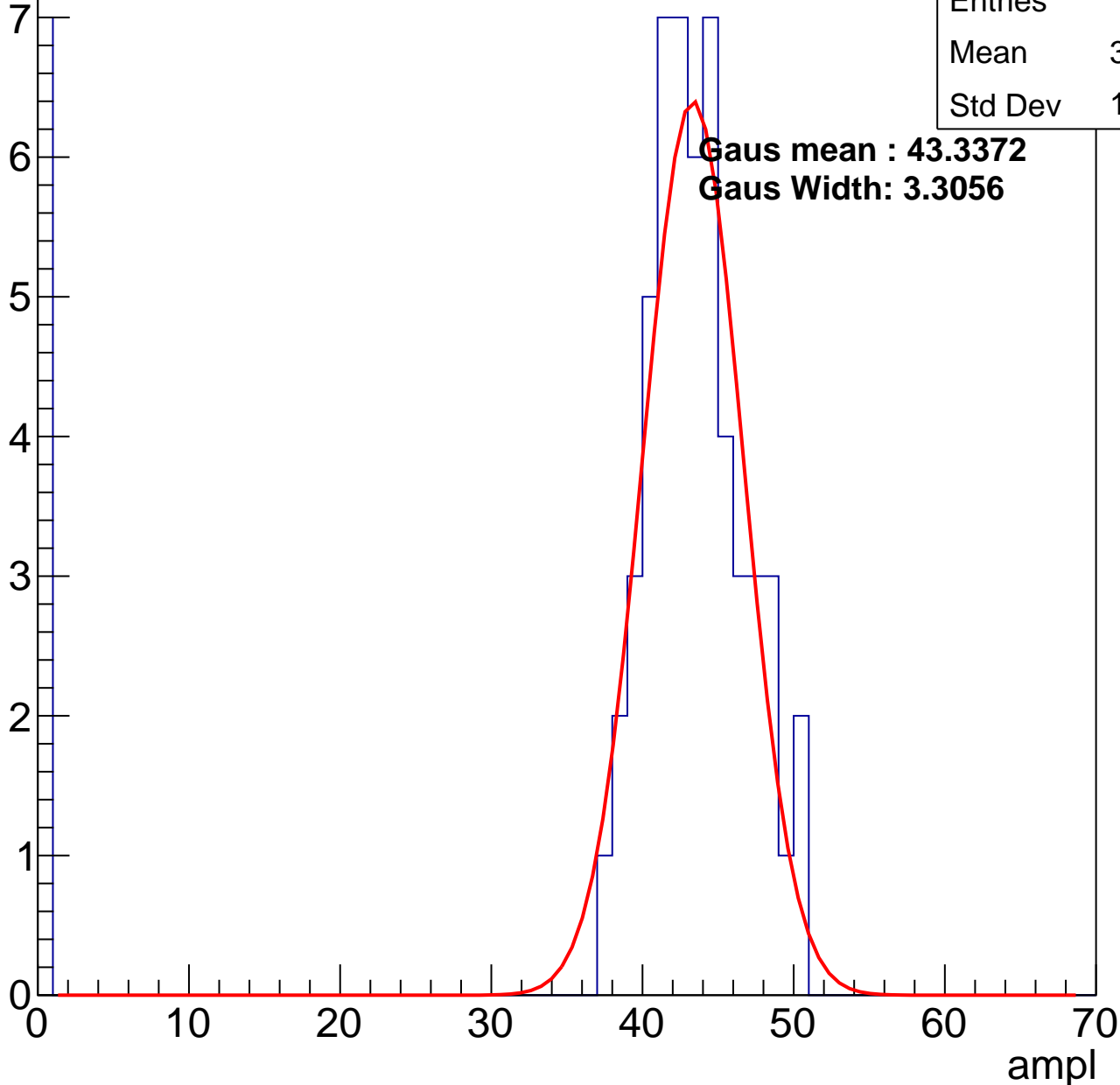
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	38.18
Std Dev	14.05

Gaus mean : 43.3372

Gaus Width: 3.3056

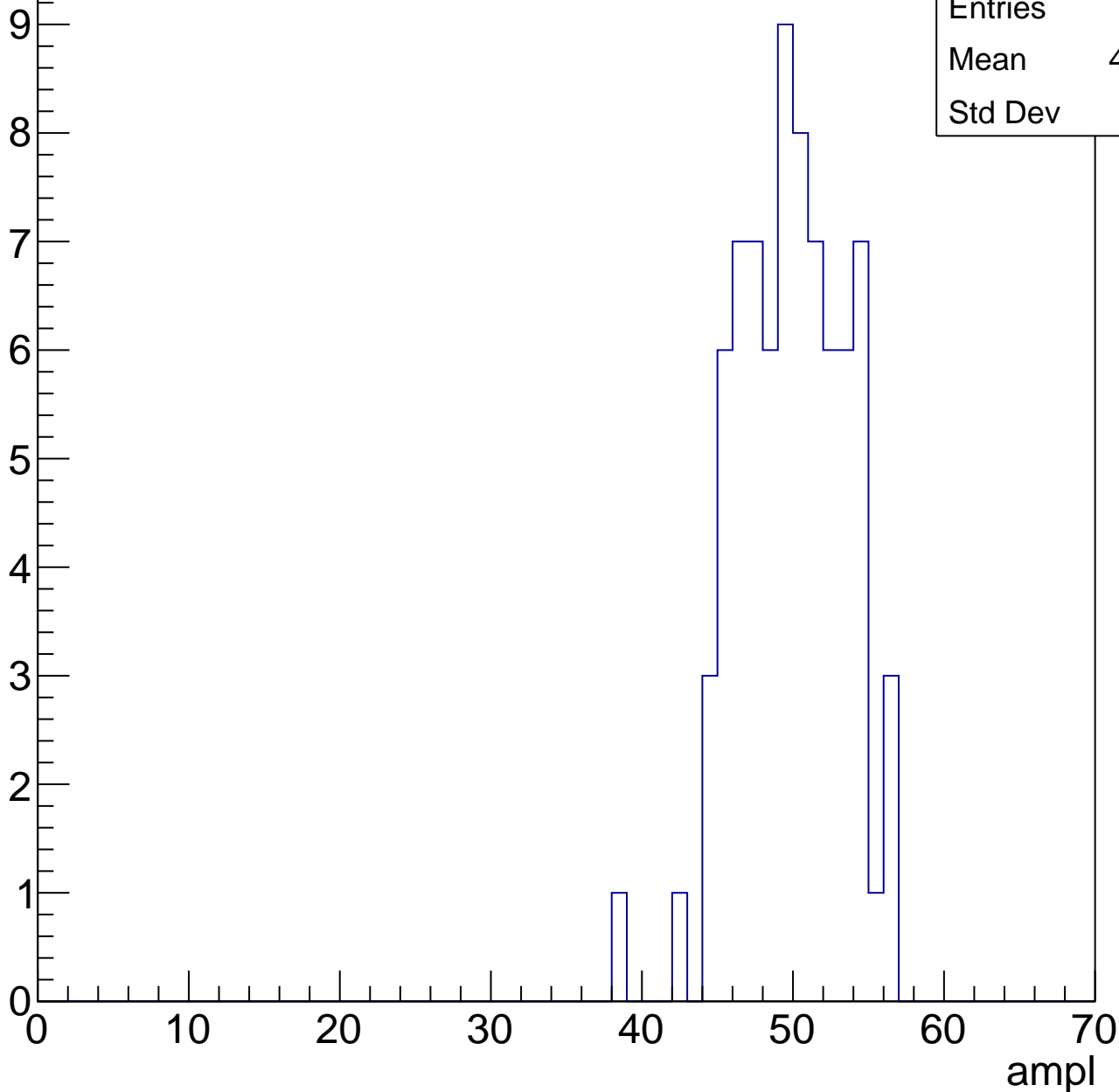


B1L103S, U13-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	49.36
Std Dev	3.53

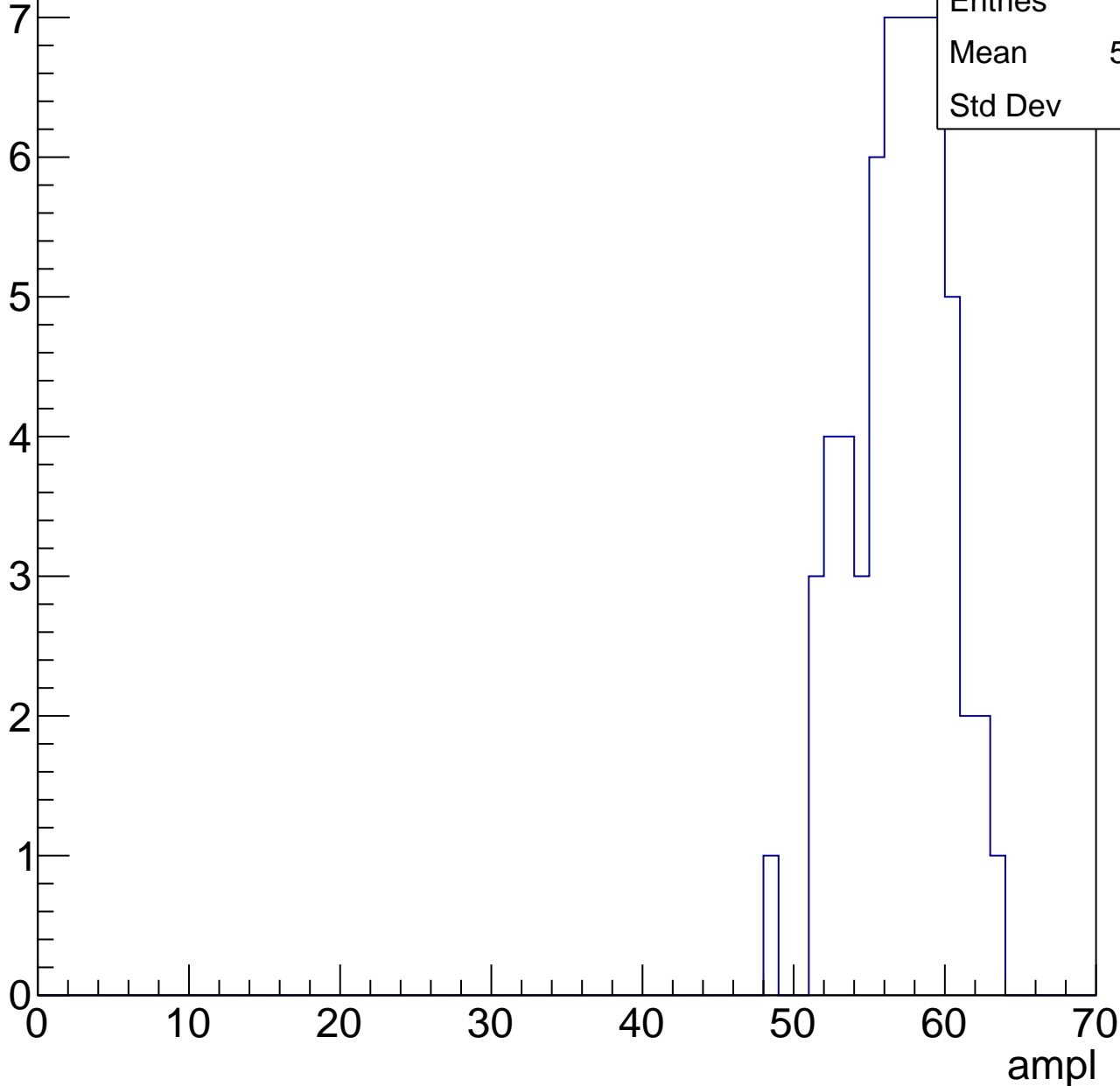


B1L103S, U13-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	56.47
Std Dev	3.17

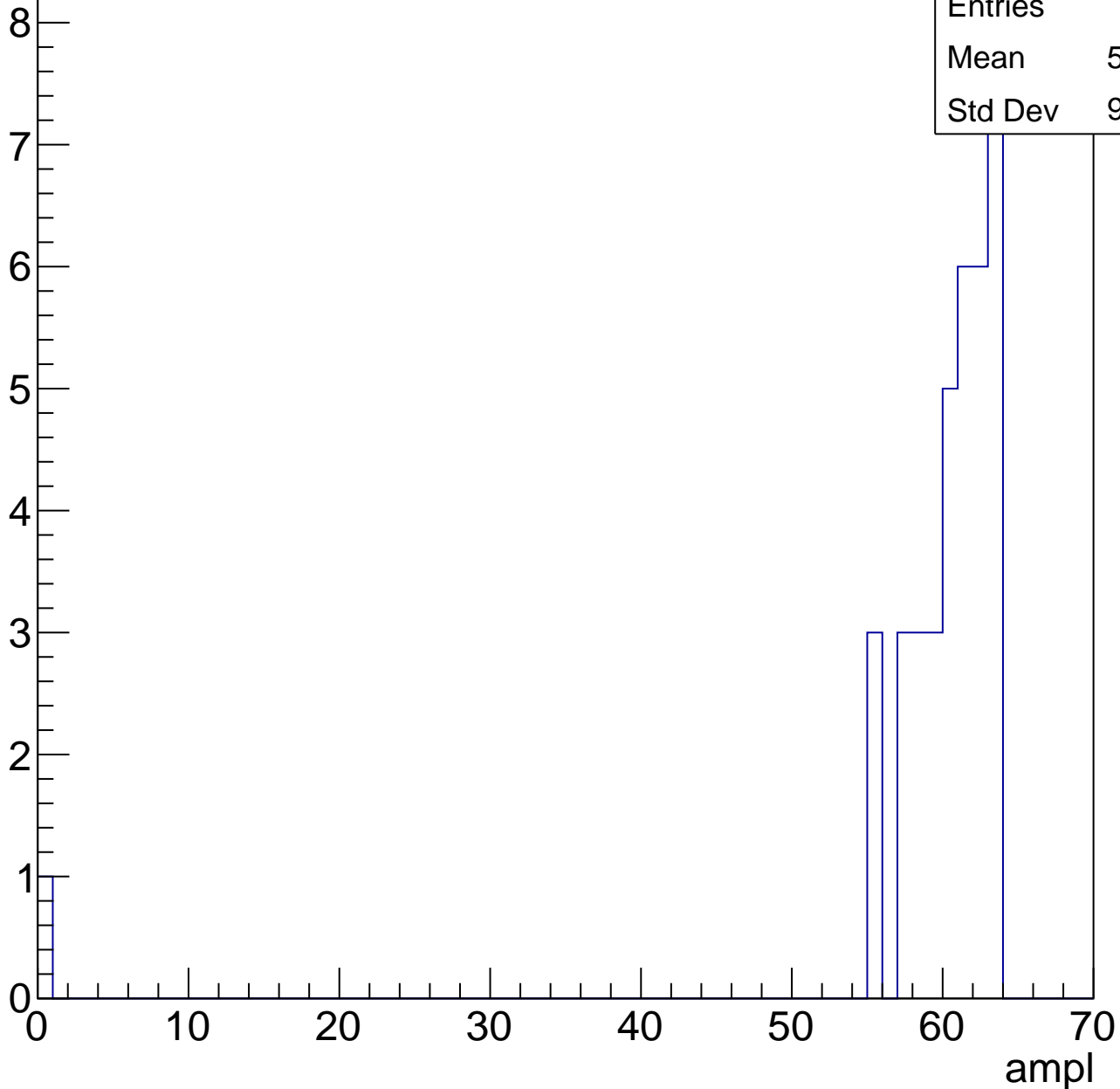


B1L103S, U13-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

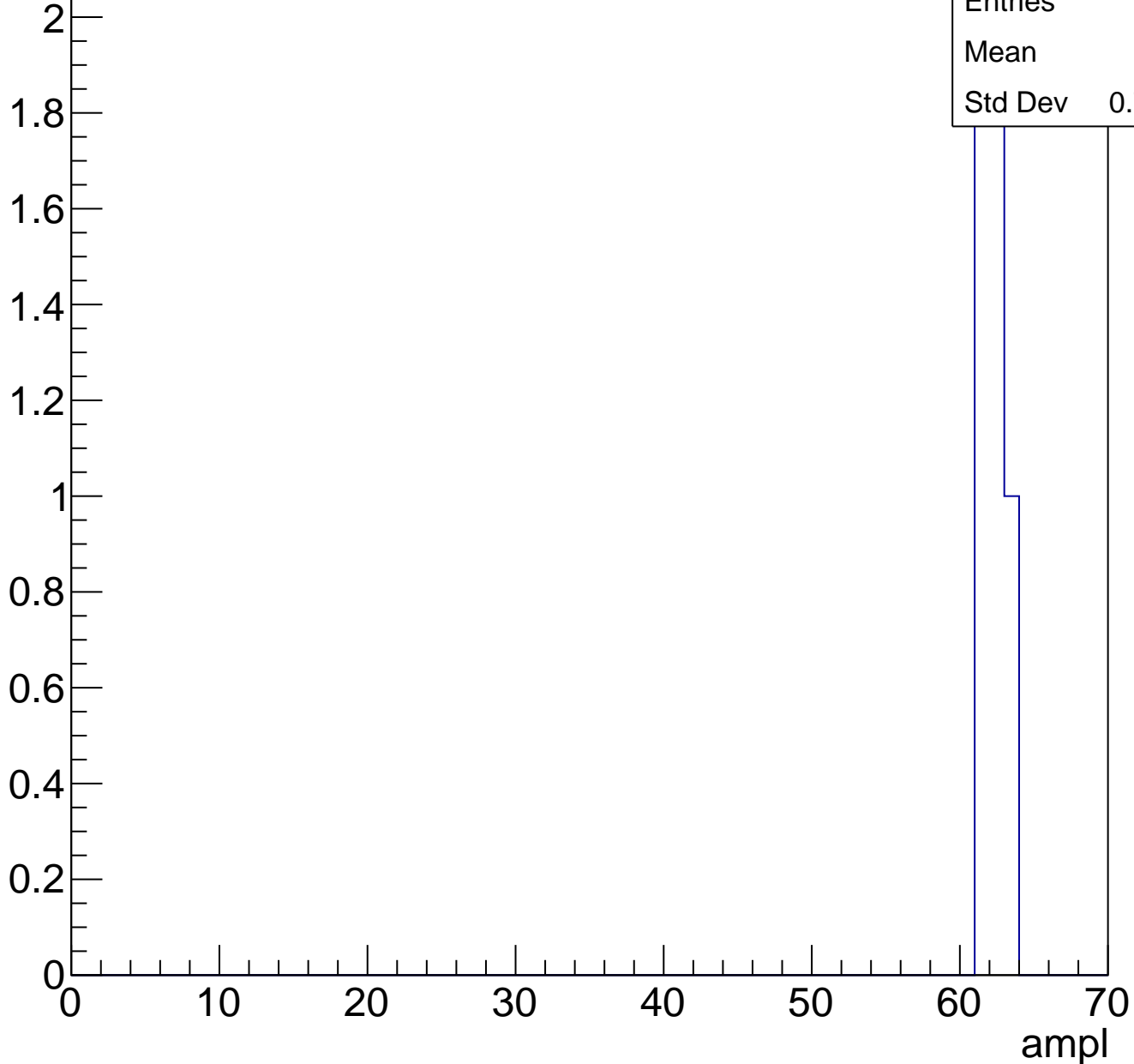
Entries	38
Mean	58.66
Std Dev	9.935



B1L103S, U13-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch15, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	25.24
Std Dev	11.29

Gaus mean : 30.9600

Gaus Width: 2.6548

Entry

10

8

6

4

2

0

0

10

20

30

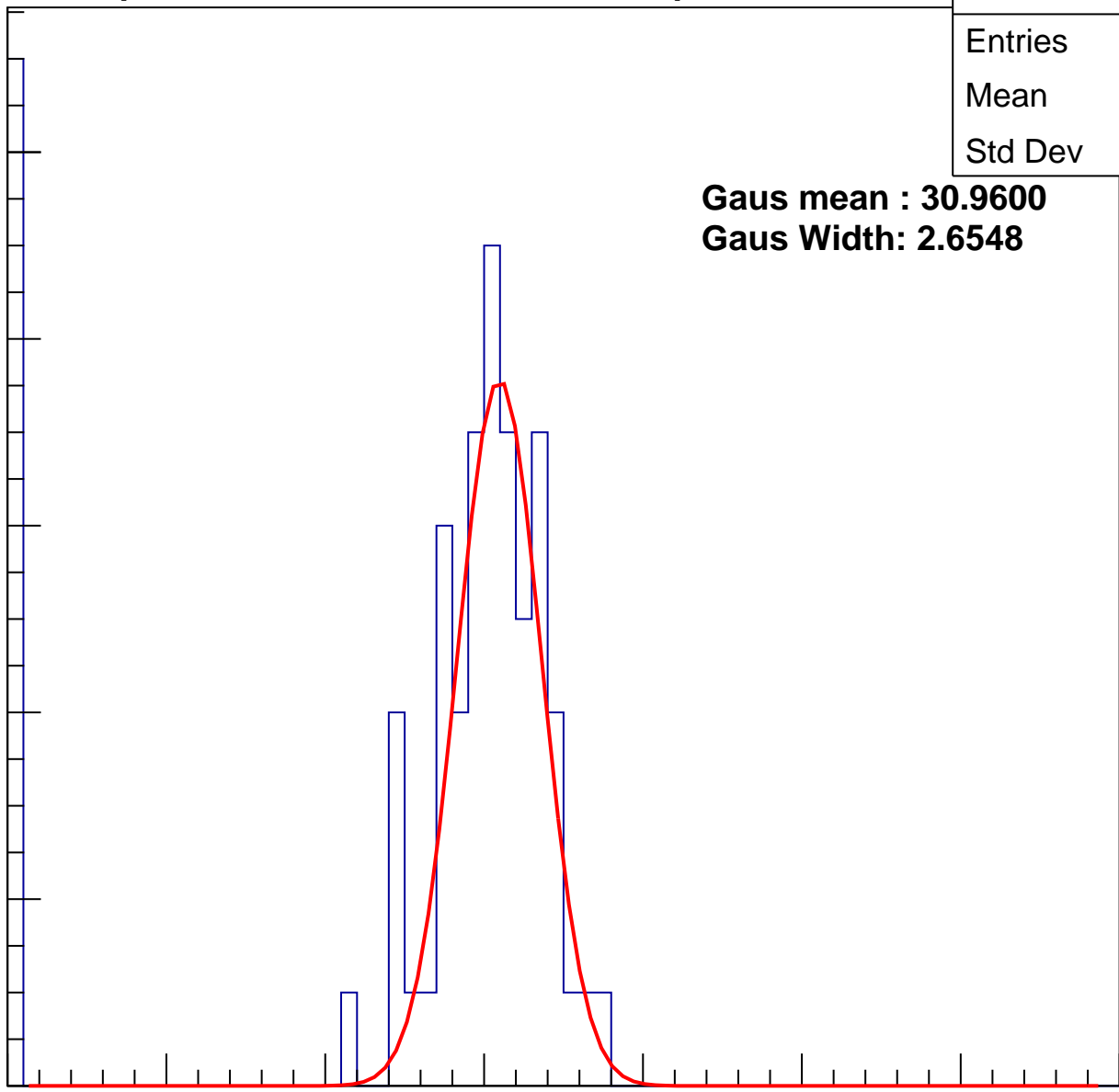
40

50

60

70

ampl



B1L103S, U13-ch15, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	31.59
Std Dev	12.88

Gaus mean : 37.1284

Gaus Width: 3.6816

Entry

10

8

6

4

2

0

0

10

20

30

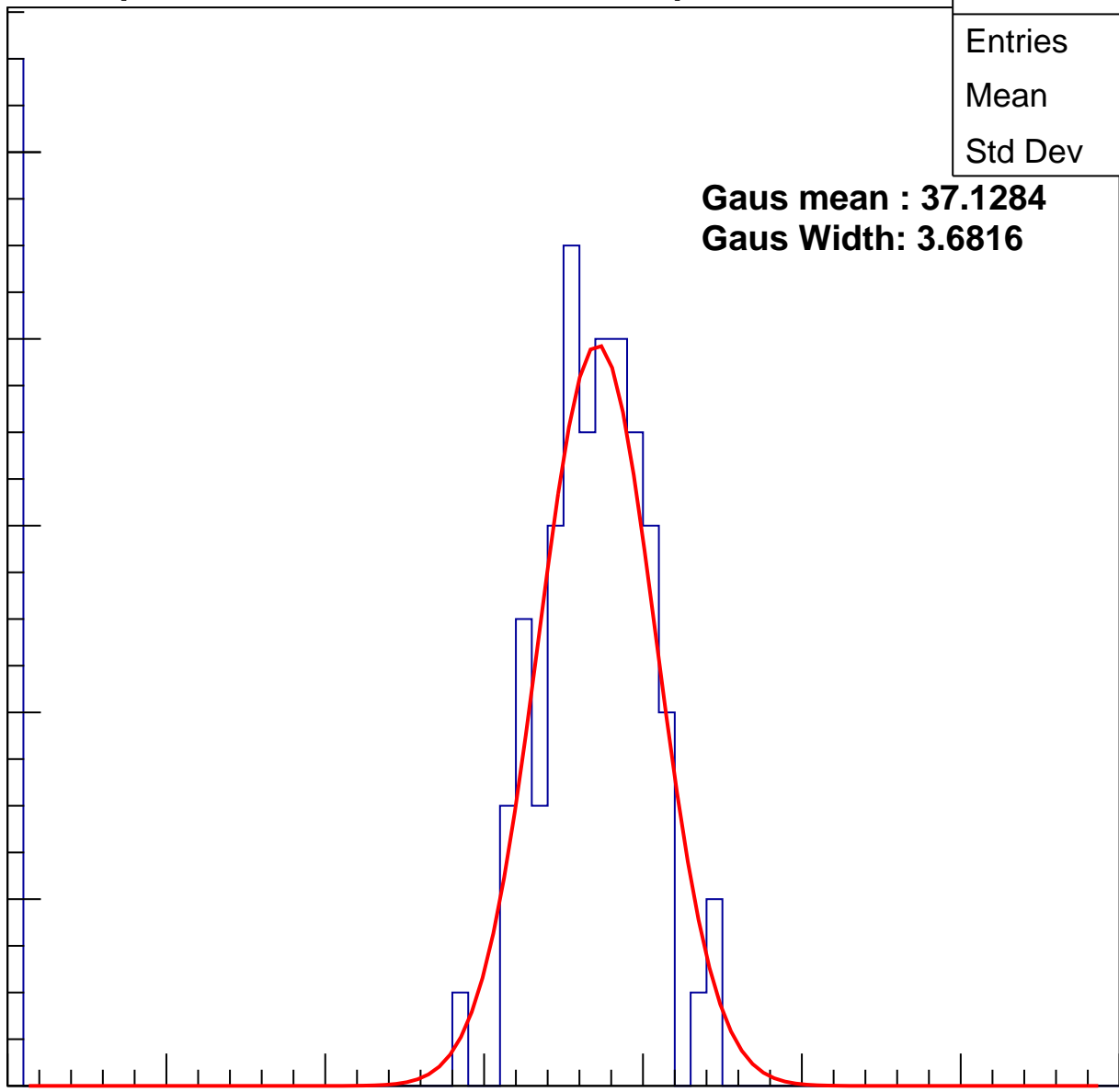
40

50

60

70

ampl



B1L103S, U13-ch15, adc2

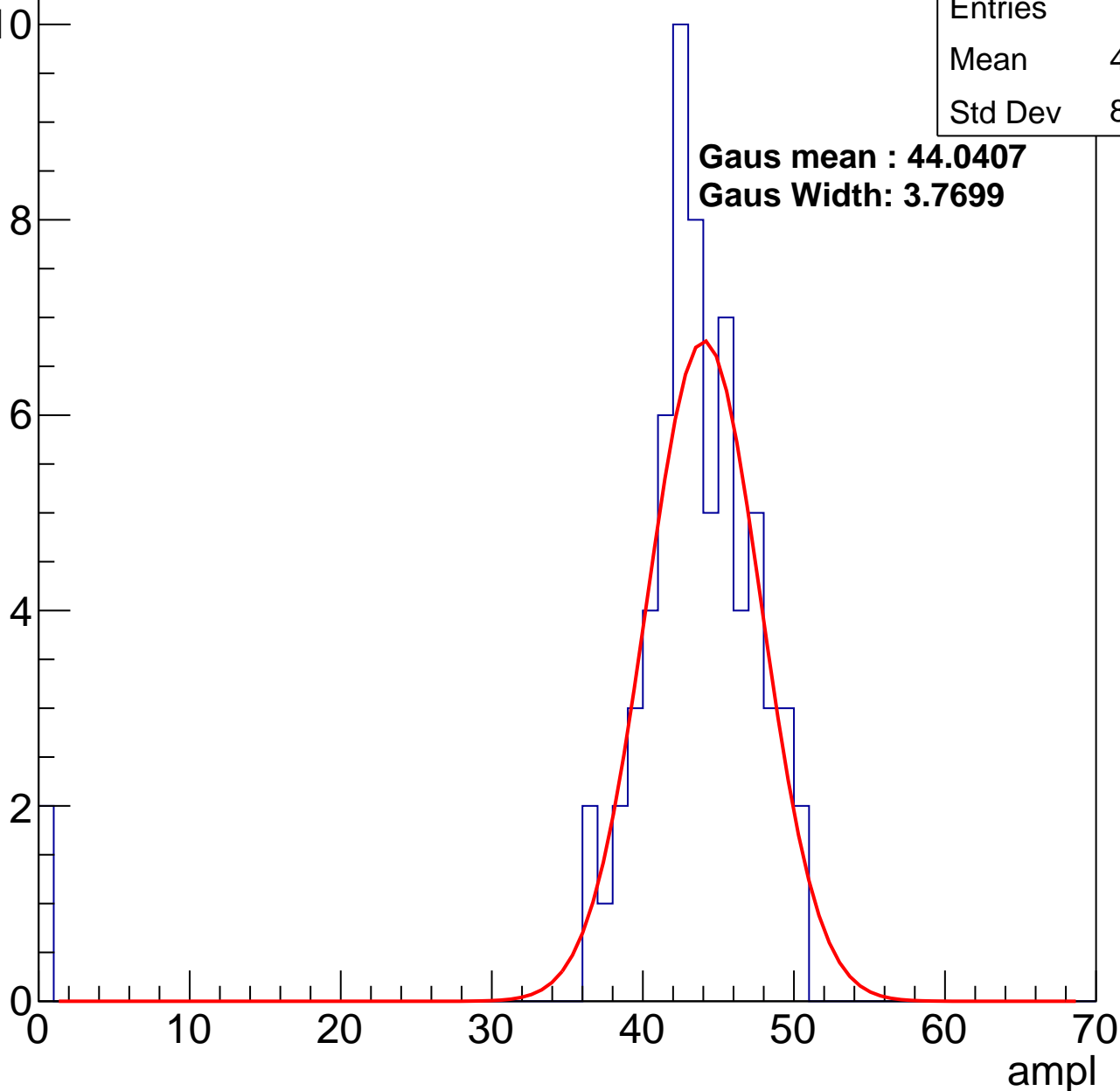
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	42.04
Std Dev	8.075

Gaus mean : 44.0407

Gaus Width: 3.7699

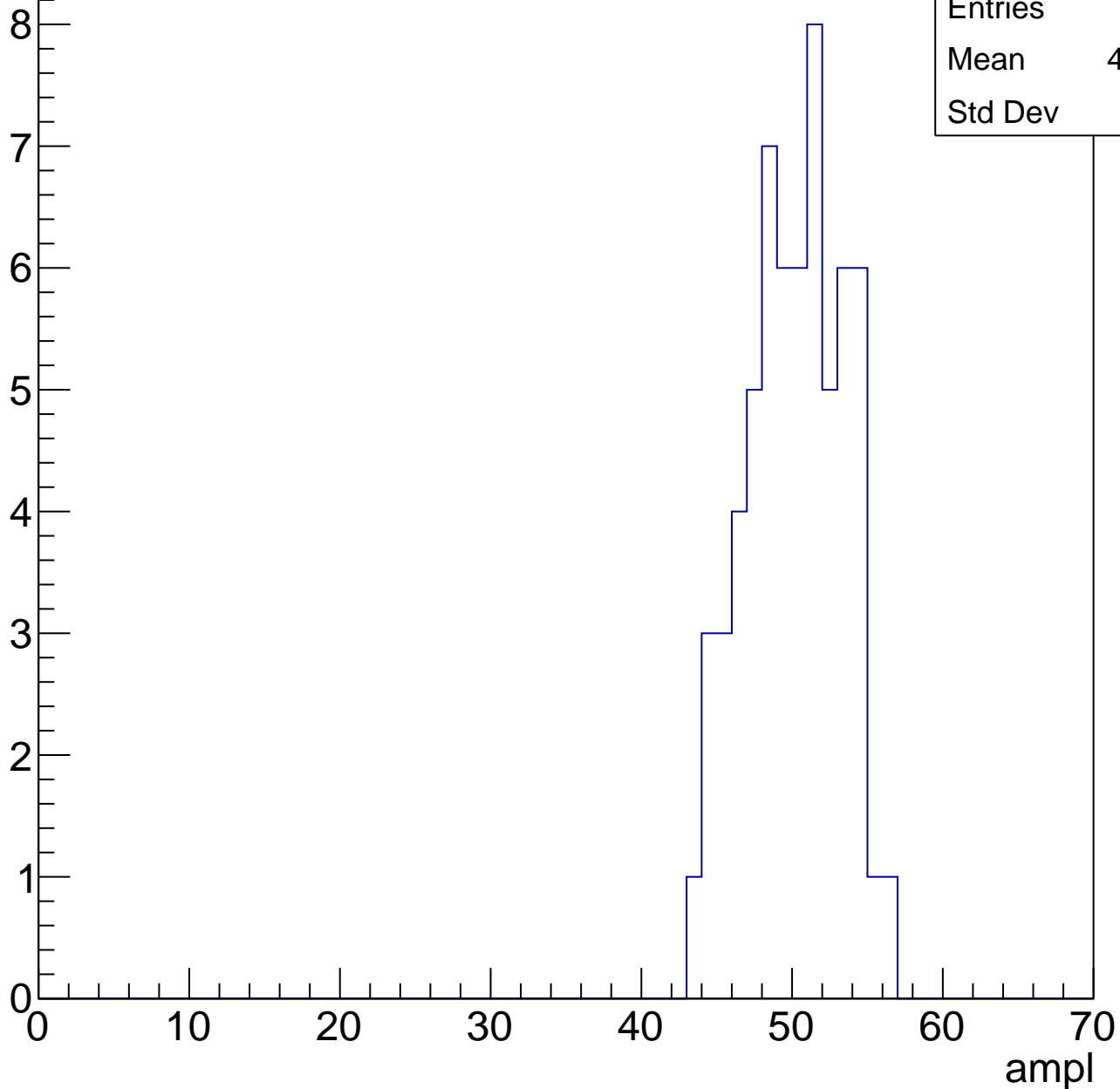


B1L103S, U13-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

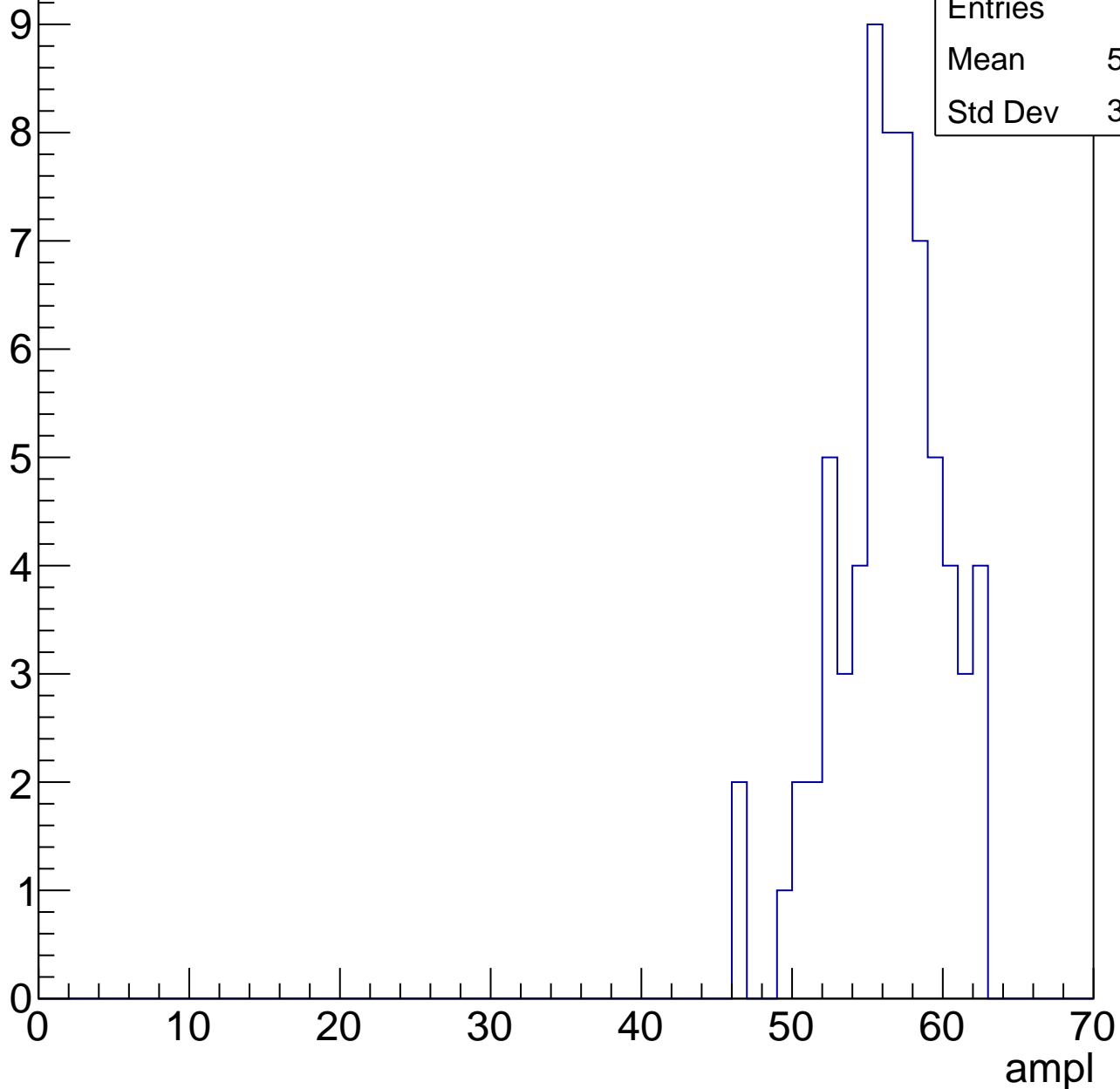
Entries	62
Mean	49.68
Std Dev	3.13



B1L103S, U13-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



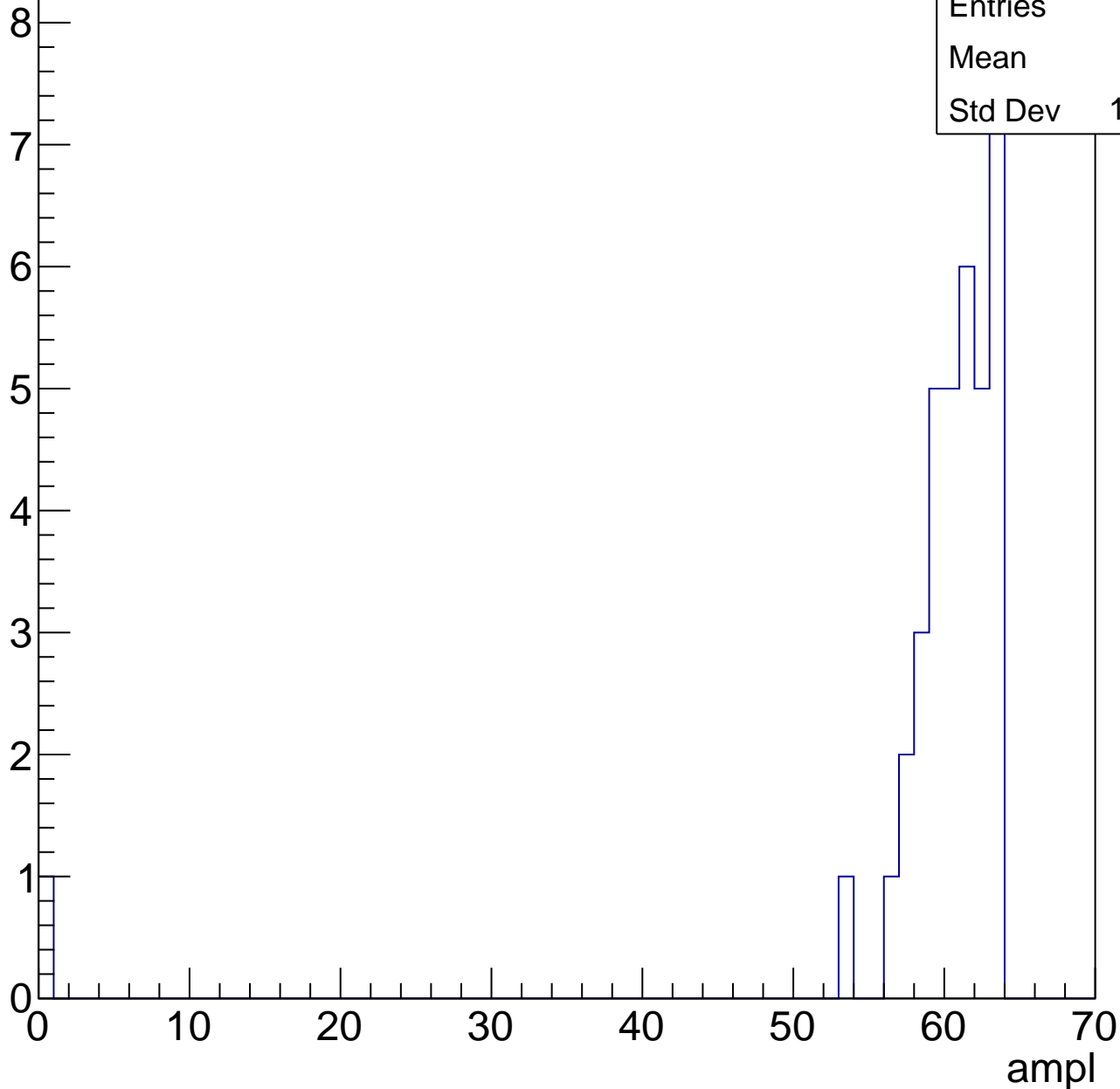
Entries	67
Mean	55.96
Std Dev	3.597

B1L103S, U13-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	58.7
Std Dev	10.05



B1L103S, U13-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

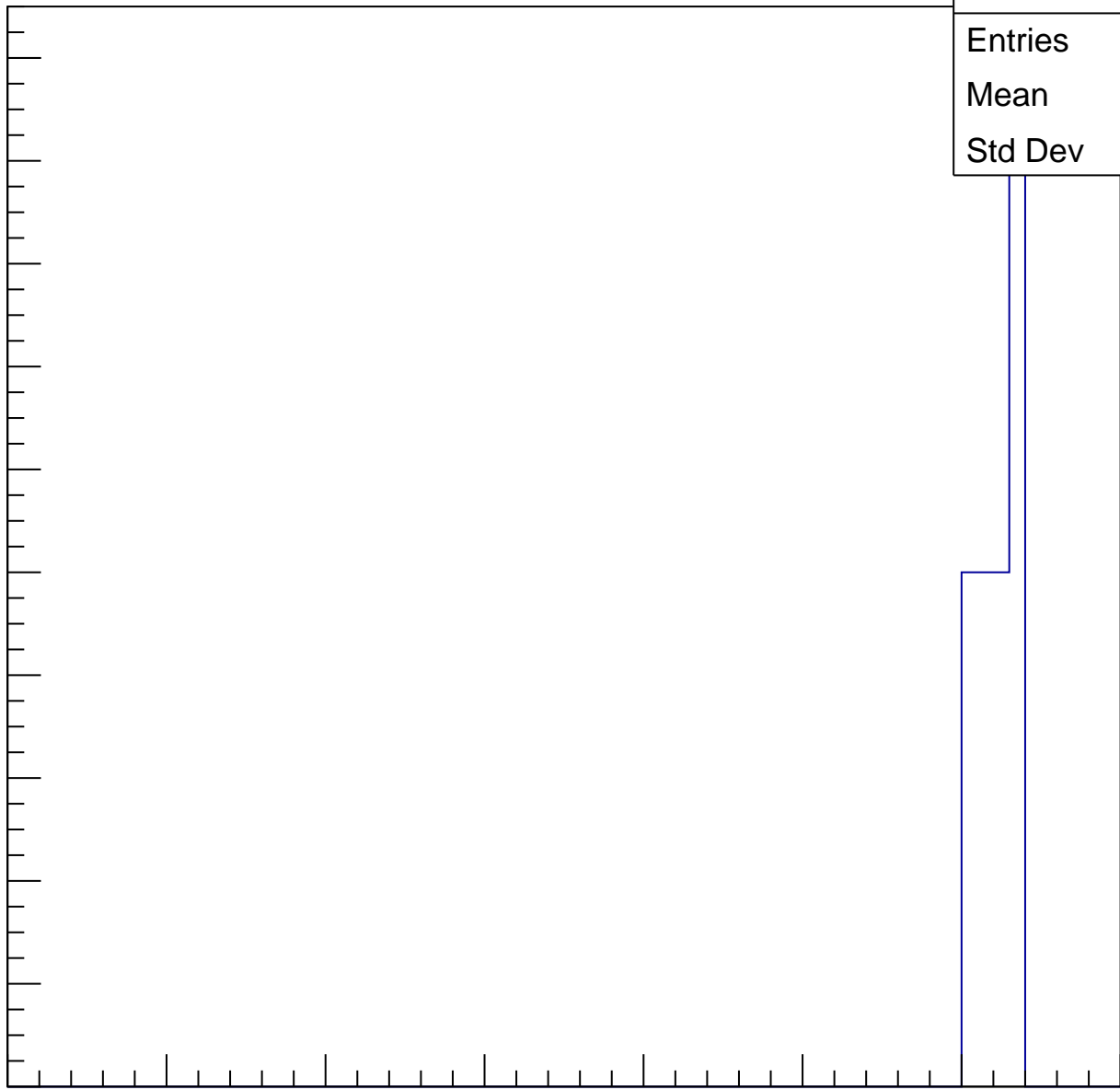
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

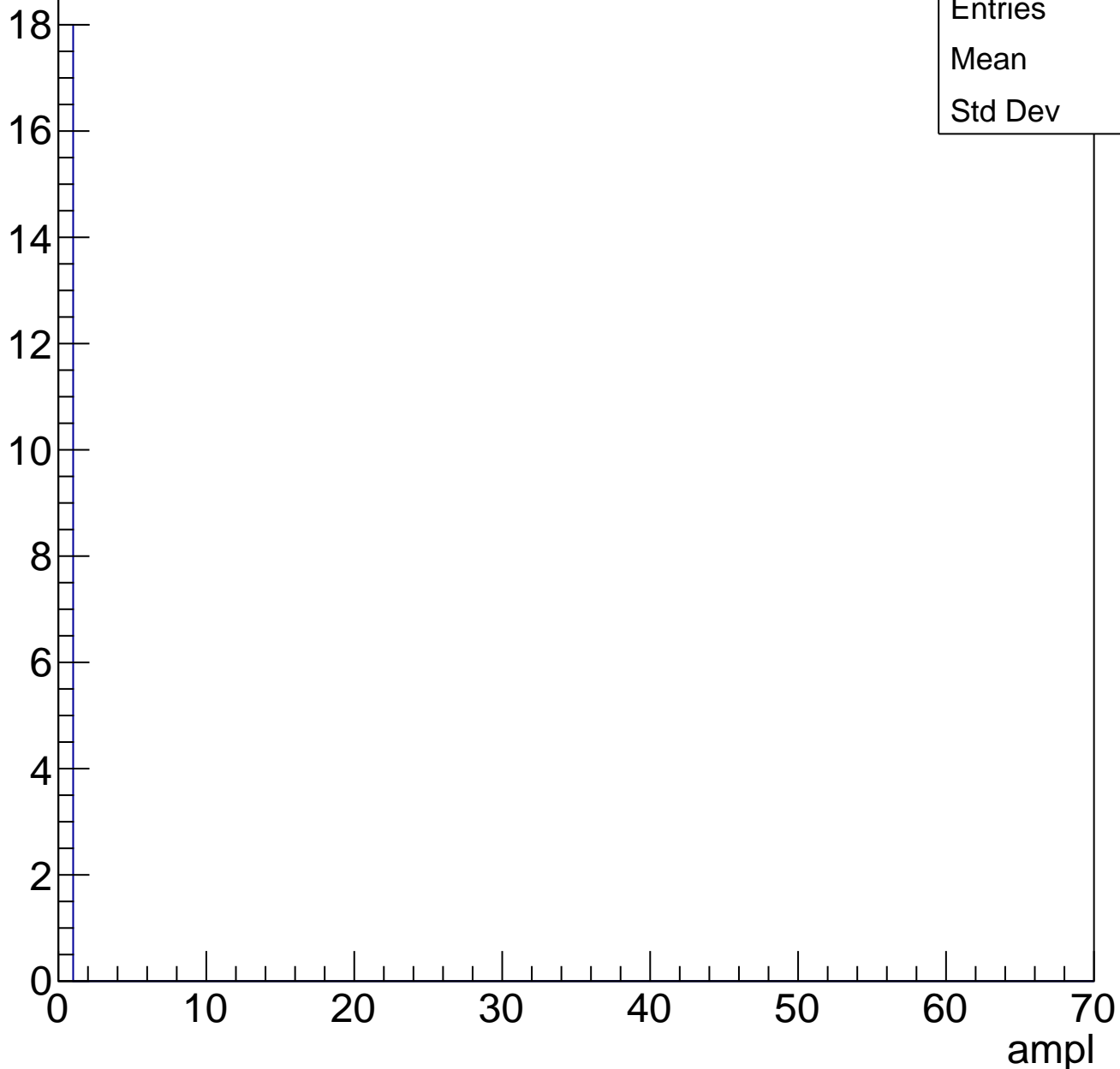
0 10 20 30 40 50 60 70



B1L103S, U13-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch16, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	25.79
Std Dev	11.69

Gaus mean : 30.9881

Gaus Width: 4.0738

Entry

12

10

8

6

4

2

0

0

10

20

30

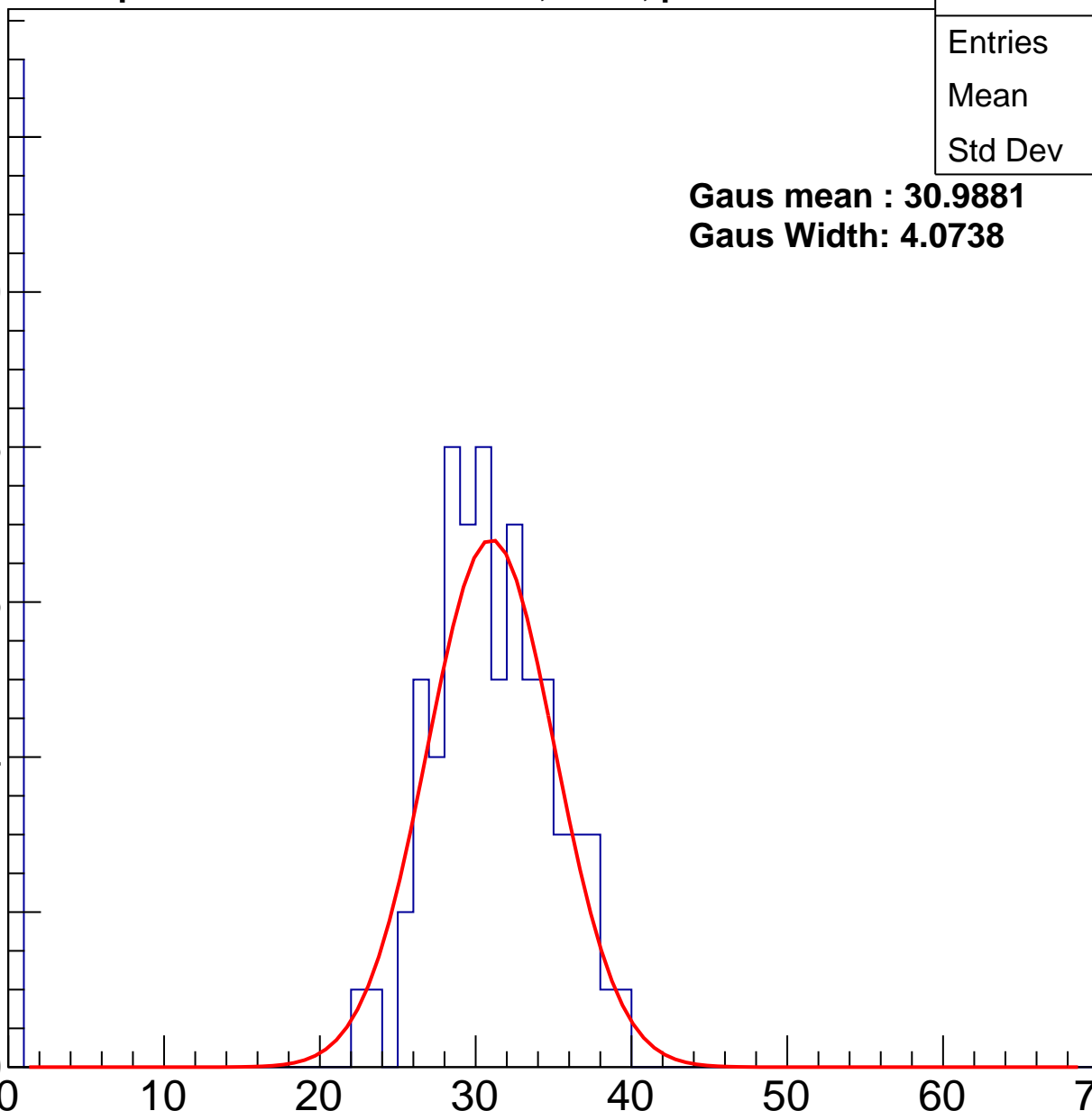
40

50

60

70

ampl



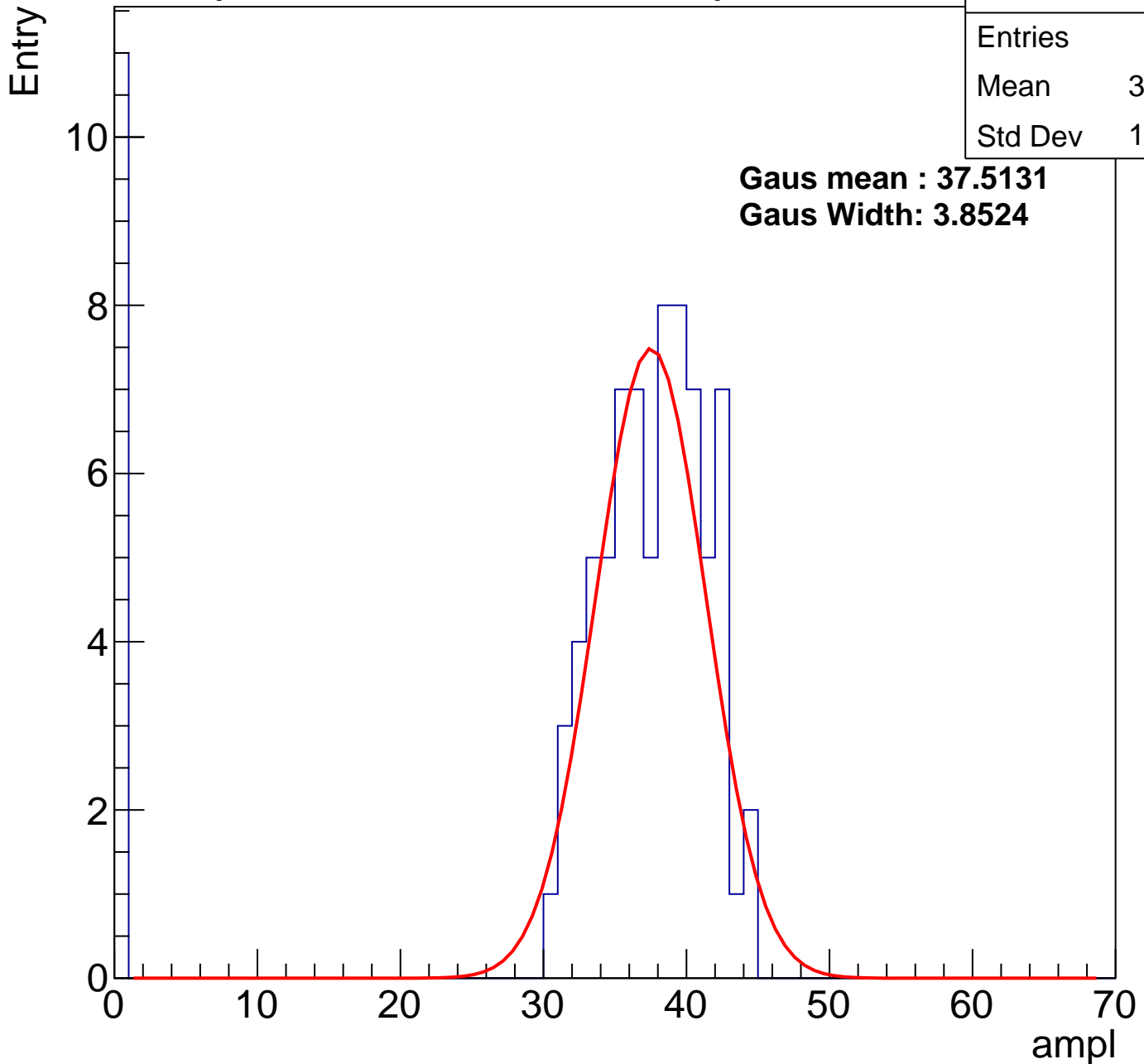
B1L103S, U13-ch16, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	32.49
Std Dev	12.86

Gaus mean : 37.5131

Gaus Width: 3.8524



B1L103S, U13-ch16, adc2

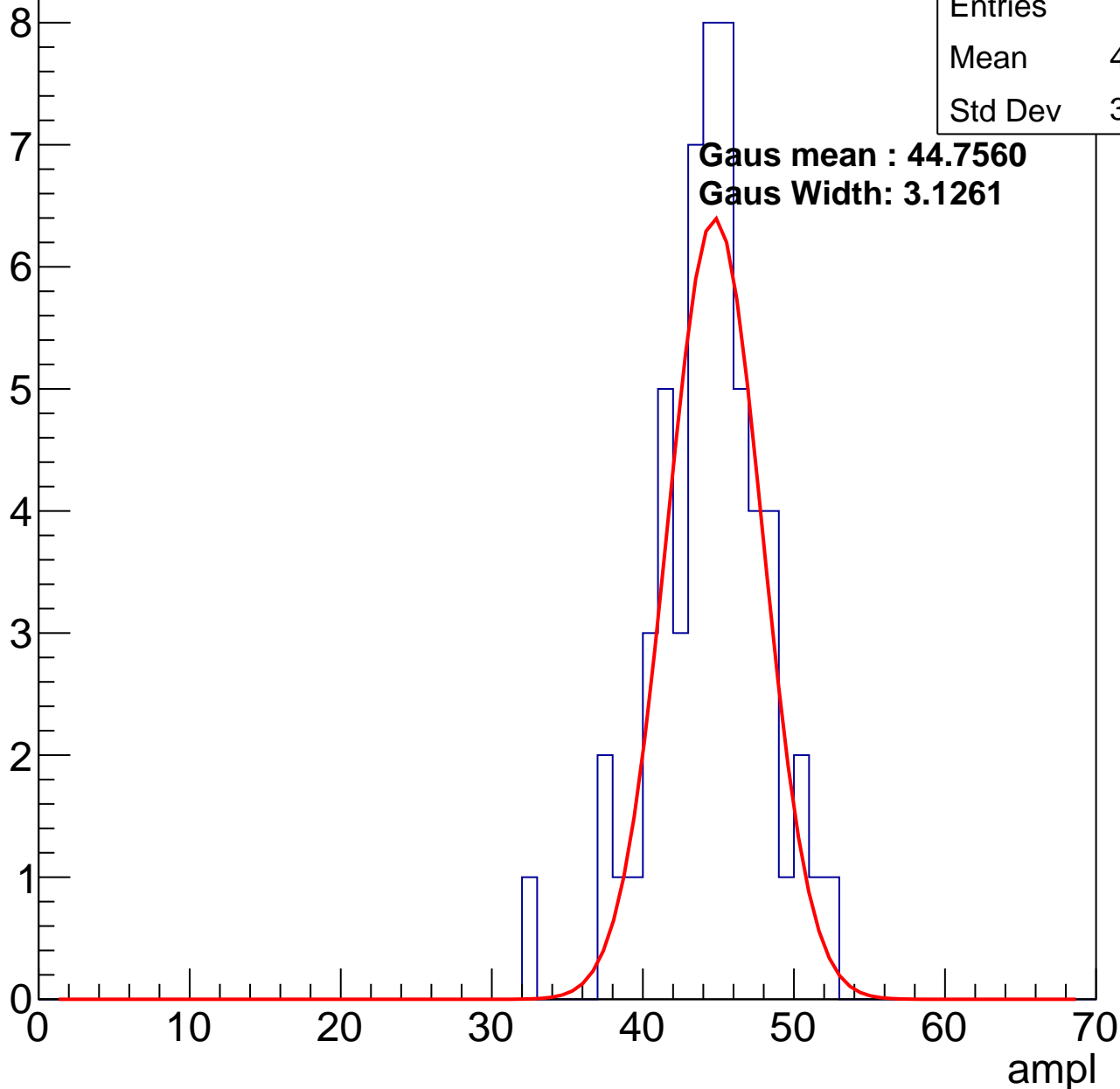
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	44.02
Std Dev	3.635

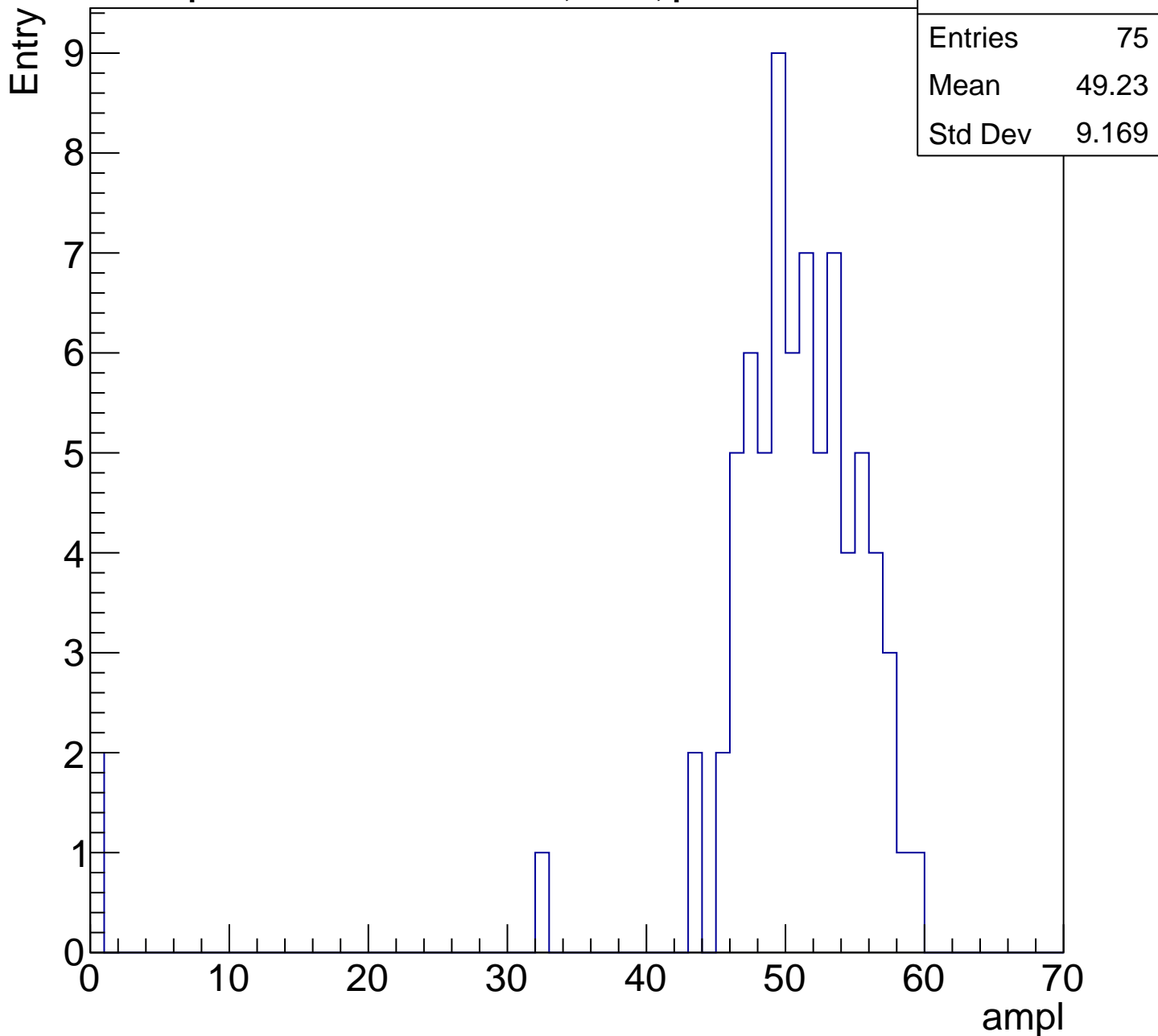
Gaus mean : 44.7560

Gaus Width: 3.1261



B1L103S, U13-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

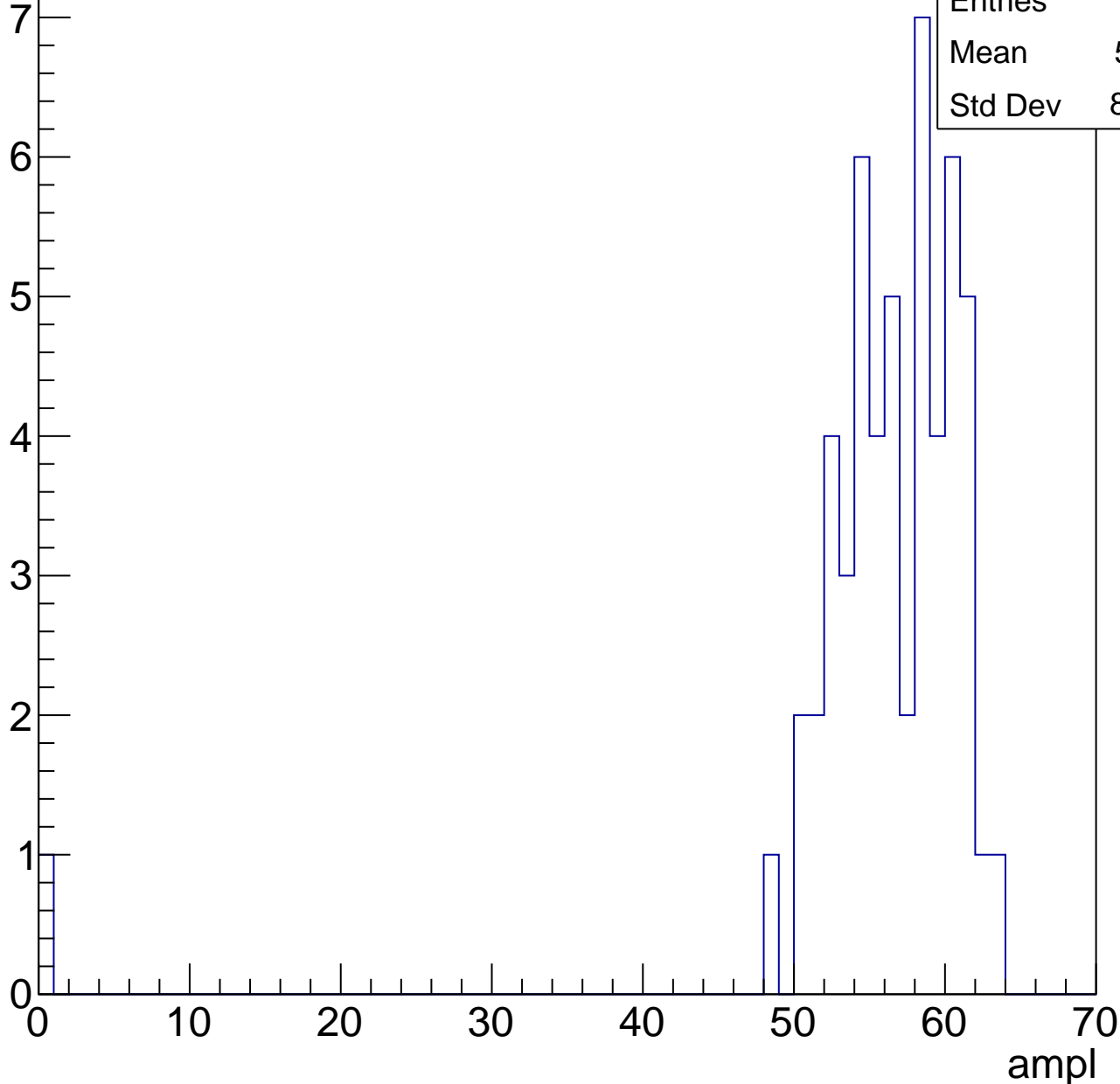


B1L103S, U13-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	55.31
Std Dev	8.375

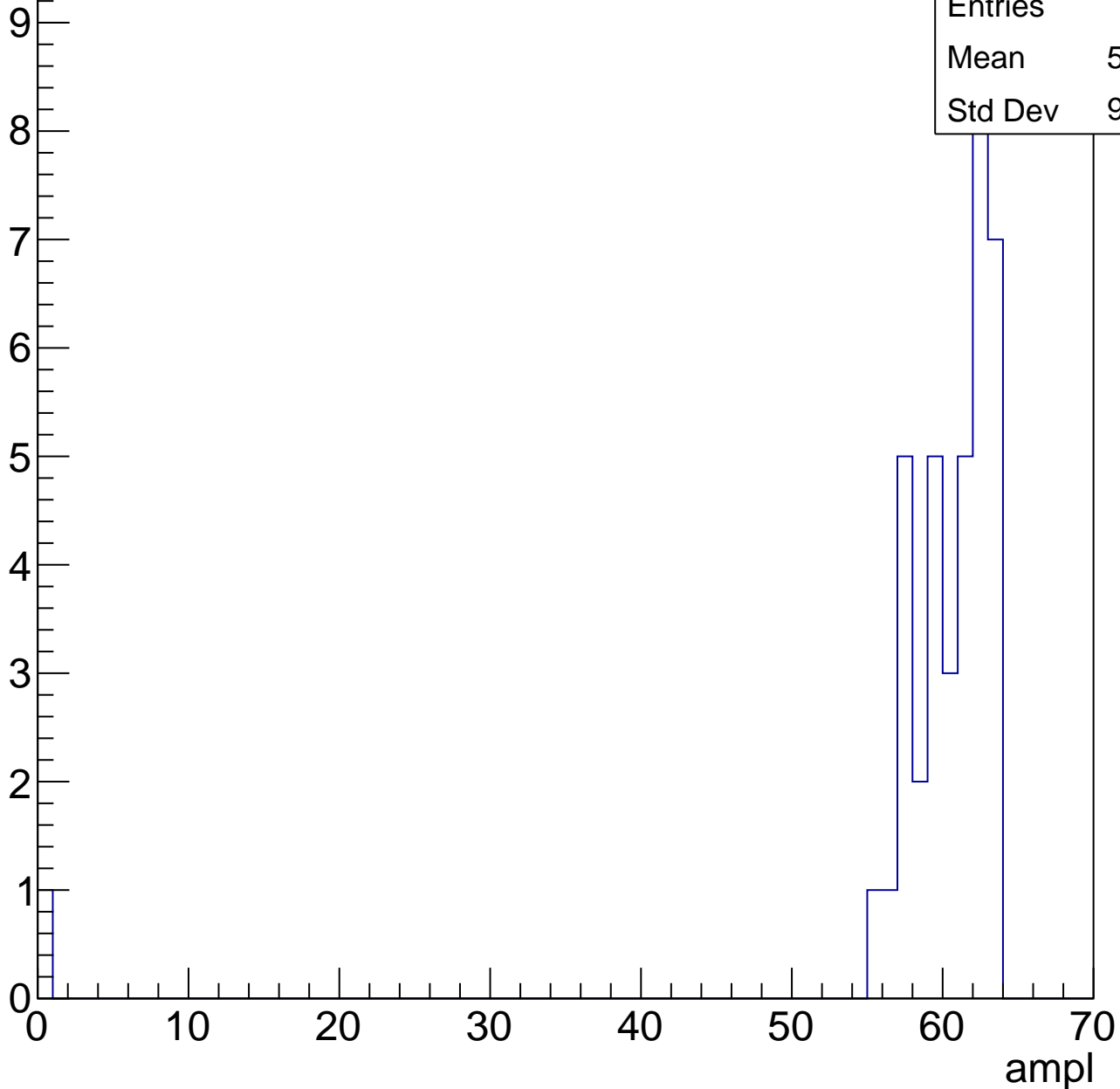


B1L103S, U13-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	58.74
Std Dev	9.795



B1L103S, U13-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

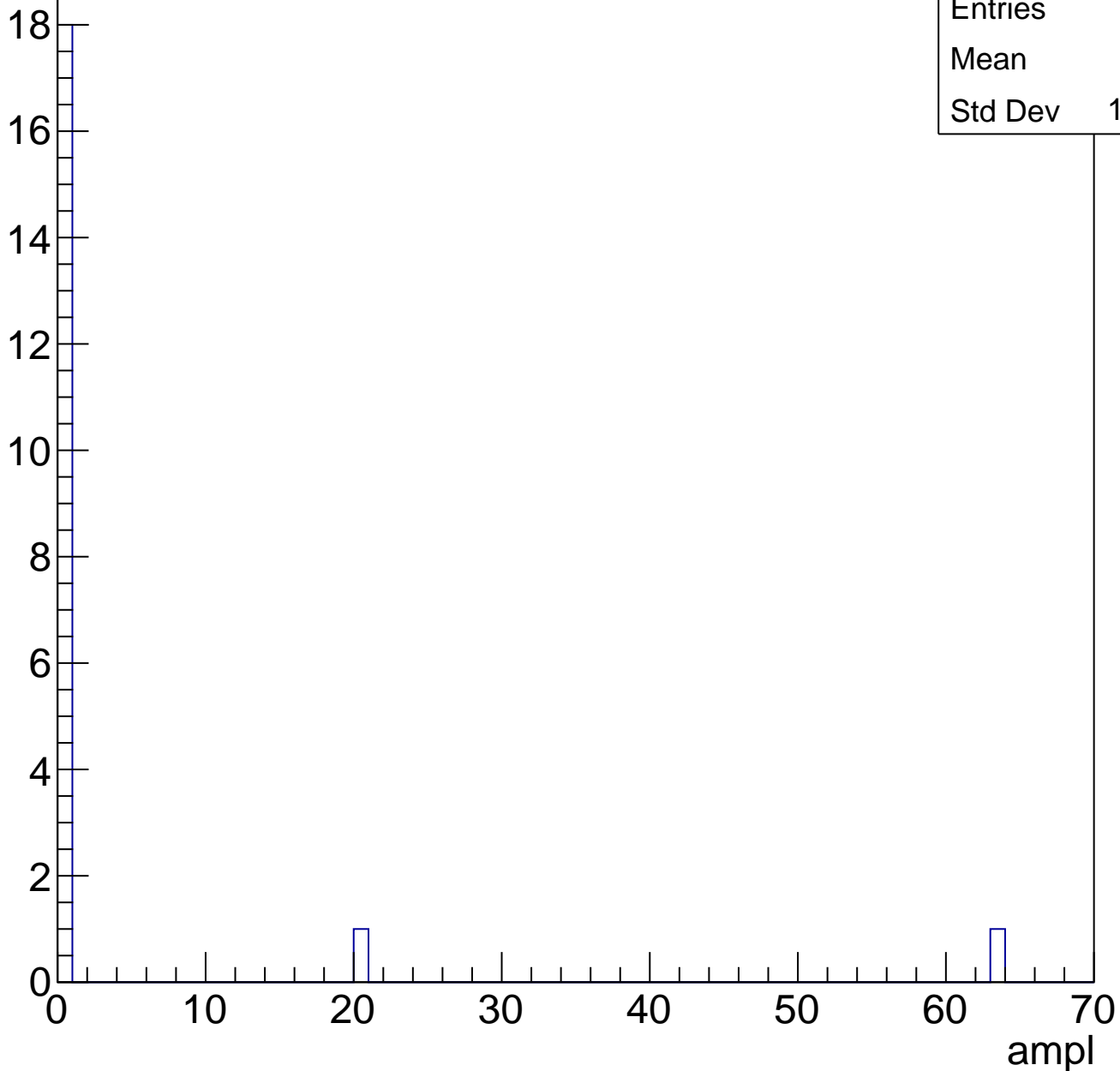


B1L103S, U13-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.15
Std Dev	14.19

Entry



B1L103S, U13-ch17, adc0

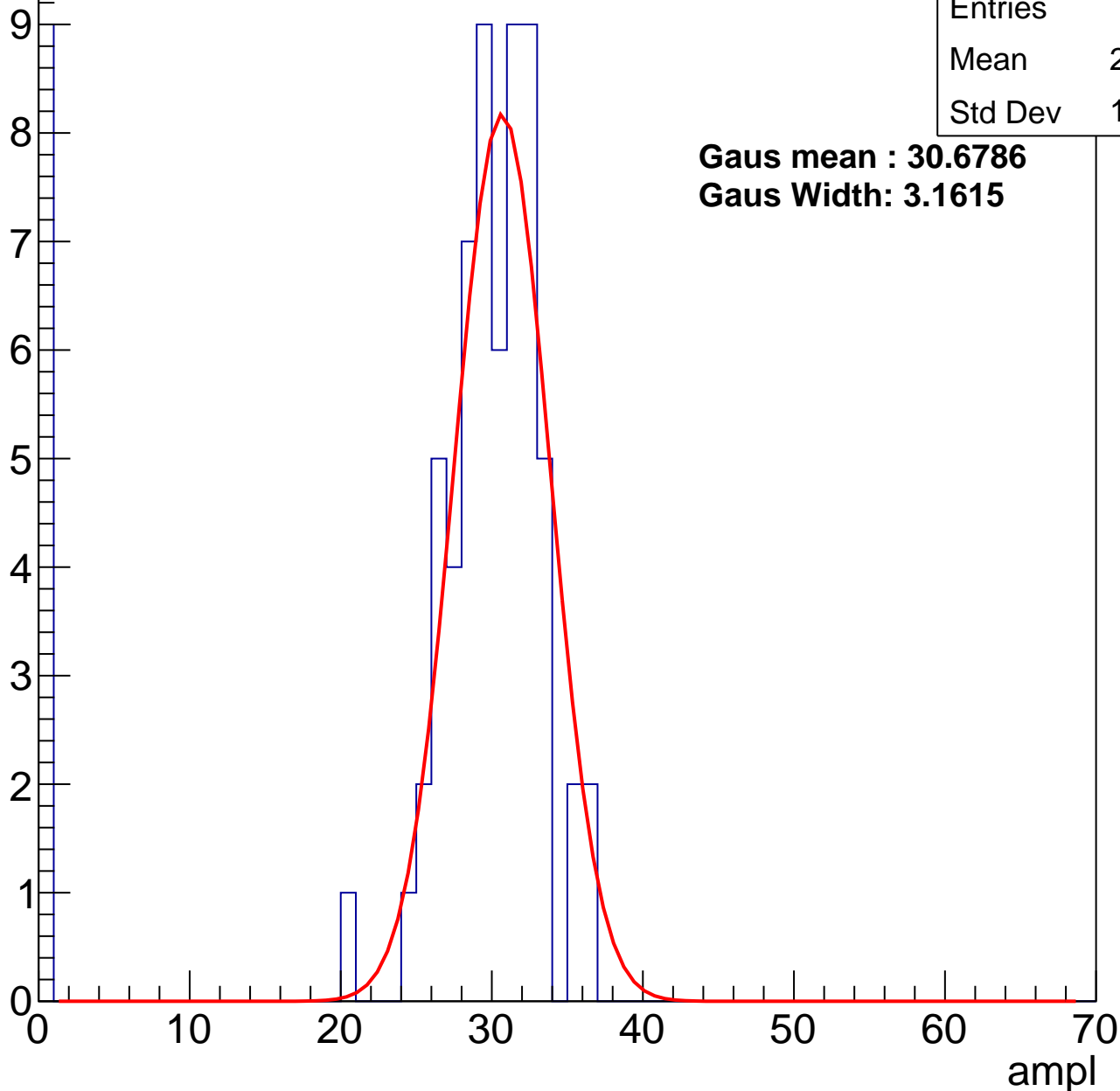
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	25.96
Std Dev	10.27

Gaus mean : 30.6786

Gaus Width: 3.1615



B1L103S, U13-ch17, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	33.6
Std Dev	10.86

Gaus mean : 37.6517

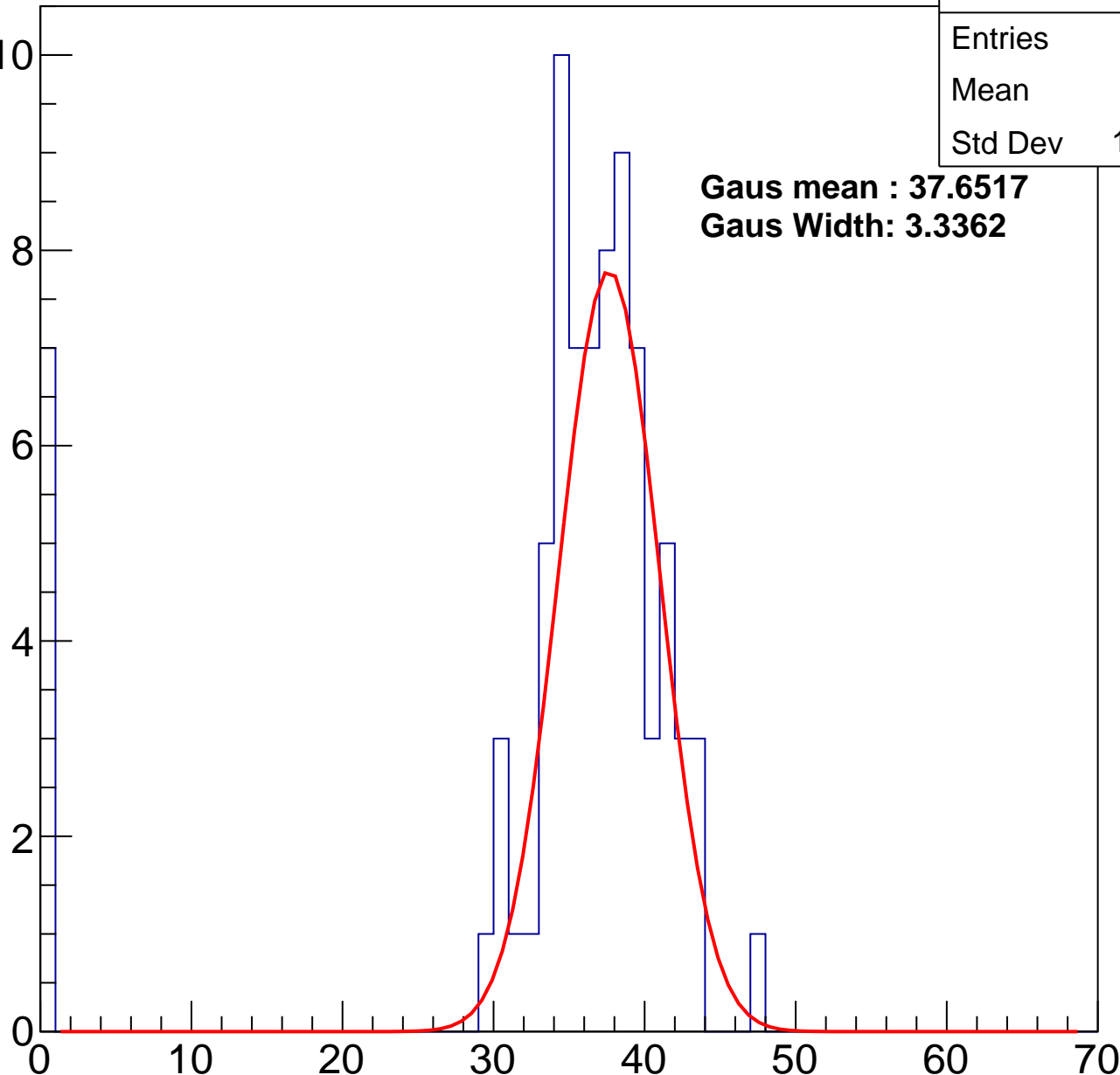
Gaus Width: 3.3362

Entry

10
8
6
4
2
0

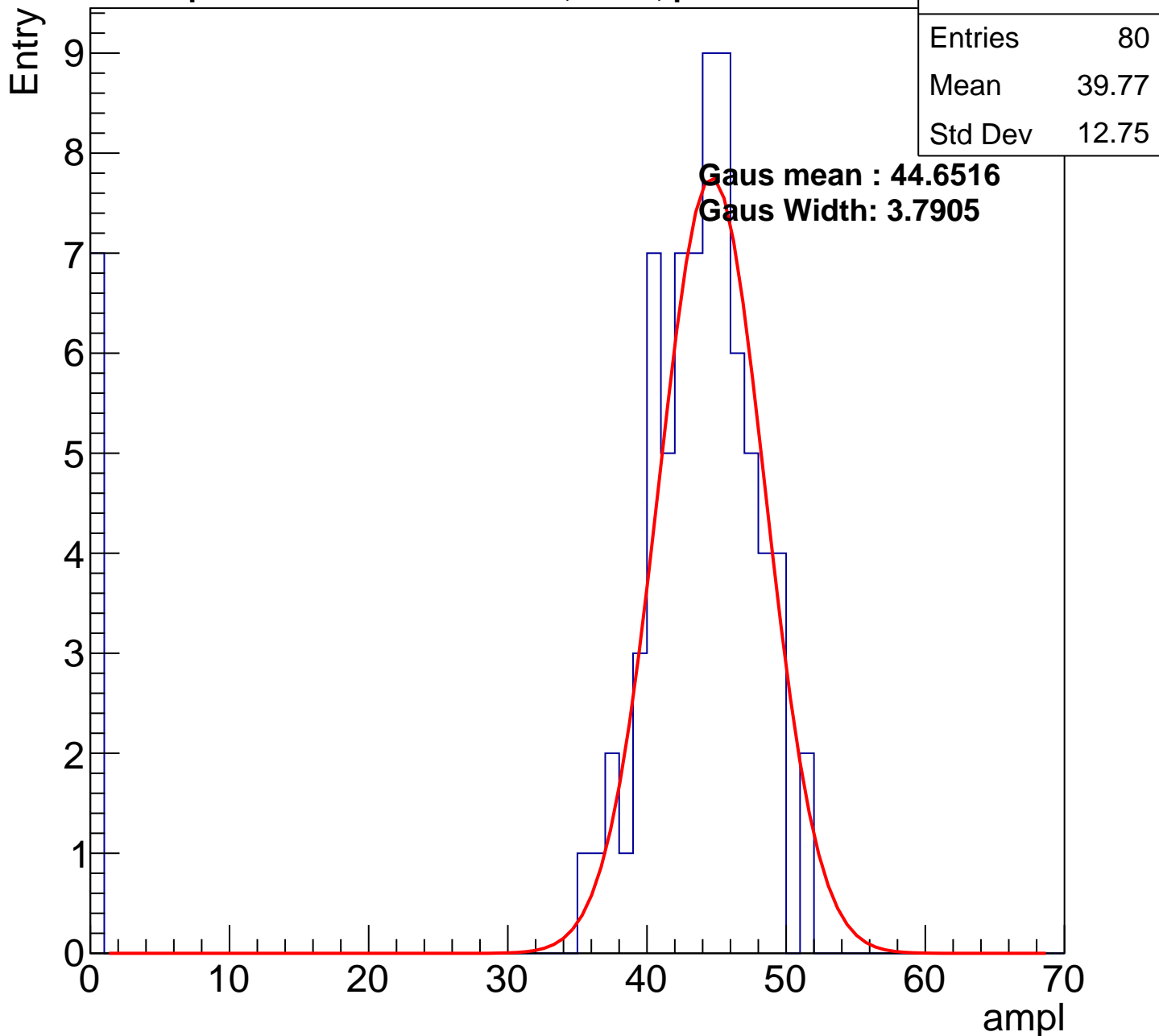
0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch17, adc2

calib_packv5_041523_1651.root, FC#0, port C2

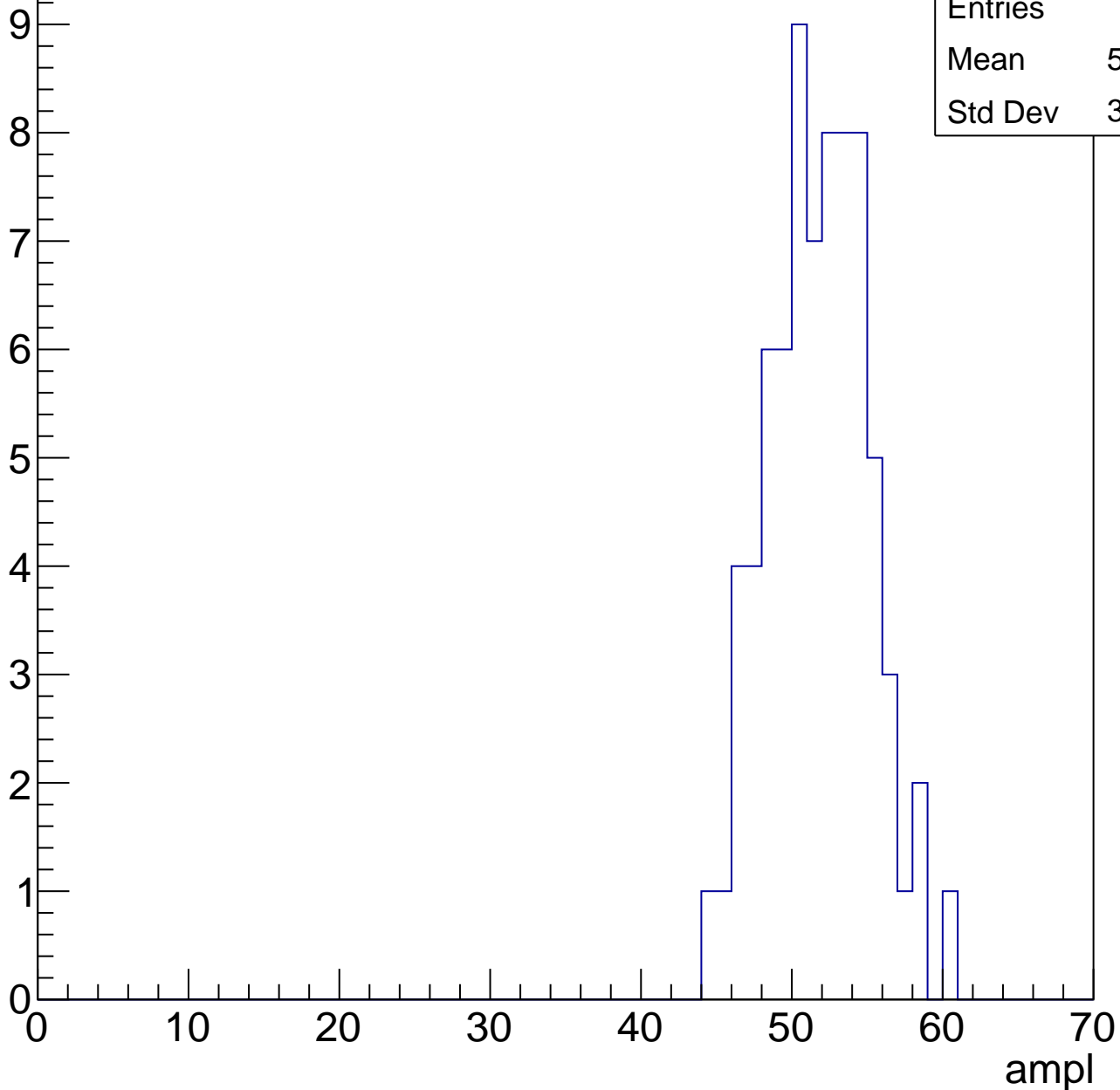


B1L103S, U13-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	51.32
Std Dev	3.309

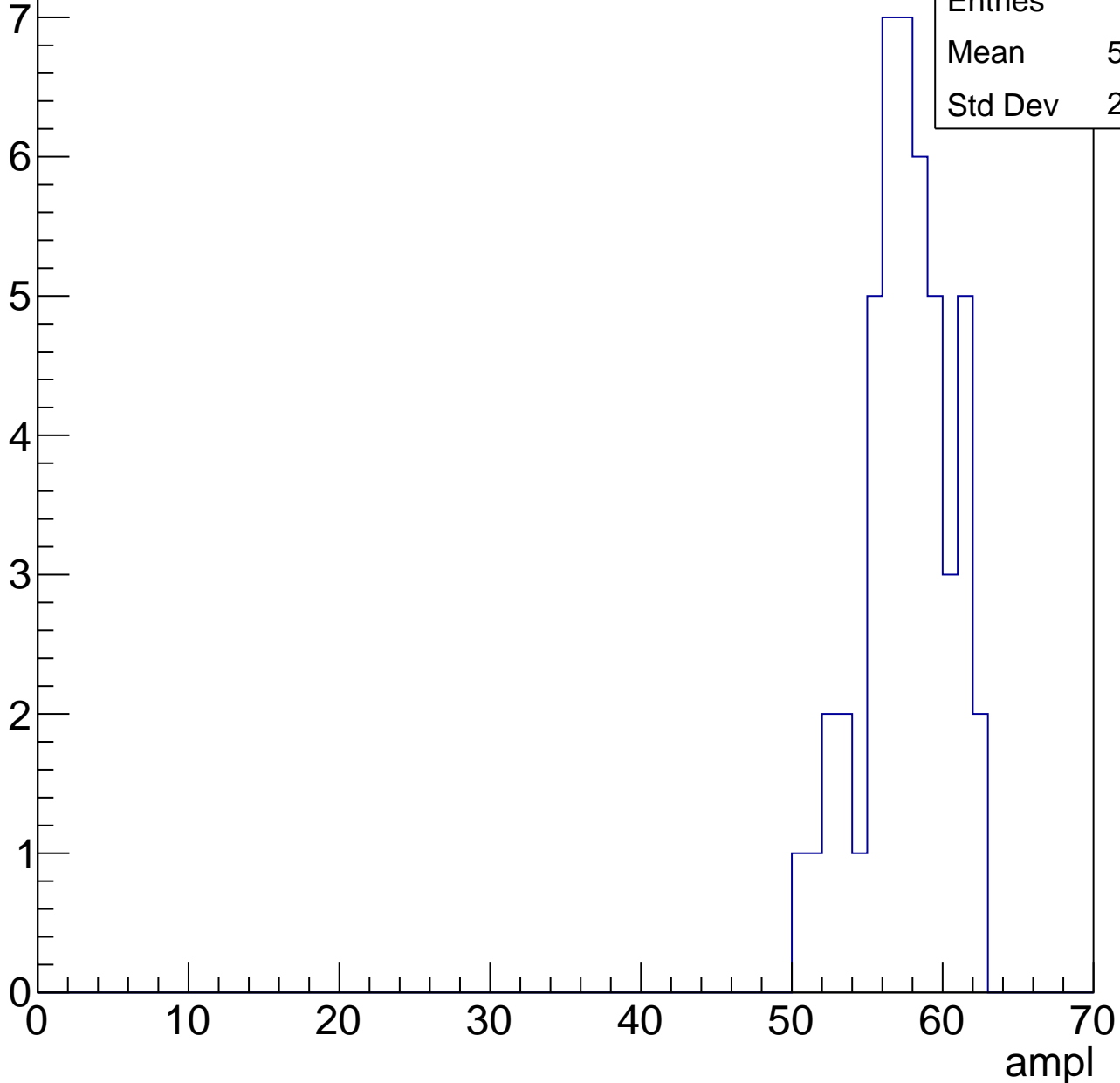


B1L103S, U13-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	57.09
Std Dev	2.865

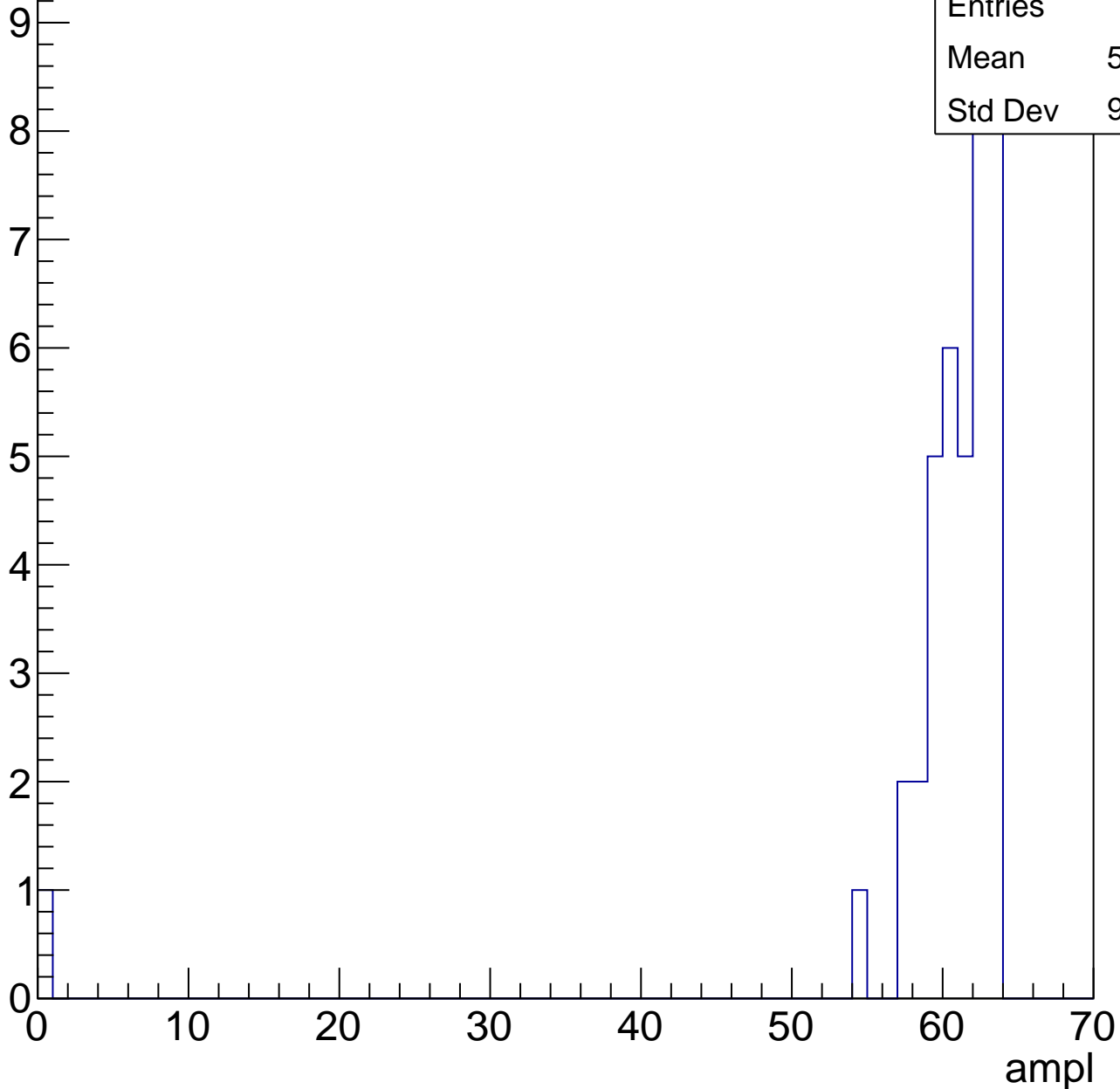


B1L103S, U13-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	59.15
Std Dev	9.815



B1L103S, U13-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch18, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	25.45
Std Dev	10.45

Gaus mean : 29.5770

Gaus Width: 3.6980

Entry

12

10

8

6

4

2

0

0

10

20

30

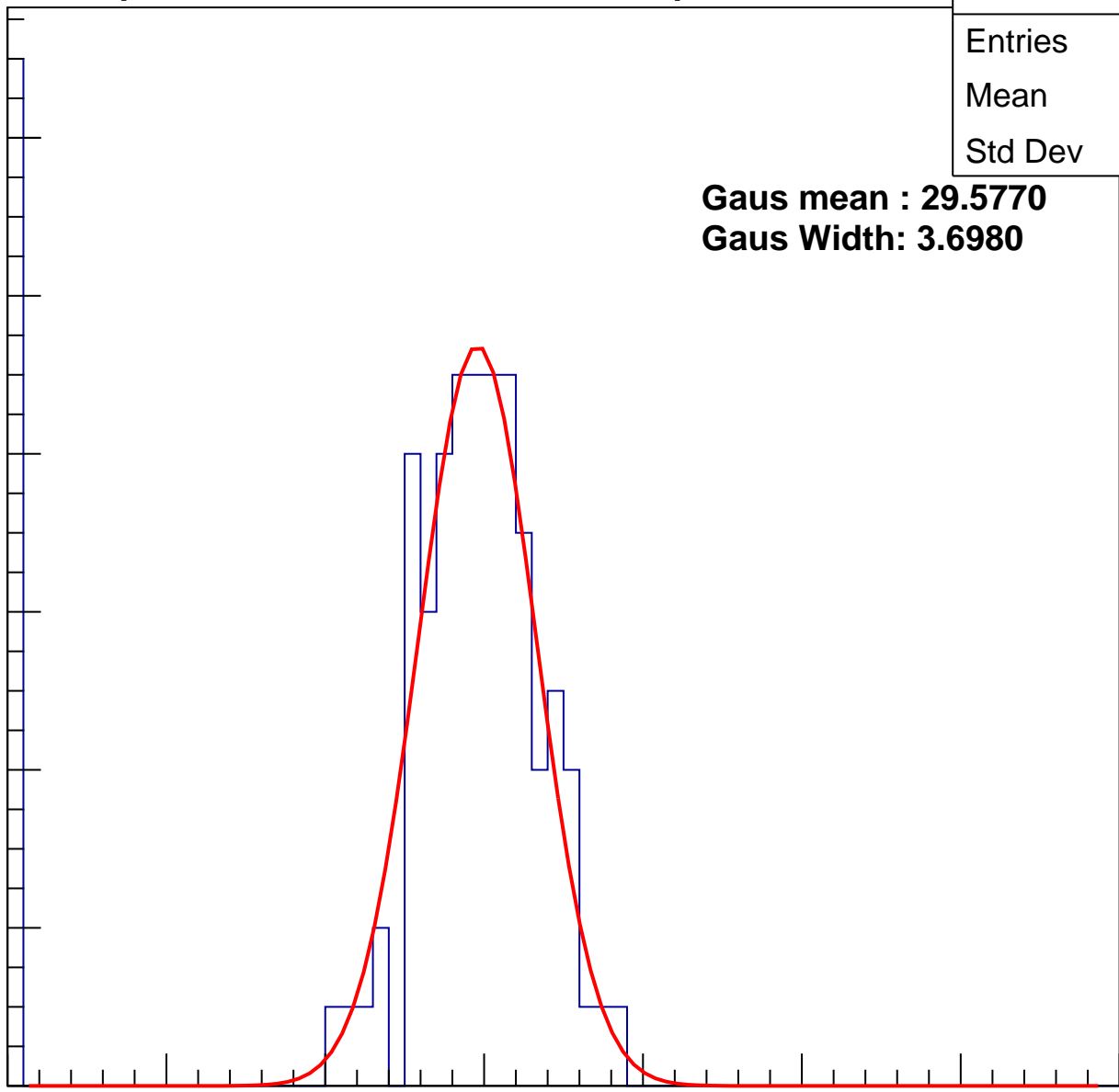
40

50

60

70

ampl



B1L103S, U13-ch18, adc1

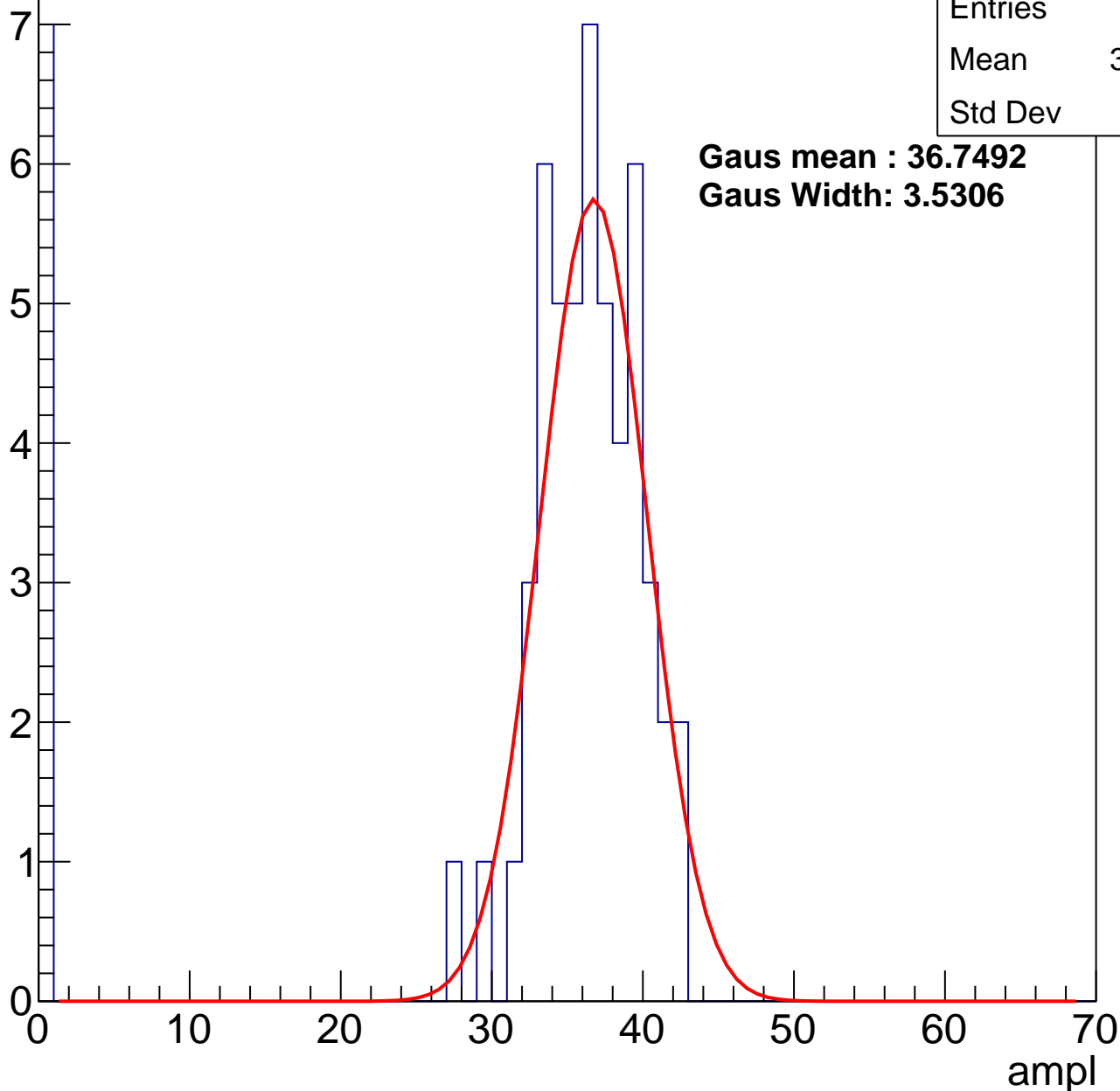
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	31.64
Std Dev	12.1

Gaus mean : 36.7492

Gaus Width: 3.5306



B1L103S, U13-ch18, adc2

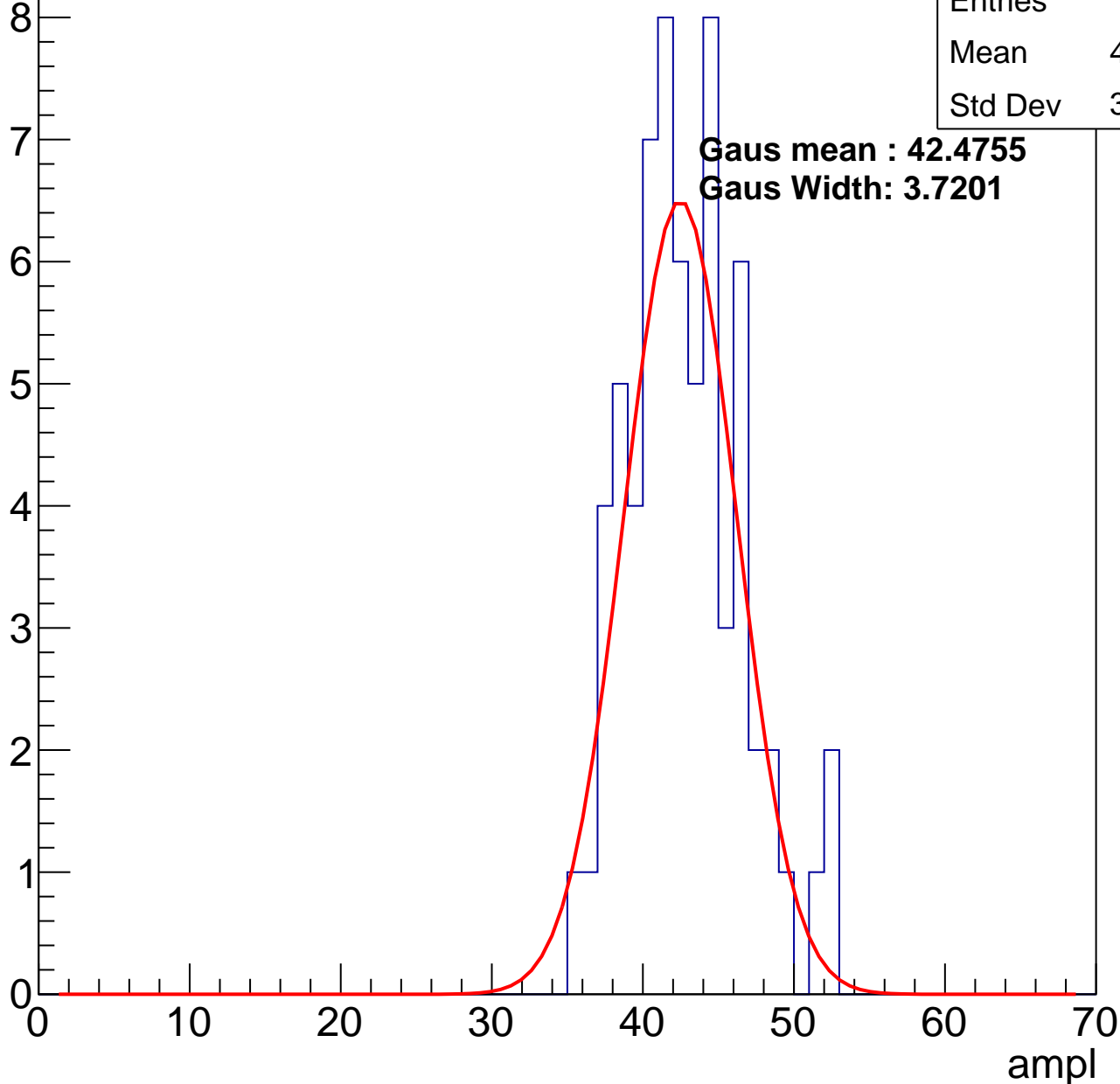
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.38
Std Dev	3.773

Gaus mean : 42.4755

Gaus Width: 3.7201

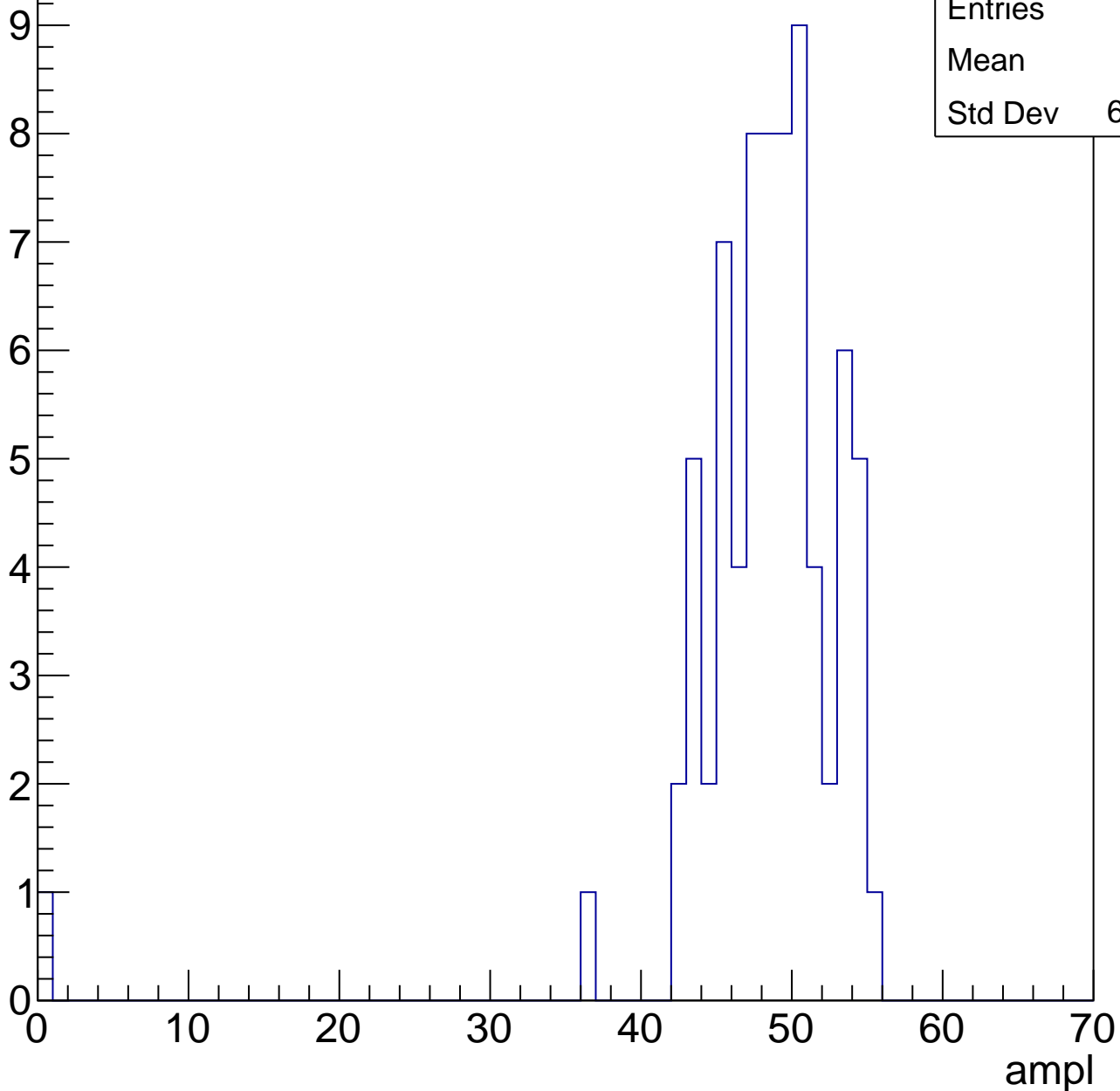


B1L103S, U13-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

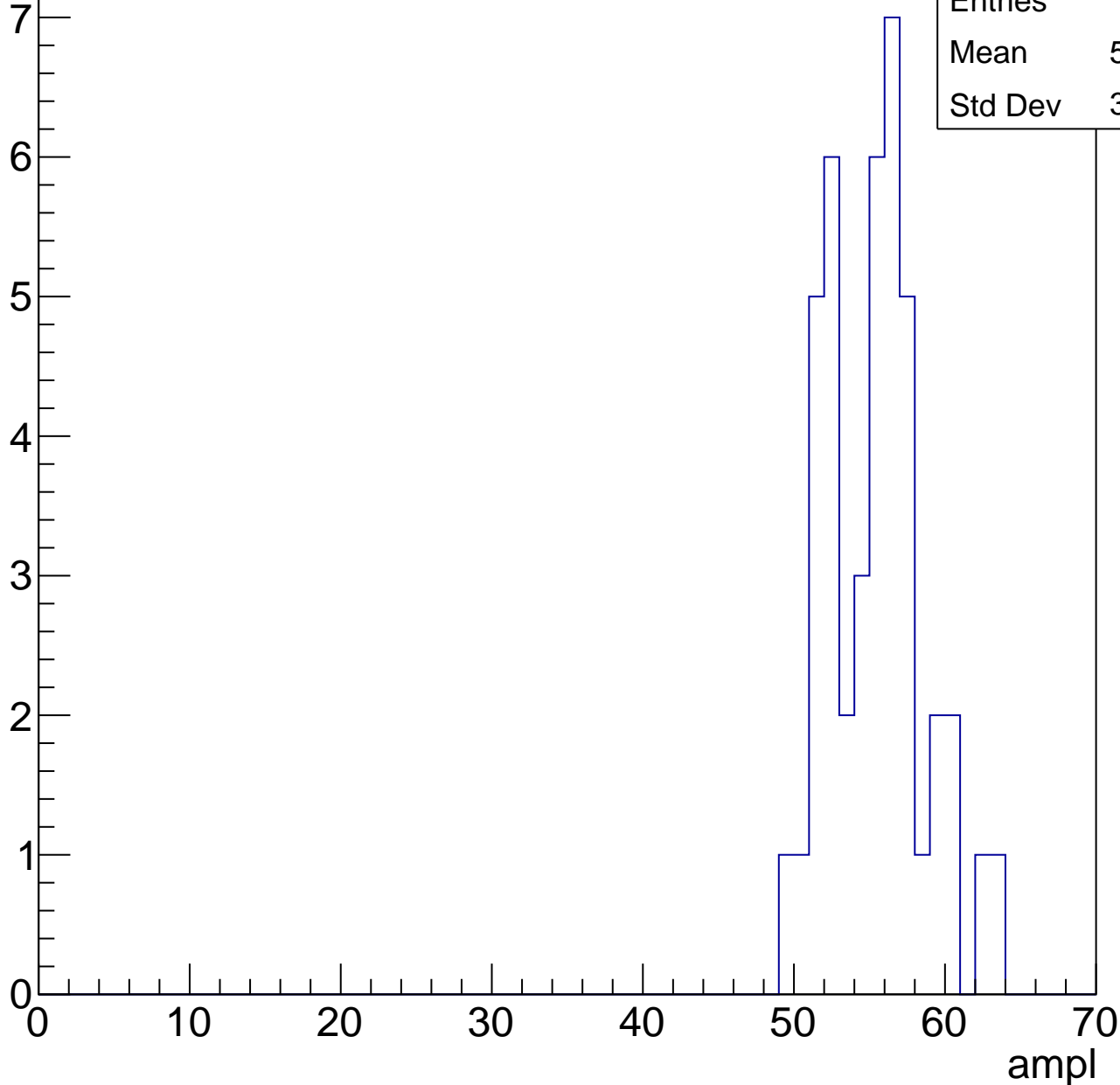
Entries	73
Mean	47.6
Std Dev	6.666



B1L103S, U13-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

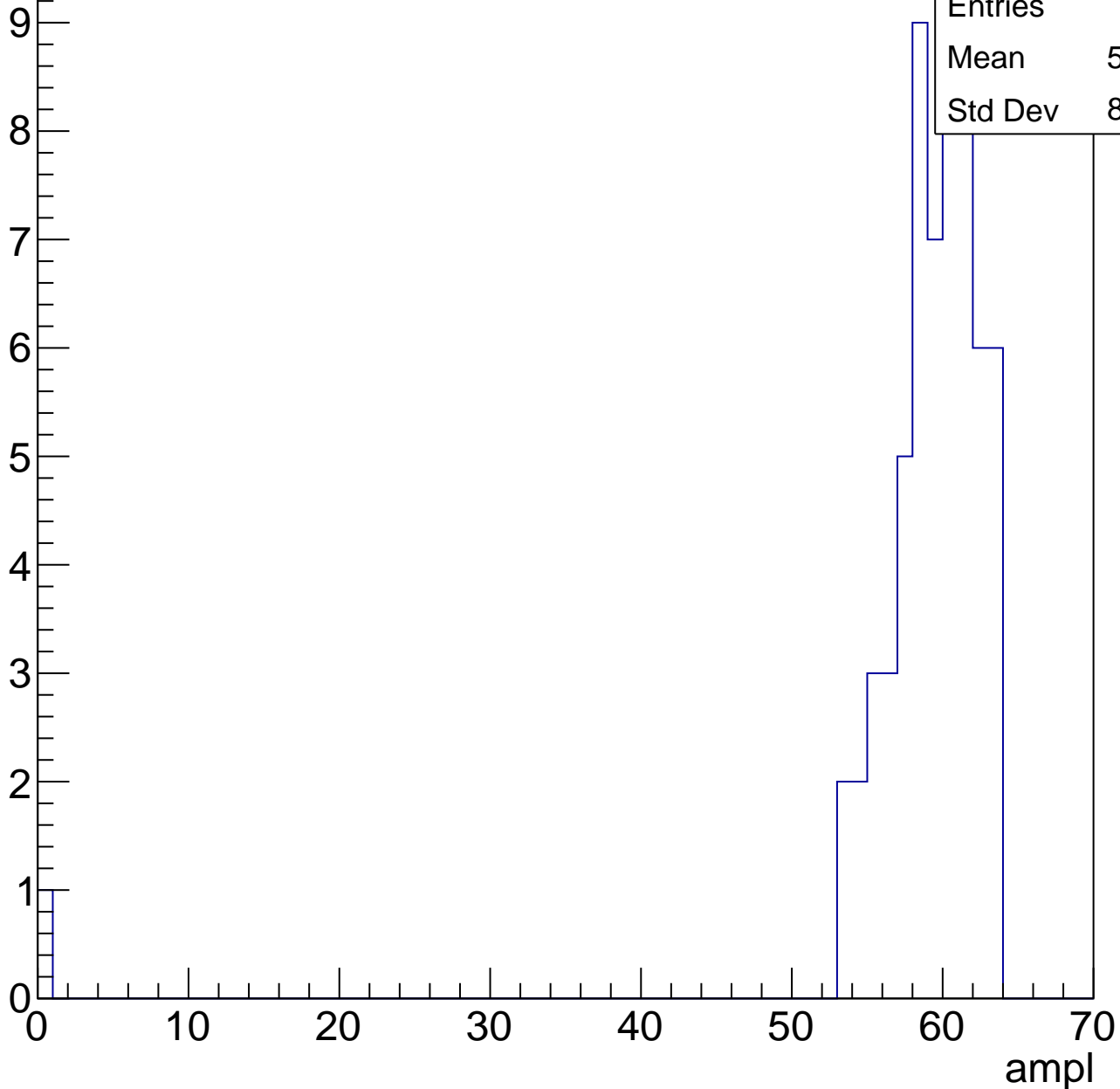
Entry



B1L103S, U13-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

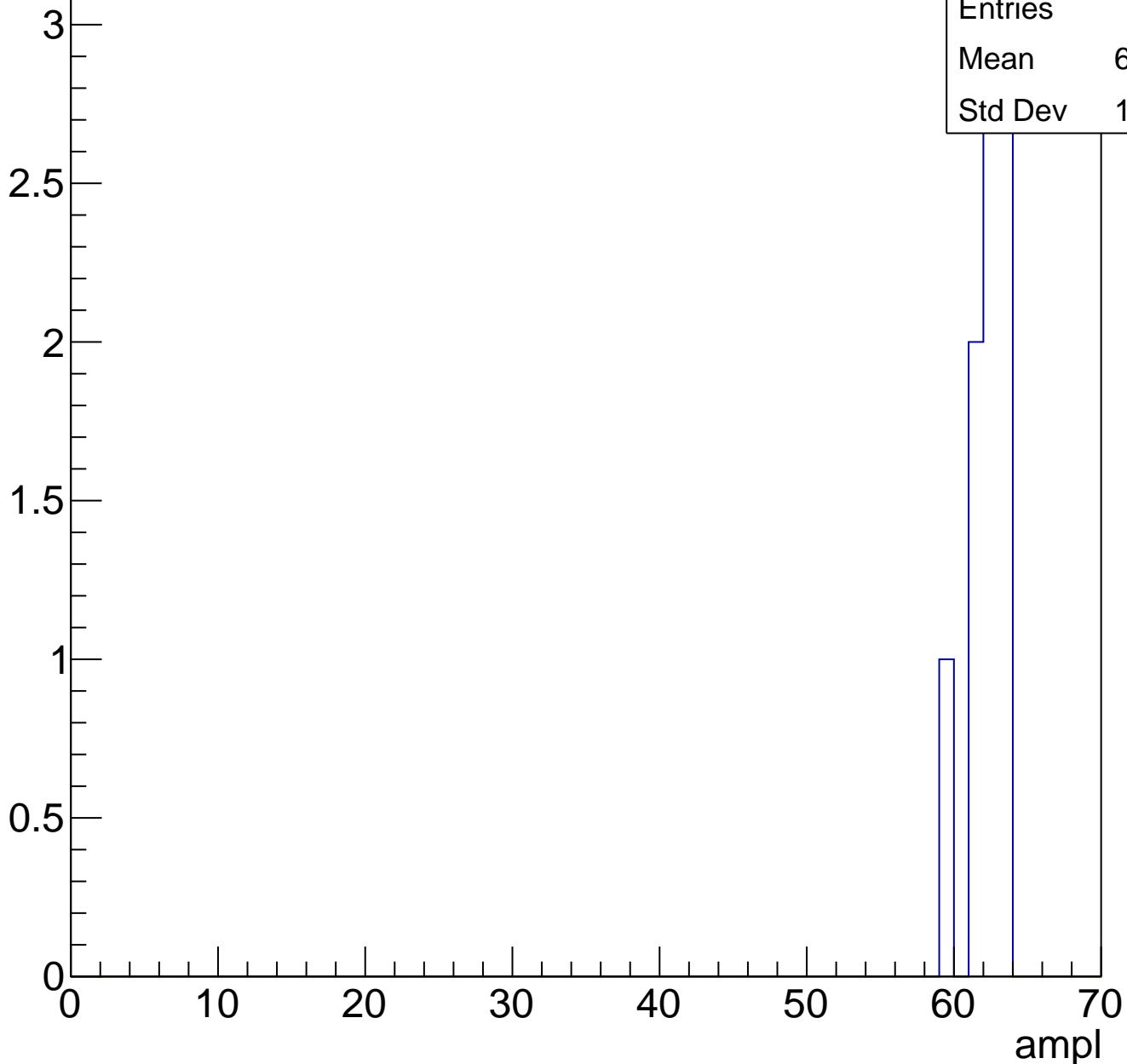
Entry



B1L103S, U13-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.056
Std Dev	4.352

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

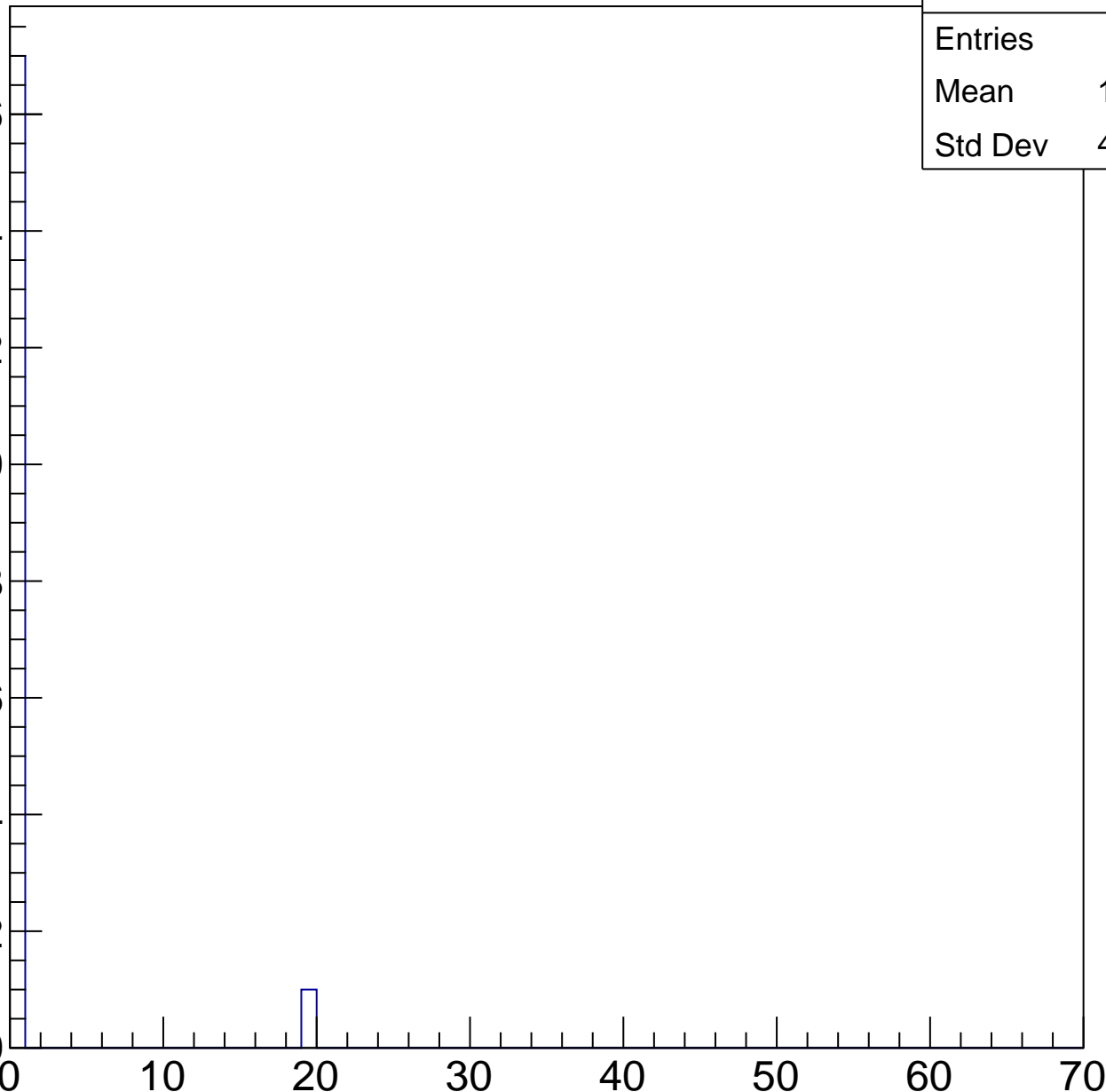
40

50

60

70

ampl



B1L103S, U13-ch19, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	25.36
Std Dev	11.58

Gaus mean : 30.7593

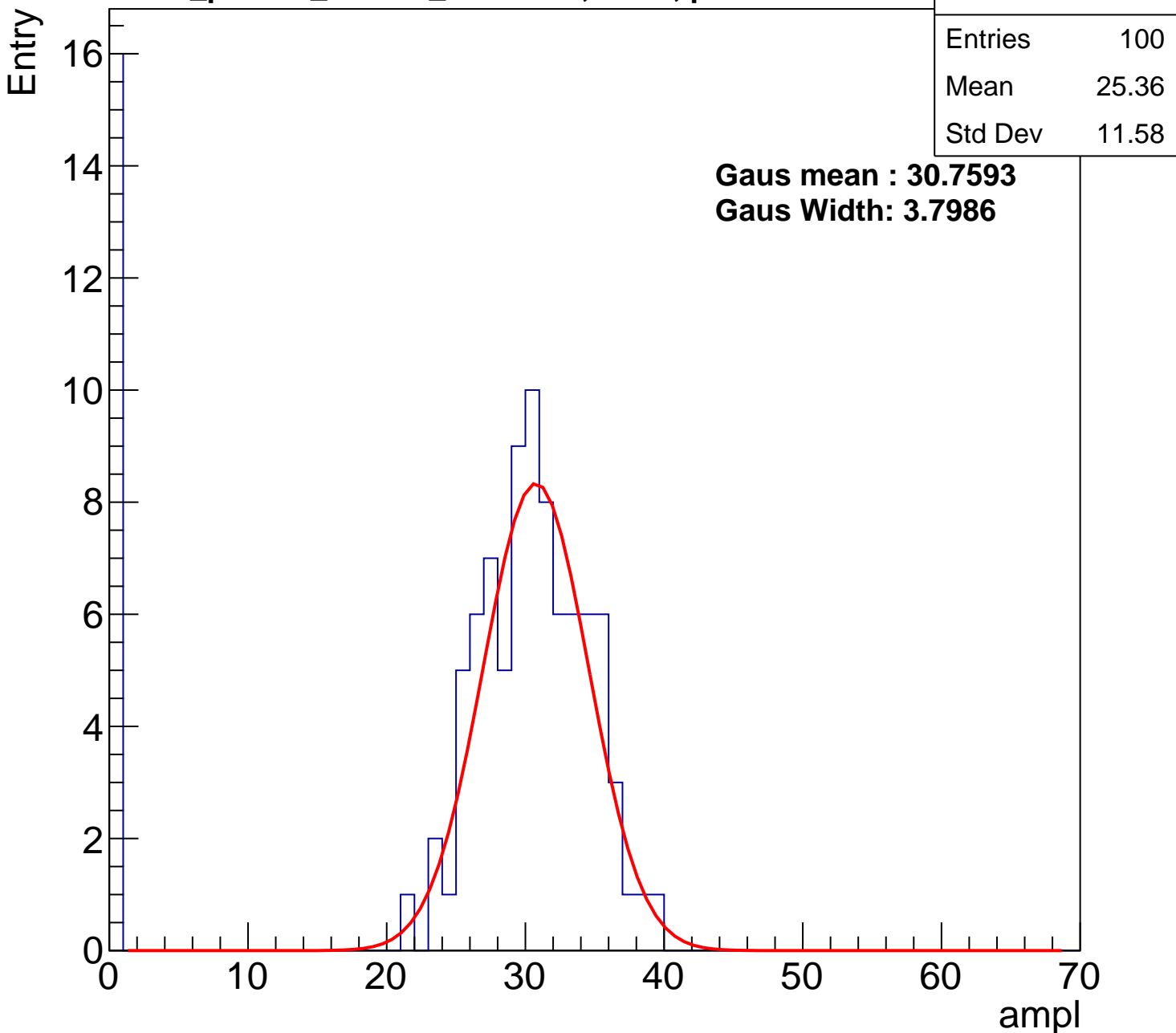
Gaus Width: 3.7986

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



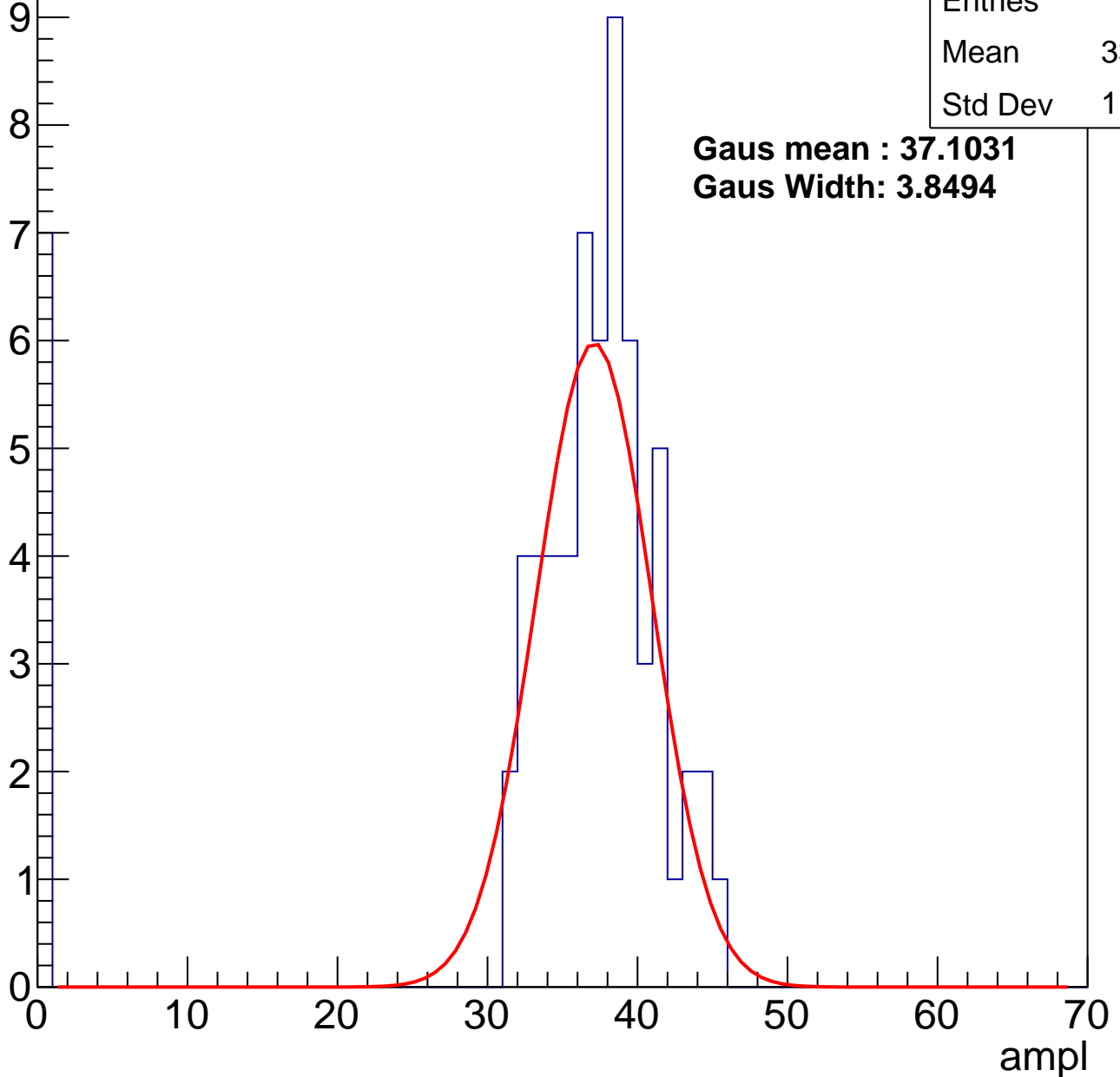
B1L103S, U13-ch19, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.34
Std Dev	11.83

Gaus mean : 37.1031
Gaus Width: 3.8494



B1L103S, U13-ch19, adc2

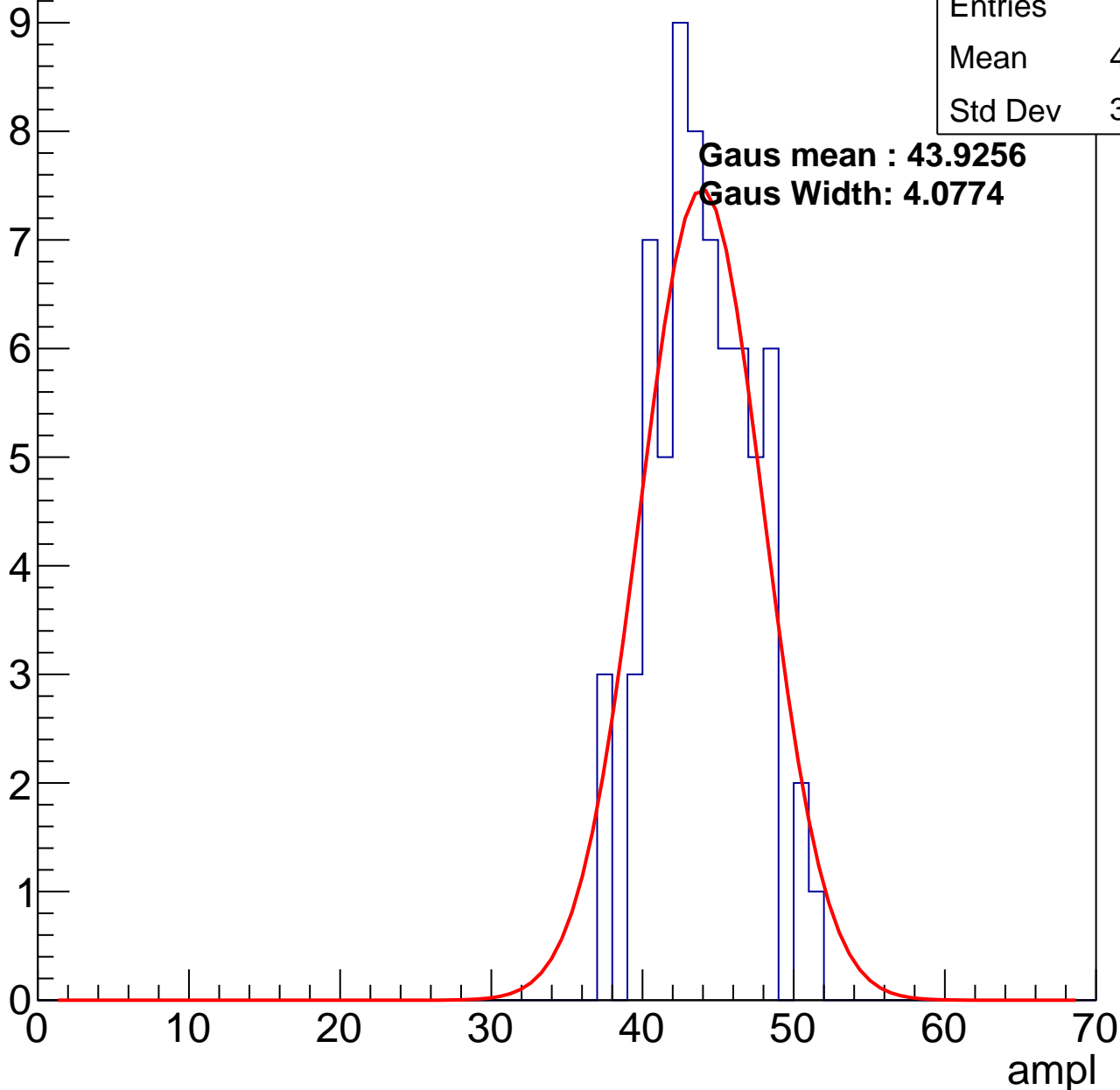
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	43.57
Std Dev	3.214

Gaus mean : 43.9256

Gaus Width: 4.0774



B1L103S, U13-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

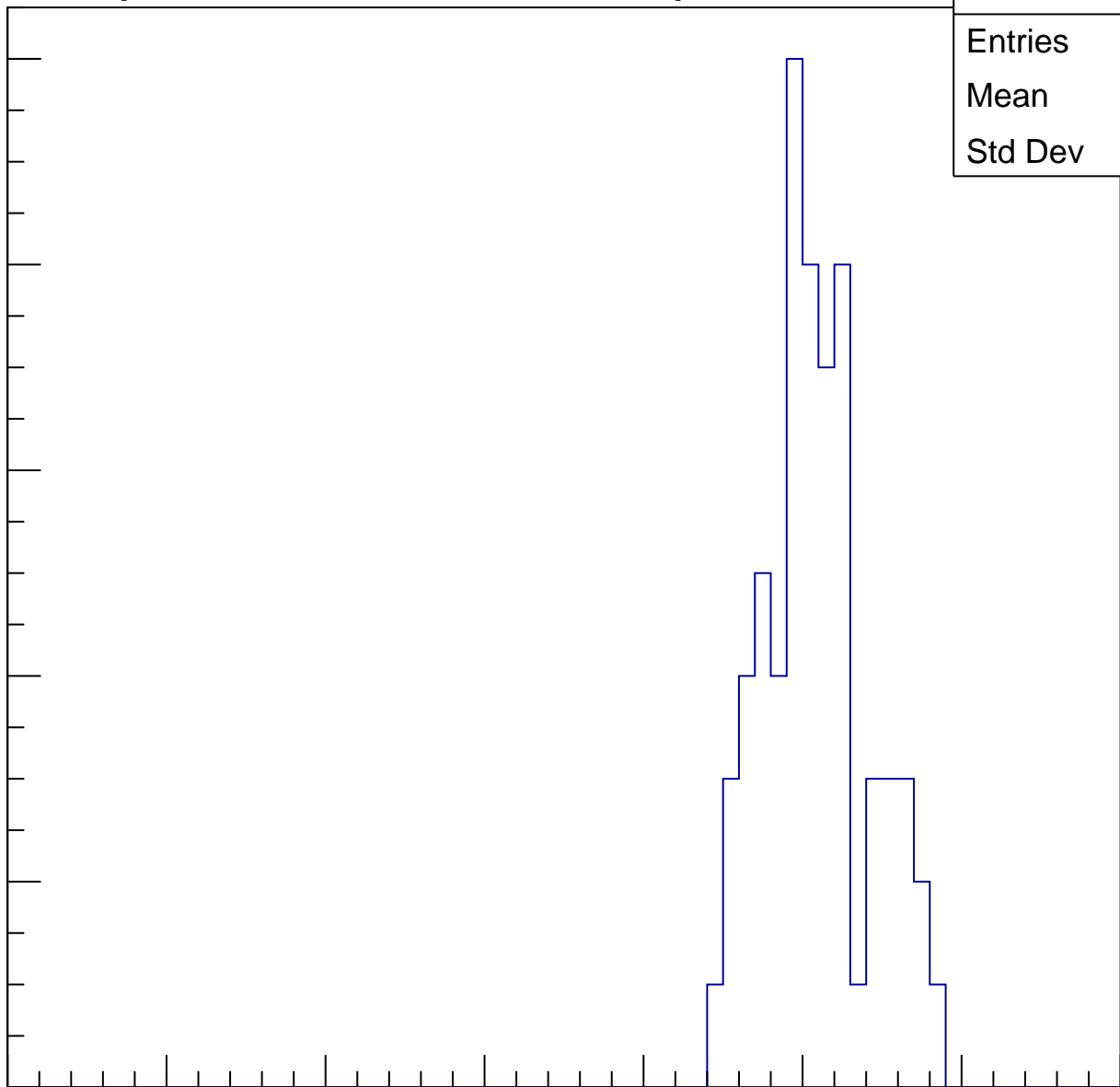
Entries	63
Mean	50.37
Std Dev	3.282

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

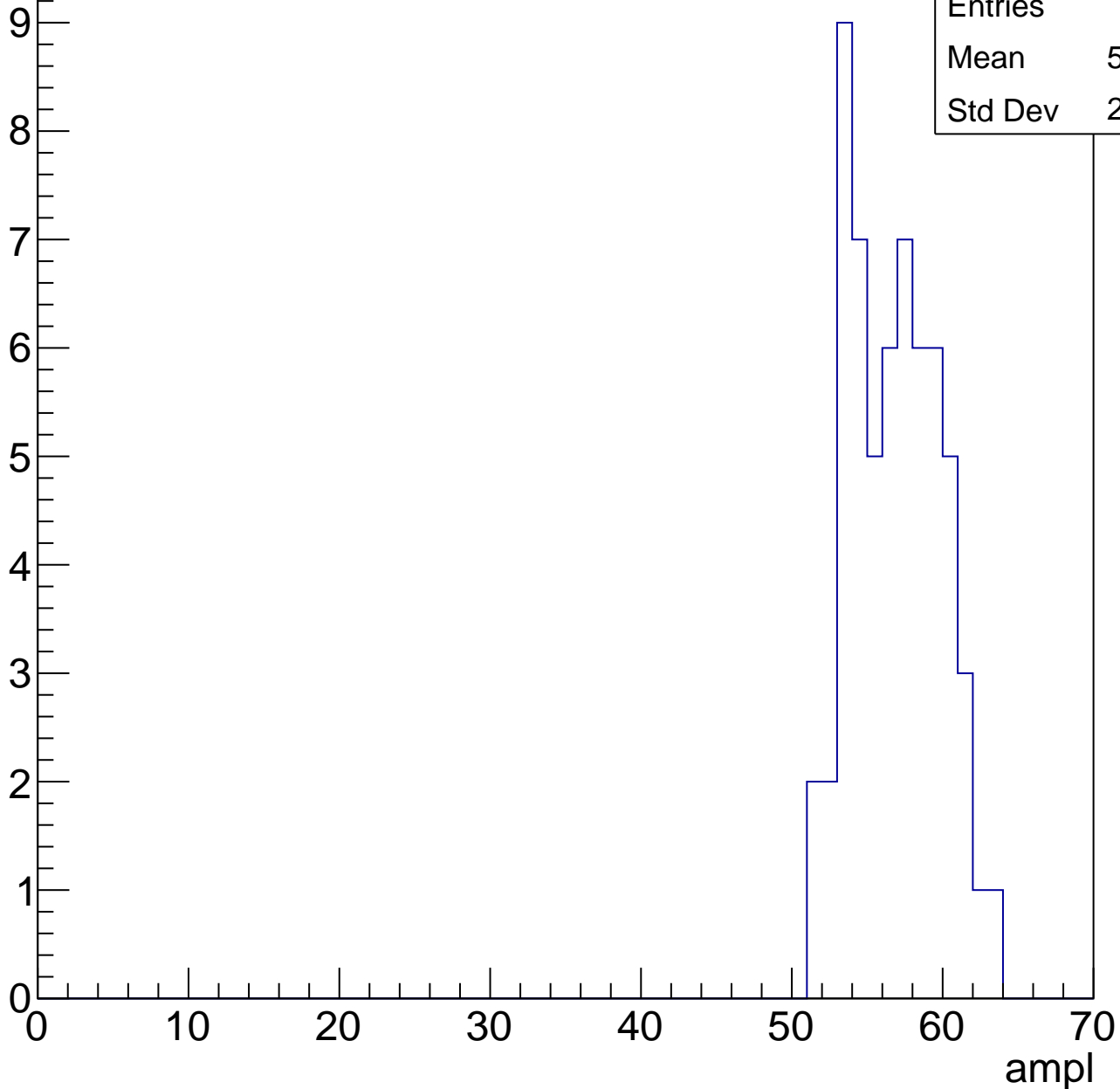


B1L103S, U13-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	56.35
Std Dev	2.926

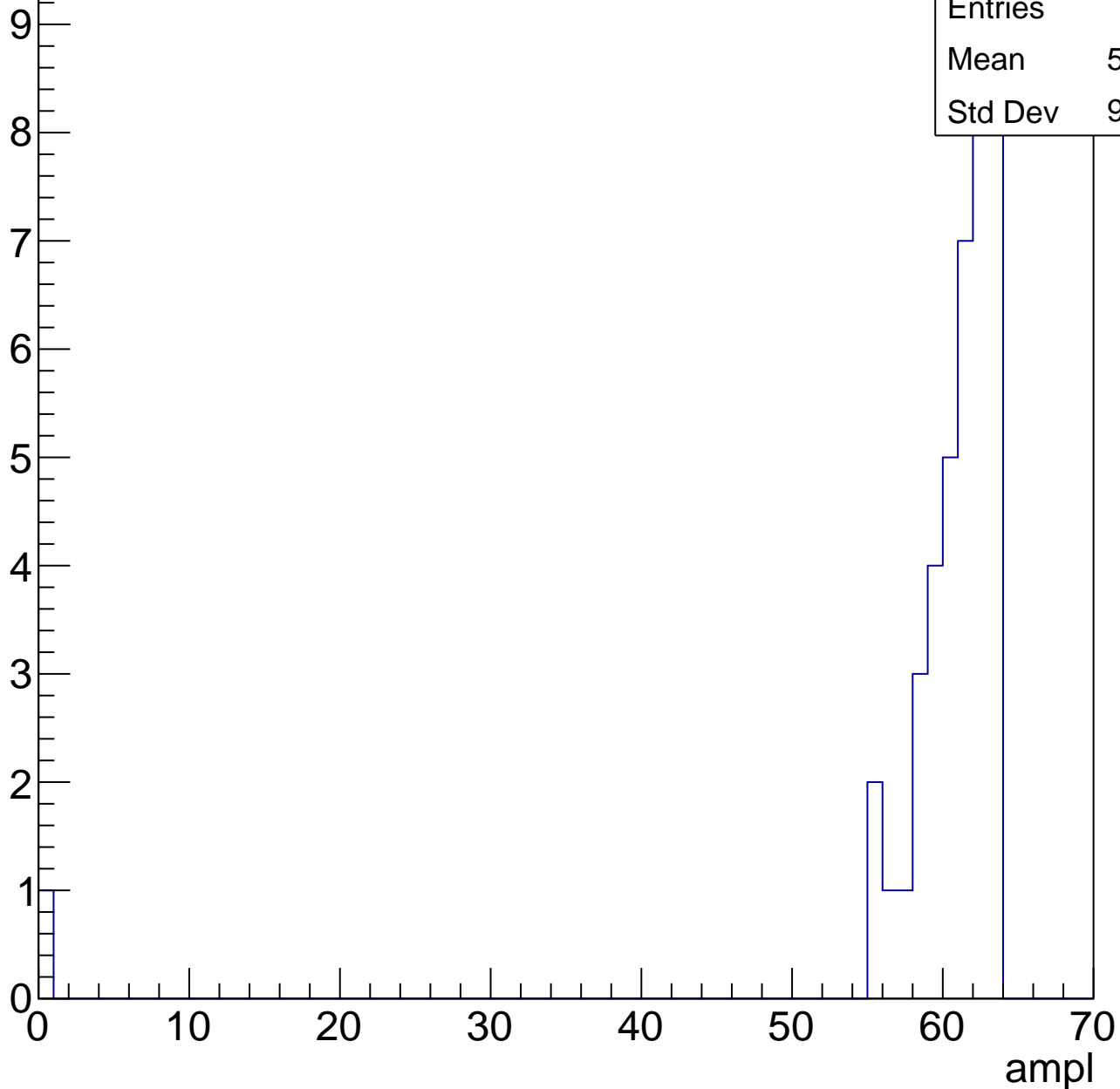


B1L103S, U13-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	59.17
Std Dev	9.494



B1L103S, U13-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch20, adc0

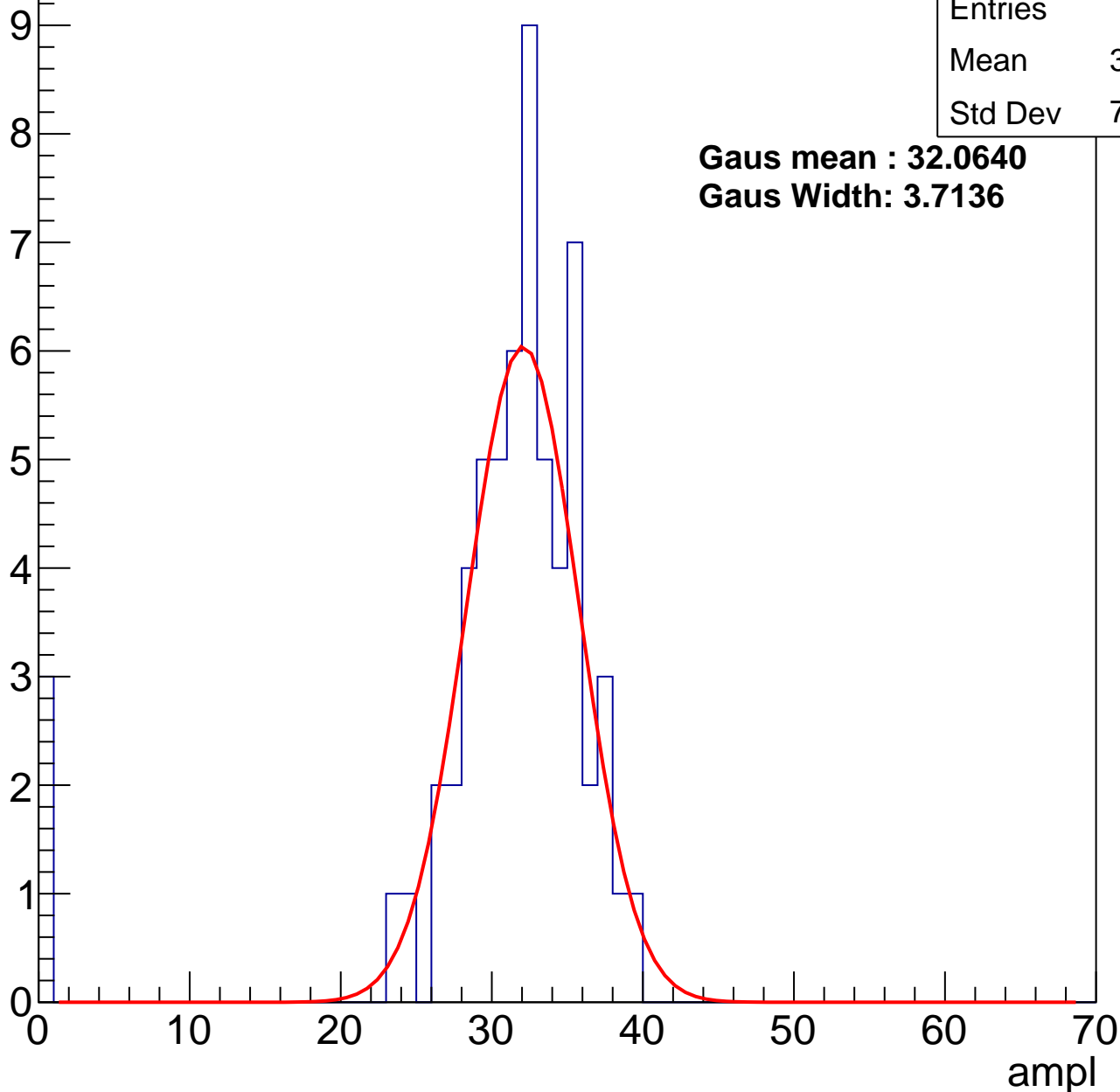
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	30.16
Std Dev	7.623

Gaus mean : 32.0640

Gaus Width: 3.7136



B1L103S, U13-ch20, adc1

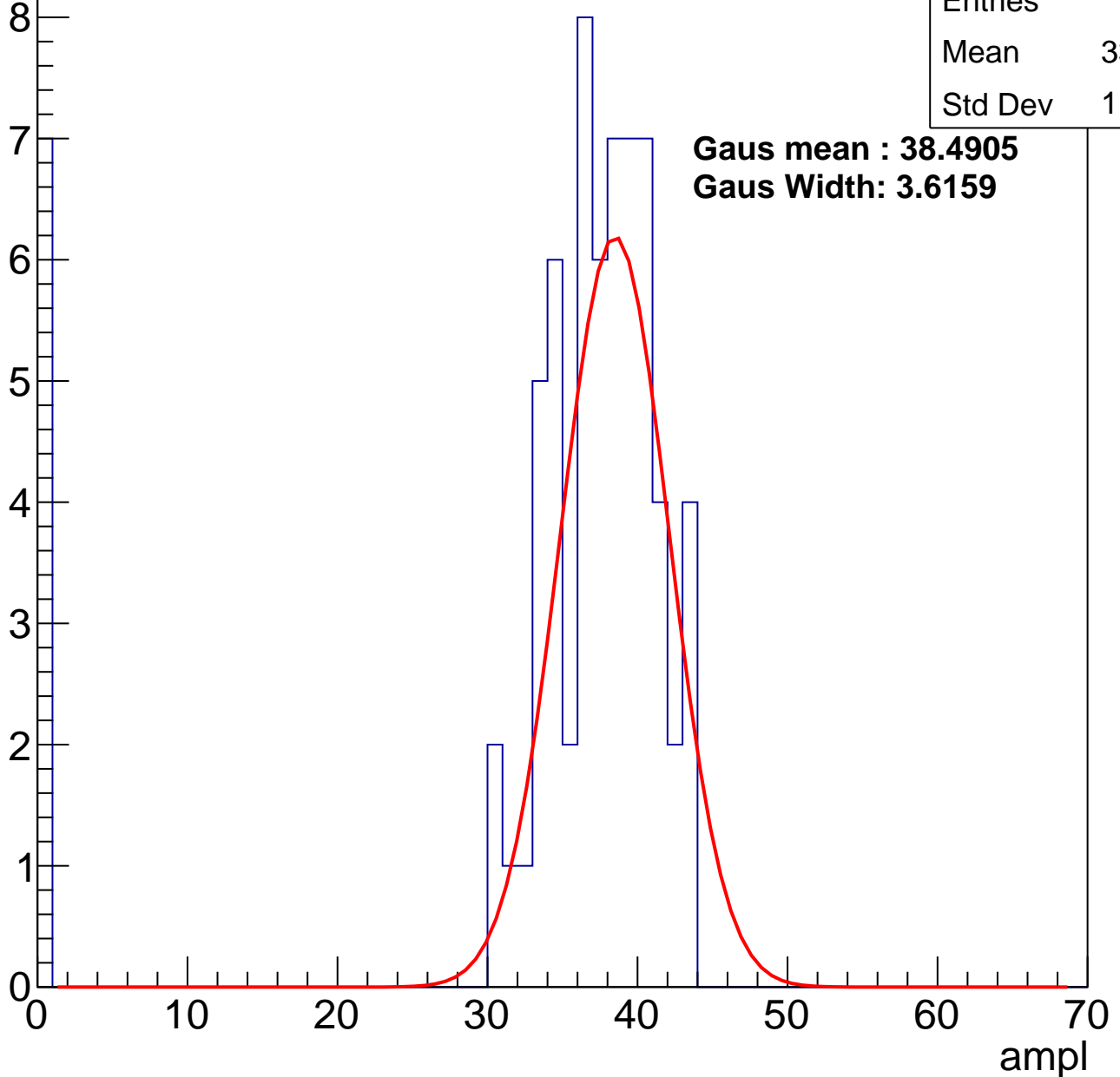
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	33.49
Std Dev	11.67

Gaus mean : 38.4905

Gaus Width: 3.6159



B1L103S, U13-ch20, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	41.46
Std Dev	11.34

Gaus mean : 44.9656

Gaus Width: 3.6594

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

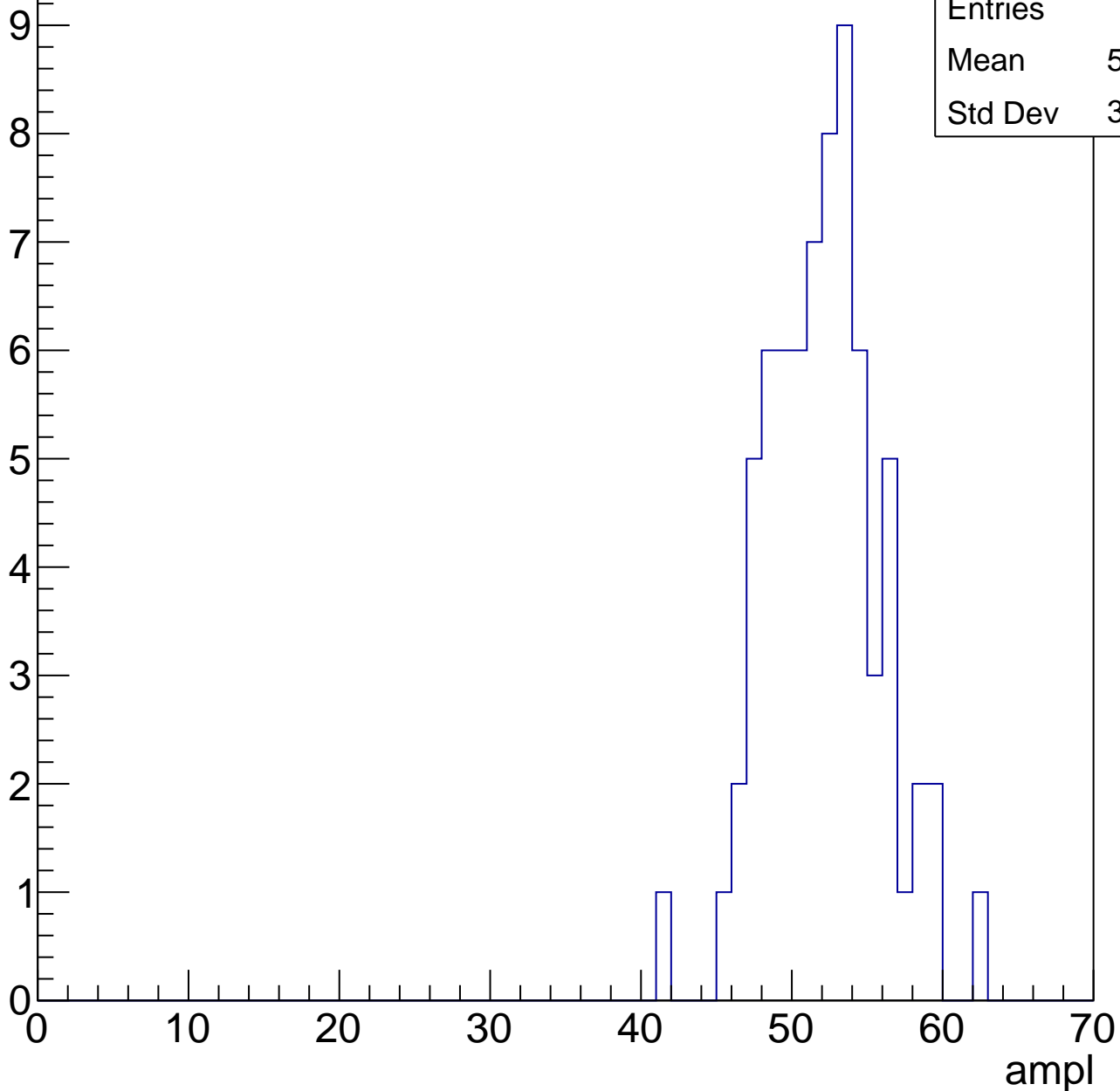
70

B1L103S, U13-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	51.65
Std Dev	3.704

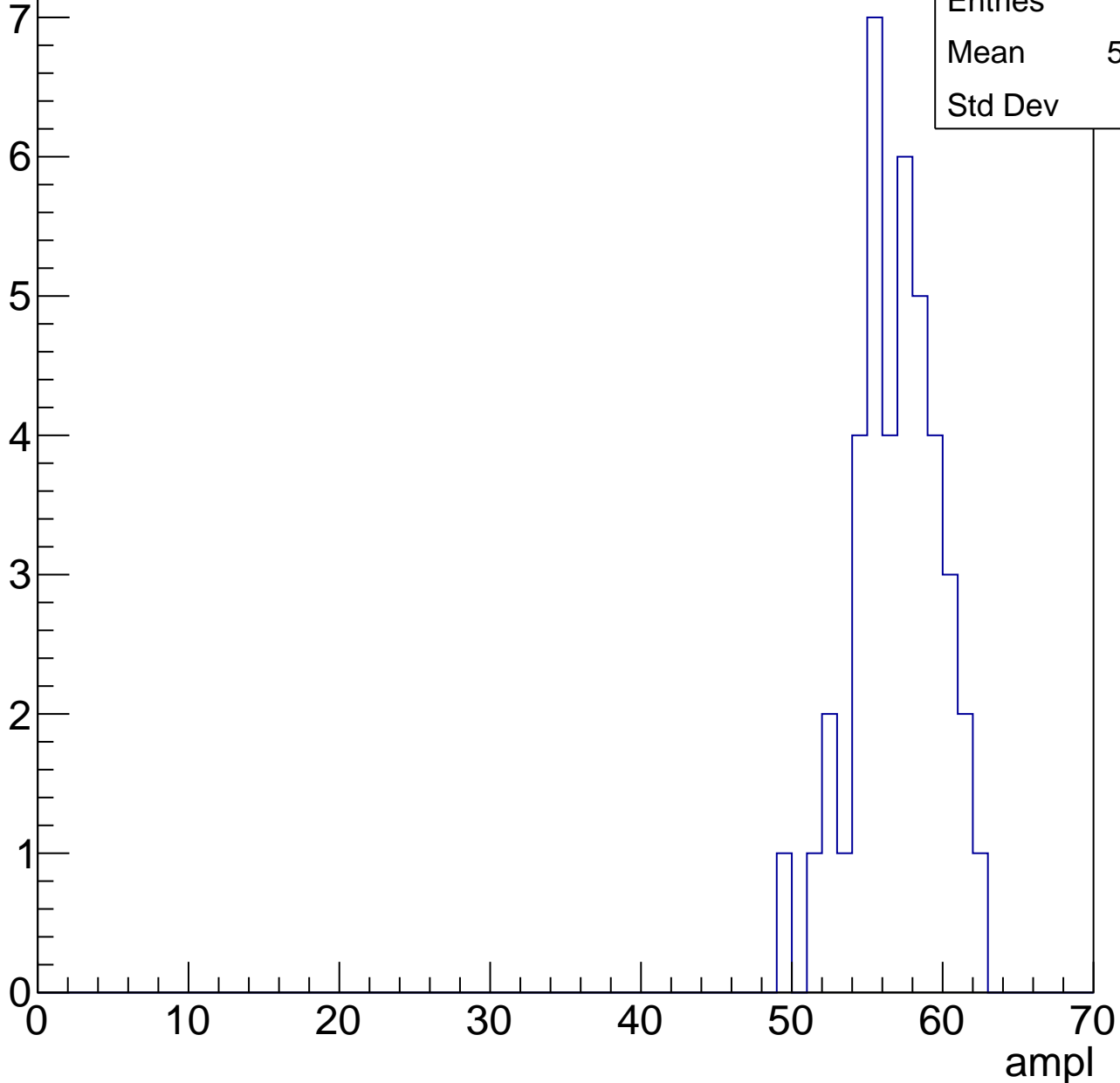


B1L103S, U13-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	56.44
Std Dev	2.82

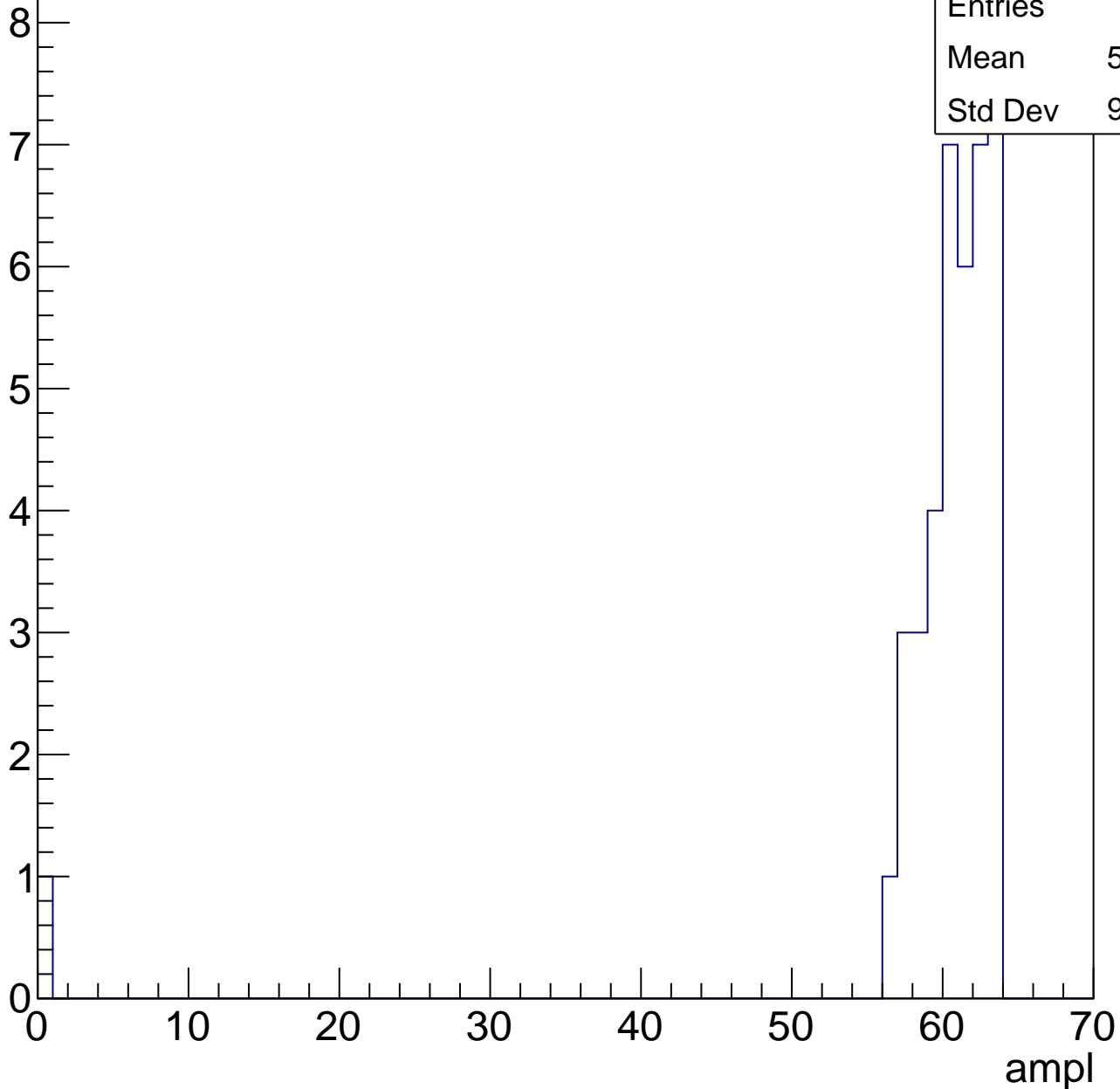


B1L103S, U13-ch20, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	59.02
Std Dev	9.653



B1L103S, U13-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

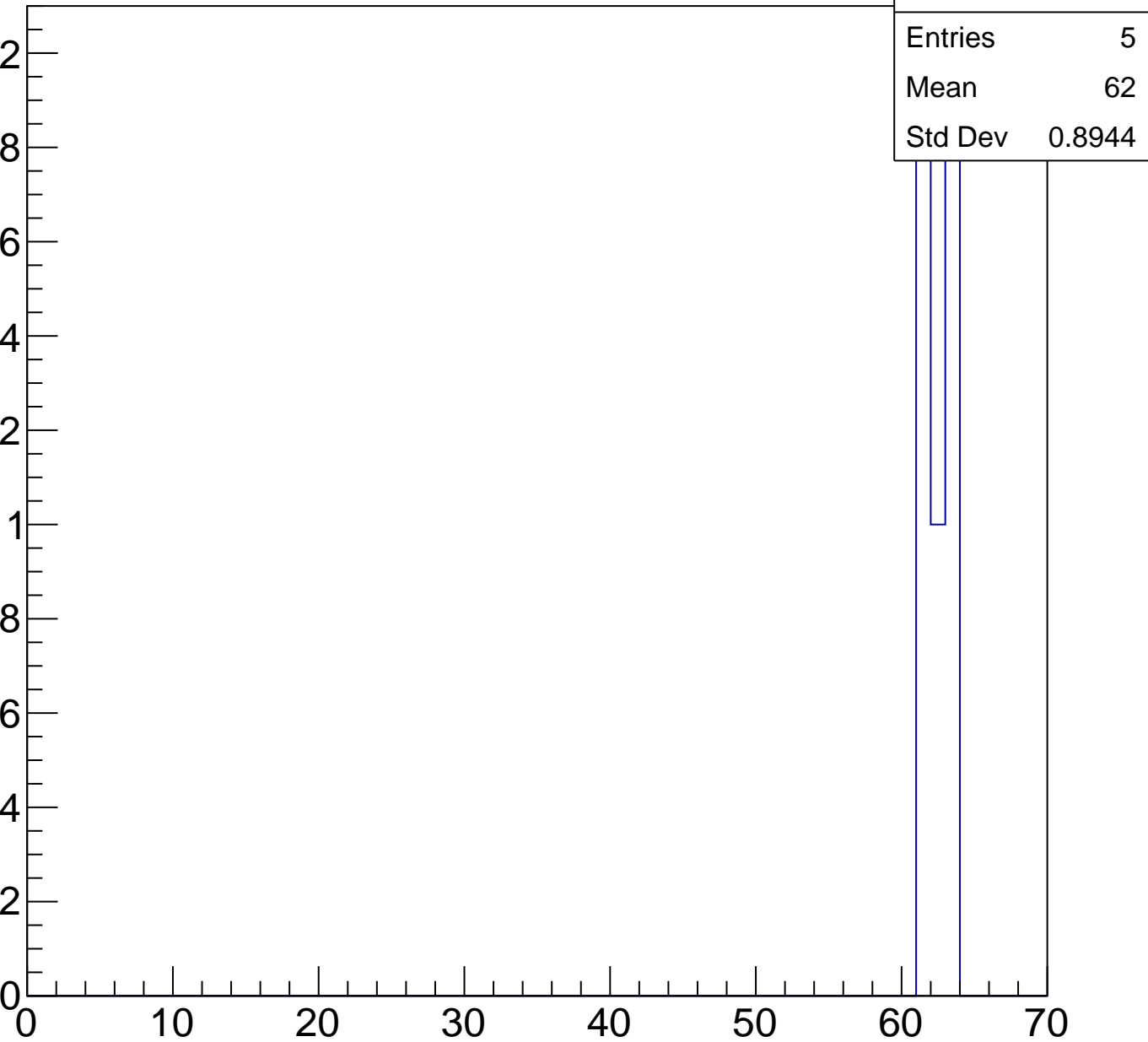
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch21, adc0

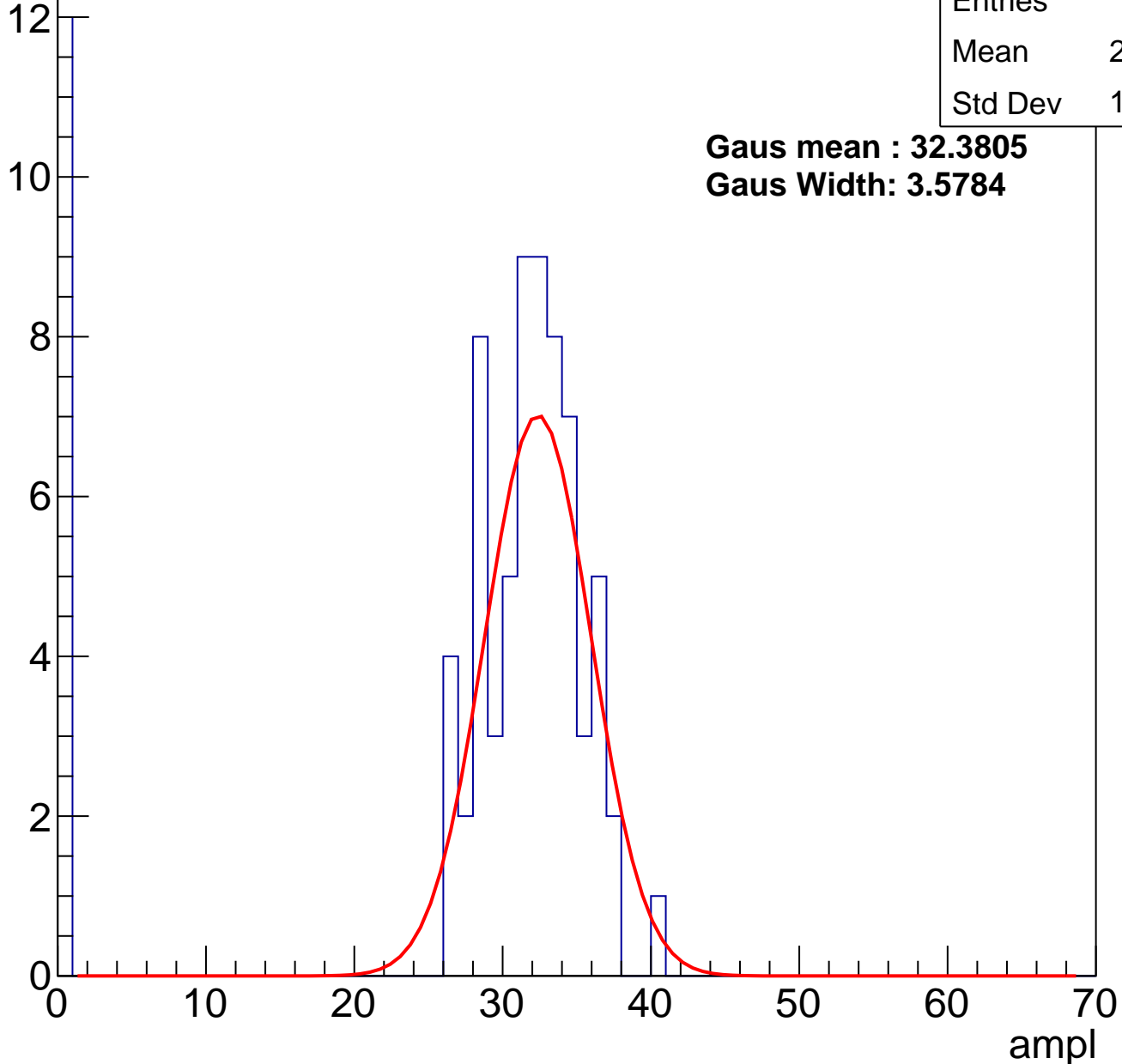
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	26.76
Std Dev	11.76

Gaus mean : 32.3805

Gaus Width: 3.5784

Entry



B1L103S, U13-ch21, adc1

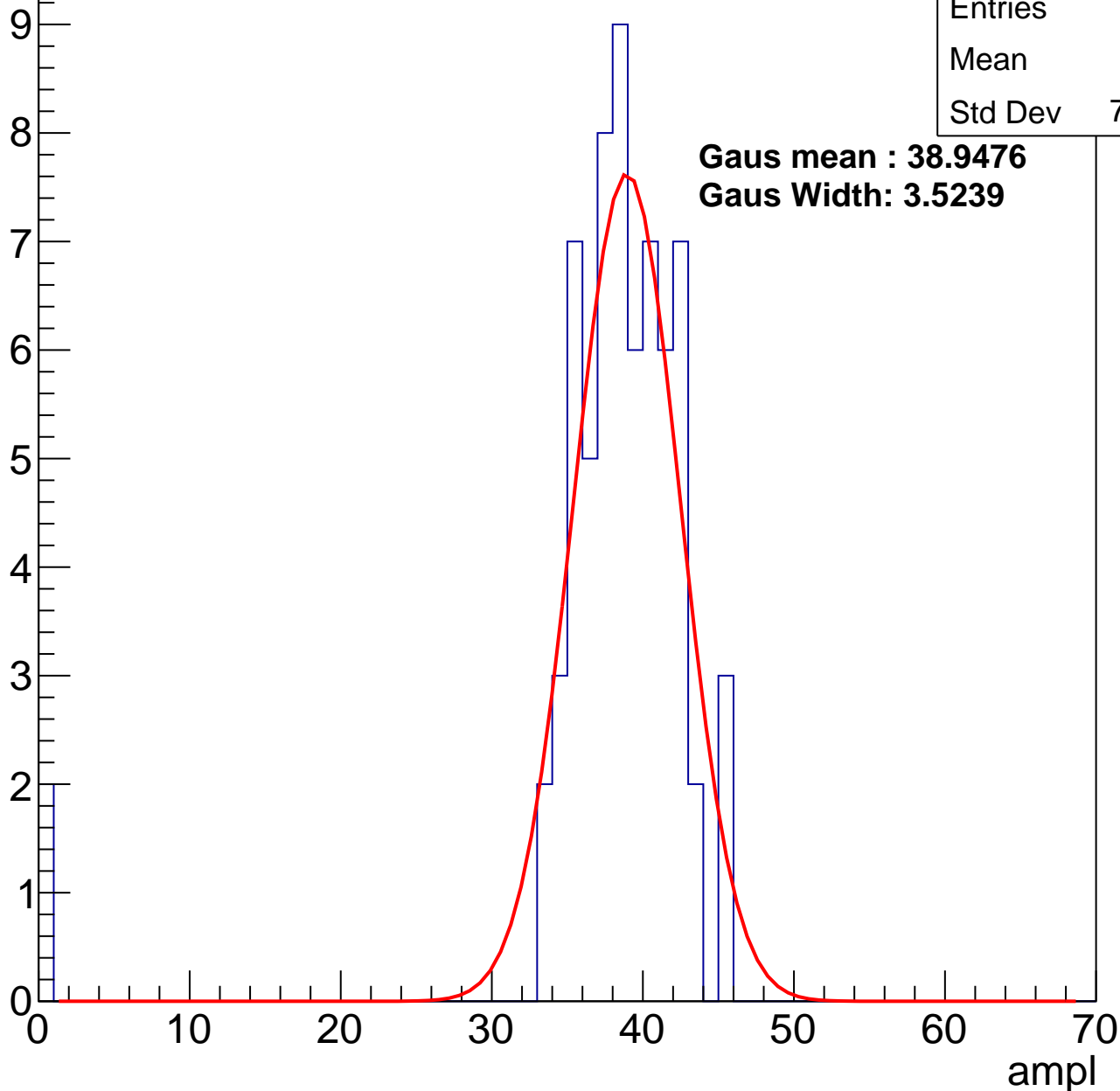
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.4
Std Dev	7.176

Gaus mean : 38.9476

Gaus Width: 3.5239



B1L103S, U13-ch21, adc2

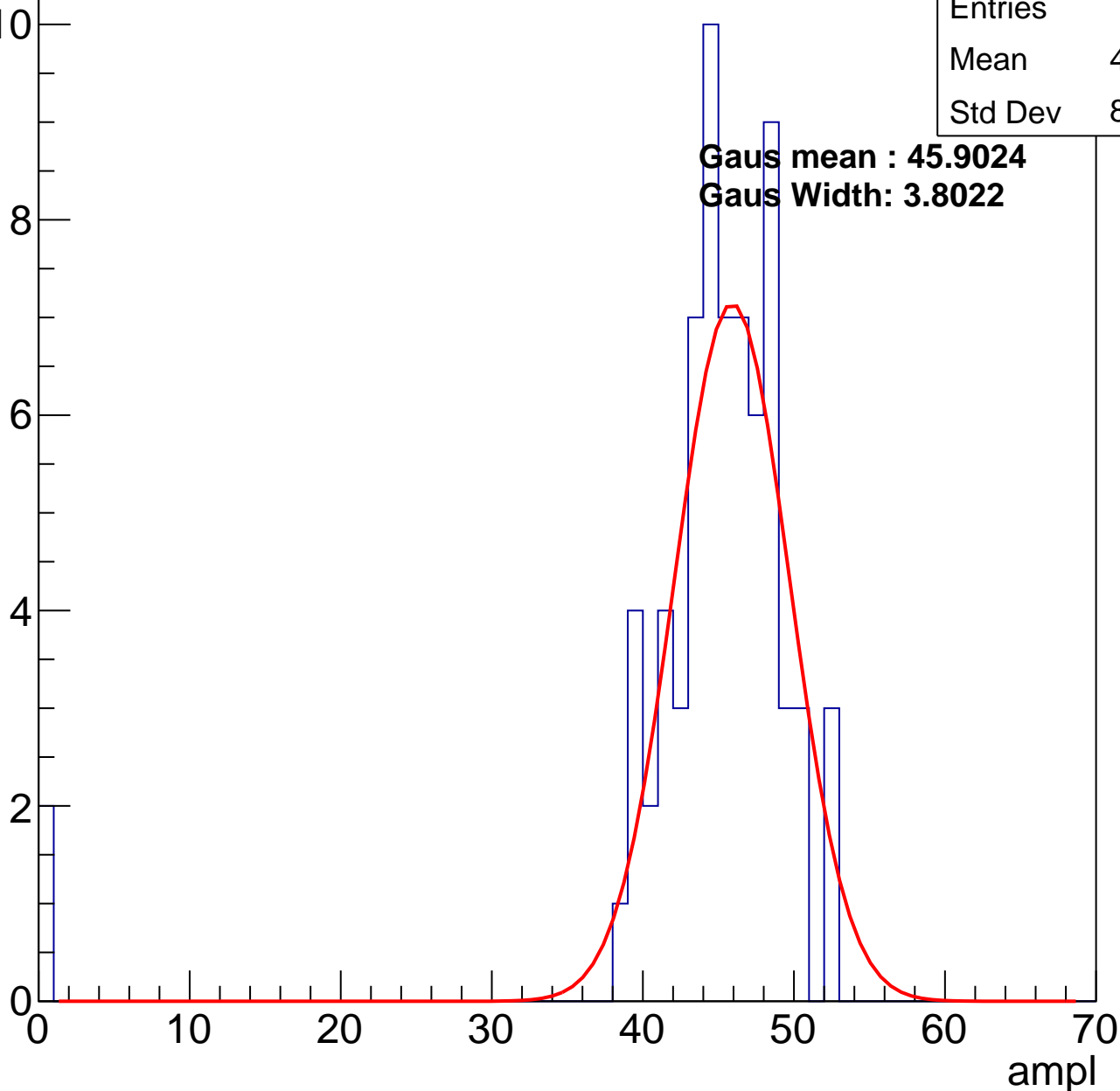
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	43.79
Std Dev	8.136

Gaus mean : 45.9024

Gaus Width: 3.8022



B1L103S, U13-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	49.95
Std Dev	9.432

Entry

10

8

6

4

2

0

ampl

0

10

20

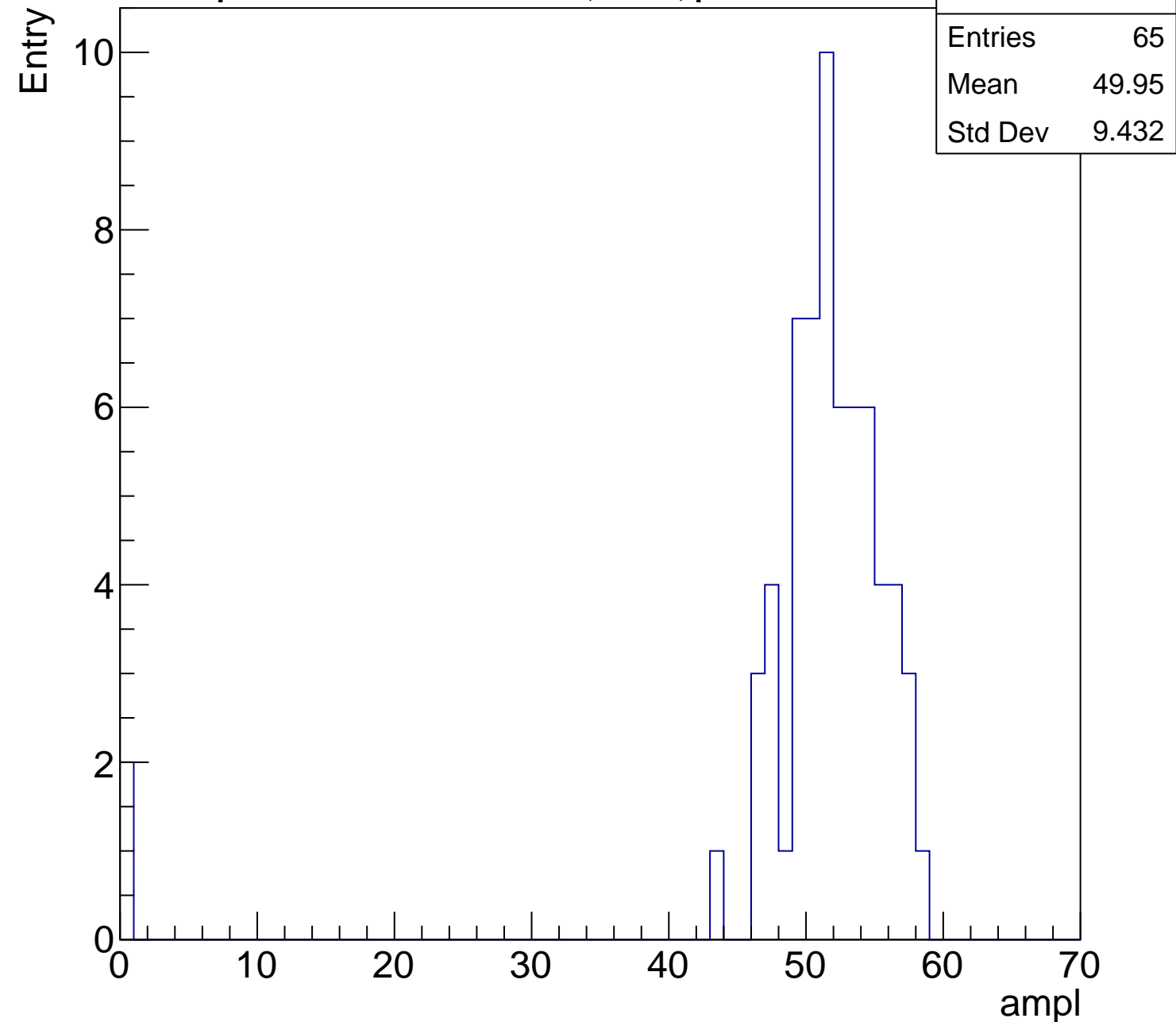
30

40

50

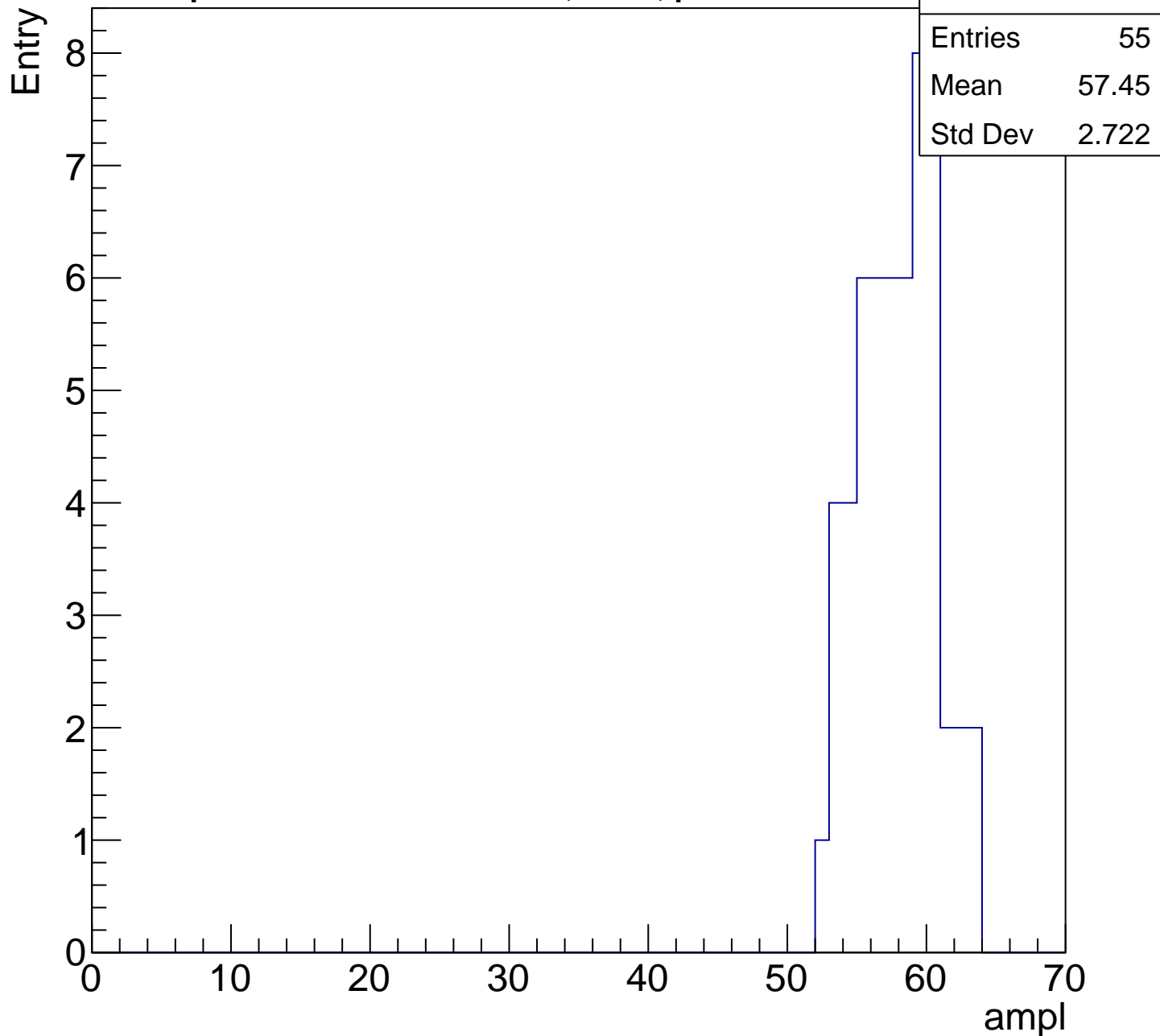
60

70



B1L103S, U13-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

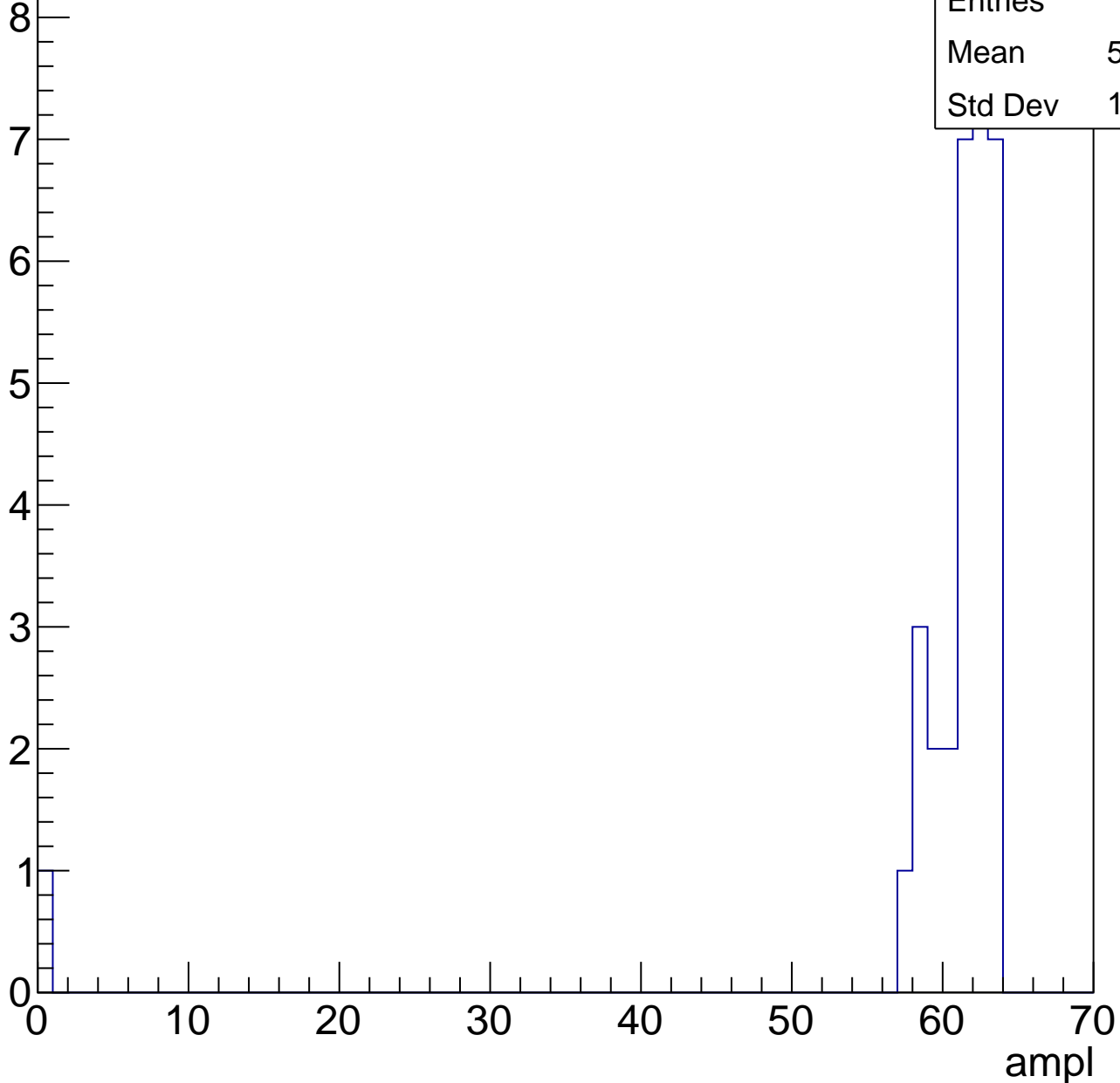


B1L103S, U13-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

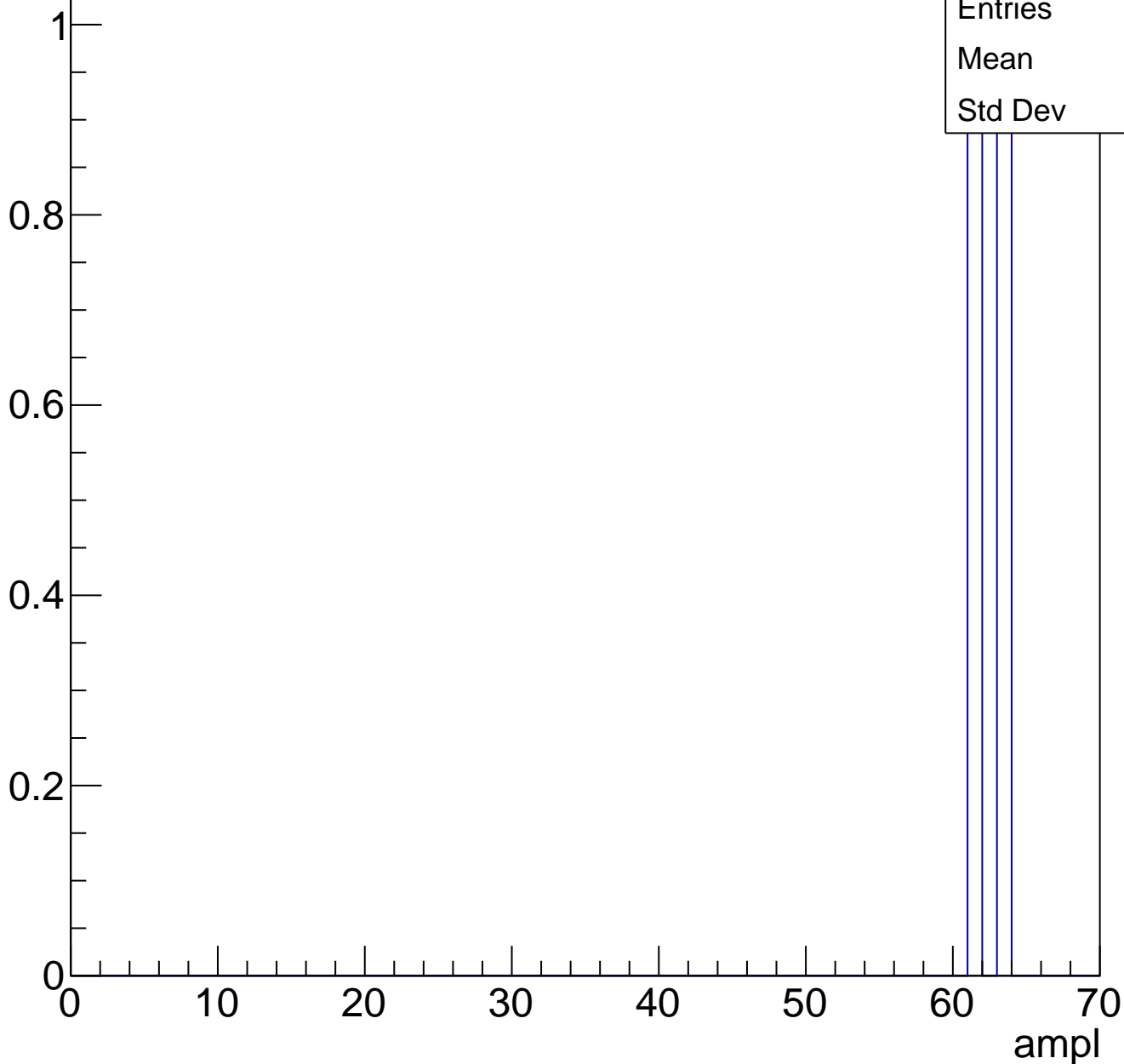
Entries	31
Mean	59.13
Std Dev	10.93



B1L103S, U13-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.389
Std Dev	5.727

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U13-ch22, adc0

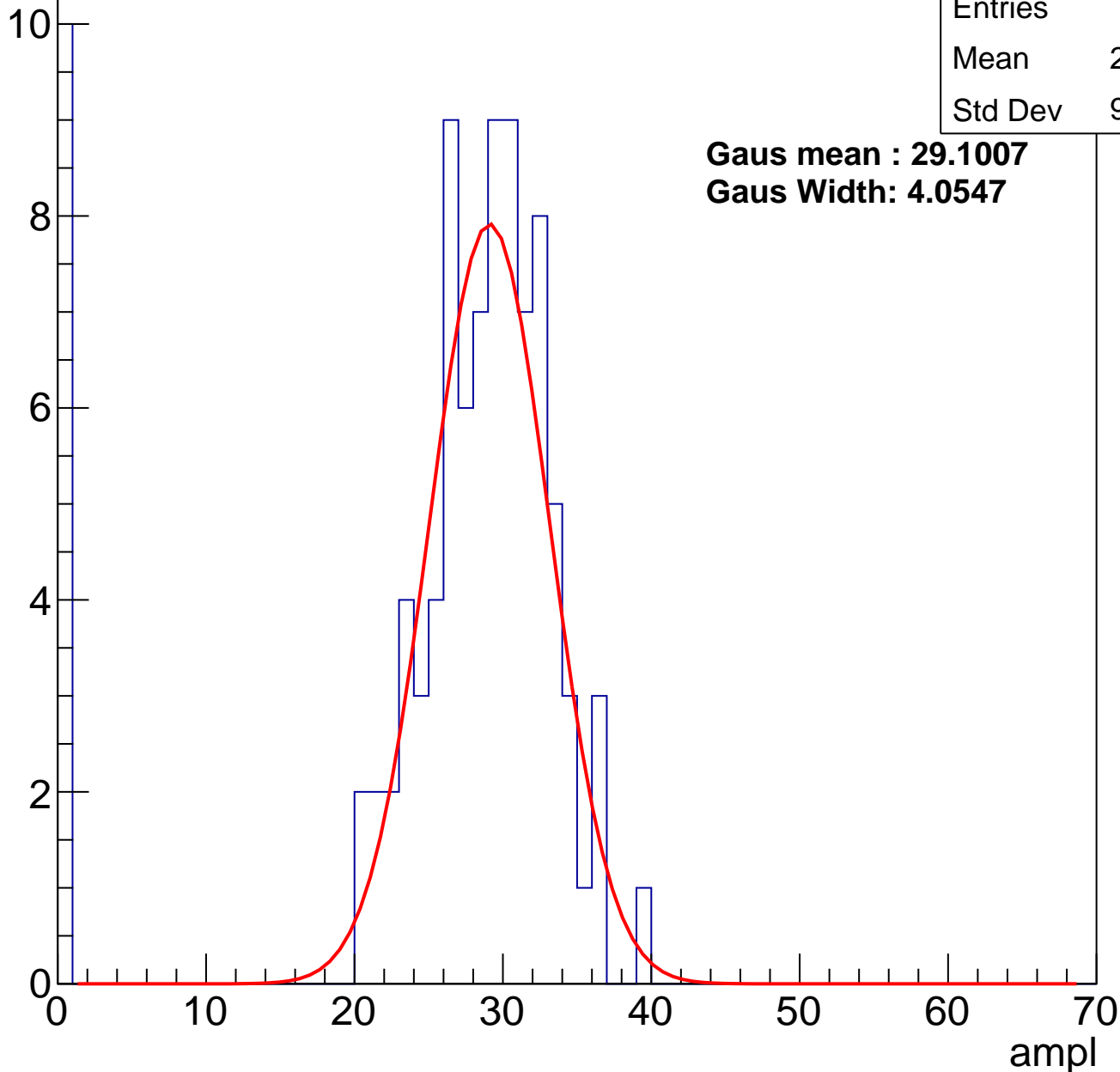
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	25.63
Std Dev	9.549

Gaus mean : 29.1007

Gaus Width: 4.0547

Entry



B1L103S, U13-ch22, adc1

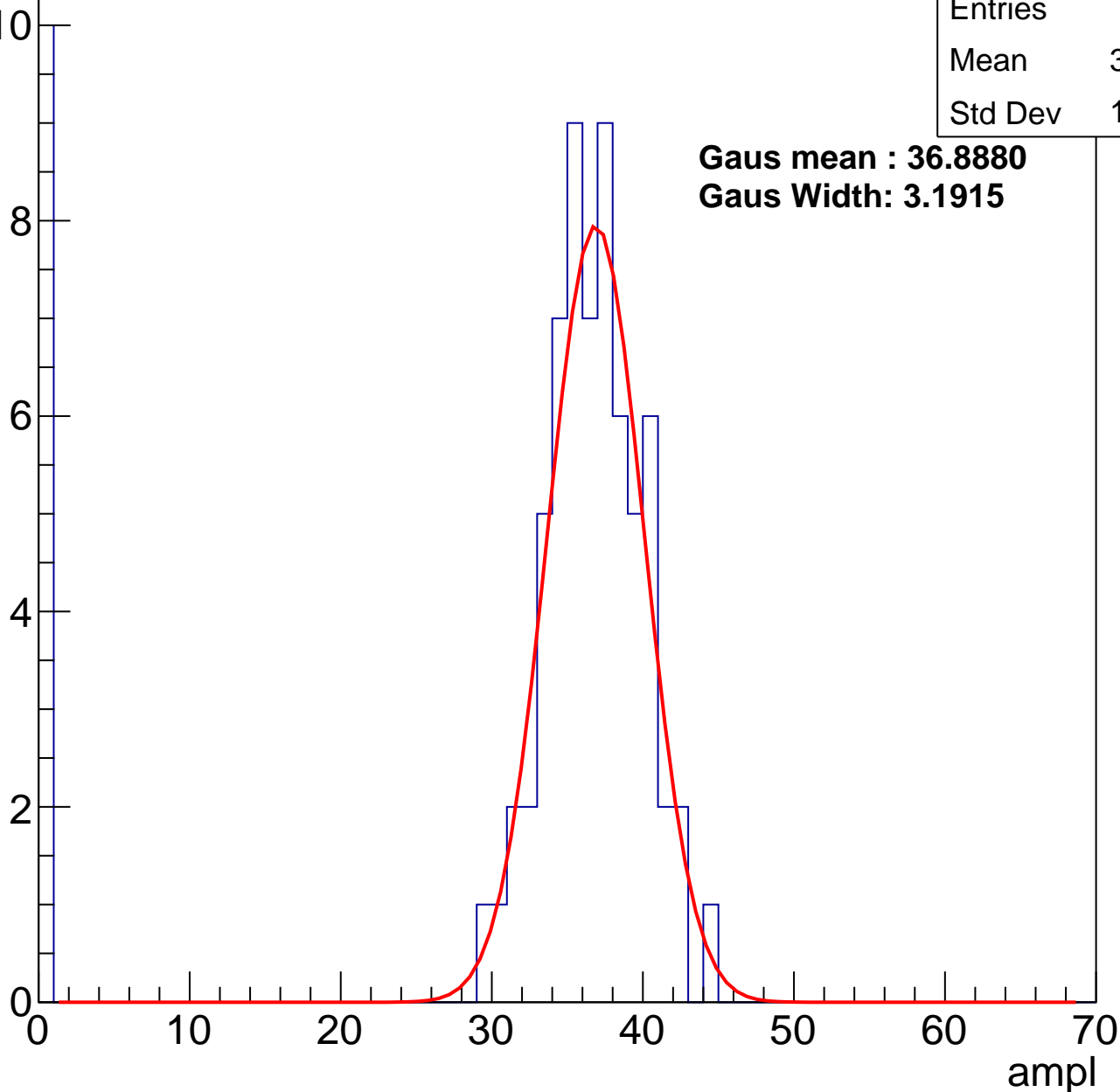
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31.48
Std Dev	12.67

Gaus mean : 36.8880

Gaus Width: 3.1915



B1L103S, U13-ch22, adc2

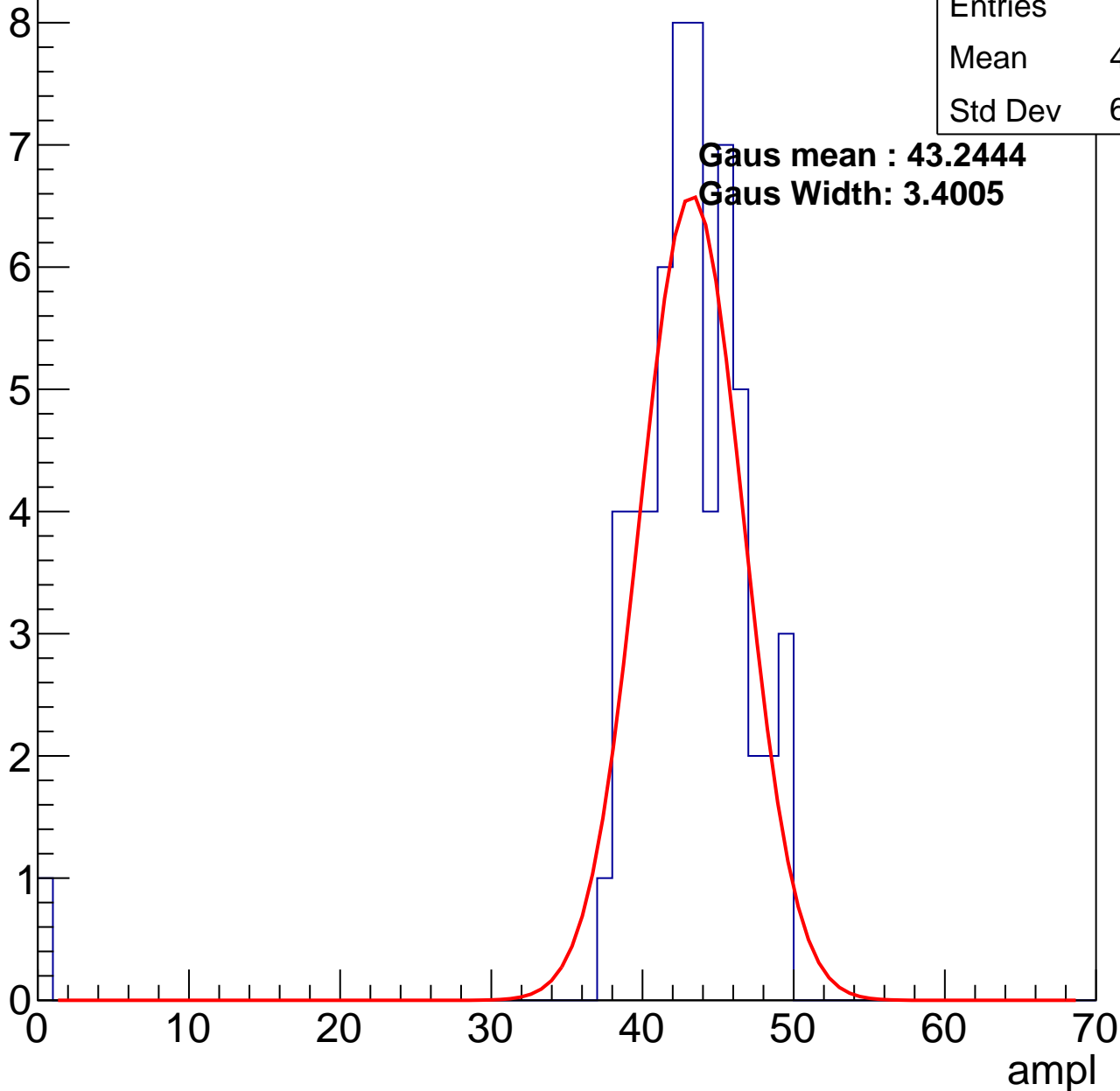
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42.19
Std Dev	6.304

Gaus mean : 43.2444

Gaus Width: 3.4005

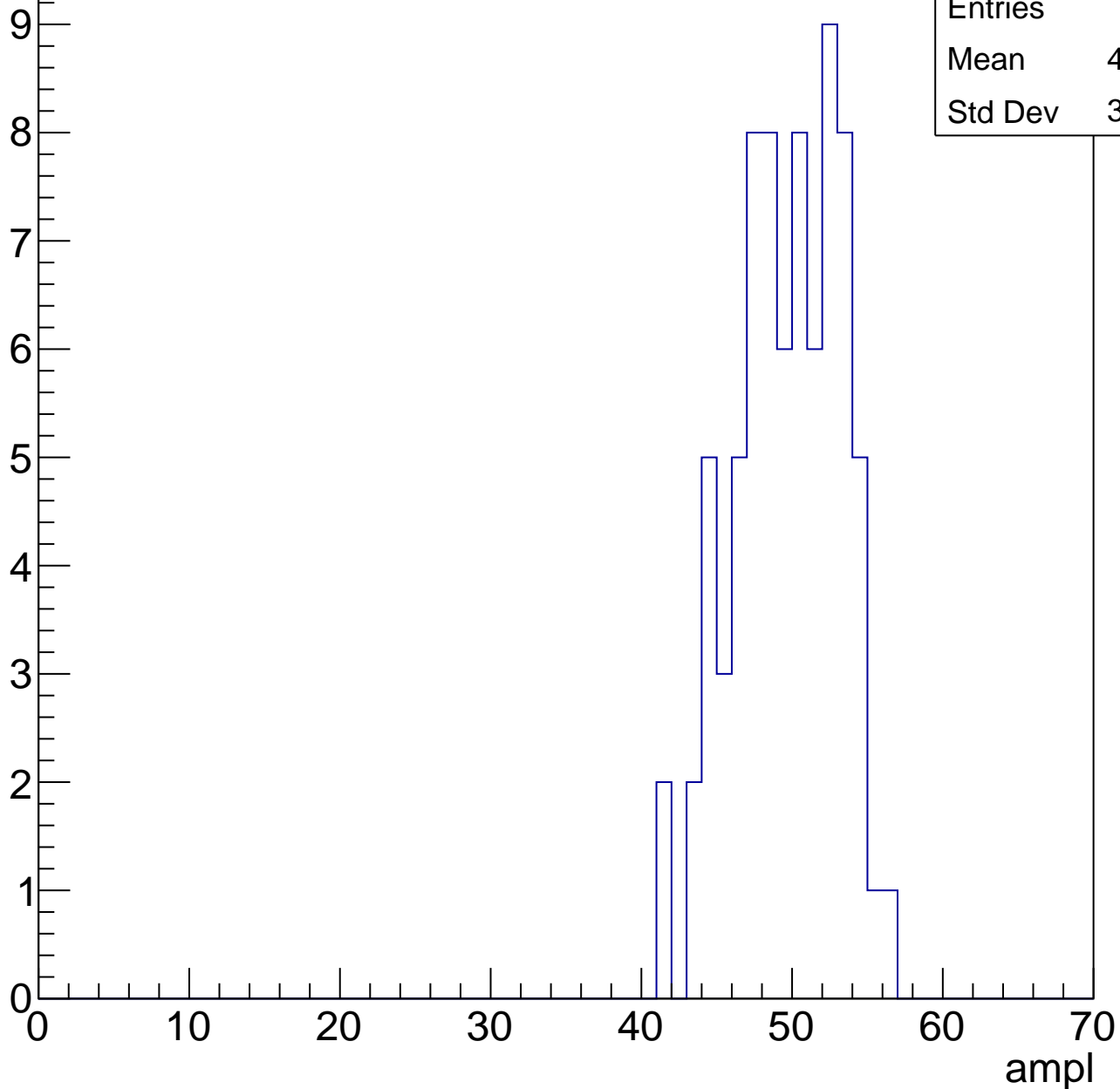


B1L103S, U13-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	49.17
Std Dev	3.432

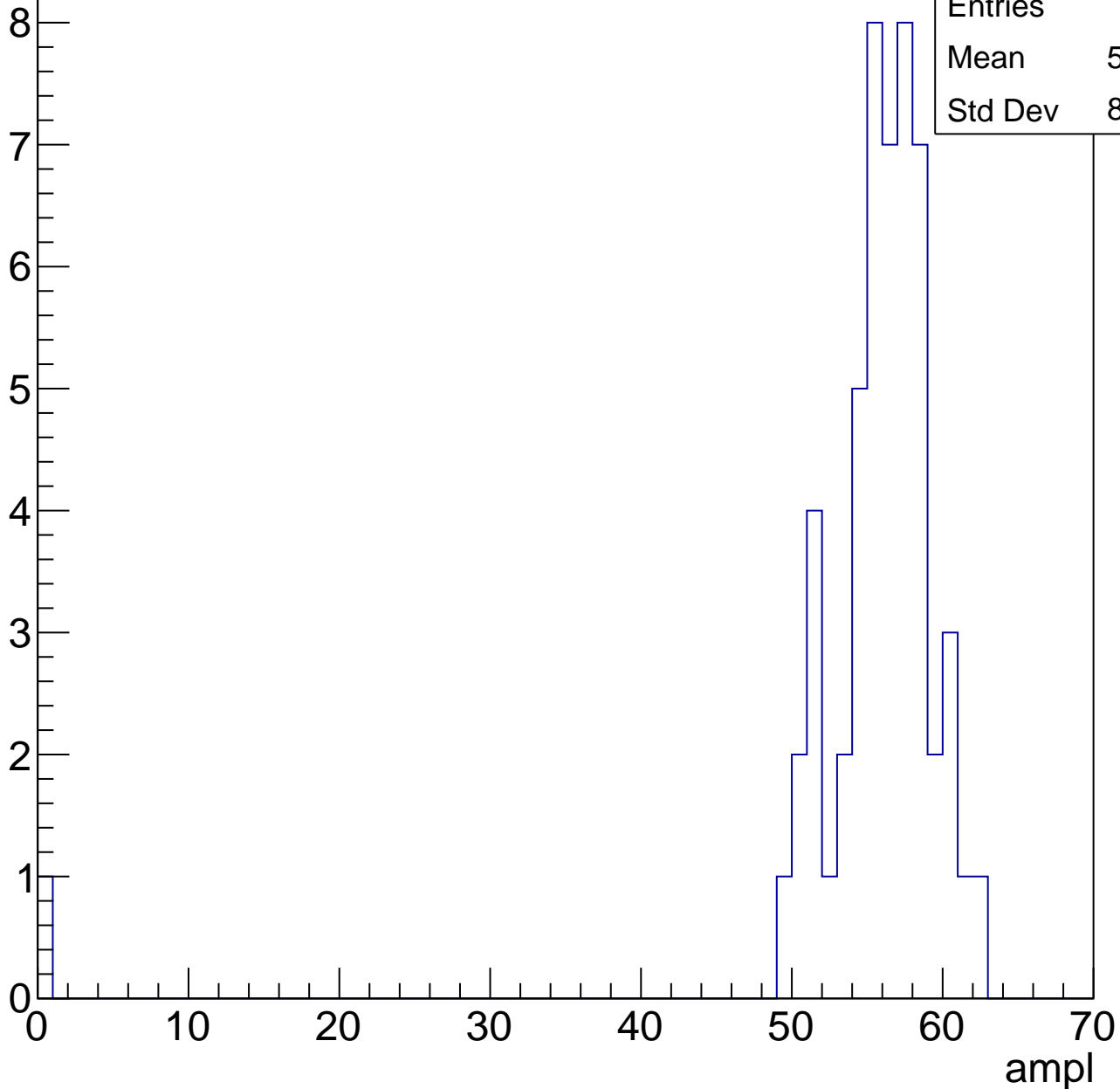


B1L103S, U13-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

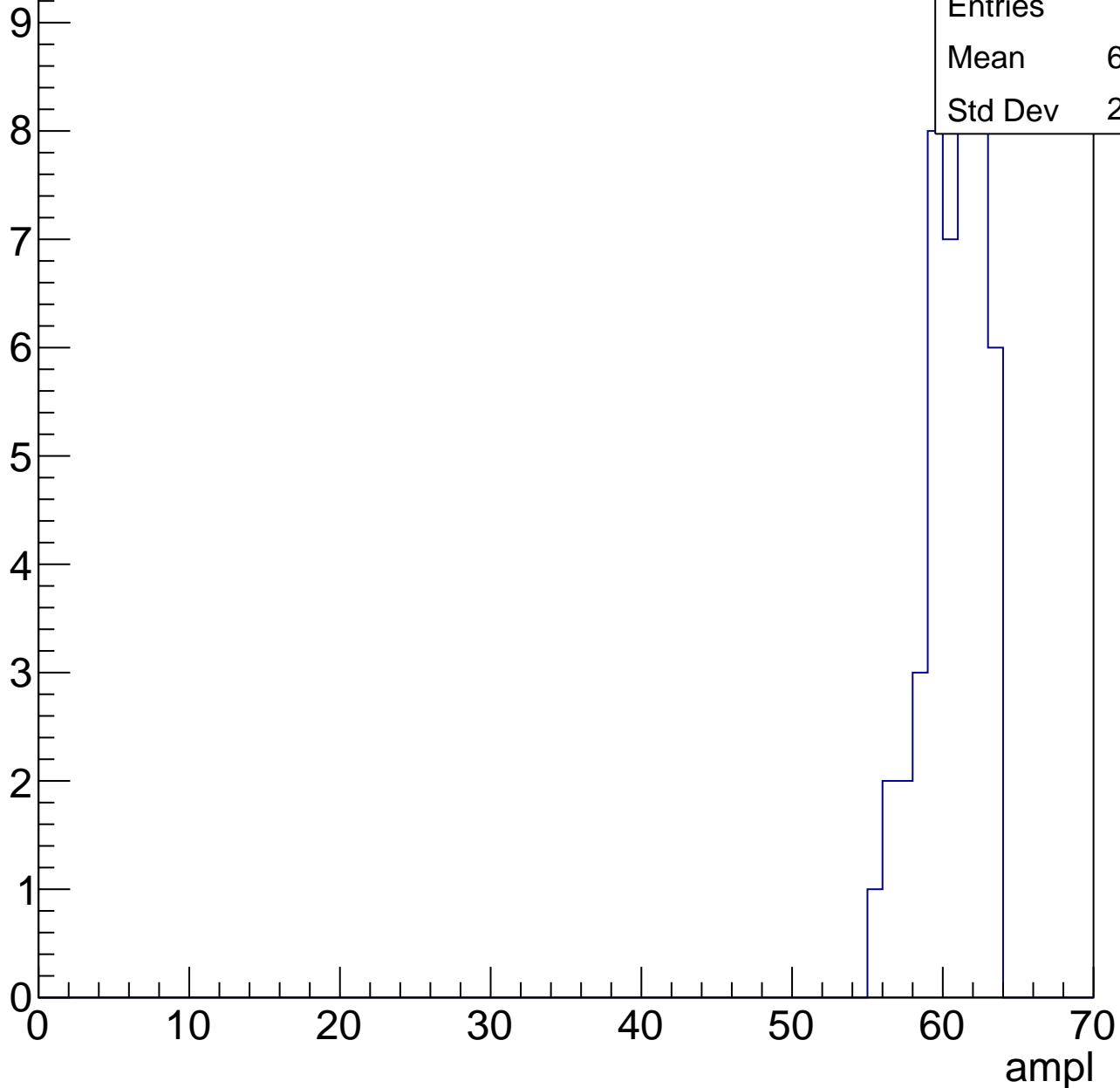
Entries	53
Mean	54.64
Std Dev	8.108



B1L103S, U13-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	47
Mean	60.26
Std Dev	2.016

B1L103S, U13-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch23, adc0

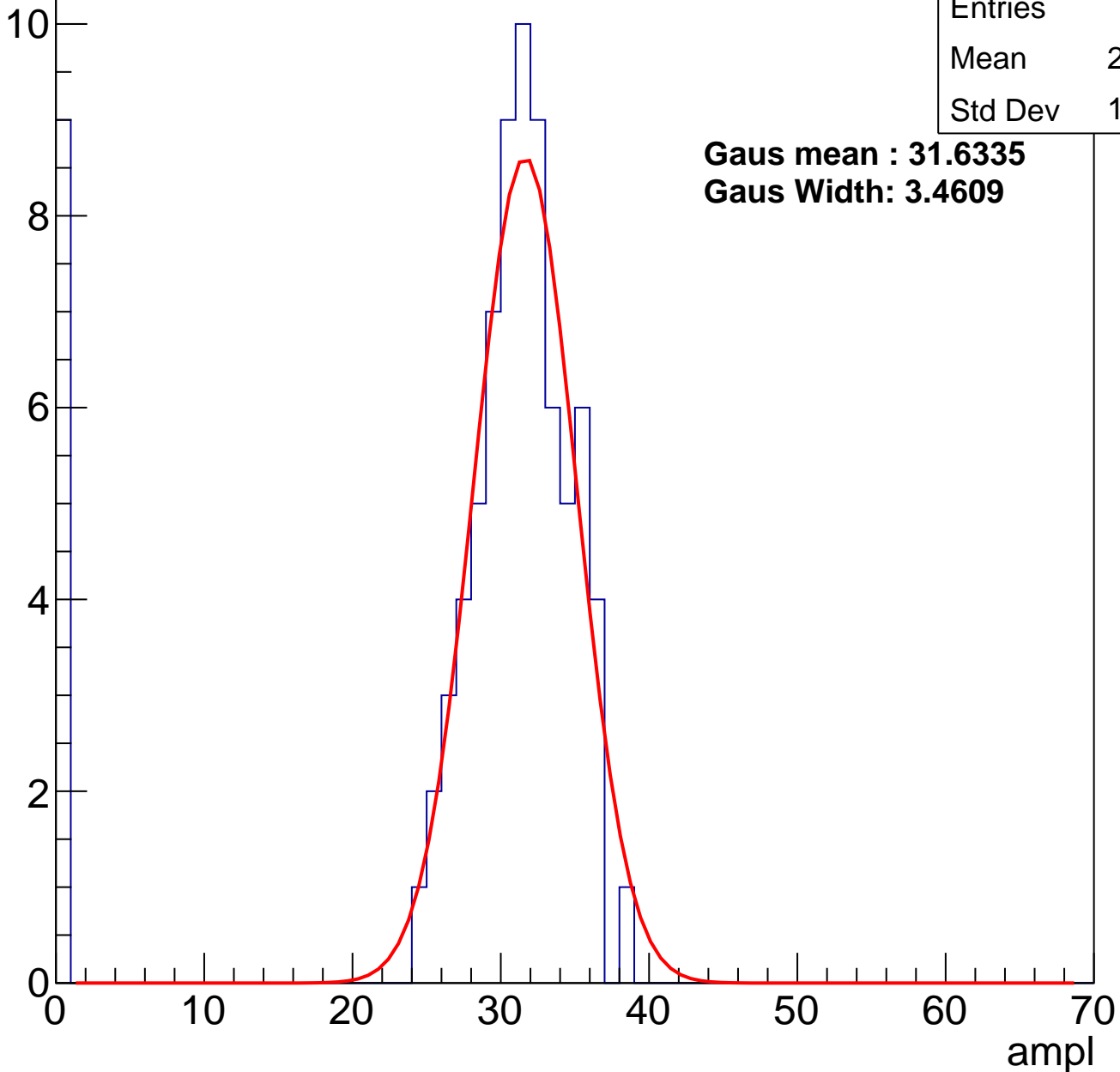
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	27.54
Std Dev	10.15

Gaus mean : 31.6335

Gaus Width: 3.4609

Entry



B1L103S, U13-ch23, adc1

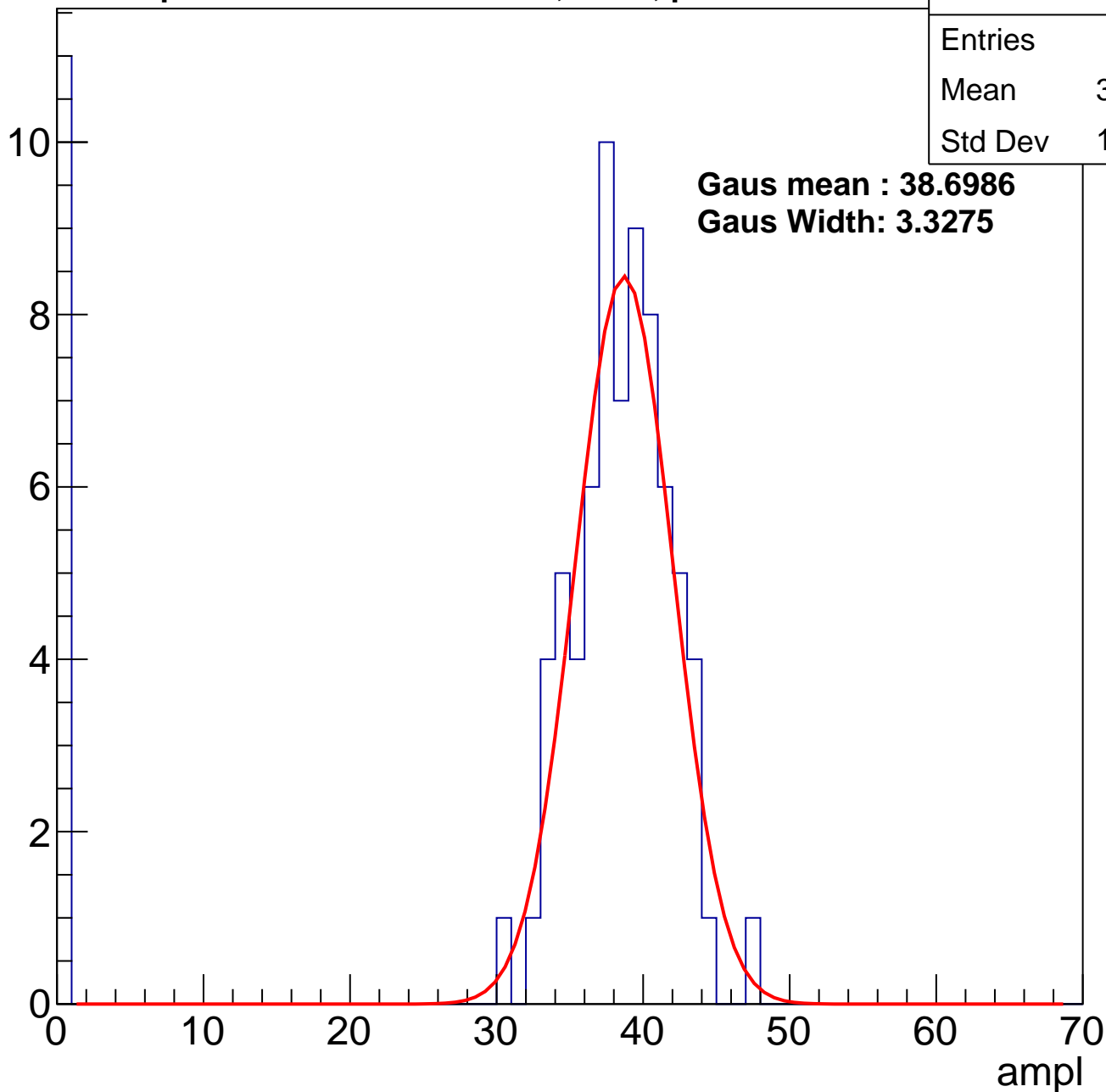
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	33.08
Std Dev	13.27

Gaus mean : 38.6986

Gaus Width: 3.3275

Entry



B1L103S, U13-ch23, adc2

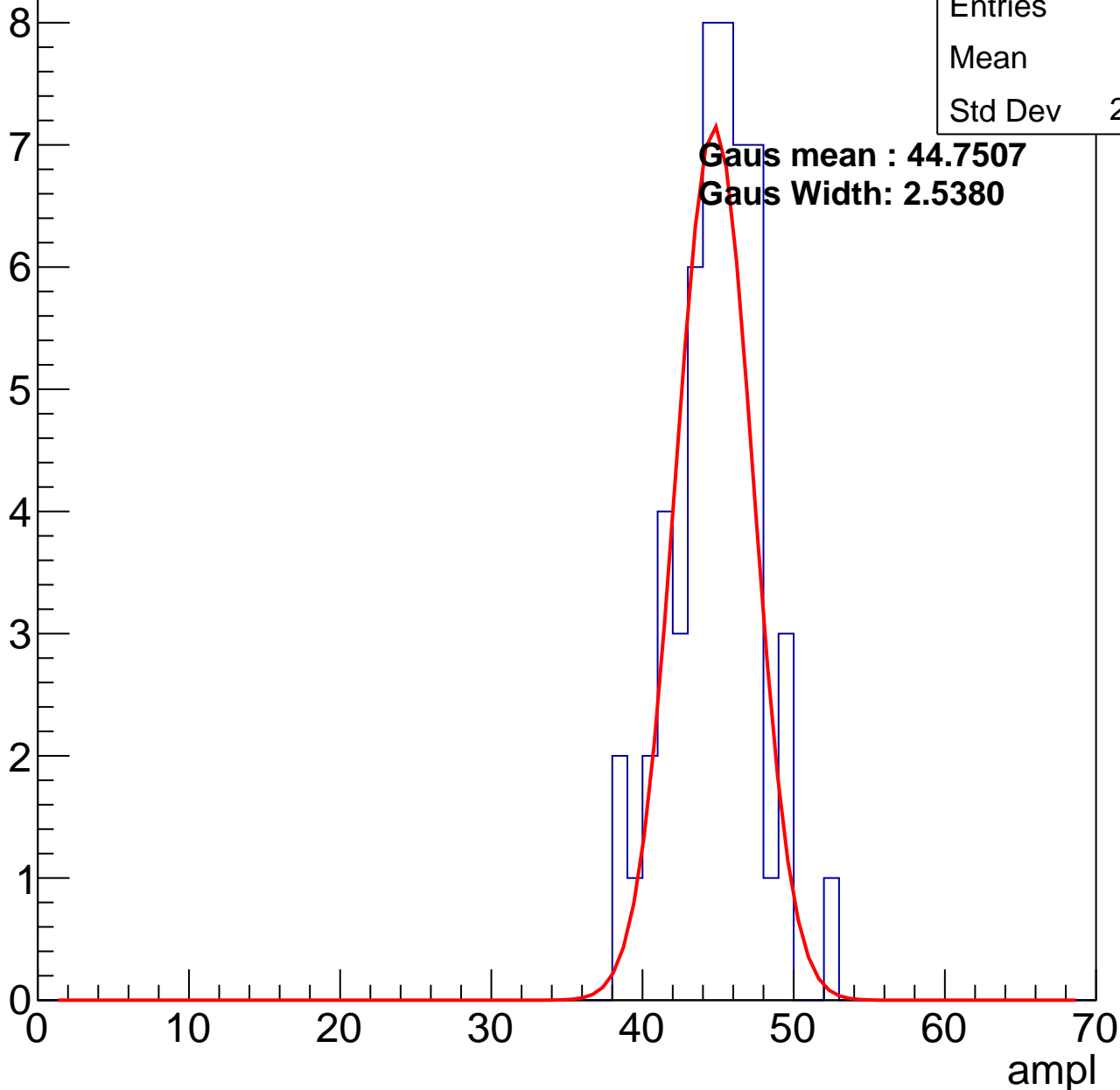
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	44.4
Std Dev	2.844

Gaus mean : 44.7507

Gaus Width: 2.5380

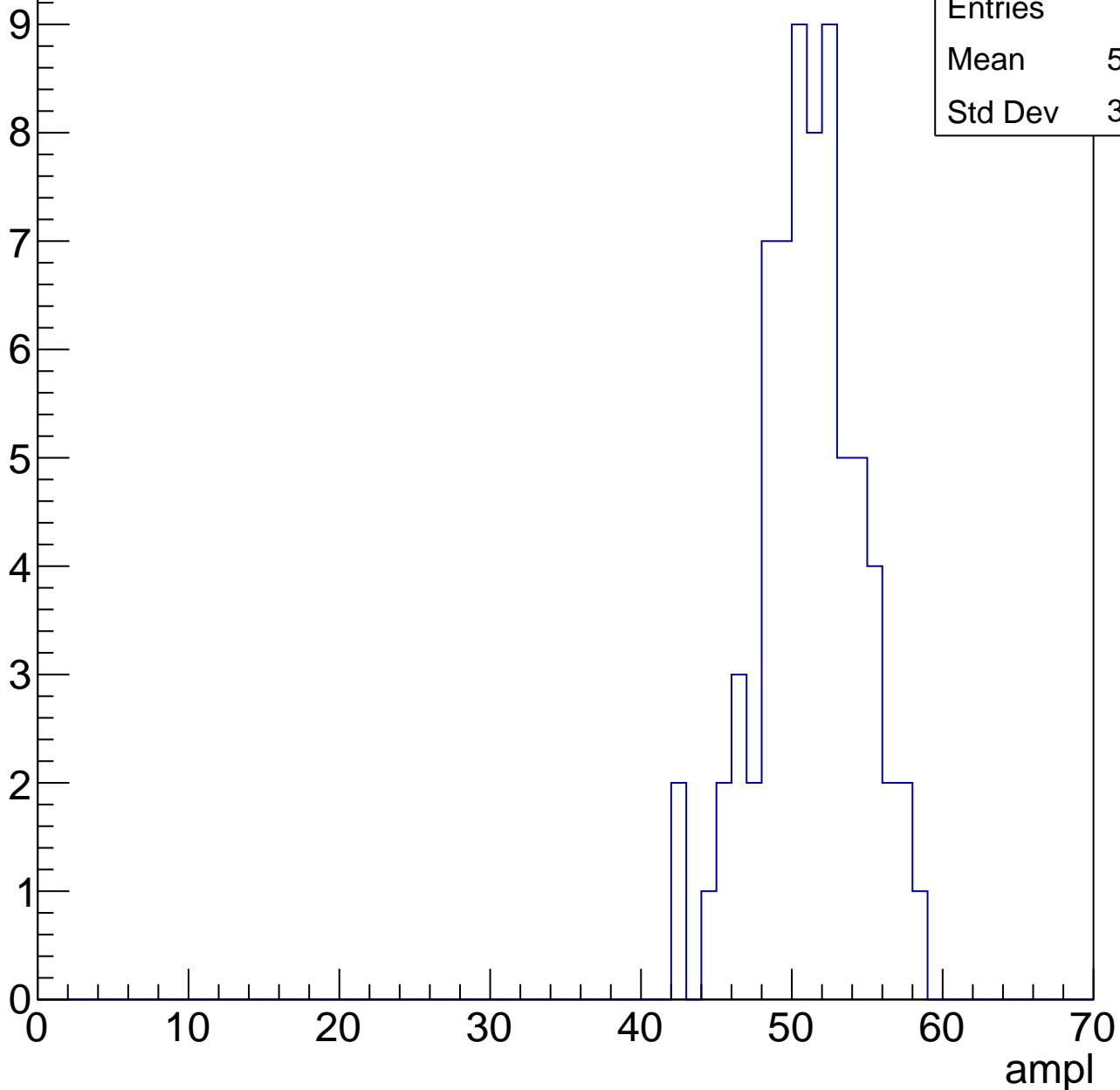


B1L103S, U13-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	50.64
Std Dev	3.384

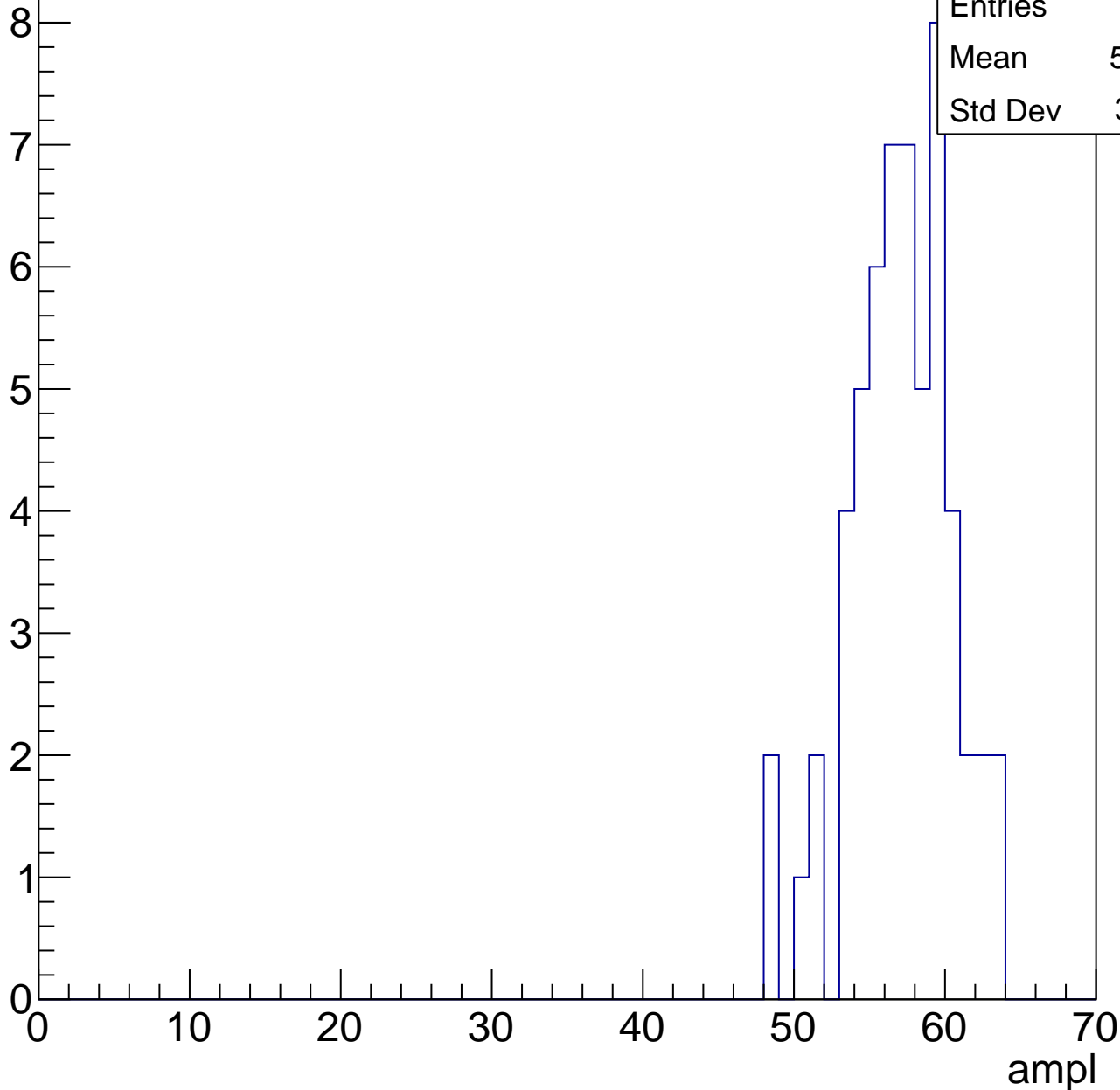


B1L103S, U13-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	56.58
Std Dev	3.351

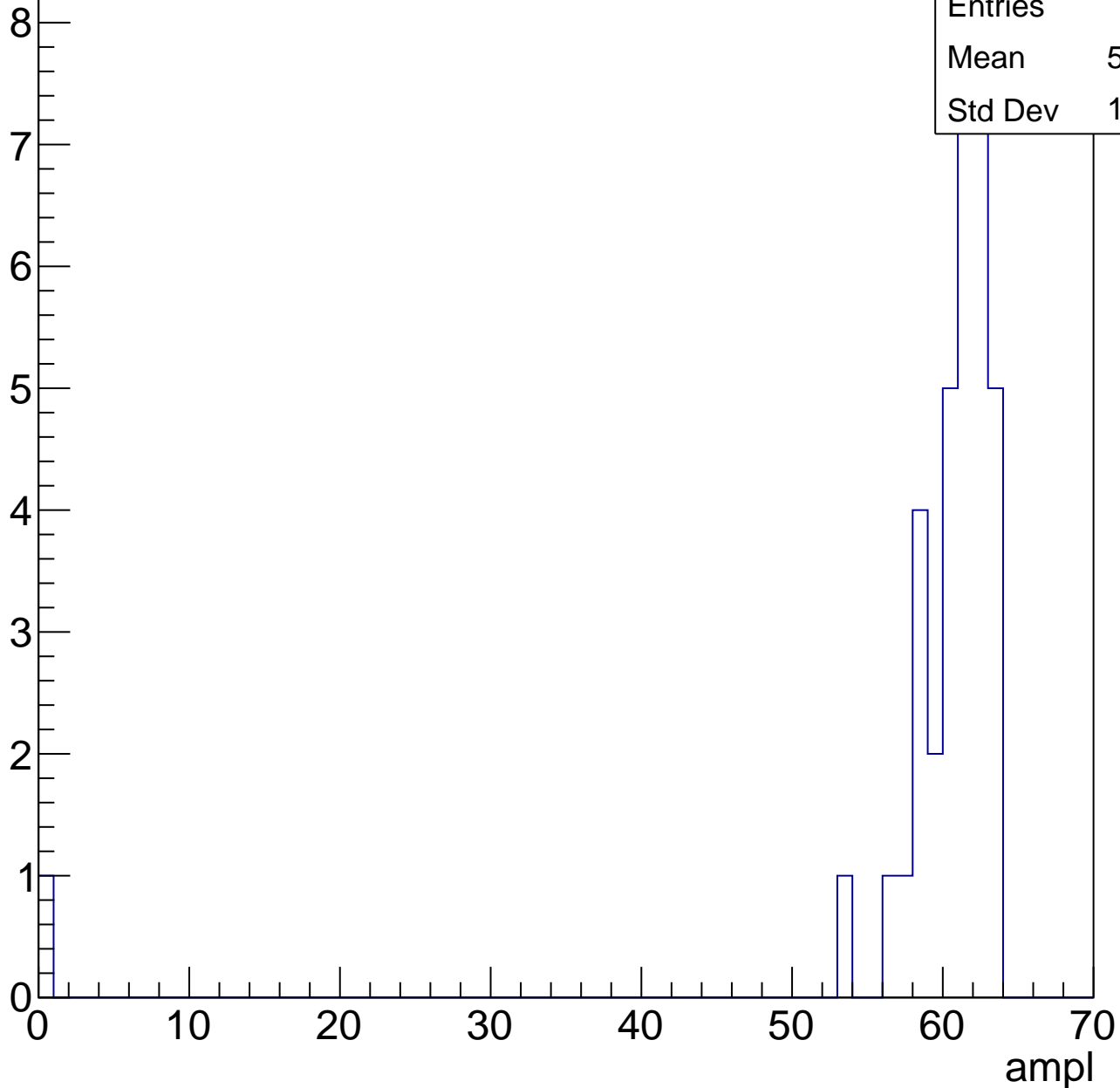


B1L103S, U13-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

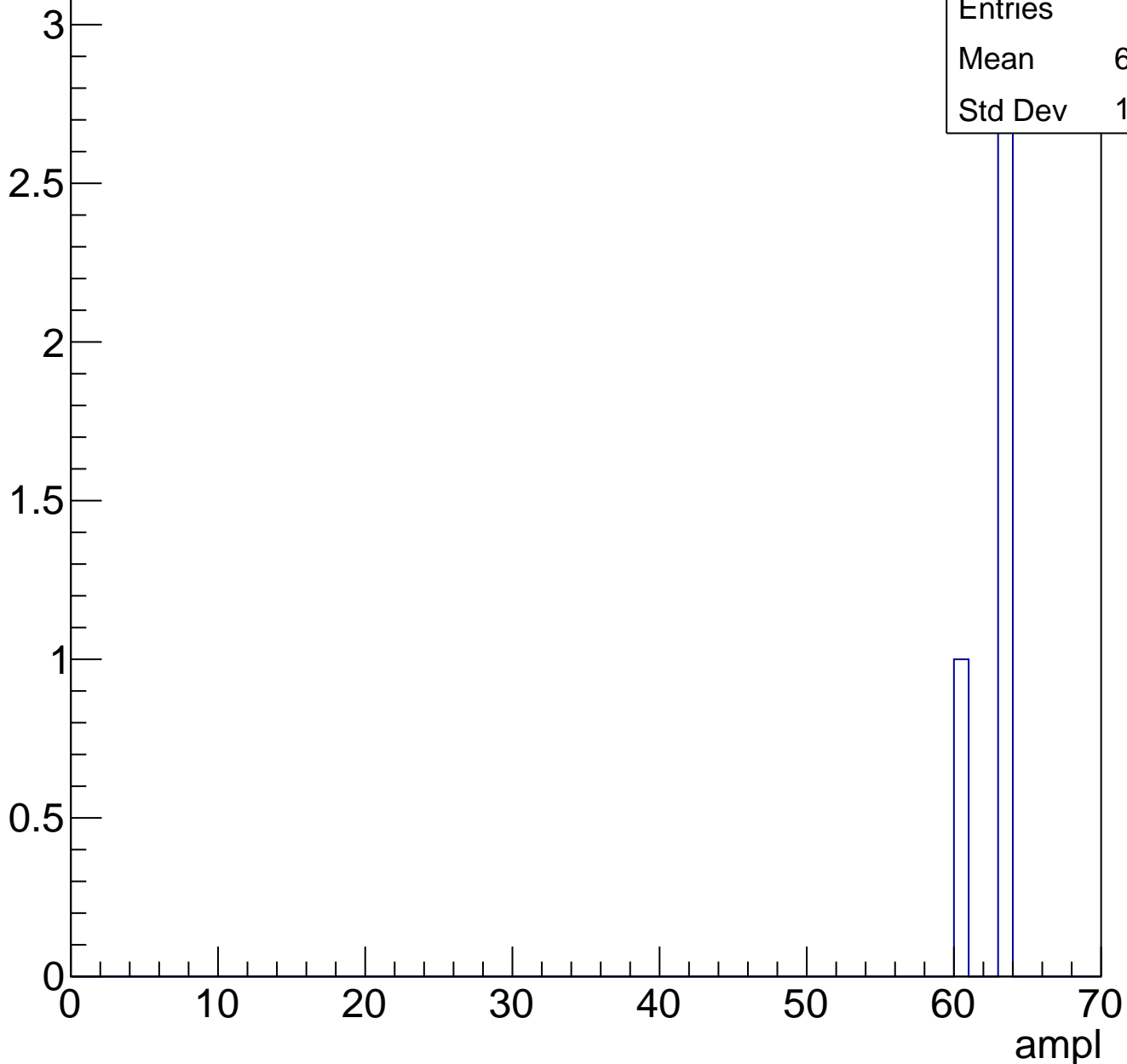
Entries	36
Mean	58.75
Std Dev	10.17



B1L103S, U13-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136

Entry



B1L103S, U13-ch24, adc0

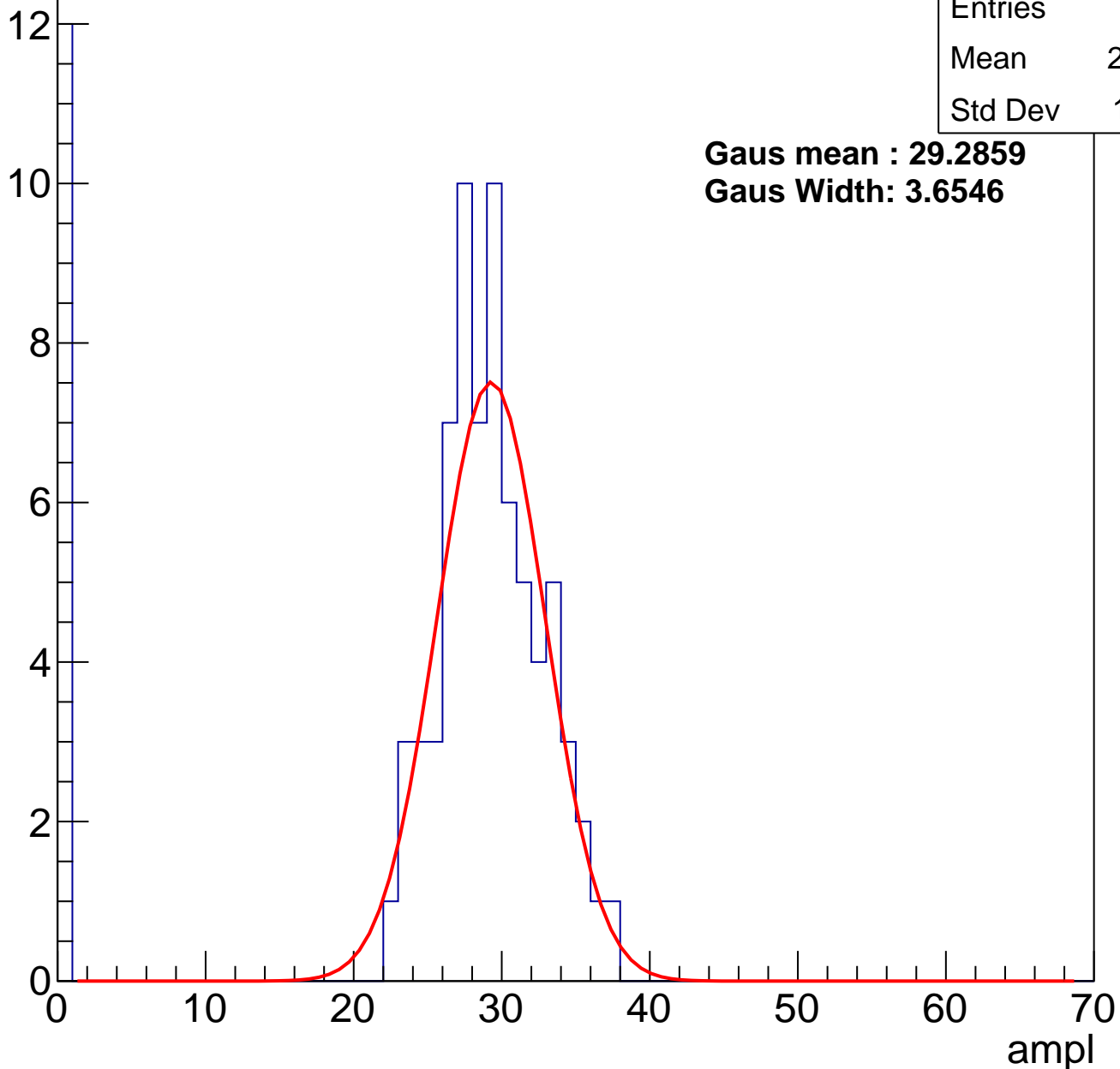
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24.69
Std Dev	10.61

Gaus mean : 29.2859

Gaus Width: 3.6546

Entry



B1L103S, U13-ch24, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	29.94
Std Dev	13.24

Gaus mean : 35.6121

Gaus Width: 4.5632

Entry

12

10

8

6

4

2

0

0

10

20

30

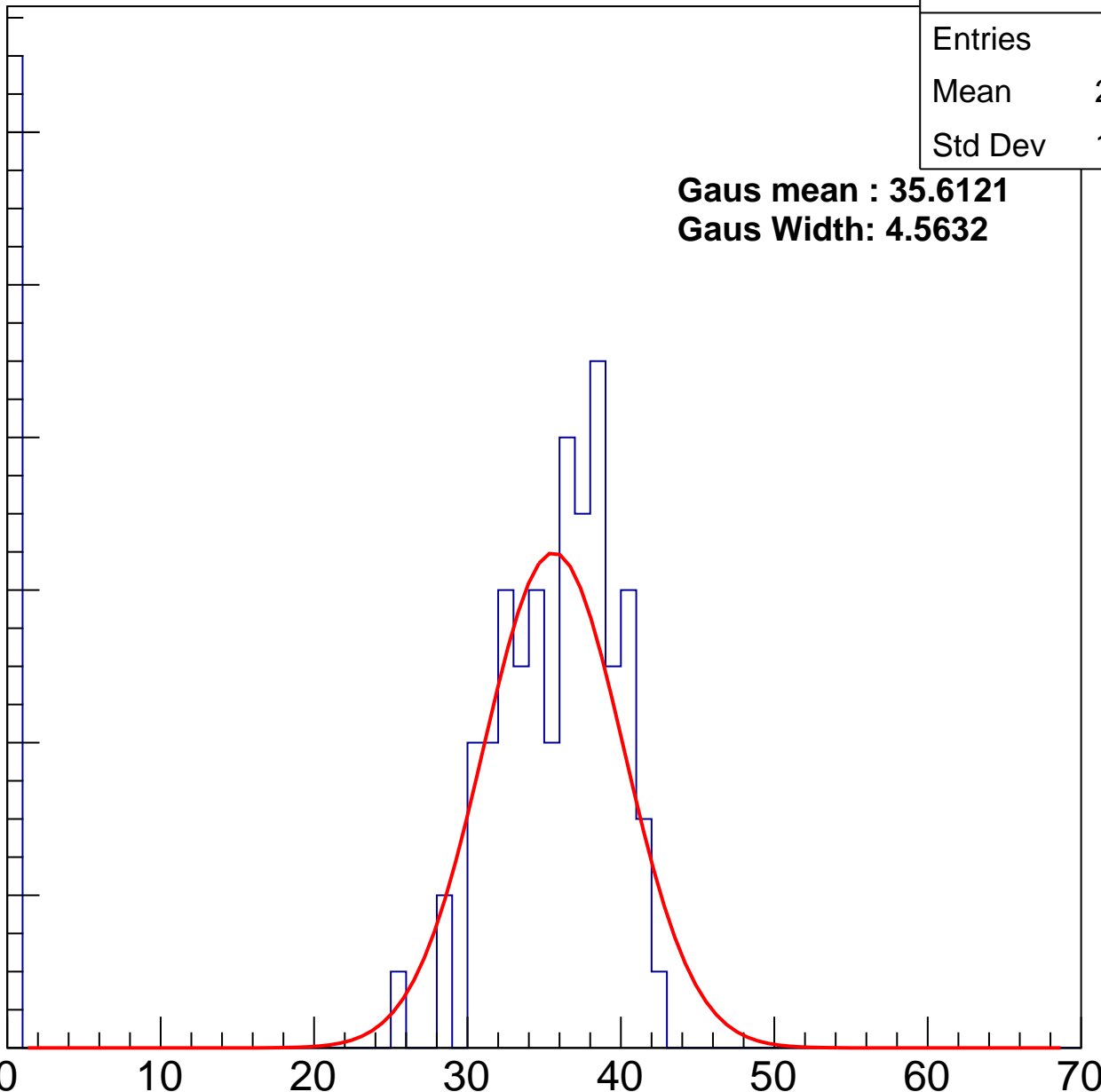
40

50

60

70

ampl



B1L103S, U13-ch24, adc2

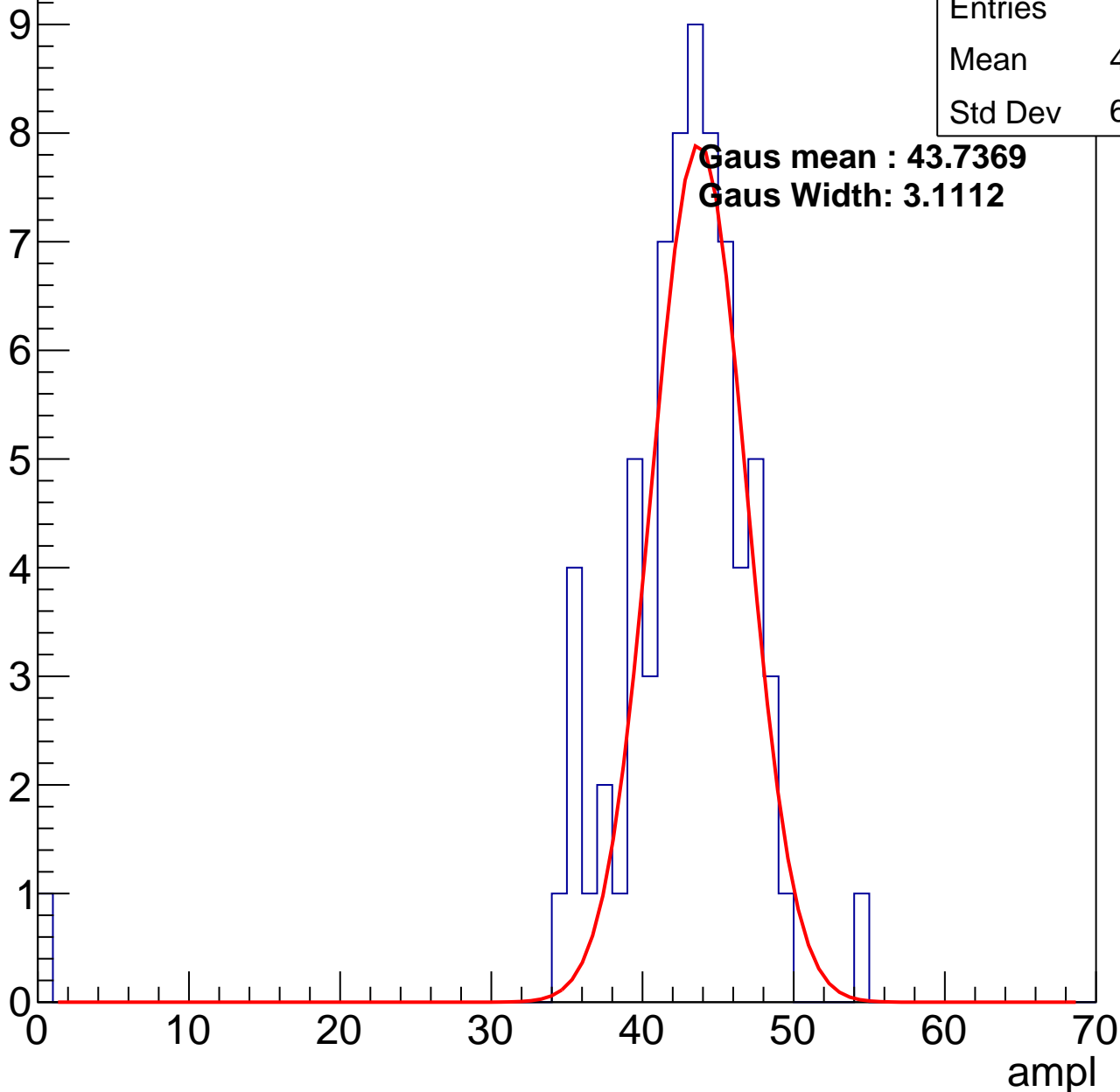
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	41.97
Std Dev	6.262

Gaus mean : 43.7369

Gaus Width: 3.1112

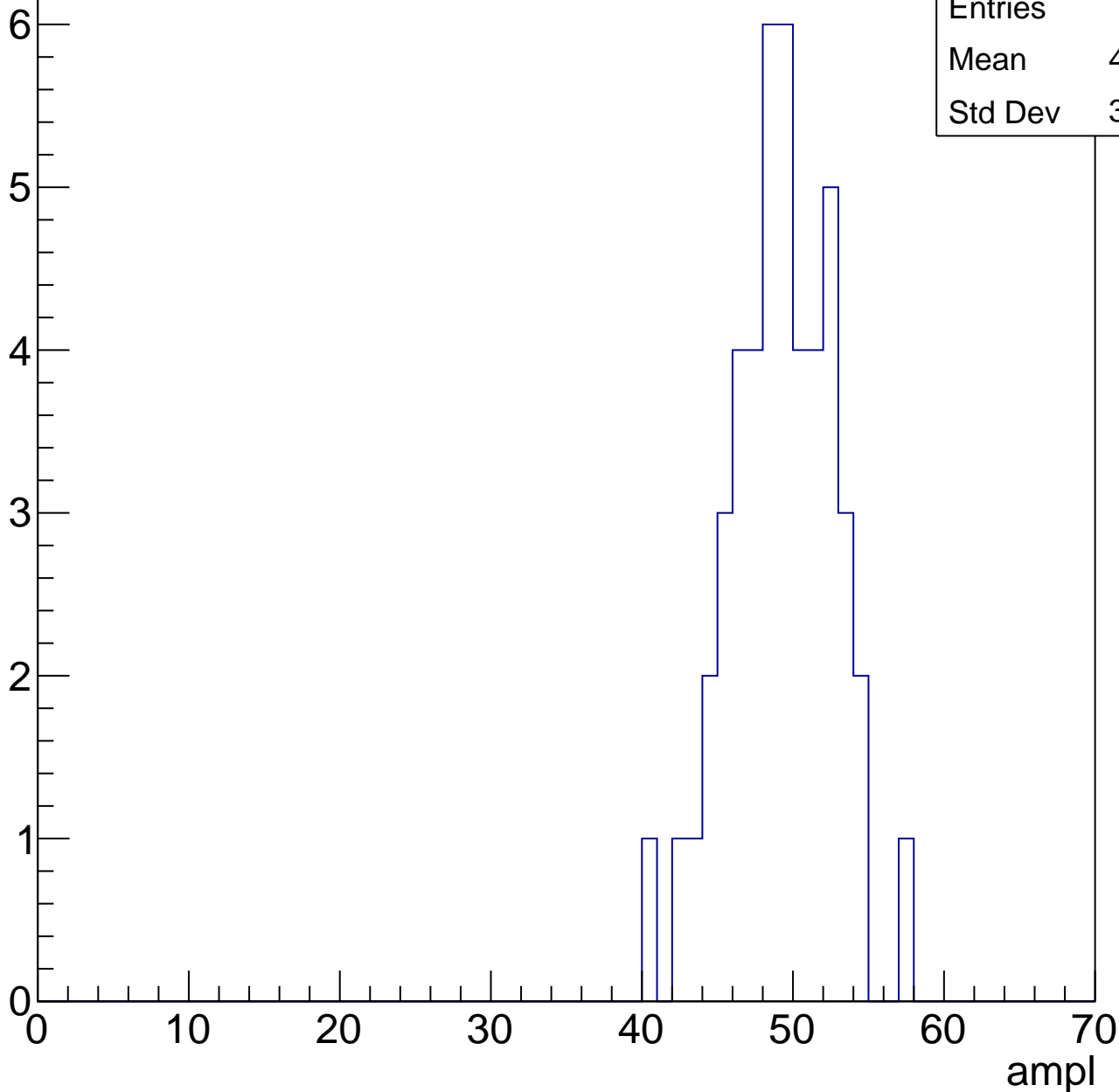


B1L103S, U13-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	48.72
Std Dev	3.407

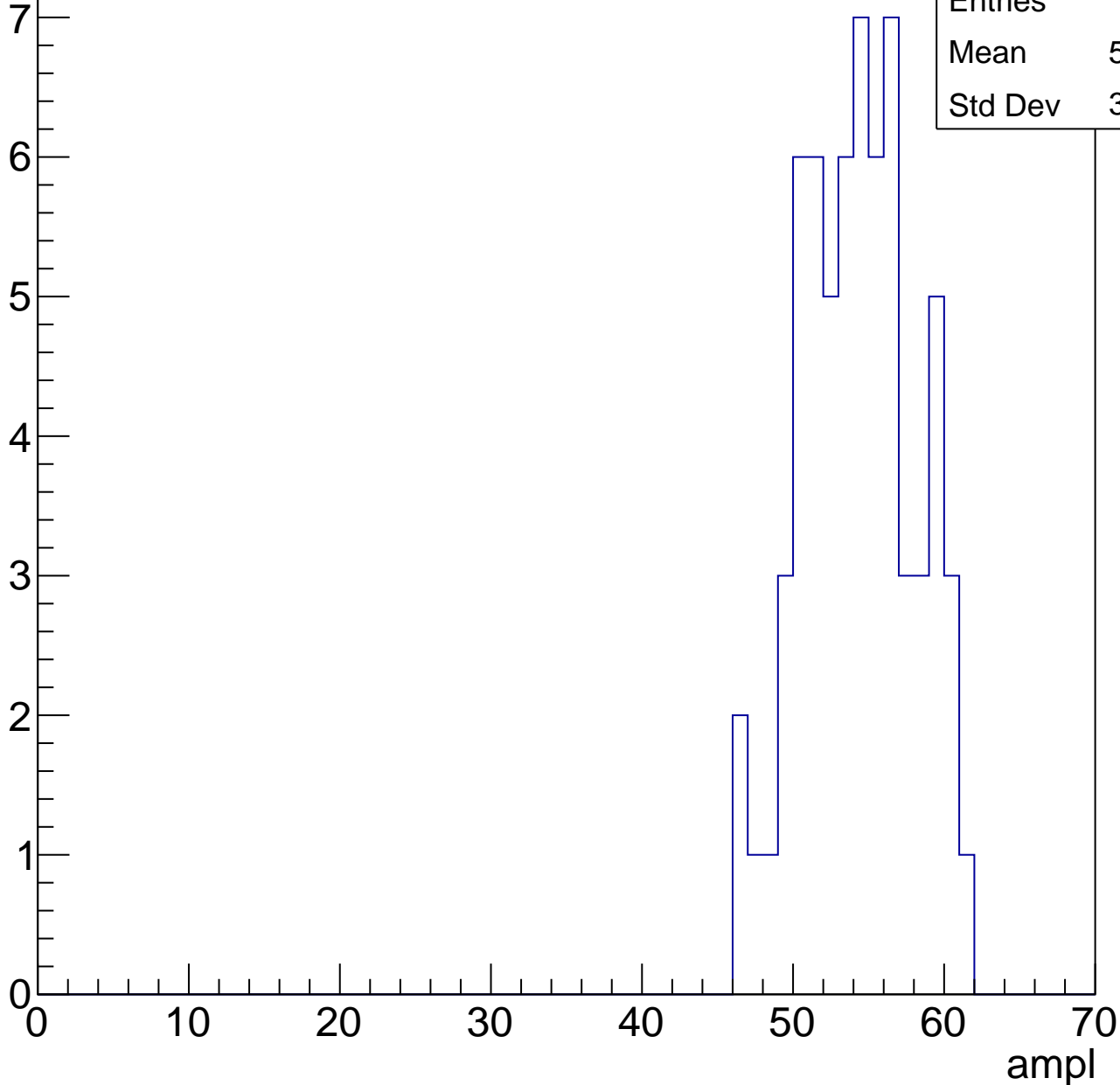


B1L103S, U13-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	53.83
Std Dev	3.623

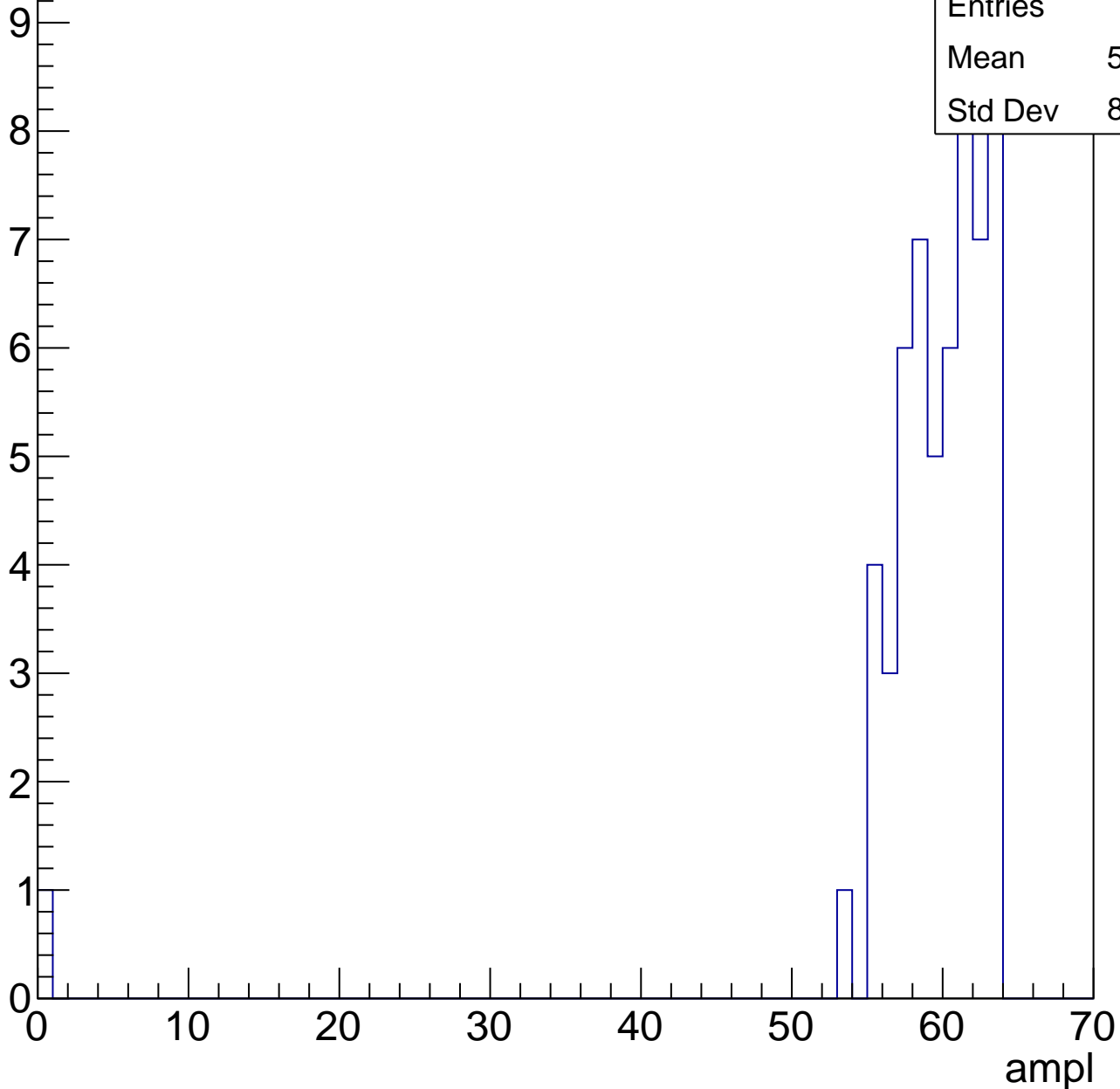


B1L103S, U13-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.44
Std Dev	8.223



B1L103S, U13-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

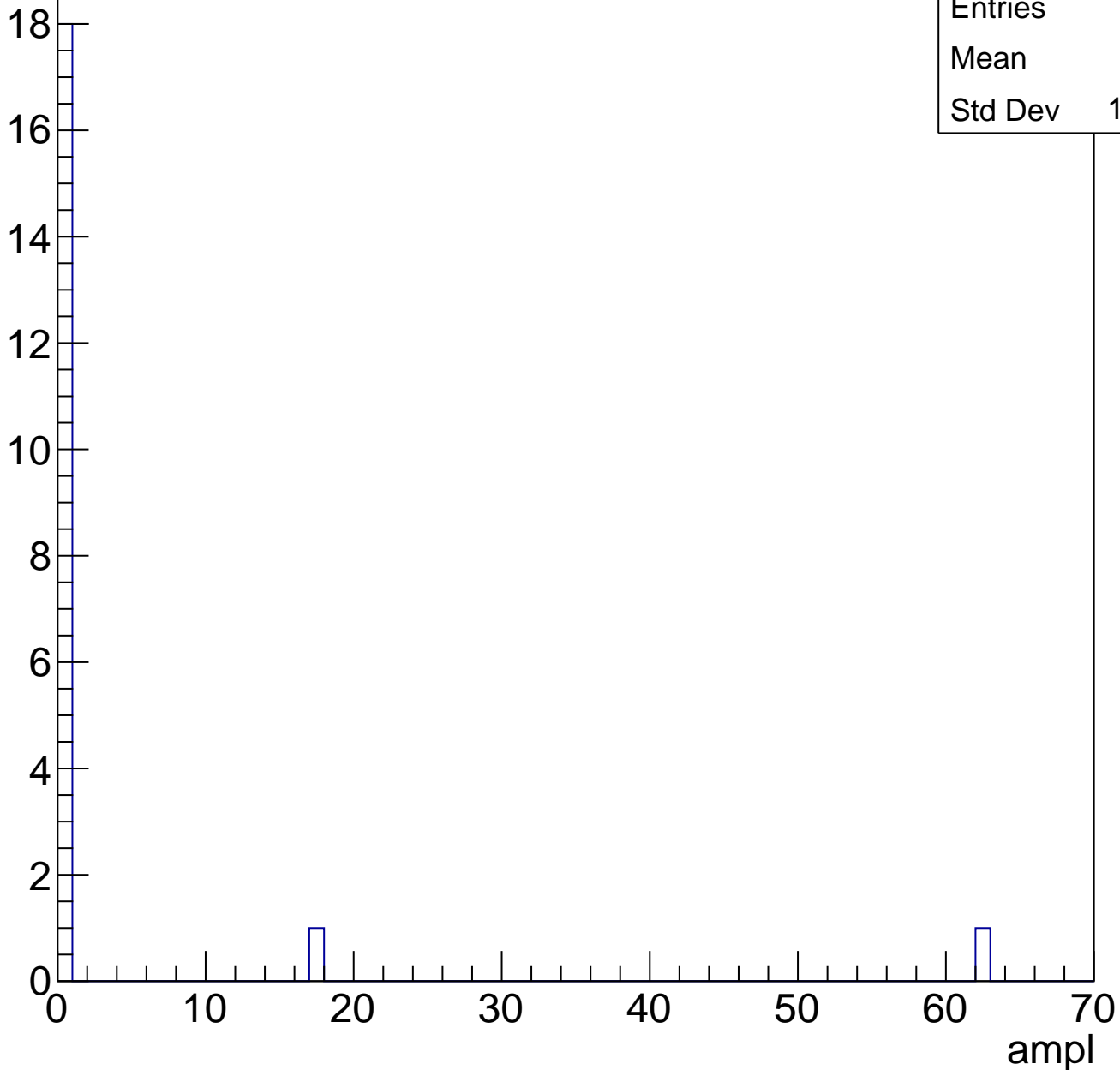


B1L103S, U13-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.95
Std Dev	13.82

Entry



B1L103S, U13-ch25, adc0

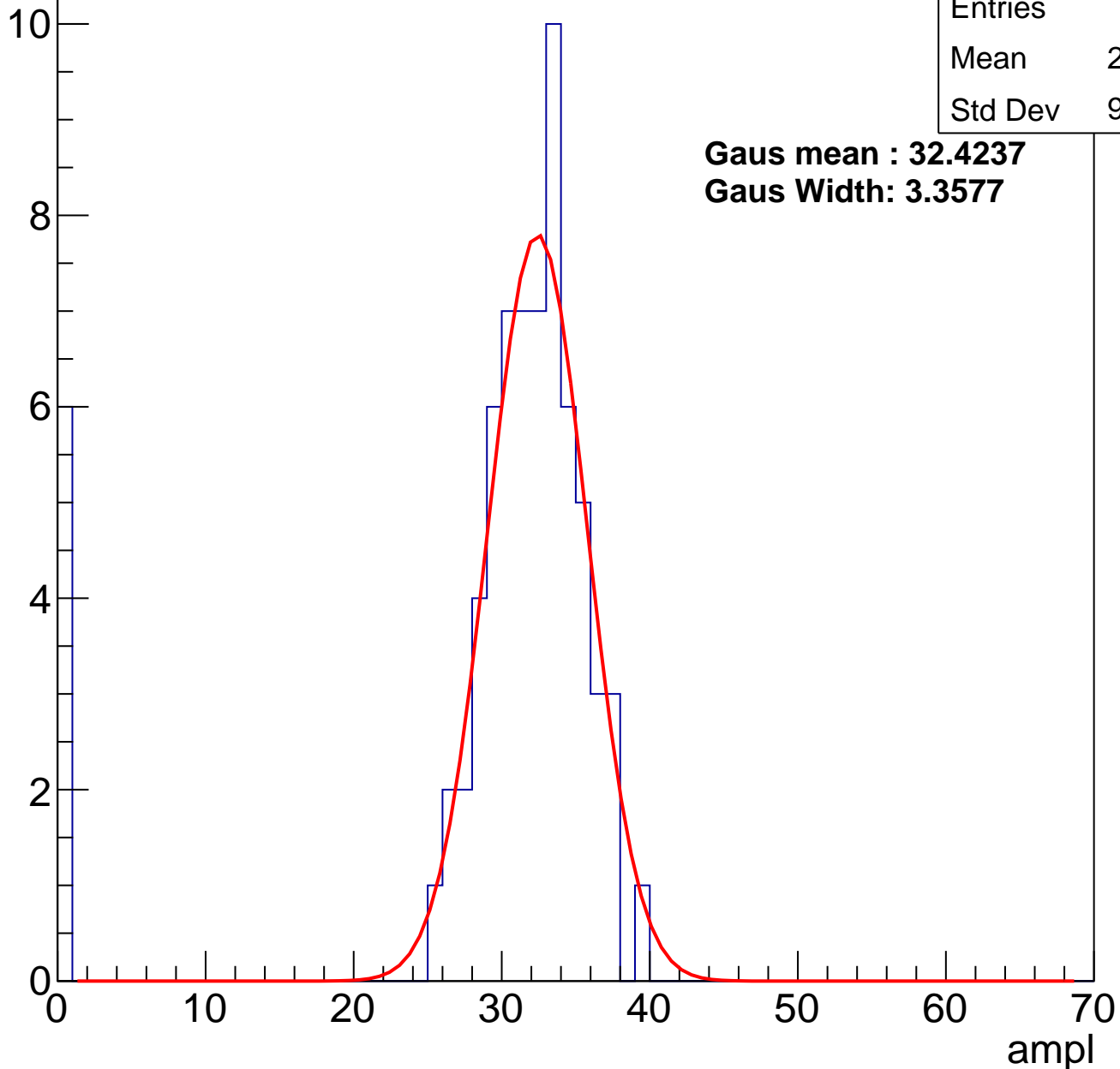
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	29.07
Std Dev	9.349

Gaus mean : 32.4237

Gaus Width: 3.3577

Entry

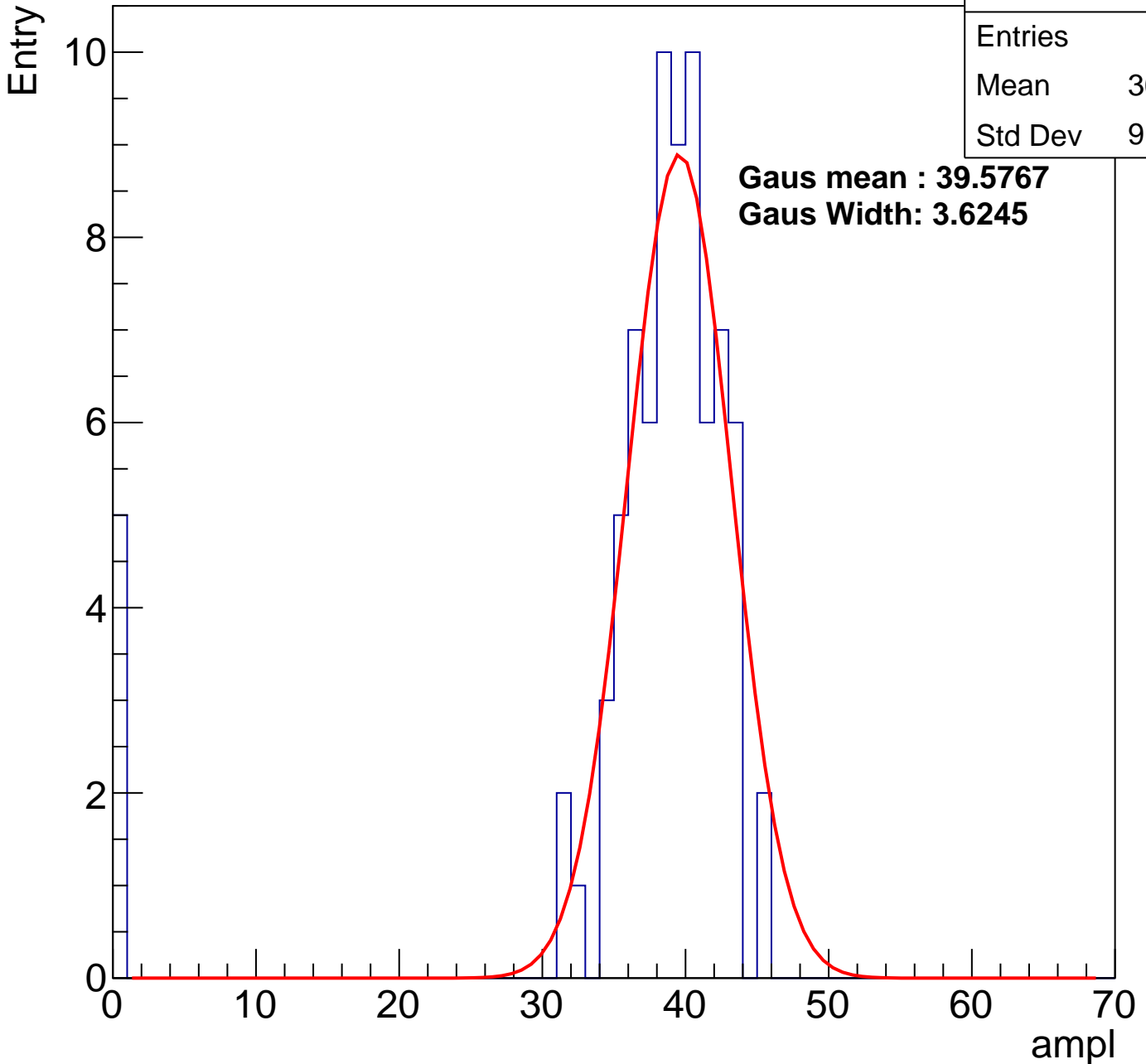


B1L103S, U13-ch25, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	36.25
Std Dev	9.874

Gaus mean : 39.5767
Gaus Width: 3.6245



B1L103S, U13-ch25, adc2

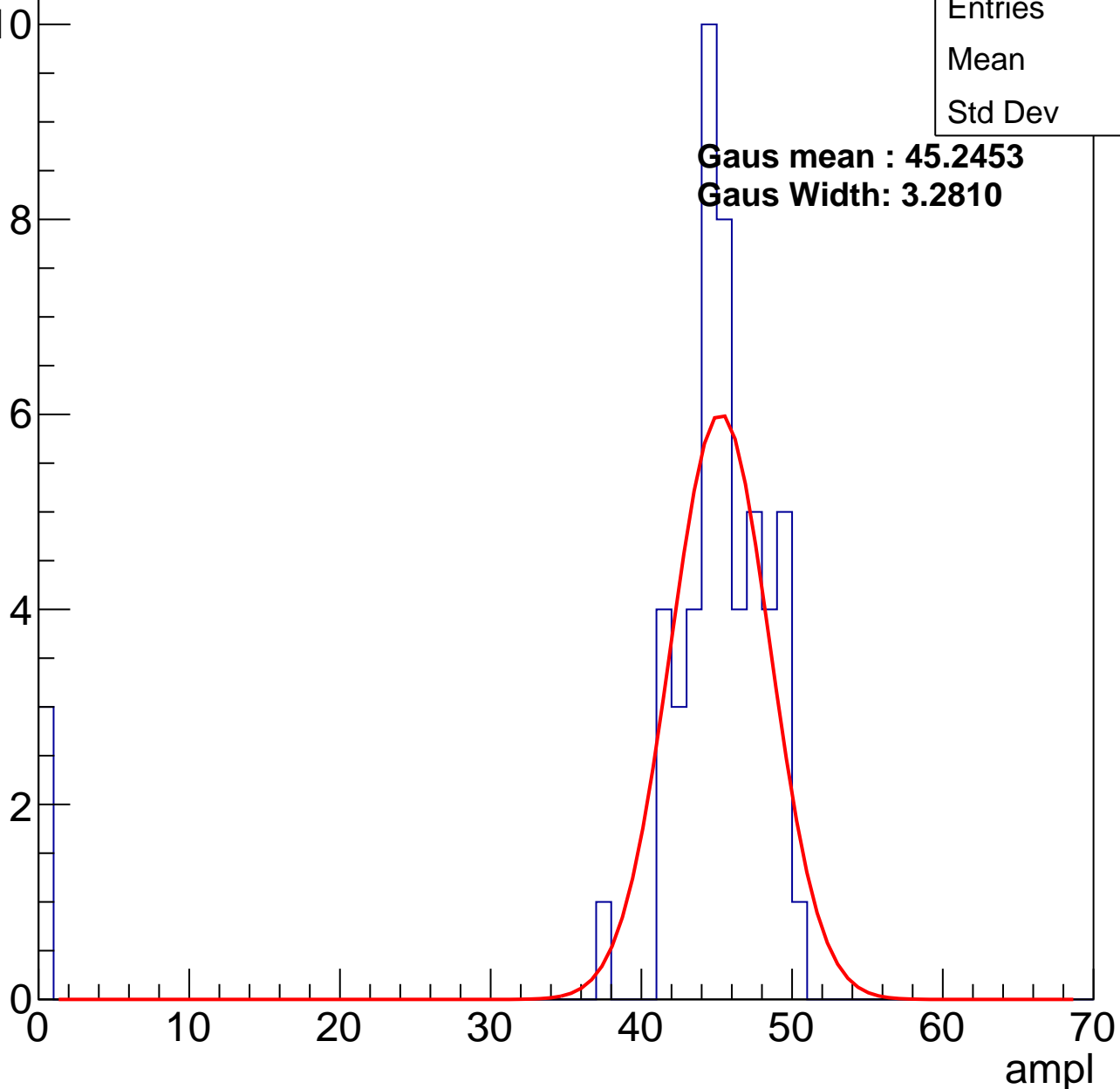
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	42.4
Std Dev	10.8

Gaus mean : 45.2453

Gaus Width: 3.2810

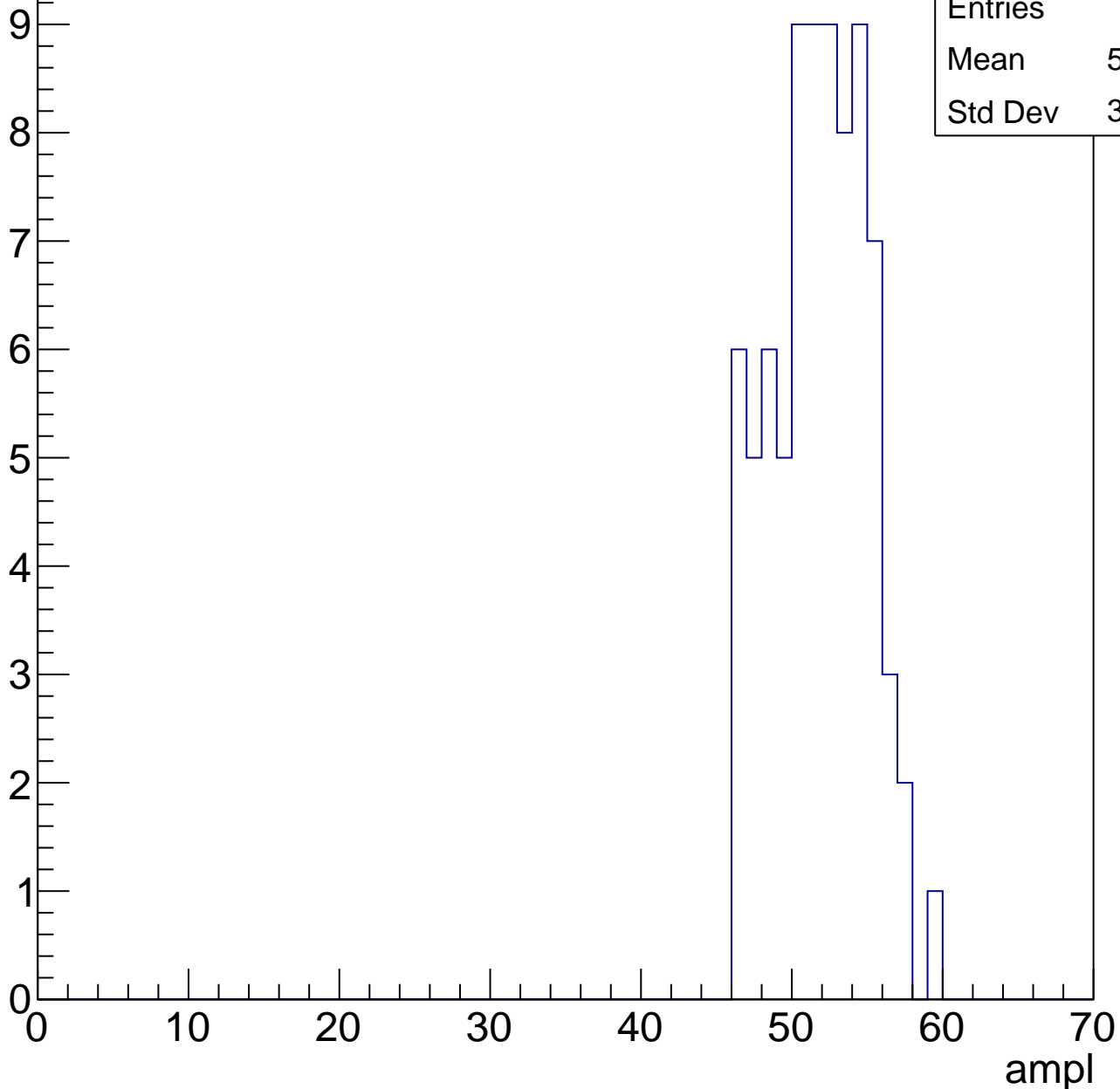


B1L103S, U13-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	51.35
Std Dev	3.077

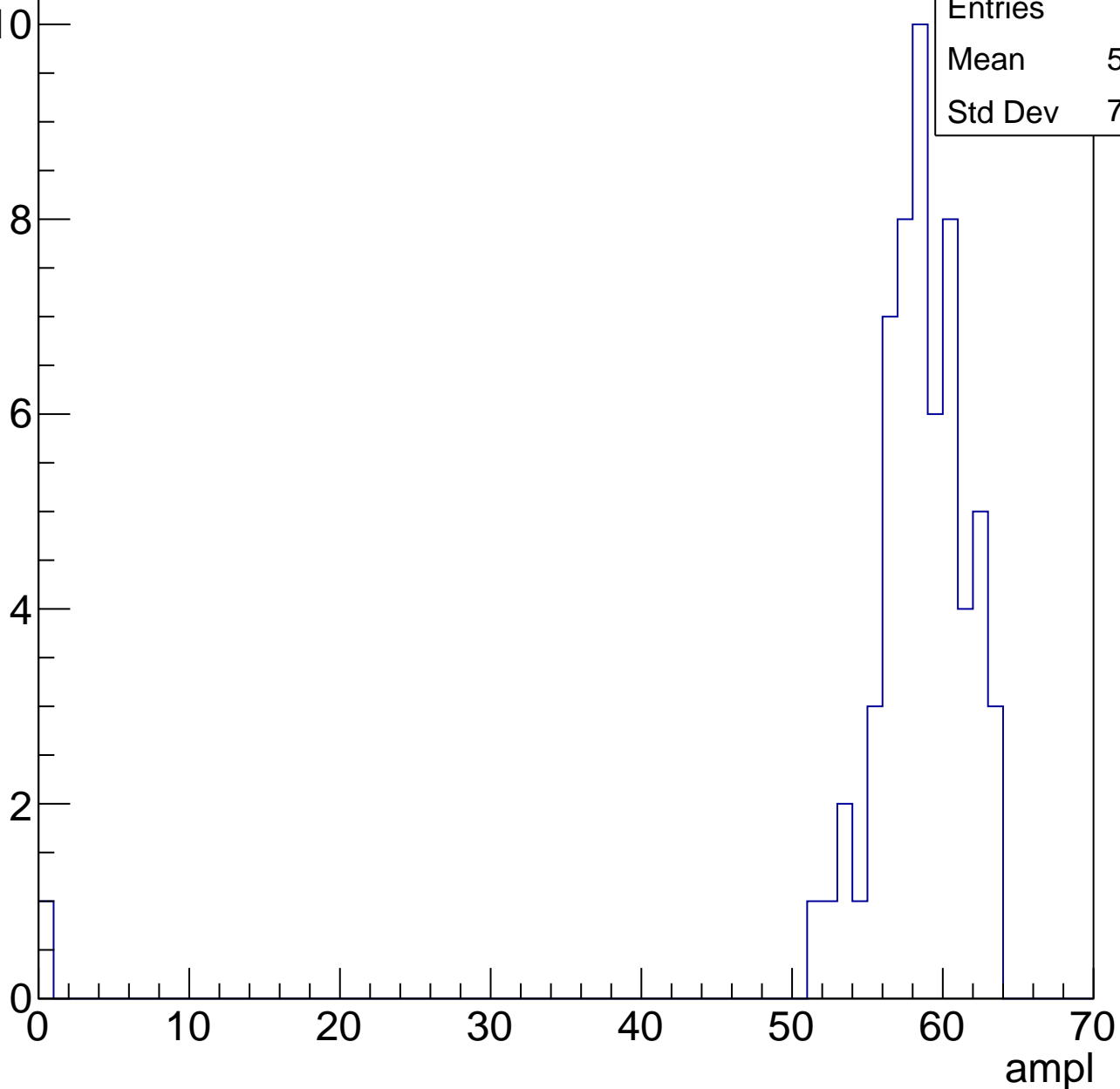


B1L103S, U13-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

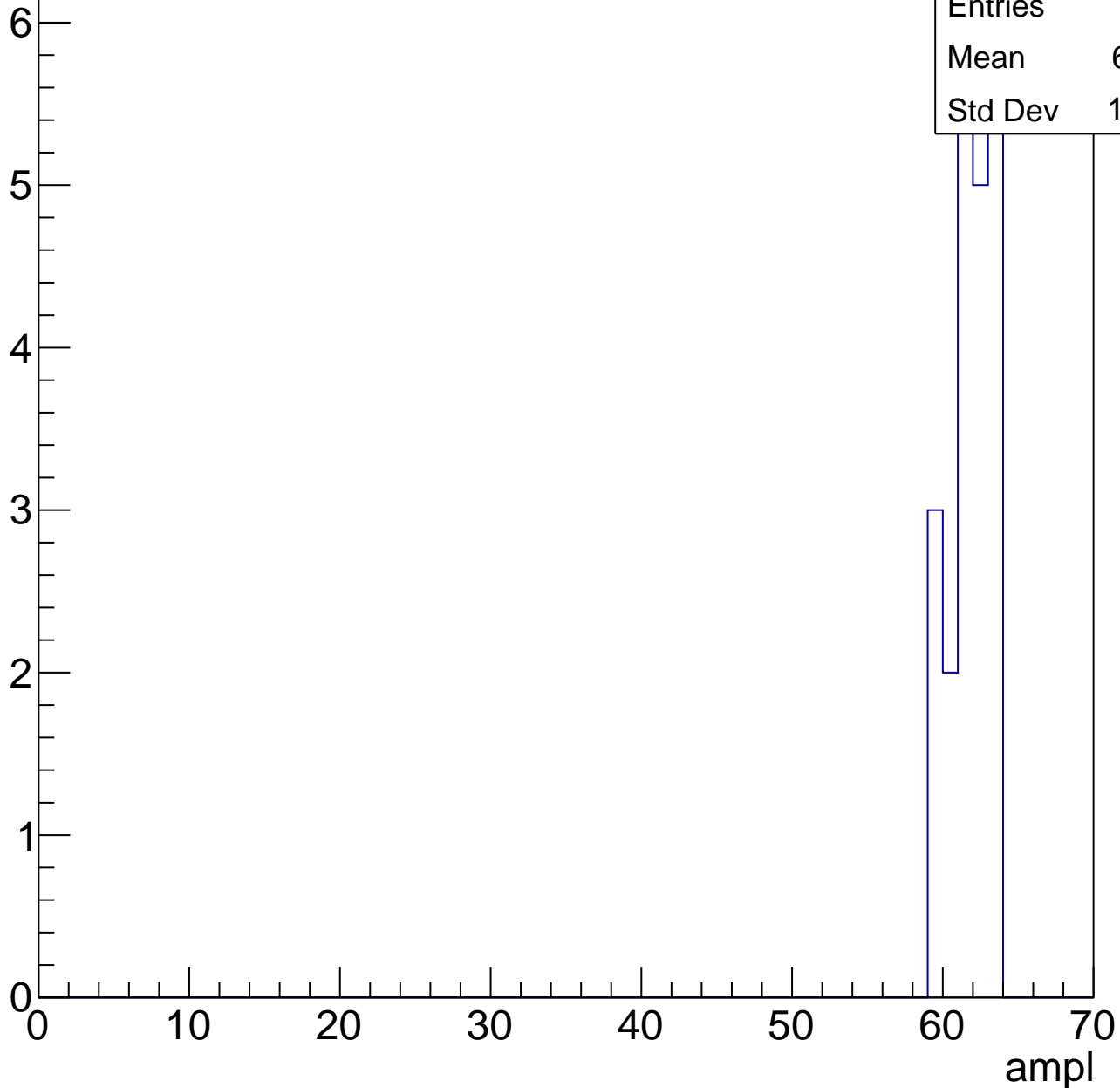
Entries	60
Mean	57.22
Std Dev	7.927



B1L103S, U13-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch26, adc0

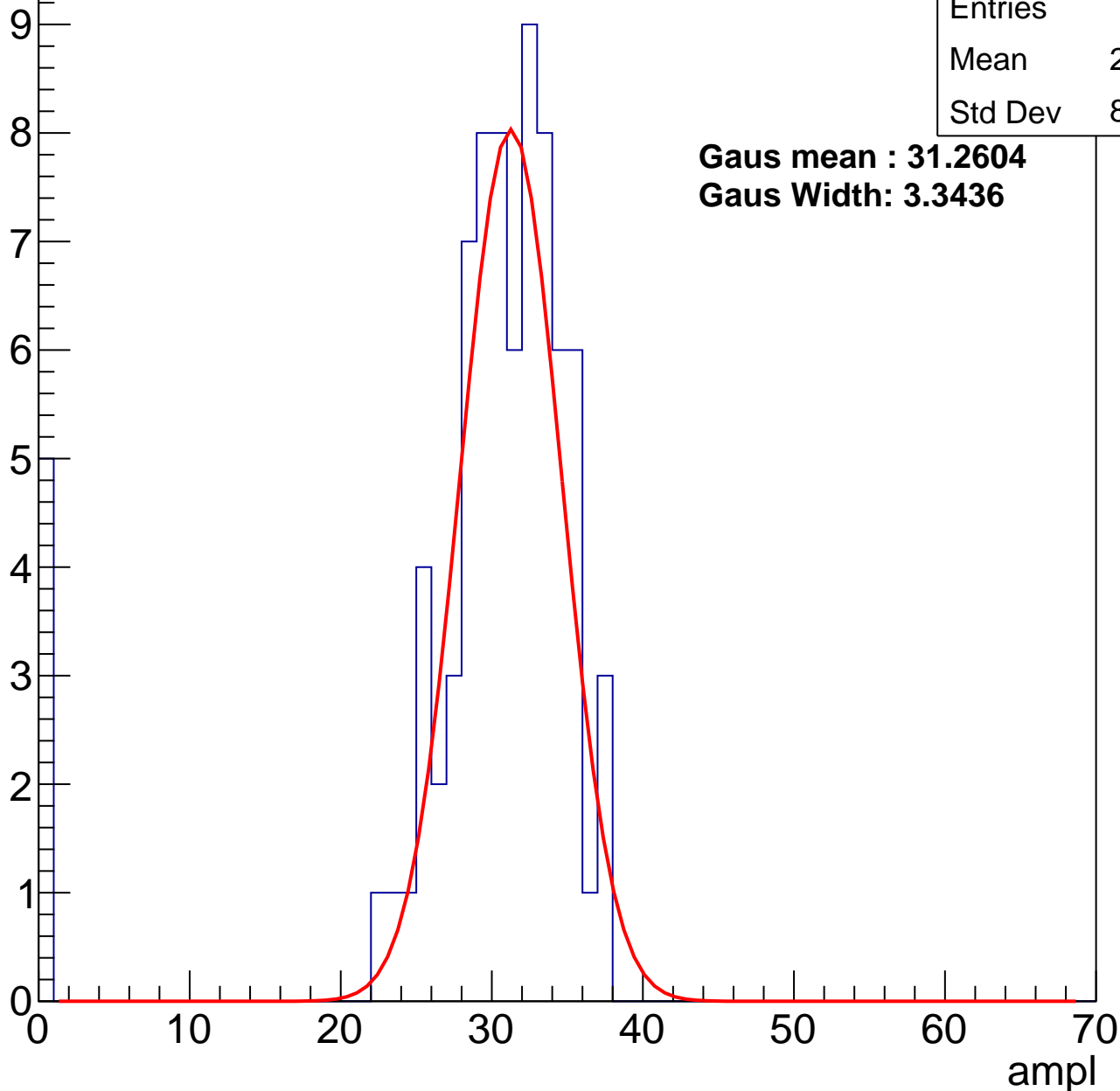
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	28.72
Std Dev	8.158

Gaus mean : 31.2604

Gaus Width: 3.3436



B1L103S, U13-ch26, adc1

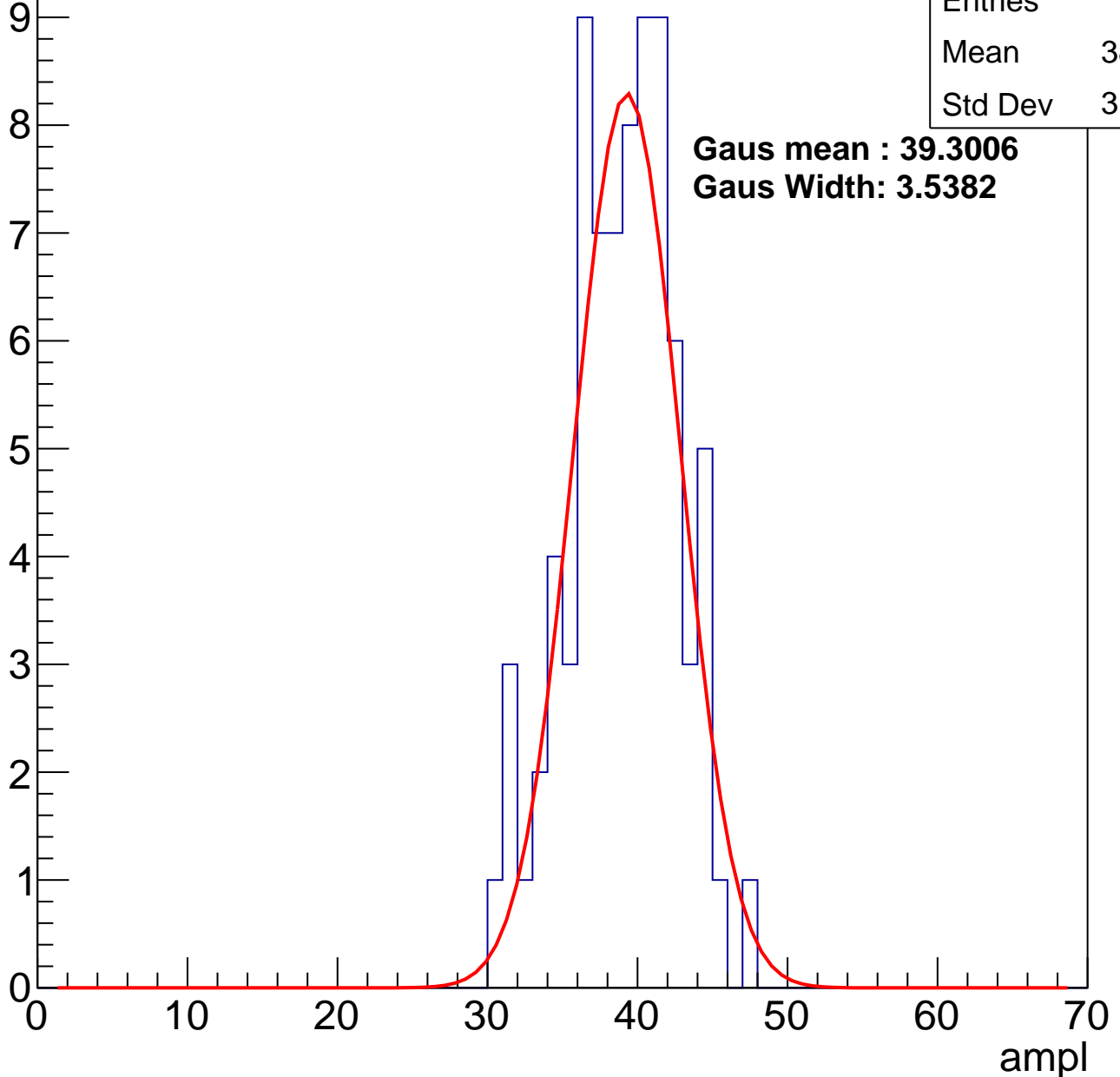
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	38.54
Std Dev	3.582

Gaus mean : 39.3006

Gaus Width: 3.5382



B1L103S, U13-ch26, adc2

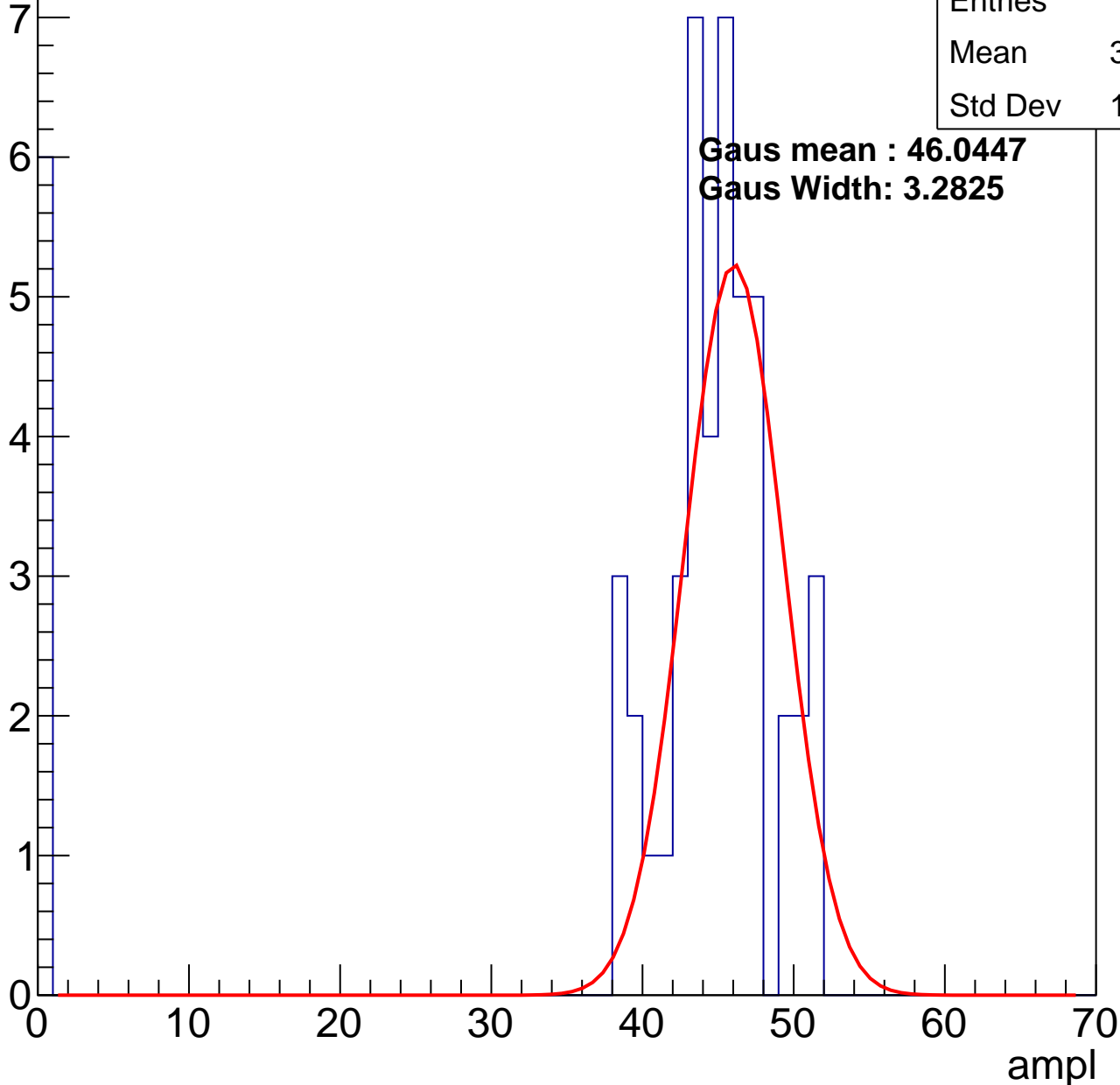
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	39.35
Std Dev	14.72

Gaus mean : 46.0447

Gaus Width: 3.2825



B1L103S, U13-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	48.46
Std Dev	11.3

Entry

10

8

6

4

2

0

0

10

20

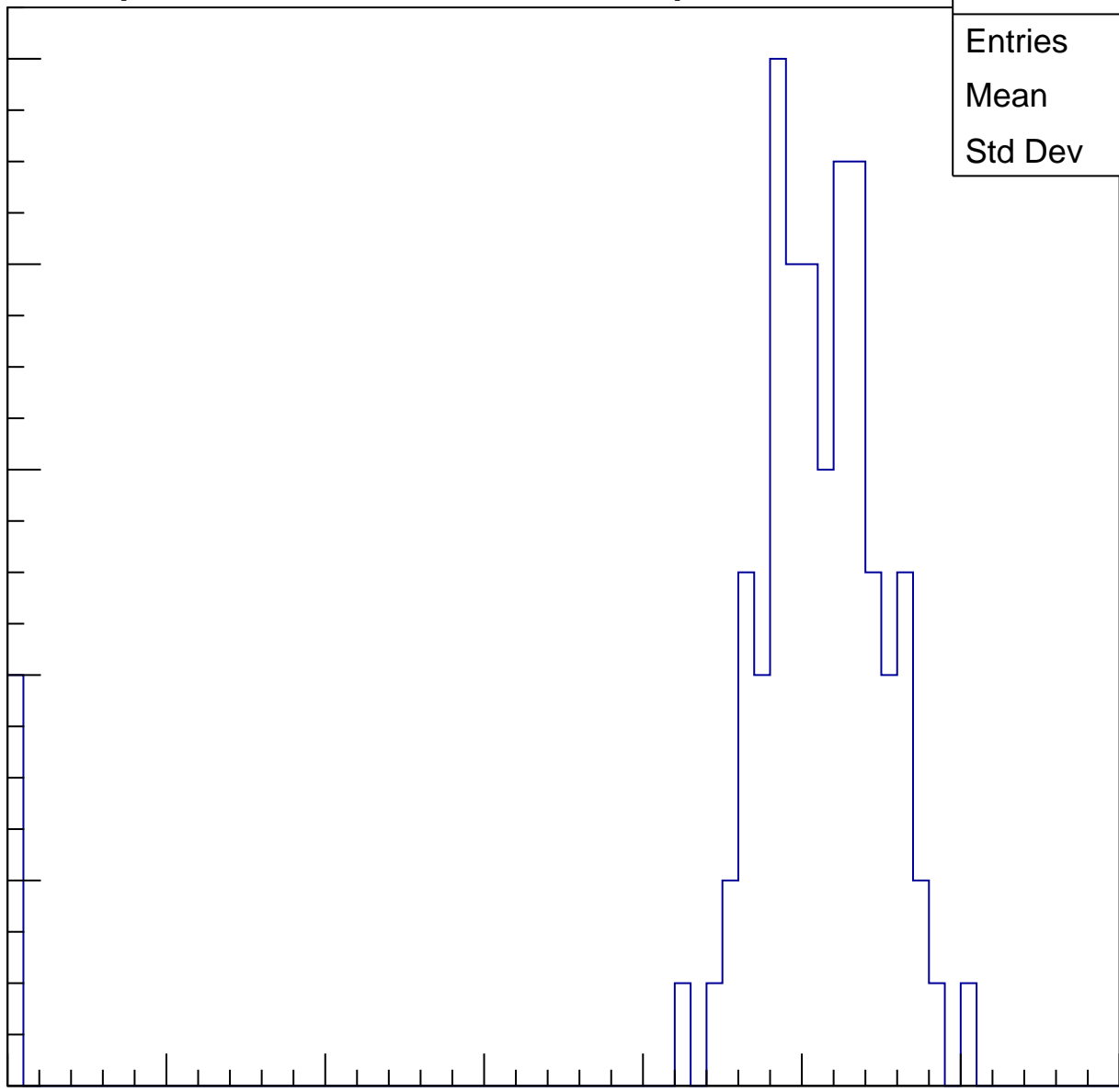
30

40

50

60

ampl

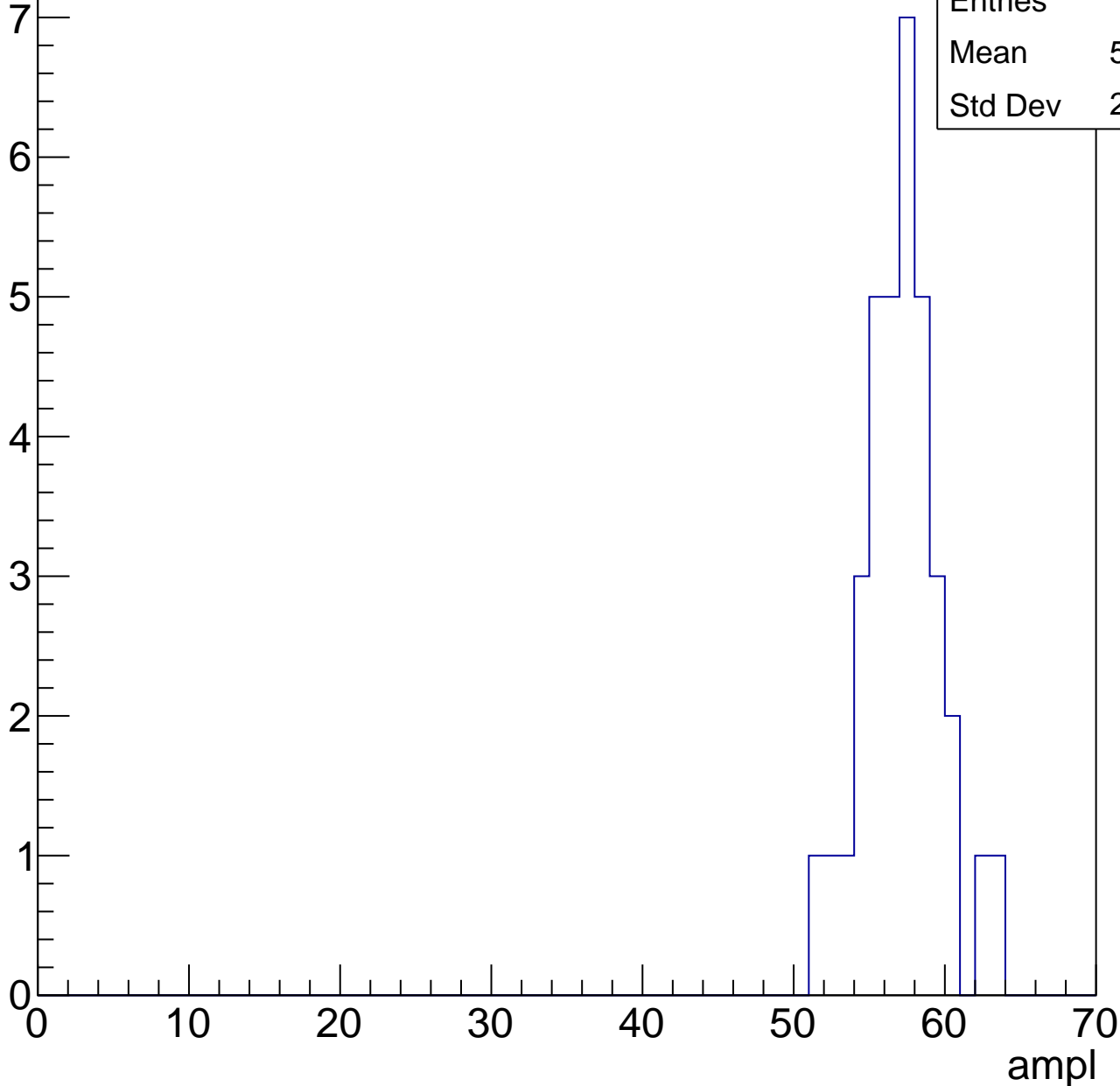


B1L103S, U13-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	56.69
Std Dev	2.516

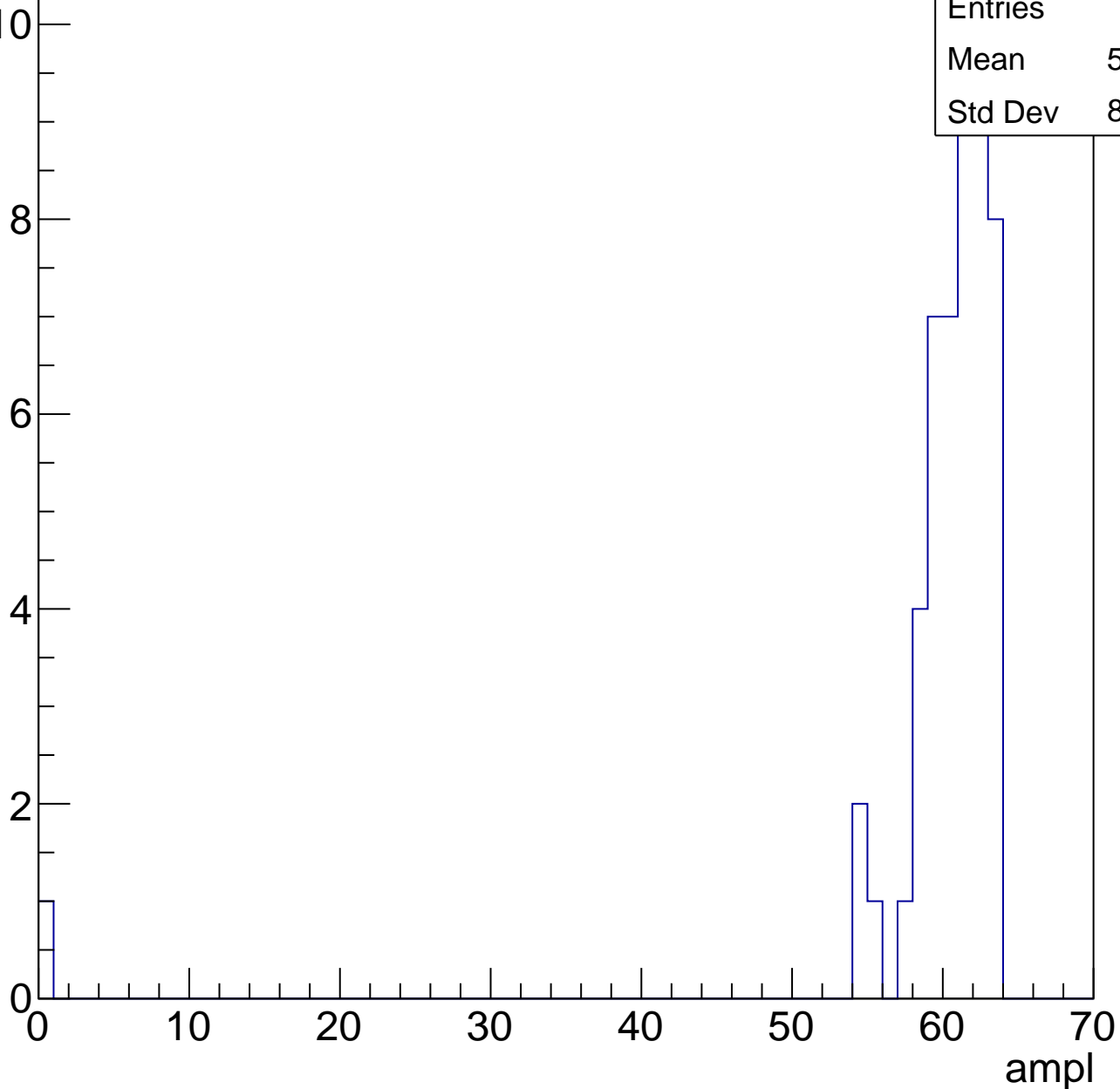


B1L103S, U13-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	59.14
Std Dev	8.729



B1L103S, U13-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U13-ch27, adc0

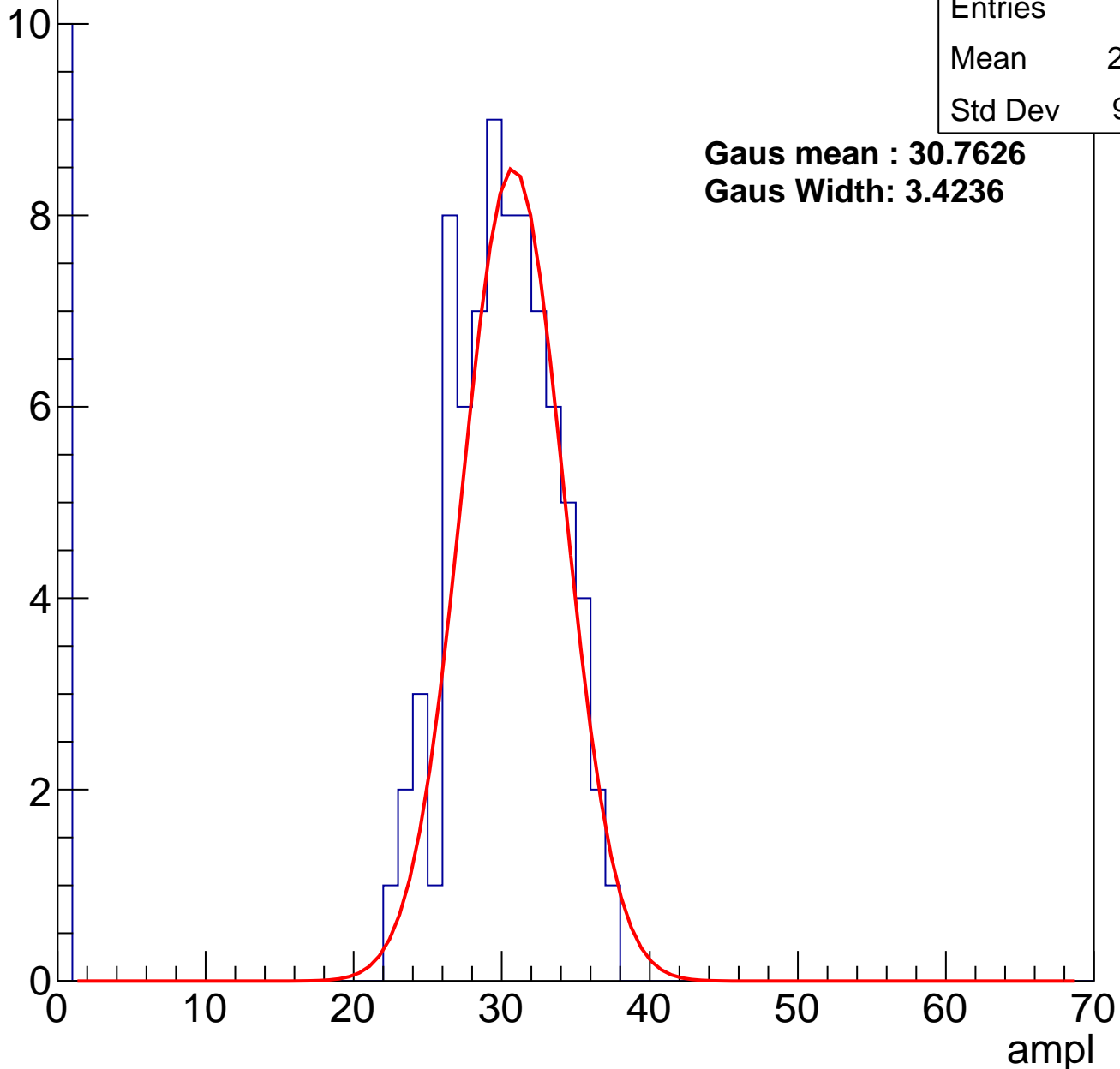
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	26.38
Std Dev	9.971

Gaus mean : 30.7626

Gaus Width: 3.4236

Entry



B1L103S, U13-ch27, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	33.34
Std Dev	12.06

Gaus mean : 38.0643

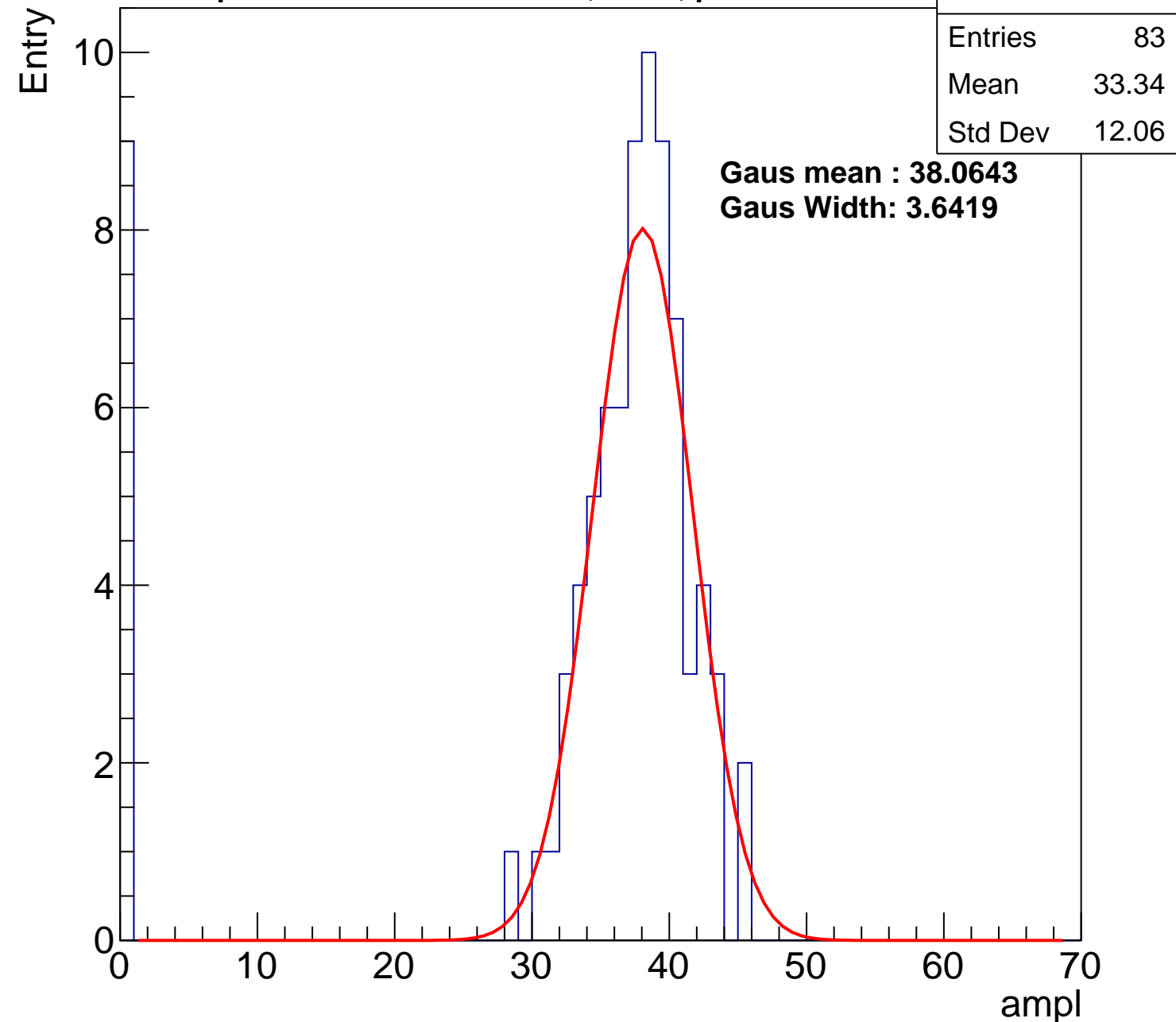
Gaus Width: 3.6419

Entry

10
8
6
4
2
0

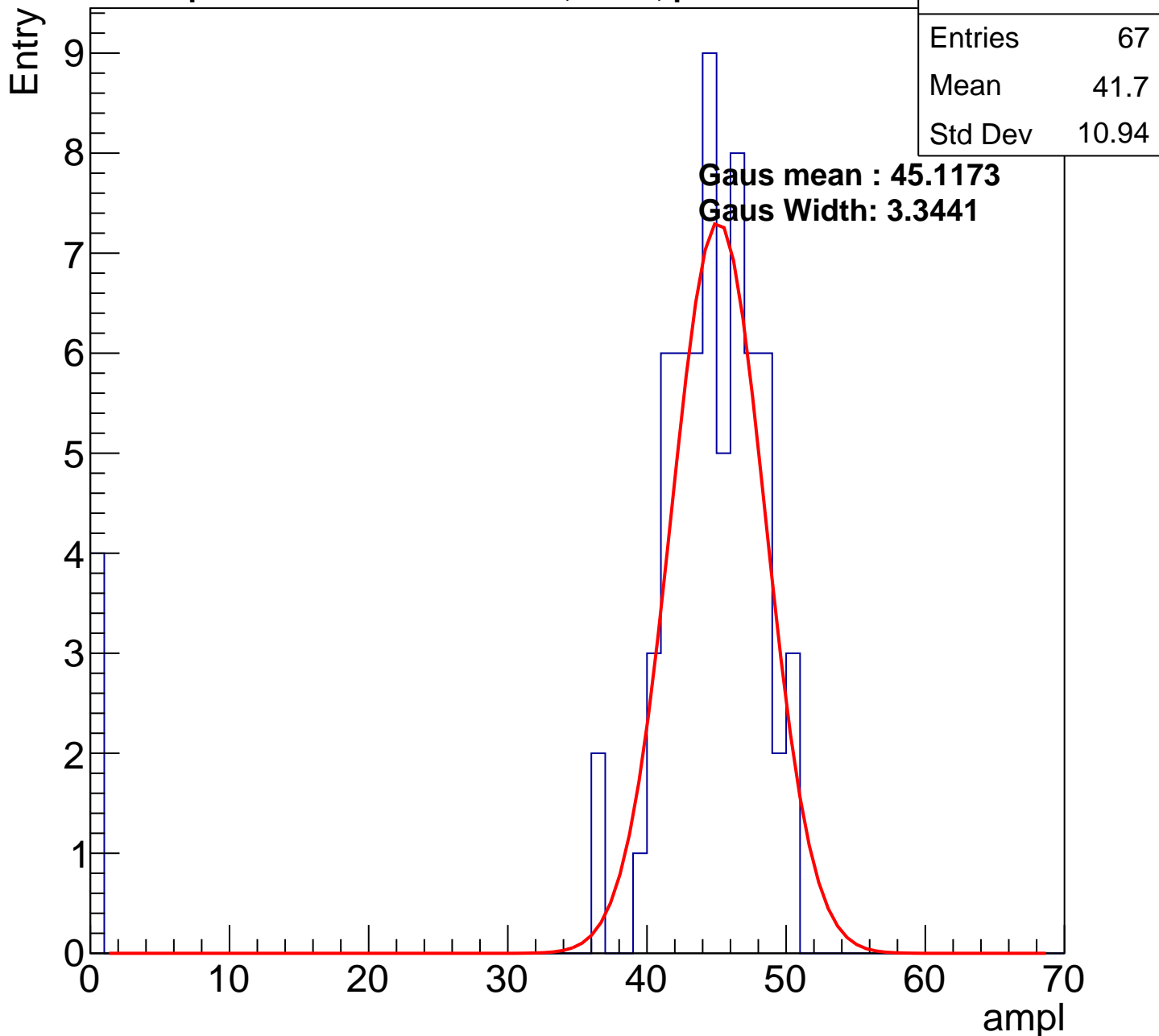
ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch27, adc2

calib_packv5_041523_1651.root, FC#0, port C2

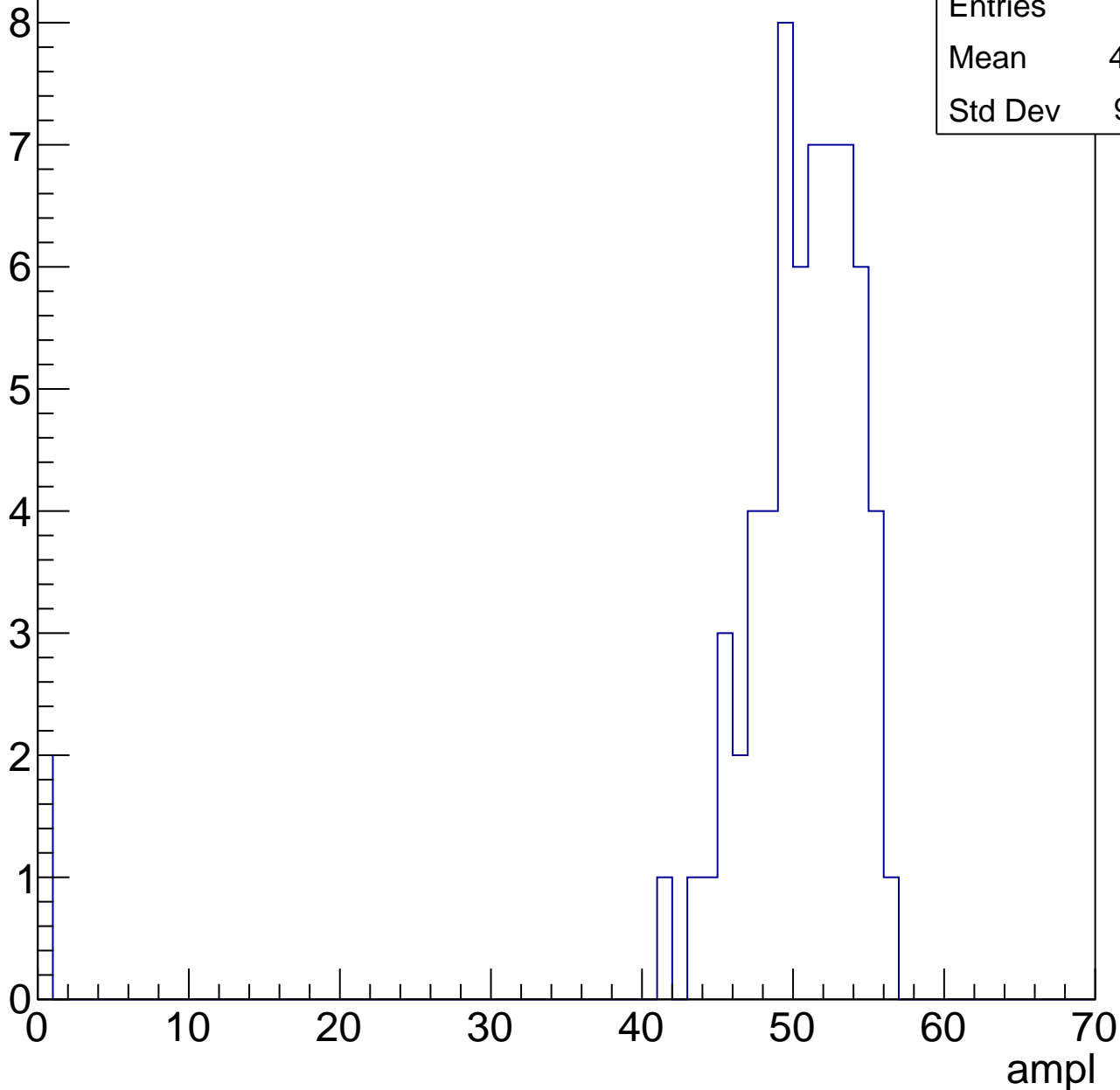


B1L103S, U13-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

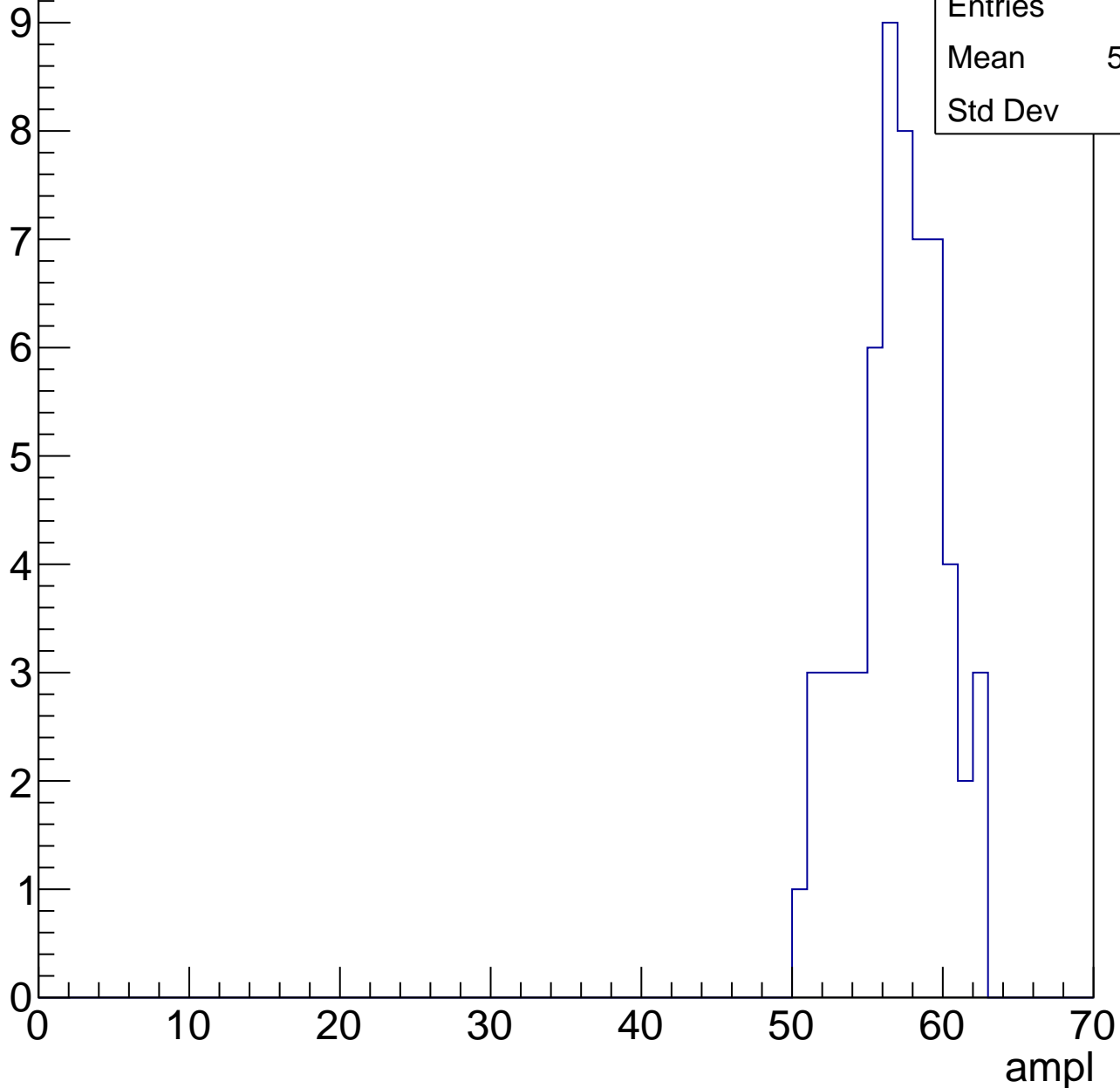
Entries	64
Mean	48.73
Std Dev	9.321



B1L103S, U13-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



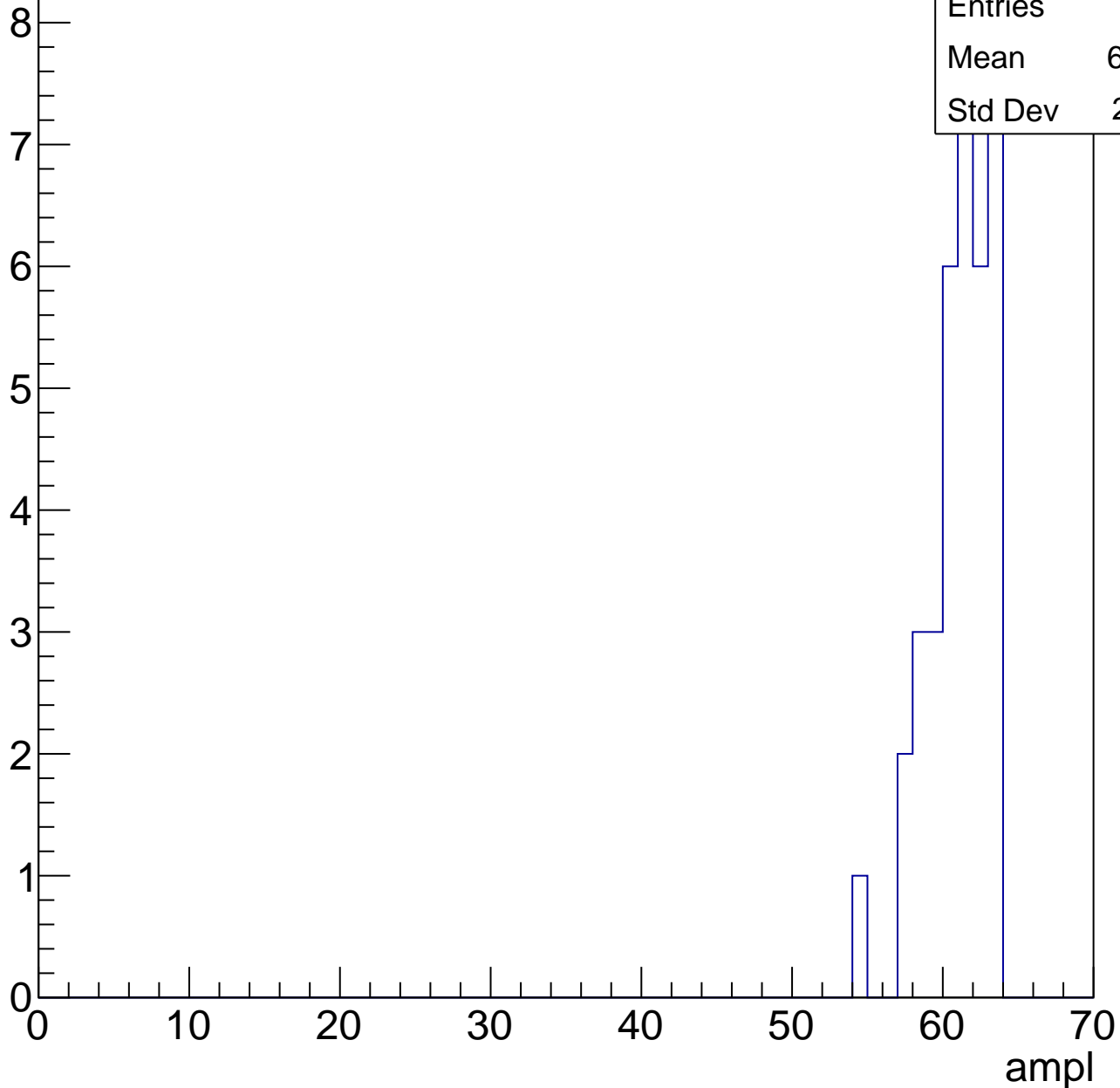
Entries	59
Mean	56.56
Std Dev	2.93

B1L103S, U13-ch27, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	60.62
Std Dev	2.071



B1L103S, U13-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U13-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch28, adc0

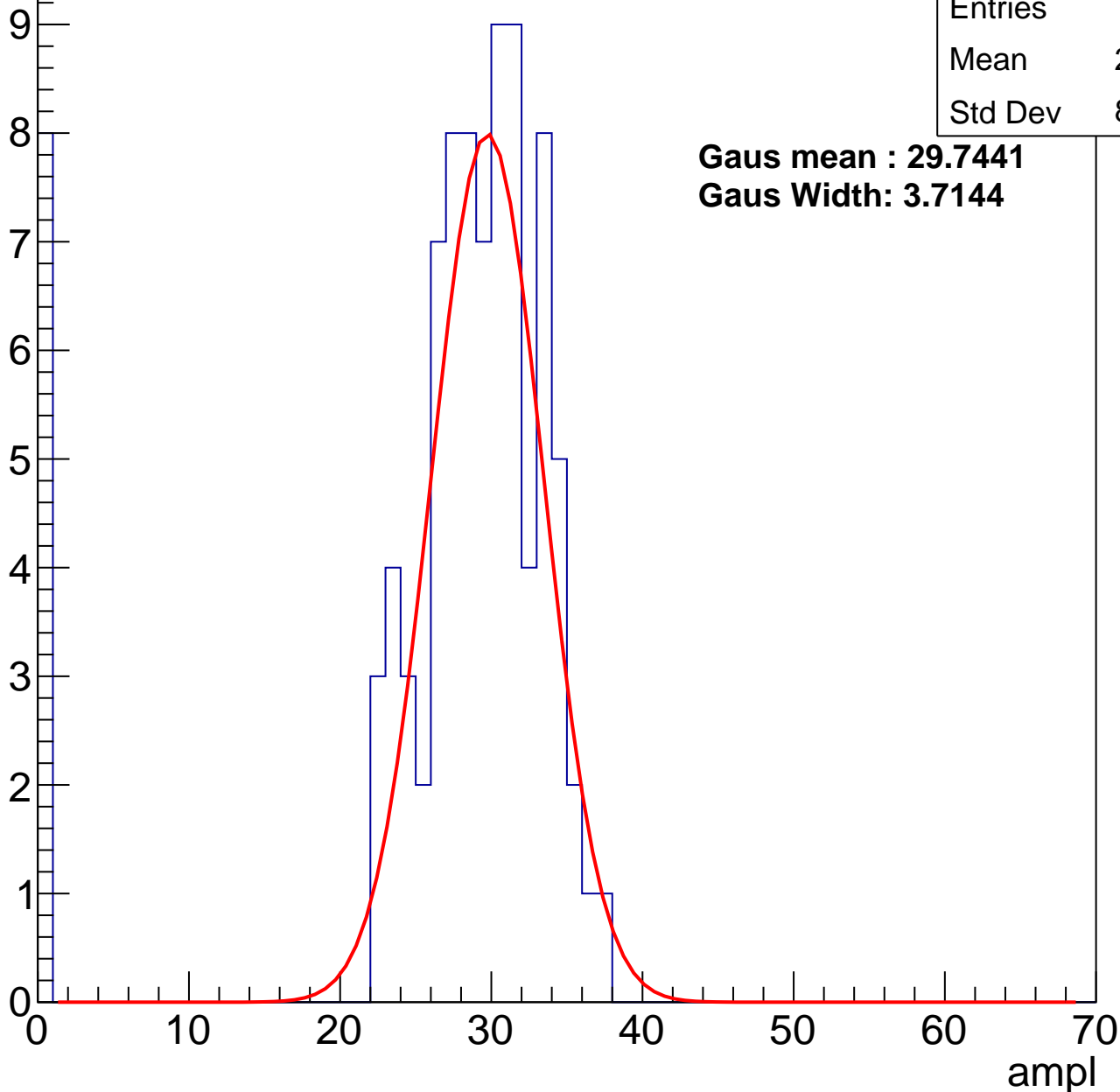
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	26.51
Std Dev	8.991

Gaus mean : 29.7441

Gaus Width: 3.7144



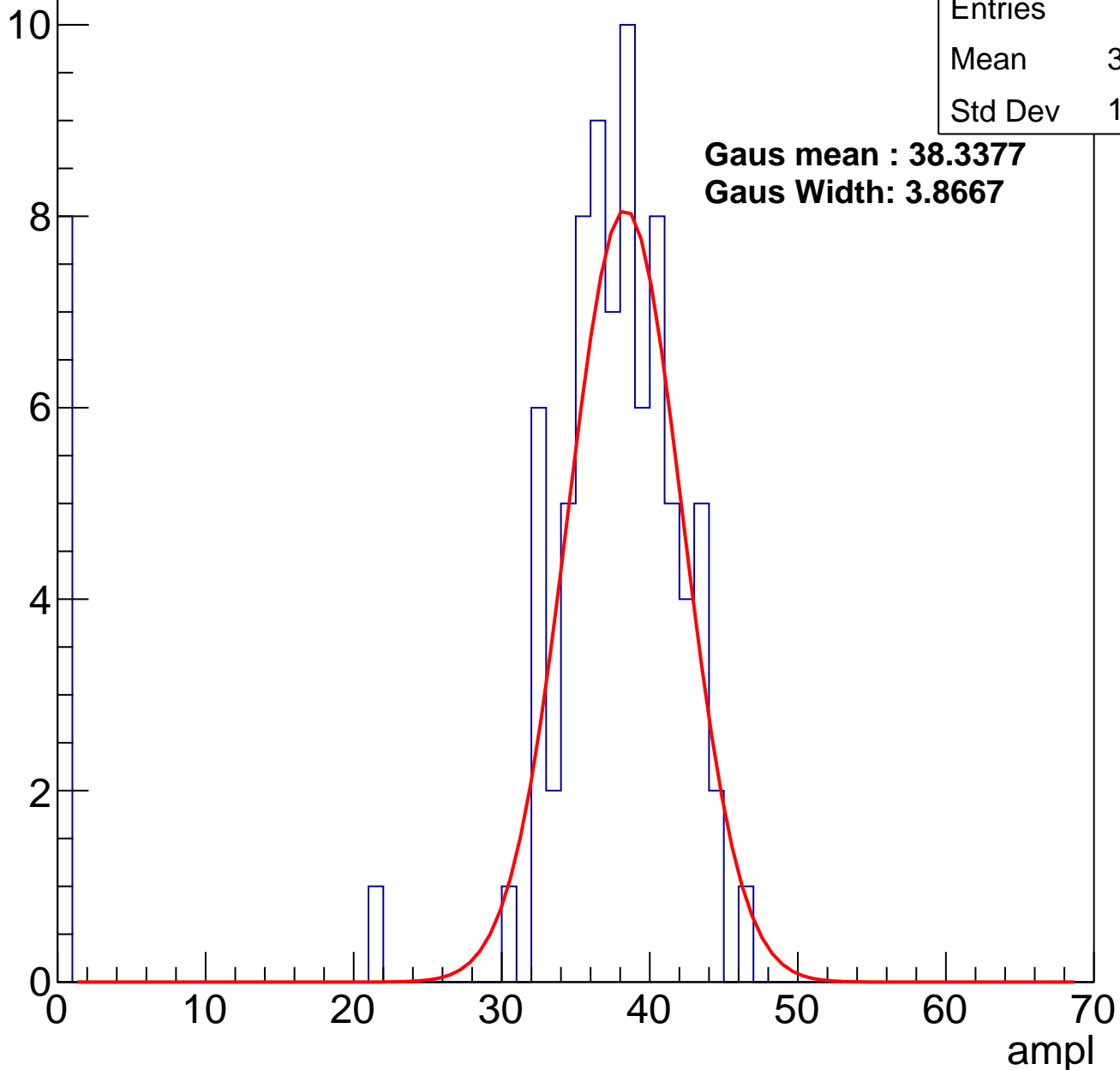
B1L103S, U13-ch28, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	34.07
Std Dev	11.39

Gaus mean : 38.3377
Gaus Width: 3.8667

Entry



B1L103S, U13-ch28, adc2

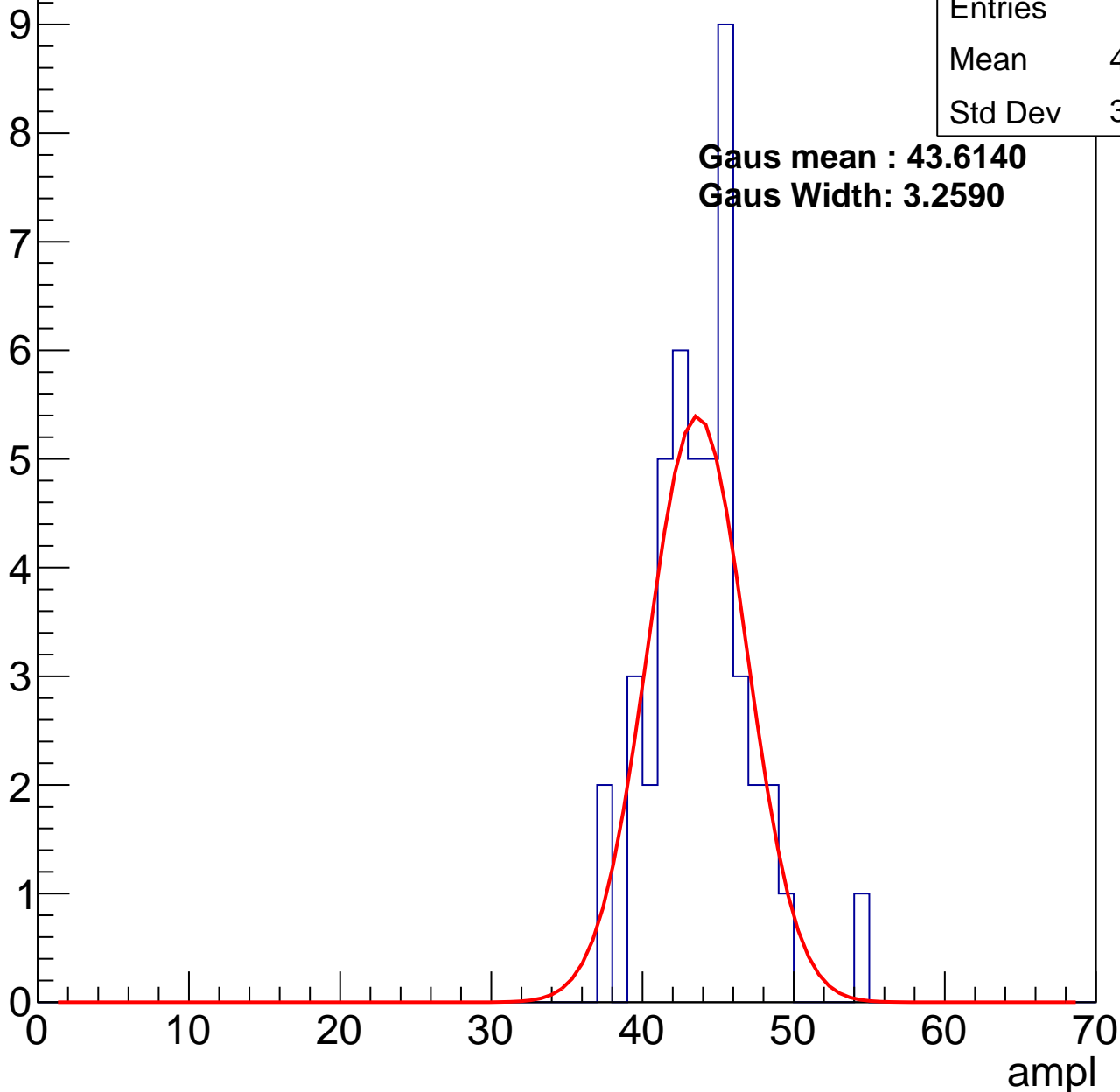
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	43.46
Std Dev	3.167

Gaus mean : 43.6140

Gaus Width: 3.2590

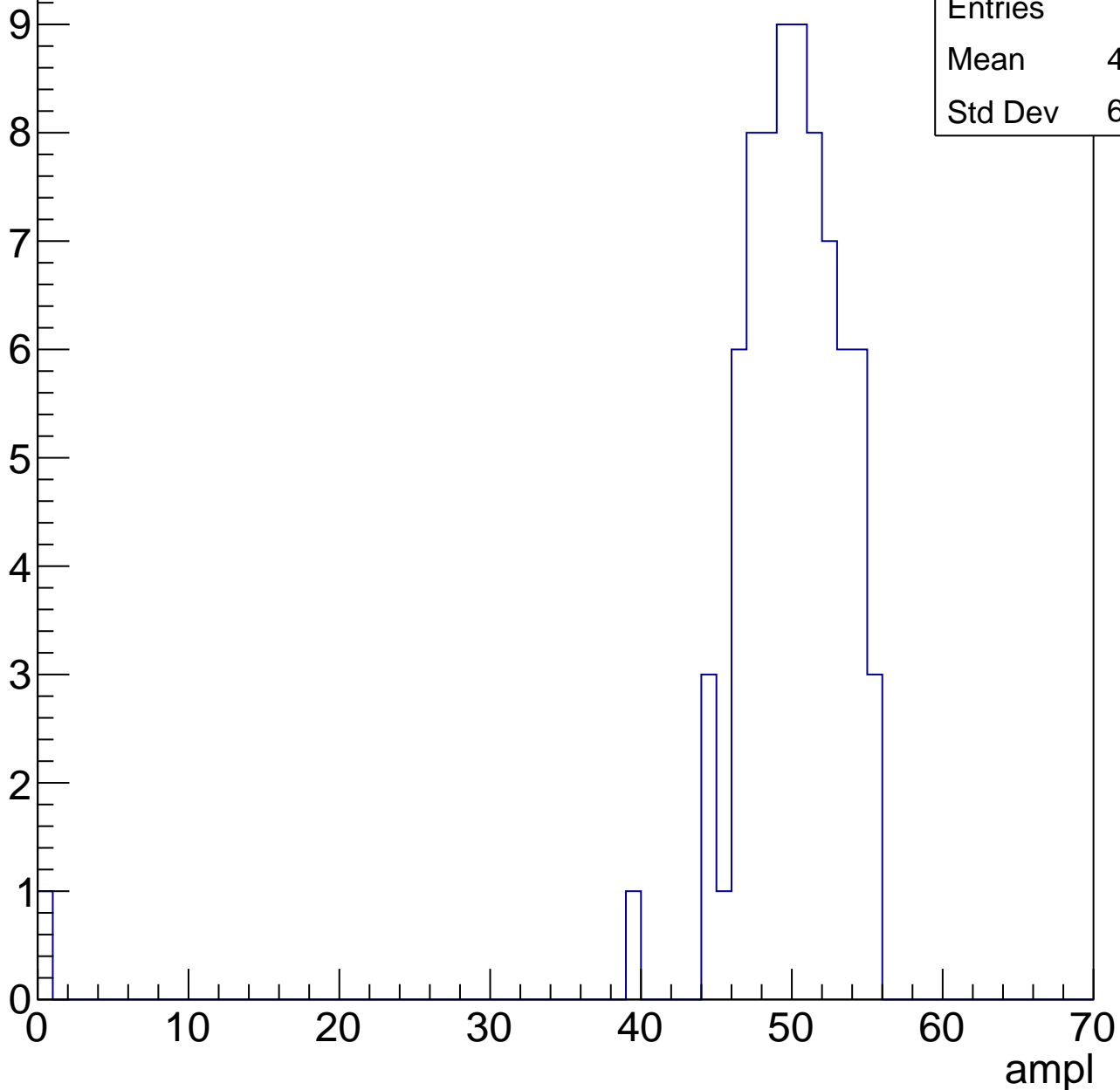


B1L103S, U13-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	48.97
Std Dev	6.432

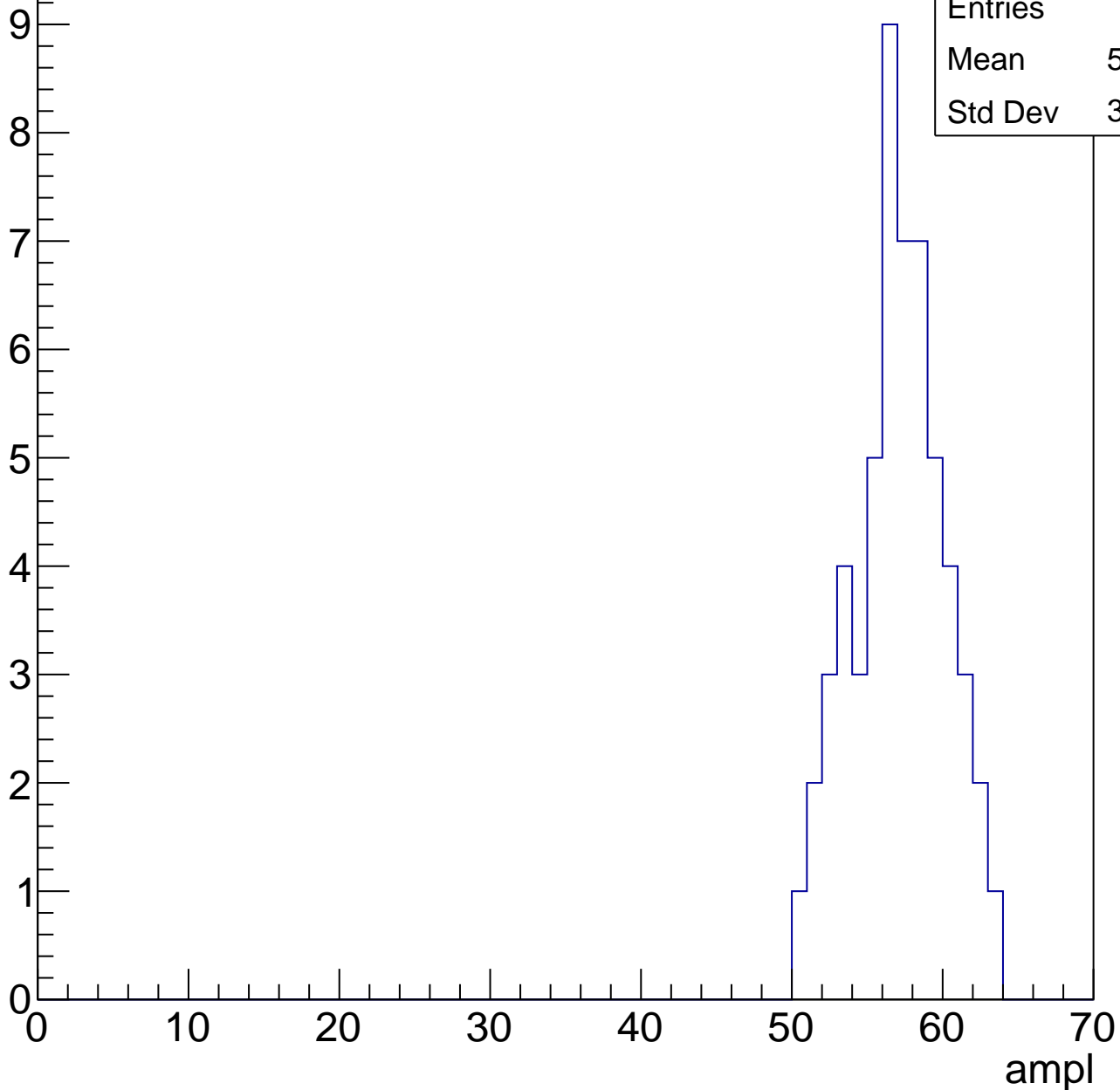


B1L103S, U13-ch28, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	56.62
Std Dev	3.003

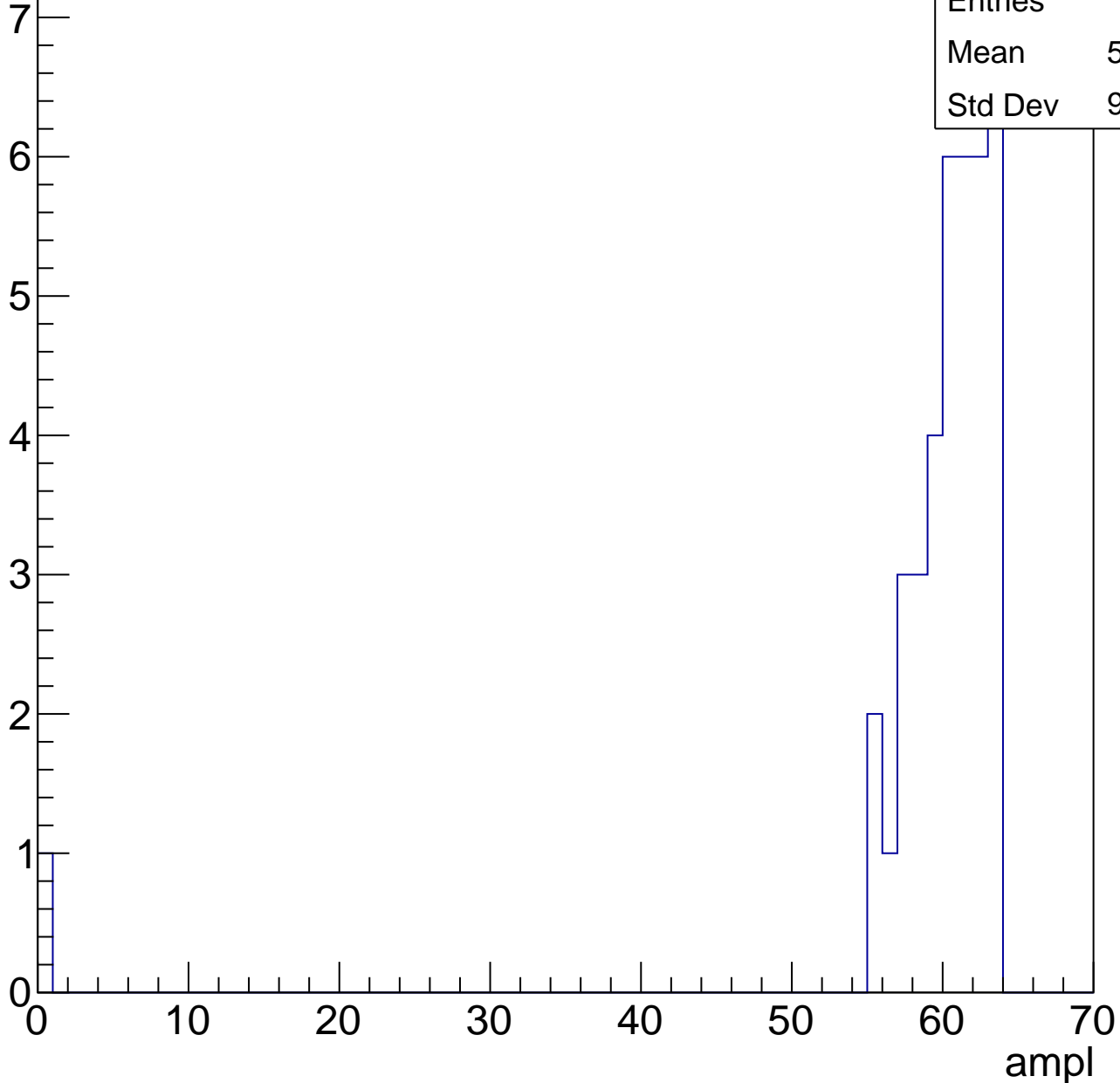


B1L103S, U13-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

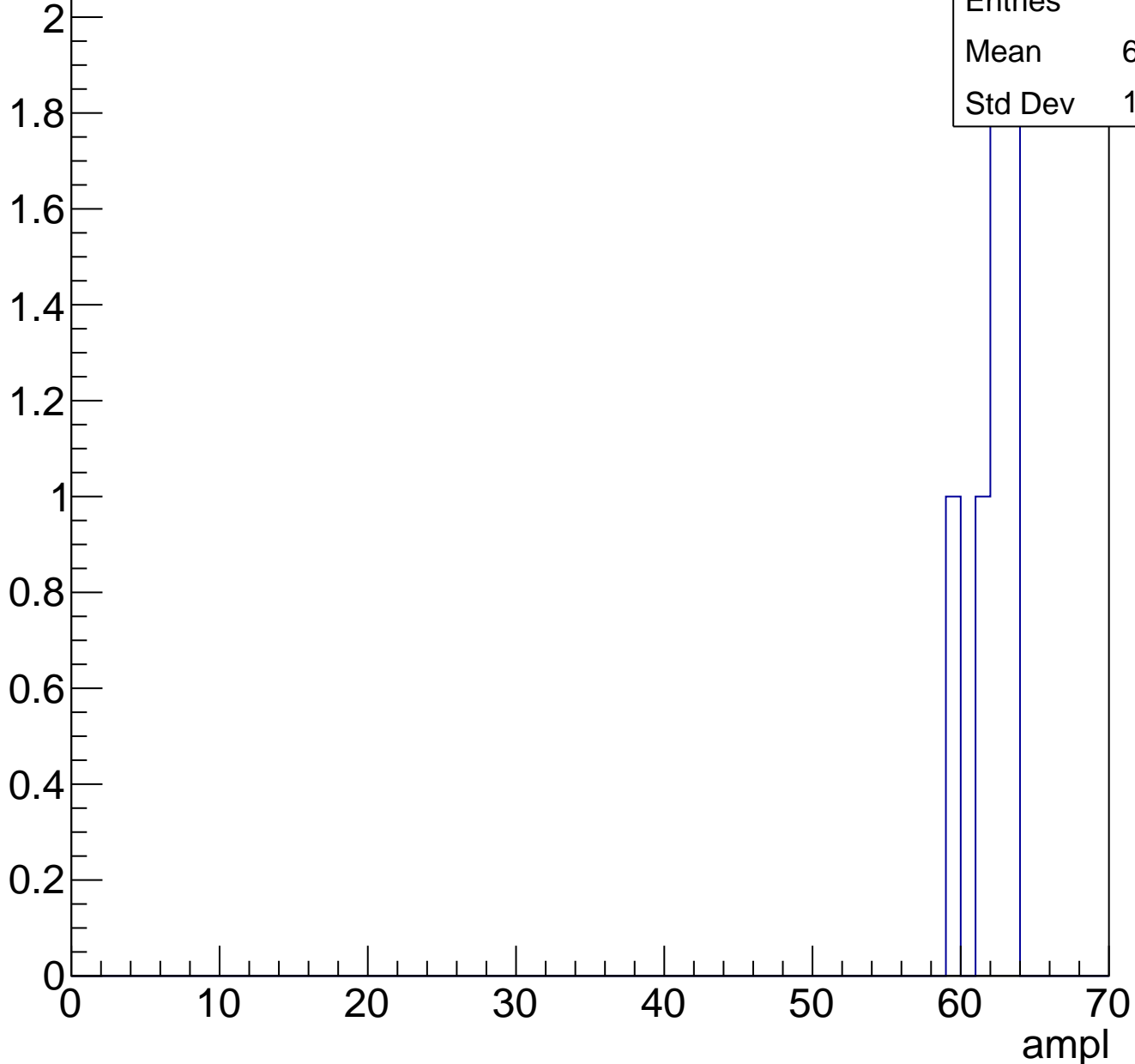
Entries	39
Mean	58.62
Std Dev	9.776



B1L103S, U13-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch29, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	27.05
Std Dev	12.07

Gaus mean : 32.6870

Gaus Width: 3.7801

Entry

12

10

8

6

4

2

0

0

10

20

30

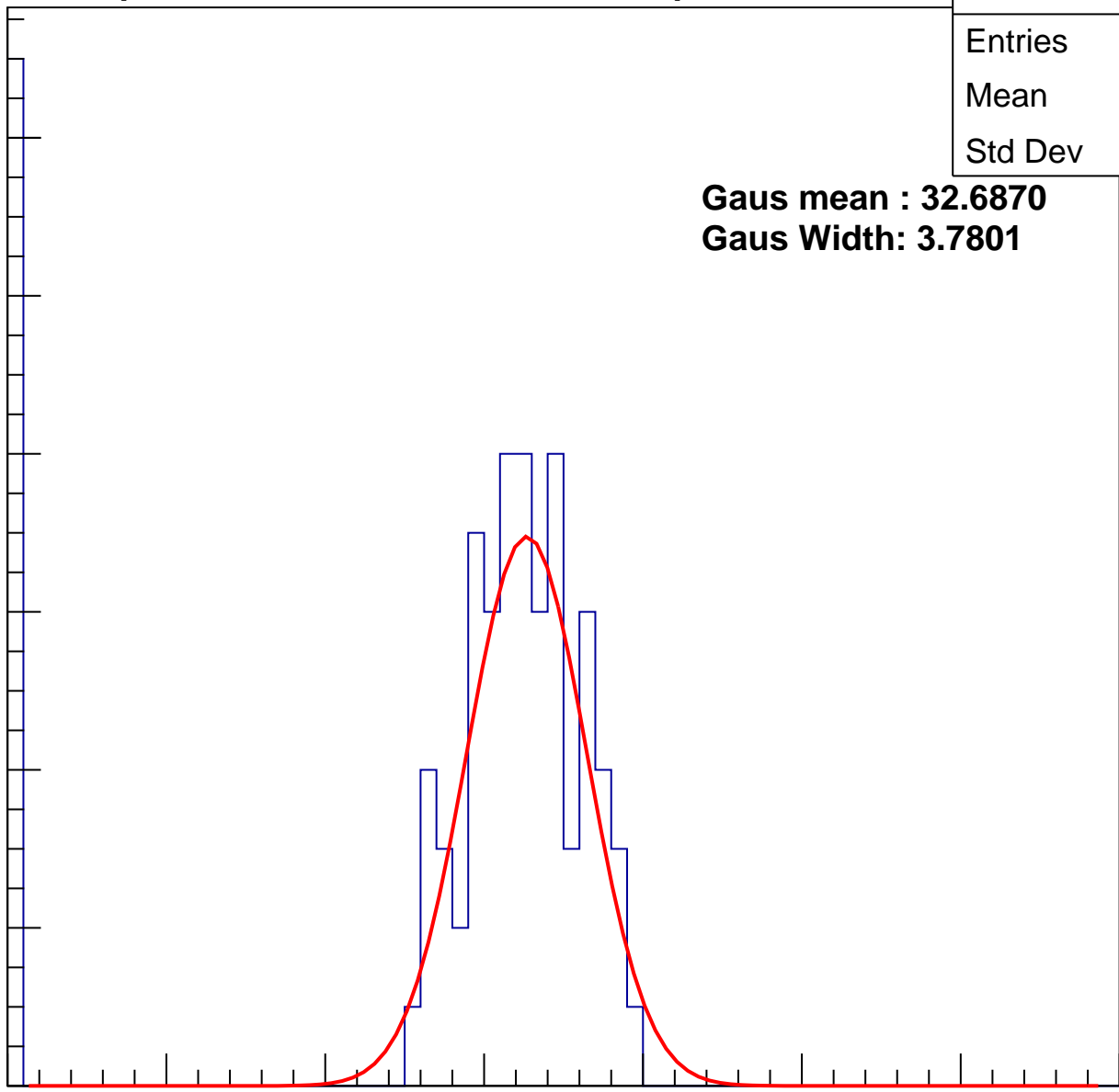
40

50

60

70

ampl



B1L103S, U13-ch29, adc1

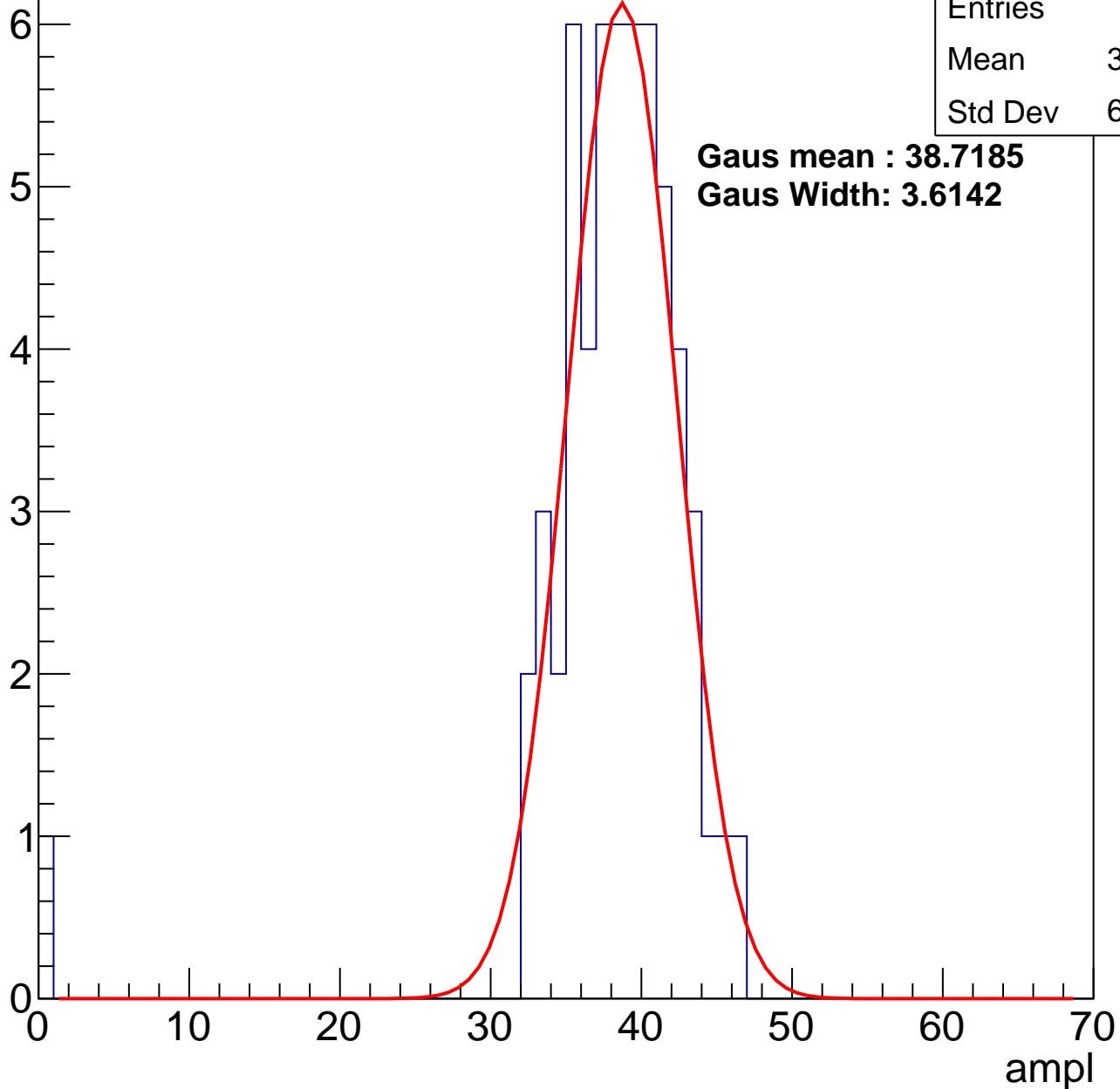
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	37.65
Std Dev	6.004

Gaus mean : 38.7185

Gaus Width: 3.6142



B1L103S, U13-ch29, adc2

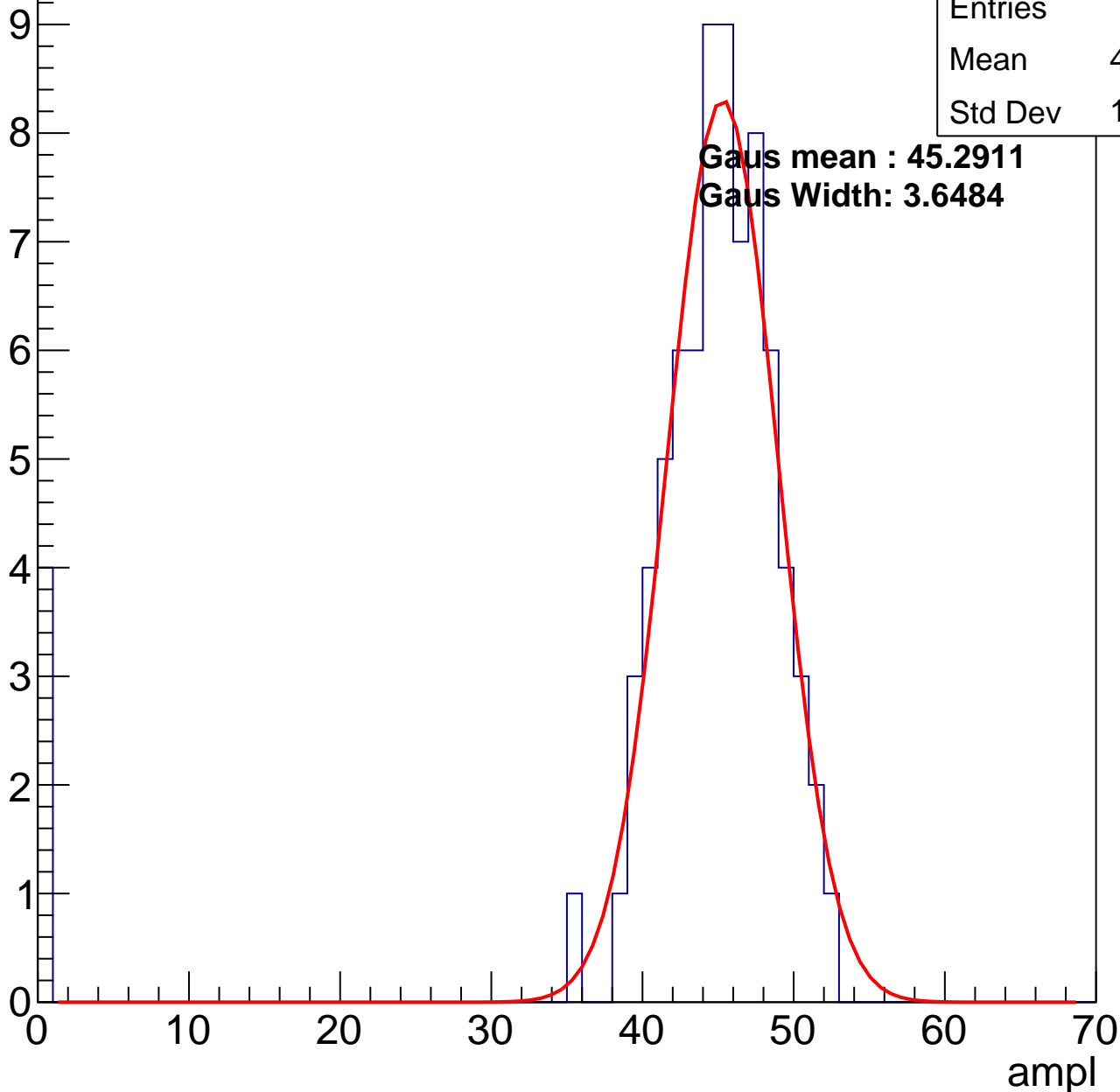
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	42.43
Std Dev	10.35

Gaus mean : 45.2911

Gaus Width: 3.6484

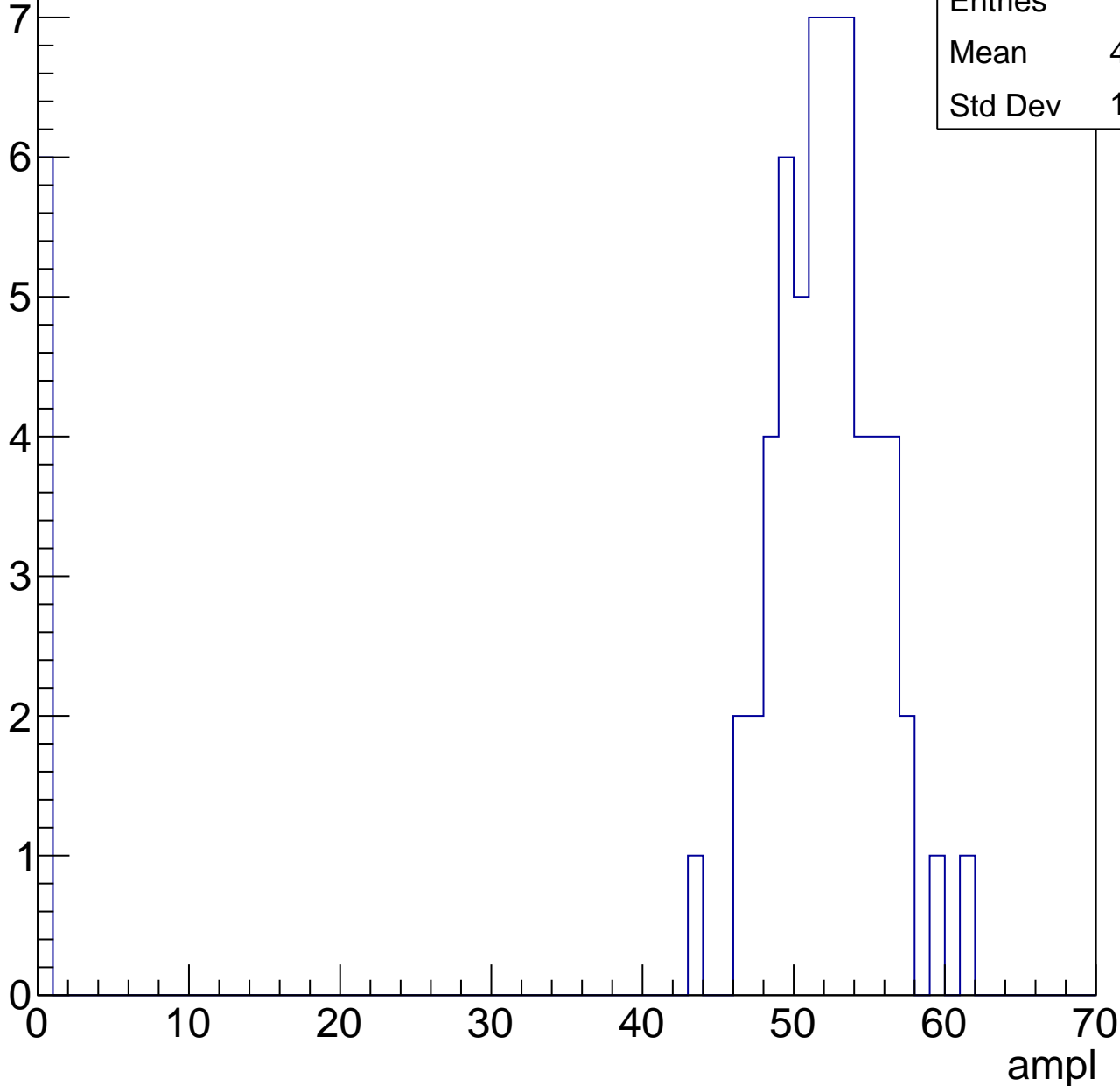


B1L103S, U13-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	46.84
Std Dev	15.53

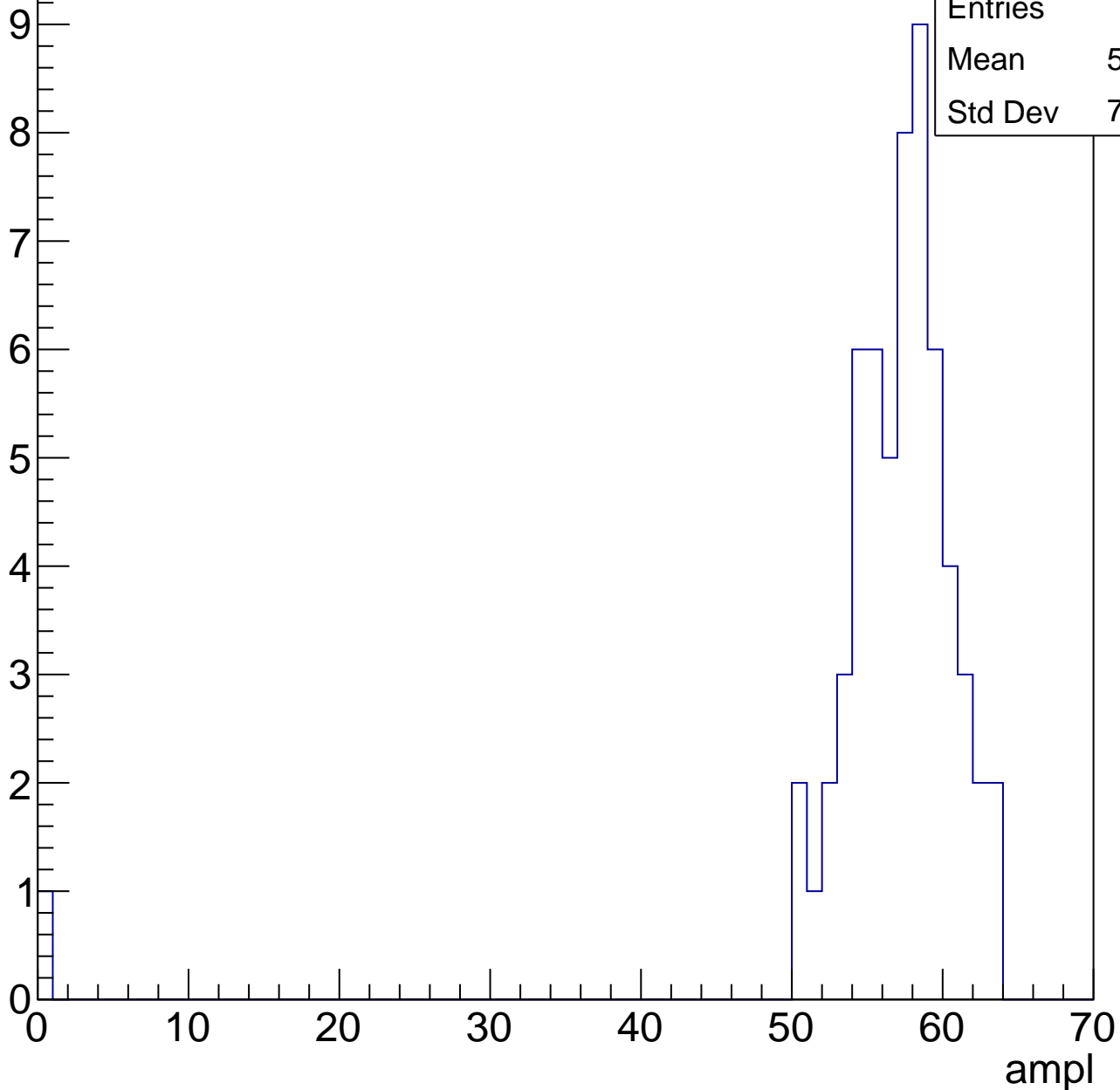


B1L103S, U13-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.88
Std Dev	7.883

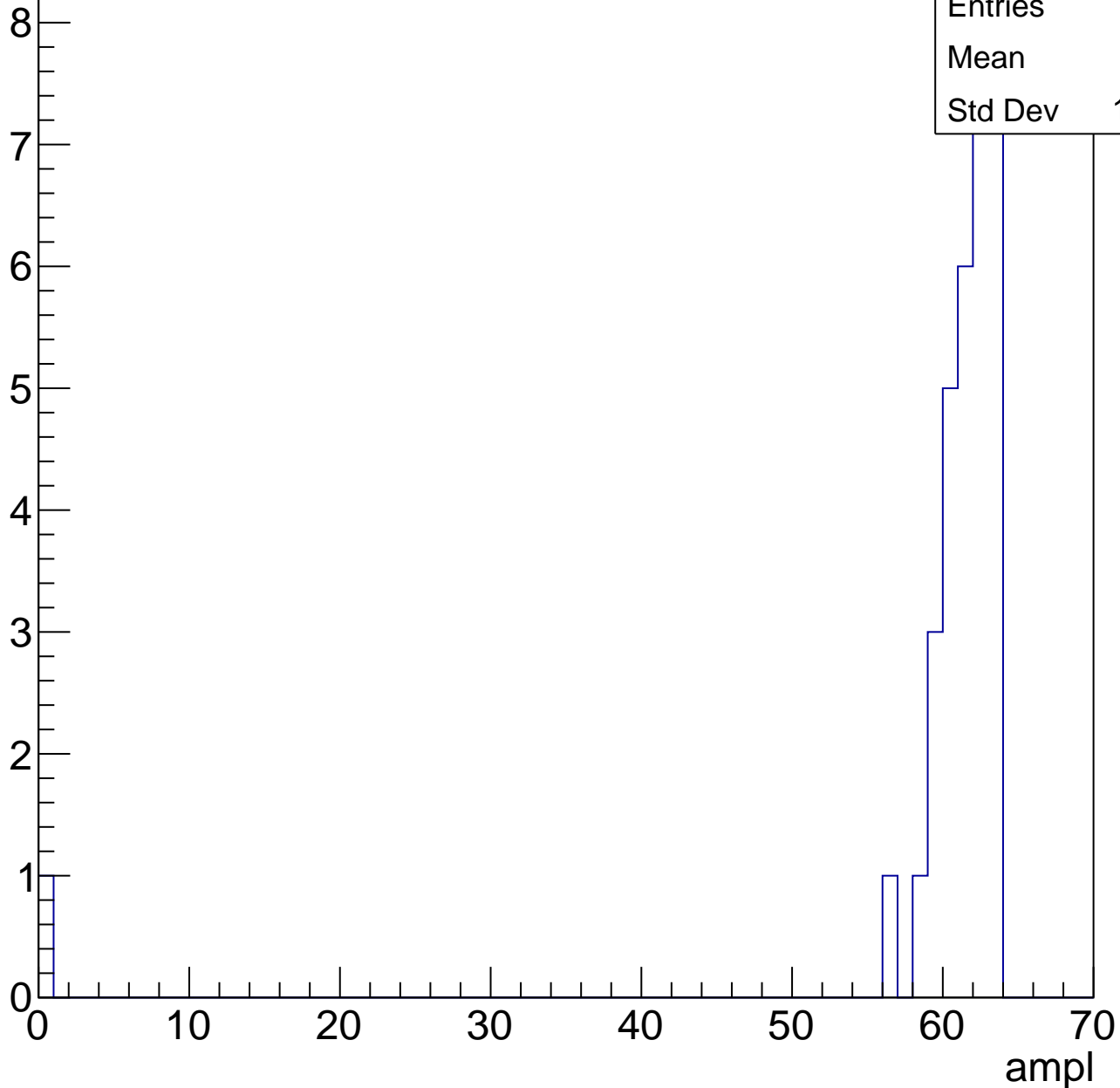


B1L103S, U13-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	59.3
Std Dev	10.61



B1L103S, U13-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	1
Mean	60
Std Dev	0

B1L103S, U13-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch30, adc0

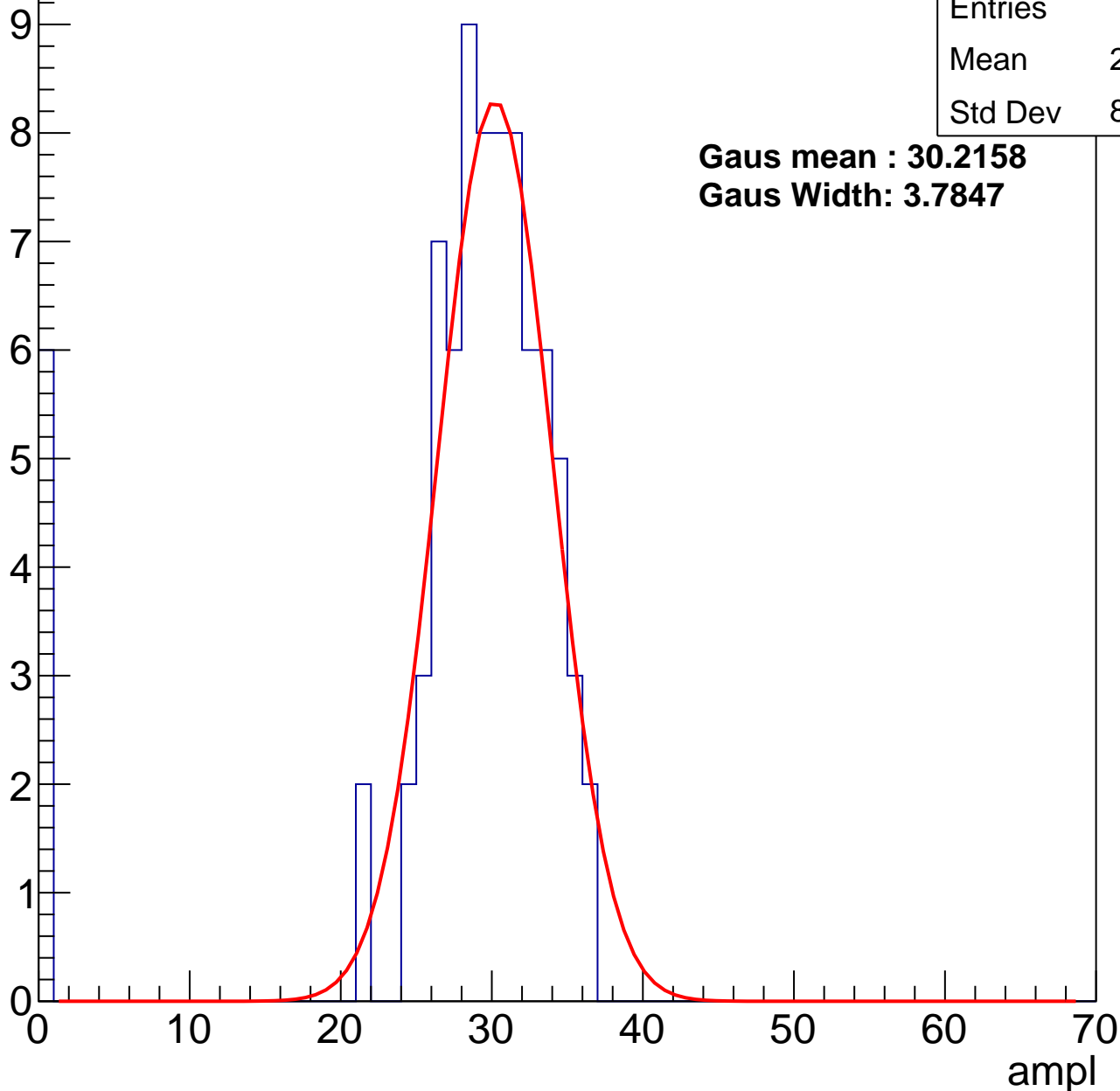
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	27.38
Std Dev	8.372

Gaus mean : 30.2158

Gaus Width: 3.7847



B1L103S, U13-ch30, adc1

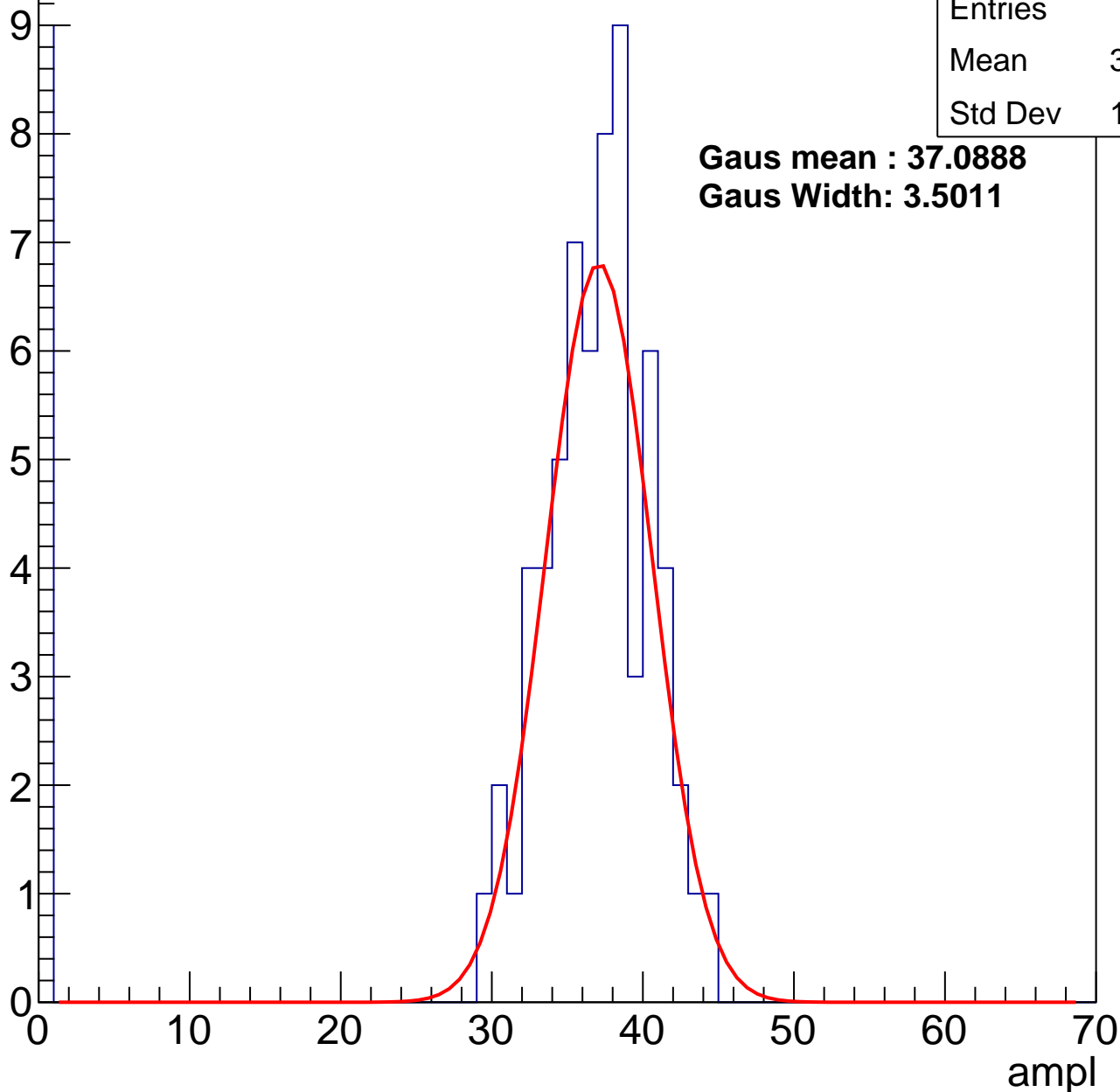
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.07
Std Dev	12.42

Gaus mean : 37.0888

Gaus Width: 3.5011



B1L103S, U13-ch30, adc2

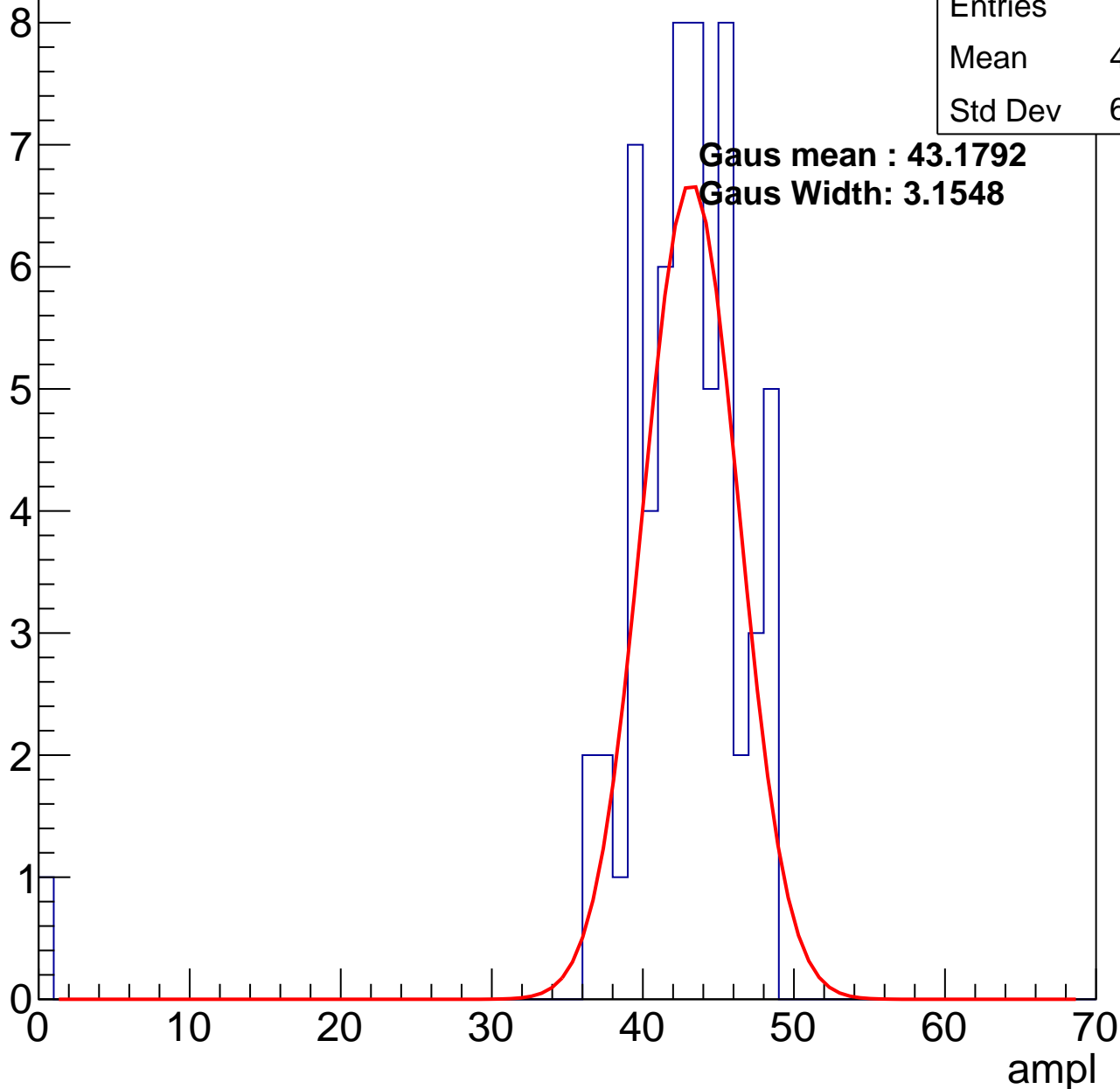
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	41.87
Std Dev	6.187

Gaus mean : 43.1792

Gaus Width: 3.1548



B1L103S, U13-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

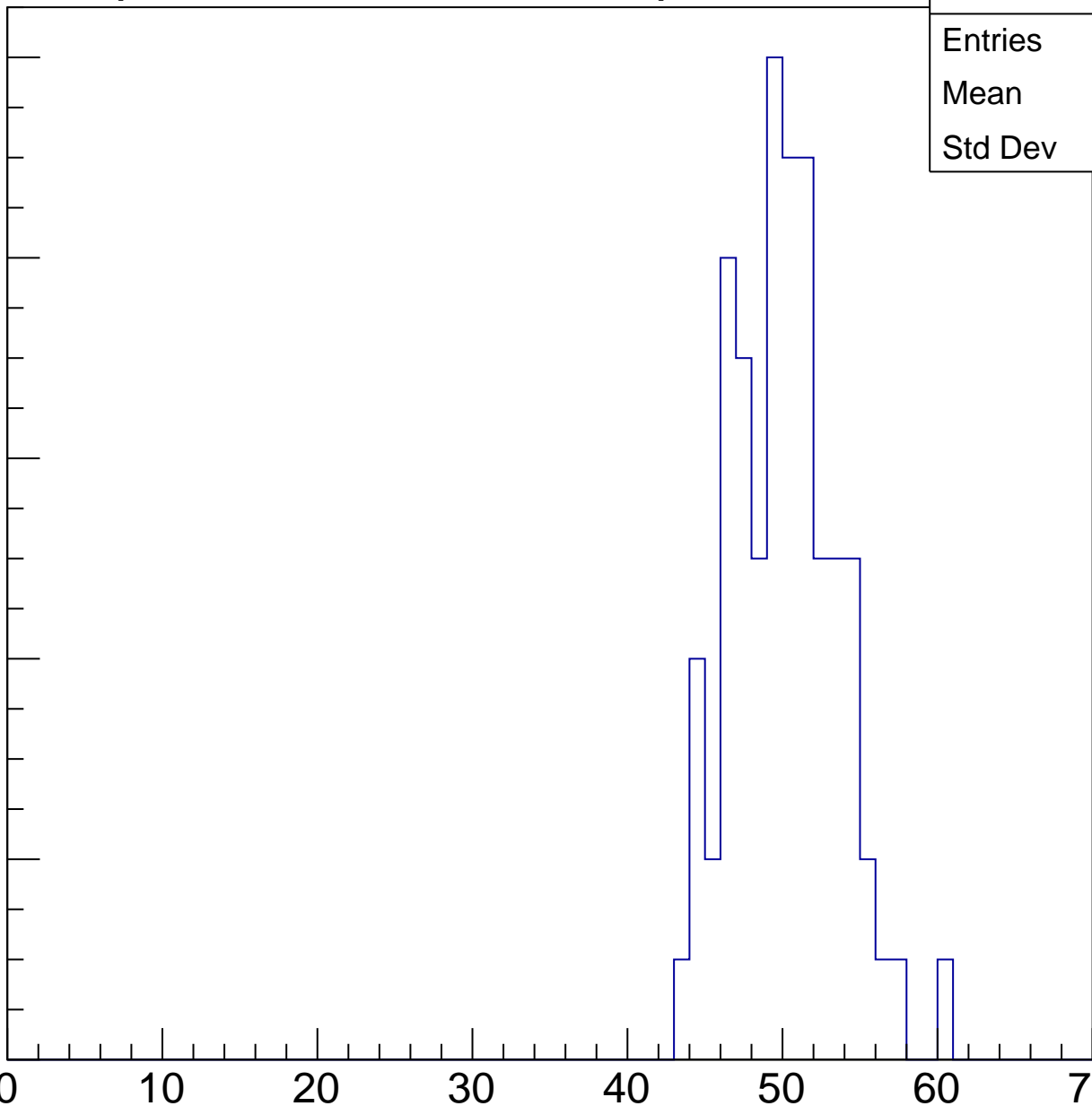
Entries	75
Mean	49.64
Std Dev	3.353

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

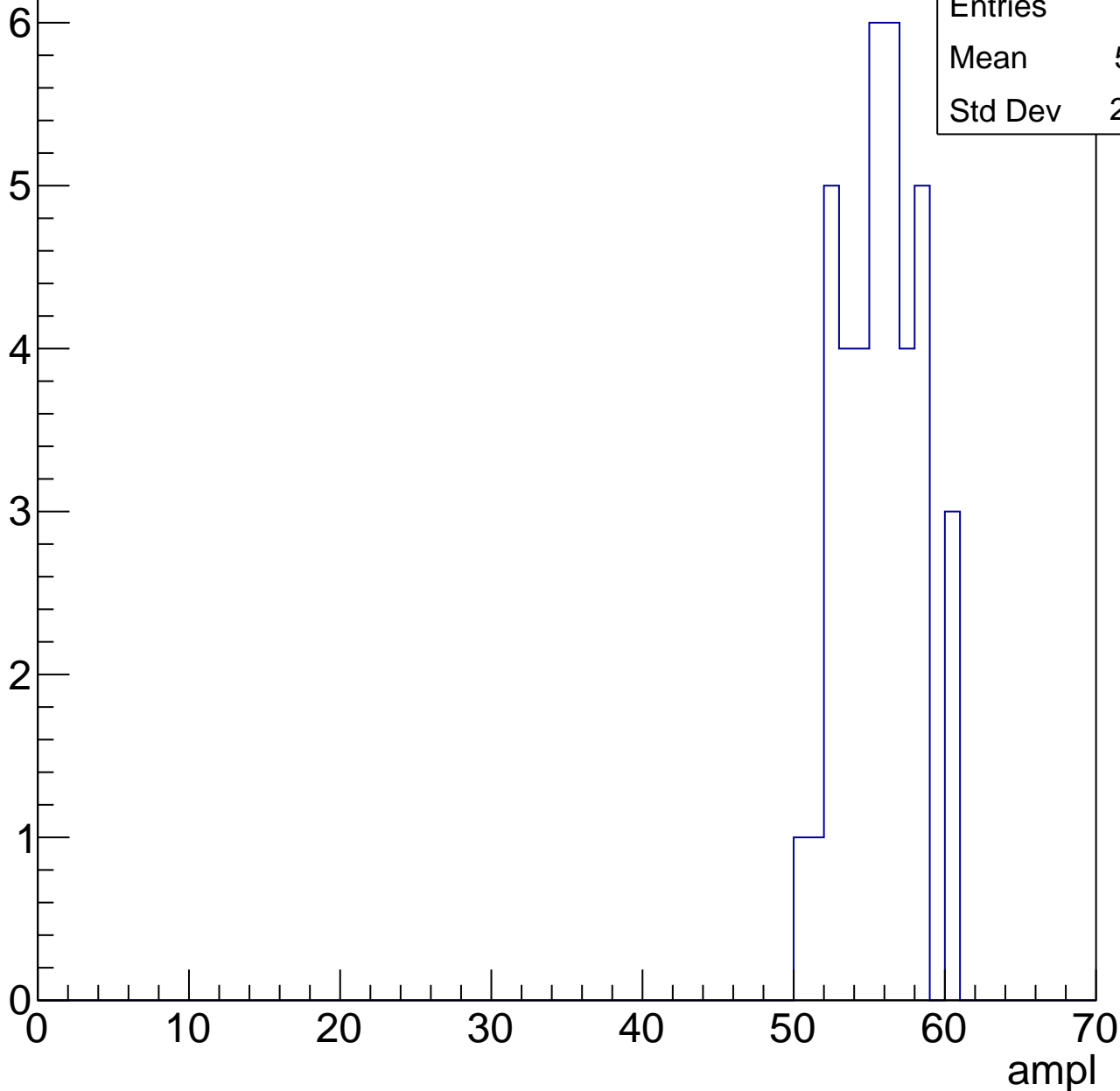


B1L103S, U13-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	55.21
Std Dev	2.513

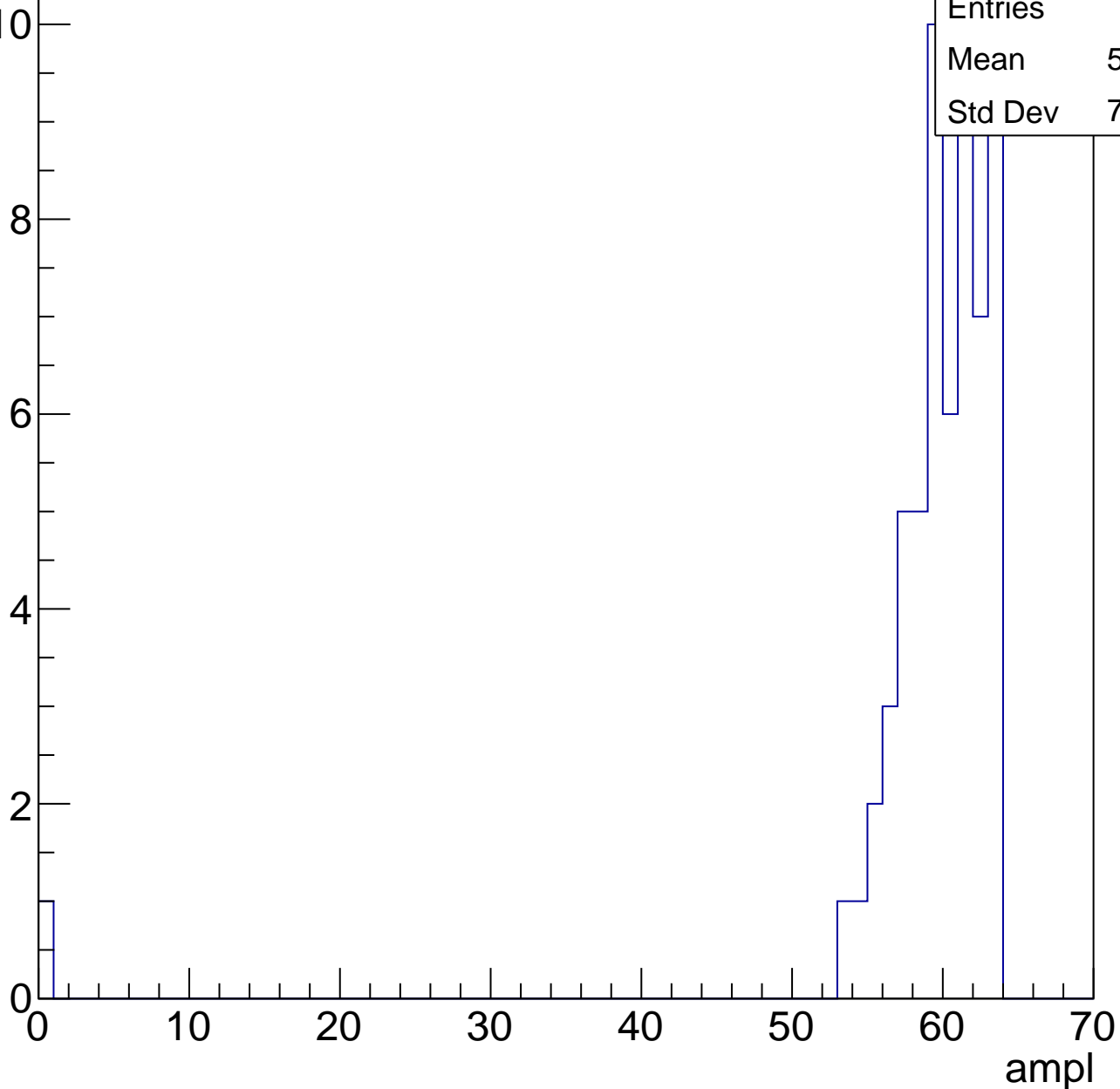


B1L103S, U13-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	58.75
Std Dev	7.987



B1L103S, U13-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	3
Mean	62
Std Dev	0

ampl

B1L103S, U13-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

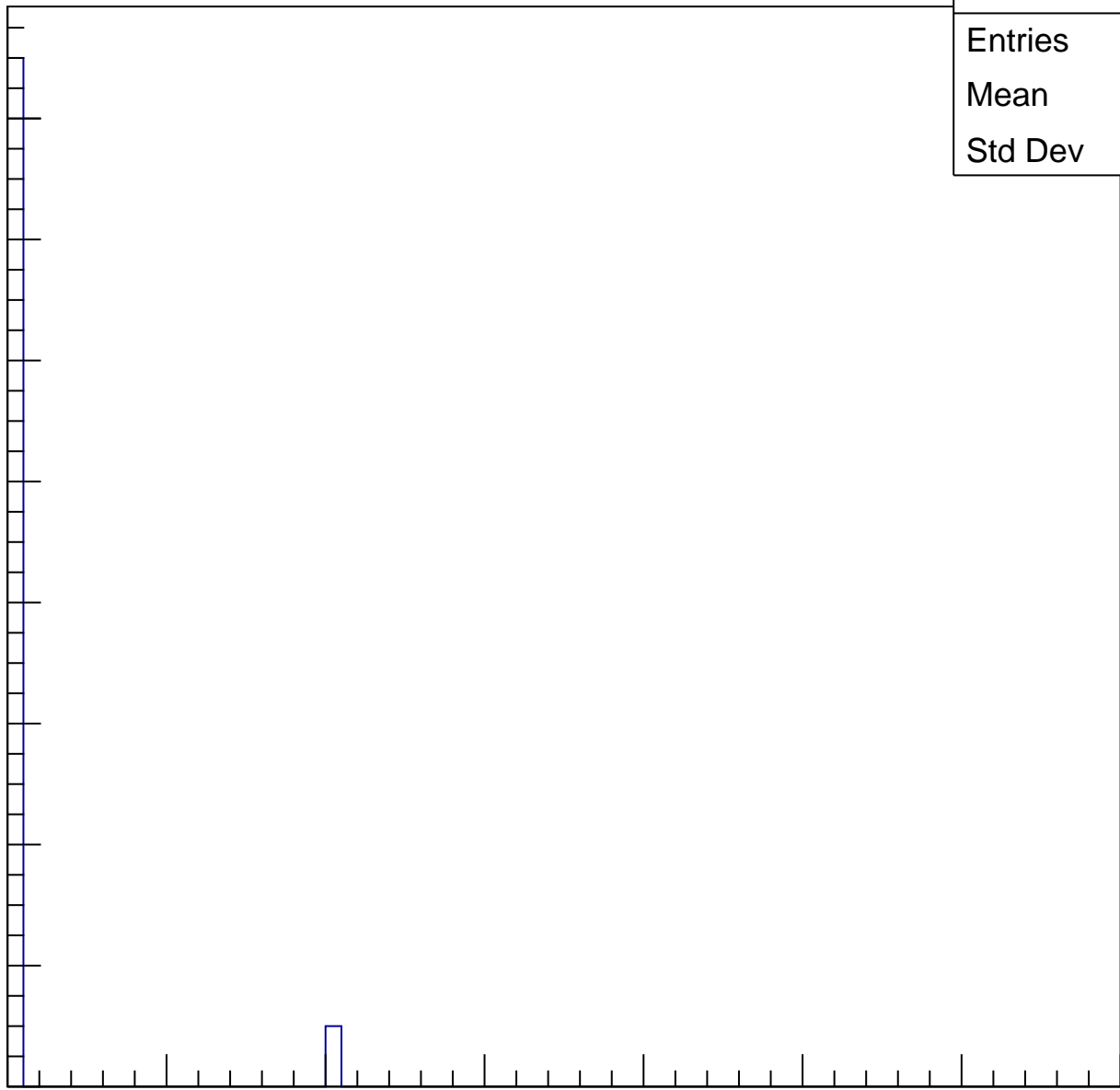
Entries	18
Mean	1.111
Std Dev	4.581

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch31, adc0

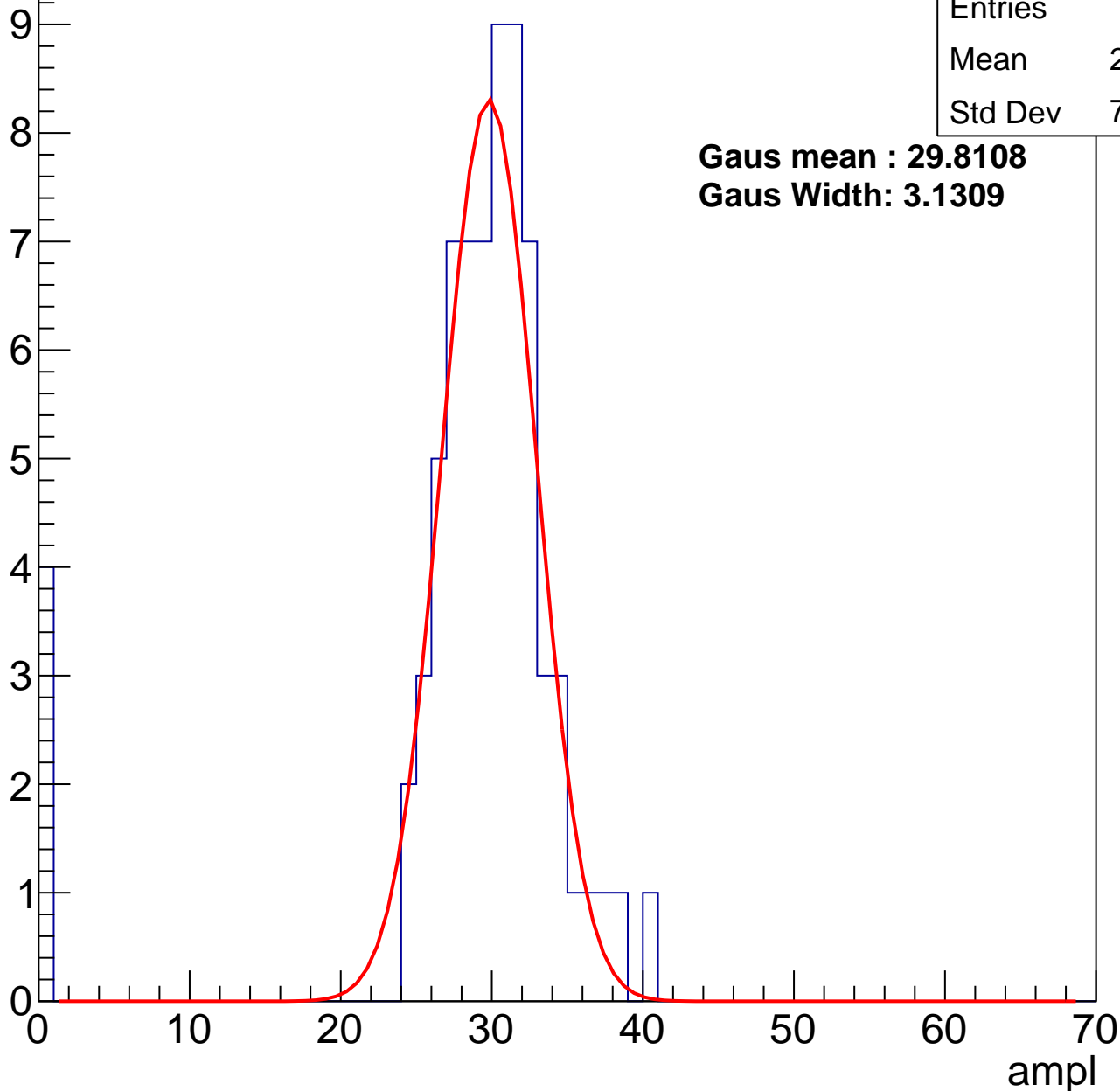
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	28.18
Std Dev	7.577

Gaus mean : 29.8108

Gaus Width: 3.1309



B1L103S, U13-ch31, adc1

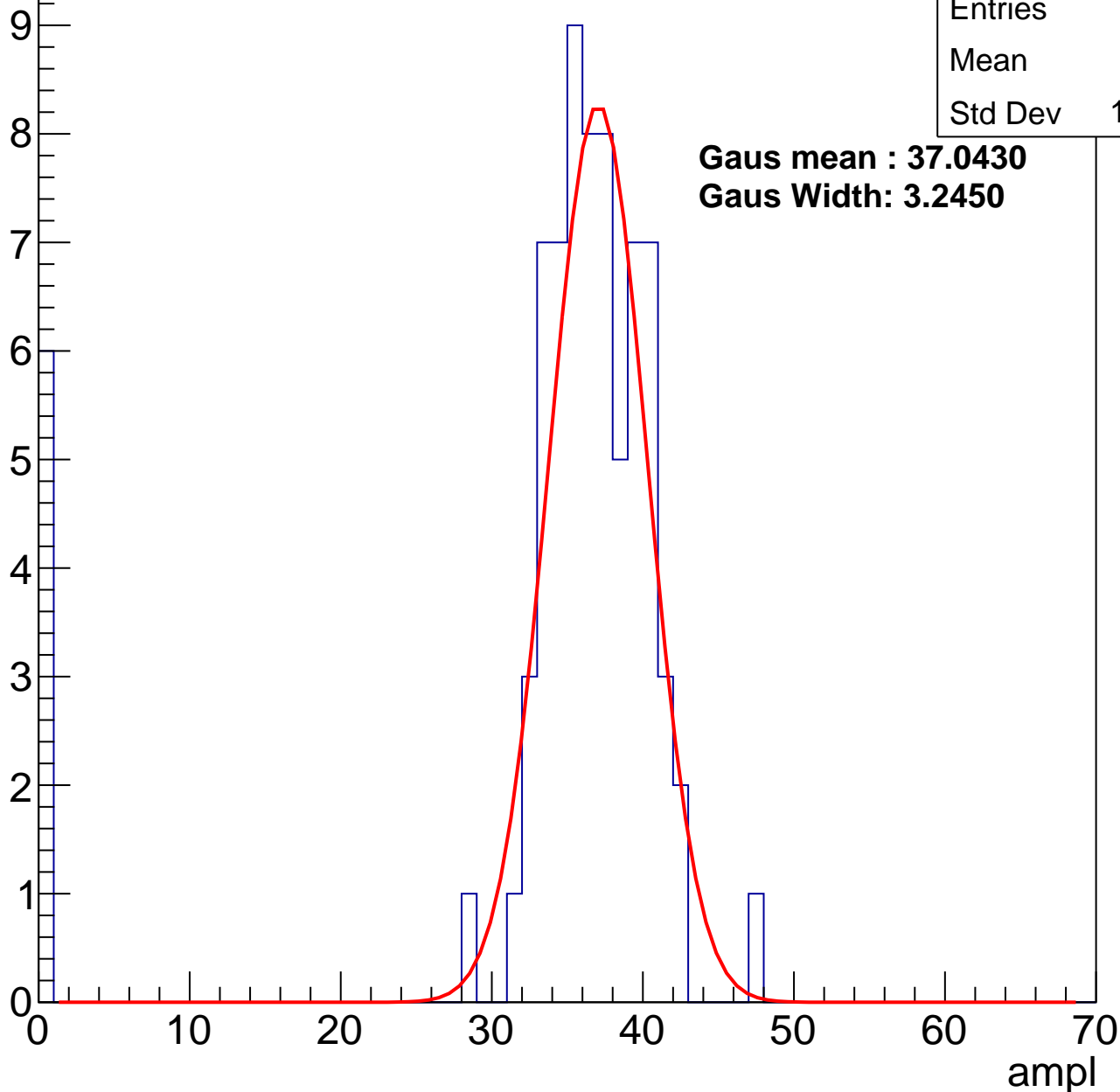
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	33.6
Std Dev	10.36

Gaus mean : 37.0430

Gaus Width: 3.2450



B1L103S, U13-ch31, adc2

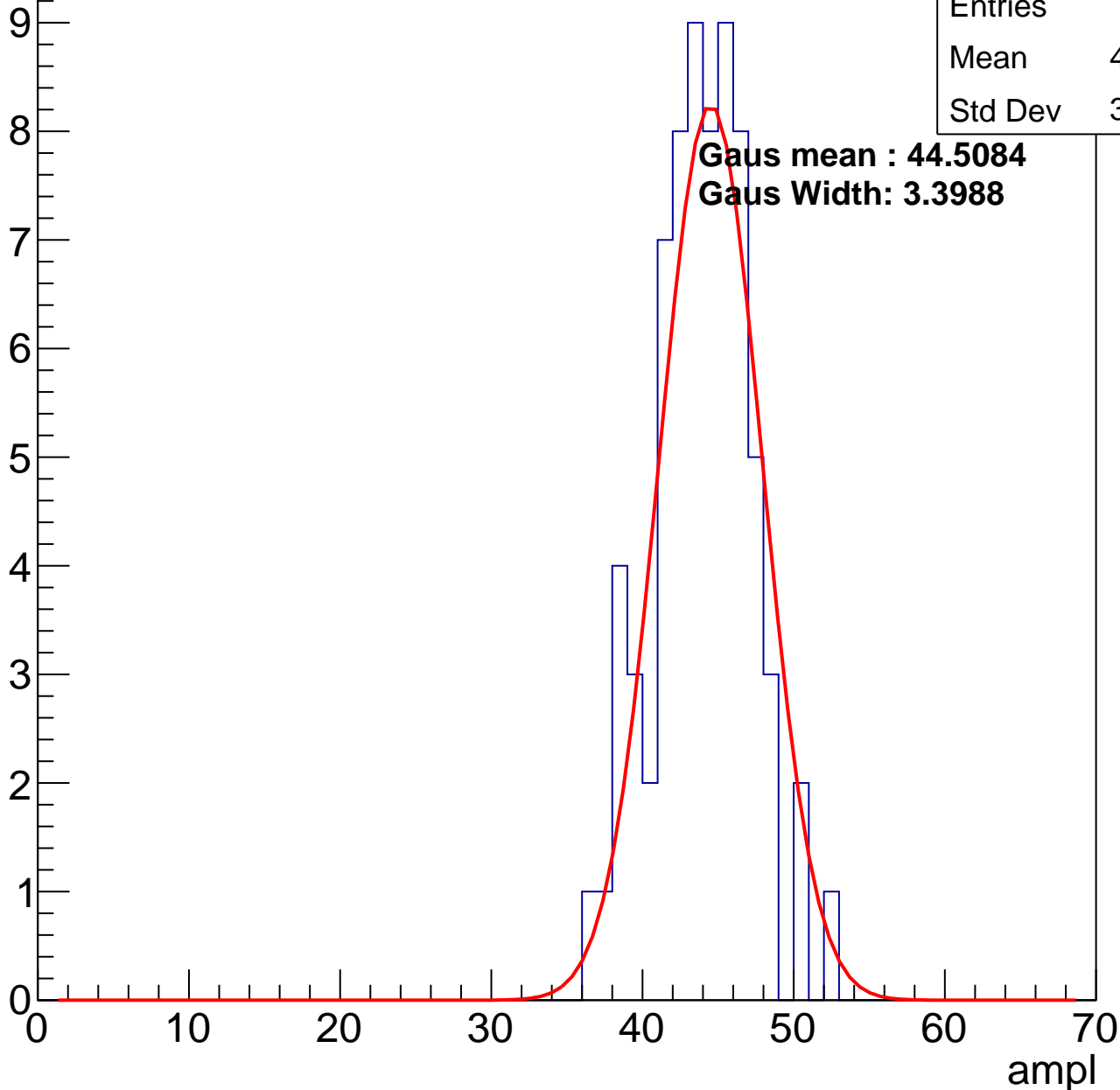
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	43.49
Std Dev	3.184

Gaus mean : 44.5084

Gaus Width: 3.3988

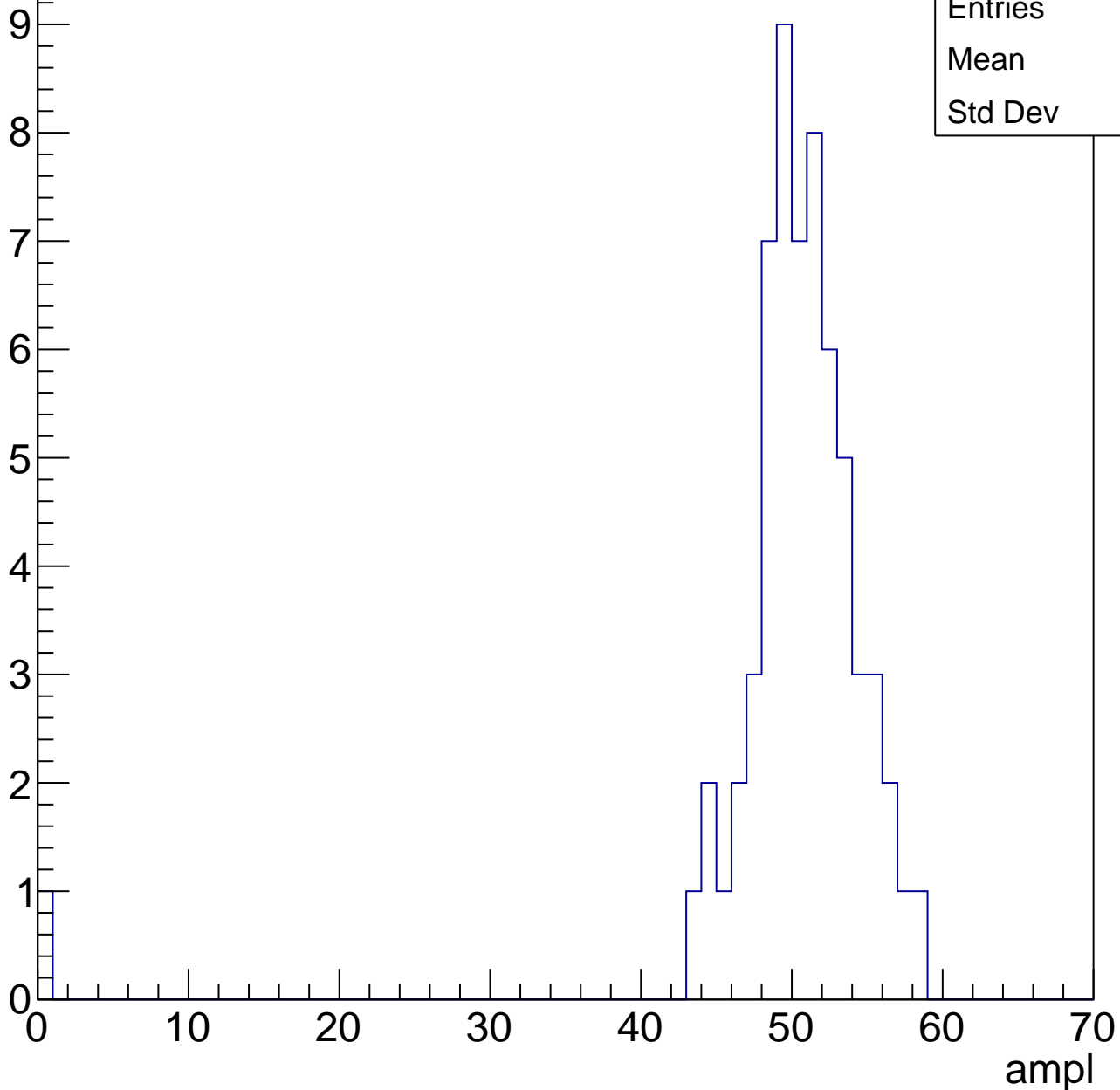


B1L103S, U13-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	49.6
Std Dev	7.09

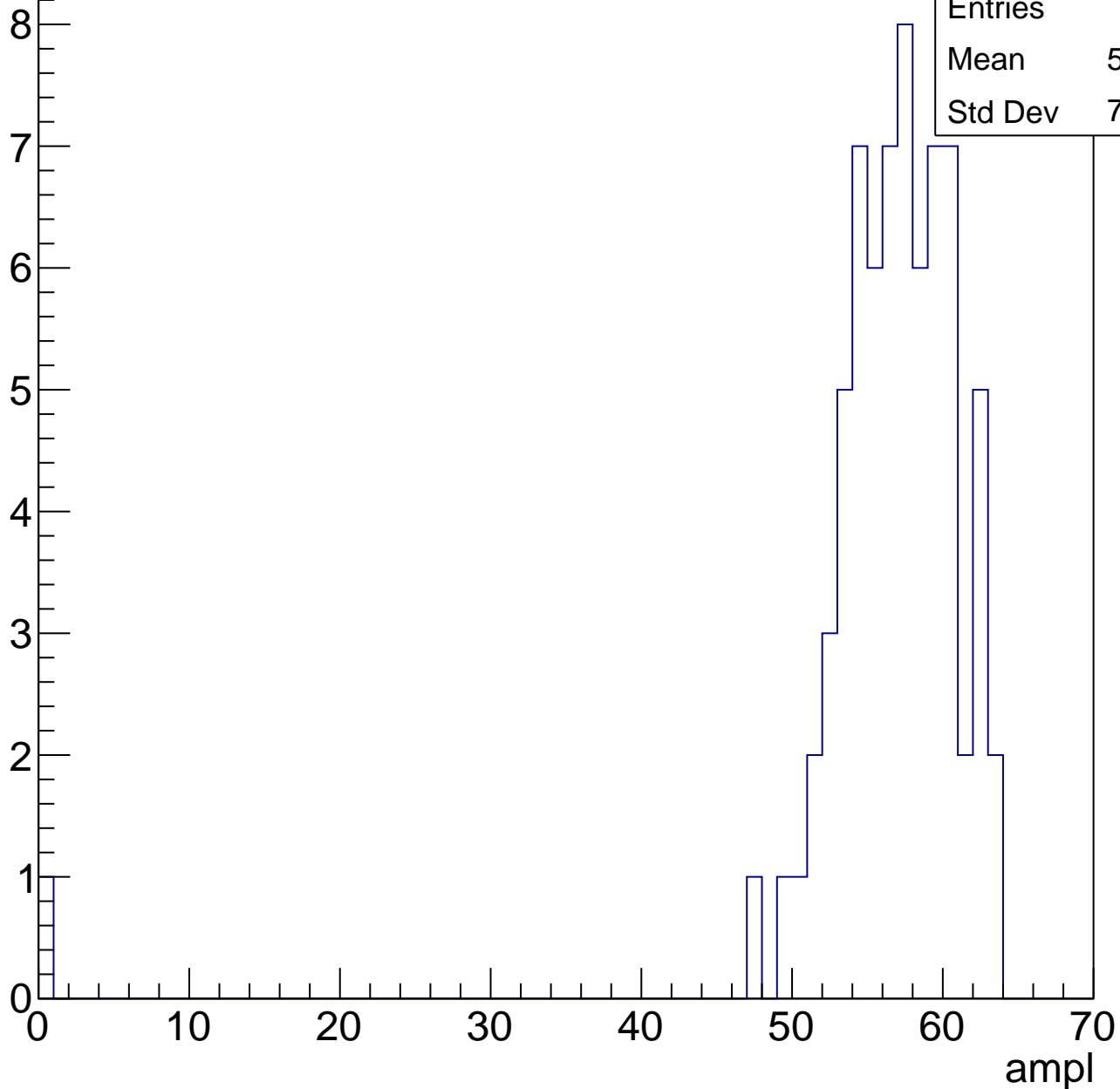


B1L103S, U13-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	55.83
Std Dev	7.517

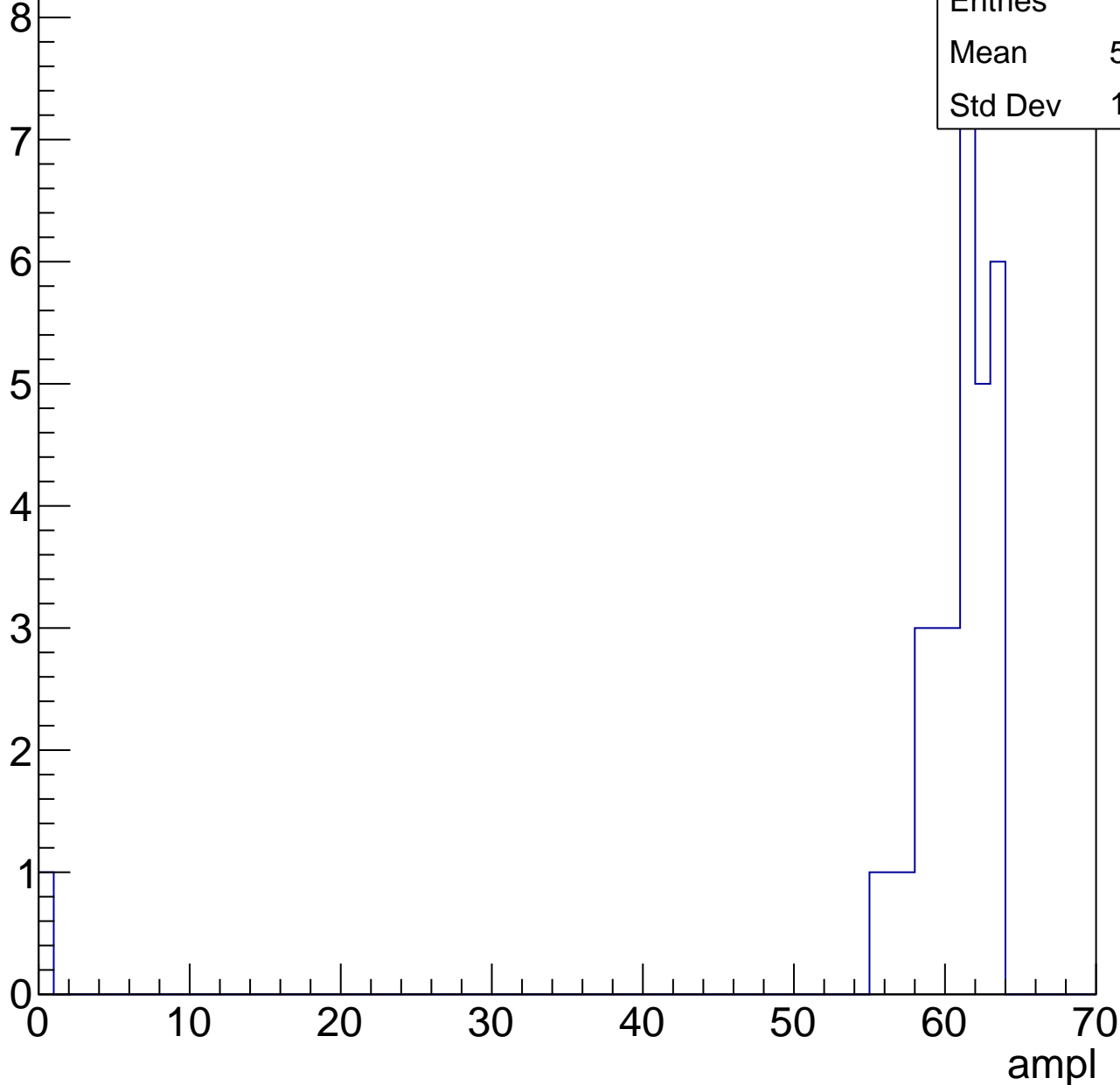


B1L103S, U13-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.59
Std Dev	10.73



B1L103S, U13-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

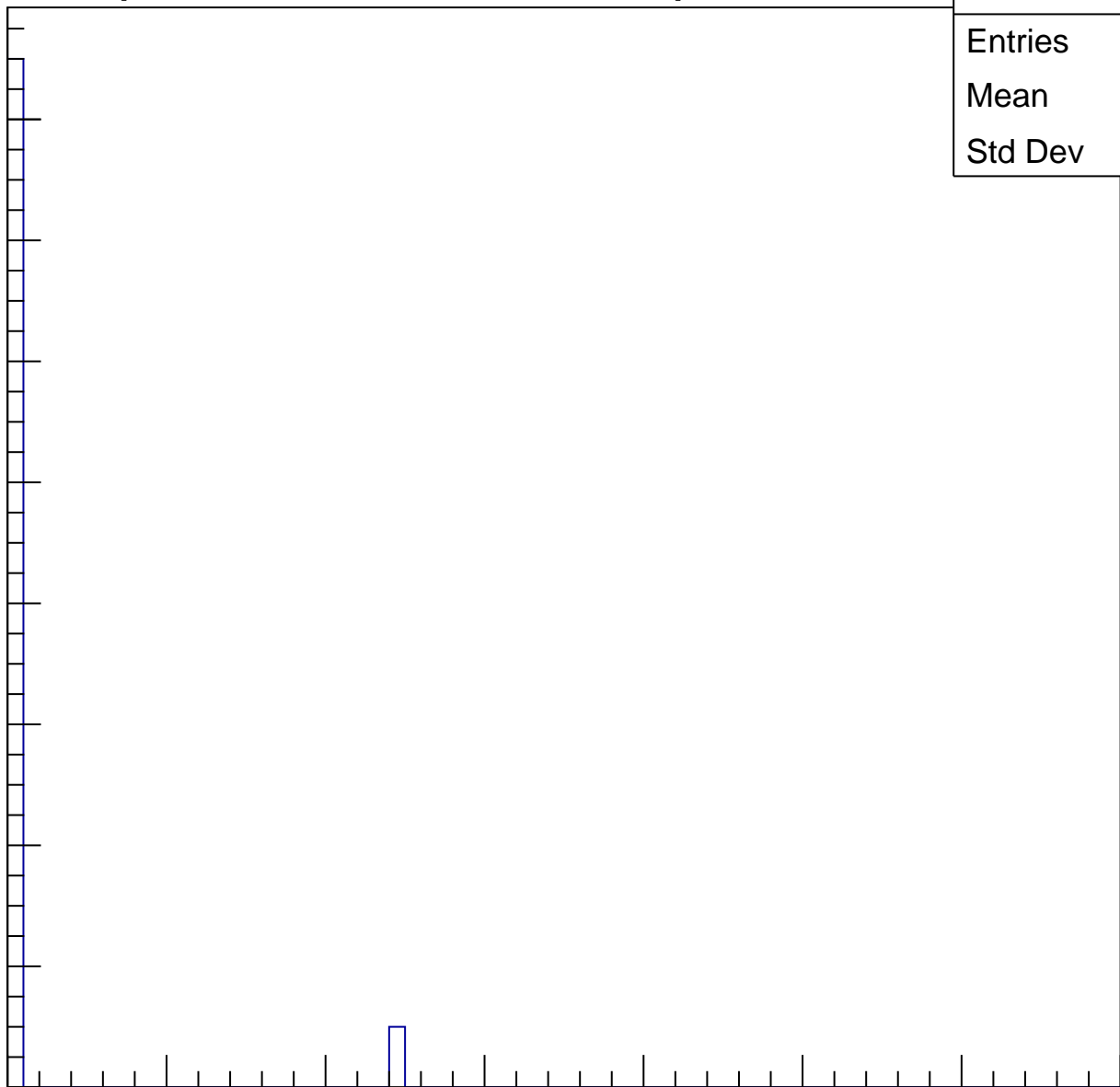
Entries	18
Mean	1.333
Std Dev	5.497

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch32, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	24.23
Std Dev	12.45

Gaus mean : 30.8929

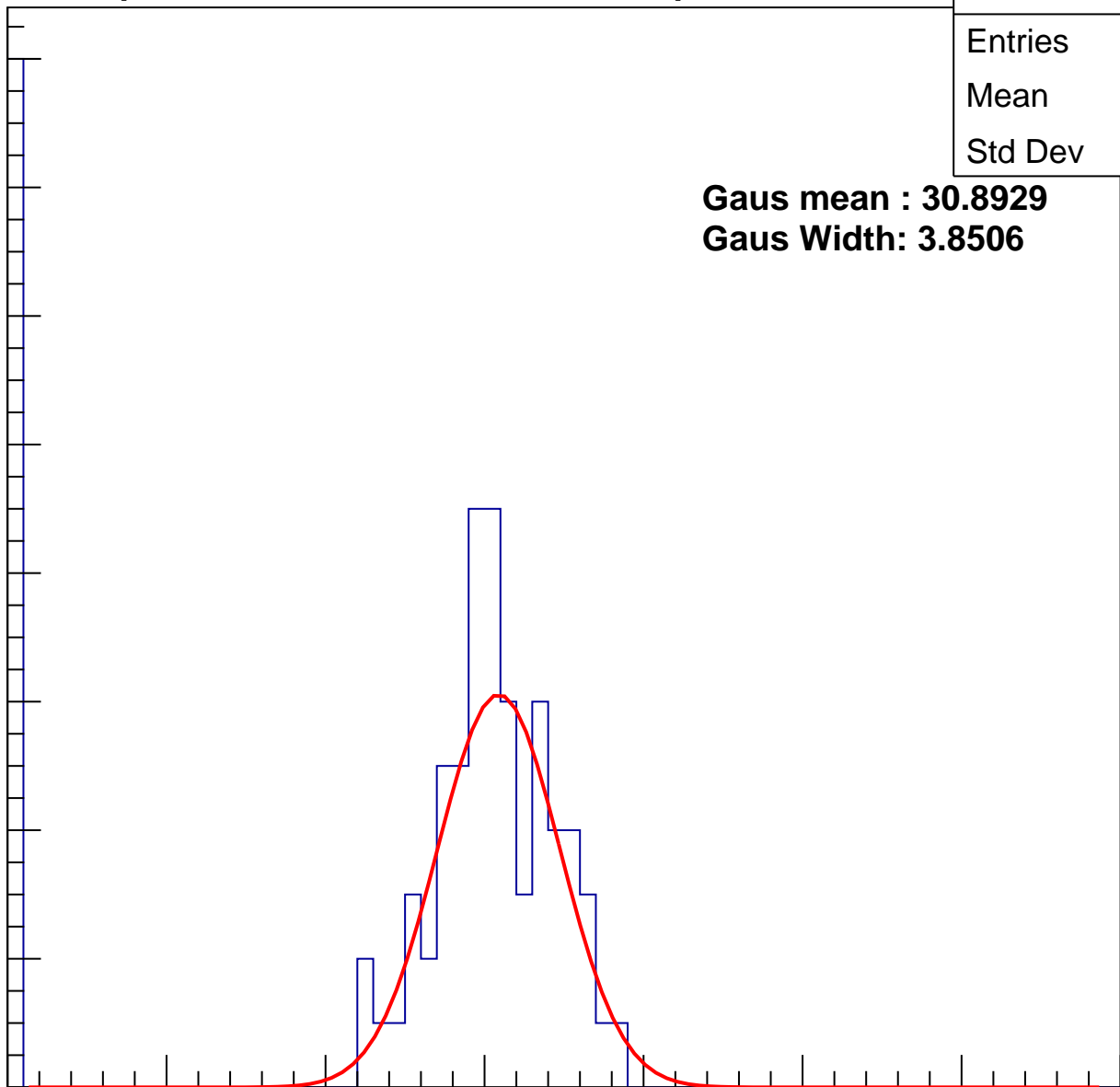
Gaus Width: 3.8506

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch32, adc1

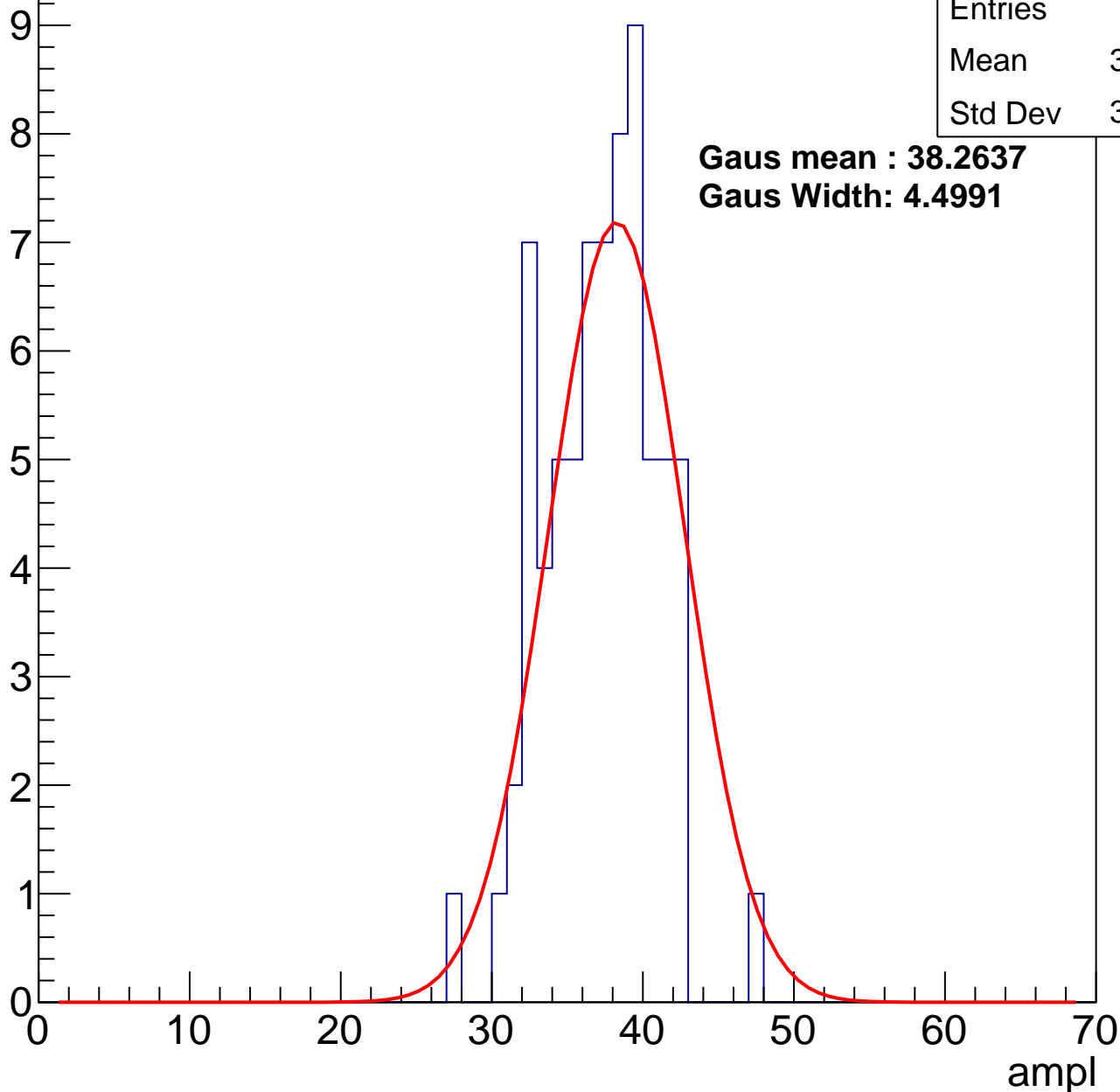
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	36.78
Std Dev	3.579

Gaus mean : 38.2637

Gaus Width: 4.4991



B1L103S, U13-ch32, adc2

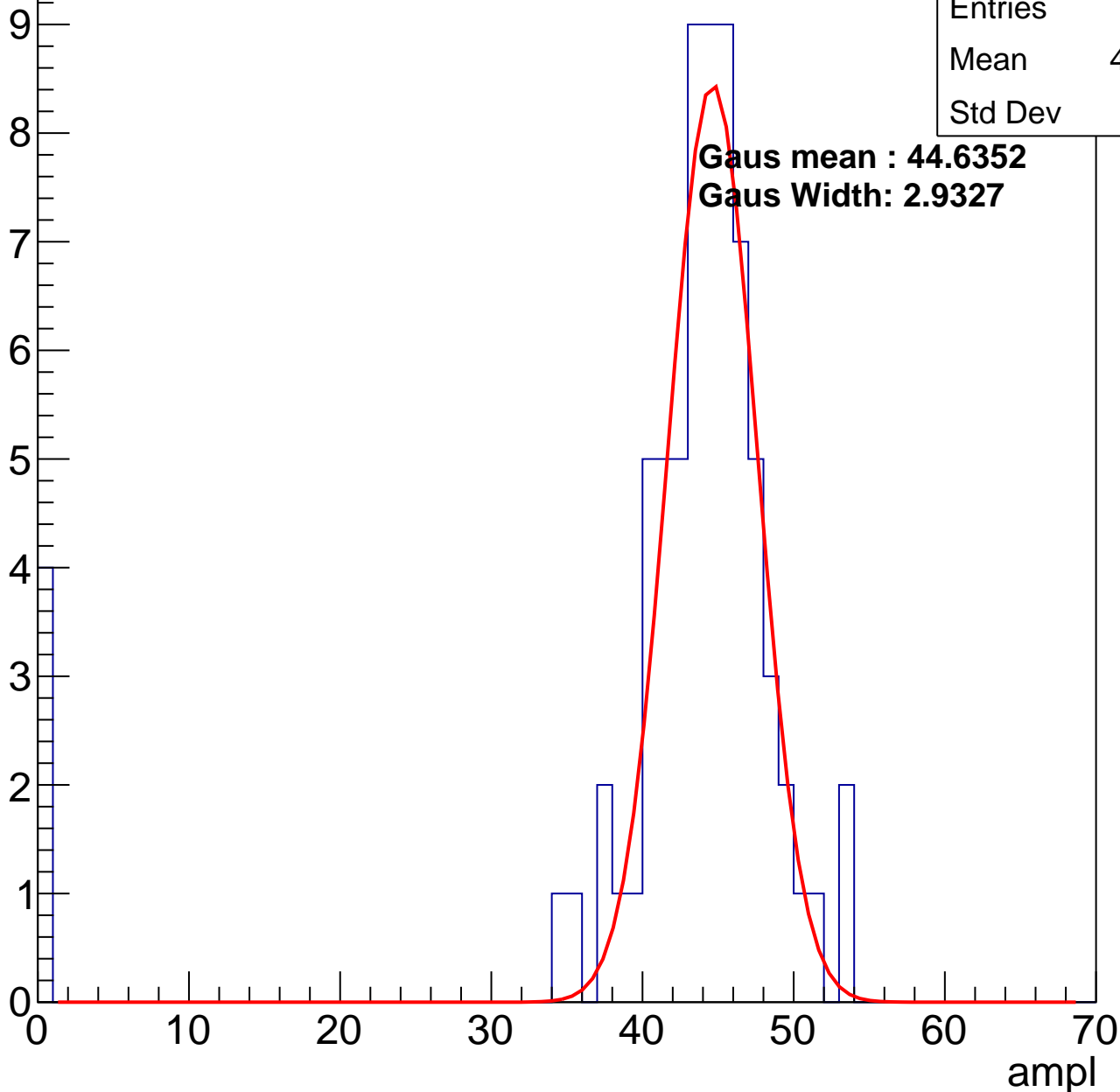
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	41.49
Std Dev	10.6

Gaus mean : 44.6352

Gaus Width: 2.9327

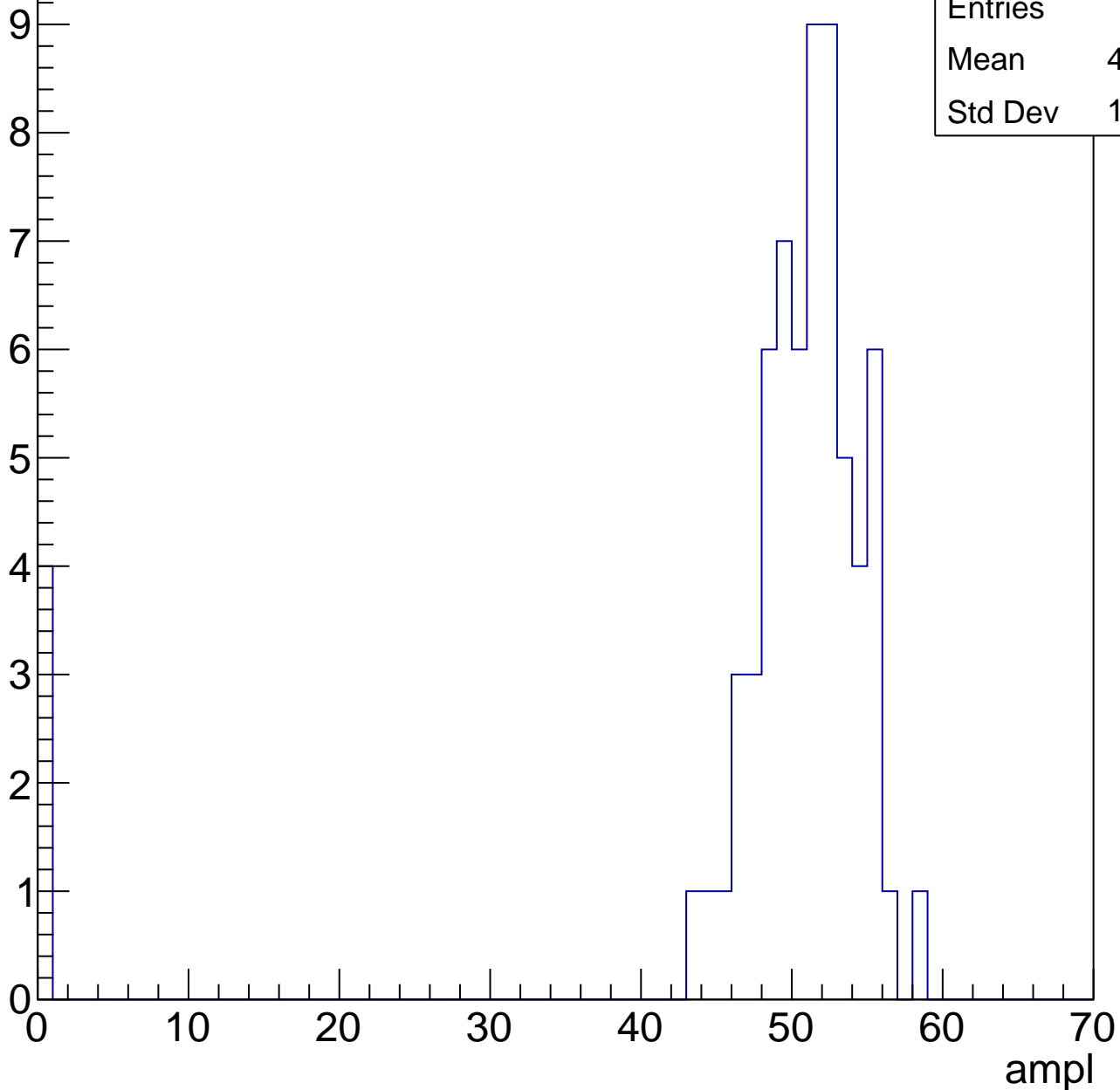


B1L103S, U13-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.67
Std Dev	12.37

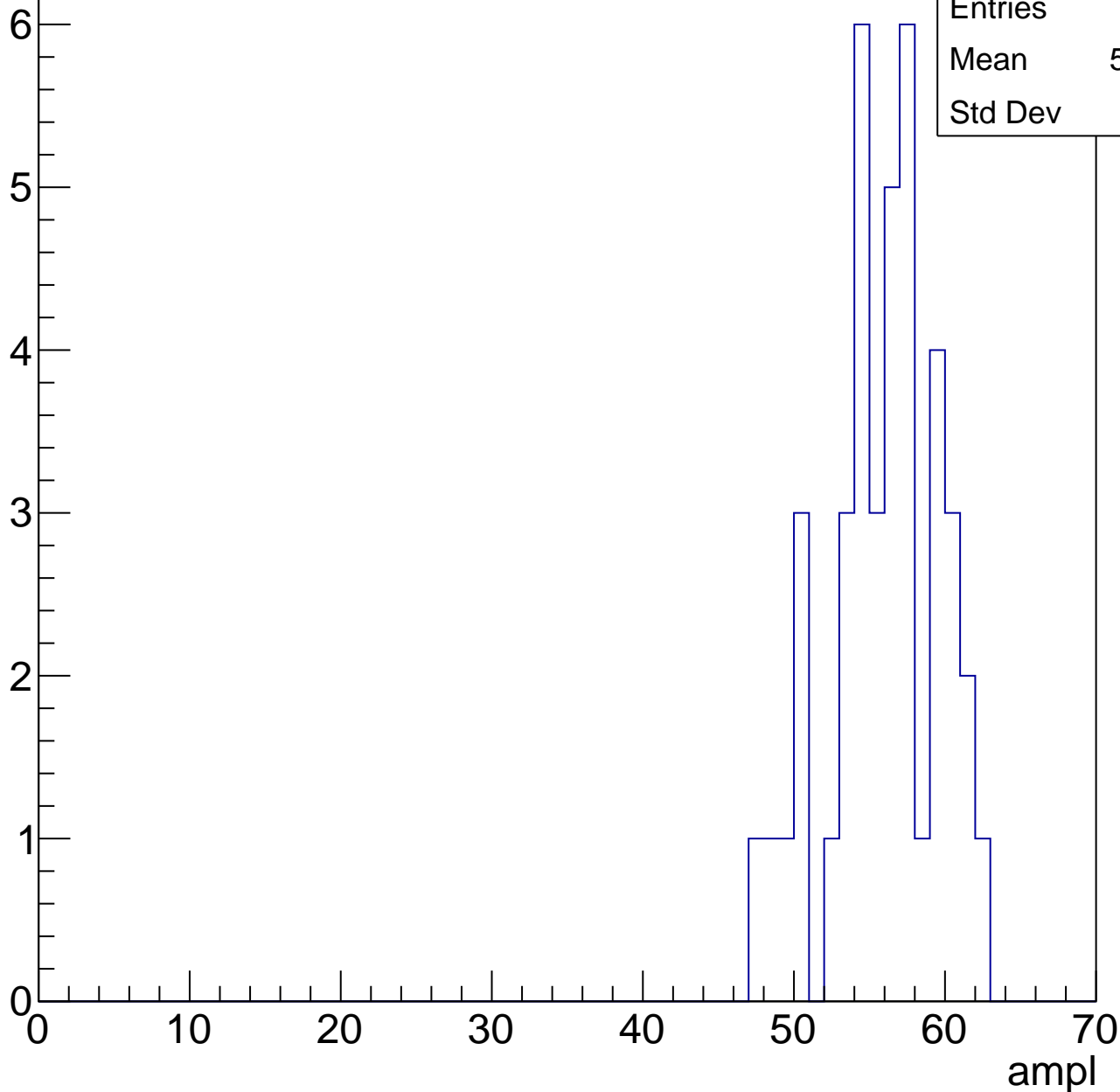


B1L103S, U13-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	55.46
Std Dev	3.63

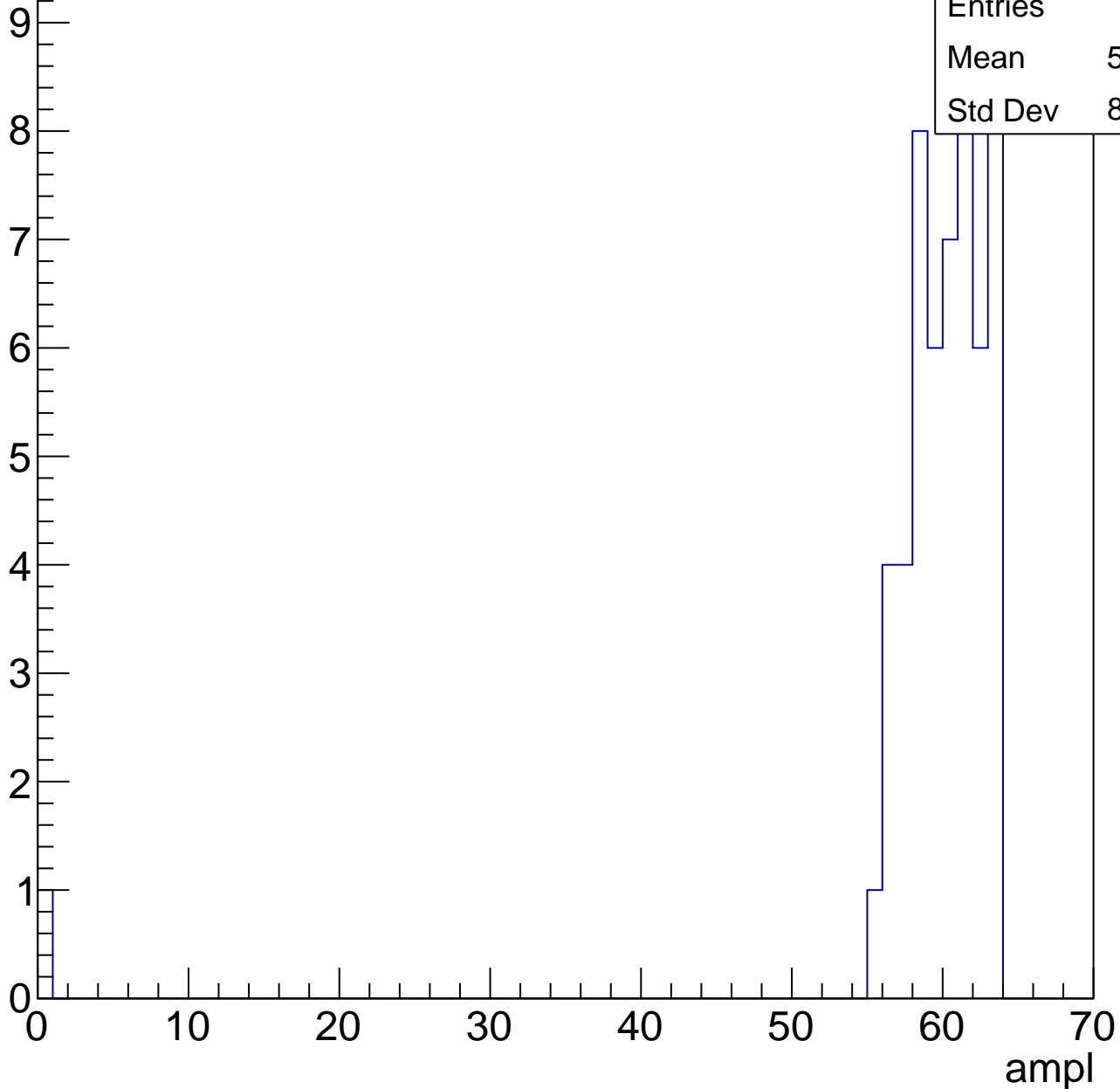


B1L103S, U13-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	58.74
Std Dev	8.378



B1L103S, U13-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

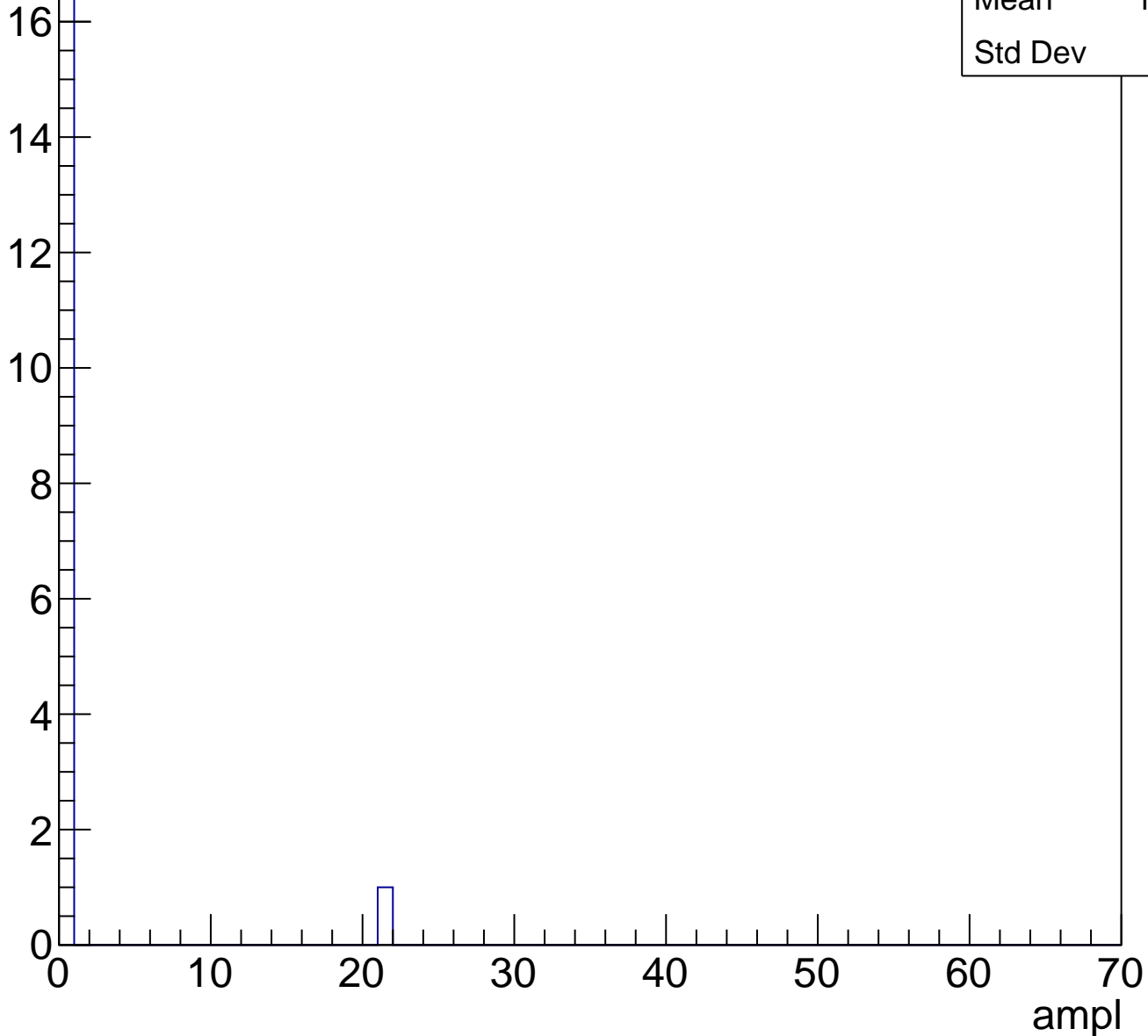


B1L103S, U13-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.167
Std Dev	4.81

Entry



B1L103S, U13-ch33, adc0

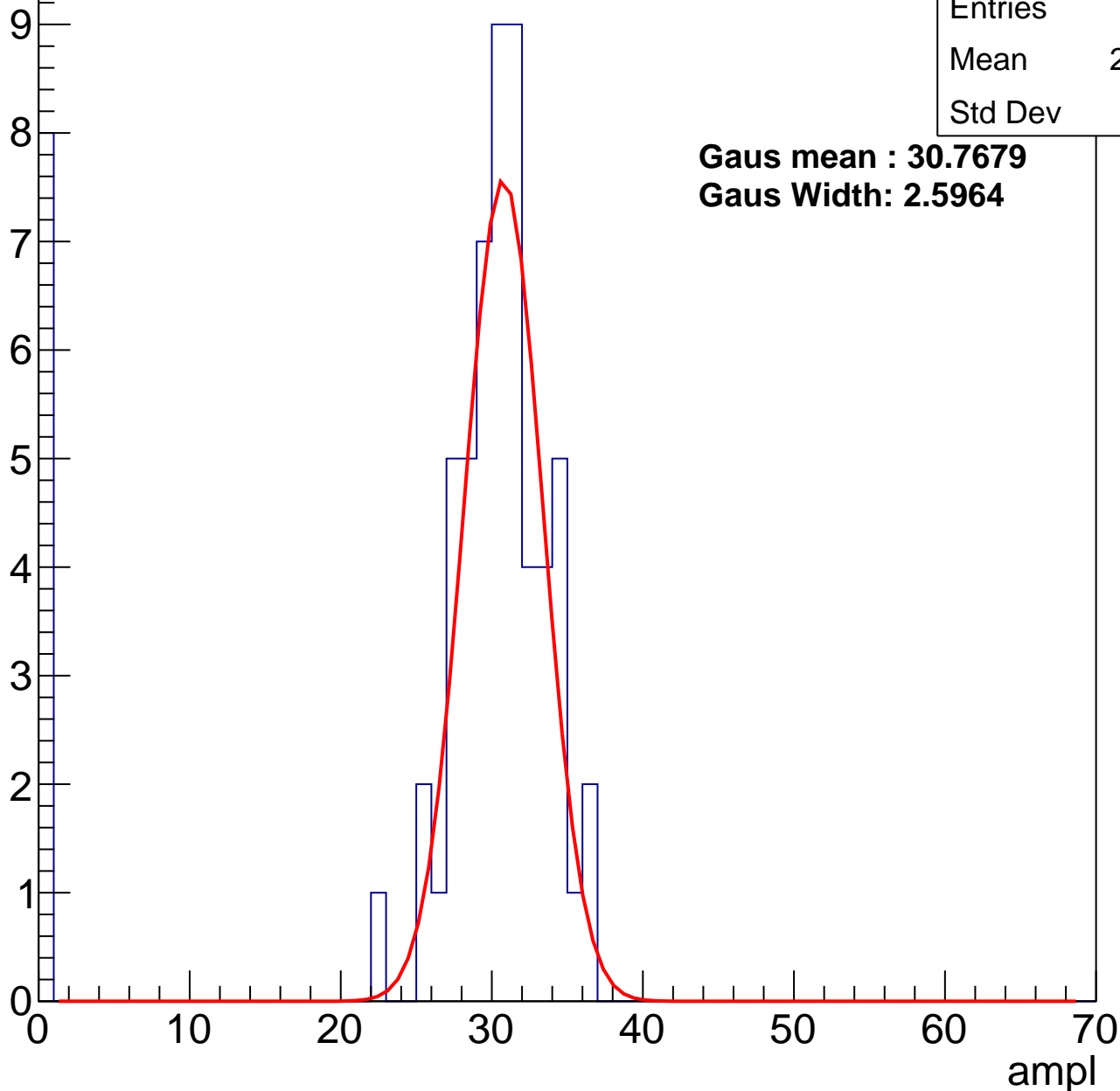
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	26.38
Std Dev	10.4

Gaus mean : 30.7679

Gaus Width: 2.5964



B1L103S, U13-ch33, adc1

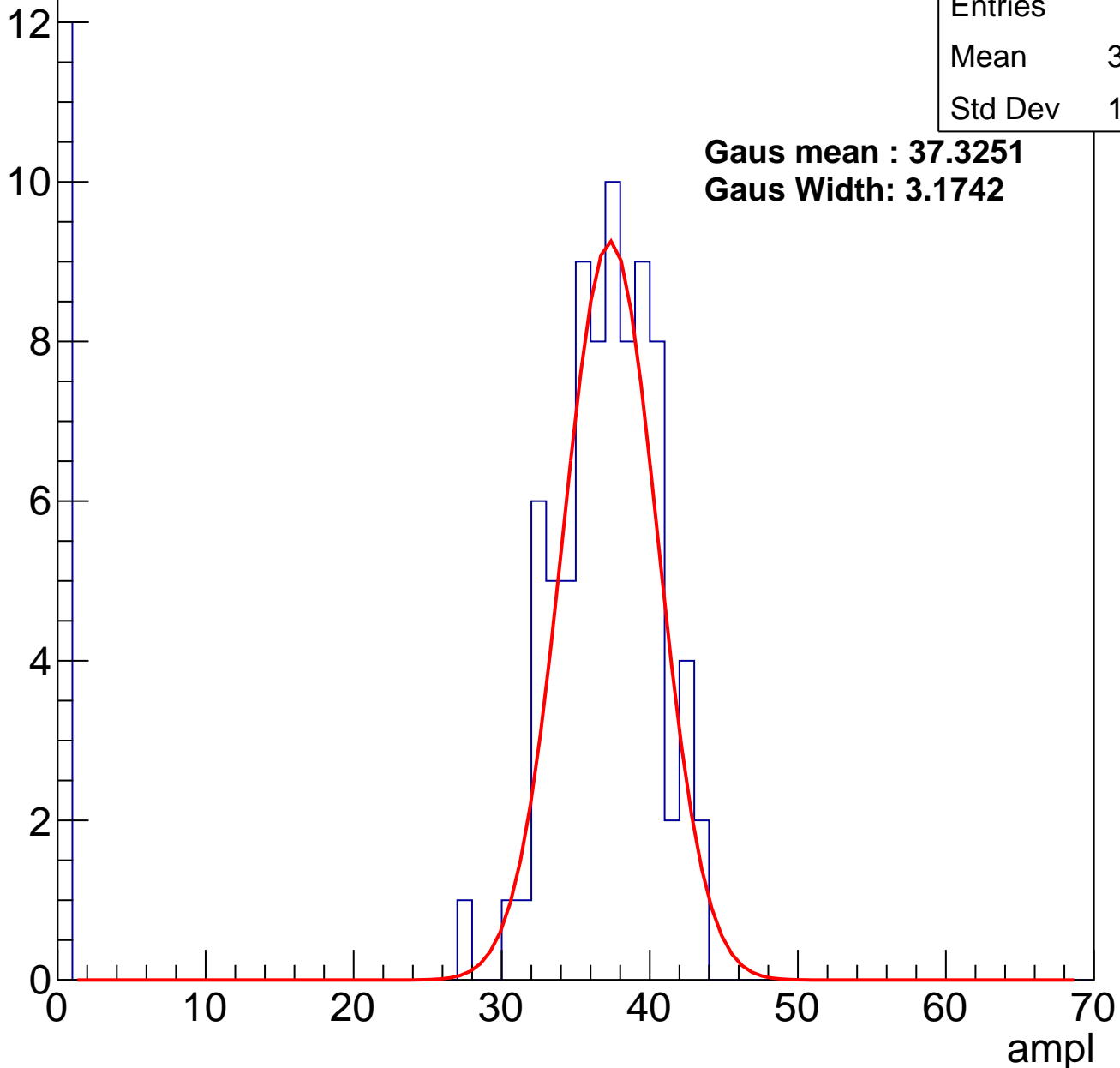
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	31.86
Std Dev	12.77

Gaus mean : 37.3251

Gaus Width: 3.1742

Entry



B1L103S, U13-ch33, adc2

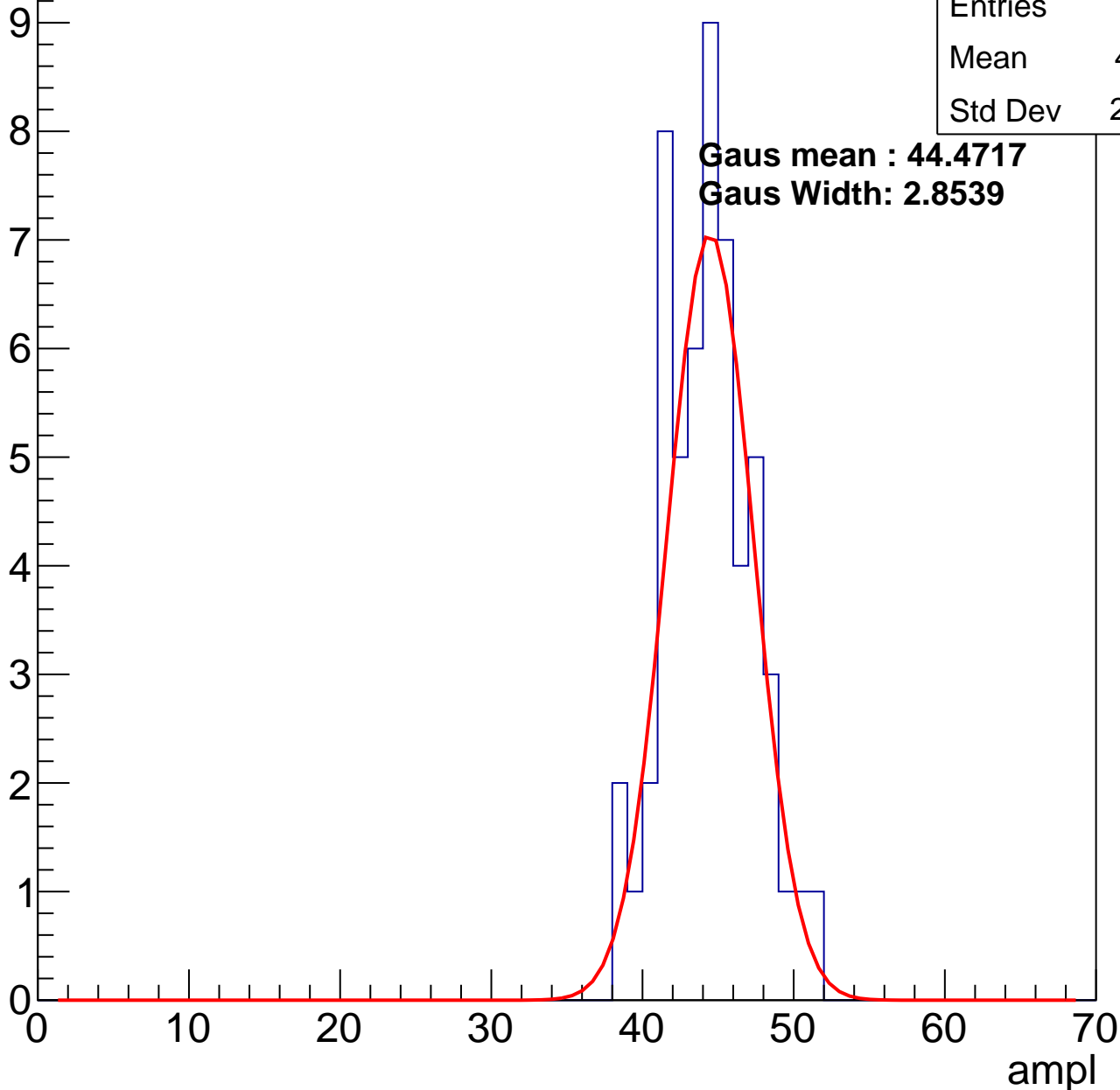
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	43.91
Std Dev	2.868

Gaus mean : 44.4717

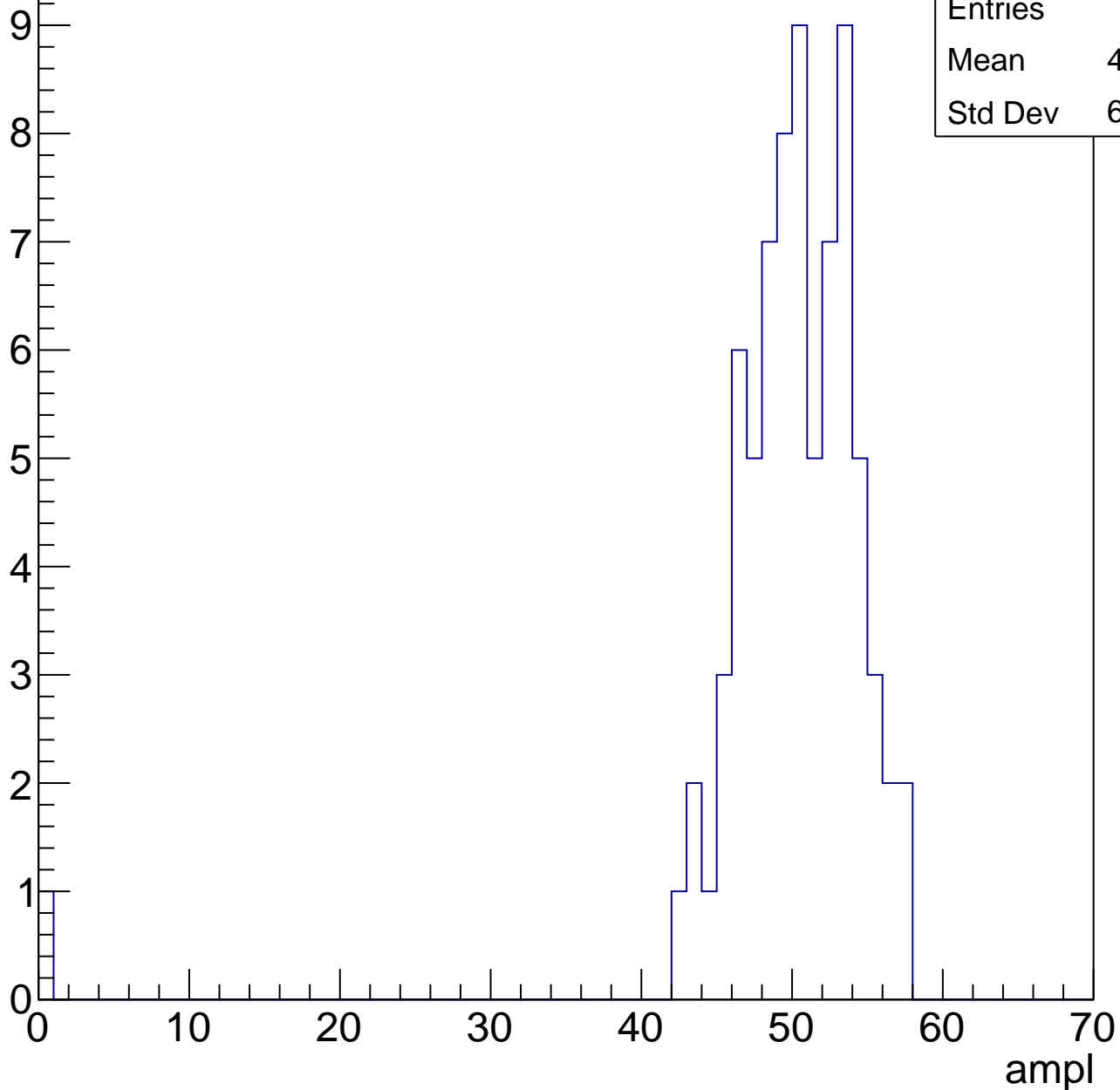
Gaus Width: 2.8539



B1L103S, U13-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

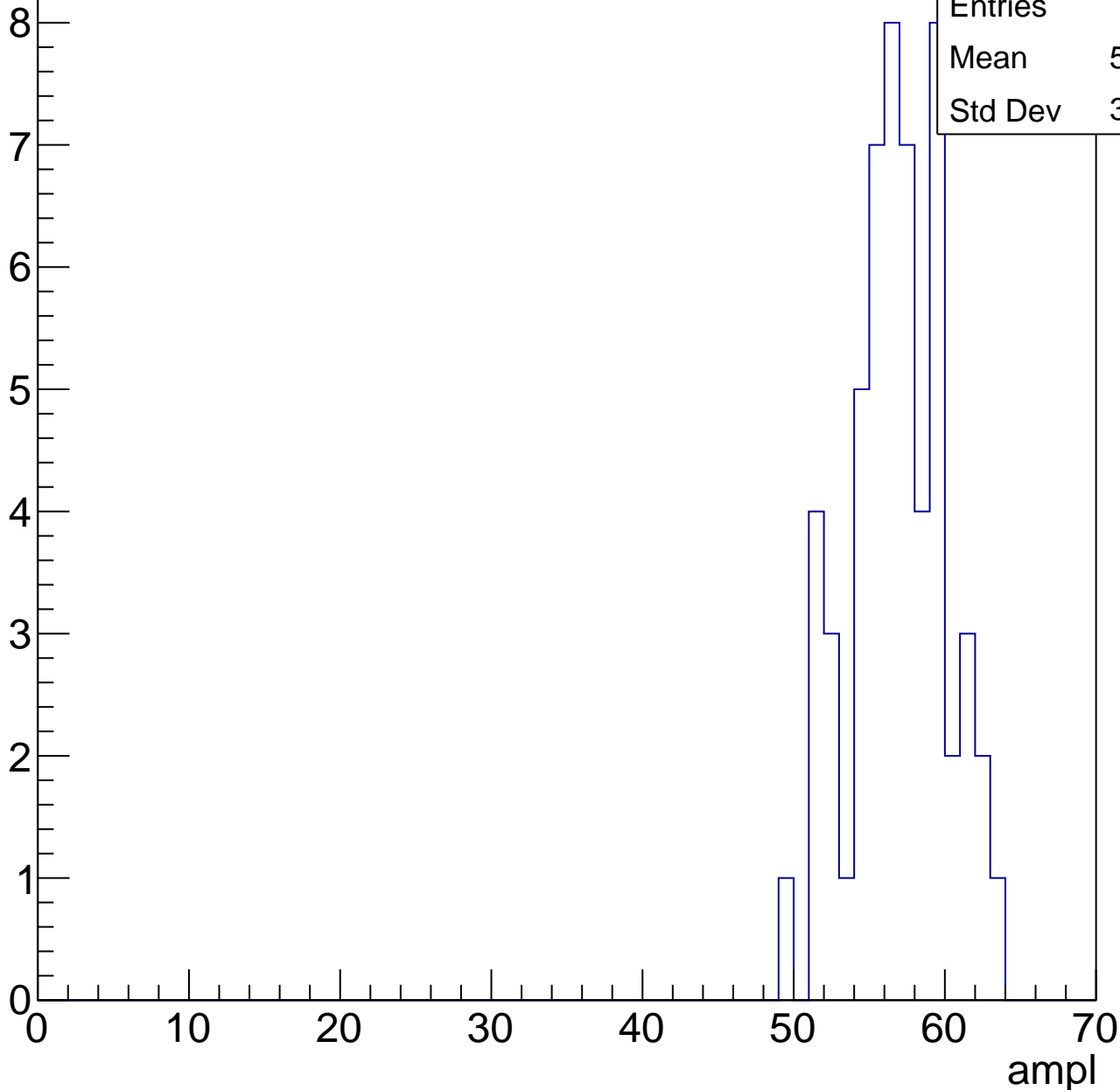


B1L103S, U13-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

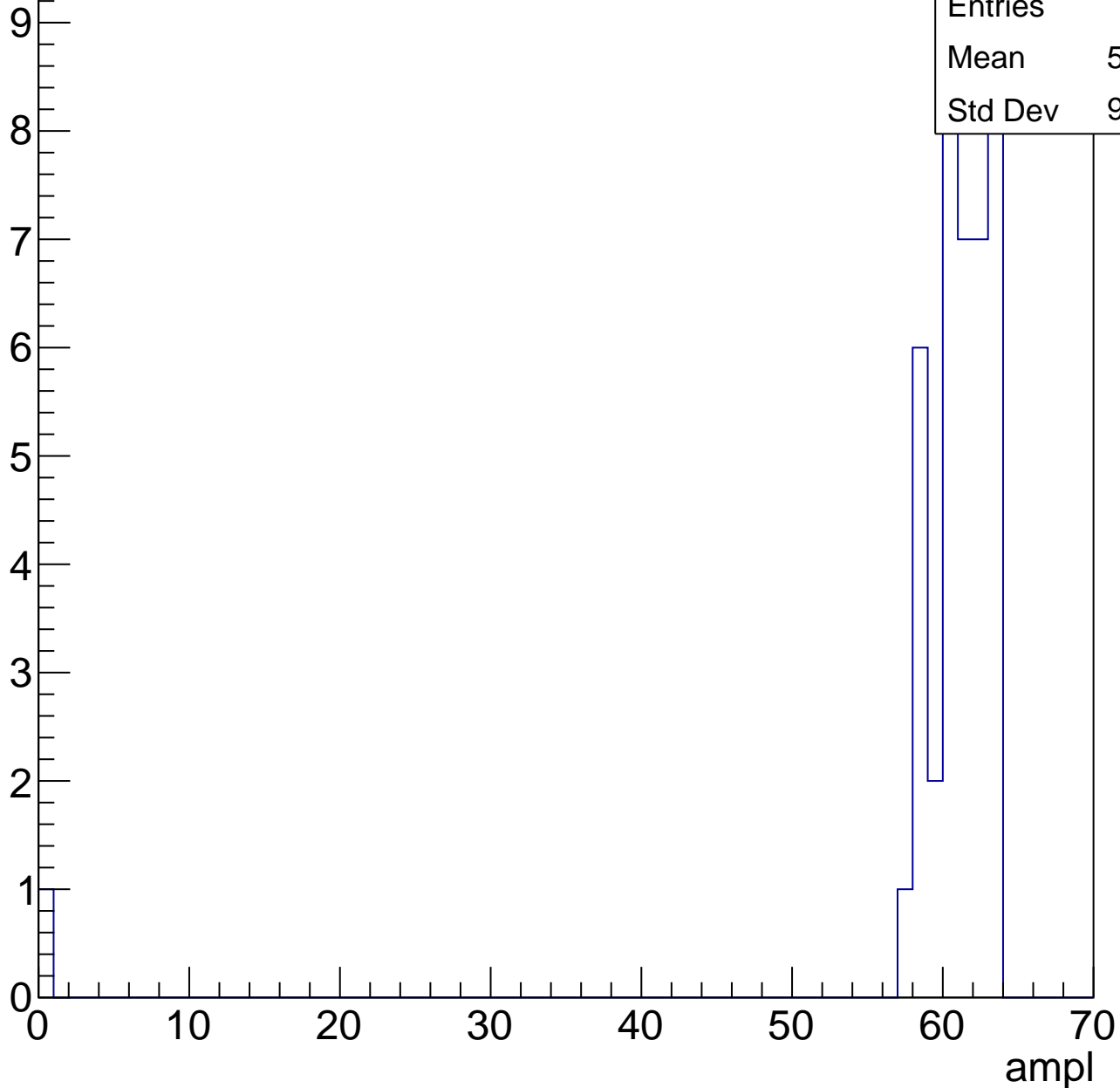
Entries	56
Mean	56.39
Std Dev	3.126



B1L103S, U13-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch34, adc0

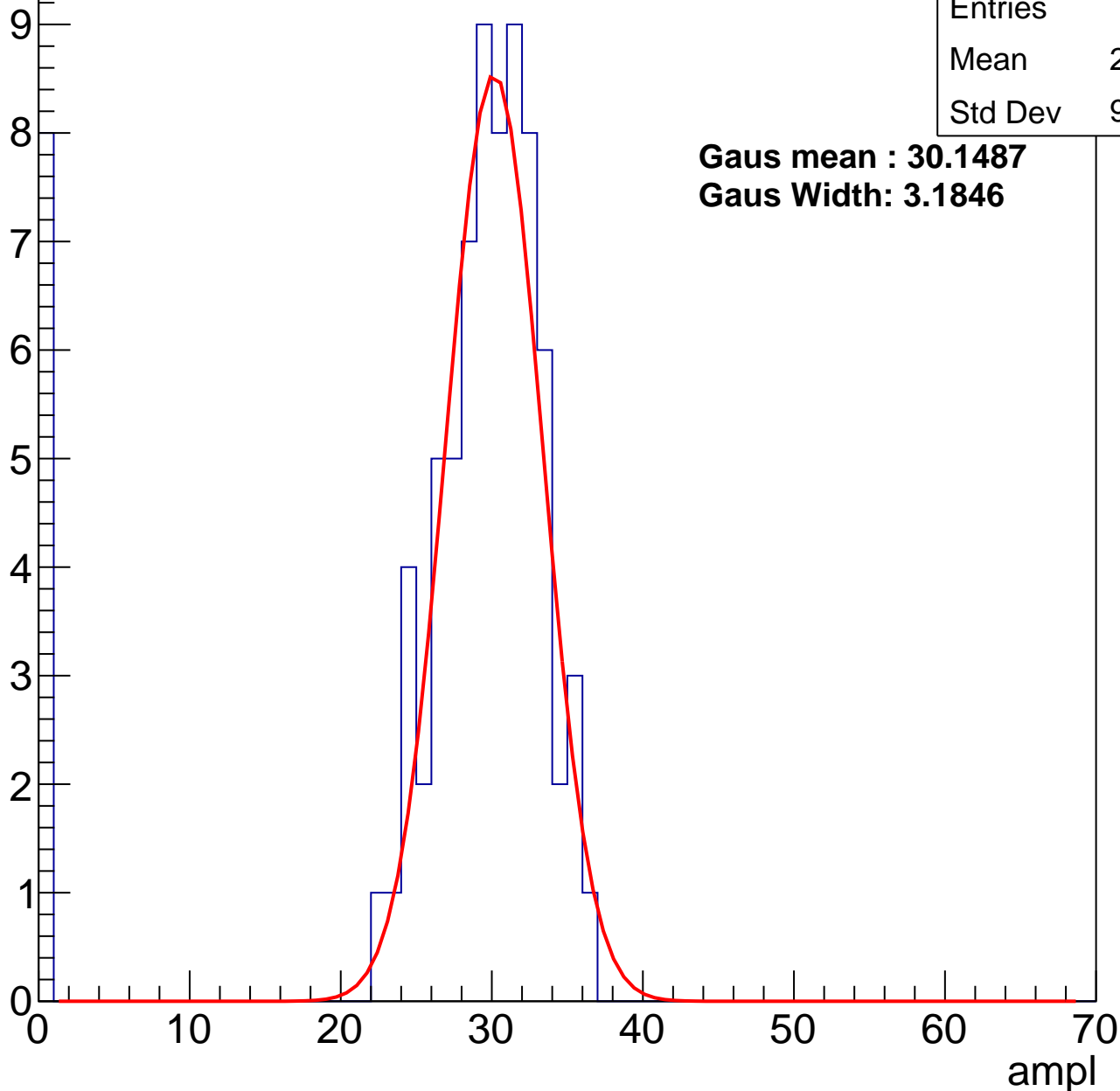
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	26.52
Std Dev	9.379

Gaus mean : 30.1487

Gaus Width: 3.1846



B1L103S, U13-ch34, adc1

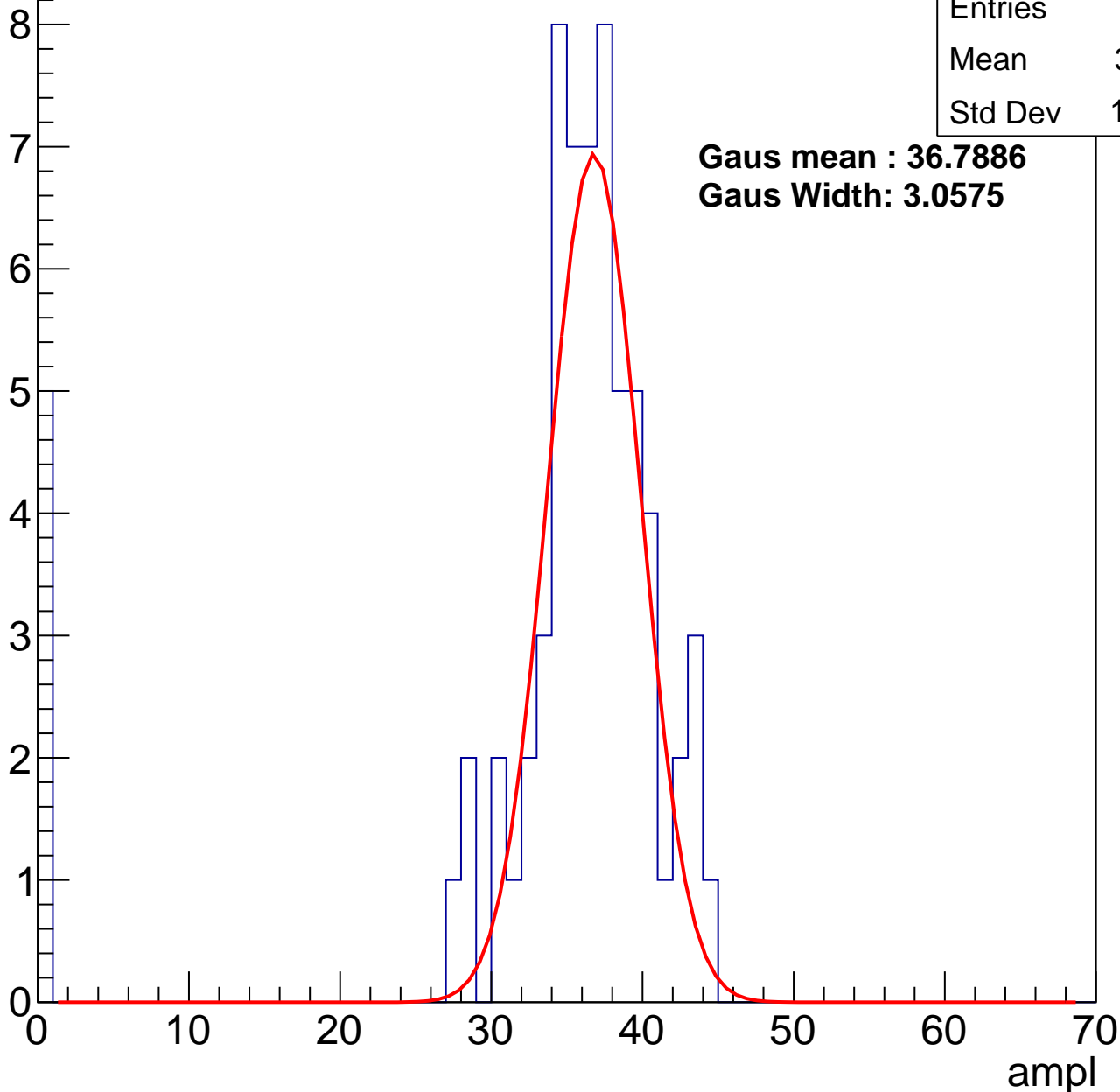
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.51
Std Dev	10.16

Gaus mean : 36.7886

Gaus Width: 3.0575



B1L103S, U13-ch34, adc2

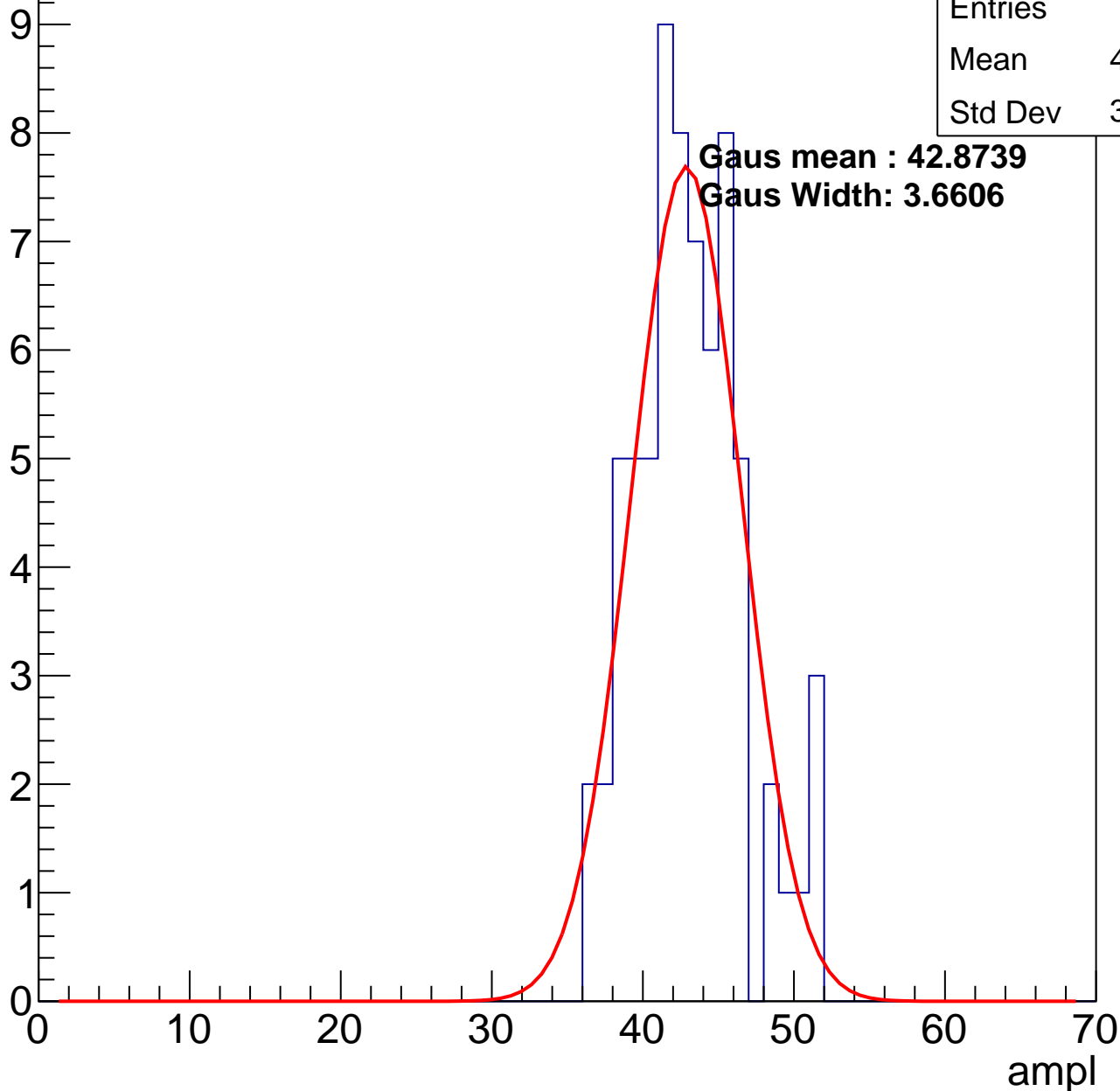
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	42.59
Std Dev	3.536

Gaus mean : 42.8739

Gaus Width: 3.6606



B1L103S, U13-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

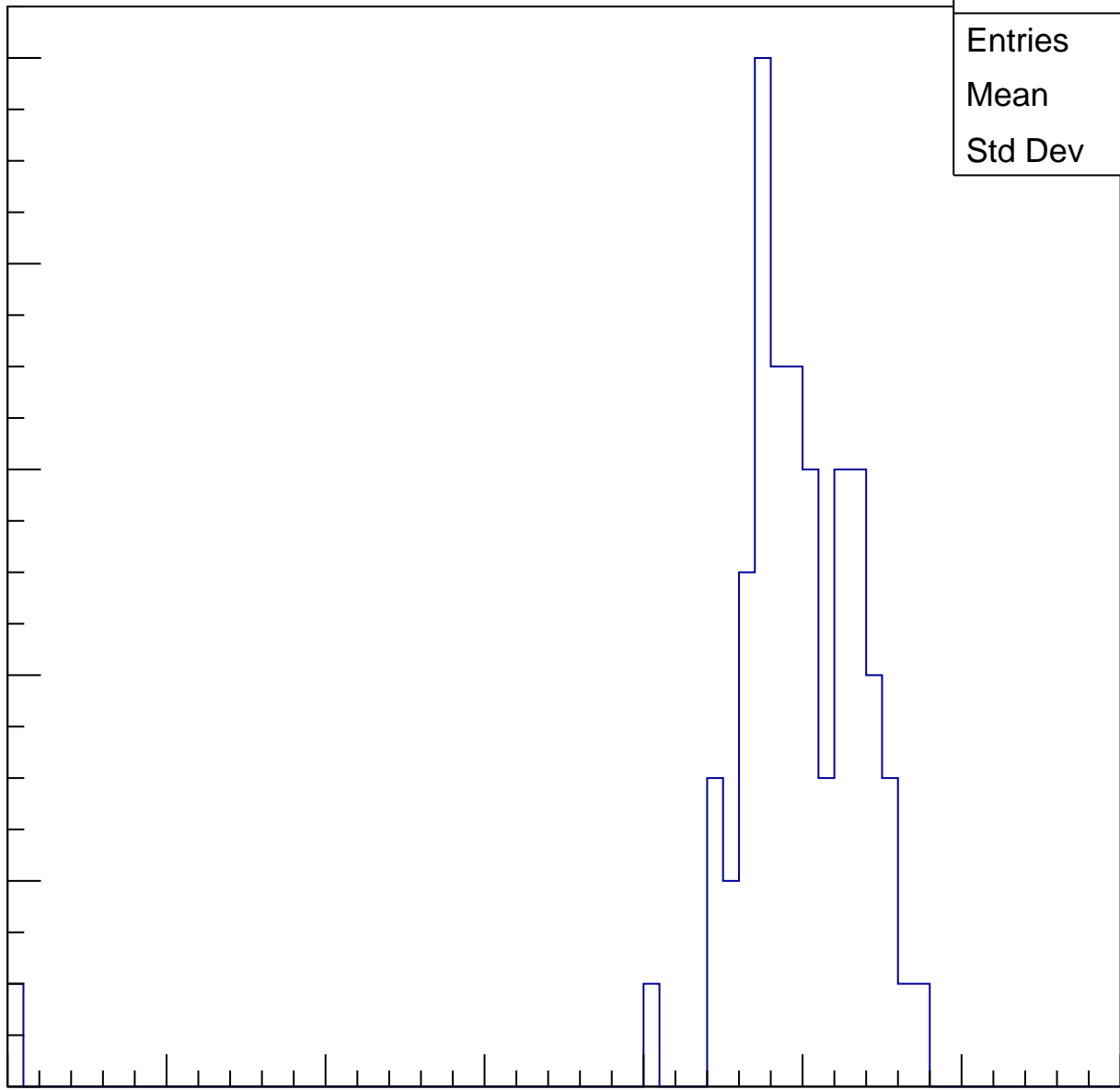
Entries	66
Mean	48.76
Std Dev	6.926

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

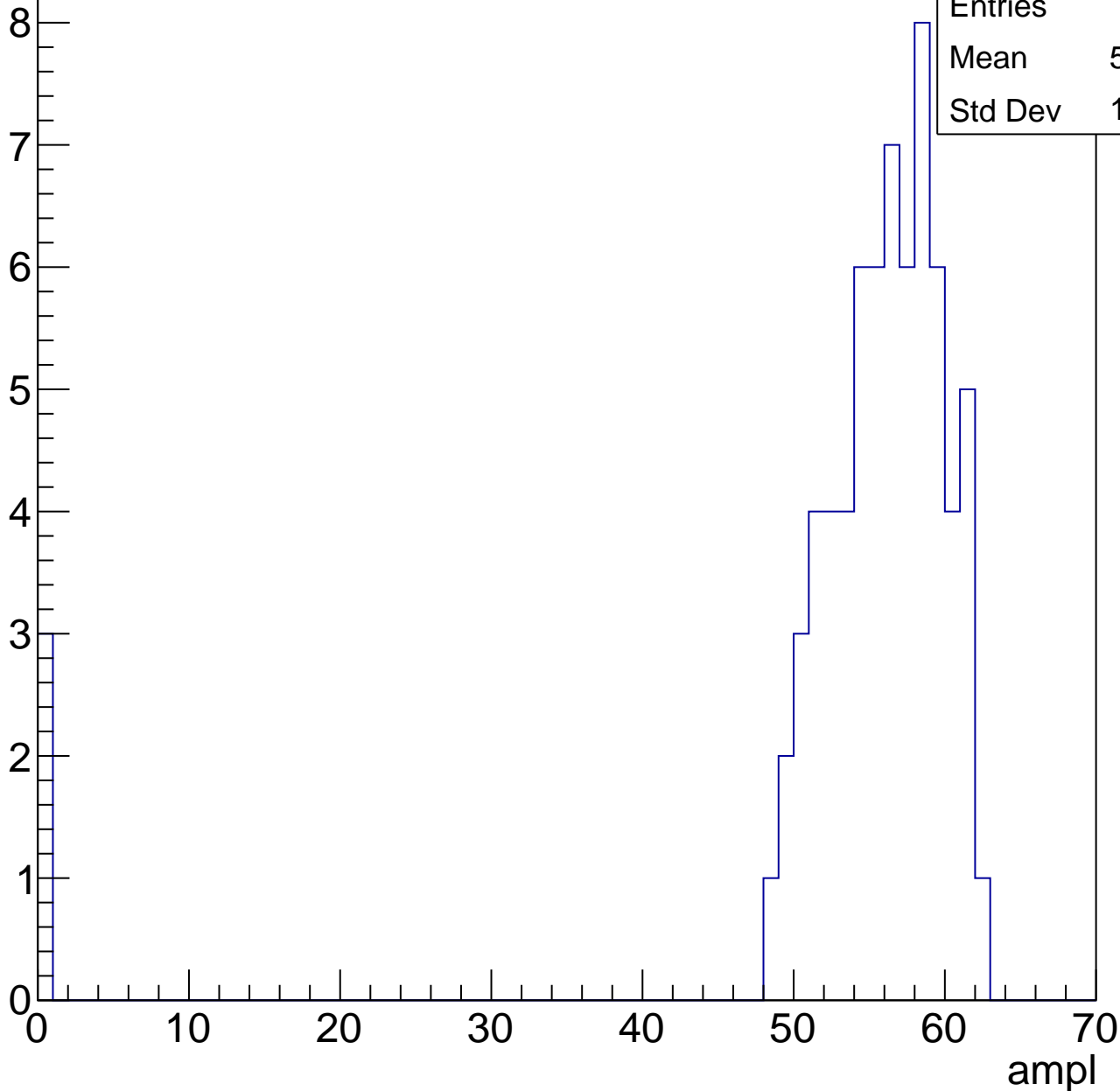


B1L103S, U13-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	53.33
Std Dev	11.79

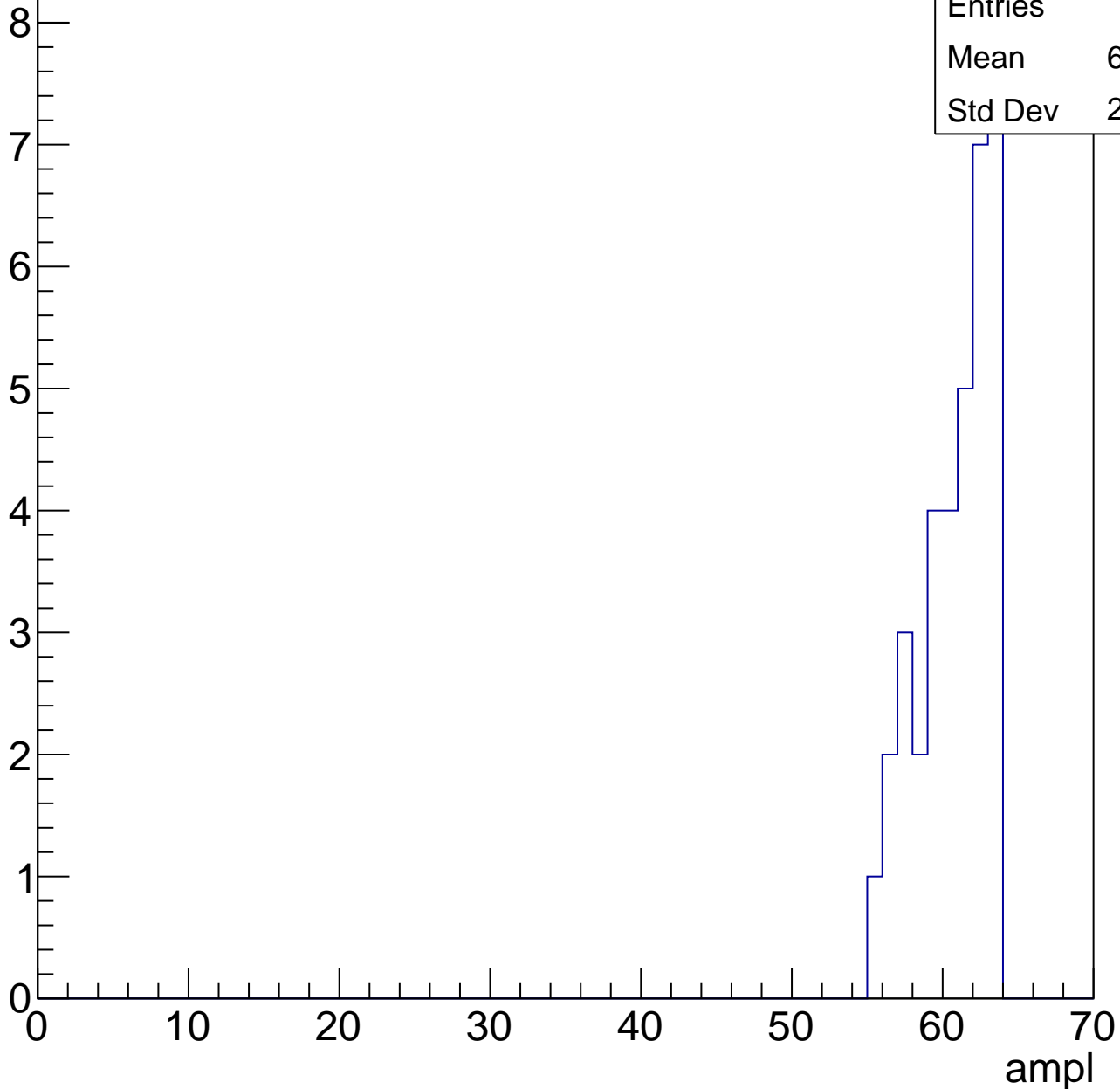


B1L103S, U13-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	60.36
Std Dev	2.335



B1L103S, U13-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

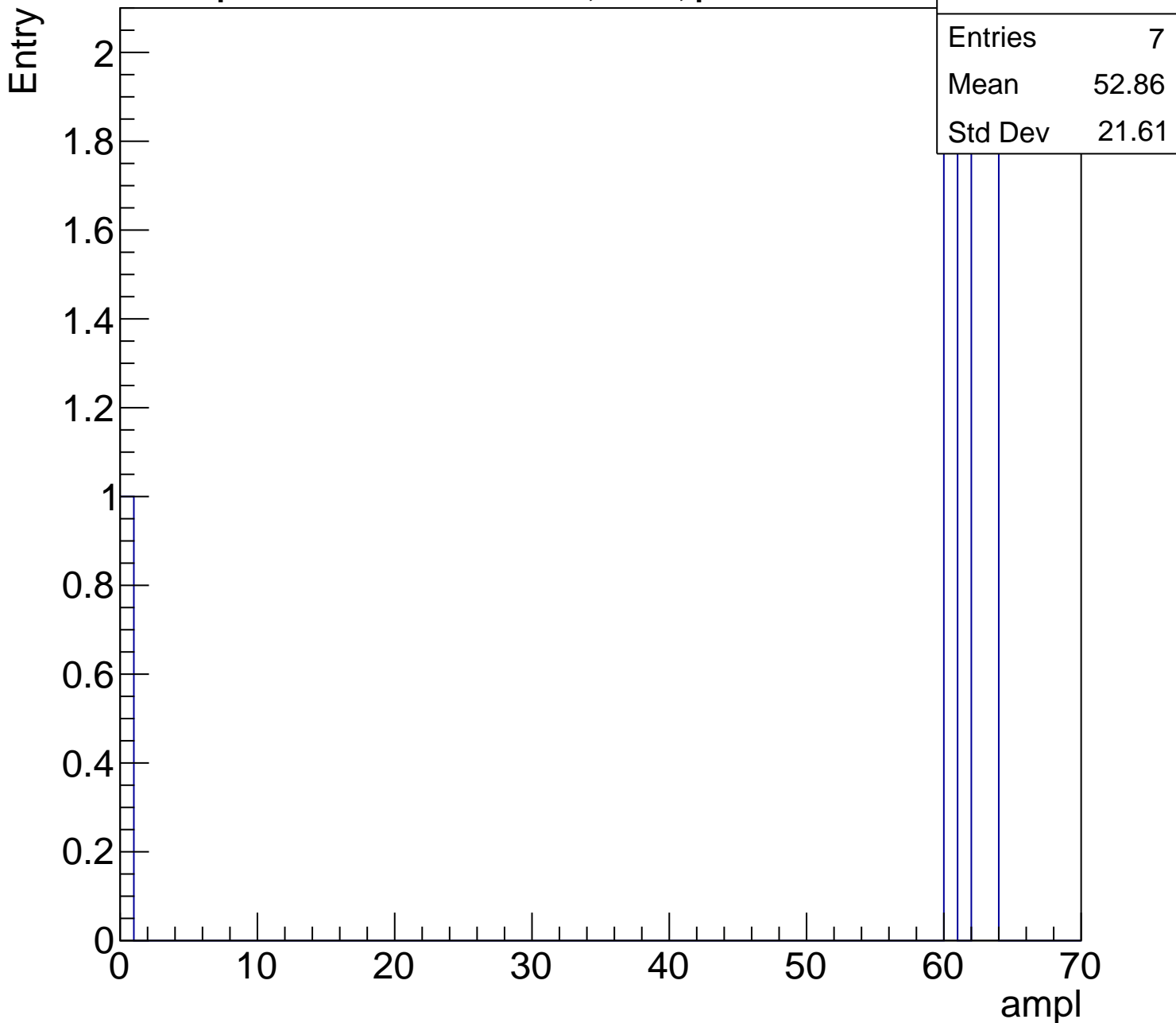
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.61

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch35, adc0

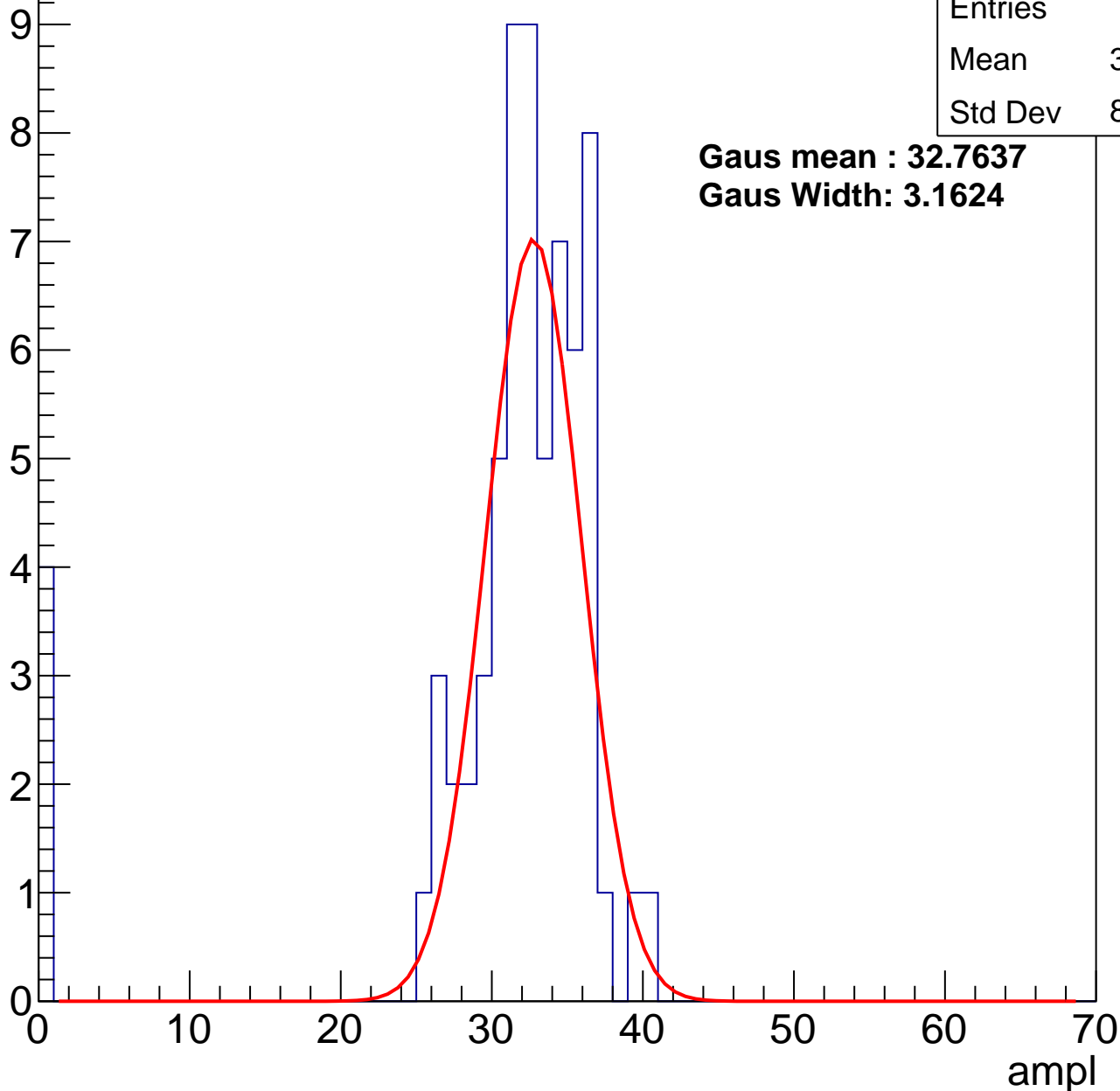
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	30.36
Std Dev	8.253

Gaus mean : 32.7637

Gaus Width: 3.1624

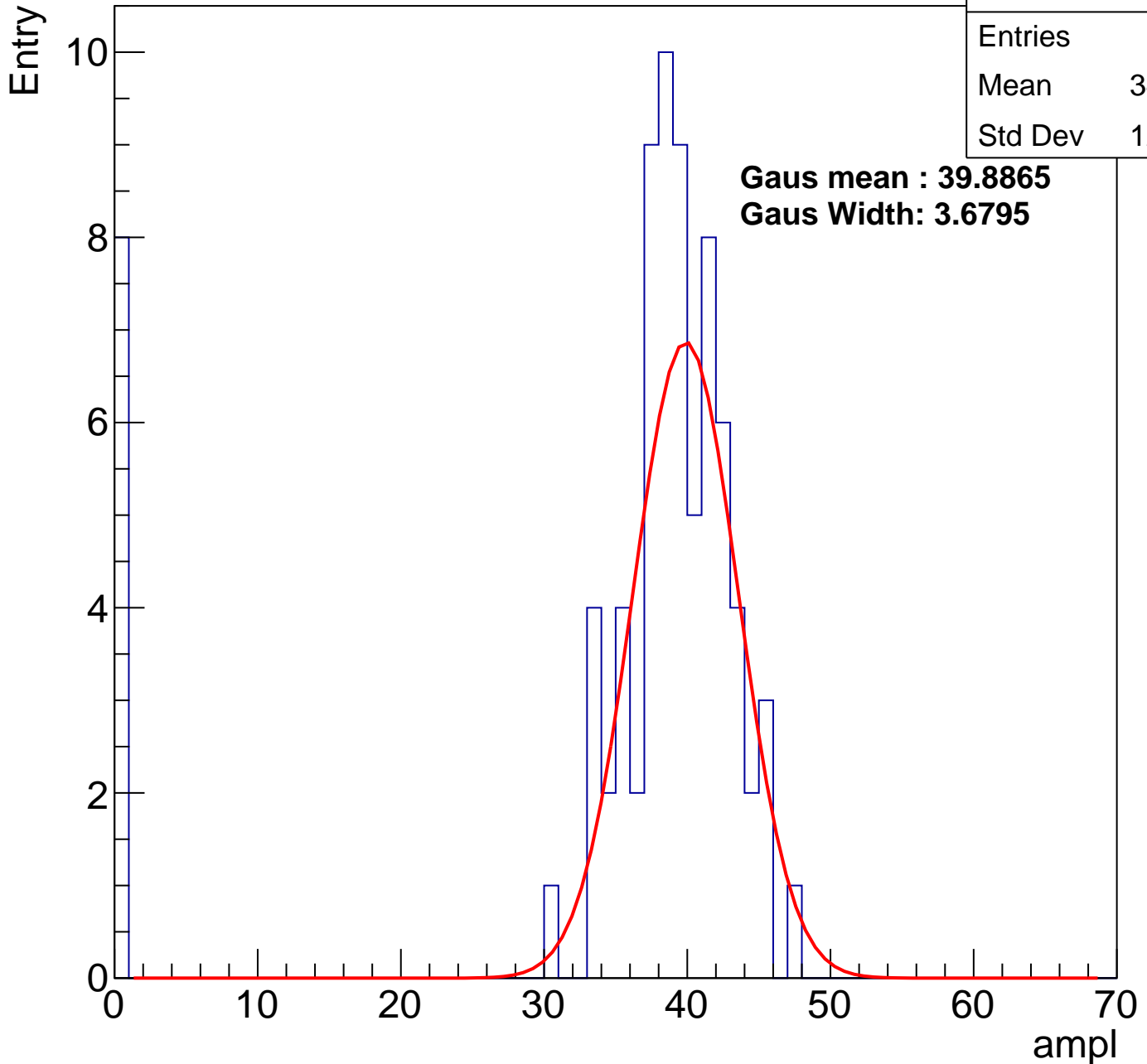


B1L103S, U13-ch35, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	34.97
Std Dev	12.24

Gaus mean : 39.8865
Gaus Width: 3.6795



B1L103S, U13-ch35, adc2

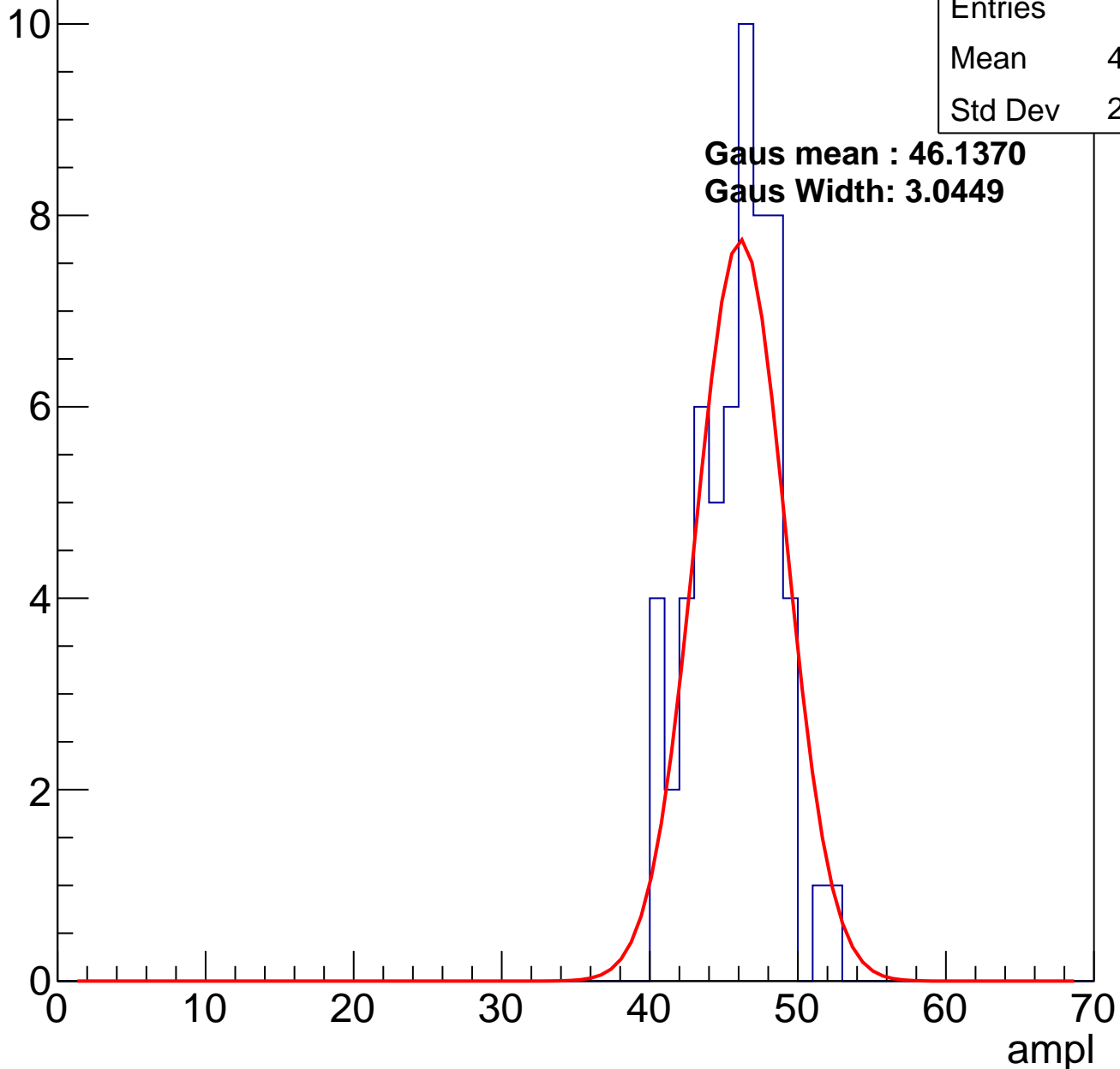
calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	45.37
Std Dev	2.767

Gaus mean : 46.1370

Gaus Width: 3.0449

Entry

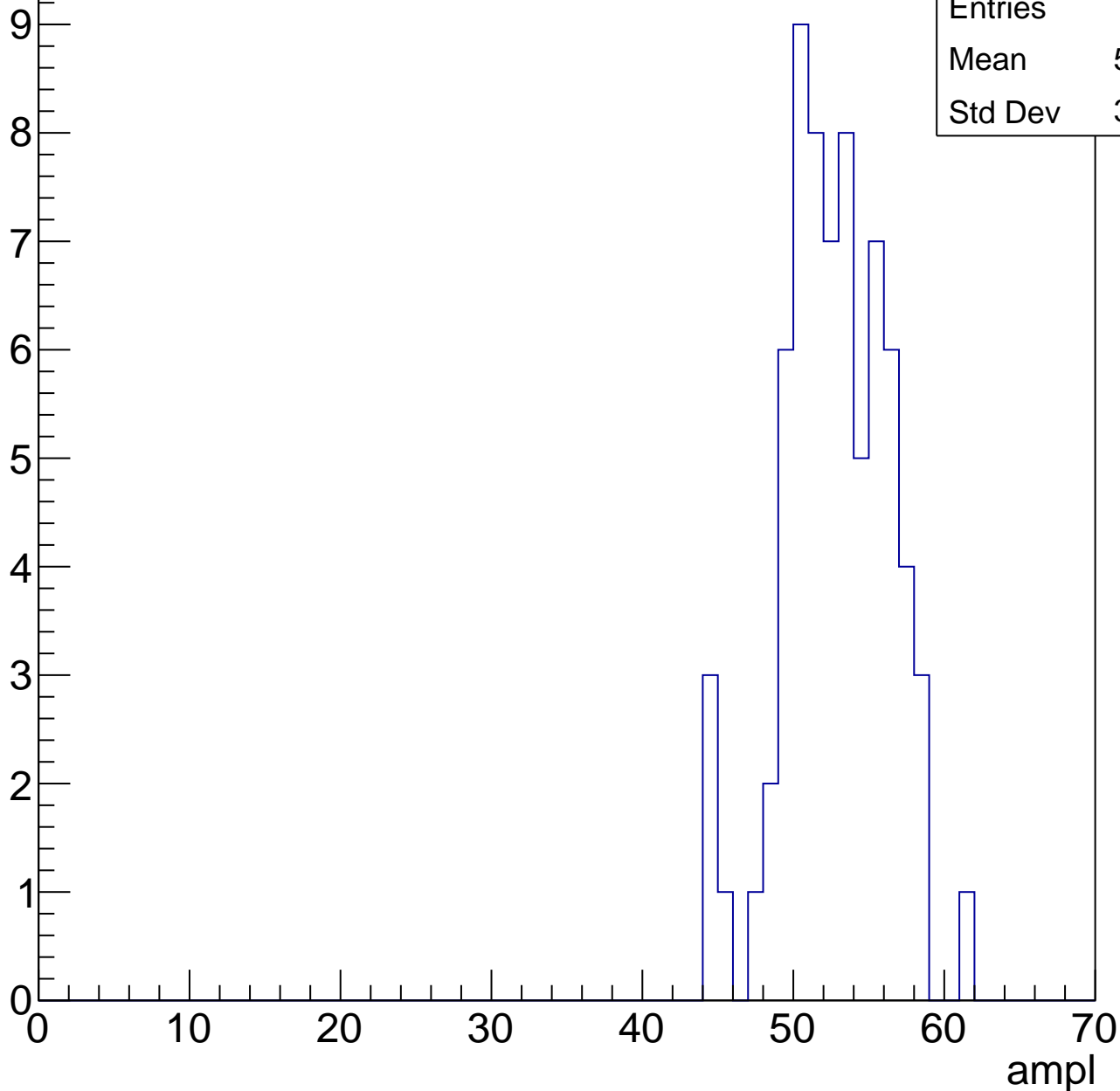


B1L103S, U13-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	52.31
Std Dev	3.491

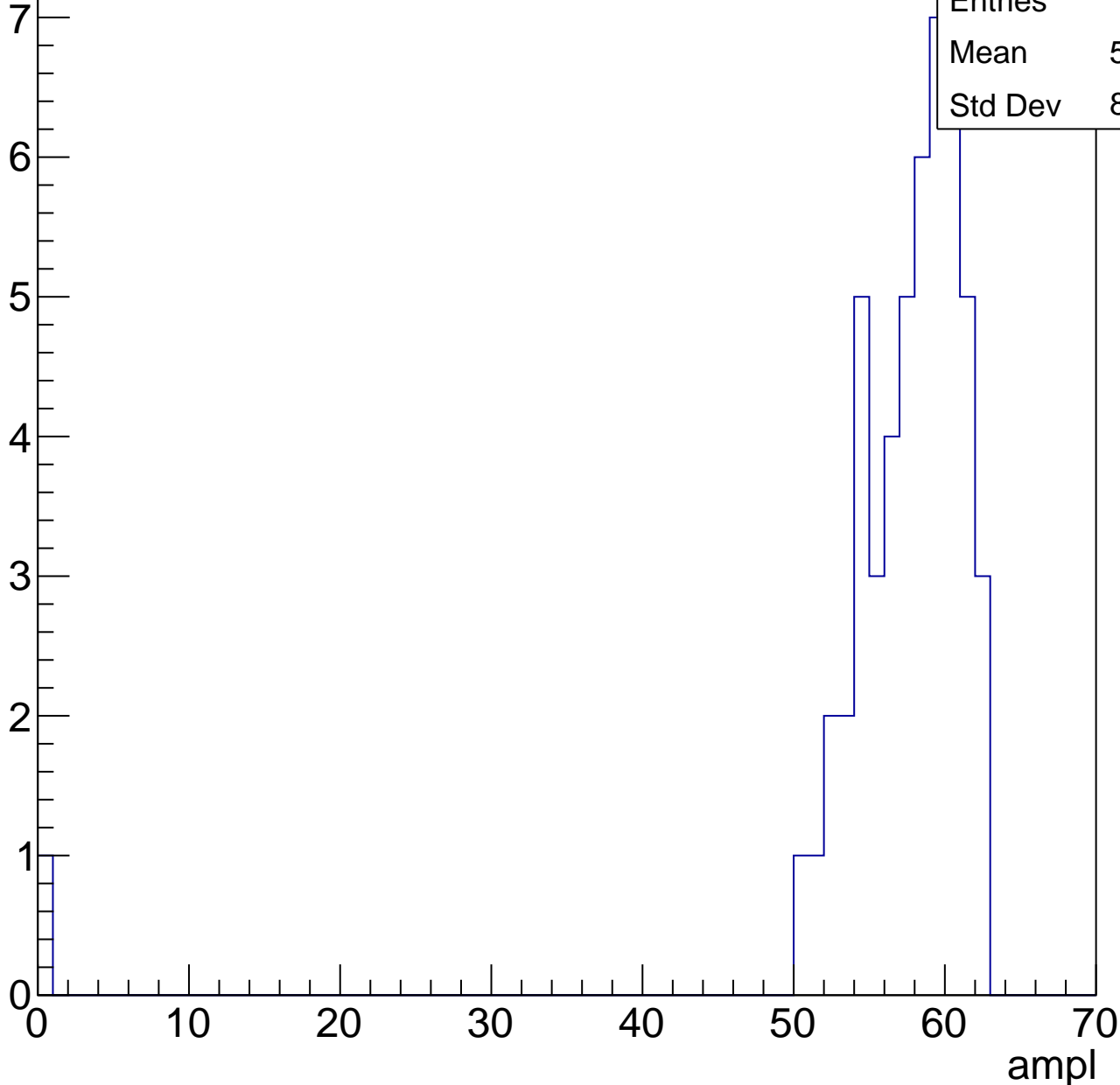


B1L103S, U13-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	56.29
Std Dev	8.438

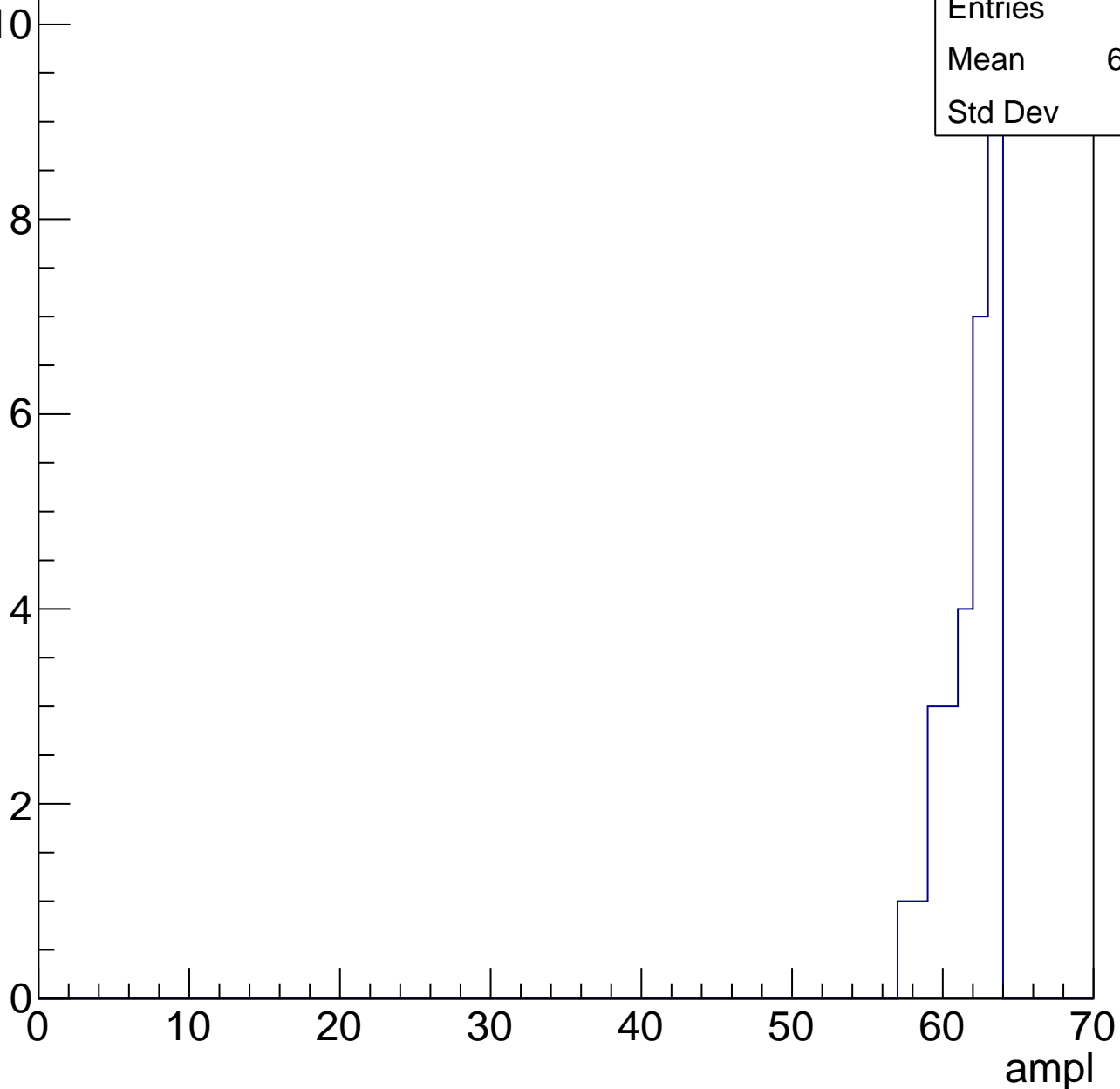


B1L103S, U13-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	61.38
Std Dev	1.69



B1L103S, U13-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

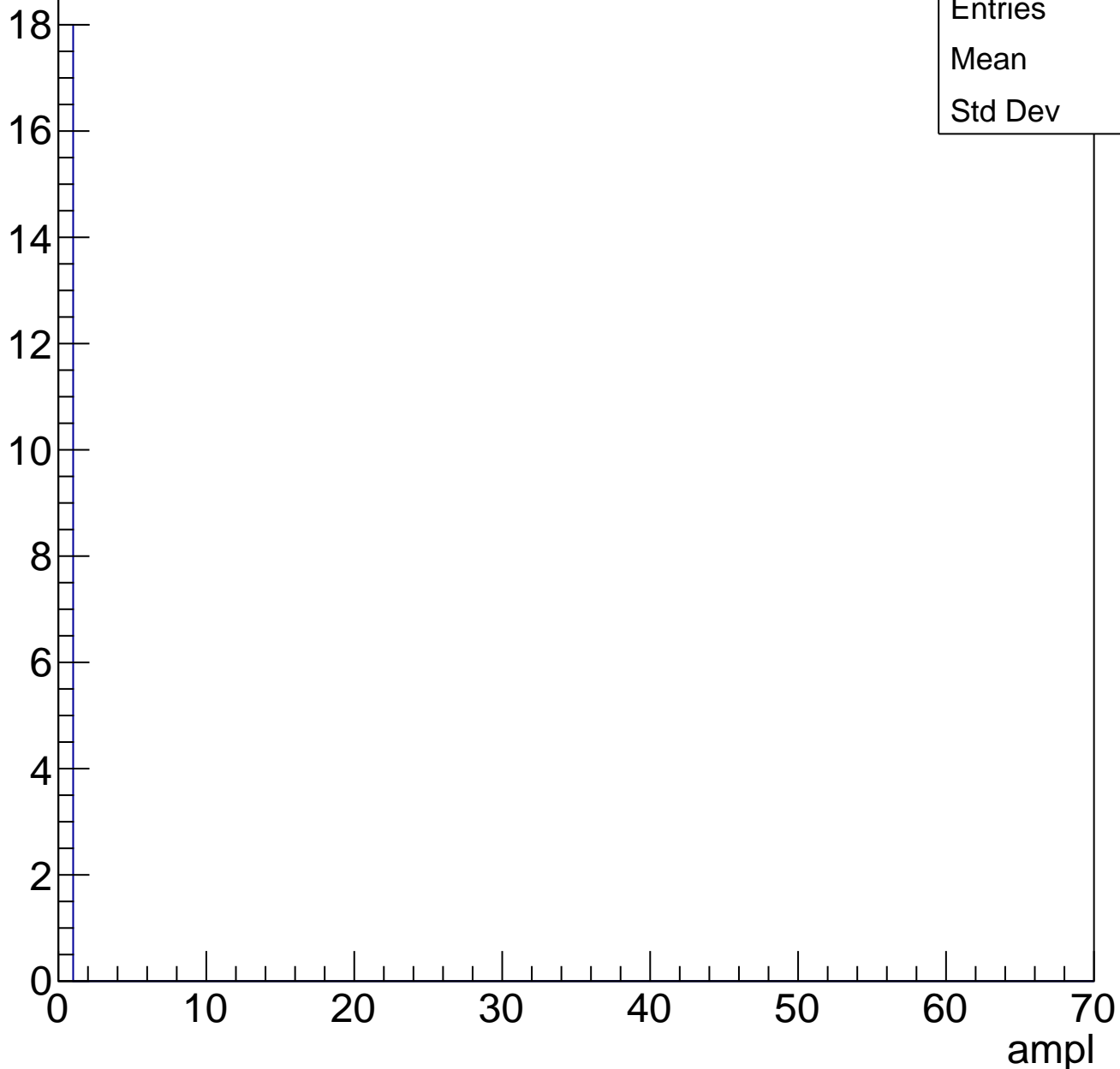


Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch36, adc0

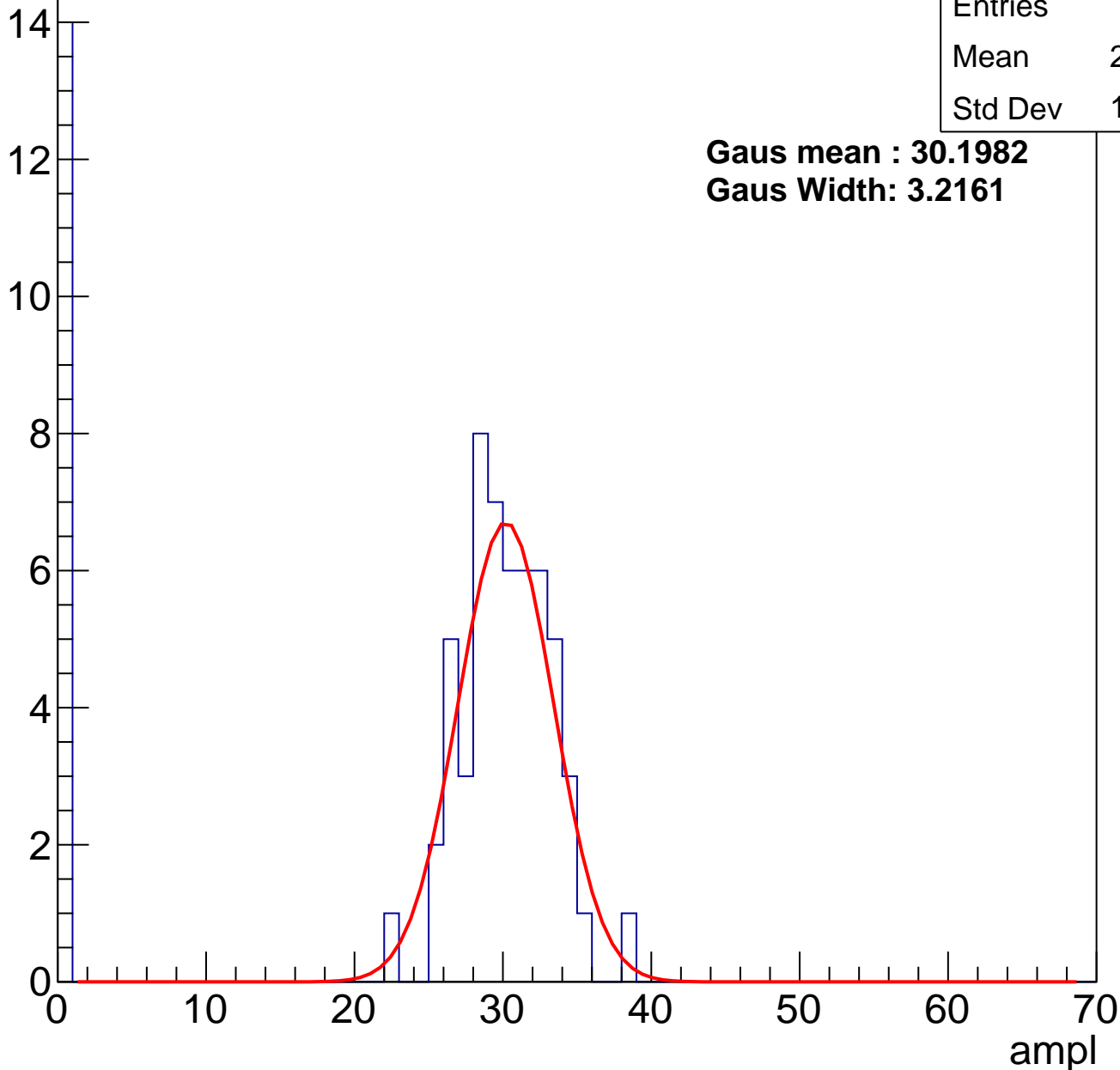
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	23.65
Std Dev	12.32

Gaus mean : 30.1982

Gaus Width: 3.2161

Entry



B1L103S, U13-ch36, adc1

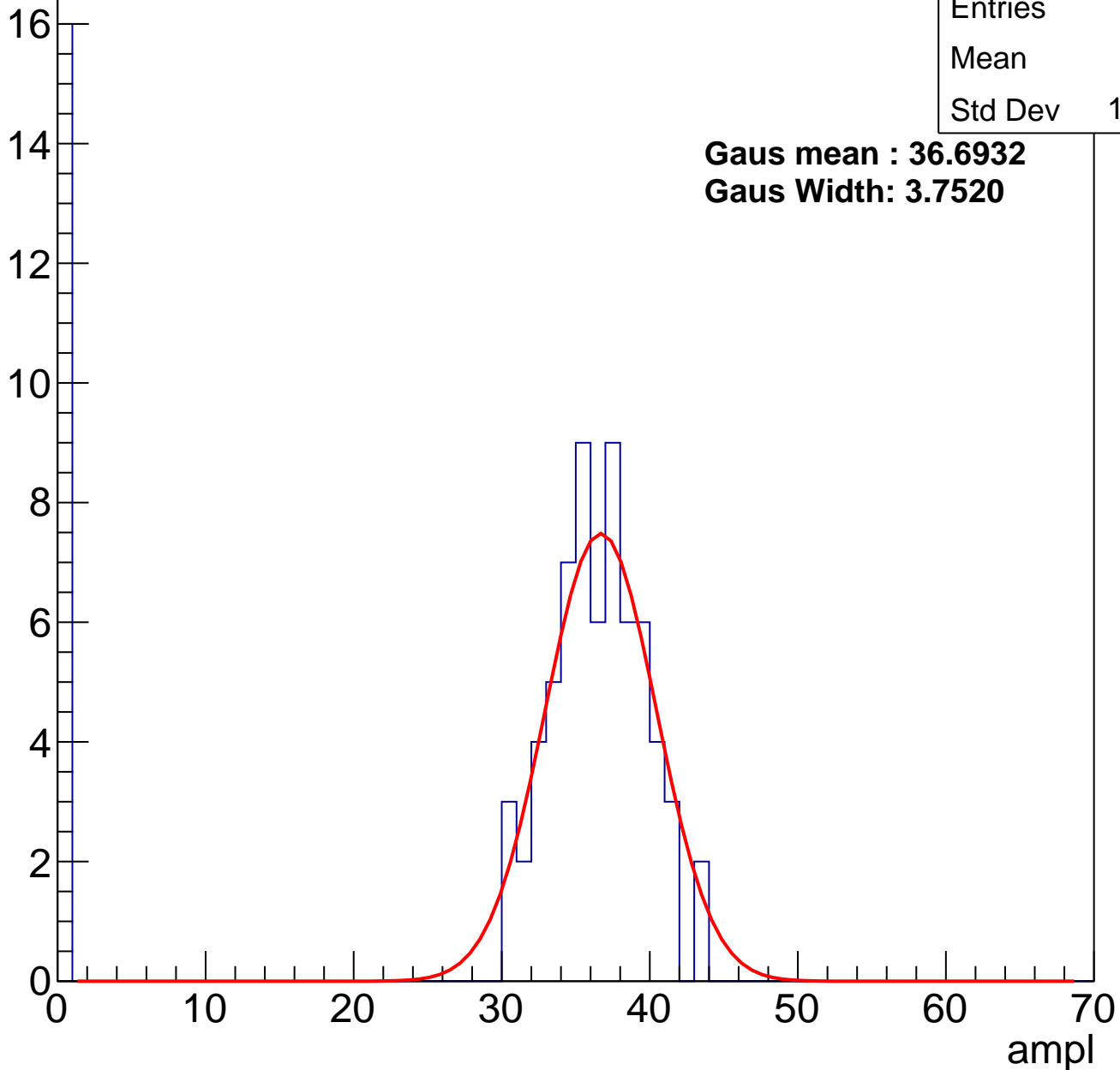
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	29
Std Dev	14.54

Gaus mean : 36.6932

Gaus Width: 3.7520

Entry



B1L103S, U13-ch36, adc2

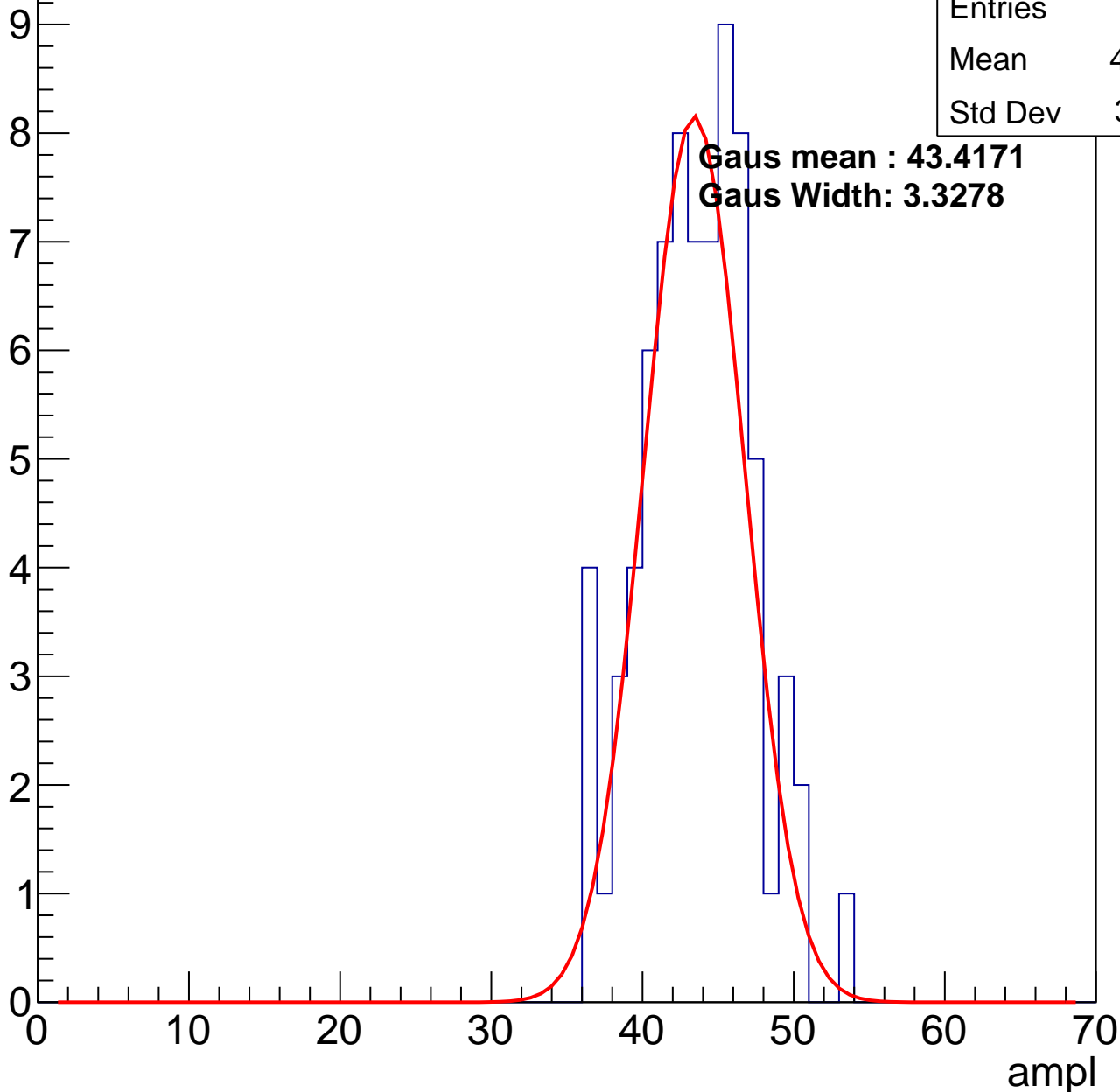
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	43.14
Std Dev	3.601

Gaus mean : 43.4171

Gaus Width: 3.3278

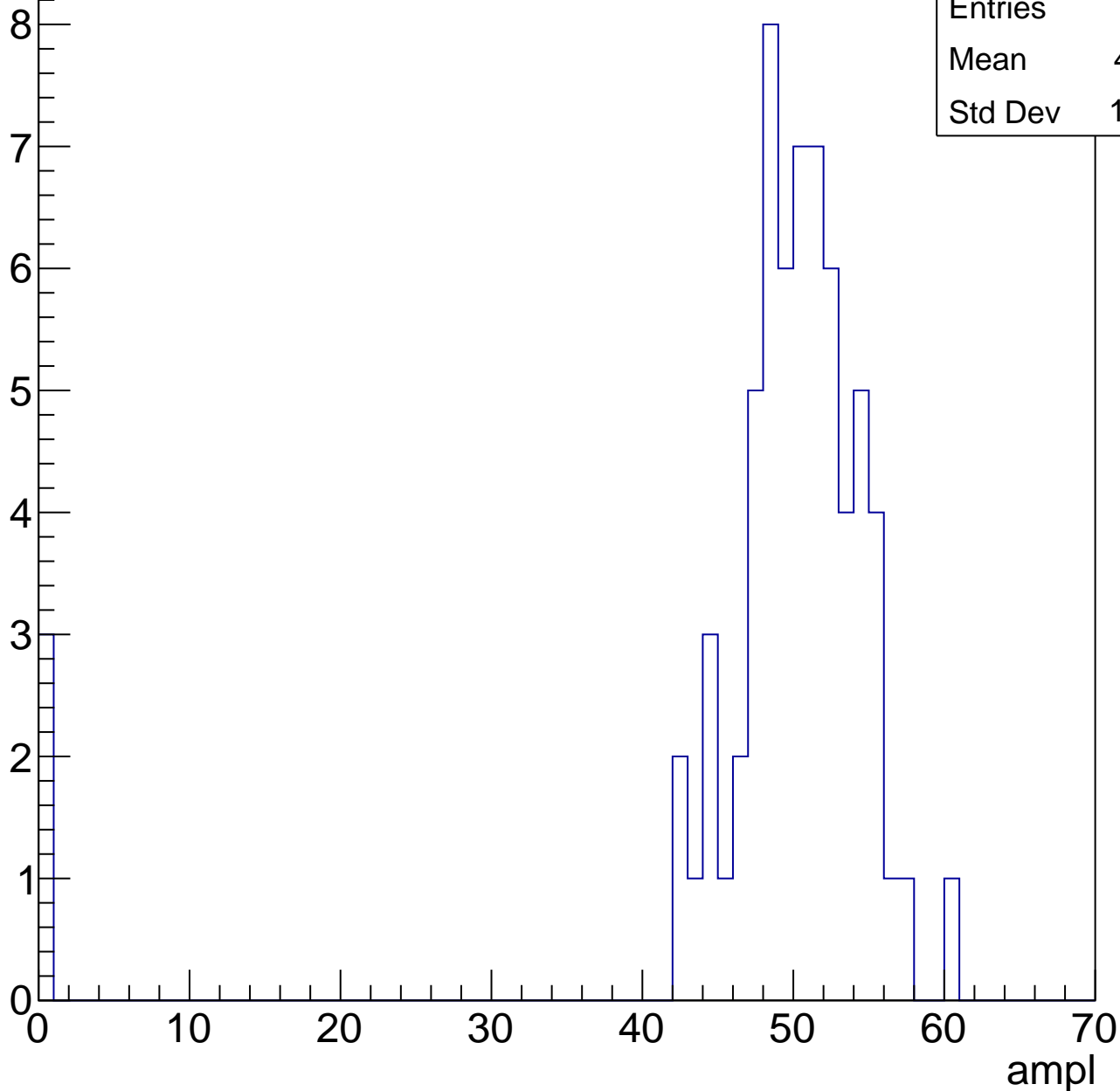


B1L103S, U13-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

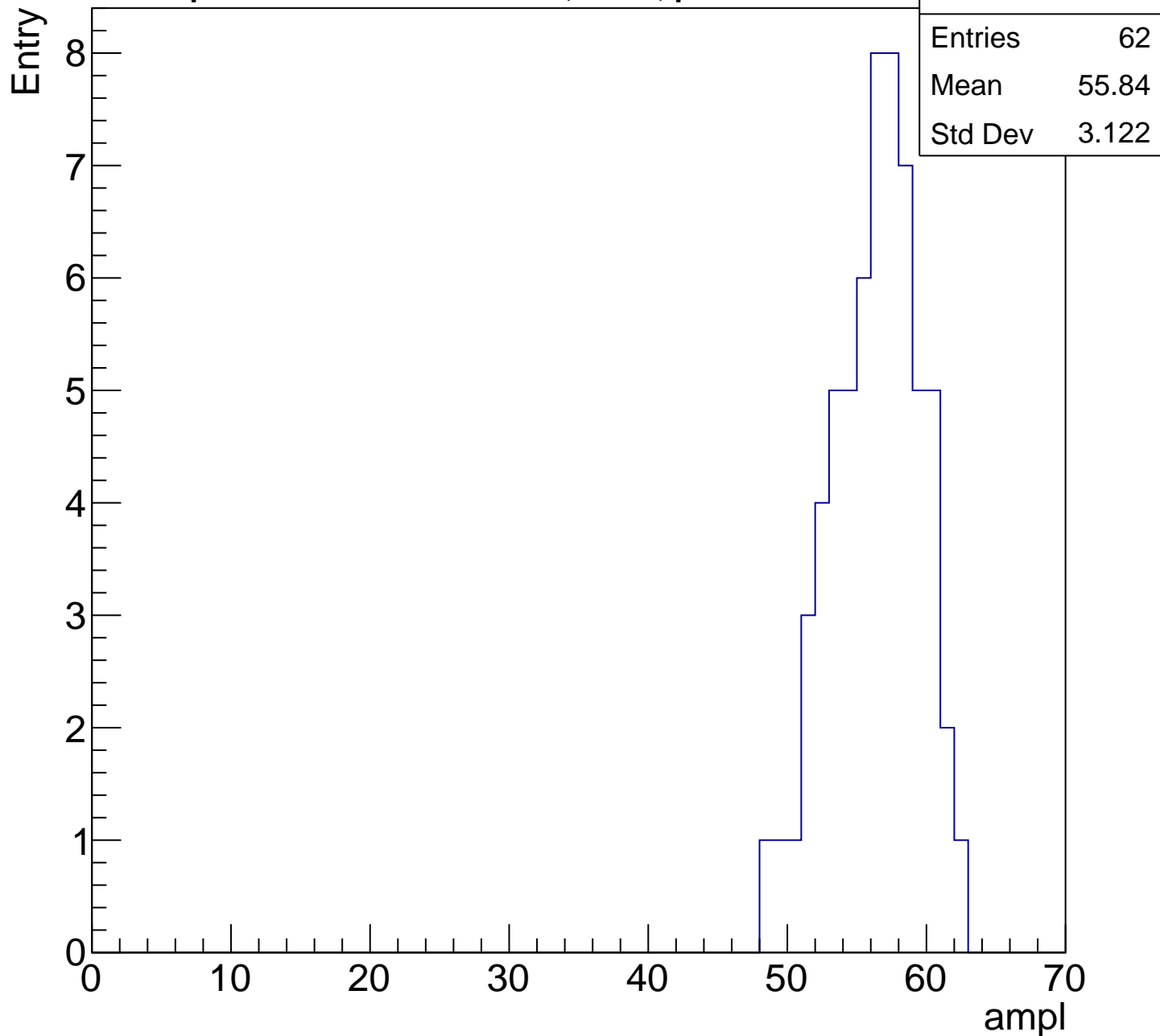
Entry

Entries	67
Mean	47.81
Std Dev	10.95



B1L103S, U13-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

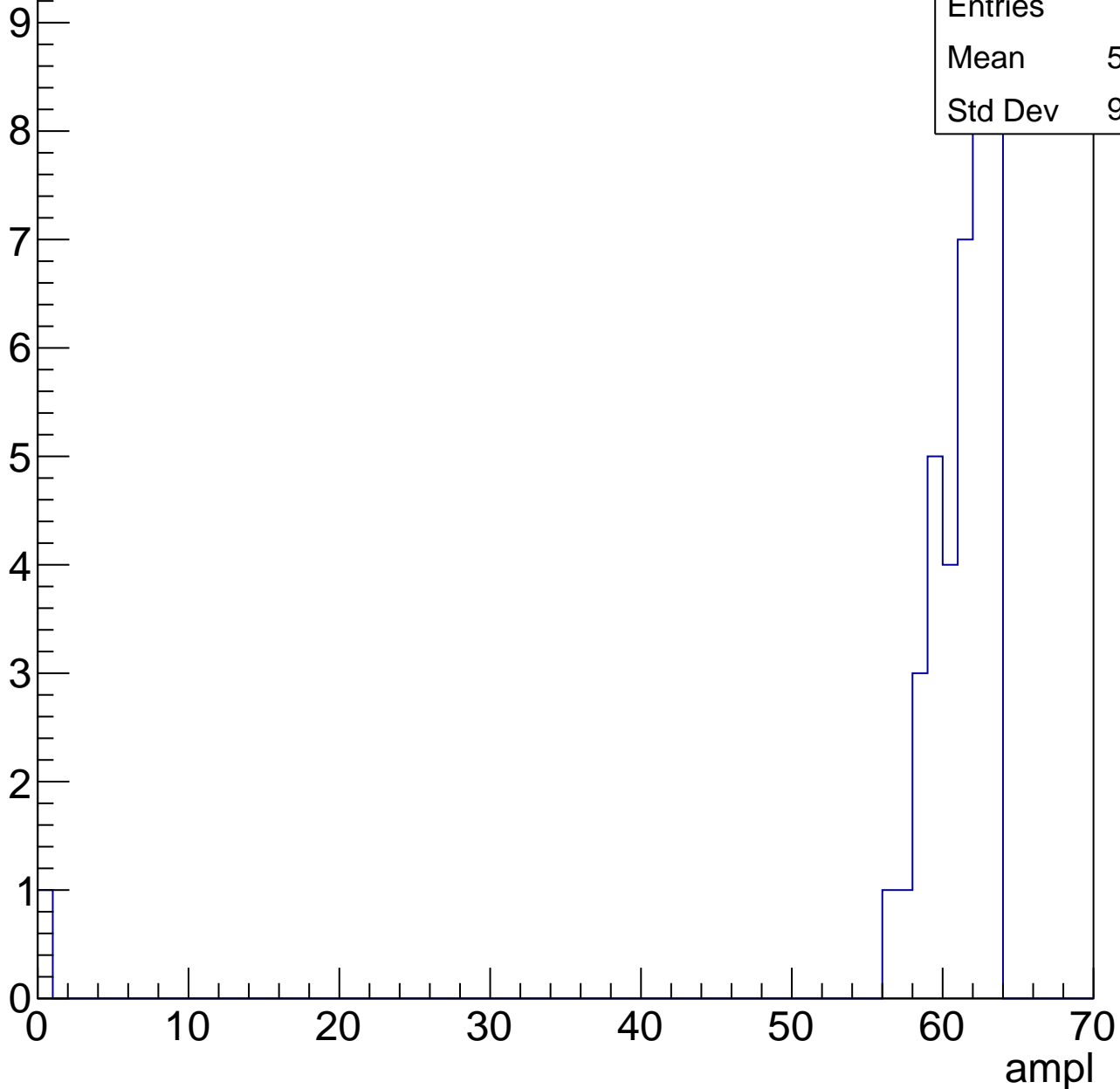


B1L103S, U13-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	59.26
Std Dev	9.787



B1L103S, U13-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

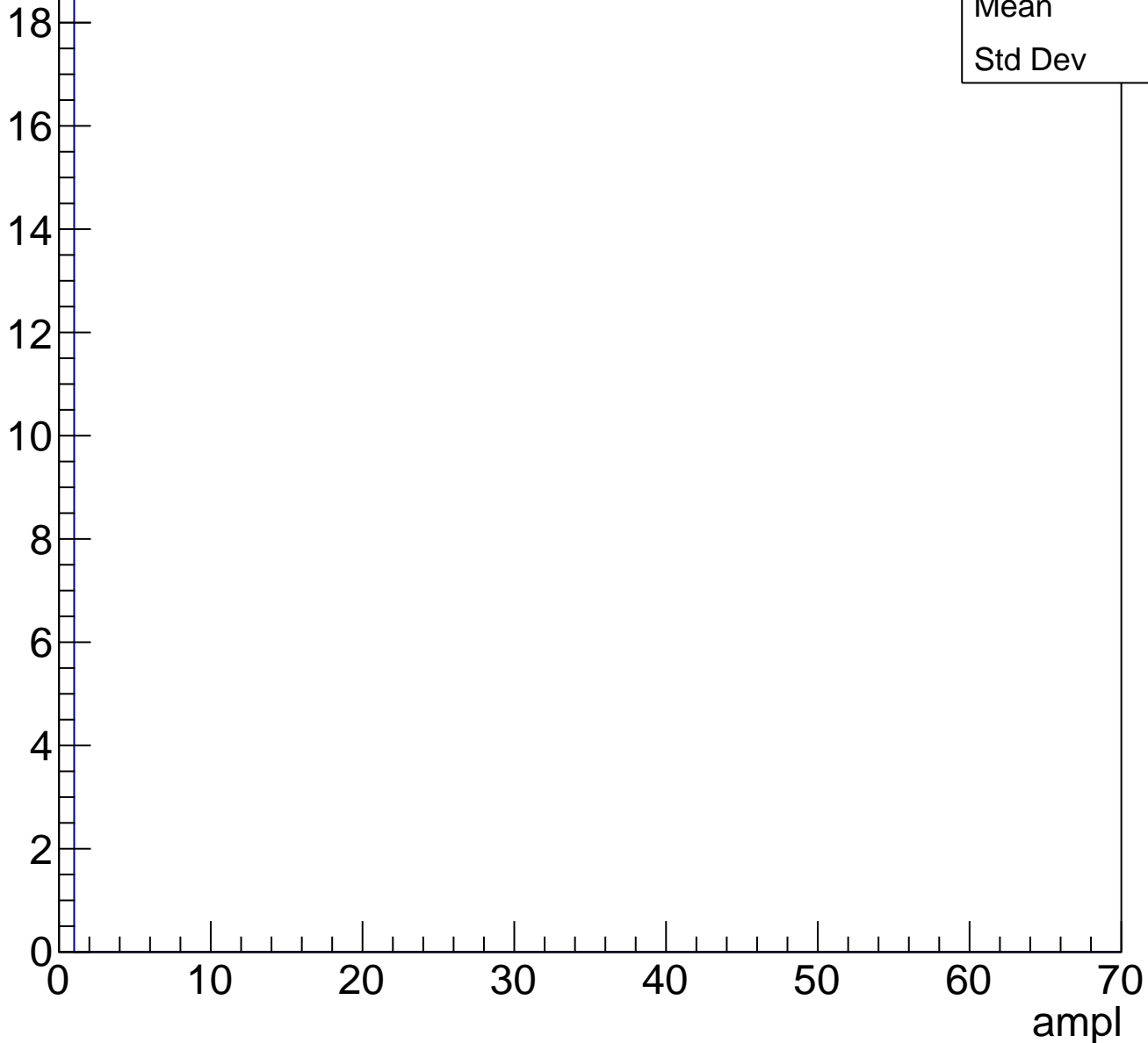
Entry



B1L103S, U13-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch37, adc0

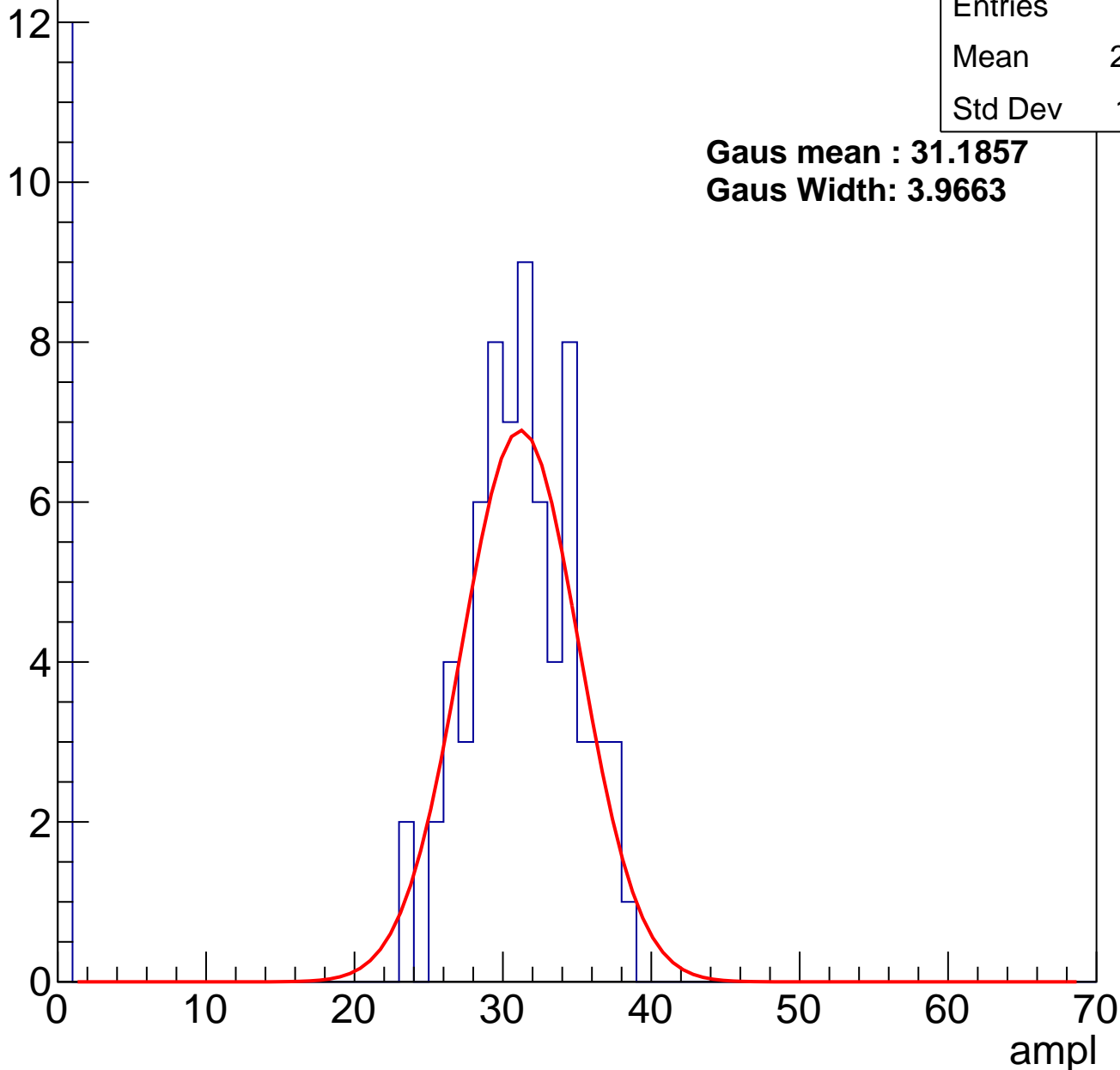
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	26.27
Std Dev	11.41

Gaus mean : 31.1857

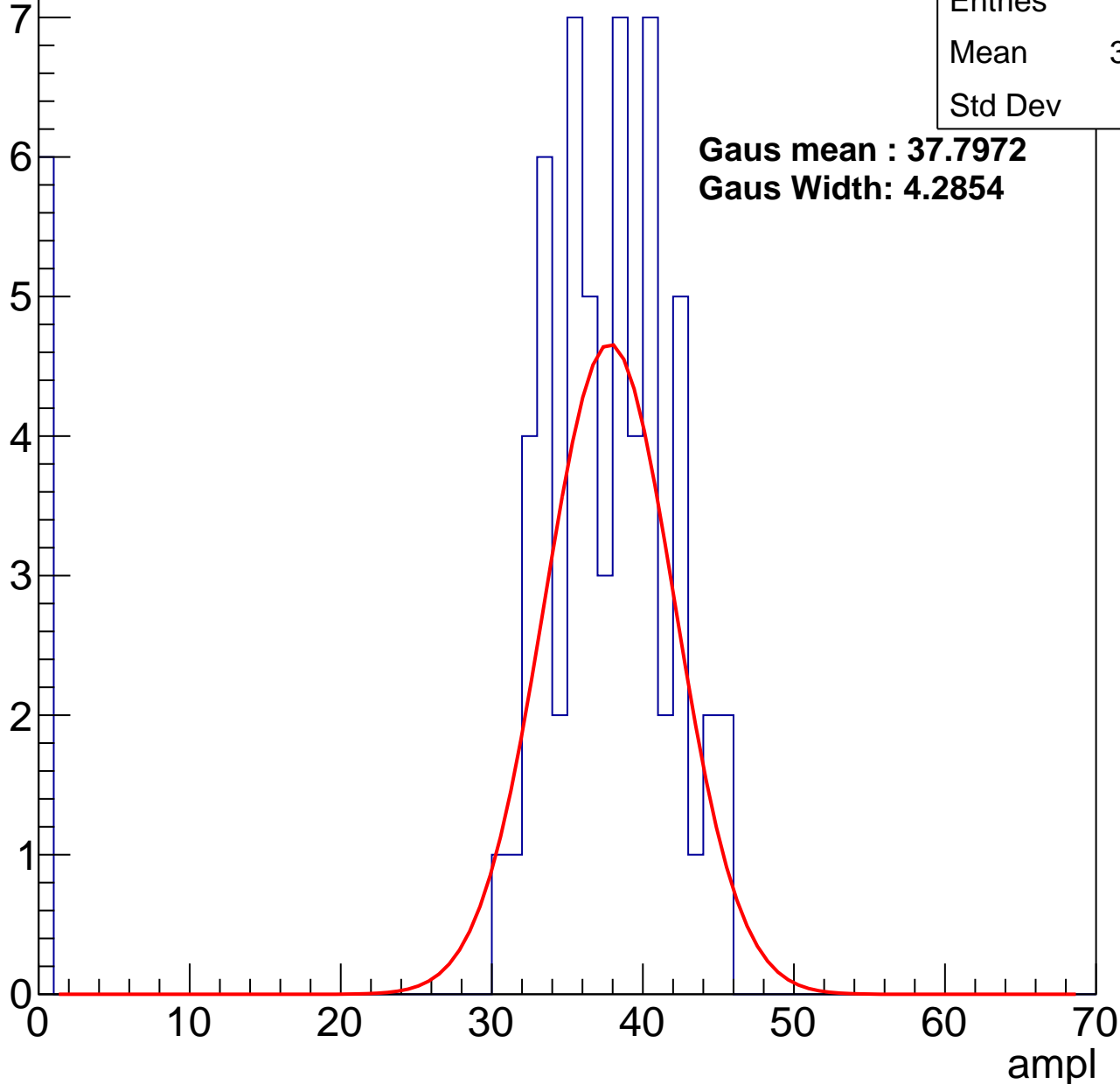
Gaus Width: 3.9663



B1L103S, U13-ch37, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch37, adc2

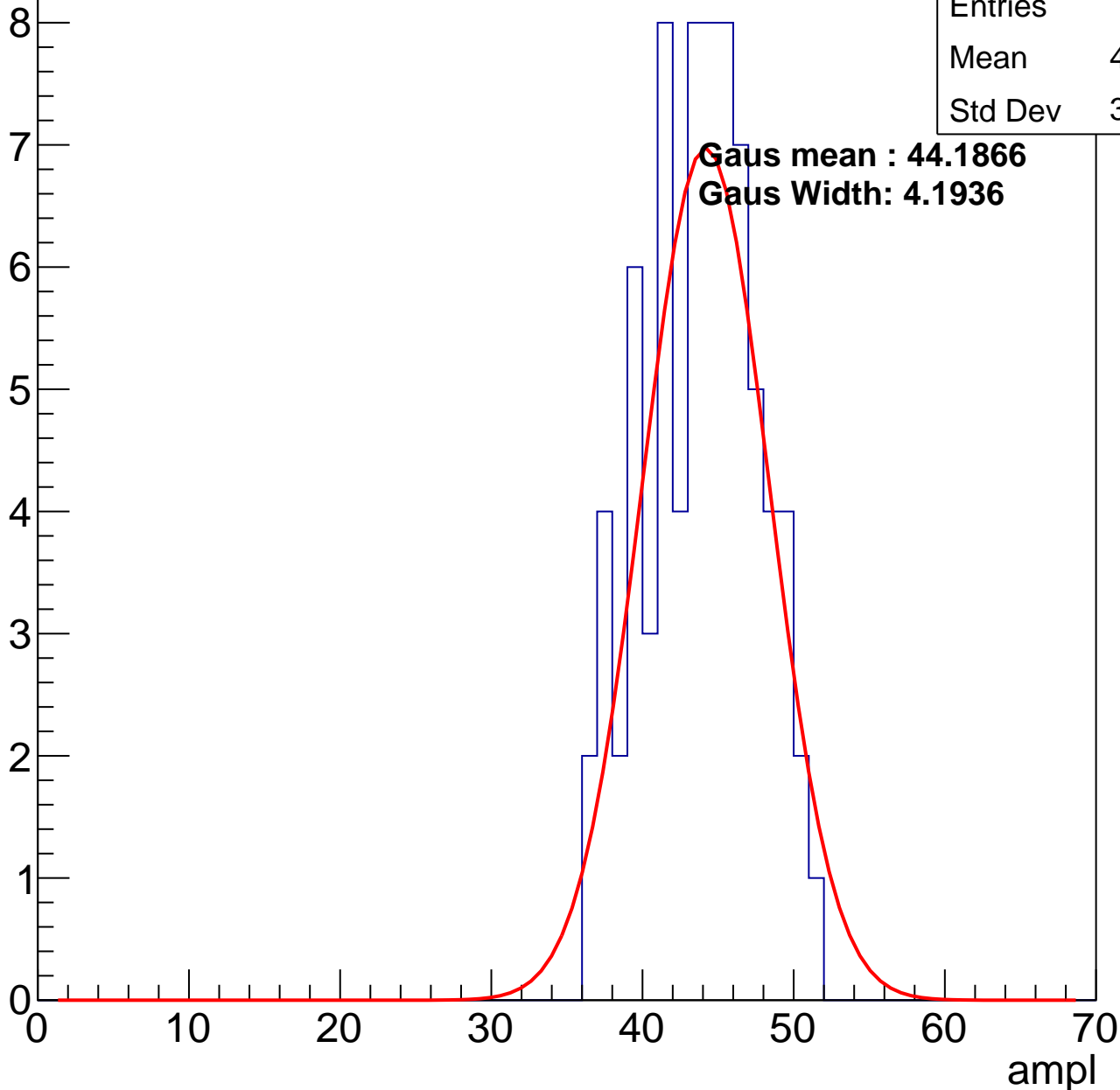
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	43.39
Std Dev	3.678

Gaus mean : 44.1866

Gaus Width: 4.1936

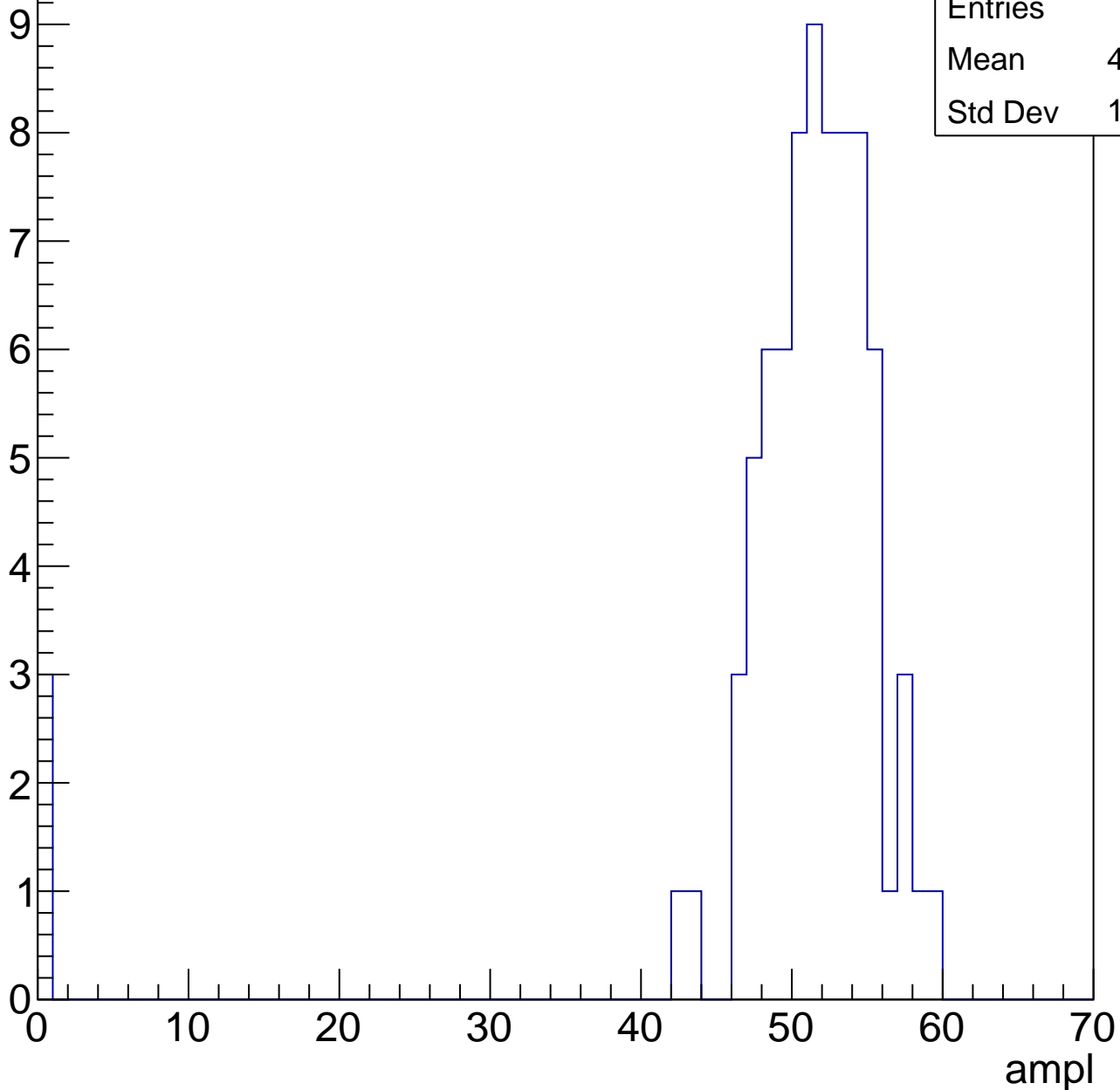


B1L103S, U13-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	49.29
Std Dev	10.39

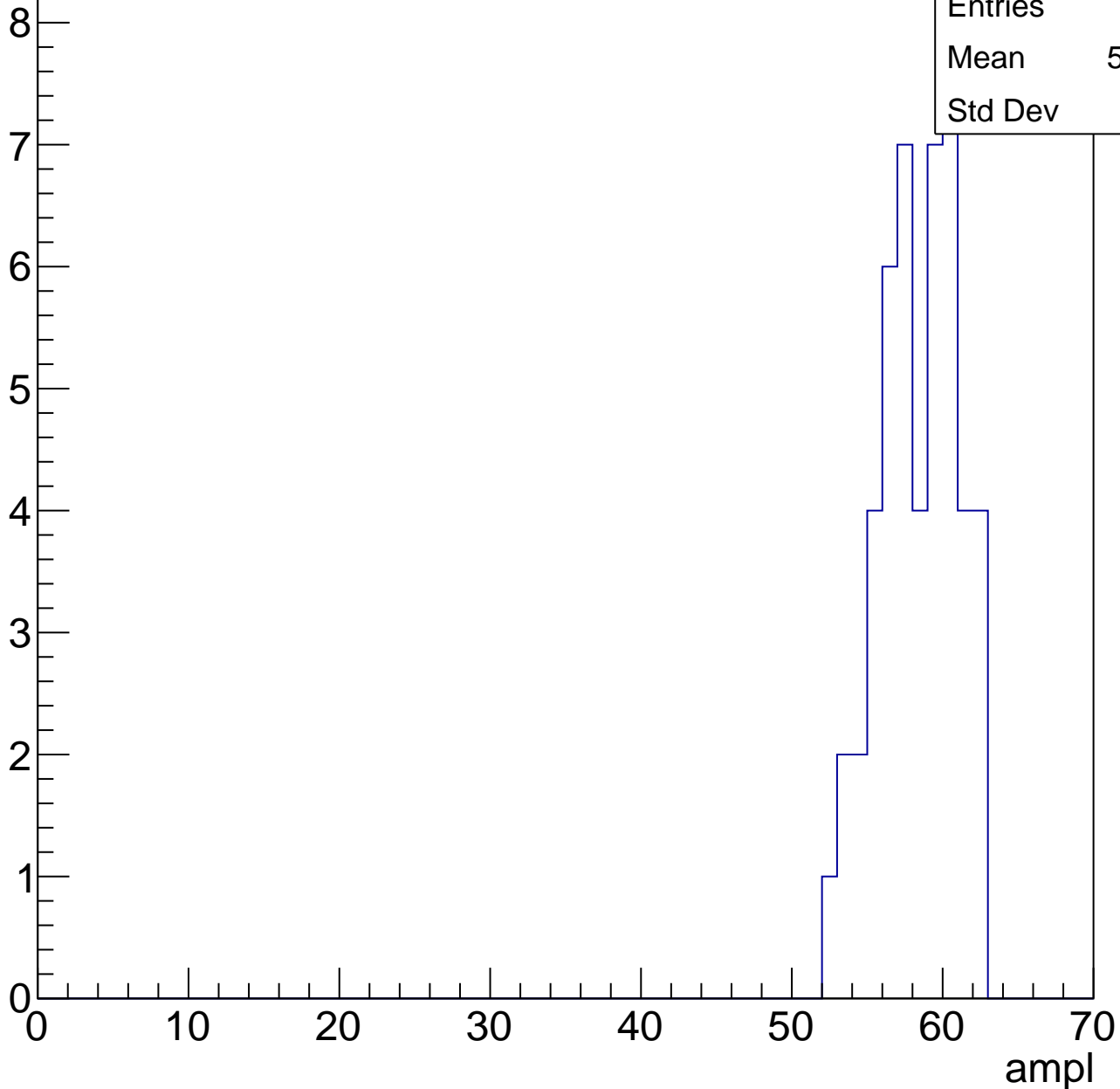


B1L103S, U13-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.92
Std Dev	2.57

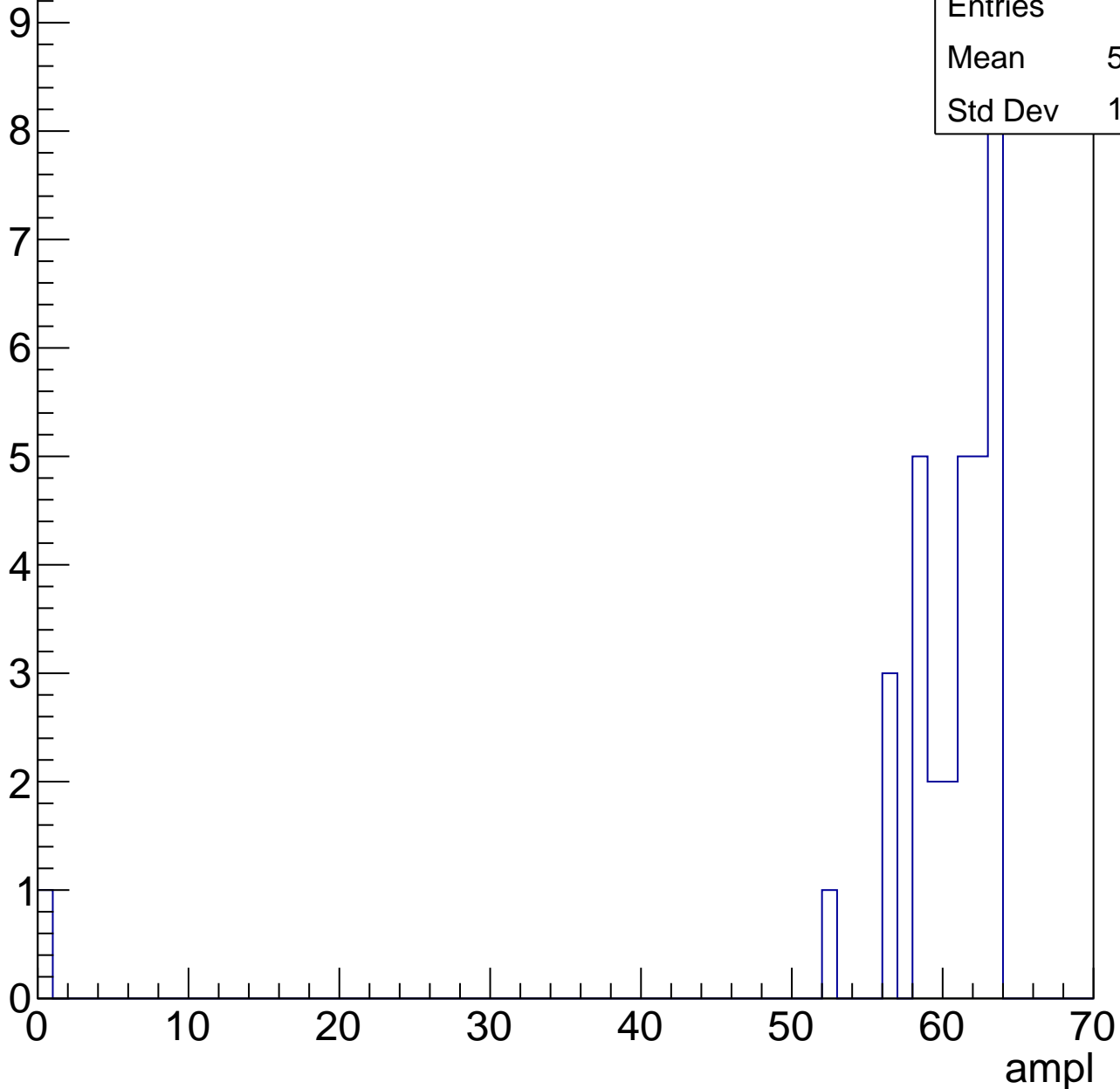


B1L103S, U13-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.48
Std Dev	10.68



B1L103S, U13-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch38, adc0

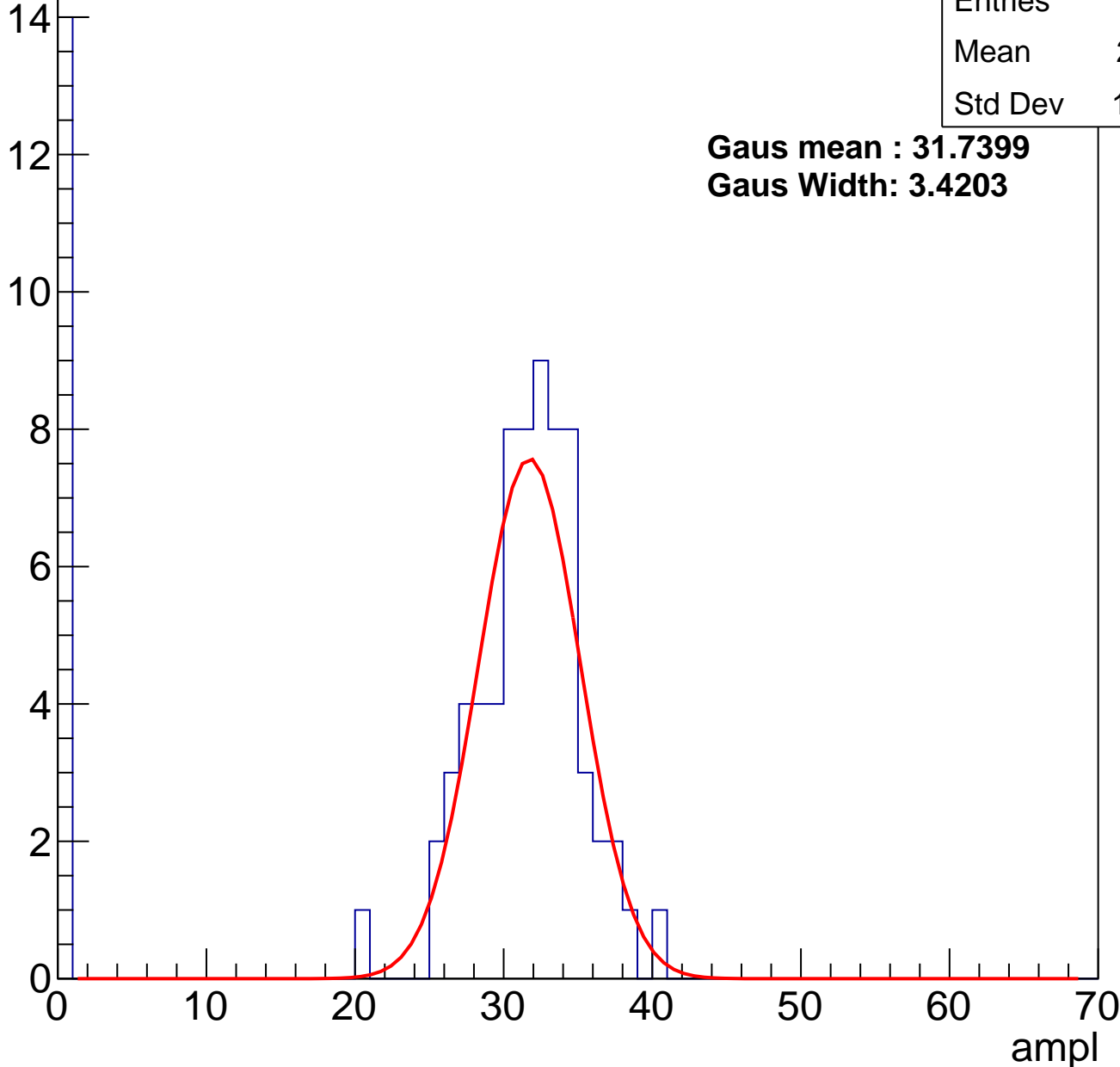
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	25.91
Std Dev	12.17

Gaus mean : 31.7399

Gaus Width: 3.4203

Entry



B1L103S, U13-ch38, adc1

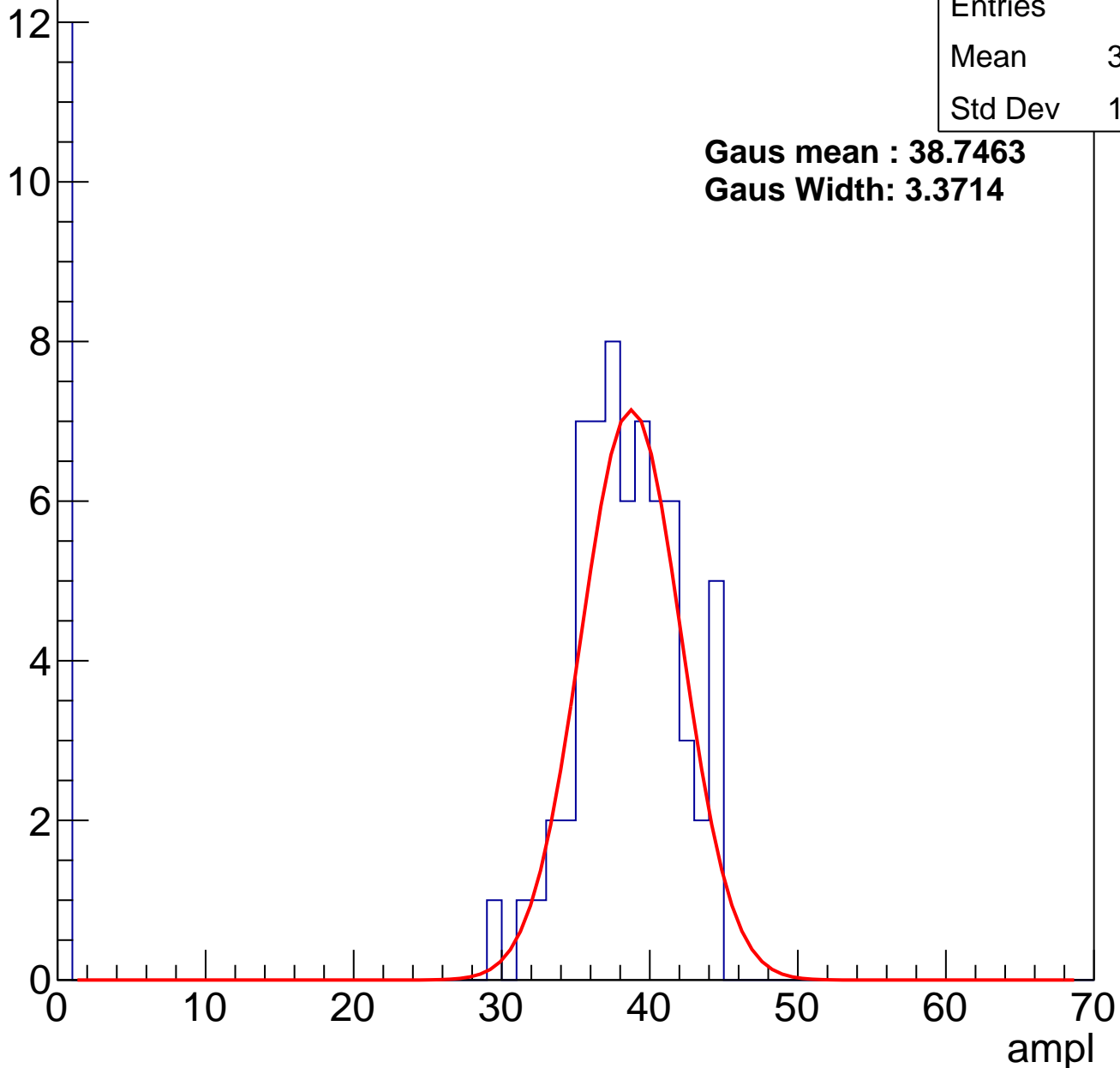
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	32.08
Std Dev	14.22

Gaus mean : 38.7463

Gaus Width: 3.3714

Entry



B1L103S, U13-ch38, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	42.38
Std Dev	8.293

Gaus mean : 44.1152

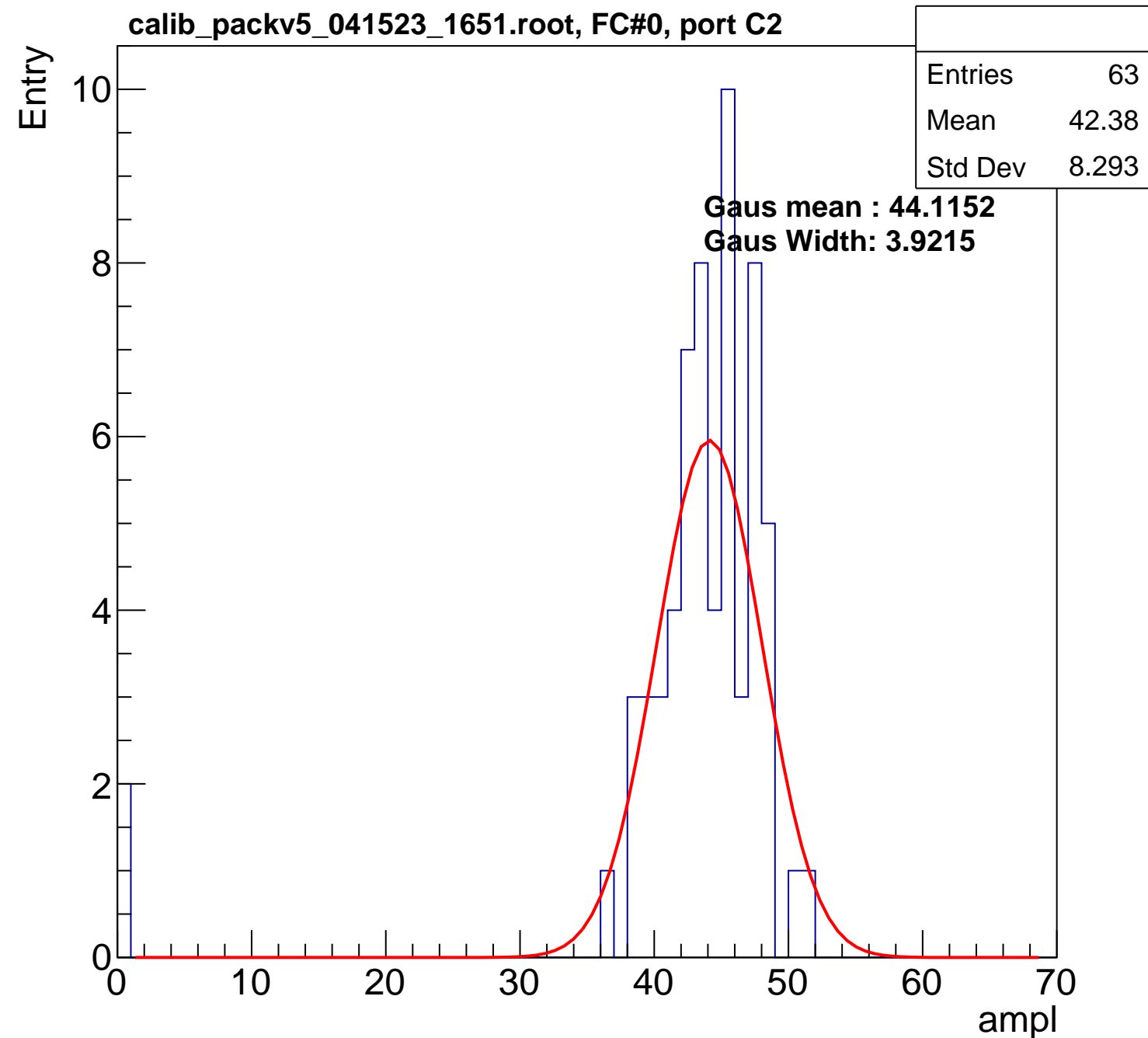
Gaus Width: 3.9215

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

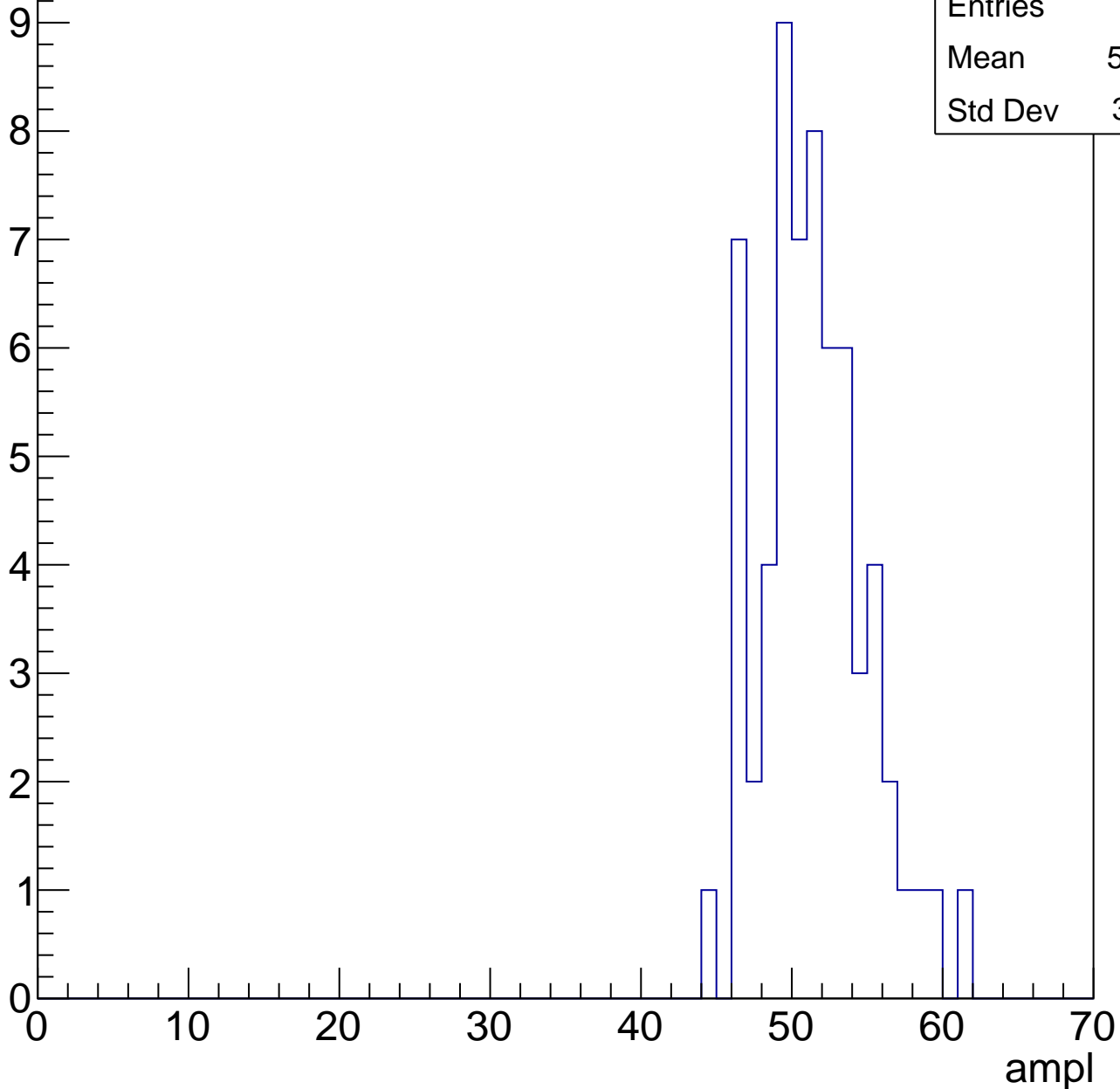


B1L103S, U13-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	50.95
Std Dev	3.461

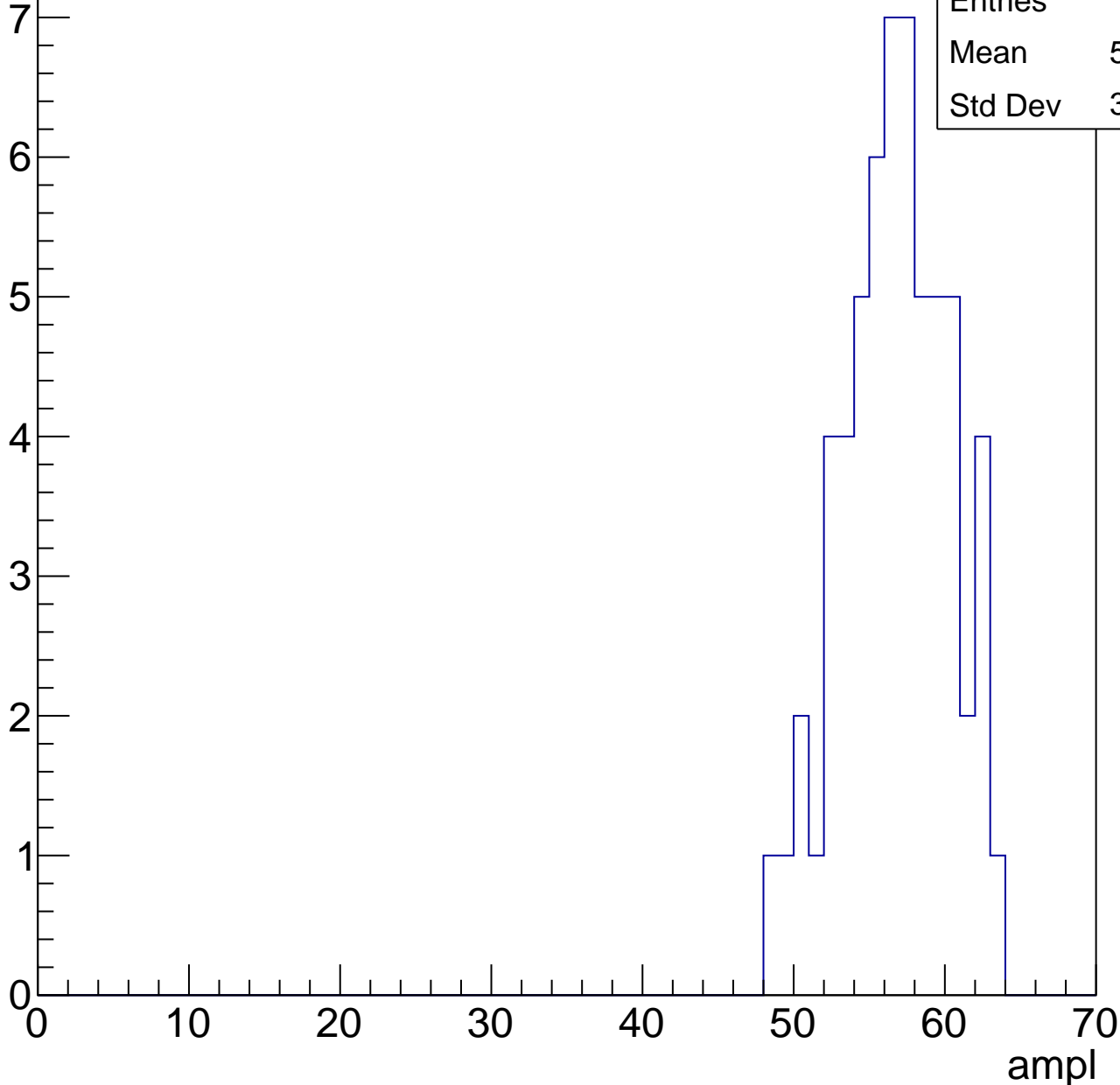


B1L103S, U13-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	56.28
Std Dev	3.479

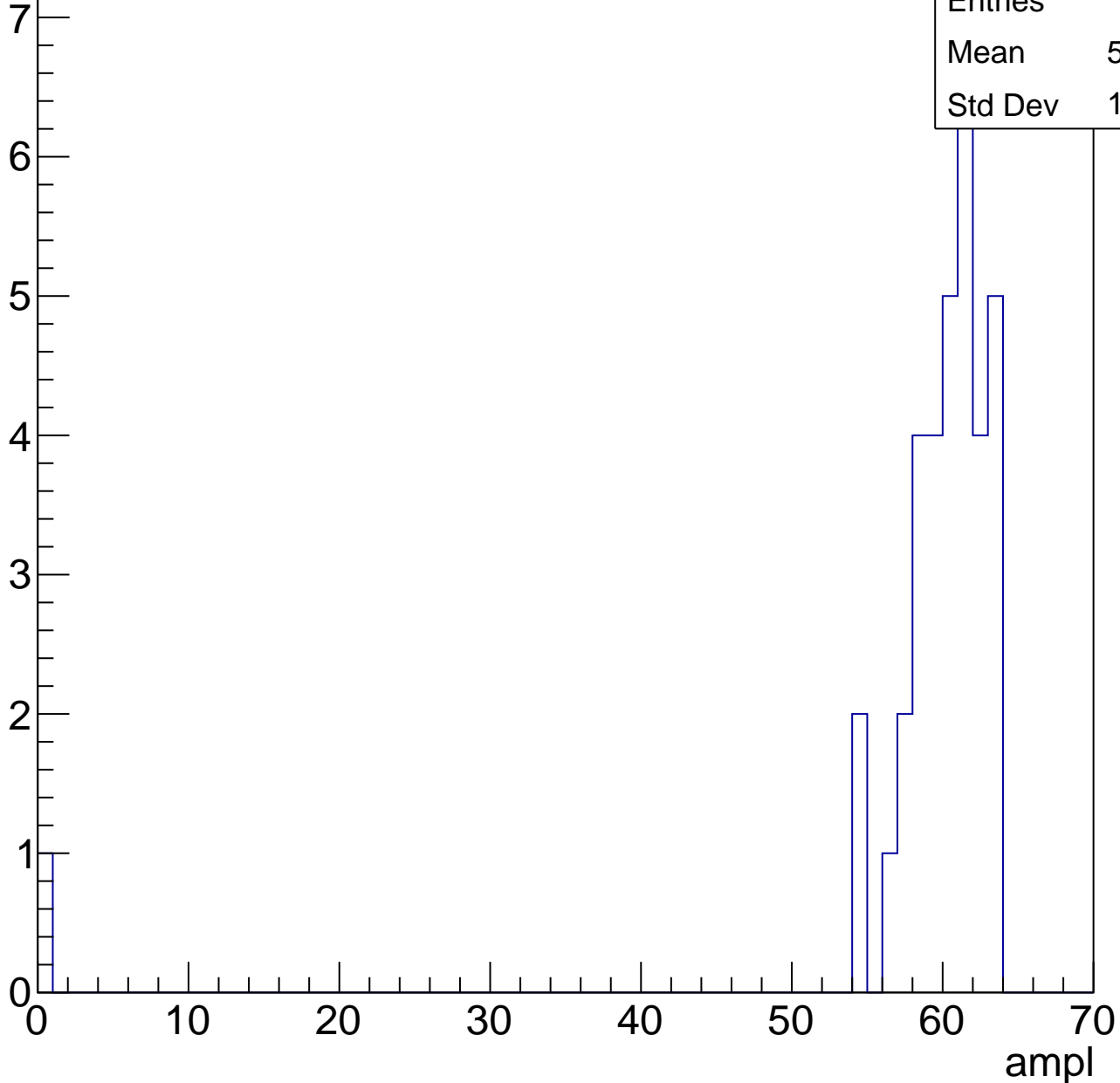


B1L103S, U13-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	58.17
Std Dev	10.25



B1L103S, U13-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

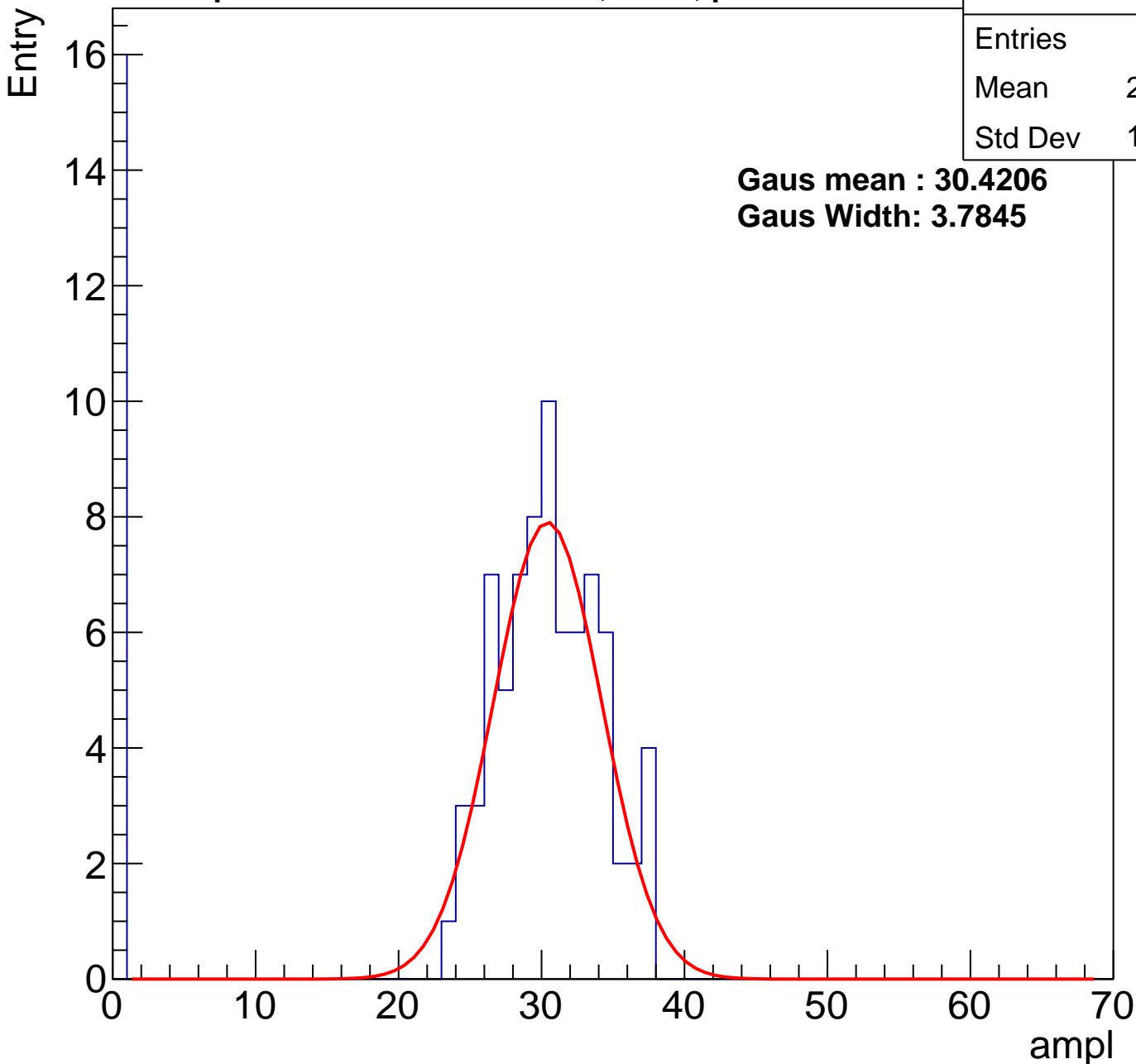
B1L103S, U13-ch39, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	24.92
Std Dev	11.79

Gaus mean : 30.4206

Gaus Width: 3.7845



B1L103S, U13-ch39, adc1

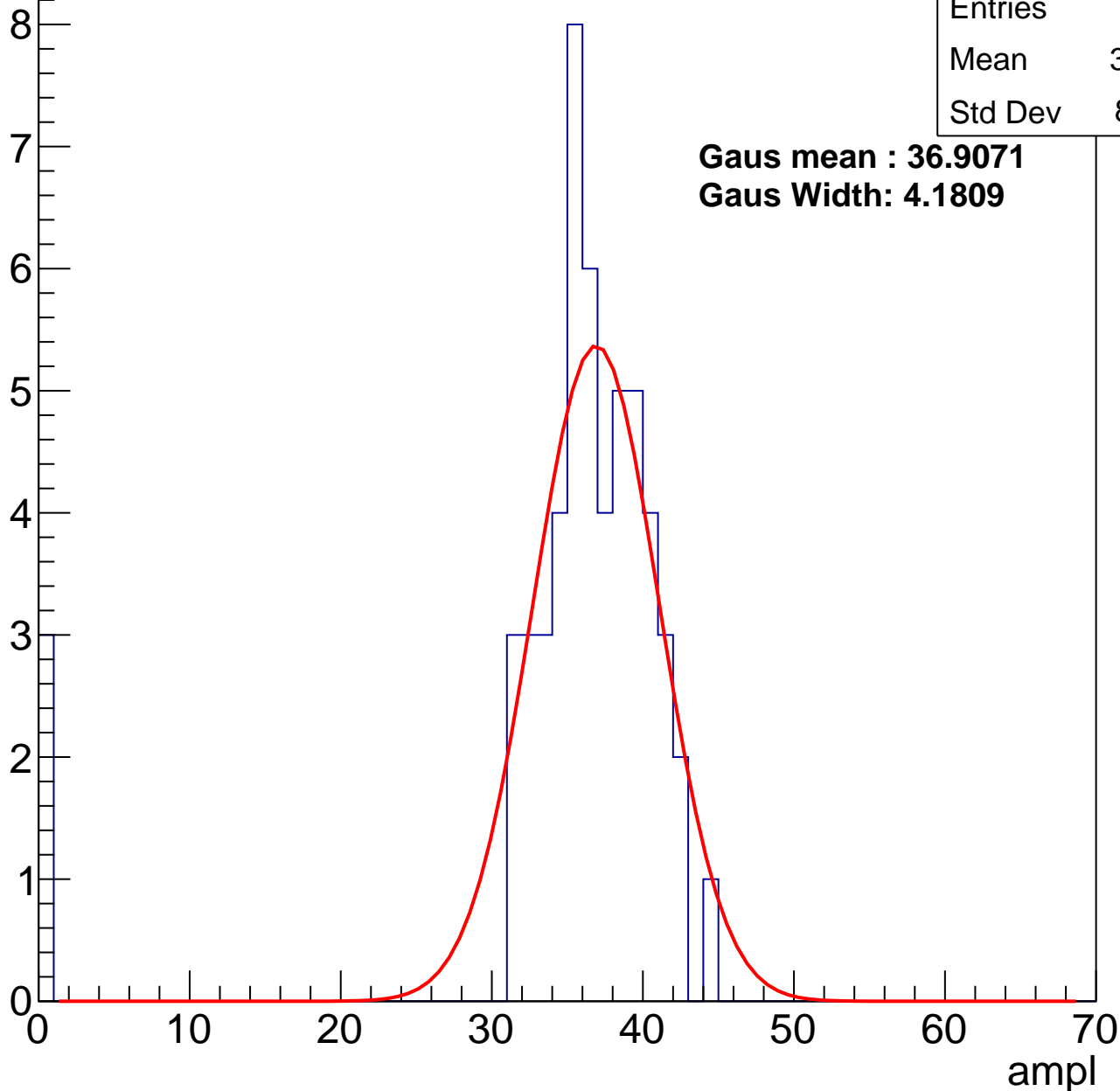
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	34.52
Std Dev	8.911

Gaus mean : 36.9071

Gaus Width: 4.1809



B1L103S, U13-ch39, adc2

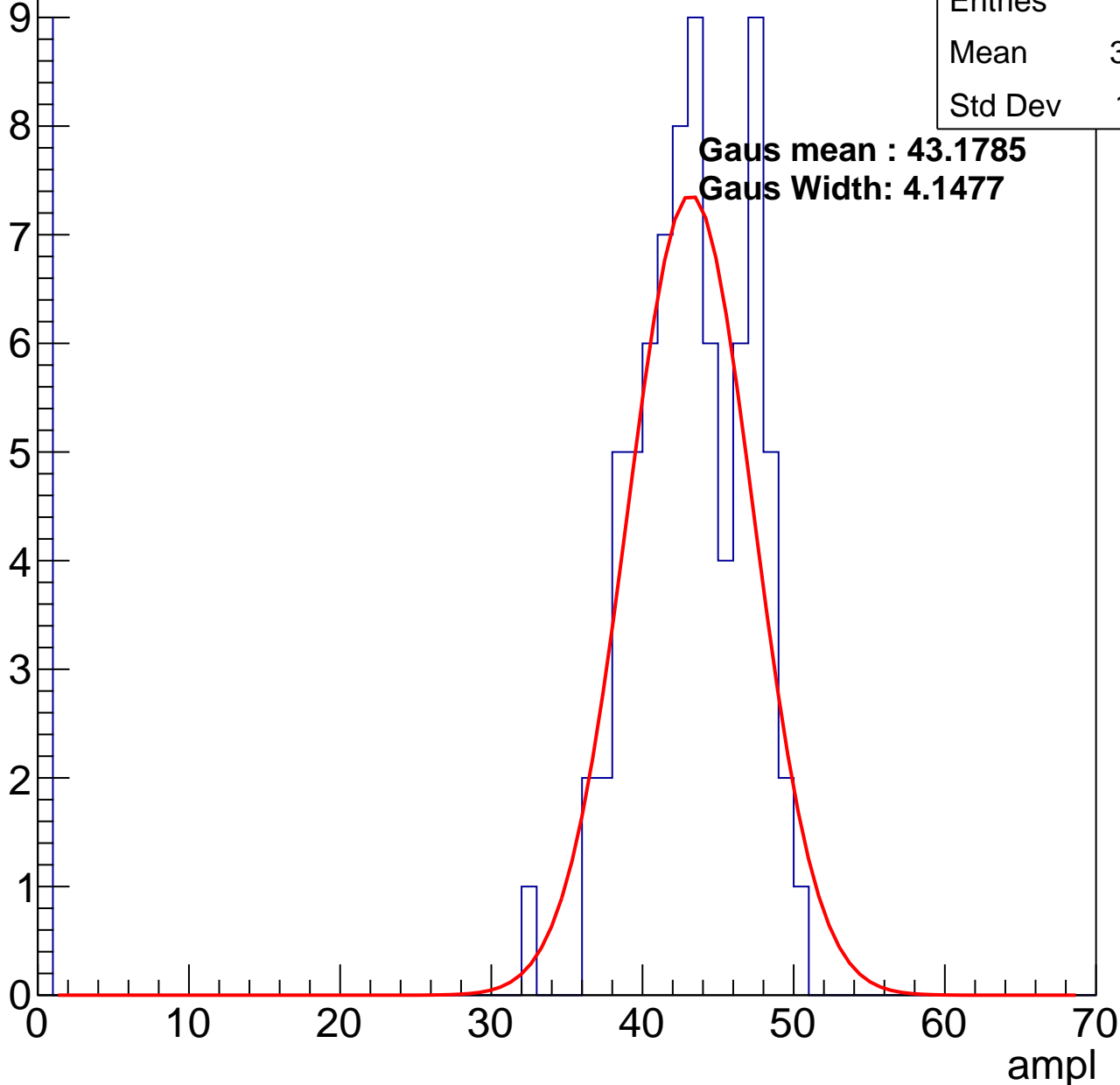
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	38.44
Std Dev	13.51

Gaus mean : 43.1785

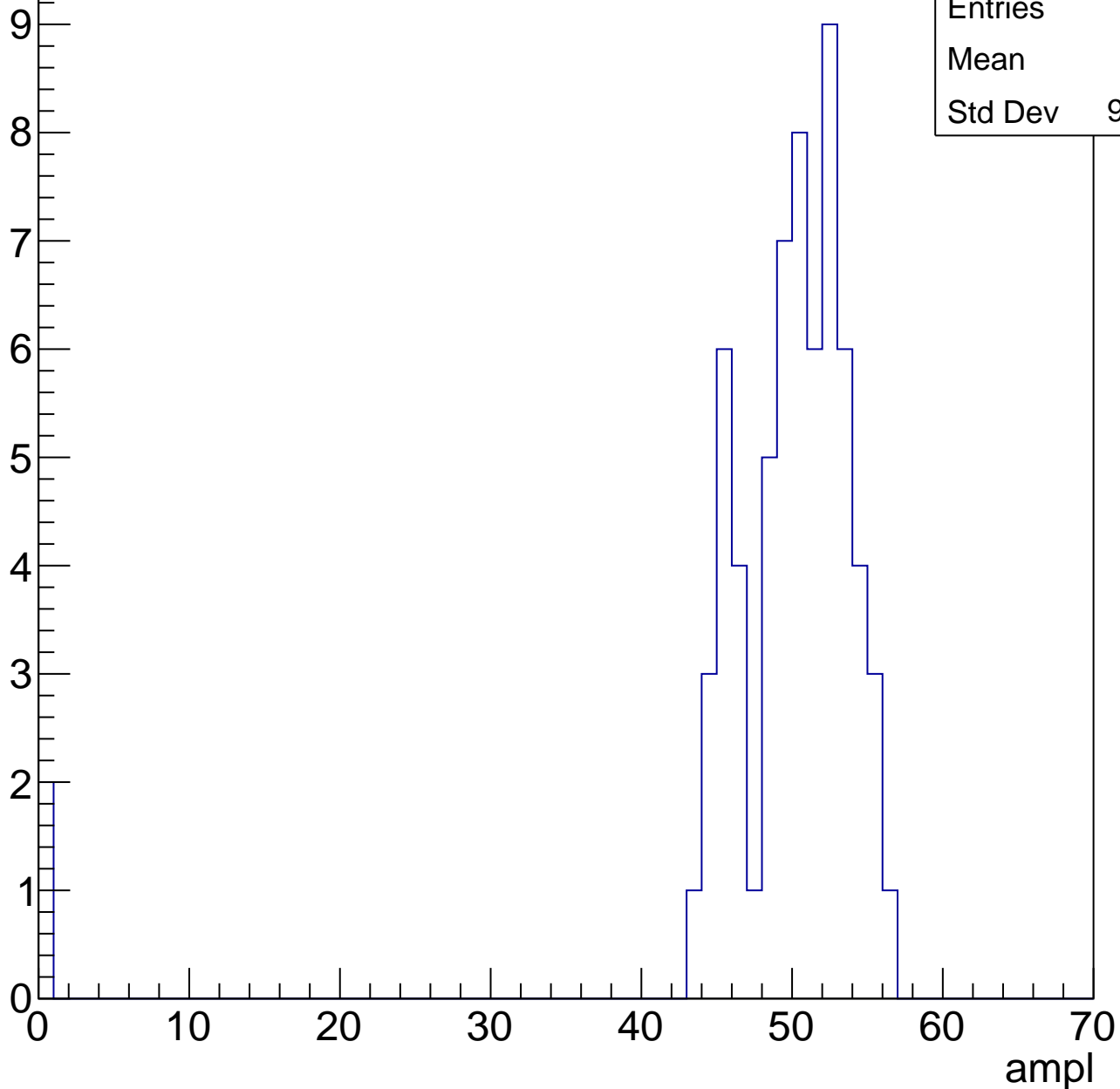
Gaus Width: 4.1477



B1L103S, U13-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

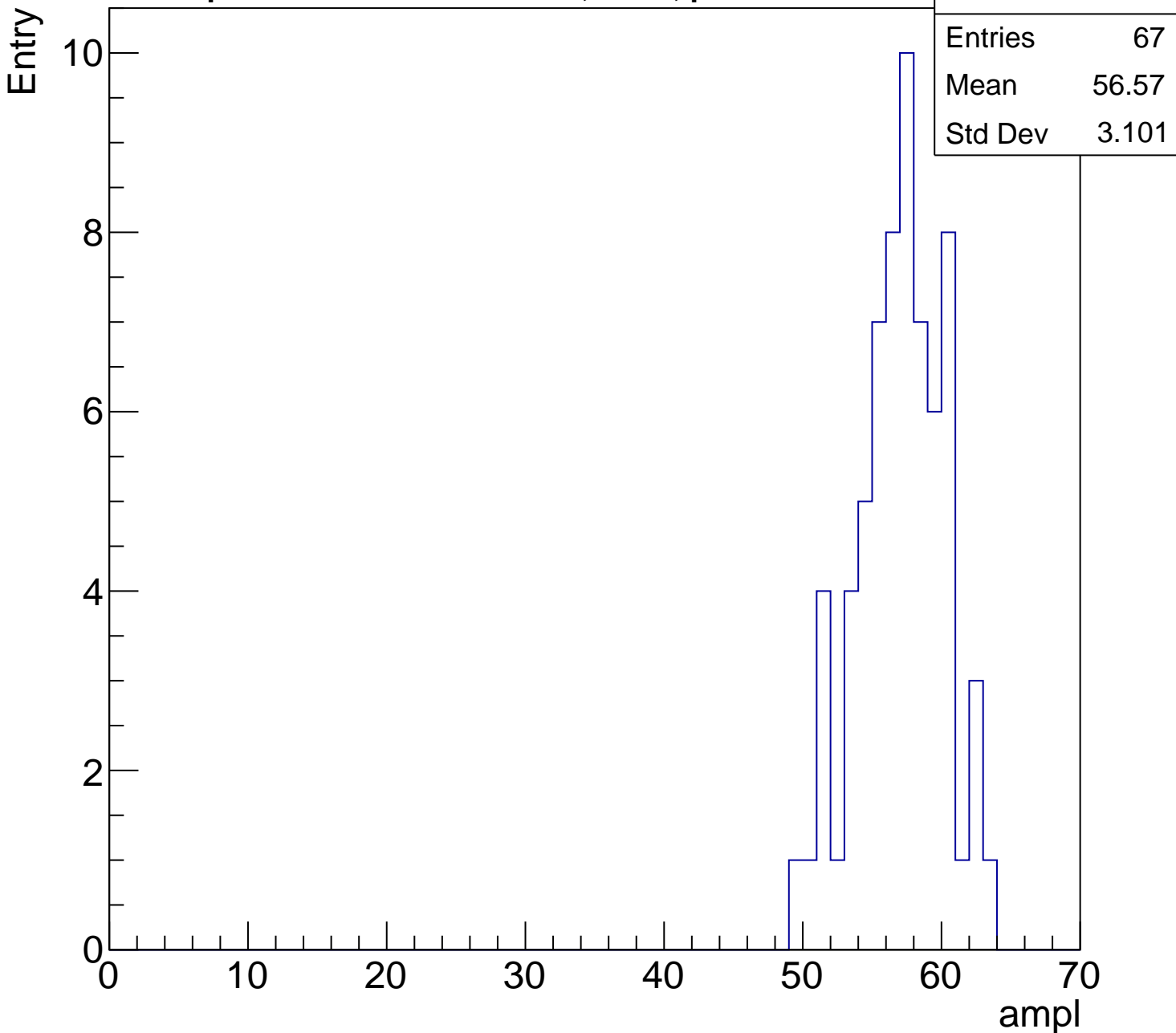
Entries	67
Mean	56.57
Std Dev	3.101

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

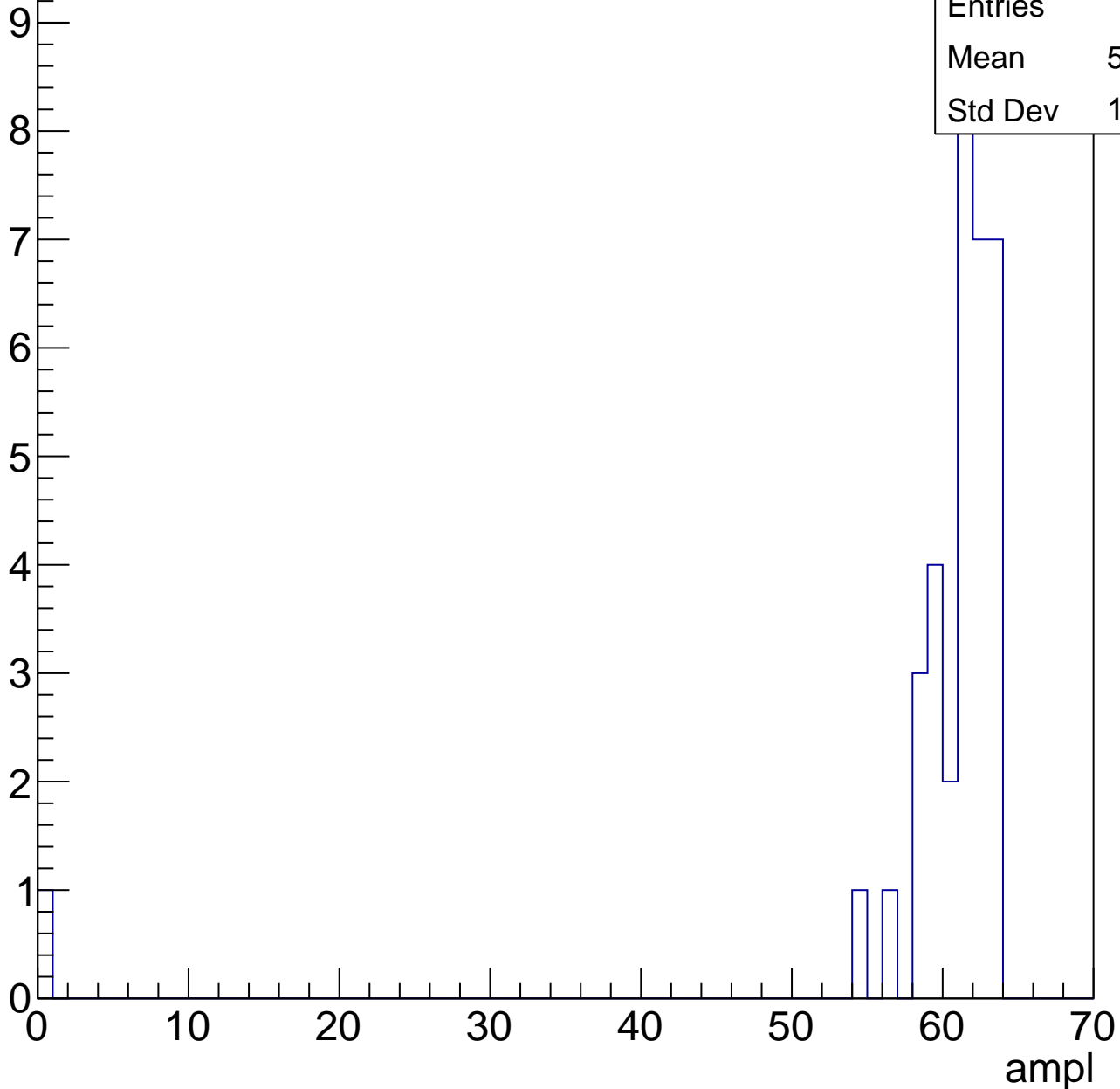


B1L103S, U13-ch39, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	58.97
Std Dev	10.32



B1L103S, U13-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L103S, U13-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



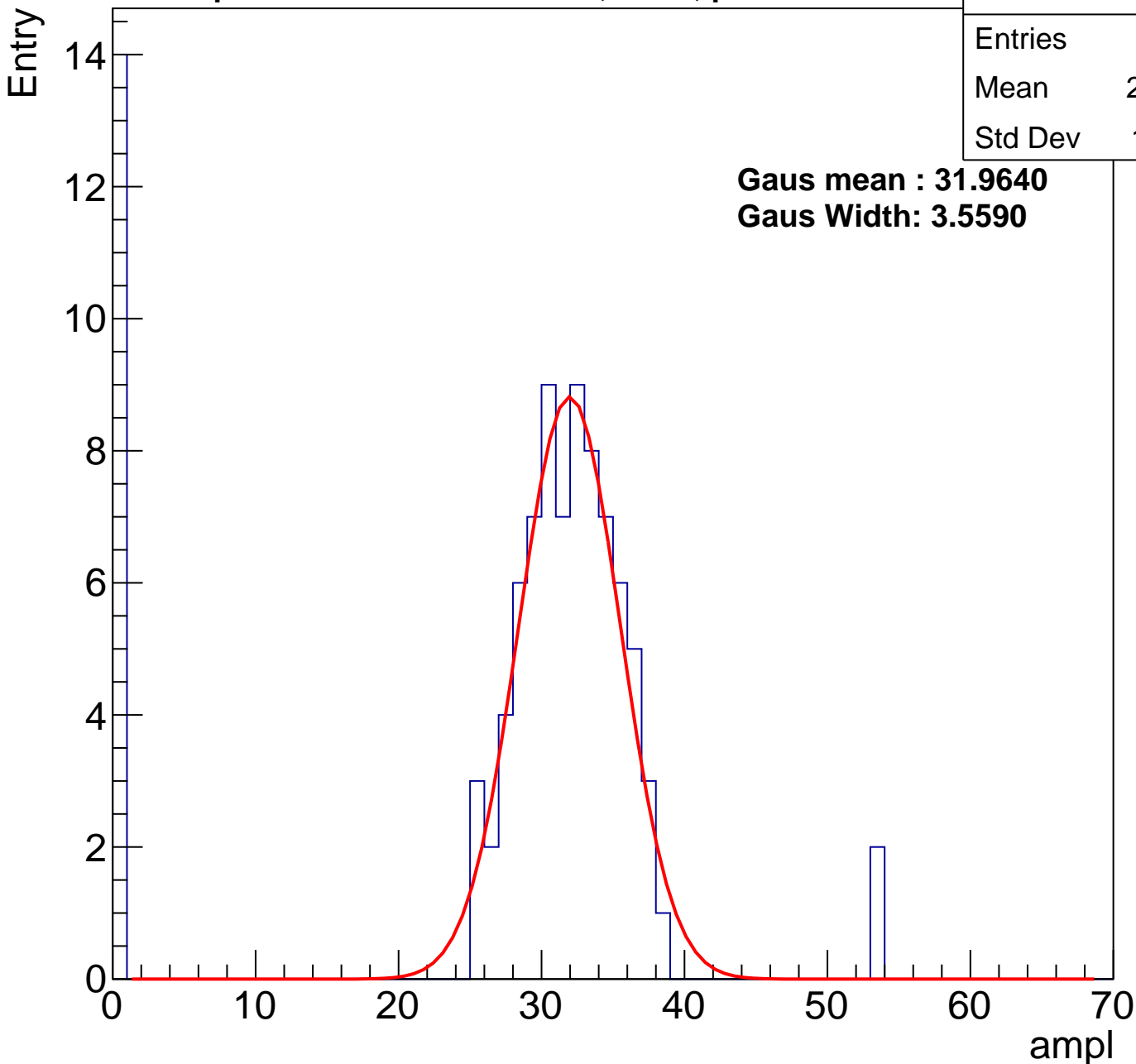
B1L103S, U13-ch40, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	27.18
Std Dev	12.21

Gaus mean : 31.9640

Gaus Width: 3.5590



B1L103S, U13-ch40, adc1

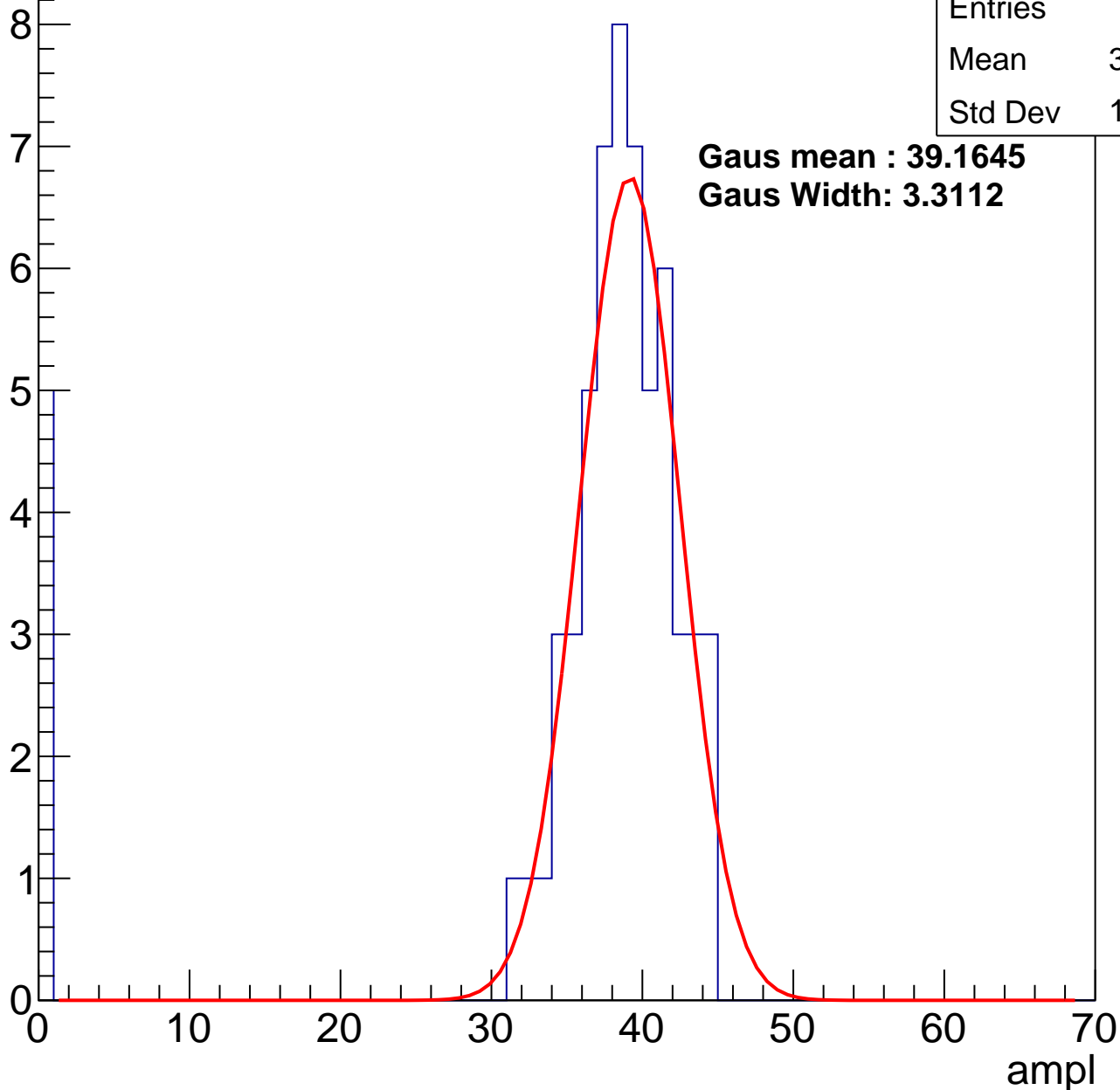
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	35.28
Std Dev	10.93

Gaus mean : 39.1645

Gaus Width: 3.3112



B1L103S, U13-ch40, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	40.42
Std Dev	13.88

Gaus mean : 45.6135

Gaus Width: 3.8019

Entry

10

8

6

4

2

0

0

10

20

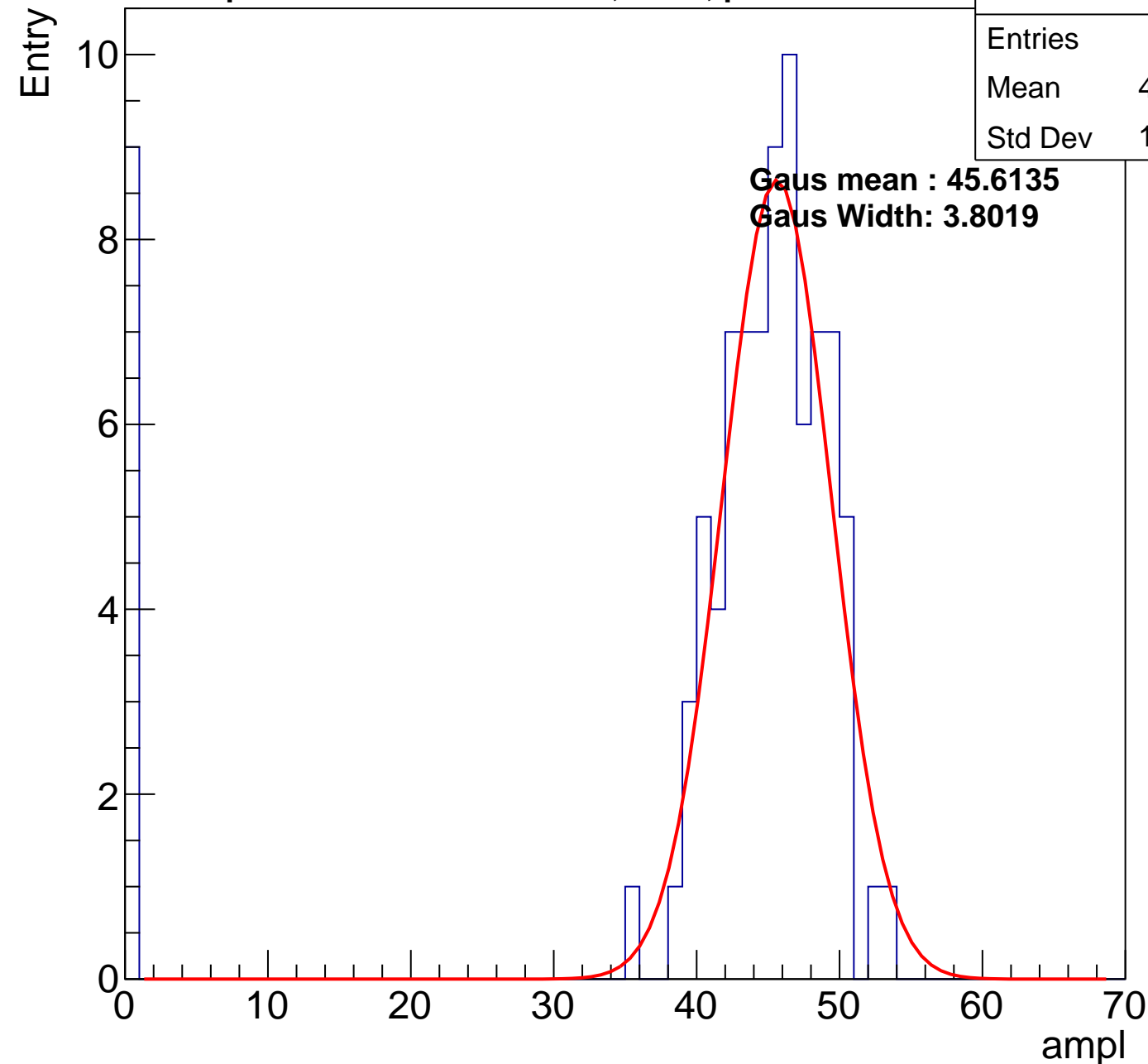
30

40

50

60

ampl

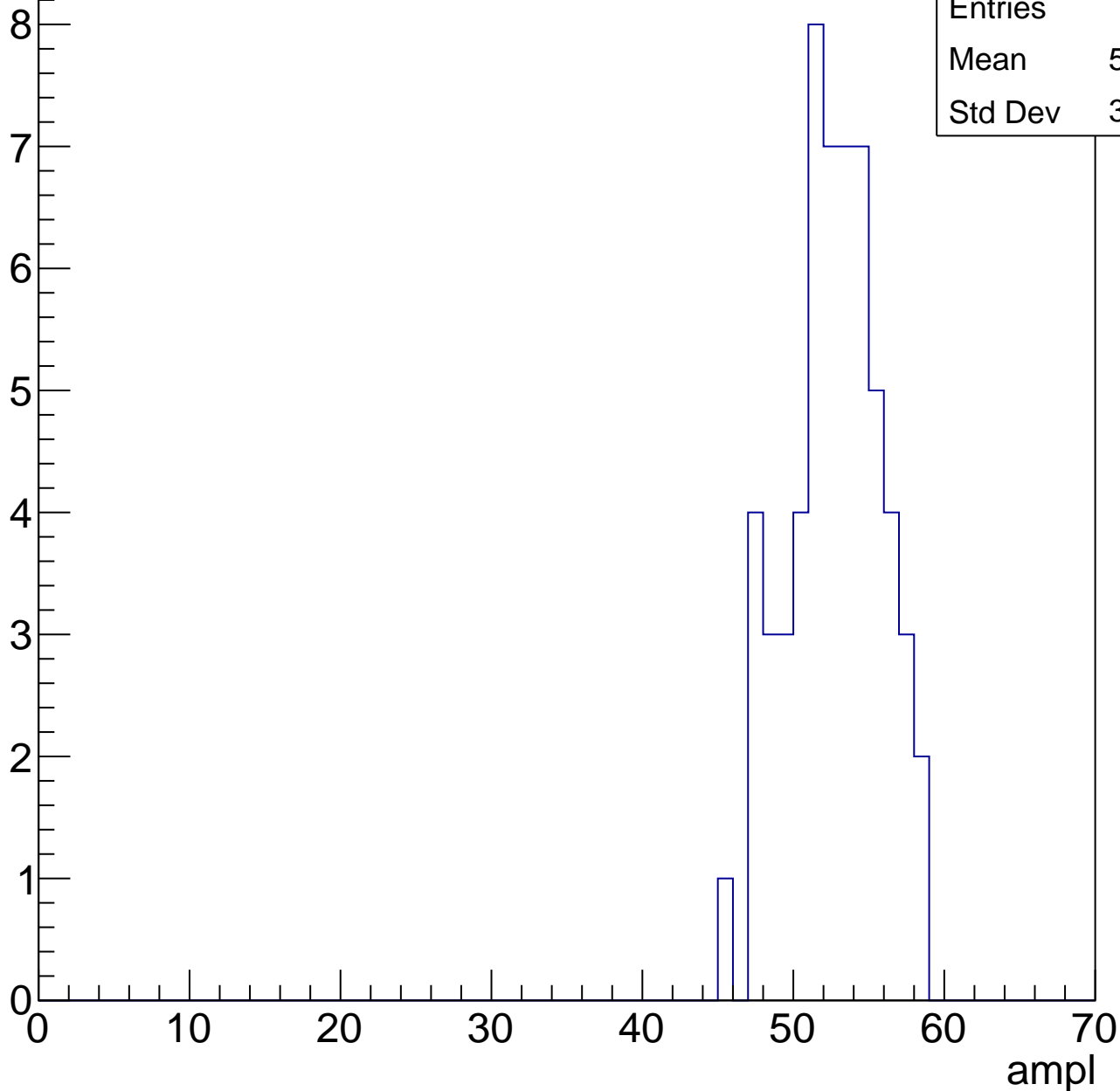


B1L103S, U13-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.26
Std Dev	3.037

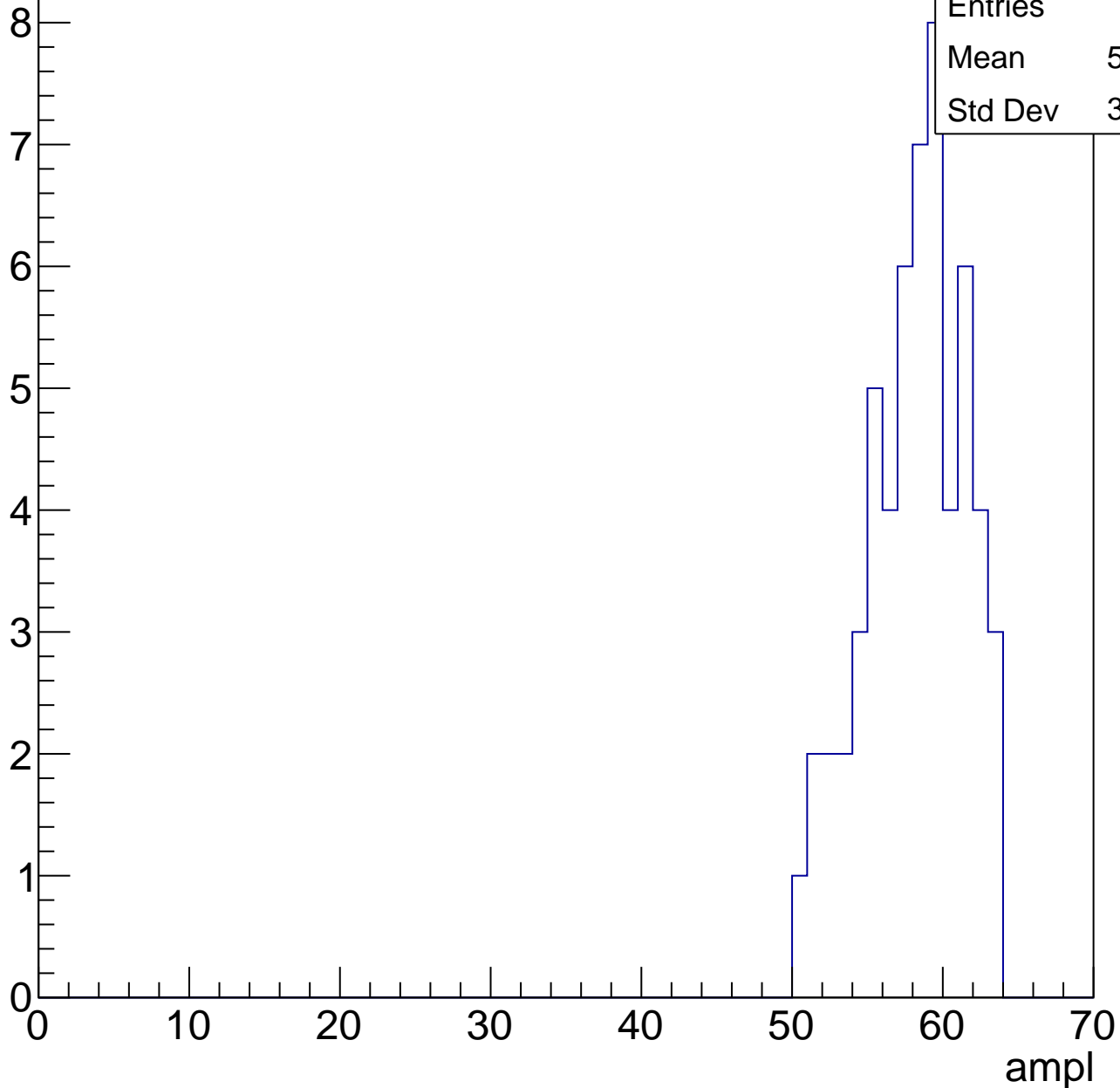


B1L103S, U13-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

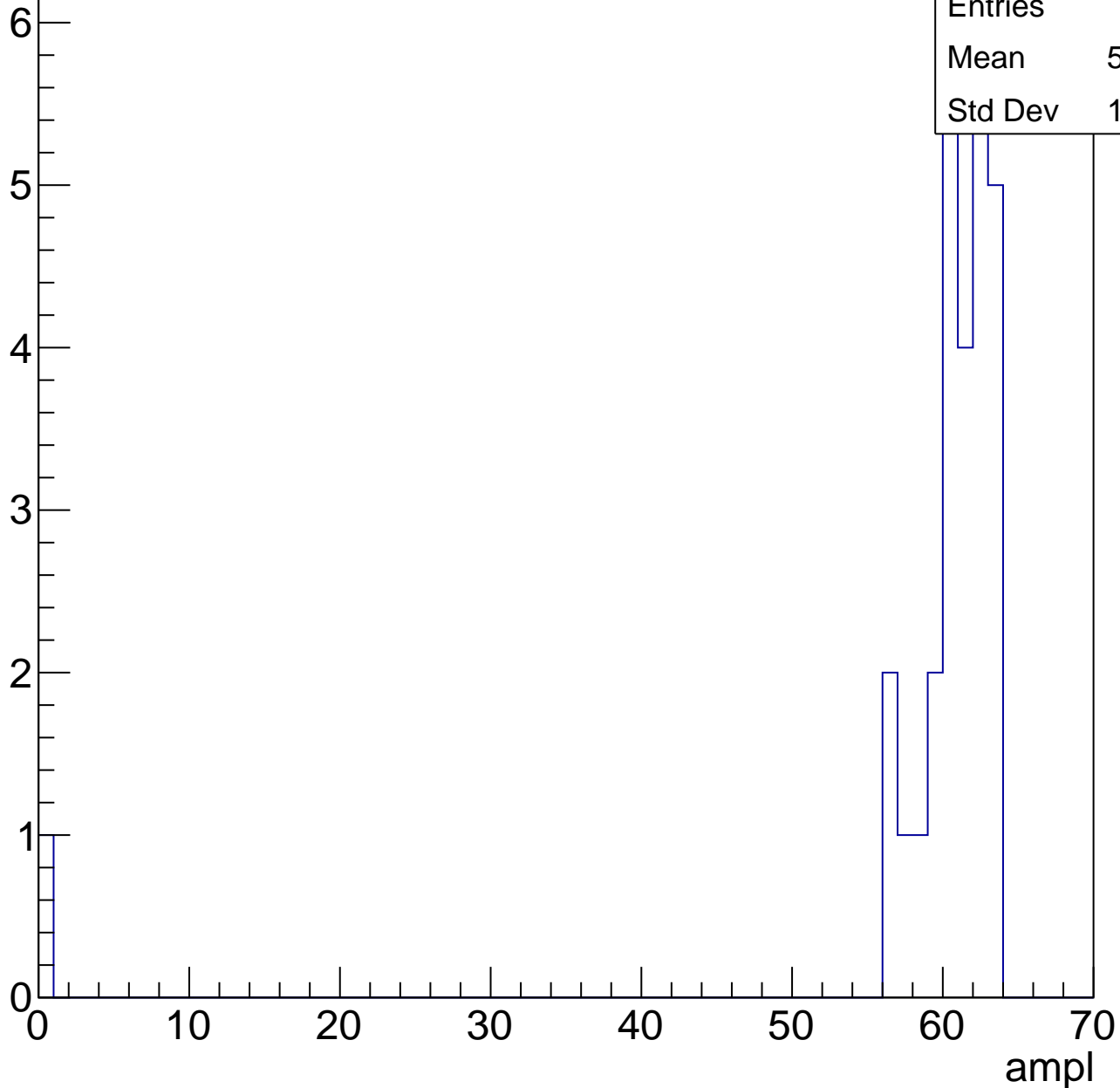
Entries	57
Mean	57.65
Std Dev	3.269



B1L103S, U13-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch41, adc0

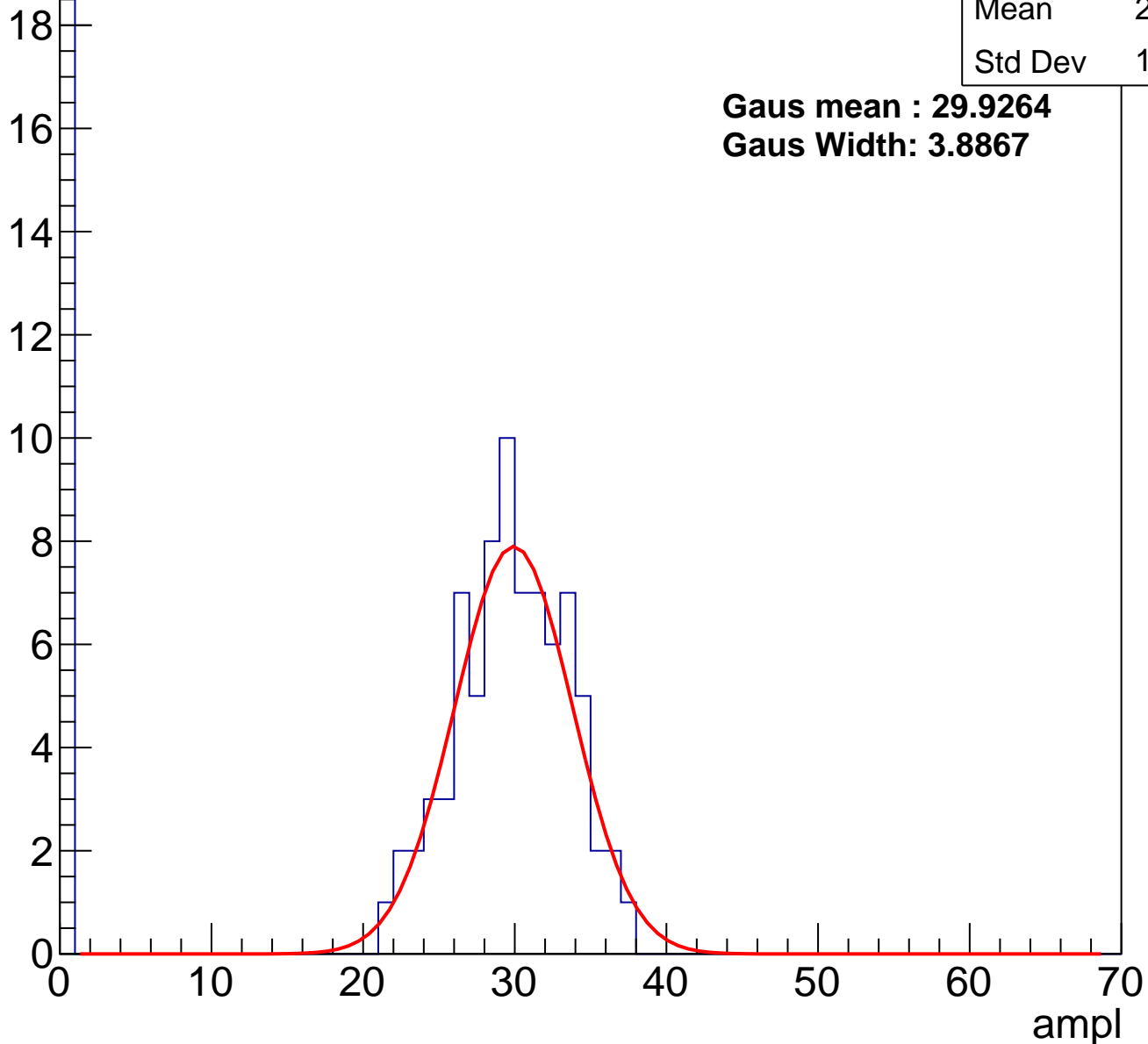
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	23.59
Std Dev	12.08

Gaus mean : 29.9264

Gaus Width: 3.8867

Entry



B1L103S, U13-ch41, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	31.56
Std Dev	12.86

Gaus mean : 37.2373

Gaus Width: 4.1023

Entry

10

8

6

4

2

0

0

10

20

30

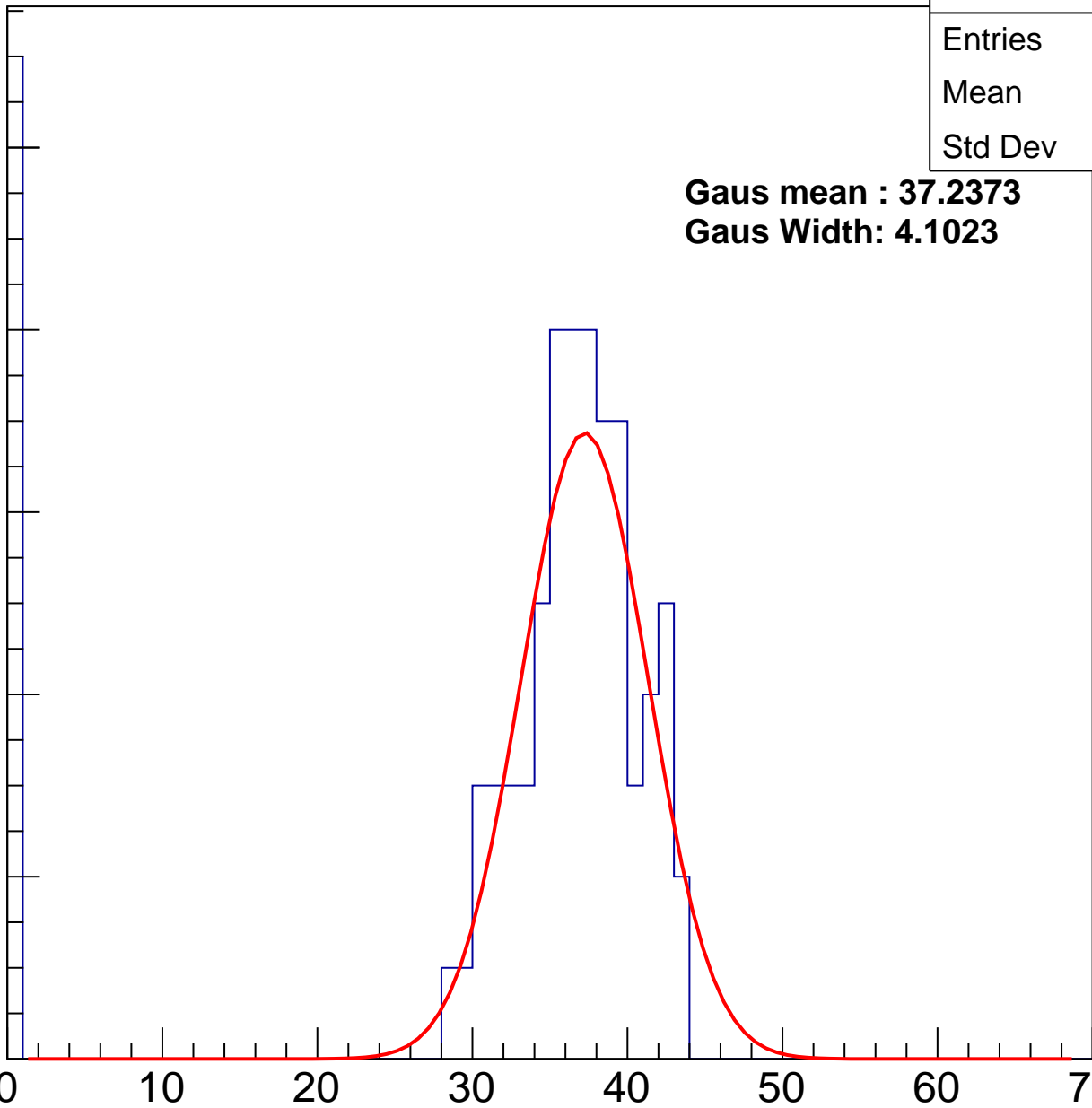
40

50

60

70

ampl



B1L103S, U13-ch41, adc2

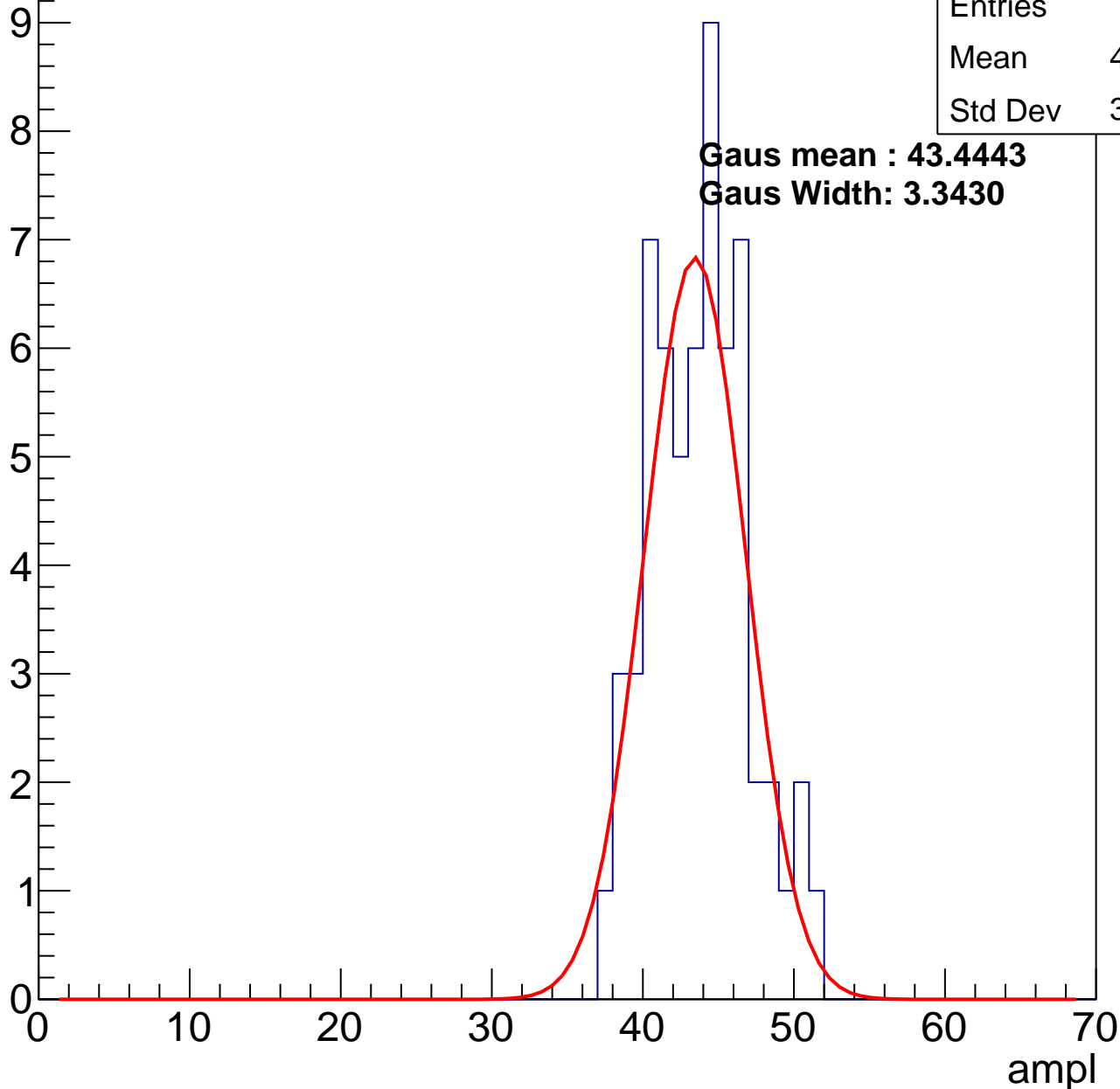
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	43.28
Std Dev	3.194

Gaus mean : 43.4443

Gaus Width: 3.3430

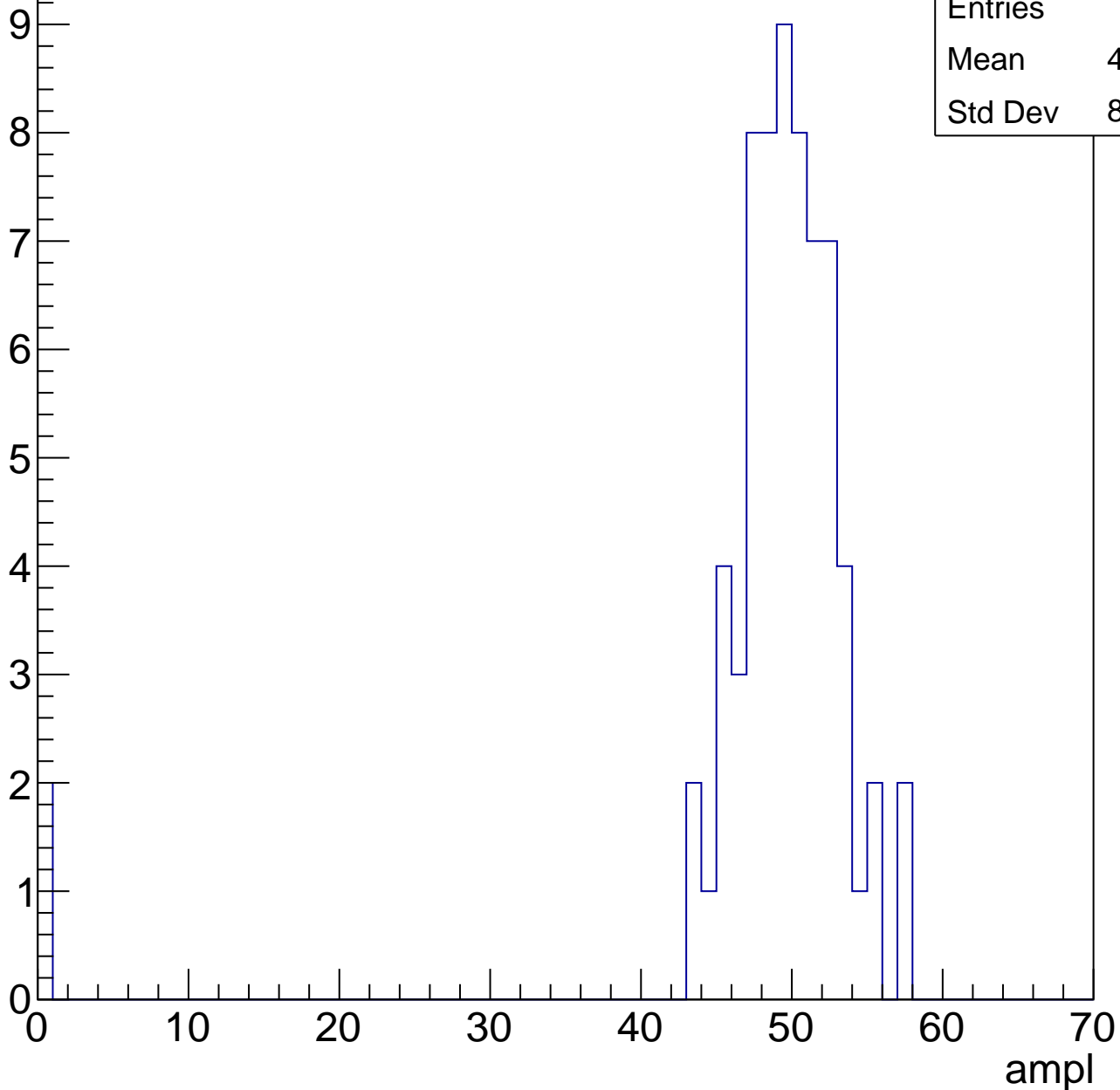


B1L103S, U13-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

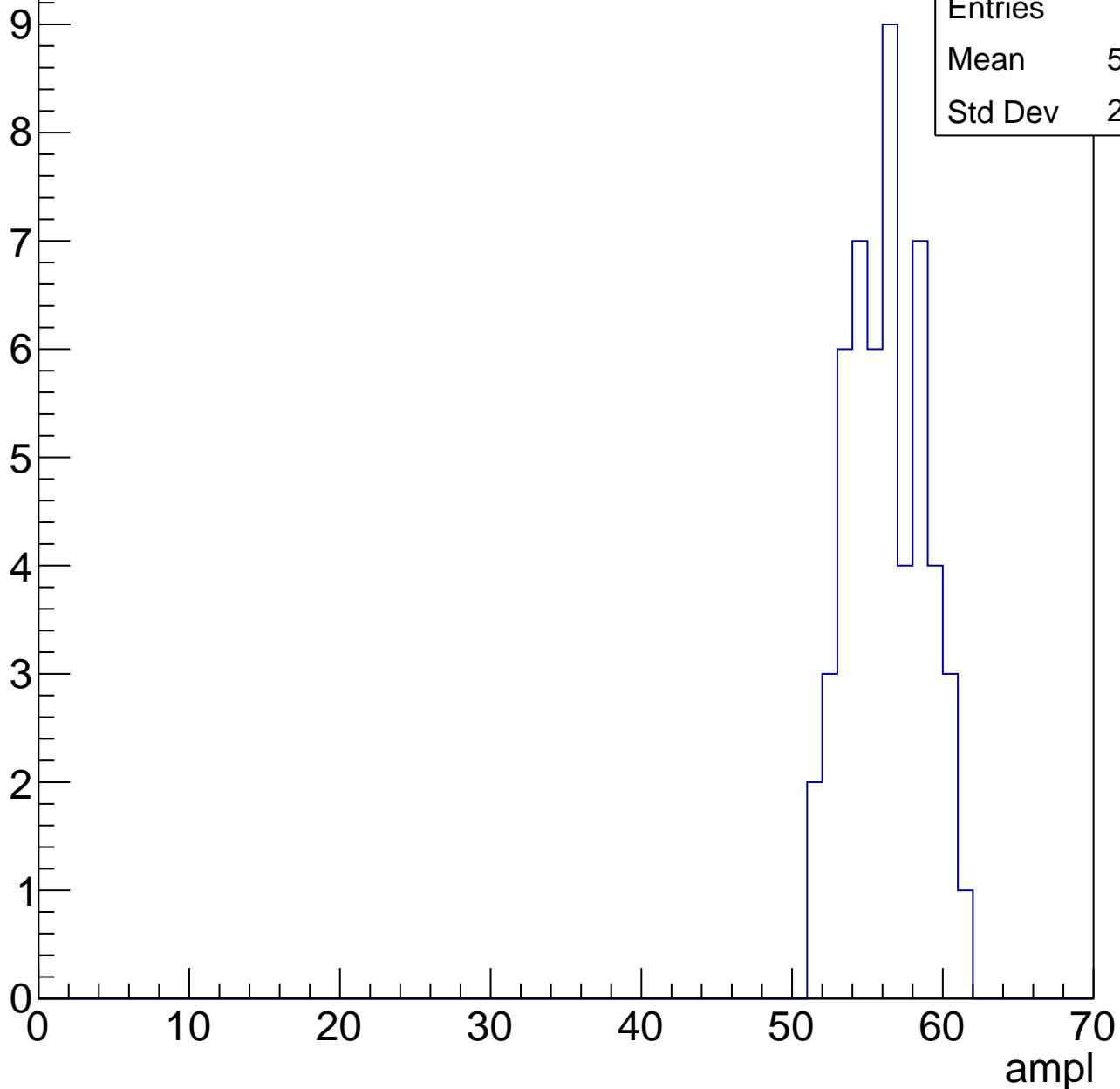
Entries	68
Mean	47.94
Std Dev	8.863



B1L103S, U13-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

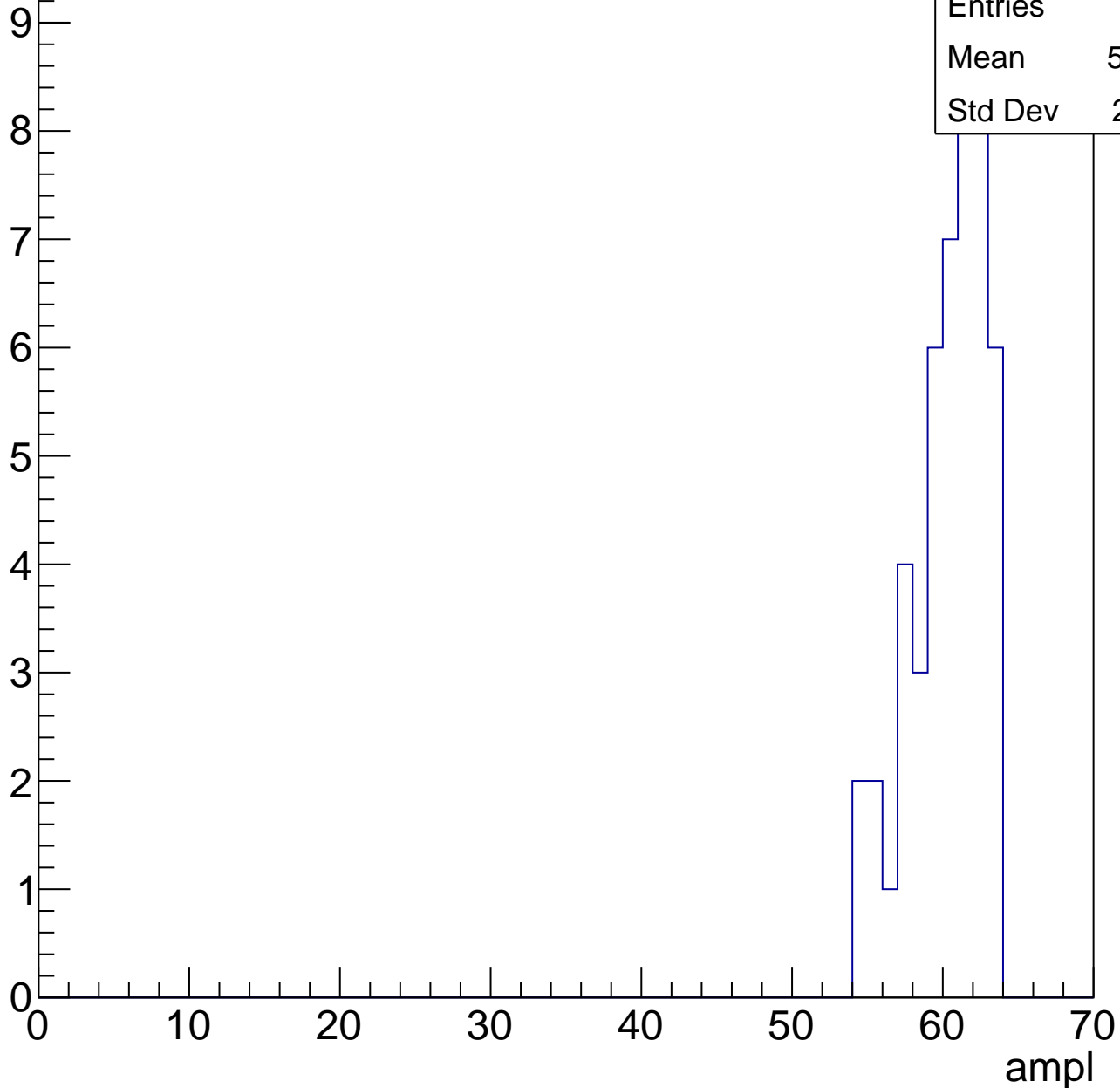


Entries	52
Mean	55.75
Std Dev	2.495

B1L103S, U13-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

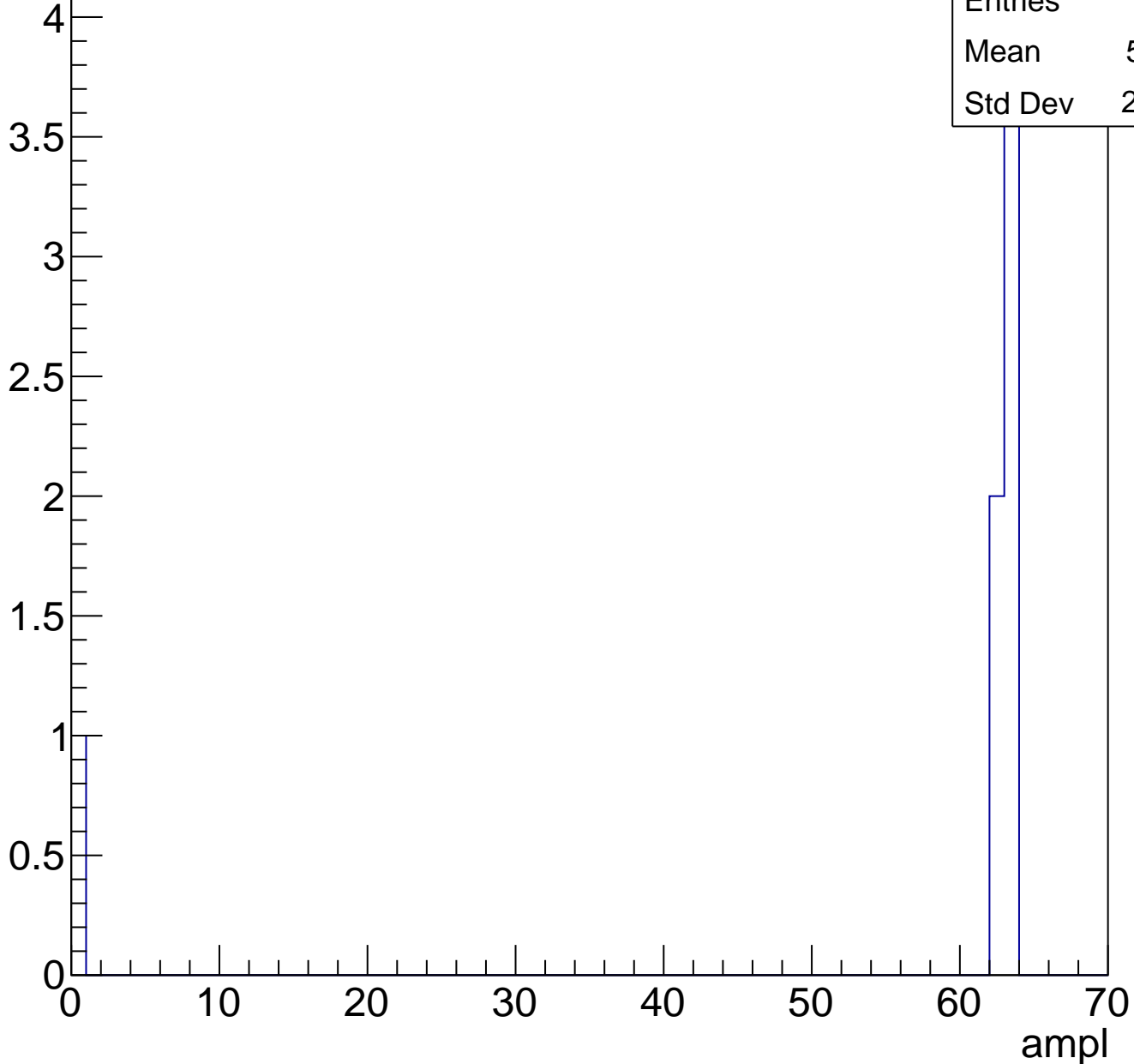


Entries	48
Mean	59.85
Std Dev	2.441

B1L103S, U13-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U13-ch42, adc0

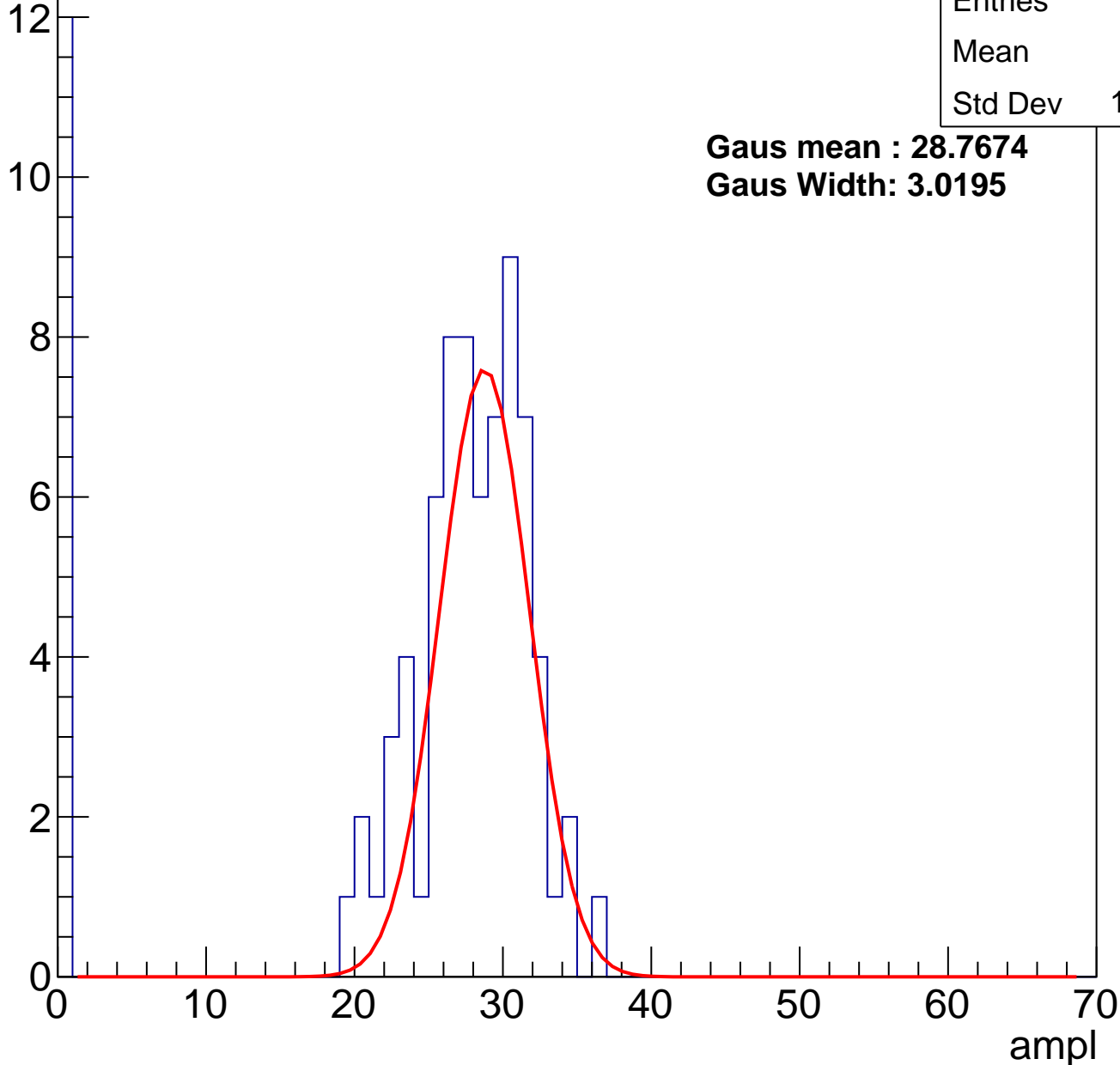
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	23.6
Std Dev	10.24

Gaus mean : 28.7674

Gaus Width: 3.0195

Entry



B1L103S, U13-ch42, adc1

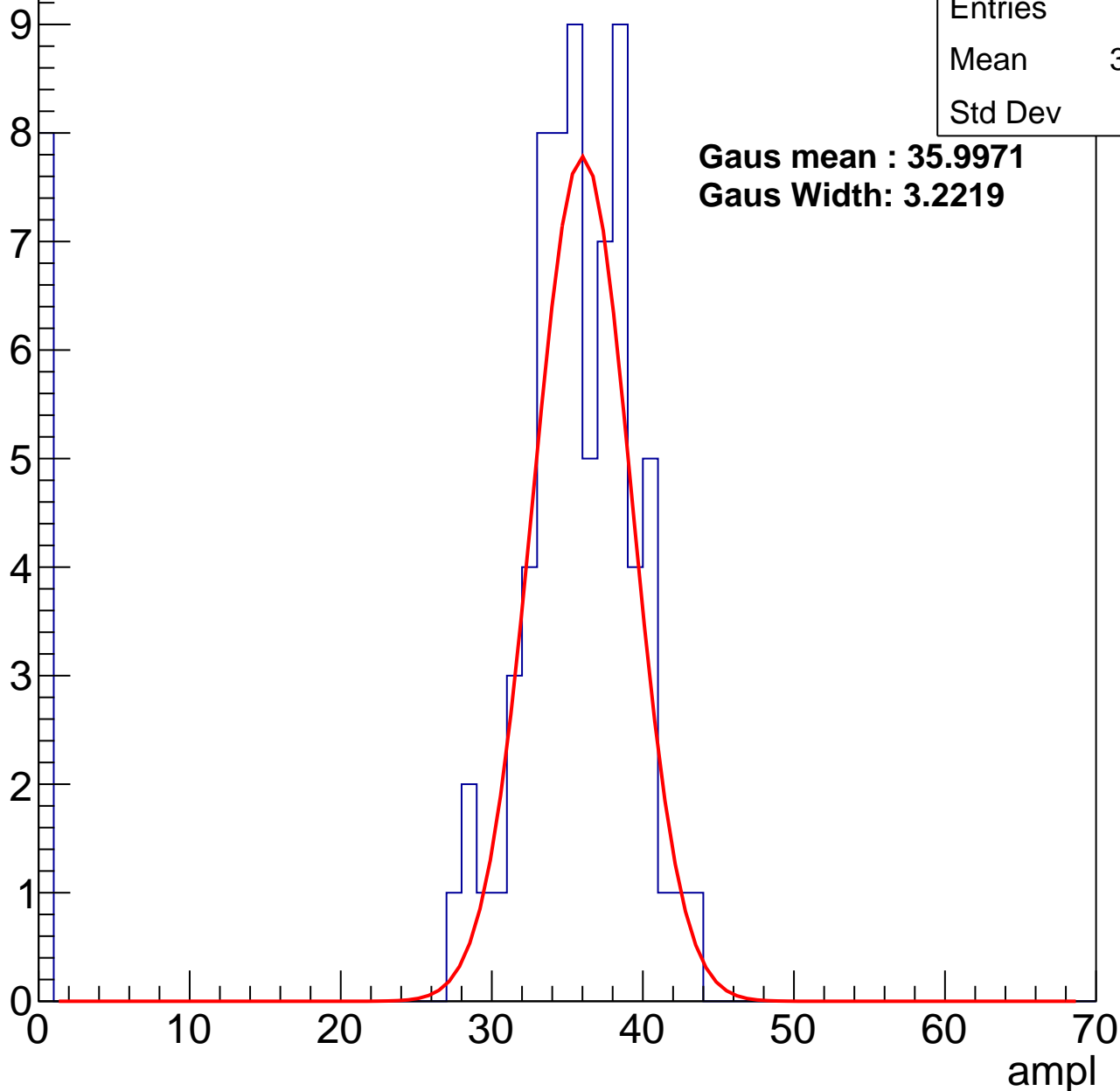
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	31.76
Std Dev	11.2

Gaus mean : 35.9971

Gaus Width: 3.2219

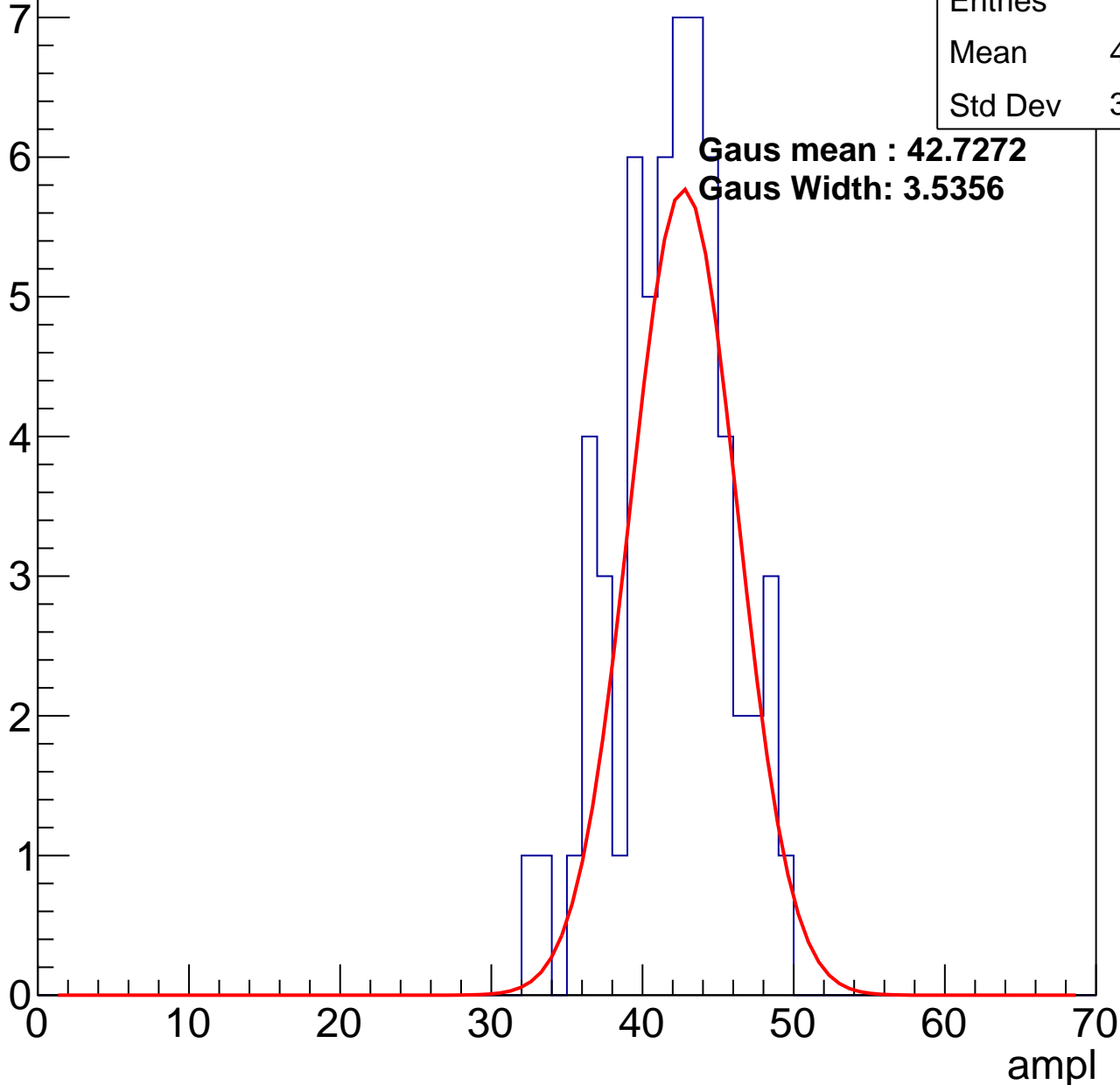


B1L103S, U13-ch42, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	41.52
Std Dev	3.735

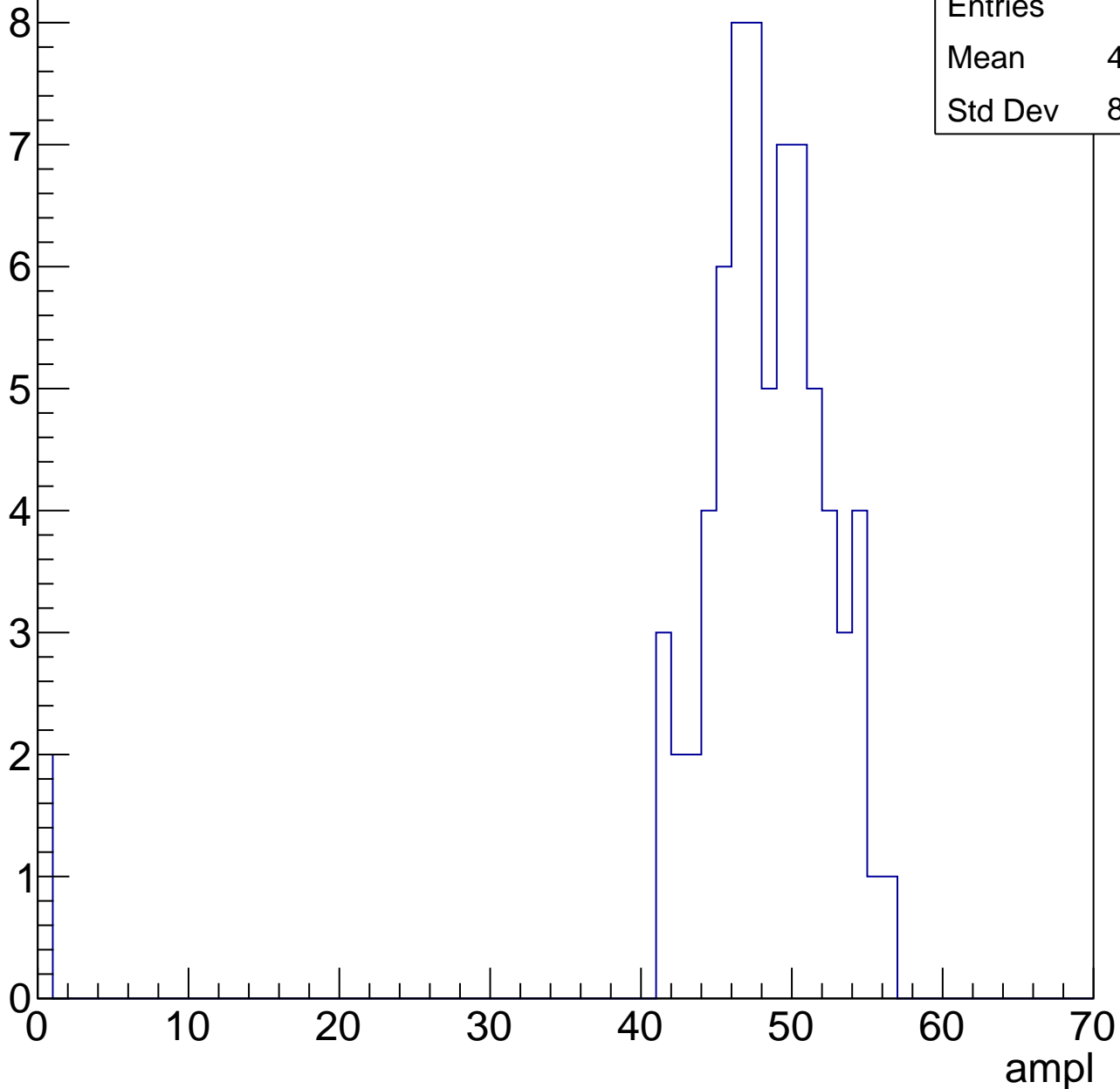


B1L103S, U13-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	46.74
Std Dev	8.657

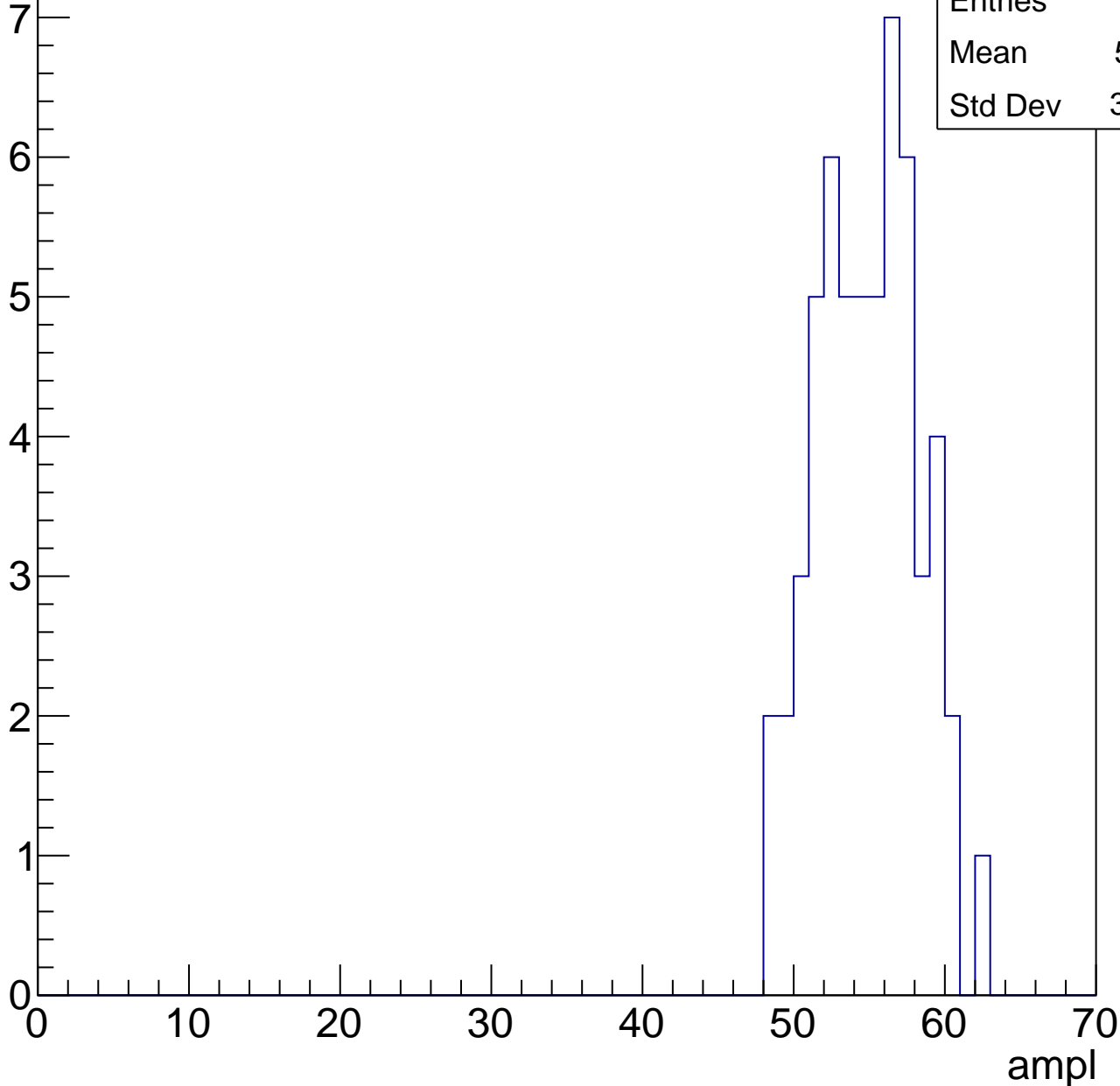


B1L103S, U13-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	54.41
Std Dev	3.288

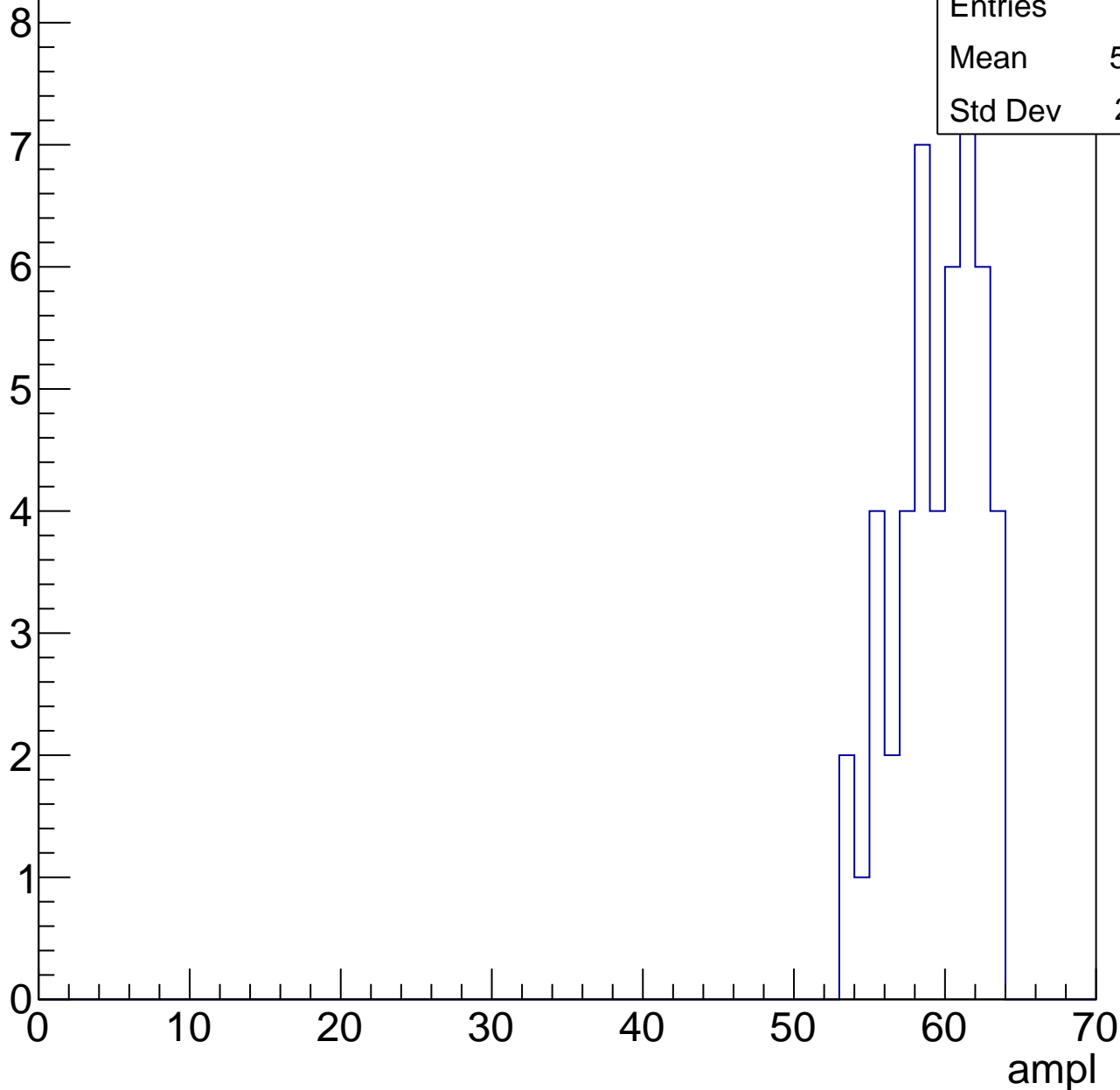


B1L103S, U13-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	59.04
Std Dev	2.731

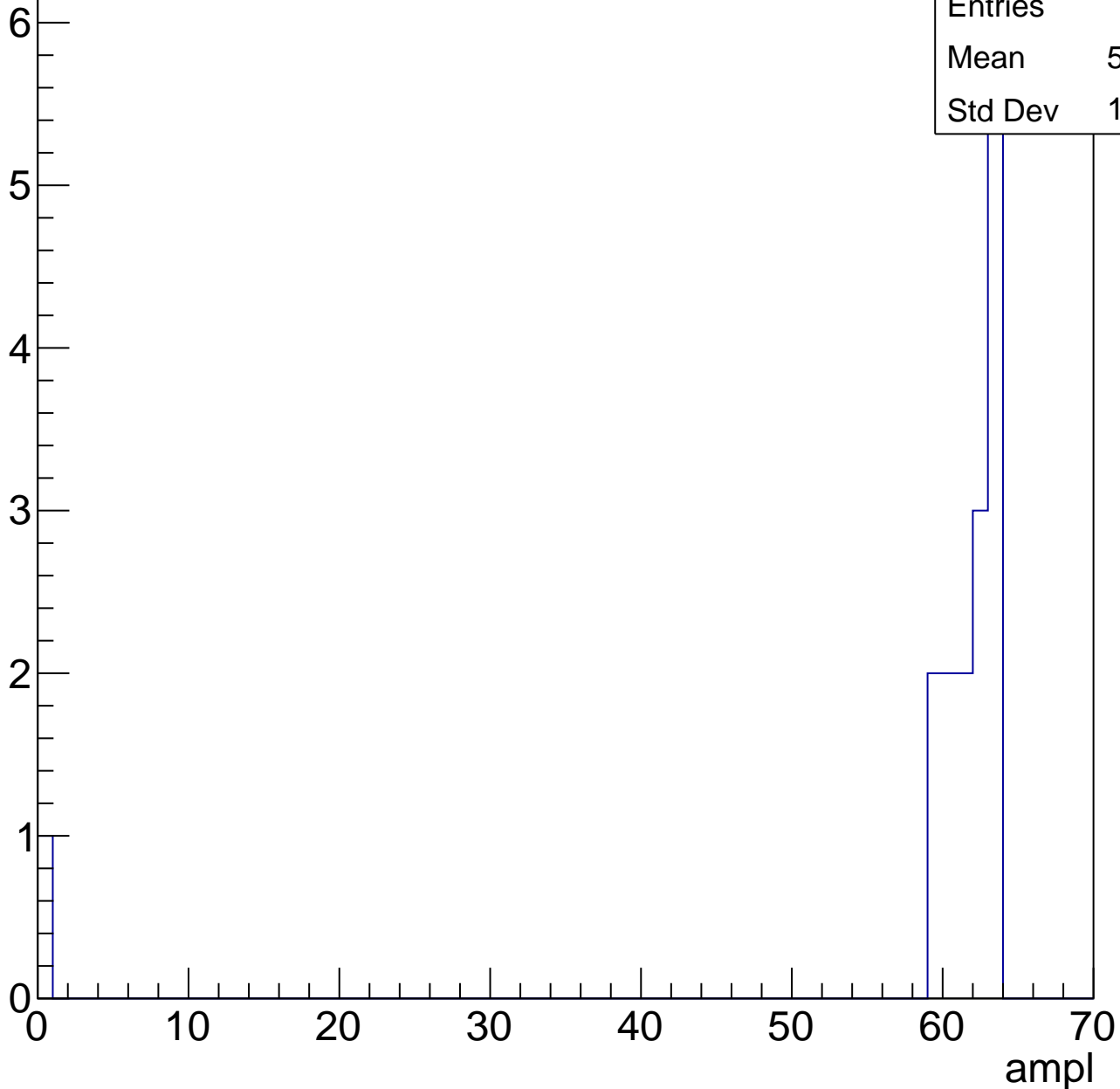


B1L103S, U13-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.75
Std Dev	14.98

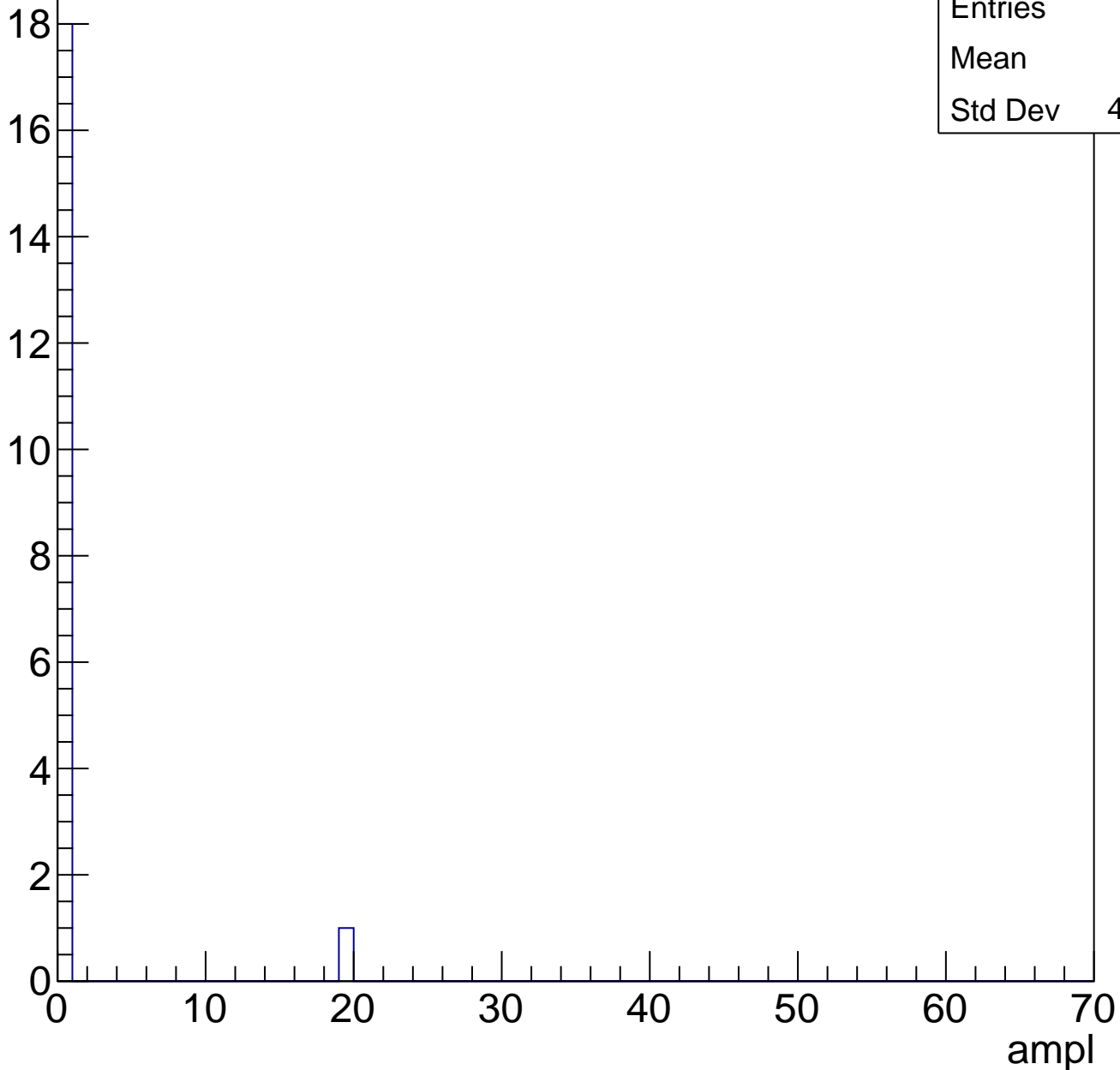


B1L103S, U13-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1
Std Dev	4.243

Entry



B1L103S, U13-ch43, adc0

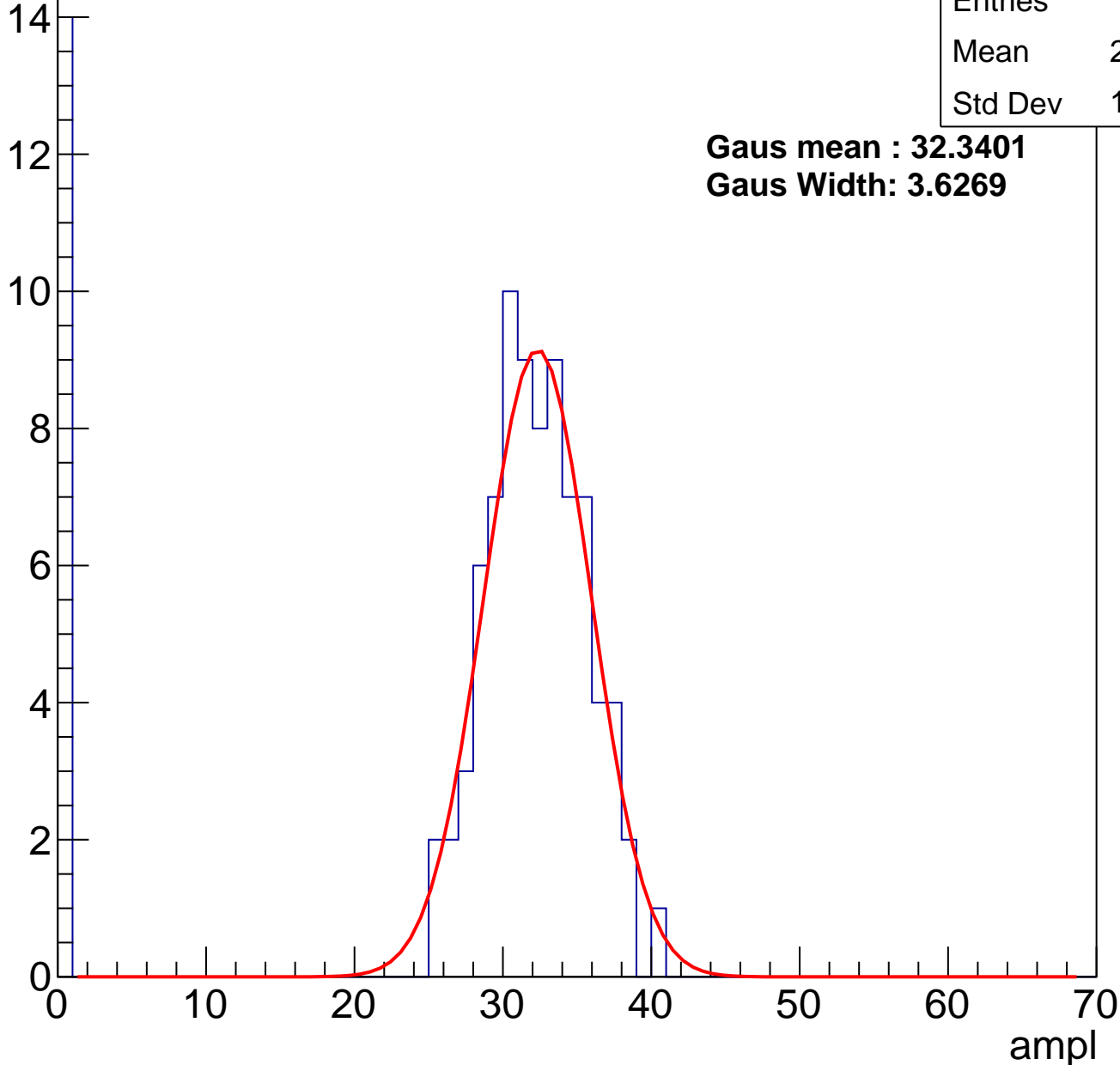
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	27.13
Std Dev	11.67

Gaus mean : 32.3401

Gaus Width: 3.6269

Entry



B1L103S, U13-ch43, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	33.32
Std Dev	14.14

Gaus mean : 39.6481

Gaus Width: 3.4878

Entry

10

8

6

4

2

0

0

10

20

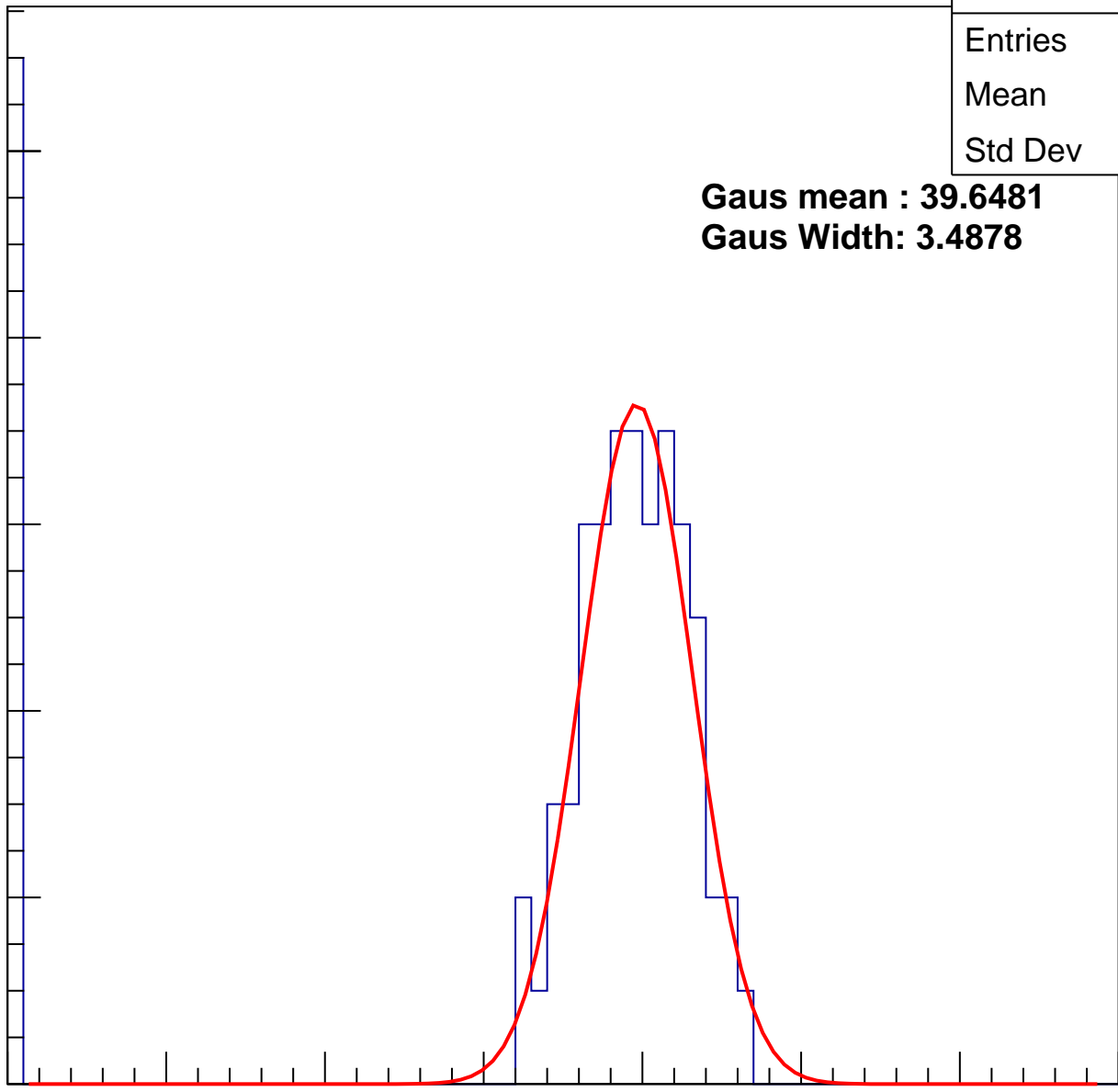
30

40

50

60

ampl



B1L103S, U13-ch43, adc2

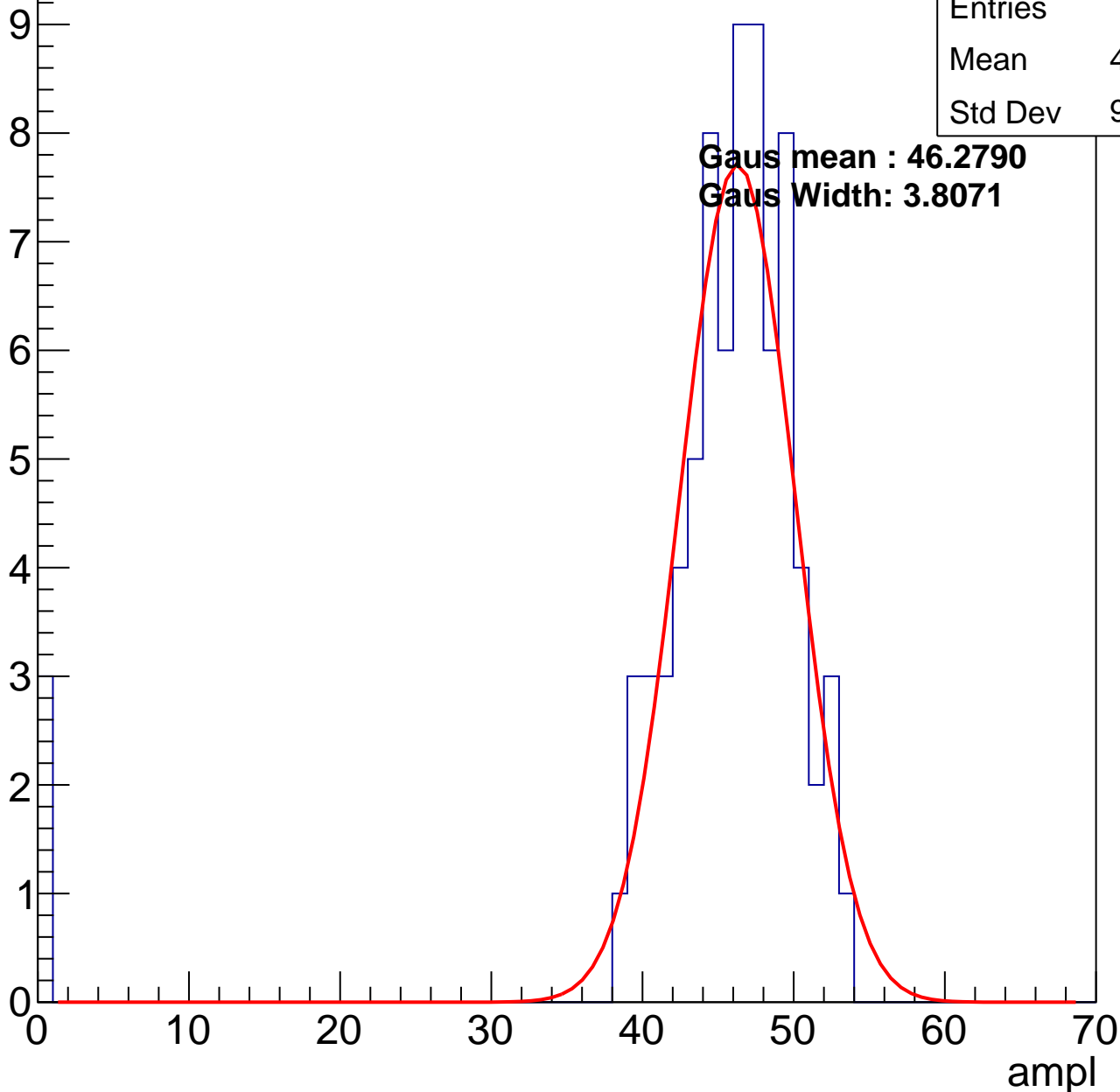
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	43.99
Std Dev	9.439

Gaus mean : 46.2790

Gaus Width: 3.8071

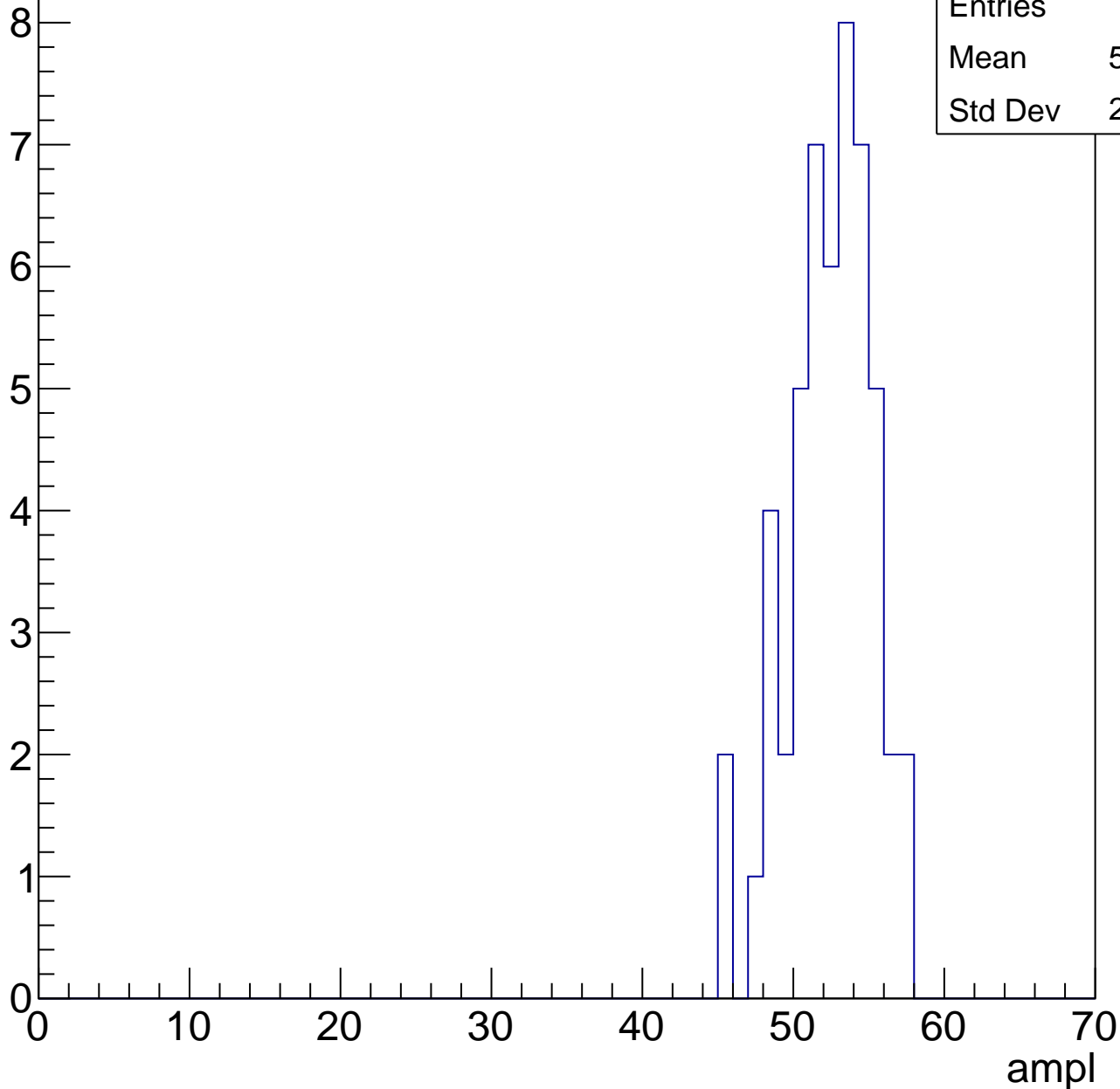


B1L103S, U13-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	51.94
Std Dev	2.782

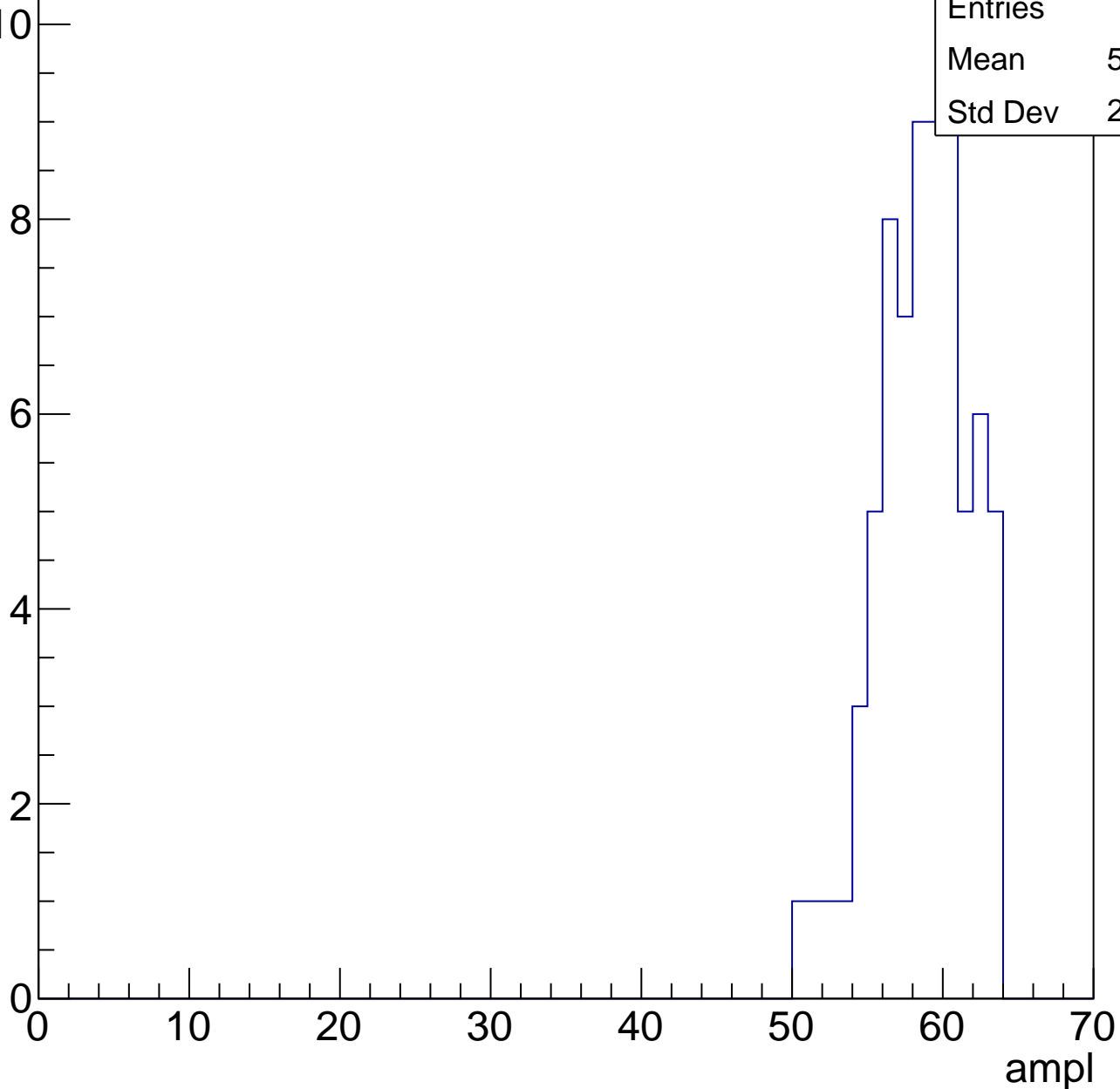


B1L103S, U13-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	58.24
Std Dev	2.948

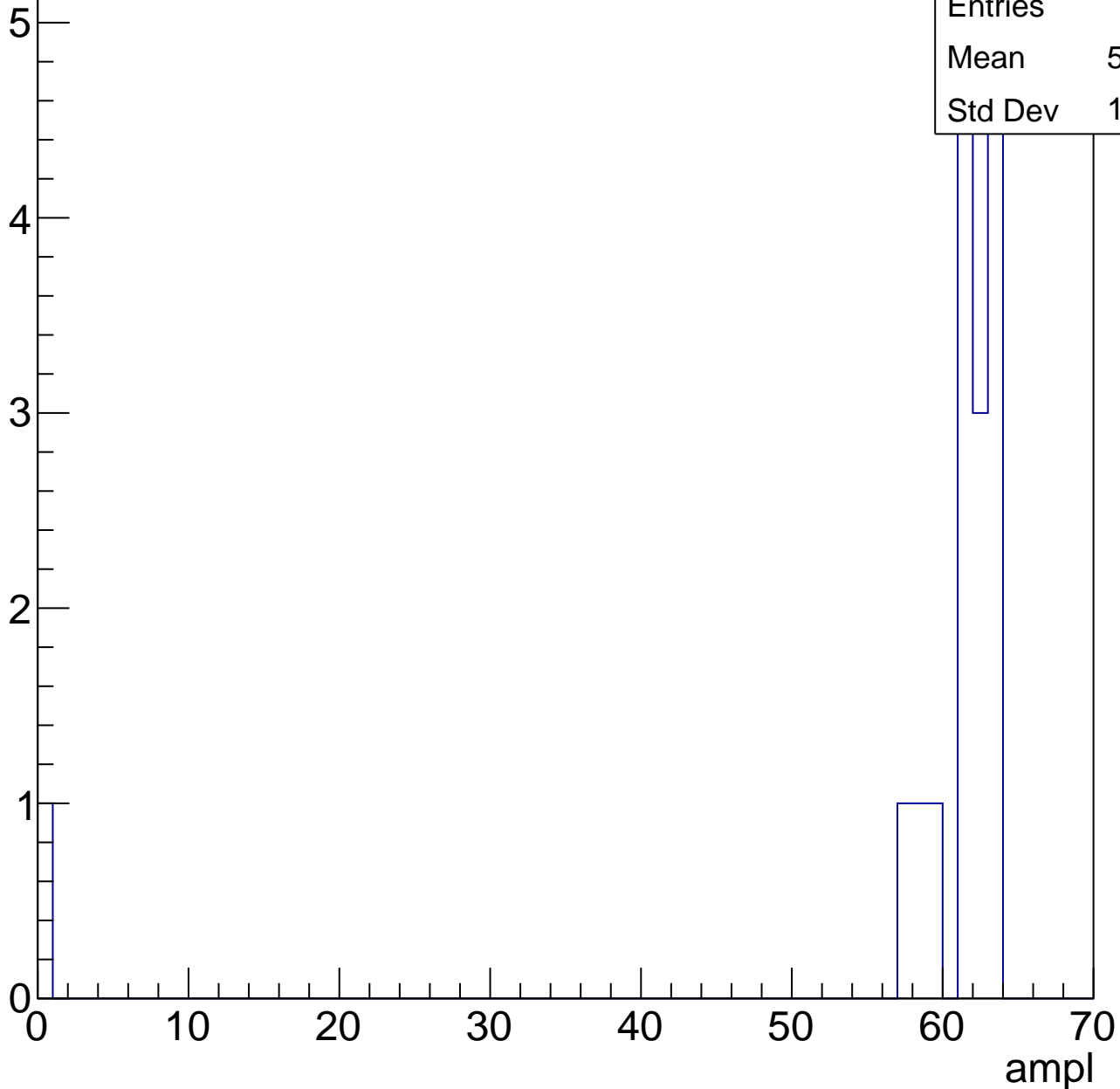


B1L103S, U13-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.65
Std Dev	14.52



B1L103S, U13-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch44, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	24.07
Std Dev	11.89

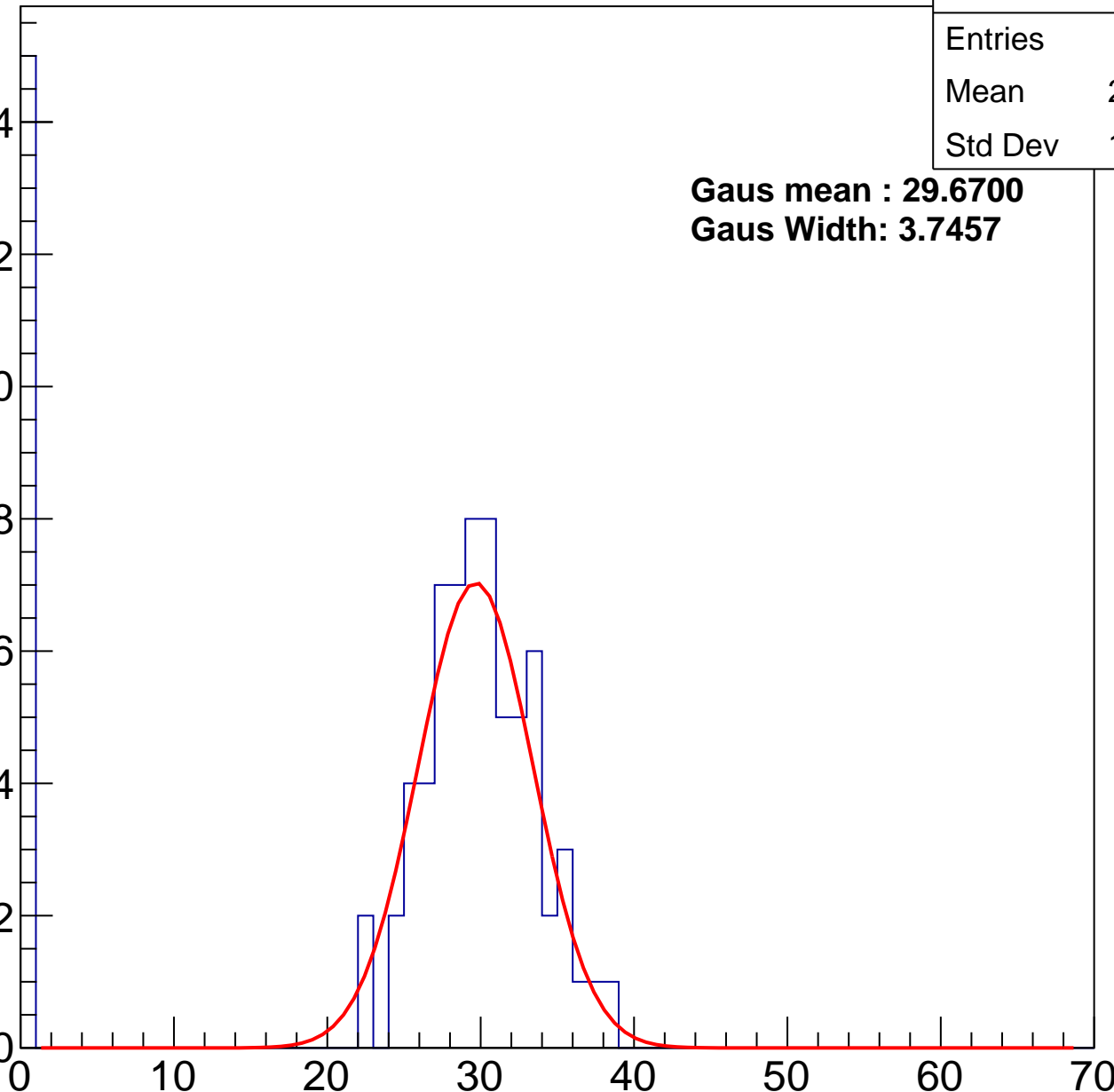
Gaus mean : 29.6700

Gaus Width: 3.7457

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch44, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	30.79
Std Dev	12.7

Gaus mean : 36.2229

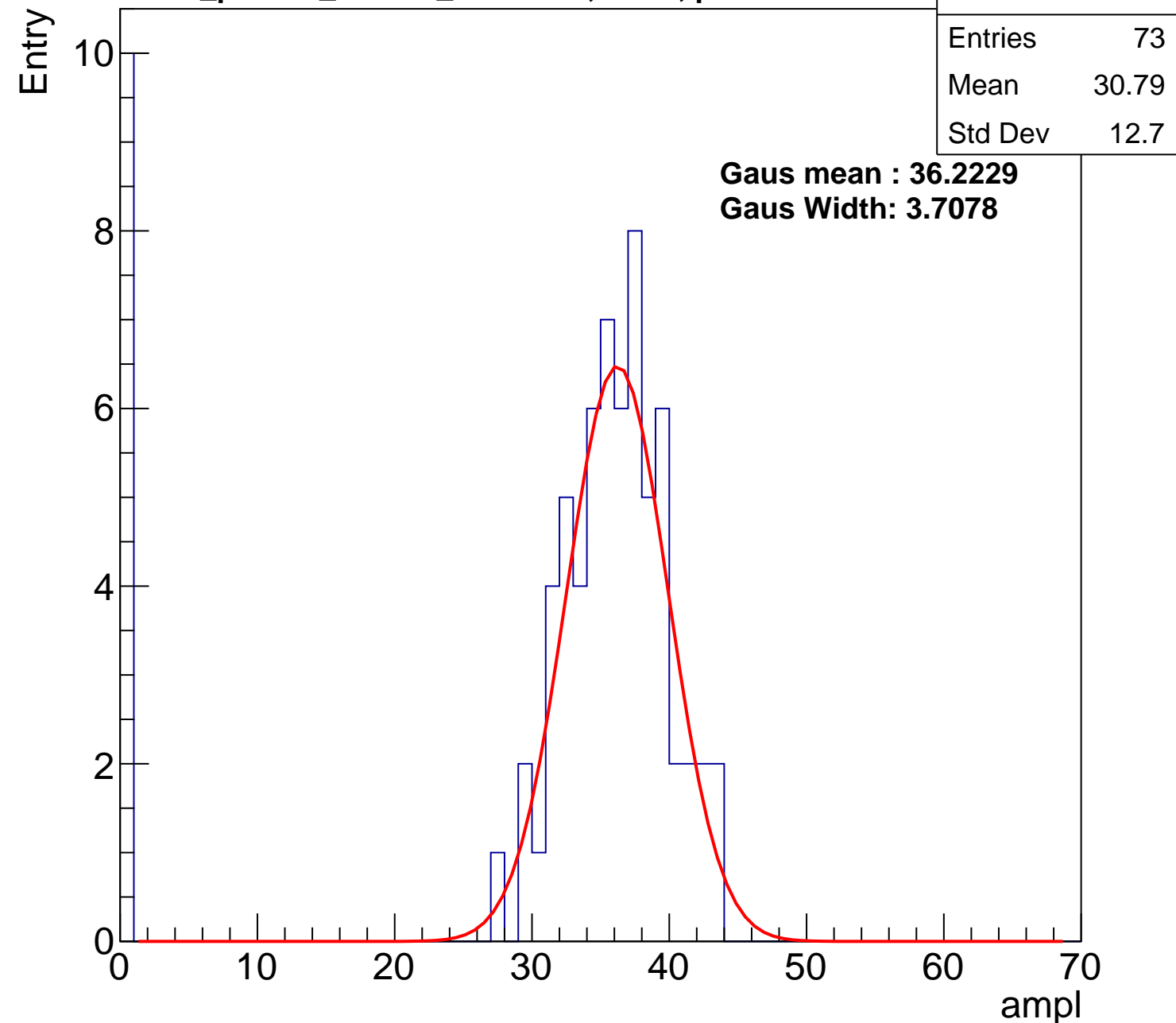
Gaus Width: 3.7078

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch44, adc2

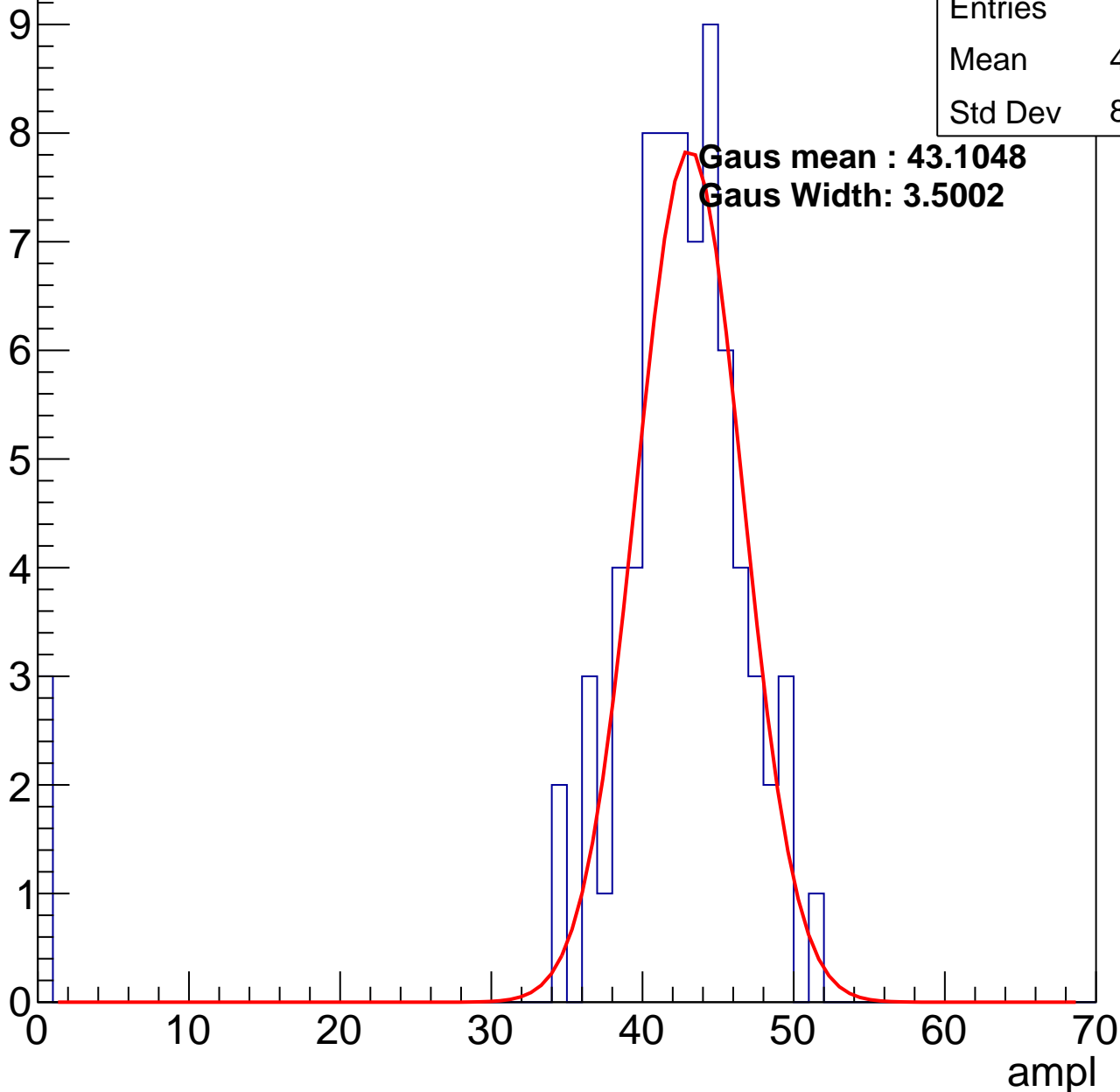
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	40.67
Std Dev	8.955

Gaus mean : 43.1048

Gaus Width: 3.5002

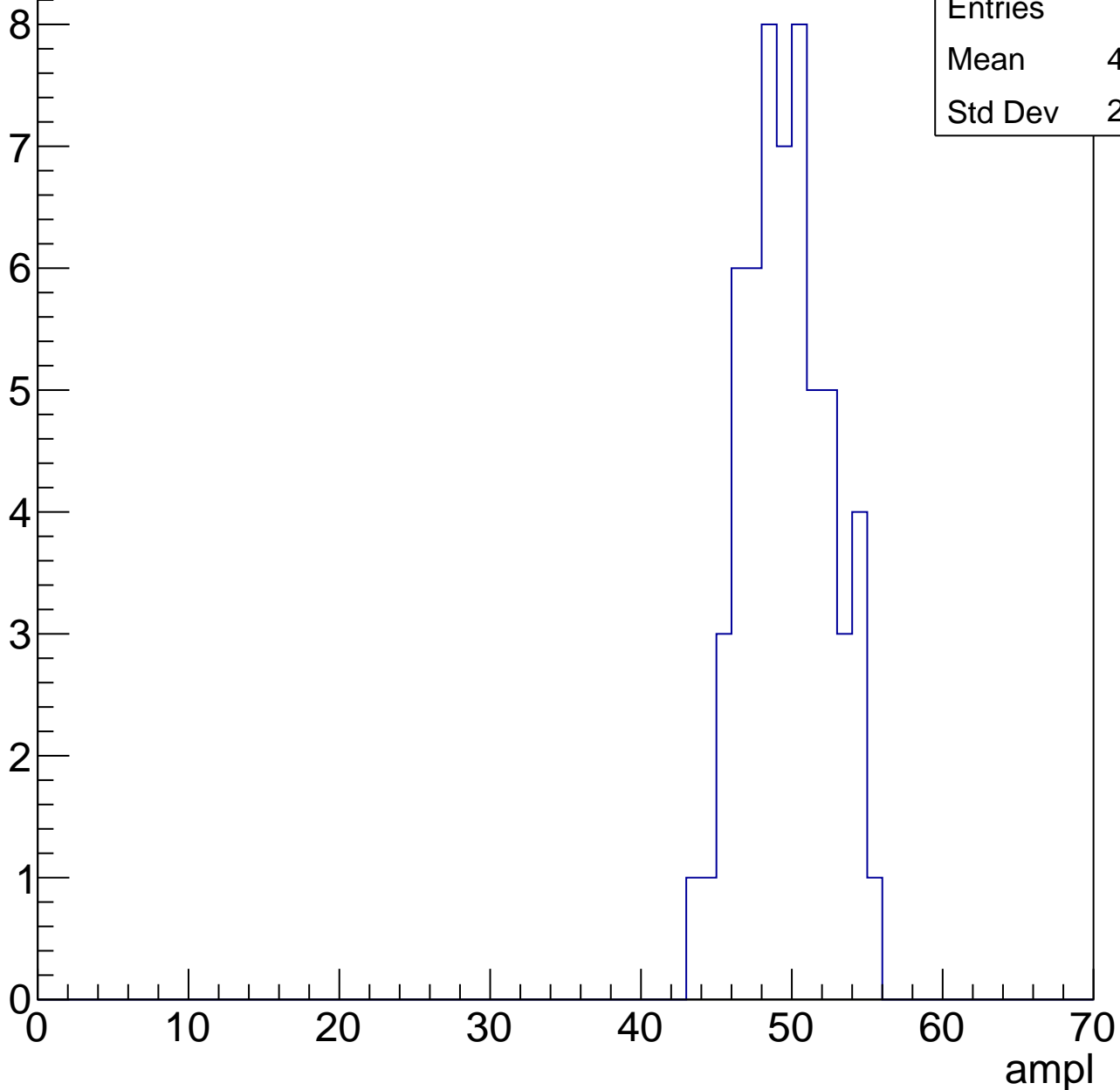


B1L103S, U13-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	49.17
Std Dev	2.786

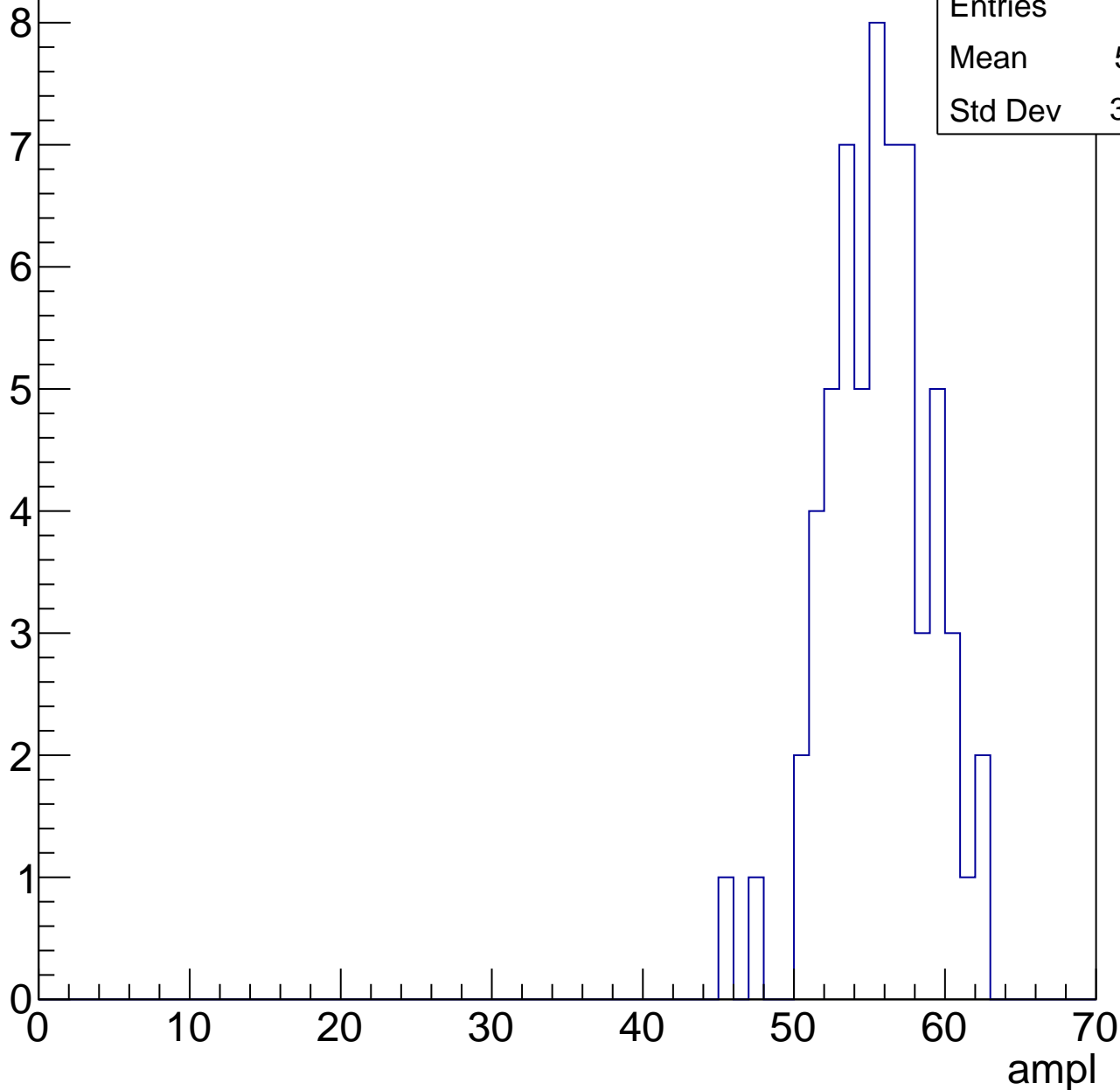


B1L103S, U13-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

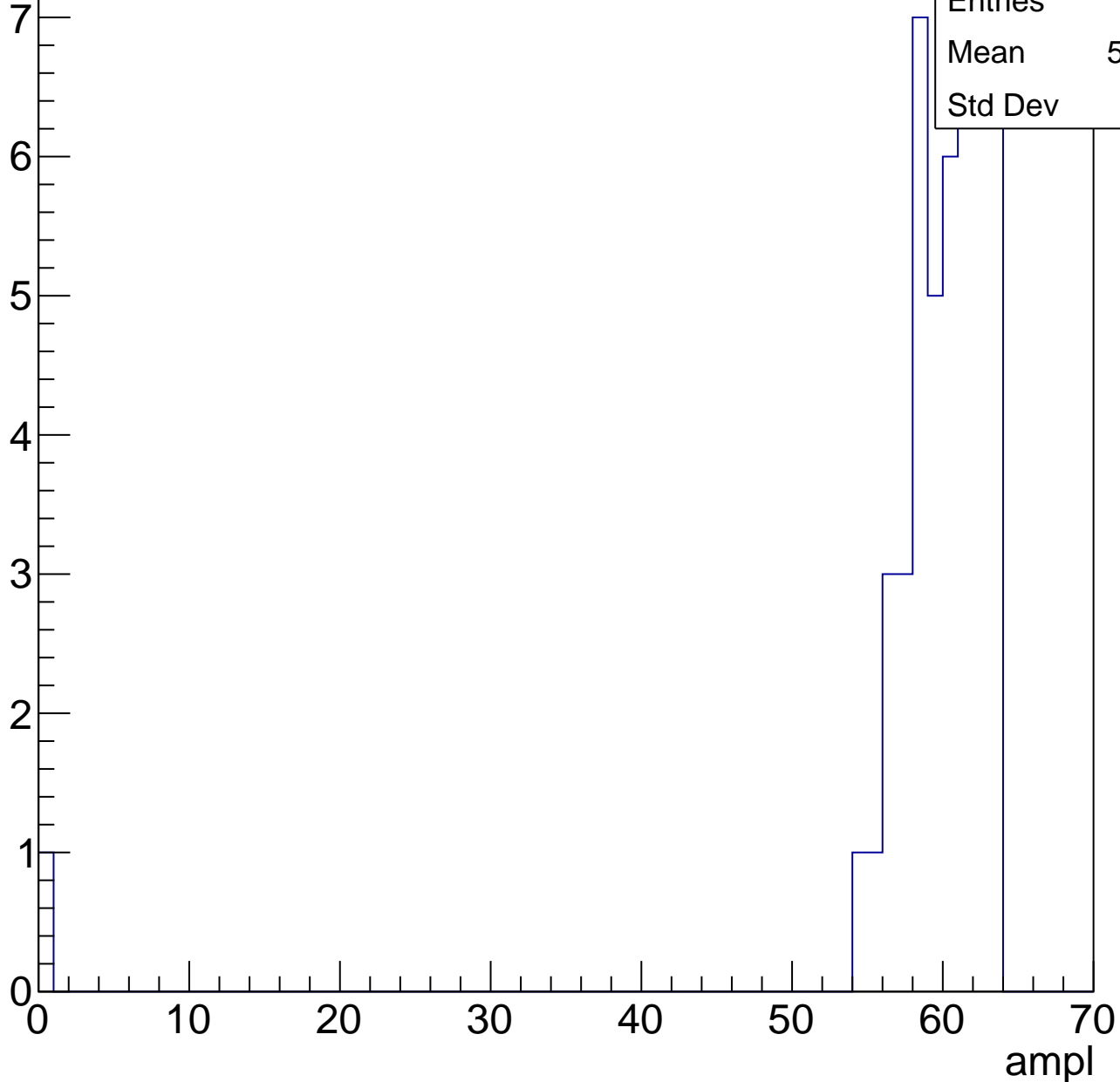
Entries	61
Mean	55.11
Std Dev	3.402



B1L103S, U13-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch45, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	29.44
Std Dev	7.735

Gaus mean : 30.9706

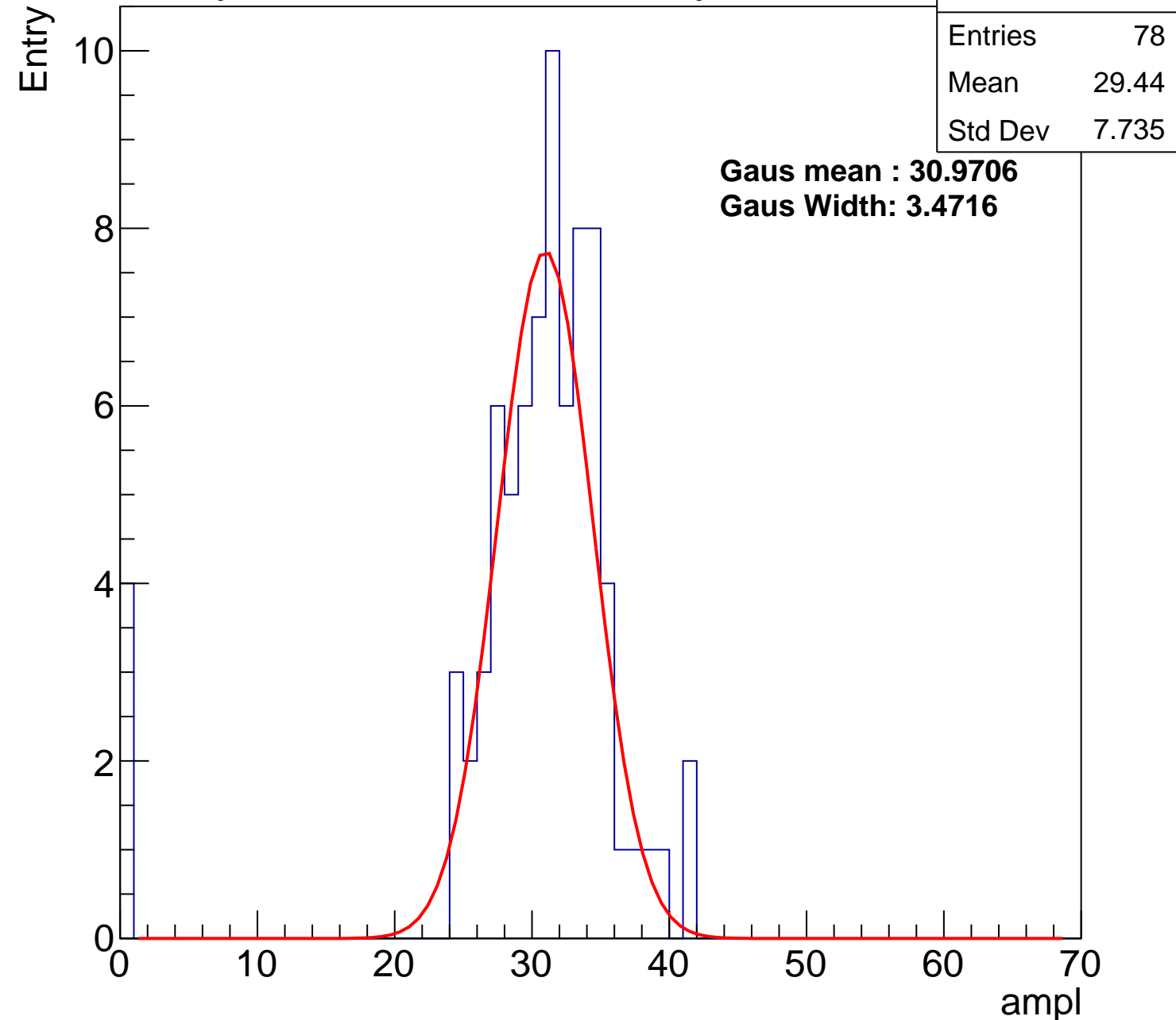
Gaus Width: 3.4716

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



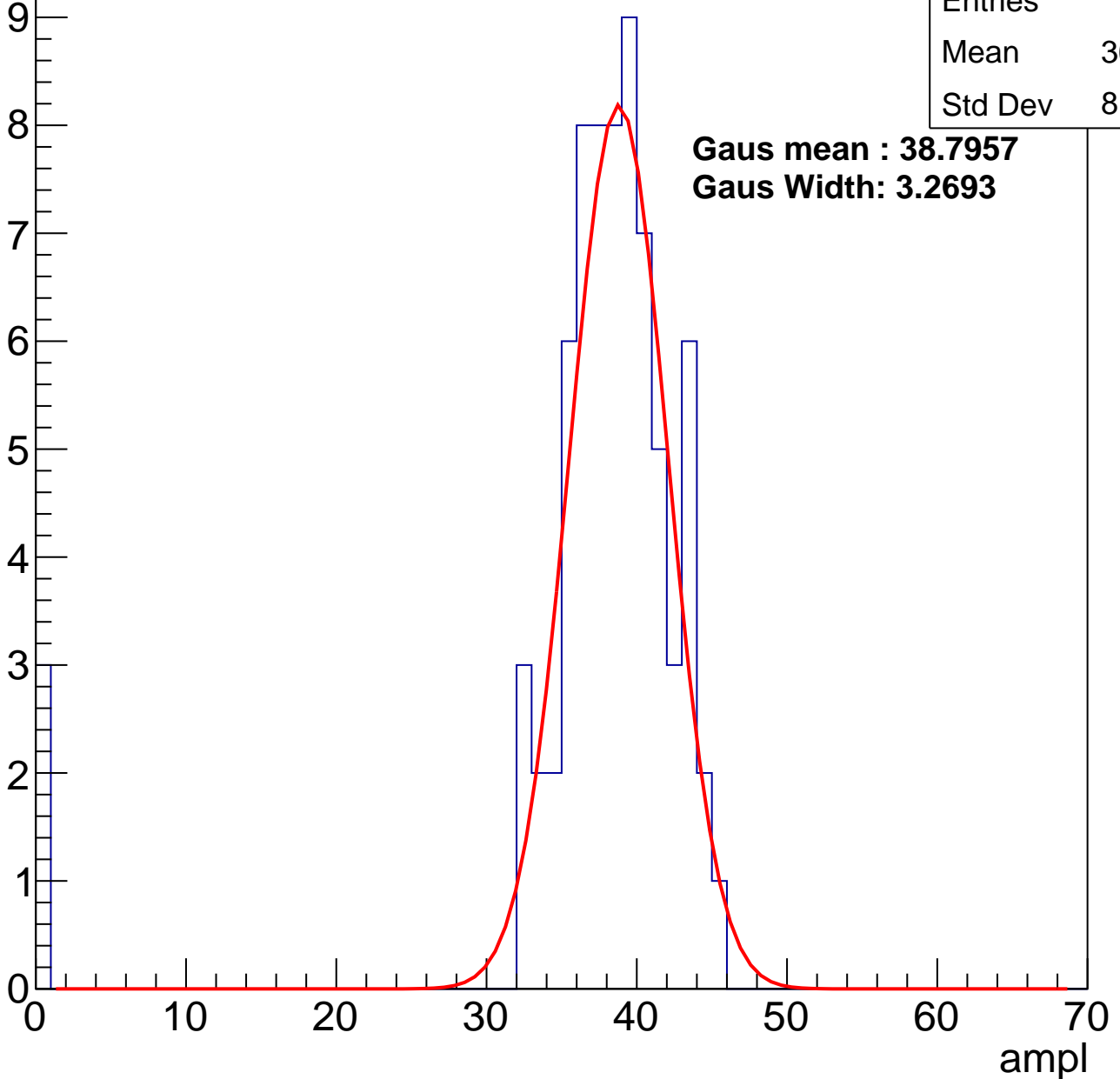
B1L103S, U13-ch45, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

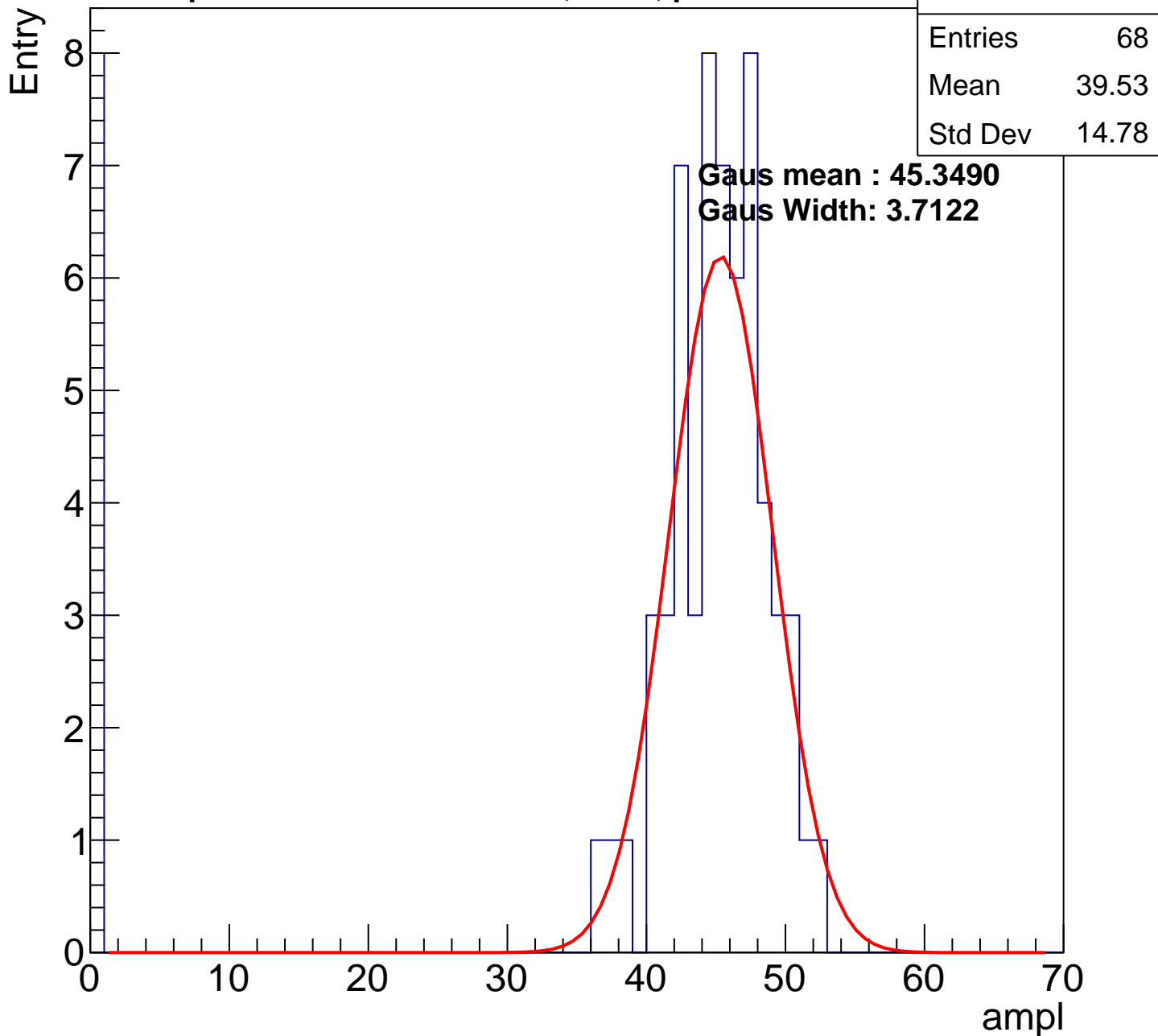
Entries	73
Mean	36.73
Std Dev	8.192

Gaus mean : 38.7957
Gaus Width: 3.2693



B1L103S, U13-ch45, adc2

calib_packv5_041523_1651.root, FC#0, port C2

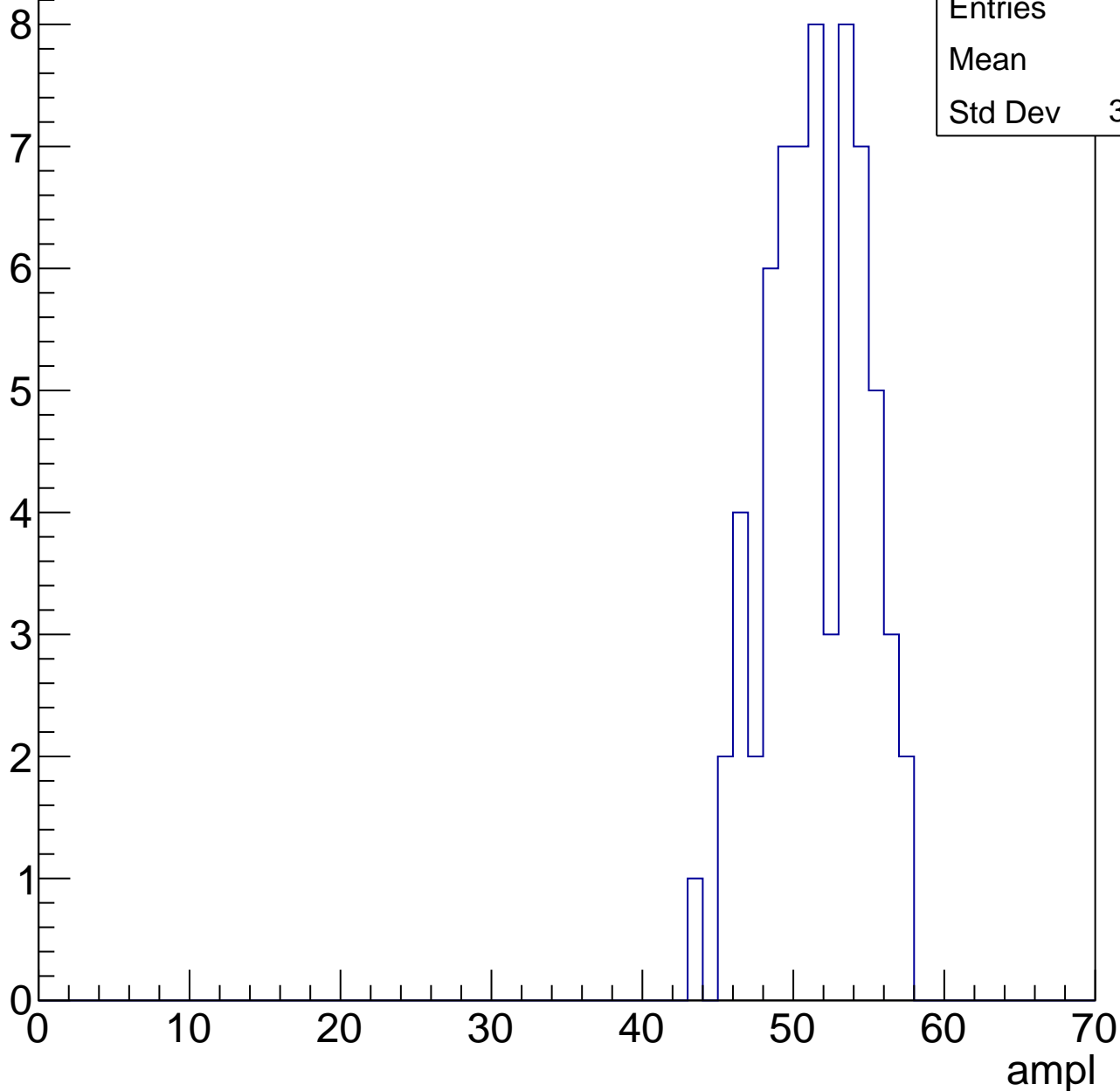


B1L103S, U13-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

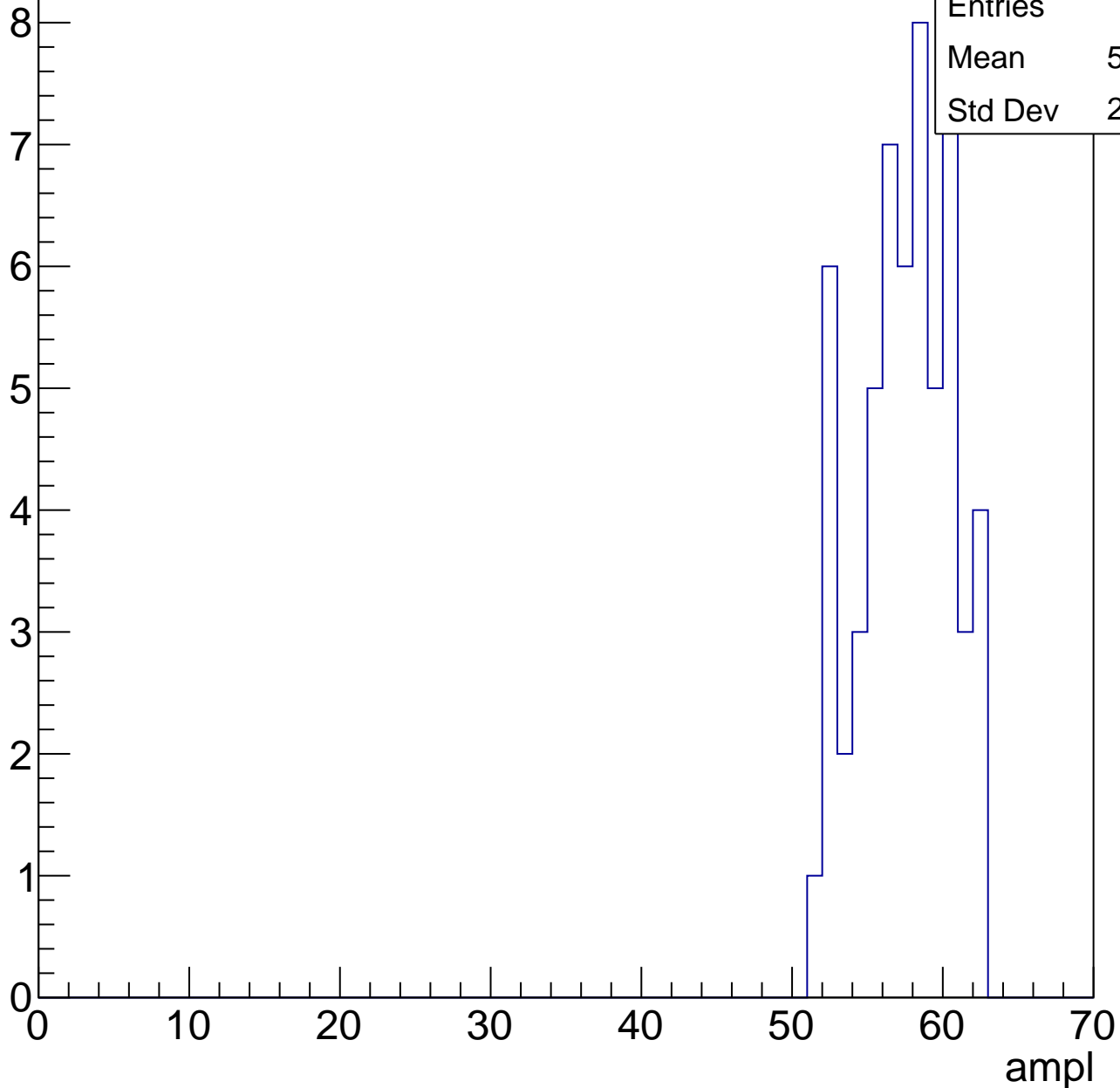
Entries	65
Mean	51
Std Dev	3.239



B1L103S, U13-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



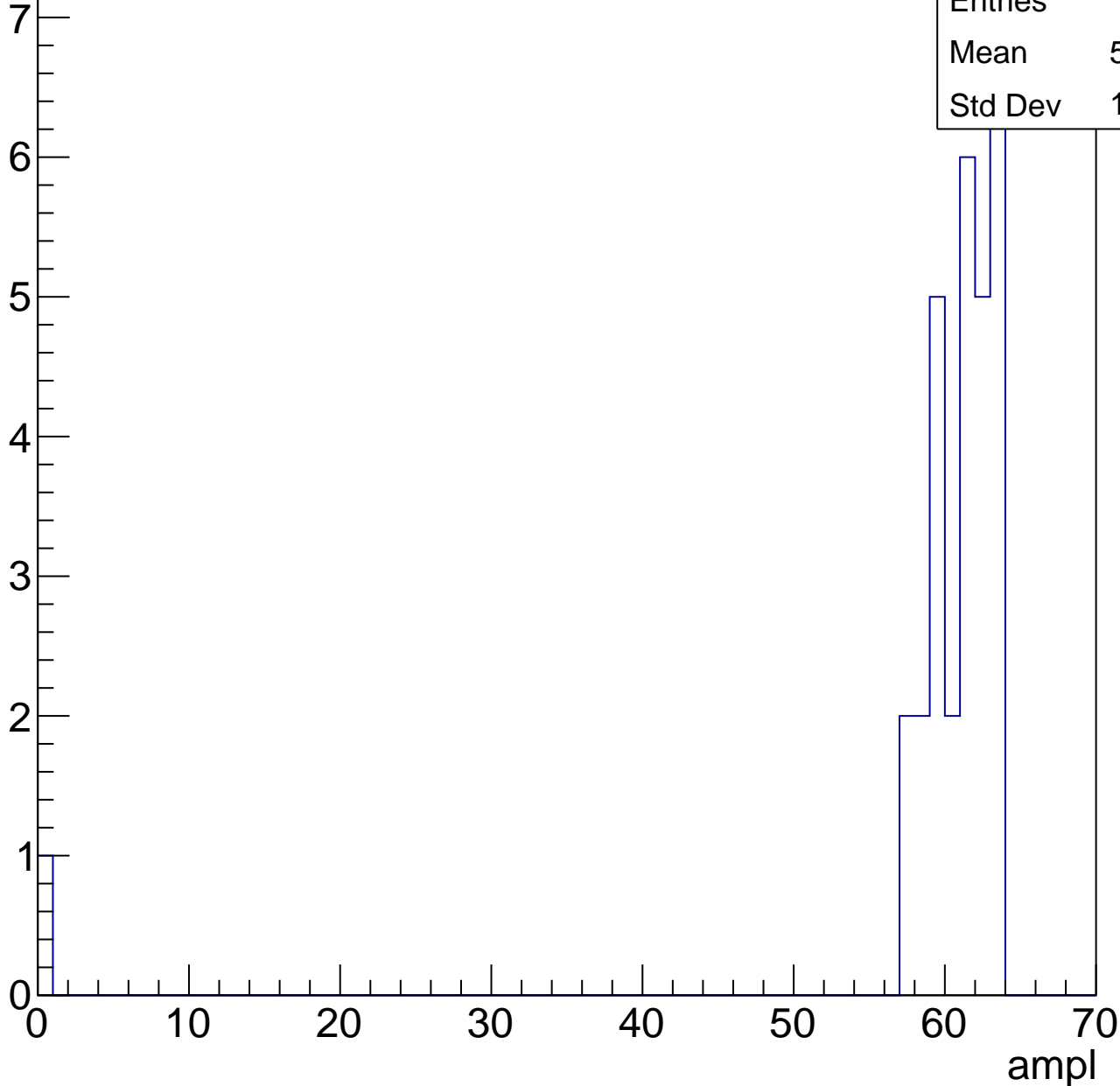
Entries	58
Mean	57.07
Std Dev	2.993

B1L103S, U13-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	58.73
Std Dev	11.06



B1L103S, U13-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch46, adc0

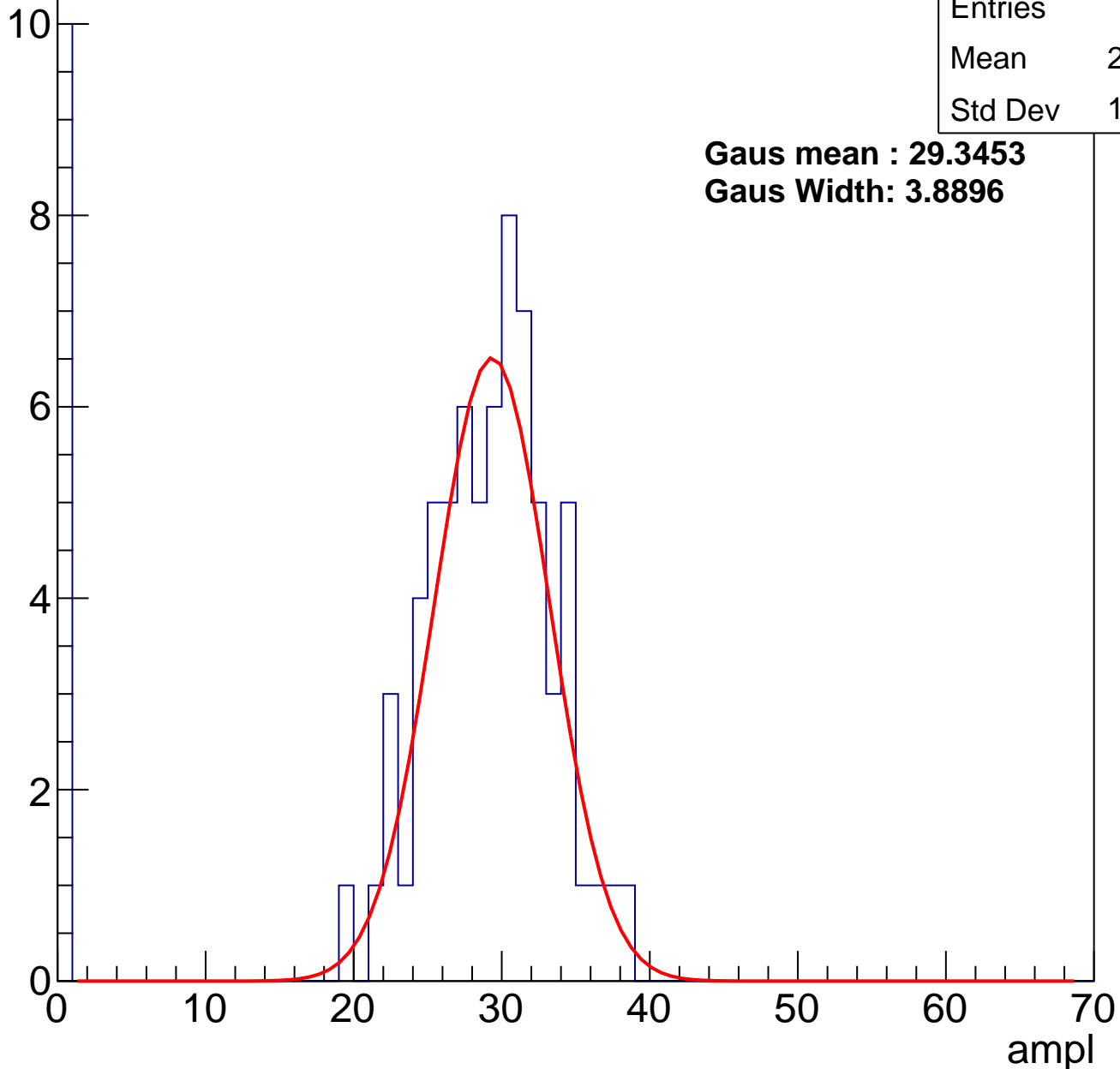
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	25.16
Std Dev	10.27

Gaus mean : 29.3453

Gaus Width: 3.8896

Entry



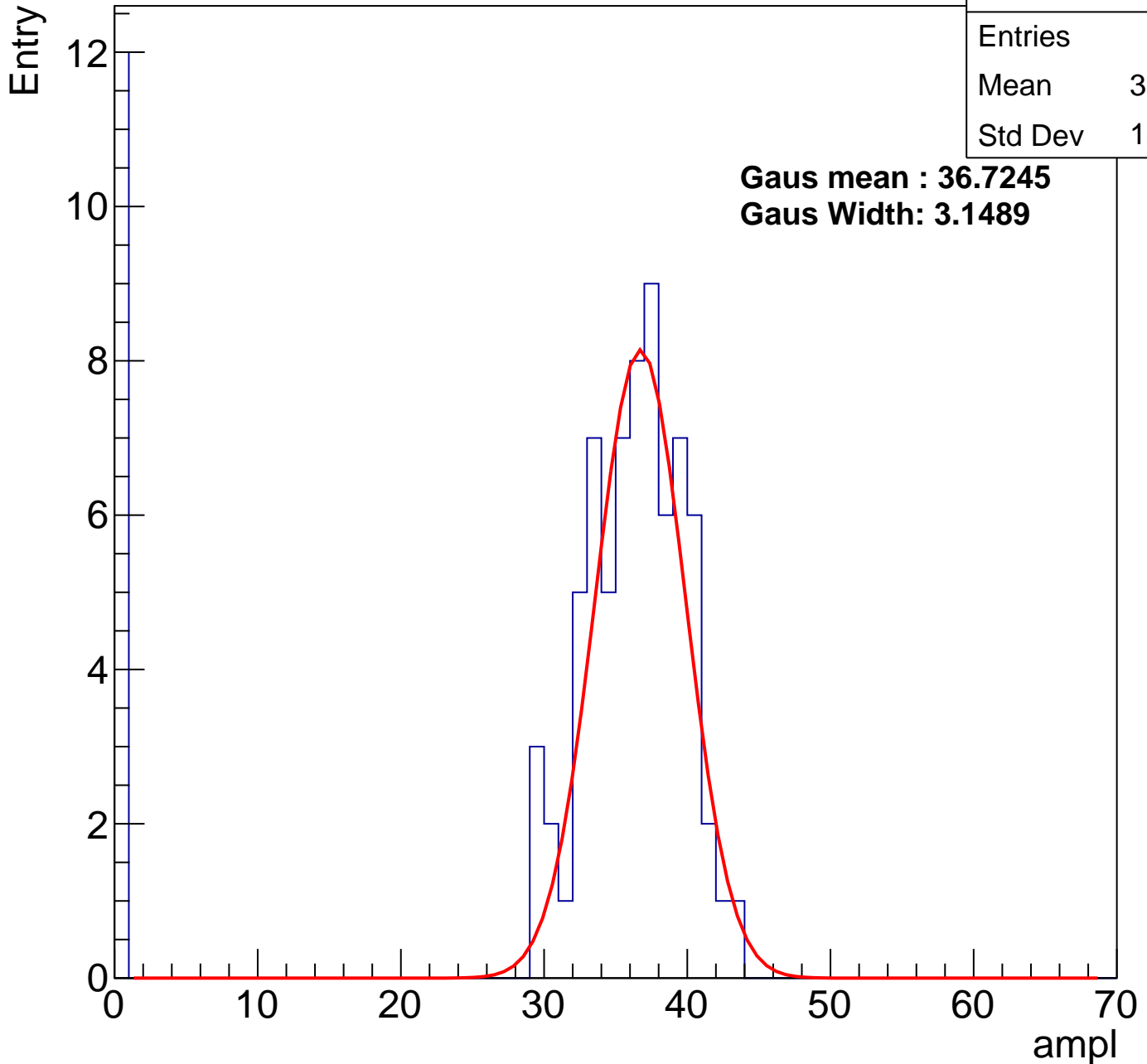
B1L103S, U13-ch46, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	30.65
Std Dev	13.04

Gaus mean : 36.7245

Gaus Width: 3.1489



B1L103S, U13-ch46, adc2

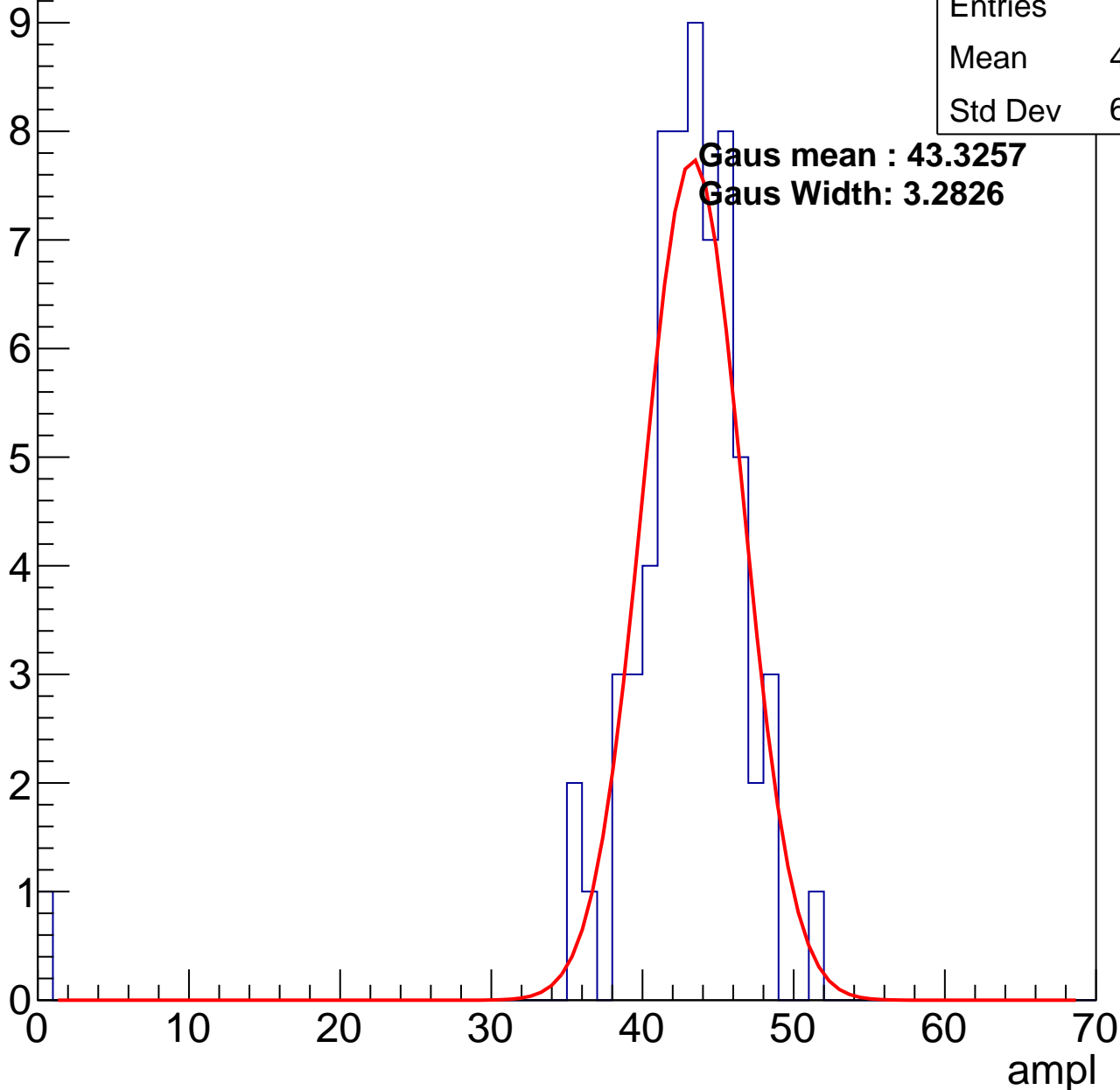
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	42.08
Std Dev	6.108

Gaus mean : 43.3257

Gaus Width: 3.2826



B1L103S, U13-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

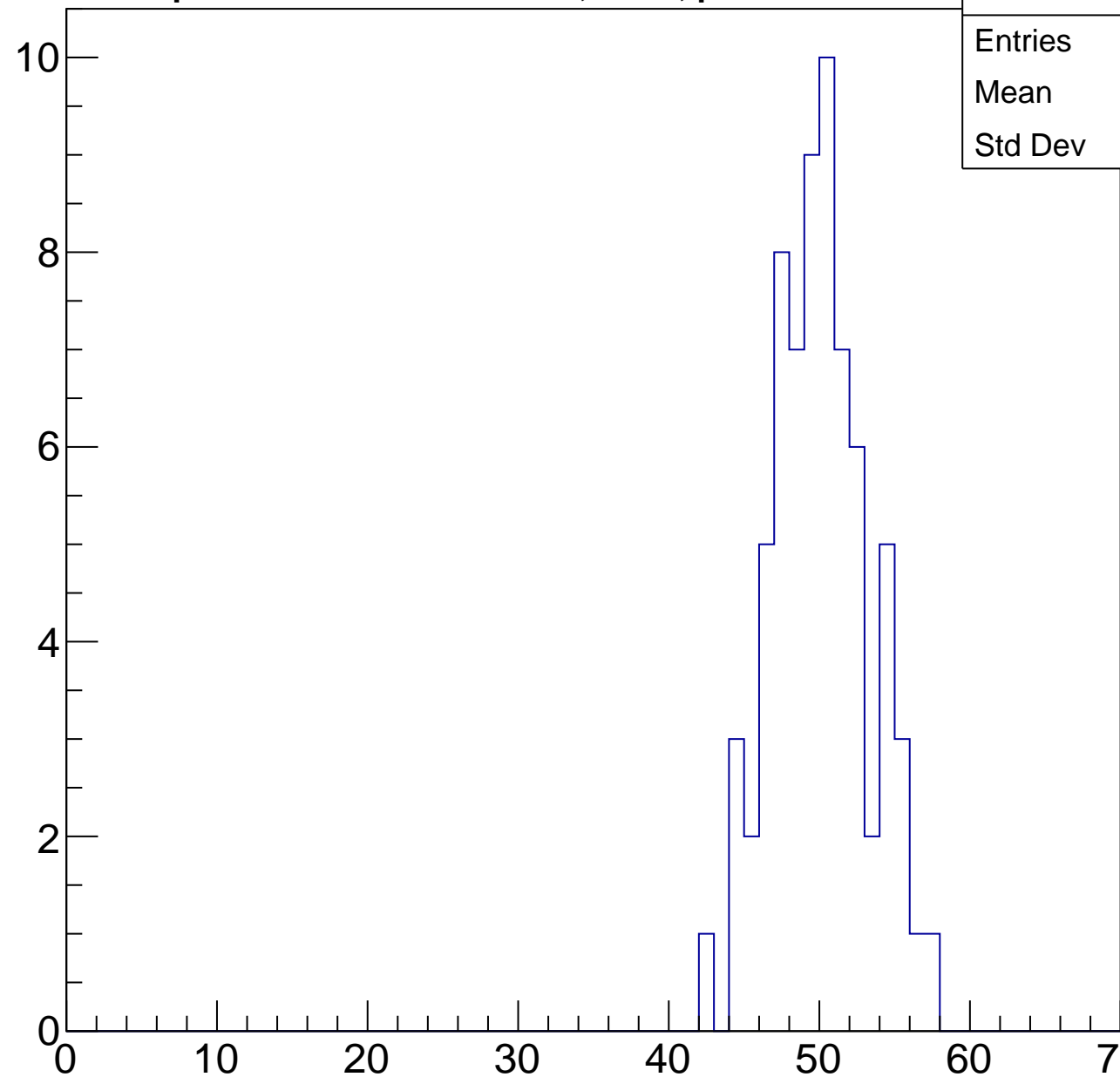
Entries	70
Mean	49.57
Std Dev	3.133

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

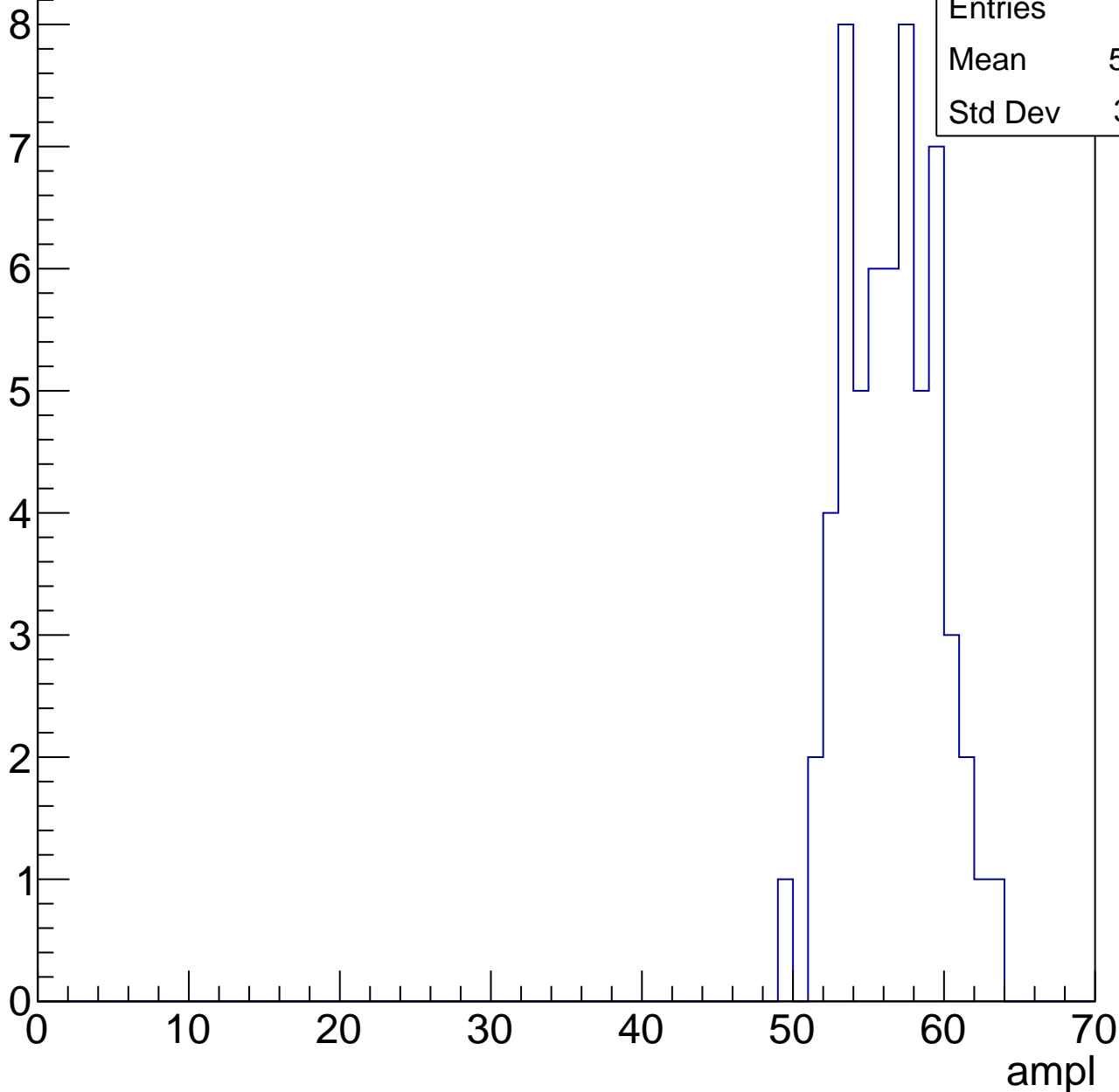


B1L103S, U13-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	56.02
Std Dev	3.011

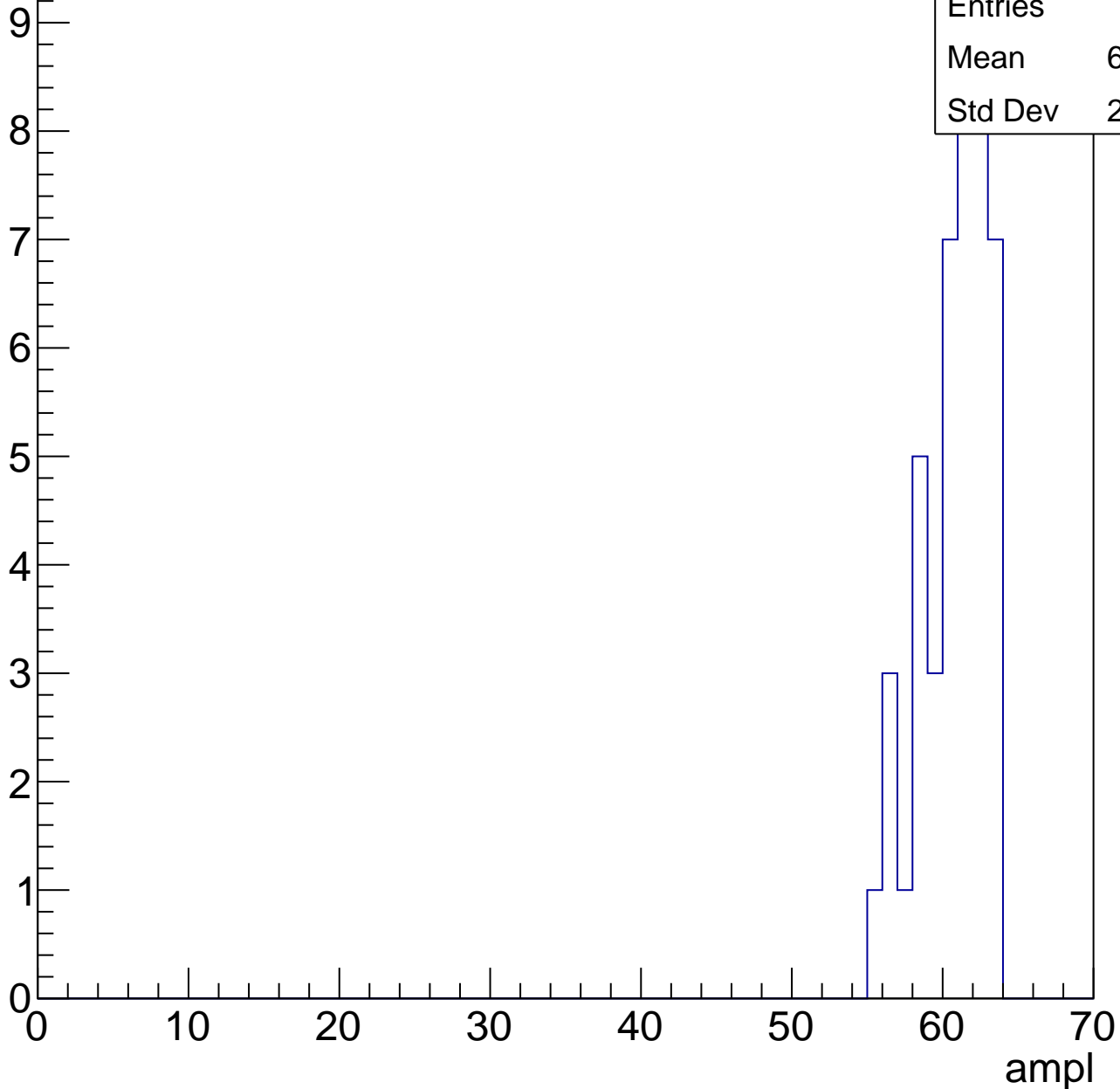


B1L103S, U13-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	60.32
Std Dev	2.172



B1L103S, U13-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch47, adc0

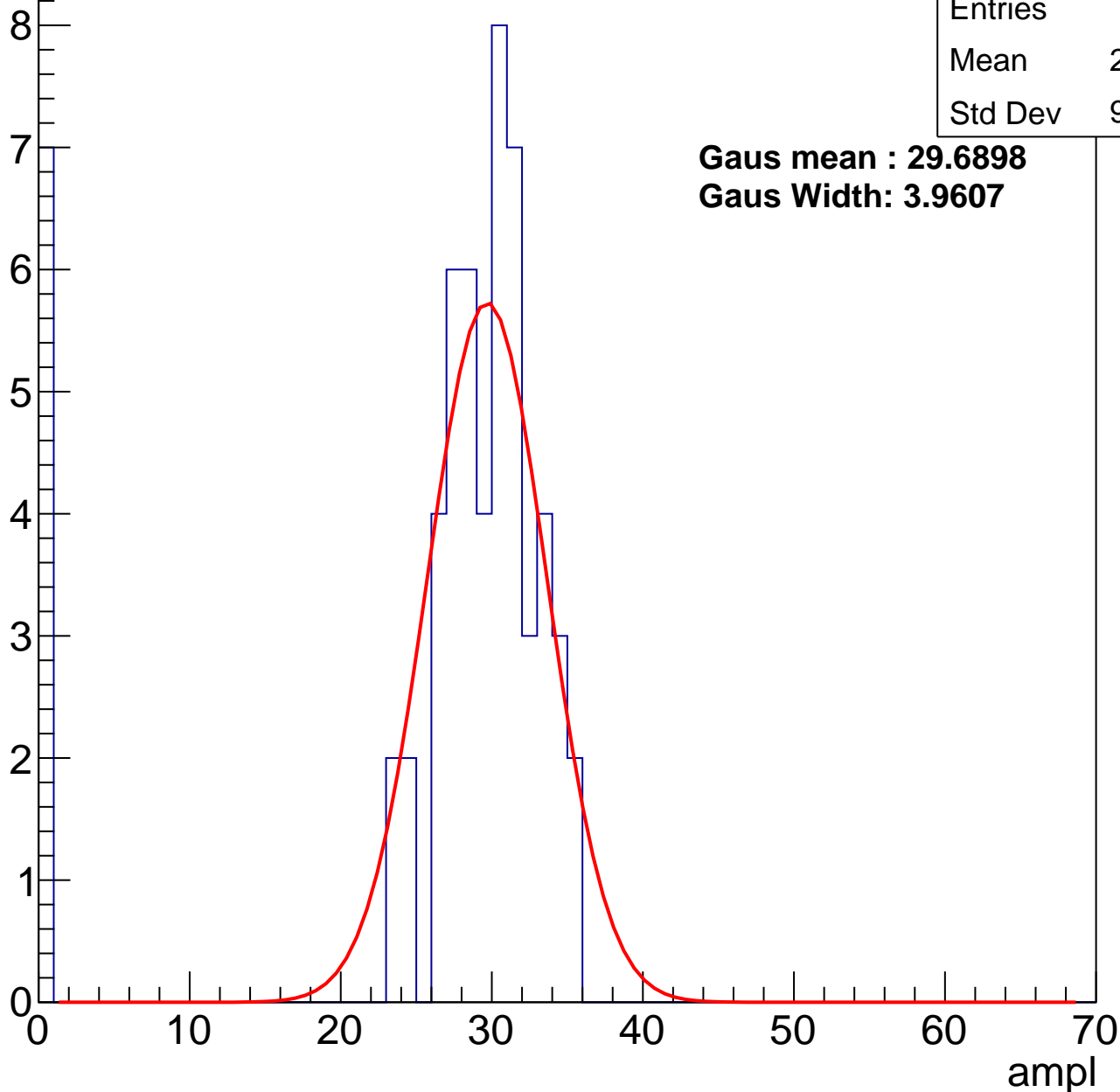
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	25.88
Std Dev	9.986

Gaus mean : 29.6898

Gaus Width: 3.9607



B1L103S, U13-ch47, adc1

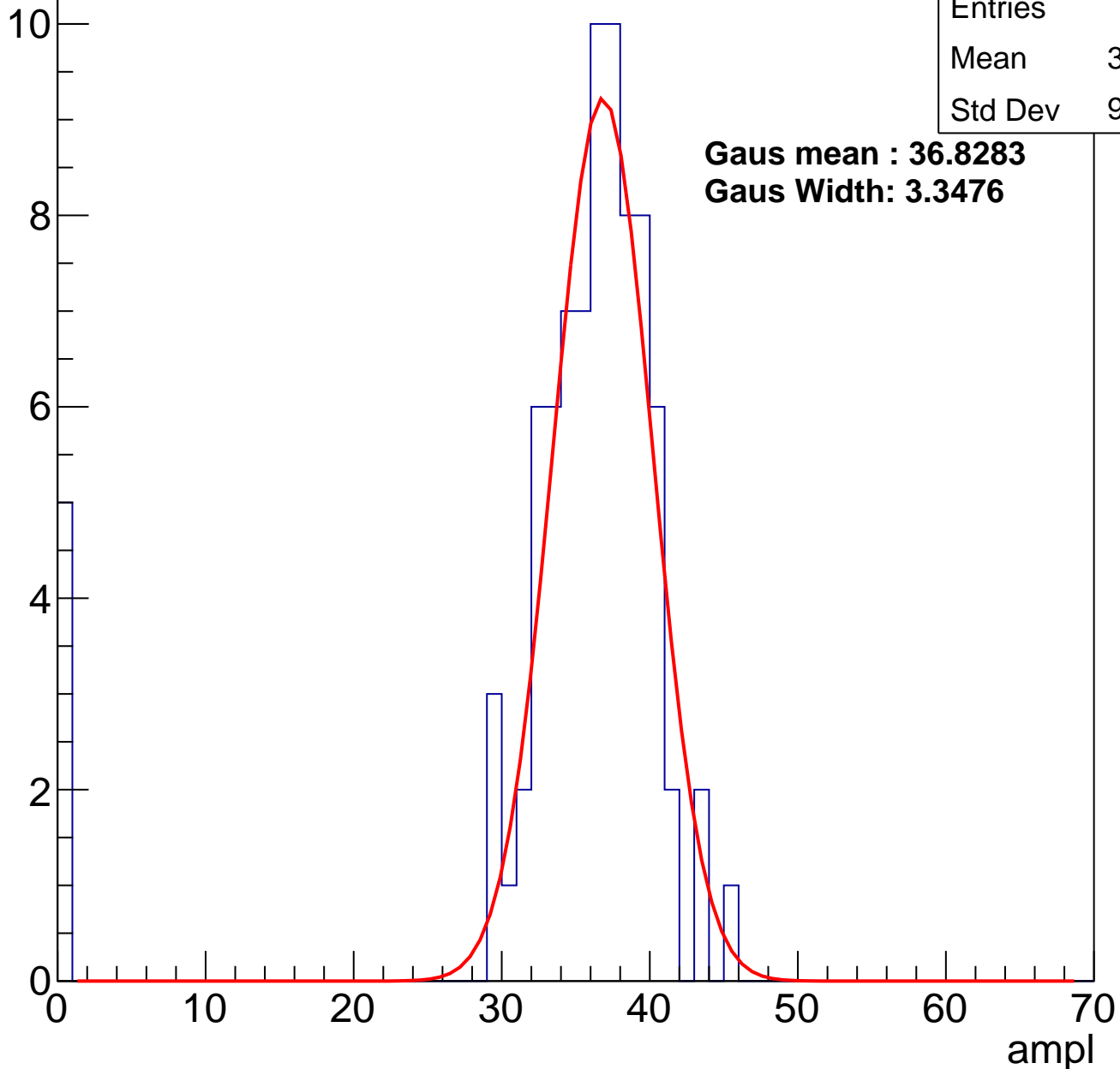
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	33.94
Std Dev	9.116

Gaus mean : 36.8283

Gaus Width: 3.3476

Entry



B1L103S, U13-ch47, adc2

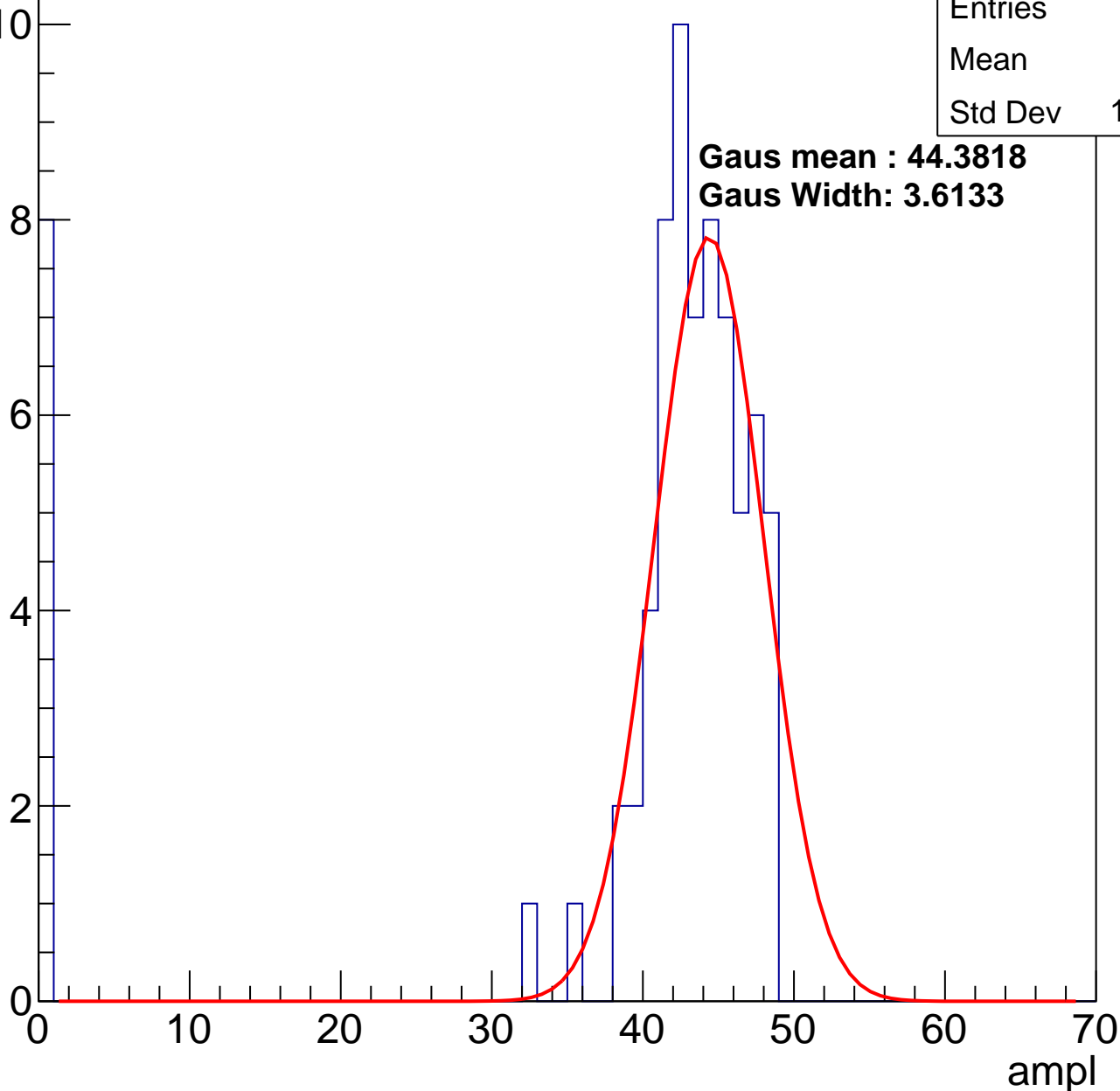
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	38.5
Std Dev	13.73

Gaus mean : 44.3818

Gaus Width: 3.6133

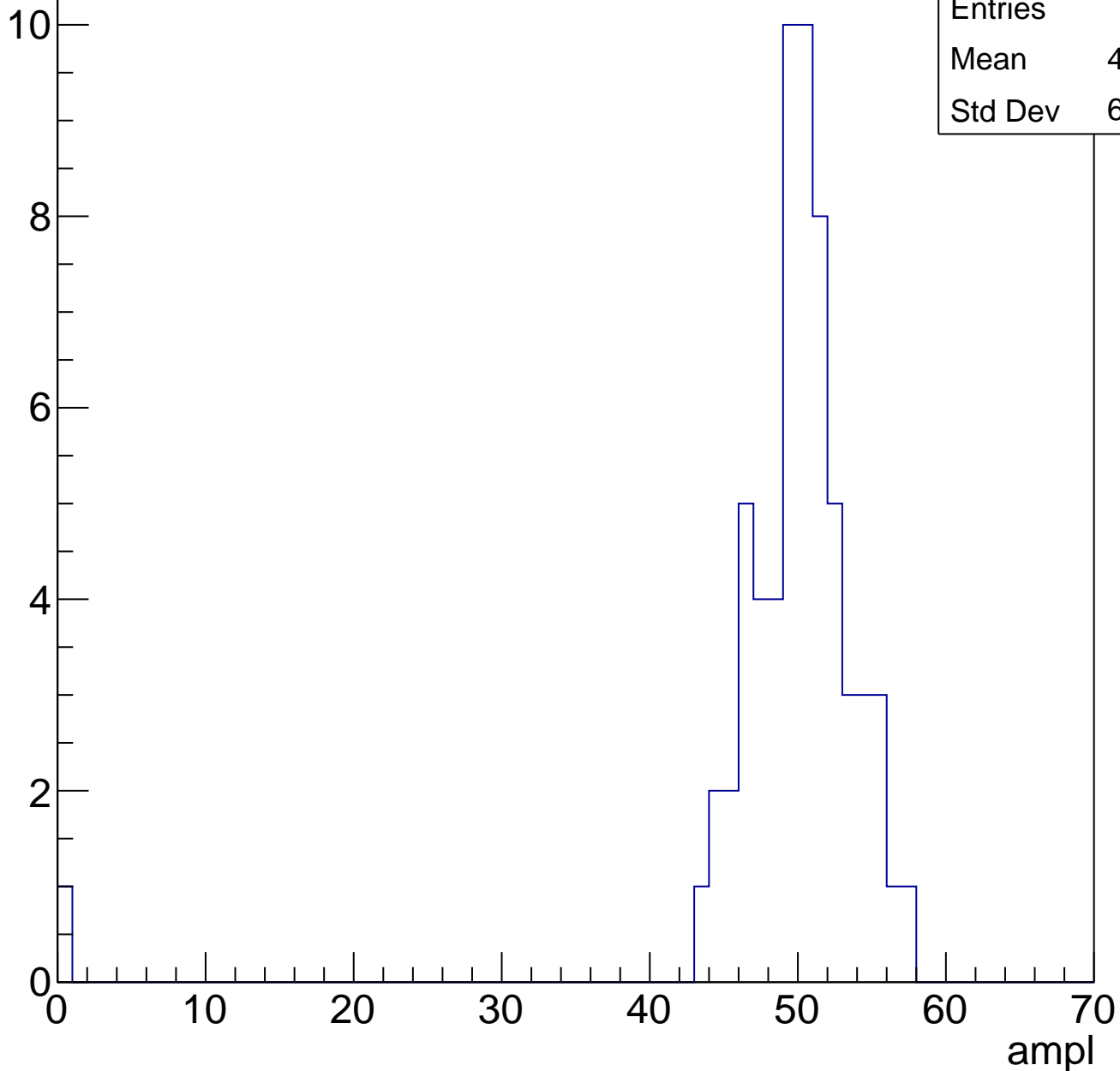


B1L103S, U13-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.02
Std Dev	6.918

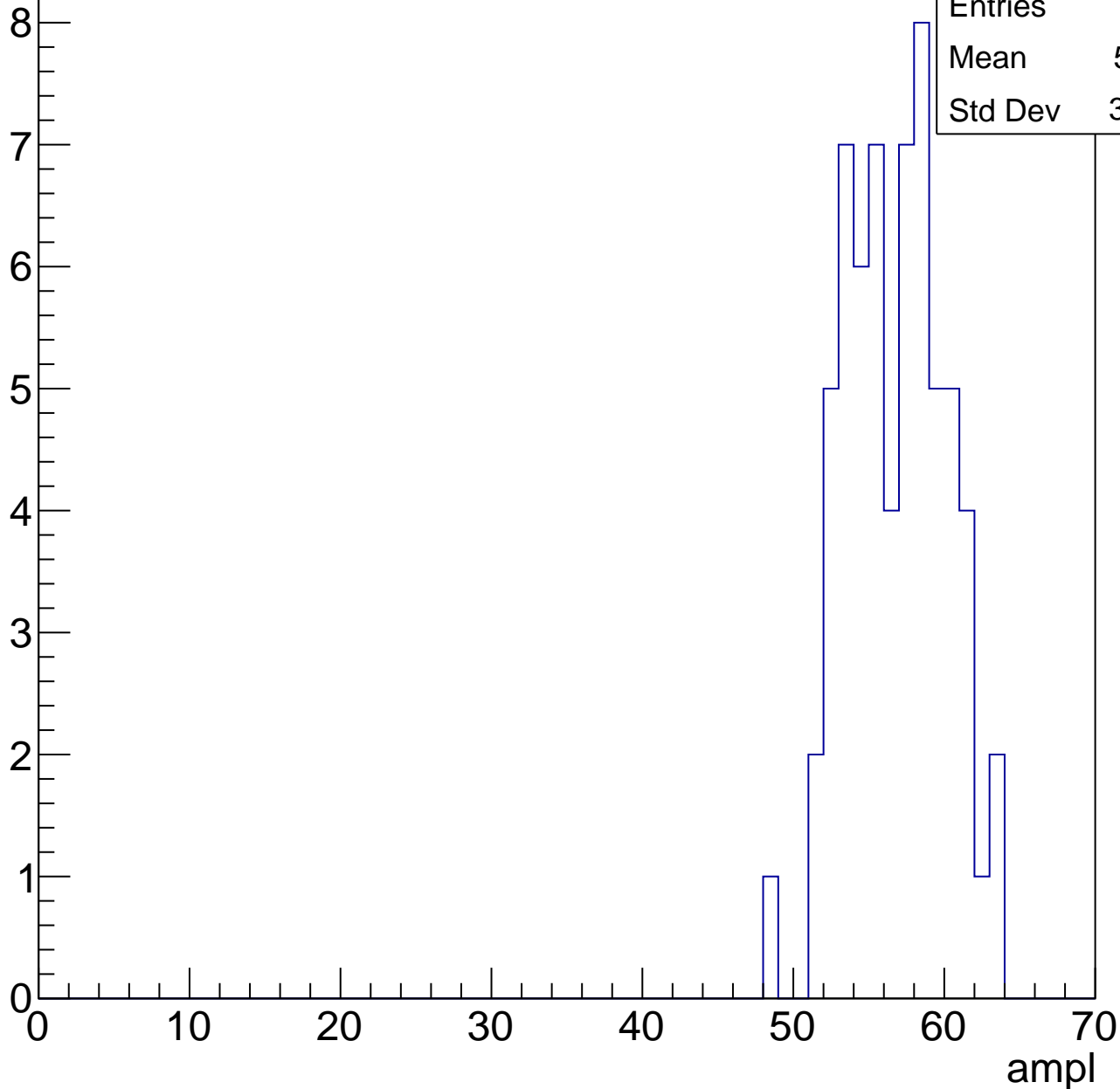


B1L103S, U13-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	56.31
Std Dev	3.259

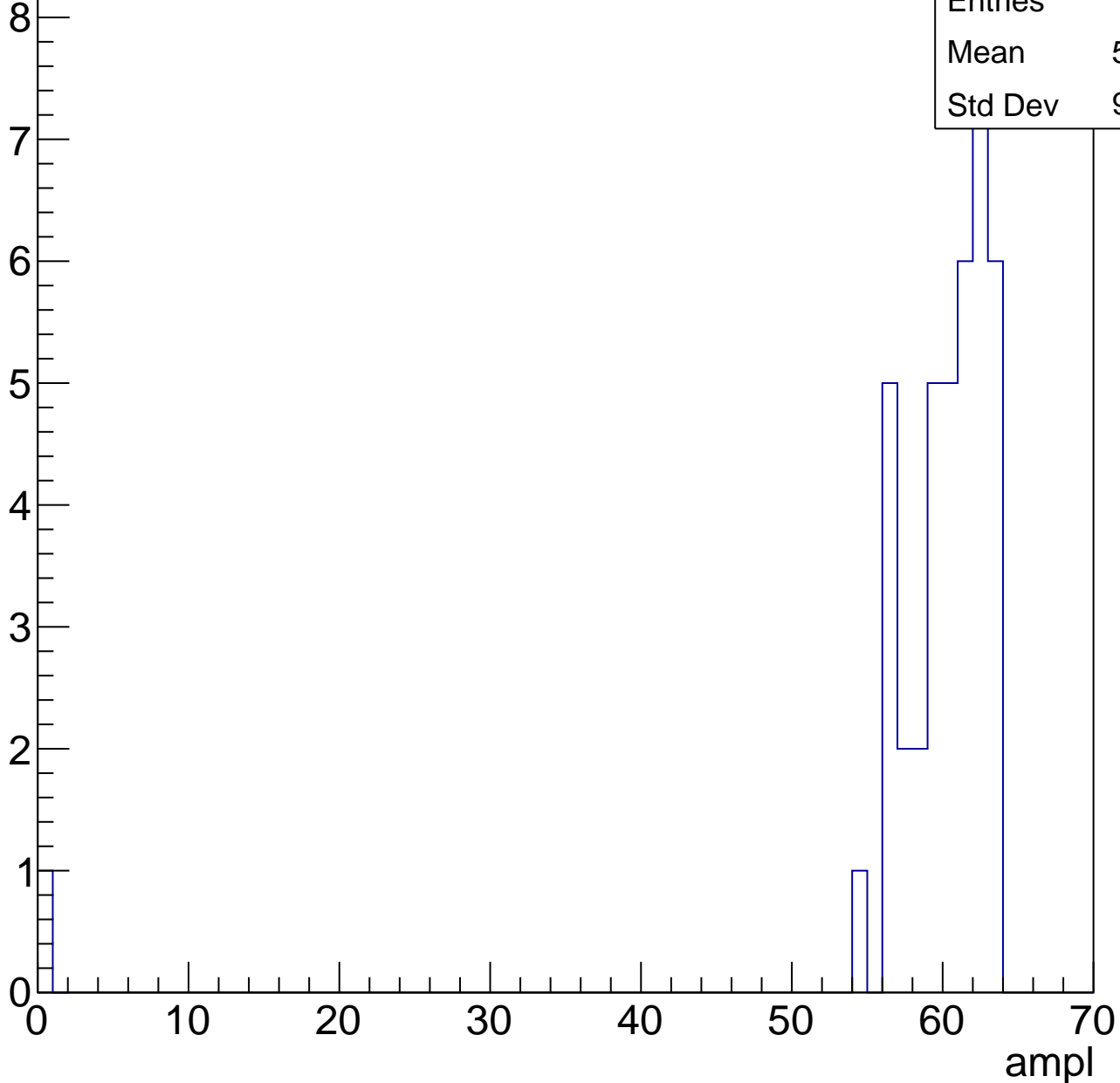


B1L103S, U13-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	58.51
Std Dev	9.561



B1L103S, U13-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch48, adc0

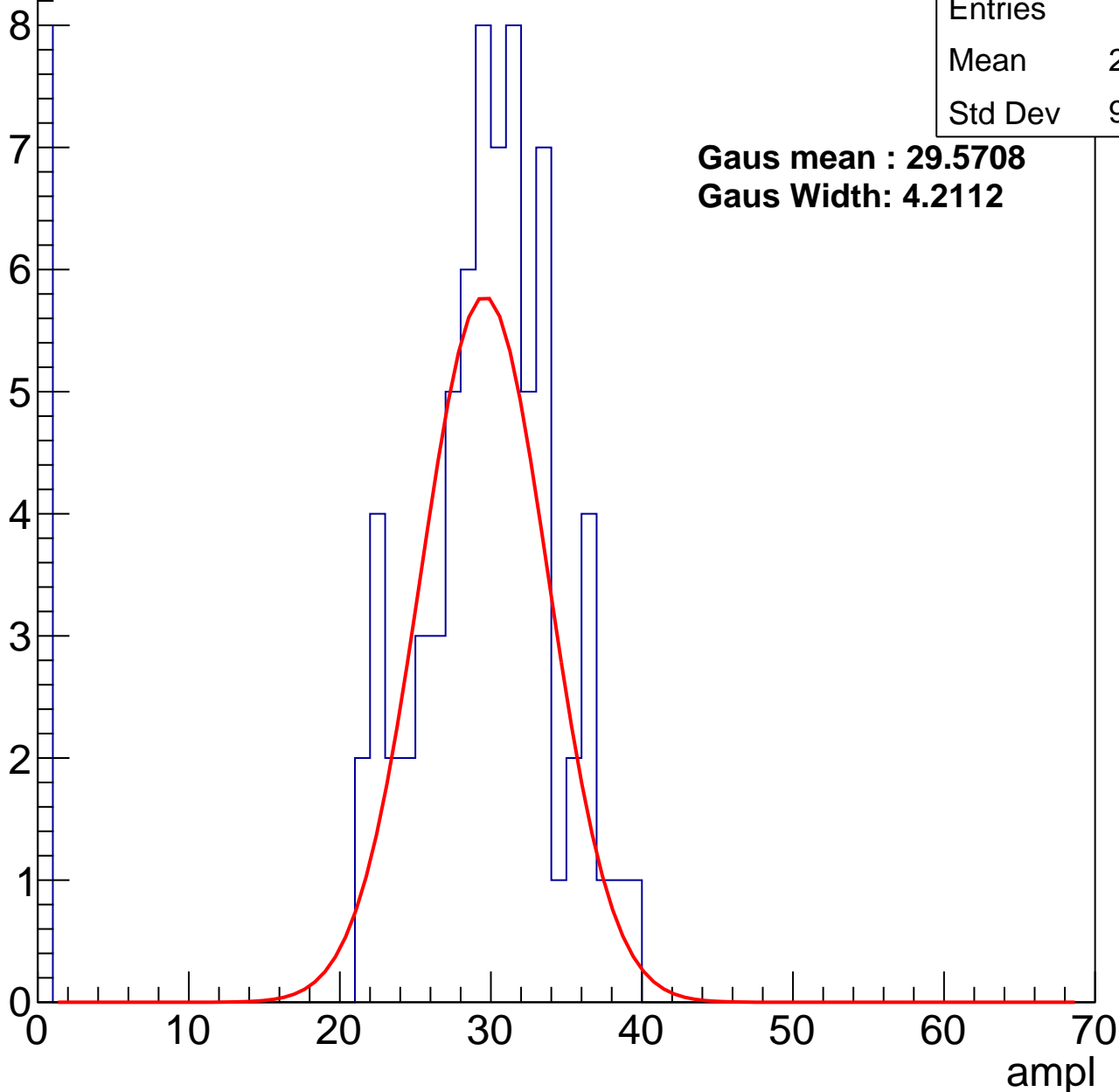
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	26.54
Std Dev	9.698

Gaus mean : 29.5708

Gaus Width: 4.2112



B1L103S, U13-ch48, adc1

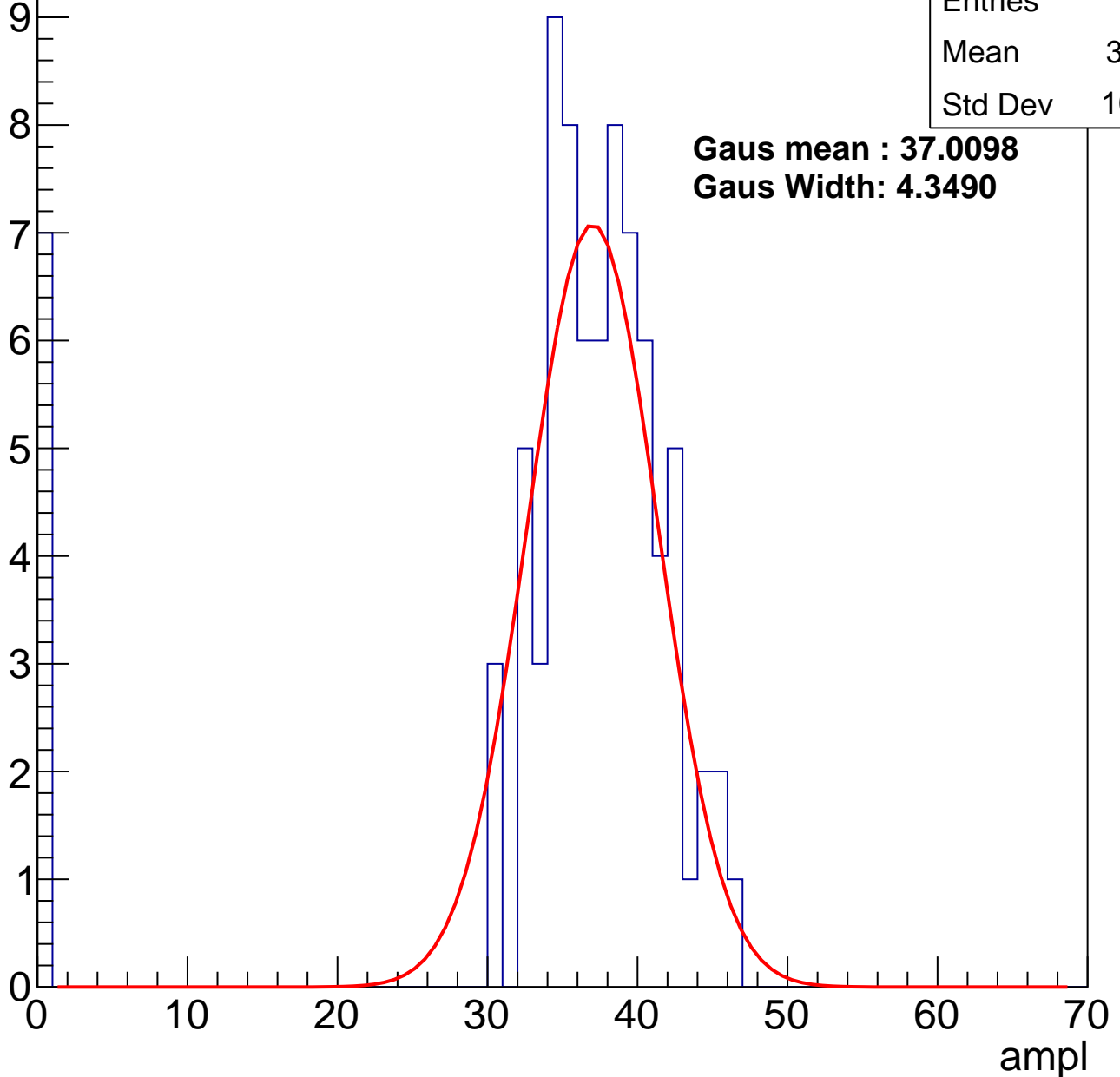
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	34.11
Std Dev	10.95

Gaus mean : 37.0098

Gaus Width: 4.3490



B1L103S, U13-ch48, adc2

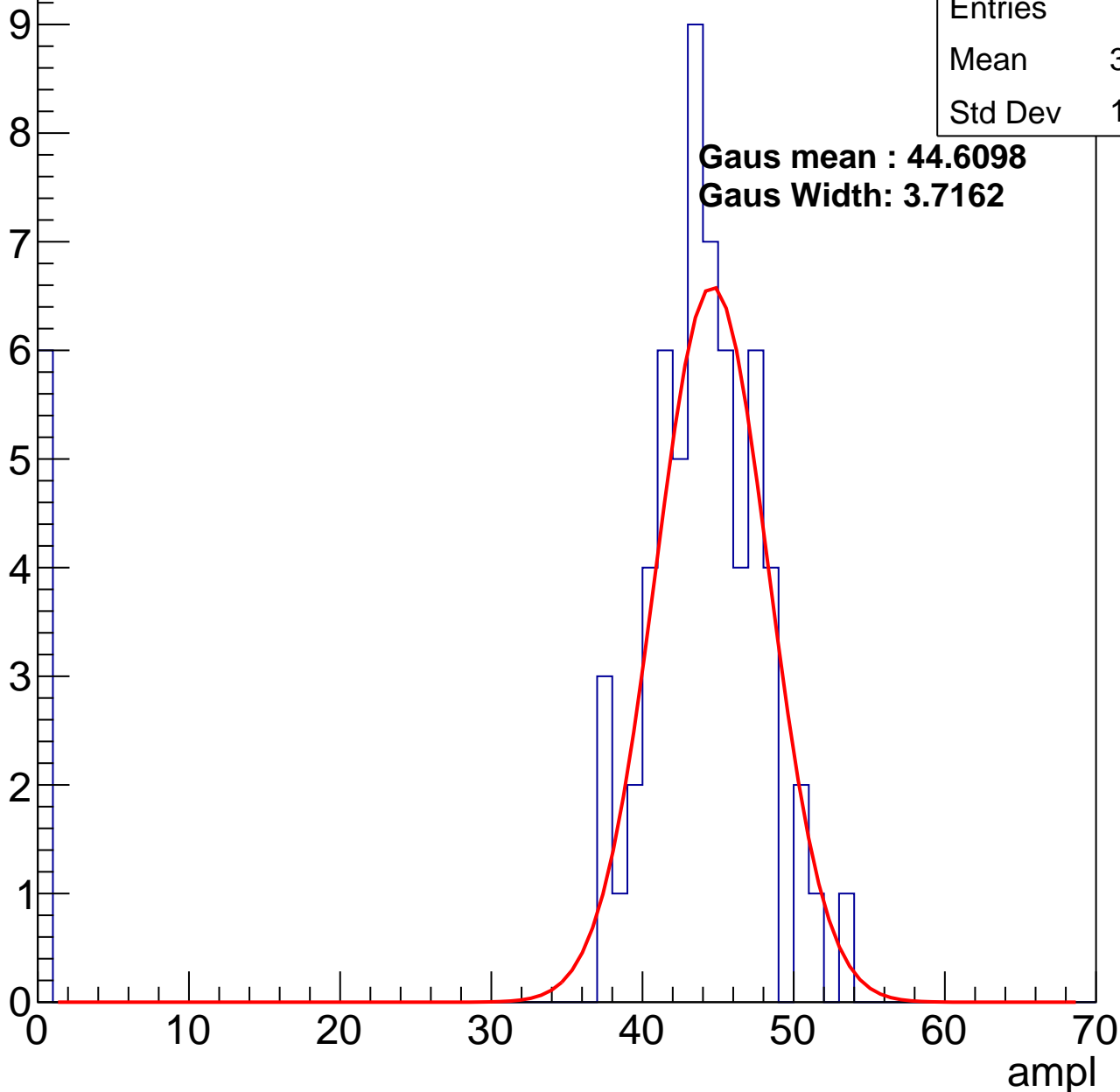
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	39.85
Std Dev	12.92

Gaus mean : 44.6098

Gaus Width: 3.7162



B1L103S, U13-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

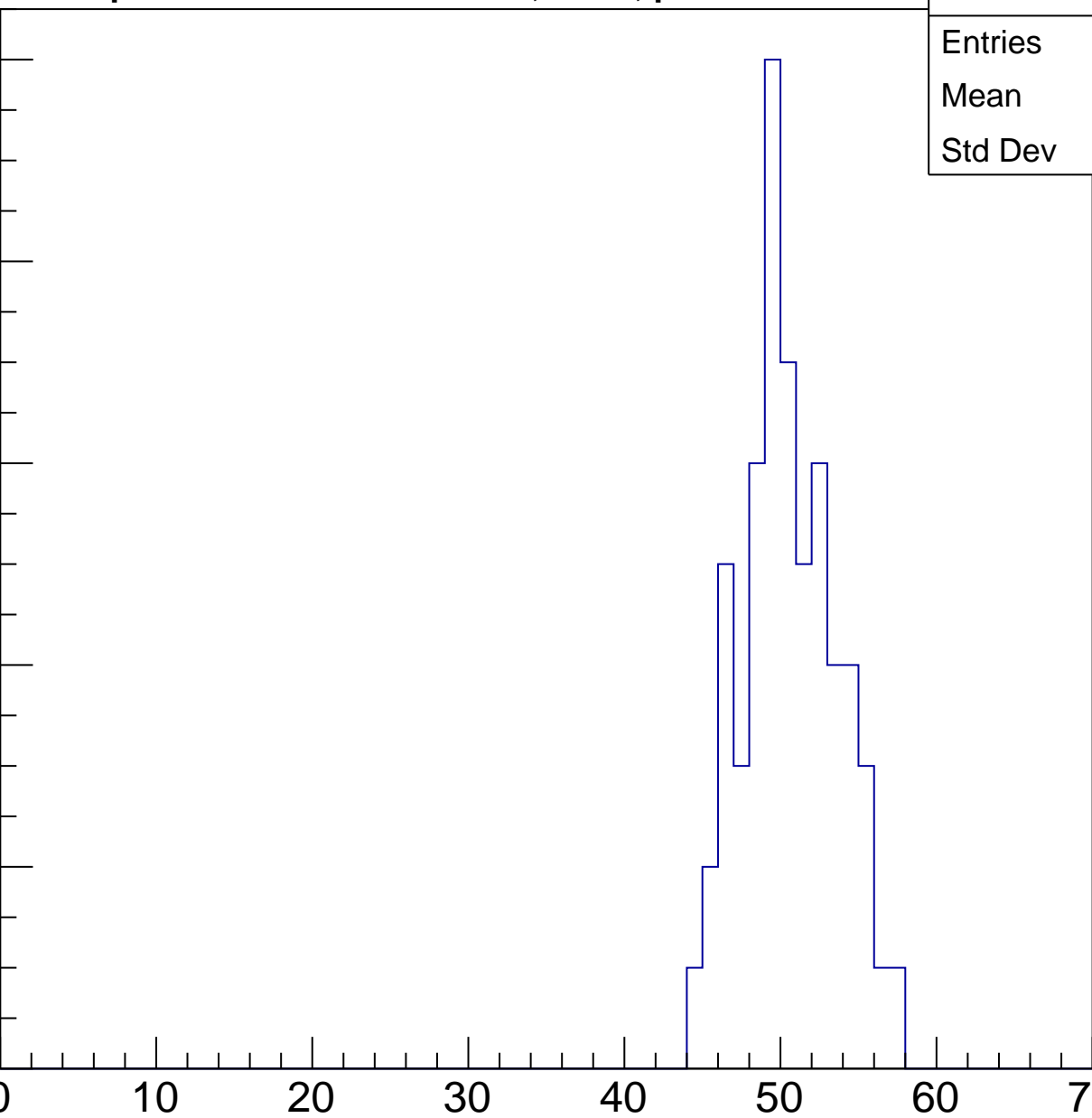
Entries	58
Mean	50.1
Std Dev	2.981

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

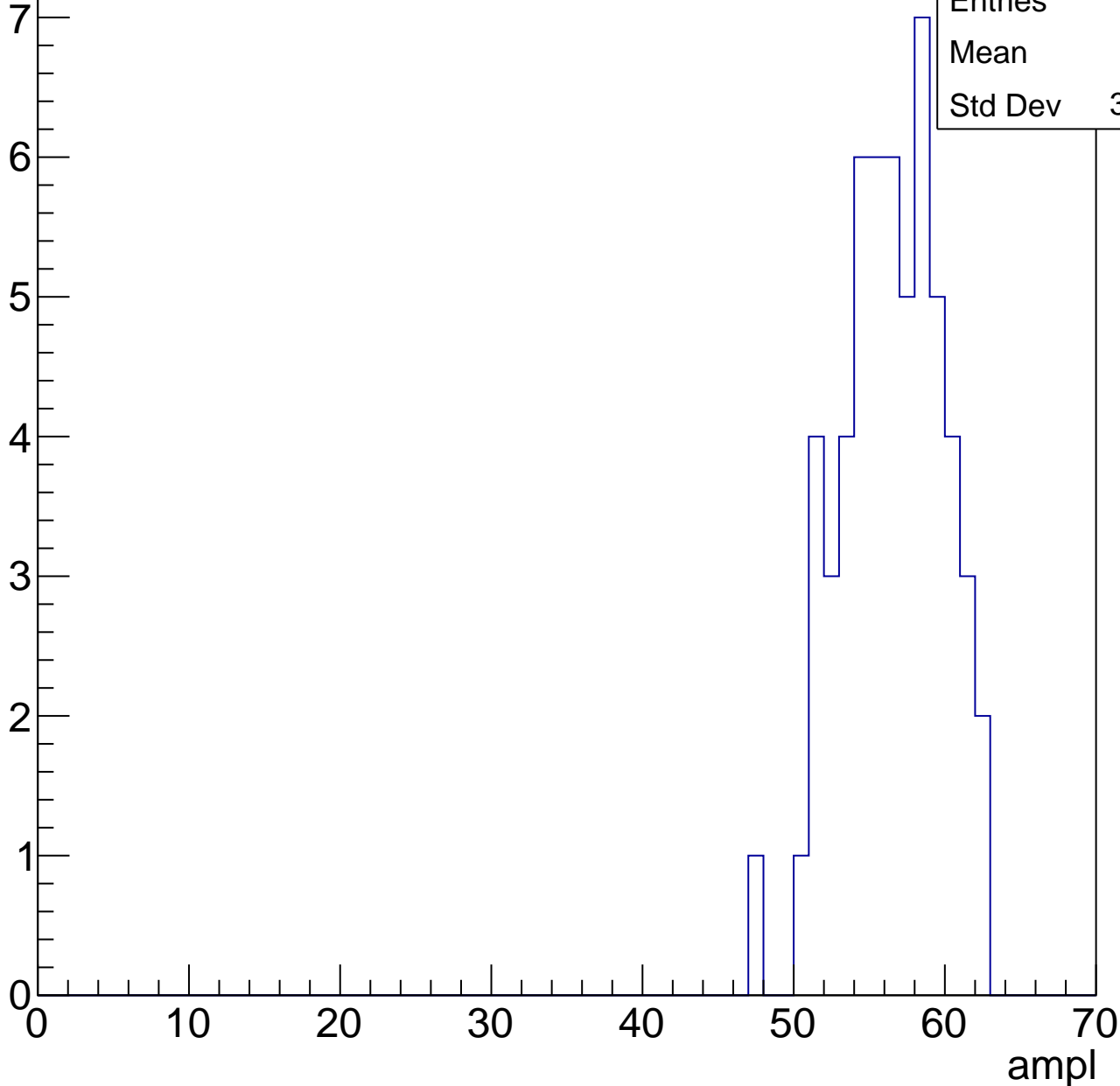


B1L103S, U13-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	56
Std Dev	3.298

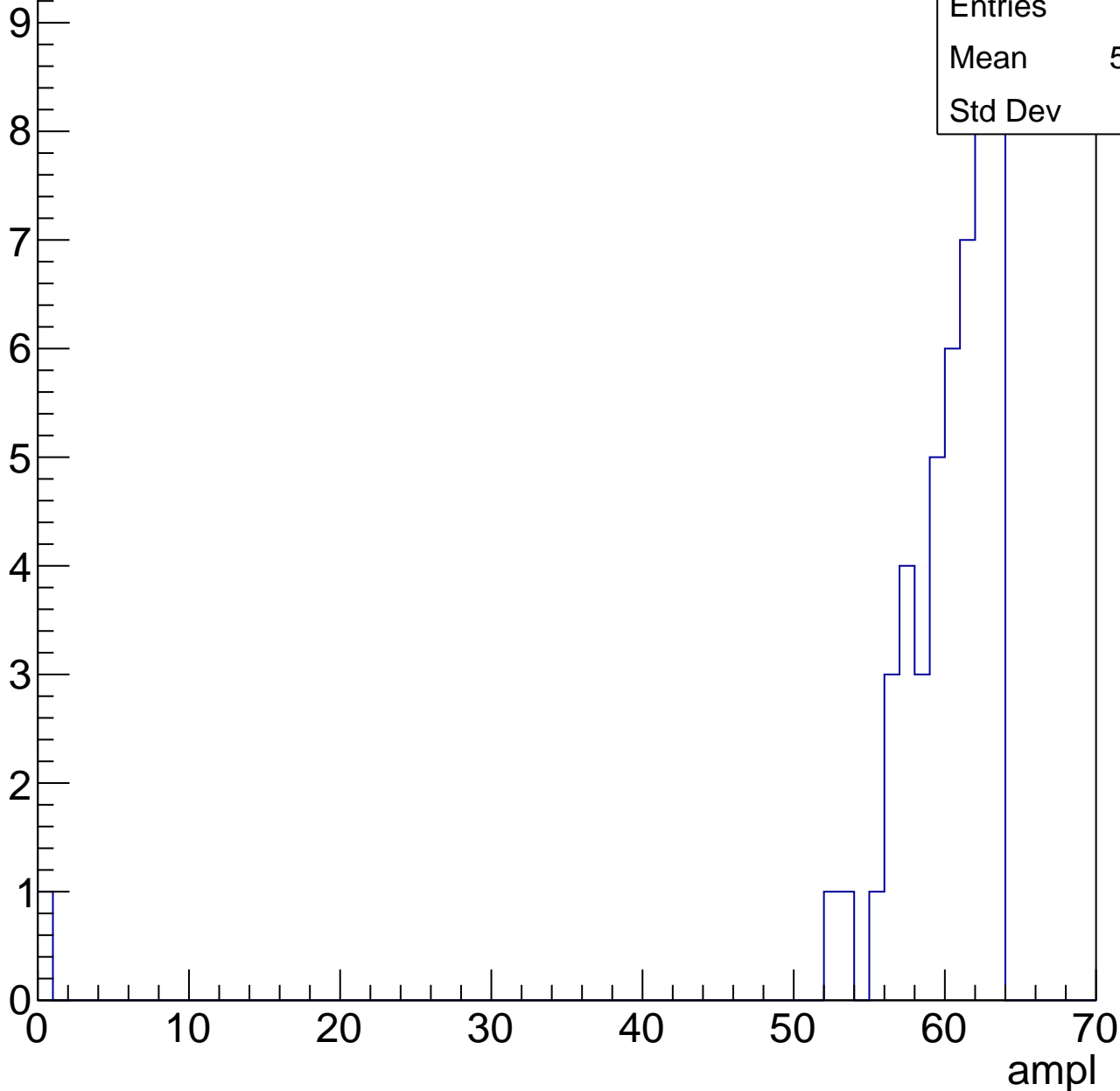


B1L103S, U13-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.67
Std Dev	8.89



B1L103S, U13-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

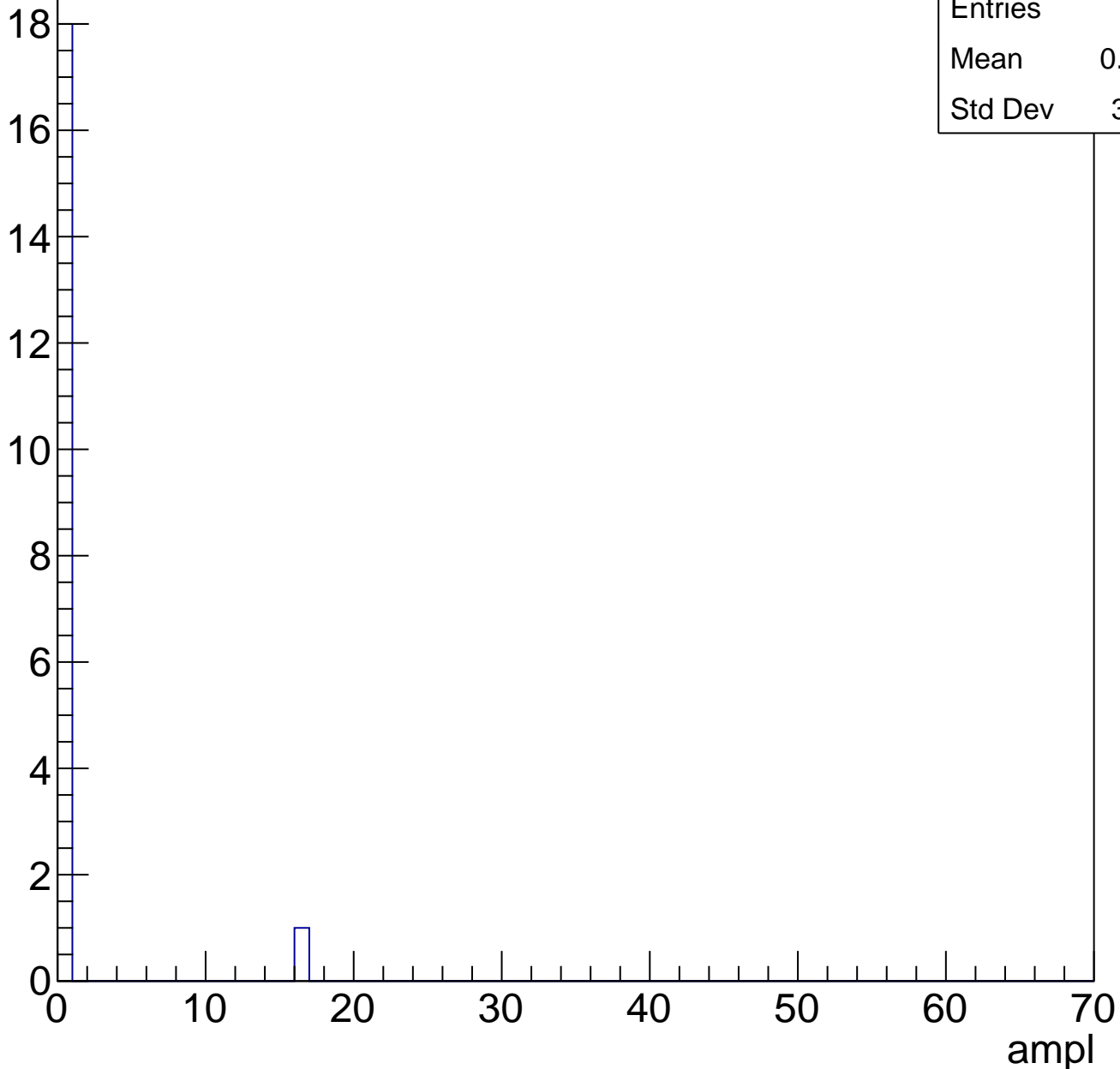


B1L103S, U13-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0.8421
Std Dev	3.573

Entry



B1L103S, U13-ch49, adc0

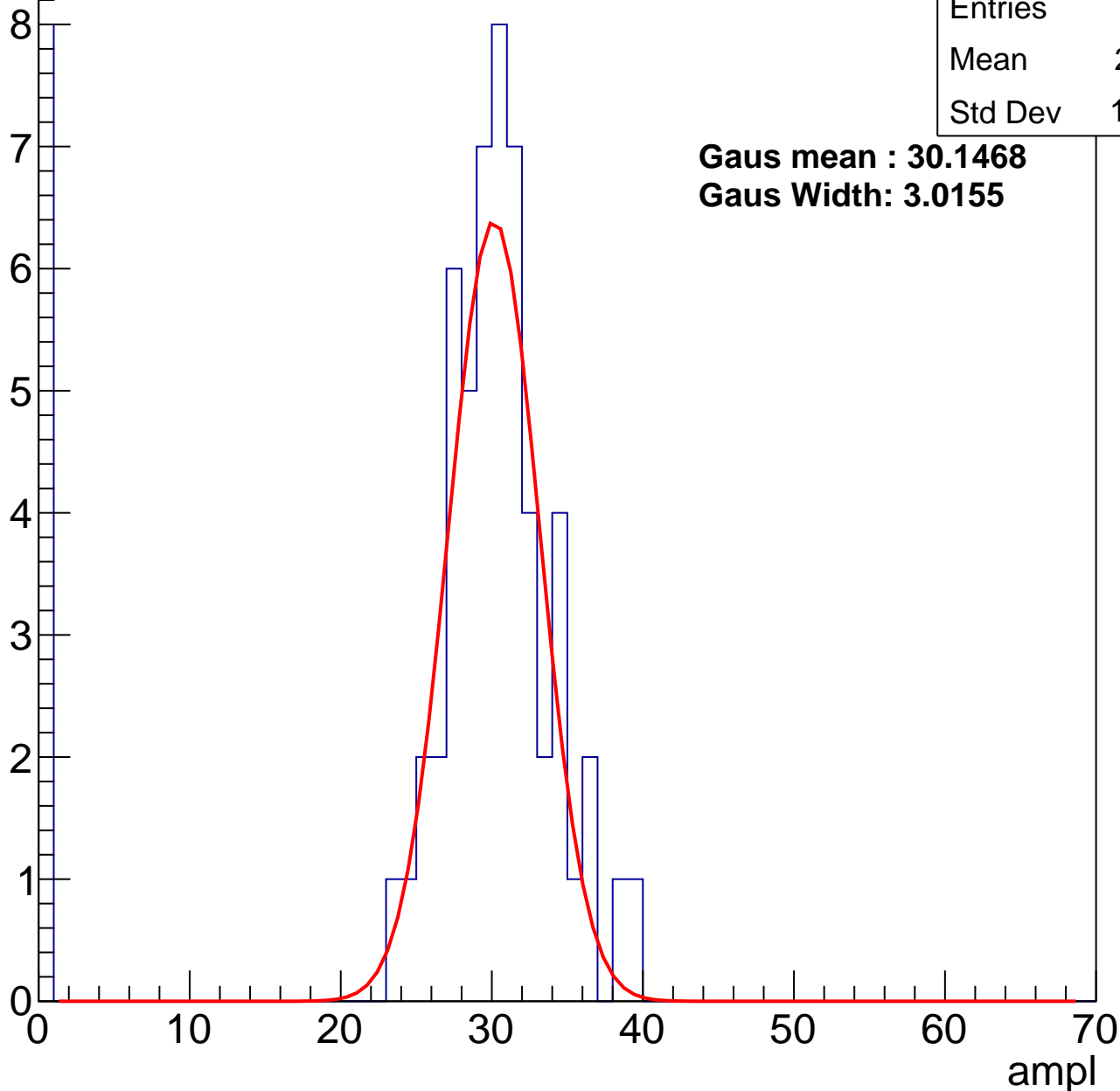
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	26.21
Std Dev	10.55

Gaus mean : 30.1468

Gaus Width: 3.0155



B1L103S, U13-ch49, adc1

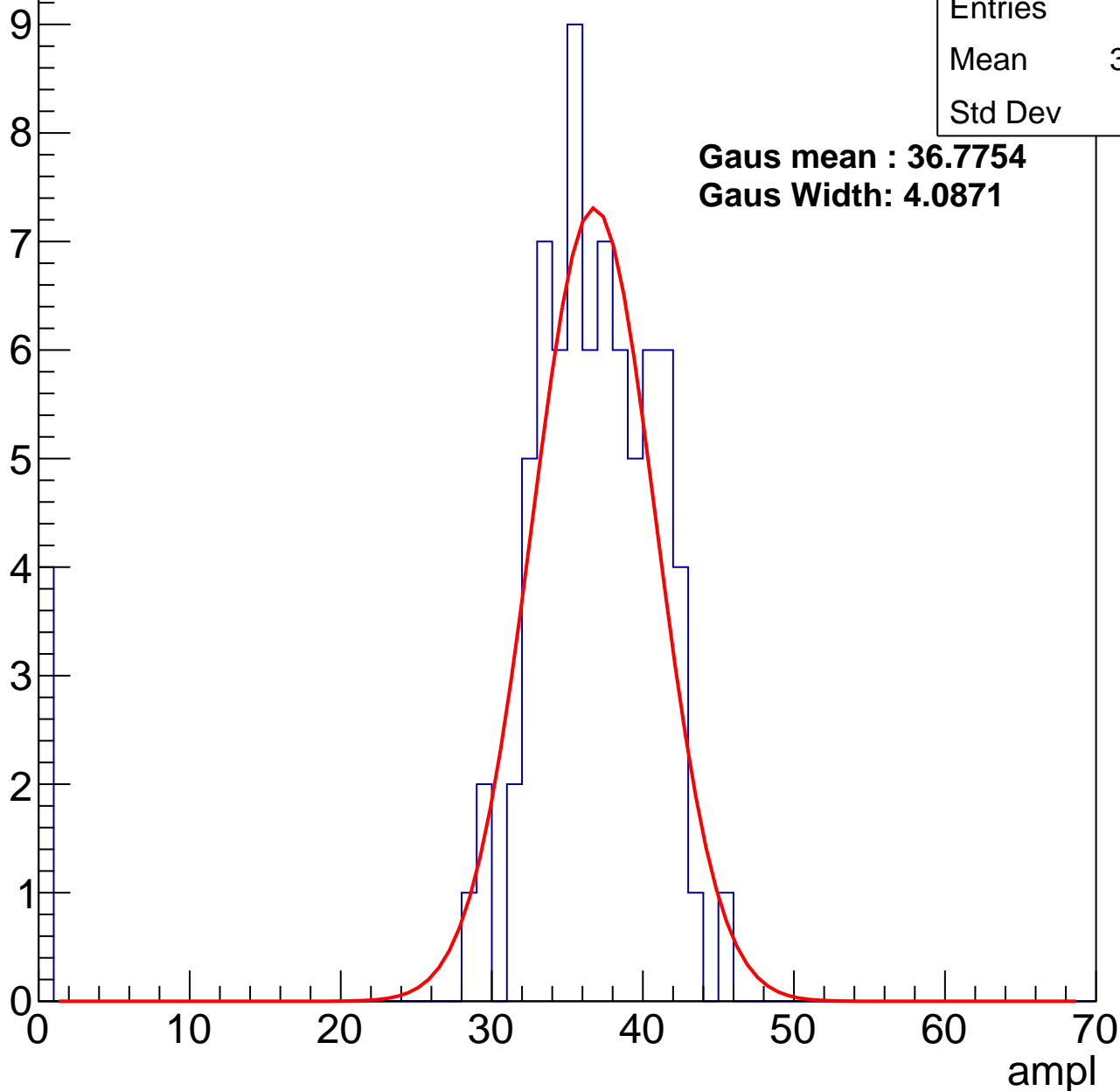
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.59
Std Dev	8.78

Gaus mean : 36.7754

Gaus Width: 4.0871



B1L103S, U13-ch49, adc2

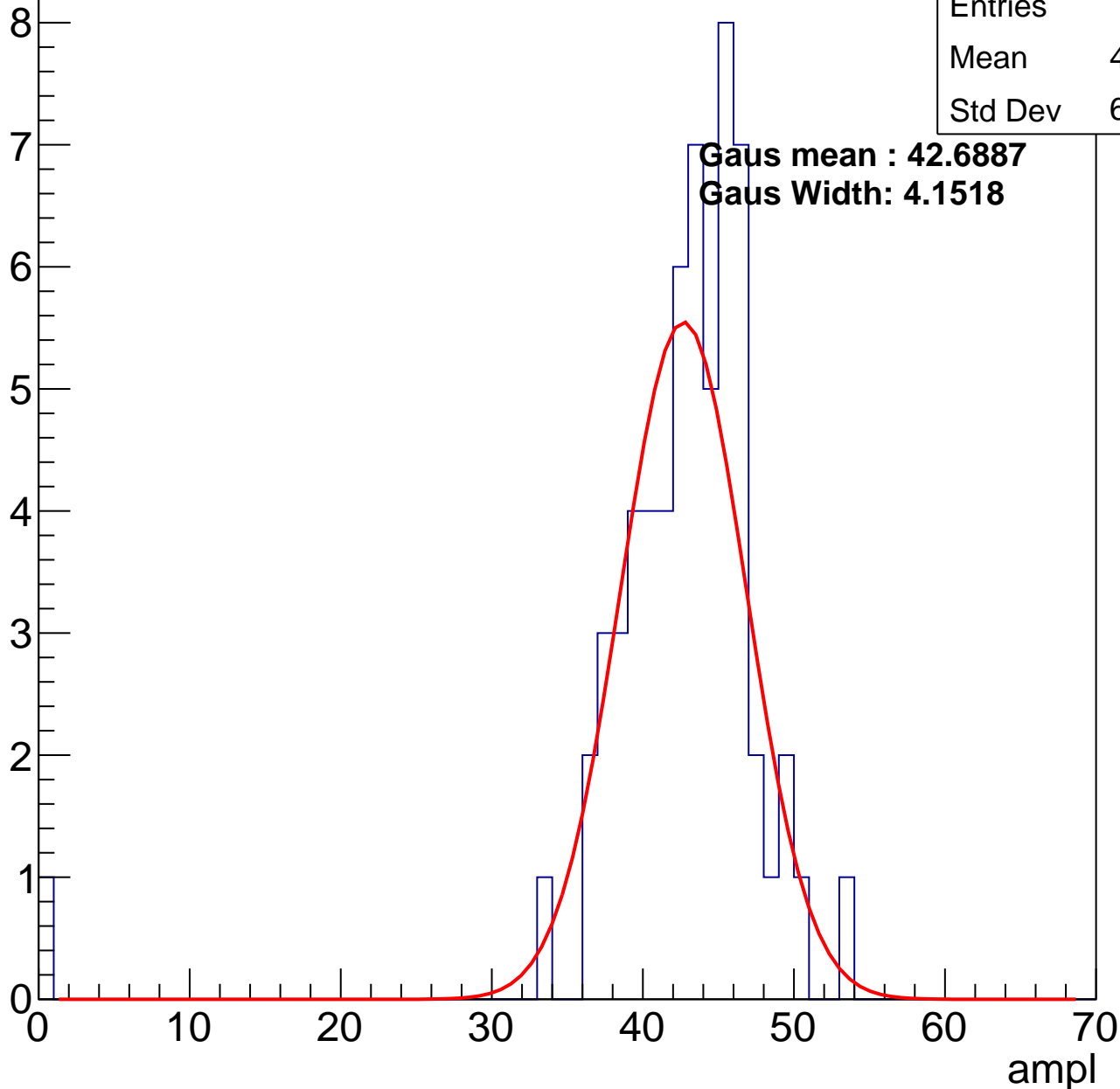
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	42.06
Std Dev	6.569

Gaus mean : 42.6887

Gaus Width: 4.1518

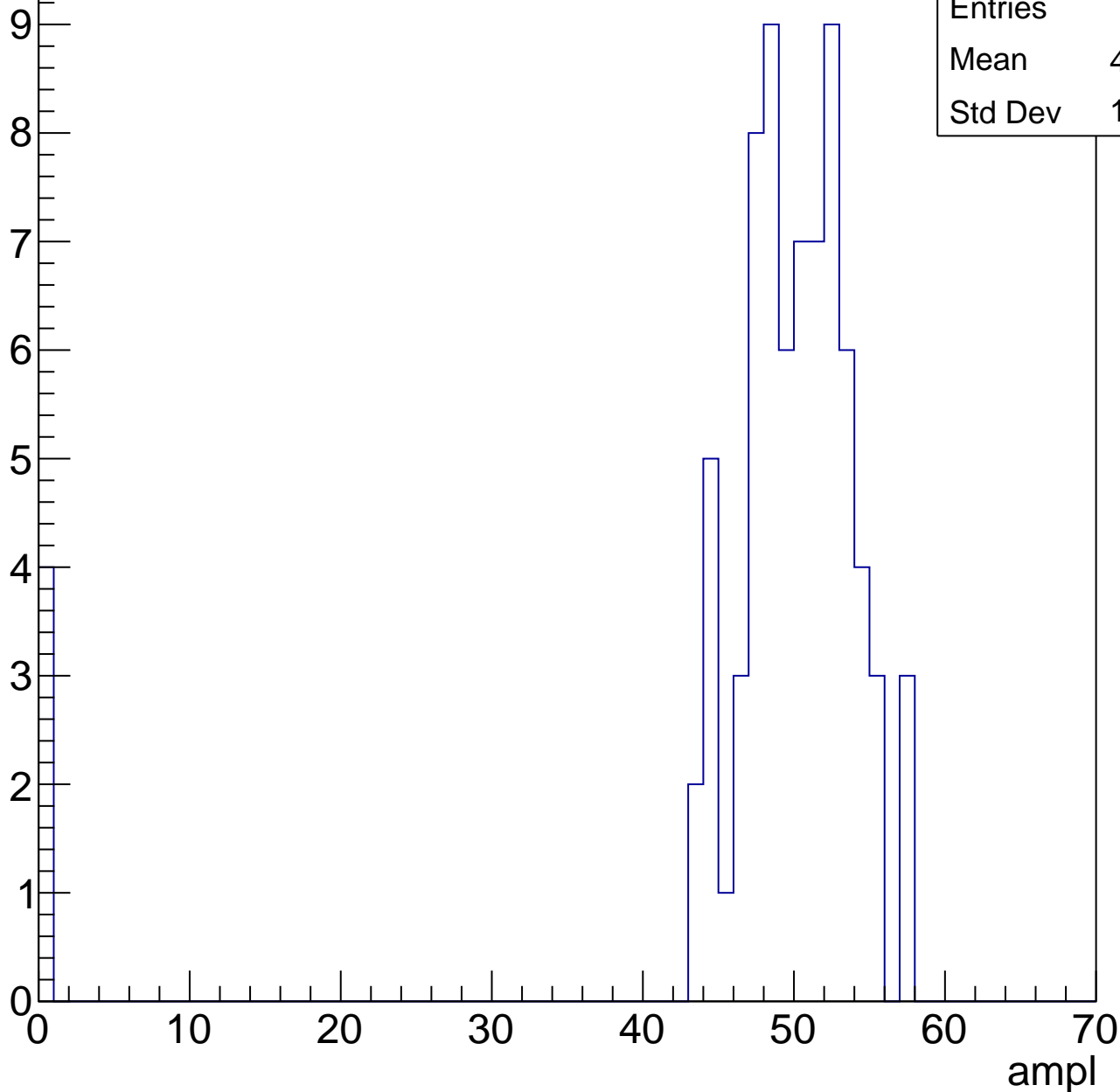


B1L103S, U13-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.22
Std Dev	11.54

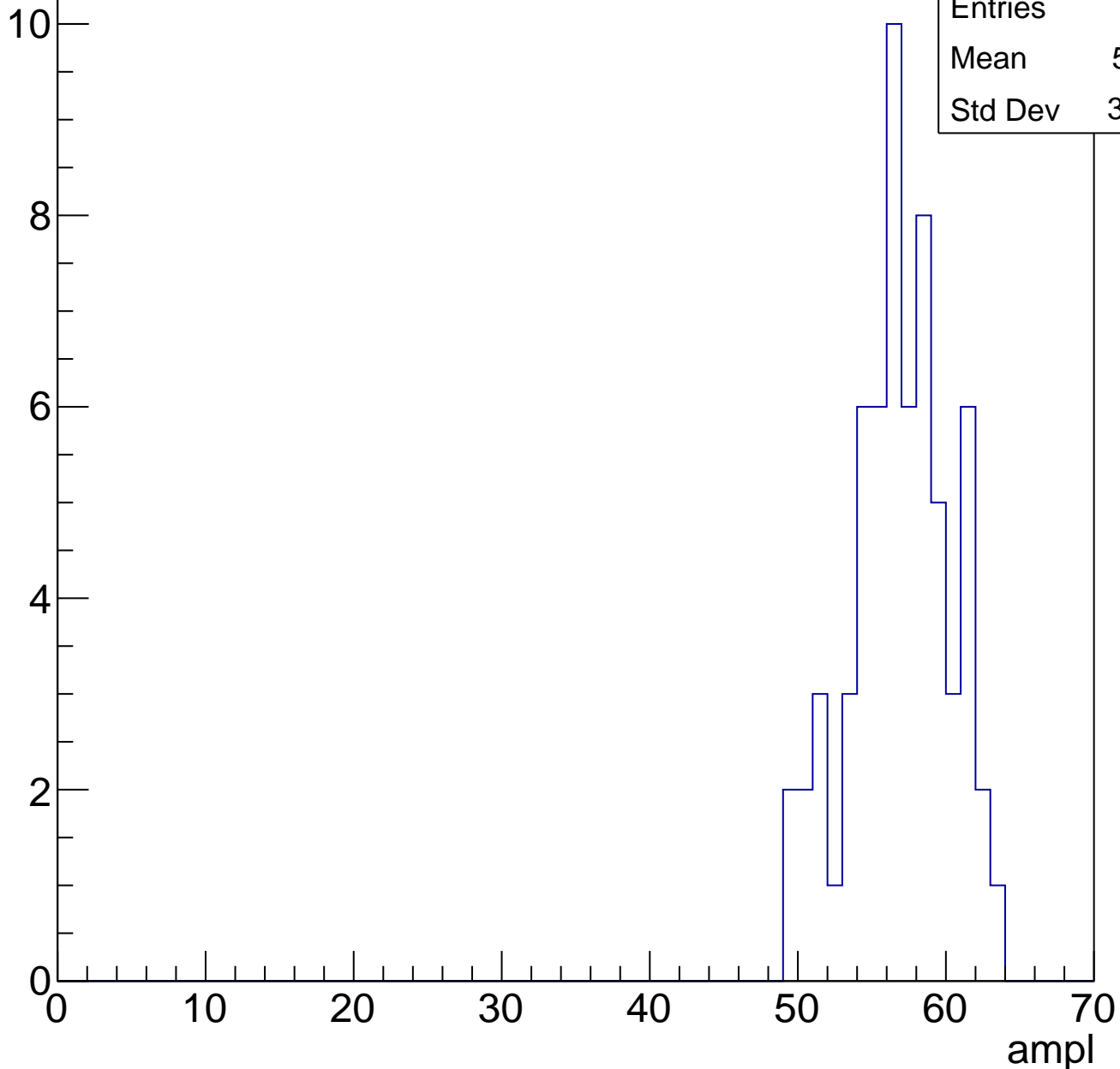


B1L103S, U13-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	56.41
Std Dev	3.329

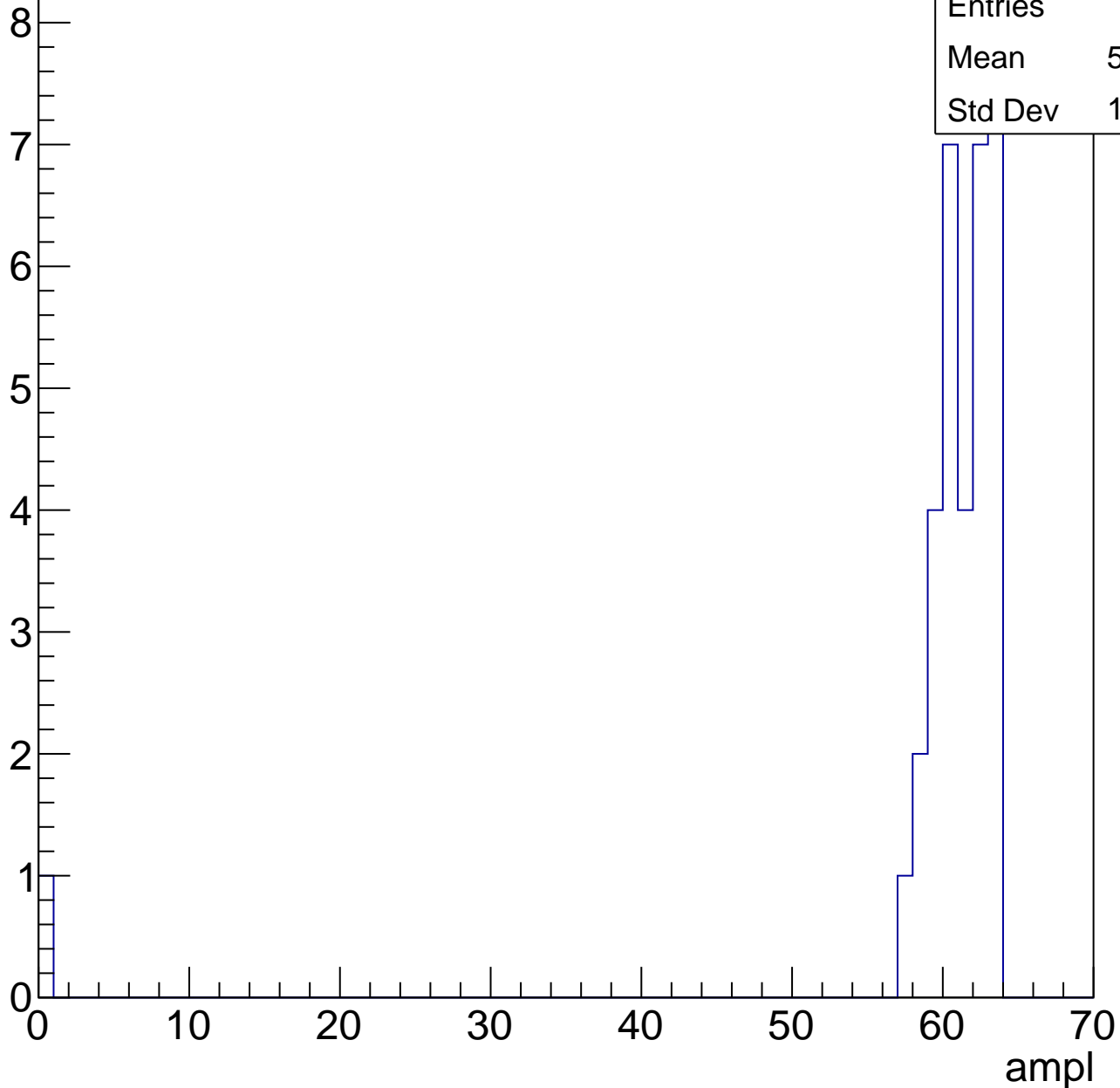
Entry



B1L103S, U13-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

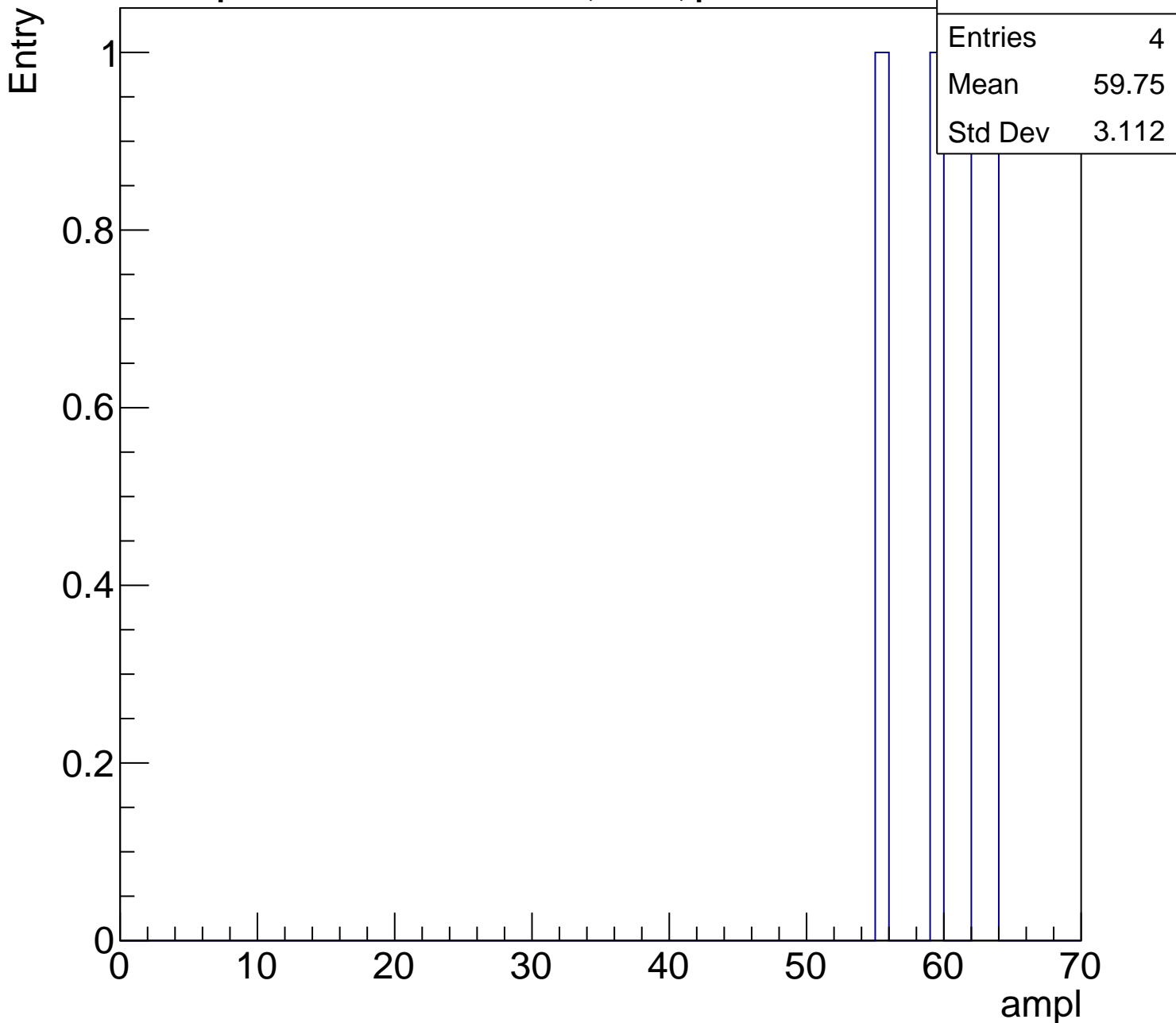
Entry

1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	59.75
Std Dev	3.112

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch50, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	26.31
Std Dev	6.963

Gaus mean : 27.8706

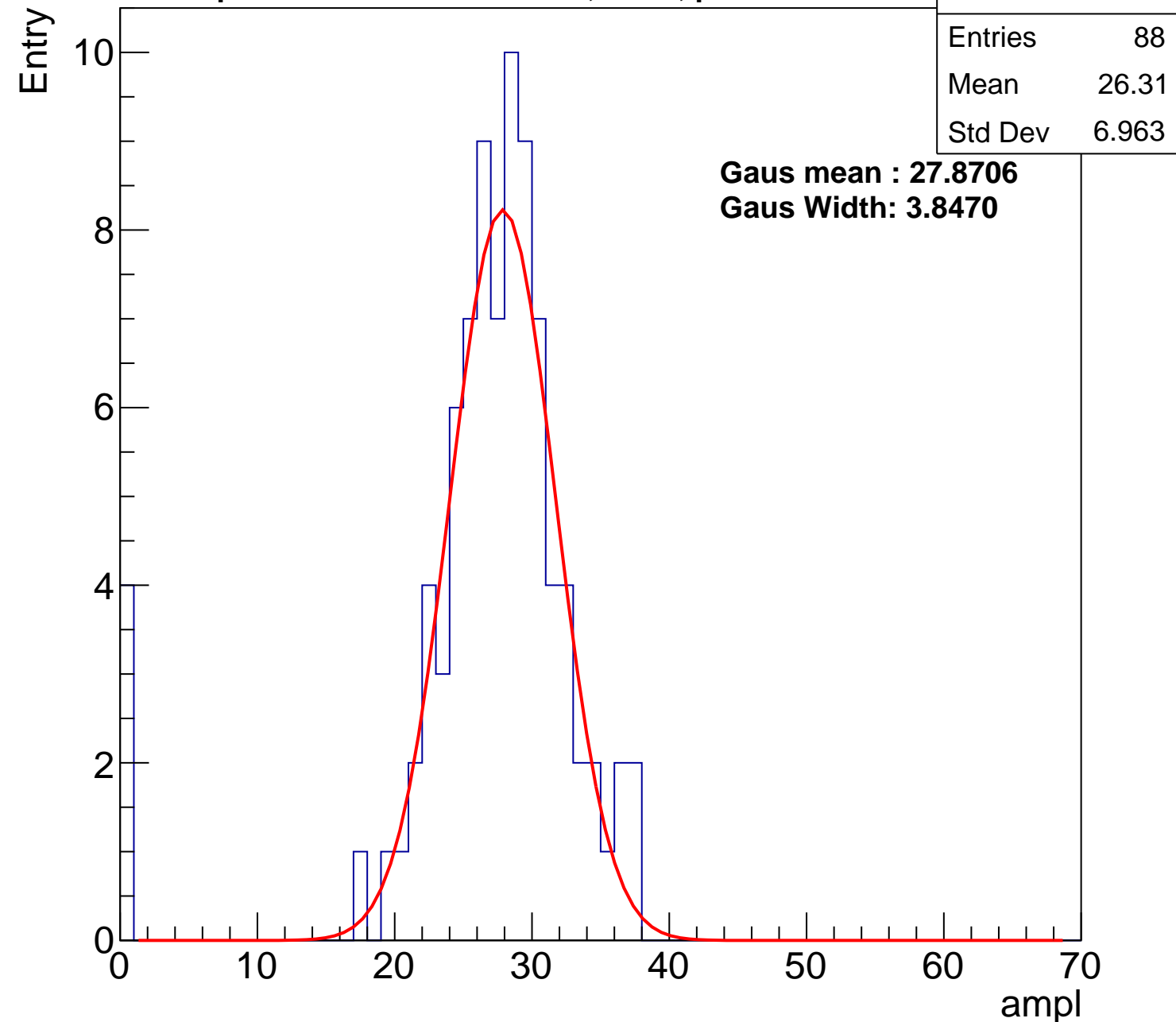
Gaus Width: 3.8470

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch50, adc1

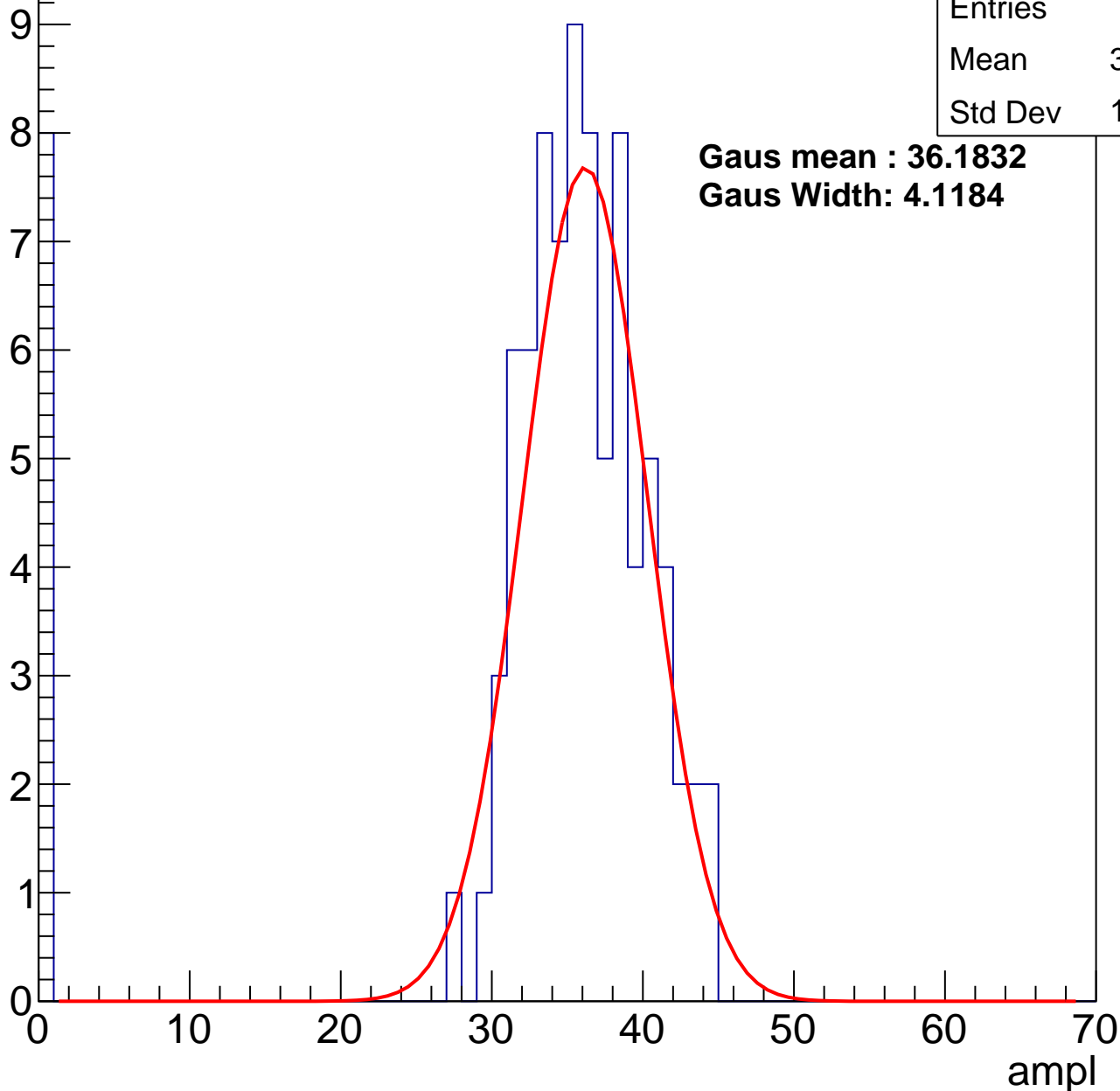
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	32.54
Std Dev	10.83

Gaus mean : 36.1832

Gaus Width: 4.1184



B1L103S, U13-ch50, adc2

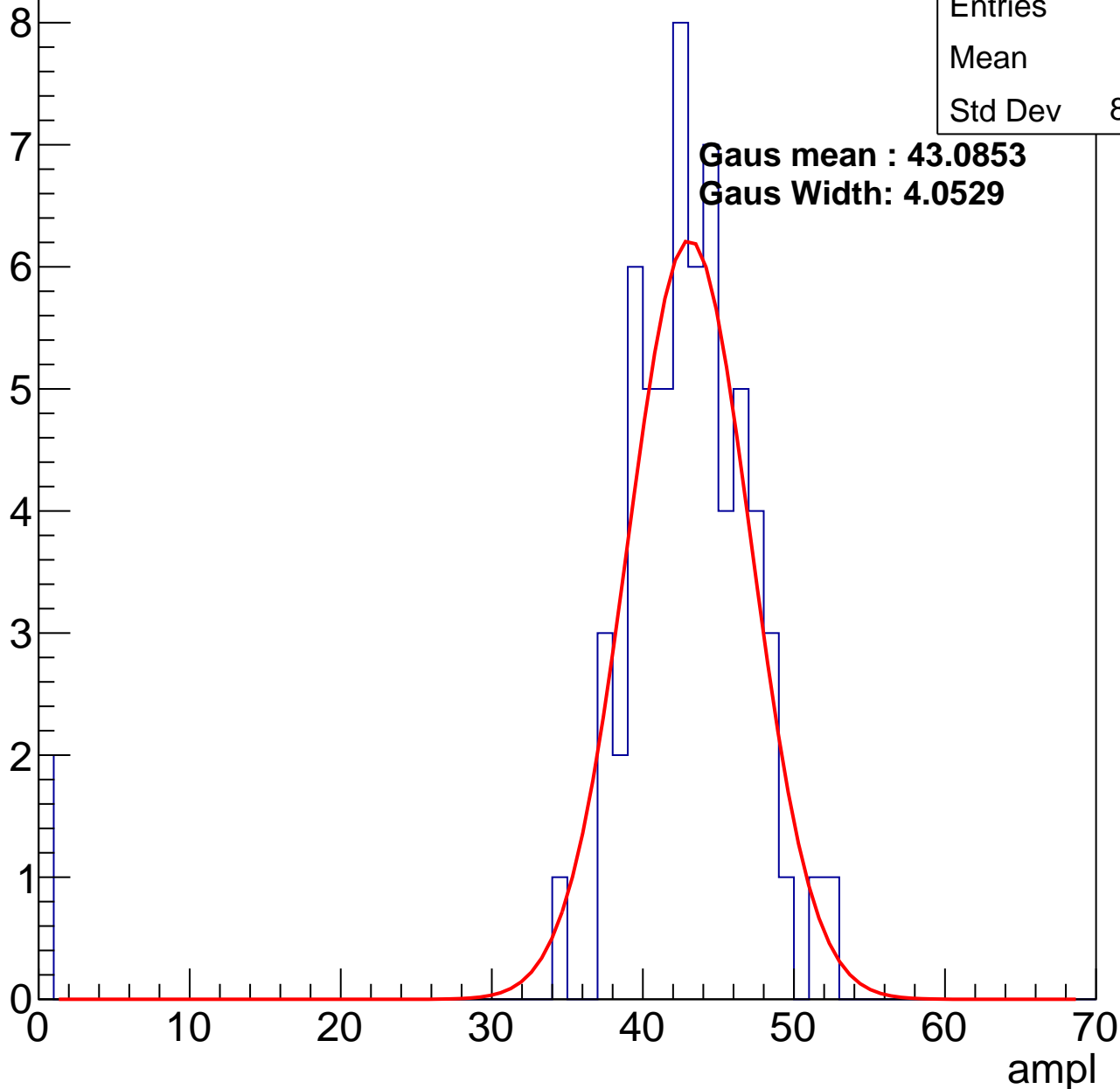
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	41.5
Std Dev	8.244

Gaus mean : 43.0853

Gaus Width: 4.0529

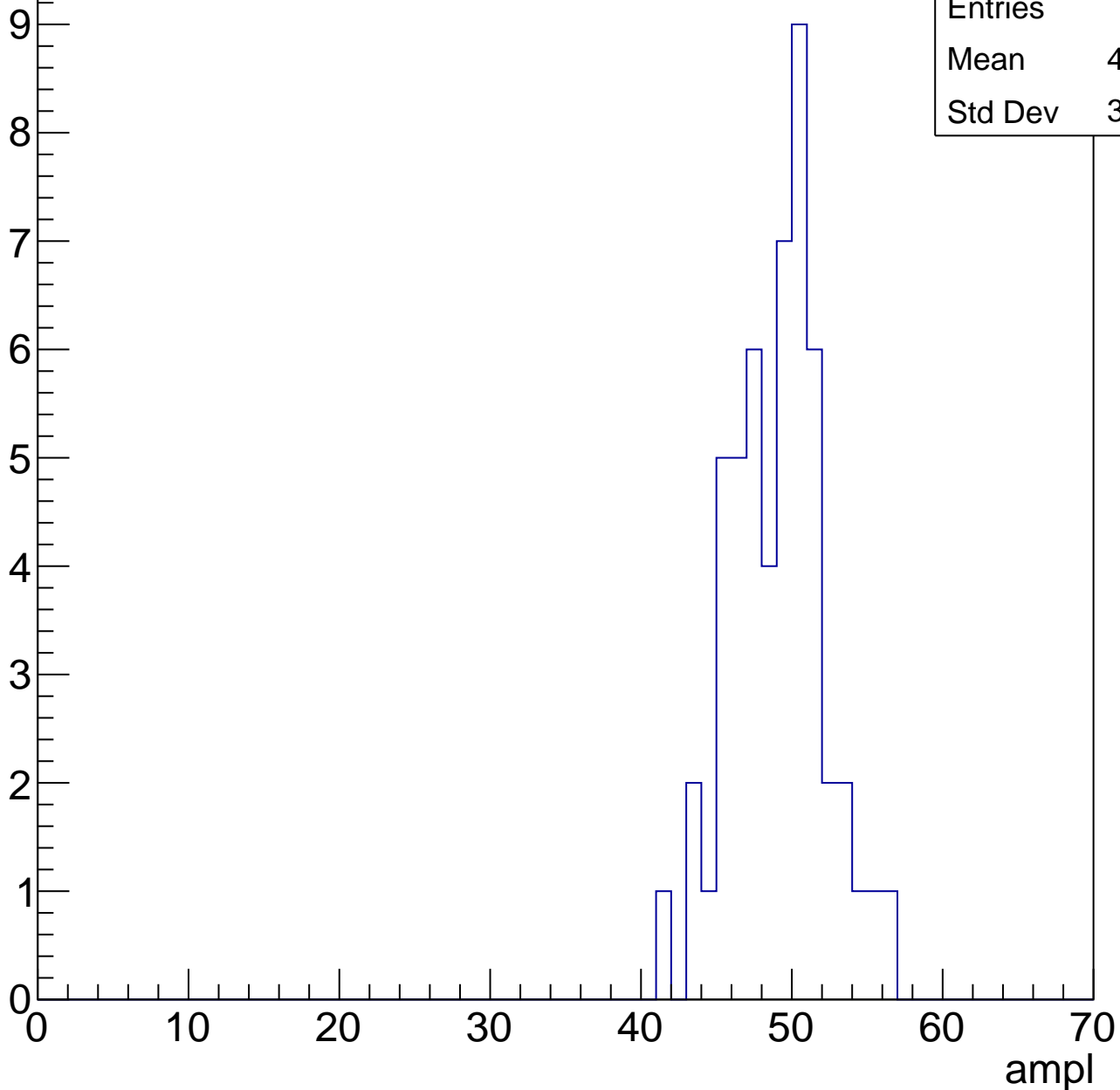


B1L103S, U13-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	48.57
Std Dev	3.062

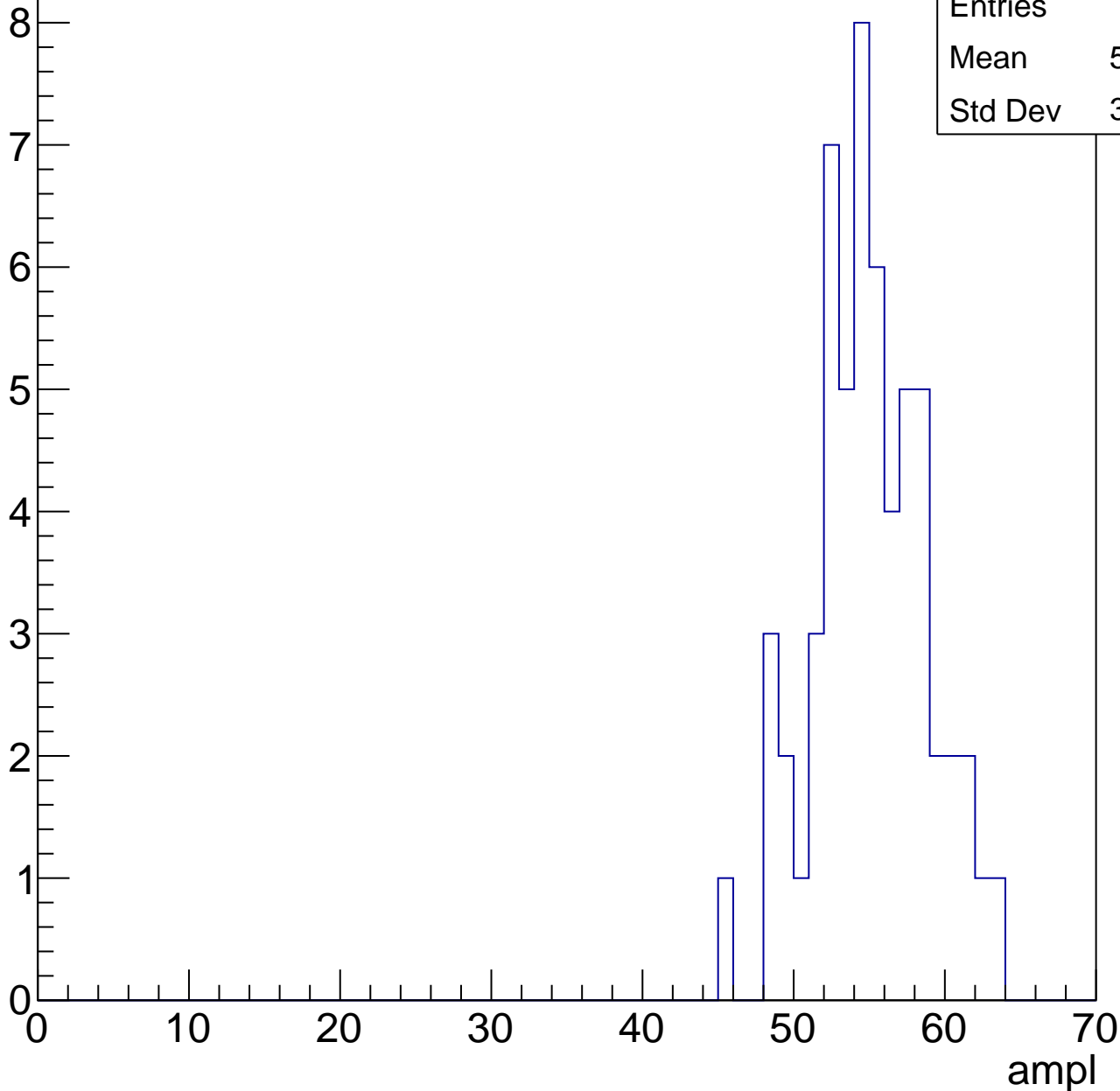


B1L103S, U13-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	54.57
Std Dev	3.733

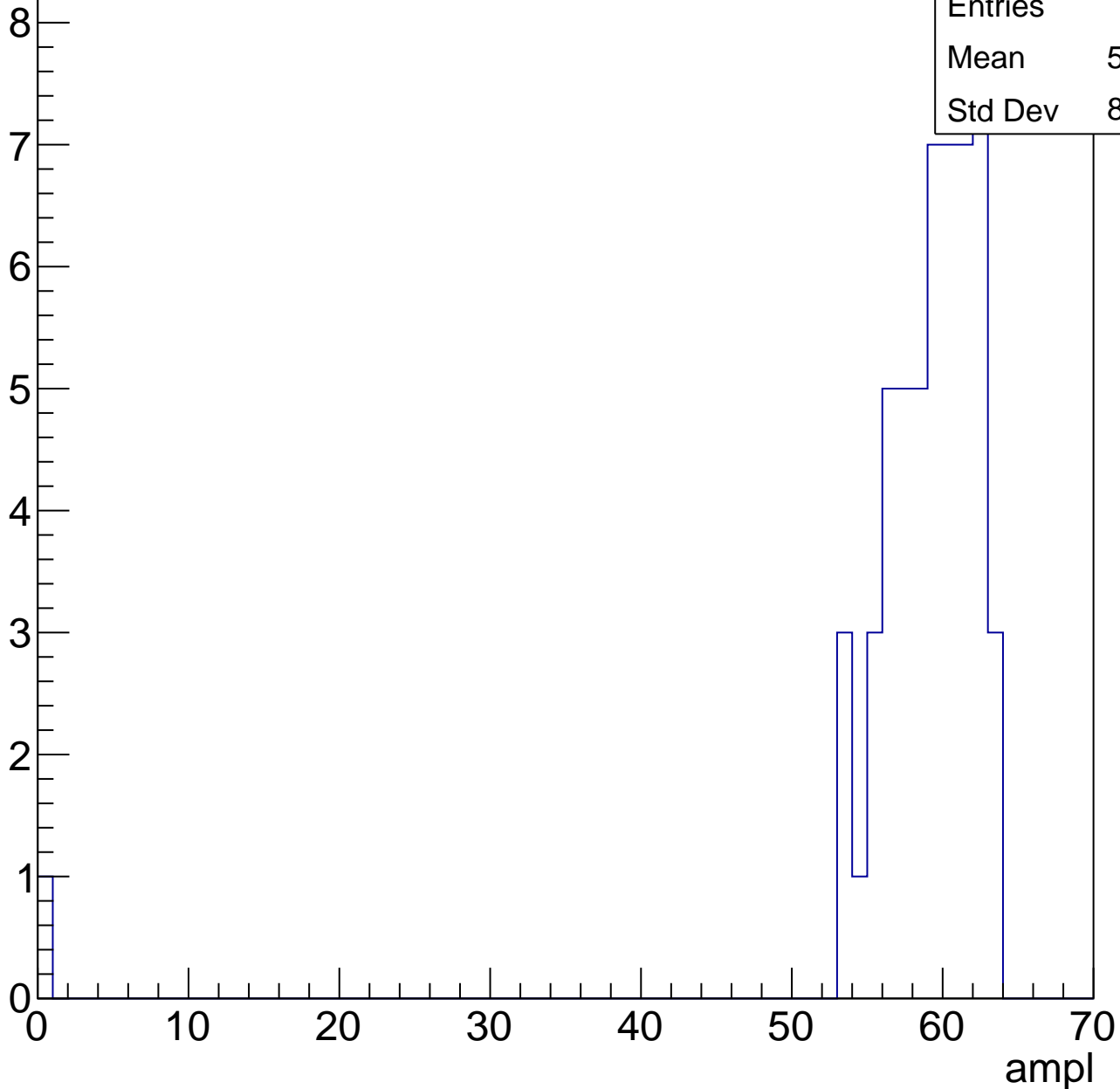


B1L103S, U13-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

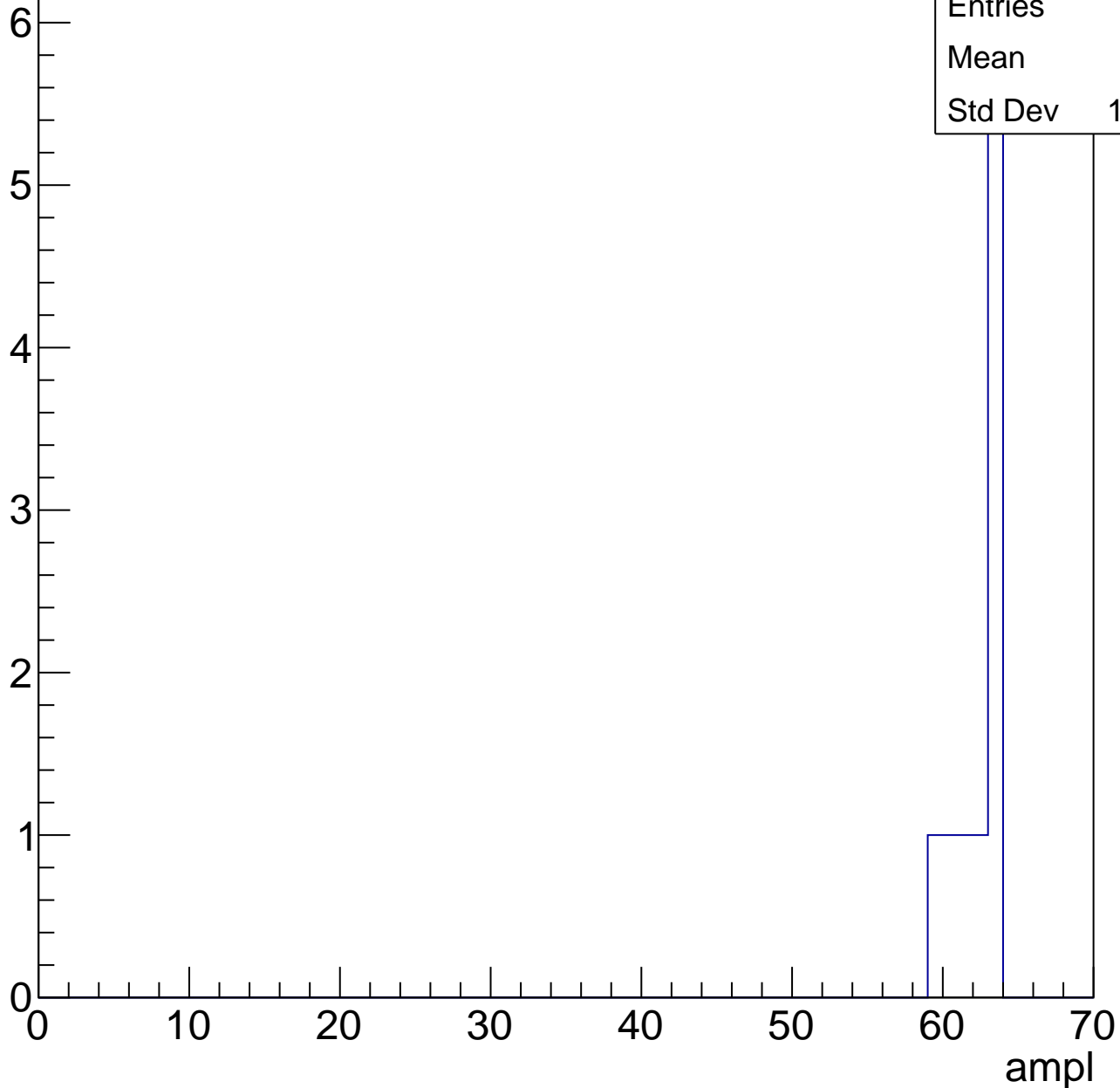
Entries	55
Mean	57.78
Std Dev	8.318



B1L103S, U13-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch51, adc0

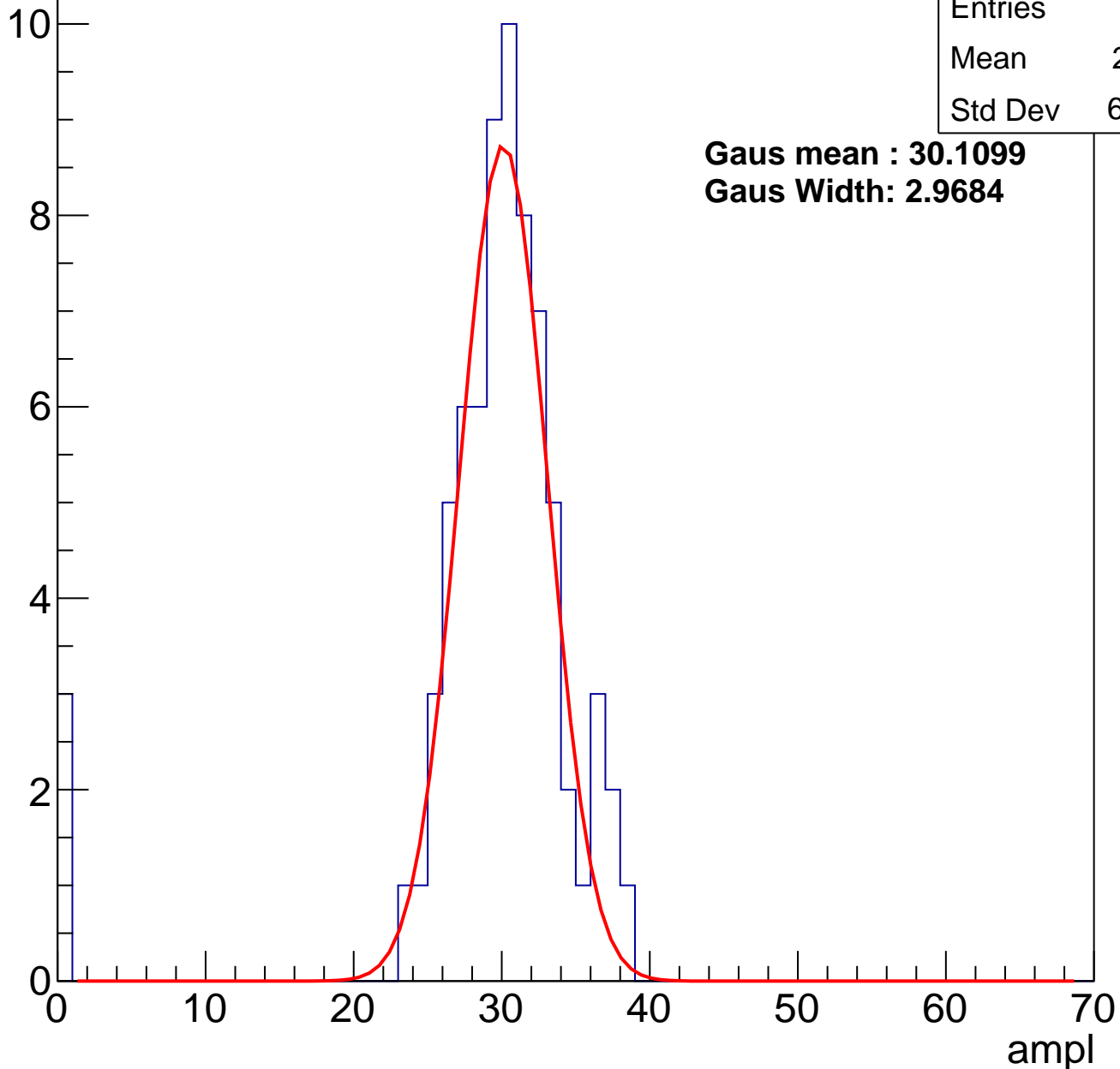
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	28.81
Std Dev	6.755

Gaus mean : 30.1099

Gaus Width: 2.9684

Entry



B1L103S, U13-ch51, adc1

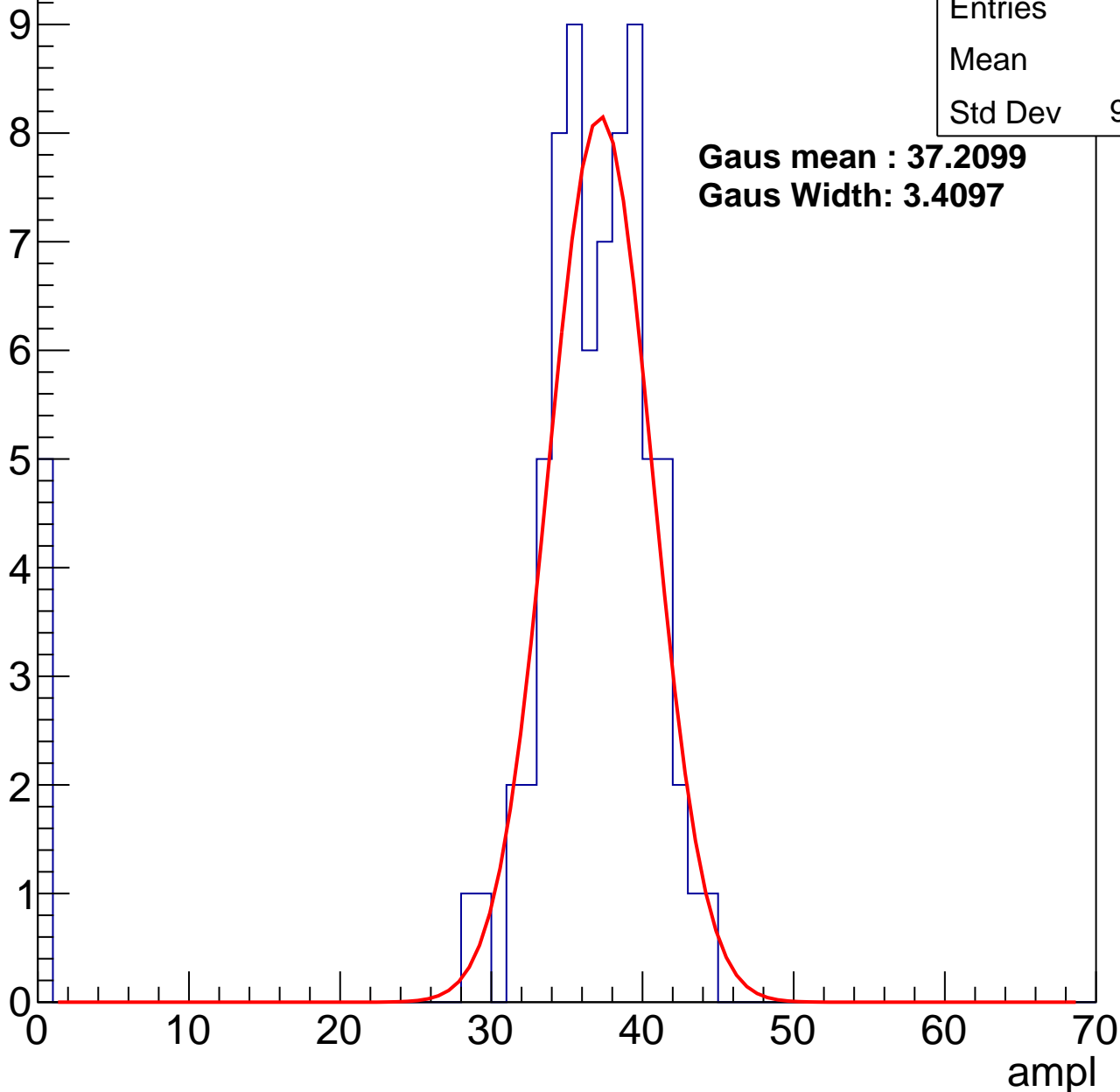
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	34.3
Std Dev	9.567

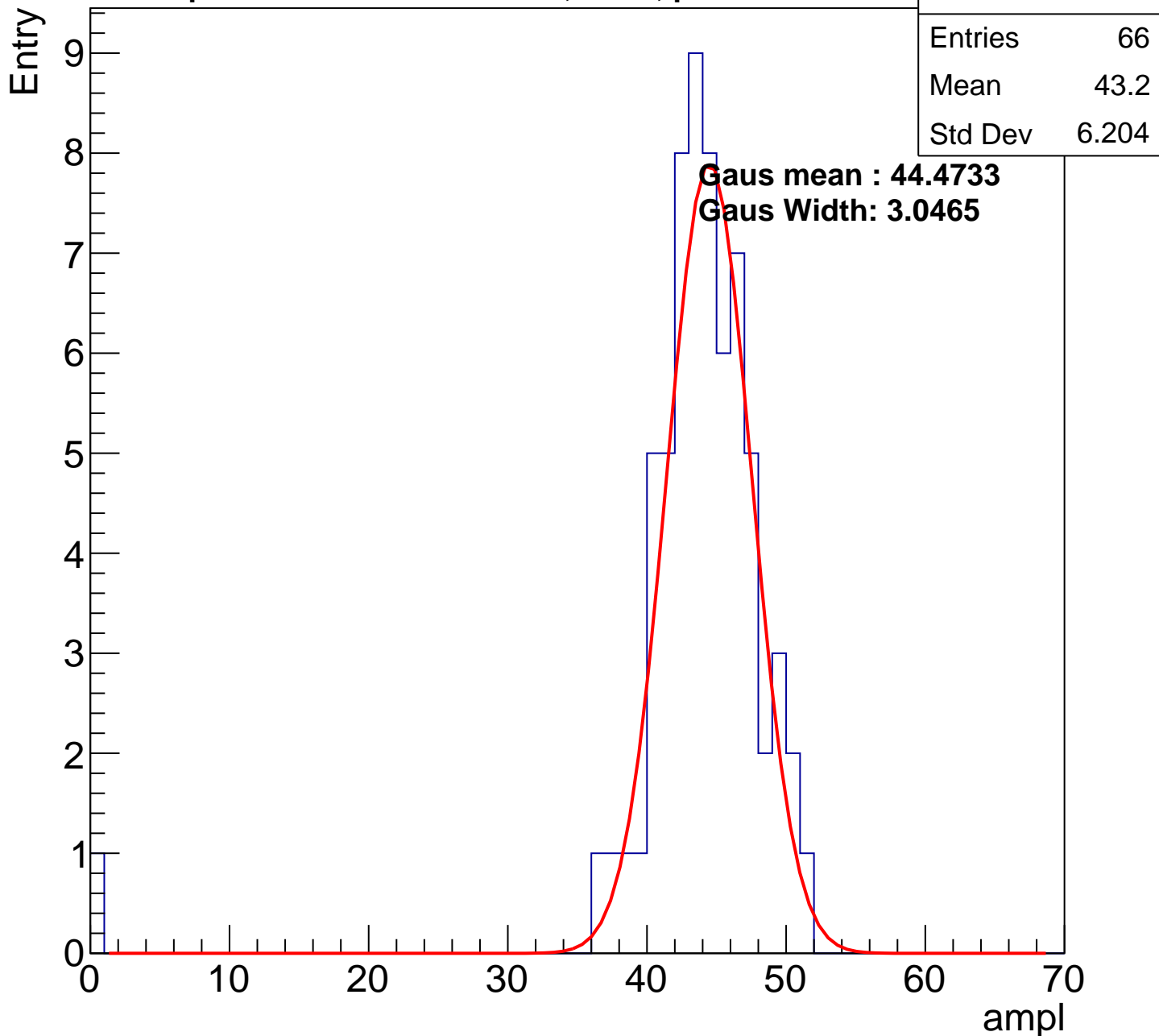
Gaus mean : 37.2099

Gaus Width: 3.4097



B1L103S, U13-ch51, adc2

calib_packv5_041523_1651.root, FC#0, port C2

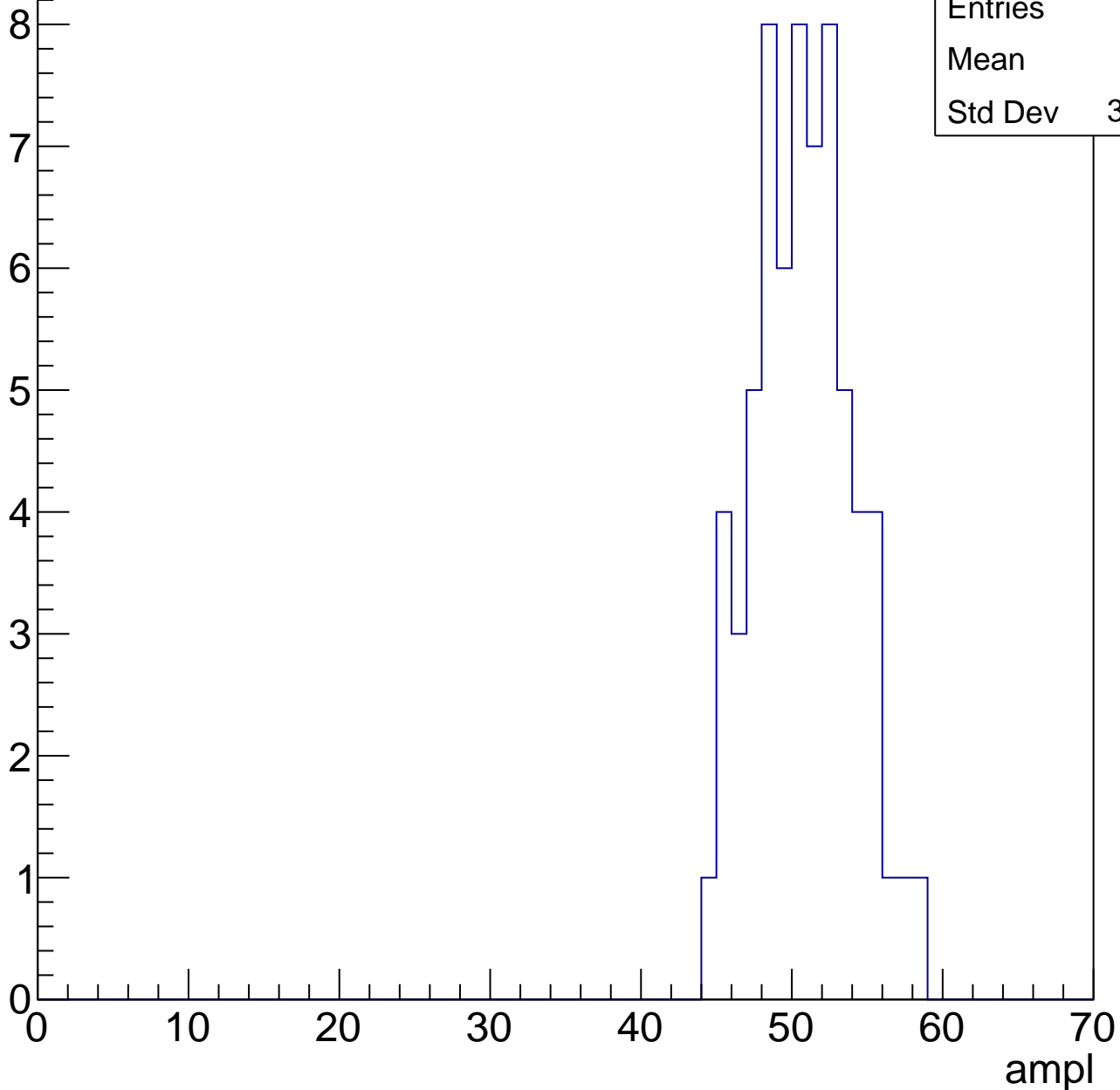


B1L103S, U13-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	50.3
Std Dev	3.157

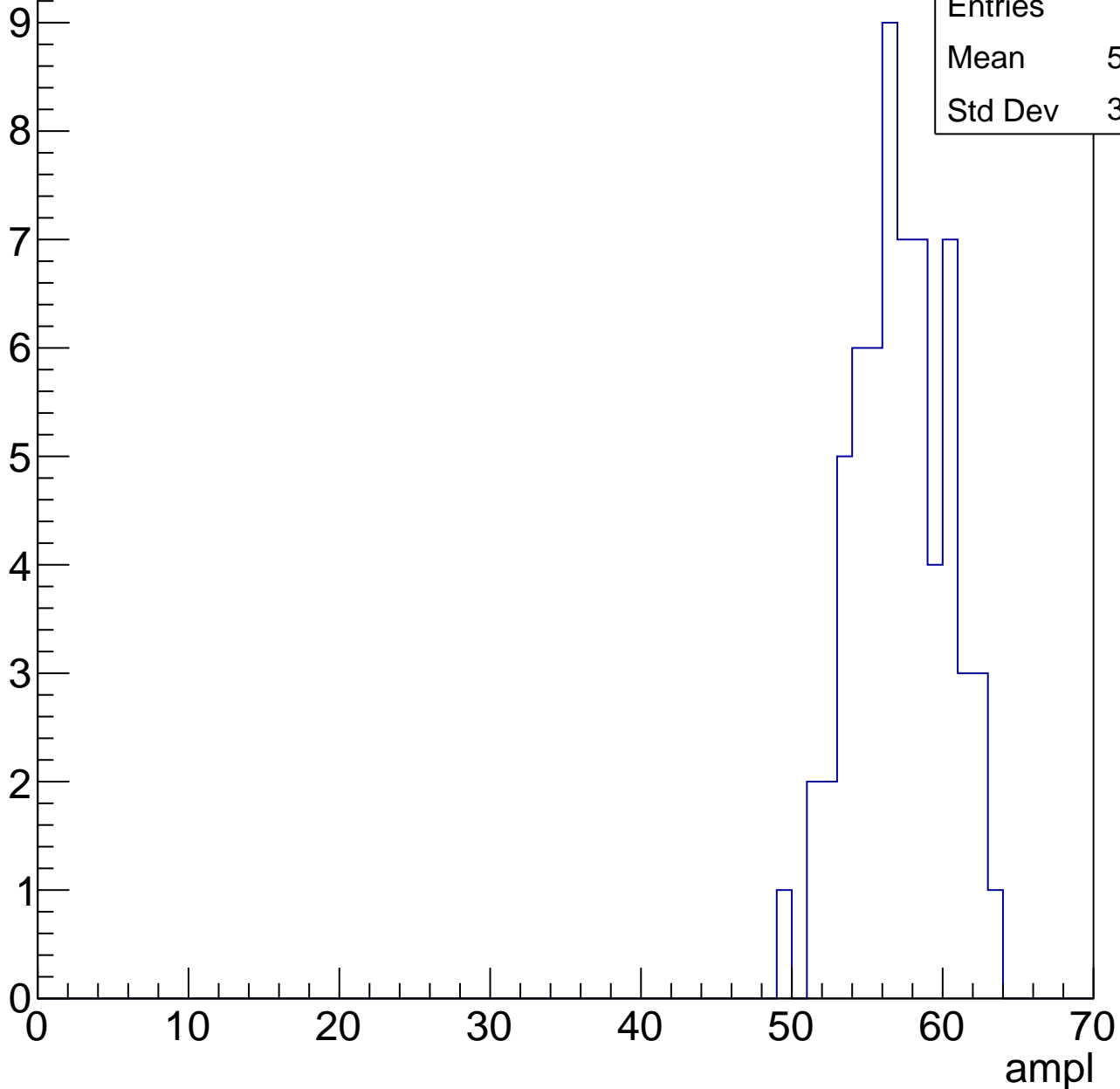


B1L103S, U13-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	56.68
Std Dev	3.065

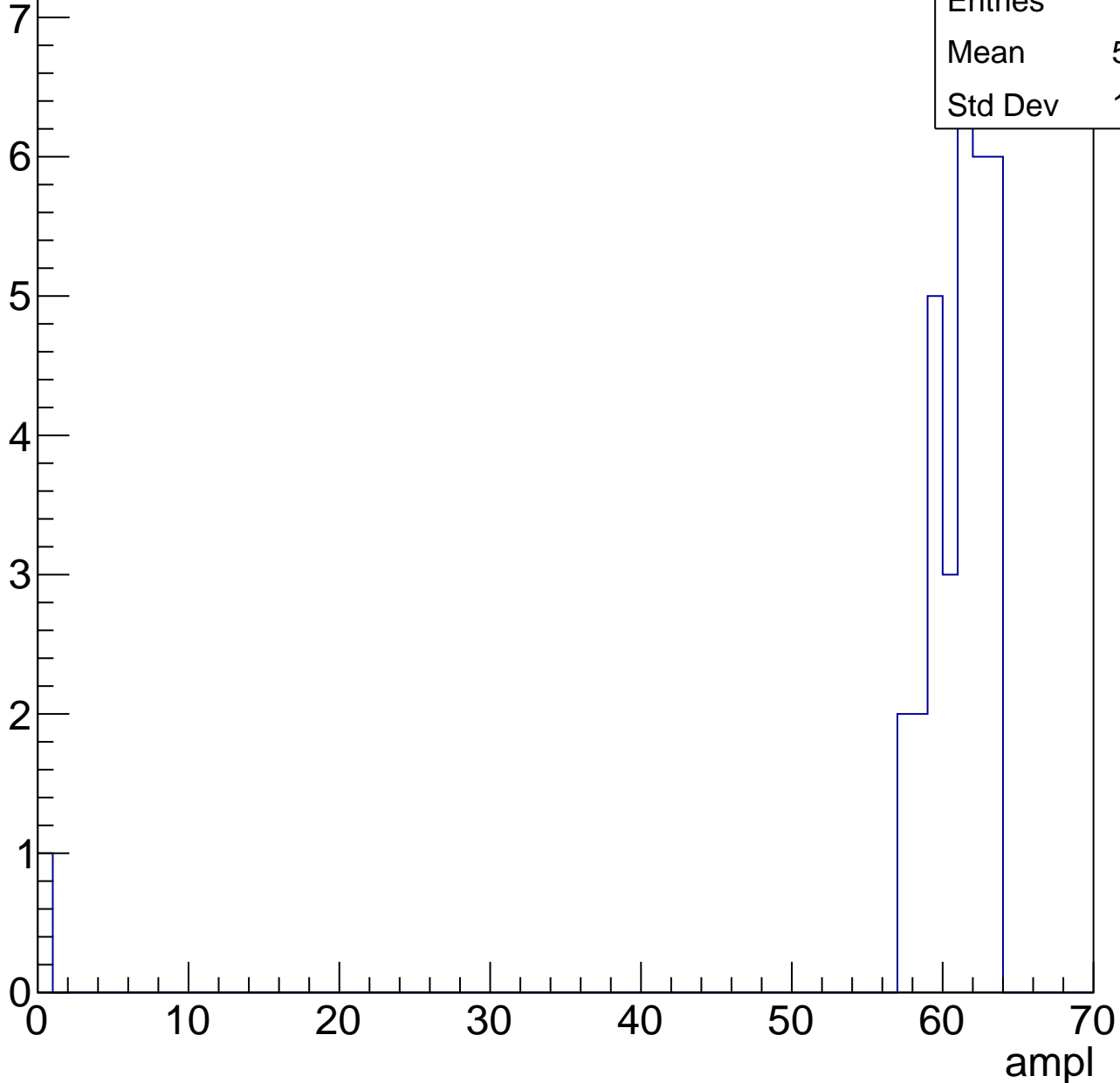


B1L103S, U13-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.81
Std Dev	10.71



B1L103S, U13-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch52, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	27.25
Std Dev	8.98

Gaus mean : 30.1731

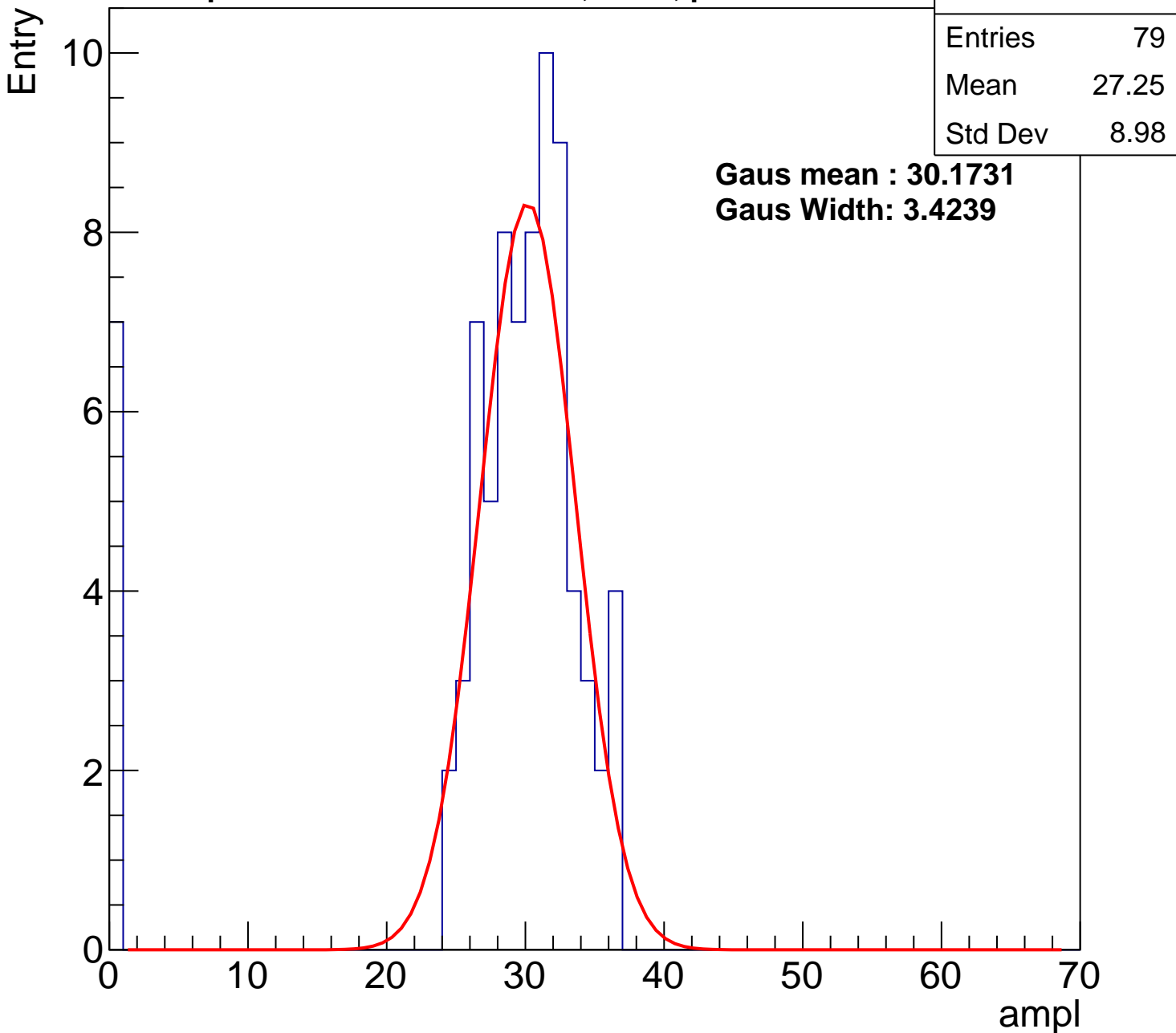
Gaus Width: 3.4239

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch52, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	34.68
Std Dev	8.972

Gaus mean : 37.4435

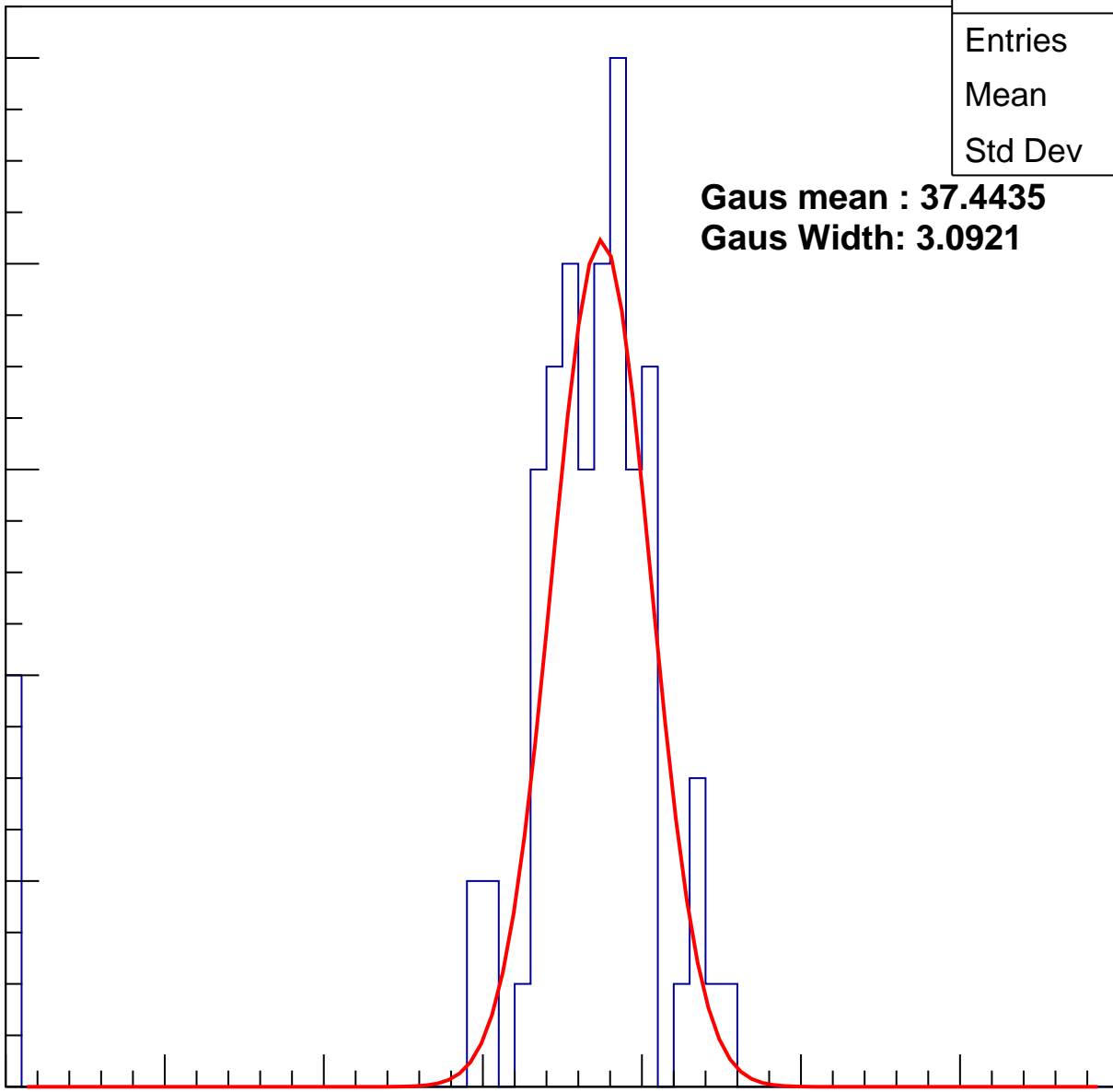
Gaus Width: 3.0921

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch52, adc2

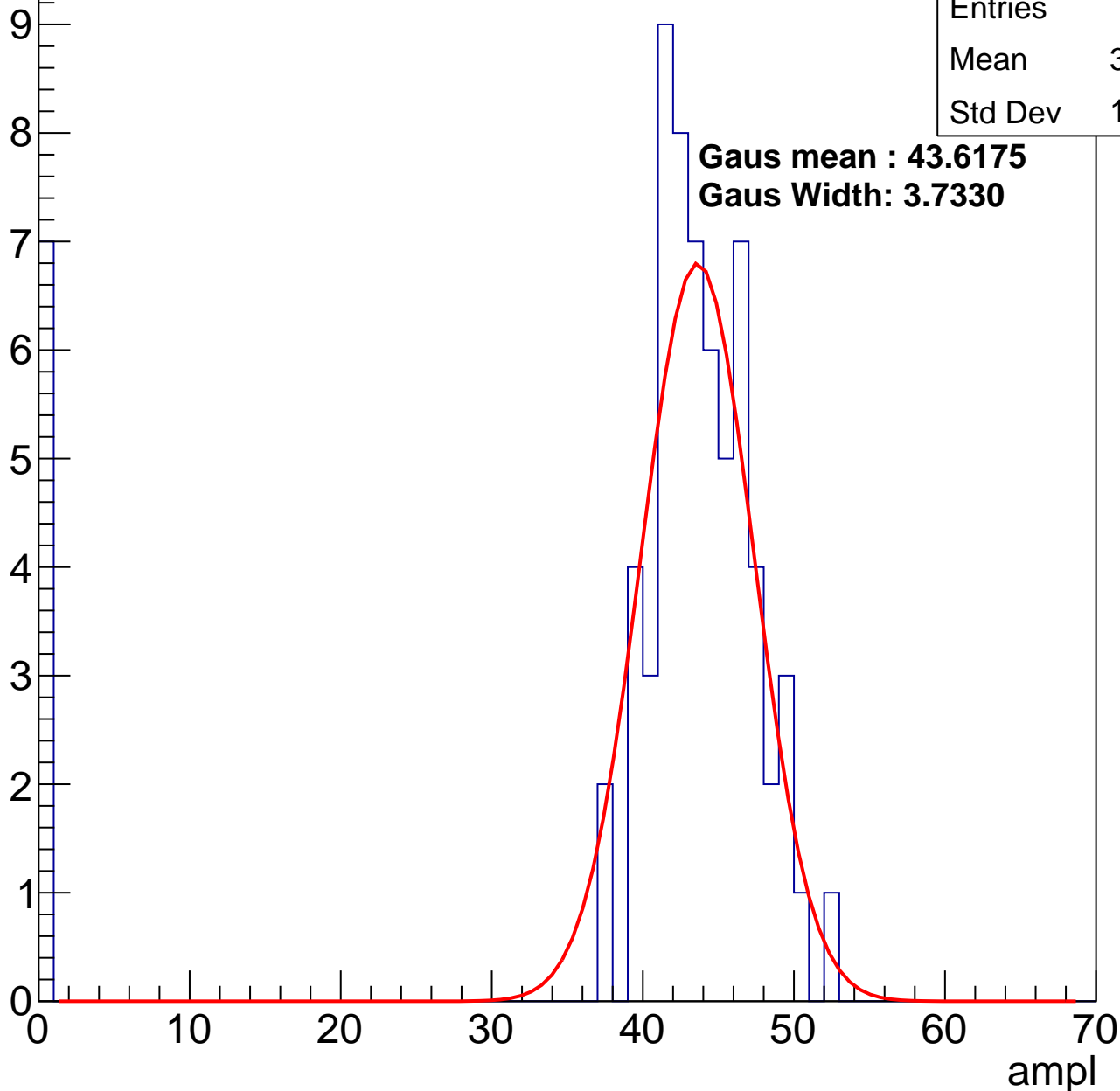
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	39.13
Std Dev	13.49

Gaus mean : 43.6175

Gaus Width: 3.7330

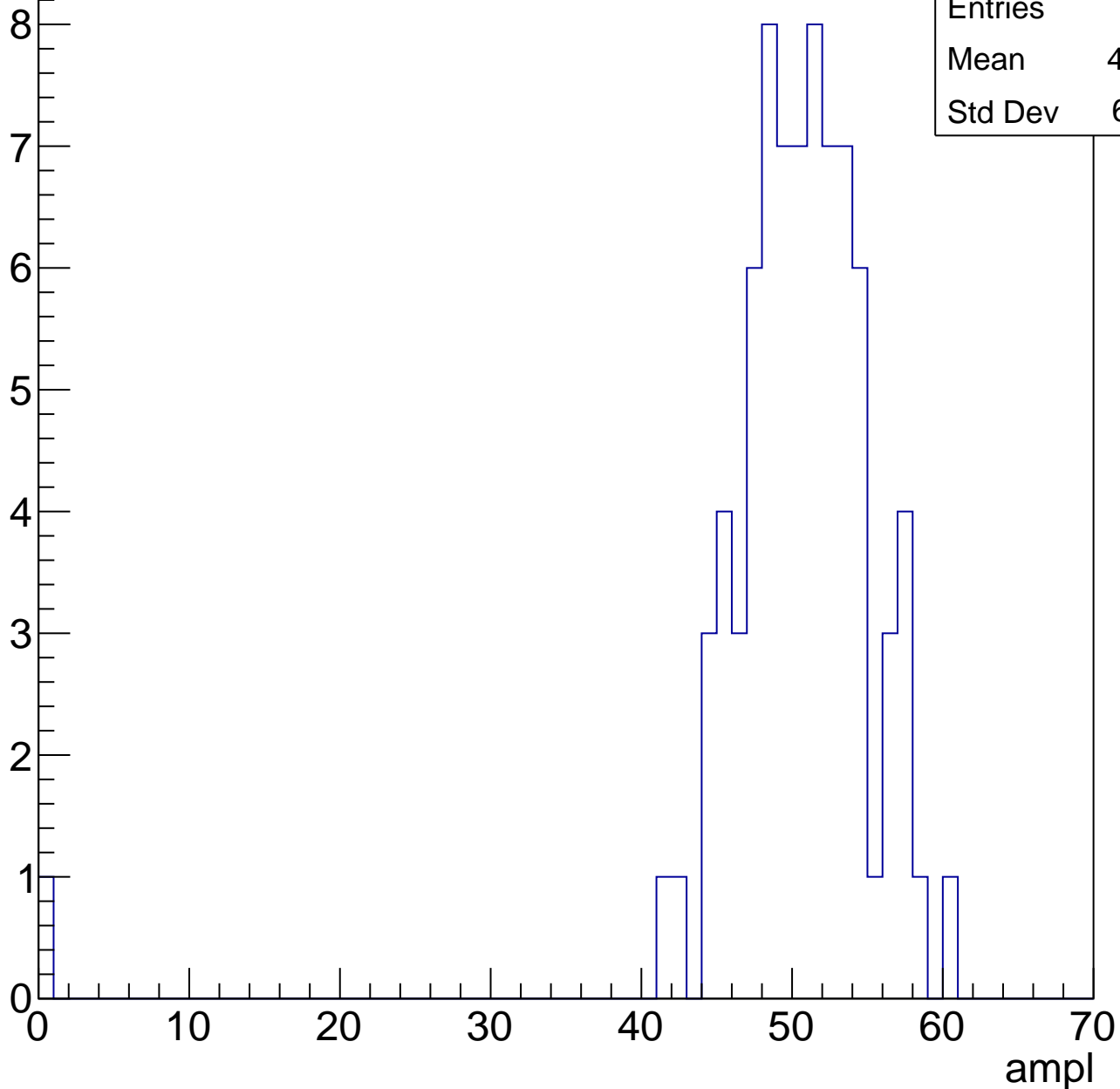


B1L103S, U13-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	49.72
Std Dev	6.821



B1L103S, U13-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	56.17
Std Dev	3.041

ampl

0

10

20

30

40

50

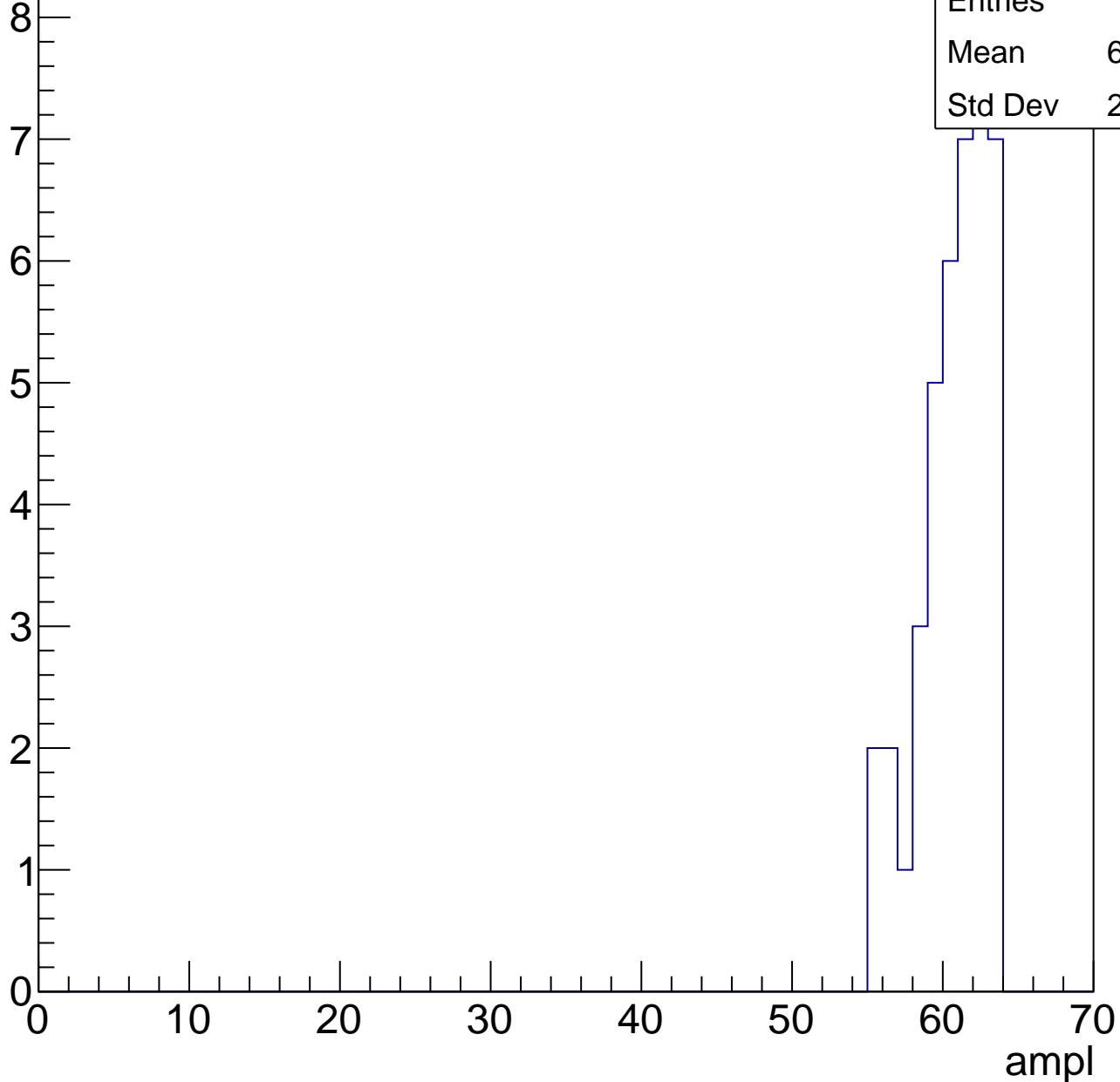
60

70

B1L103S, U13-ch52, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

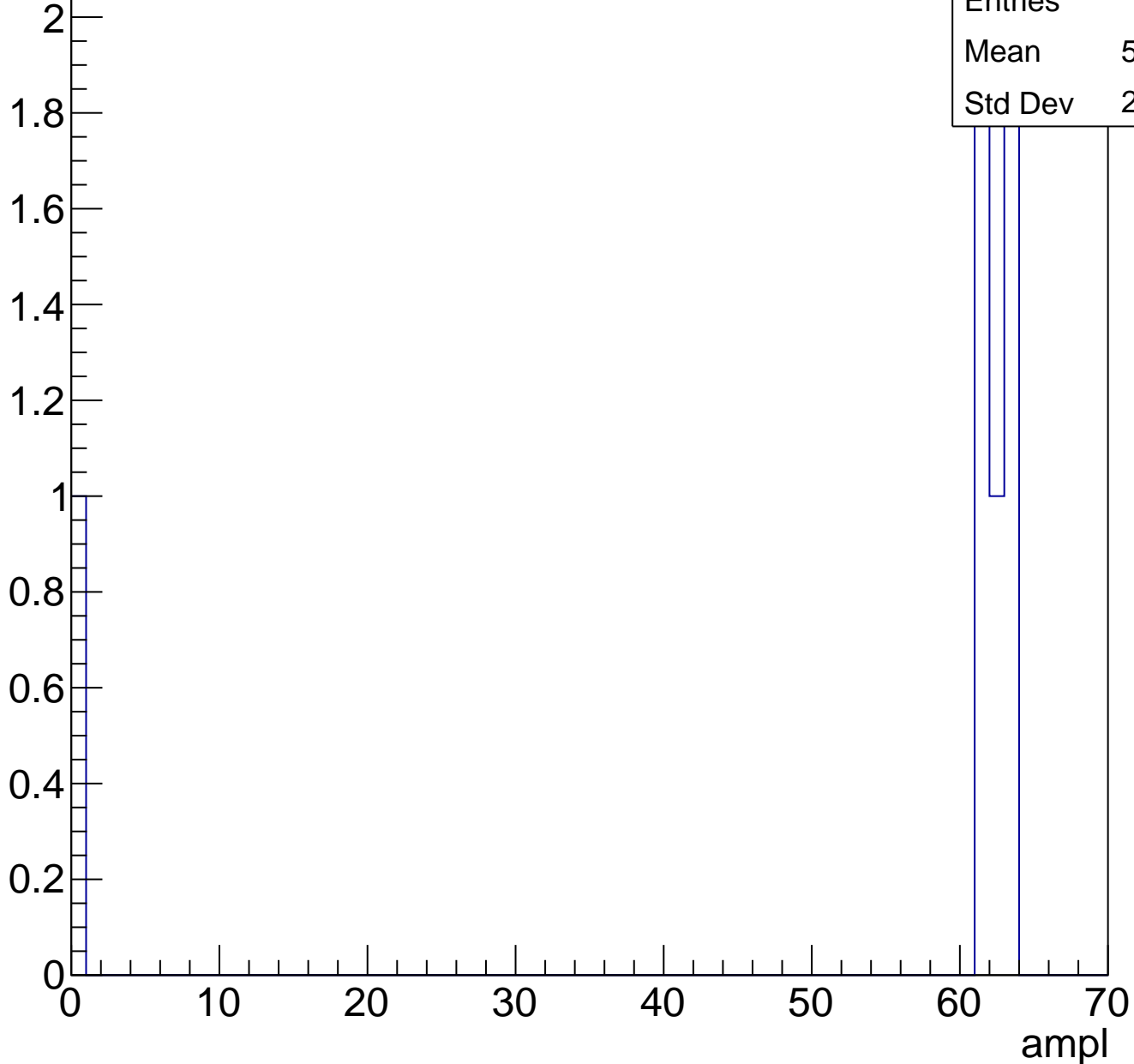


Entries	41
Mean	60.29
Std Dev	2.244

B1L103S, U13-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

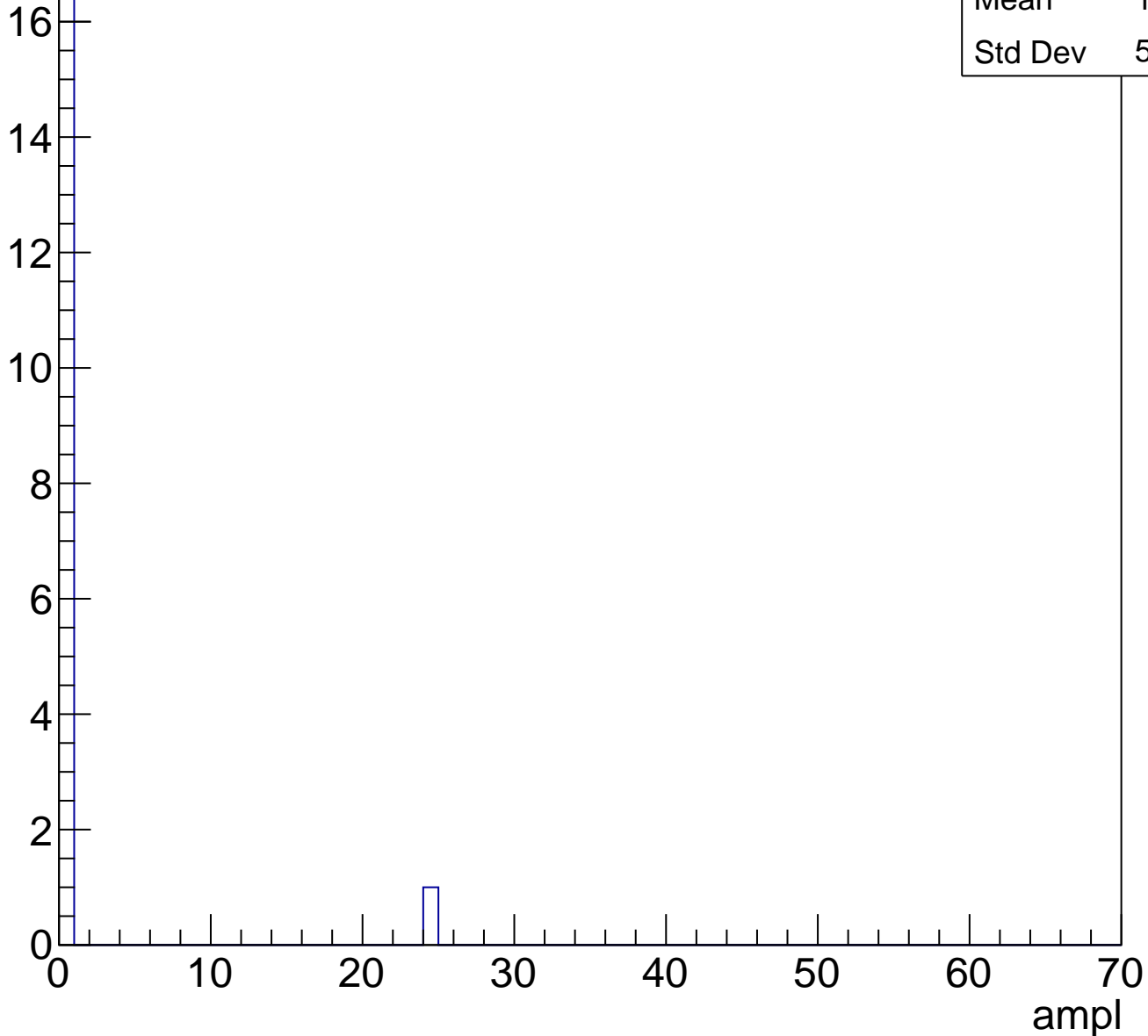


B1L103S, U13-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.333
Std Dev	5.497

Entry



B1L103S, U13-ch53, adc0

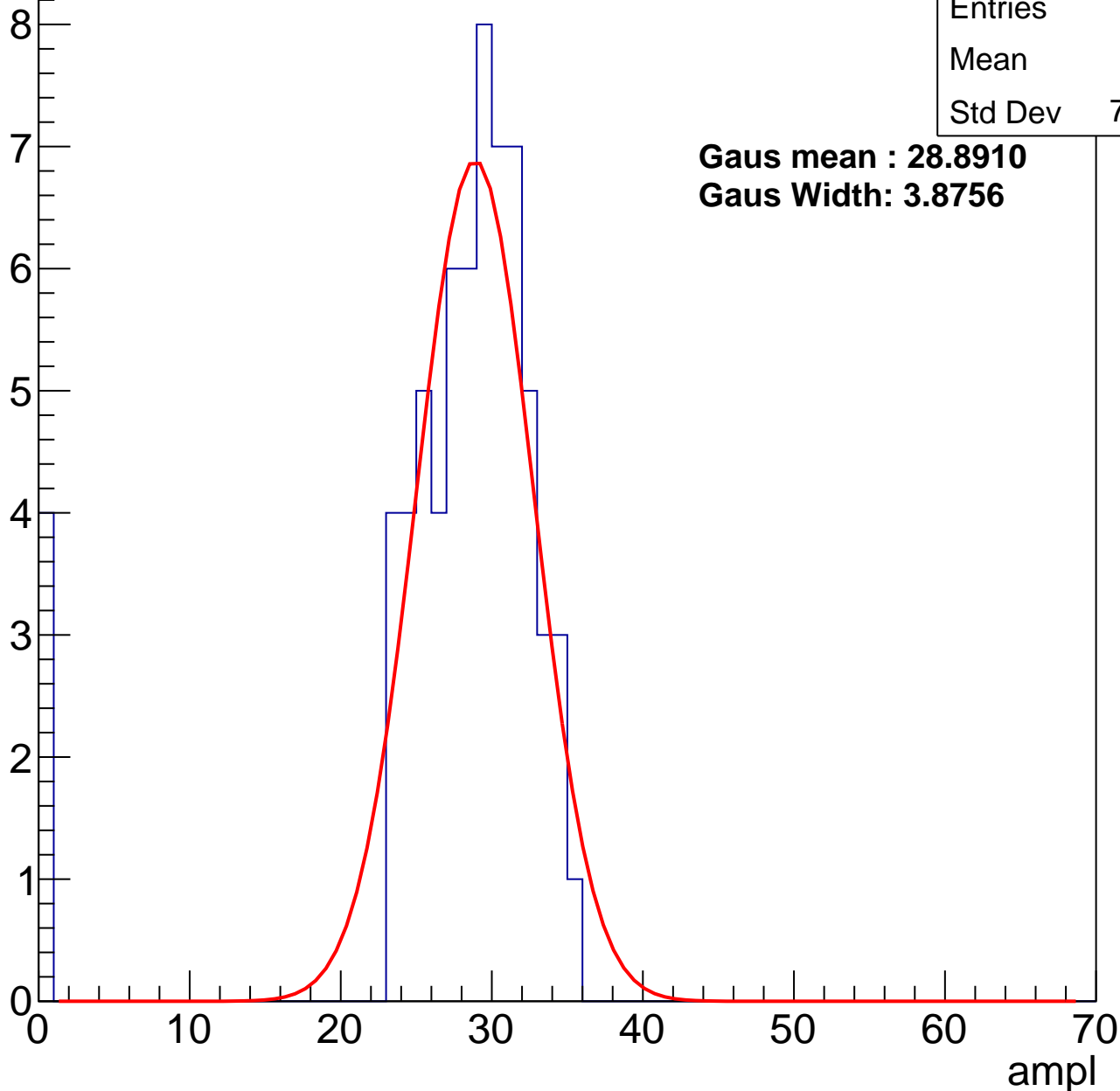
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	26.9
Std Dev	7.428

Gaus mean : 28.8910

Gaus Width: 3.8756

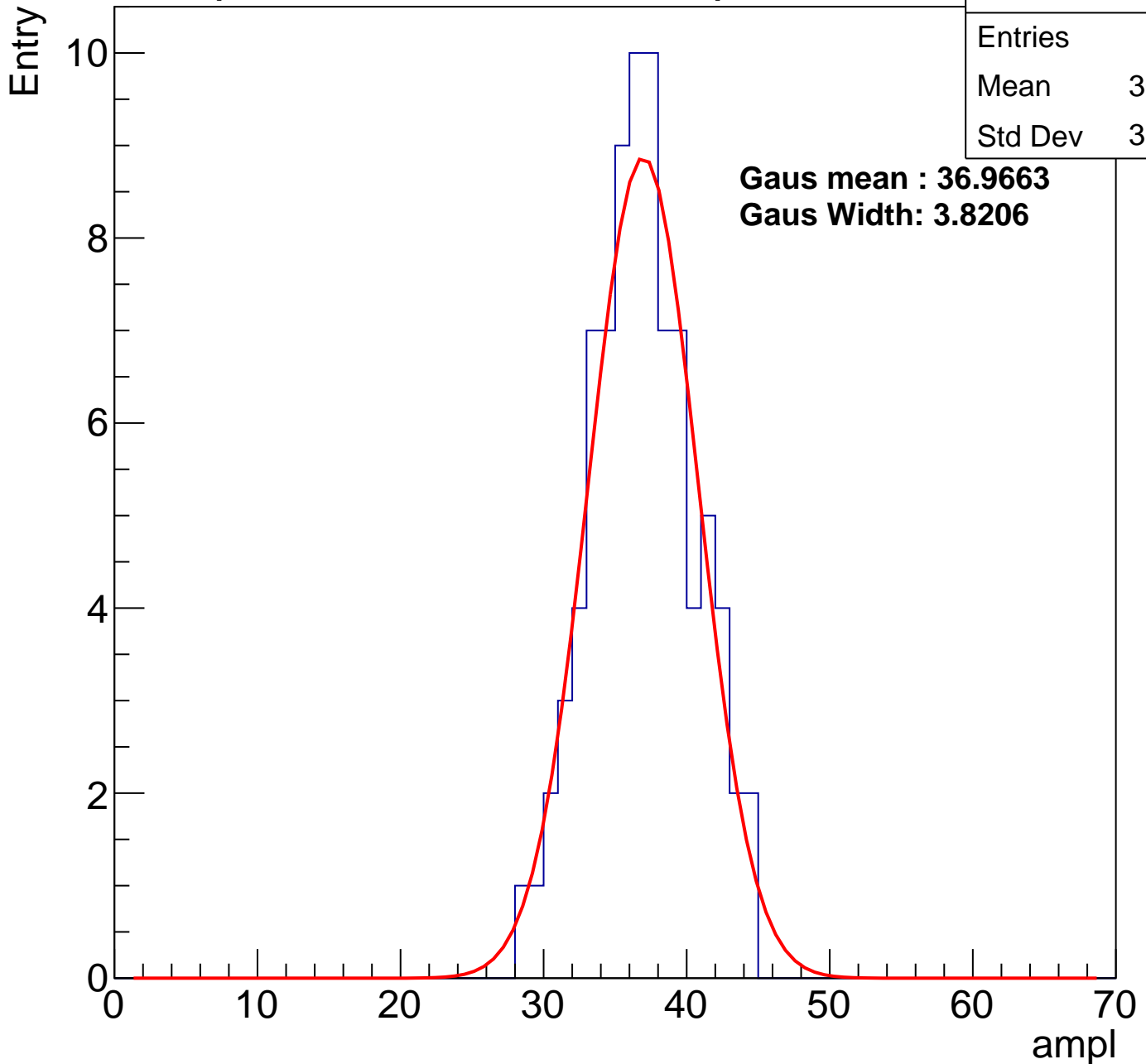


B1L103S, U13-ch53, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	36.45
Std Dev	3.543

Gaus mean : 36.9663
Gaus Width: 3.8206



B1L103S, U13-ch53, adc2

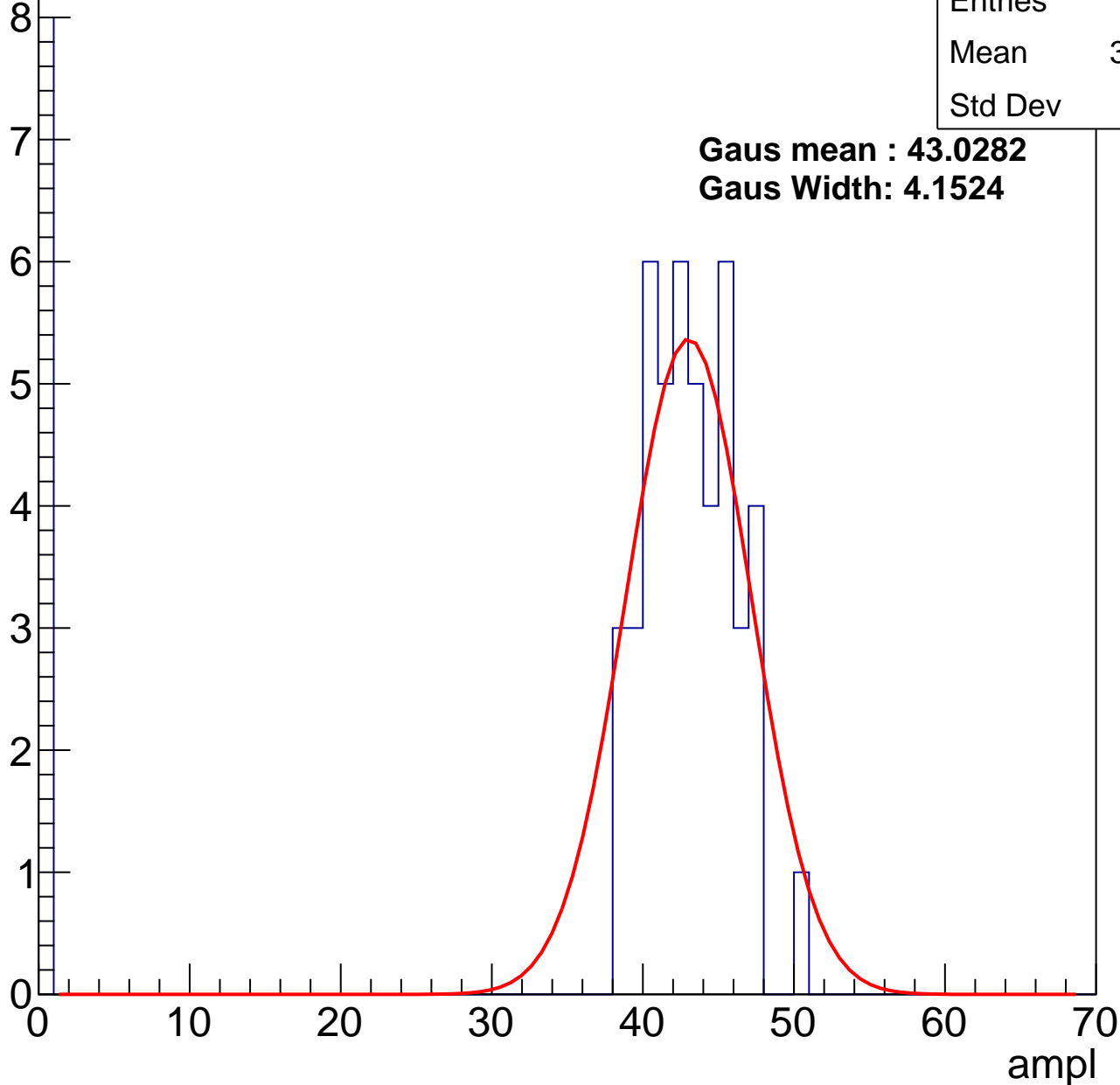
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	36.39
Std Dev	15.4

Gaus mean : 43.0282

Gaus Width: 4.1524



B1L103S, U13-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	49.46
Std Dev	3.468

Entry

10

8

6

4

2

0

0

10

20

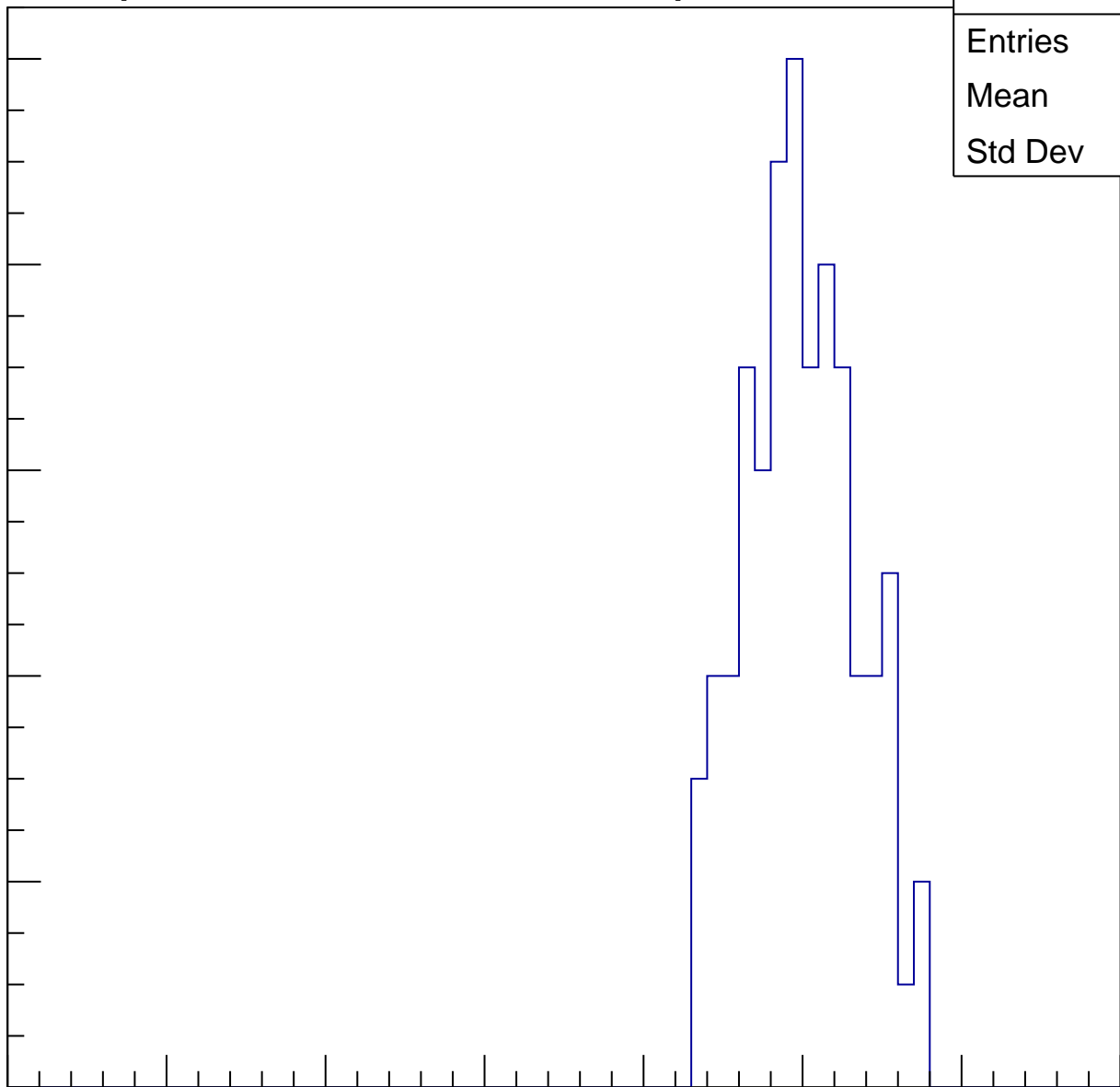
30

40

50

60

ampl

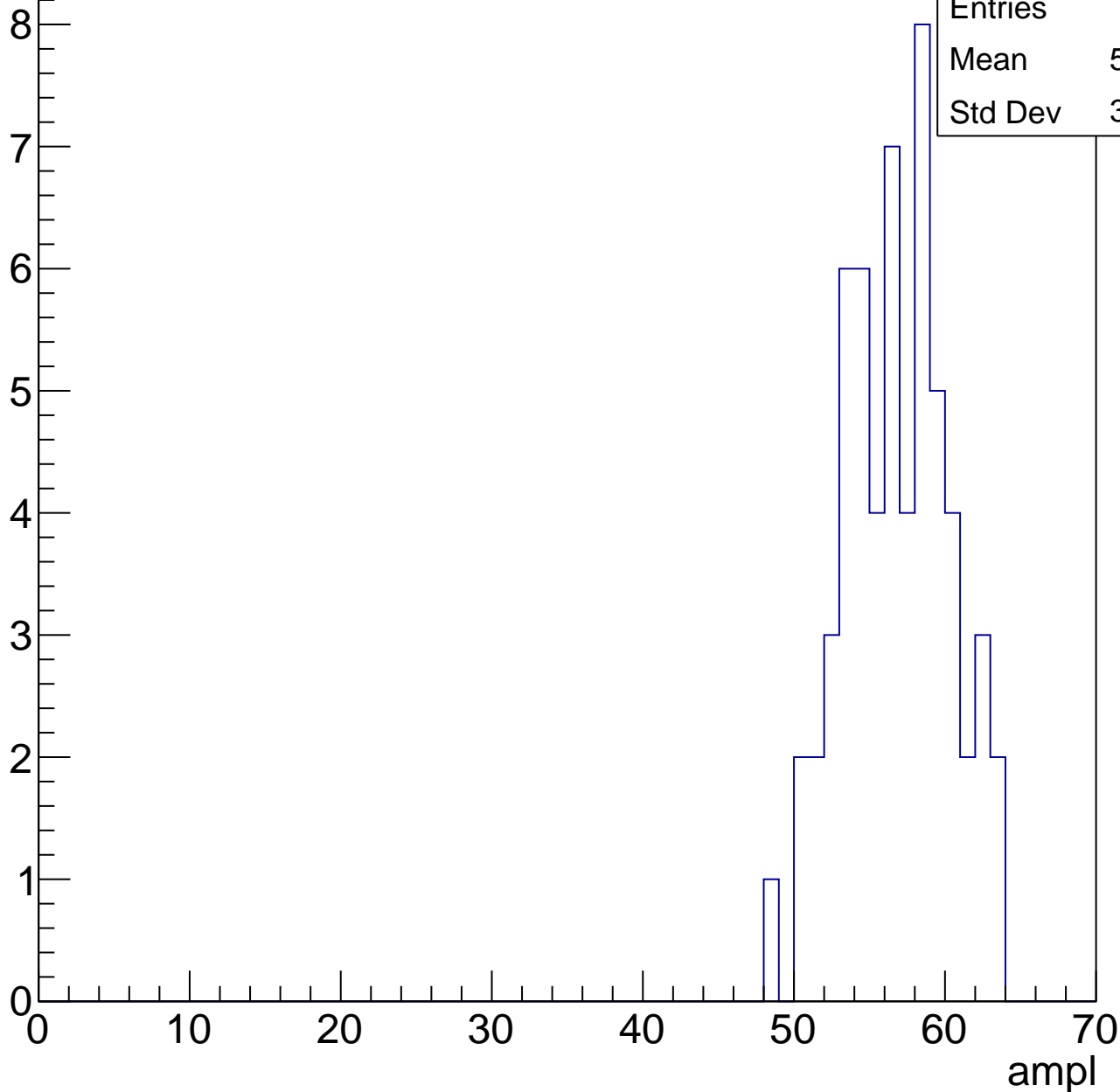


B1L103S, U13-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	56.29
Std Dev	3.474

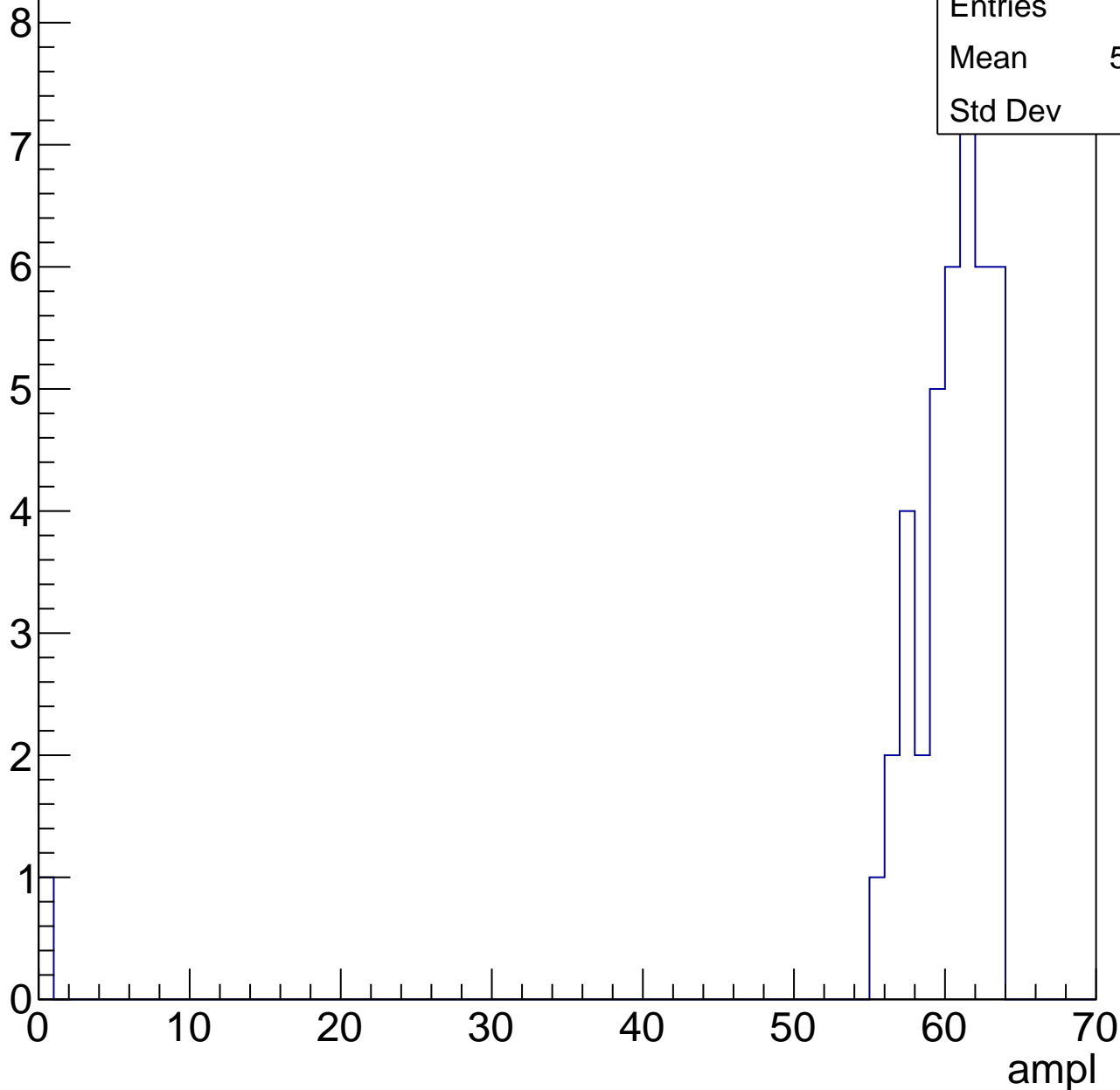


B1L103S, U13-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	58.63
Std Dev	9.52



B1L103S, U13-ch53, adc6

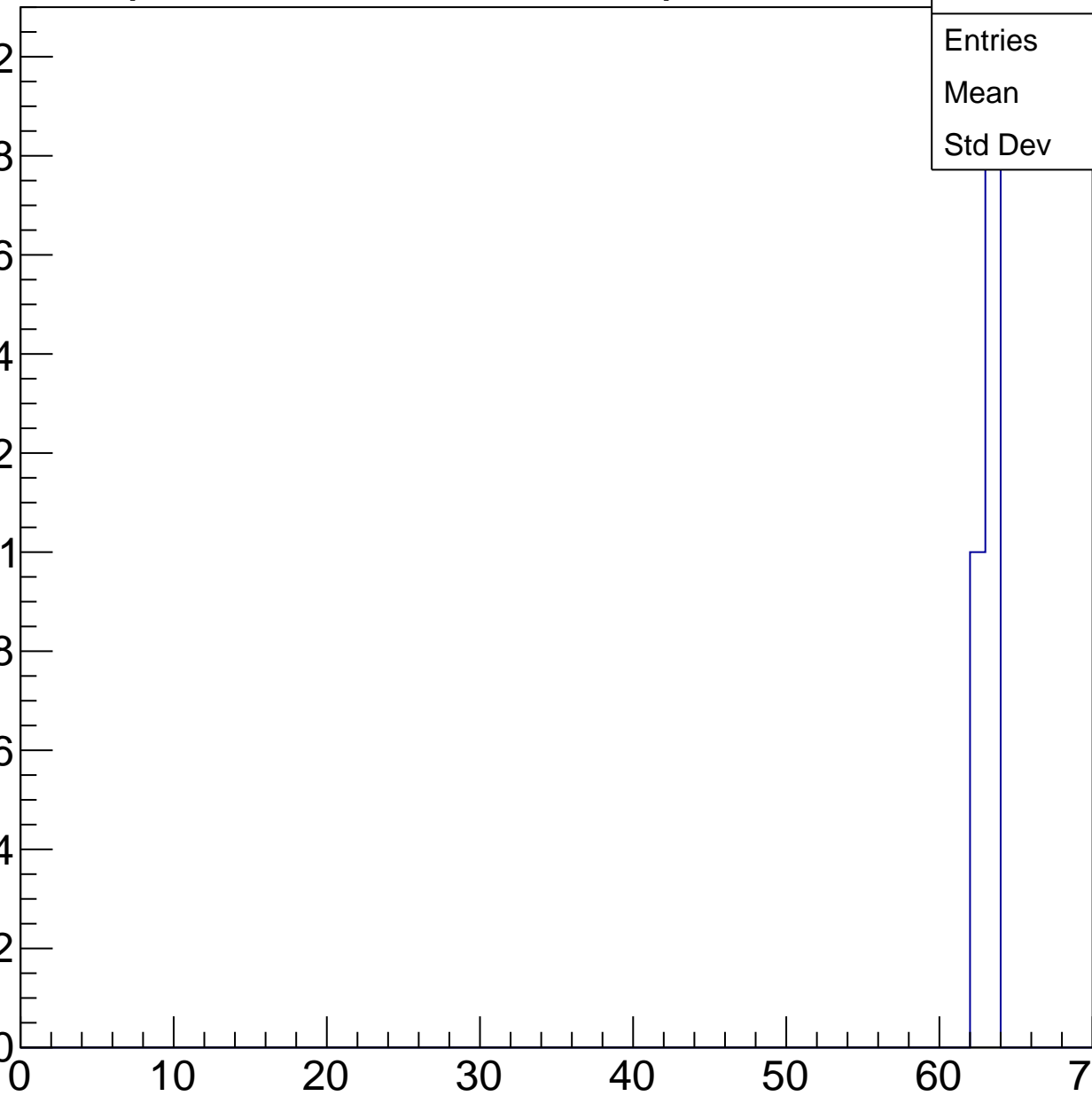
calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L103S, U13-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch54, adc0

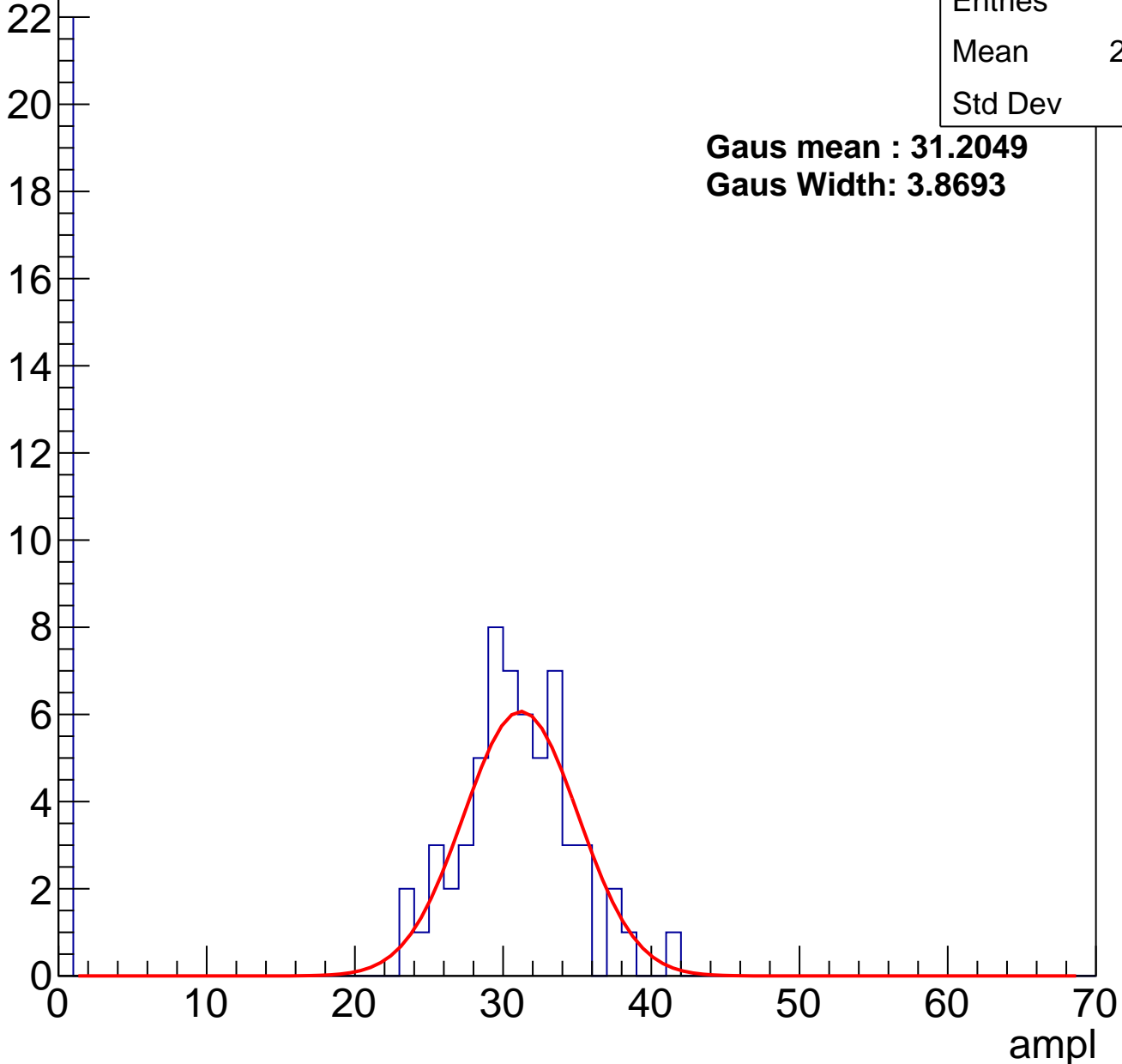
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	22.19
Std Dev	13.9

Gaus mean : 31.2049

Gaus Width: 3.8693

Entry



B1L103S, U13-ch54, adc1

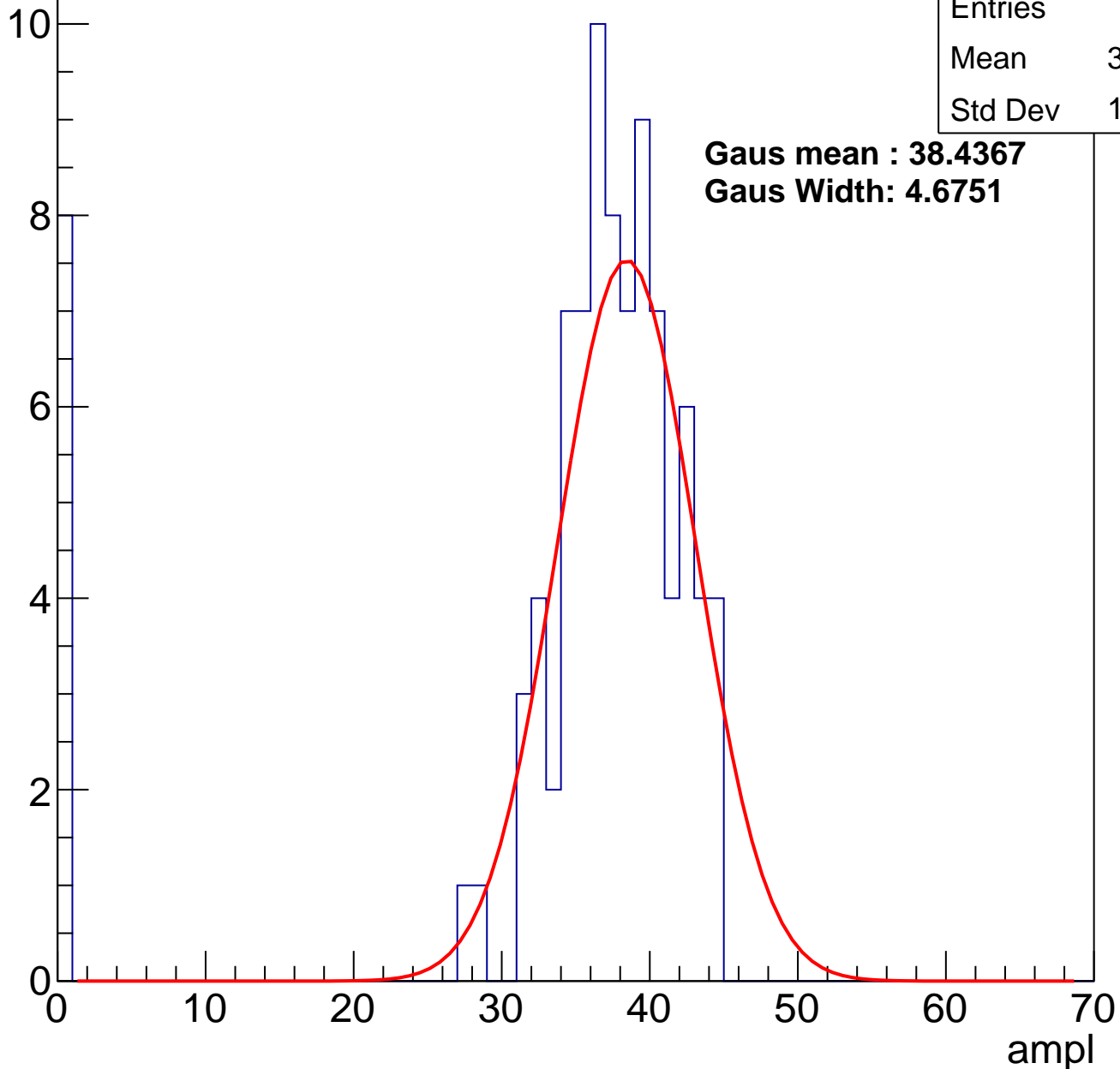
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	34.15
Std Dev	11.13

Gaus mean : 38.4367

Gaus Width: 4.6751

Entry



B1L103S, U13-ch54, adc2

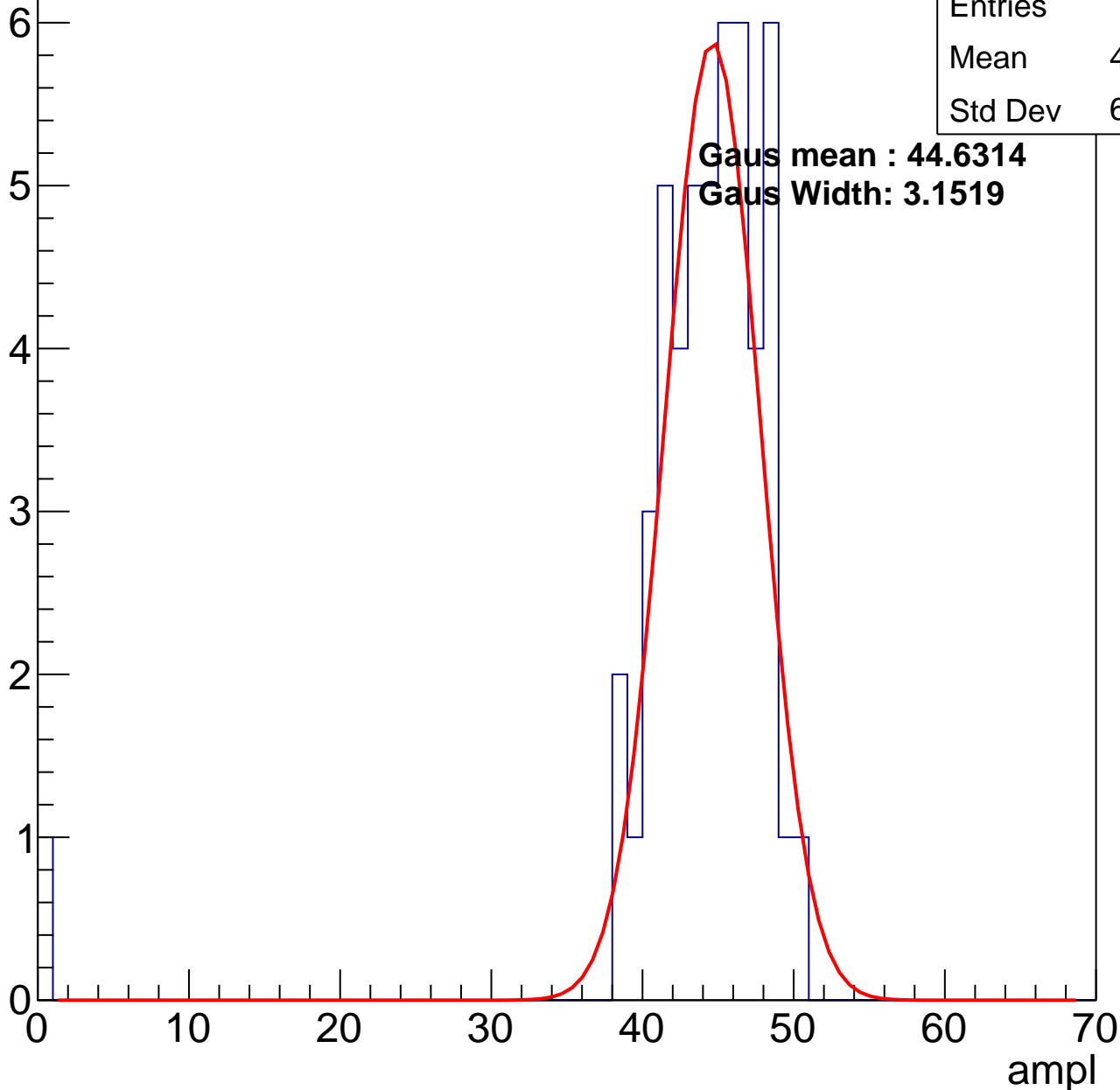
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	43.28
Std Dev	6.847

Gaus mean : 44.6314

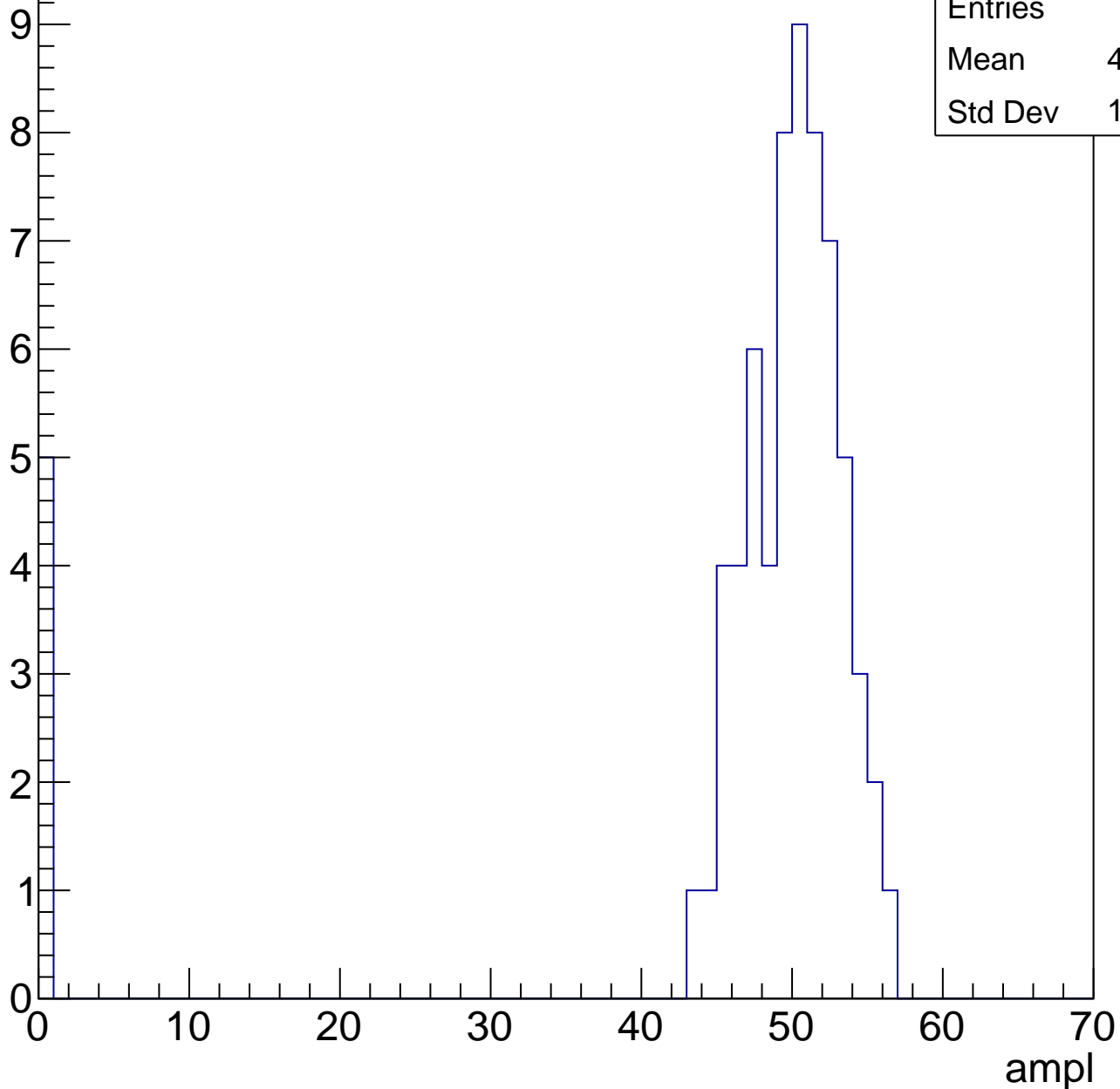
Gaus Width: 3.1519



B1L103S, U13-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

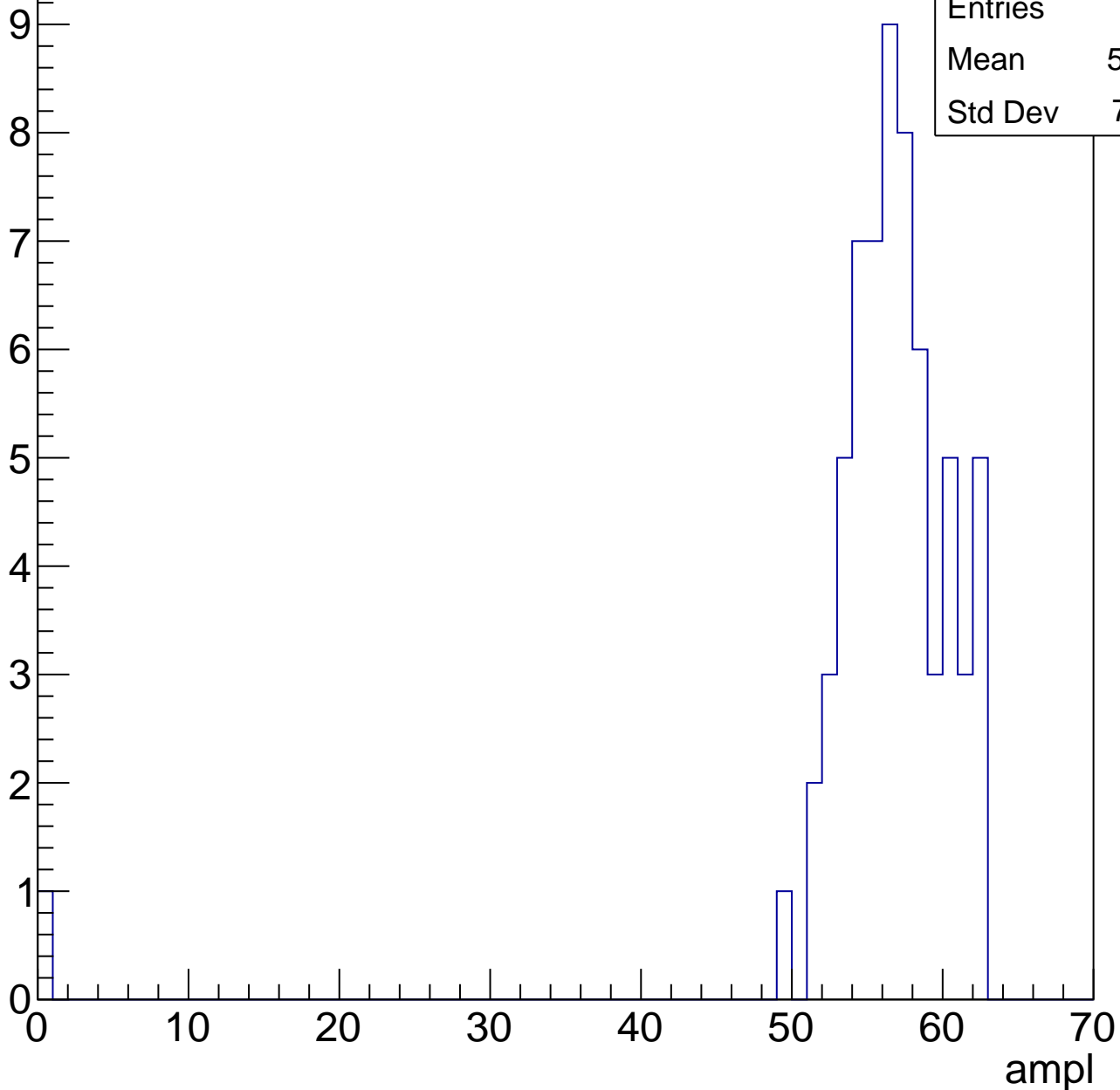


B1L103S, U13-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	55.58
Std Dev	7.591



B1L103S, U13-ch54, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	42
Mean	59.07
Std Dev	9.445

Entry

10

8

6

4

2

0

0

10

20

30

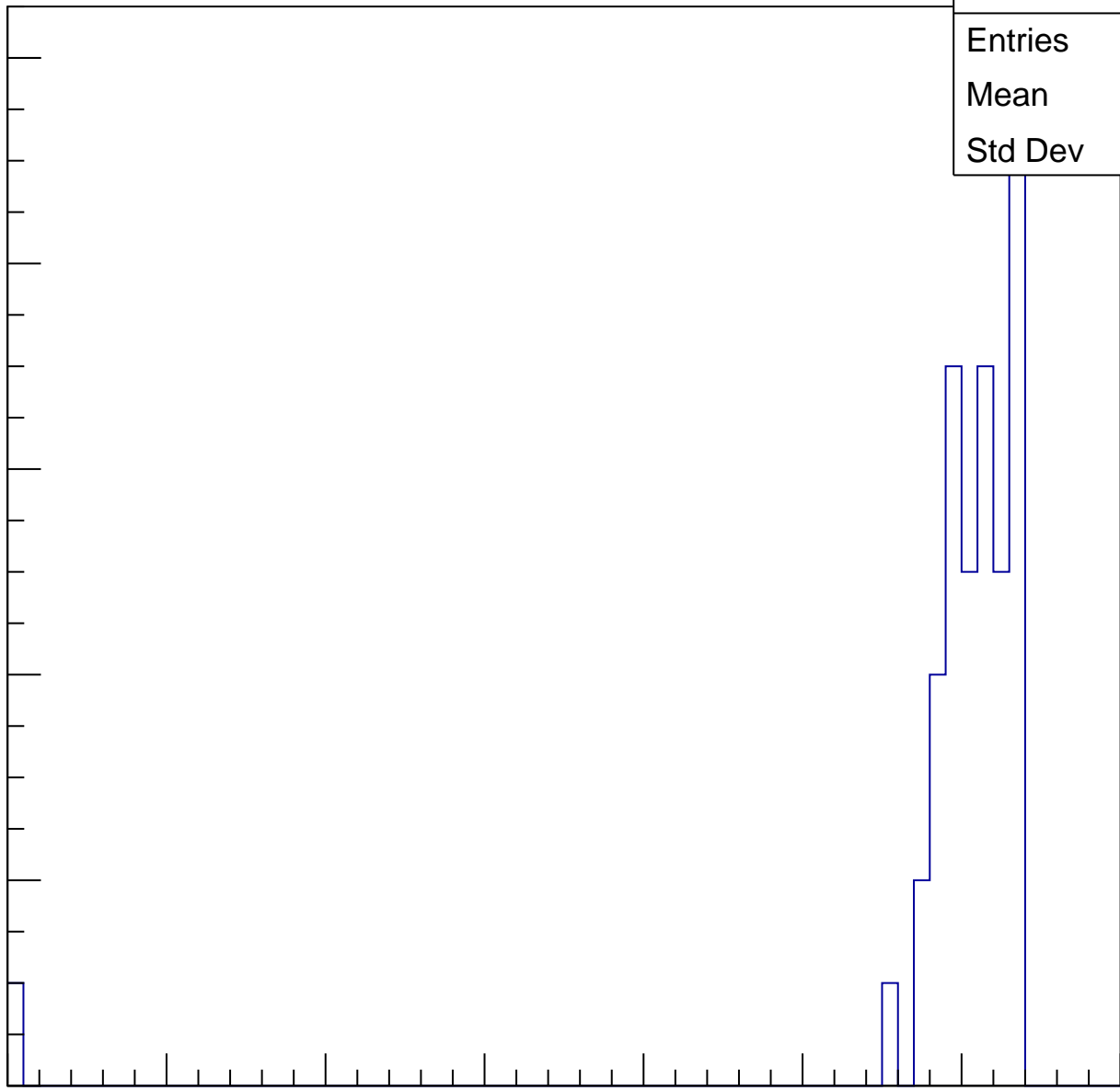
40

50

60

70

ampl



B1L103S, U13-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch55, adc0

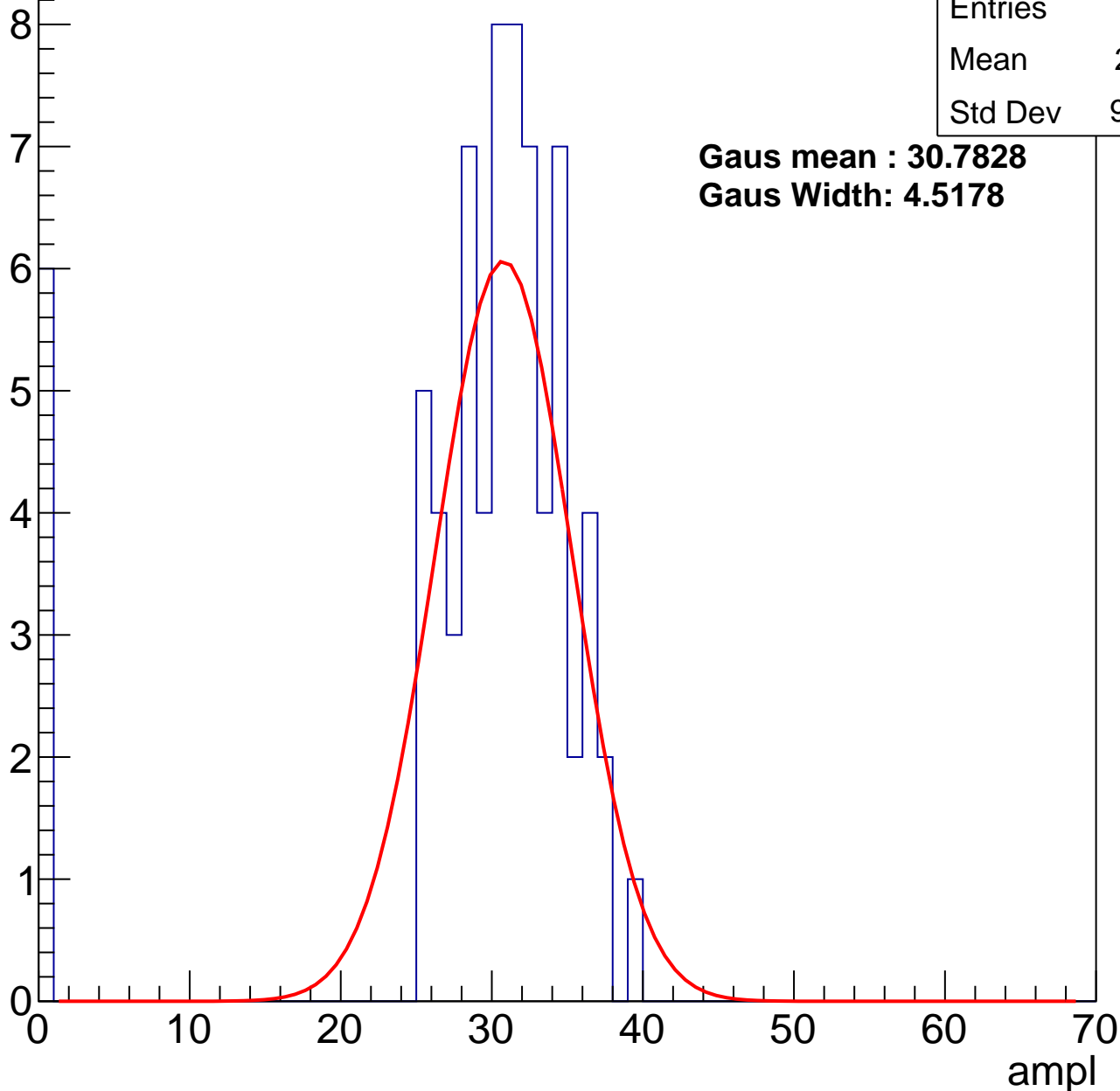
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	28.21
Std Dev	9.109

Gaus mean : 30.7828

Gaus Width: 4.5178



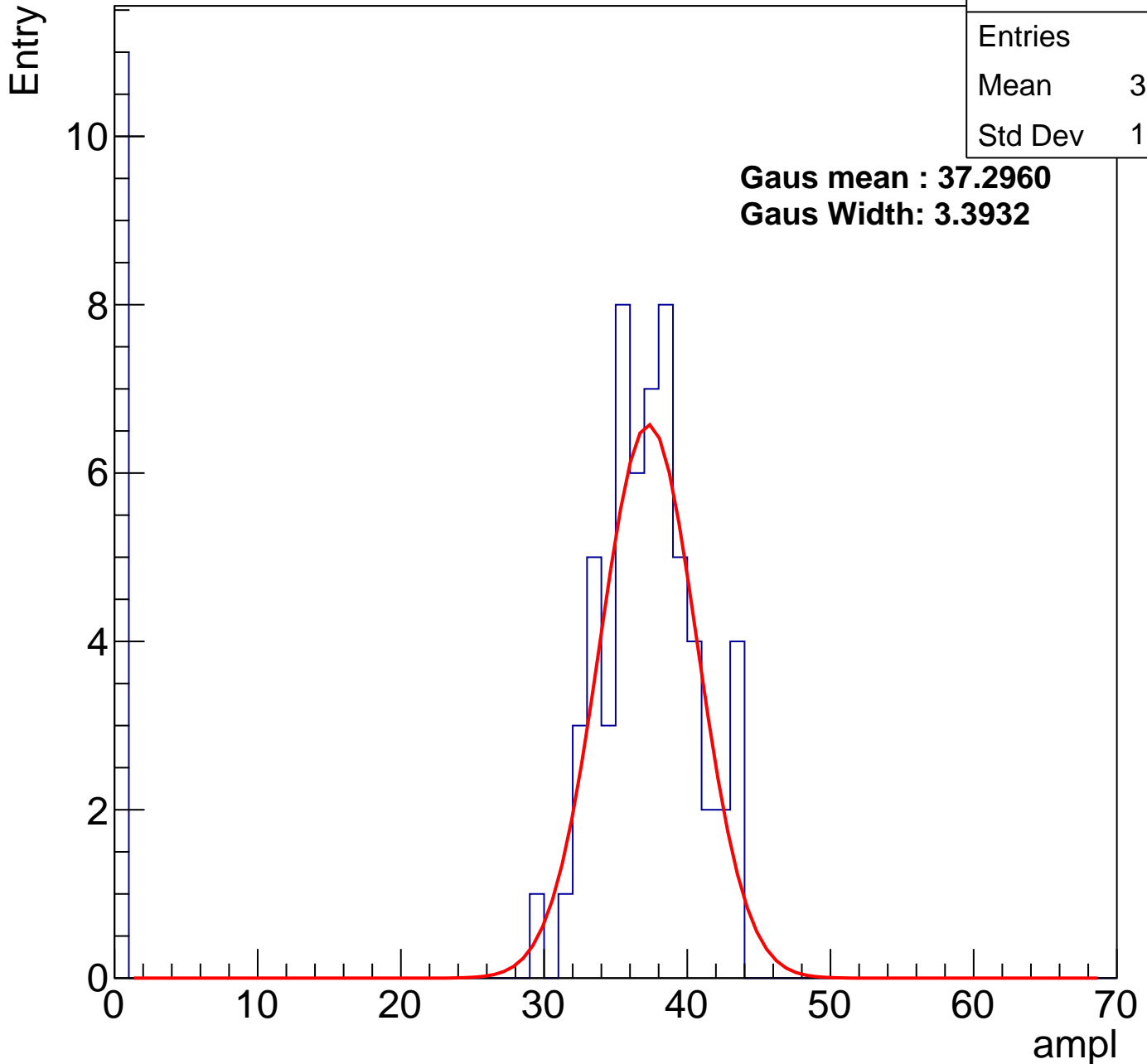
B1L103S, U13-ch55, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	31.07
Std Dev	13.74

Gaus mean : 37.2960

Gaus Width: 3.3932



B1L103S, U13-ch55, adc2

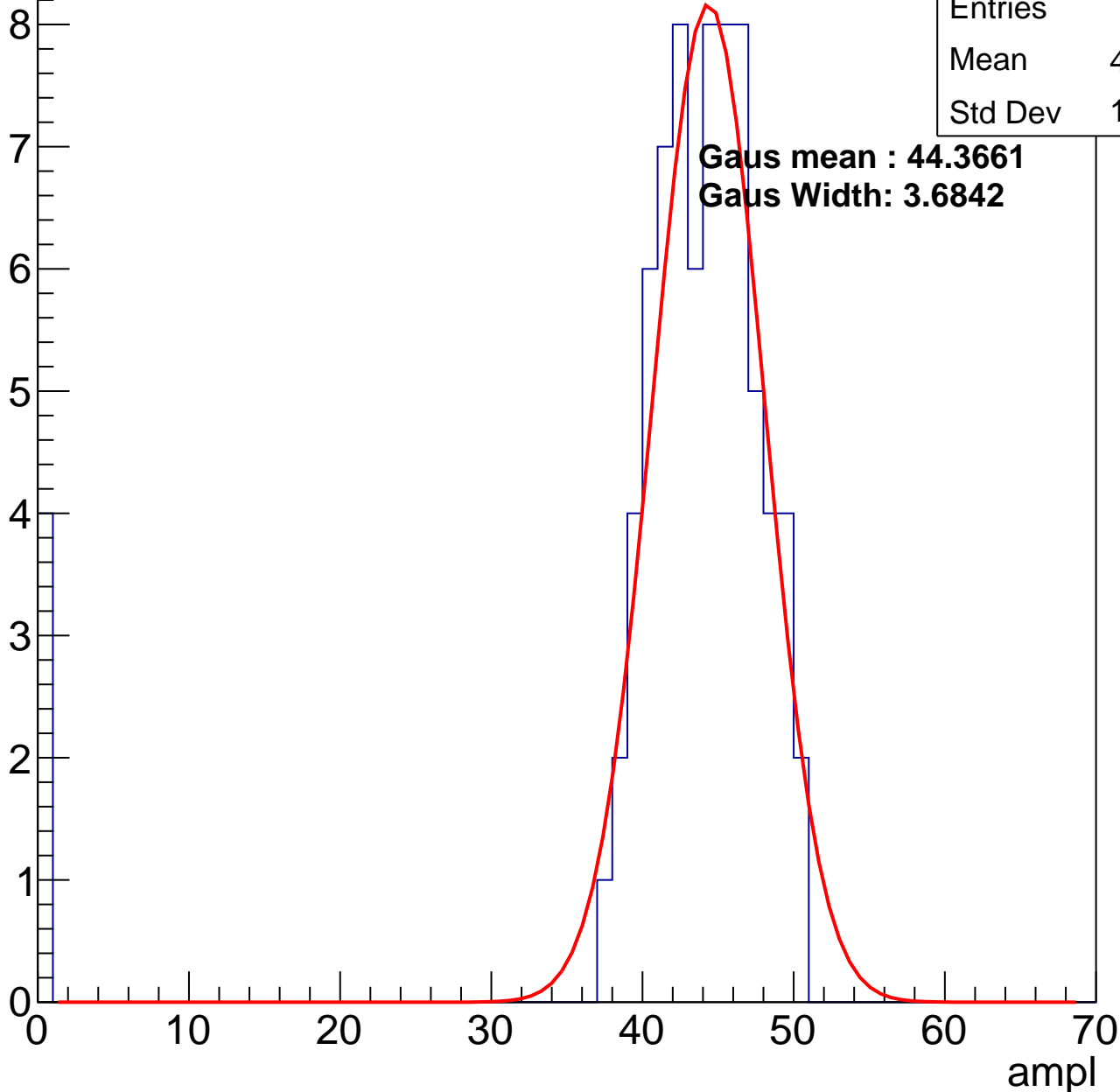
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	41.47
Std Dev	10.19

Gaus mean : 44.3661

Gaus Width: 3.6842

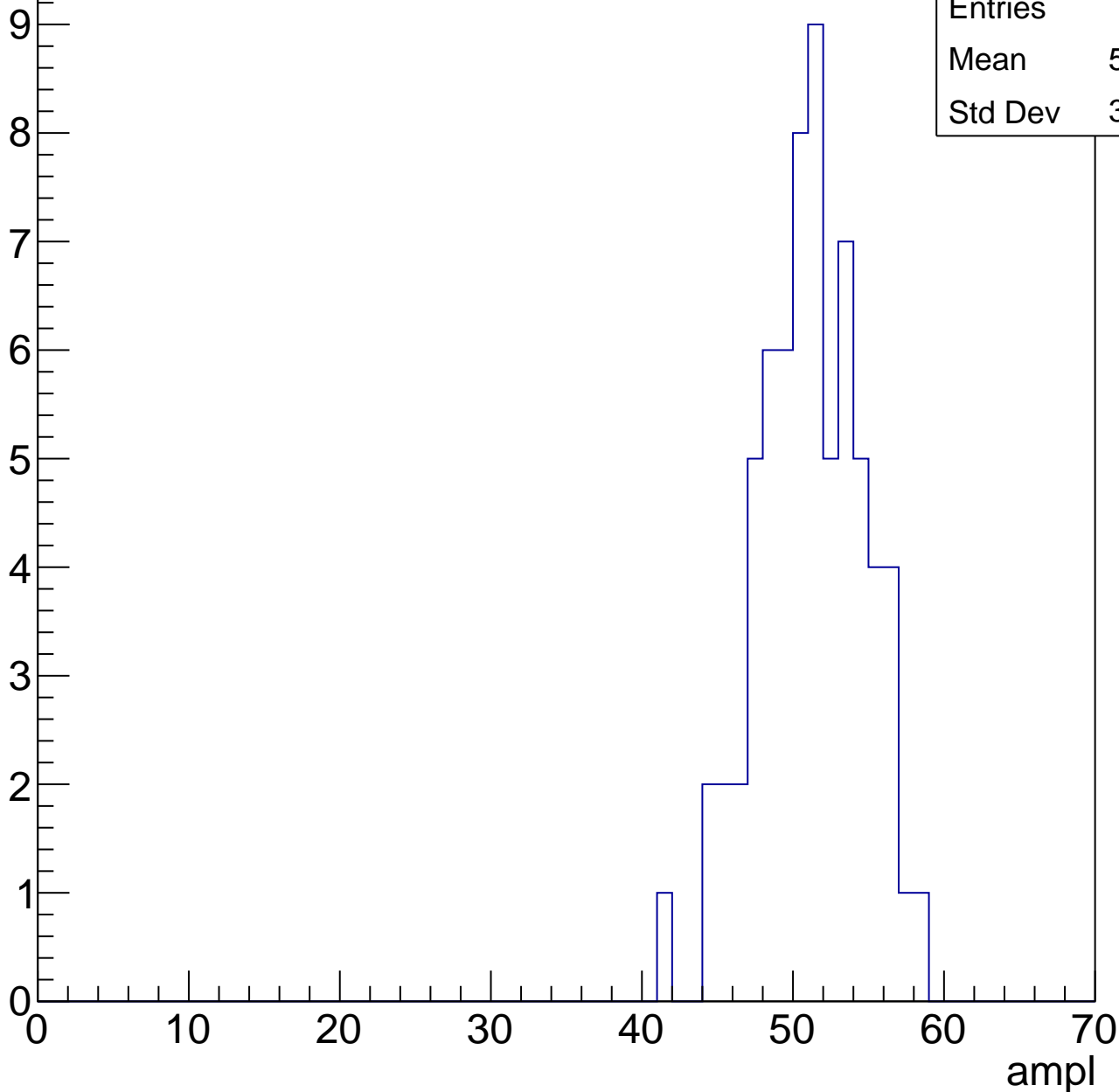


B1L103S, U13-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	50.69
Std Dev	3.444

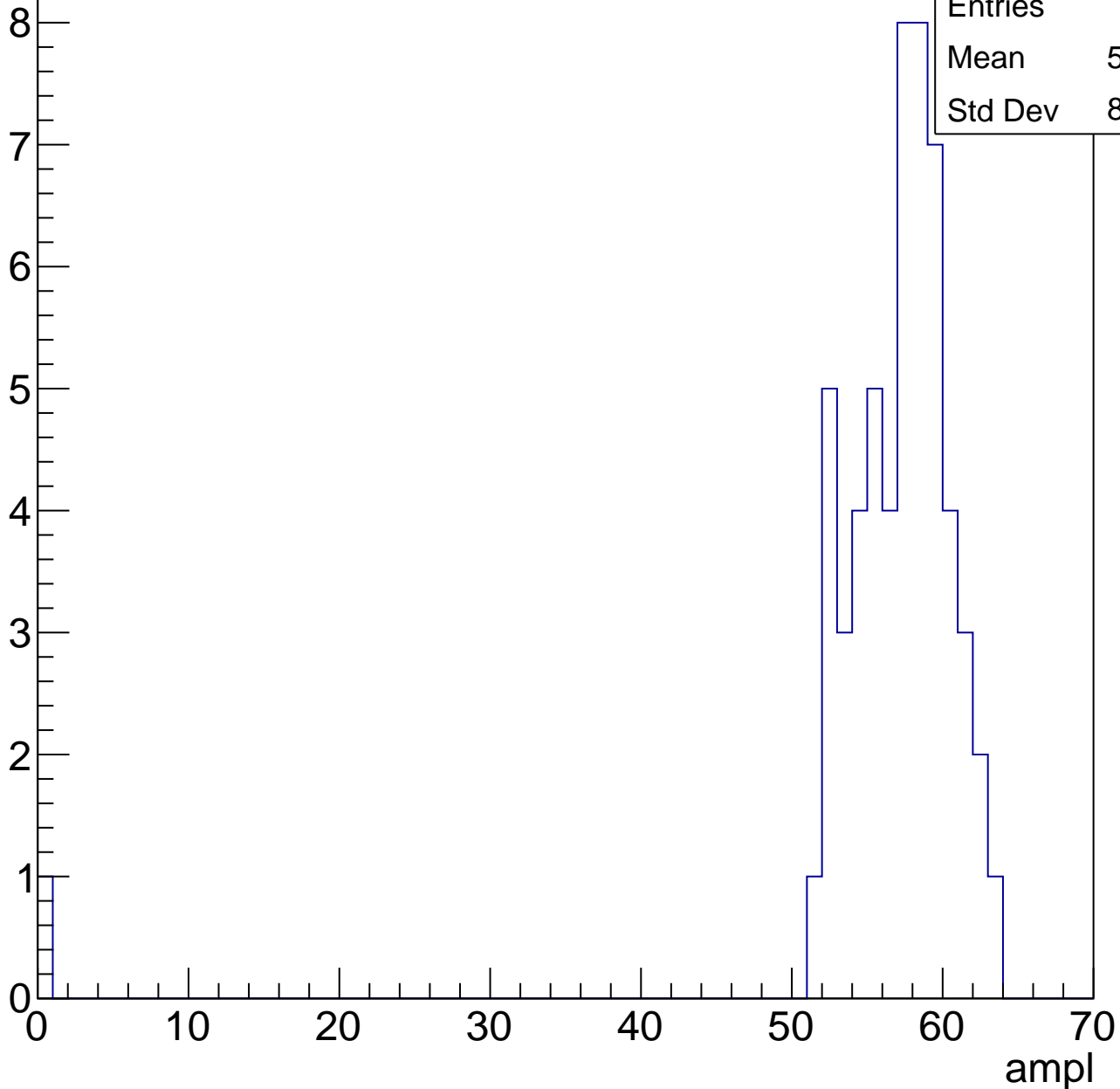


B1L103S, U13-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	55.86
Std Dev	8.074

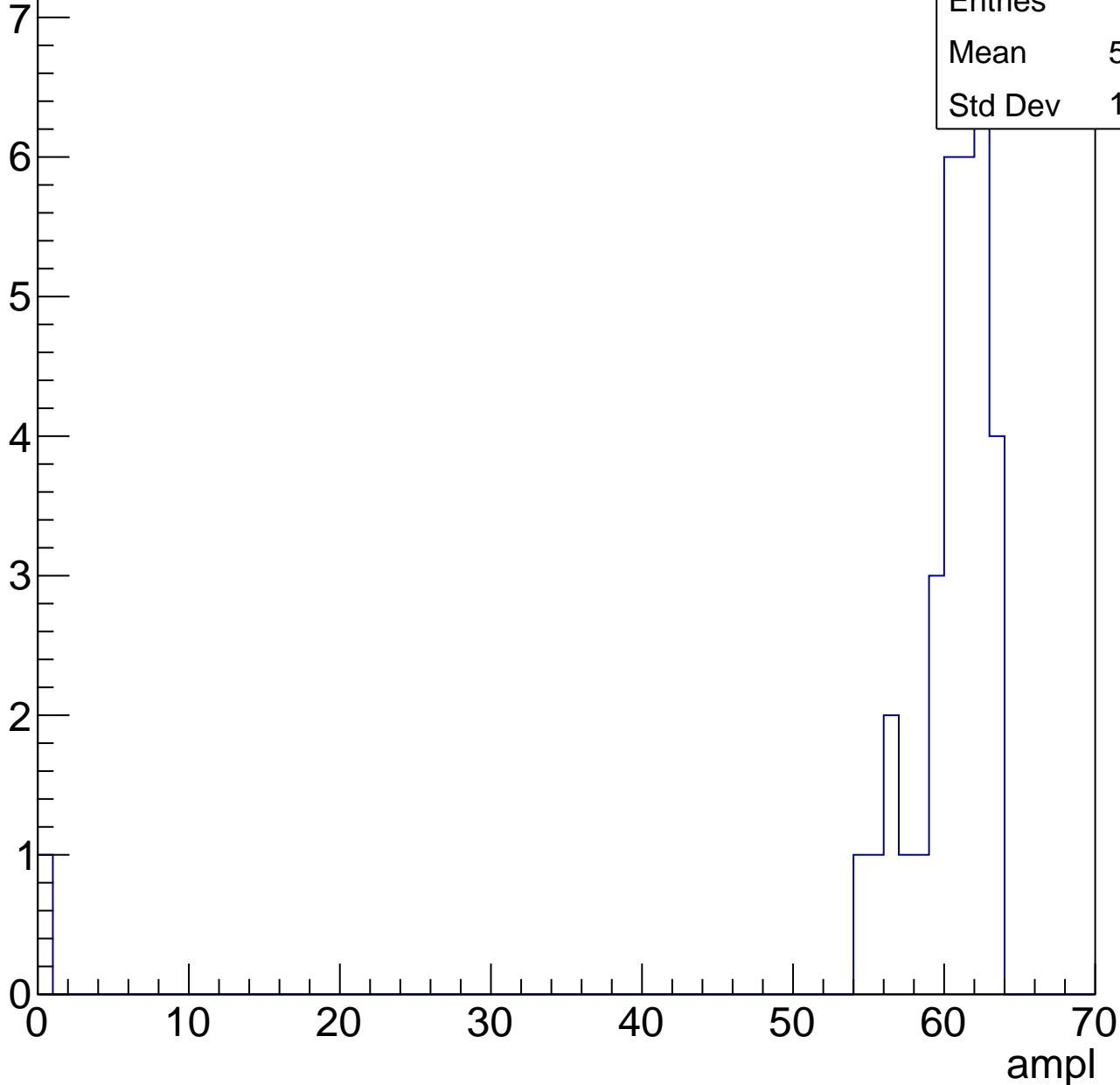


B1L103S, U13-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.33
Std Dev	10.57

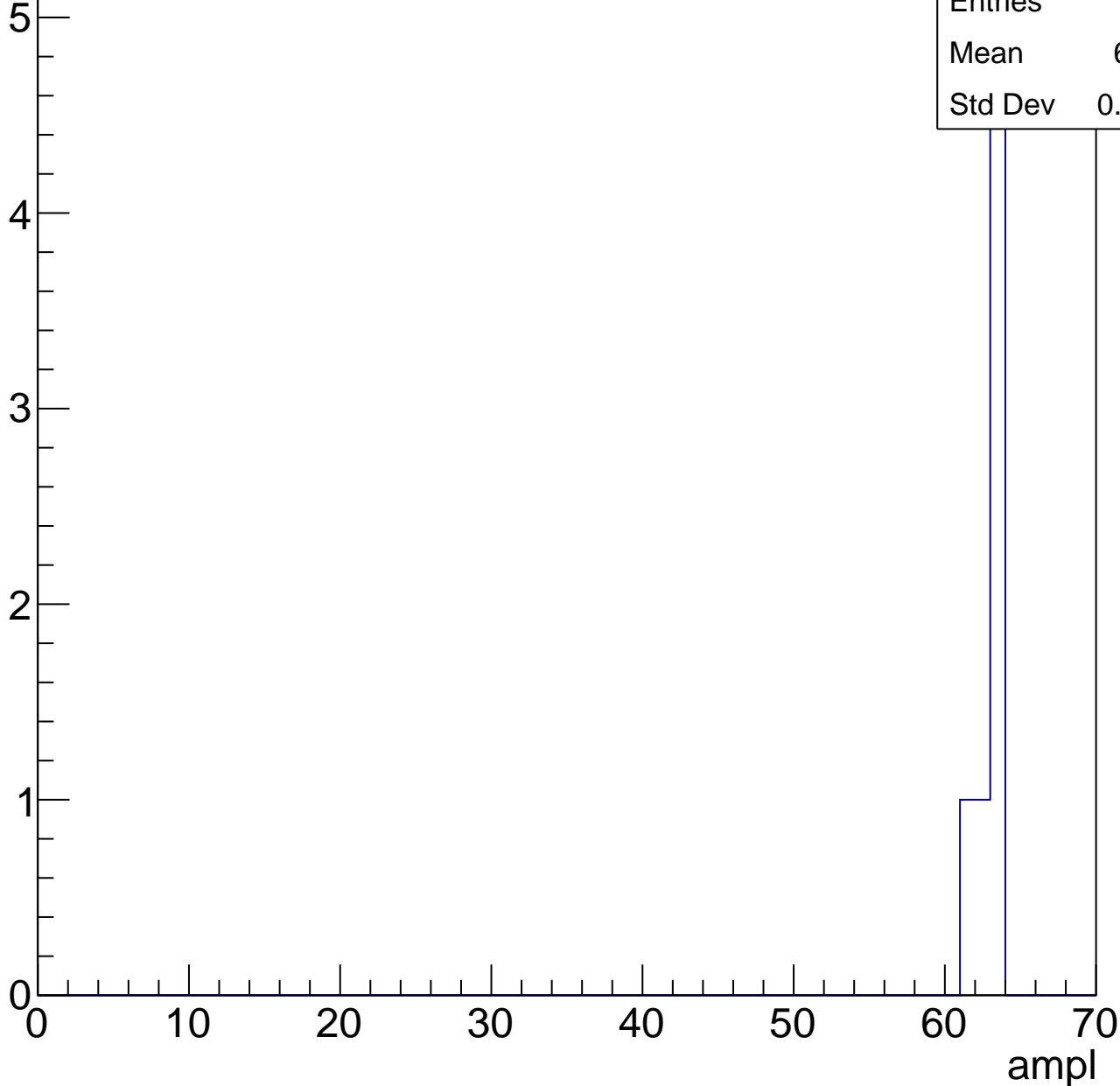


B1L103S, U13-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	7
Mean	62.57
Std Dev	0.7284



B1L103S, U13-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.333
Std Dev	5.497

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U13-ch56, adc0

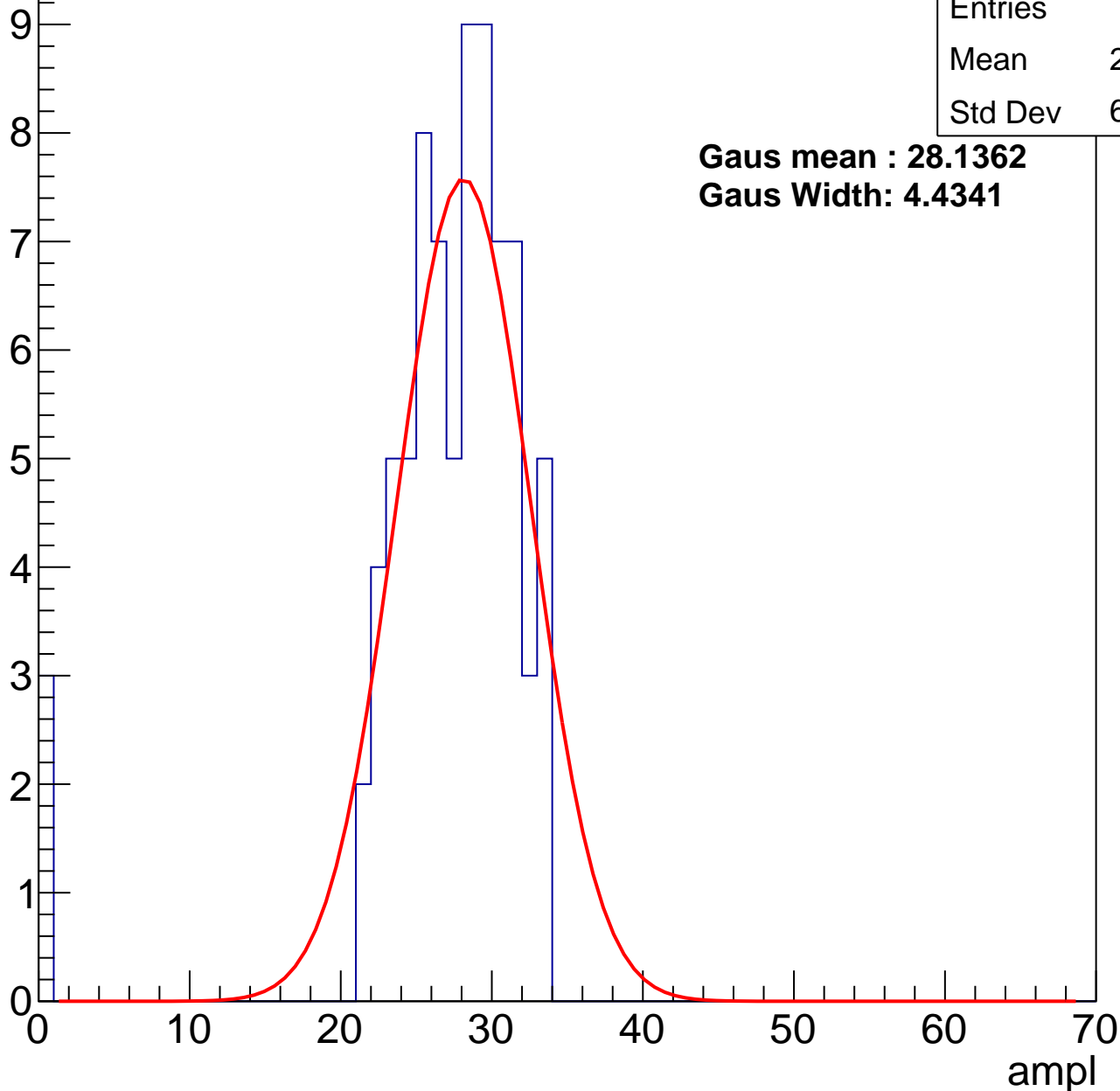
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	26.37
Std Dev	6.128

Gaus mean : 28.1362

Gaus Width: 4.4341



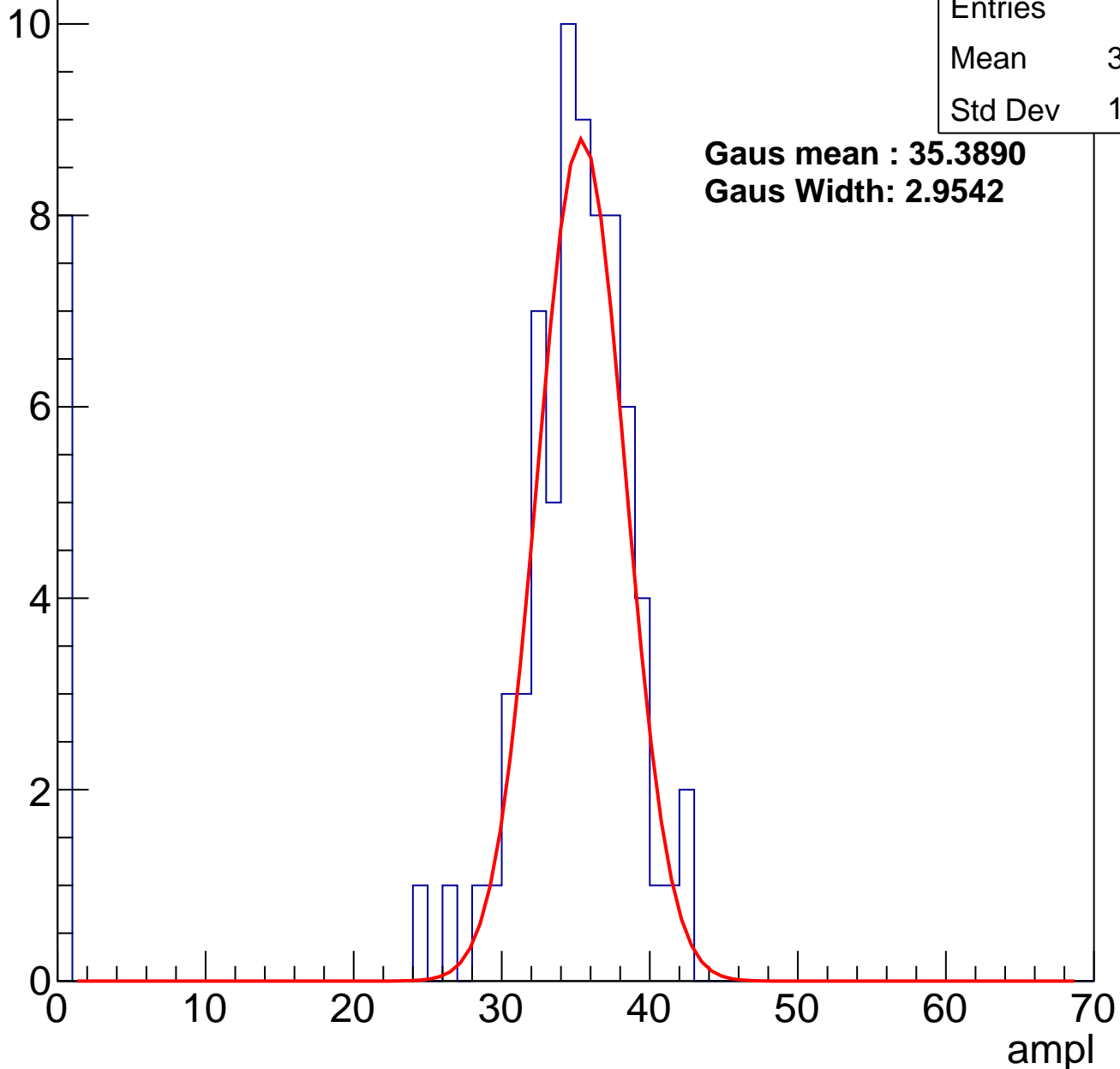
B1L103S, U13-ch56, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	31.23
Std Dev	10.96

Gaus mean : 35.3890
Gaus Width: 2.9542

Entry



B1L103S, U13-ch56, adc2

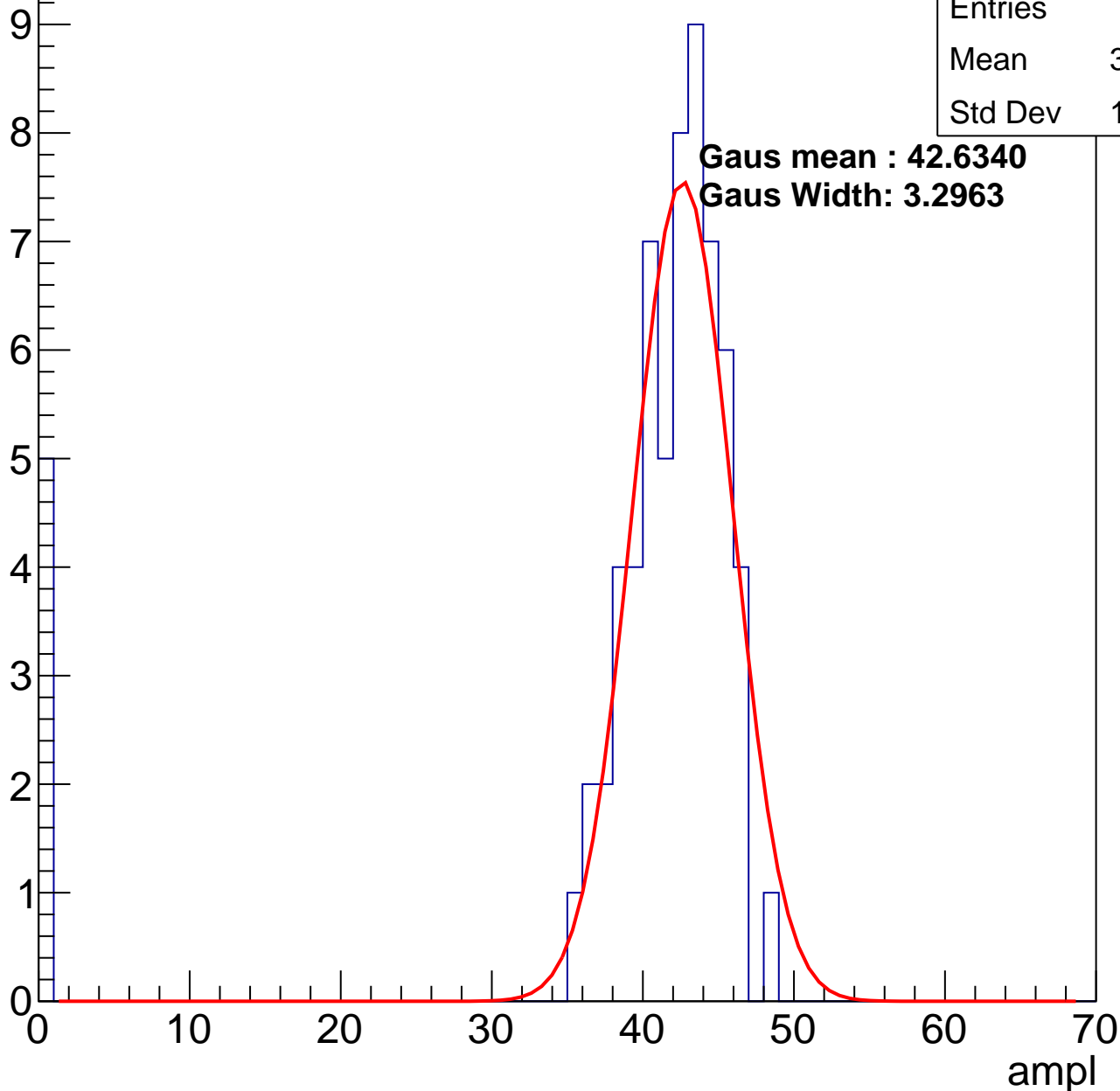
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.57
Std Dev	11.47

Gaus mean : 42.6340

Gaus Width: 3.2963



B1L103S, U13-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

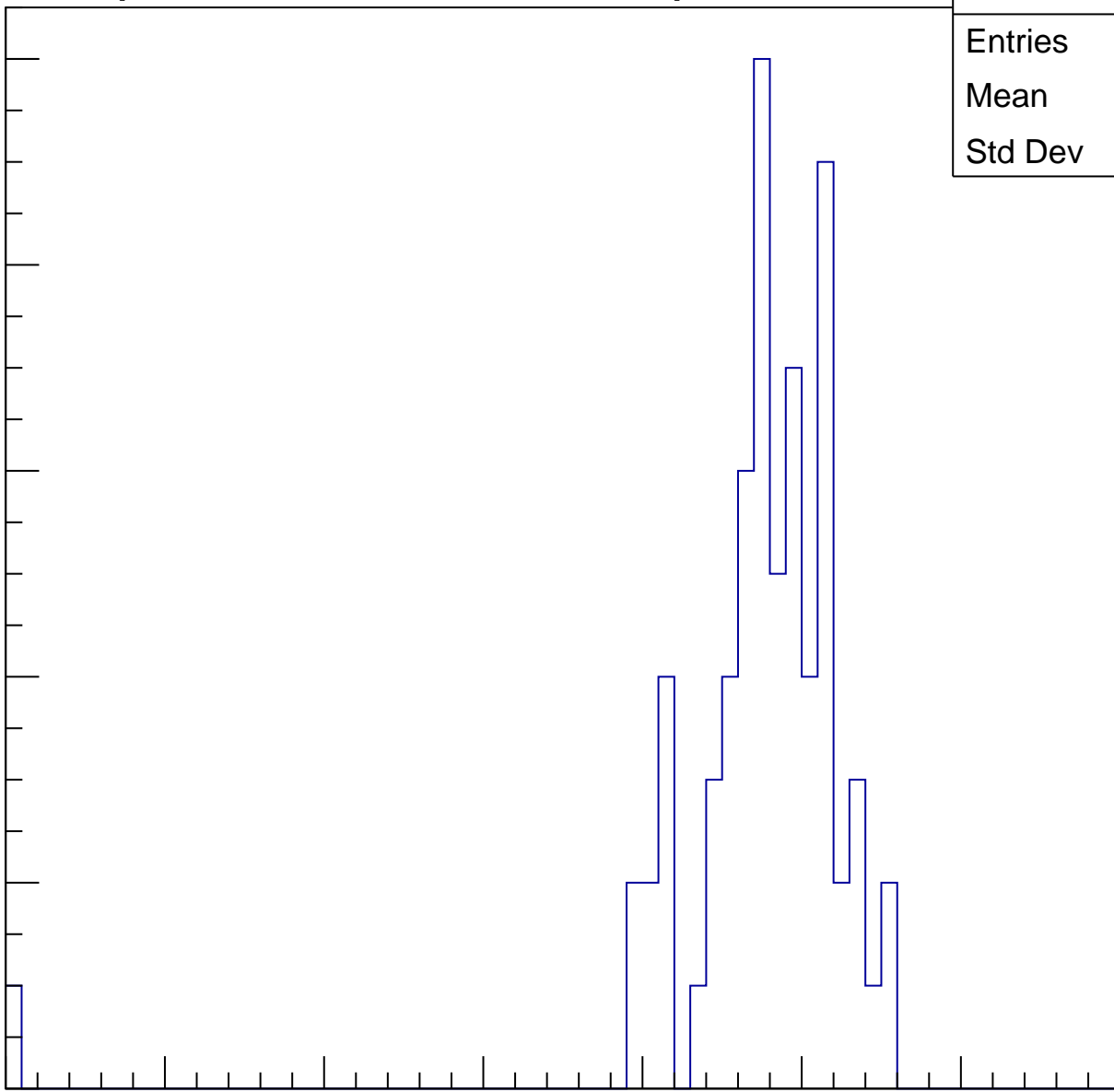
Entries	66
Mean	46.85
Std Dev	6.953

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

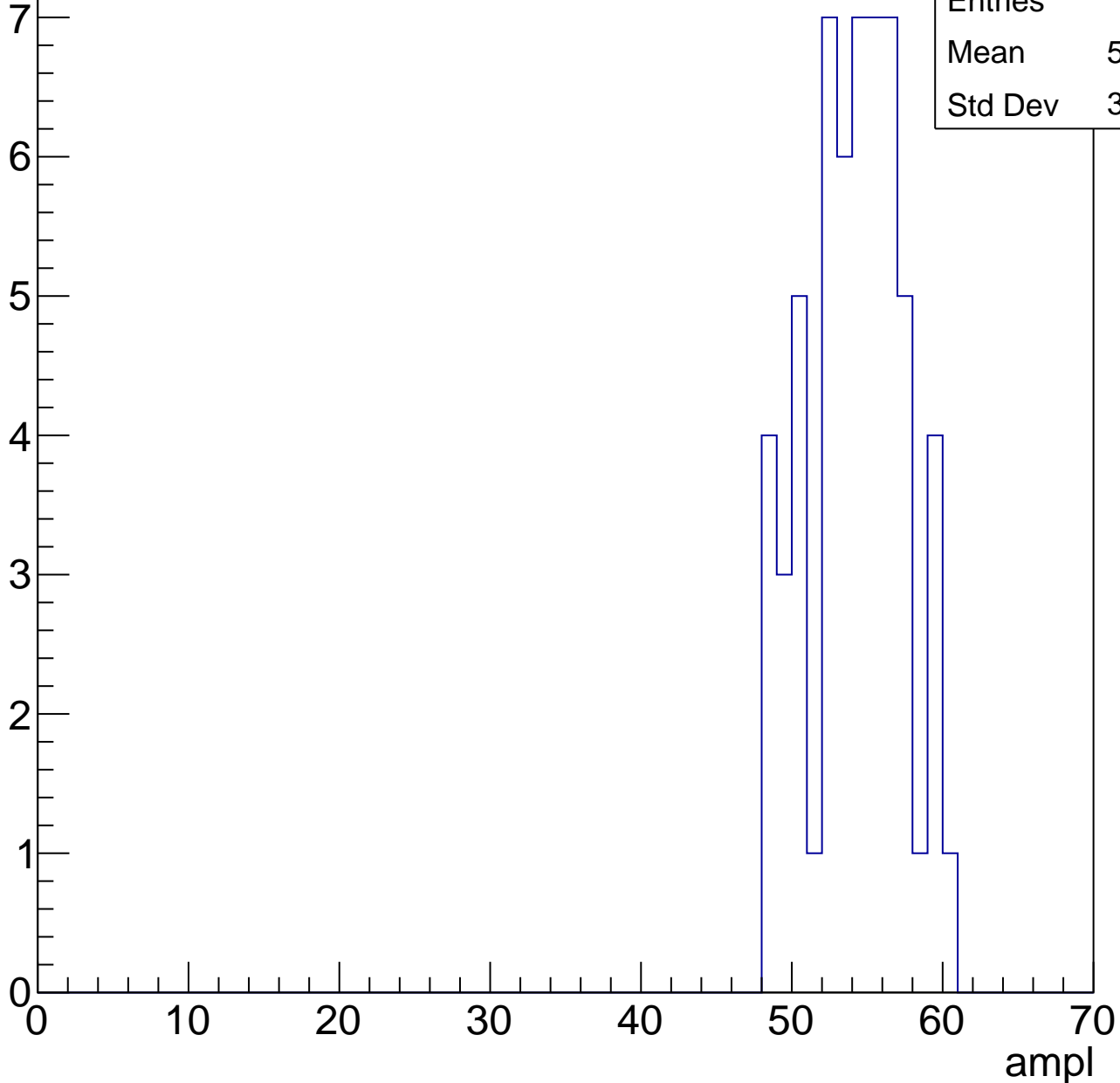


B1L103S, U13-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.72
Std Dev	3.134

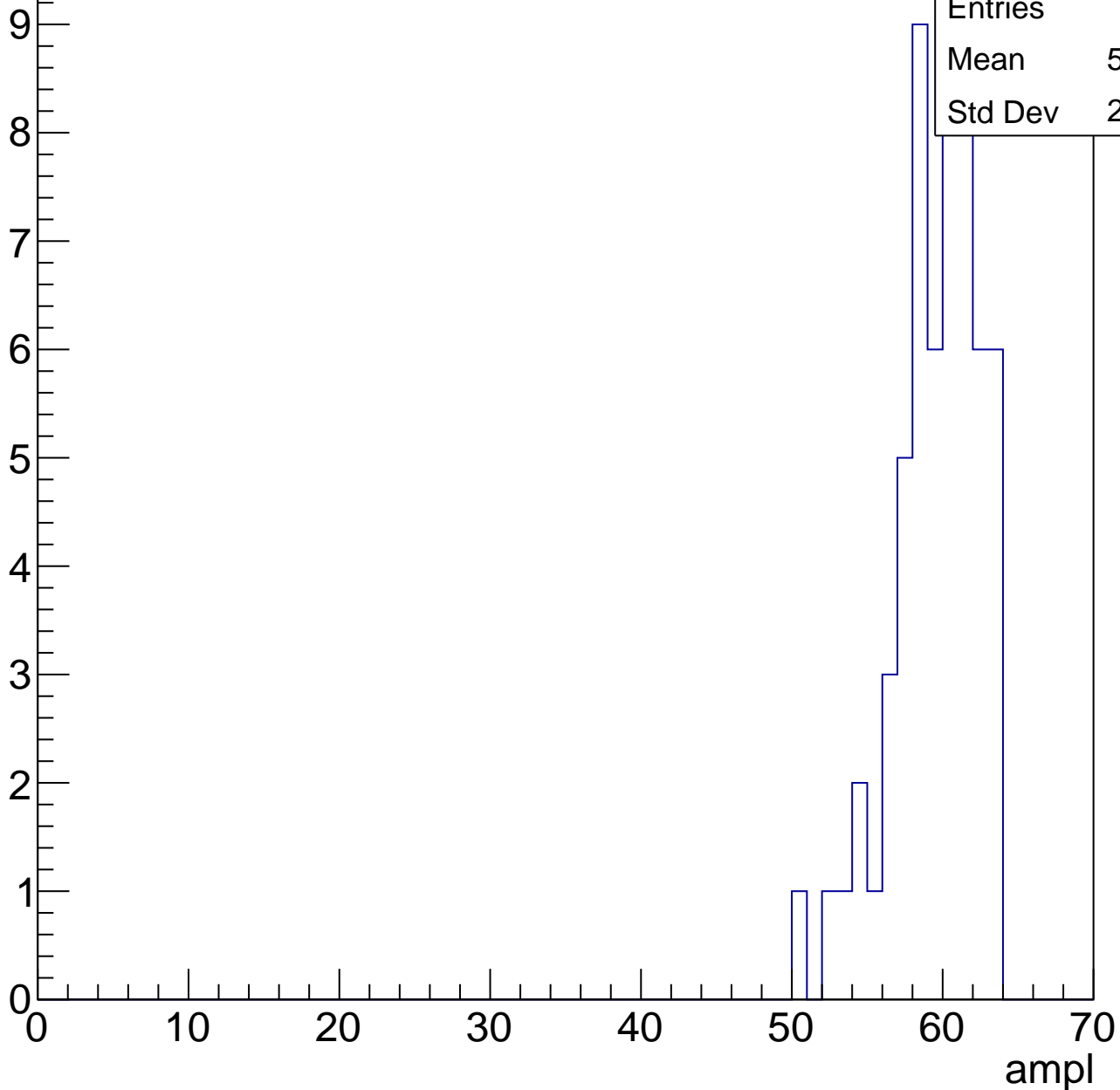


B1L103S, U13-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

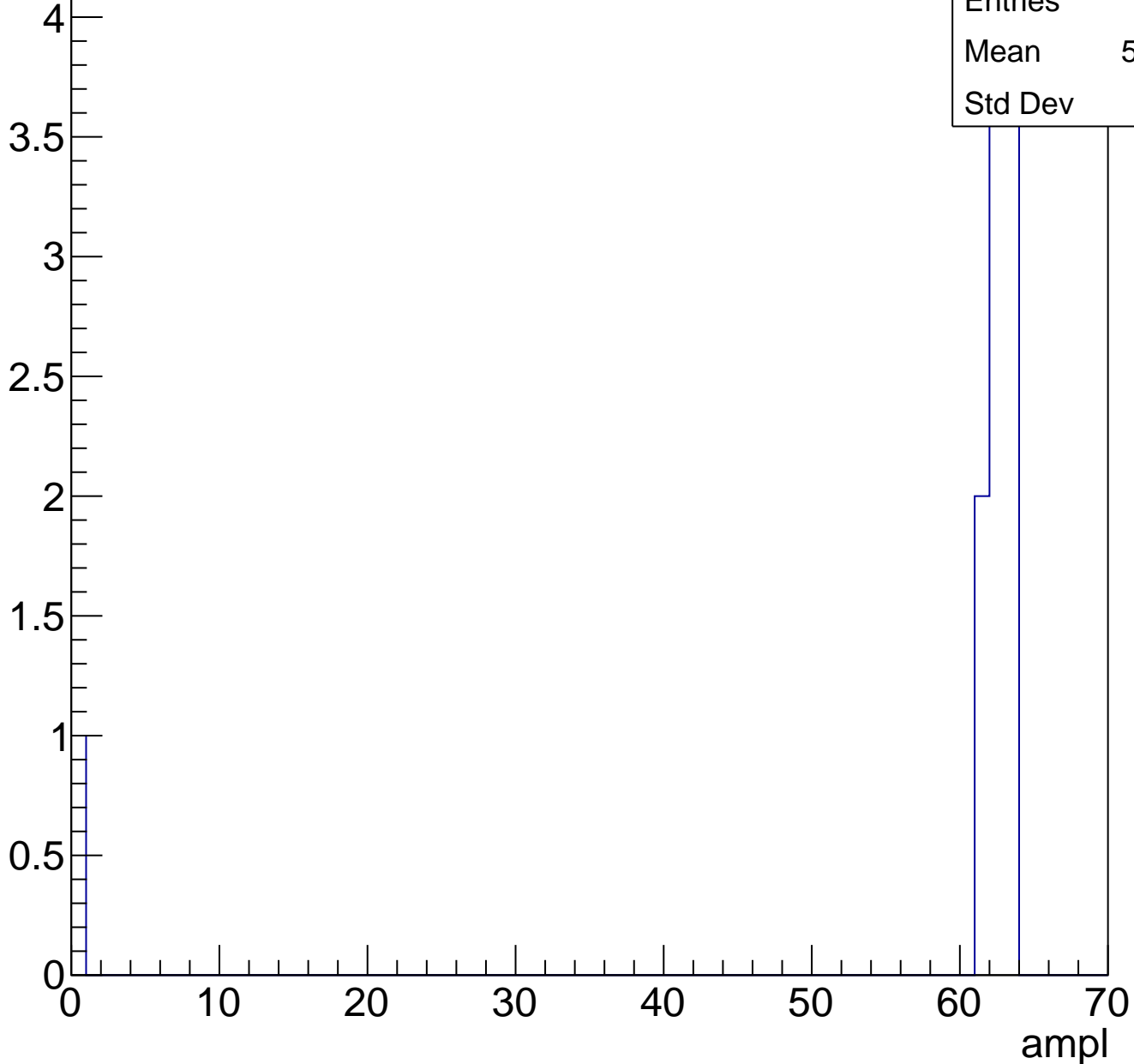
Entries	58
Mean	59.05
Std Dev	2.873



B1L103S, U13-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

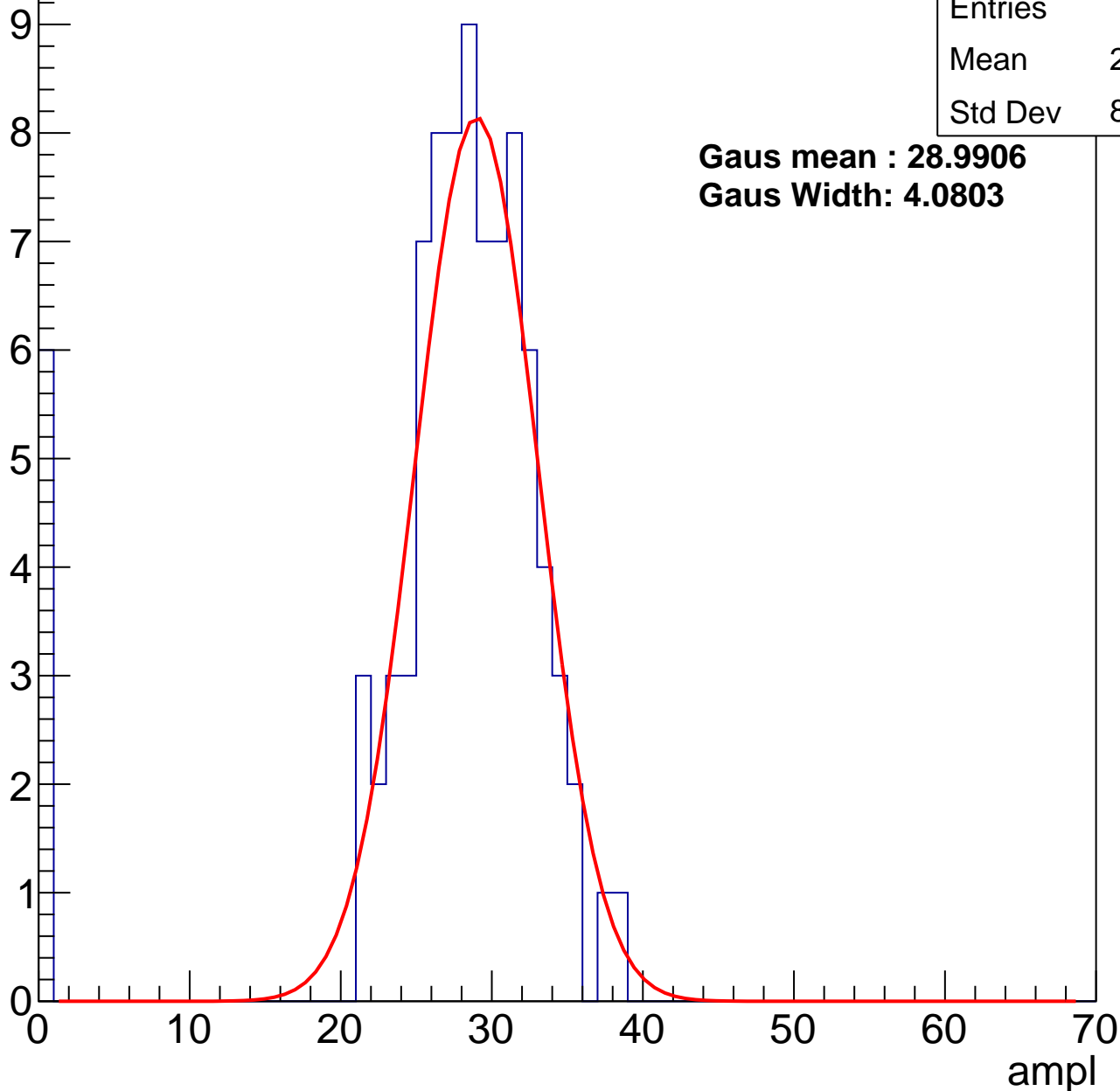
Entry



B1L103S, U13-ch57, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	88
Mean	26.49
Std Dev	8.004

Gaus mean : 28.9906
Gaus Width: 4.0803

B1L103S, U13-ch57, adc1

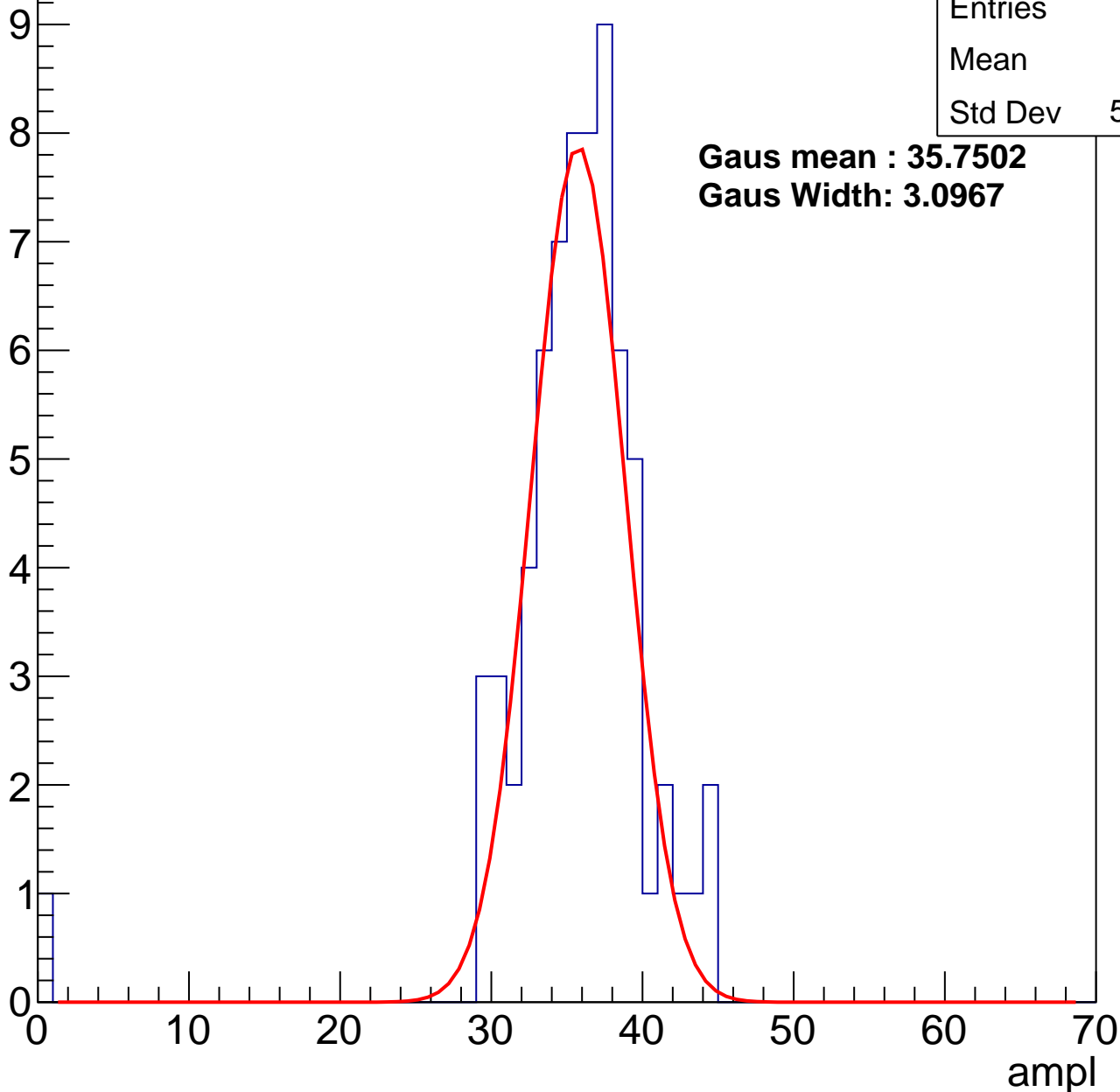
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.1
Std Dev	5.459

Gaus mean : 35.7502

Gaus Width: 3.0967



B1L103S, U13-ch57, adc2

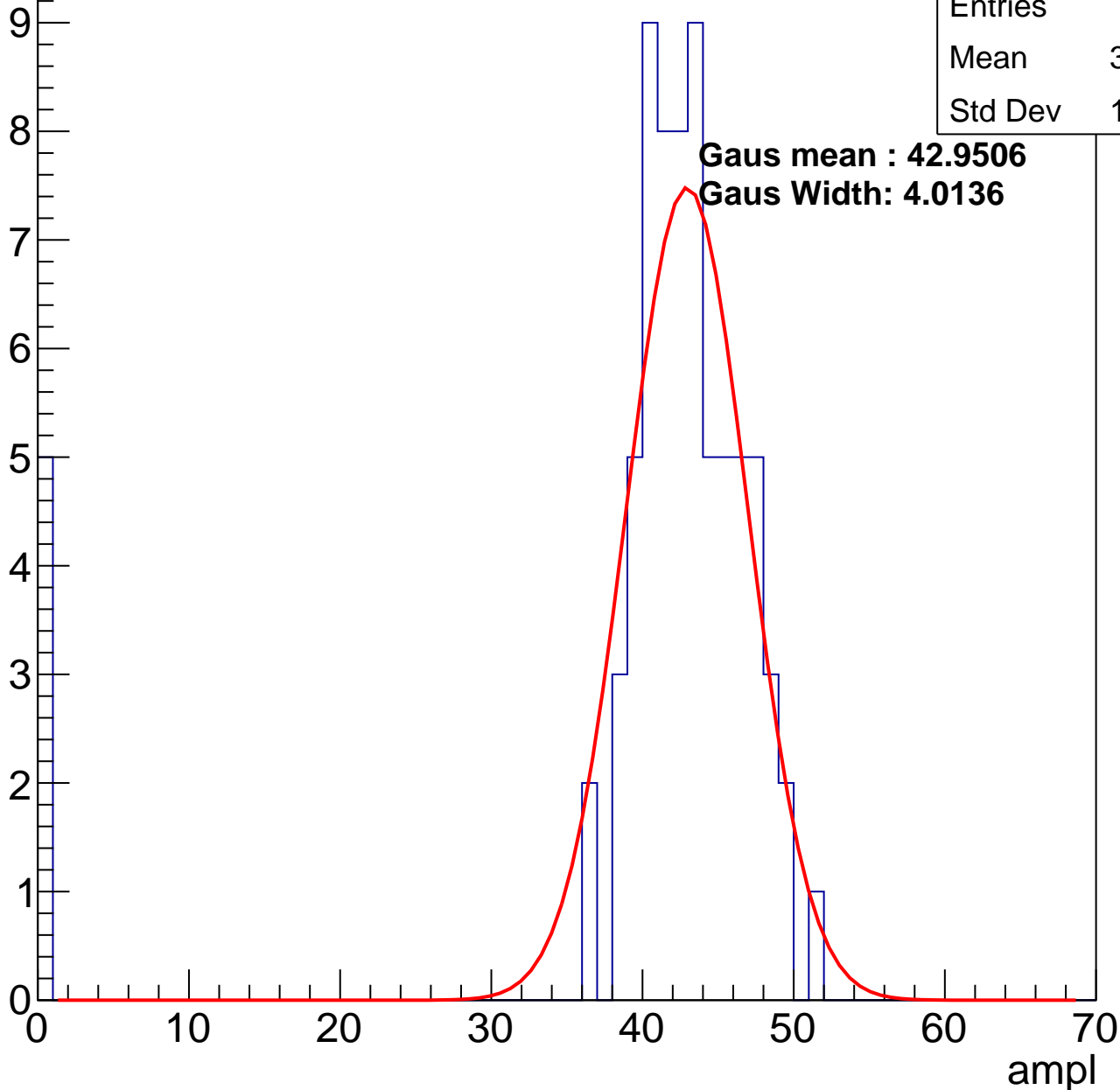
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	39.93
Std Dev	11.12

Gaus mean : 42.9506

Gaus Width: 4.0136

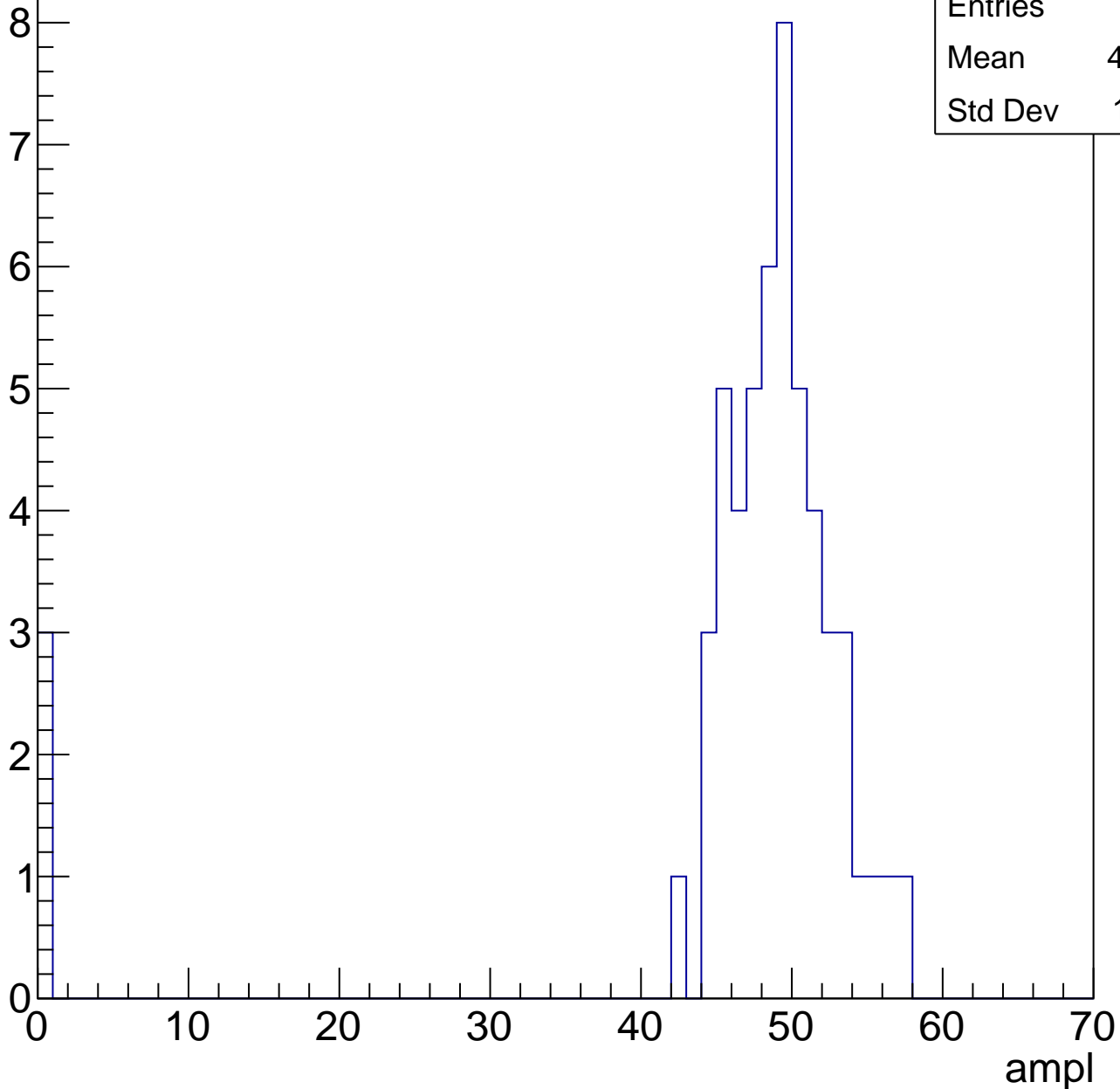


B1L103S, U13-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	46.09
Std Dev	11.61

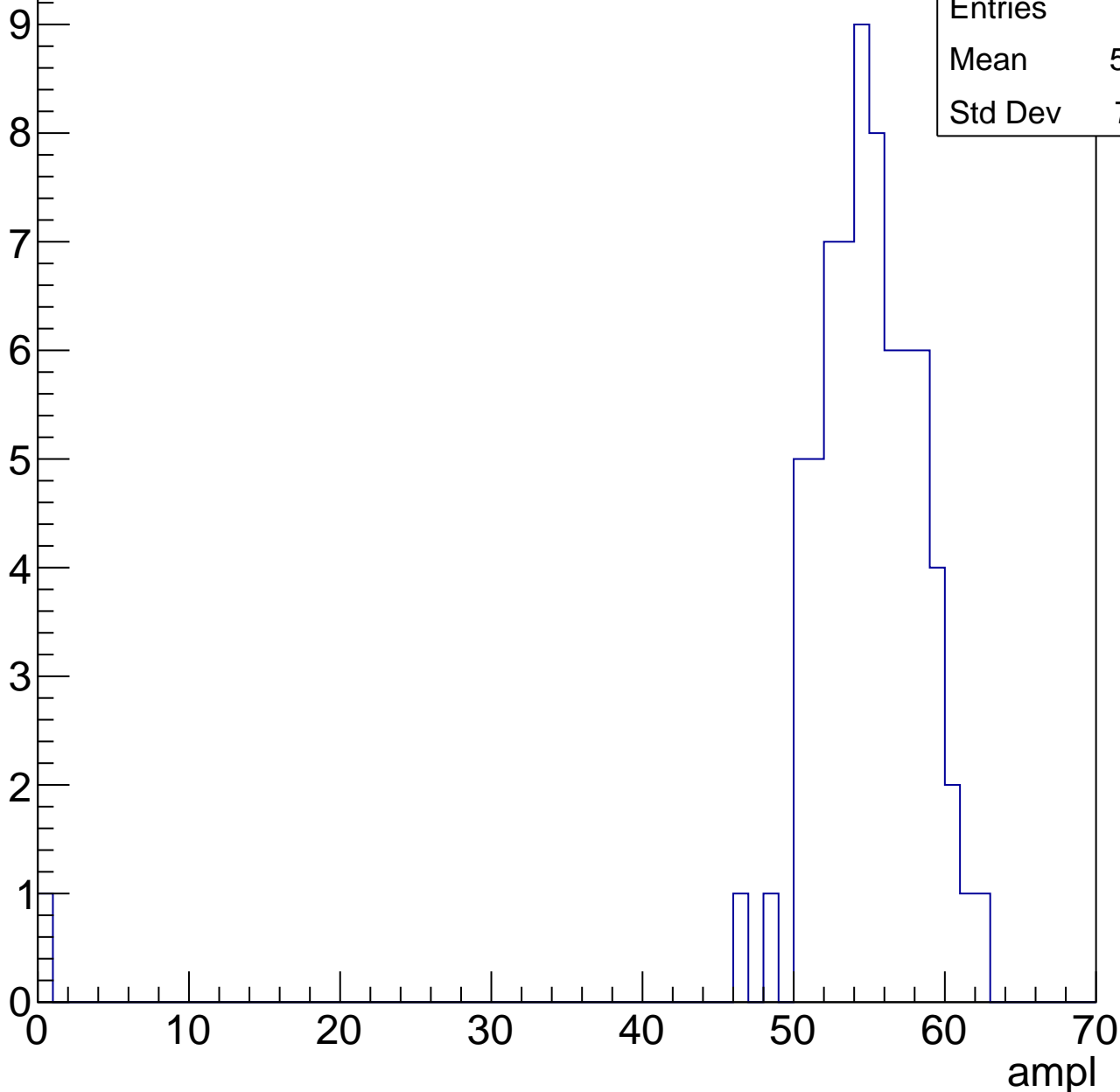


B1L103S, U13-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	53.79
Std Dev	7.211



B1L103S, U13-ch57, adc5

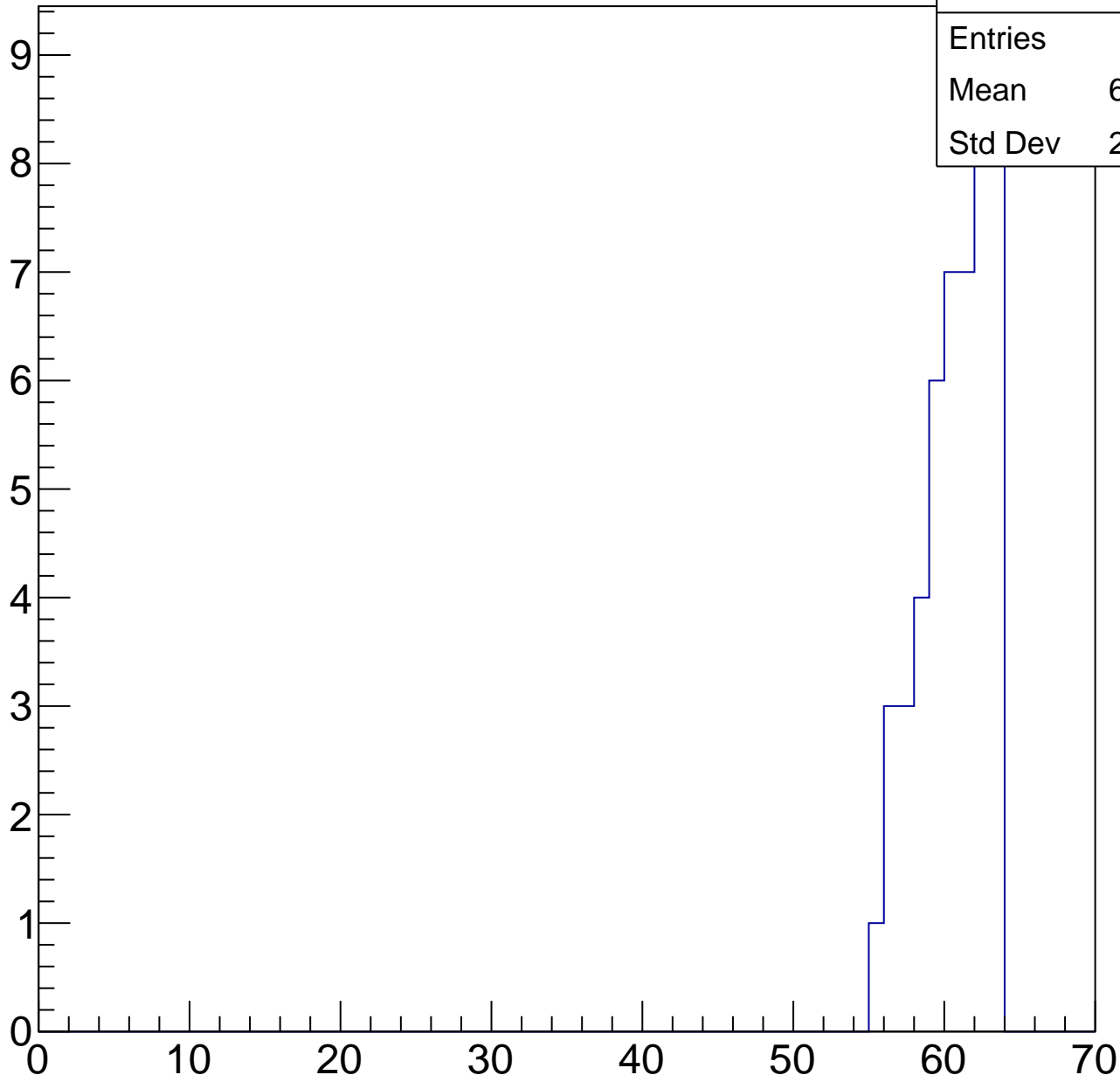
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	60.24
Std Dev	2.227

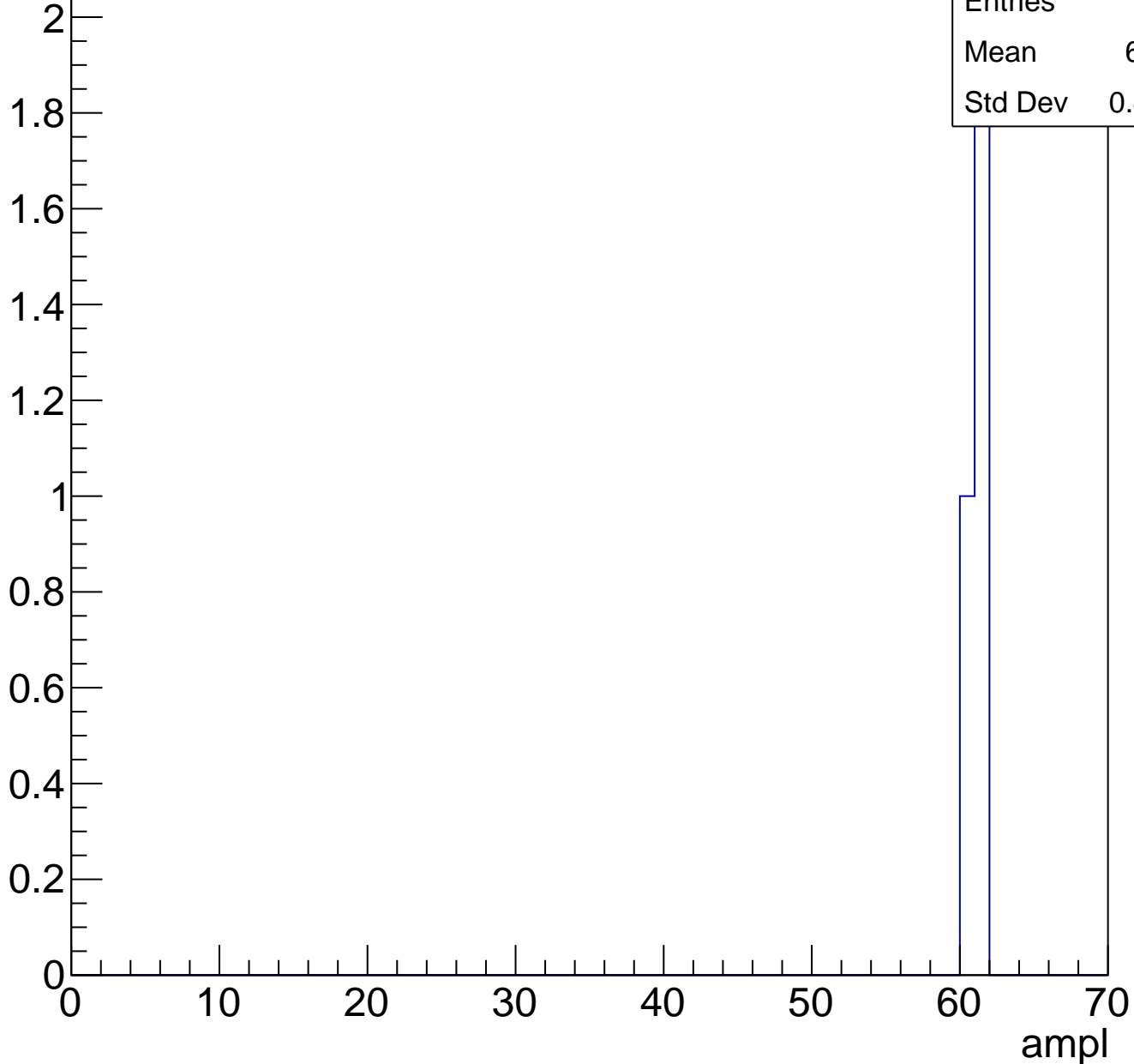
ampl



B1L103S, U13-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

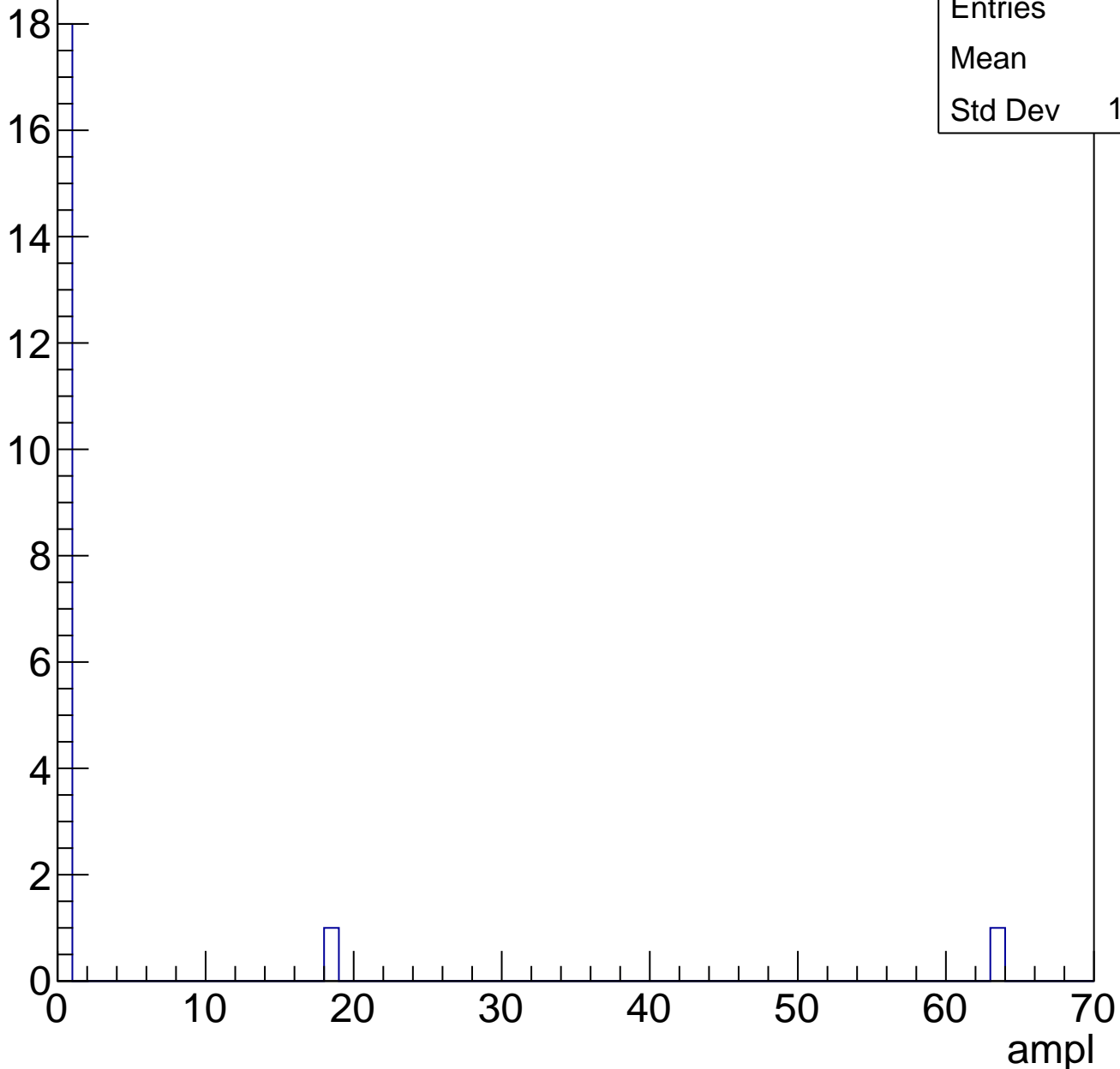


B1L103S, U13-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.05
Std Dev	14.08

Entry



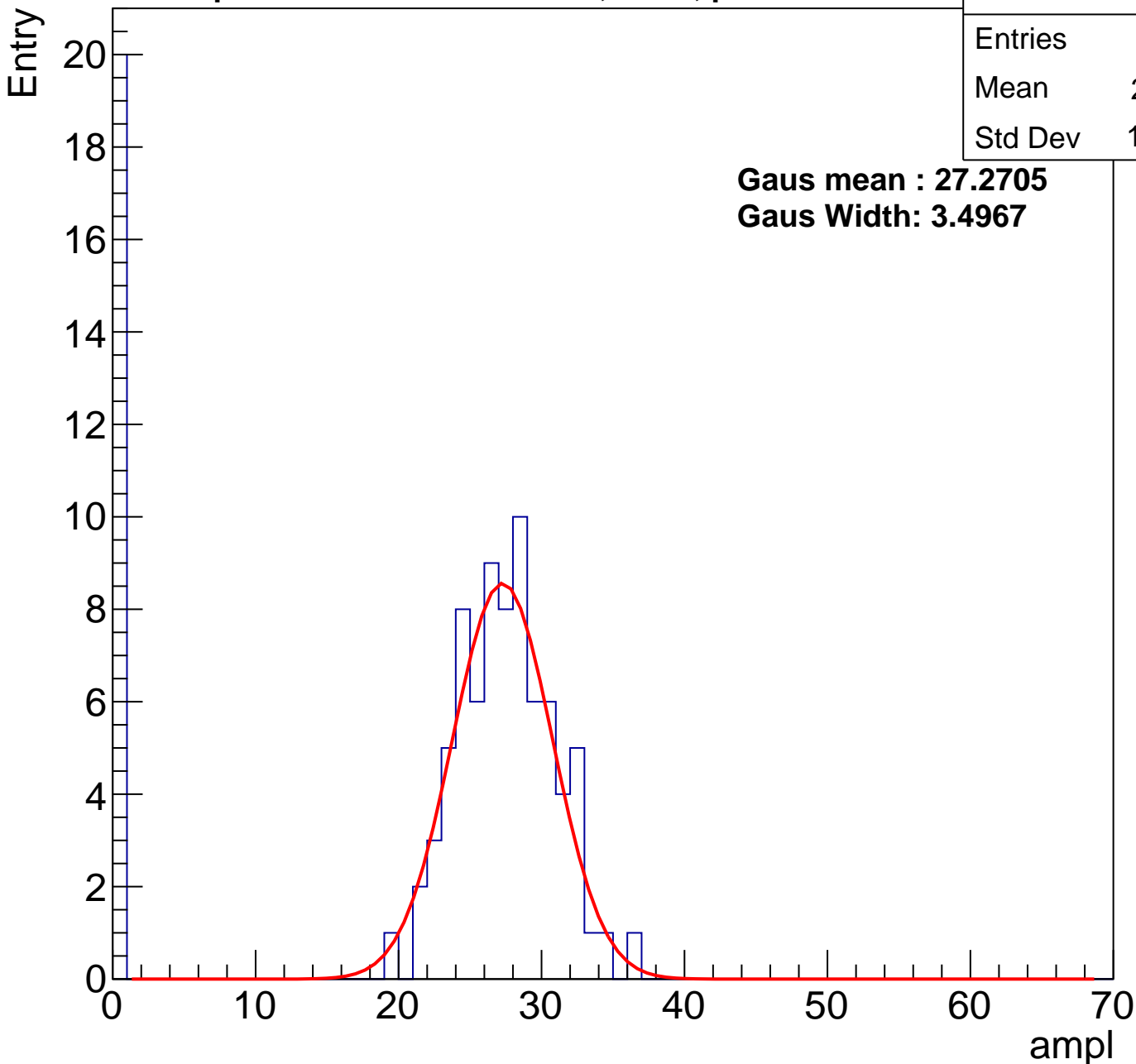
B1L103S, U13-ch58, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	21.41
Std Dev	11.37

Gaus mean : 27.2705

Gaus Width: 3.4967



B1L103S, U13-ch58, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	28.94
Std Dev	12.62

Gaus mean : 34.5218

Gaus Width: 3.2430

Entry

10

8

6

4

2

0

0

10

20

30

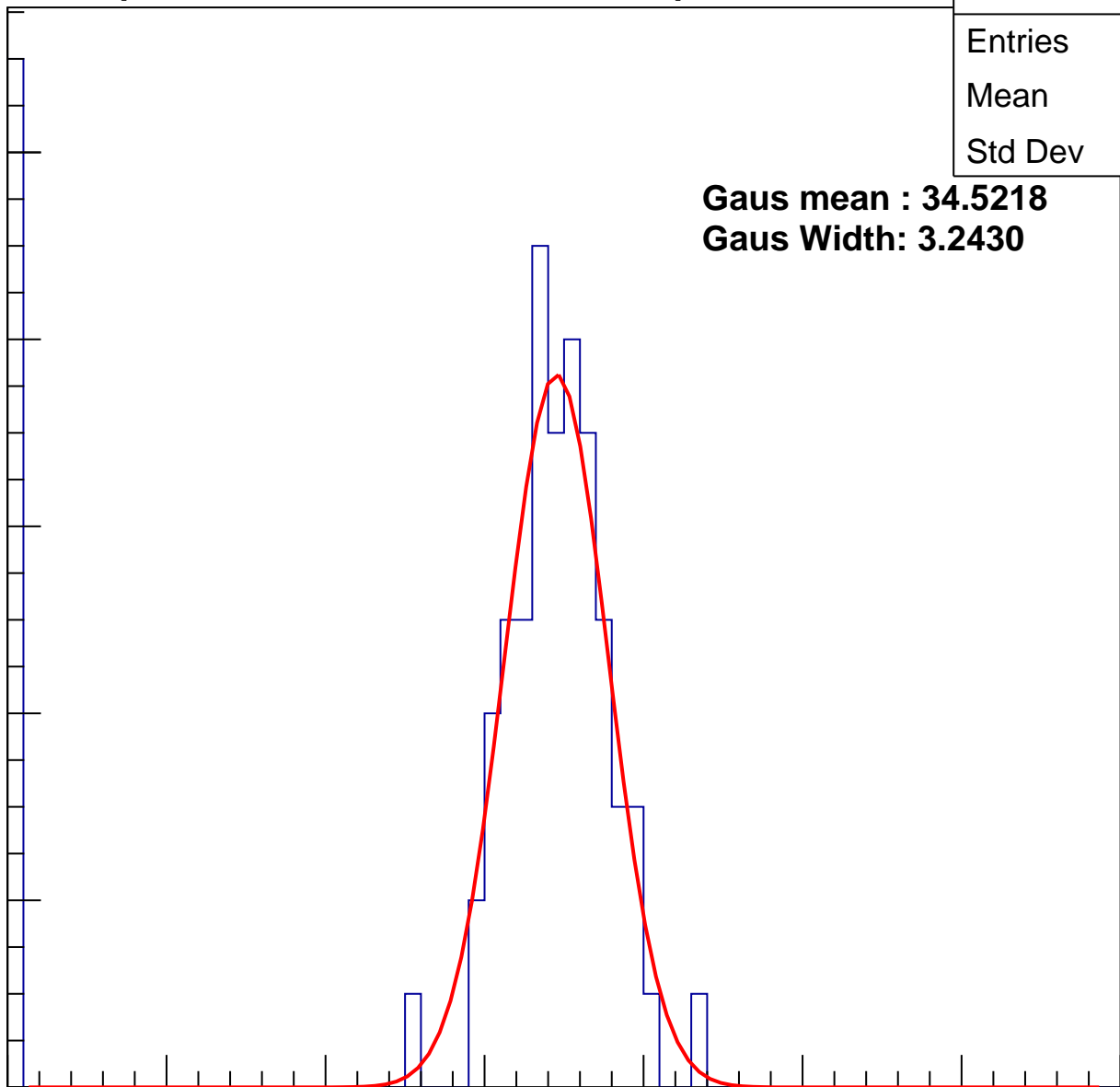
40

50

60

70

ampl



B1L103S, U13-ch58, adc2

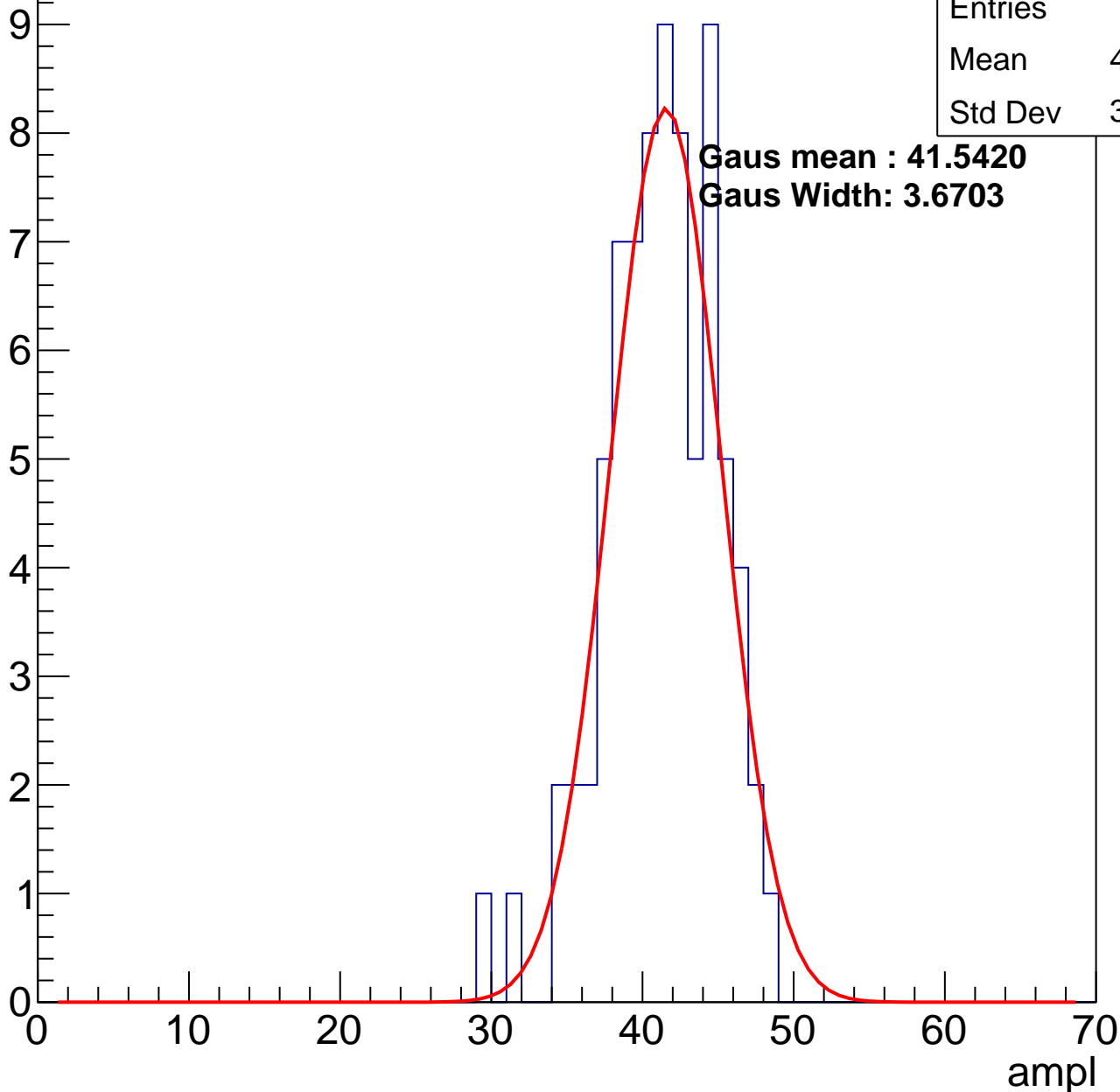
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	40.78
Std Dev	3.668

Gaus mean : 41.5420

Gaus Width: 3.6703

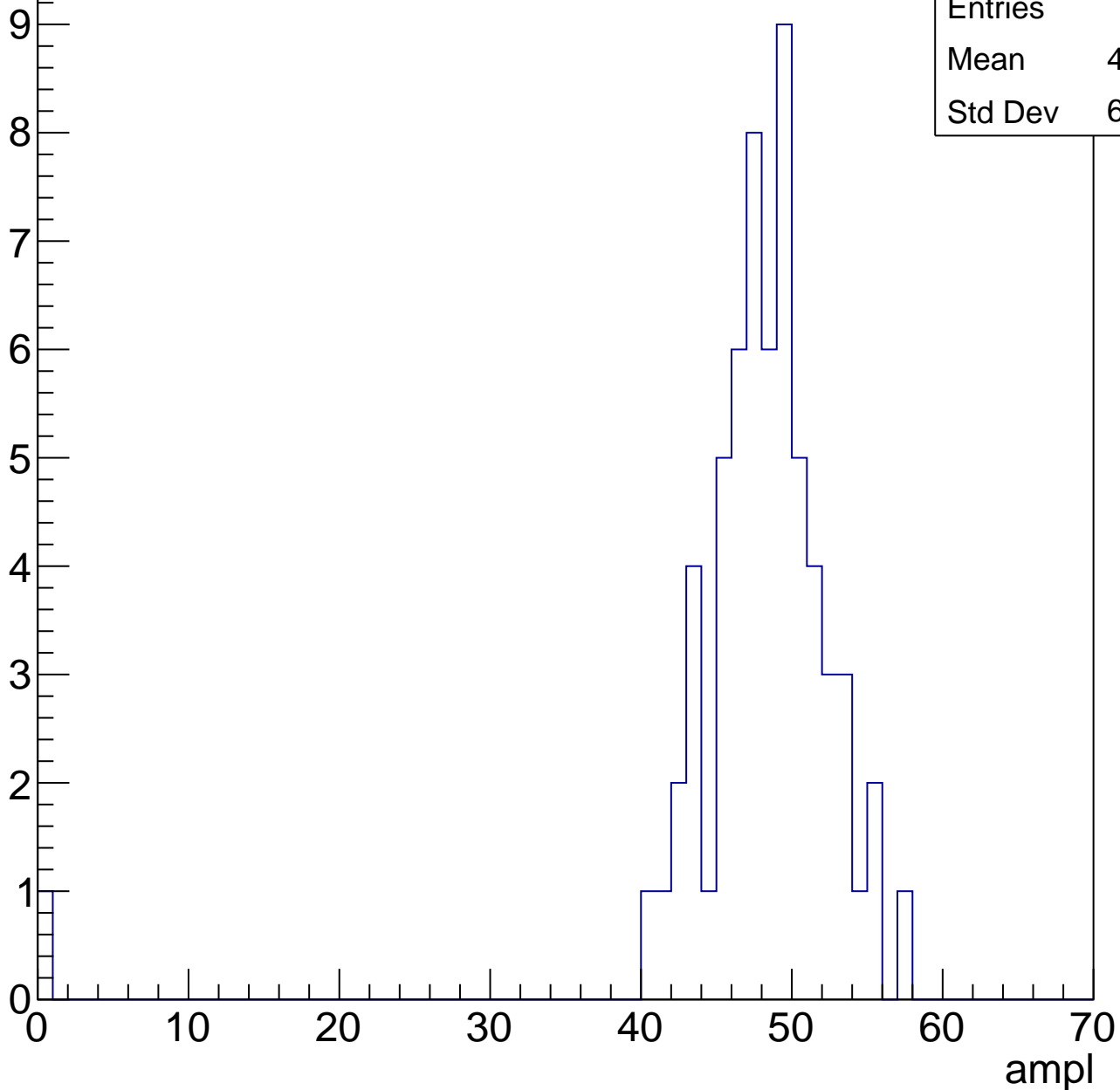


B1L103S, U13-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.25
Std Dev	6.958

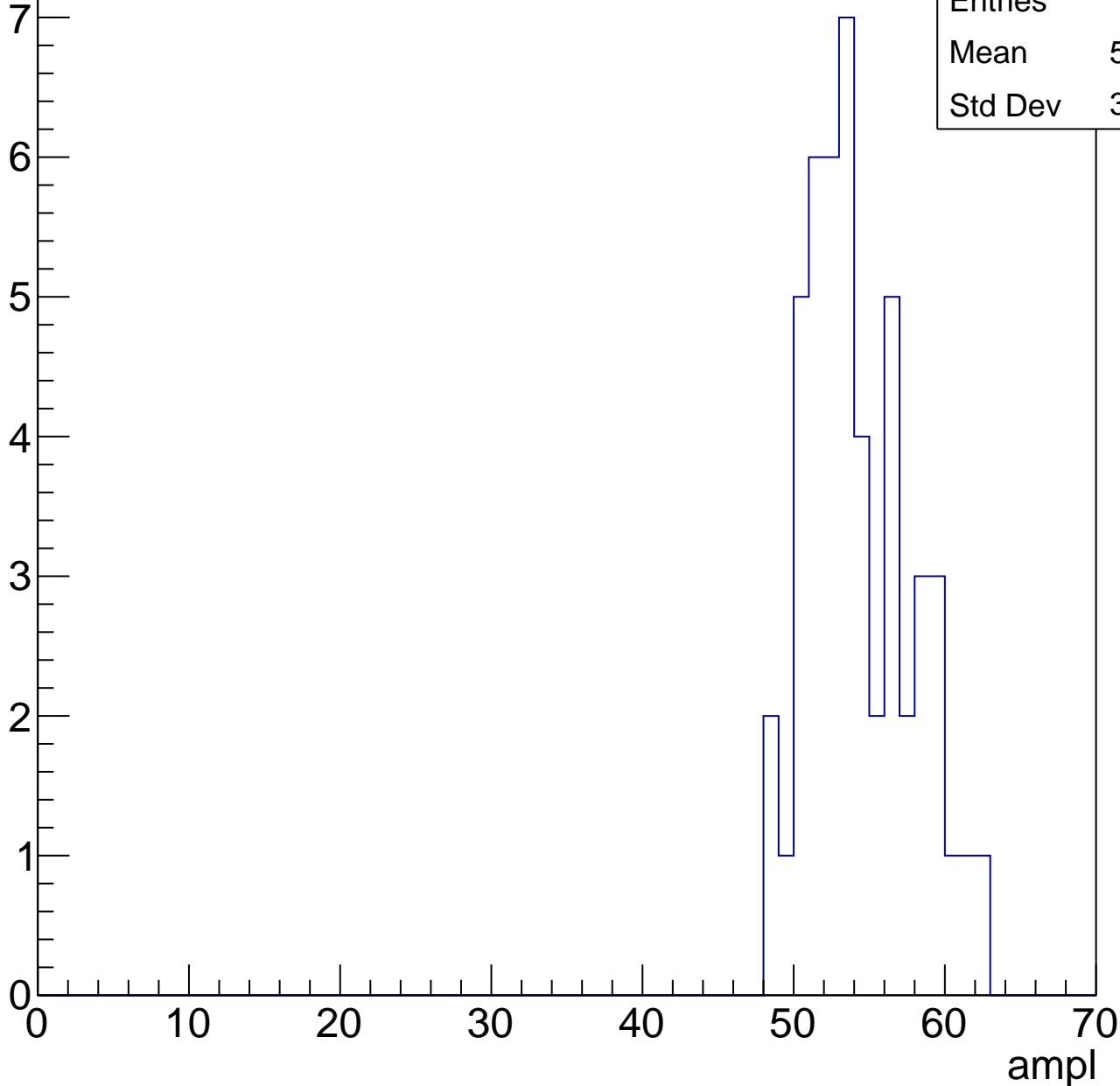


B1L103S, U13-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	53.84
Std Dev	3.419

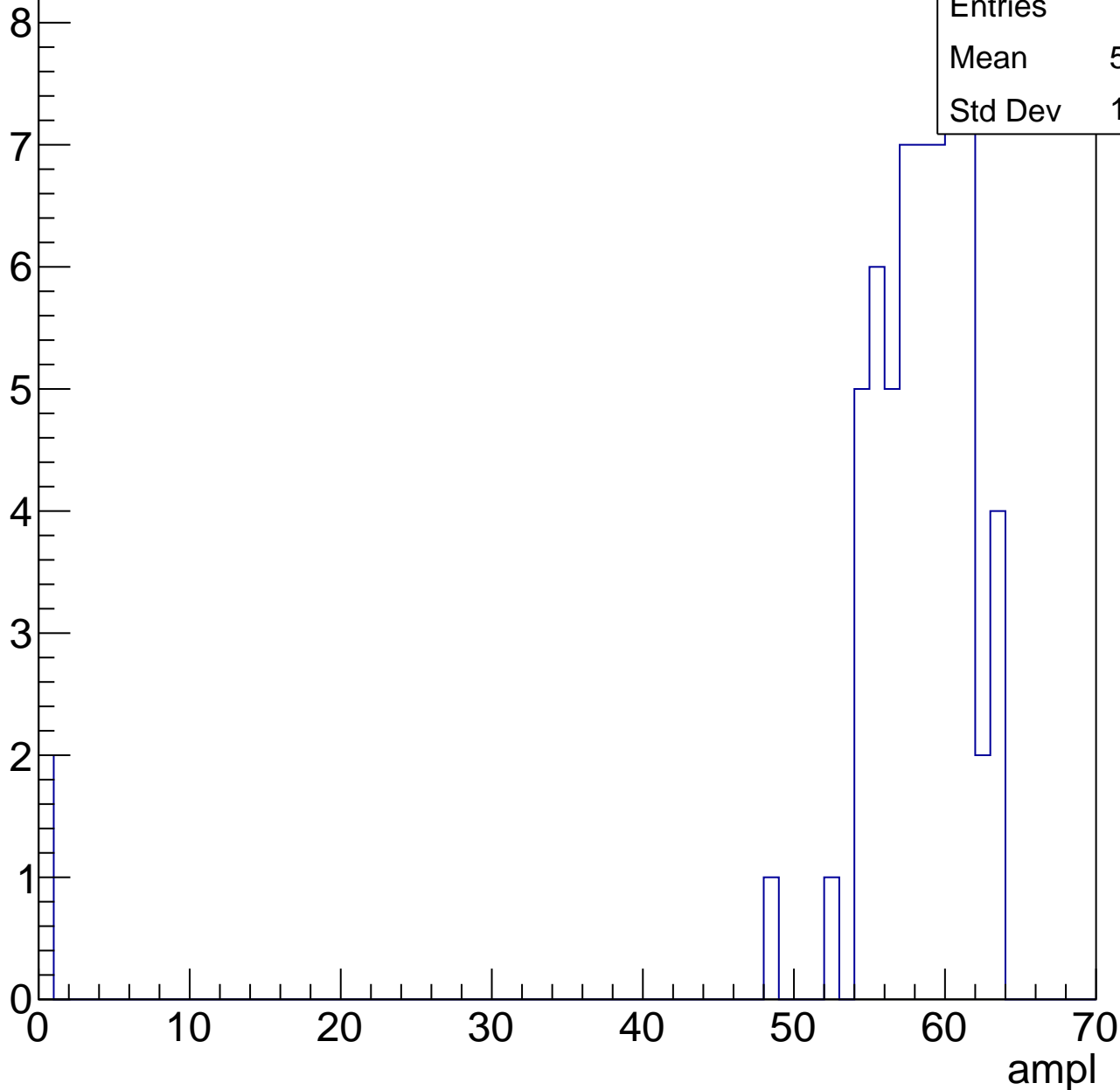


B1L103S, U13-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	56.22
Std Dev	10.59



B1L103S, U13-ch58, adc6

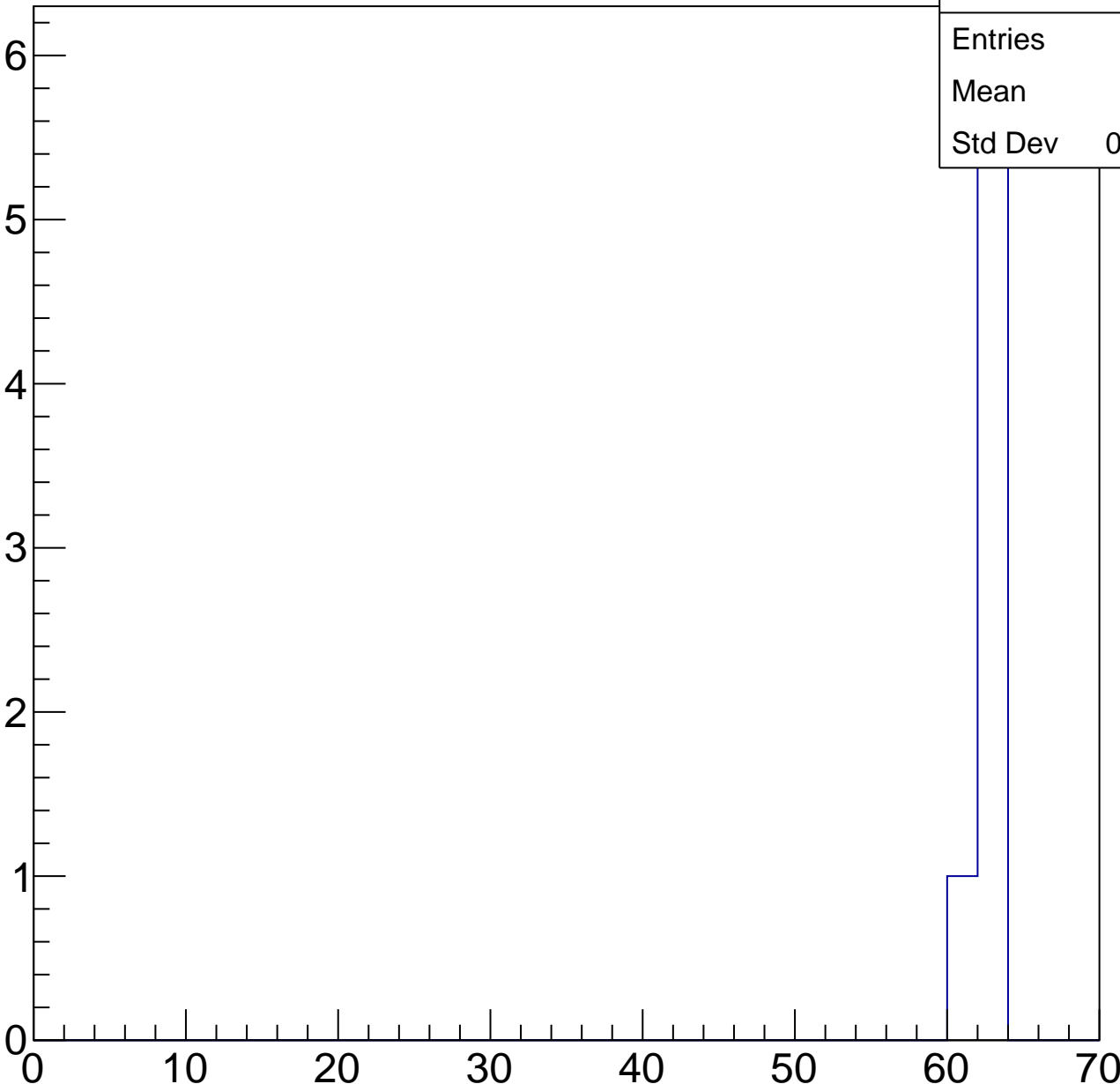
calib_packv5_041523_1651.root, FC#0, port C2

Entry

6
5
4
3
2
1
0

Entries	14
Mean	62.21
Std Dev	0.8601

ampl



B1L103S, U13-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U13-ch59, adc0

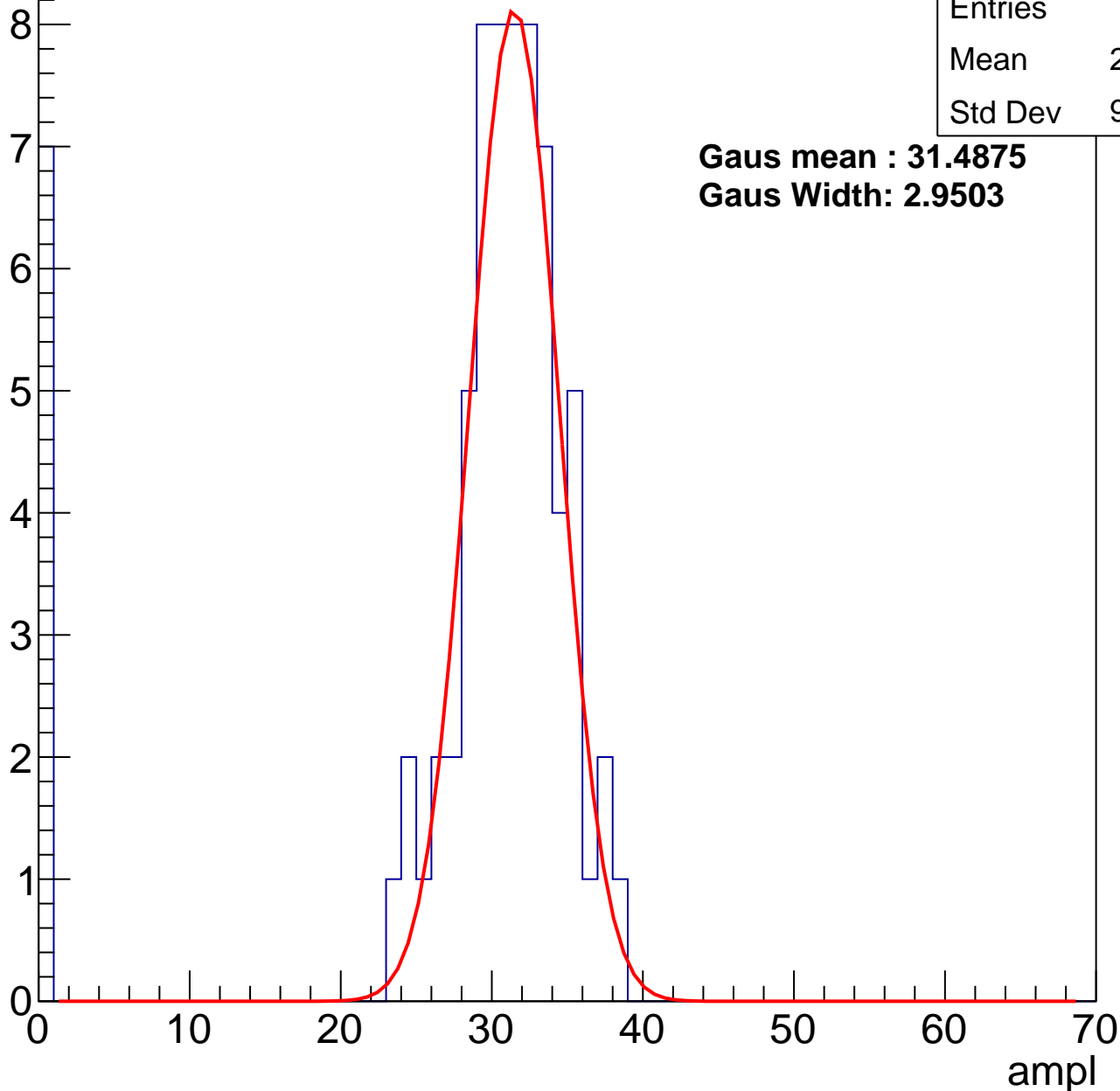
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	27.89
Std Dev	9.642

Gaus mean : 31.4875

Gaus Width: 2.9503



B1L103S, U13-ch59, adc1

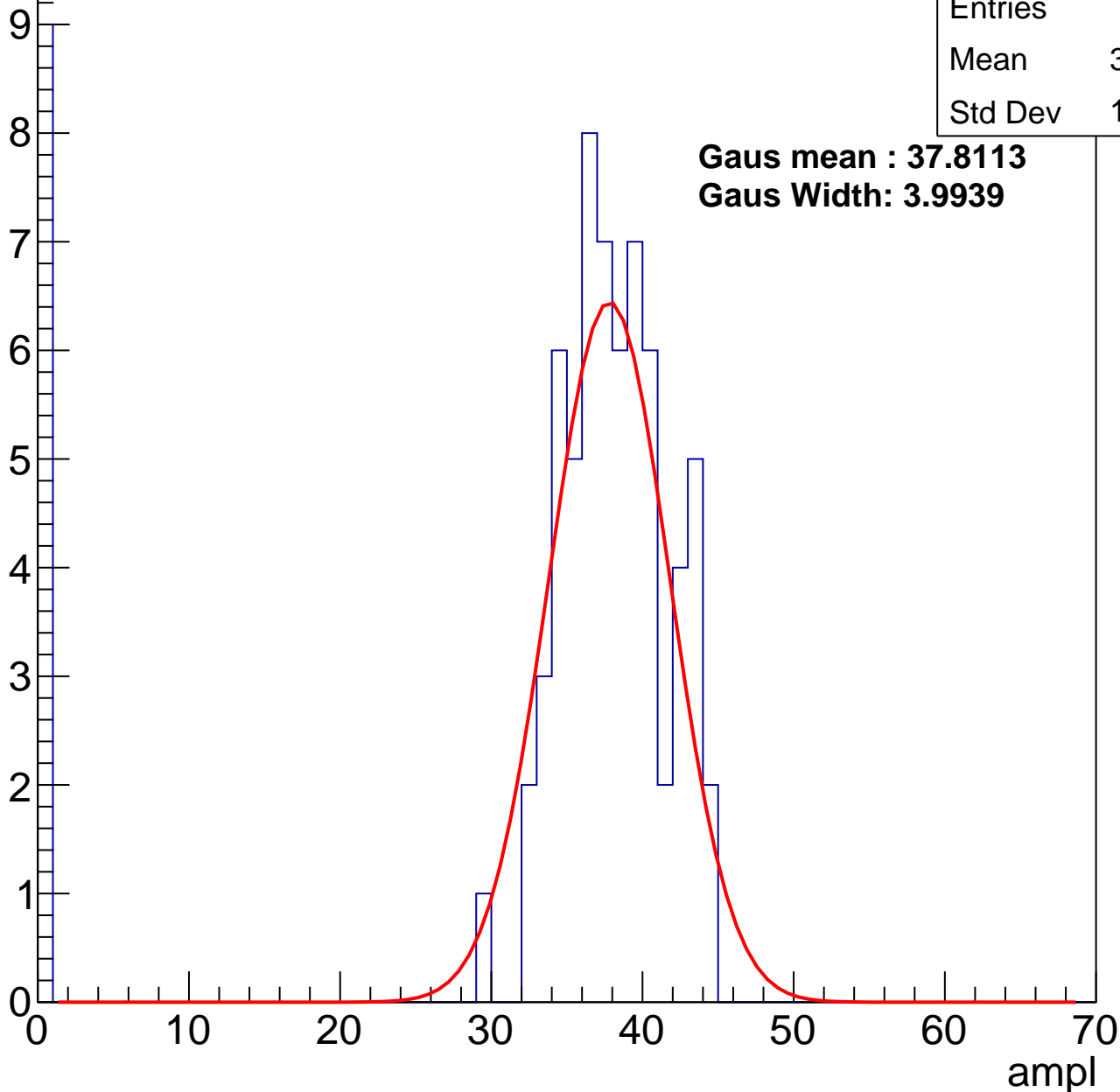
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	33.04
Std Dev	12.78

Gaus mean : 37.8113

Gaus Width: 3.9939



B1L103S, U13-ch59, adc2

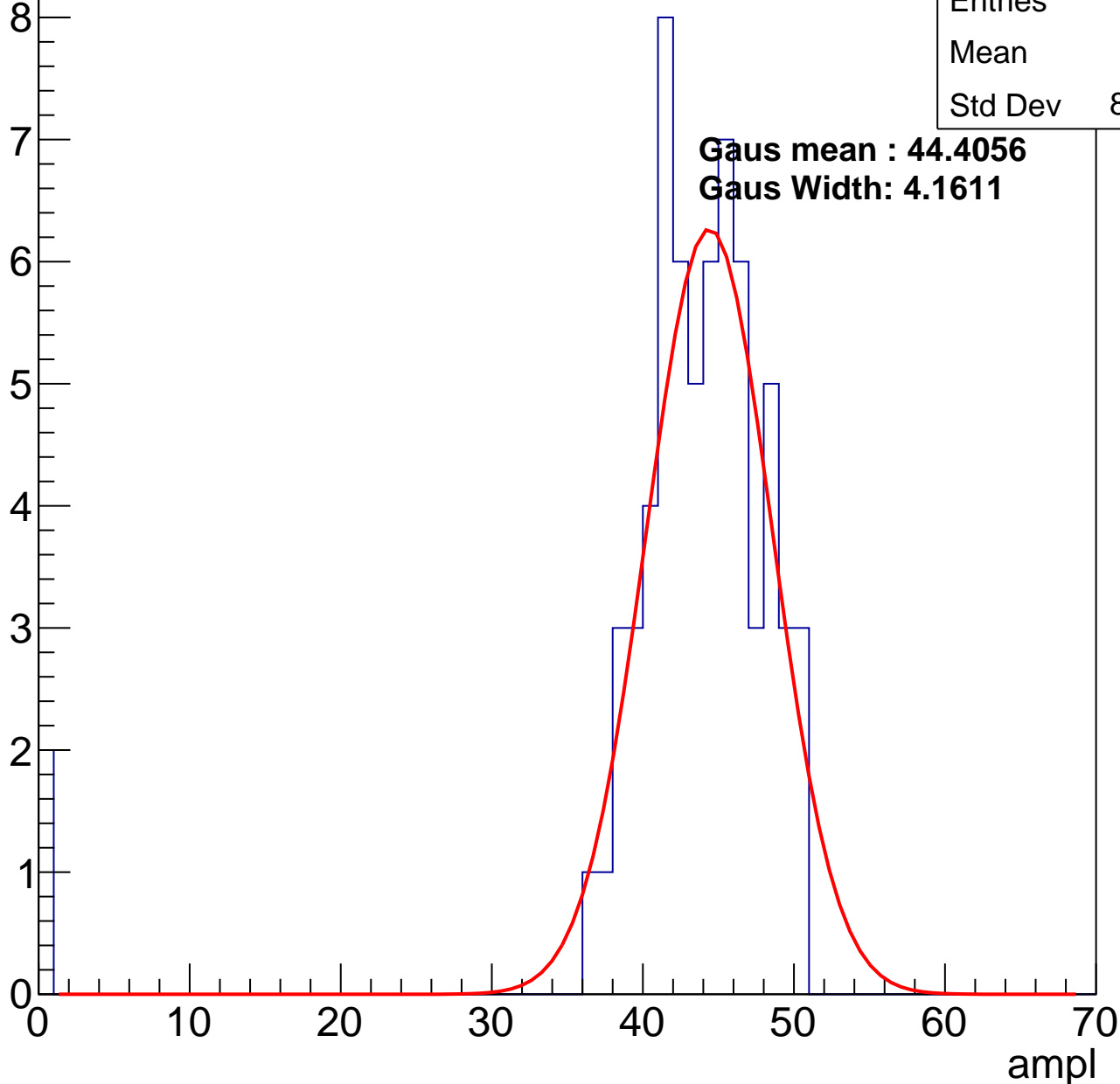
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.3
Std Dev	8.224

Gaus mean : 44.4056

Gaus Width: 4.1611

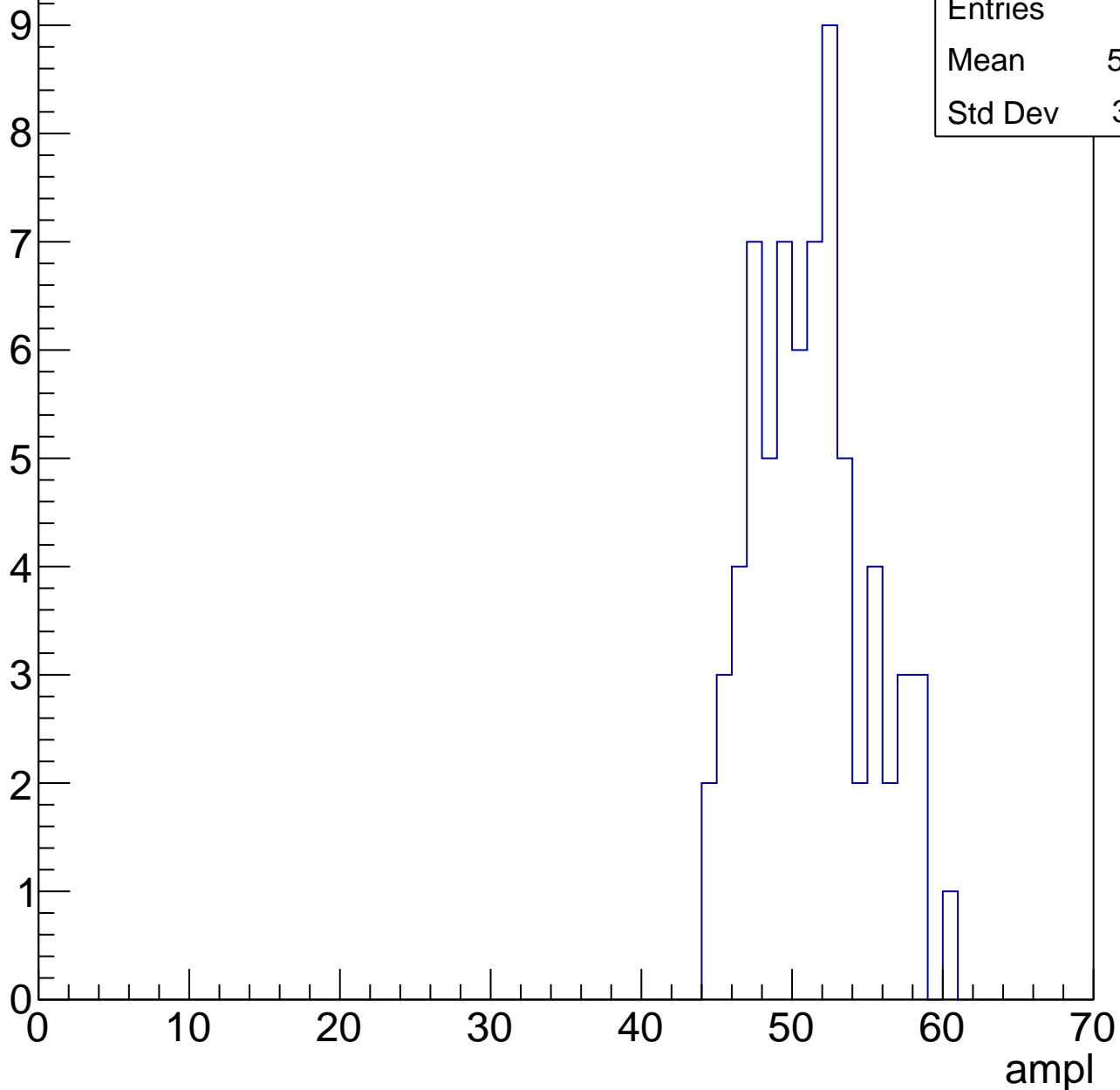


B1L103S, U13-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	50.77
Std Dev	3.761

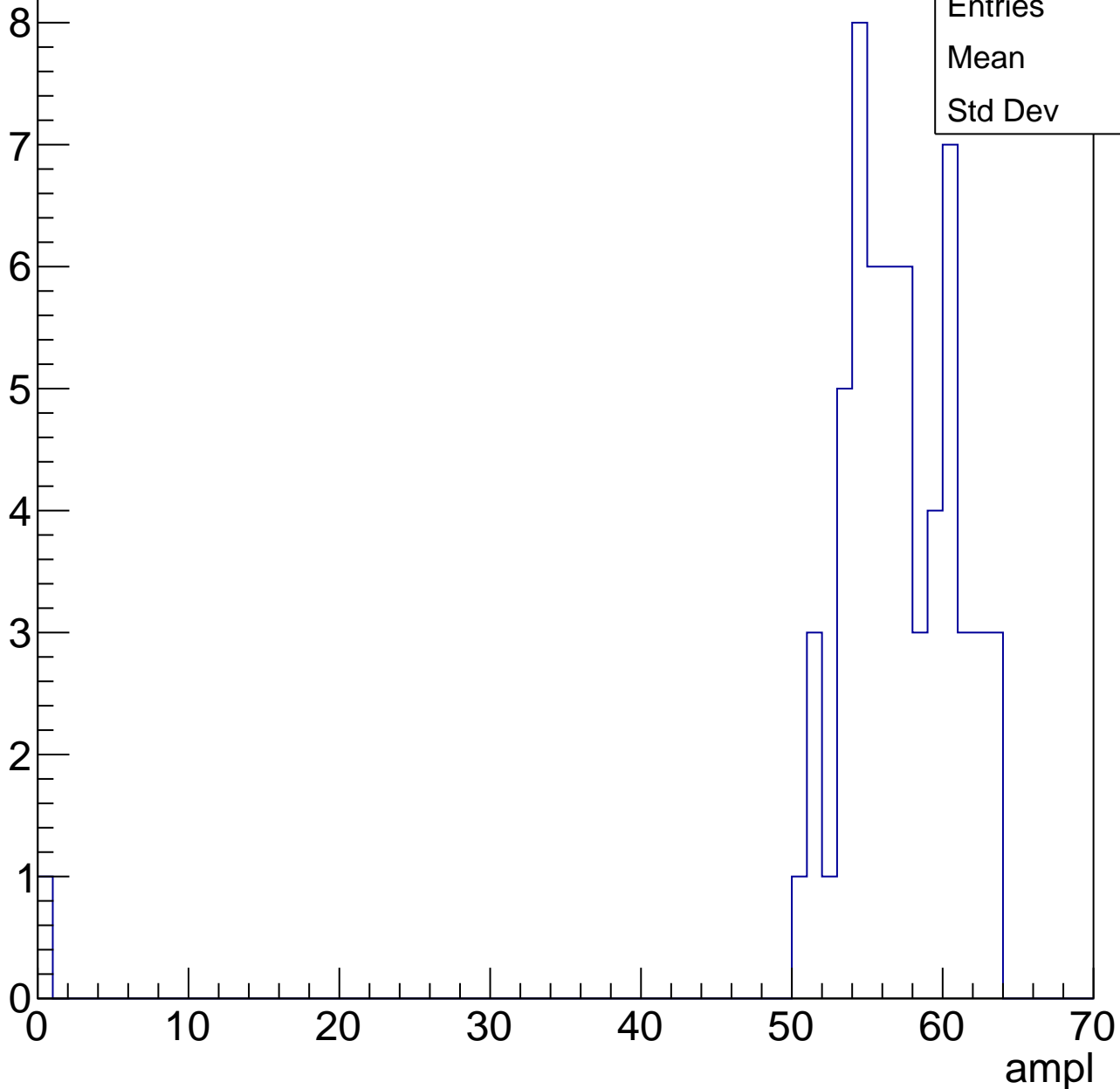


B1L103S, U13-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

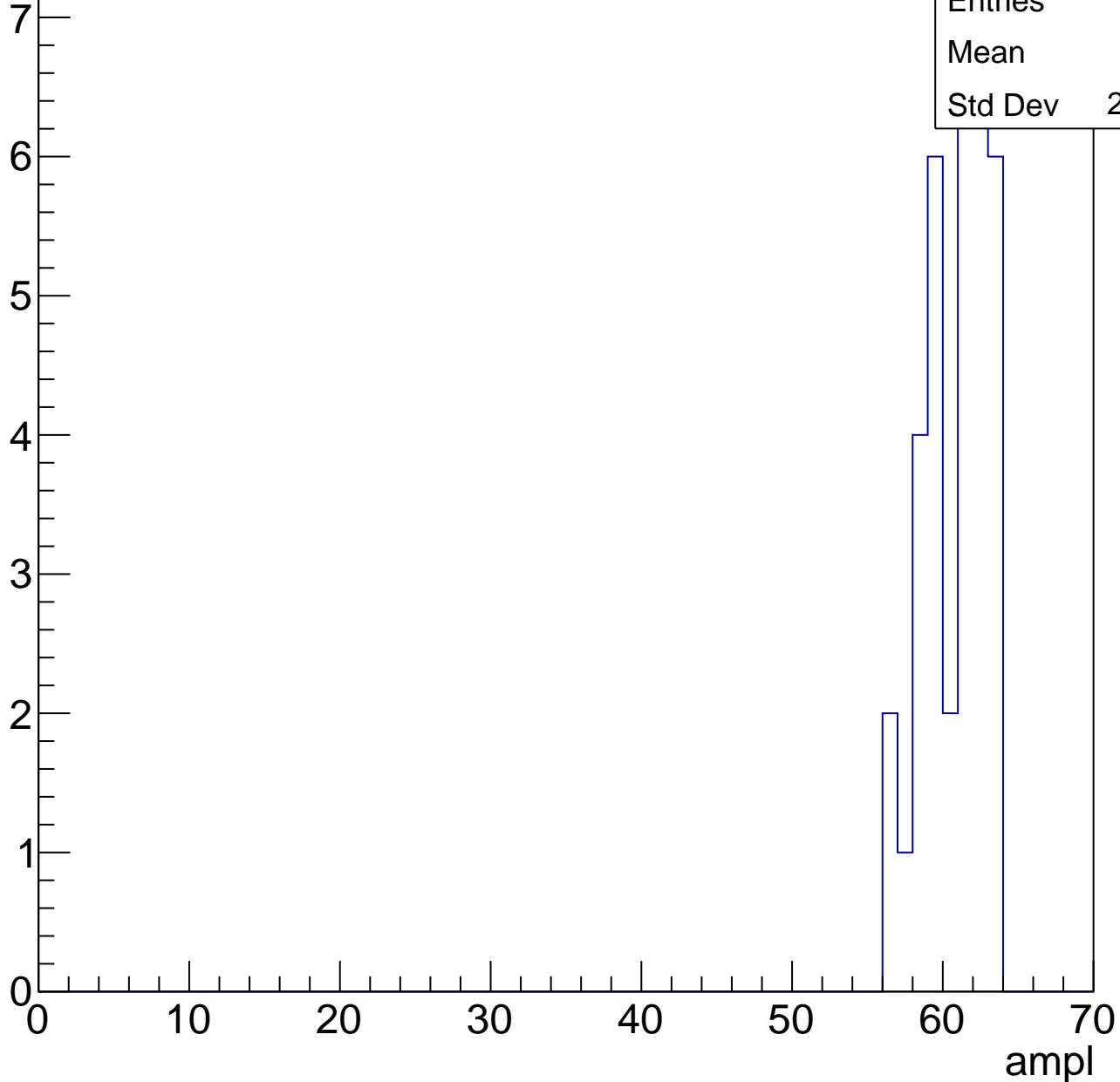
Entries	60
Mean	55.8
Std Dev	8



B1L103S, U13-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	35
Mean	60.4
Std Dev	2.045

B1L103S, U13-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch60, adc0

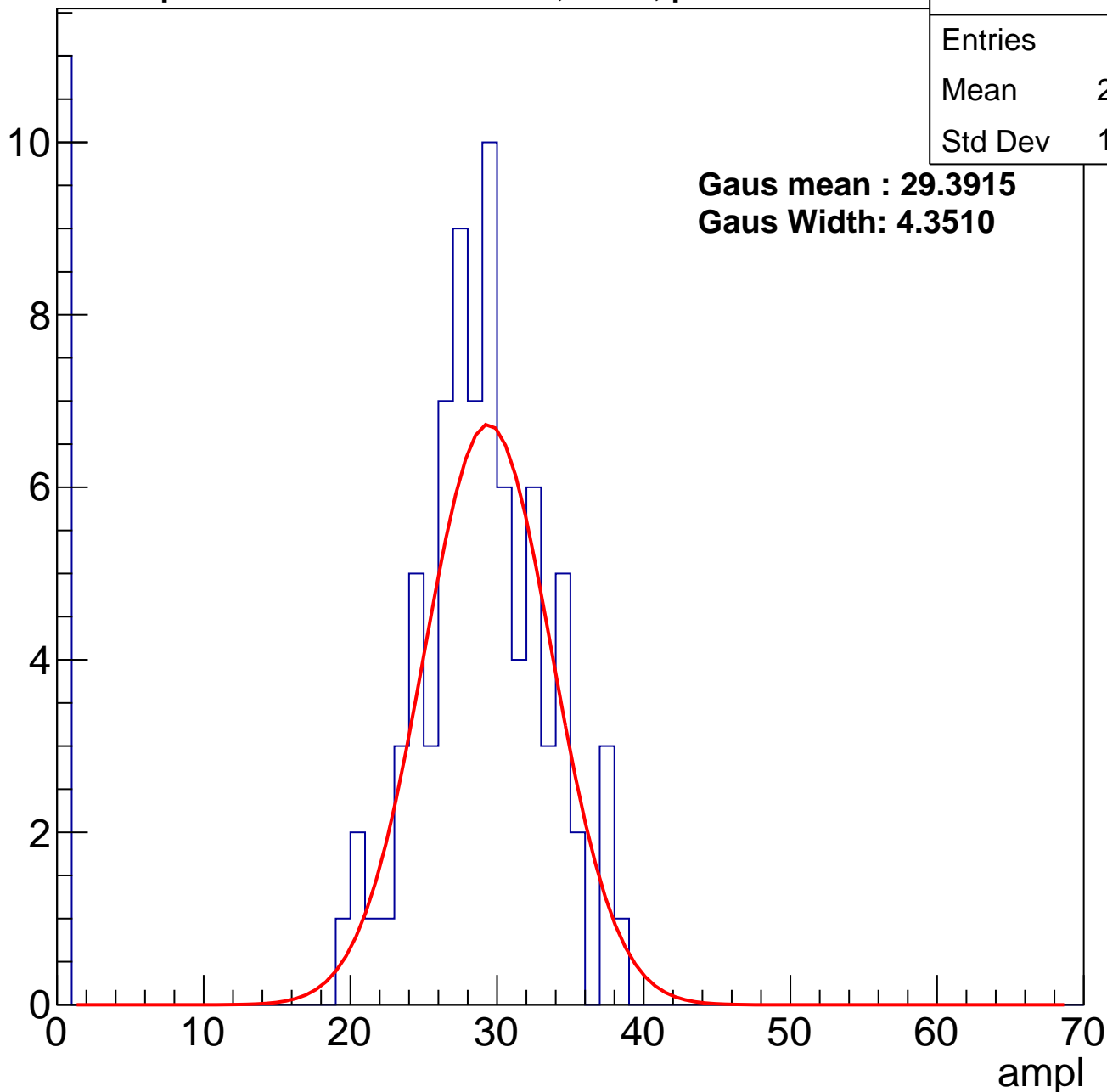
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	25.12
Std Dev	10.14

Gaus mean : 29.3915

Gaus Width: 4.3510

Entry



B1L103S, U13-ch60, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	33.01
Std Dev	10.05

Gaus mean : 35.7426

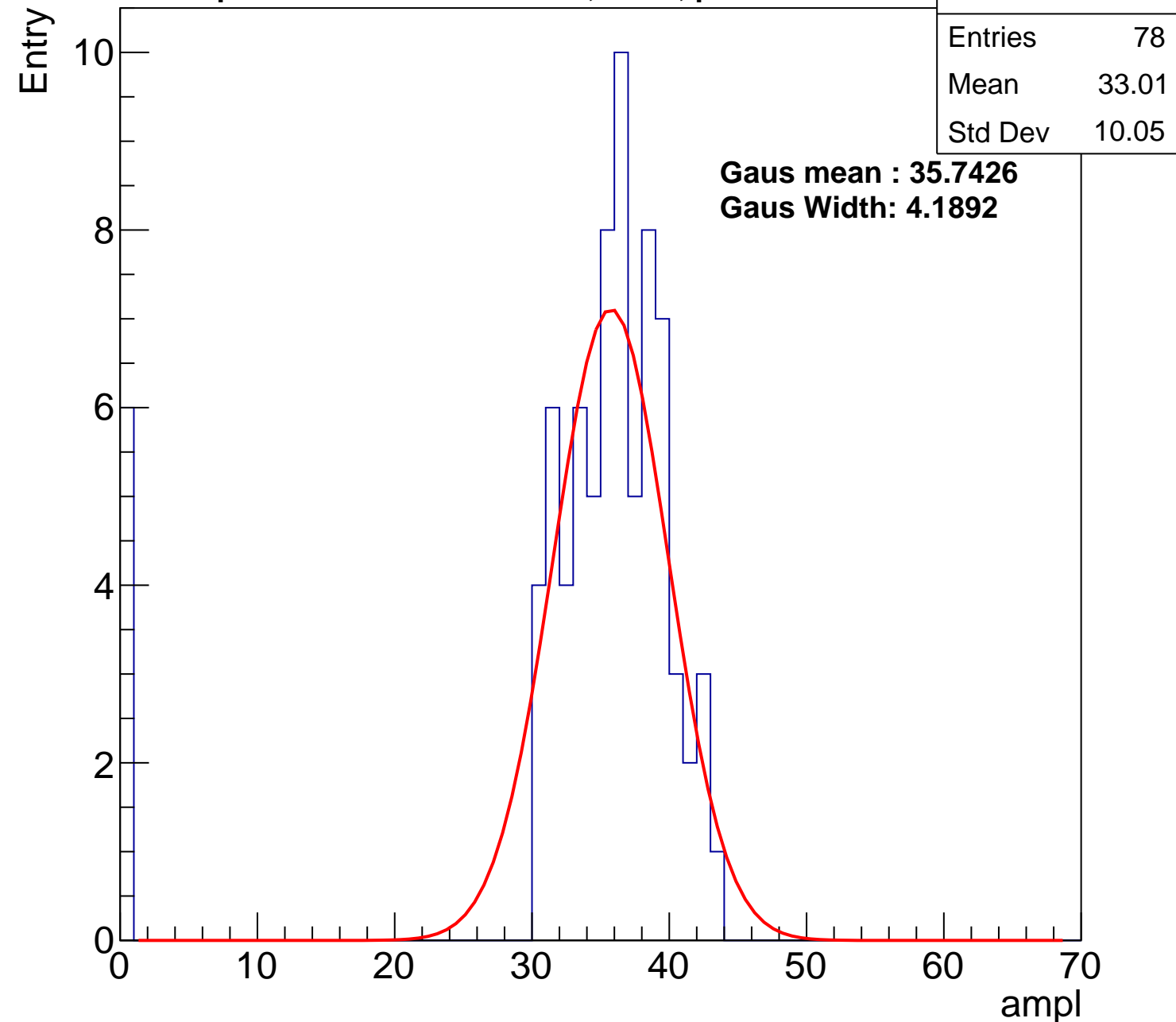
Gaus Width: 4.1892

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch60, adc2

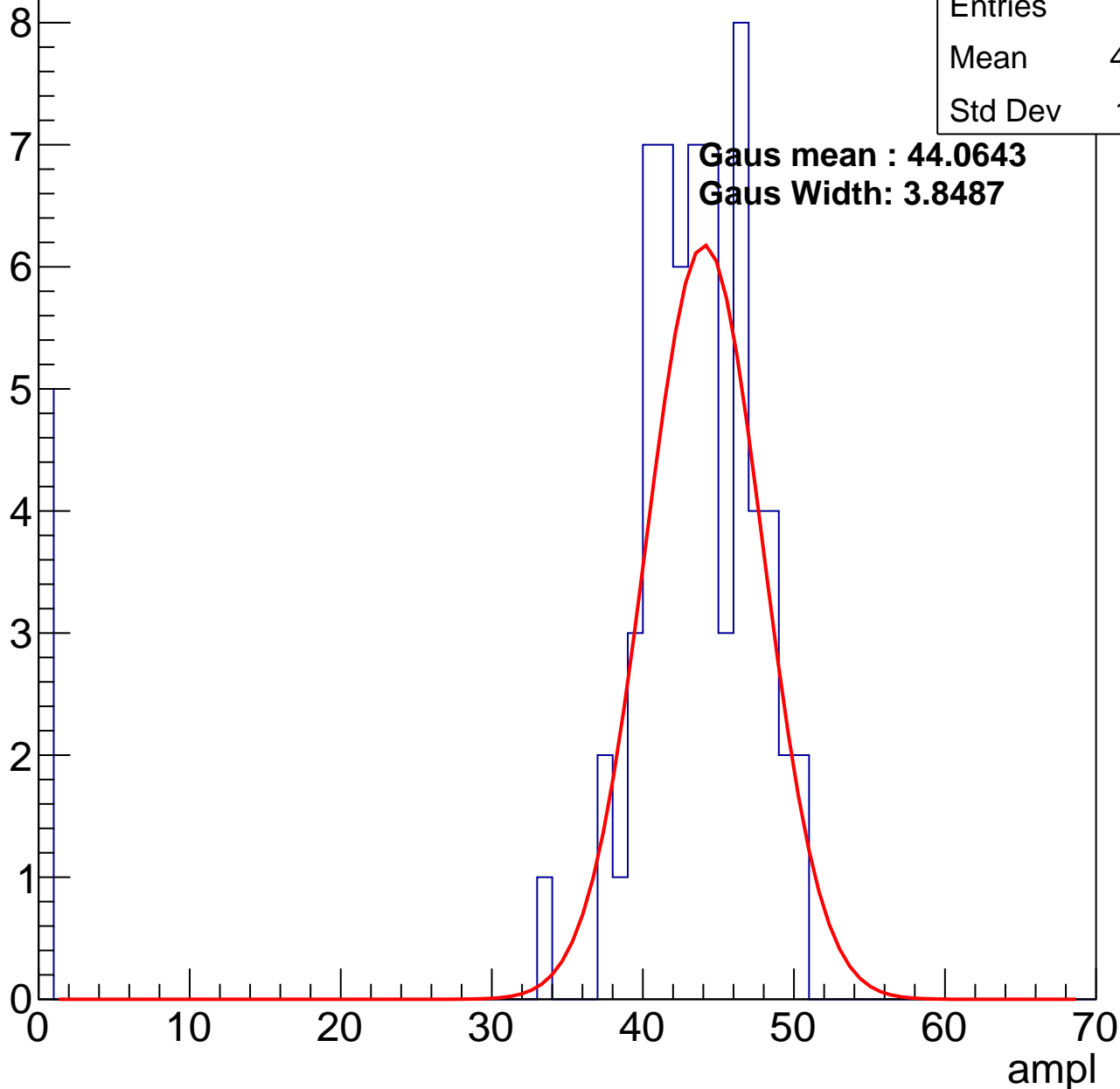
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.16
Std Dev	11.71

Gaus mean : 44.0643

Gaus Width: 3.8487

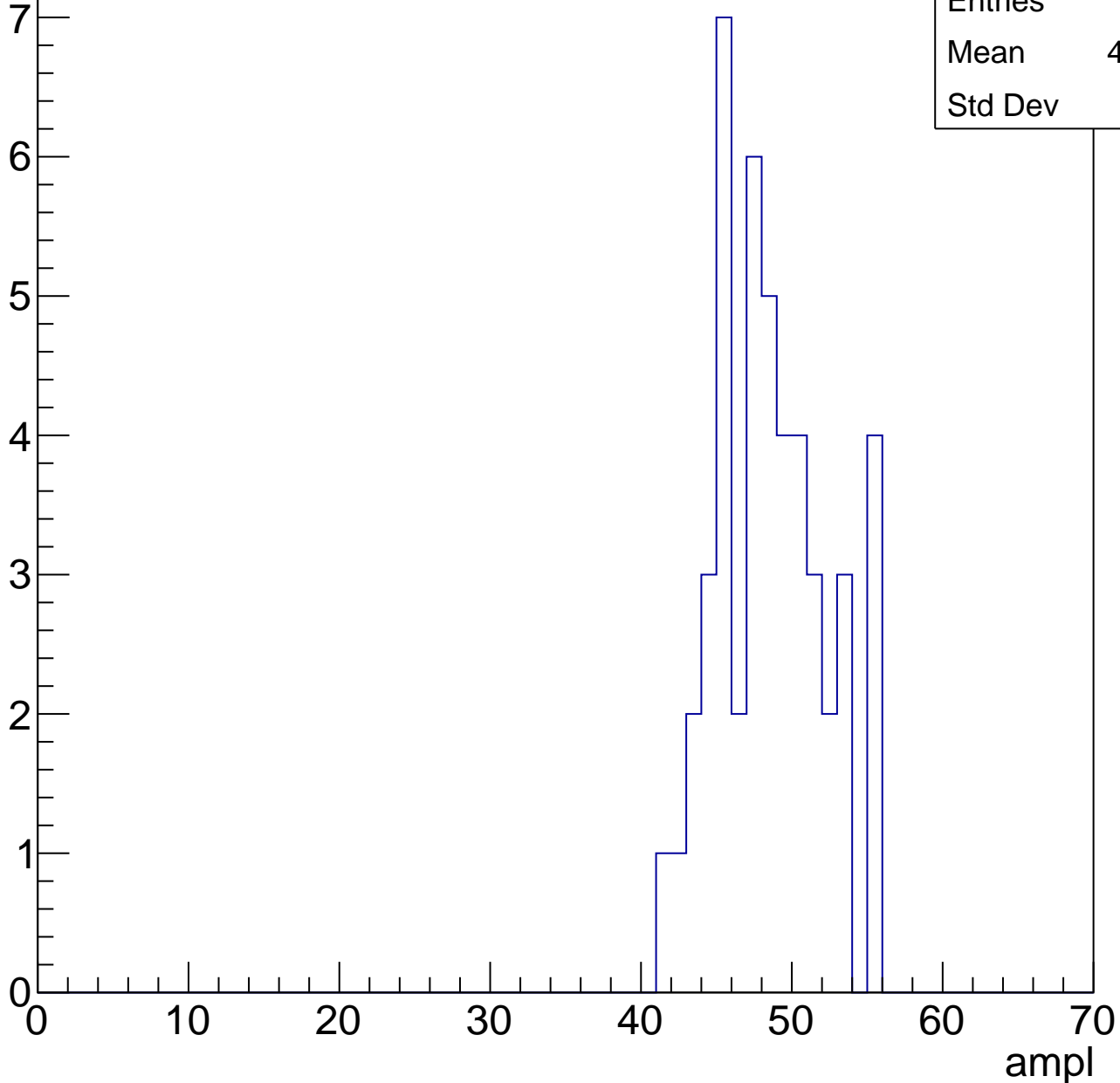


B1L103S, U13-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

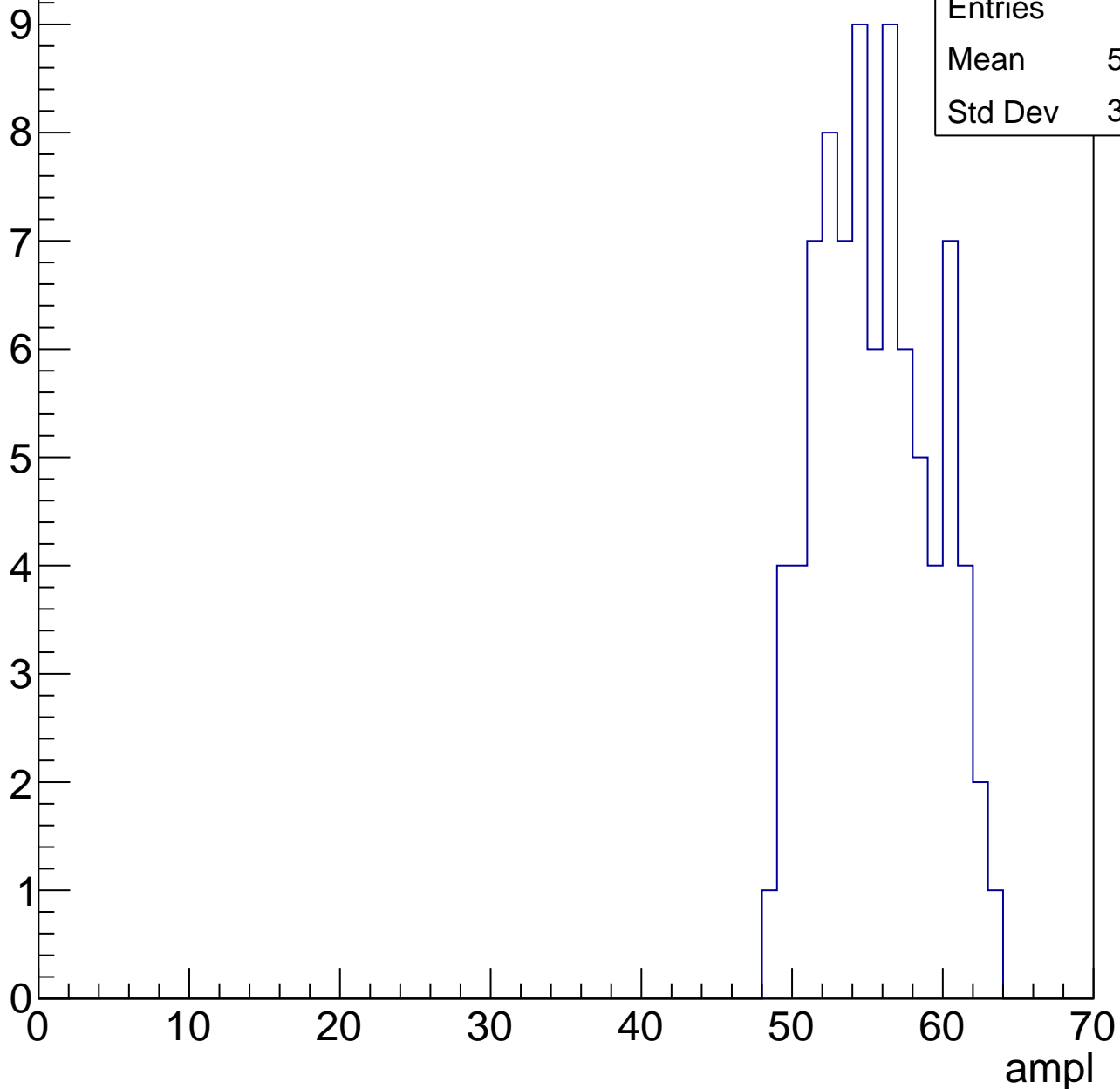
Entries	47
Mean	48.13
Std Dev	3.6



B1L103S, U13-ch60, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

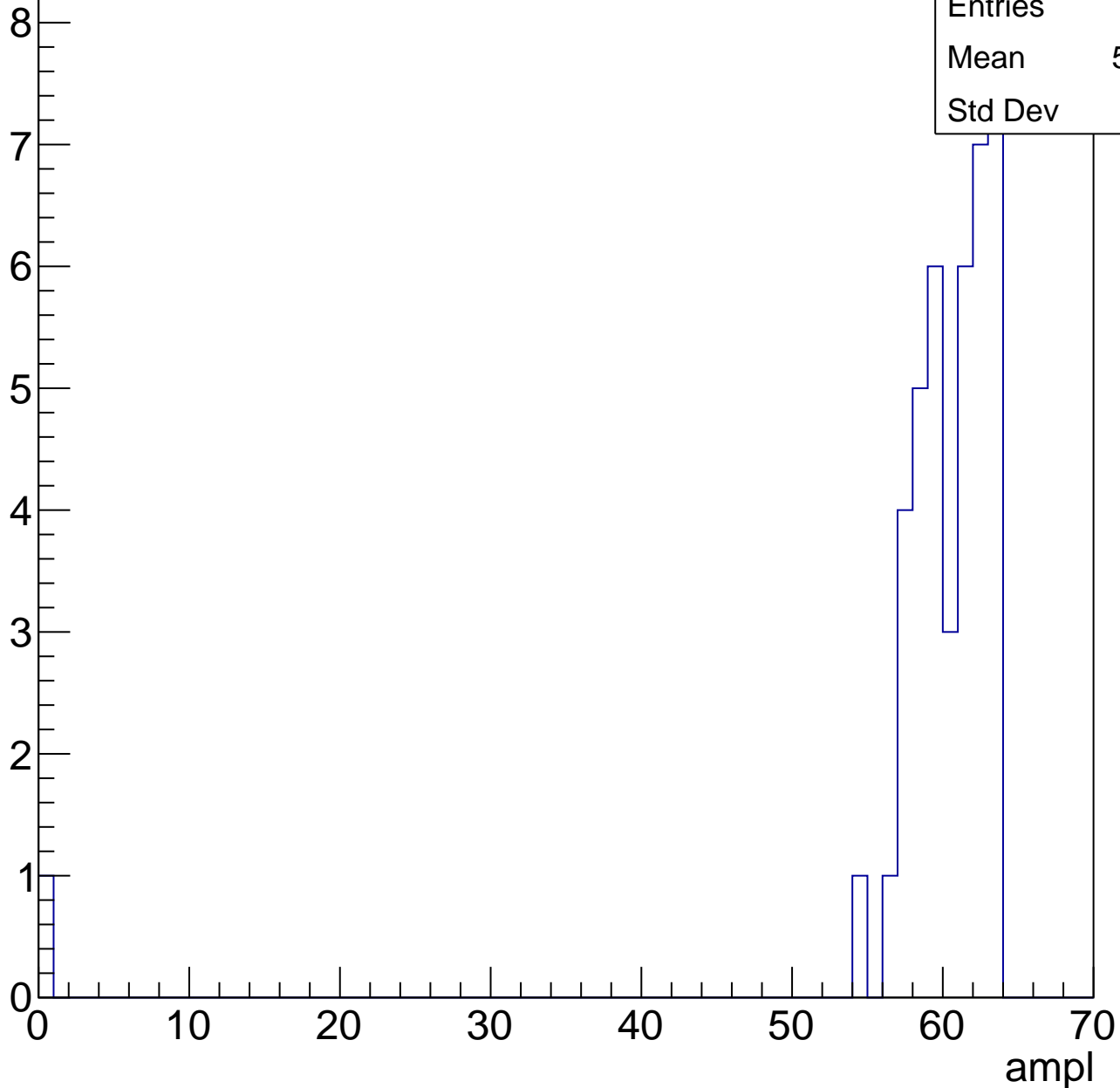


B1L103S, U13-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.71
Std Dev	9.45



B1L103S, U13-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch61, adc0

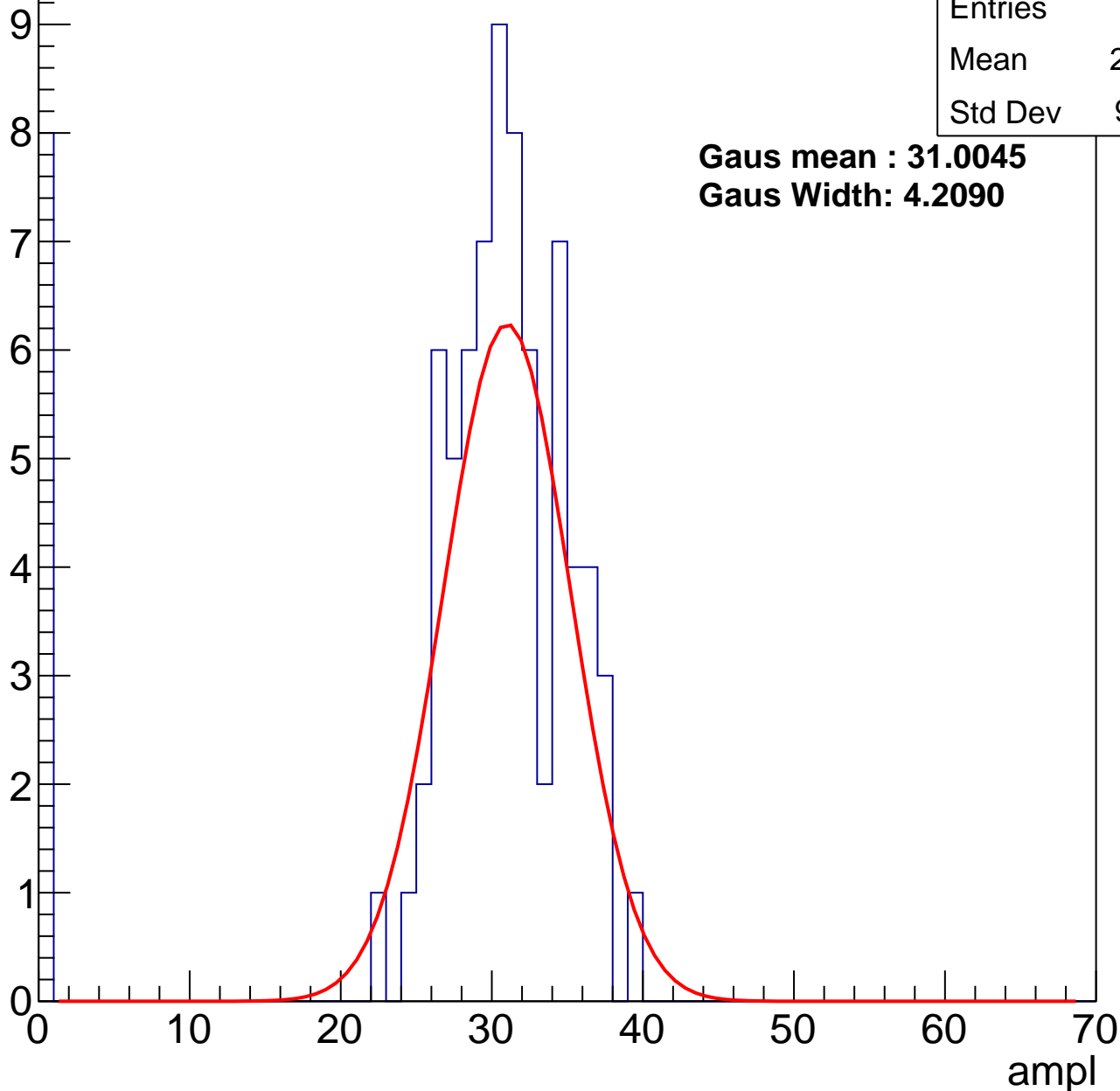
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	27.57
Std Dev	9.801

Gaus mean : 31.0045

Gaus Width: 4.2090



B1L103S, U13-ch61, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	36.35
Std Dev	5.576

Gaus mean : 37.6363

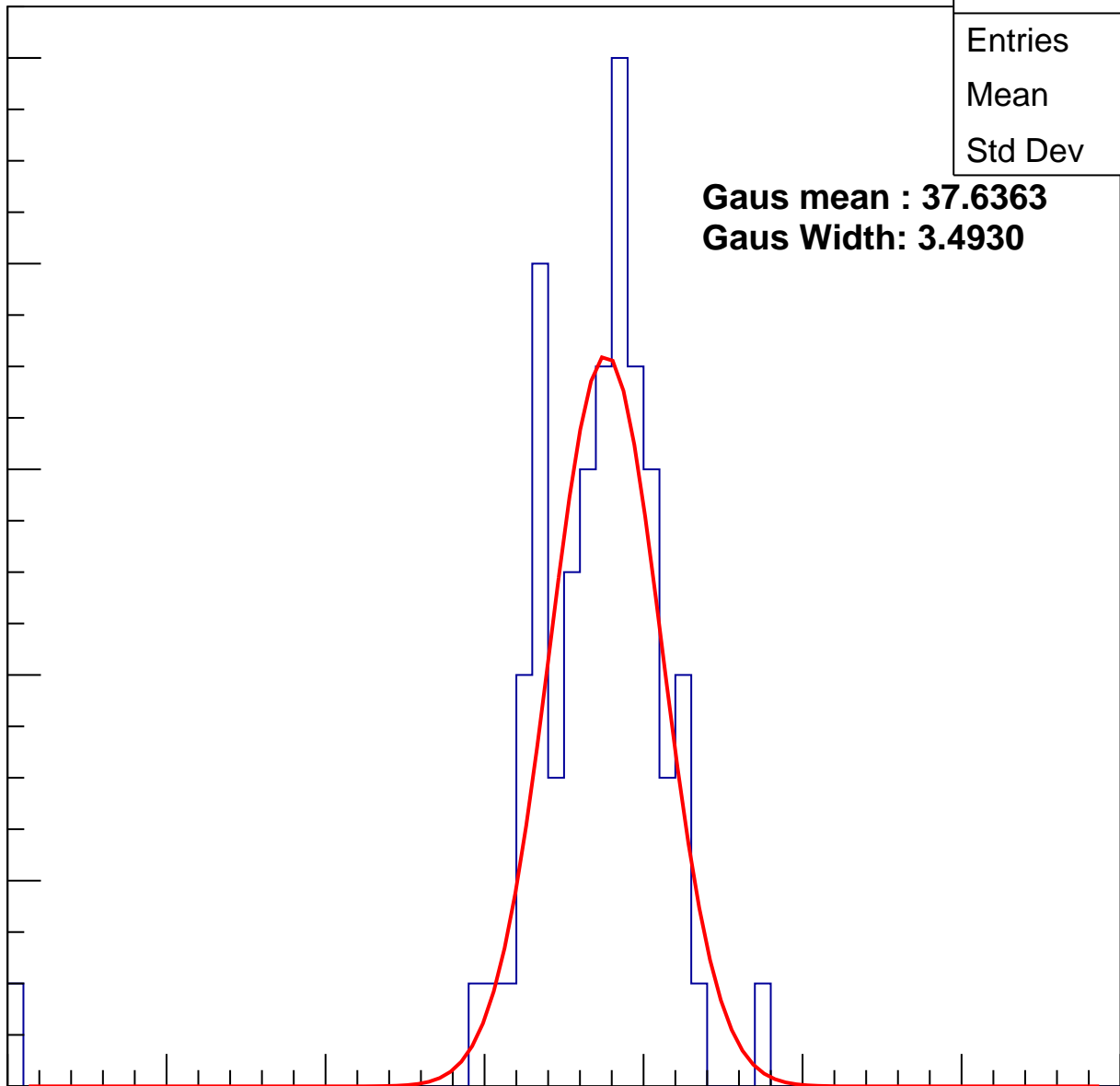
Gaus Width: 3.4930

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch61, adc2

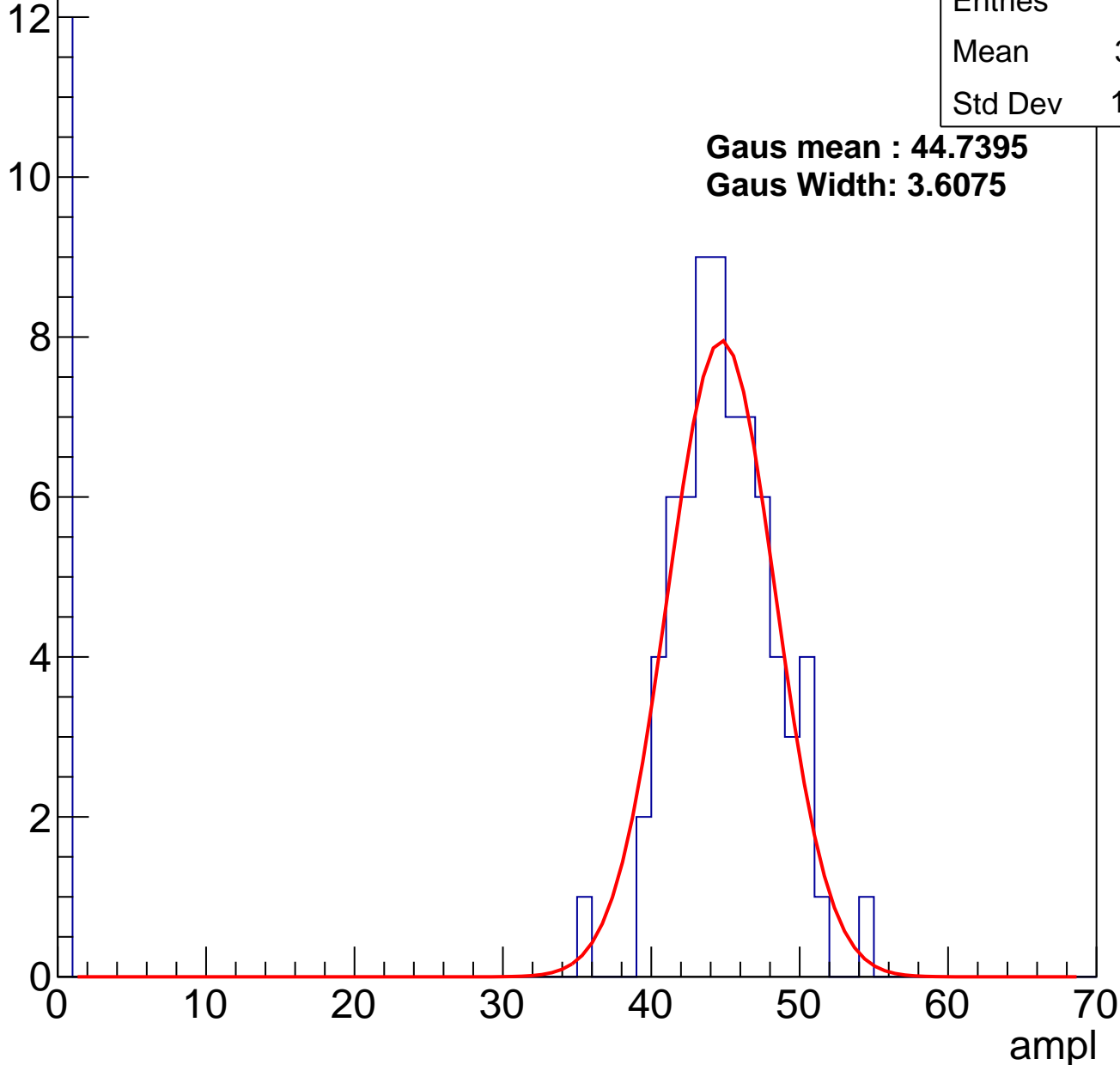
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	38.01
Std Dev	16.04

Gaus mean : 44.7395

Gaus Width: 3.6075

Entry

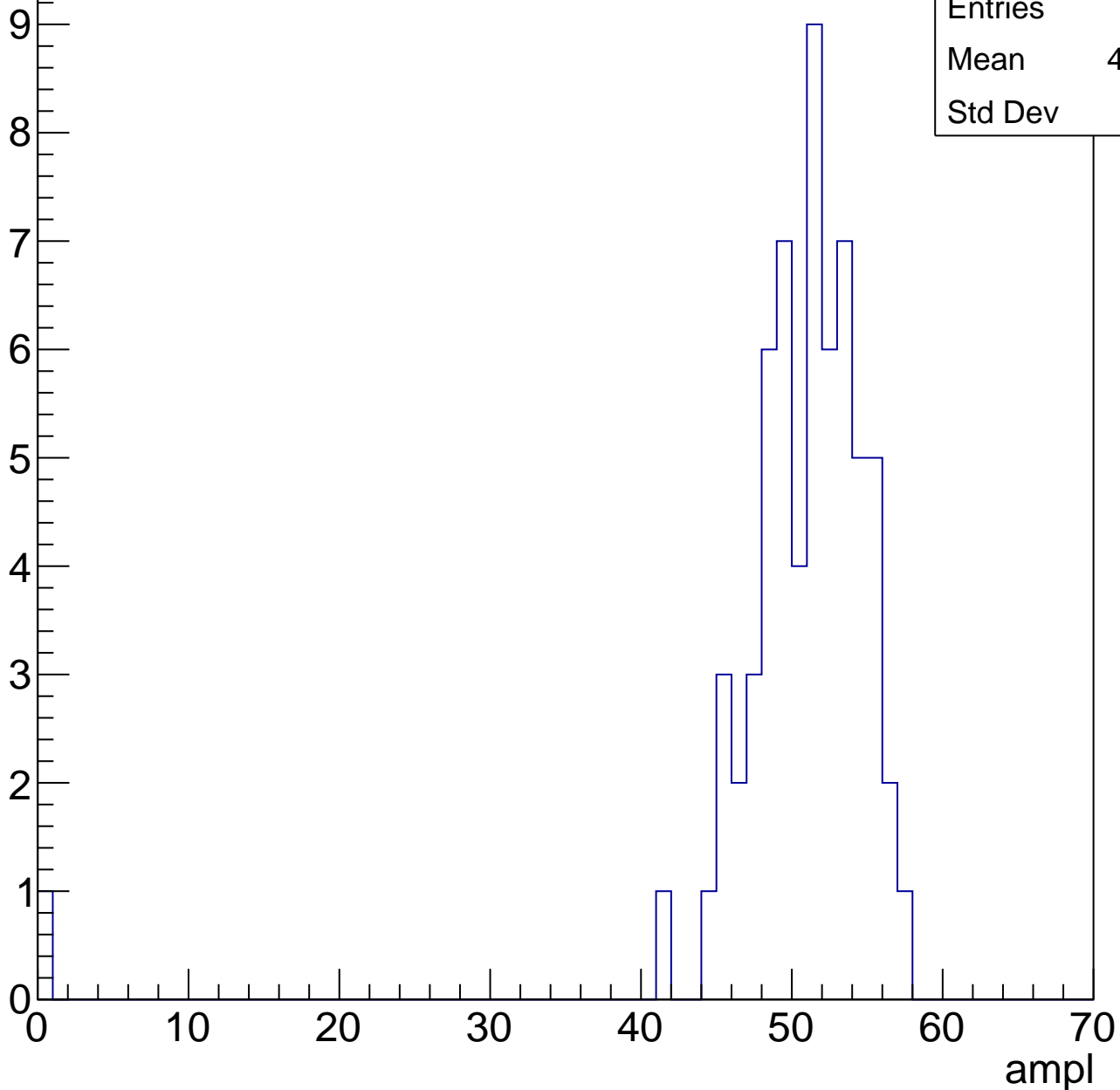


B1L103S, U13-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.84
Std Dev	7.13

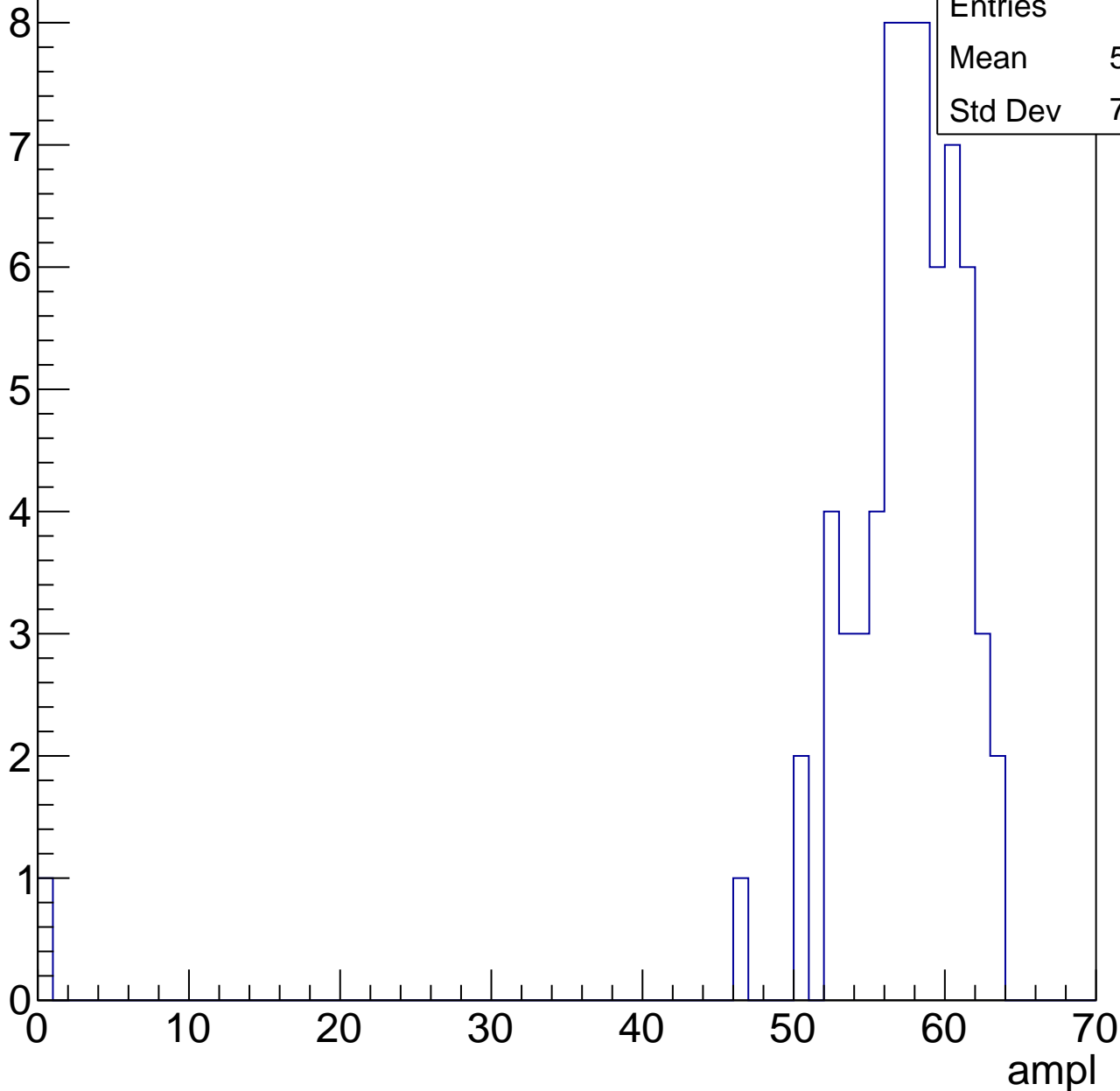


B1L103S, U13-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	56.29
Std Dev	7.757

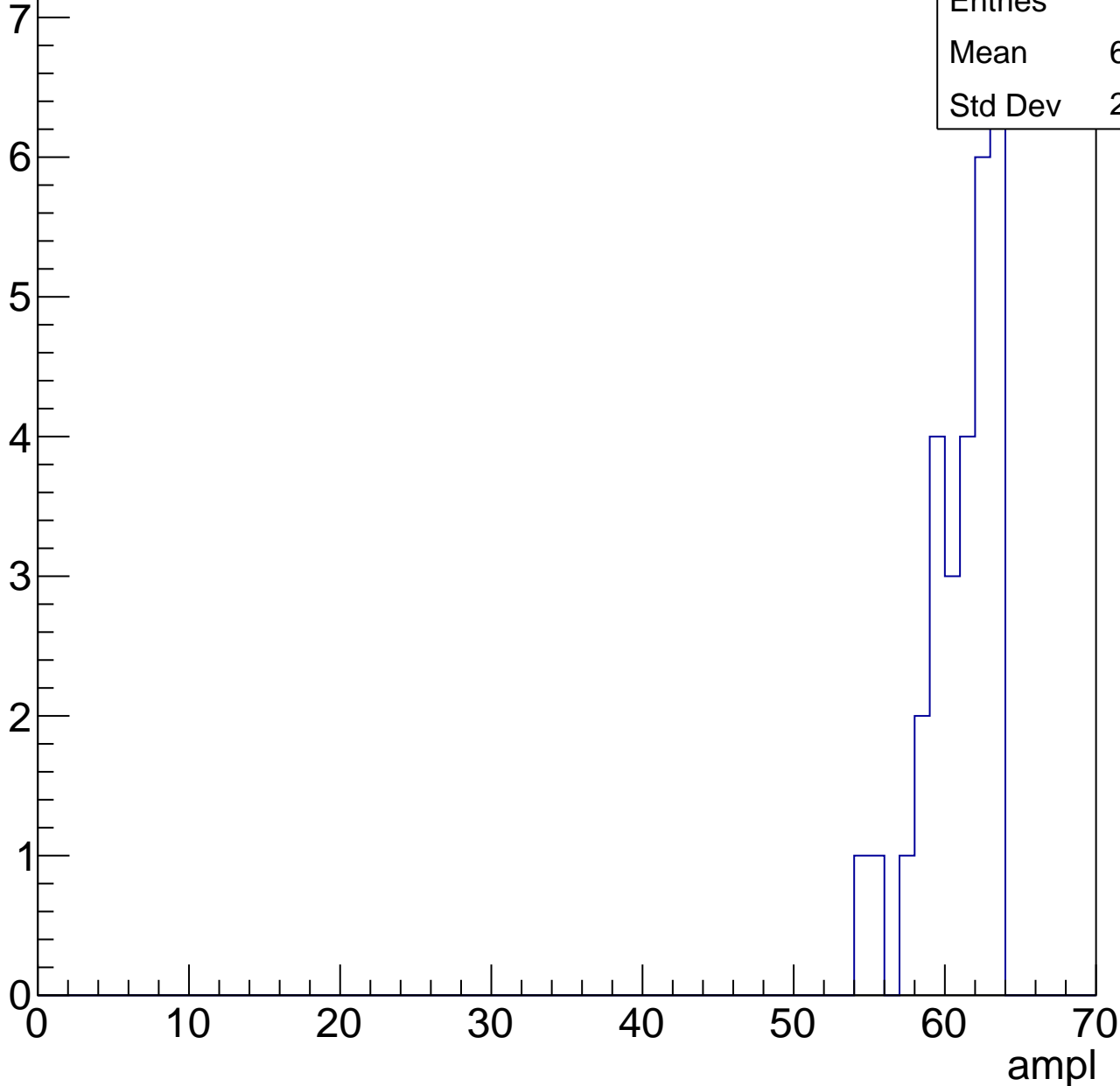


B1L103S, U13-ch61, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	60.52
Std Dev	2.387



B1L103S, U13-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch62, adc0

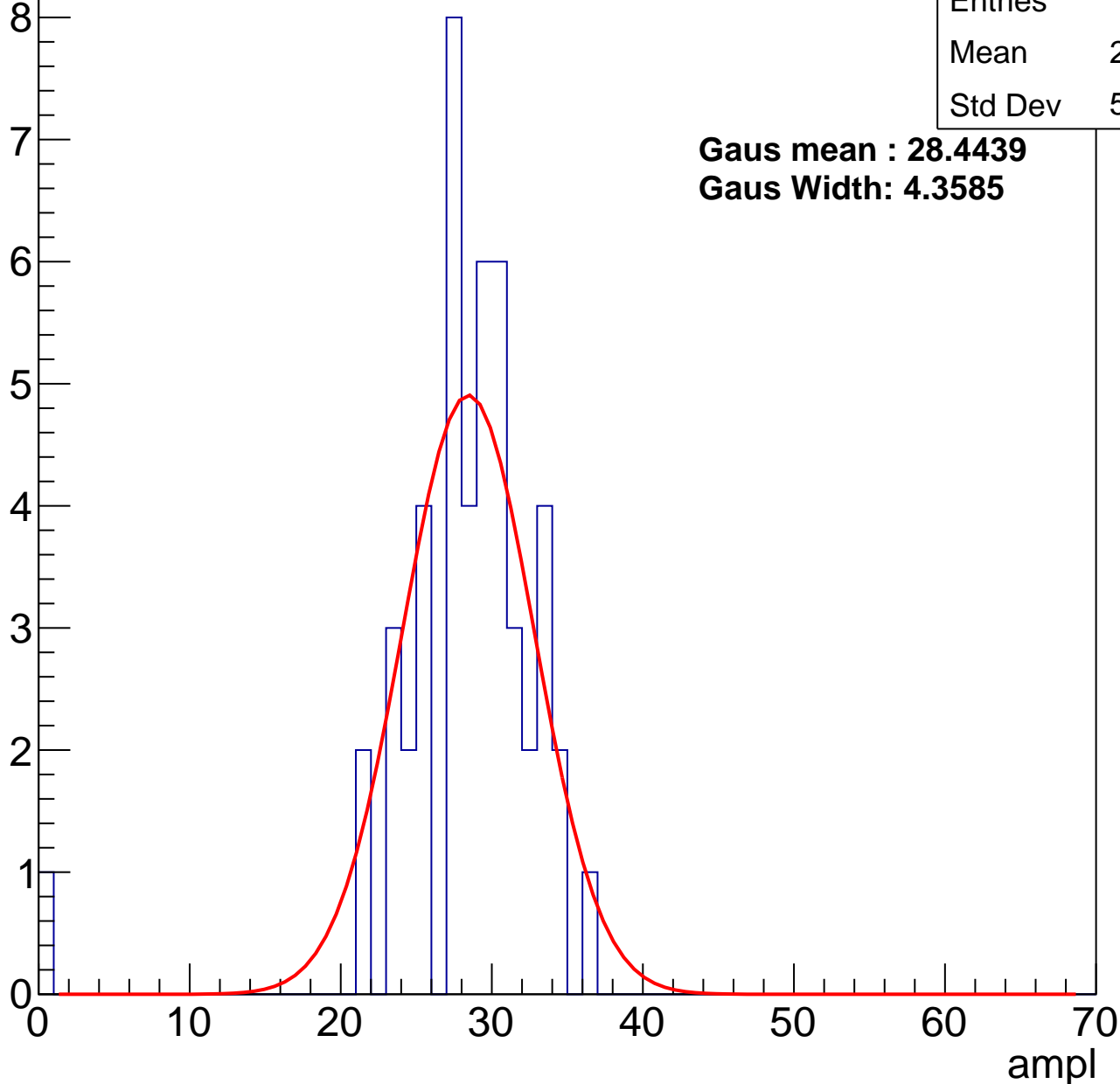
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	27.79
Std Dev	5.315

Gaus mean : 28.4439

Gaus Width: 4.3585



B1L103S, U13-ch62, adc1

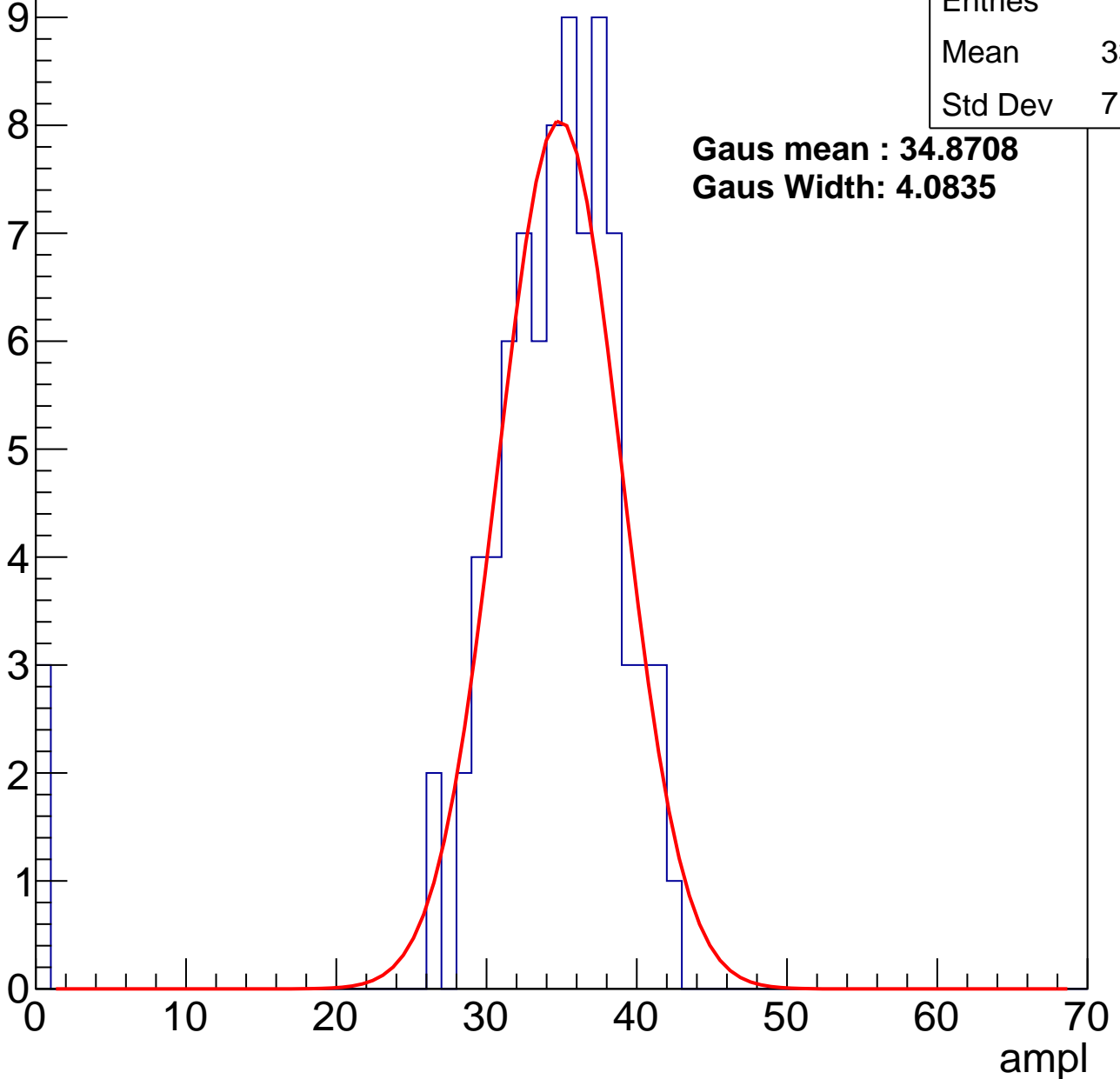
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	33.24
Std Dev	7.315

Gaus mean : 34.8708

Gaus Width: 4.0835



B1L103S, U13-ch62, adc2

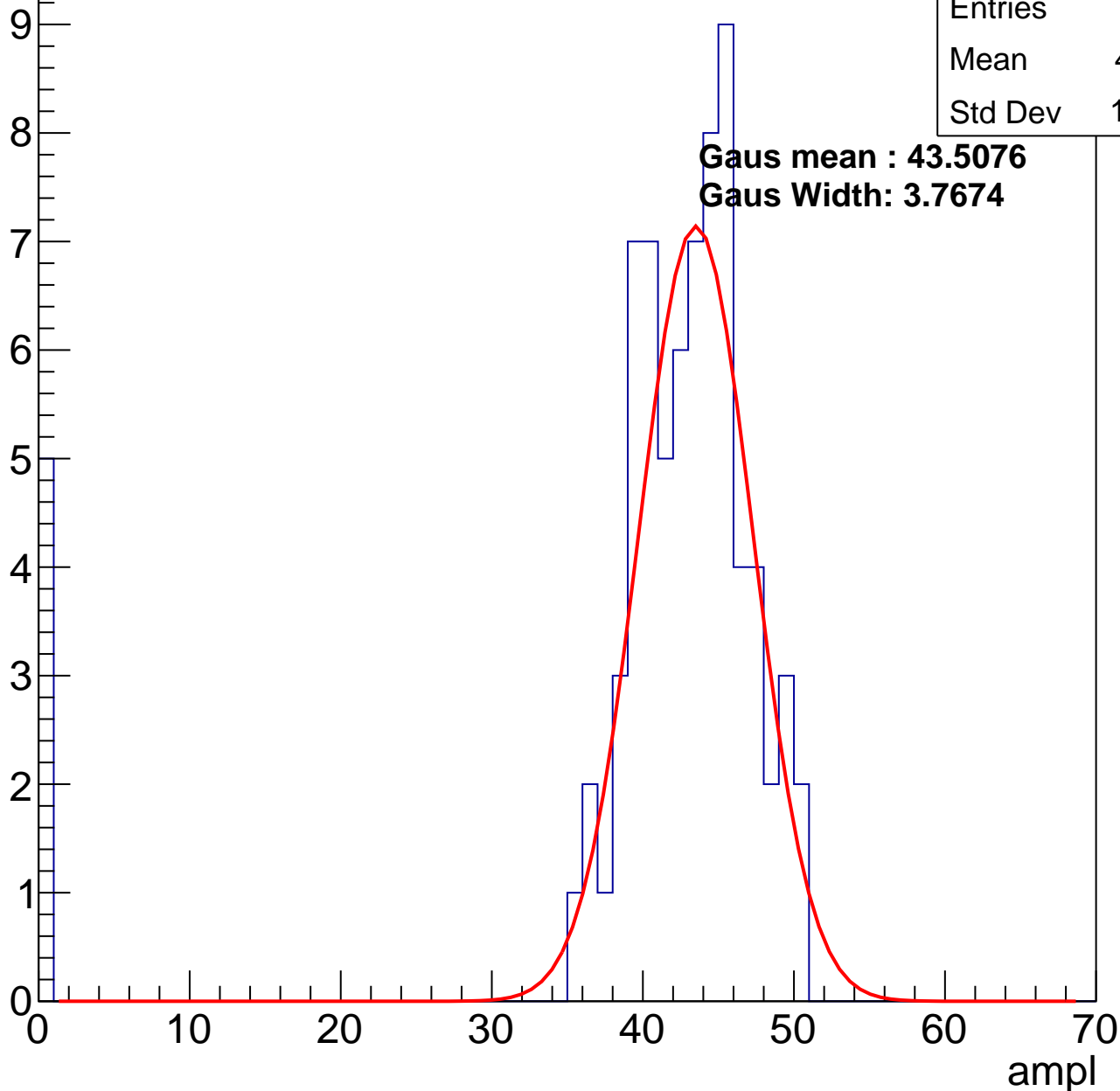
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	40.01
Std Dev	11.15

Gaus mean : 43.5076

Gaus Width: 3.7674

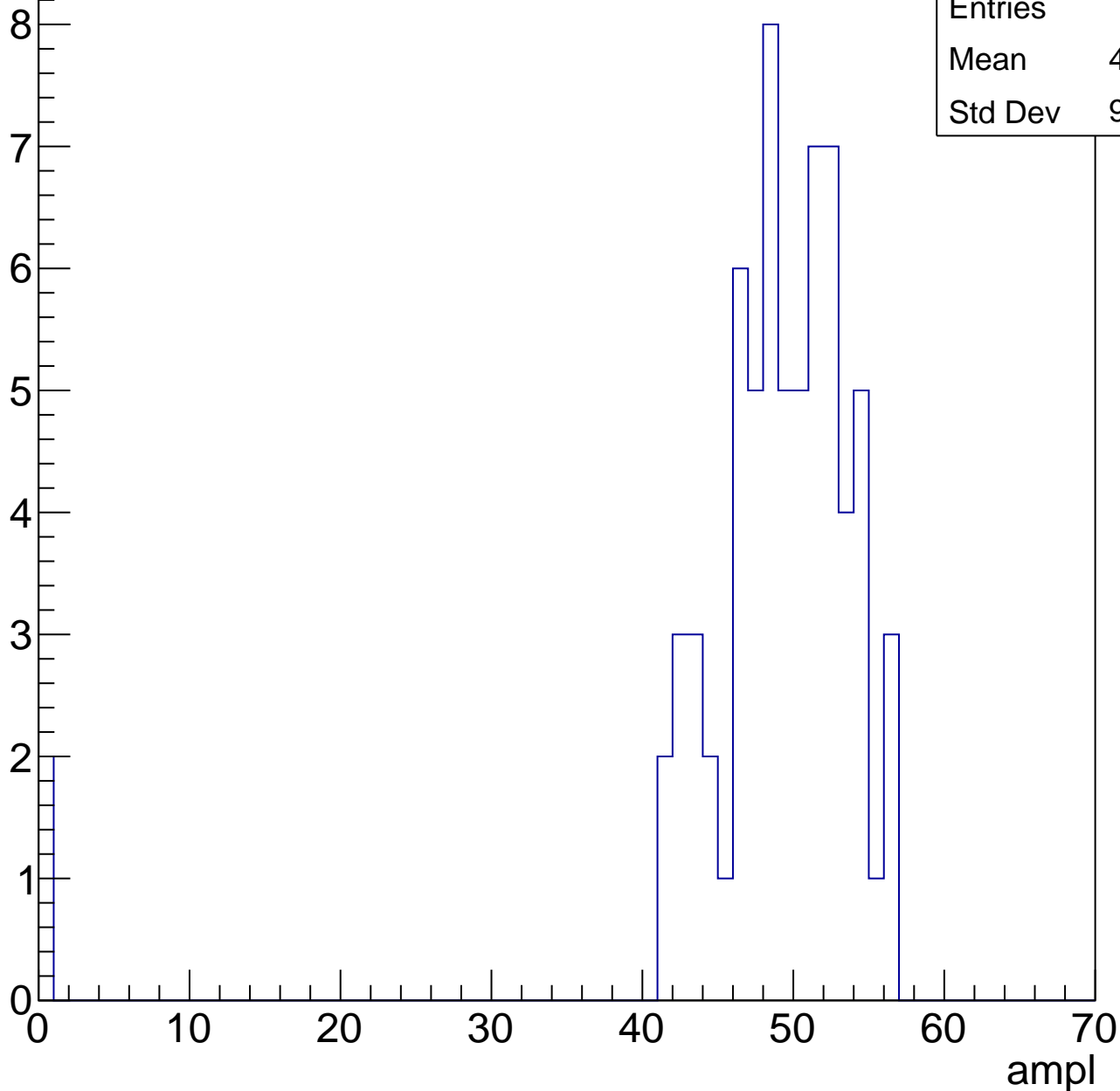


B1L103S, U13-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	47.62
Std Dev	9.065

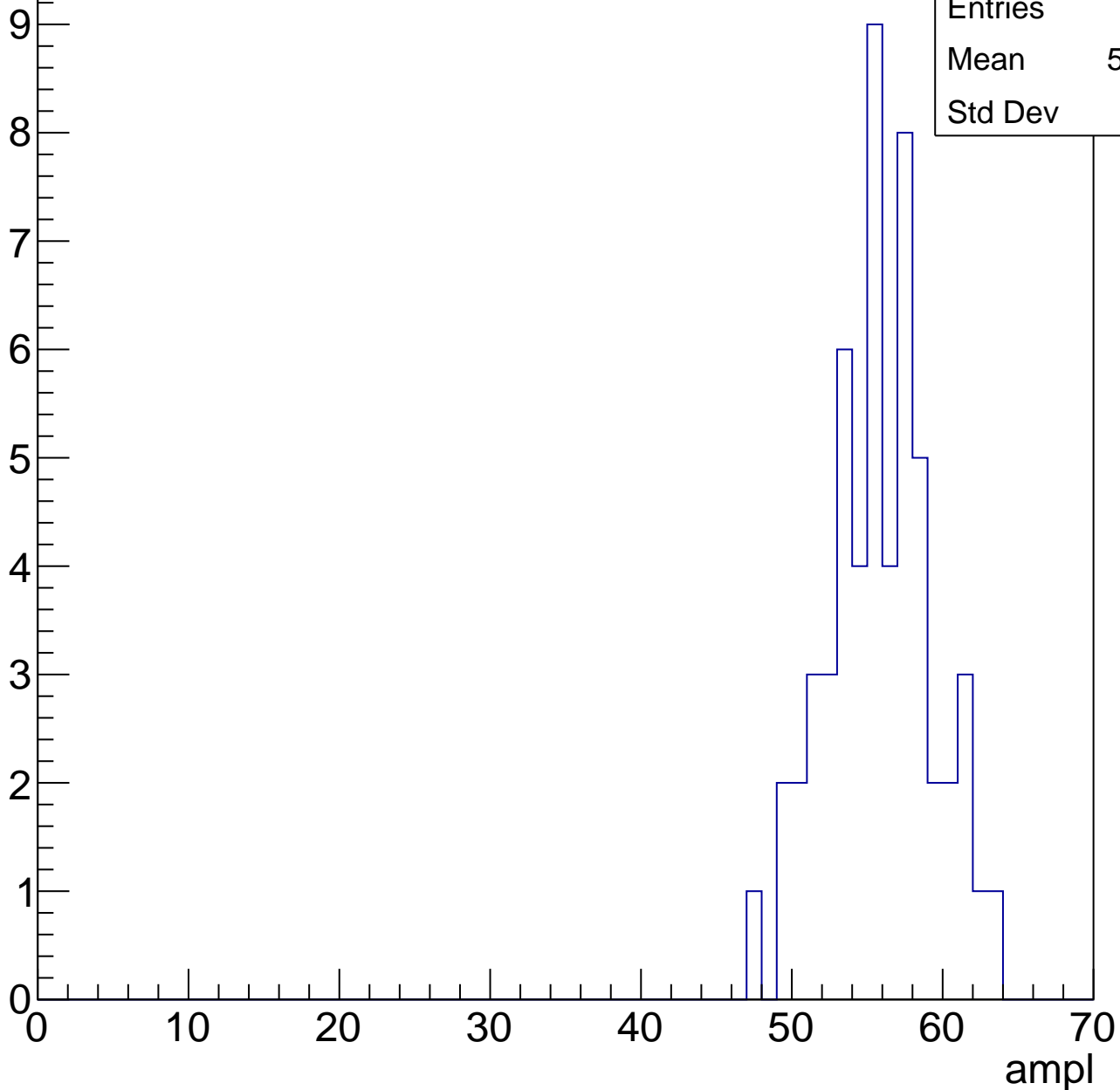


B1L103S, U13-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	55.34
Std Dev	3.45

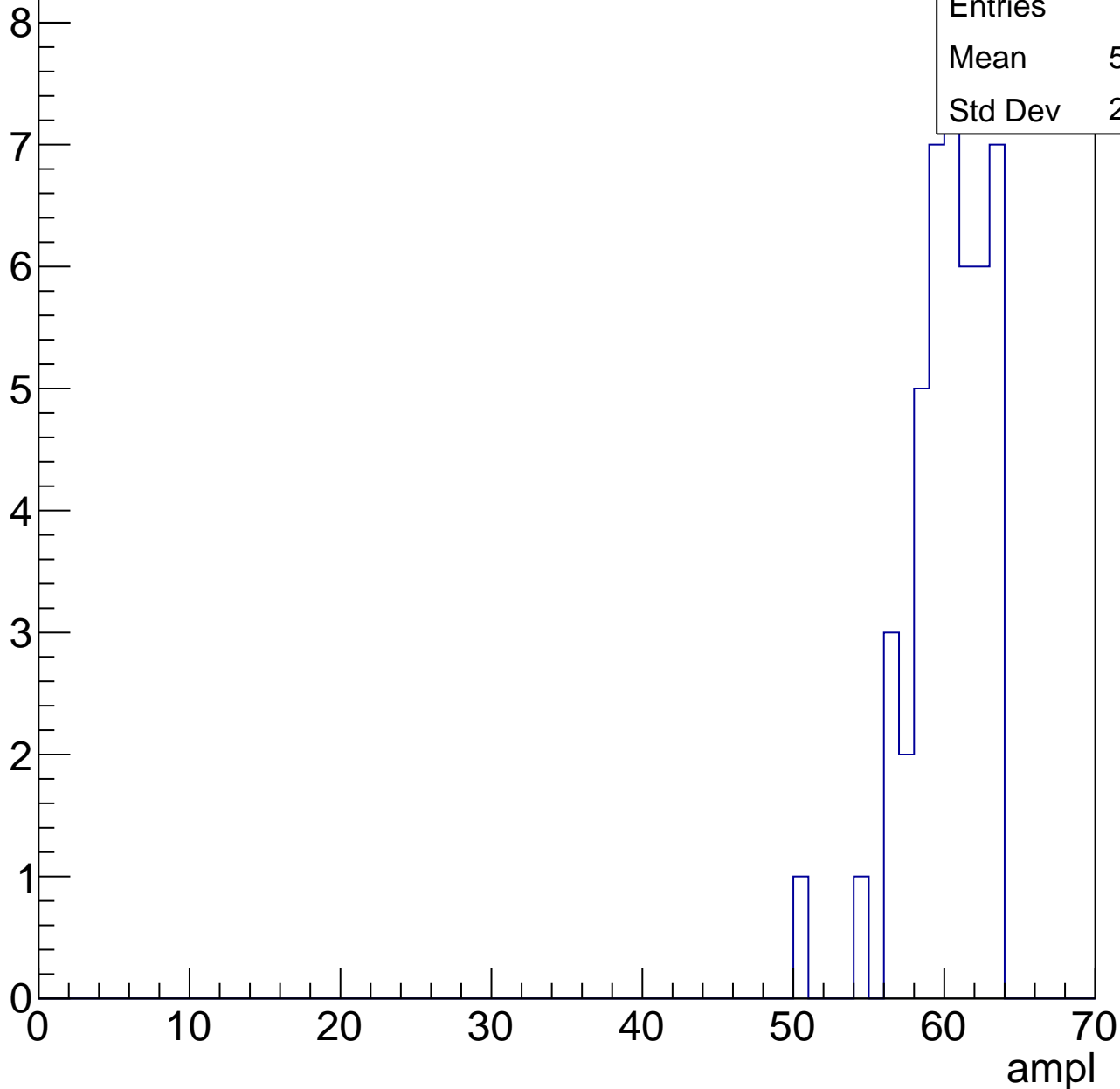


B1L103S, U13-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

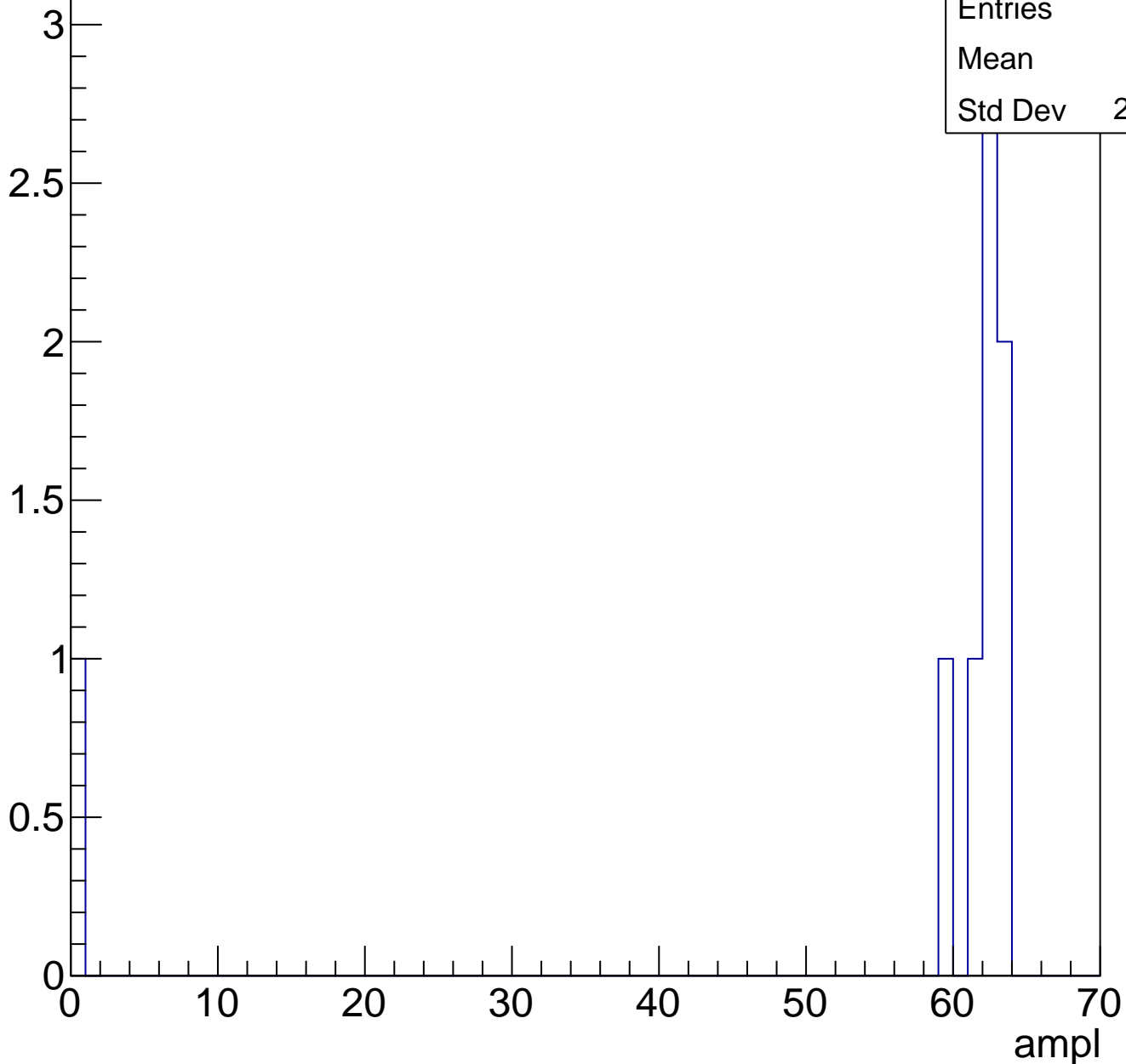
Entries	46
Mean	59.74
Std Dev	2.633



B1L103S, U13-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch63, adc0

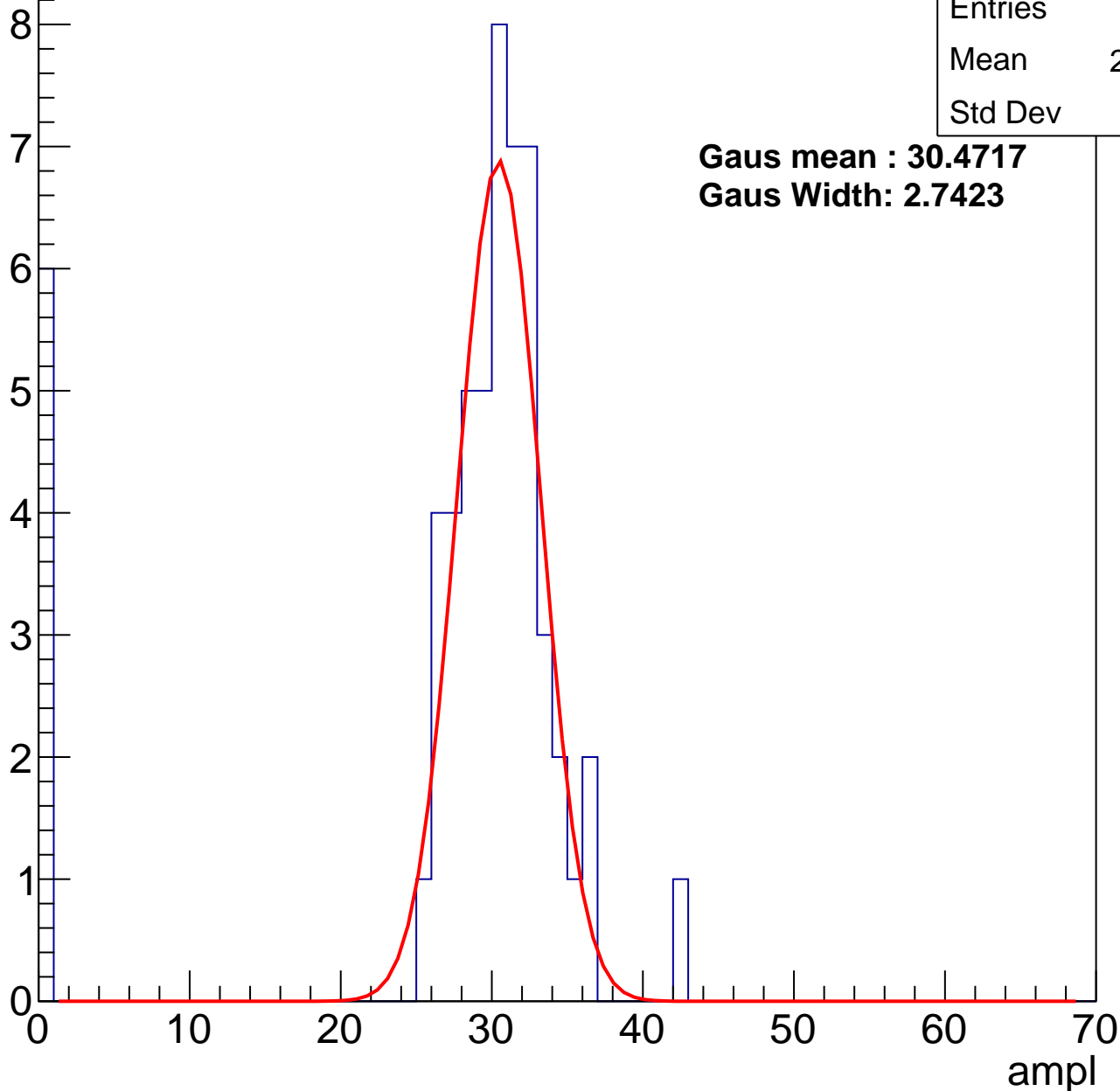
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	27.12
Std Dev	9.84

Gaus mean : 30.4717

Gaus Width: 2.7423



B1L103S, U13-ch63, adc1

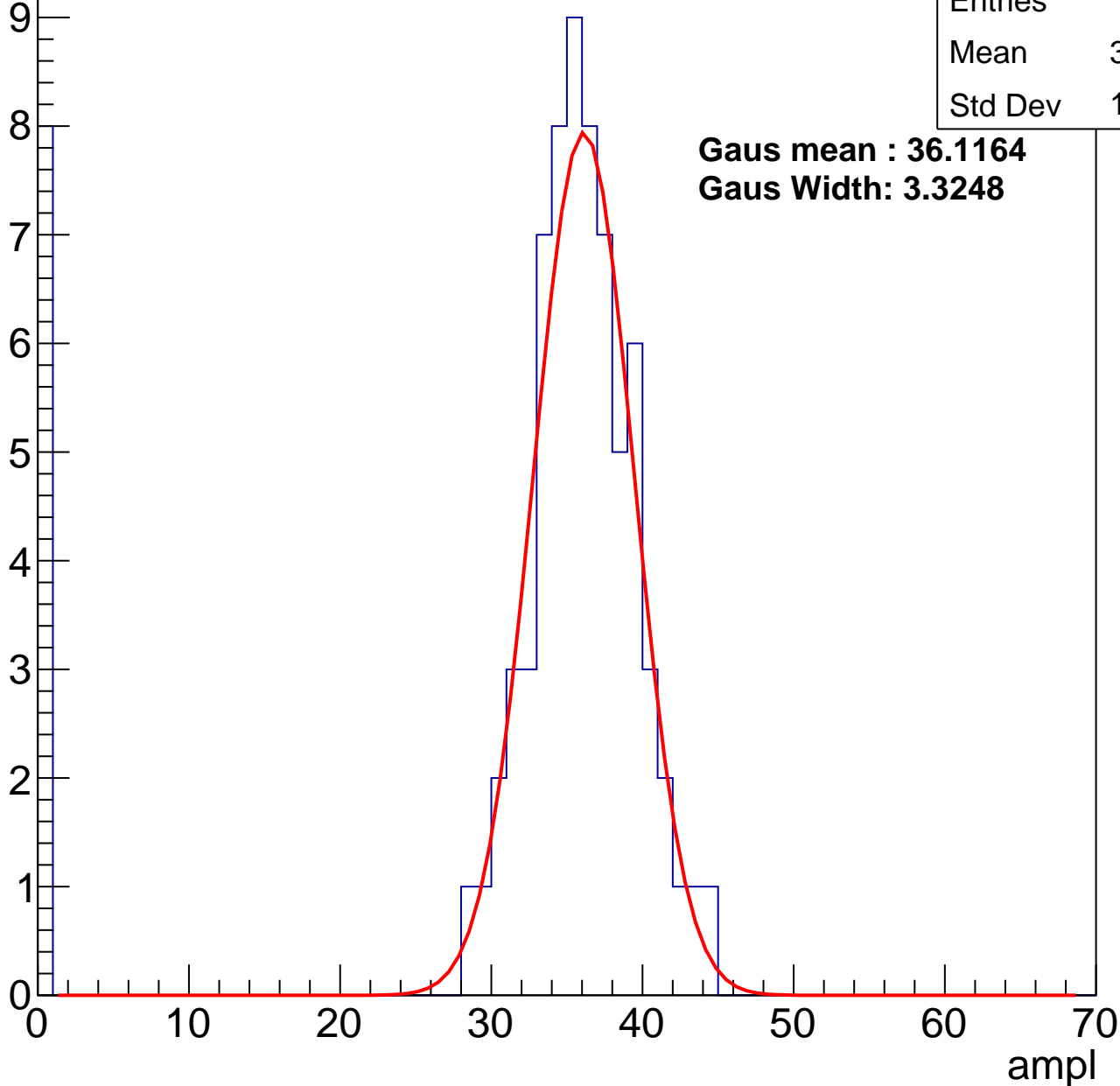
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	31.92
Std Dev	11.38

Gaus mean : 36.1164

Gaus Width: 3.3248



B1L103S, U13-ch63, adc2

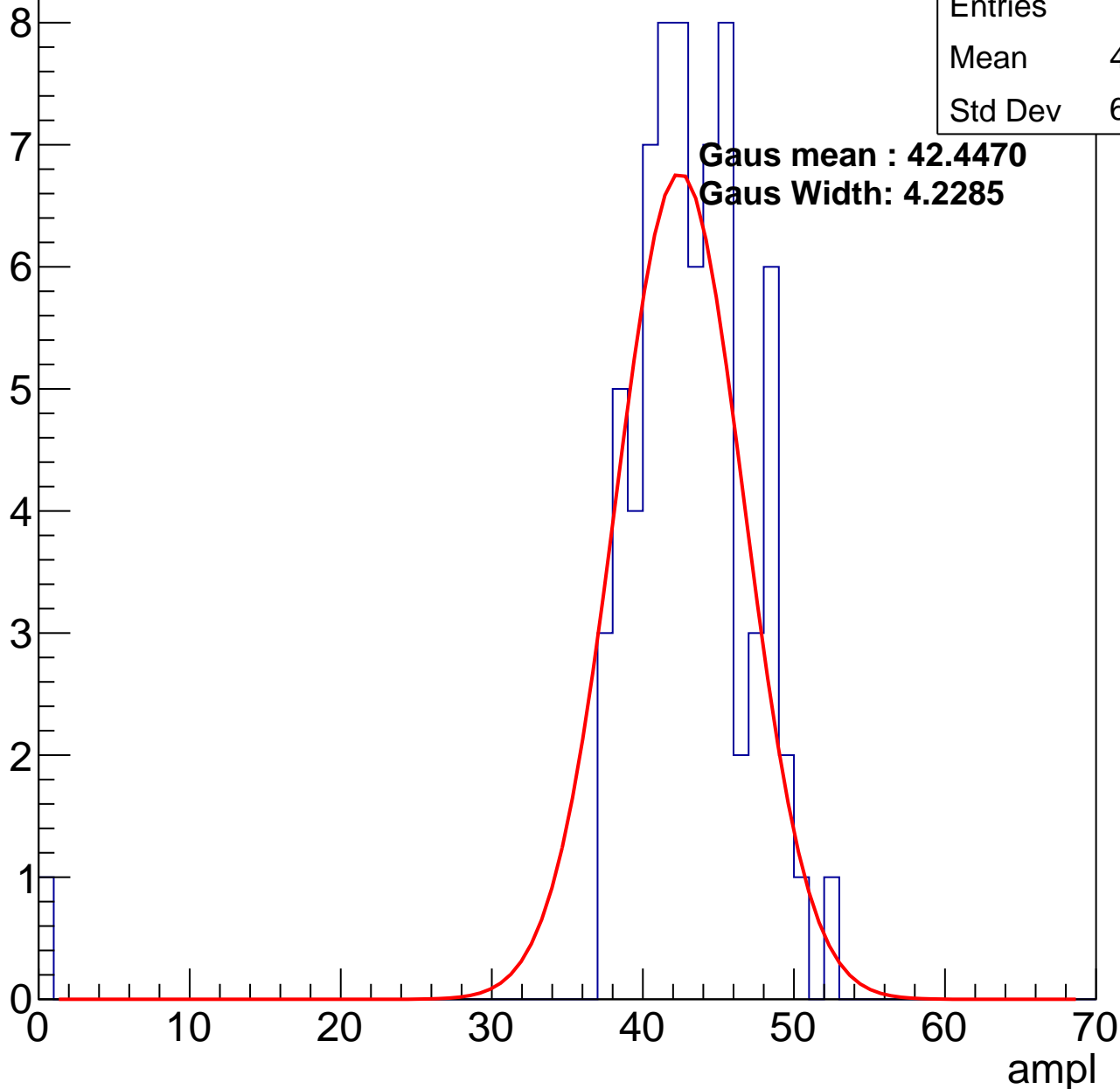
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	42.33
Std Dev	6.094

Gaus mean : 42.4470

Gaus Width: 4.2285

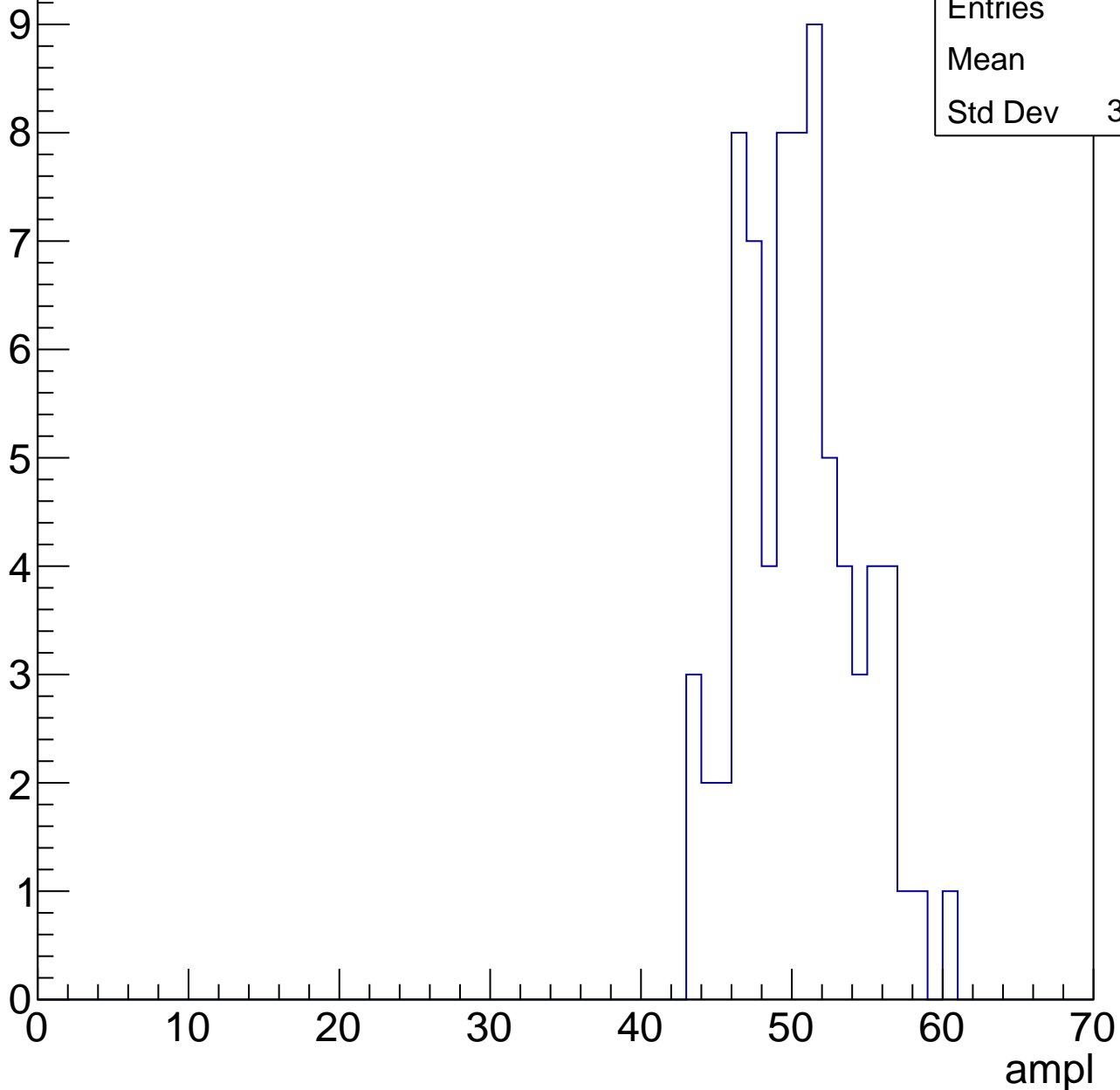


B1L103S, U13-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	50
Std Dev	3.774

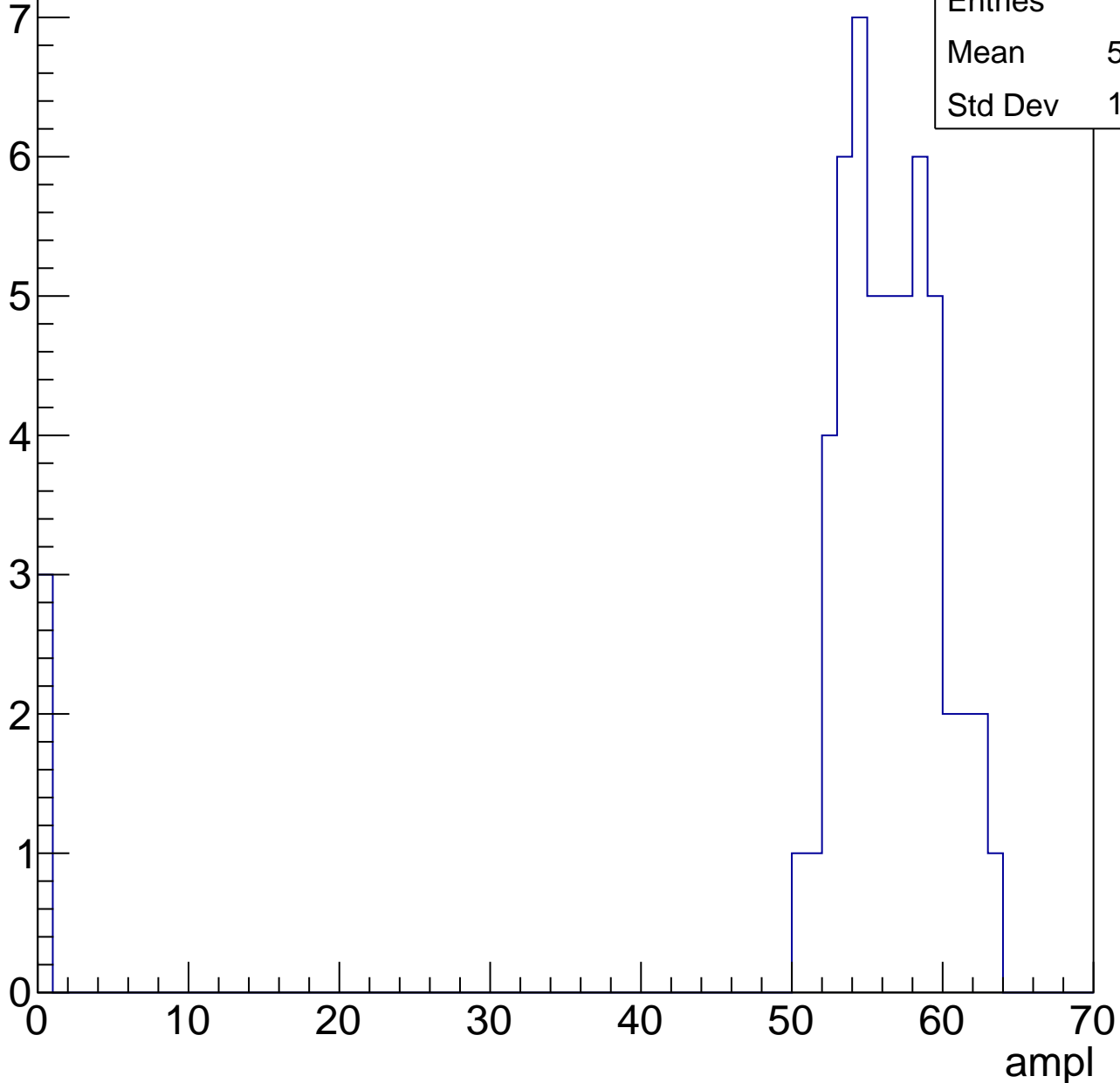


B1L103S, U13-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.04
Std Dev	13.08

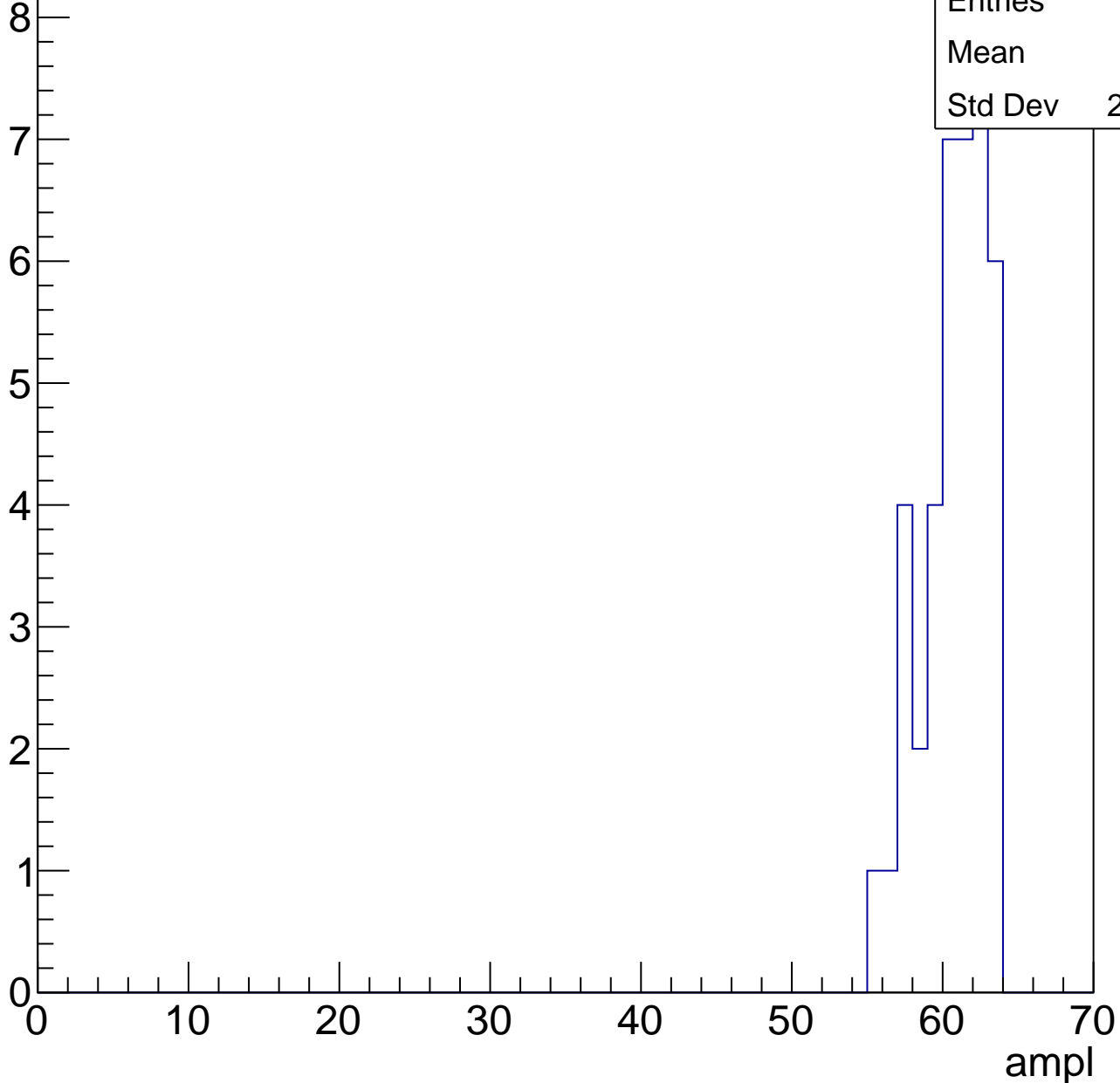


B1L103S, U13-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

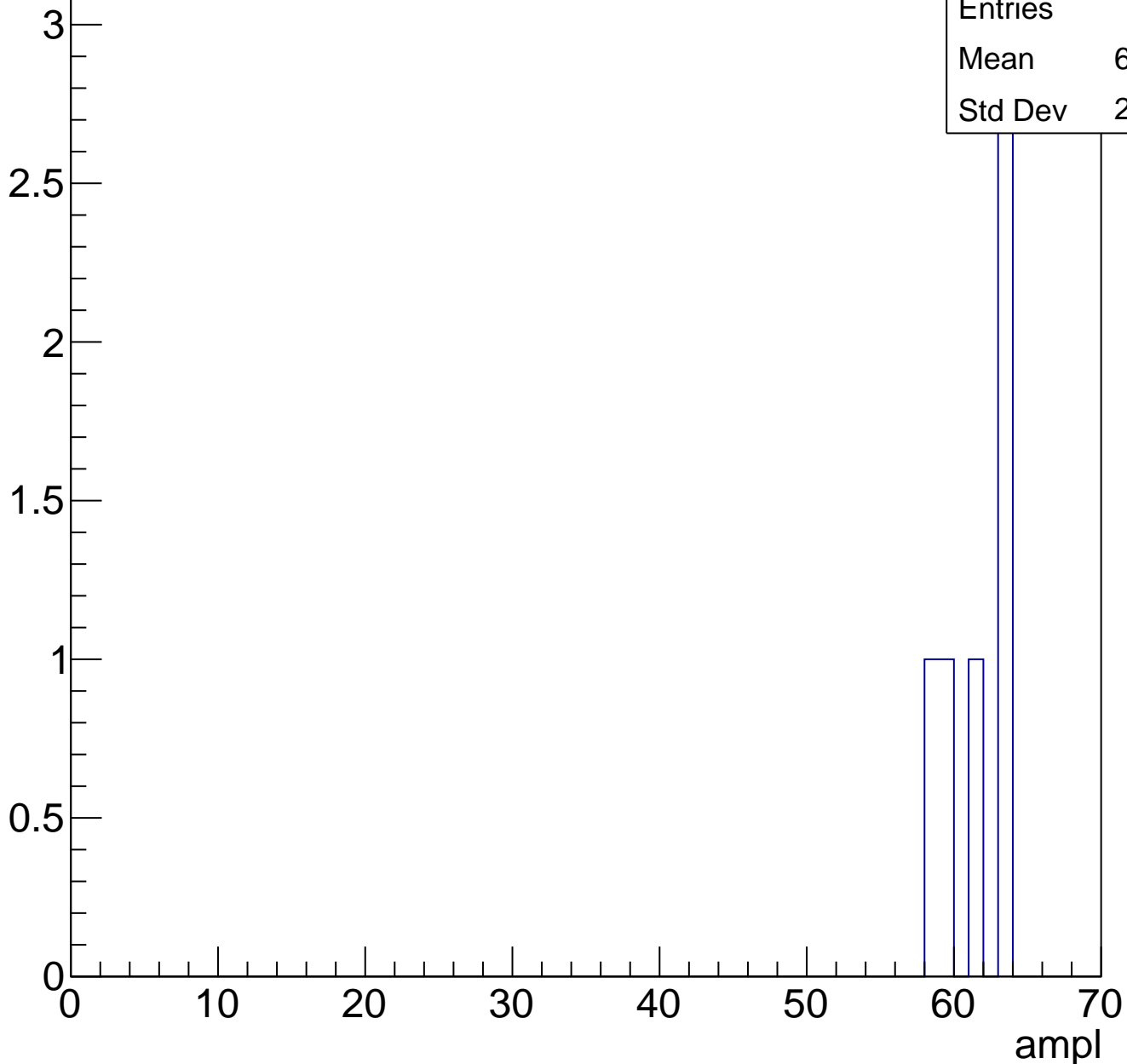
Entries	40
Mean	60.3
Std Dev	2.112



B1L103S, U13-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136

Entry



B1L103S, U13-ch64, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	25.06
Std Dev	11.73

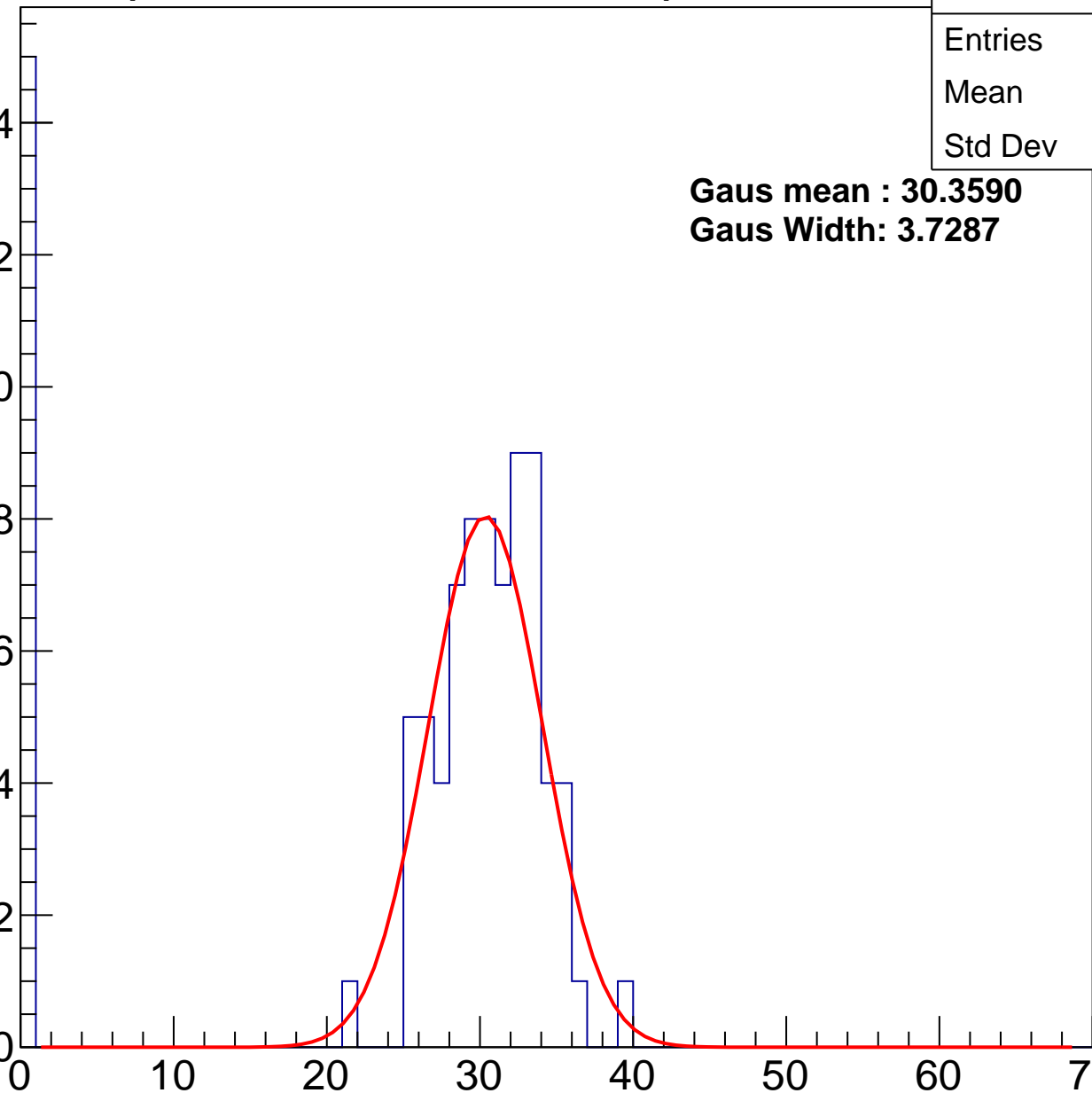
Gaus mean : 30.3590

Gaus Width: 3.7287

Entry

14
12
10
8
6
4
2
0

ampl

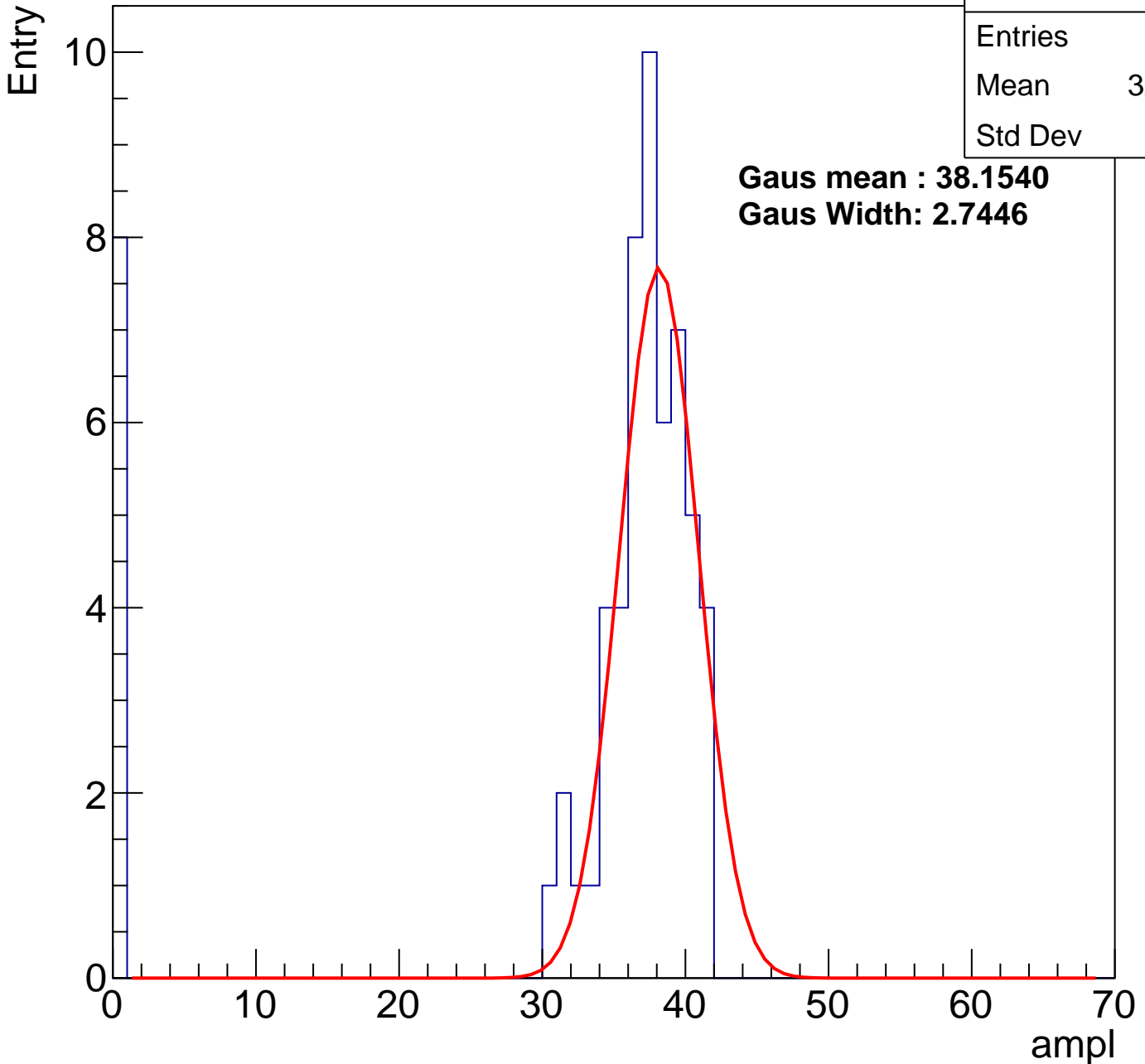


B1L103S, U13-ch64, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	32.07
Std Dev	12.7

Gaus mean : 38.1540
Gaus Width: 2.7446



B1L103S, U13-ch64, adc2

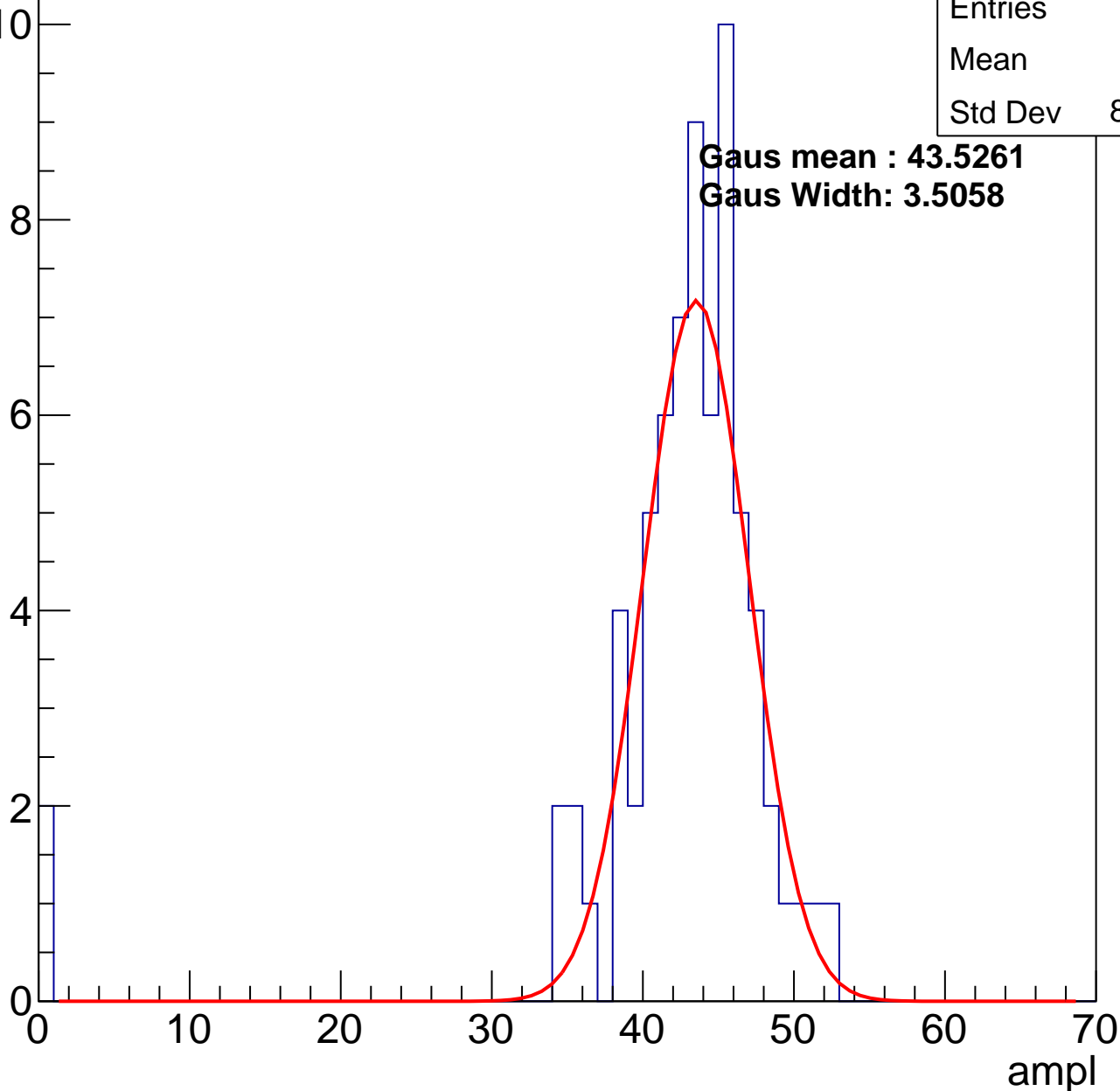
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	41.7
Std Dev	8.015

Gaus mean : 43.5261

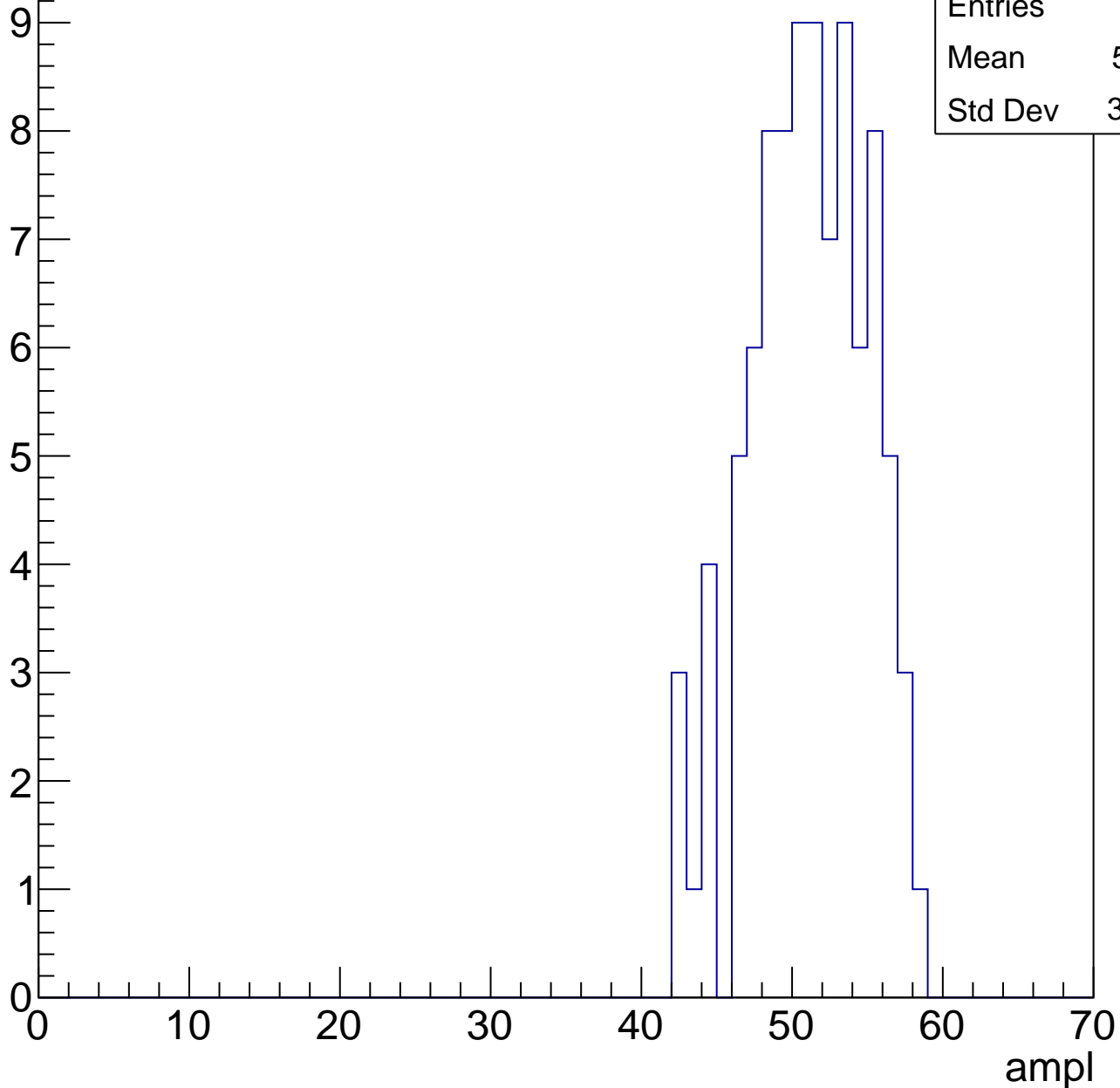
Gaus Width: 3.5058



B1L103S, U13-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



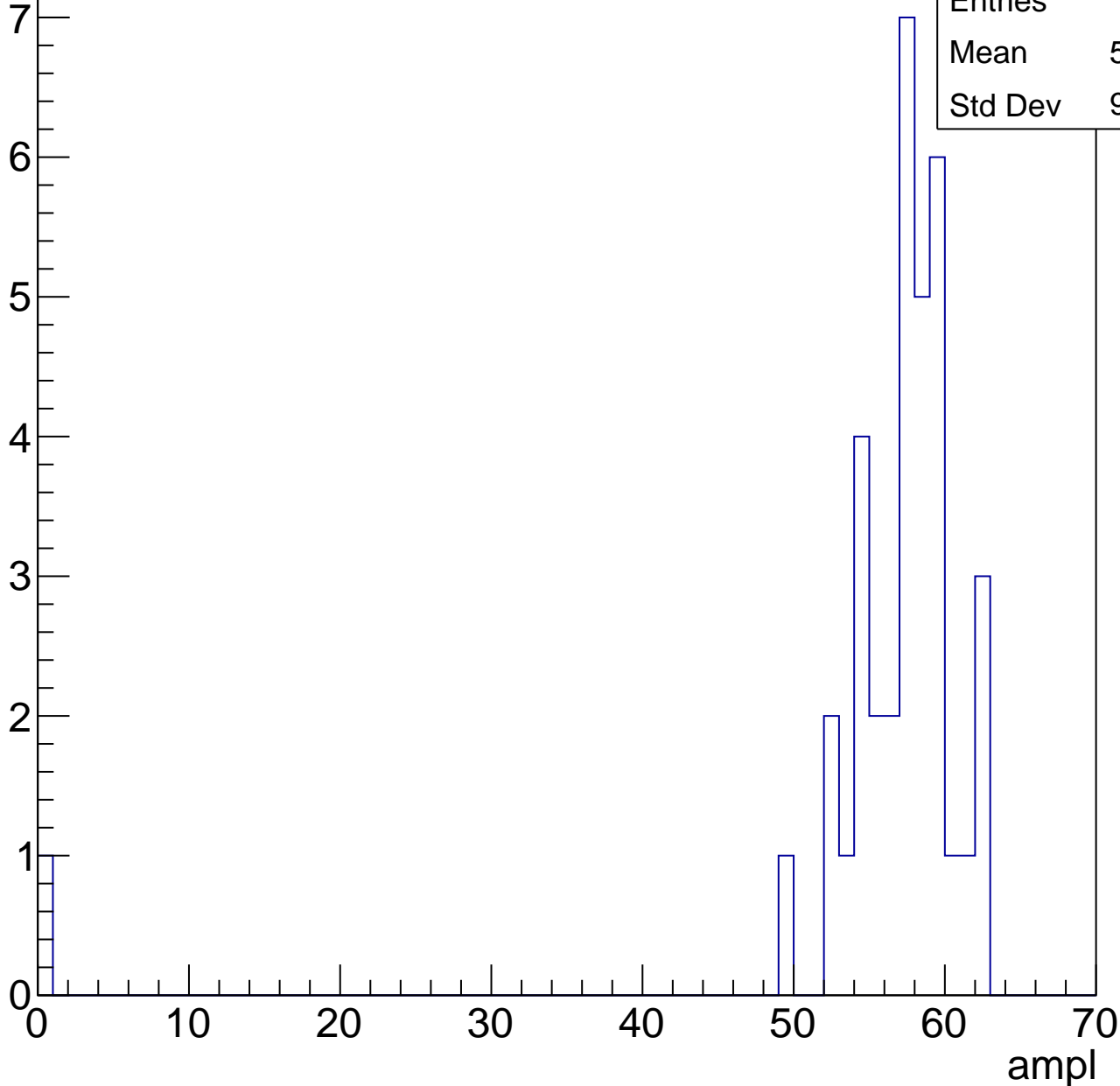
Entries	92
Mean	50.61
Std Dev	3.802

B1L103S, U13-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	55.39
Std Dev	9.804



B1L103S, U13-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.5
Std Dev	1.991

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B1L103S, U13-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch65, adc0

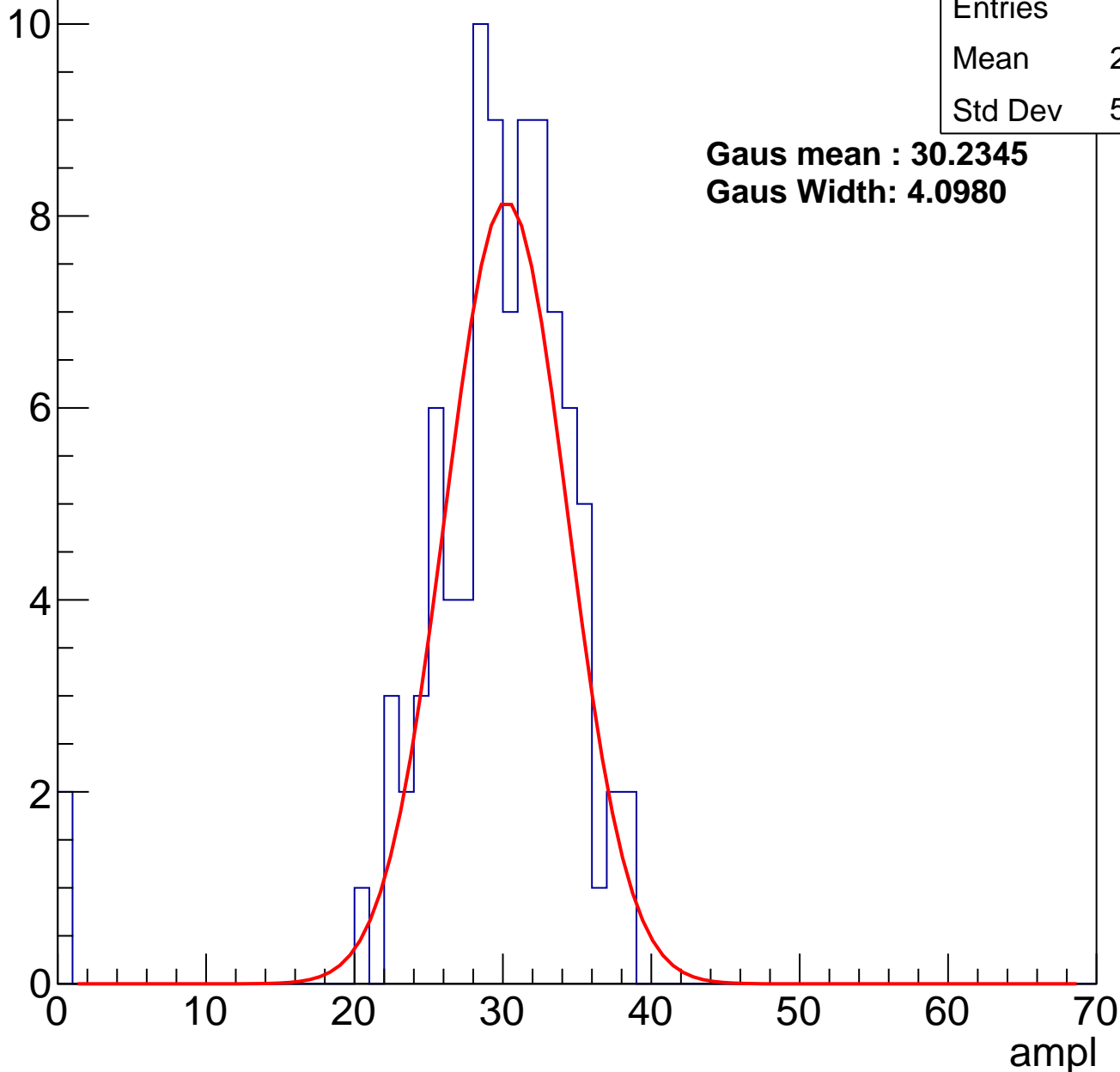
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	29.13
Std Dev	5.822

Gaus mean : 30.2345

Gaus Width: 4.0980

Entry



B1L103S, U13-ch65, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	37.25
Std Dev	5.813

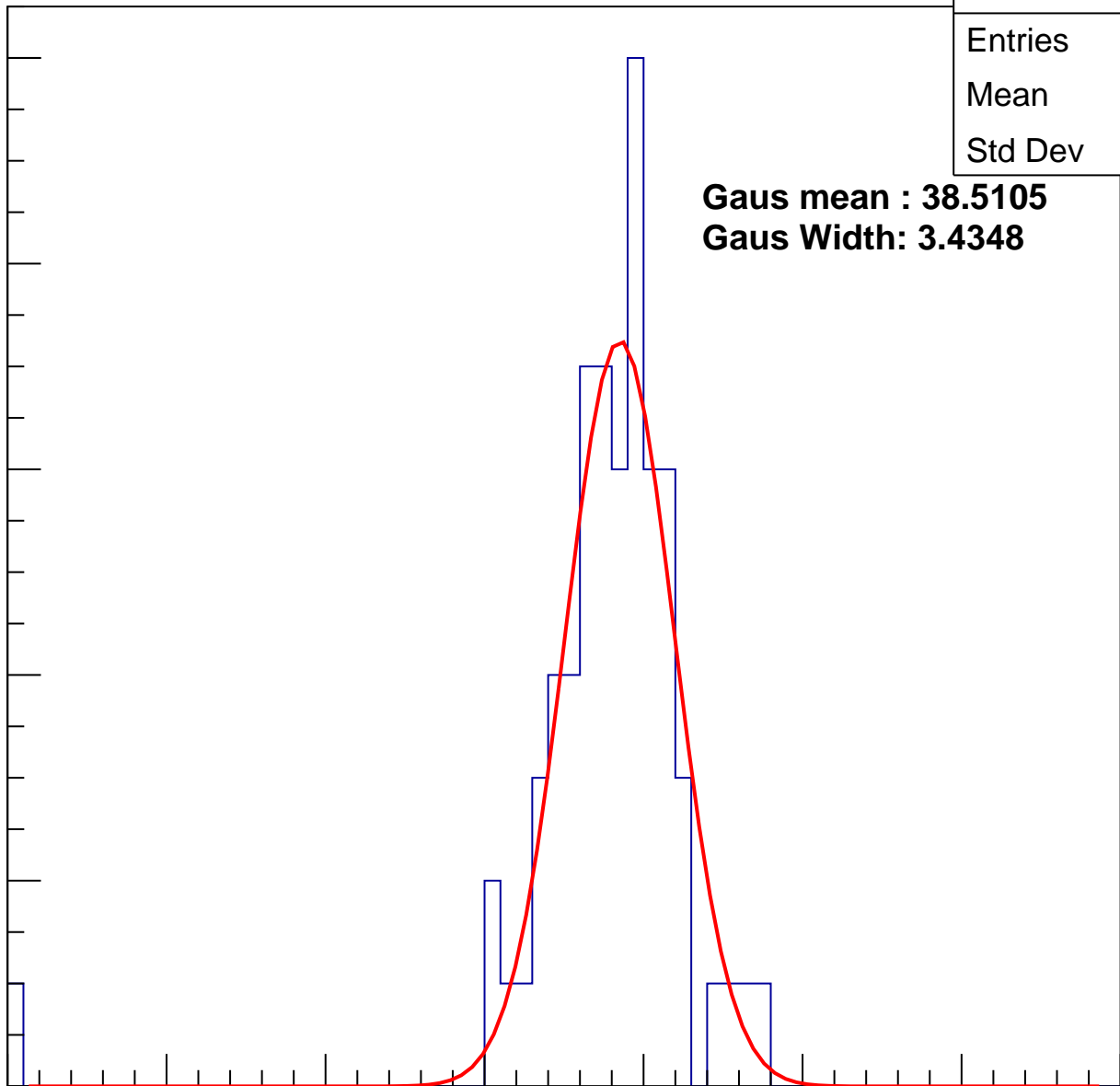
Gaus mean : 38.5105
Gaus Width: 3.4348

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	38.56
Std Dev	14.63

Gaus mean : 43.9609

Gaus Width: 3.4877

Entry

10

8

6

4

2

0

0

10

20

30

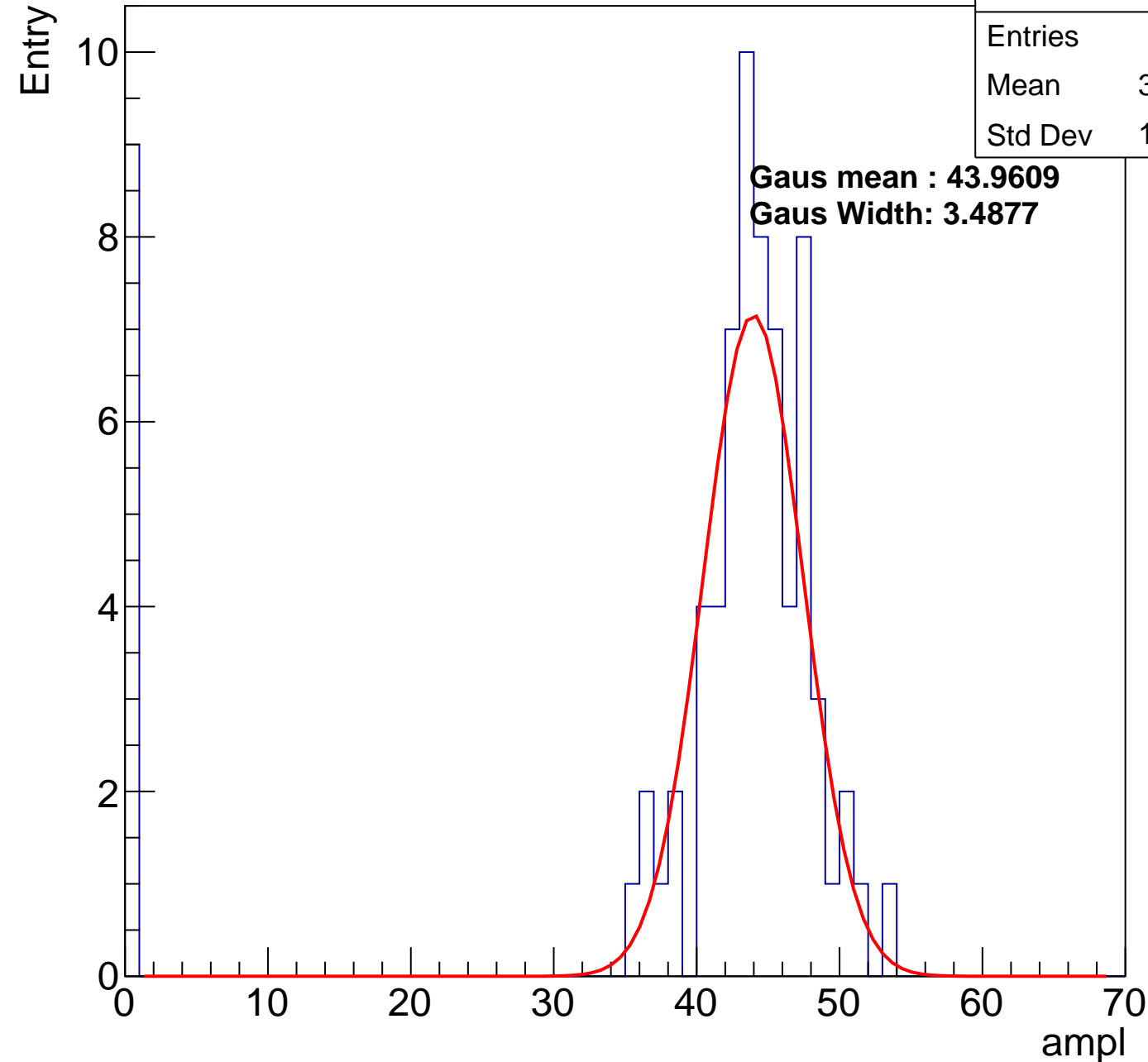
40

50

60

70

ampl

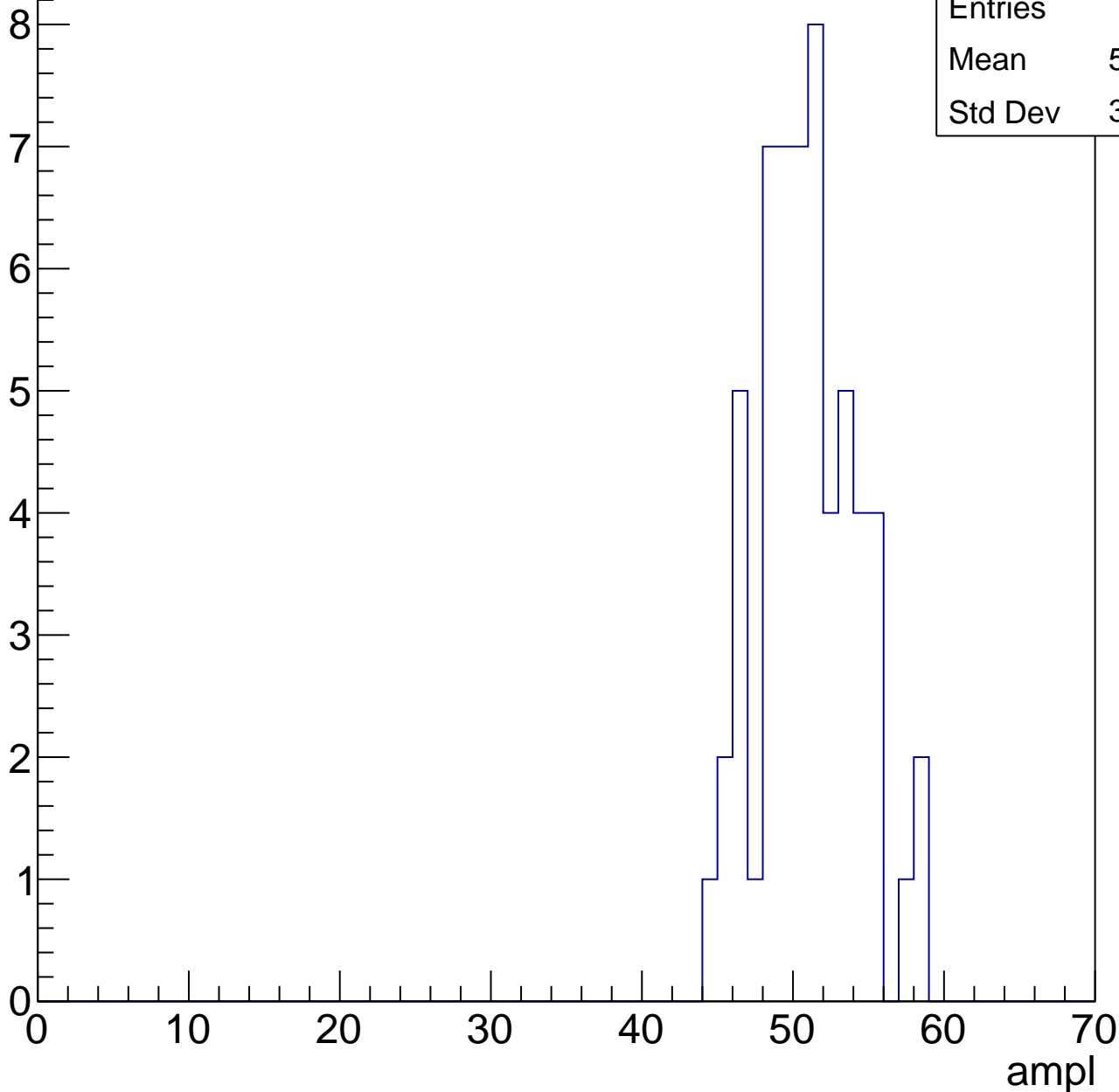


B1L103S, U13-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	50.52
Std Dev	3.228

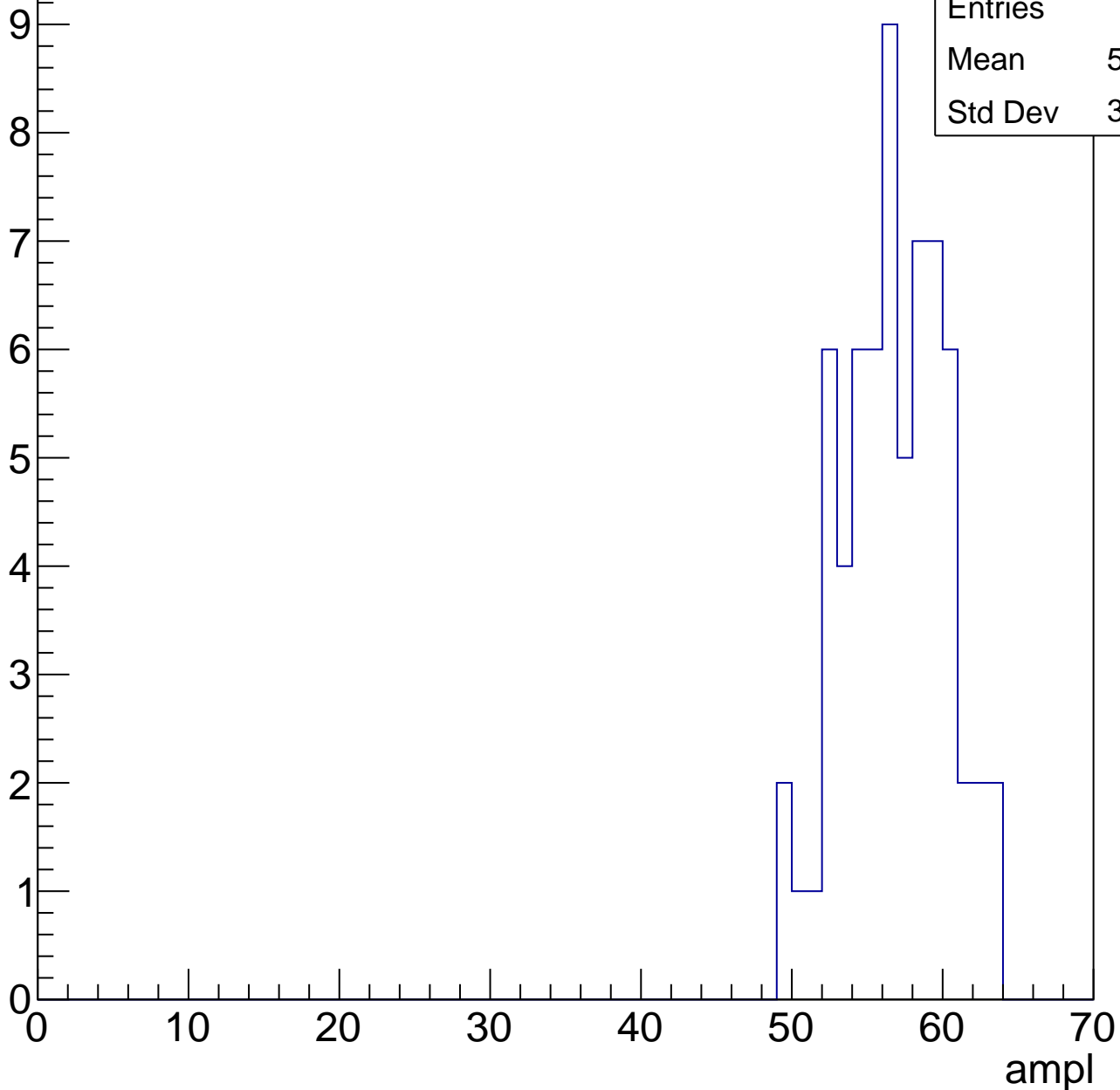


B1L103S, U13-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	56.32
Std Dev	3.317

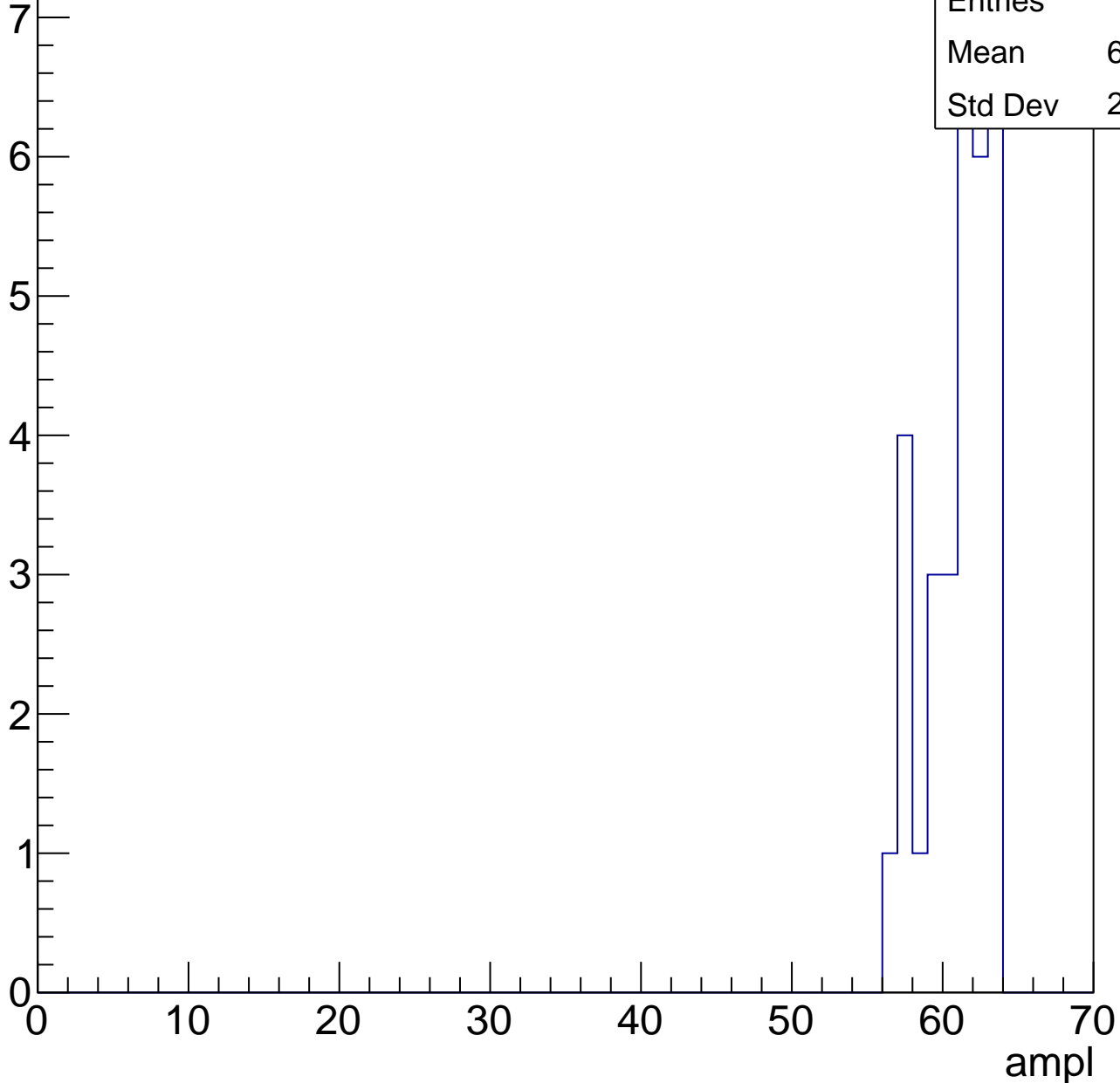


B1L103S, U13-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

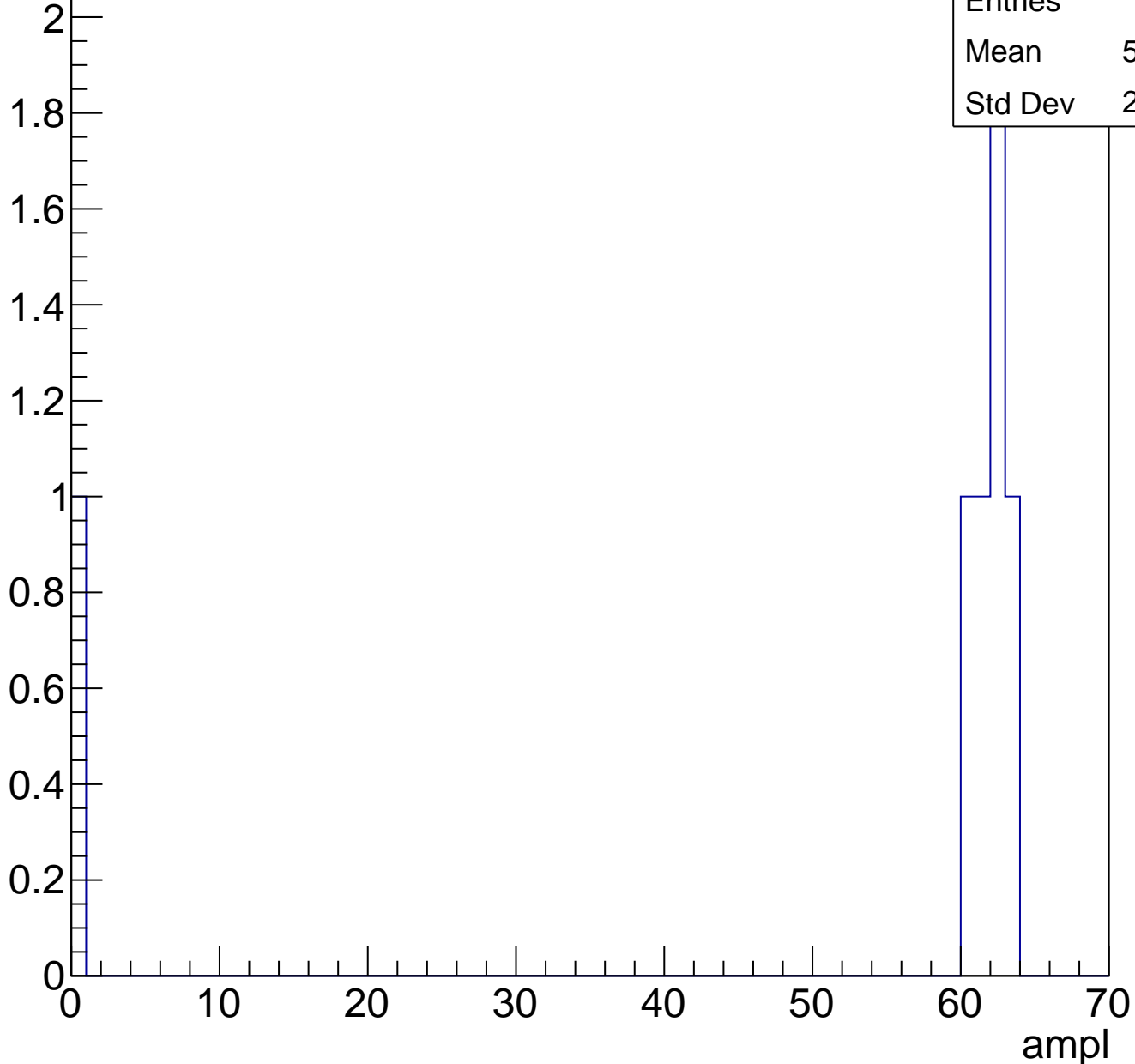
Entries	32
Mean	60.59
Std Dev	2.104



B1L103S, U13-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	6
Mean	51.33
Std Dev	22.98

B1L103S, U13-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch66, adc0

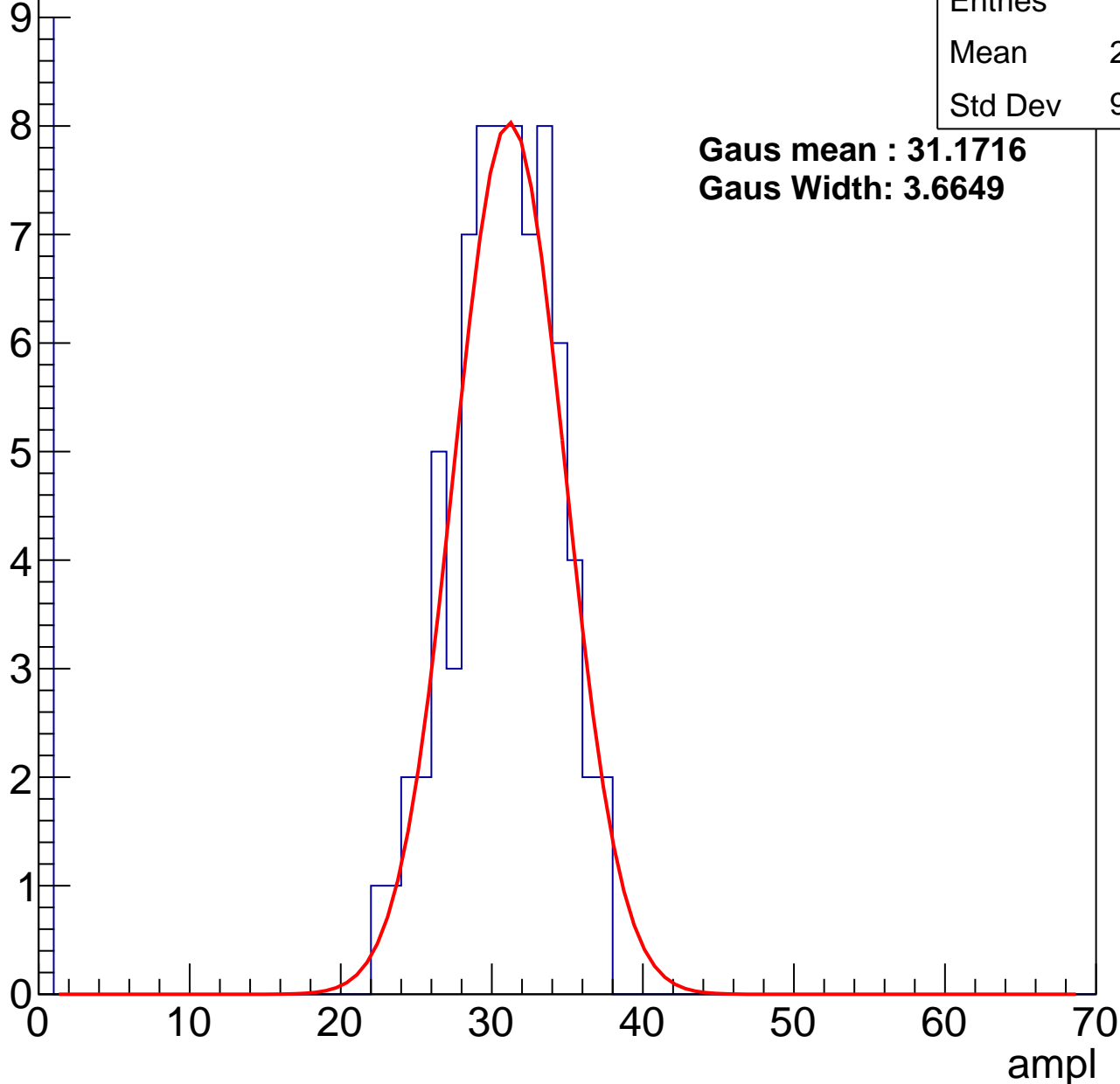
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	27.08
Std Dev	9.968

Gaus mean : 31.1716

Gaus Width: 3.6649



B1L103S, U13-ch66, adc1

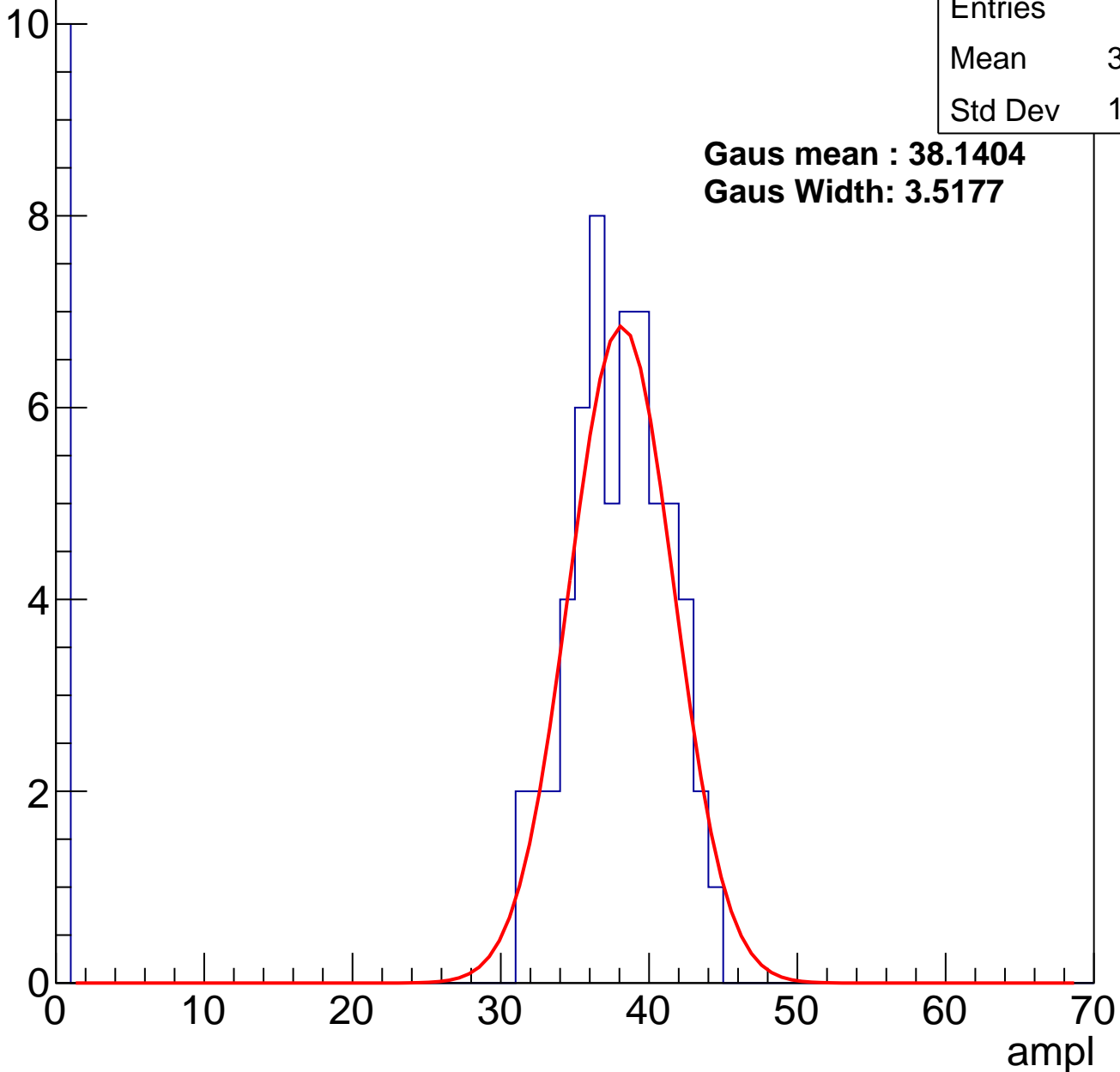
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	32.19
Std Dev	13.45

Gaus mean : 38.1404

Gaus Width: 3.5177

Entry



B1L103S, U13-ch66, adc2

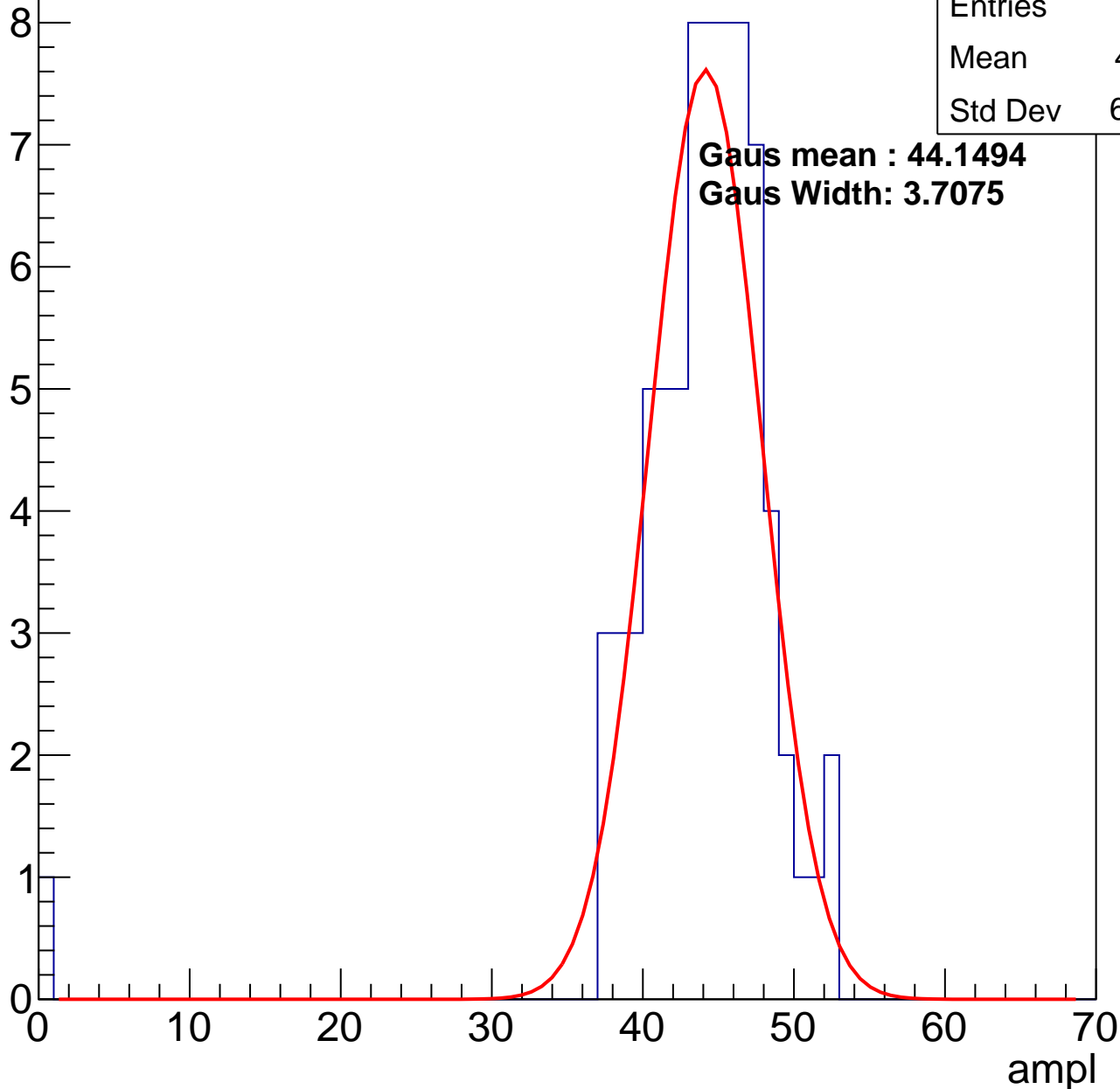
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	43.31
Std Dev	6.166

Gaus mean : 44.1494

Gaus Width: 3.7075

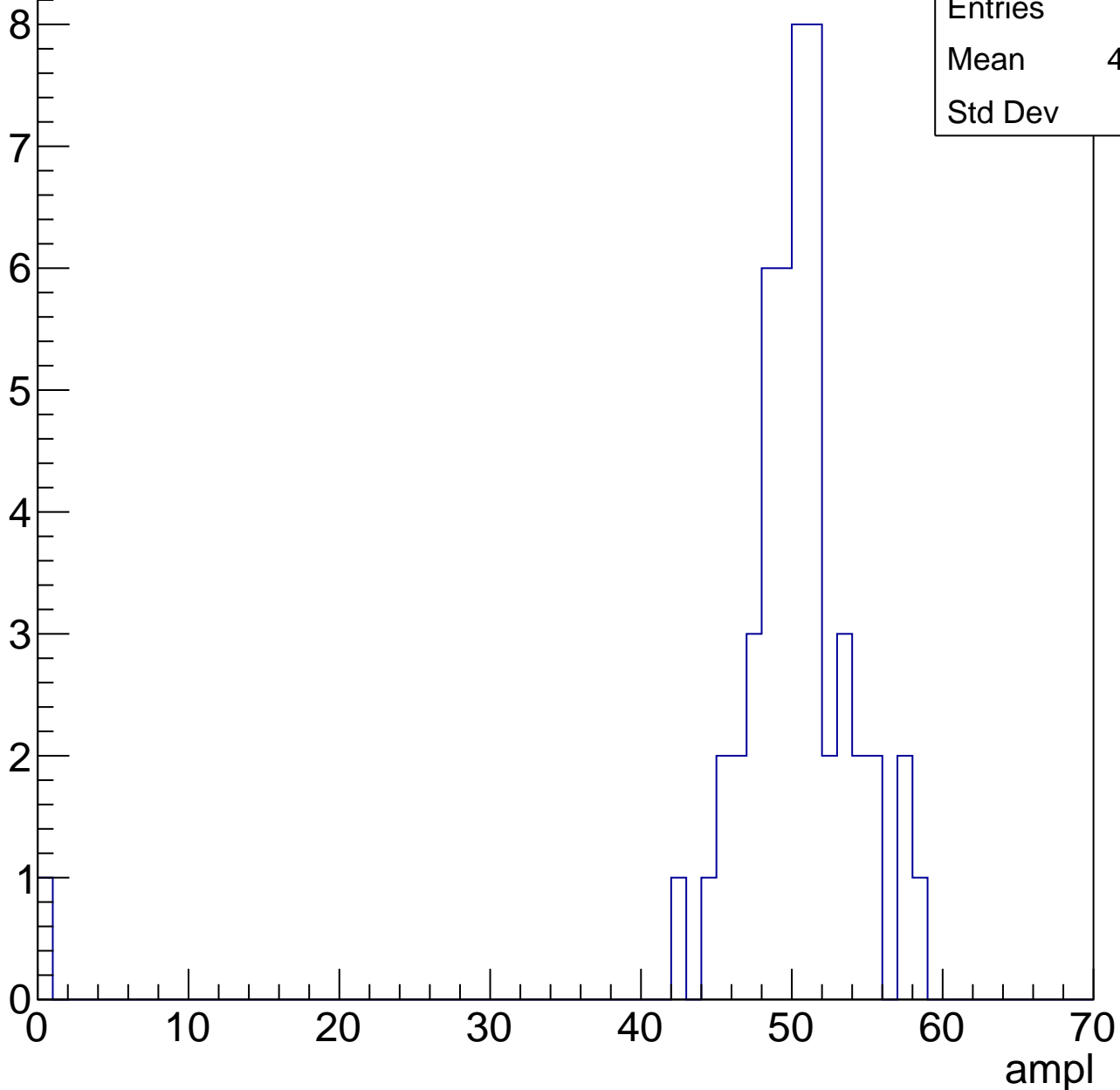


B1L103S, U13-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

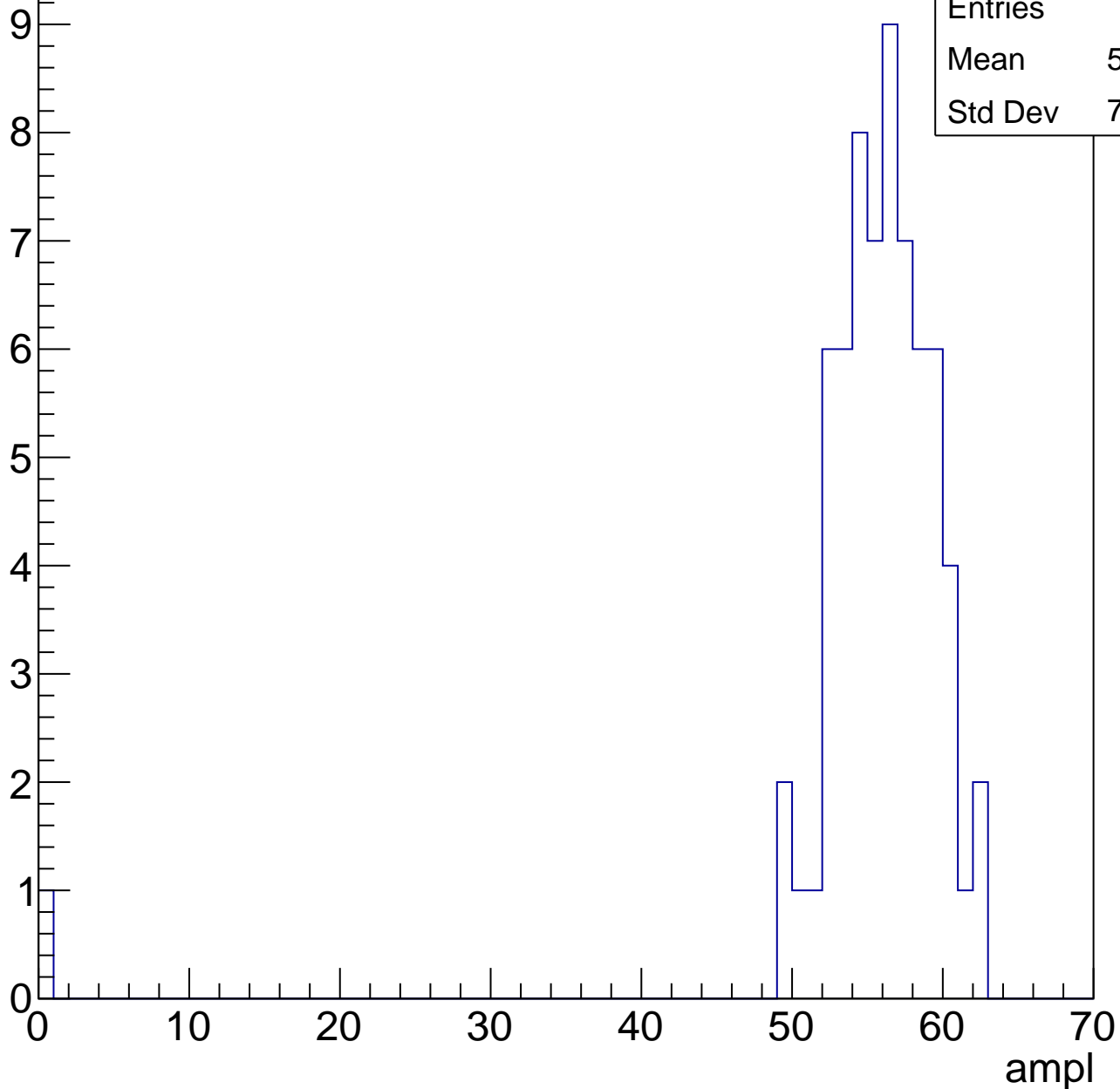
Entries	50
Mean	49.04
Std Dev	7.72



B1L103S, U13-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

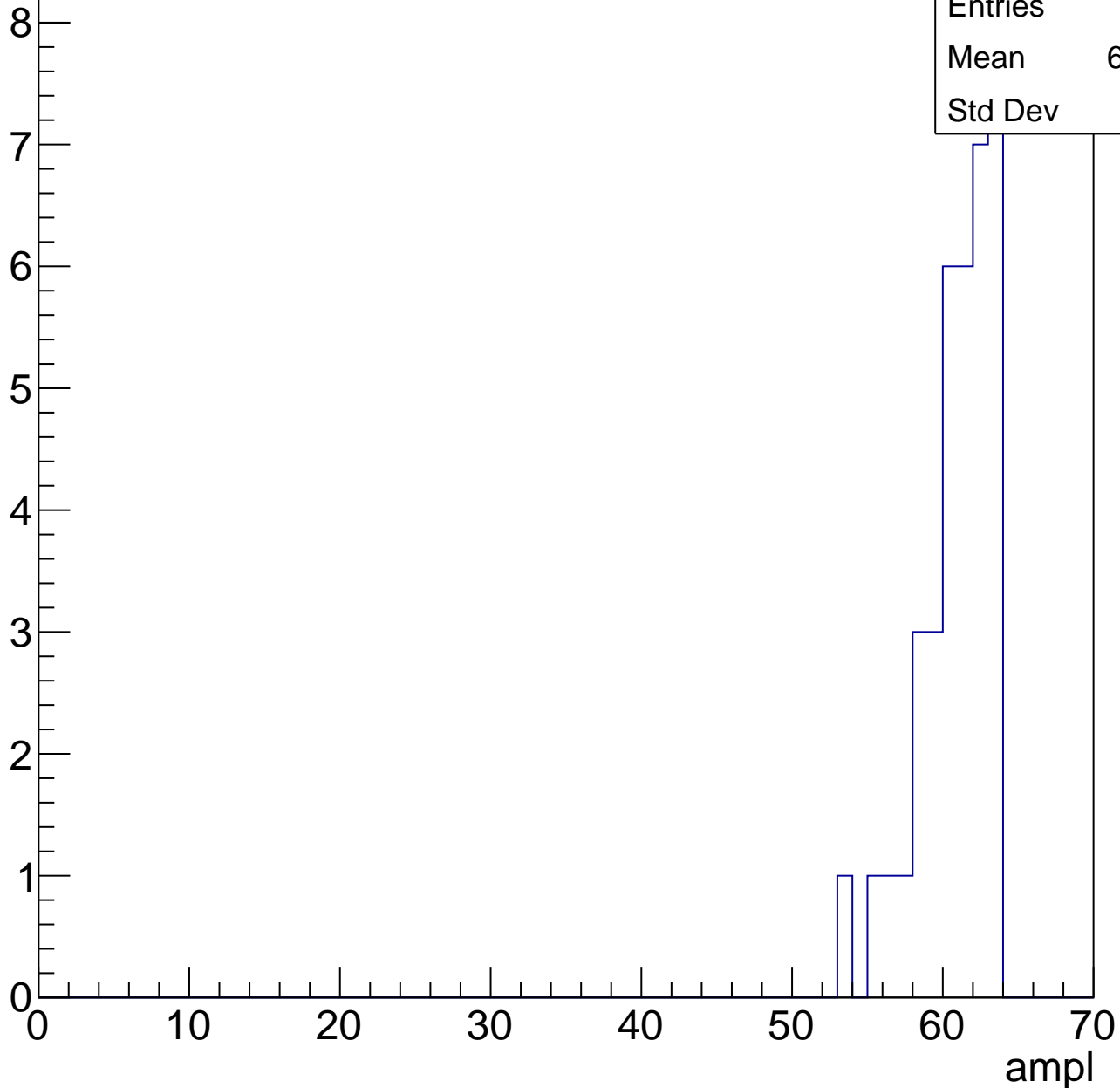


B1L103S, U13-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

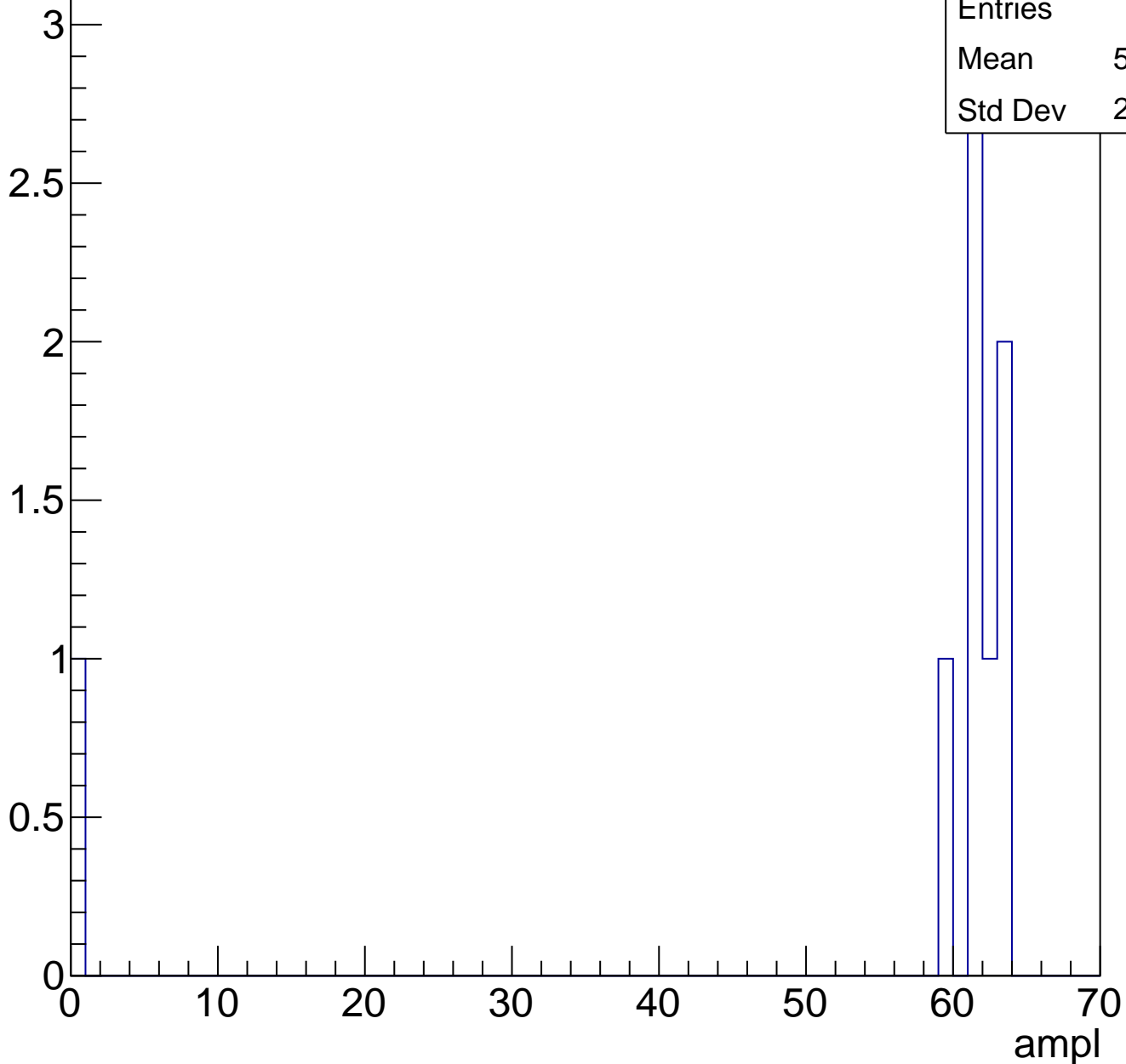
Entries	37
Mean	60.43
Std Dev	2.4



B1L103S, U13-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch67, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	25.55
Std Dev	11.85

Gaus mean : 31.7584

Gaus Width: 3.9702

Entry

10

8

6

4

2

0

0

10

20

30

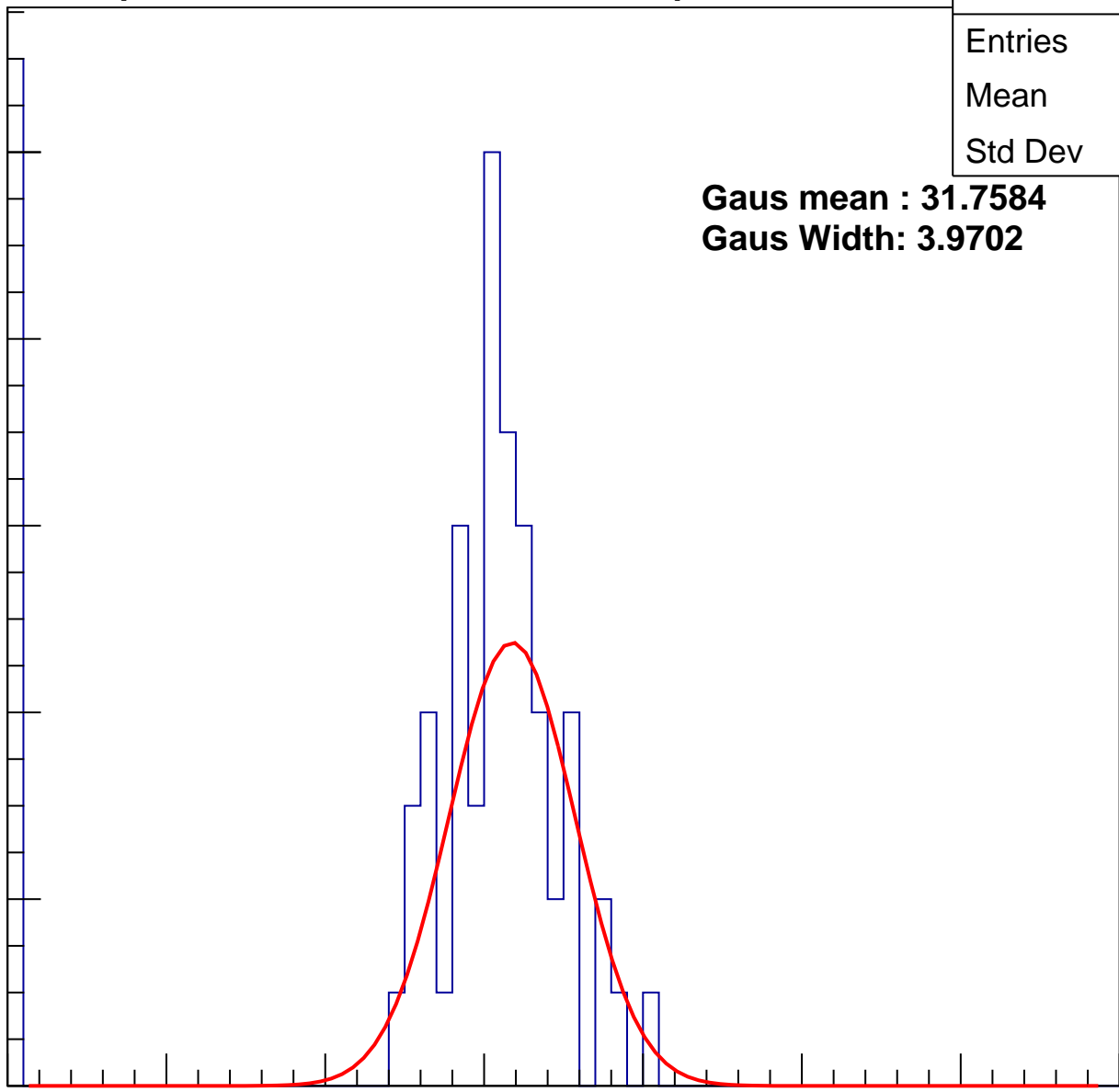
40

50

60

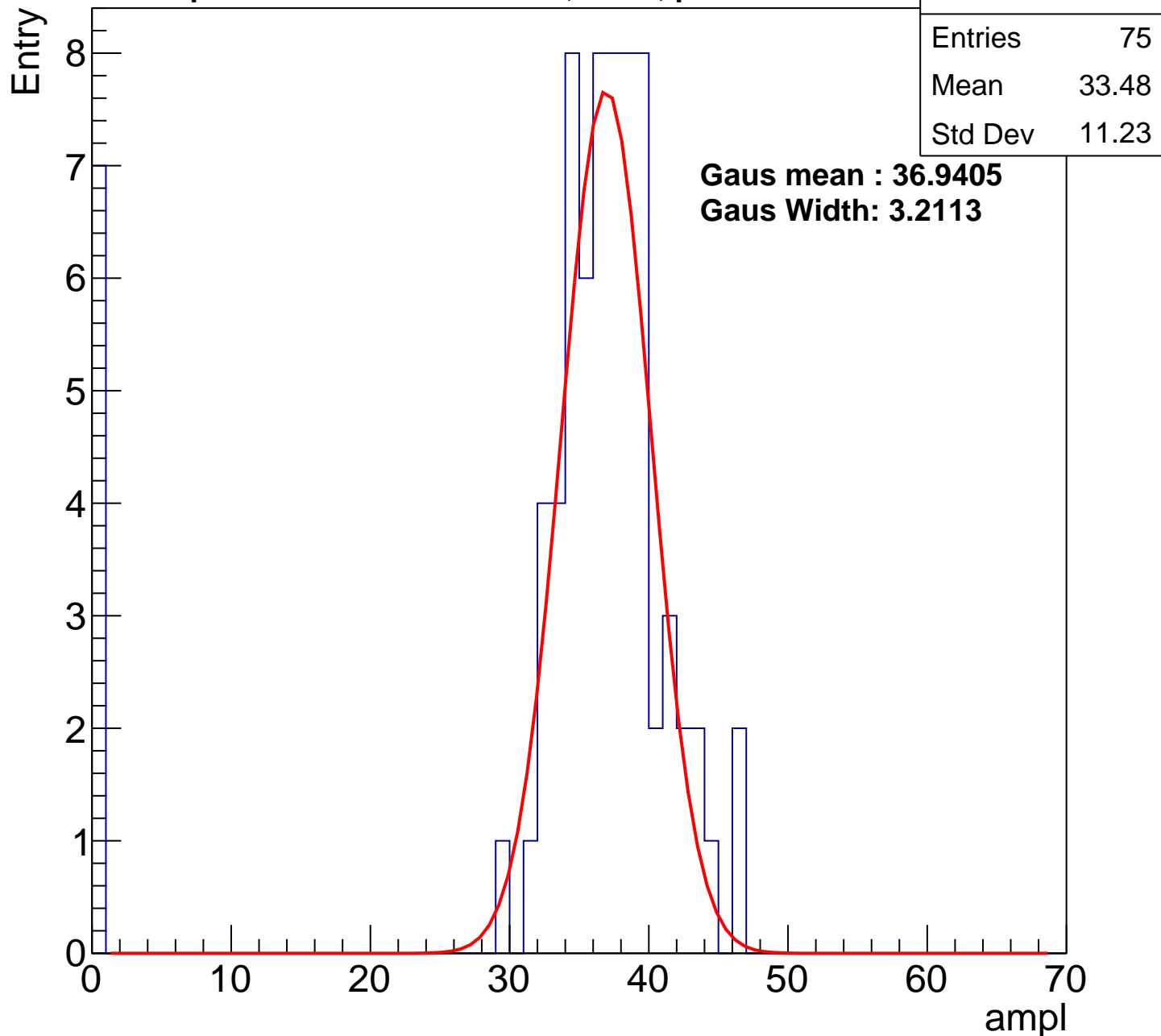
70

ampl



B1L103S, U13-ch67, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U13-ch67, adc2

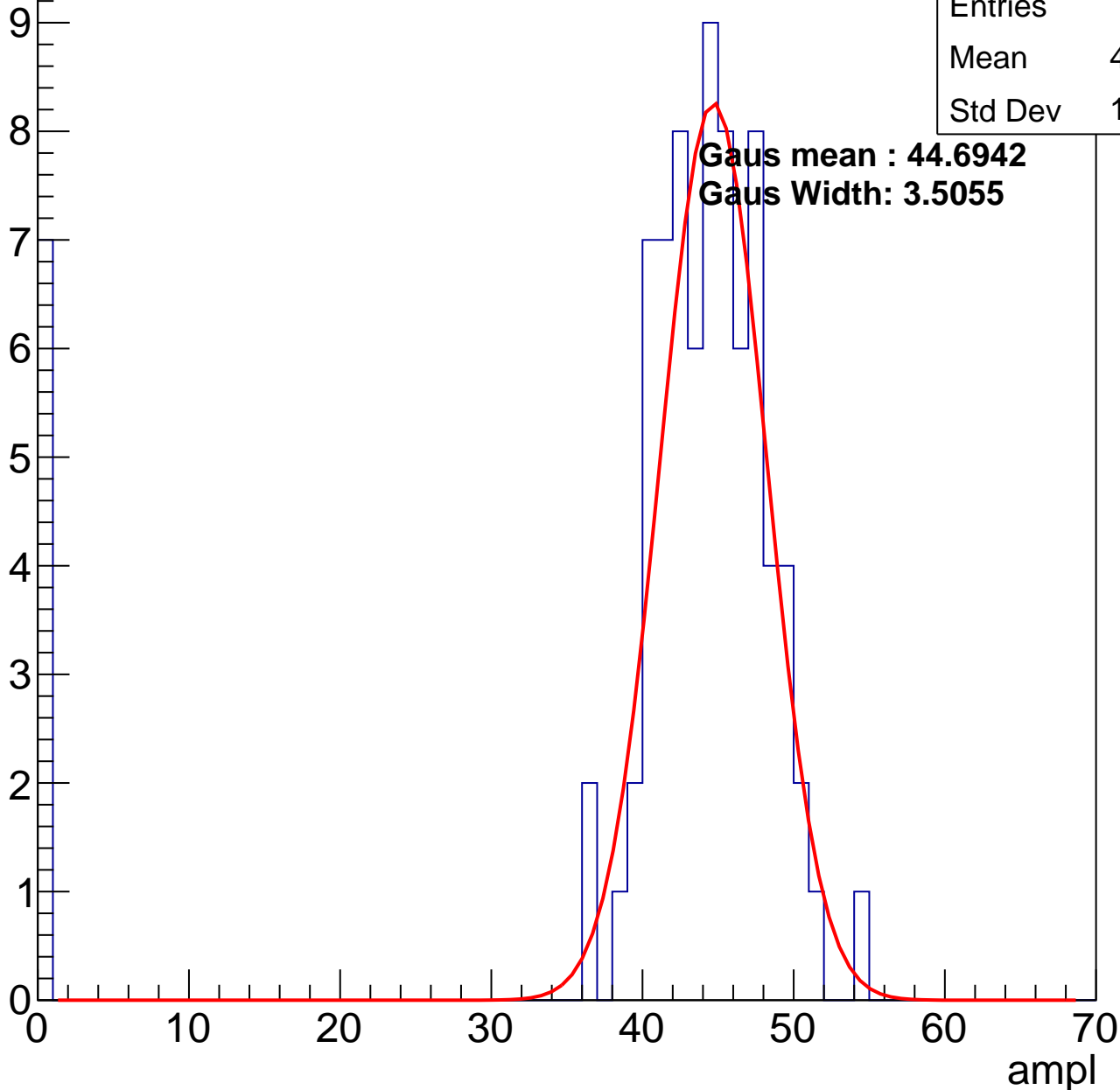
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	40.36
Std Dev	12.69

Gaus mean : 44.6942

Gaus Width: 3.5055

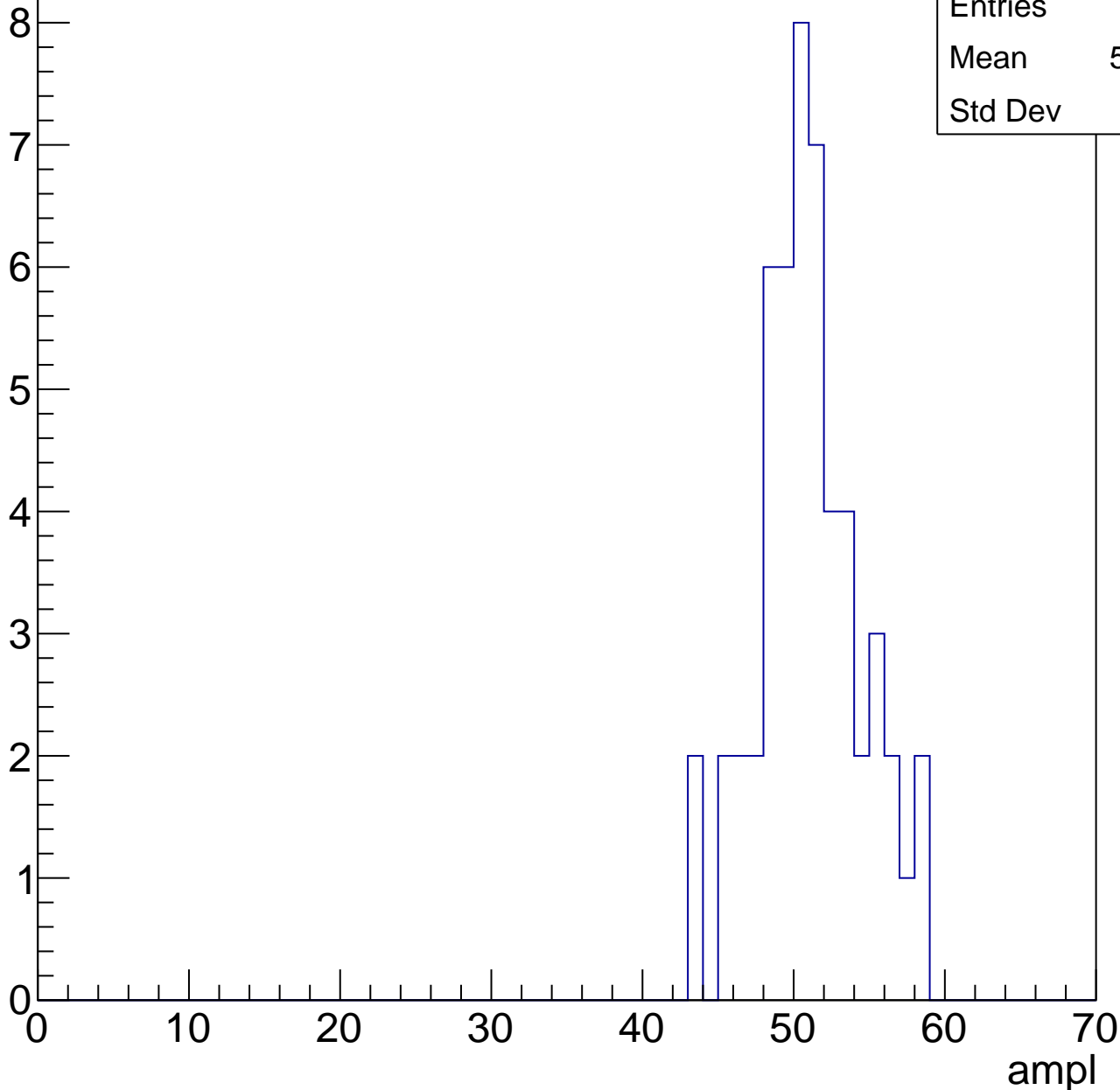


B1L103S, U13-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	50.55
Std Dev	3.44

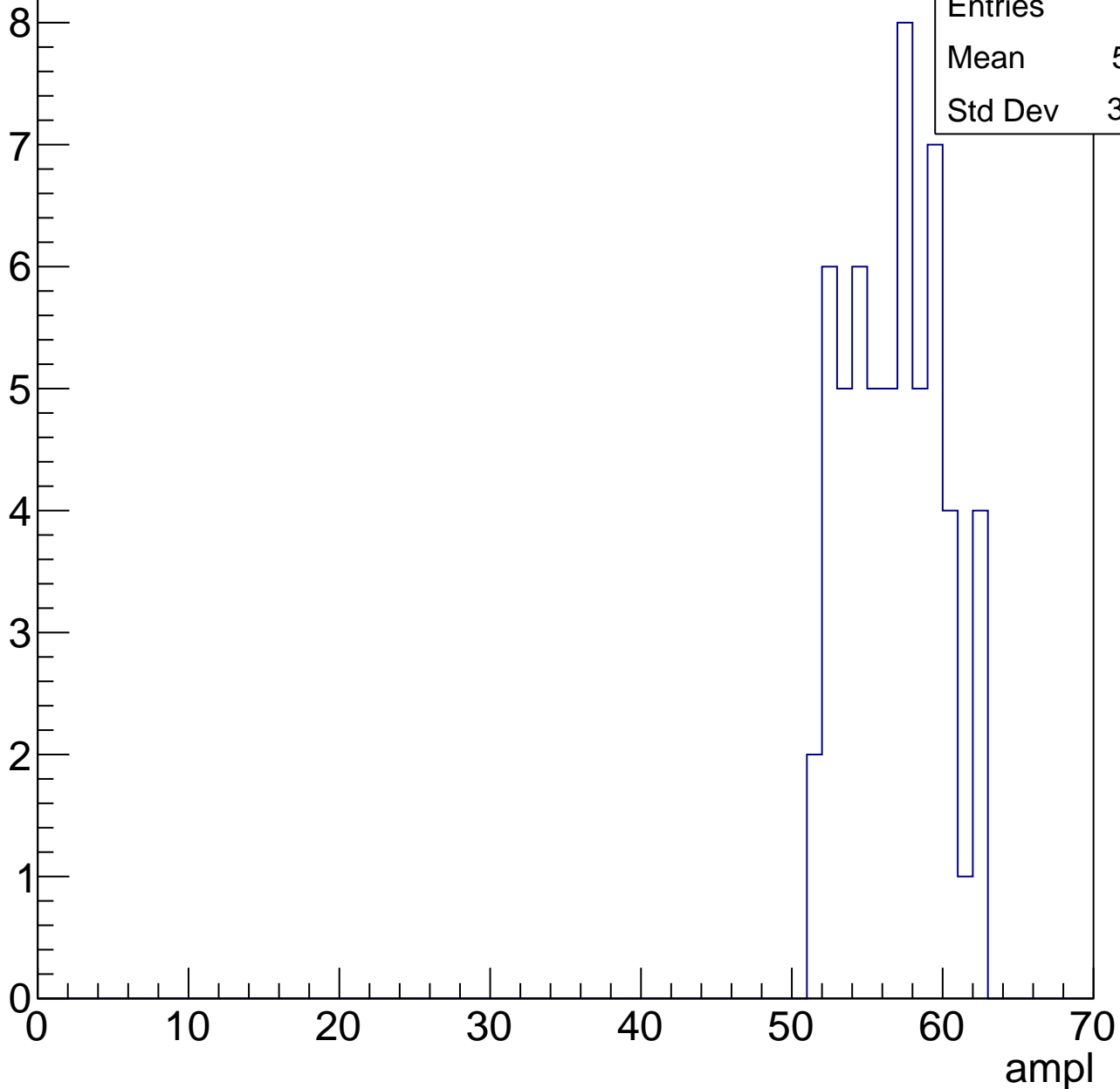


B1L103S, U13-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.31
Std Dev	3.047

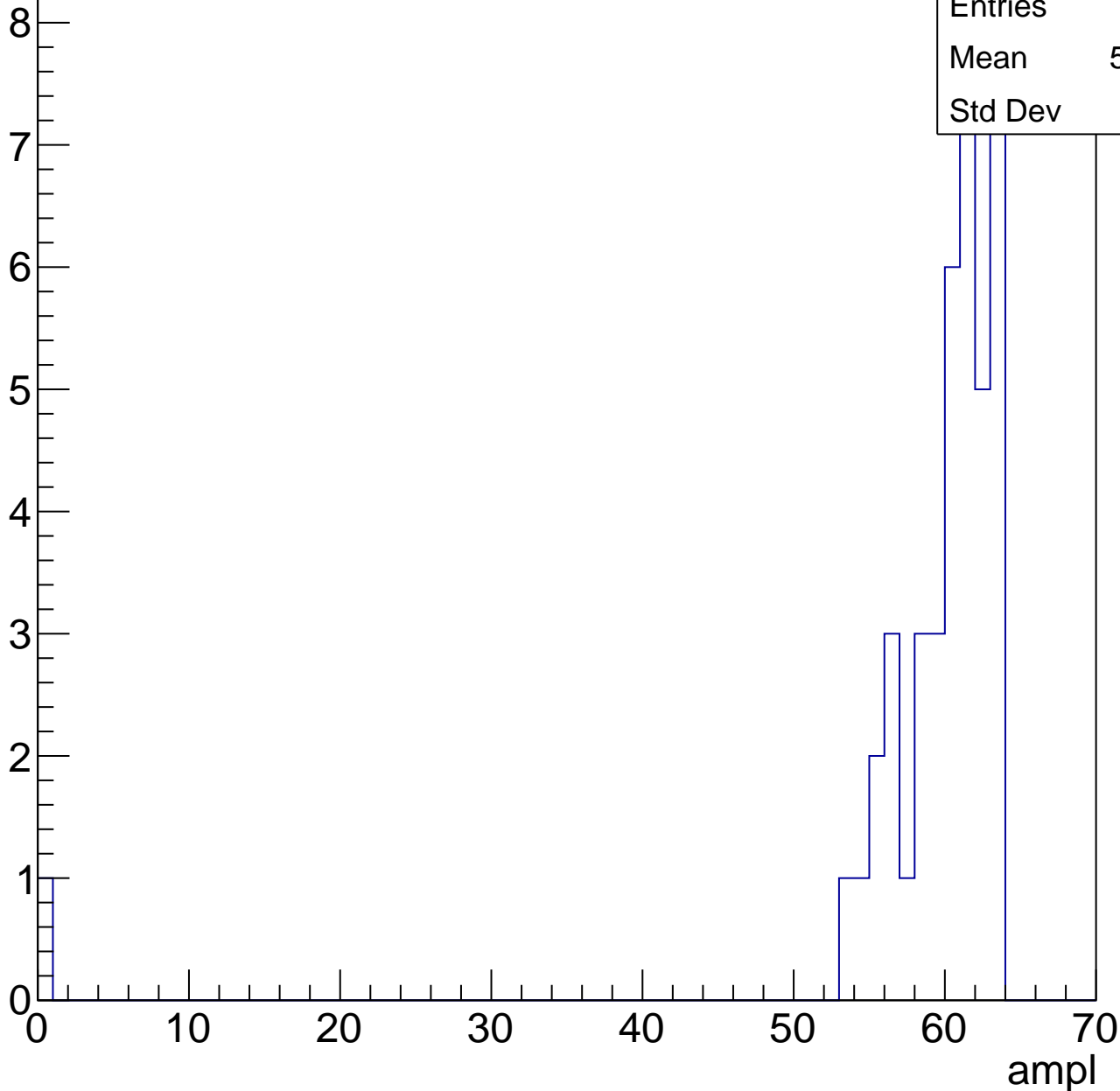


B1L103S, U13-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.45
Std Dev	9.52



B1L103S, U13-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch68, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	27.21
Std Dev	9.364

Gaus mean : 30.8023

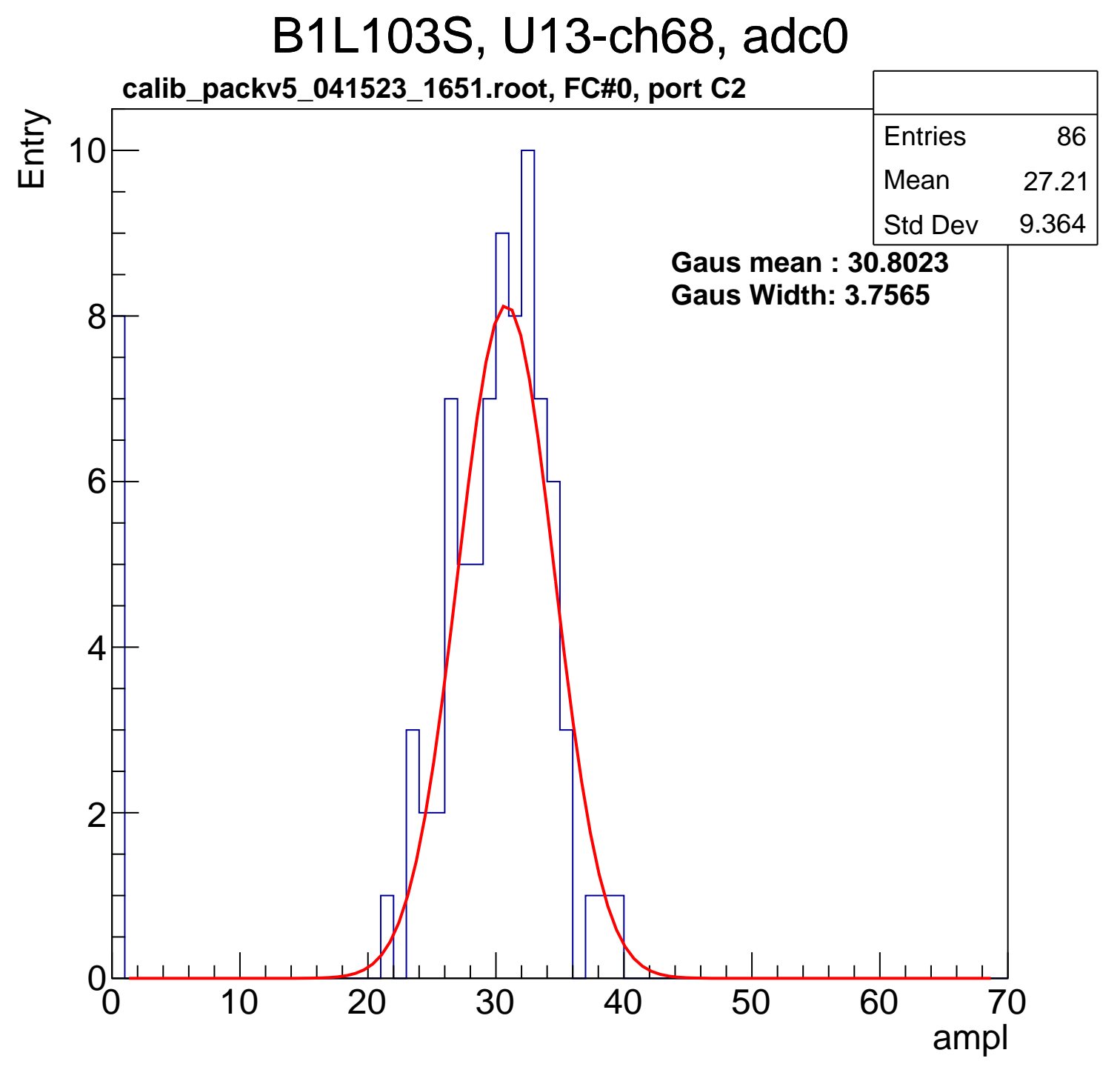
Gaus Width: 3.7565

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



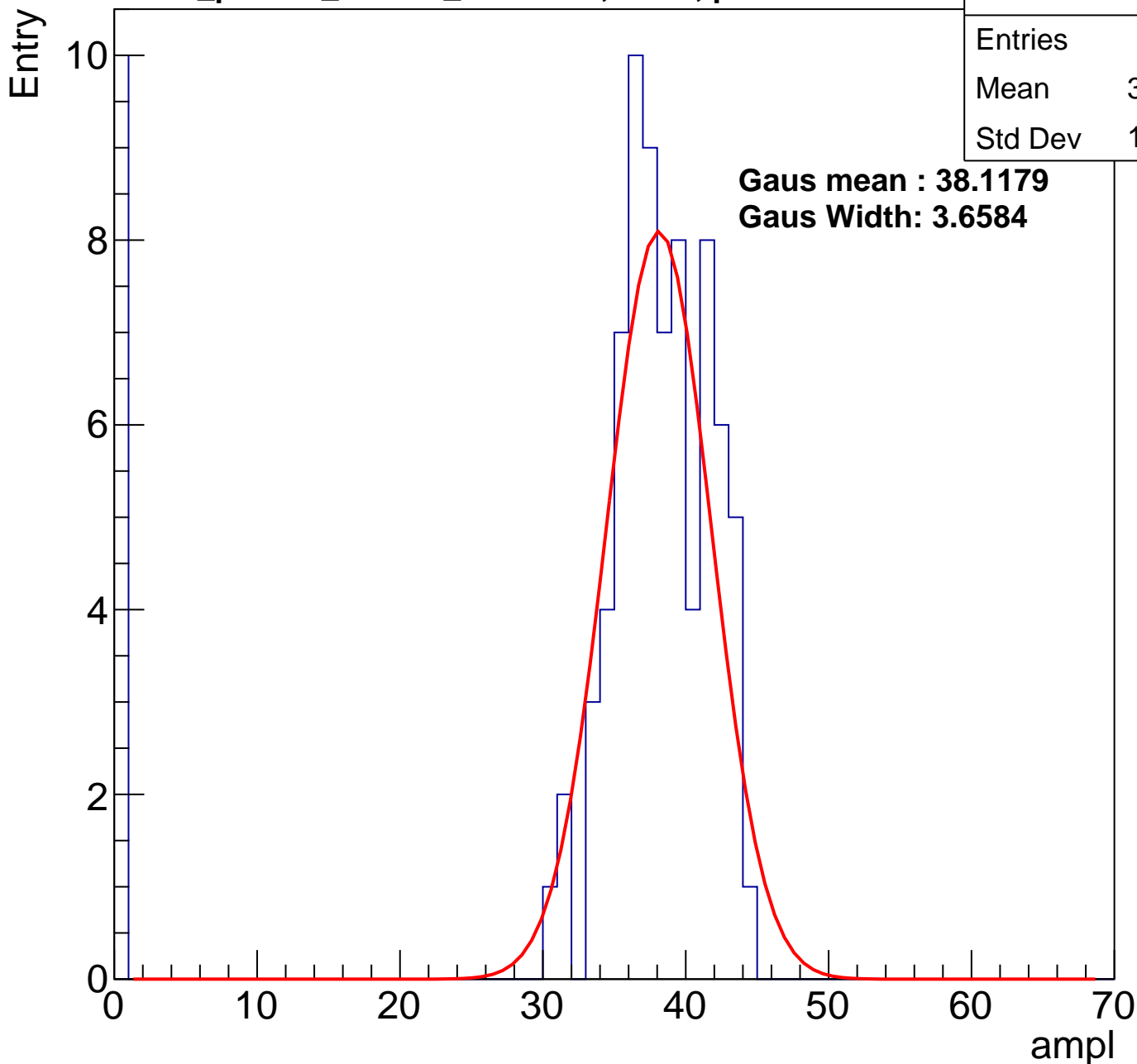
B1L103S, U13-ch68, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	33.44
Std Dev	12.57

Gaus mean : 38.1179

Gaus Width: 3.6584

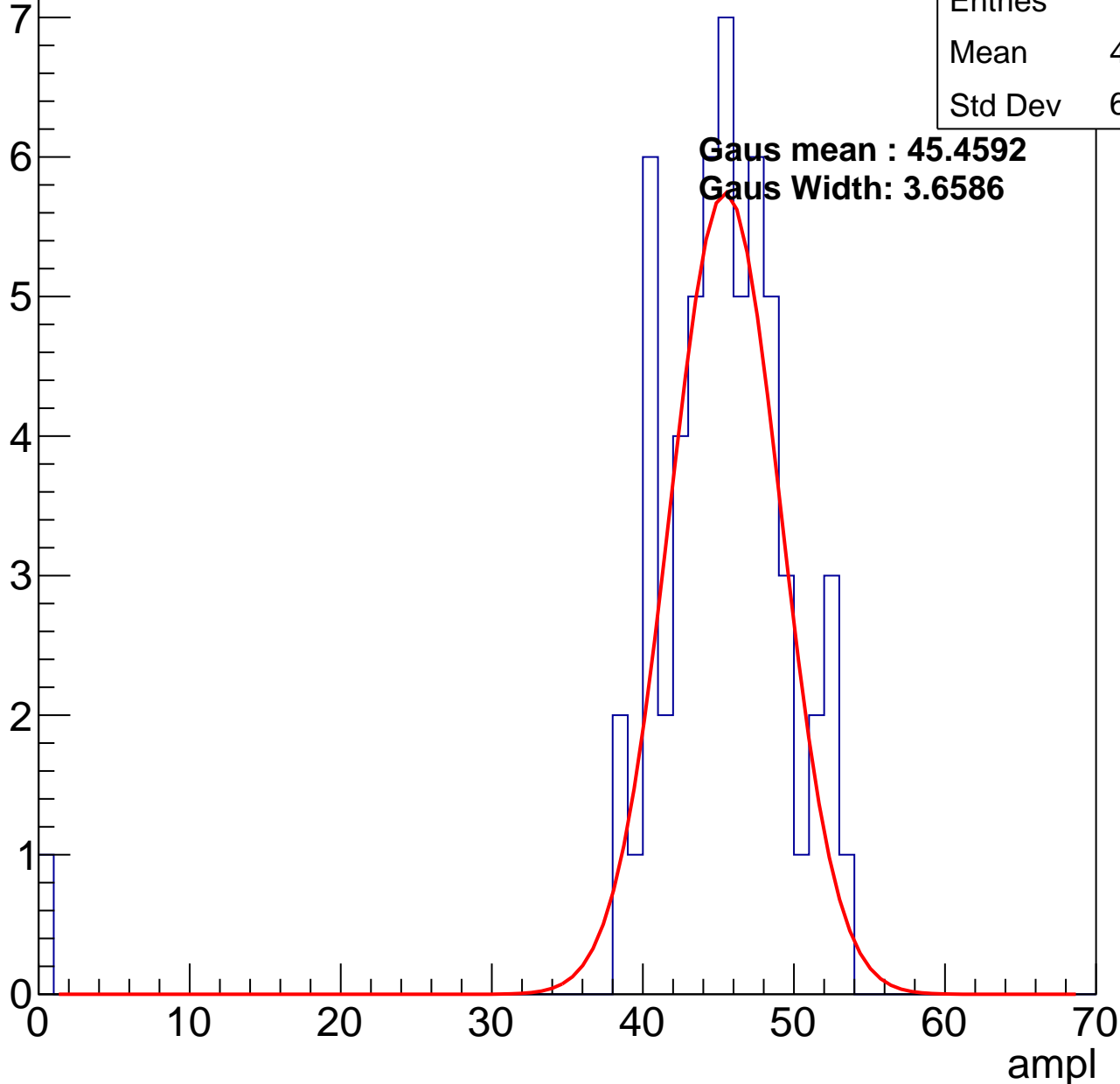


B1L103S, U13-ch68, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	44.32
Std Dev	6.837

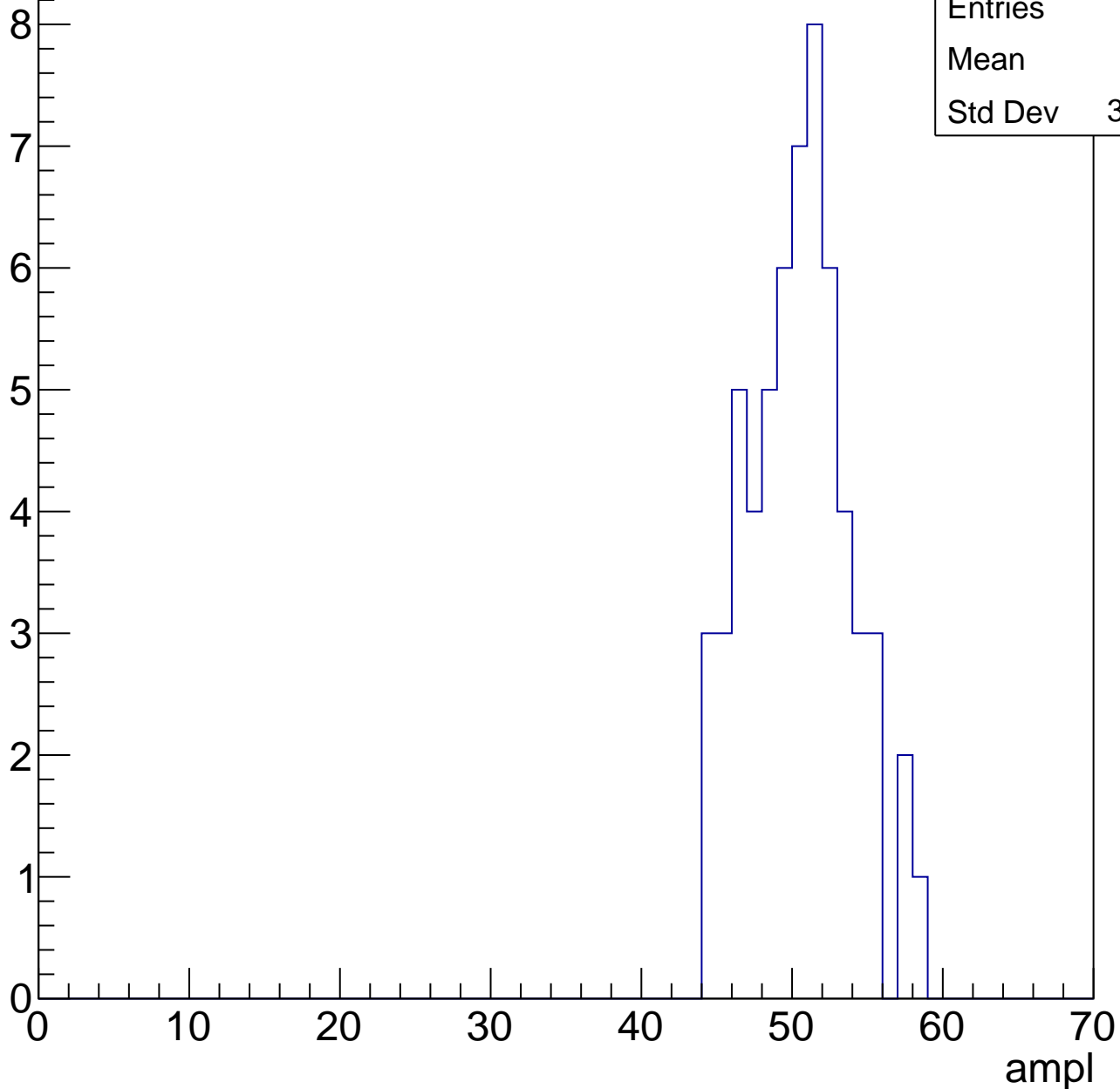


B1L103S, U13-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	50
Std Dev	3.362

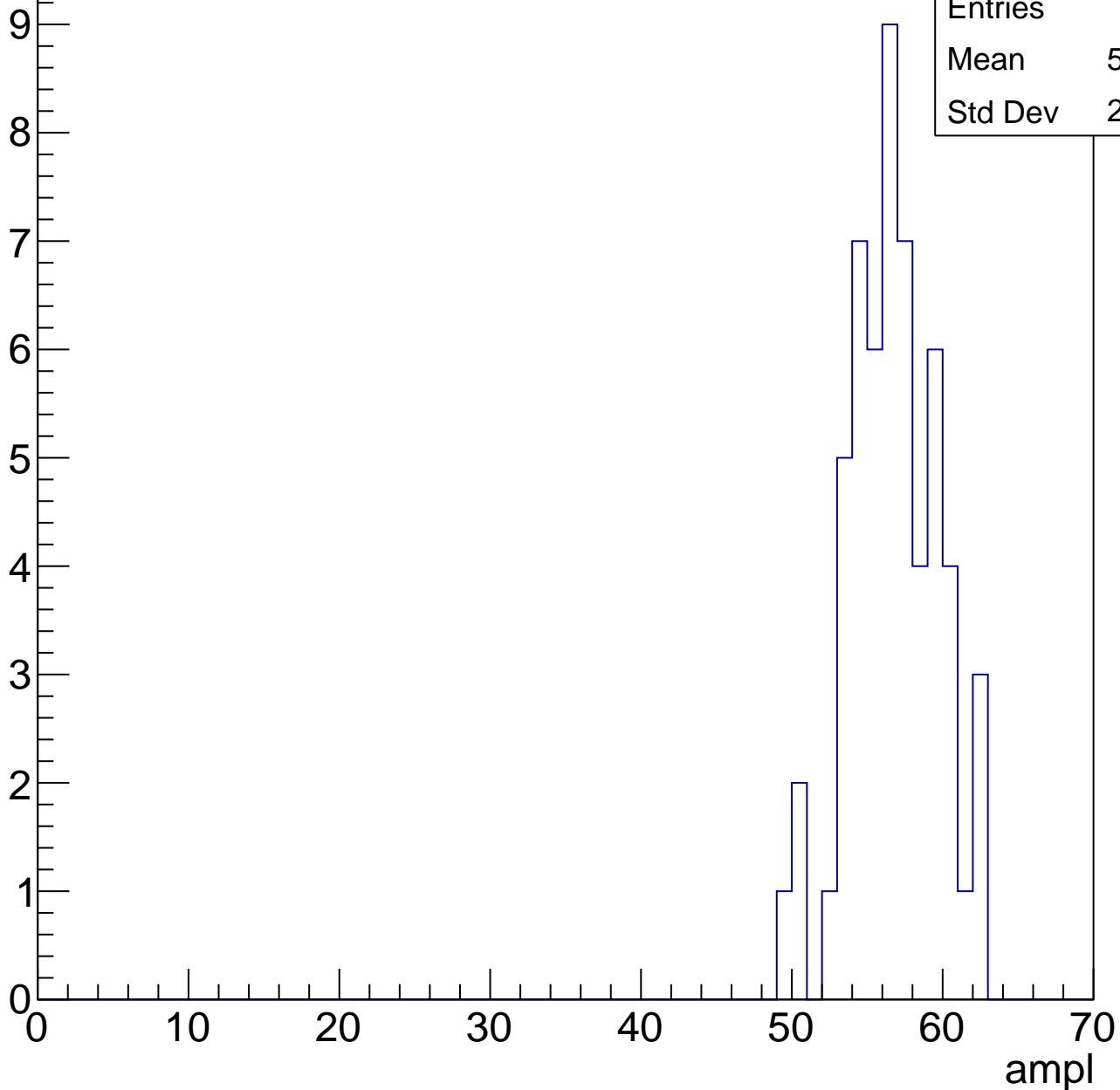


B1L103S, U13-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	56.25
Std Dev	2.947



B1L103S, U13-ch68, adc5

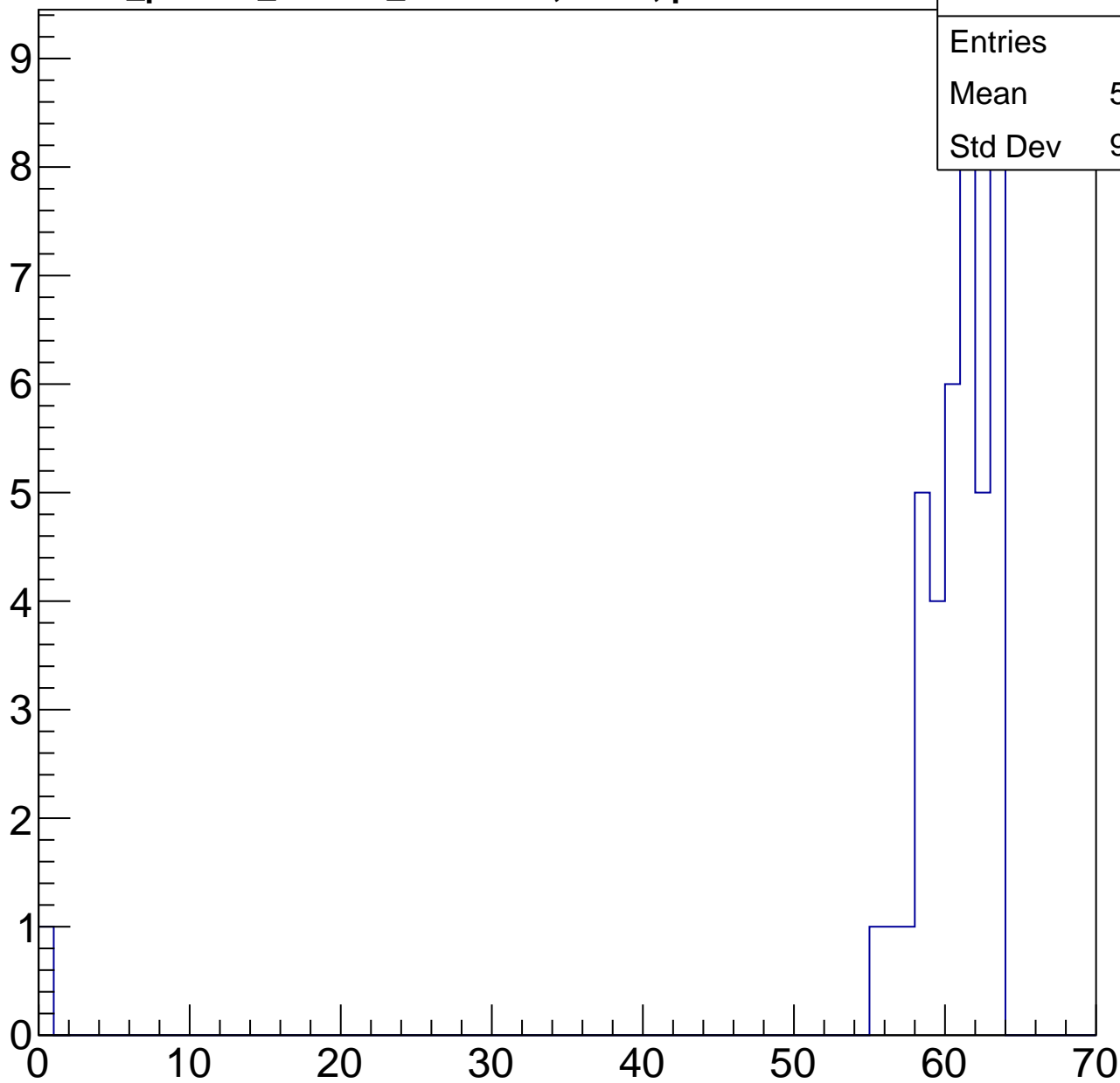
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.95
Std Dev	9.538

ampl



B1L103S, U13-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B1L103S, U13-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch69, adc0

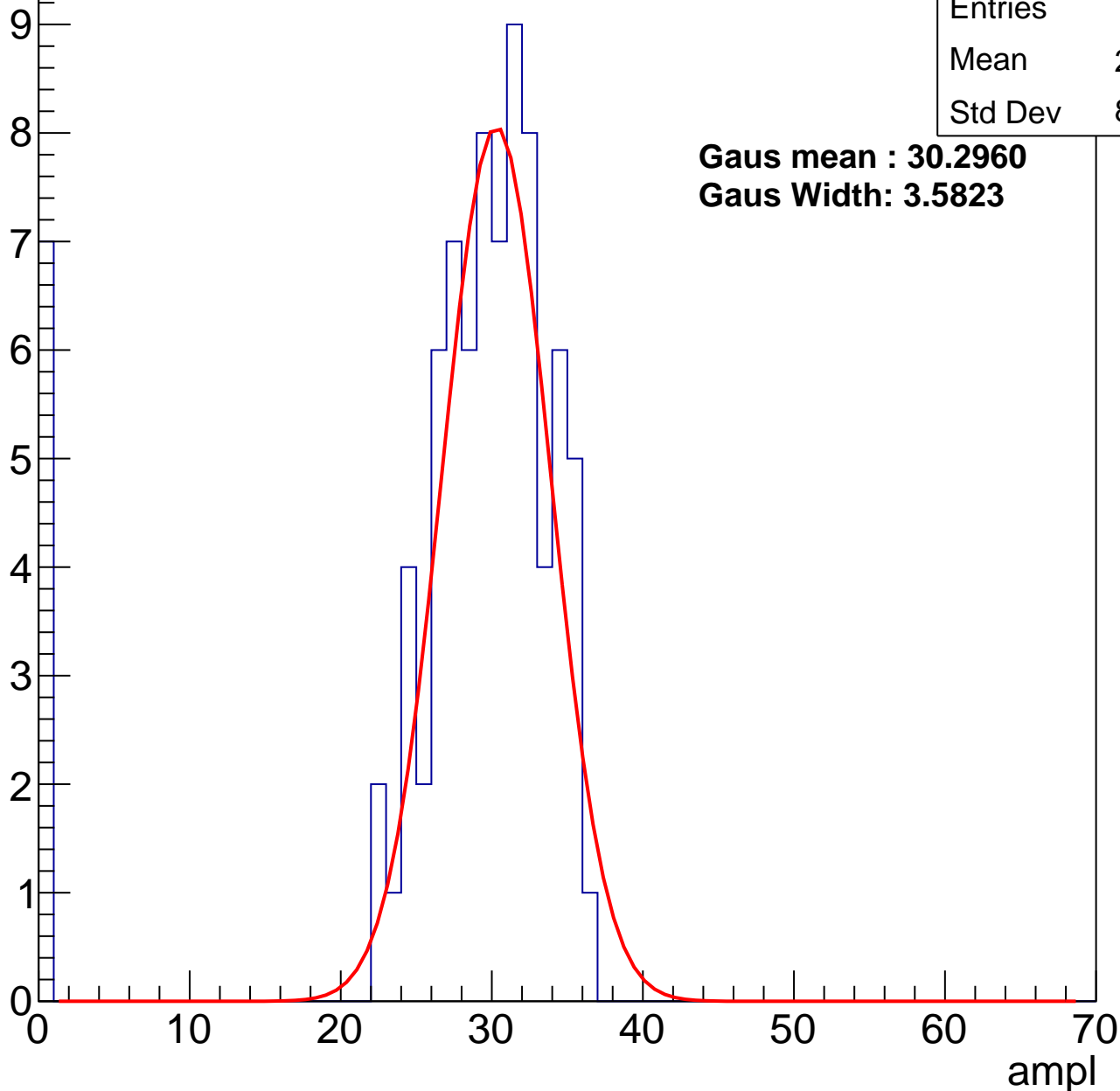
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	27.11
Std Dev	8.851

Gaus mean : 30.2960

Gaus Width: 3.5823



B1L103S, U13-ch69, adc1

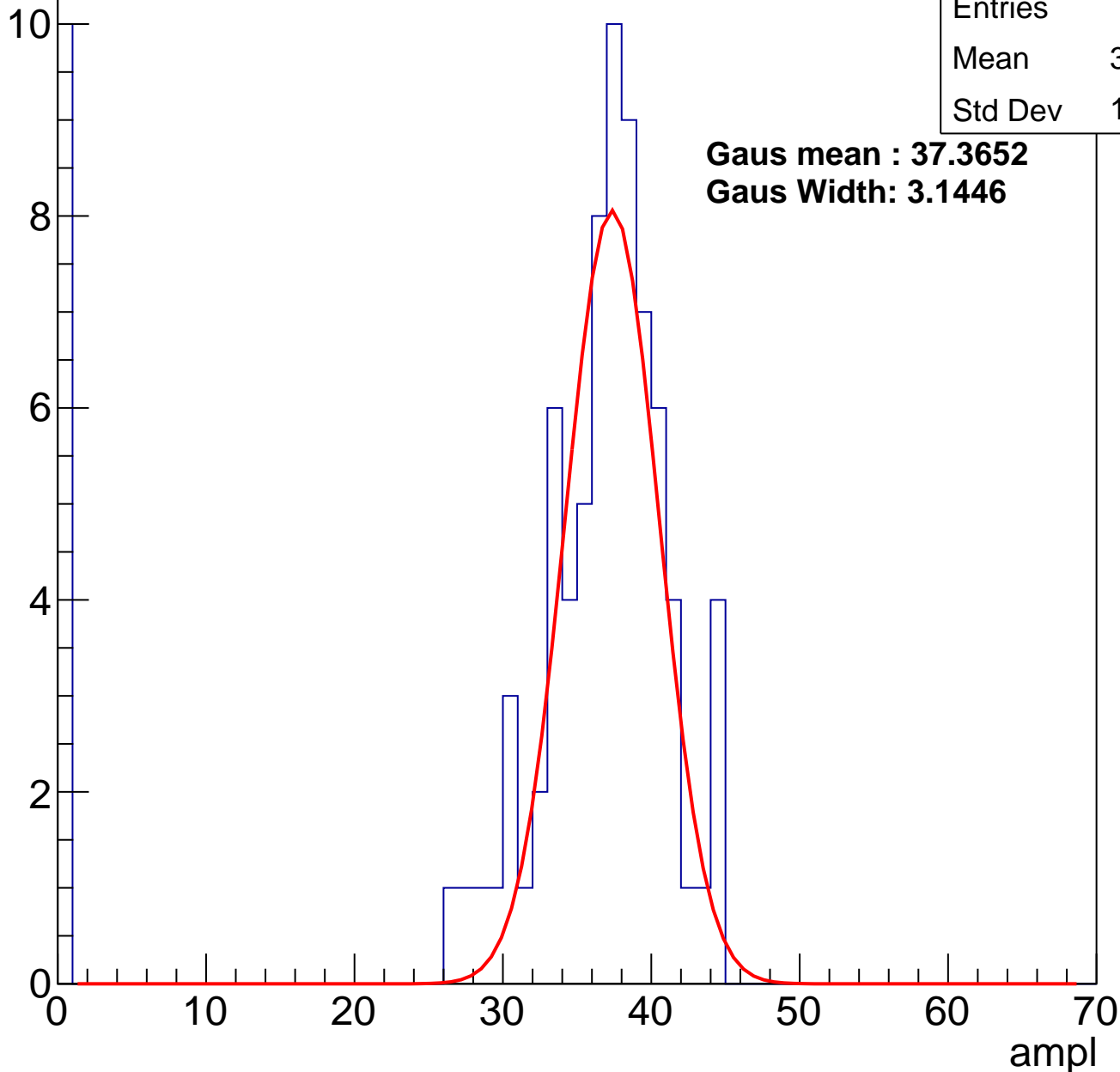
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	32.26
Std Dev	12.34

Gaus mean : 37.3652

Gaus Width: 3.1446

Entry



B1L103S, U13-ch69, adc2

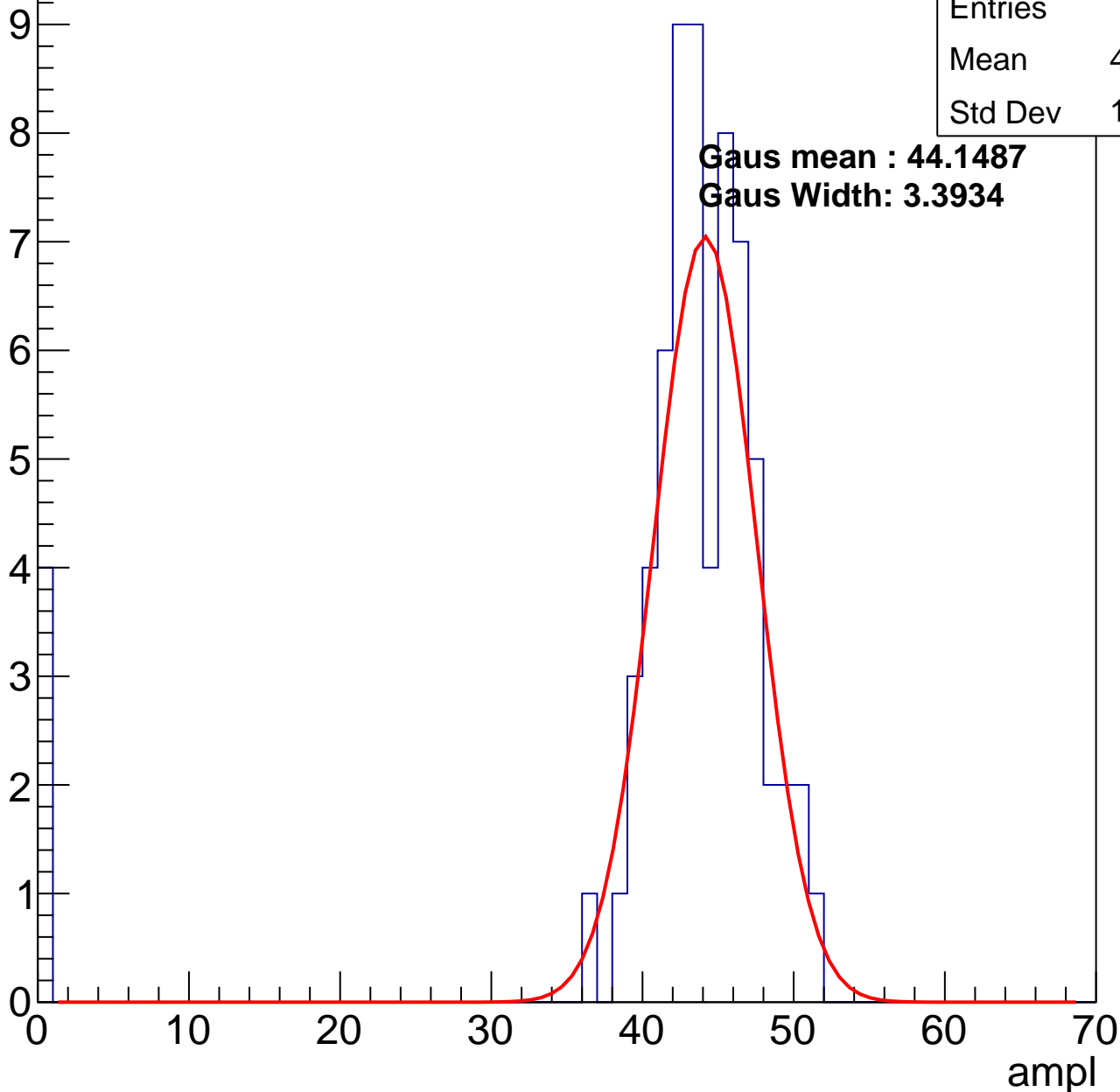
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.18
Std Dev	10.73

Gaus mean : 44.1487

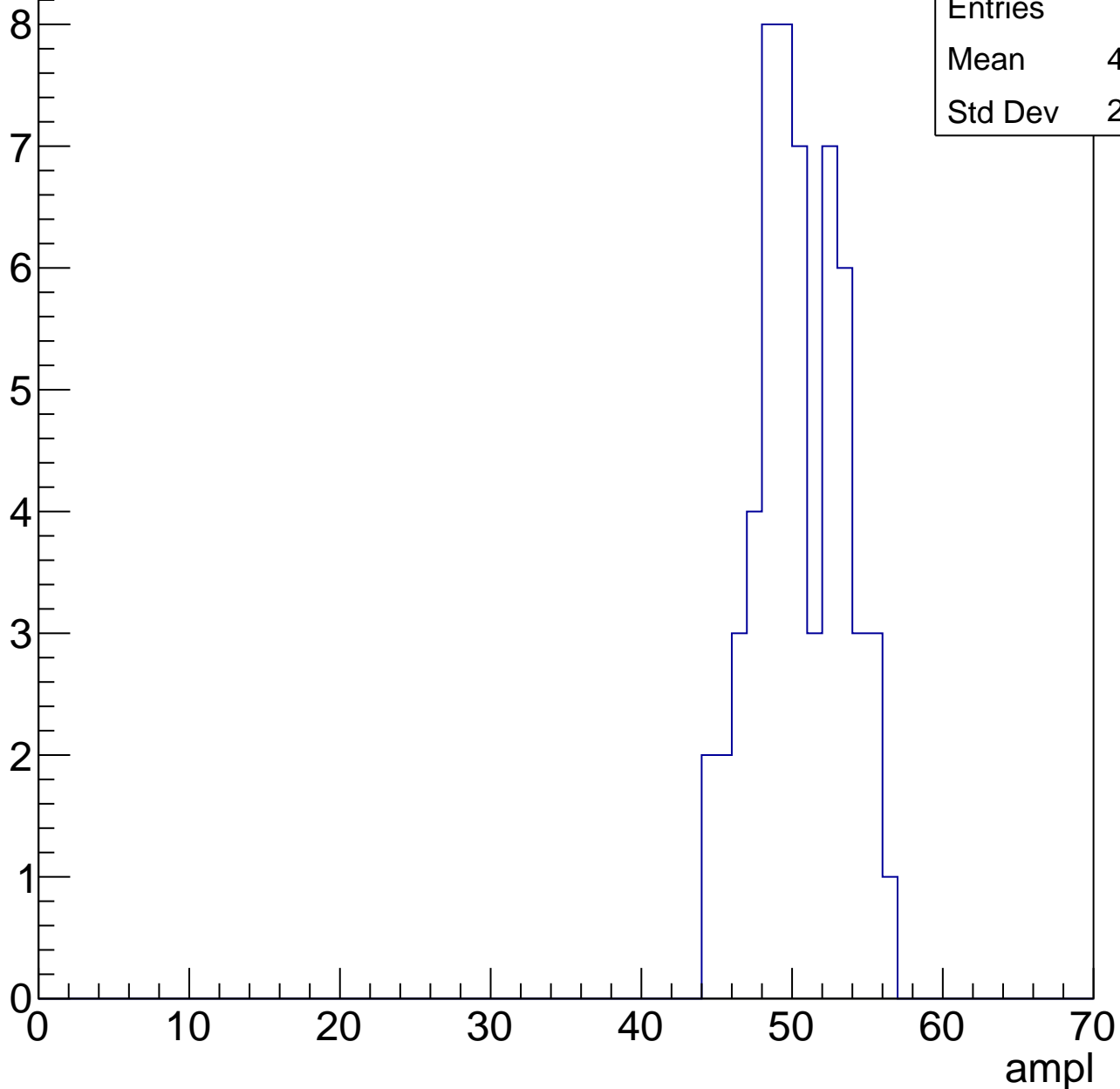
Gaus Width: 3.3934



B1L103S, U13-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

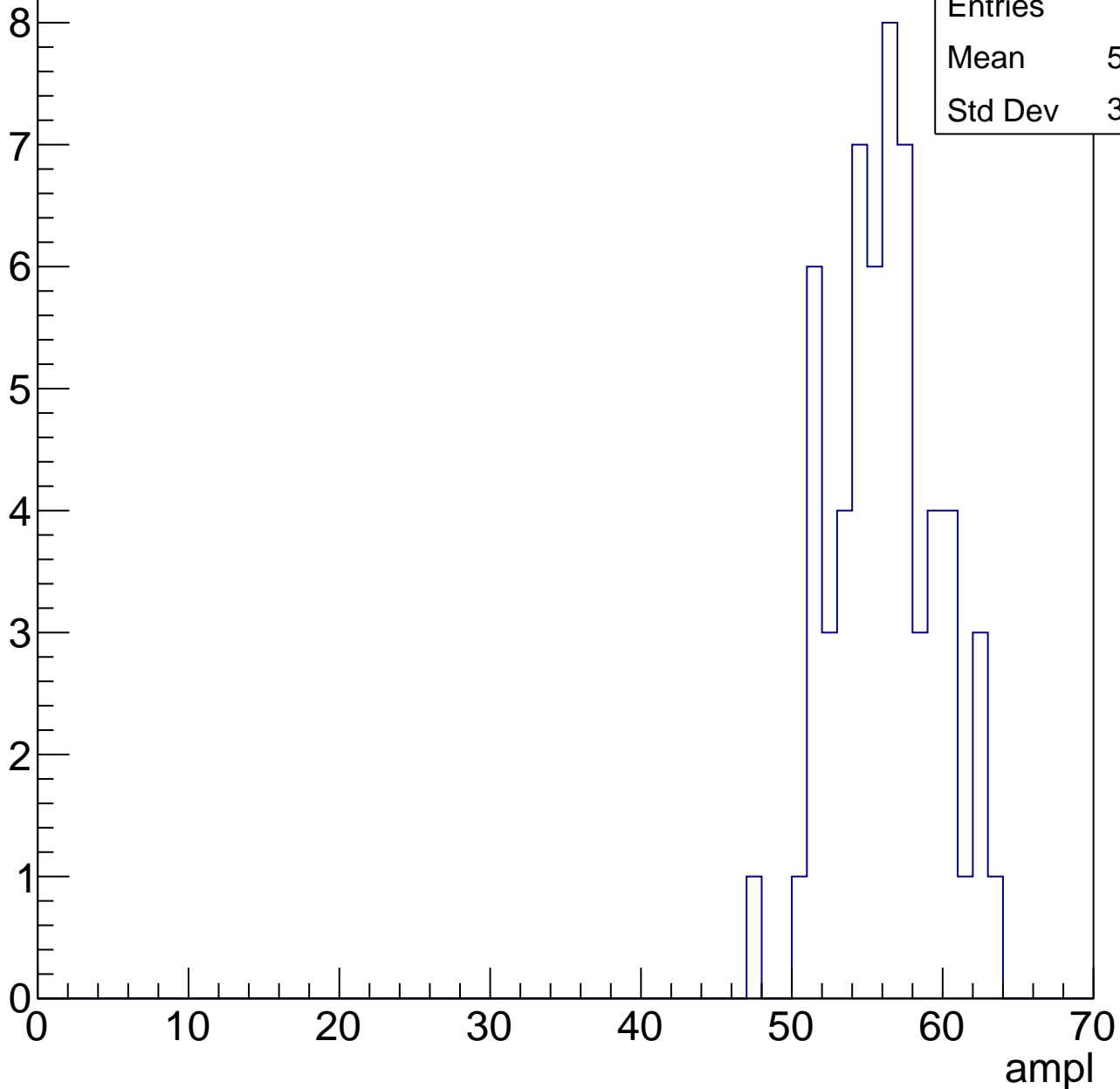


B1L103S, U13-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	55.69
Std Dev	3.396

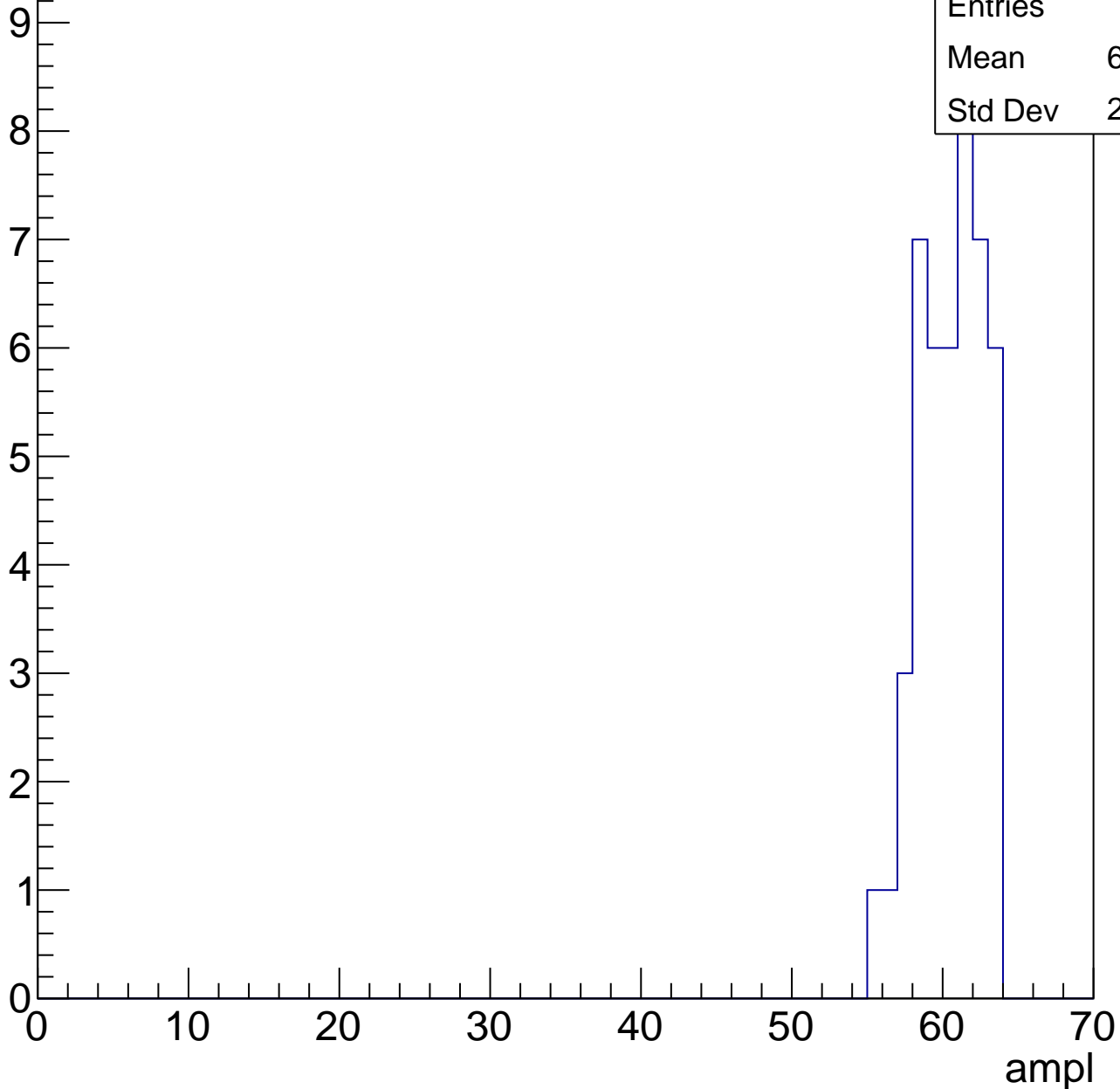


B1L103S, U13-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

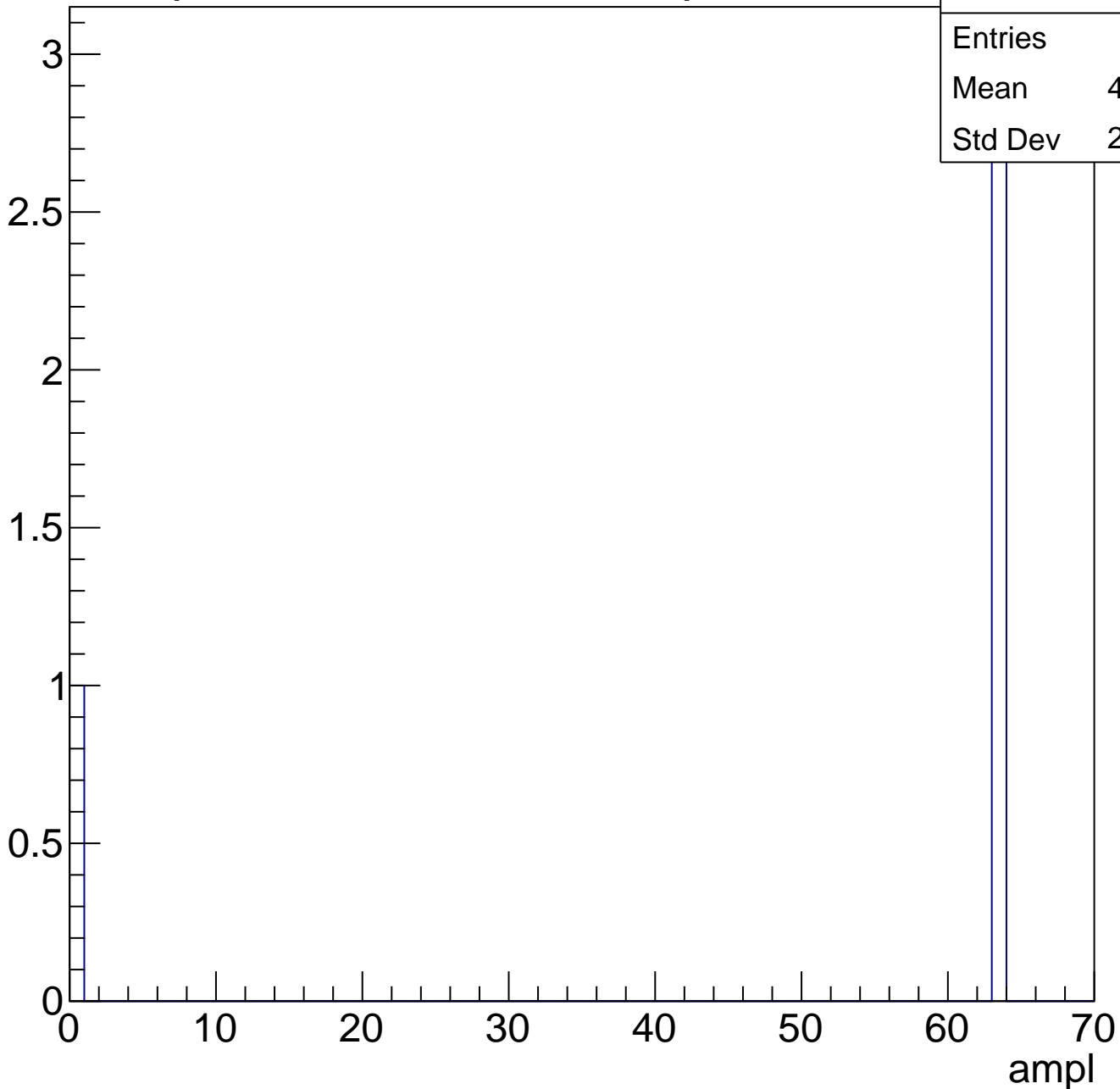
Entries	46
Mean	60.07
Std Dev	2.047



B1L103S, U13-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U13-ch70, adc0

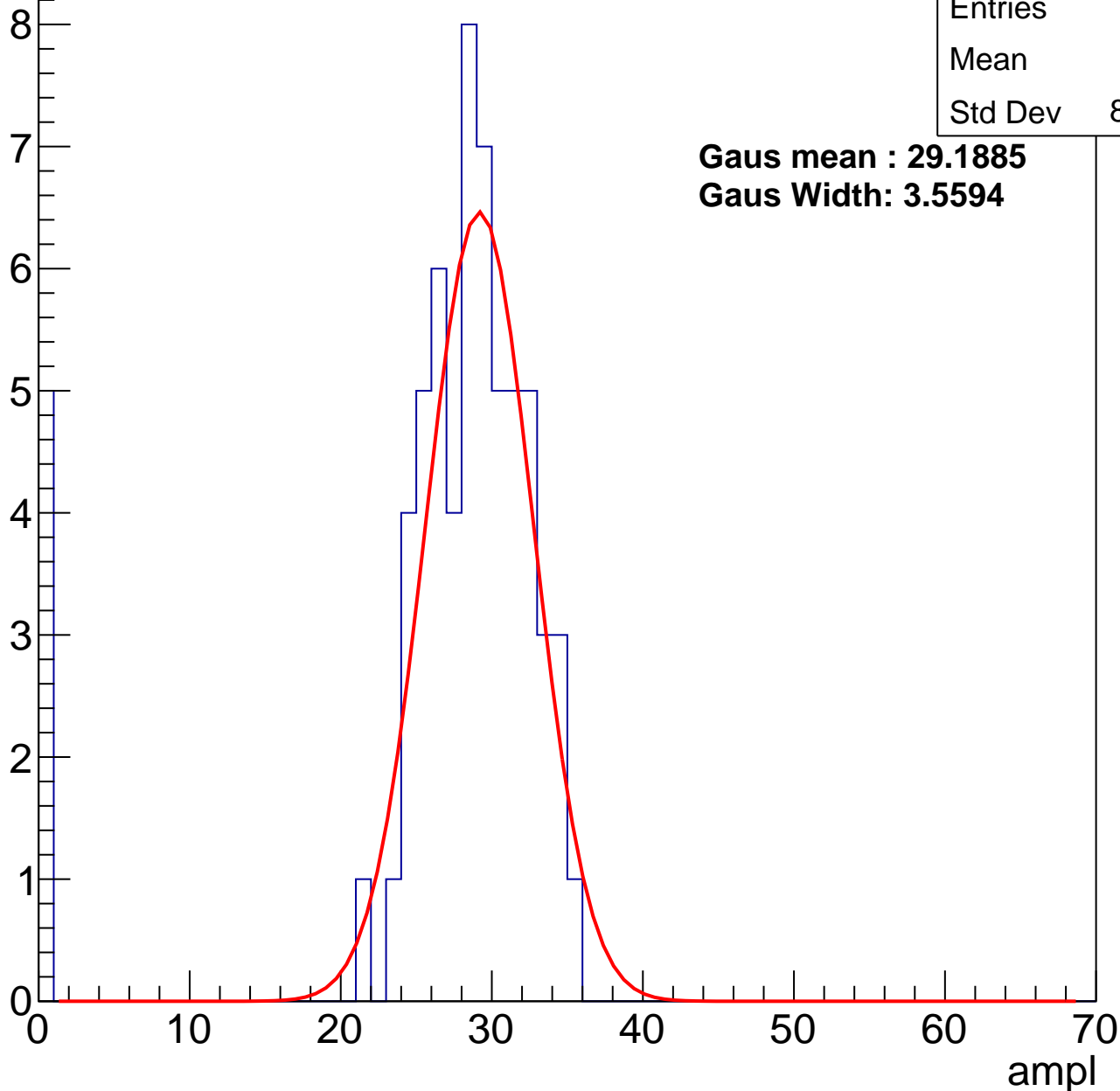
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	26.3
Std Dev	8.293

Gaus mean : 29.1885

Gaus Width: 3.5594



B1L103S, U13-ch70, adc1

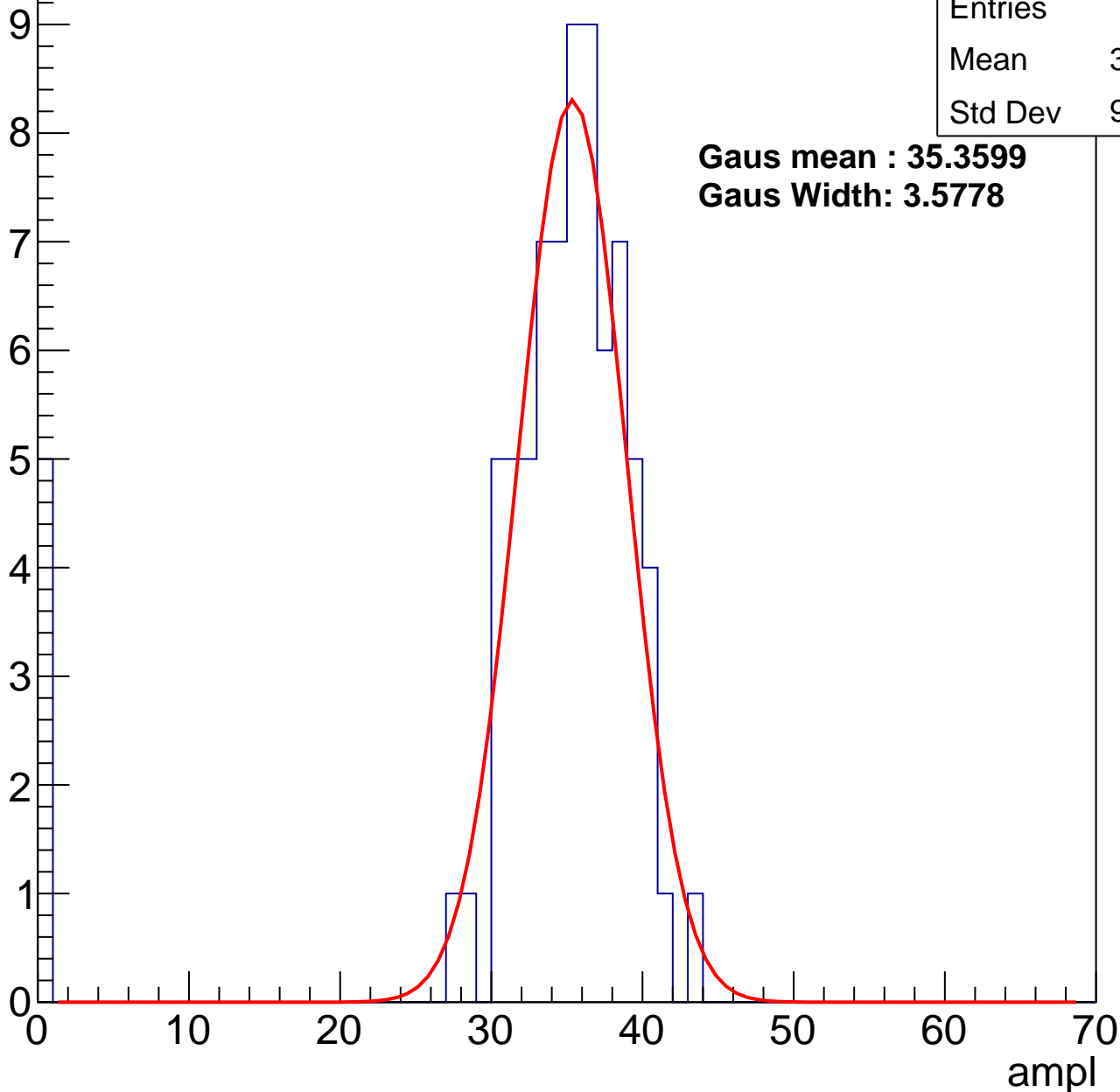
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	32.76
Std Dev	9.133

Gaus mean : 35.3599

Gaus Width: 3.5778



B1L103S, U13-ch70, adc2

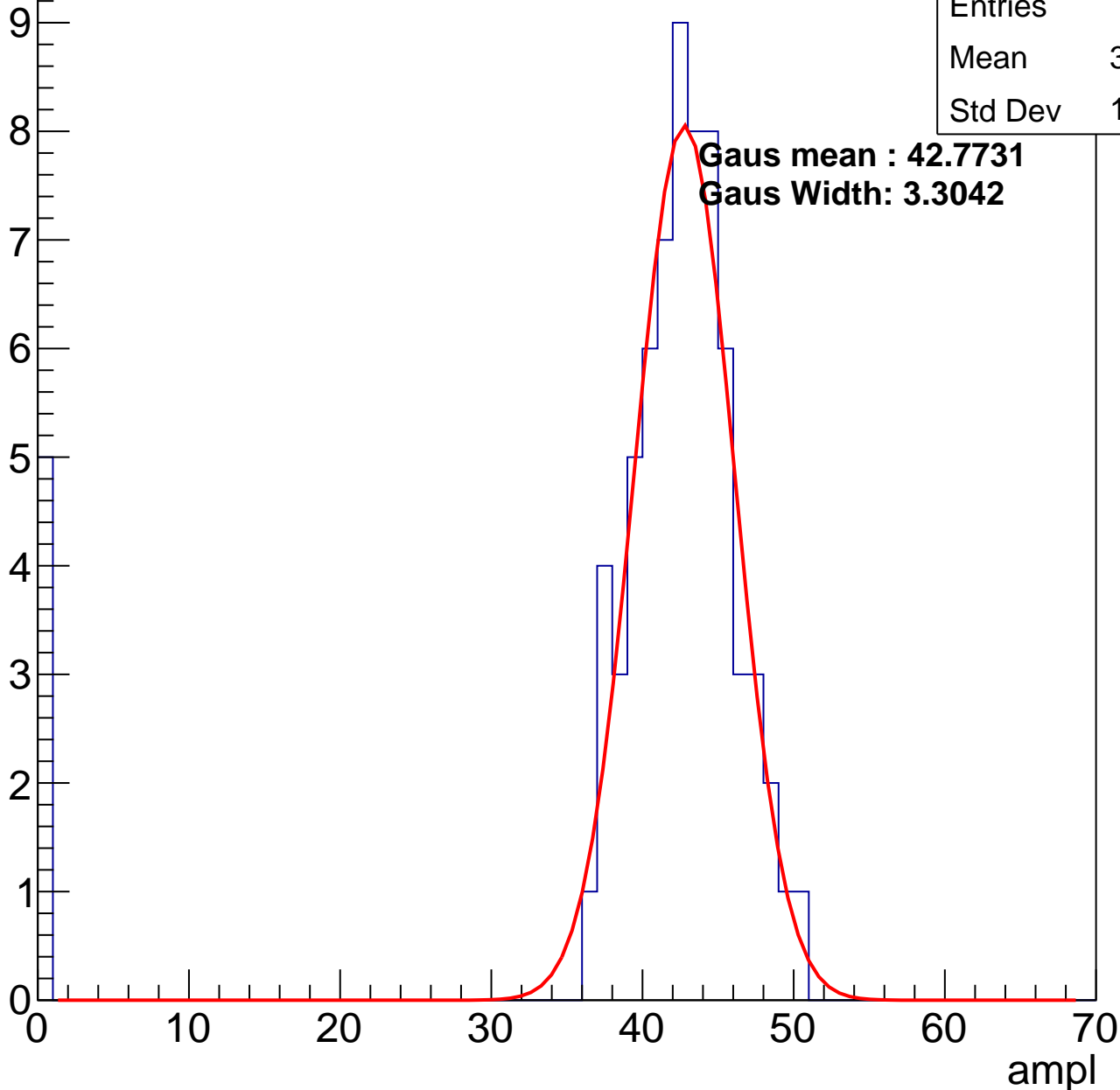
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	39.42
Std Dev	11.18

Gaus mean : 42.7731

Gaus Width: 3.3042

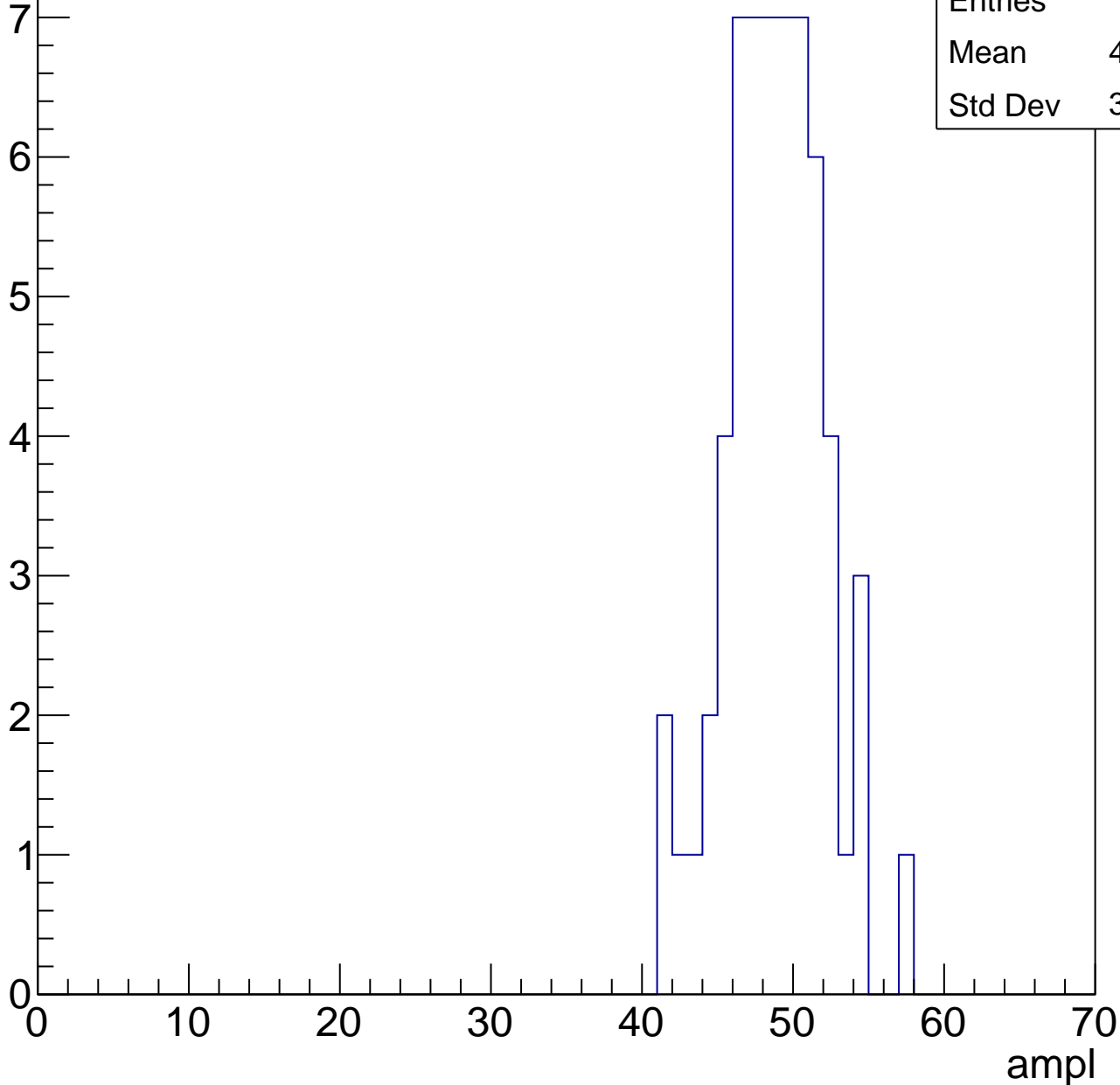


B1L103S, U13-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

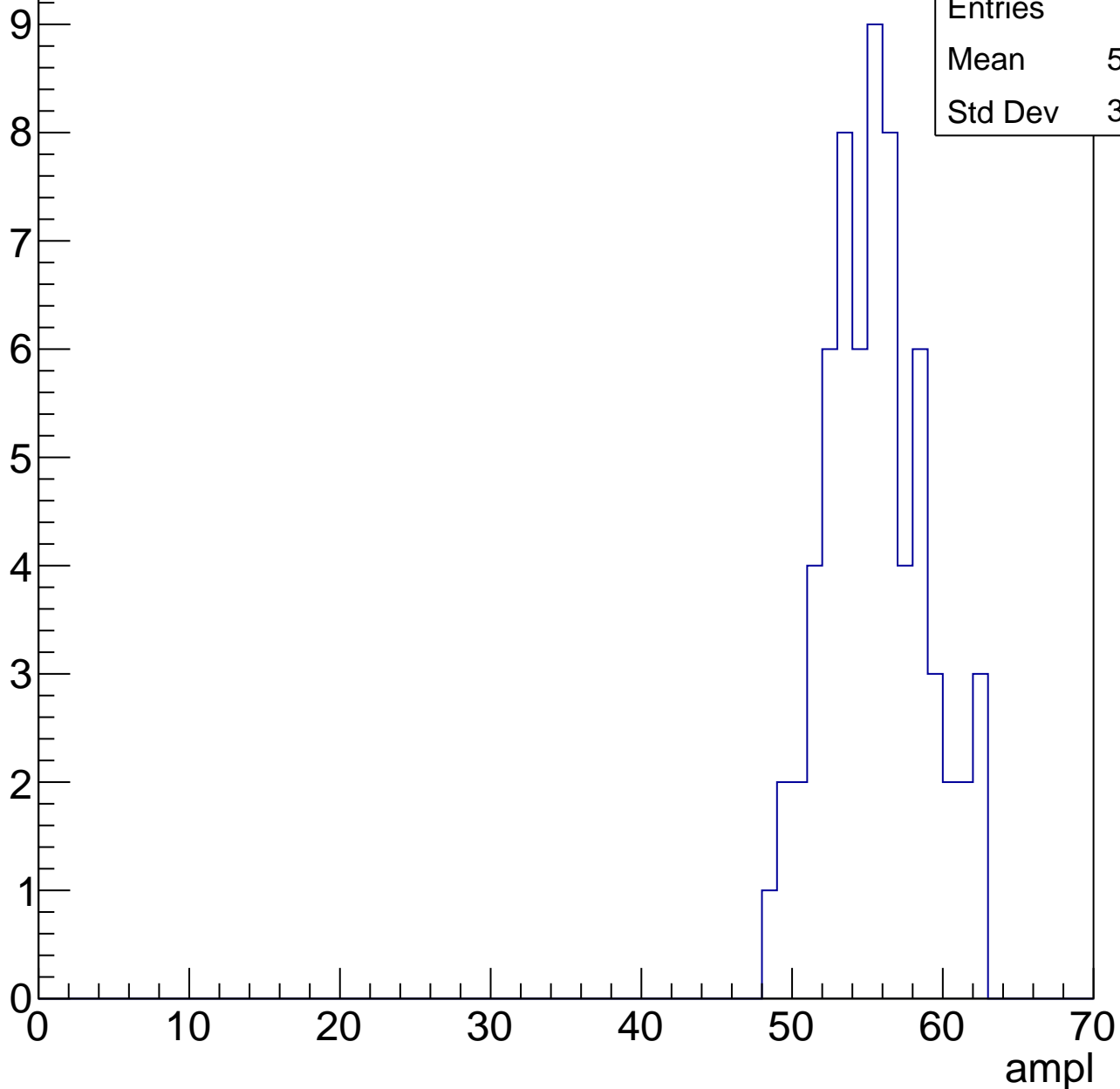
Entries	60
Mean	48.35
Std Dev	3.219



B1L103S, U13-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

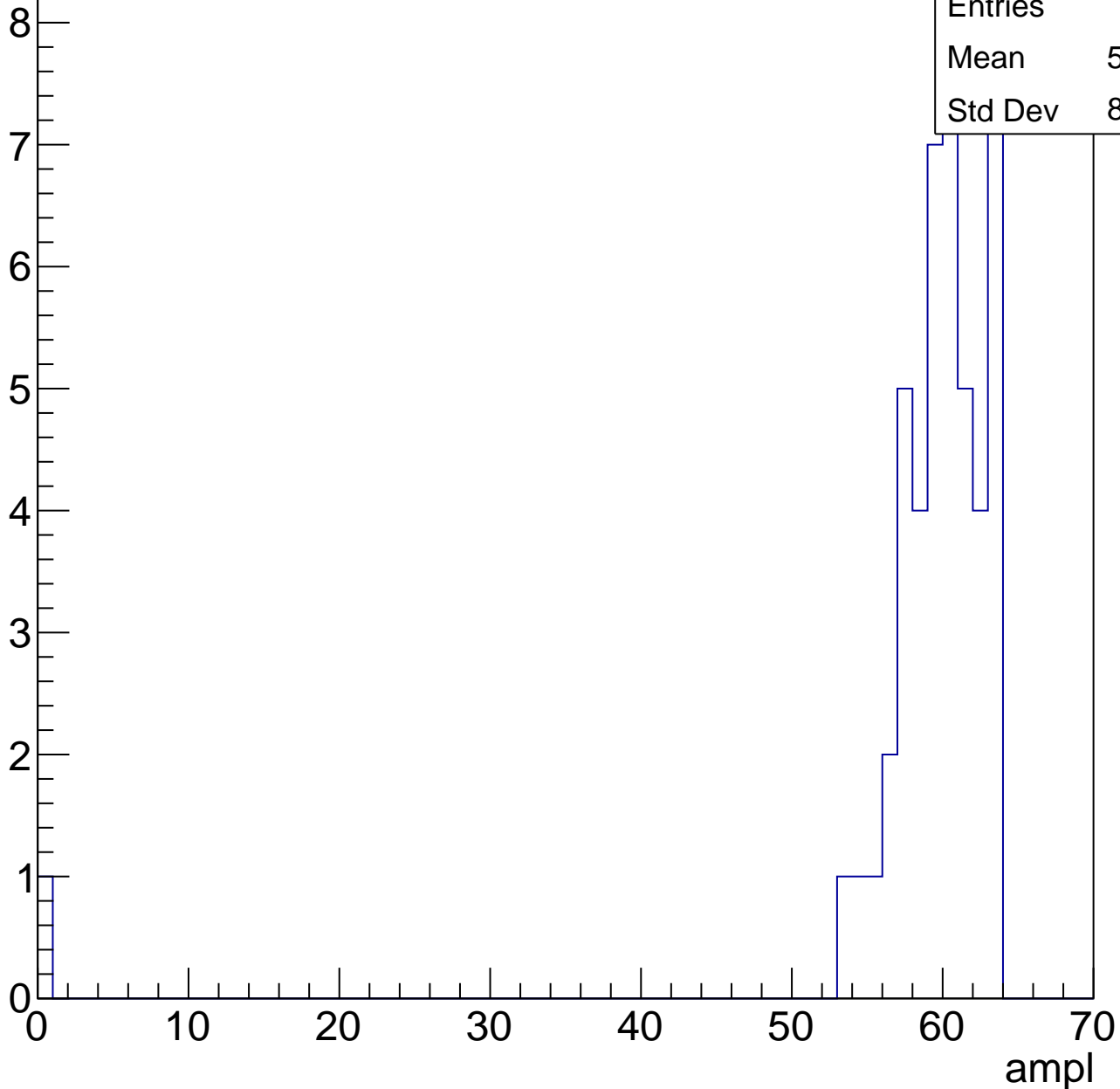


B1L103S, U13-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.32
Std Dev	8.956



B1L103S, U13-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.88

Std Dev

0.7806

B1L103S, U13-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch71, adc0

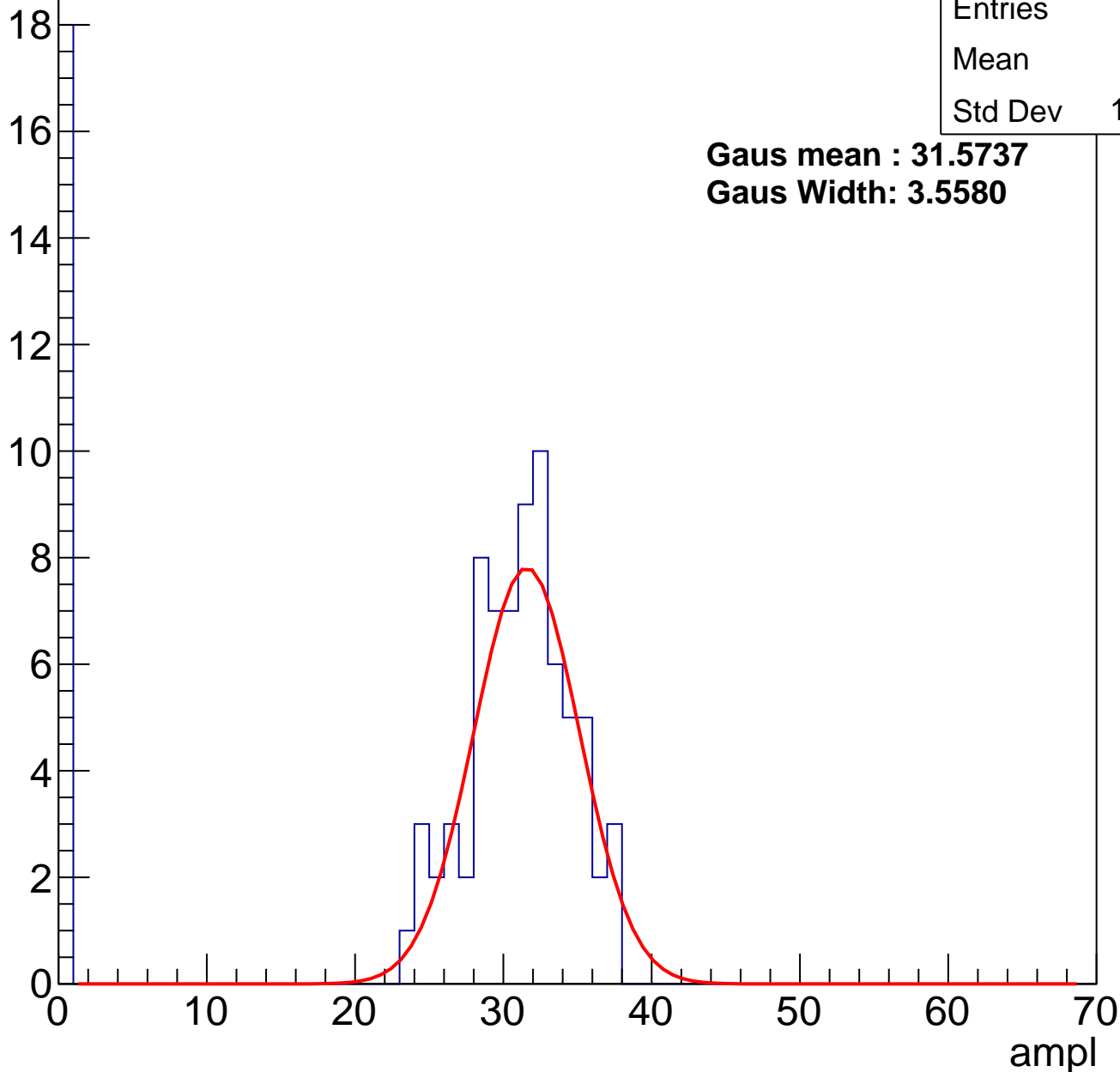
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	24.6
Std Dev	12.57

Gaus mean : 31.5737

Gaus Width: 3.5580

Entry



B1L103S, U13-ch71, adc1

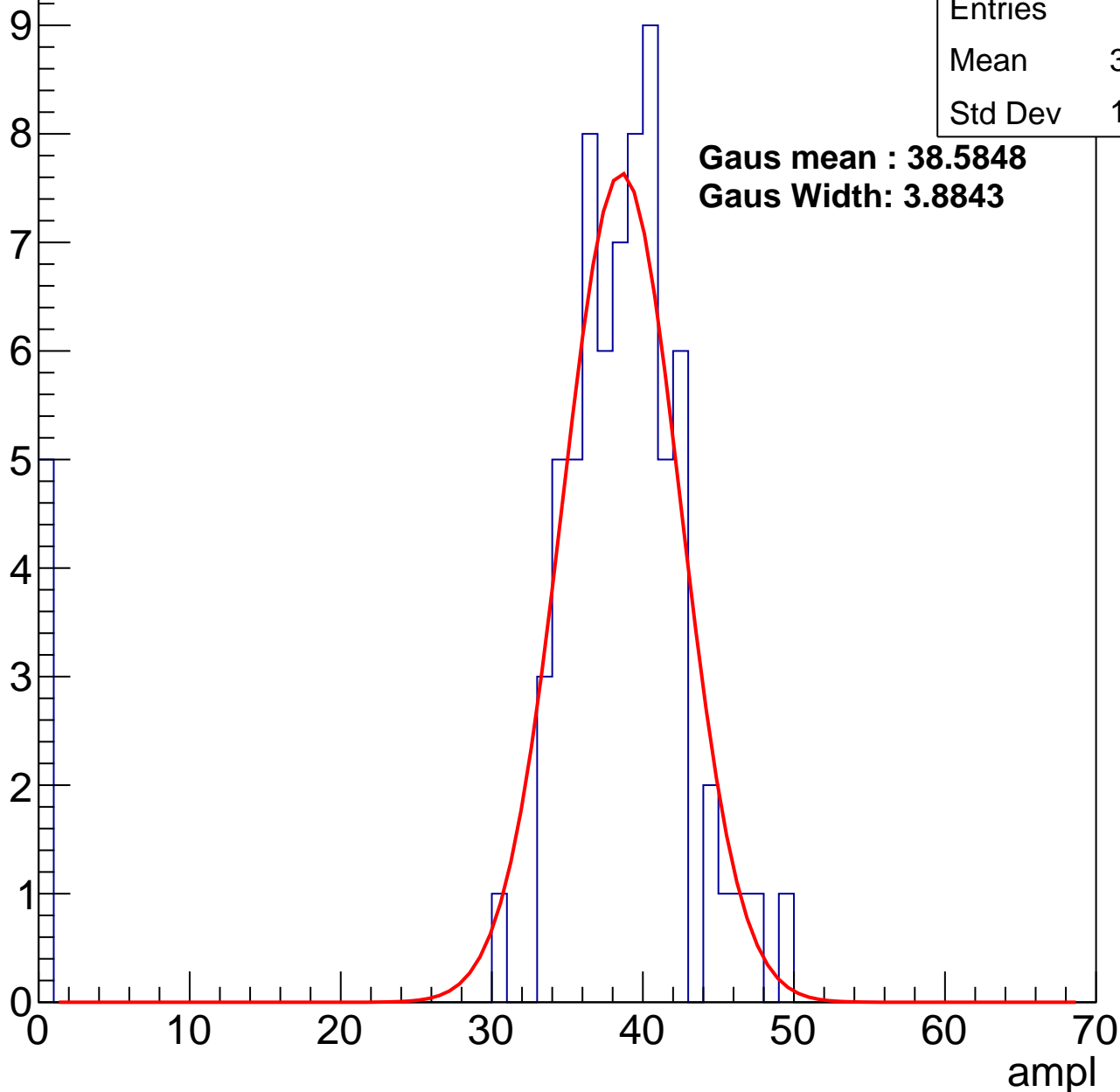
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	35.86
Std Dev	10.24

Gaus mean : 38.5848

Gaus Width: 3.8843



B1L103S, U13-ch71, adc2

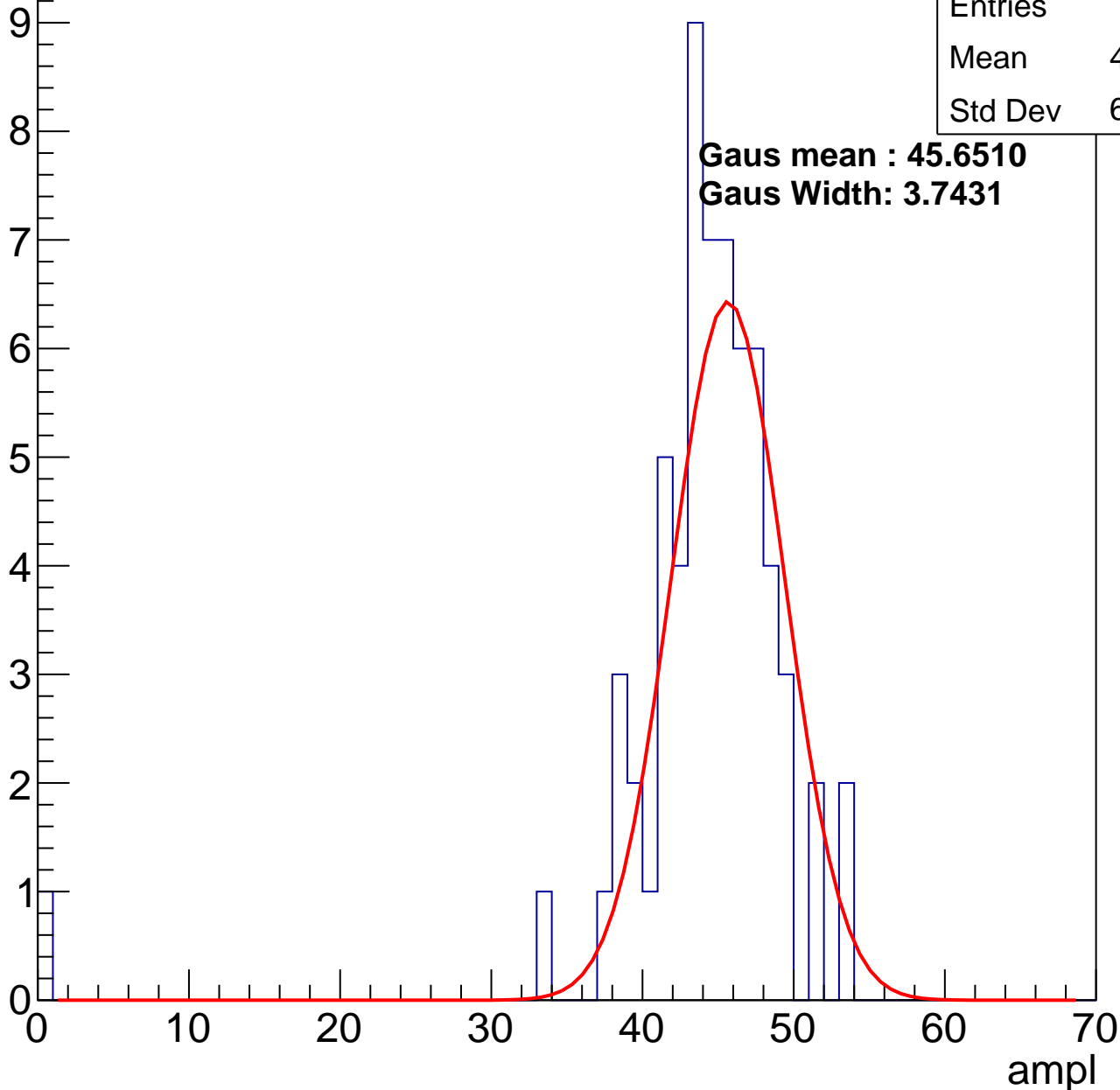
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	43.59
Std Dev	6.642

Gaus mean : 45.6510

Gaus Width: 3.7431

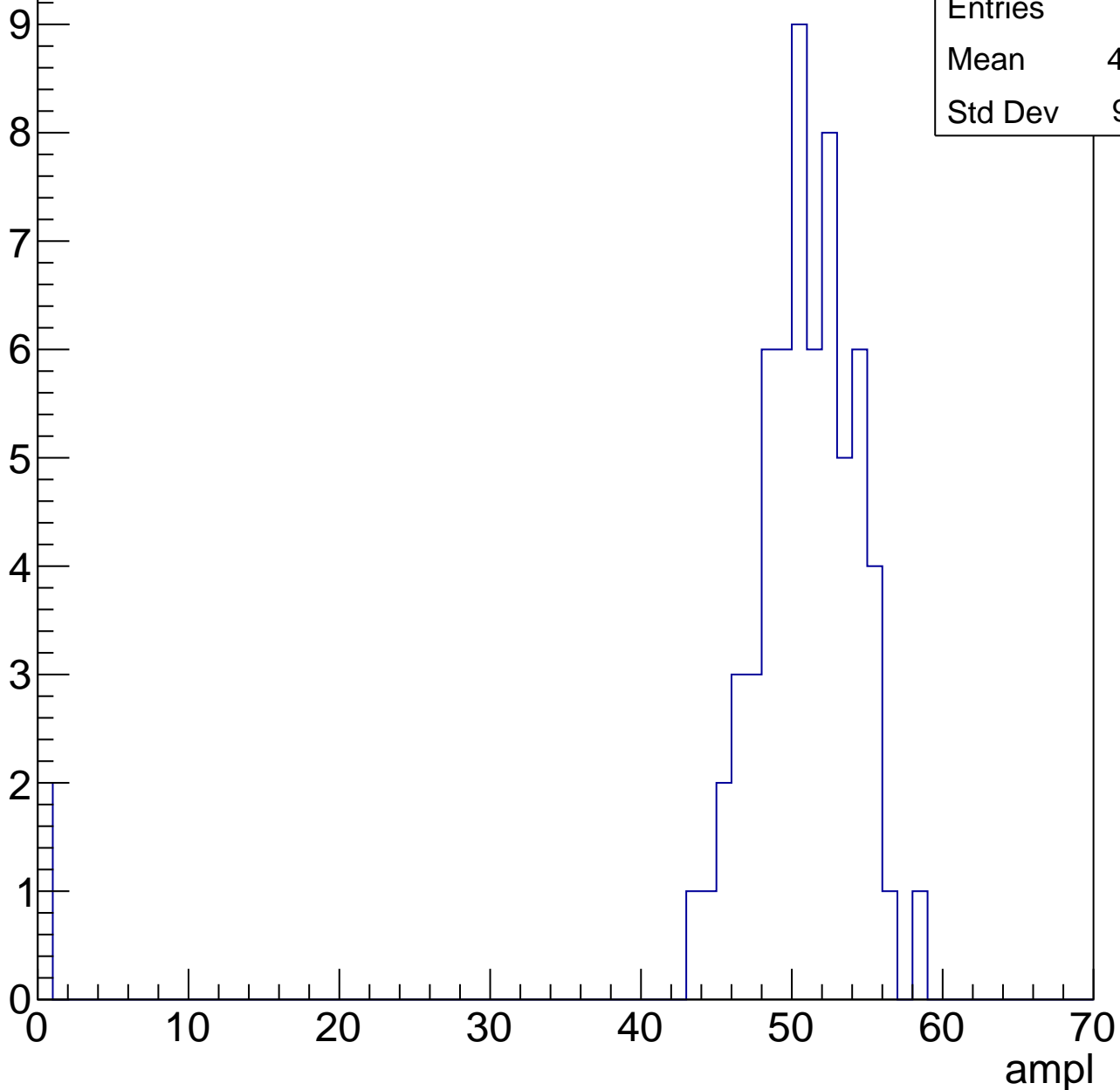


B1L103S, U13-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	48.95
Std Dev	9.311

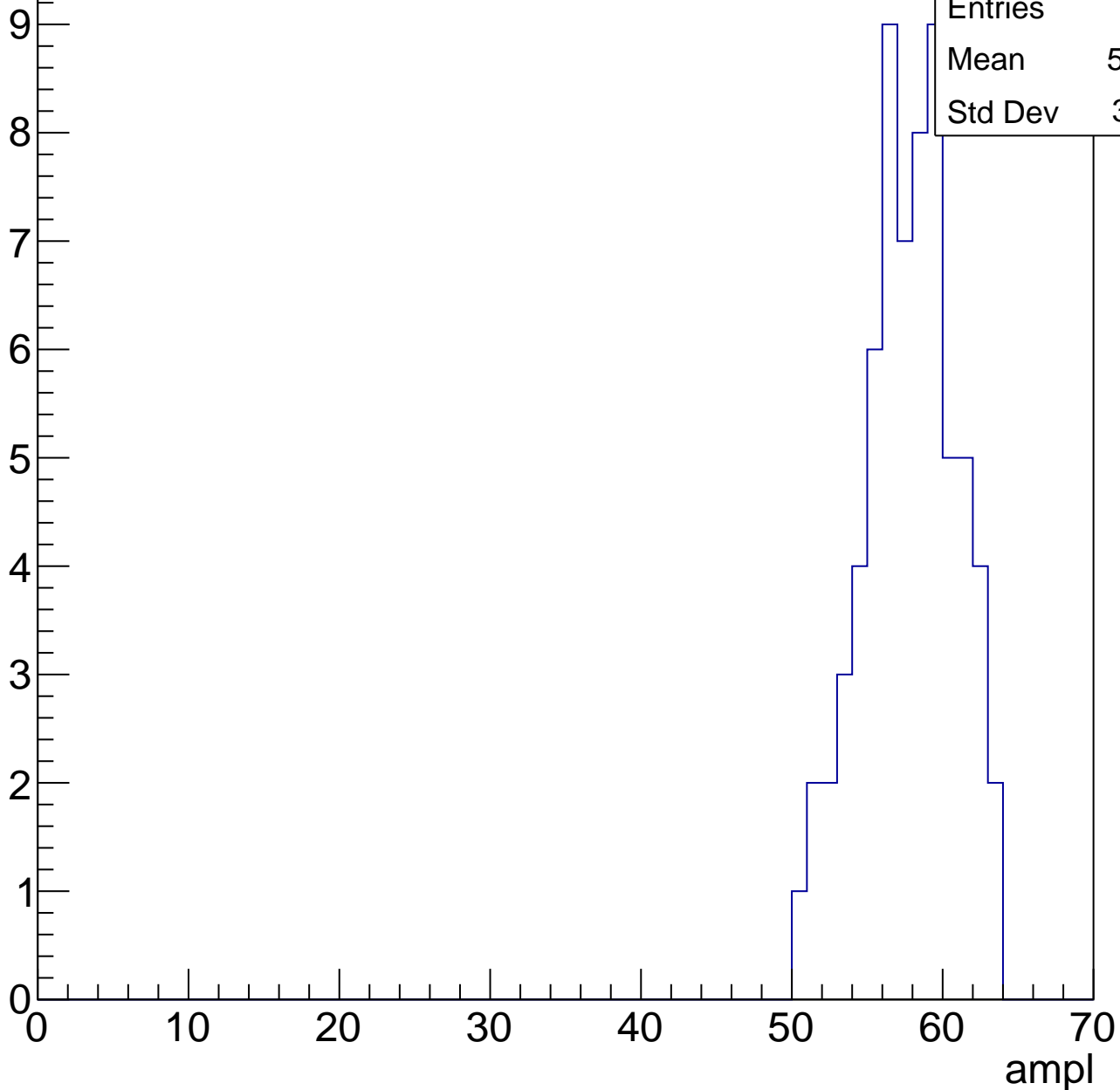


B1L103S, U13-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	57.28
Std Dev	3.051

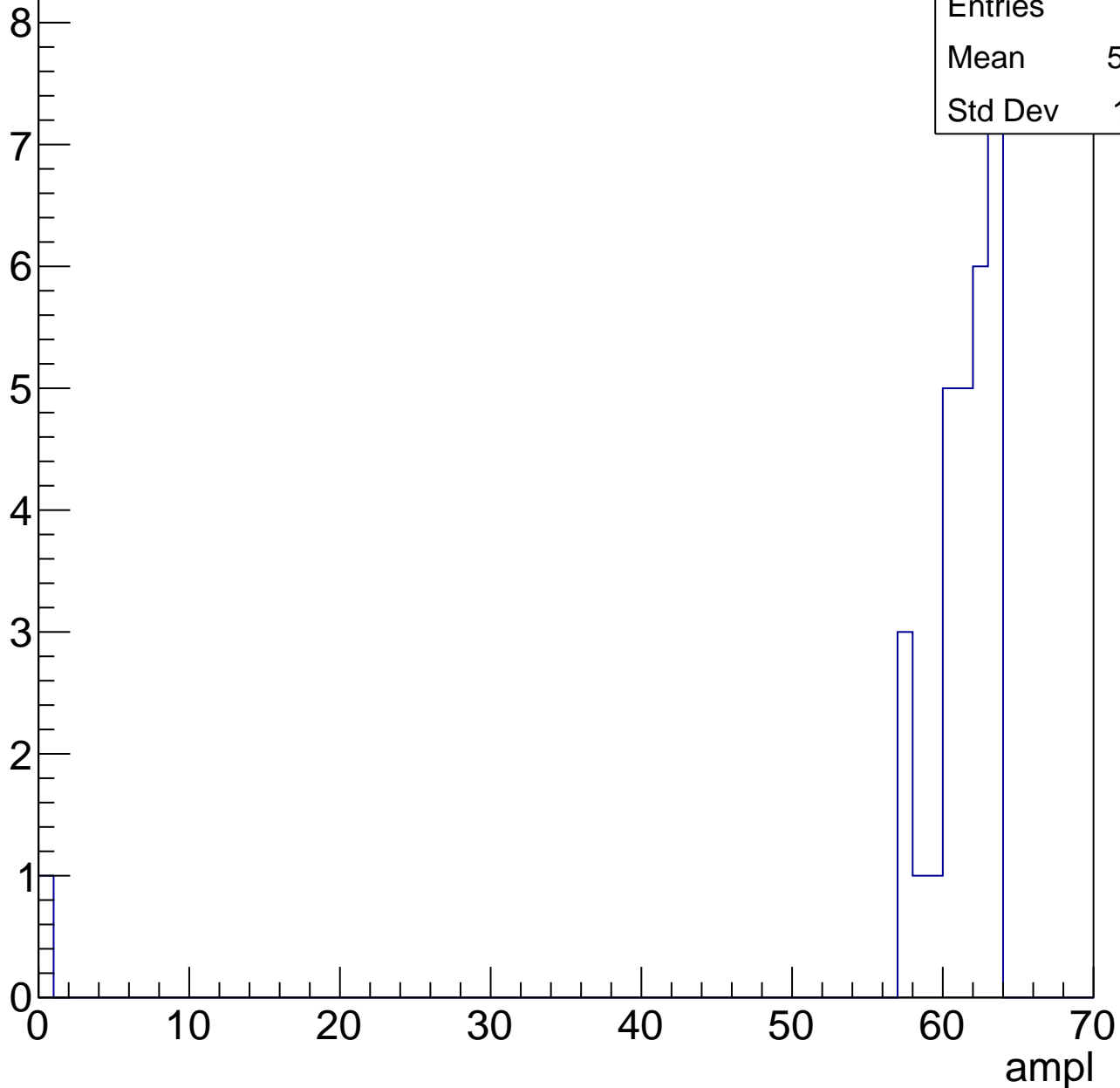


B1L103S, U13-ch71, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	58.97
Std Dev	11.11



B1L103S, U13-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U13-ch72, adc0

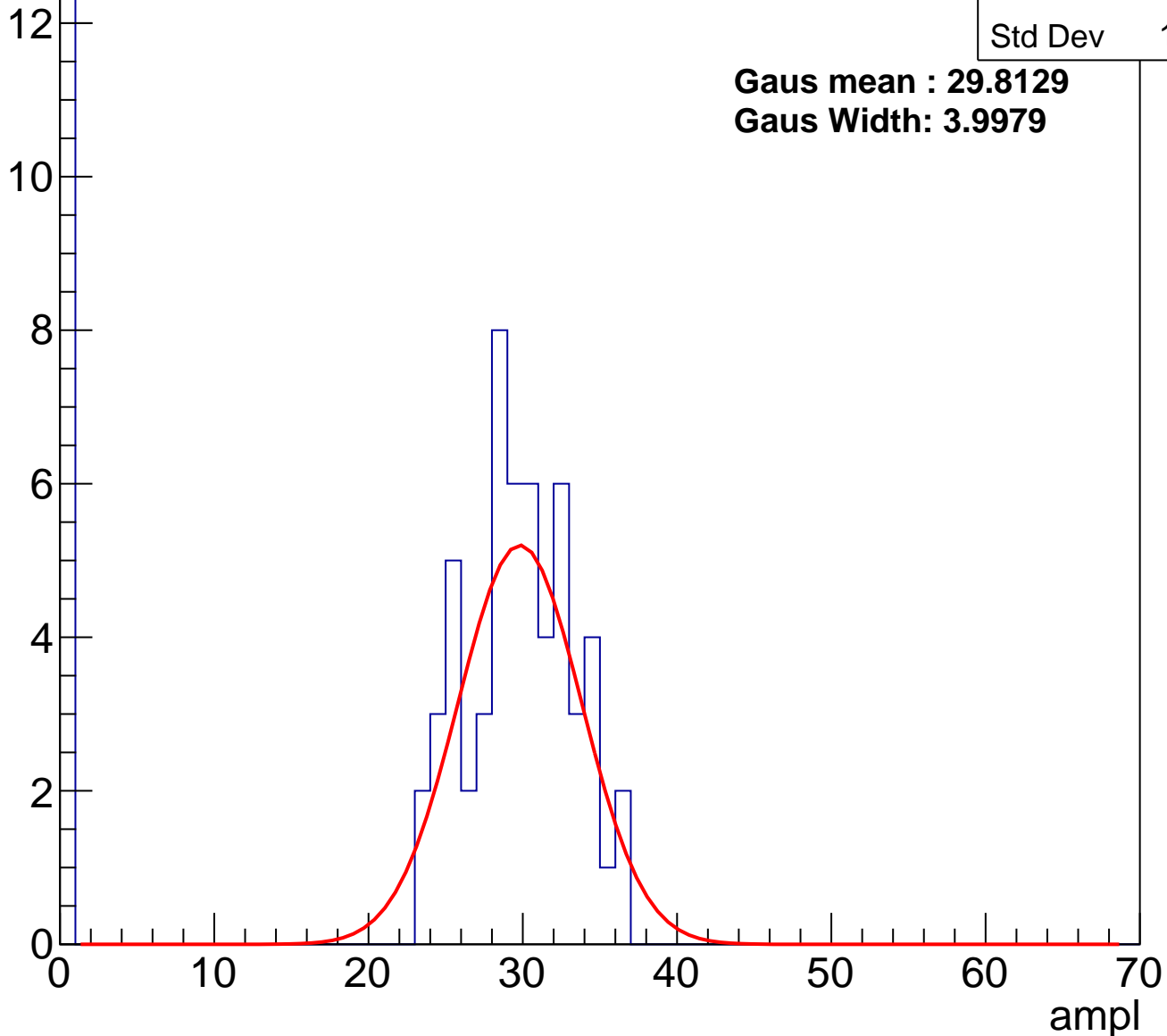
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	23.71
Std Dev	11.91

Gaus mean : 29.8129

Gaus Width: 3.9979



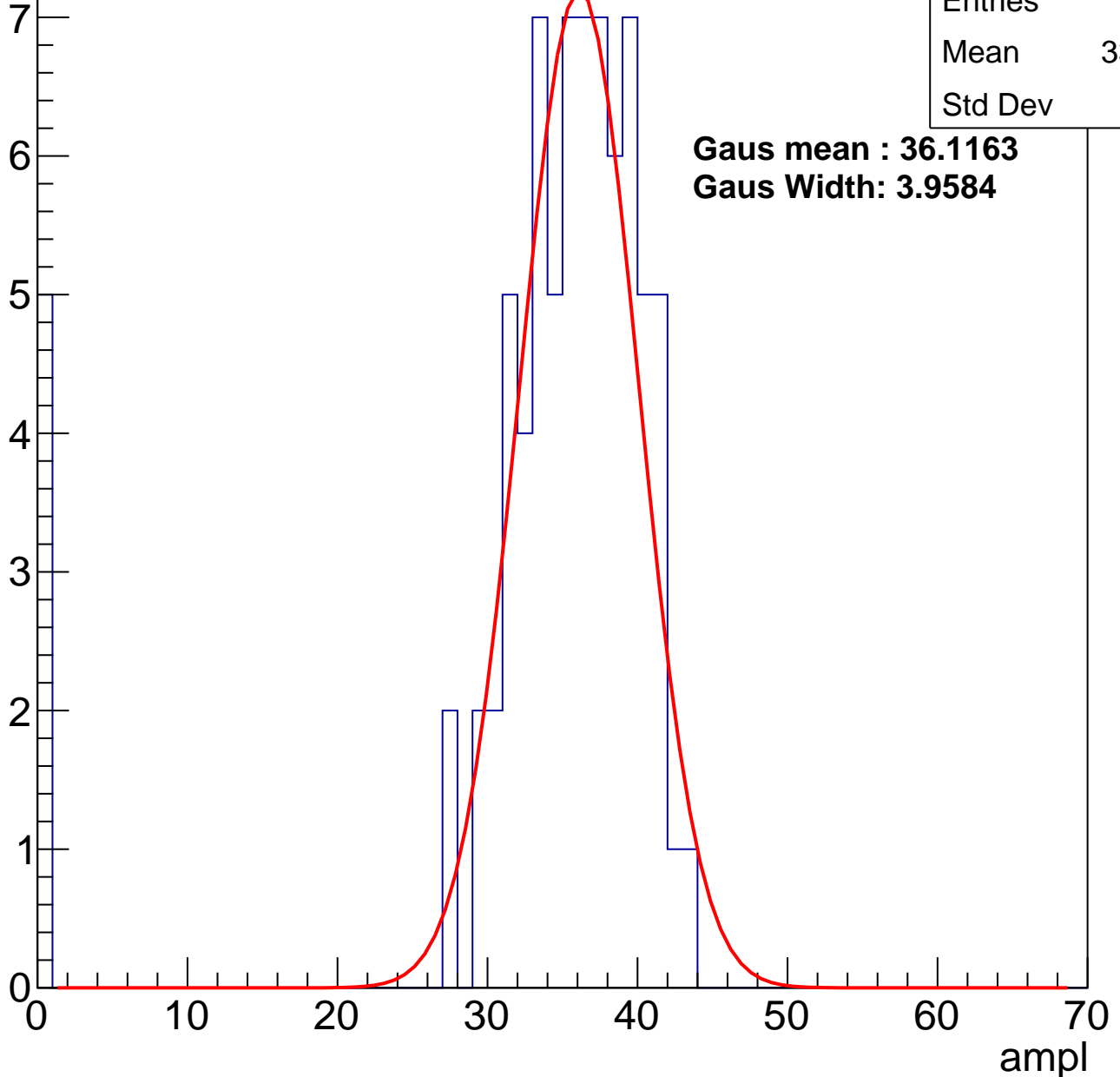
B1L103S, U13-ch72, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	33.37
Std Dev	9.43

Gaus mean : 36.1163
Gaus Width: 3.9584



B1L103S, U13-ch72, adc2

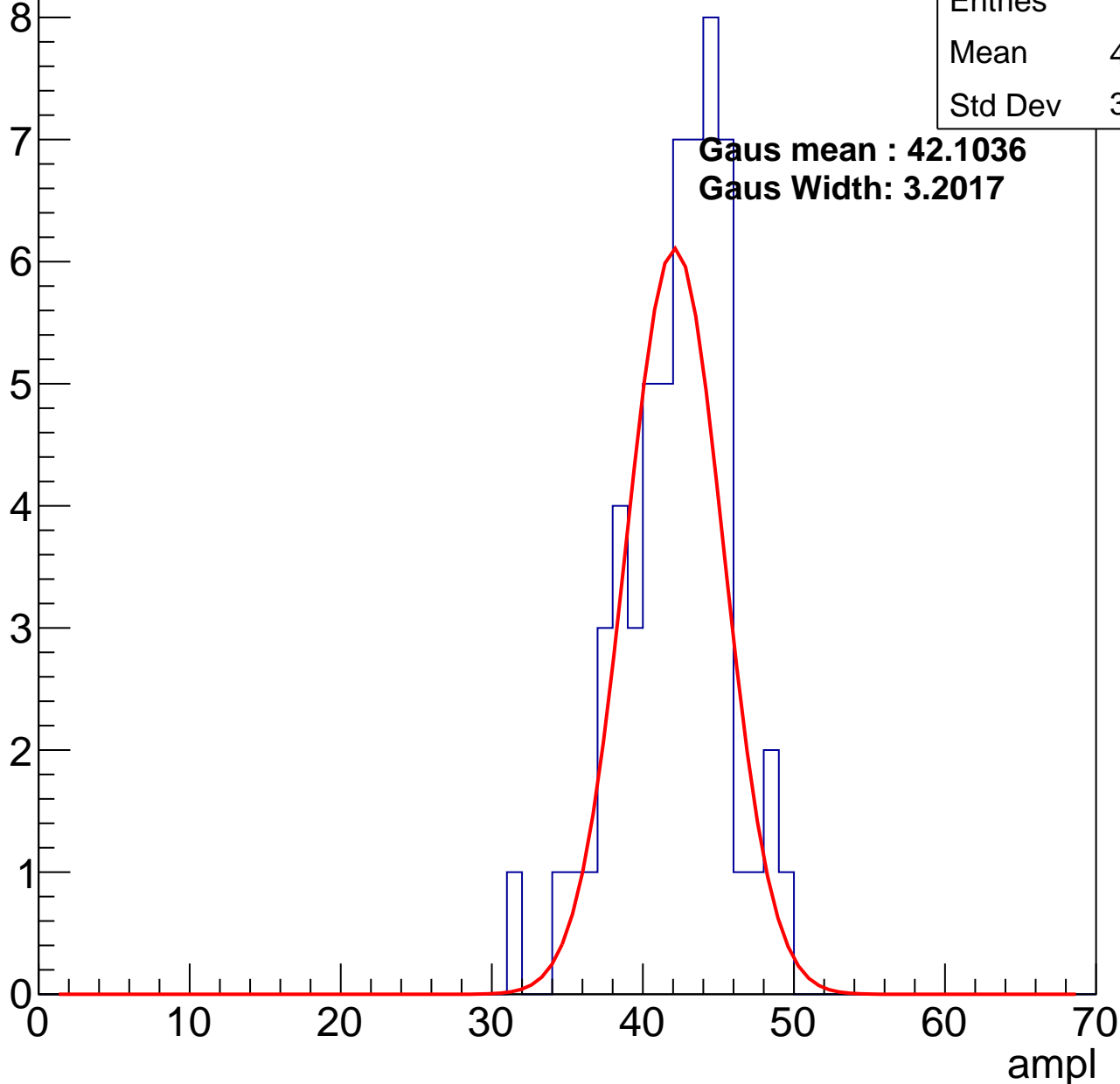
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	41.74
Std Dev	3.526

Gaus mean : 42.1036

Gaus Width: 3.2017

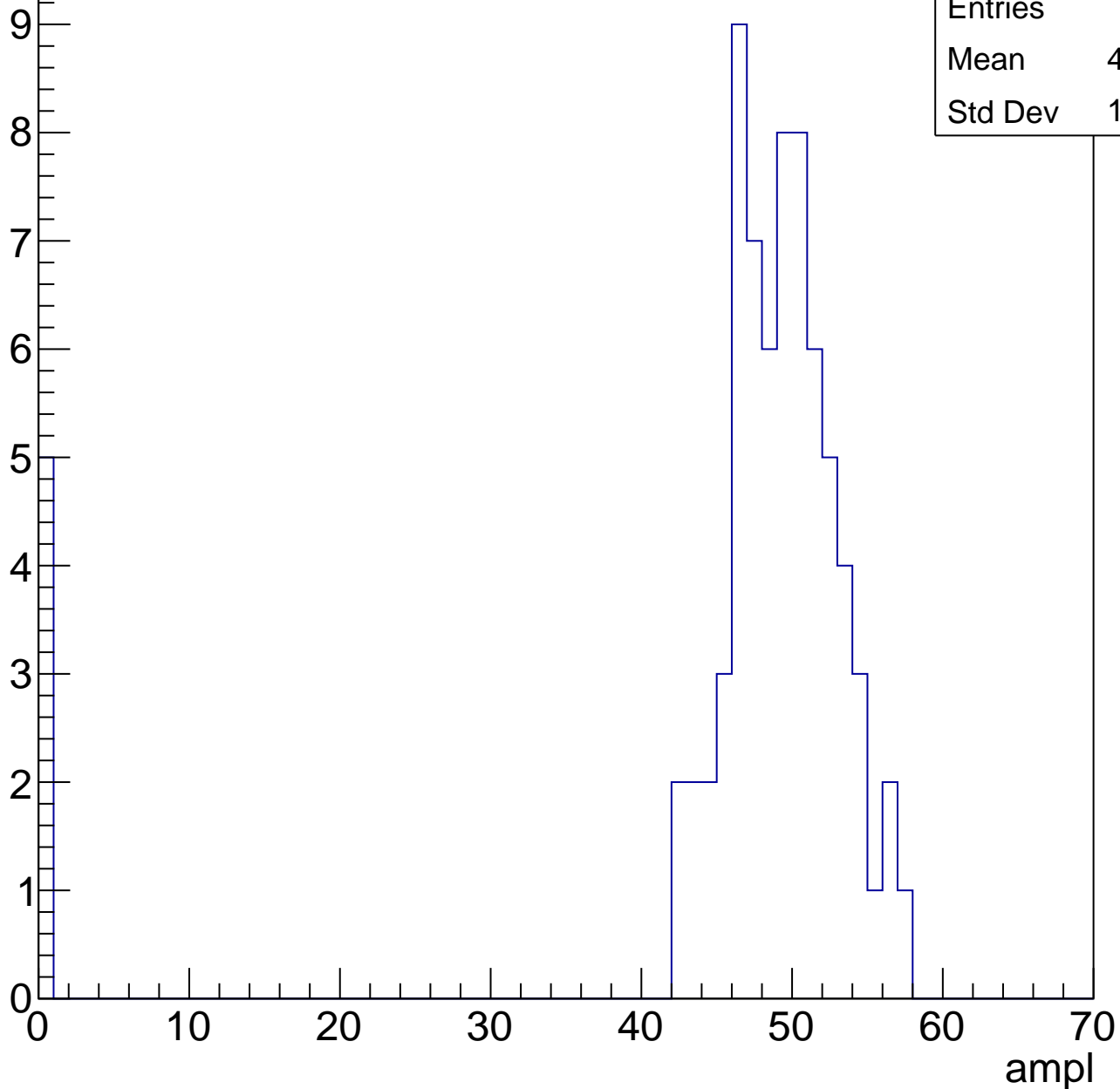


B1L103S, U13-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	45.68
Std Dev	12.73

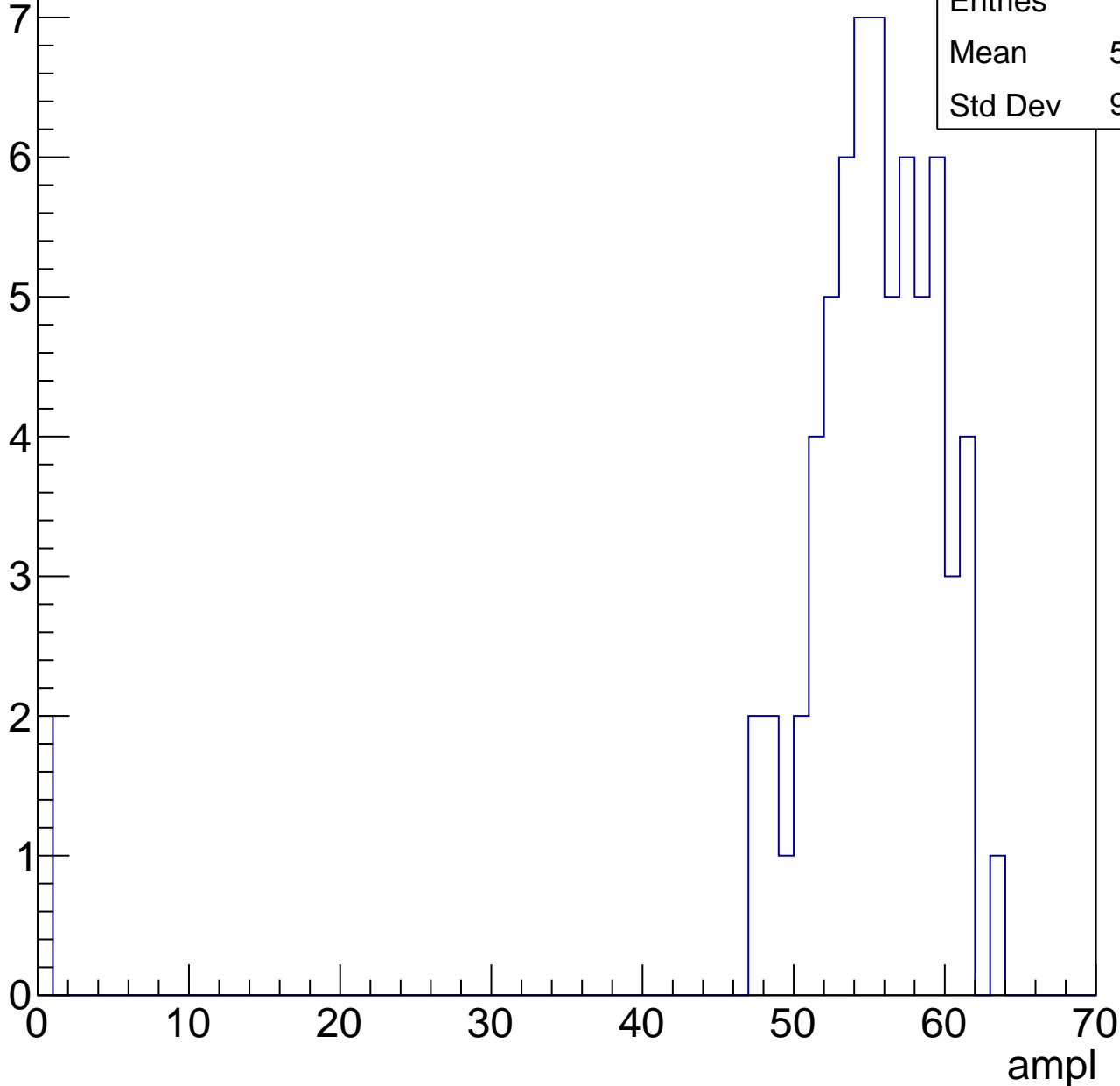


B1L103S, U13-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	53.49
Std Dev	9.999

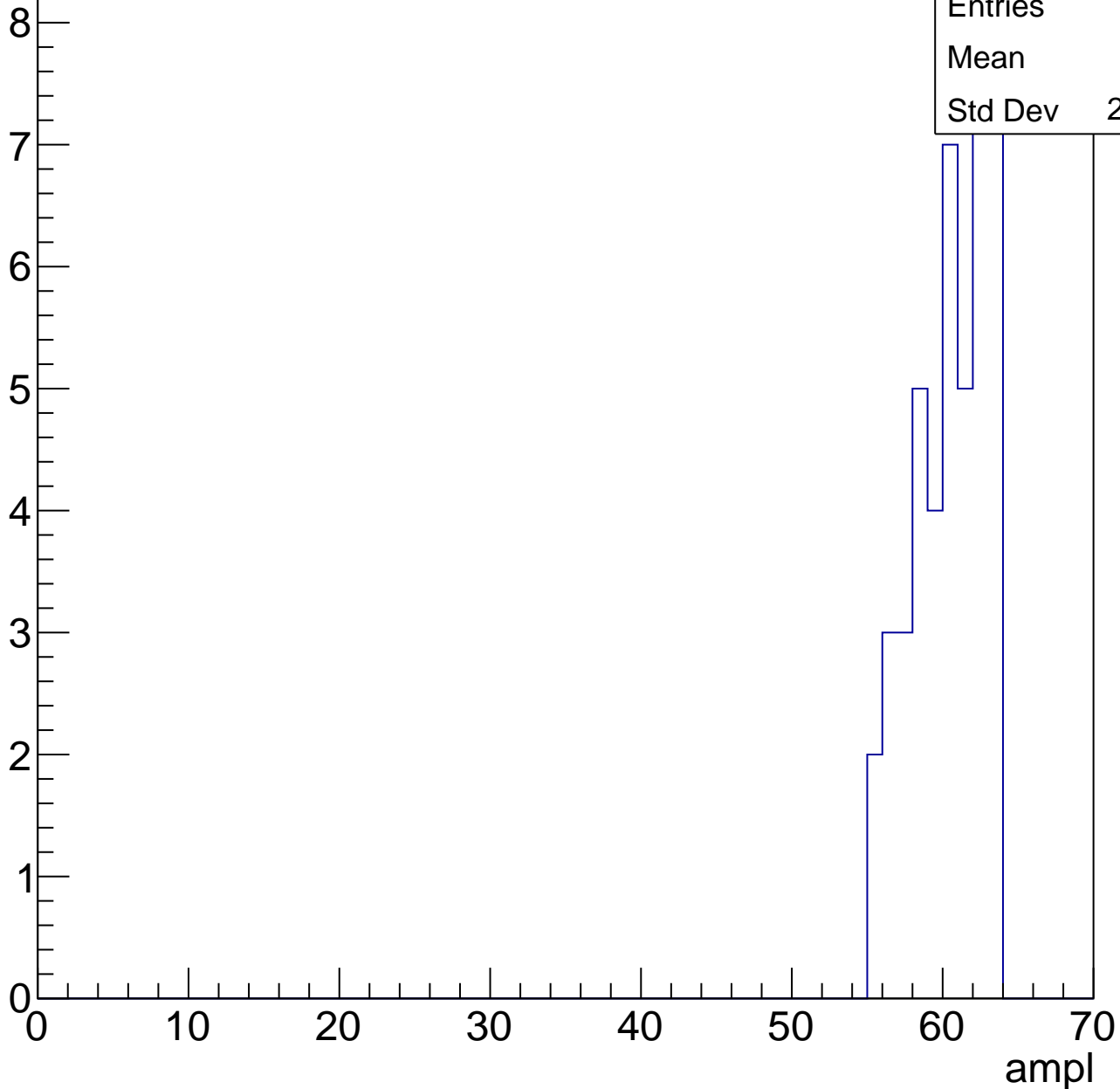


B1L103S, U13-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	60
Std Dev	2.394



B1L103S, U13-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	49.6
Std Dev	24.81

B1L103S, U13-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch73, adc0

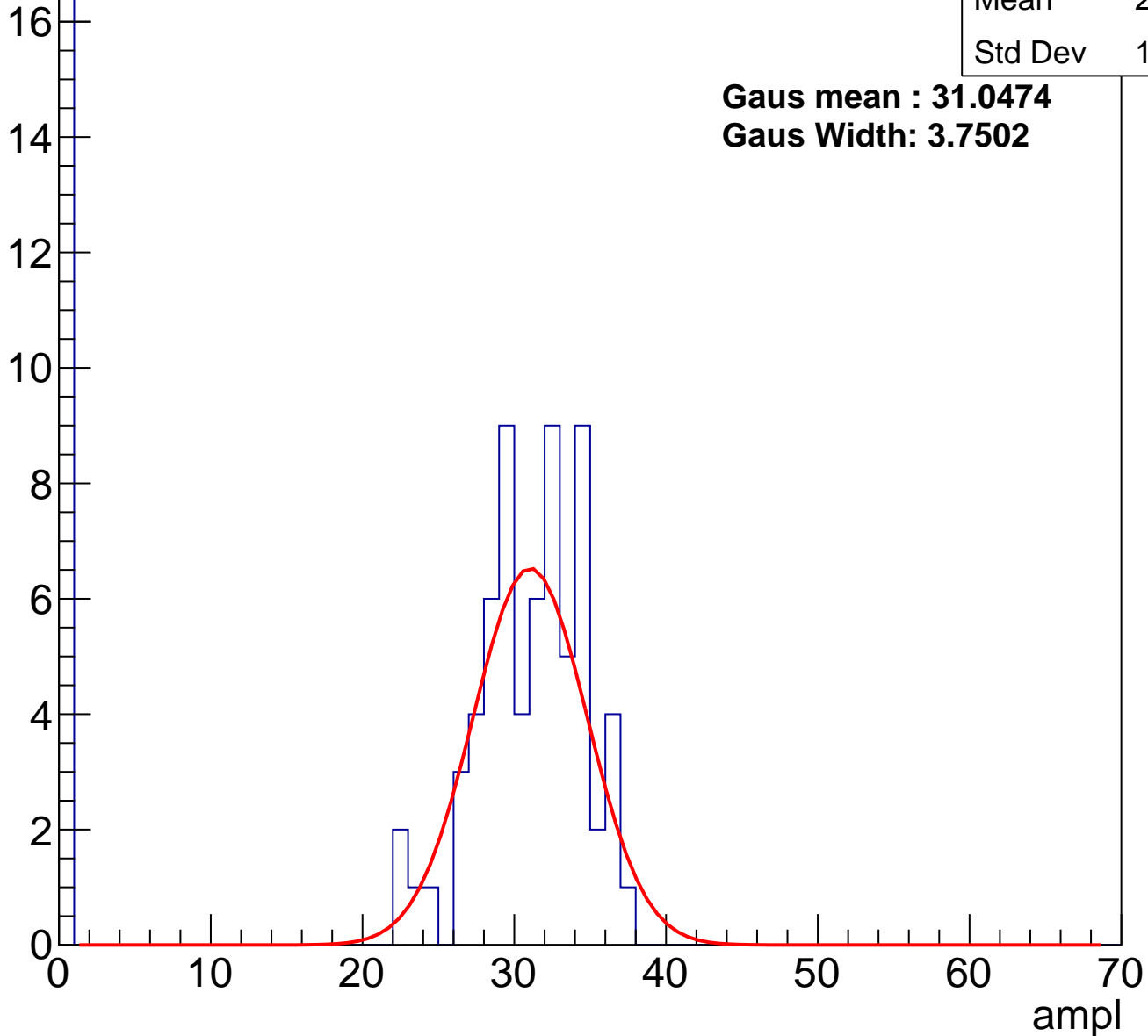
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24.36
Std Dev	12.74

Gaus mean : 31.0474

Gaus Width: 3.7502

Entry



B1L103S, U13-ch73, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	32.29
Std Dev	14.37

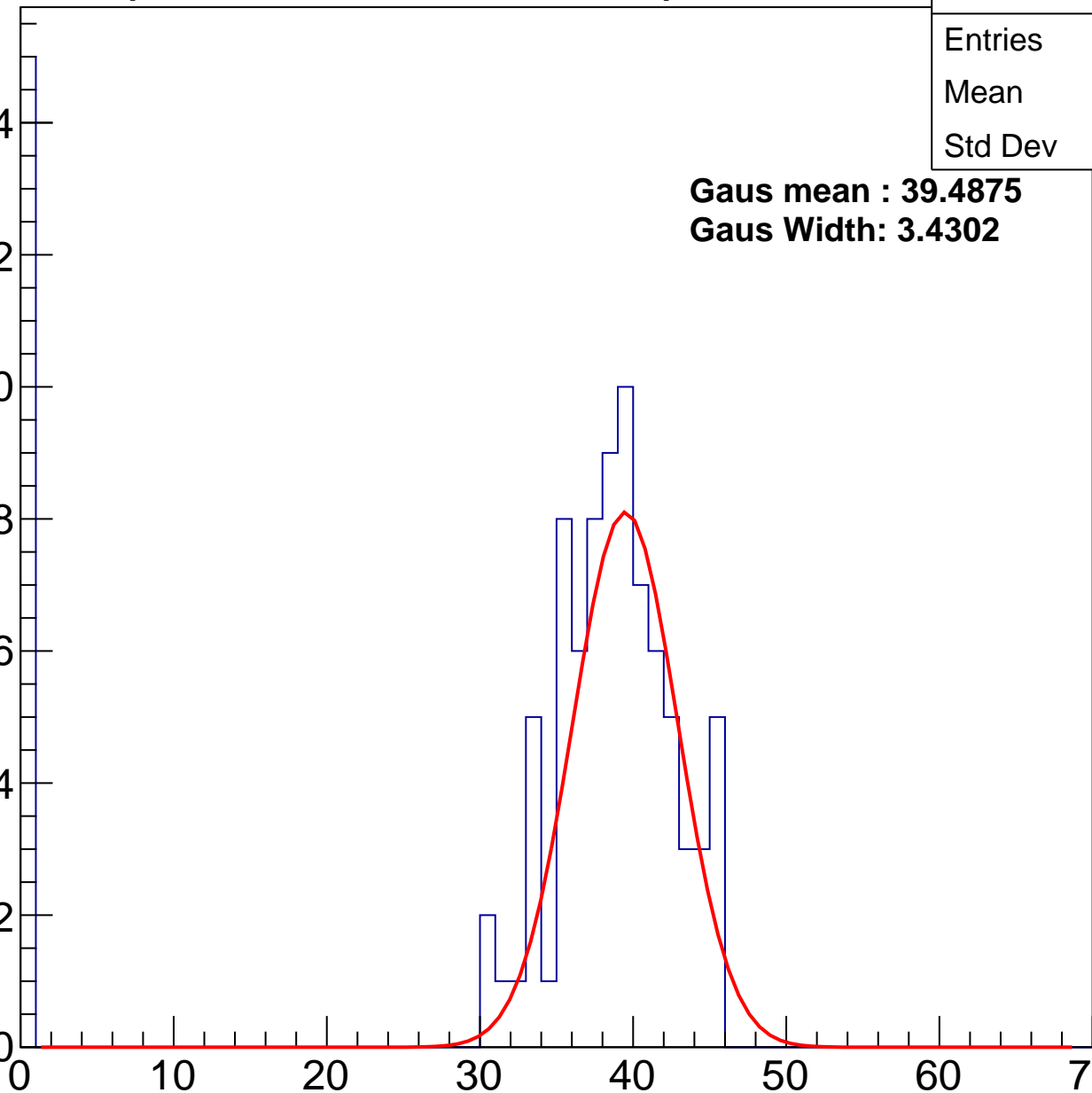
Gaus mean : 39.4875

Gaus Width: 3.4302

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch73, adc2

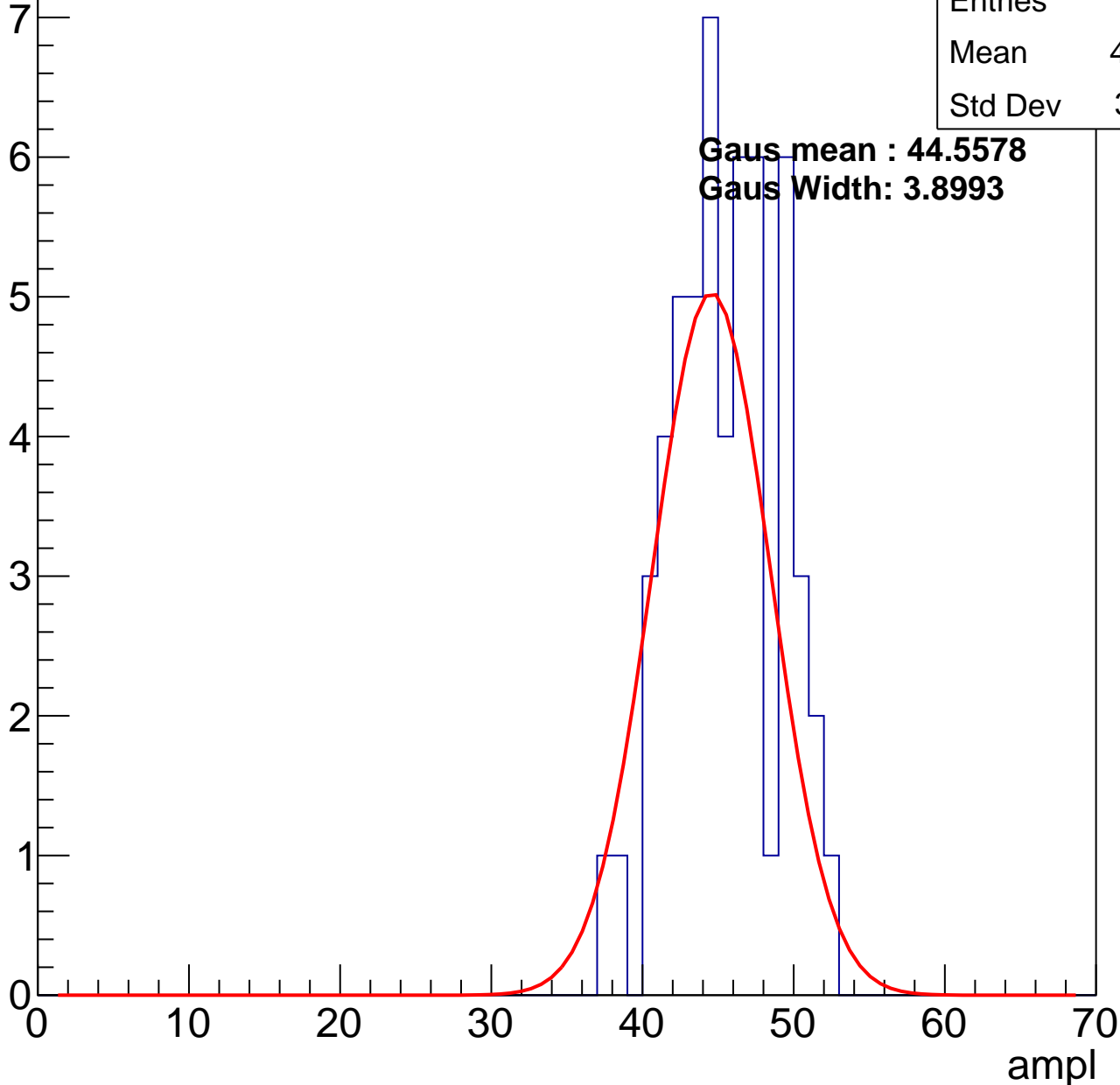
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	45.02
Std Dev	3.451

Gaus mean : 44.5578

Gaus Width: 3.8993

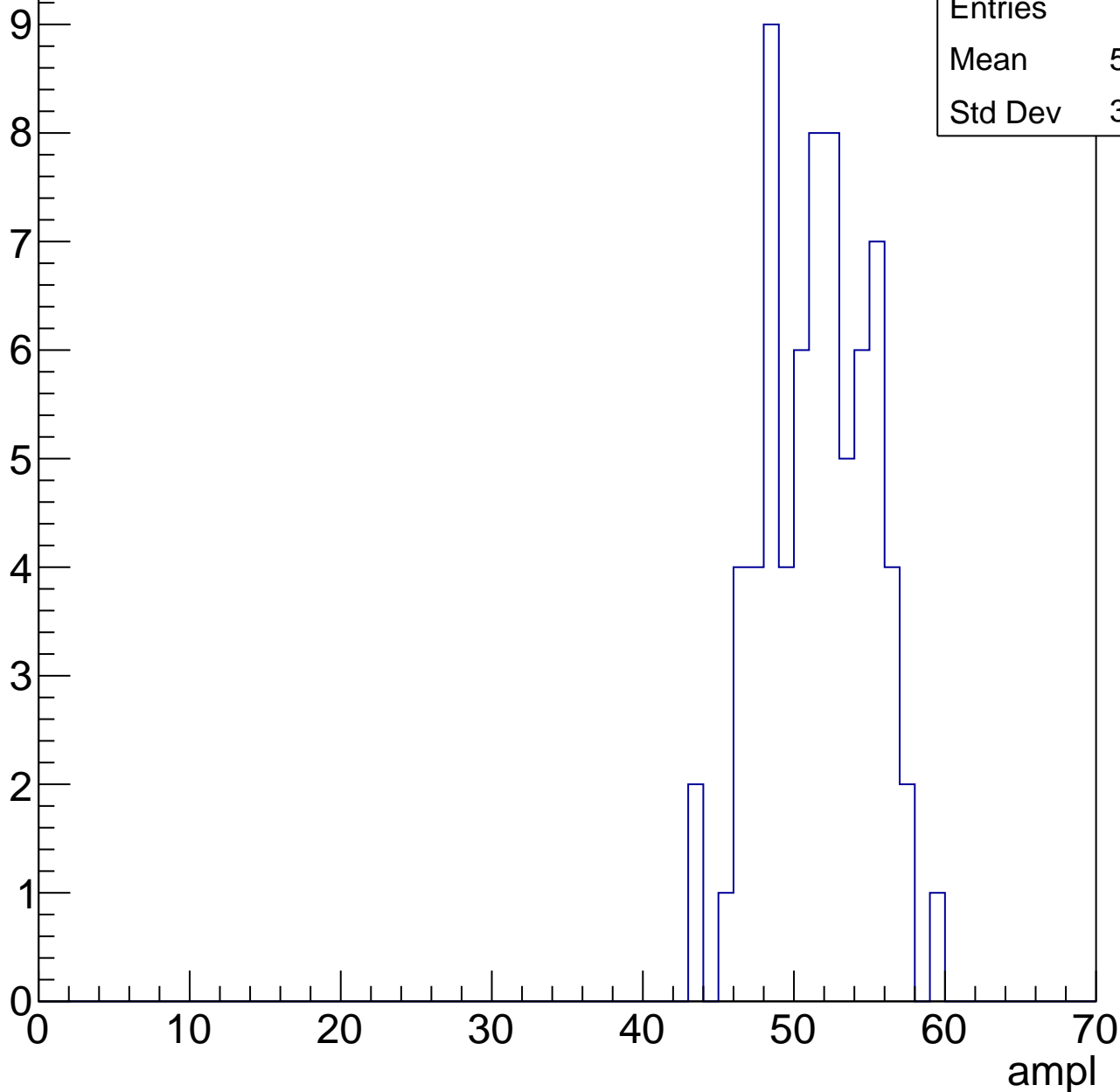


B1L103S, U13-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	51.07
Std Dev	3.482

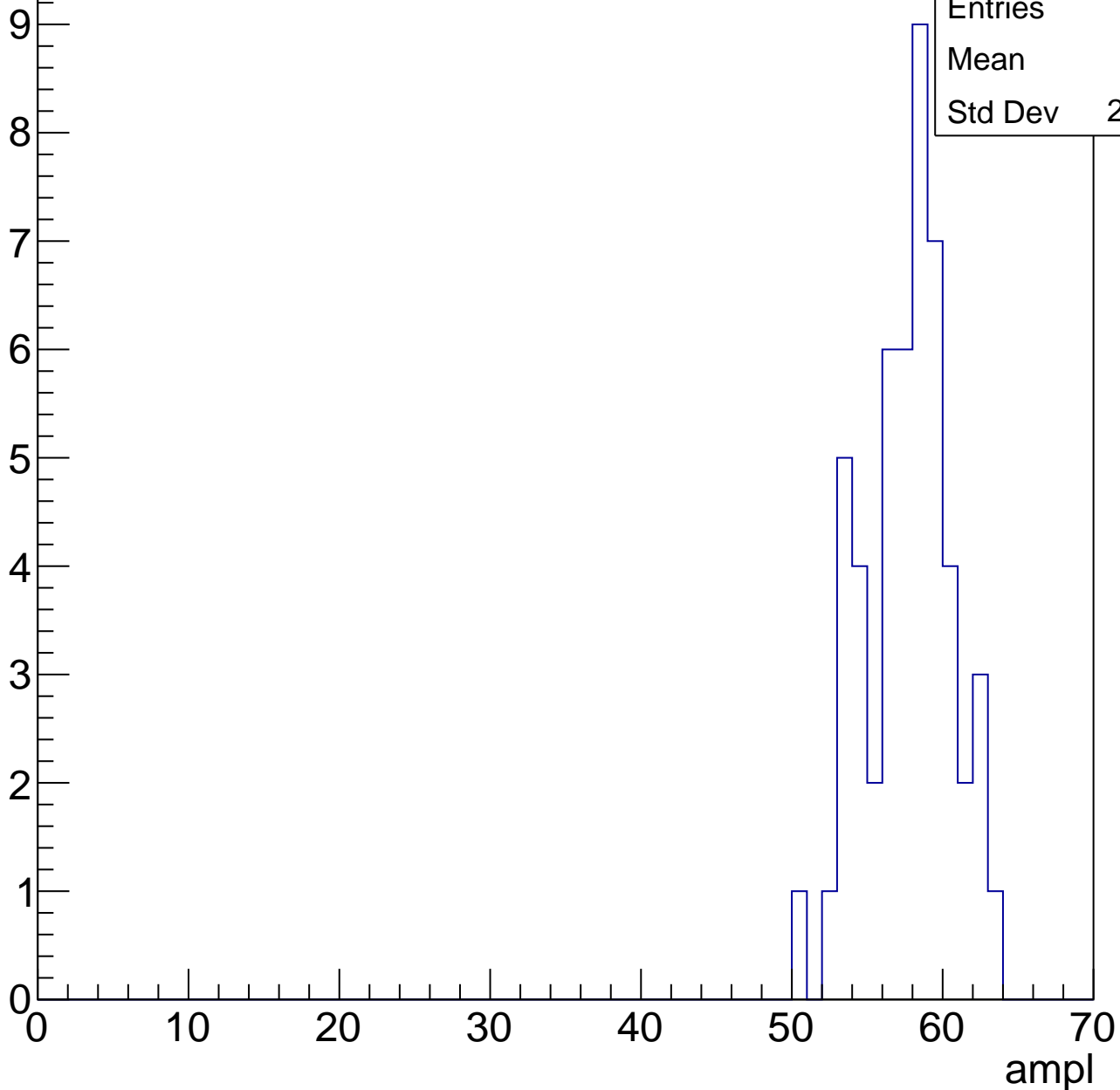


B1L103S, U13-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.2
Std Dev	2.863

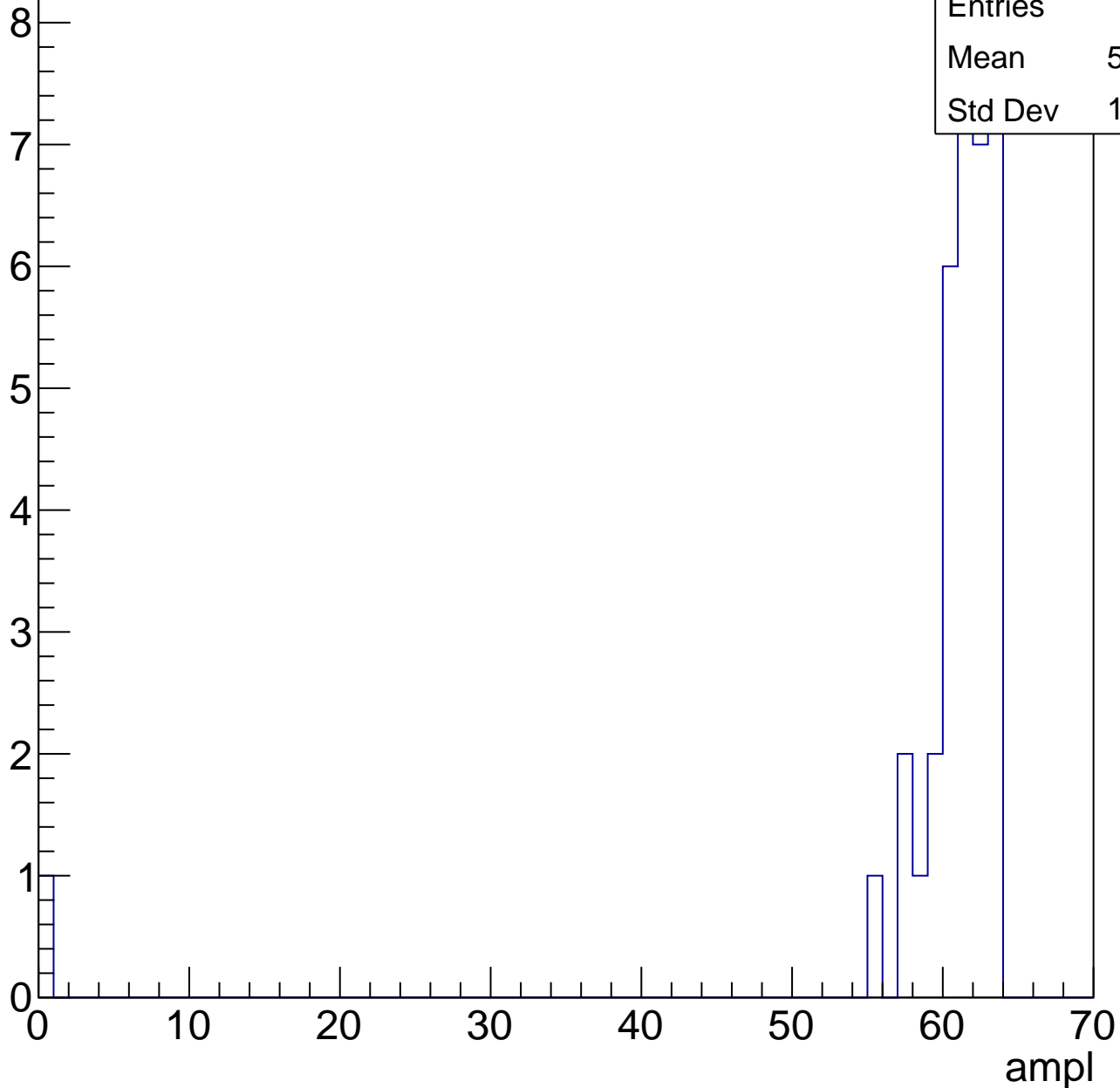


B1L103S, U13-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	59.19
Std Dev	10.18



B1L103S, U13-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch74, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	23.76
Std Dev	11.15

Gaus mean : 30.0479

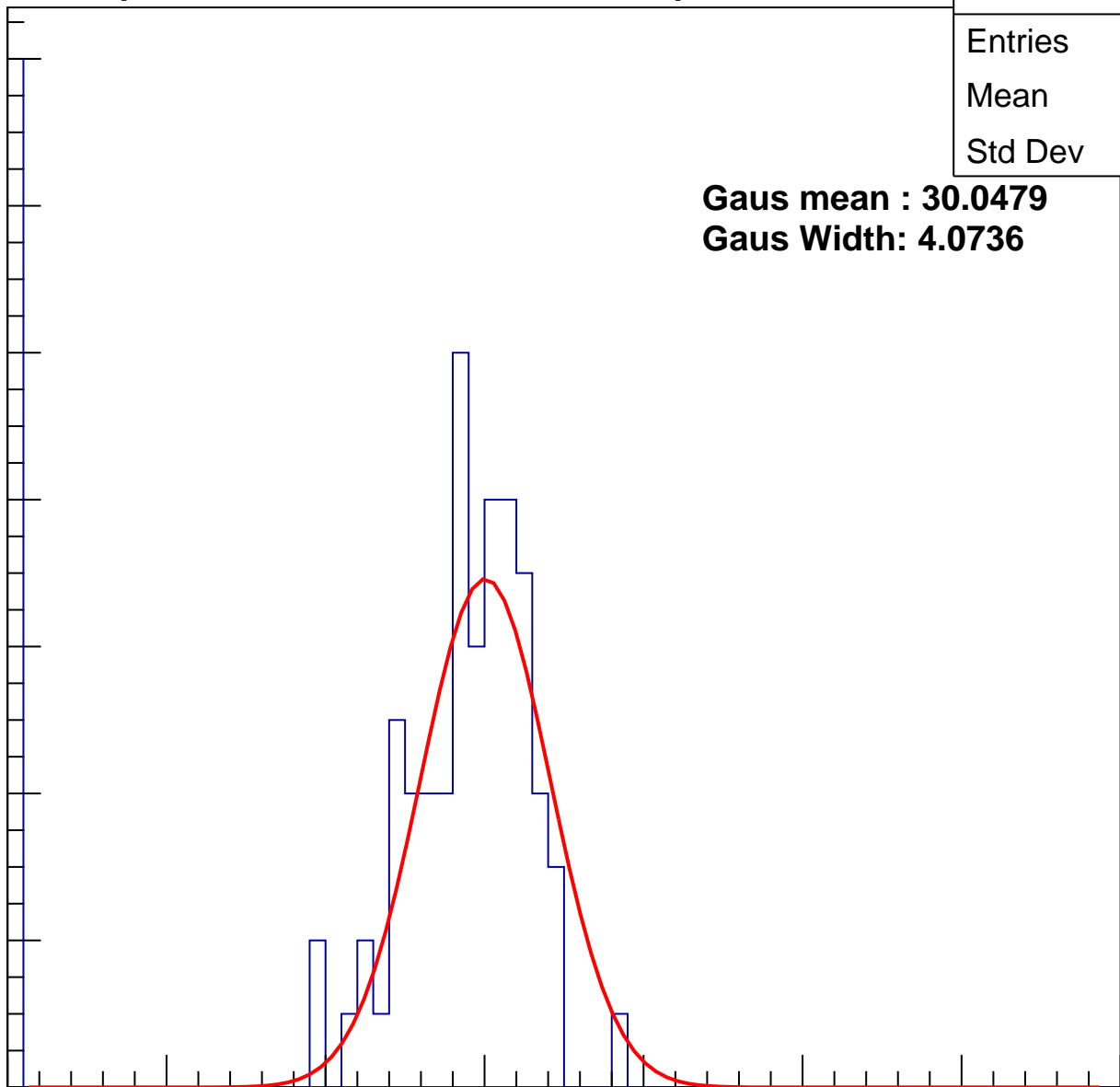
Gaus Width: 4.0736

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch74, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	28.7
Std Dev	14.02

Gaus mean : 36.3542

Gaus Width: 3.5294

Entry

12

10

8

6

4

2

0

0

10

20

30

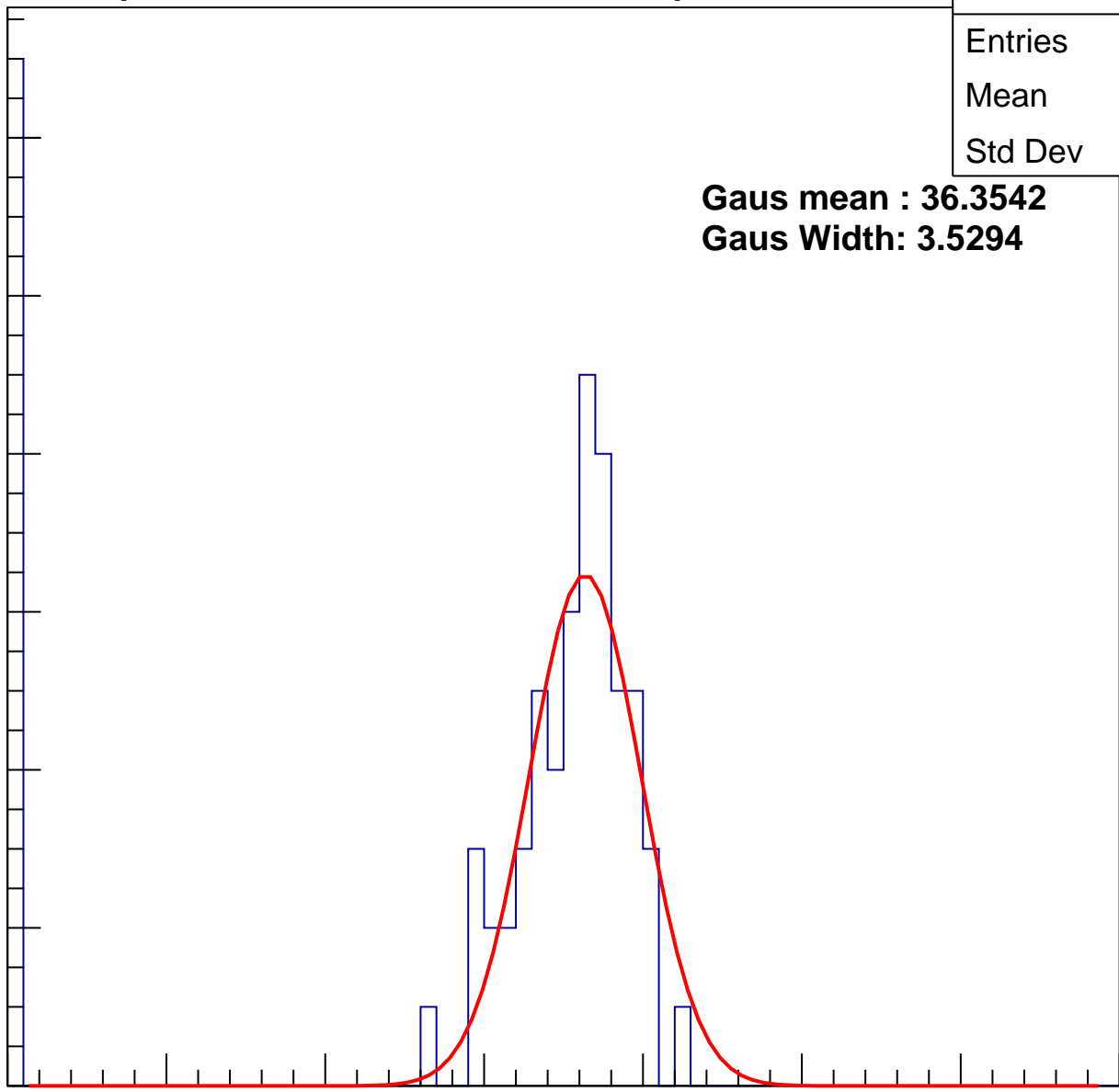
40

50

60

70

ampl



B1L103S, U13-ch74, adc2

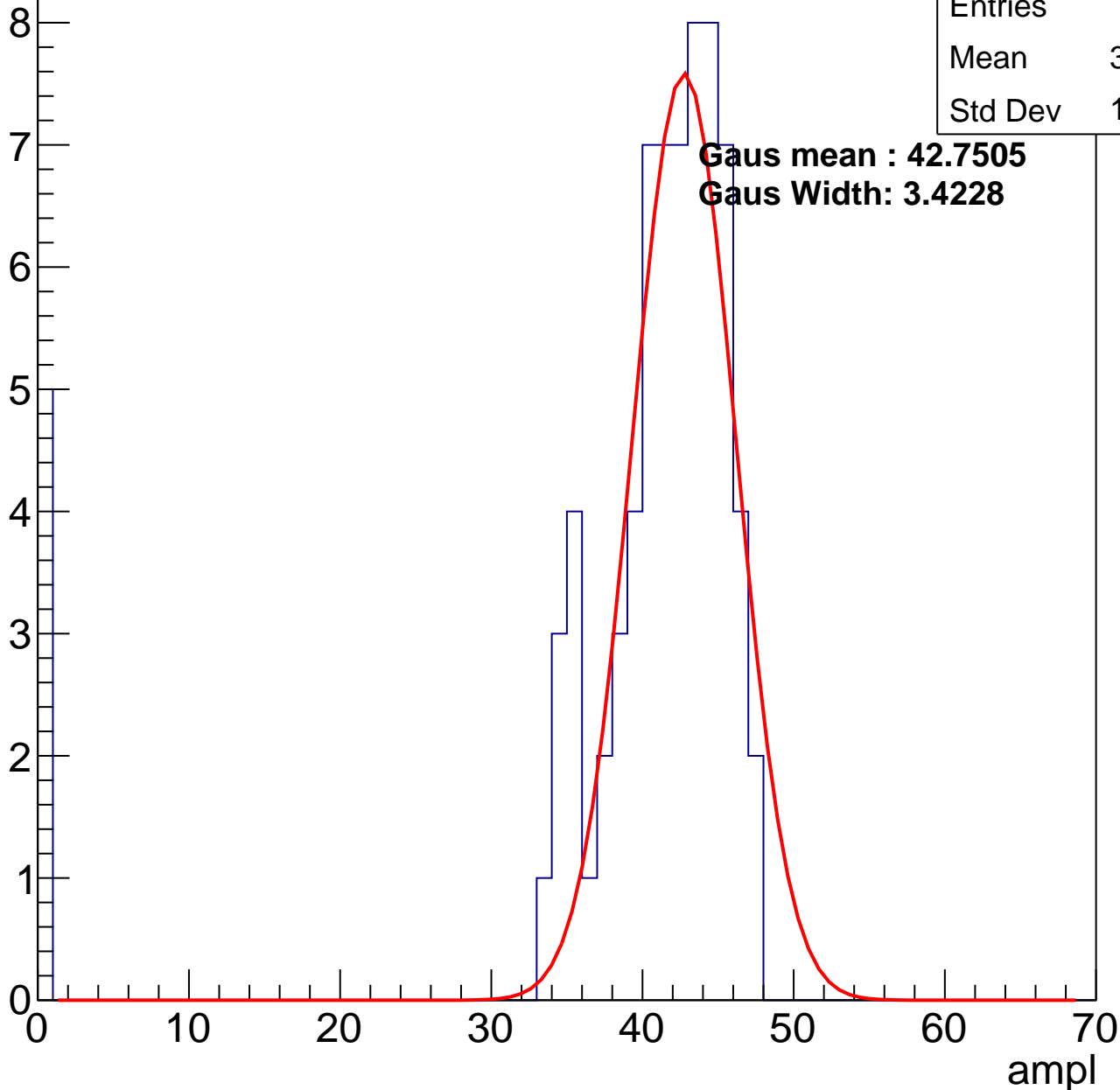
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	38.42
Std Dev	10.97

Gaus mean : 42.7505

Gaus Width: 3.4228

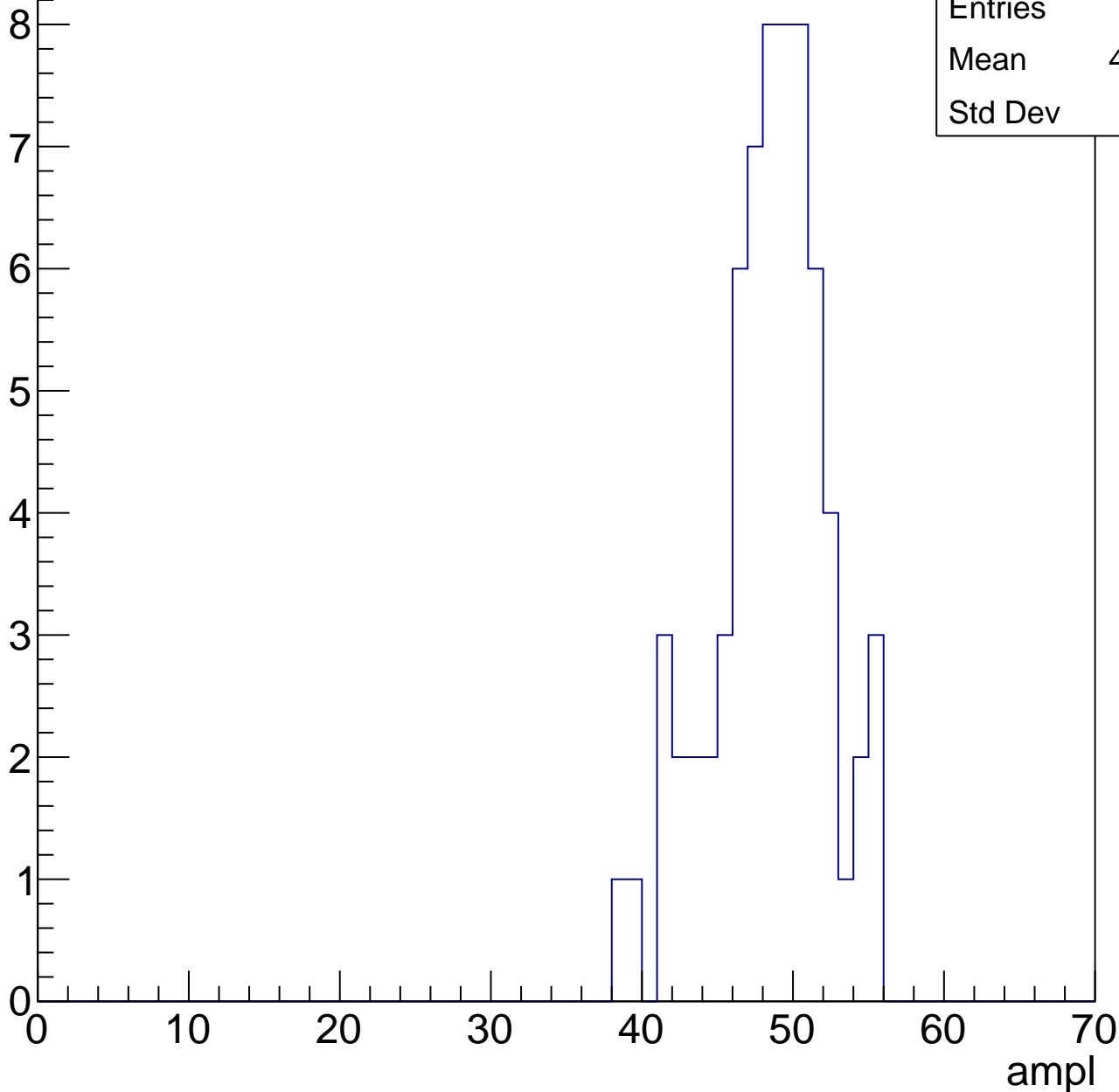


B1L103S, U13-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.97
Std Dev	3.75

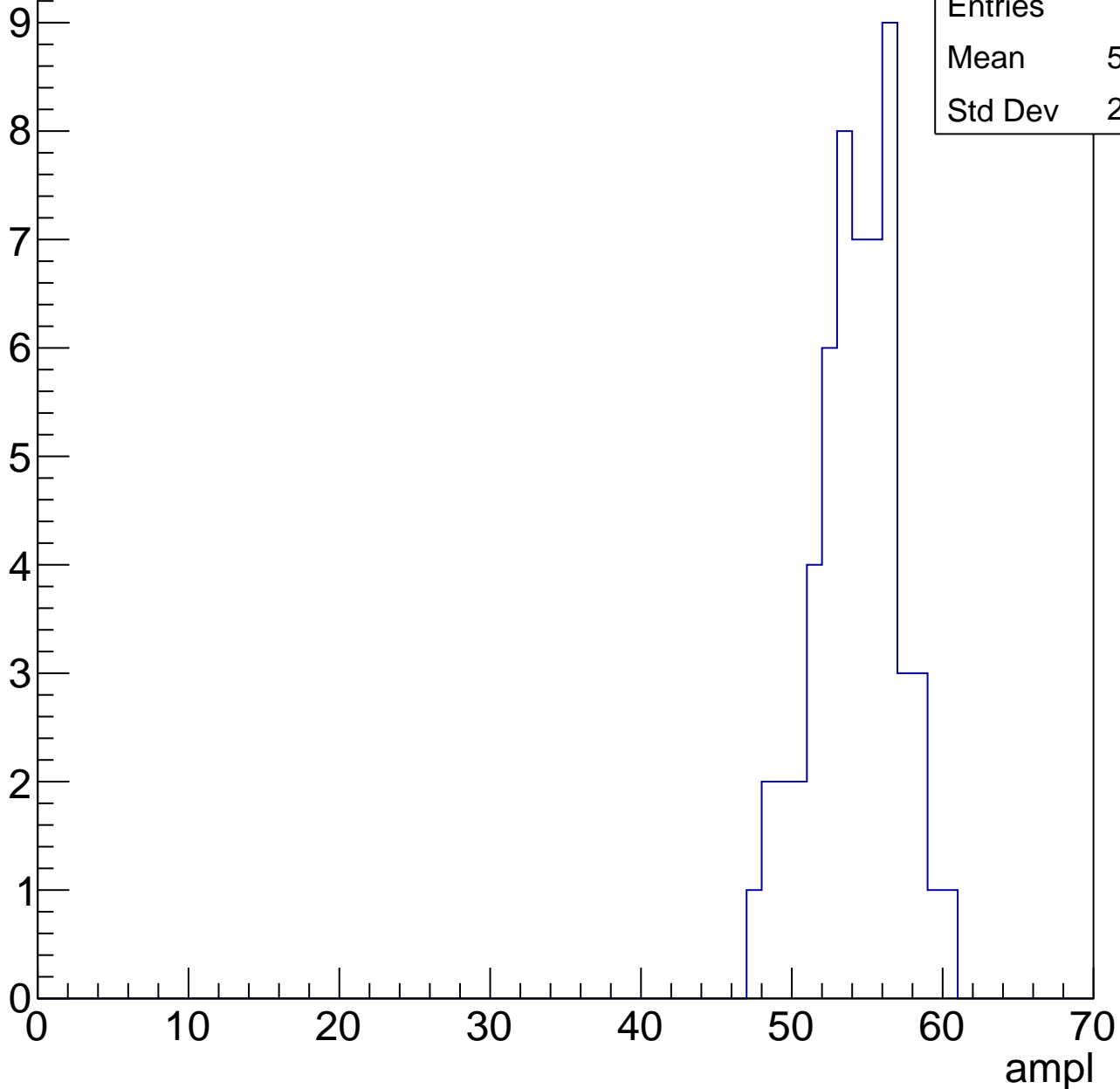


B1L103S, U13-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

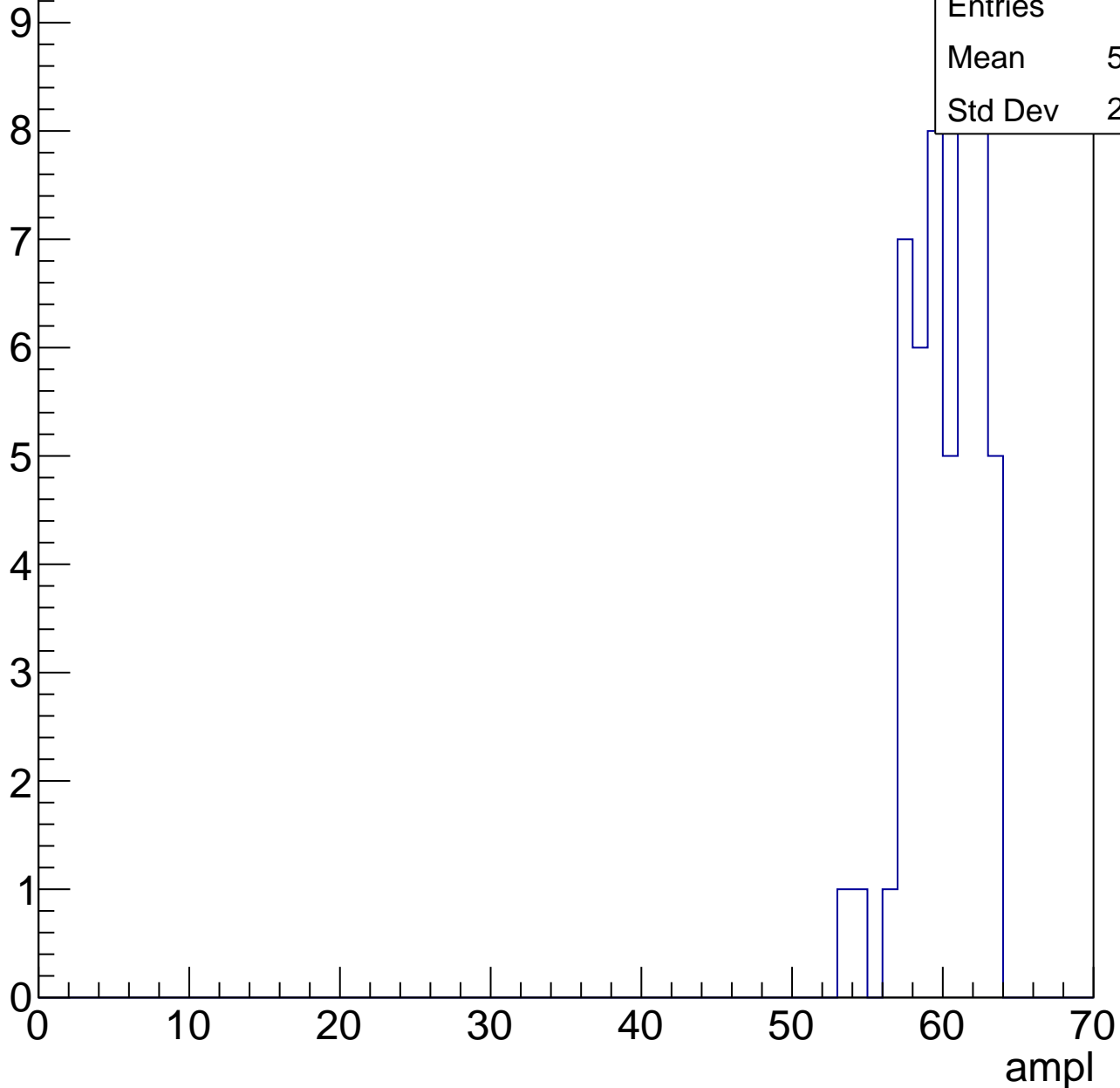
Entries	56
Mean	53.79
Std Dev	2.827



B1L103S, U13-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

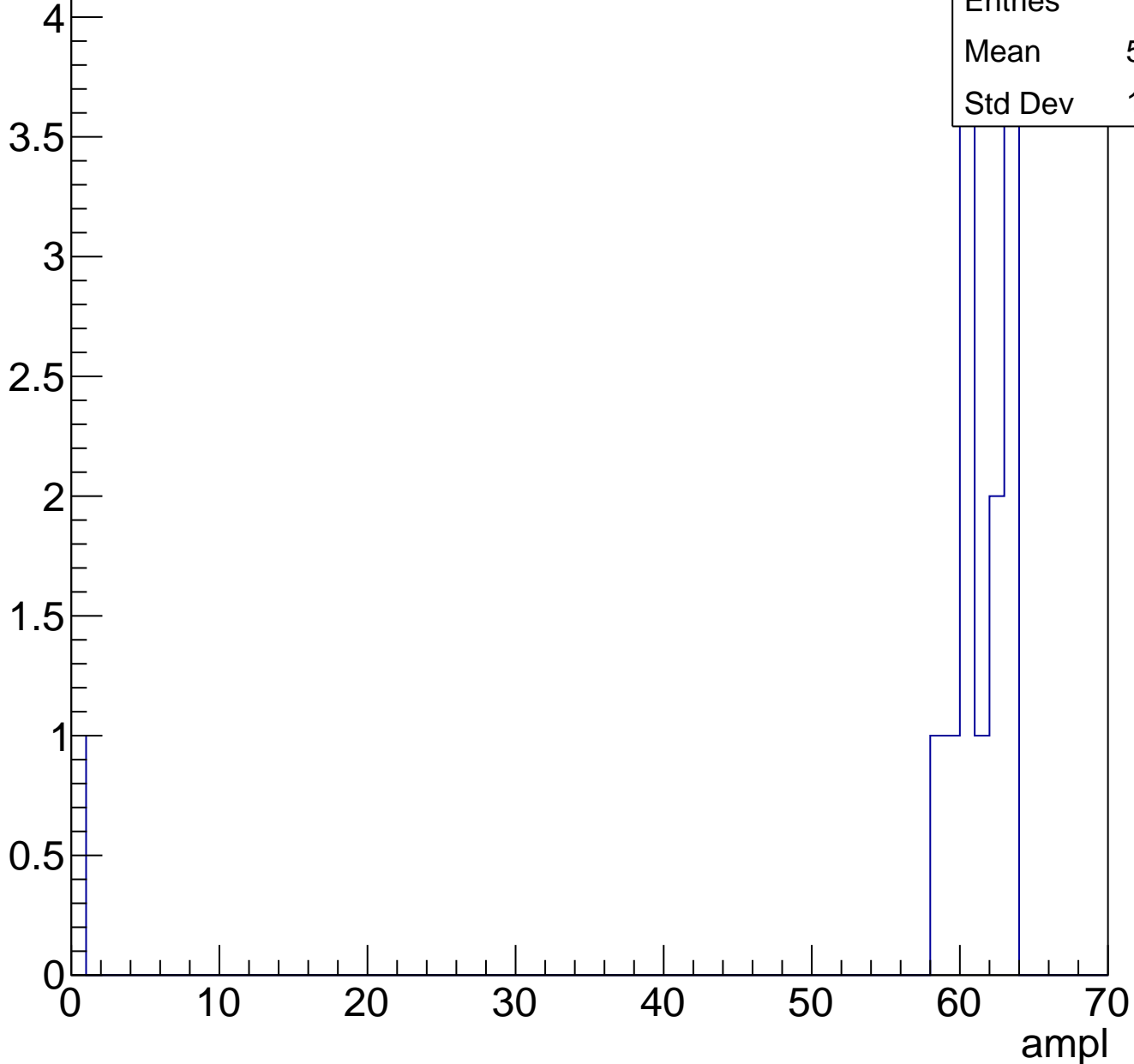
Entry



B1L103S, U13-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U13-ch75, adc0

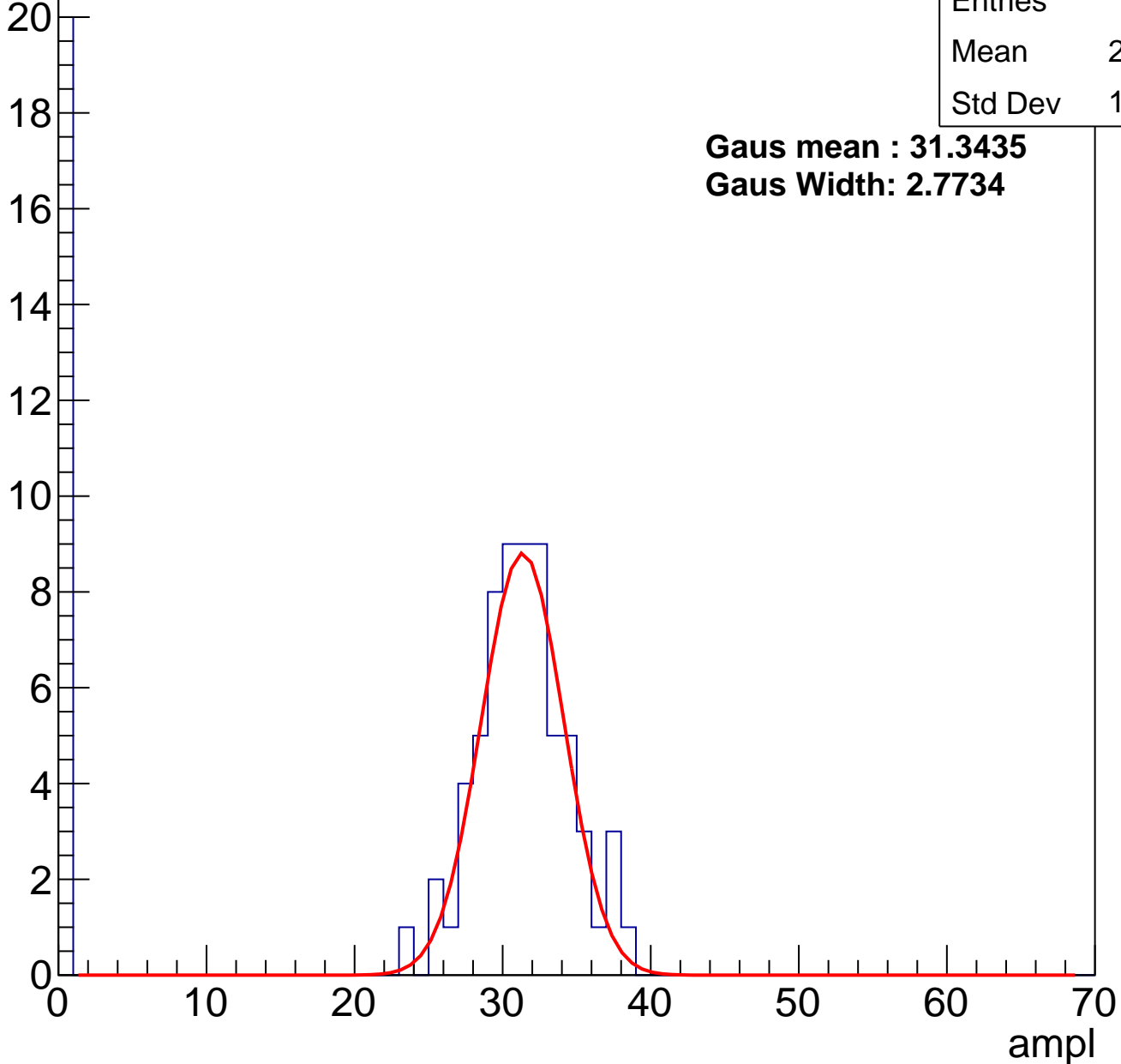
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.73
Std Dev	13.34

Gaus mean : 31.3435

Gaus Width: 2.7734

Entry



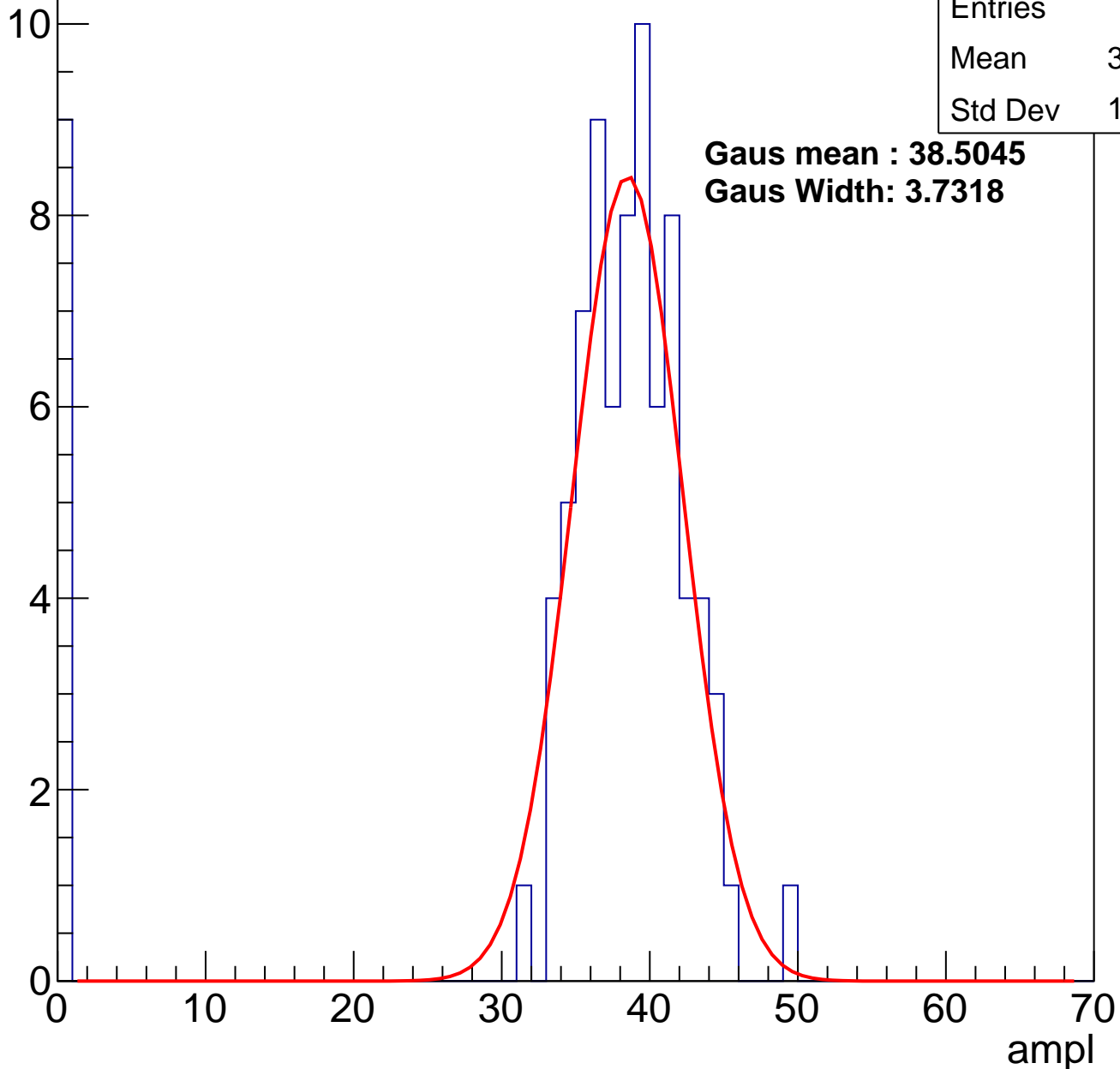
B1L103S, U13-ch75, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	34.33
Std Dev	12.16

Gaus mean : 38.5045
Gaus Width: 3.7318

Entry



B1L103S, U13-ch75, adc2

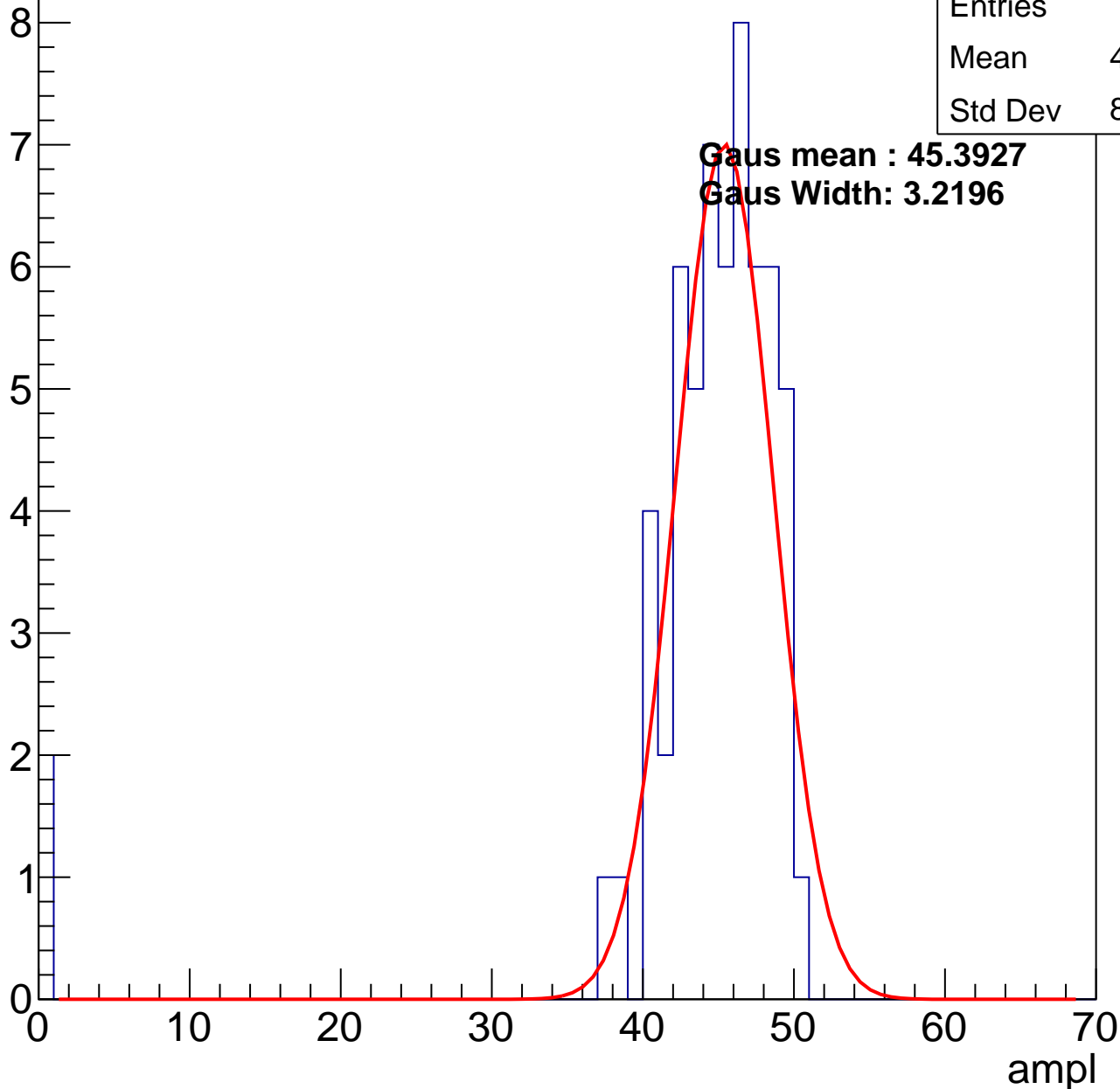
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	43.25
Std Dev	8.547

Gaus mean : 45.3927

Gaus Width: 3.2196



B1L103S, U13-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	51.44
Std Dev	3.448

Entry

10

8

6

4

2

0

0

10

20

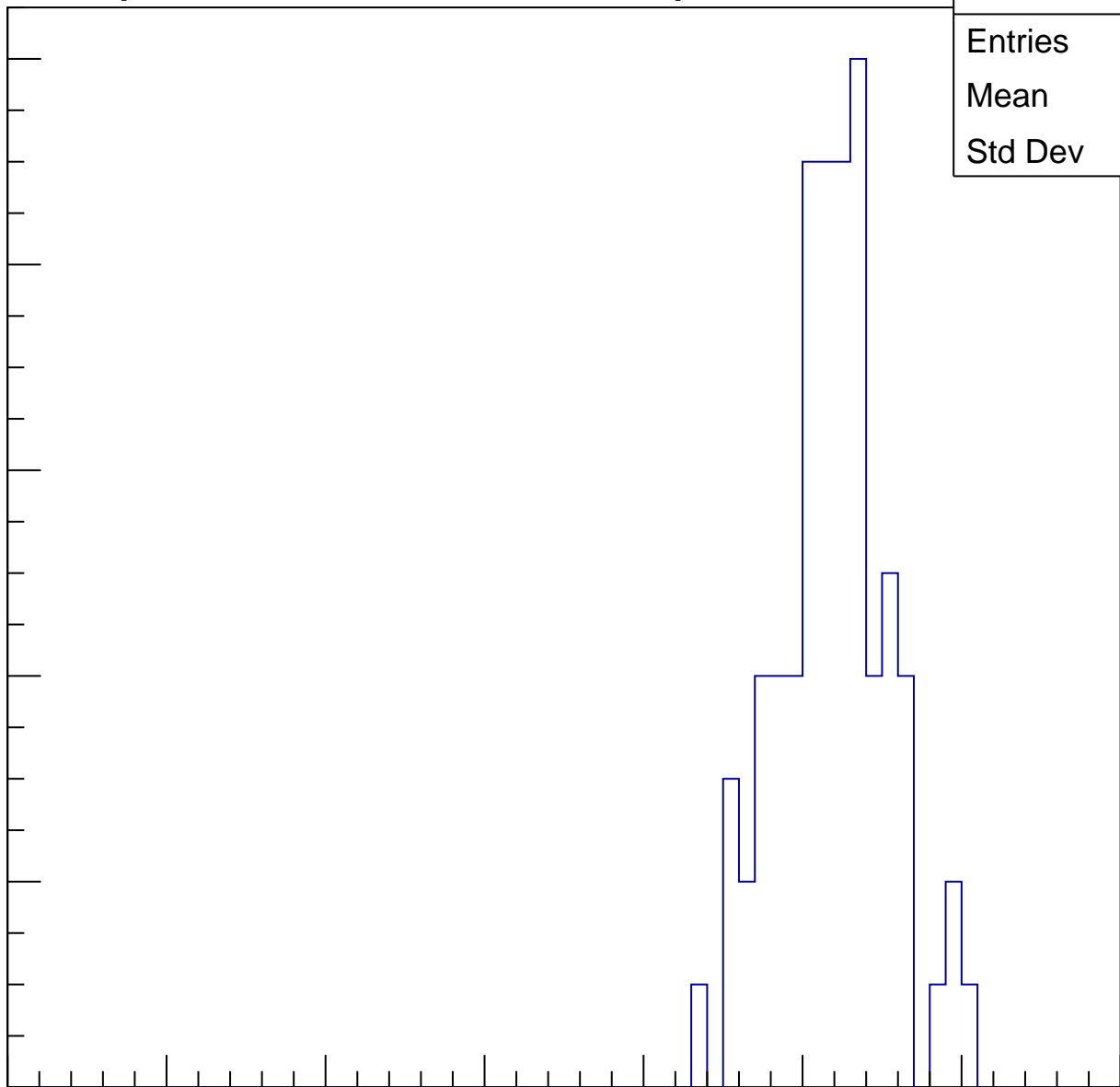
30

40

50

60

ampl

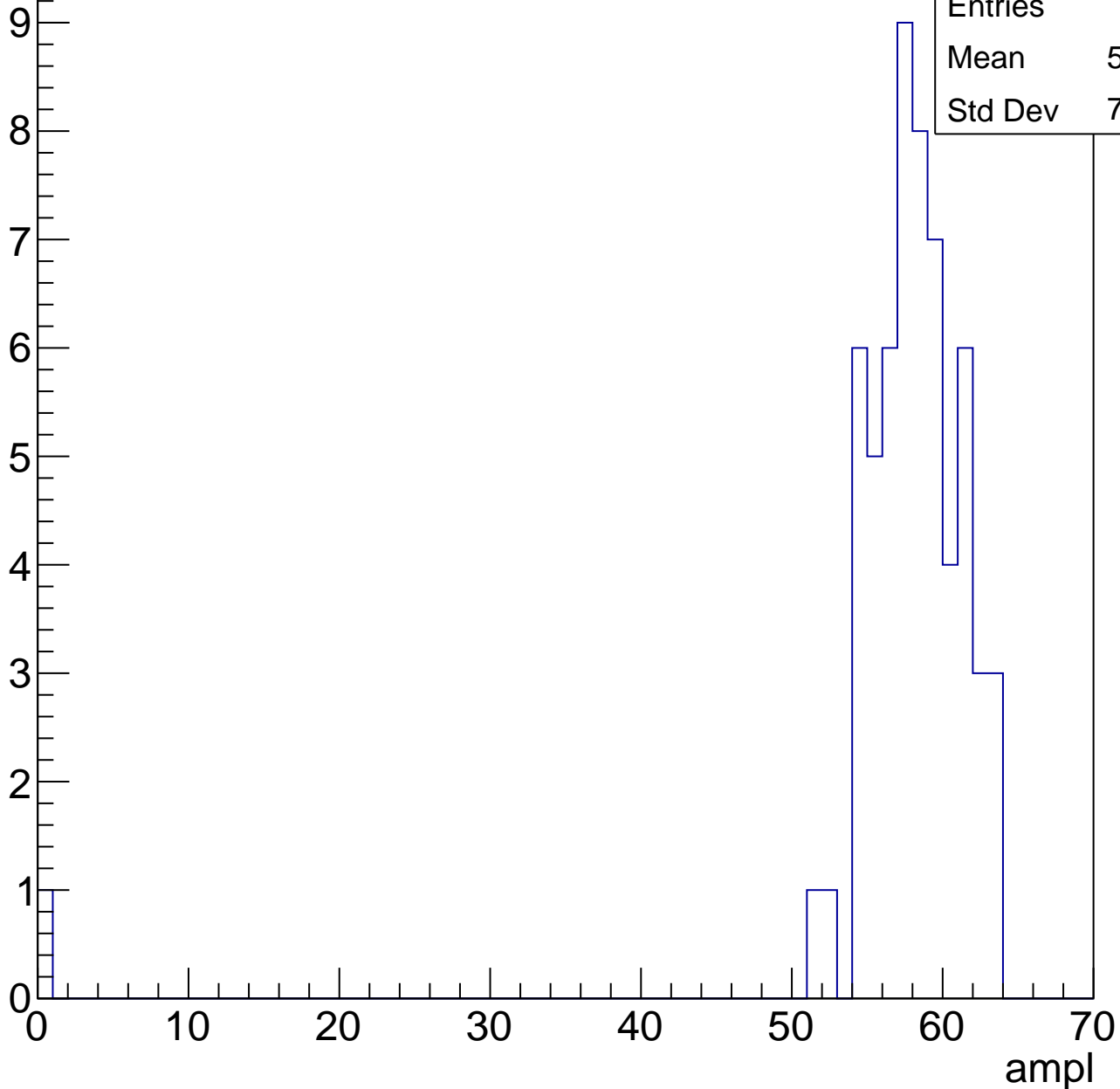


B1L103S, U13-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

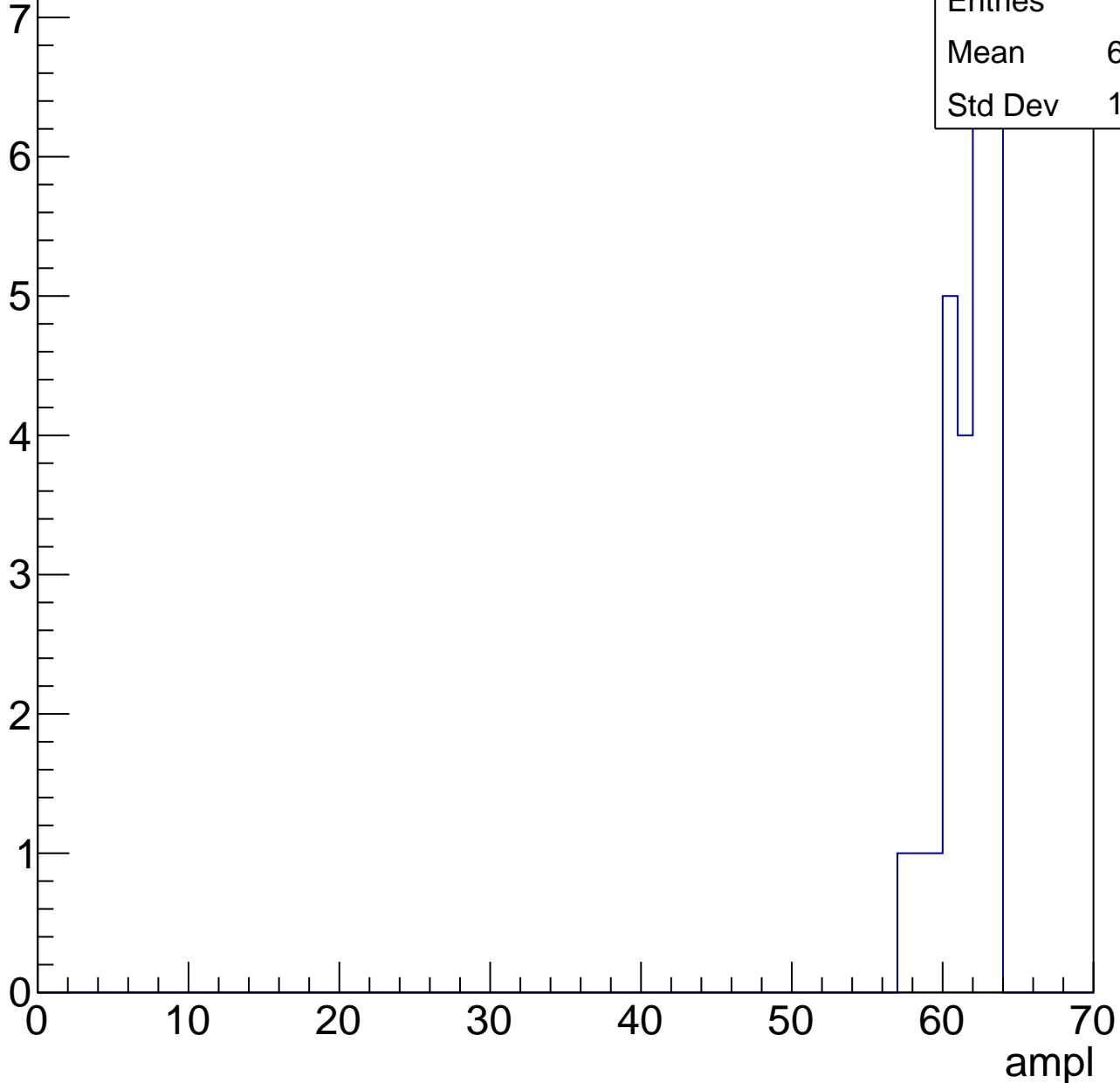
Entries	60
Mean	56.82
Std Dev	7.894



B1L103S, U13-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch76, adc0

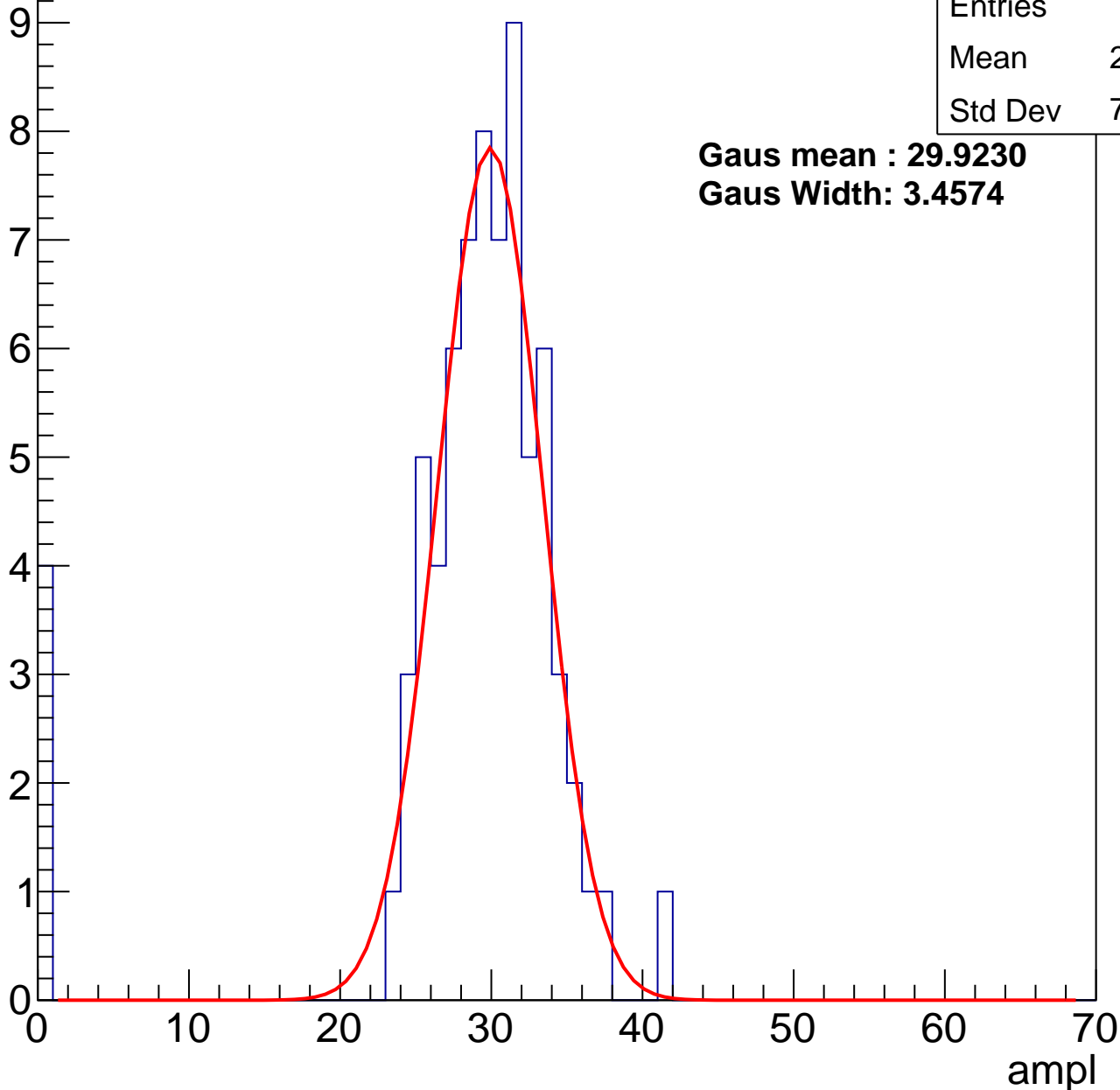
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	28.04
Std Dev	7.533

Gaus mean : 29.9230

Gaus Width: 3.4574



B1L103S, U13-ch76, adc1

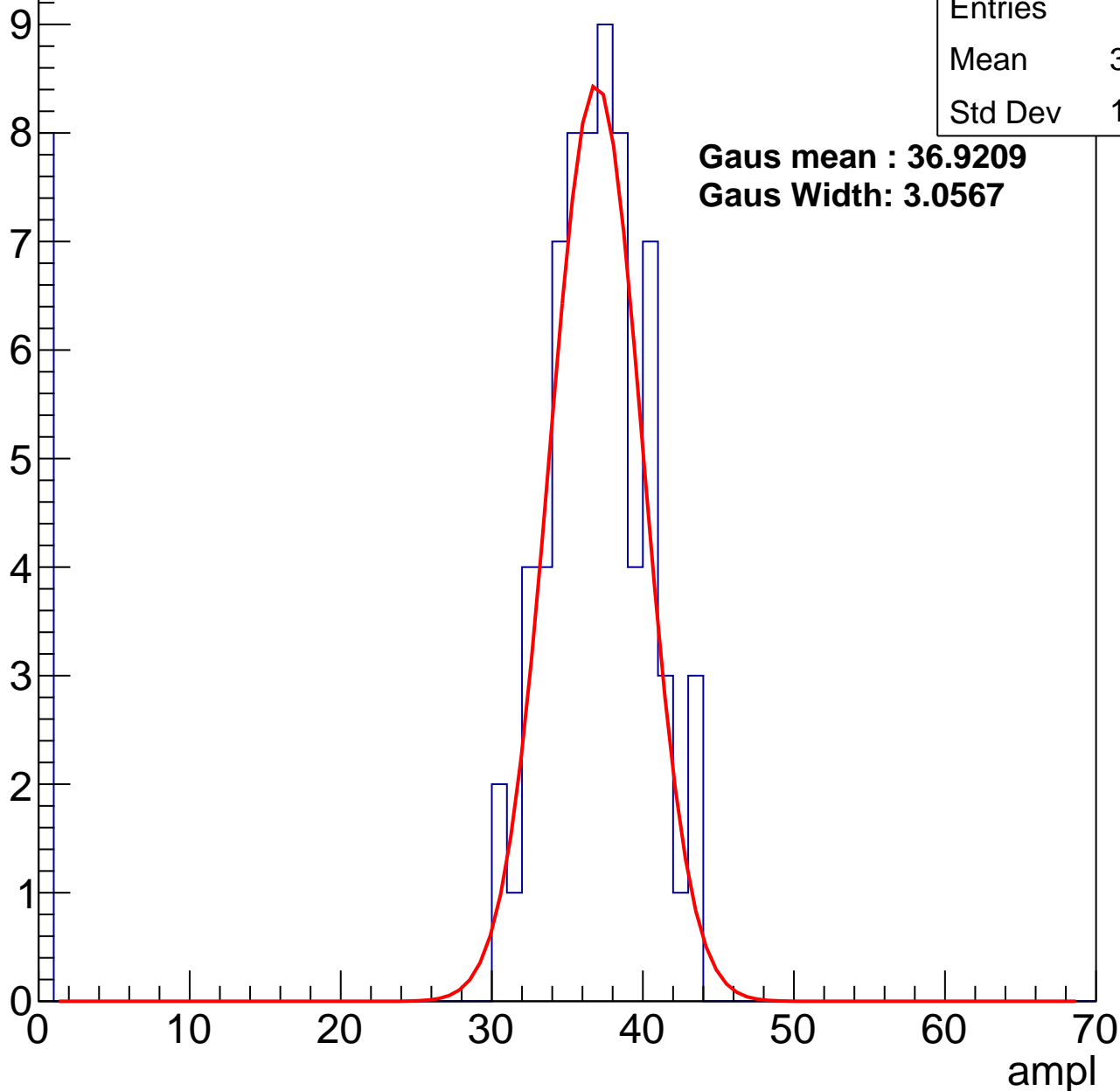
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	32.78
Std Dev	11.54

Gaus mean : 36.9209

Gaus Width: 3.0567



B1L103S, U13-ch76, adc2

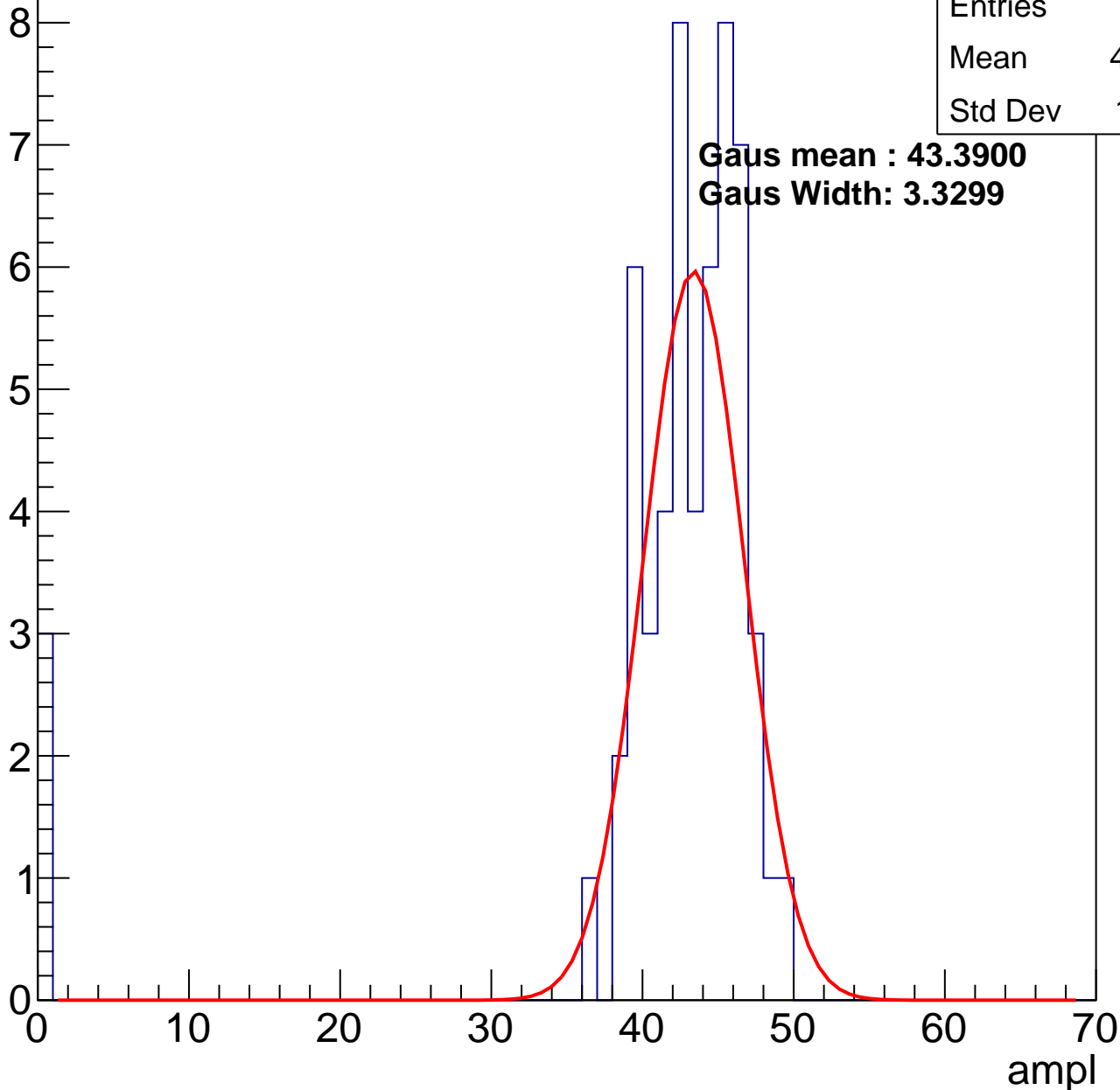
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	40.74
Std Dev	10.01

Gaus mean : 43.3900

Gaus Width: 3.3299

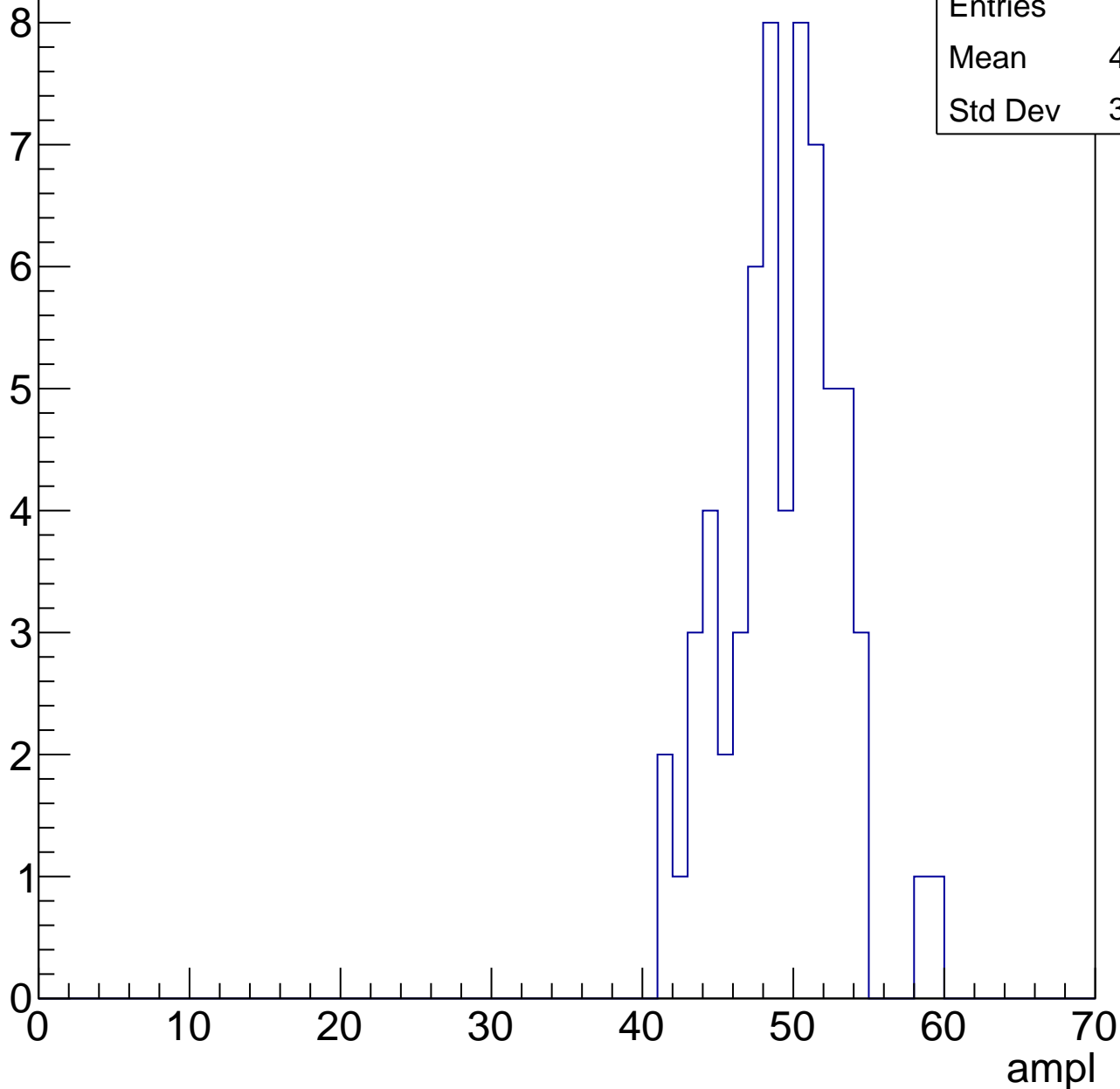


B1L103S, U13-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.89
Std Dev	3.763

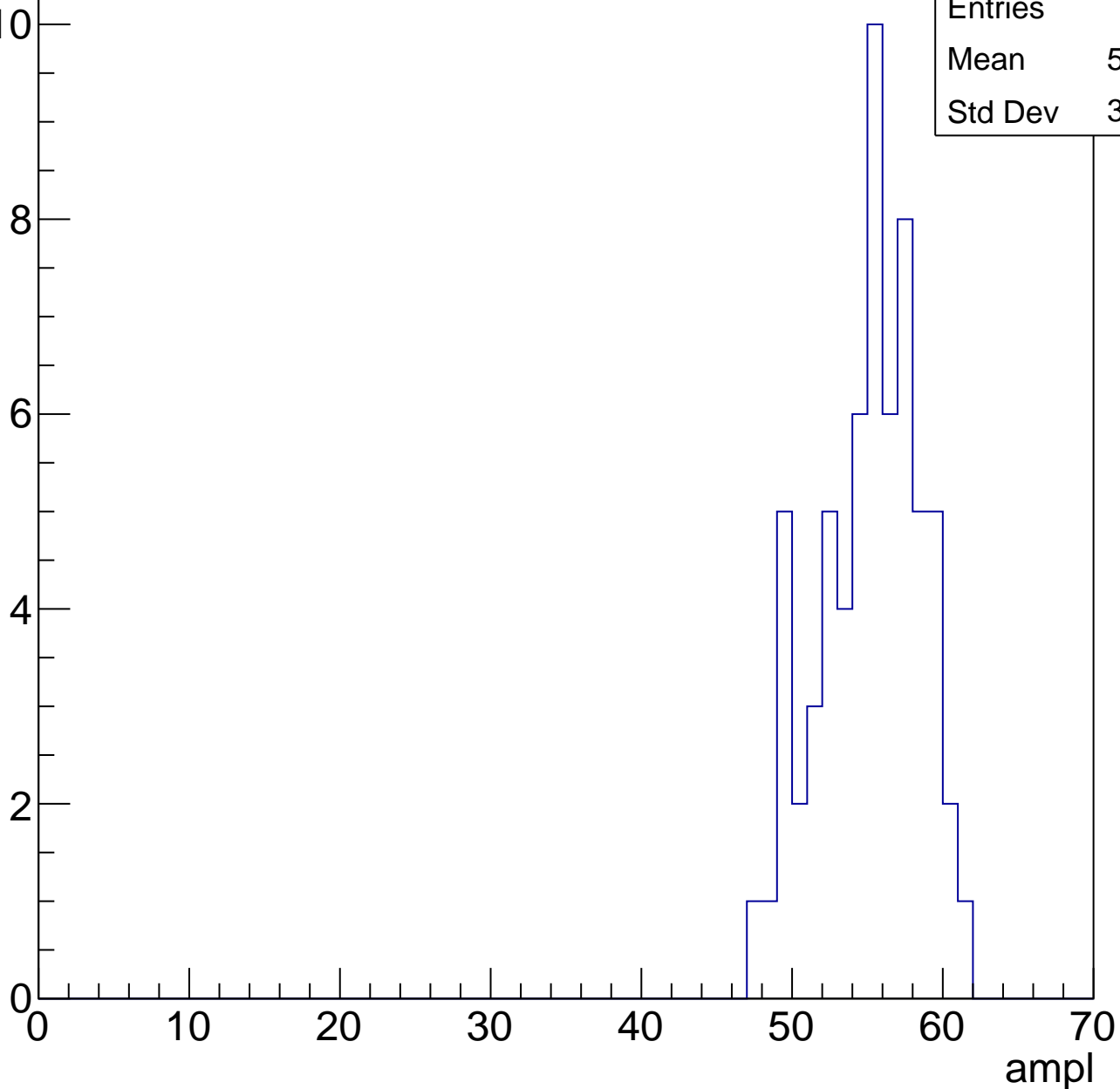


B1L103S, U13-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.64
Std Dev	3.304



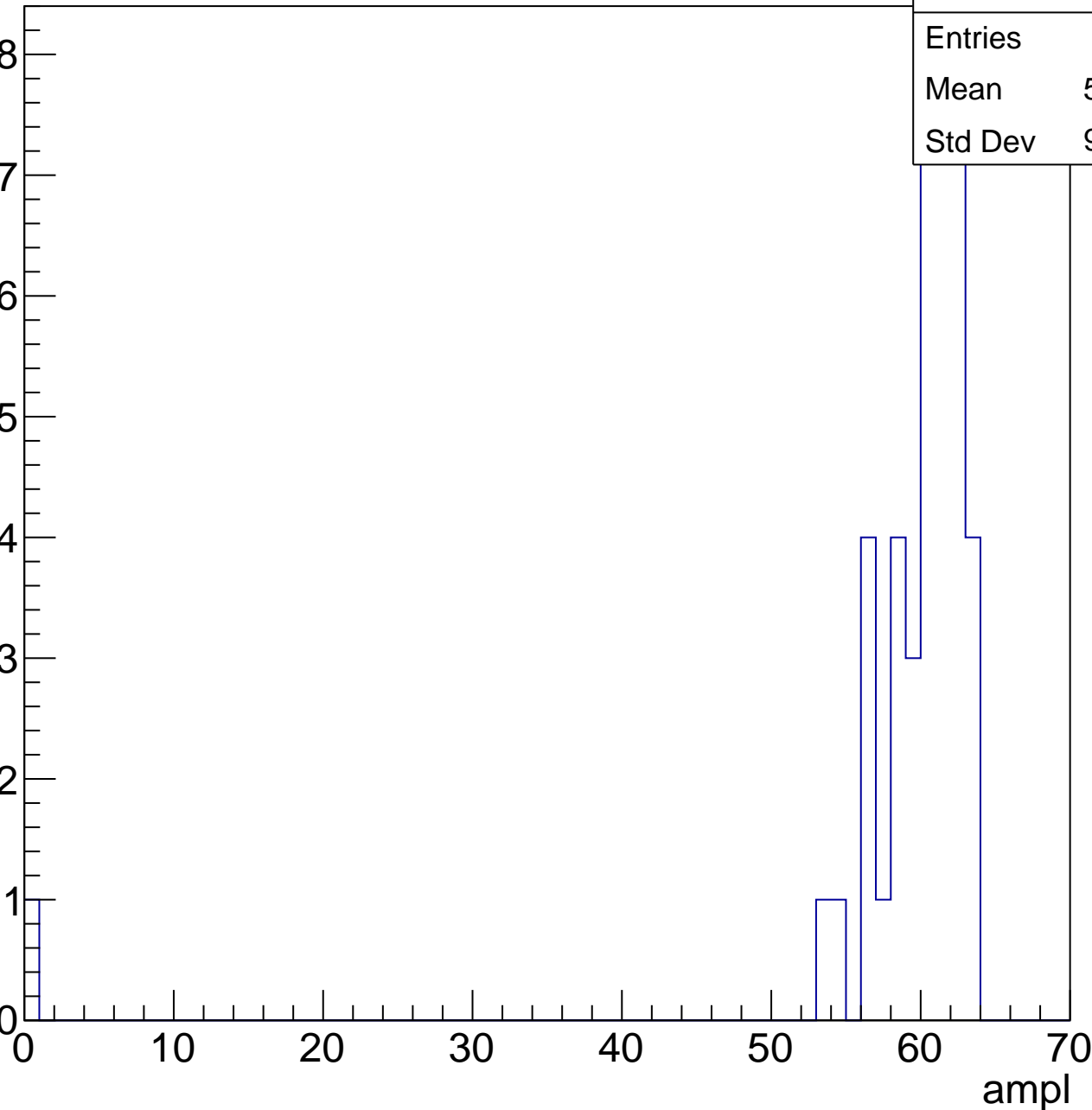
B1L103S, U13-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	58.44
Std Dev	9.337

8
7
6
5
4
3
2
1
0

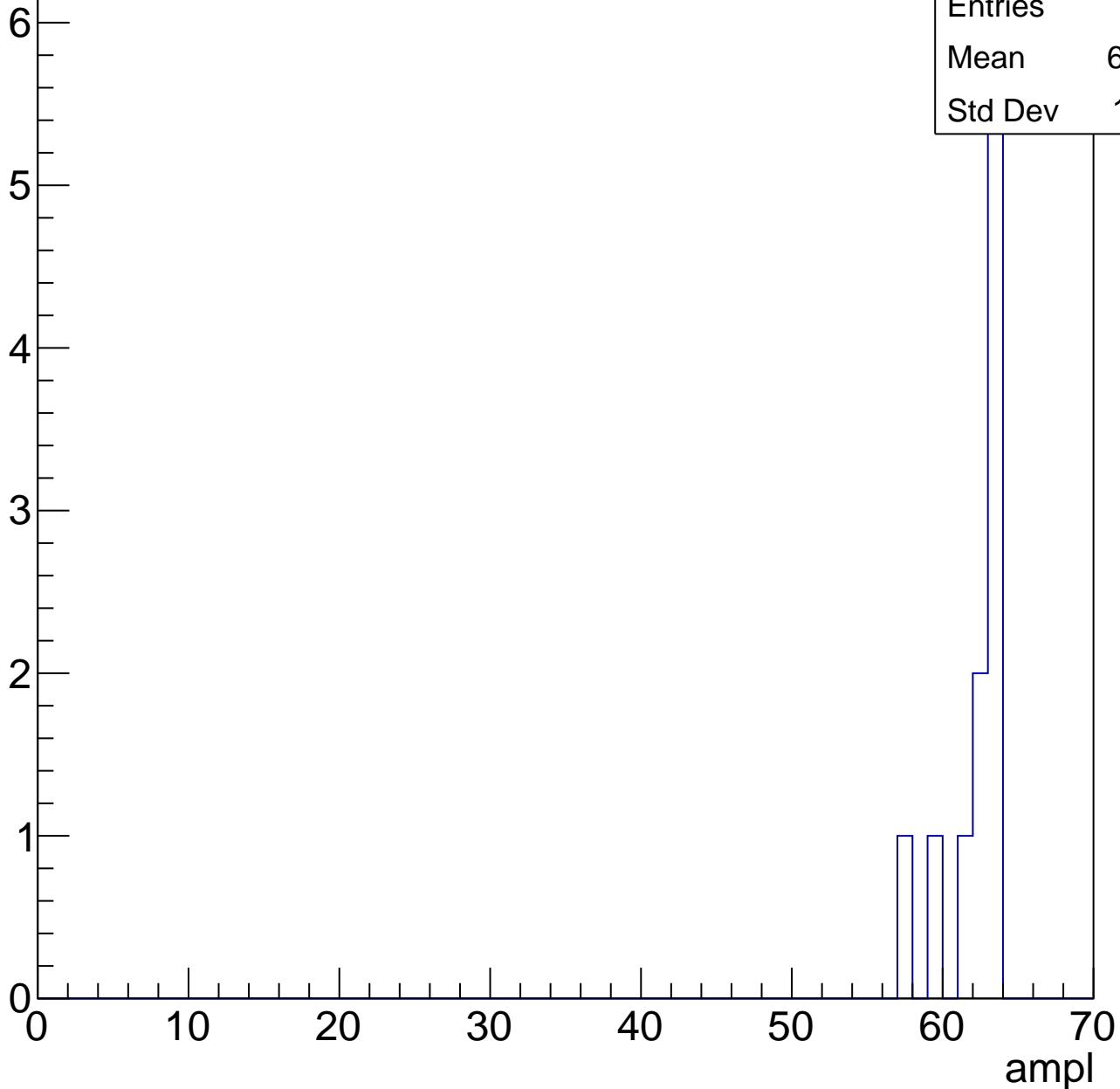


B1L103S, U13-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	61.73
Std Dev	1.911



B1L103S, U13-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U13-ch77, adc0

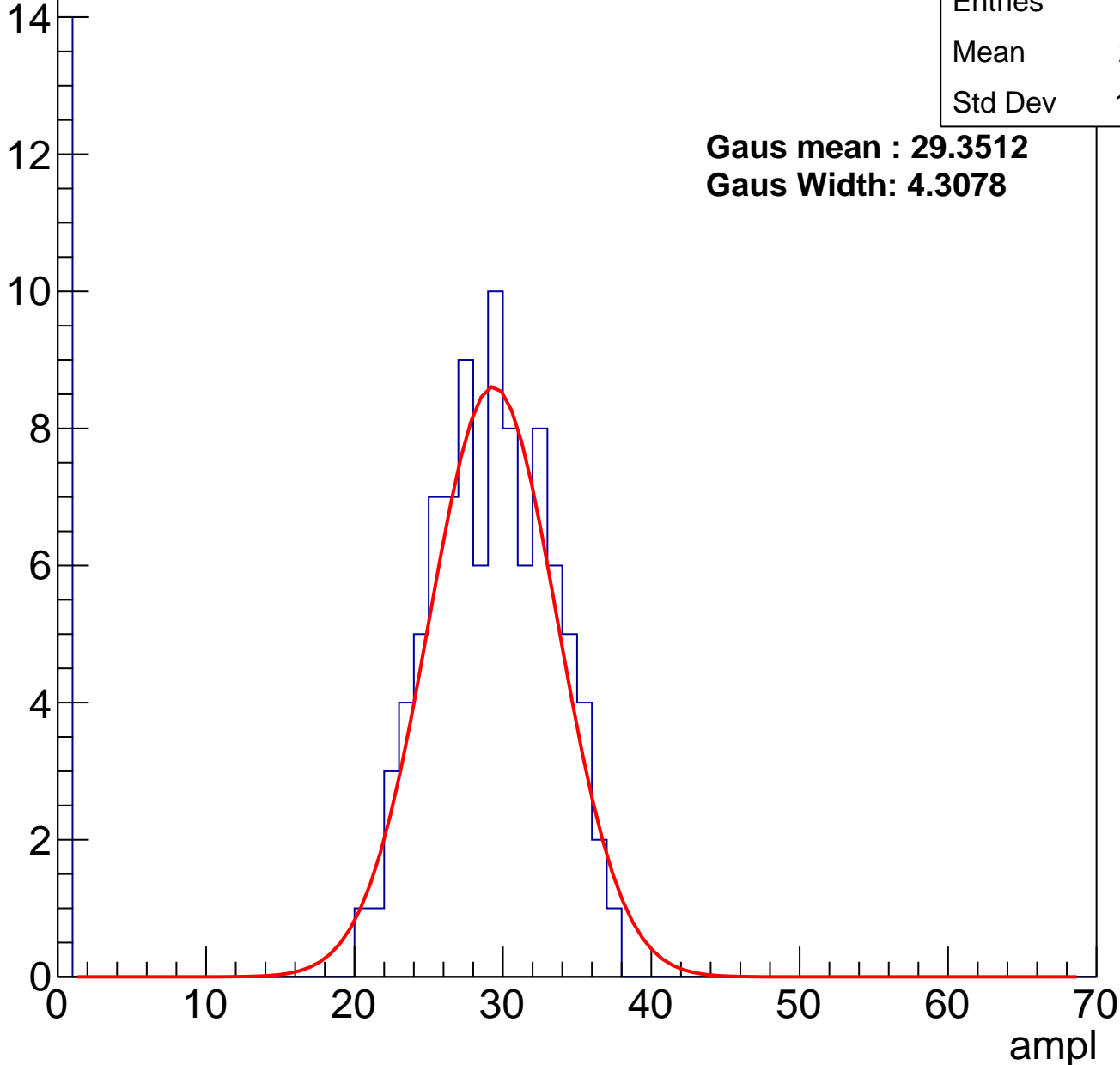
calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	25.01
Std Dev	10.36

Gaus mean : 29.3512

Gaus Width: 4.3078

Entry



B1L103S, U13-ch77, adc1

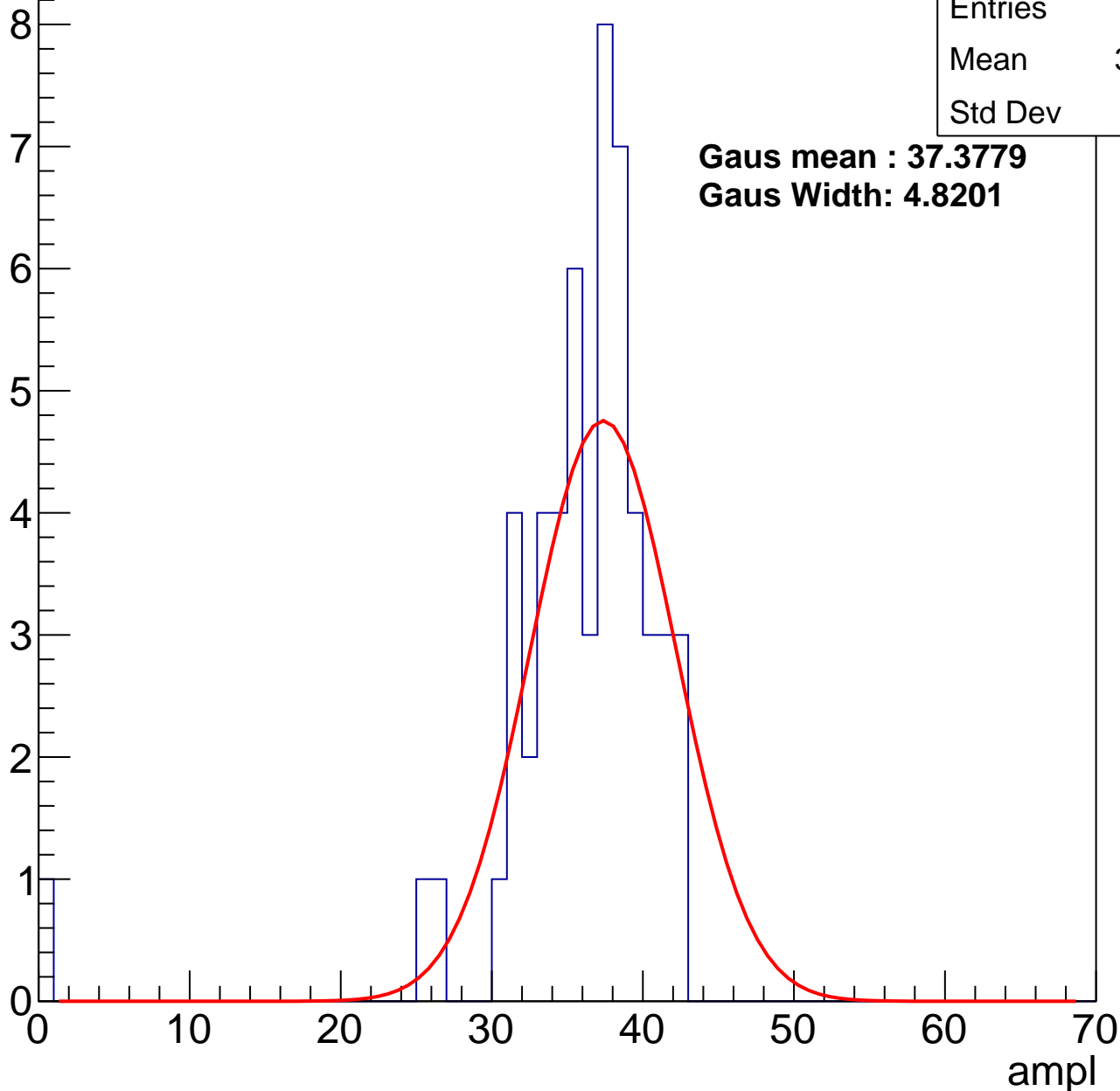
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	35.31
Std Dev	6.06

Gaus mean : 37.3779

Gaus Width: 4.8201



B1L103S, U13-ch77, adc2

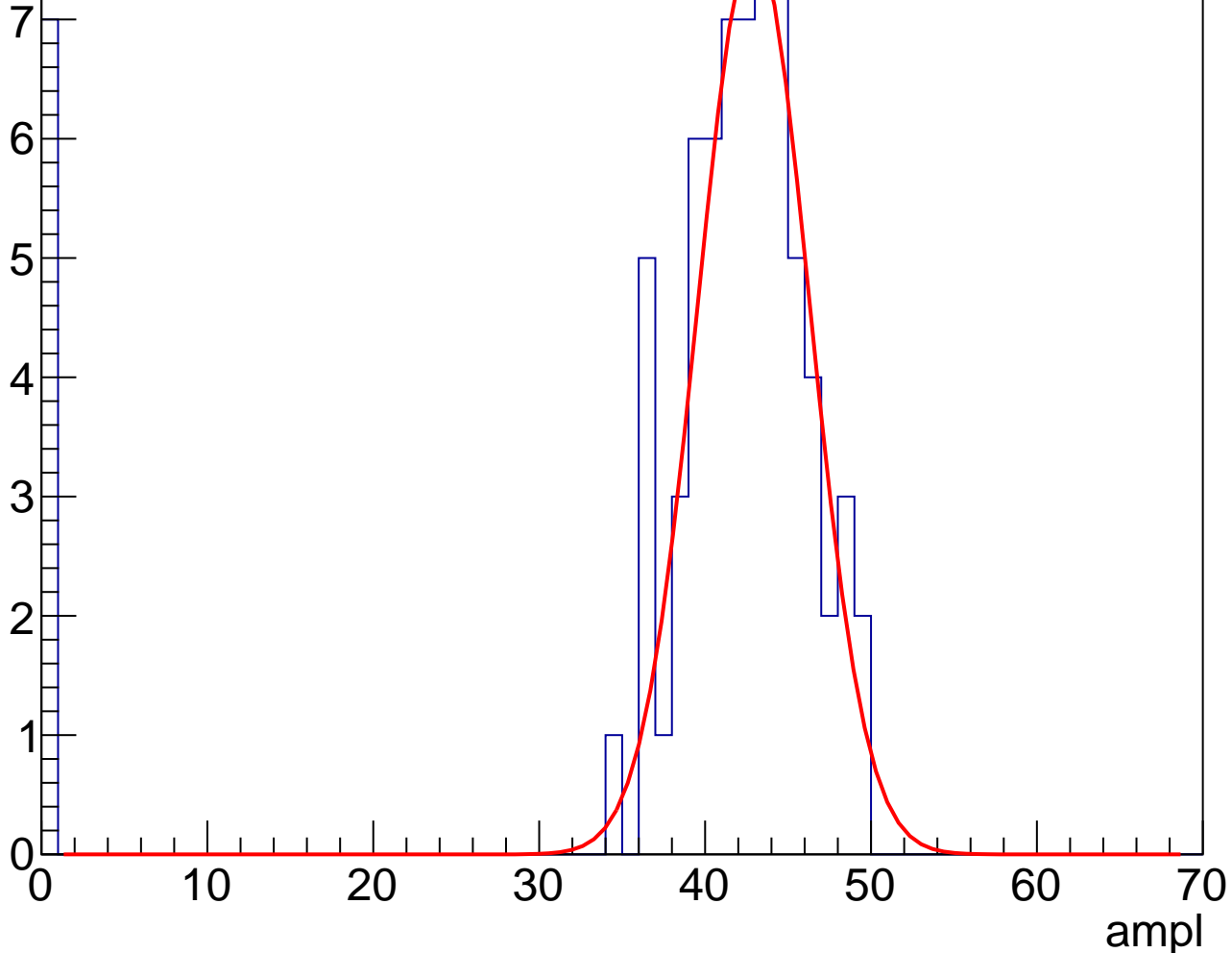
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	38.21
Std Dev	12.6

Gaus mean : 42.9332

Gaus Width: 3.3662

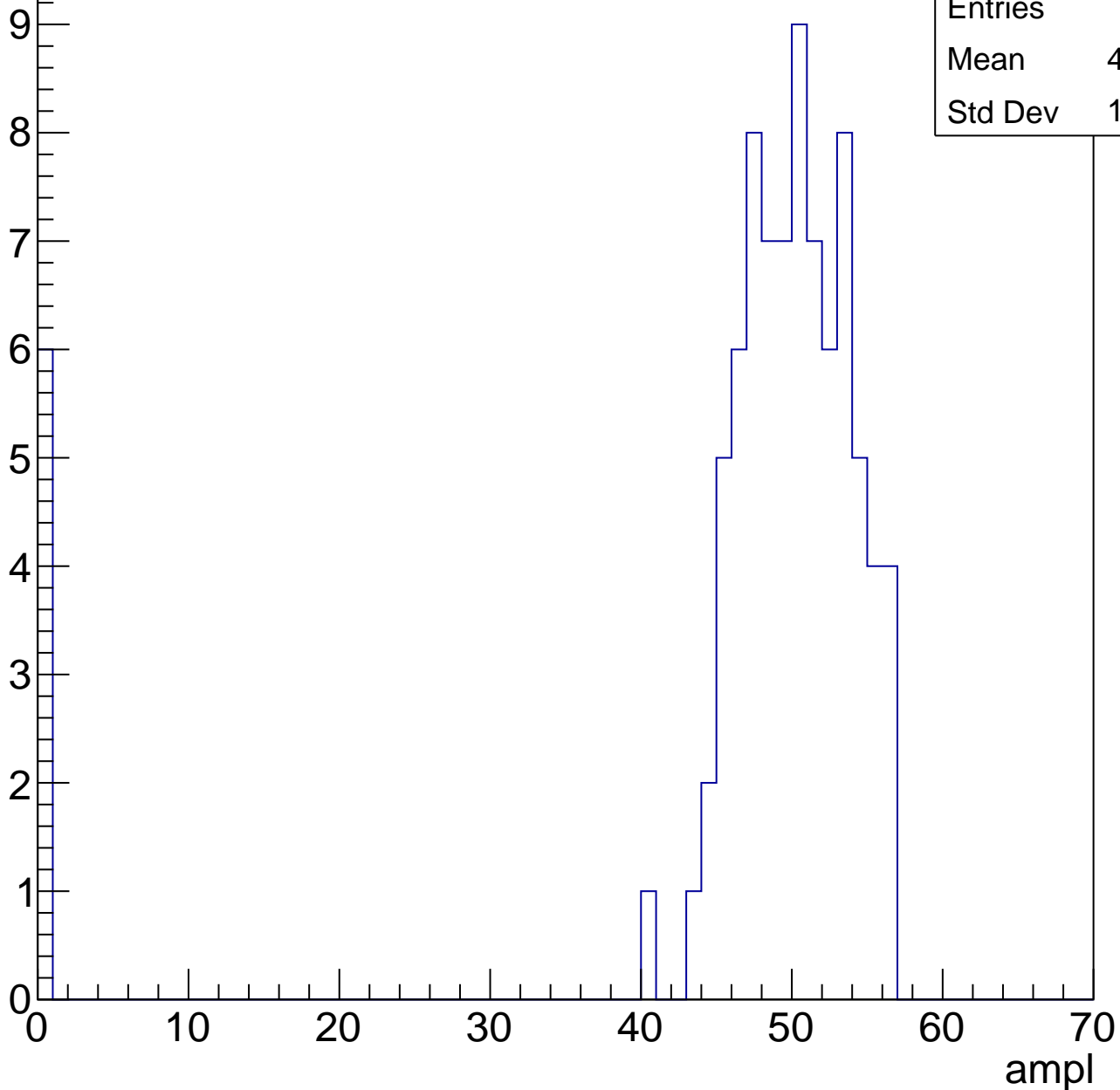


B1L103S, U13-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	46.33
Std Dev	13.13

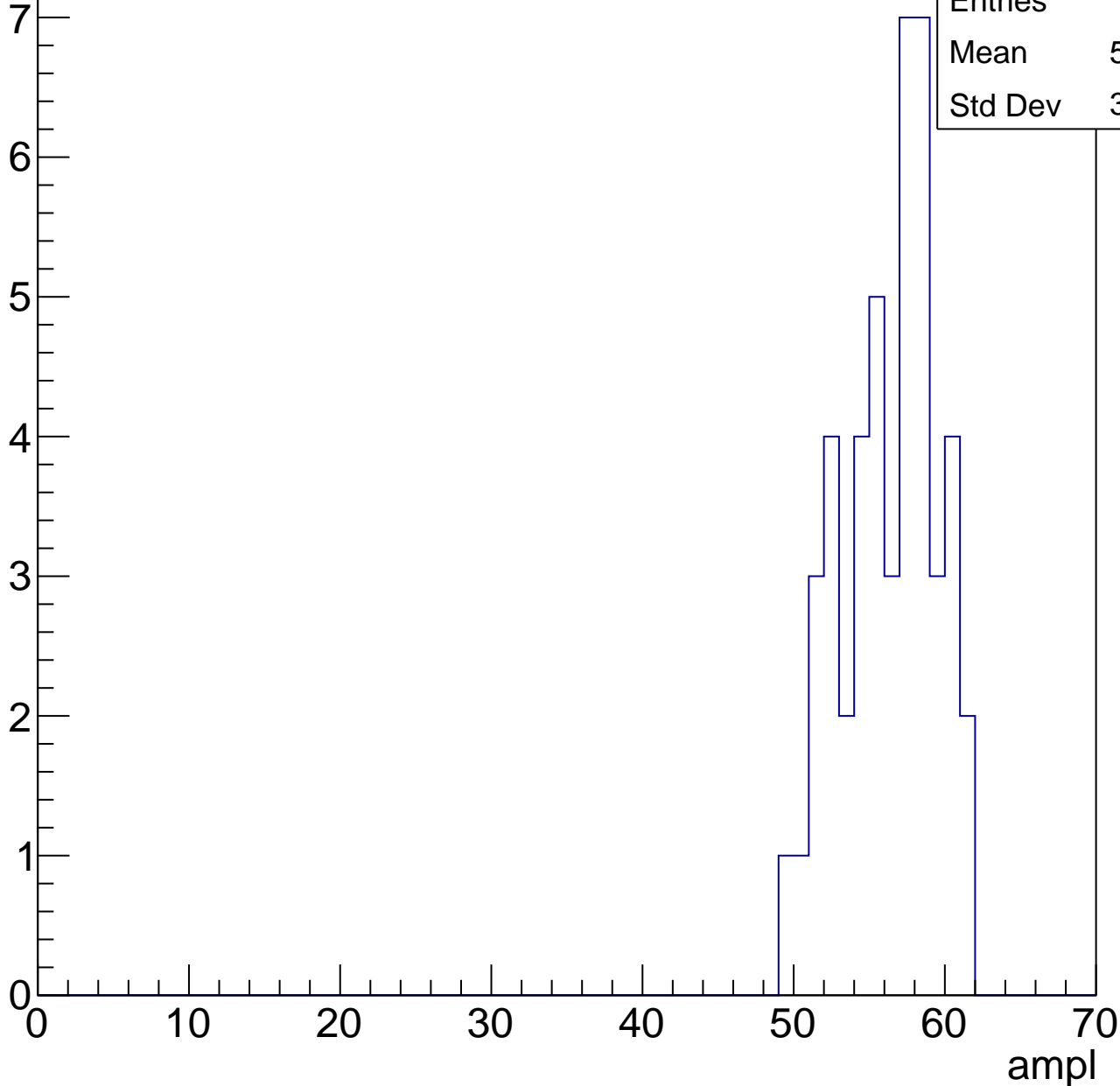


B1L103S, U13-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	55.85
Std Dev	3.086

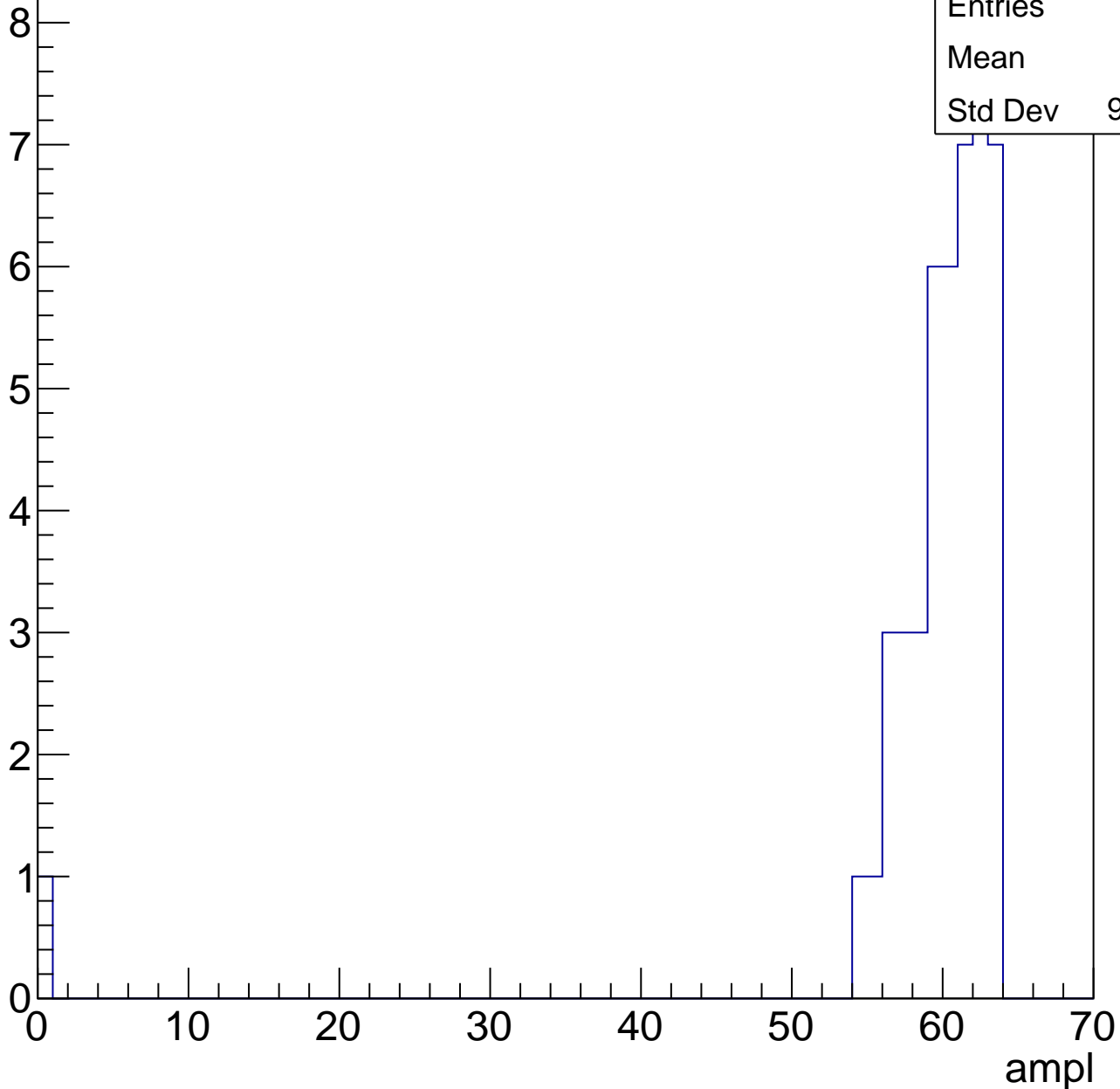


B1L103S, U13-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.7
Std Dev	9.062



B1L103S, U13-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

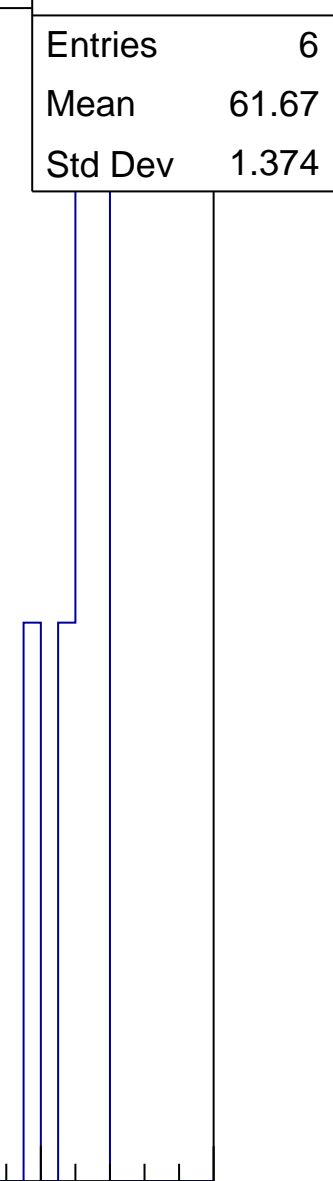
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.374

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U13-ch78, adc0

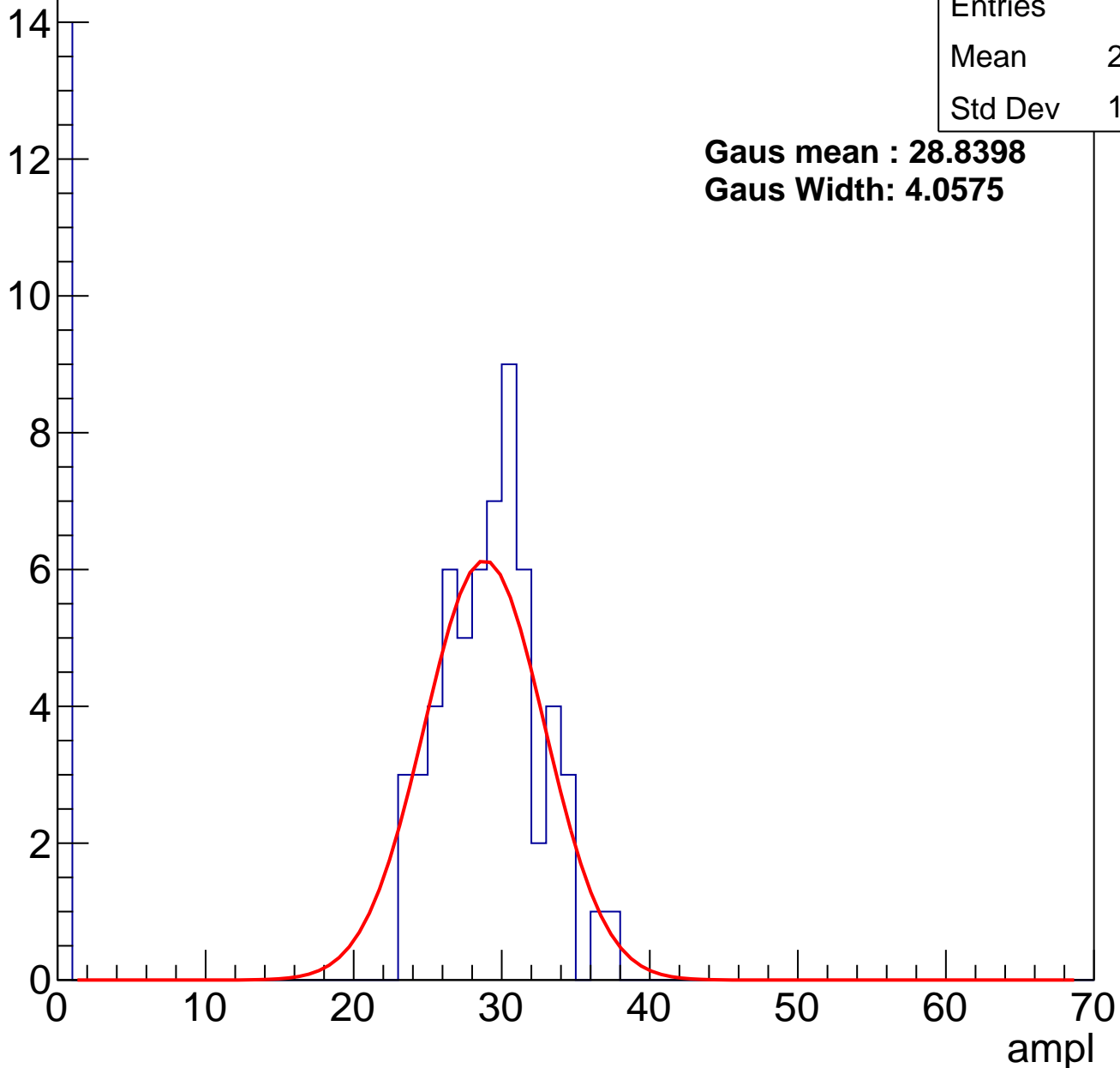
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	23.38
Std Dev	11.66

Gaus mean : 28.8398

Gaus Width: 4.0575

Entry



B1L103S, U13-ch78, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	31.27
Std Dev	11.04

Gaus mean : 35.5862

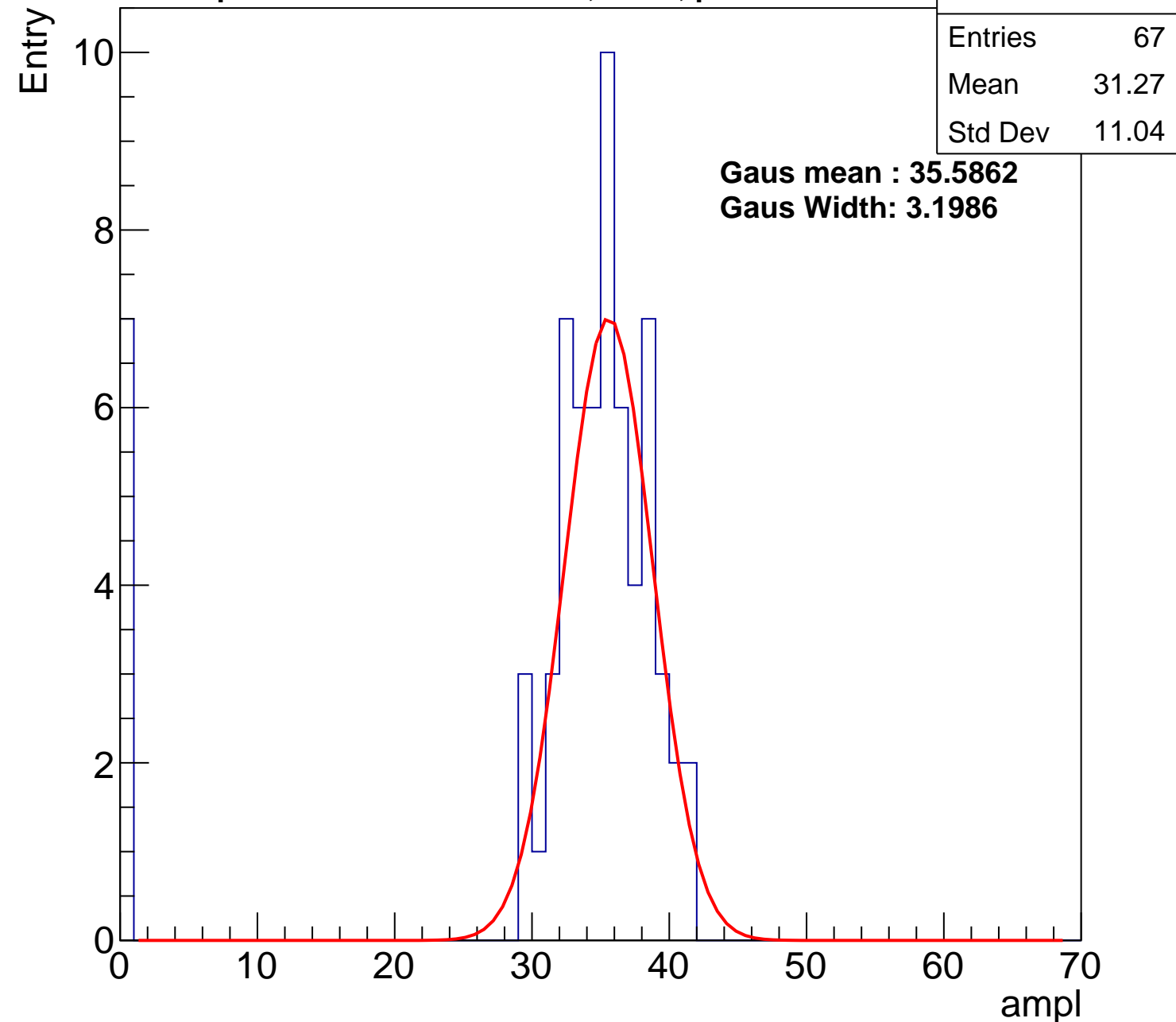
Gaus Width: 3.1986

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch78, adc2

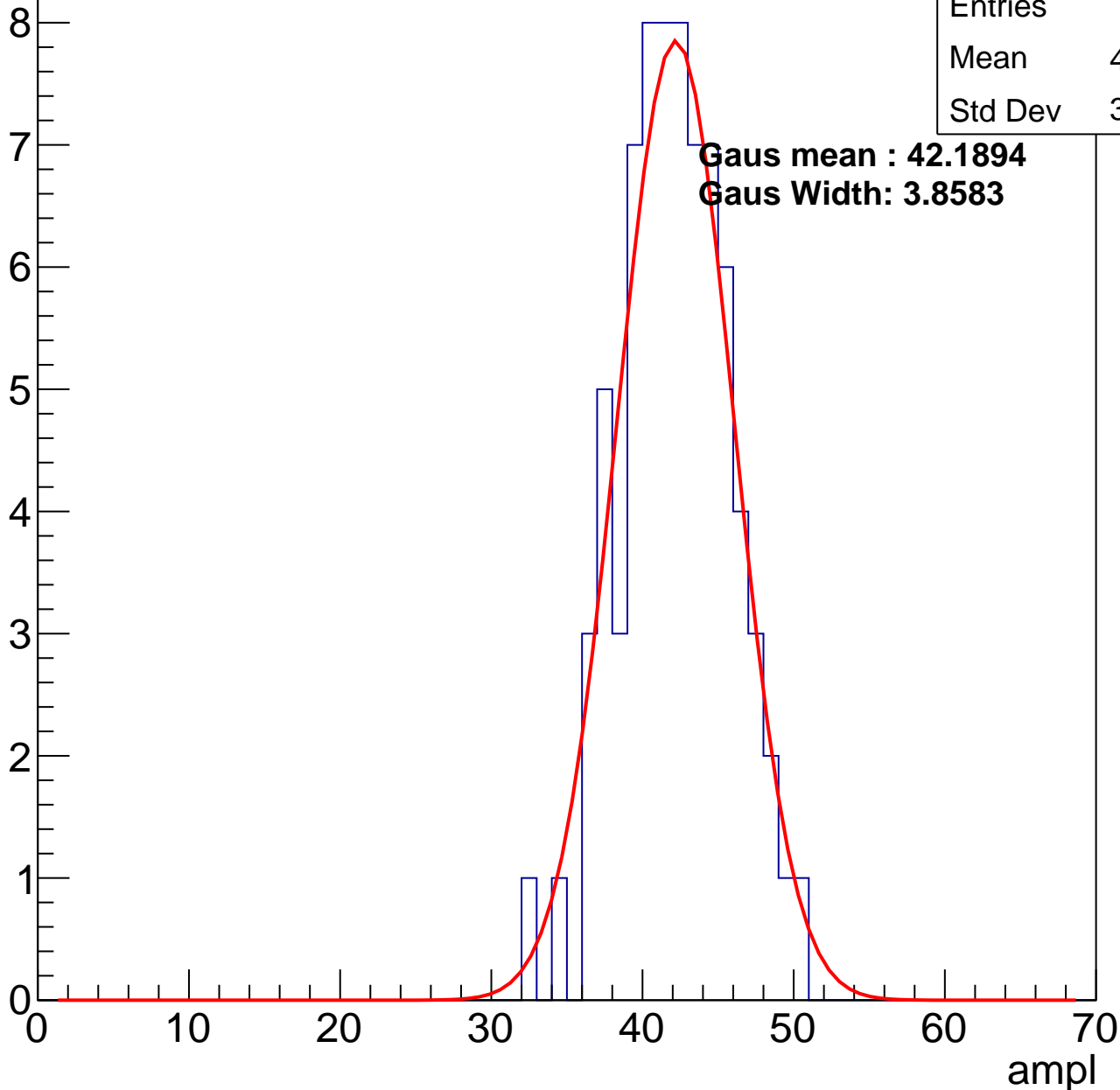
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	41.72
Std Dev	3.569

Gaus mean : 42.1894

Gaus Width: 3.8583

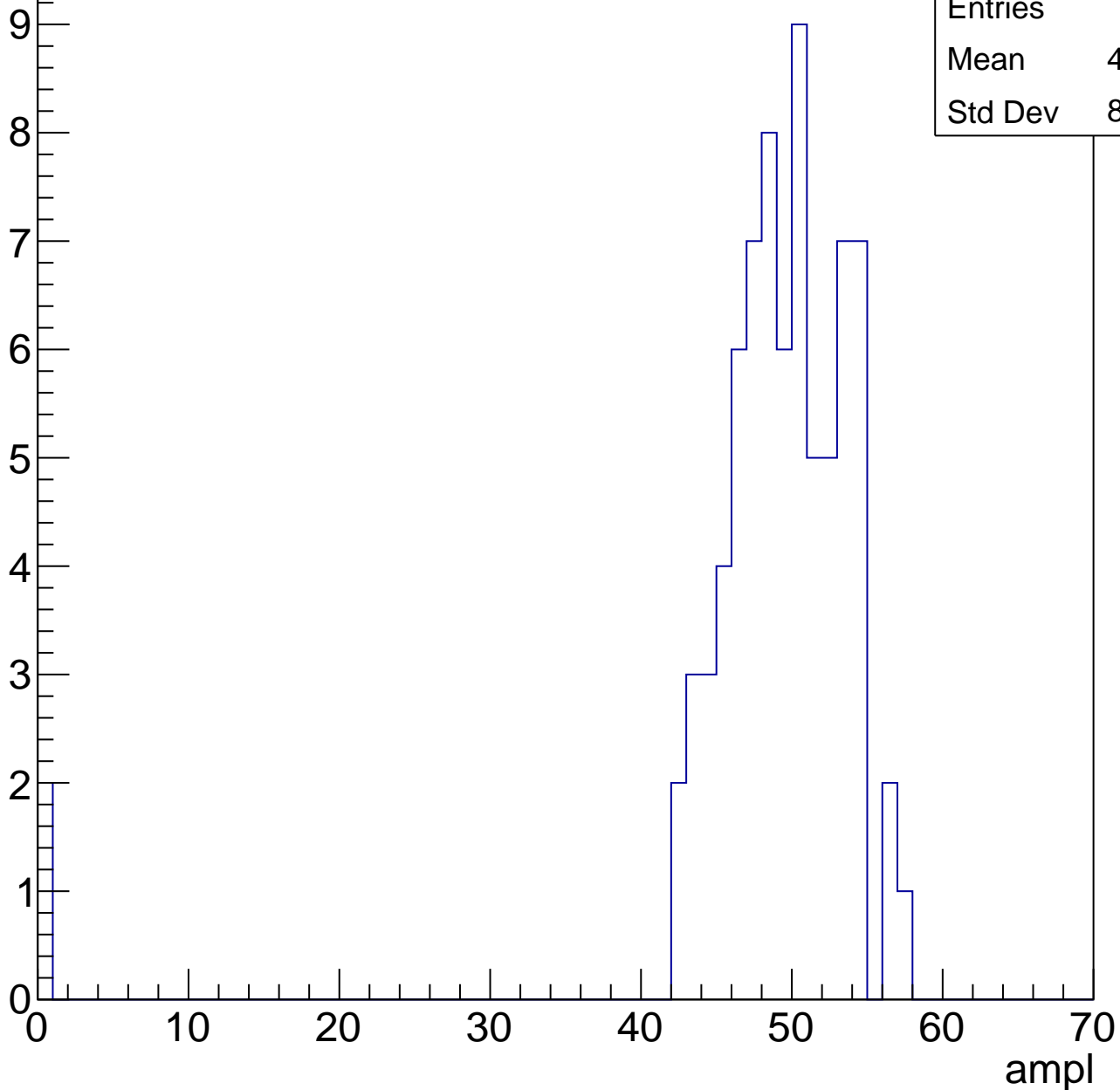


B1L103S, U13-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.94
Std Dev	8.583

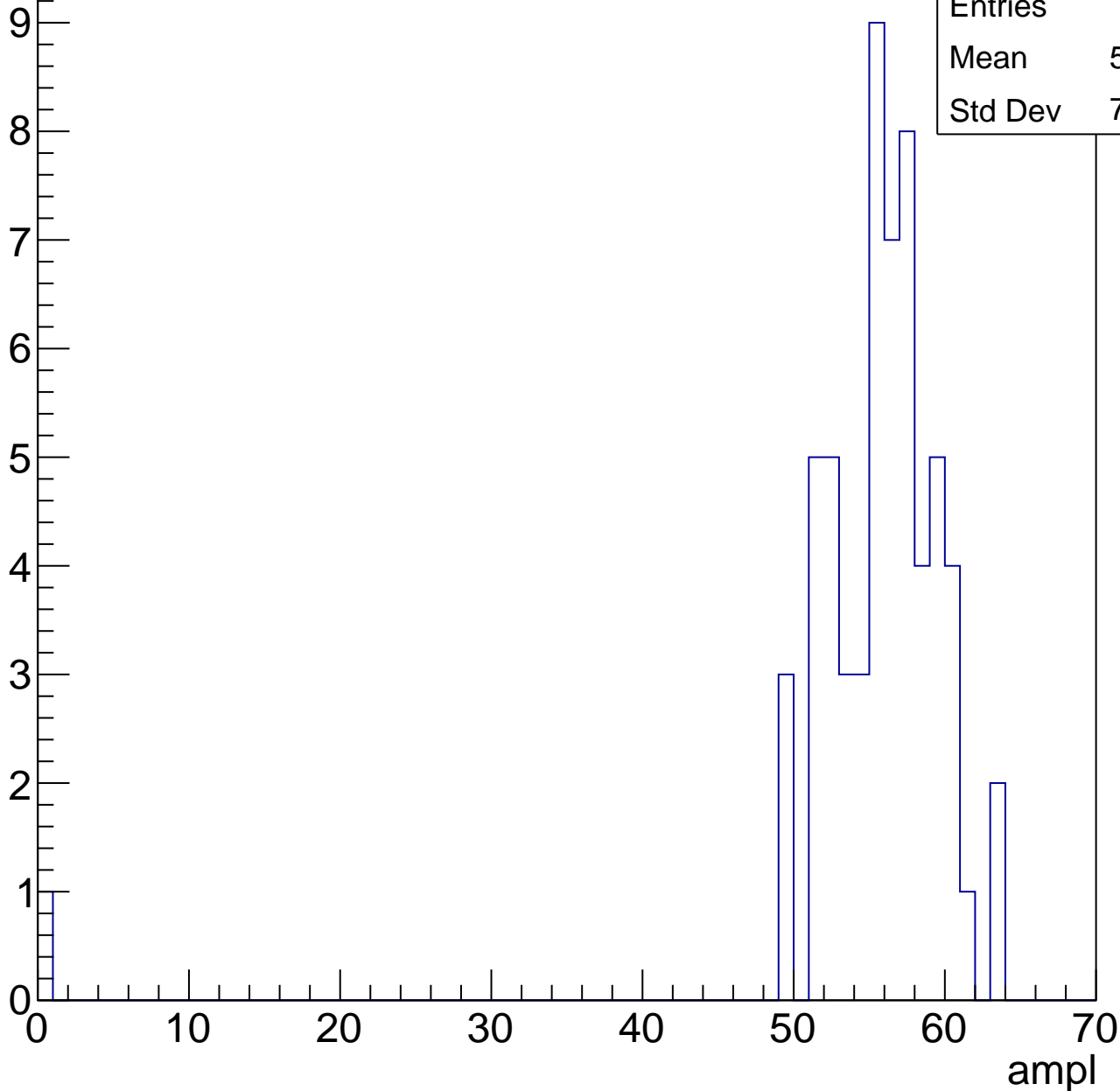


B1L103S, U13-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.67
Std Dev	7.837

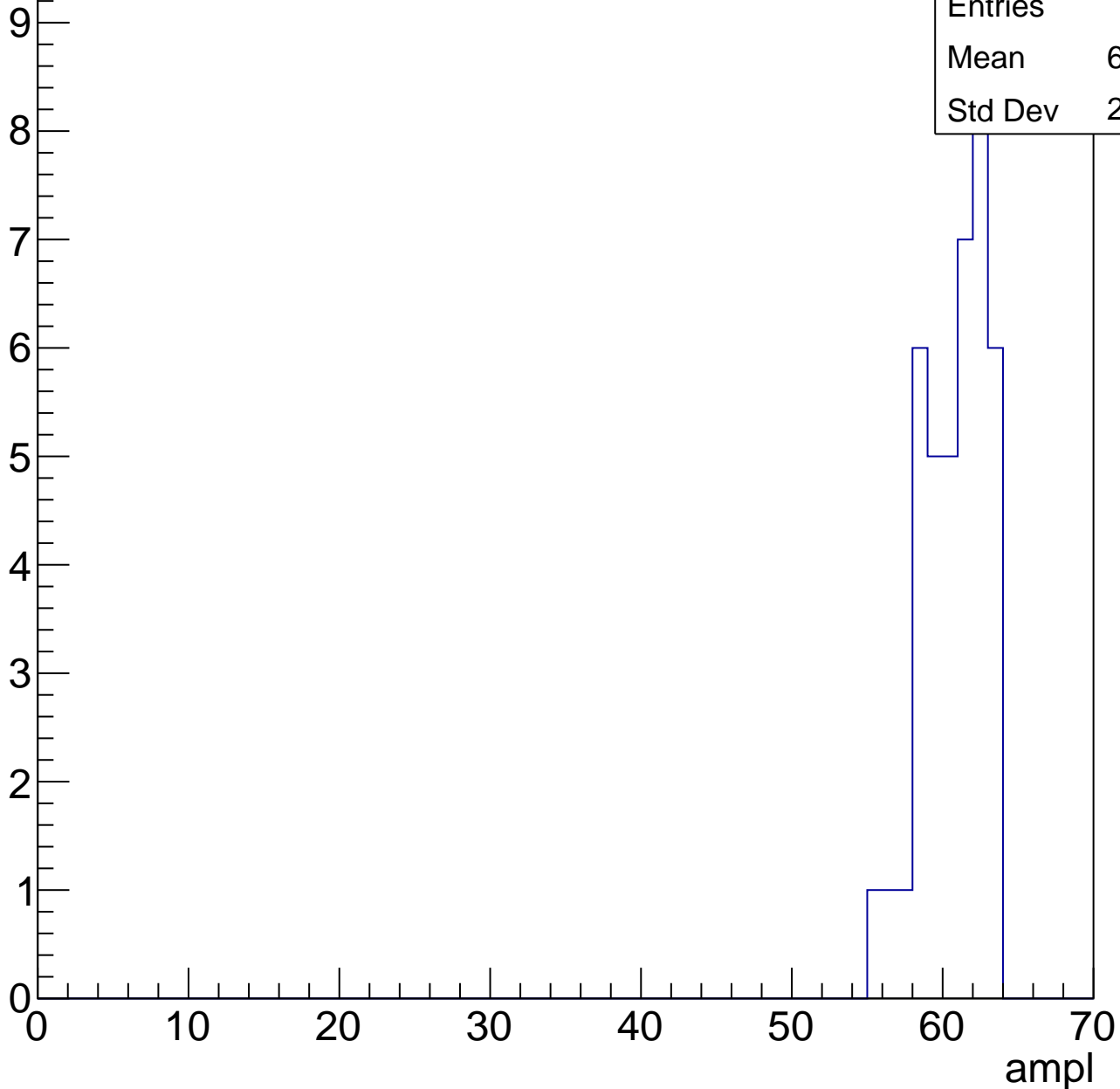


B1L103S, U13-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

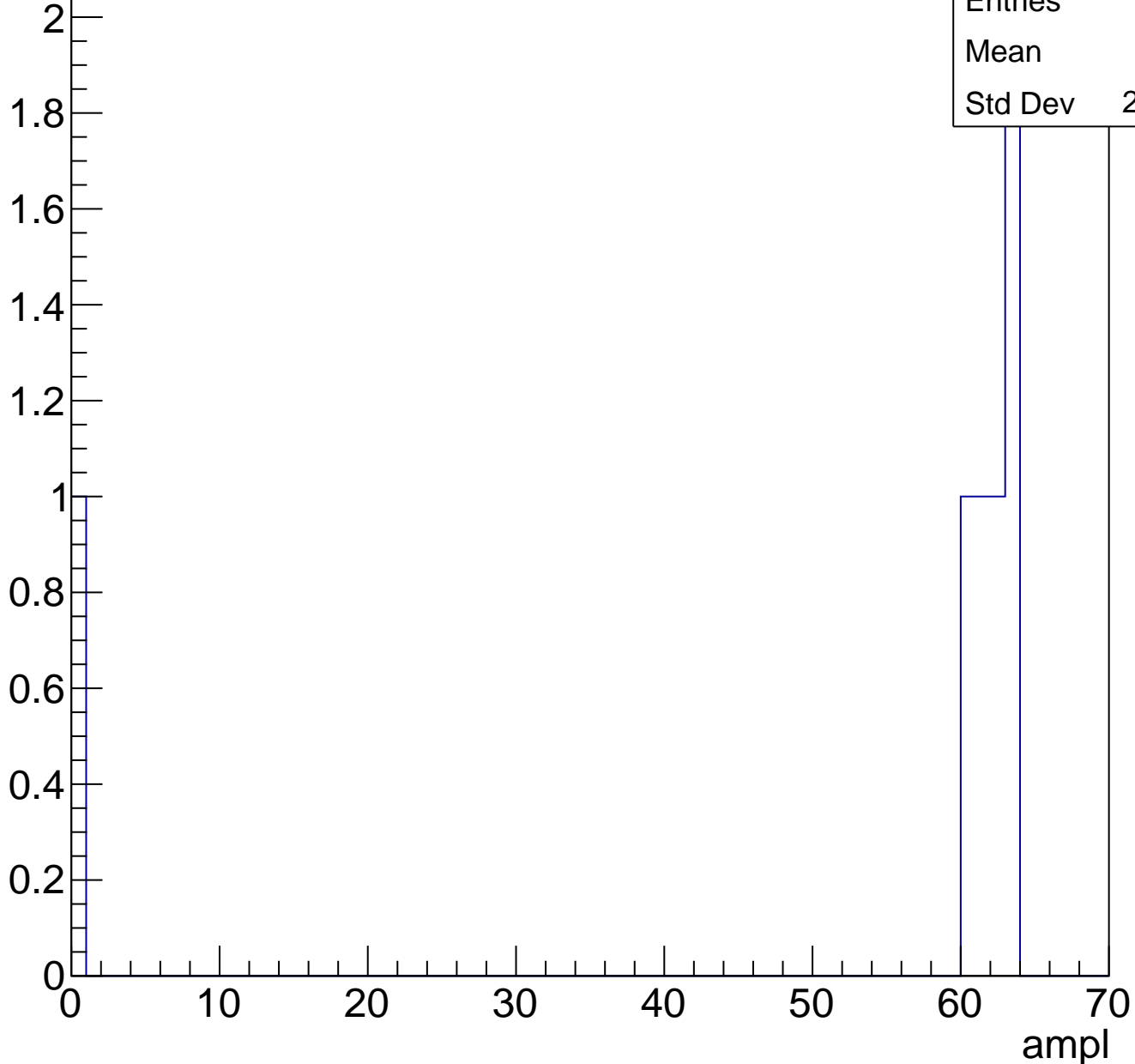
Entries	41
Mean	60.34
Std Dev	2.044



B1L103S, U13-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U13-ch79, adc0

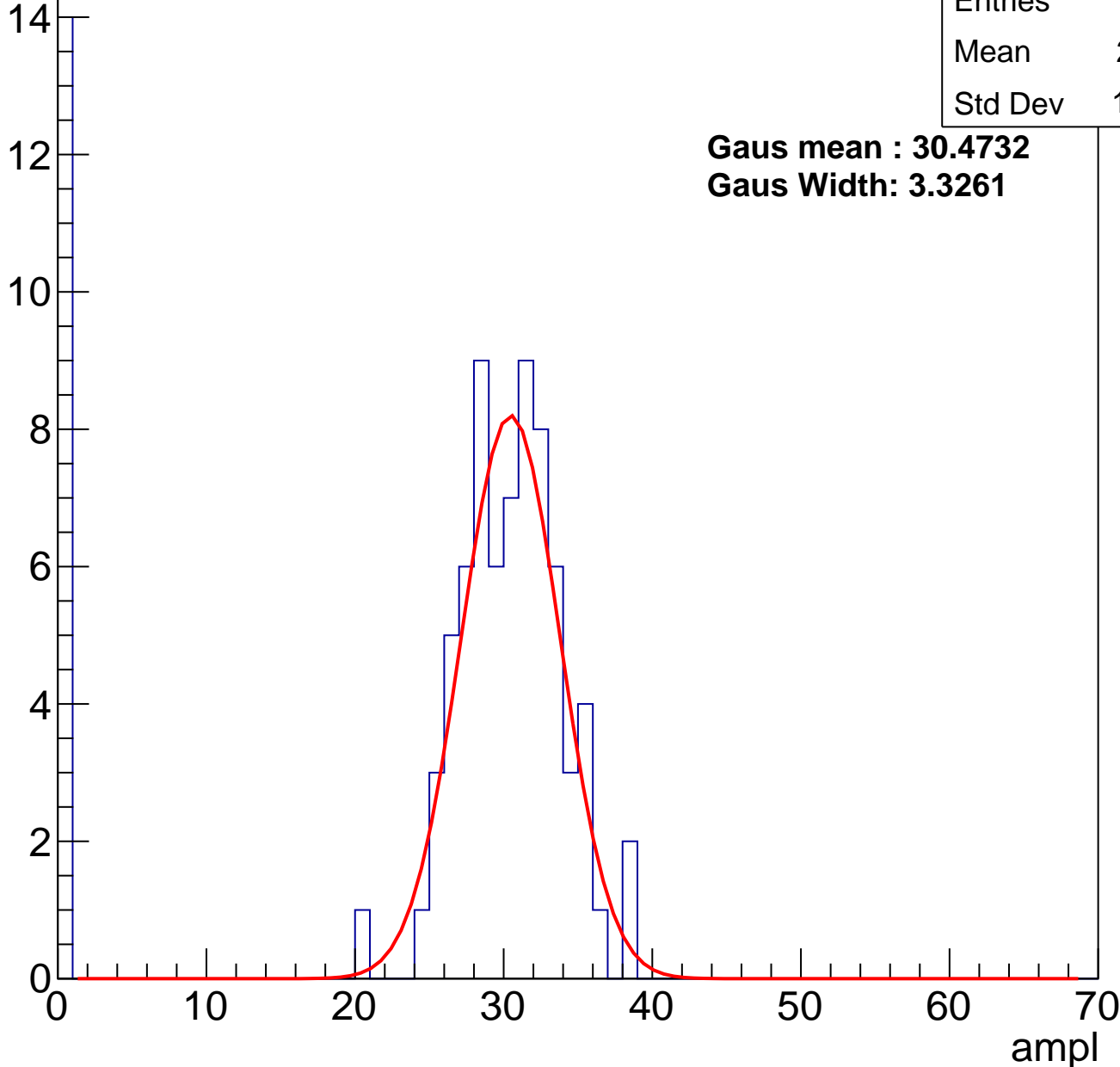
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	25.11
Std Dev	11.56

Gaus mean : 30.4732

Gaus Width: 3.3261

Entry



B1L103S, U13-ch79, adc1

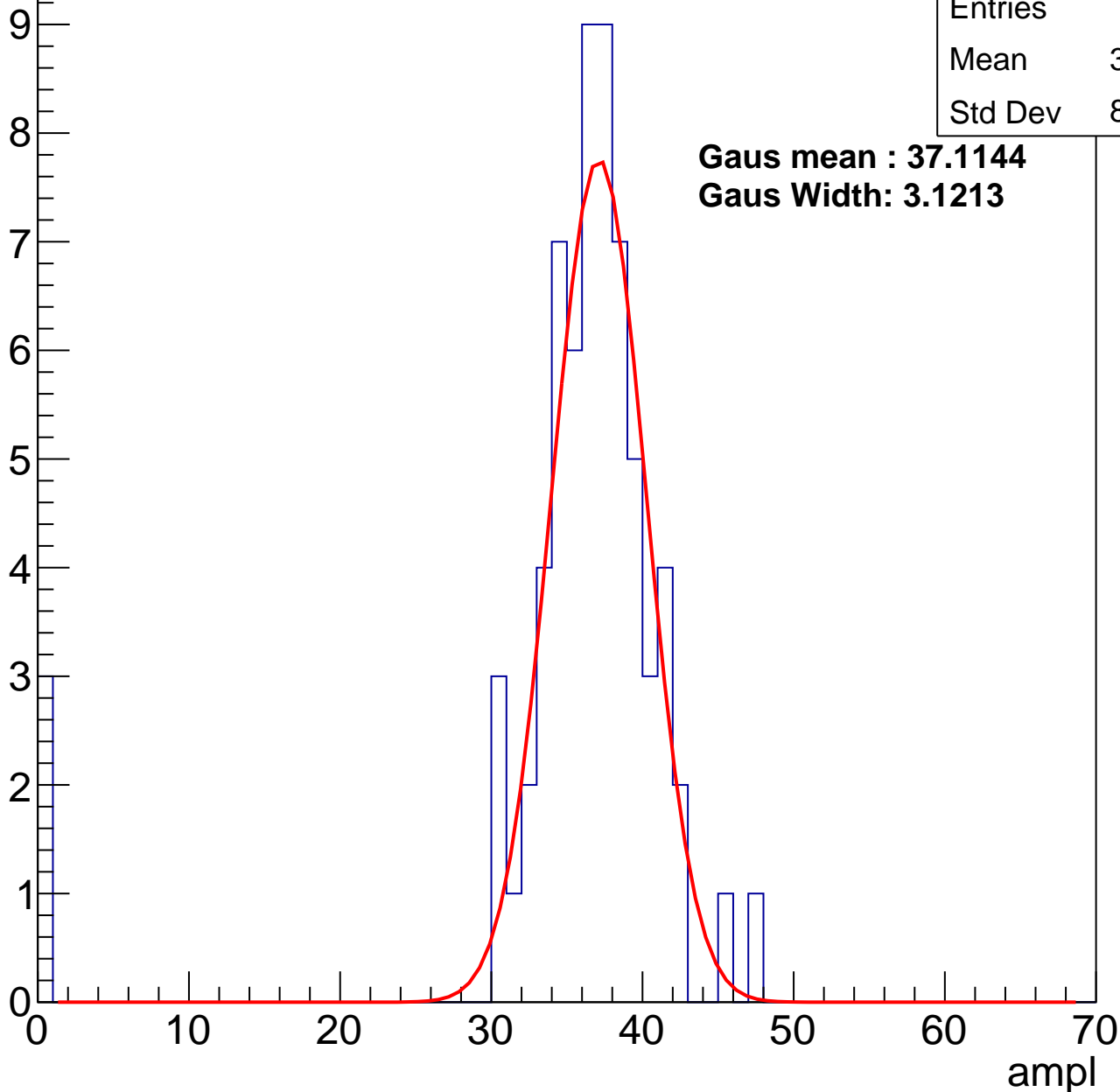
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	34.97
Std Dev	8.246

Gaus mean : 37.1144

Gaus Width: 3.1213



B1L103S, U13-ch79, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	37.97
Std Dev	14.87

Gaus mean : 43.5645

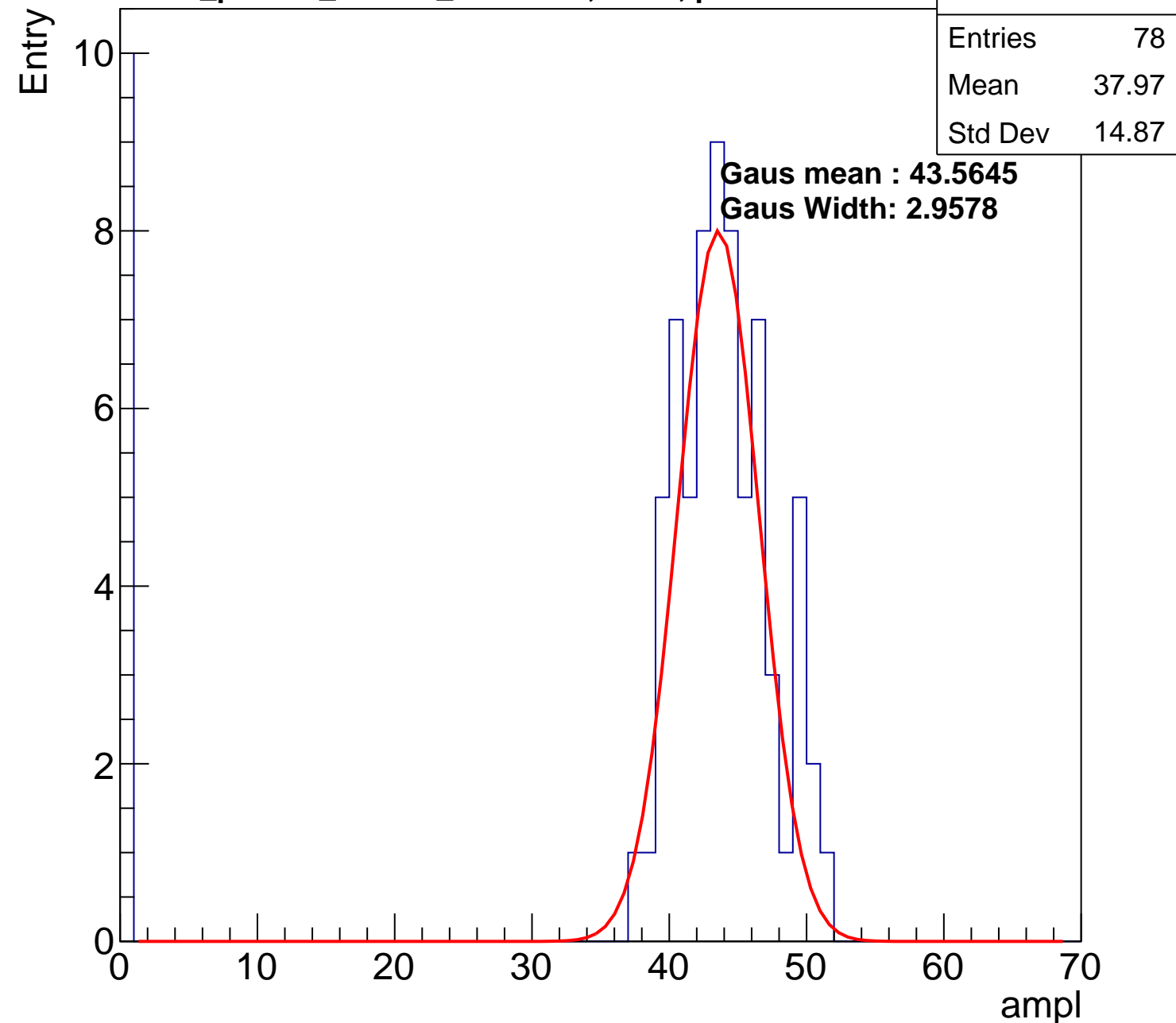
Gaus Width: 2.9578

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

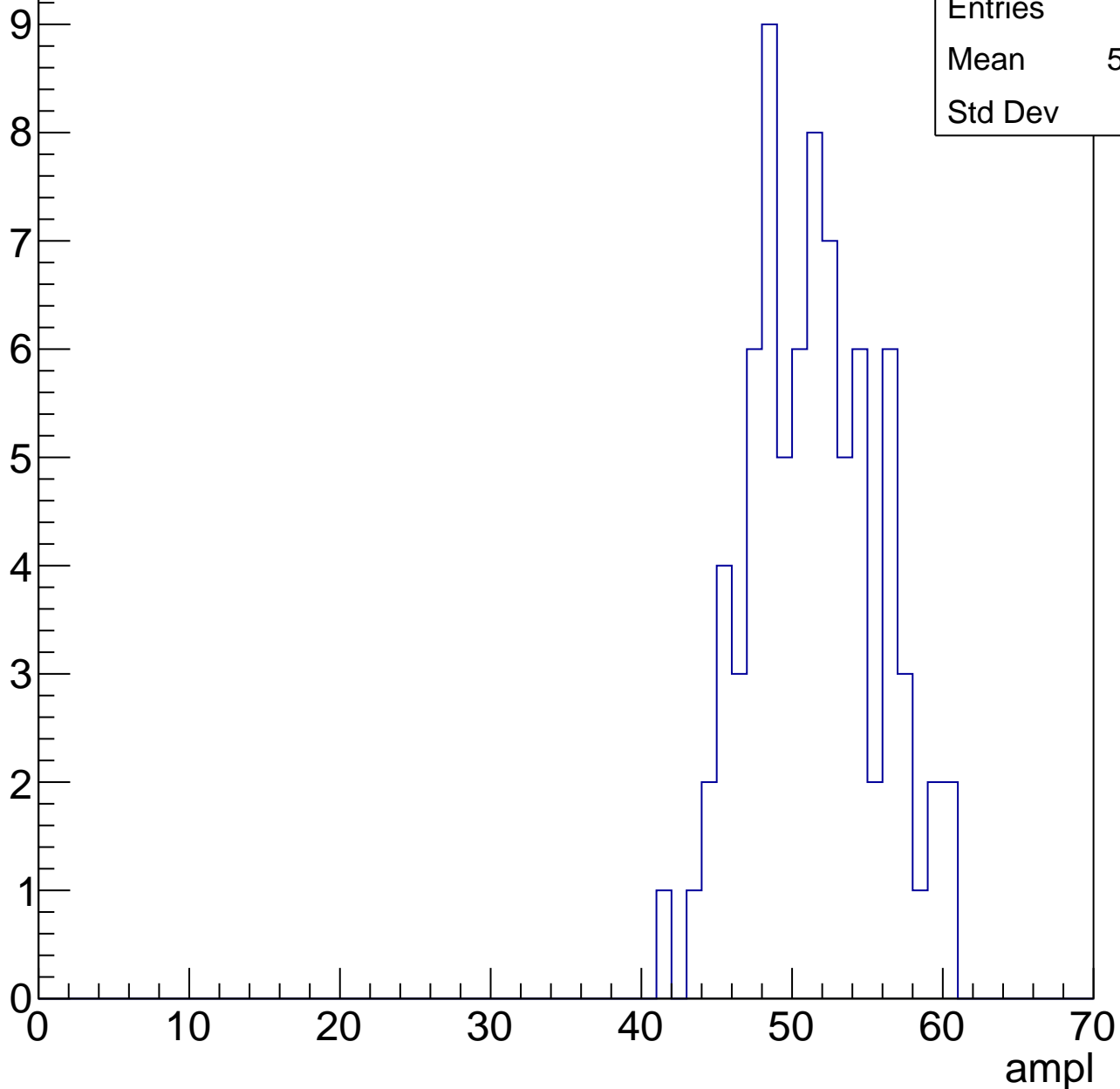


B1L103S, U13-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

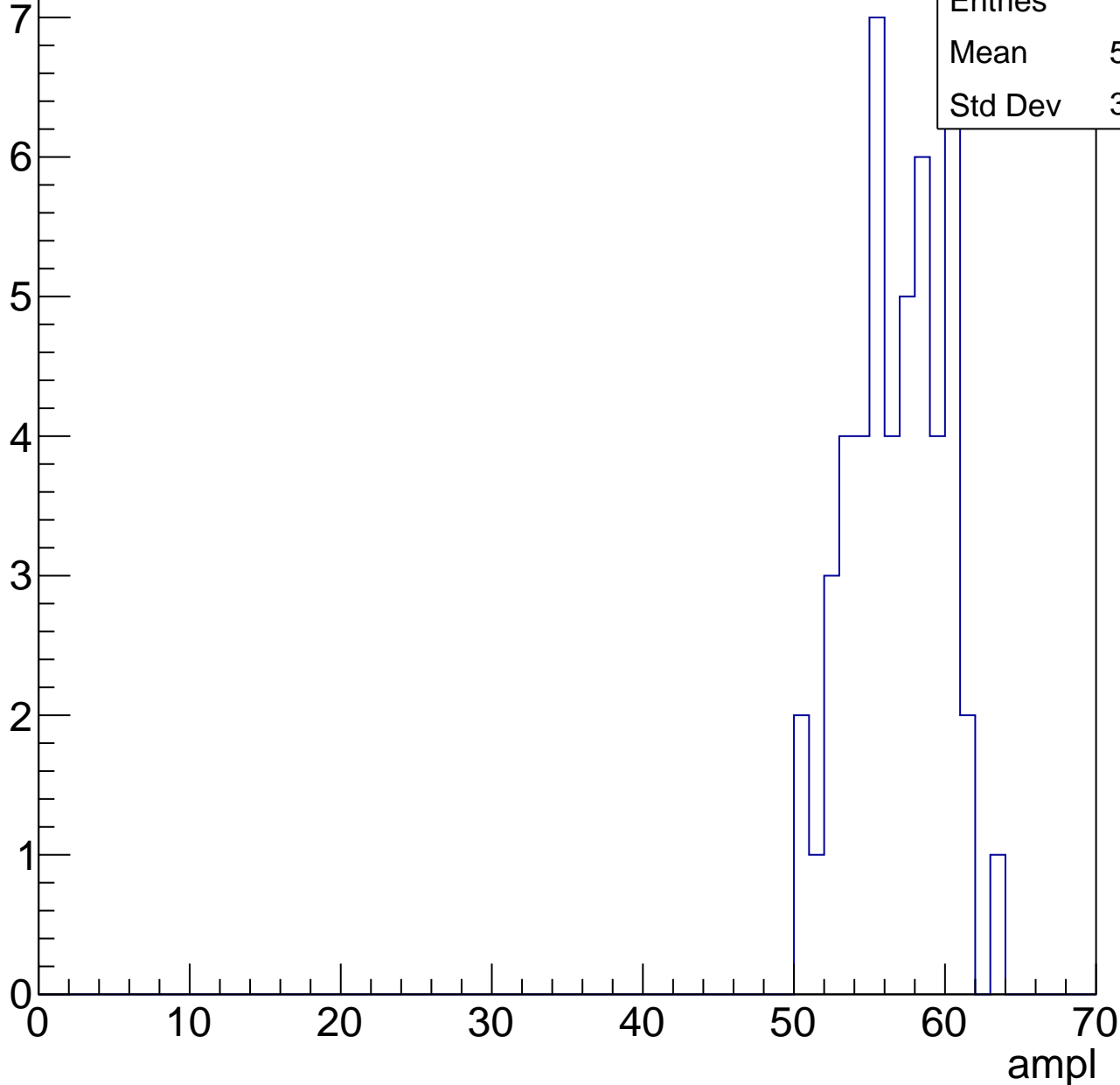
Entries	79
Mean	50.92
Std Dev	4.2



B1L103S, U13-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

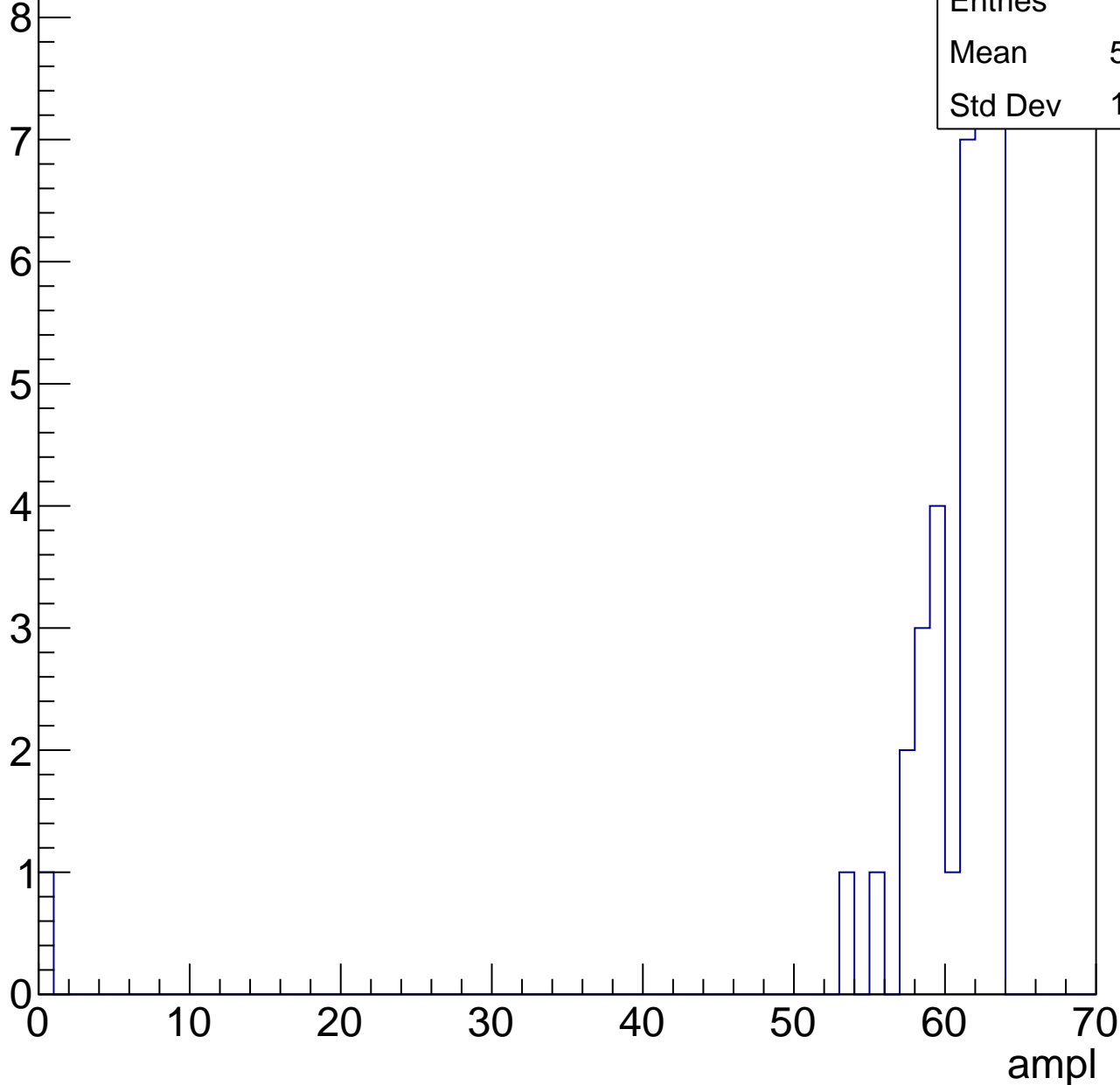


B1L103S, U13-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.86
Std Dev	10.24



B1L103S, U13-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

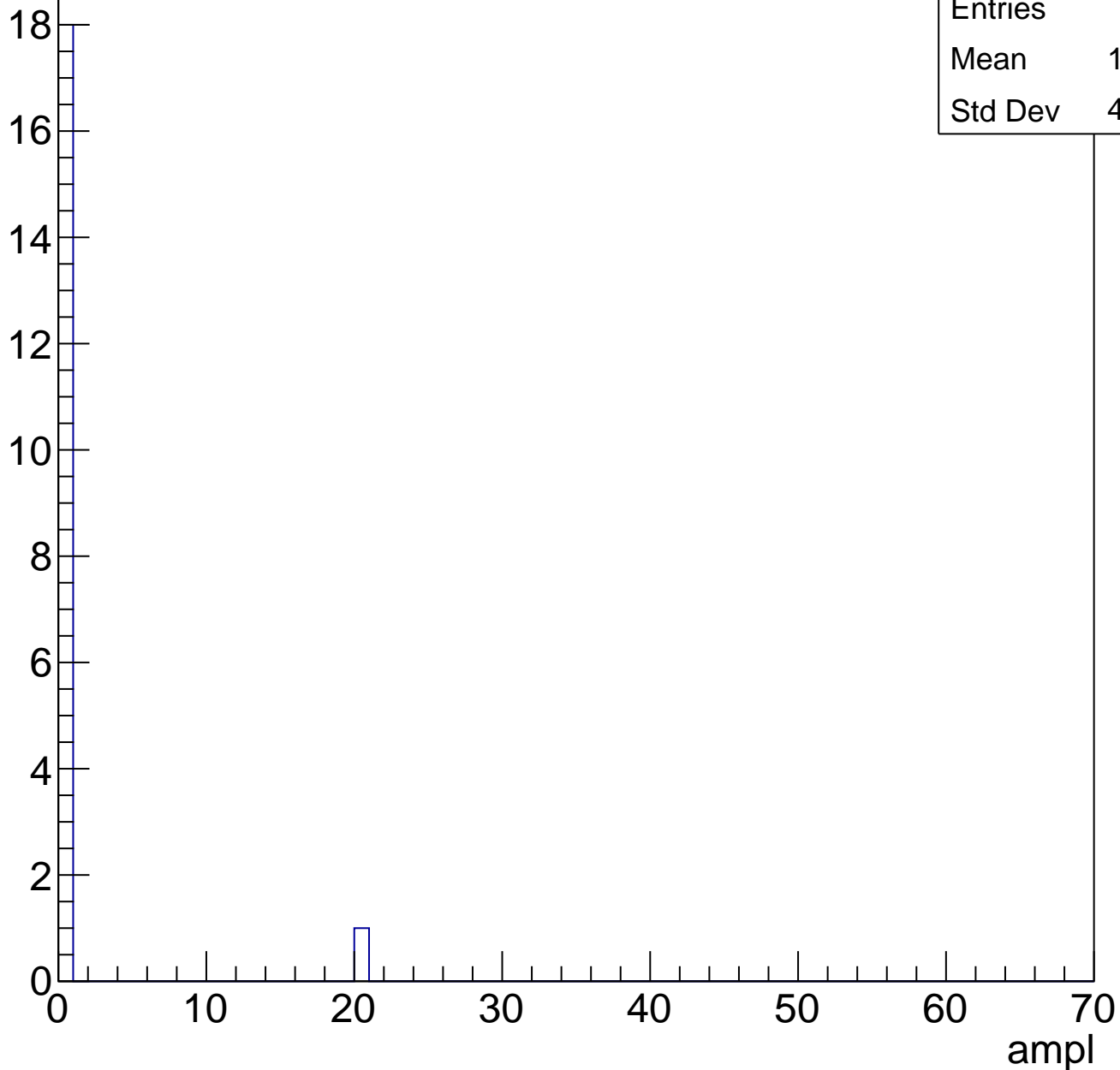


B1L103S, U13-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U13-ch80, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	27.46
Std Dev	8.77

Gaus mean : 29.3235

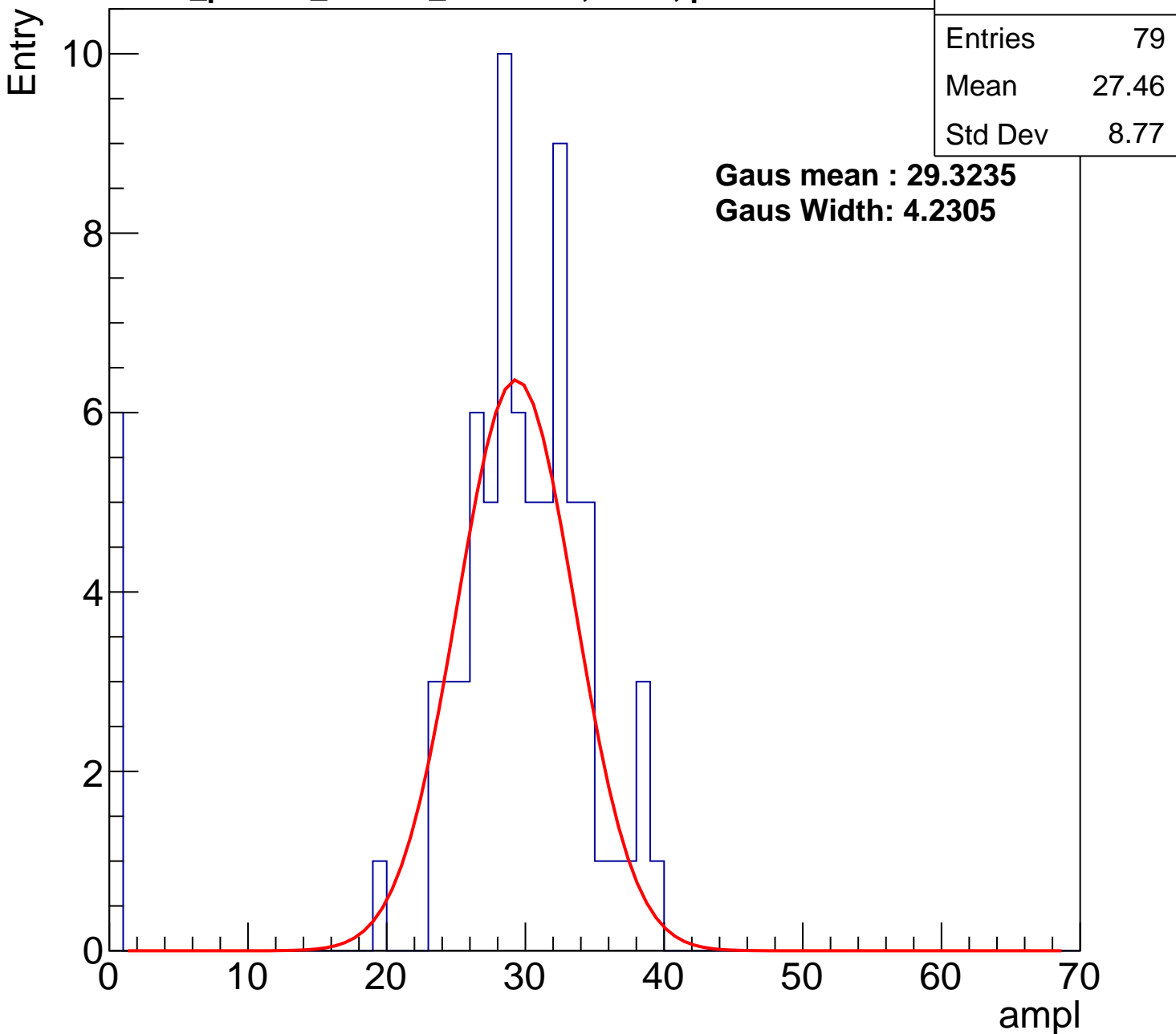
Gaus Width: 4.2305

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch80, adc1

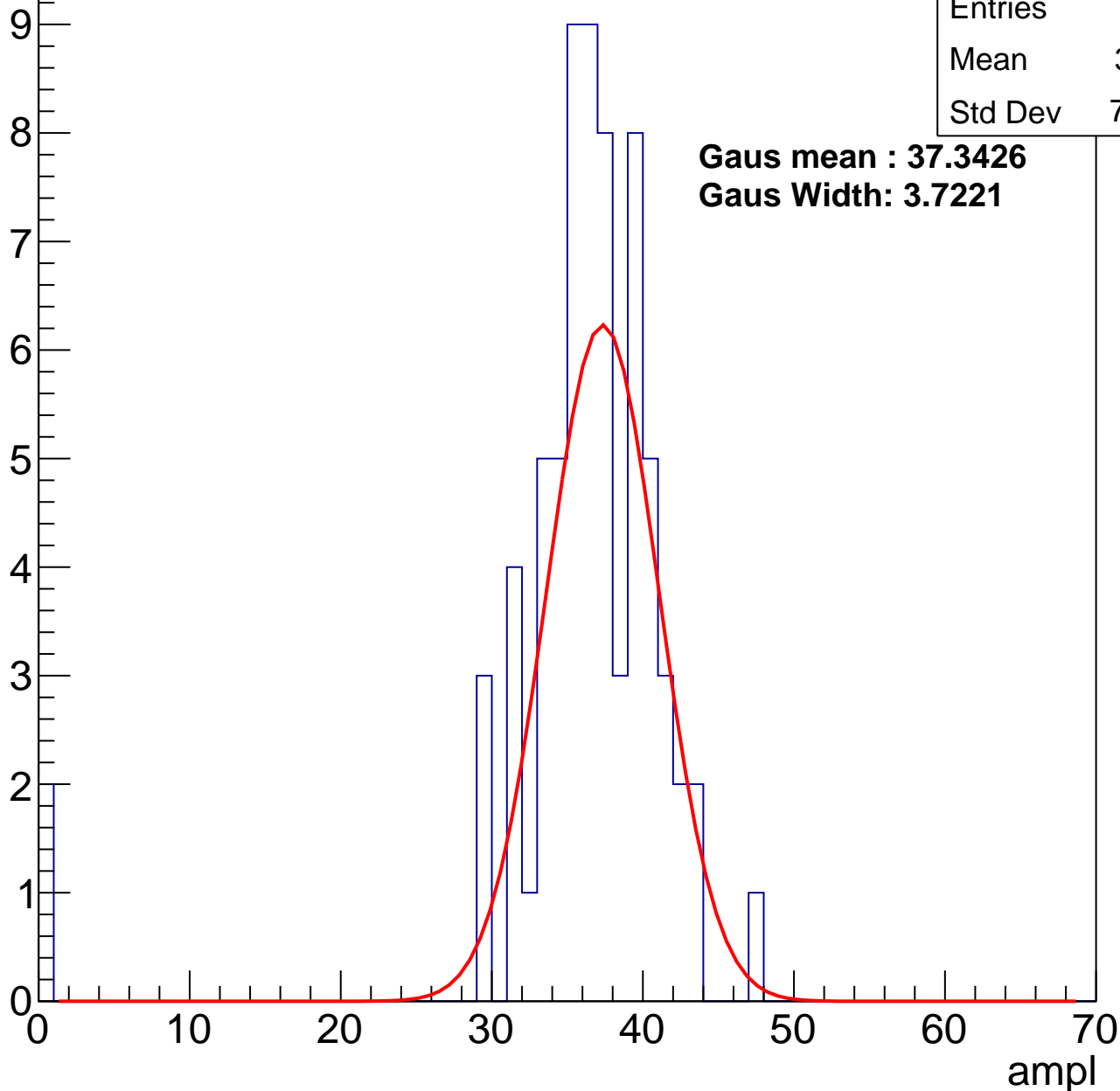
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	35.41
Std Dev	7.013

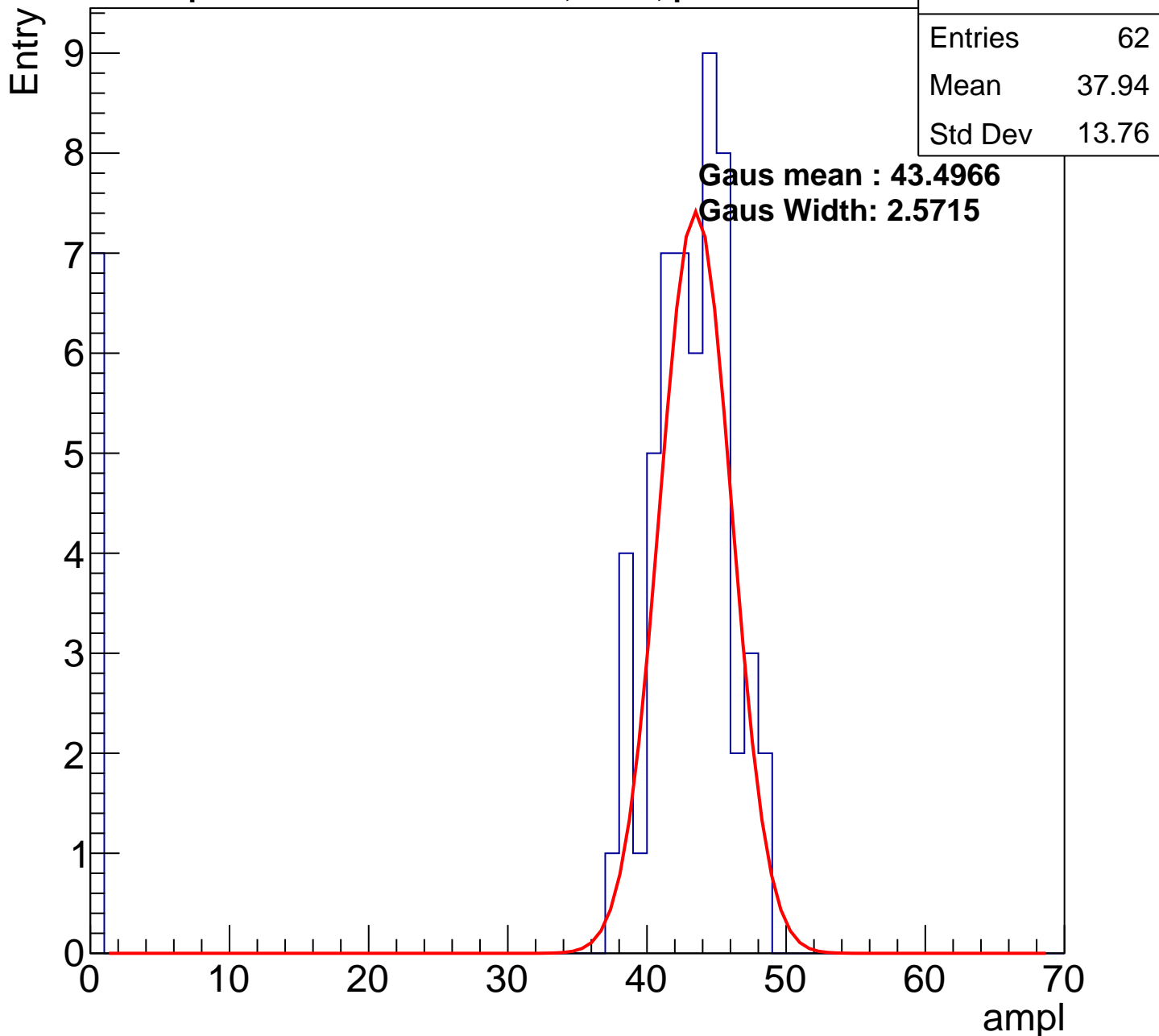
Gaus mean : 37.3426

Gaus Width: 3.7221



B1L103S, U13-ch80, adc2

calib_packv5_041523_1651.root, FC#0, port C2

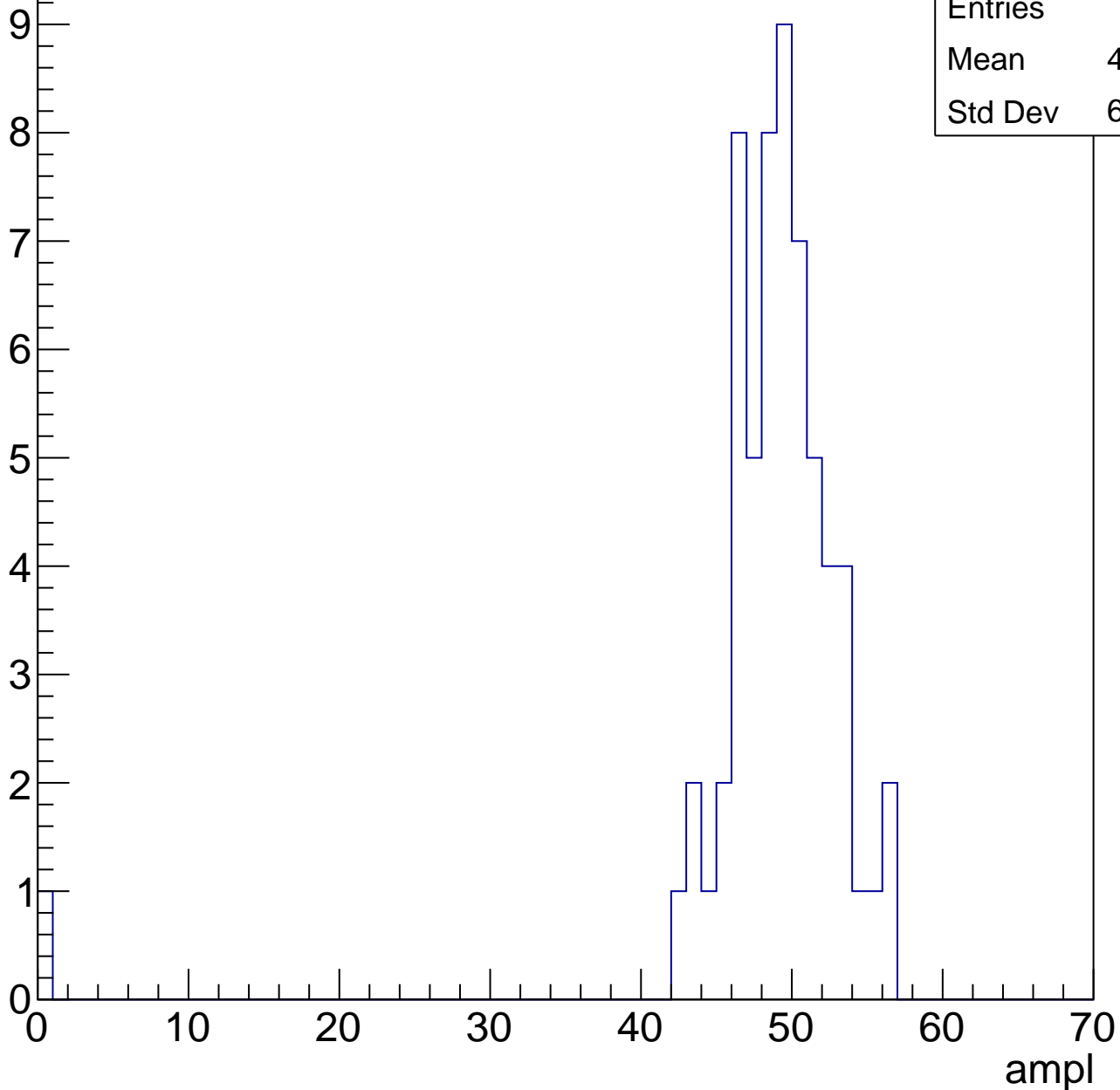


B1L103S, U13-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	48.13
Std Dev	6.917

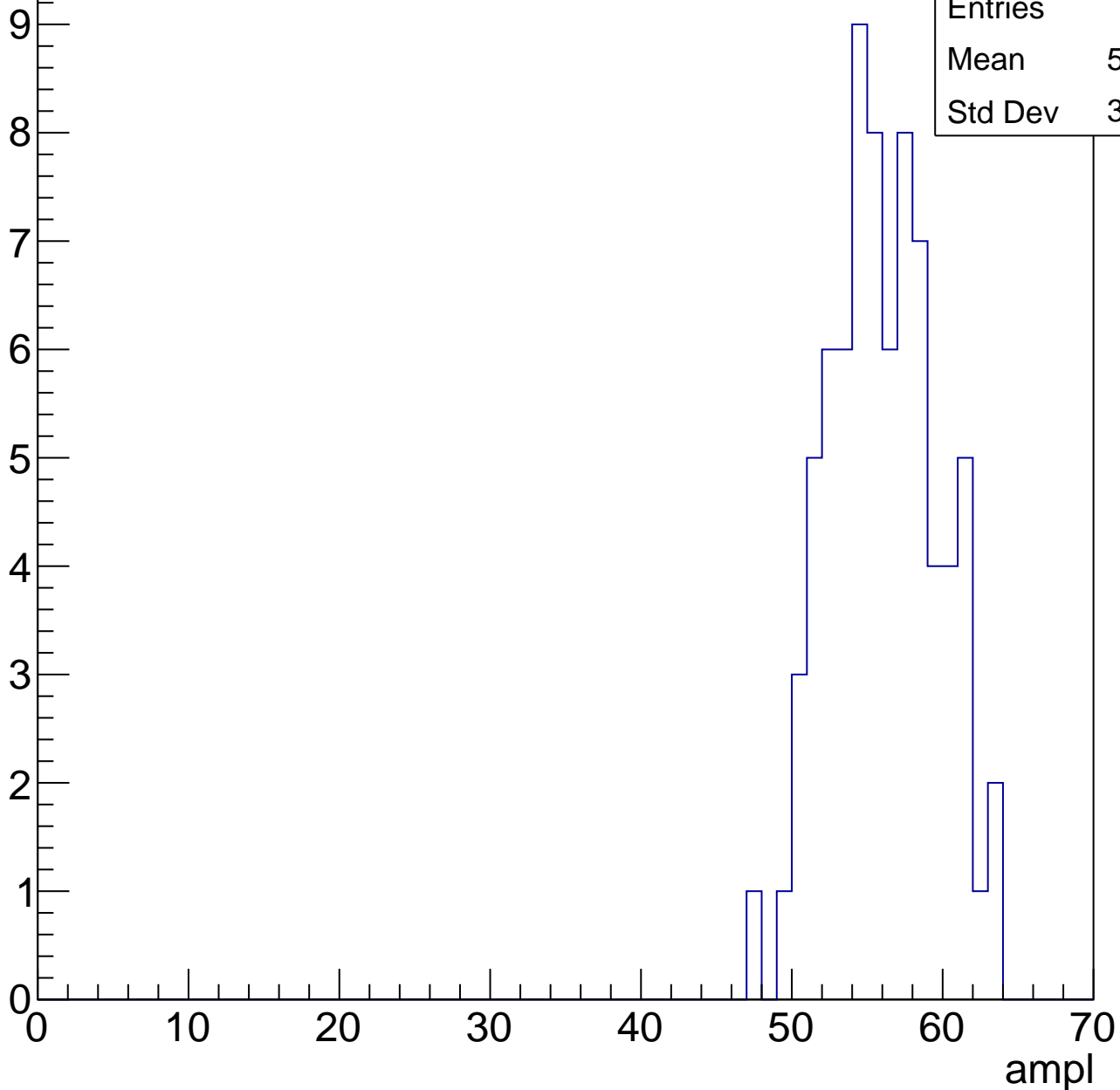


B1L103S, U13-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	55.58
Std Dev	3.514

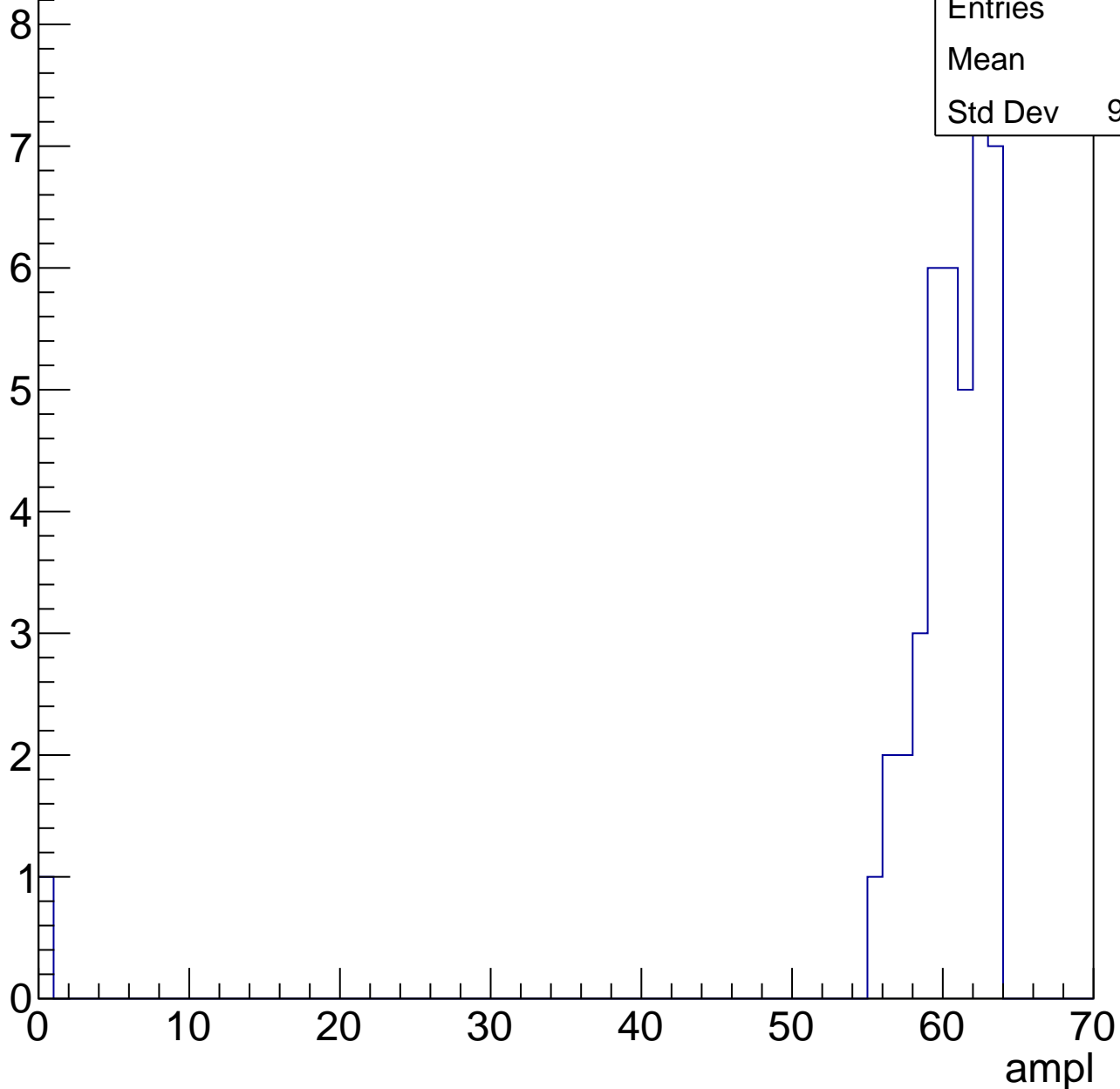


B1L103S, U13-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

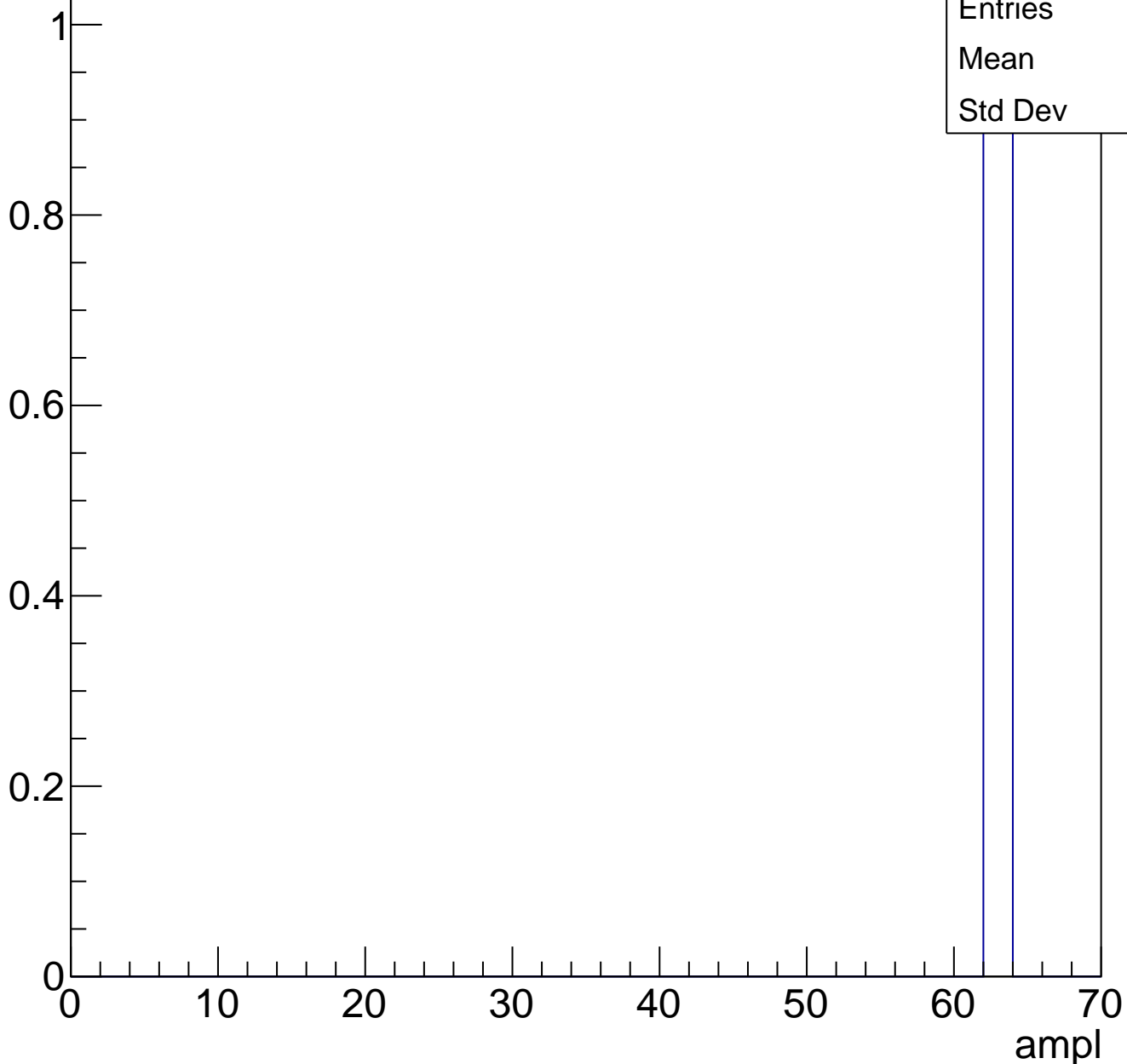
Entries	41
Mean	58.8
Std Dev	9.544



B1L103S, U13-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch81, adc0

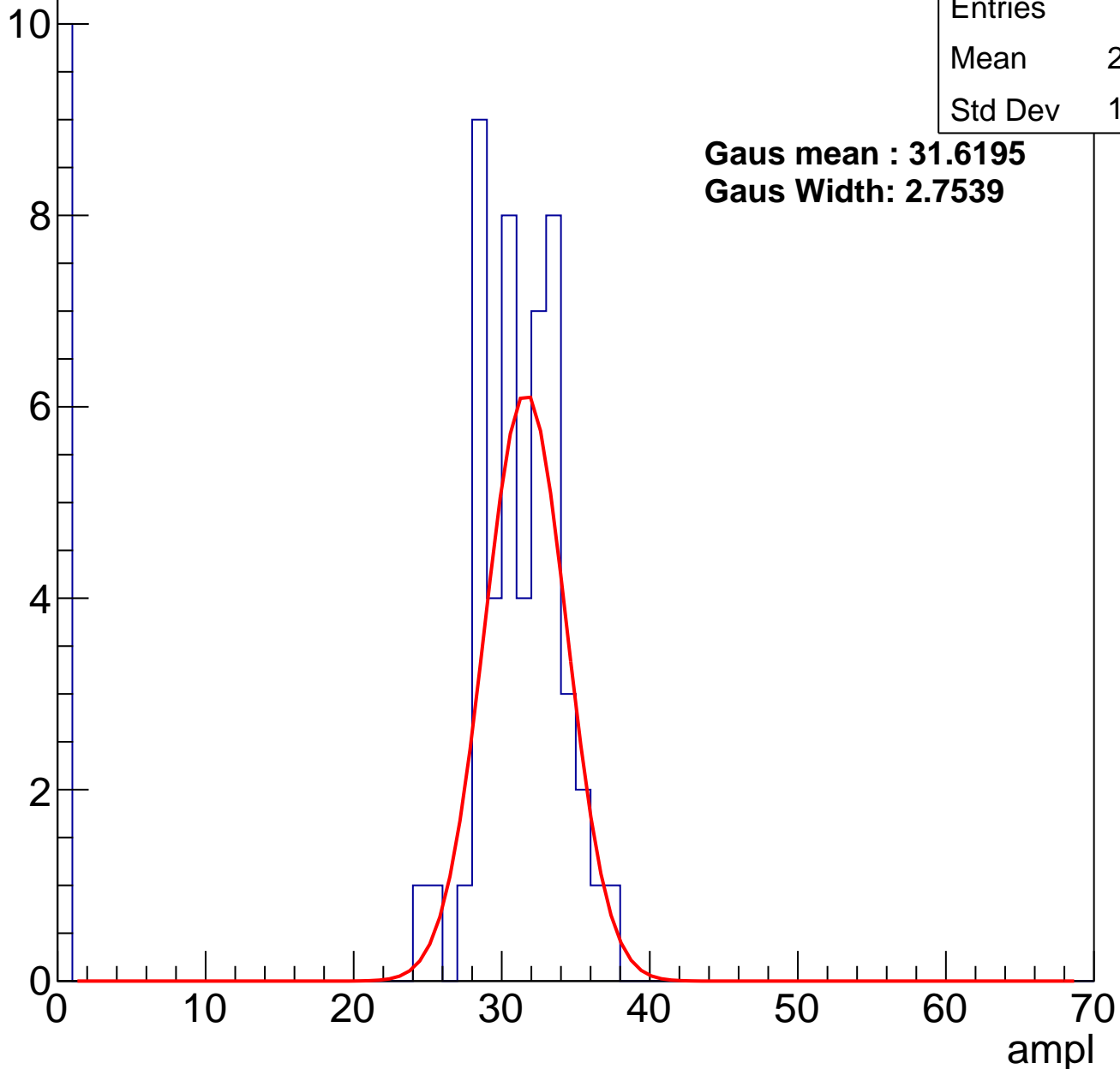
calib_packv5_041523_1651.root, FC#0, port C2

Entries	60
Mean	25.68
Std Dev	11.75

Gaus mean : 31.6195

Gaus Width: 2.7539

Entry



B1L103S, U13-ch81, adc1

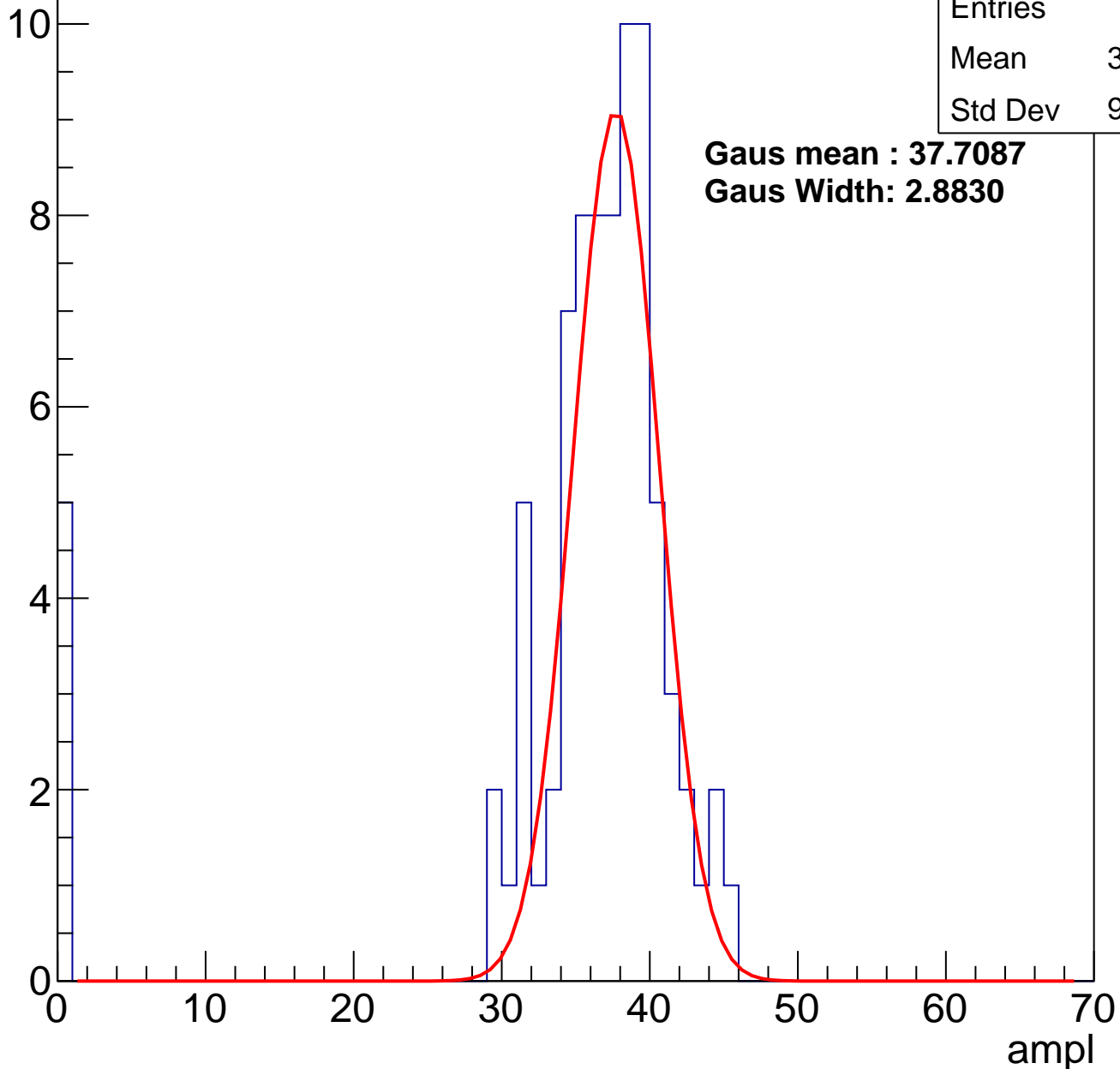
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	34.52
Std Dev	9.464

Gaus mean : 37.7087

Gaus Width: 2.8830

Entry



B1L103S, U13-ch81, adc2

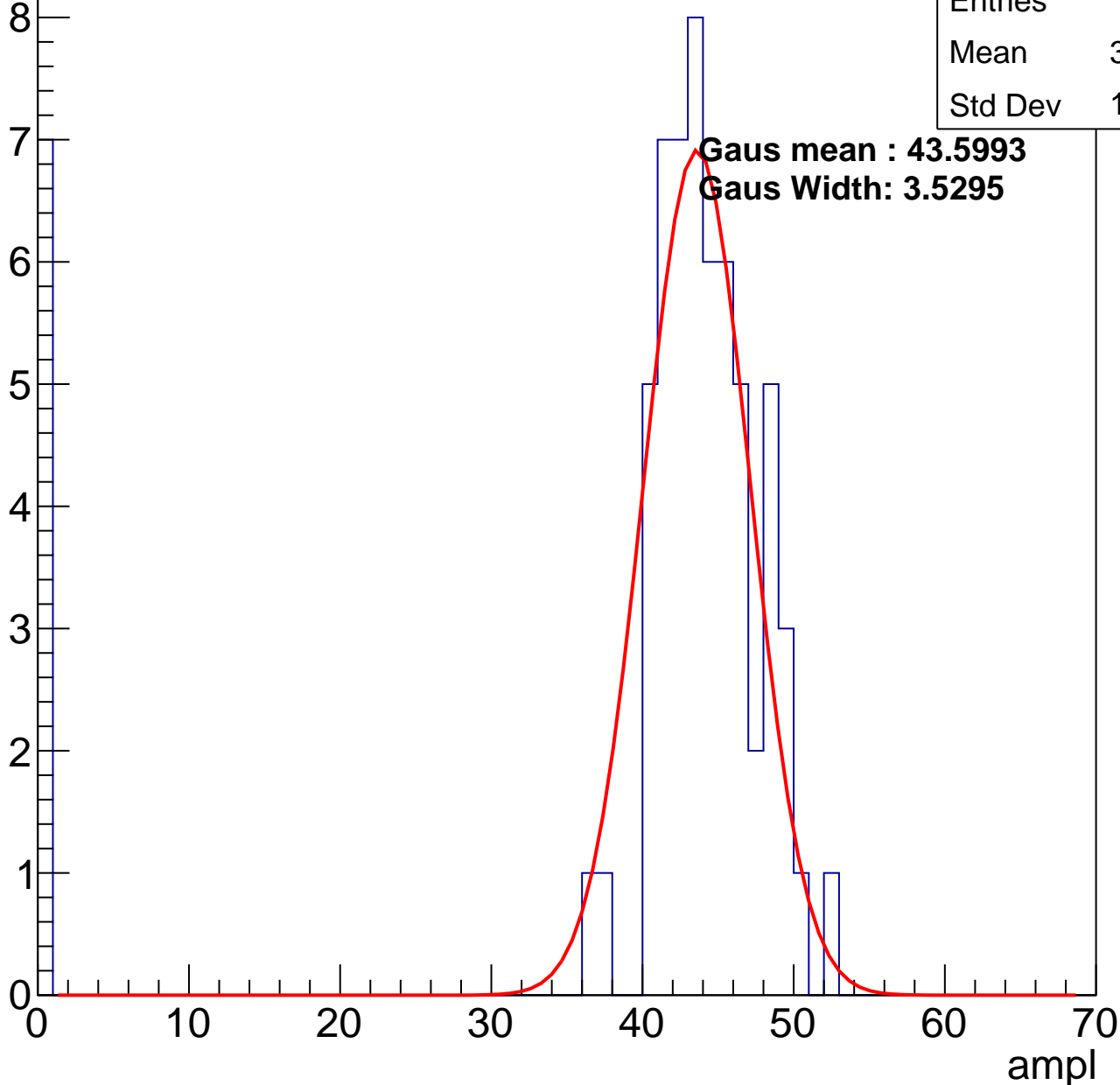
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	39.15
Std Dev	13.93

Gaus mean : 43.5993

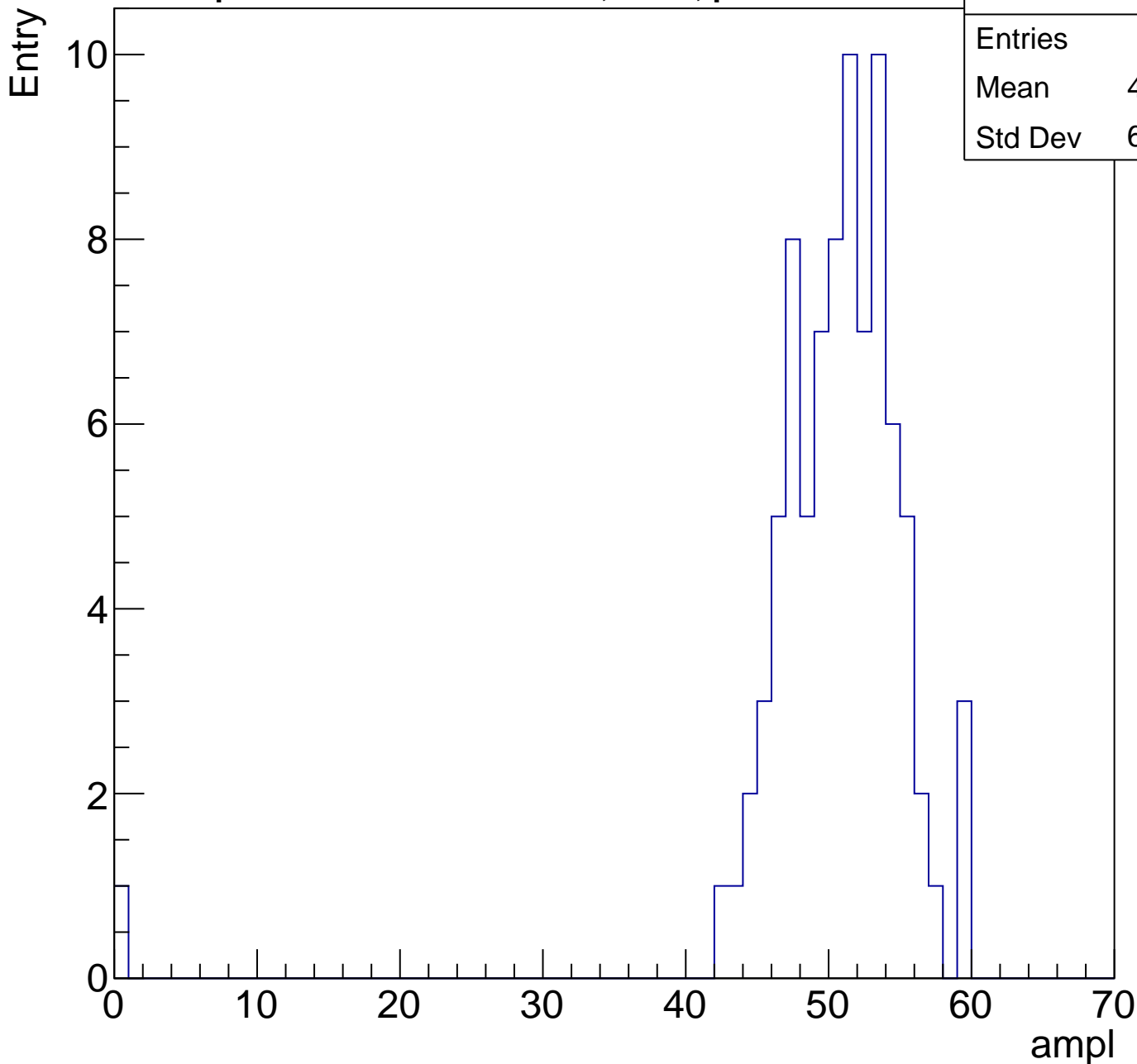
Gaus Width: 3.5295



B1L103S, U13-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	49.95
Std Dev	6.555

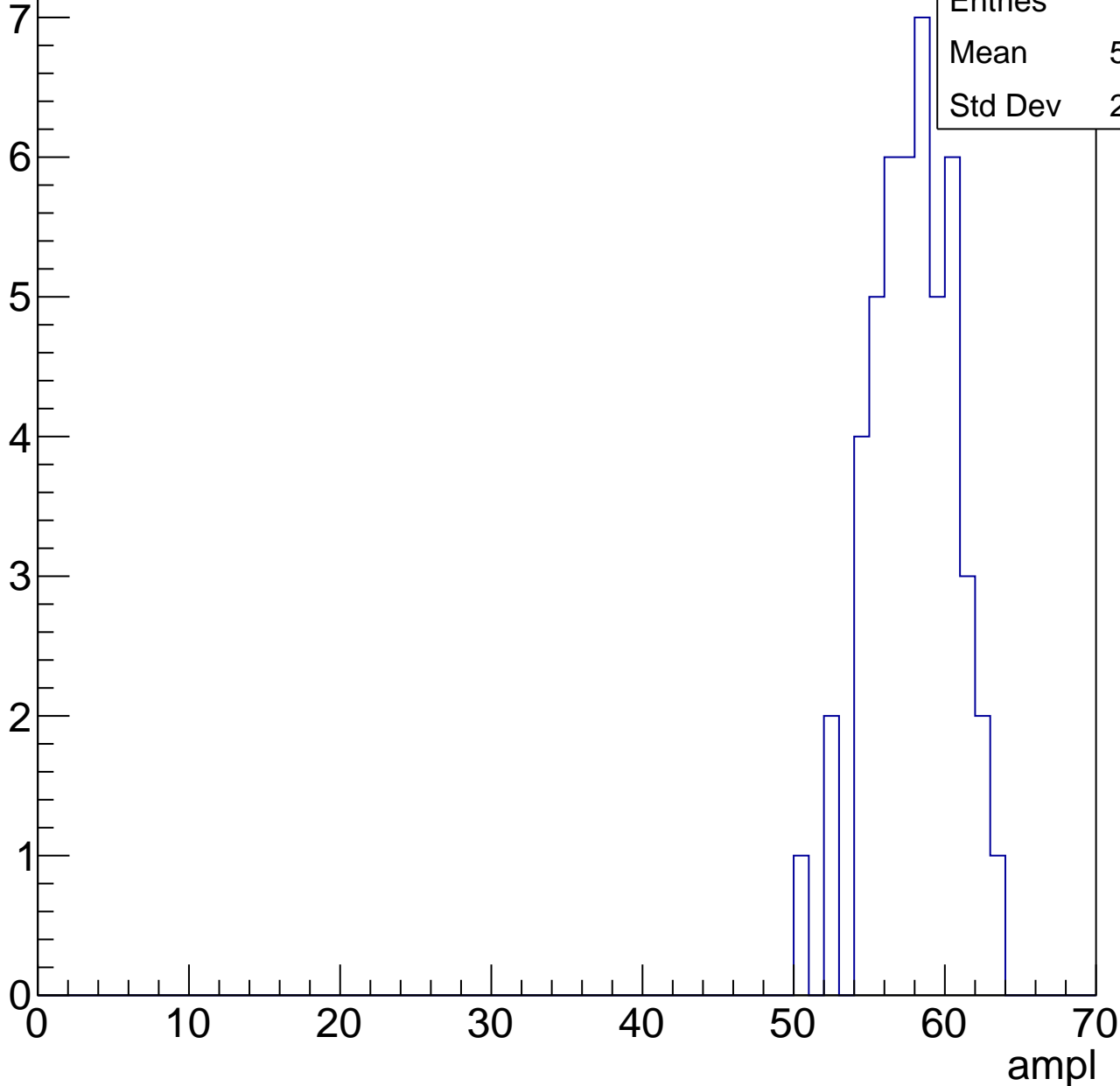


B1L103S, U13-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	57.38
Std Dev	2.774

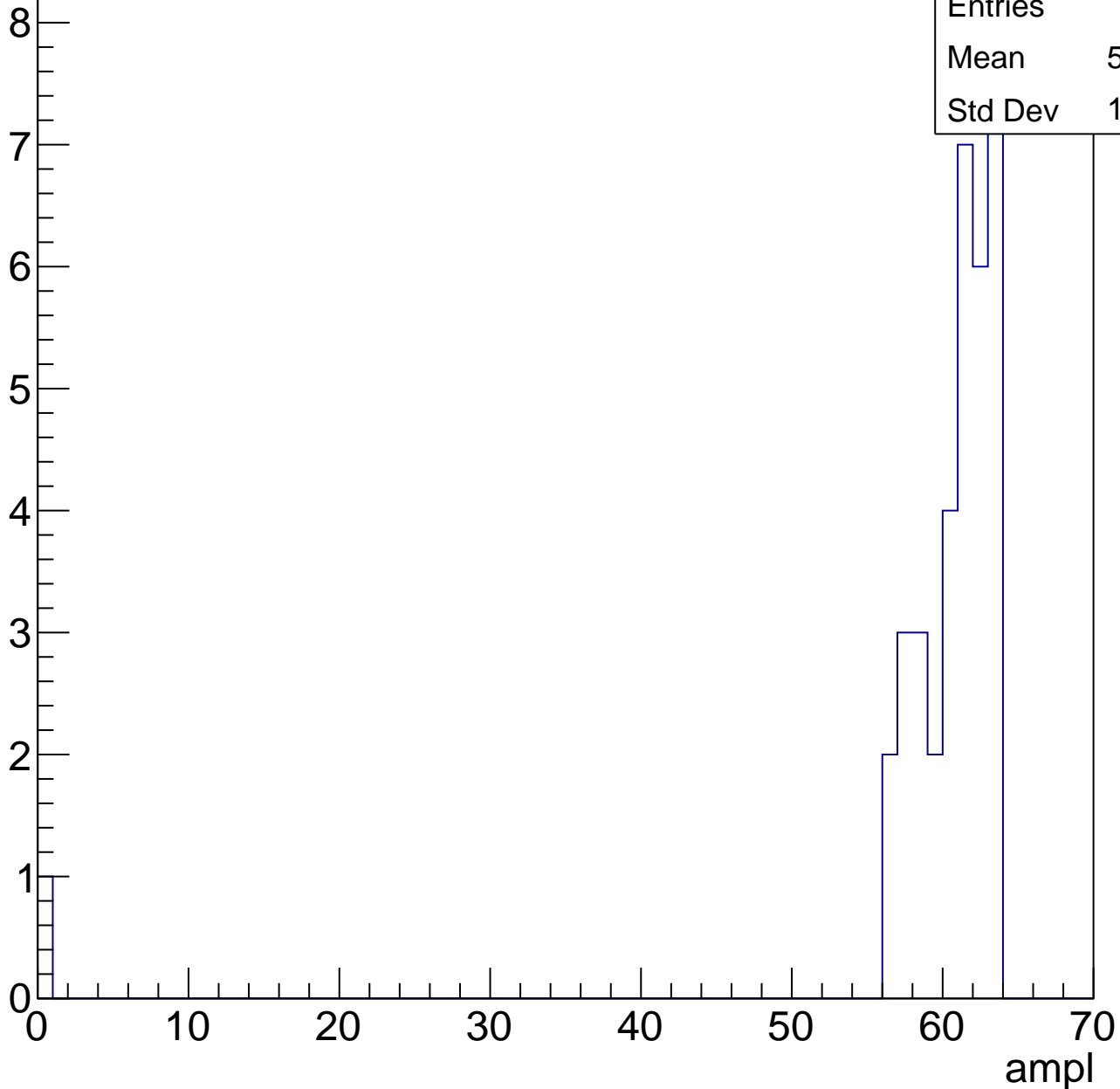


B1L103S, U13-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.83
Std Dev	10.17



B1L103S, U13-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

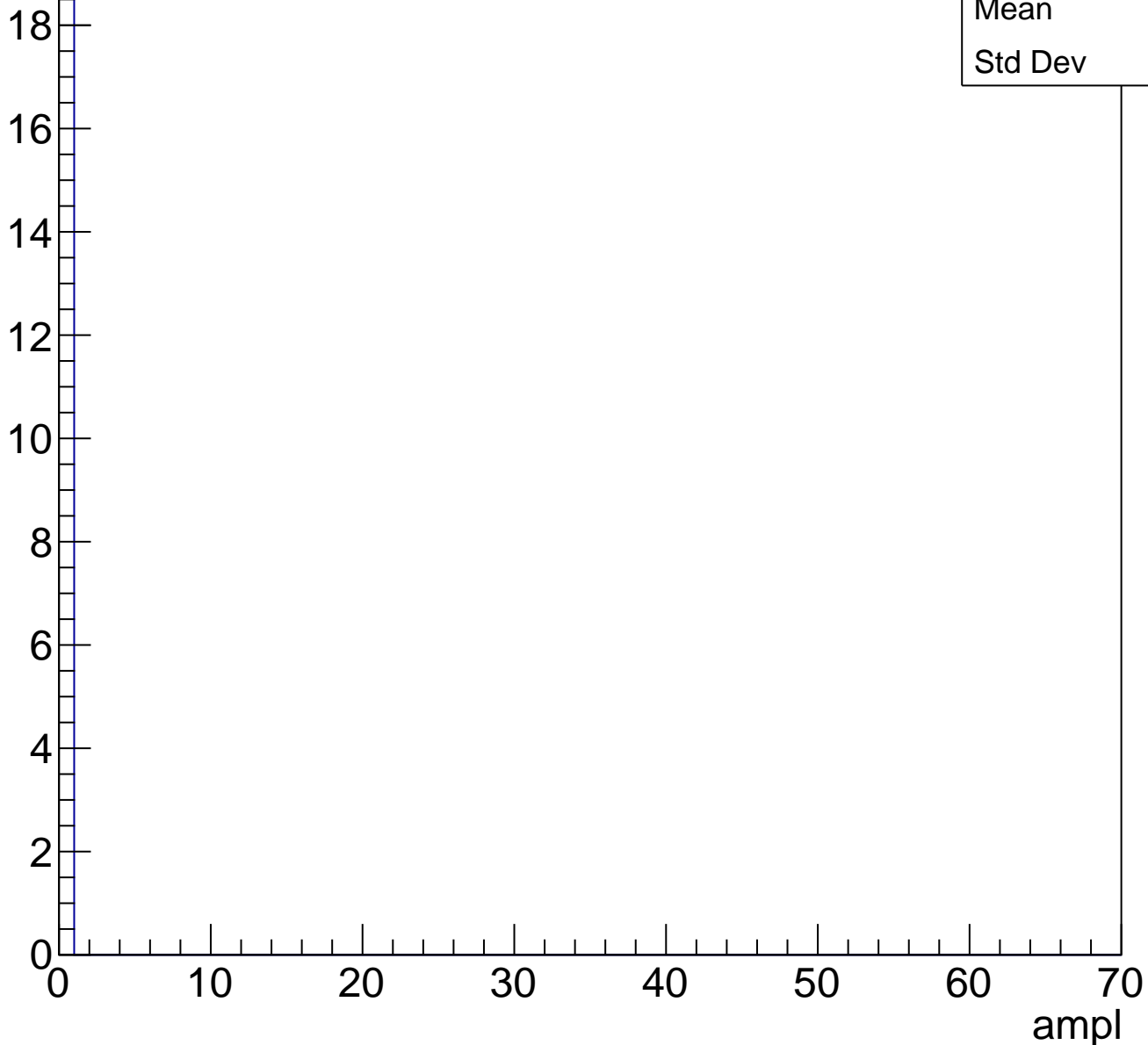


B1L103S, U13-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



B1L103S, U13-ch82, adc0

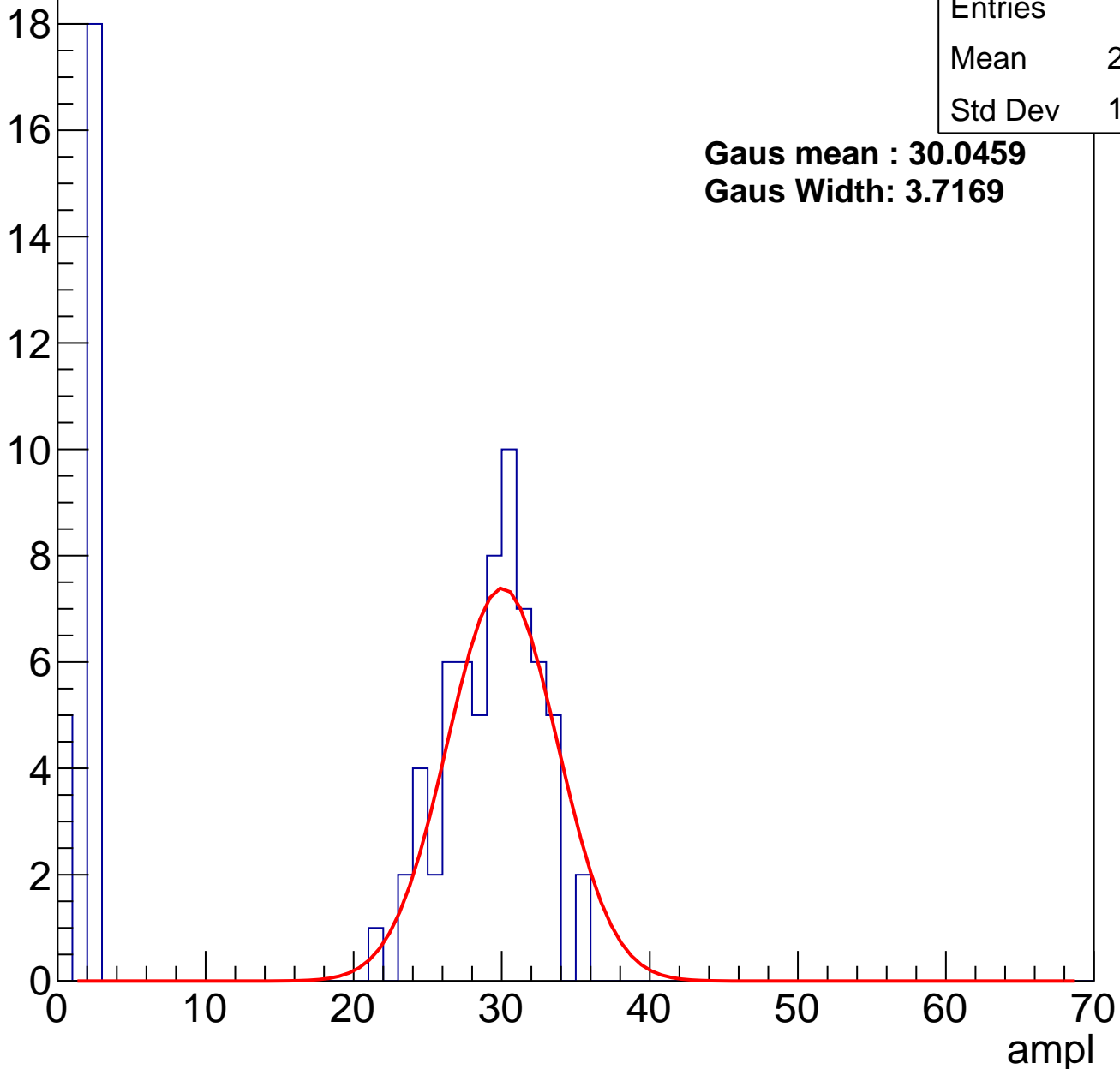
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	21.64
Std Dev	12.32

Gaus mean : 30.0459

Gaus Width: 3.7169

Entry



B1L103S, U13-ch82, adc1

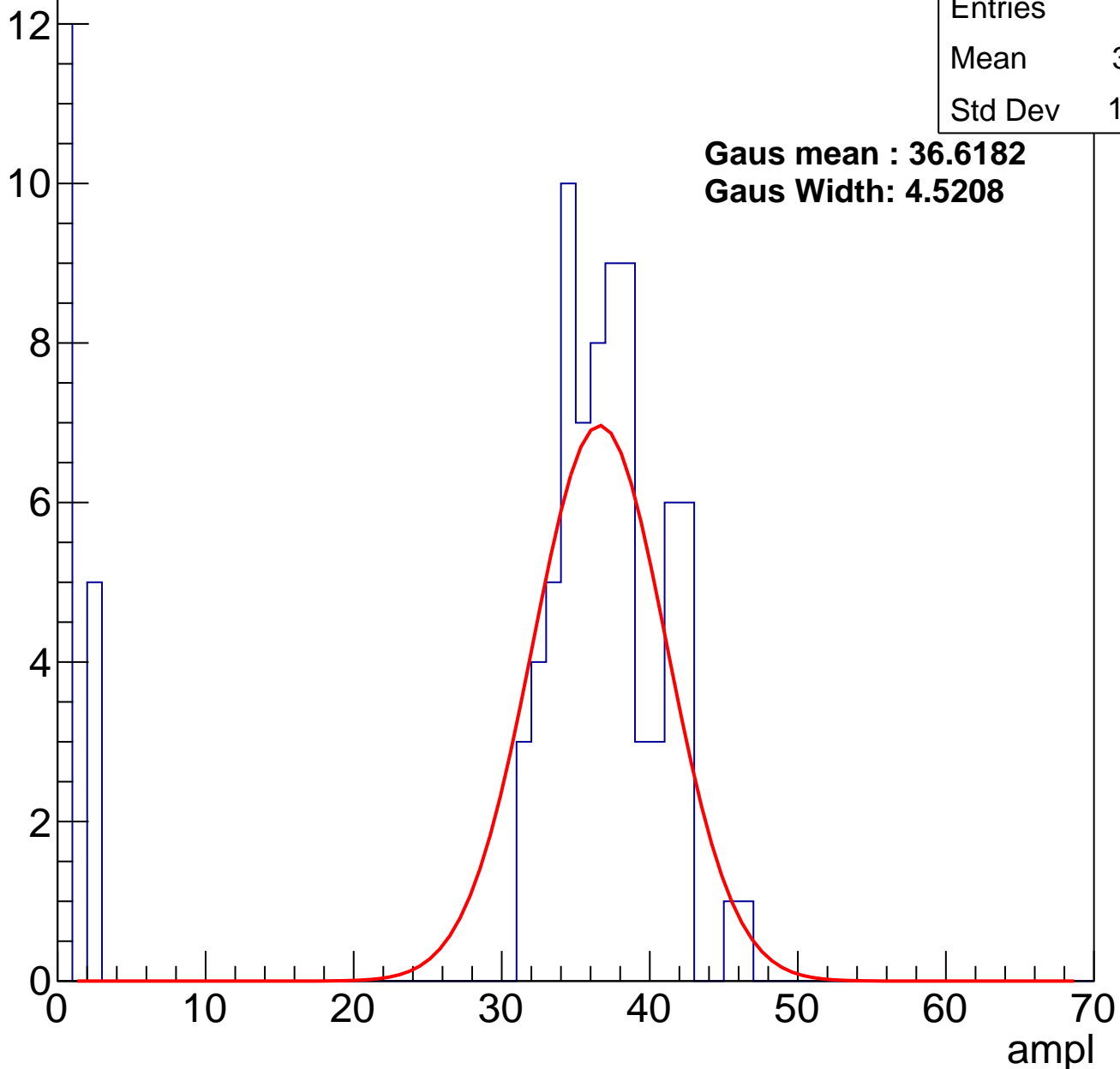
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	30.11
Std Dev	14.38

Gaus mean : 36.6182

Gaus Width: 4.5208

Entry



B1L103S, U13-ch82, adc2

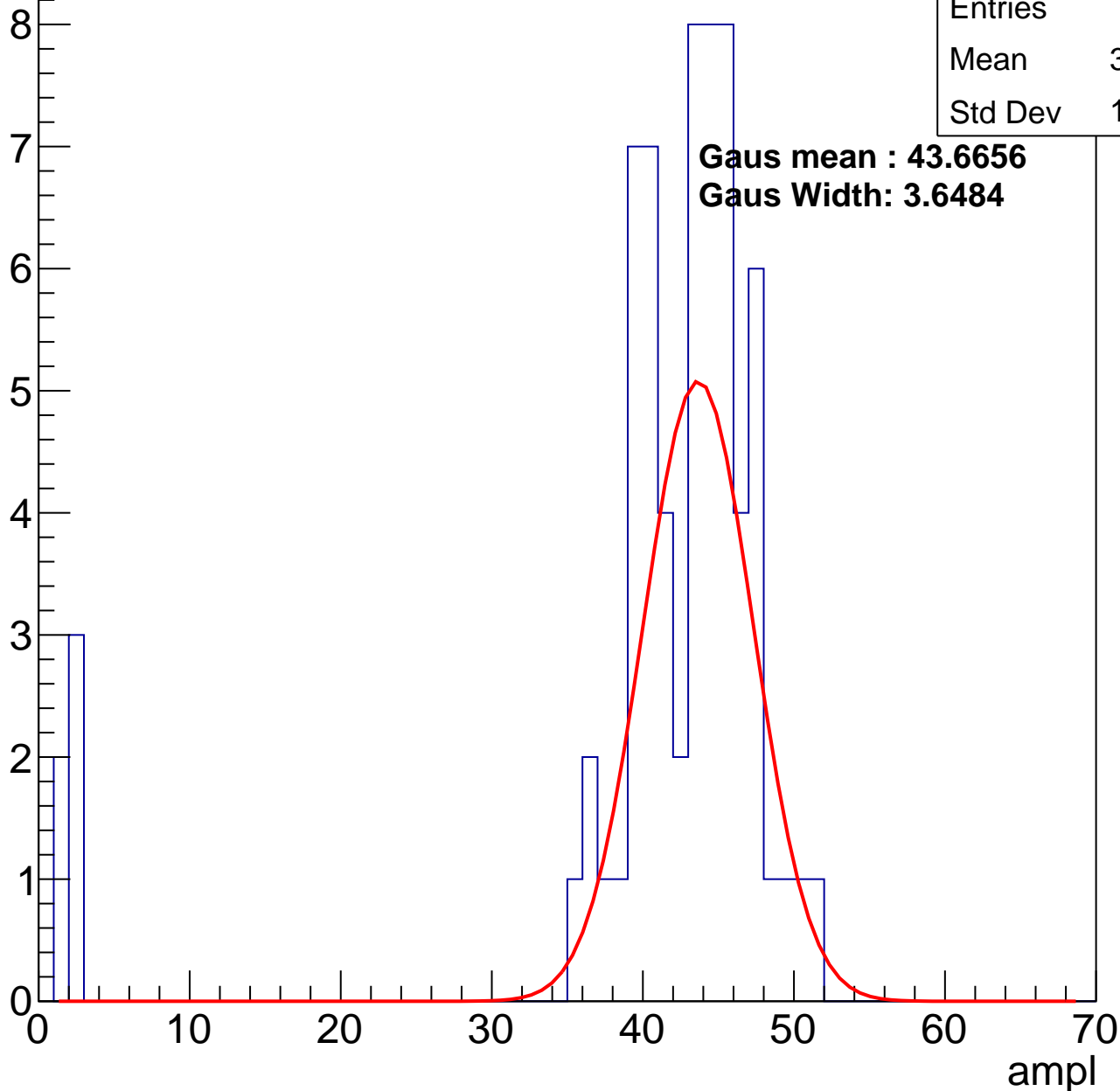
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	39.84
Std Dev	11.39

Gaus mean : 43.6656

Gaus Width: 3.6484

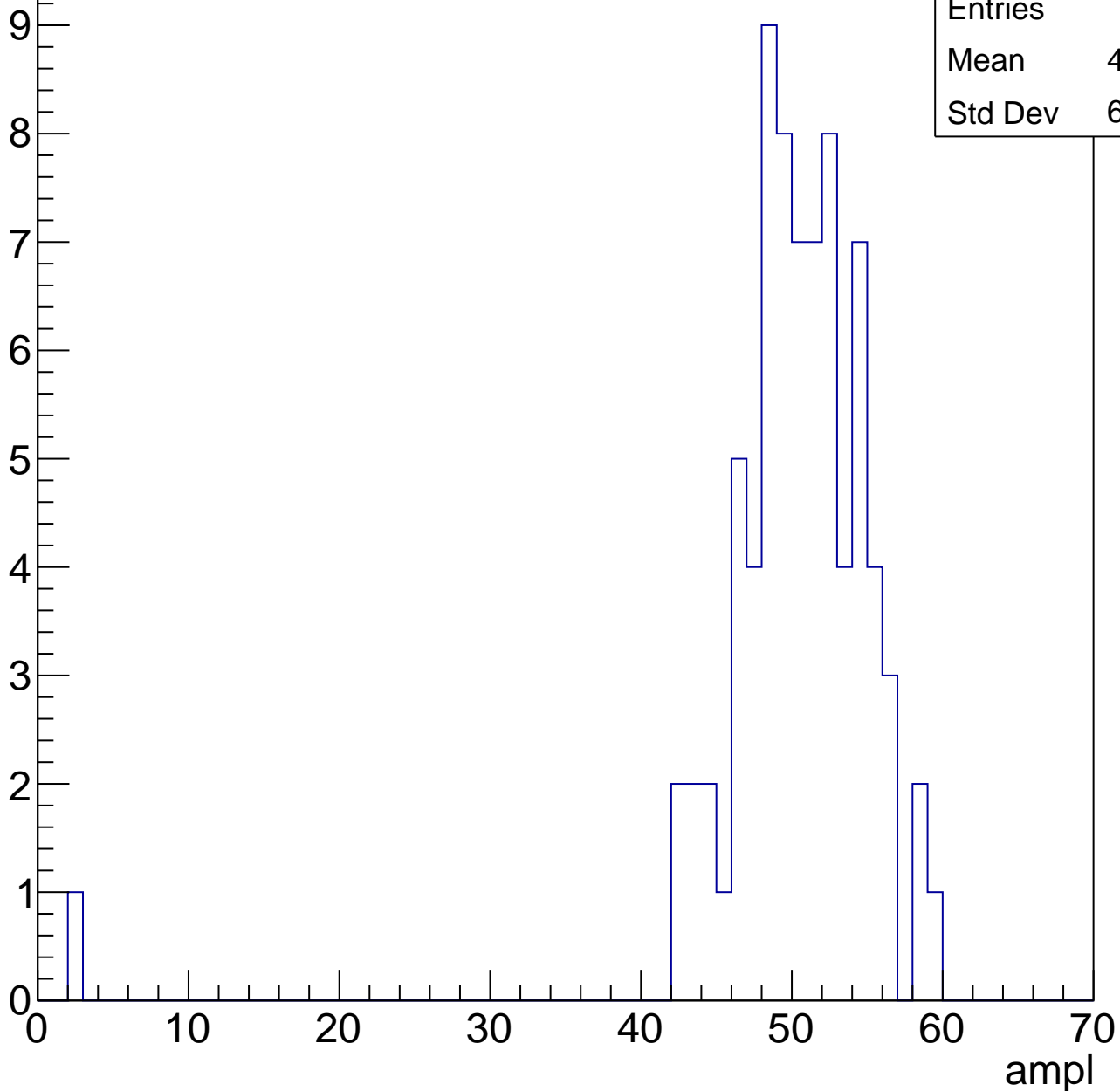


B1L103S, U13-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	49.65
Std Dev	6.627

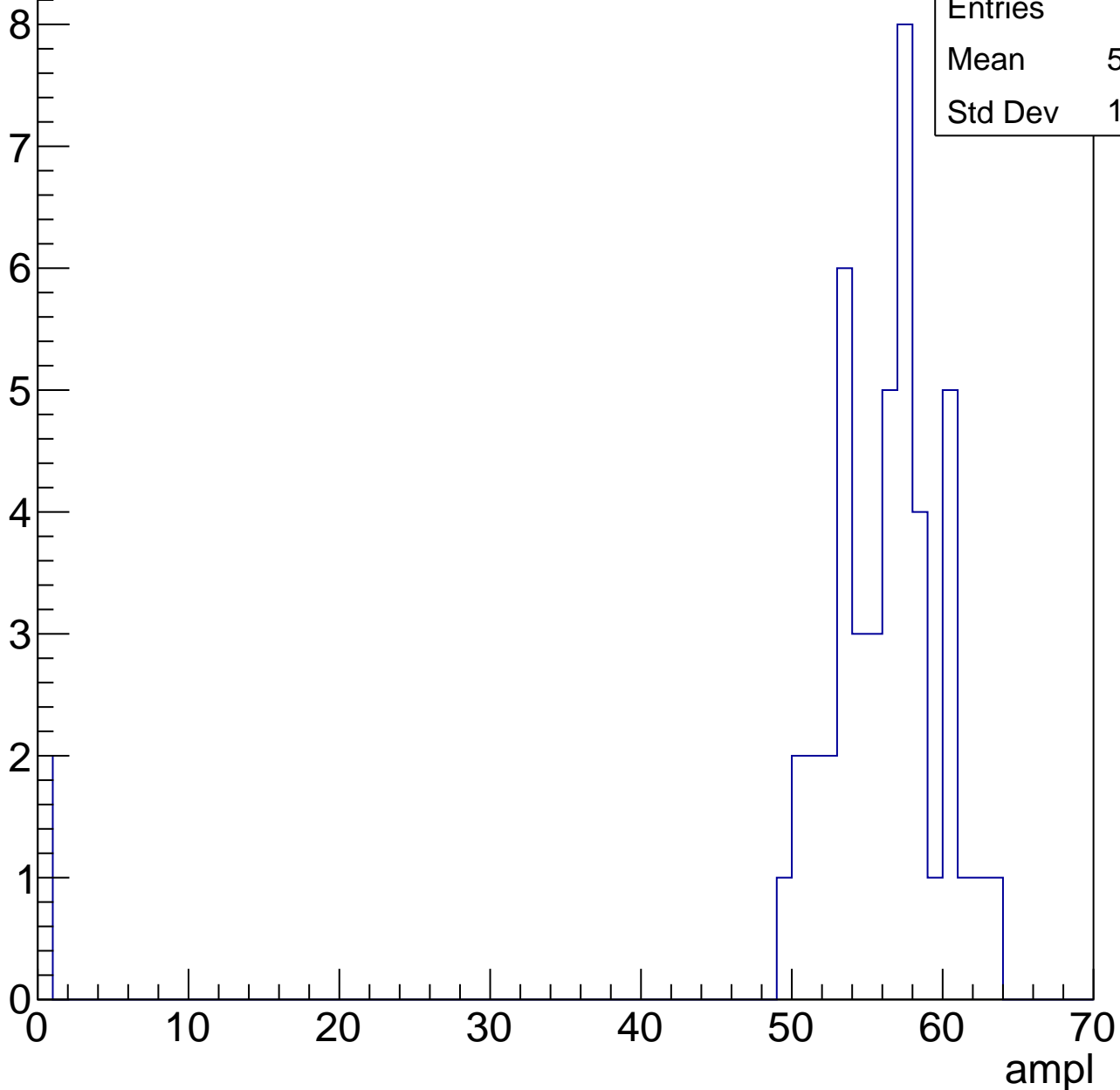


B1L103S, U13-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

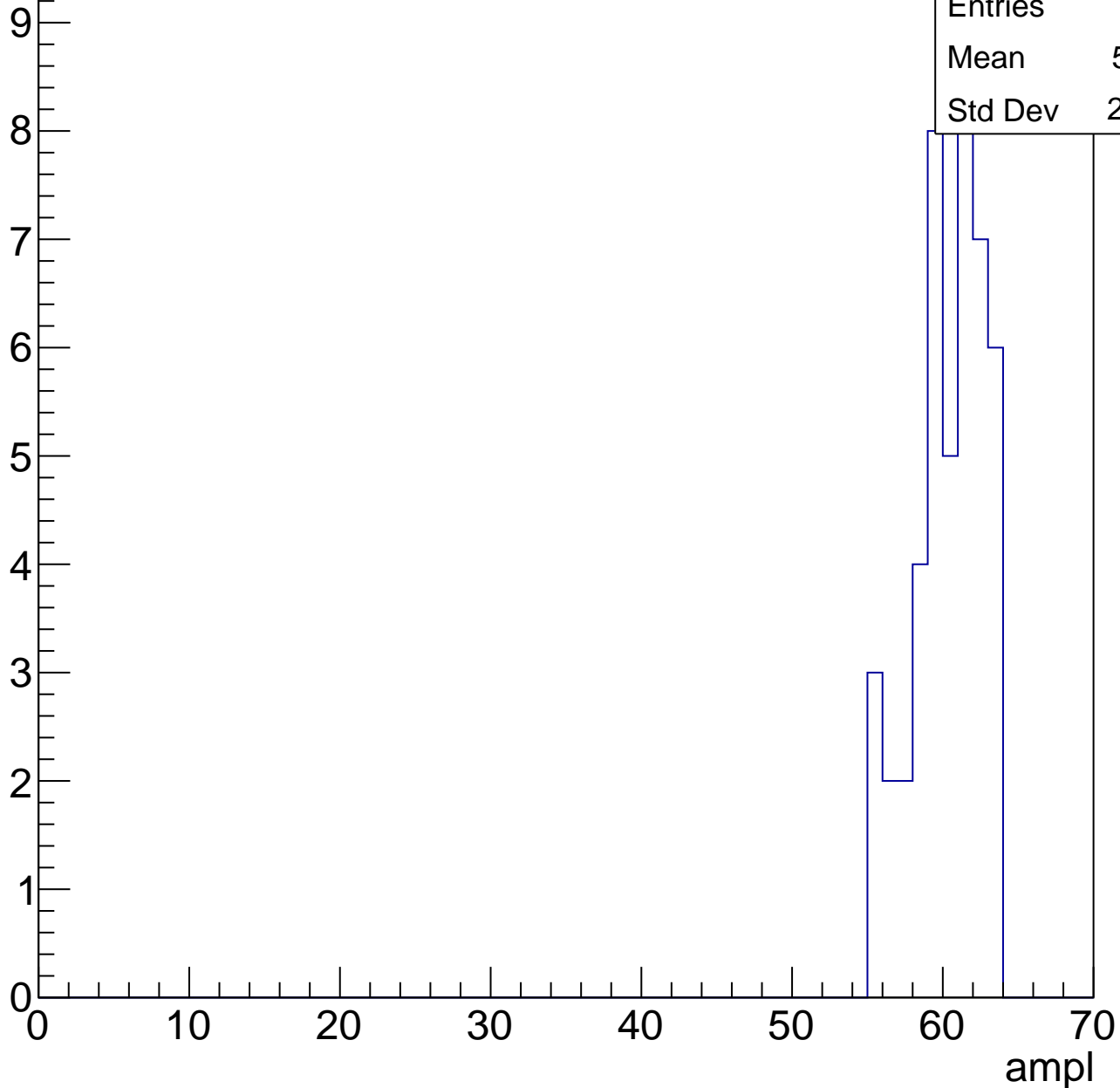
Entries	47
Mean	53.47
Std Dev	11.73



B1L103S, U13-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	46
Mean	59.91
Std Dev	2.283

B1L103S, U13-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

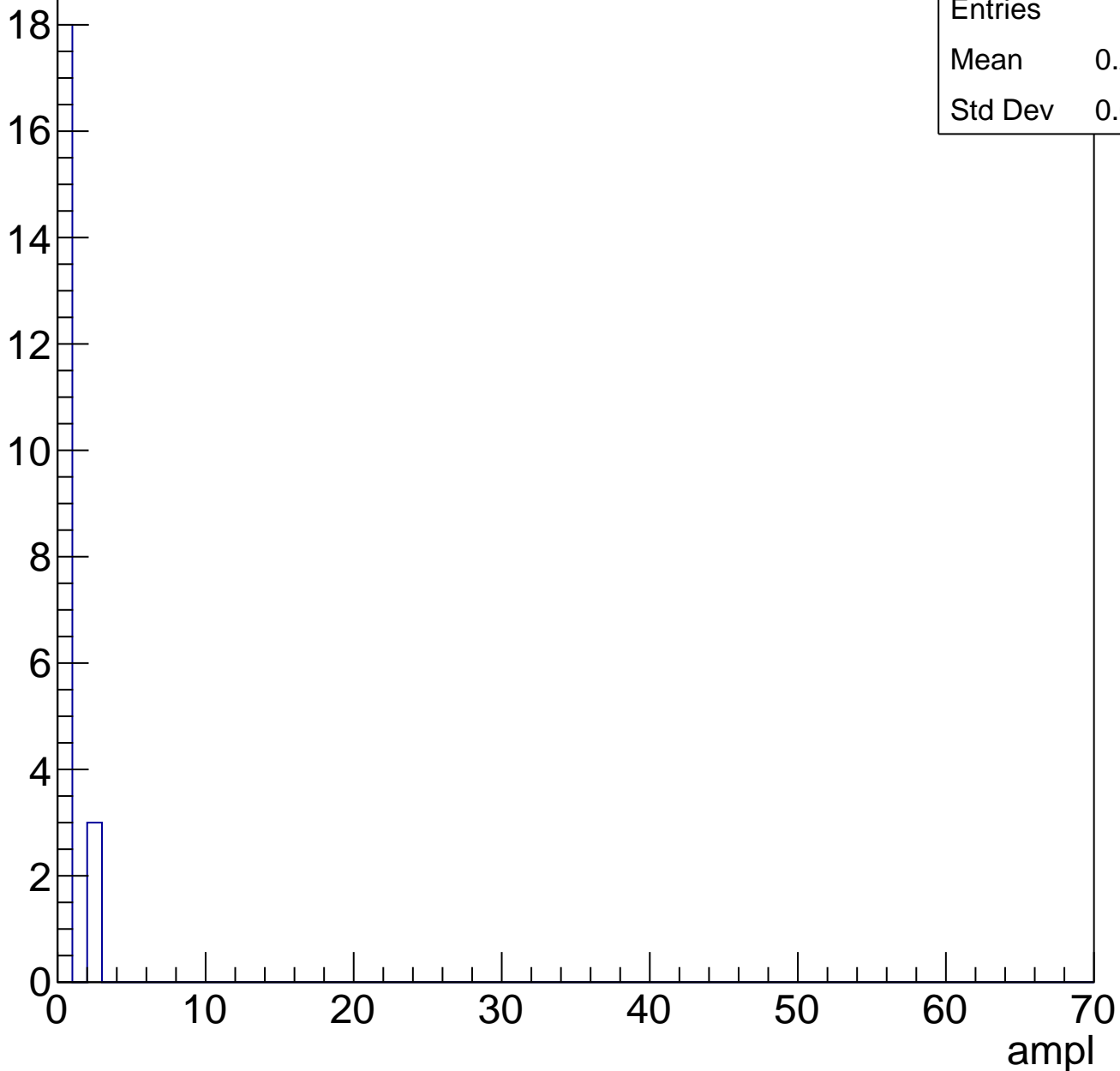


B1L103S, U13-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	0.2857
Std Dev	0.6999

Entry



B1L103S, U13-ch83, adc0

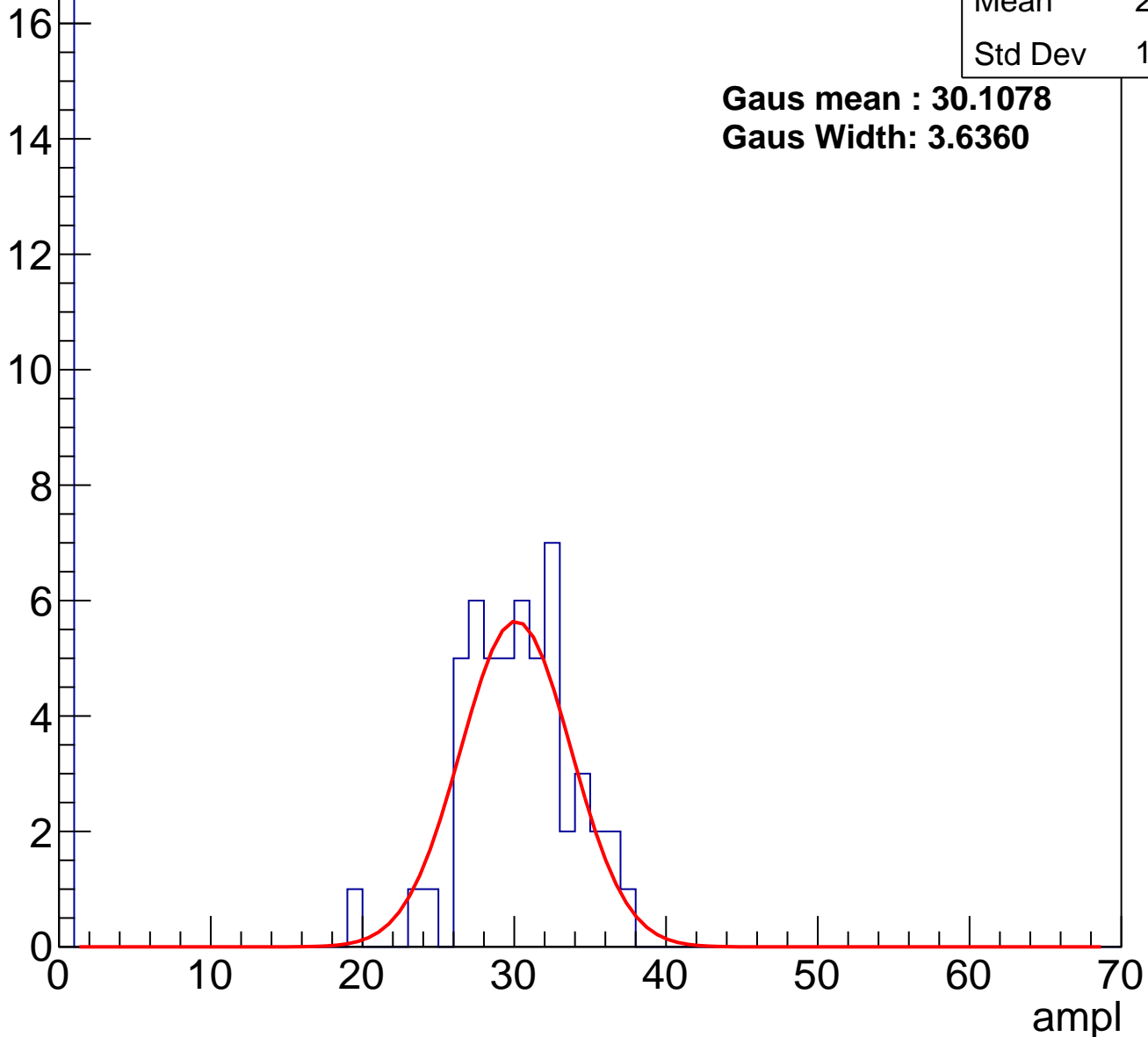
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	22.45
Std Dev	13.19

Gaus mean : 30.1078

Gaus Width: 3.6360

Entry



B1L103S, U13-ch83, adc1

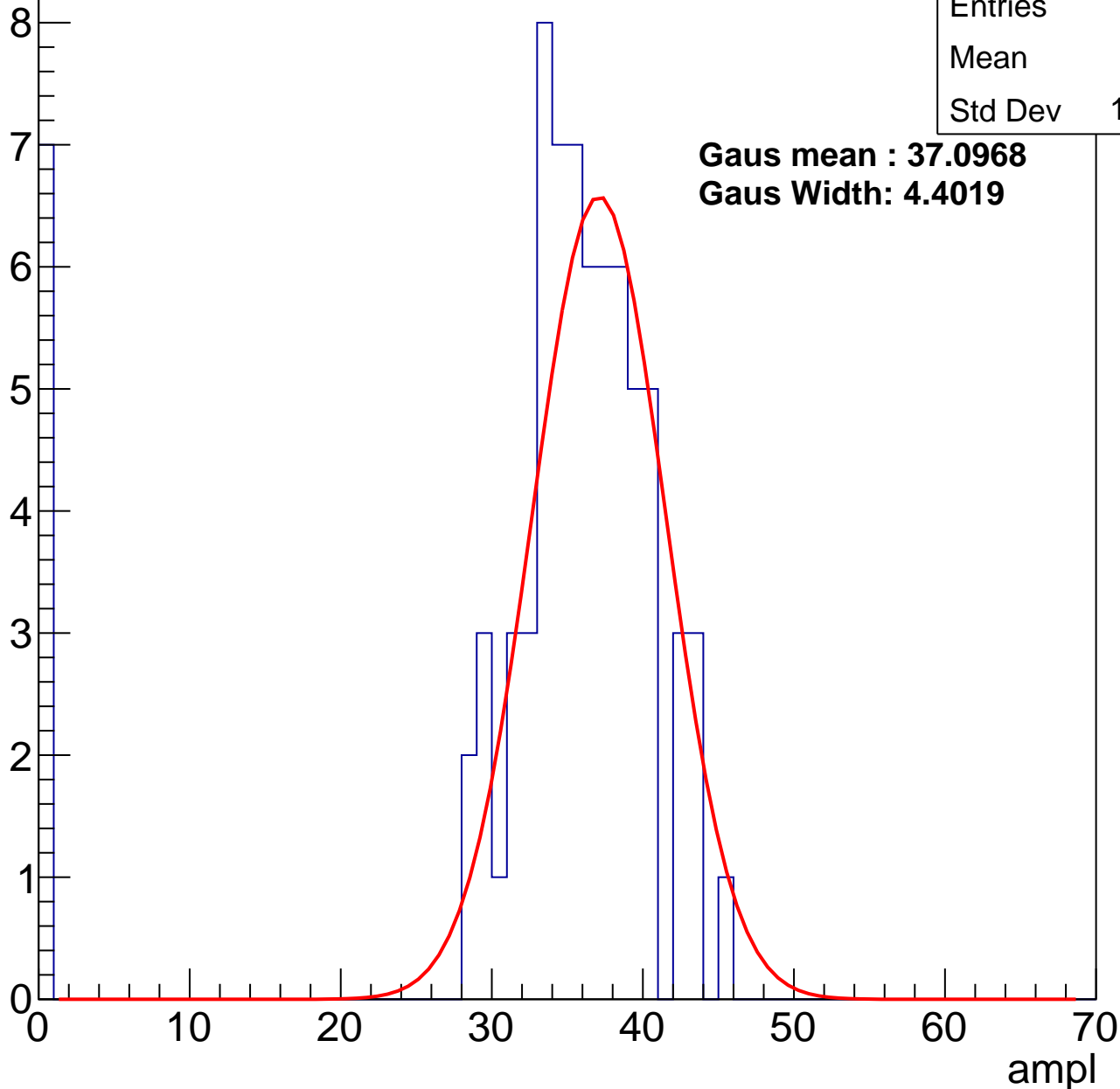
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	32.5
Std Dev	10.99

Gaus mean : 37.0968

Gaus Width: 4.4019



B1L103S, U13-ch83, adc2

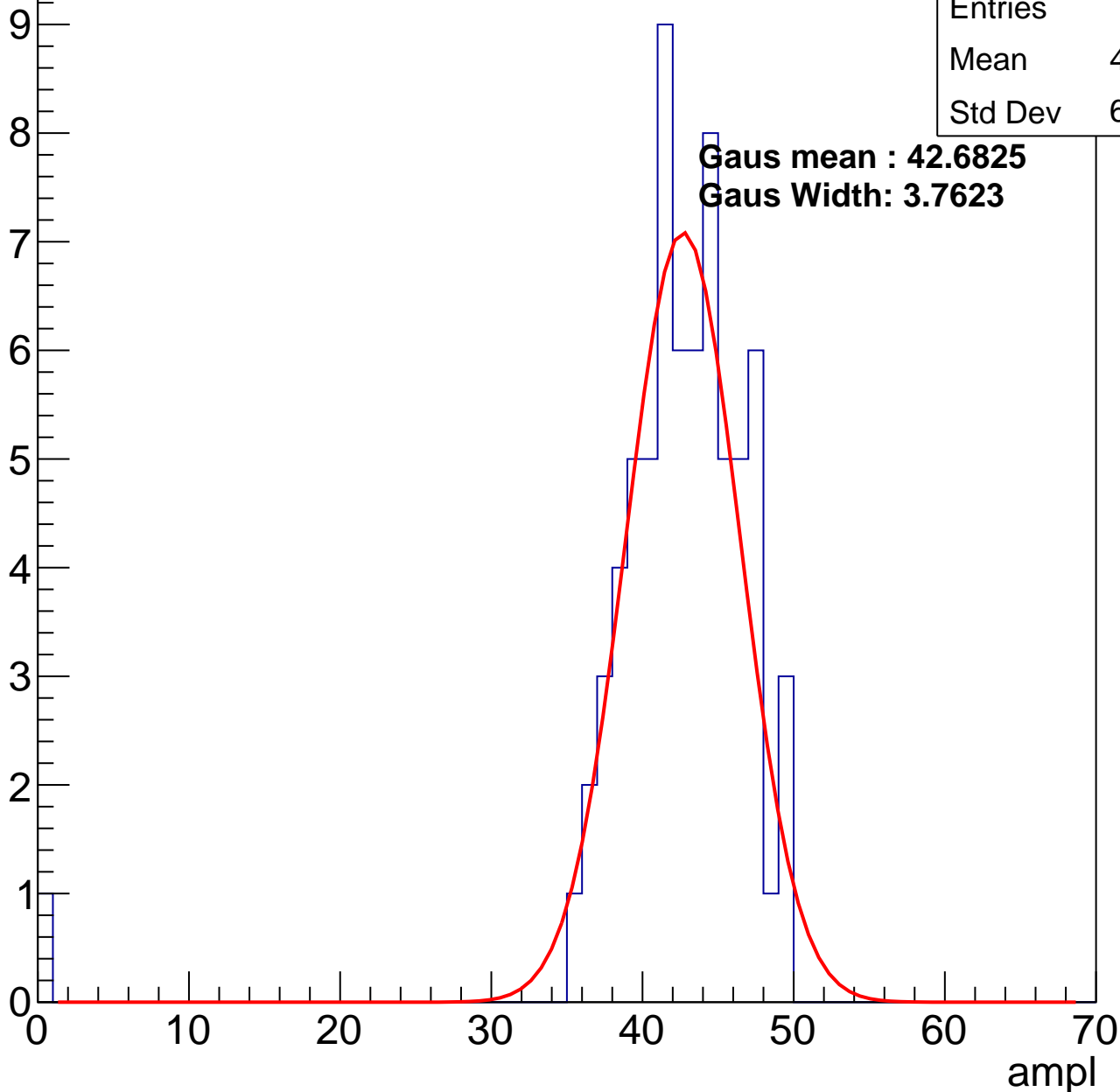
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	41.83
Std Dev	6.085

Gaus mean : 42.6825

Gaus Width: 3.7623



B1L103S, U13-ch83, adc3

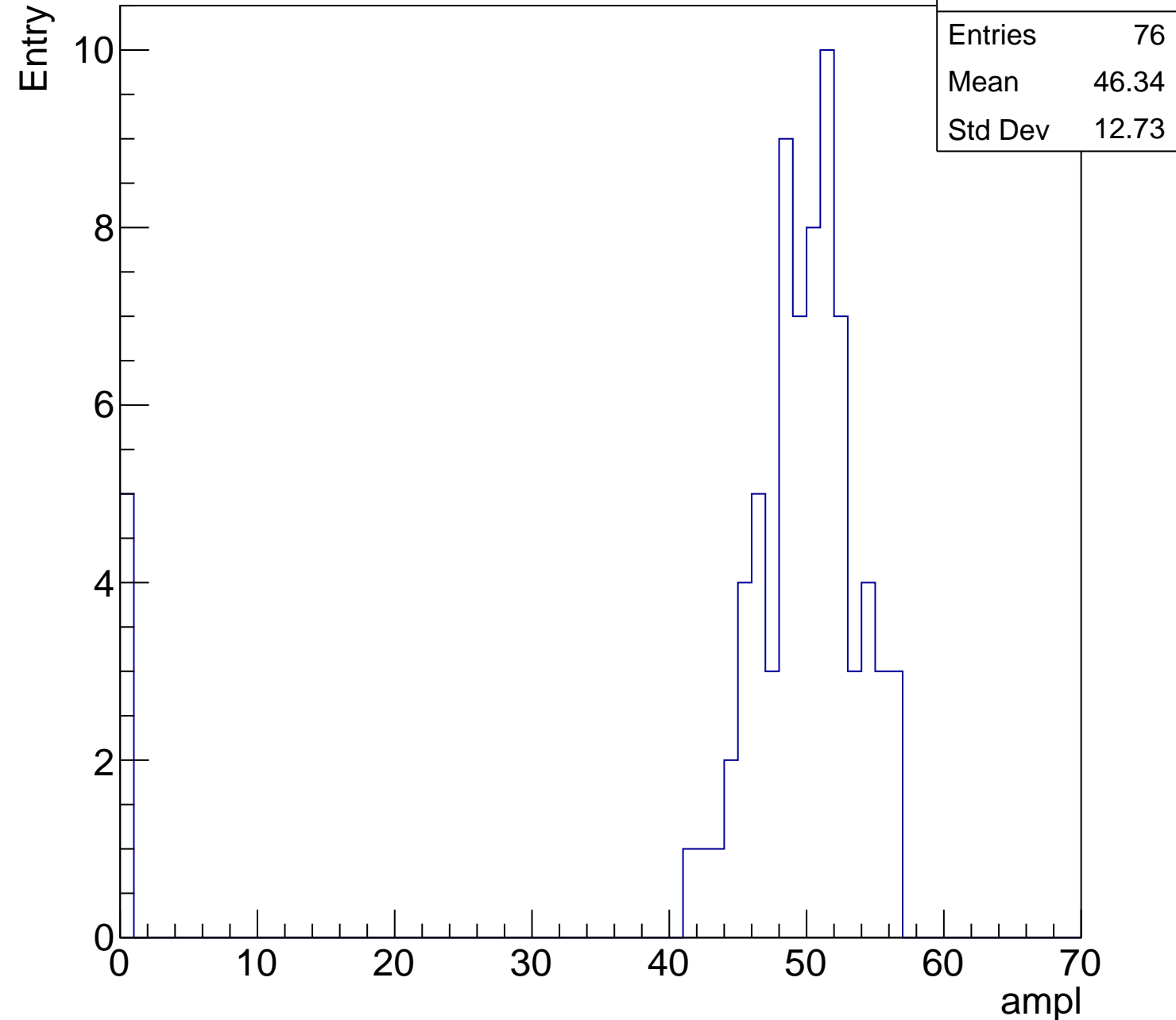
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	46.34
Std Dev	12.73

Entry

10
8
6
4
2
0

ampl

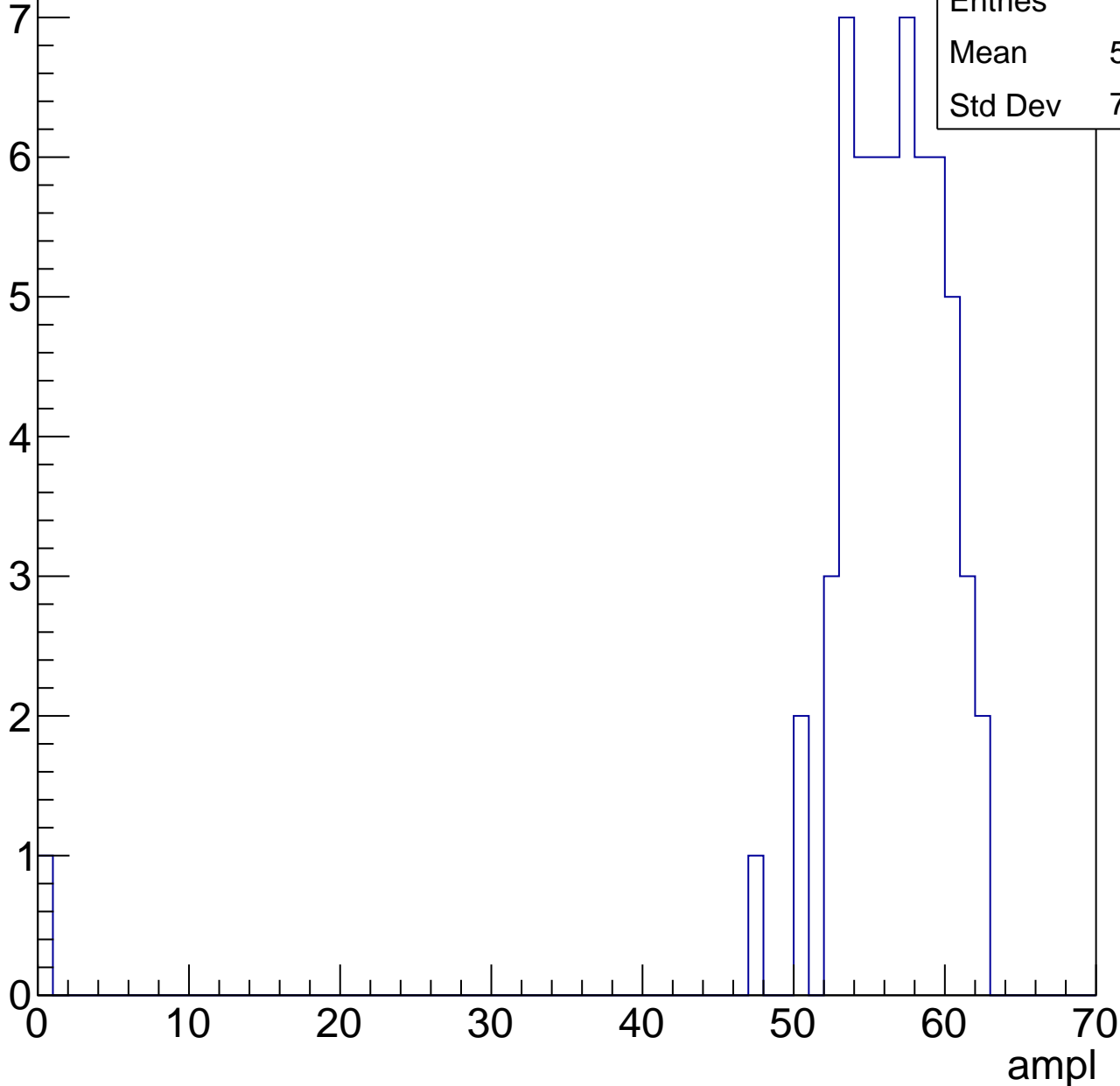


B1L103S, U13-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

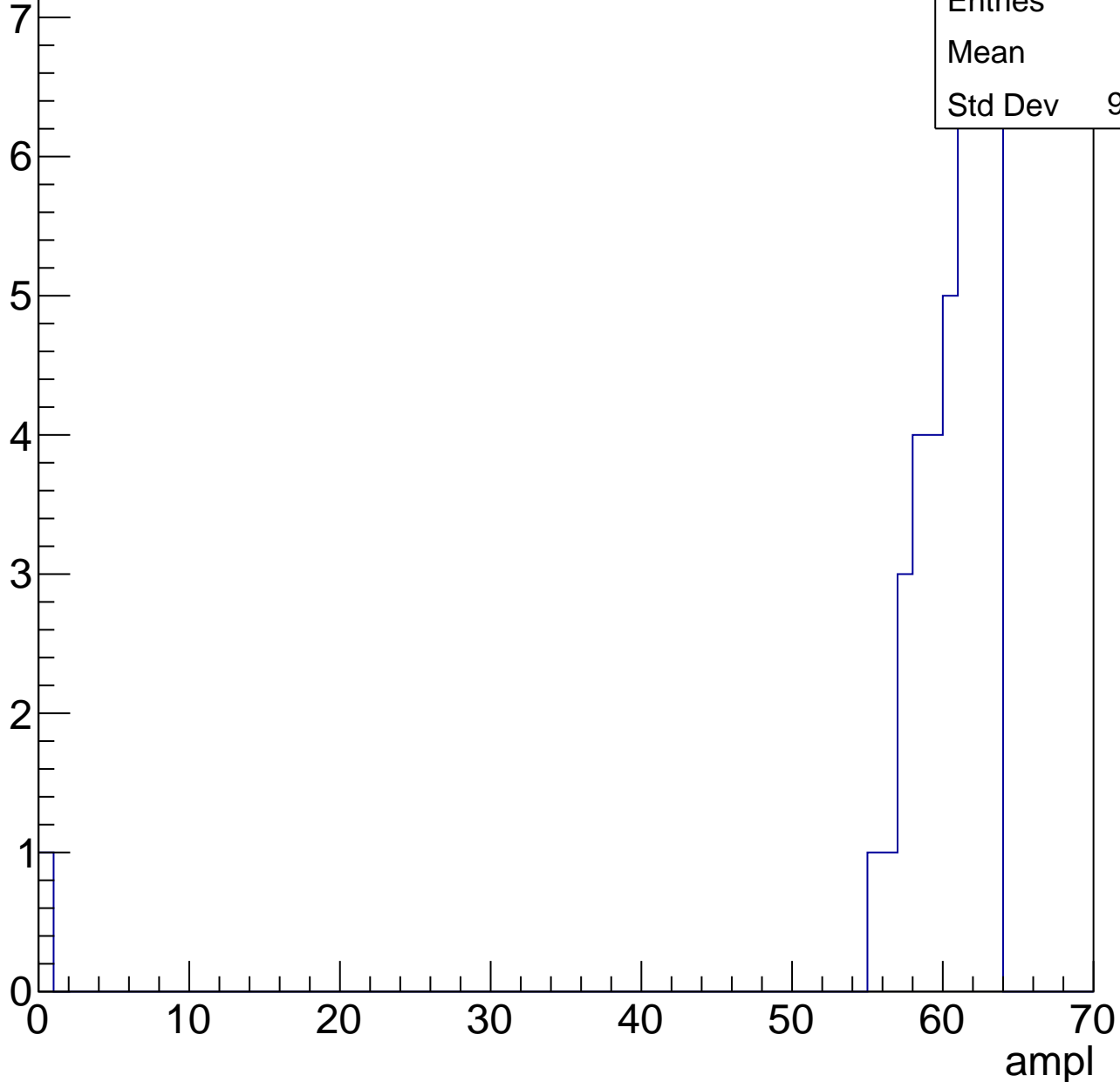
Entries	61
Mean	55.28
Std Dev	7.799



B1L103S, U13-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	4
Mean	62.75
Std Dev	0.433

B1L103S, U13-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch84, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	22.59
Std Dev	11.15

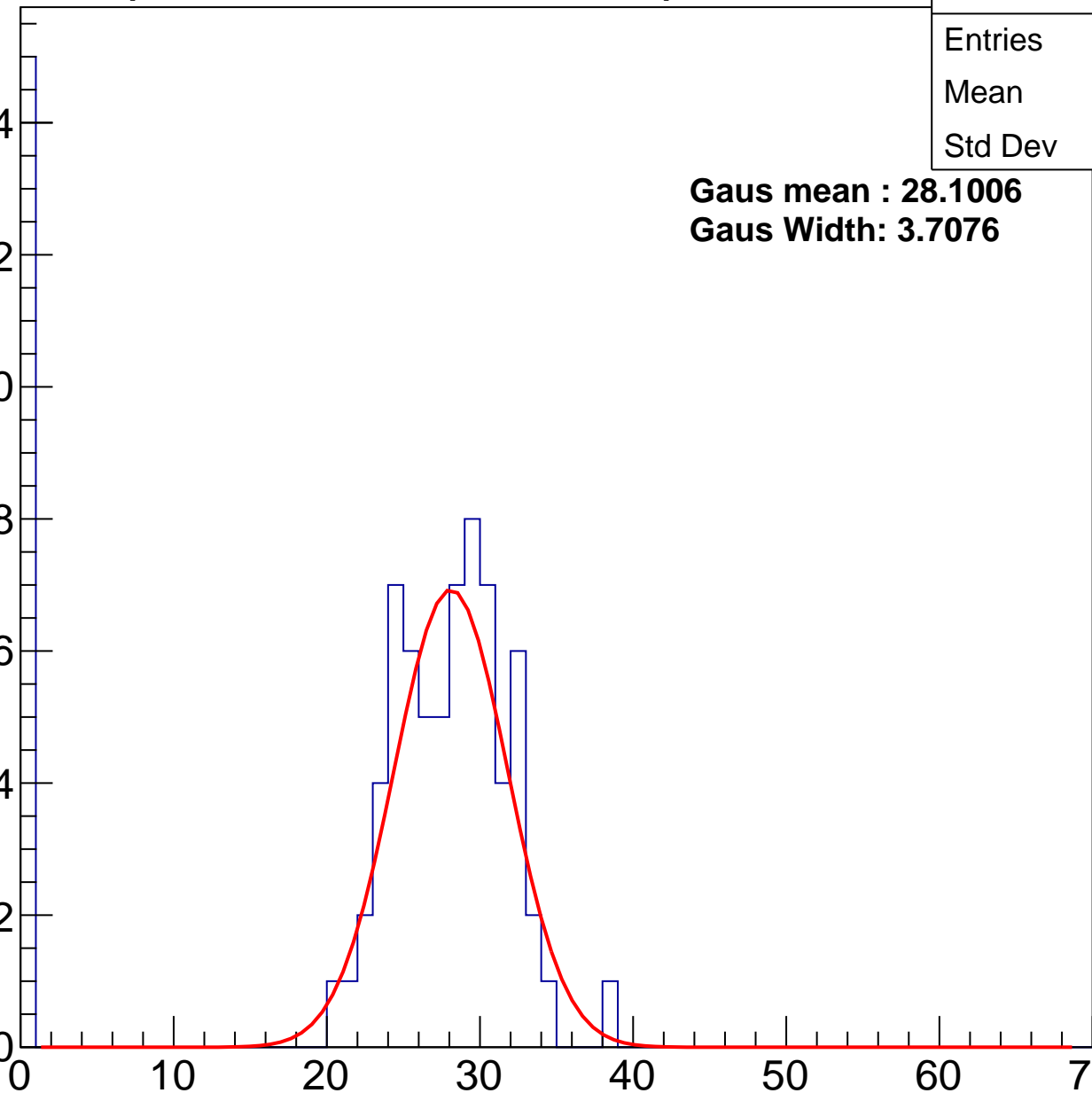
Gaus mean : 28.1006

Gaus Width: 3.7076

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch84, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	33.54
Std Dev	7.516

Gaus mean : 35.3859

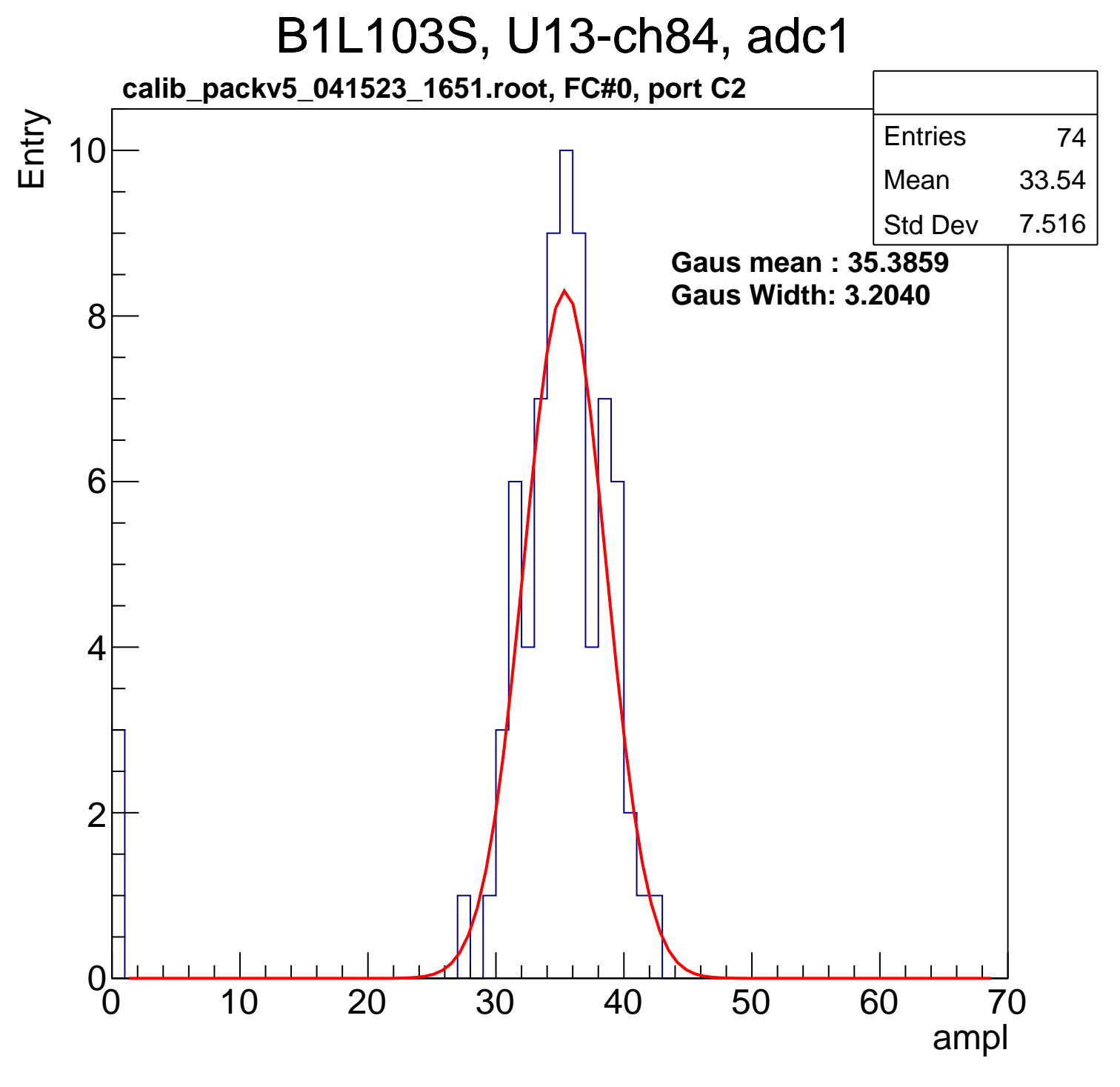
Gaus Width: 3.2040

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch84, adc2

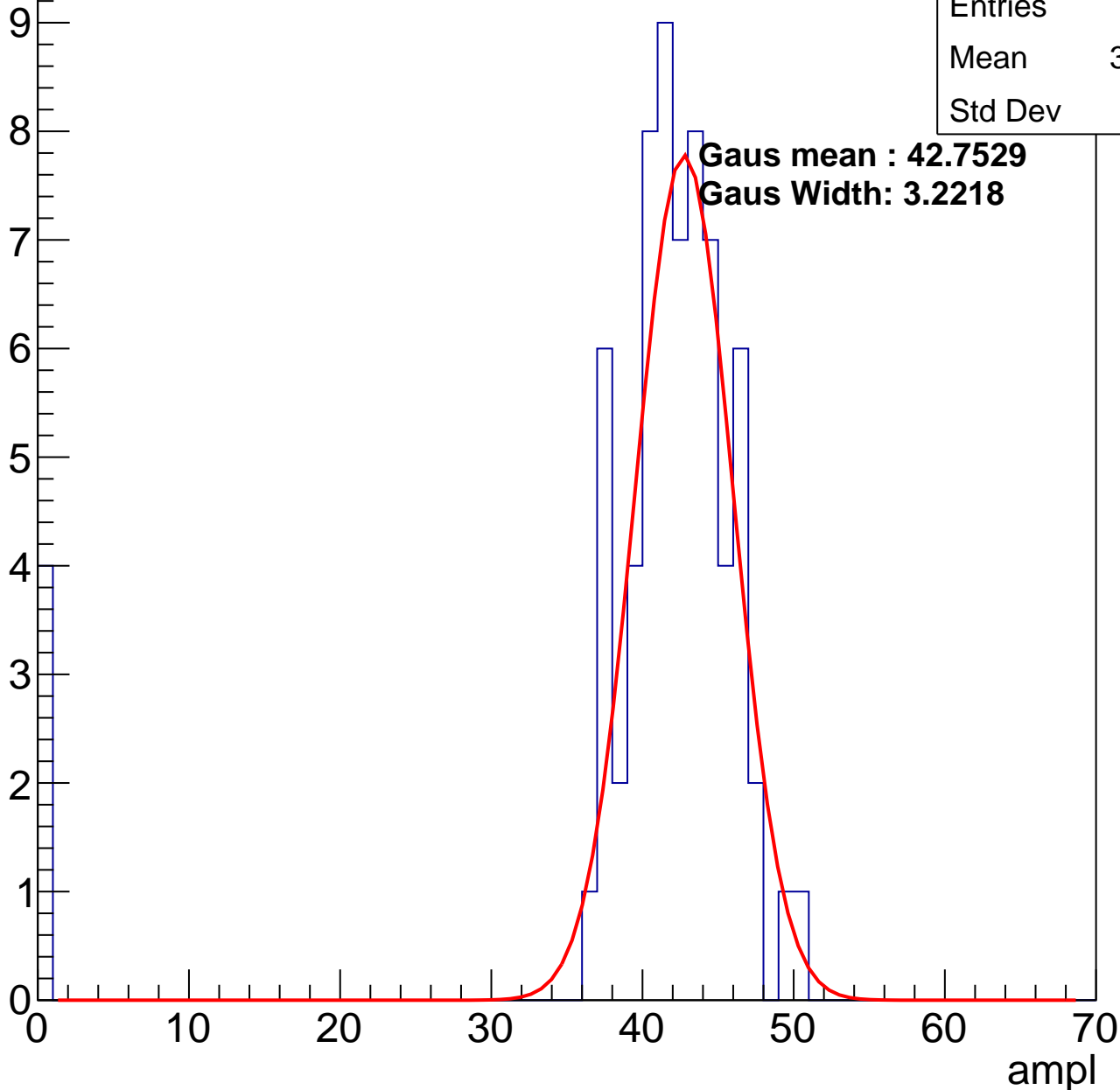
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	39.63
Std Dev	10.2

Gaus mean : 42.7529

Gaus Width: 3.2218

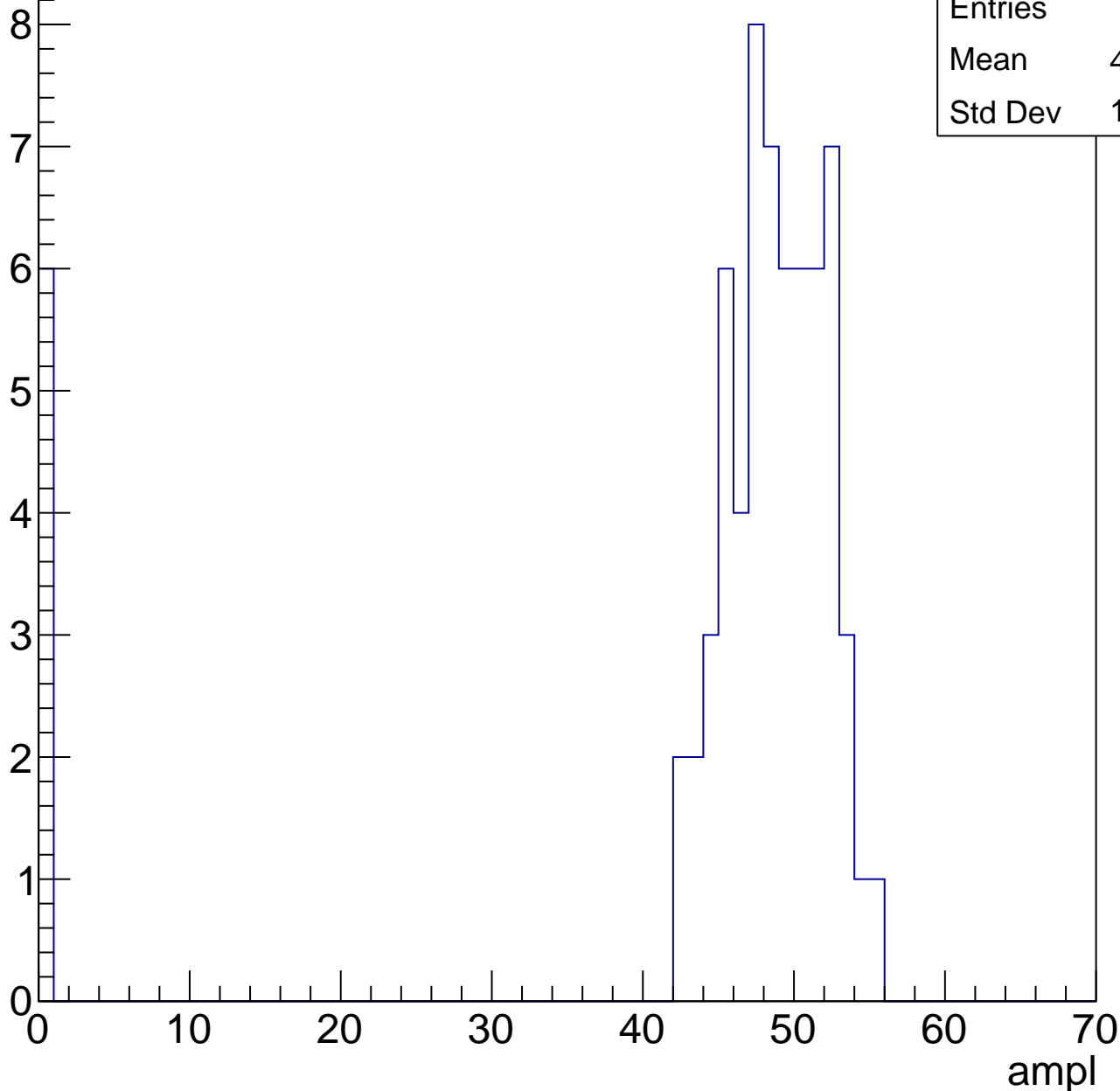


B1L103S, U13-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

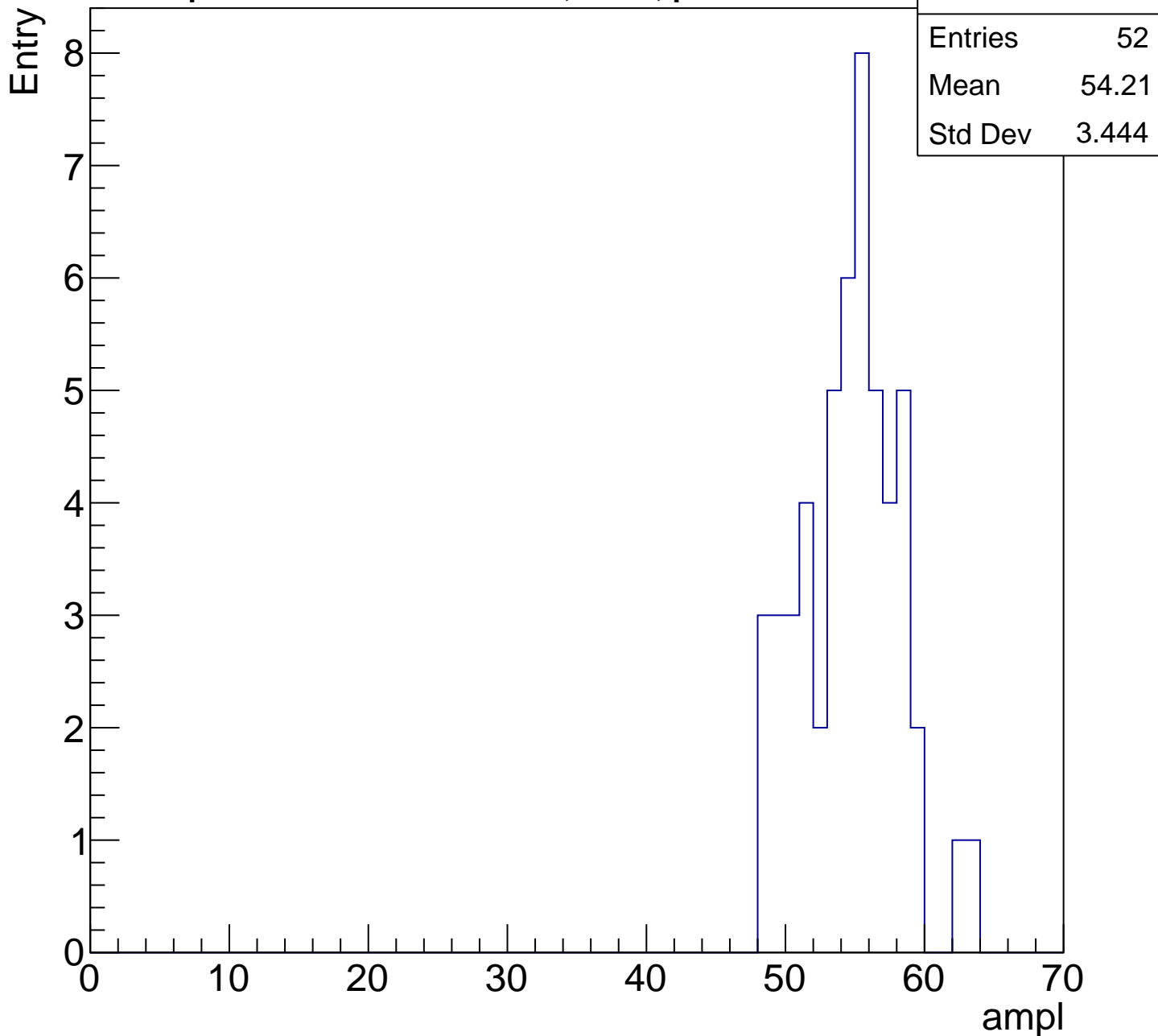
Entry

Entries	68
Mean	44.12
Std Dev	14.04



B1L103S, U13-ch84, adc4

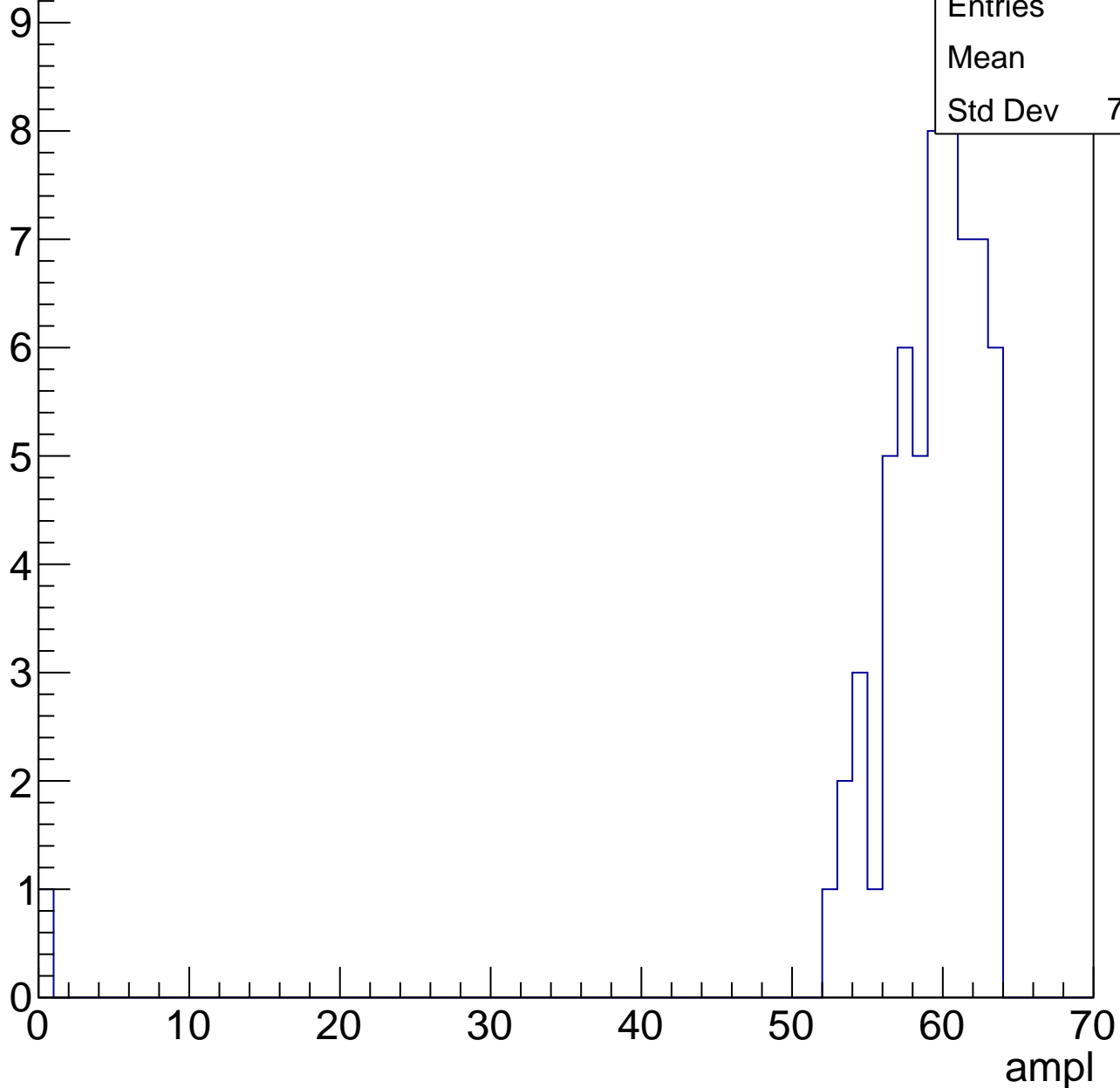
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U13-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

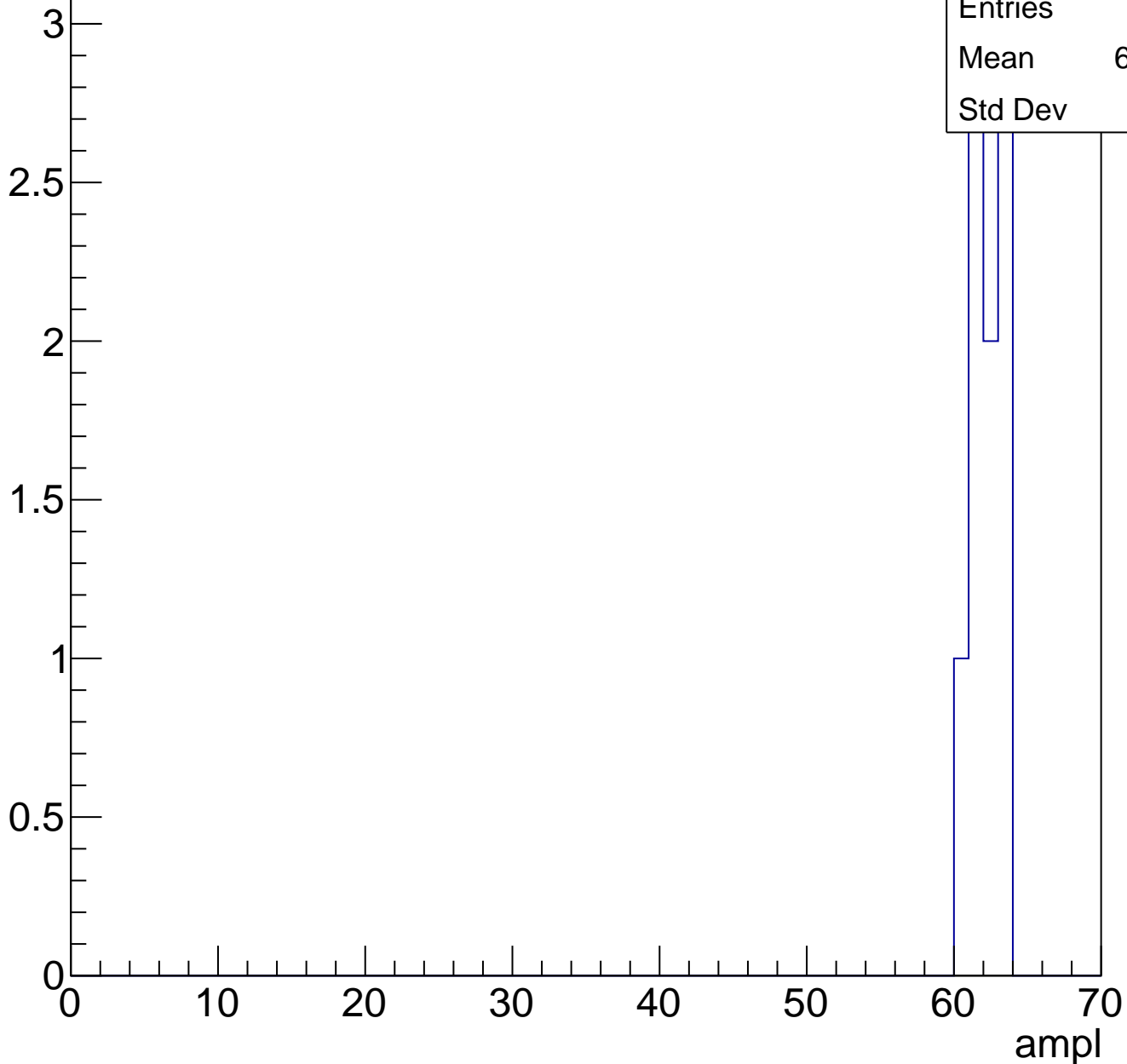


Entries	61
Mean	58
Std Dev	7.998

B1L103S, U13-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	9
Mean	61.78
Std Dev	1.03

B1L103S, U13-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch85, adc0

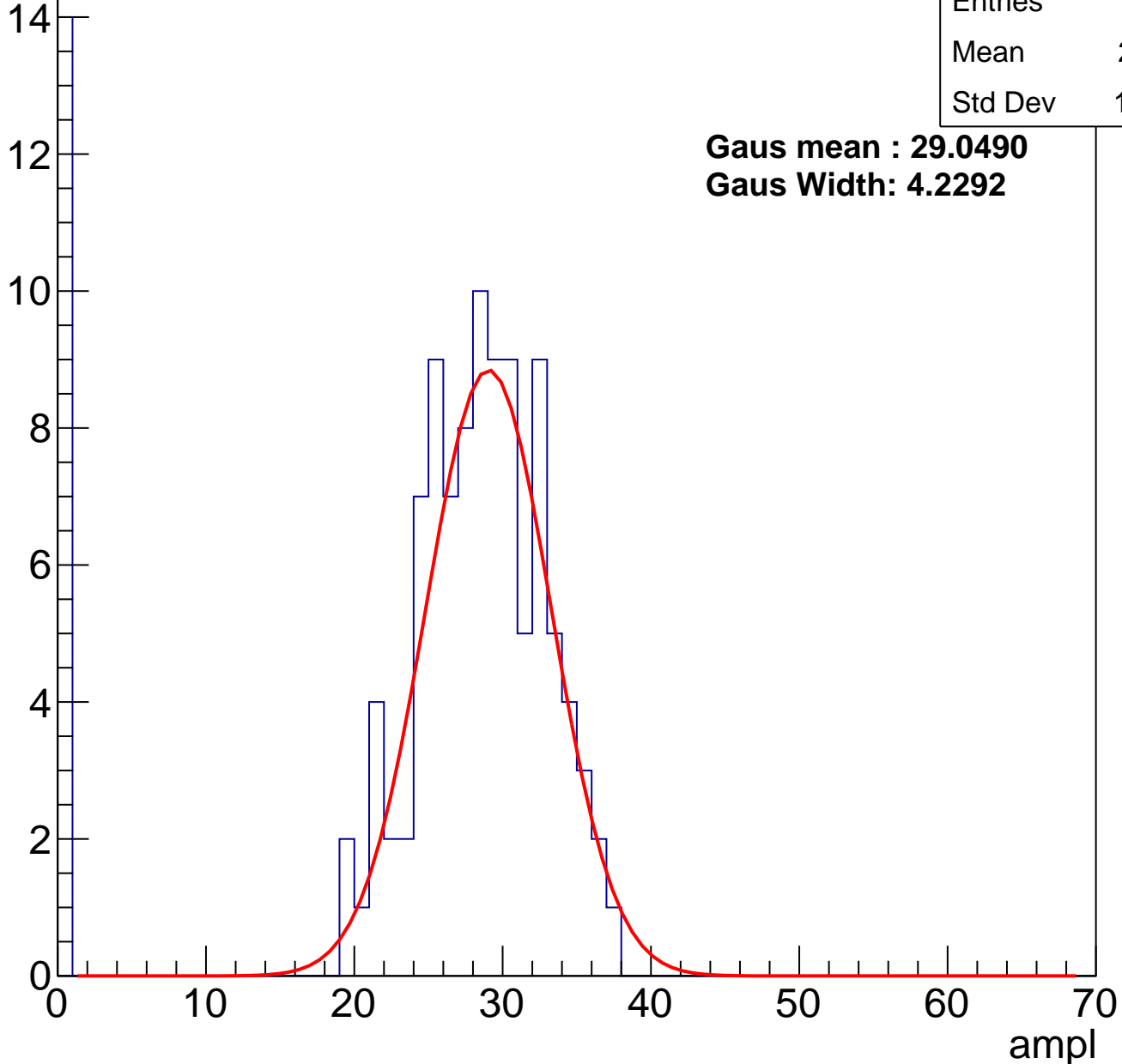
calib_packv5_041523_1651.root, FC#0, port C2

Entries	113
Mean	24.71
Std Dev	10.04

Gaus mean : 29.0490

Gaus Width: 4.2292

Entry



B1L103S, U13-ch85, adc1

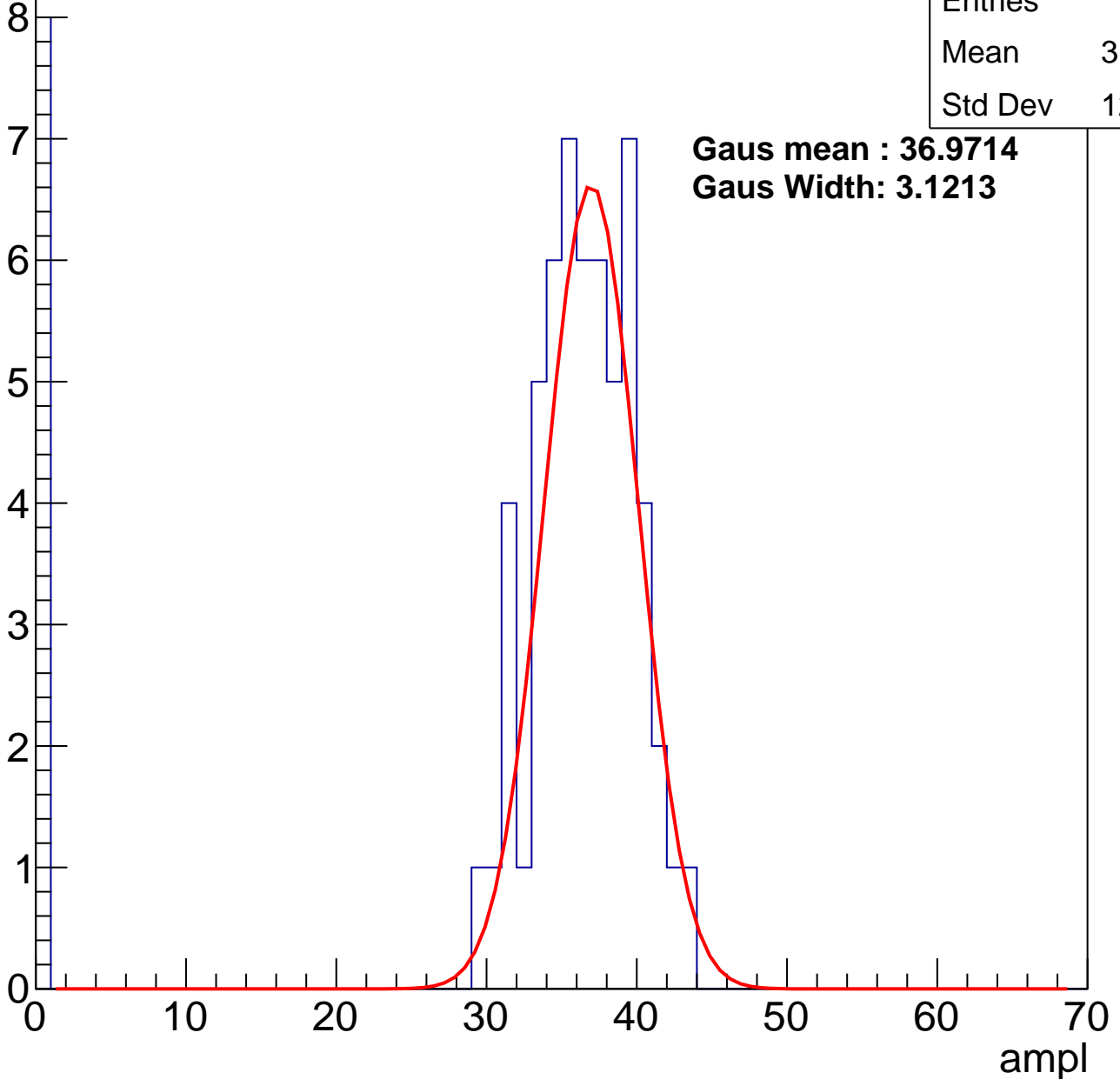
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	31.65
Std Dev	12.22

Gaus mean : 36.9714

Gaus Width: 3.1213



B1L103S, U13-ch85, adc2

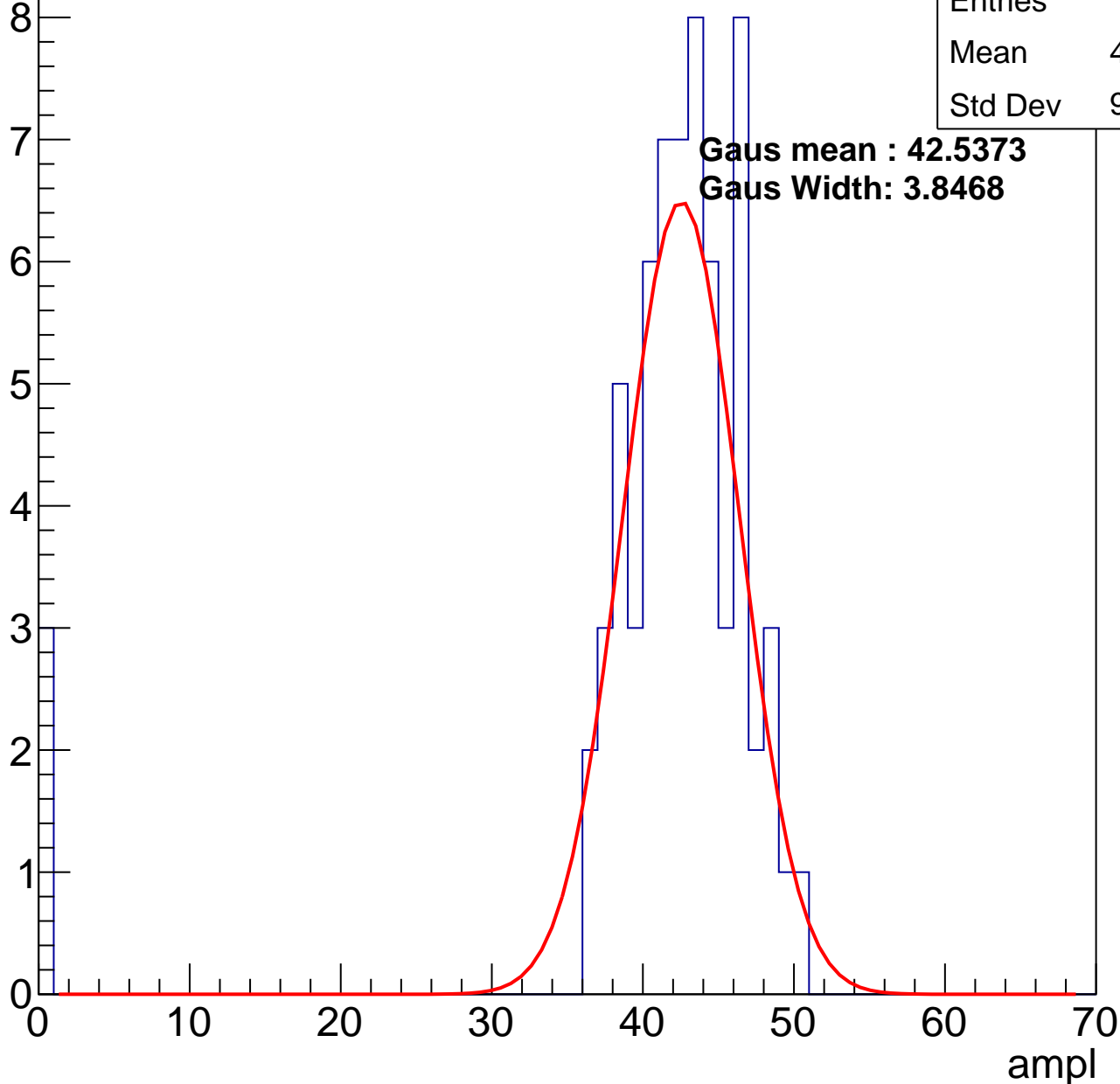
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.57
Std Dev	9.312

Gaus mean : 42.5373

Gaus Width: 3.8468

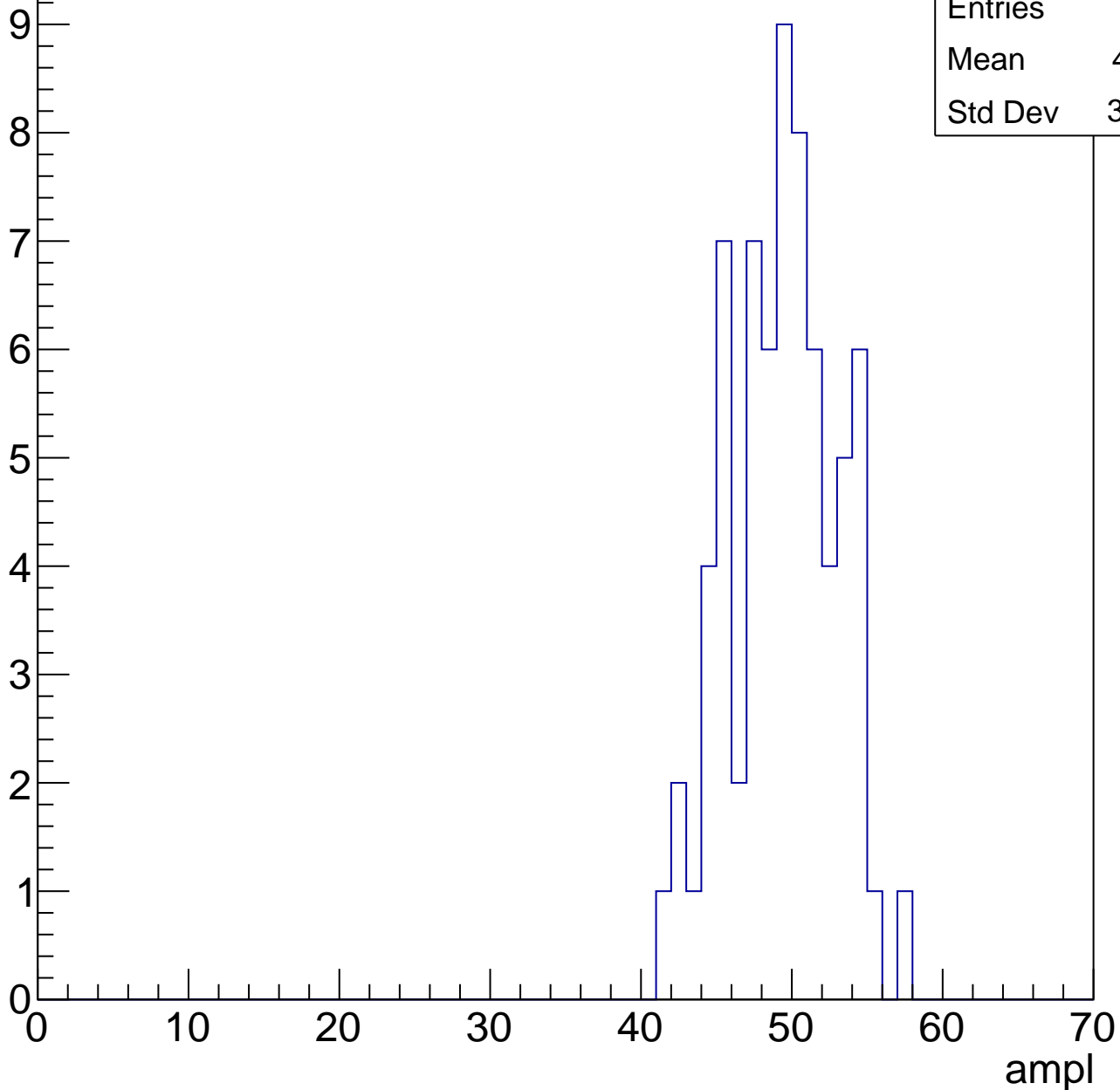


B1L103S, U13-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

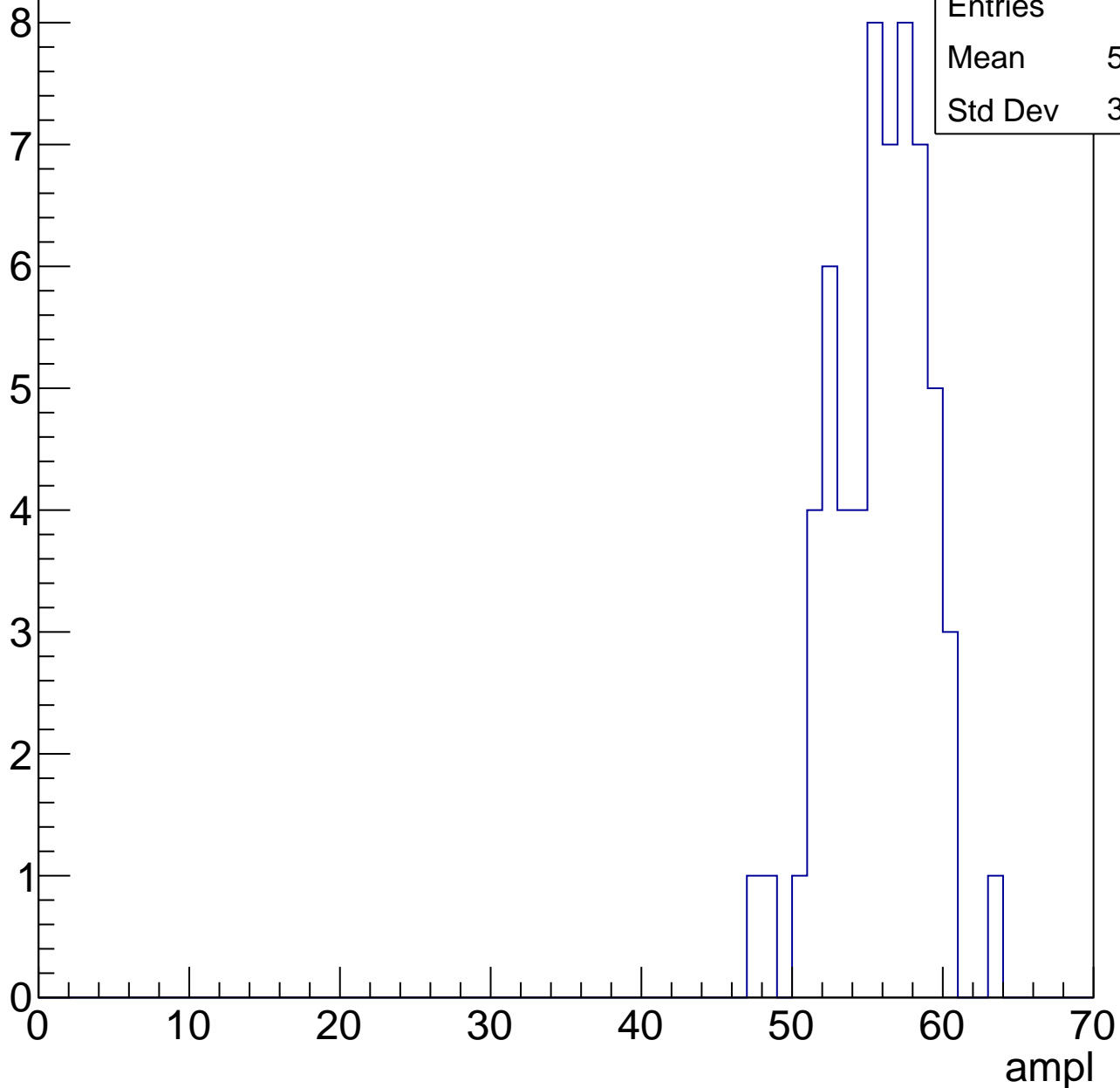
Entries	70
Mean	48.91
Std Dev	3.504



B1L103S, U13-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

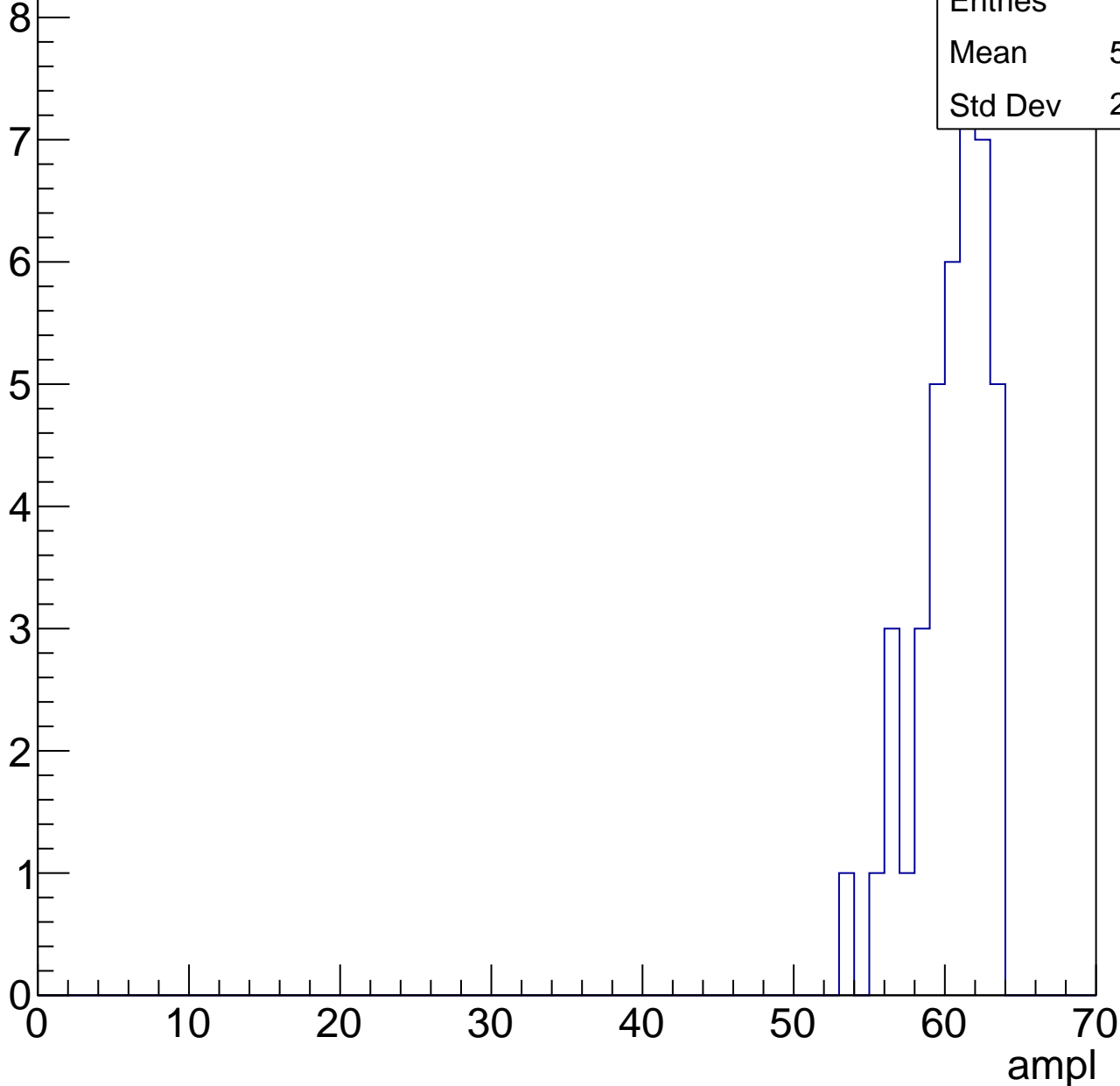


B1L103S, U13-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

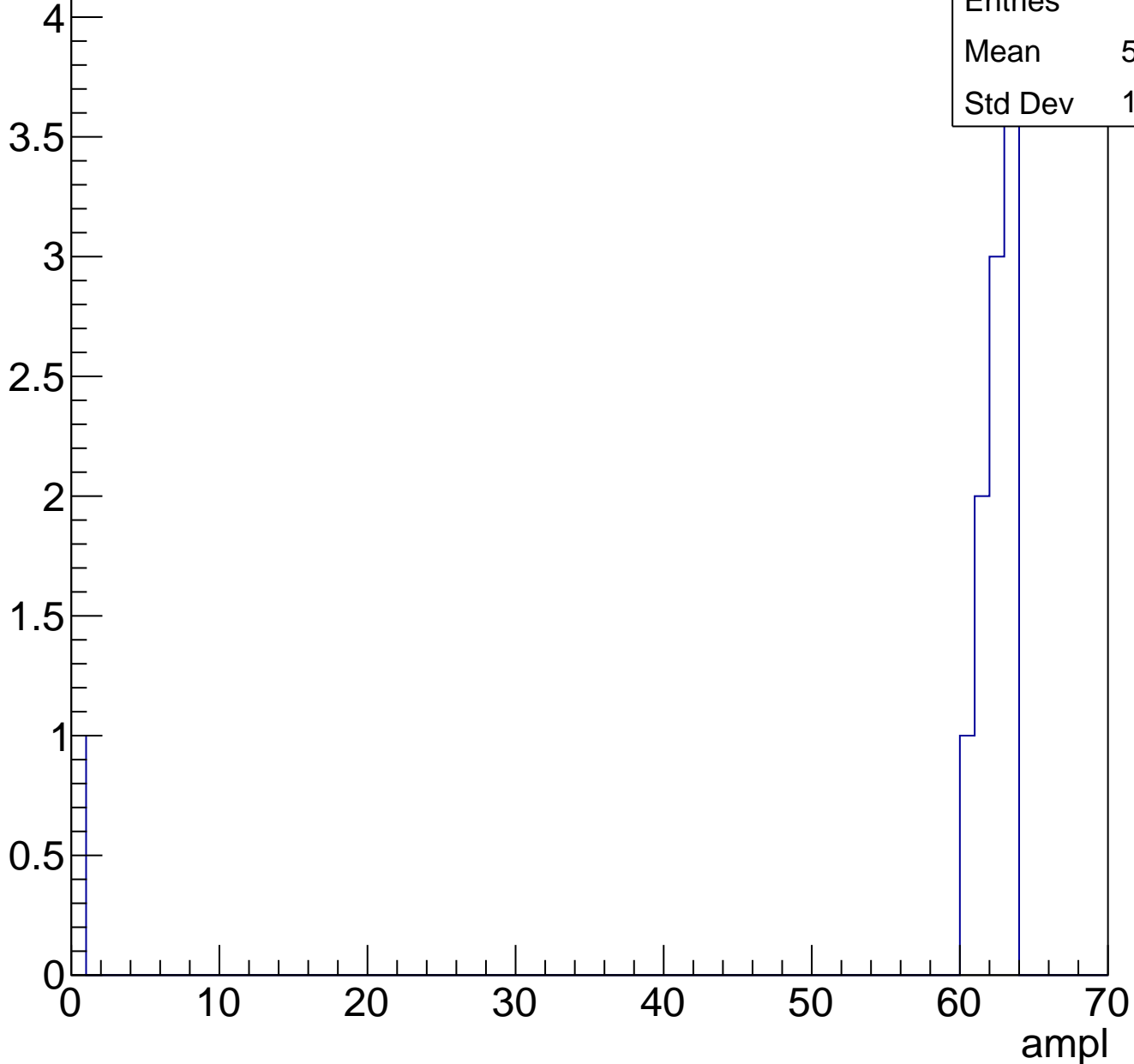
Entries	40
Mean	59.98
Std Dev	2.393



B1L103S, U13-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch86, adc0

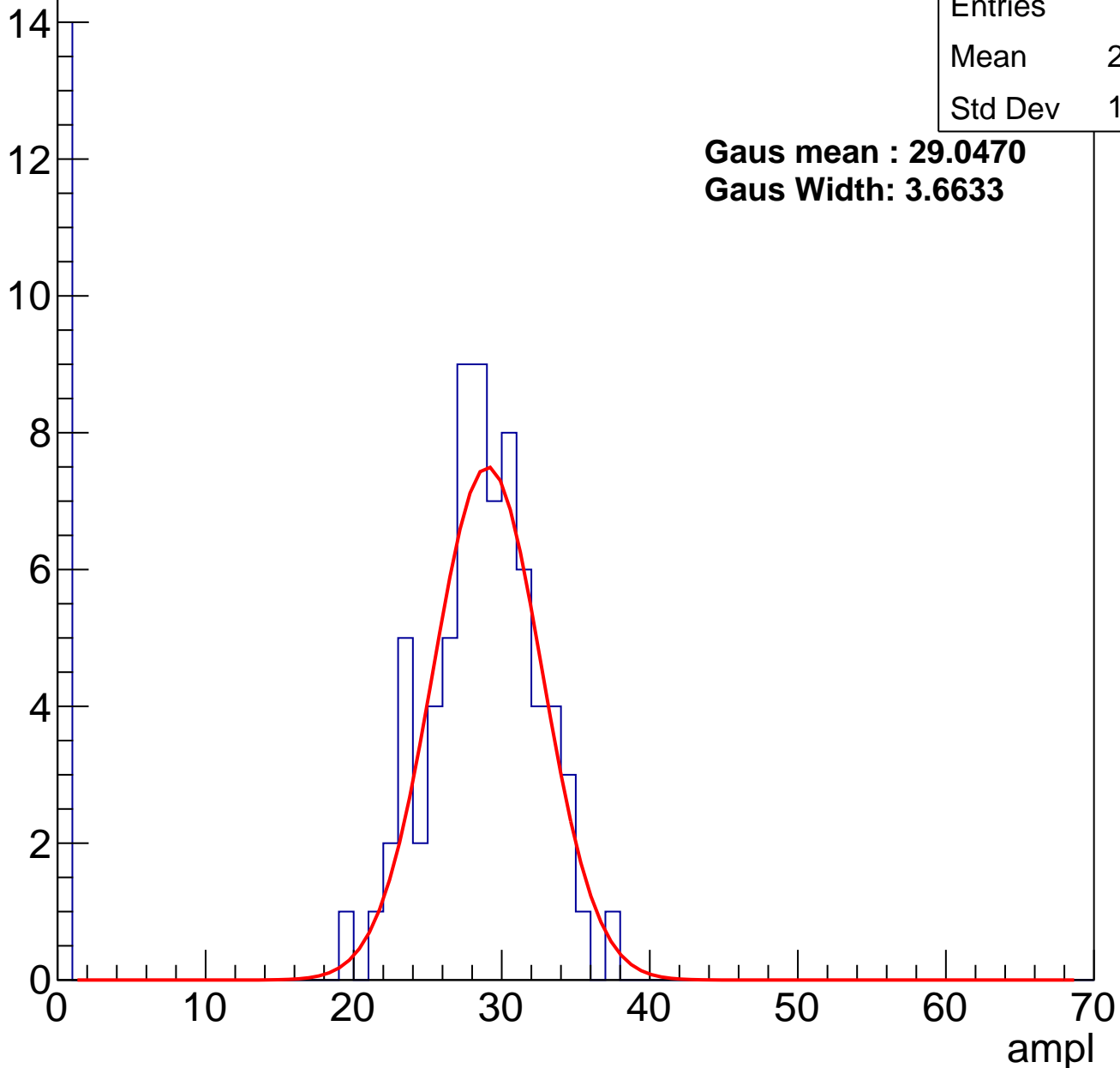
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.66
Std Dev	10.93

Gaus mean : 29.0470

Gaus Width: 3.6633

Entry



B1L103S, U13-ch86, adc1

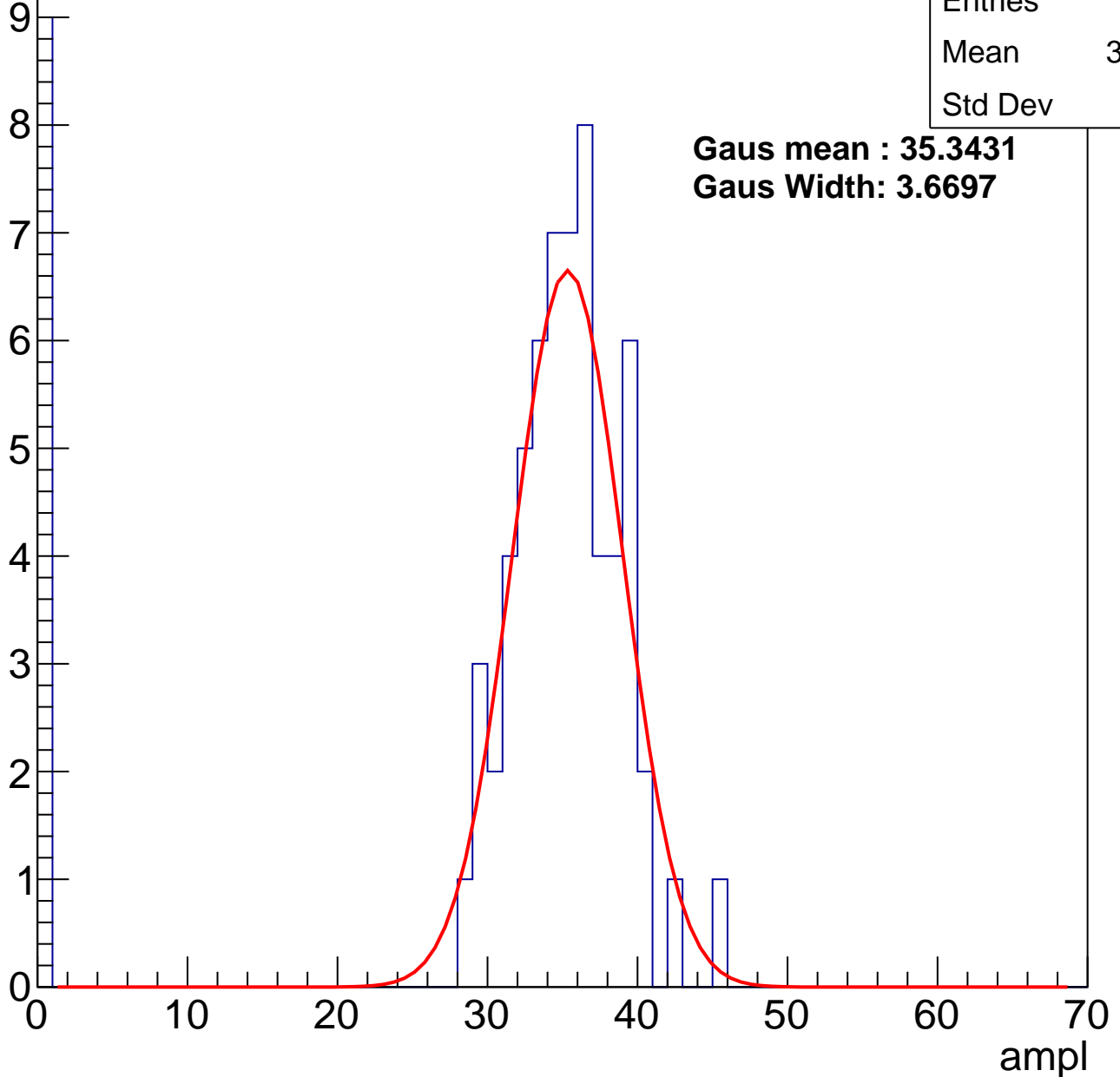
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	30.41
Std Dev	12.1

Gaus mean : 35.3431

Gaus Width: 3.6697



B1L103S, U13-ch86, adc2

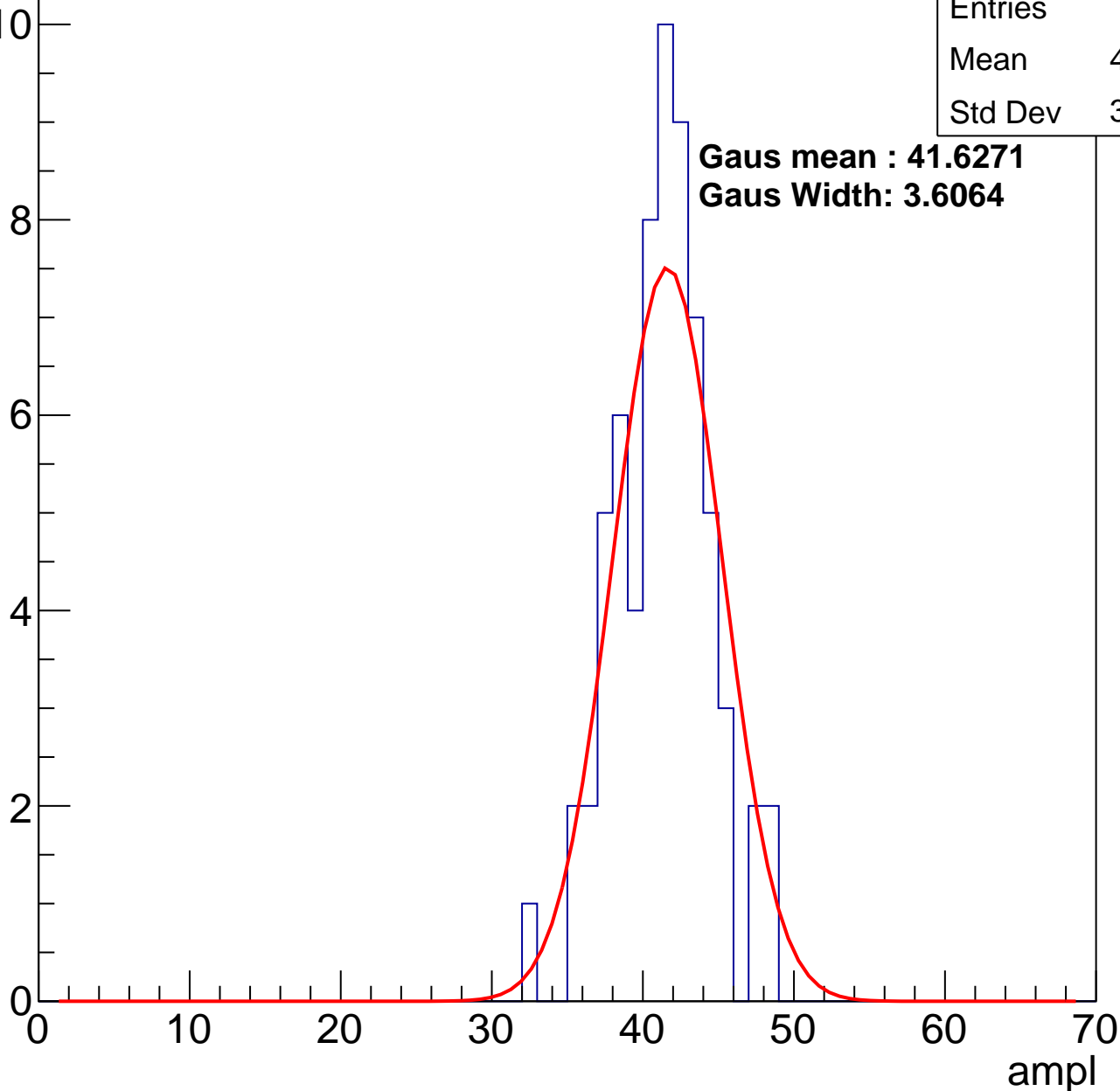
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	40.86
Std Dev	3.162

Gaus mean : 41.6271

Gaus Width: 3.6064



B1L103S, U13-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	46.88
Std Dev	8.328

Entry

10

8

6

4

2

0

ampl

0

10

20

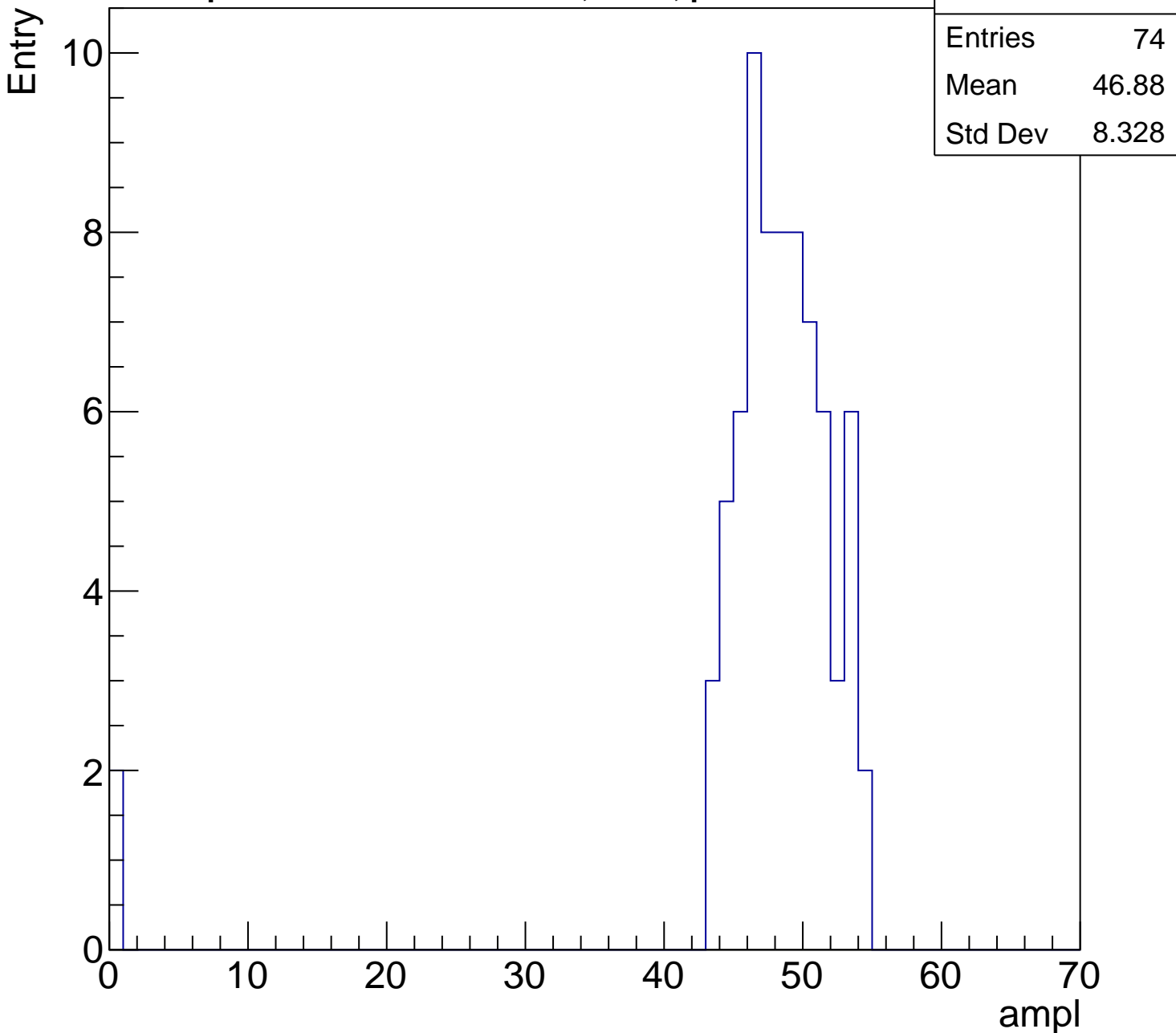
30

40

50

60

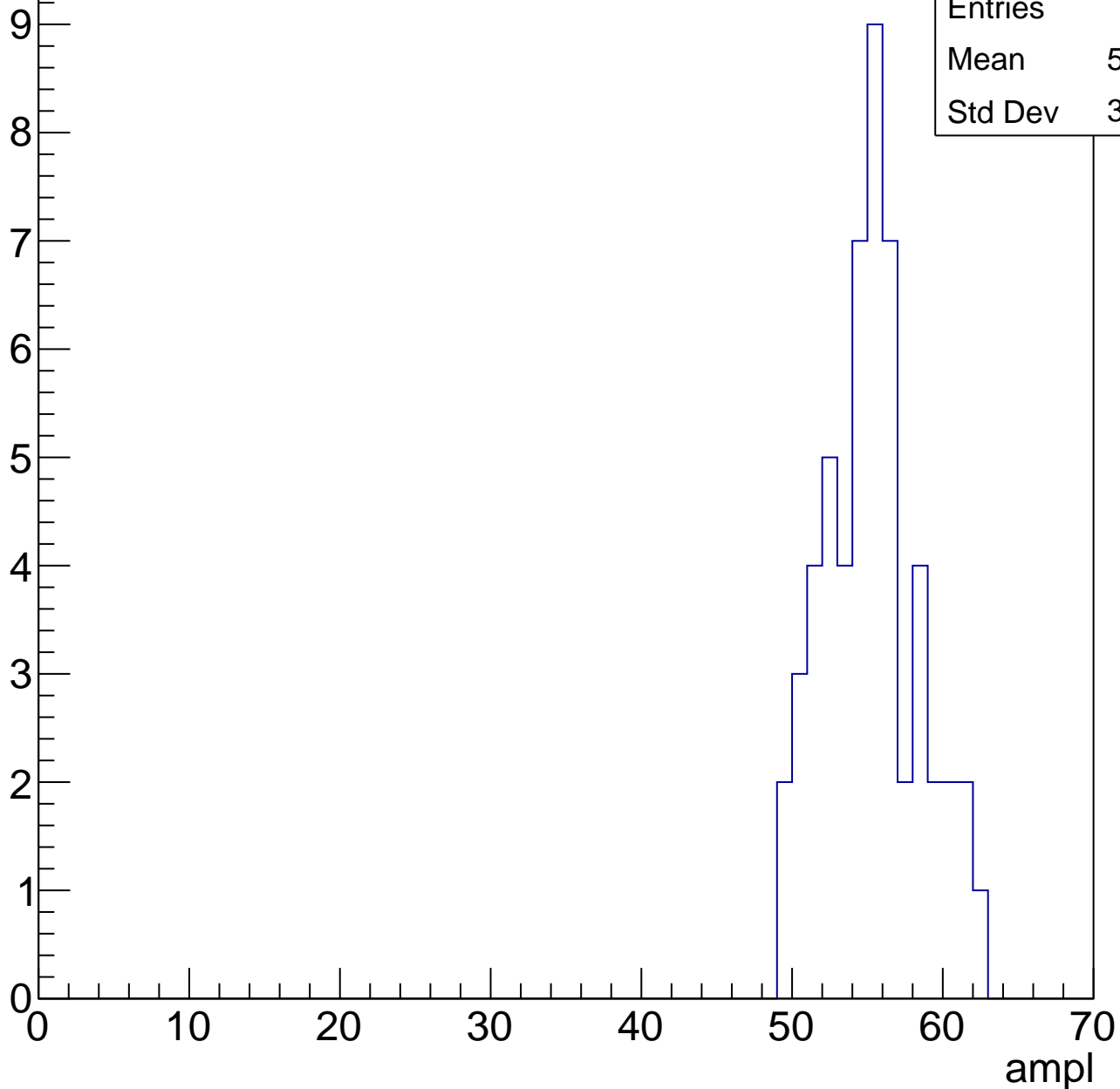
70



B1L103S, U13-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



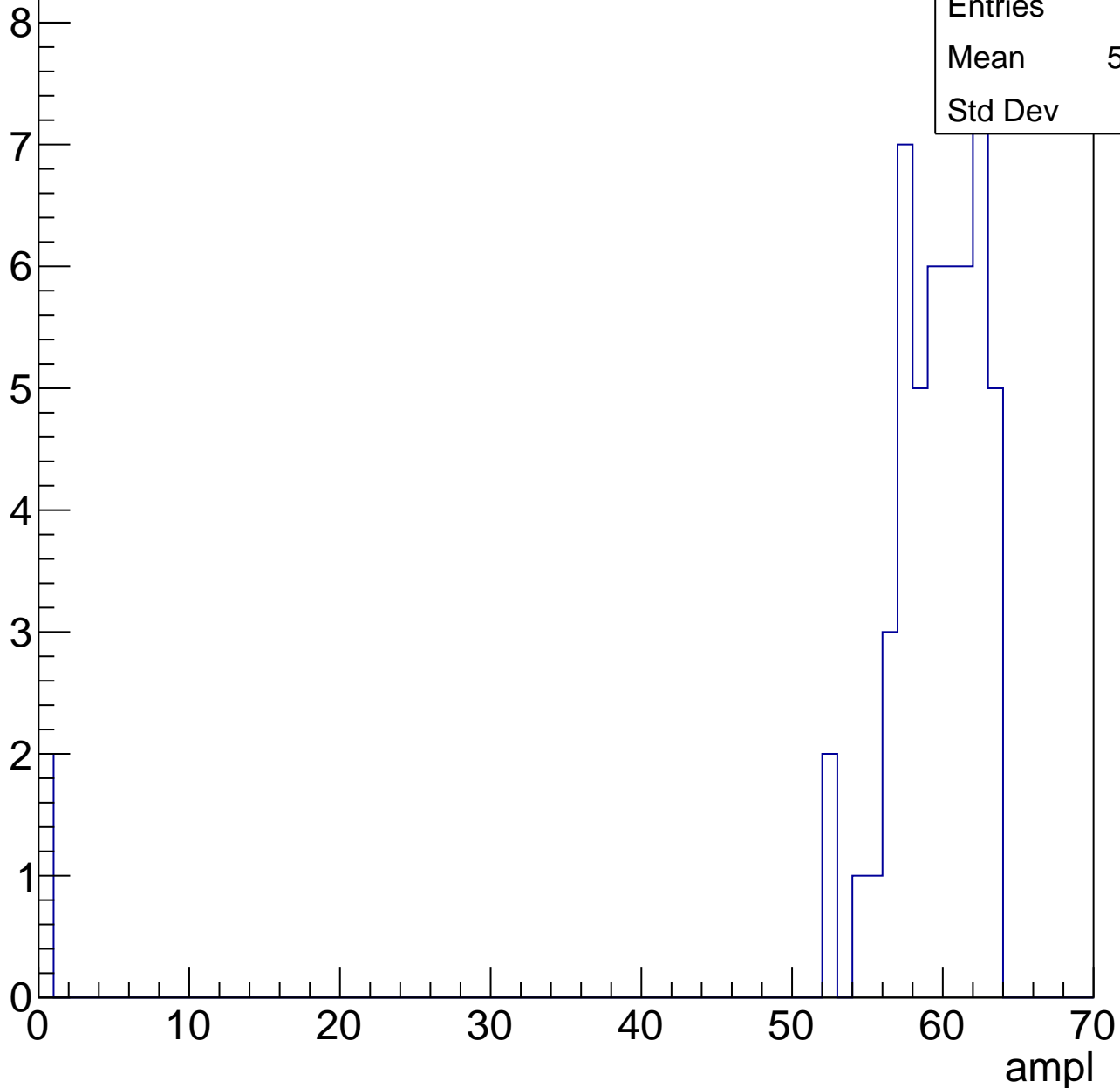
Entries	54
Mean	54.76
Std Dev	3.132

B1L103S, U13-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	56.94
Std Dev	11.7

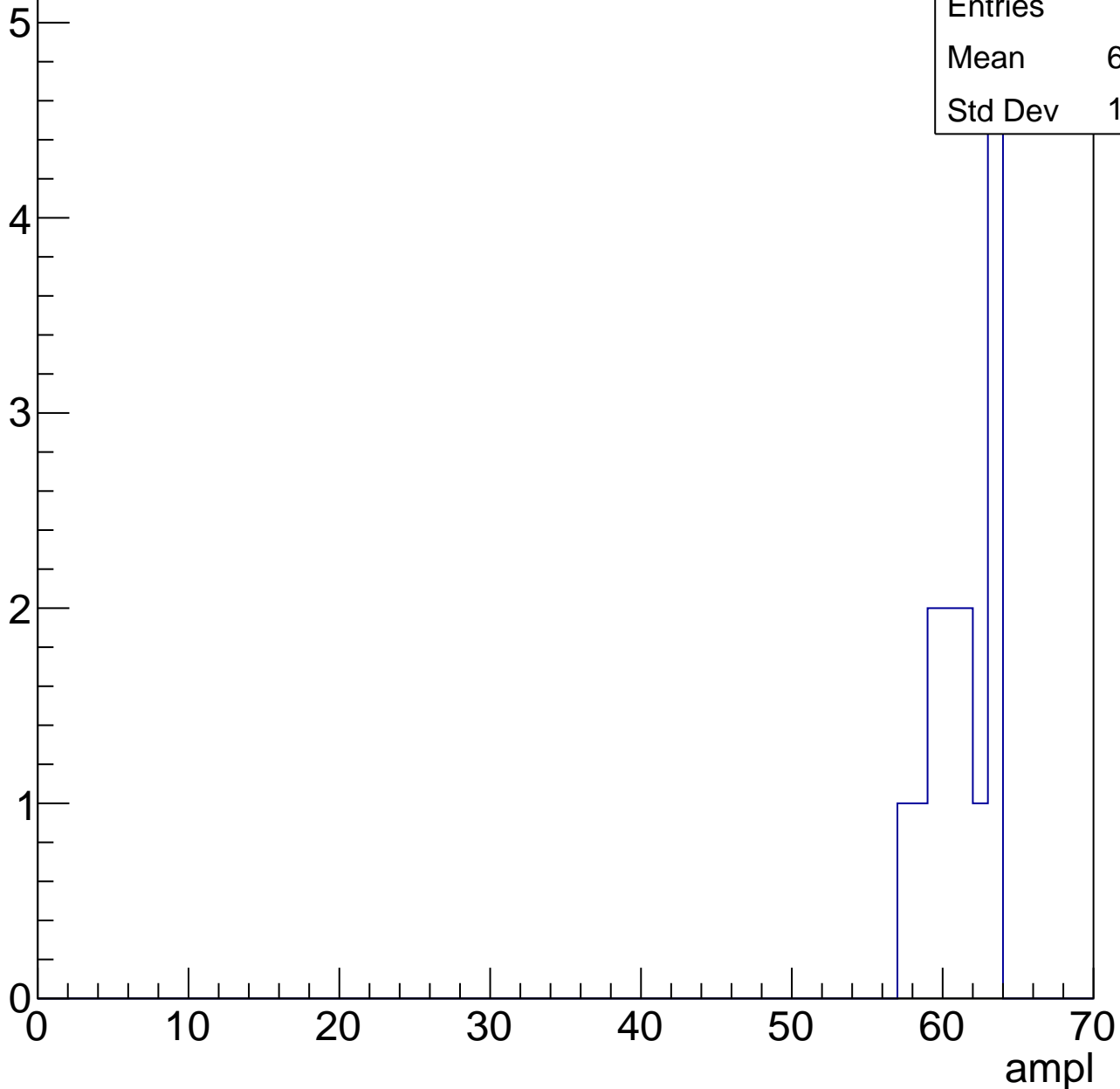


B1L103S, U13-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	60.86
Std Dev	1.995

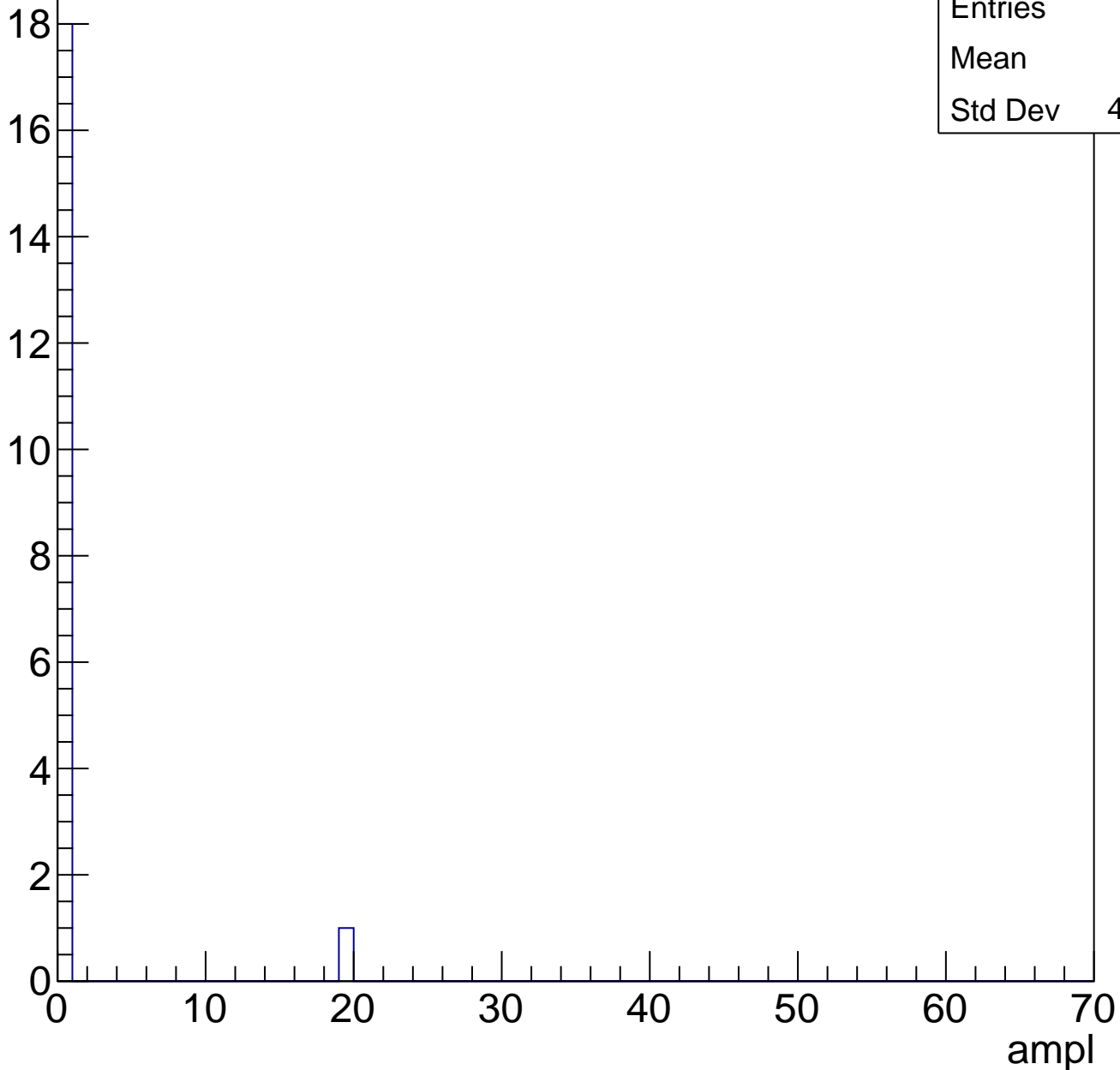


B1L103S, U13-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1
Std Dev	4.243

Entry



B1L103S, U13-ch87, adc0

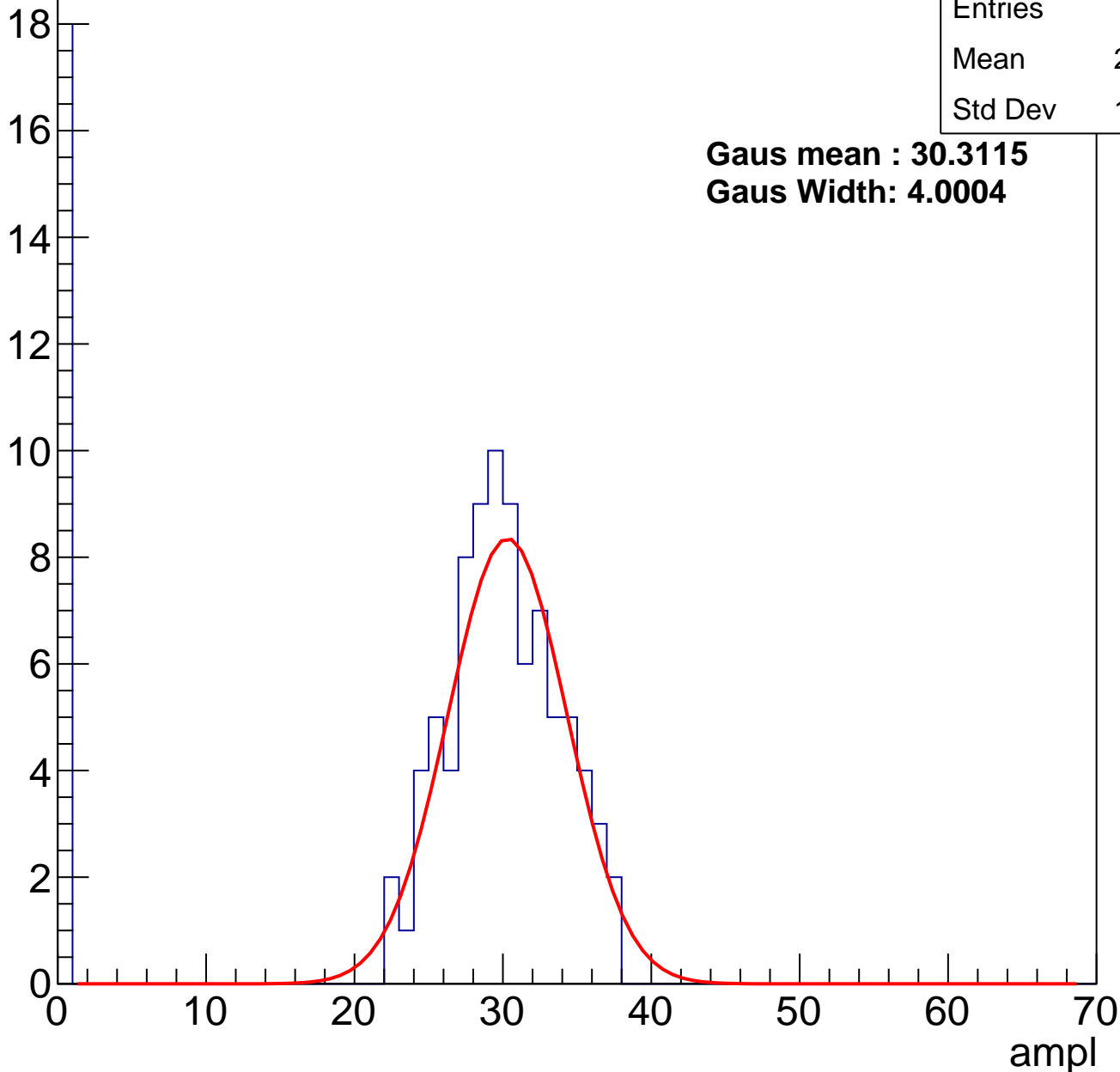
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	24.38
Std Dev	11.75

Gaus mean : 30.3115

Gaus Width: 4.0004

Entry

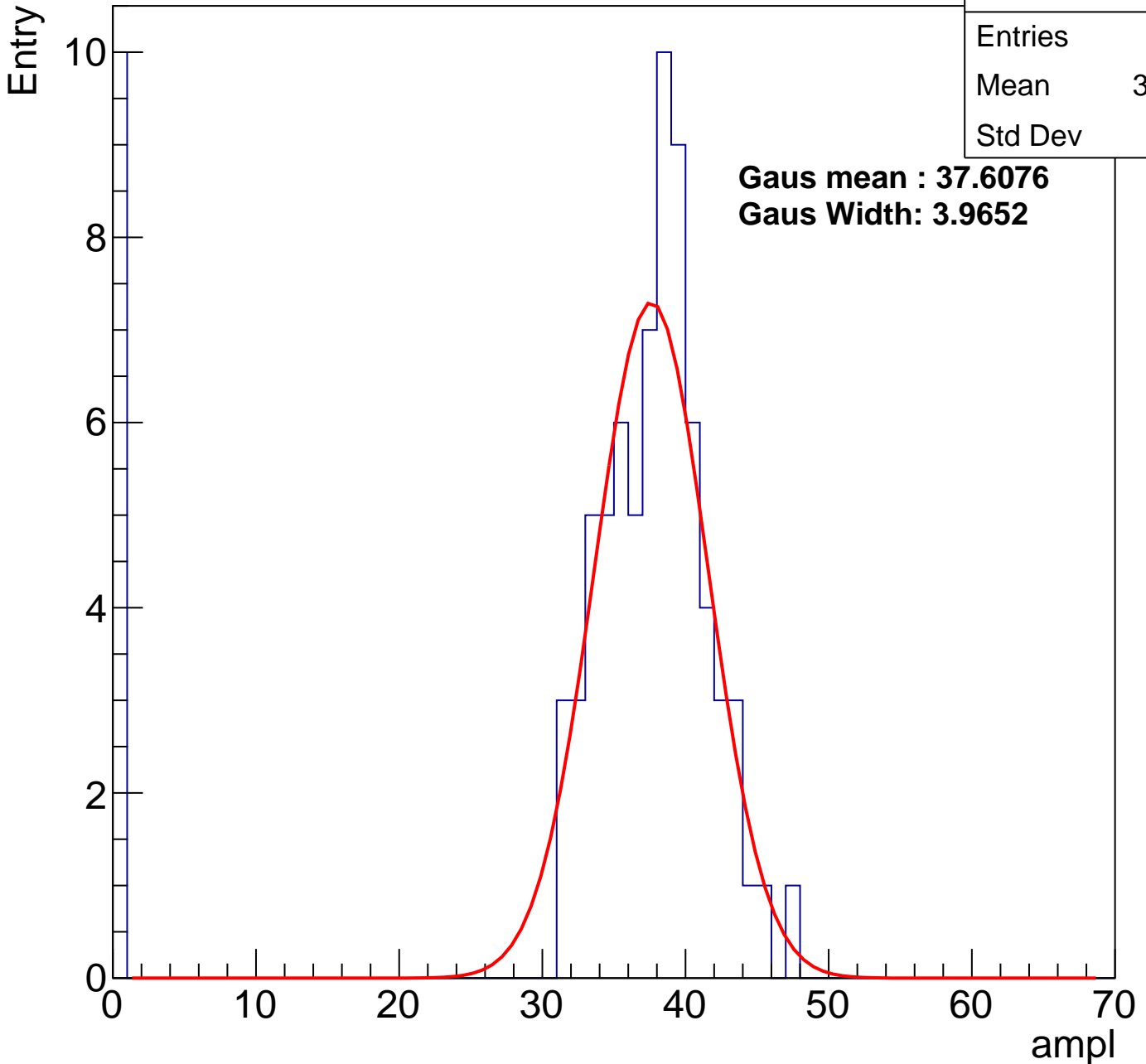


B1L103S, U13-ch87, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	32.91
Std Dev	12.7

Gaus mean : 37.6076
Gaus Width: 3.9652



B1L103S, U13-ch87, adc2

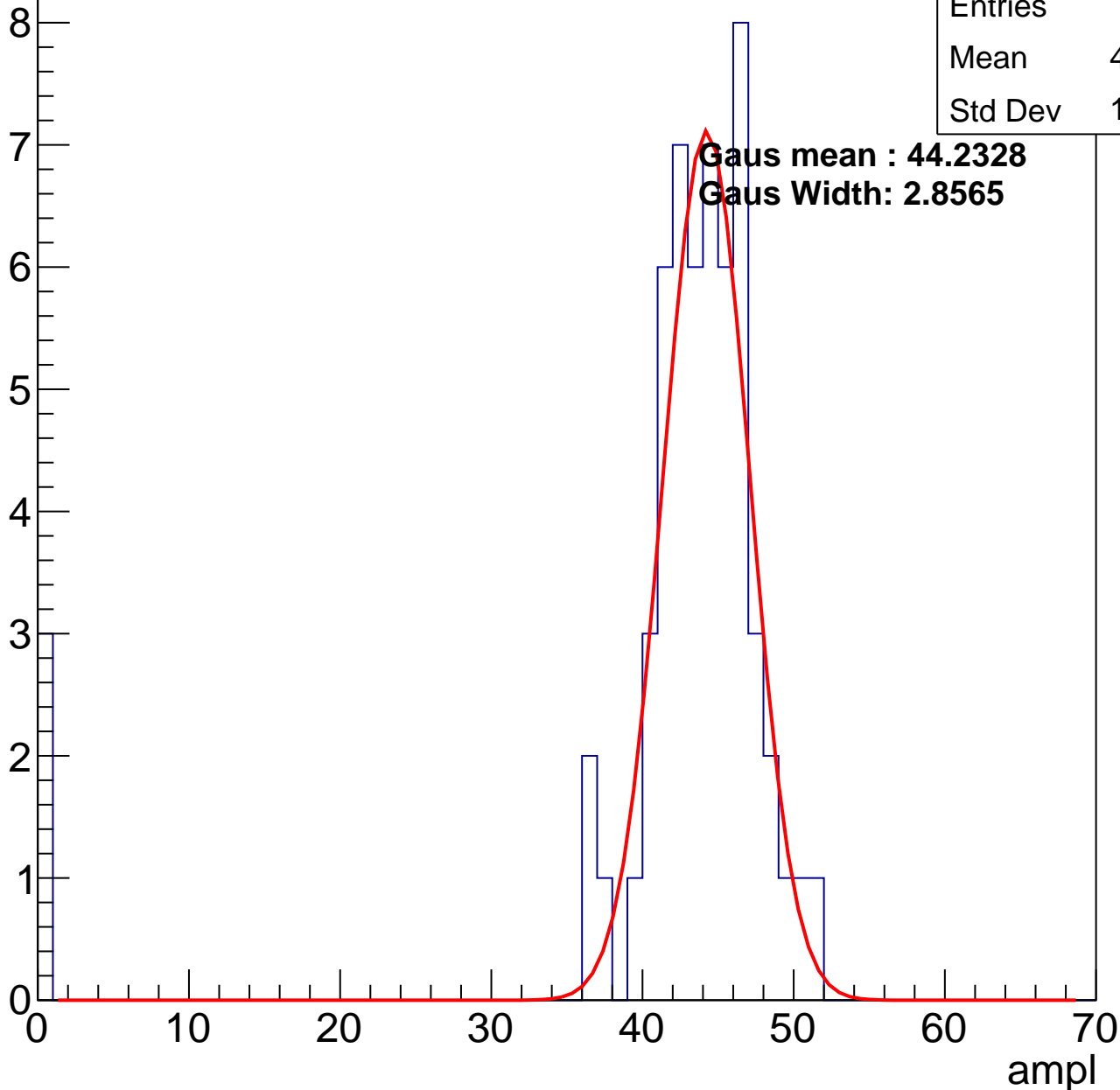
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	41.36
Std Dev	10.13

Gaus mean : 44.2328

Gaus Width: 2.8565

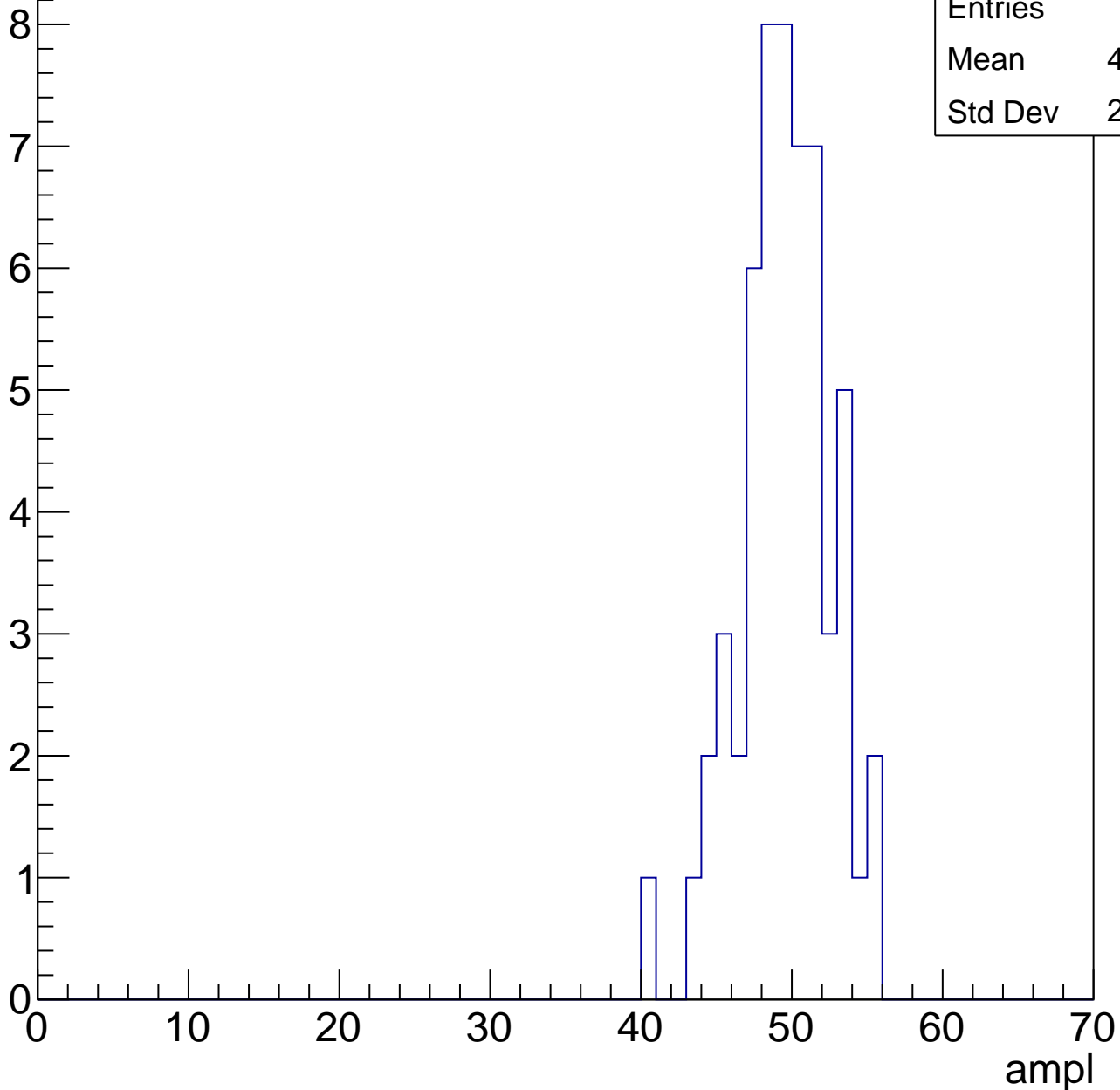


B1L103S, U13-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	49.07
Std Dev	2.999

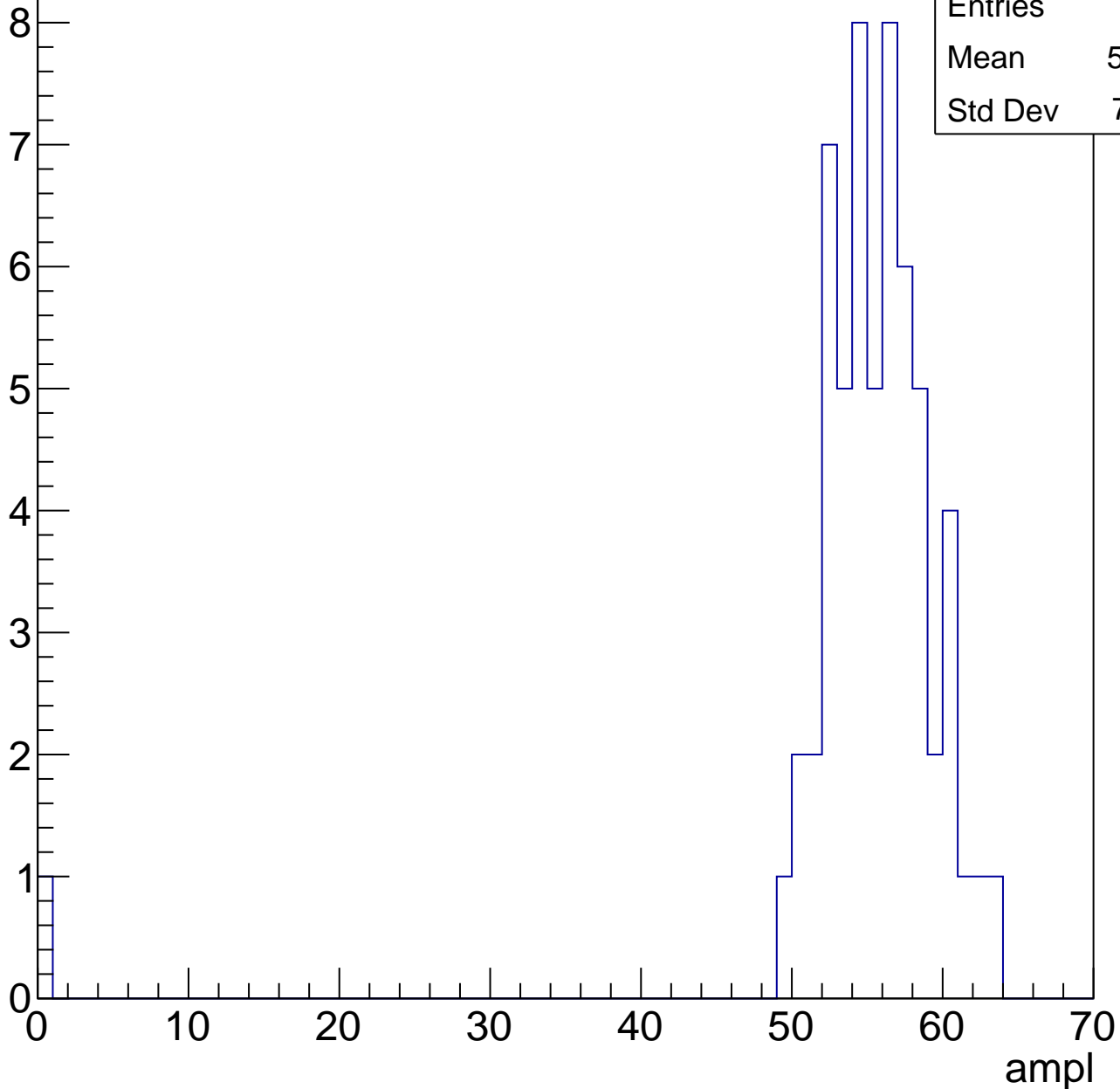


B1L103S, U13-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

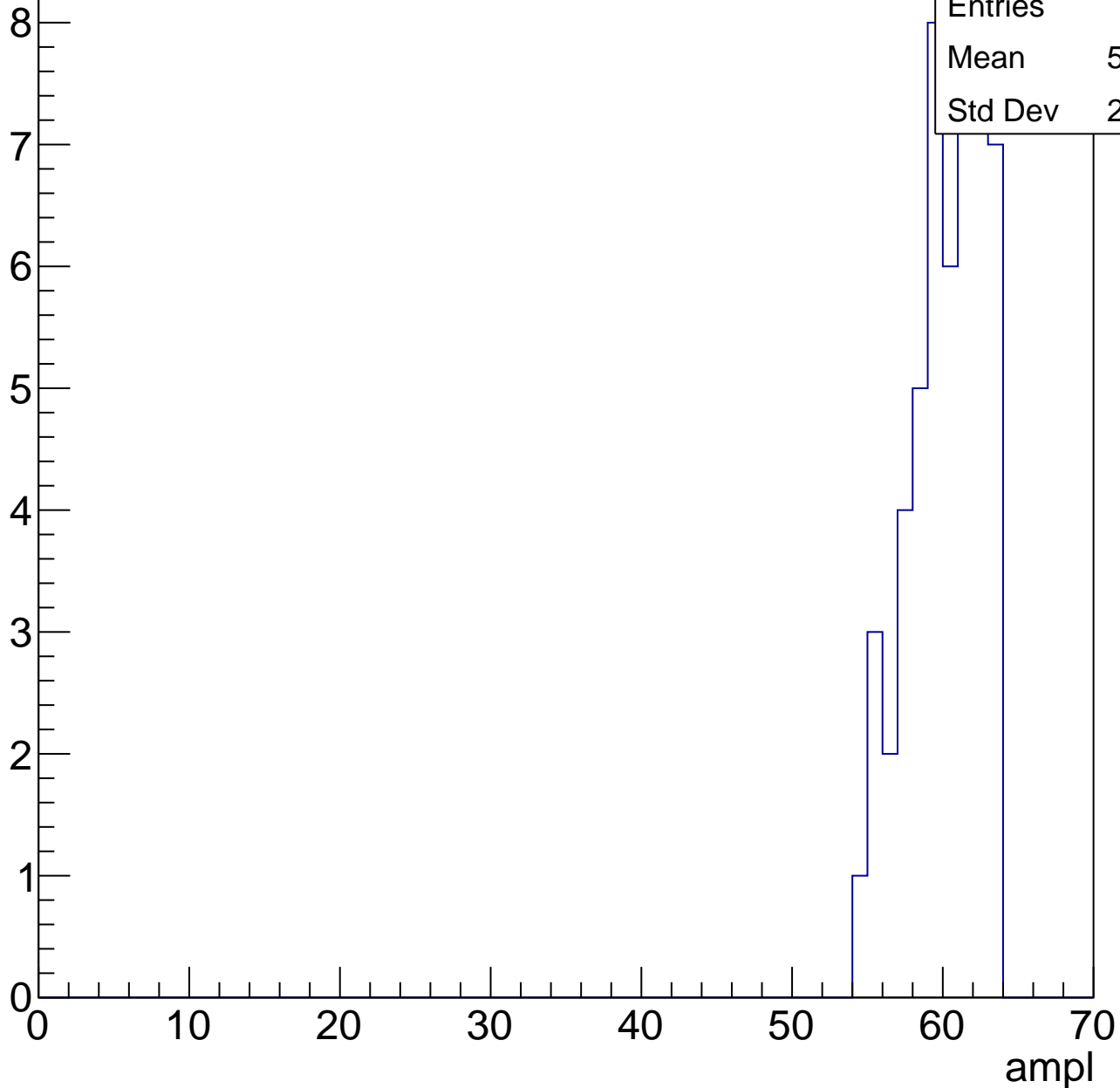
Entries	59
Mean	54.42
Std Dev	7.781



B1L103S, U13-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L103S, U13-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch88, adc0

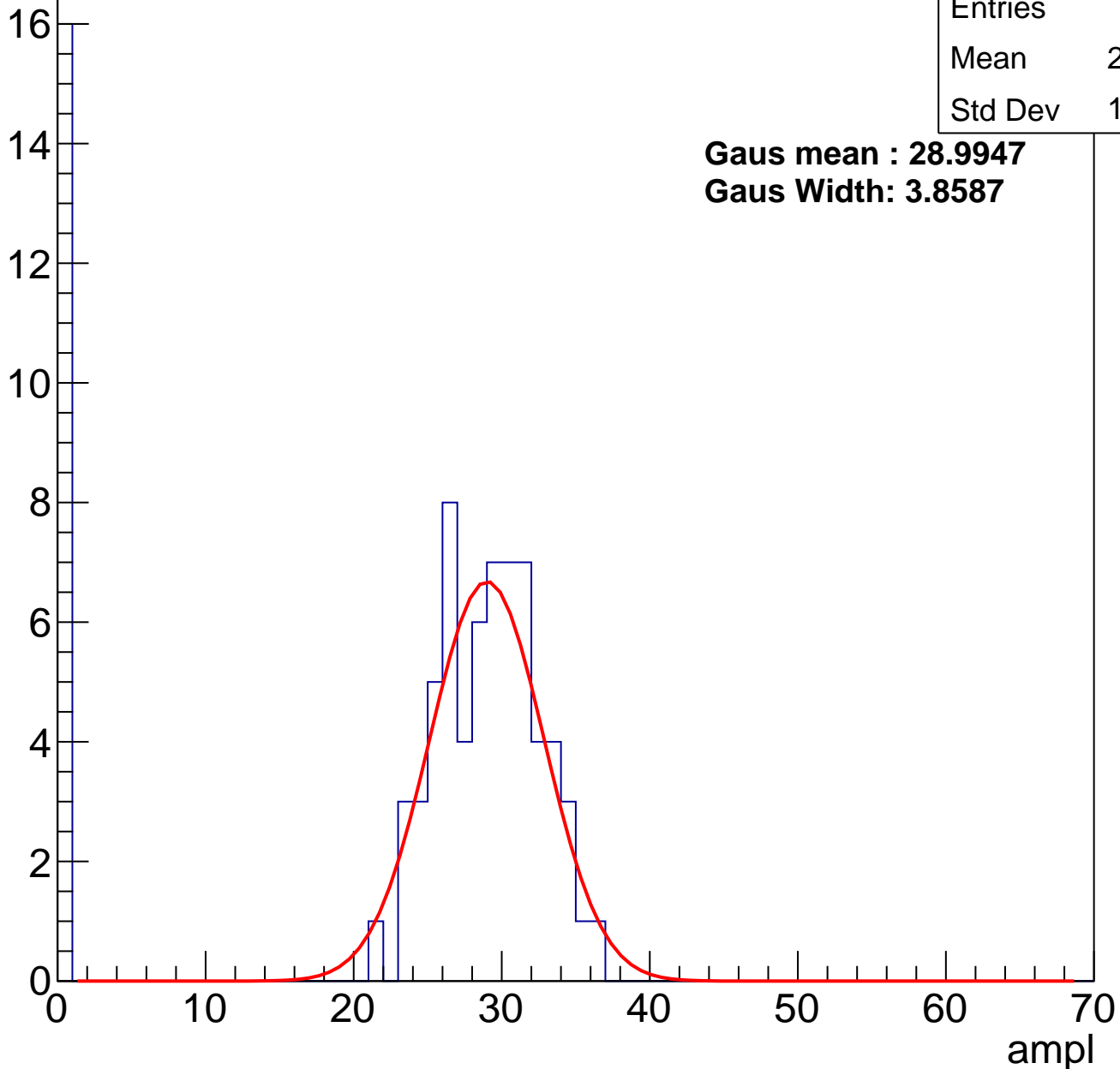
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	22.93
Std Dev	11.84

Gaus mean : 28.9947

Gaus Width: 3.8587

Entry



B1L103S, U13-ch88, adc1

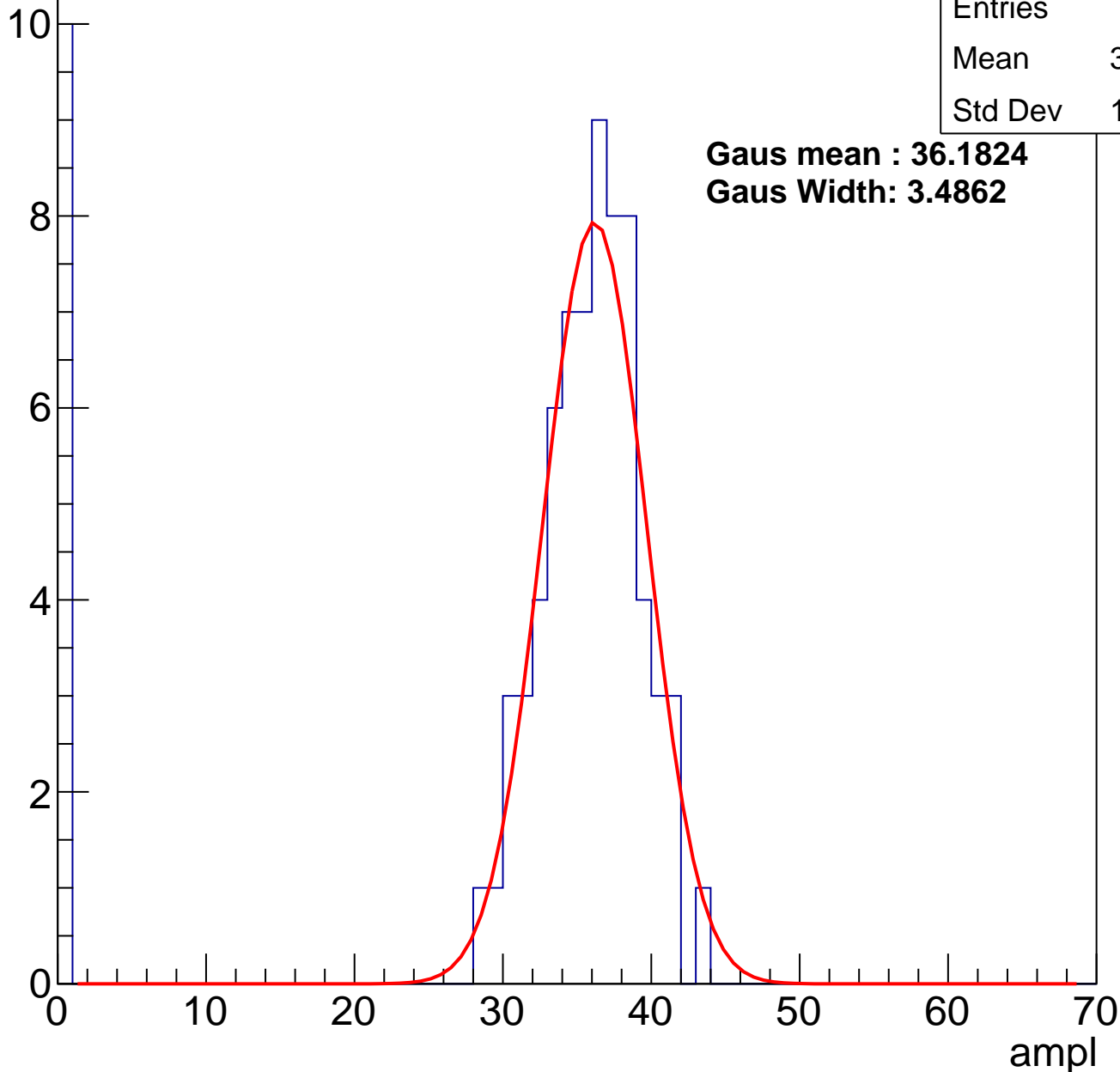
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	30.96
Std Dev	12.24

Gaus mean : 36.1824

Gaus Width: 3.4862

Entry



B1L103S, U13-ch88, adc2

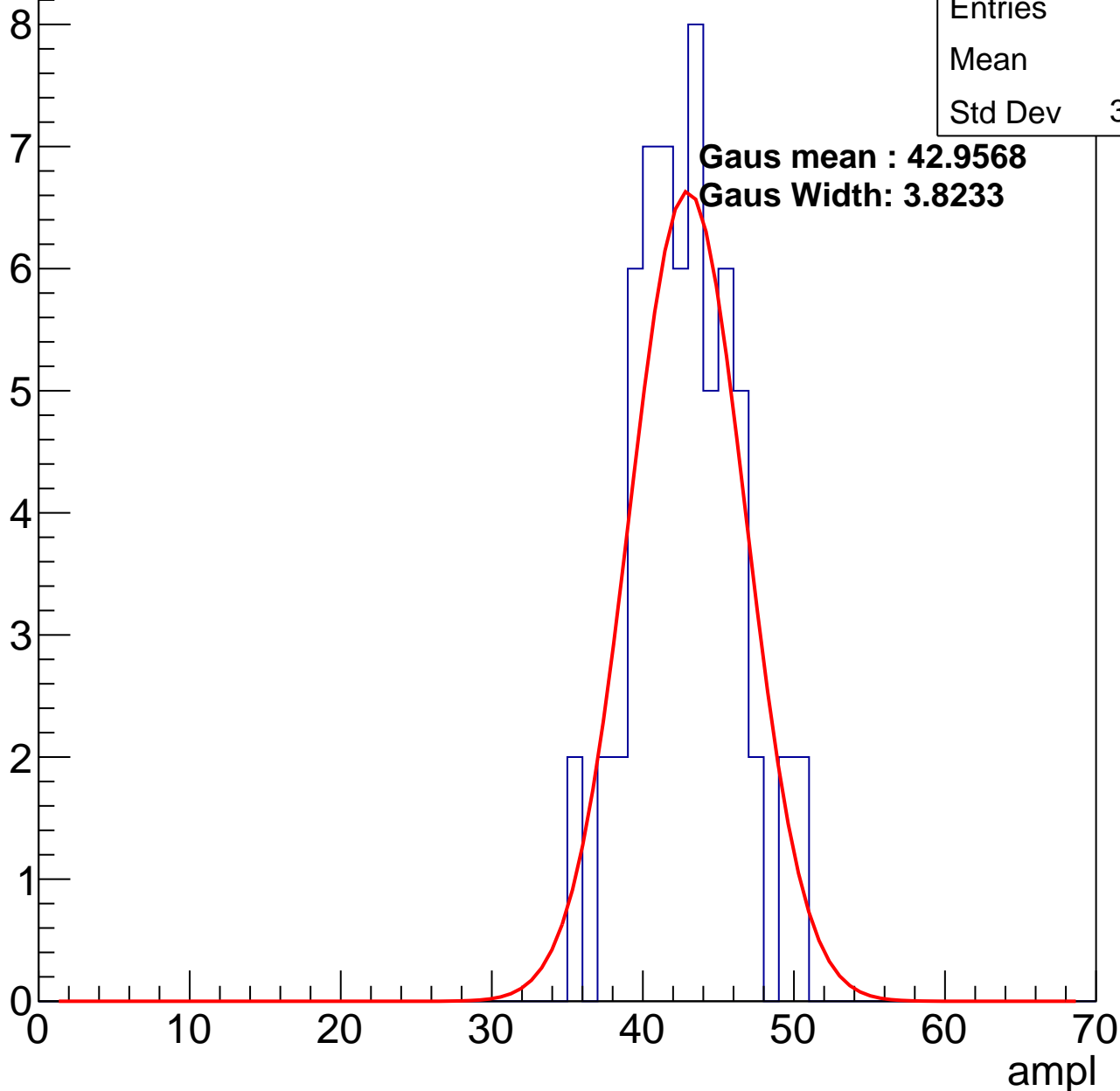
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	42.4
Std Dev	3.353

Gaus mean : 42.9568

Gaus Width: 3.8233

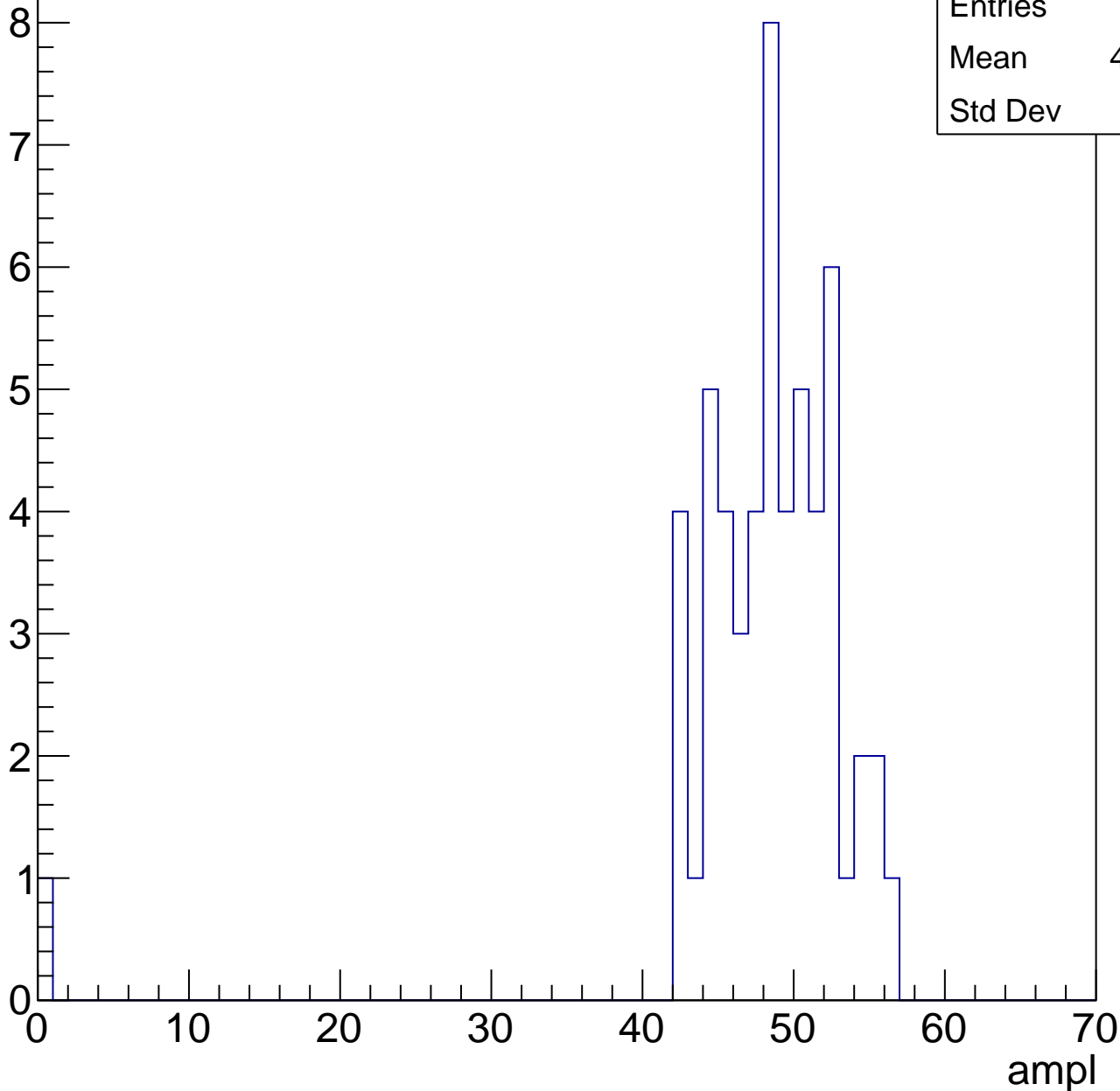


B1L103S, U13-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	47.45
Std Dev	7.39

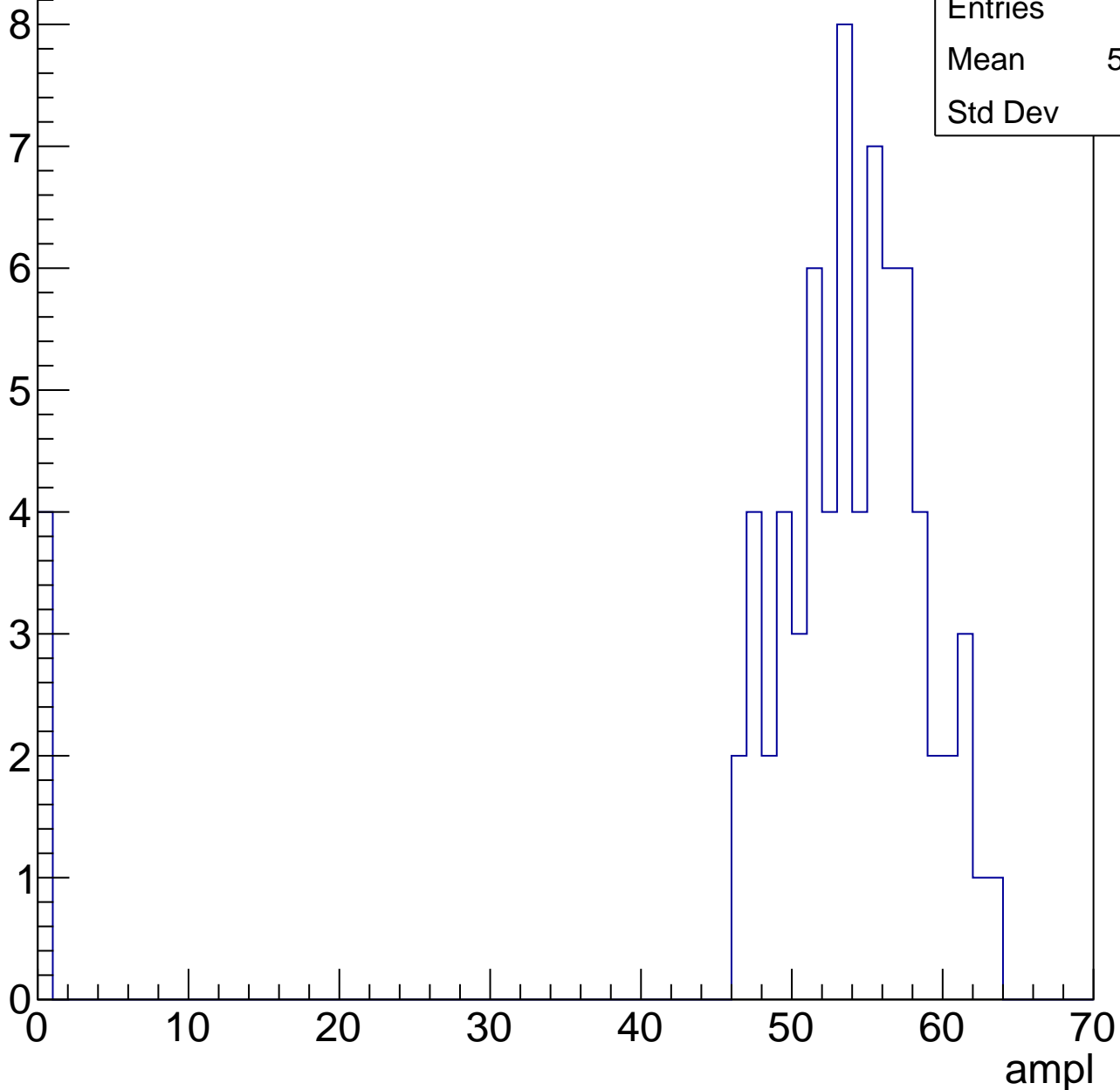


B1L103S, U13-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	50.92
Std Dev	12.9

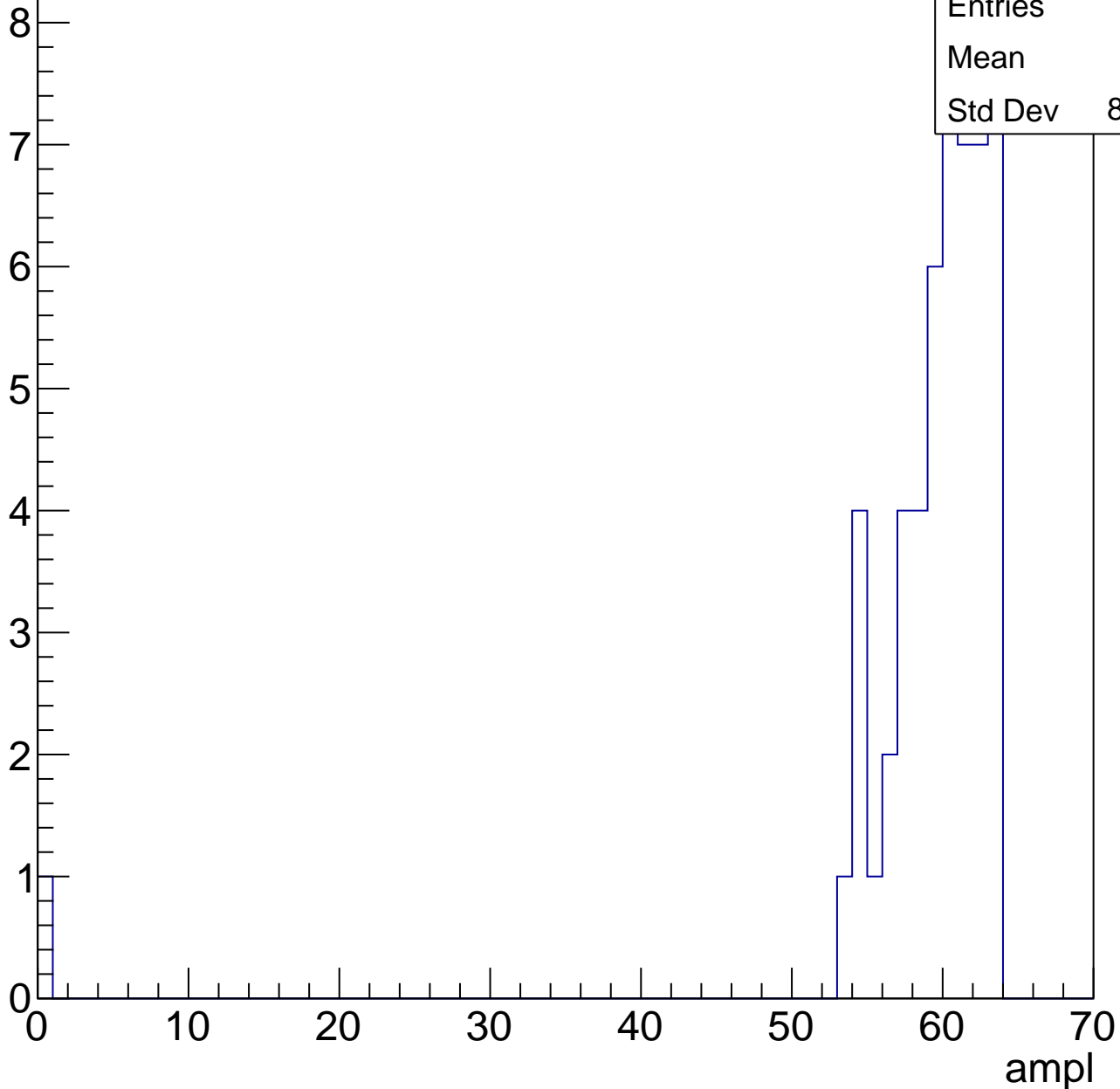


B1L103S, U13-ch88, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.4
Std Dev	8.555



B1L103S, U13-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

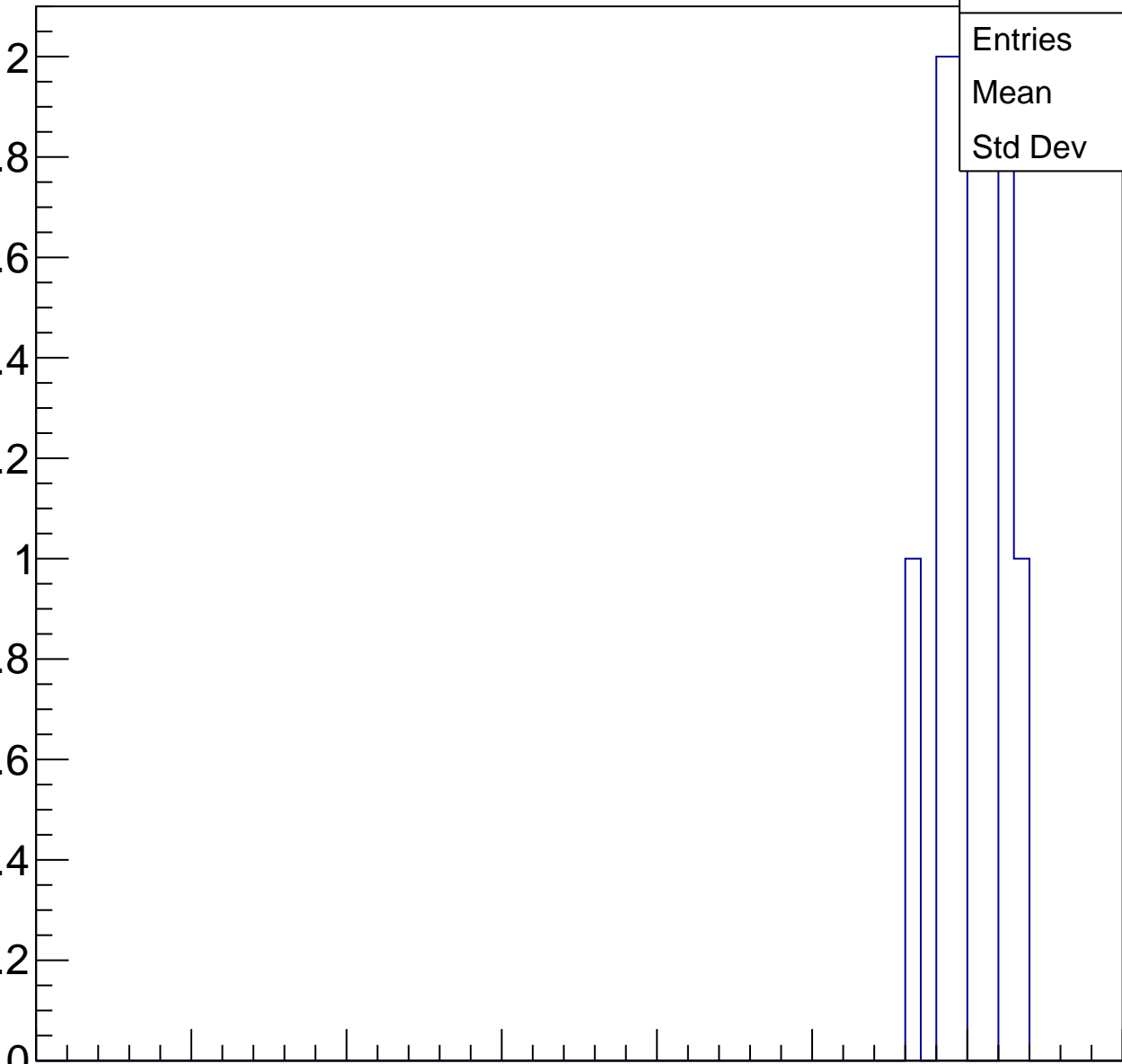
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	59.62
Std Dev	2.288

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch89, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.87
Std Dev	11.31

Gaus mean : 29.0973

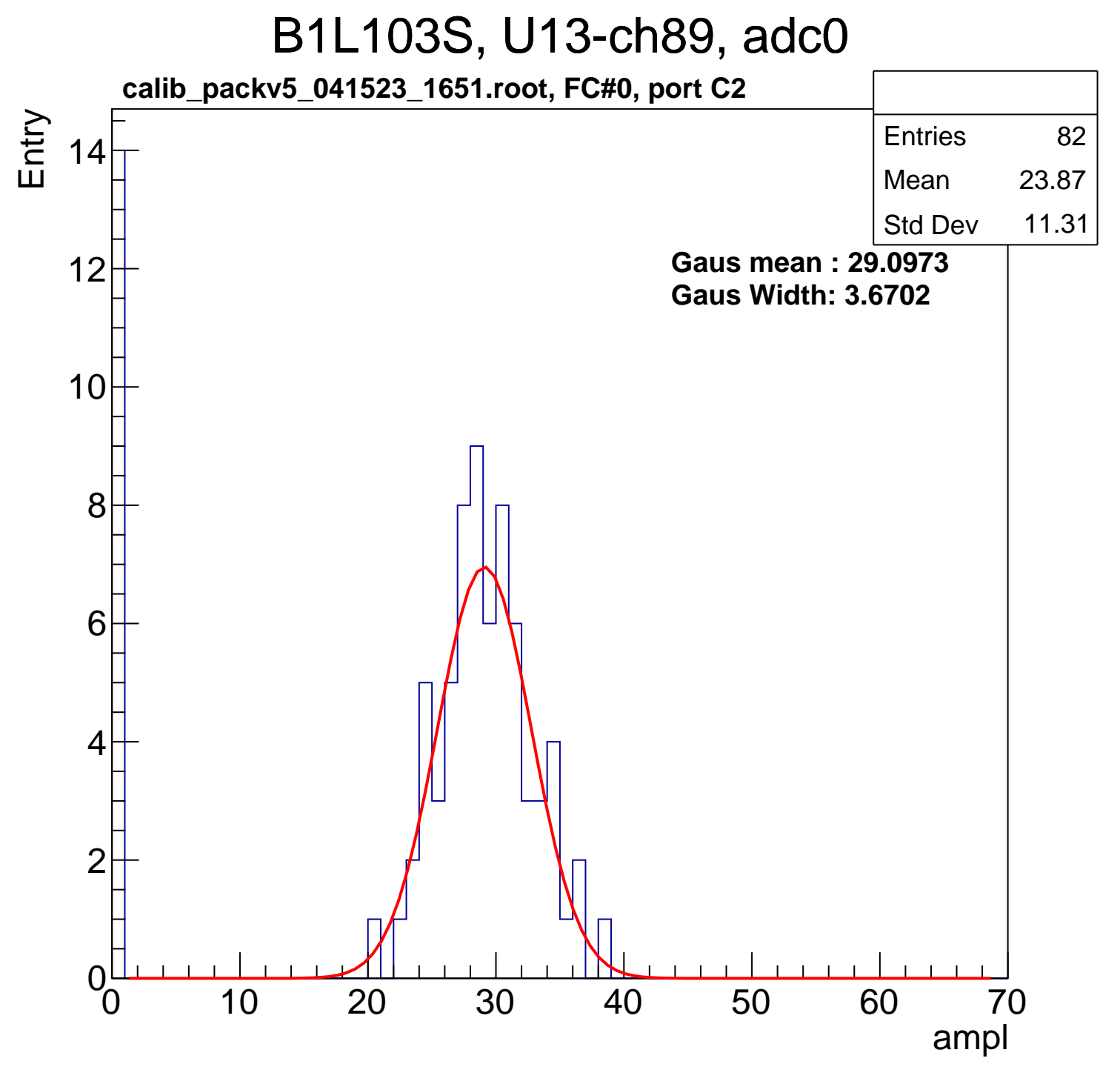
Gaus Width: 3.6702

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch89, adc1

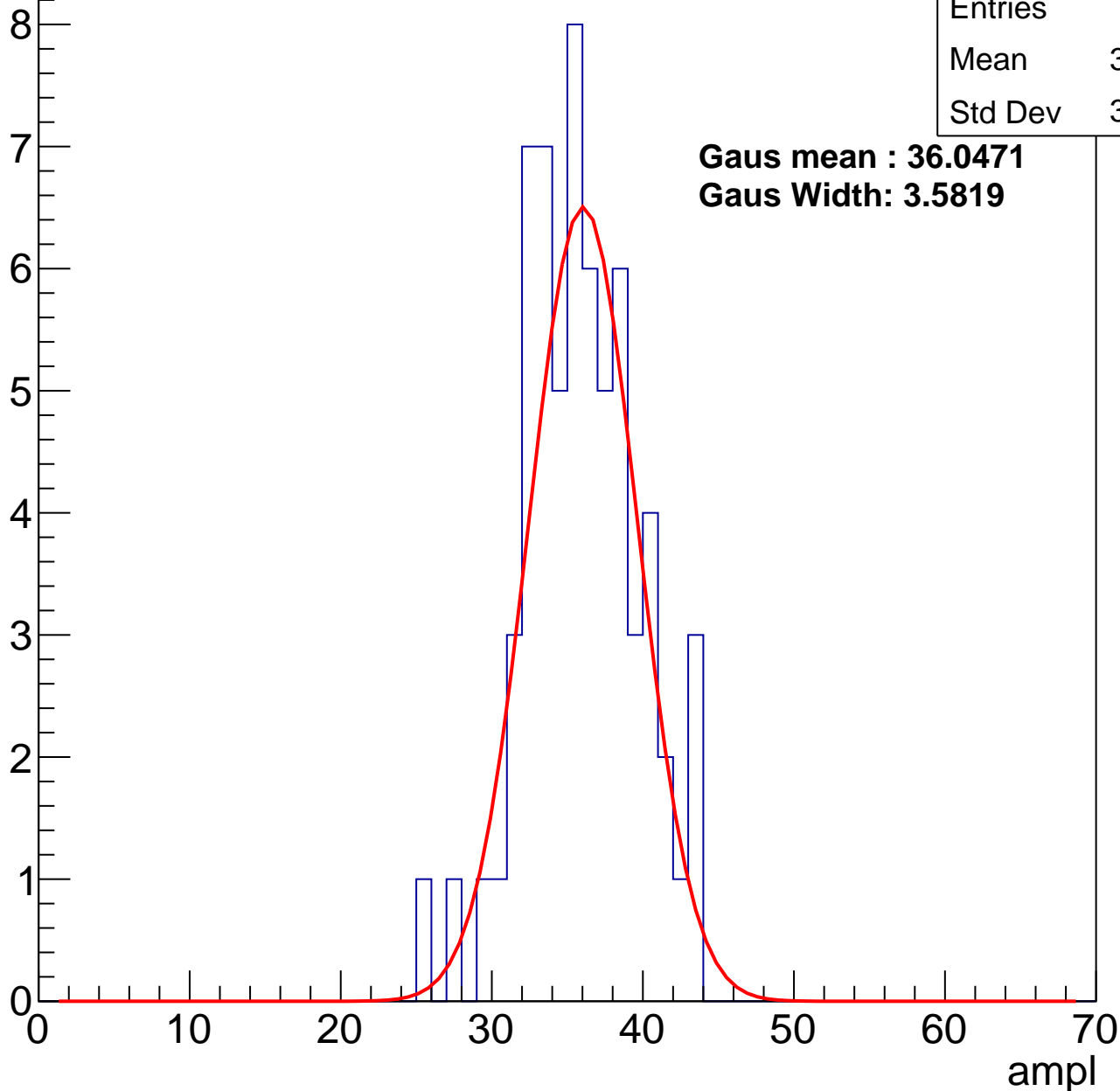
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	35.44
Std Dev	3.745

Gaus mean : 36.0471

Gaus Width: 3.5819



B1L103S, U13-ch89, adc2

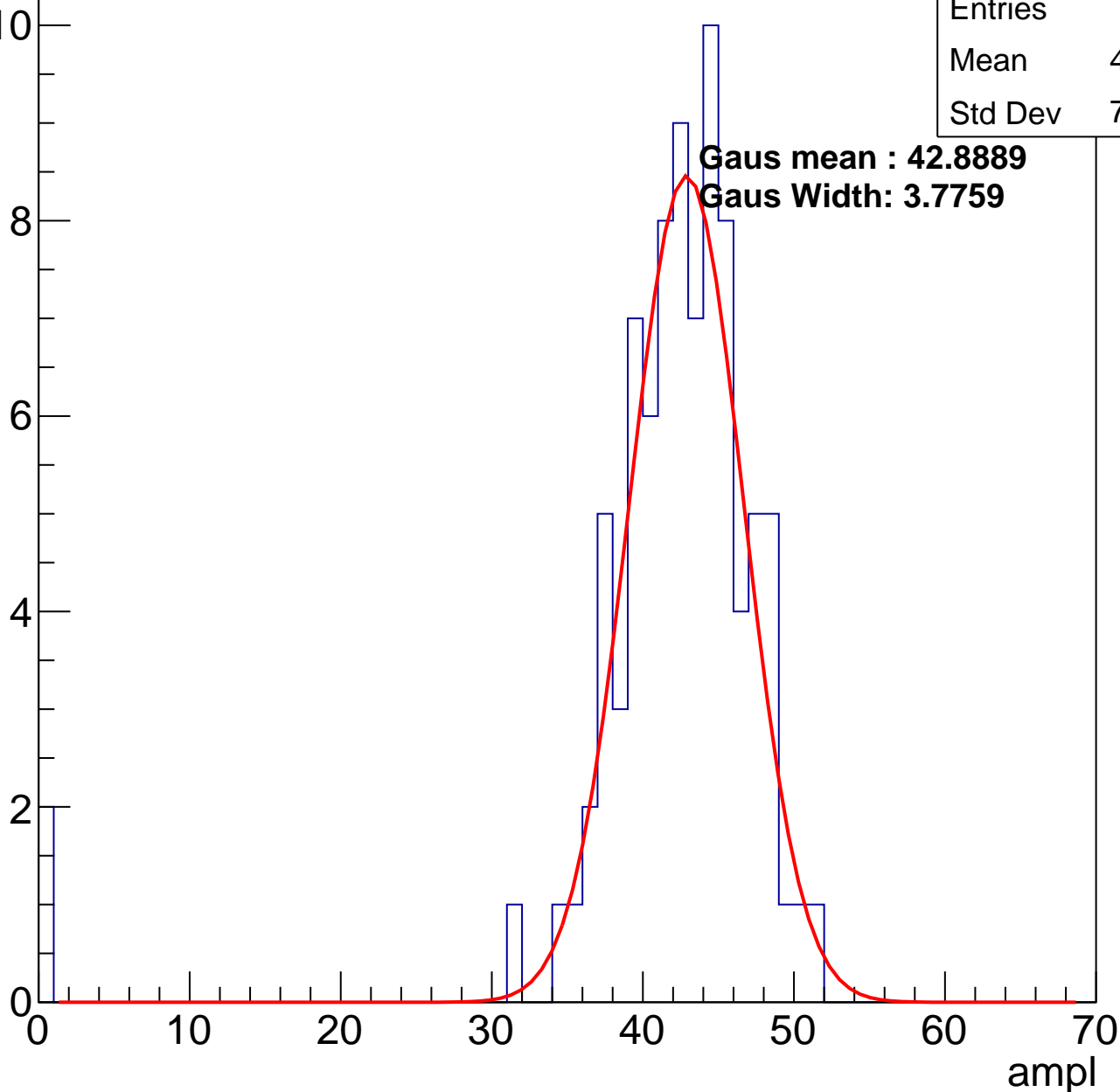
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	41.38
Std Dev	7.389

Gaus mean : 42.8889

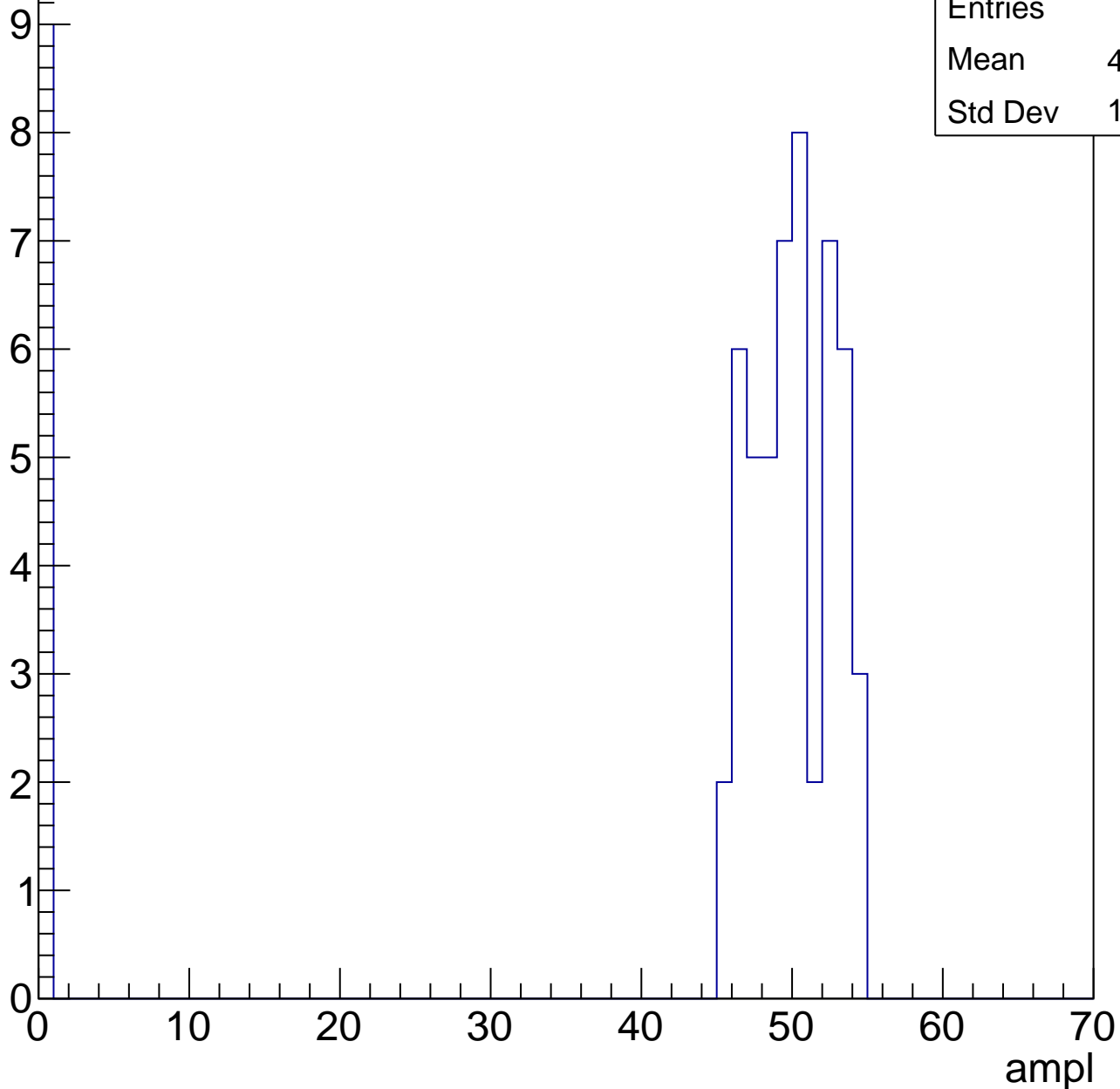
Gaus Width: 3.7759



B1L103S, U13-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

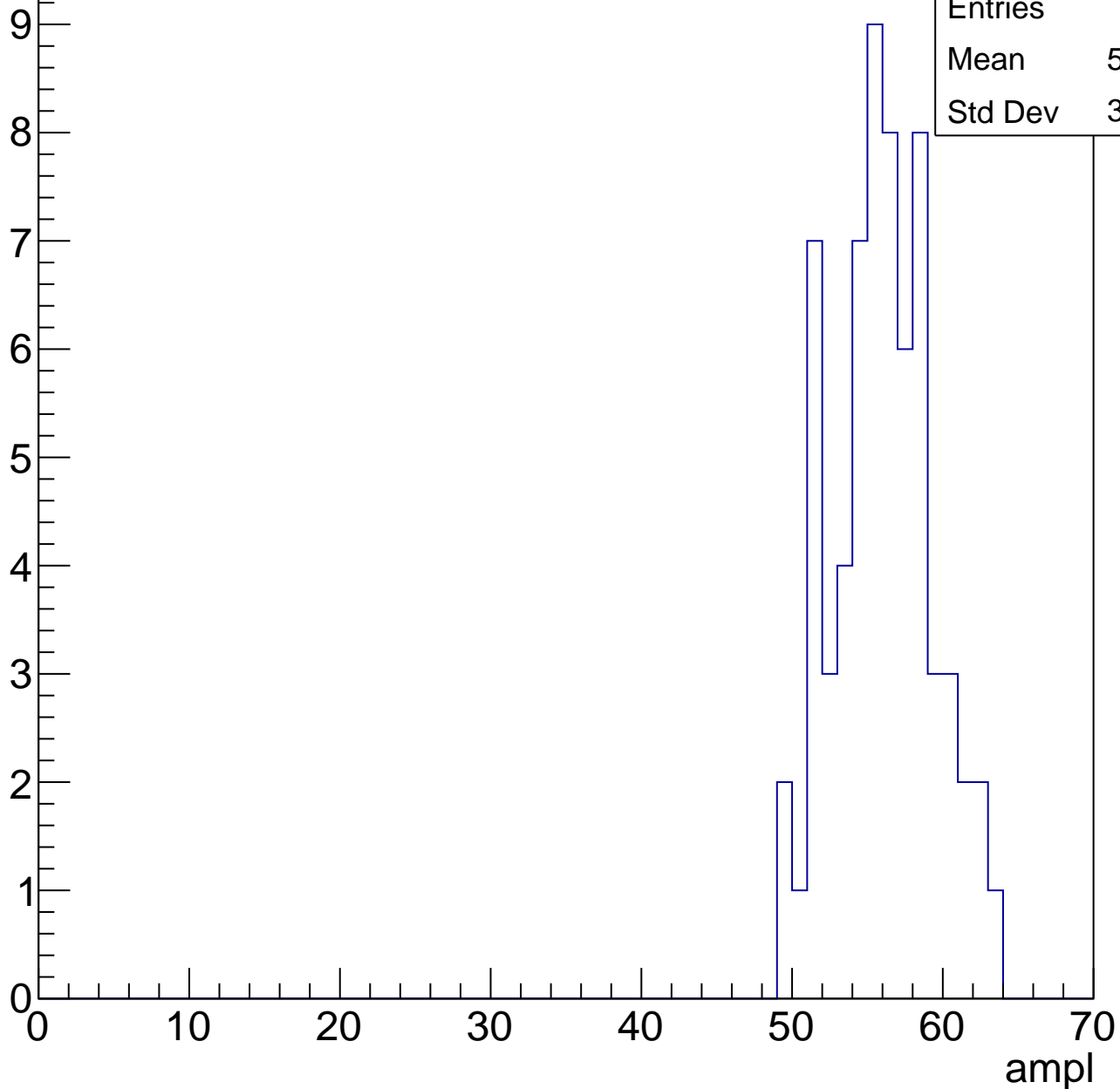
Entry



B1L103S, U13-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

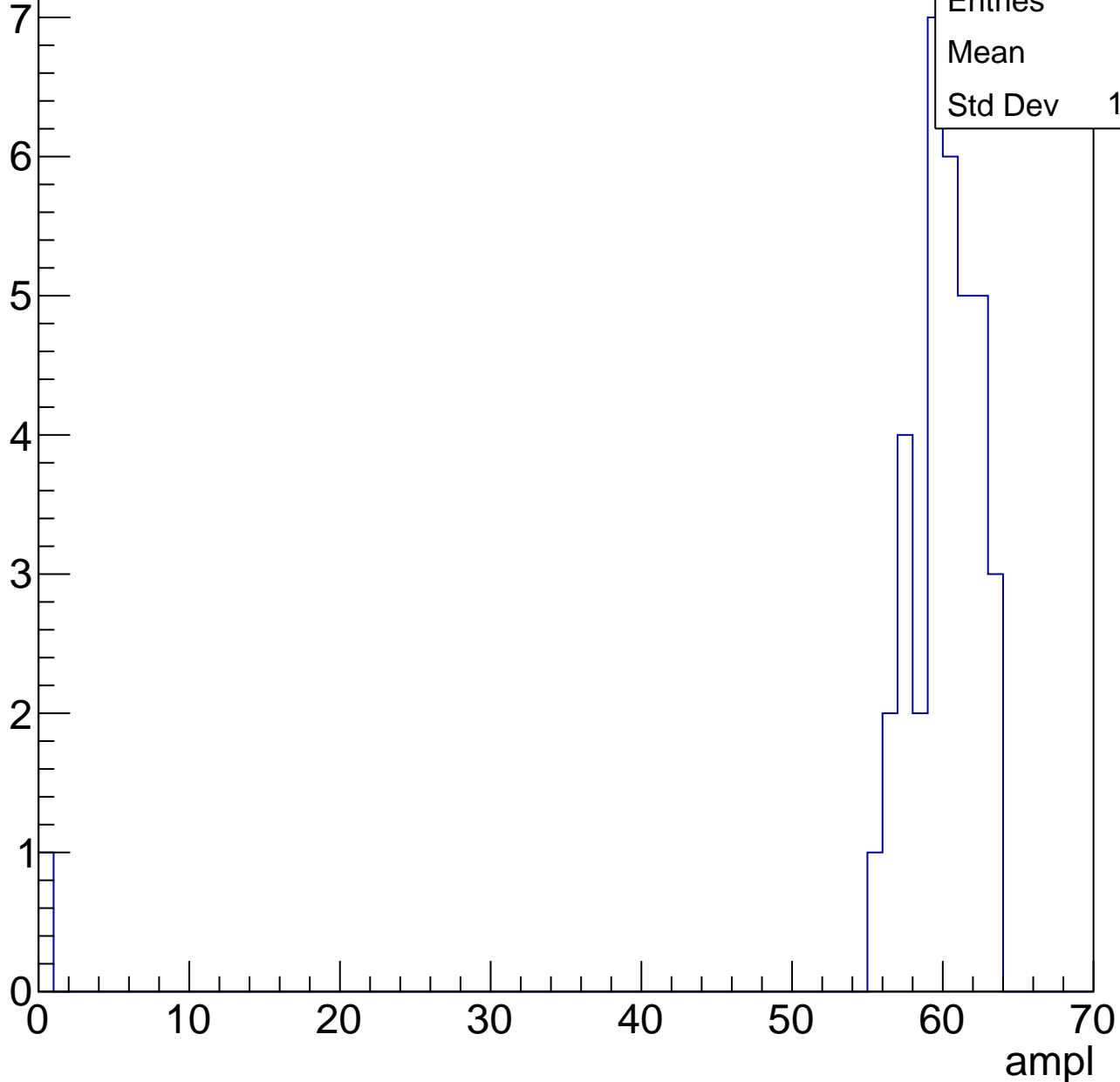


Entries	66
Mean	55.55
Std Dev	3.258

B1L103S, U13-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

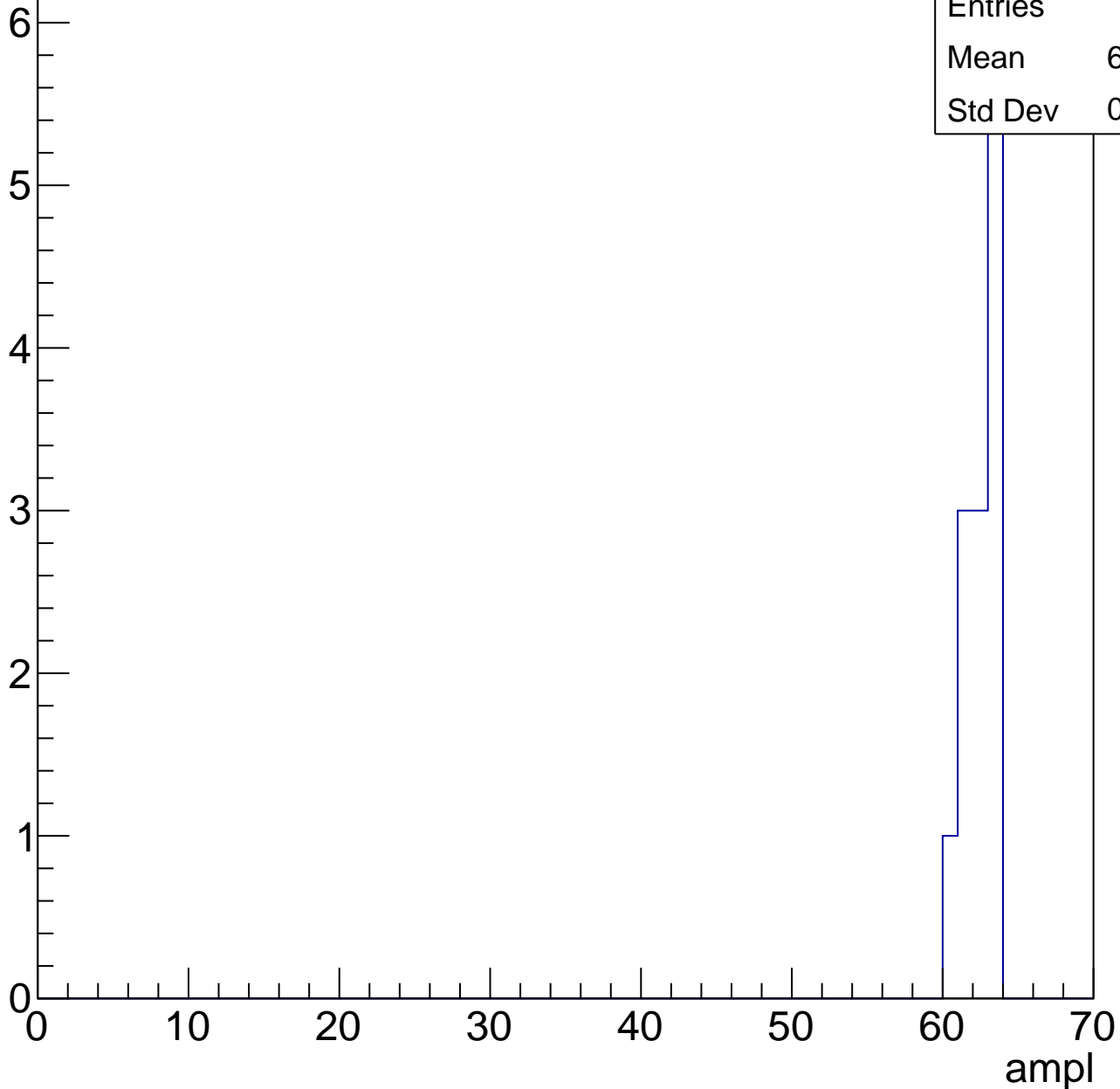


B1L103S, U13-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	62.08
Std Dev	0.997



B1L103S, U13-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



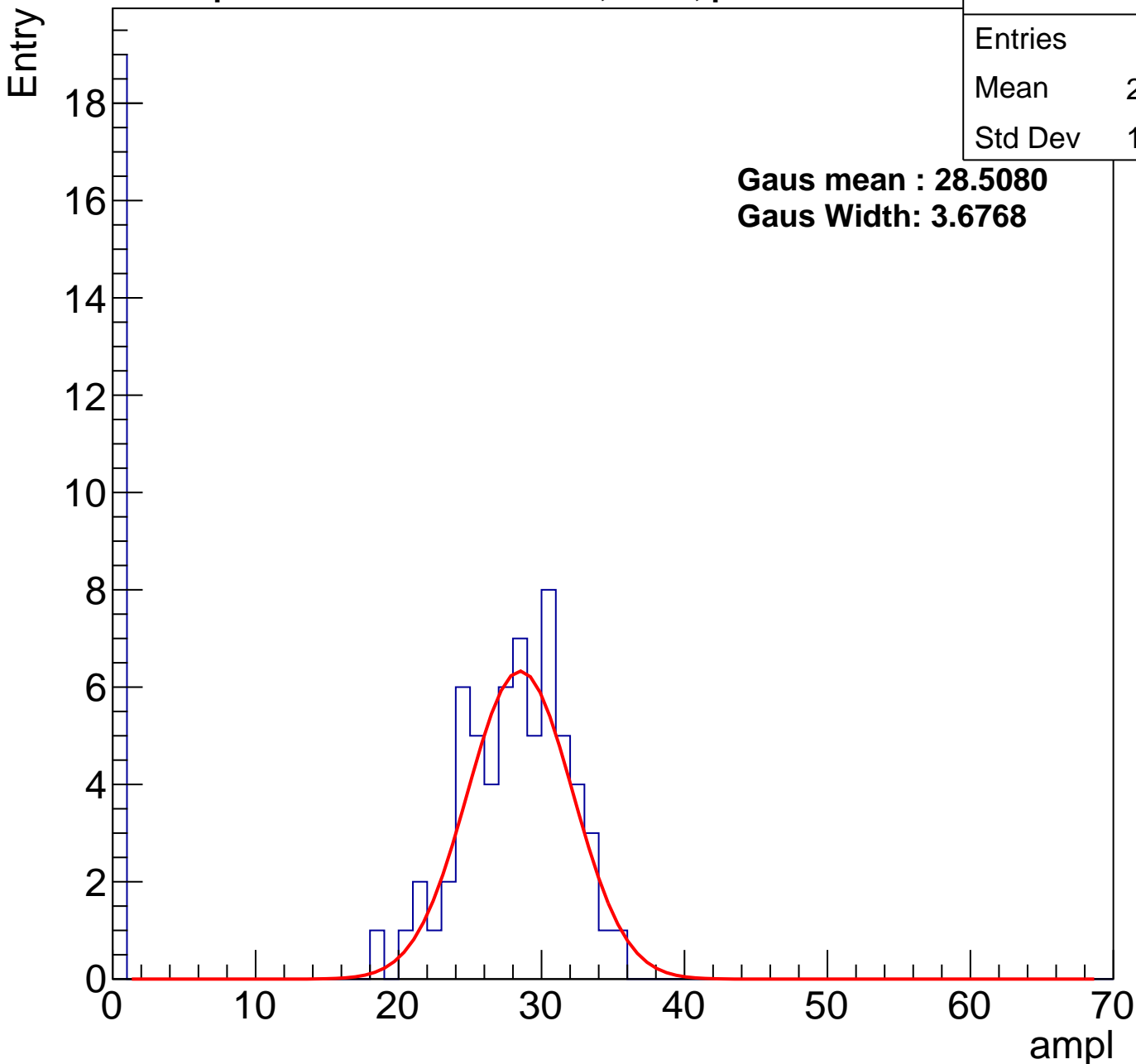
B1L103S, U13-ch90, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	21.17
Std Dev	12.14

Gaus mean : 28.5080

Gaus Width: 3.6768



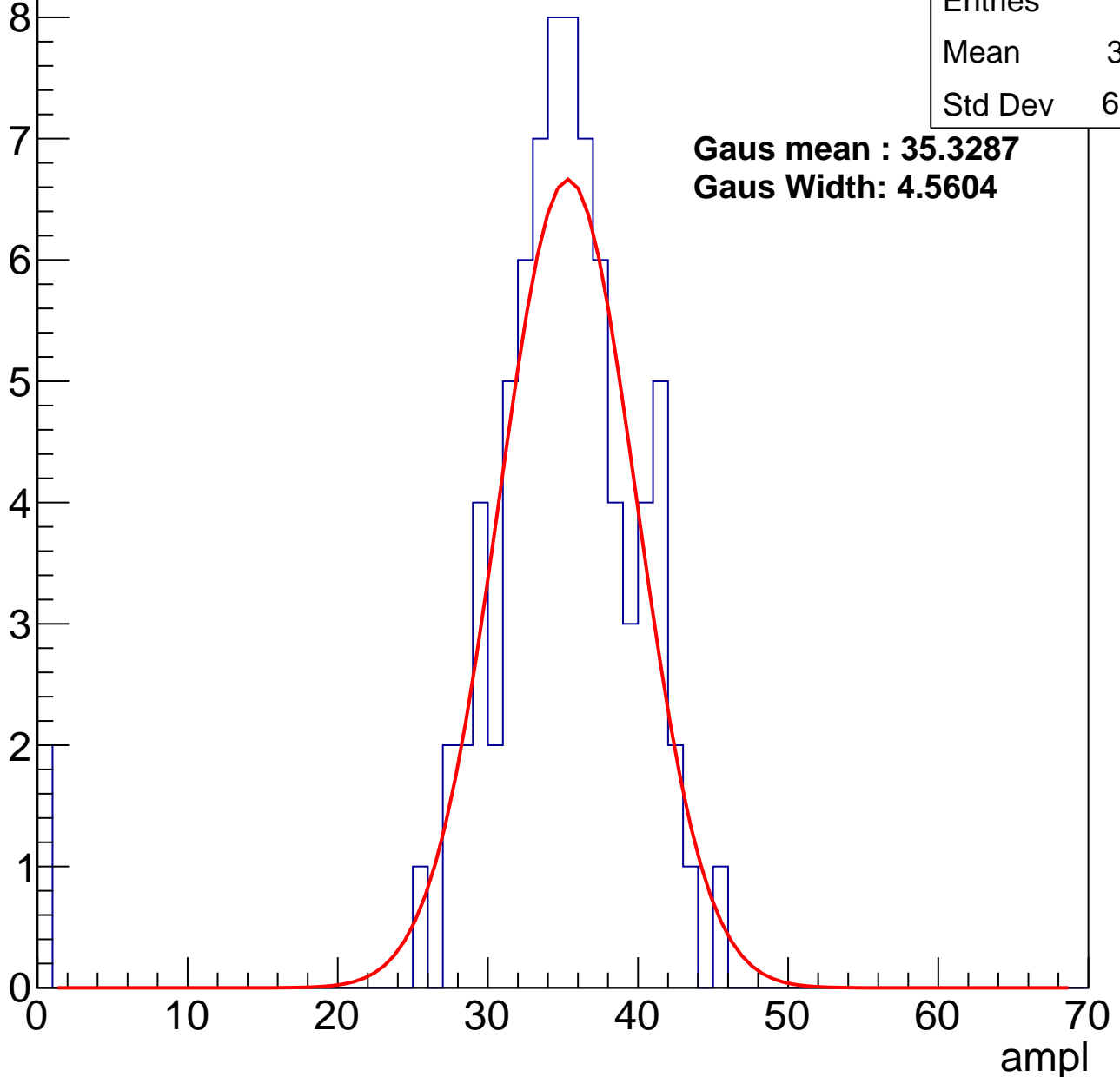
B1L103S, U13-ch90, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	34.01
Std Dev	6.818

Gaus mean : 35.3287
Gaus Width: 4.5604



B1L103S, U13-ch90, adc2

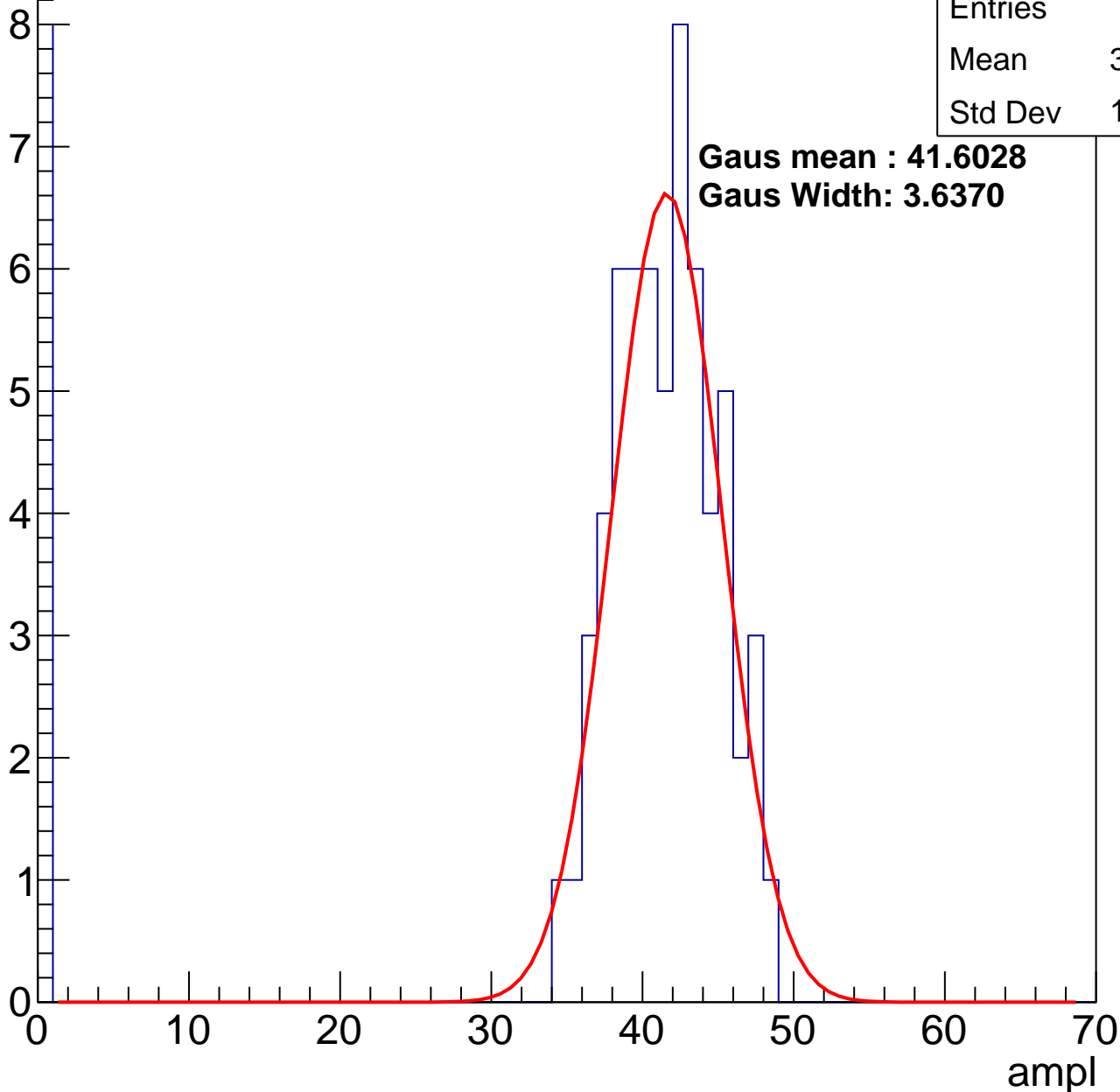
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	36.35
Std Dev	13.52

Gaus mean : 41.6028

Gaus Width: 3.6370

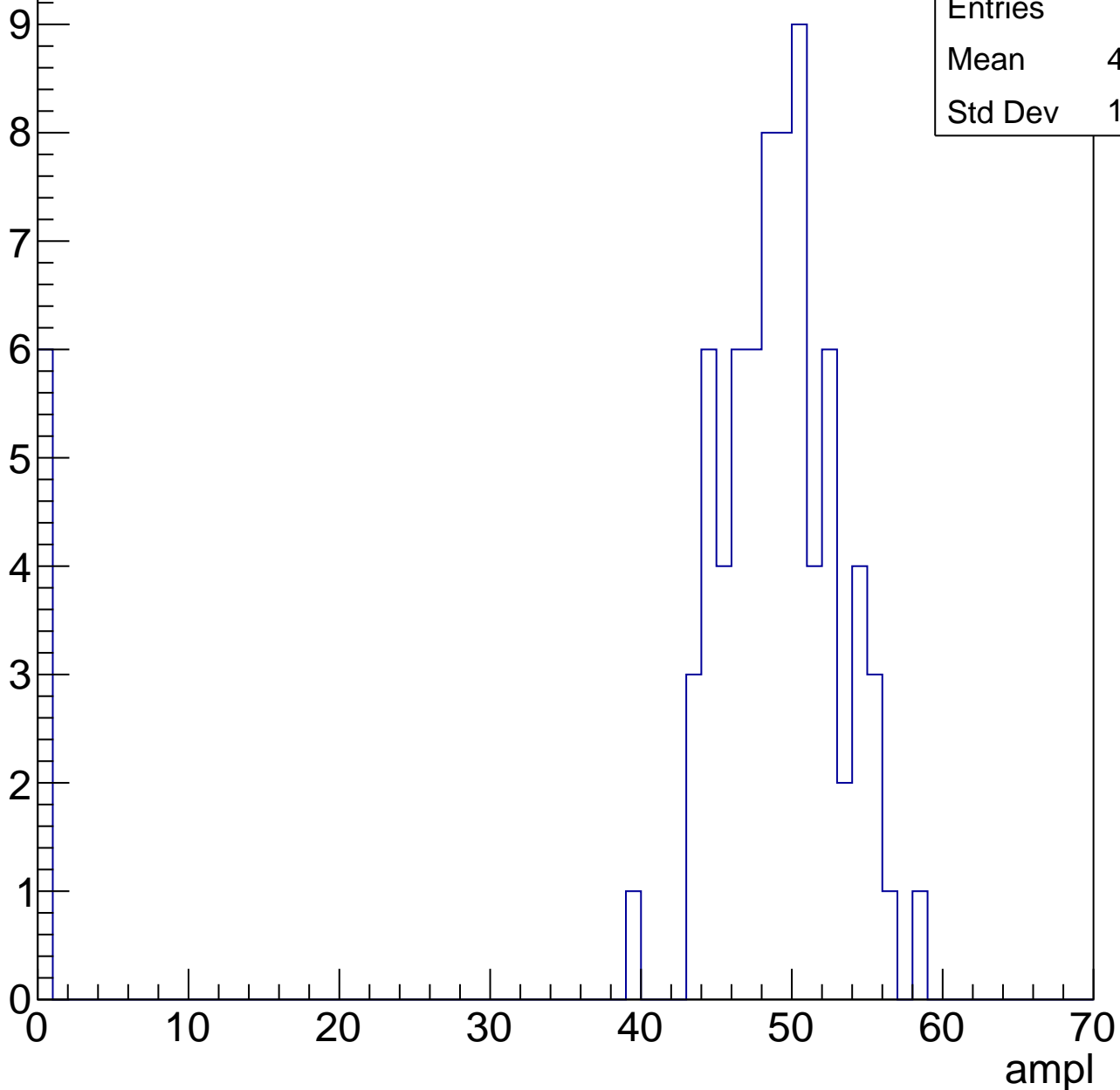


B1L103S, U13-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

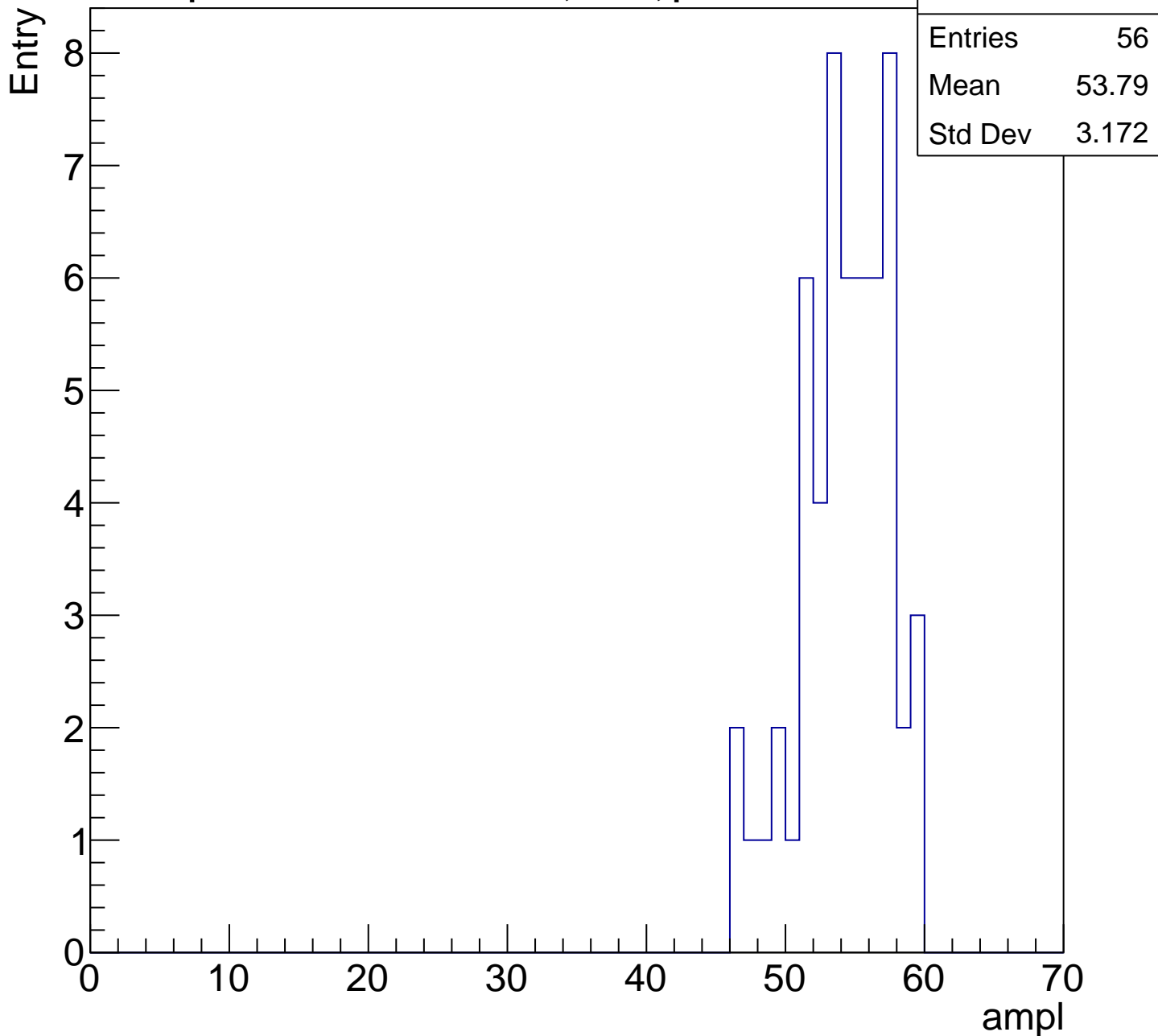
Entry

Entries	78
Mean	45.04
Std Dev	13.46



B1L103S, U13-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

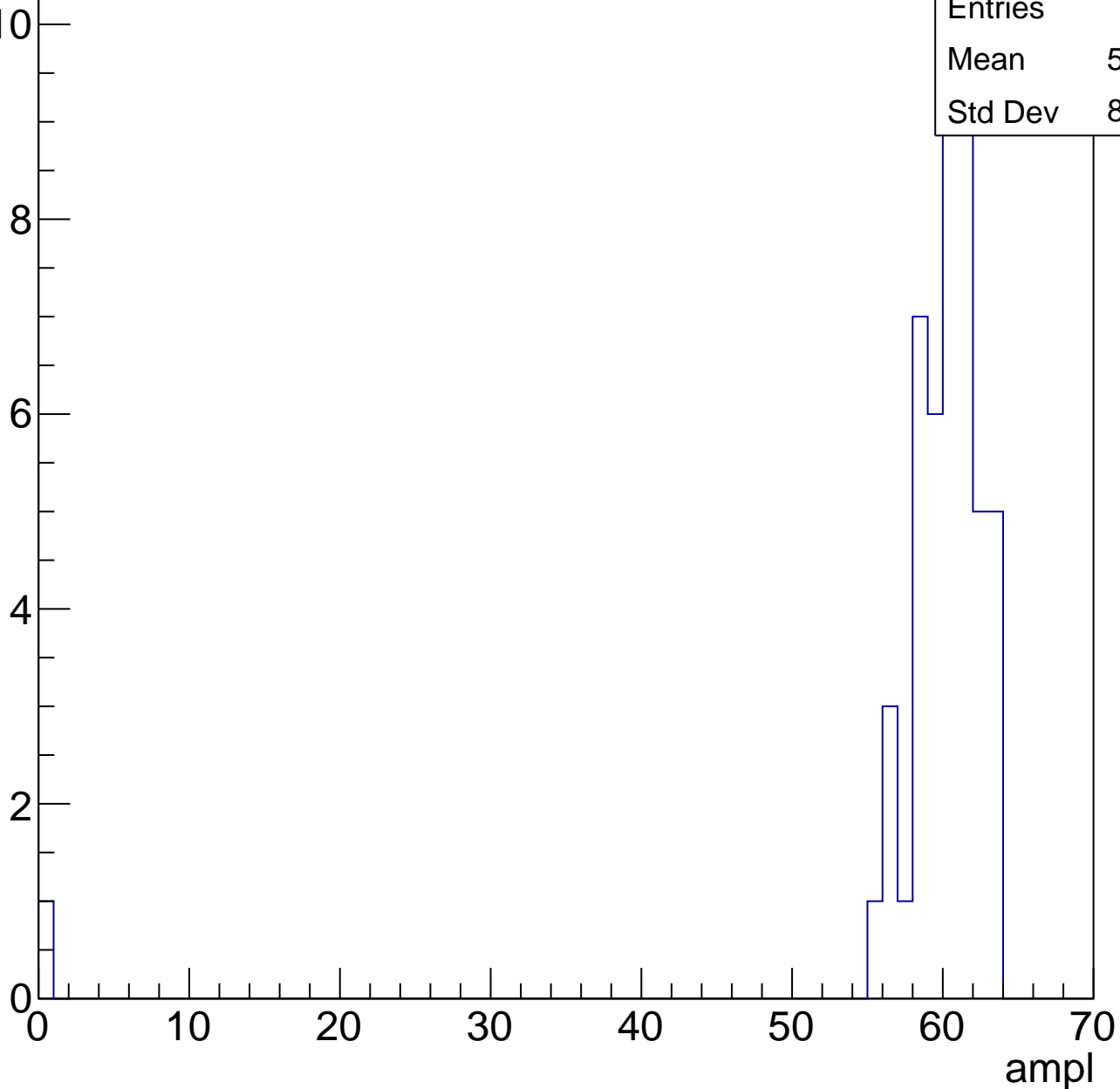


B1L103S, U13-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	58.65
Std Dev	8.783

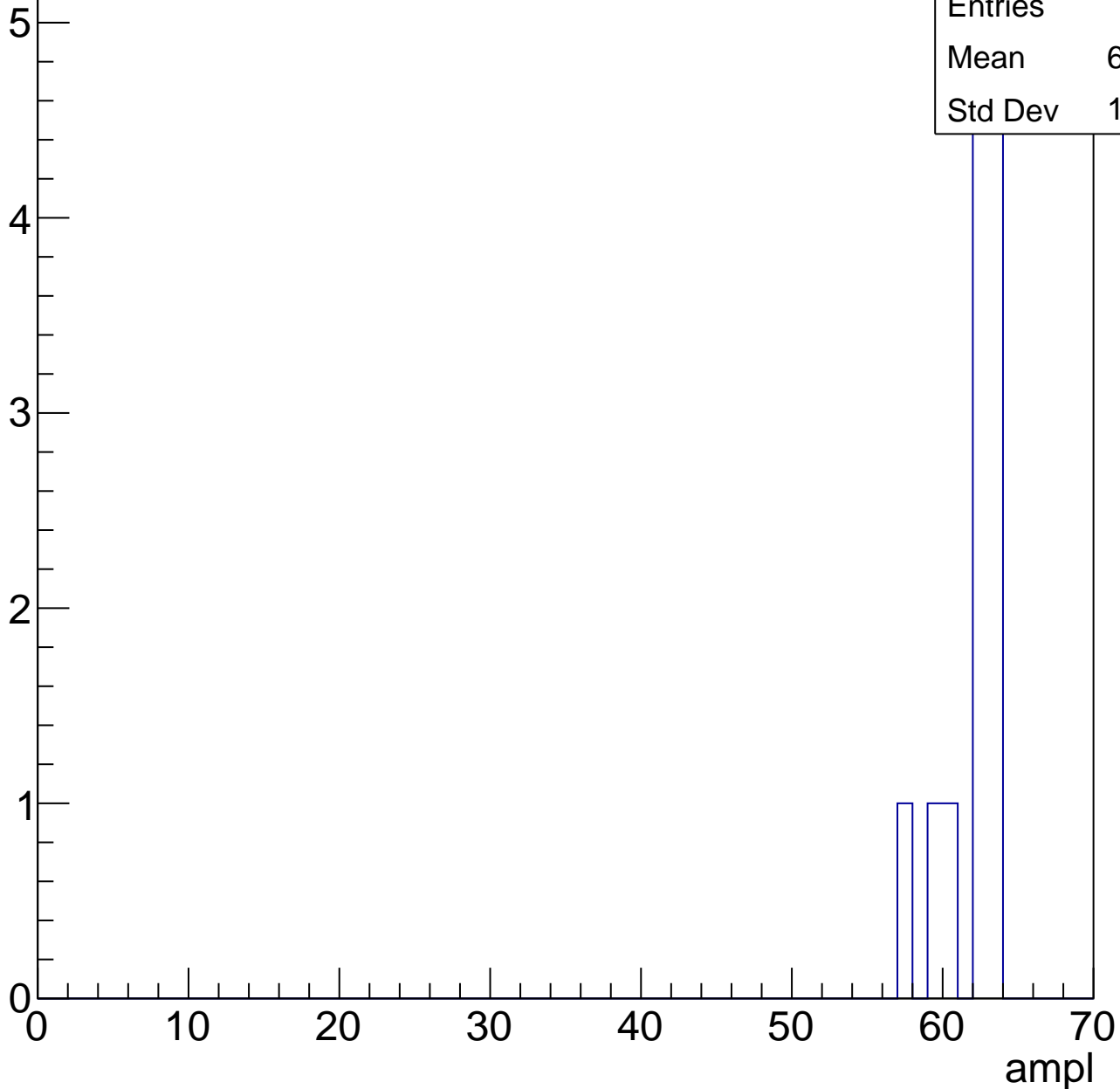


B1L103S, U13-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.62
Std Dev	1.778



B1L103S, U13-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch91, adc0

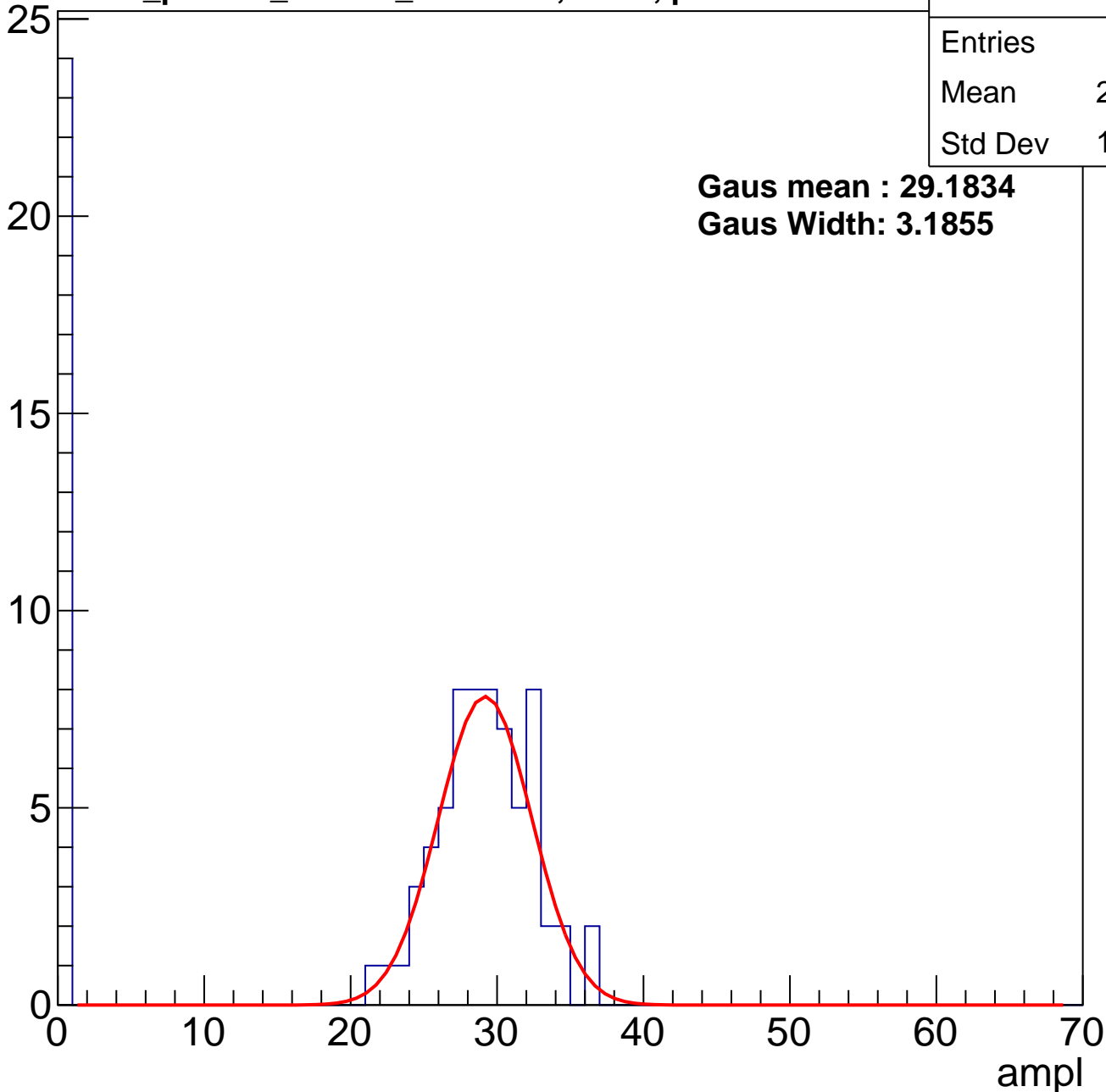
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	20.98
Std Dev	13.03

Gaus mean : 29.1834

Gaus Width: 3.1855

Entry



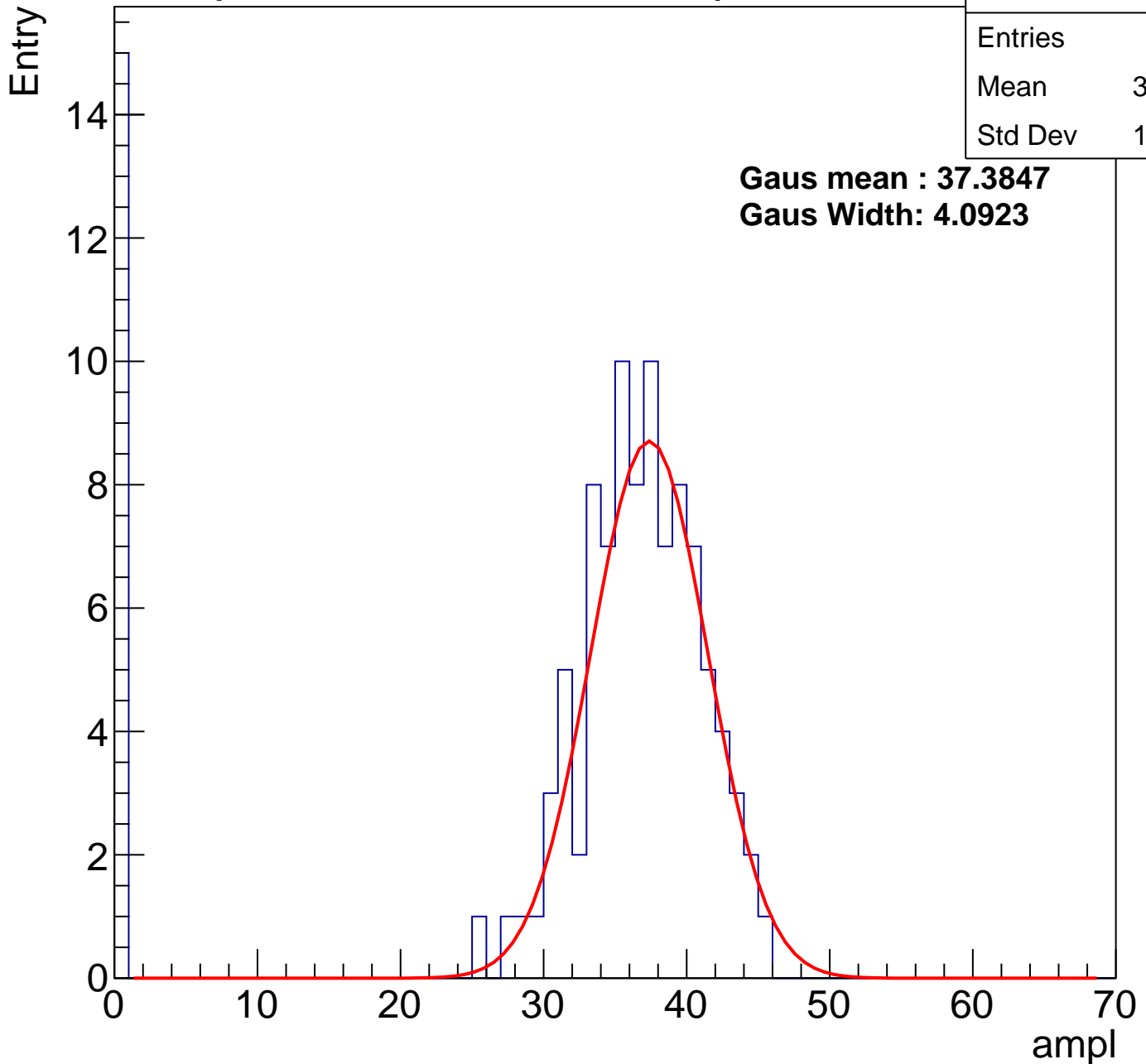
B1L103S, U13-ch91, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	31.39
Std Dev	13.08

Gaus mean : 37.3847

Gaus Width: 4.0923



B1L103S, U13-ch91, adc2

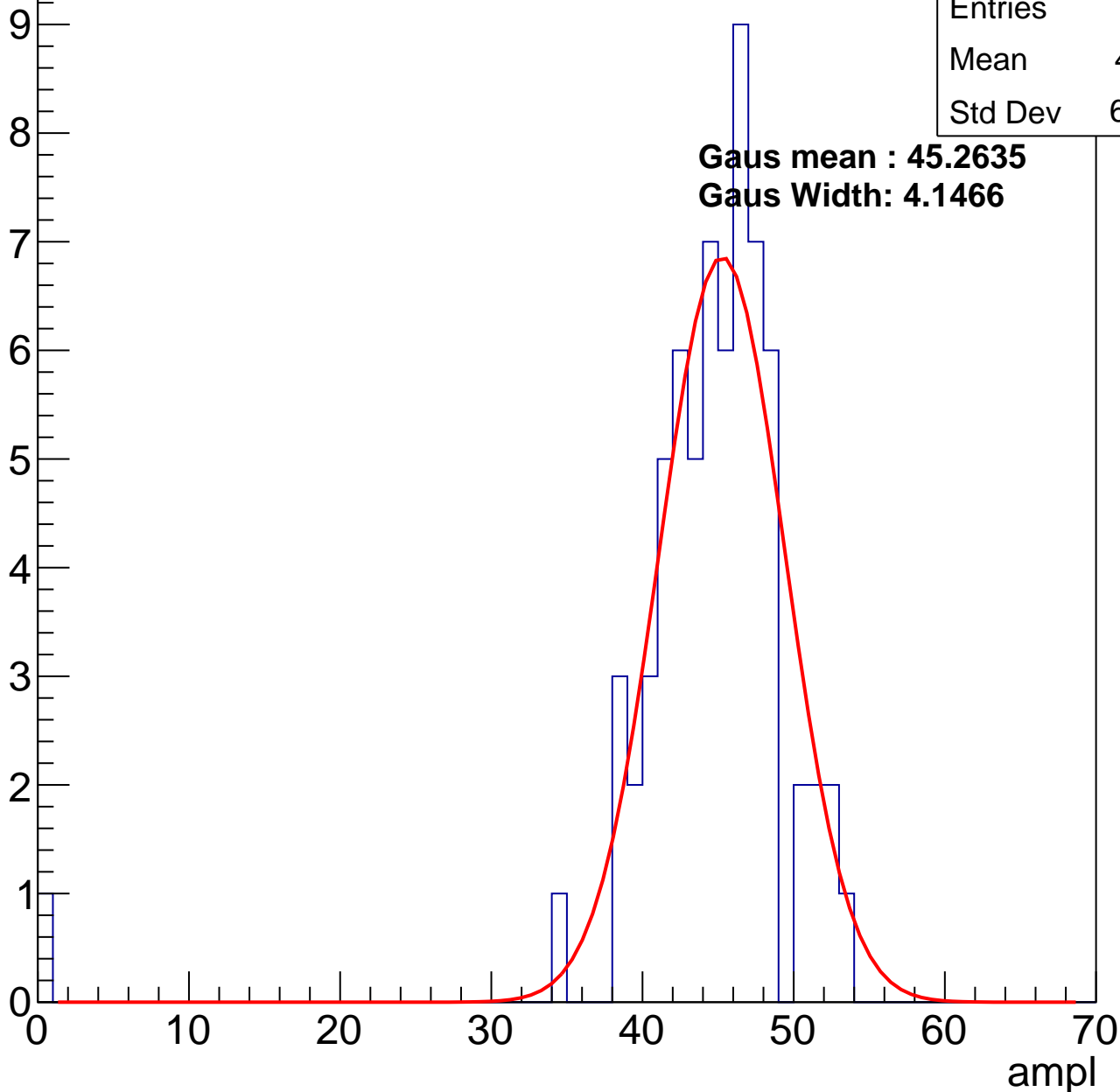
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	43.91
Std Dev	6.525

Gaus mean : 45.2635

Gaus Width: 4.1466

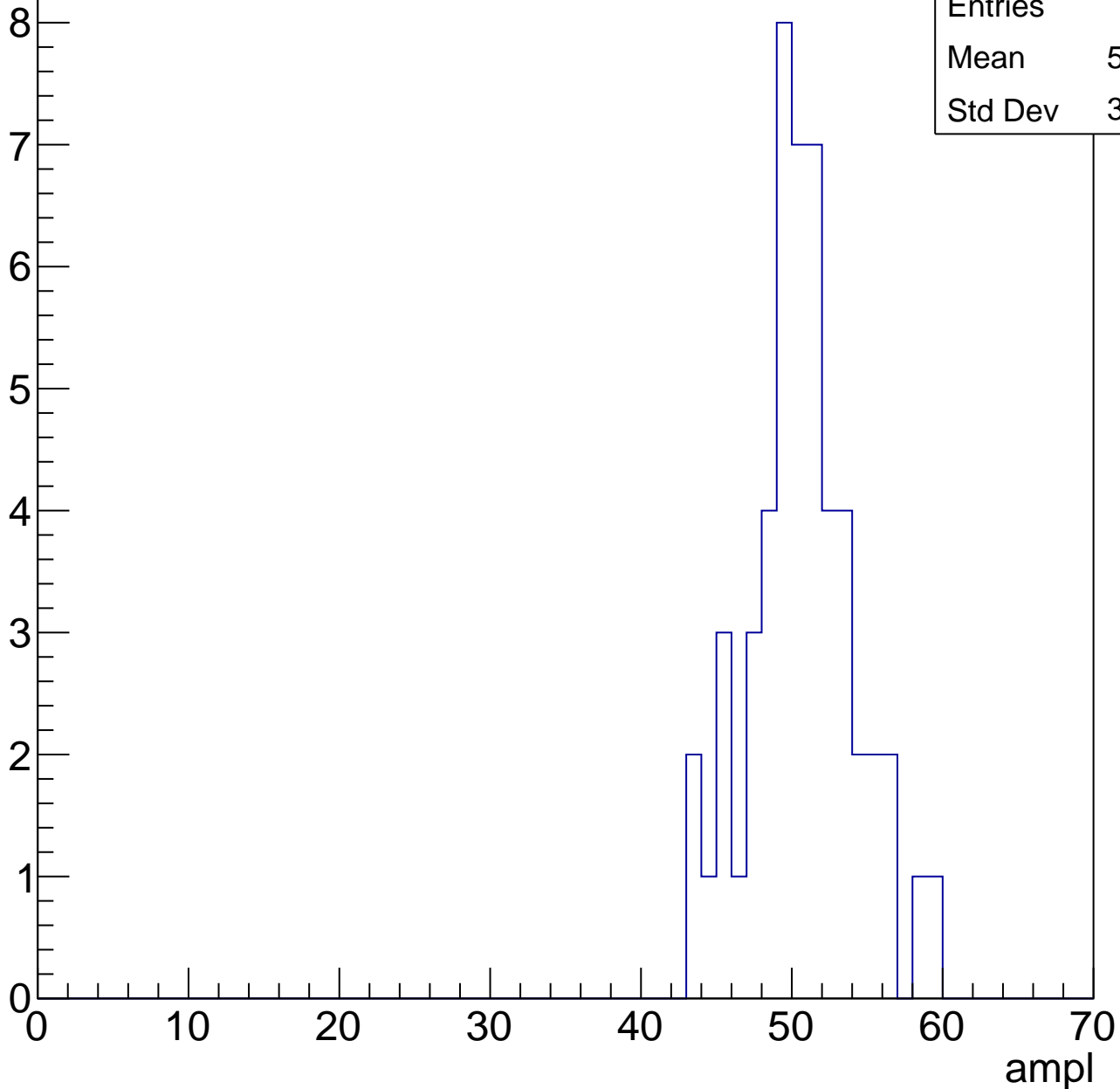


B1L103S, U13-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

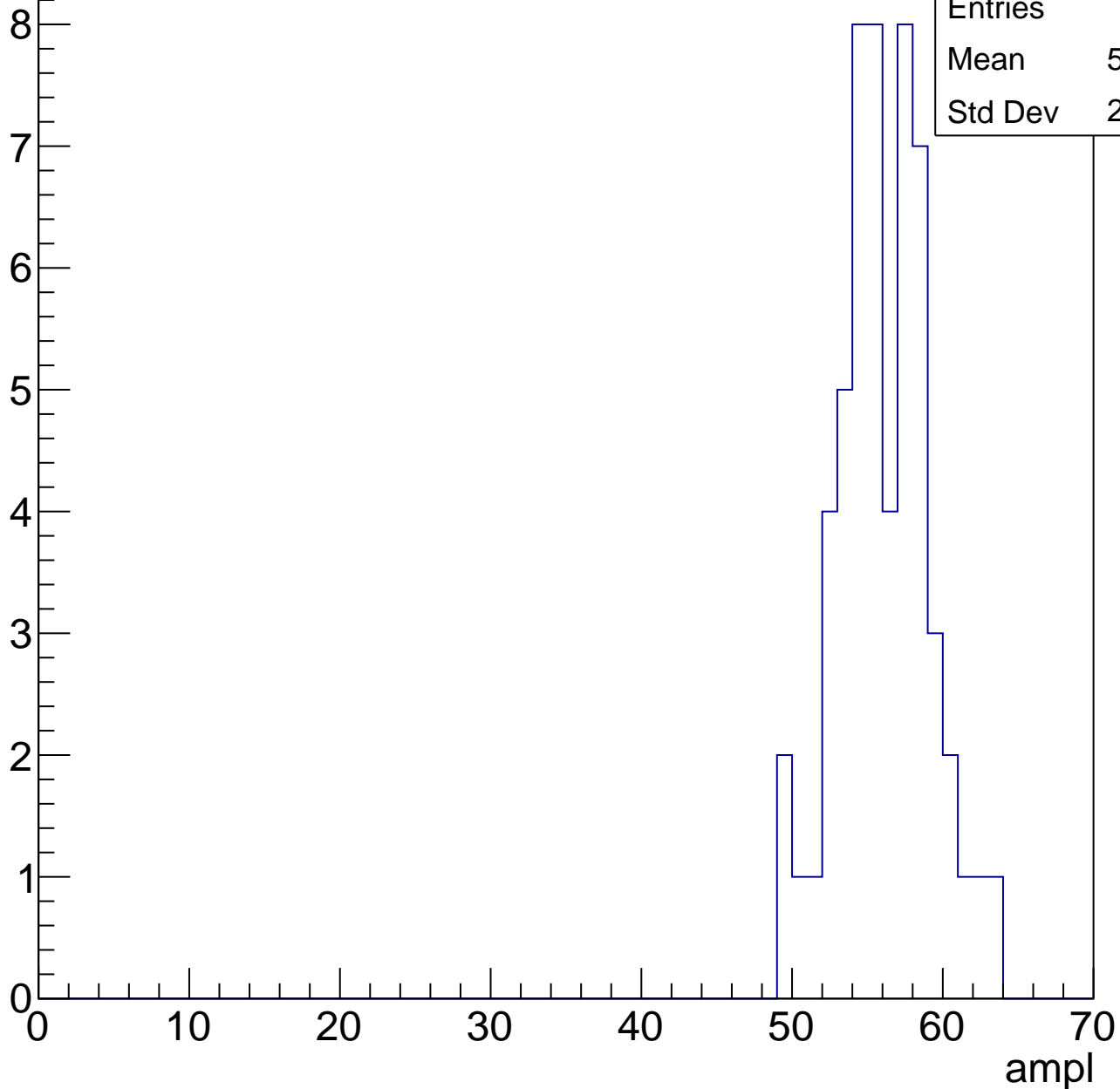
Entries	52
Mean	50.19
Std Dev	3.486



B1L103S, U13-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

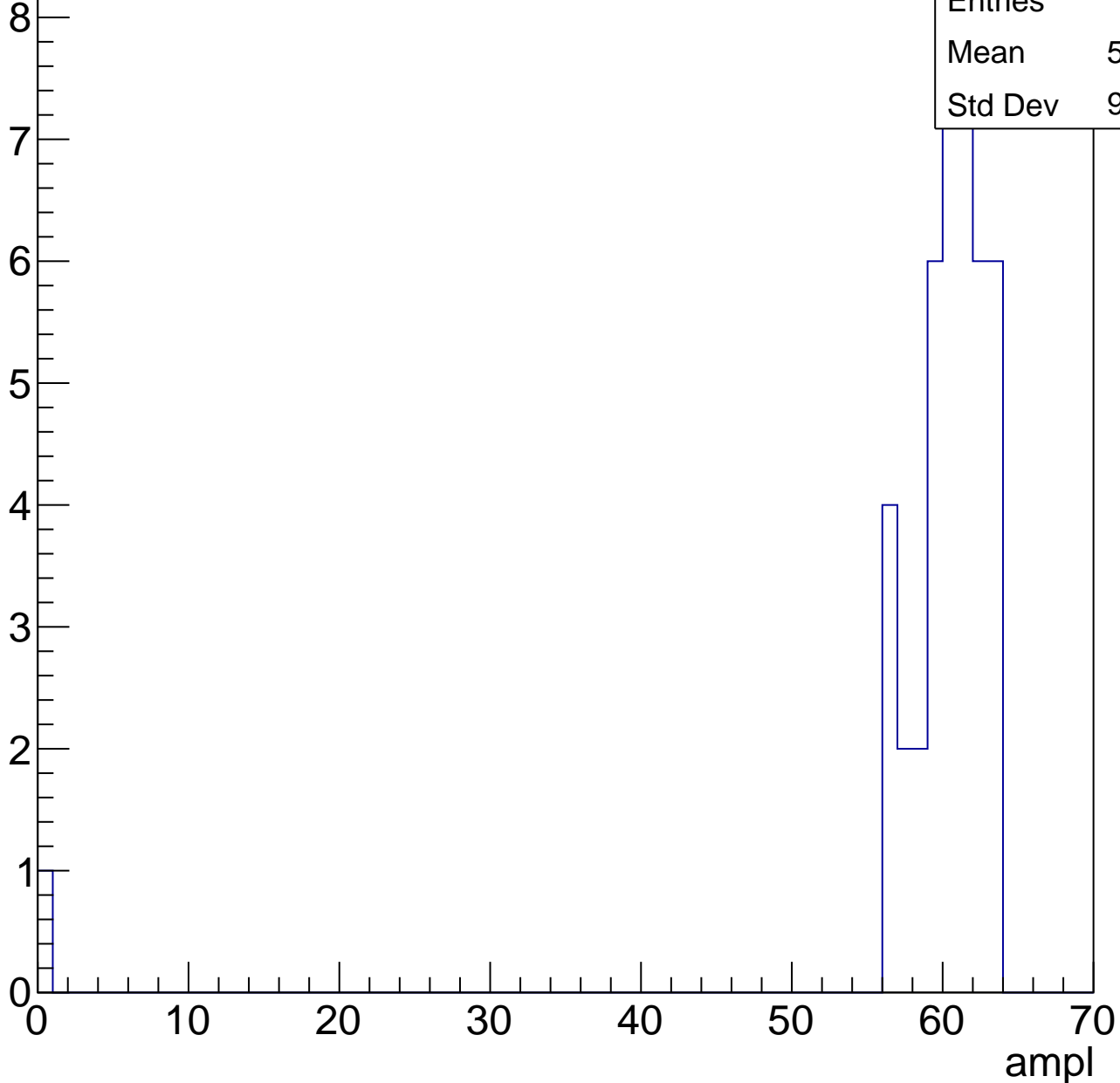


B1L103S, U13-ch91, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	58.74
Std Dev	9.294



B1L103S, U13-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	7
Mean	62.29
Std Dev	0.6999

B1L103S, U13-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch92, adc0

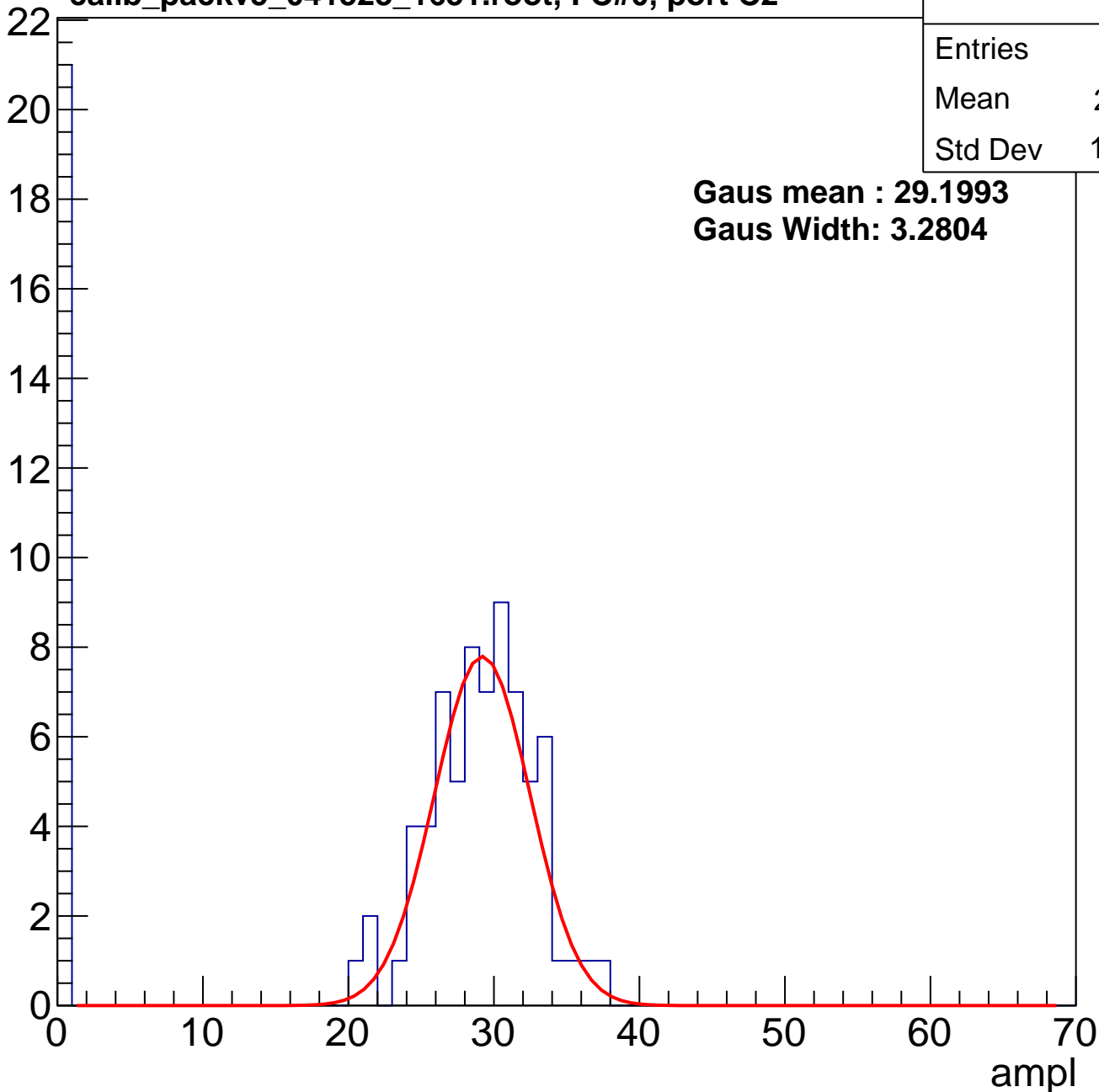
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	22.11
Std Dev	12.49

Gaus mean : 29.1993

Gaus Width: 3.2804

Entry



B1L103S, U13-ch92, adc1

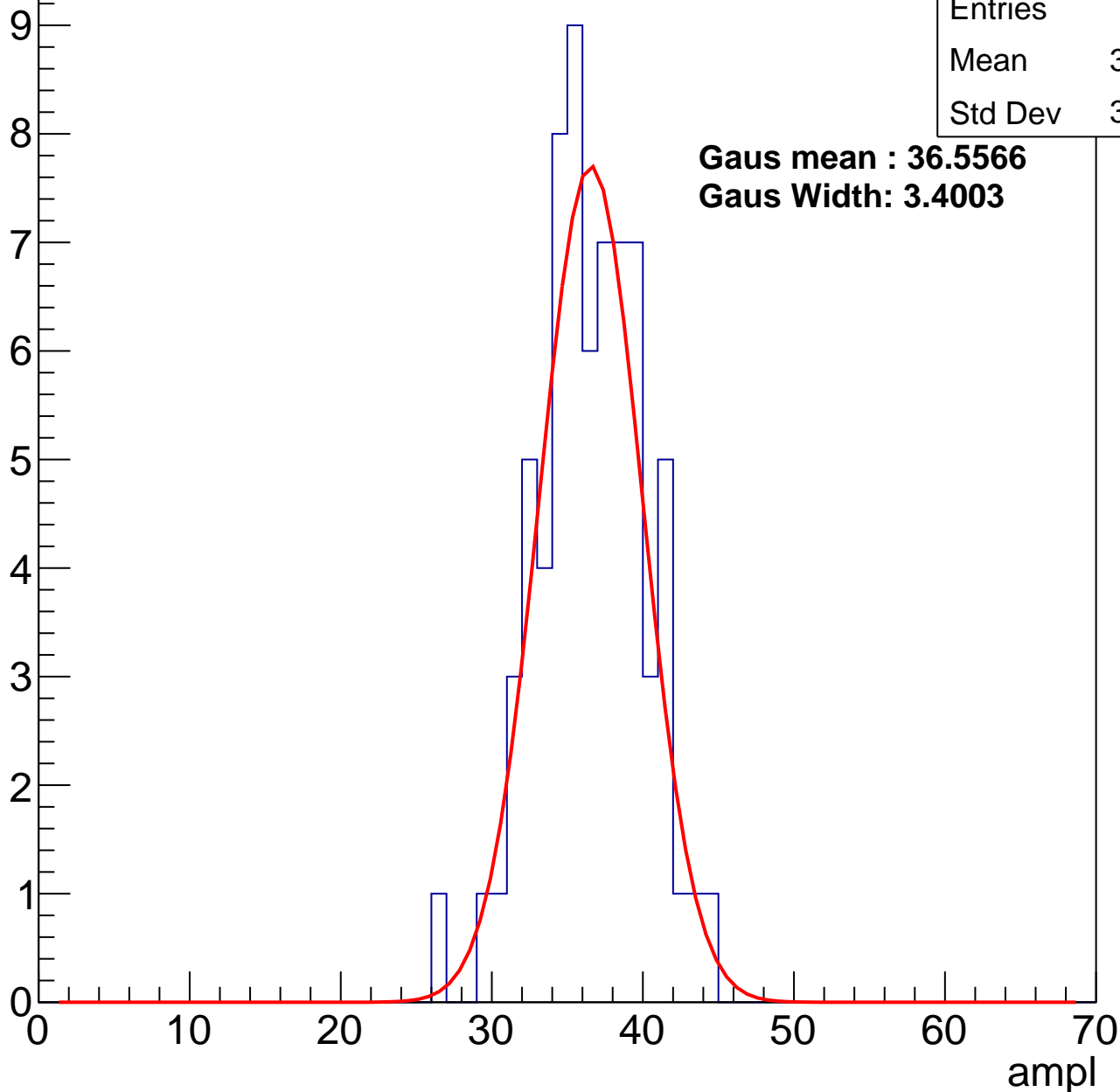
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.07
Std Dev	3.457

Gaus mean : 36.5566

Gaus Width: 3.4003



B1L103S, U13-ch92, adc2

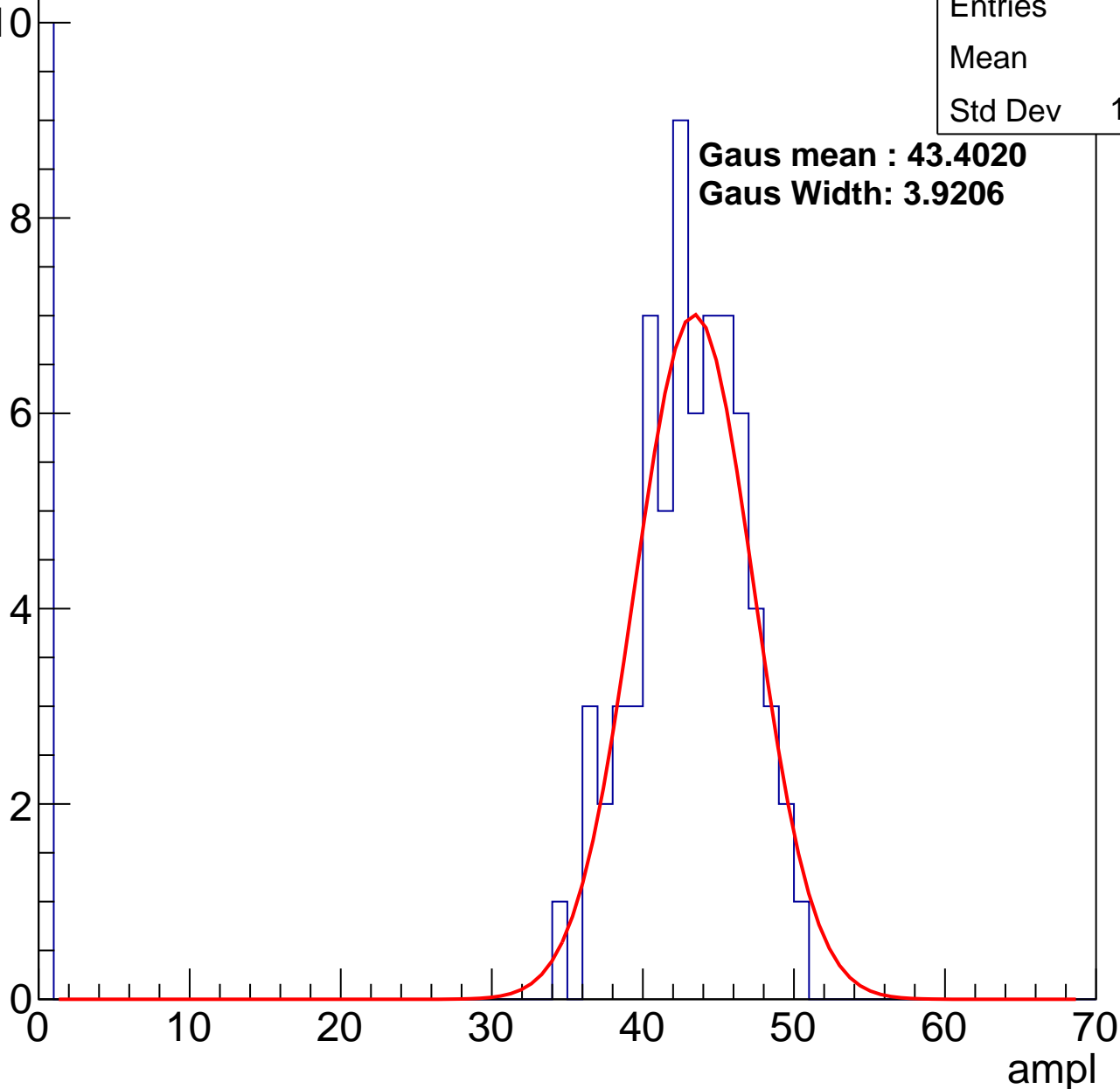
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	37.3
Std Dev	14.58

Gaus mean : 43.4020

Gaus Width: 3.9206

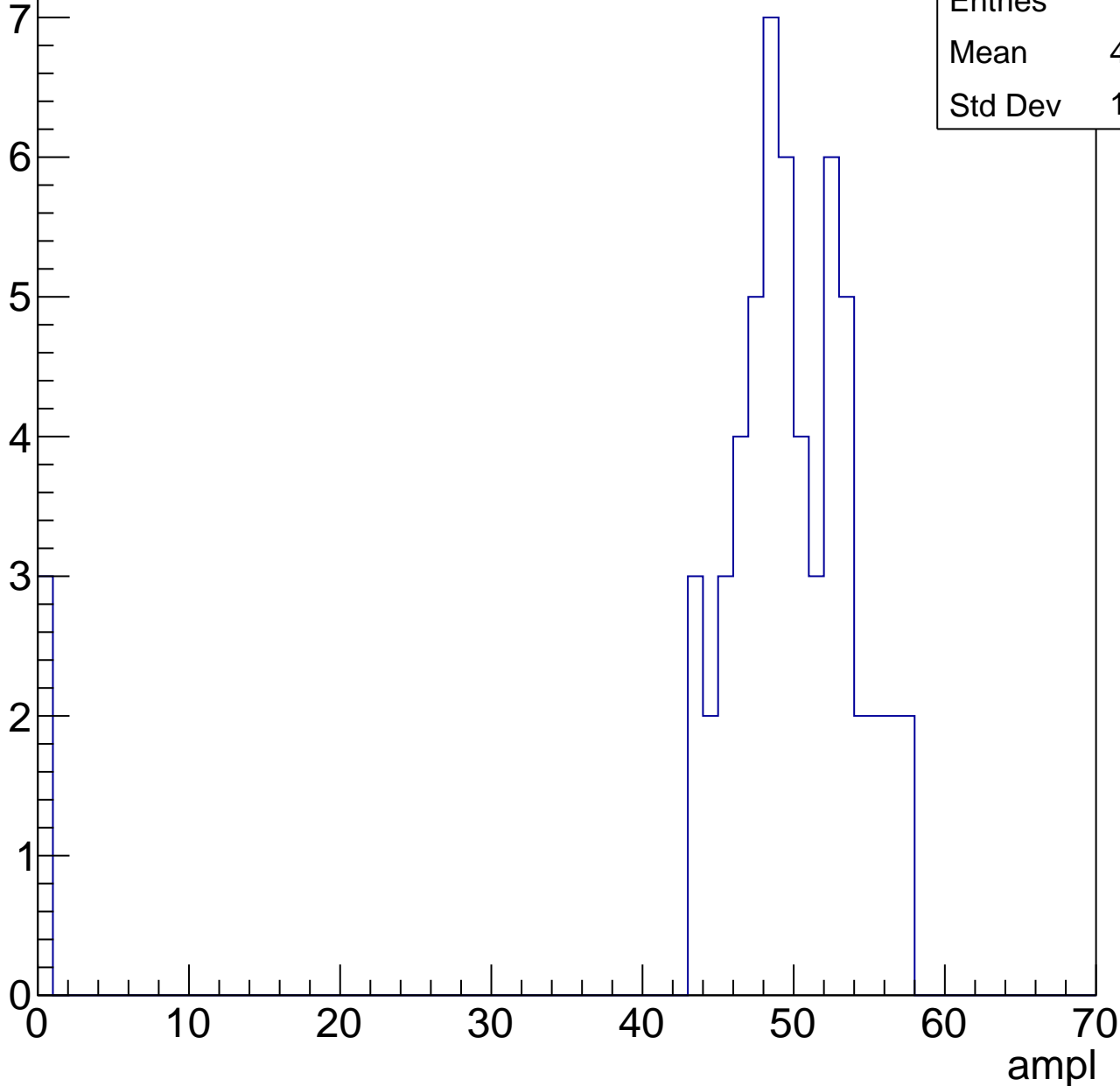


B1L103S, U13-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	47.03
Std Dev	11.46

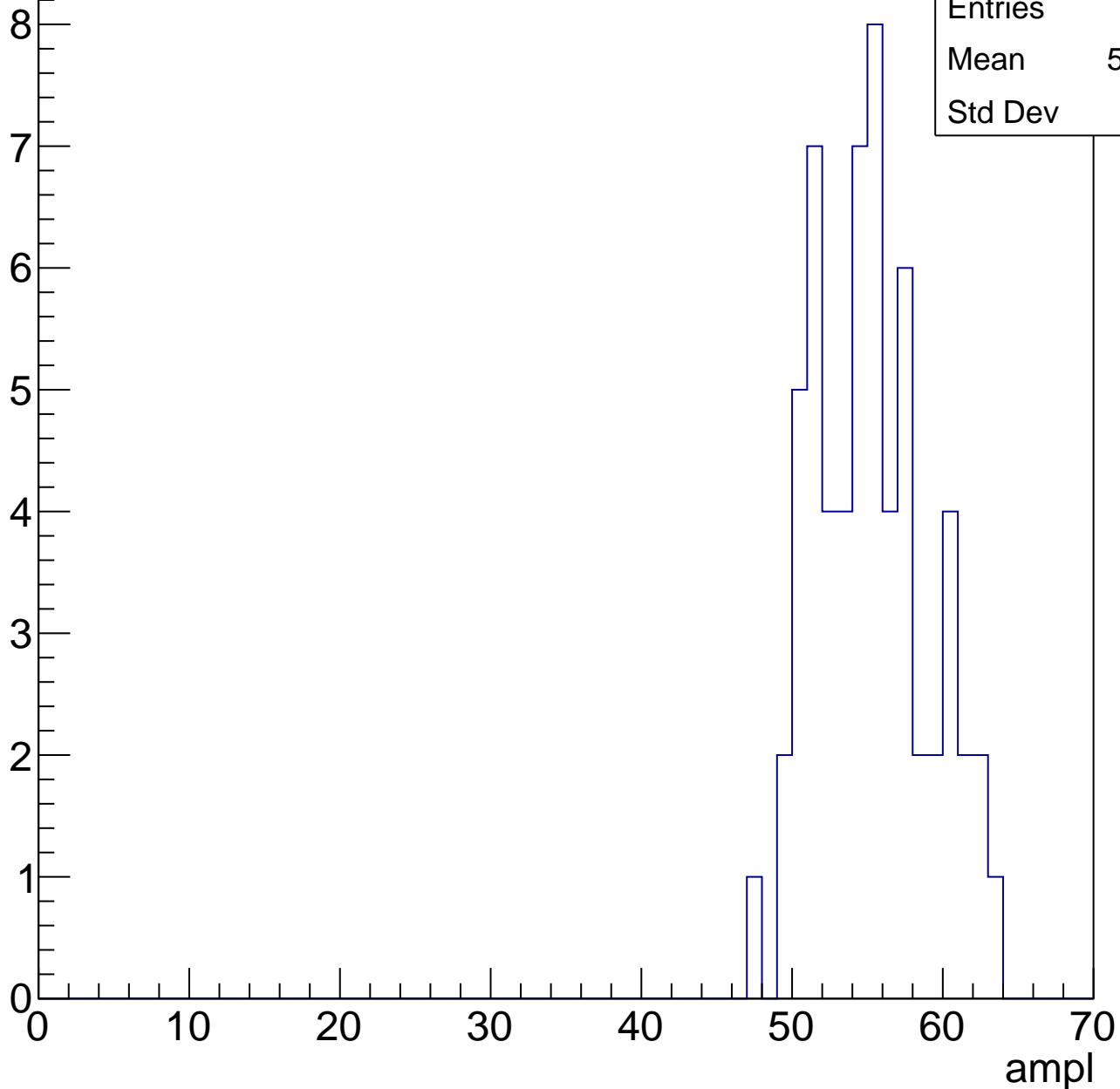


B1L103S, U13-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

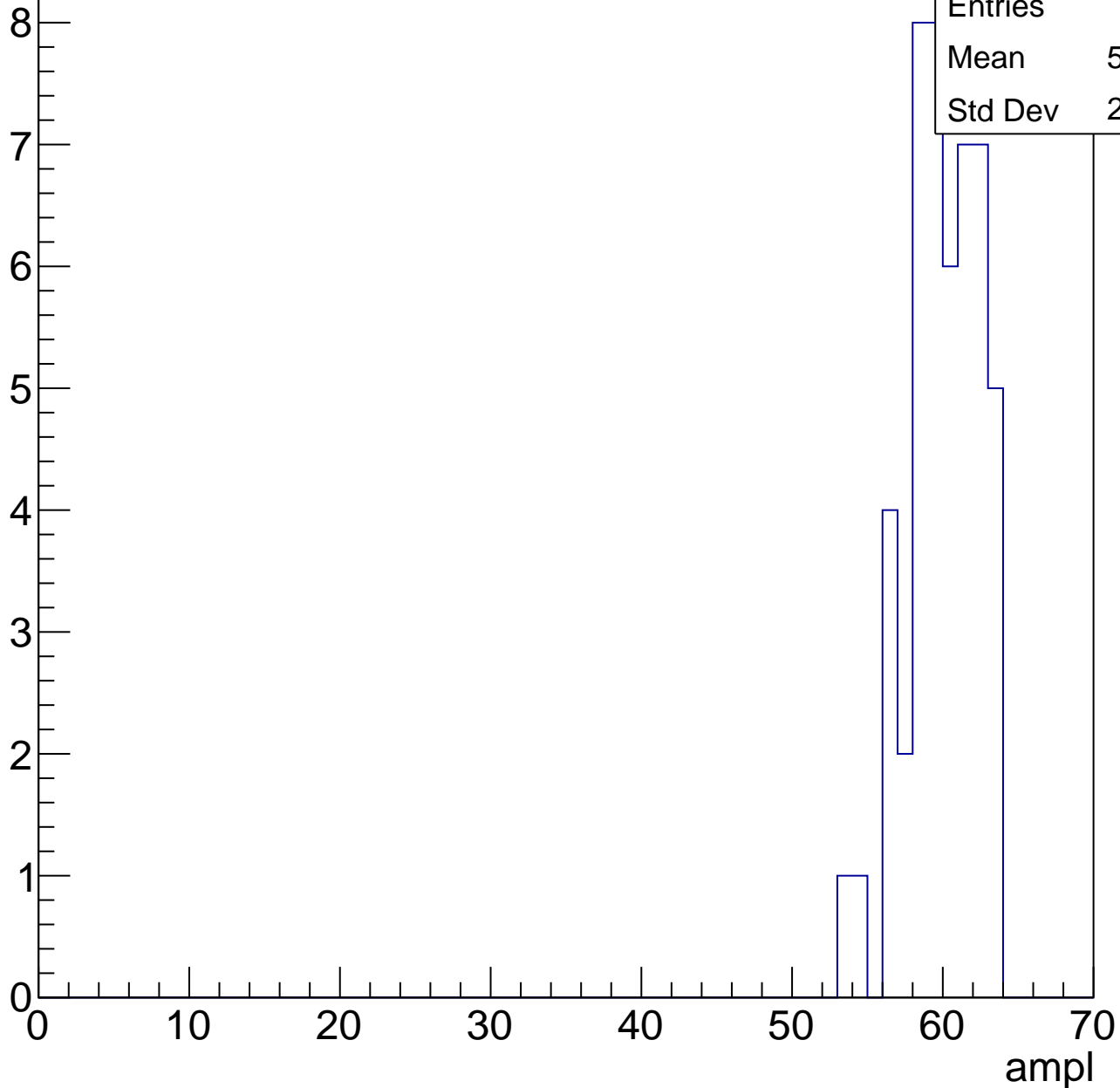
Entries	61
Mean	54.74
Std Dev	3.71



B1L103S, U13-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

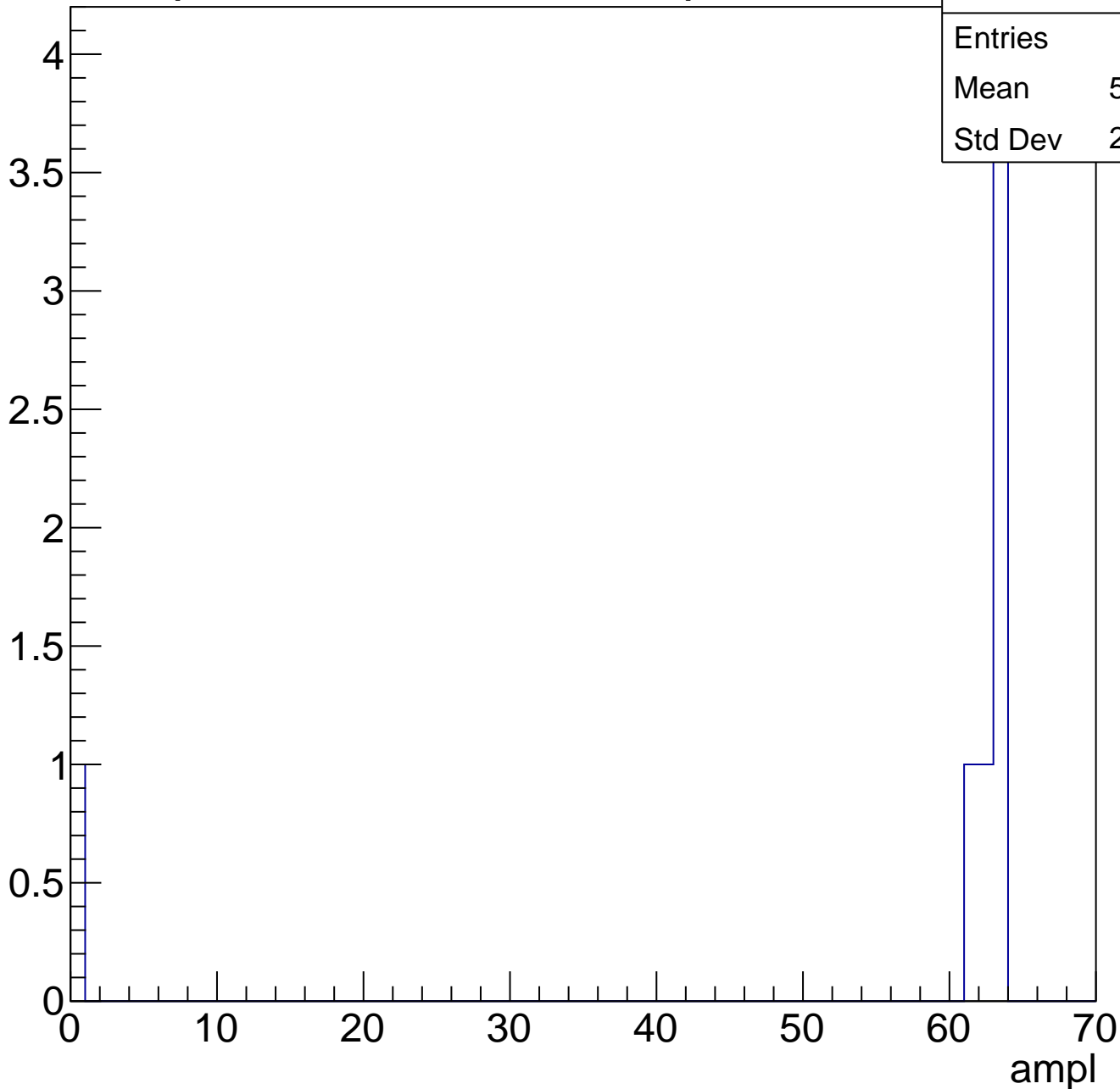
Entry



B1L103S, U13-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch93, adc0

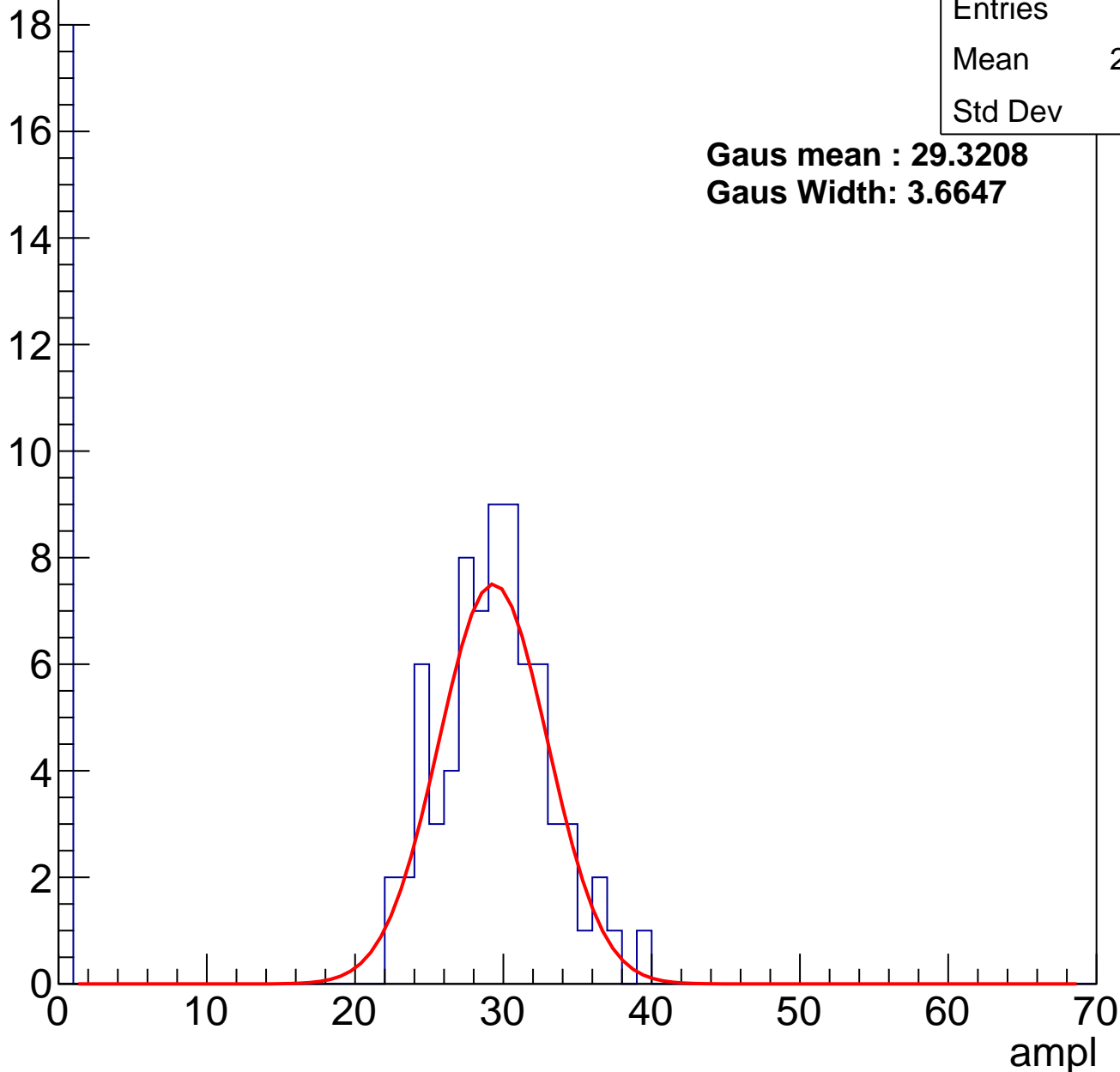
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	23.27
Std Dev	12

Gaus mean : 29.3208

Gaus Width: 3.6647

Entry



B1L103S, U13-ch93, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	30.36
Std Dev	14.09

Gaus mean : 36.9064

Gaus Width: 3.6148

Entry

14

12

10

8

6

4

2

0

0

10

20

30

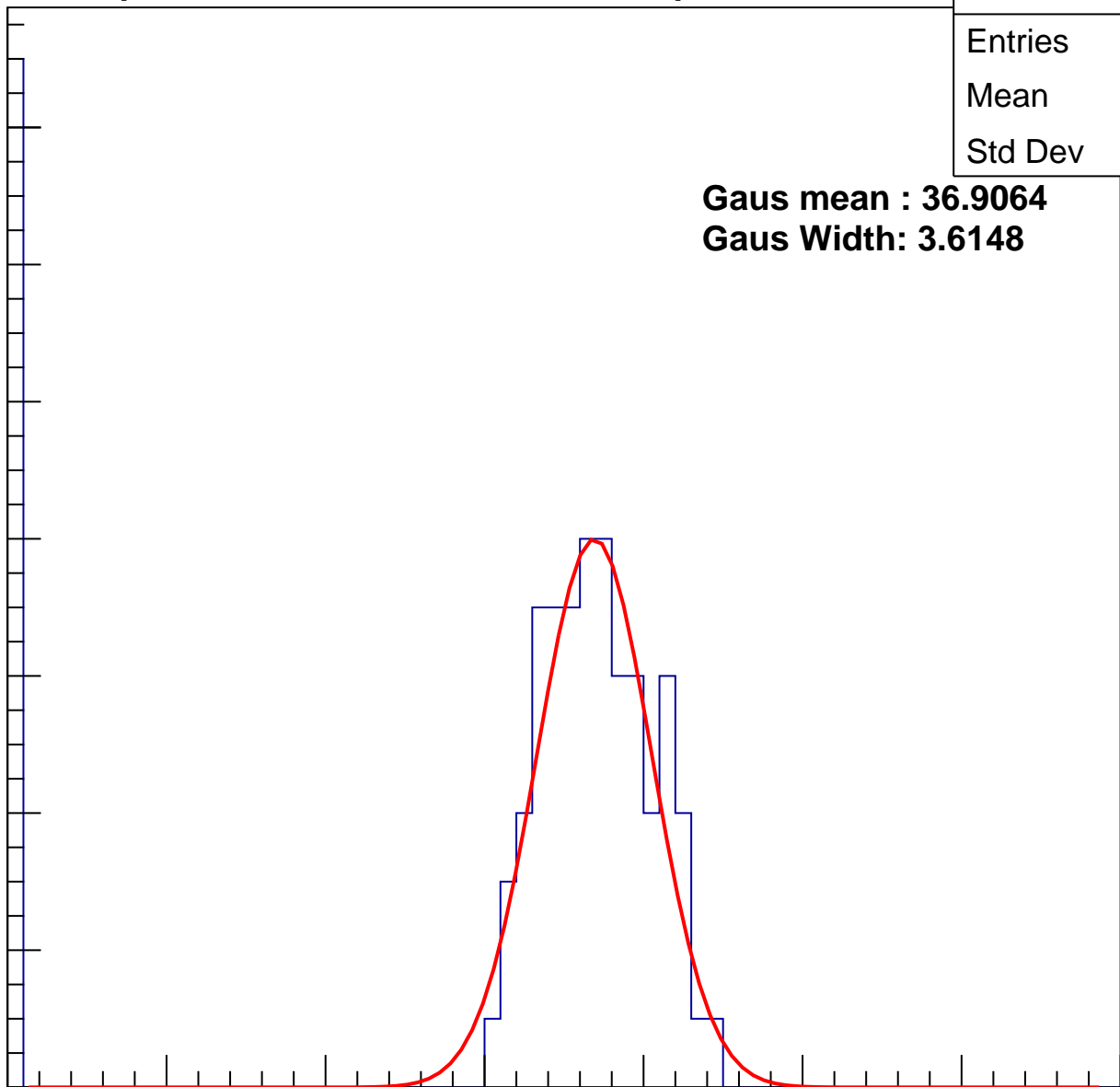
40

50

60

70

ampl



B1L103S, U13-ch93, adc2

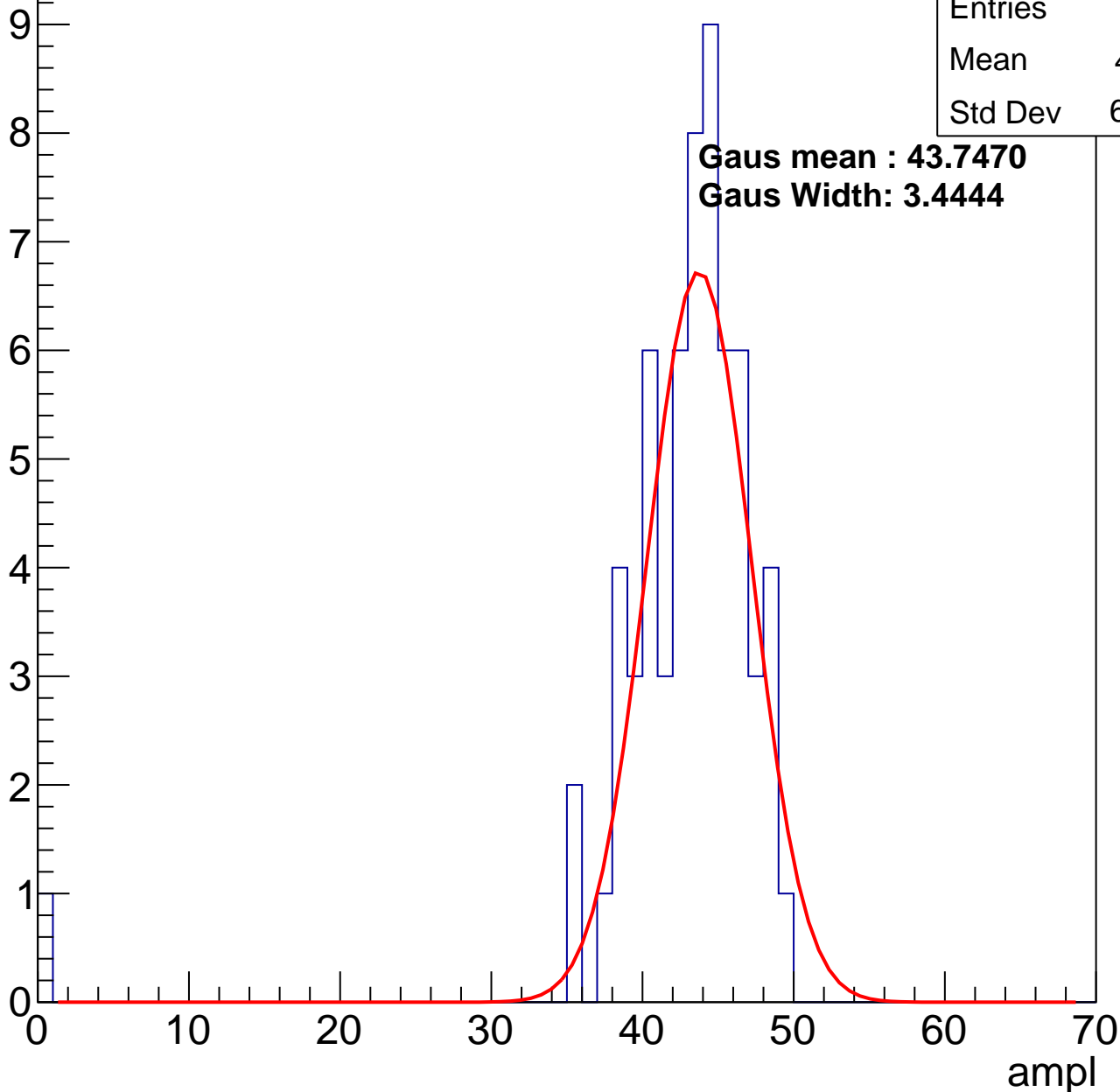
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	42.21
Std Dev	6.254

Gaus mean : 43.7470

Gaus Width: 3.4444



B1L103S, U13-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

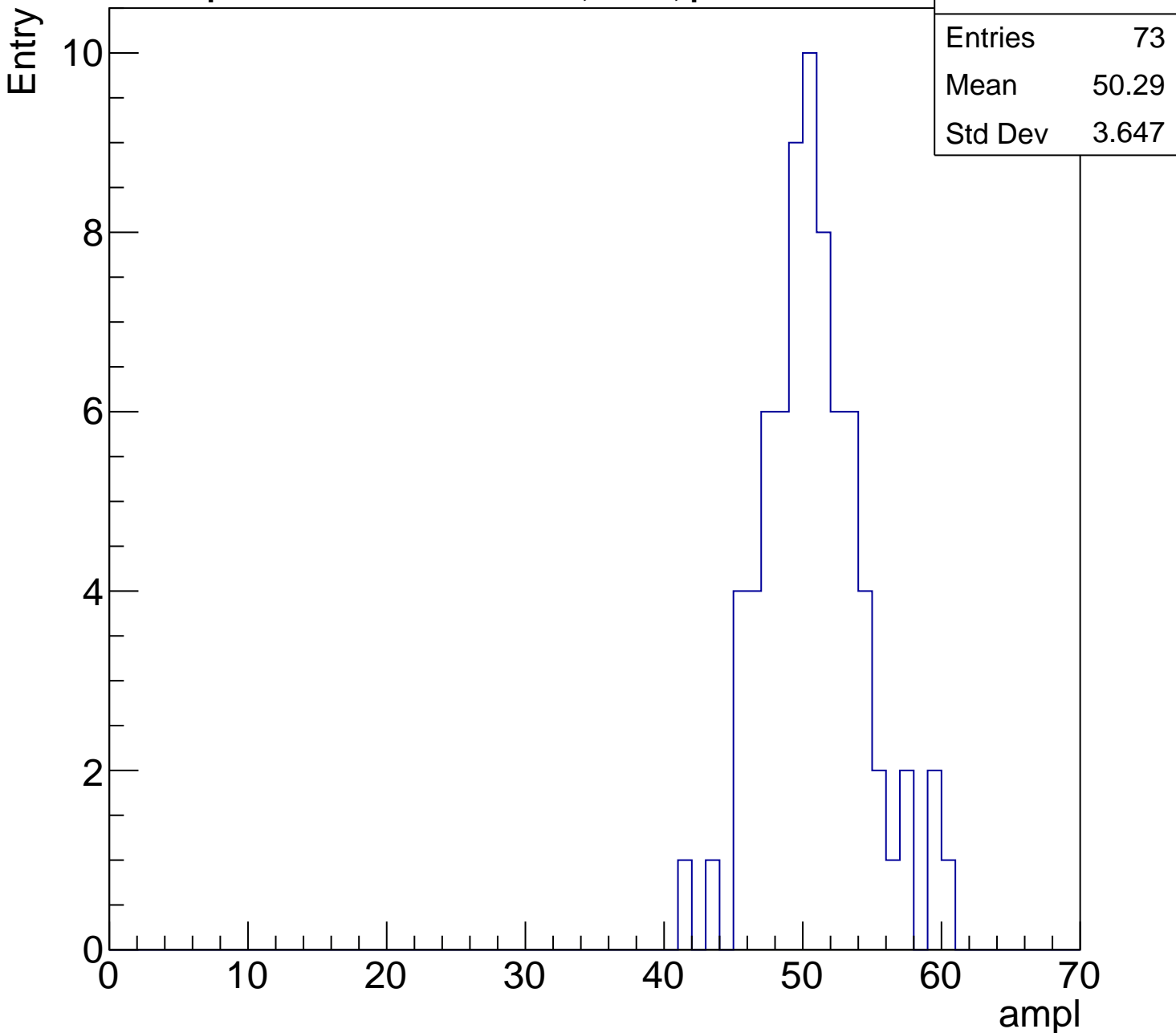
Entries	73
Mean	50.29
Std Dev	3.647

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

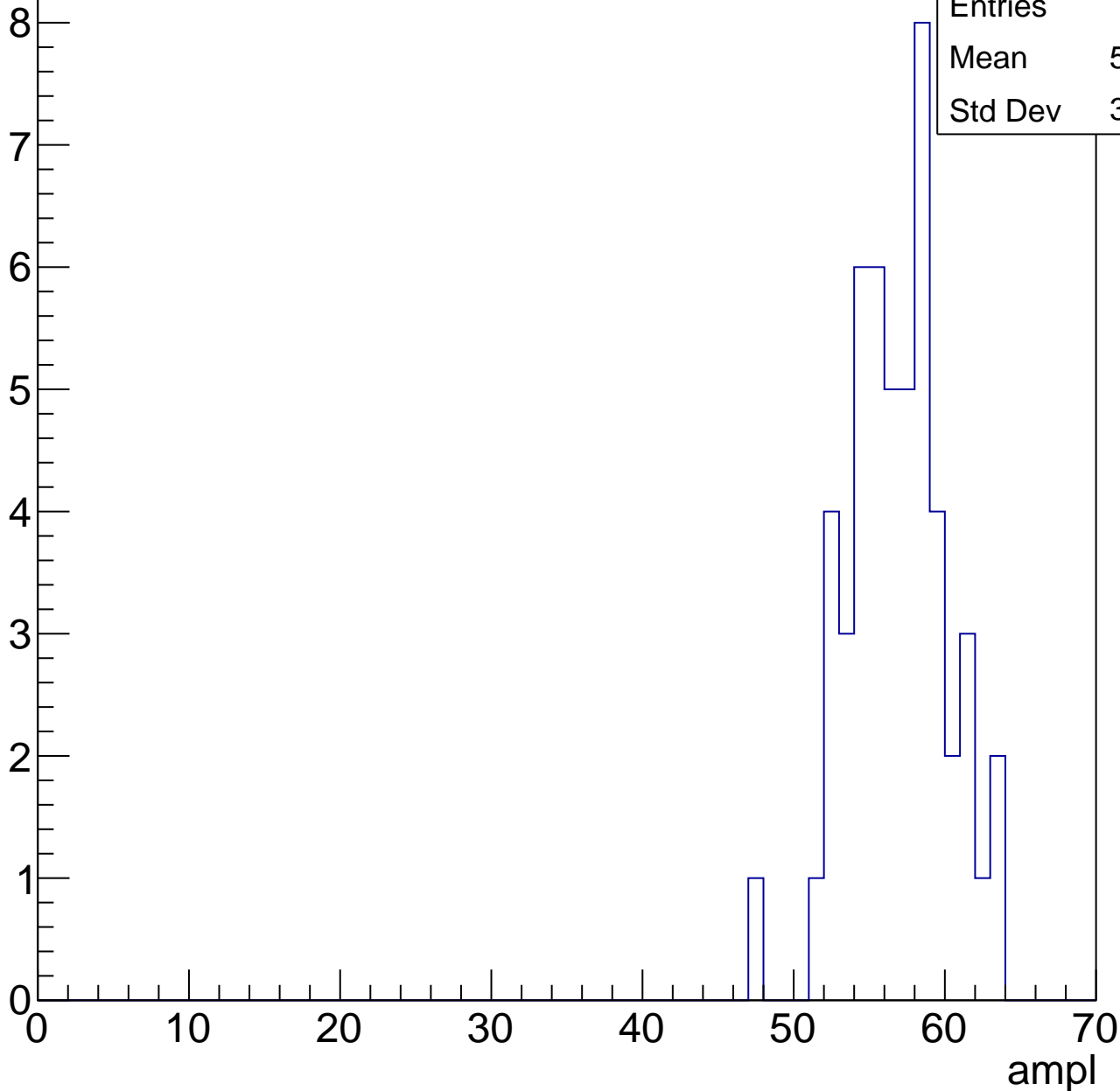


B1L103S, U13-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	56.37
Std Dev	3.236

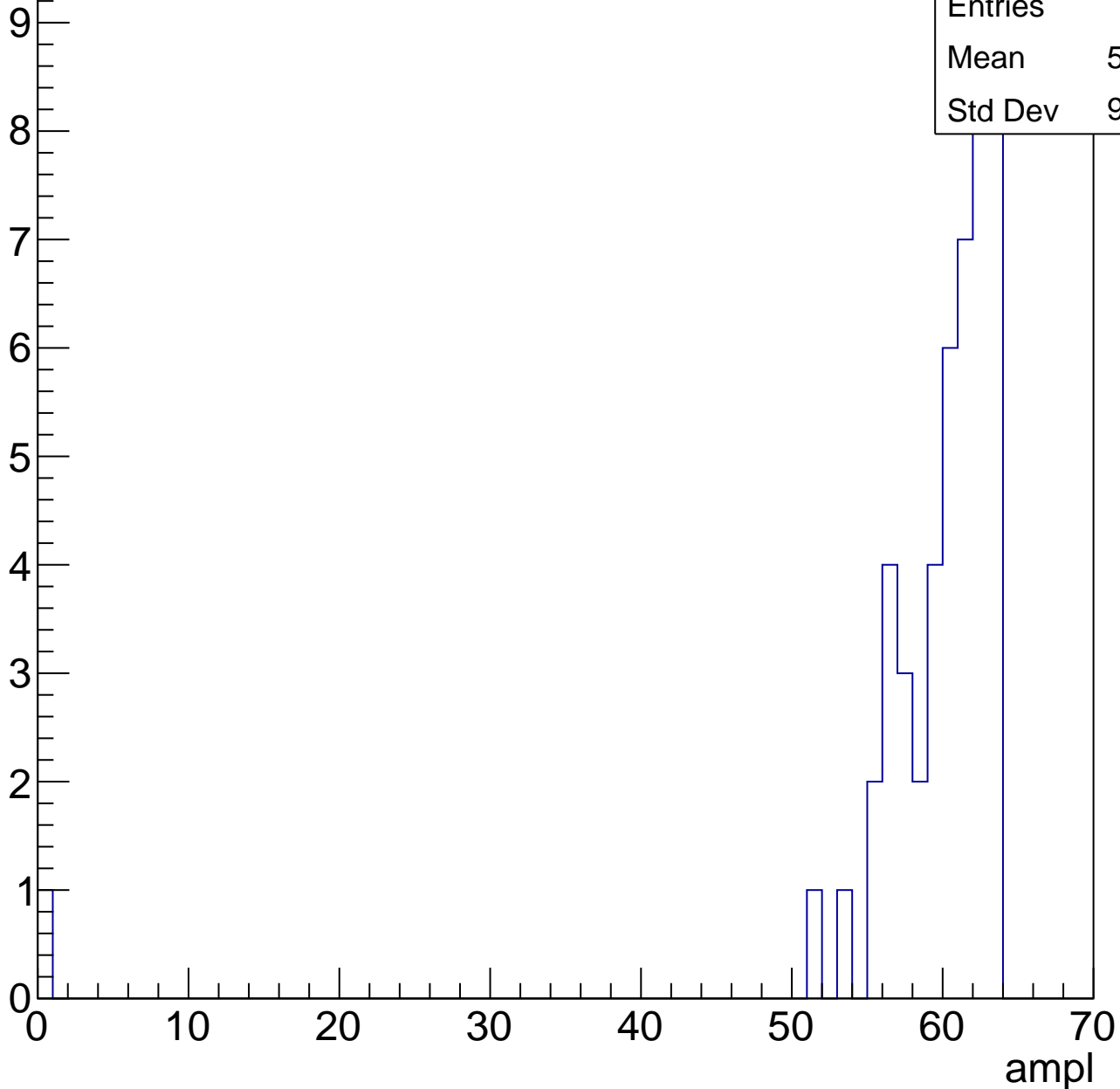


B1L103S, U13-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	58.54
Std Dev	9.009



B1L103S, U13-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	1
Mean	60
Std Dev	0

B1L103S, U13-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch94, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	25.41
Std Dev	12.19

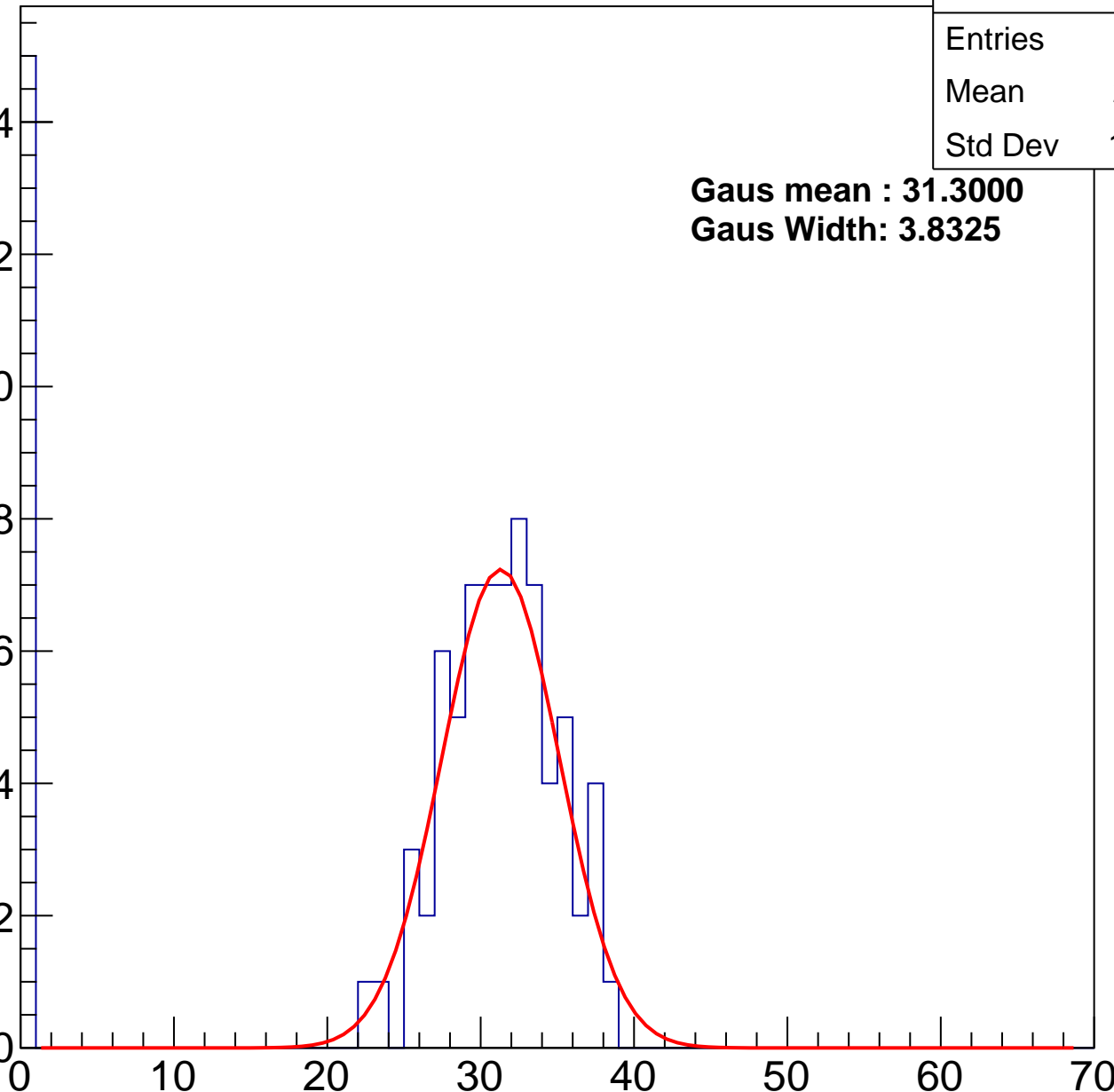
Gaus mean : 31.3000

Gaus Width: 3.8325

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch94, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	30.6
Std Dev	13.65

Gaus mean : 36.9916

Gaus Width: 4.0659

Entry

10

8

6

4

2

0

0

10

20

30

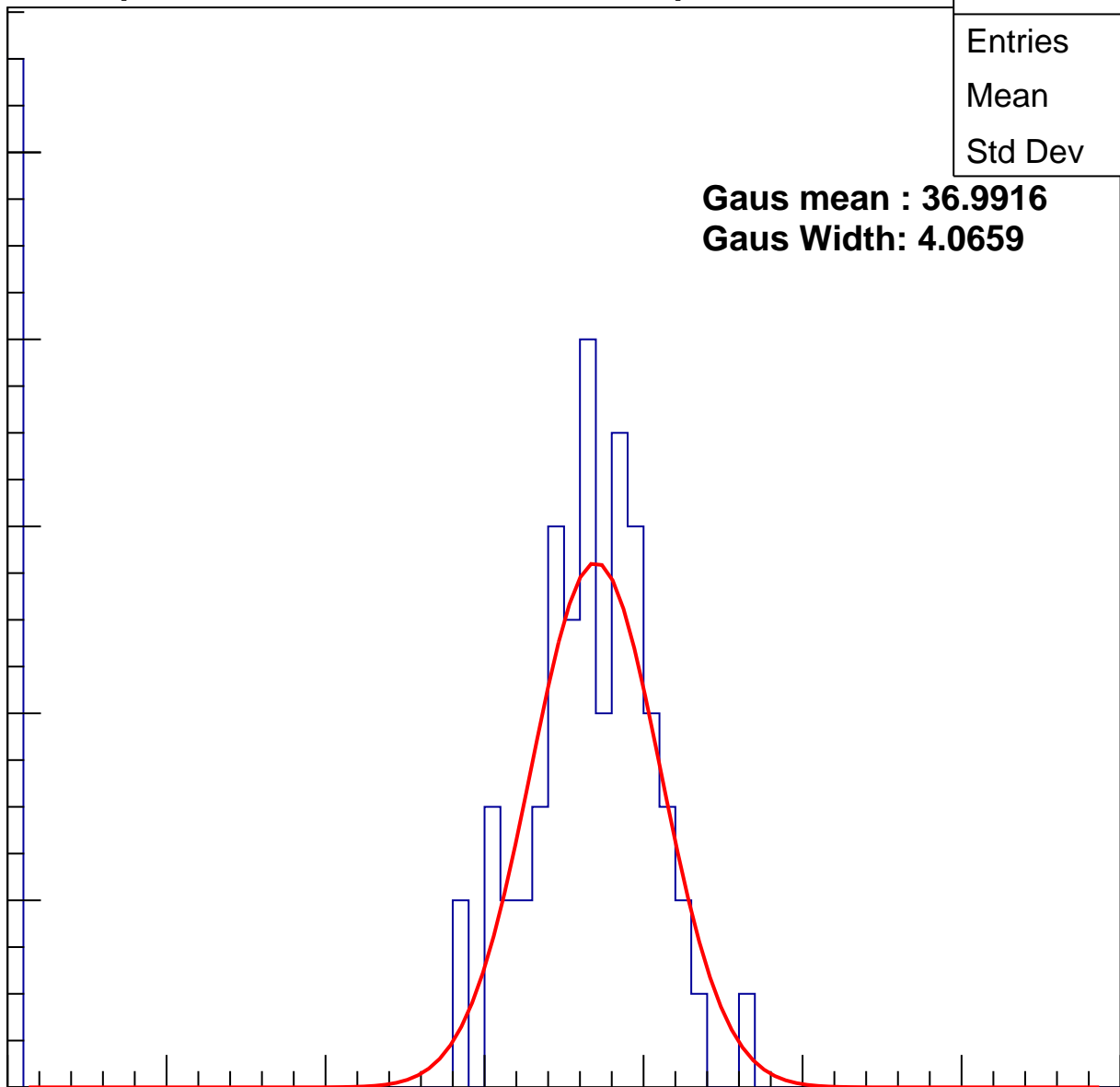
40

50

60

70

ampl



B1L103S, U13-ch94, adc2

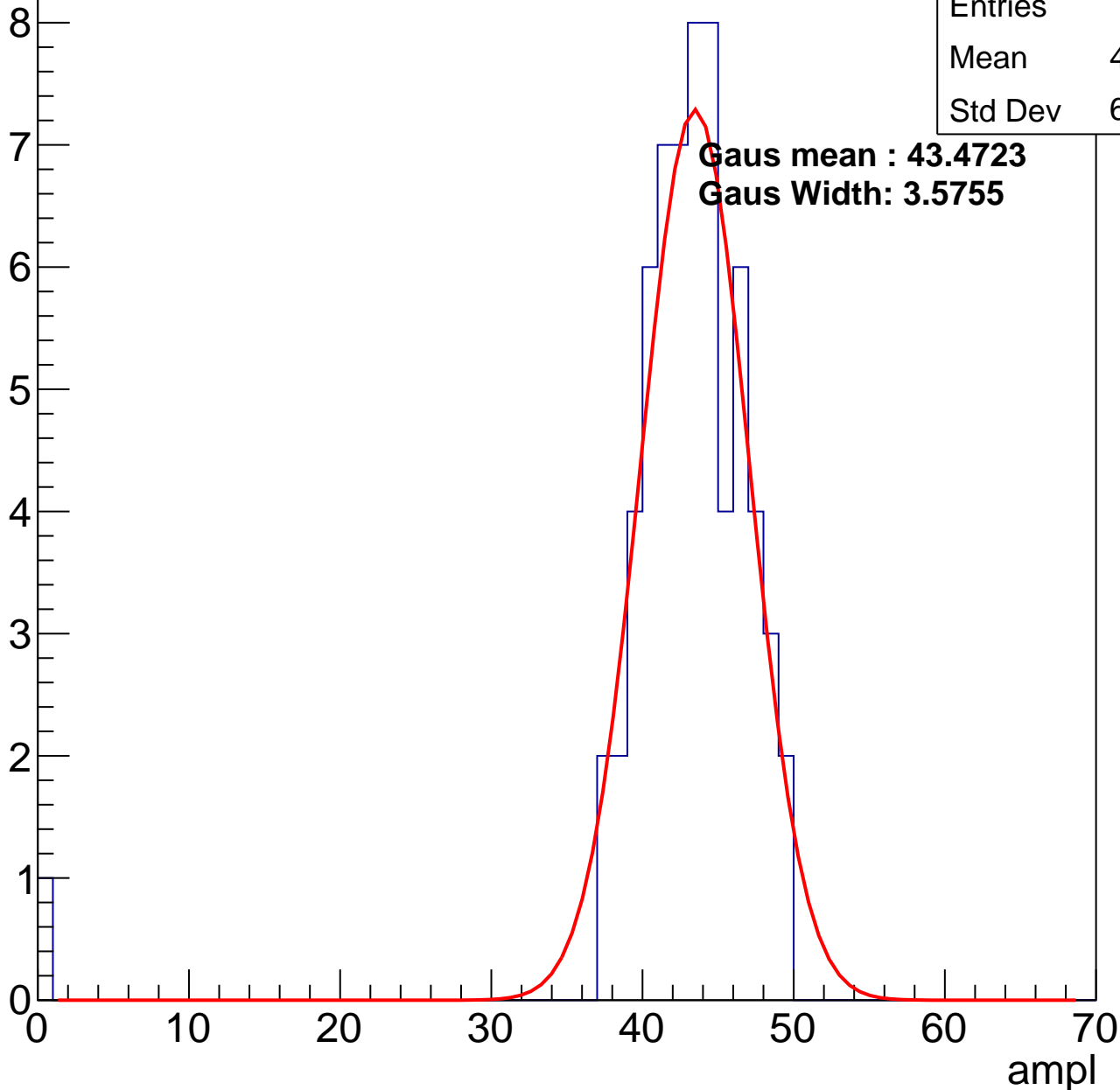
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	42.33
Std Dev	6.103

Gaus mean : 43.4723

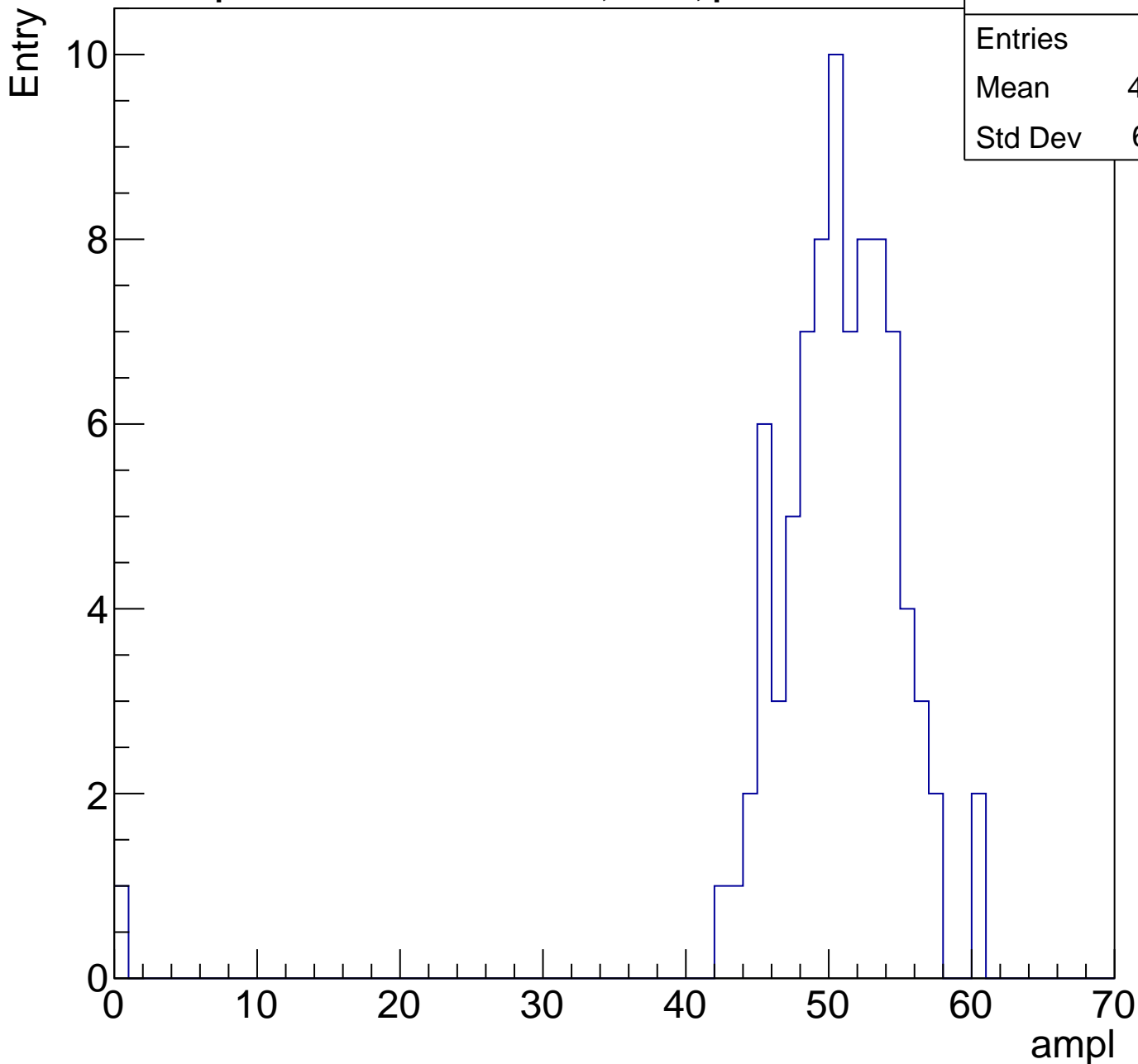
Gaus Width: 3.5755



B1L103S, U13-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	49.89
Std Dev	6.591

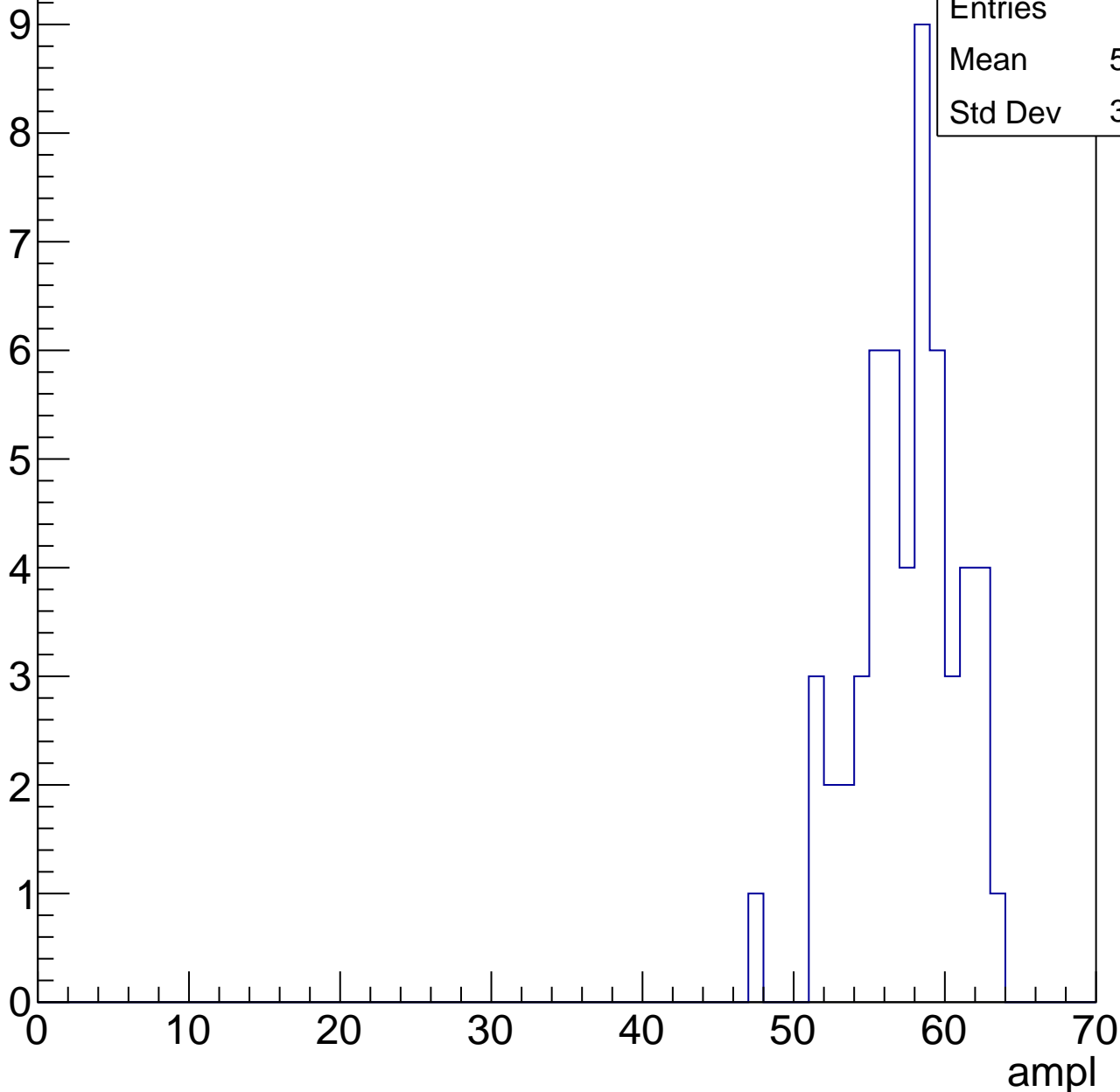


B1L103S, U13-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.98
Std Dev	3.353

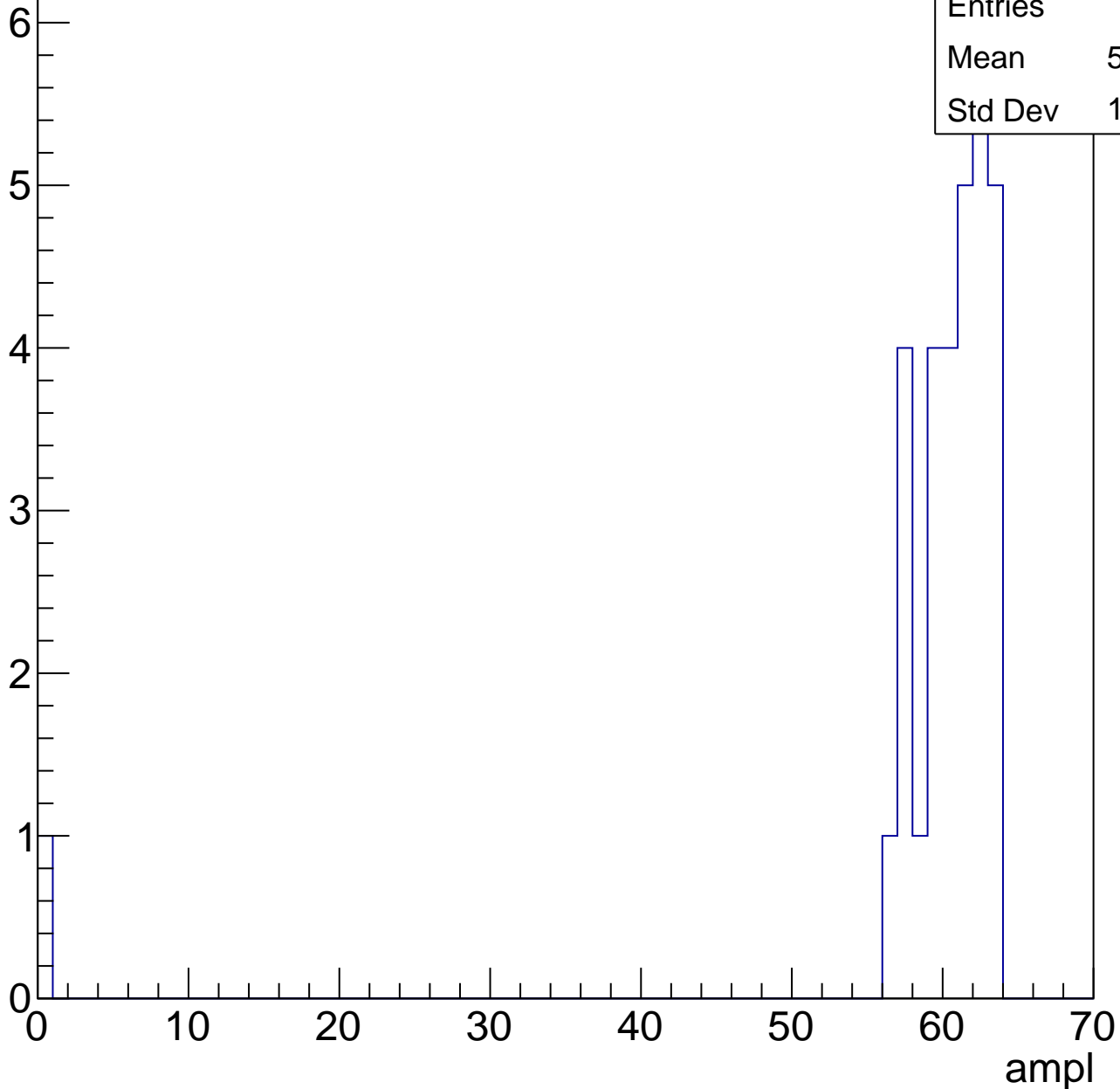


B1L103S, U13-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

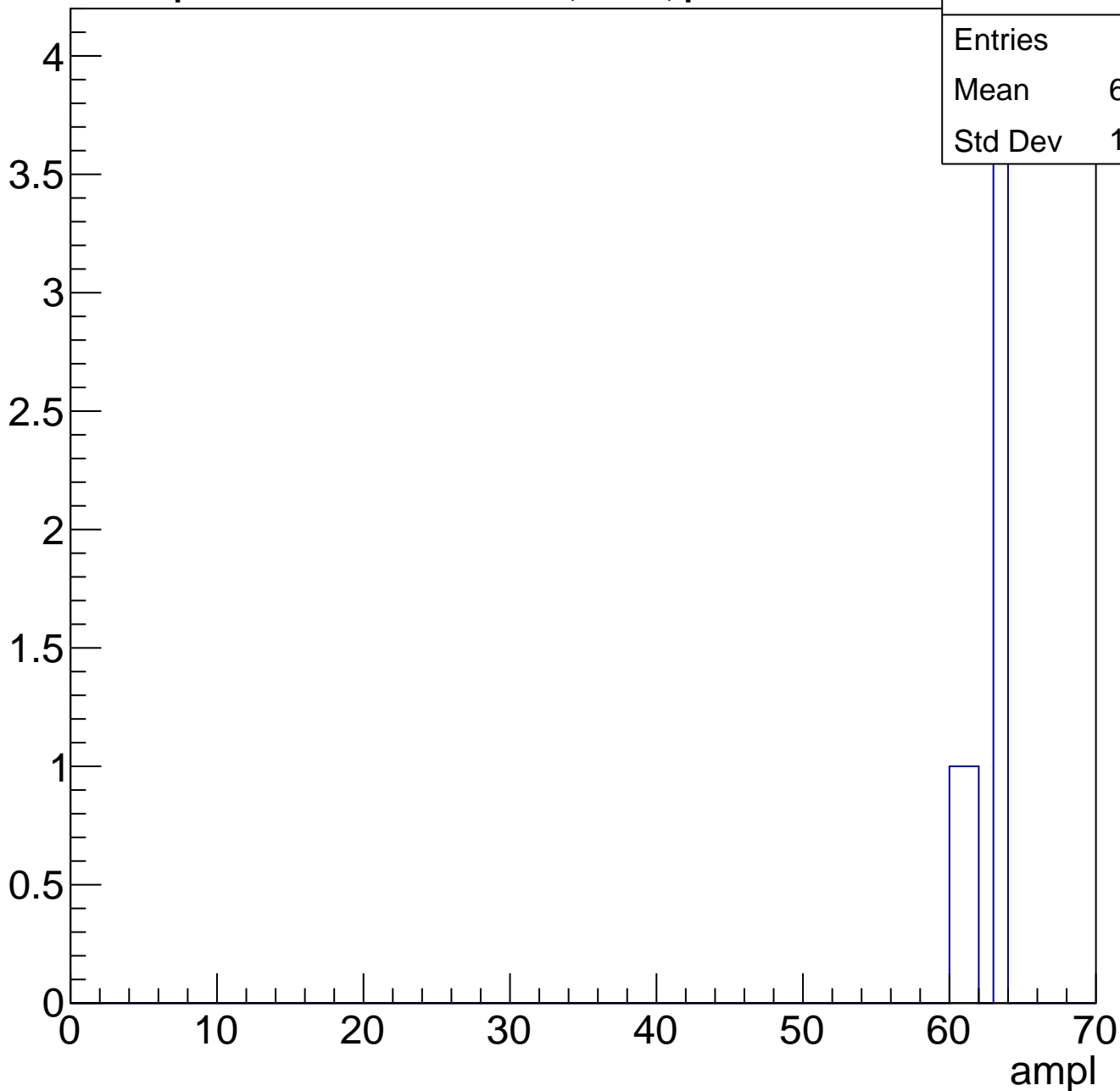
Entries	31
Mean	58.39
Std Dev	10.86



B1L103S, U13-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch95, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	25.97
Std Dev	11.41

Gaus mean : 31.1618

Gaus Width: 4.2137

Entry

12

10

8

6

4

2

0

0

10

20

30

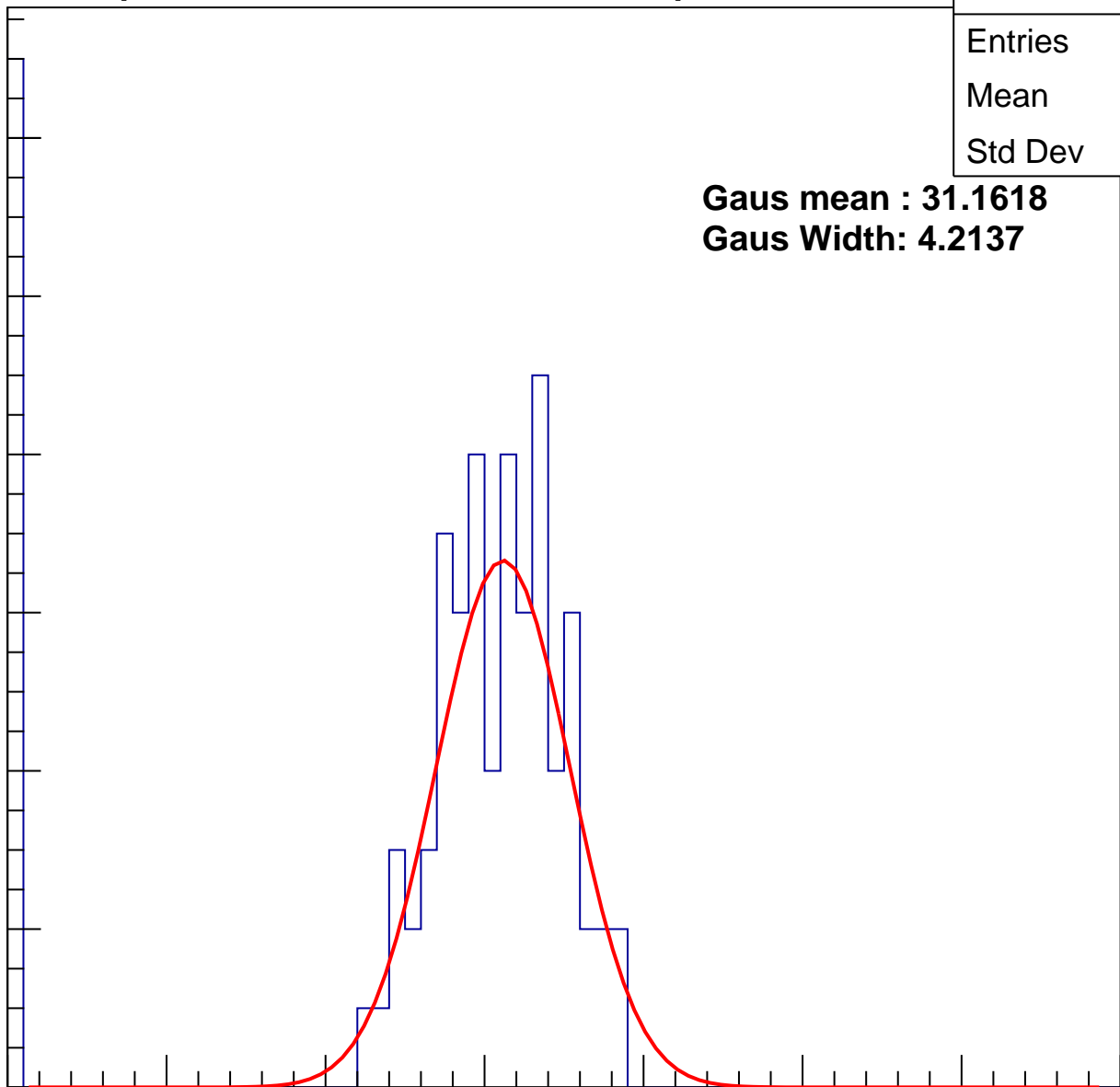
40

50

60

70

ampl



B1L103S, U13-ch95, adc1

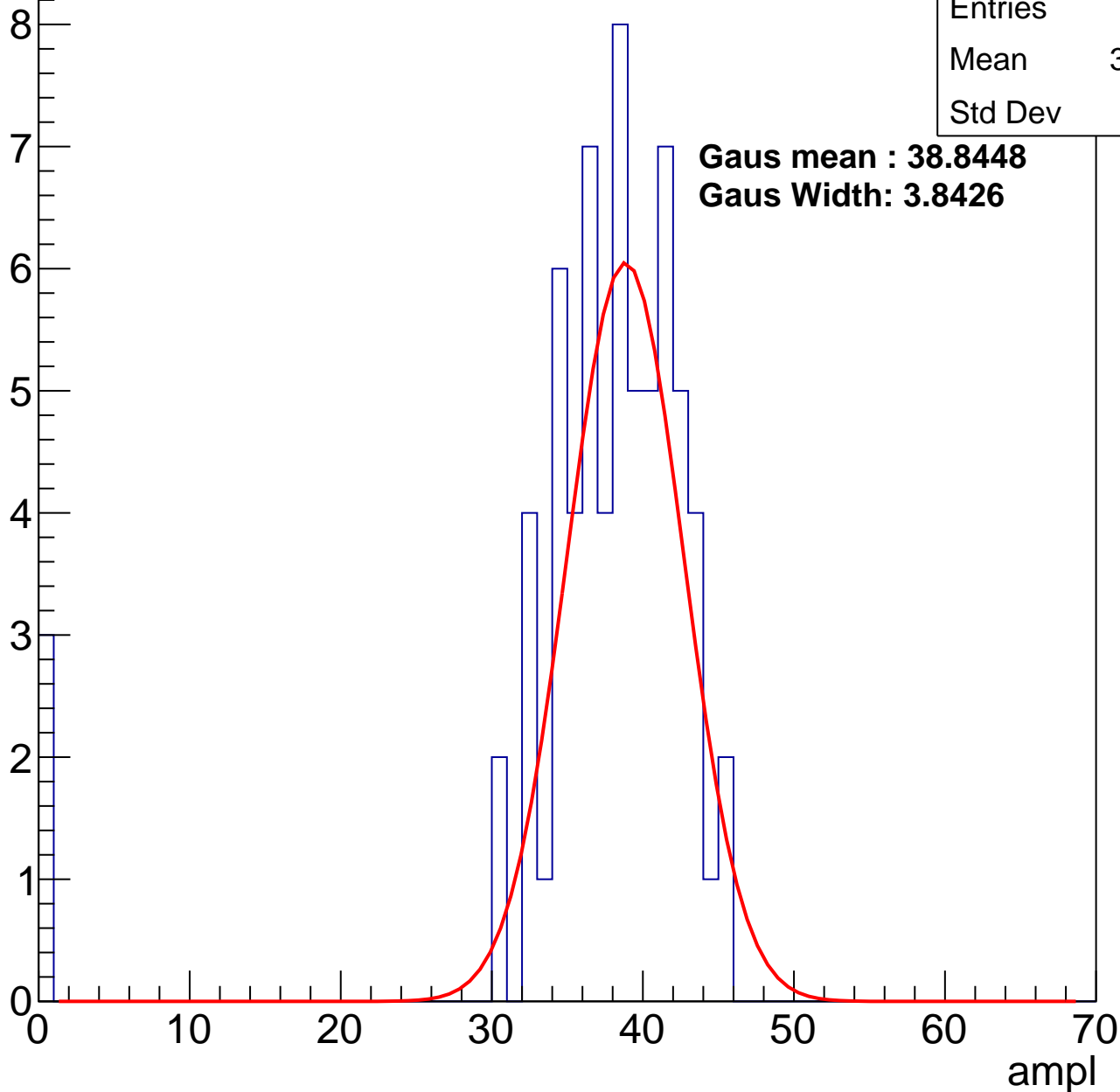
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	36.28
Std Dev	8.57

Gaus mean : 38.8448

Gaus Width: 3.8426



B1L103S, U13-ch95, adc2

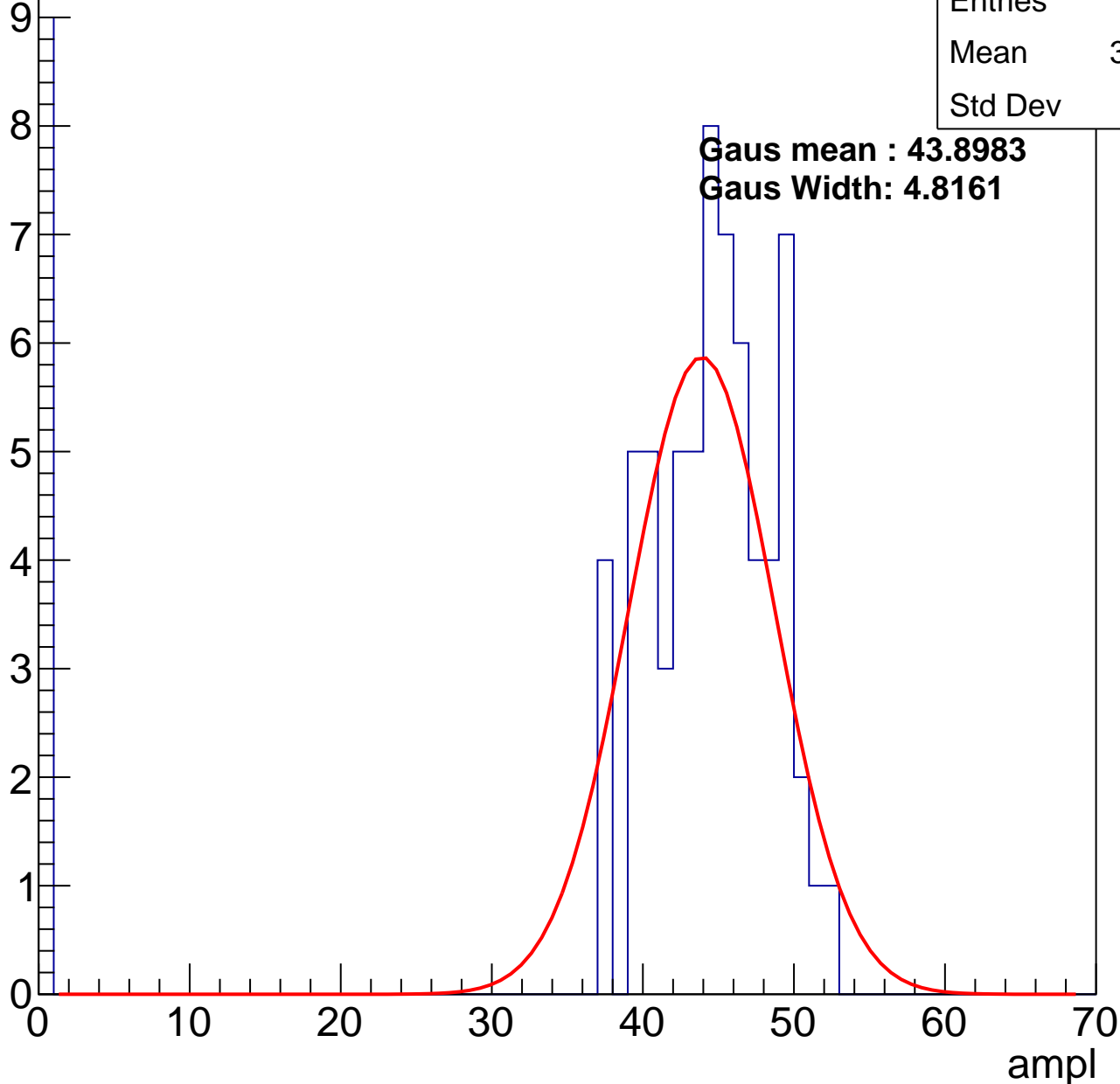
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	38.95
Std Dev	14.7

Gaus mean : 43.8983

Gaus Width: 4.8161

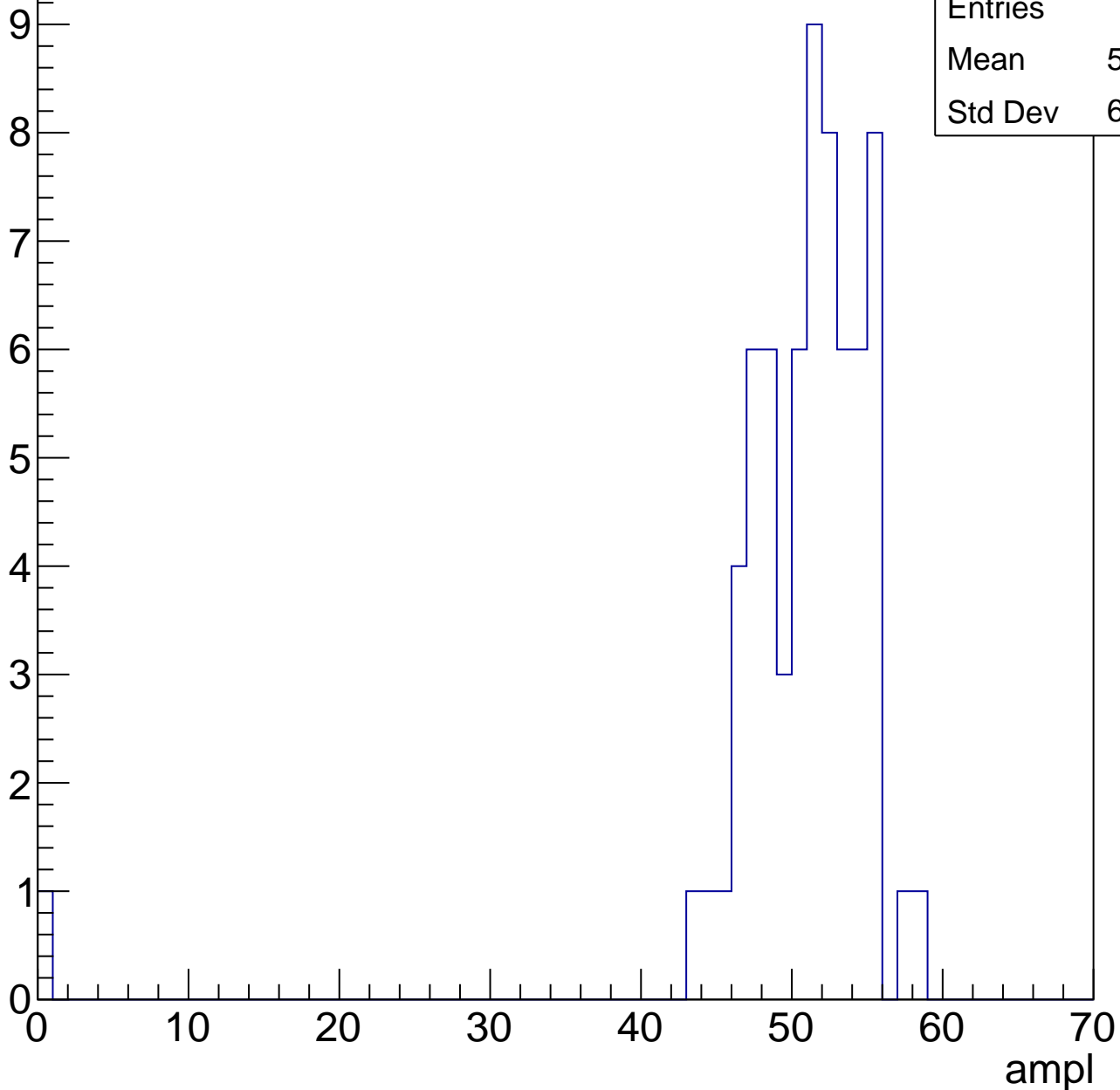


B1L103S, U13-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

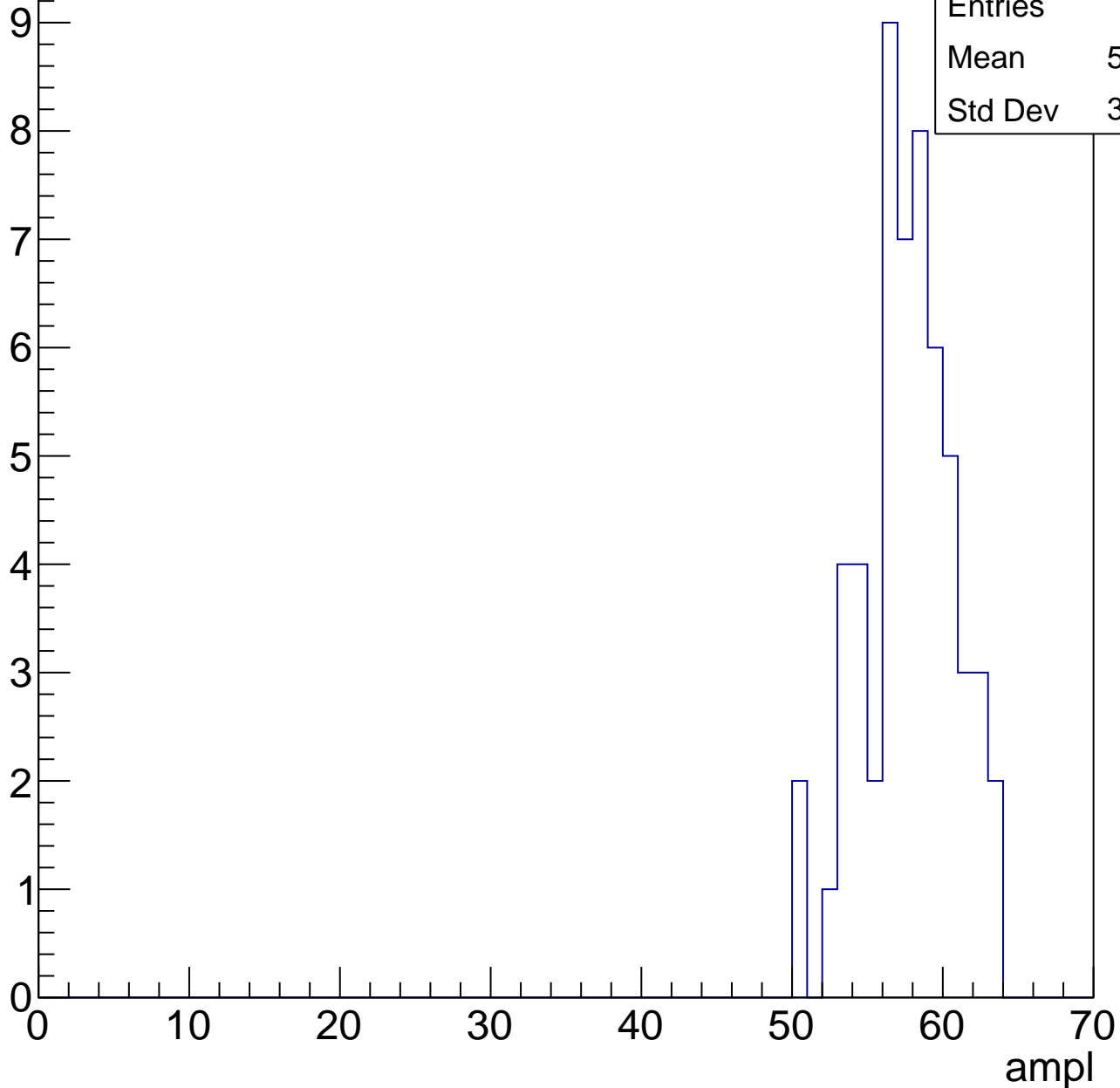
Entries	68
Mean	50.07
Std Dev	6.923



B1L103S, U13-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



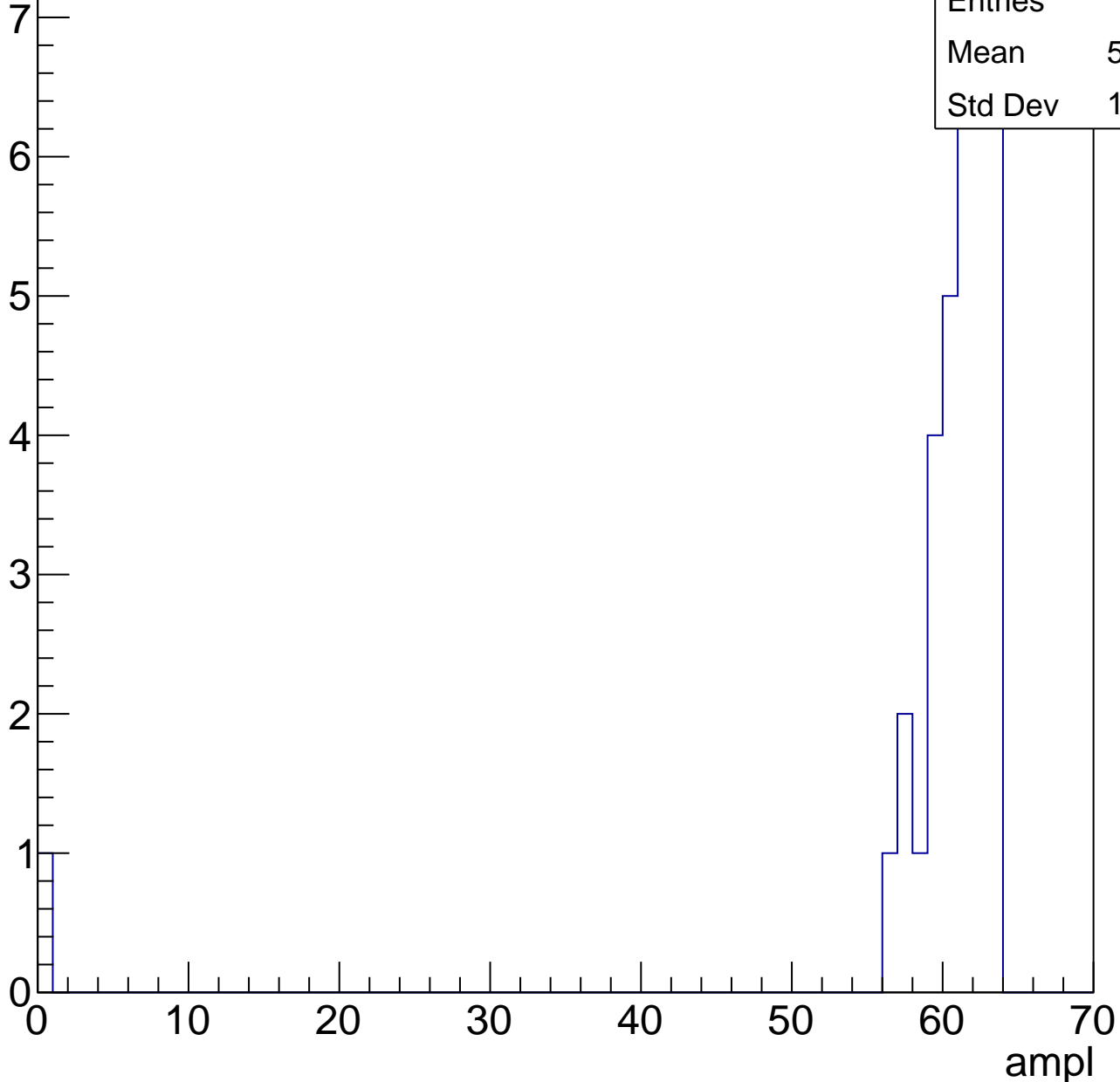
Entries	56
Mean	57.25
Std Dev	3.013

B1L103S, U13-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	59.03
Std Dev	10.29



B1L103S, U13-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch96, adc0

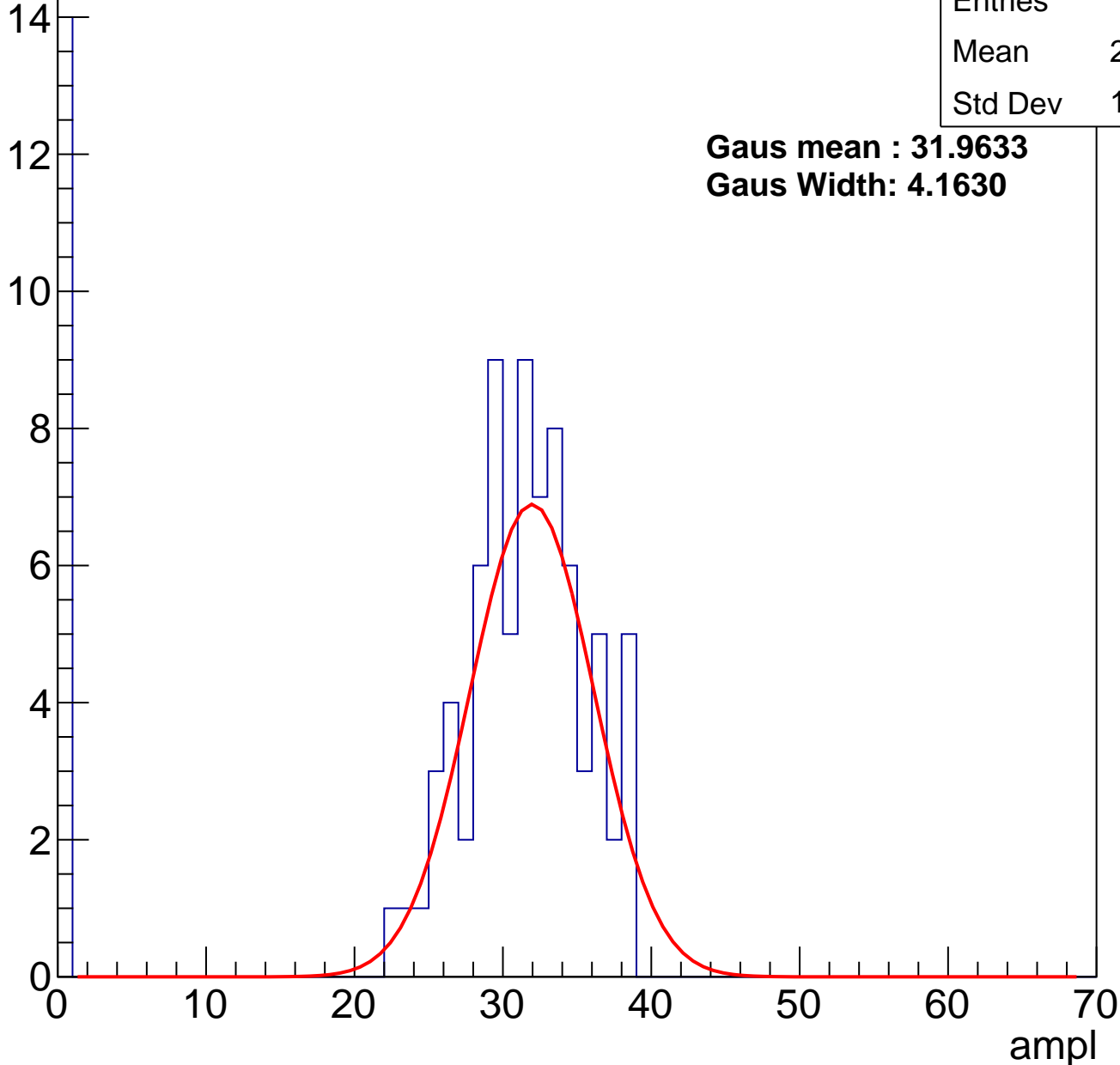
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	26.38
Std Dev	11.78

Gaus mean : 31.9633

Gaus Width: 4.1630

Entry



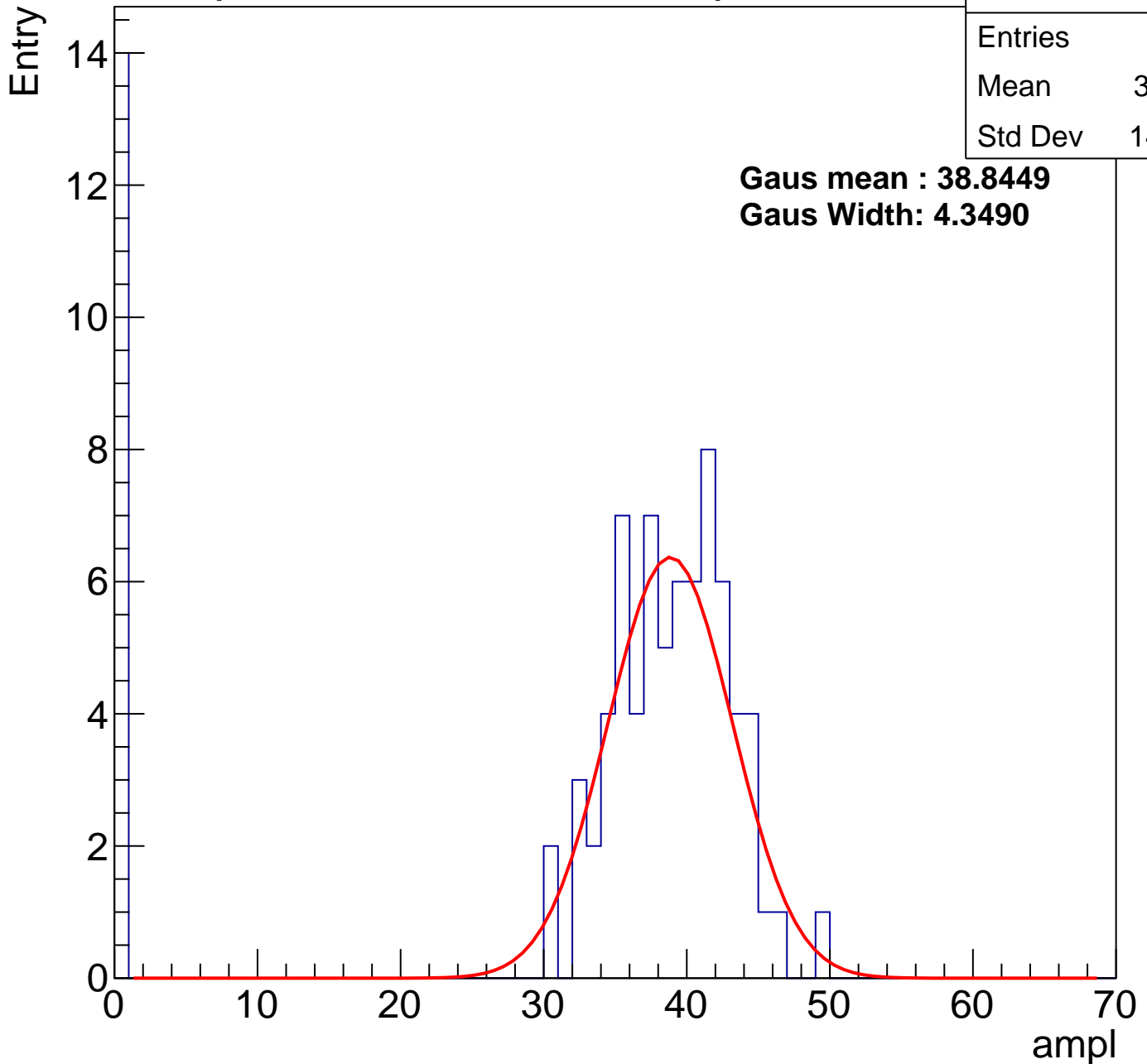
B1L103S, U13-ch96, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	32.21
Std Dev	14.74

Gaus mean : 38.8449

Gaus Width: 4.3490



B1L103S, U13-ch96, adc2

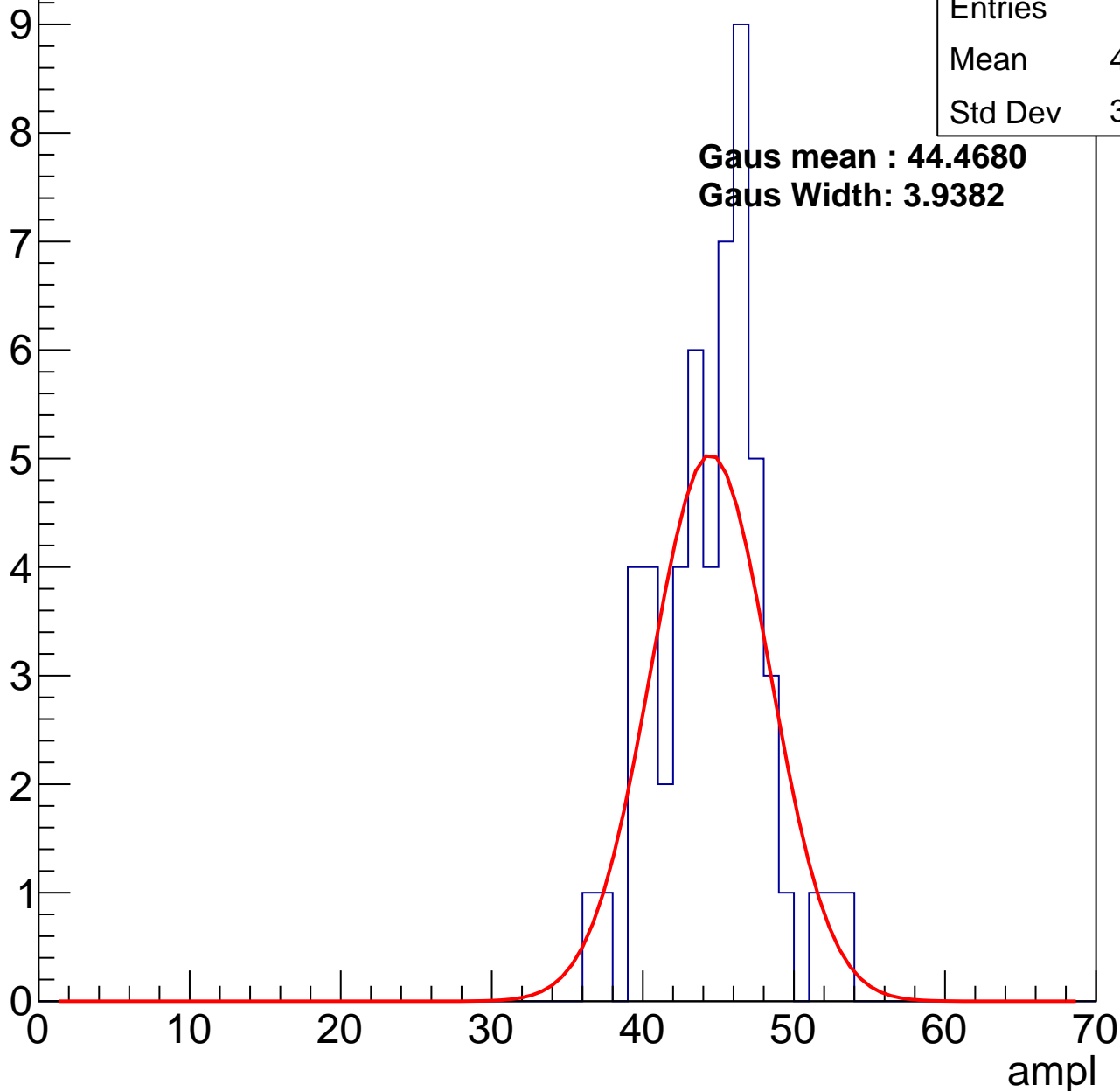
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	44.19
Std Dev	3.523

Gaus mean : 44.4680

Gaus Width: 3.9382



B1L103S, U13-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

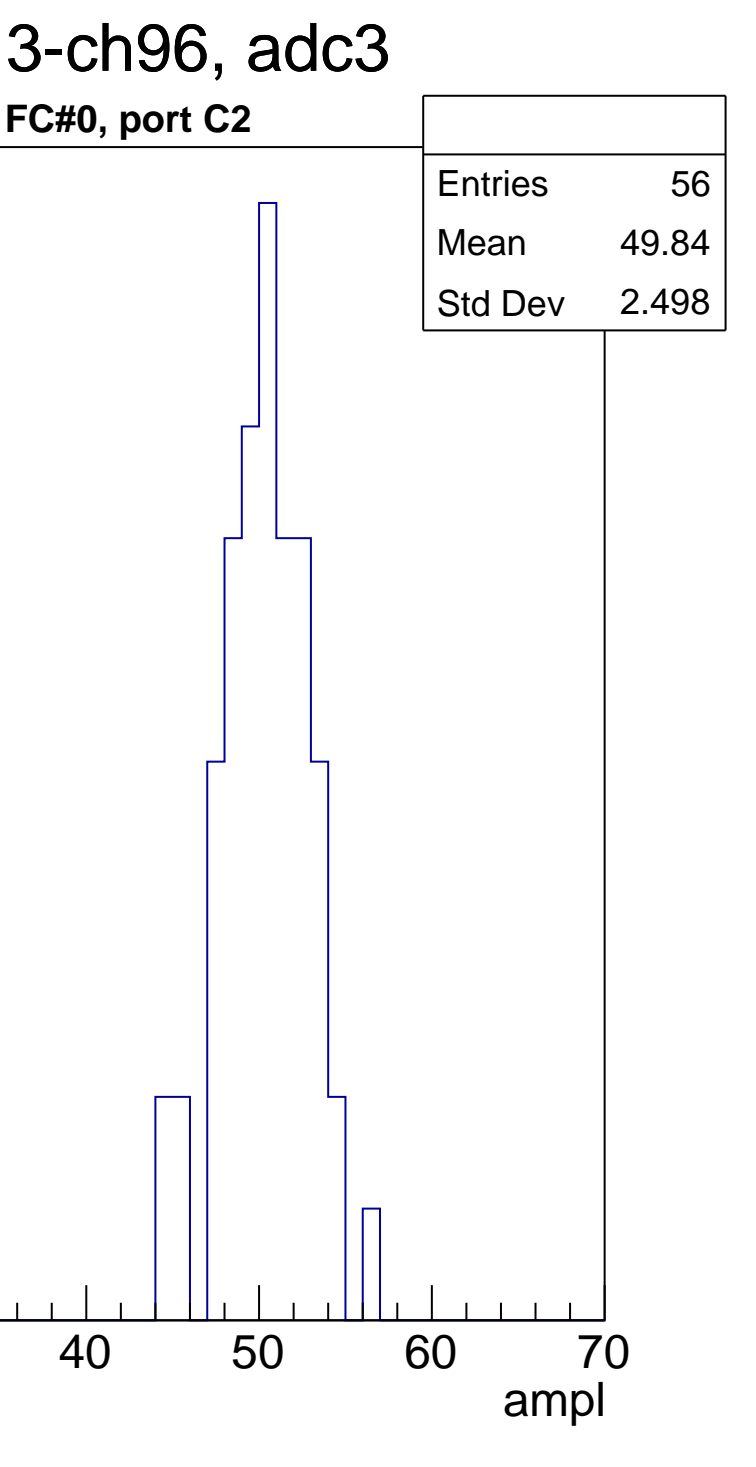
Entries	56
Mean	49.84
Std Dev	2.498

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

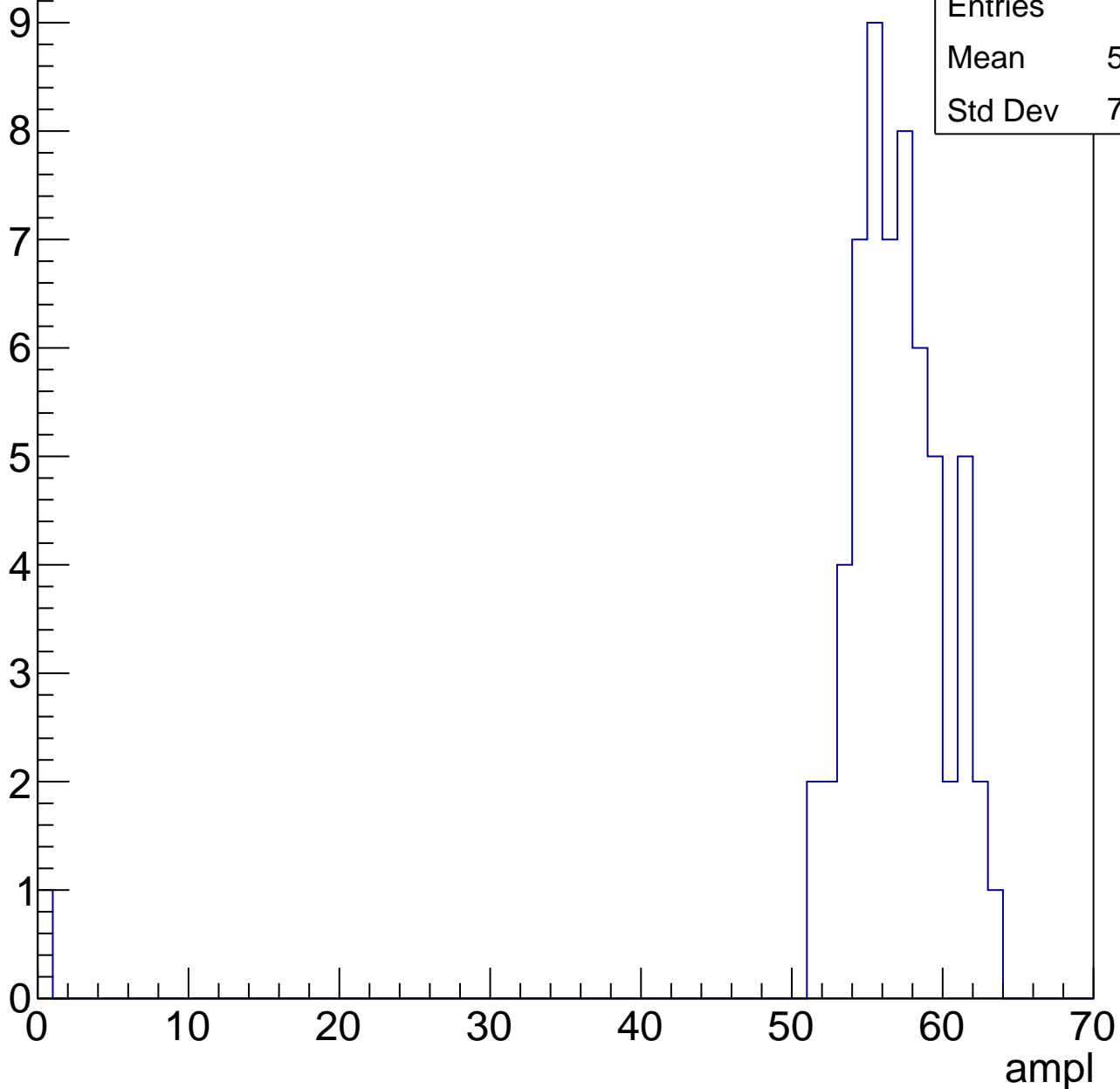


B1L103S, U13-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

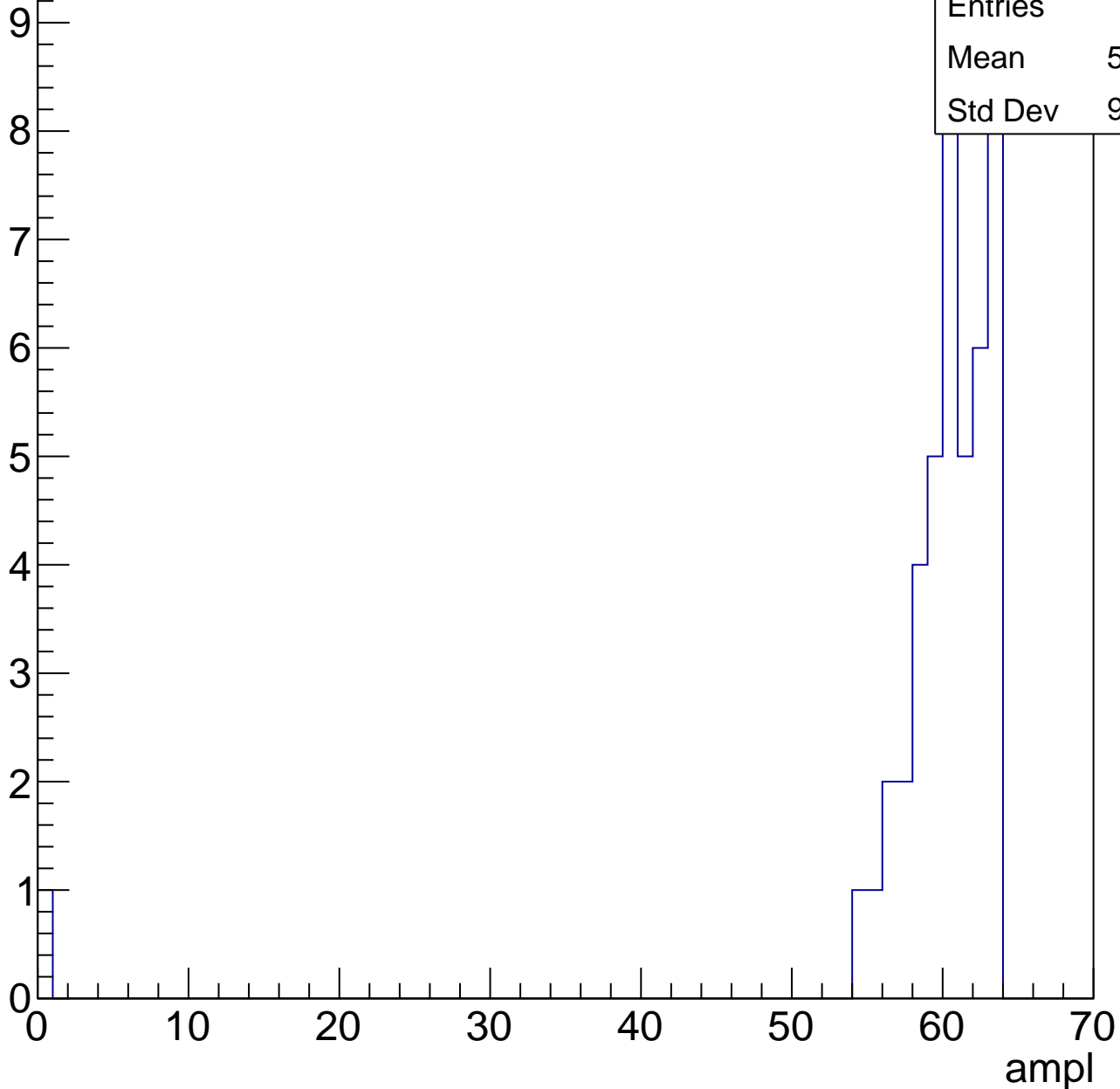
Entries	61
Mean	55.64
Std Dev	7.723



B1L103S, U13-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch97, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	24.57
Std Dev	11.68

Gaus mean : 30.2841

Gaus Width: 3.6567

Entry

12

10

8

6

4

2

0

0

10

20

30

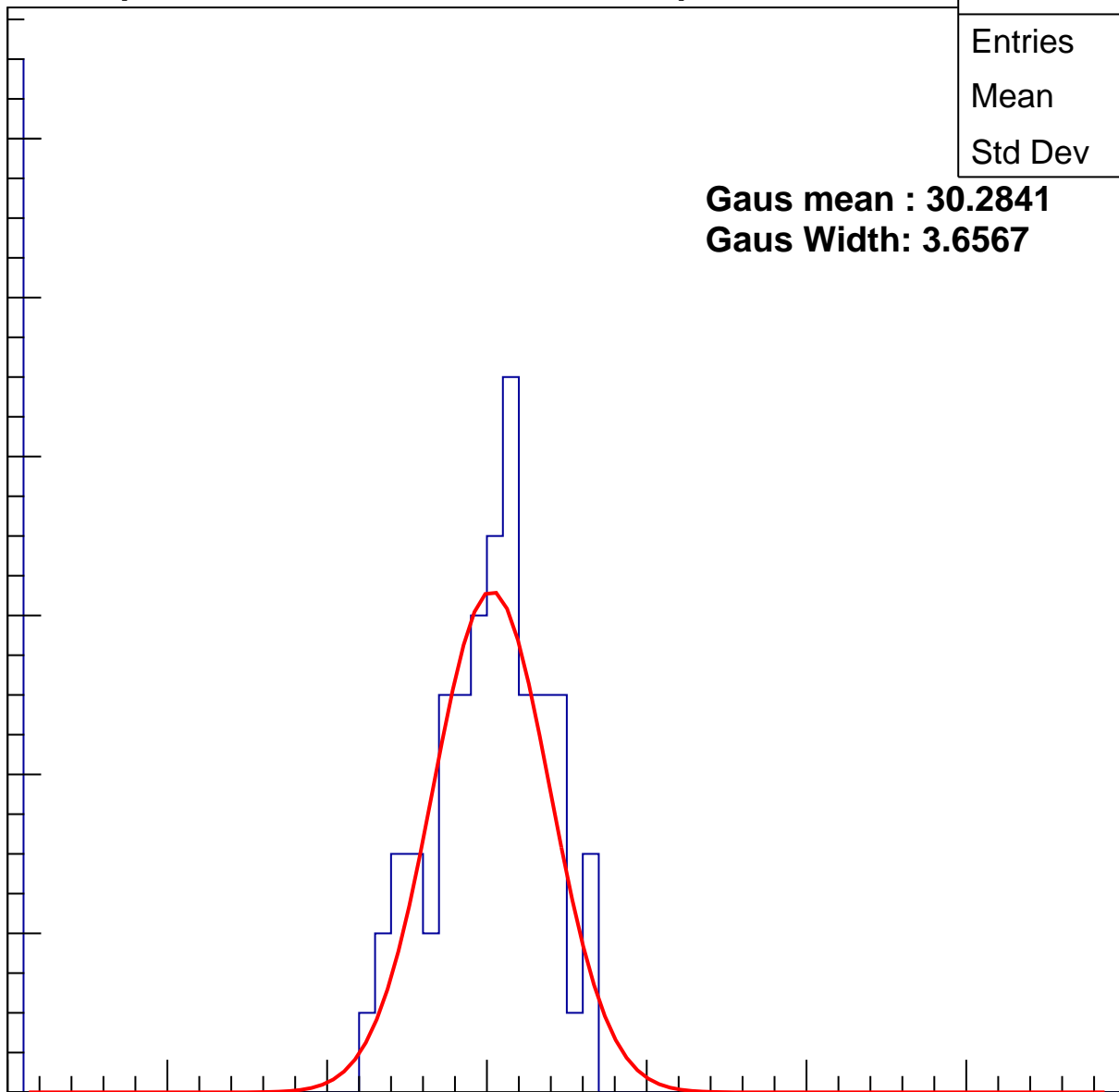
40

50

60

70

ampl



B1L103S, U13-ch97, adc1

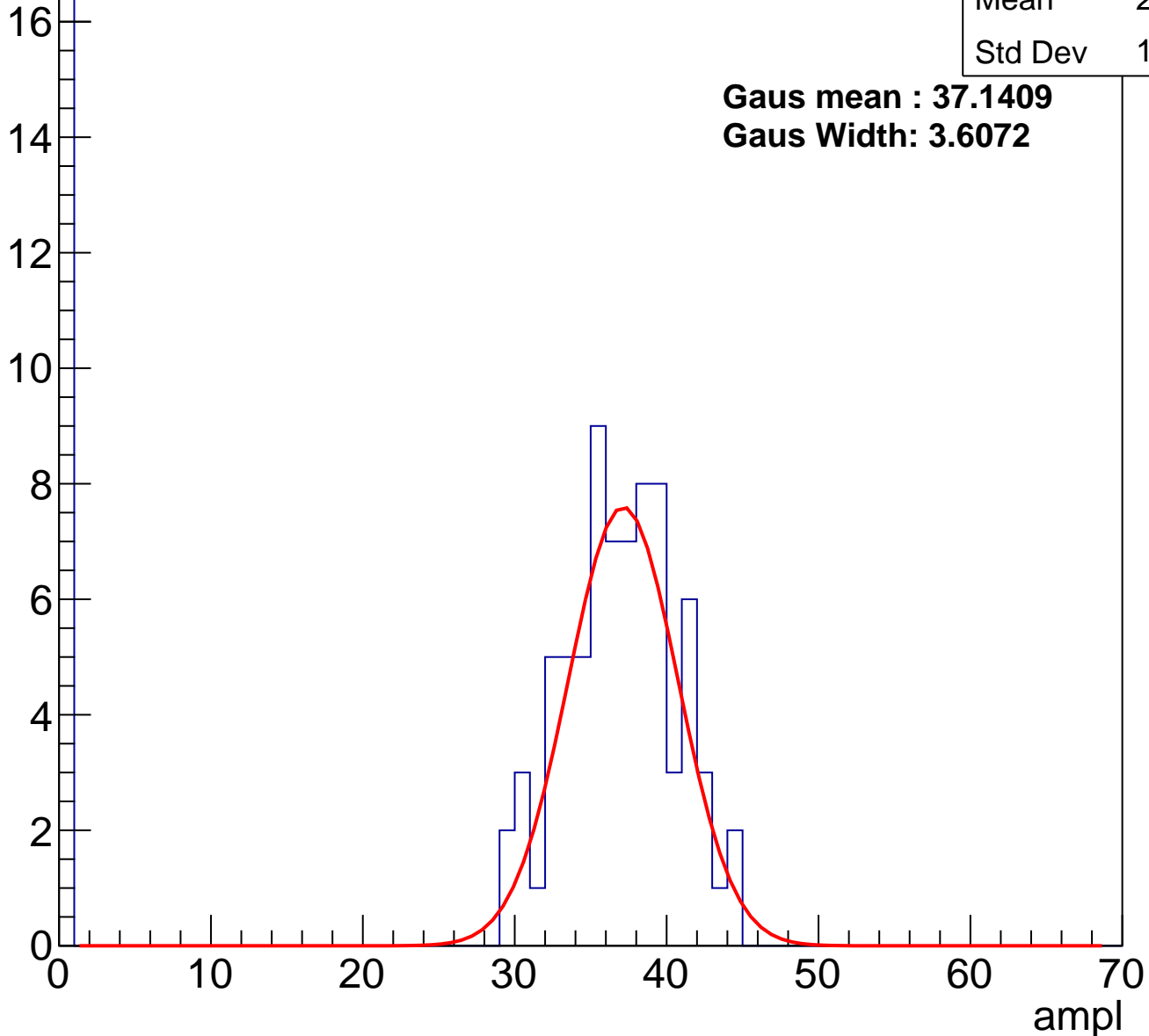
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	29.77
Std Dev	14.54

Gaus mean : 37.1409

Gaus Width: 3.6072

Entry



B1L103S, U13-ch97, adc2

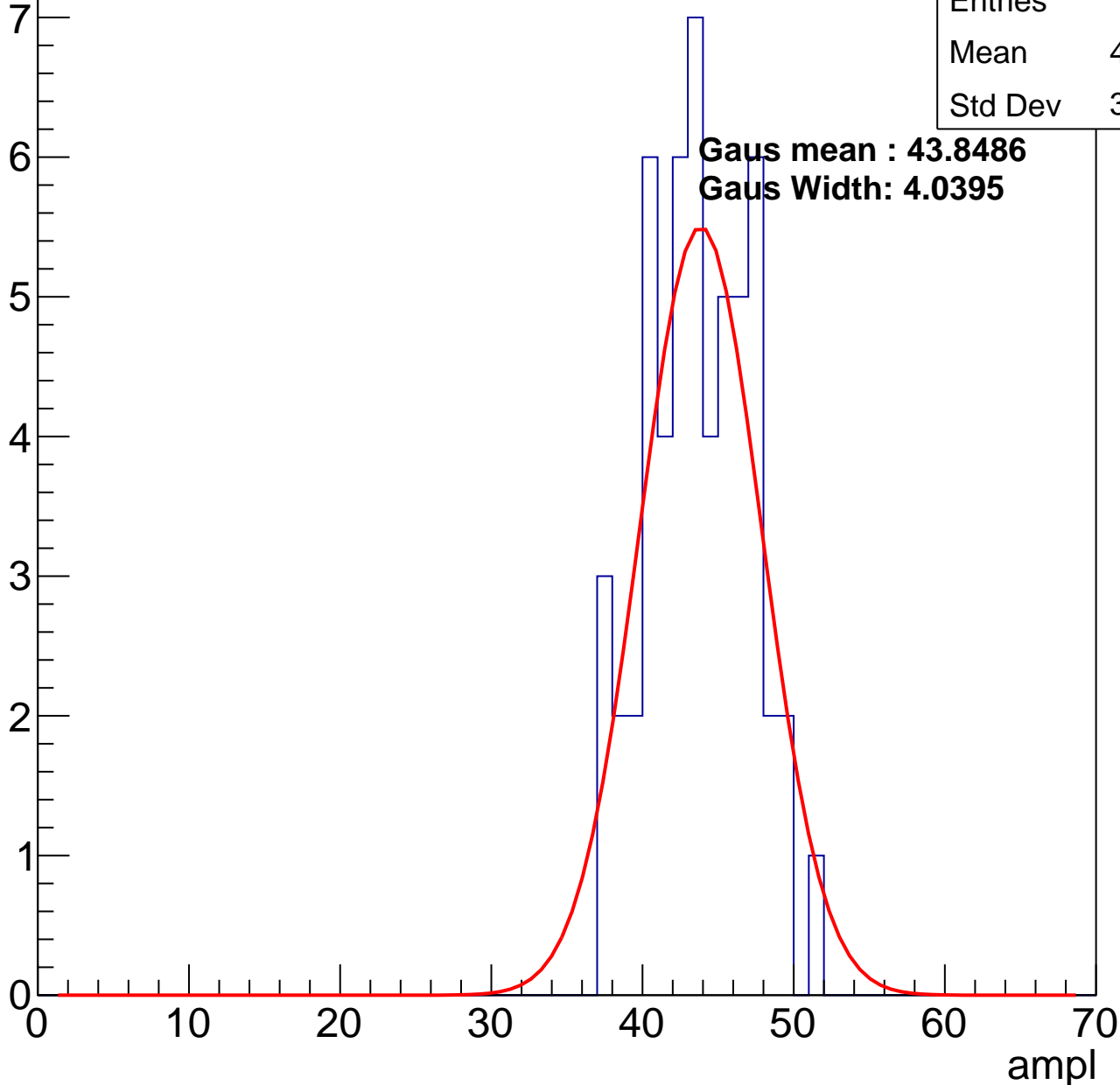
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	43.27
Std Dev	3.338

Gaus mean : 43.8486

Gaus Width: 4.0395

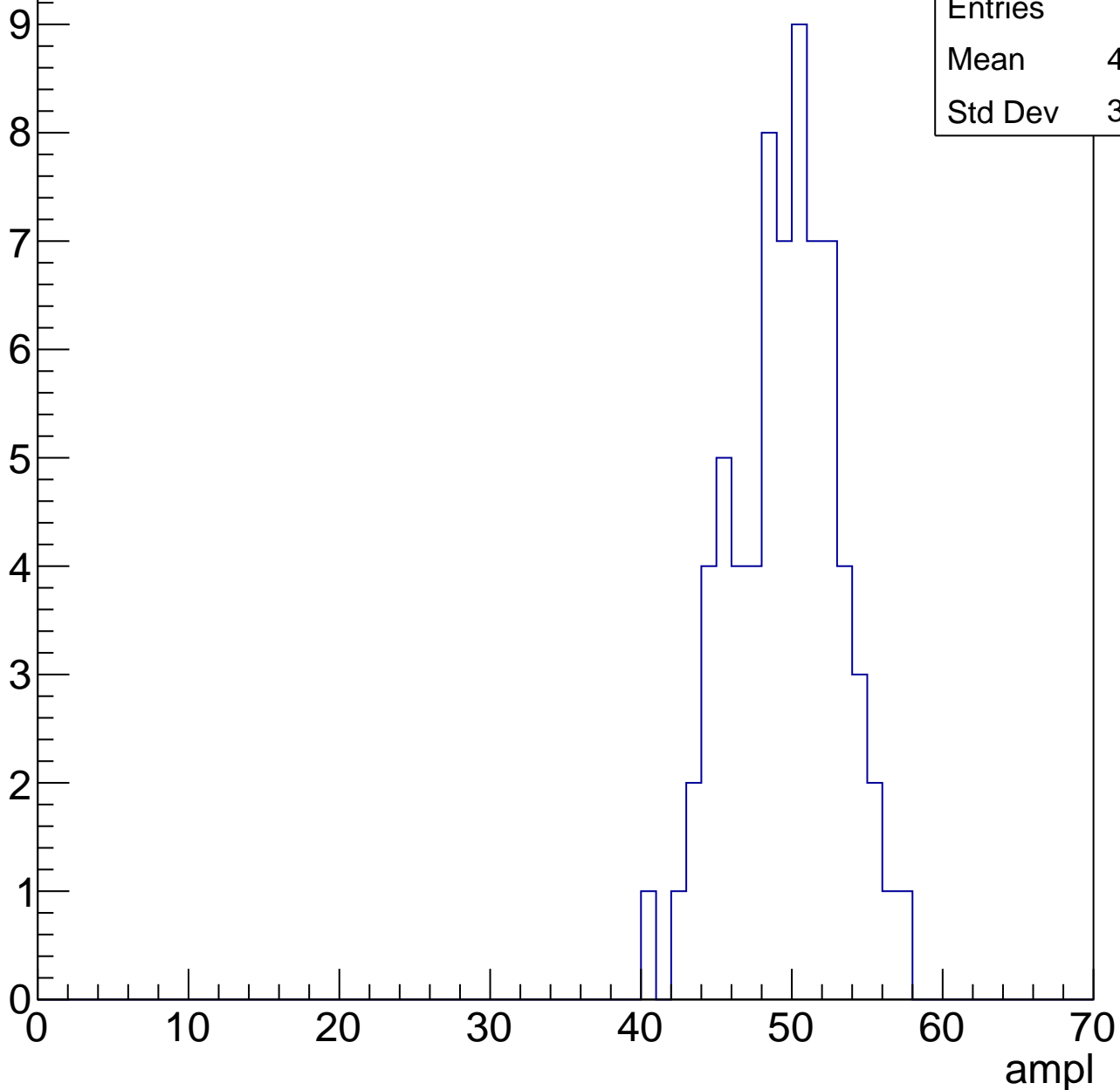


B1L103S, U13-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

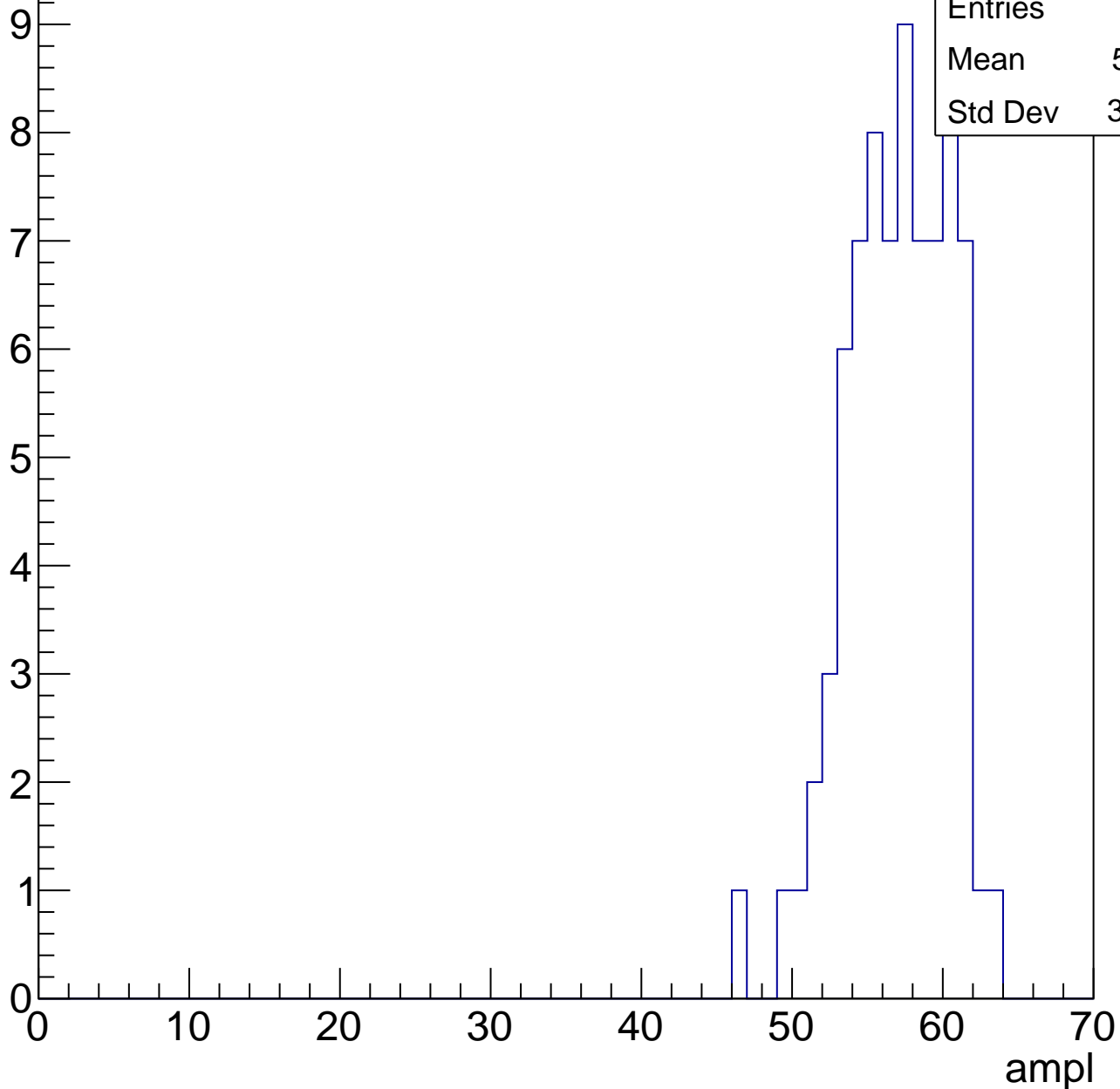
Entries	70
Mean	49.09
Std Dev	3.516



B1L103S, U13-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



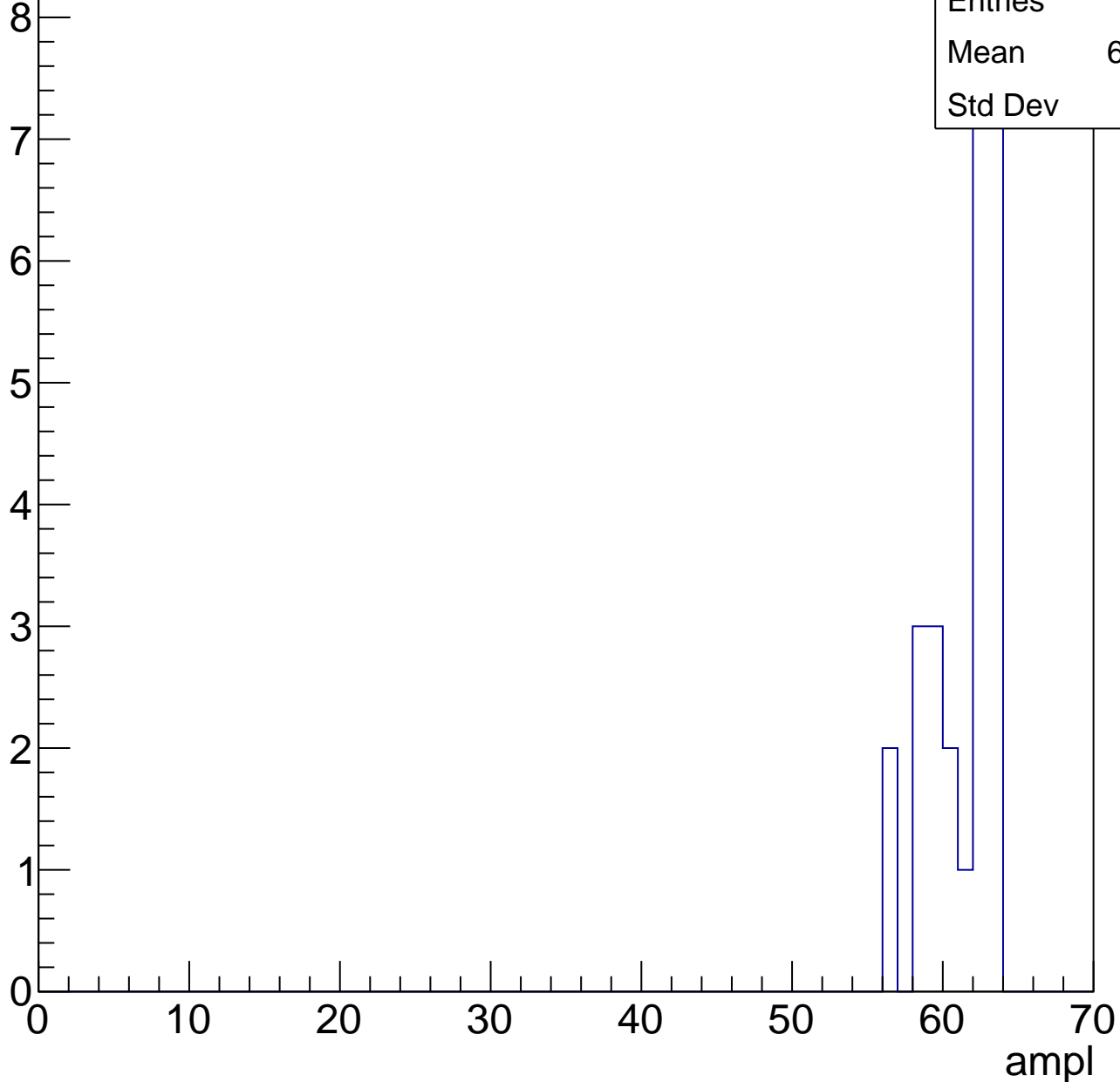
Entries	76
Mean	56.51
Std Dev	3.327

B1L103S, U13-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

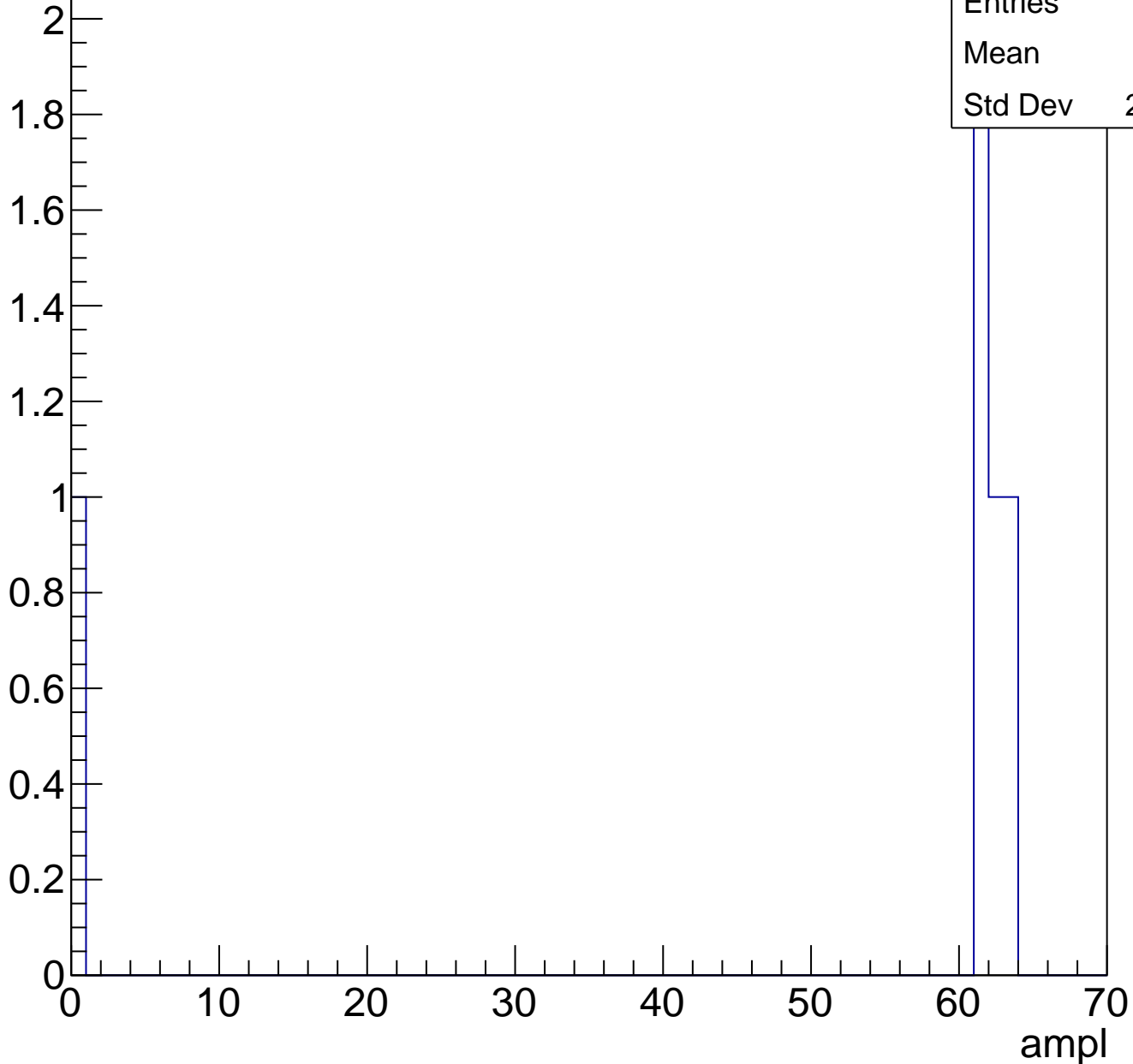
Entries	27
Mean	60.89
Std Dev	2.2



B1L103S, U13-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch98, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	19.79
Std Dev	13.39

Gaus mean : 29.8572

Gaus Width: 3.5042

Entry

25

20

15

10

5

0

0

10

20

30

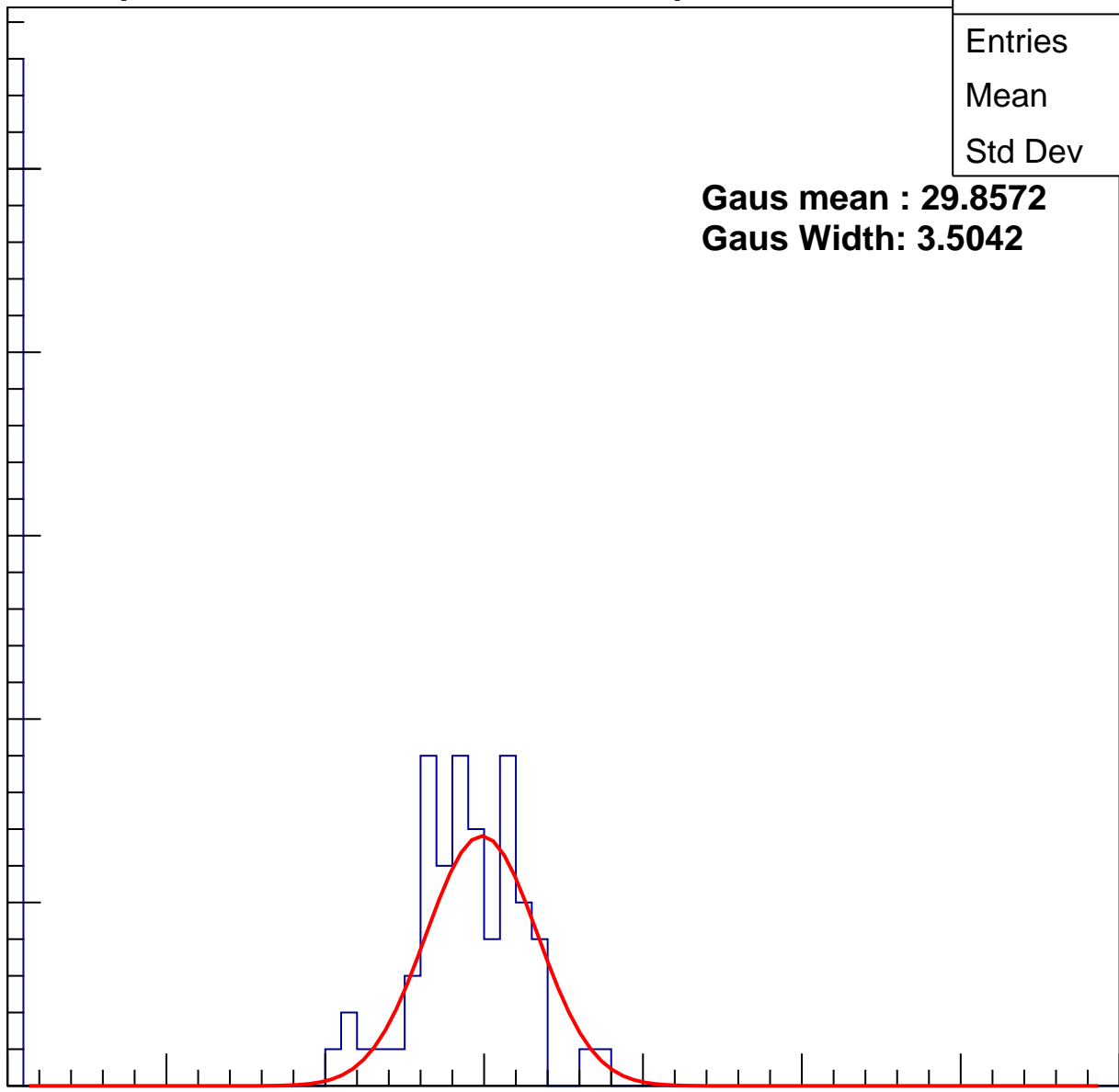
40

50

60

70

ampl



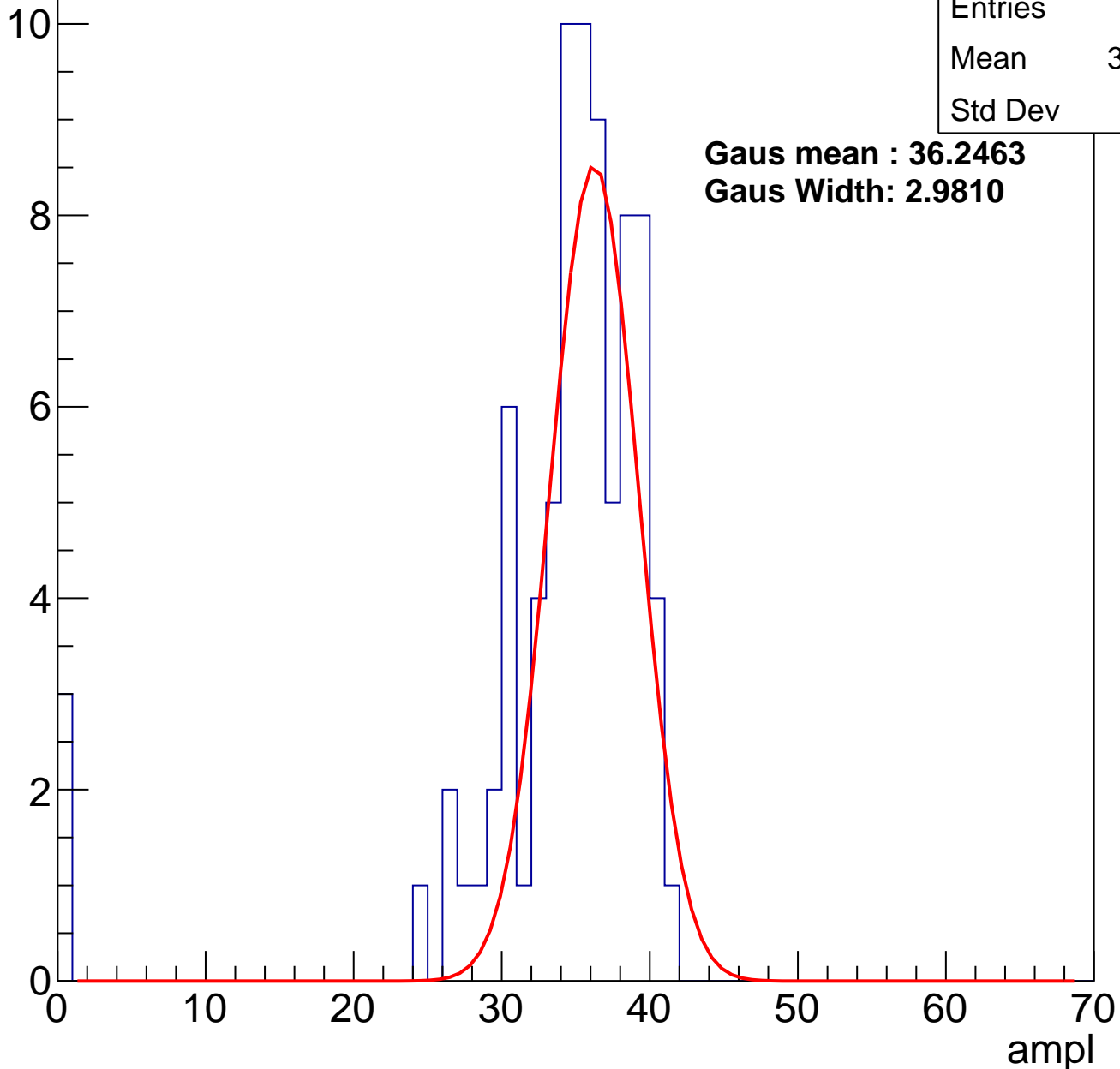
B1L103S, U13-ch98, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	33.44
Std Dev	7.49

Gaus mean : 36.2463
Gaus Width: 2.9810

Entry



B1L103S, U13-ch98, adc2

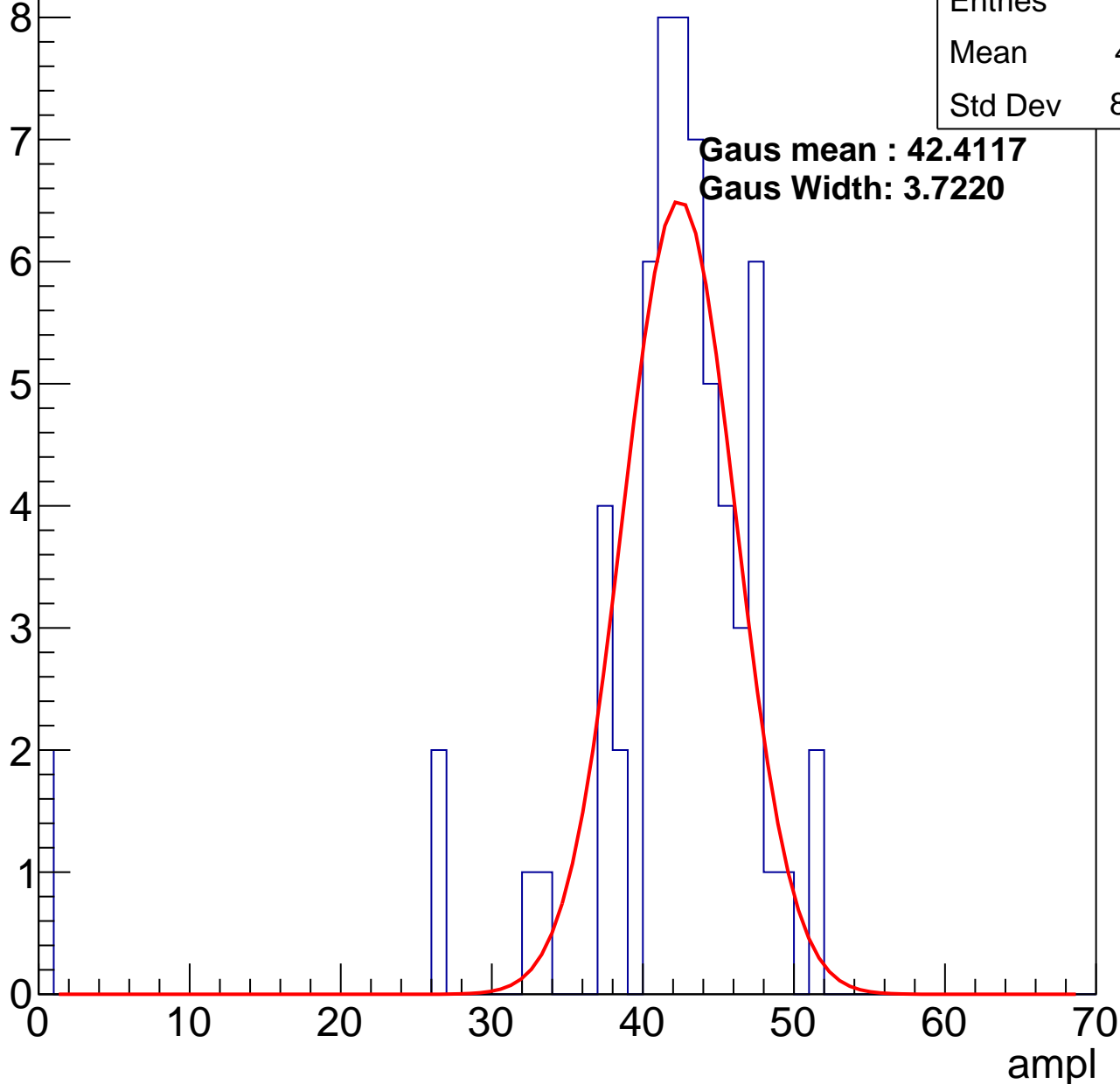
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	40.71
Std Dev	8.719

Gaus mean : 42.4117

Gaus Width: 3.7220

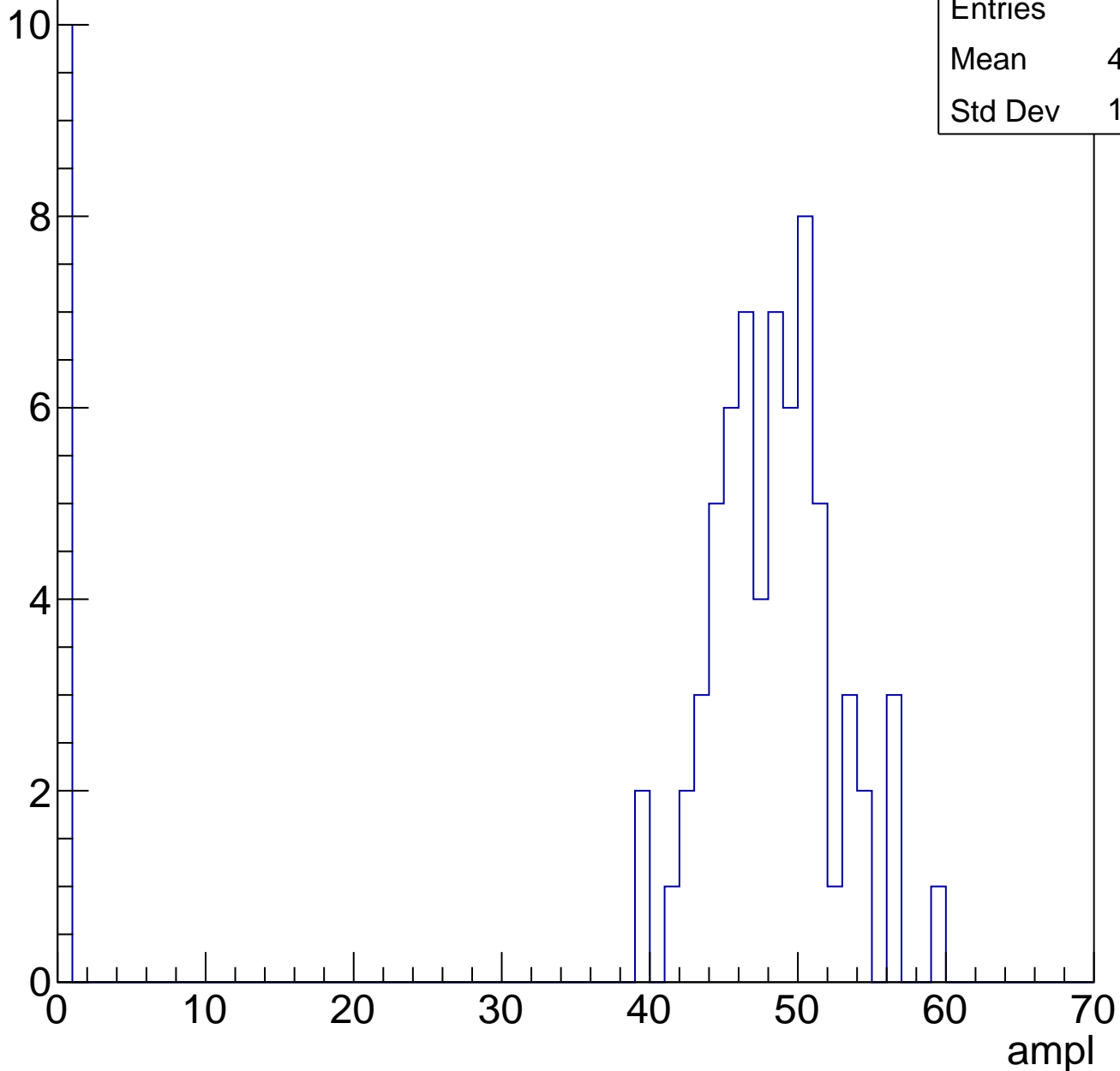


B1L103S, U13-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	41.62
Std Dev	16.63

Entry

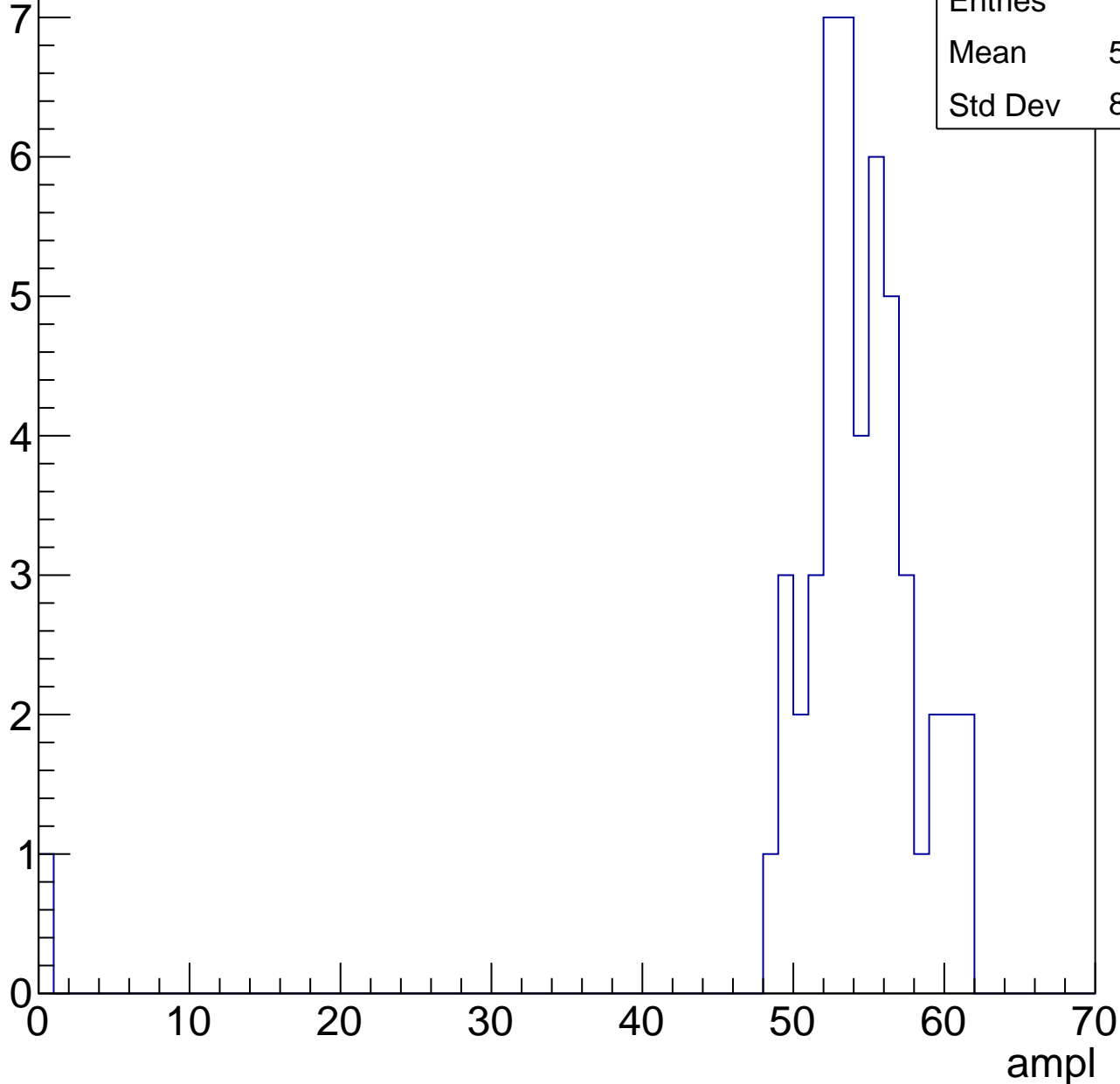


B1L103S, U13-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	53.02
Std Dev	8.284

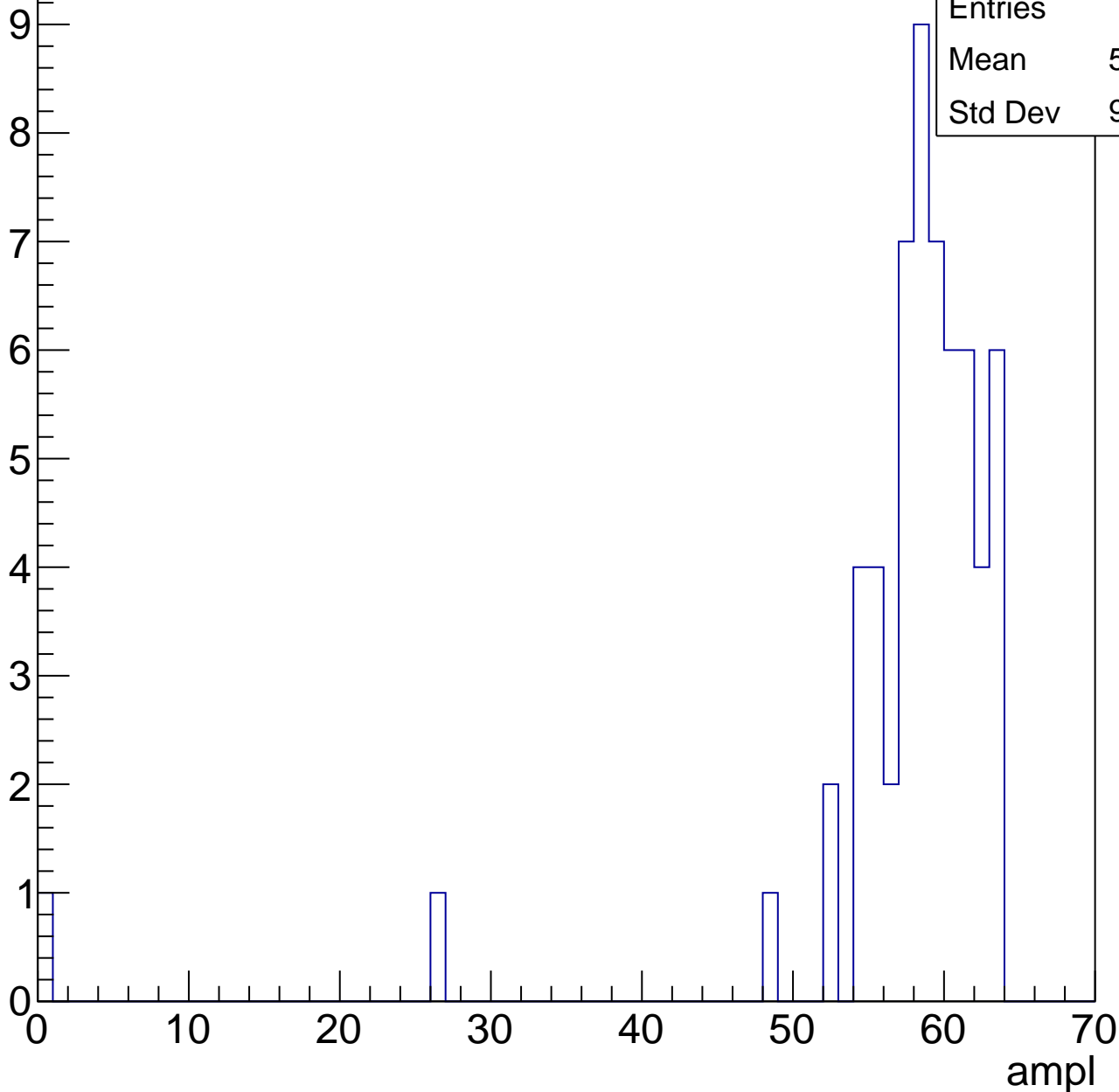


B1L103S, U13-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	56.87
Std Dev	9.034

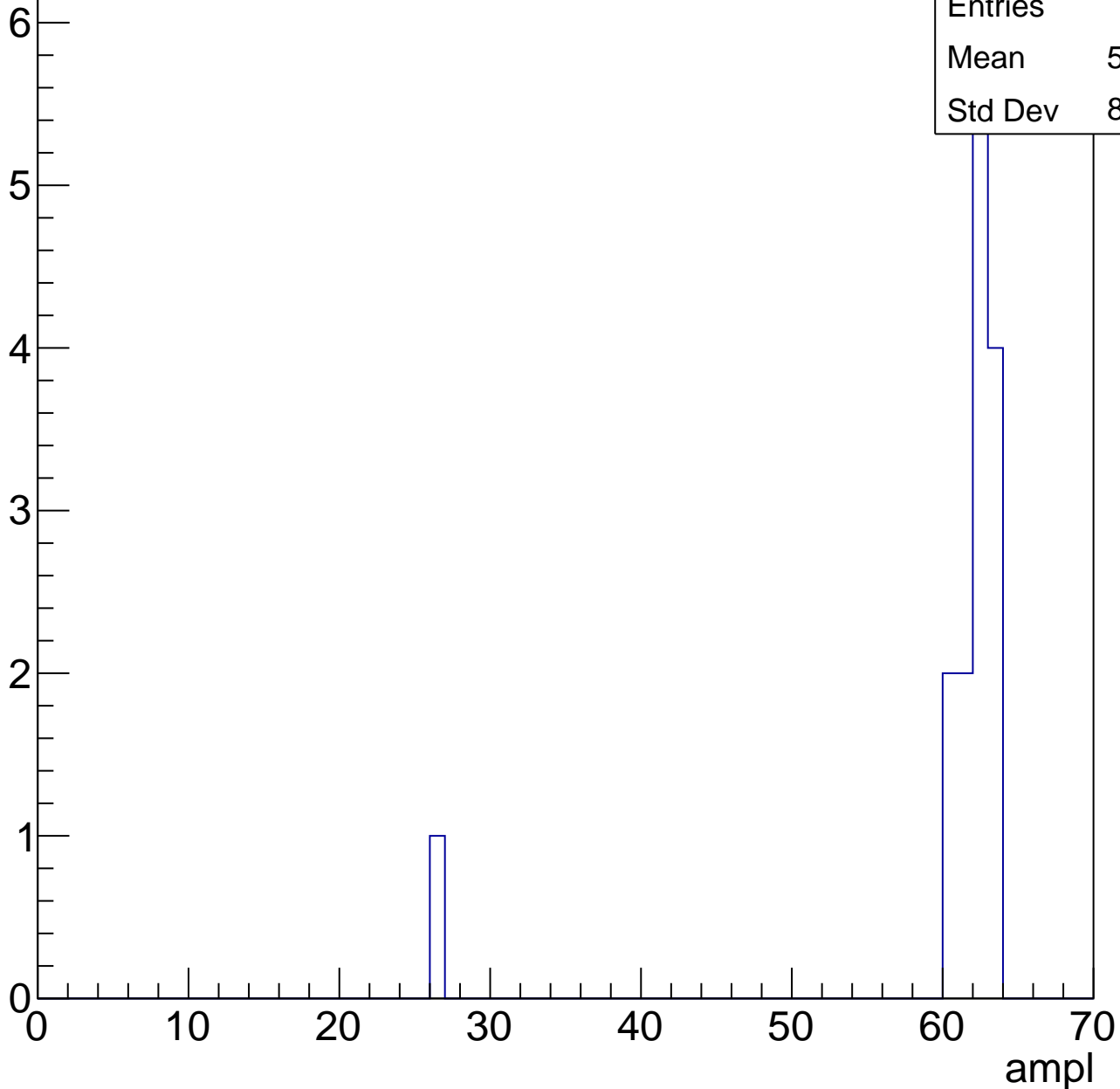


B1L103S, U13-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	59.47
Std Dev	8.995



B1L103S, U13-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch99, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	23.61
Std Dev	10.91

Gaus mean : 28.7738

Gaus Width: 4.1367

Entry

12

10

8

6

4

2

0

0

10

20

30

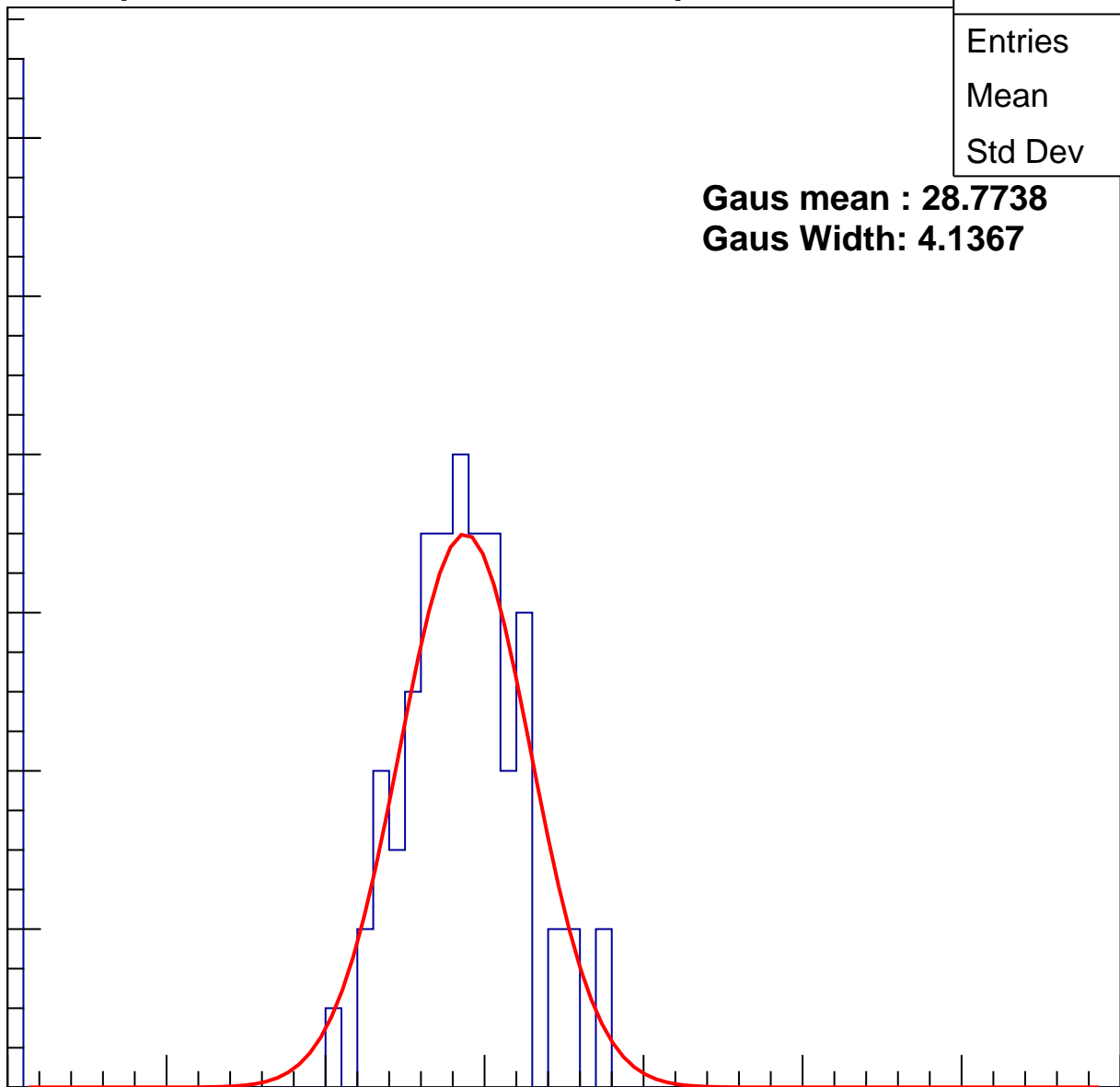
40

50

60

70

ampl



B1L103S, U13-ch99, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	111
Mean	32.36
Std Dev	12.44

Gaus mean : 37.0904

Gaus Width: 4.8060

Entry

12

10

8

6

4

2

0

0

10

20

30

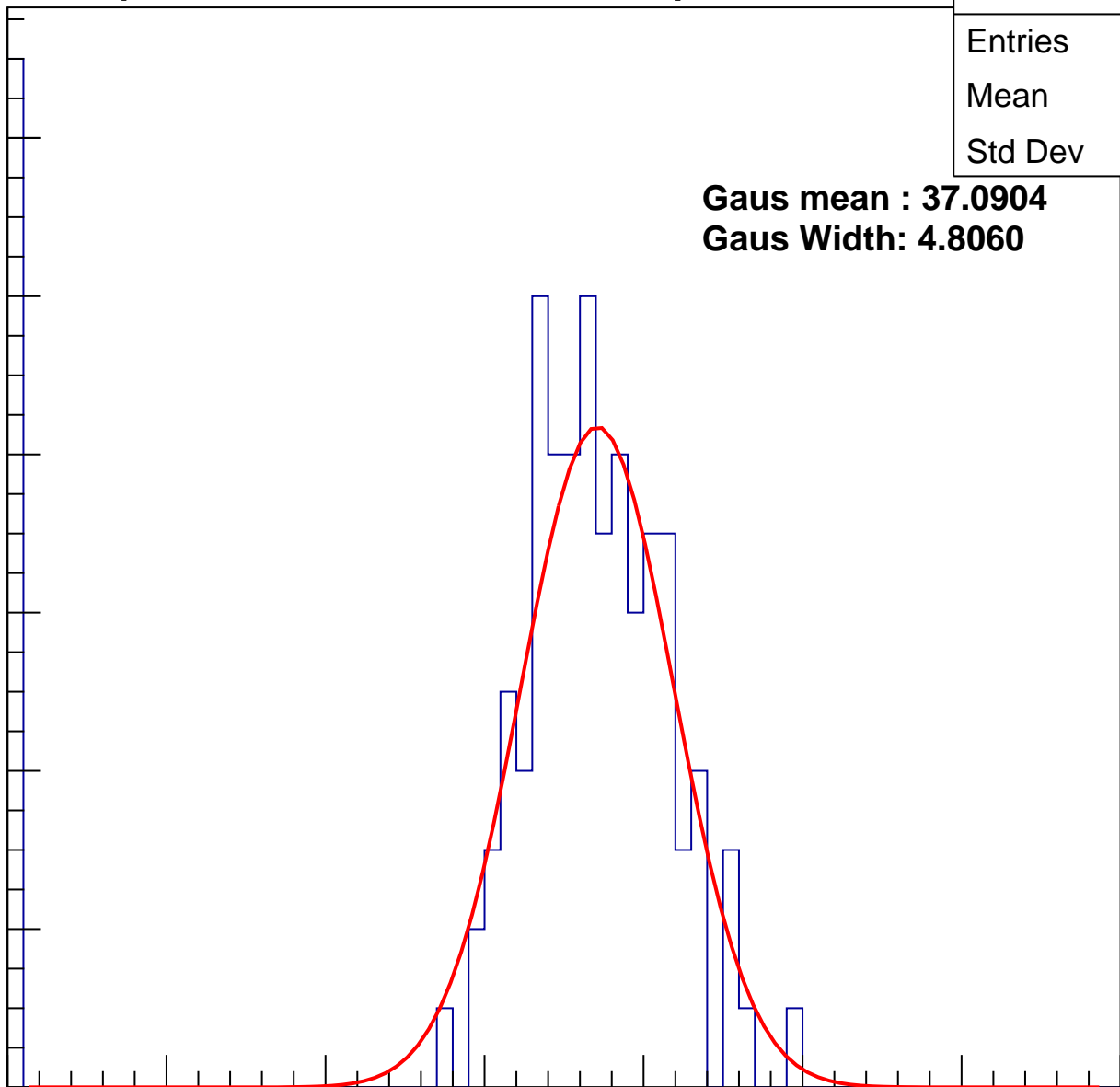
40

50

60

70

ampl



B1L103S, U13-ch99, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	41.87
Std Dev	11.11

Gaus mean : 44.9068

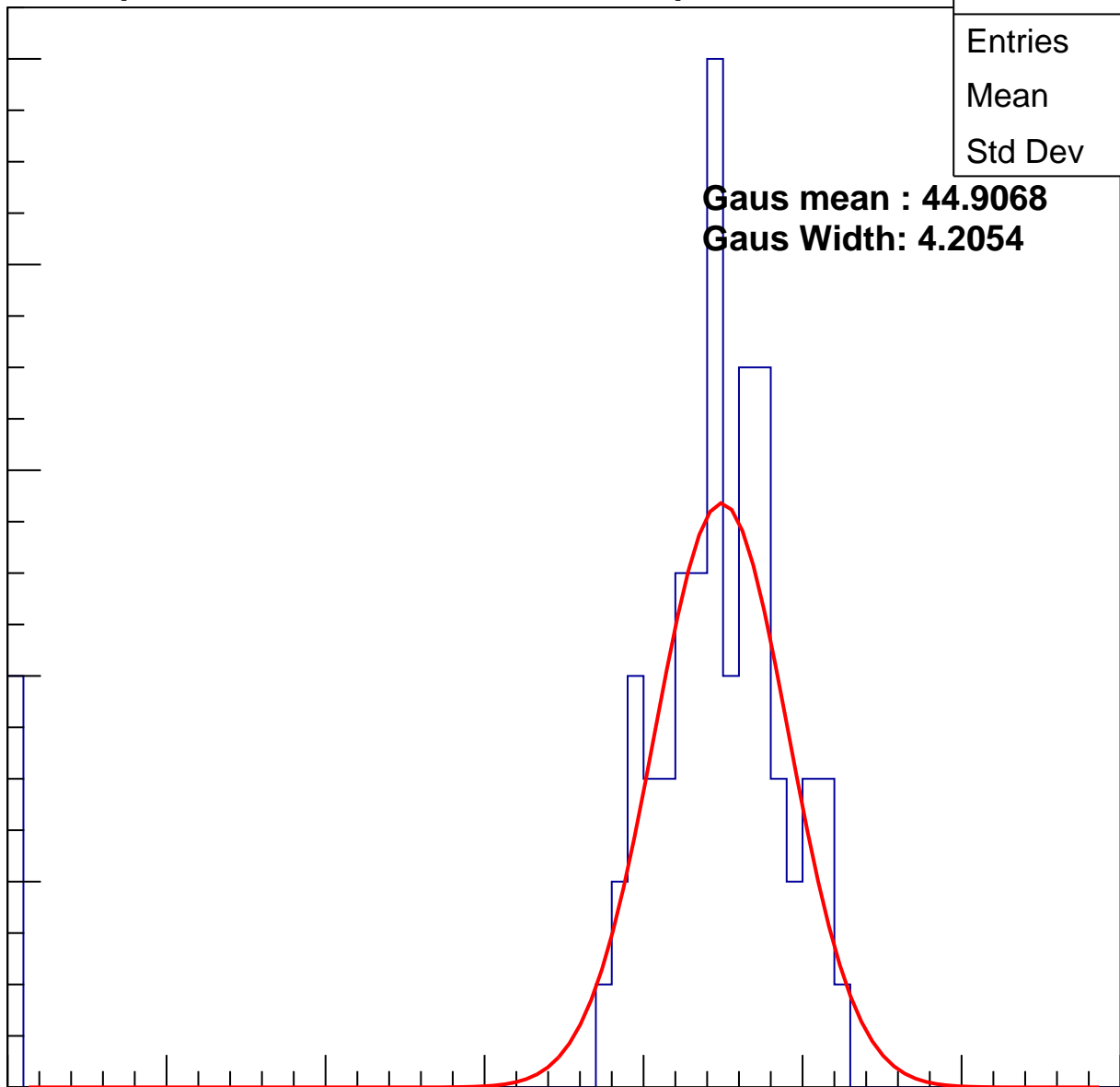
Gaus Width: 4.2054

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

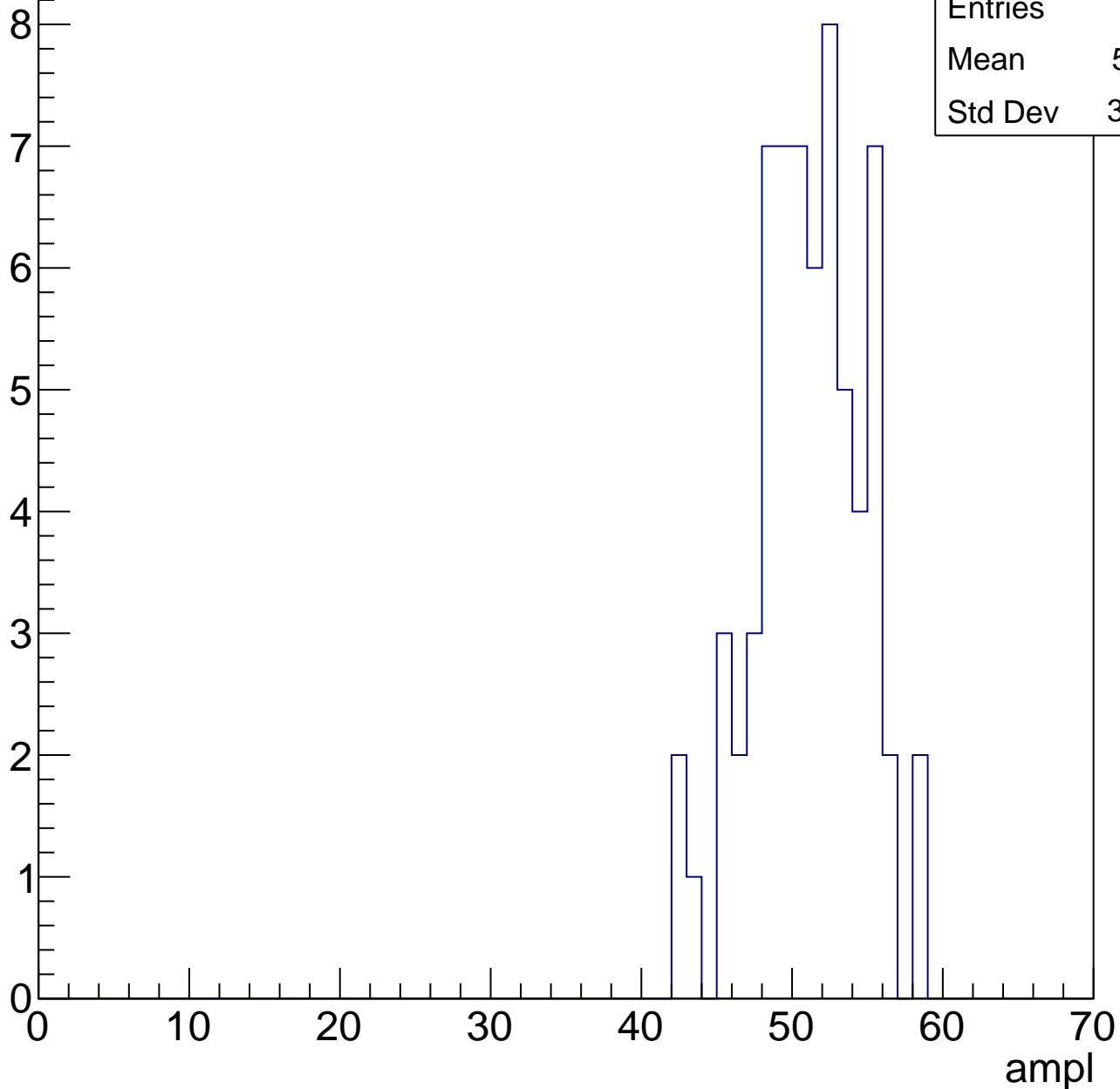


B1L103S, U13-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	50.61
Std Dev	3.576

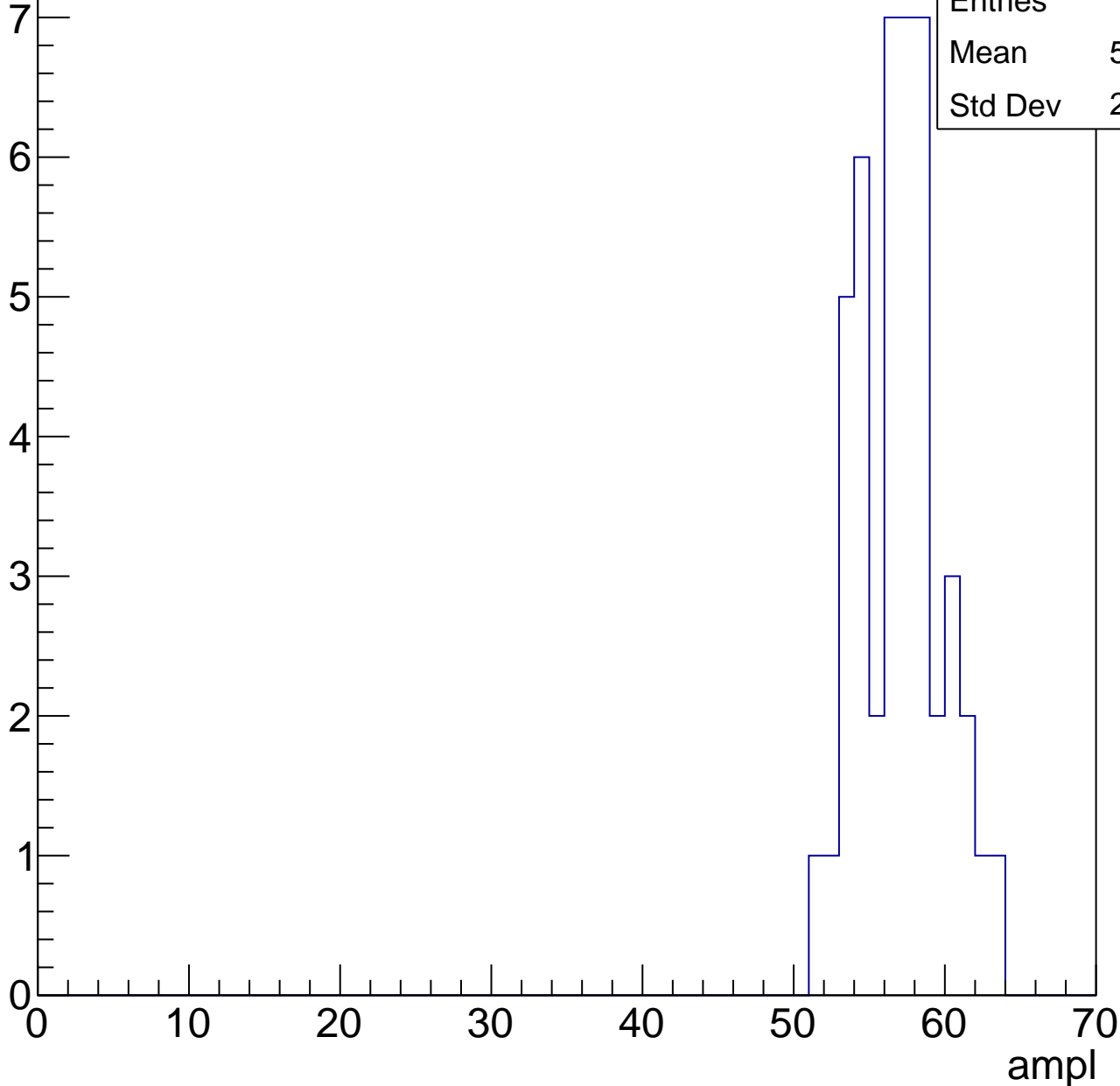


B1L103S, U13-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	56.53
Std Dev	2.729



B1L103S, U13-ch99, adc5

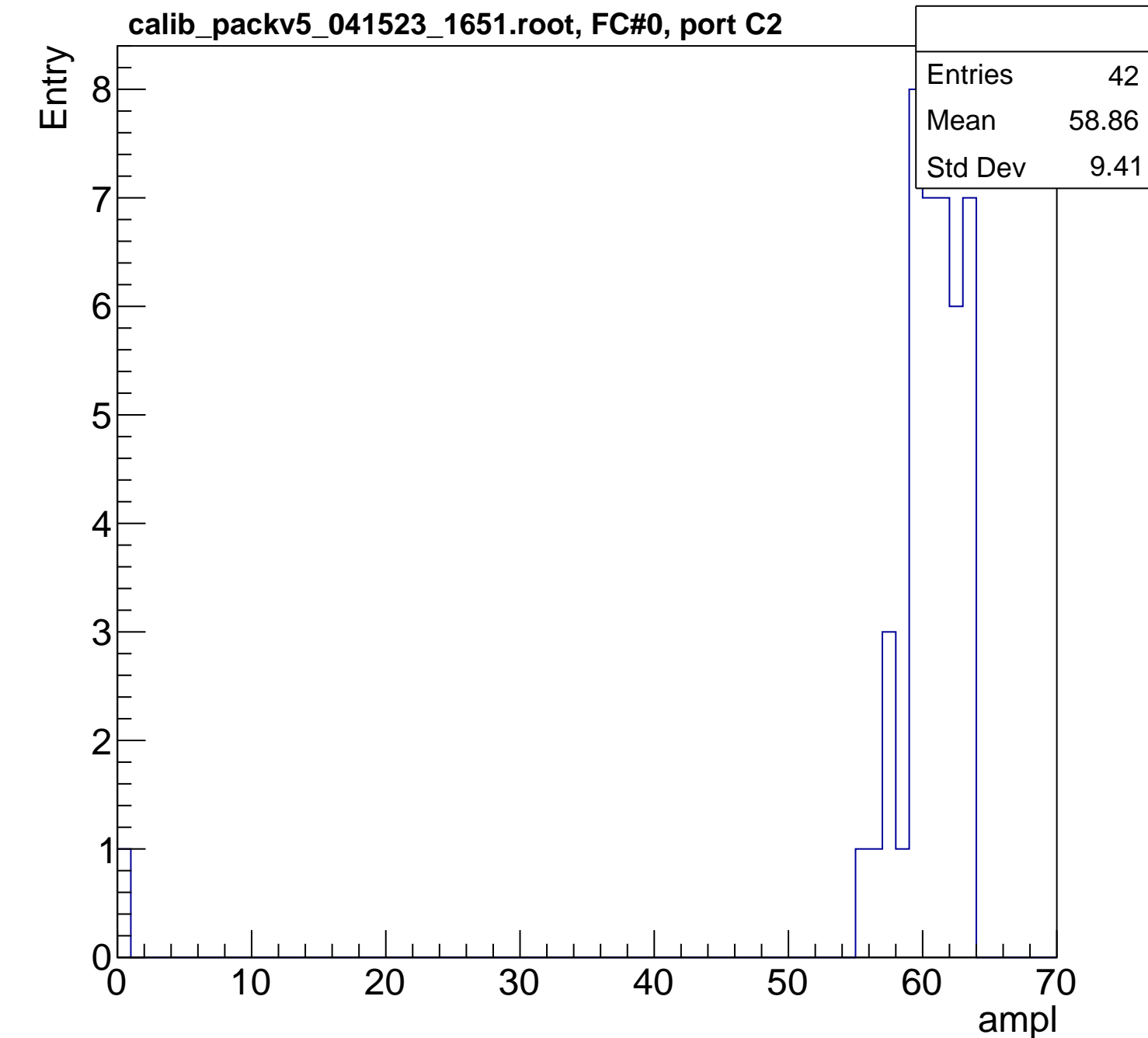
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.86
Std Dev	9.41

ampl



B1L103S, U13-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	6
Mean	62.17
Std Dev	0.6872

B1L103S, U13-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch100, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	18.39
Std Dev	12.91

Gaus mean : 27.6931

Gaus Width: 2.9182

Entry

25

20

15

10

5

0

0

10

20

30

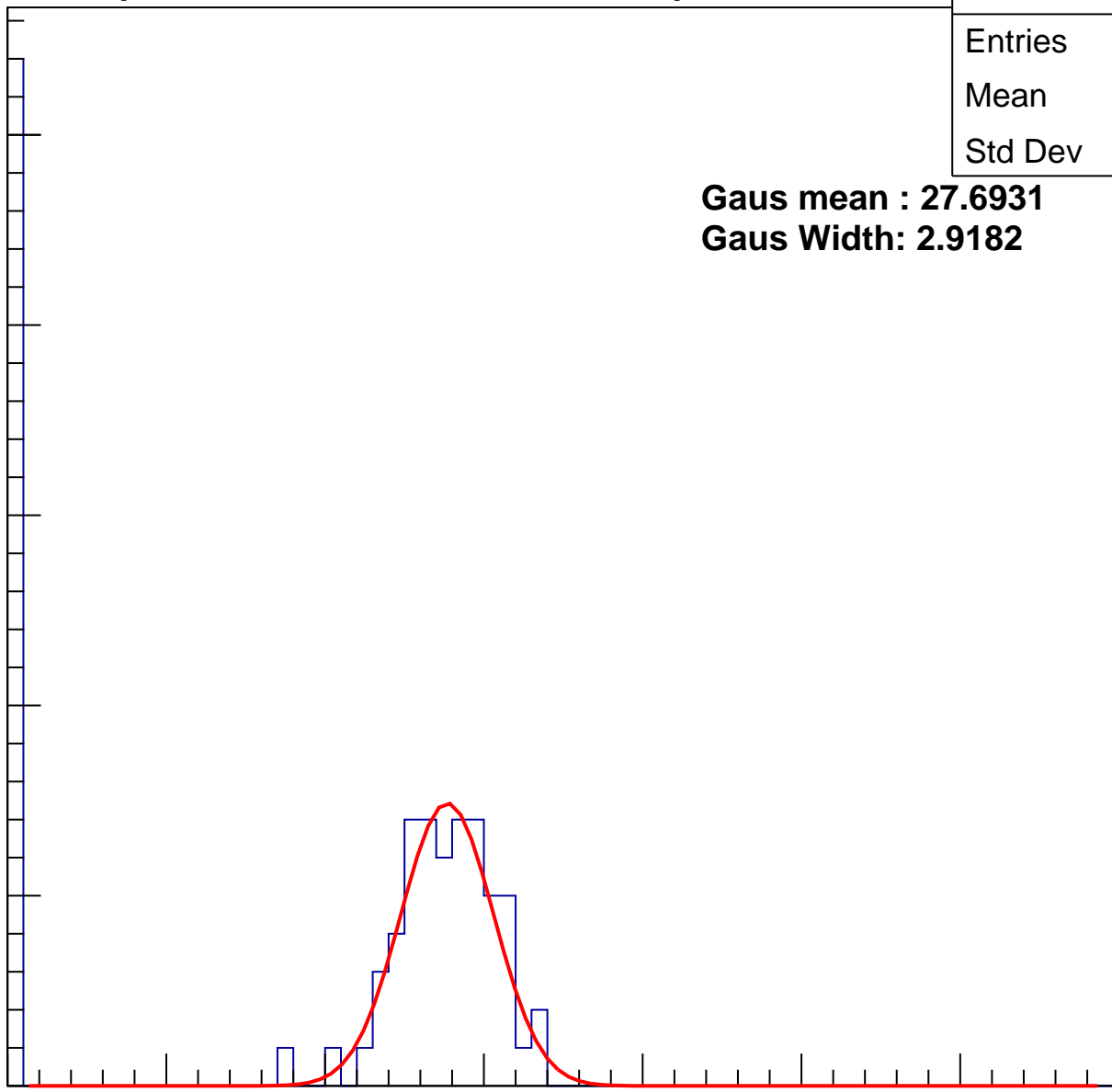
40

50

60

70

ampl



B1L103S, U13-ch100, adc1

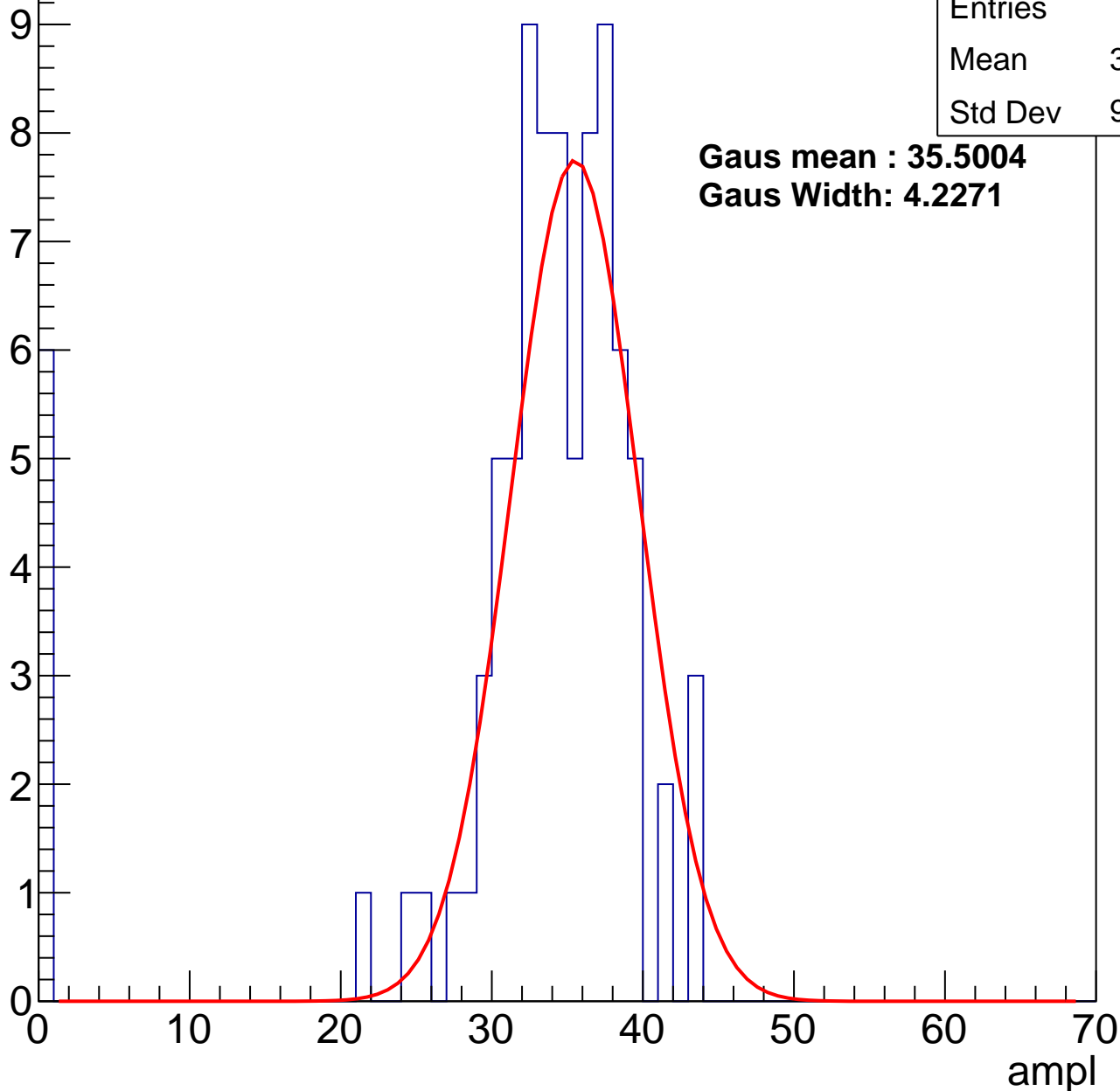
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	31.85
Std Dev	9.525

Gaus mean : 35.5004

Gaus Width: 4.2271



B1L103S, U13-ch100, adc2

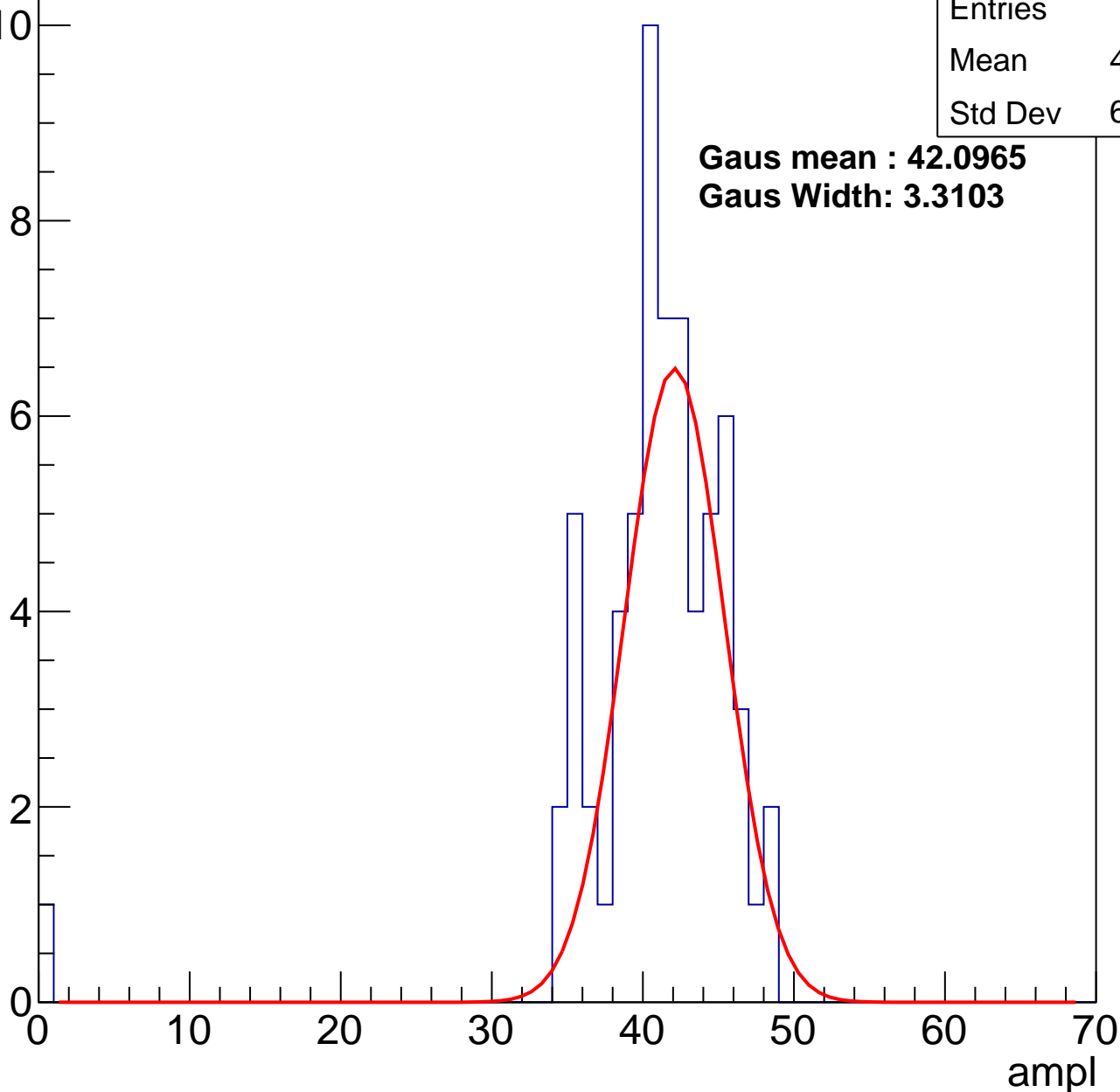
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	40.35
Std Dev	6.123

Gaus mean : 42.0965

Gaus Width: 3.3103

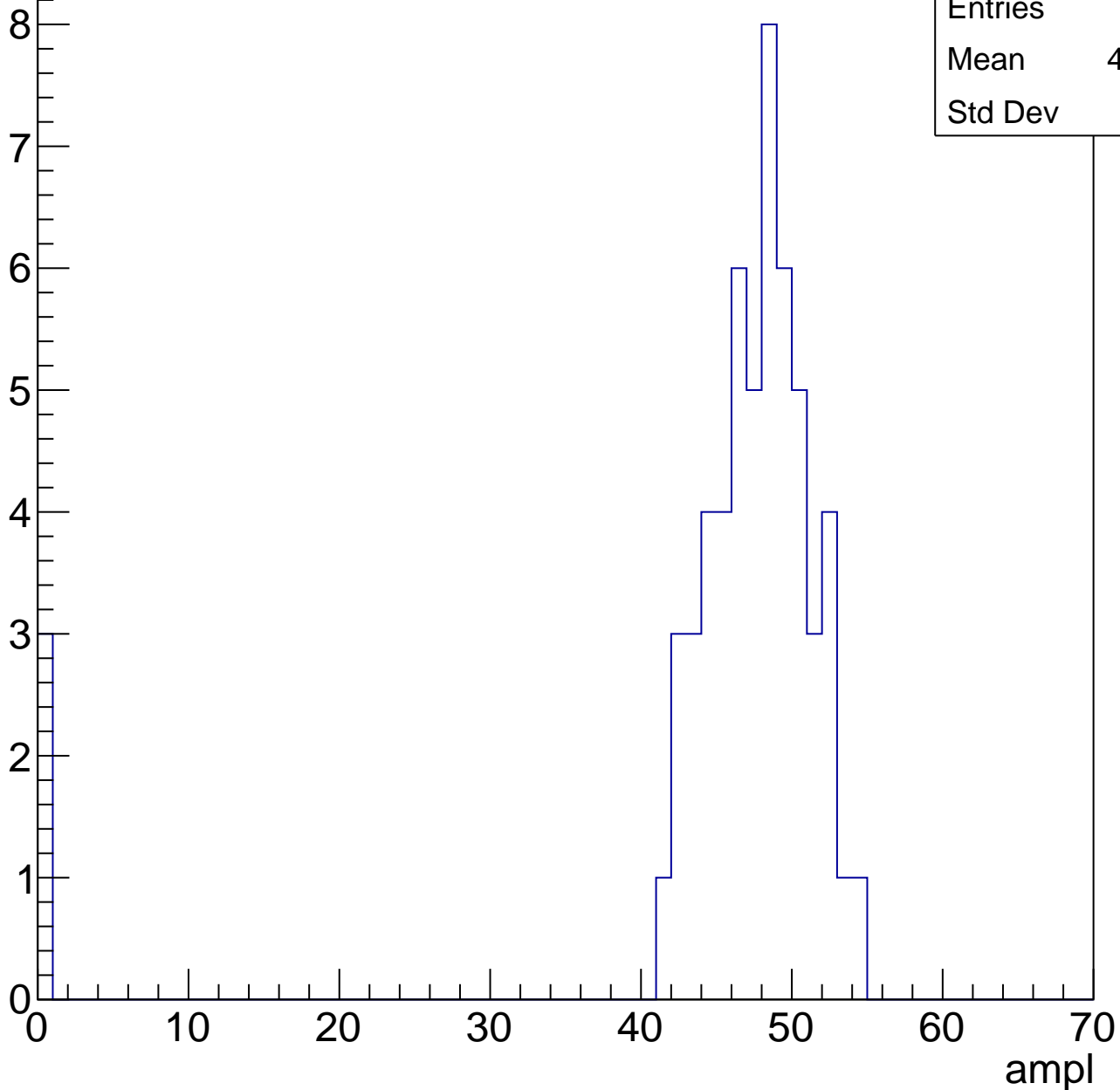


B1L103S, U13-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	44.89
Std Dev	11

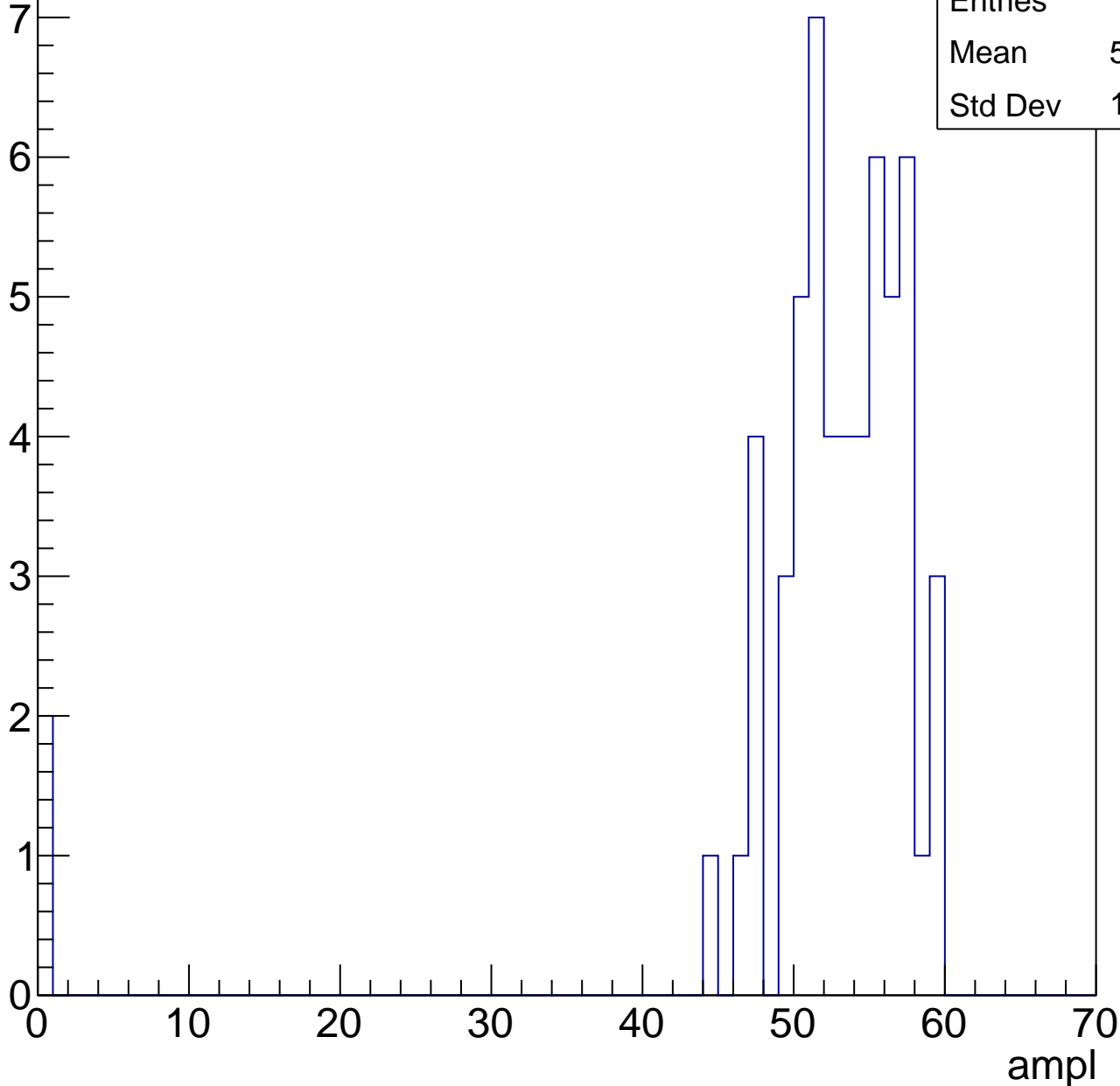


B1L103S, U13-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	50.98
Std Dev	10.43

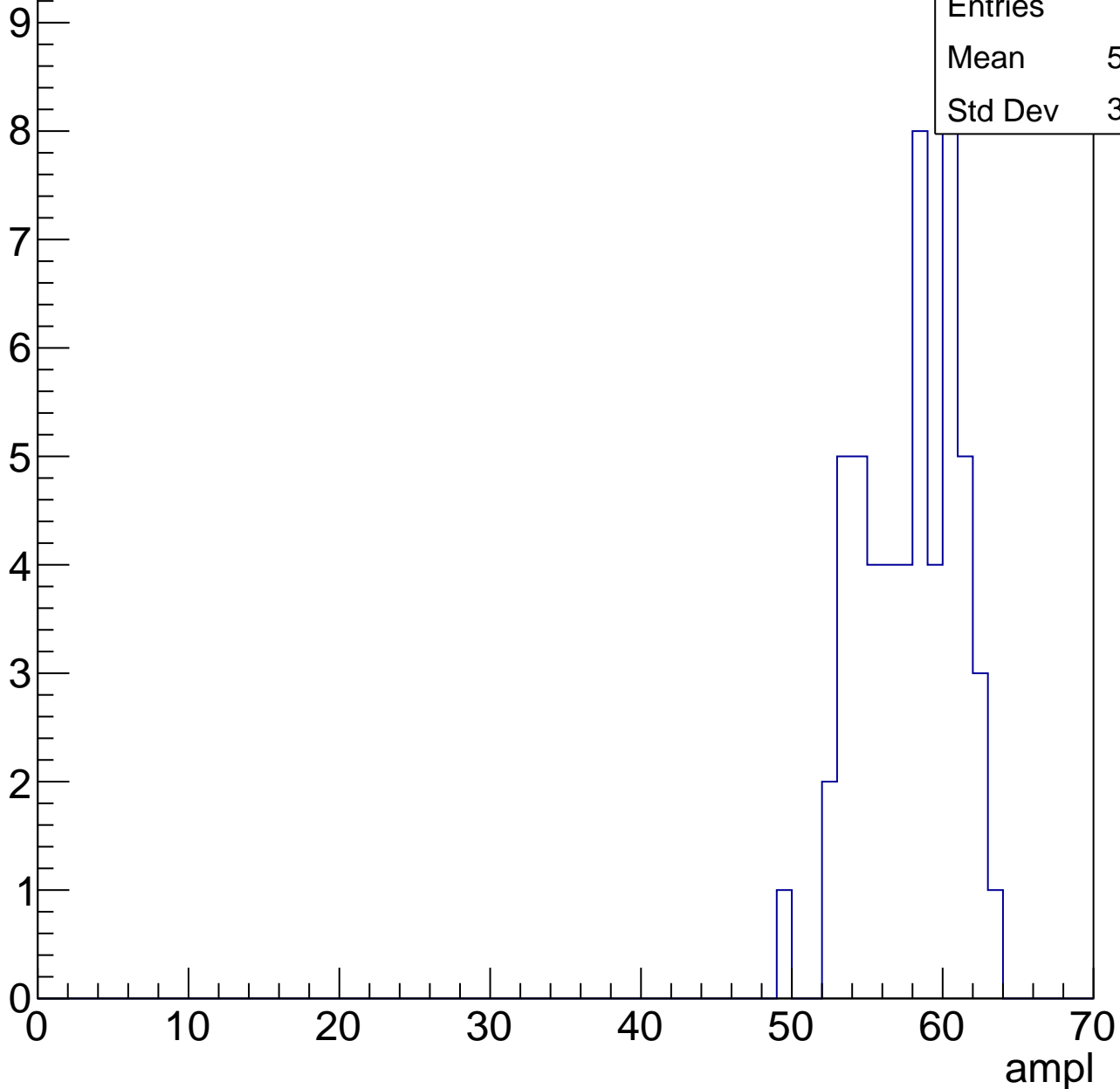


B1L103S, U13-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.35
Std Dev	3.158

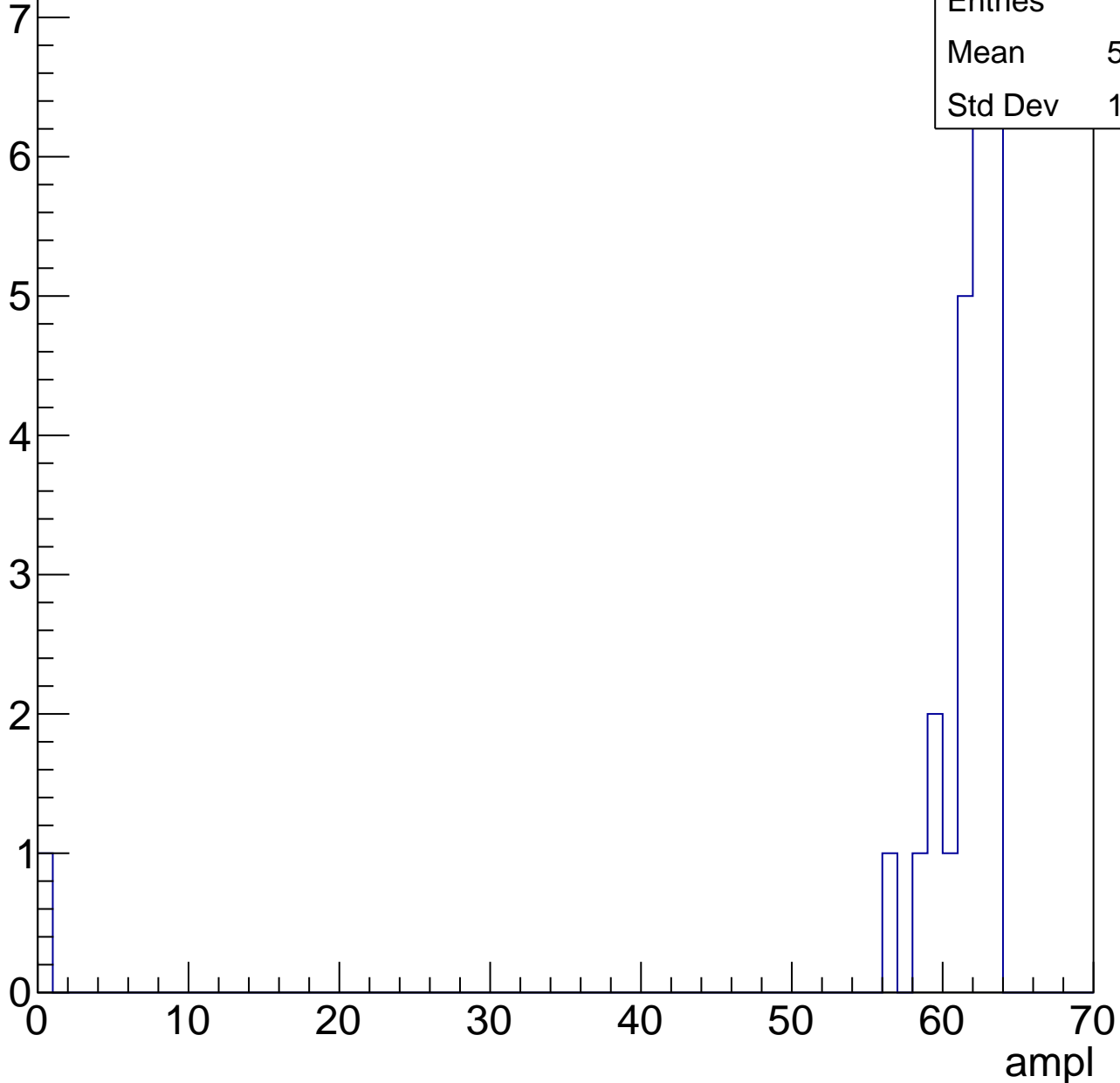


B1L103S, U13-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

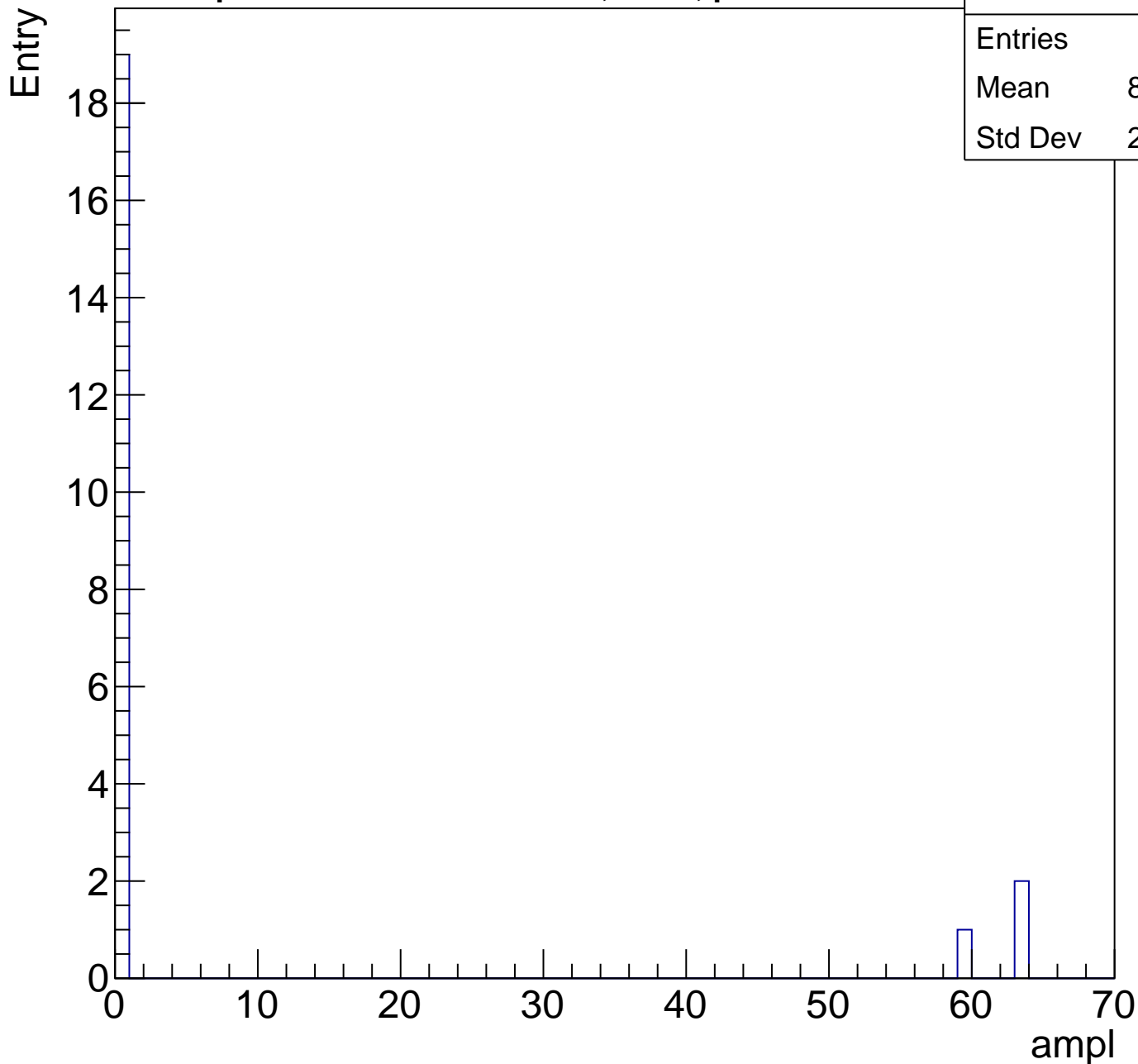
Entries	25
Mean	58.88
Std Dev	12.14



B1L103S, U13-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.409
Std Dev	21.17



B1L103S, U13-ch101, adc0

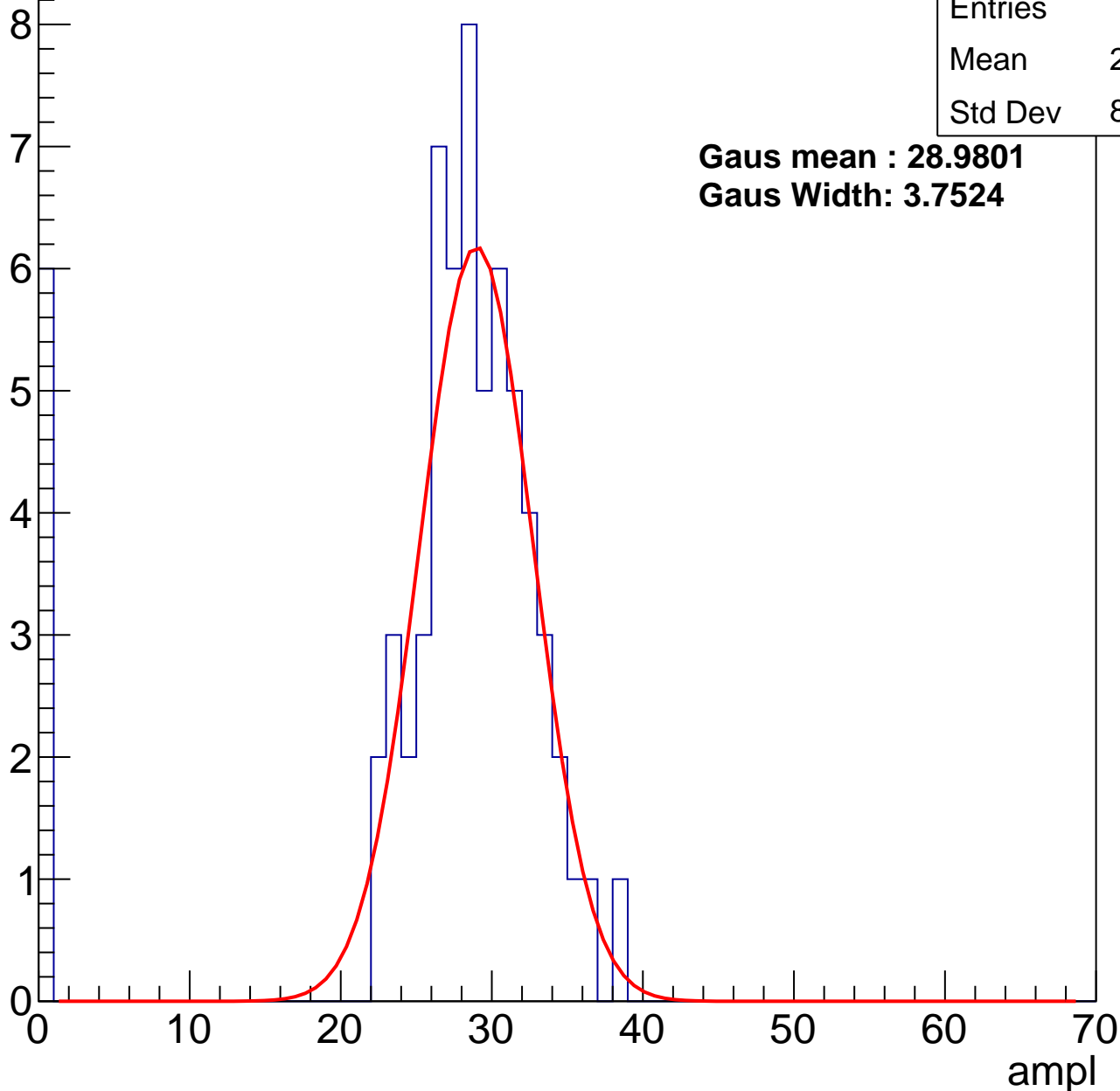
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	25.97
Std Dev	8.922

Gaus mean : 28.9801

Gaus Width: 3.7524



B1L103S, U13-ch101, adc1

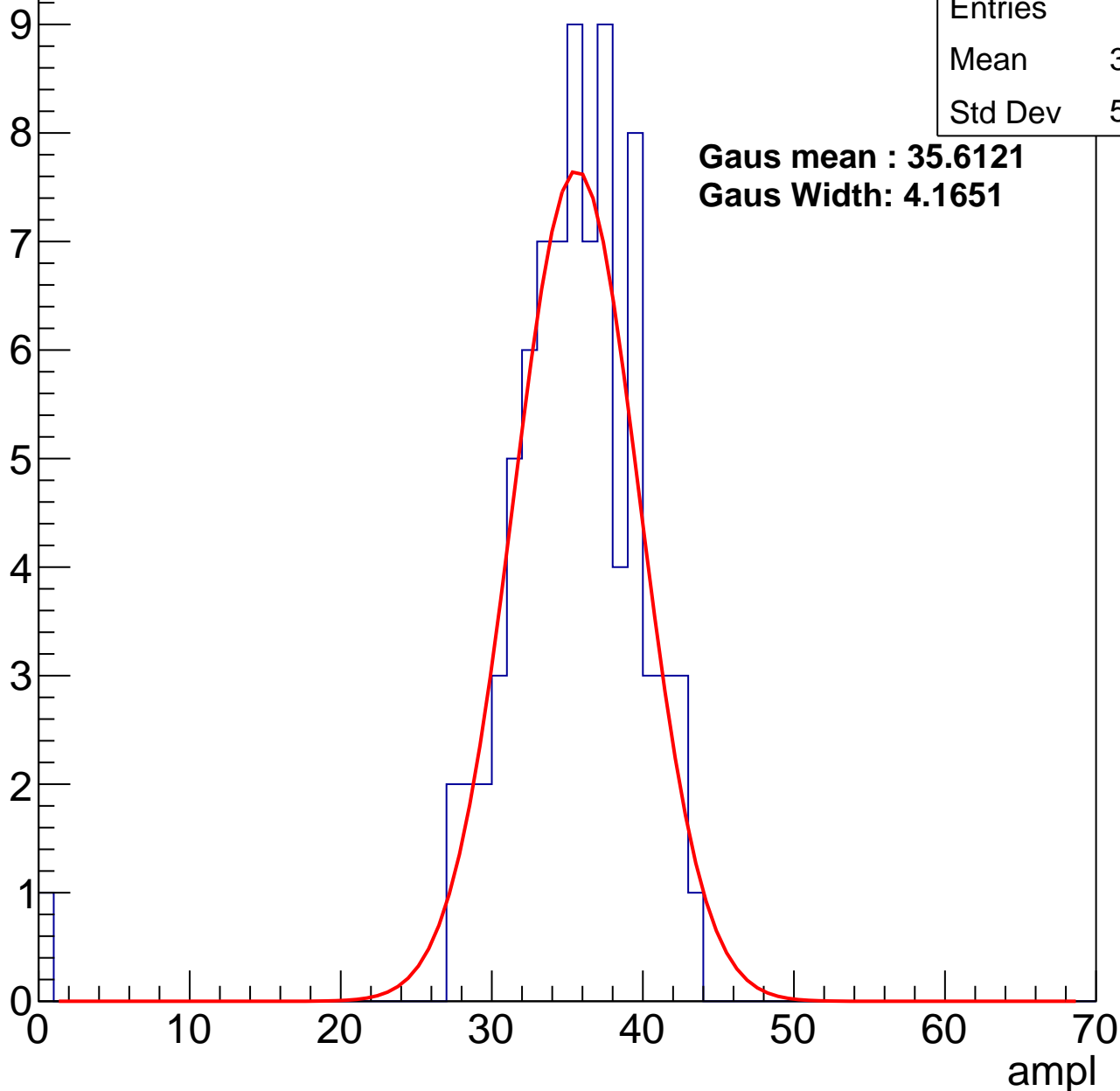
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	34.76
Std Dev	5.368

Gaus mean : 35.6121

Gaus Width: 4.1651



B1L103S, U13-ch101, adc2

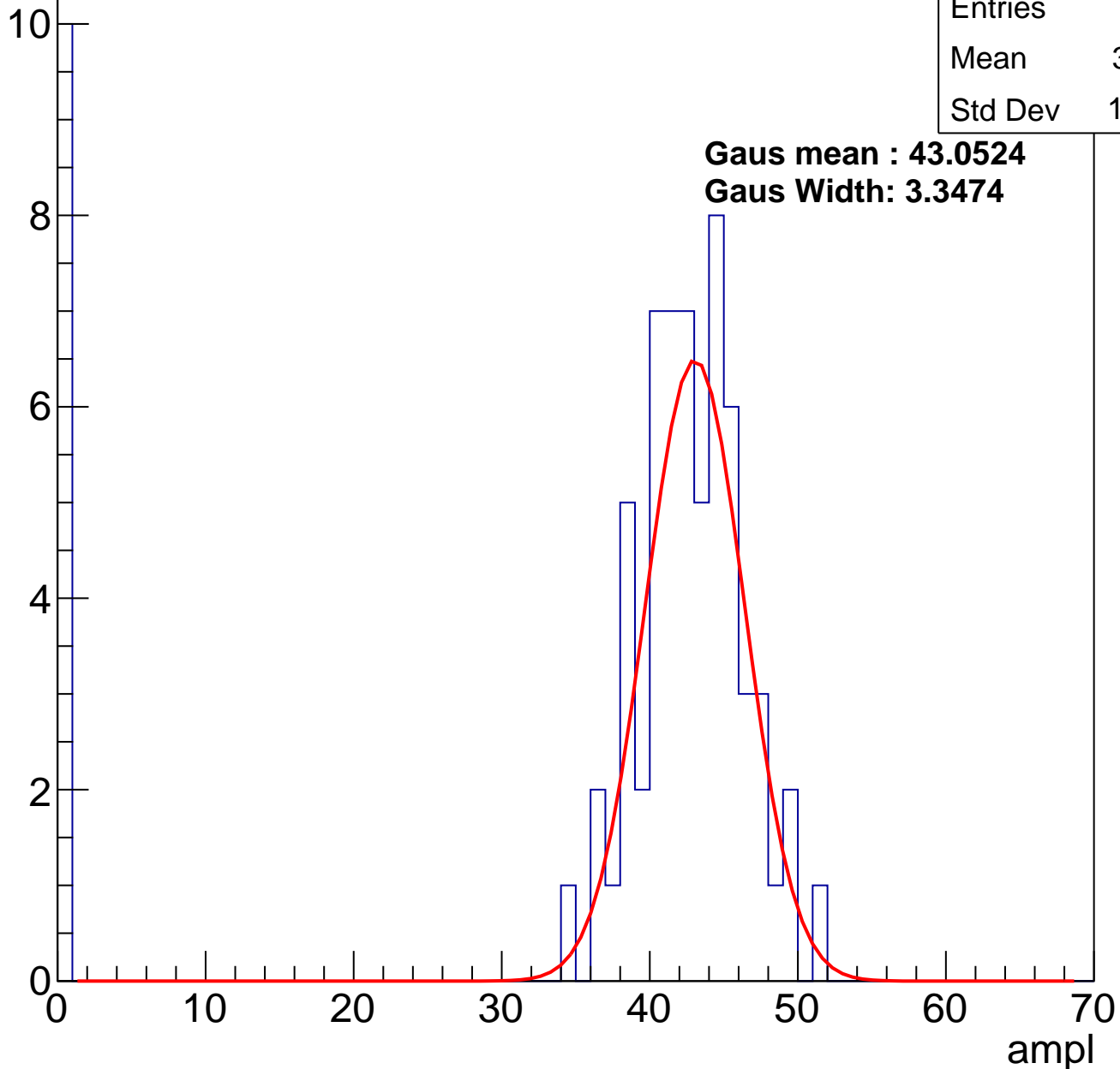
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	36.41
Std Dev	15.08

Gaus mean : 43.0524

Gaus Width: 3.3474

Entry

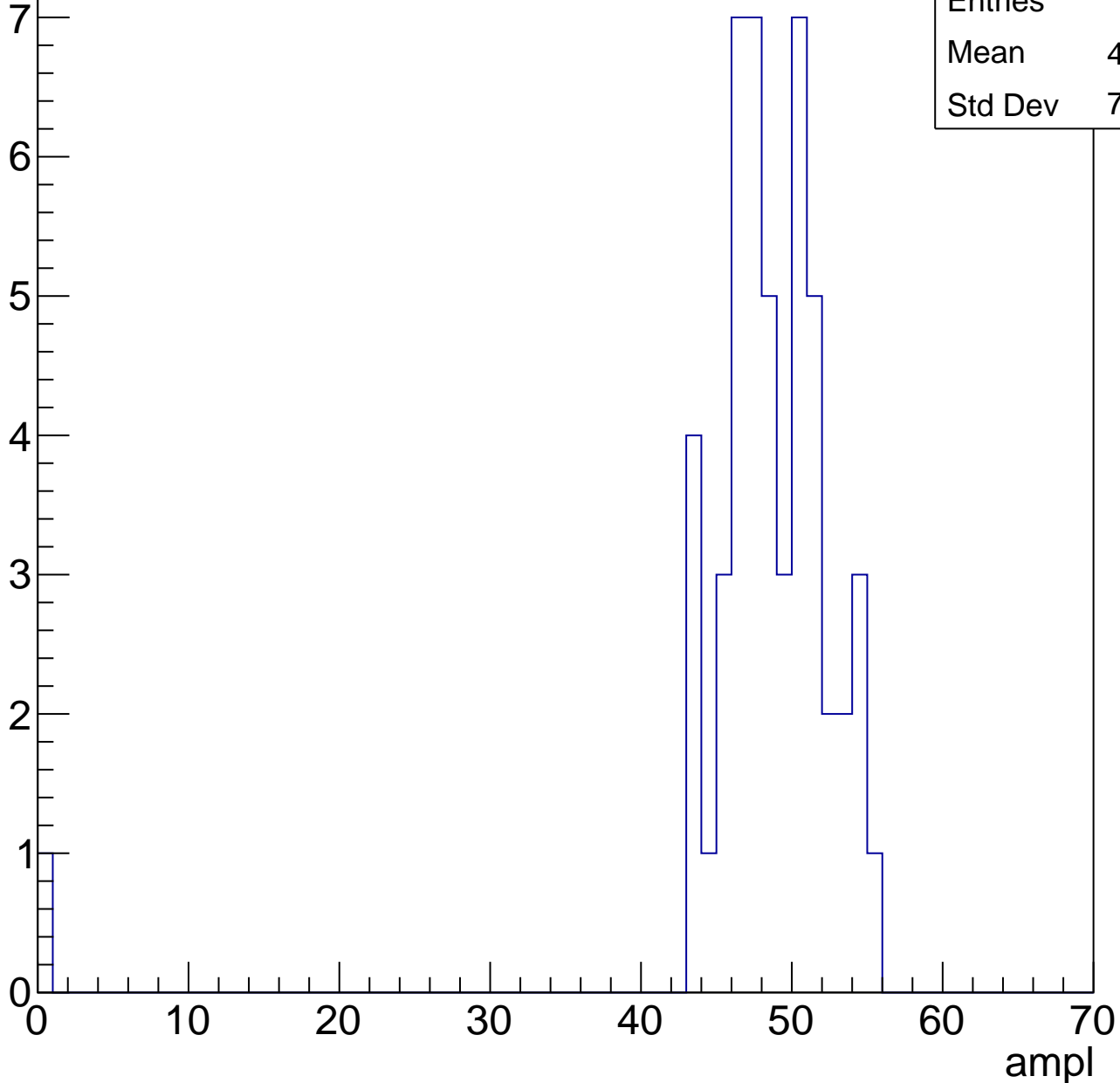


B1L103S, U13-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	47.47
Std Dev	7.387

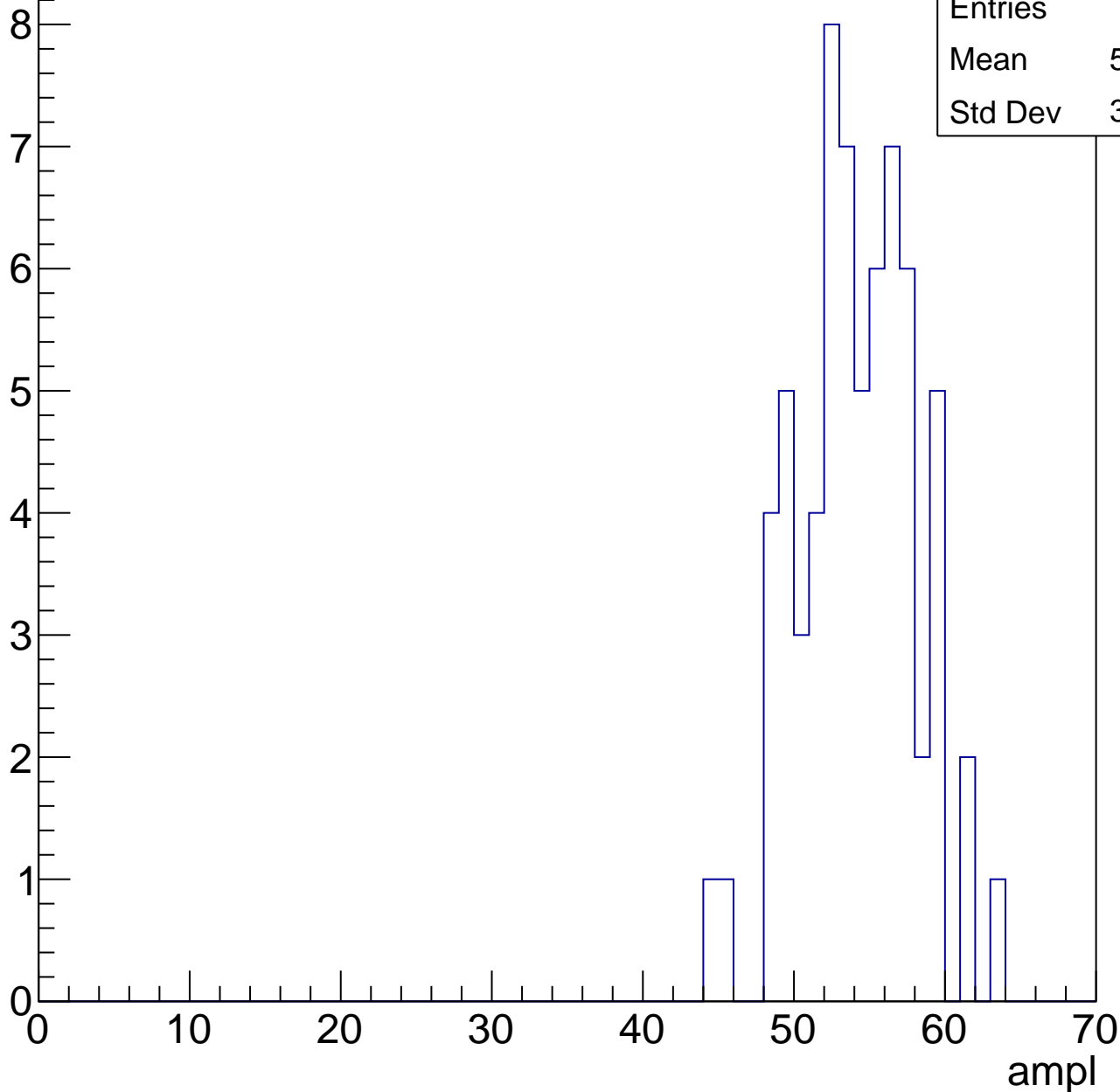


B1L103S, U13-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	53.69
Std Dev	3.845

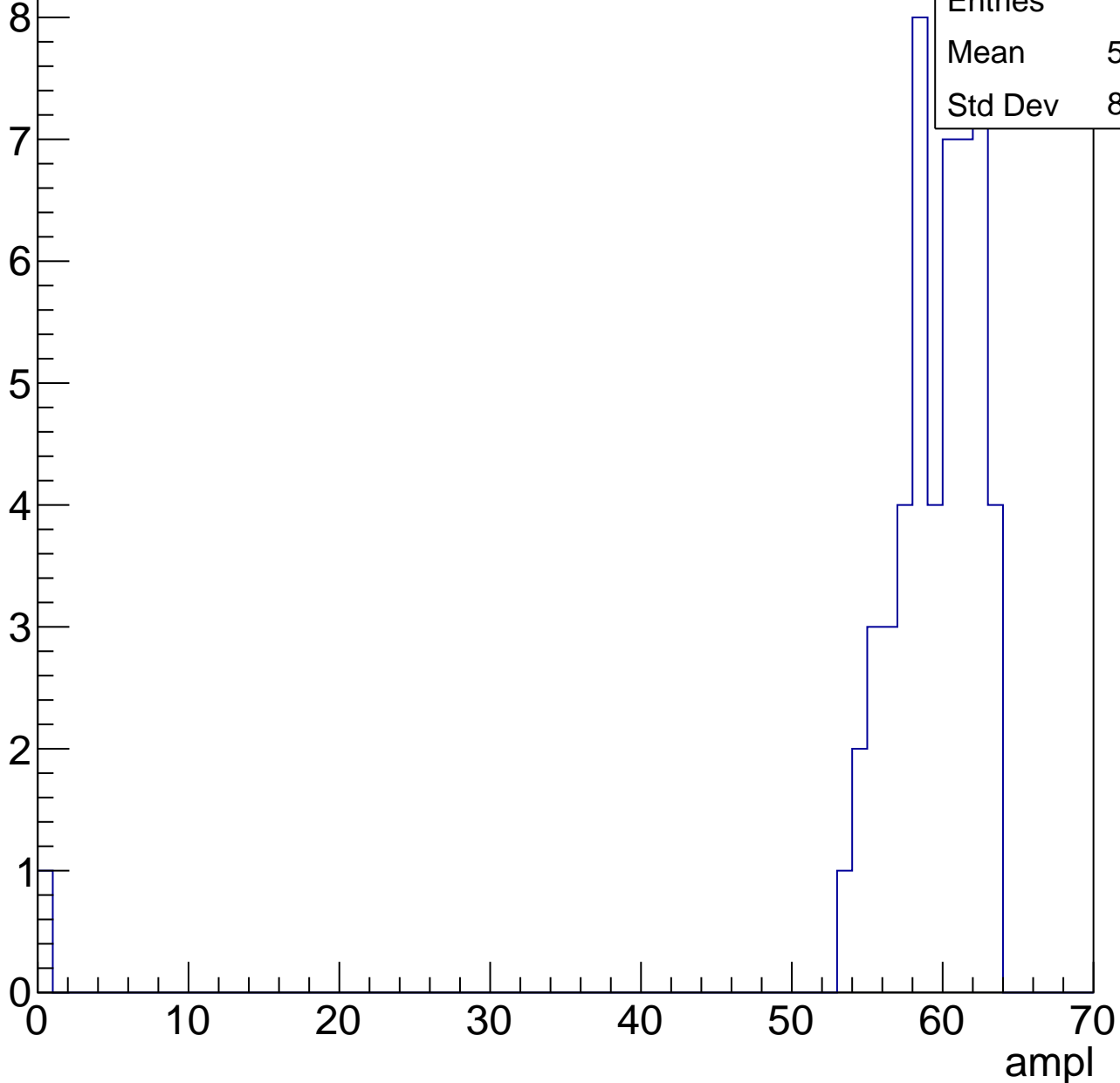


B1L103S, U13-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.02
Std Dev	8.534

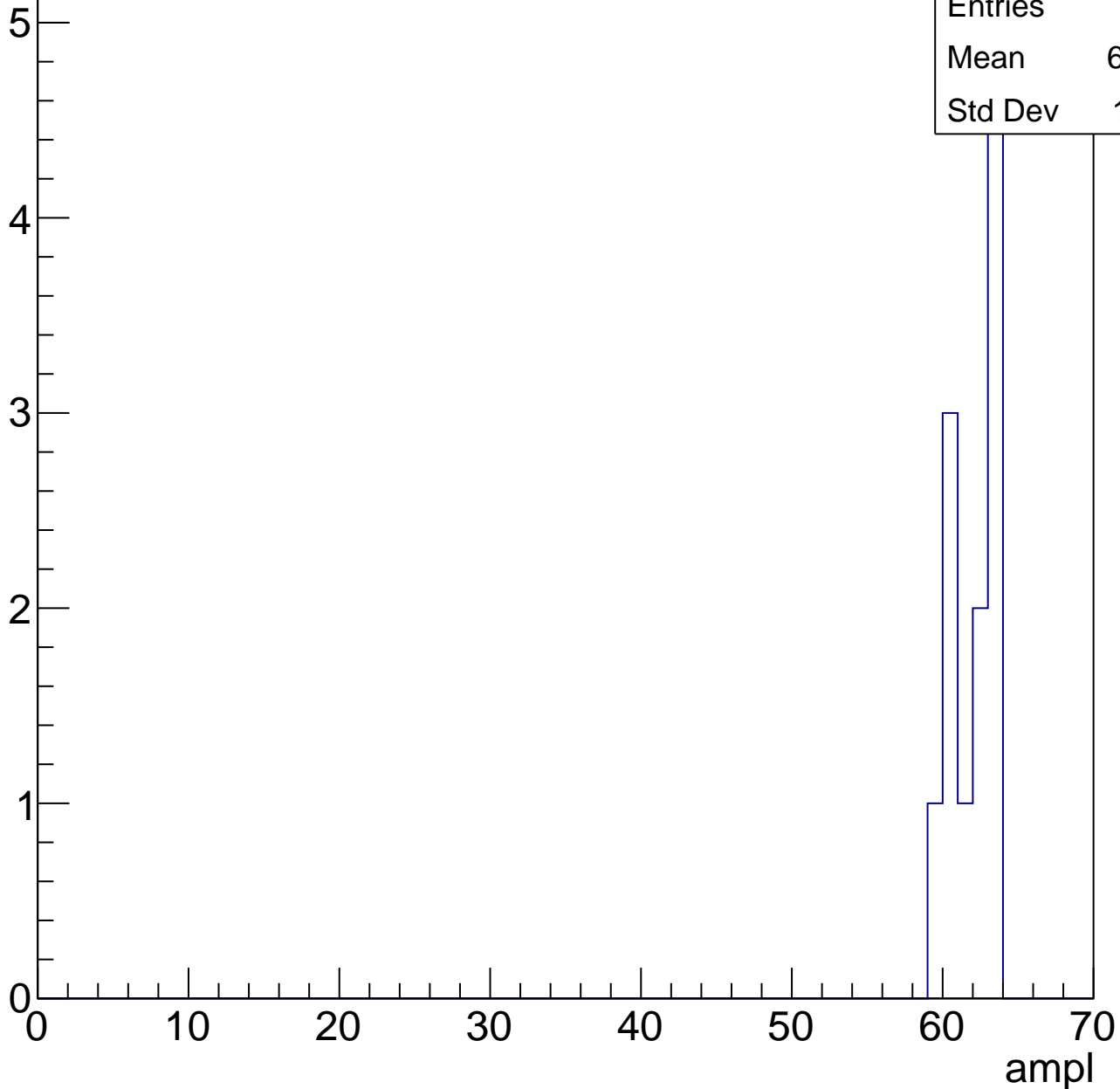


B1L103S, U13-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.441



B1L103S, U13-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch102, adc0

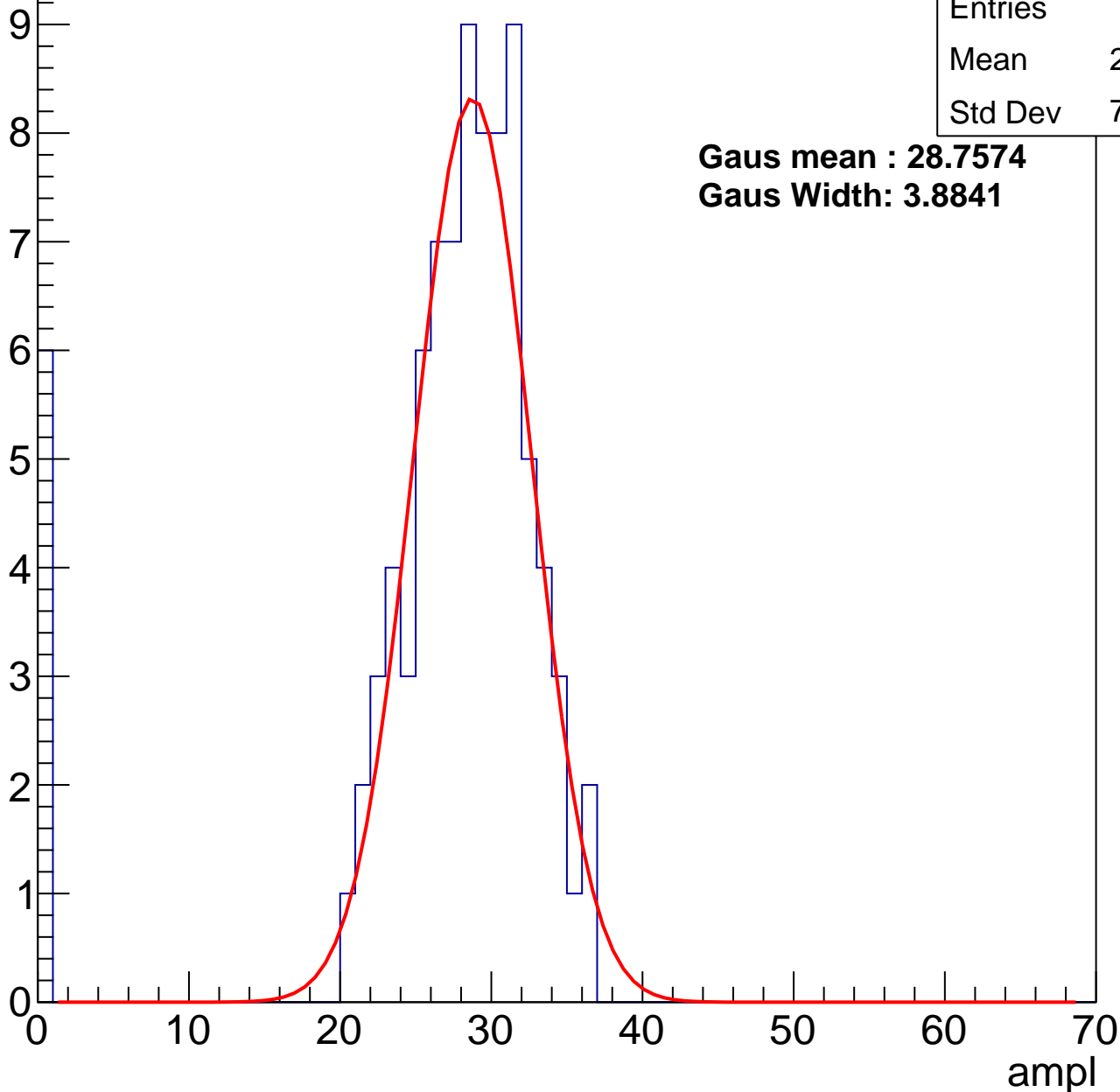
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	26.33
Std Dev	7.943

Gaus mean : 28.7574

Gaus Width: 3.8841



B1L103S, U13-ch102, adc1

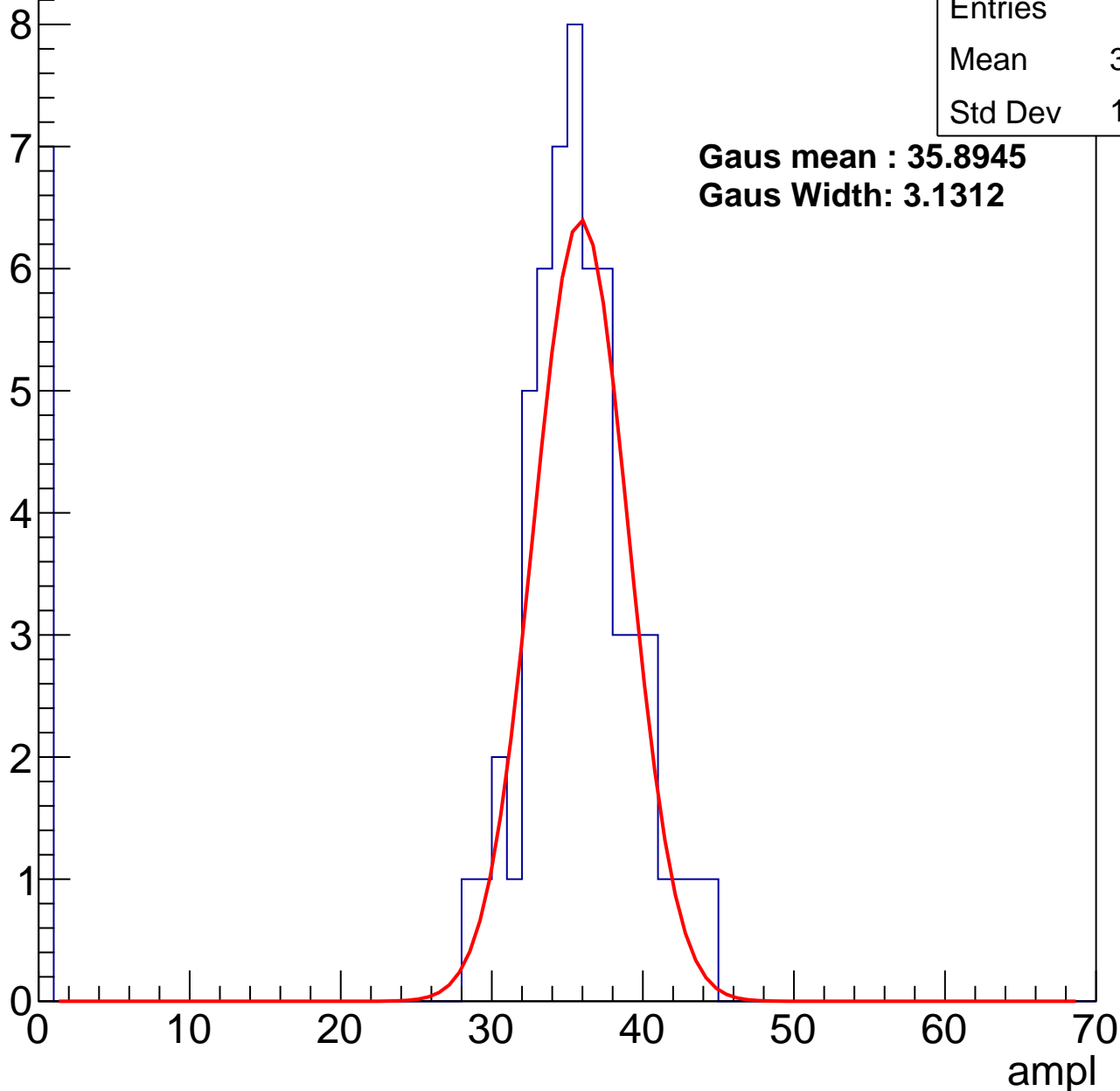
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	31.48
Std Dev	11.57

Gaus mean : 35.8945

Gaus Width: 3.1312



B1L103S, U13-ch102, adc2

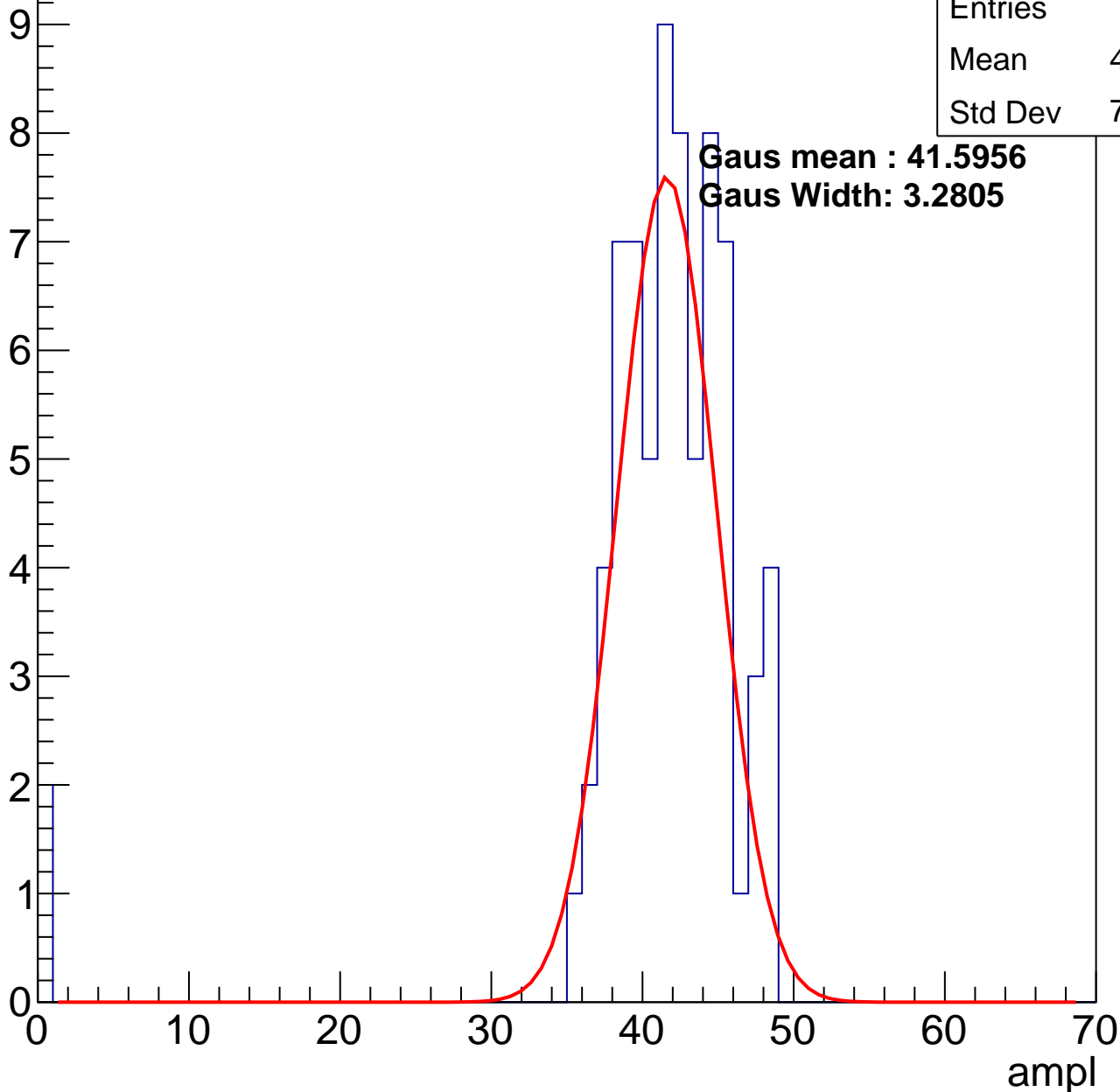
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.55
Std Dev	7.525

Gaus mean : 41.5956

Gaus Width: 3.2805

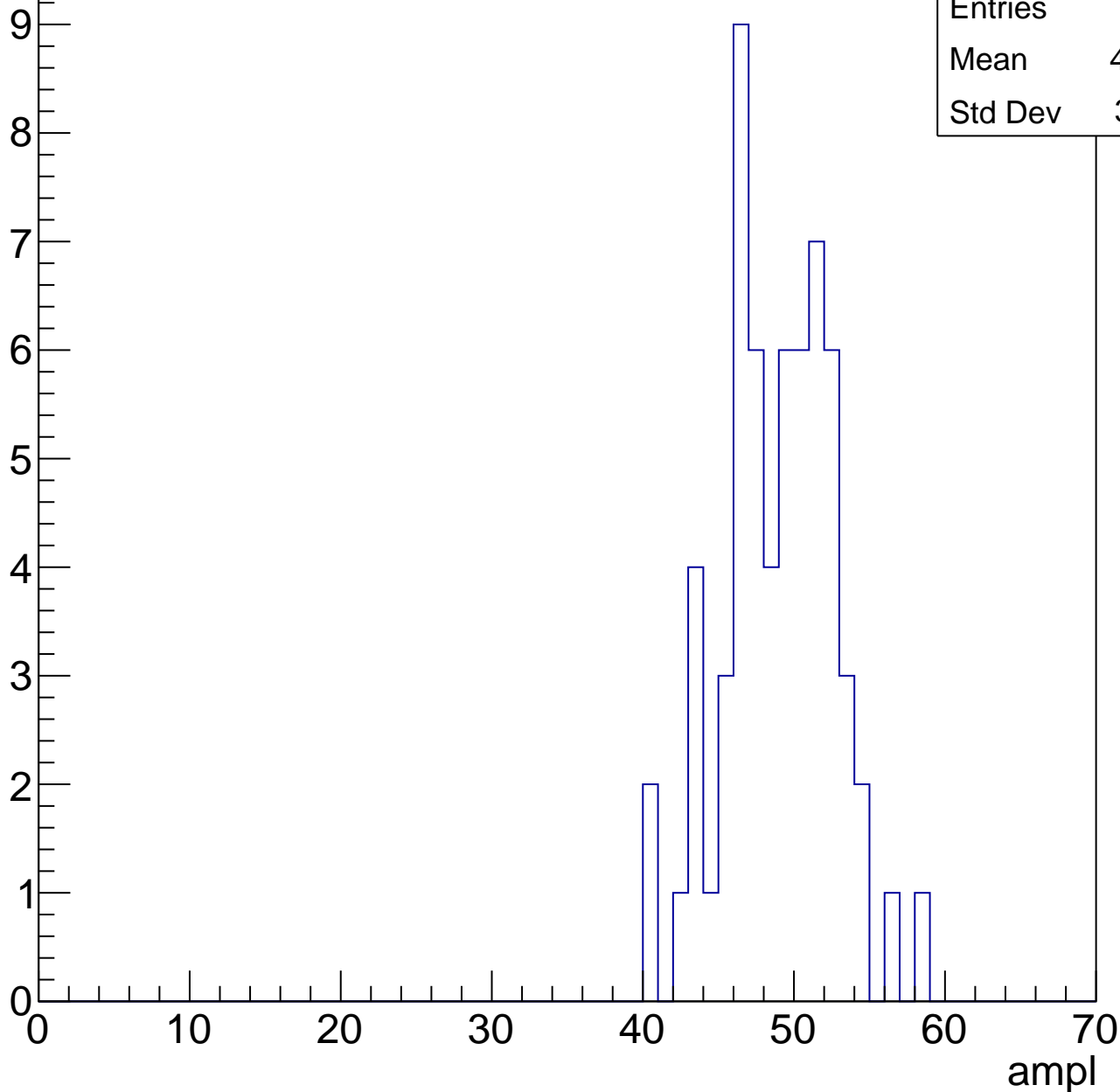


B1L103S, U13-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.47
Std Dev	3.671

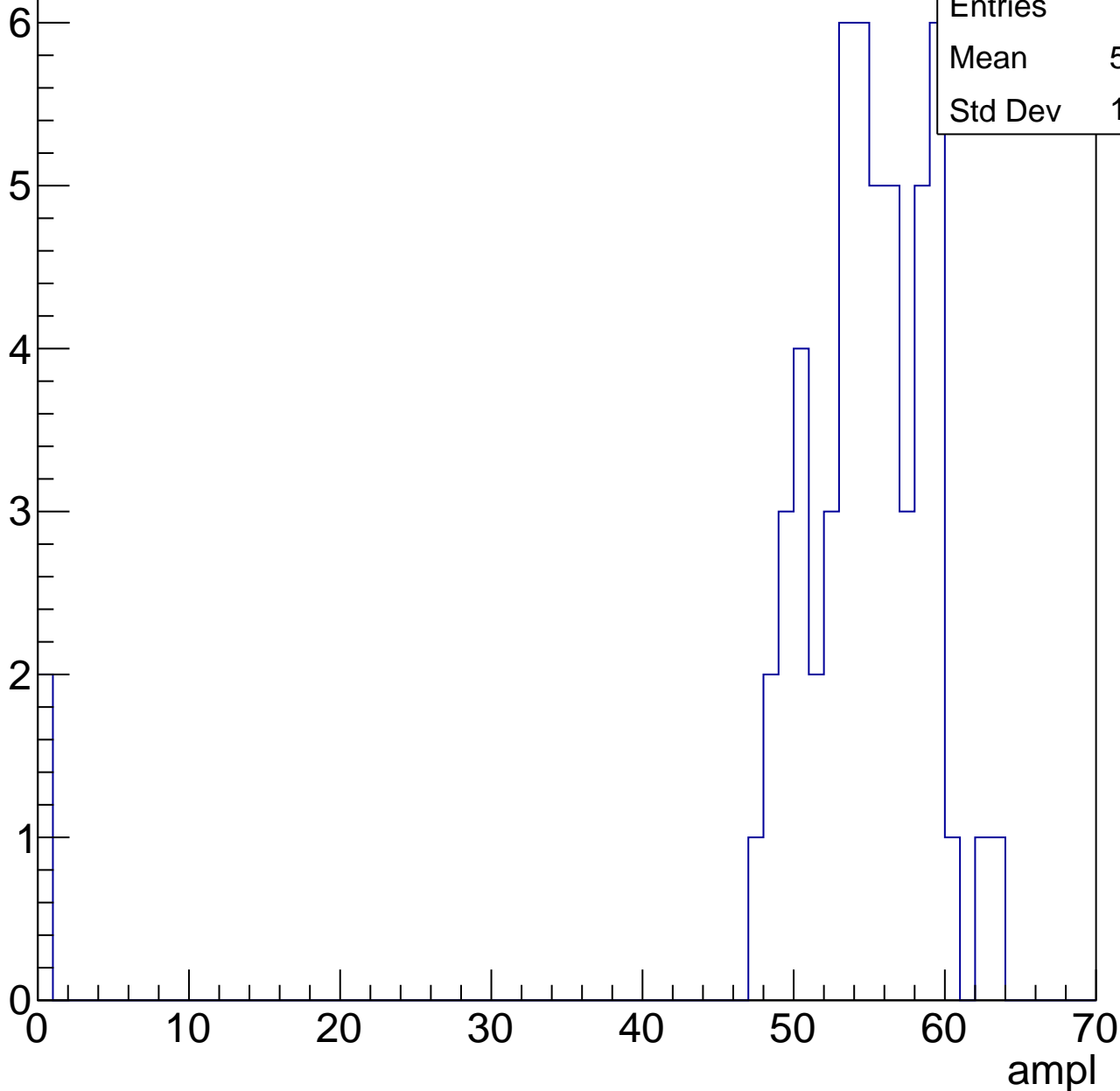


B1L103S, U13-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

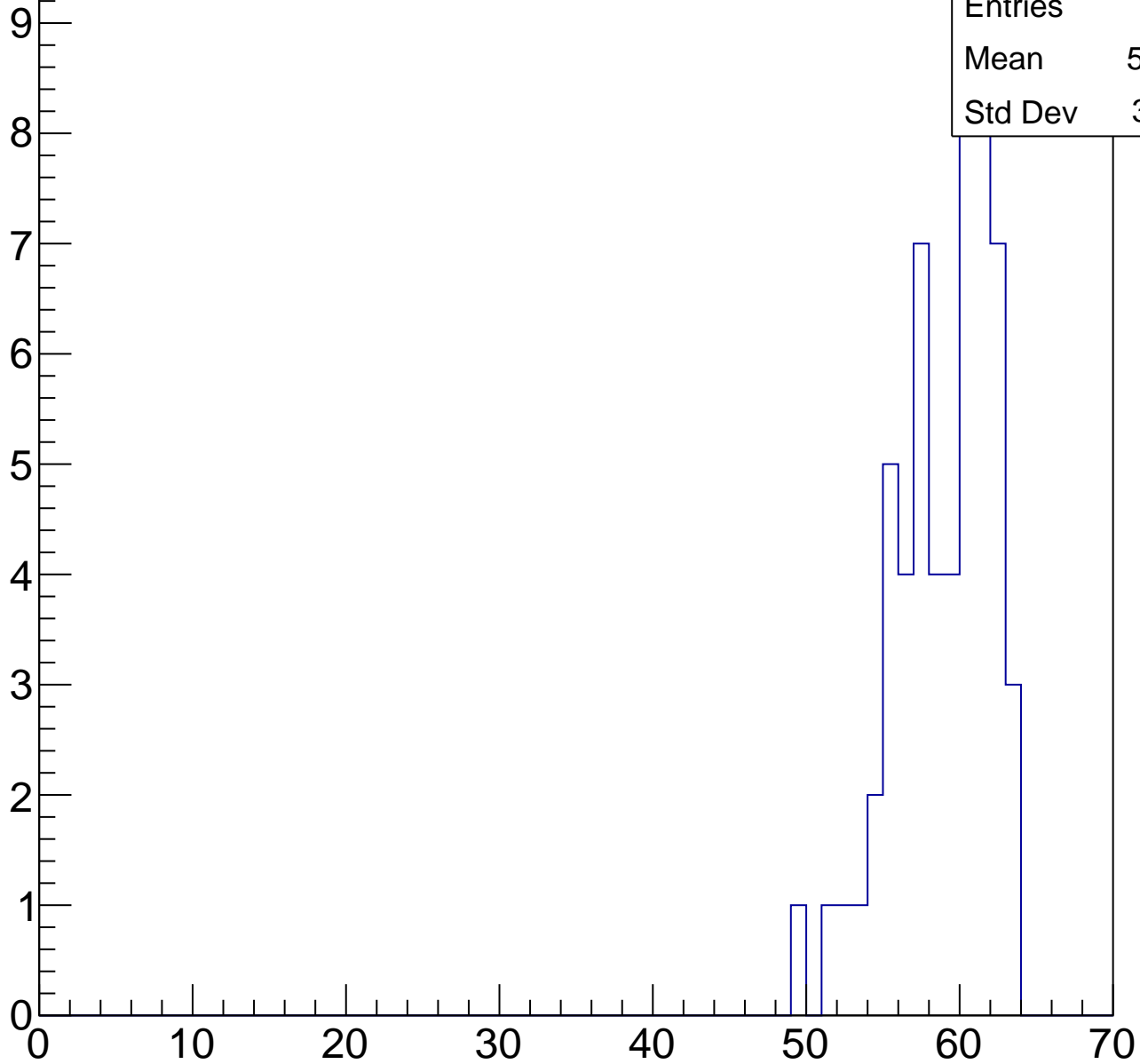
Entries	56
Mean	52.59
Std Dev	10.76



B1L103S, U13-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	57
Mean	58.44
Std Dev	3.201

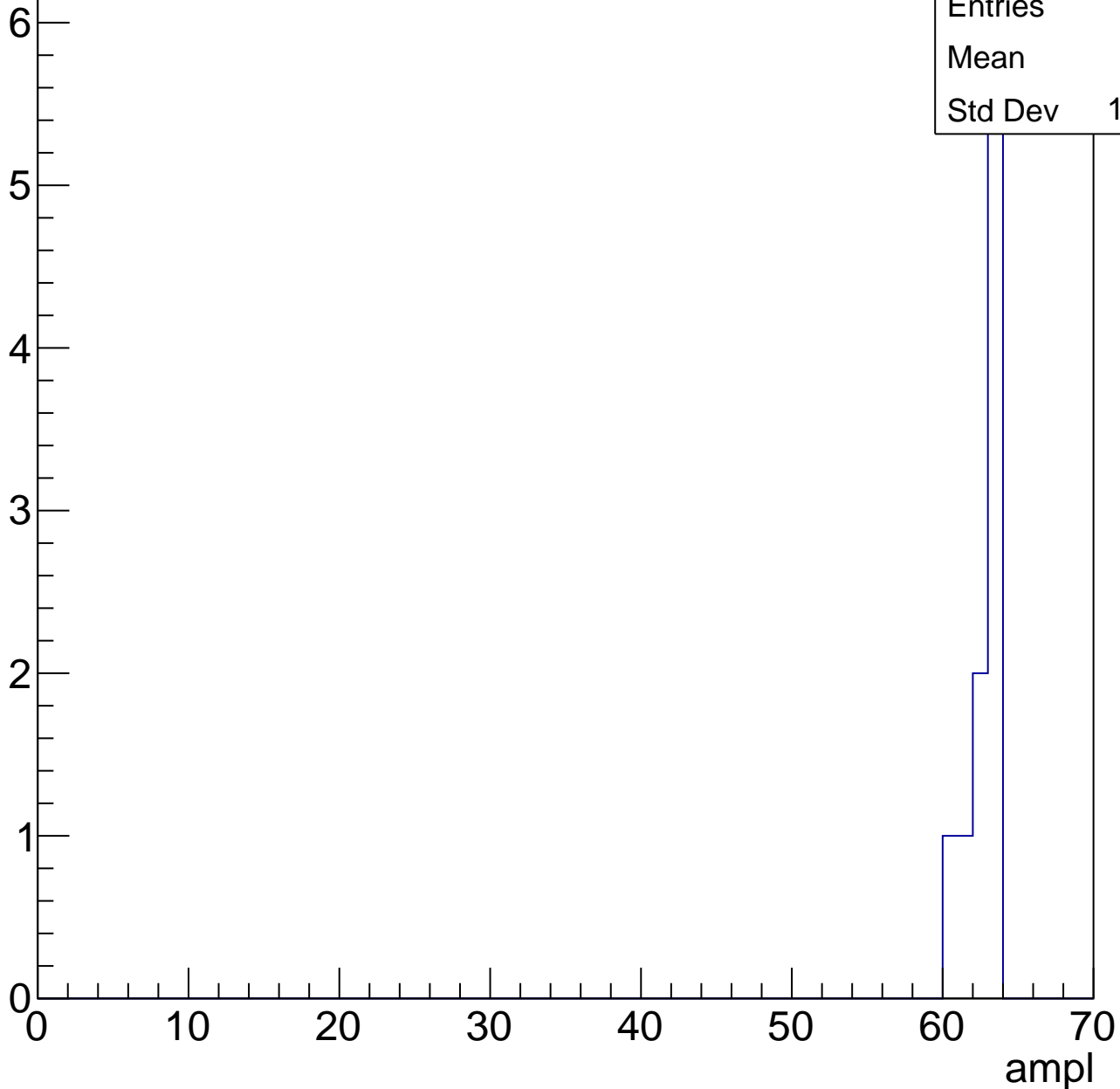
ampl

B1L103S, U13-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	62.3
Std Dev	1.005



B1L103S, U13-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

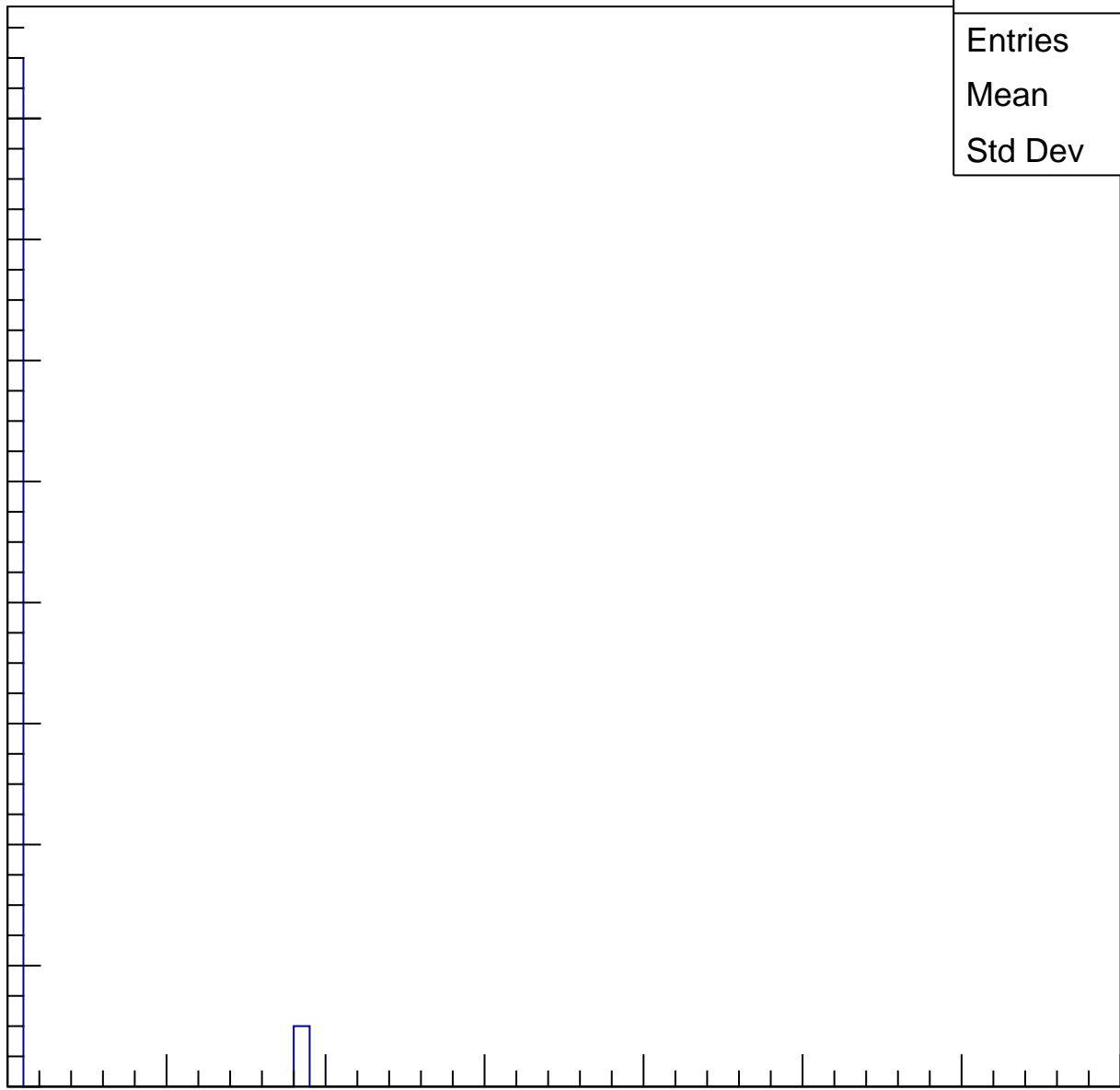
Entries	18
Mean	1
Std Dev	4.123

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch103, adc0

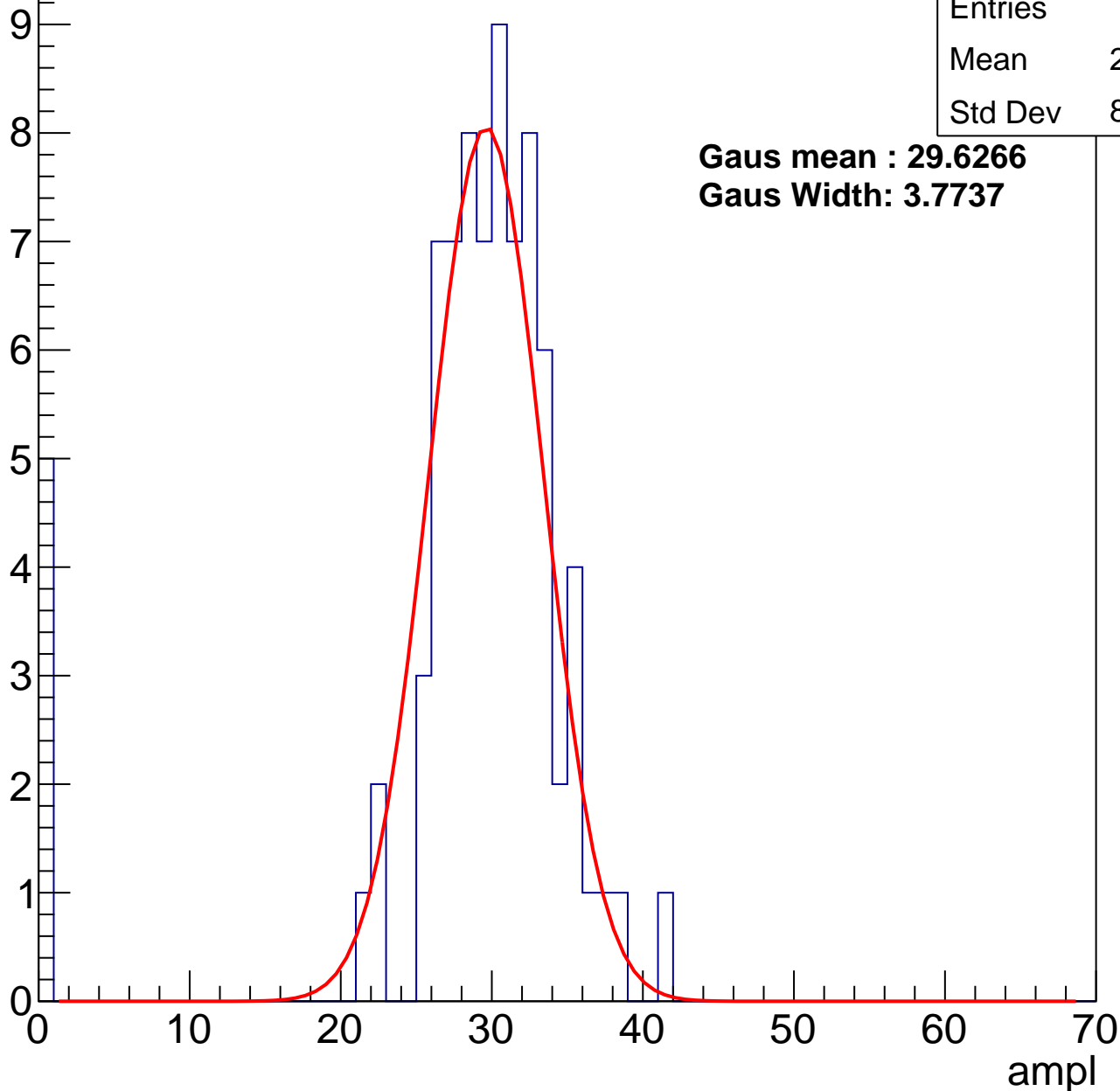
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	27.99
Std Dev	8.038

Gaus mean : 29.6266

Gaus Width: 3.7737



B1L103S, U13-ch103, adc1

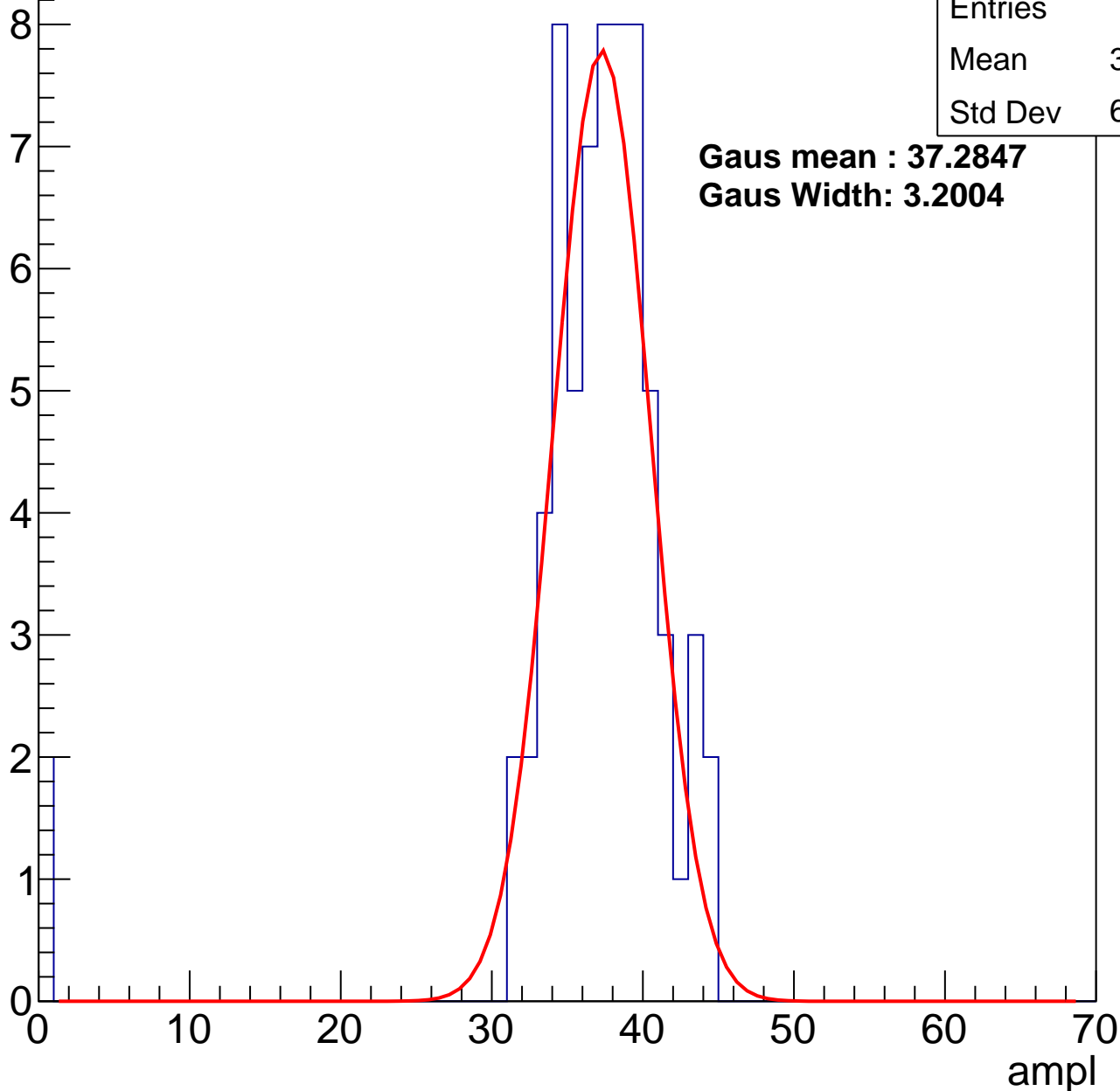
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	36.04
Std Dev	6.993

Gaus mean : 37.2847

Gaus Width: 3.2004



B1L103S, U13-ch103, adc2

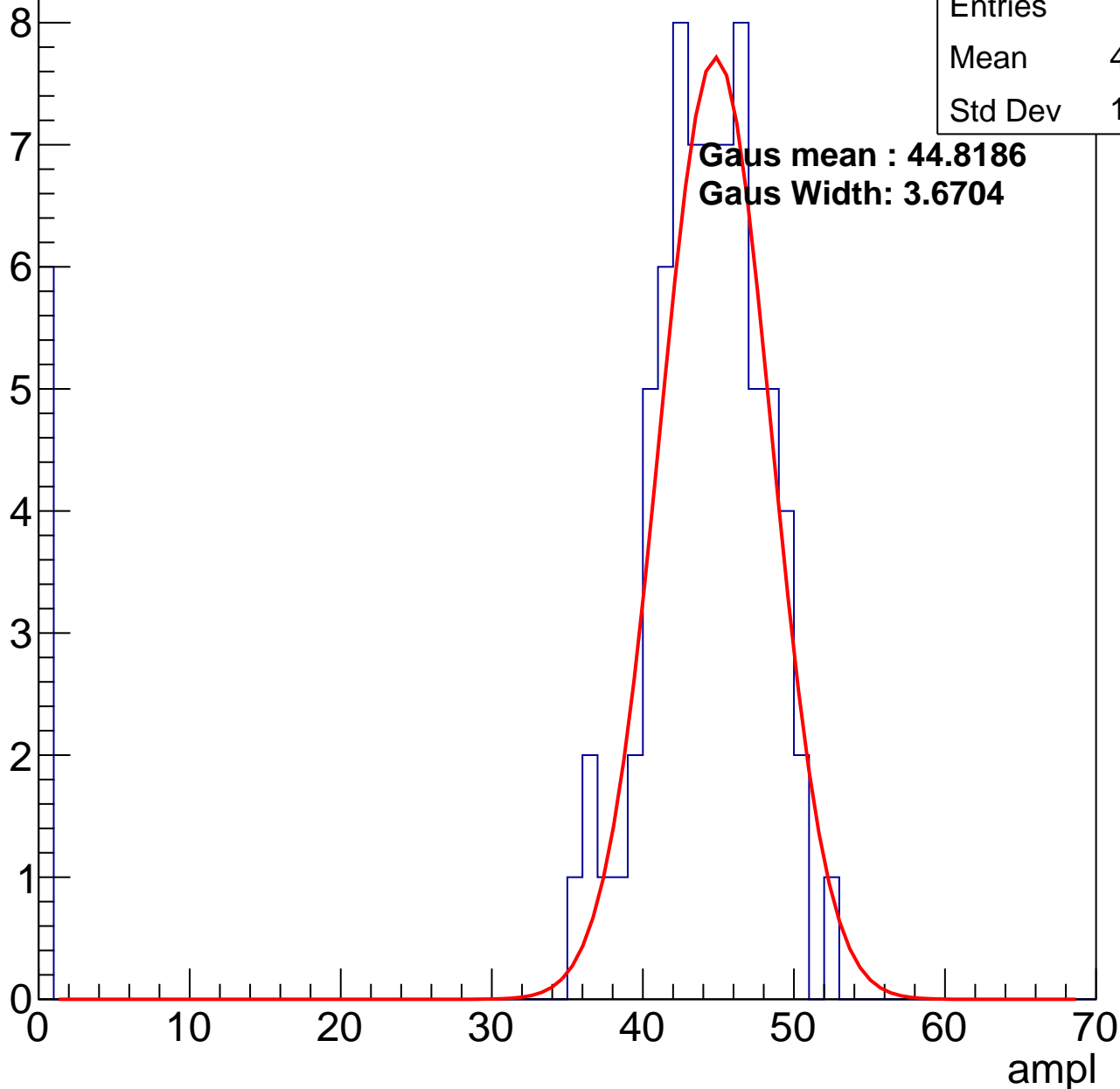
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	40.47
Std Dev	12.18

Gaus mean : 44.8186

Gaus Width: 3.6704

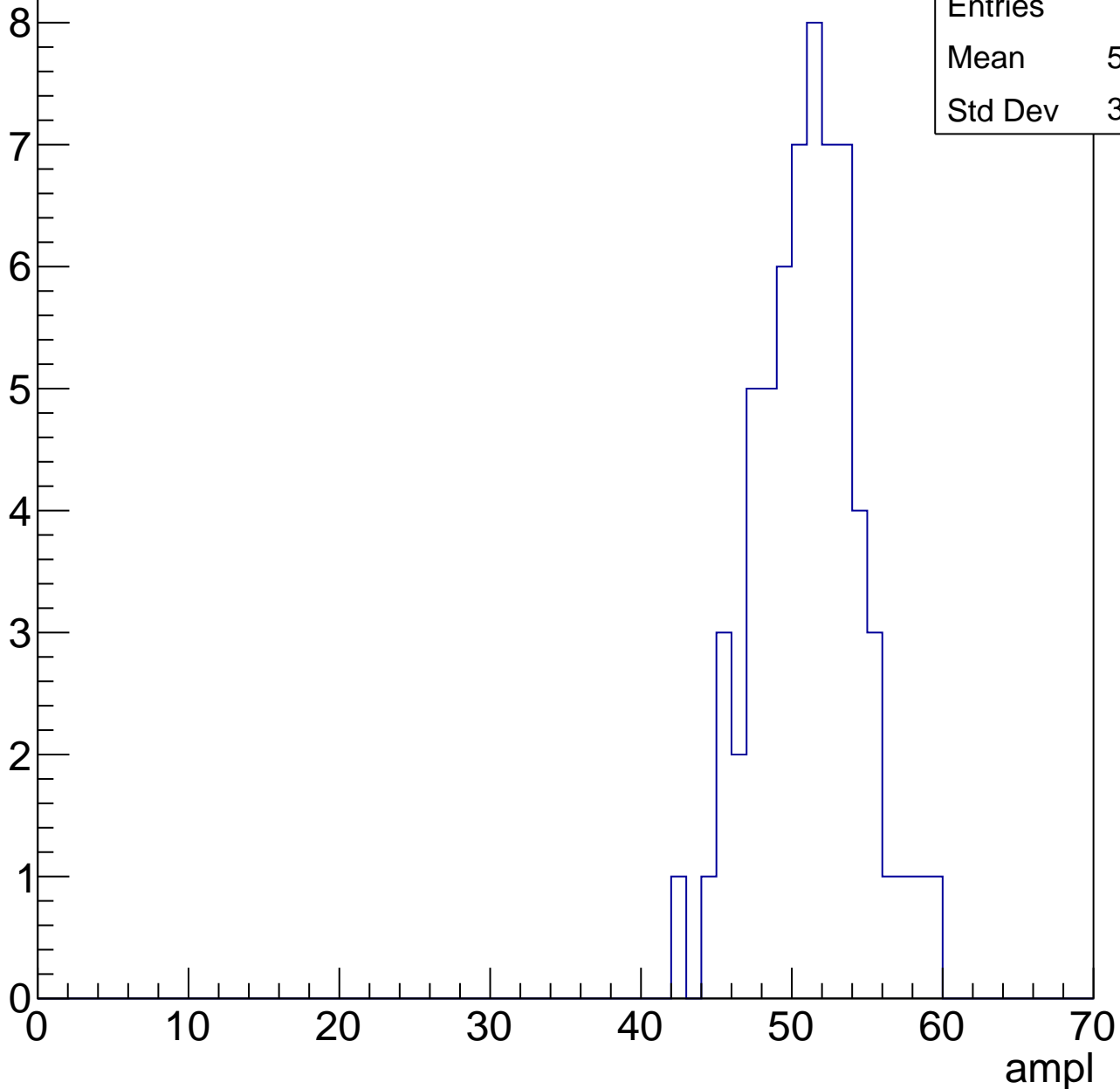


B1L103S, U13-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	50.57
Std Dev	3.393

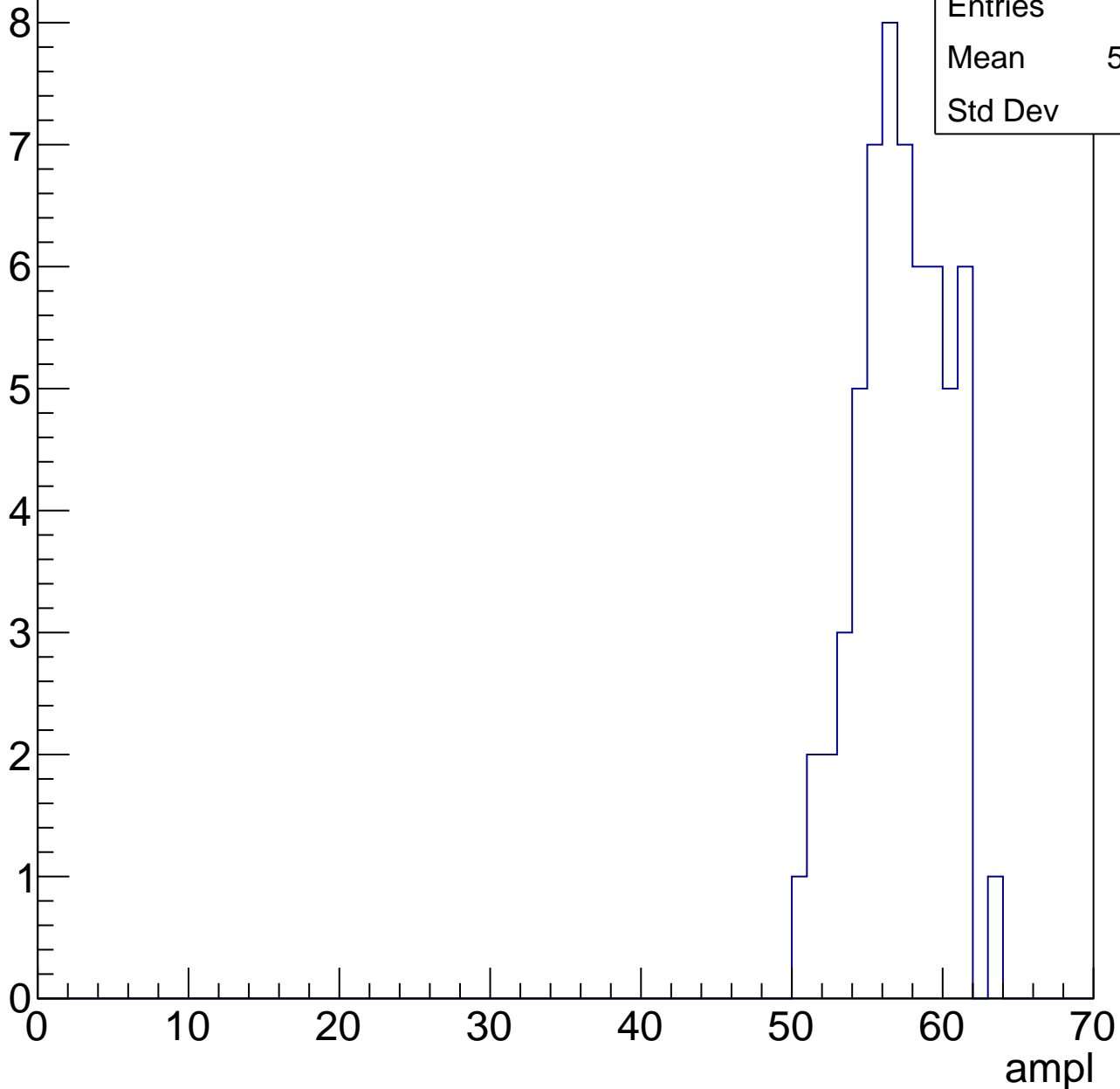


B1L103S, U13-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	56.75
Std Dev	2.92



B1L103S, U13-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	38
Mean	59.08
Std Dev	9.969

Entry

10

8

6

4

2

0

0

10

20

30

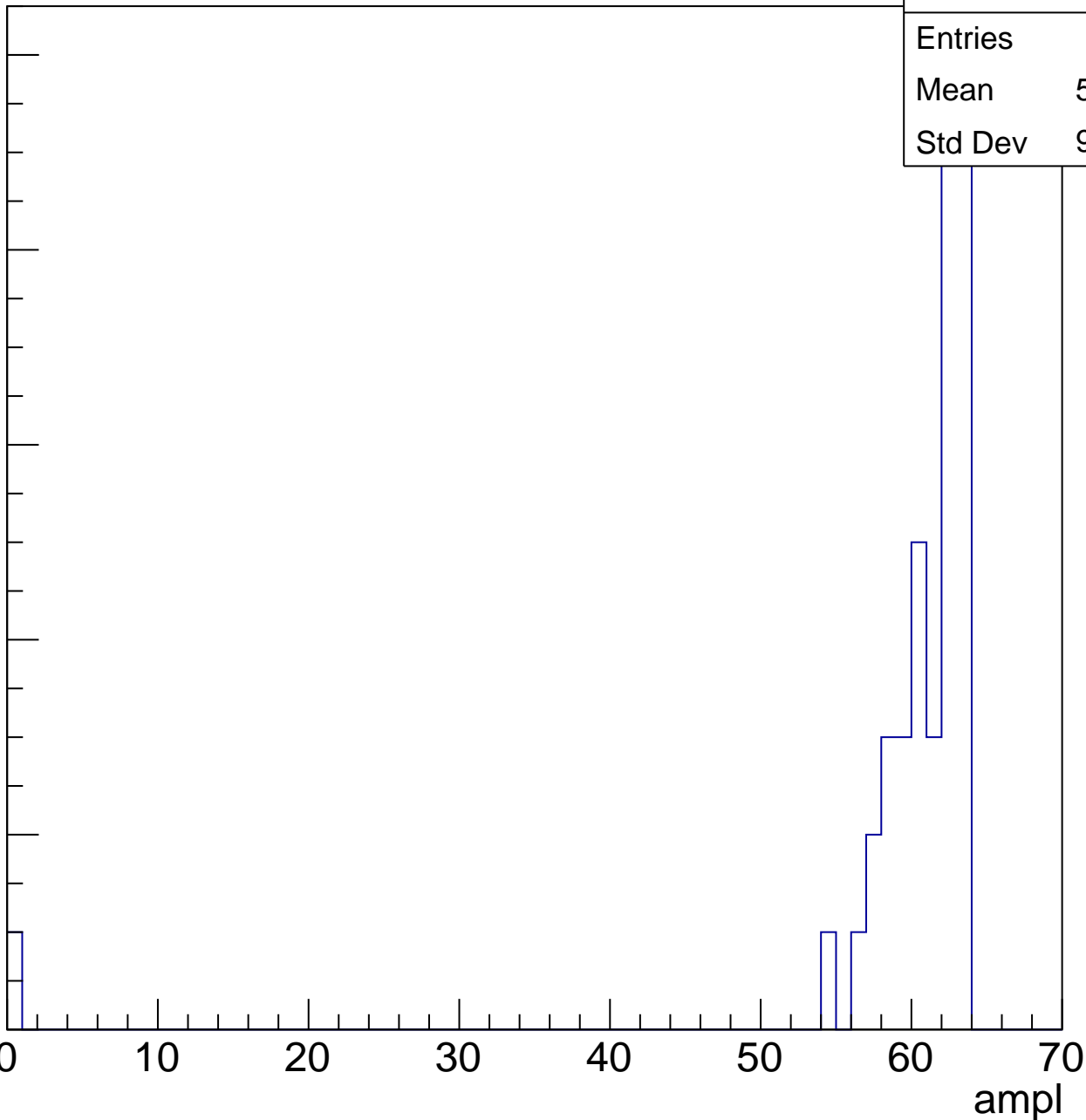
40

50

60

70

ampl



B1L103S, U13-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch104, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	24.66
Std Dev	9.176

Gaus mean : 27.7499

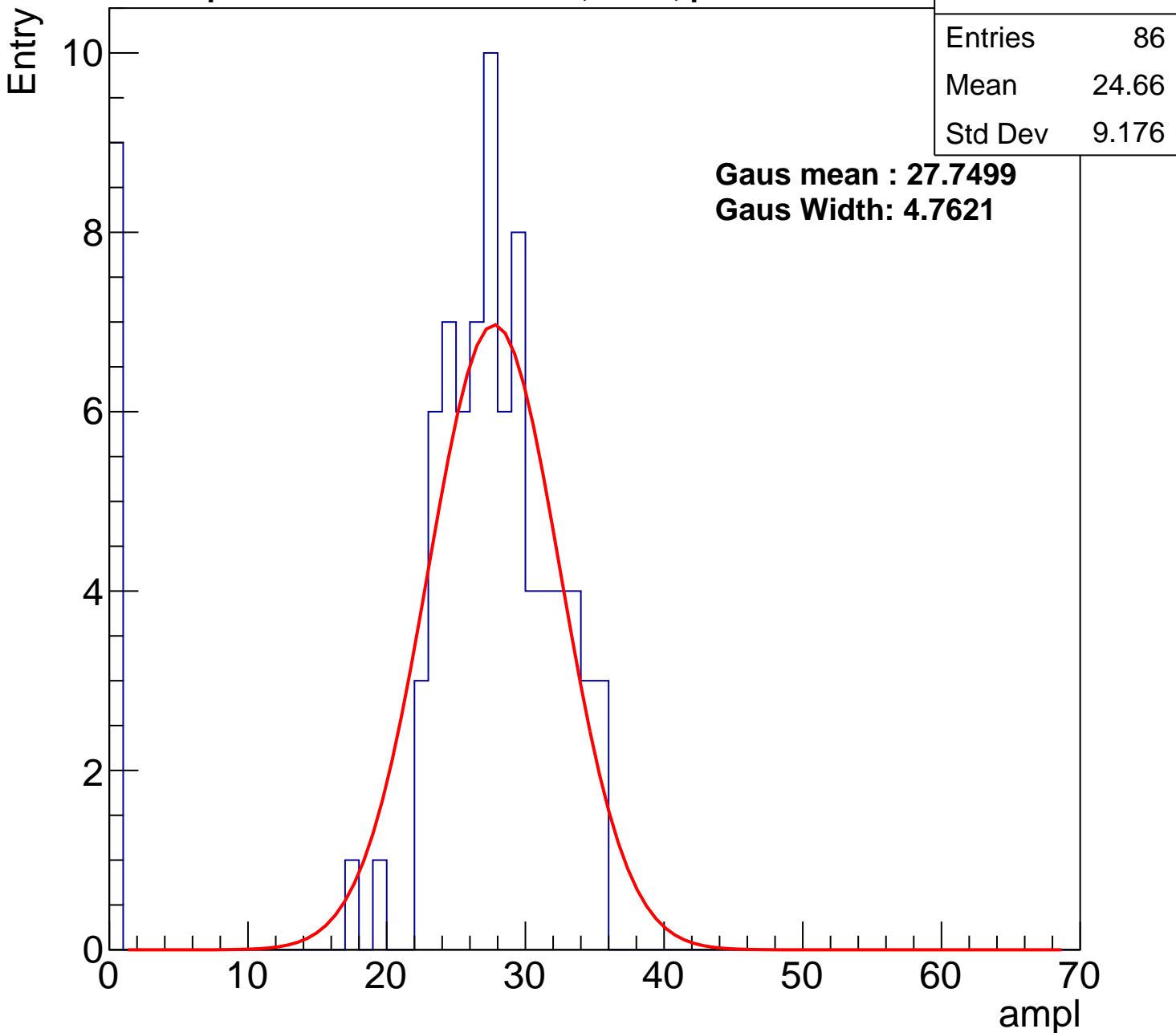
Gaus Width: 4.7621

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch104, adc1

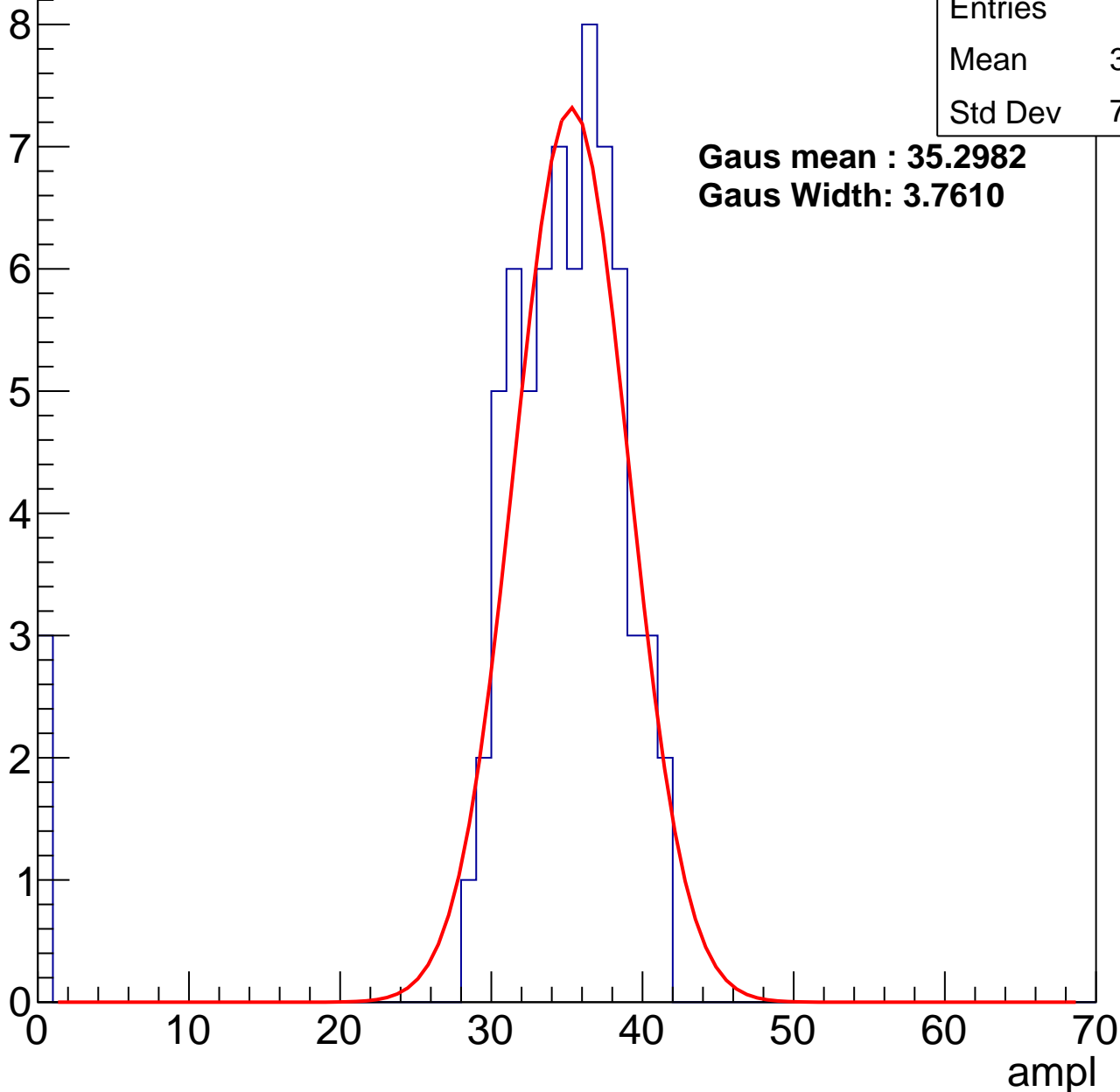
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	33.17
Std Dev	7.694

Gaus mean : 35.2982

Gaus Width: 3.7610



B1L103S, U13-ch104, adc2

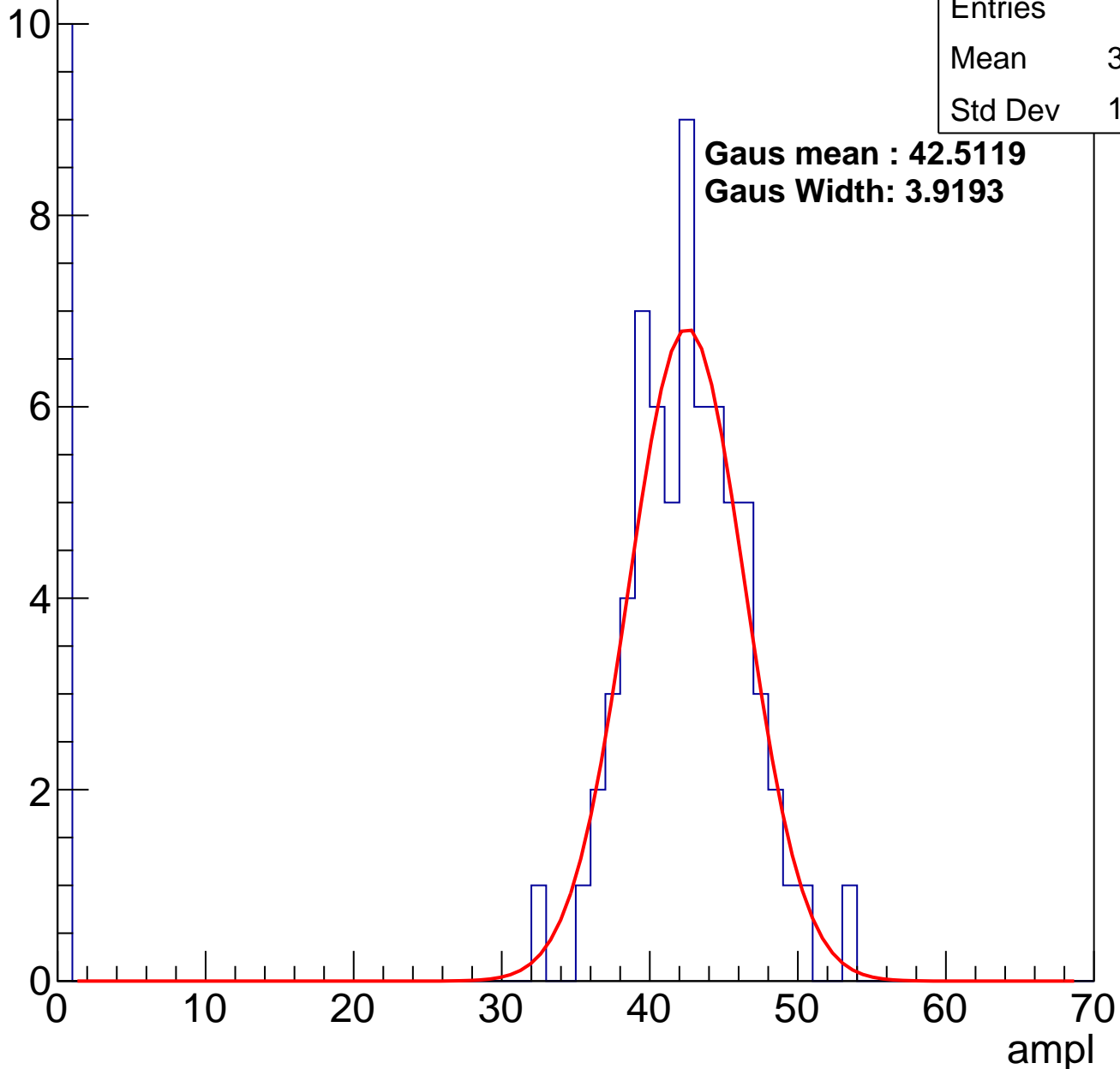
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	36.72
Std Dev	14.52

Gaus mean : 42.5119

Gaus Width: 3.9193

Entry

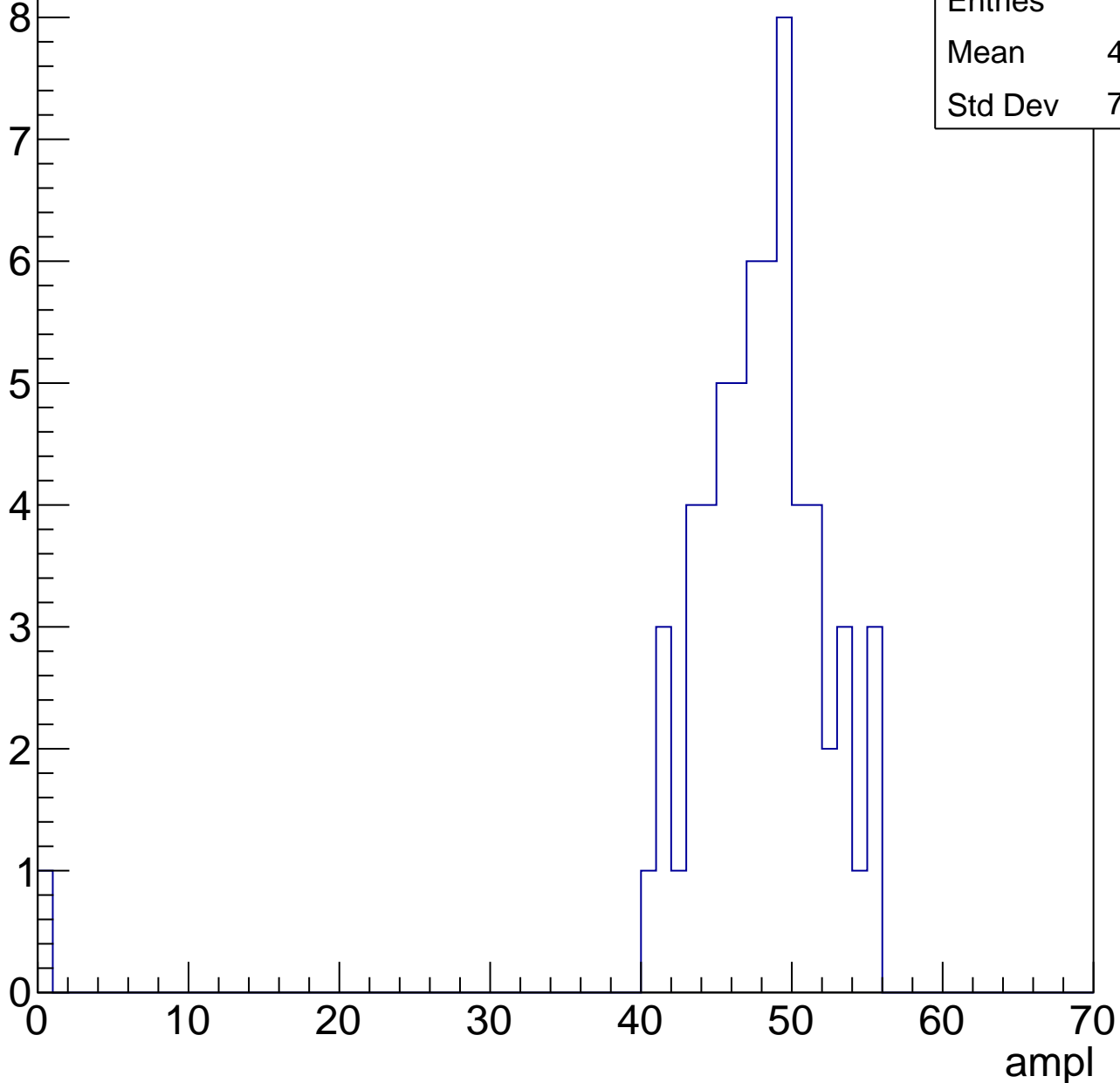


B1L103S, U13-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	46.82
Std Dev	7.072

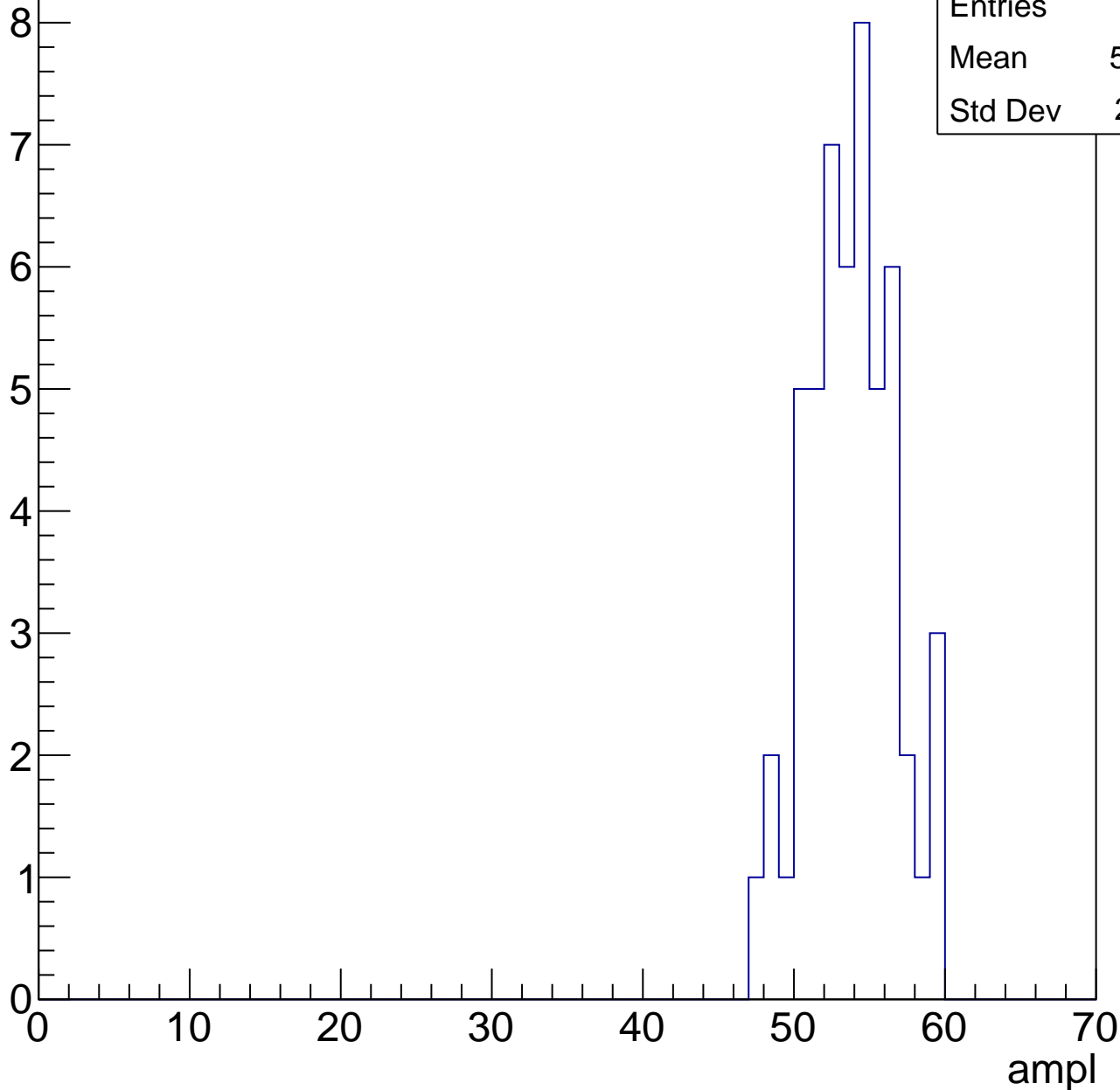


B1L103S, U13-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.29
Std Dev	2.831

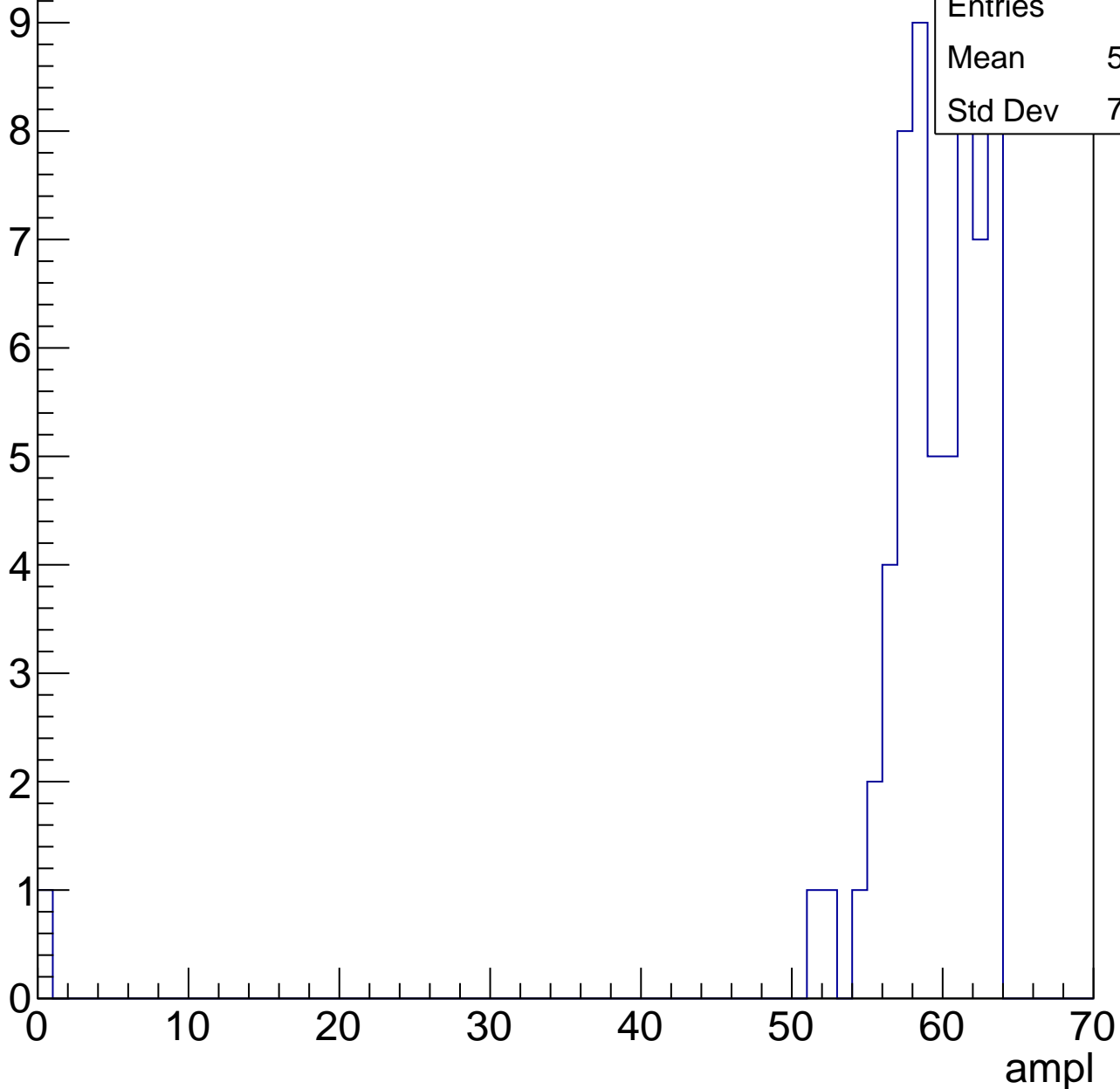


B1L103S, U13-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

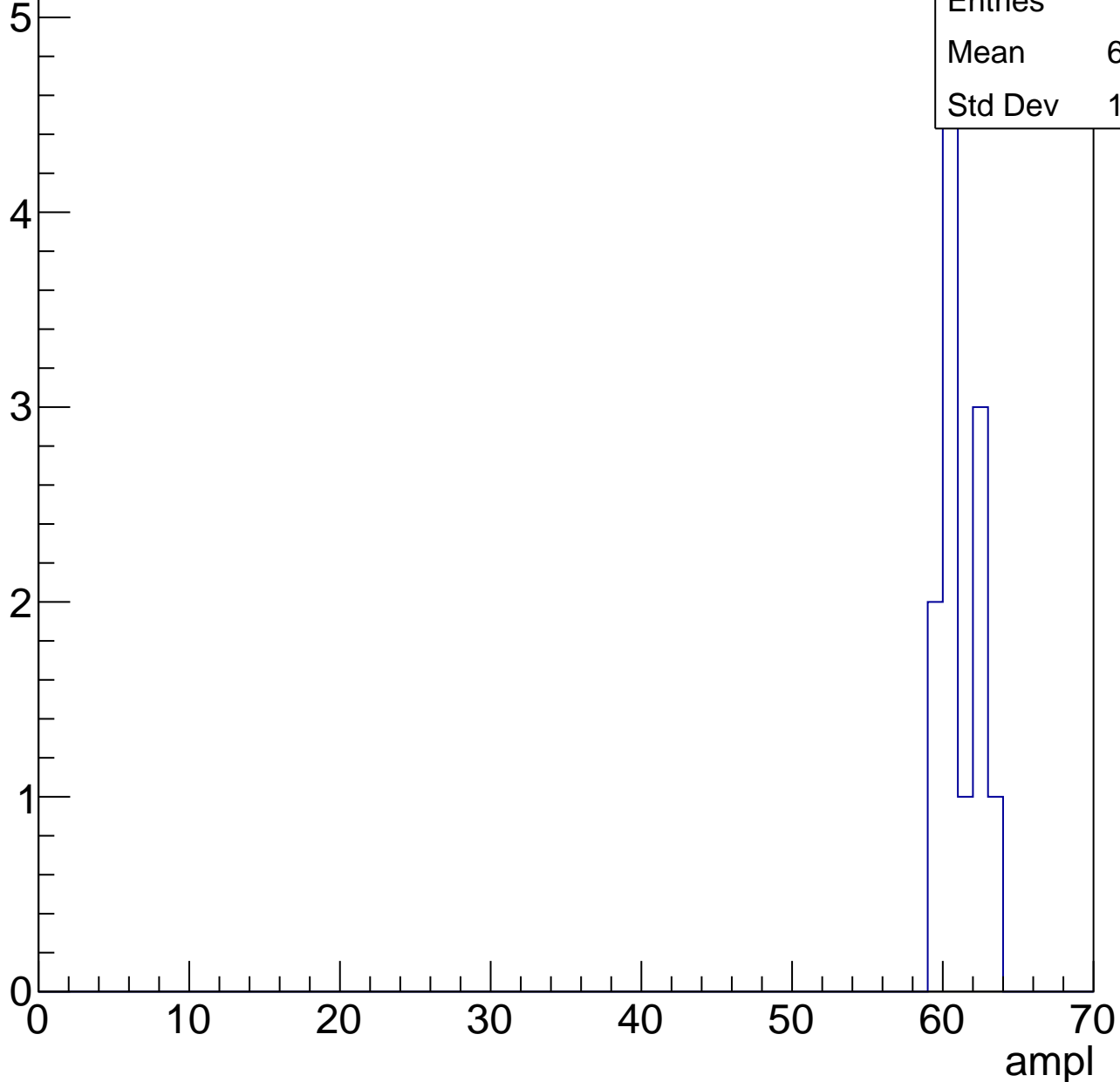
Entries	62
Mean	58.29
Std Dev	7.977



B1L103S, U13-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch105, adc0

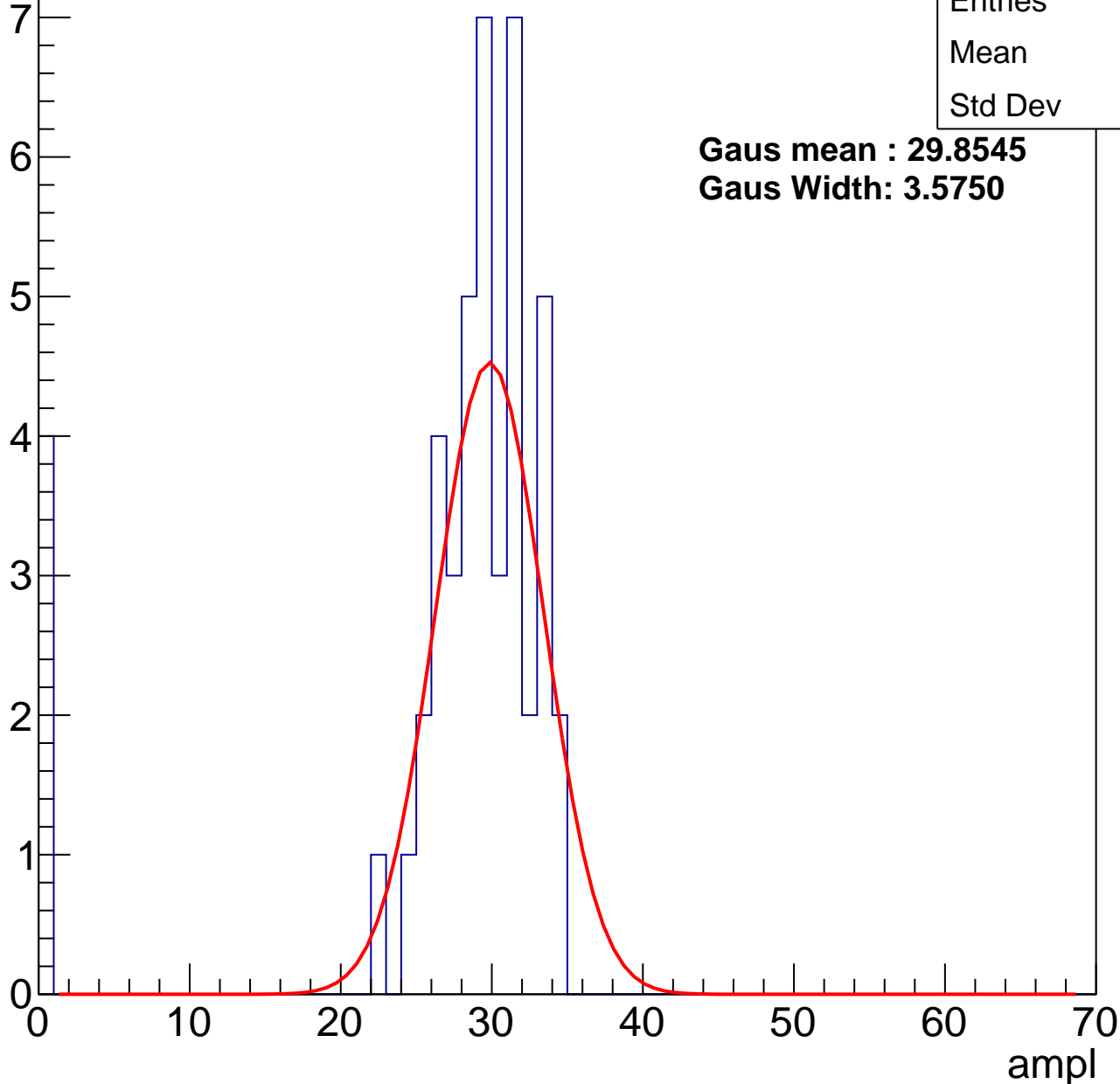
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	26.7
Std Dev	8.67

Gaus mean : 29.8545

Gaus Width: 3.5750



B1L103S, U13-ch105, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	30.75
Std Dev	11.5

Gaus mean : 35.2559

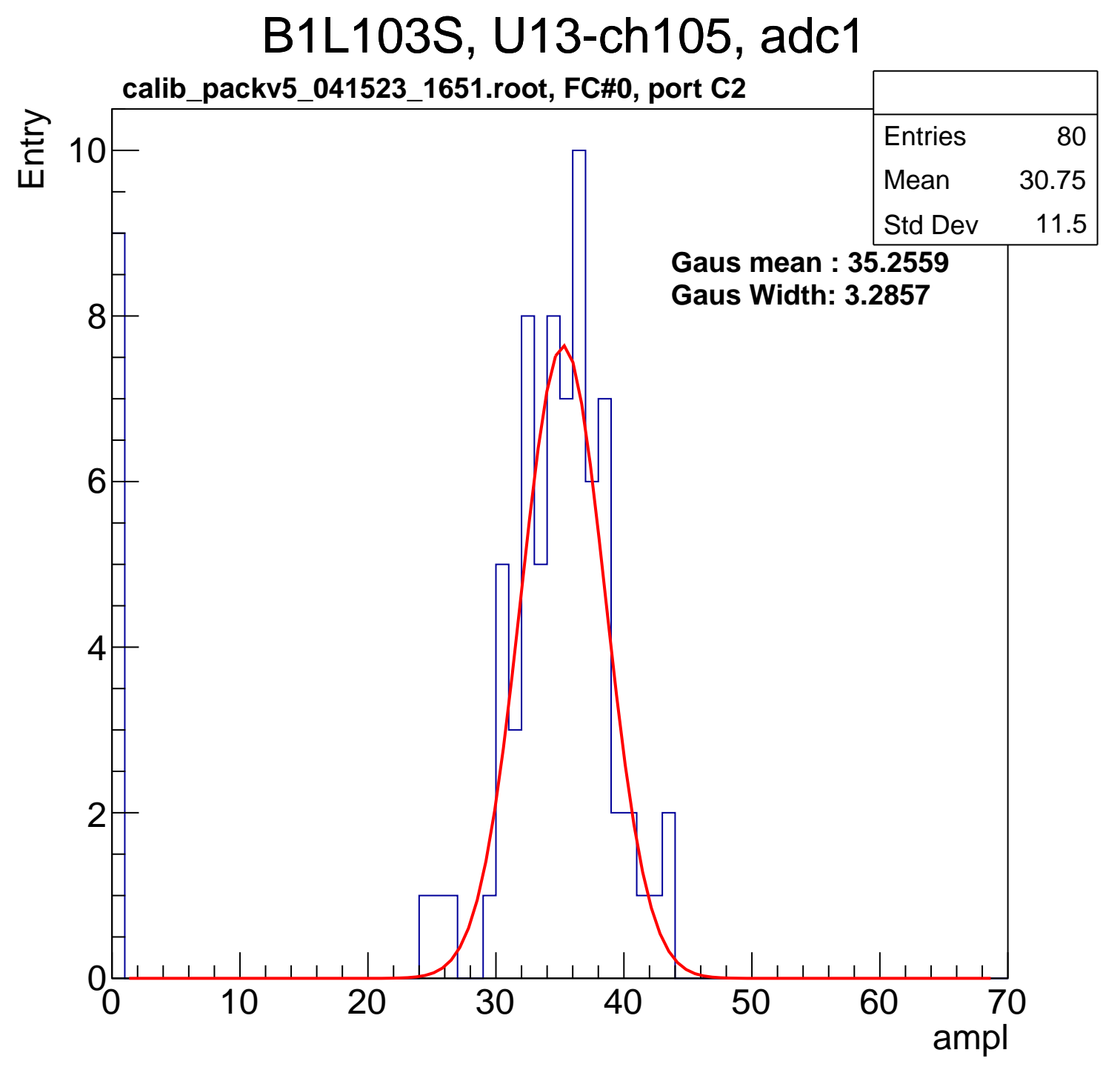
Gaus Width: 3.2857

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch105, adc2

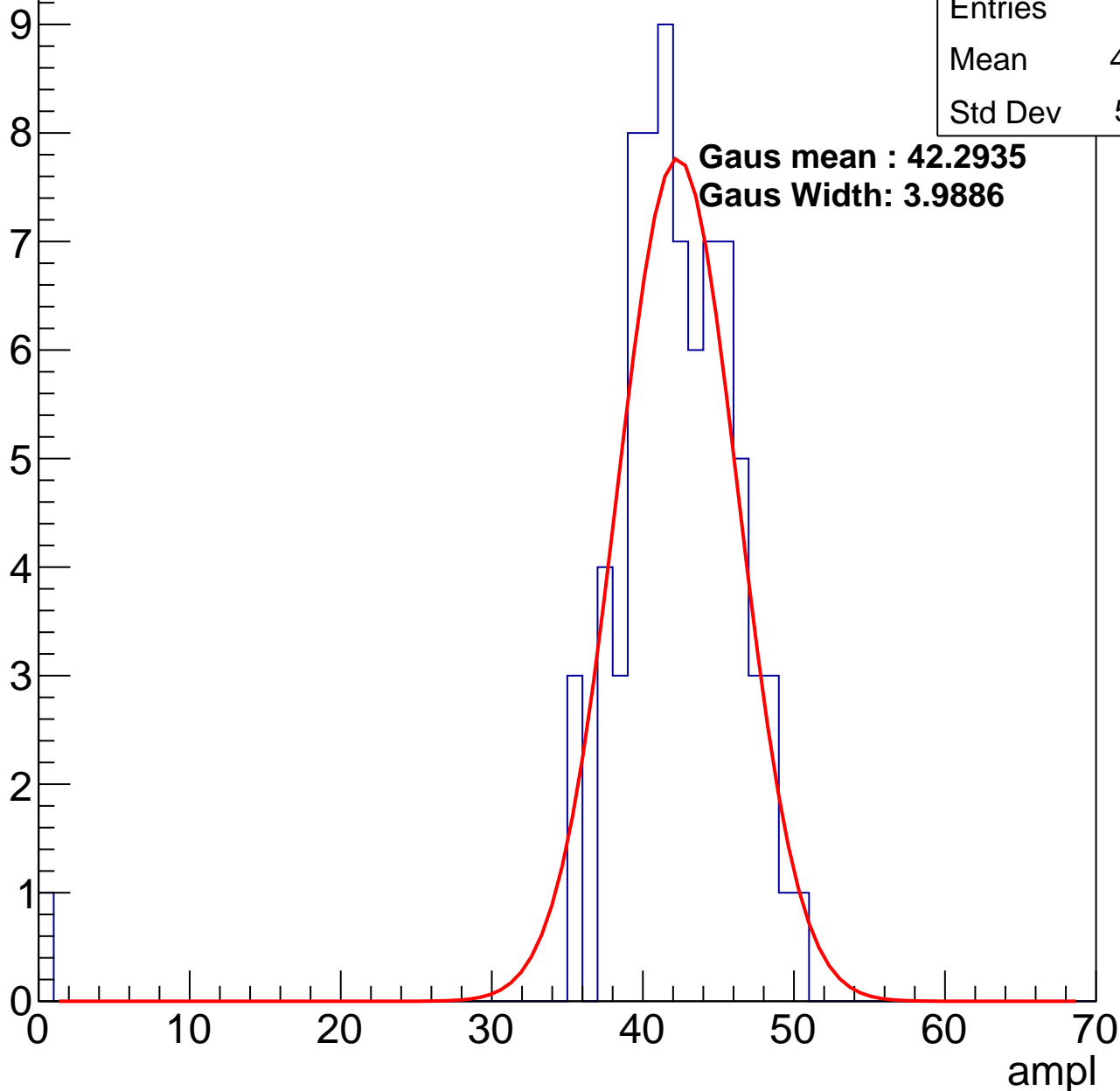
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	41.54
Std Dev	5.881

Gaus mean : 42.2935

Gaus Width: 3.9886

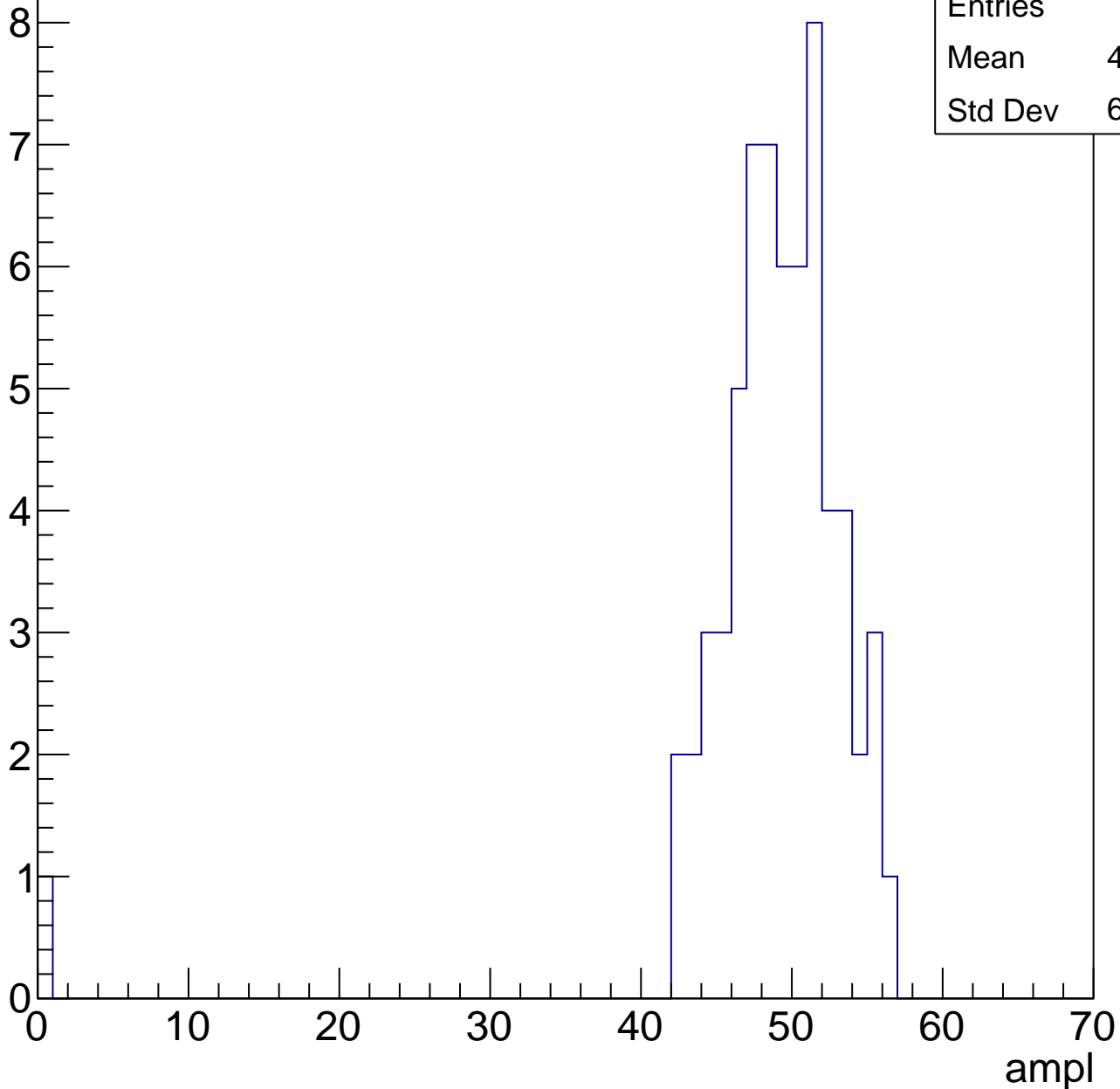


B1L103S, U13-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	48.17
Std Dev	6.932

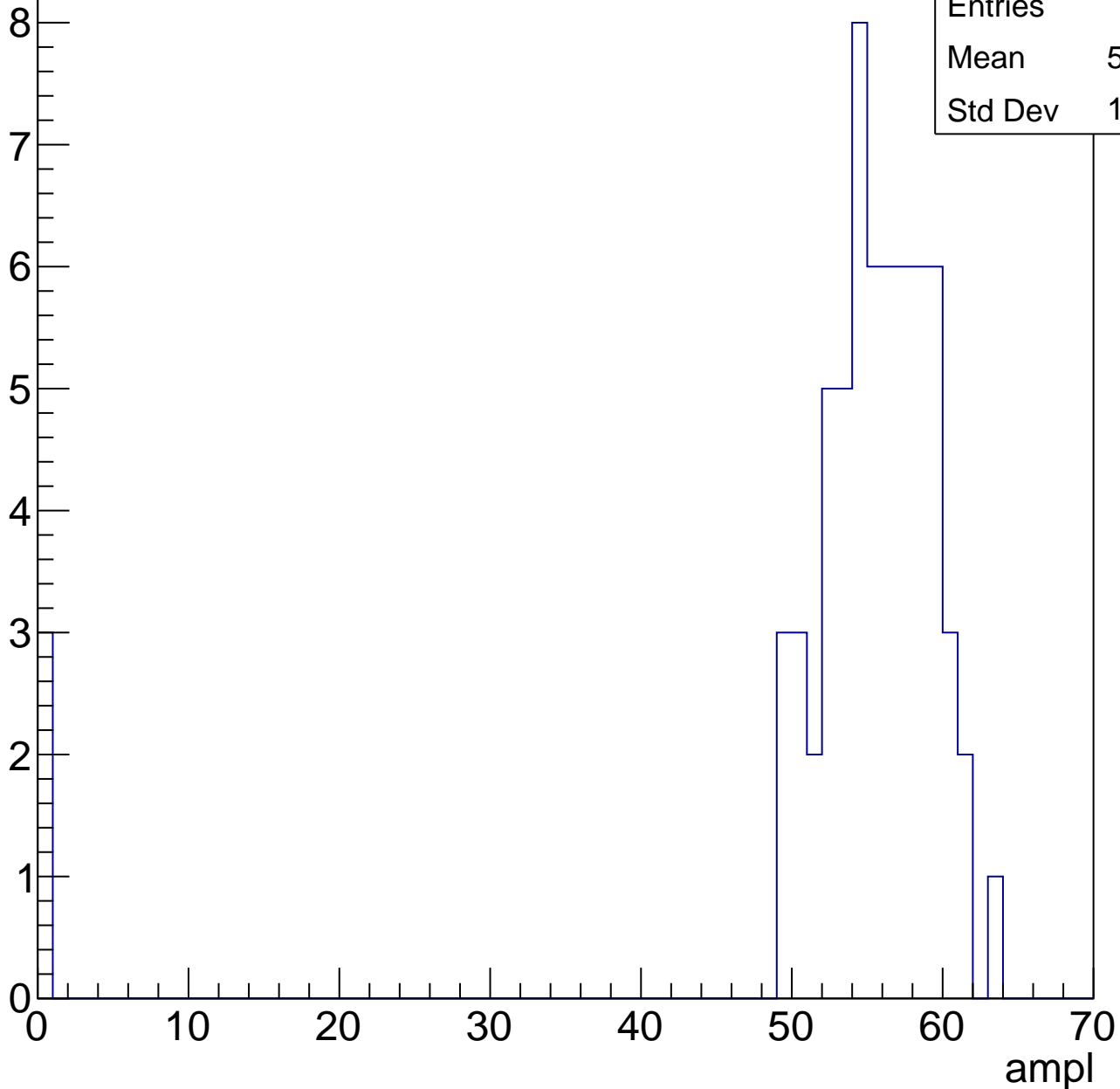


B1L103S, U13-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	52.78
Std Dev	12.05

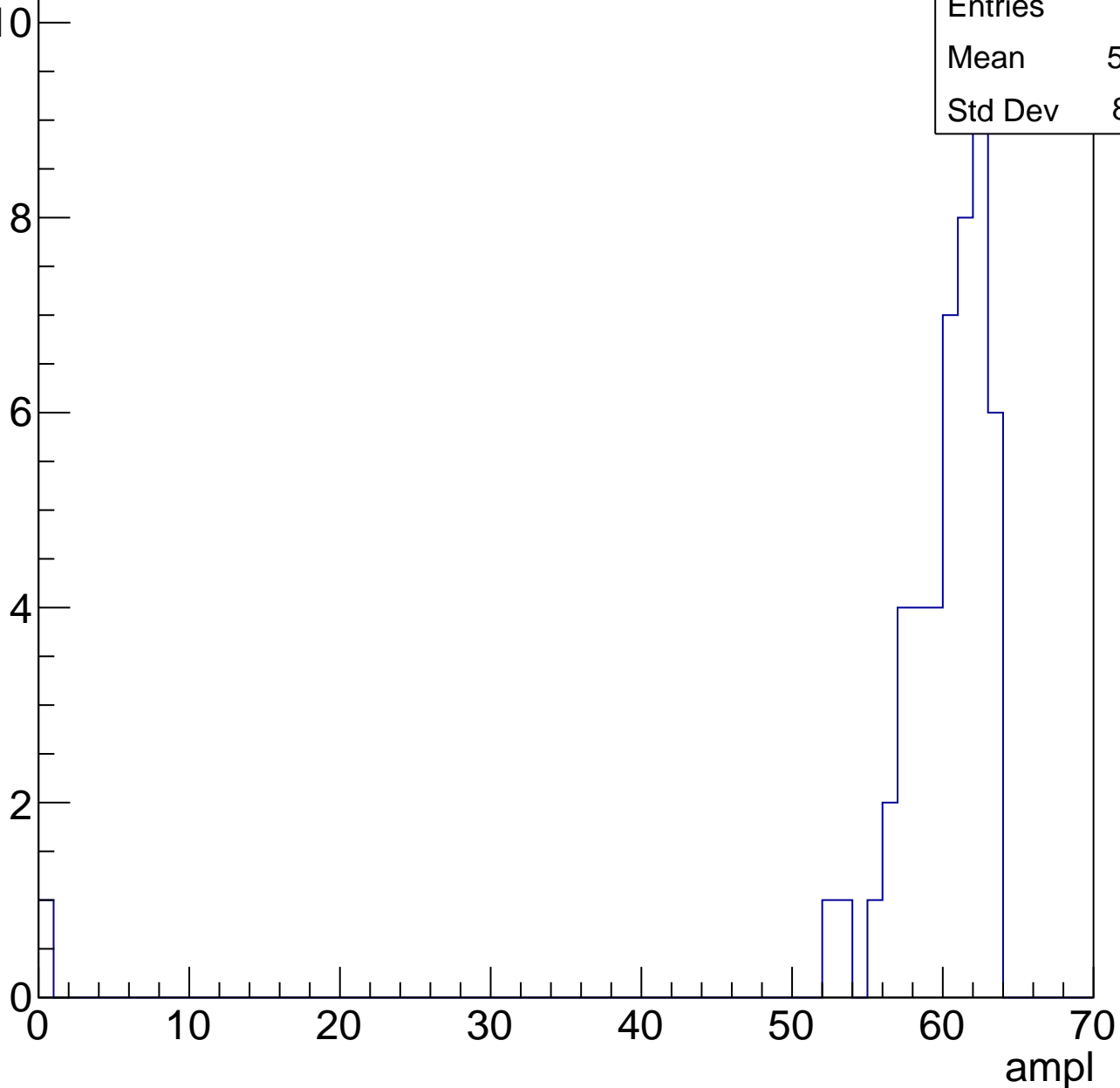


B1L103S, U13-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

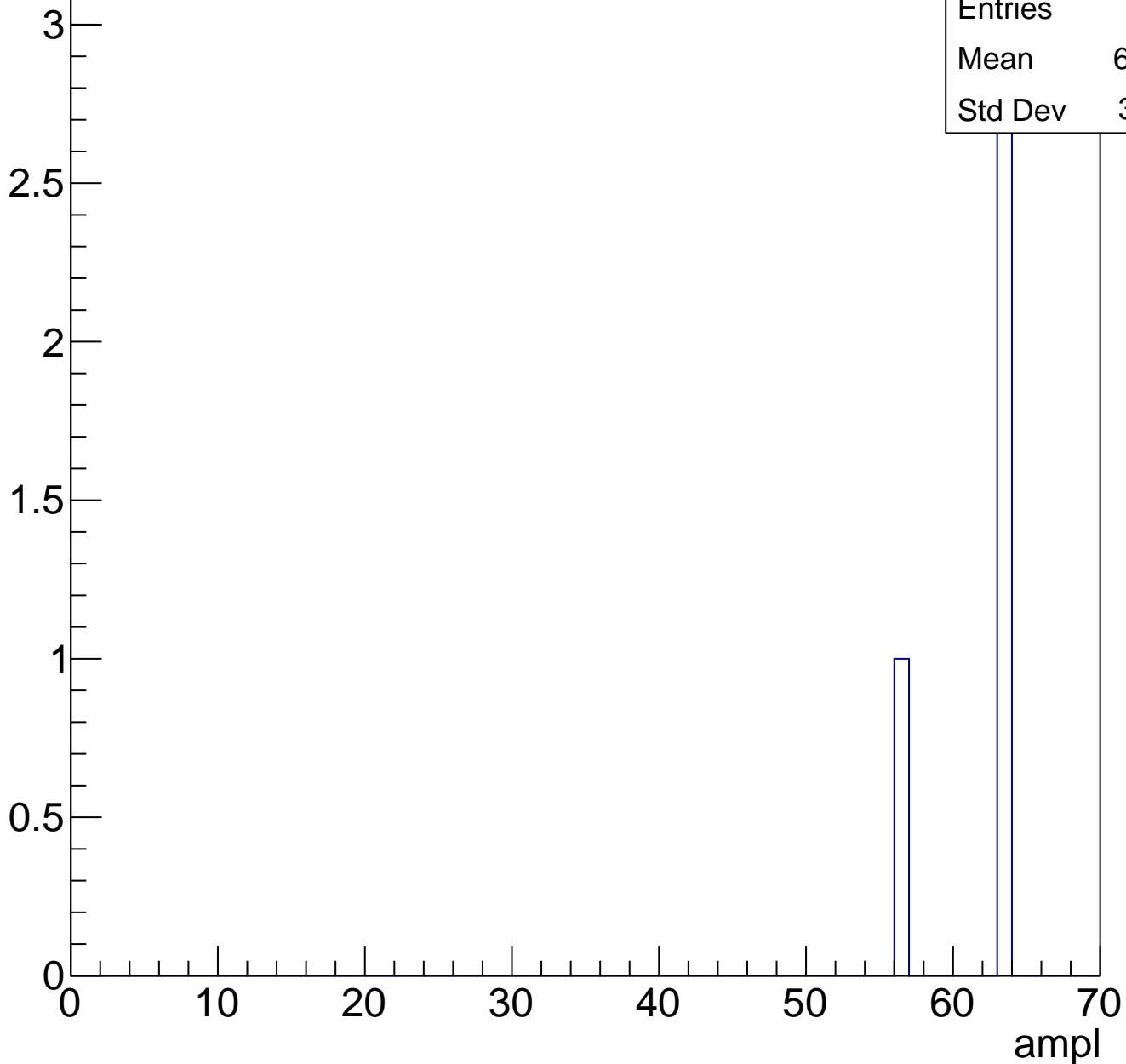
Entries	49
Mean	58.65
Std Dev	8.851



B1L103S, U13-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

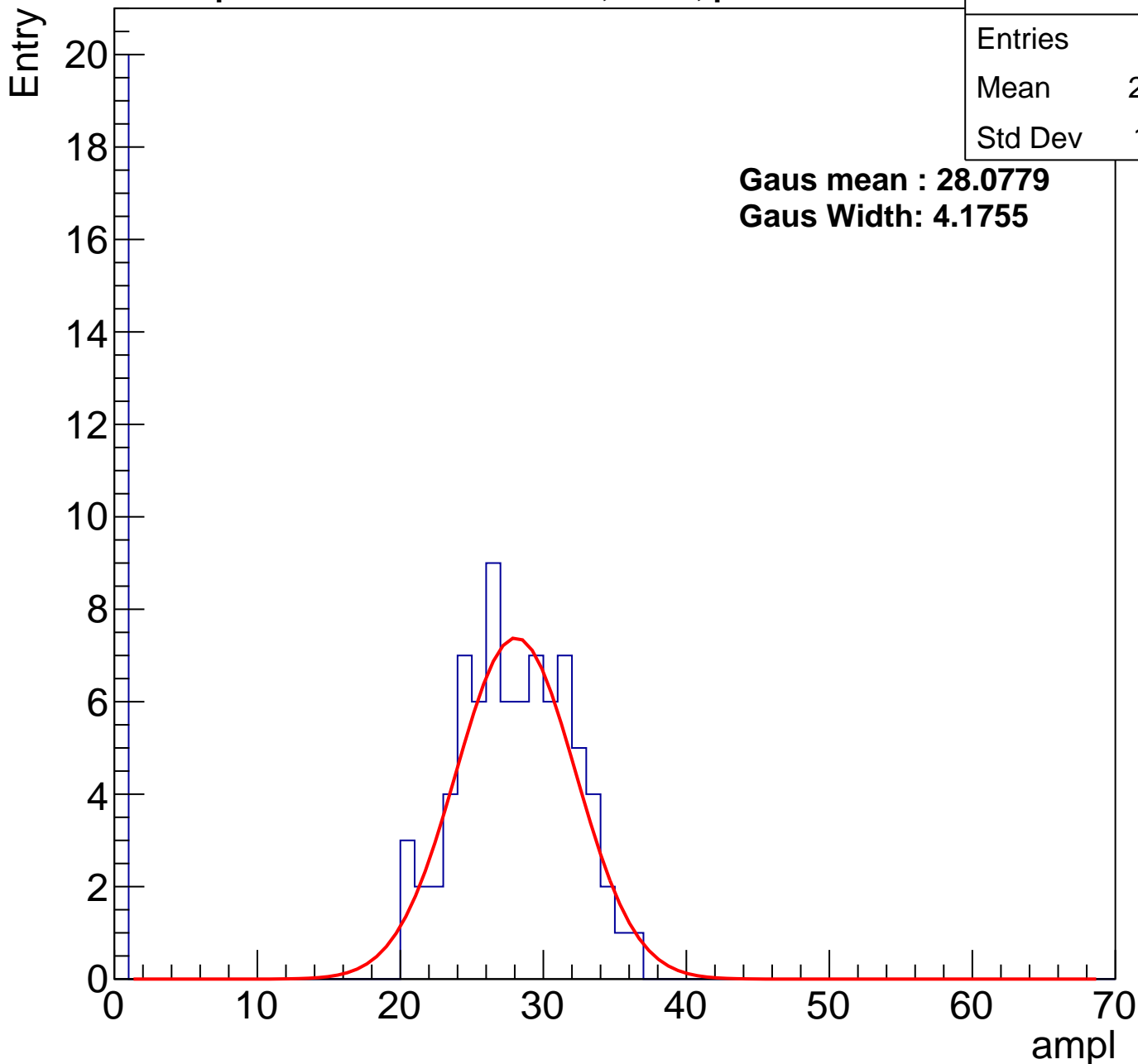
B1L103S, U13-ch106, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	21.95
Std Dev	11.61

Gaus mean : 28.0779

Gaus Width: 4.1755



B1L103S, U13-ch106, adc1

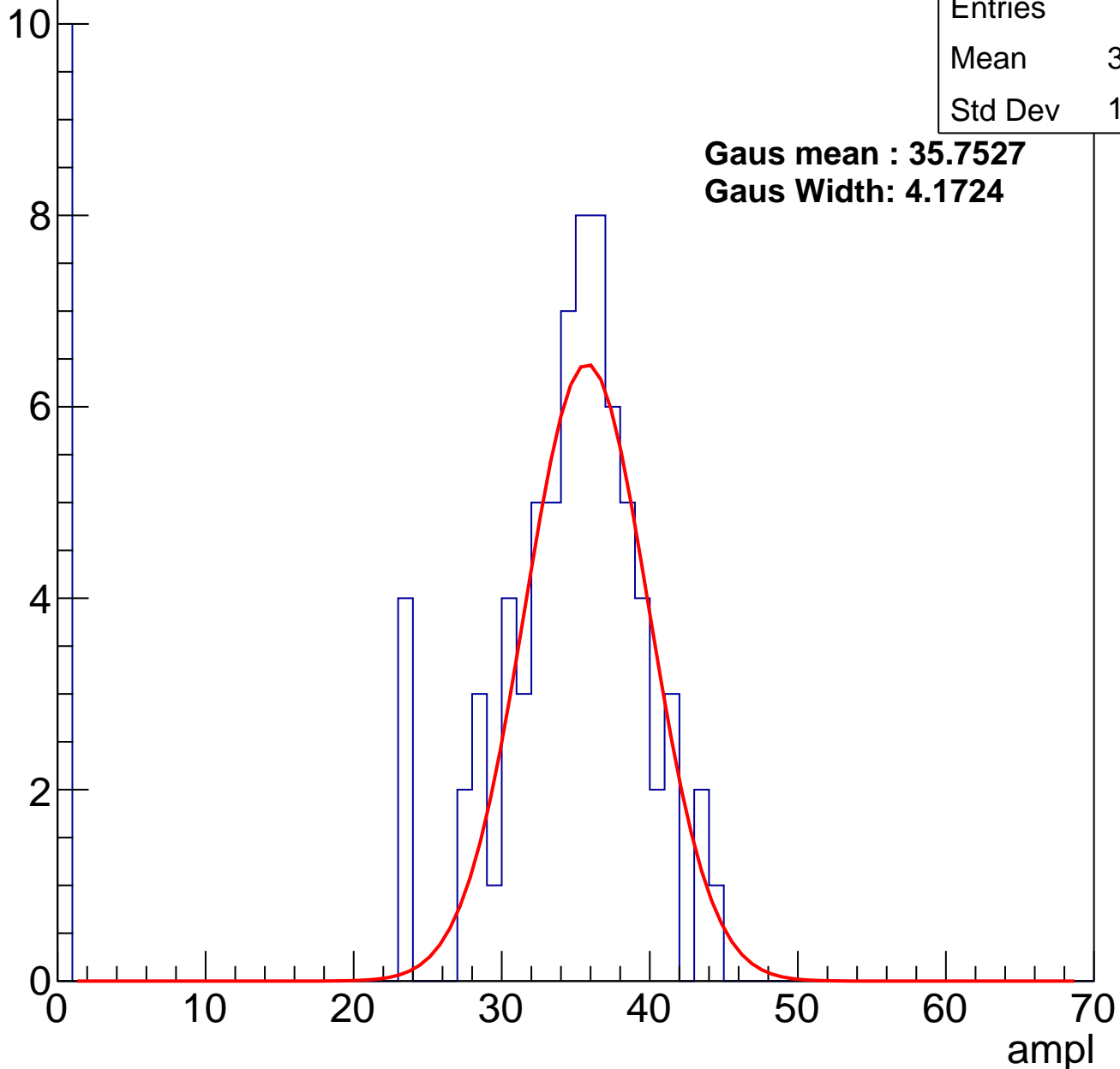
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	30.17
Std Dev	11.99

Gaus mean : 35.7527

Gaus Width: 4.1724

Entry



B1L103S, U13-ch106, adc2

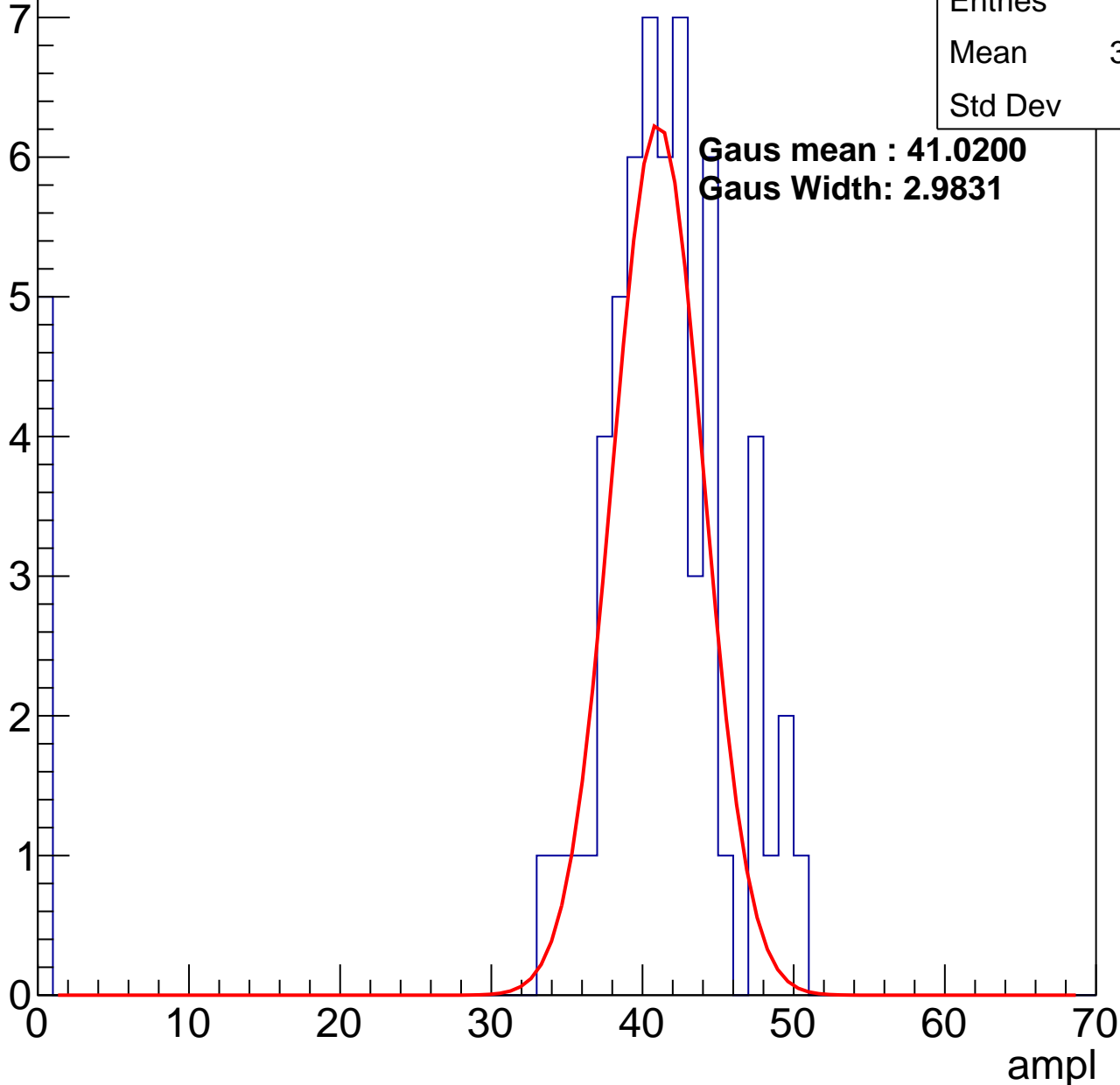
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	37.94
Std Dev	11.8

Gaus mean : 41.0200

Gaus Width: 2.9831

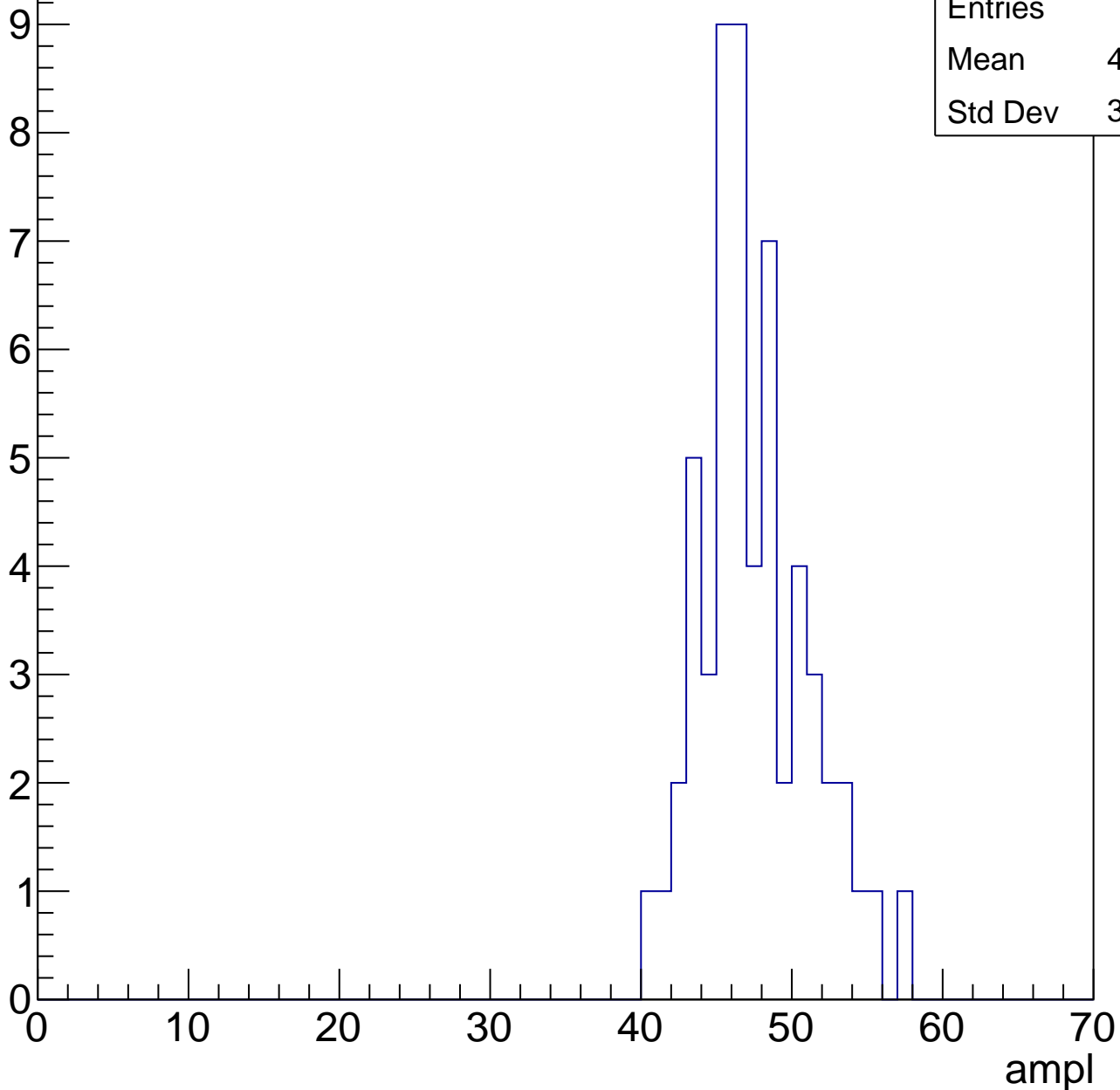


B1L103S, U13-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	47.05
Std Dev	3.556

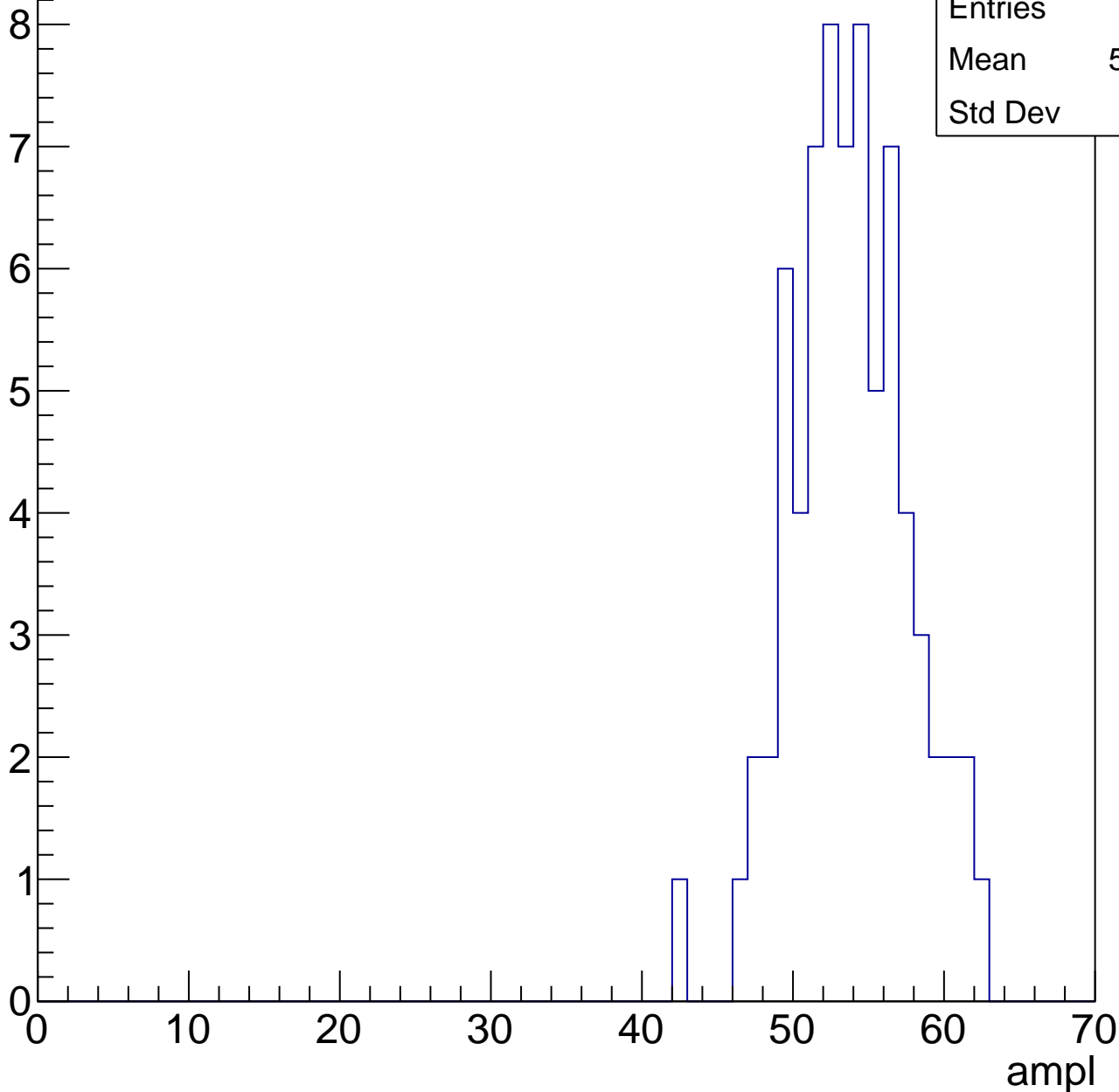


B1L103S, U13-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	53.32
Std Dev	3.84

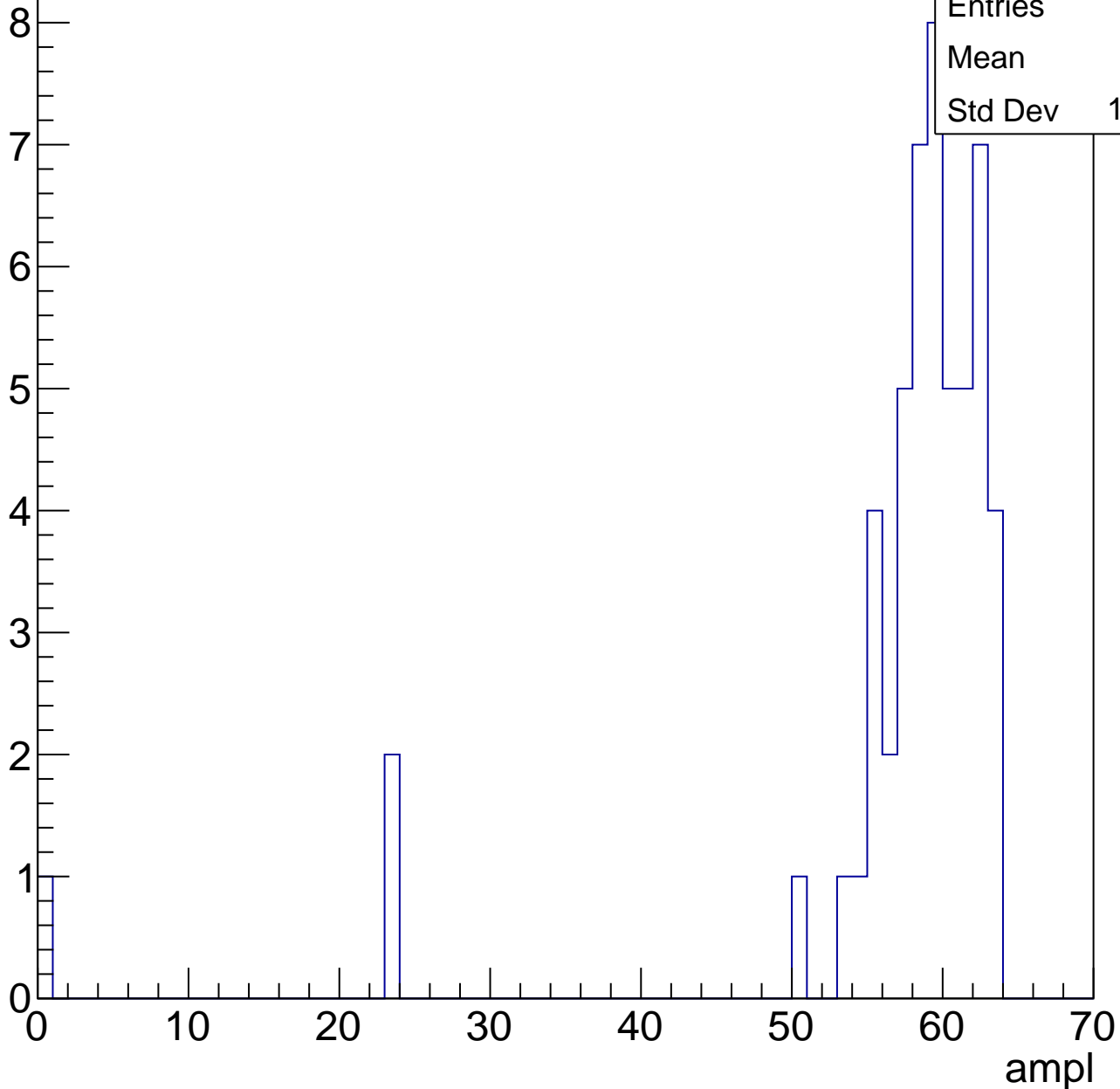


B1L103S, U13-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	56.4
Std Dev	10.74

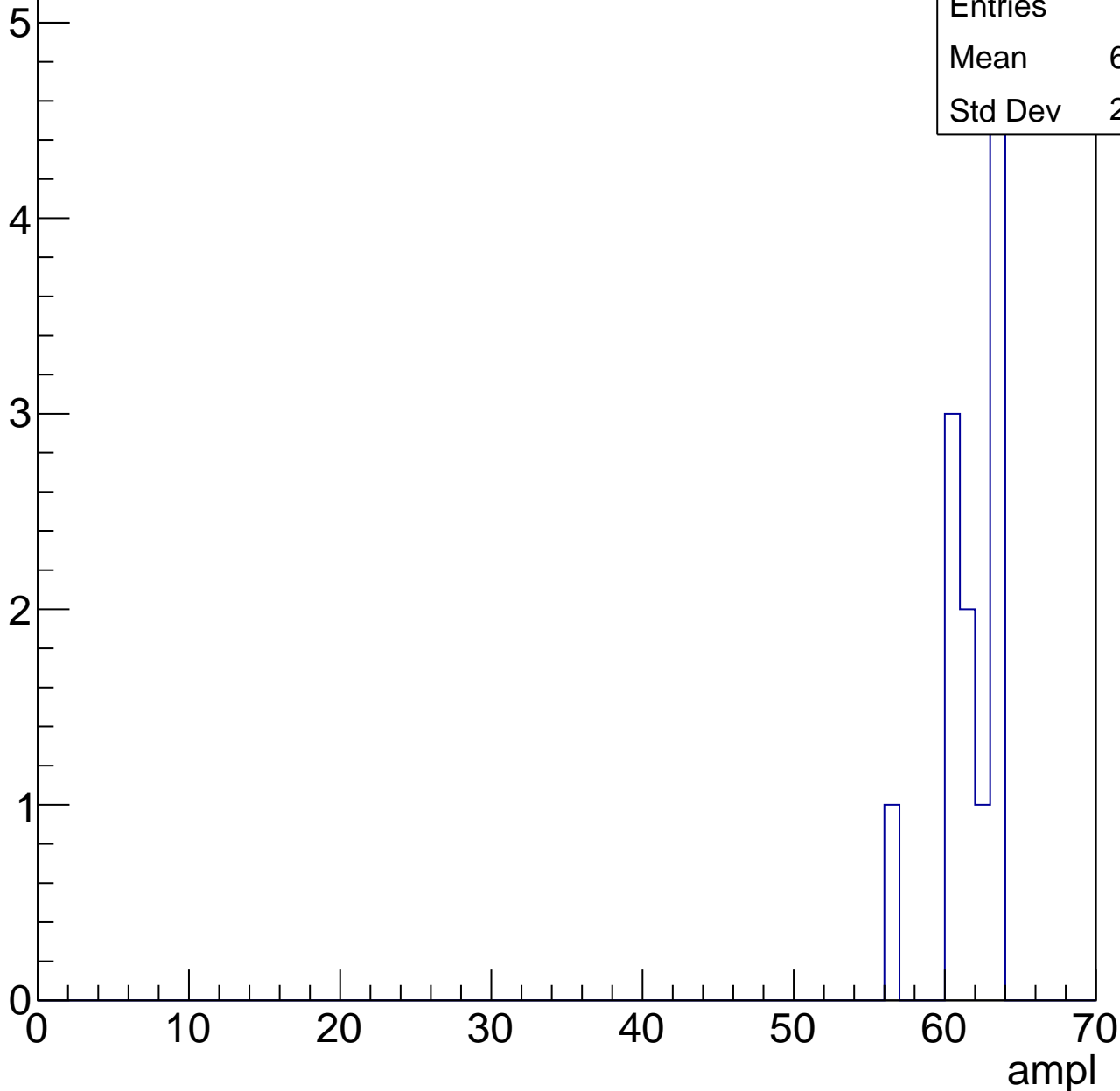


B1L103S, U13-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.25
Std Dev	2.005

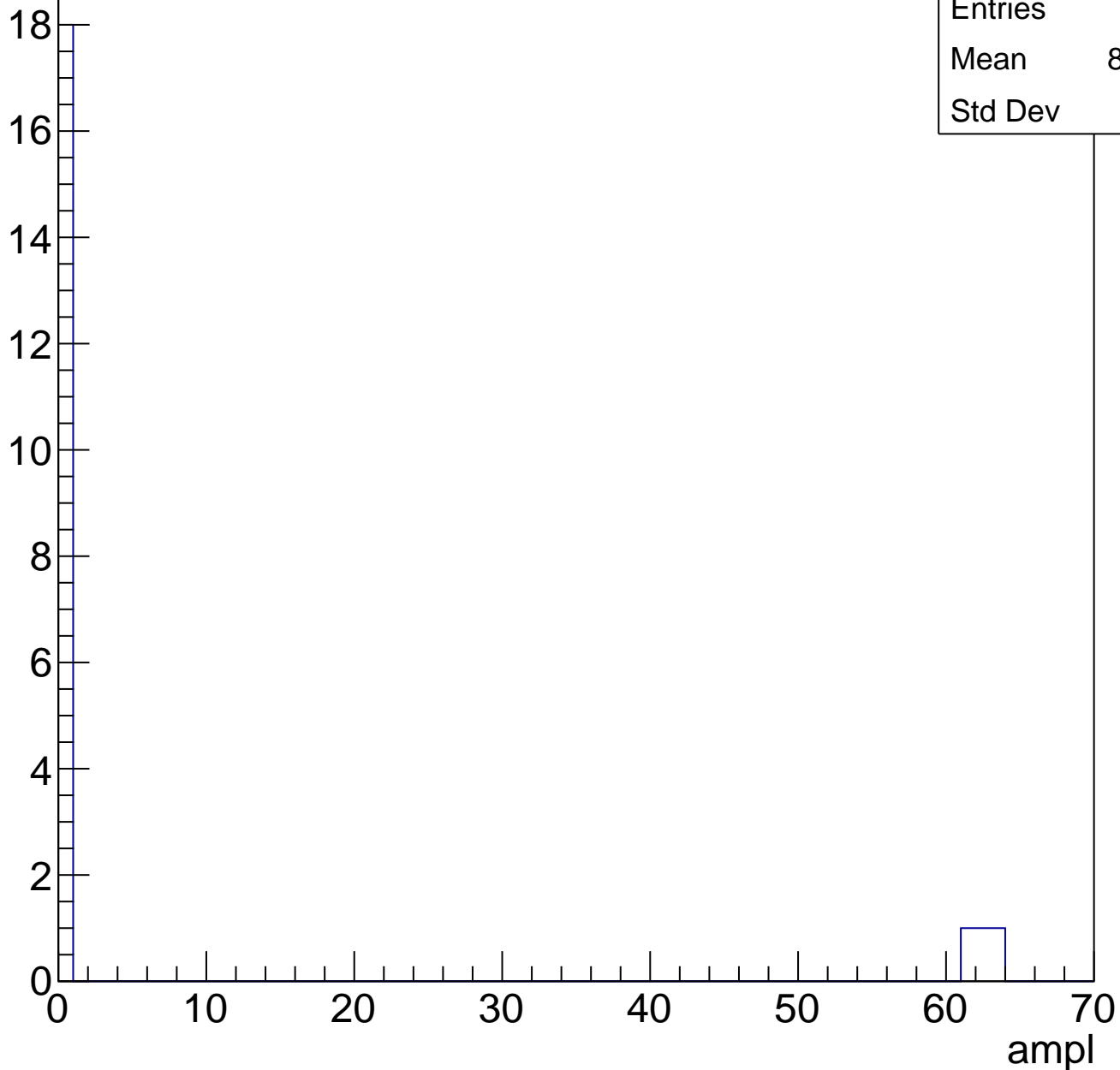


B1L103S, U13-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	8.857
Std Dev	21.7

Entry



B1L103S, U13-ch107, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	26.28
Std Dev	11.26

Gaus mean : 31.1291

Gaus Width: 3.8572

Entry

10

8

6

4

2

0

0

10

20

30

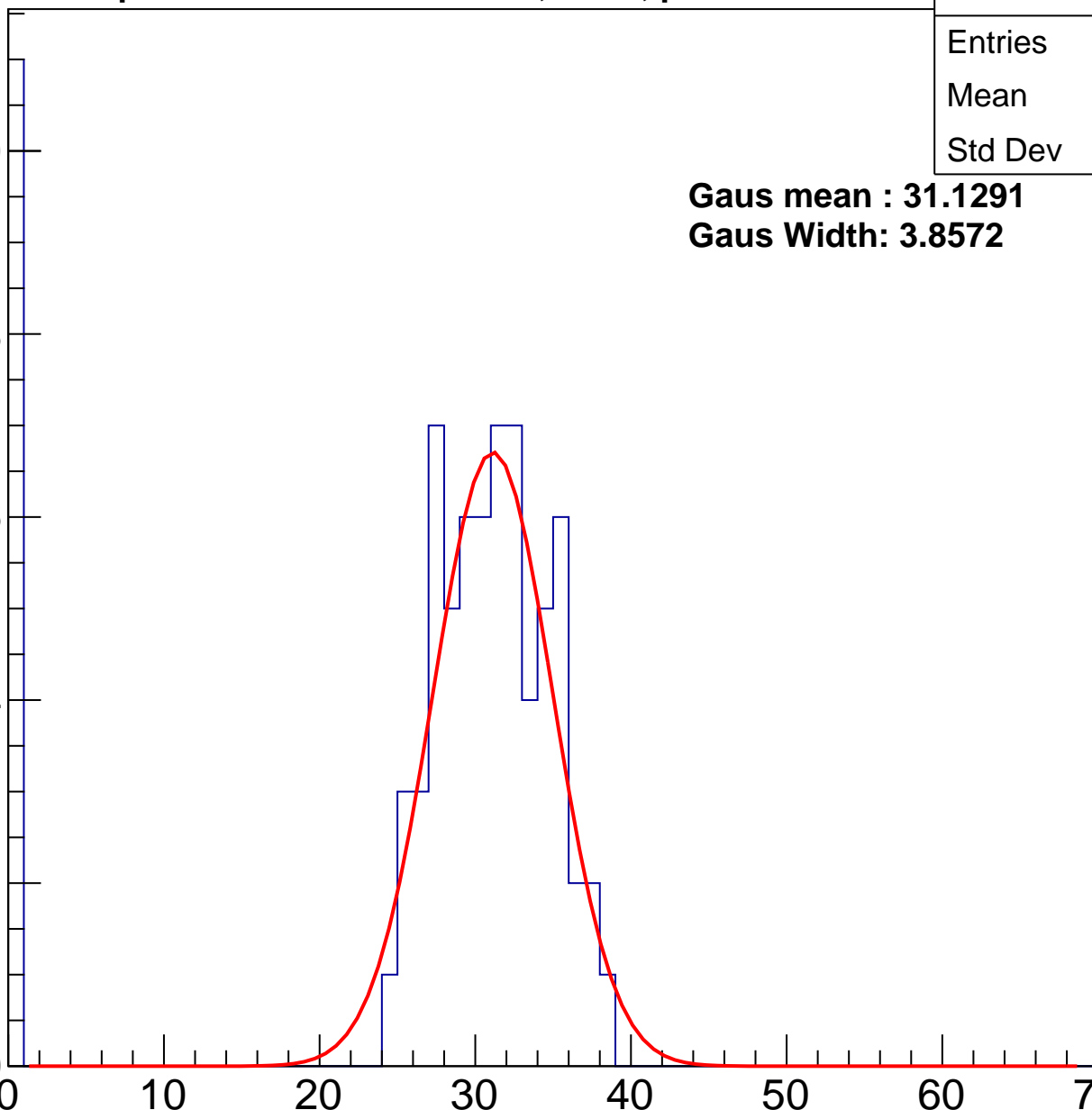
40

50

60

70

ampl



B1L103S, U13-ch107, adc1

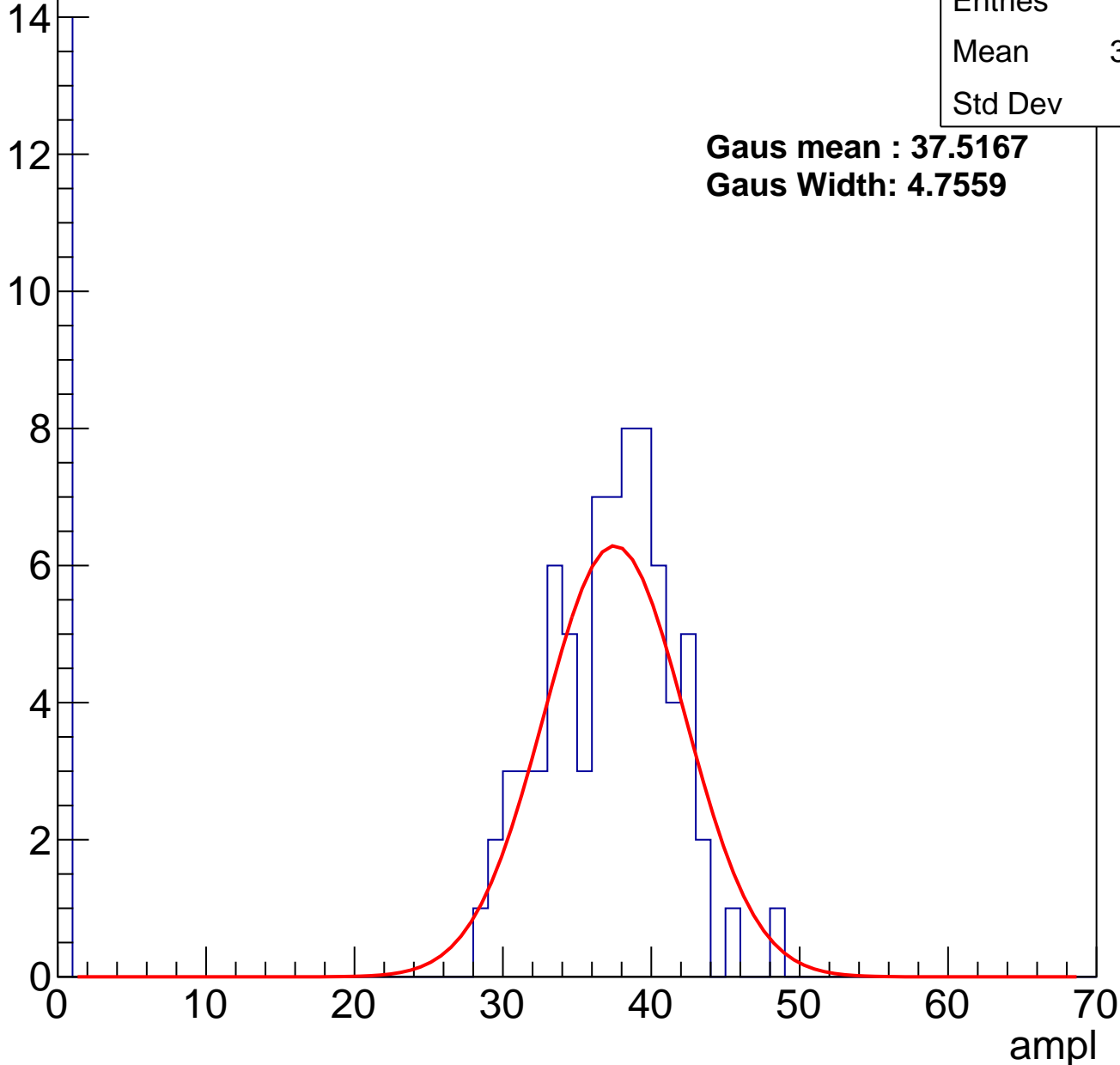
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	30.99
Std Dev	13.9

Gaus mean : 37.5167

Gaus Width: 4.7559

Entry



B1L103S, U13-ch107, adc2

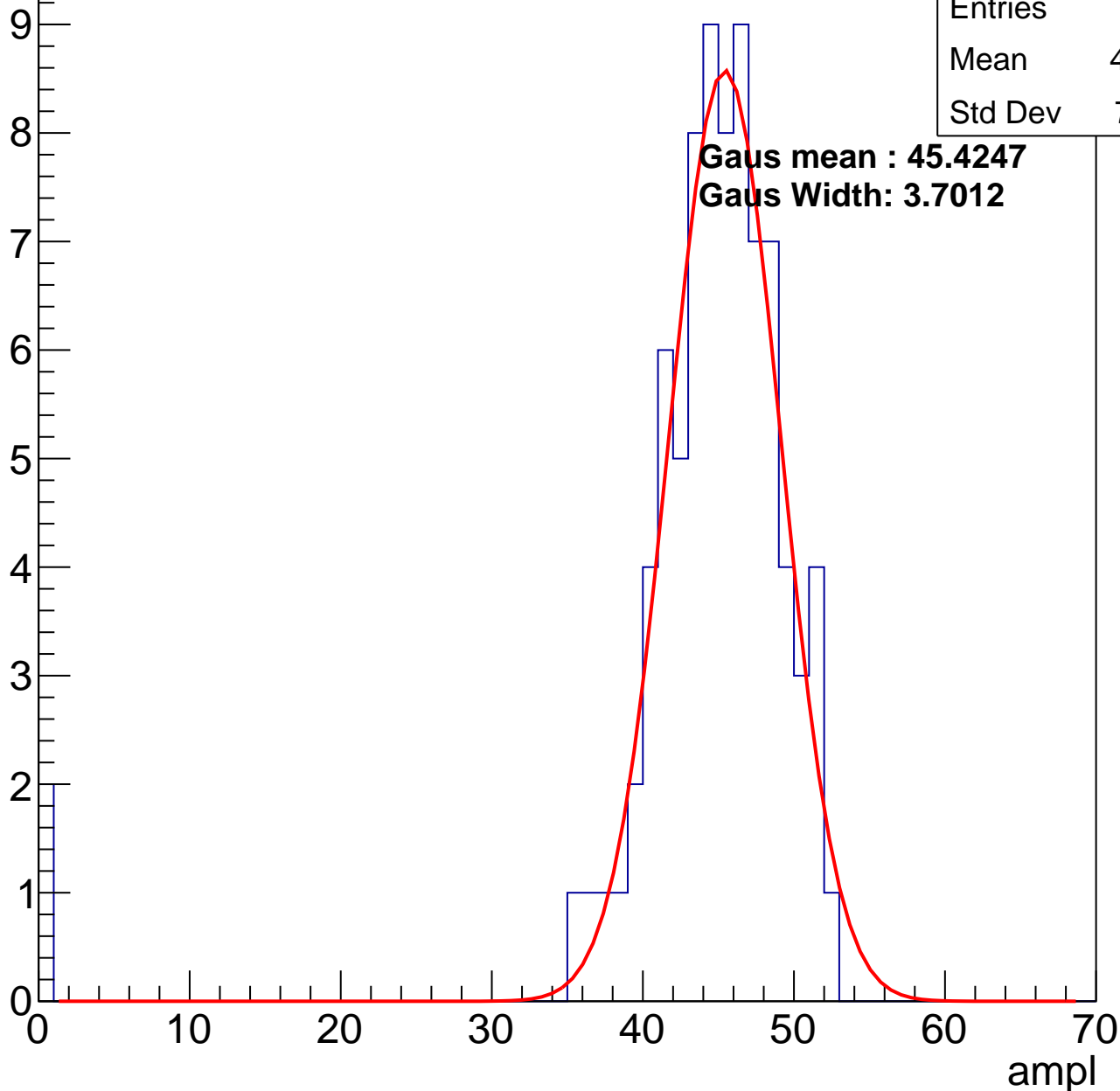
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	43.63
Std Dev	7.741

Gaus mean : 45.4247

Gaus Width: 3.7012

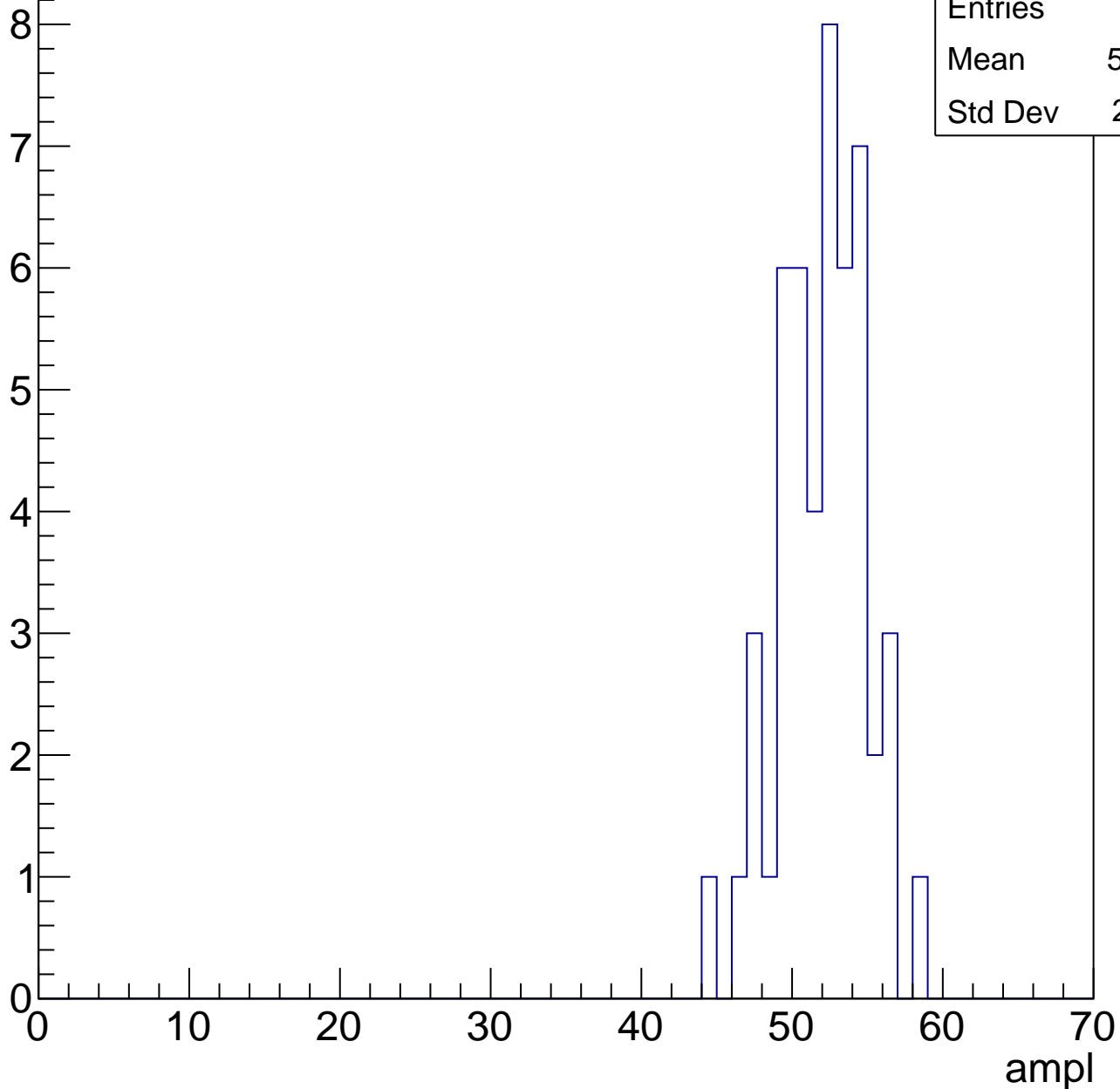


B1L103S, U13-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	51.53
Std Dev	2.851

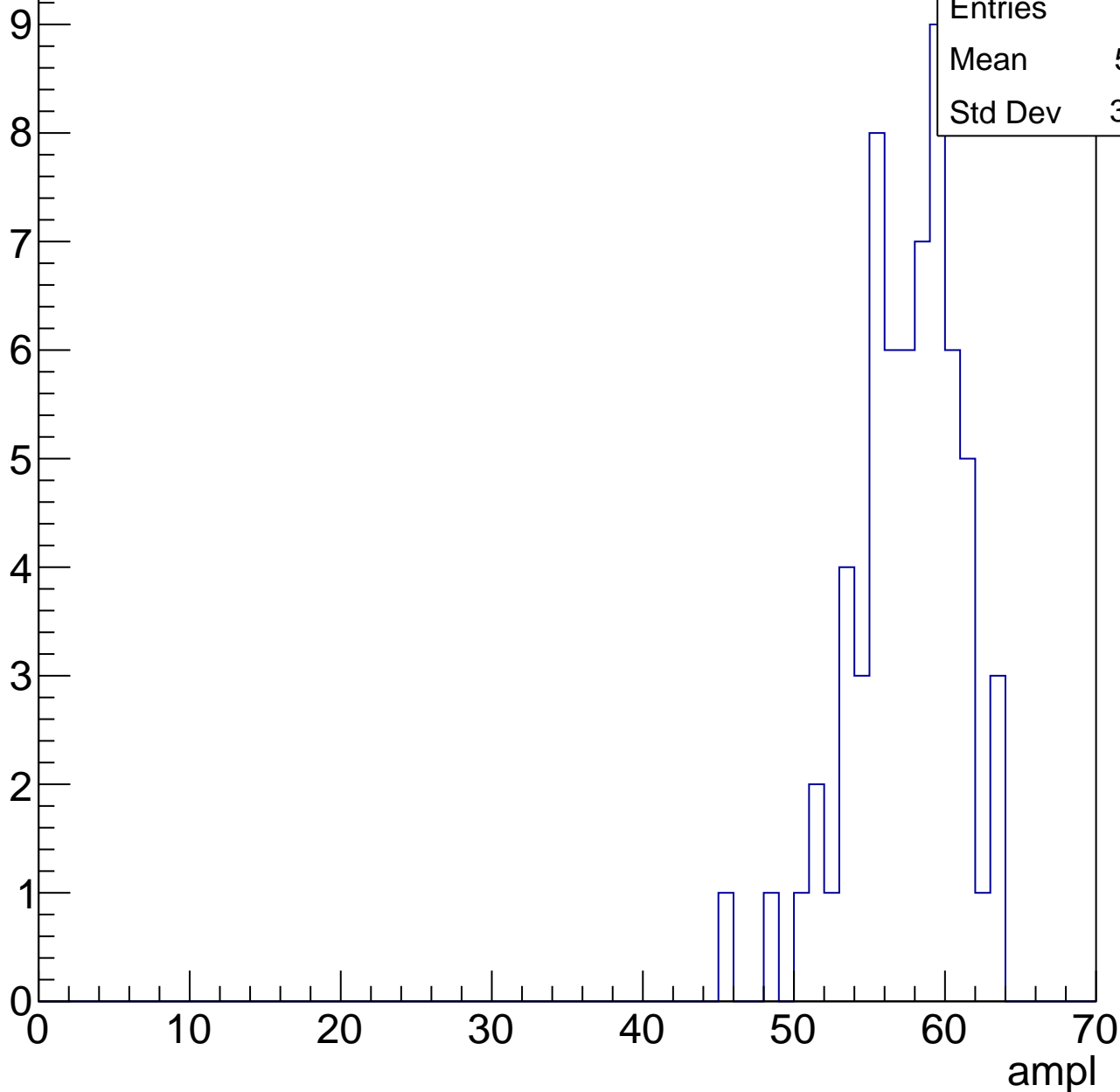


B1L103S, U13-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	56.91
Std Dev	3.556

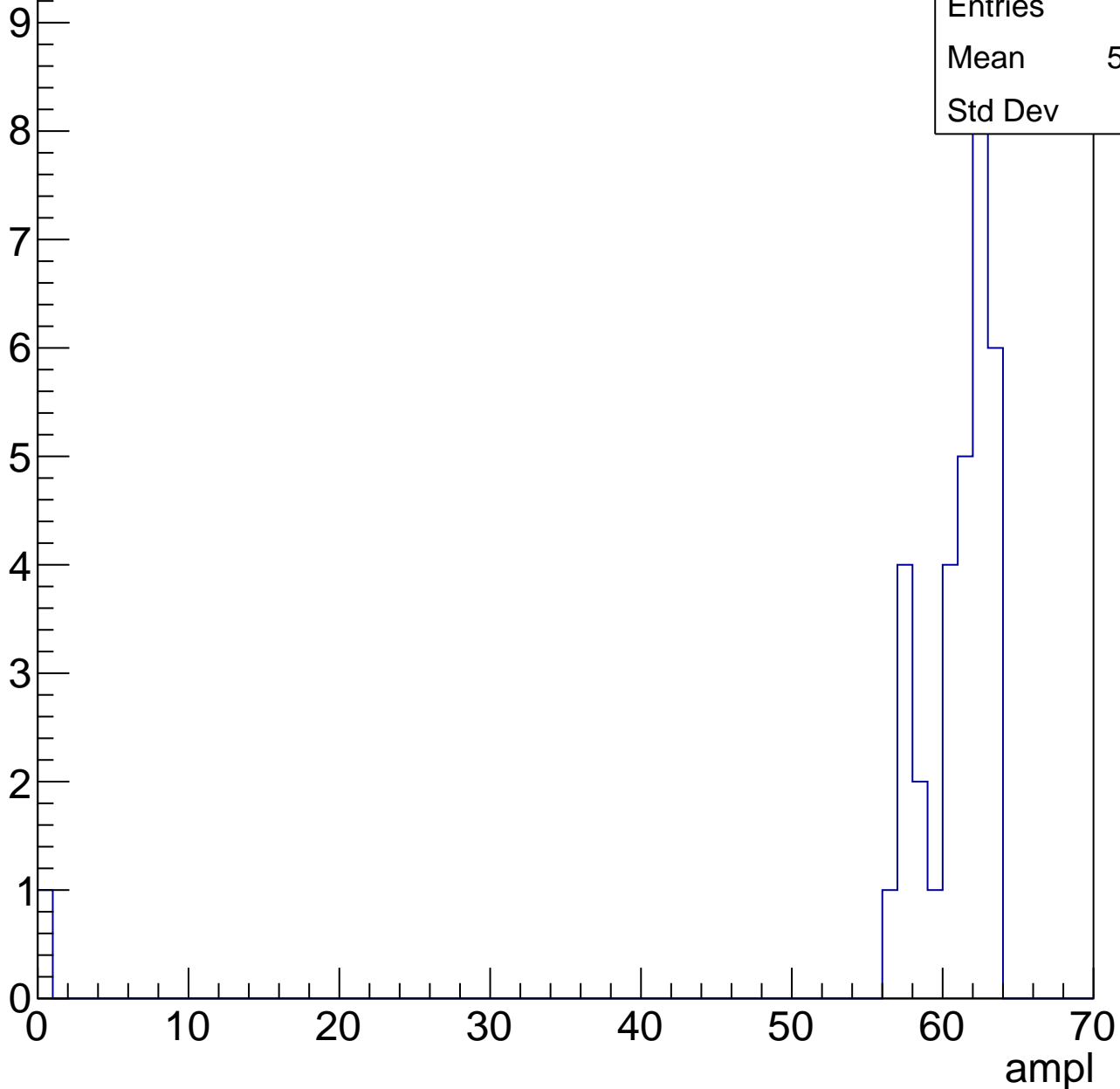


B1L103S, U13-ch107, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.79
Std Dev	10.6



B1L103S, U13-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch108, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	117
Mean	22.12
Std Dev	12.3

Gaus mean : 29.0278

Gaus Width: 3.6263

Entry

25

20

15

10

5

0

0

10

20

30

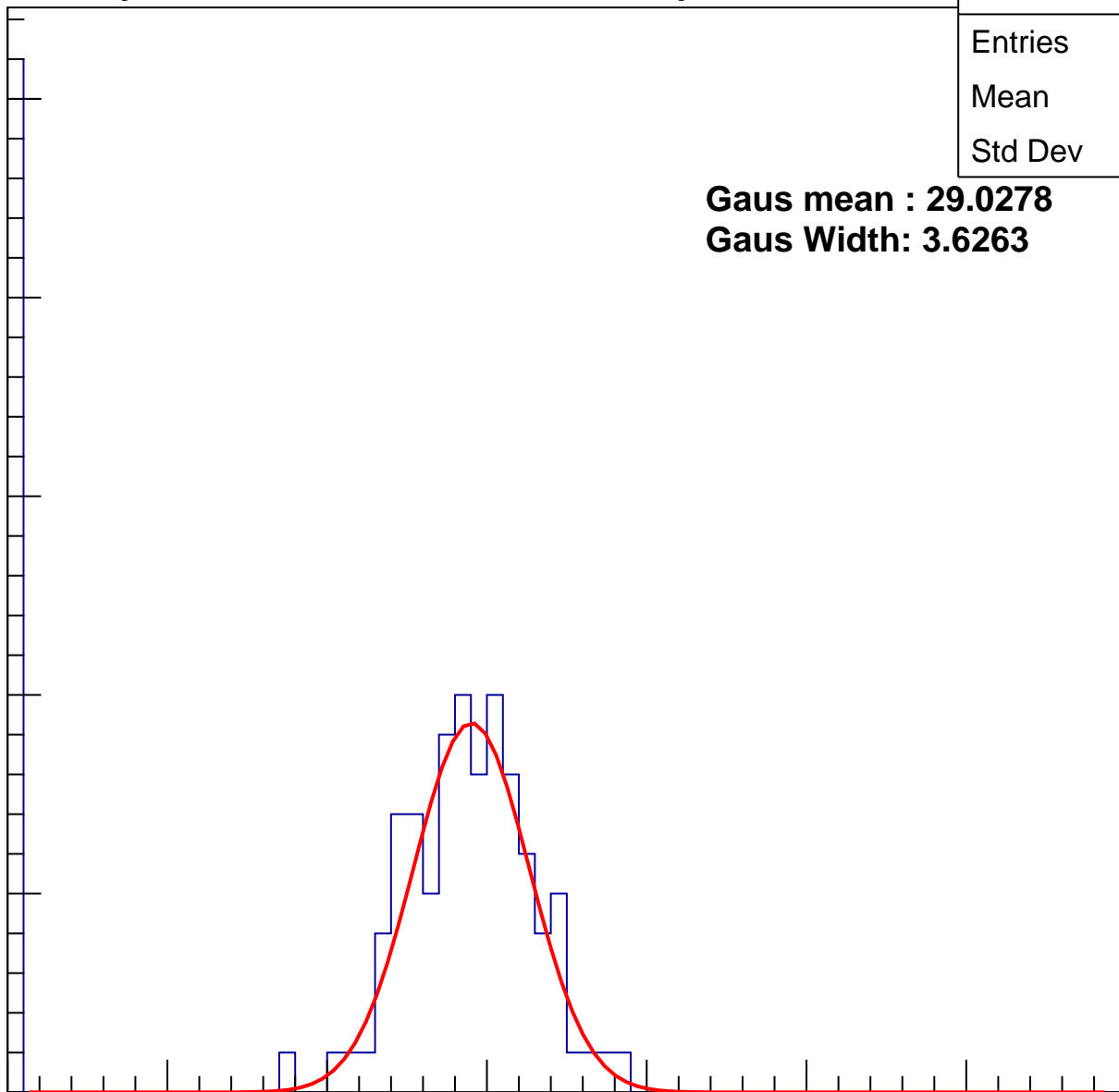
40

50

60

70

ampl



B1L103S, U13-ch108, adc1

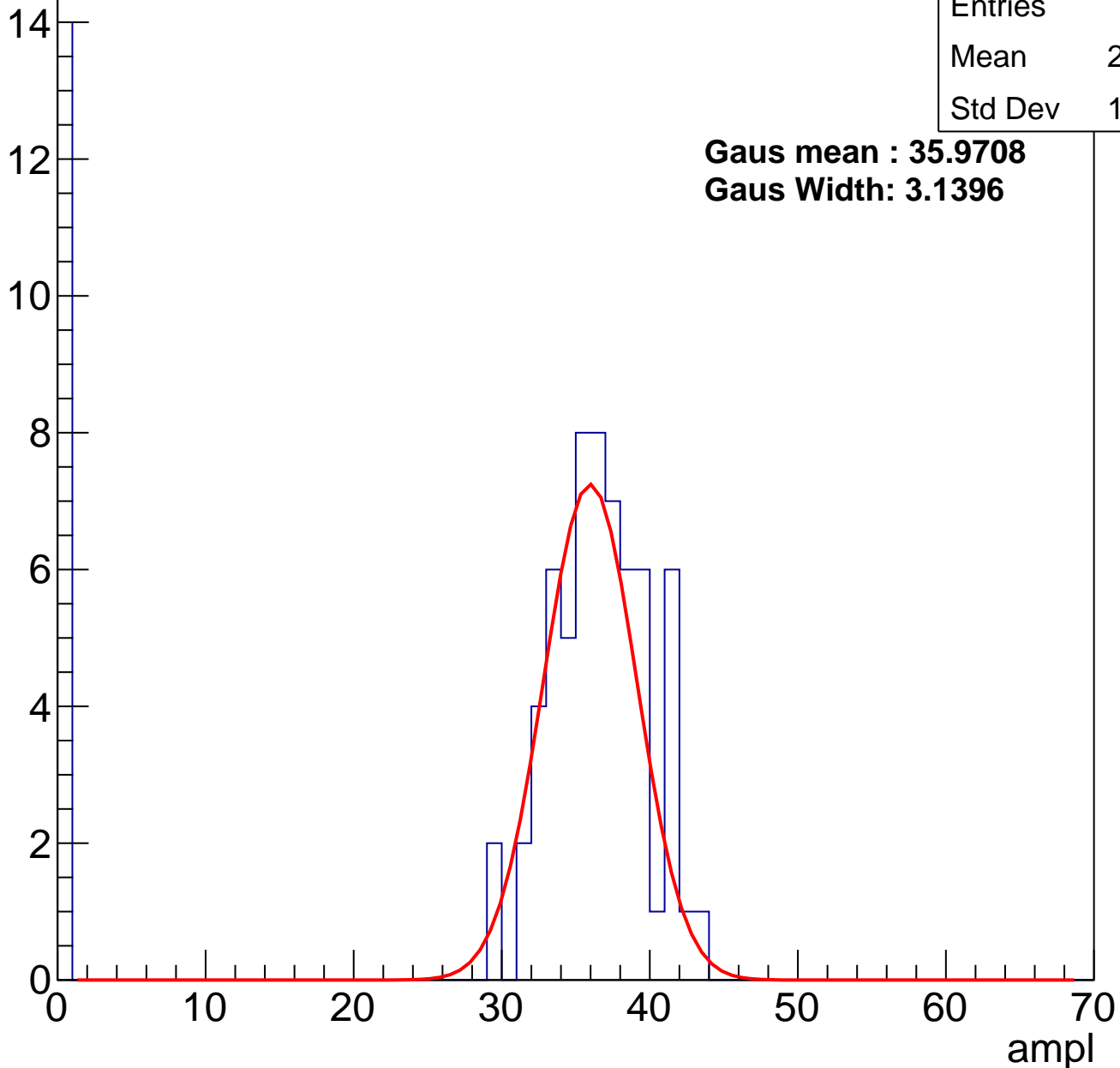
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	29.56
Std Dev	14.22

Gaus mean : 35.9708

Gaus Width: 3.1396

Entry



B1L103S, U13-ch108, adc2

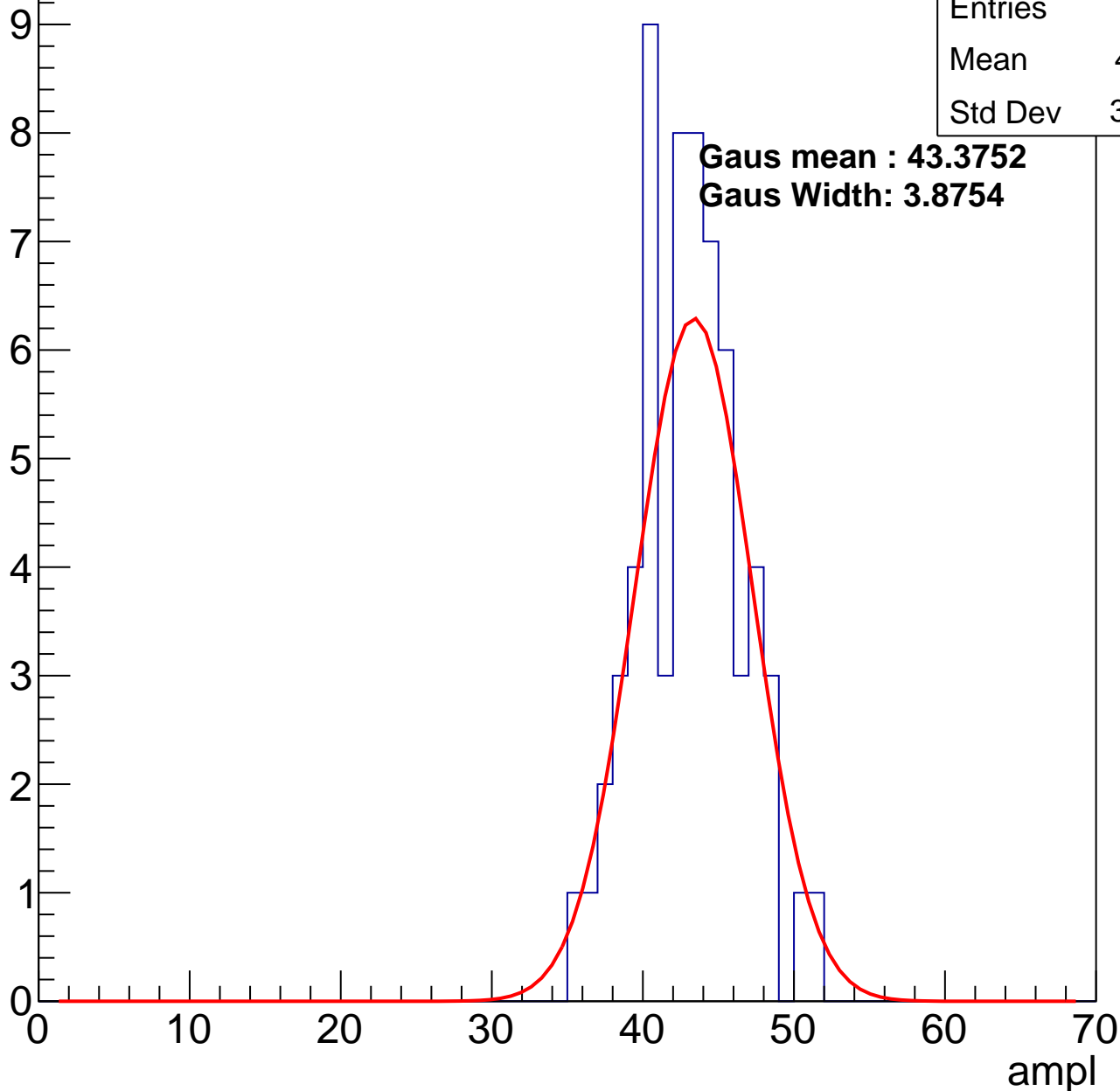
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	42.61
Std Dev	3.366

Gaus mean : 43.3752

Gaus Width: 3.8754

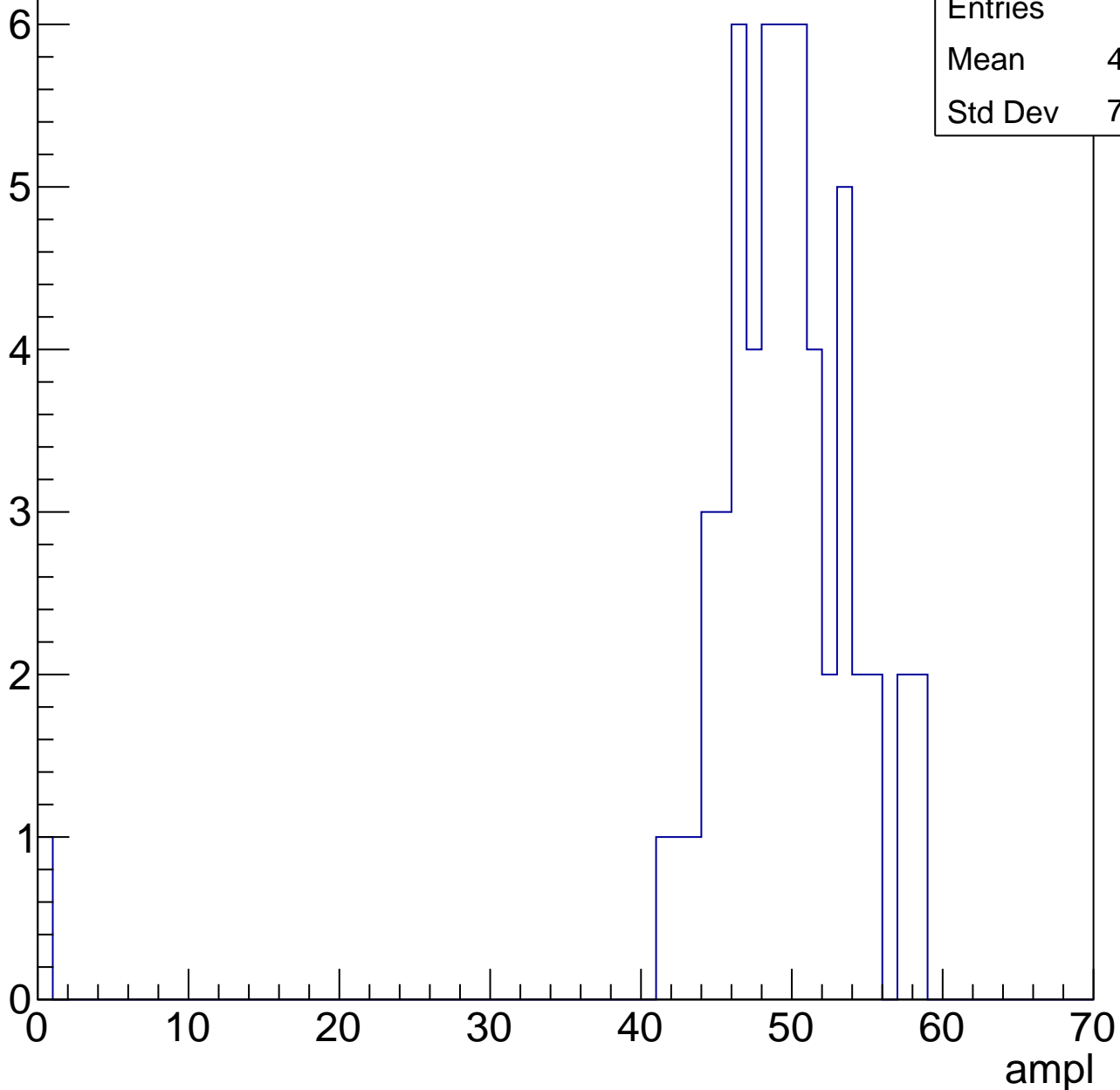


B1L103S, U13-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	48.42
Std Dev	7.565

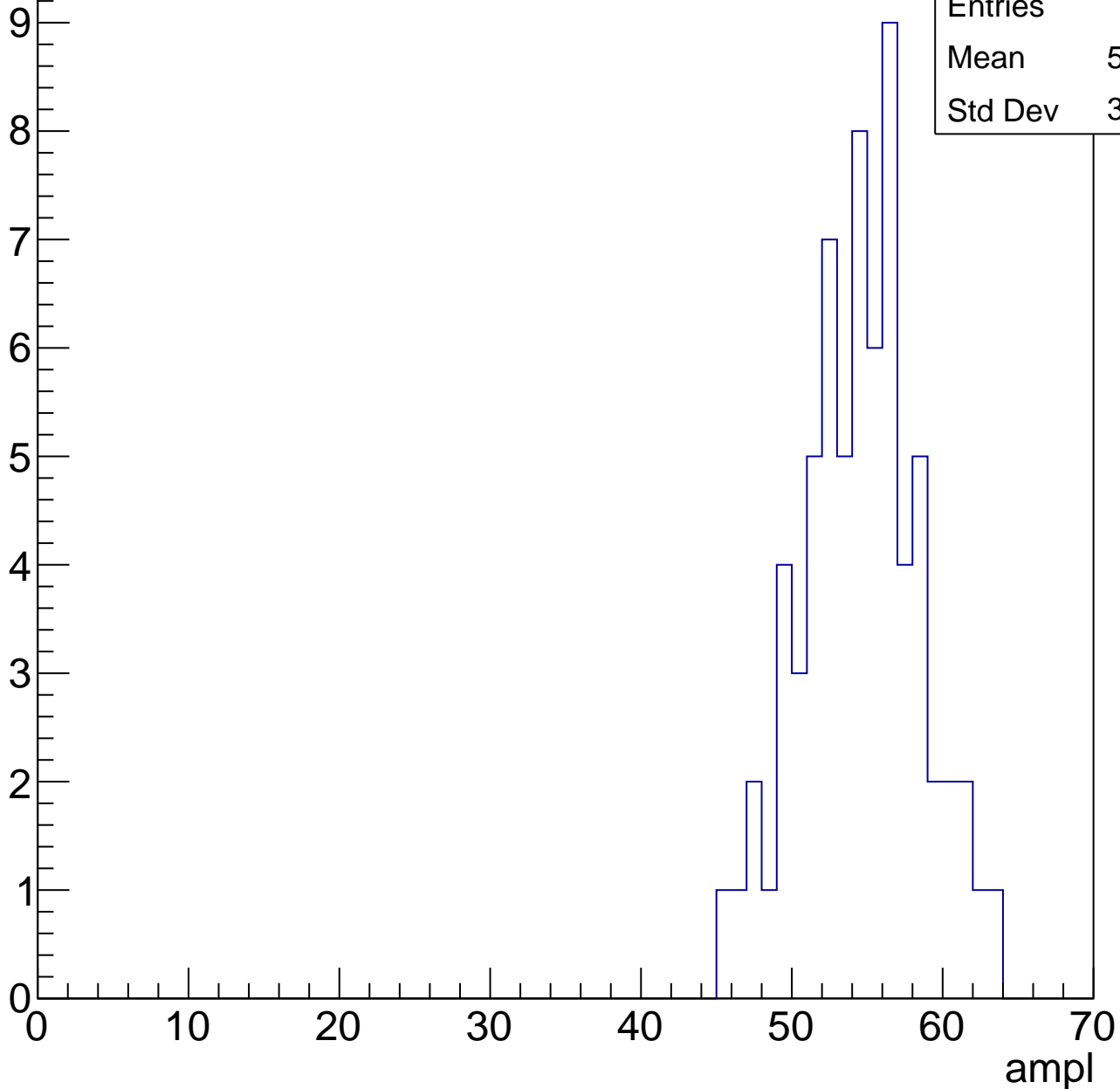


B1L103S, U13-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	54.09
Std Dev	3.859

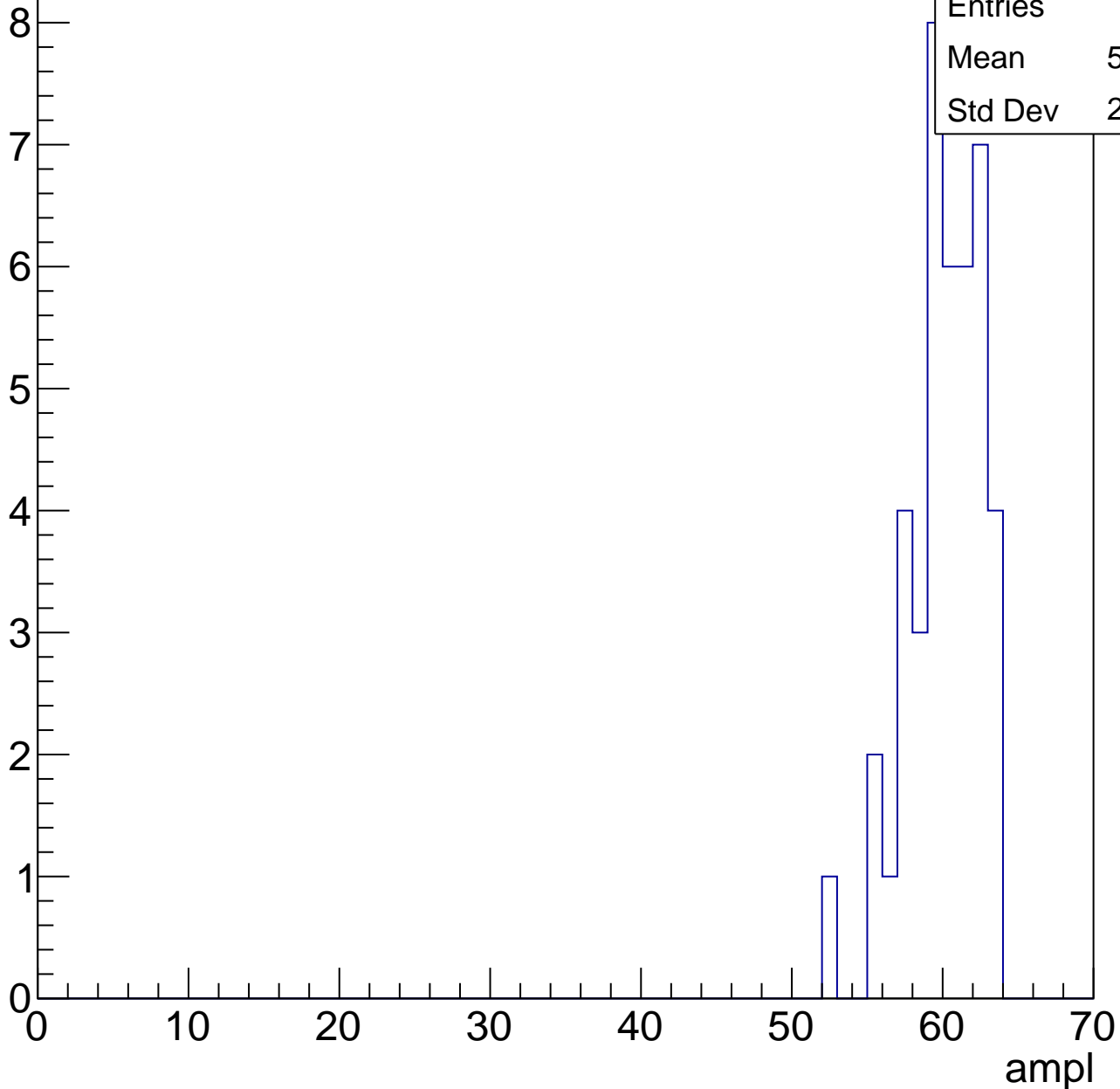


B1L103S, U13-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	59.62
Std Dev	2.439

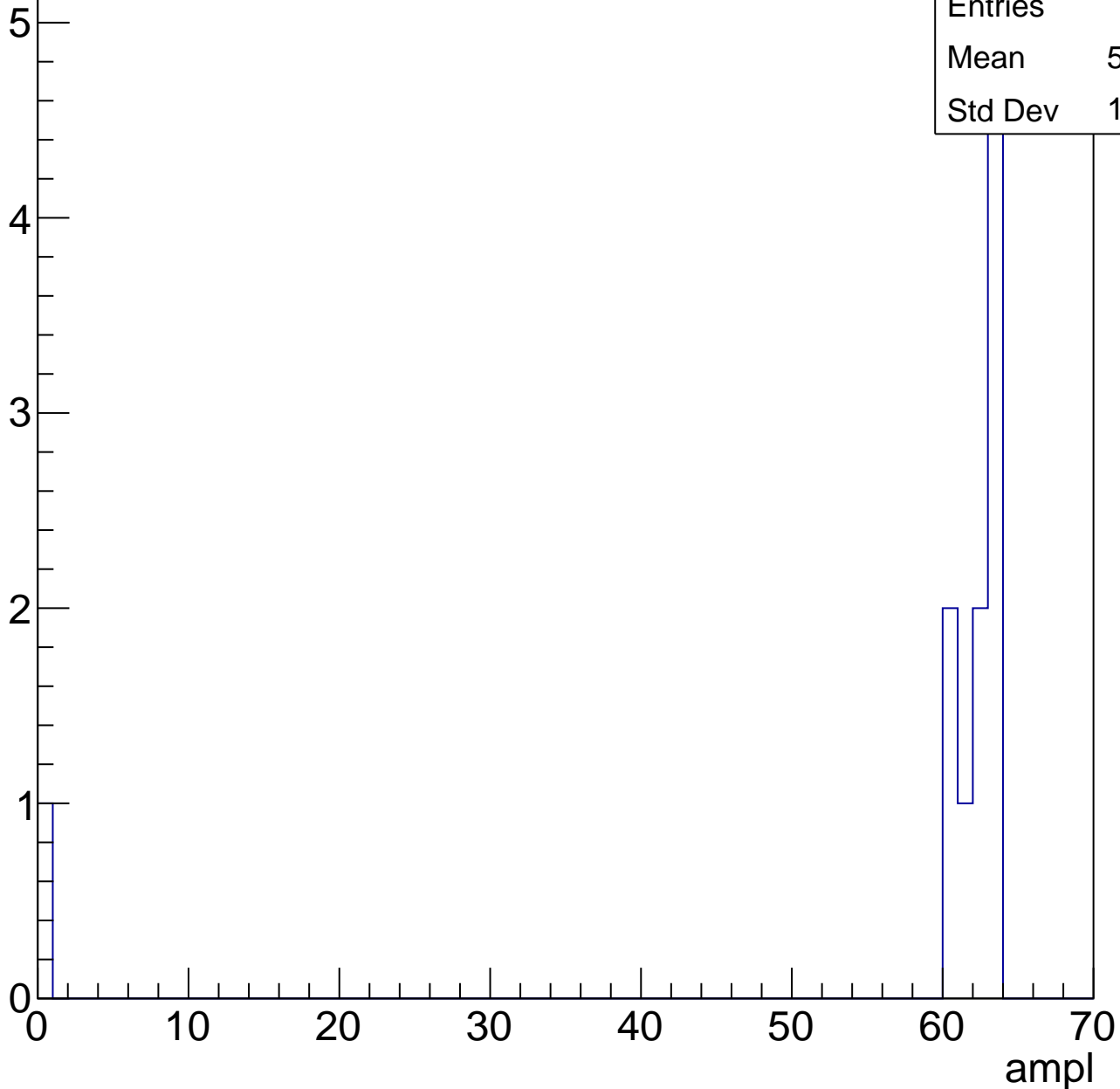


B1L103S, U13-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

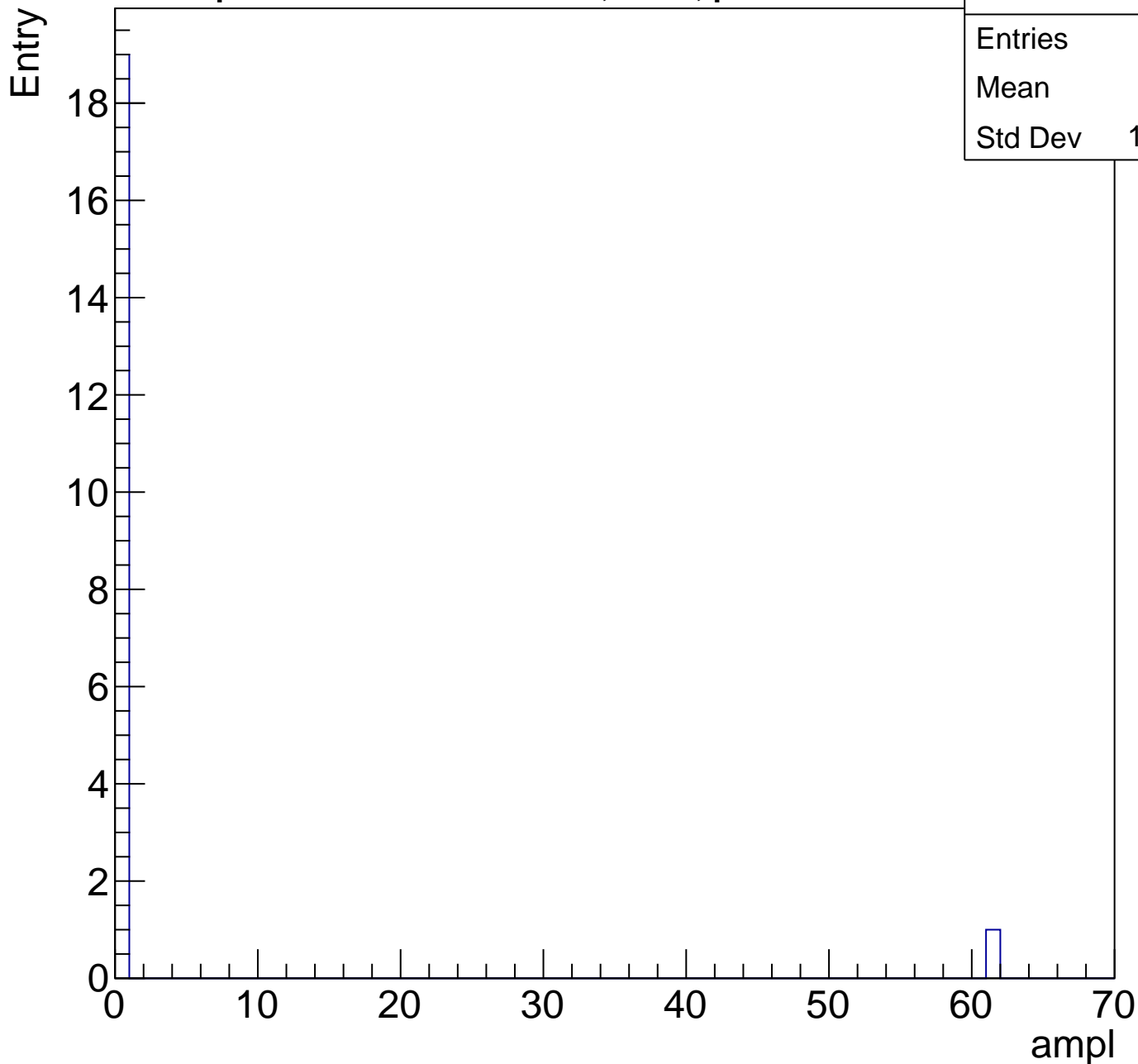
Entries	11
Mean	56.36
Std Dev	17.86



B1L103S, U13-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29



B1L103S, U13-ch109, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	116
Mean	25.98
Std Dev	9.68

Gaus mean : 29.5323

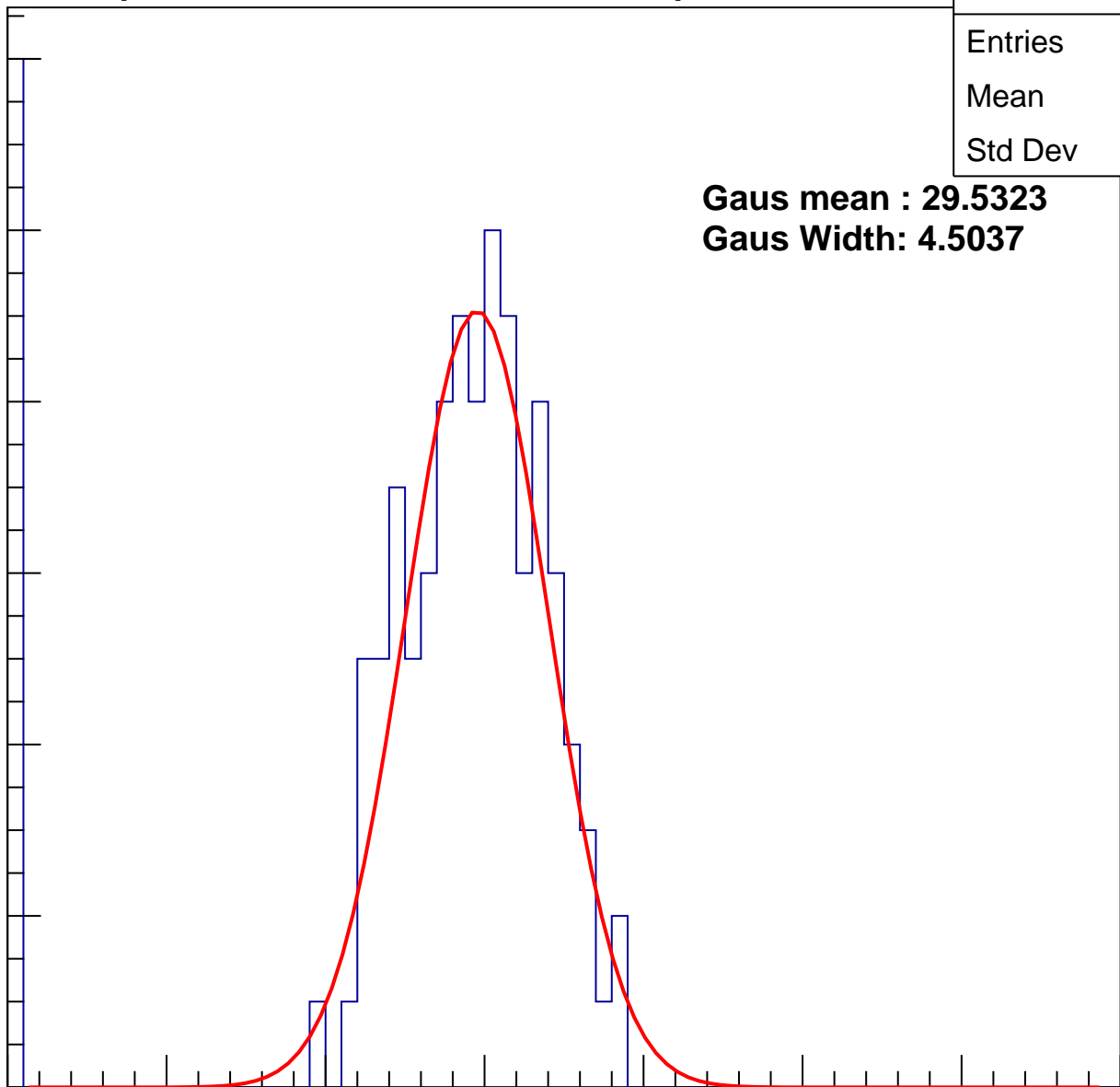
Gaus Width: 4.5037

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch109, adc1

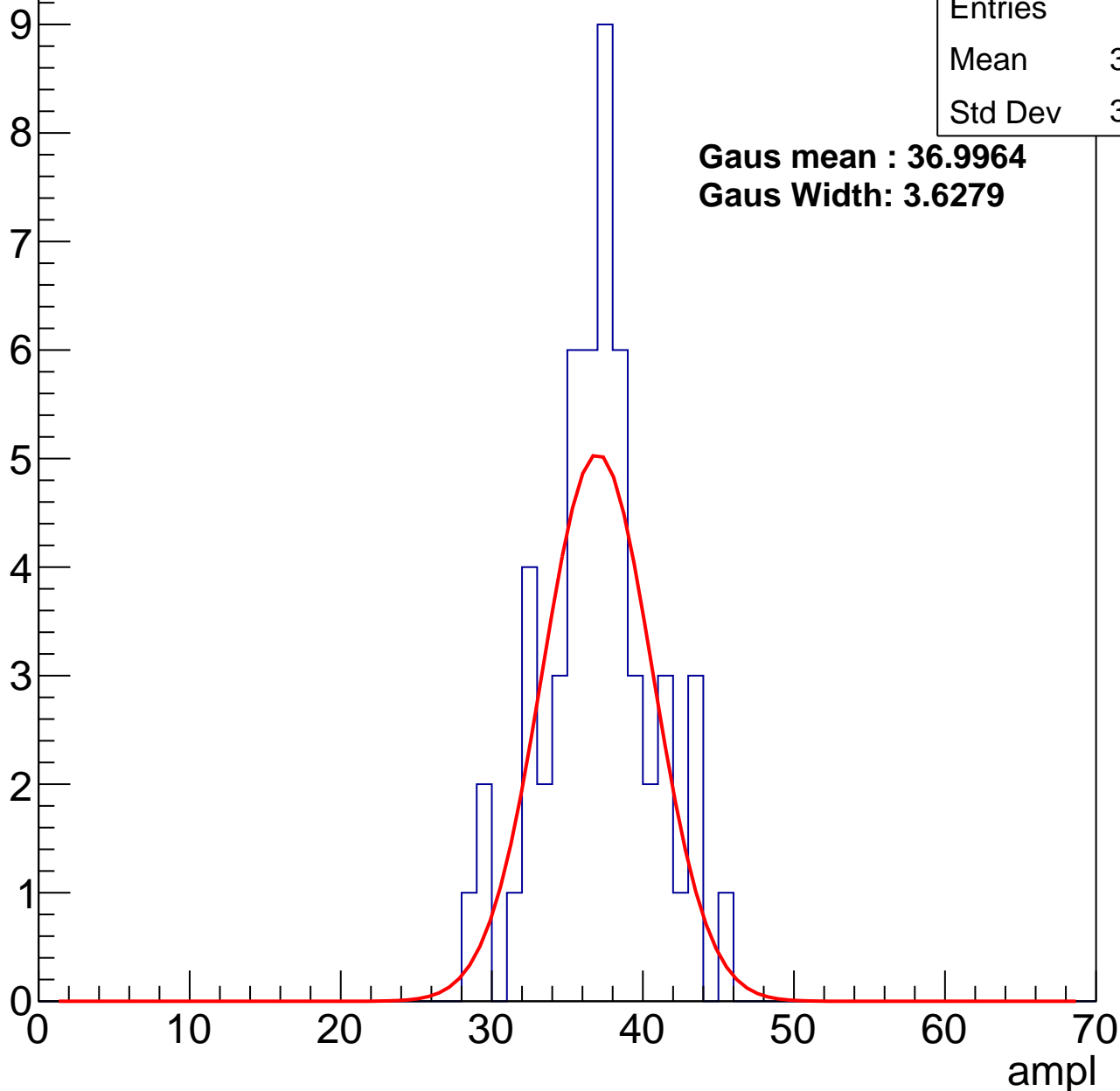
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	36.53
Std Dev	3.643

Gaus mean : 36.9964

Gaus Width: 3.6279



B1L103S, U13-ch109, adc2

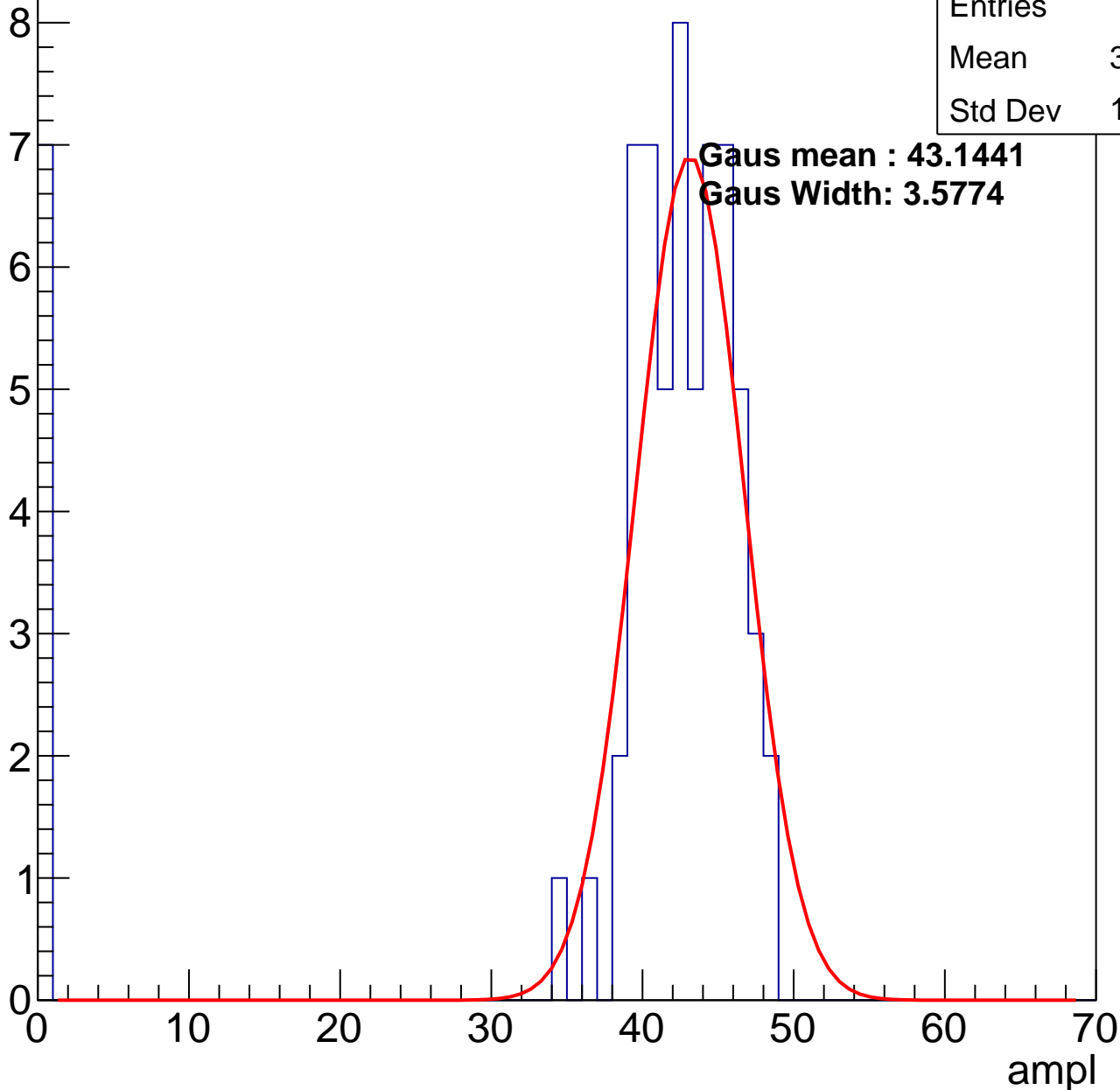
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.99
Std Dev	13.28

Gaus mean : 43.1441

Gaus Width: 3.5774

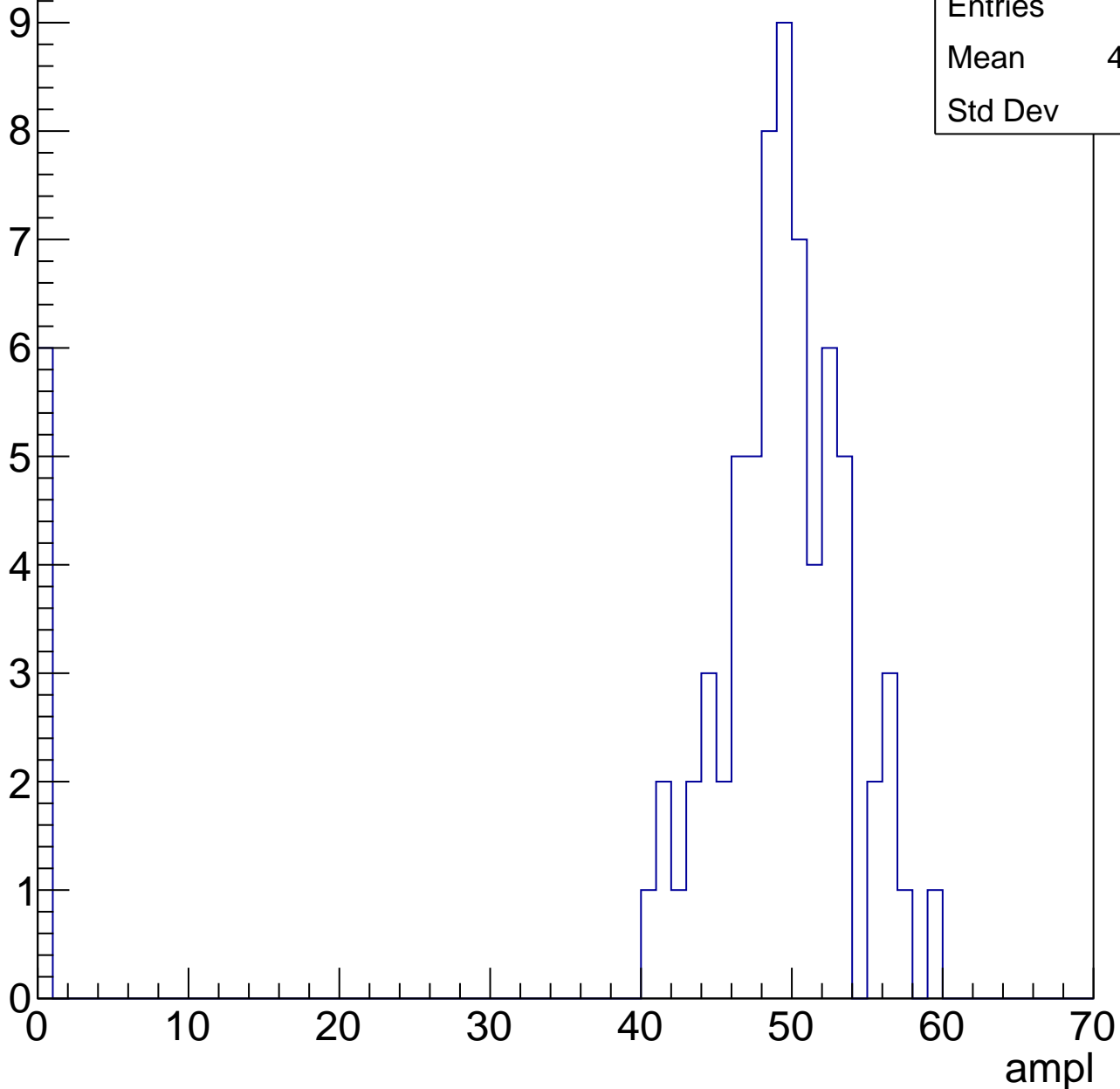


B1L103S, U13-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	45.03
Std Dev	14

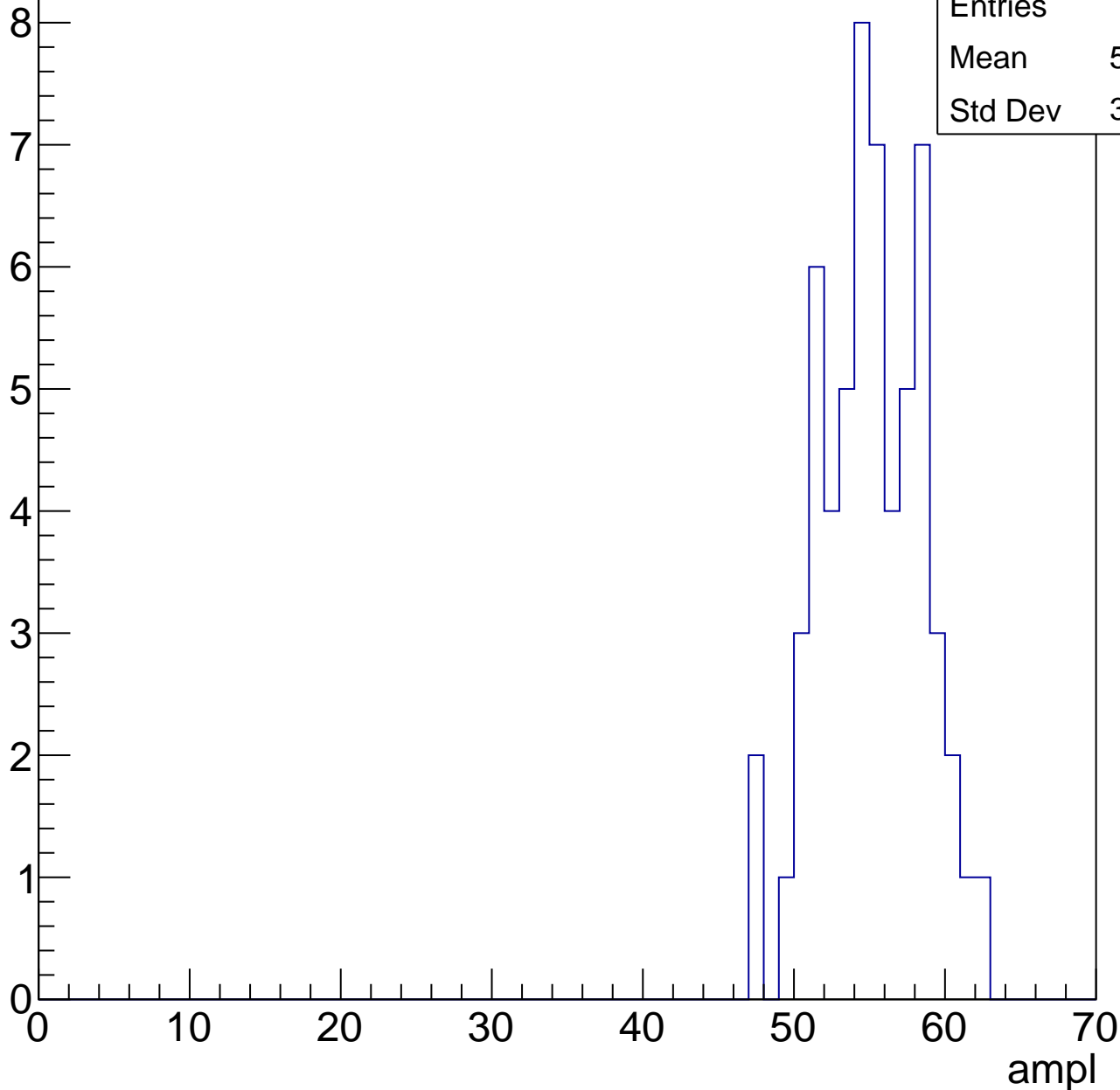


B1L103S, U13-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.64
Std Dev	3.348

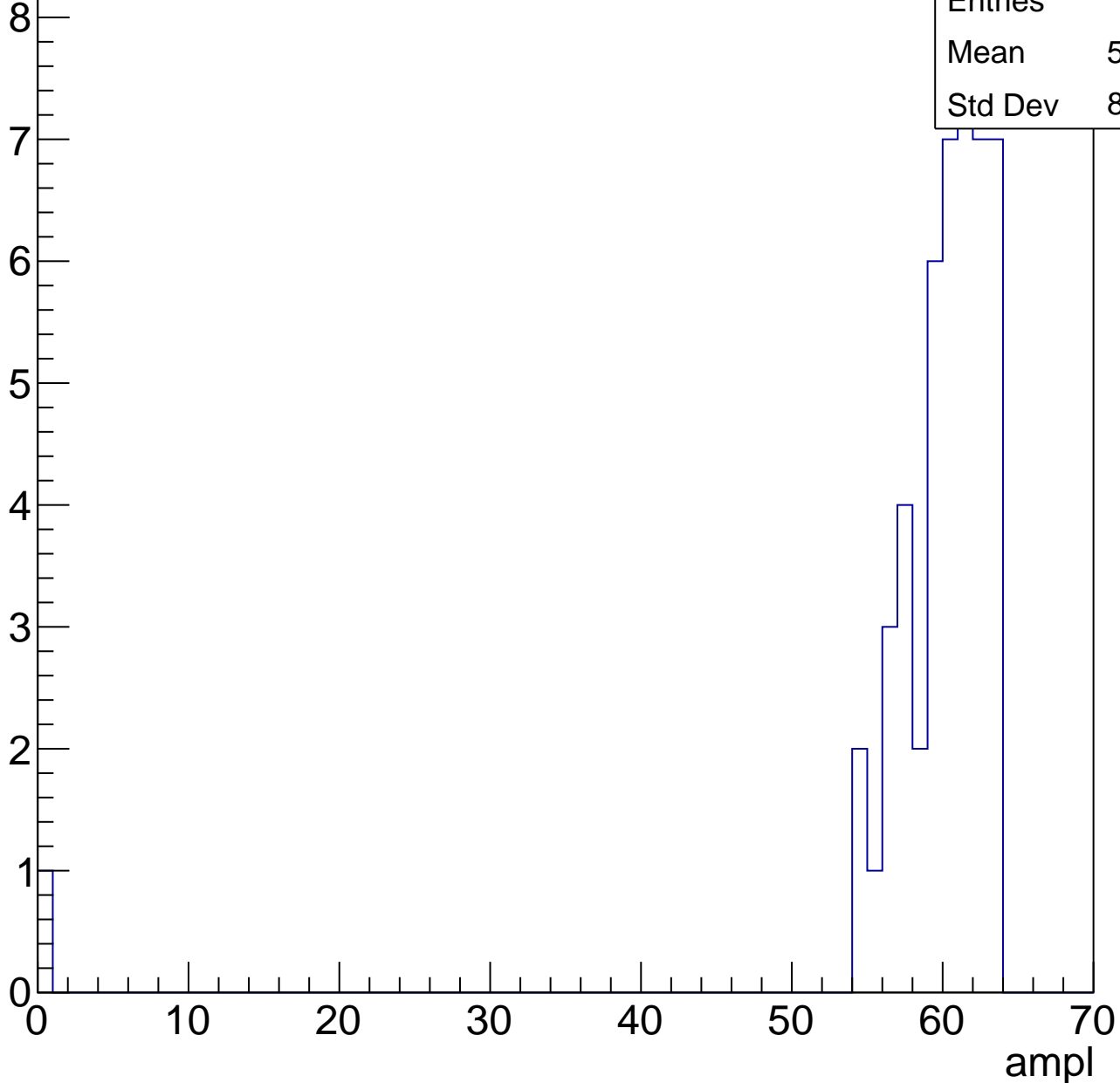


B1L103S, U13-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

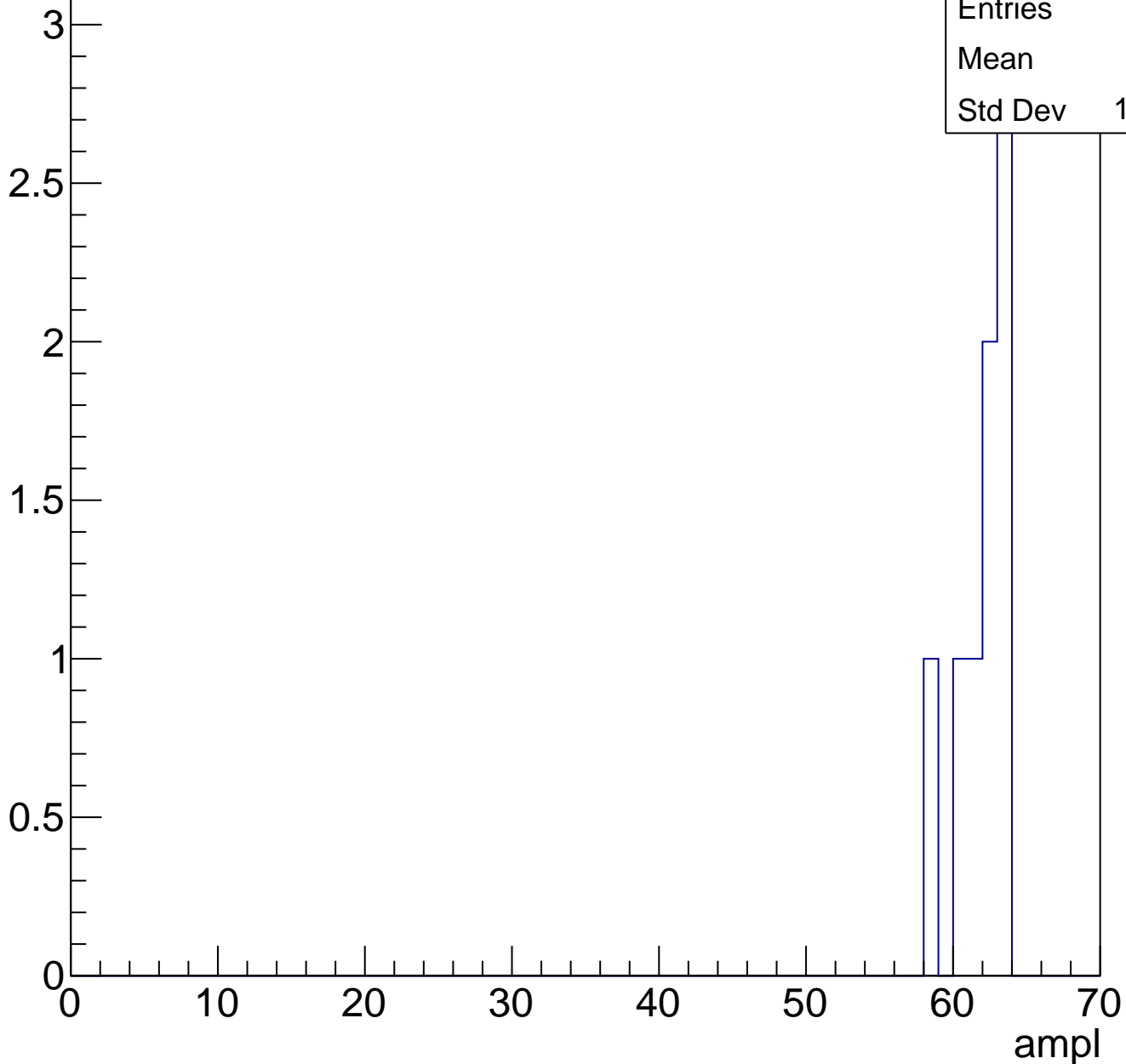
Entries	48
Mean	58.58
Std Dev	8.895



B1L103S, U13-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

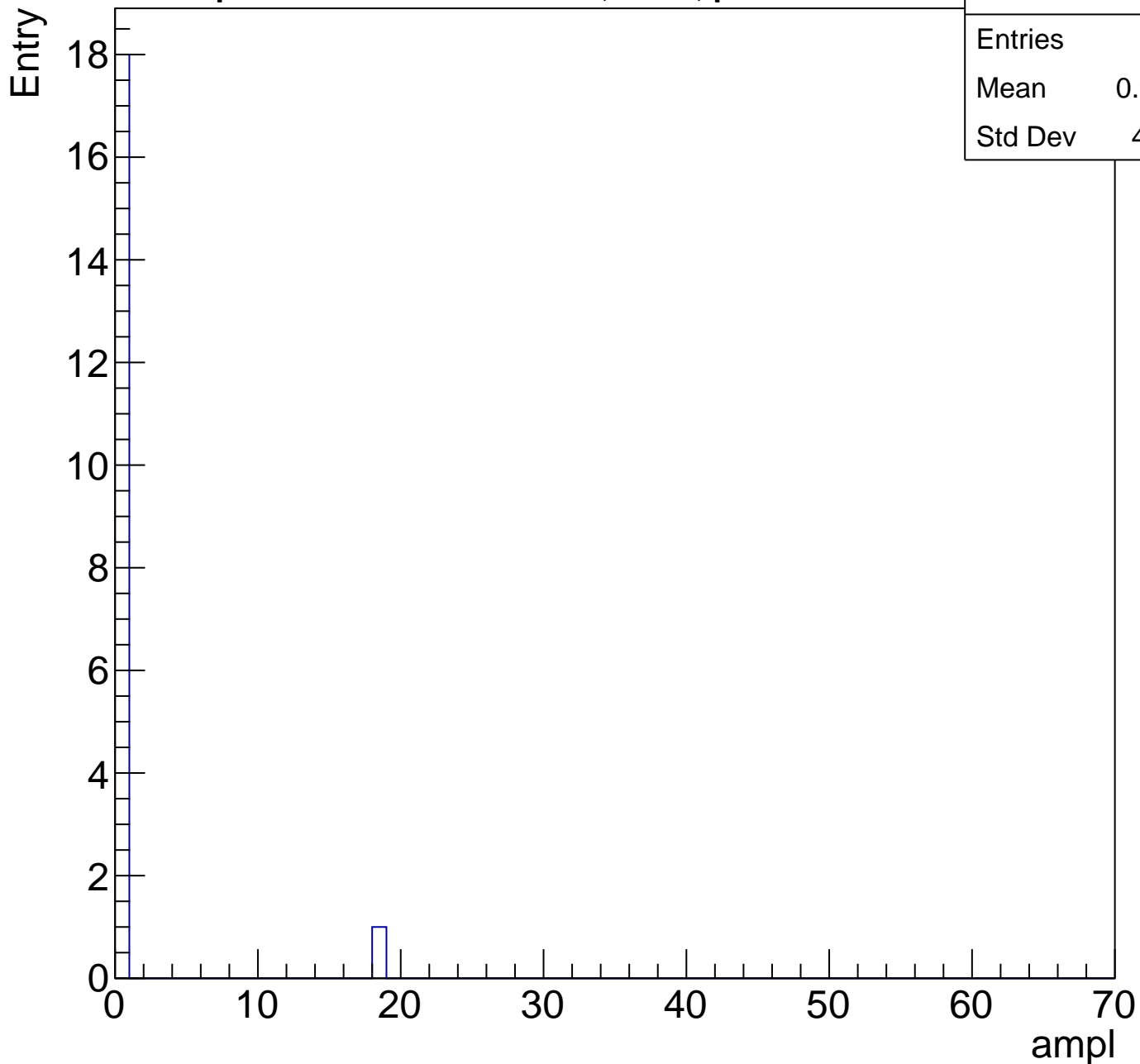
Entry



B1L103S, U13-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0.9474
Std Dev	4.019



B1L103S, U13-ch110, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	25.08
Std Dev	12

Gaus mean : 30.7298

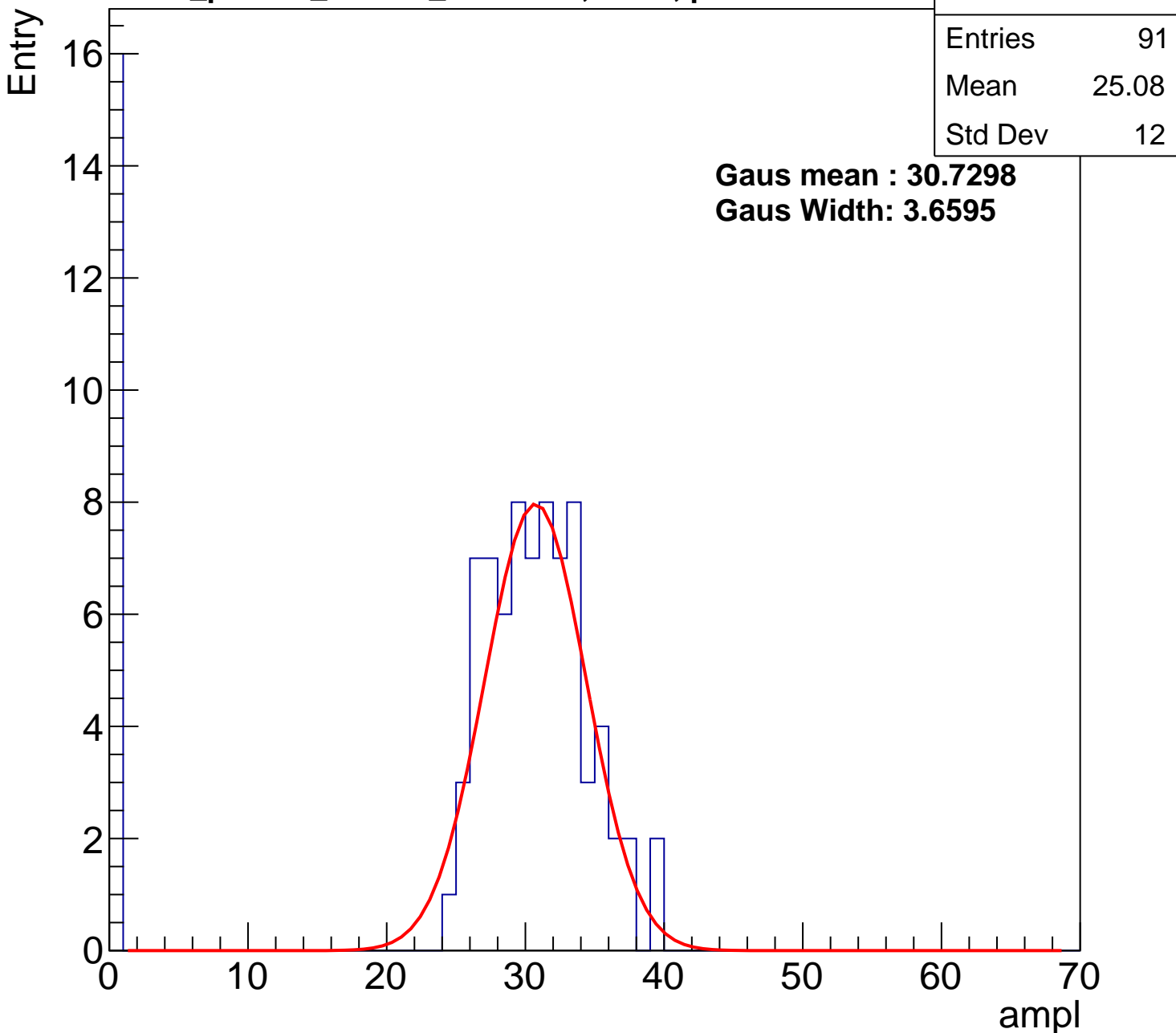
Gaus Width: 3.6595

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch110, adc1

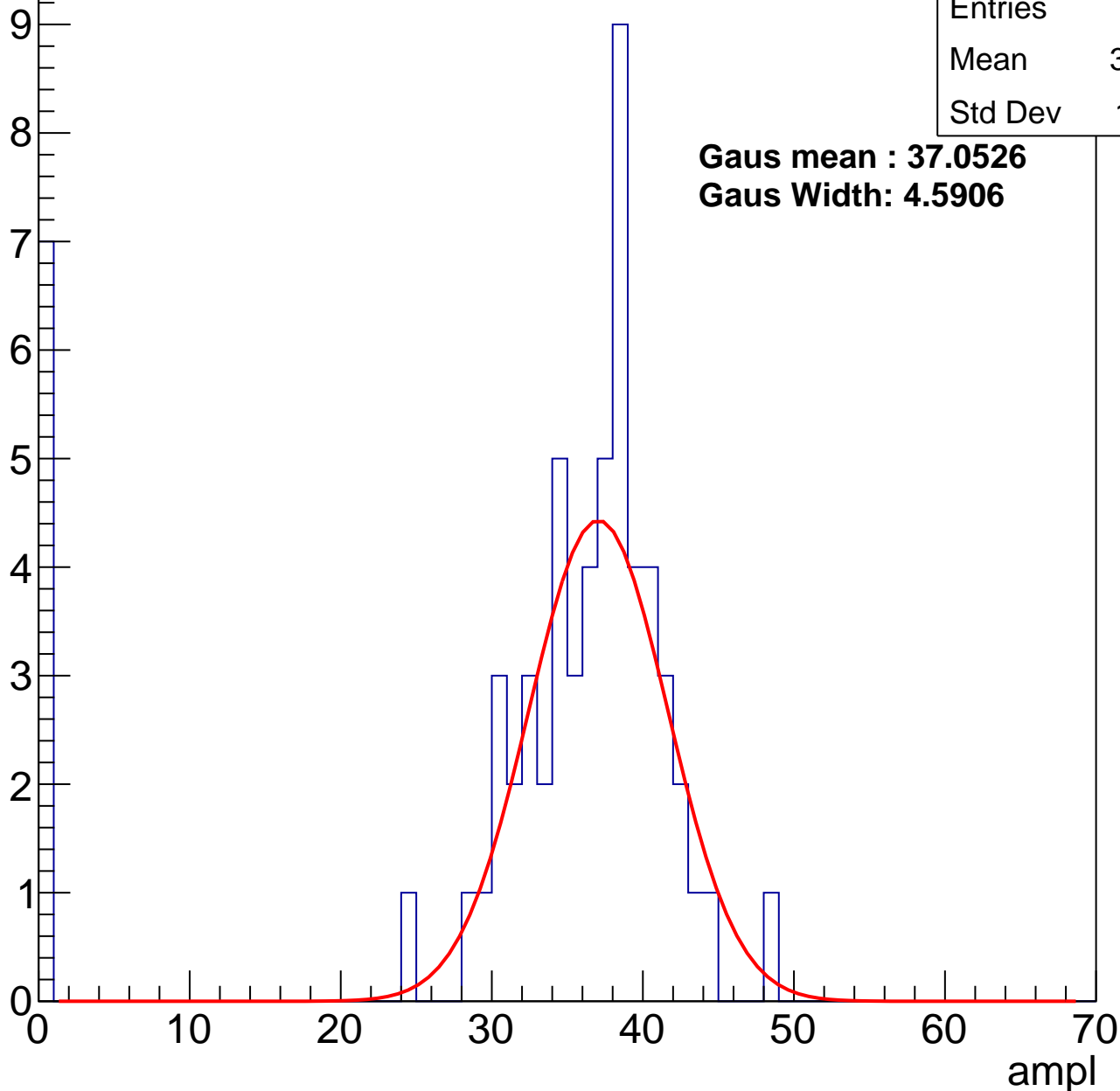
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.24
Std Dev	12.21

Gaus mean : 37.0526

Gaus Width: 4.5906

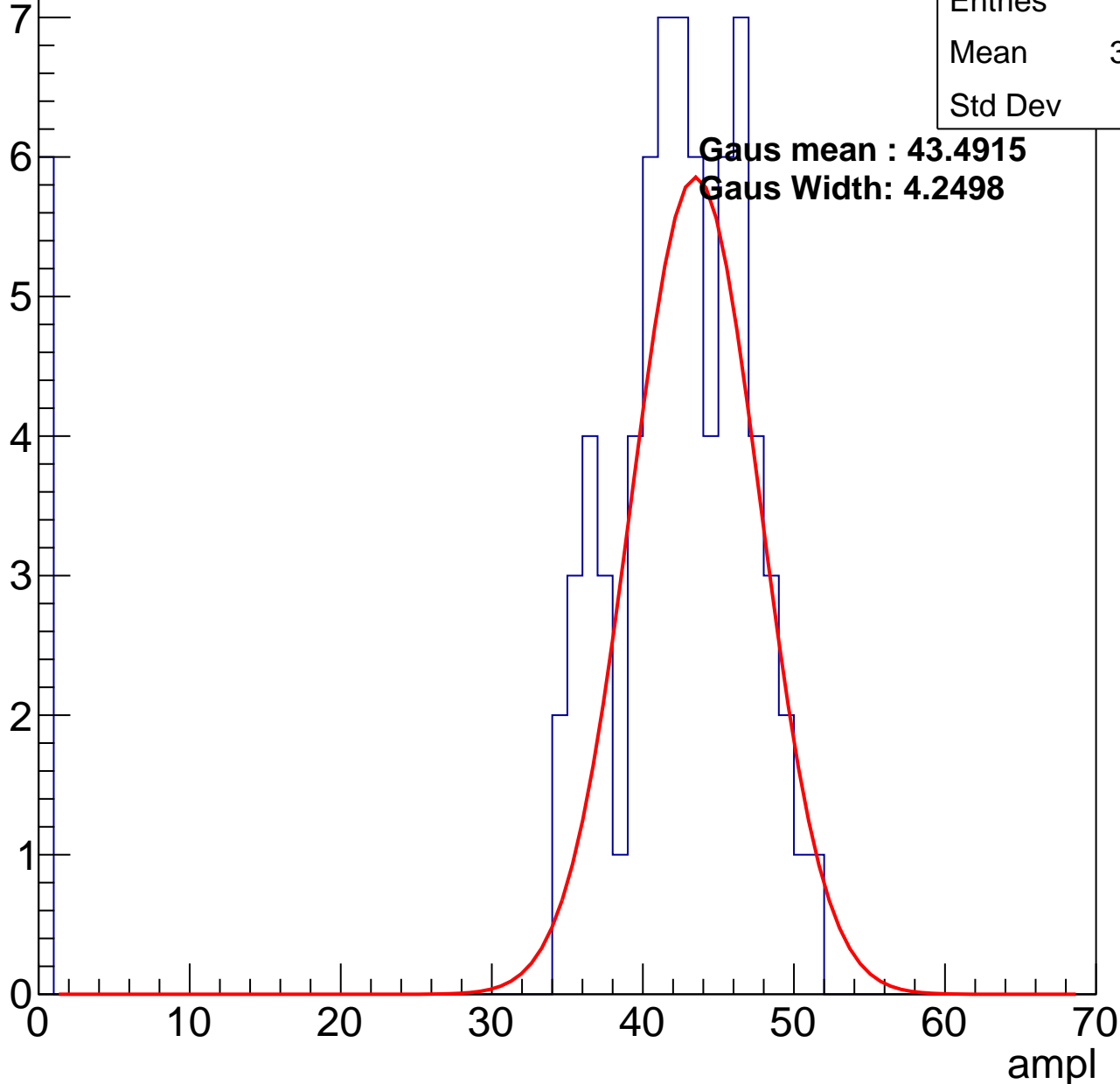


B1L103S, U13-ch110, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	38.96
Std Dev	12

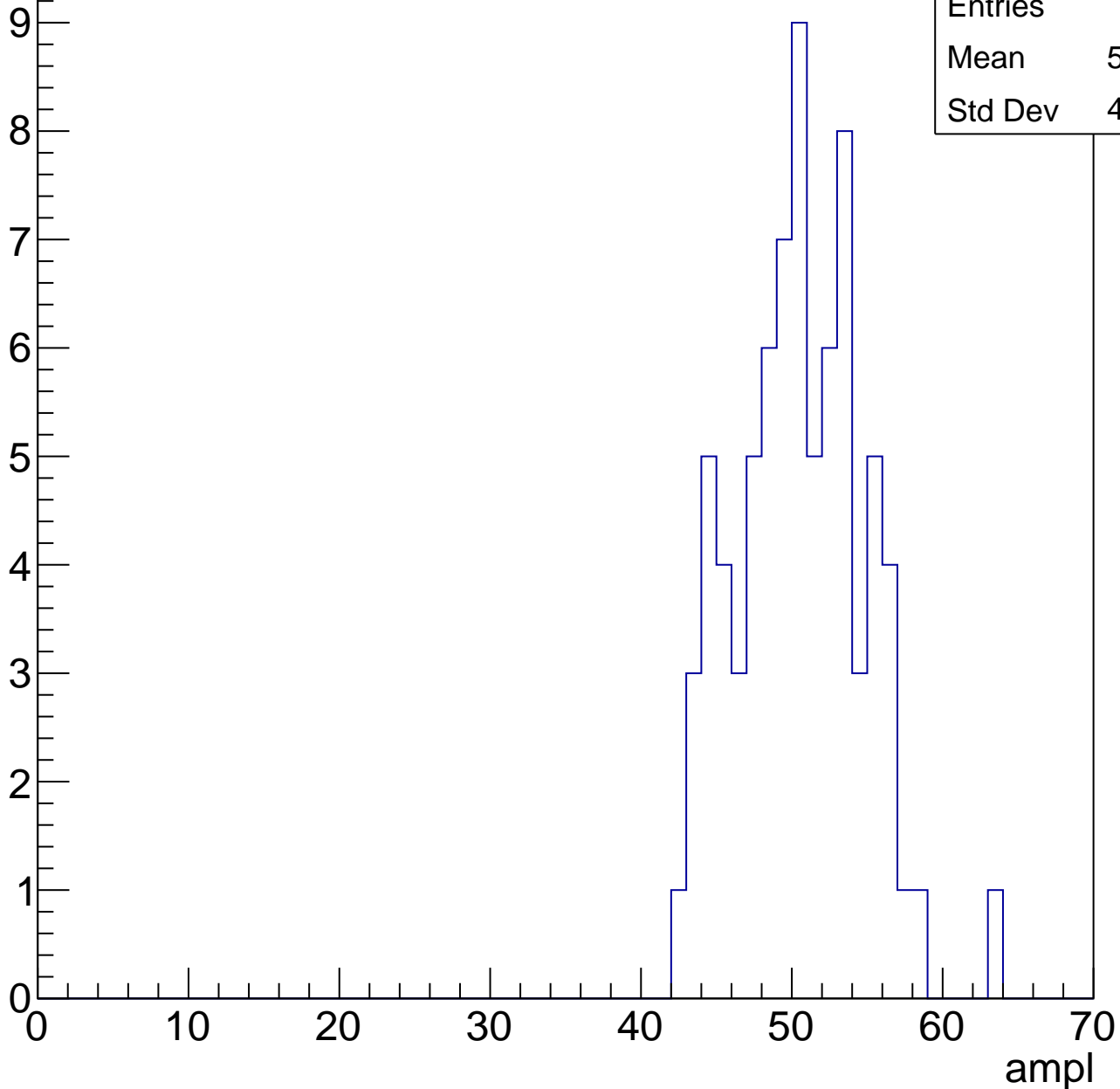


B1L103S, U13-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	50.06
Std Dev	4.132

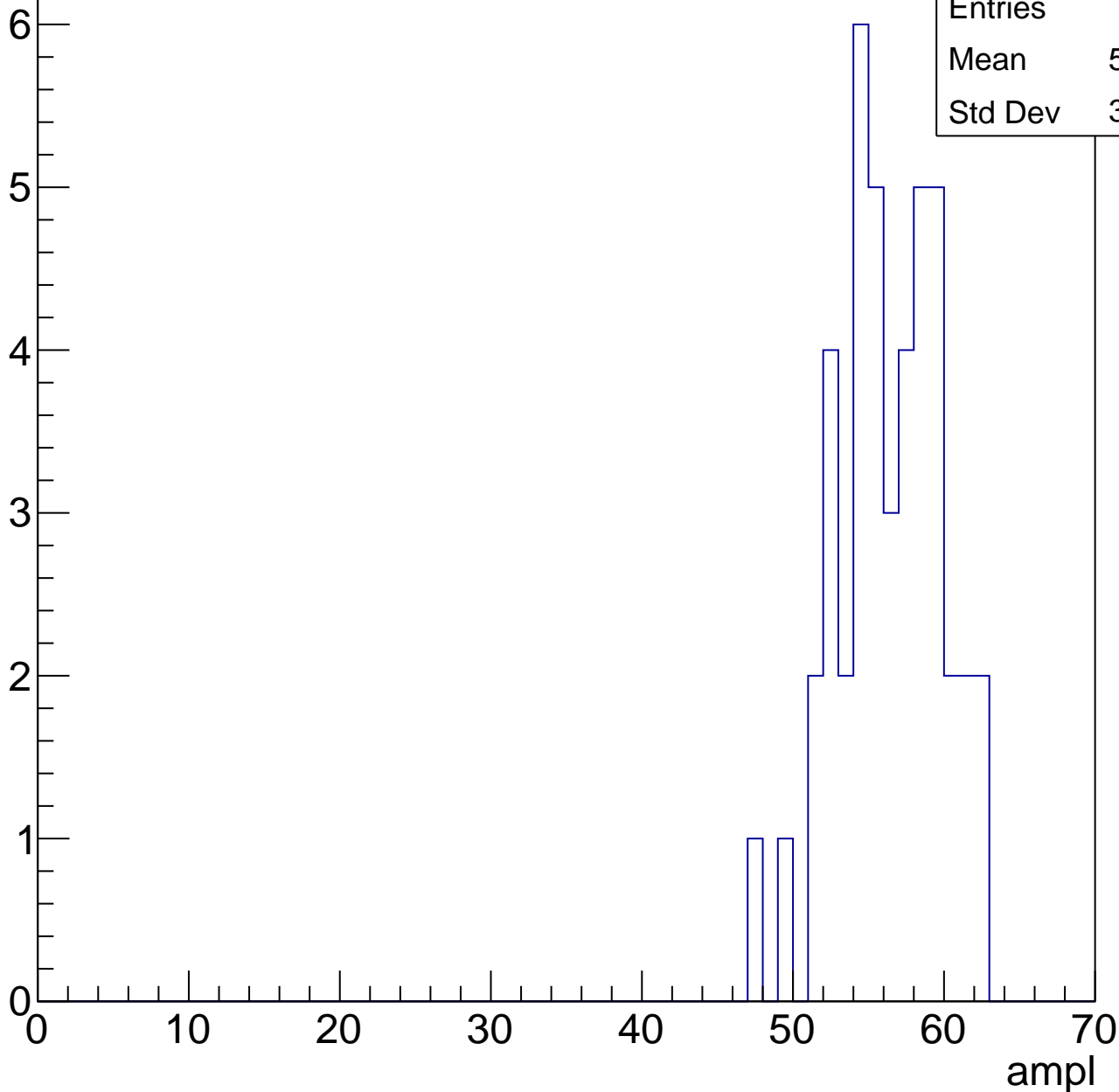


B1L103S, U13-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	55.86
Std Dev	3.415

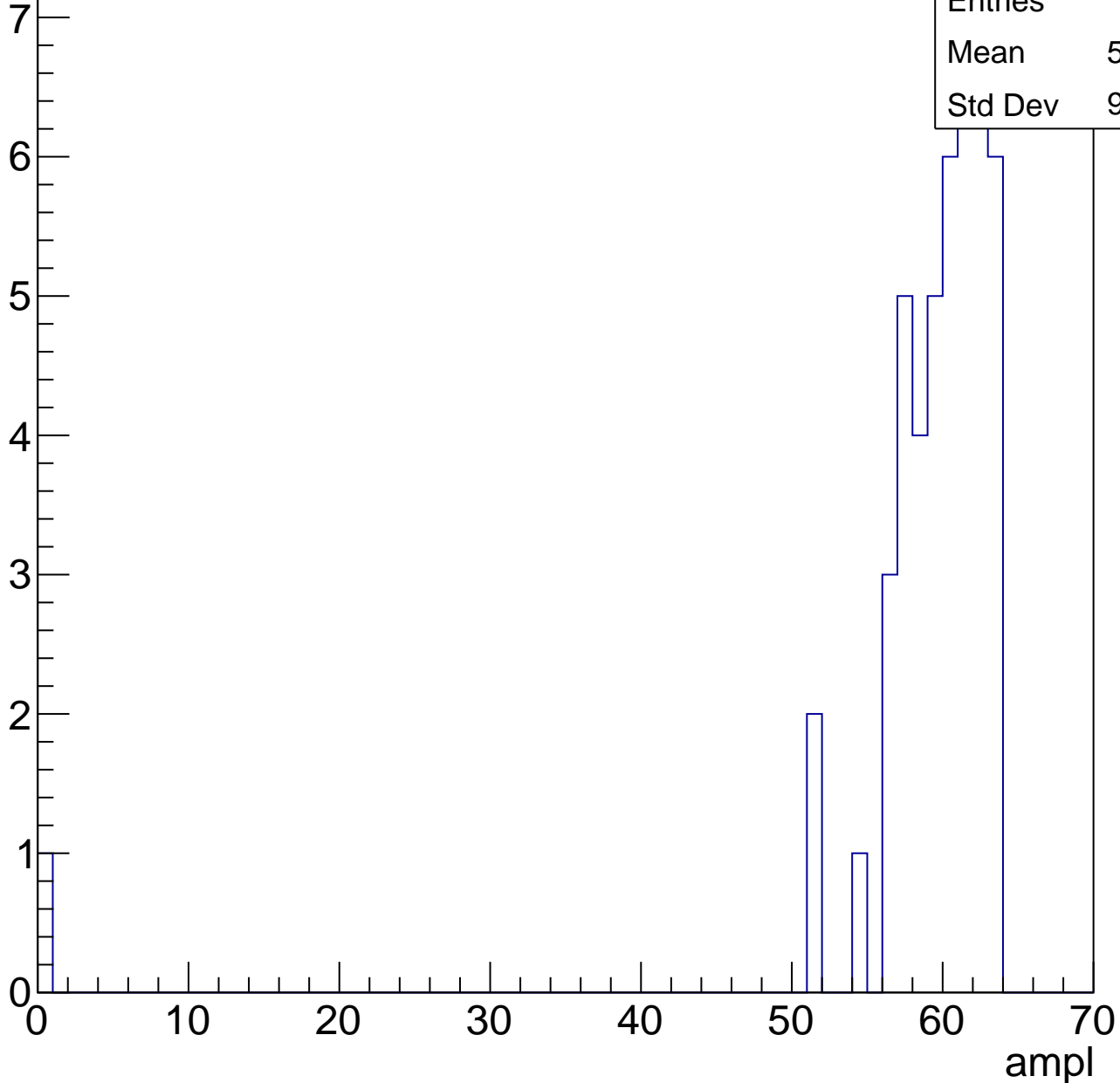


B1L103S, U13-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

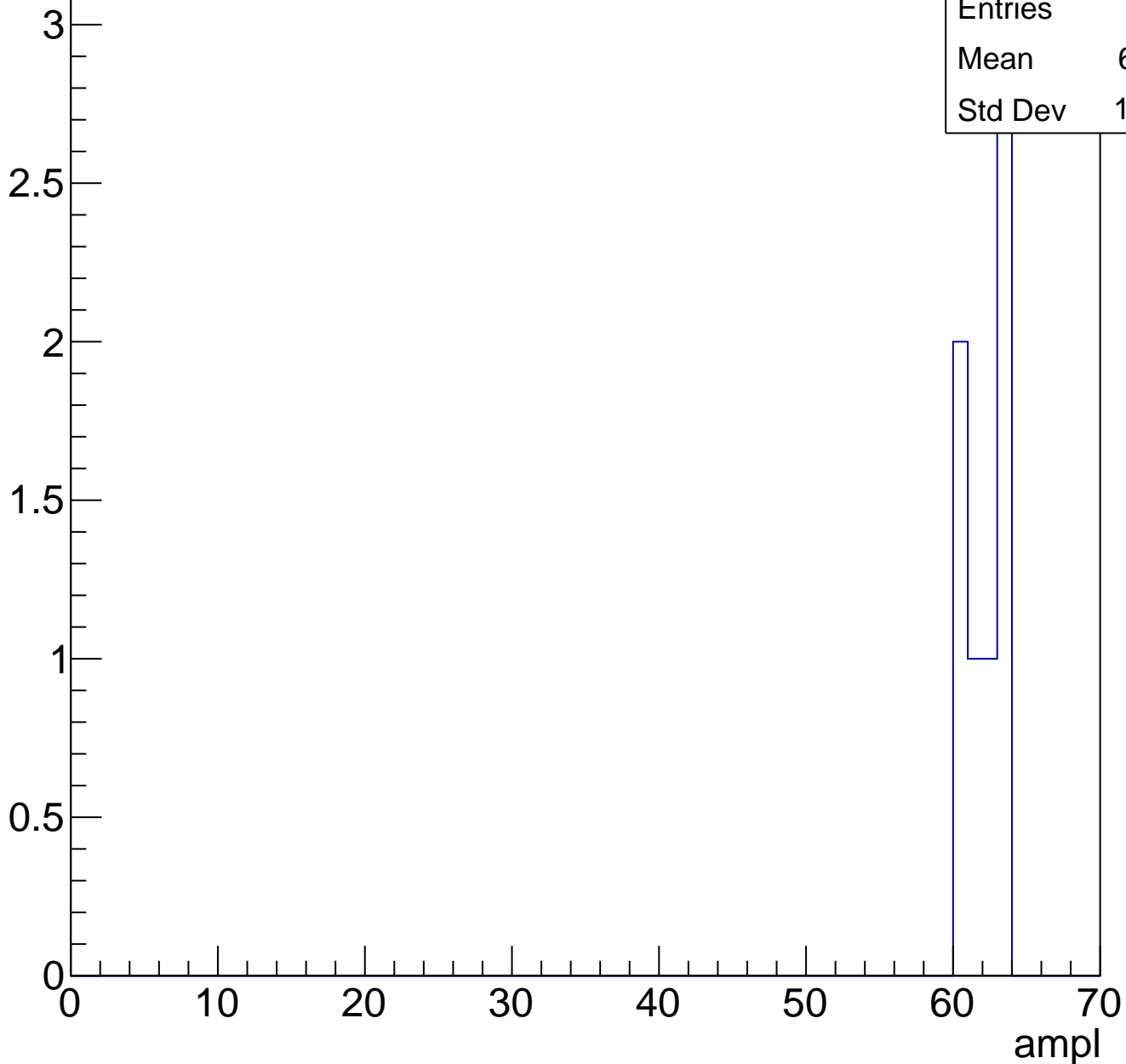
Entries	47
Mean	58.19
Std Dev	9.047



B1L103S, U13-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U13-ch111, adc0

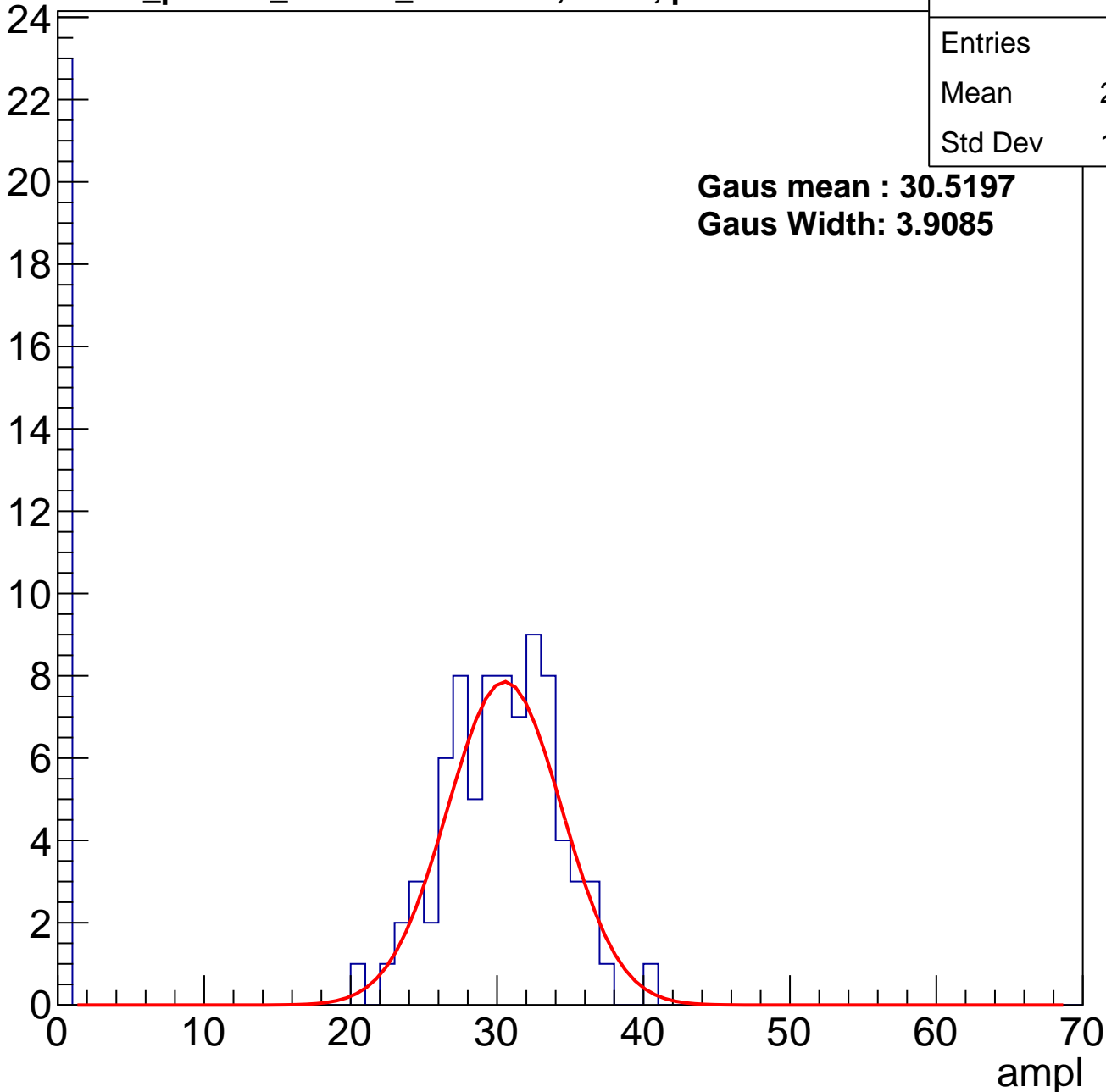
calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	23.19
Std Dev	12.87

Gaus mean : 30.5197

Gaus Width: 3.9085

Entry



B1L103S, U13-ch111, adc1

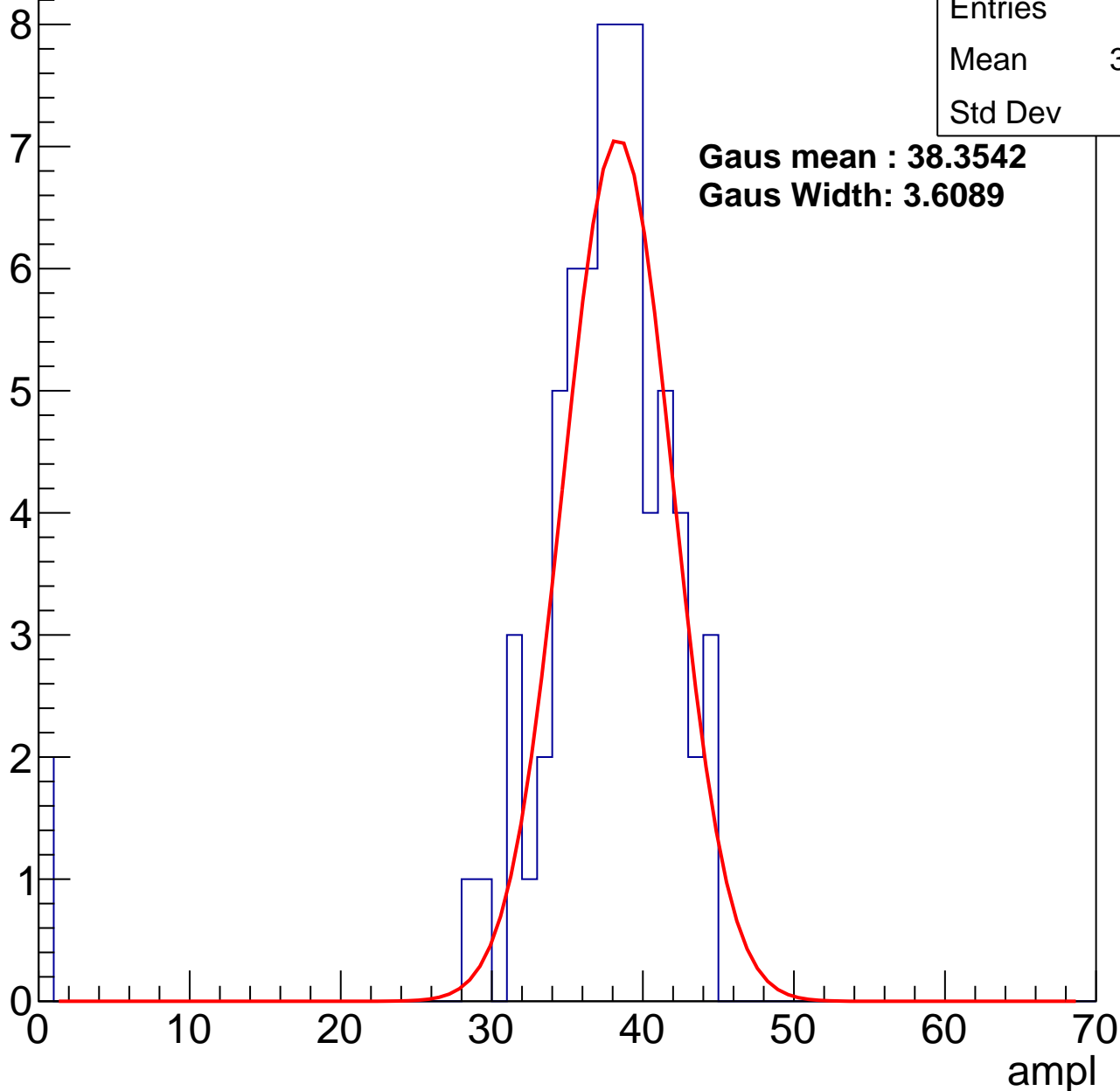
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	36.33
Std Dev	7.19

Gaus mean : 38.3542

Gaus Width: 3.6089



B1L103S, U13-ch111, adc2

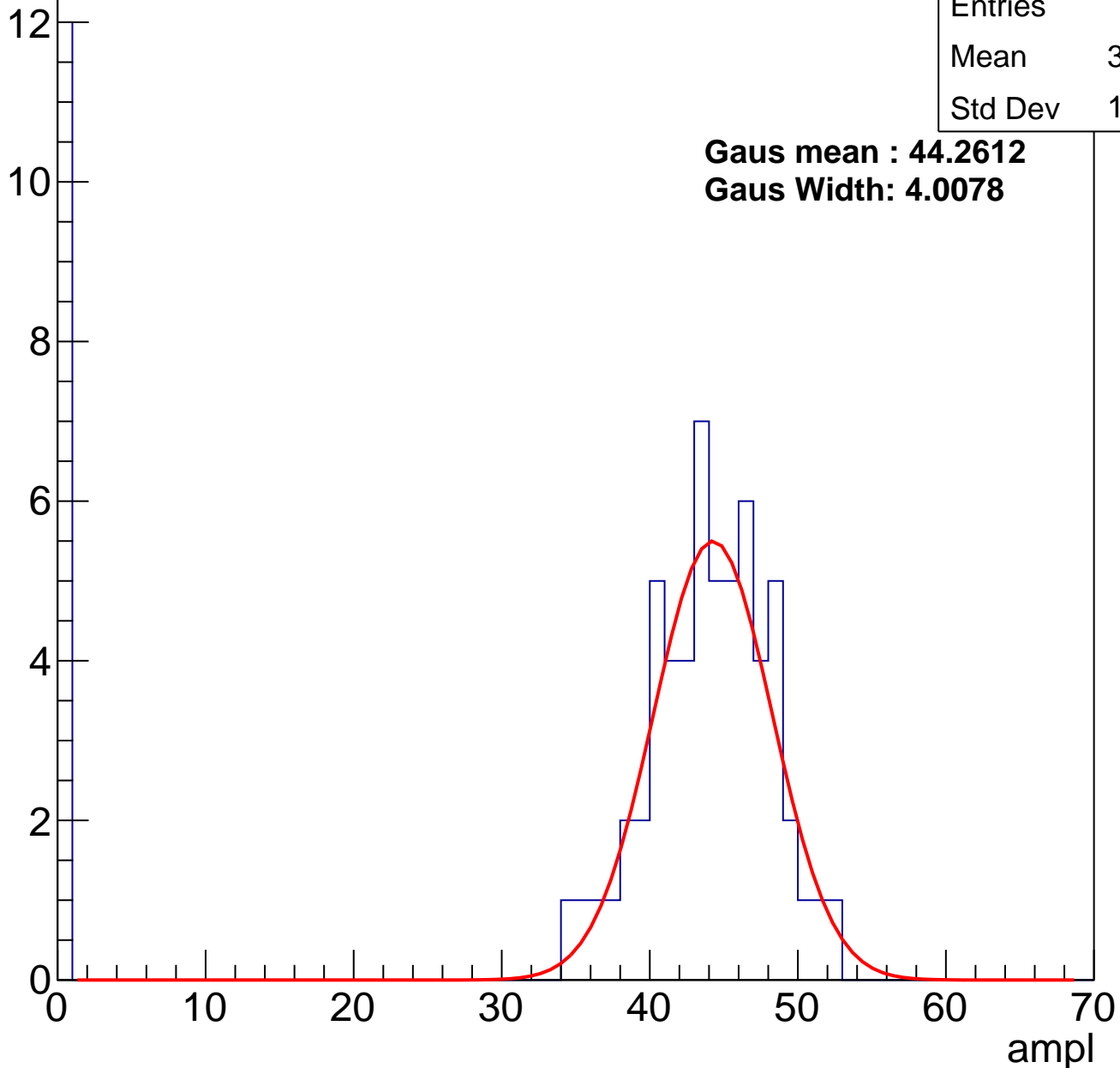
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	36.13
Std Dev	16.82

Gaus mean : 44.2612

Gaus Width: 4.0078

Entry

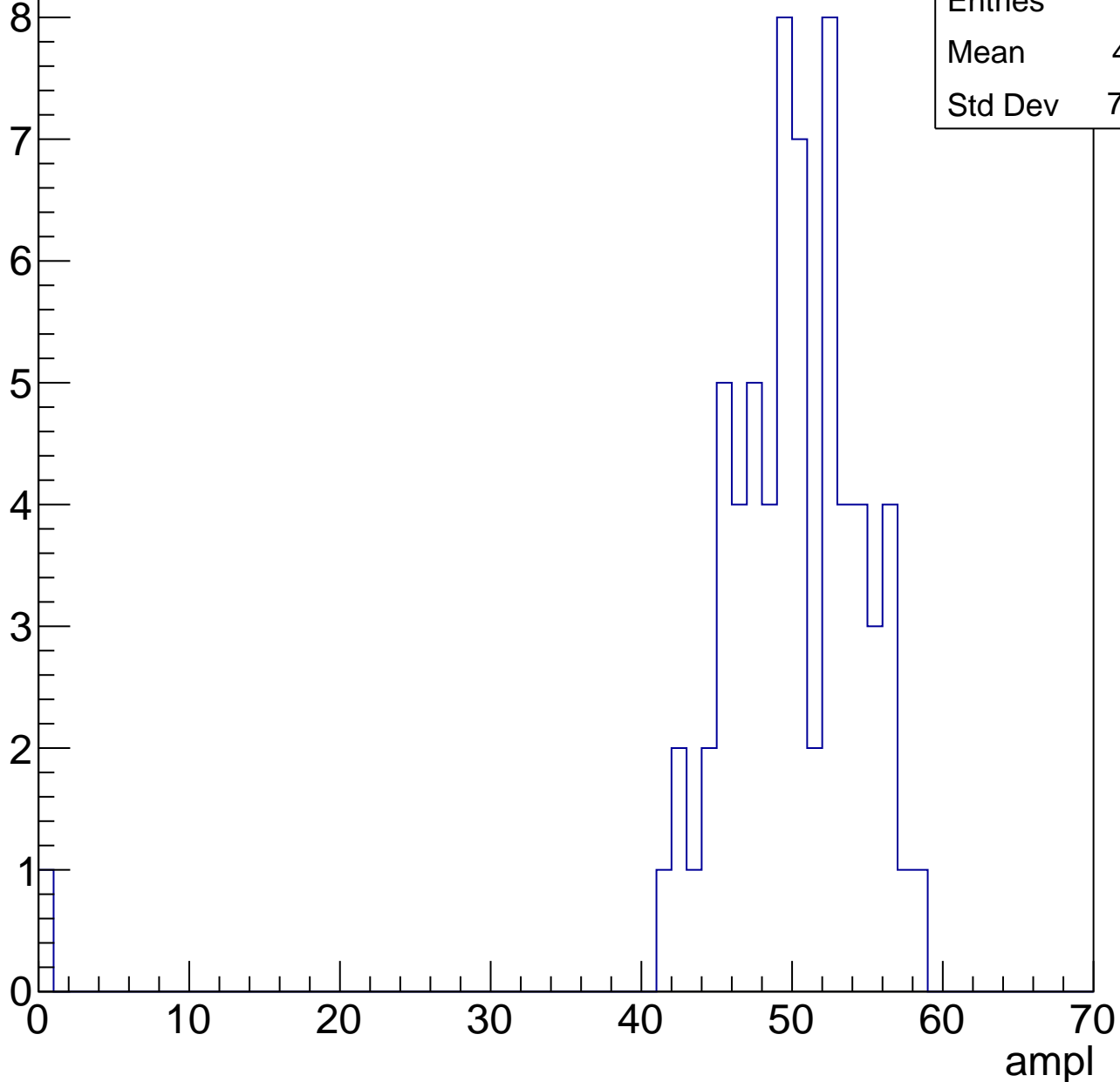


B1L103S, U13-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	49.01
Std Dev	7.216

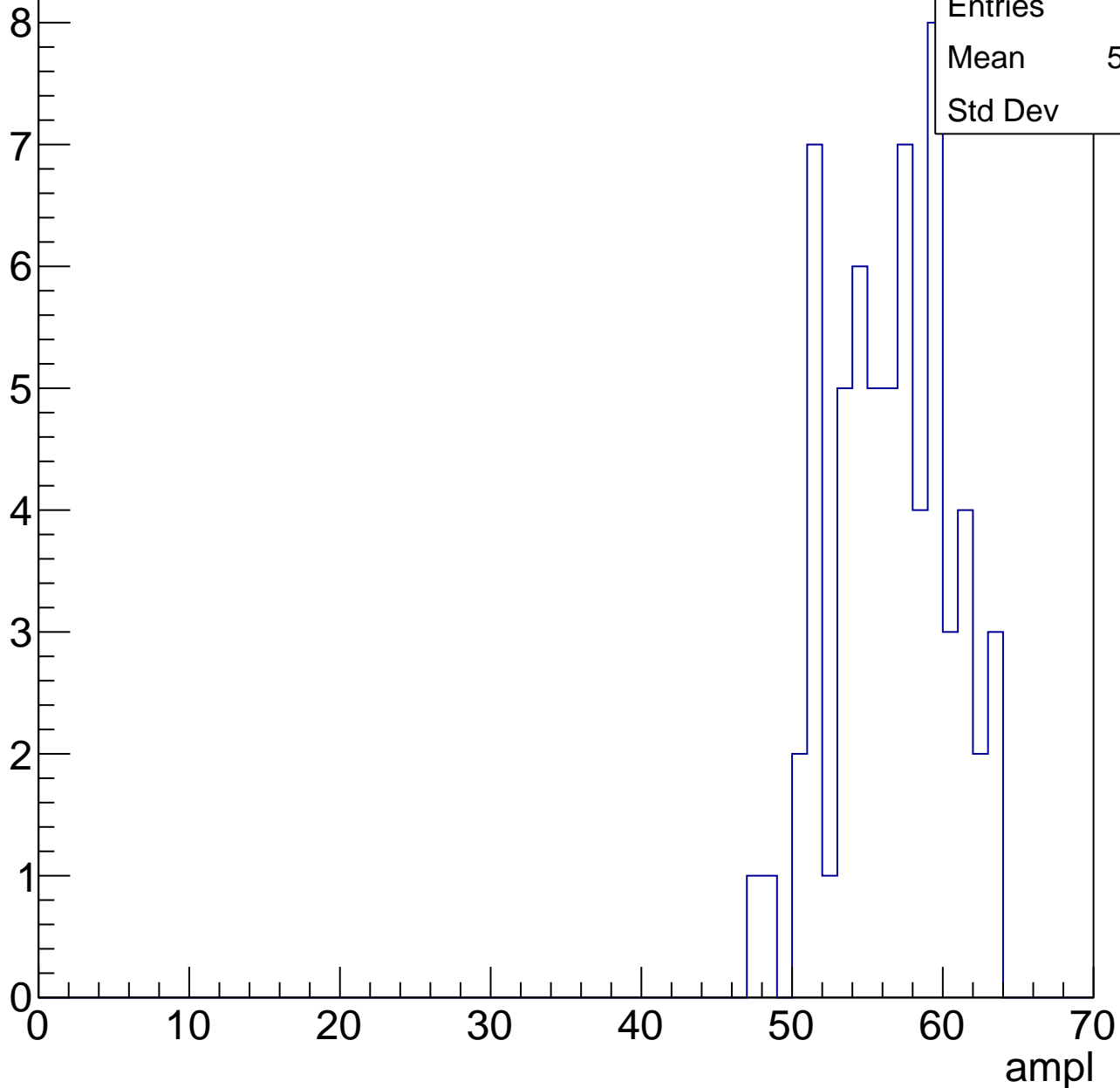


B1L103S, U13-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	56.06
Std Dev	3.84

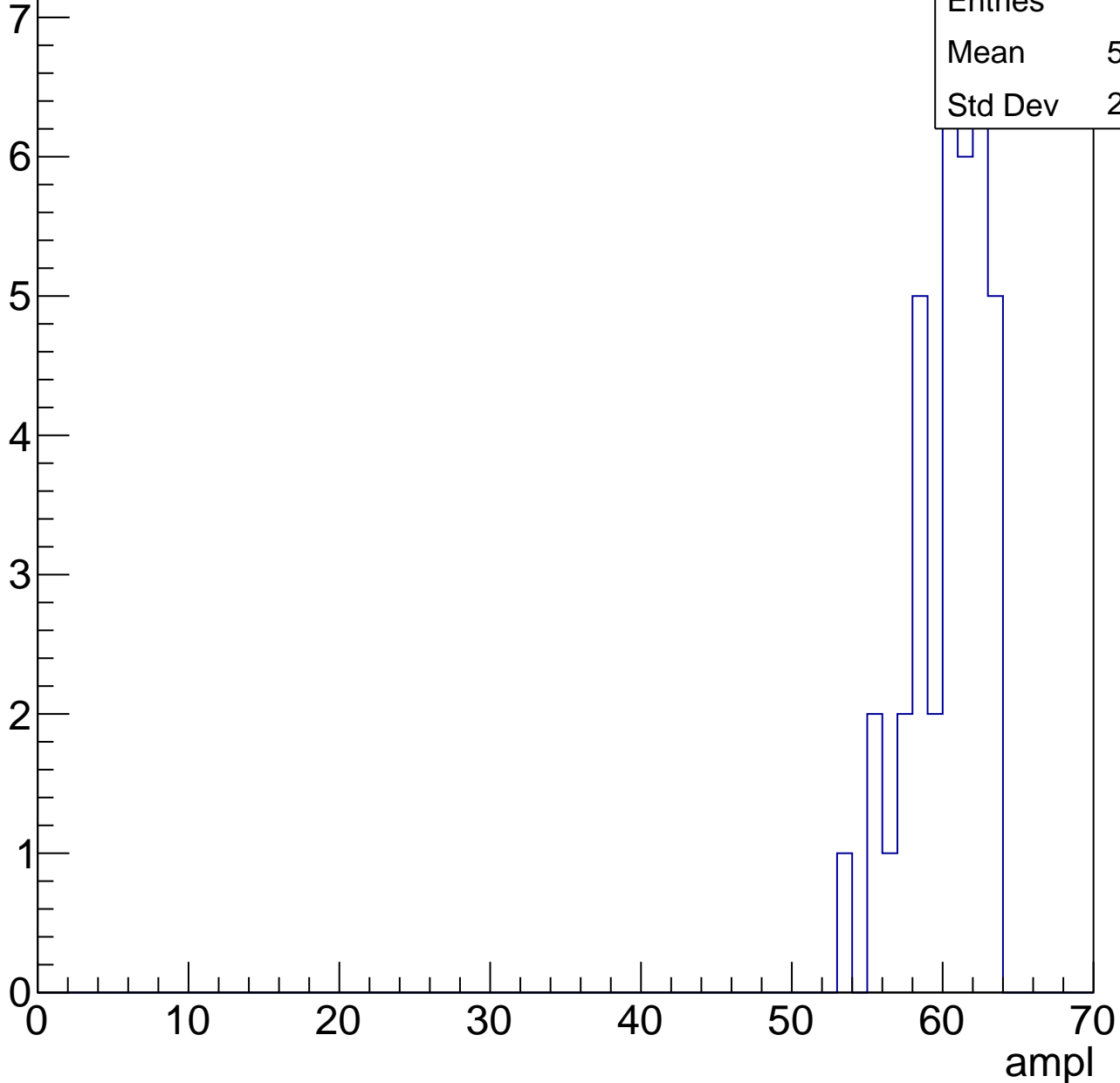


B1L103S, U13-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	59.89
Std Dev	2.479



B1L103S, U13-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch112, adc0

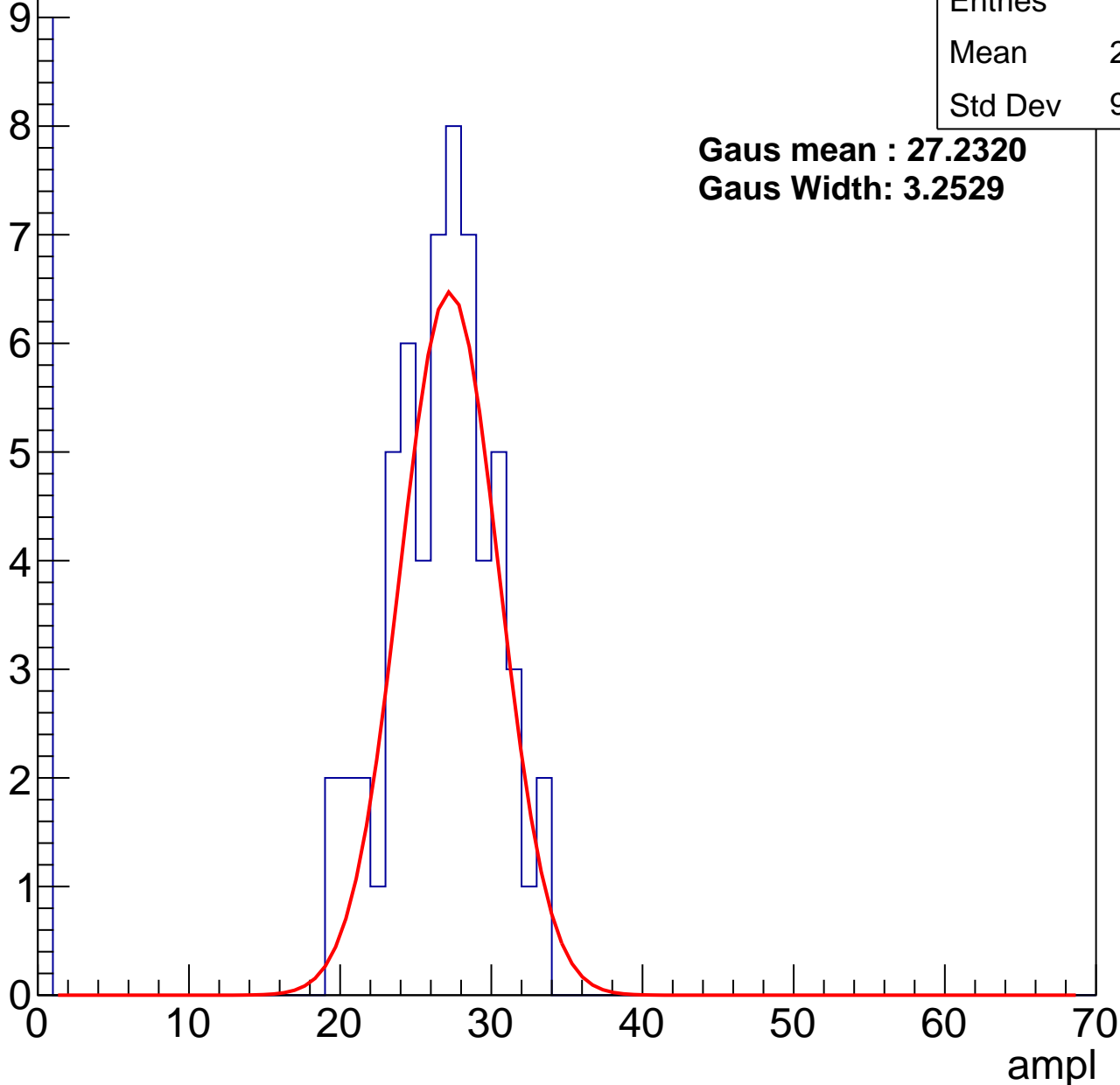
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	22.82
Std Dev	9.446

Gaus mean : 27.2320

Gaus Width: 3.2529



B1L103S, U13-ch112, adc1

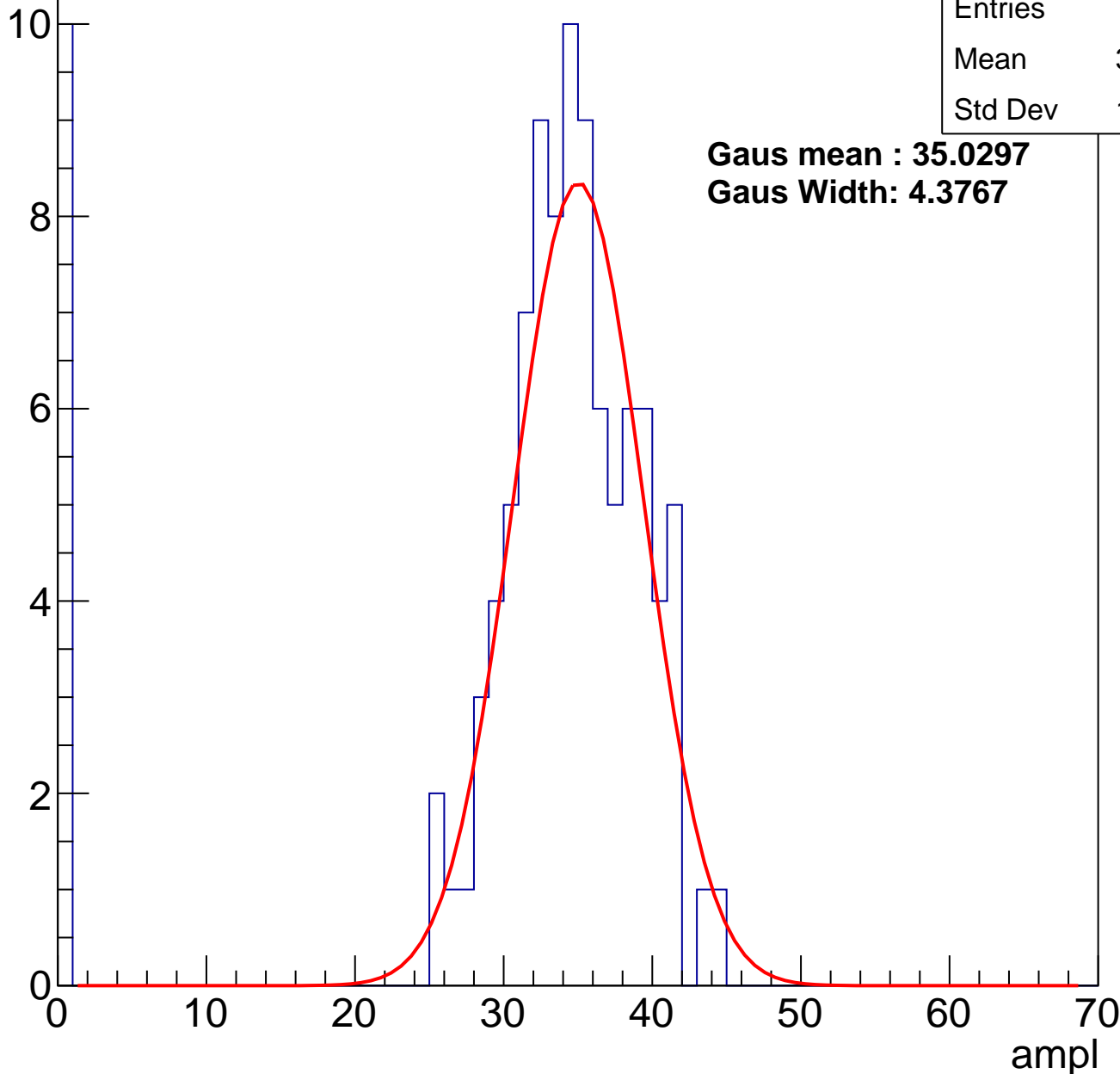
calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	30.99
Std Dev	10.88

Gaus mean : 35.0297

Gaus Width: 4.3767

Entry



B1L103S, U13-ch112, adc2

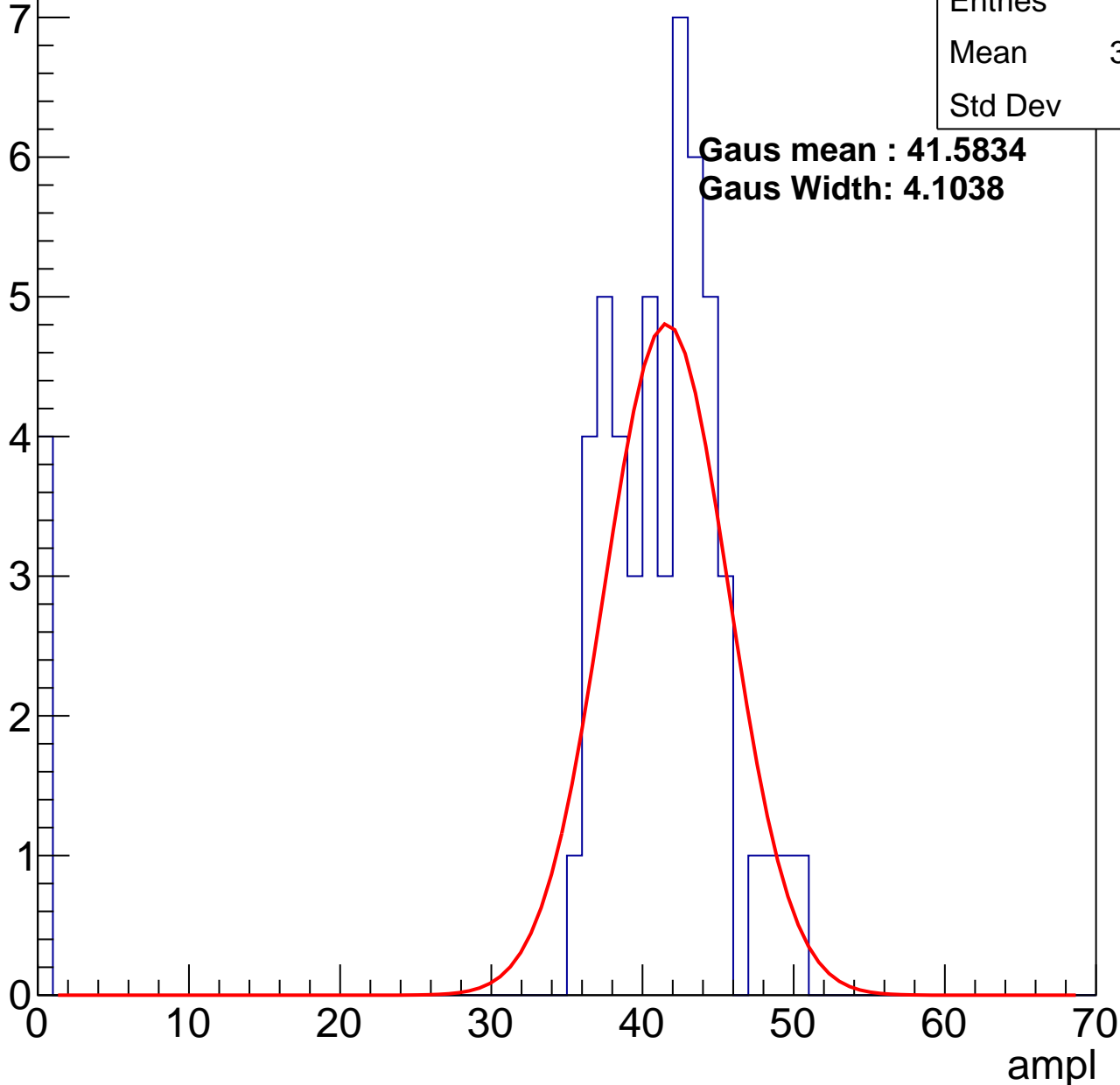
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	38.09
Std Dev	11.3

Gaus mean : 41.5834

Gaus Width: 4.1038

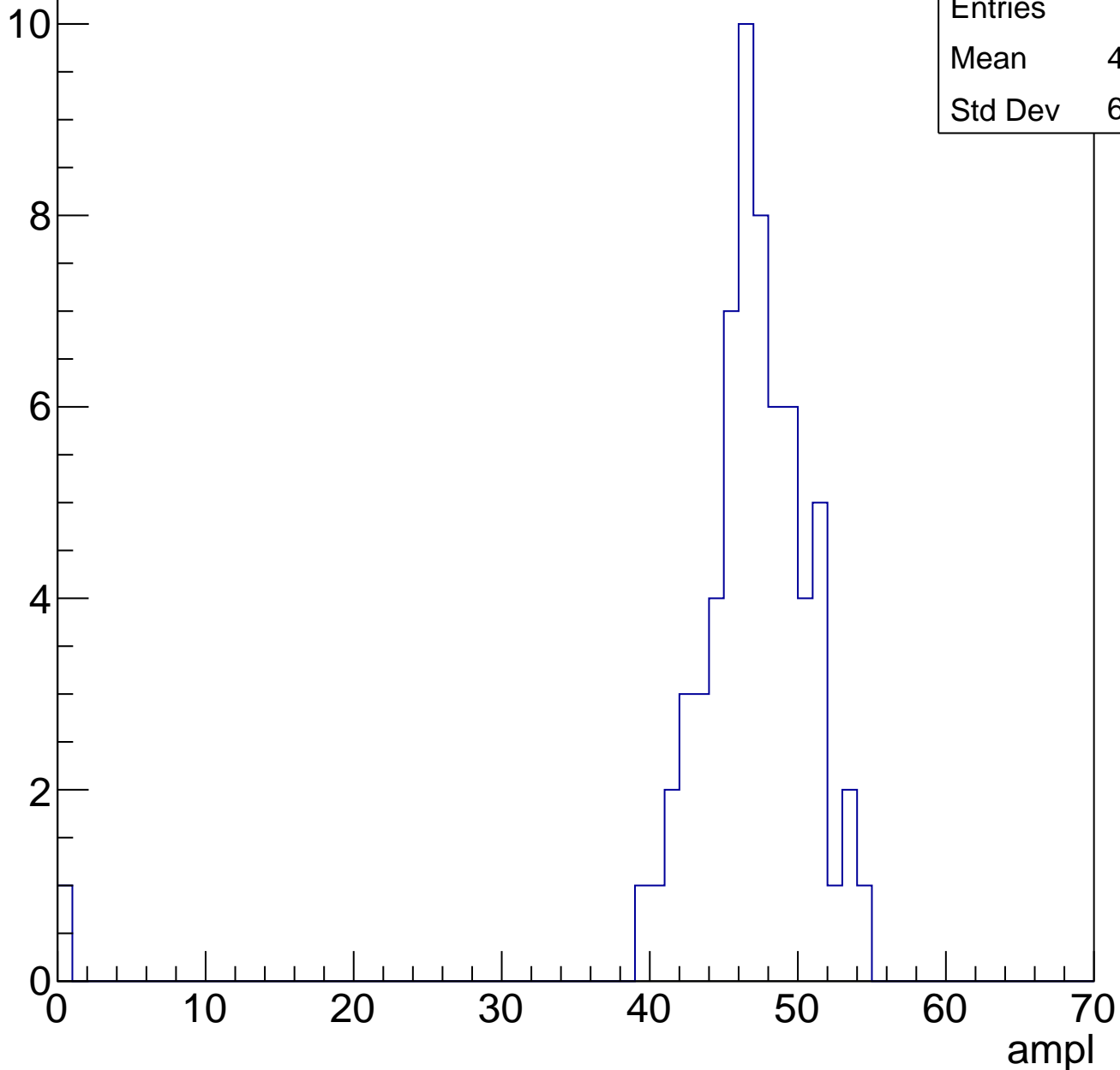


B1L103S, U13-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	46.03
Std Dev	6.589

Entry

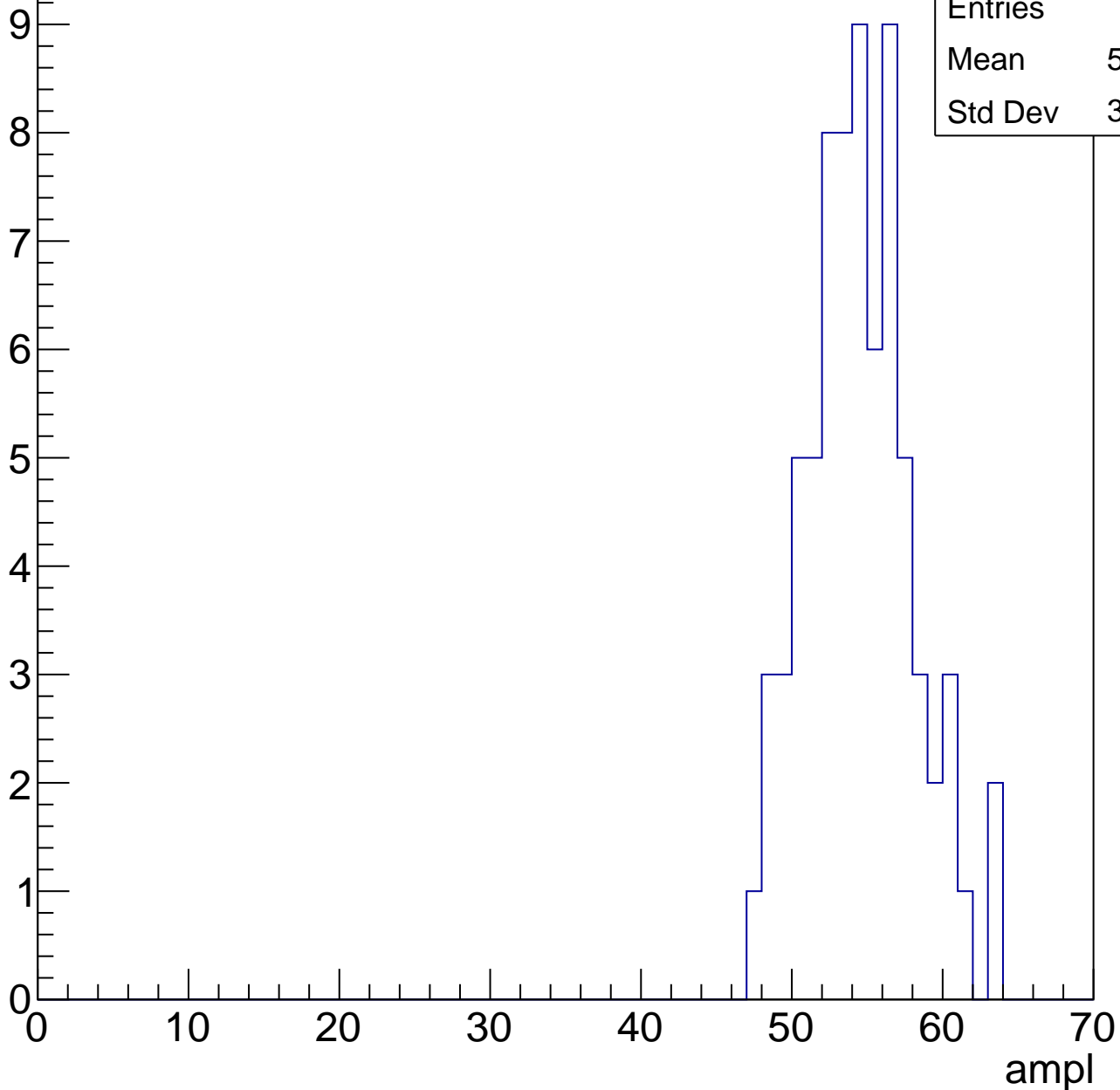


B1L103S, U13-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	54.07
Std Dev	3.513

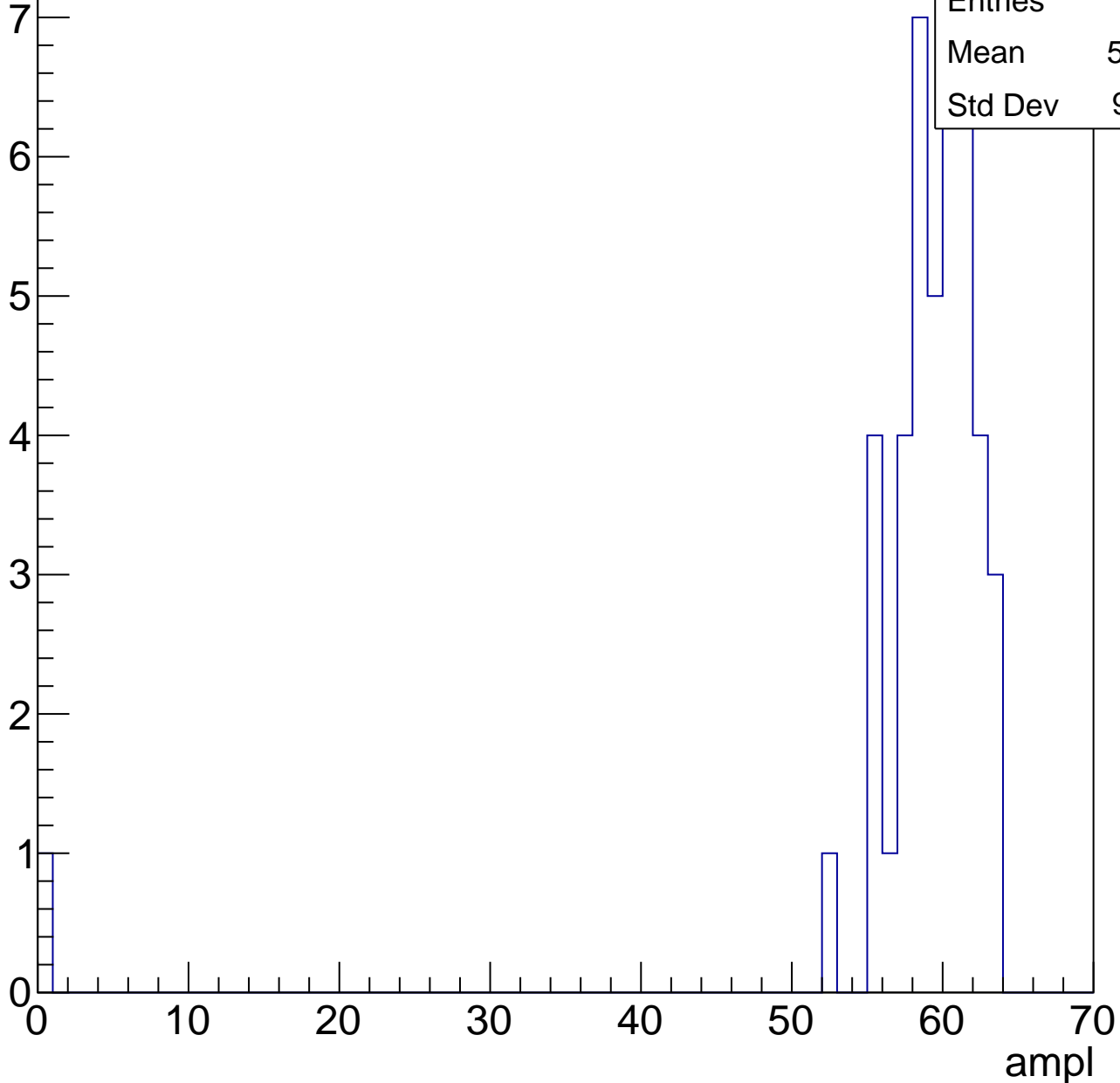


B1L103S, U13-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	57.75
Std Dev	9.141

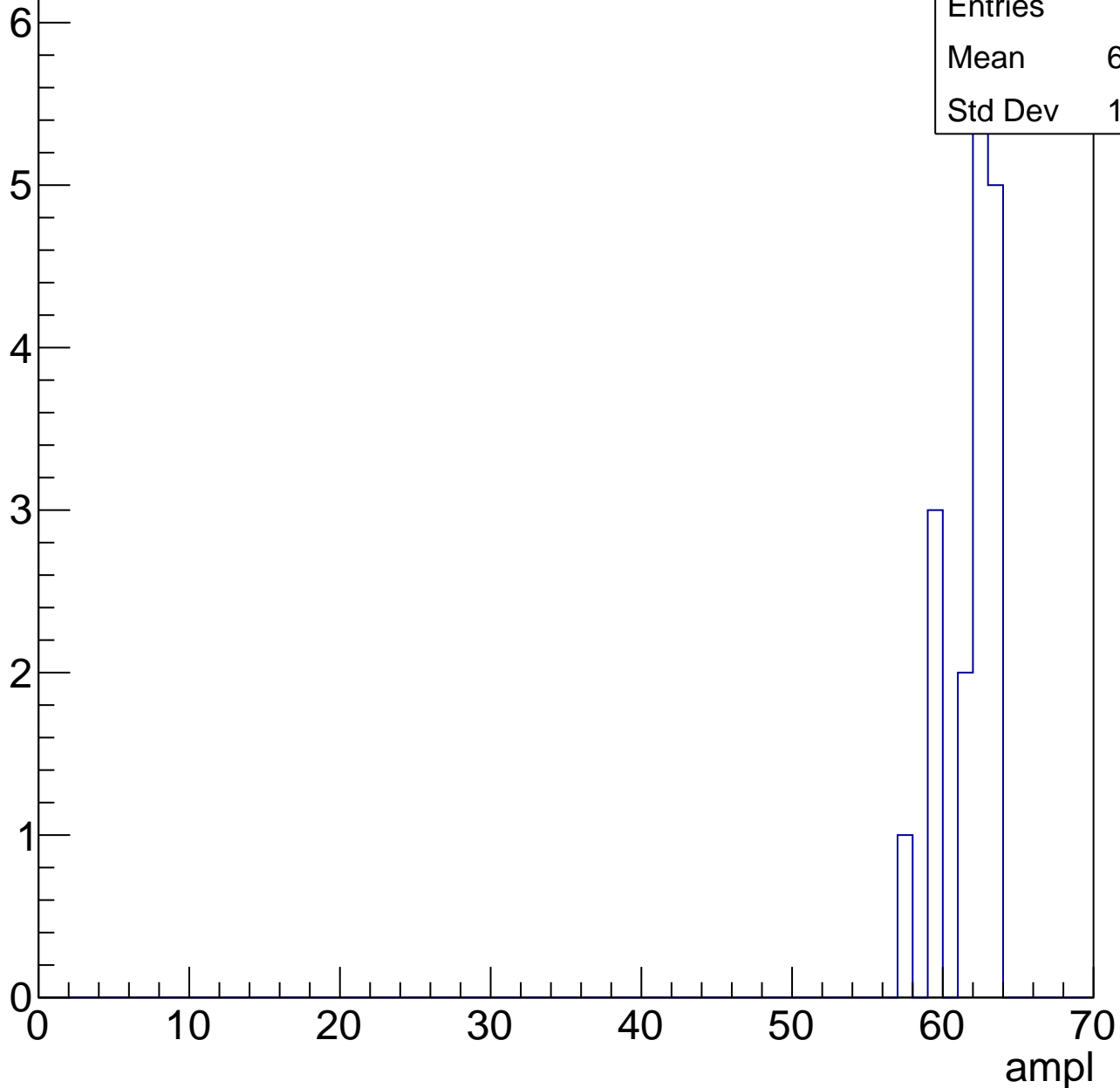


B1L103S, U13-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.35
Std Dev	1.747



B1L103S, U13-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch113, adc0

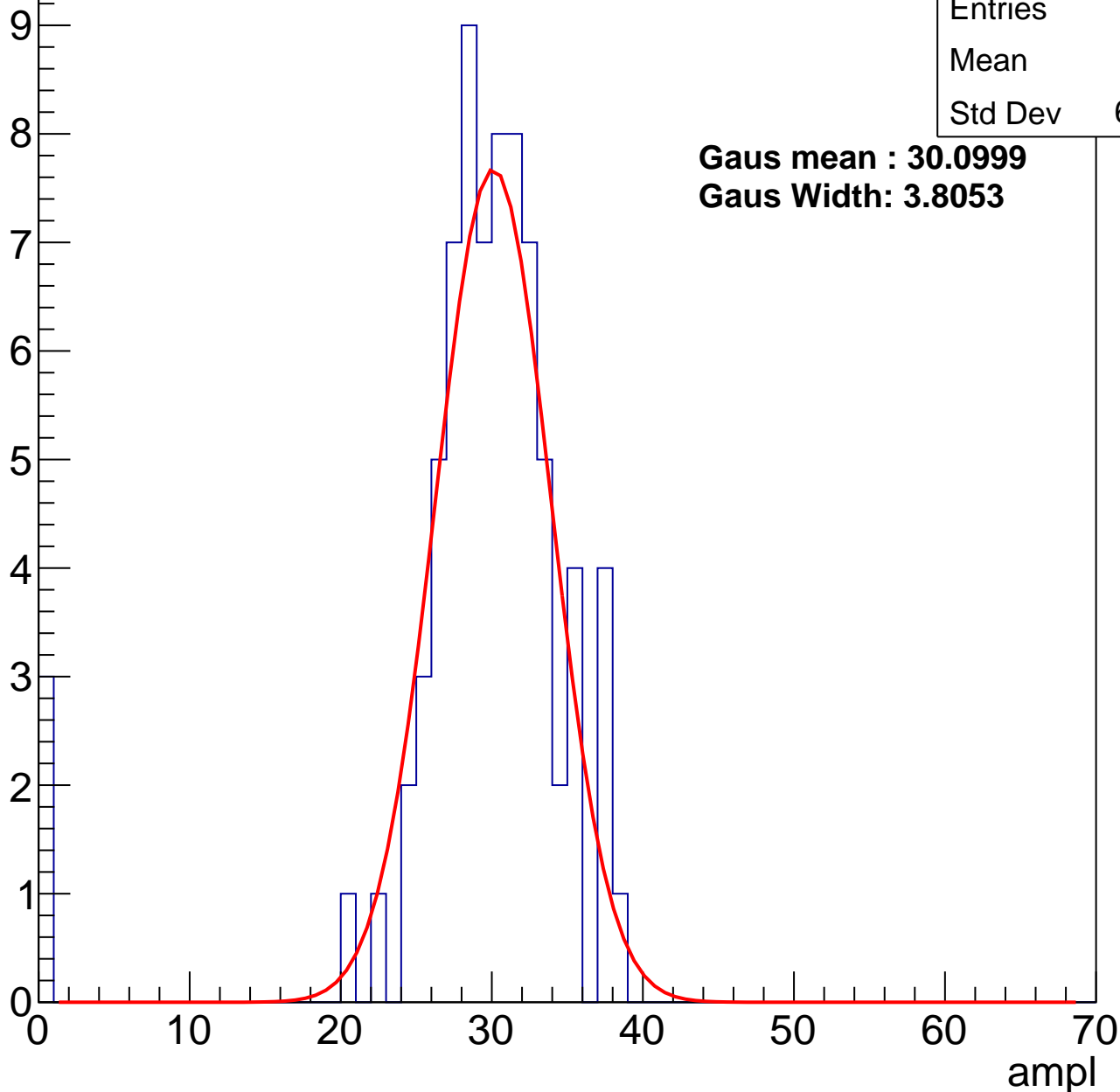
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	28.7
Std Dev	6.781

Gaus mean : 30.0999

Gaus Width: 3.8053



B1L103S, U13-ch113, adc1

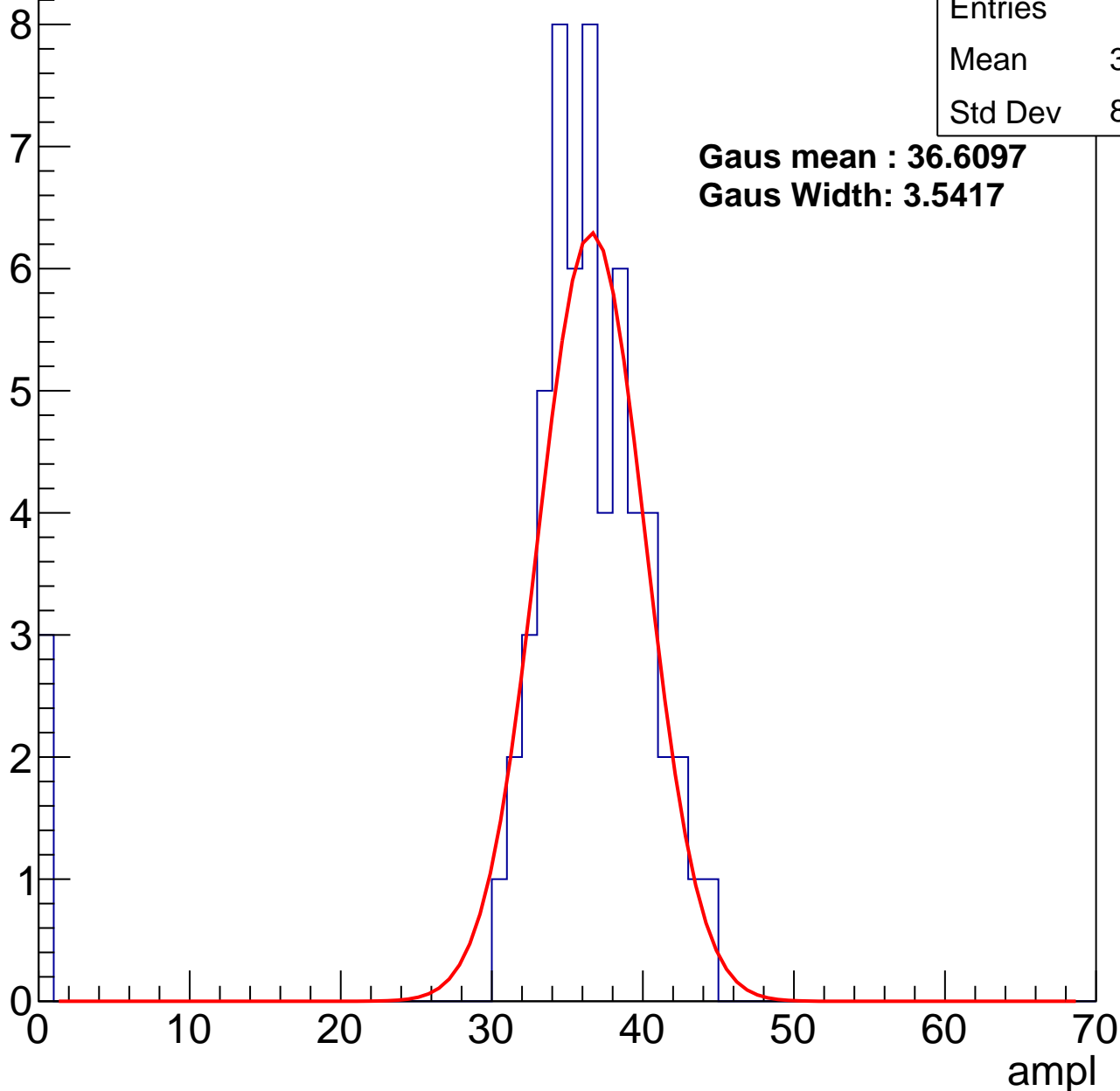
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	34.47
Std Dev	8.492

Gaus mean : 36.6097

Gaus Width: 3.5417



B1L103S, U13-ch113, adc2

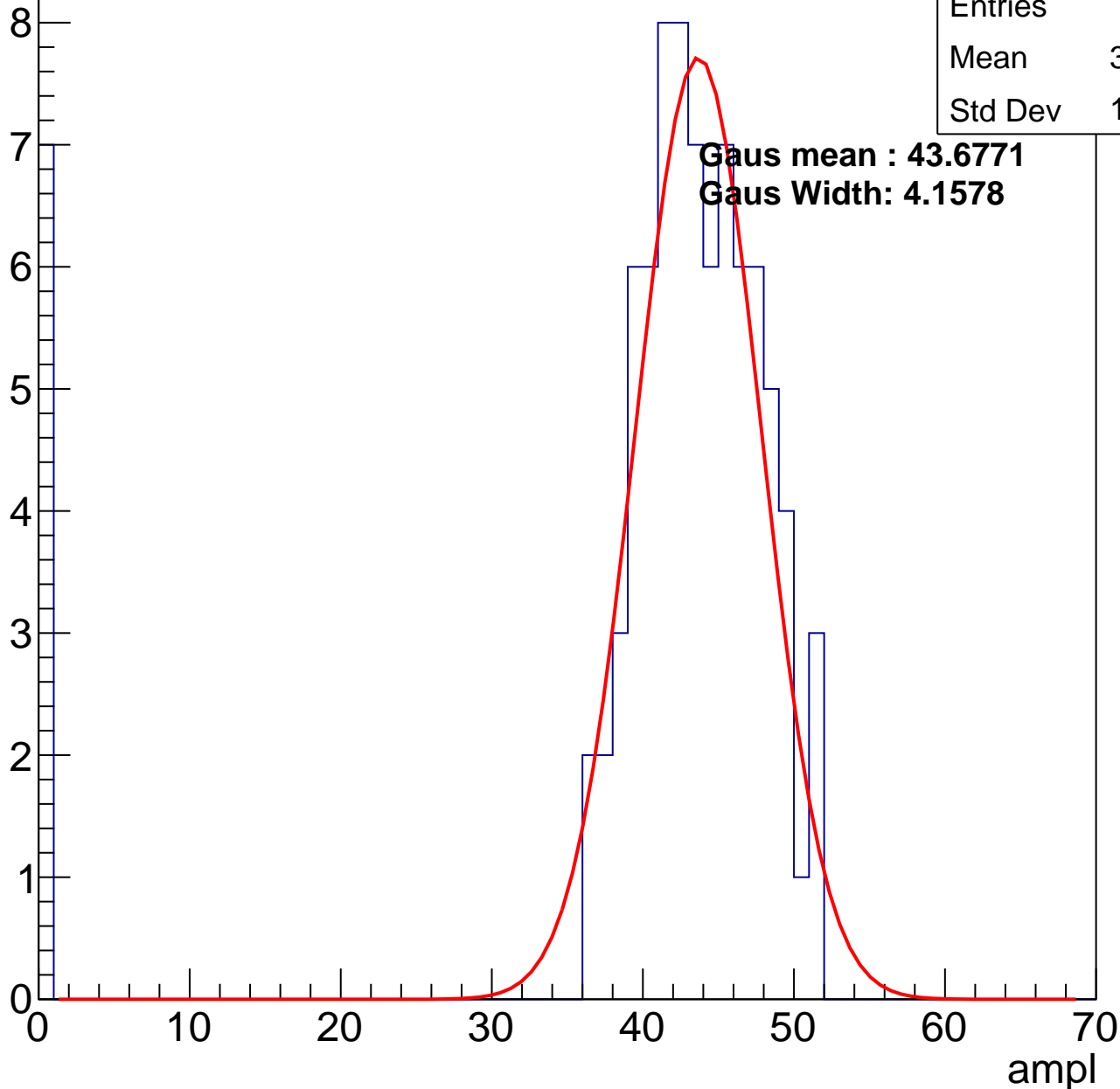
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	39.94
Std Dev	12.34

Gaus mean : 43.6771

Gaus Width: 4.1578

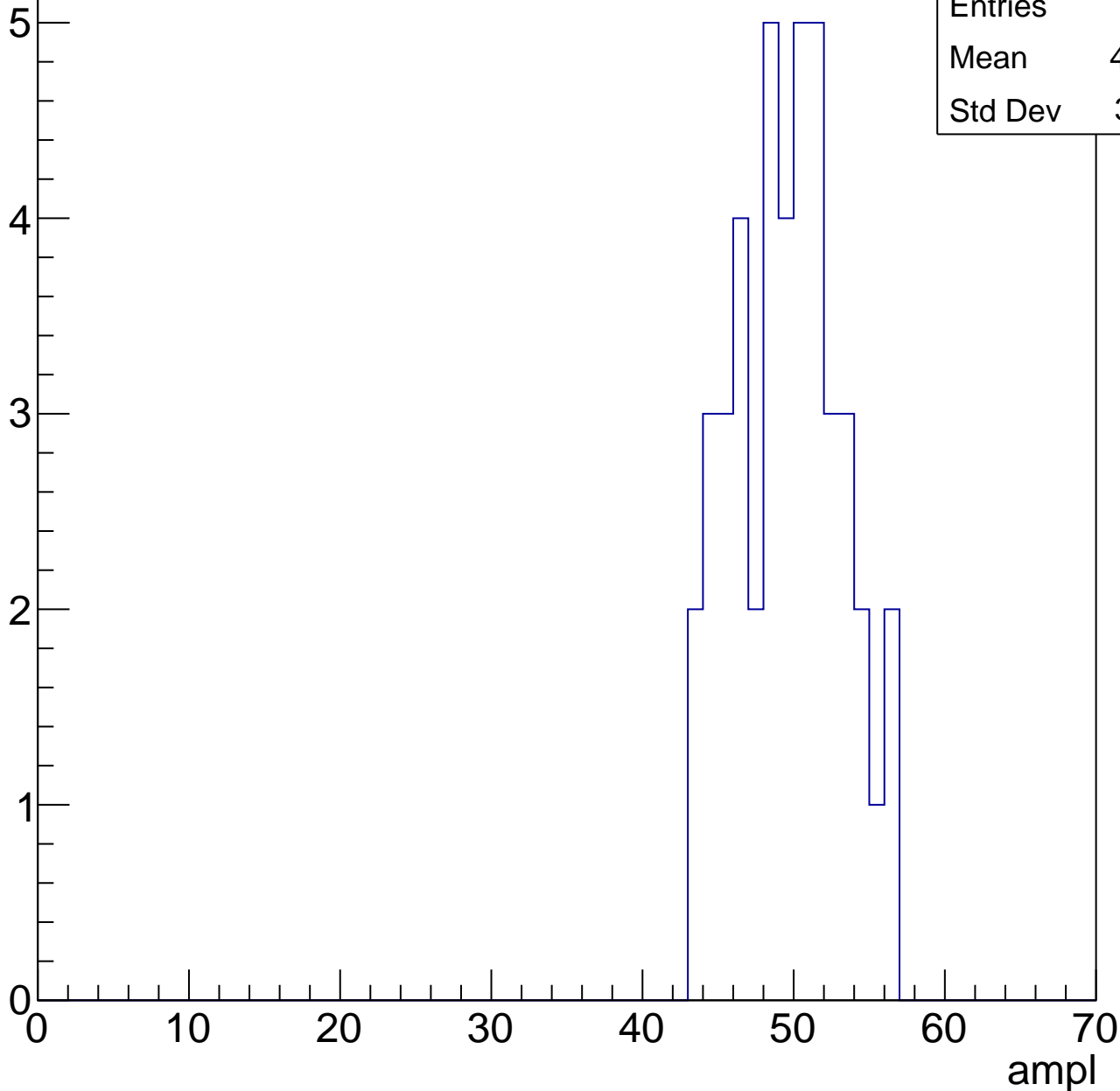


B1L103S, U13-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	49.14
Std Dev	3.461

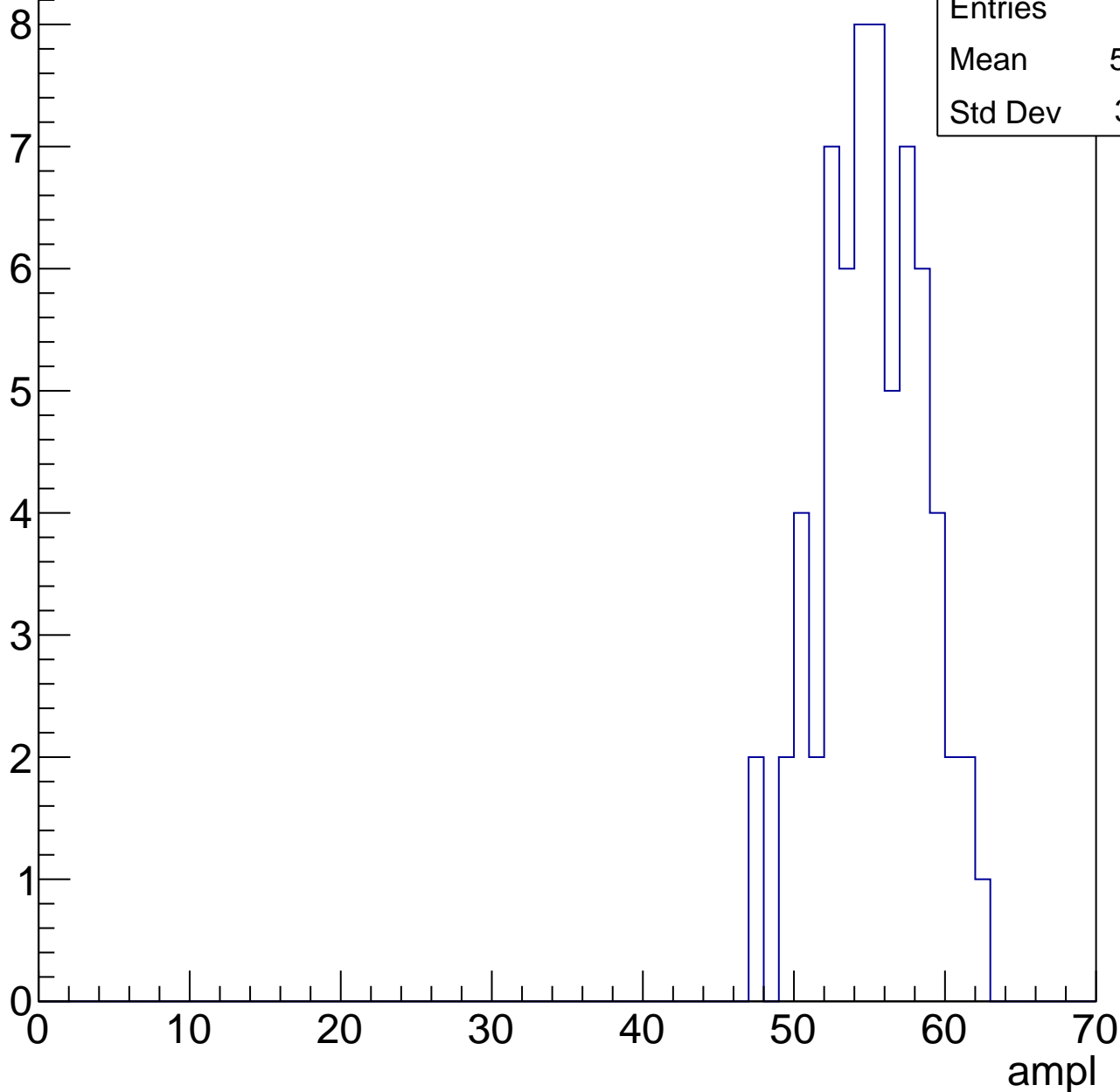


B1L103S, U13-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	54.77
Std Dev	3.361

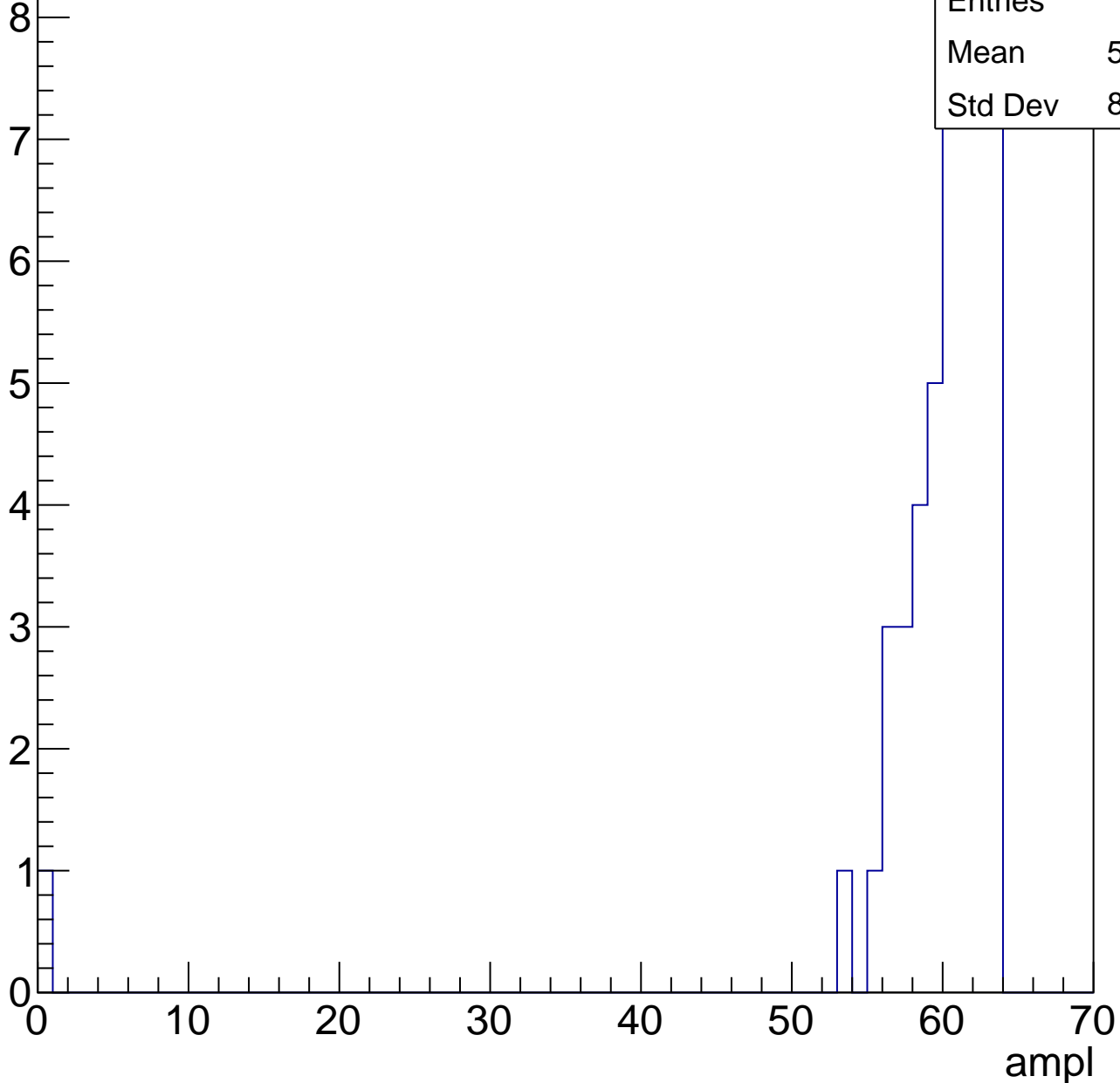


B1L103S, U13-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

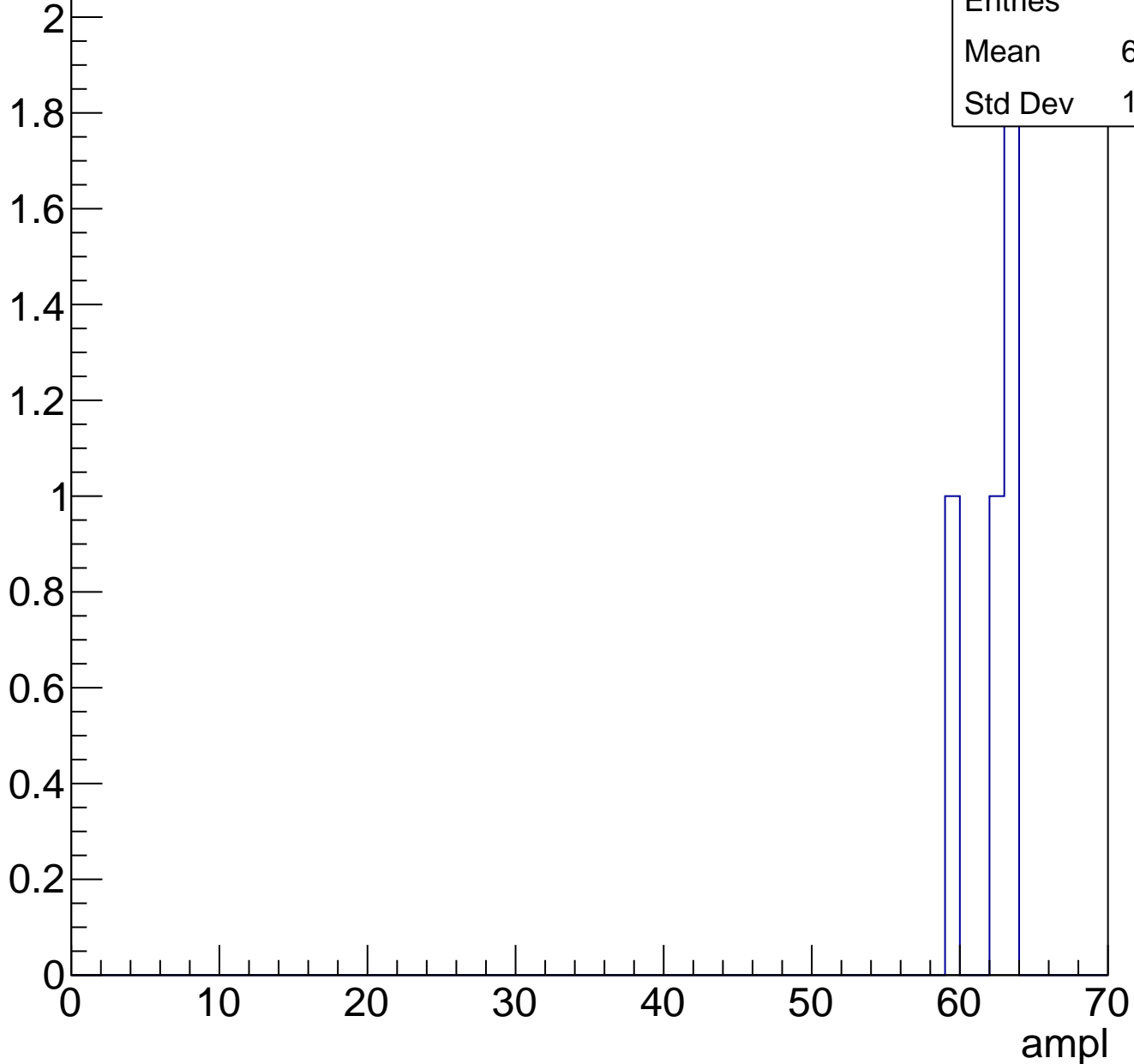
Entries	50
Mean	58.84
Std Dev	8.735



B1L103S, U13-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

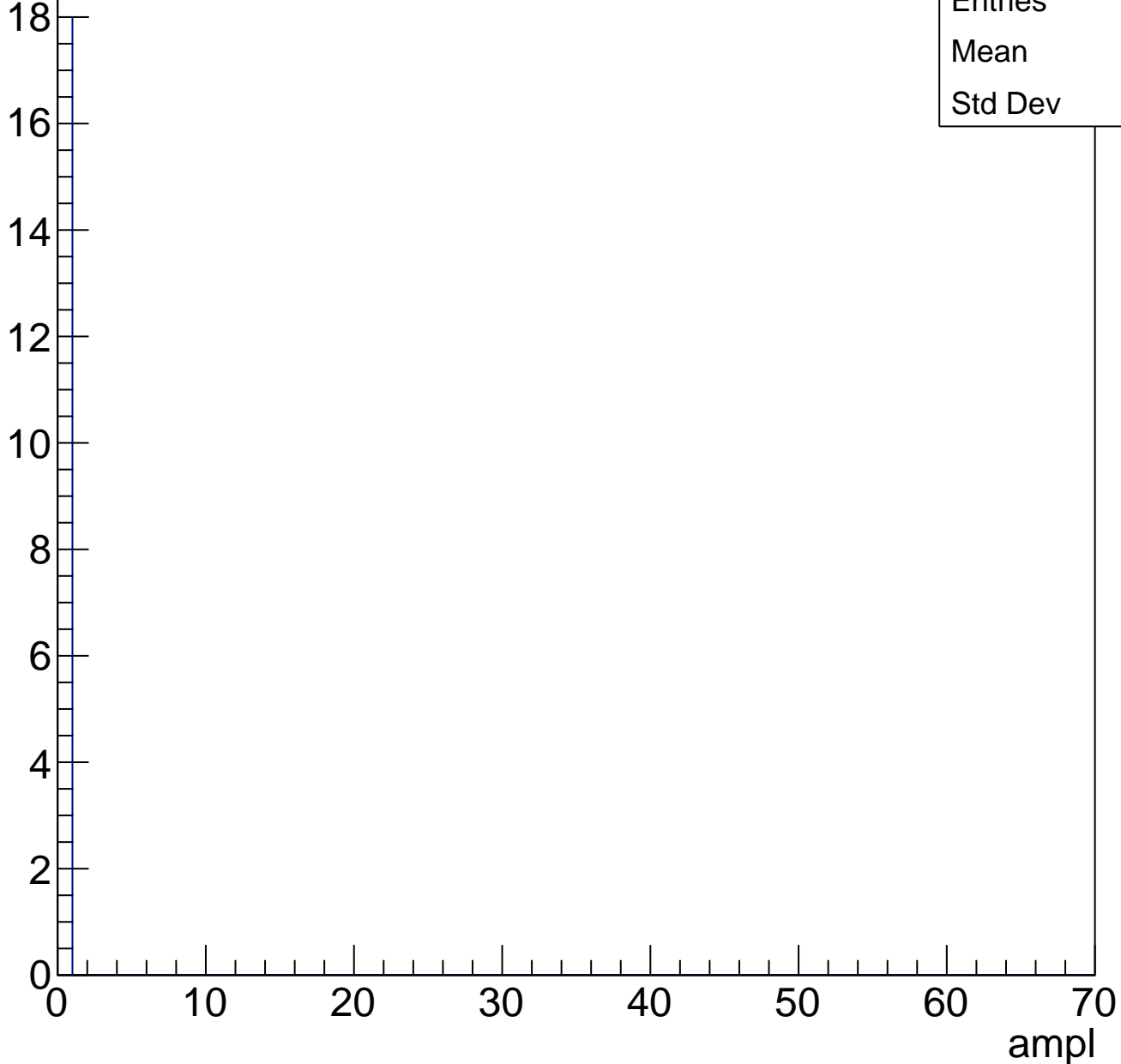
Entry



B1L103S, U13-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U13-ch114, adc0

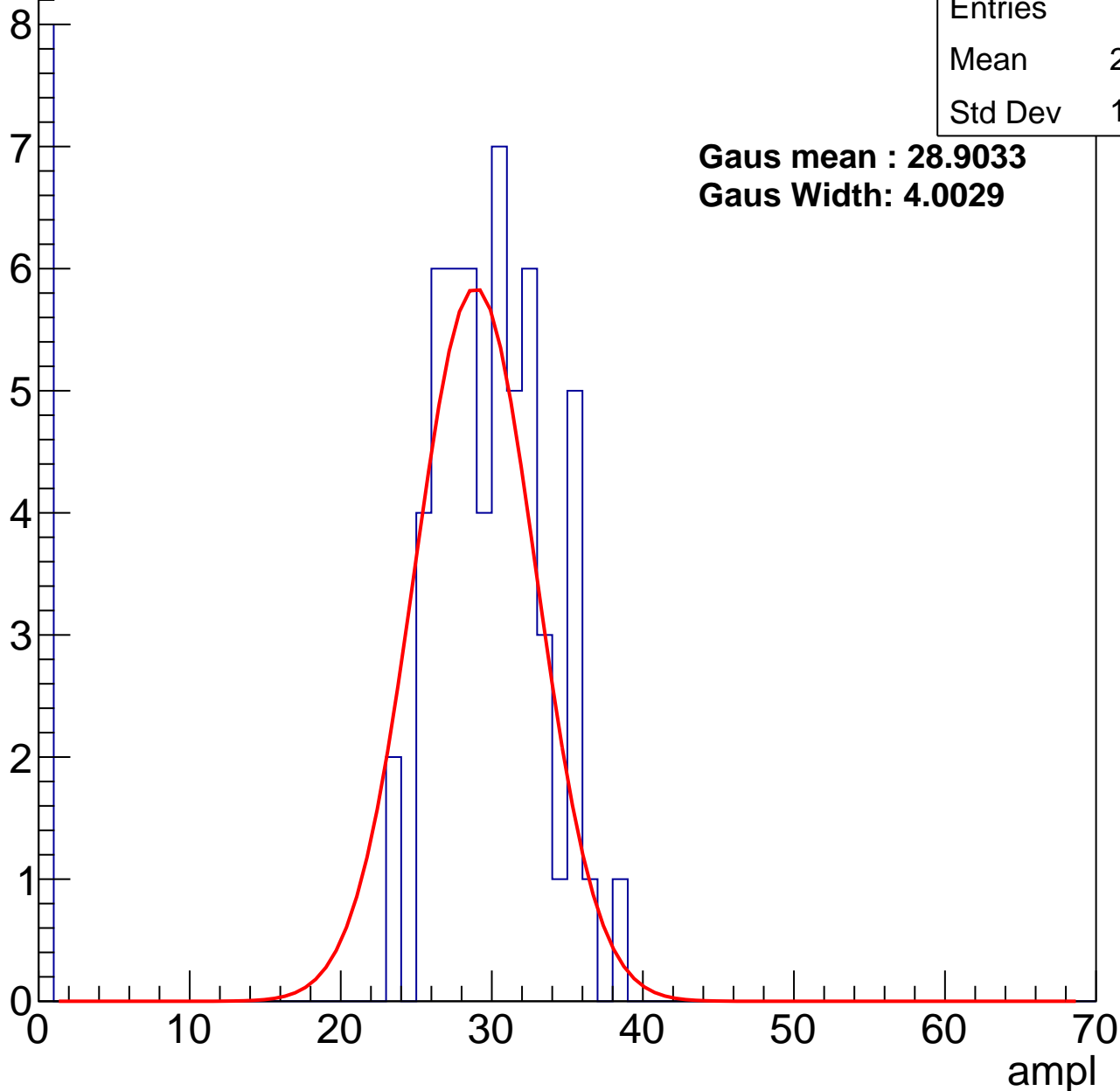
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	25.95
Std Dev	10.23

Gaus mean : 28.9033

Gaus Width: 4.0029



B1L103S, U13-ch114, adc1

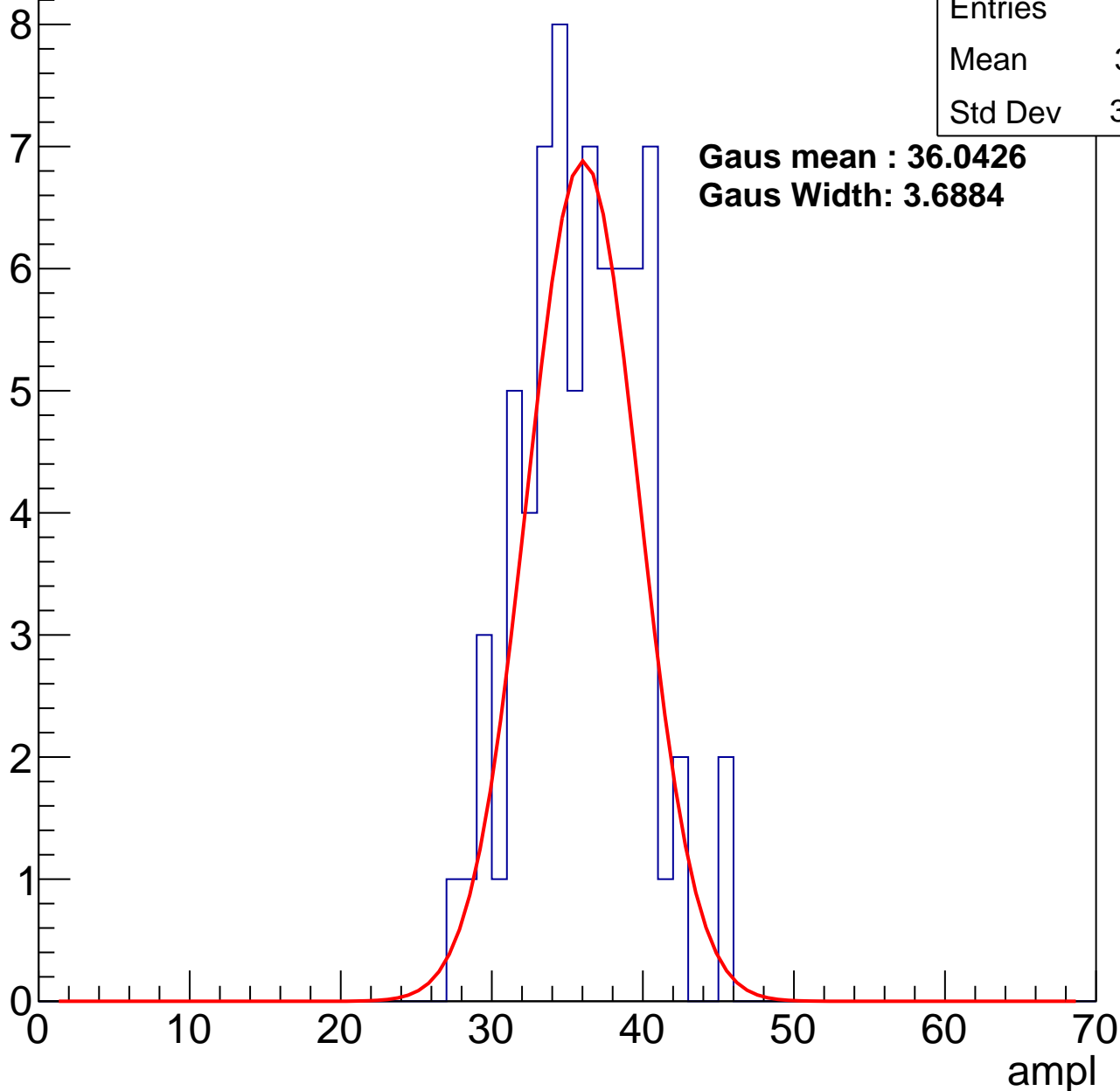
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.61
Std Dev	3.839

Gaus mean : 36.0426

Gaus Width: 3.6884



B1L103S, U13-ch114, adc2

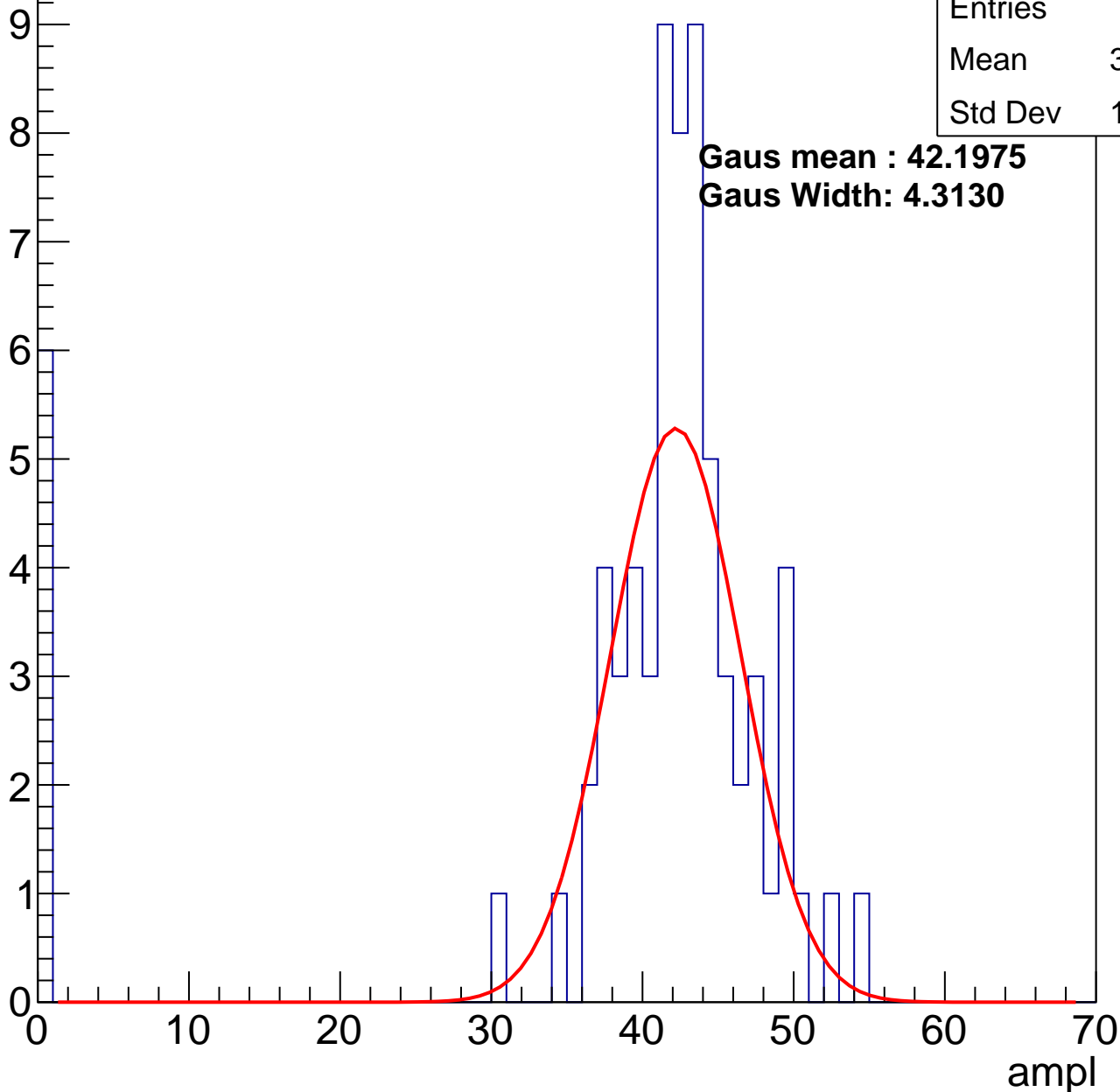
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	38.79
Std Dev	12.47

Gaus mean : 42.1975

Gaus Width: 4.3130

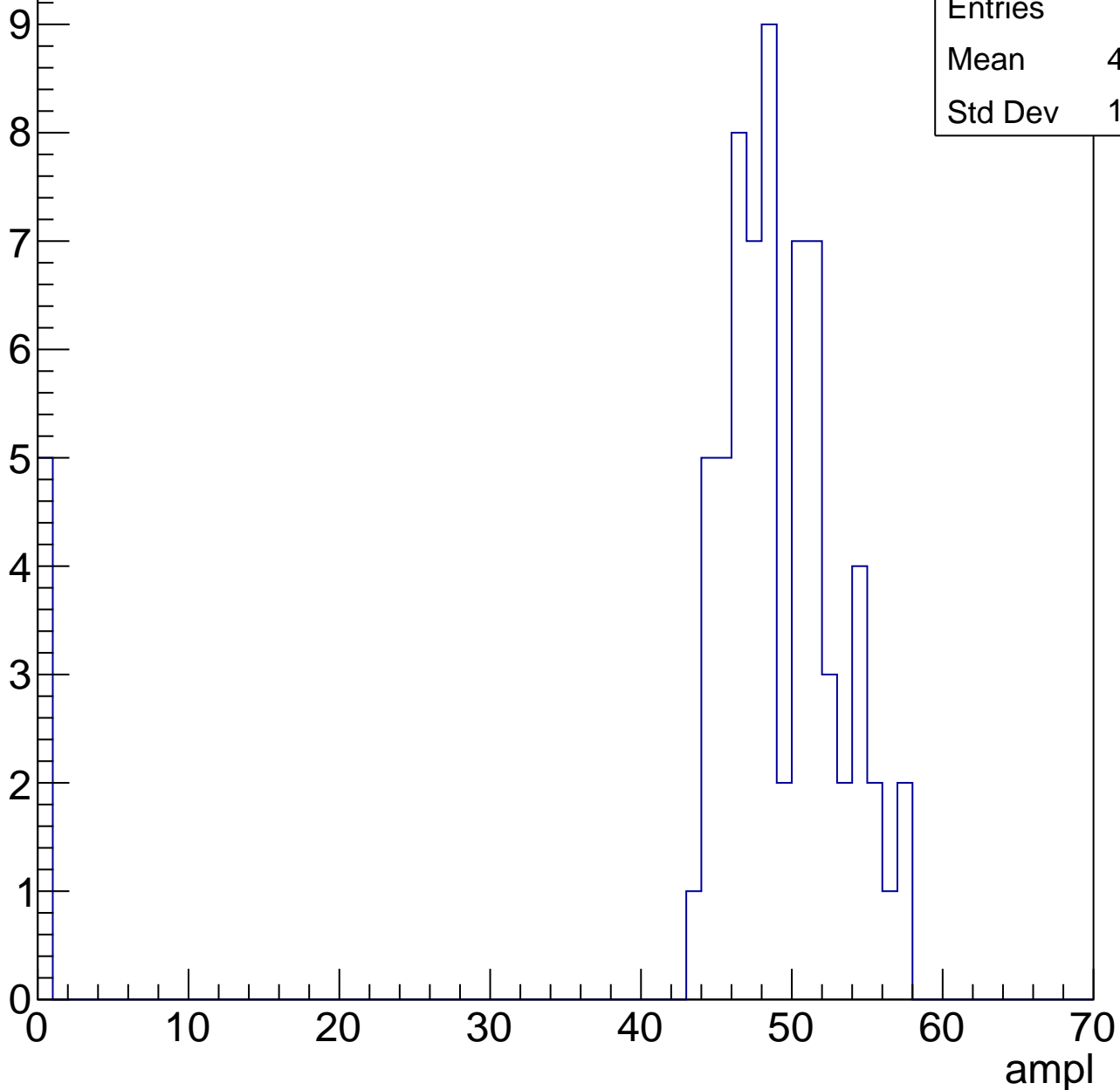


B1L103S, U13-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	45.43
Std Dev	13.04

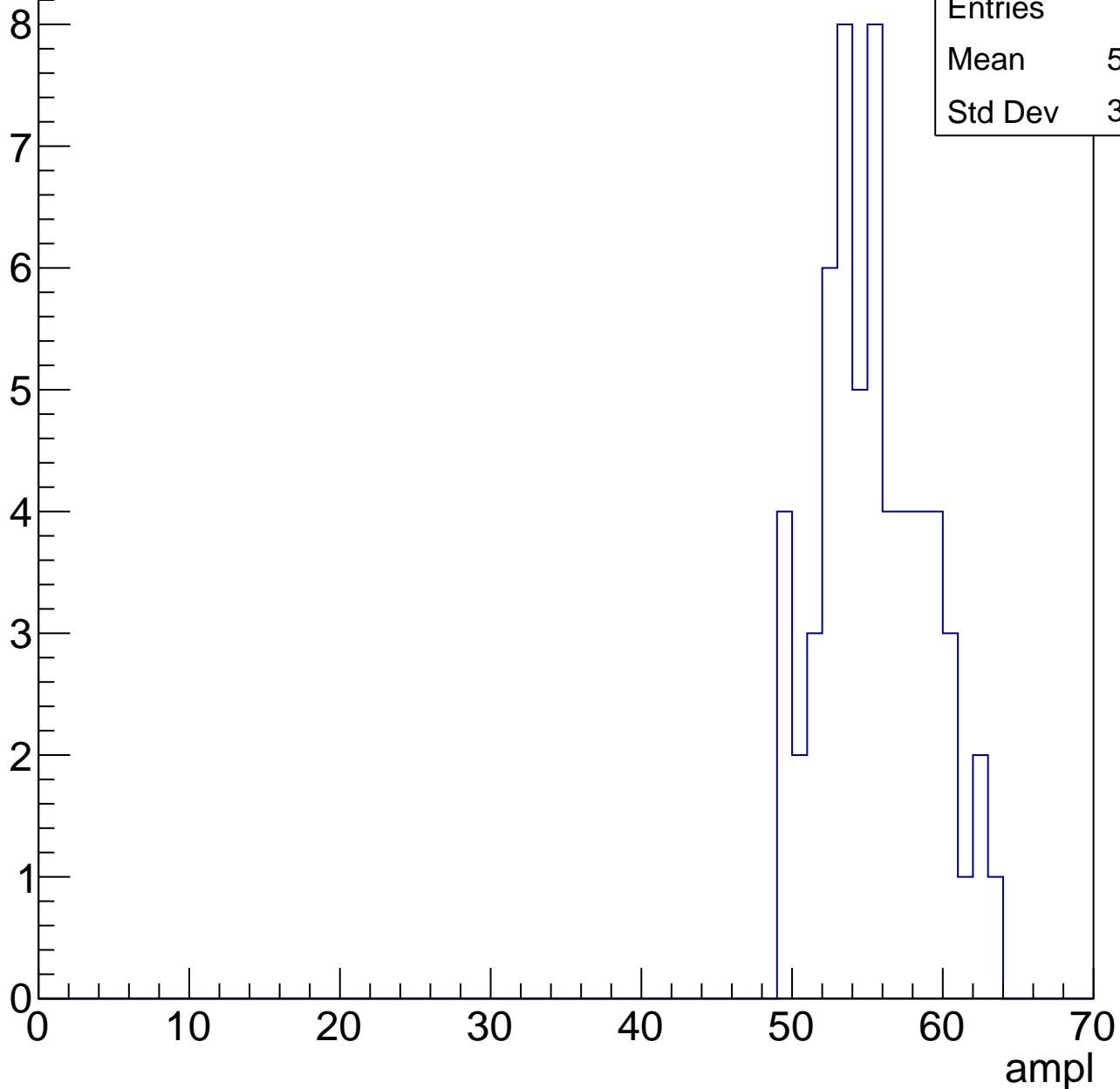


B1L103S, U13-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.97
Std Dev	3.508

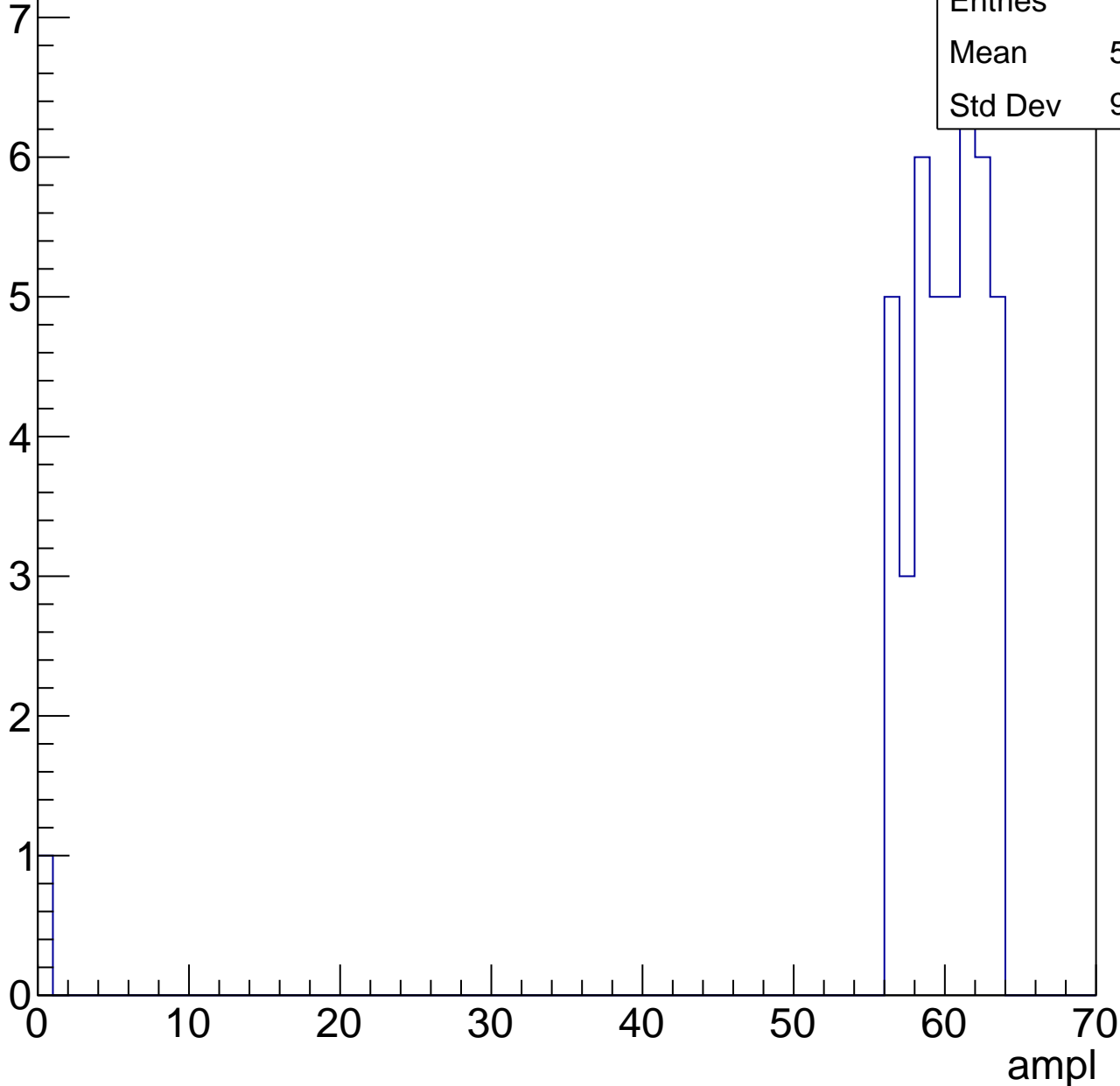


B1L103S, U13-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

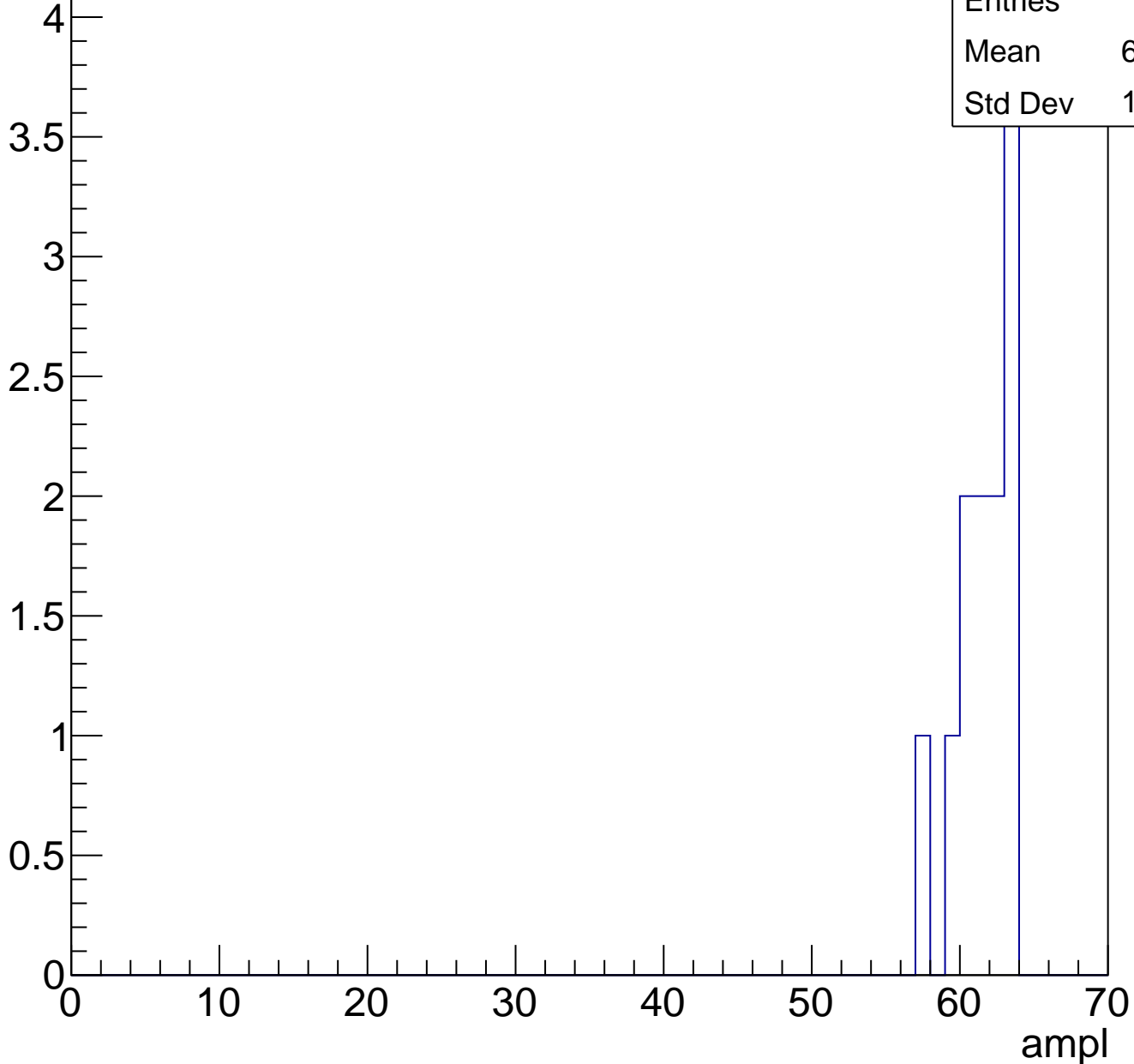
Entries	43
Mean	58.33
Std Dev	9.265



B1L103S, U13-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

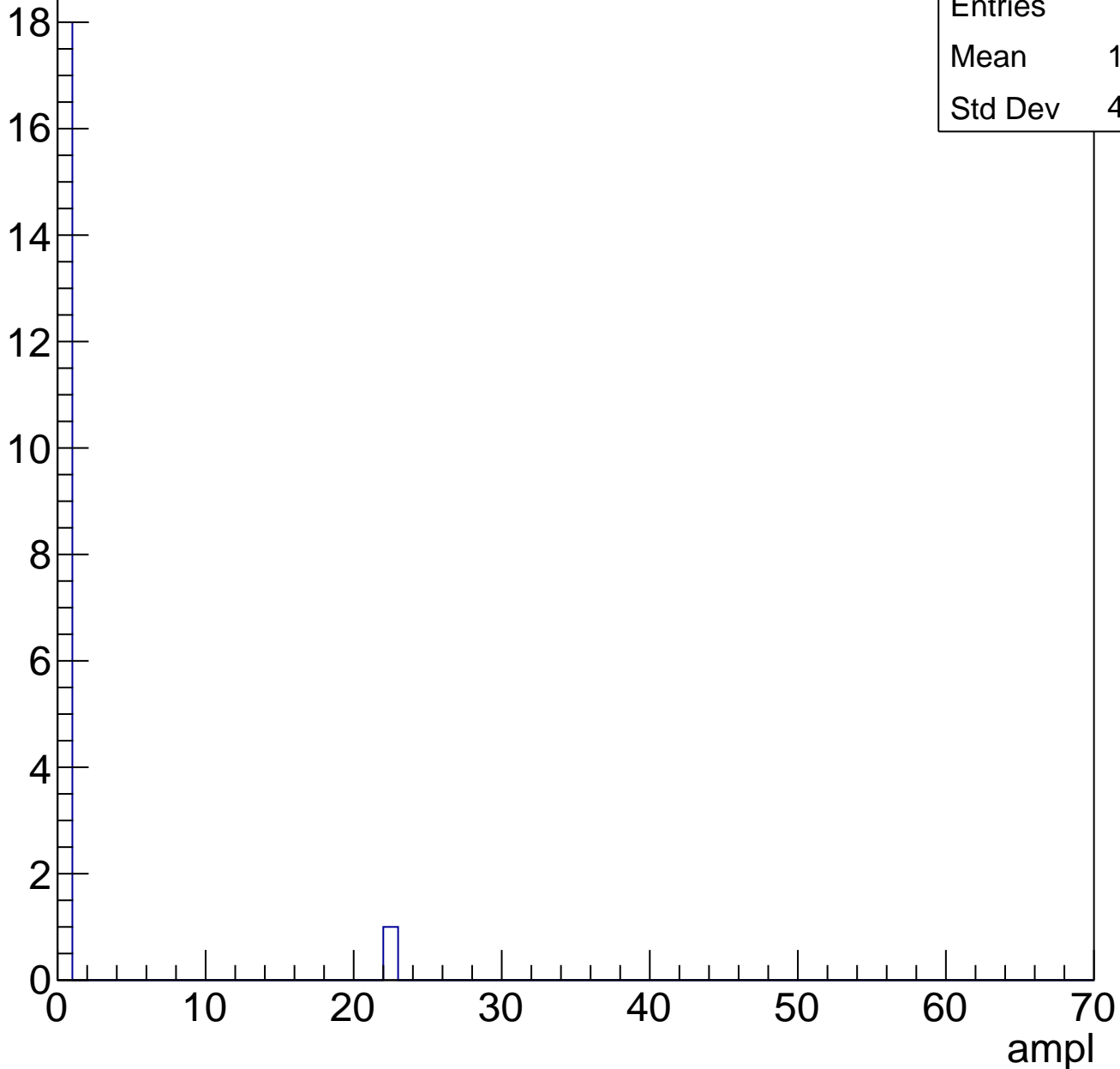


B1L103S, U13-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U13-ch115, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	25.64
Std Dev	9.685

Gaus mean : 29.4681

Gaus Width: 3.4232

Entry

10

8

6

4

2

0

0

10

20

30

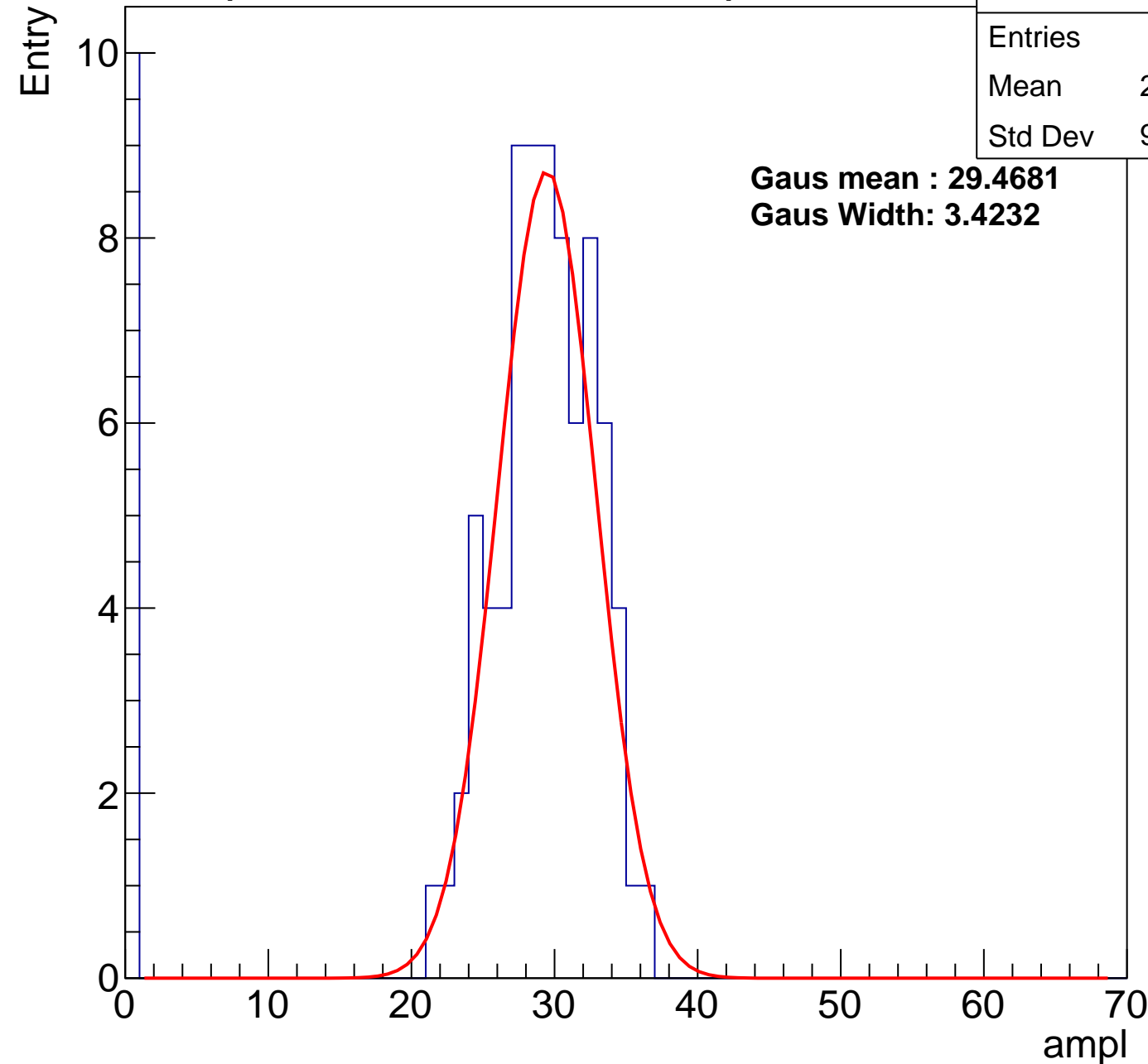
40

50

60

70

ampl



B1L103S, U13-ch115, adc1

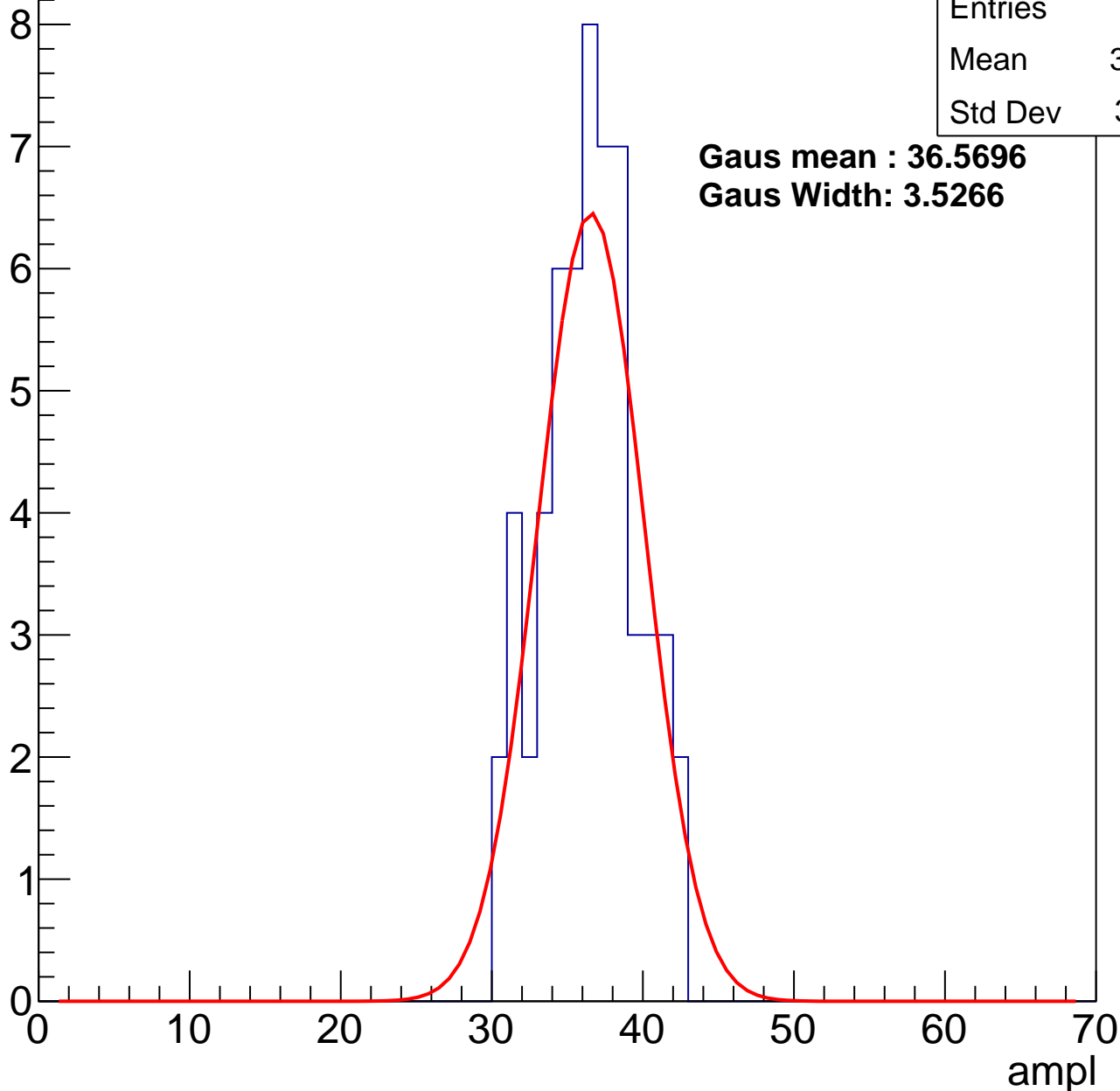
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	35.98
Std Dev	3.041

Gaus mean : 36.5696

Gaus Width: 3.5266



B1L103S, U13-ch115, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	37.29
Std Dev	15.01

Gaus mean : 43.2682

Gaus Width: 3.7478

Entry

12

10

8

6

4

2

0

0

10

20

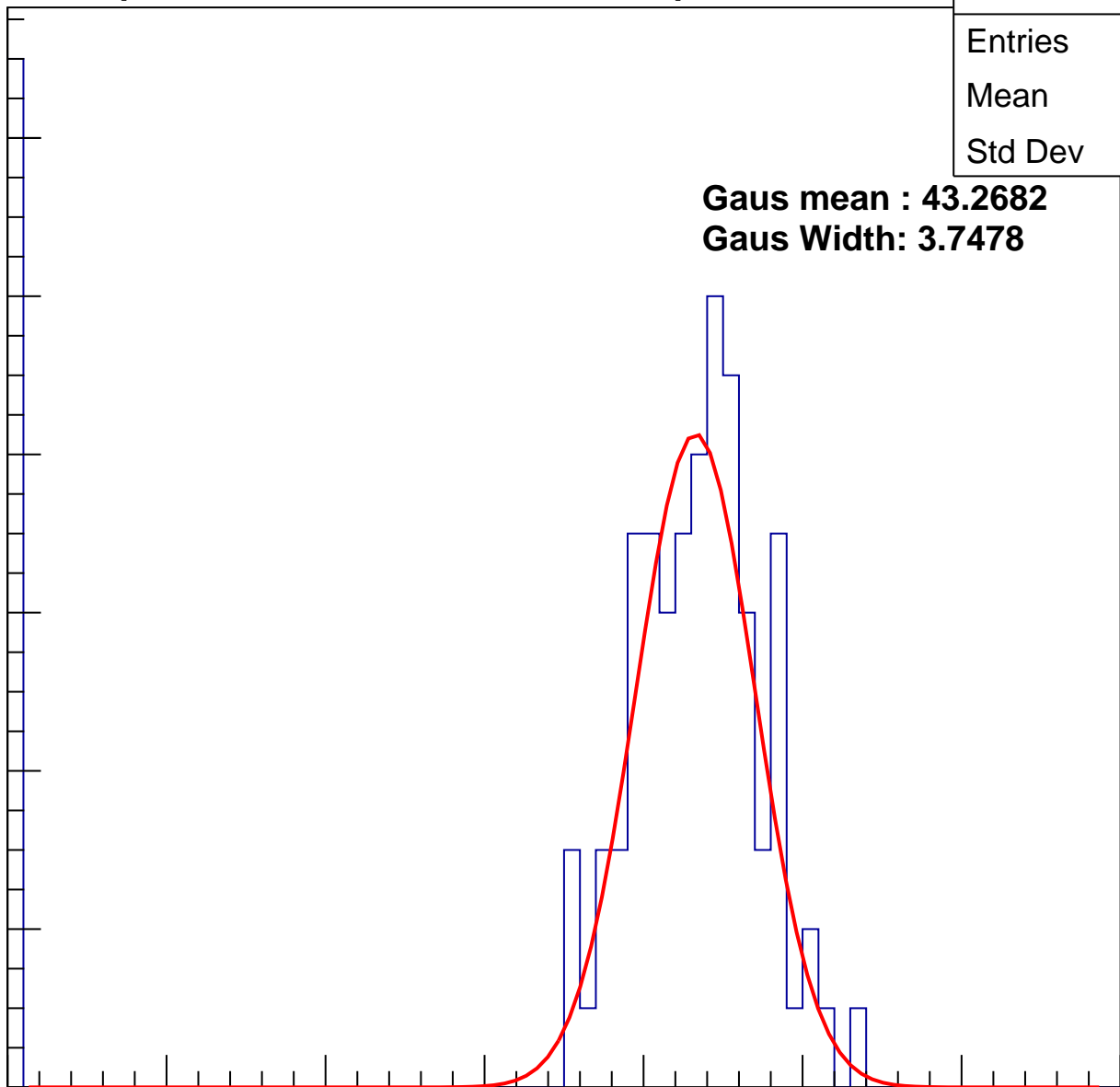
30

40

50

60

ampl

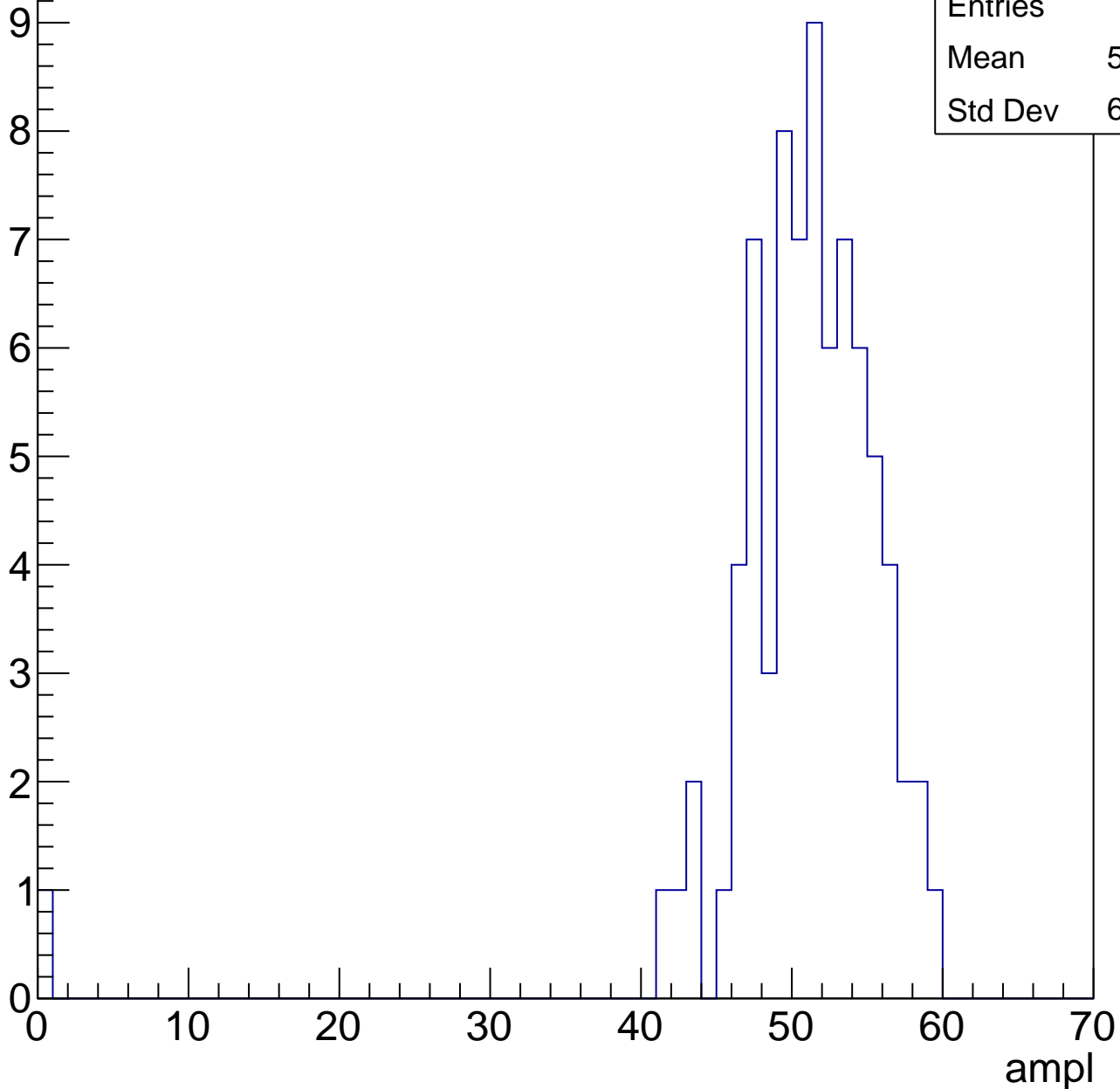


B1L103S, U13-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	50.22
Std Dev	6.912

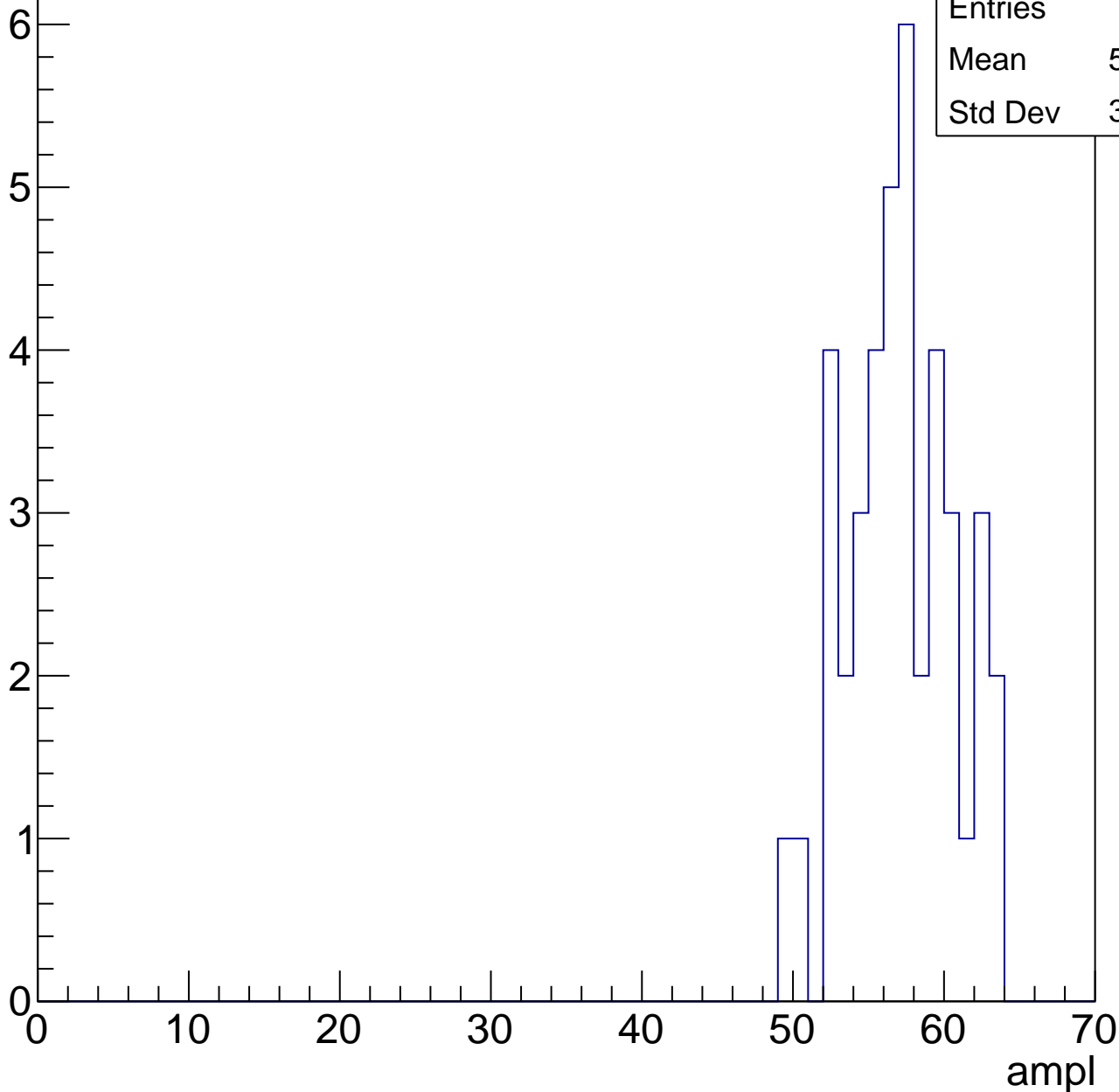


B1L103S, U13-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	56.63
Std Dev	3.476

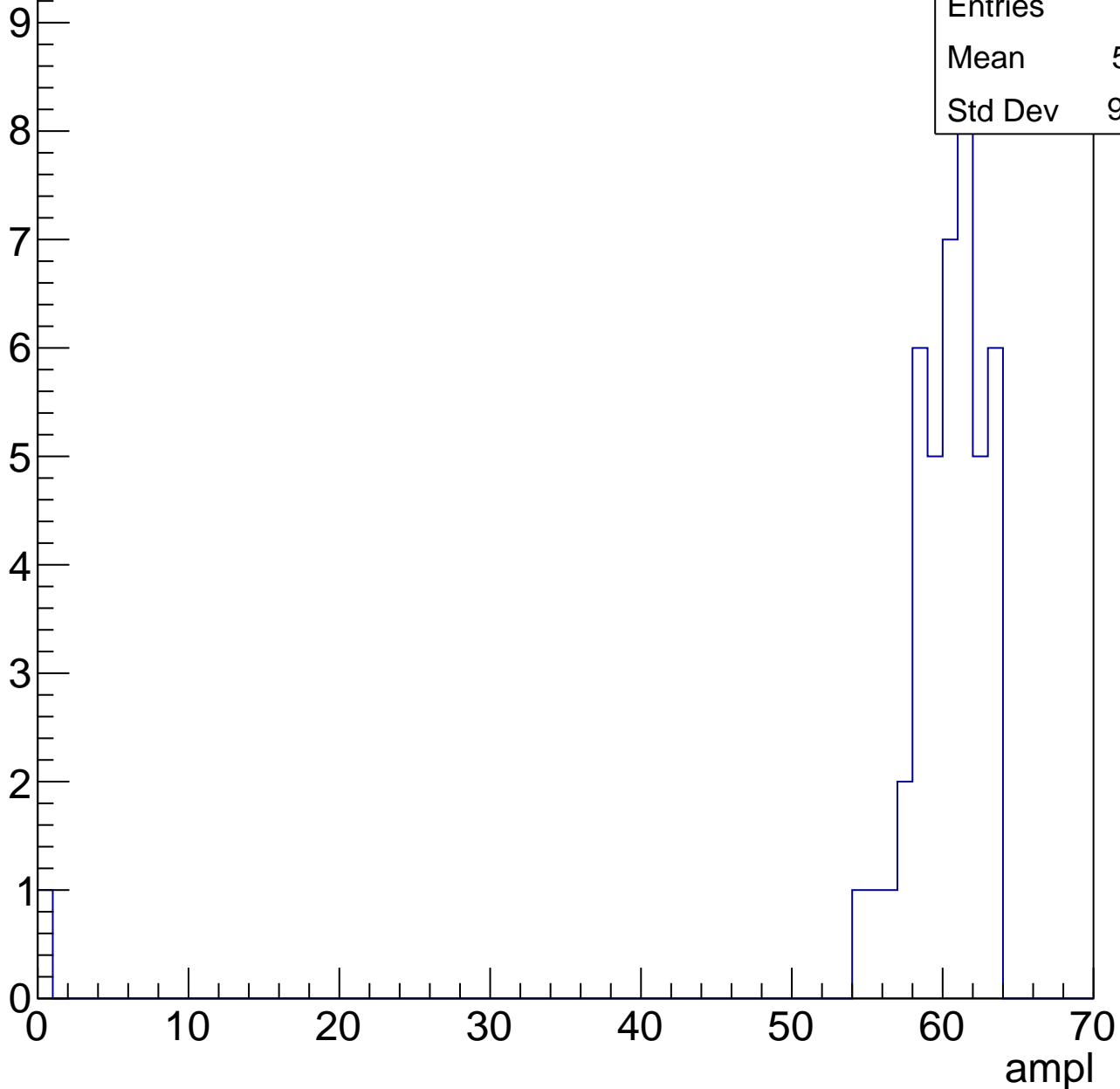


B1L103S, U13-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	58.61
Std Dev	9.198



B1L103S, U13-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

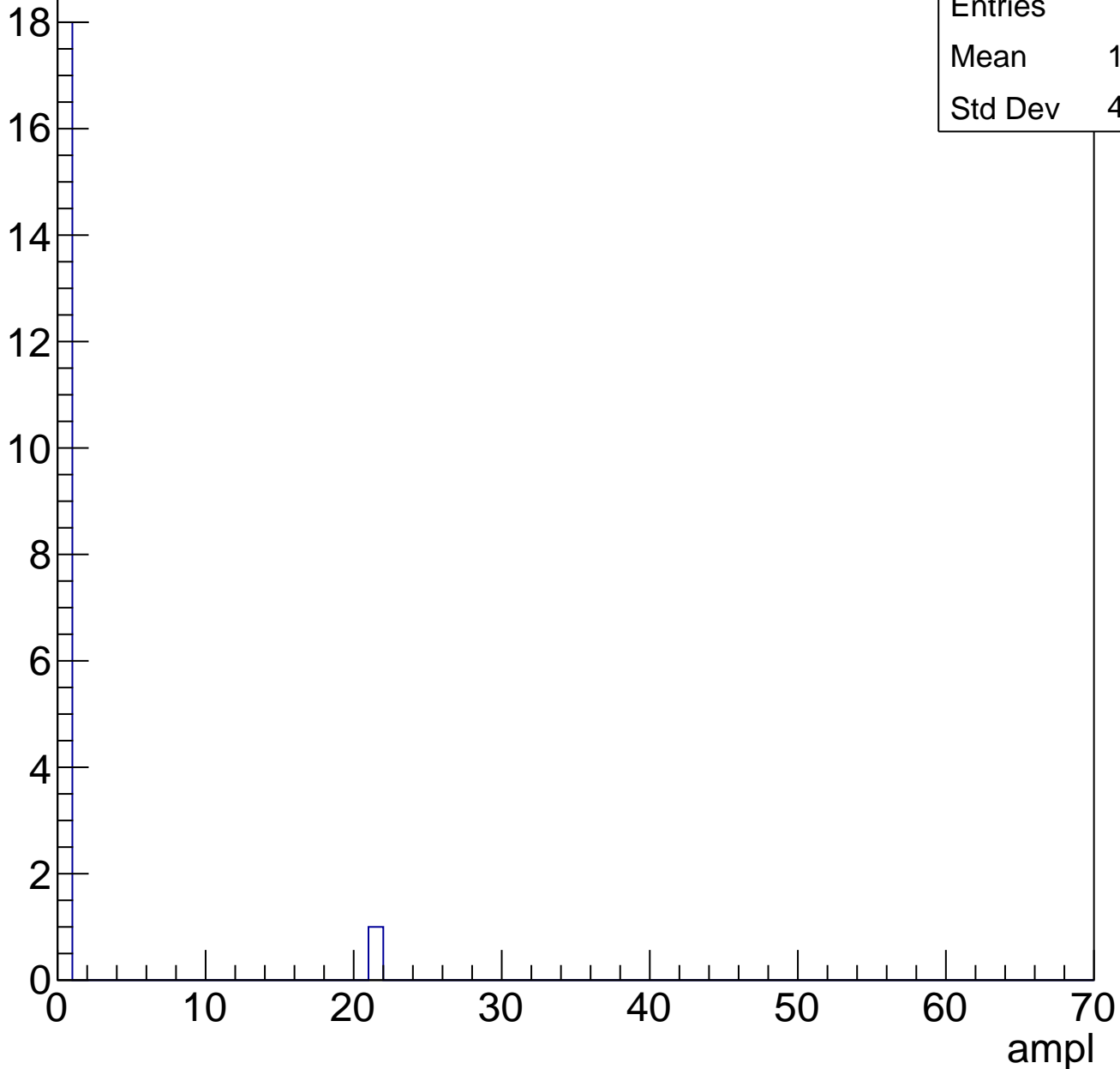
ampl

B1L103S, U13-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U13-ch116, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	22.22
Std Dev	12.17

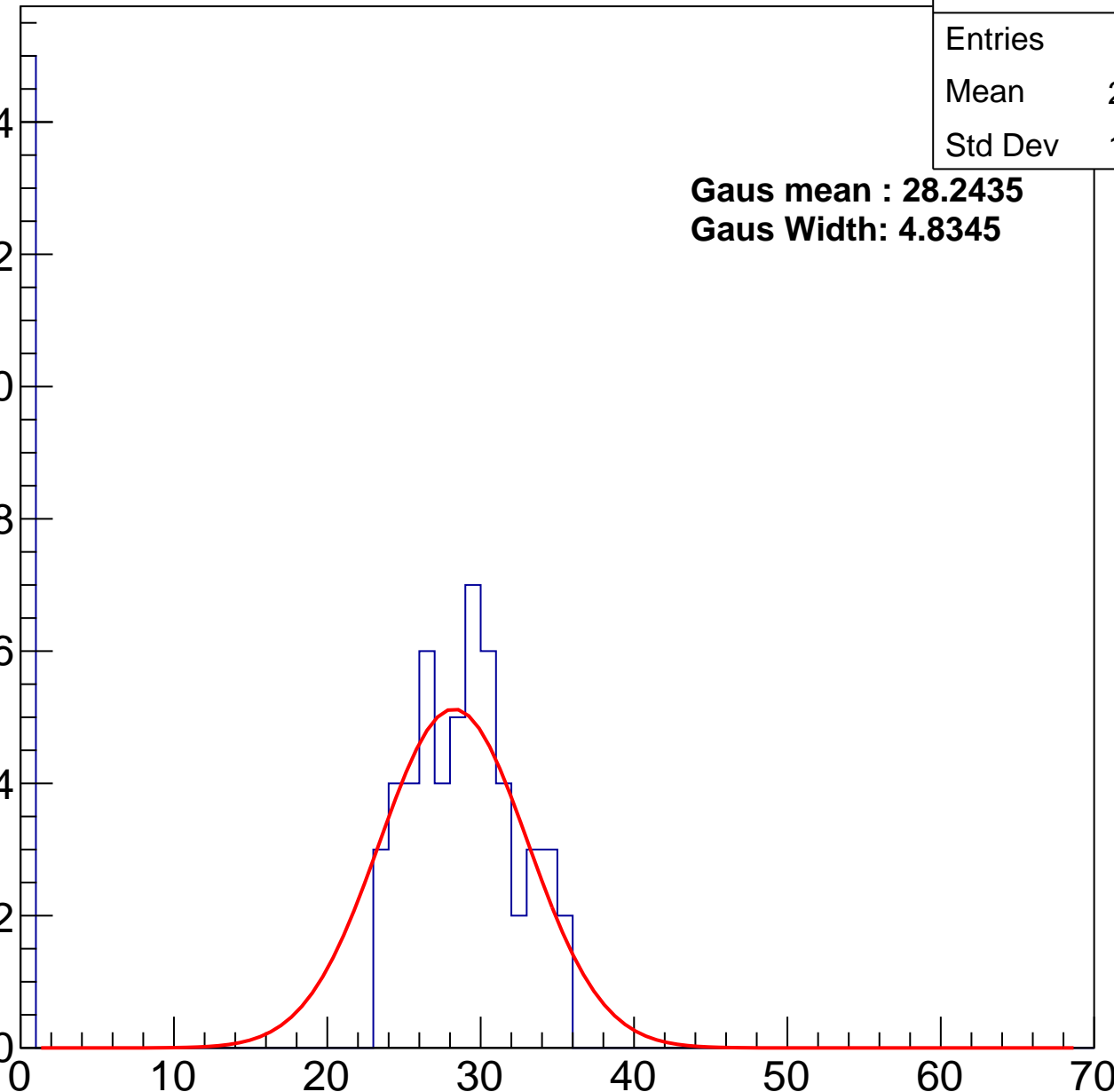
Gaus mean : 28.2435

Gaus Width: 4.8345

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch116, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	30.46
Std Dev	12.54

Gaus mean : 35.8880

Gaus Width: 4.4543

Entry

12

10

8

6

4

2

0

0

10

20

30

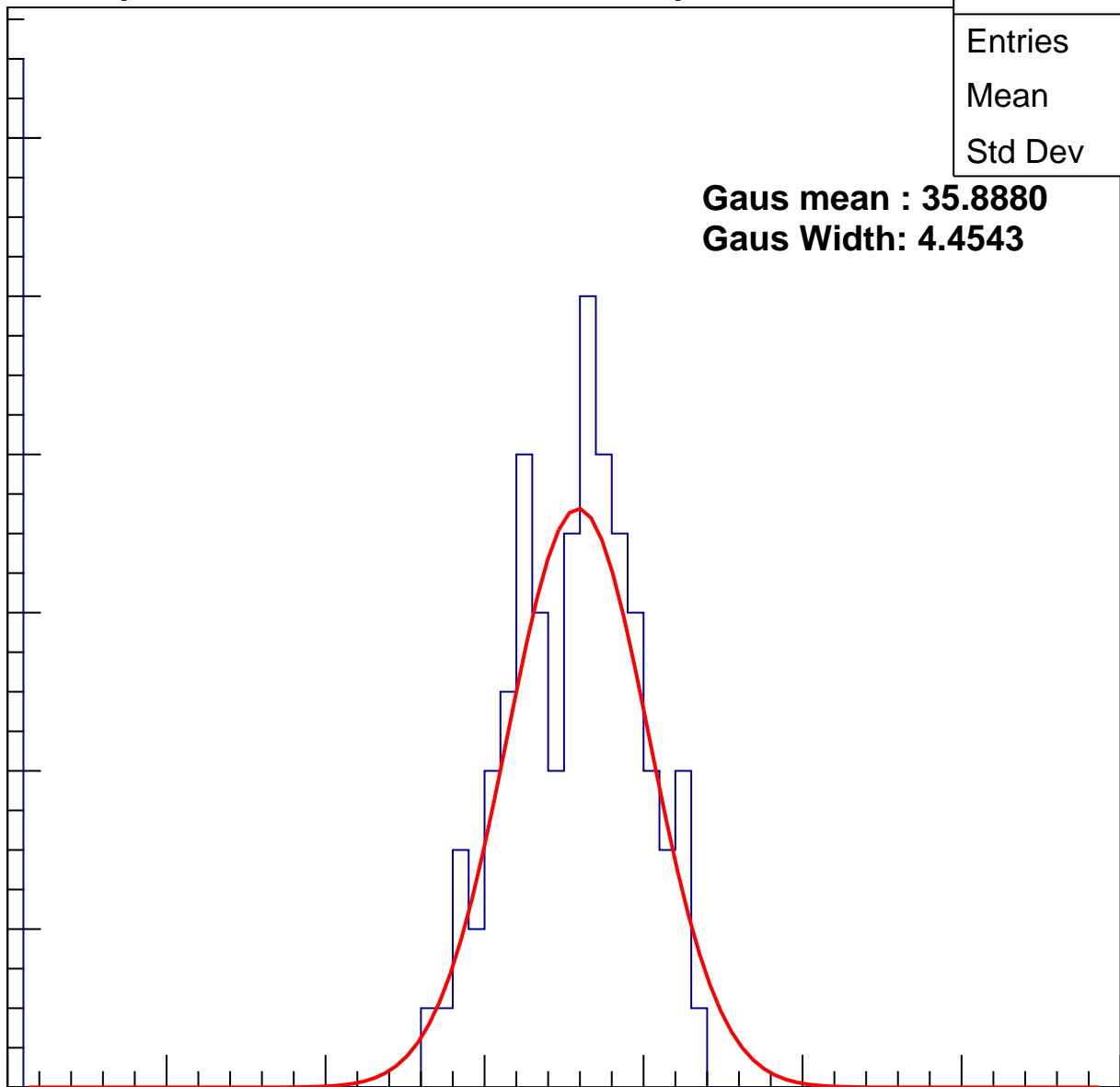
40

50

60

70

ampl



B1L103S, U13-ch116, adc2

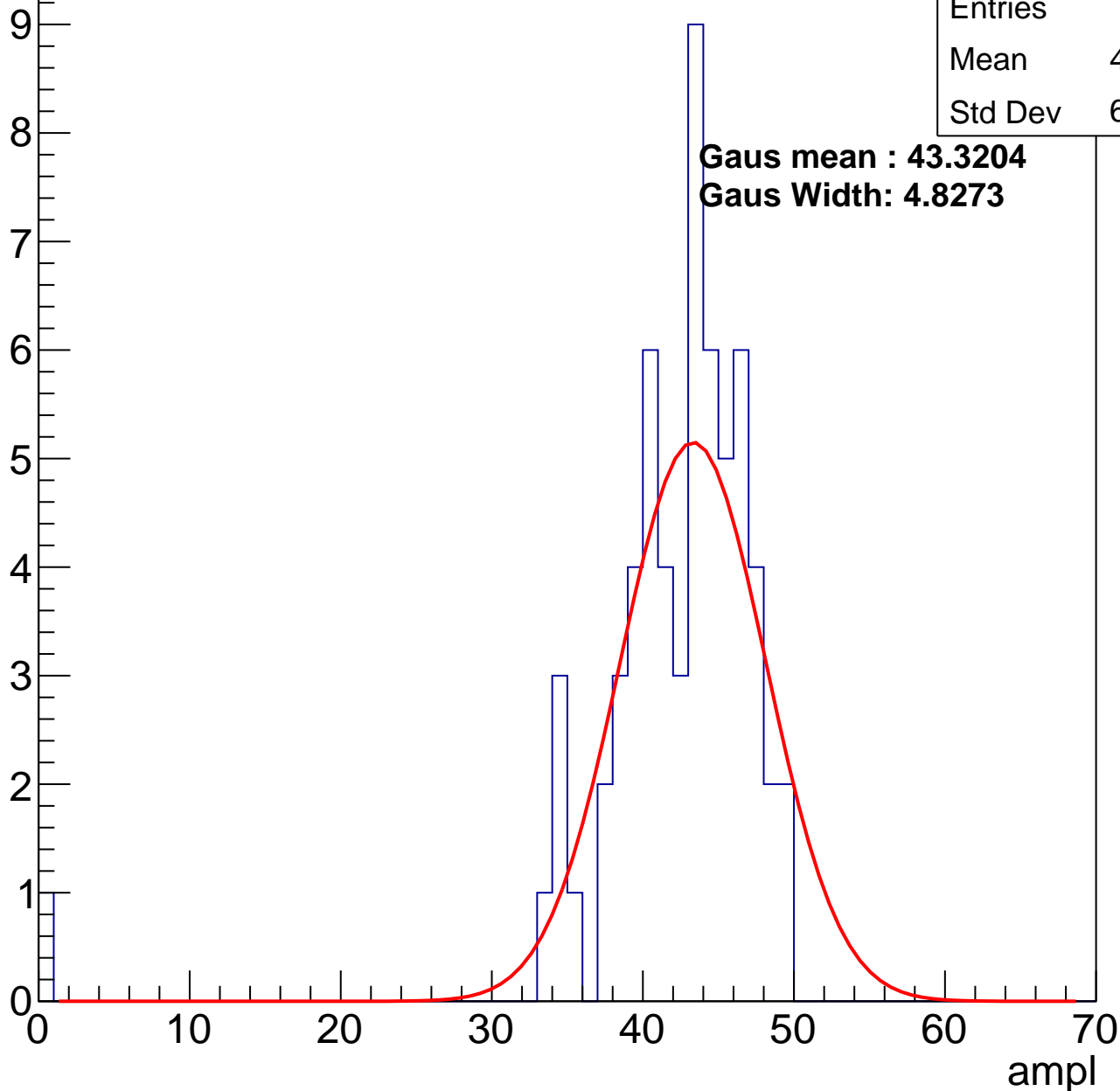
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	41.58
Std Dev	6.576

Gaus mean : 43.3204

Gaus Width: 4.8273

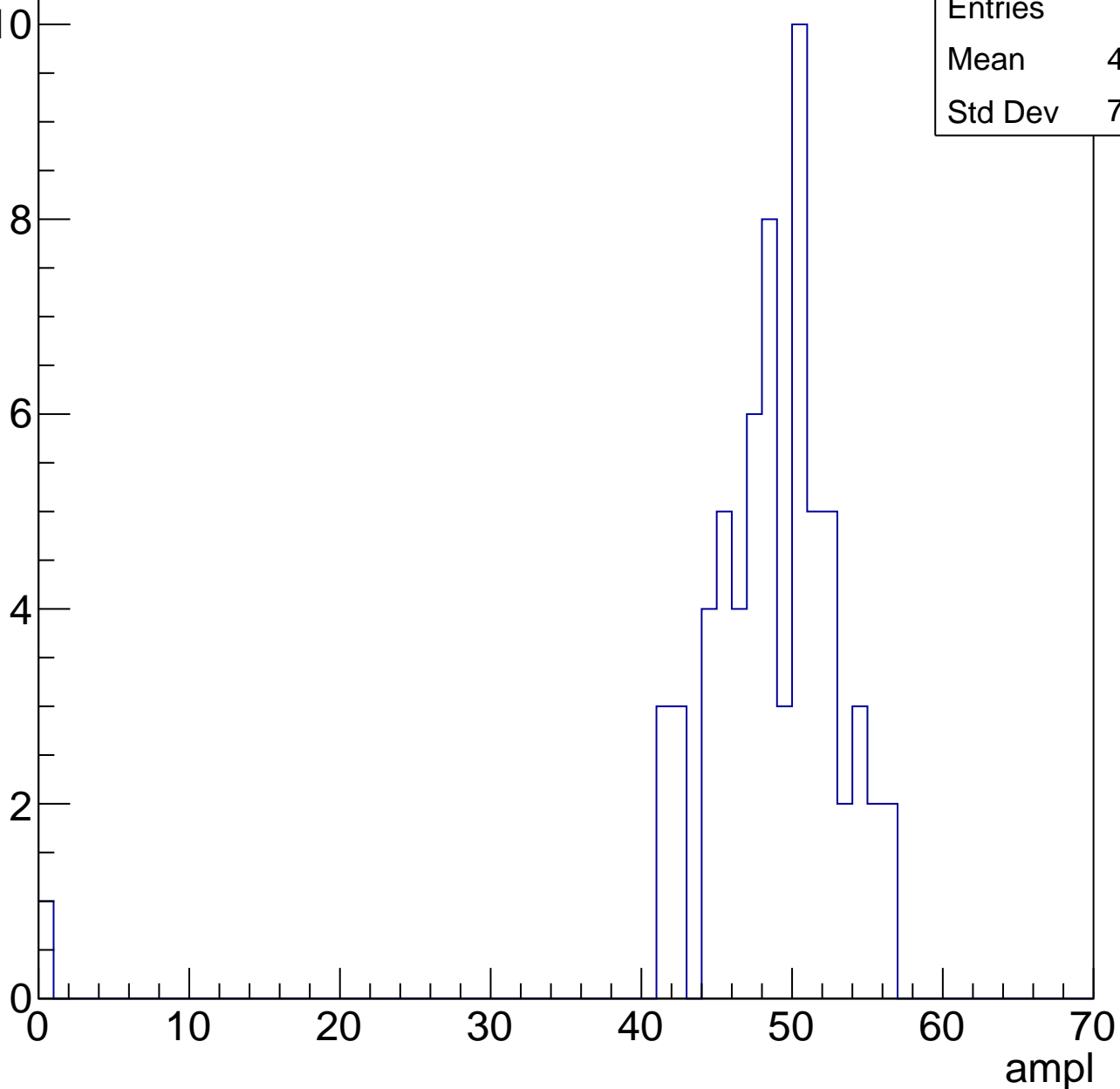


B1L103S, U13-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	47.76
Std Dev	7.004

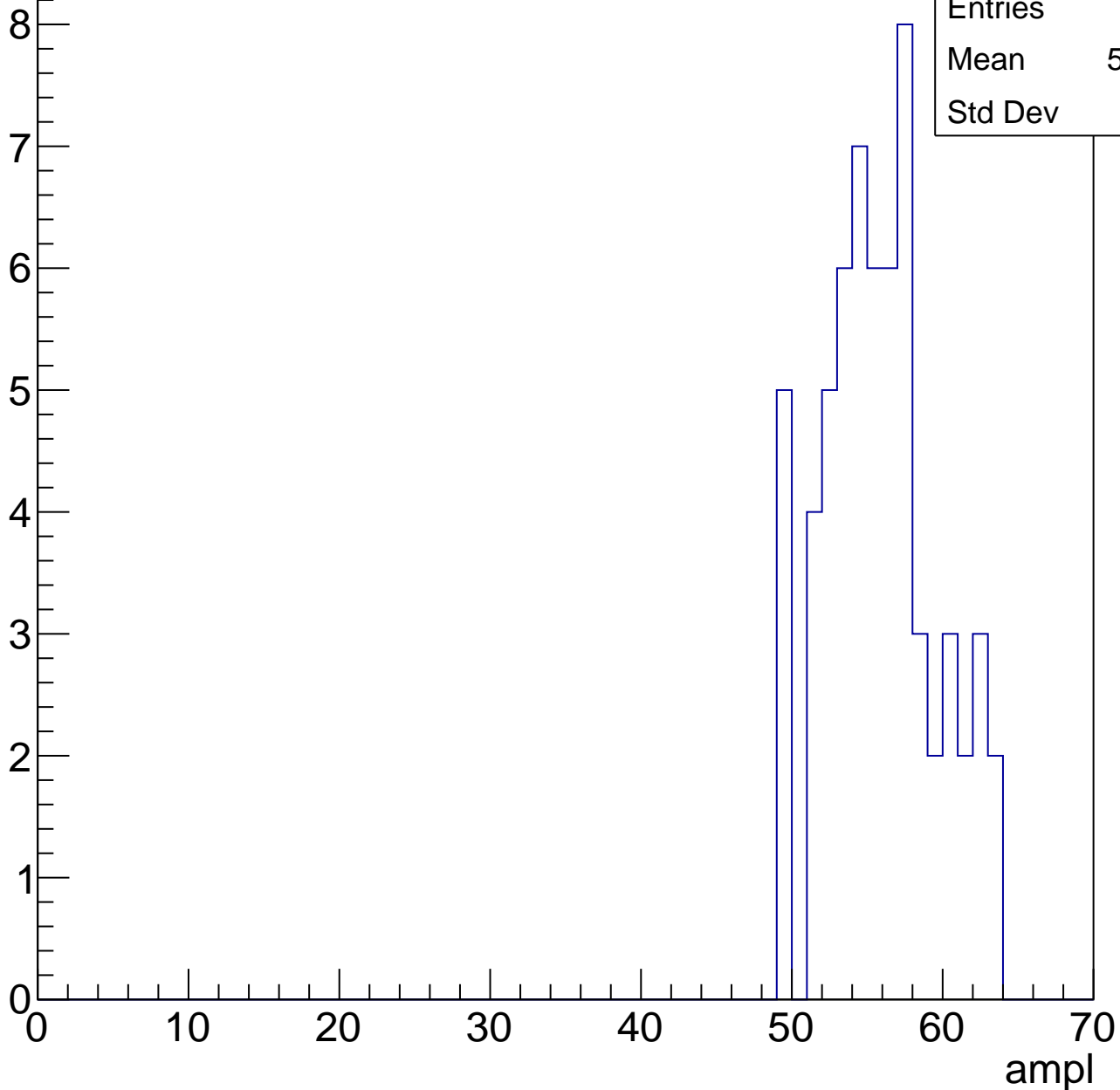


B1L103S, U13-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	55.37
Std Dev	3.66

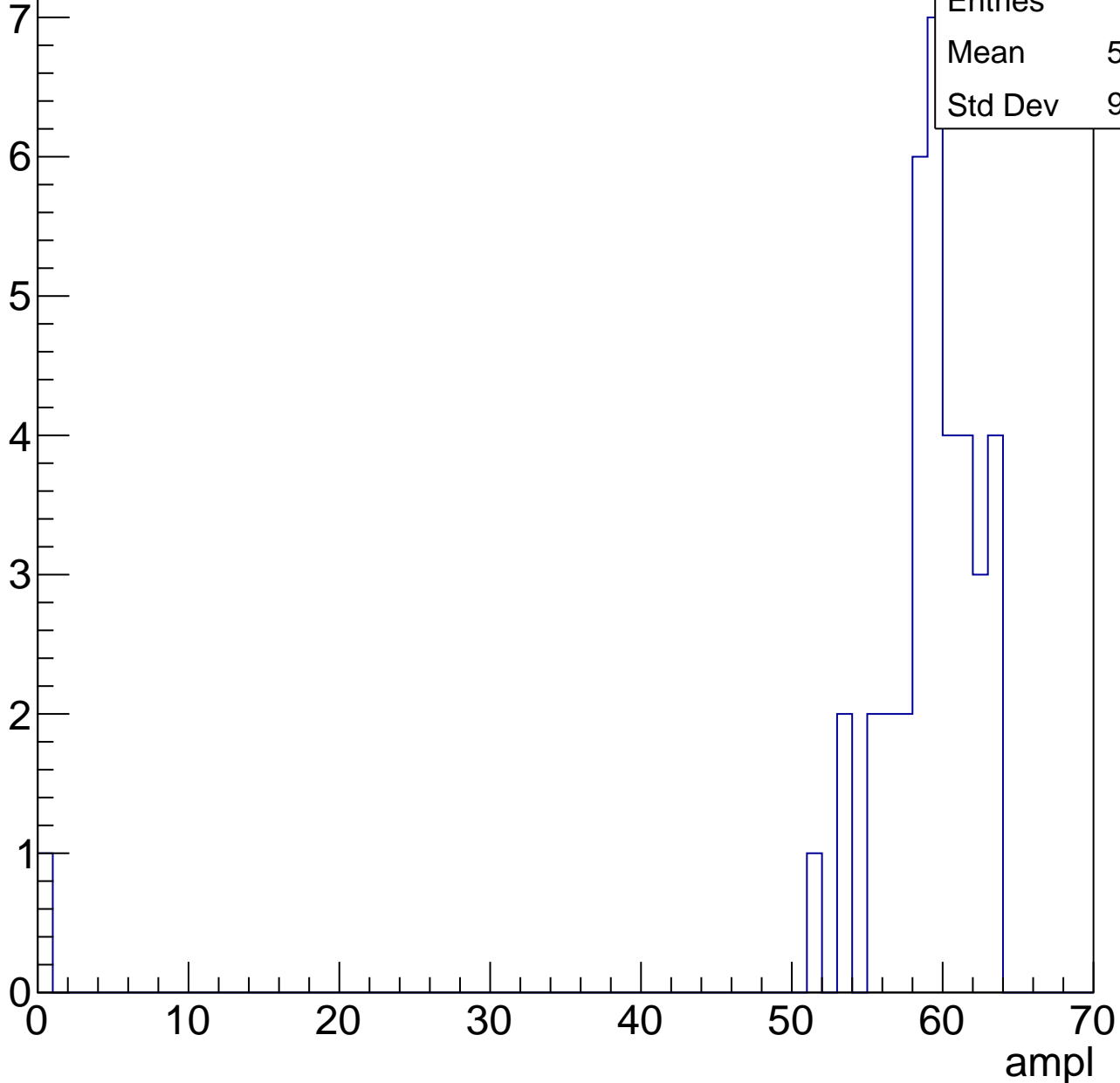


B1L103S, U13-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

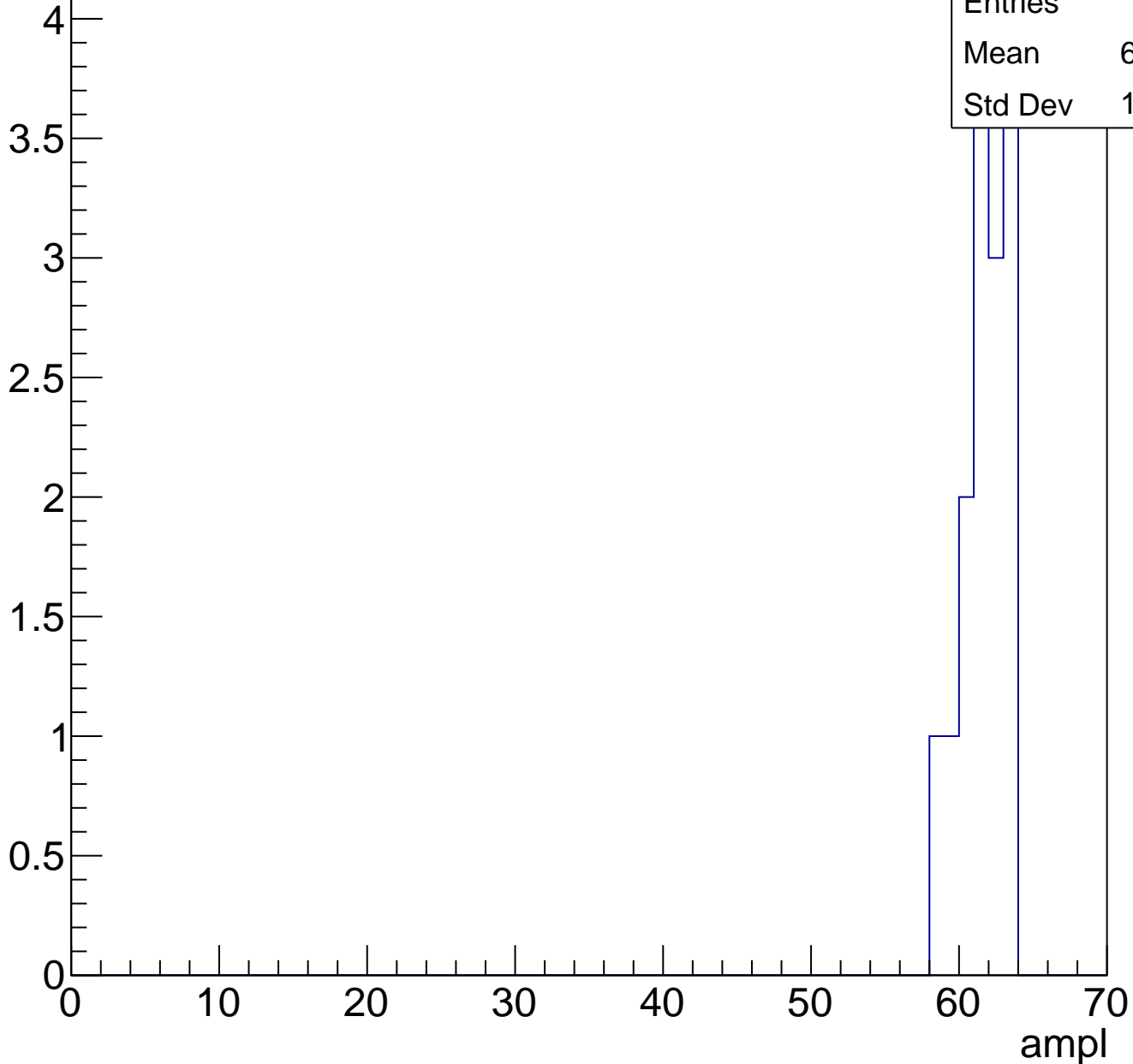
Entries	38
Mean	57.26
Std Dev	9.837



B1L103S, U13-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

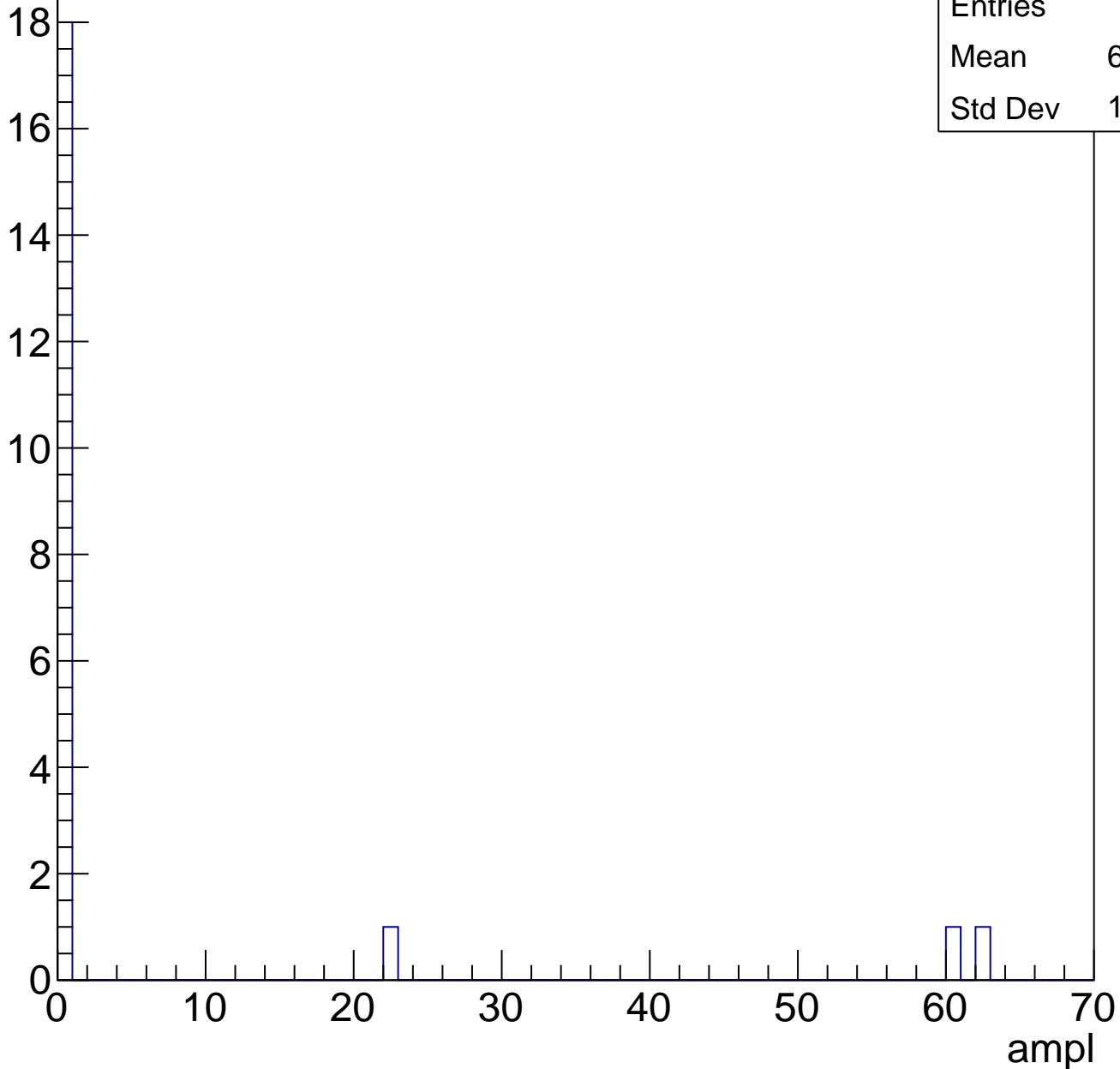


B1L103S, U13-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.857
Std Dev	18.18

Entry



B1L103S, U13-ch117, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	24.66
Std Dev	11.59

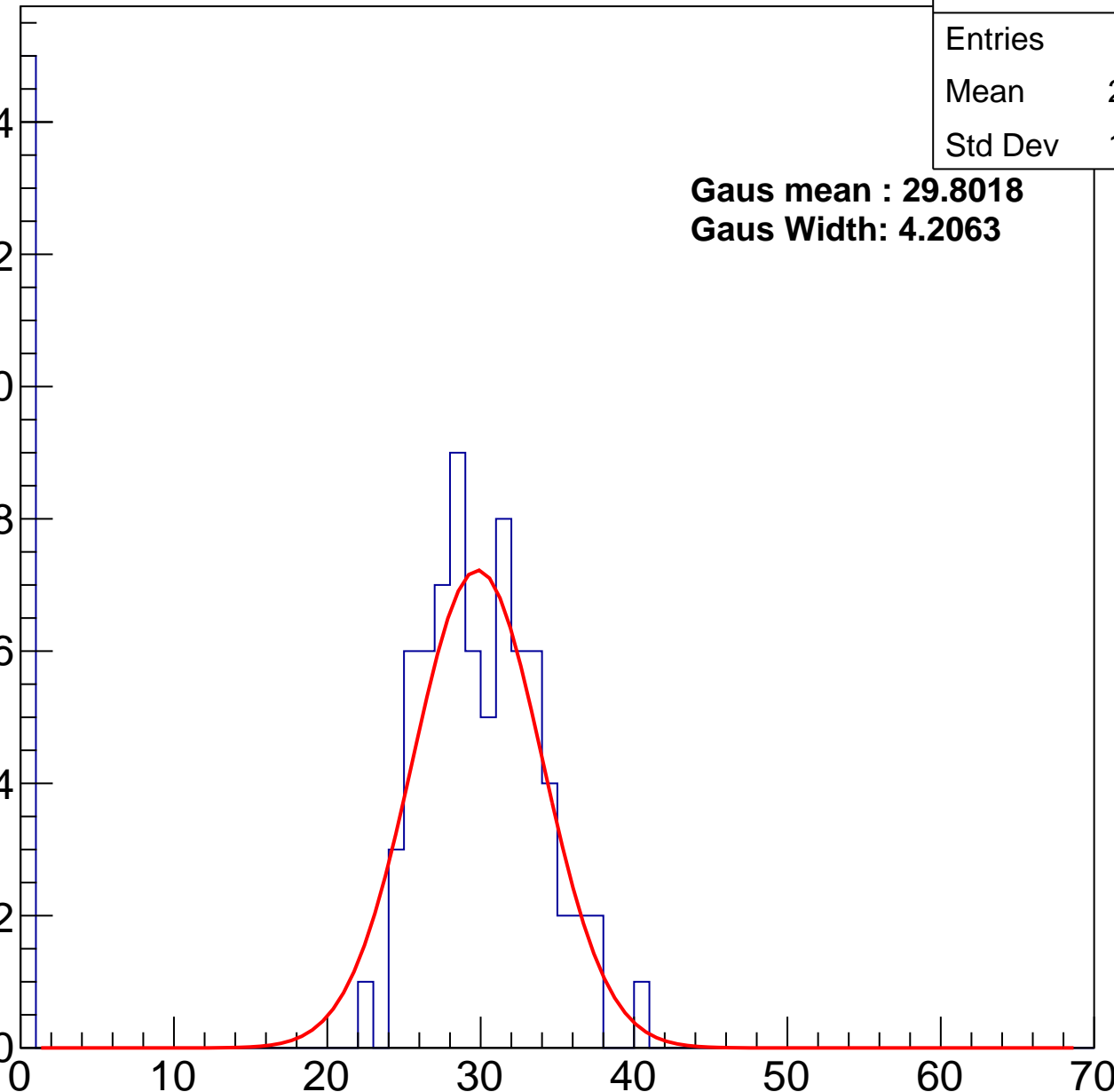
Gaus mean : 29.8018

Gaus Width: 4.2063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch117, adc1

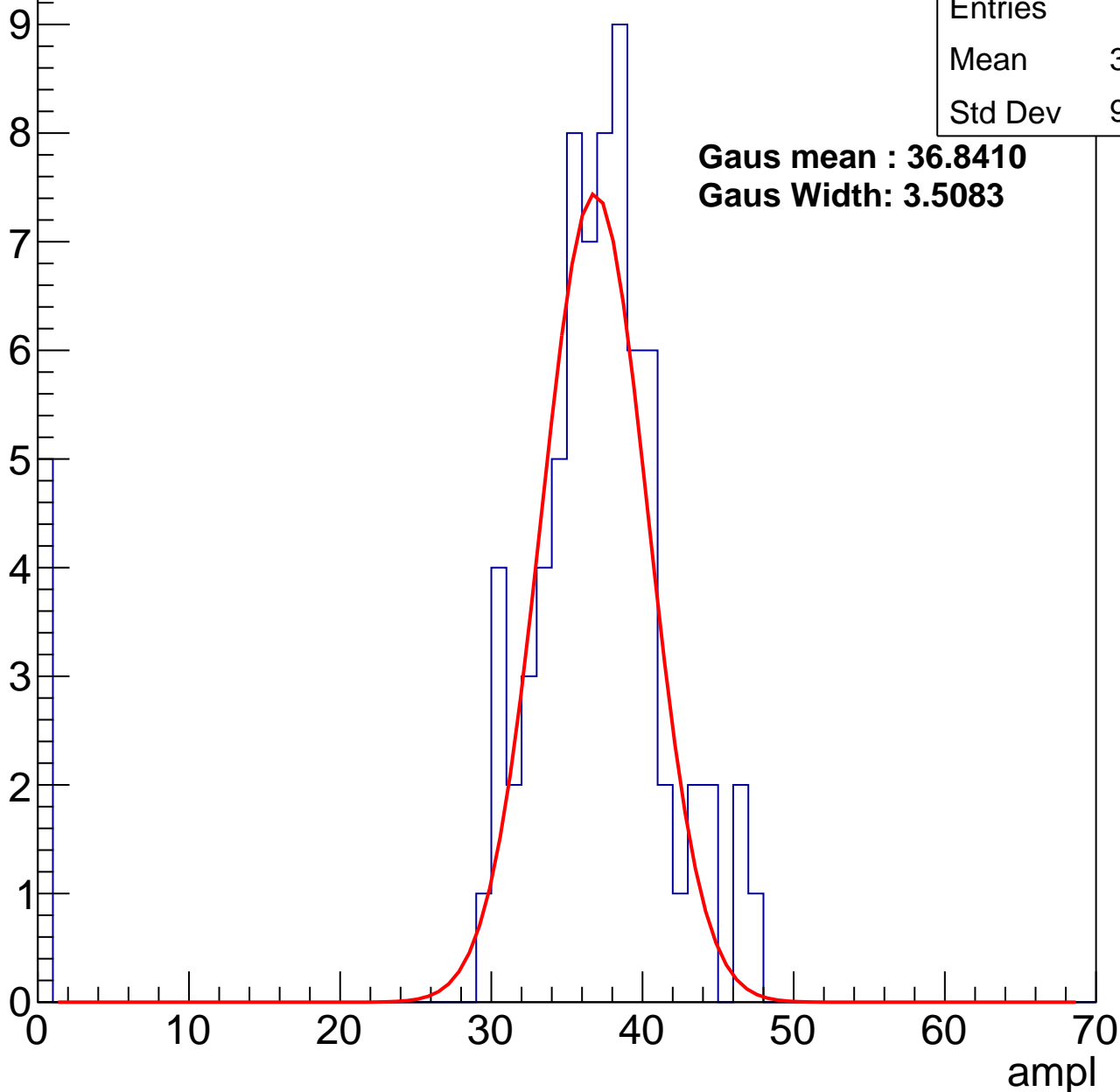
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.49
Std Dev	9.795

Gaus mean : 36.8410

Gaus Width: 3.5083



B1L103S, U13-ch117, adc2

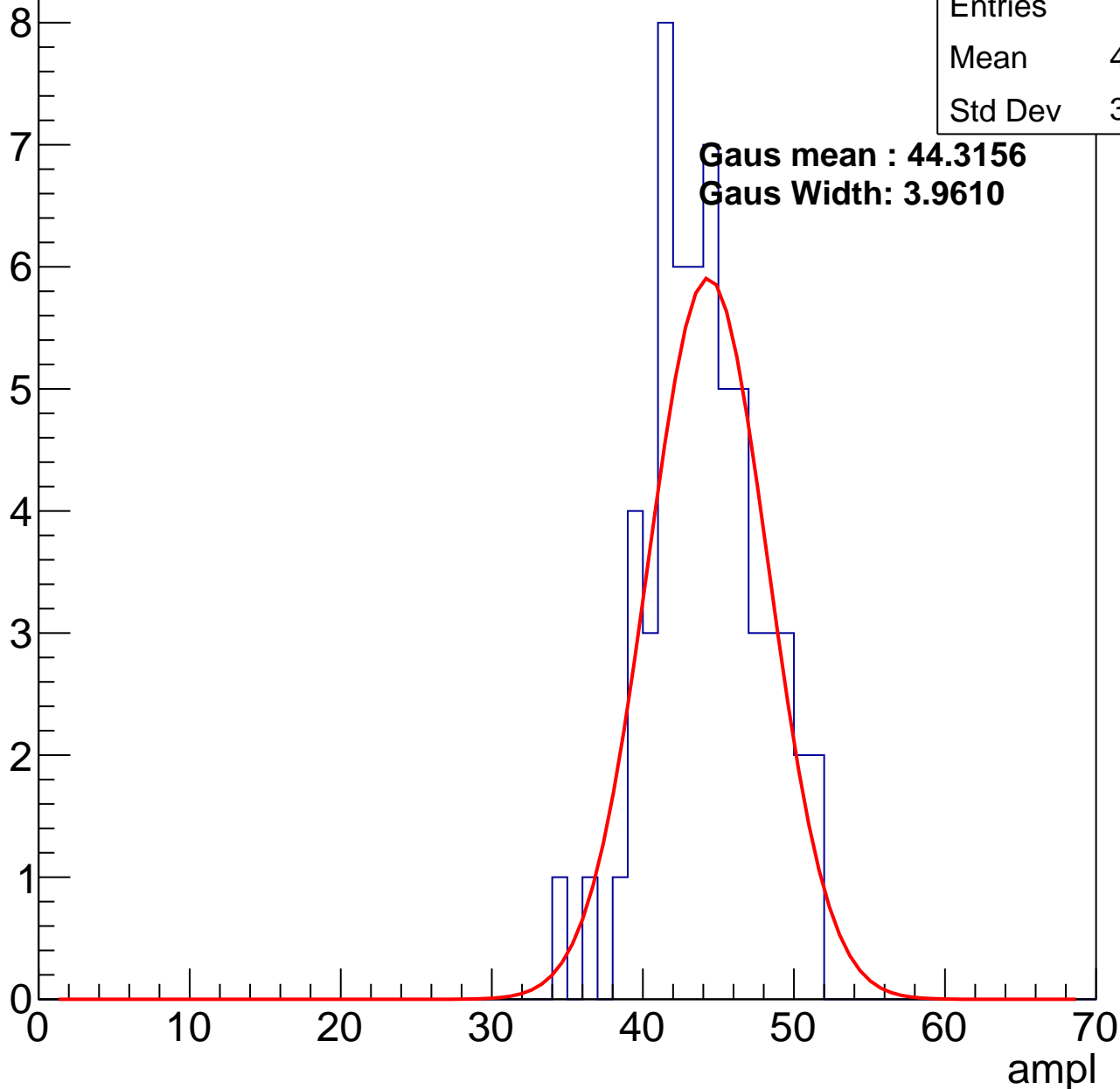
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	43.65
Std Dev	3.623

Gaus mean : 44.3156

Gaus Width: 3.9610

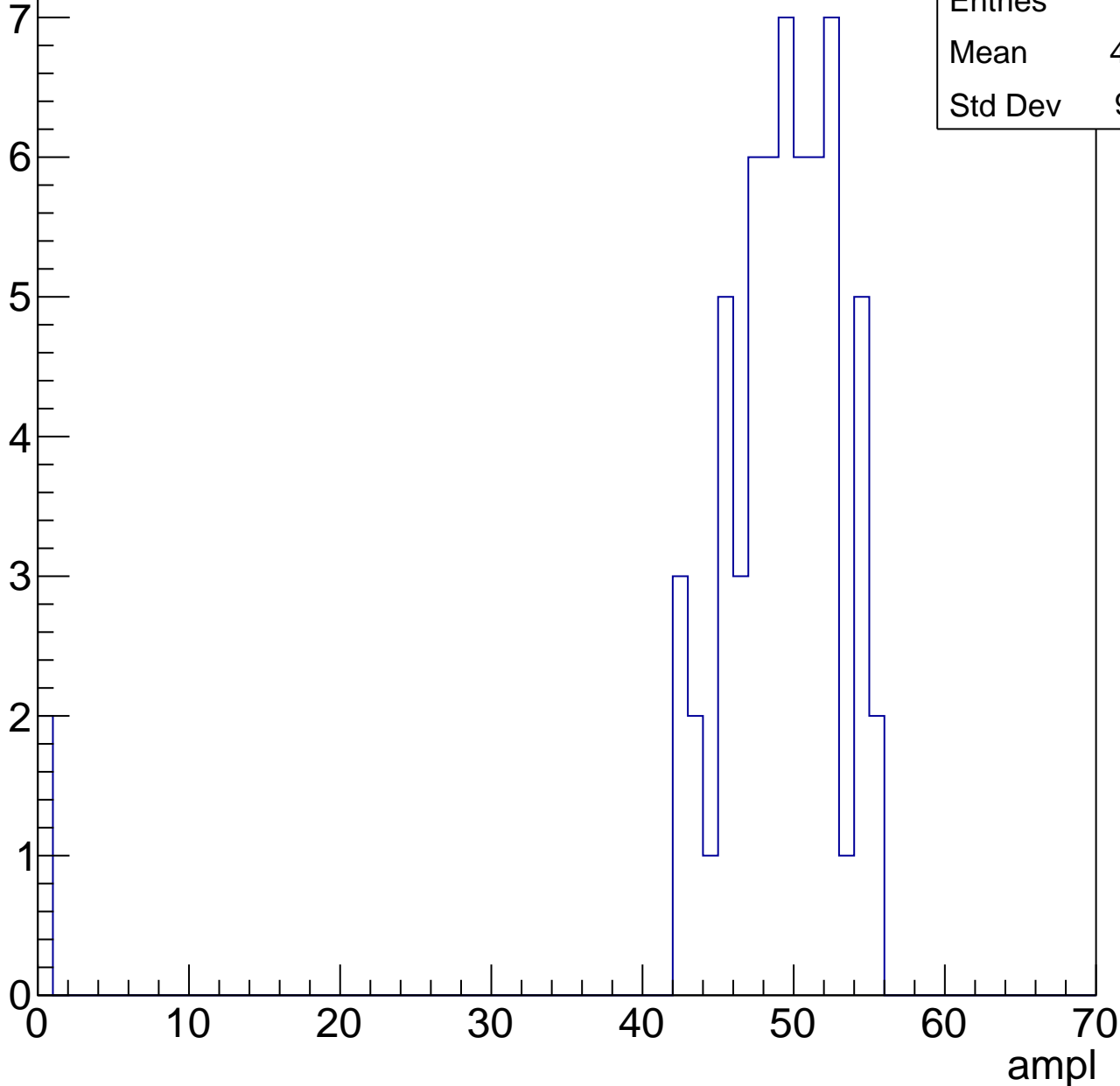


B1L103S, U13-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	47.34
Std Dev	9.261



B1L103S, U13-ch117, adc4

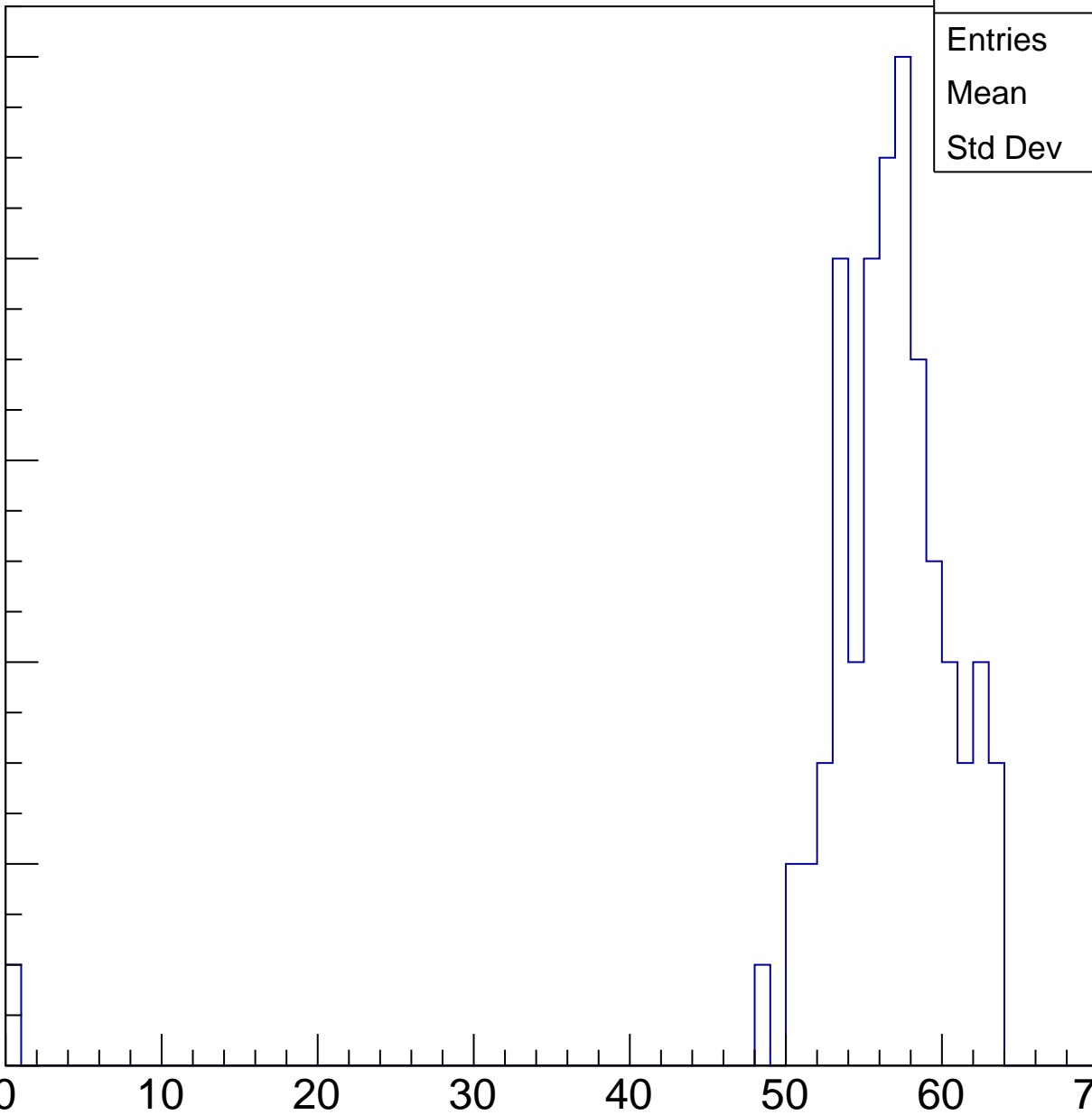
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	55.69
Std Dev	7.326

Entry

10
8
6
4
2
0

ampl

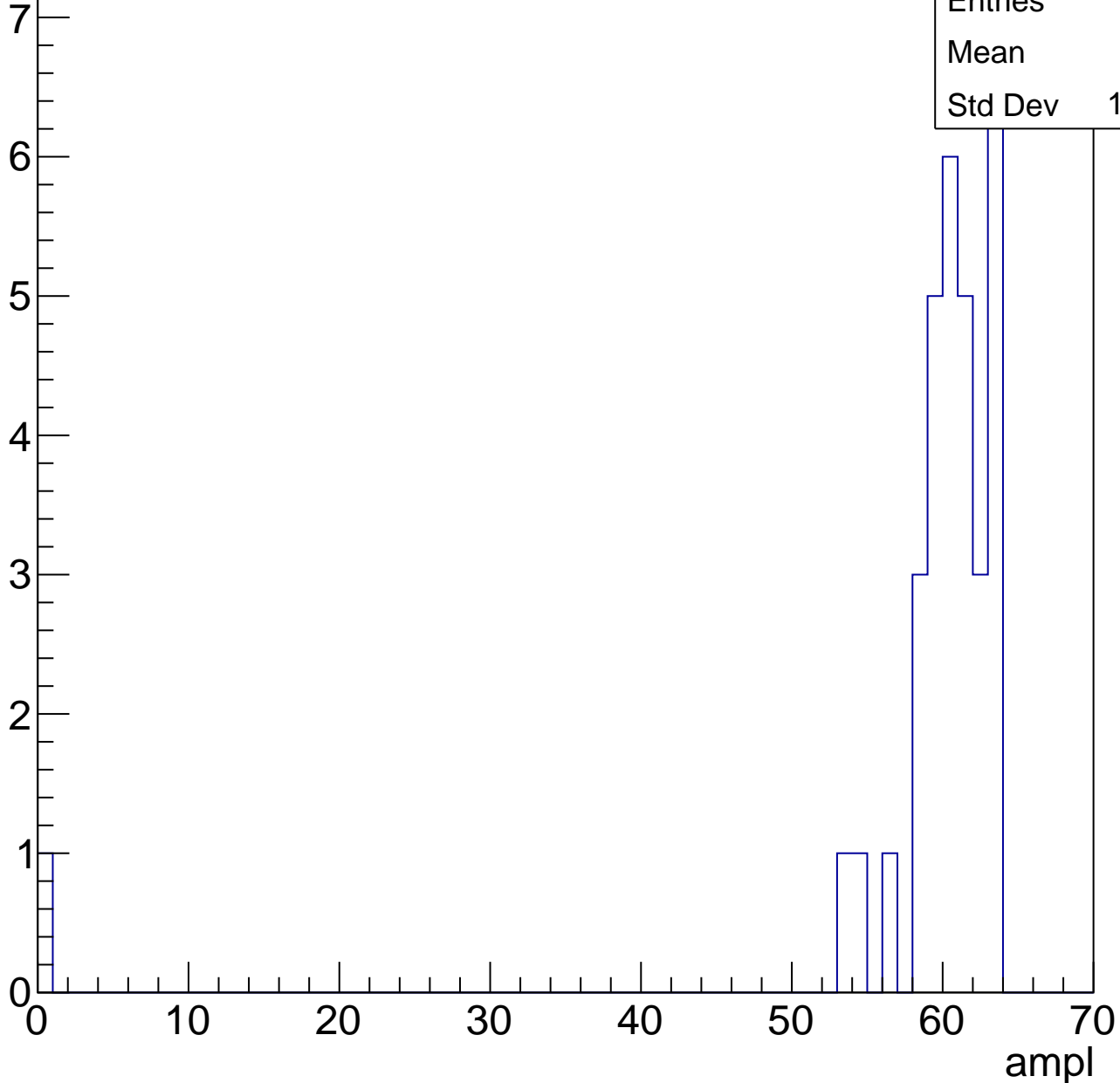


B1L103S, U13-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

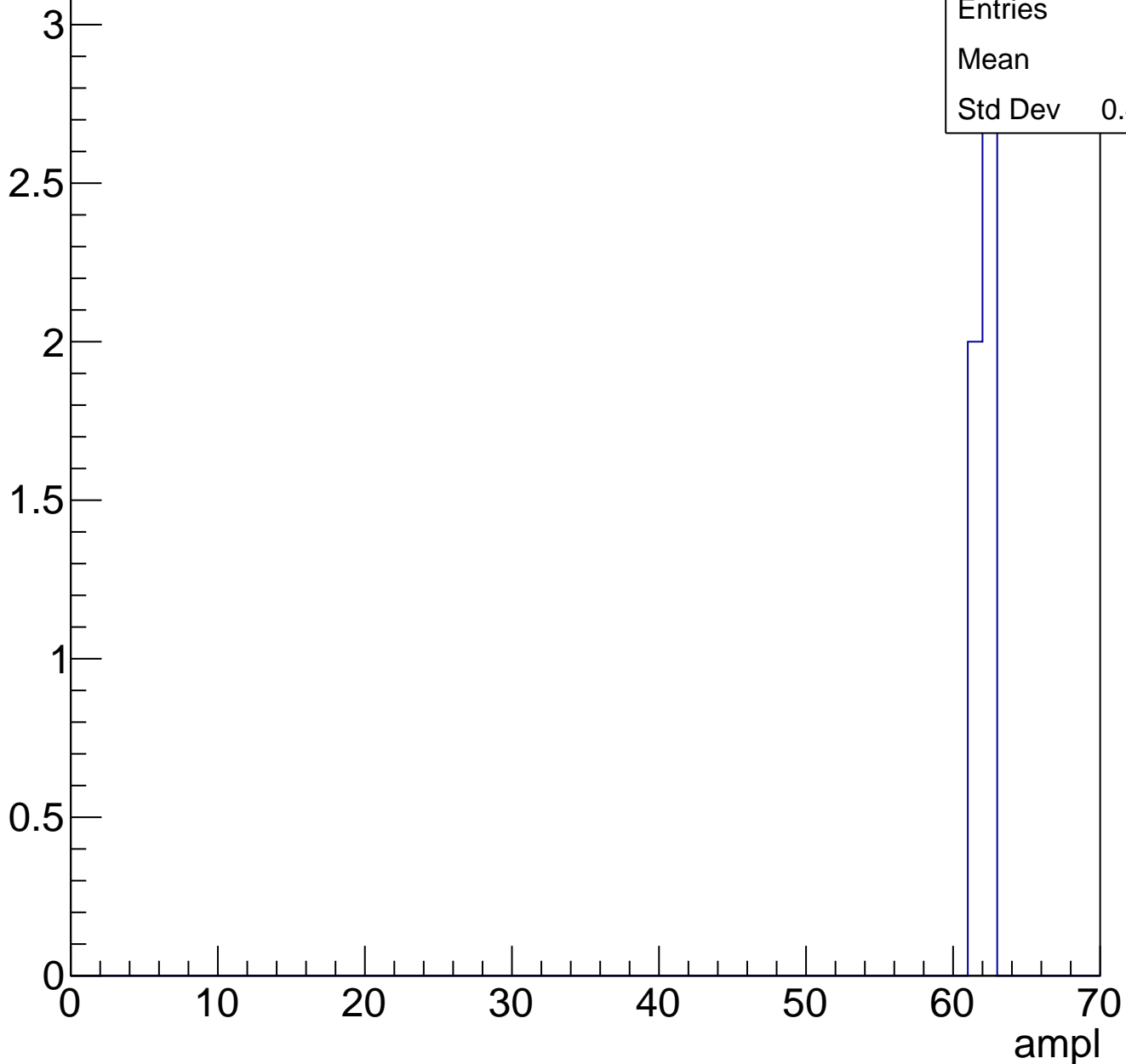
Entries	33
Mean	58.3
Std Dev	10.59



B1L103S, U13-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

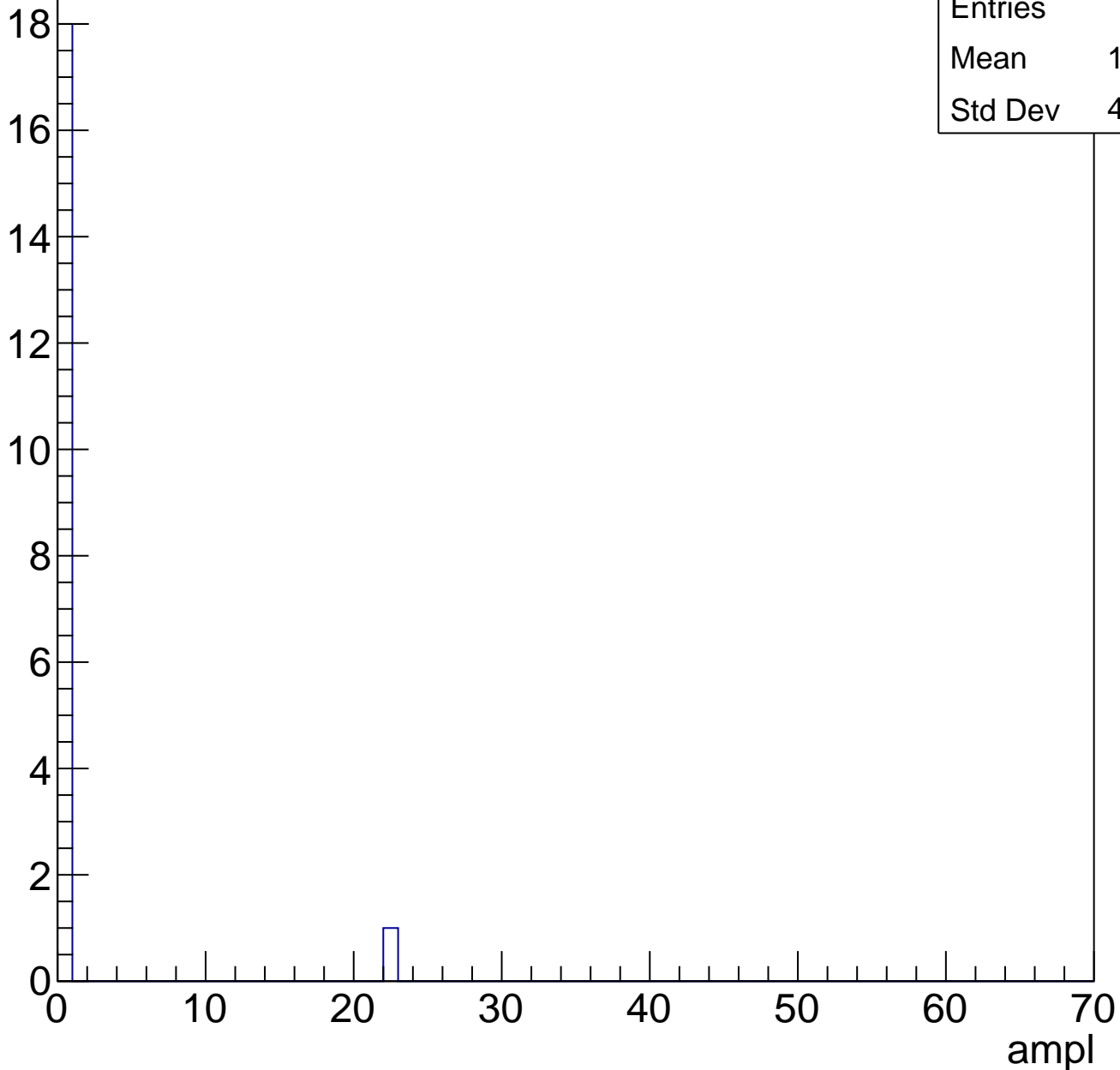


B1L103S, U13-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U13-ch118, adc0

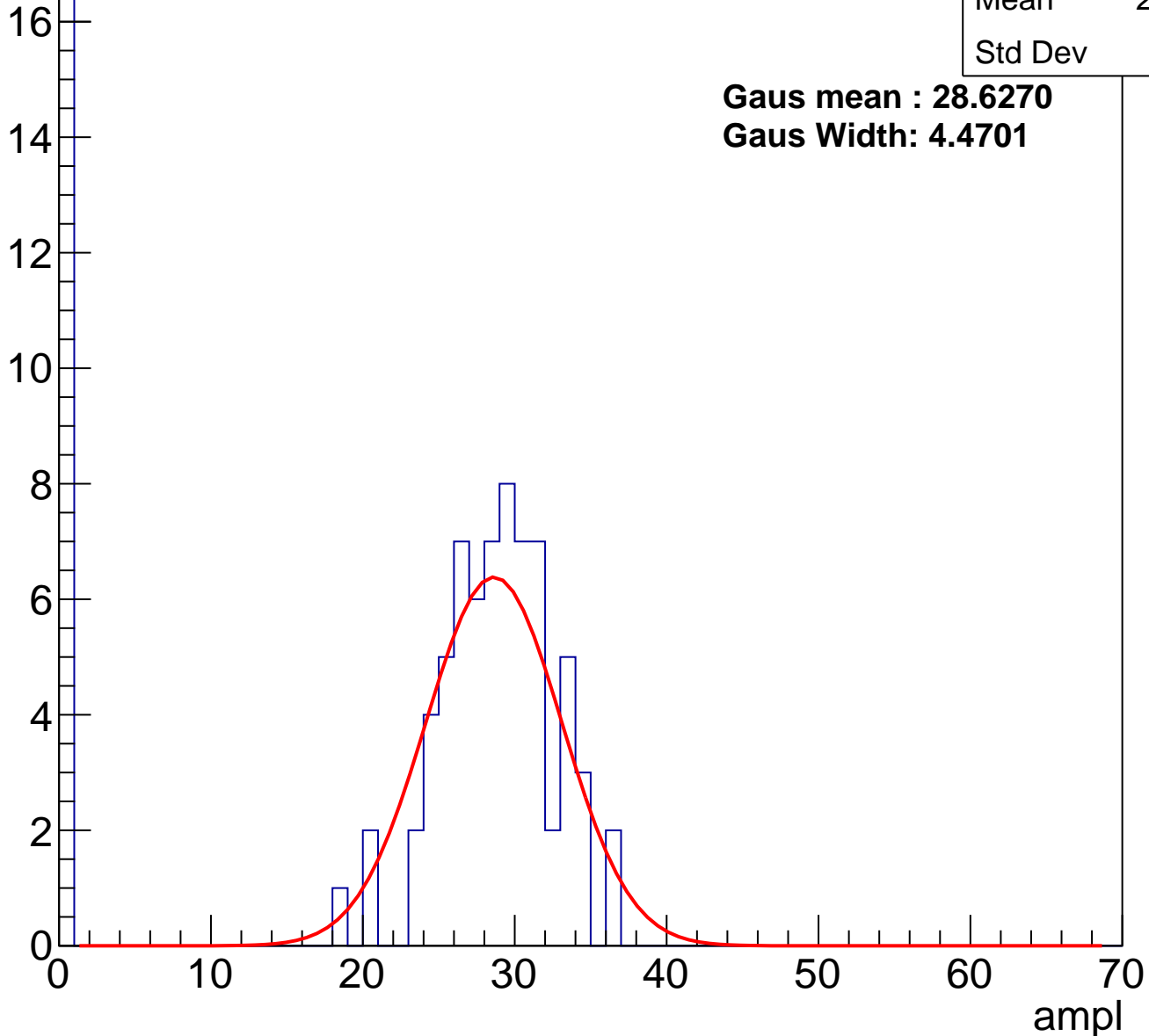
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	22.67
Std Dev	11.8

Gaus mean : 28.6270

Gaus Width: 4.4701

Entry



B1L103S, U13-ch118, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	33.16
Std Dev	8.662

Gaus mean : 35.8853

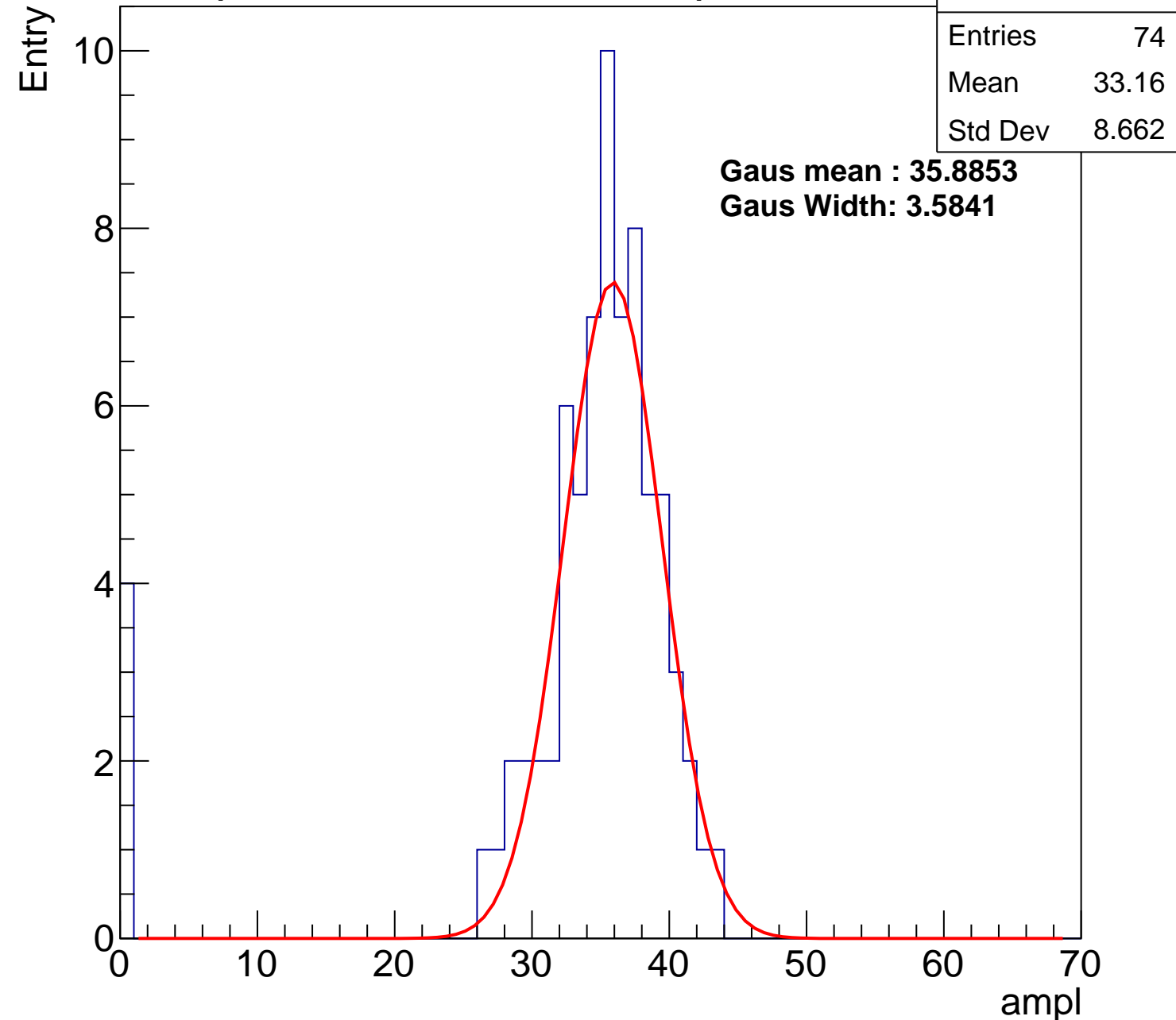
Gaus Width: 3.5841

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch118, adc2

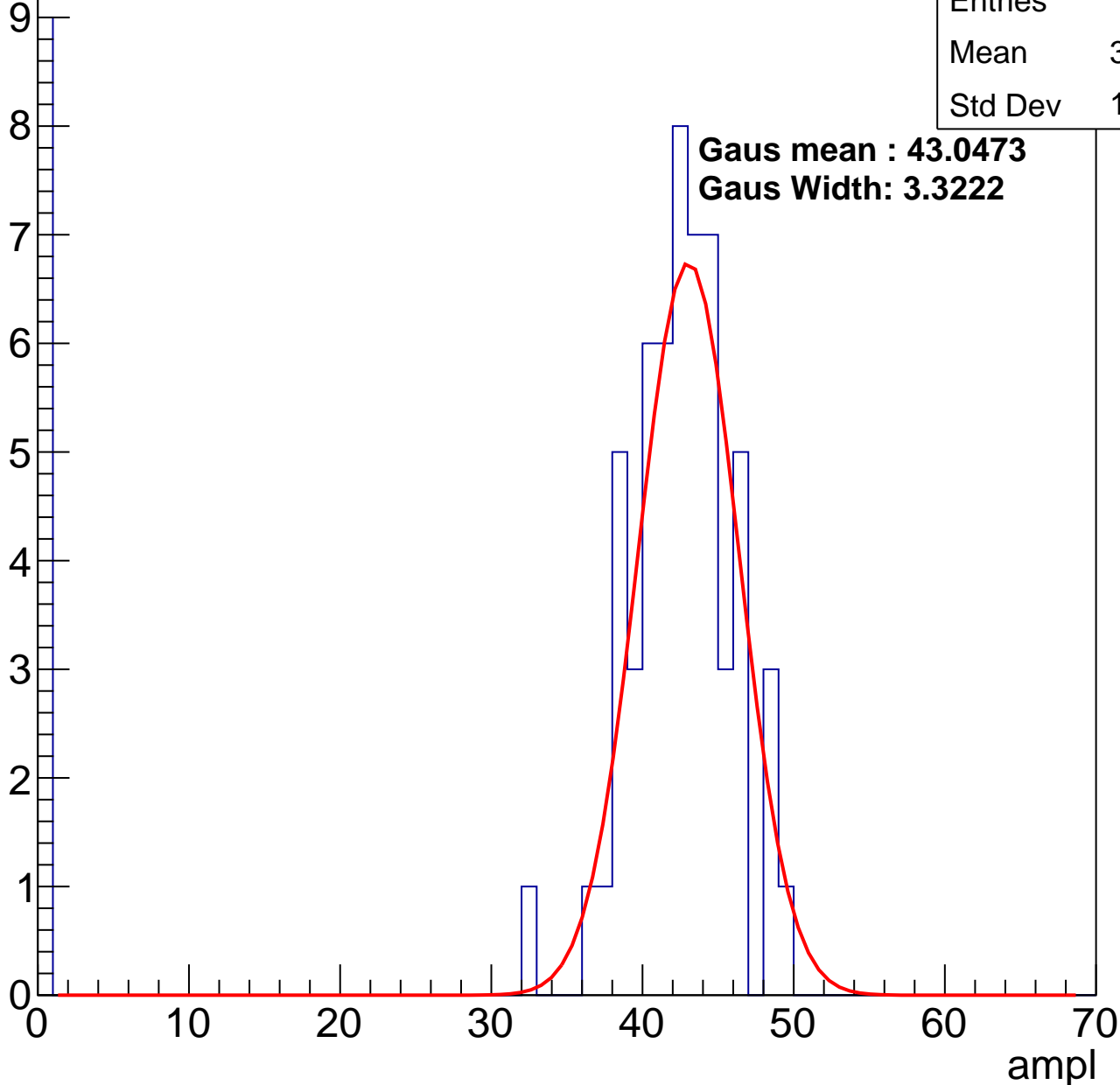
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.38
Std Dev	14.76

Gaus mean : 43.0473

Gaus Width: 3.3222

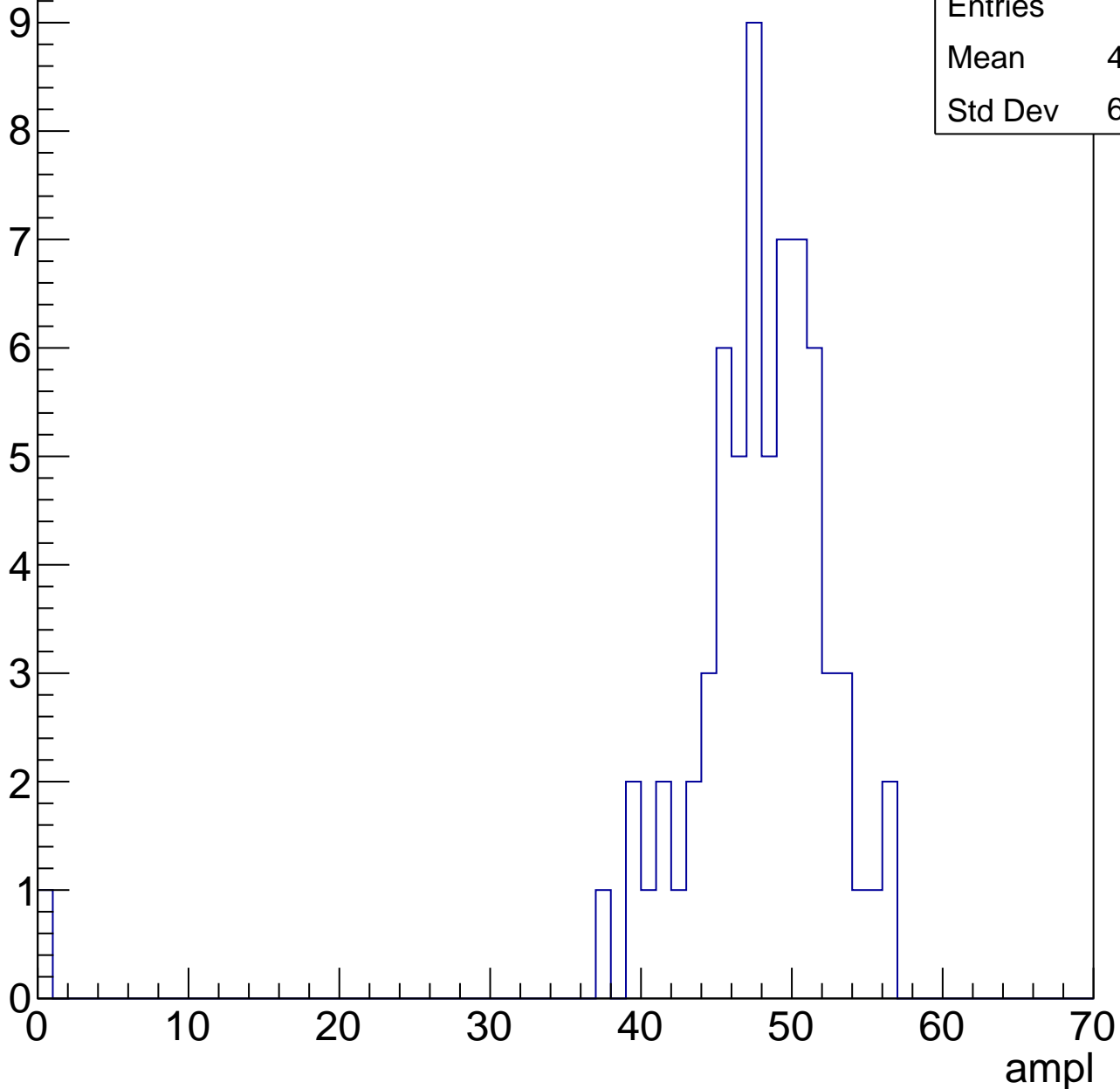


B1L103S, U13-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	46.99
Std Dev	6.988

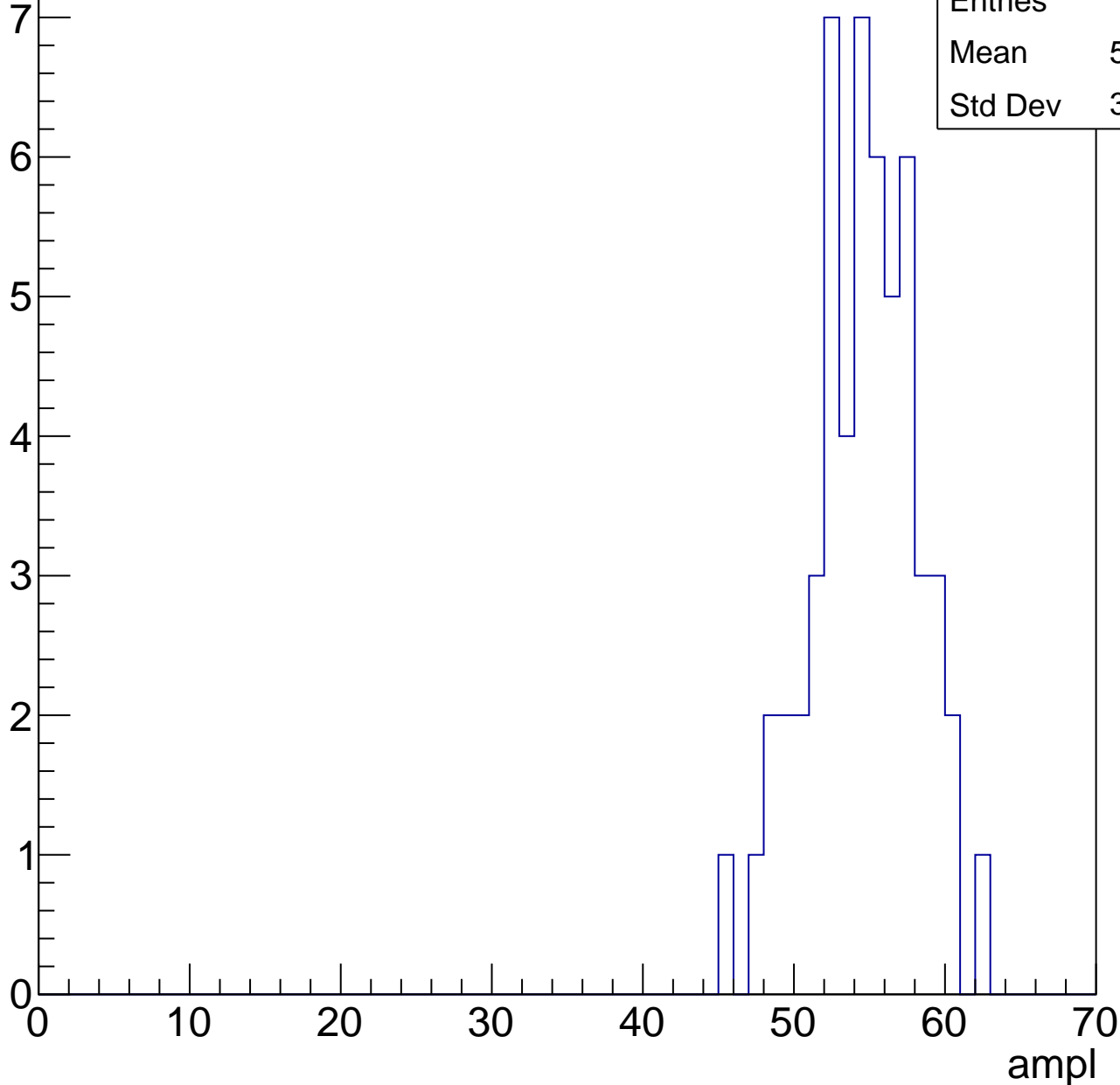


B1L103S, U13-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.15
Std Dev	3.518

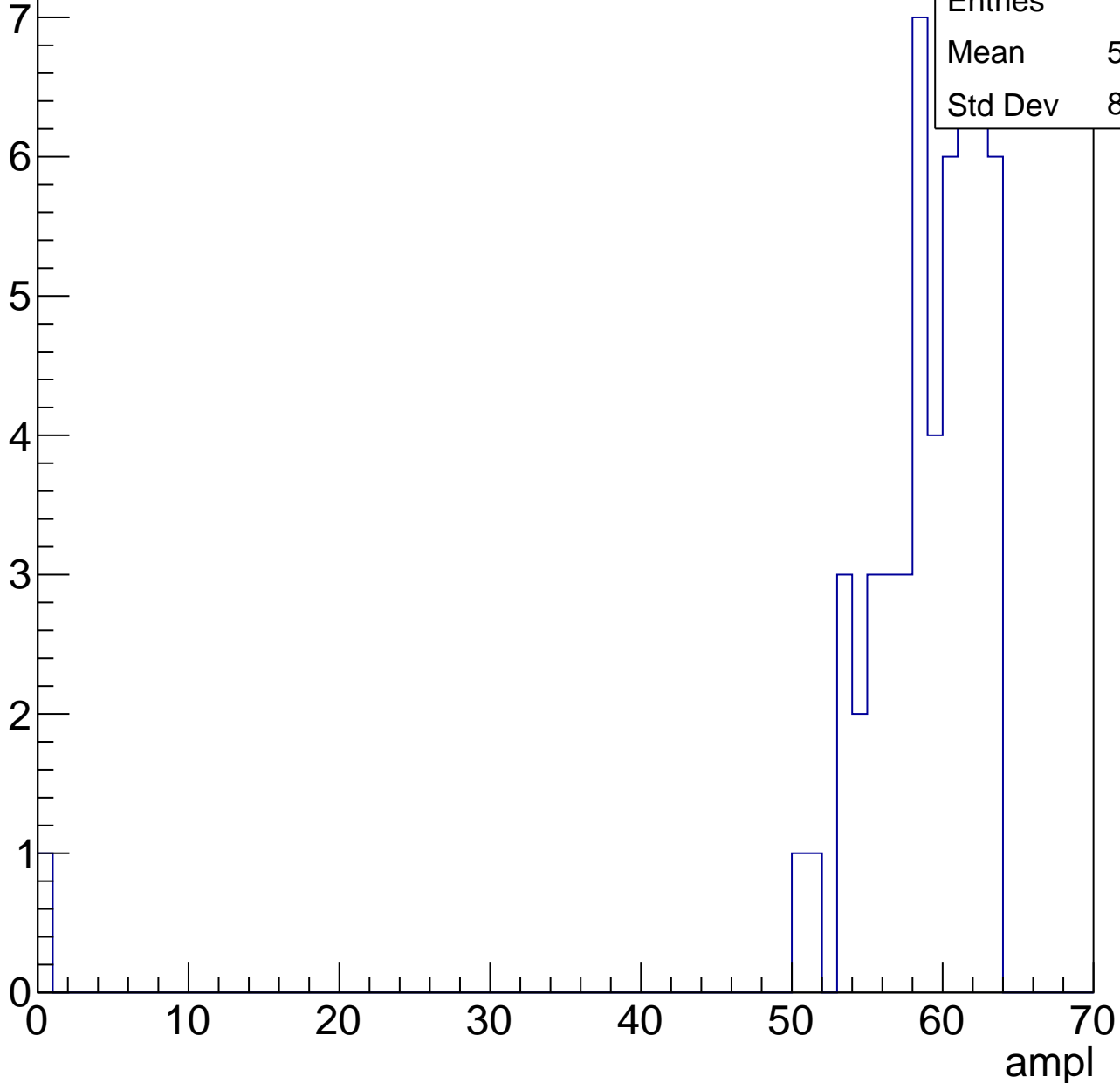


B1L103S, U13-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

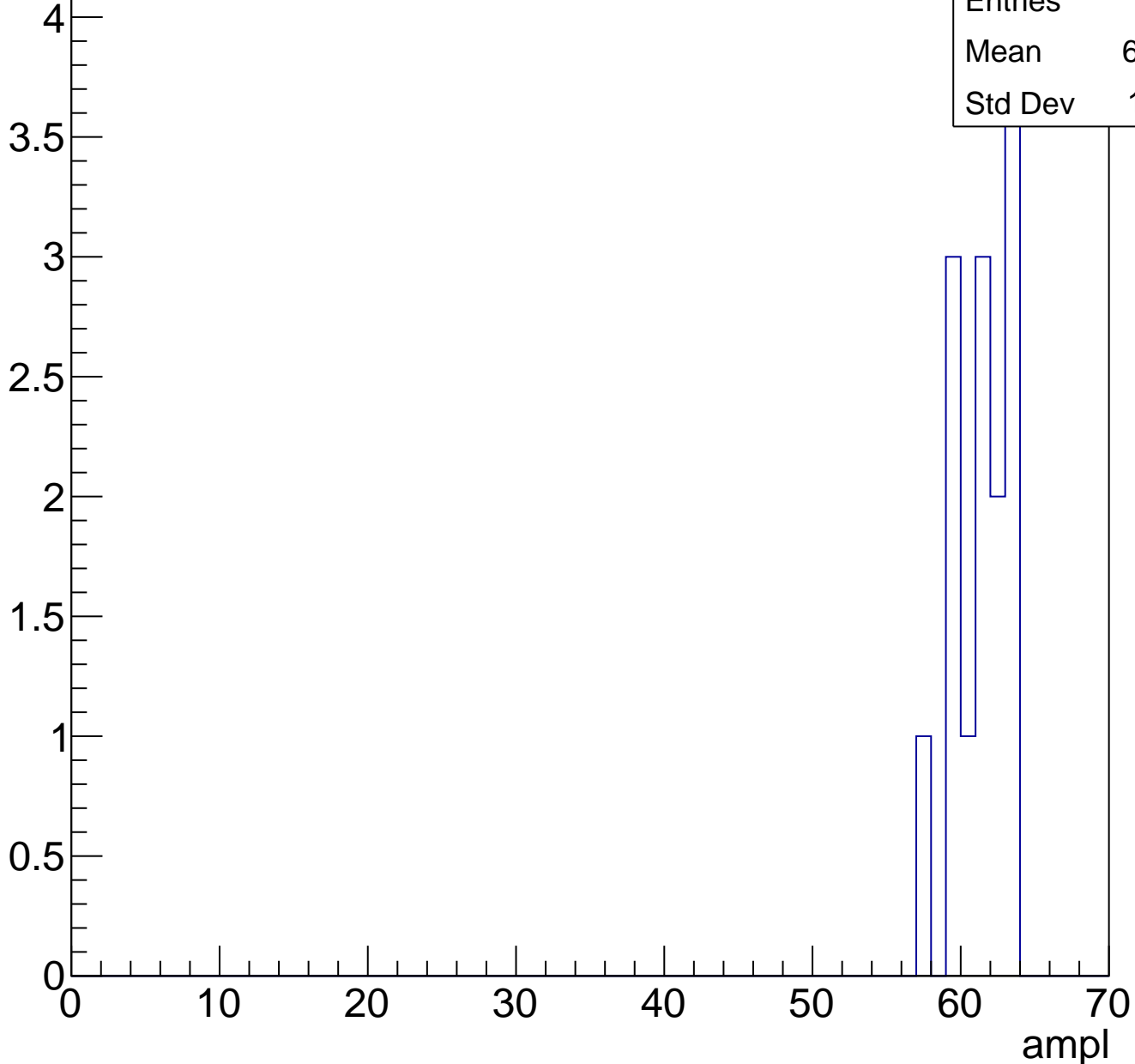
Entries	54
Mean	57.65
Std Dev	8.577



B1L103S, U13-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

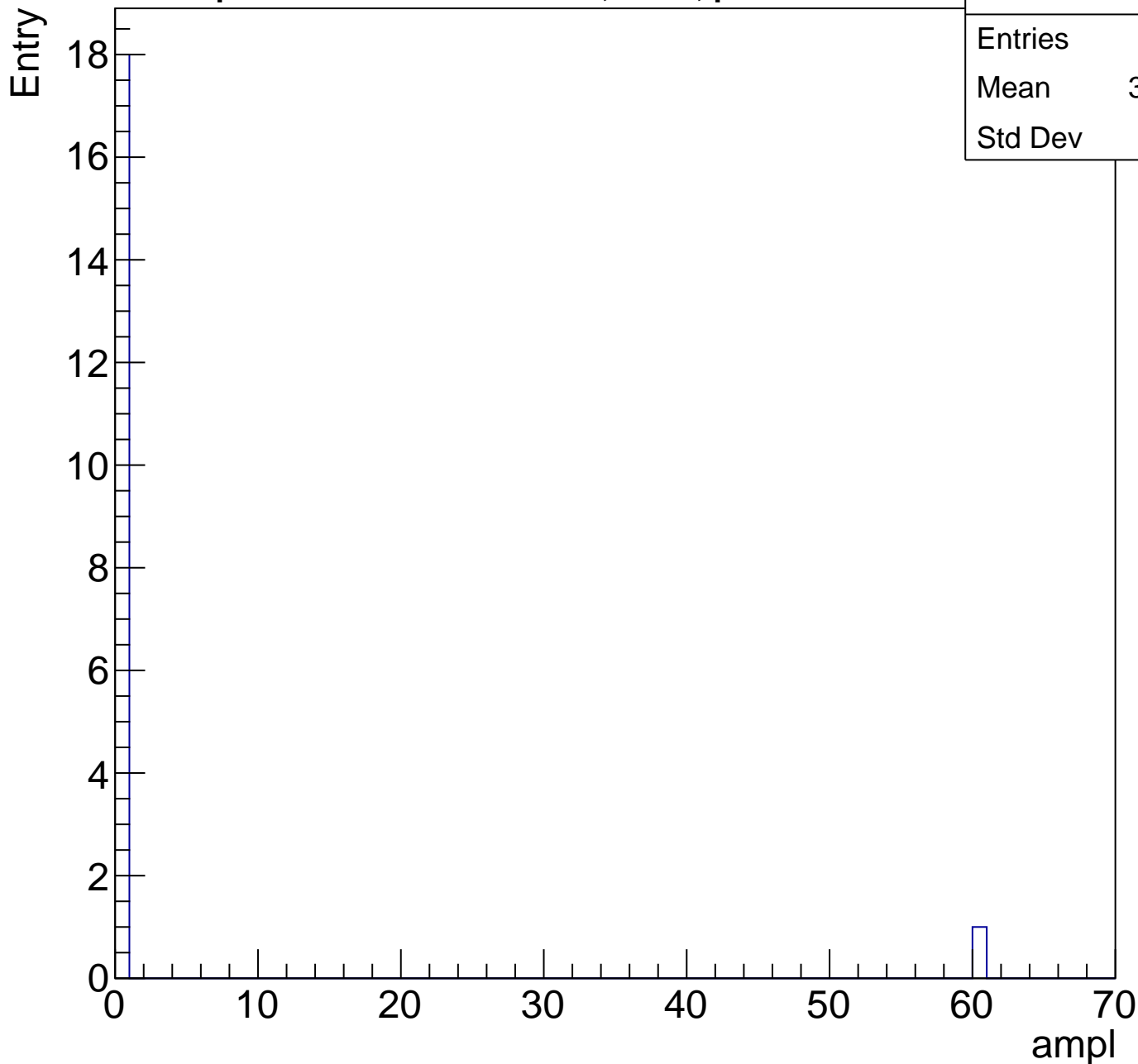


Entries	14
Mean	60.93
Std Dev	1.831

B1L103S, U13-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.158
Std Dev	13.4



B1L103S, U13-ch119, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	24.79
Std Dev	12.1

Gaus mean : 31.5682

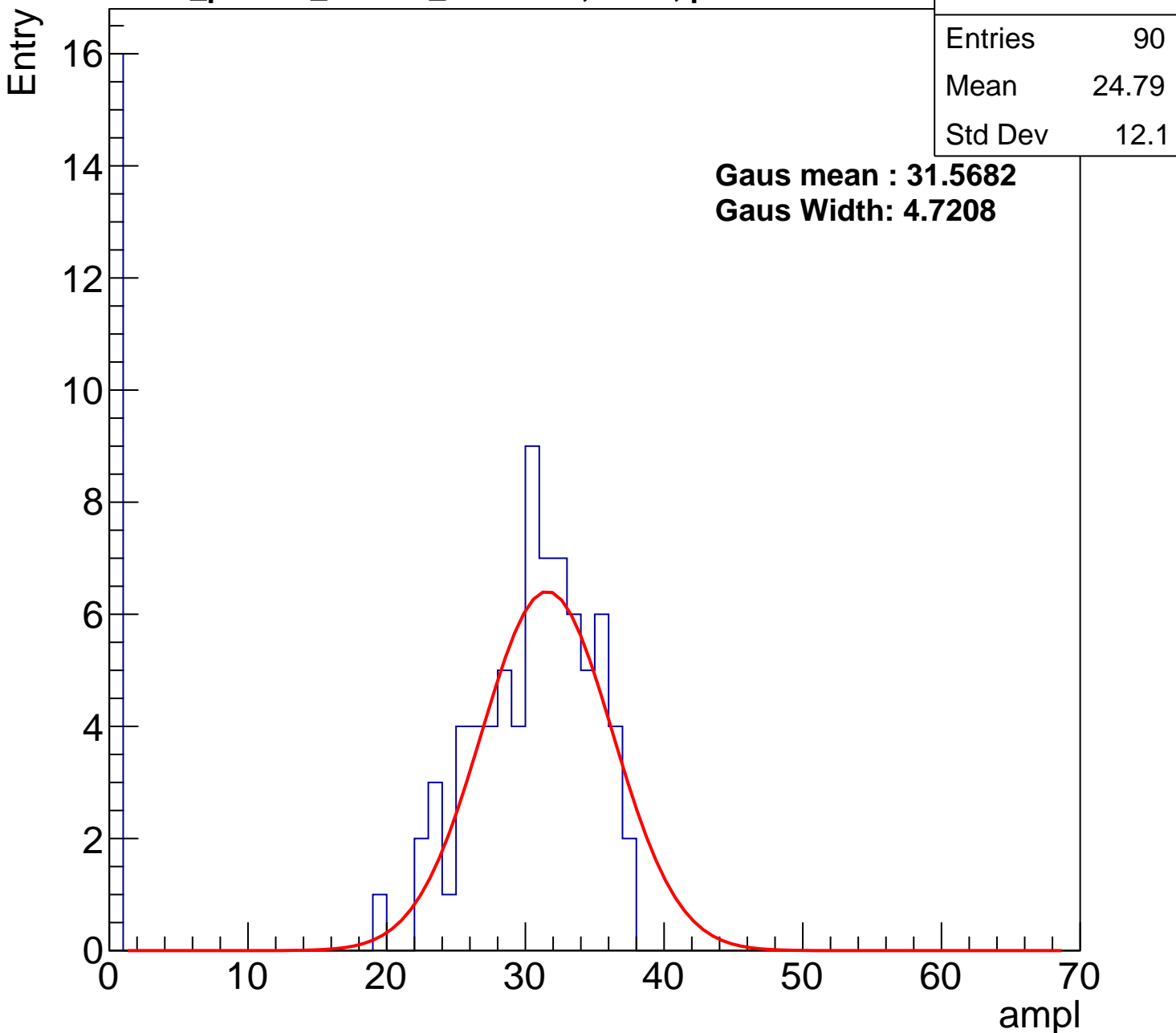
Gaus Width: 4.7208

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch119, adc1

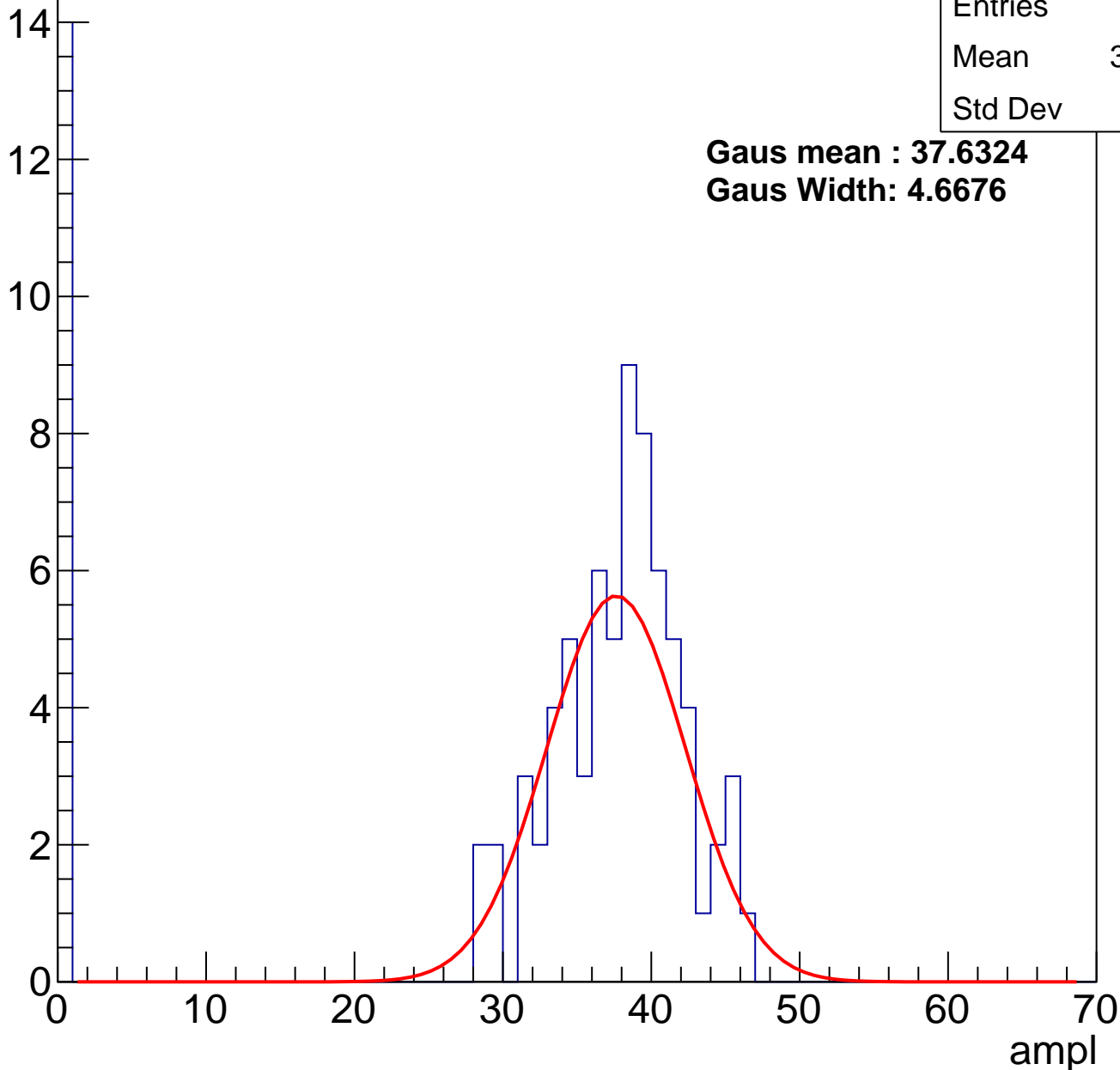
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	31.27
Std Dev	14.4

Gaus mean : 37.6324

Gaus Width: 4.6676

Entry



B1L103S, U13-ch119, adc2

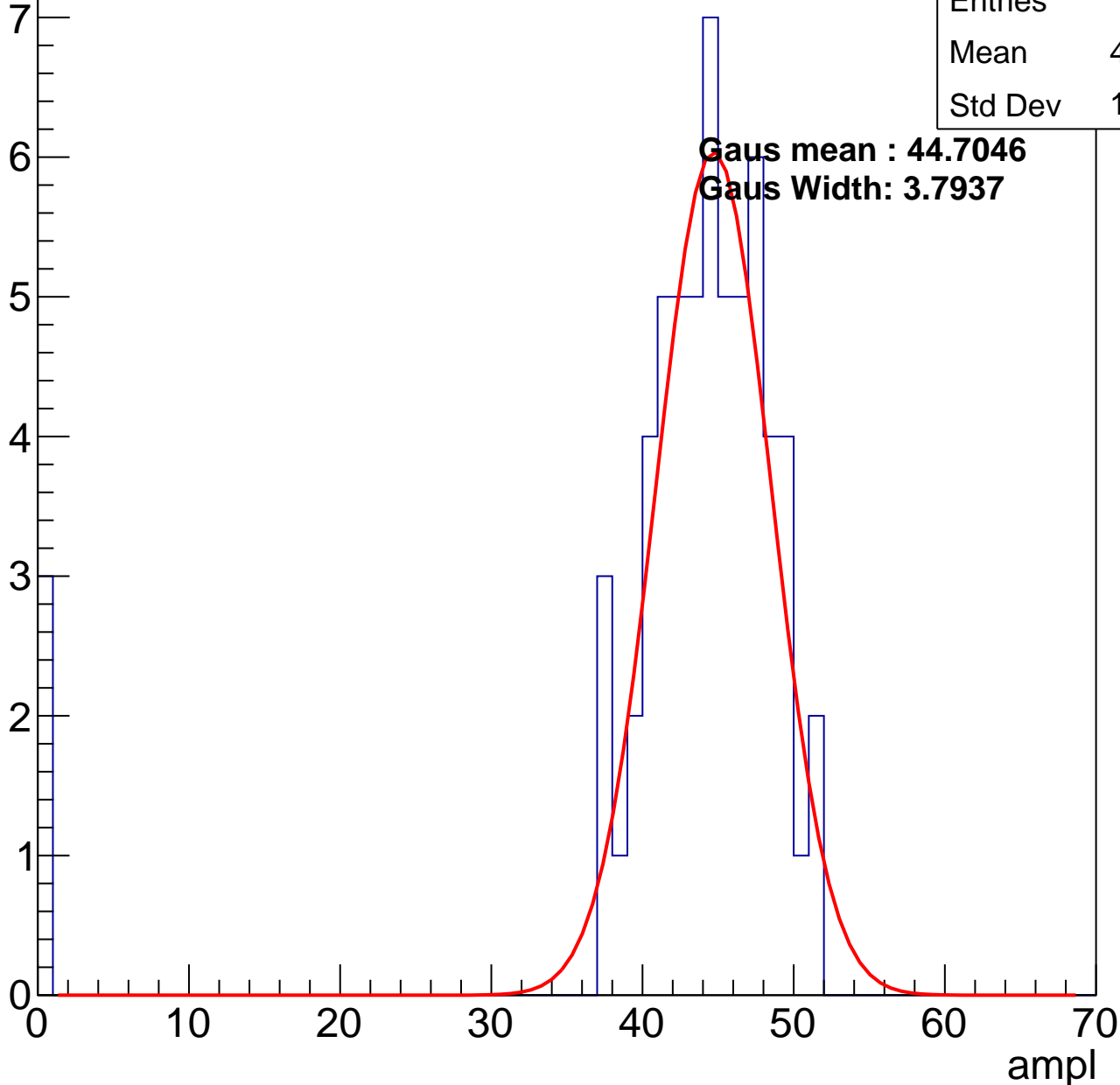
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	41.97
Std Dev	10.08

Gaus mean : 44.7046

Gaus Width: 3.7937

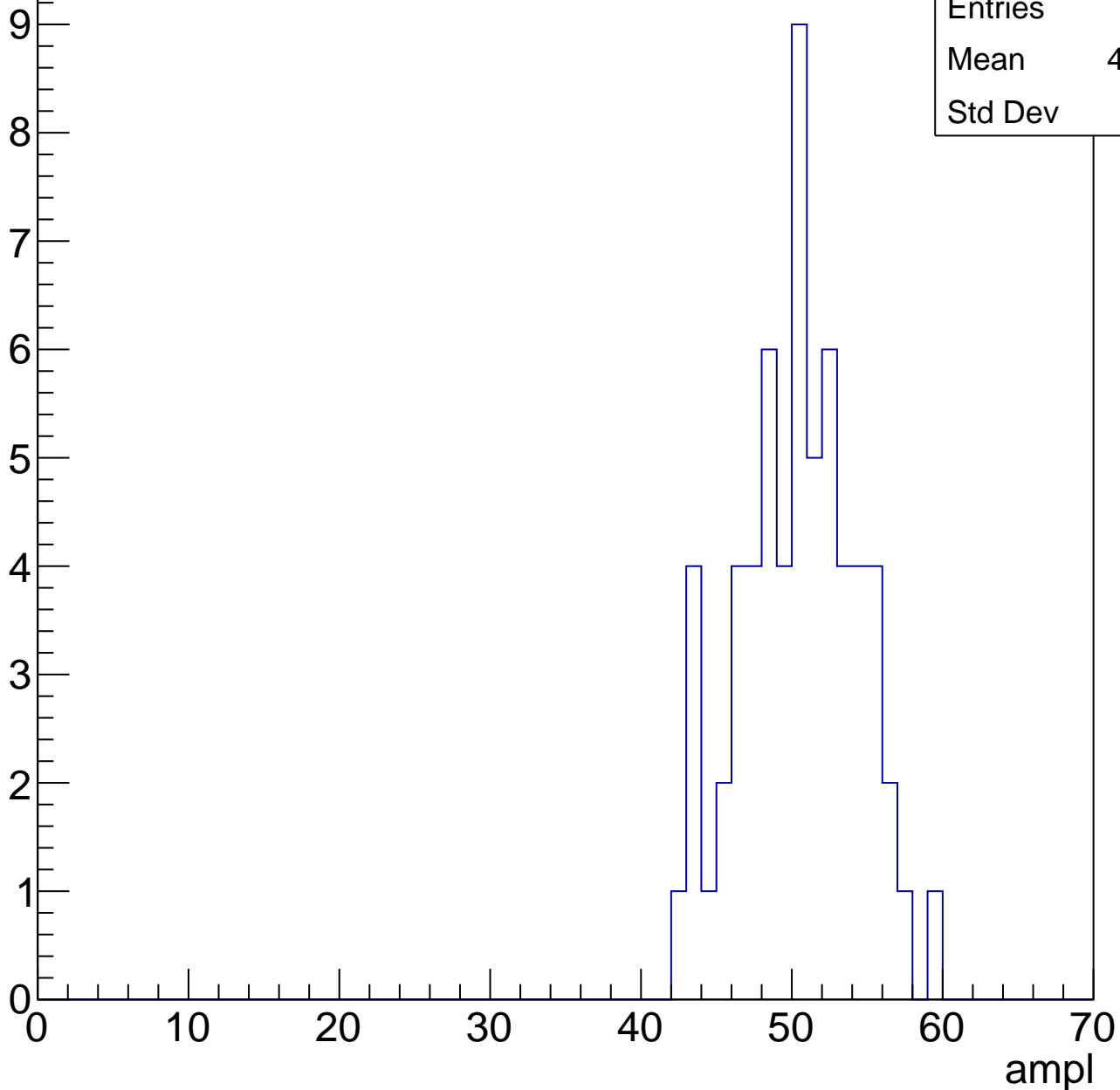


B1L103S, U13-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	49.95
Std Dev	3.82

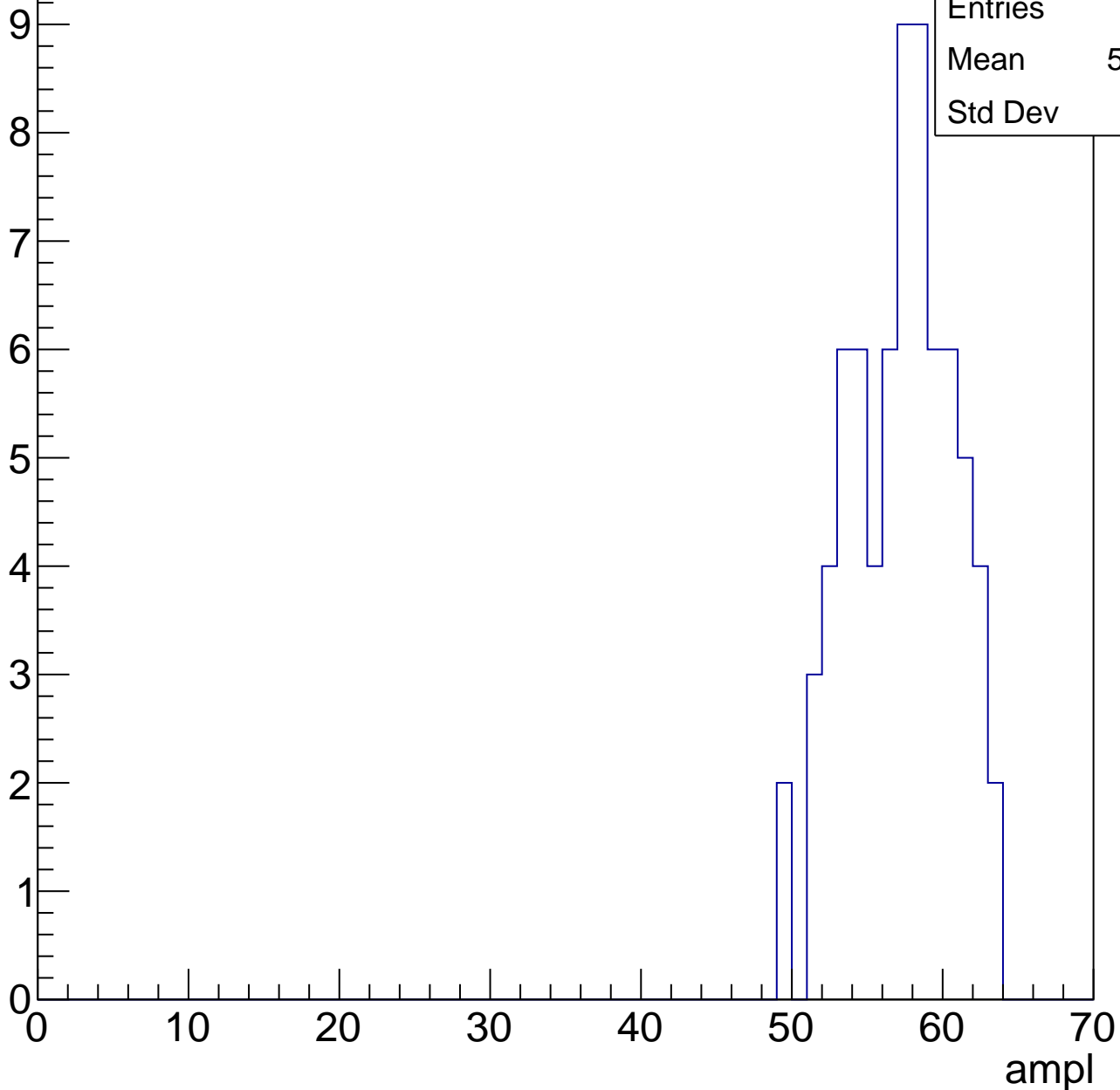


B1L103S, U13-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	56.74
Std Dev	3.42

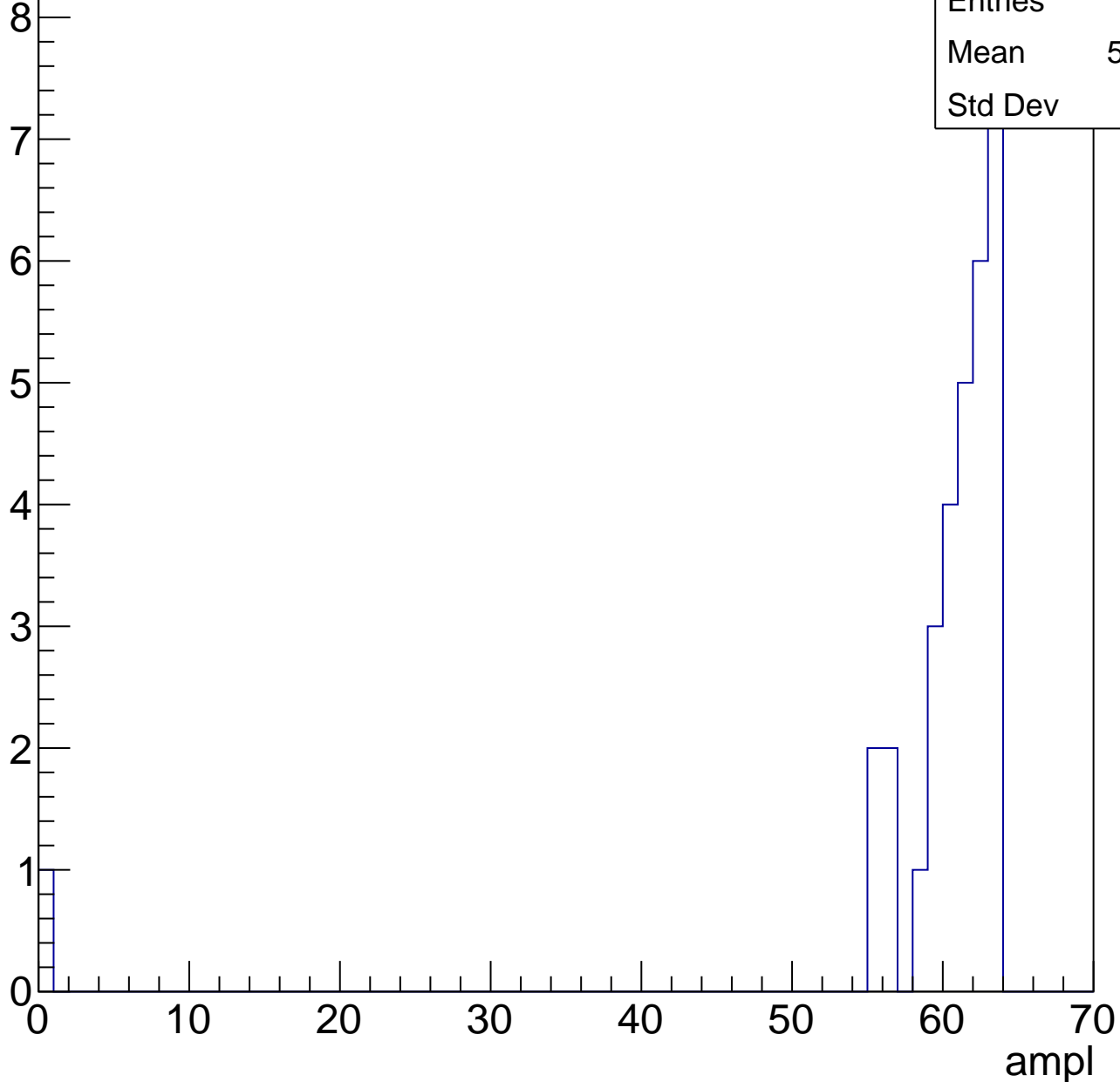


B1L103S, U13-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.69
Std Dev	10.8



B1L103S, U13-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U13-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch120, adc0

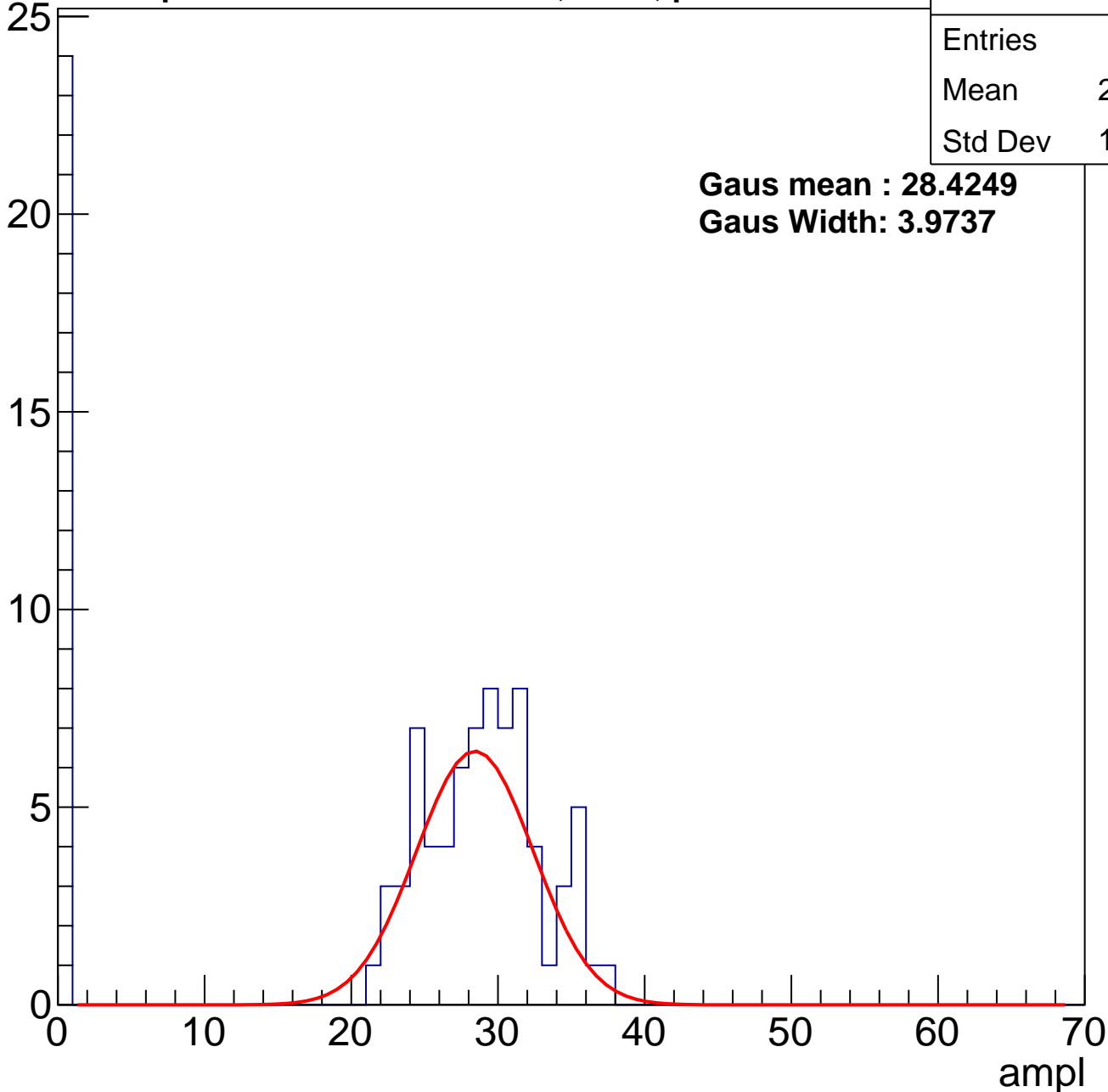
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	21.52
Std Dev	12.78

Gaus mean : 28.4249

Gaus Width: 3.9737

Entry



B1L103S, U13-ch120, adc1

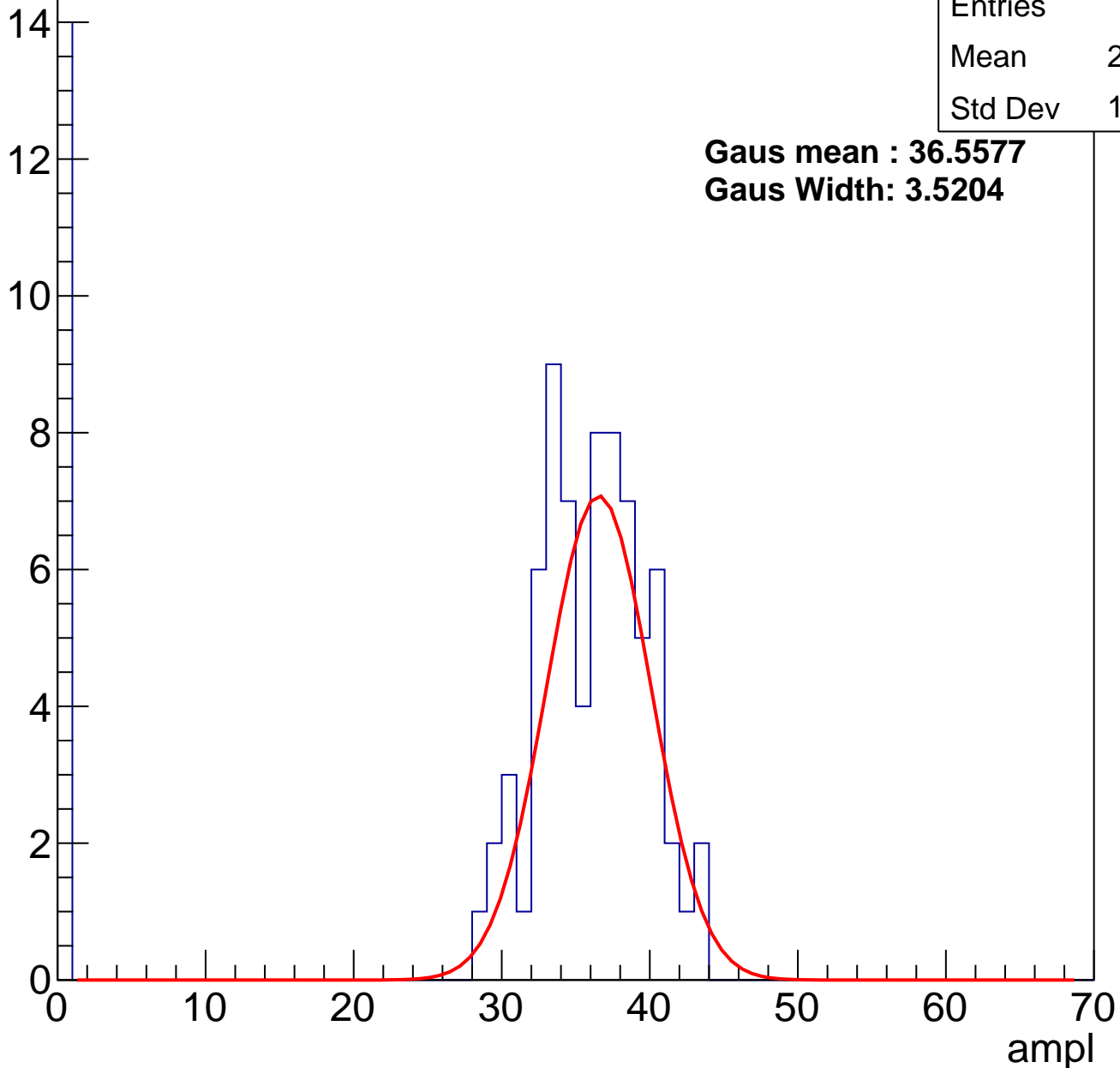
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	29.87
Std Dev	13.55

Gaus mean : 36.5577

Gaus Width: 3.5204

Entry



B1L103S, U13-ch120, adc2

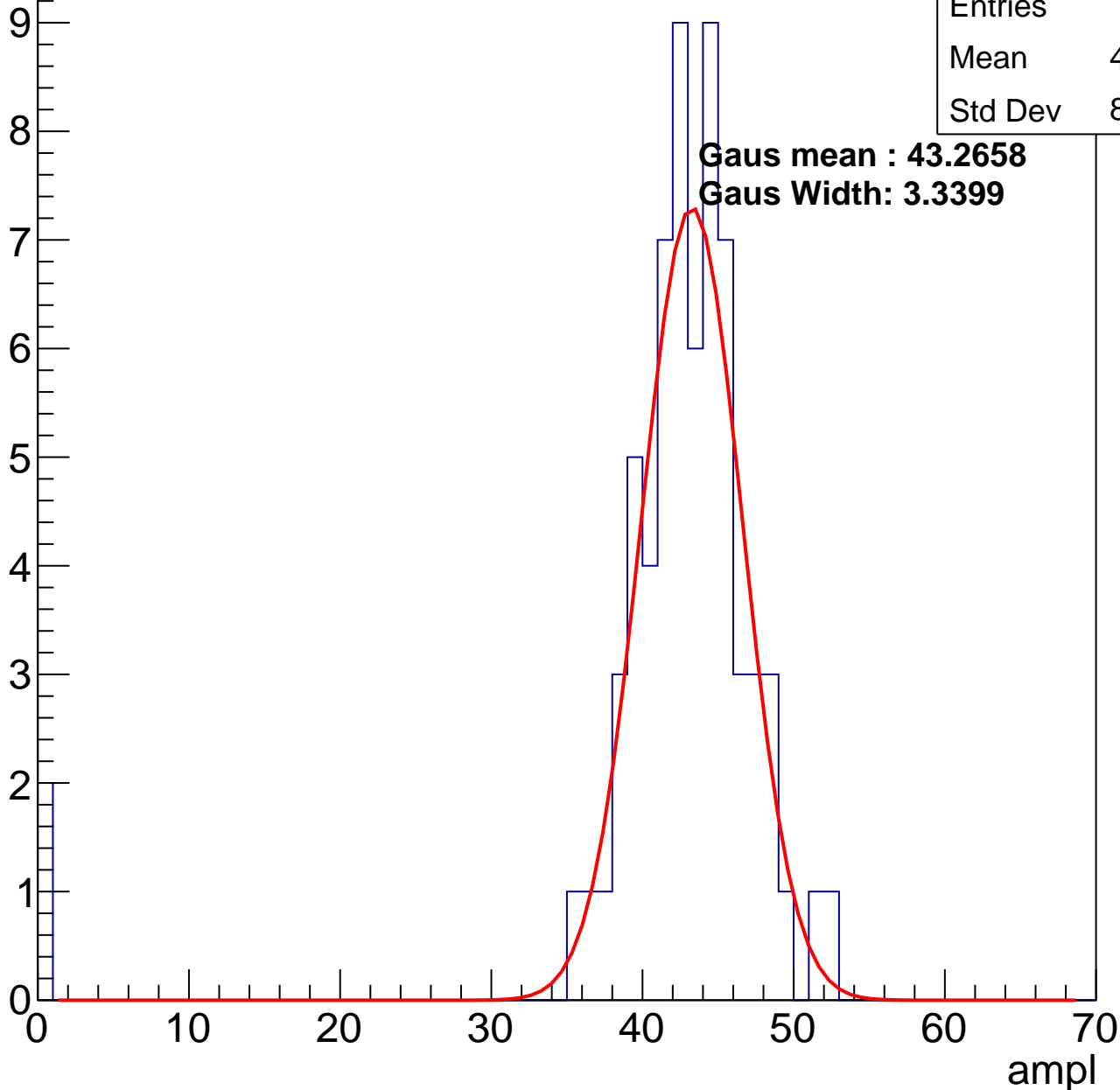
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	41.58
Std Dev	8.019

Gaus mean : 43.2658

Gaus Width: 3.3399

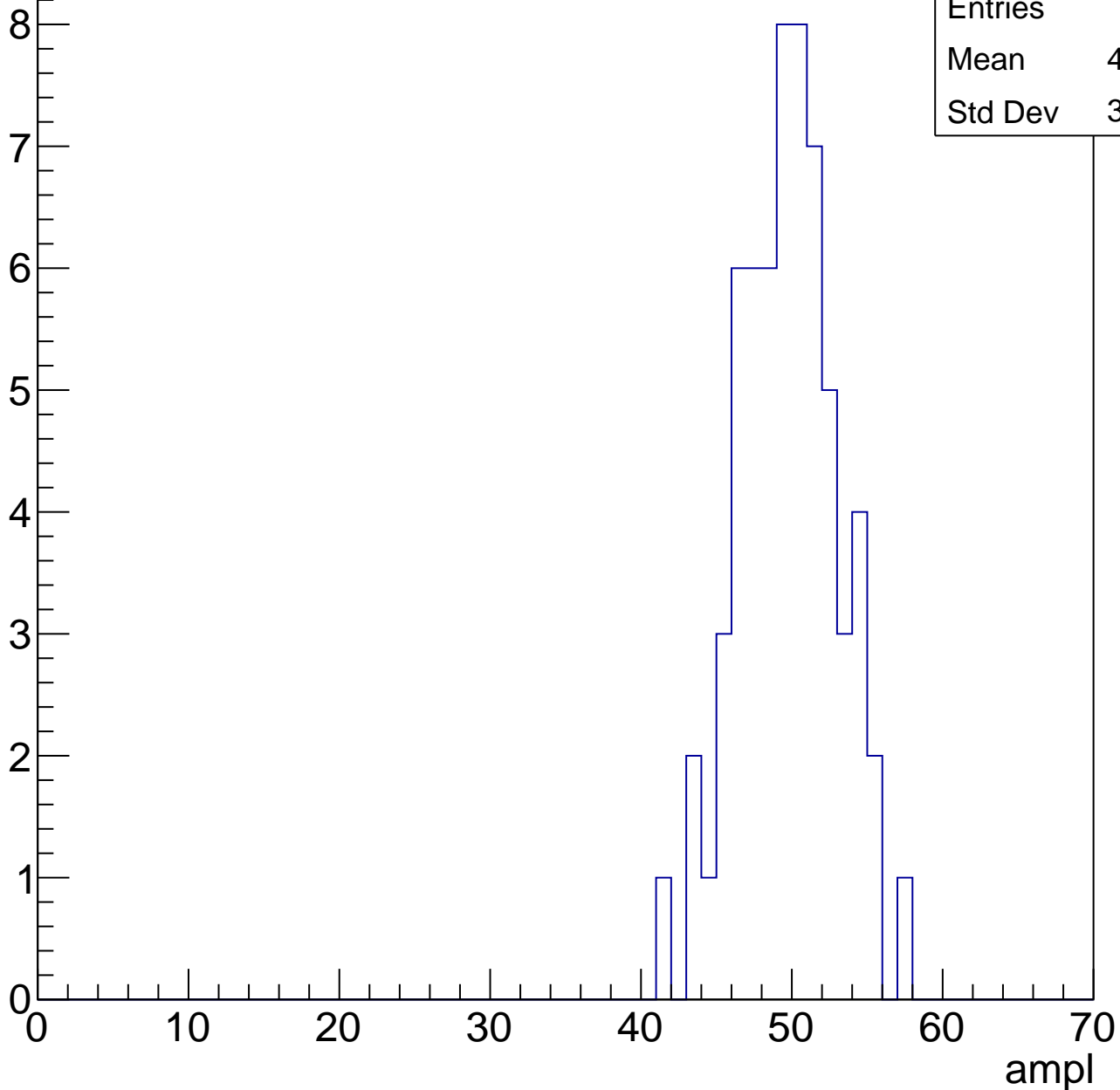


B1L103S, U13-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.25
Std Dev	3.222

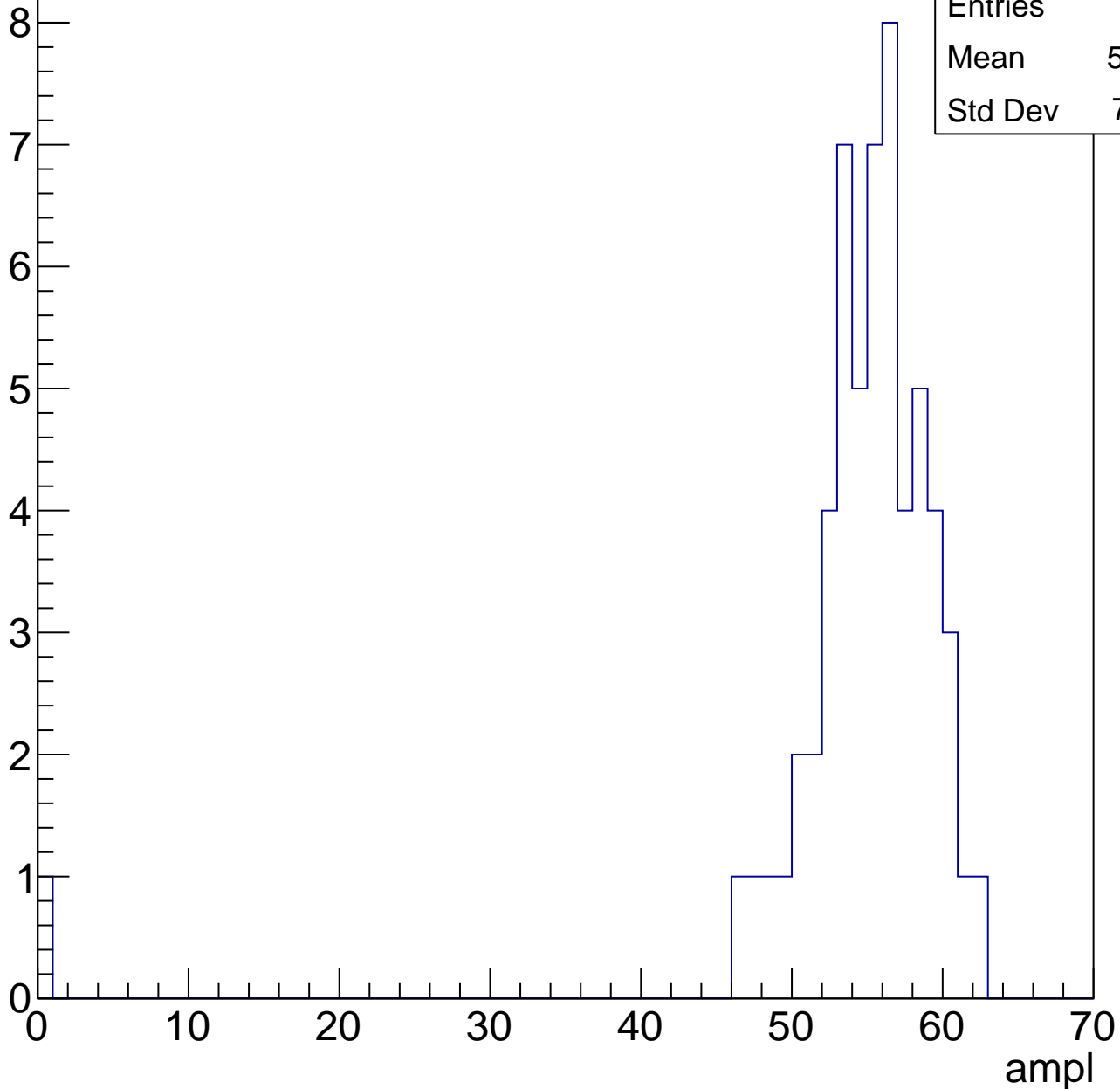


B1L103S, U13-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.98
Std Dev	7.921

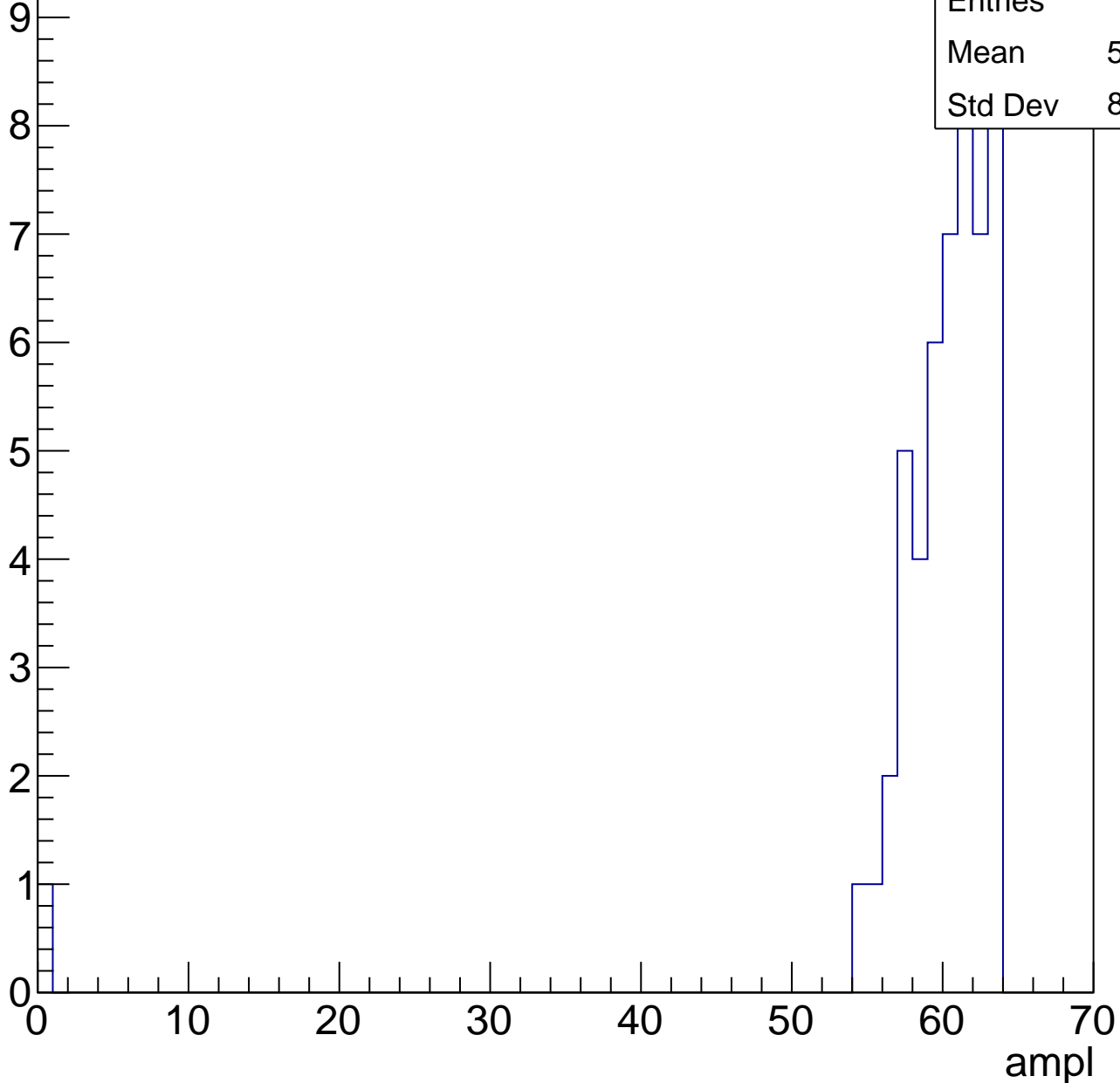


B1L103S, U13-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.84
Std Dev	8.642



B1L103S, U13-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.72

ampl

0 10 20 30 40 50 60 70

B1L103S, U13-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch121, adc0

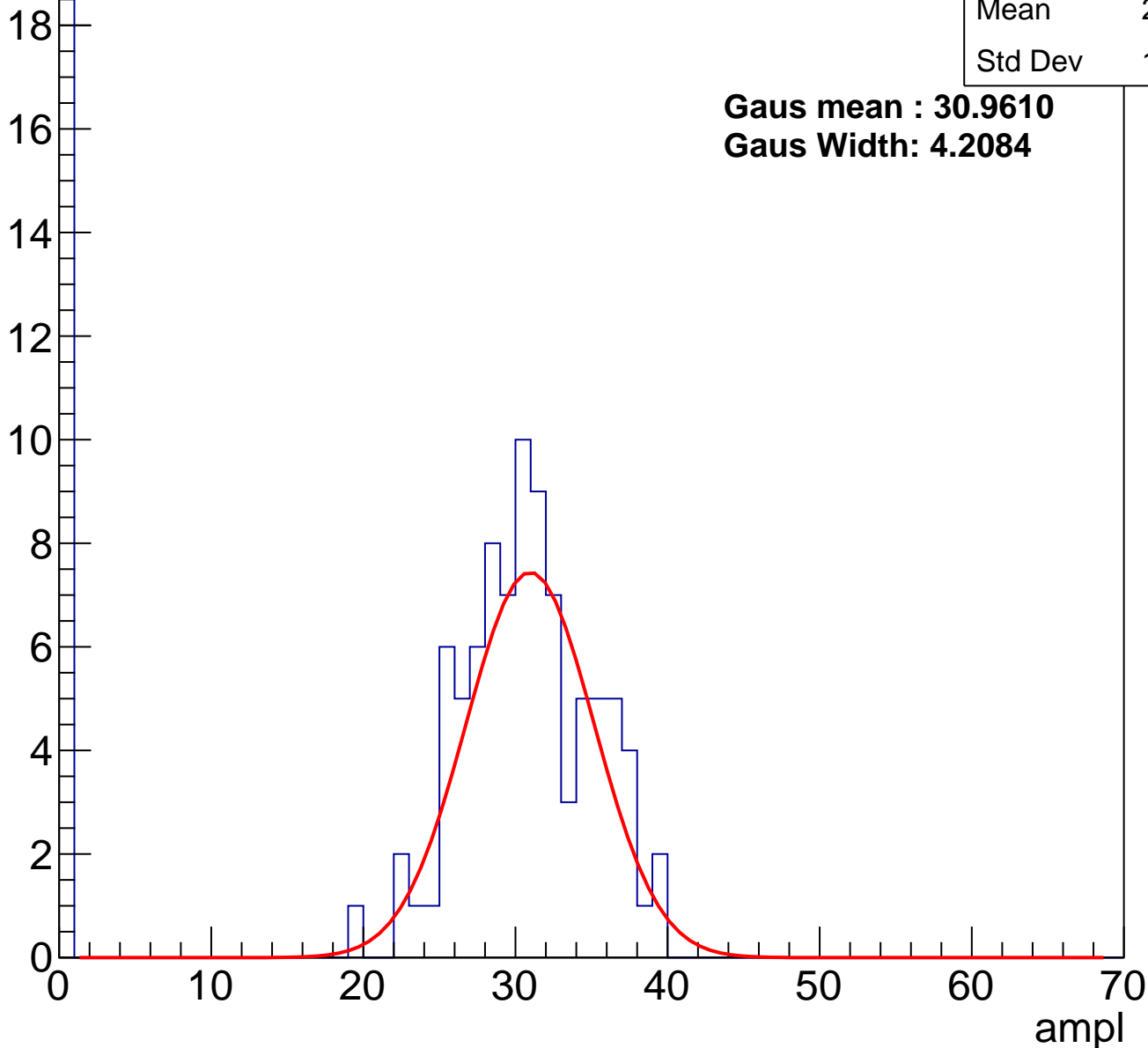
calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	24.95
Std Dev	12.19

Gaus mean : 30.9610

Gaus Width: 4.2084

Entry



B1L103S, U13-ch121, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	30.97
Std Dev	14.22

Gaus mean : 37.4574

Gaus Width: 3.8266

Entry

10

8

6

4

2

0

0

10

20

30

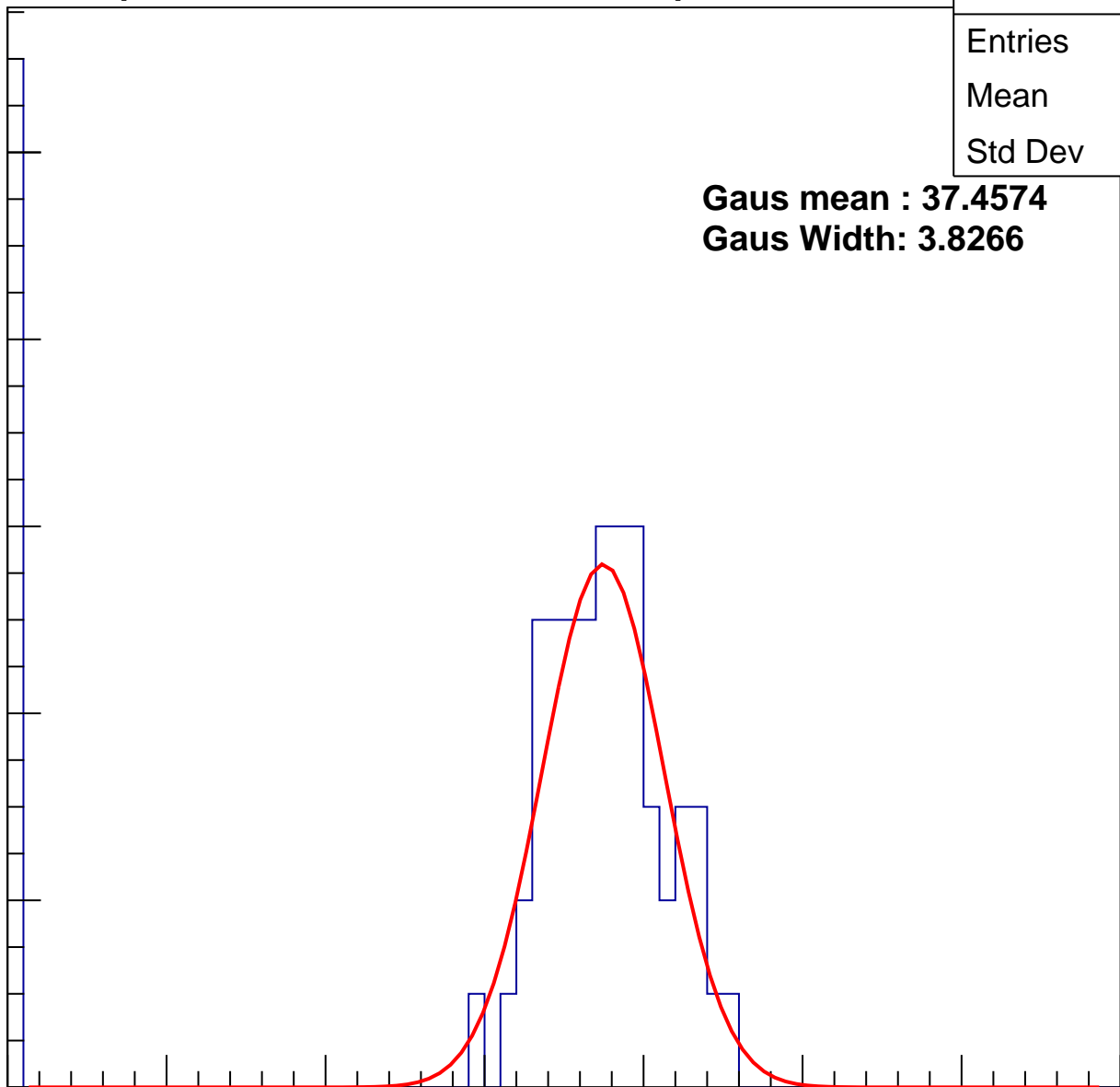
40

50

60

70

ampl



B1L103S, U13-ch121, adc2

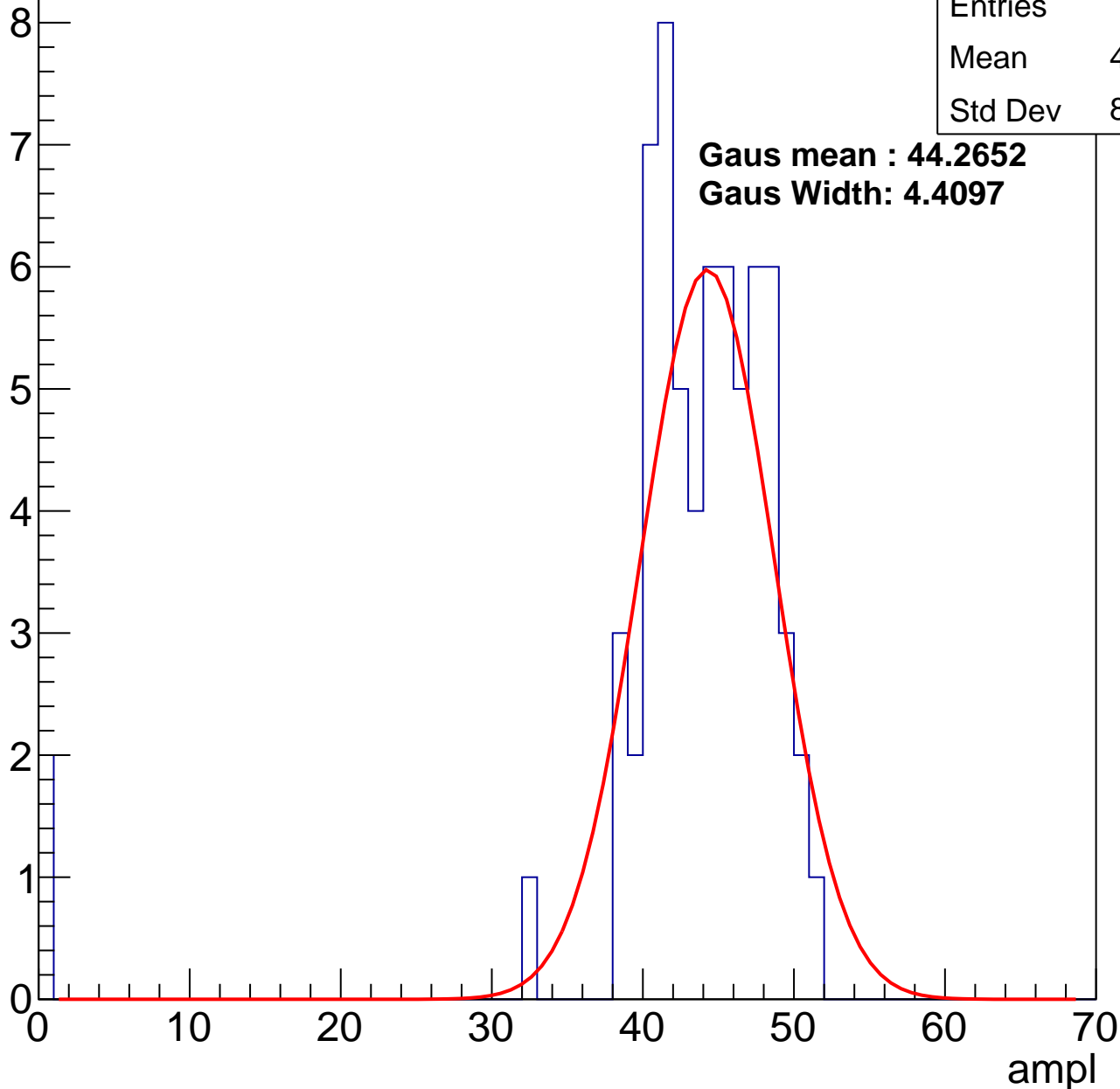
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	42.48
Std Dev	8.285

Gaus mean : 44.2652

Gaus Width: 4.4097

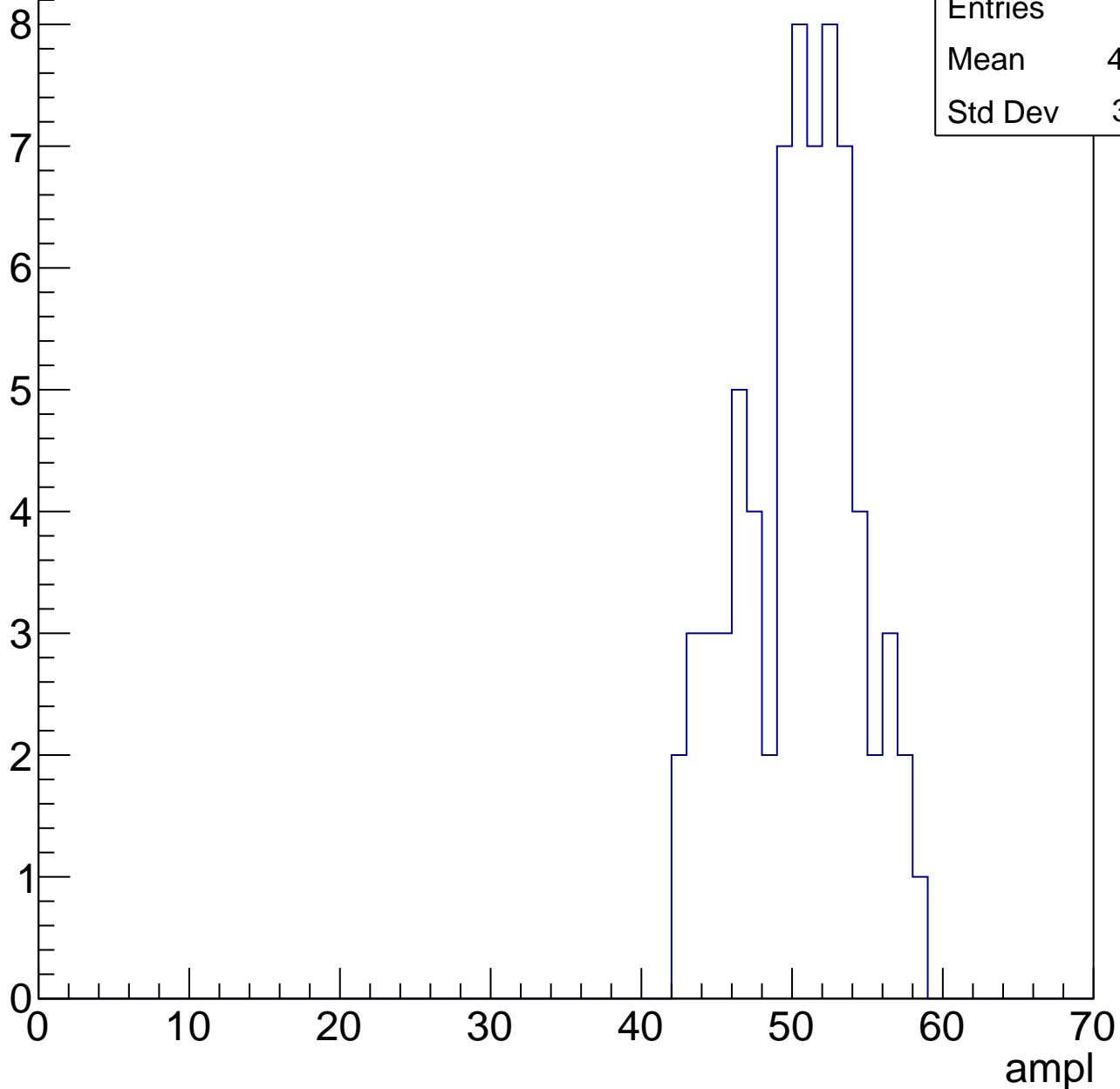


B1L103S, U13-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	49.96
Std Dev	3.891

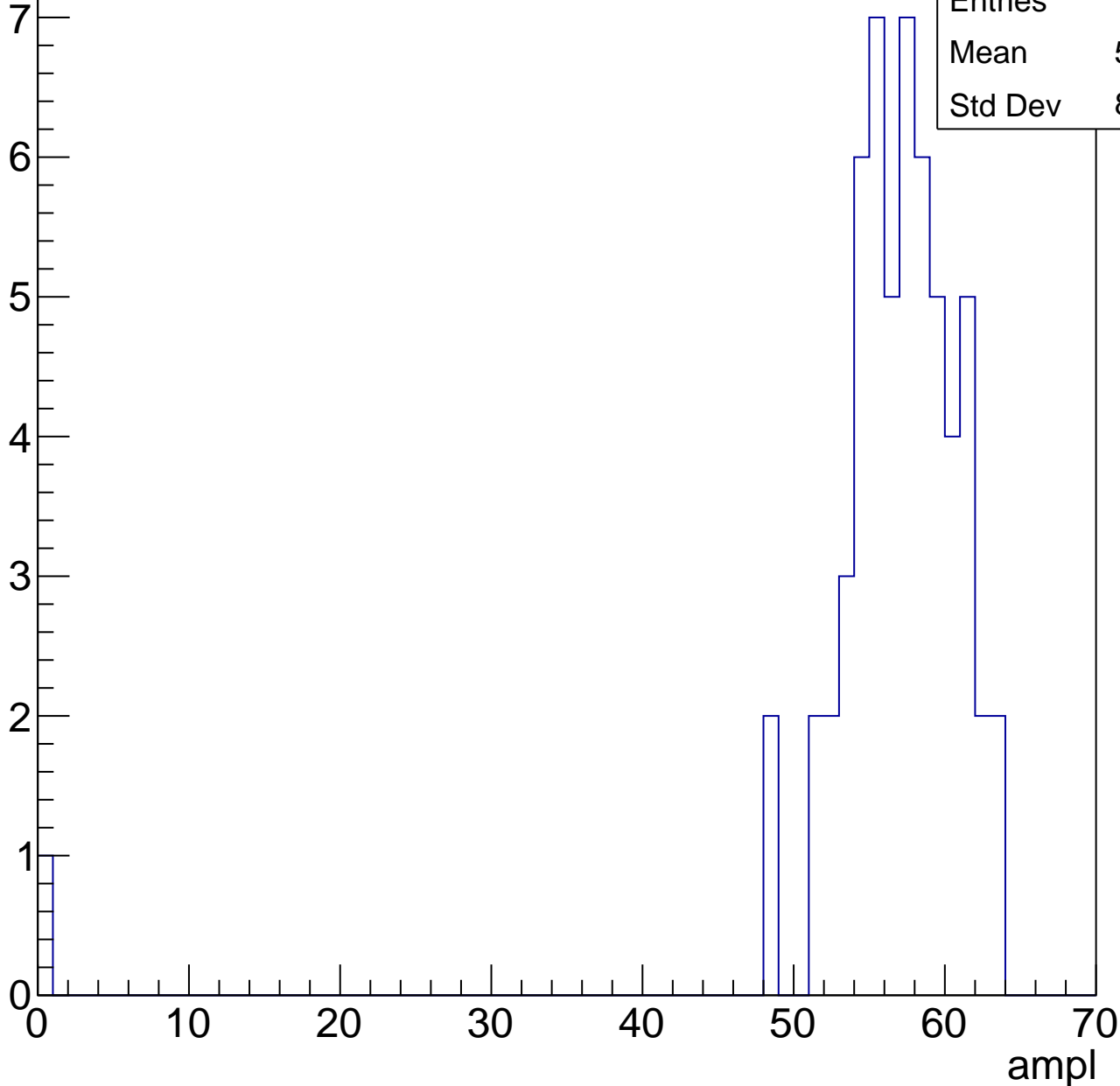


B1L103S, U13-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	55.71
Std Dev	8.061

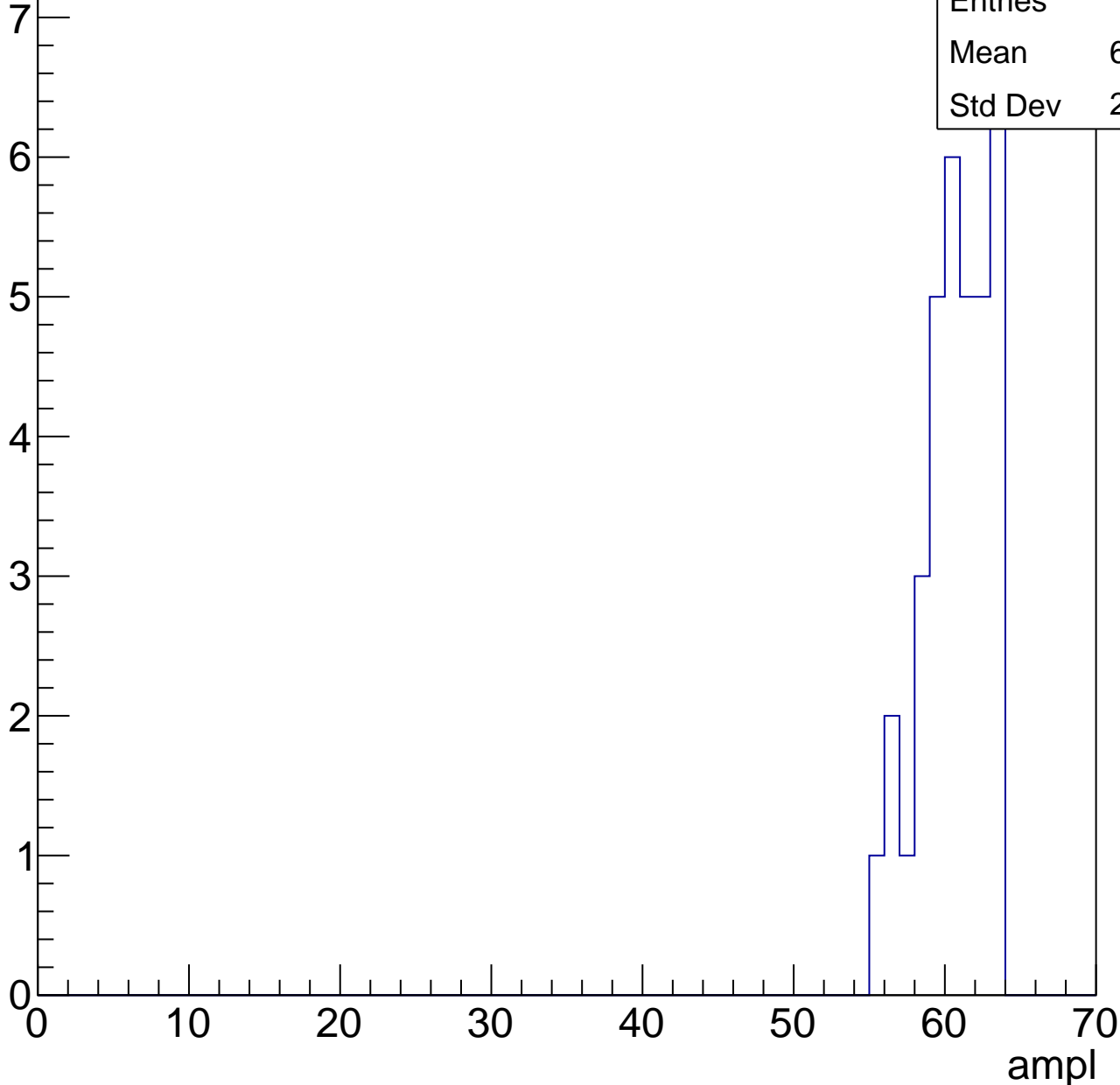


B1L103S, U13-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	60.26
Std Dev	2.195



B1L103S, U13-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	4
Mean	62.25
Std Dev	0.433

B1L103S, U13-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch122, adc0

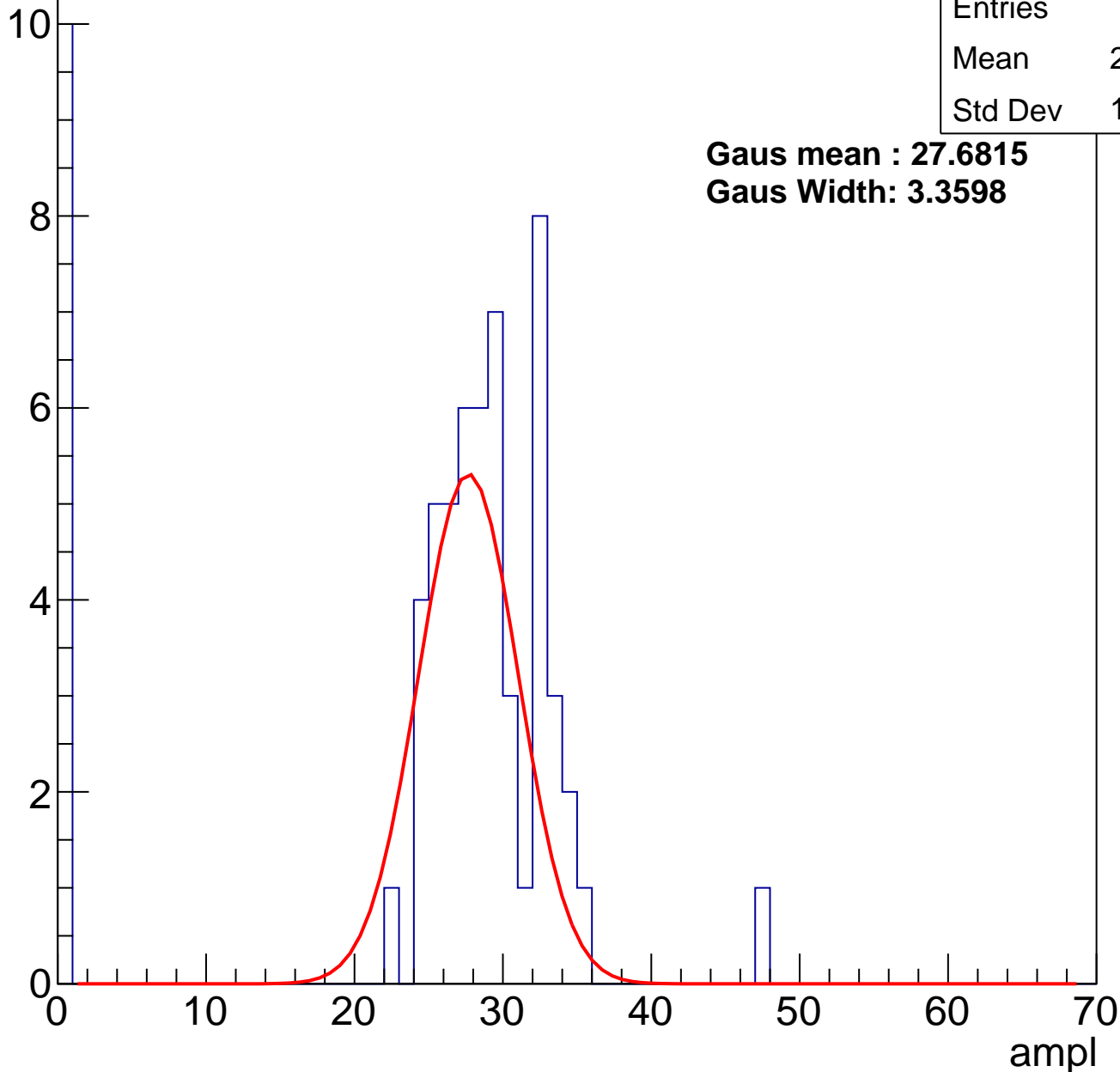
calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	24.32
Std Dev	11.18

Gaus mean : 27.6815

Gaus Width: 3.3598

Entry



B1L103S, U13-ch122, adc1

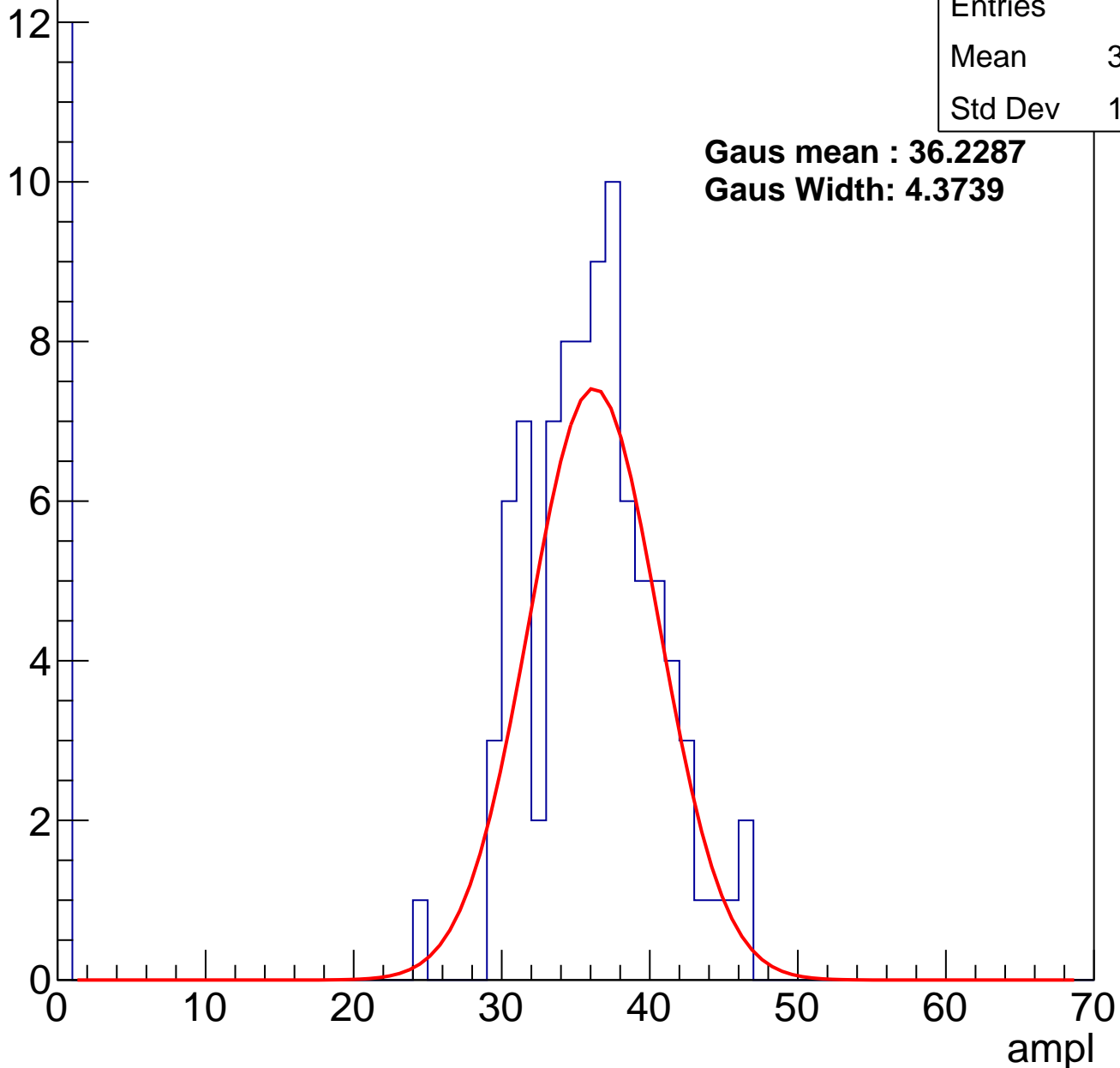
calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	31.54
Std Dev	12.24

Gaus mean : 36.2287

Gaus Width: 4.3739

Entry



B1L103S, U13-ch122, adc2

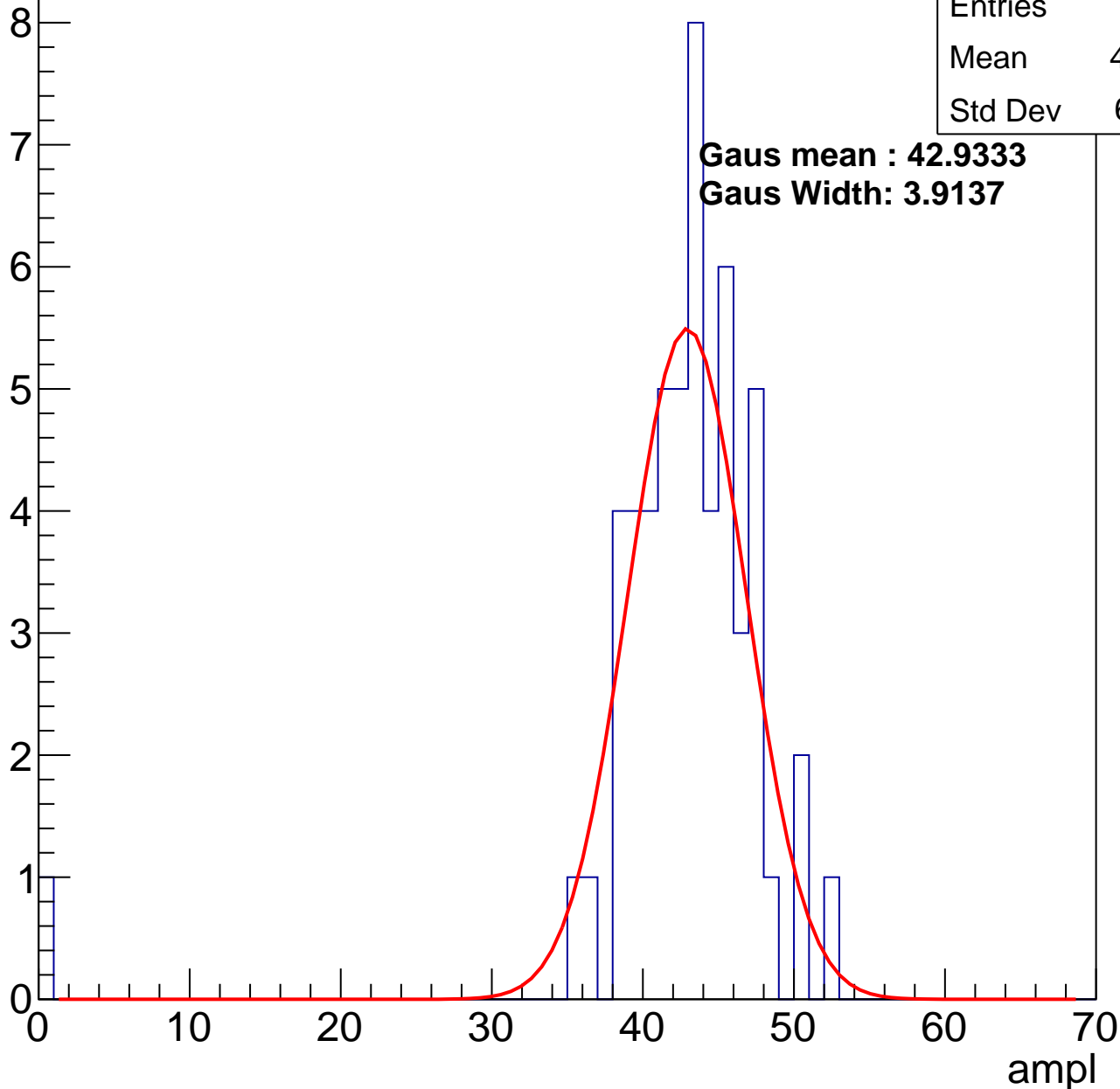
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	42.13
Std Dev	6.721

Gaus mean : 42.9333

Gaus Width: 3.9137

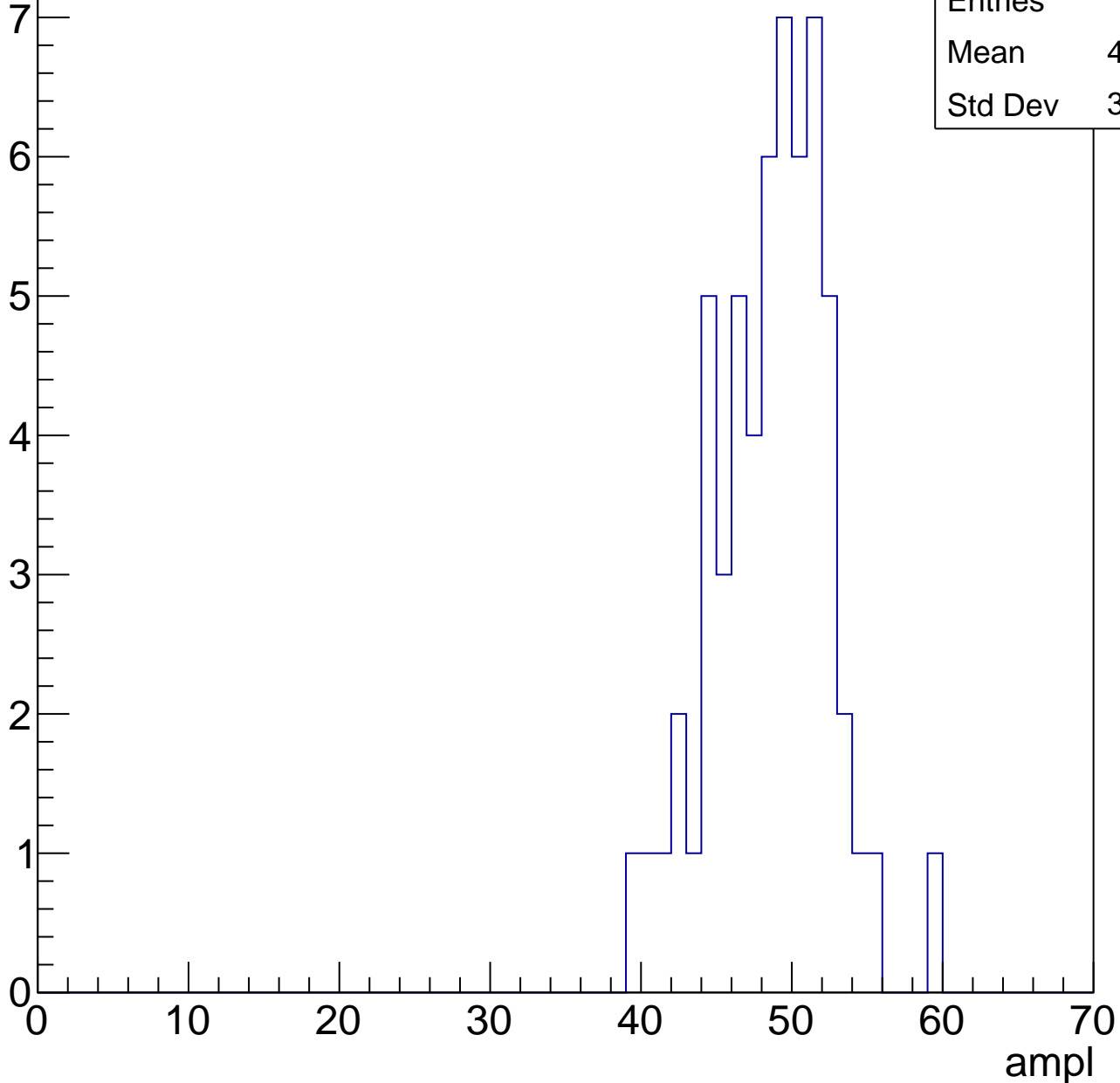


B1L103S, U13-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	48.17
Std Dev	3.796

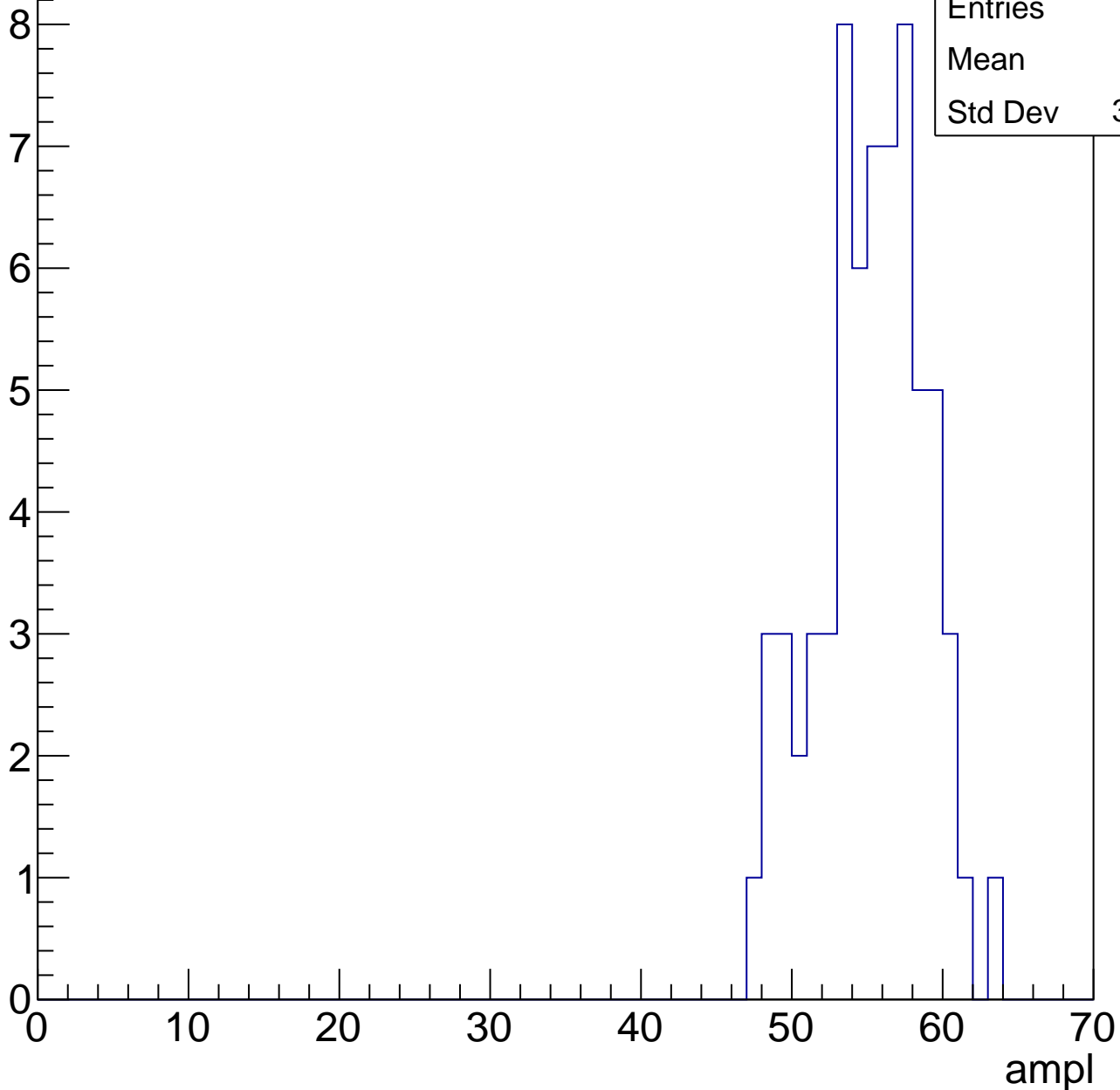


B1L103S, U13-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	54.8
Std Dev	3.521

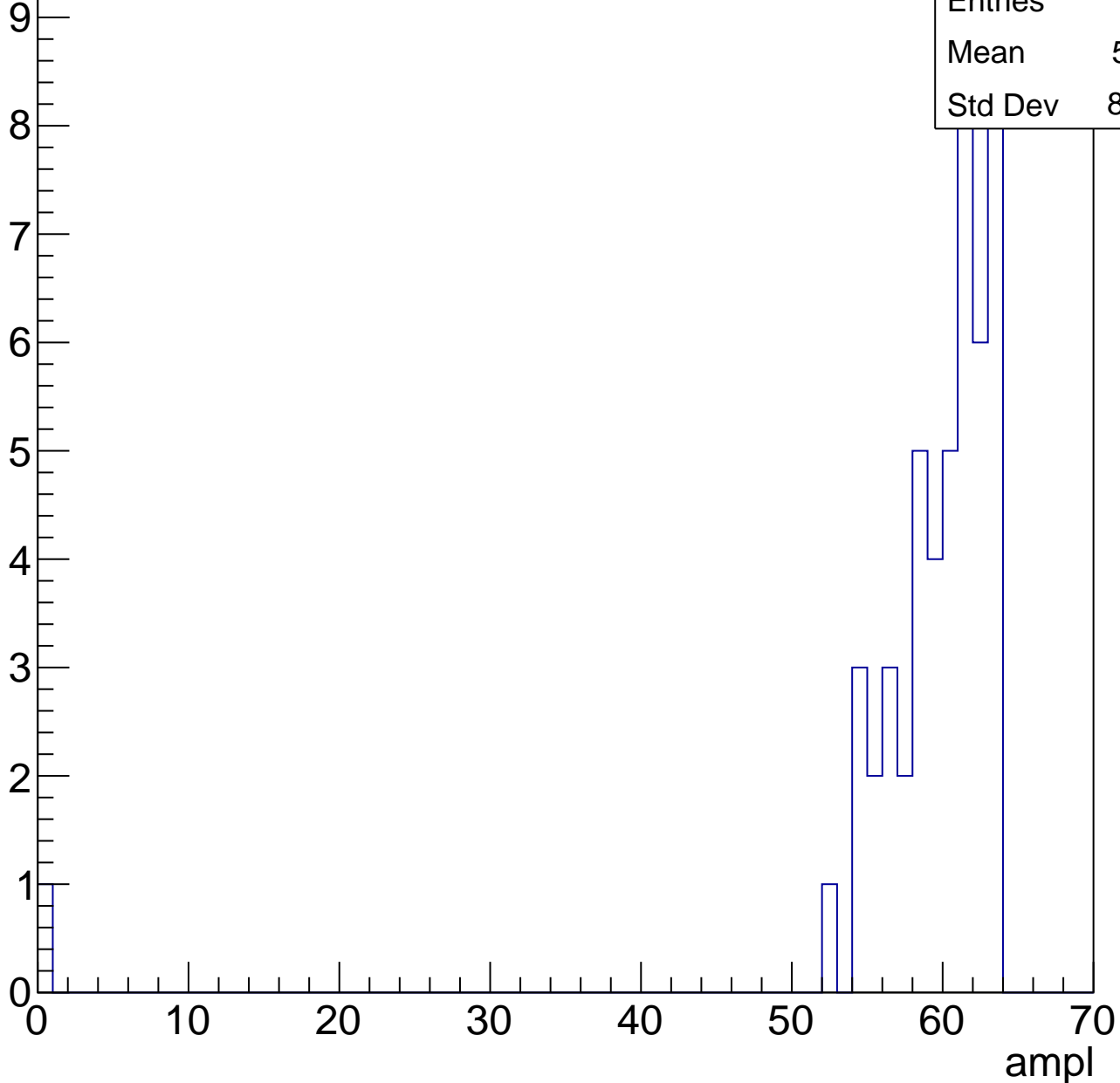


B1L103S, U13-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

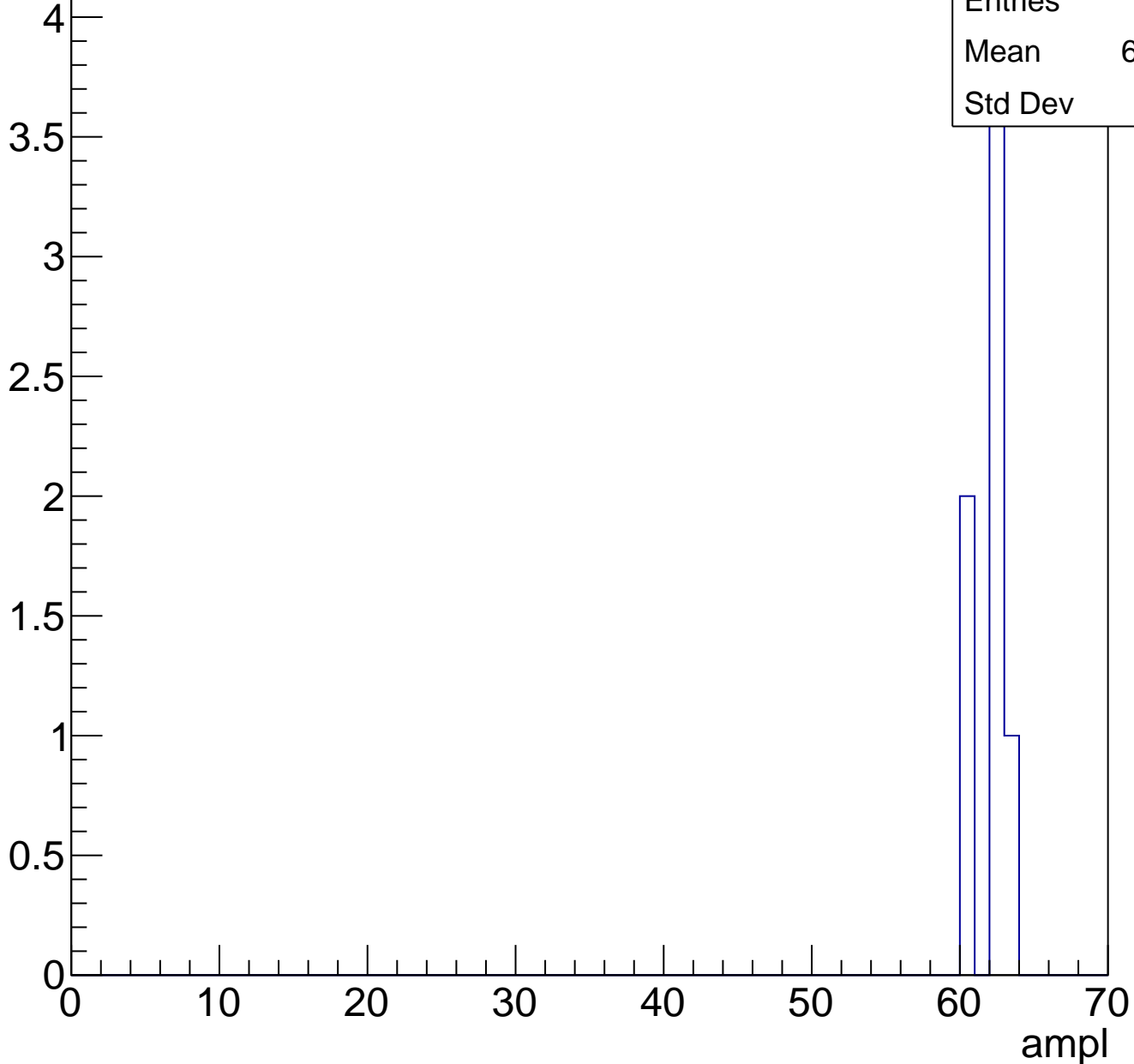
Entries	49
Mean	58.31
Std Dev	8.894



B1L103S, U13-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

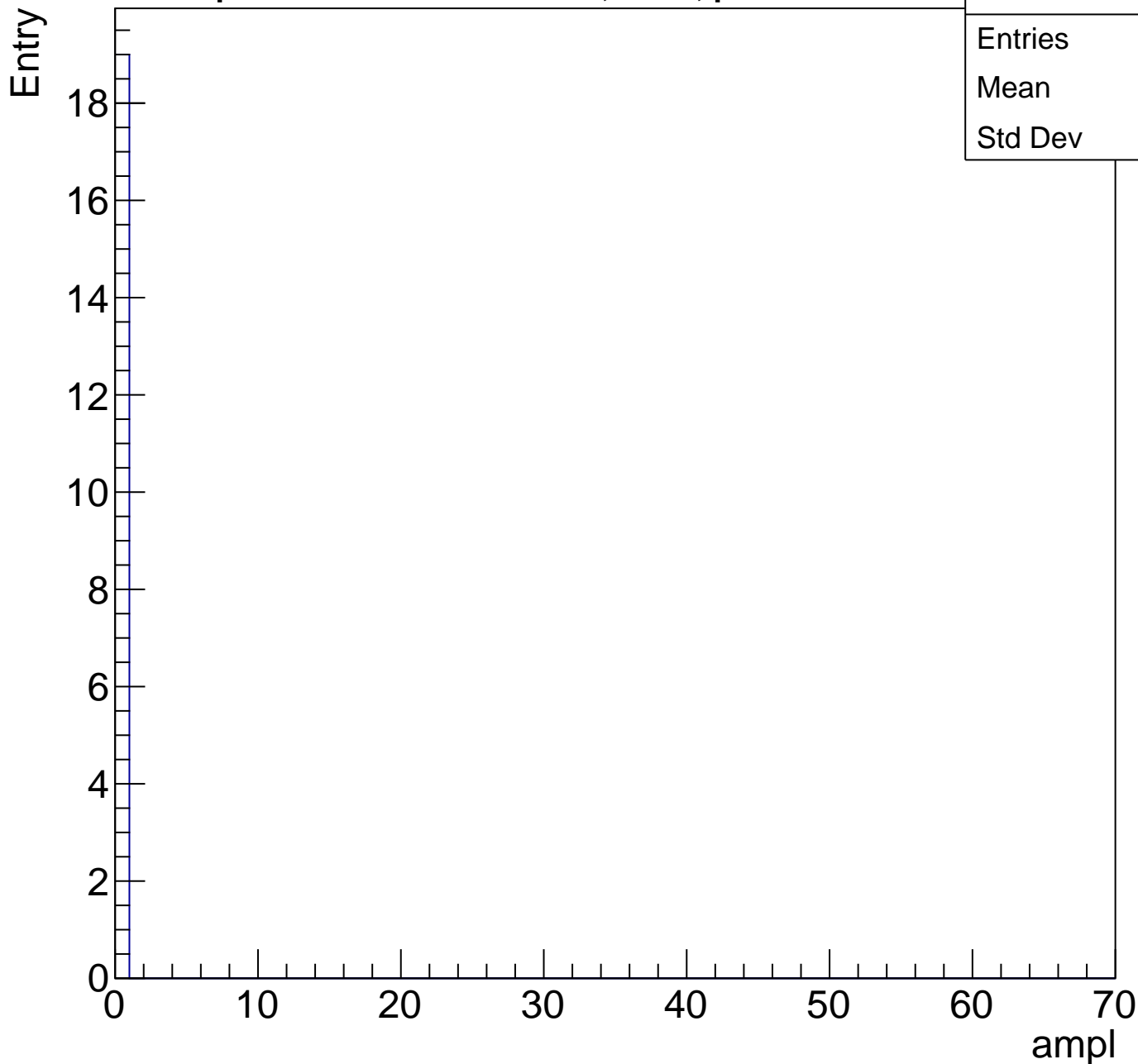
Entry



B1L103S, U13-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U13-ch123, adc0

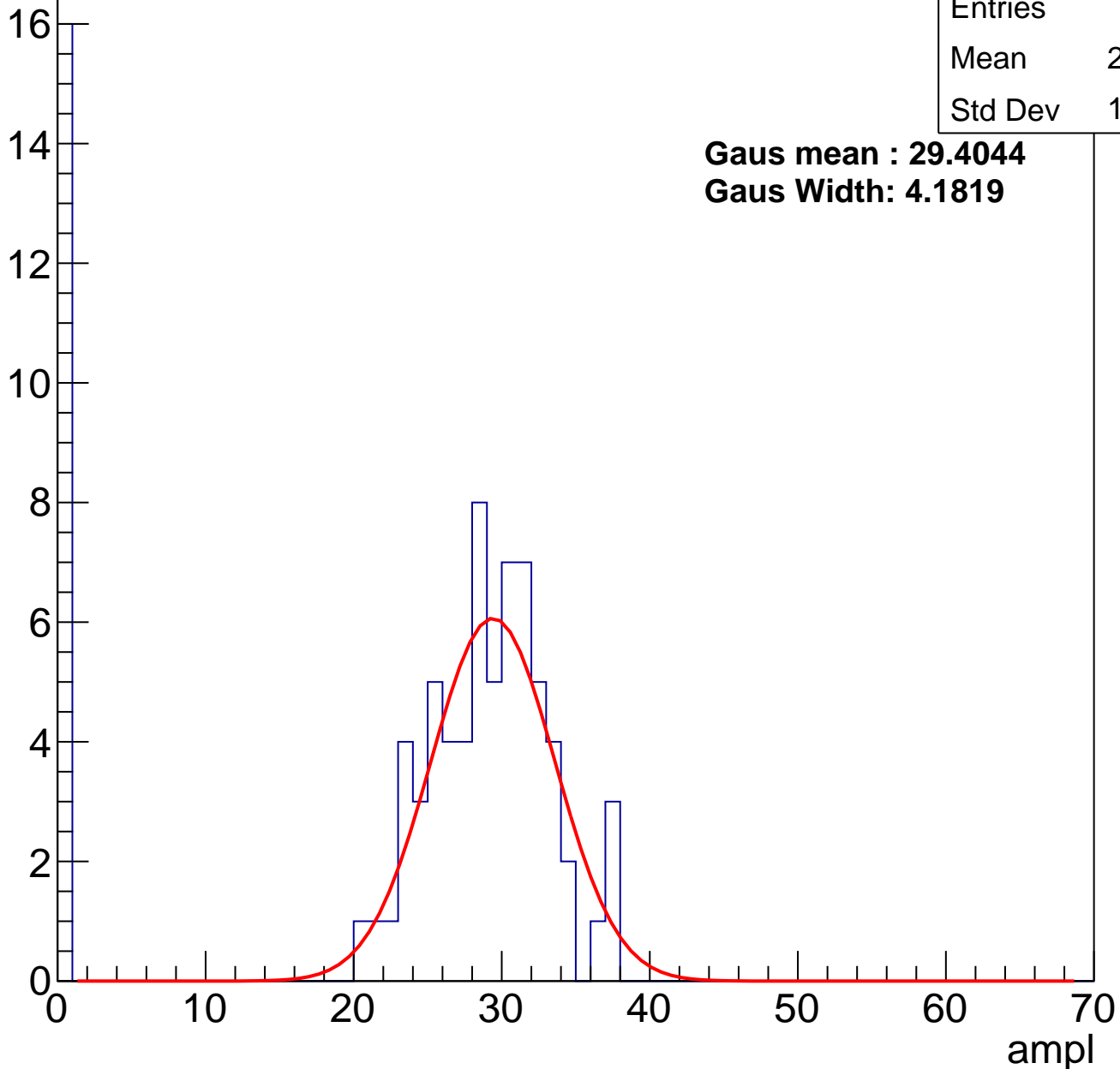
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	23.05
Std Dev	11.95

Gaus mean : 29.4044

Gaus Width: 4.1819

Entry



B1L103S, U13-ch123, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	30.08
Std Dev	12.77

Gaus mean : 35.6189

Gaus Width: 3.6385

Entry

10

8

6

4

2

0

0

10

20

30

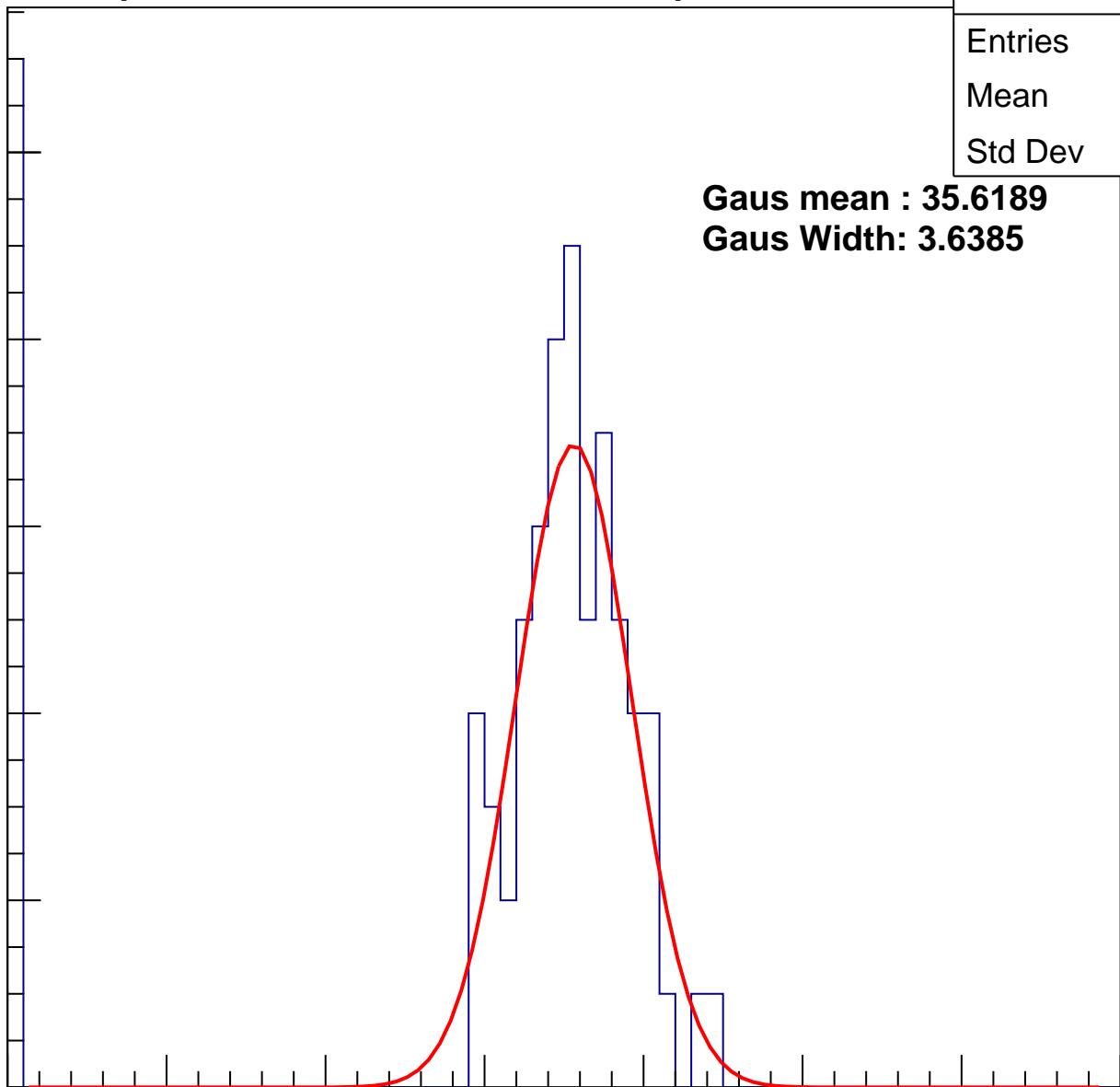
40

50

60

70

ampl



B1L103S, U13-ch123, adc2

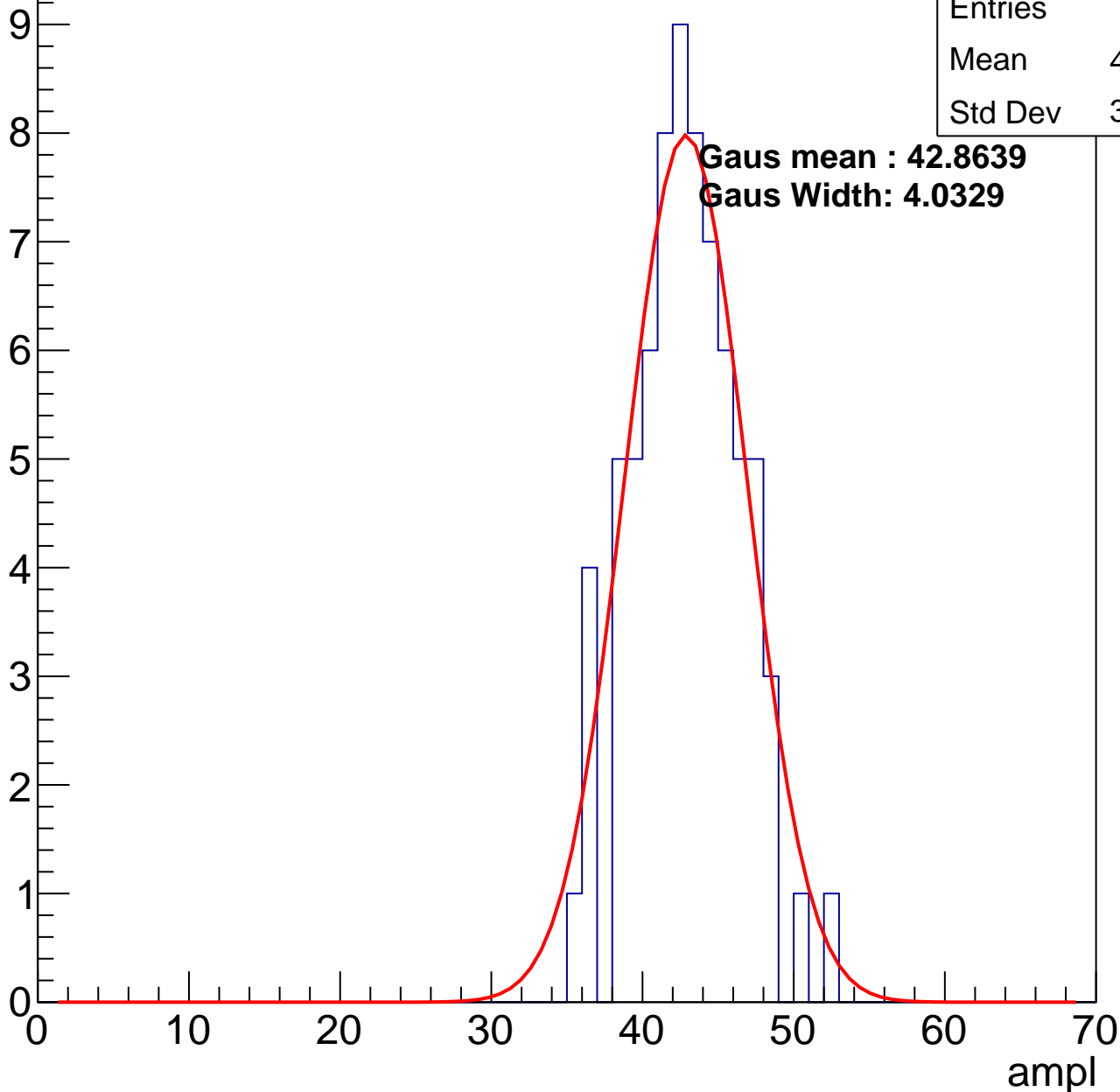
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	42.47
Std Dev	3.488

Gaus mean : 42.8639

Gaus Width: 4.0329



B1L103S, U13-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

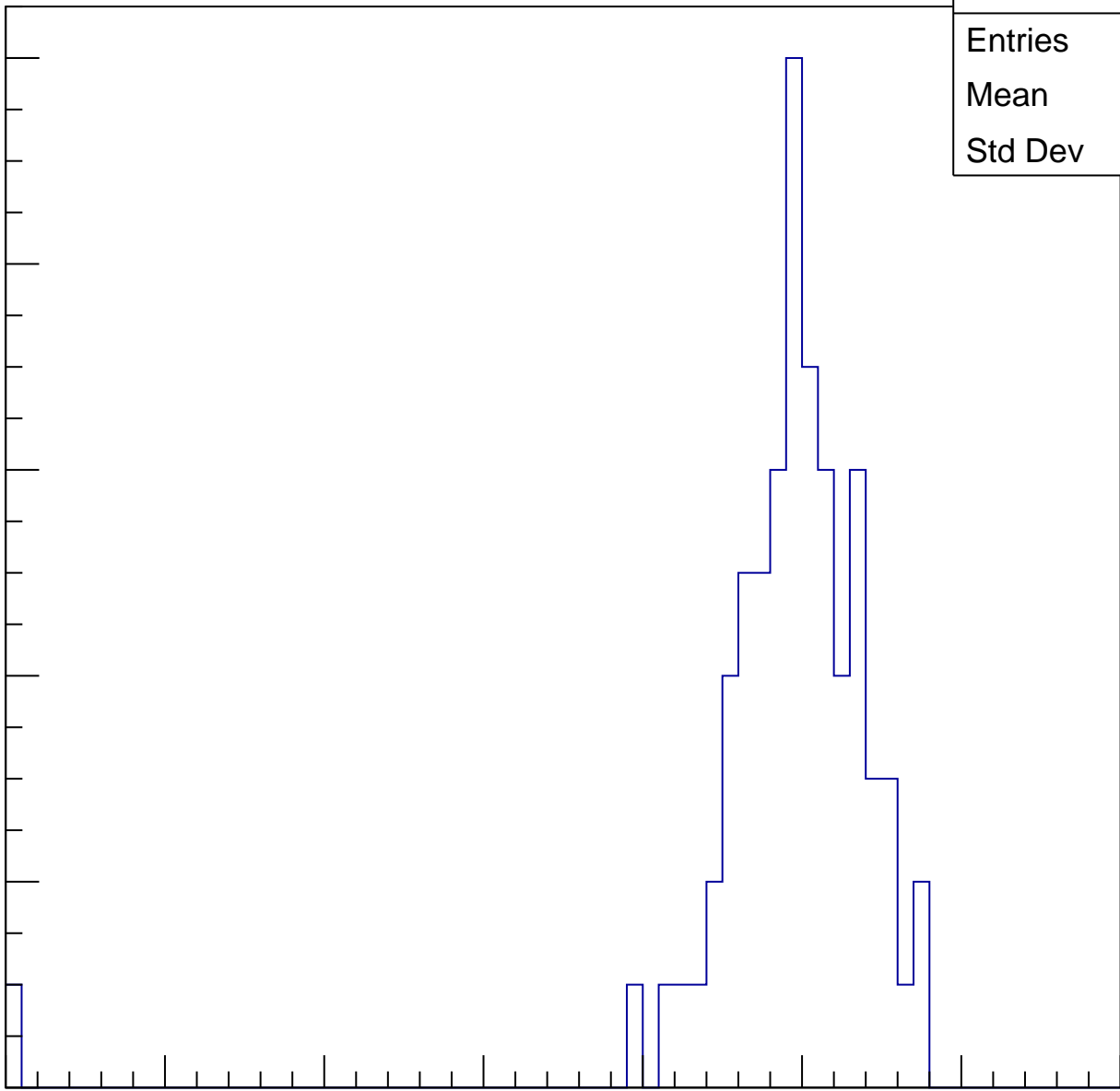
Entries	69
Mean	48.62
Std Dev	6.966

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

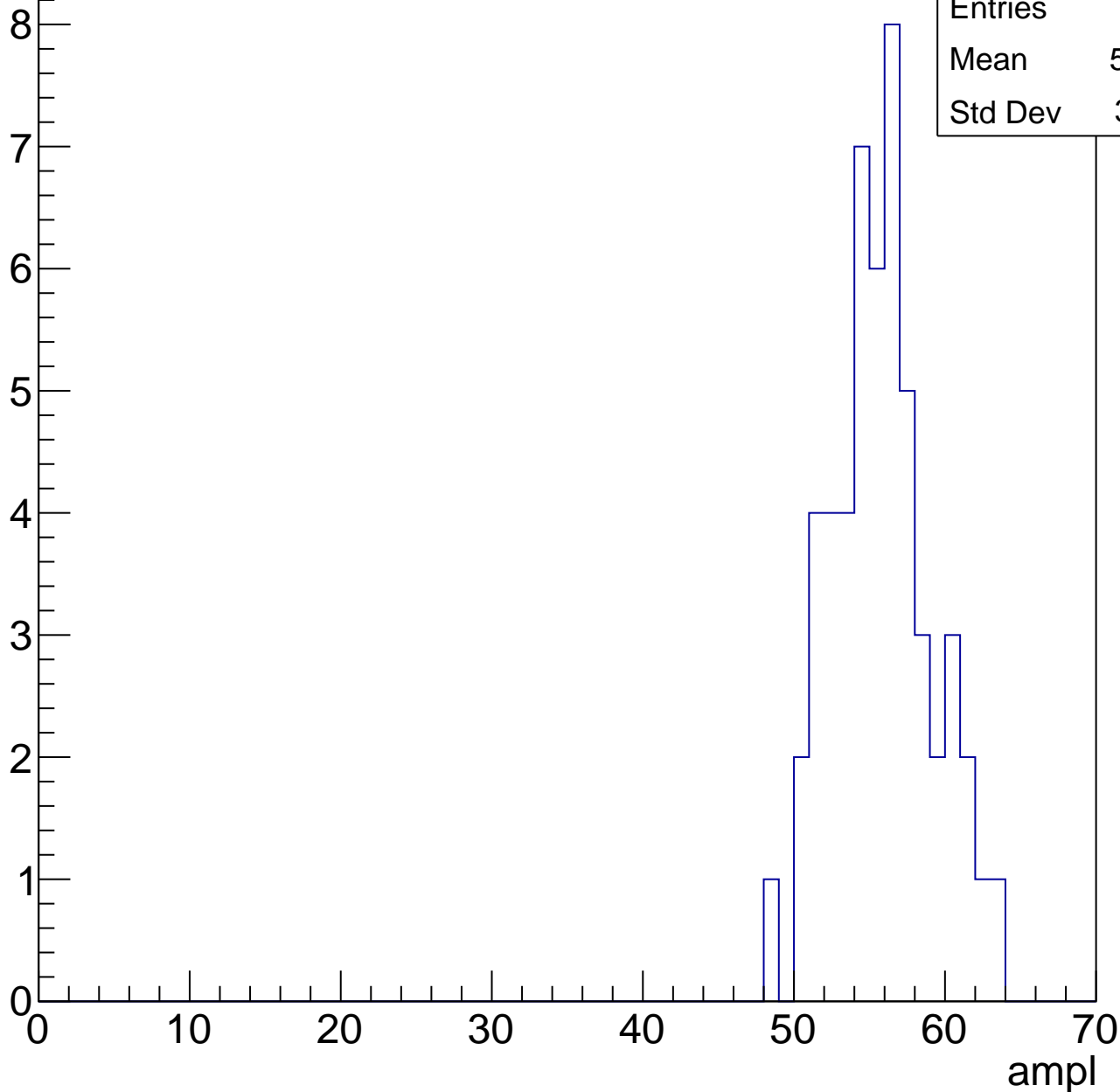


B1L103S, U13-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	55.32
Std Dev	3.261

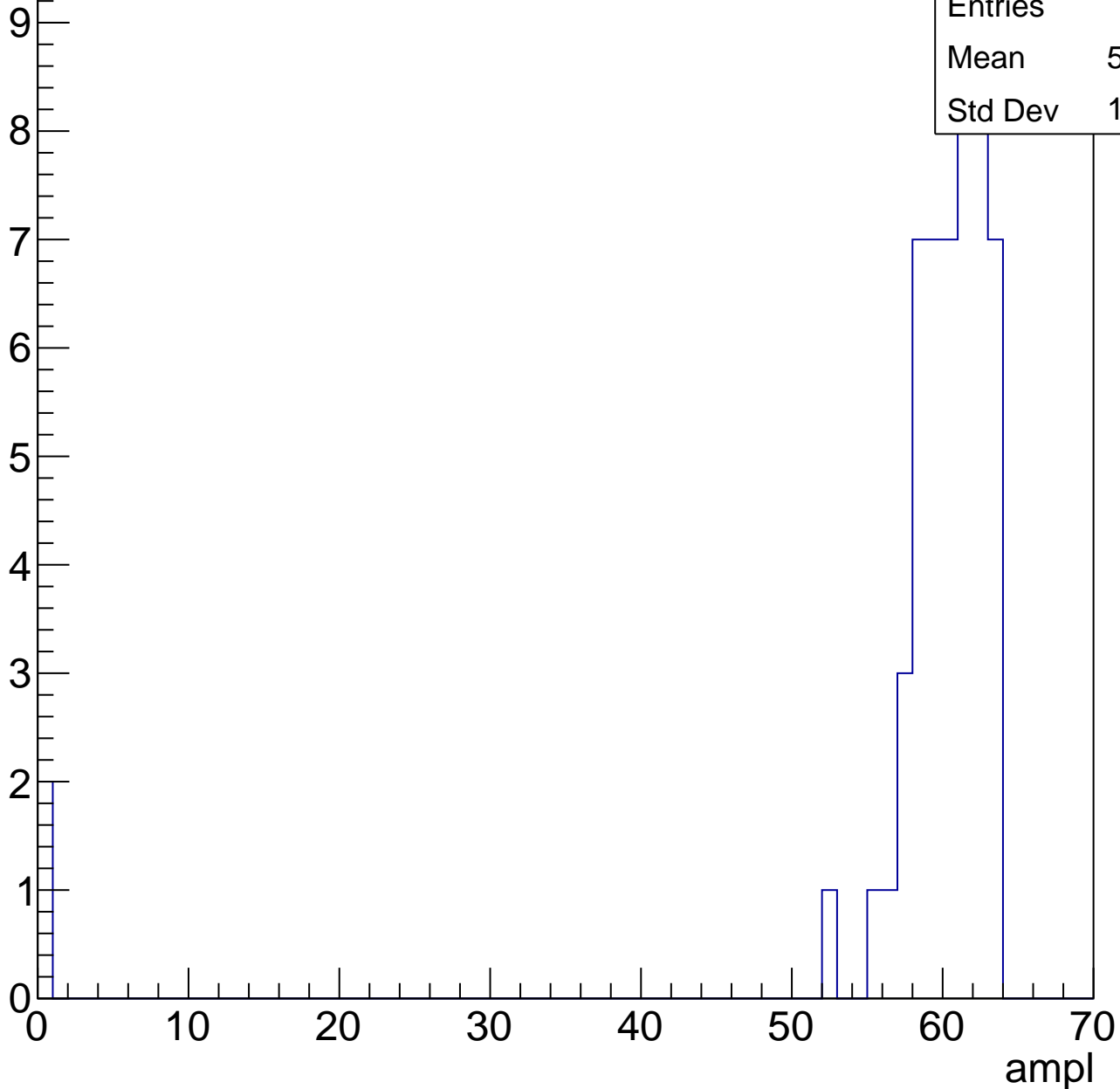


B1L103S, U13-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.74
Std Dev	11.66



B1L103S, U13-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

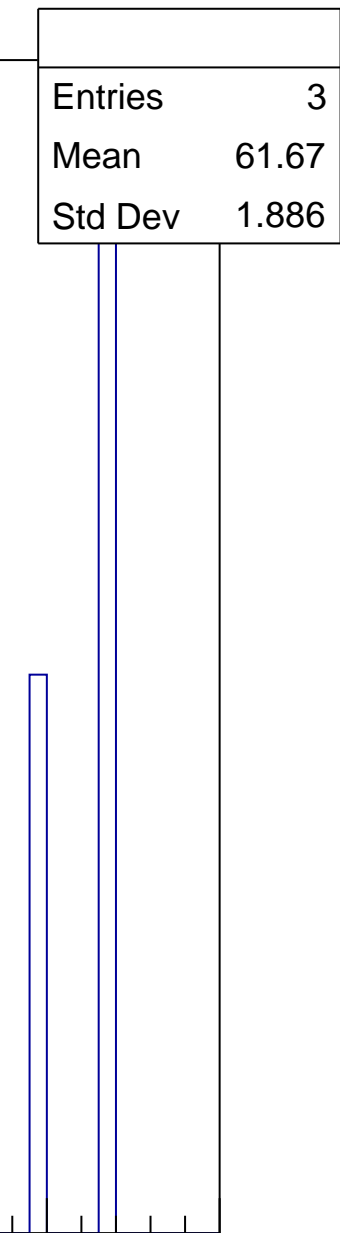
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	1.886

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch124, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	125
Mean	21.99
Std Dev	13.17

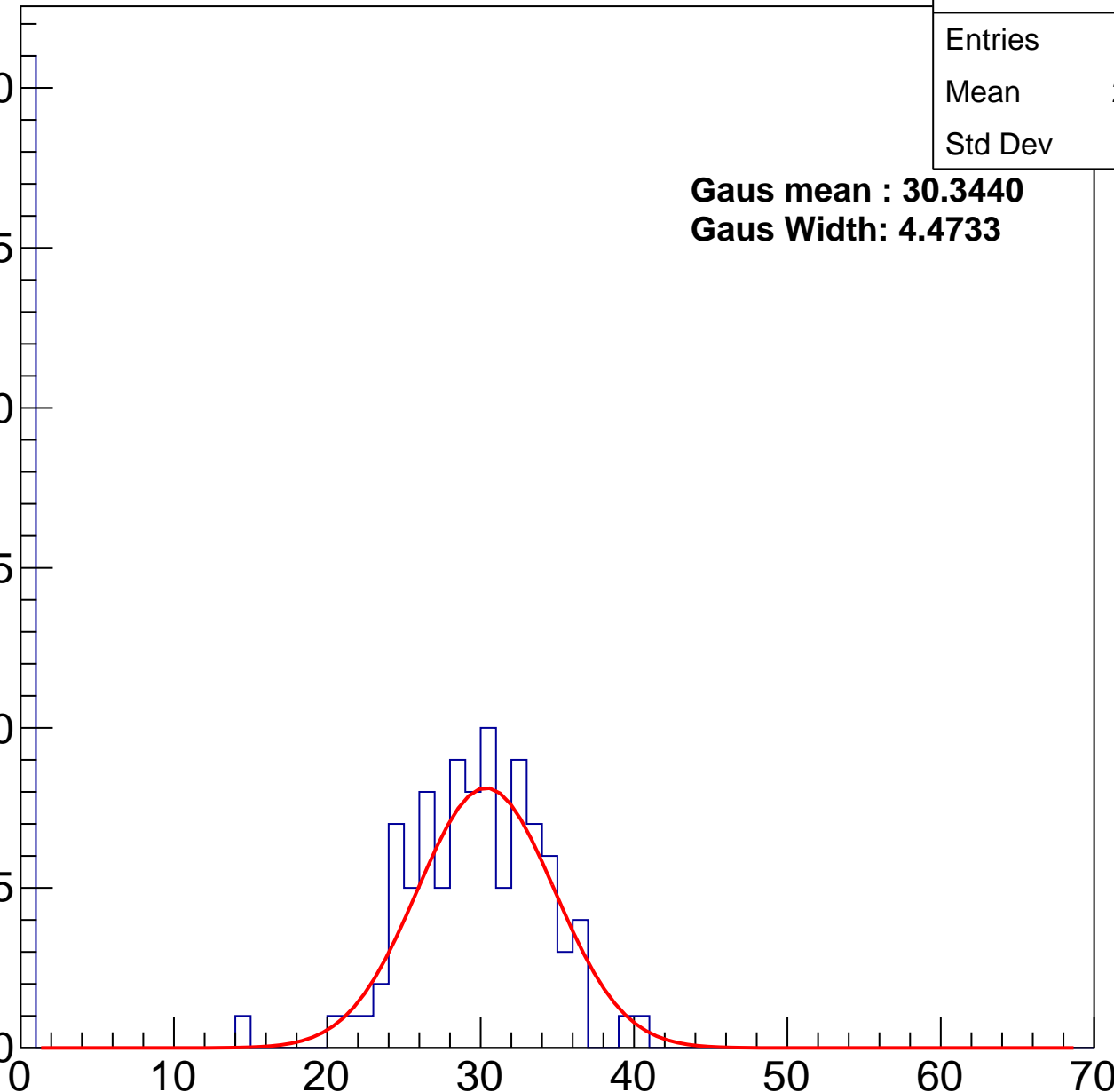
Gaus mean : 30.3440

Gaus Width: 4.4733

Entry

30
25
20
15
10
5
0

ampl



B1L103S, U13-ch124, adc1

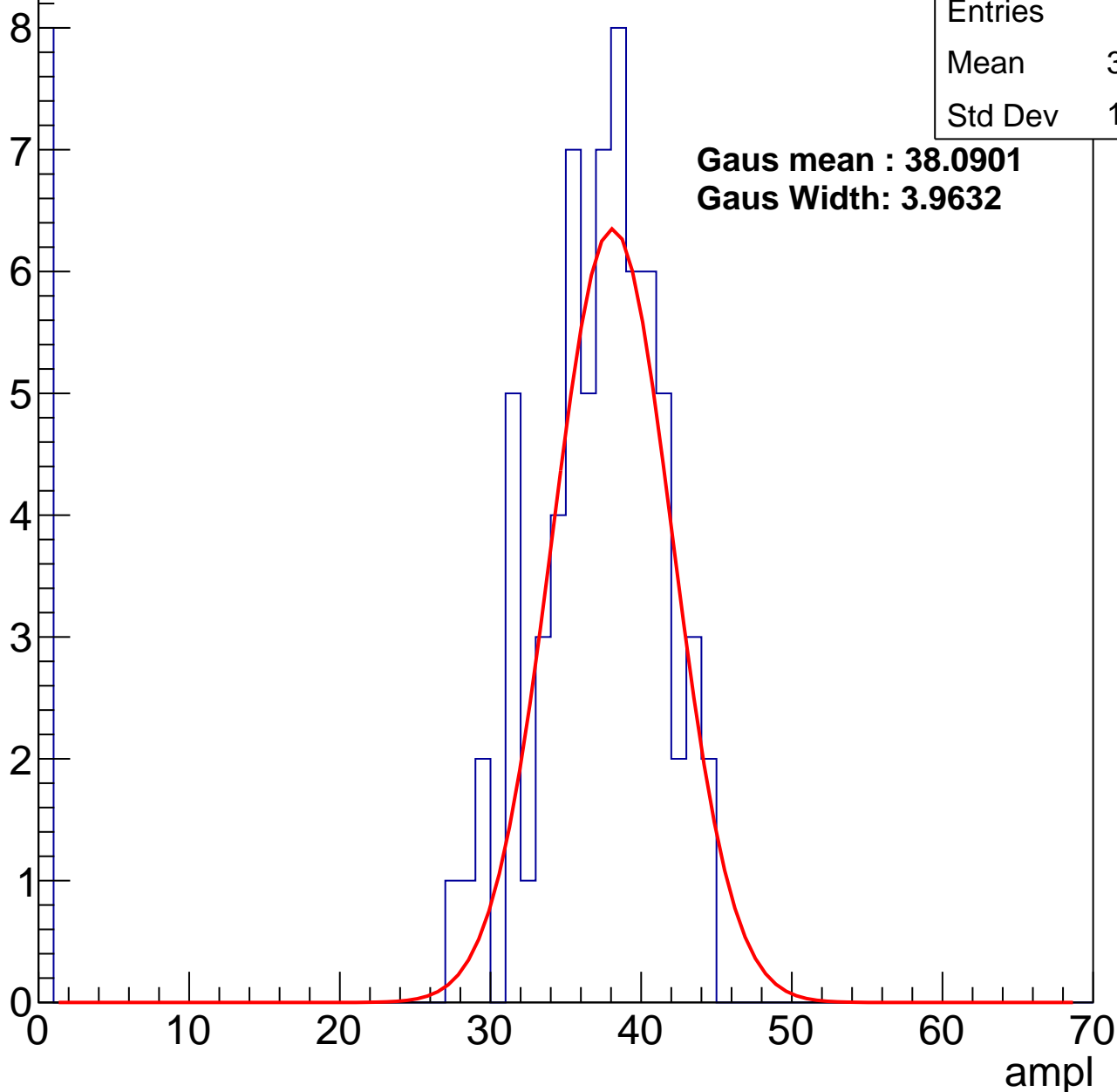
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	32.93
Std Dev	11.89

Gaus mean : 38.0901

Gaus Width: 3.9632



B1L103S, U13-ch124, adc2

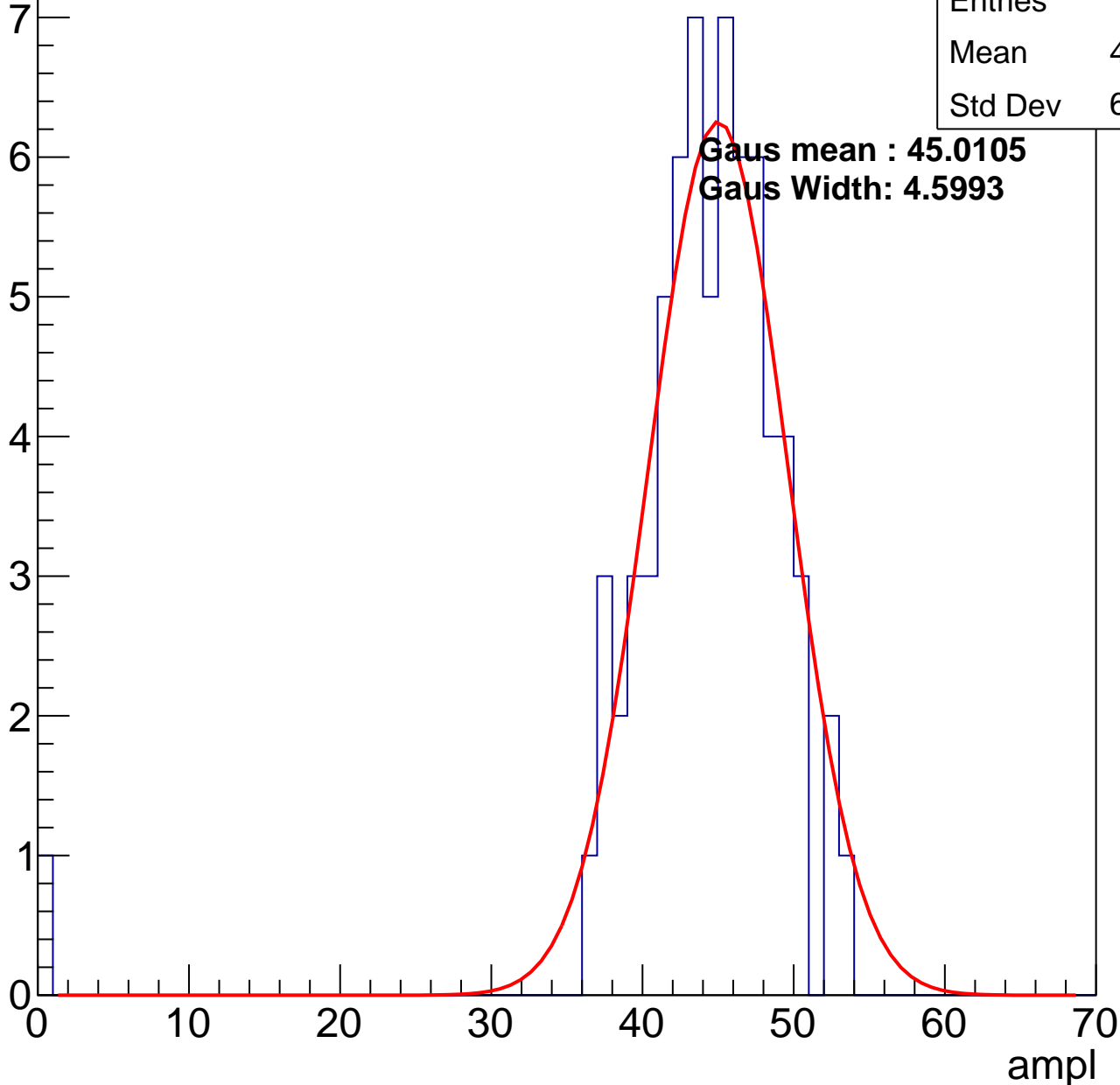
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	43.57
Std Dev	6.562

Gaus mean : 45.0105

Gaus Width: 4.5993

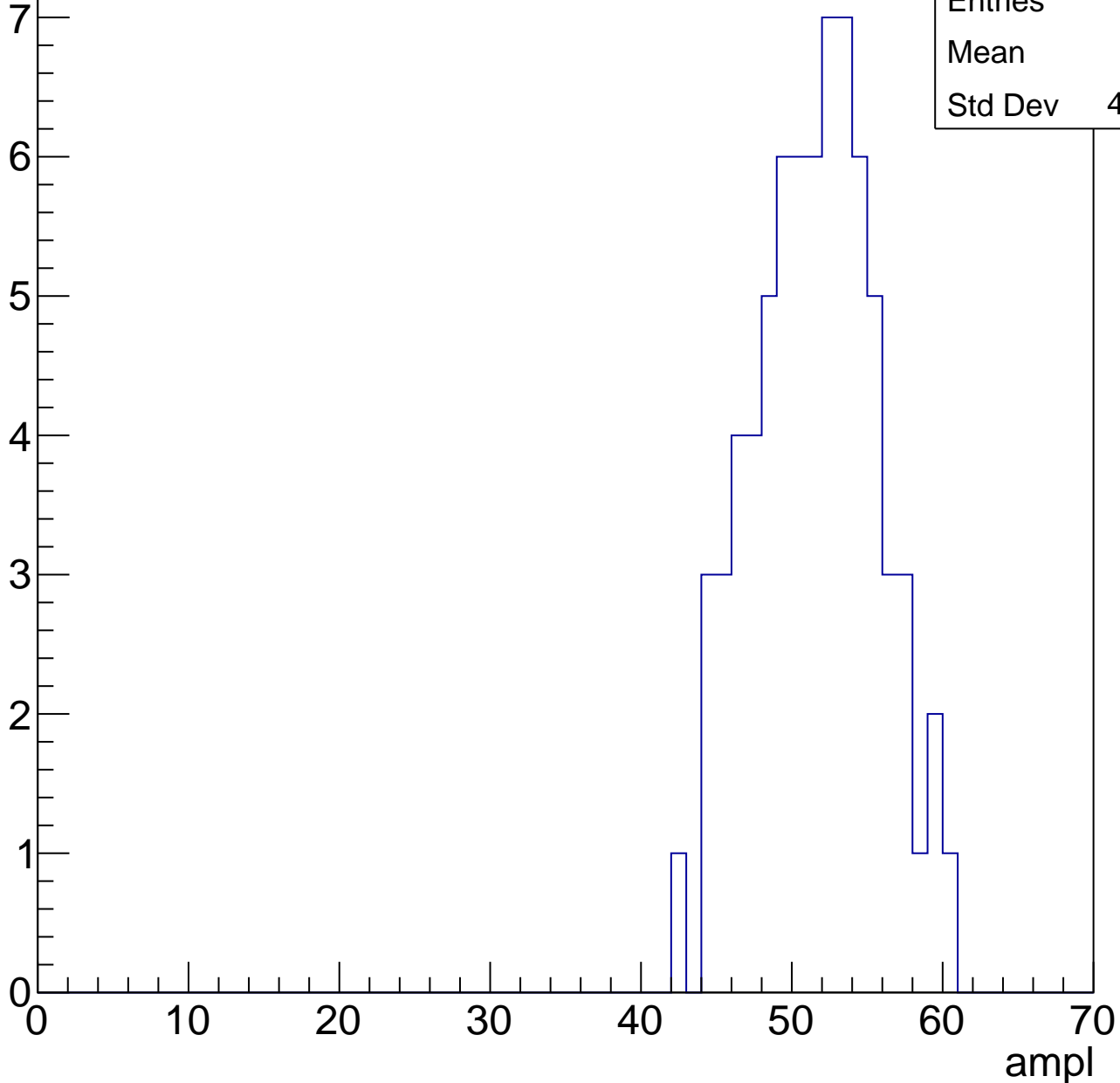


B1L103S, U13-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	51.1
Std Dev	4.028

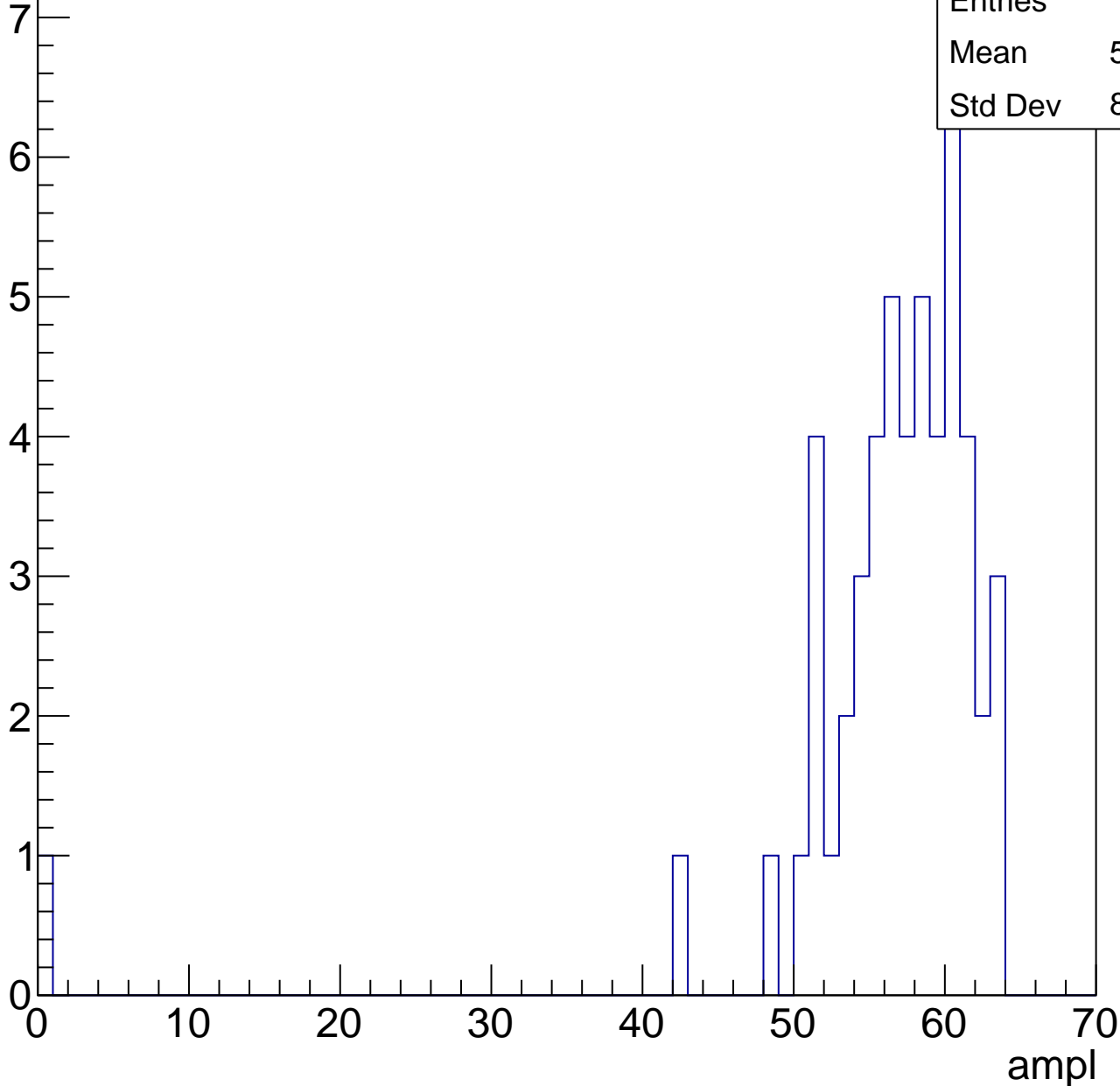


B1L103S, U13-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	55.67
Std Dev	8.844

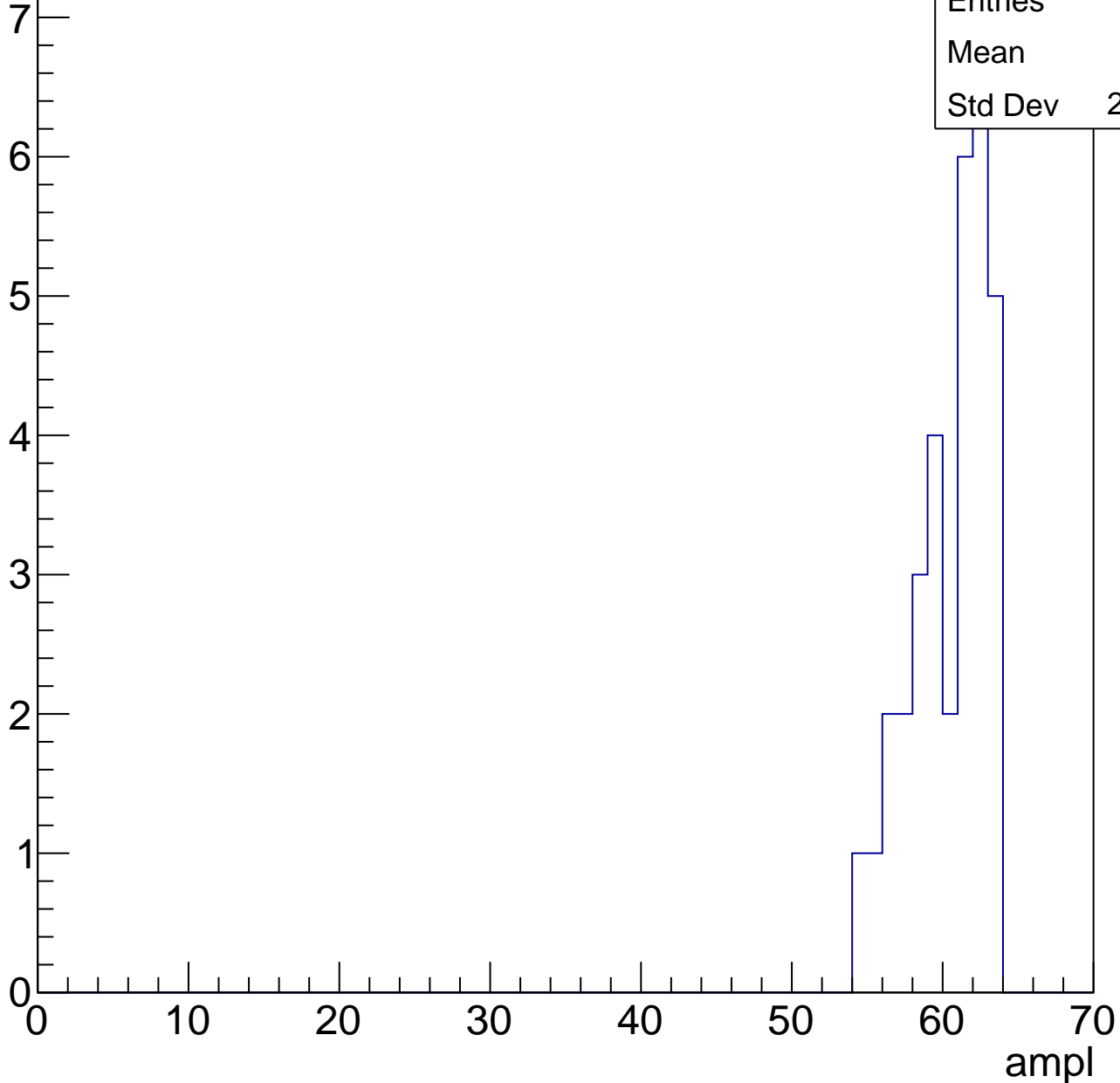


B1L103S, U13-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	60
Std Dev	2.498



B1L103S, U13-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	60.25
Std Dev	2.773

ampl

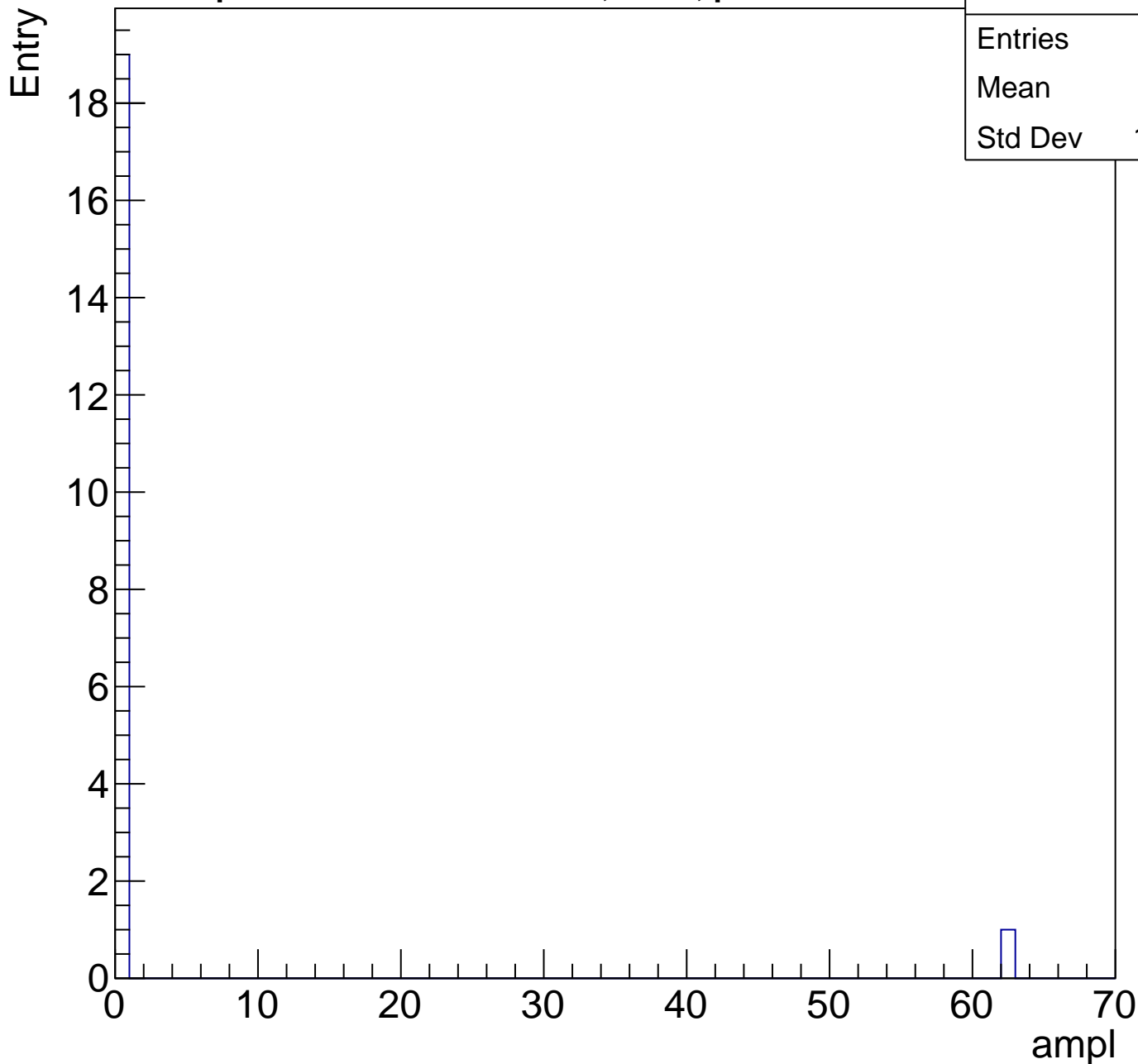
0 10 20 30 40 50 60 70



B1L103S, U13-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51



B1L103S, U13-ch125, adc0

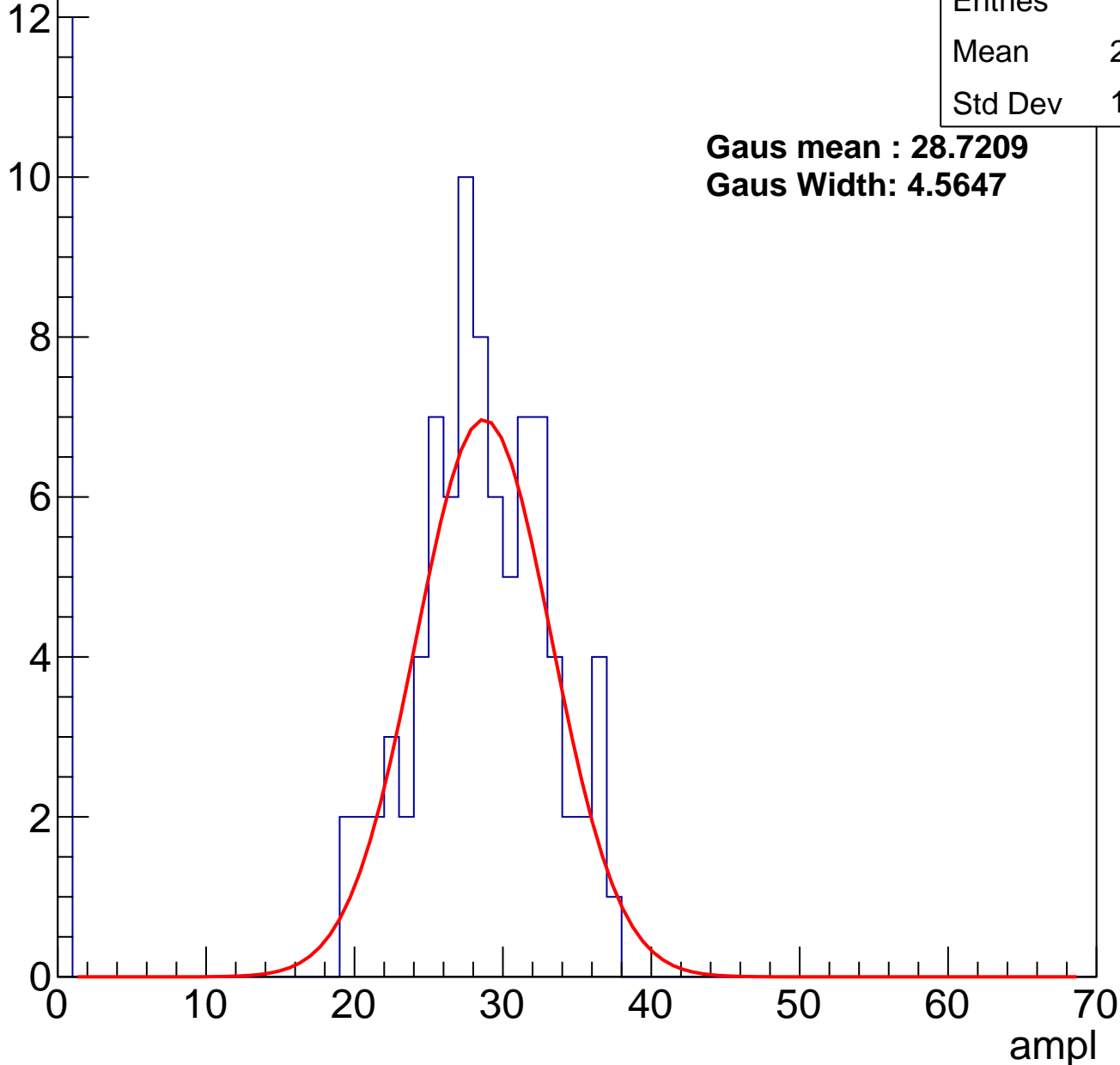
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	24.68
Std Dev	10.14

Gaus mean : 28.7209

Gaus Width: 4.5647

Entry



B1L103S, U13-ch125, adc1

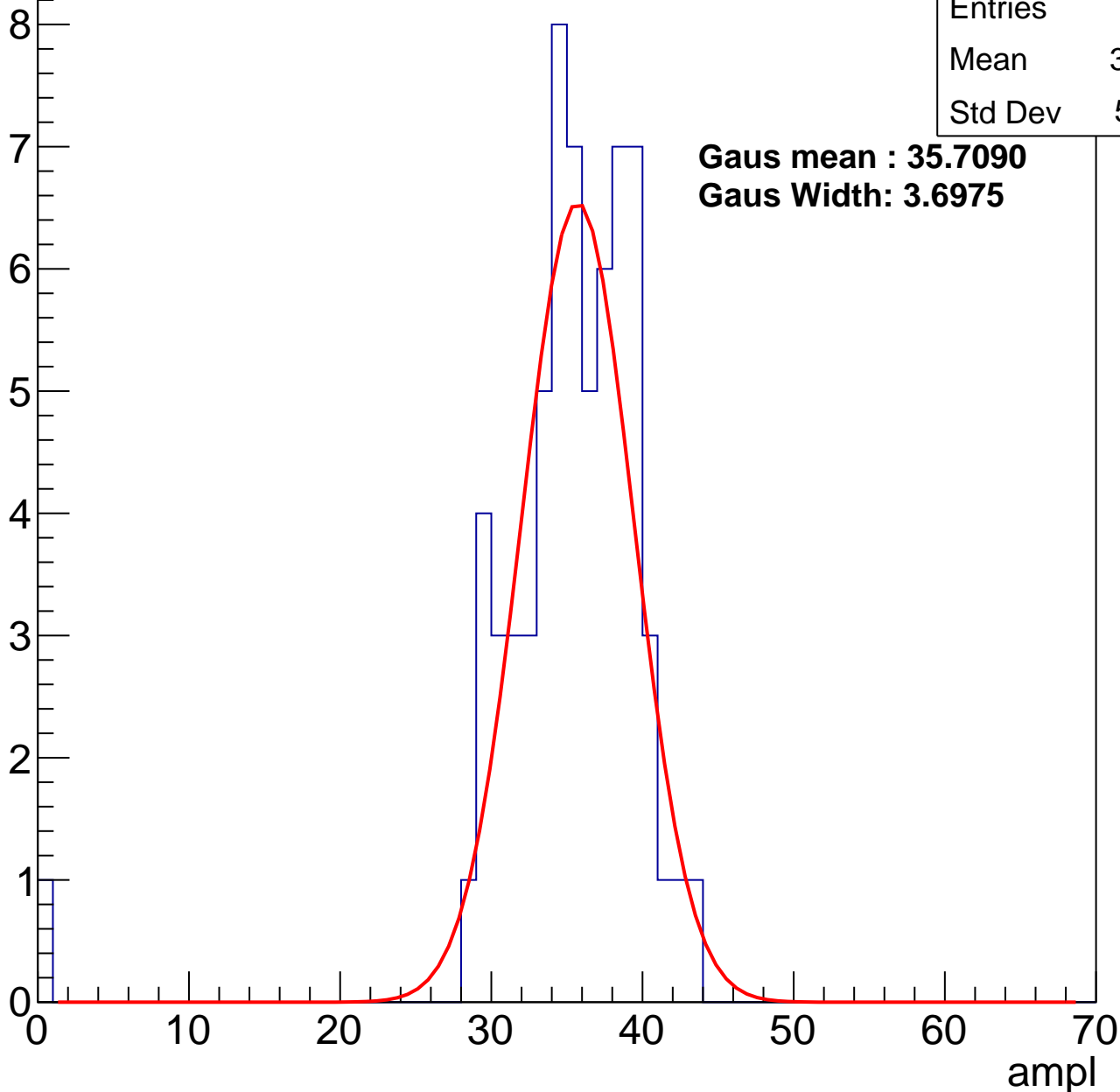
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.73
Std Dev	5.531

Gaus mean : 35.7090

Gaus Width: 3.6975



B1L103S, U13-ch125, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	34.21
Std Dev	16.45

Gaus mean : 42.7033

Gaus Width: 3.3492

Entry

12

10

8

6

4

2

0

0

10

20

30

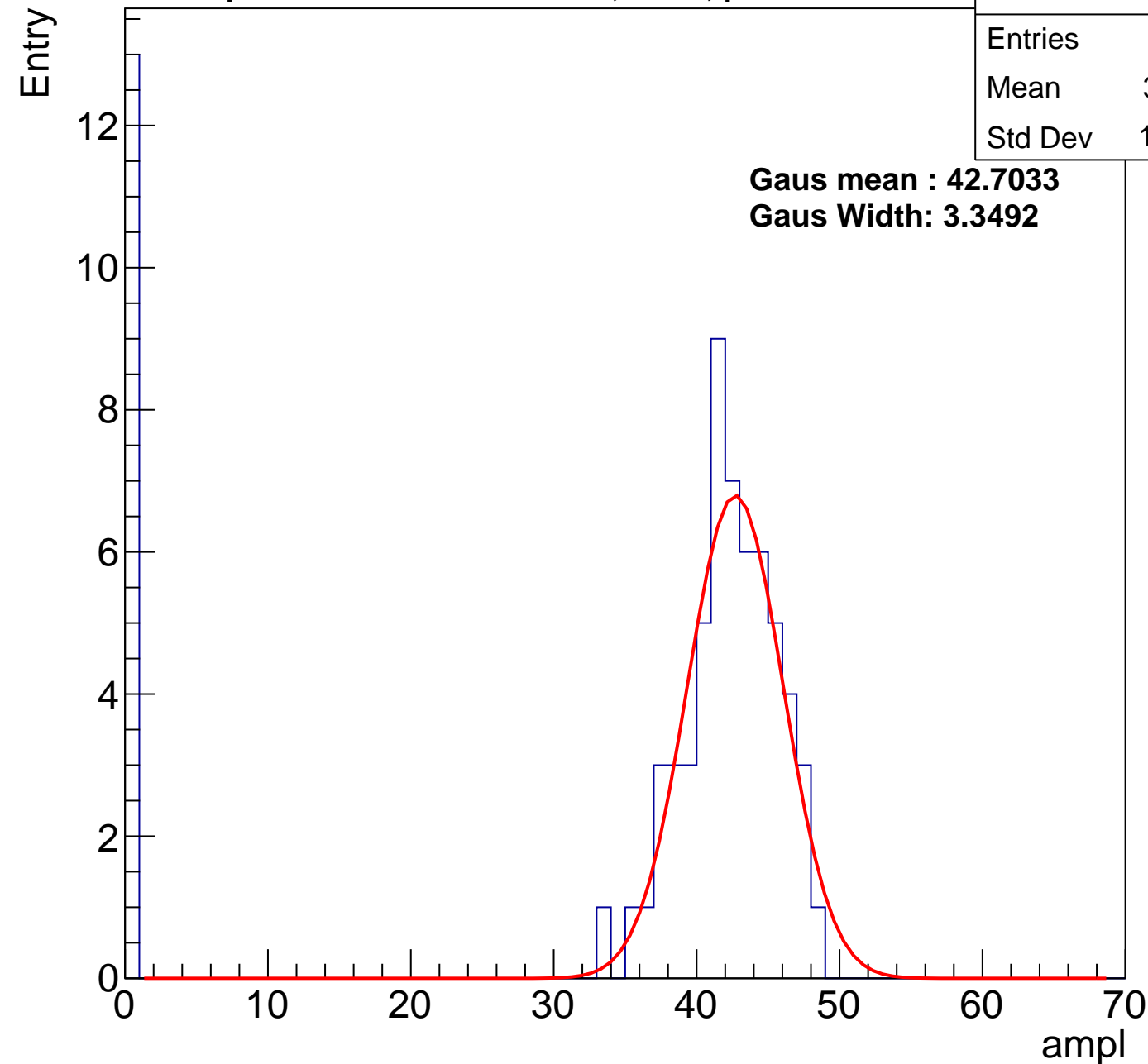
40

50

60

70

ampl

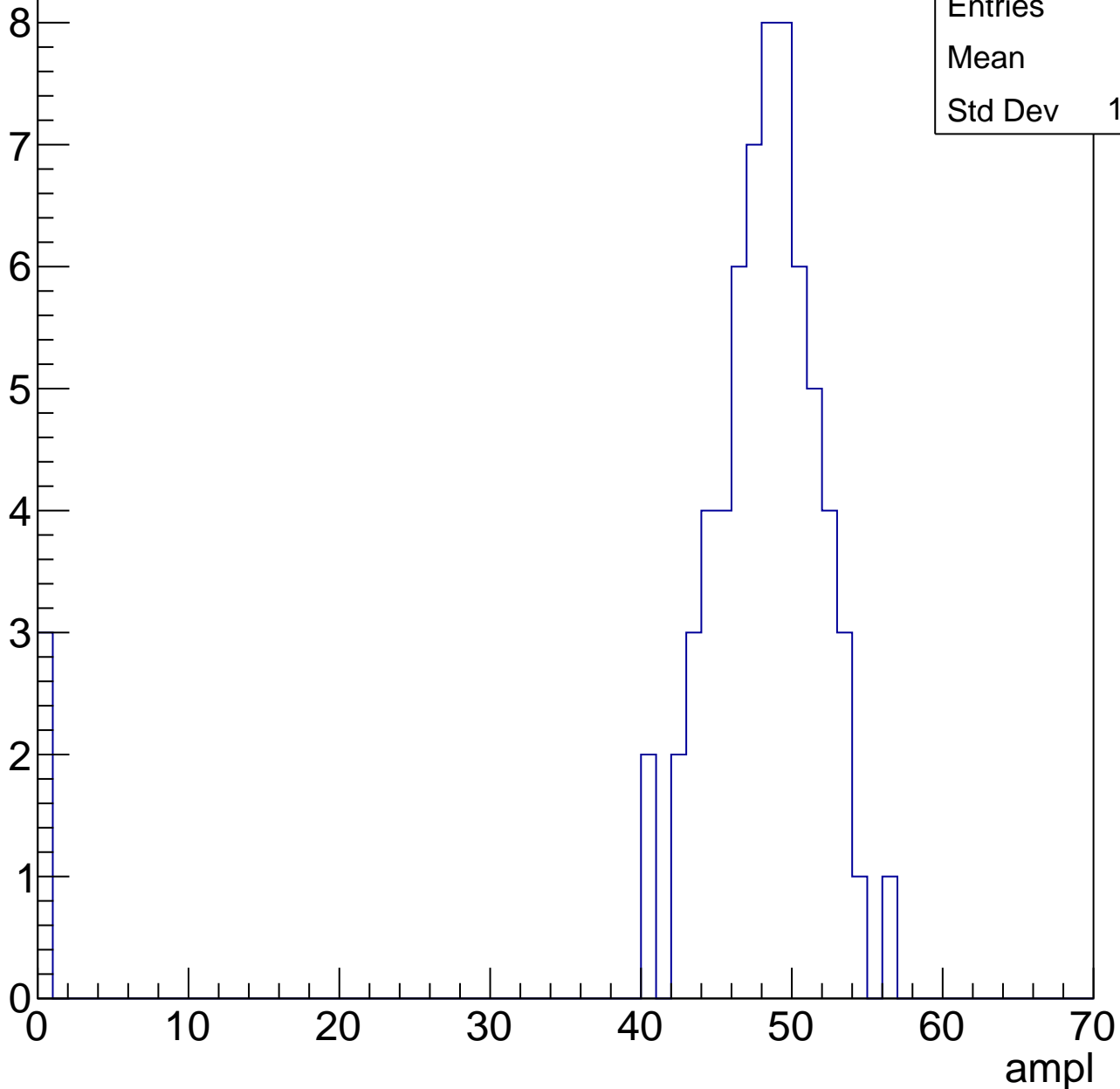


B1L103S, U13-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	45.7
Std Dev	10.42

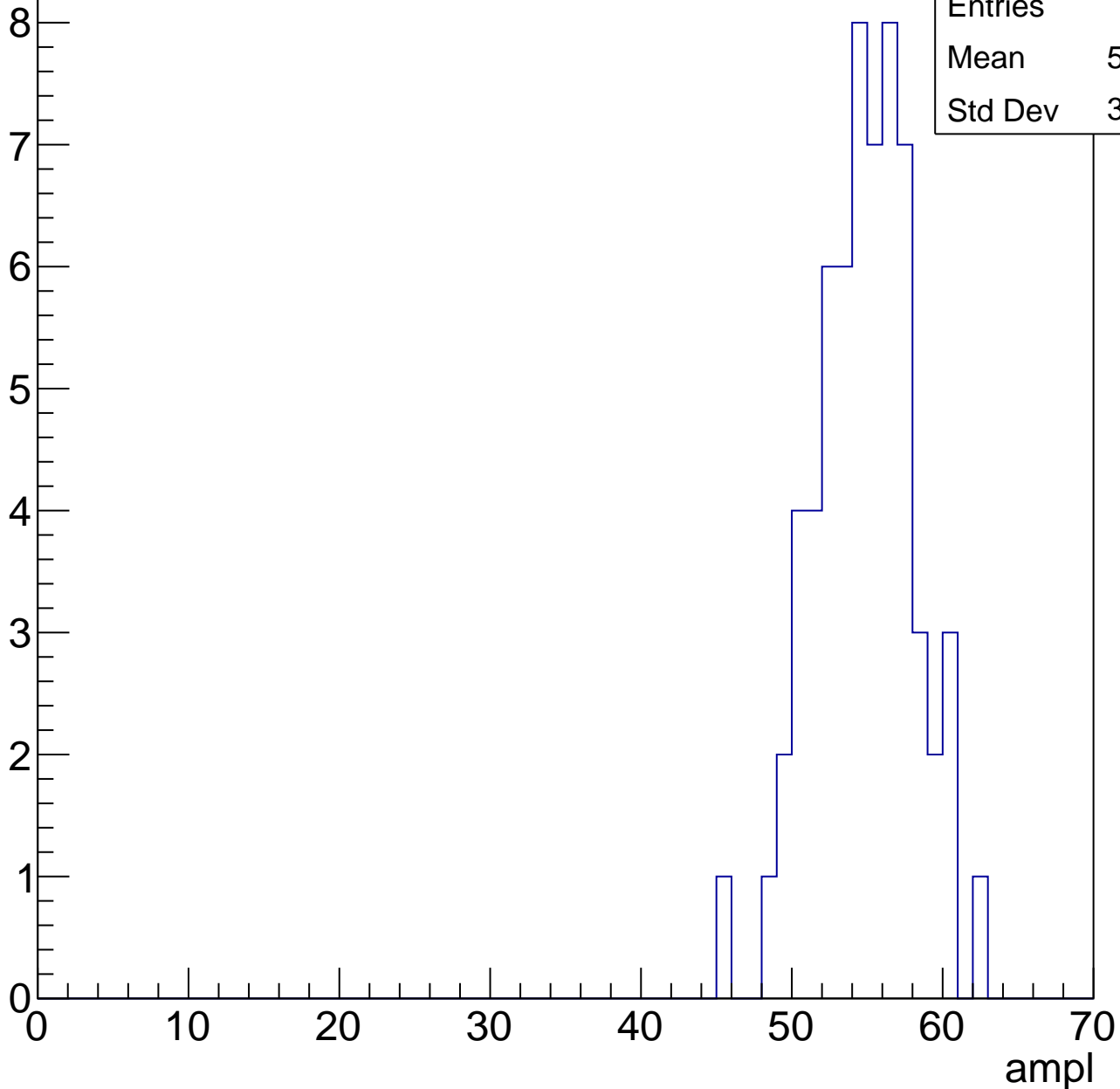


B1L103S, U13-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	54.33
Std Dev	3.242

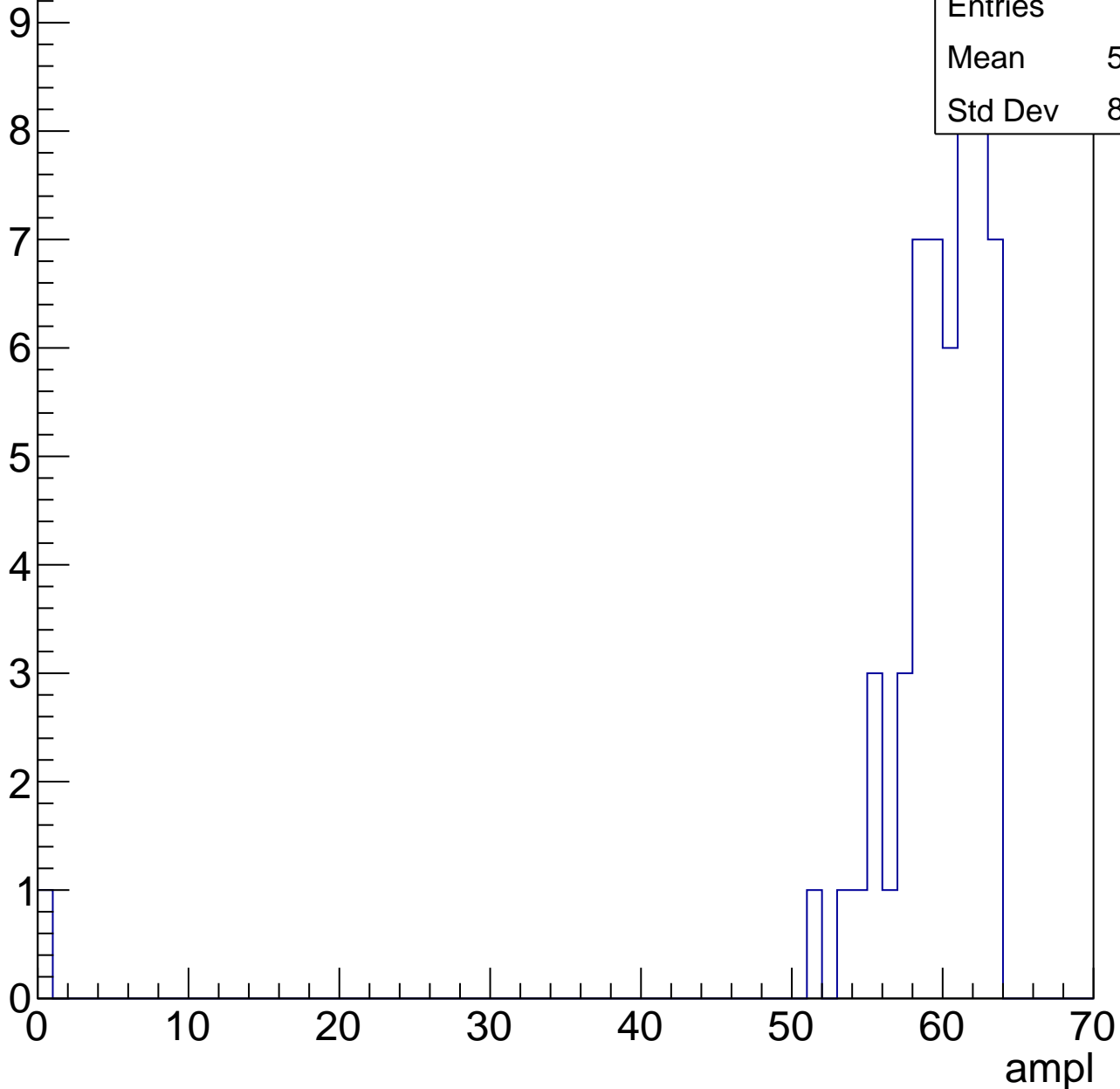


B1L103S, U13-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

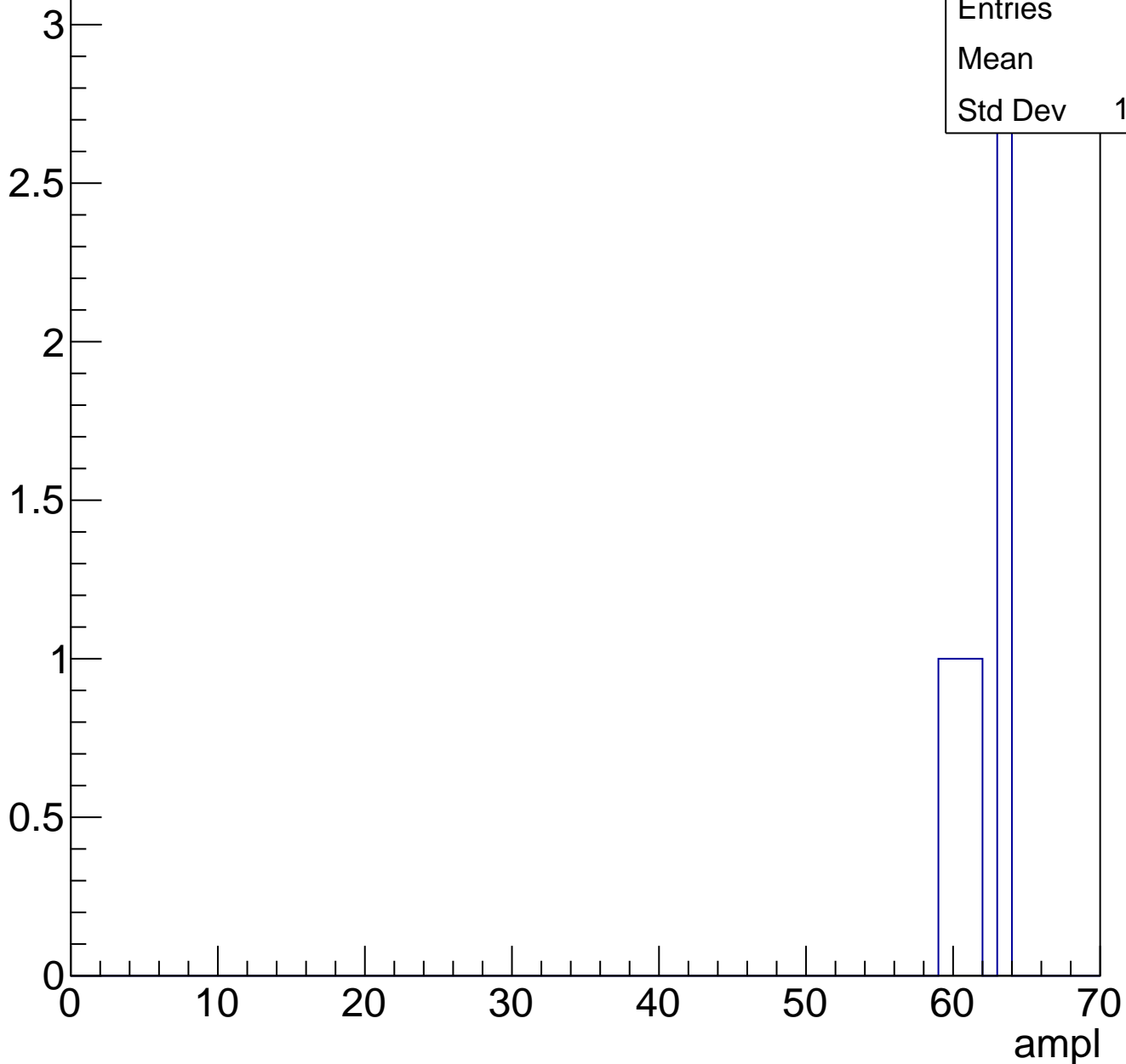
Entries	56
Mean	58.52
Std Dev	8.349



B1L103S, U13-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U13-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

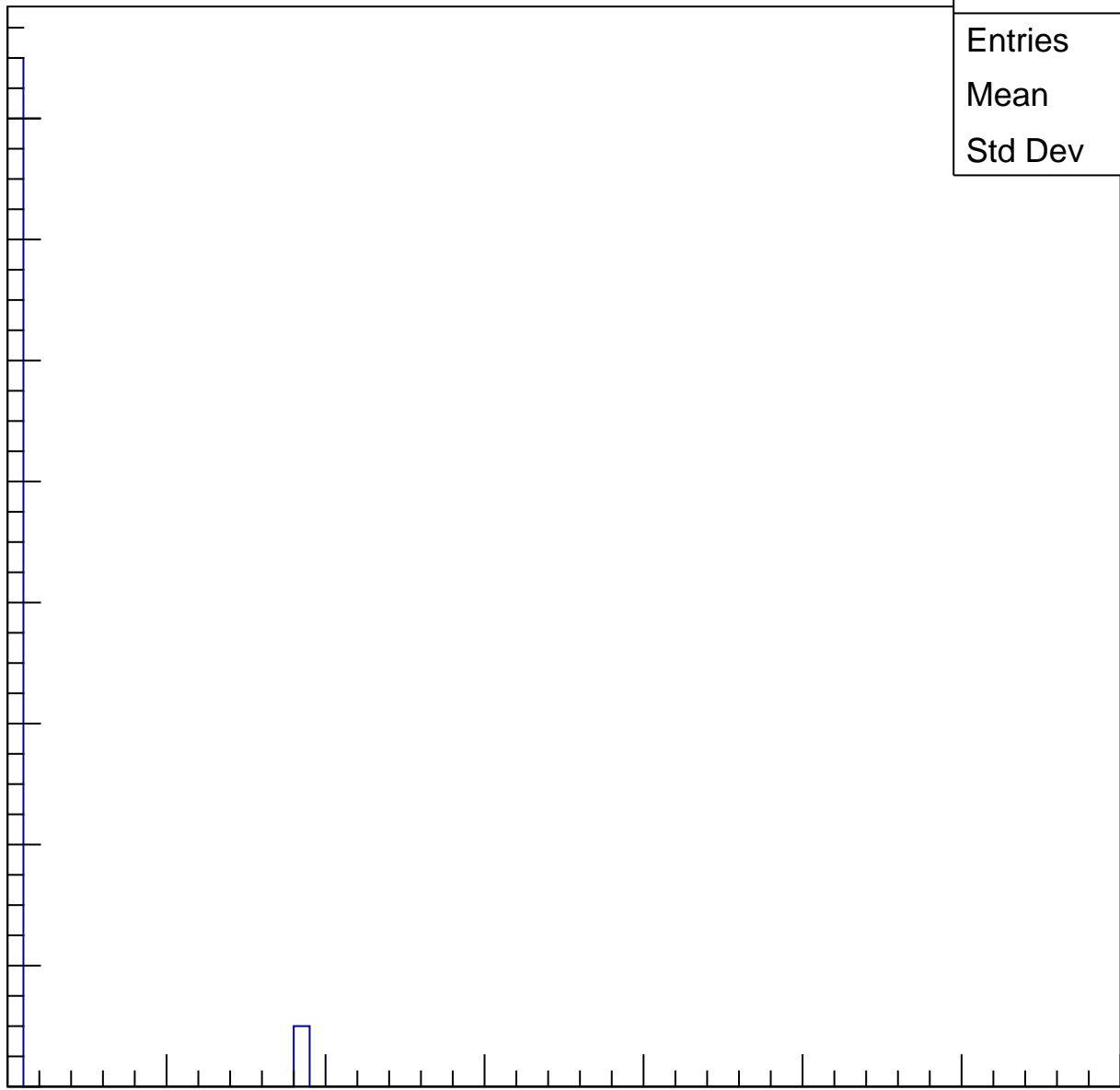
Entries	18
Mean	1
Std Dev	4.123

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch126, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	21.03
Std Dev	10.24

Gaus mean : 26.4273

Gaus Width: 4.4328

Entry

12

10

8

6

4

2

0

0

10

20

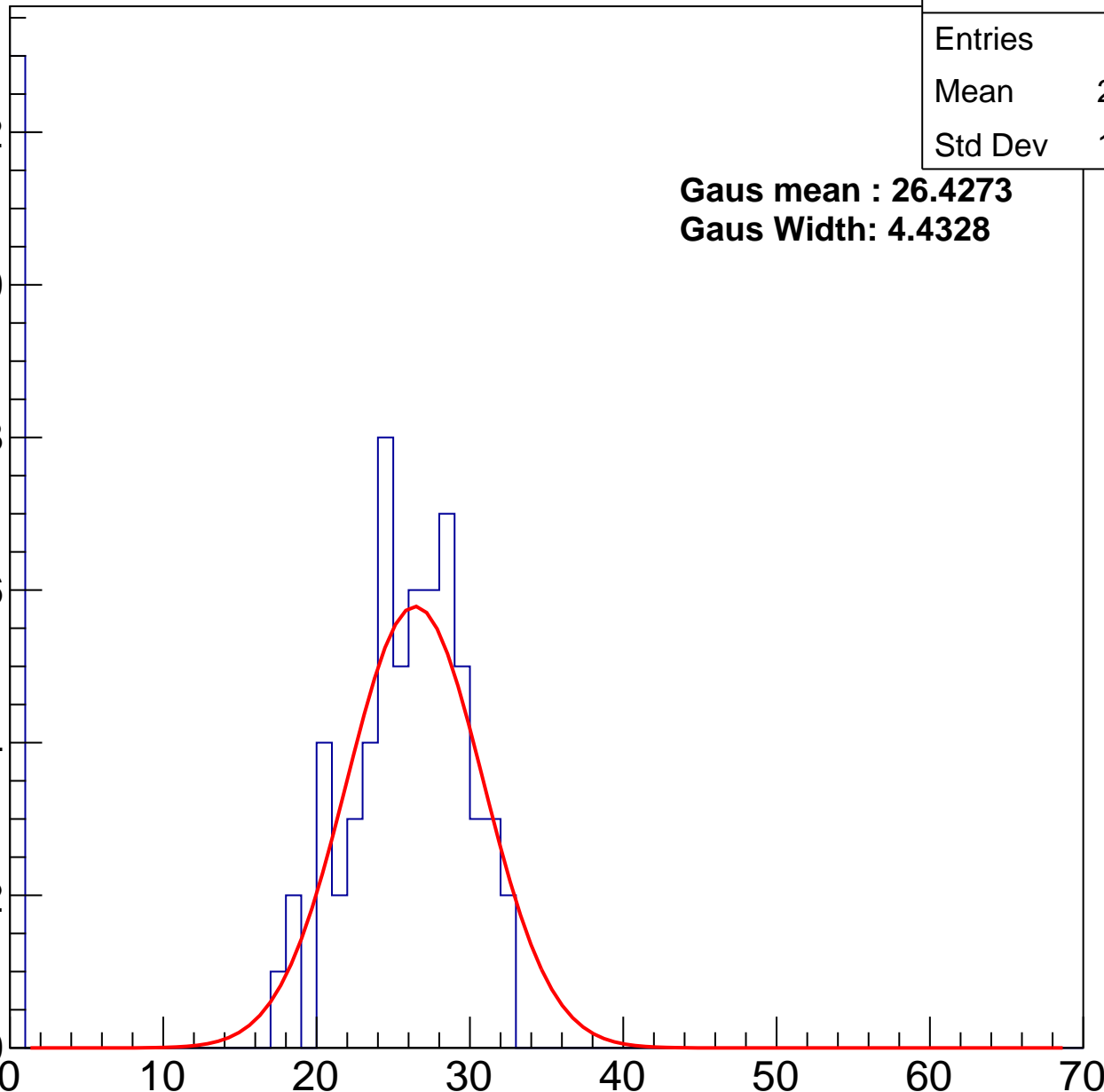
30

40

50

60

ampl



B1L103S, U13-ch126, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	27.96
Std Dev	12.14

Gaus mean : 32.9605

Gaus Width: 3.6469

Entry

12

10

8

6

4

2

0

0

10

20

30

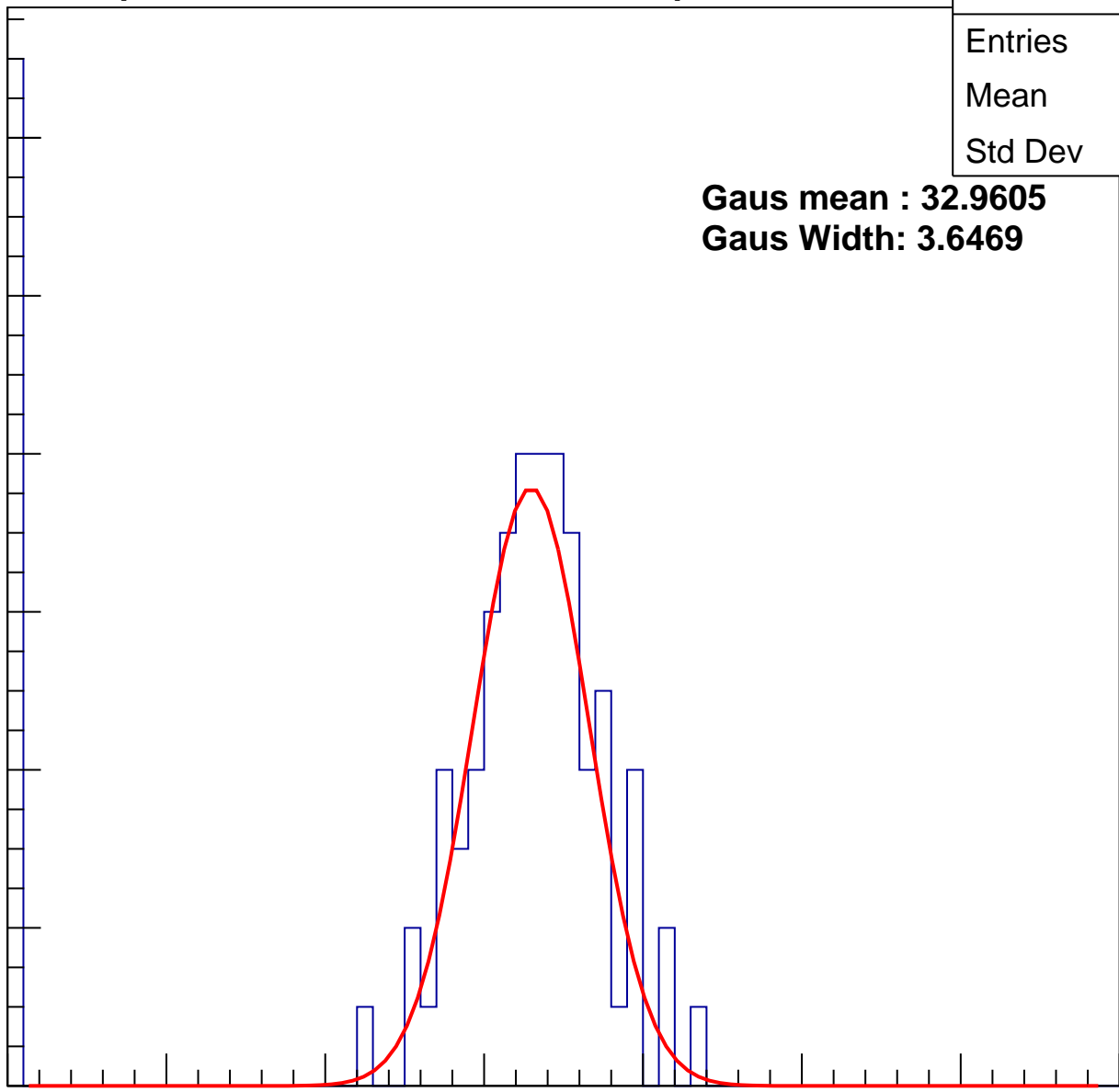
40

50

60

70

ampl



B1L103S, U13-ch126, adc2

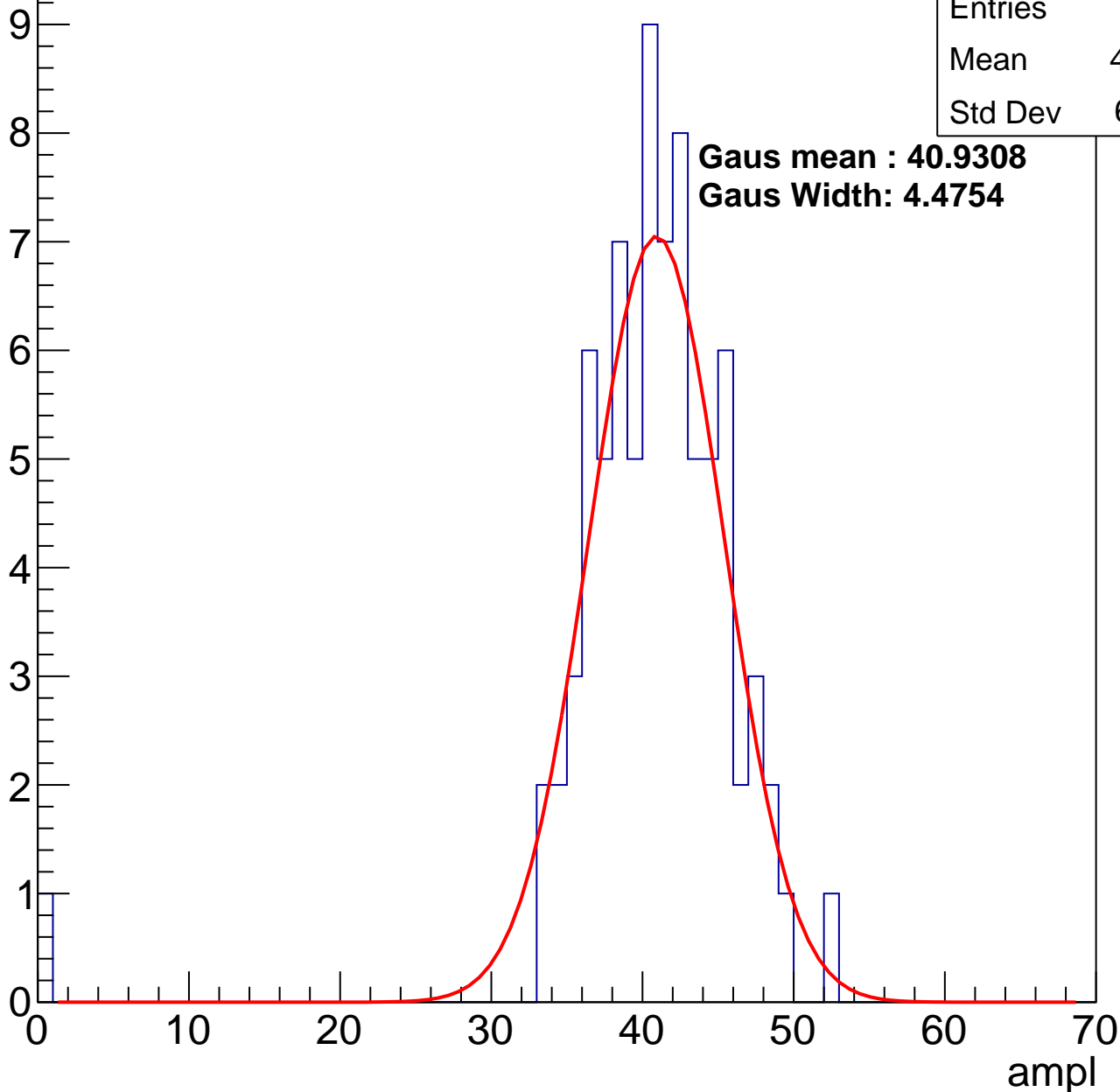
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	40.24
Std Dev	6.021

Gaus mean : 40.9308

Gaus Width: 4.4754

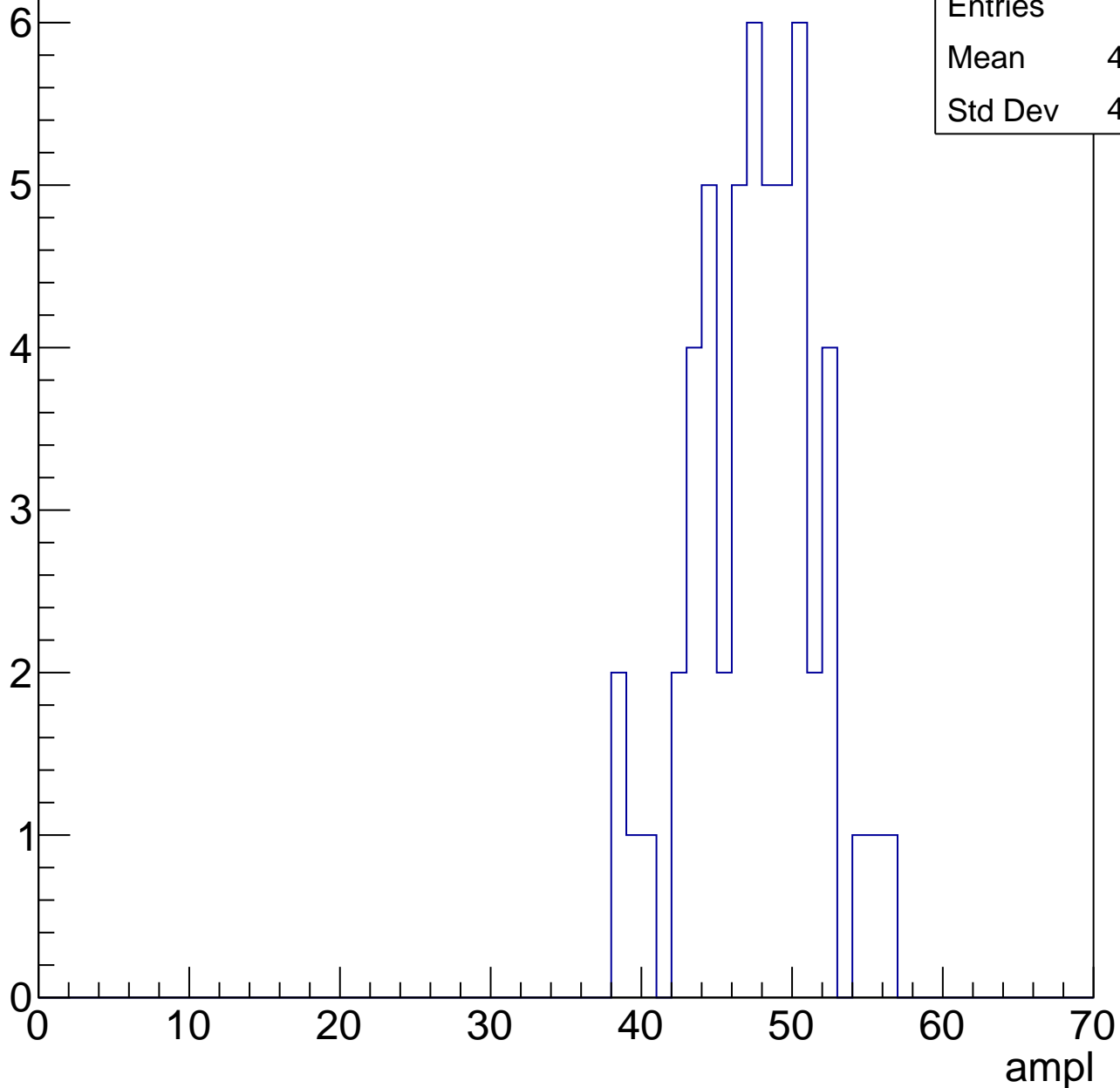


B1L103S, U13-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	47.04
Std Dev	4.009

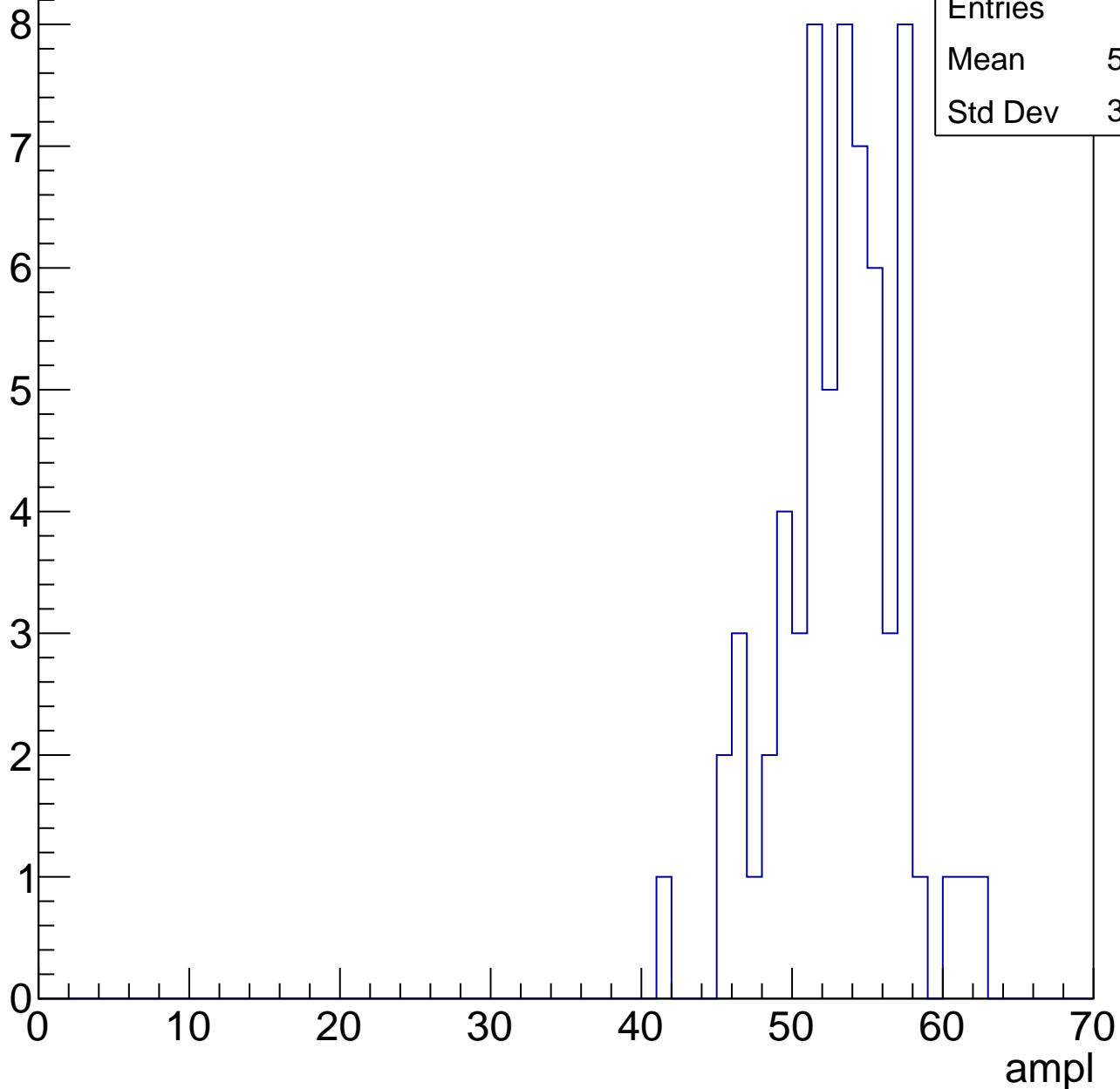


B1L103S, U13-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	52.66
Std Dev	3.982

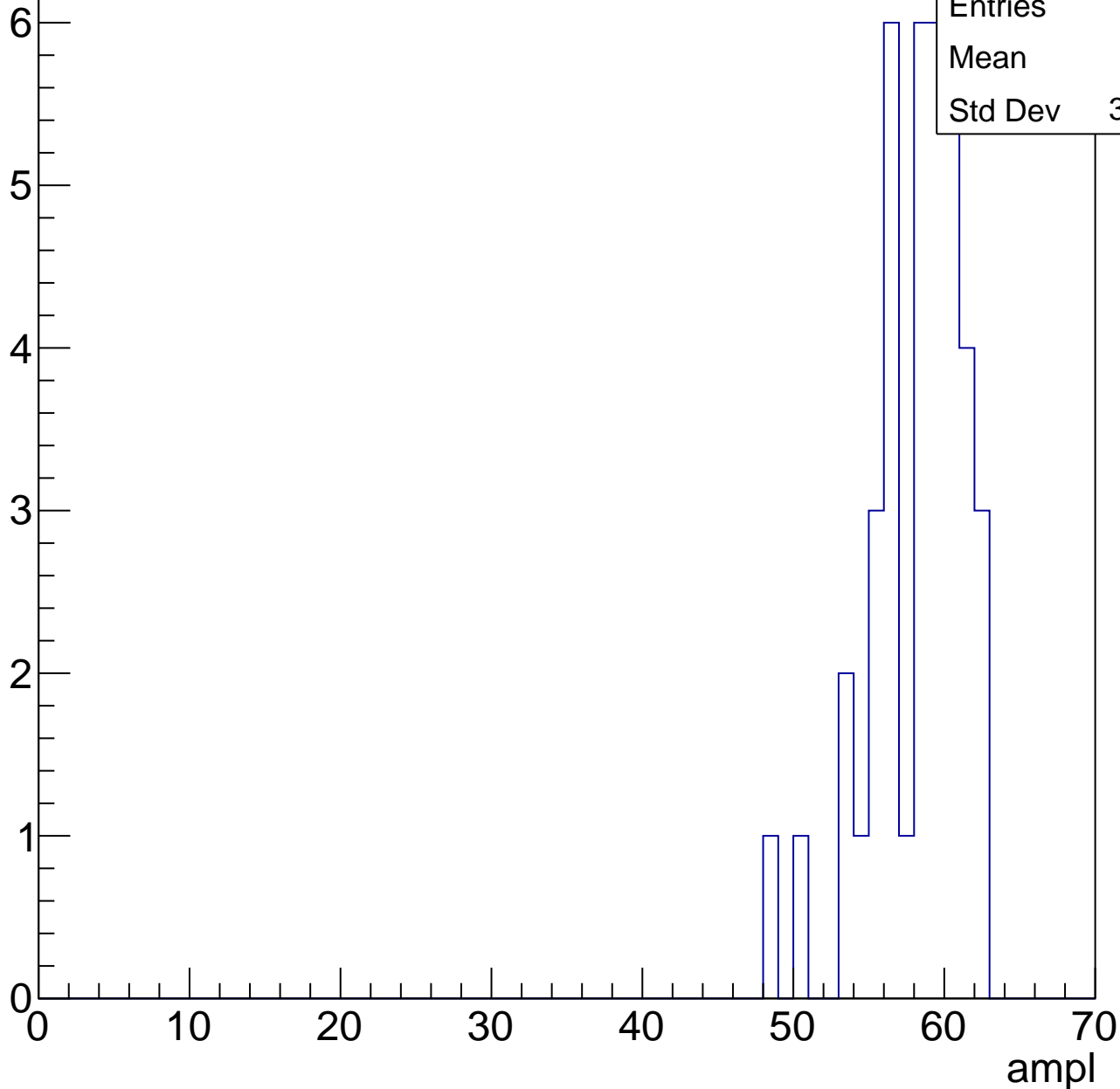


B1L103S, U13-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	57.7
Std Dev	3.132

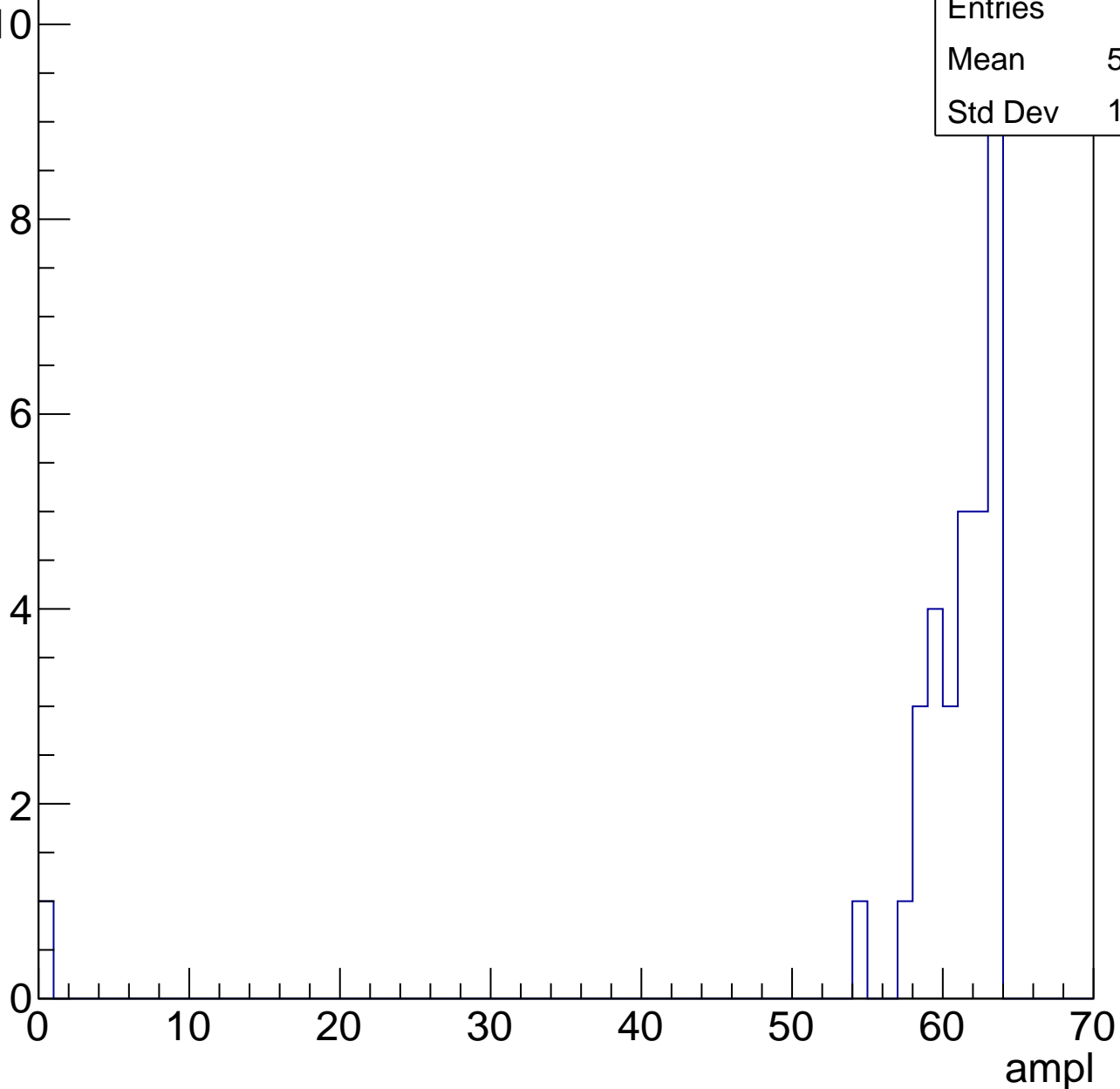


B1L103S, U13-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.97
Std Dev	10.65

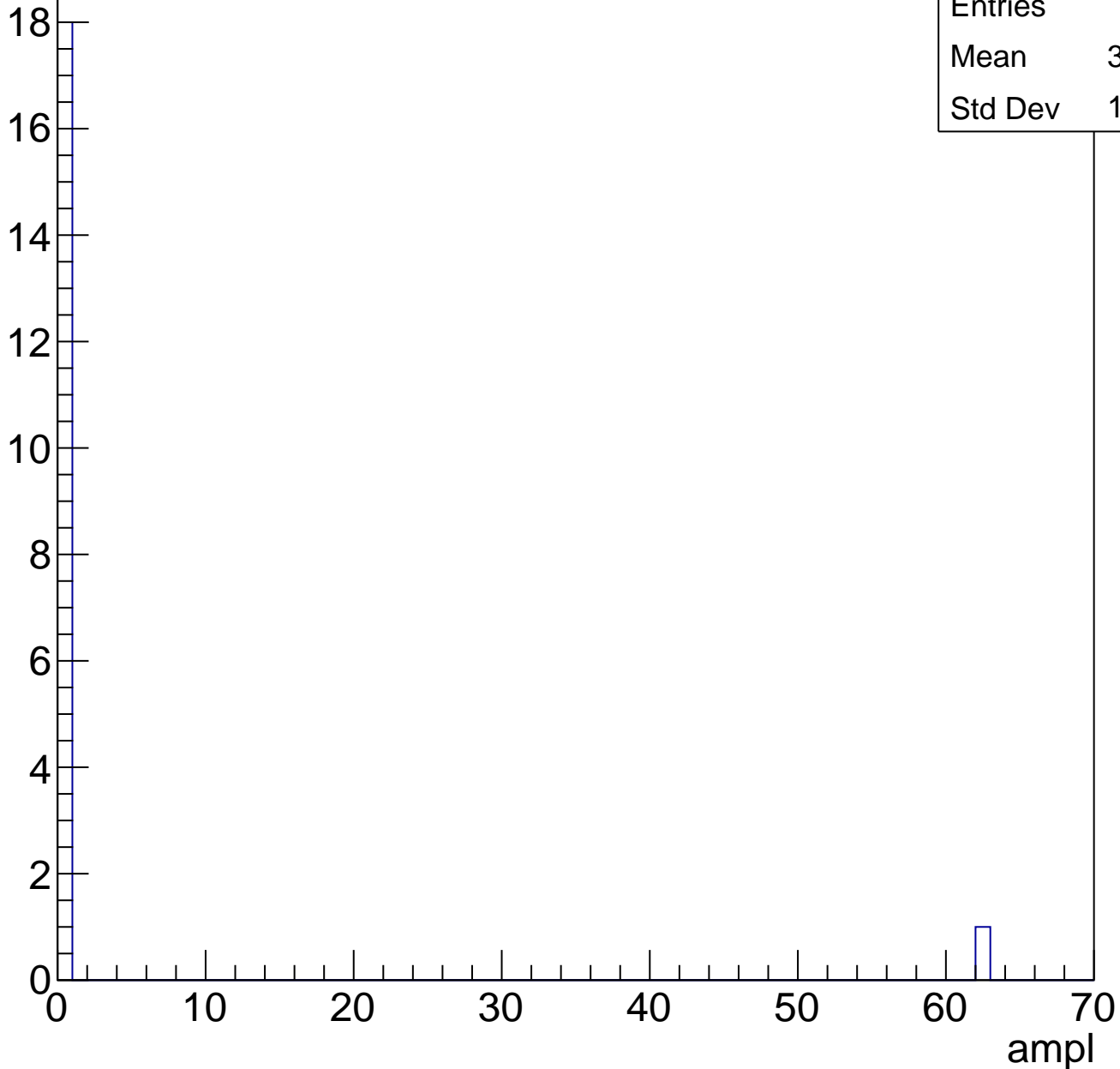


B1L103S, U13-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U13-ch127, adc0

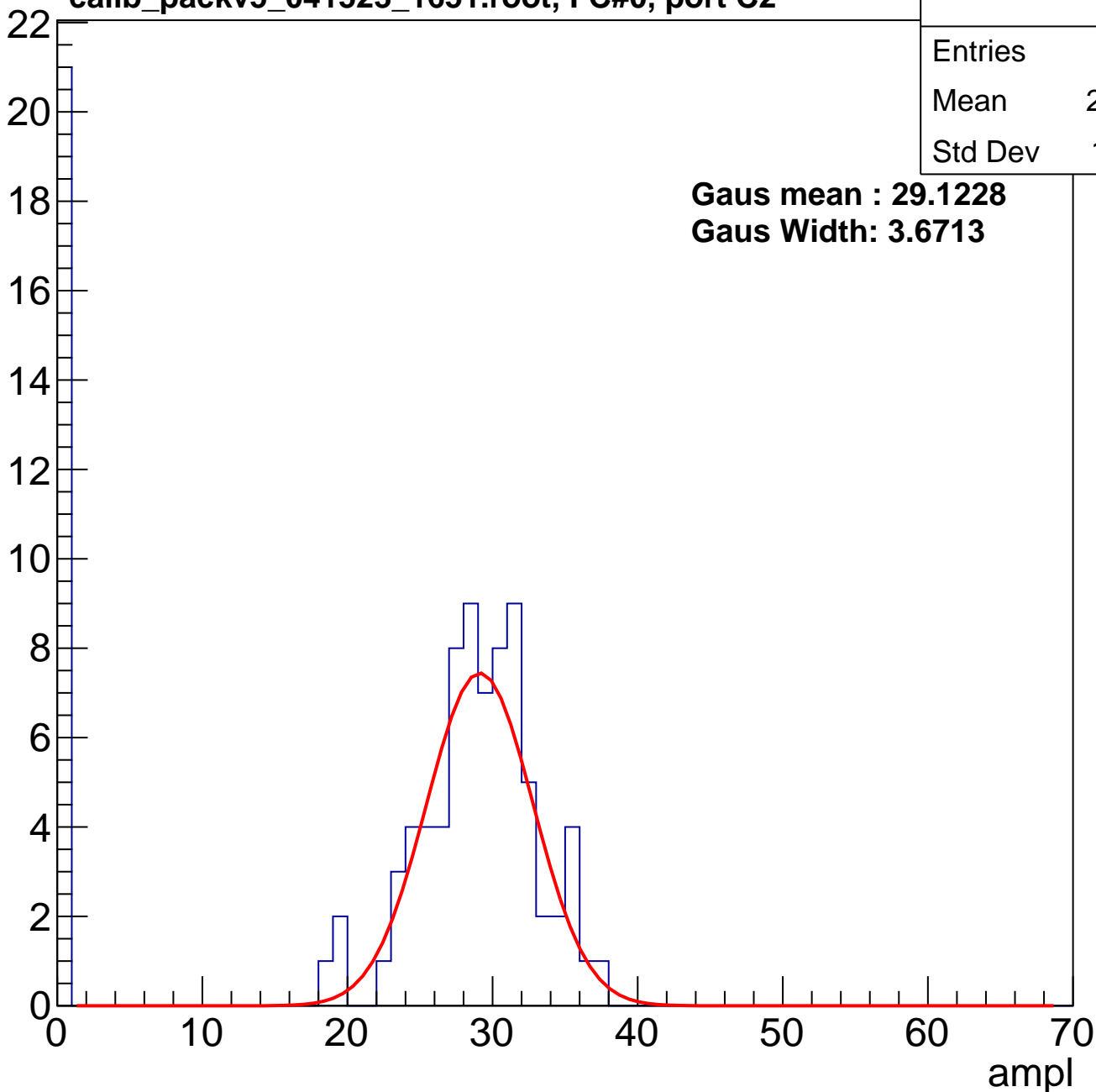
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	22.33
Std Dev	12.31

Gaus mean : 29.1228

Gaus Width: 3.6713

Entry



B1L103S, U13-ch127, adc1

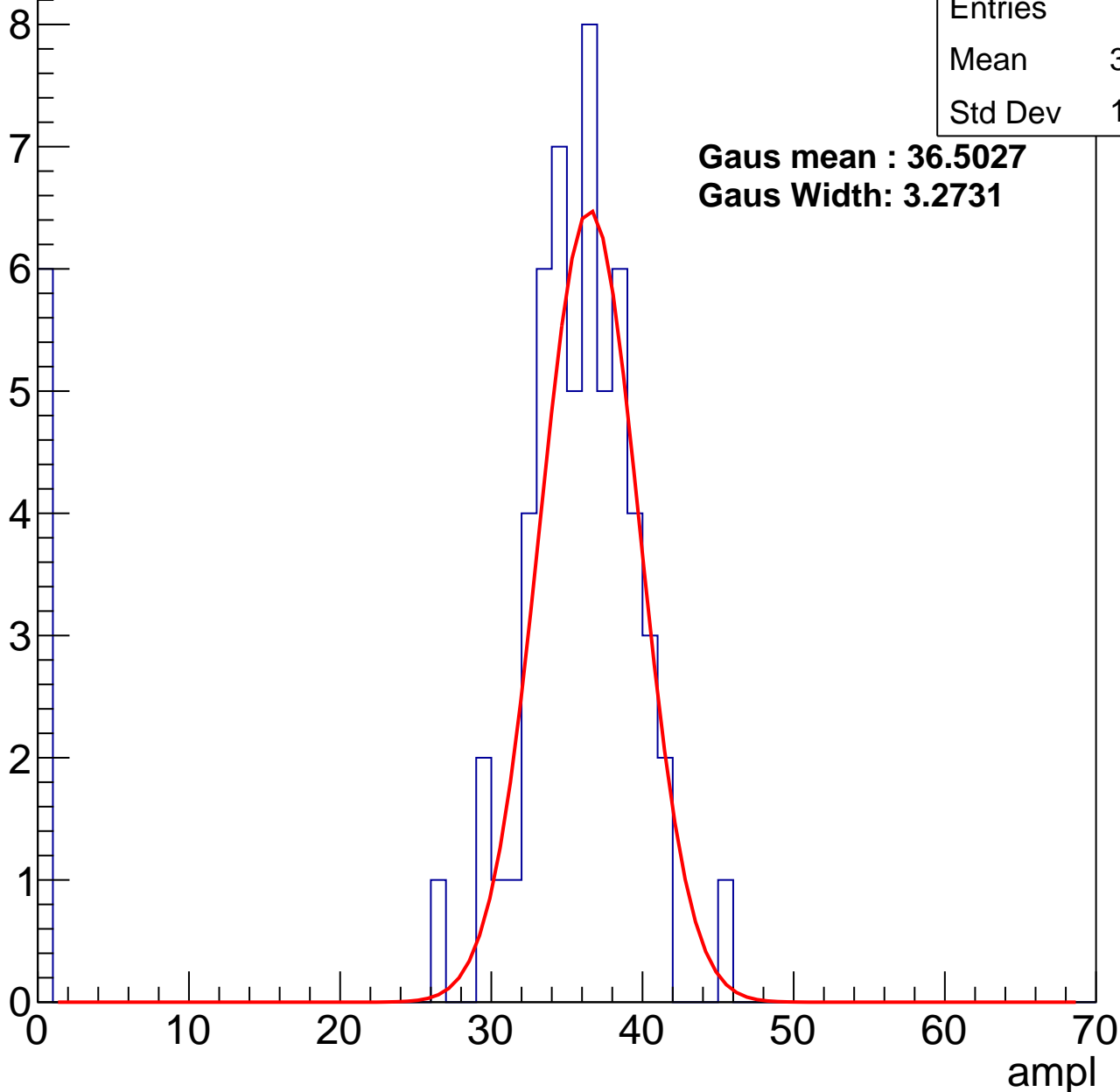
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.06
Std Dev	10.98

Gaus mean : 36.5027

Gaus Width: 3.2731



B1L103S, U13-ch127, adc2

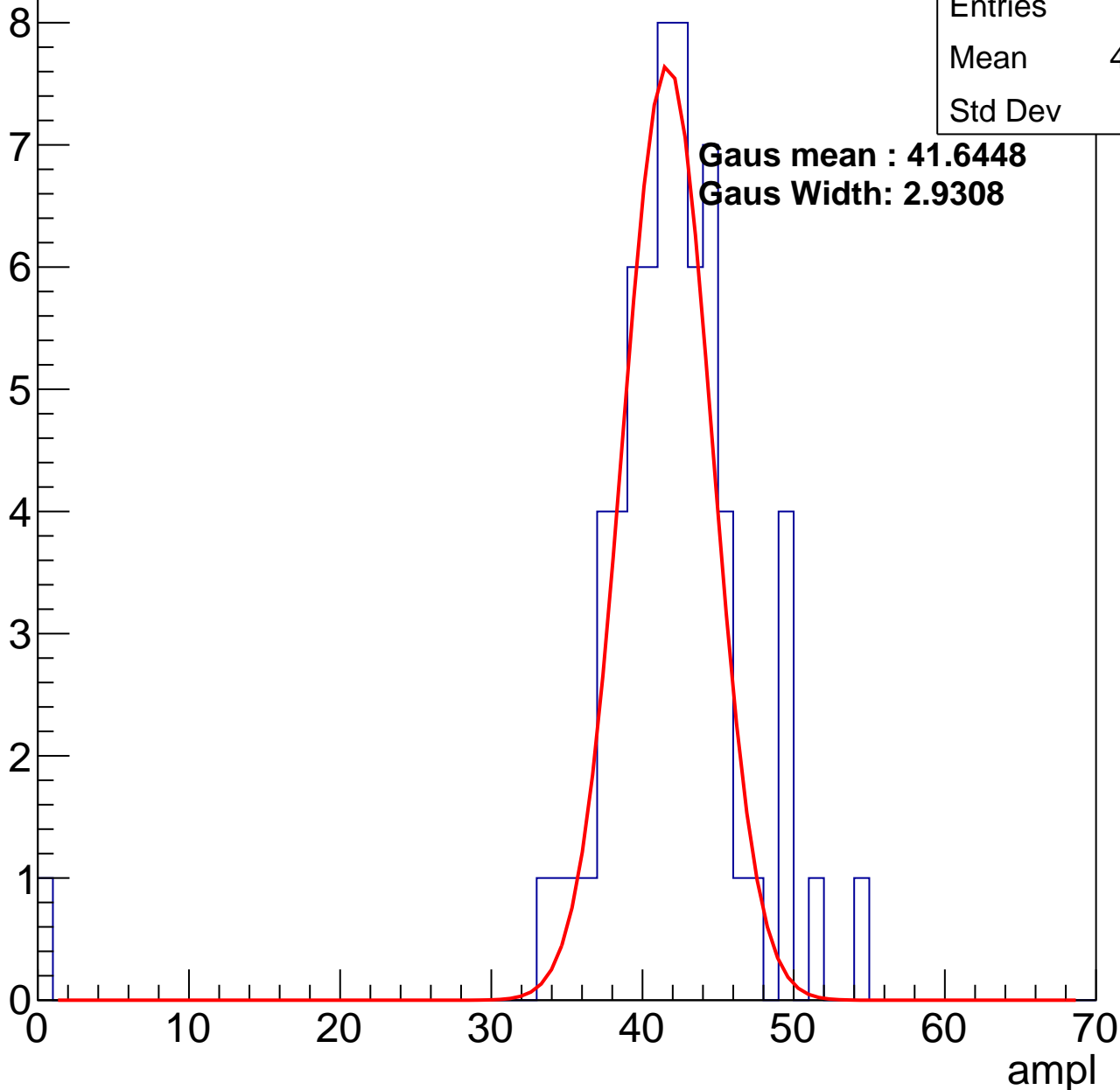
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	41.15
Std Dev	6.43

Gaus mean : 41.6448

Gaus Width: 2.9308

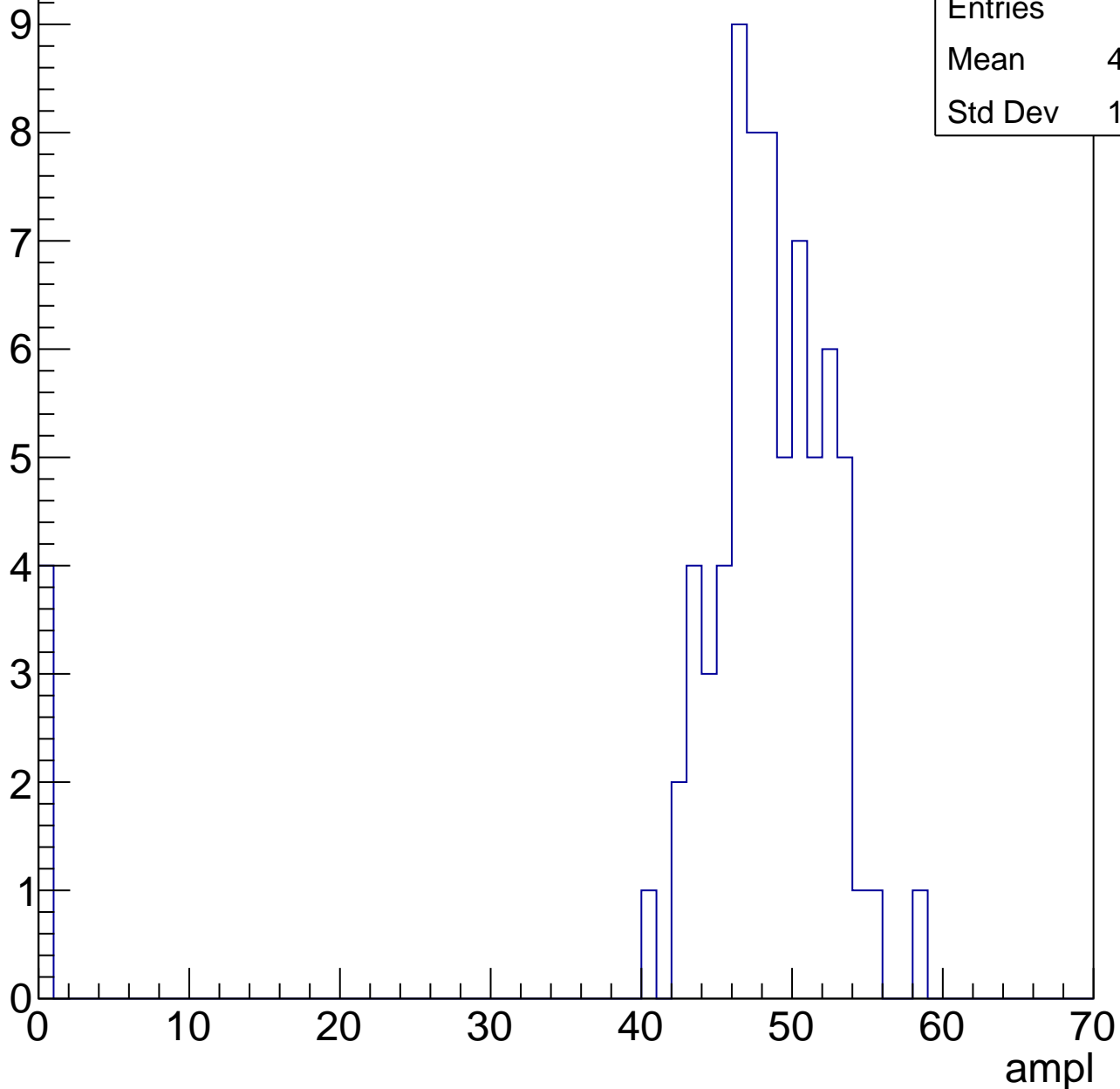


B1L103S, U13-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	45.62
Std Dev	11.42

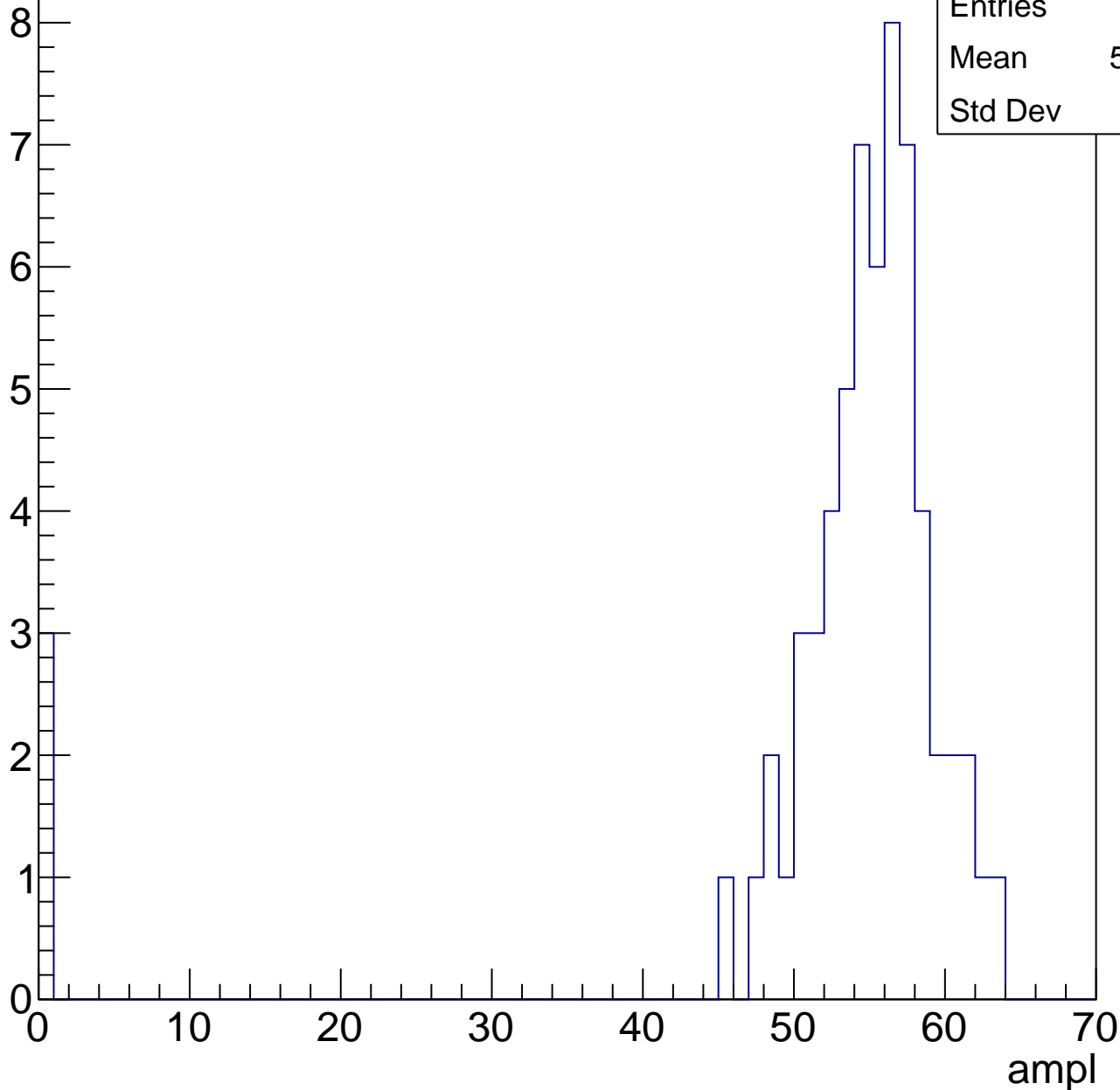


B1L103S, U13-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.14
Std Dev	12.2

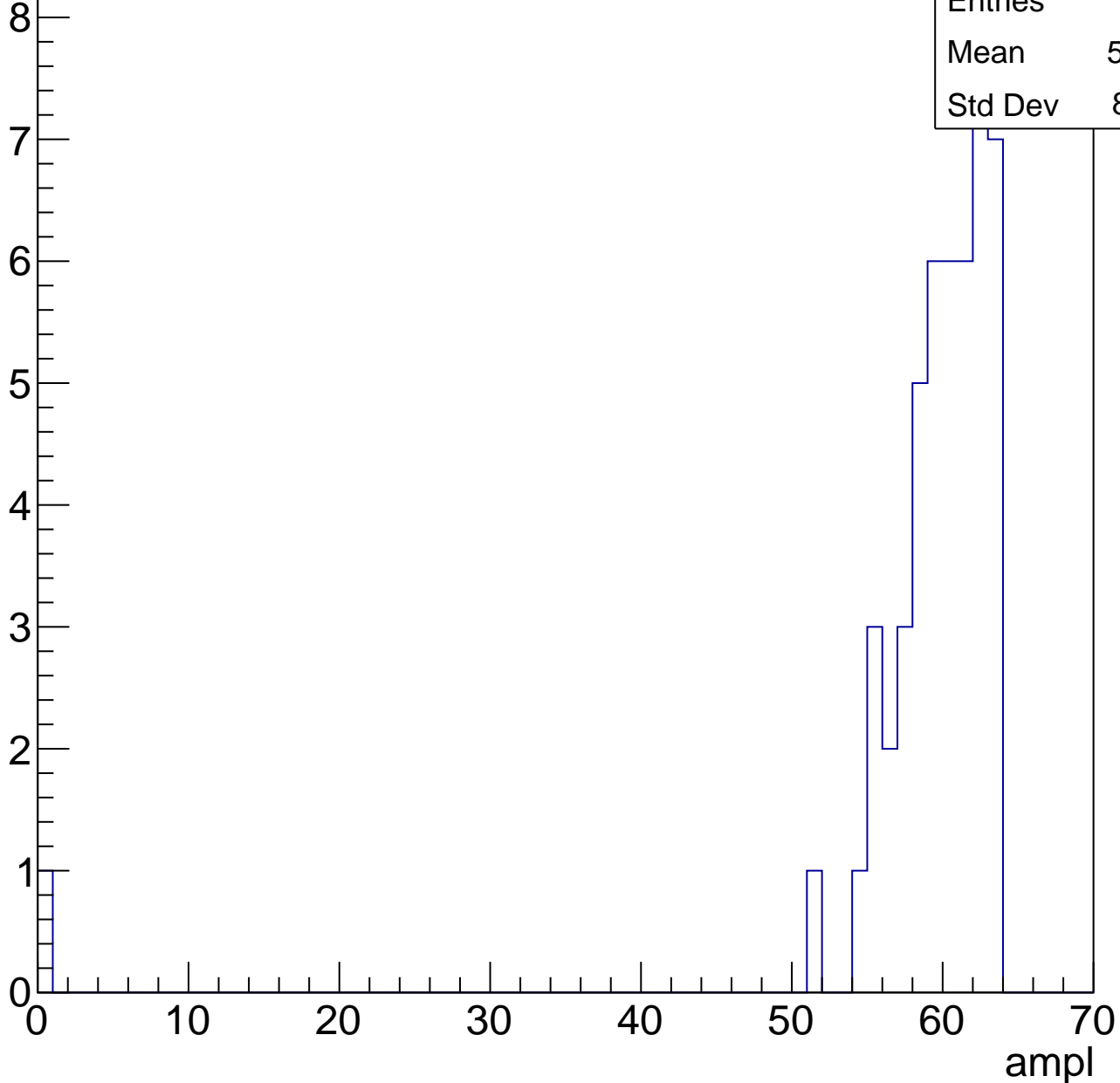


B1L103S, U13-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.37
Std Dev	8.861



B1L103S, U13-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

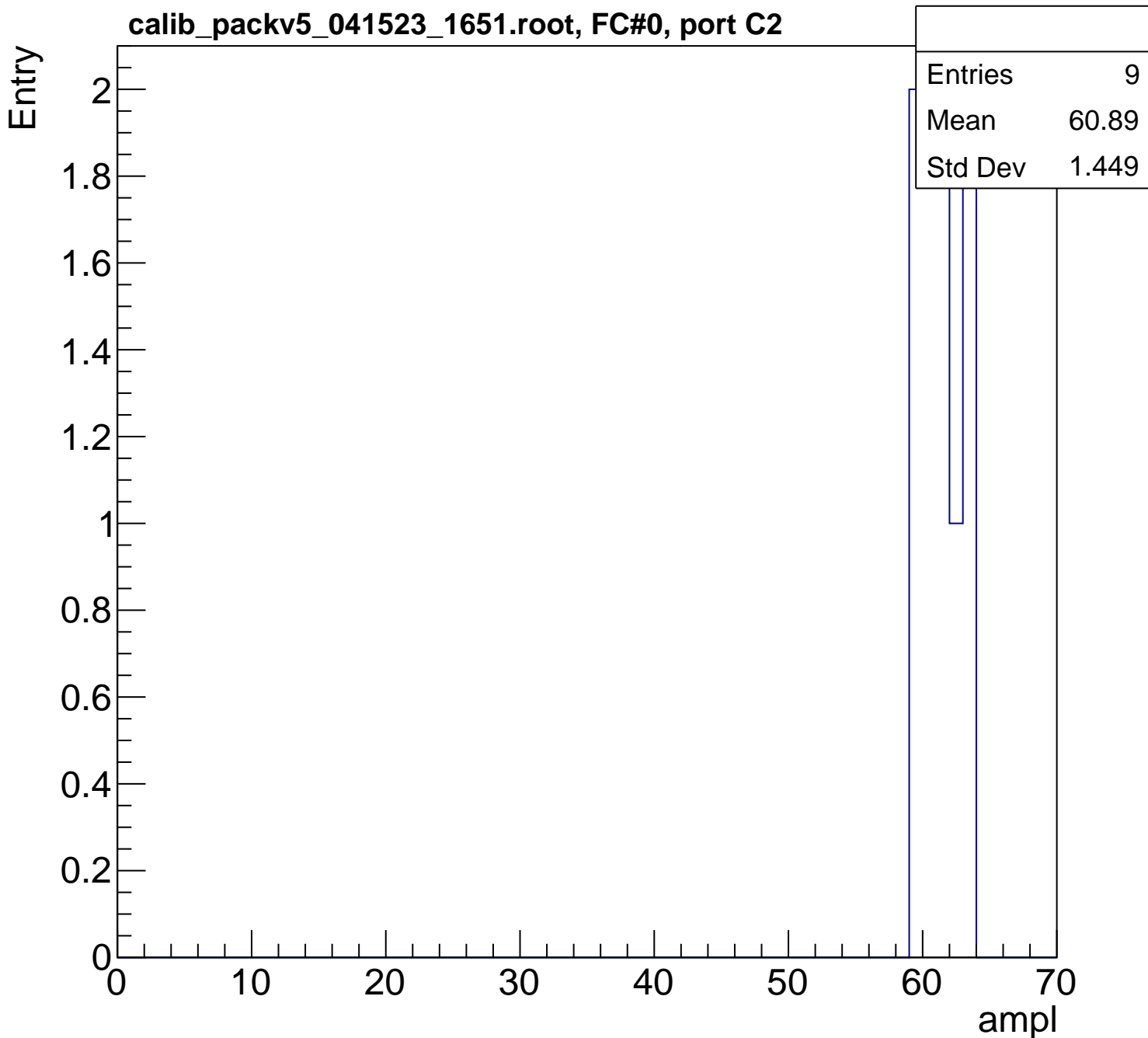
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	60.89
Std Dev	1.449

0 10 20 30 40 50 60 70

ampl



B1L103S, U13-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U13-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl