

B1L002S, U26-ch0

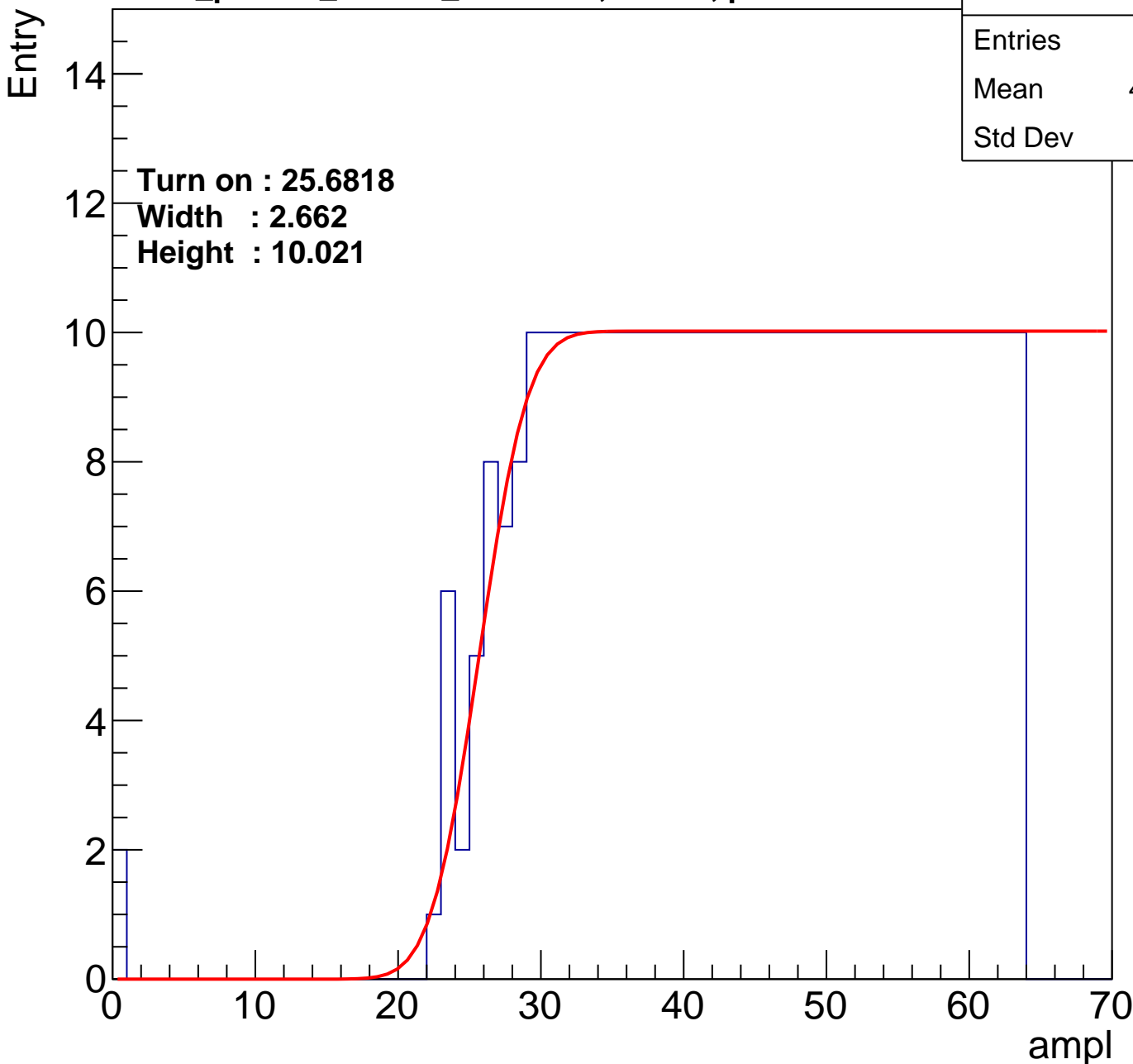
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.6818

Width : 2.662

Height : 10.021

Entries	389
Mean	43.84
Std Dev	11.71



B1L002S, U26-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.46
Std Dev	12.45

Turn on : 25.8196

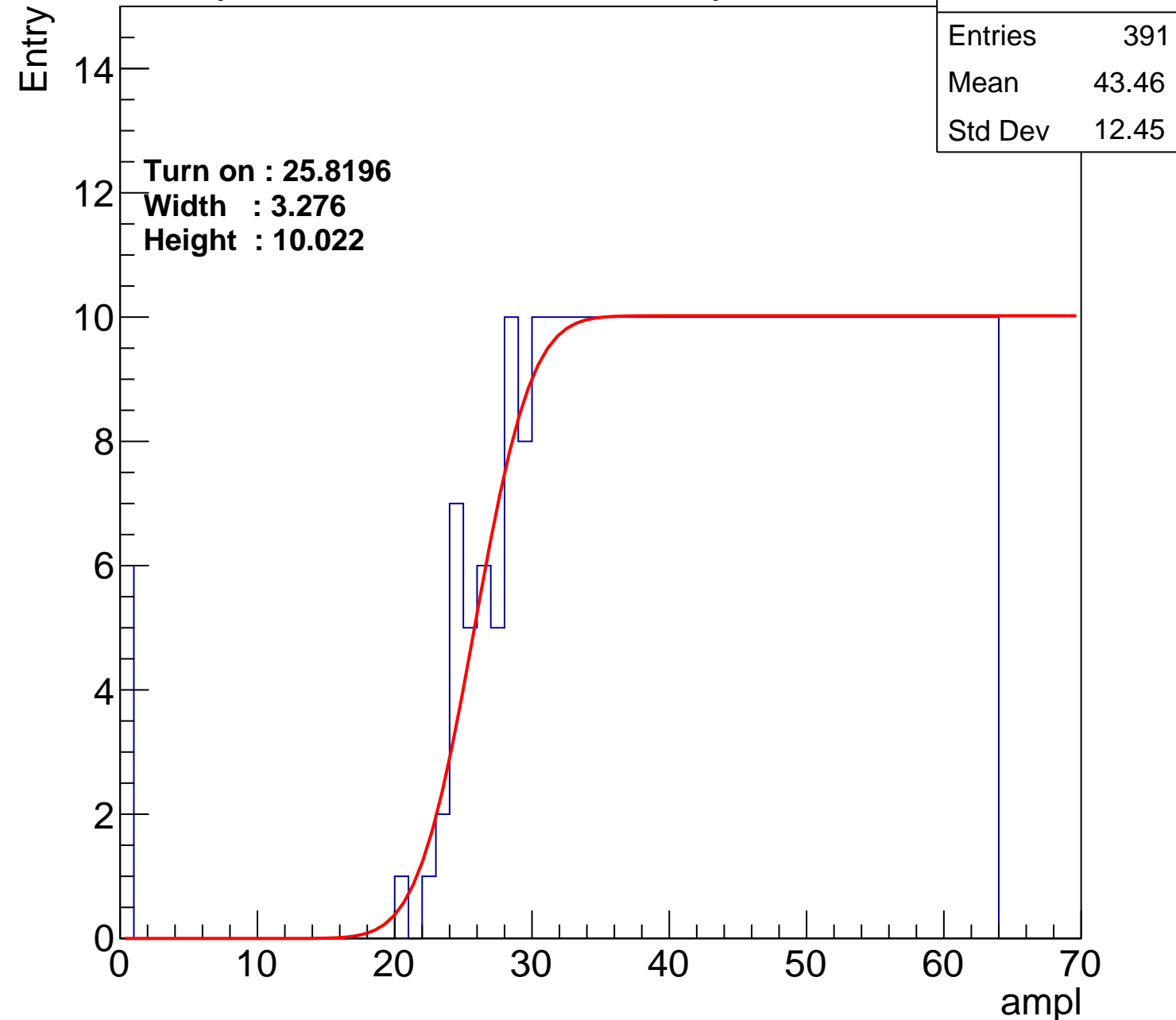
Width : 3.276

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.21
Std Dev	12.09

Turn on : 23.7694

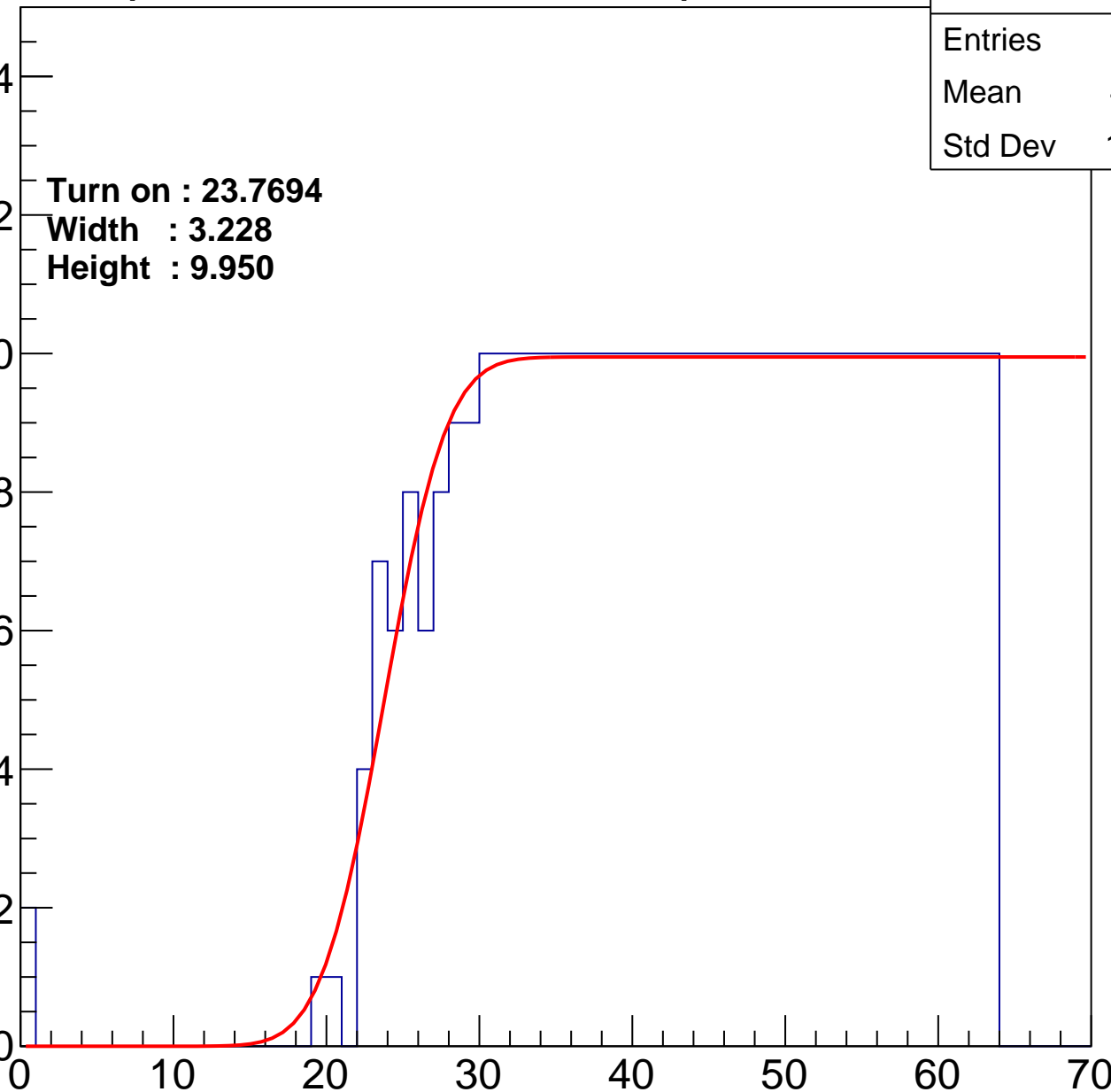
Width : 3.228

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.96
Std Dev	11.6

Turn on : 25.3069

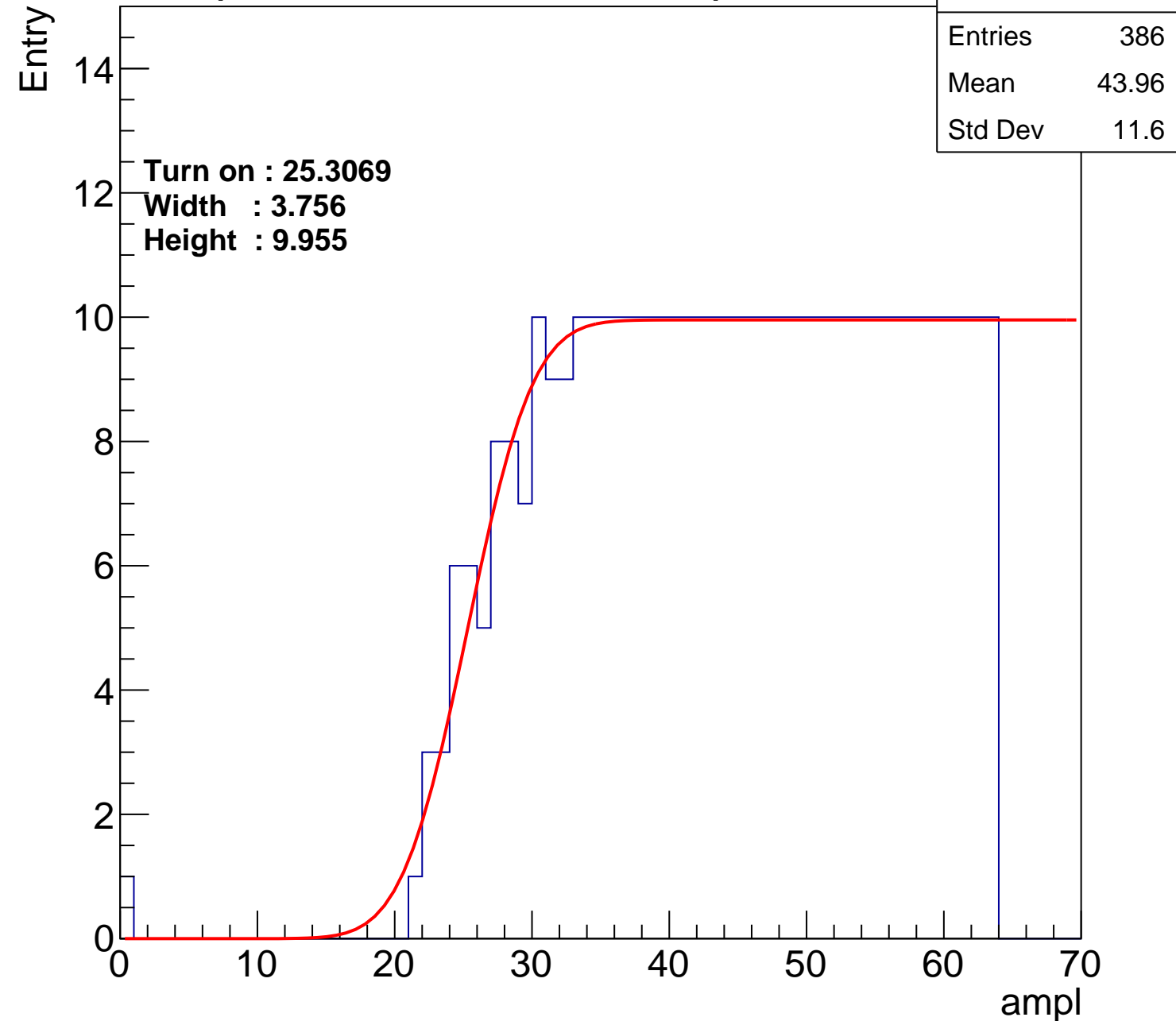
Width : 3.756

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 27.0571

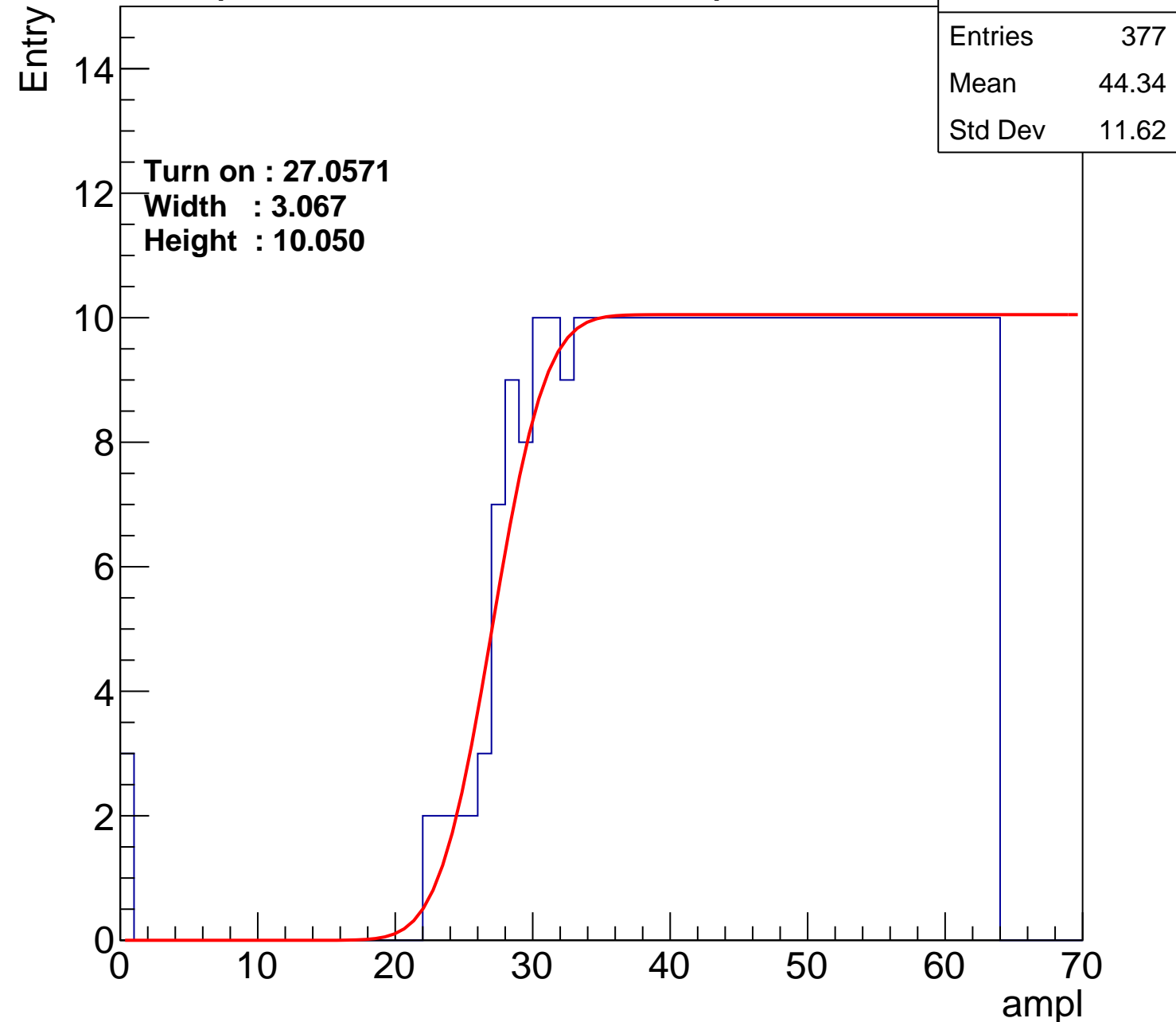
Width : 3.067

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.69
Std Dev	11.86

Turn on : 25.7038

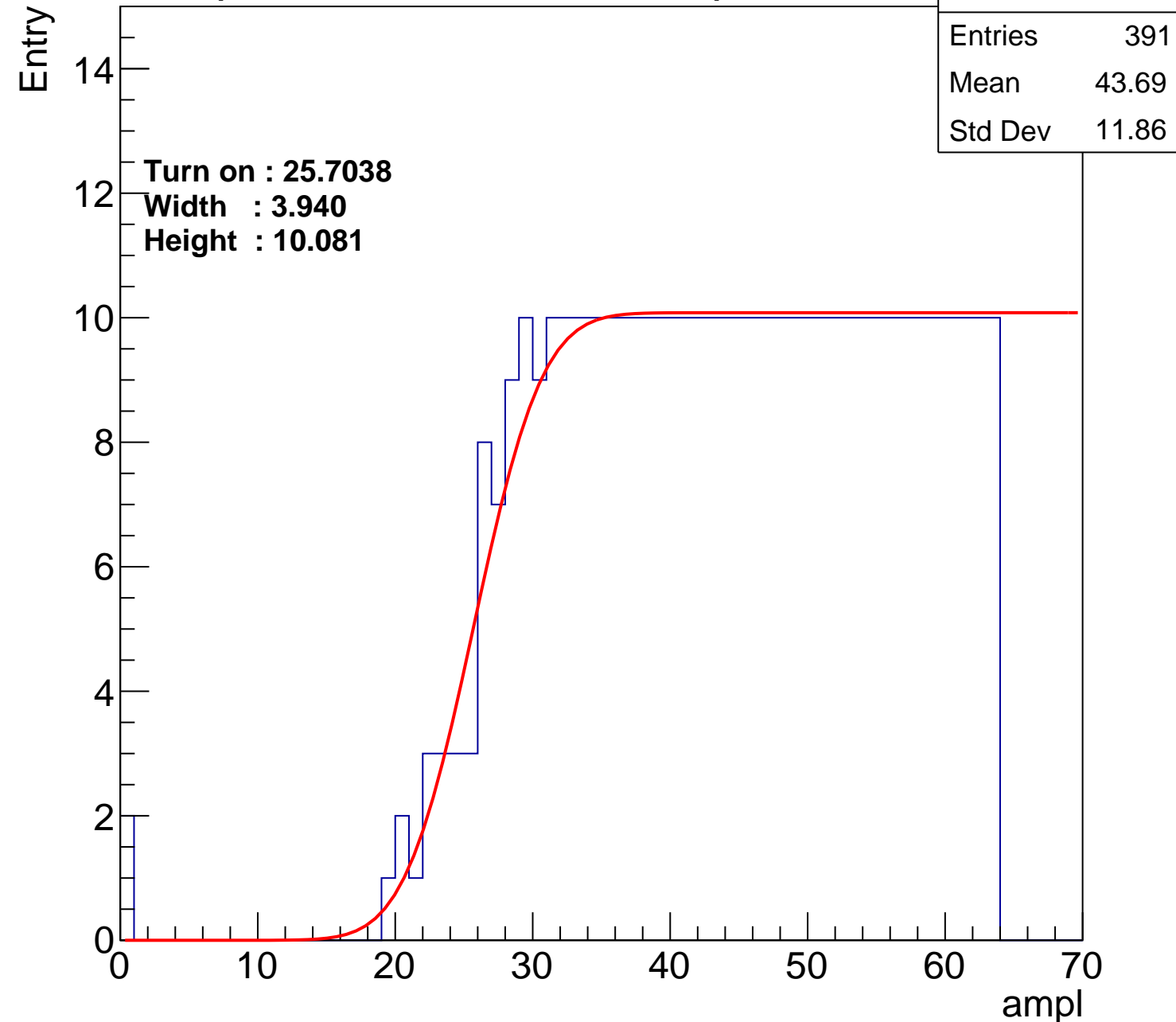
Width : 3.940

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	411
Mean	42.75
Std Dev	12.3

Turn on : 23.6090

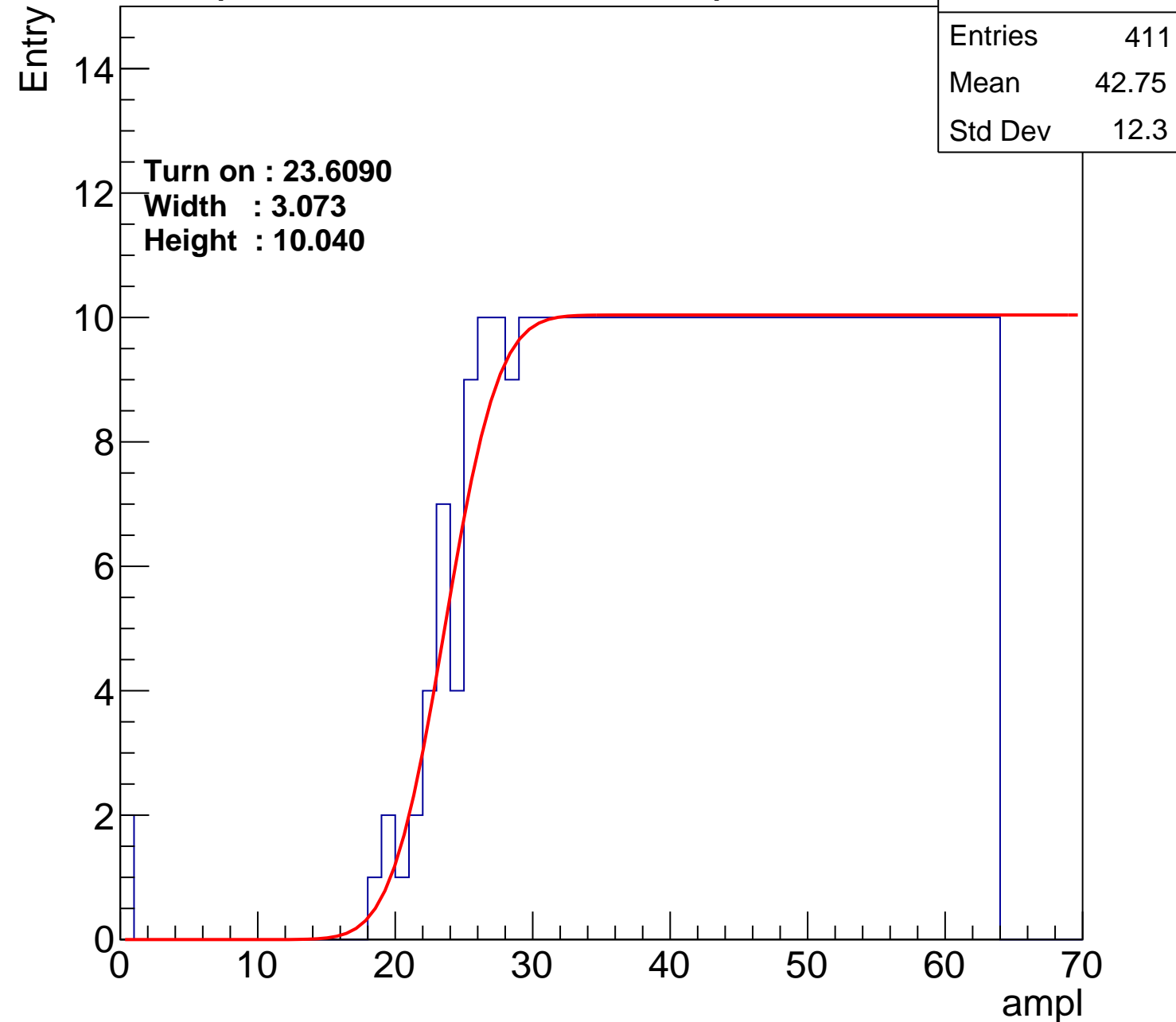
Width : 3.073

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
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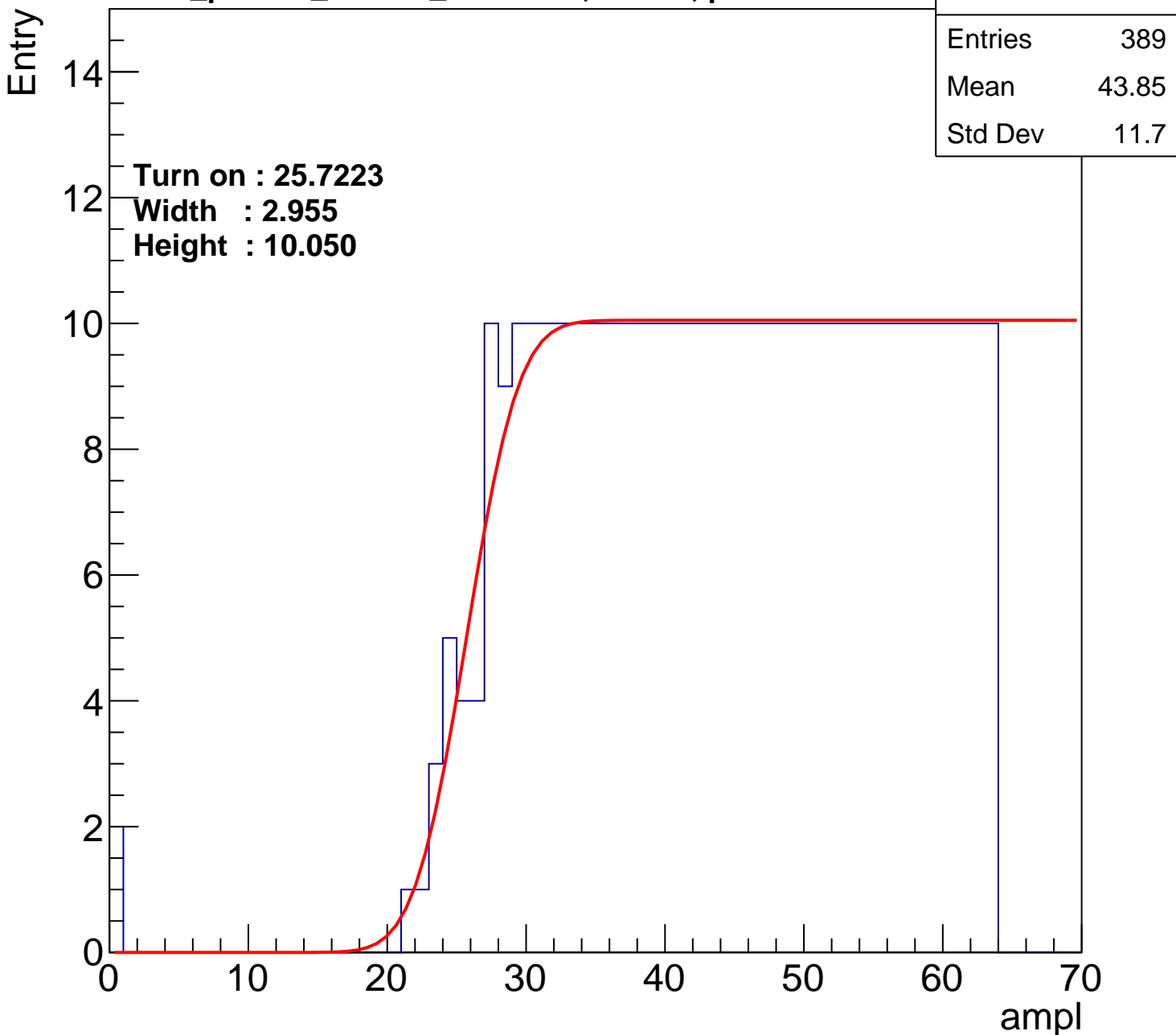
Mean	43.85
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Std Dev	11.7
---------	------

Turn on : 25.7223

Width : 2.955

Height : 10.050



B1L002S, U26-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.54
Std Dev	11.14

Turn on : 26.4398

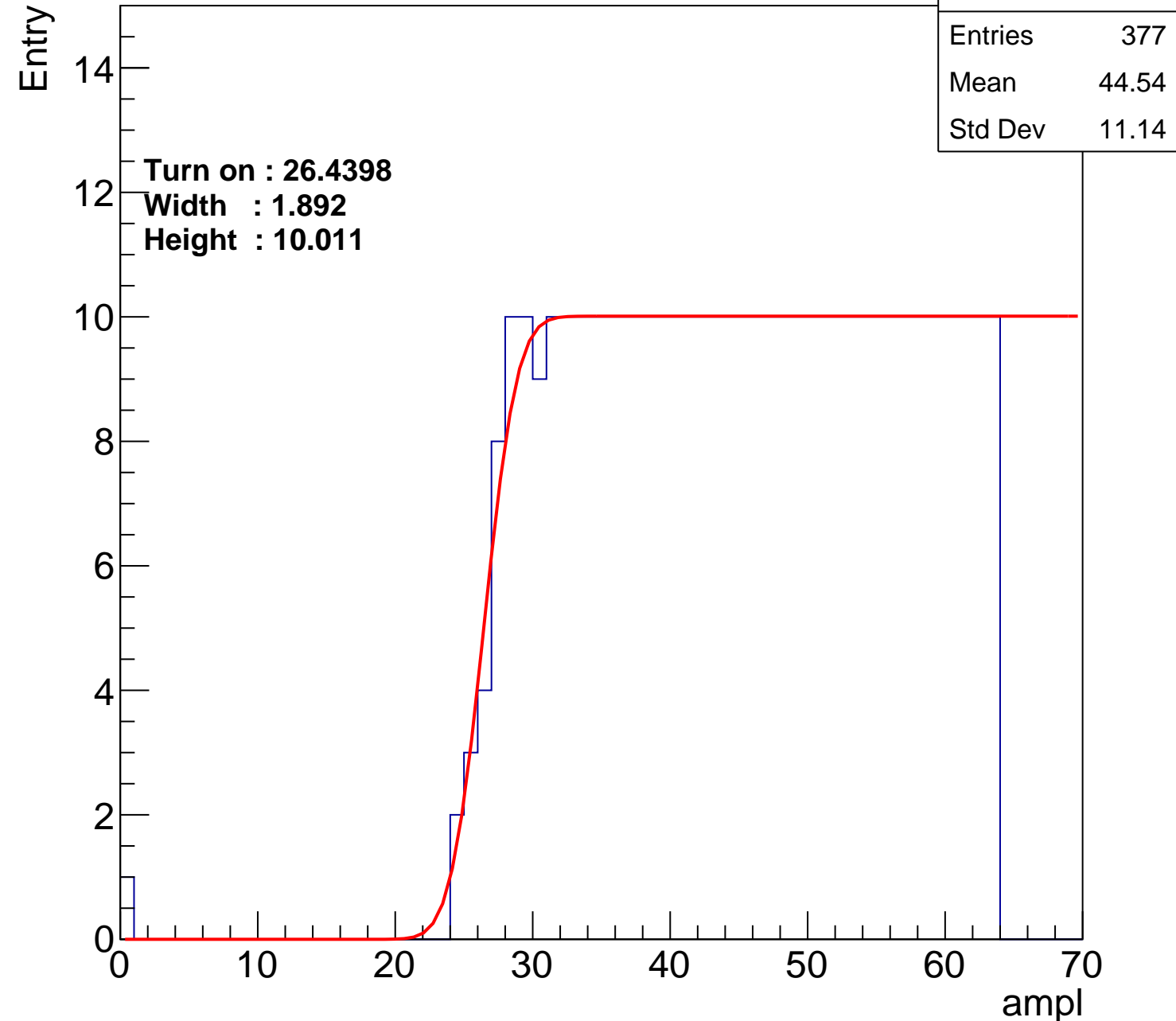
Width : 1.892

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.65
Std Dev	11.68

Turn on : 24.6811

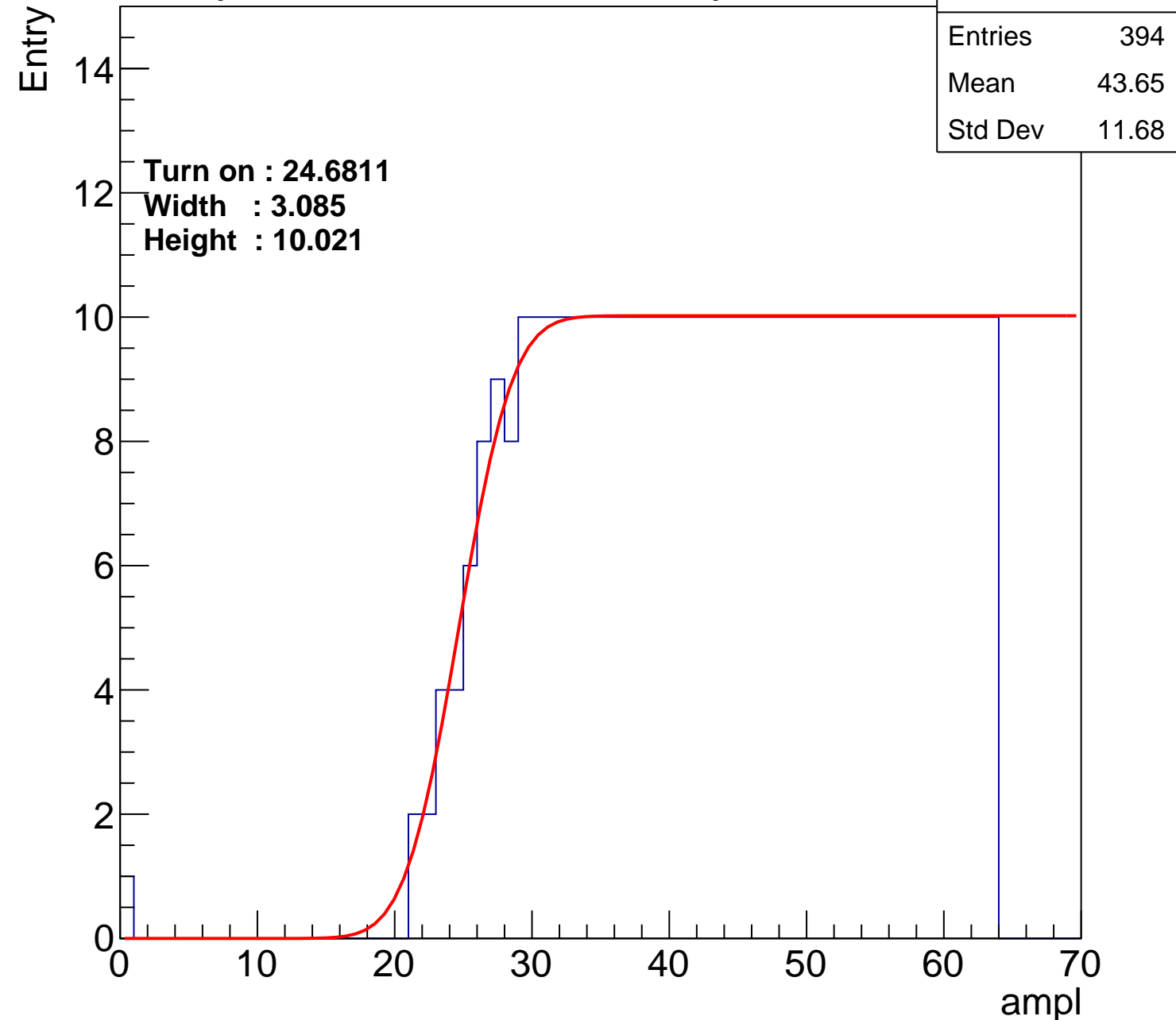
Width : 3.085

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch10

calib_packv5_042523_0143.root, FC#10, port B3

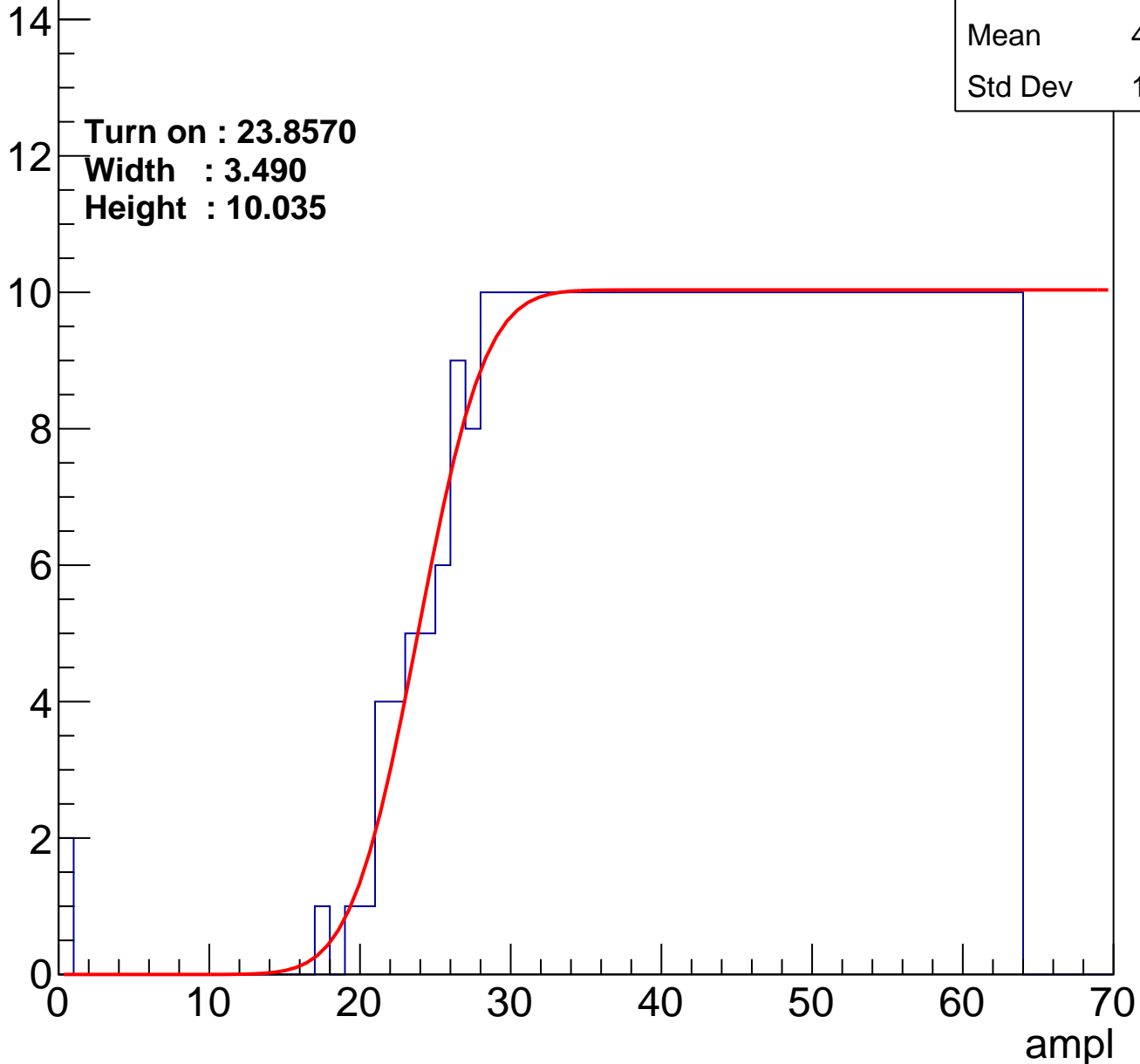
Entries	406
Mean	42.96
Std Dev	12.22

Turn on : 23.8570

Width : 3.490

Height : 10.035

Entry



B1L002S, U26-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.34
Std Dev	11.99

Turn on : 24.3405

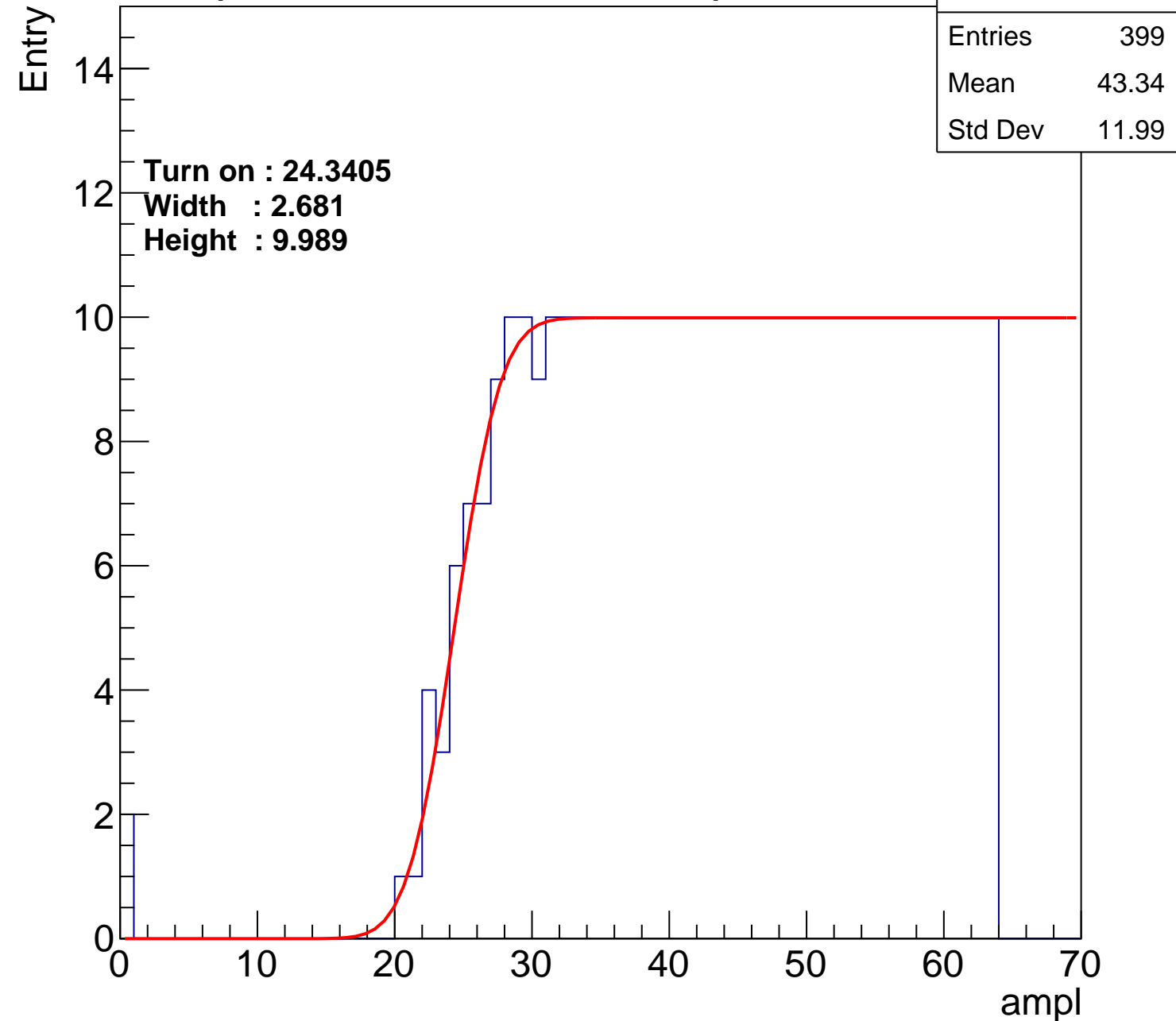
Width : 2.681

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch12

calib_packv5_042523_0143.root, FC#10, port B3

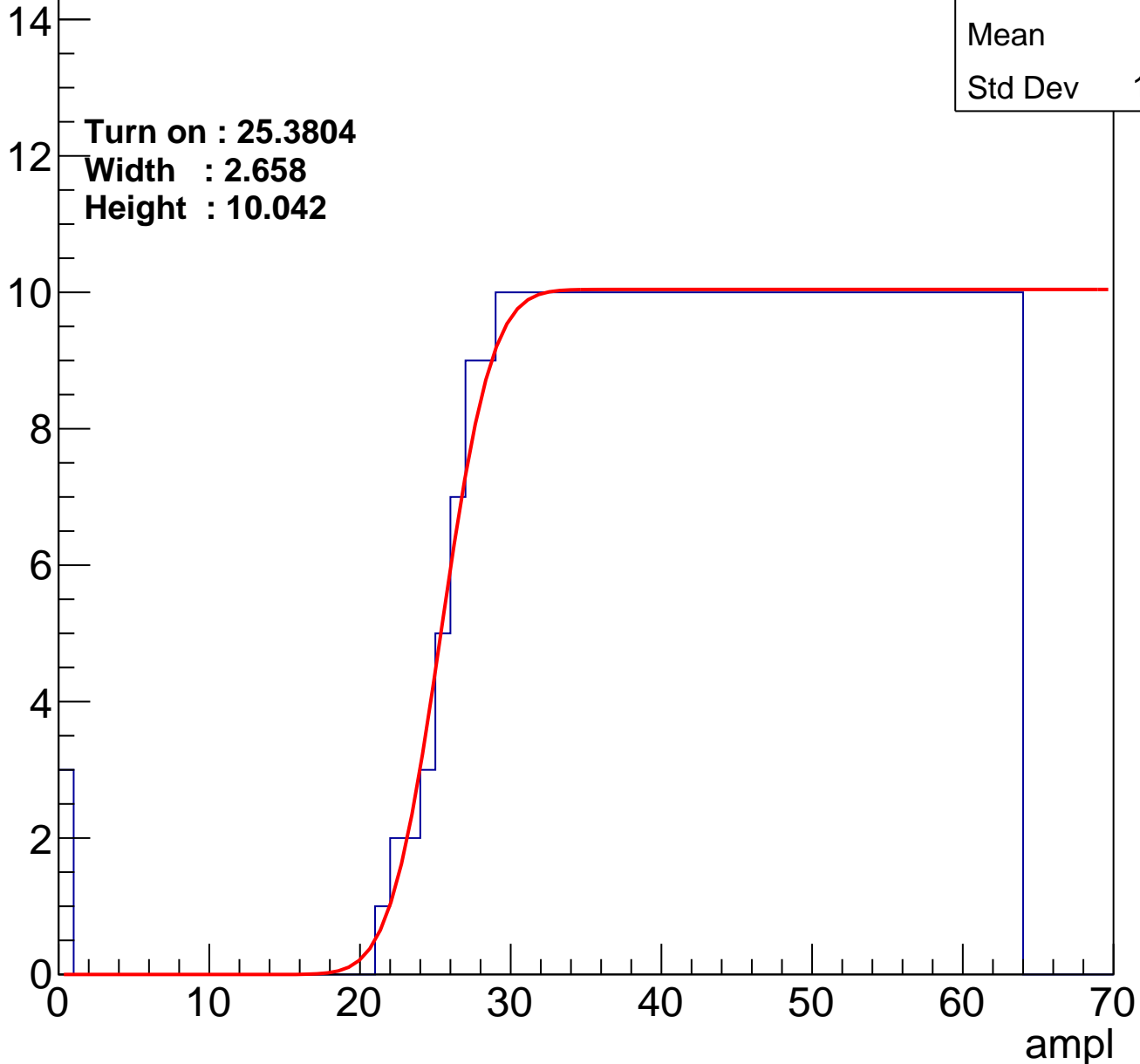
Entries	391
Mean	43.7
Std Dev	11.91

Turn on : 25.3804

Width : 2.658

Height : 10.042

Entry



B1L002S, U26-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.4
Std Dev	11.58

Turn on : 26.7168

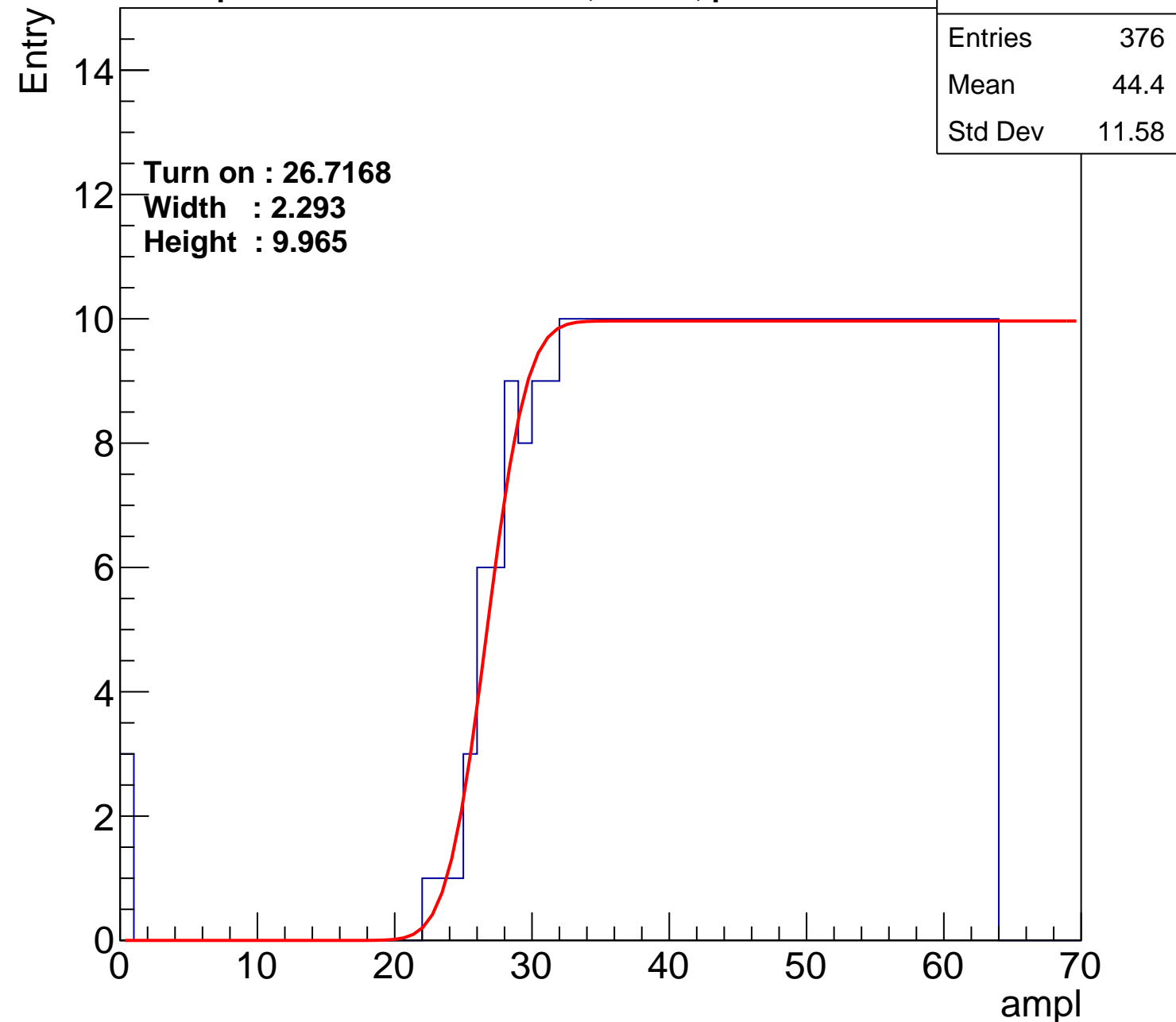
Width : 2.293

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.46
Std Dev	11.9

Turn on : 25.3866

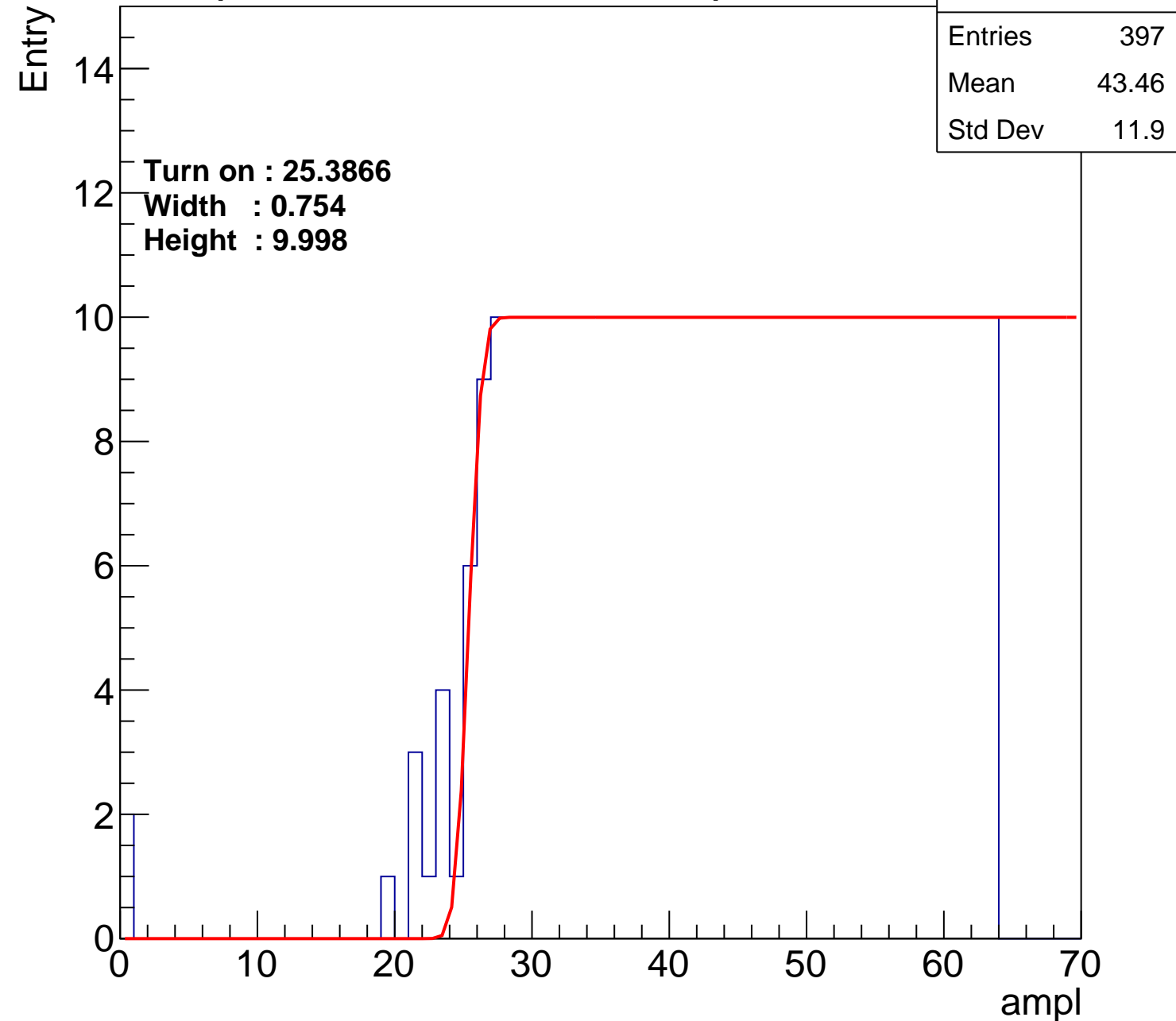
Width : 0.754

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.23
Std Dev	11.4

Turn on : 26.3950

Width : 3.039

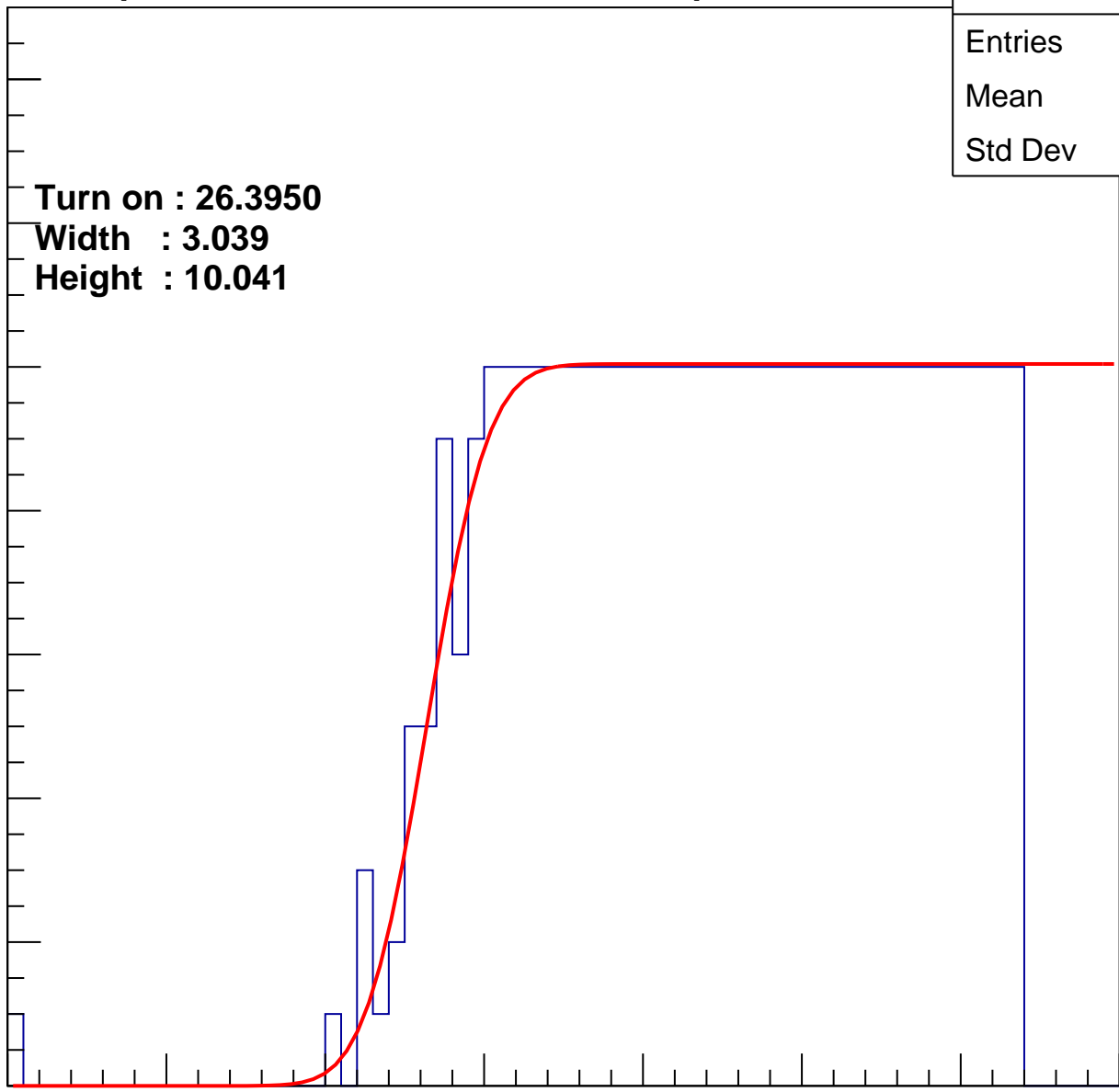
Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U26-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.62
Std Dev	11.85

Turn on : 24.9535

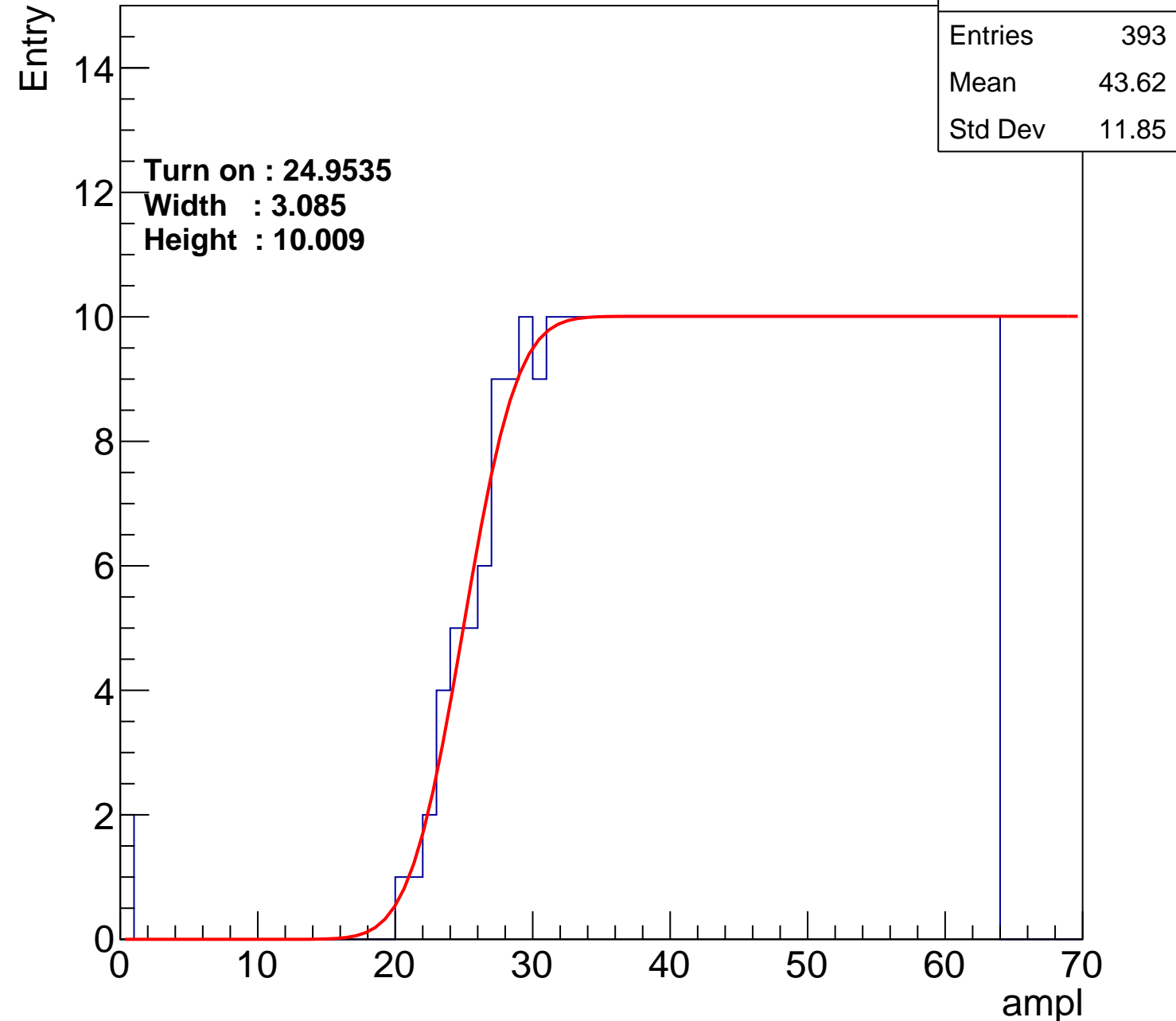
Width : 3.085

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch17

calib_packv5_042523_0143.root, FC#10, port B3

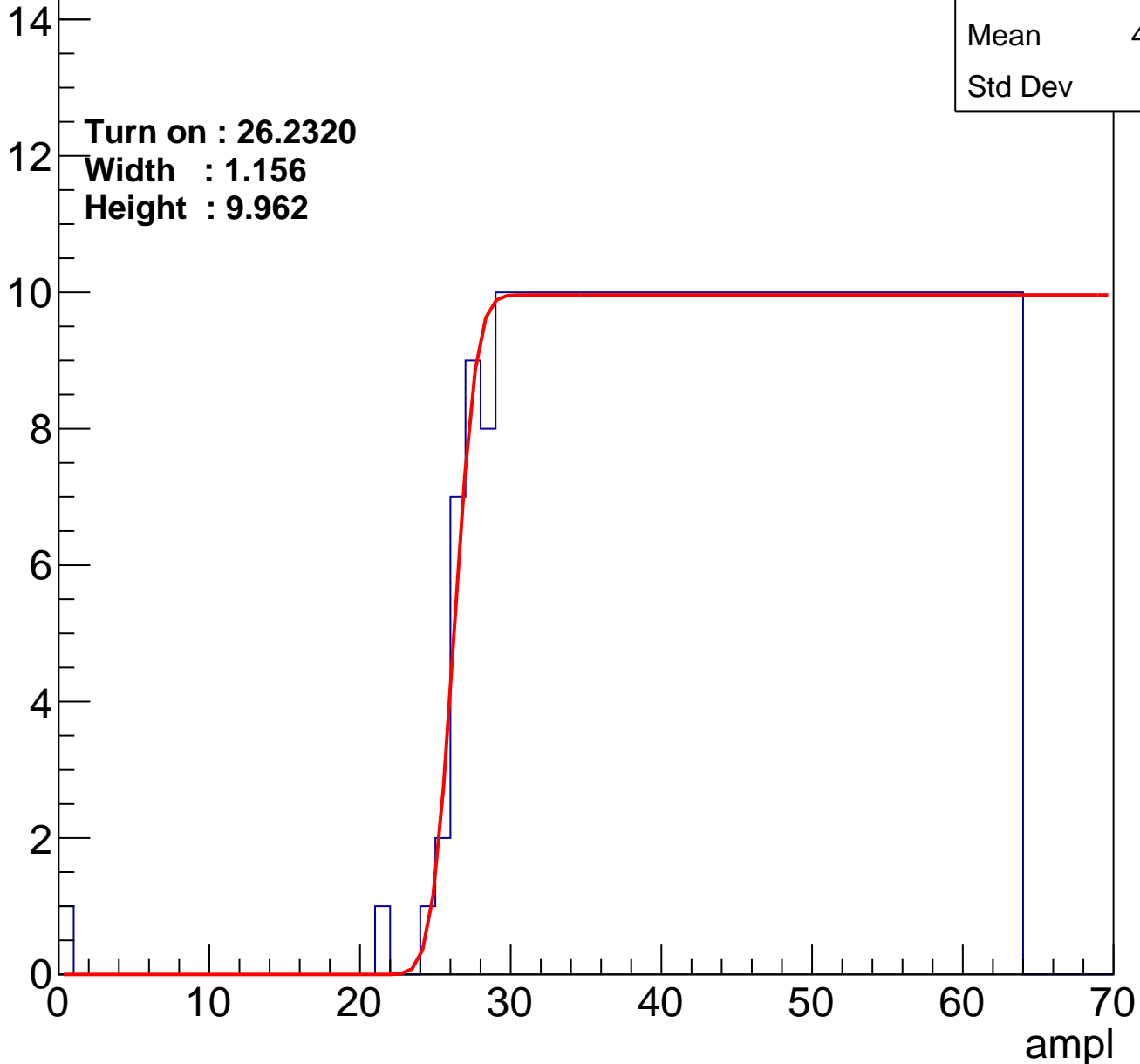
Entries	379
Mean	44.44
Std Dev	11.2

Turn on : 26.2320

Width : 1.156

Height : 9.962

Entry



B1L002S, U26-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.94
Std Dev	12.32

Turn on : 24.1647

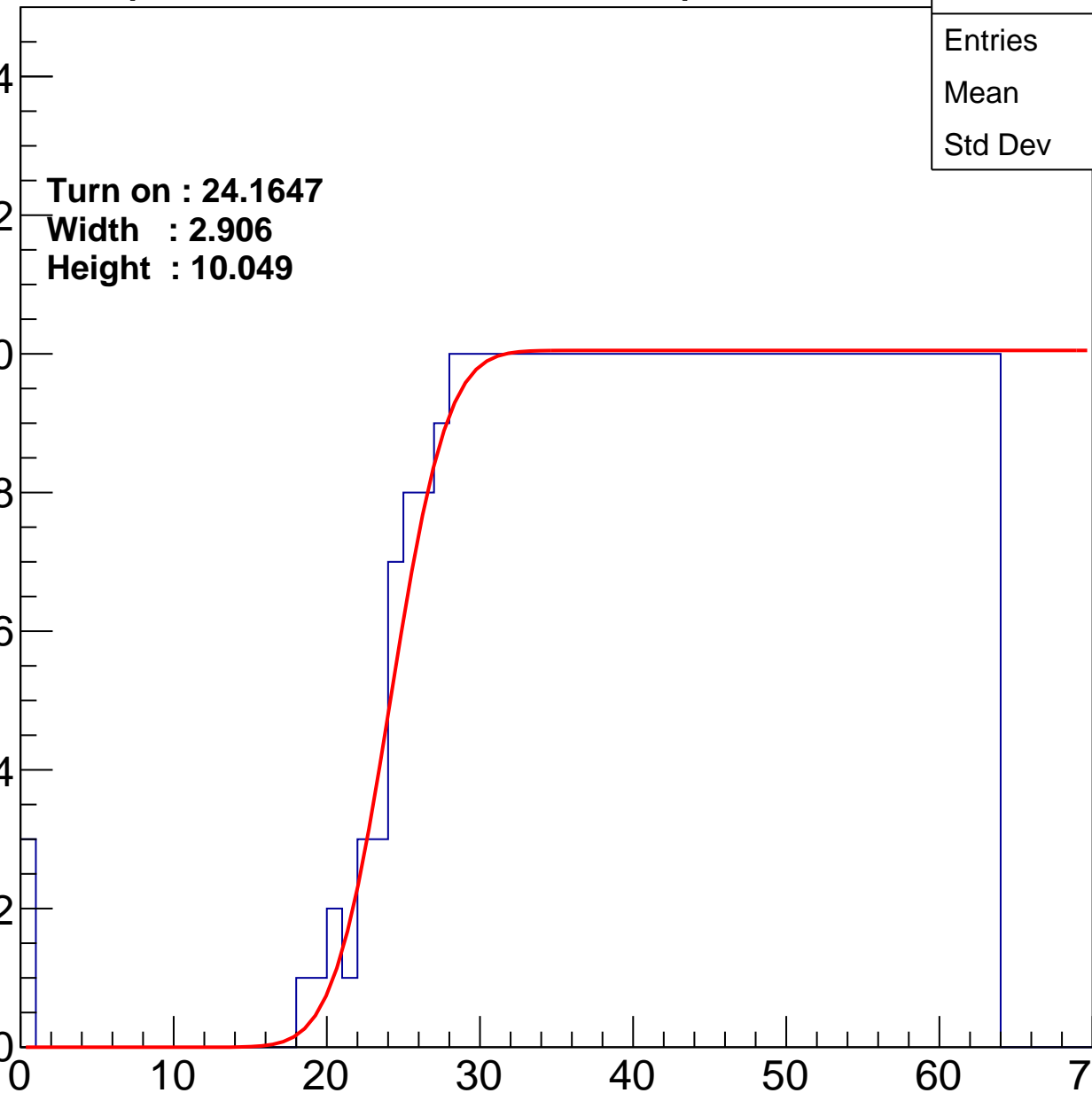
Width : 2.906

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.43
Std Dev	11.23

Turn on : 26.7799

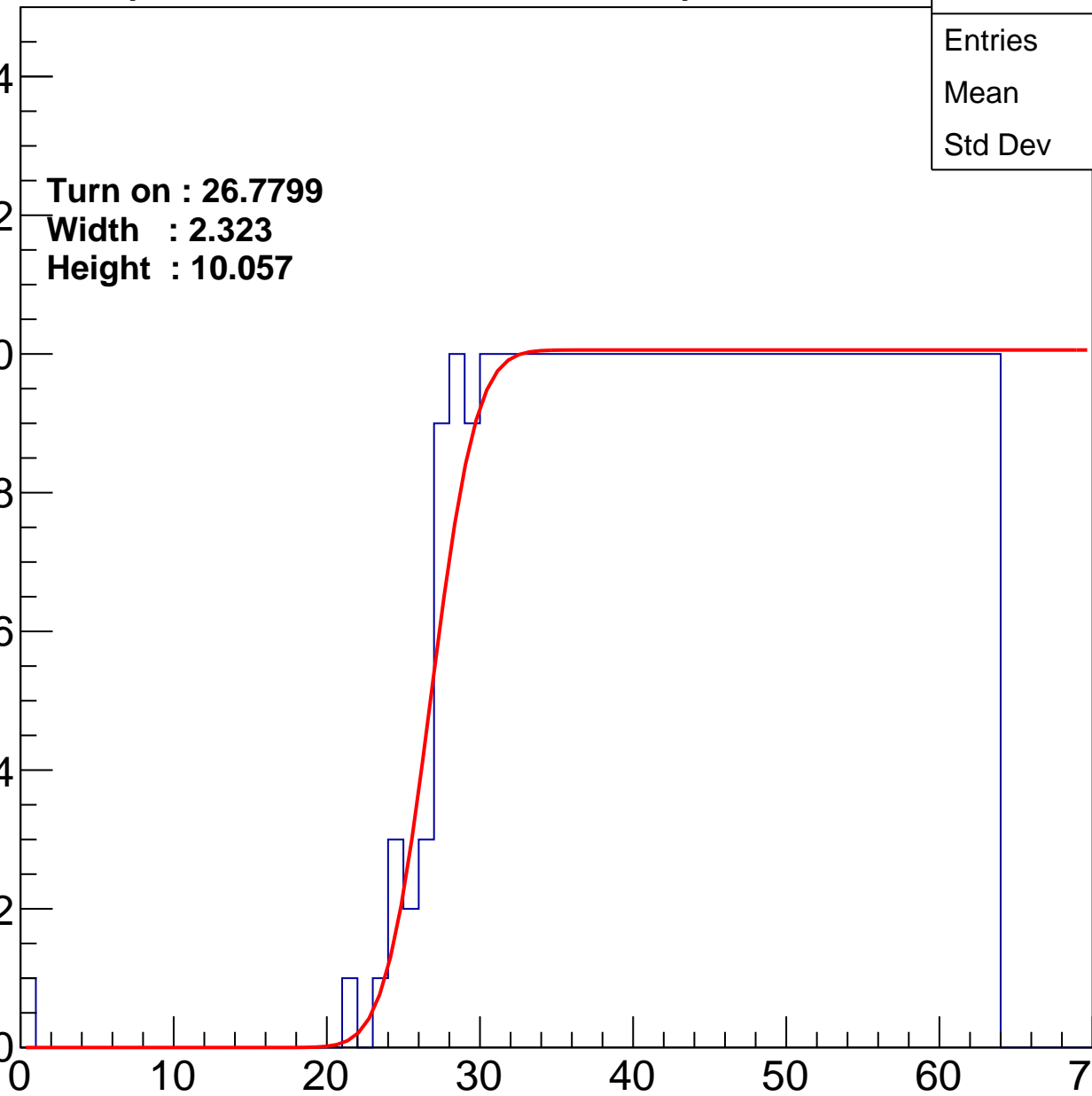
Width : 2.323

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch20

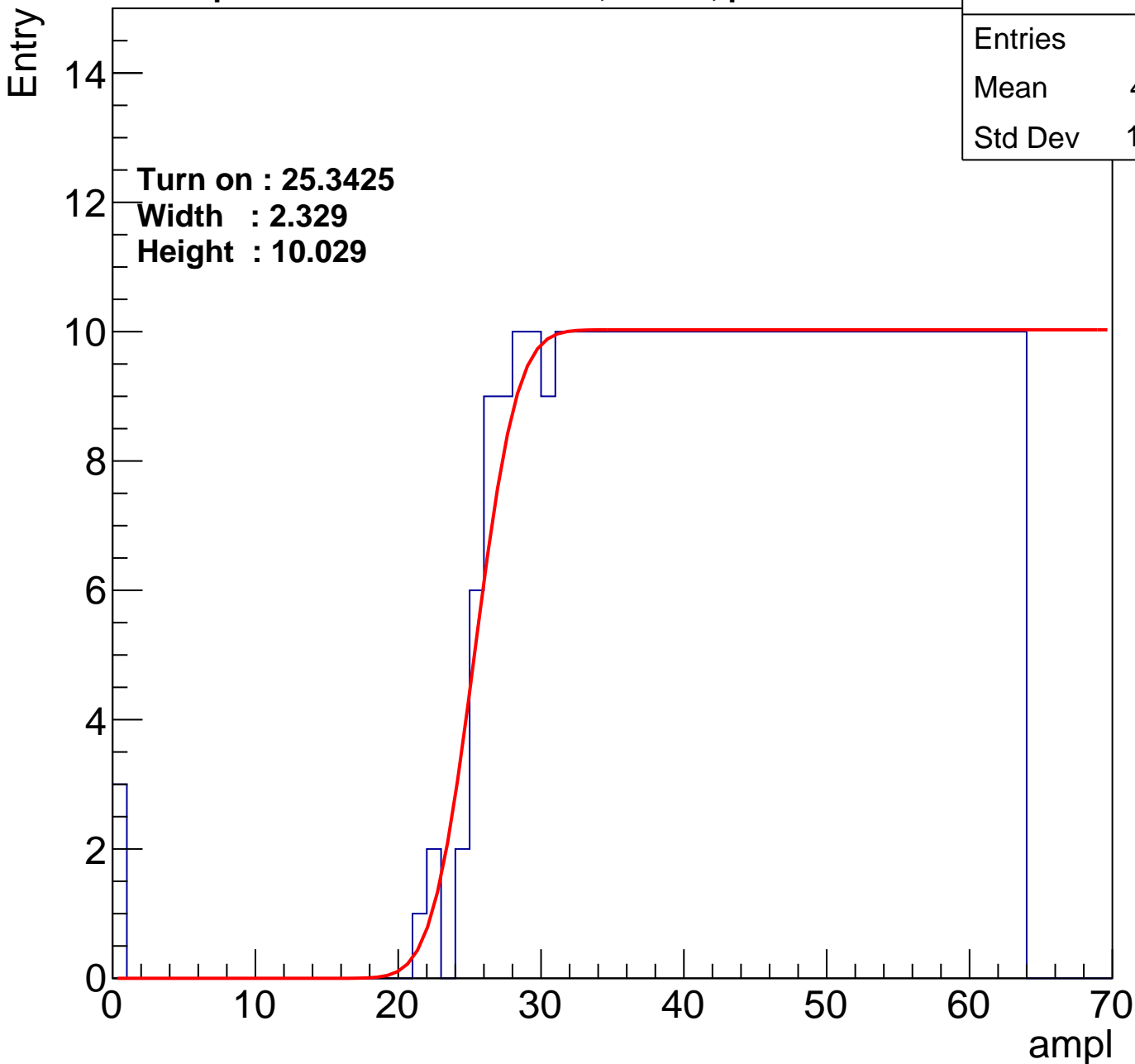
calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.71
Std Dev	11.88

Turn on : 25.3425

Width : 2.329

Height : 10.029



B1L002S, U26-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 25.8458

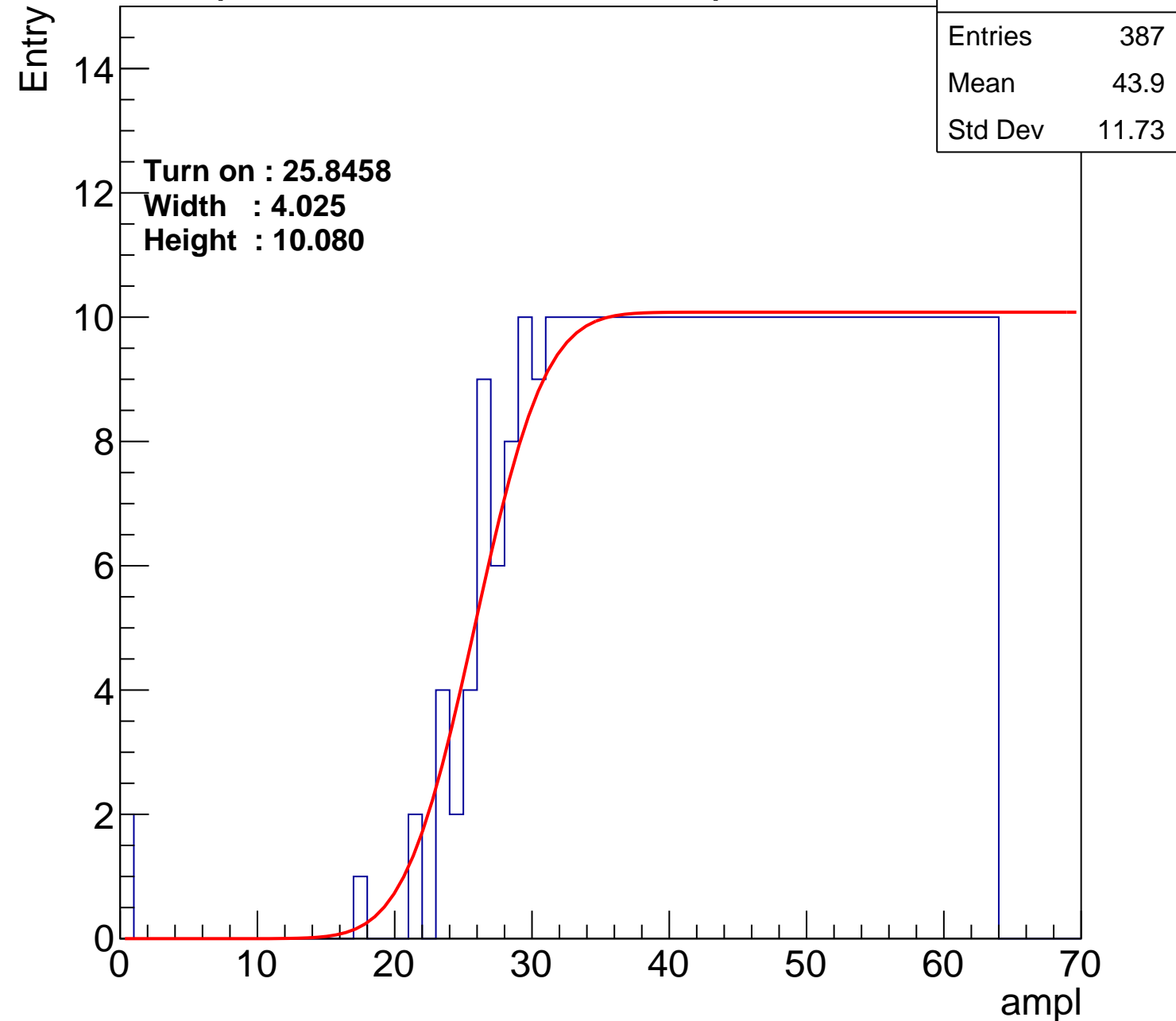
Width : 4.025

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.52
Std Dev	11.18

Turn on : 26.5314

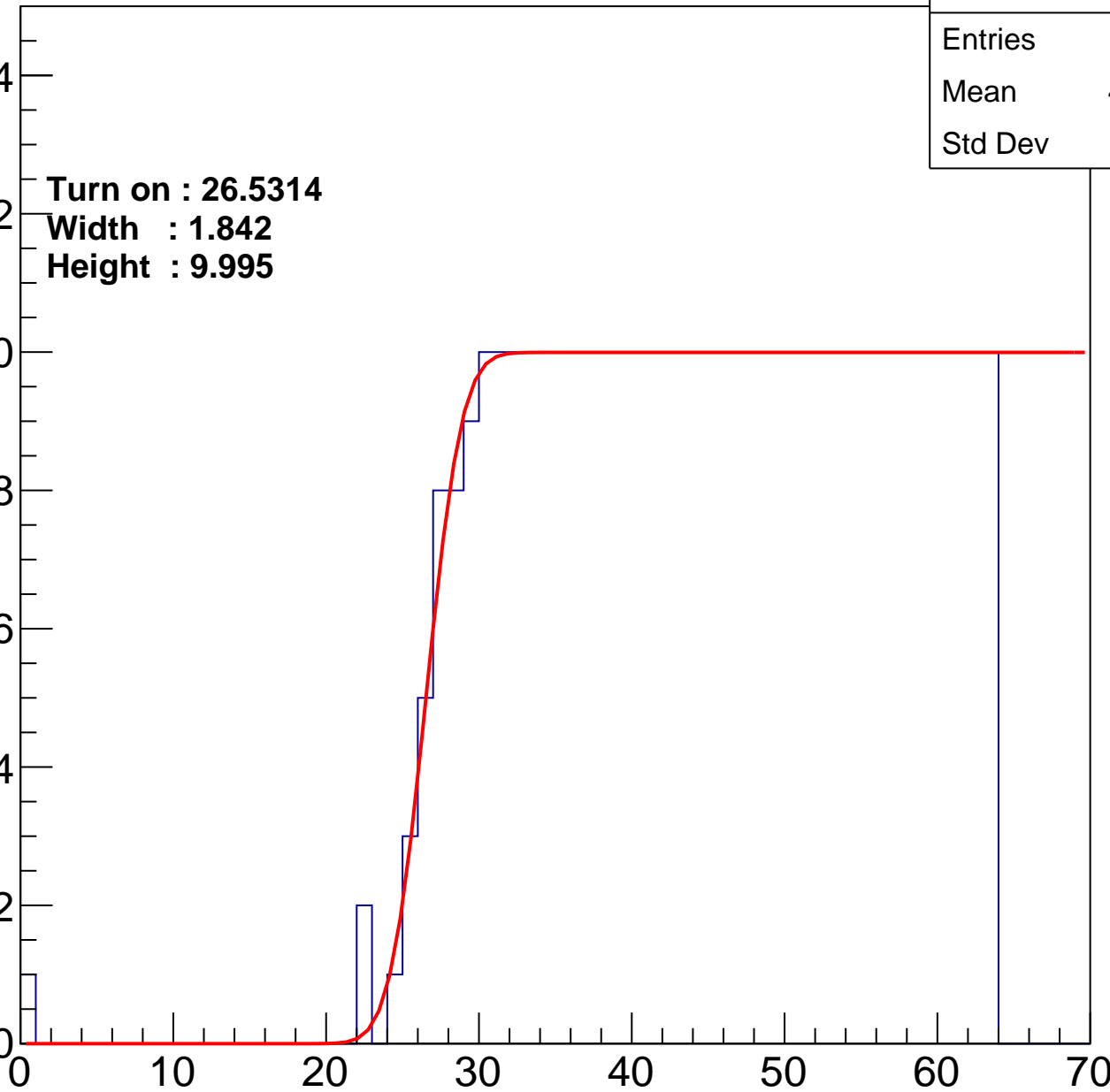
Width : 1.842

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.28
Std Dev	11.96

Turn on : 23.5534

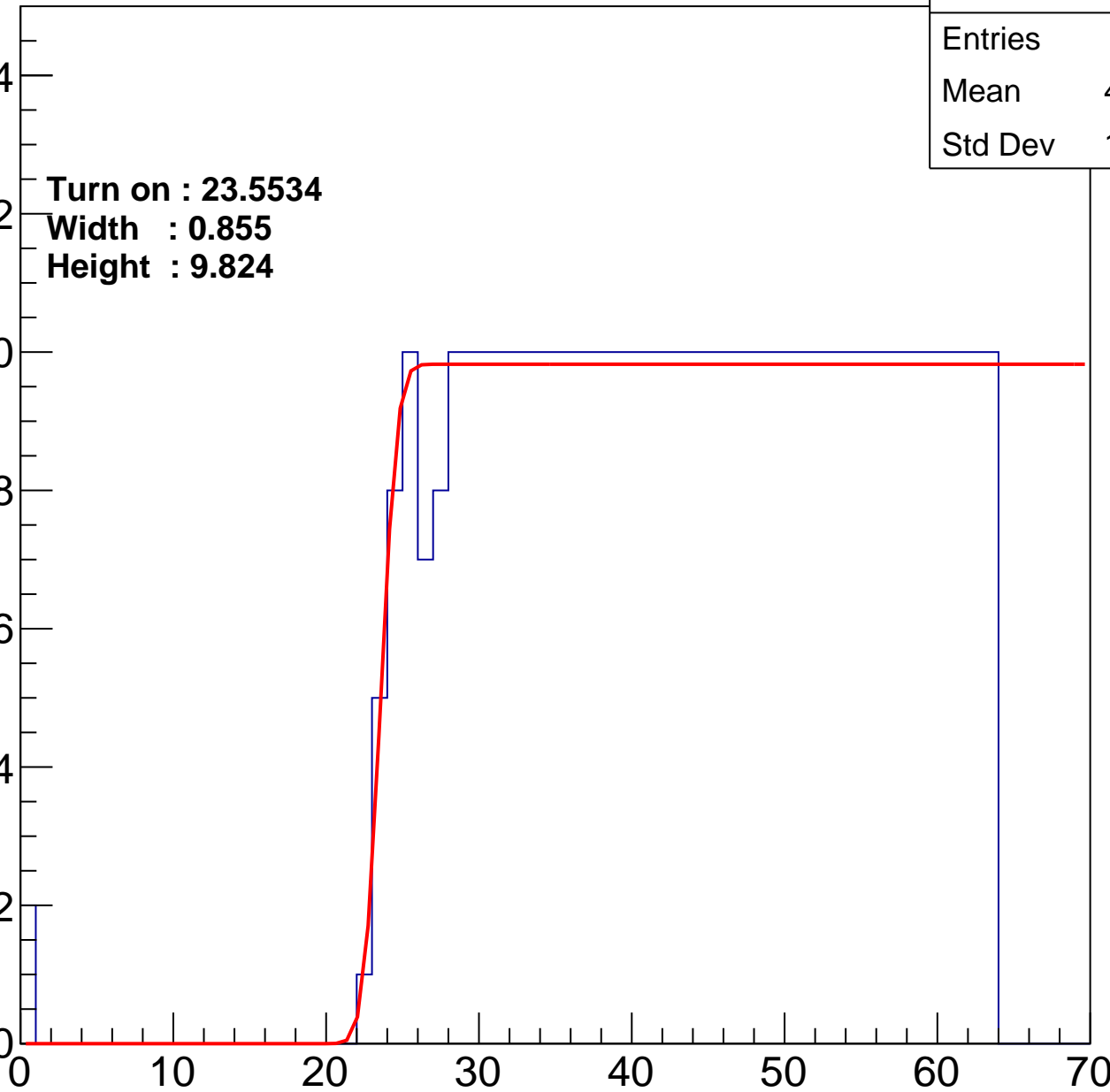
Width : 0.855

Height : 9.824

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.9799

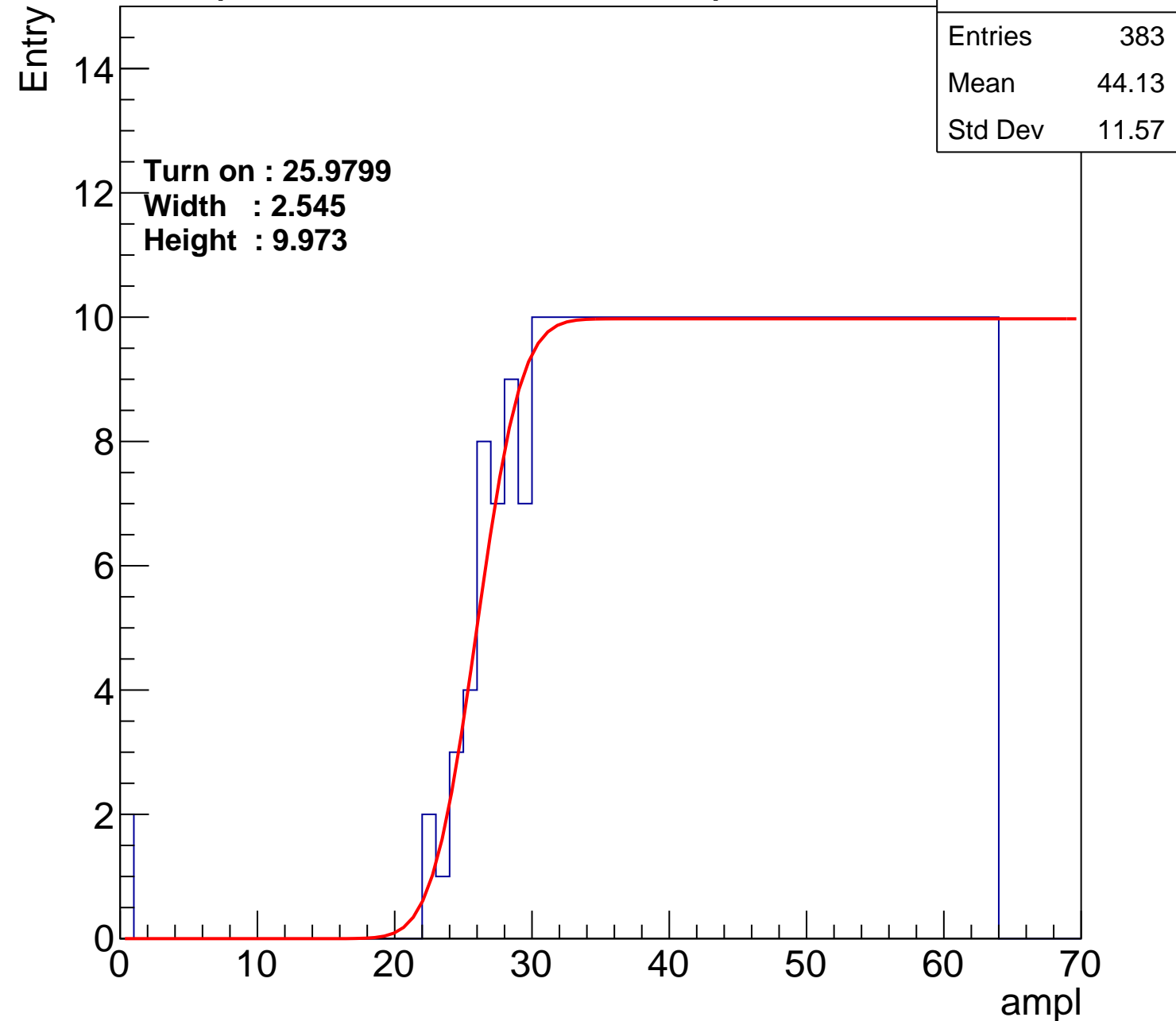
Width : 2.545

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.9146

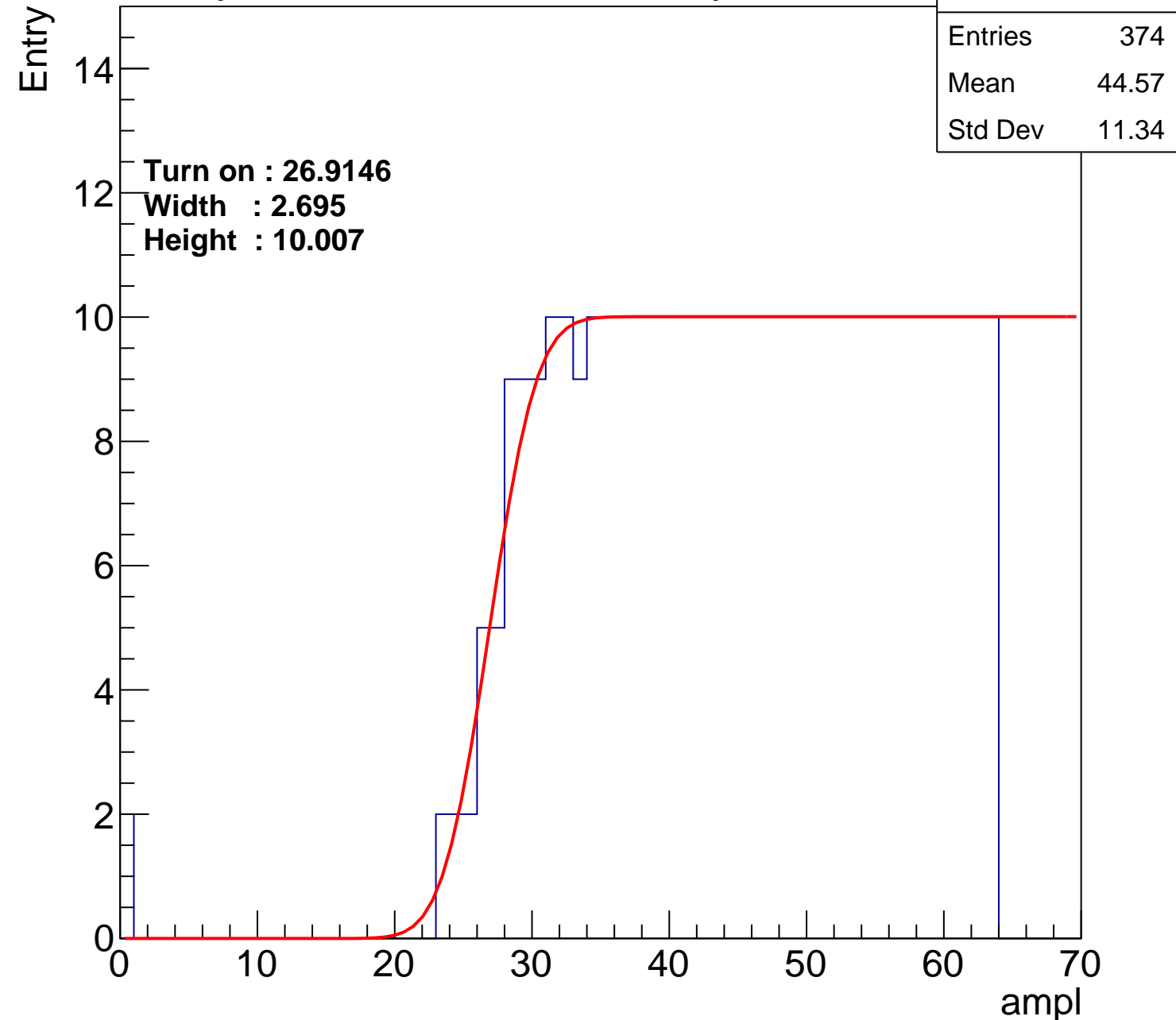
Width : 2.695

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.18
Std Dev	11.67

Turn on : 26.4500

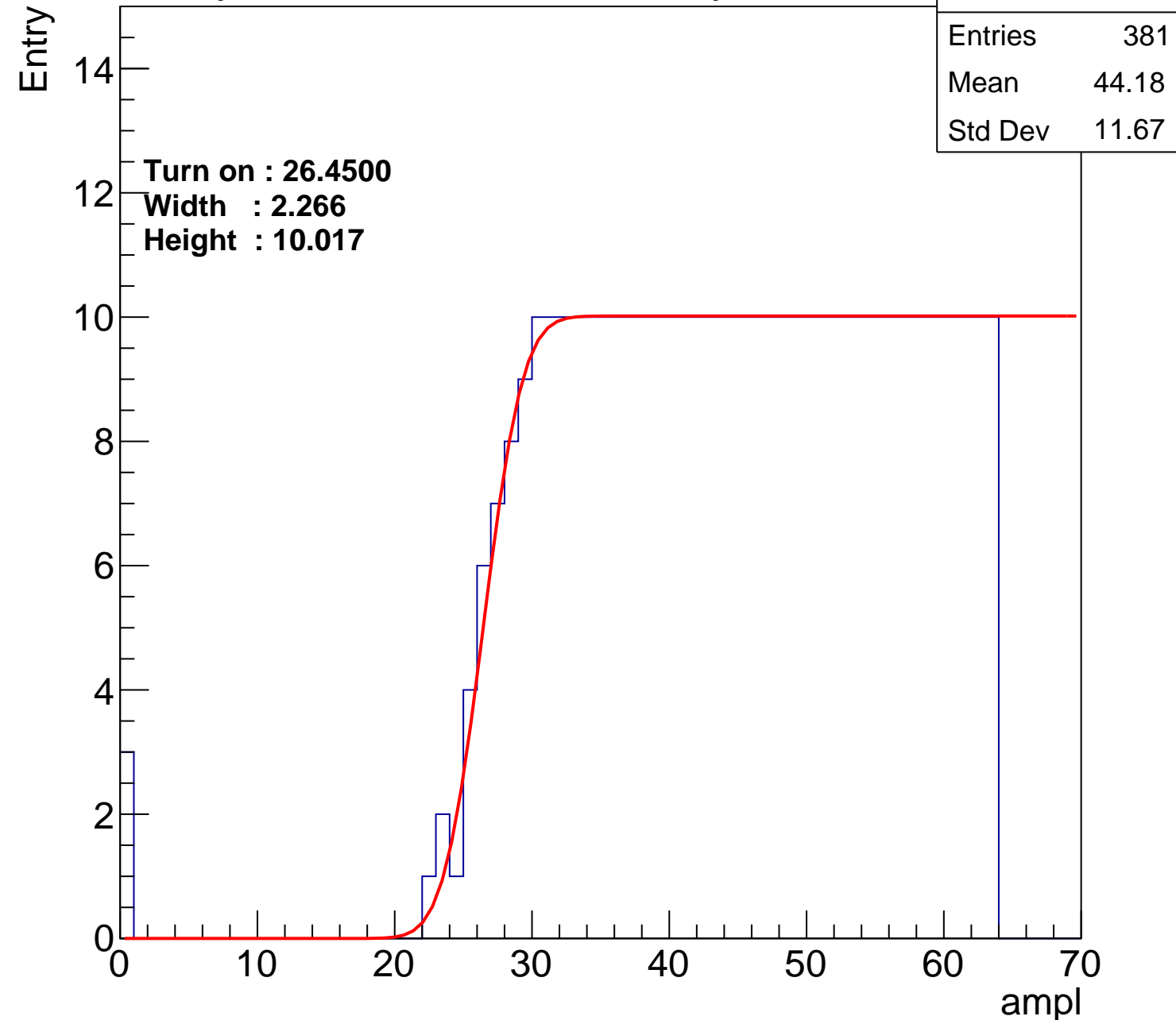
Width : 2.266

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.71
Std Dev	11.09

Turn on : 27.3415

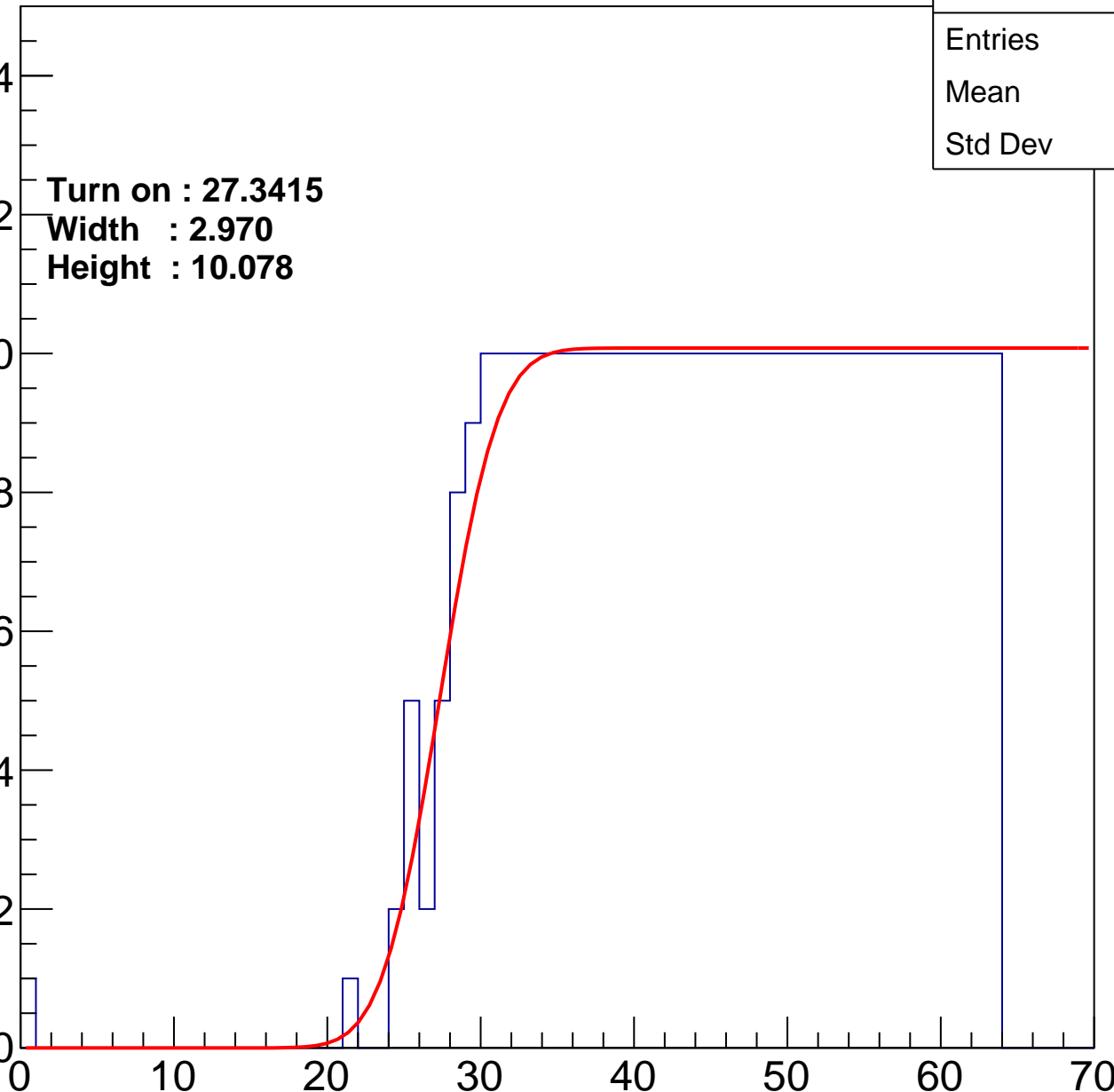
Width : 2.970

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.42
Std Dev	11.8

Turn on : 23.5584

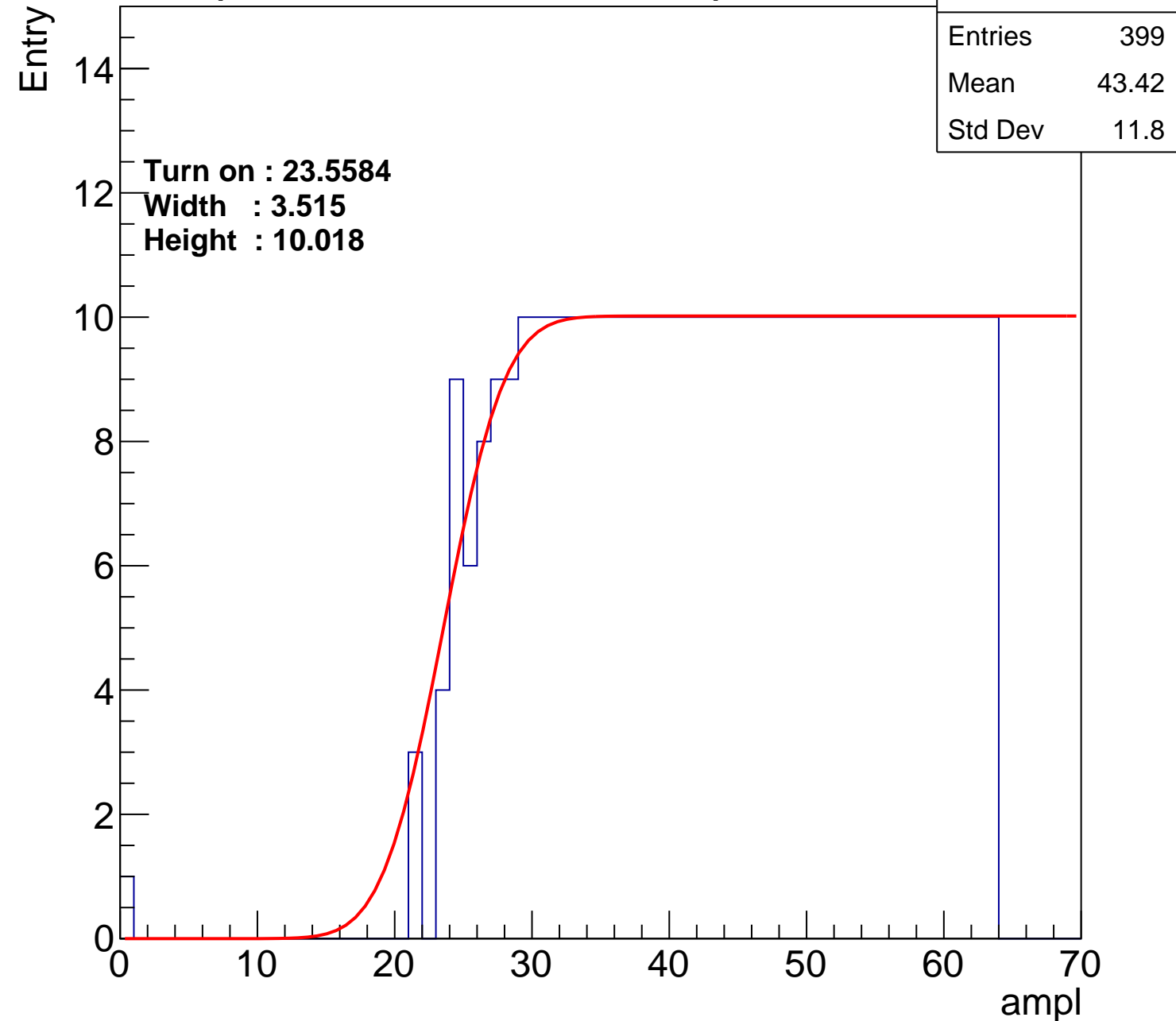
Width : 3.515

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.18
Std Dev	11.67

Turn on : 25.9302

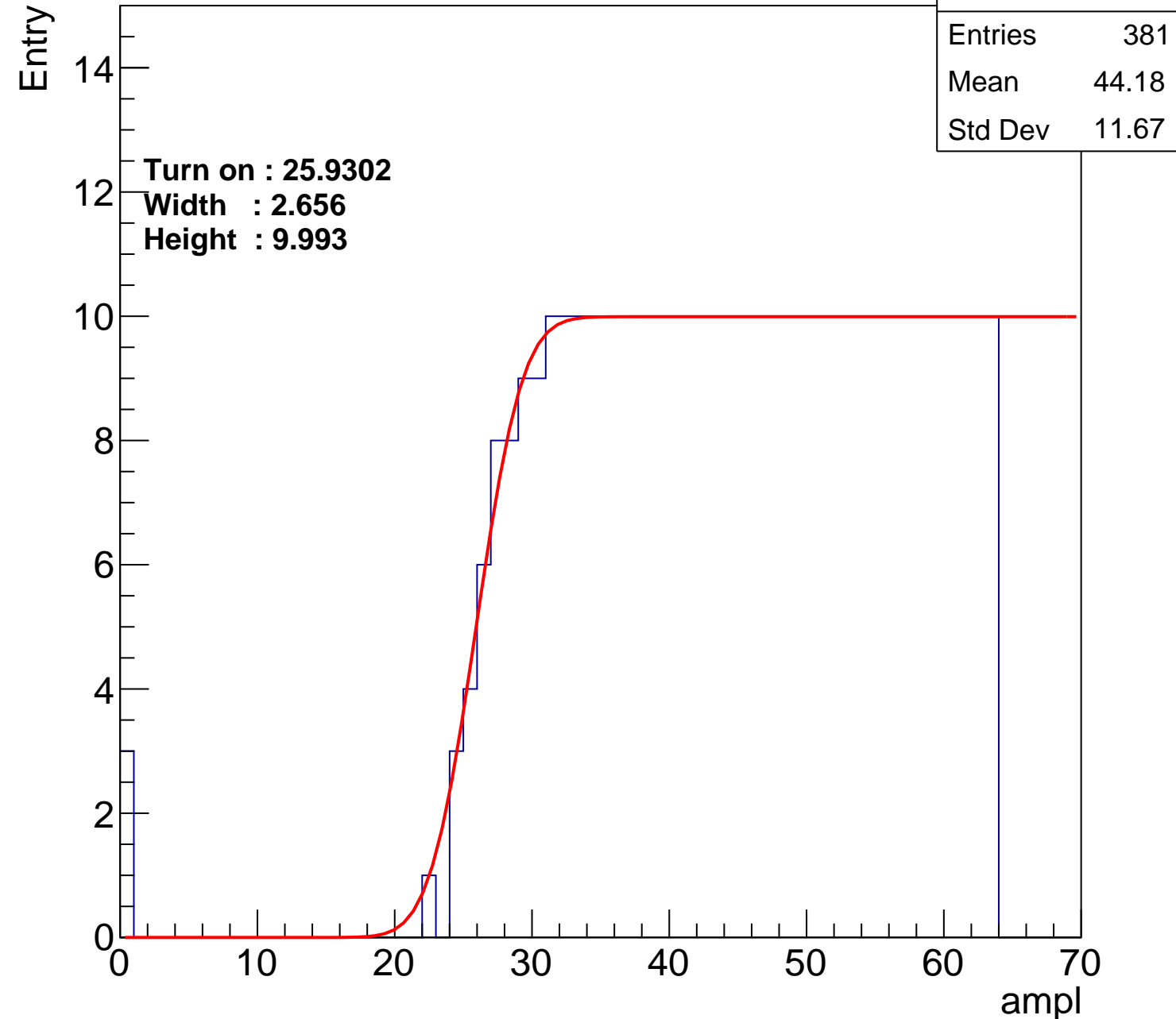
Width : 2.656

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.43
Std Dev	11.54

Turn on : 26.7177

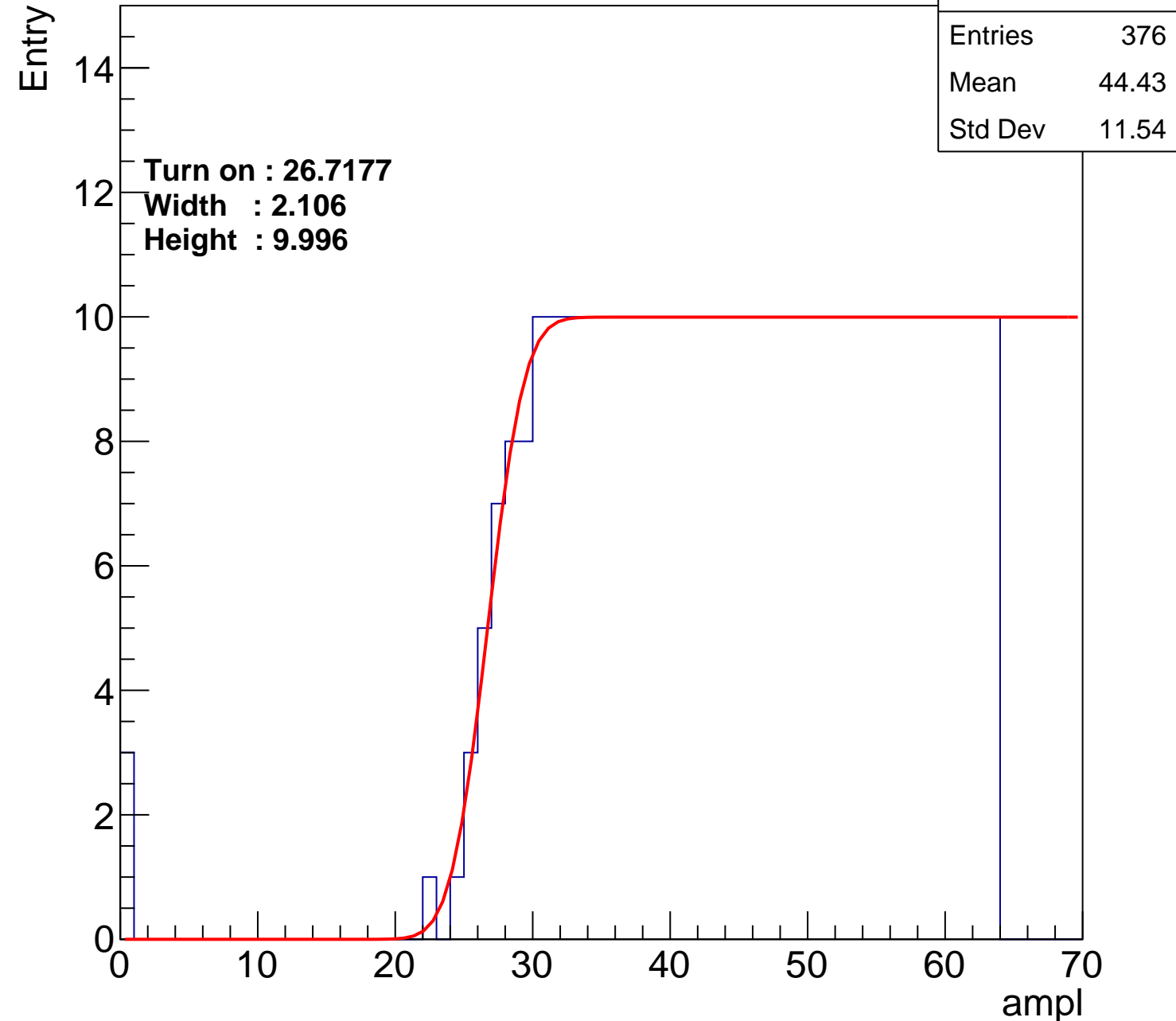
Width : 2.106

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch31

calib_packv5_042523_0143.root, FC#10, port B3

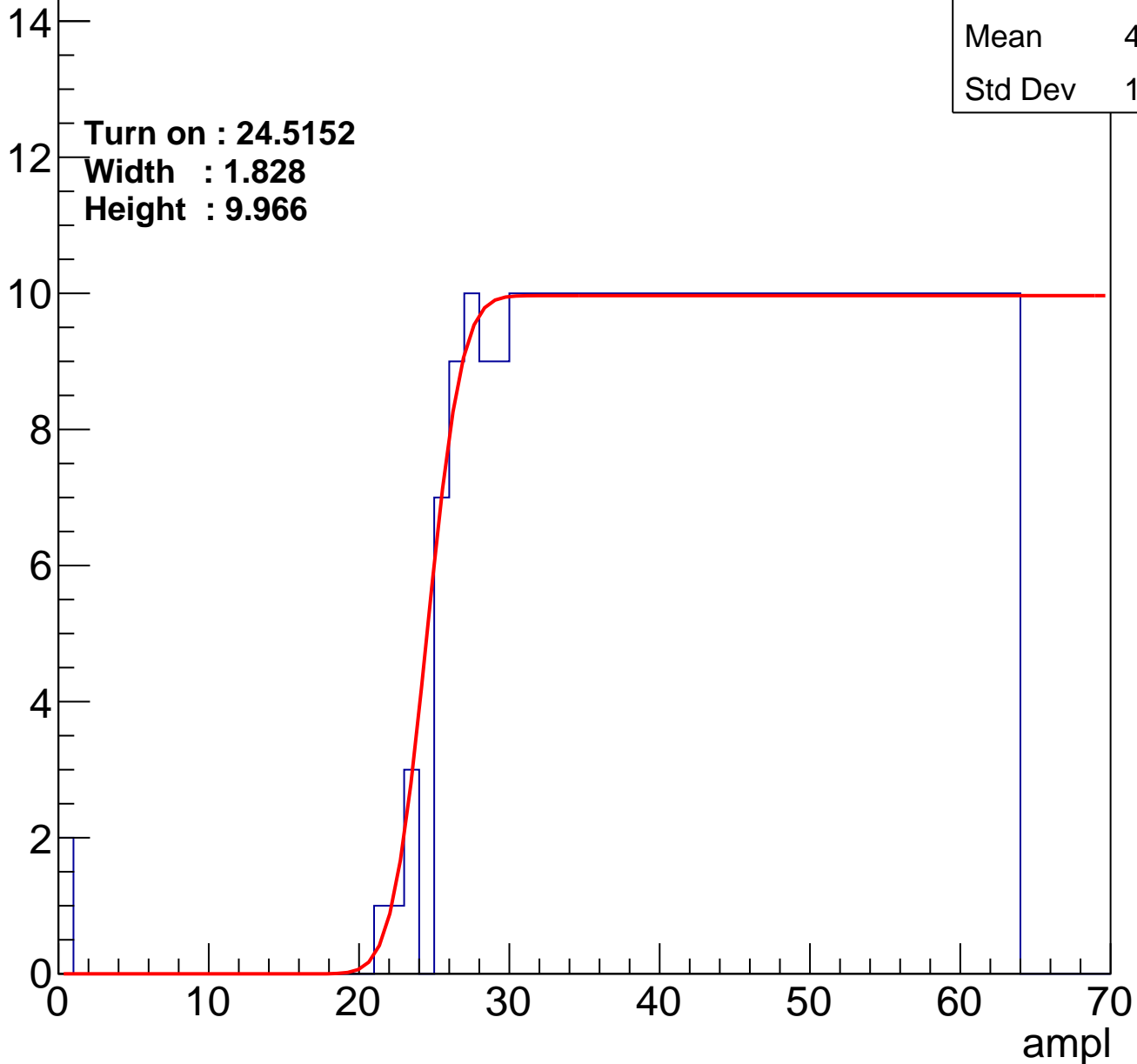
Entries	391
Mean	43.77
Std Dev	11.72

Turn on : 24.5152

Width : 1.828

Height : 9.966

Entry



B1L002S, U26-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.93
Std Dev	11.69

Turn on : 25.6392

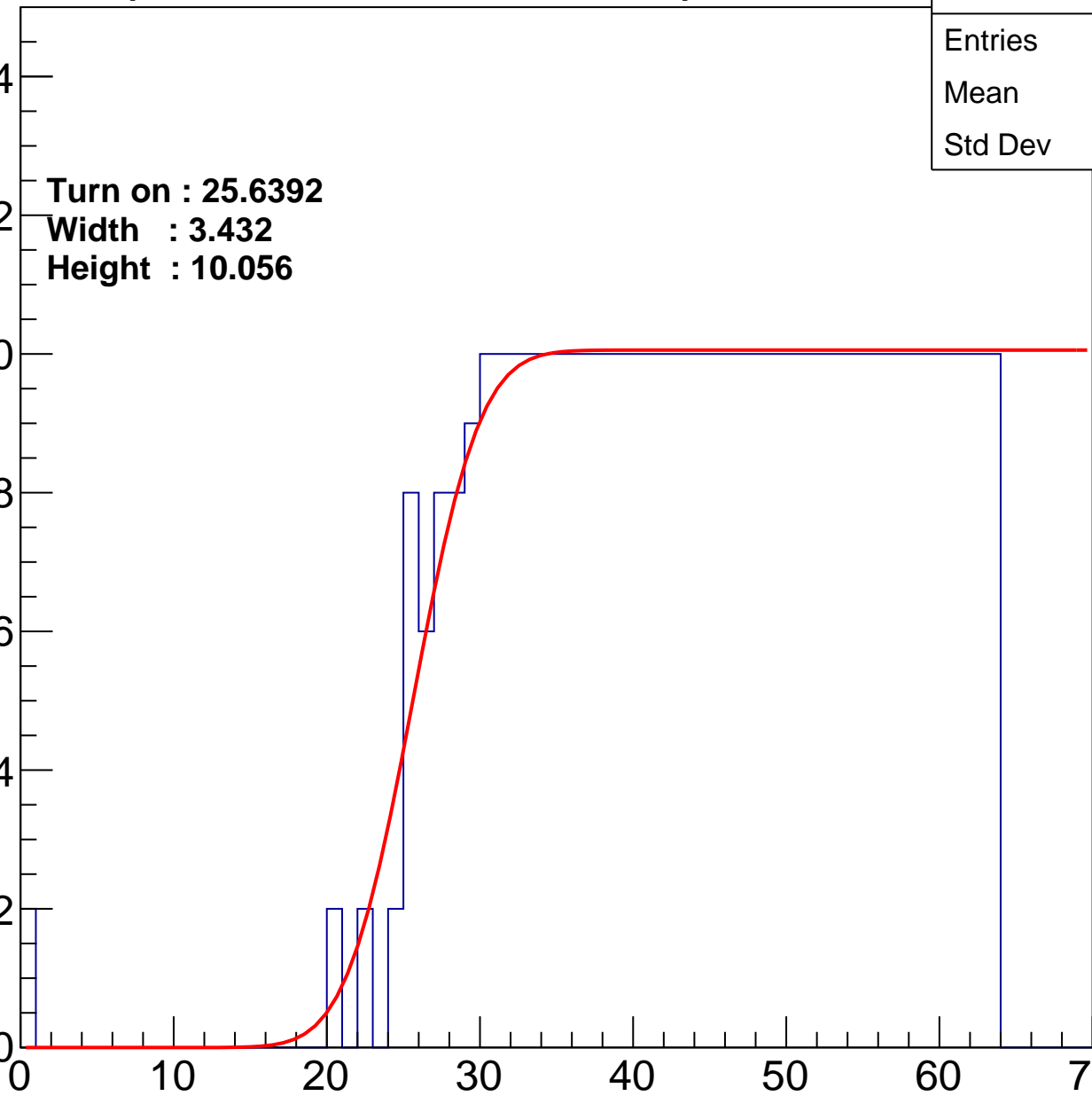
Width : 3.432

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch33

calib_packv5_042523_0143.root, FC#10, port B3

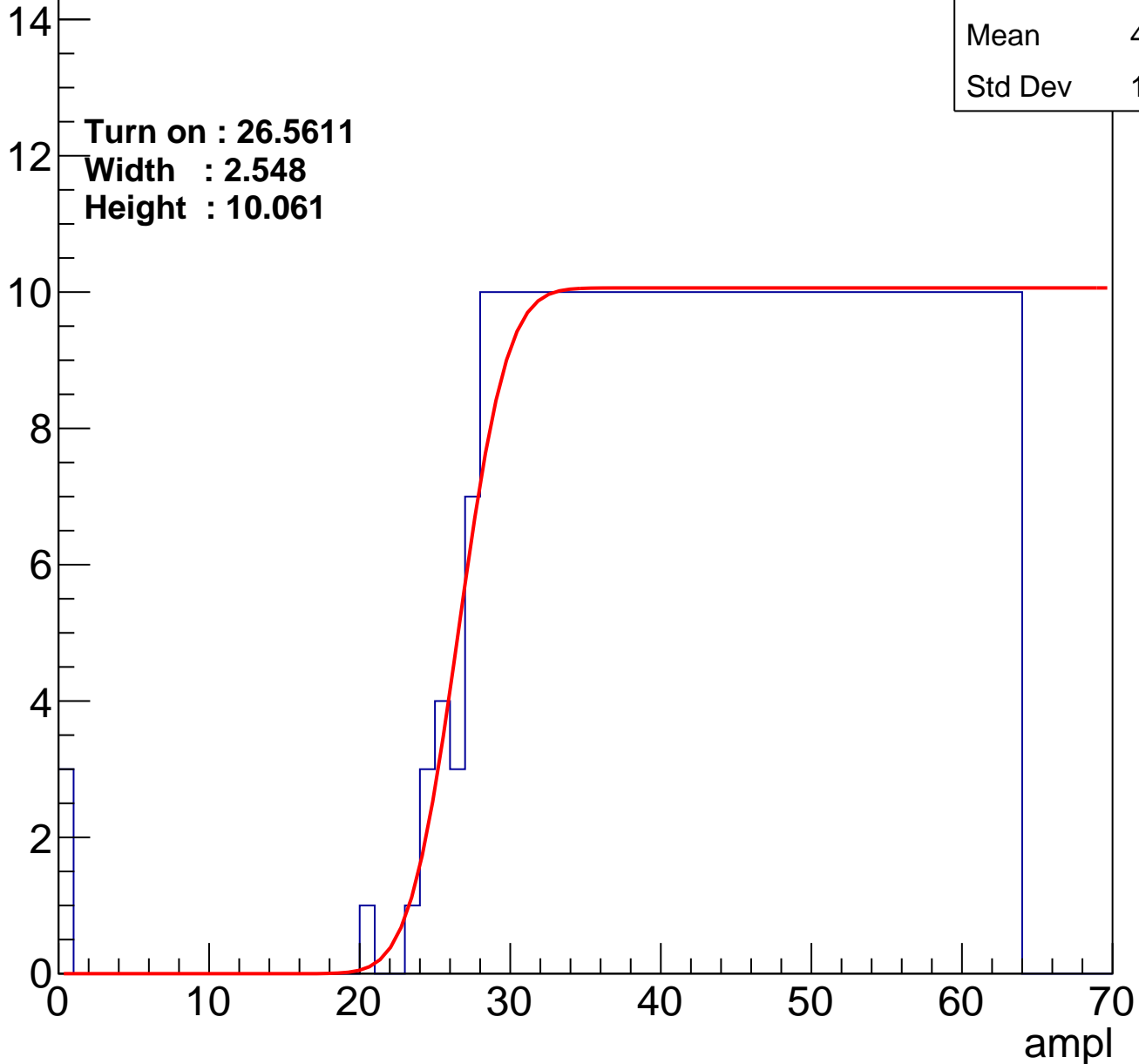
Entries	382
Mean	44.14
Std Dev	11.68

Turn on : 26.5611

Width : 2.548

Height : 10.061

Entry



B1L002S, U26-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.3738

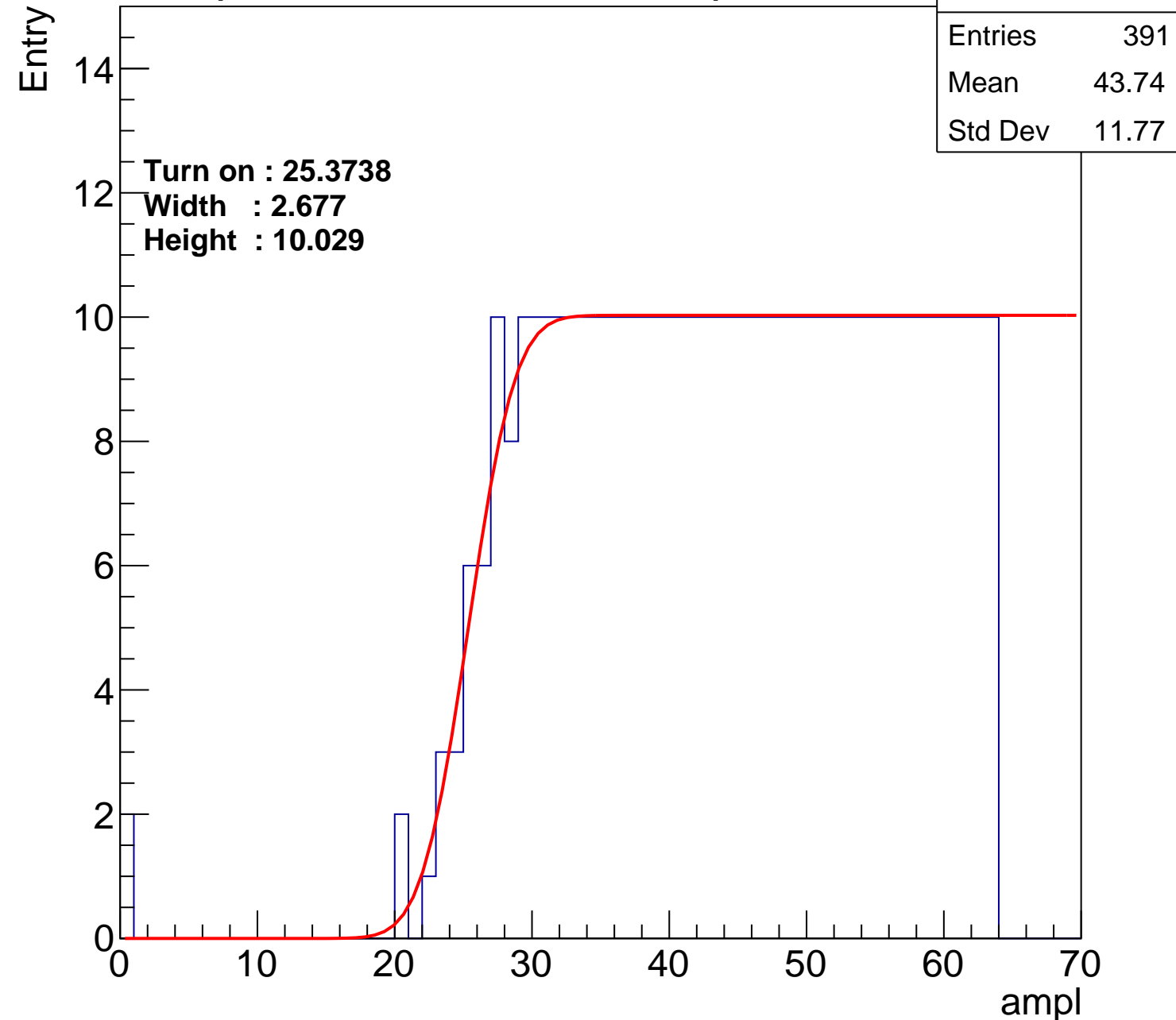
Width : 2.677

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.48
Std Dev	11.81

Turn on : 24.8407

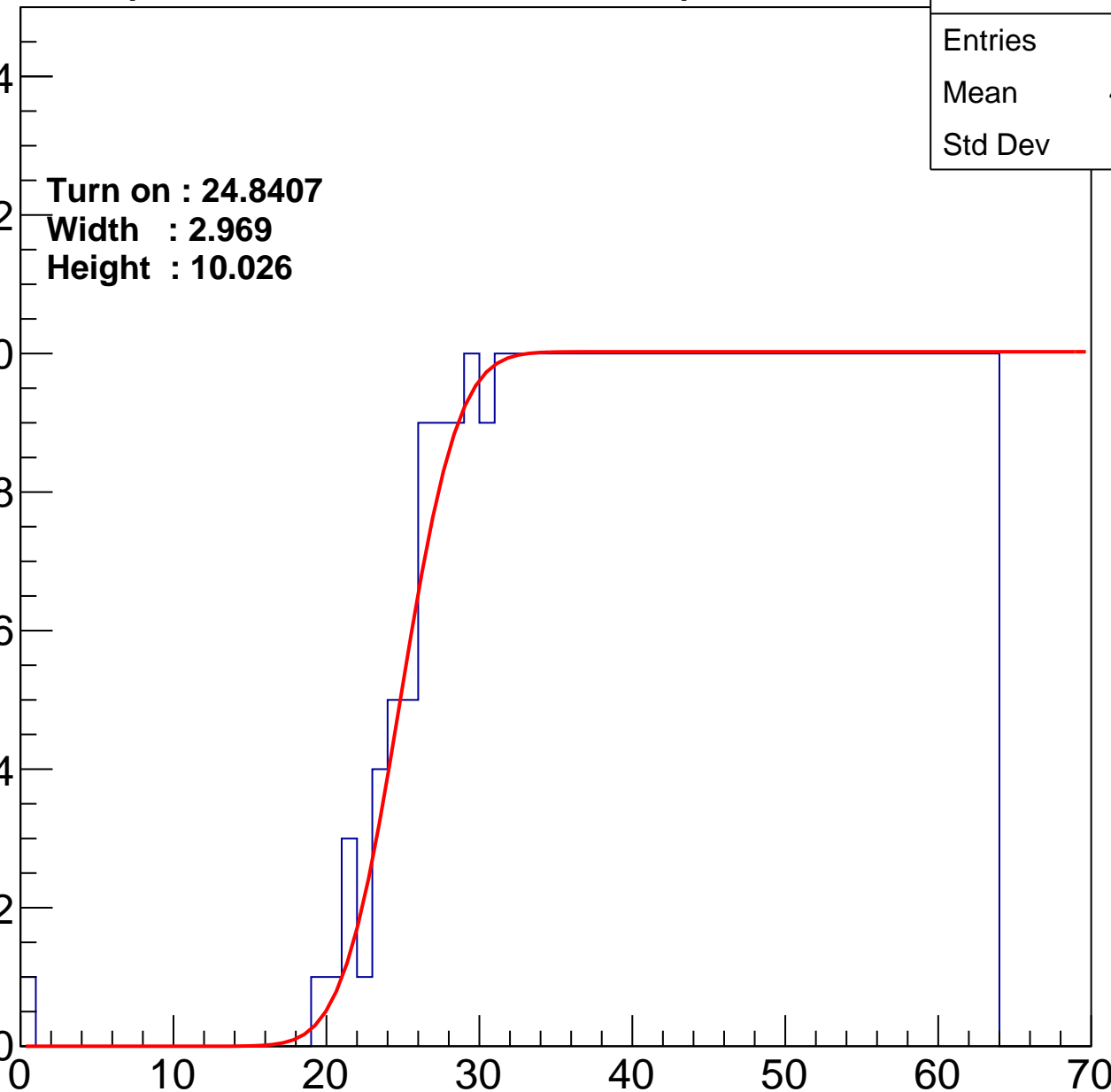
Width : 2.969

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 26.0163

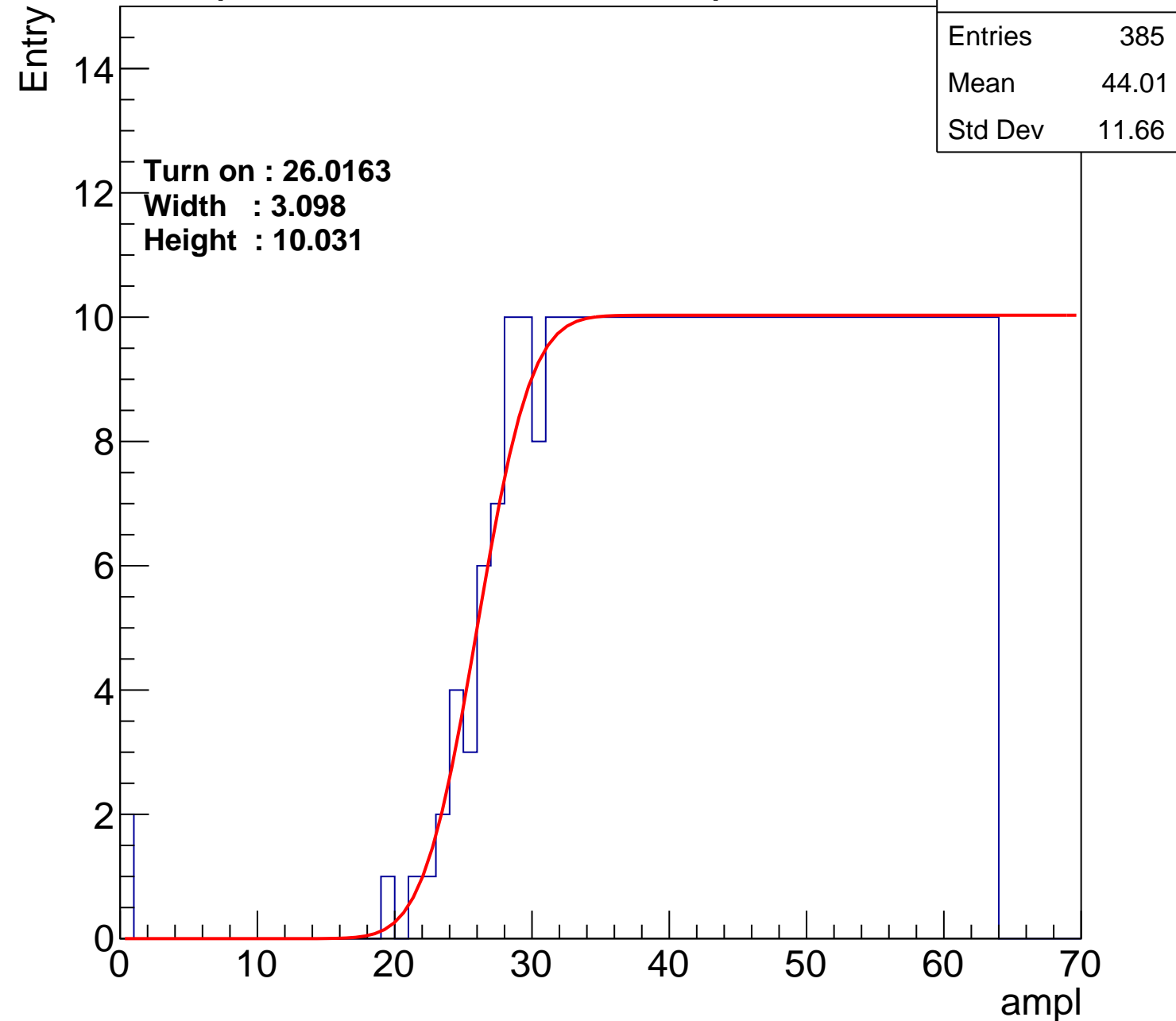
Width : 3.098

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch37

calib_packv5_042523_0143.root, FC#10, port B3

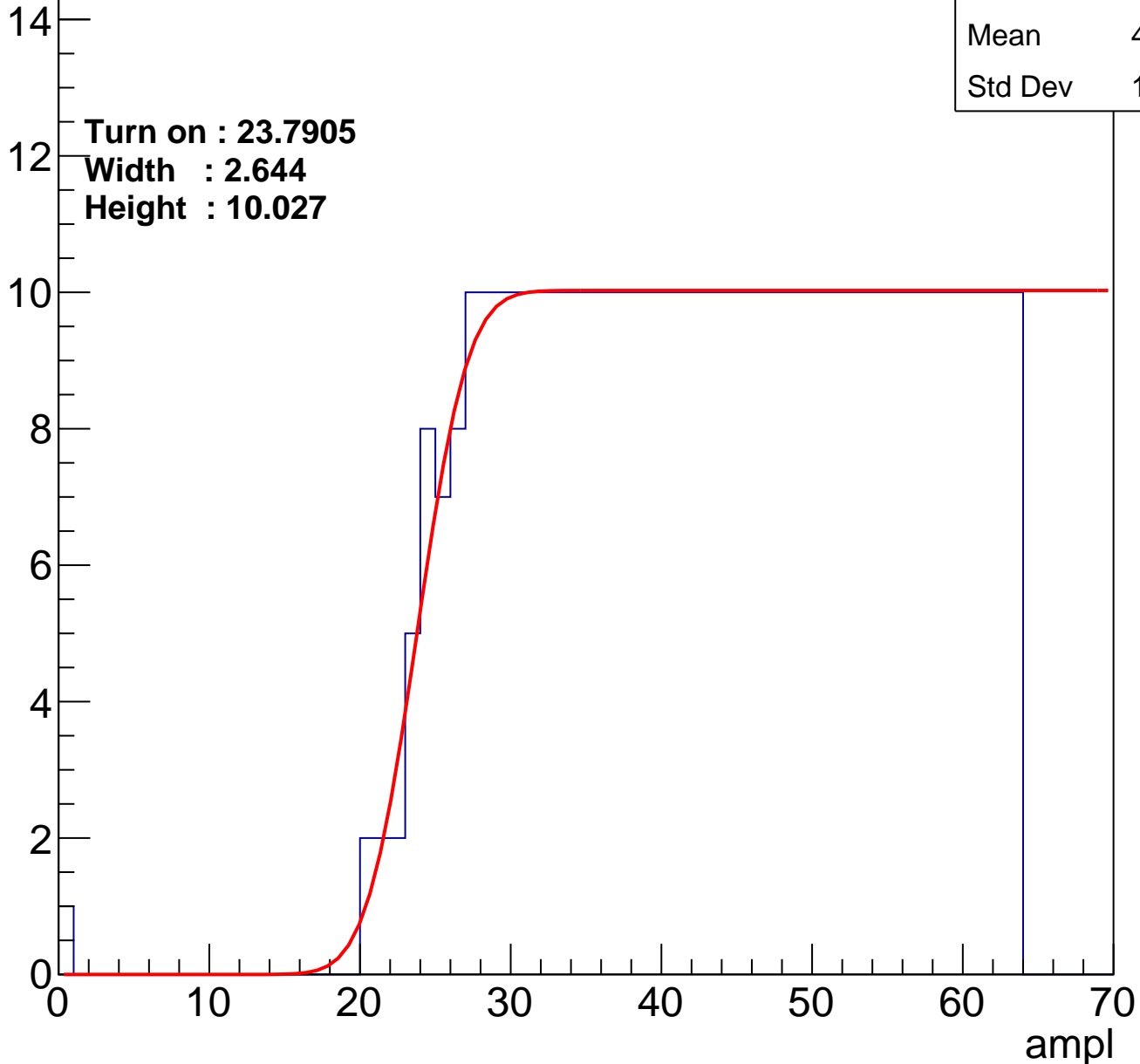
Entries	405
Mean	43.13
Std Dev	11.95

Turn on : 23.7905

Width : 2.644

Height : 10.027

Entry



B1L002S, U26-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.3832

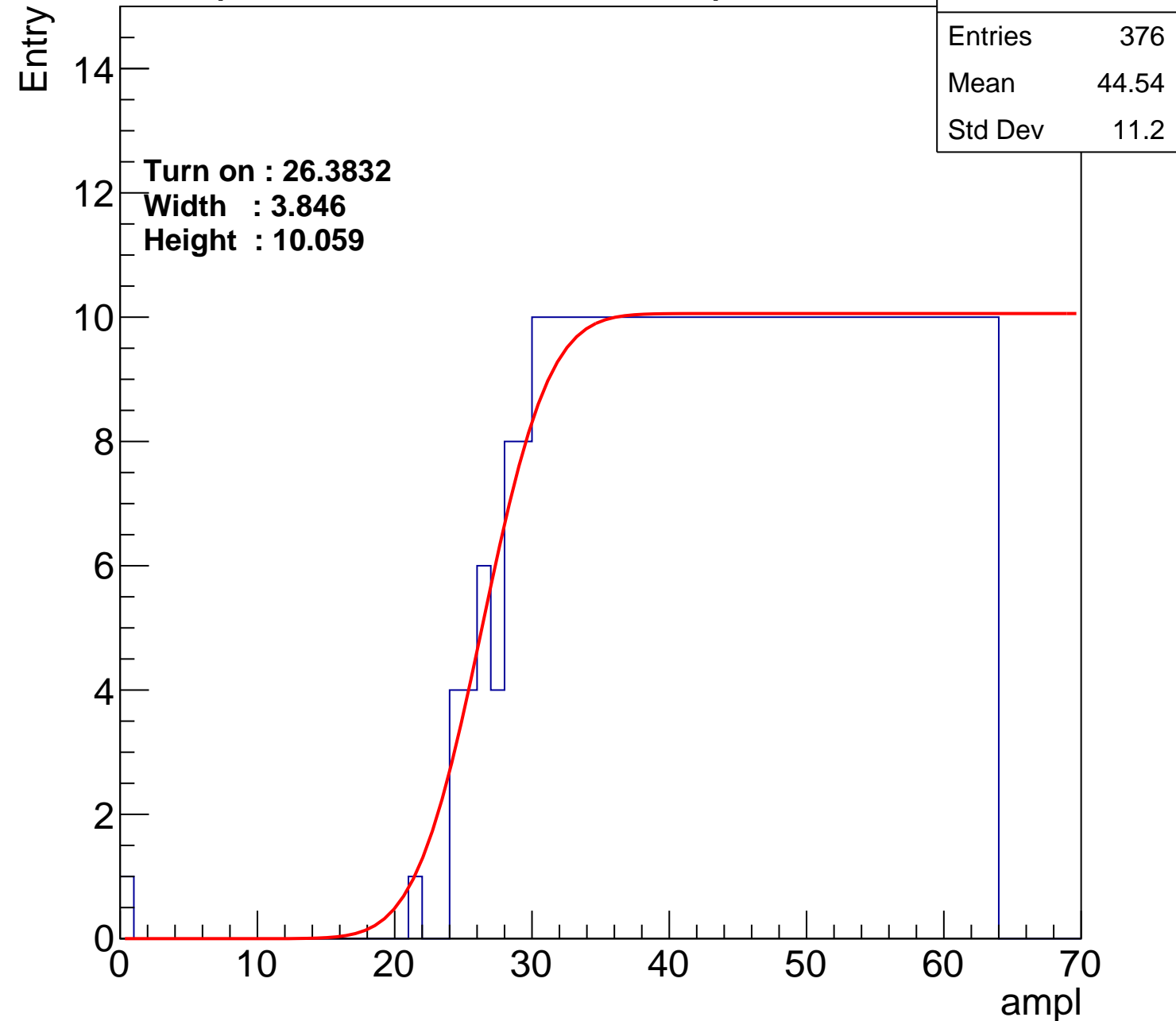
Width : 3.846

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch39

calib_packv5_042523_0143.root, FC#10, port B3

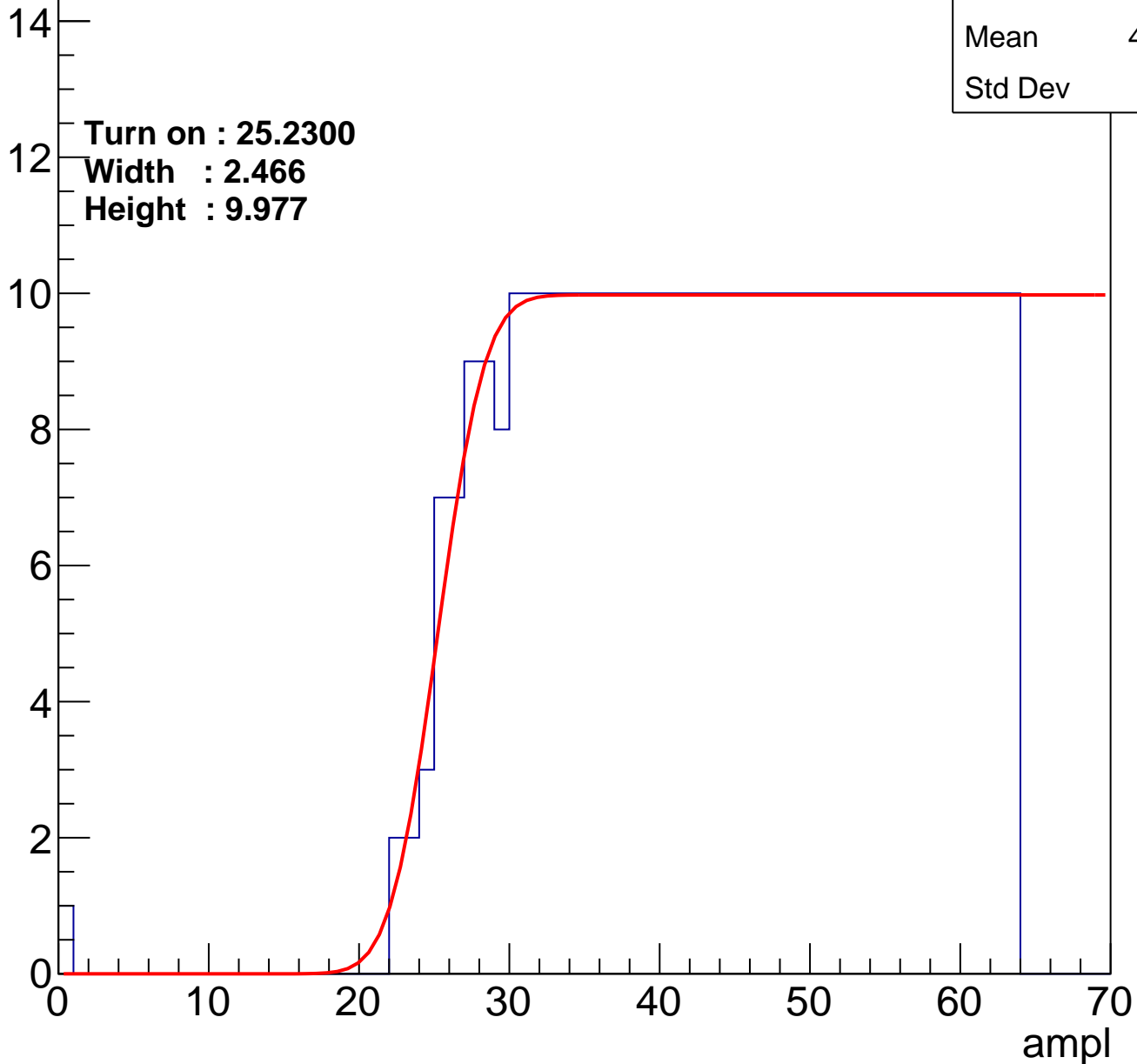
Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 25.2300

Width : 2.466

Height : 9.977

Entry



B1L002S, U26-ch40

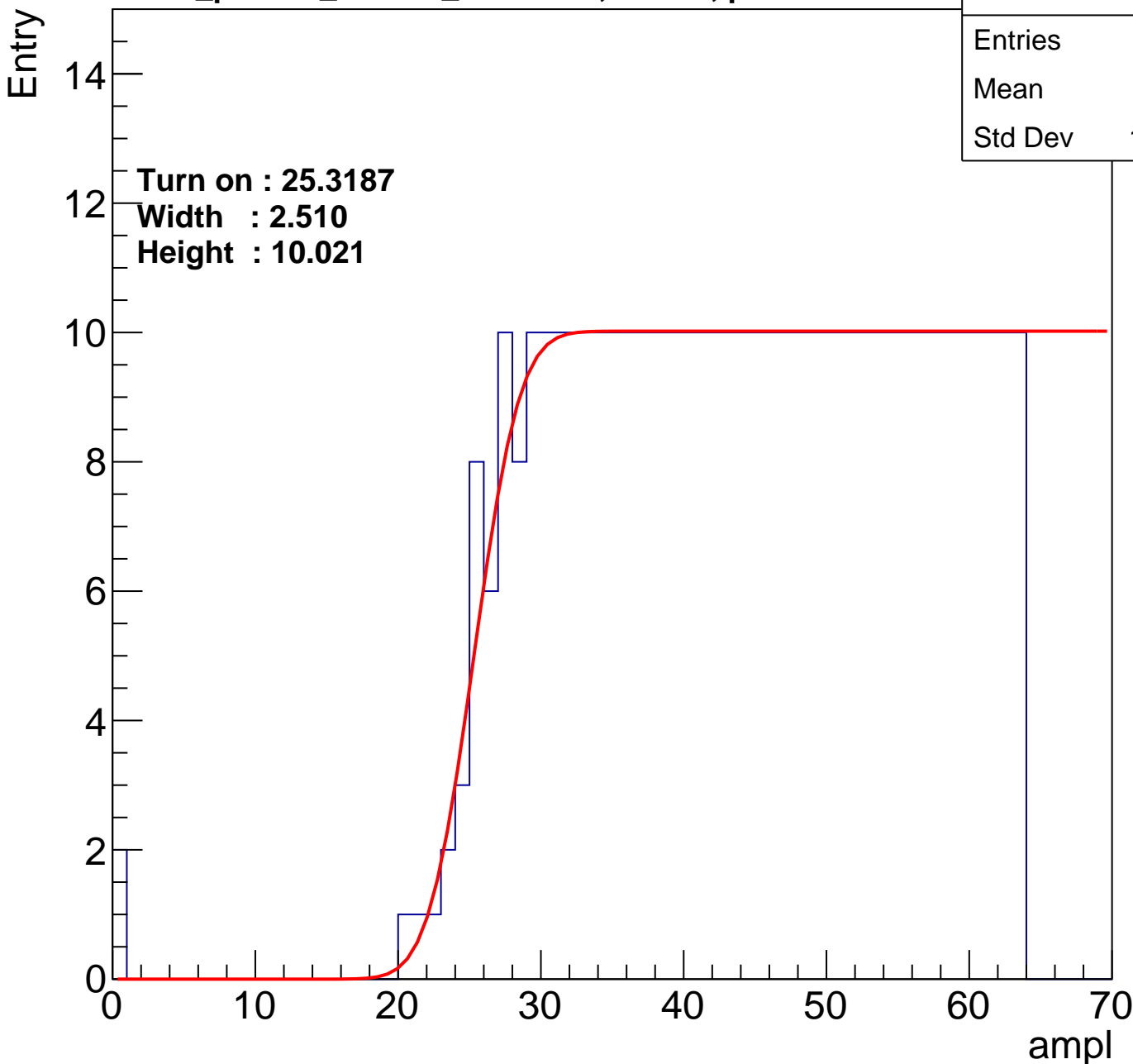
calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.3187

Width : 2.510

Height : 10.021



B1L002S, U26-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	354
Mean	45.47
Std Dev	11.07

Turn on : 29.2321

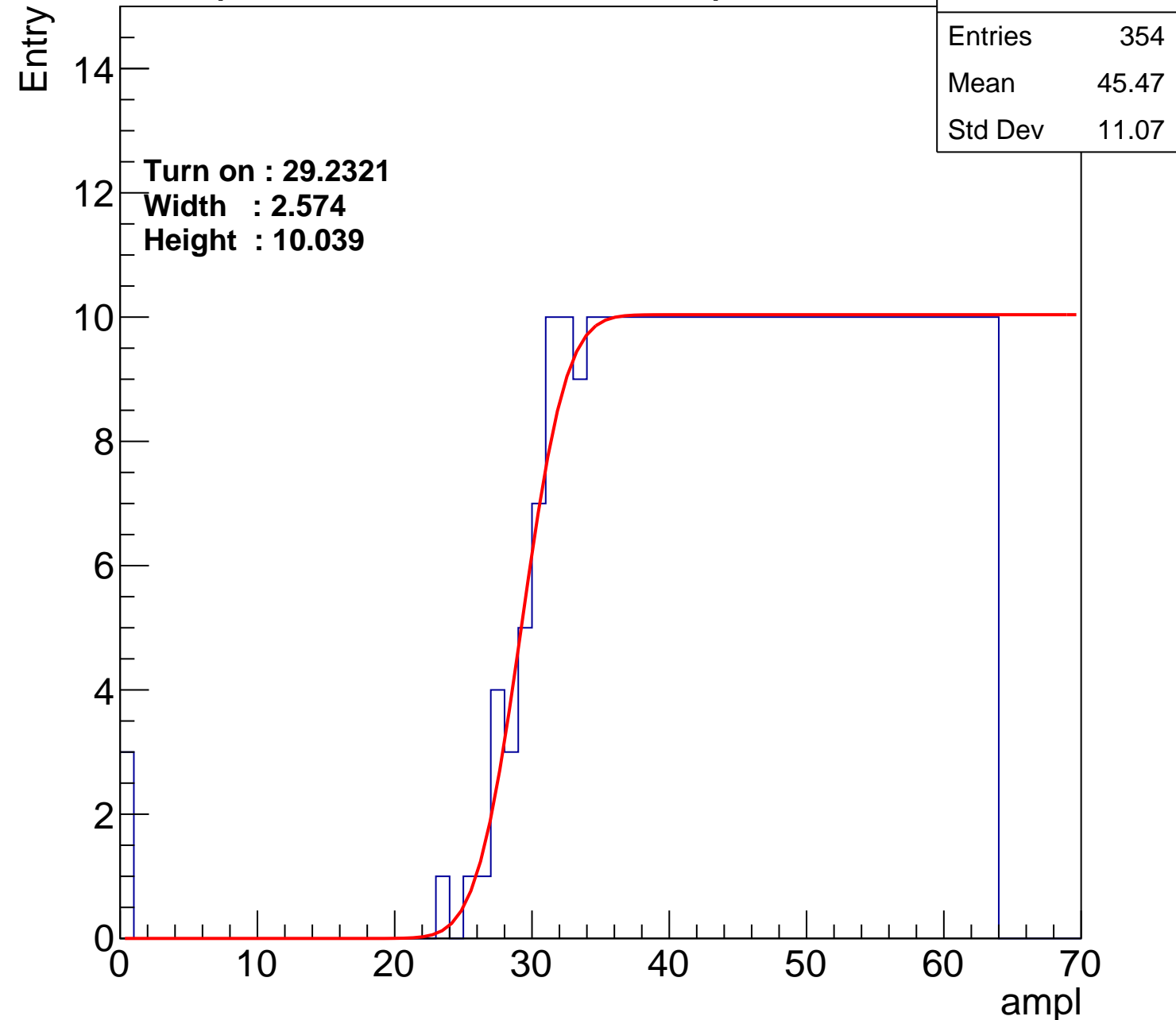
Width : 2.574

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.27
Std Dev	11.88

Turn on : 23.8998

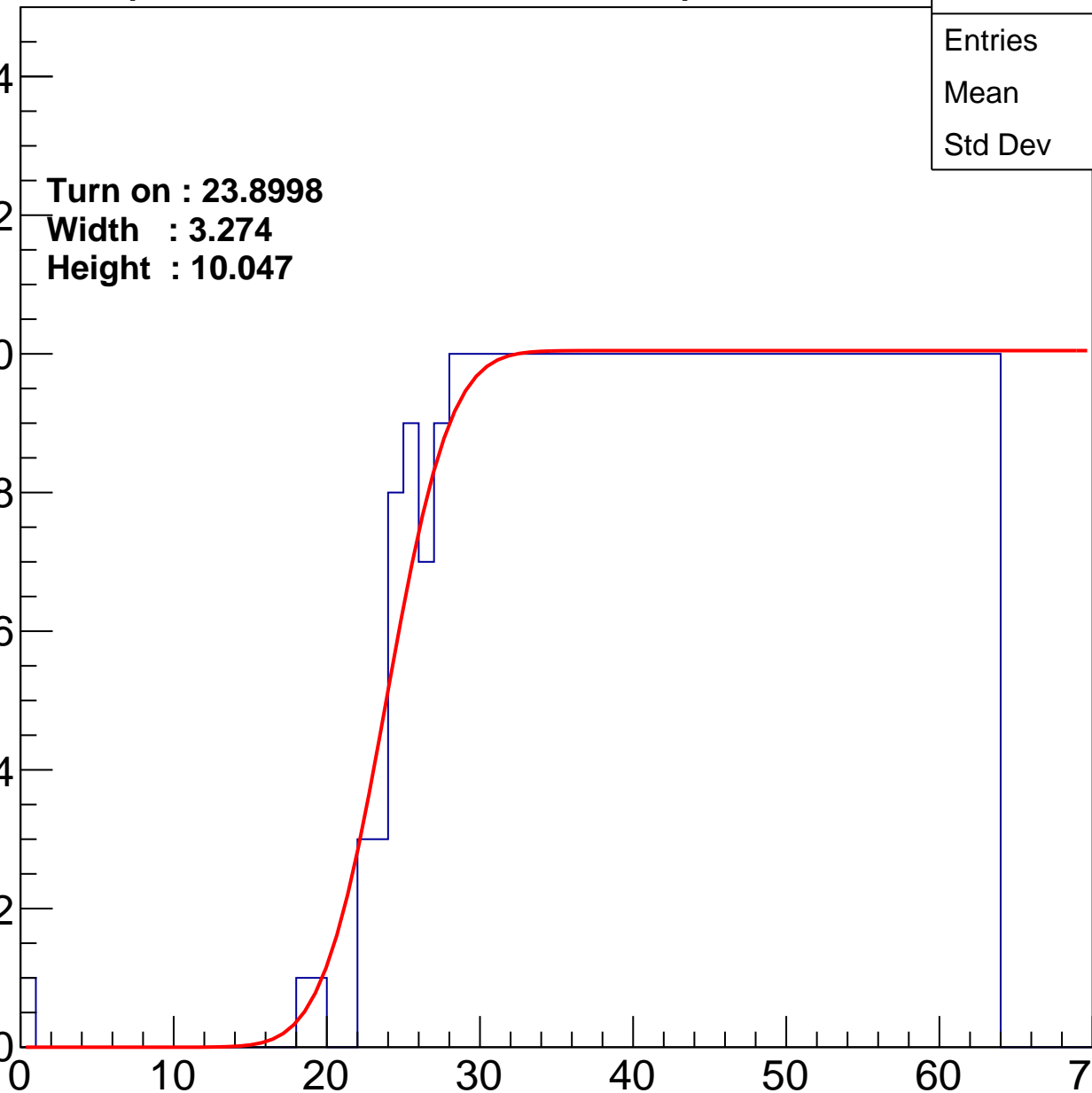
Width : 3.274

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.88
Std Dev	11.82

Turn on : 26.1037

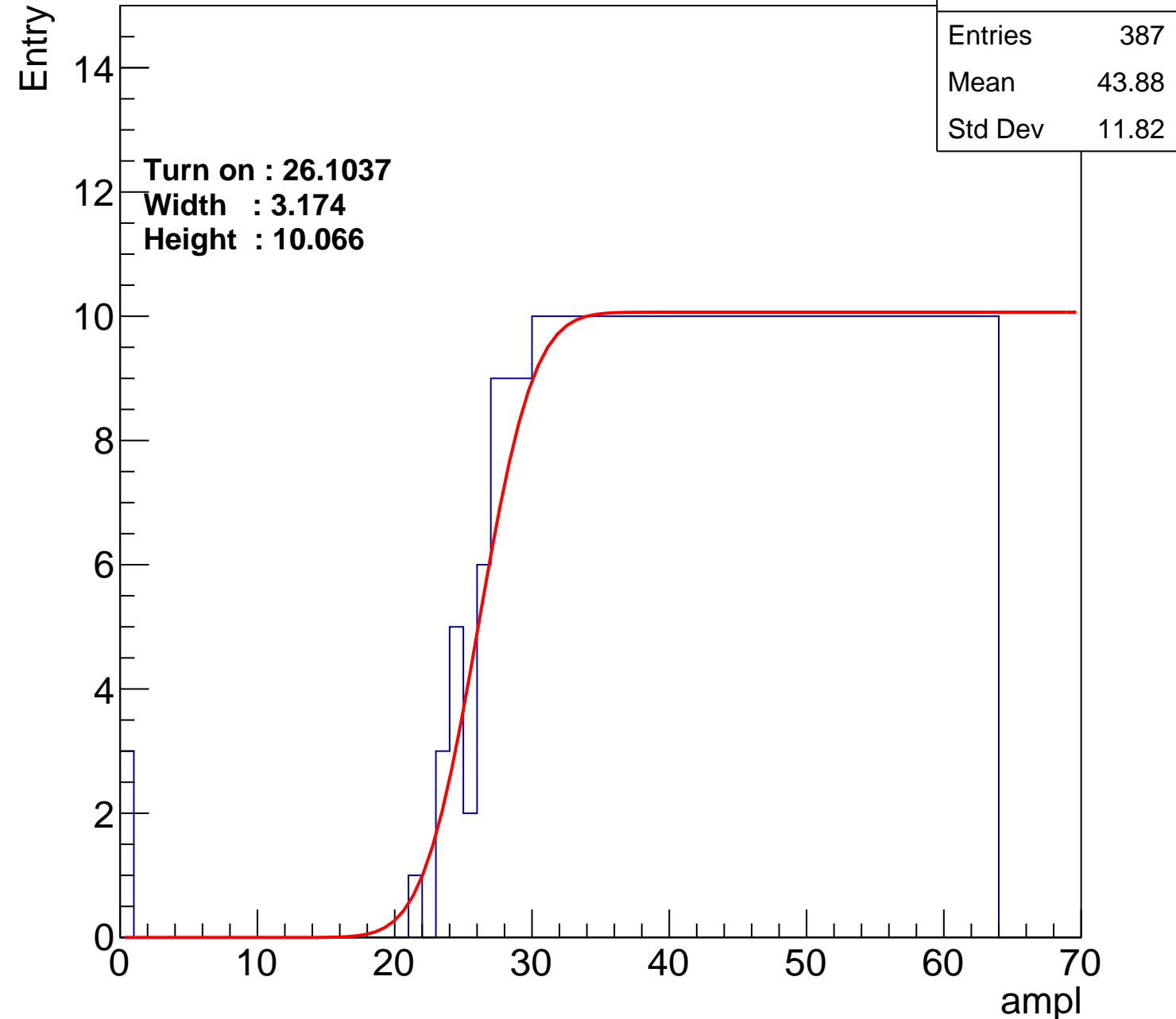
Width : 3.174

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.2
Std Dev	11.39

Turn on : 26.4241

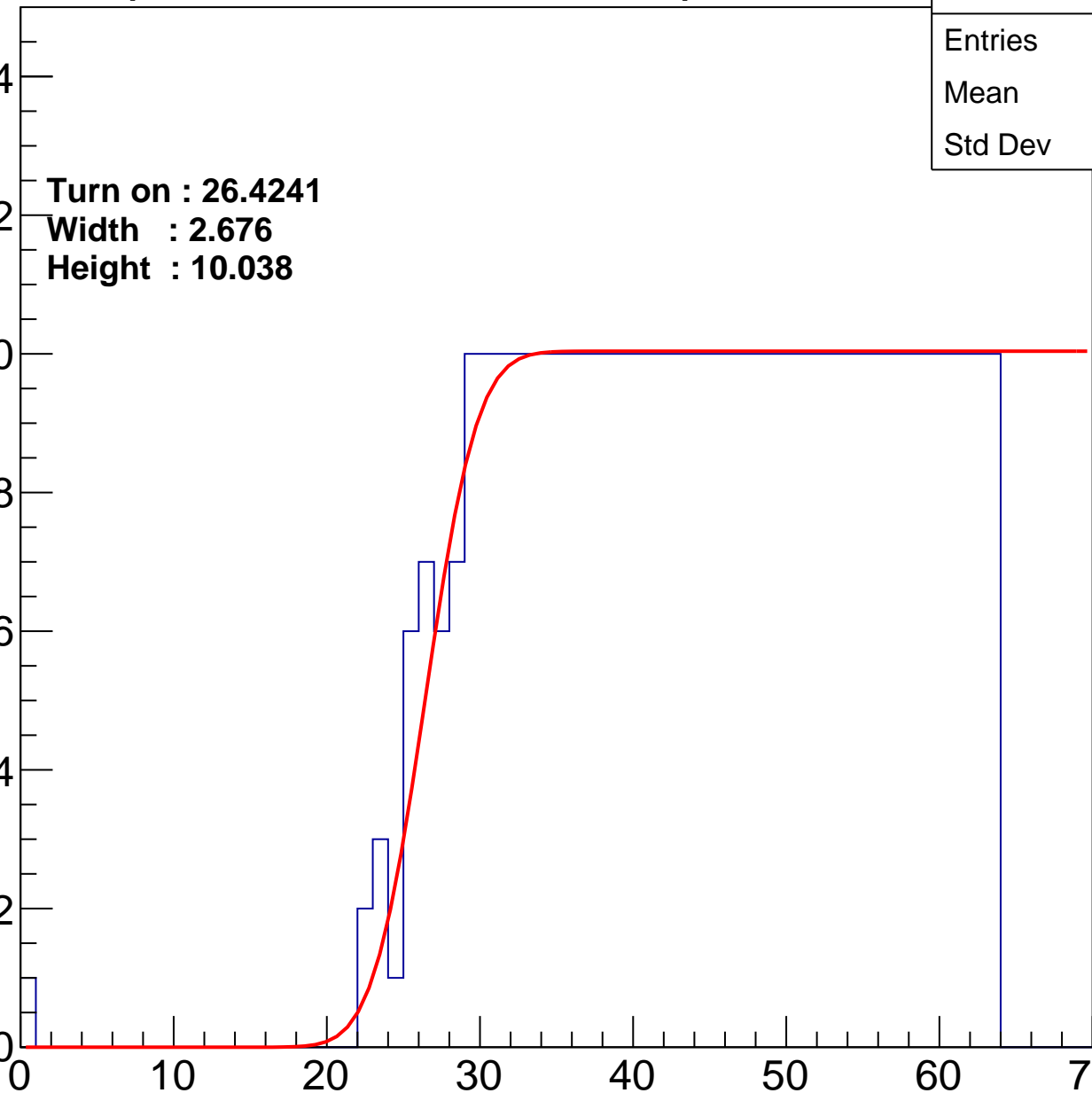
Width : 2.676

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.38
Std Dev	12.09

Turn on : 24.5811

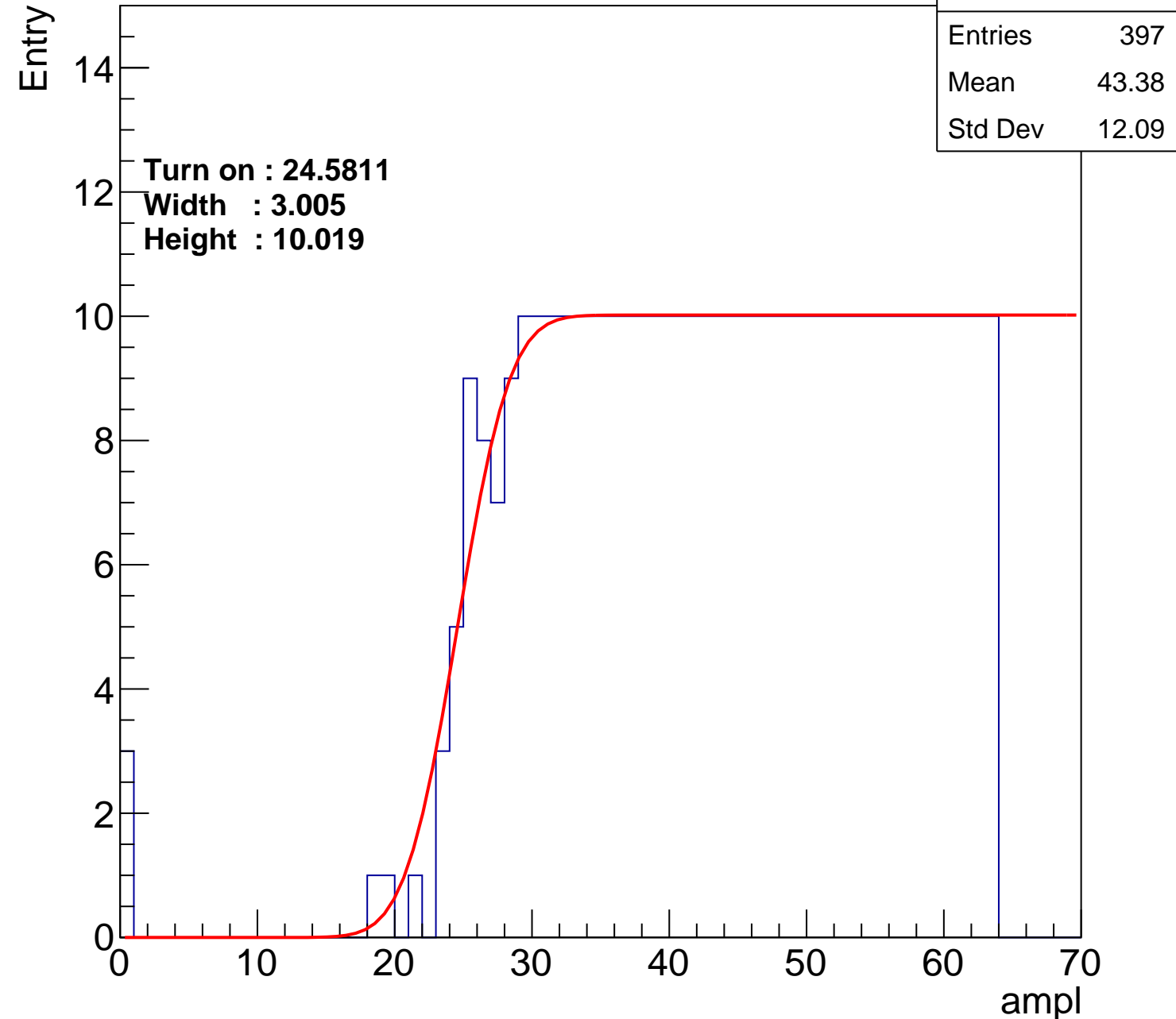
Width : 3.005

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.9
Std Dev	11.49

Turn on : 25.2825

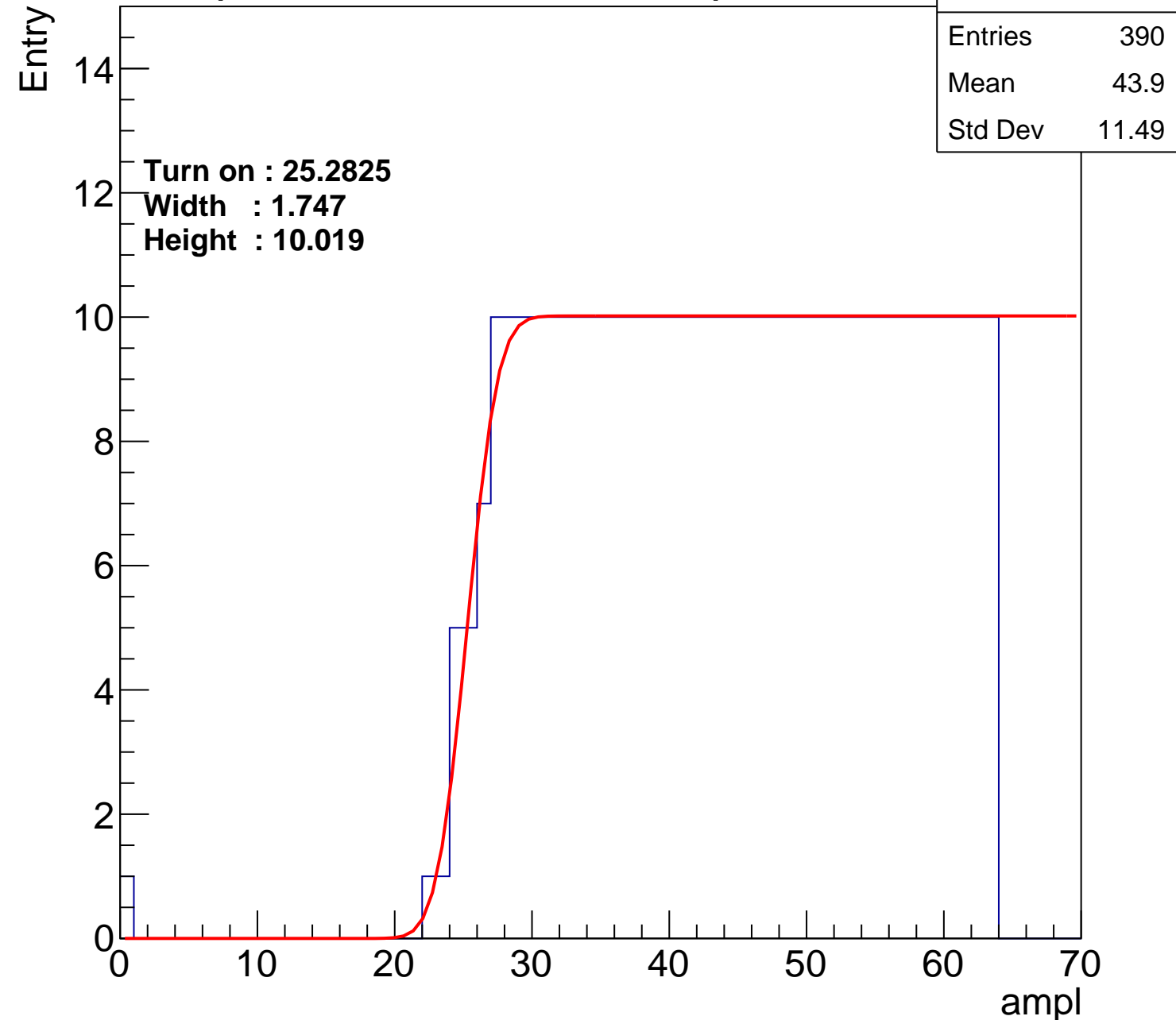
Width : 1.747

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch47

calib_packv5_042523_0143.root, FC#10, port B3

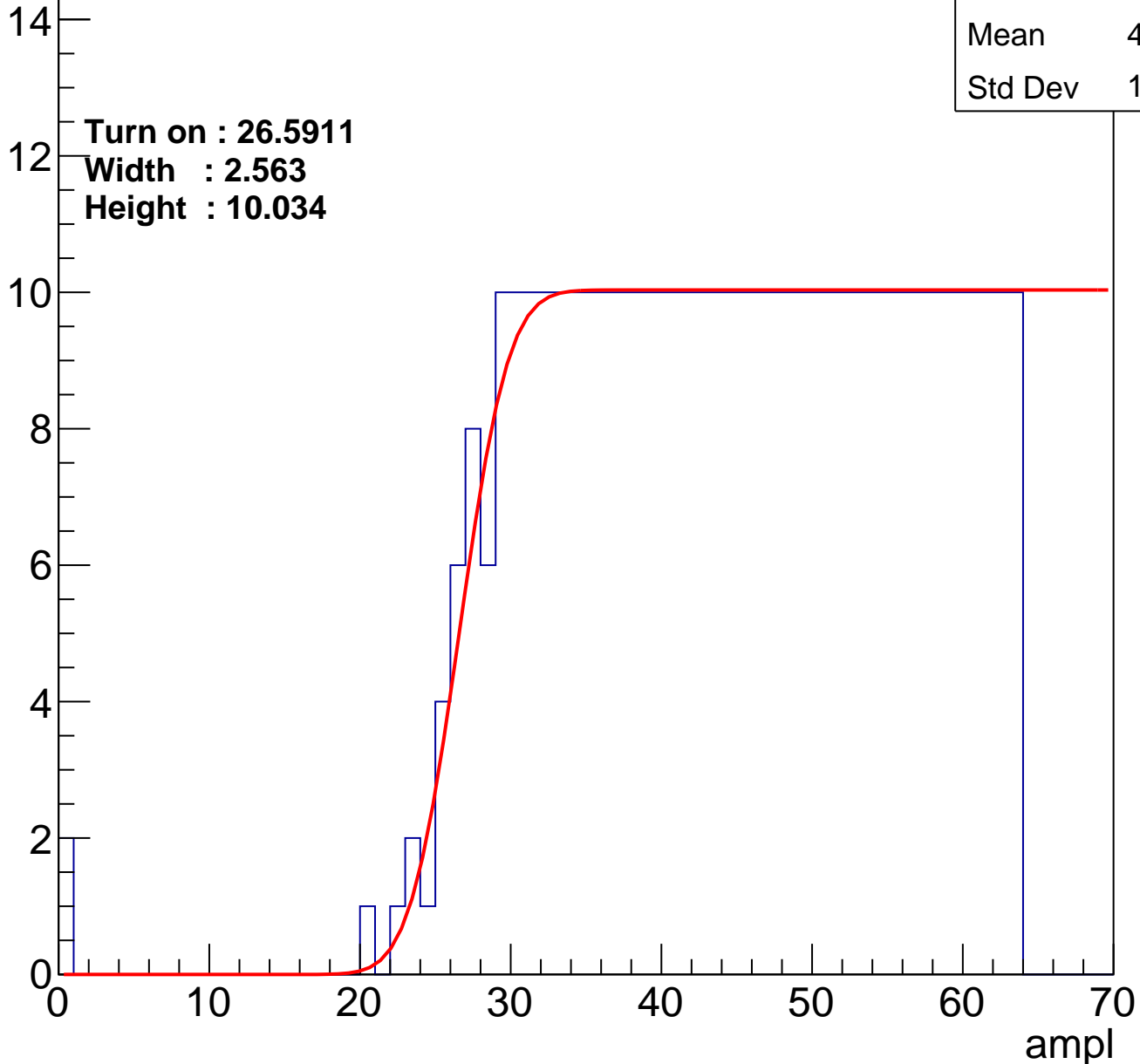
Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.5911

Width : 2.563

Height : 10.034

Entry



B1L002S, U26-ch48

calib_packv5_042523_0143.root, FC#10, port B3

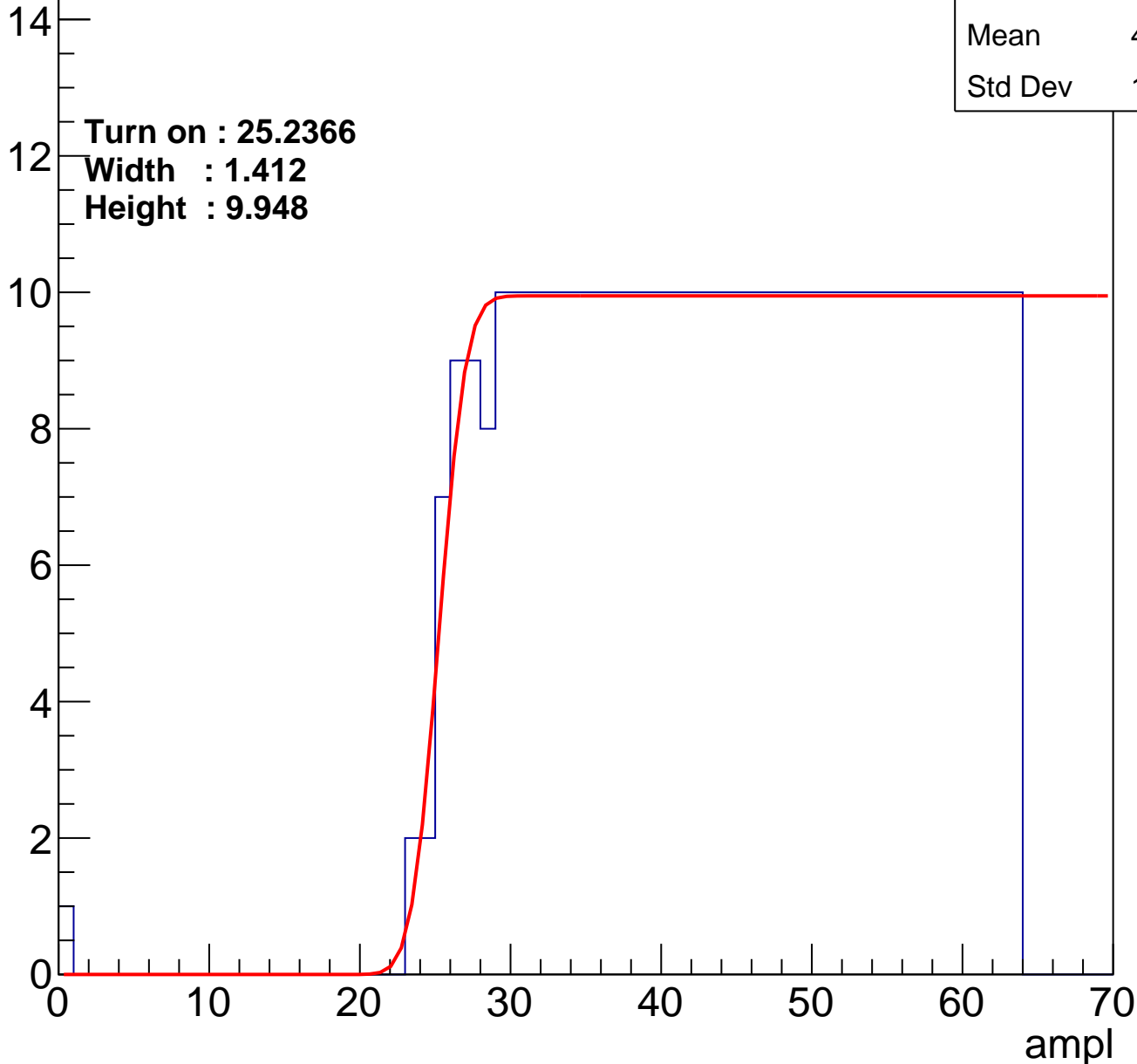
Entries	388
Mean	43.99
Std Dev	11.44

Turn on : 25.2366

Width : 1.412

Height : 9.948

Entry



B1L002S, U26-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.11
Std Dev	11.41

Turn on : 25.7896

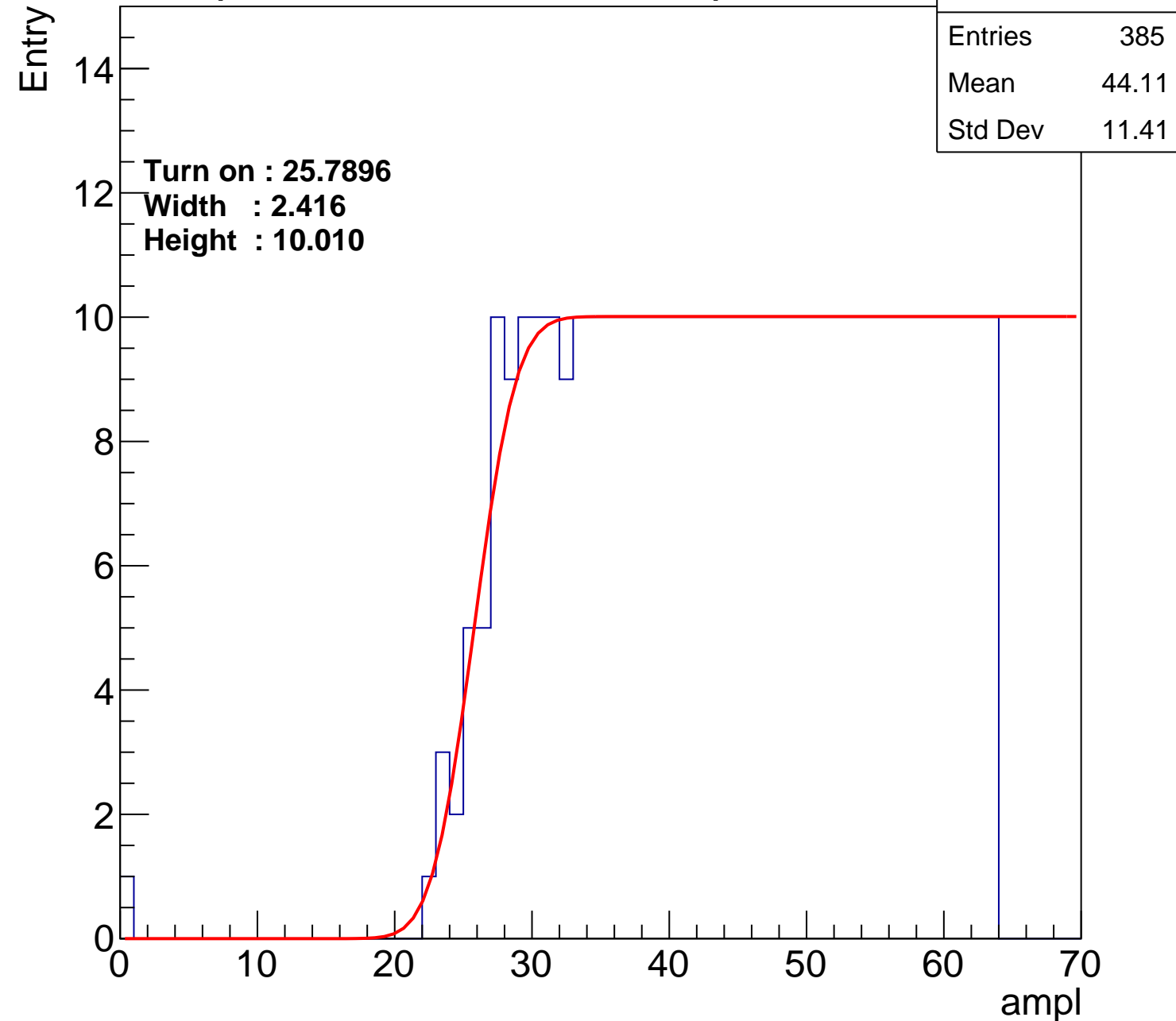
Width : 2.416

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch50

calib_packv5_042523_0143.root, FC#10, port B3

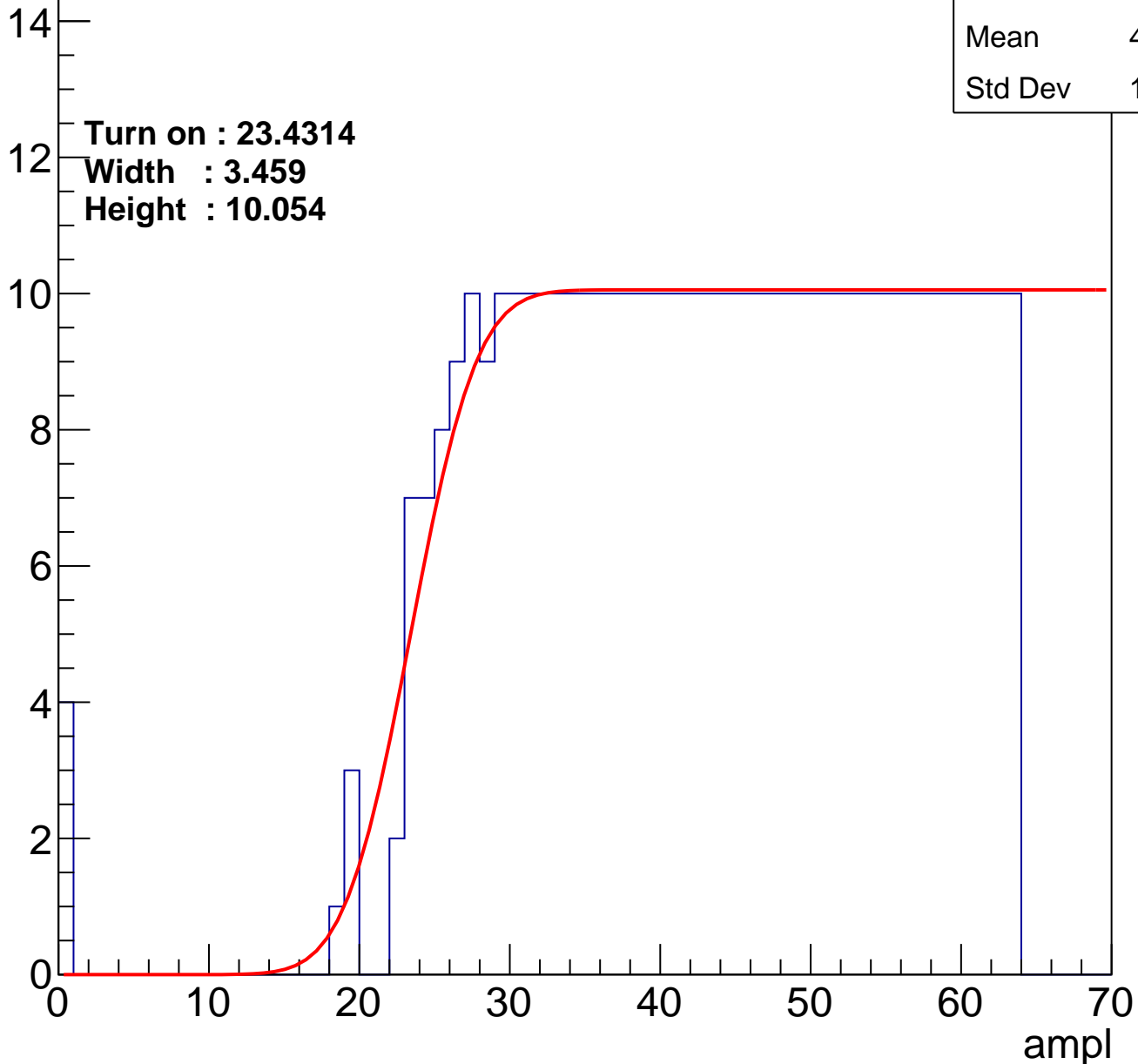
Entries	410
Mean	42.69
Std Dev	12.55

Turn on : 23.4314

Width : 3.459

Height : 10.054

Entry



B1L002S, U26-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.75
Std Dev	11.81

Turn on : 25.3378

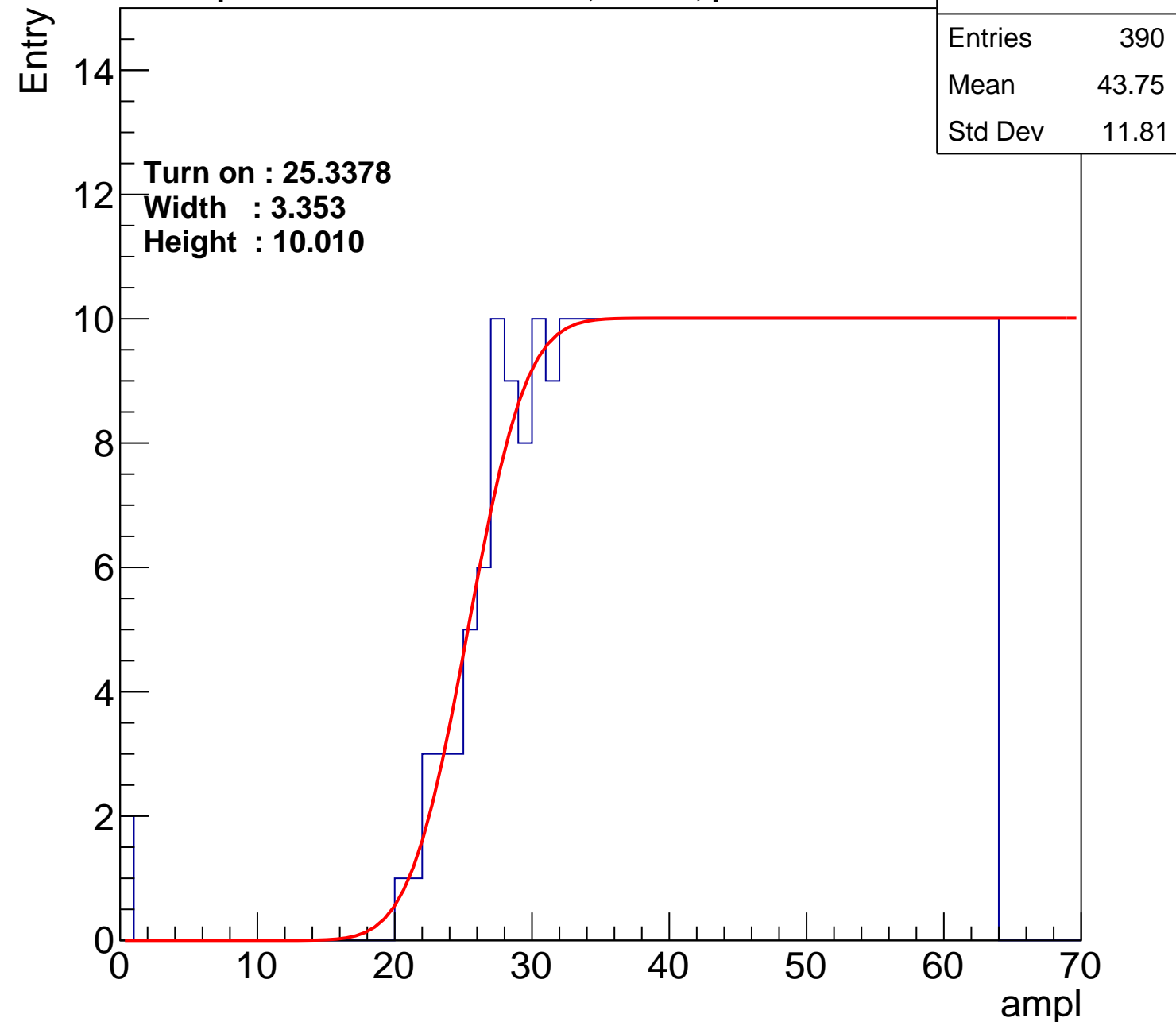
Width : 3.353

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.62
Std Dev	11.81

Turn on : 25.2395

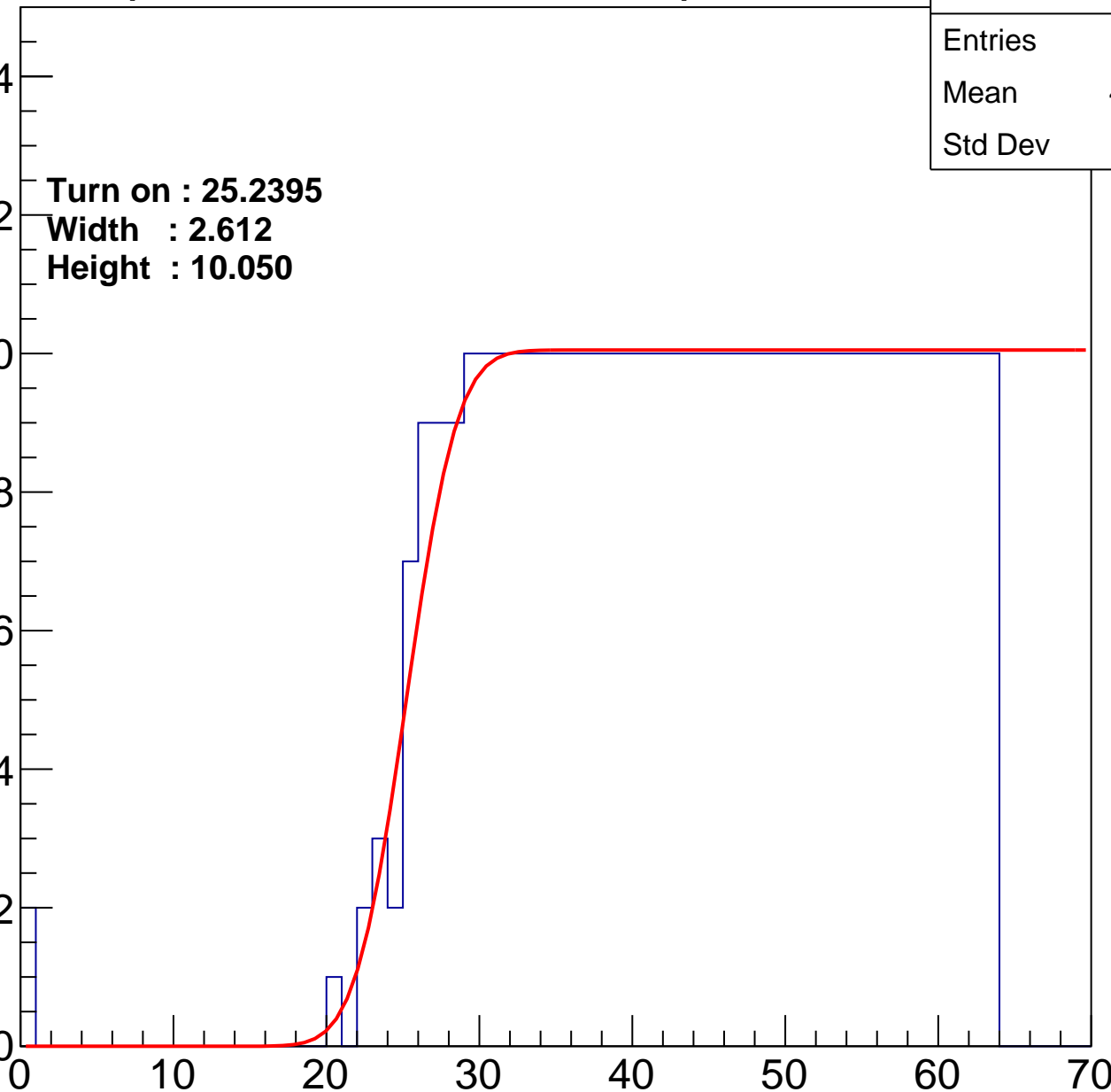
Width : 2.612

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch53

calib_packv5_042523_0143.root, FC#10, port B3

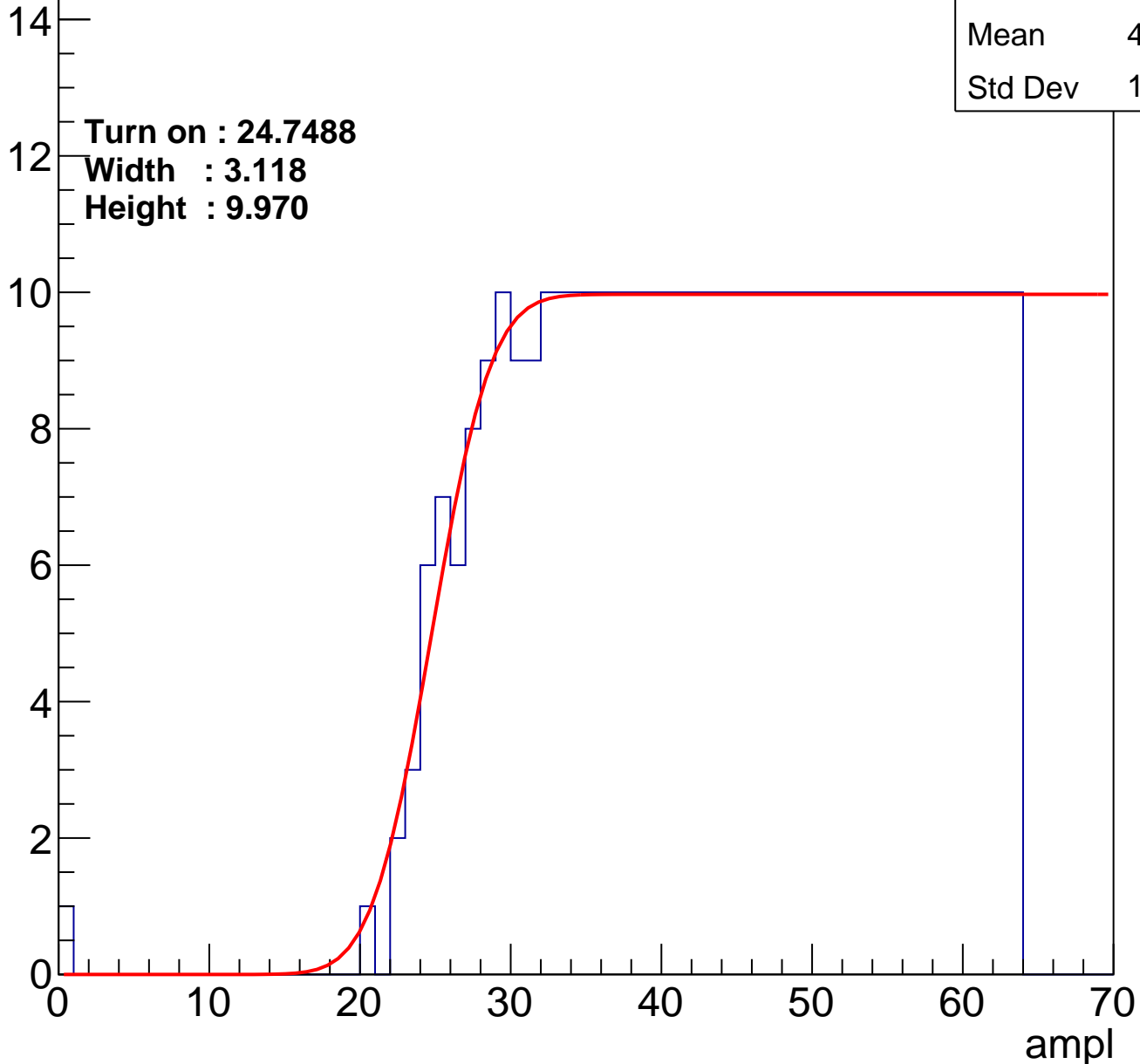
Entries	391
Mean	43.77
Std Dev	11.64

Turn on : 24.7488

Width : 3.118

Height : 9.970

Entry



B1L002S, U26-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.09
Std Dev	12.07

Turn on : 23.8269

Width : 2.679

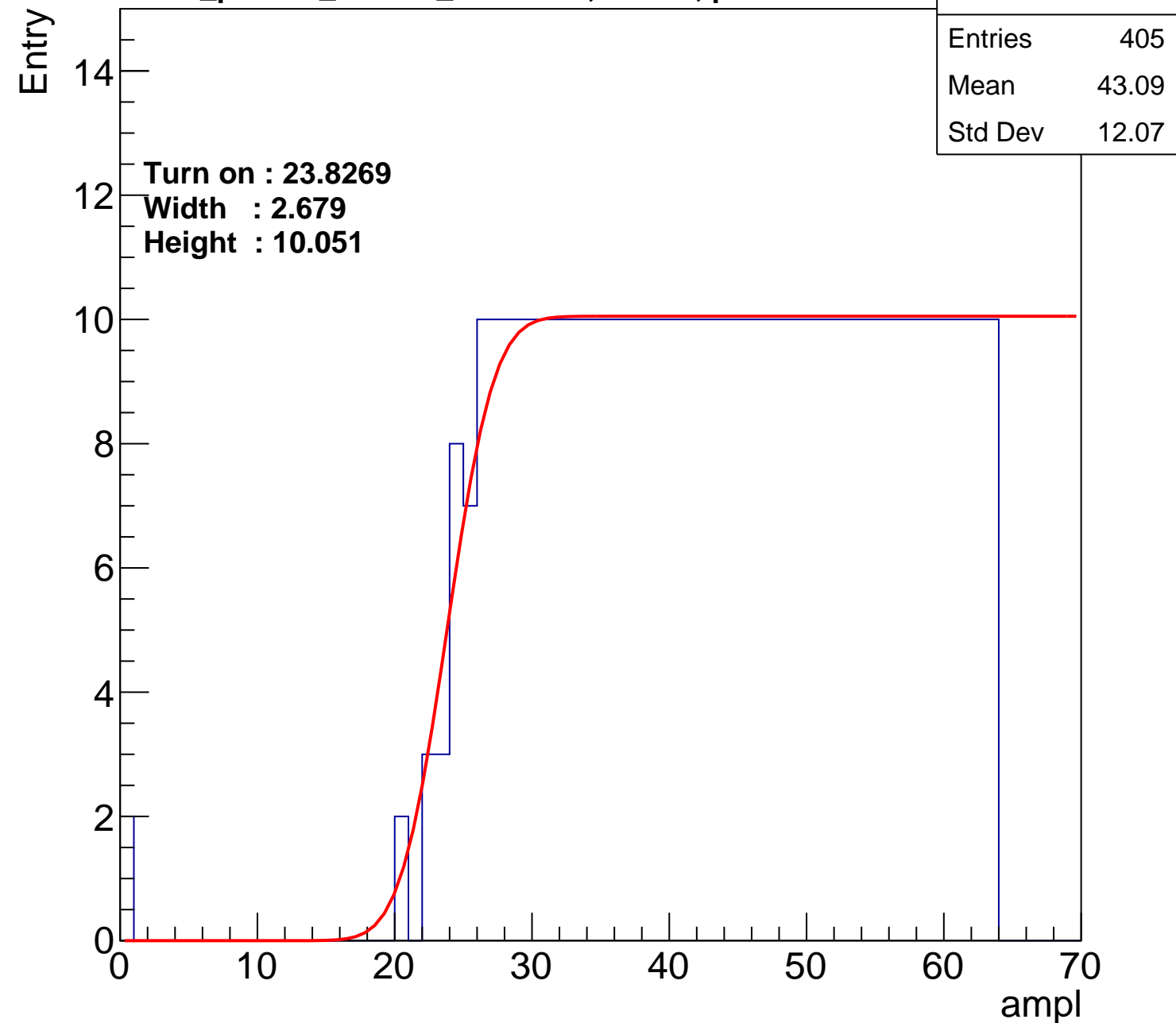
Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U26-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.31
Std Dev	11.62

Turn on : 26.7754

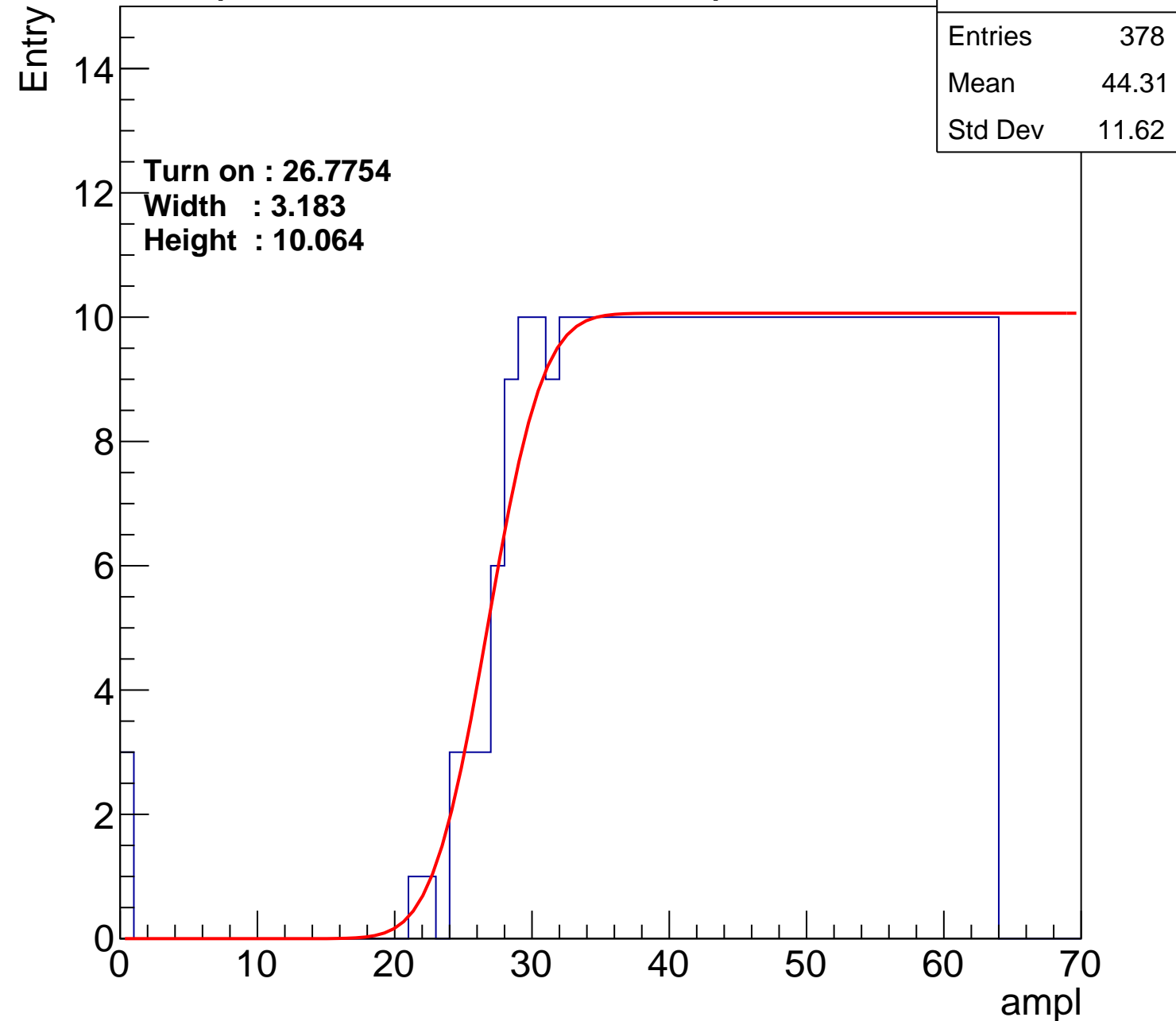
Width : 3.183

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	411
Mean	42.73
Std Dev	12.33

Turn on : 23.6182

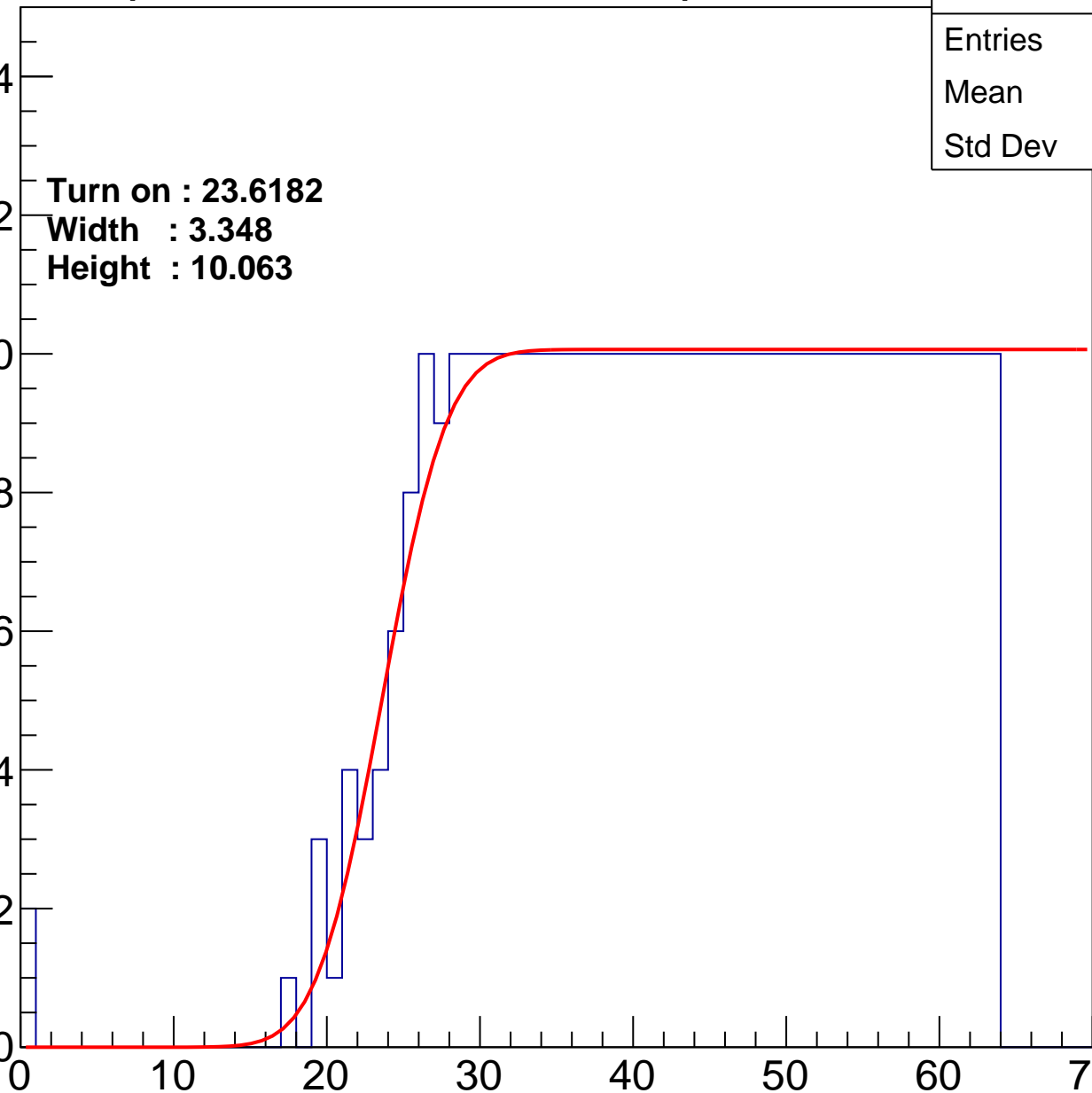
Width : 3.348

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.6332

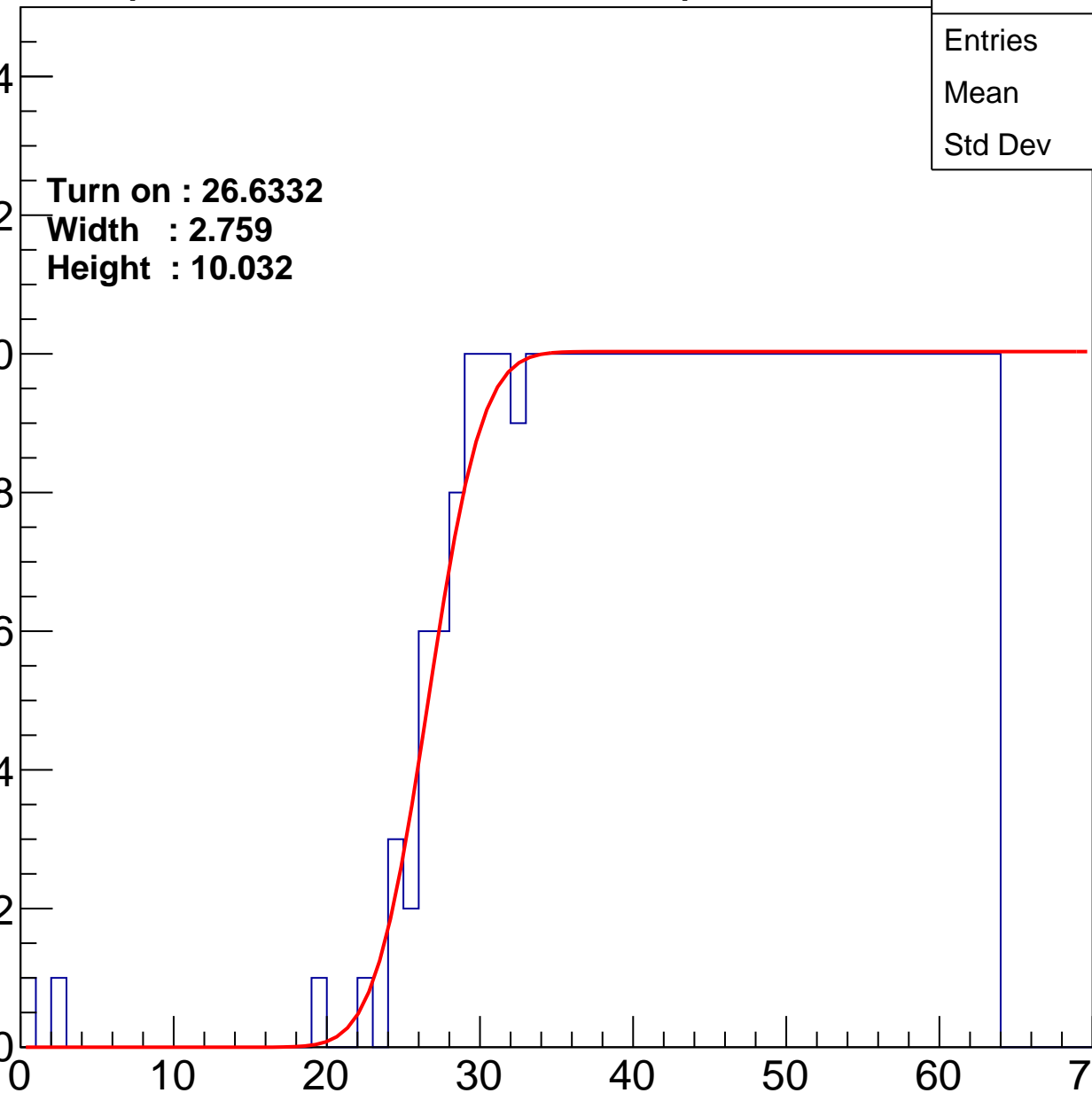
Width : 2.759

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.61
Std Dev	11.81

Turn on : 24.9432

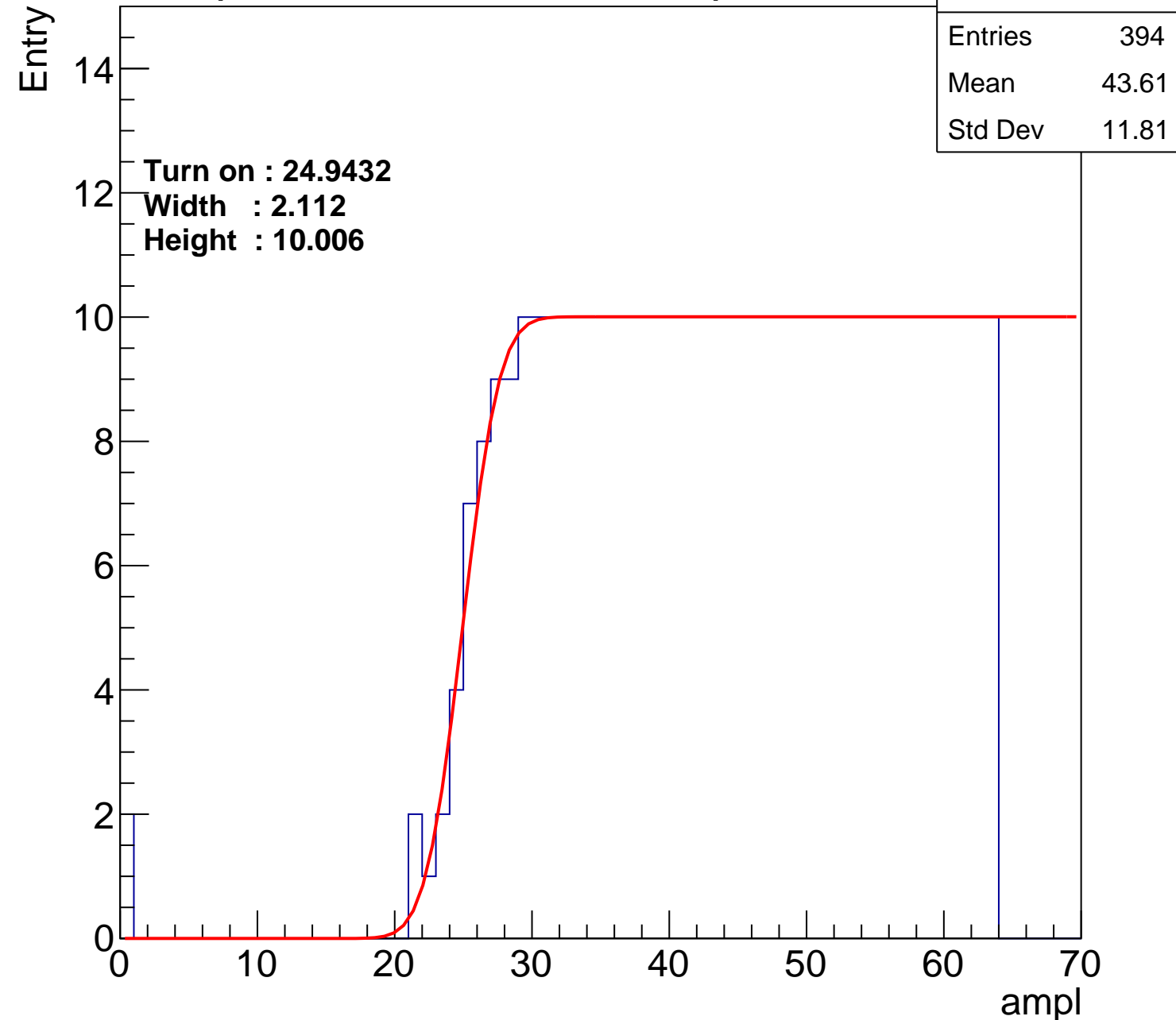
Width : 2.112

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.41
Std Dev	11.26

Turn on : 26.3028

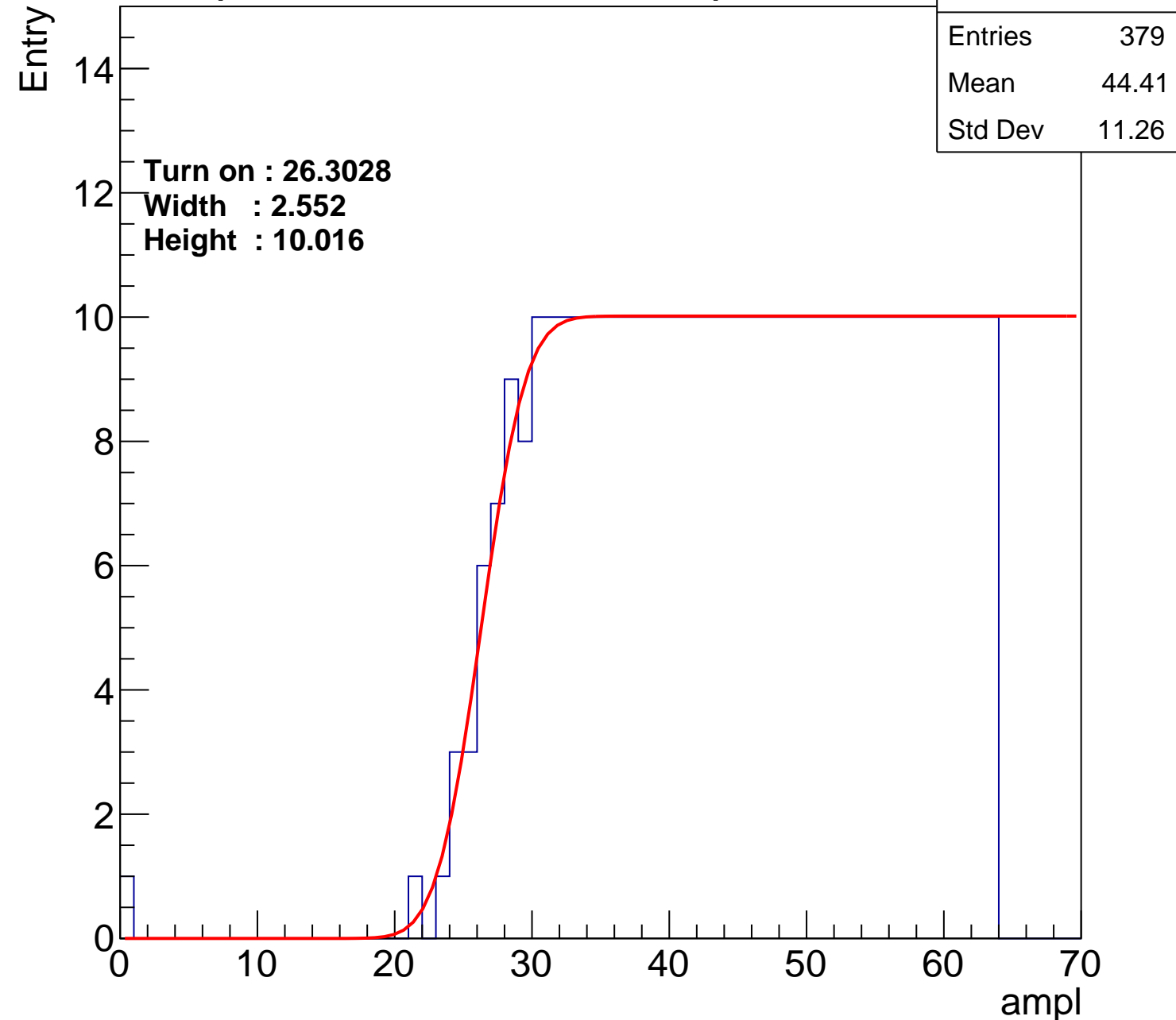
Width : 2.552

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch60

calib_packv5_042523_0143.root, FC#10, port B3

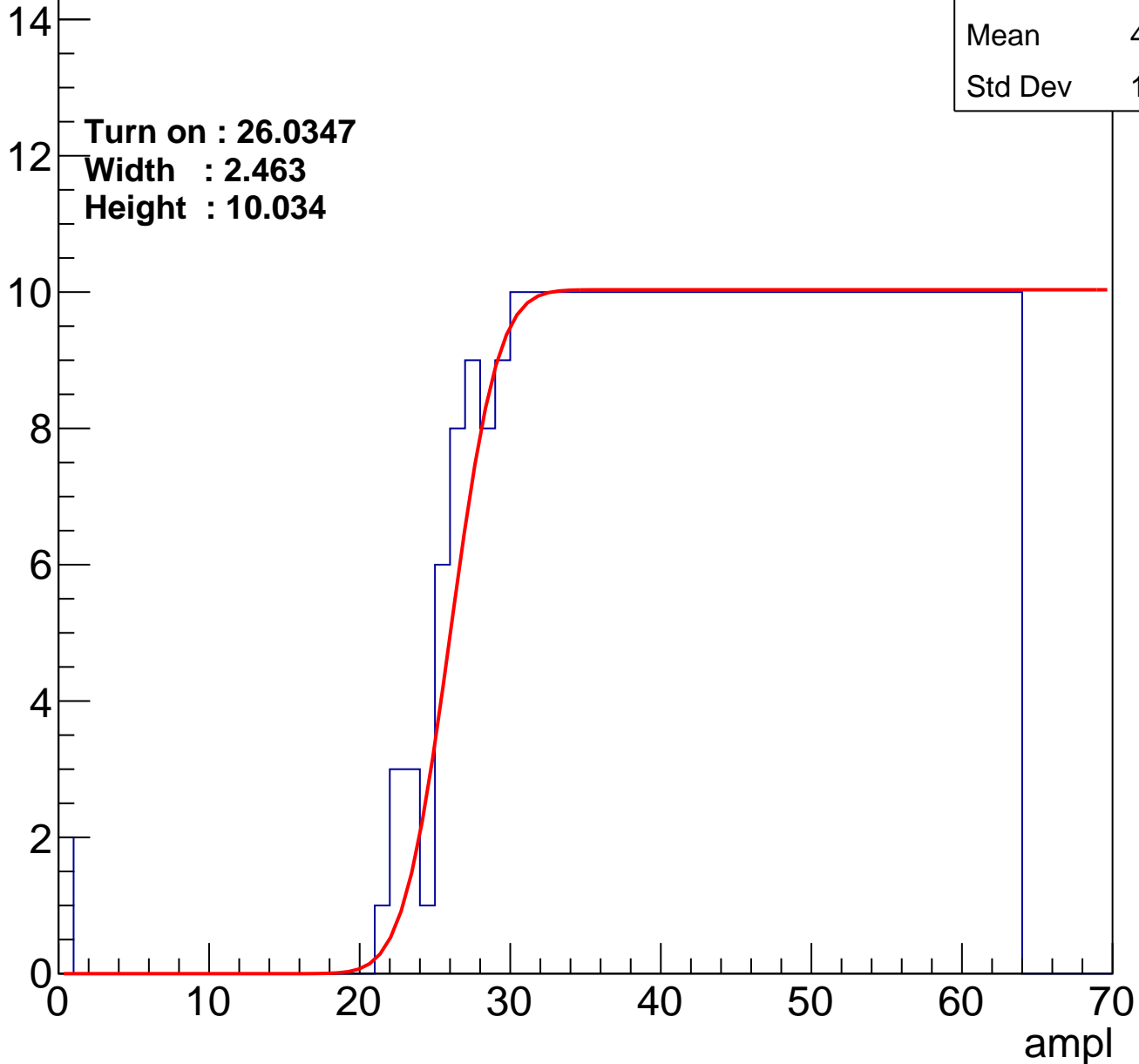
Entries	390
Mean	43.78
Std Dev	11.75

Turn on : 26.0347

Width : 2.463

Height : 10.034

Entry



B1L002S, U26-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.84
Std Dev	12.21

Turn on : 23.5152

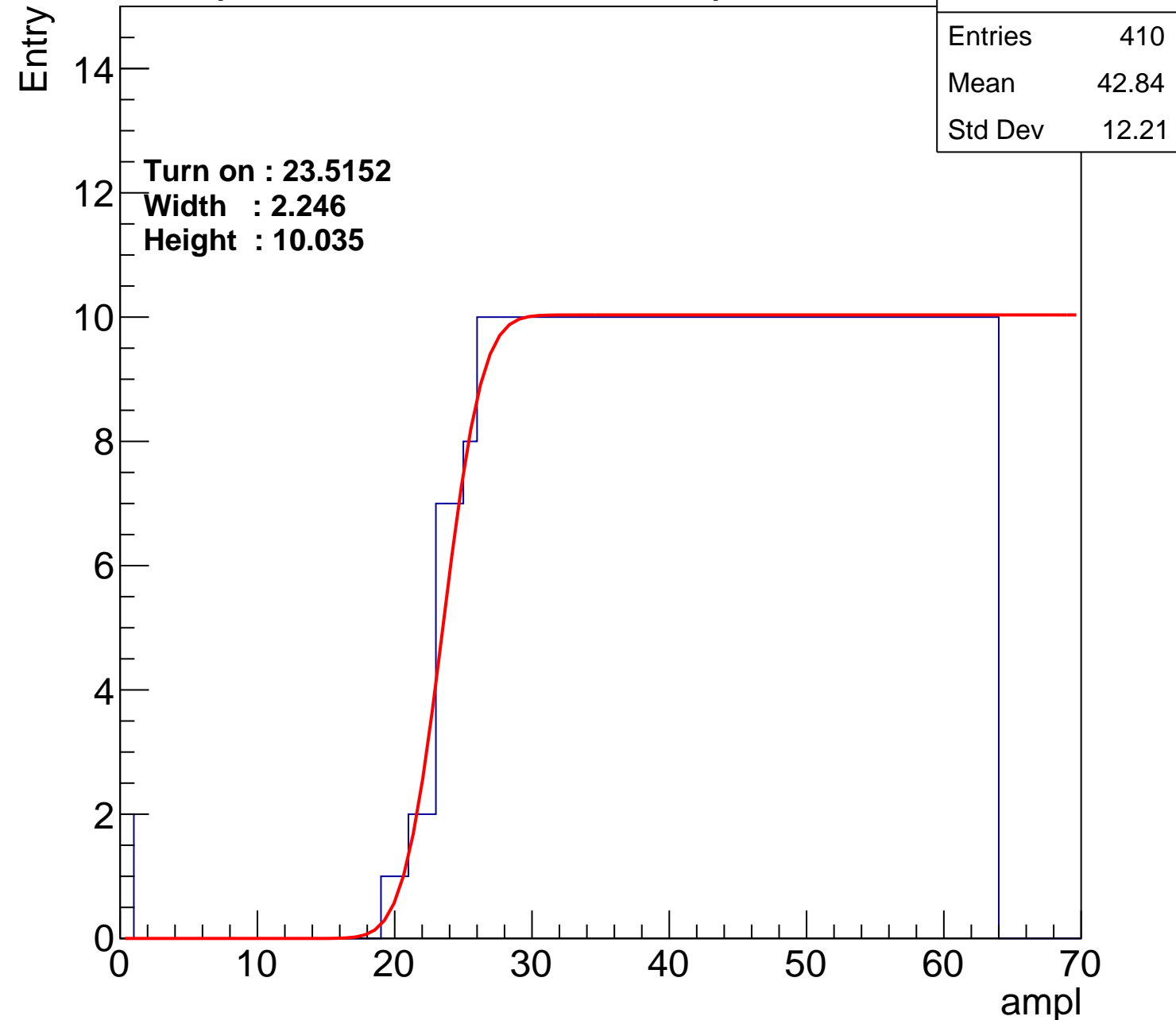
Width : 2.246

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch62

calib_packv5_042523_0143.root, FC#10, port B3

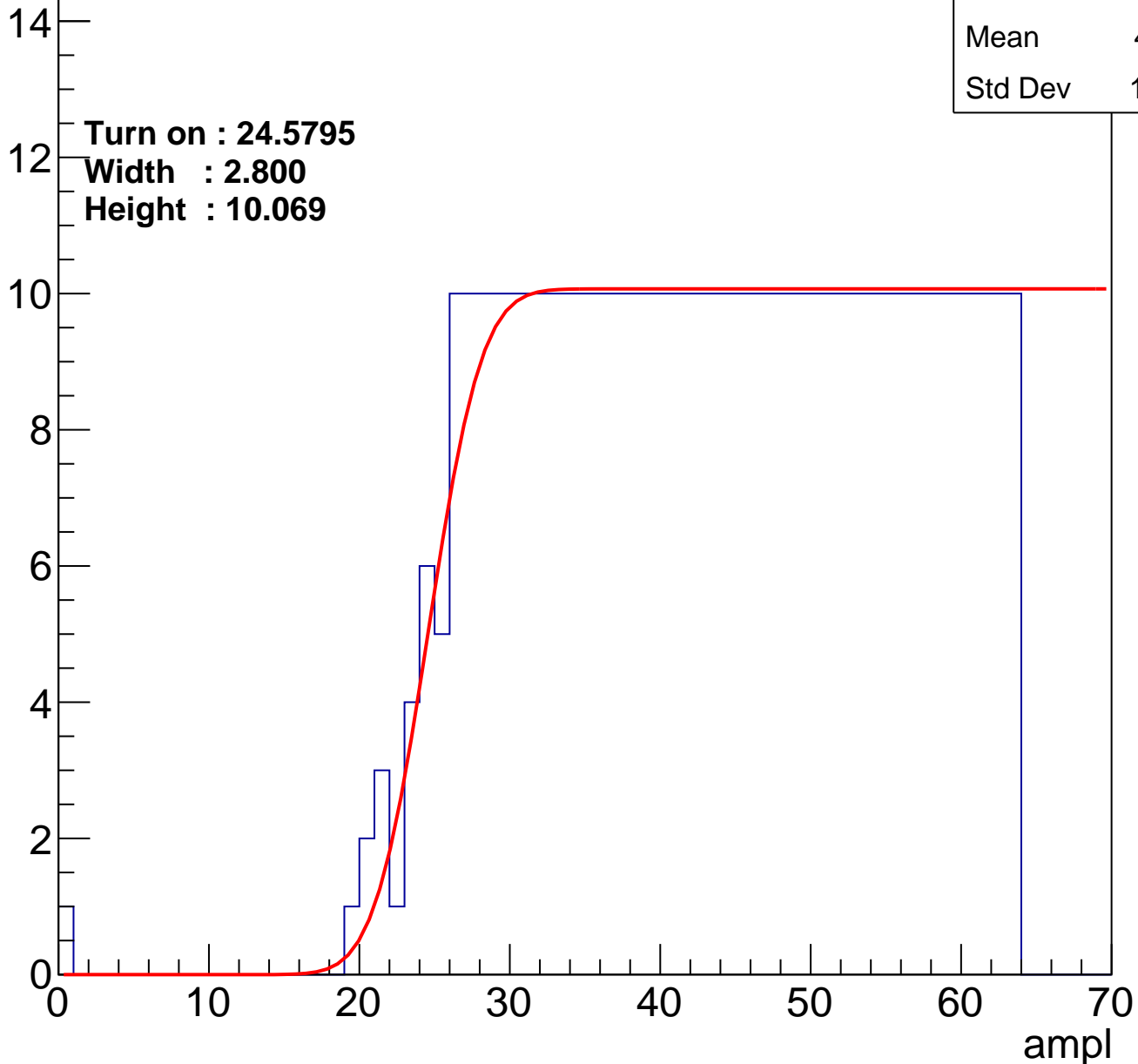
Entries	403
Mean	43.21
Std Dev	11.92

Turn on : 24.5795

Width : 2.800

Height : 10.069

Entry



B1L002S, U26-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.66
Std Dev	11.8

Turn on : 25.3316

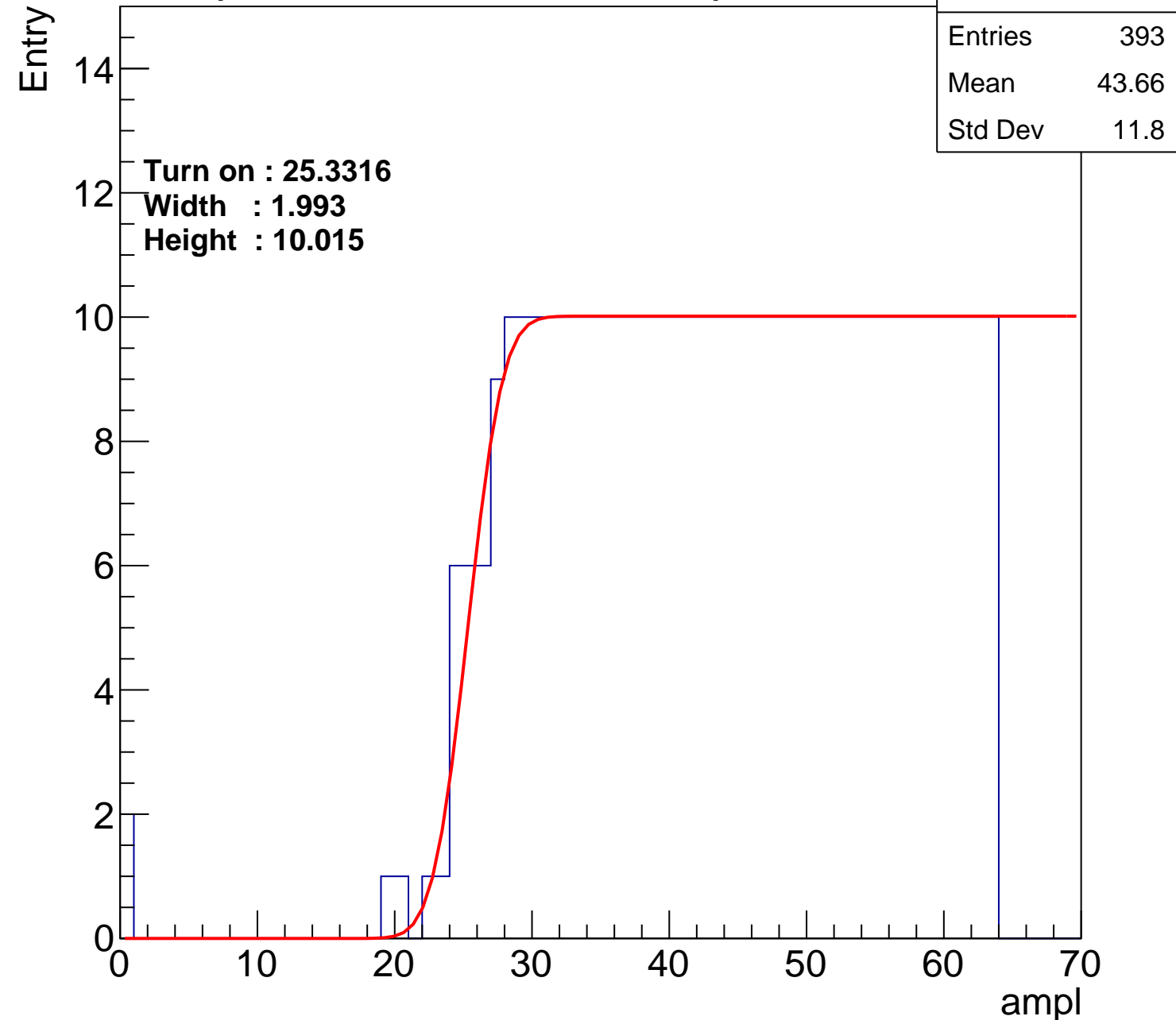
Width : 1.993

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch64

calib_packv5_042523_0143.root, FC#10, port B3

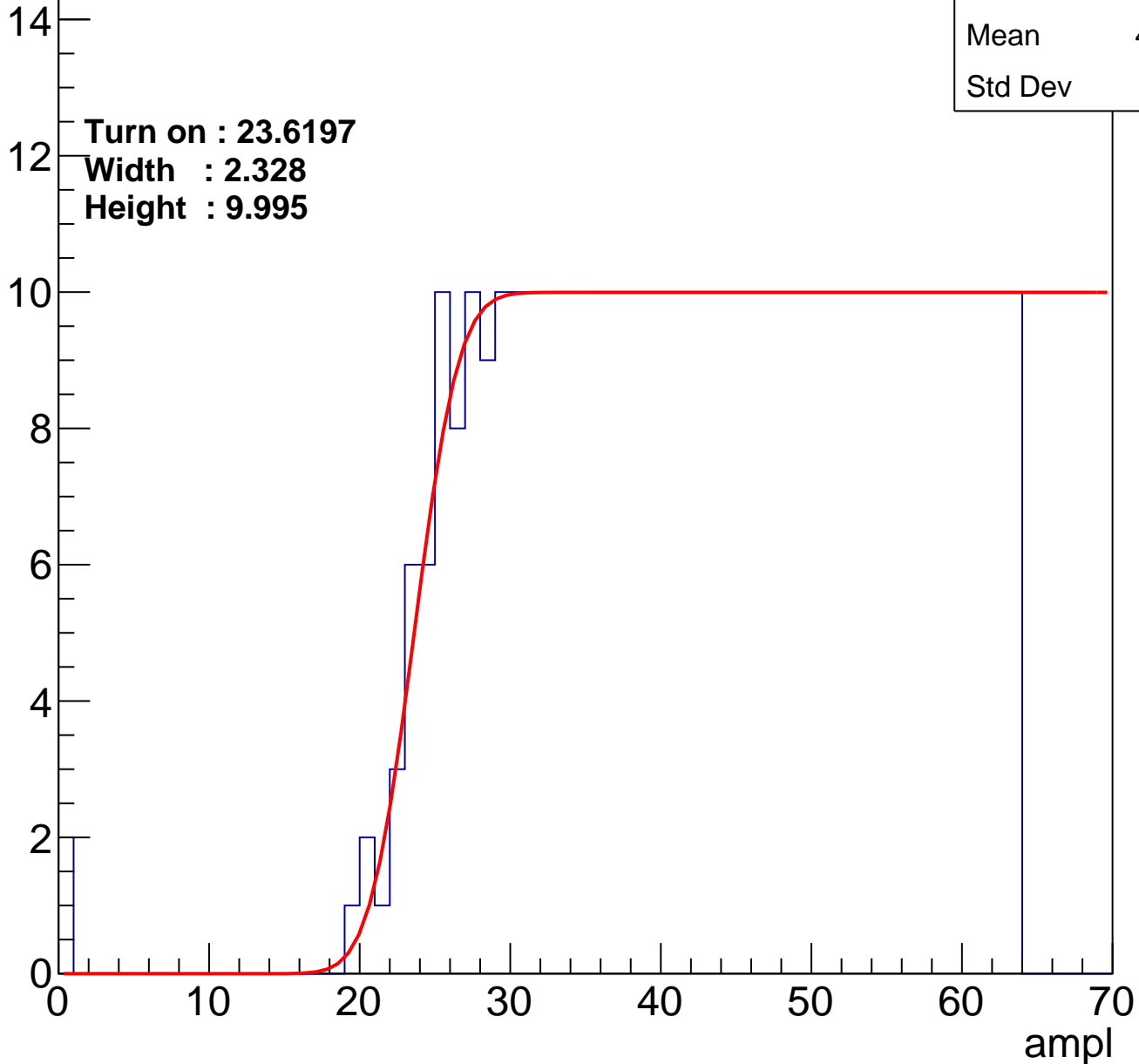
Entries	408
Mean	42.91
Std Dev	12.2

Turn on : 23.6197

Width : 2.328

Height : 9.995

Entry



B1L002S, U26-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.8
Std Dev	11.72

Turn on : 25.3072

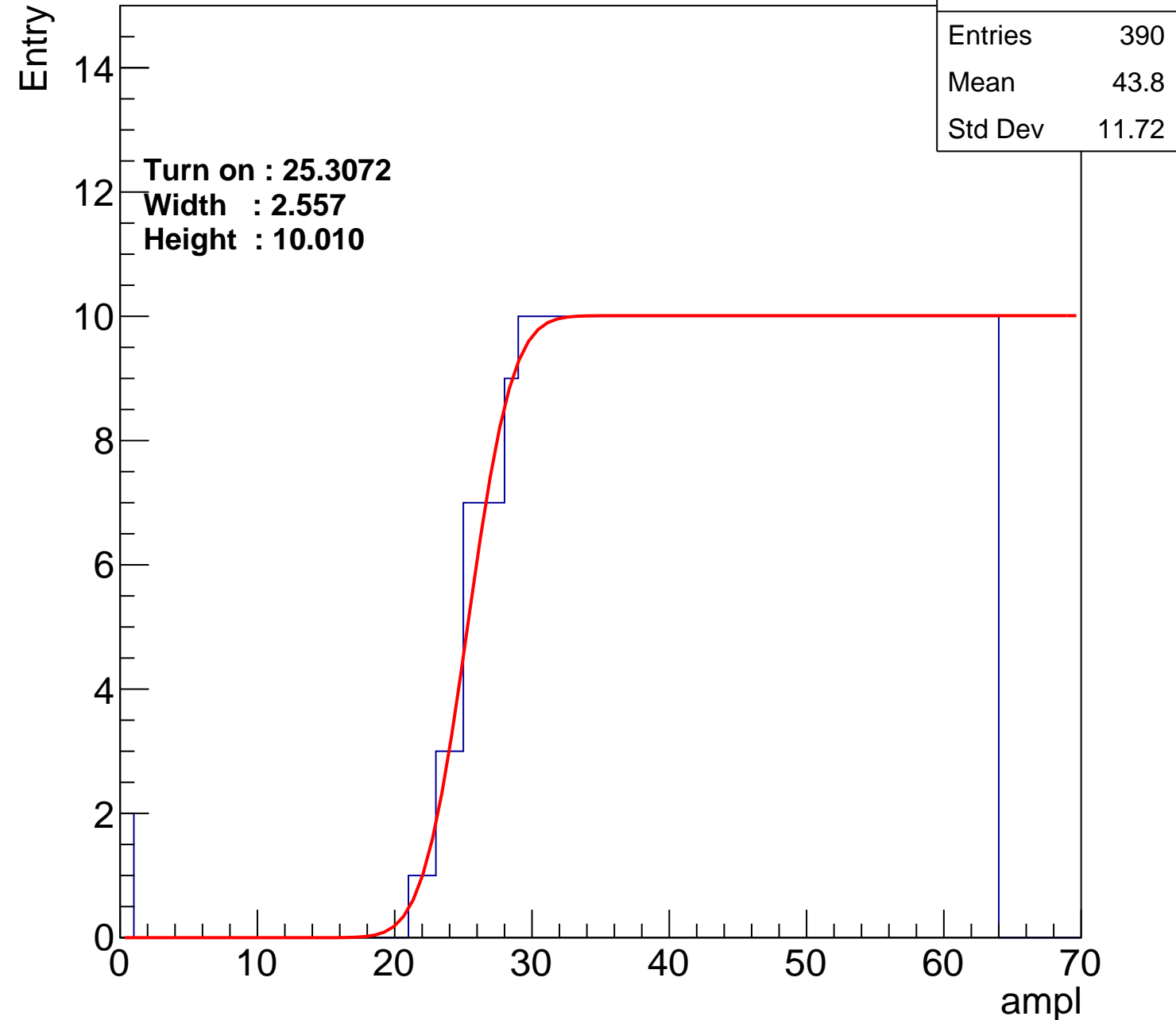
Width : 2.557

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch66

calib_packv5_042523_0143.root, FC#10, port B3

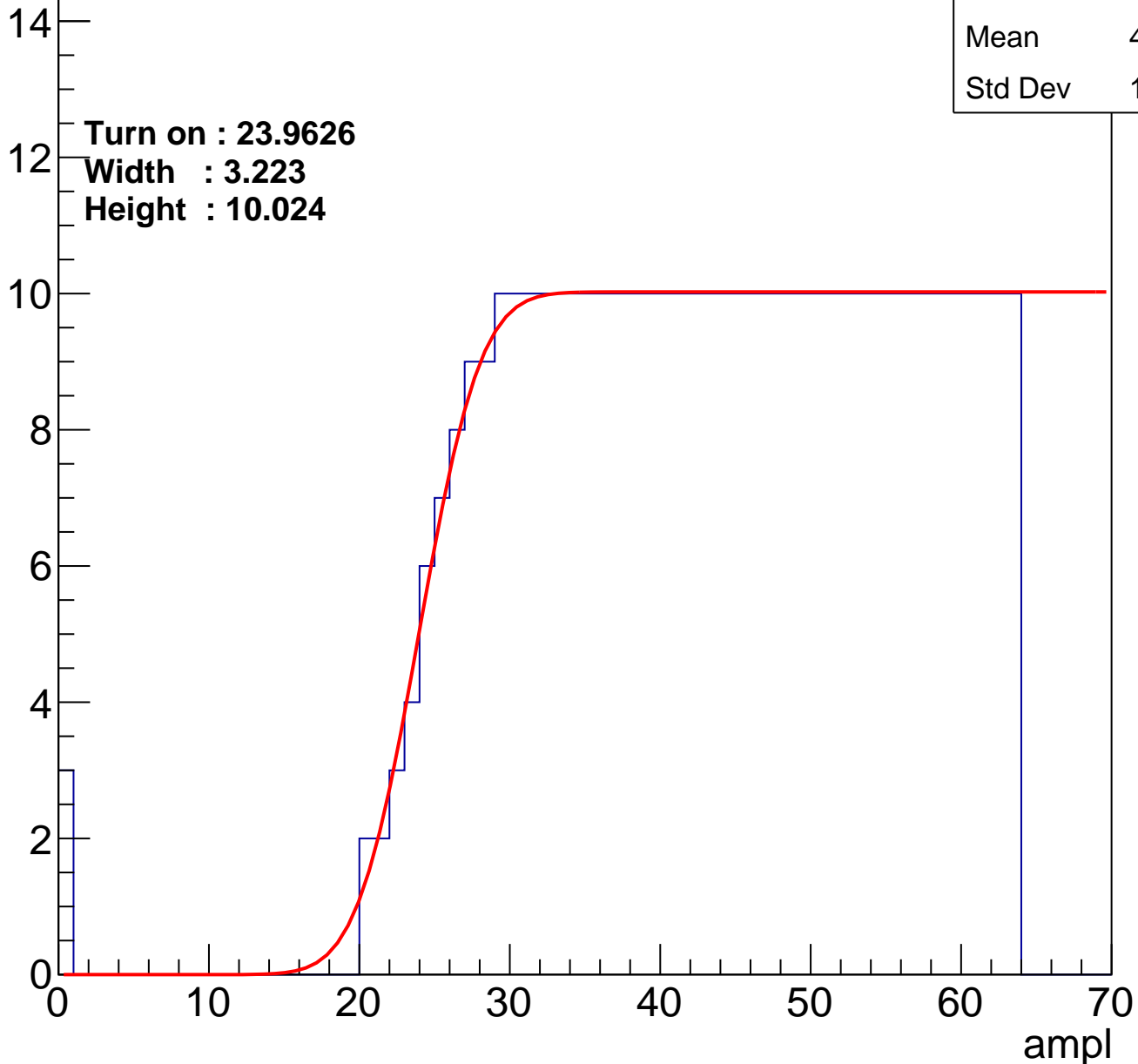
Entries	403
Mean	43.08
Std Dev	12.24

Turn on : 23.9626

Width : 3.223

Height : 10.024

Entry



B1L002S, U26-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.51
Std Dev	11.88

Turn on : 24.8128

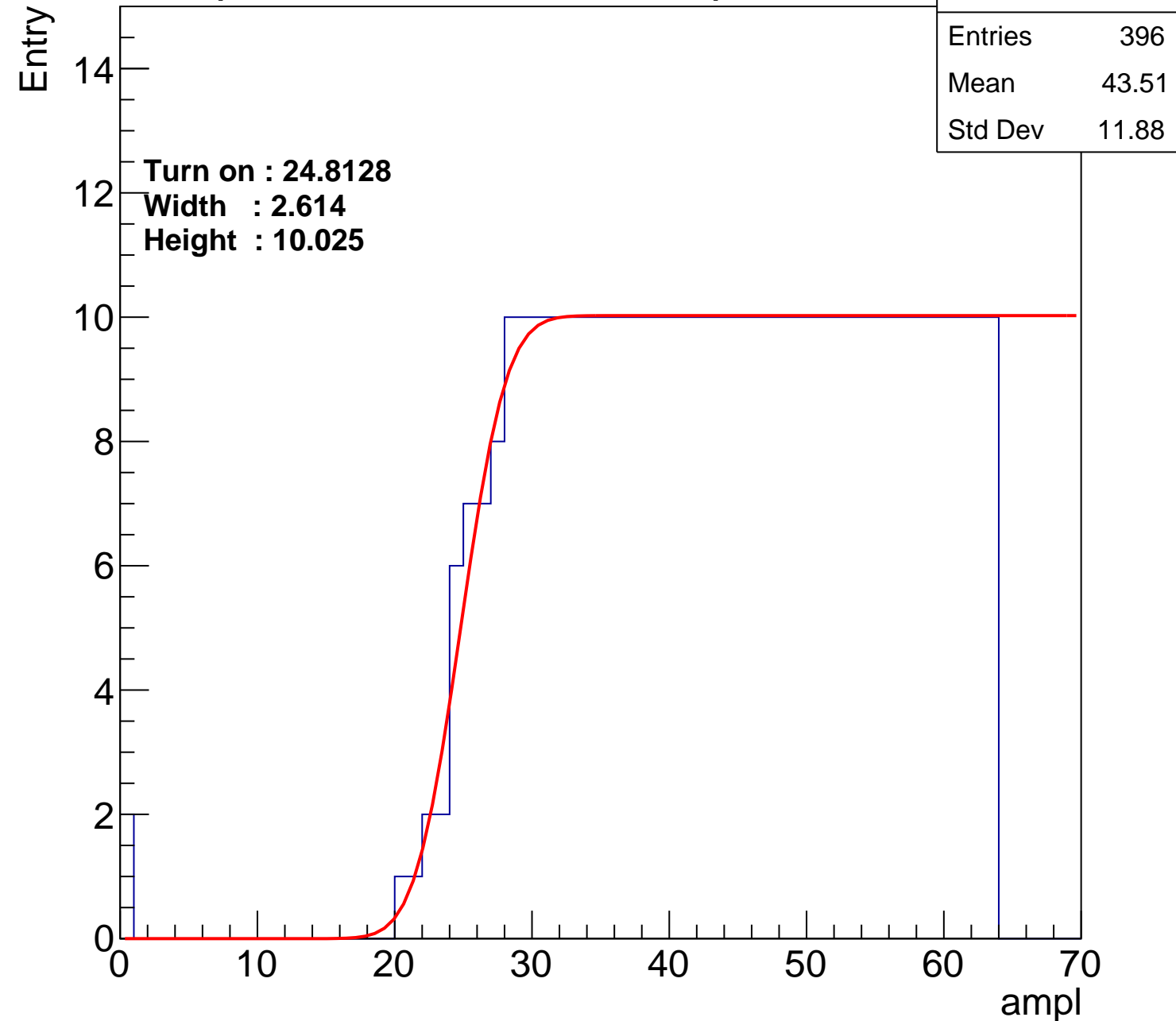
Width : 2.614

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch68

calib_packv5_042523_0143.root, FC#10, port B3

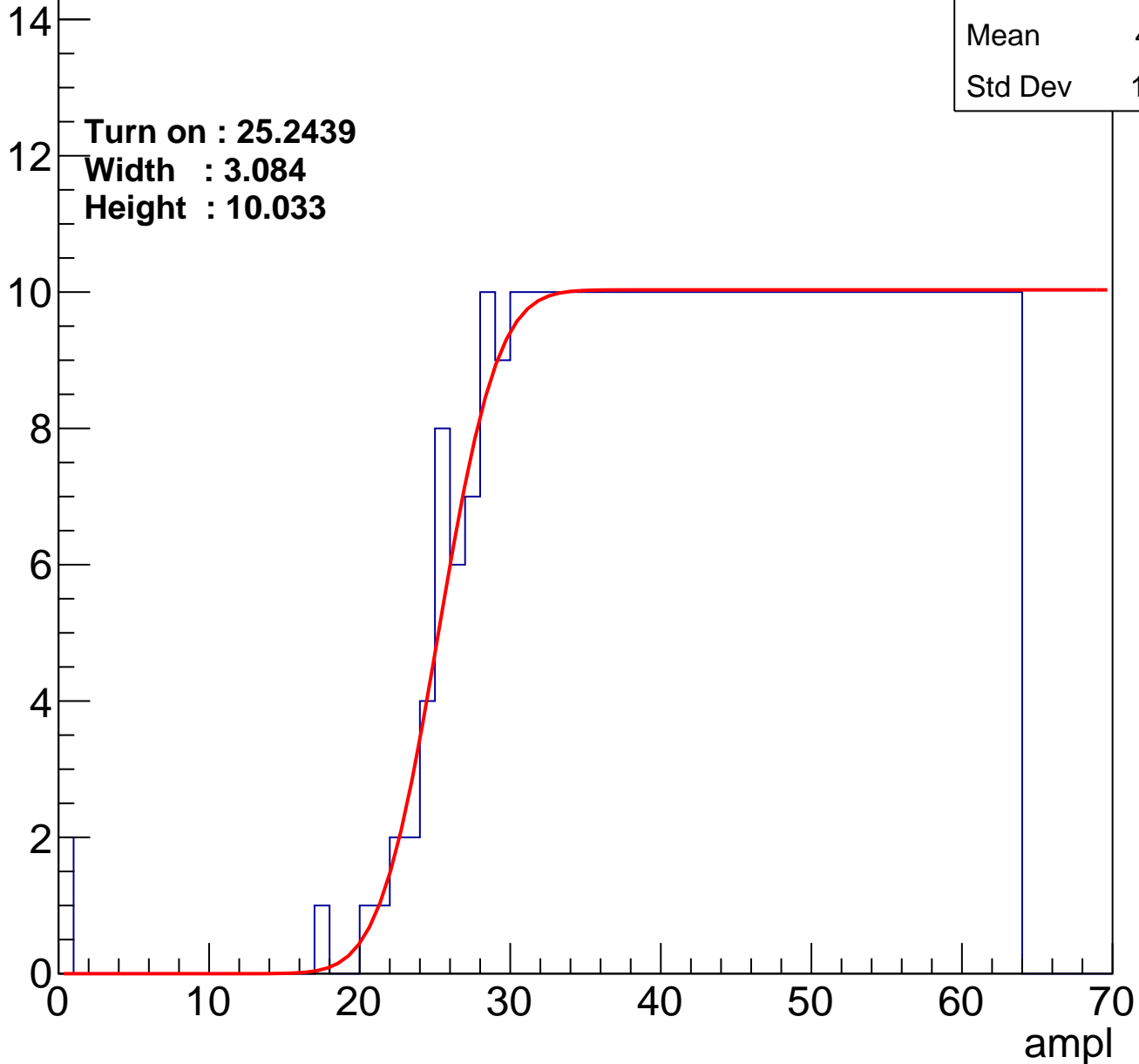
Entries	393
Mean	43.61
Std Dev	11.87

Turn on : 25.2439

Width : 3.084

Height : 10.033

Entry



B1L002S, U26-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.8656

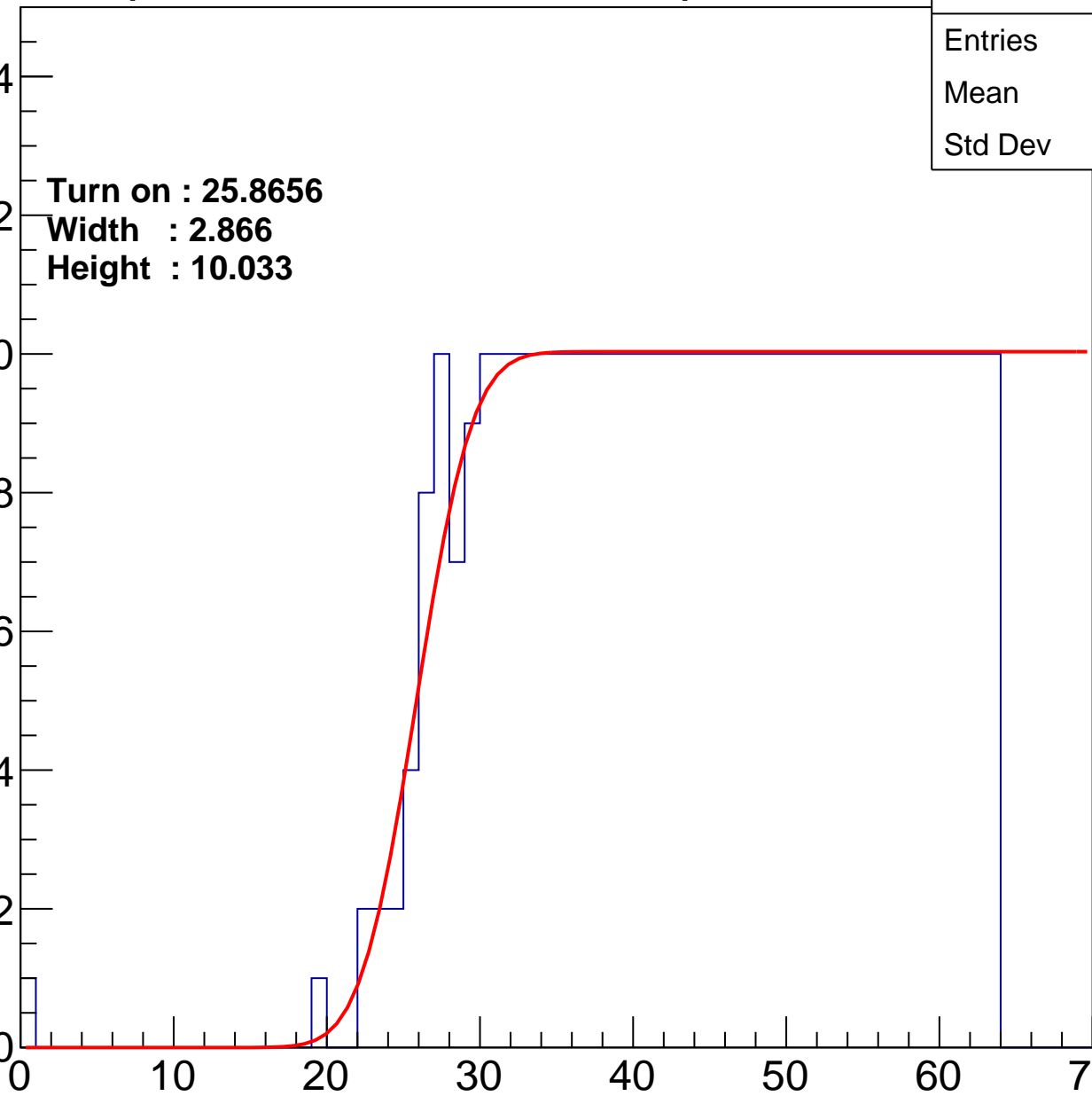
Width : 2.866

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.49
Std Dev	11.79

Turn on : 24.8307

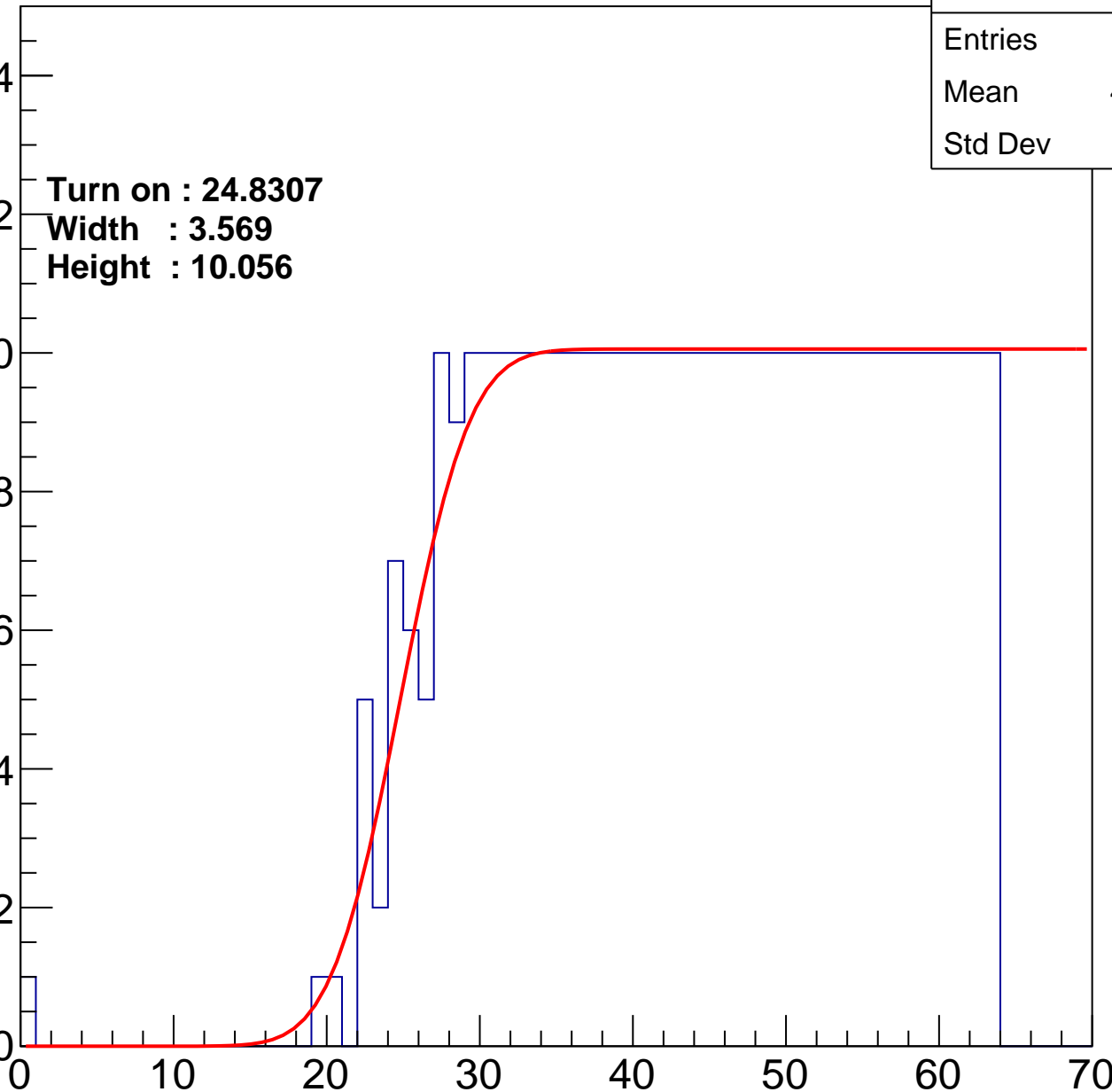
Width : 3.569

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch71

calib_packv5_042523_0143.root, FC#10, port B3

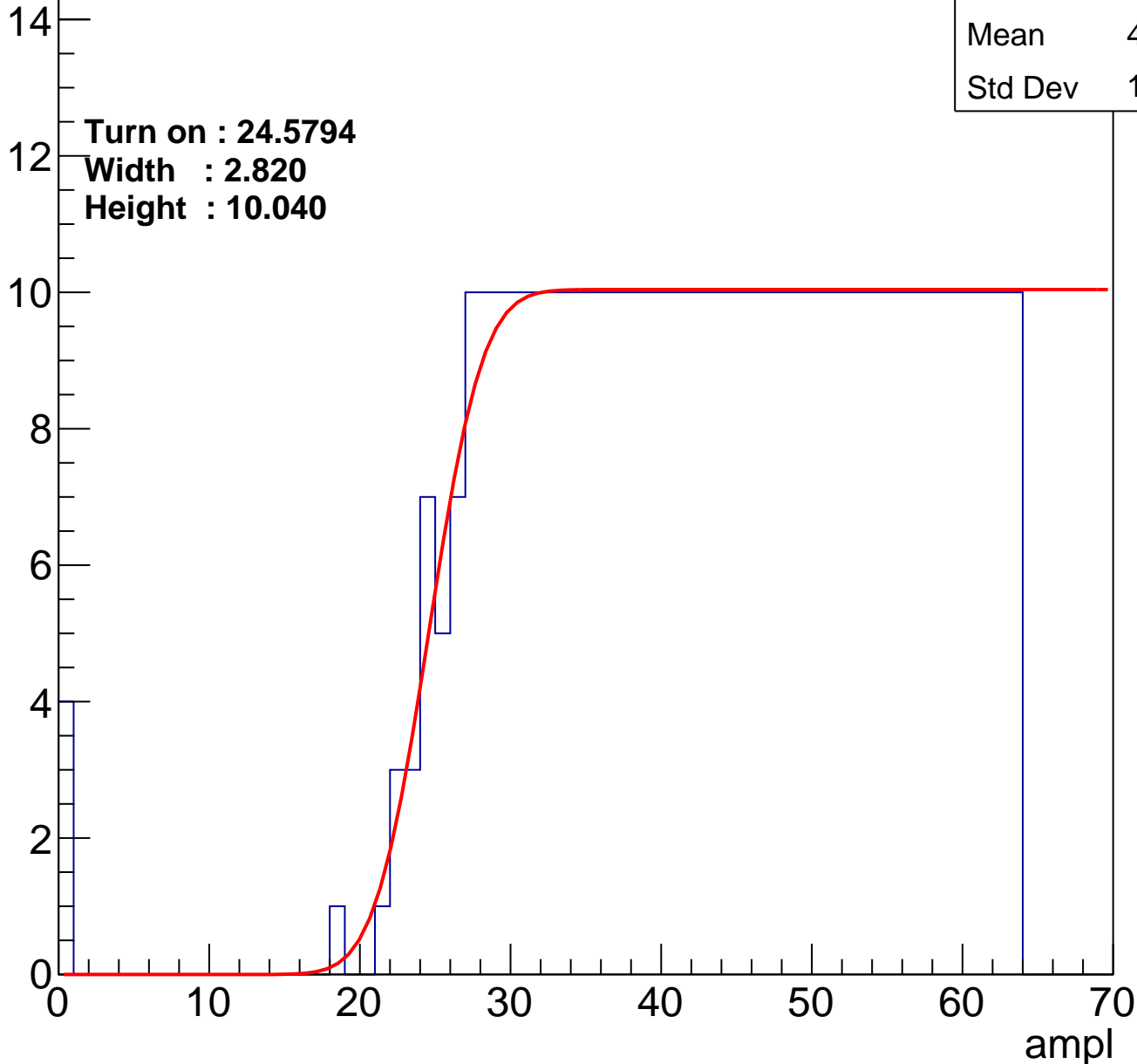
Entries	401
Mean	43.14
Std Dev	12.32

Turn on : 24.5794

Width : 2.820

Height : 10.040

Entry



B1L002S, U26-ch72

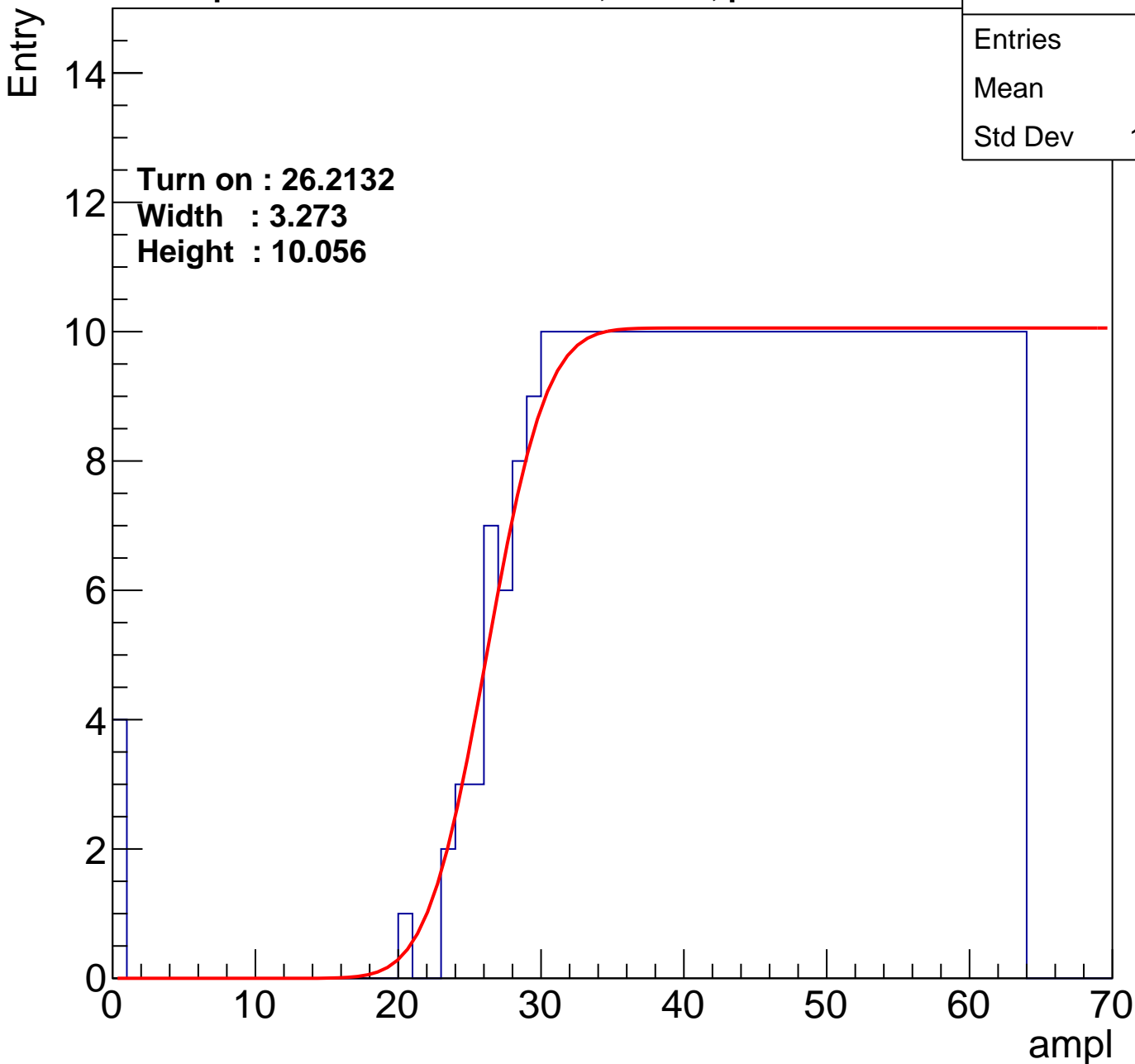
calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44
Std Dev	11.92

Turn on : 26.2132

Width : 3.273

Height : 10.056



B1L002S, U26-ch73

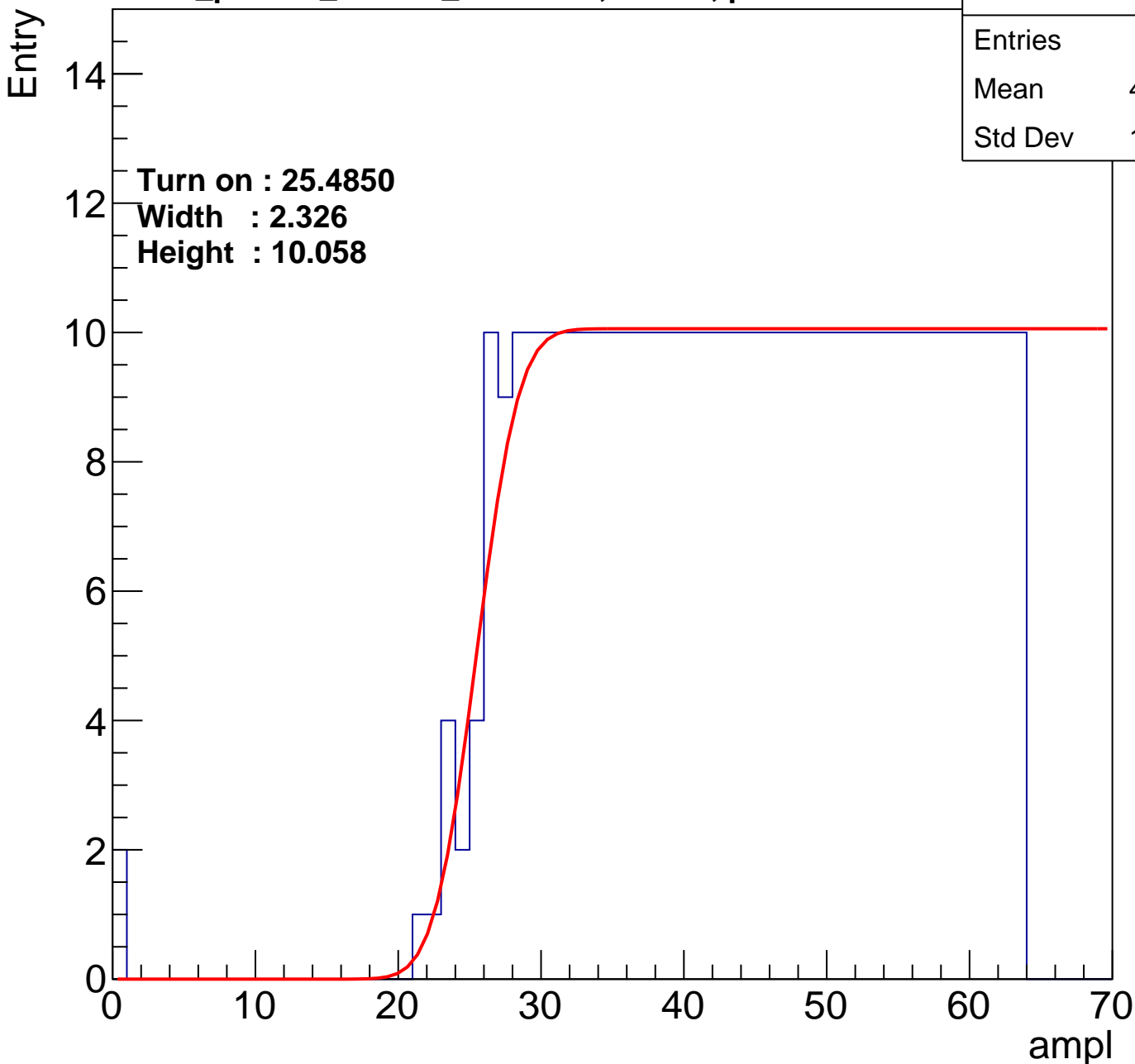
calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.68
Std Dev	11.76

Turn on : 25.4850

Width : 2.326

Height : 10.058



B1L002S, U26-ch74

calib_packv5_042523_0143.root, FC#10, port B3

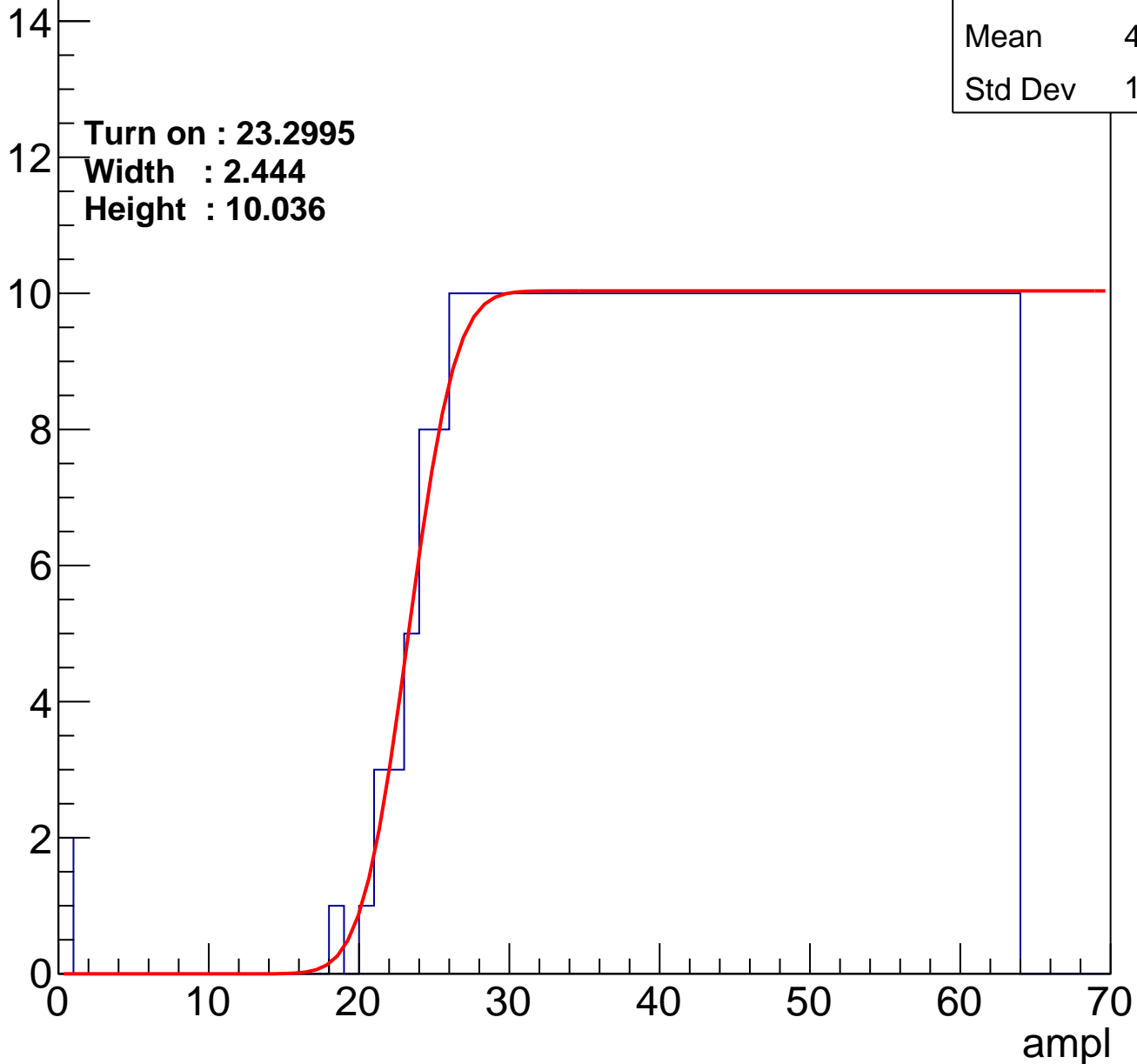
Entries	411
Mean	42.78
Std Dev	12.25

Turn on : 23.2995

Width : 2.444

Height : 10.036

Entry



B1L002S, U26-ch75

calib_packv5_042523_0143.root, FC#10, port B3

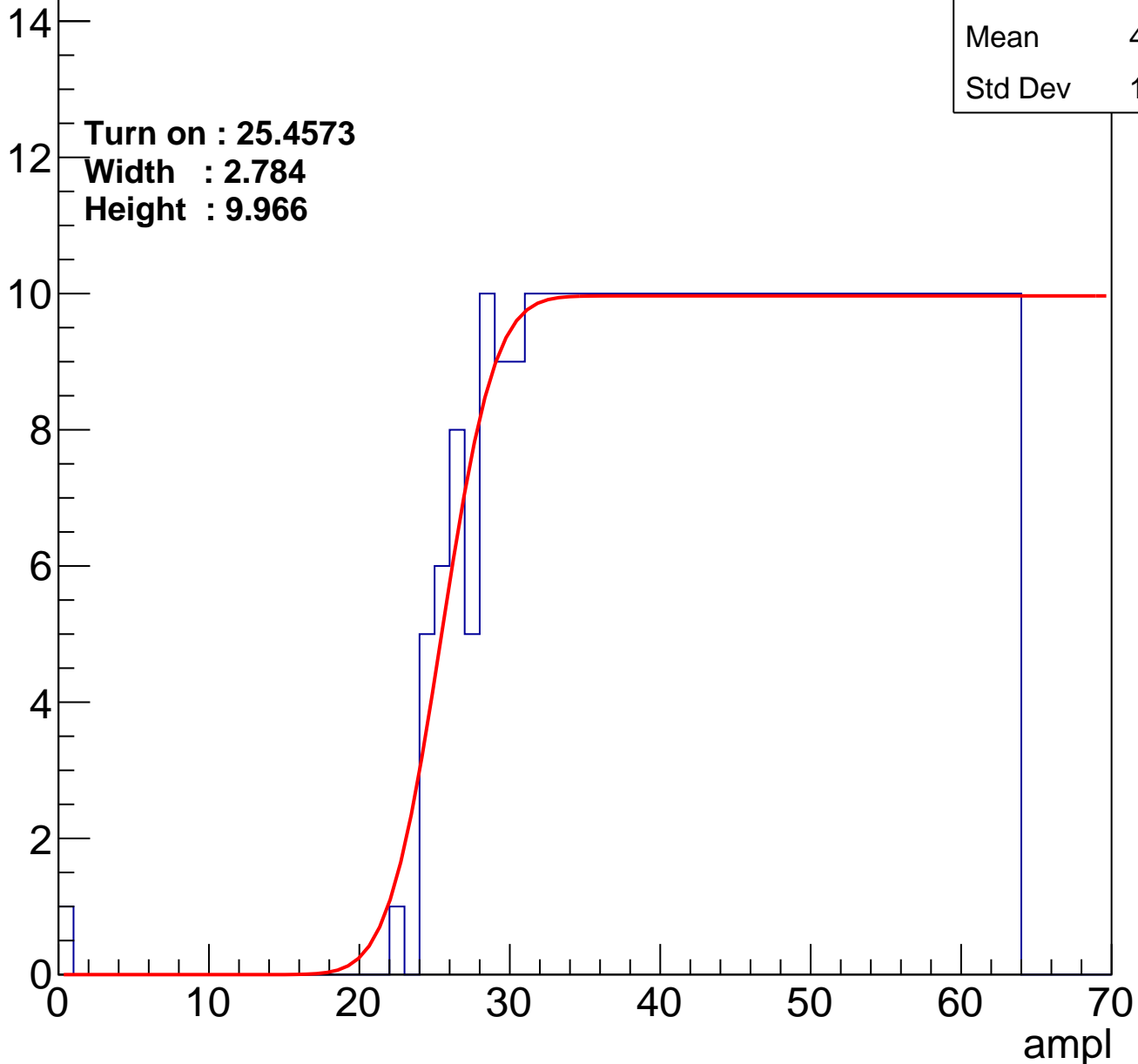
Entries	384
Mean	44.16
Std Dev	11.39

Turn on : 25.4573

Width : 2.784

Height : 9.966

Entry



B1L002S, U26-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.53
Std Dev	11.78

Turn on : 24.8204

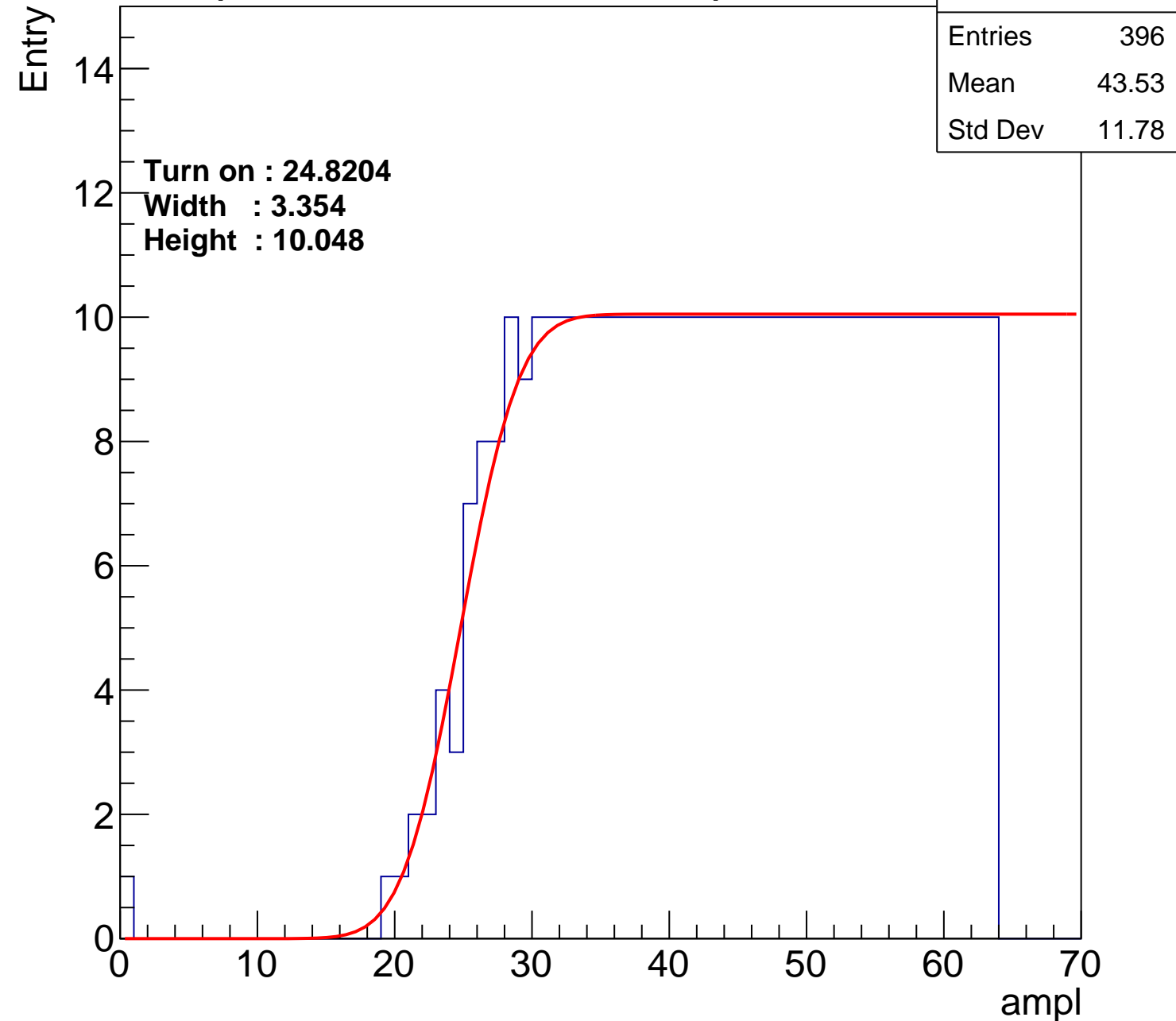
Width : 3.354

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch77

calib_packv5_042523_0143.root, FC#10, port B3

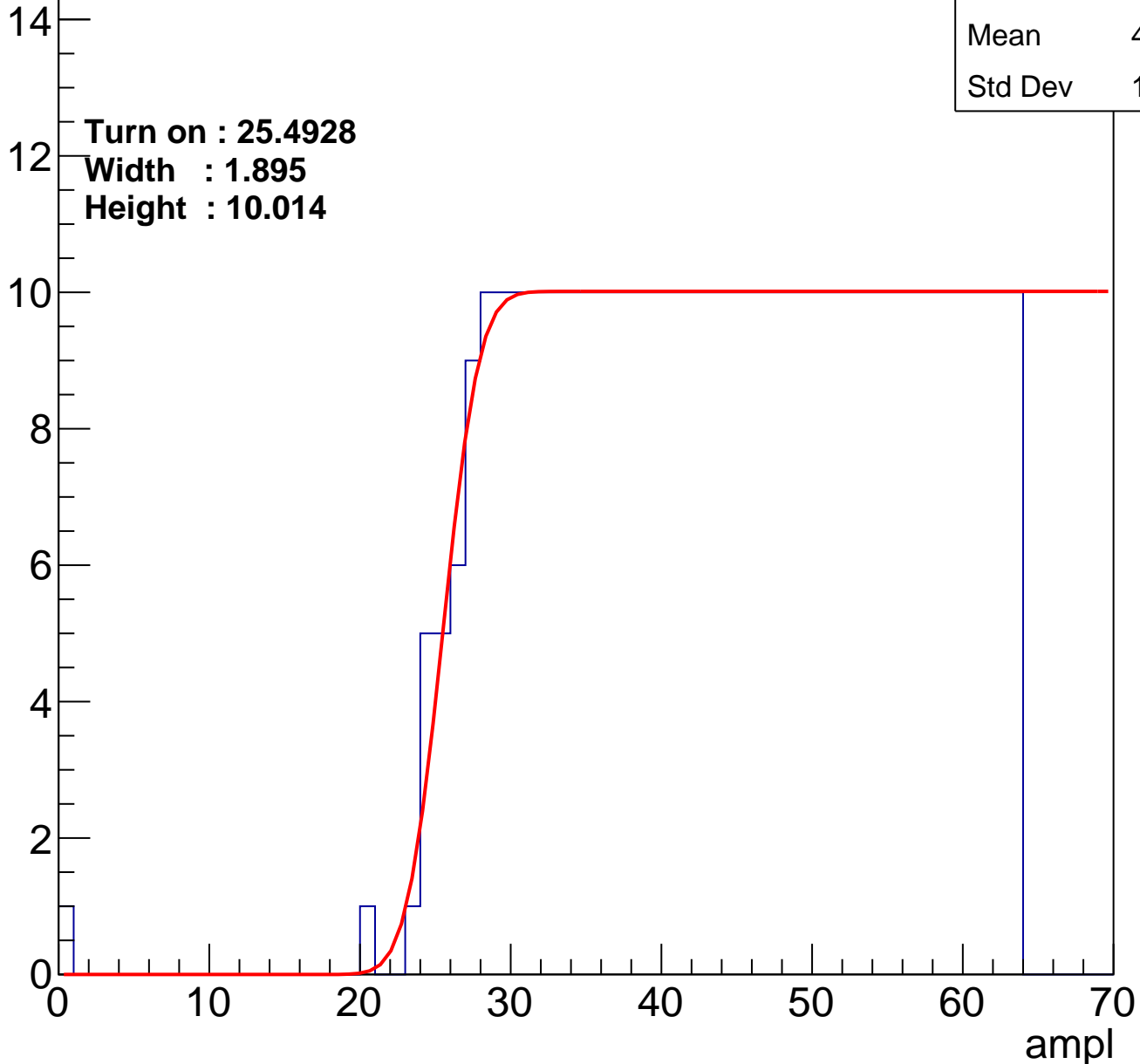
Entries	388
Mean	43.99
Std Dev	11.46

Turn on : 25.4928

Width : 1.895

Height : 10.014

Entry



B1L002S, U26-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.28
Std Dev	11.3

Turn on : 25.4629

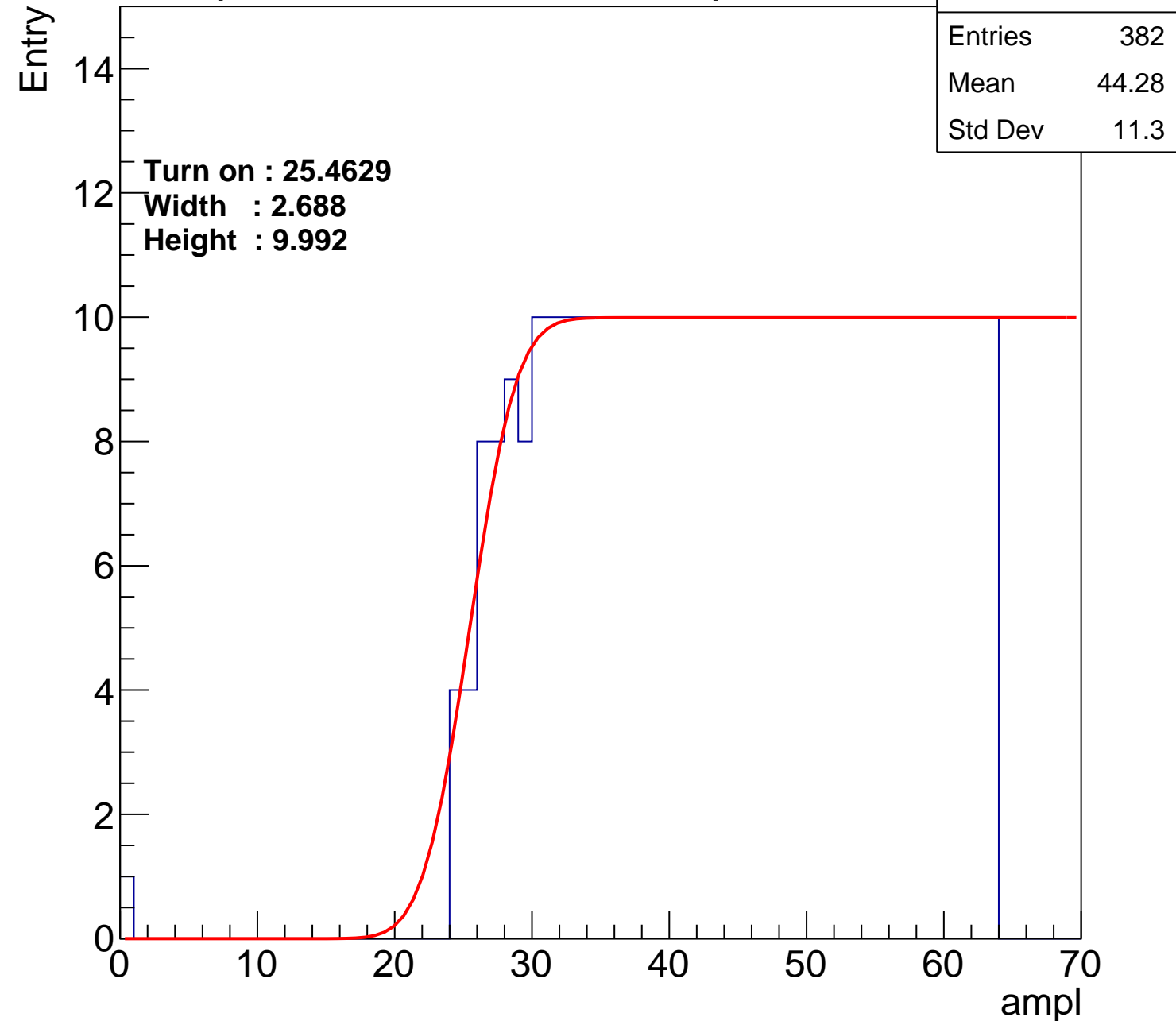
Width : 2.688

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 26.2387

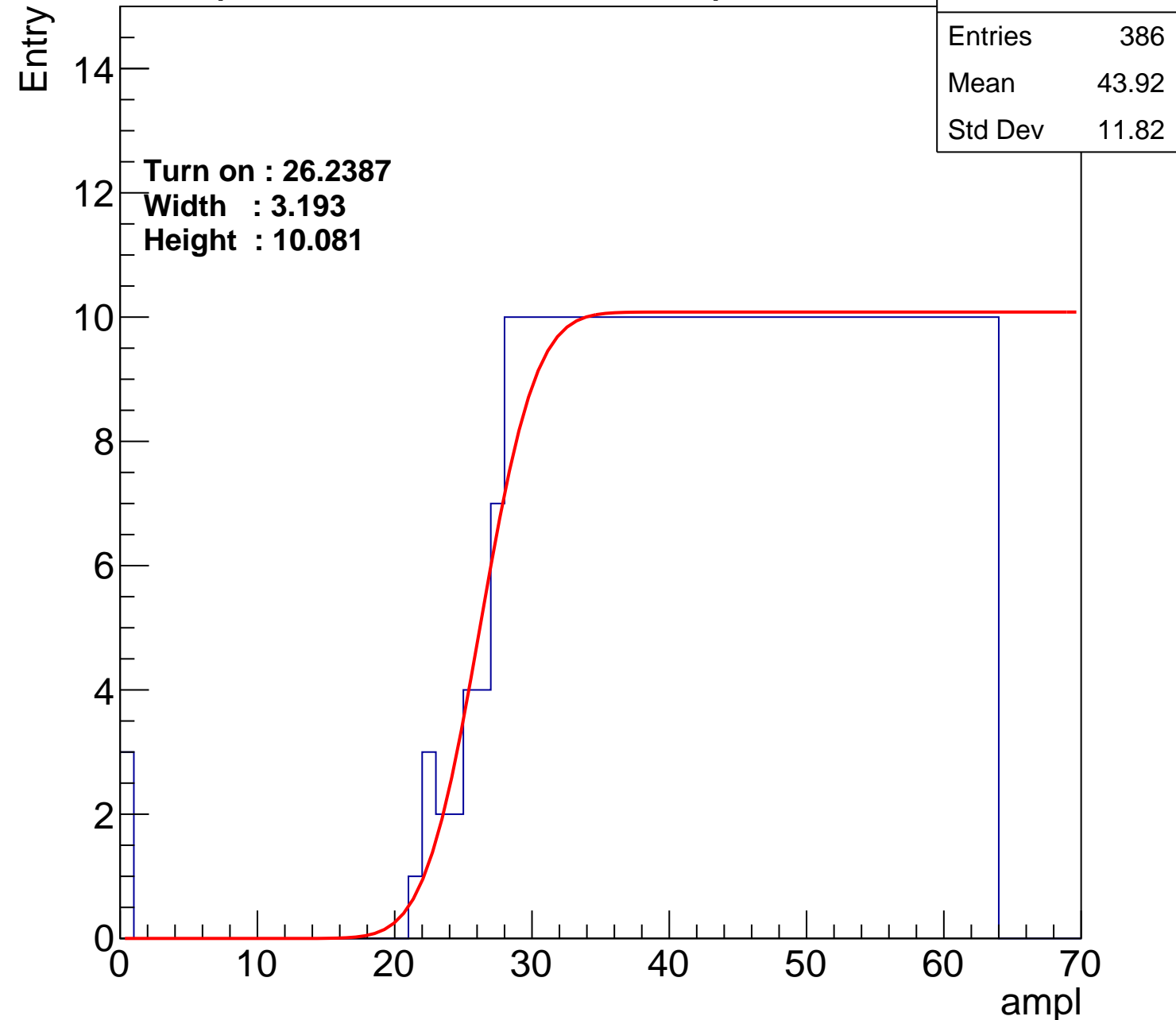
Width : 3.193

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch80

calib_packv5_042523_0143.root, FC#10, port B3

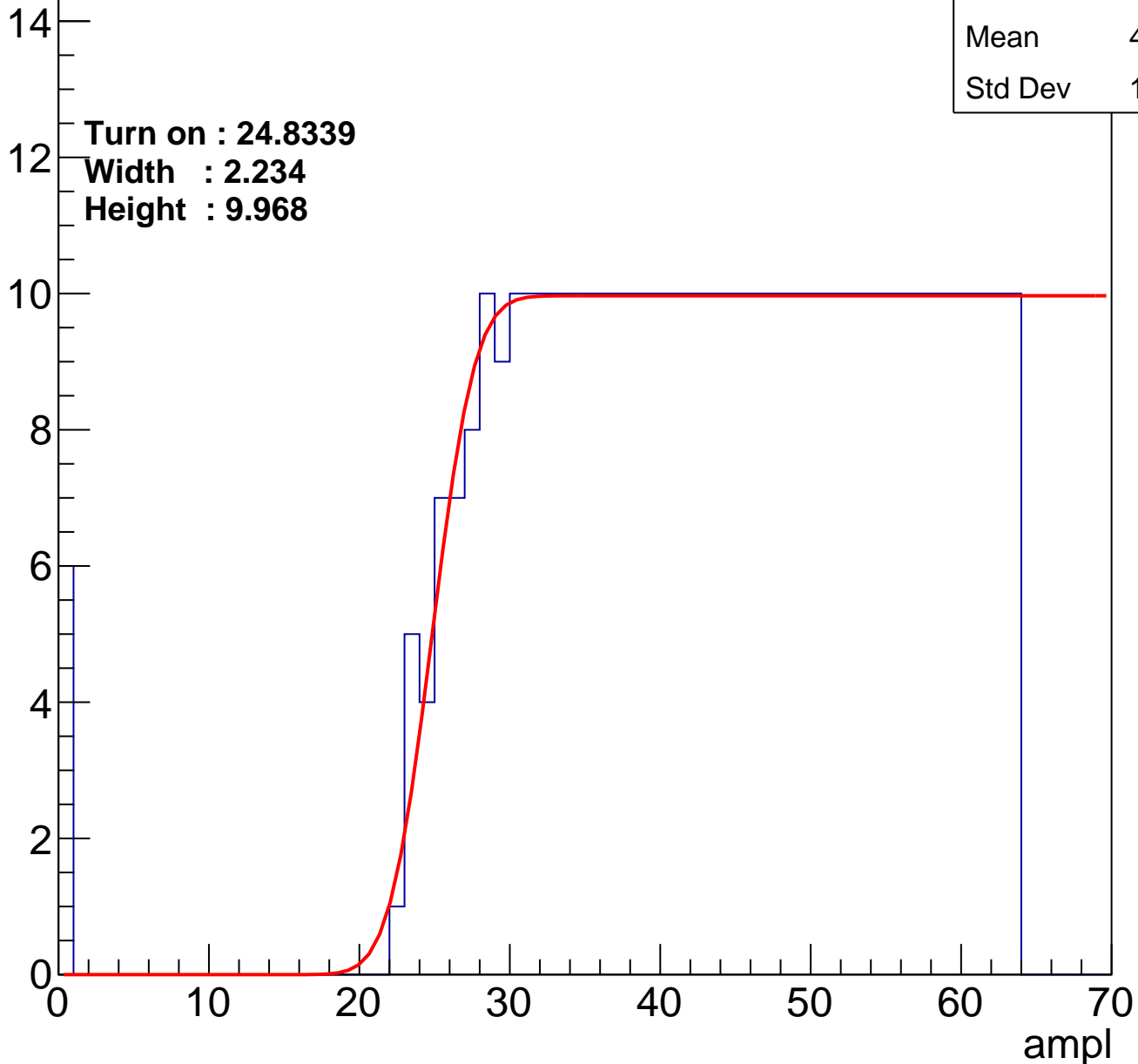
Entries	397
Mean	43.22
Std Dev	12.52

Turn on : 24.8339

Width : 2.234

Height : 9.968

Entry



B1L002S, U26-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.53
Std Dev	11.73

Turn on : 24.7196

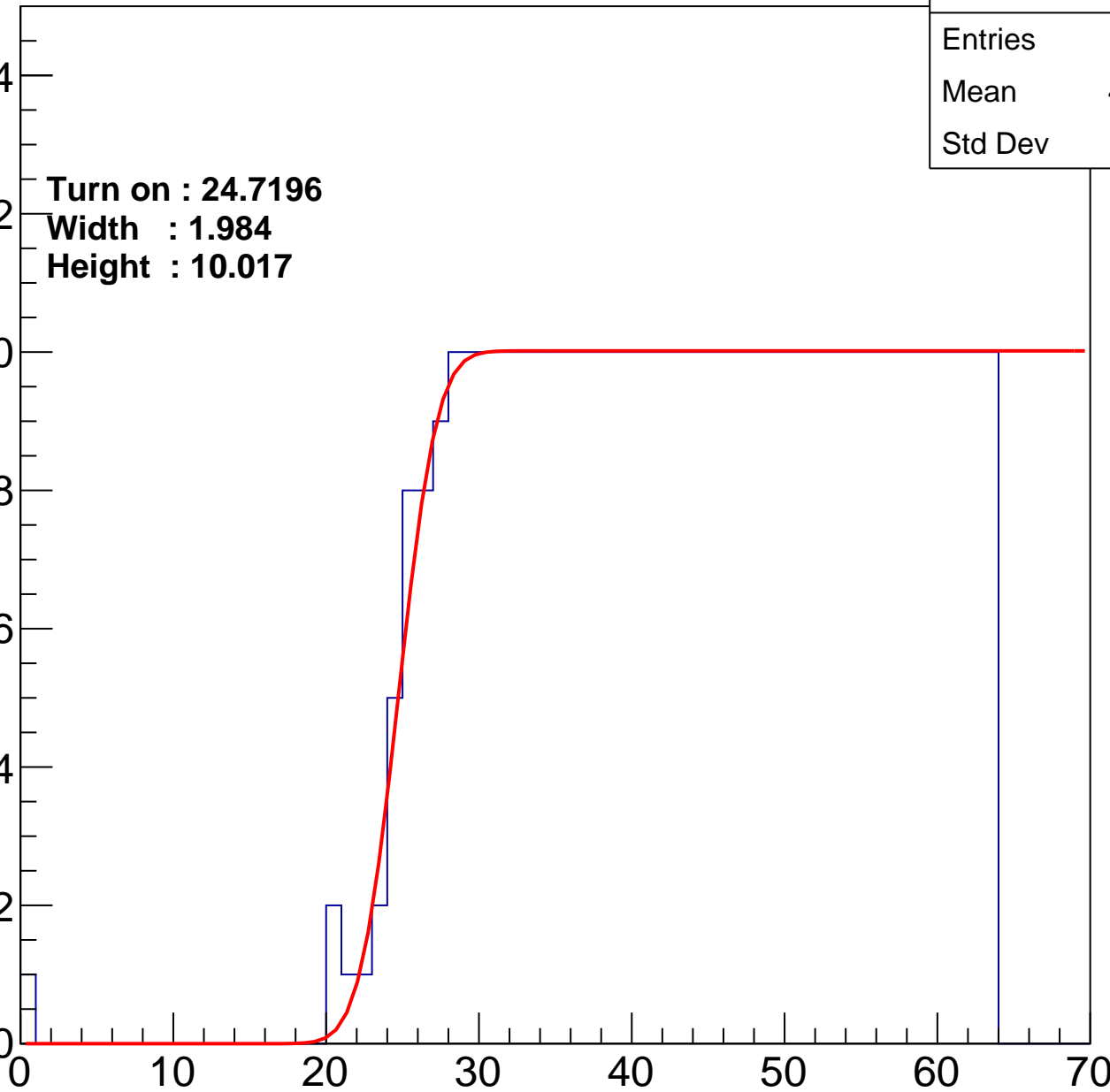
Width : 1.984

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.5
Std Dev	11.94

Turn on : 24.8357

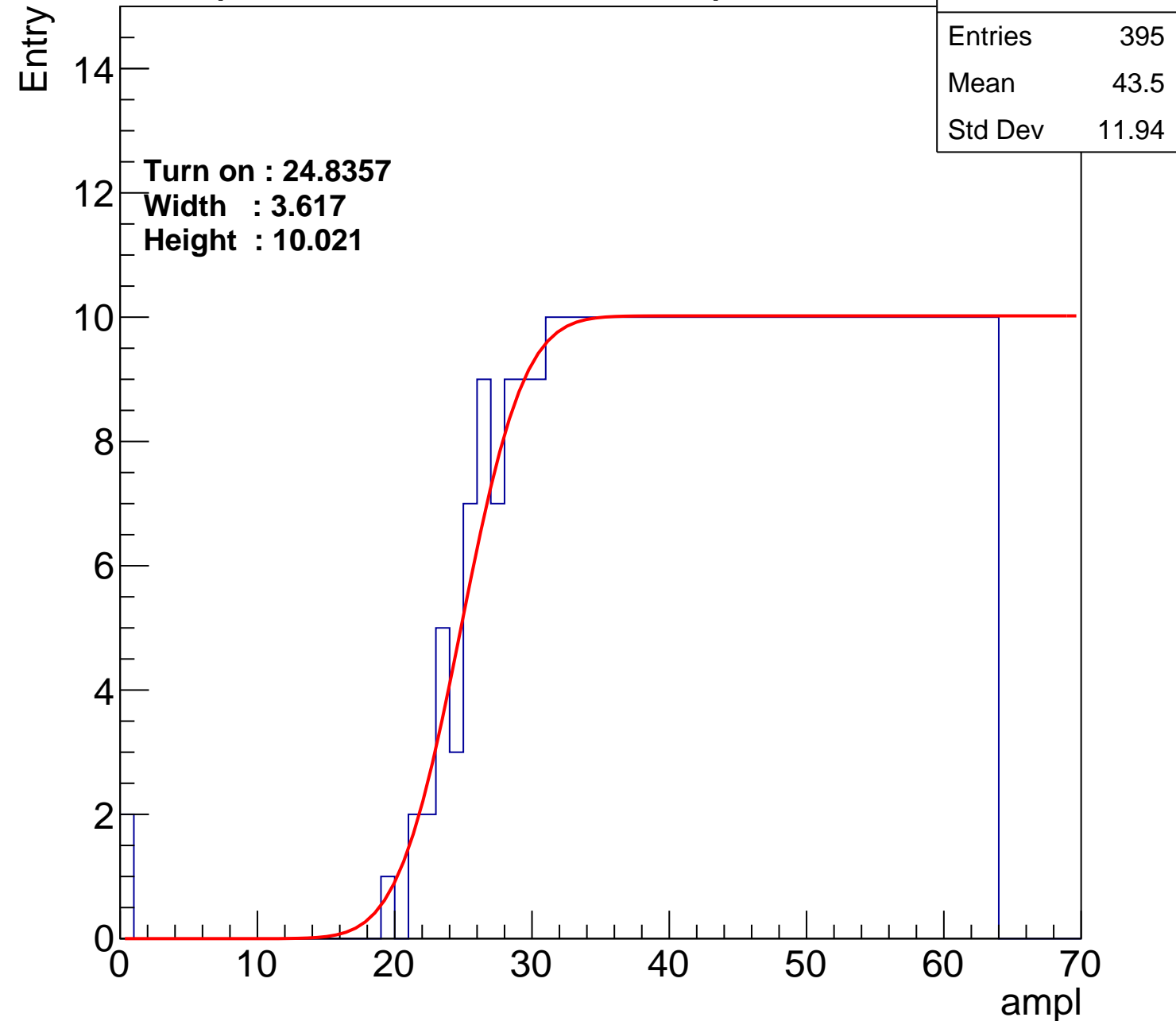
Width : 3.617

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.9316

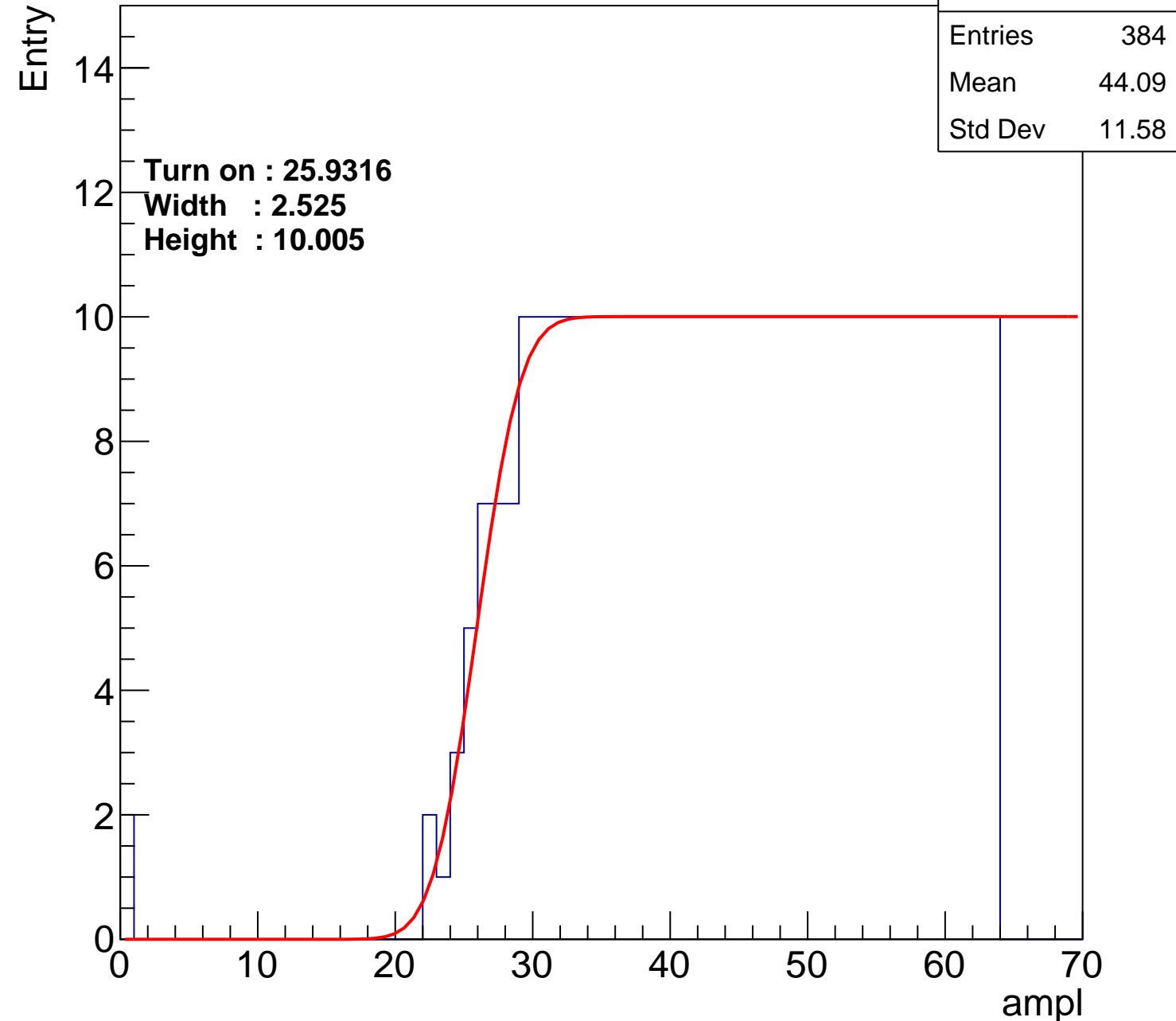
Width : 2.525

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.28
Std Dev	12.13

Turn on : 24.5117

Width : 2.148

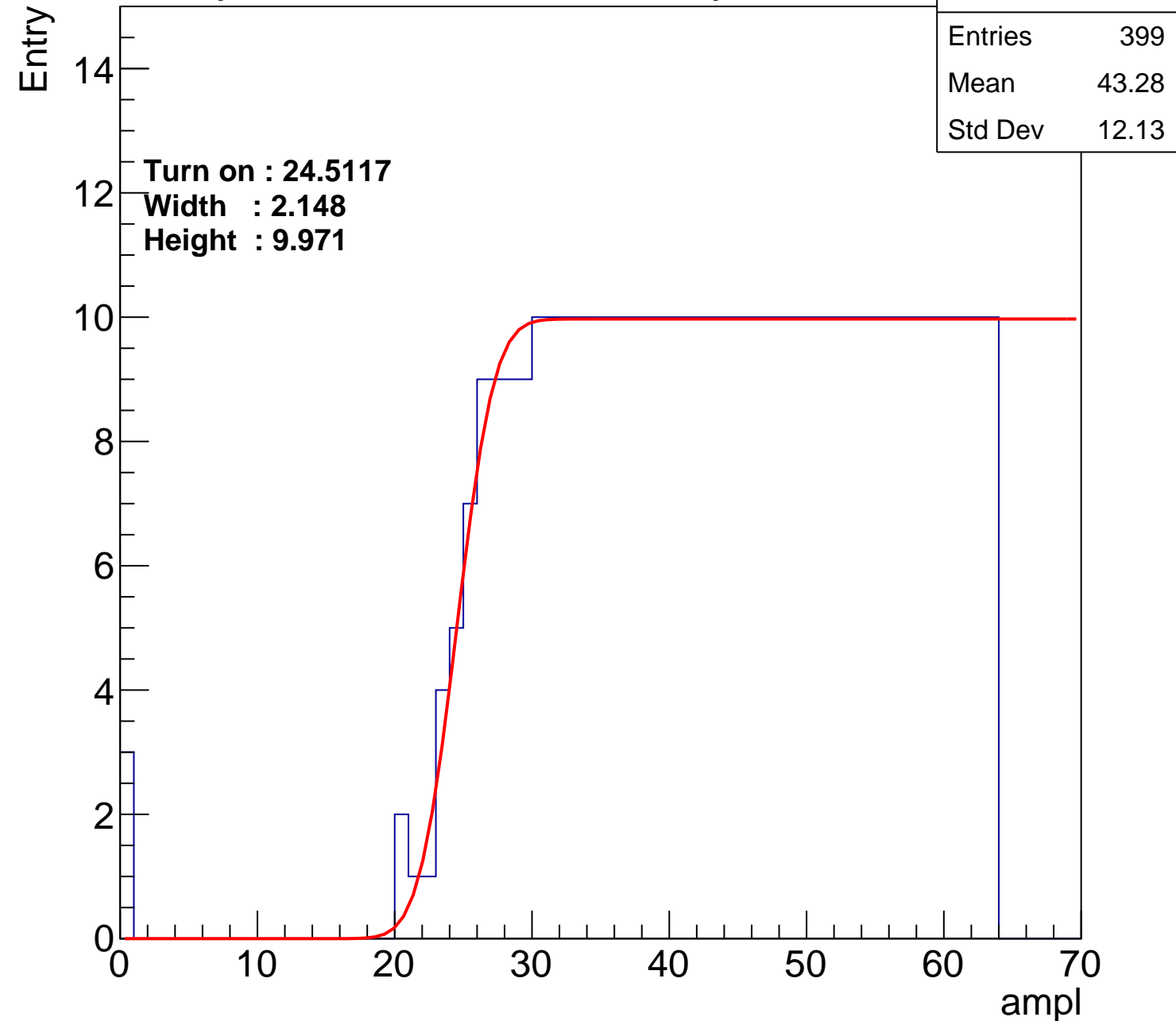
Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U26-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.51
Std Dev	12.16

Turn on : 24.9188

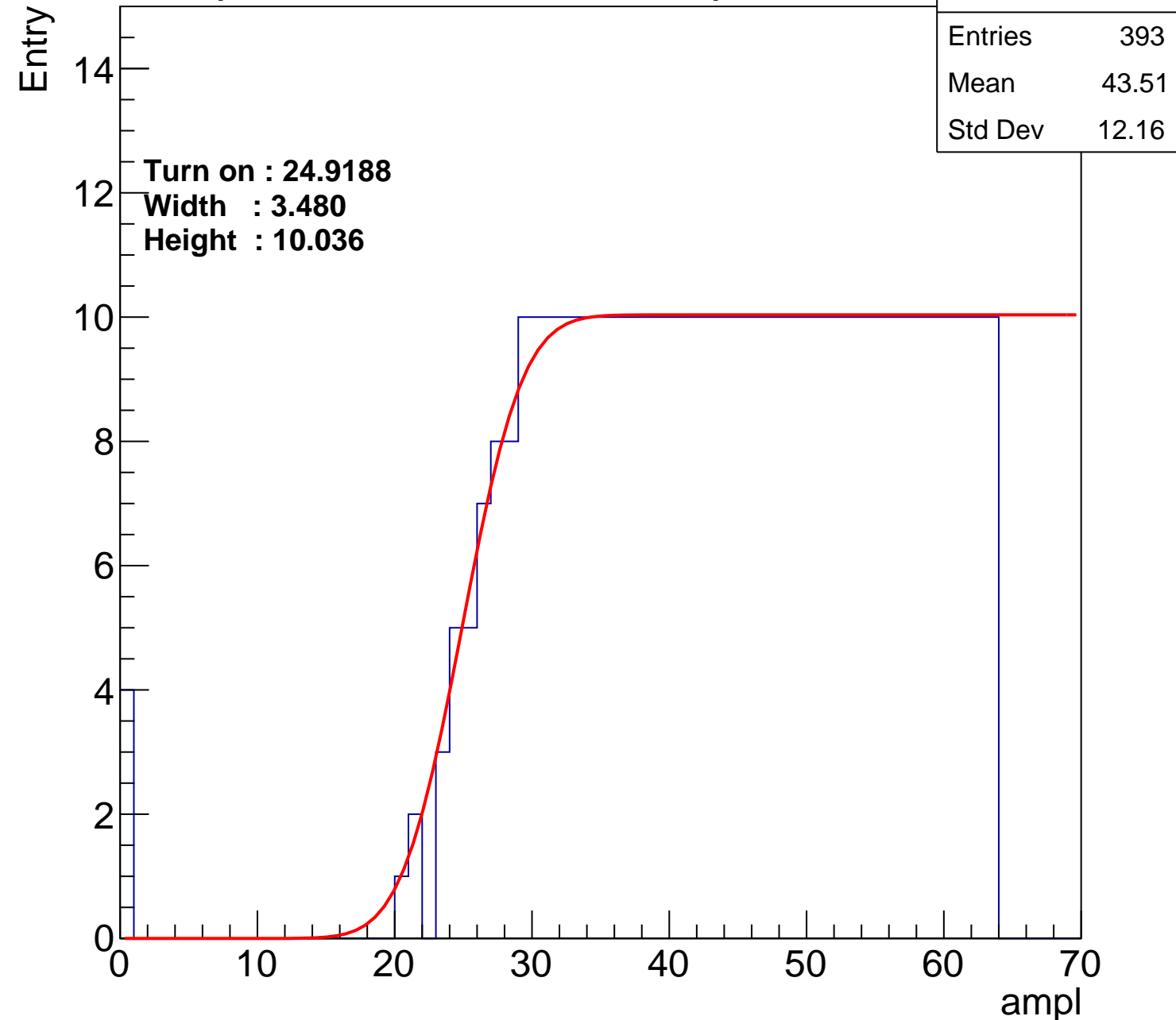
Width : 3.480

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch86

calib_packv5_042523_0143.root, FC#10, port B3

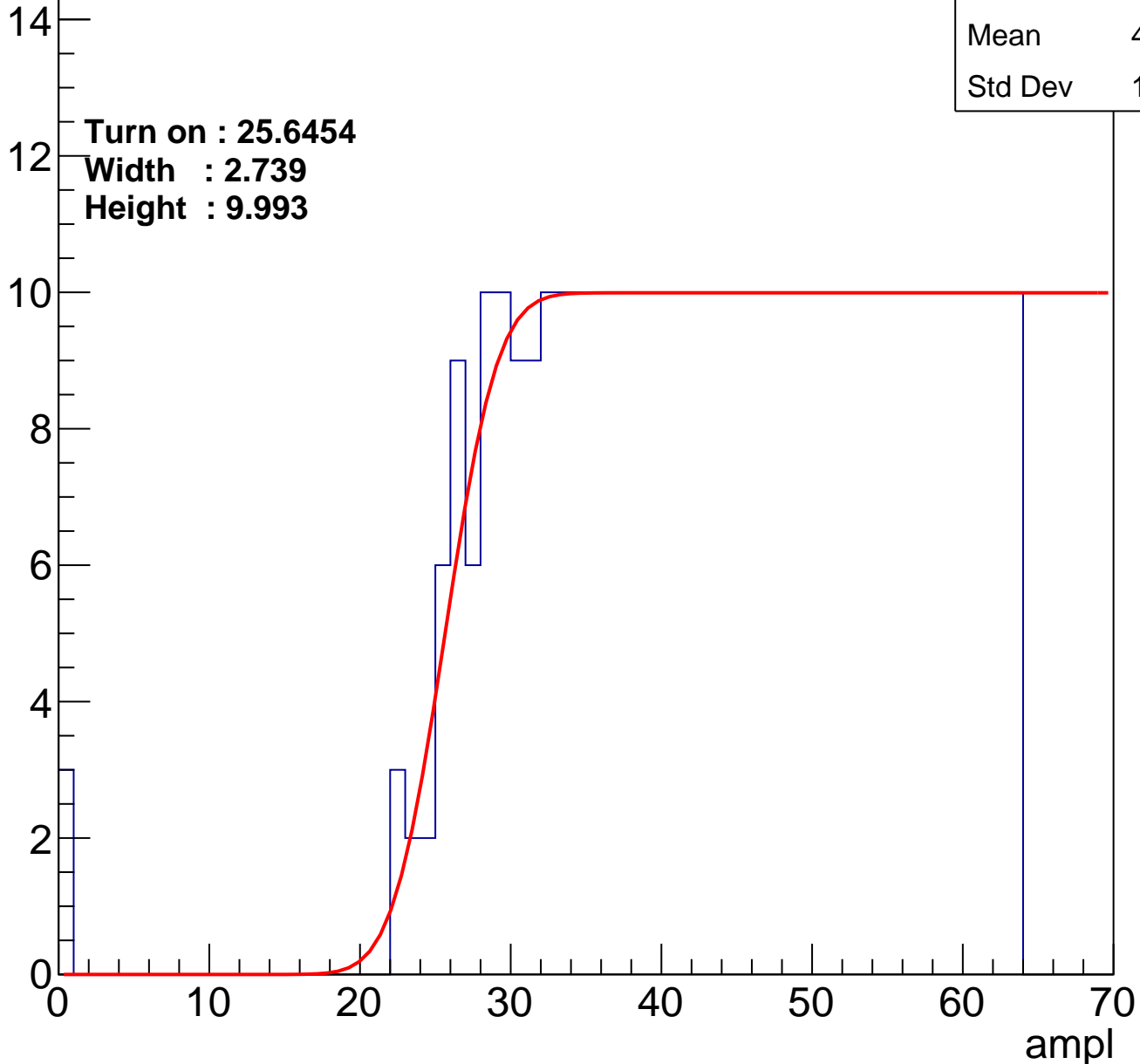
Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.6454

Width : 2.739

Height : 9.993

Entry



B1L002S, U26-ch87

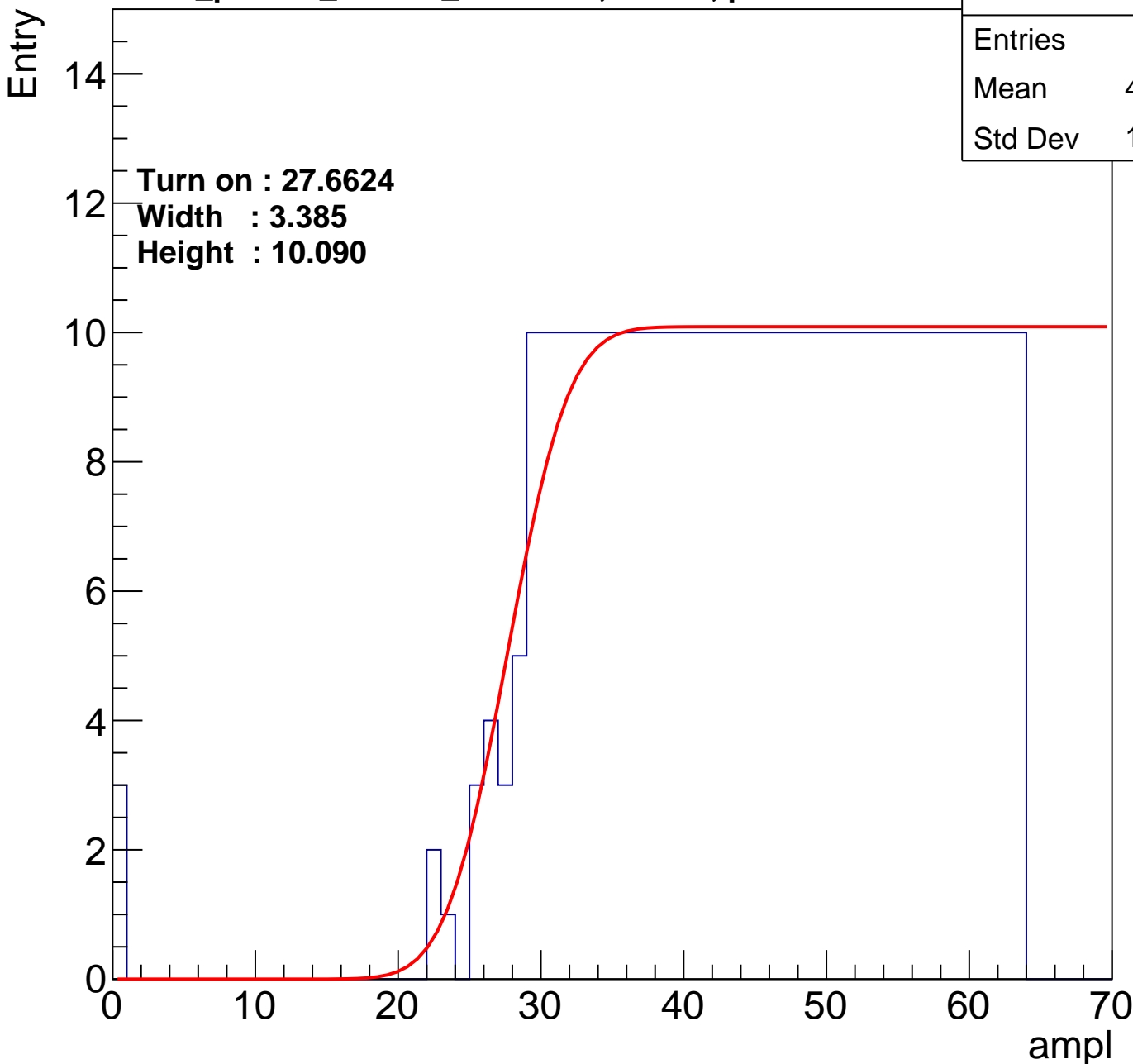
calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.65
Std Dev	11.46

Turn on : 27.6624

Width : 3.385

Height : 10.090



B1L002S, U26-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.14
Std Dev	11.43

Turn on : 25.9250

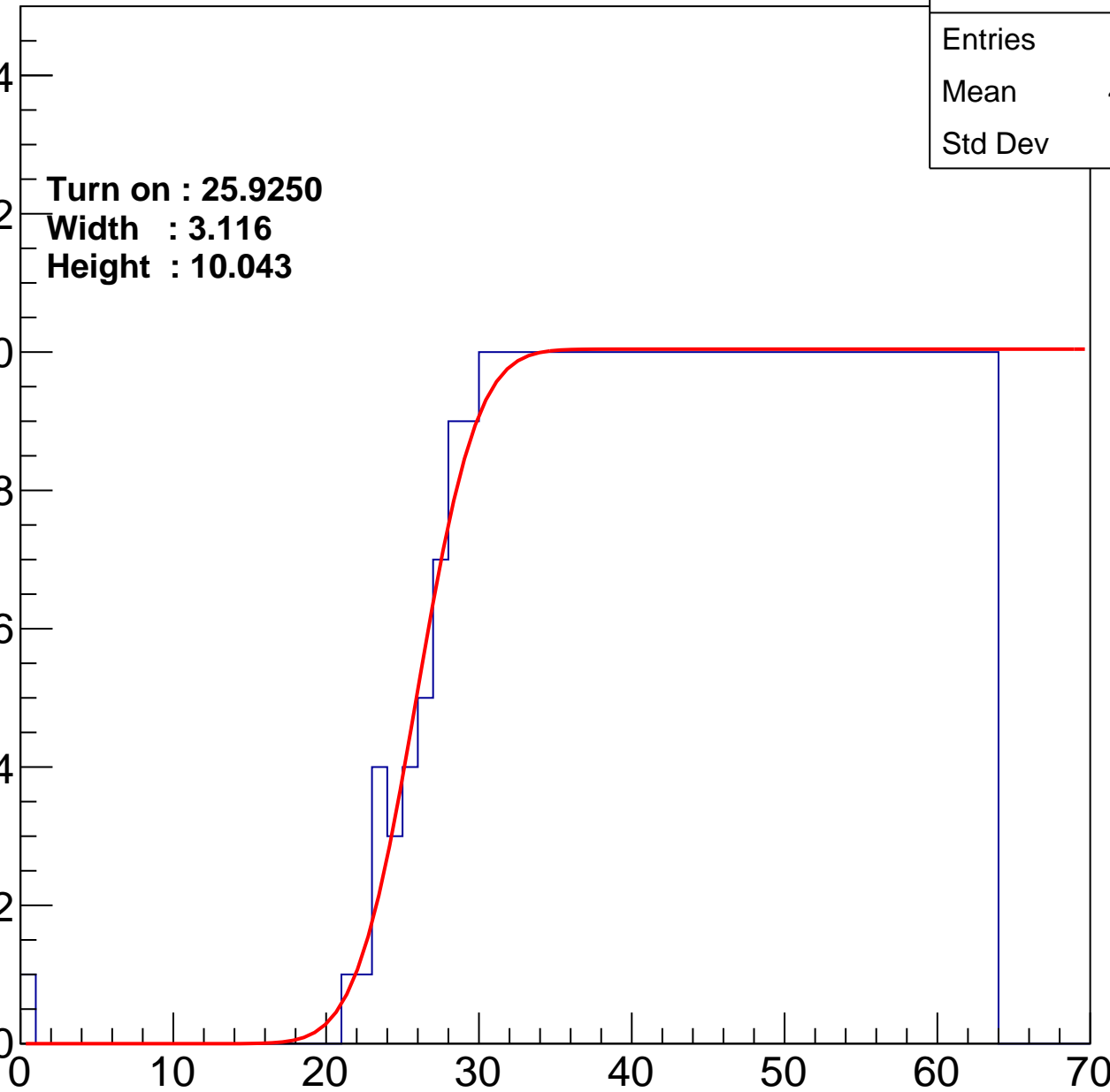
Width : 3.116

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.73
Std Dev	11.38

Turn on : 26.6787

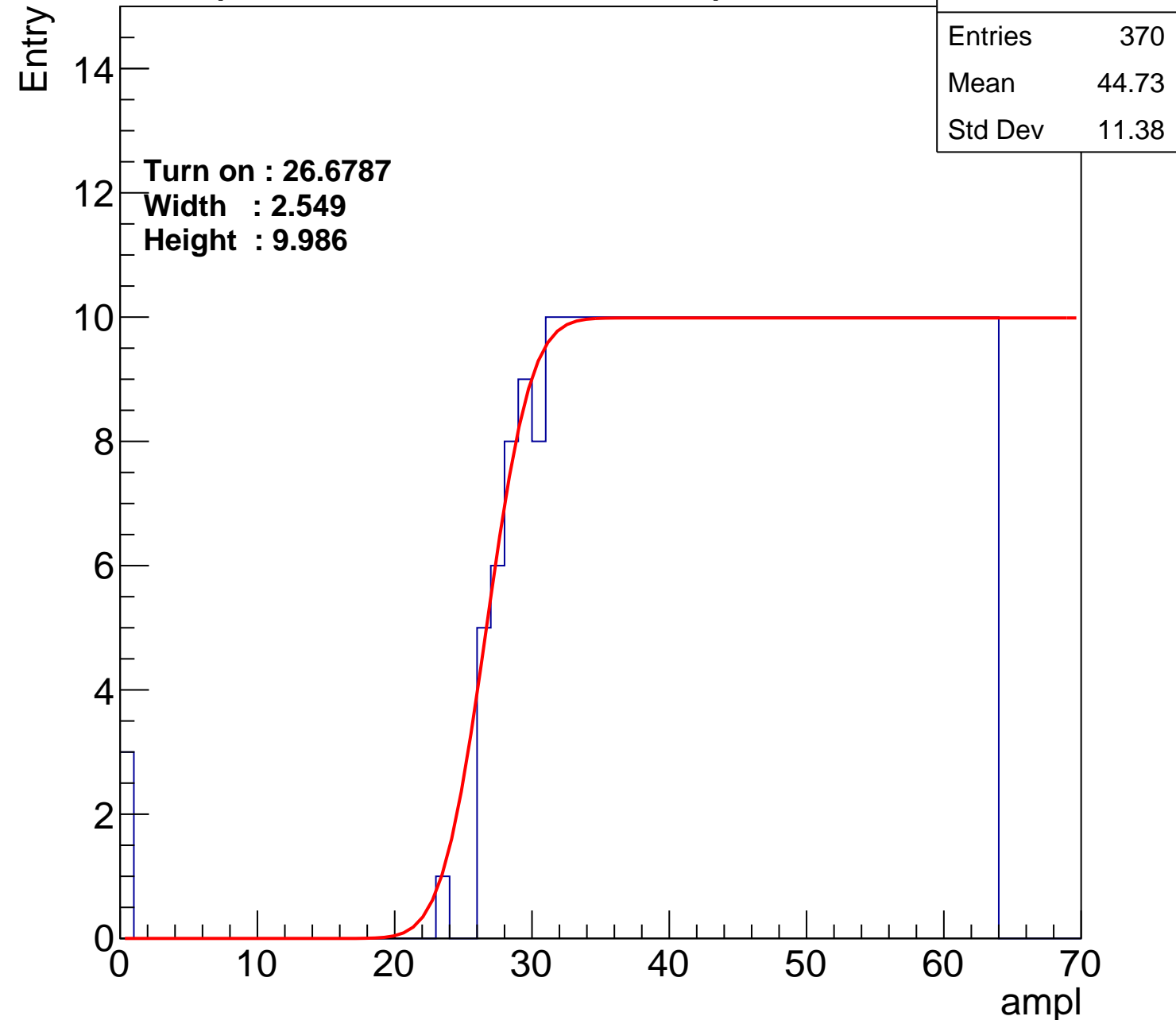
Width : 2.549

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch90

calib_packv5_042523_0143.root, FC#10, port B3

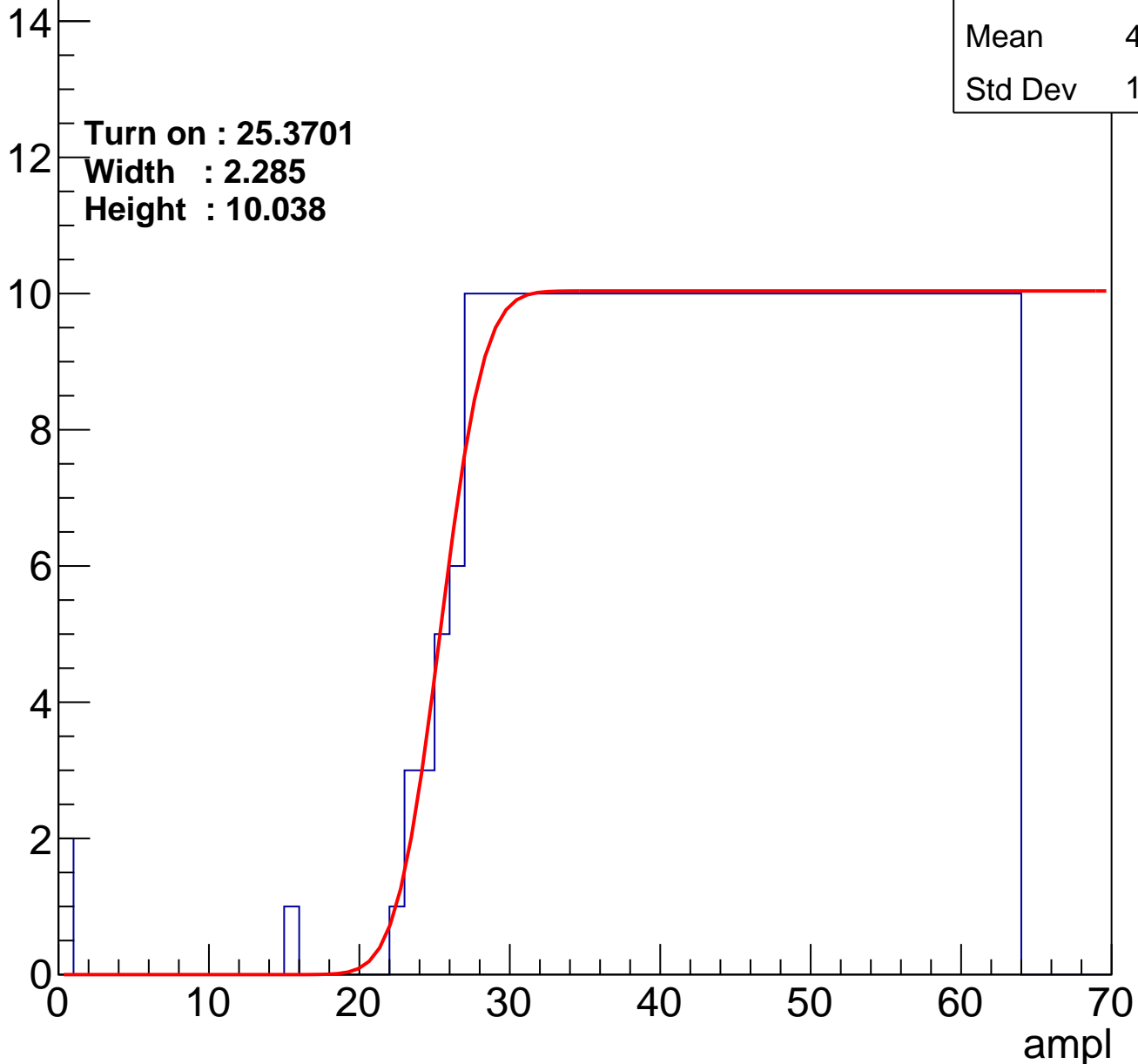
Entries	391
Mean	43.76
Std Dev	11.75

Turn on : 25.3701

Width : 2.285

Height : 10.038

Entry



B1L002S, U26-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.02
Std Dev	11.94

Turn on : 26.4511

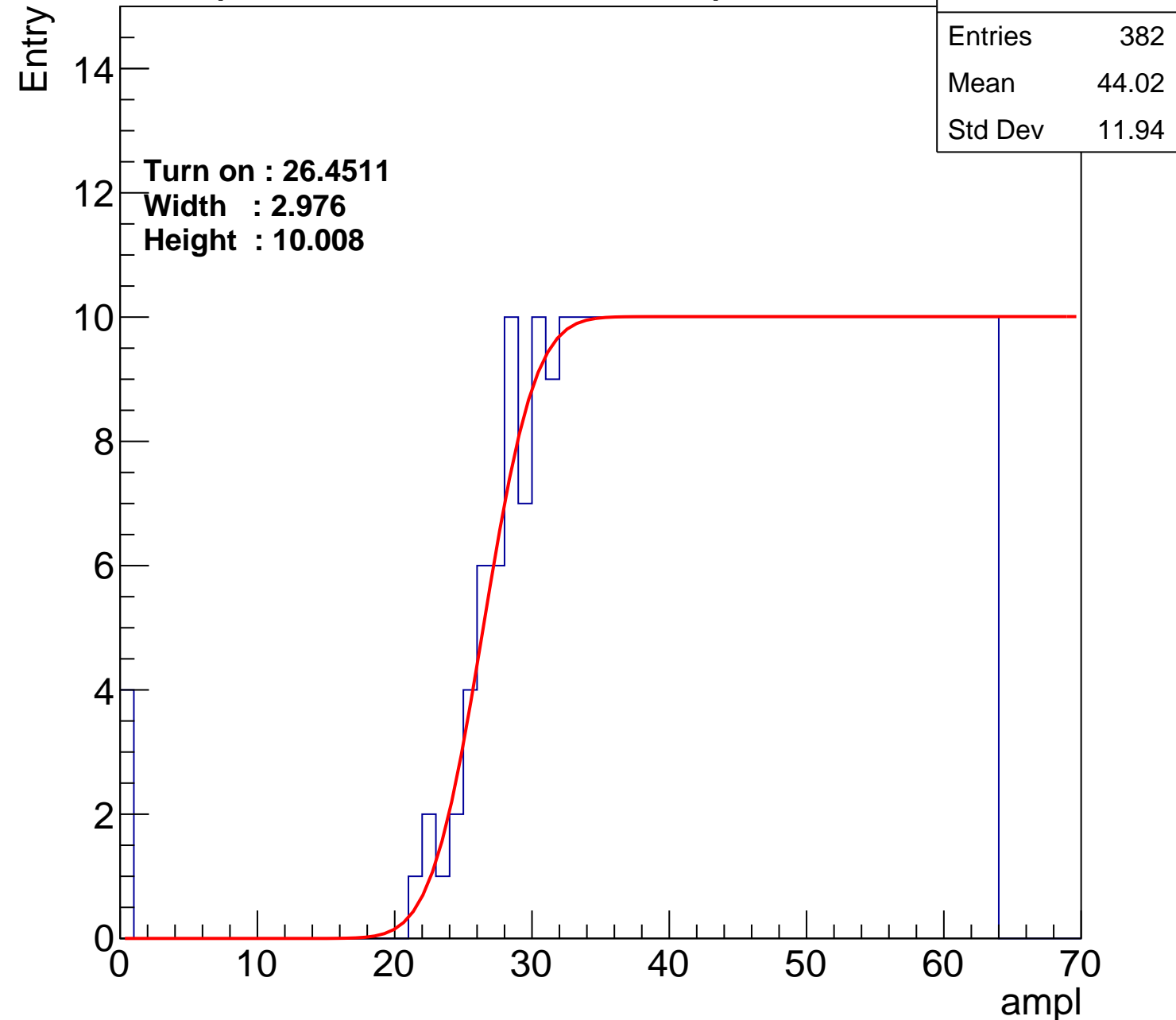
Width : 2.976

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.3375

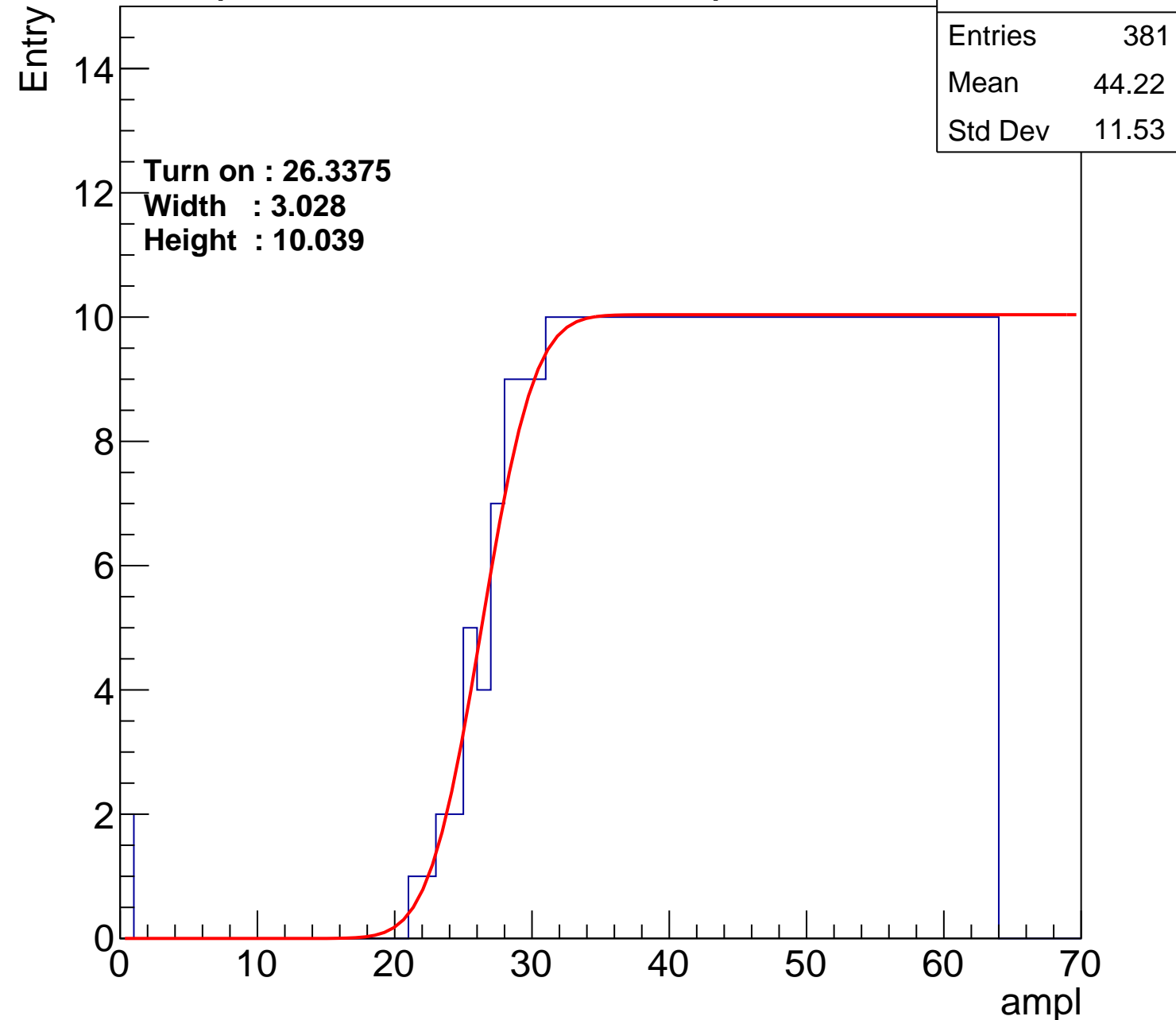
Width : 3.028

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch93

calib_packv5_042523_0143.root, FC#10, port B3

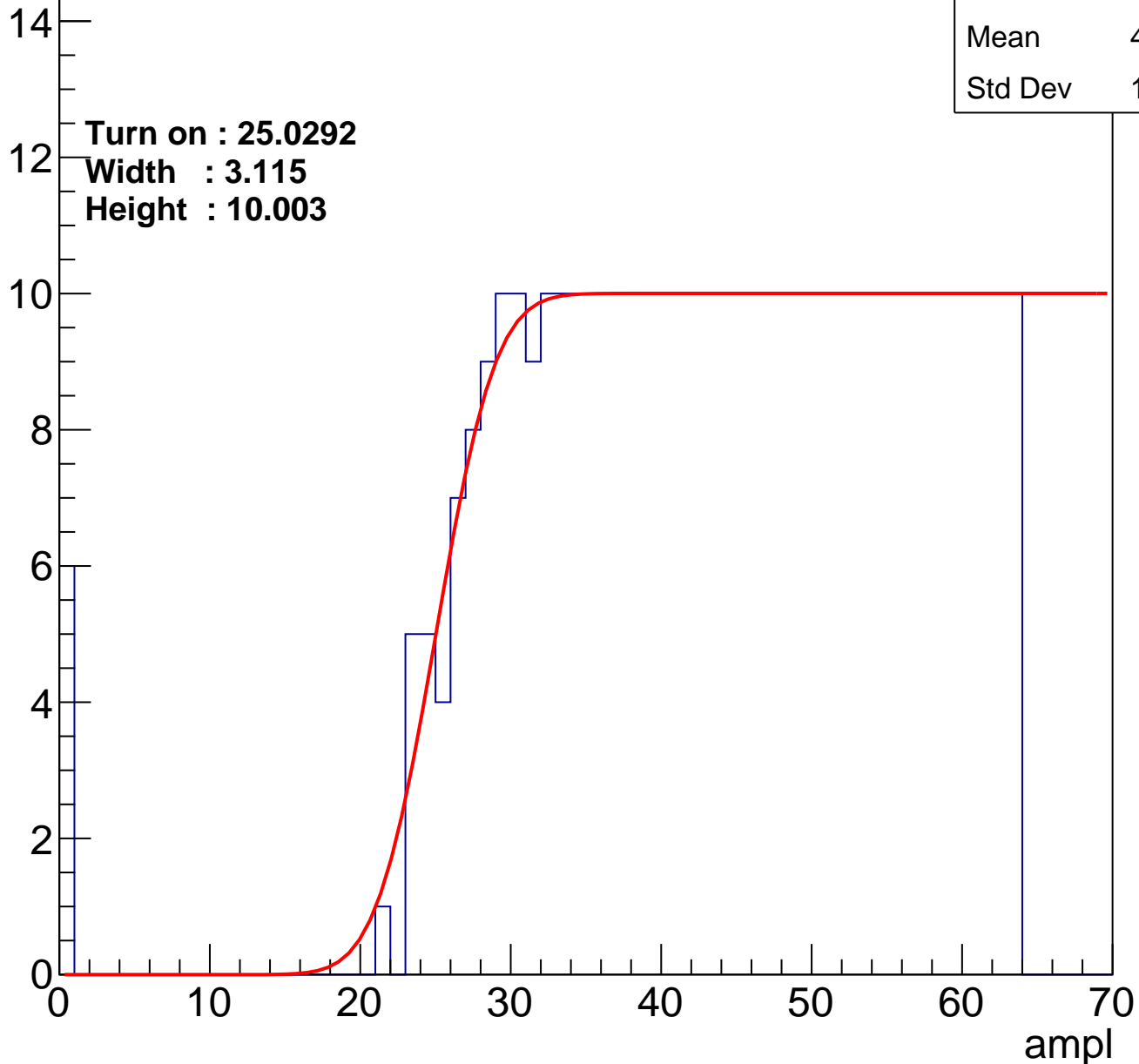
Entries	394
Mean	43.34
Std Dev	12.49

Turn on : 25.0292

Width : 3.115

Height : 10.003

Entry



B1L002S, U26-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.85
Std Dev	12.56

Turn on : 24.1293

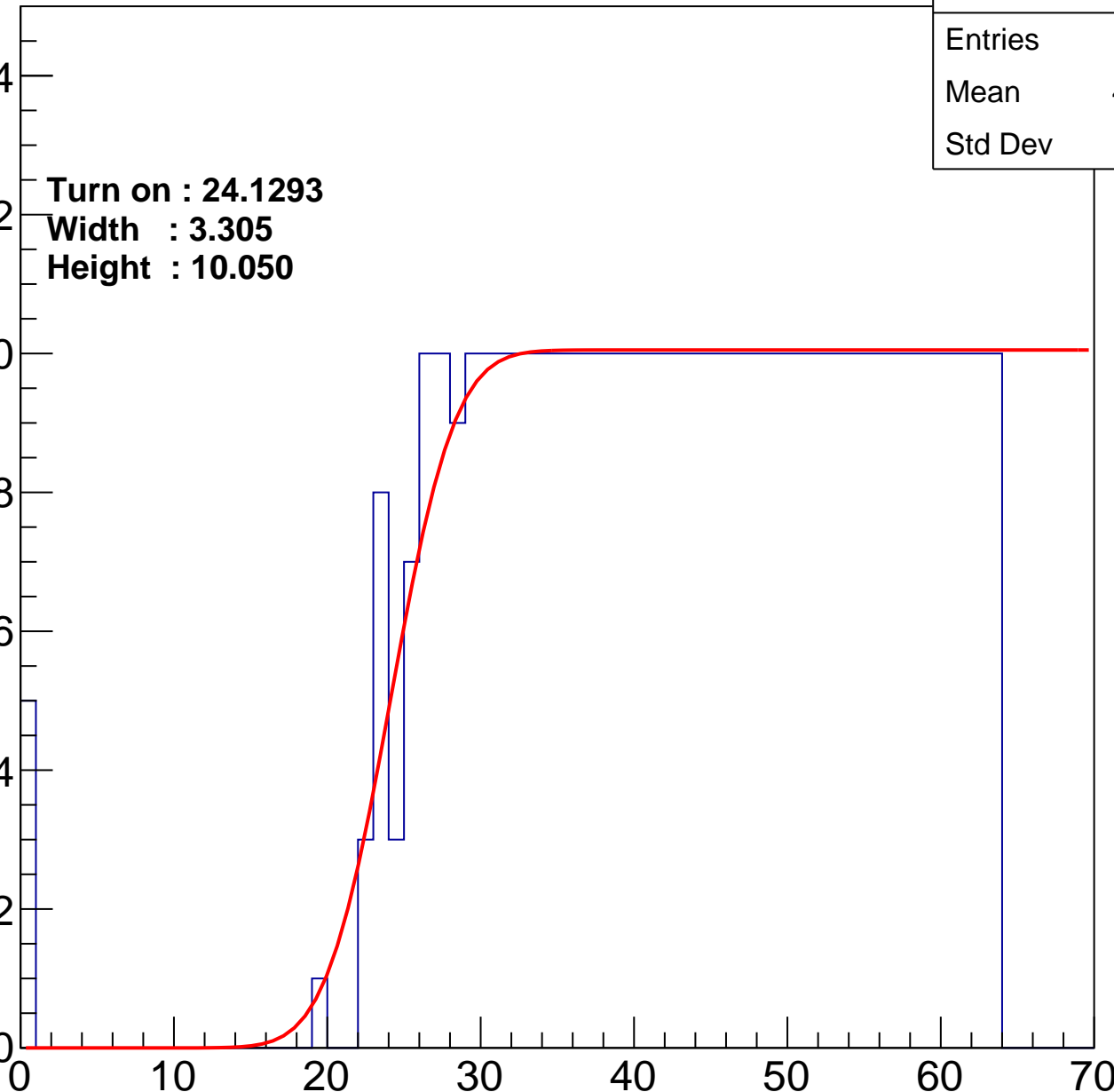
Width : 3.305

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.79
Std Dev	11.62

Turn on : 25.4721

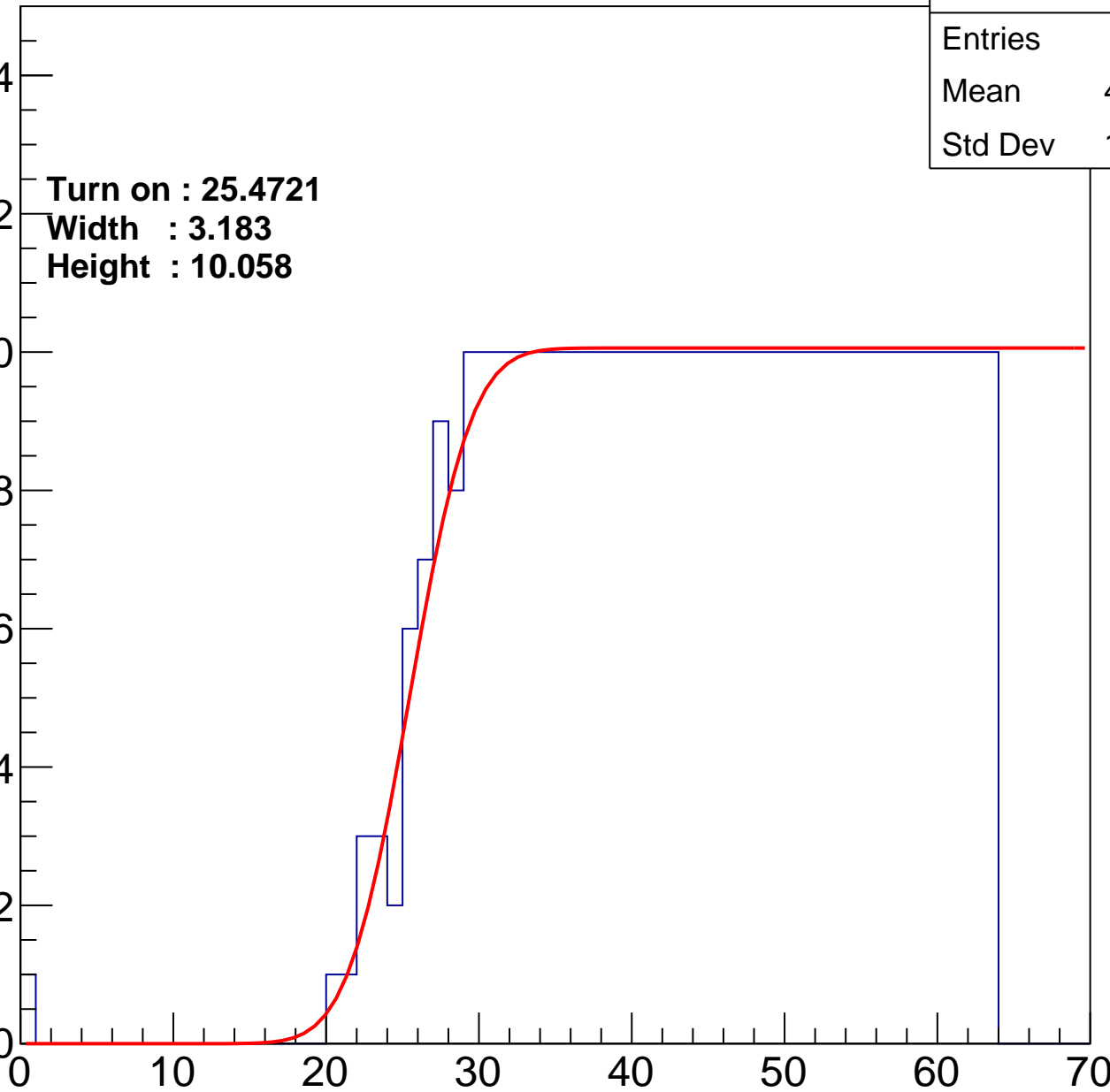
Width : 3.183

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.02
Std Dev	11.77

Turn on : 26.1874

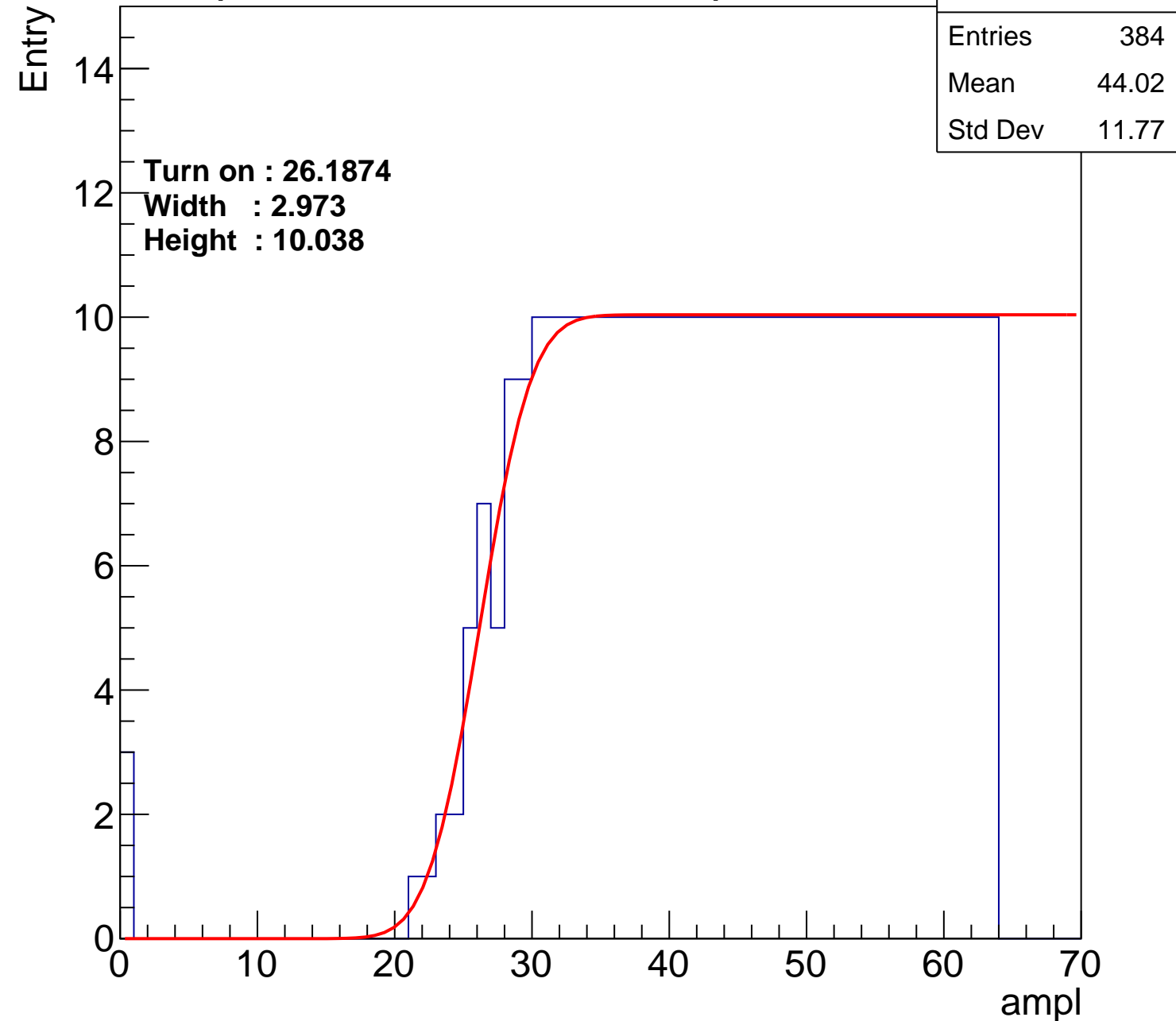
Width : 2.973

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.37
Std Dev	11.44

Turn on : 26.6349

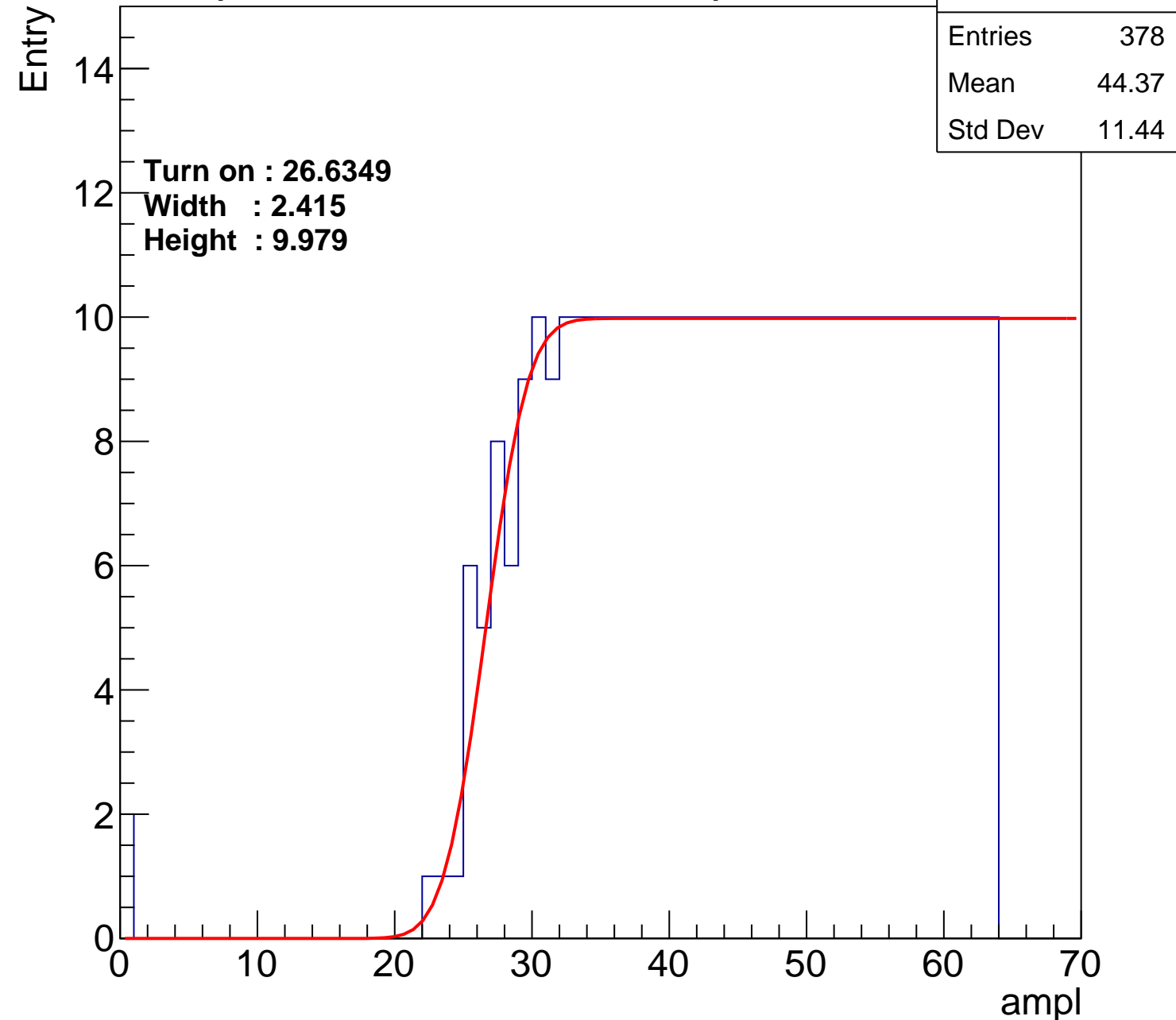
Width : 2.415

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch98

calib_packv5_042523_0143.root, FC#10, port B3

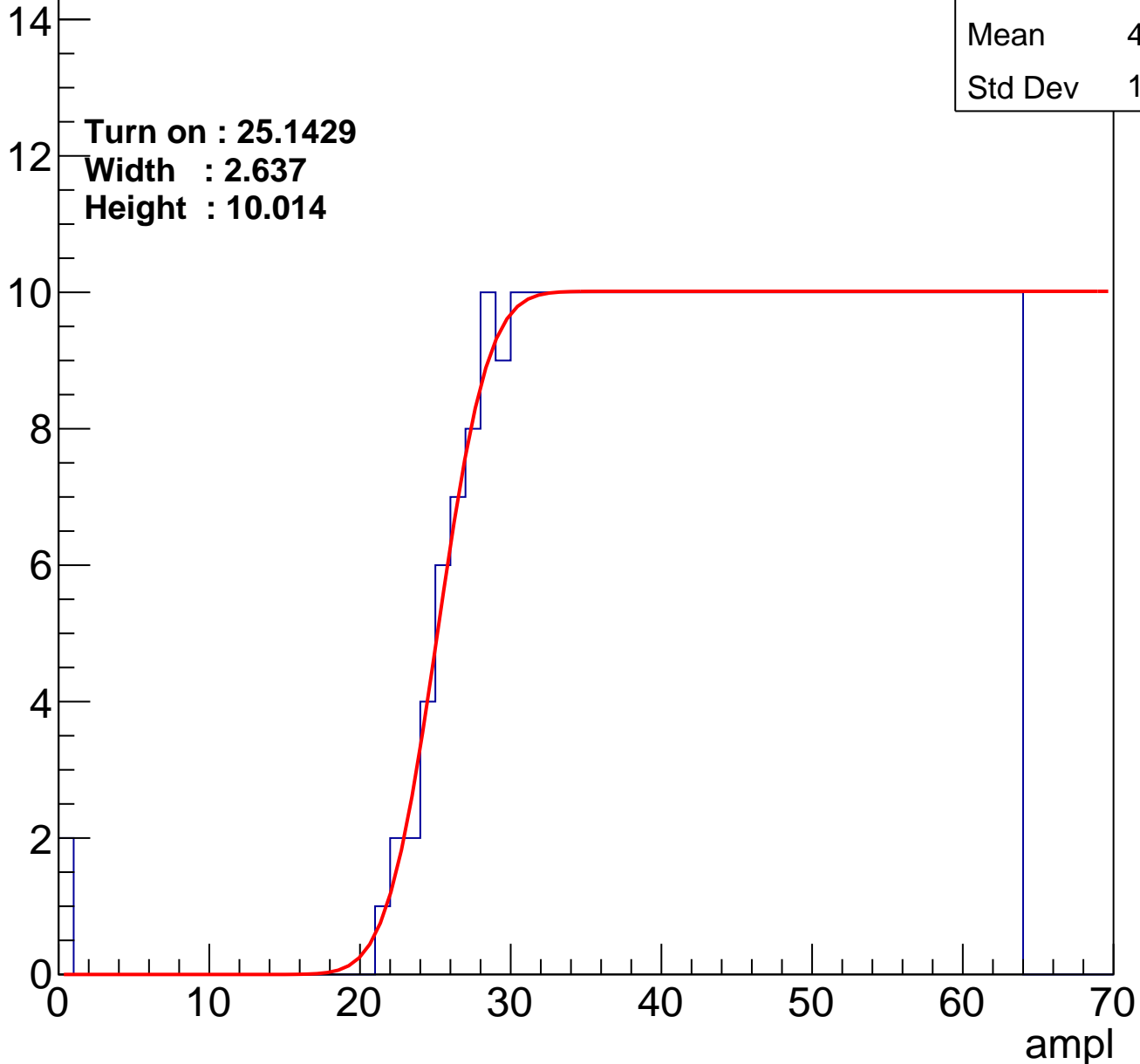
Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.1429

Width : 2.637

Height : 10.014

Entry



B1L002S, U26-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.49
Std Dev	12.03

Turn on : 25.0593

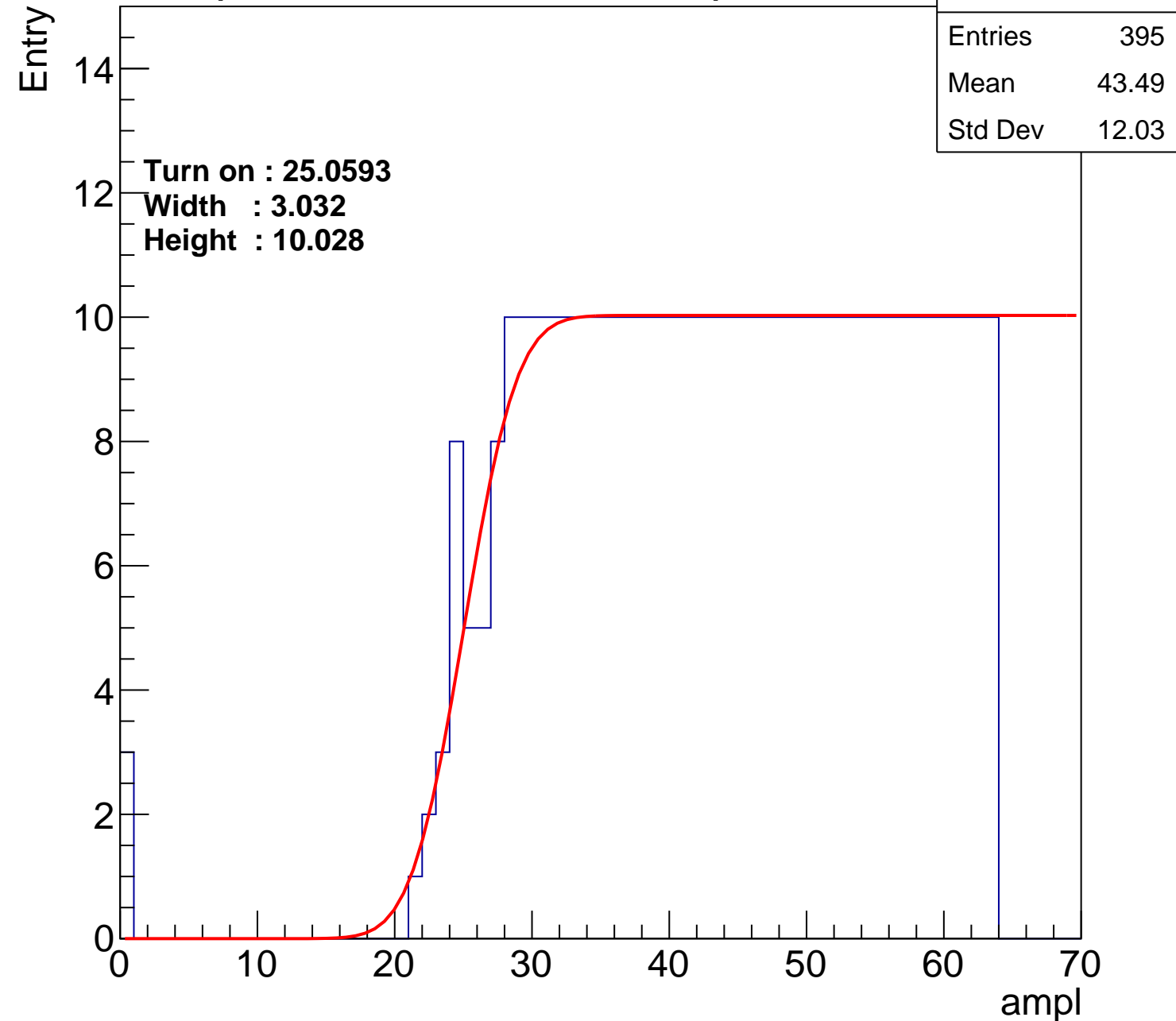
Width : 3.032

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.84
Std Dev	11.09

Turn on : 27.3927

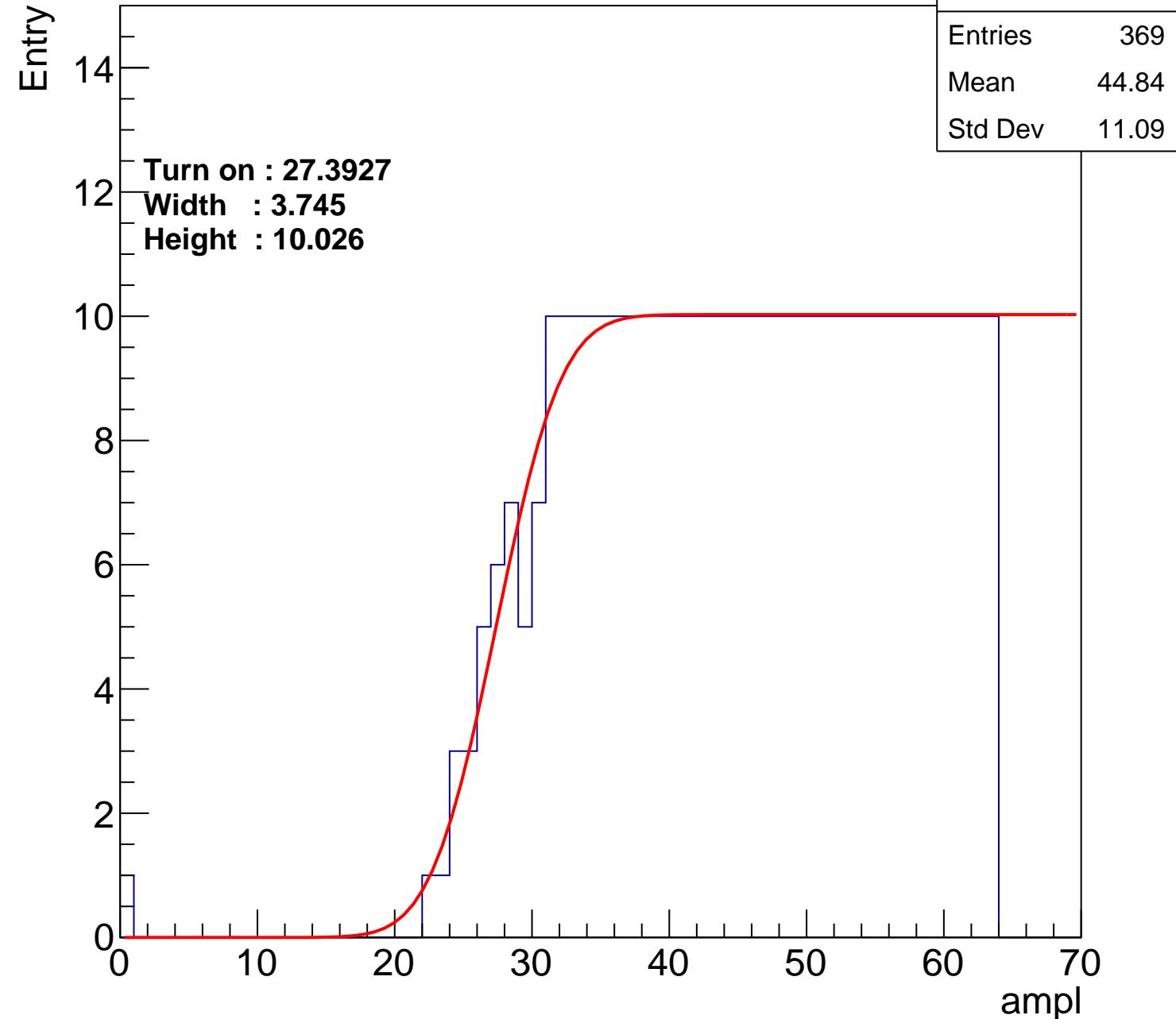
Width : 3.745

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 24.7852

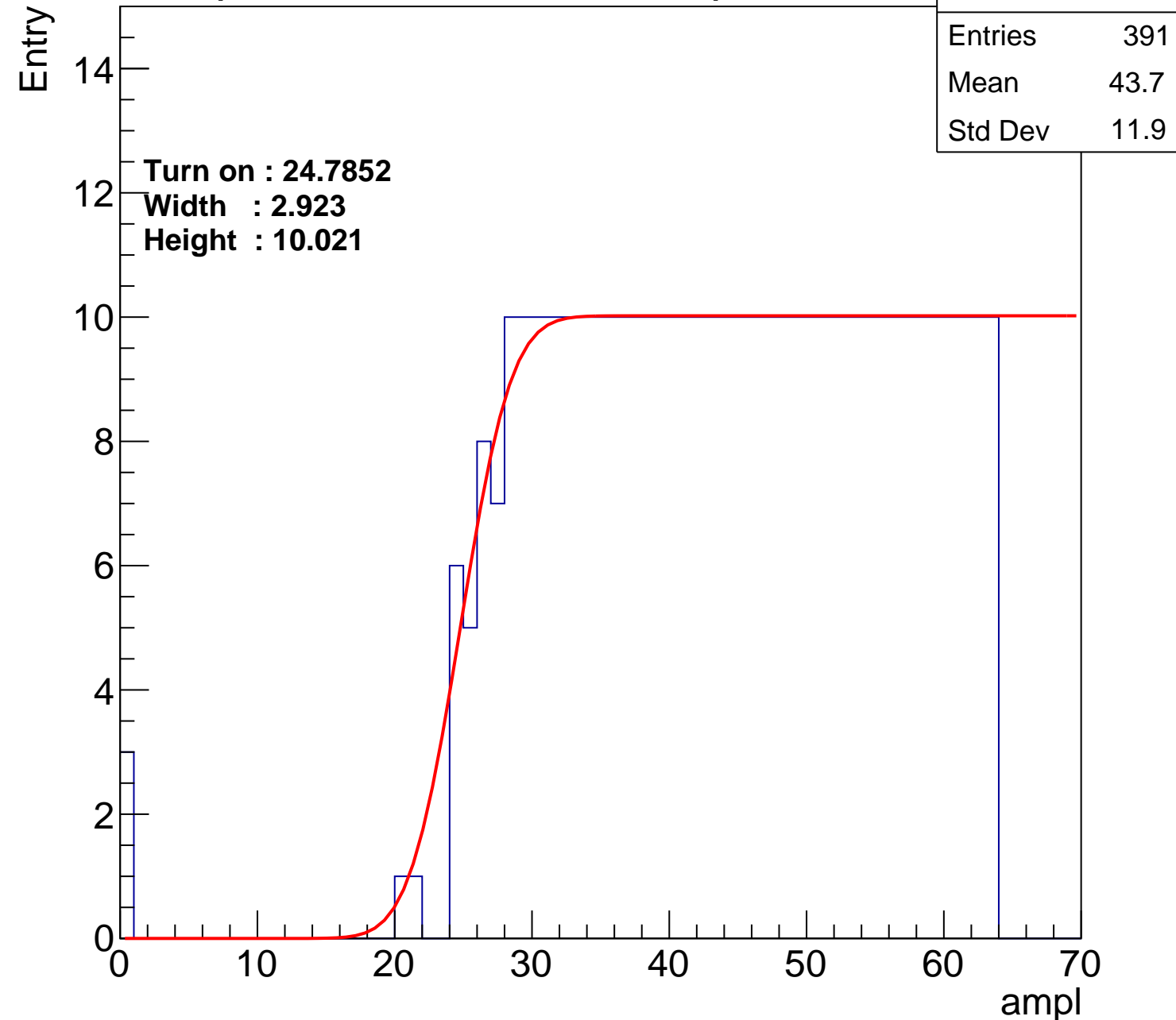
Width : 2.923

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch102

calib_packv5_042523_0143.root, FC#10, port B3

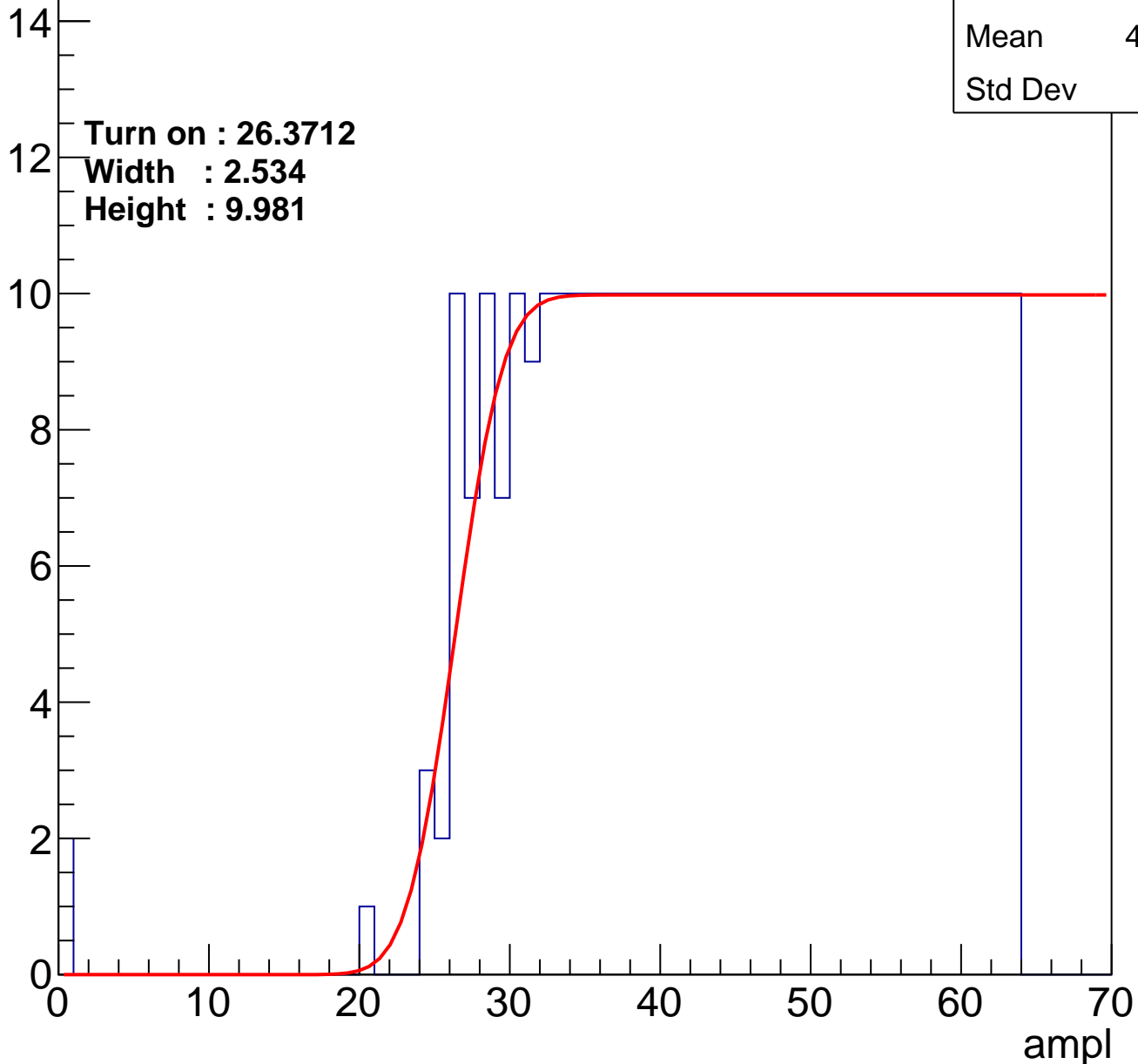
Entries	381
Mean	44.23
Std Dev	11.5

Turn on : 26.3712

Width : 2.534

Height : 9.981

Entry



B1L002S, U26-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.78
Std Dev	11.99

Turn on : 25.6854

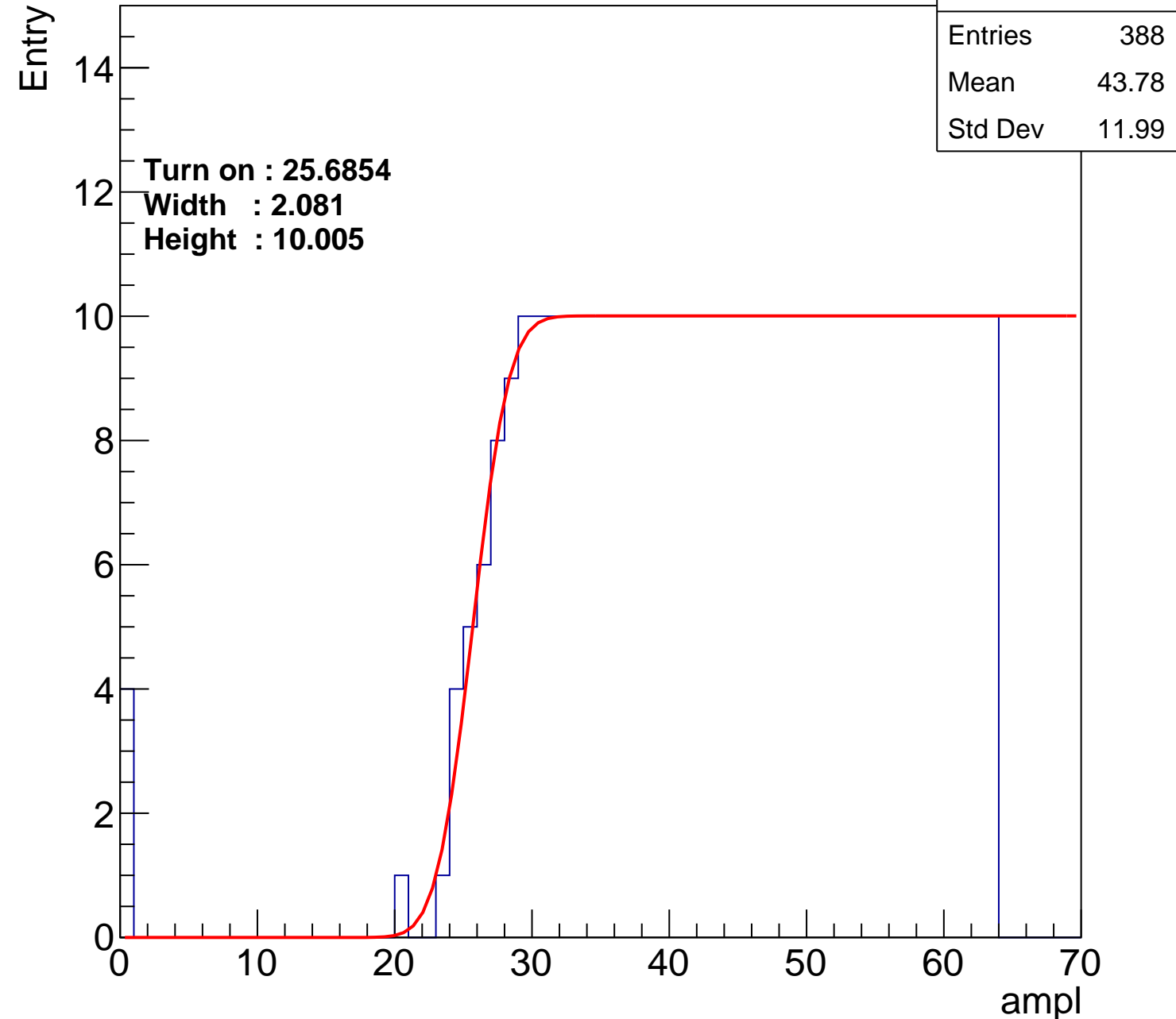
Width : 2.081

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch104

calib_packv5_042523_0143.root, FC#10, port B3

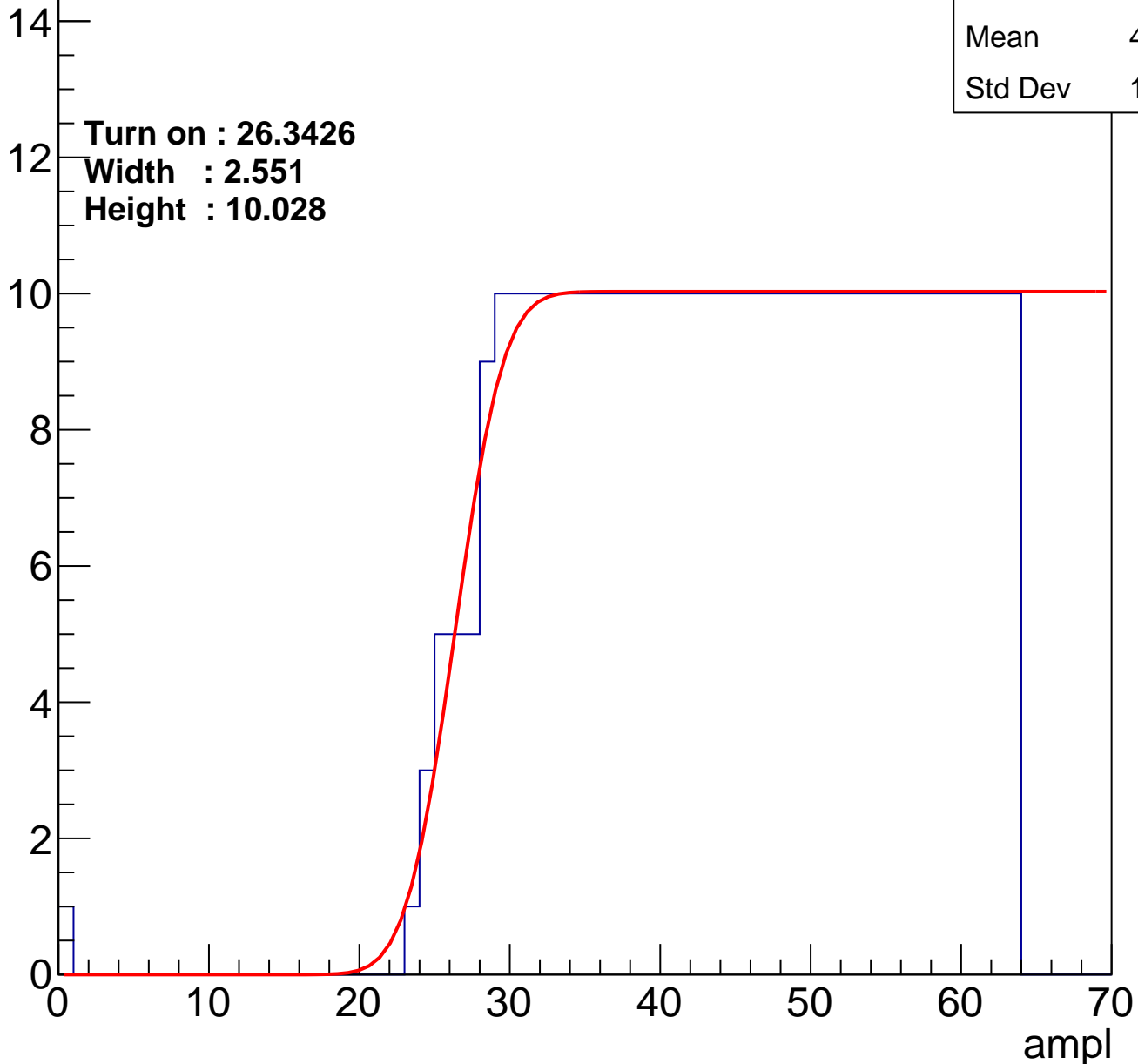
Entries	379
Mean	44.42
Std Dev	11.23

Turn on : 26.3426

Width : 2.551

Height : 10.028

Entry



B1L002S, U26-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.3
Std Dev	11.32

Turn on : 25.6858

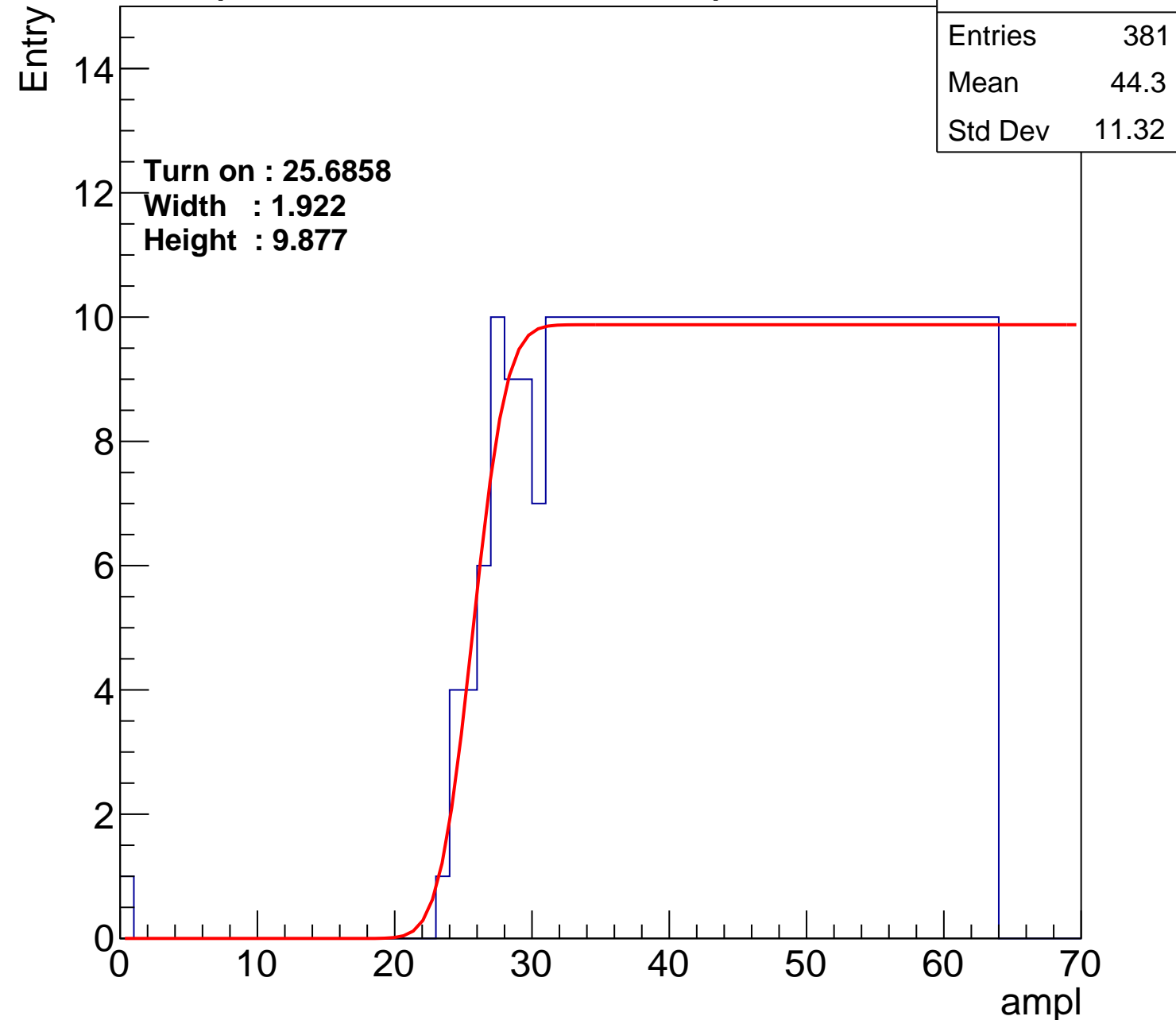
Width : 1.922

Height : 9.877

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.19
Std Dev	12.18

Turn on : 24.3784

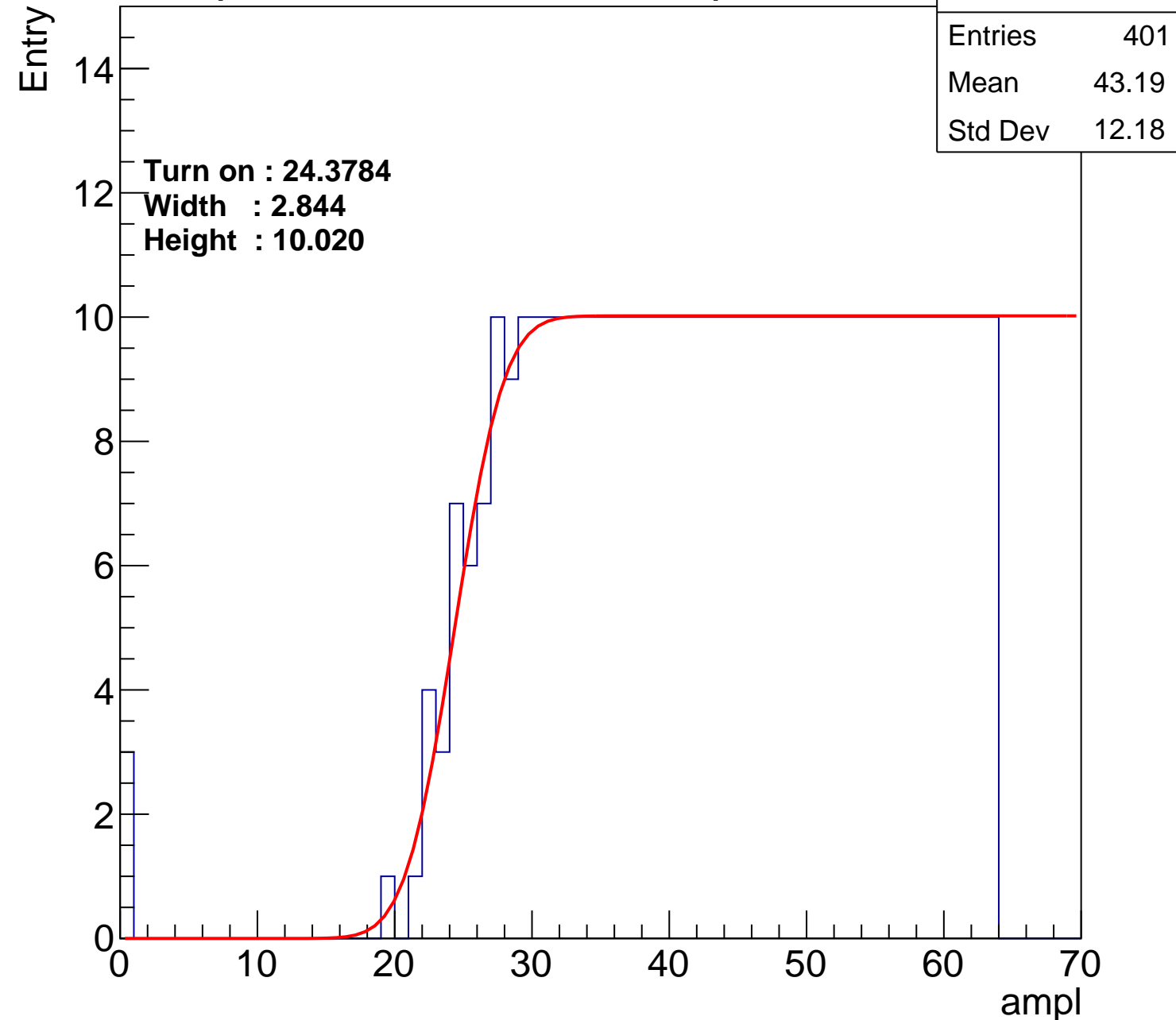
Width : 2.844

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.84
Std Dev	10.99

Turn on : 27.3575

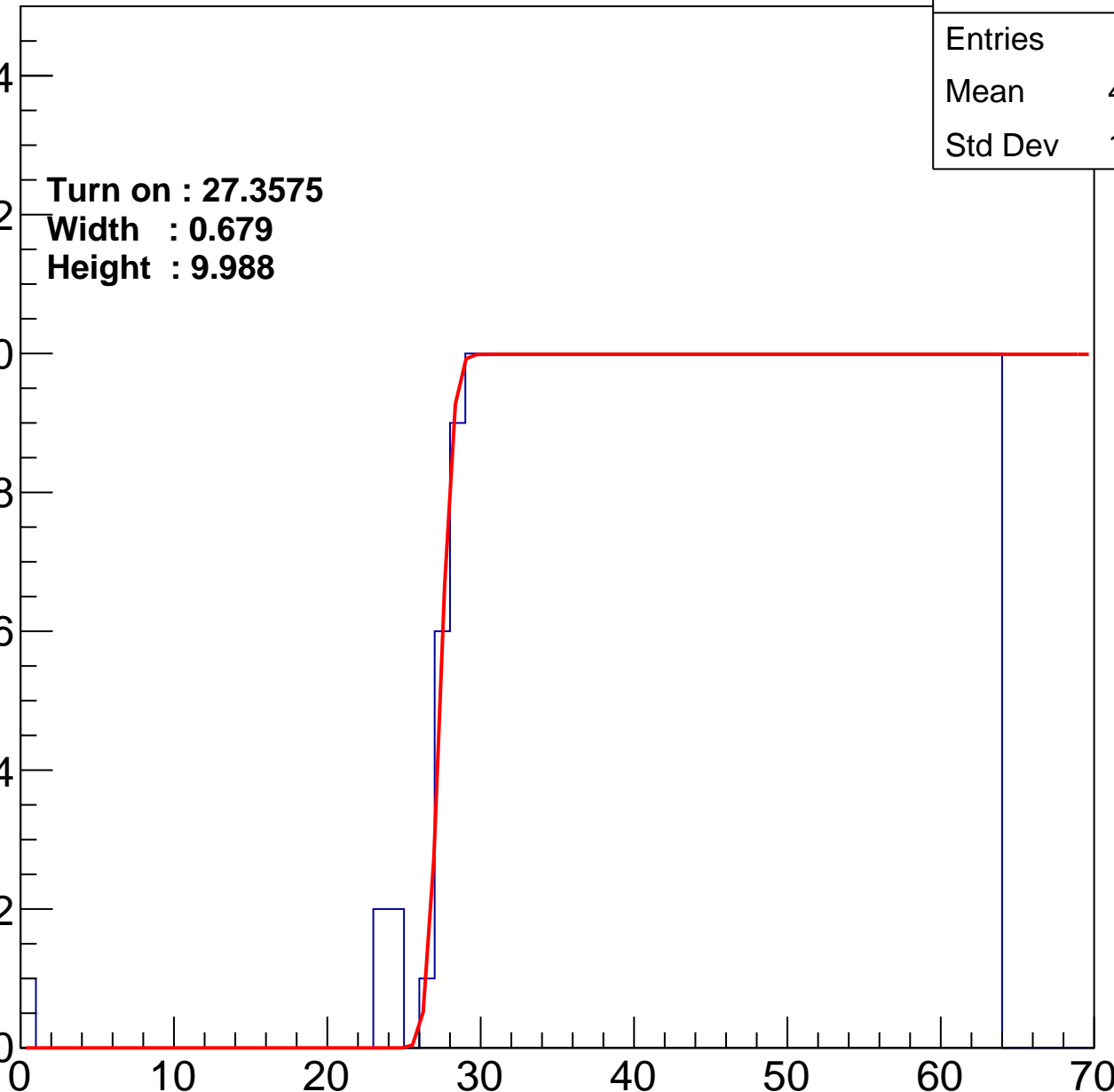
Width : 0.679

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.89
Std Dev	11.56

Turn on : 25.6556

Width : 2.814

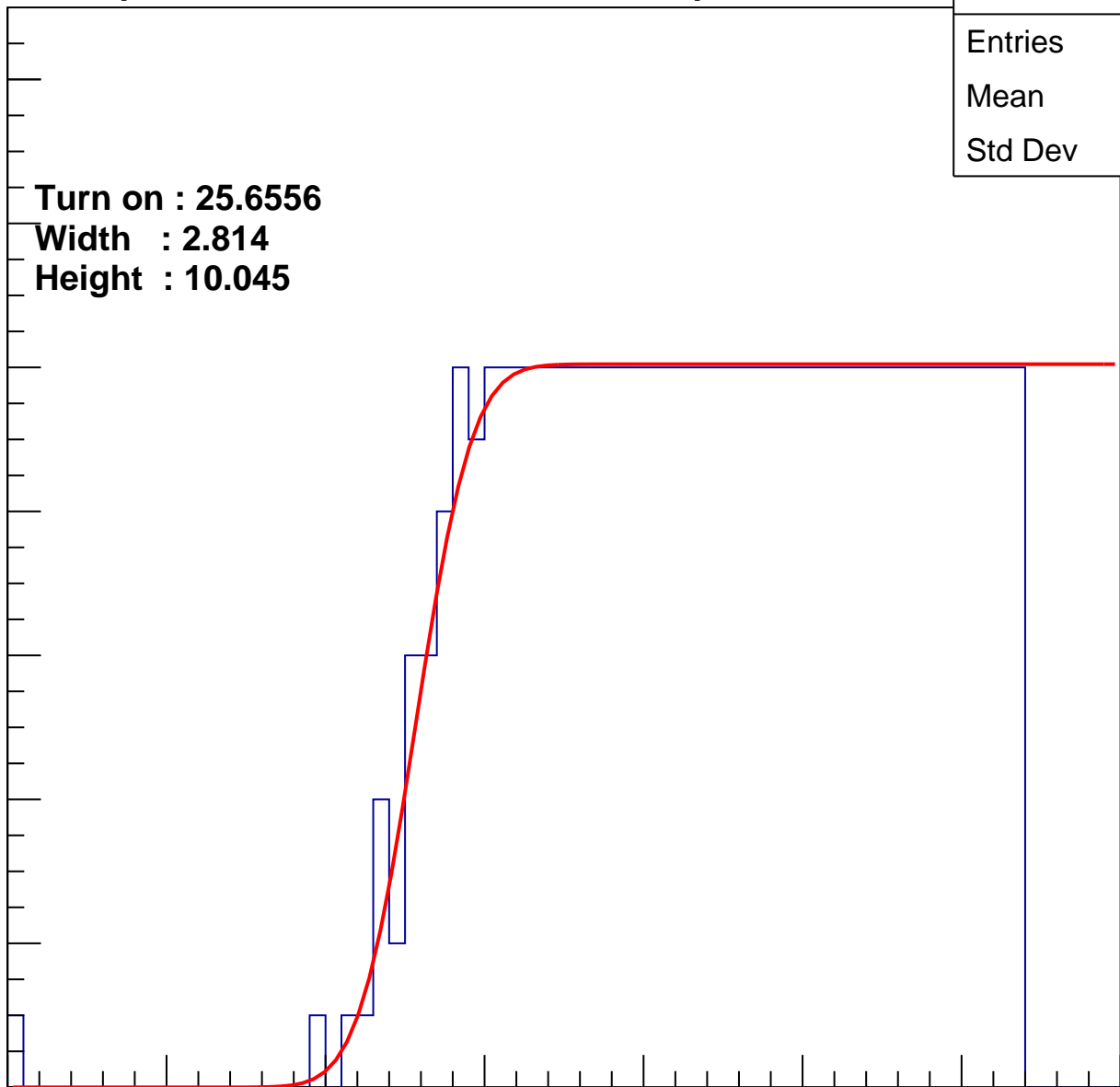
Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U26-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 26.9830

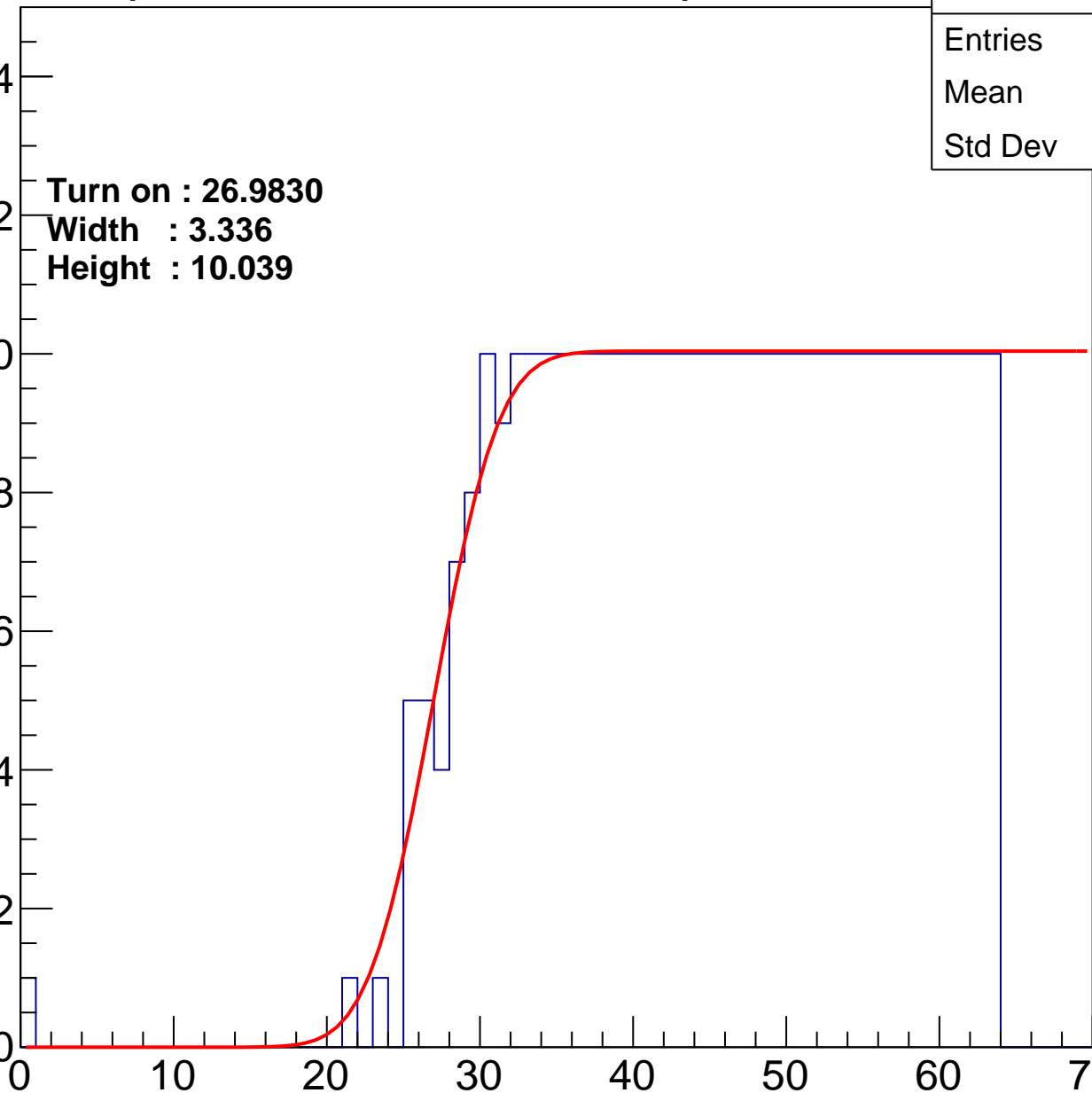
Width : 3.336

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch110

calib_packv5_042523_0143.root, FC#10, port B3

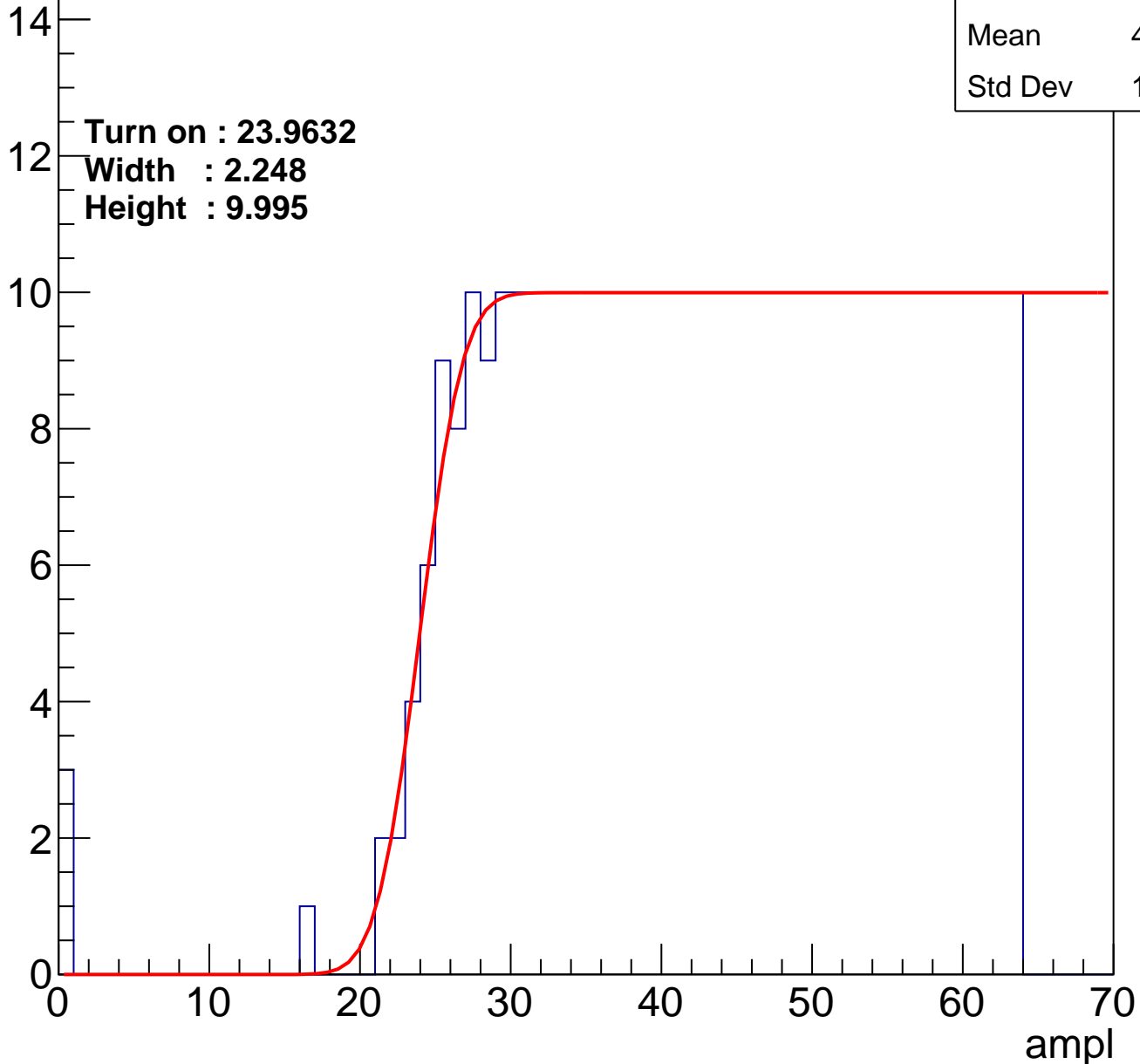
Entries	404
Mean	43.05
Std Dev	12.24

Turn on : 23.9632

Width : 2.248

Height : 9.995

Entry



B1L002S, U26-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.4114

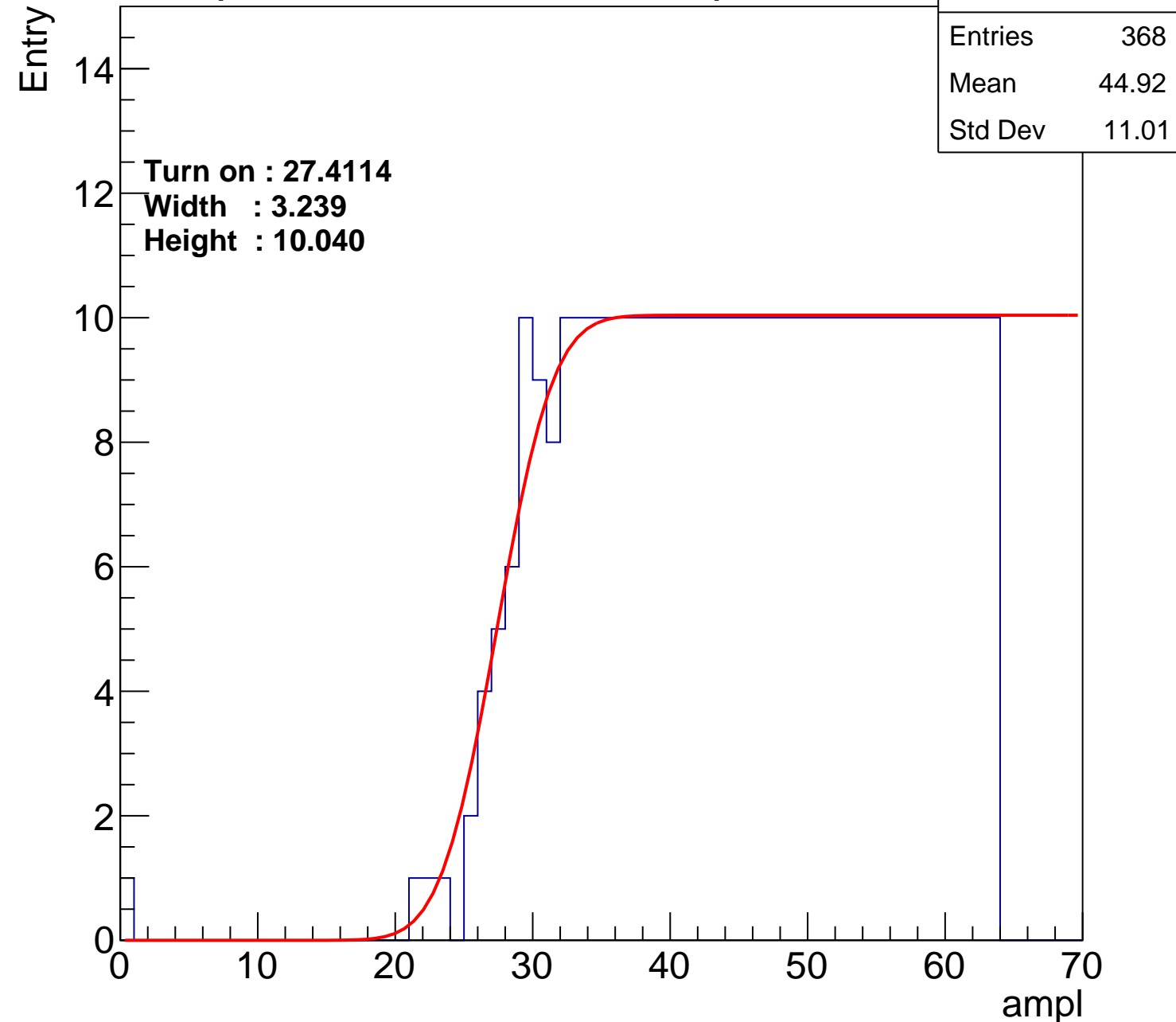
Width : 3.239

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.59
Std Dev	12.01

Turn on : 25.5293

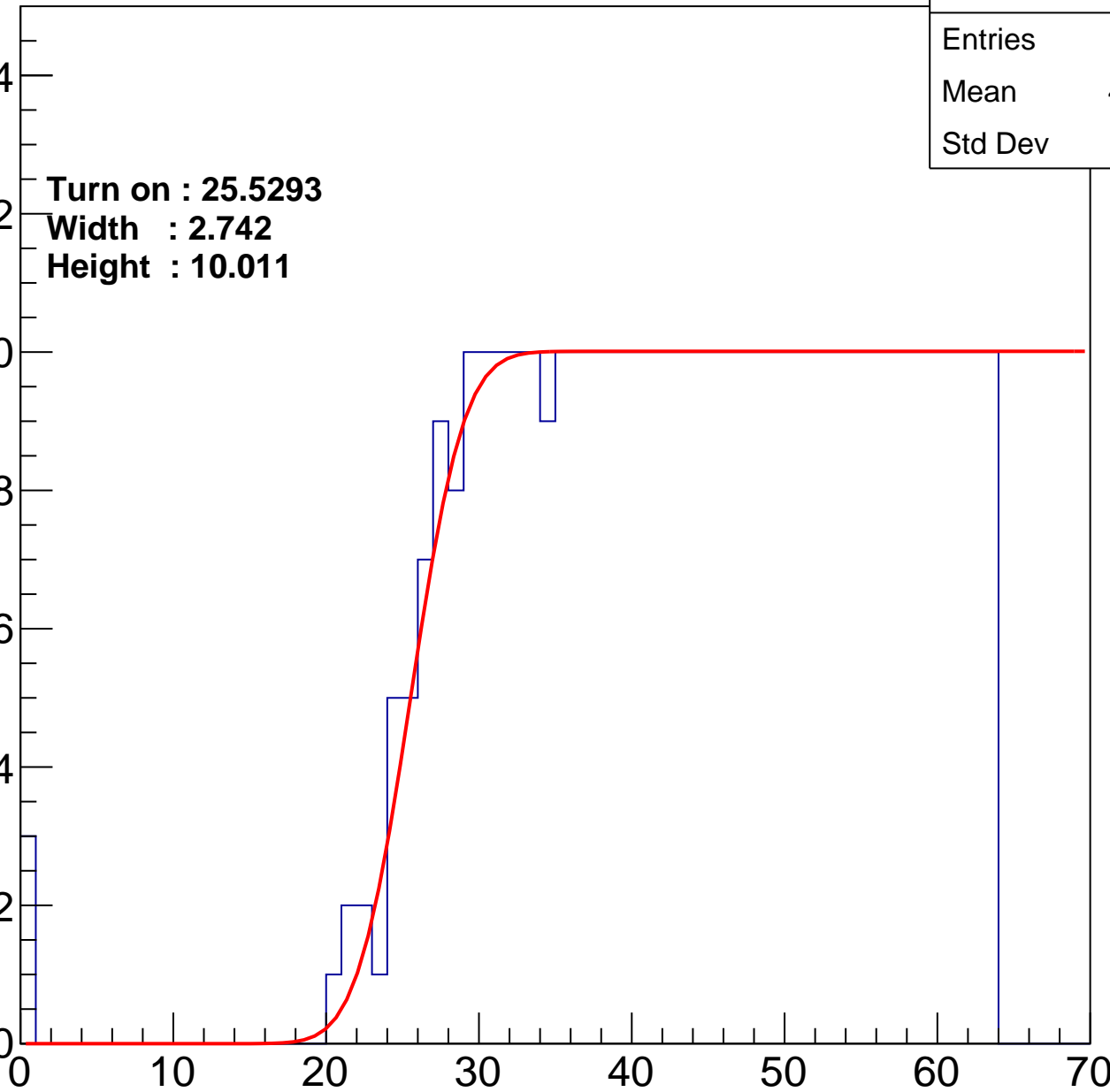
Width : 2.742

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch113

calib_packv5_042523_0143.root, FC#10, port B3

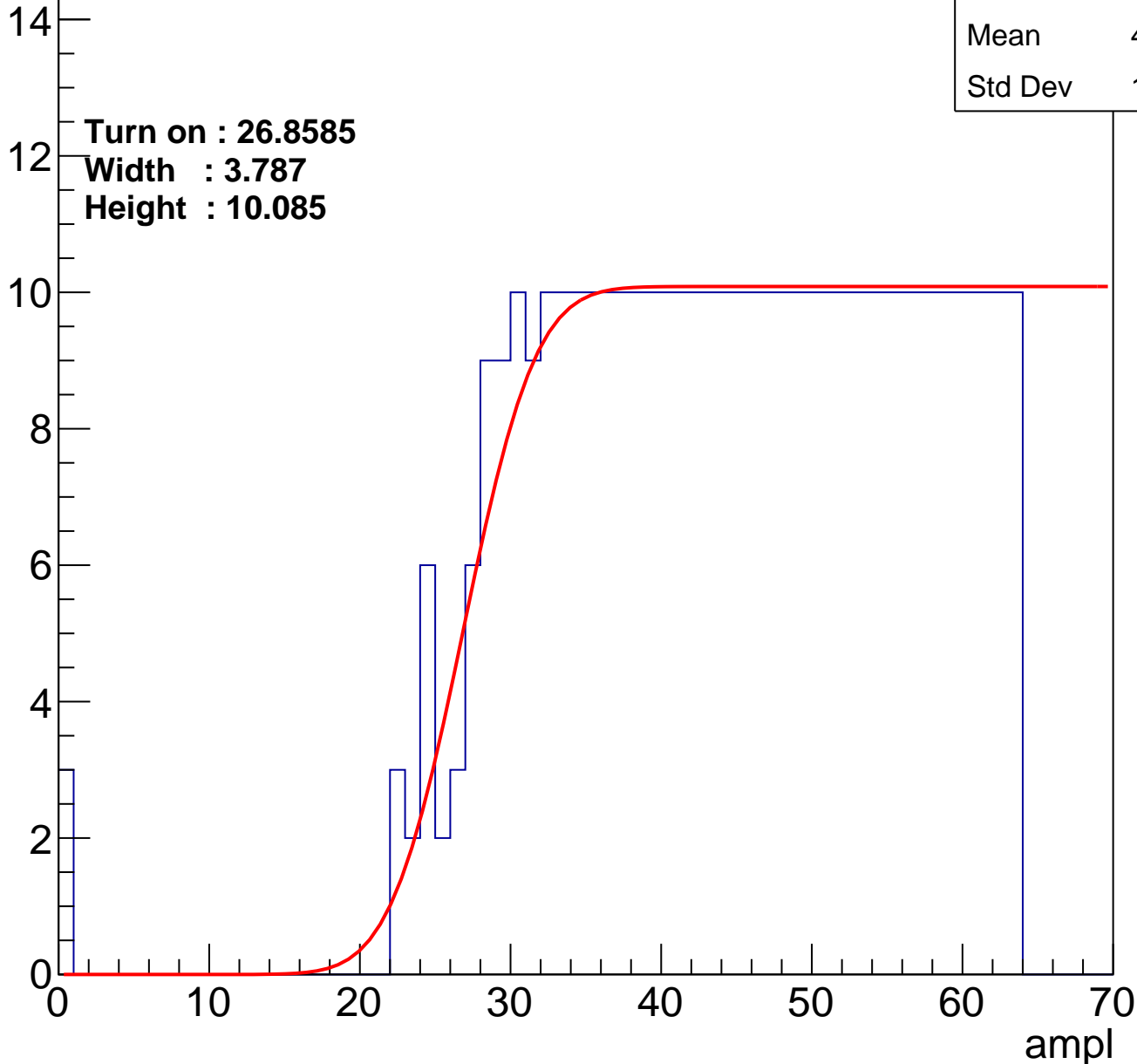
Entries	382
Mean	44.08
Std Dev	11.78

Turn on : 26.8585

Width : 3.787

Height : 10.085

Entry



B1L002S, U26-ch114

calib_packv5_042523_0143.root, FC#10, port B3

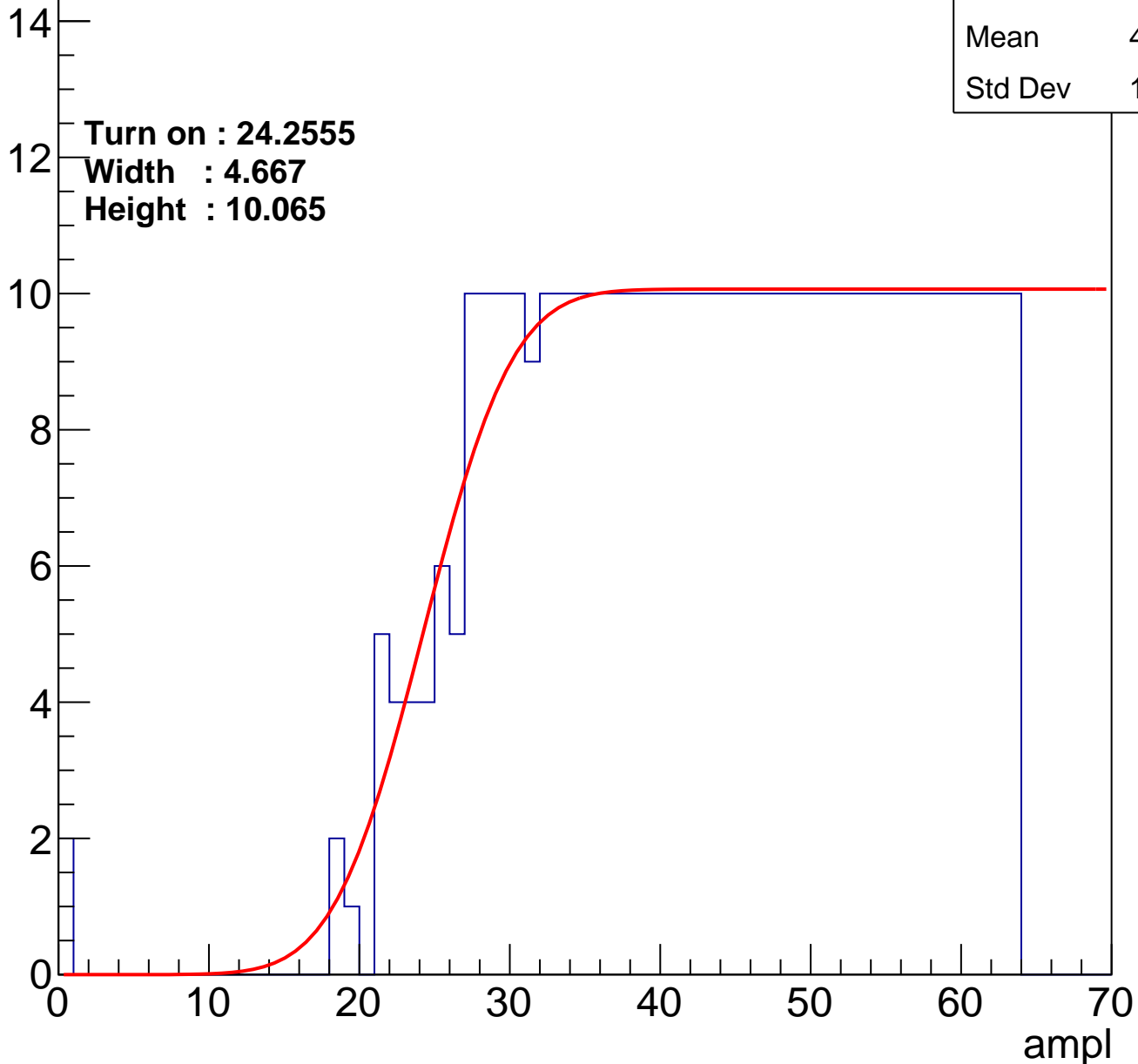
Entries	402
Mean	43.12
Std Dev	12.18

Turn on : 24.2555

Width : 4.667

Height : 10.065

Entry



B1L002S, U26-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.52
Std Dev	12.04

Turn on : 25.3649

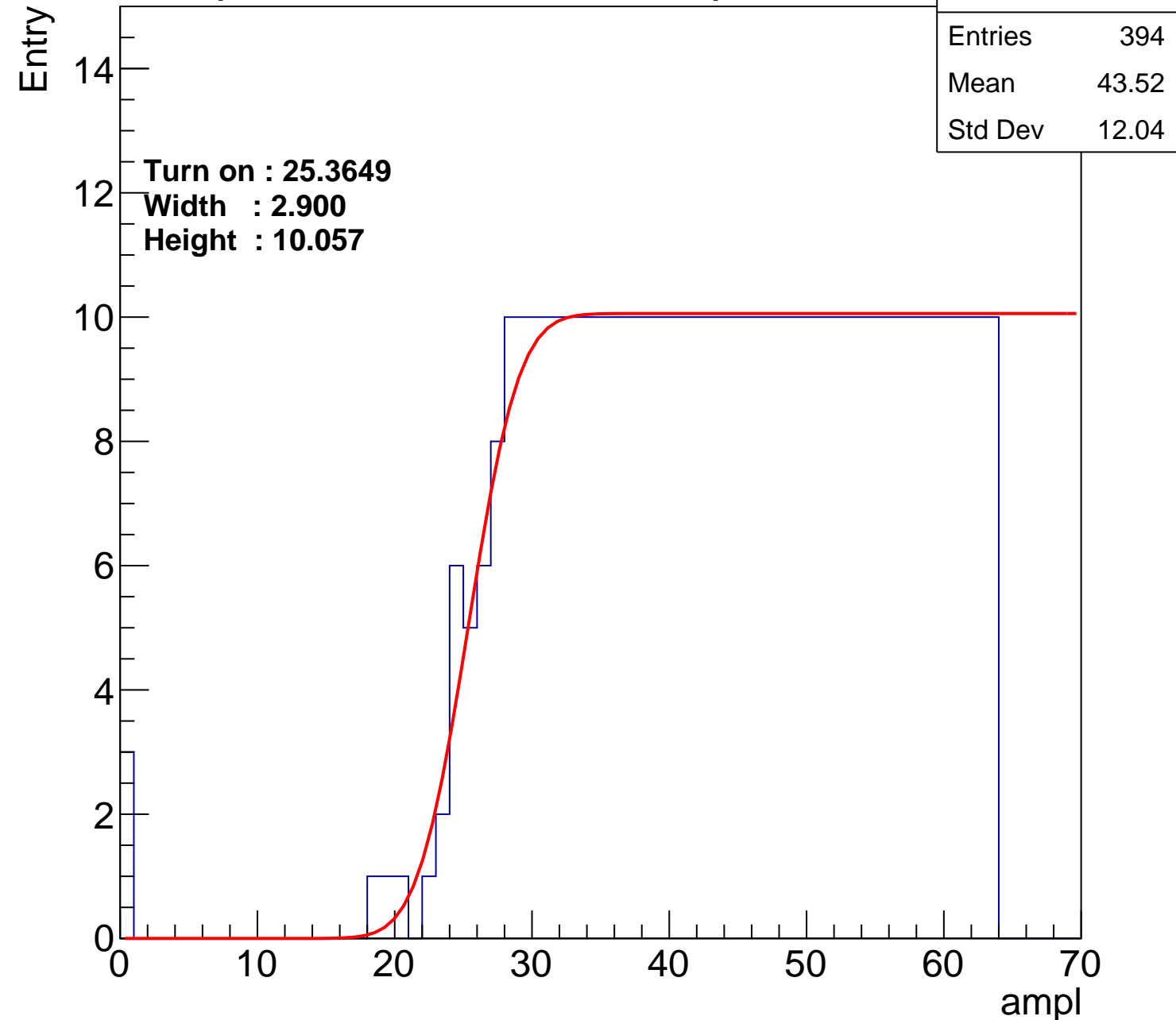
Width : 2.900

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.83
Std Dev	11.61

Turn on : 25.7840

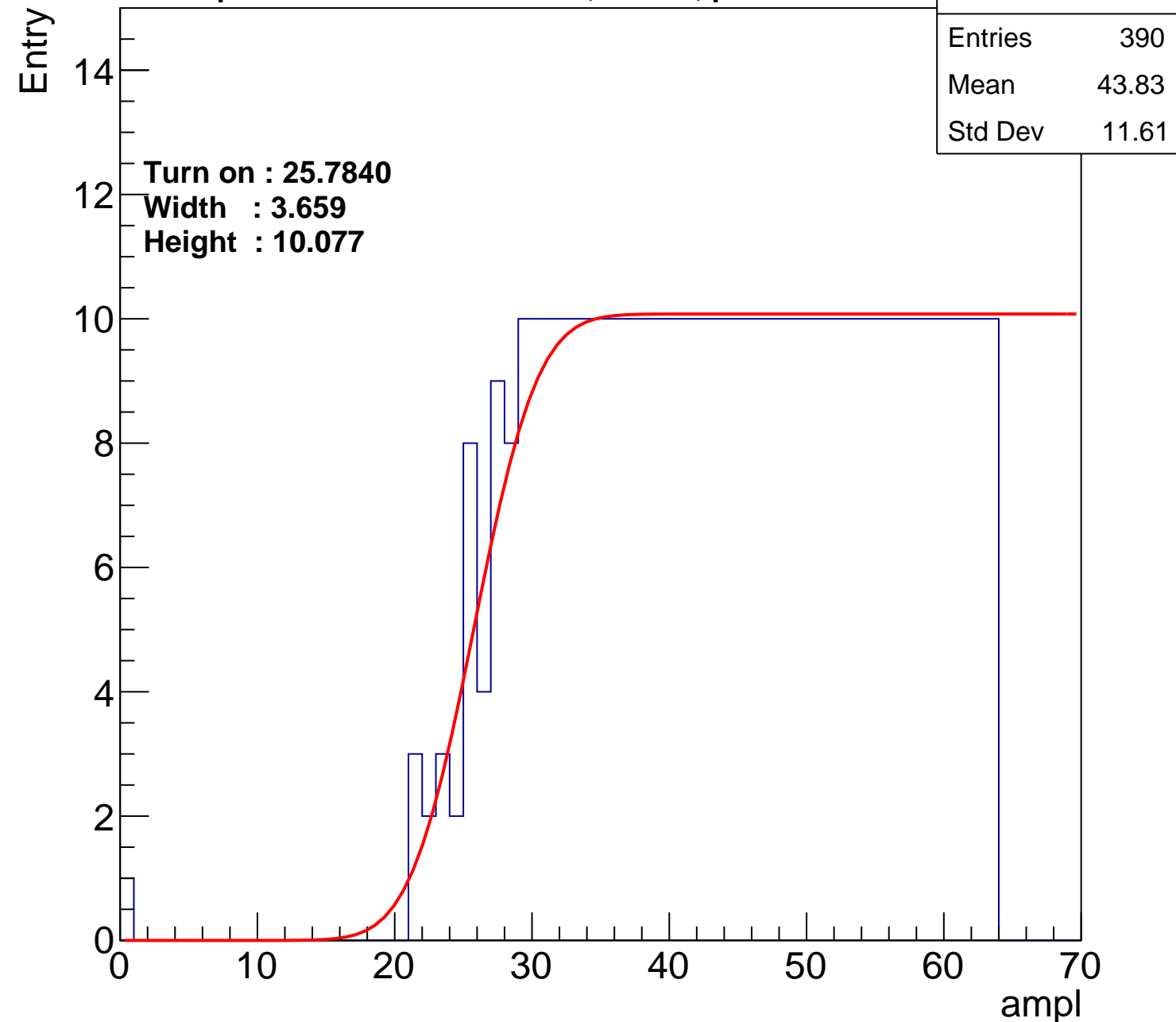
Width : 3.659

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.7105

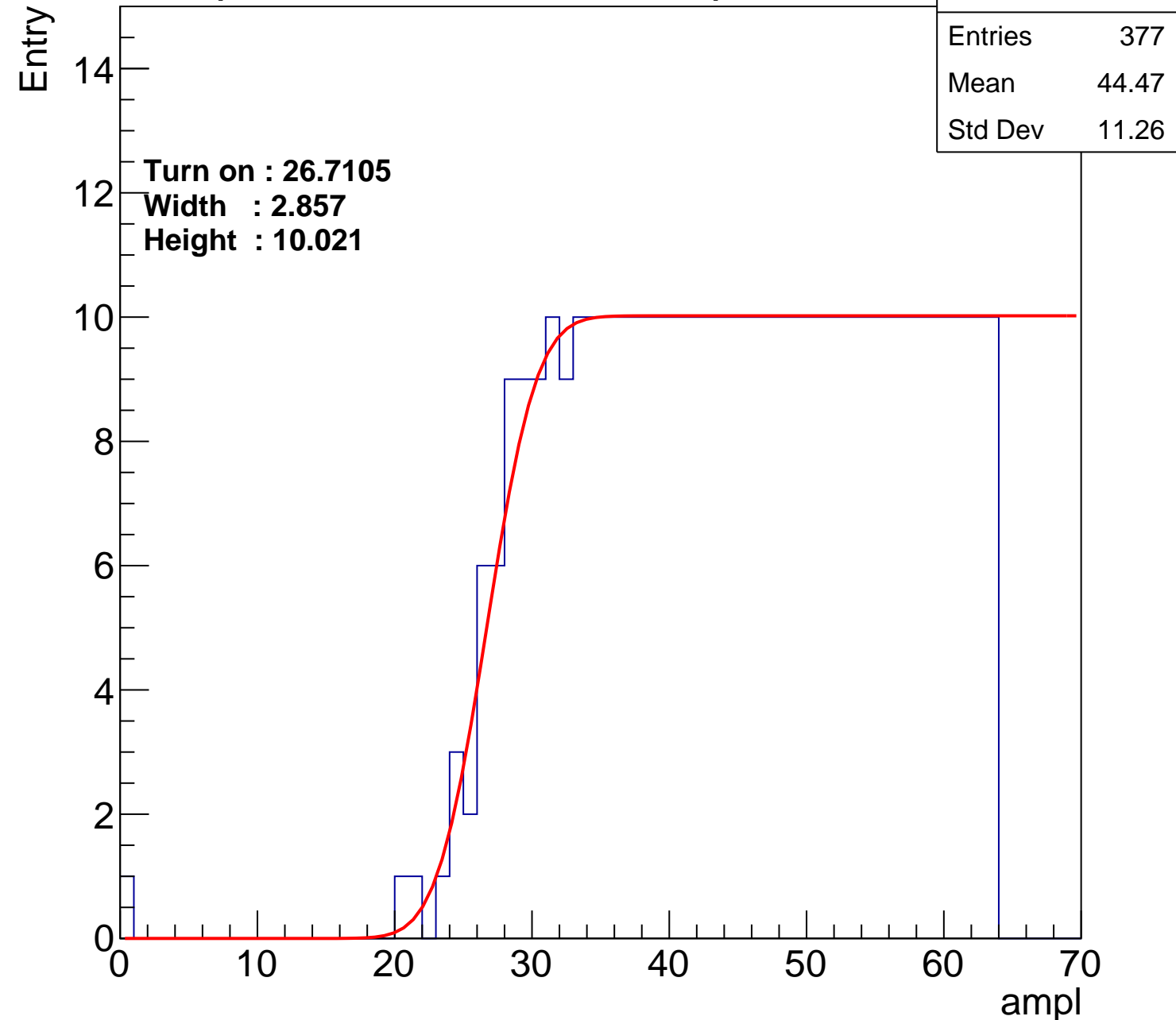
Width : 2.857

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch118

calib_packv5_042523_0143.root, FC#10, port B3

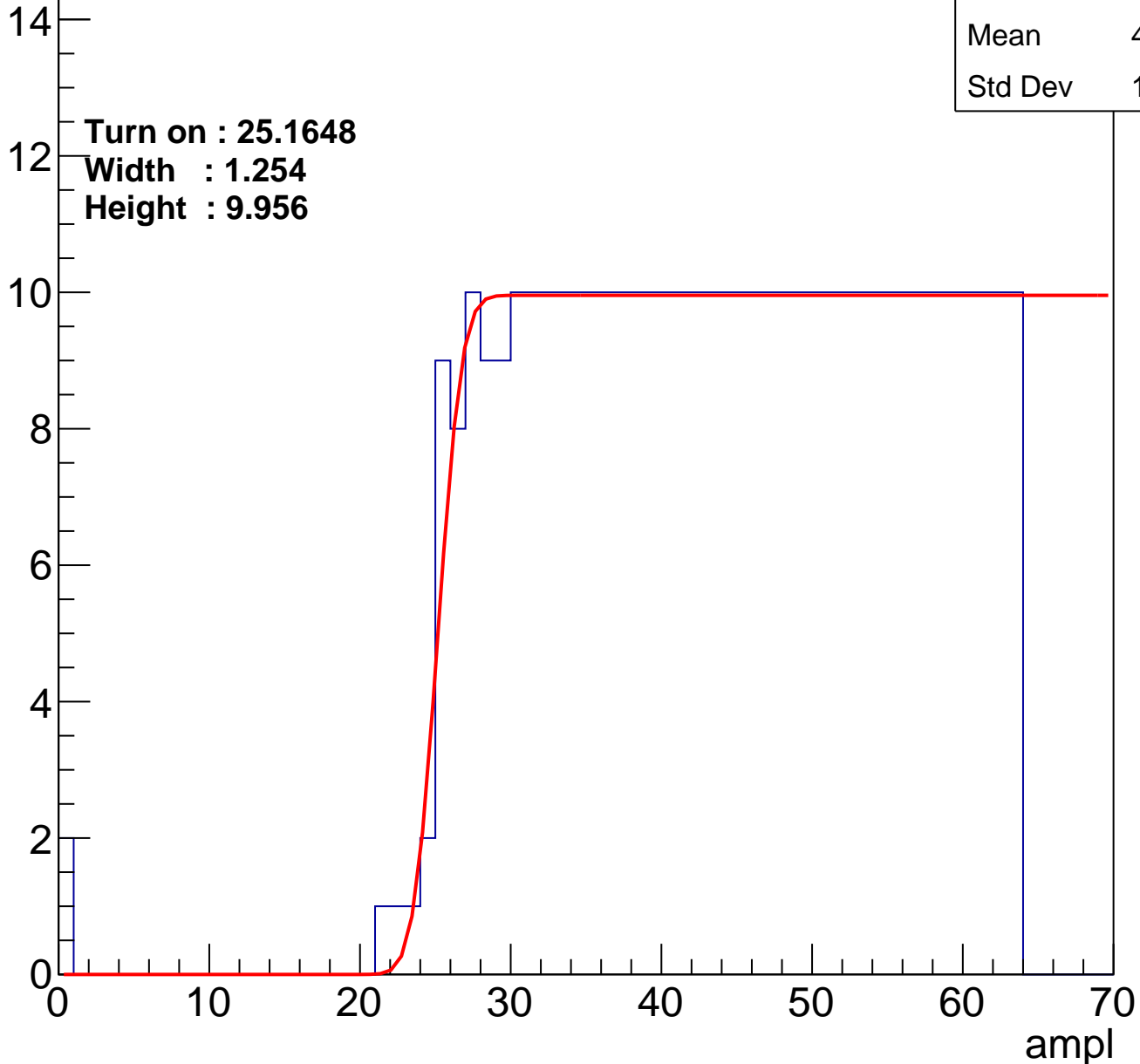
Entries	392
Mean	43.72
Std Dev	11.74

Turn on : 25.1648

Width : 1.254

Height : 9.956

Entry



B1L002S, U26-ch119

calib_packv5_042523_0143.root, FC#10, port B3

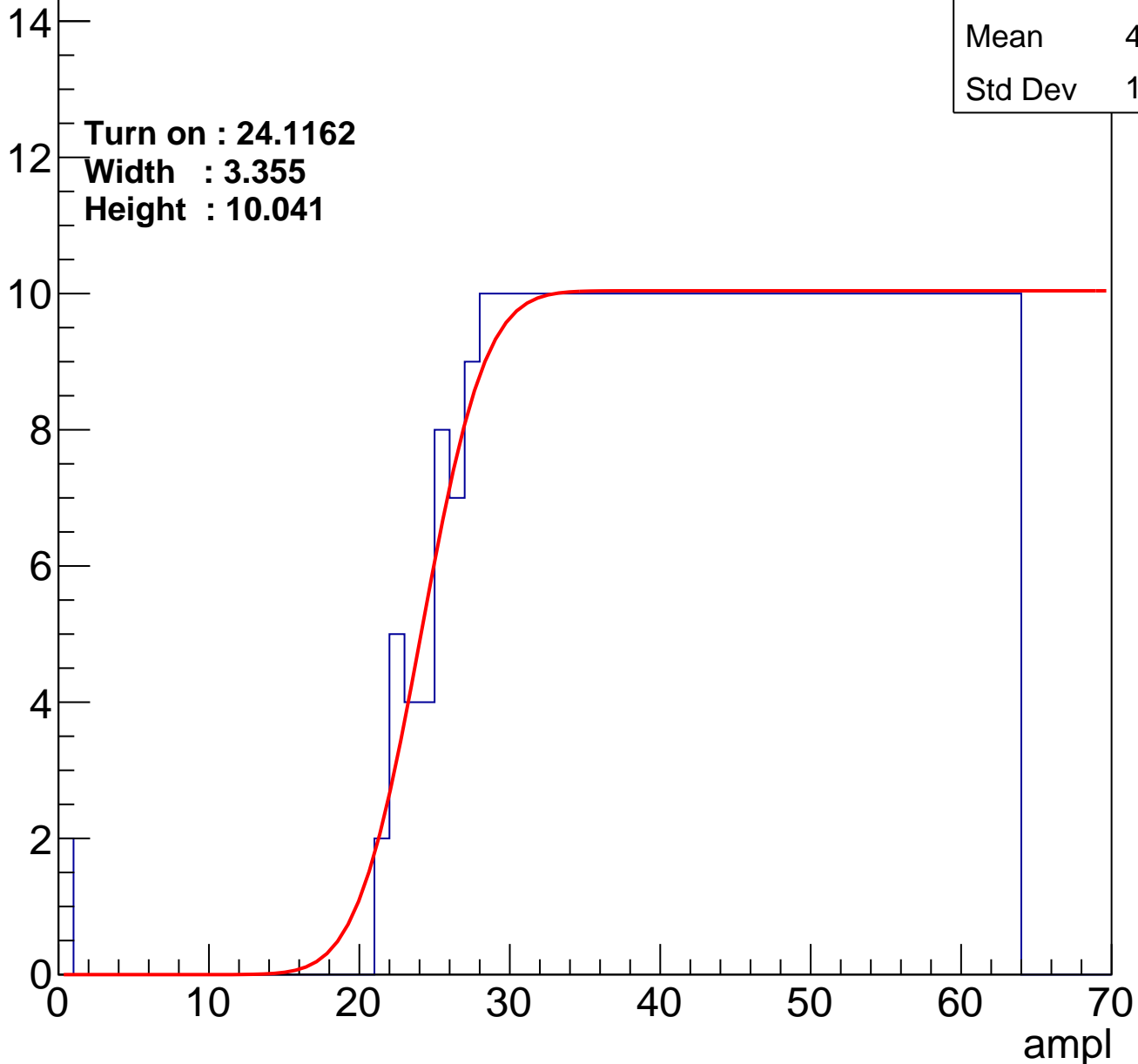
Entries	401
Mean	43.25
Std Dev	12.02

Turn on : 24.1162

Width : 3.355

Height : 10.041

Entry



B1L002S, U26-ch120

calib_packv5_042523_0143.root, FC#10, port B3

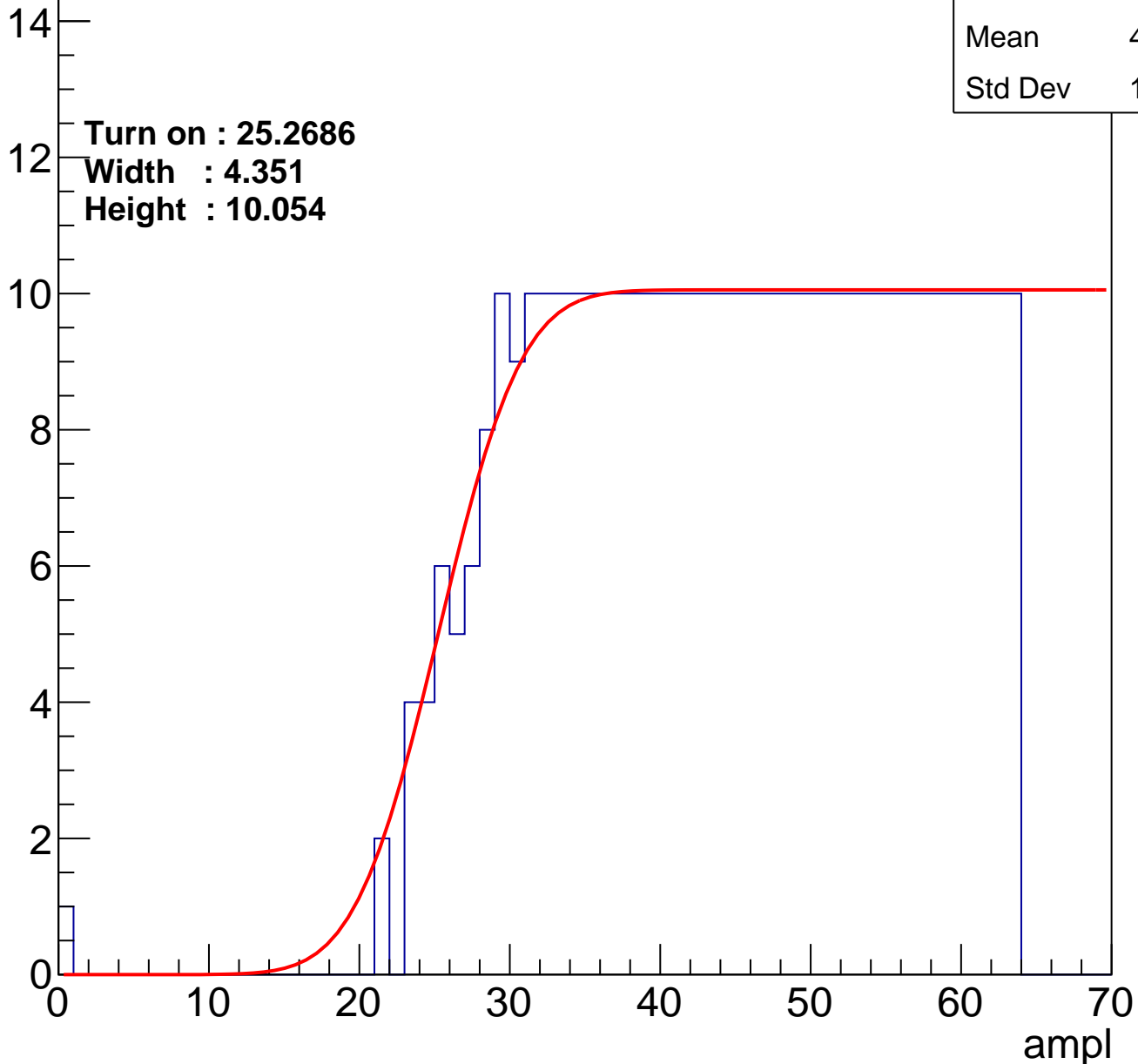
Entries	385
Mean	44.07
Std Dev	11.49

Turn on : 25.2686

Width : 4.351

Height : 10.054

Entry



B1L002S, U26-ch121

calib_packv5_042523_0143.root, FC#10, port B3

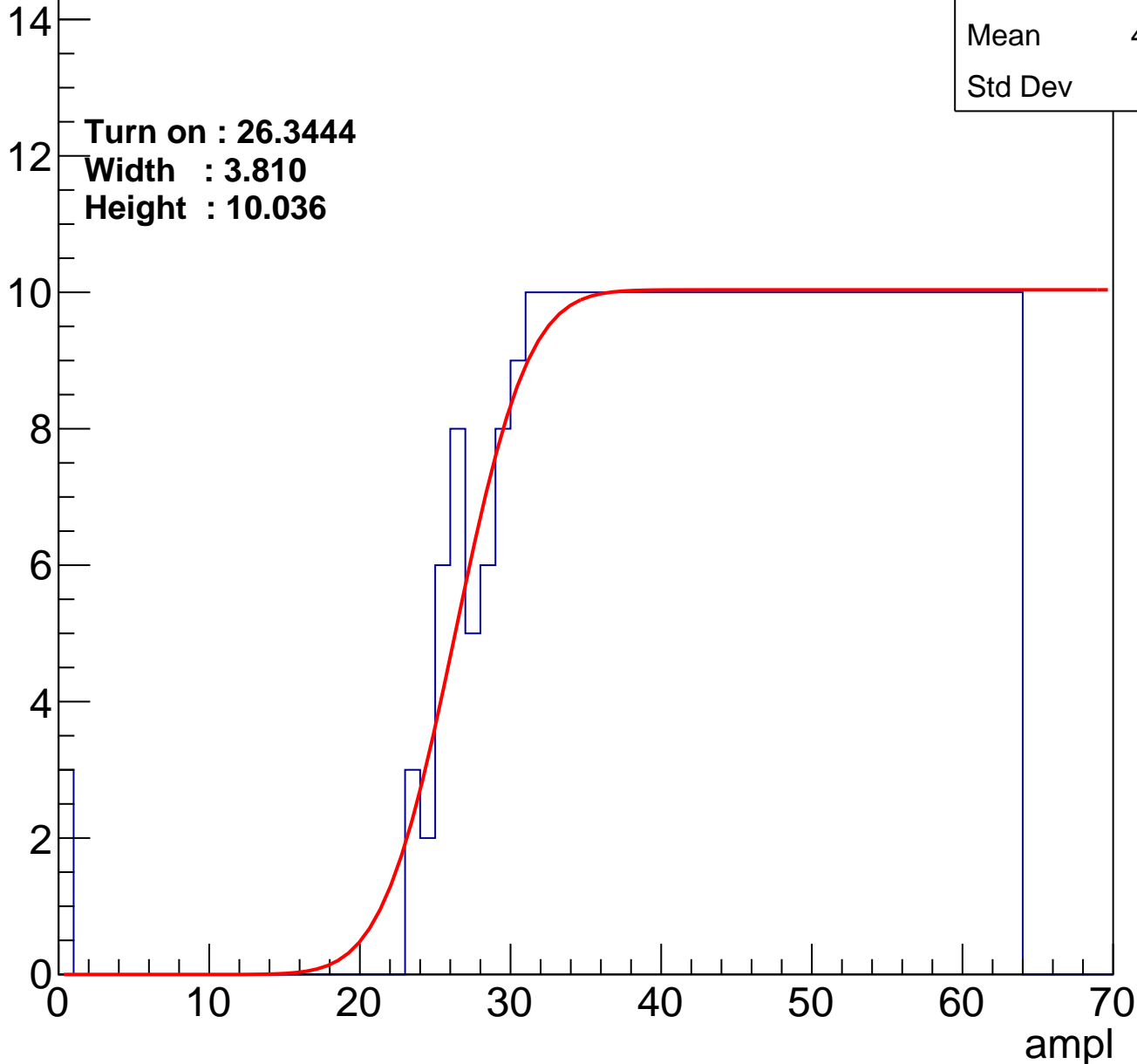
Entries	380
Mean	44.18
Std Dev	11.71

Turn on : 26.3444

Width : 3.810

Height : 10.036

Entry



B1L002S, U26-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.65
Std Dev	11.27

Turn on : 27.1902

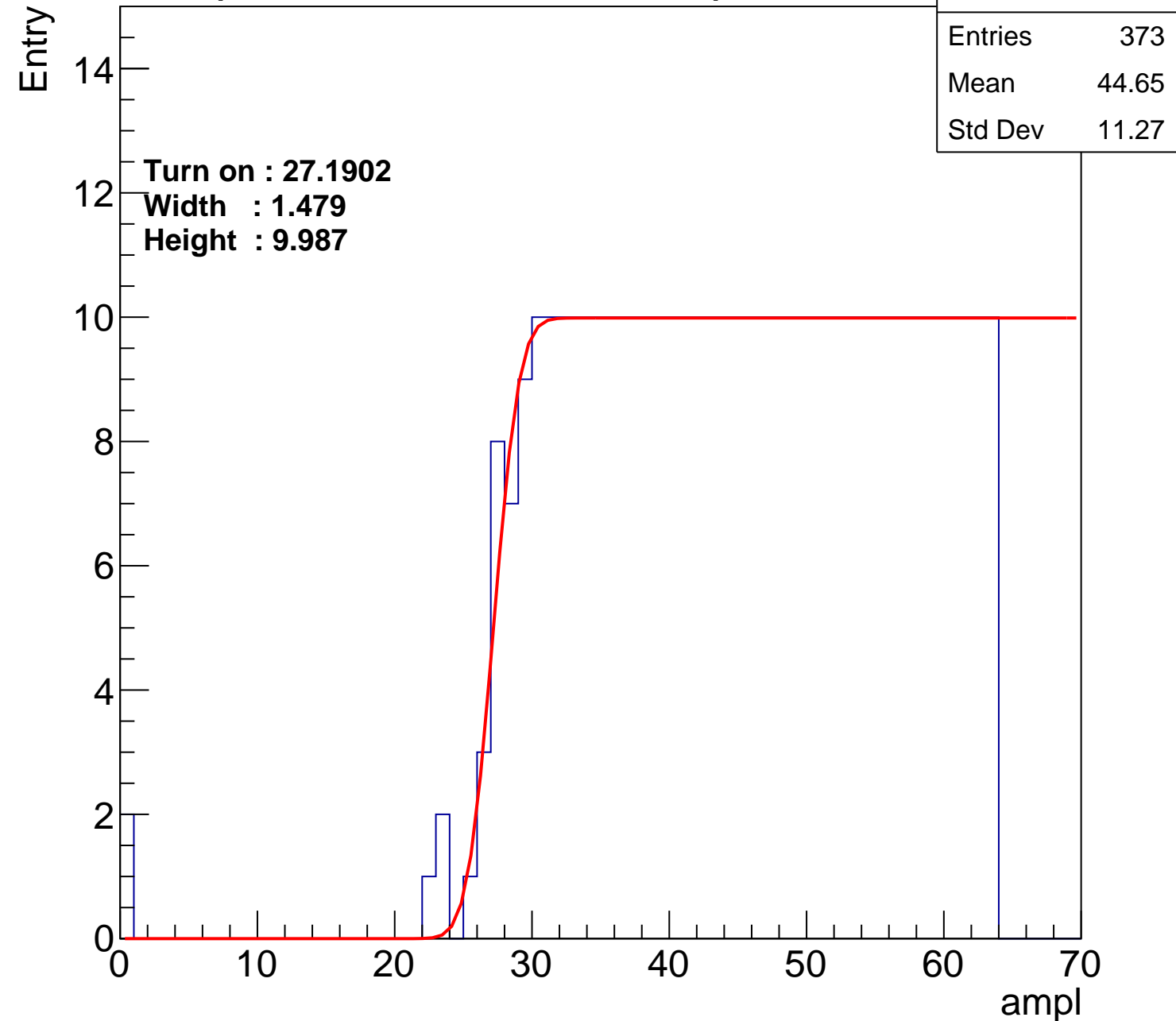
Width : 1.479

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	367
Mean	45.02
Std Dev	10.91

Turn on : 27.3918

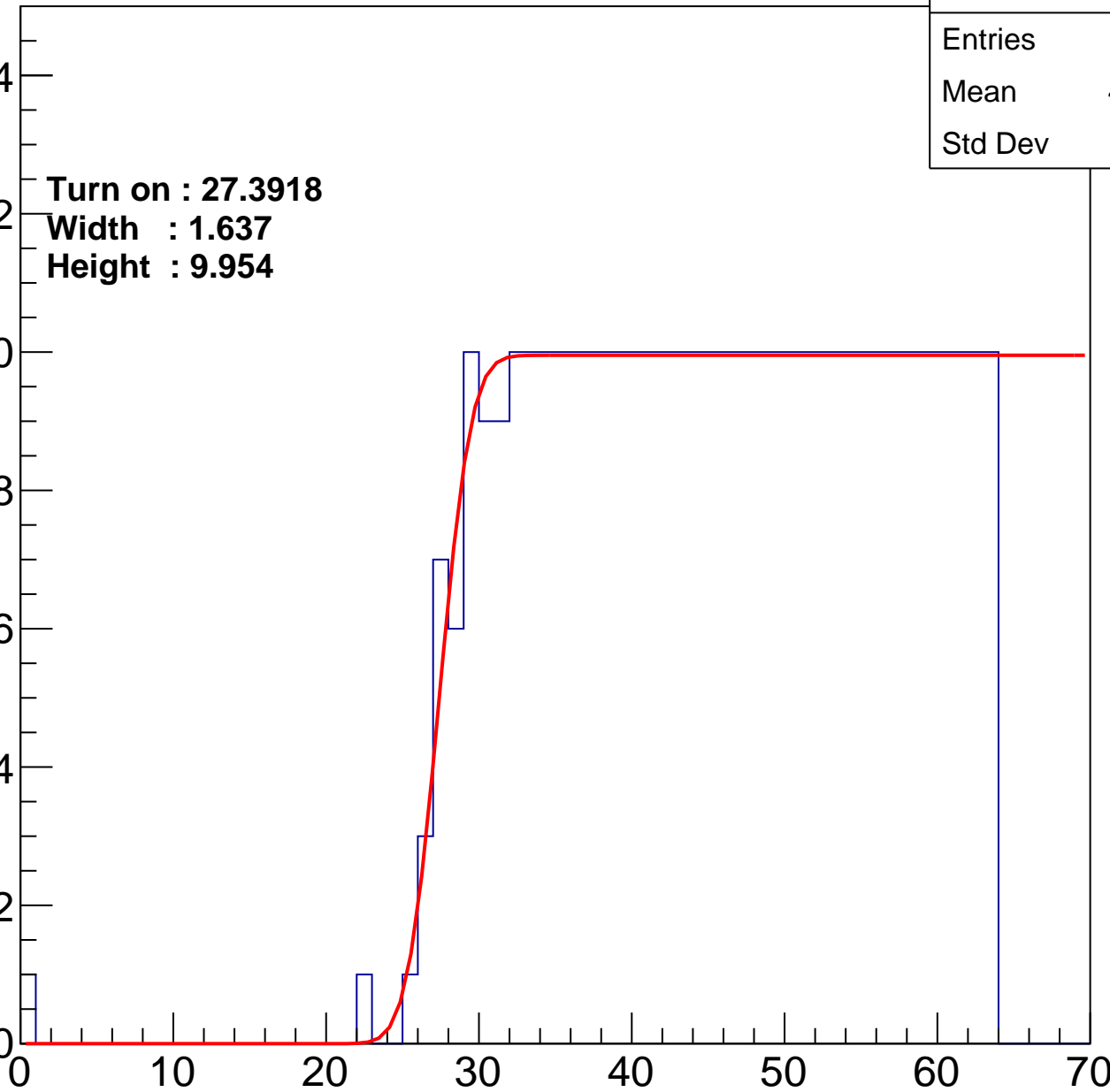
Width : 1.637

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.17
Std Dev	11.43

Turn on : 25.9916

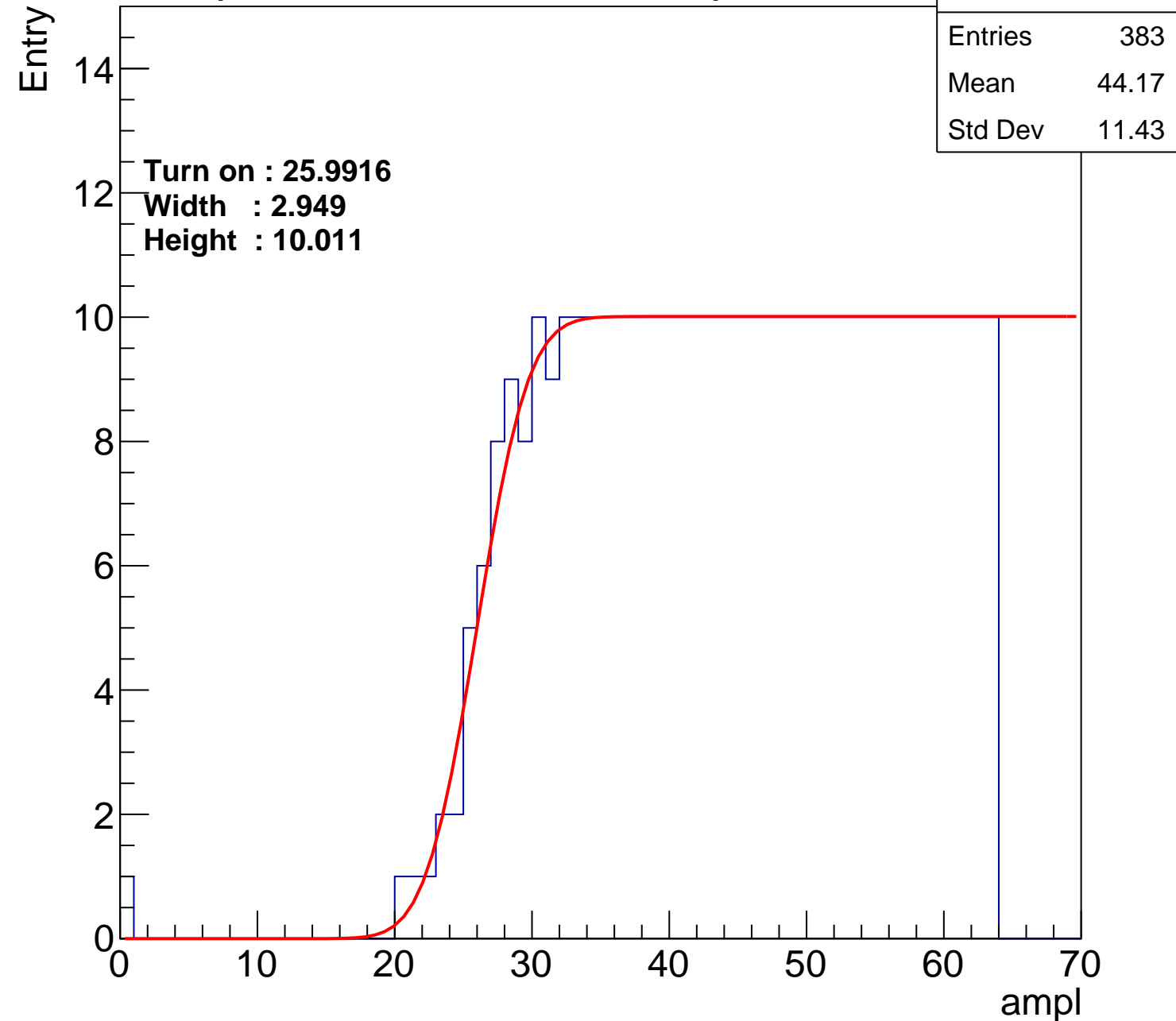
Width : 2.949

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch125

calib_packv5_042523_0143.root, FC#10, port B3

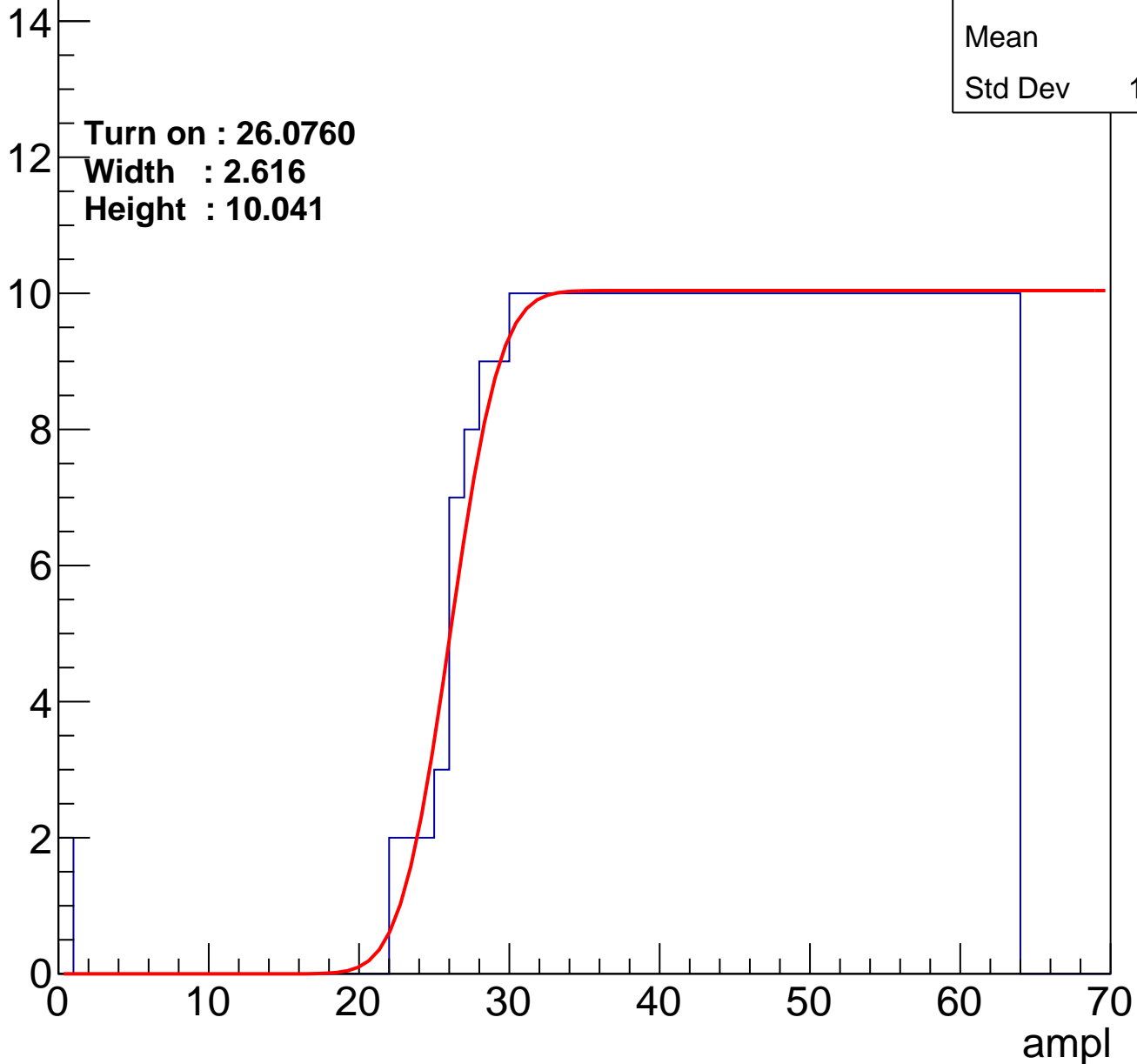
Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 26.0760

Width : 2.616

Height : 10.041

Entry



B1L002S, U26-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.44
Std Dev	11.81

Turn on : 24.2435

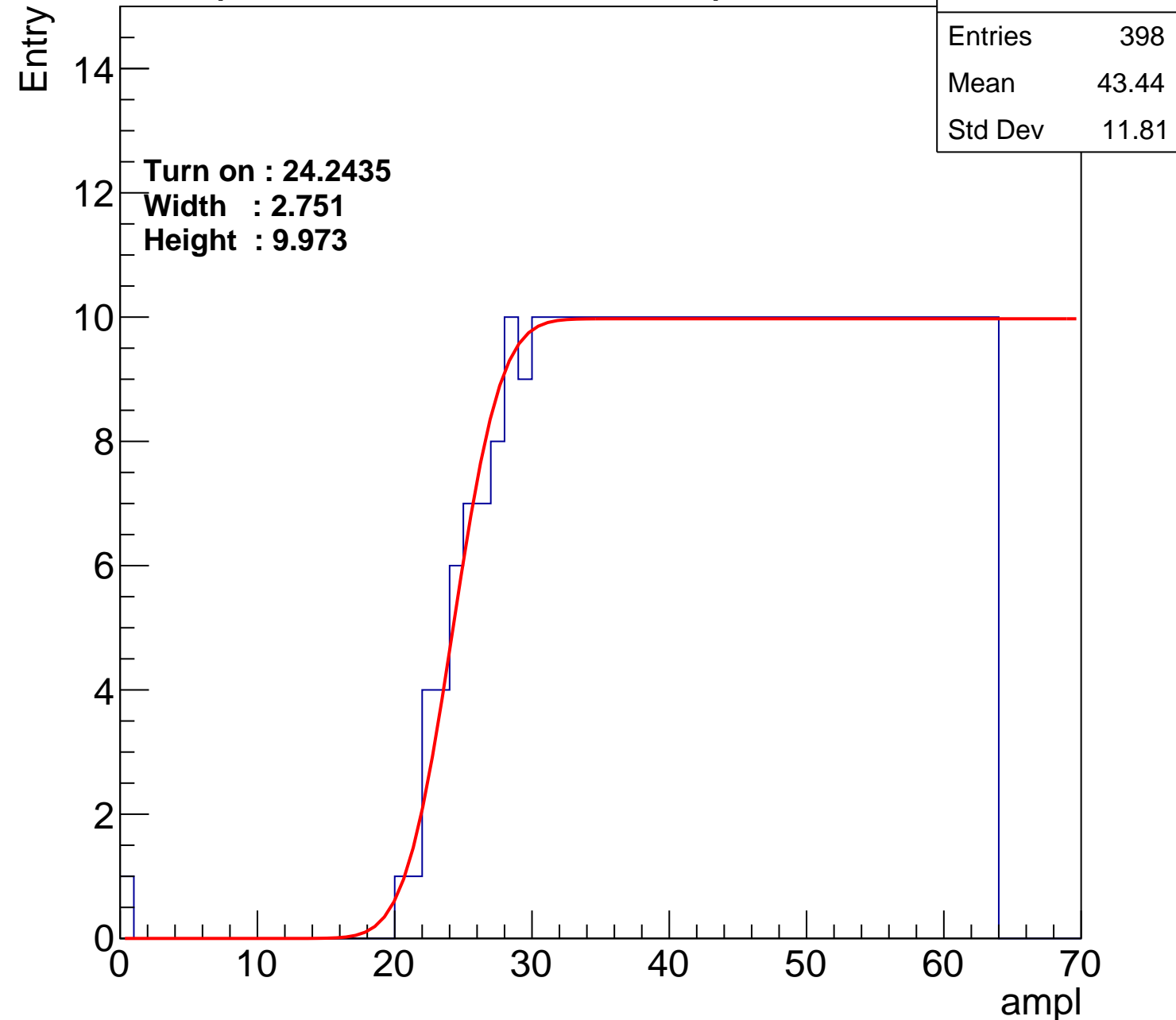
Width : 2.751

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.0937

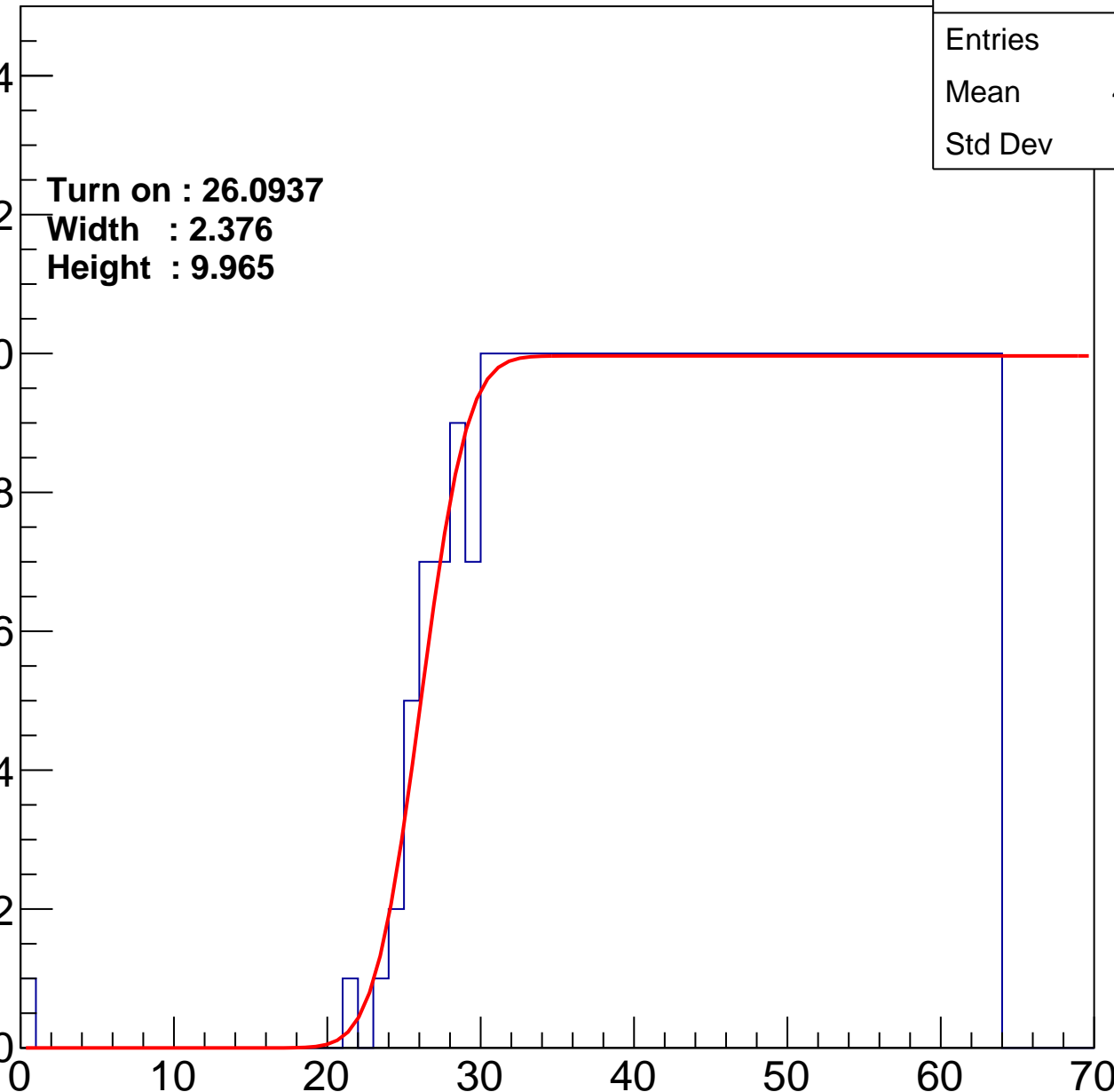
Width : 2.376

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U26-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.0937

Width : 2.376

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl

