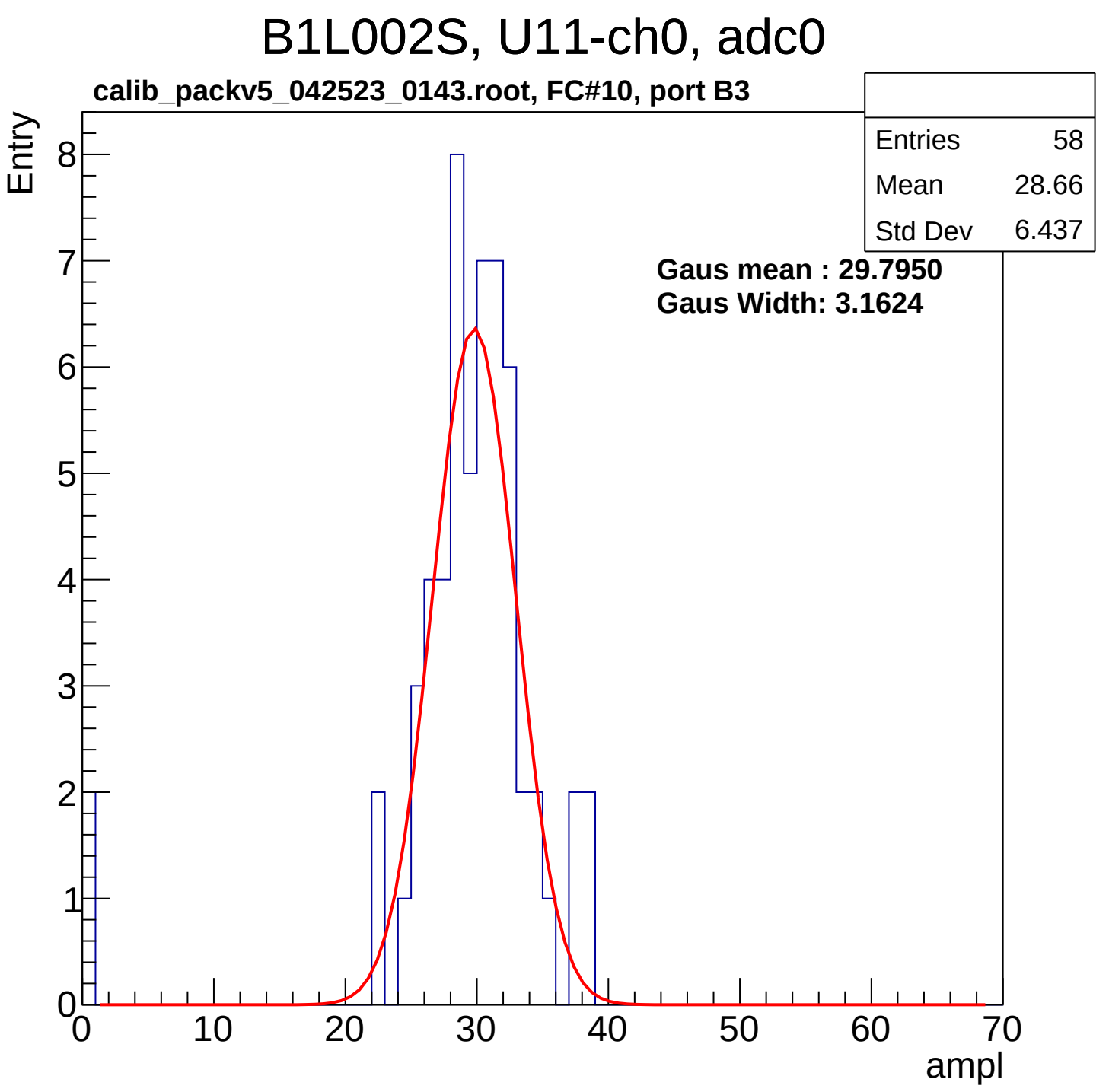
B1L002S, U11-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28.66
Std Dev 6.437
Gaus mean : 29.7950
Gaus Width: 3.1624
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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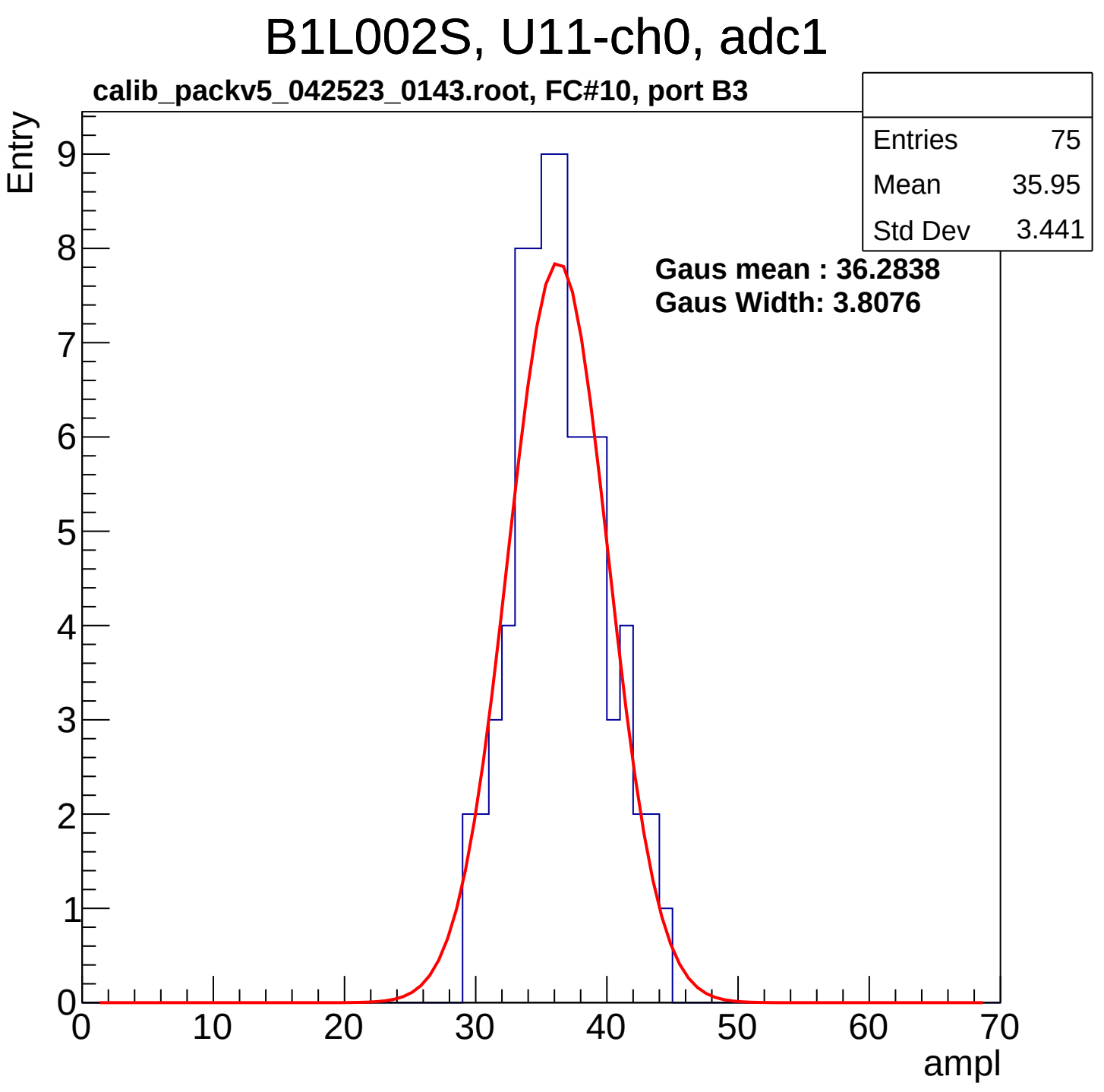
B1L002S, U11-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 35.95
Std Dev 3.441
Gaus mean : 36.2838
Gaus Width: 3.8076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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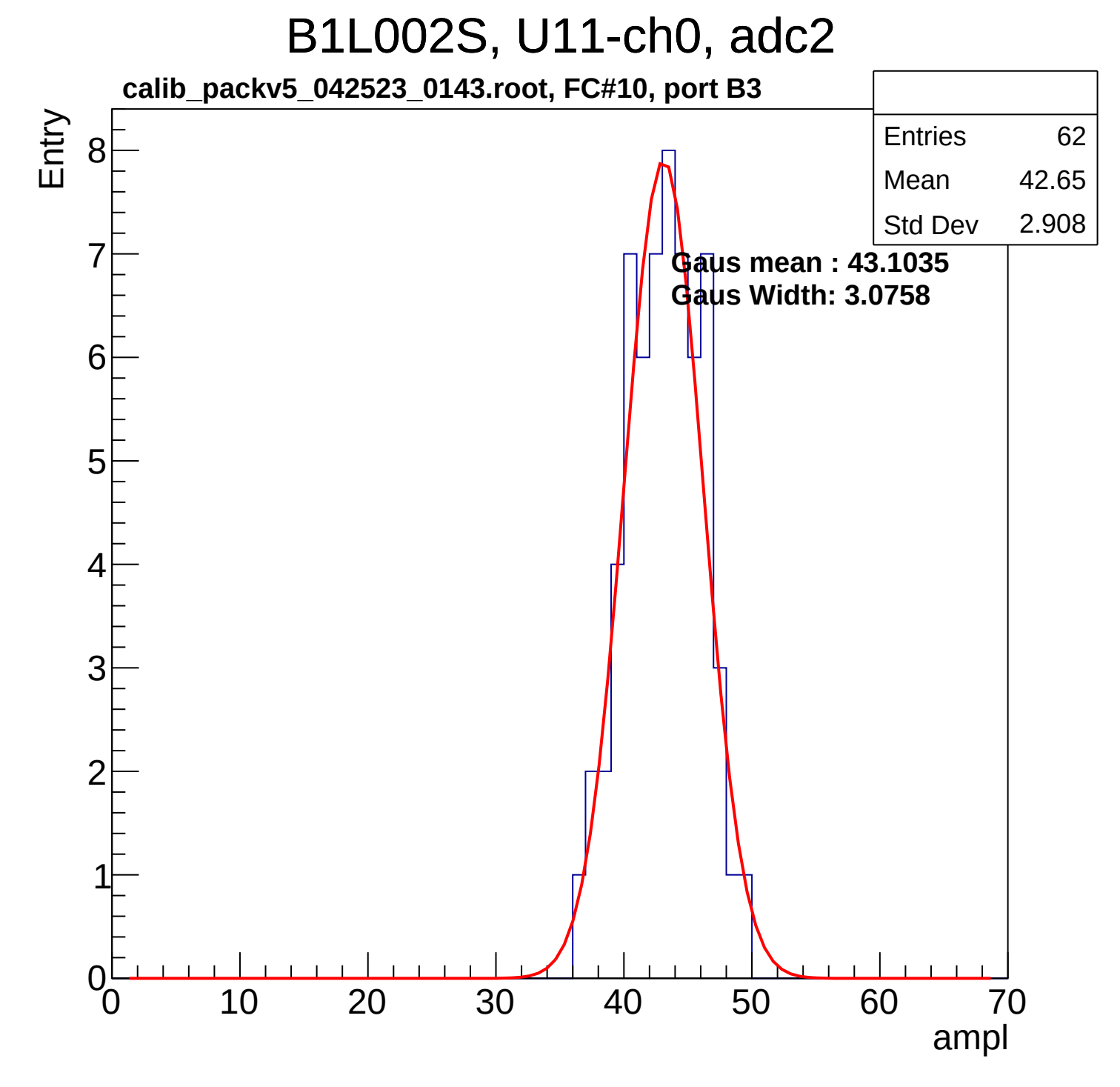
B1L002S, U11-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.65
Std Dev 2.908
Gaus mean : 43.1035
Gaus Width: 3.0758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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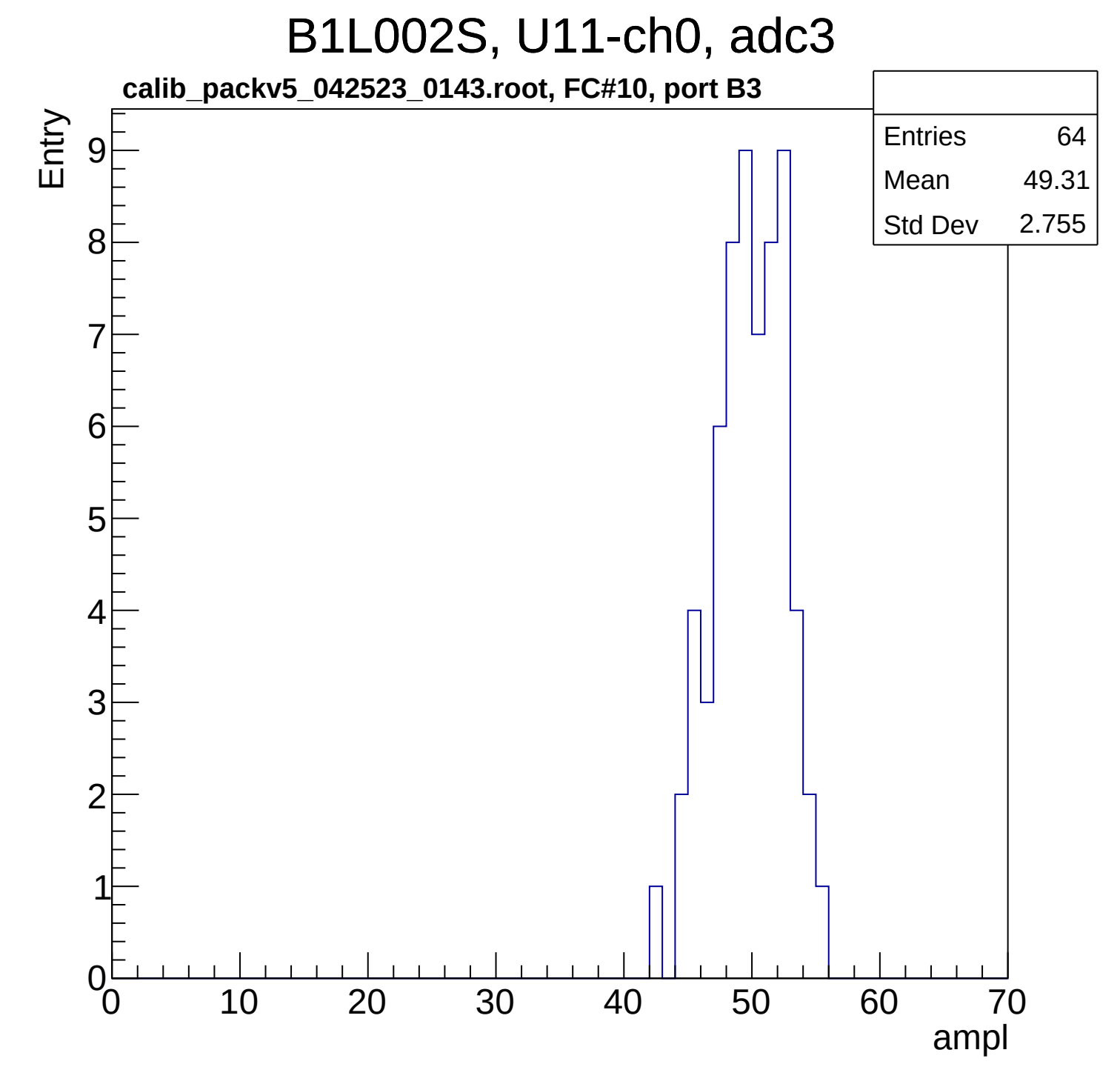
B1L002S, U11-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 49.31
Std Dev 2.755
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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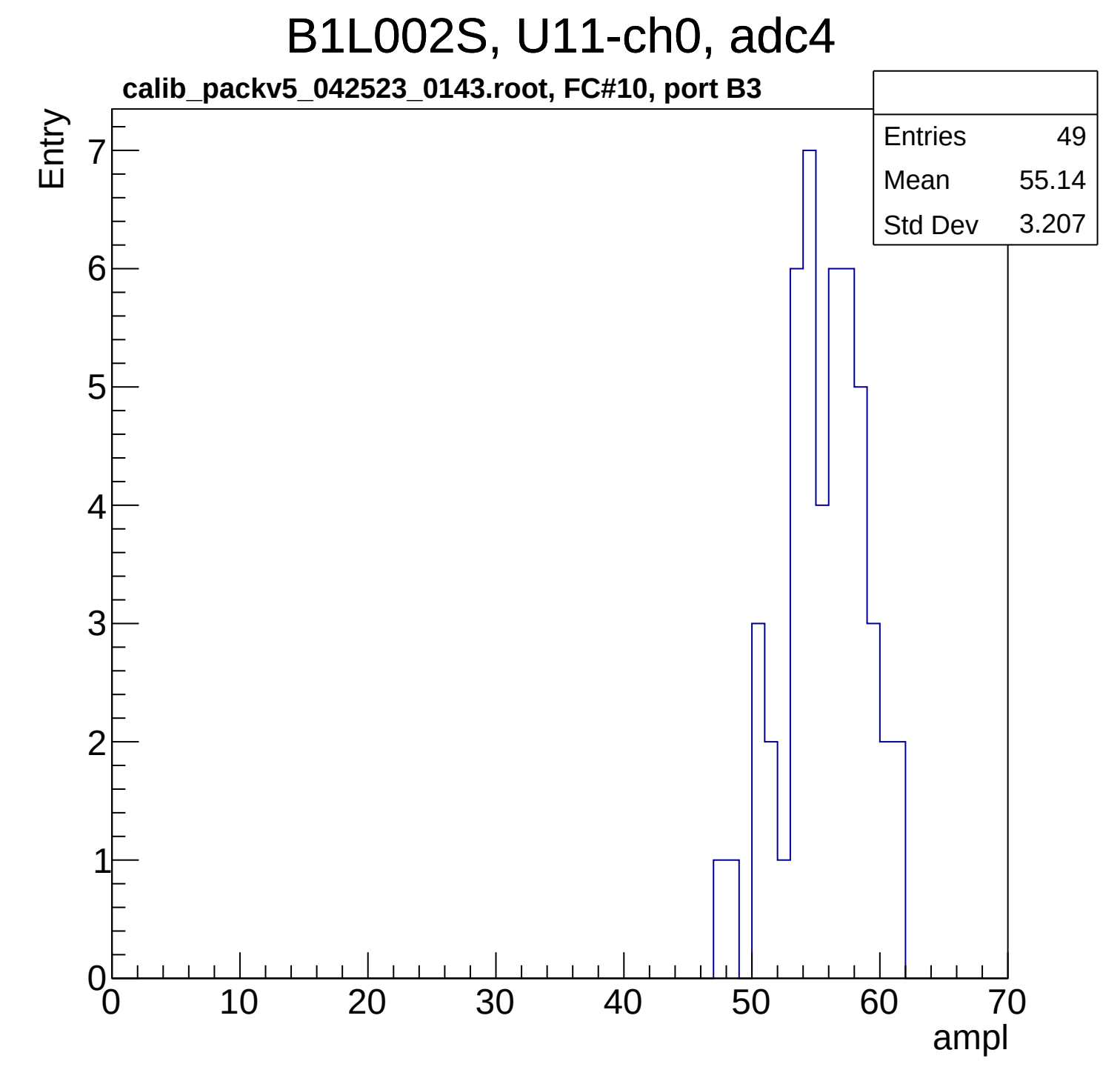
B1L002S, U11-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 55.14
Std Dev 3.207
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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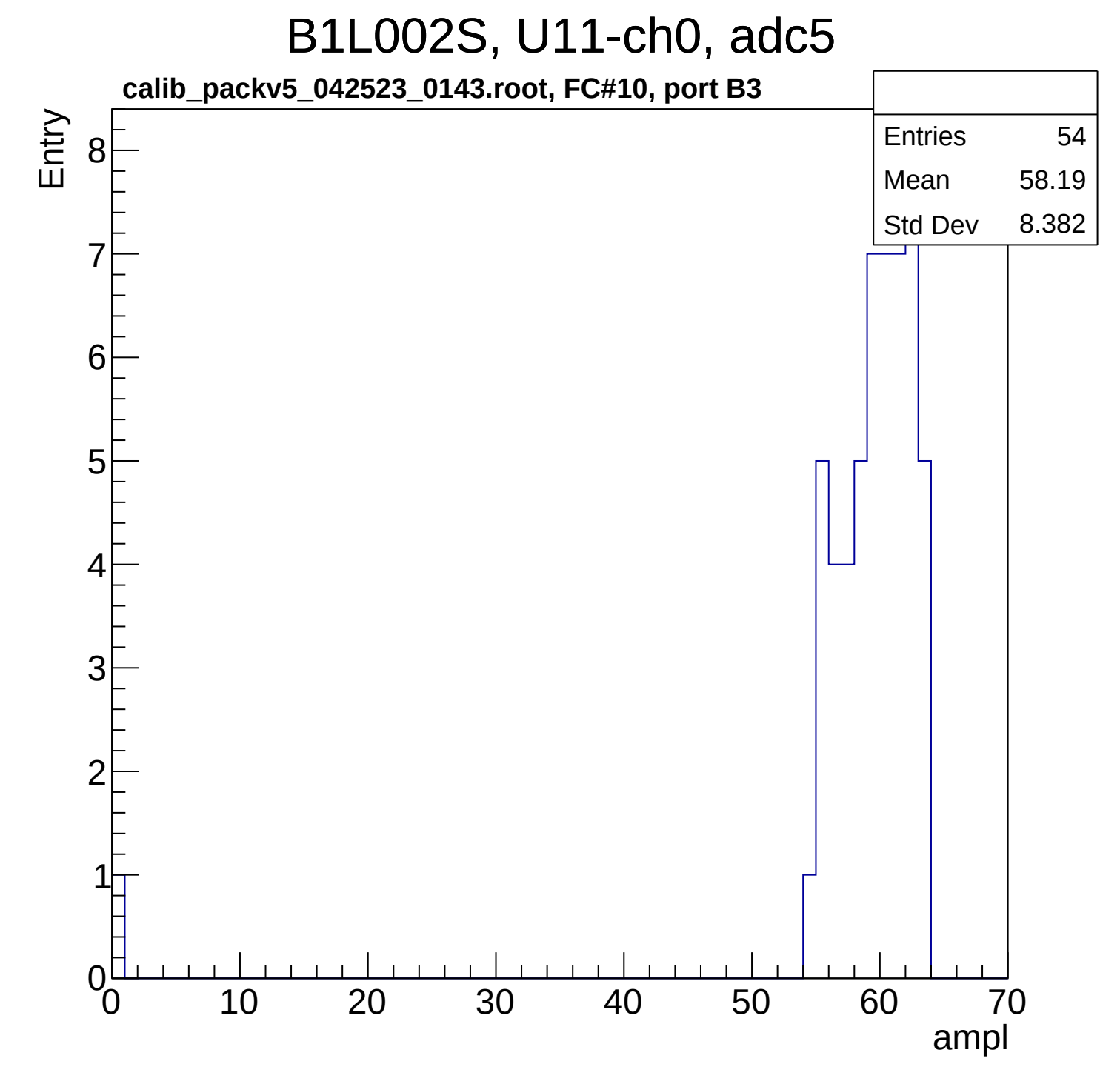
B1L002S, U11-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.19
Std Dev 8.382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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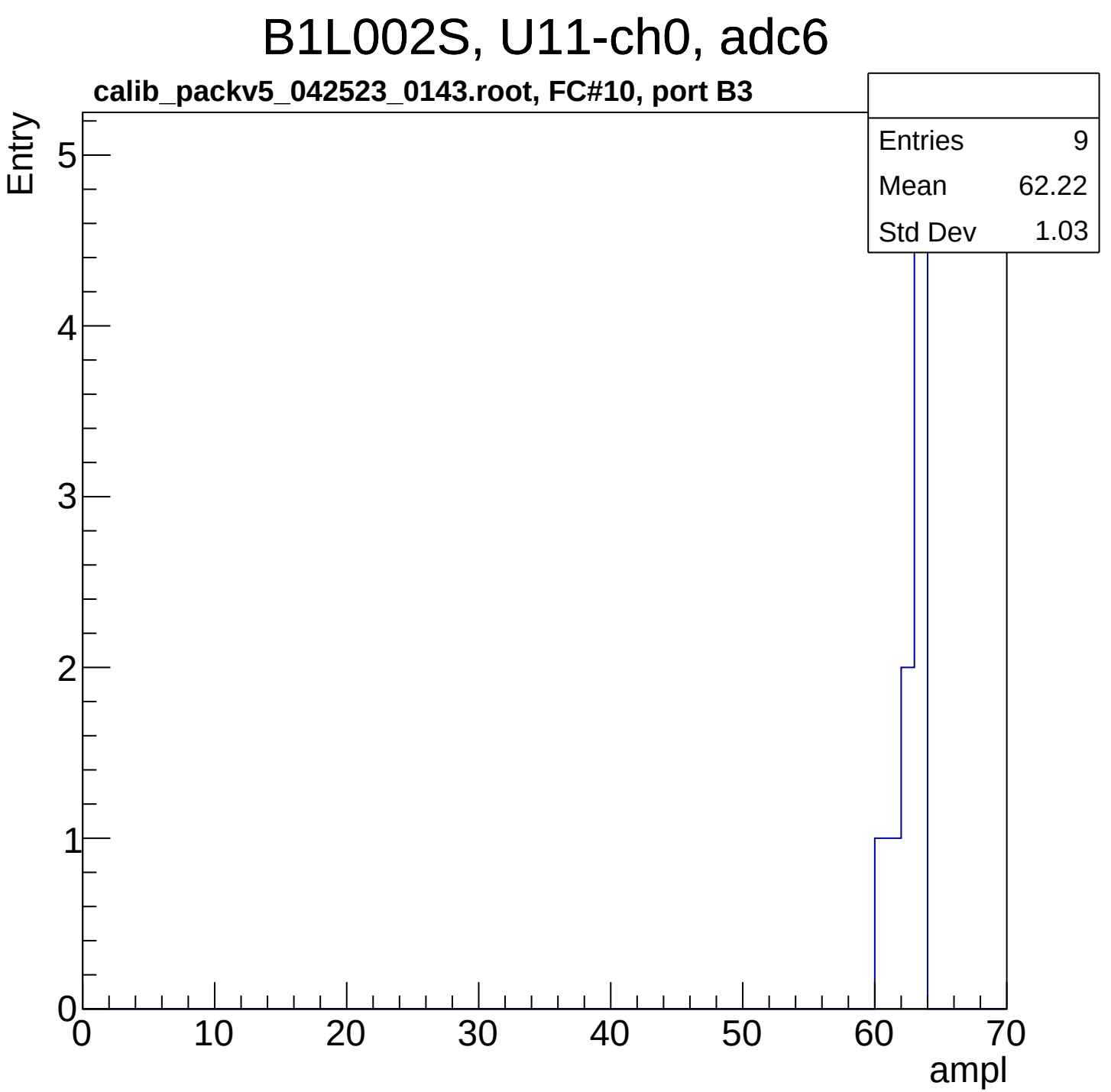
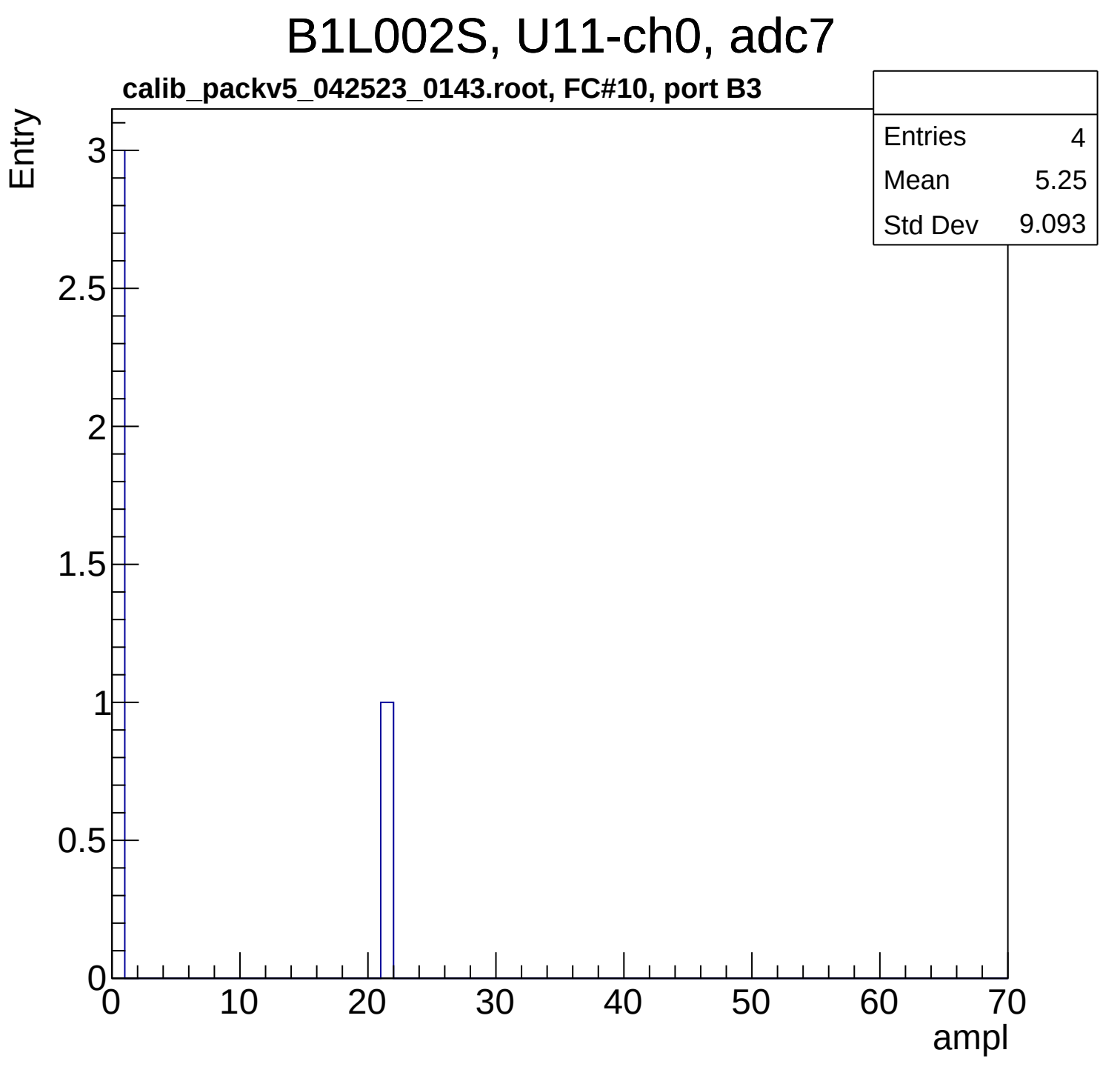
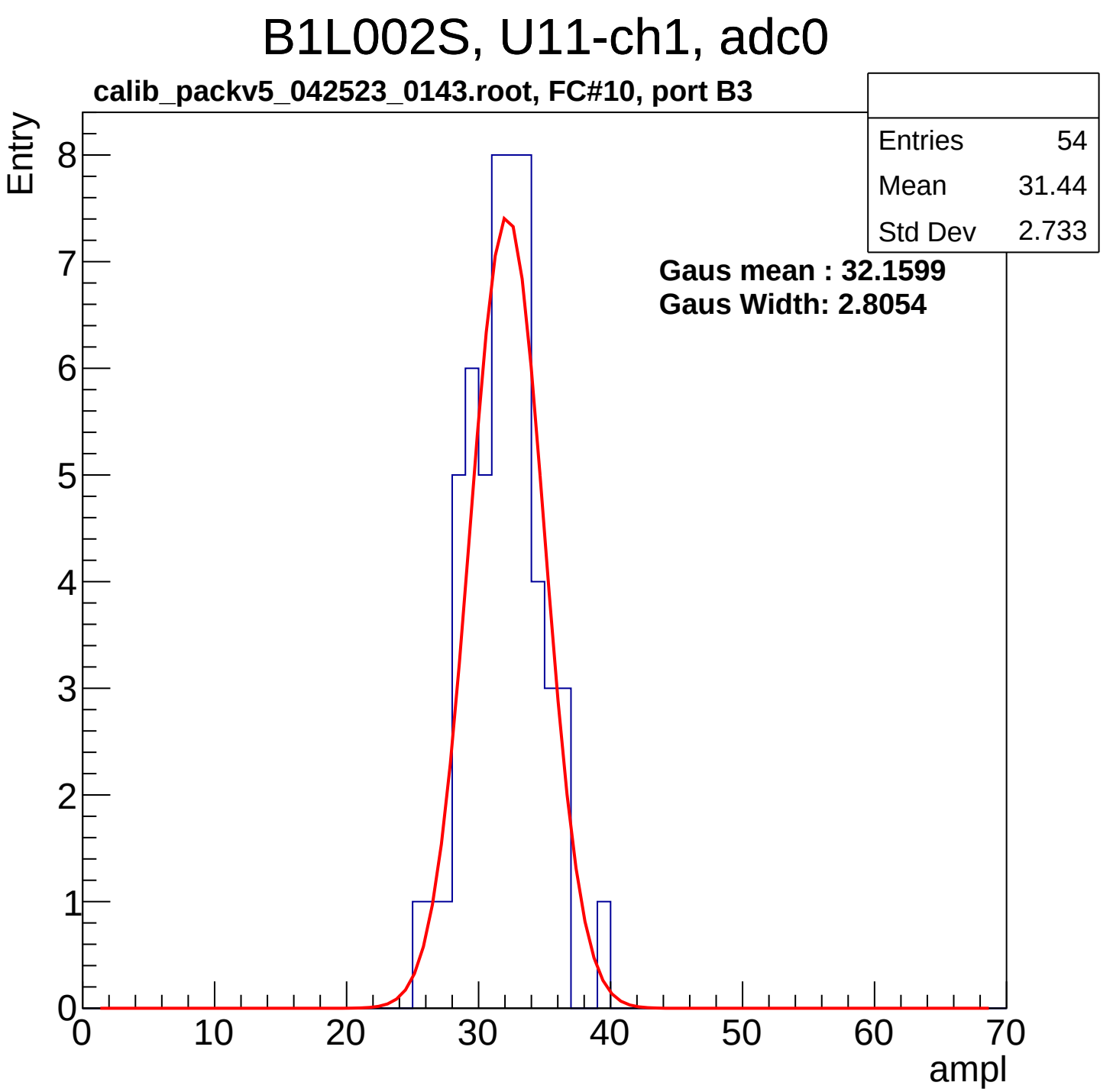

B1L002S, U11-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 62.22
Std Dev 1.03
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 5.25
Std Dev 9.093
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 31.44
Std Dev 2.733
Gaus mean : 32.1599
Gaus Width: 2.8054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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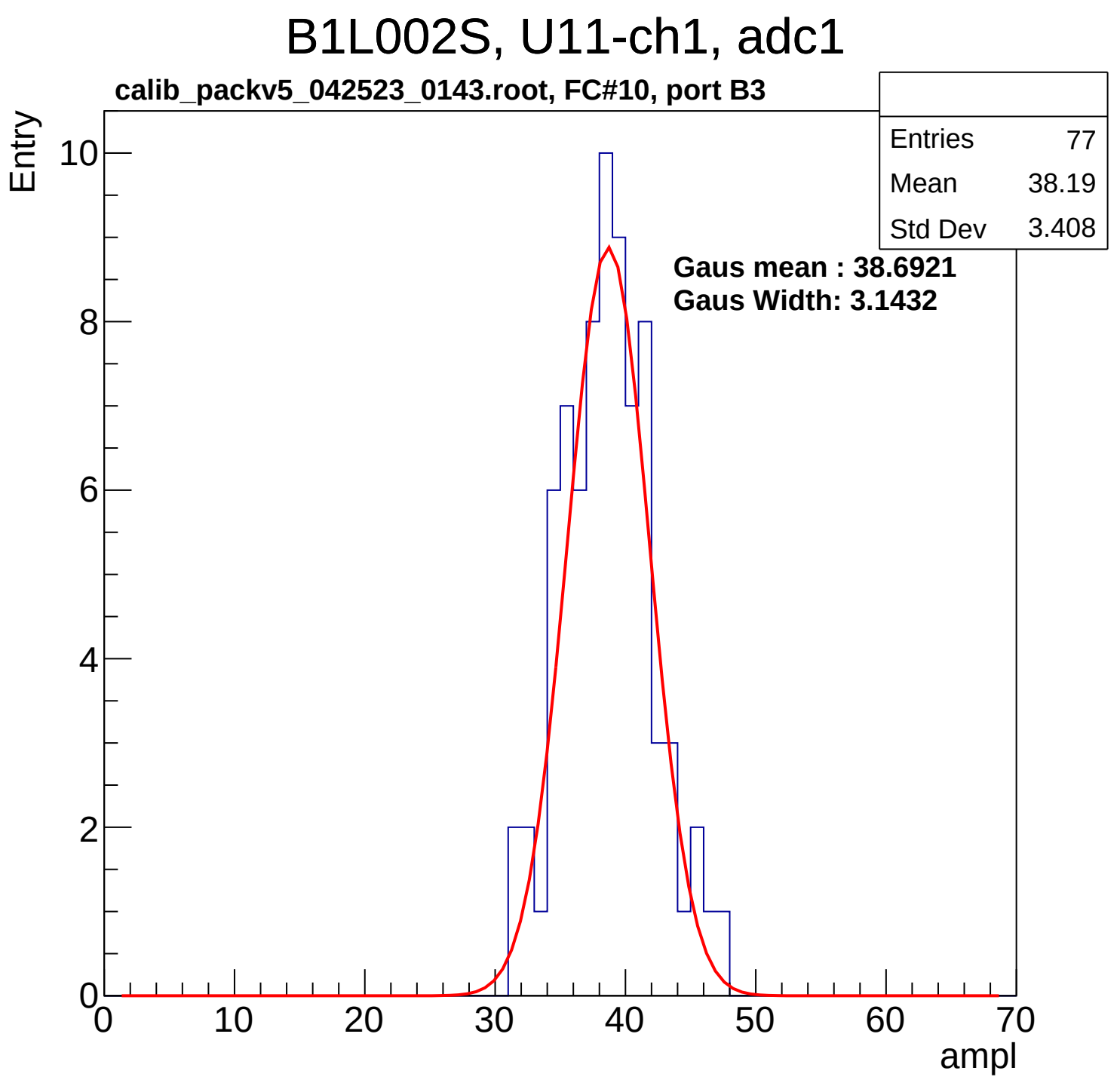
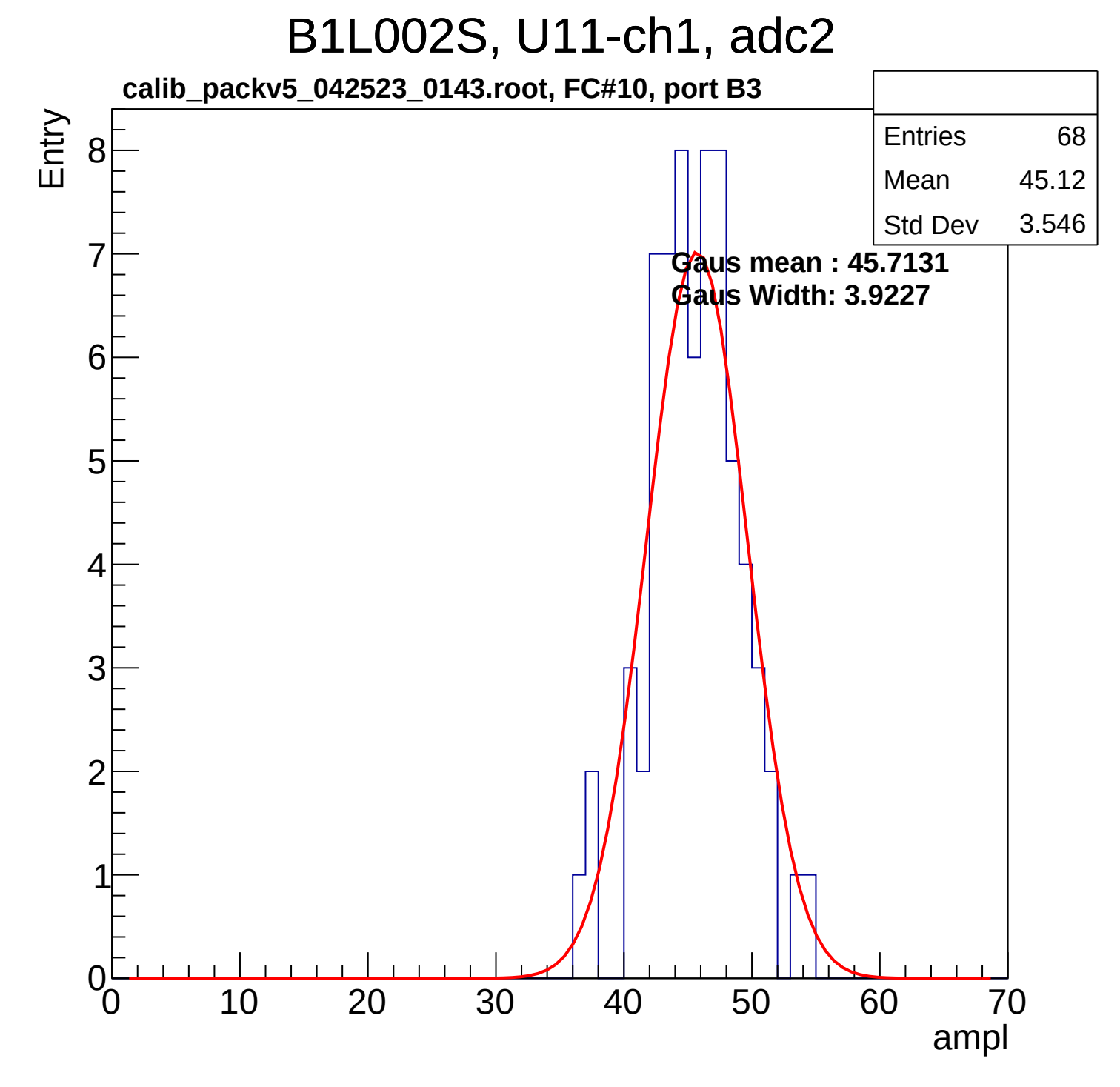

B1L002S, U11-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 38.19
Std Dev 3.408
Gaus mean : 38.6921
Gaus Width: 3.1432
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 45.12
Std Dev 3.546
Gaus mean : 45.7131
Gaus Width: 3.9227
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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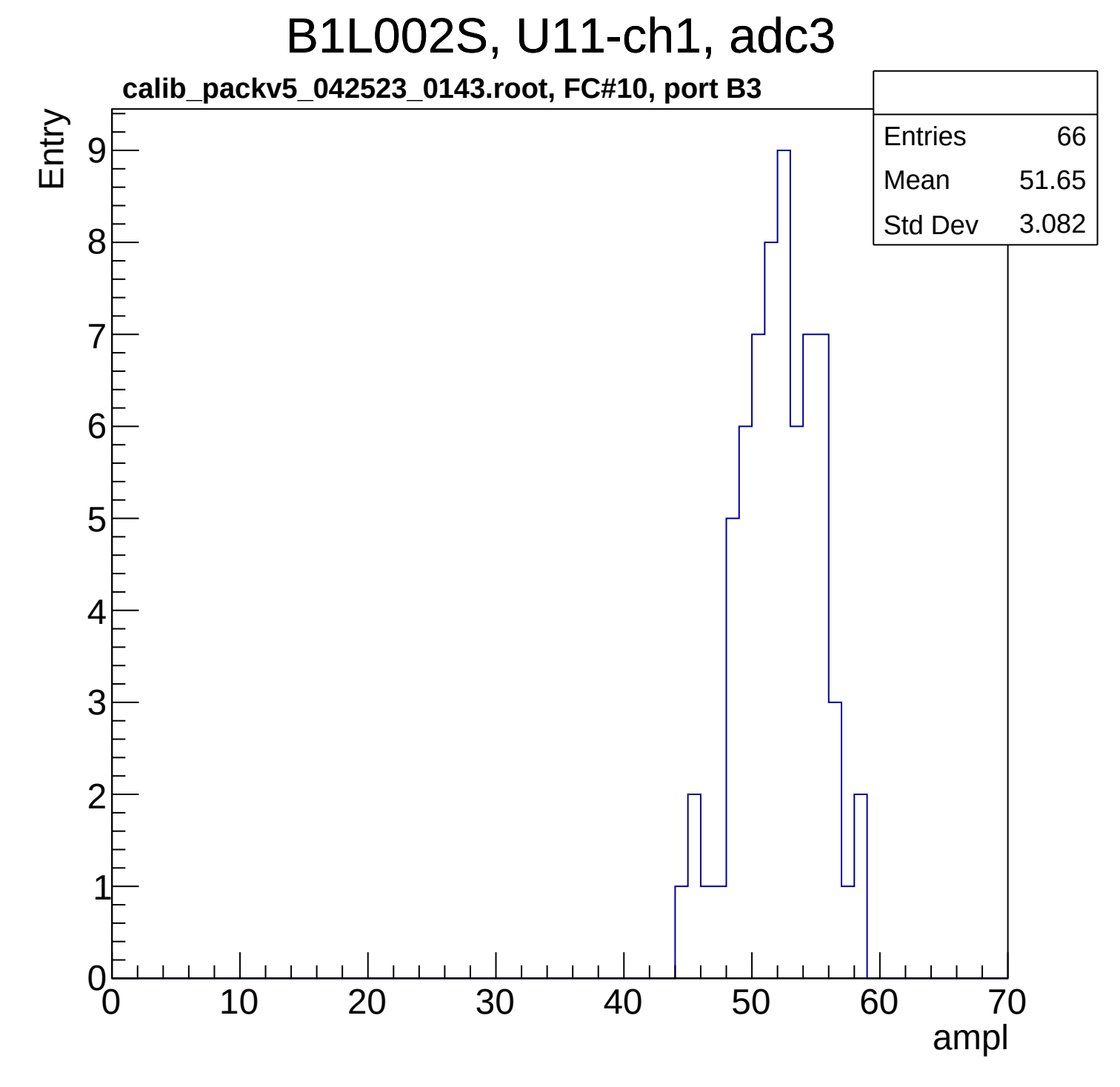
B1L002S, U11-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 51.65
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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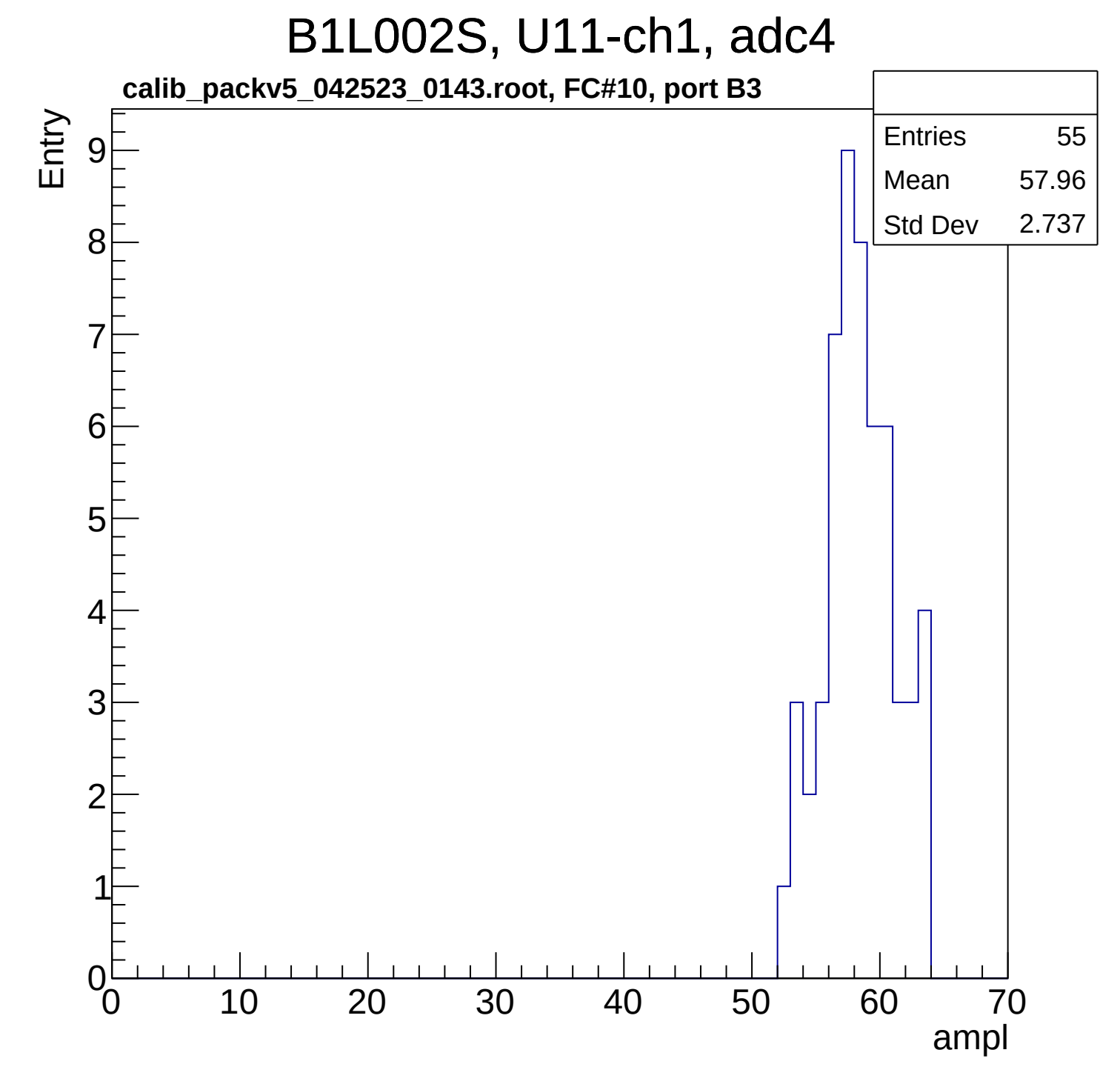
B1L002S, U11-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.96
Std Dev 2.737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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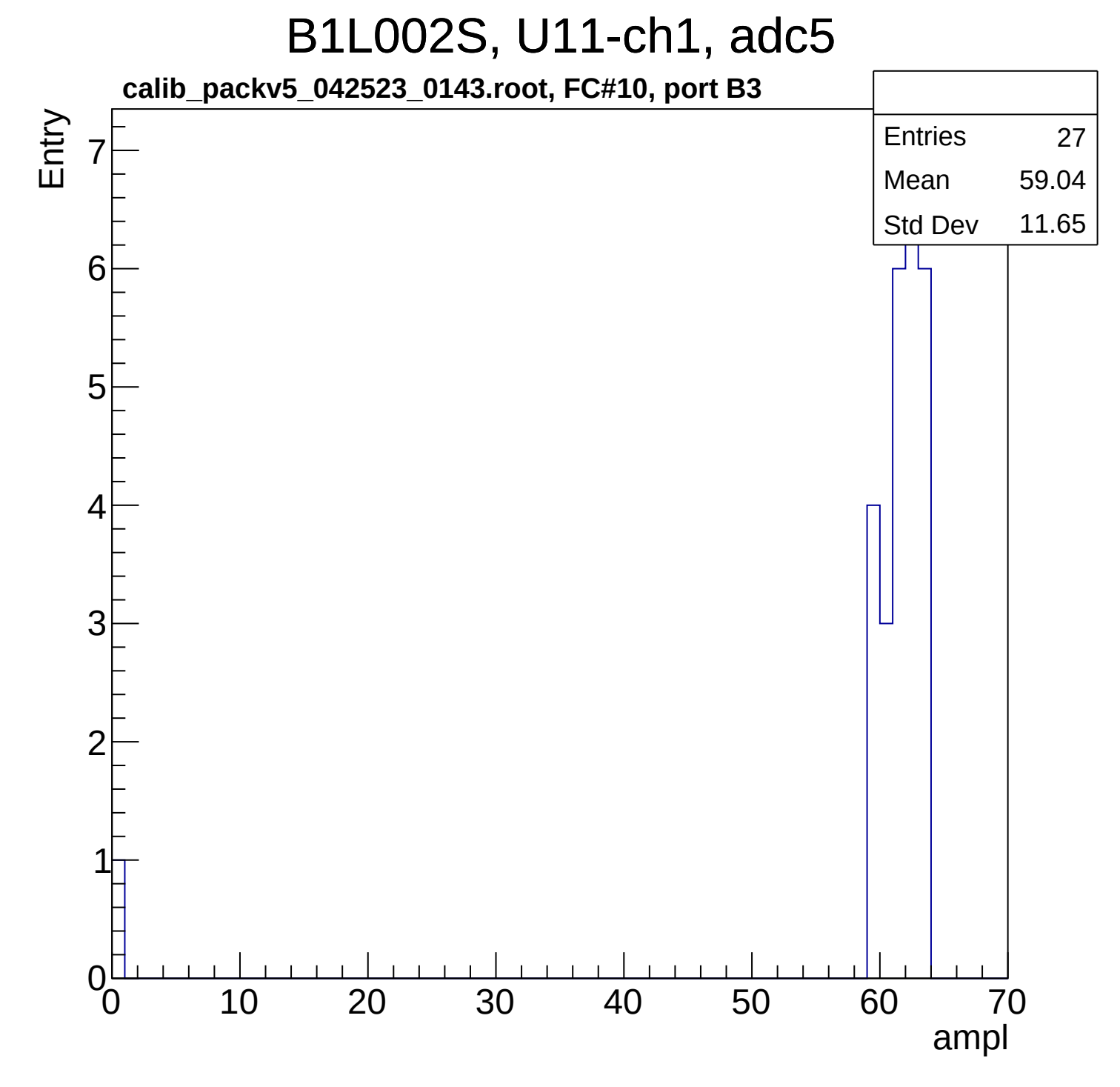
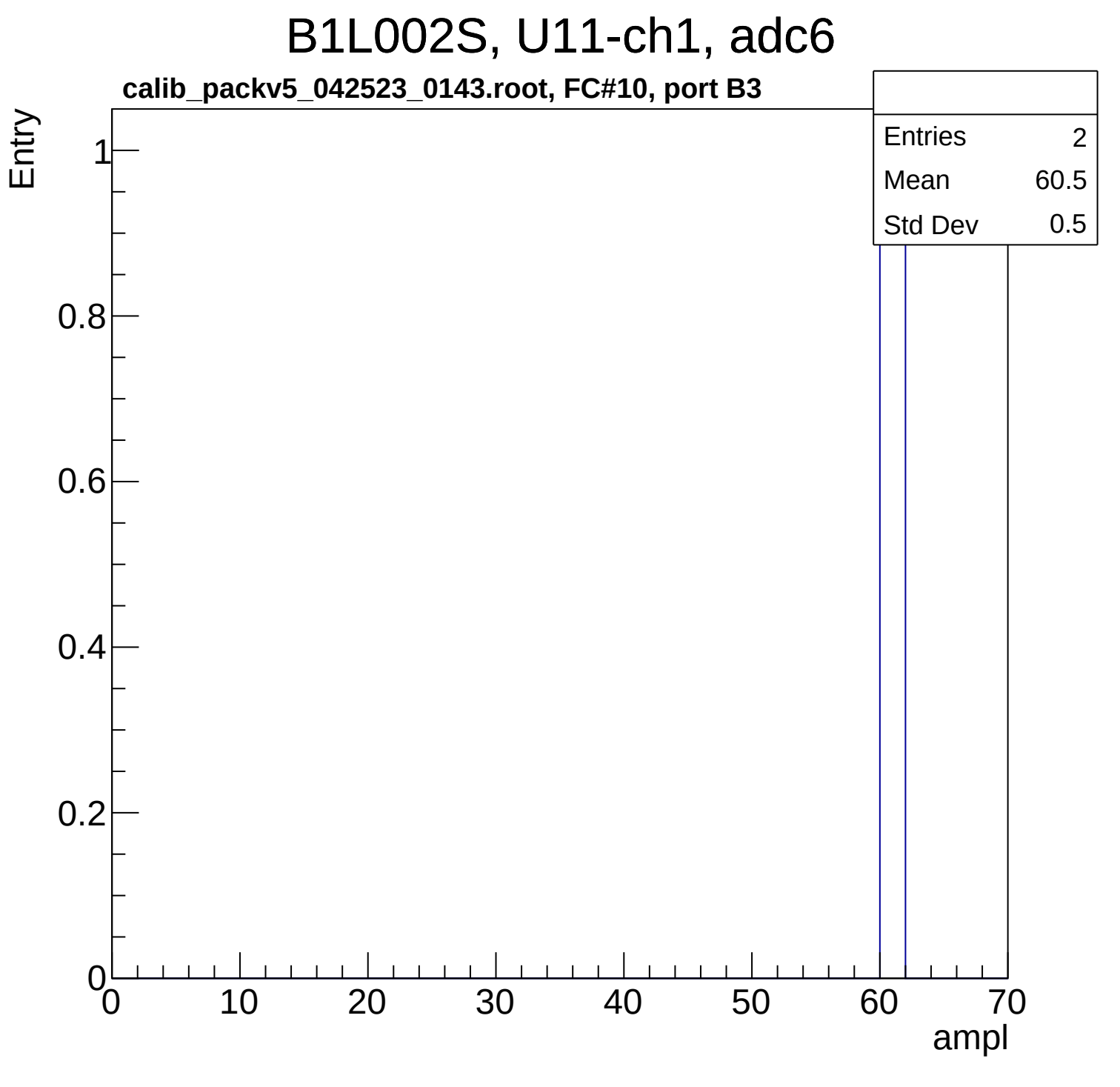
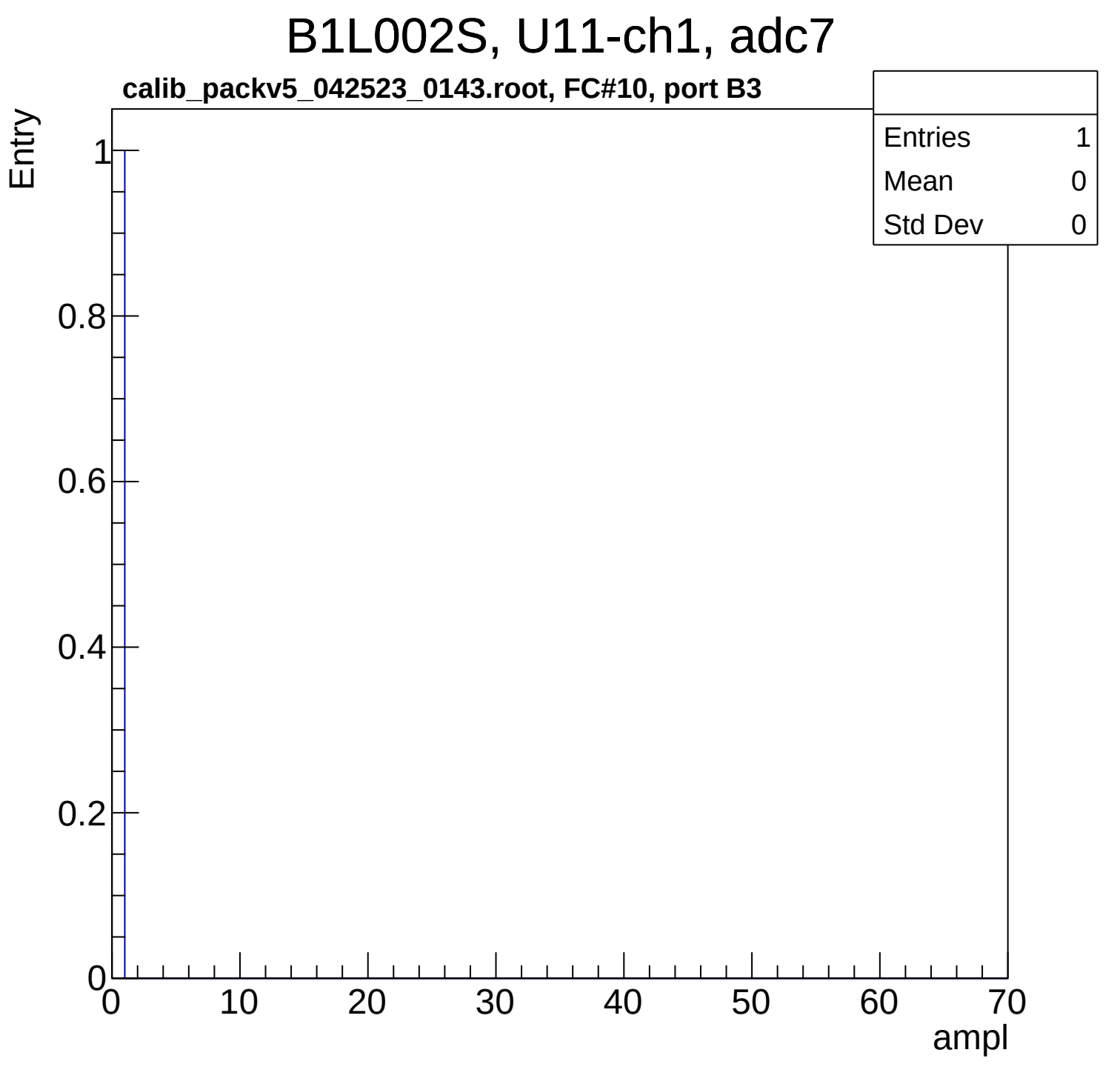
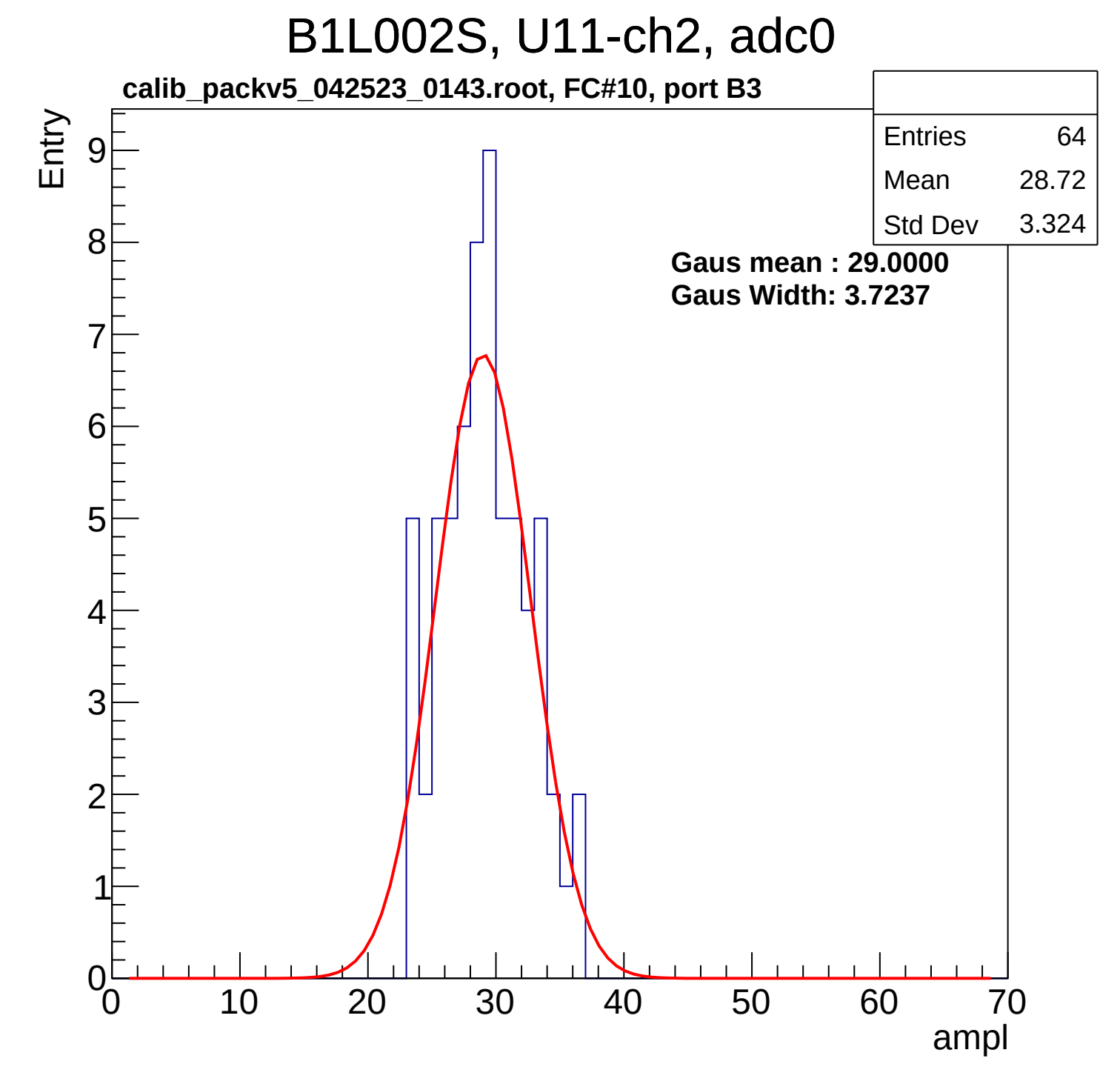
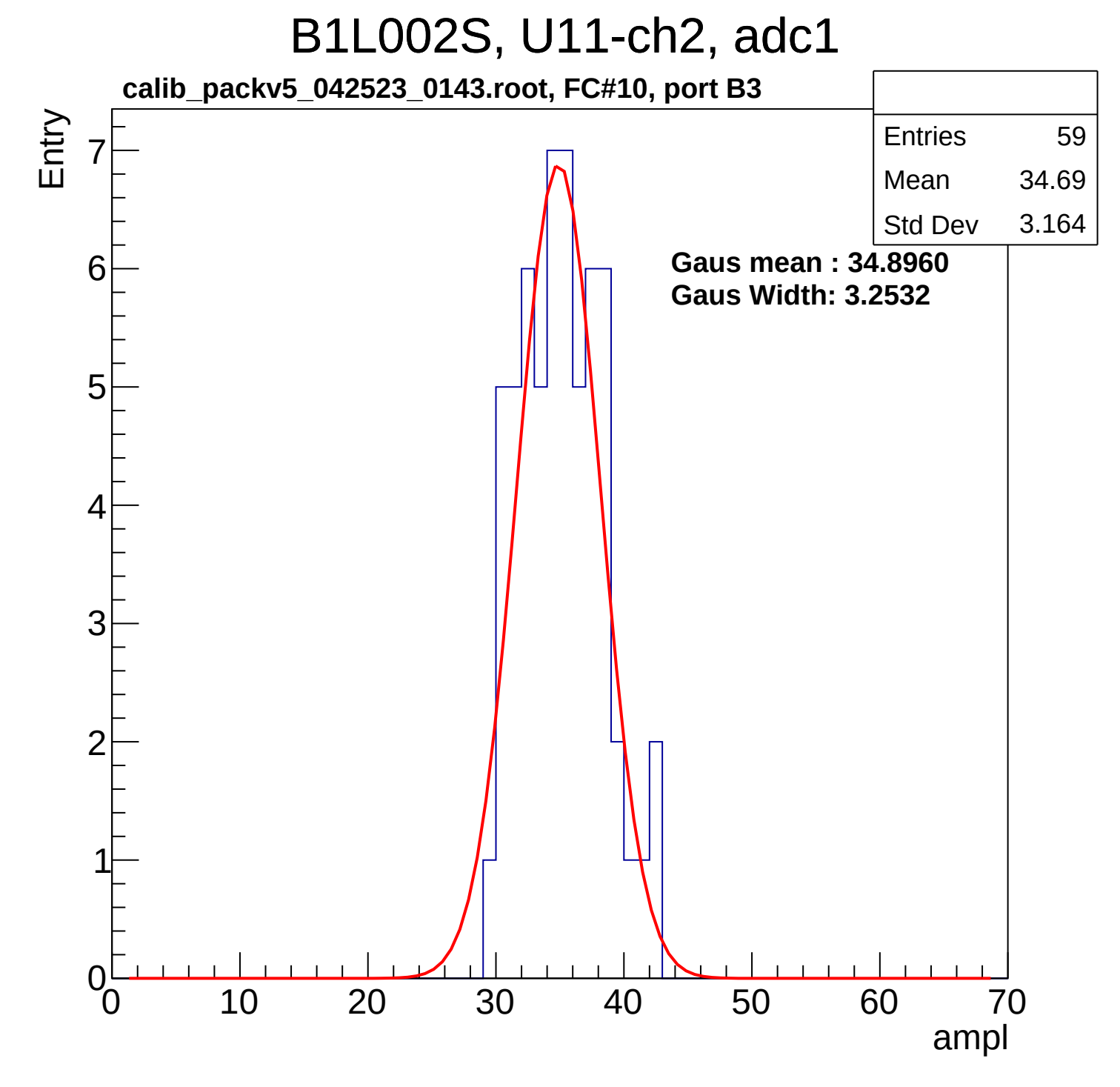
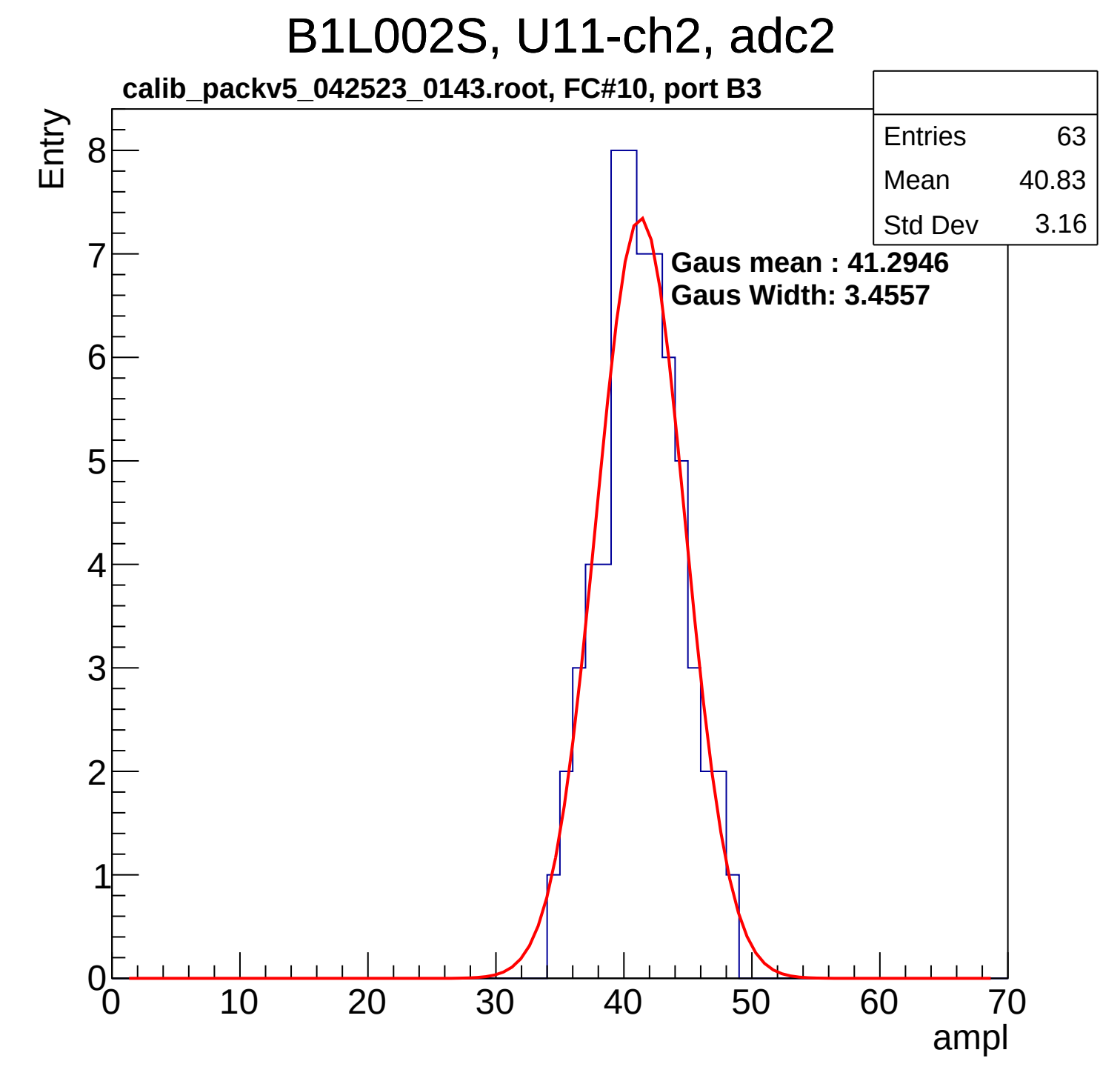

B1L002S, U11-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59.04
Std Dev 11.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 60.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.72
Std Dev 3.324
Gaus mean : 29.0000
Gaus Width: 3.7237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.69
Std Dev 3.164
Gaus mean : 34.8960
Gaus Width: 3.2532
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.83
Std Dev 3.16
Gaus mean : 41.2946
Gaus Width: 3.4557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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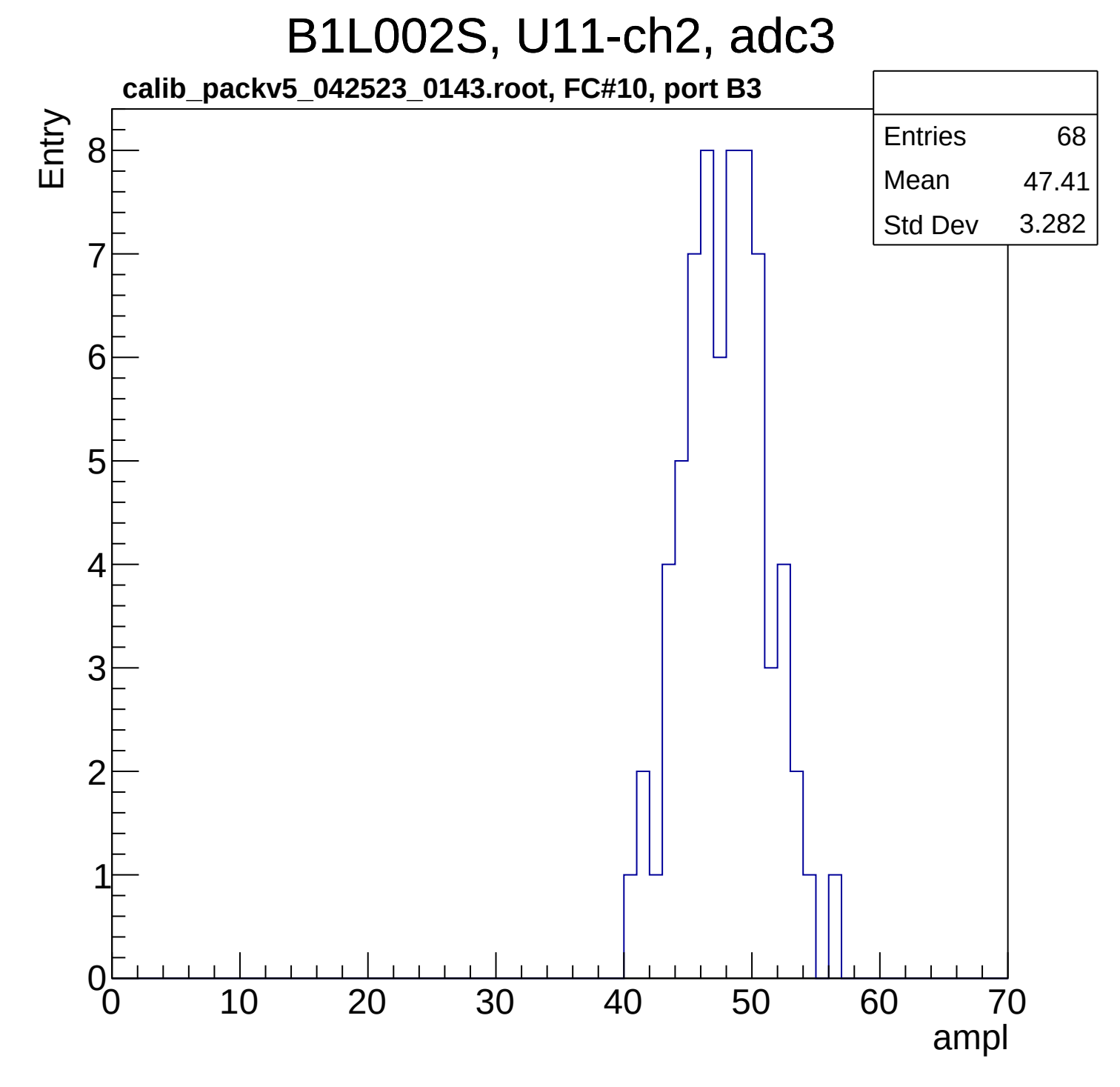
B1L002S, U11-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.41
Std Dev 3.282
Entry
ampl
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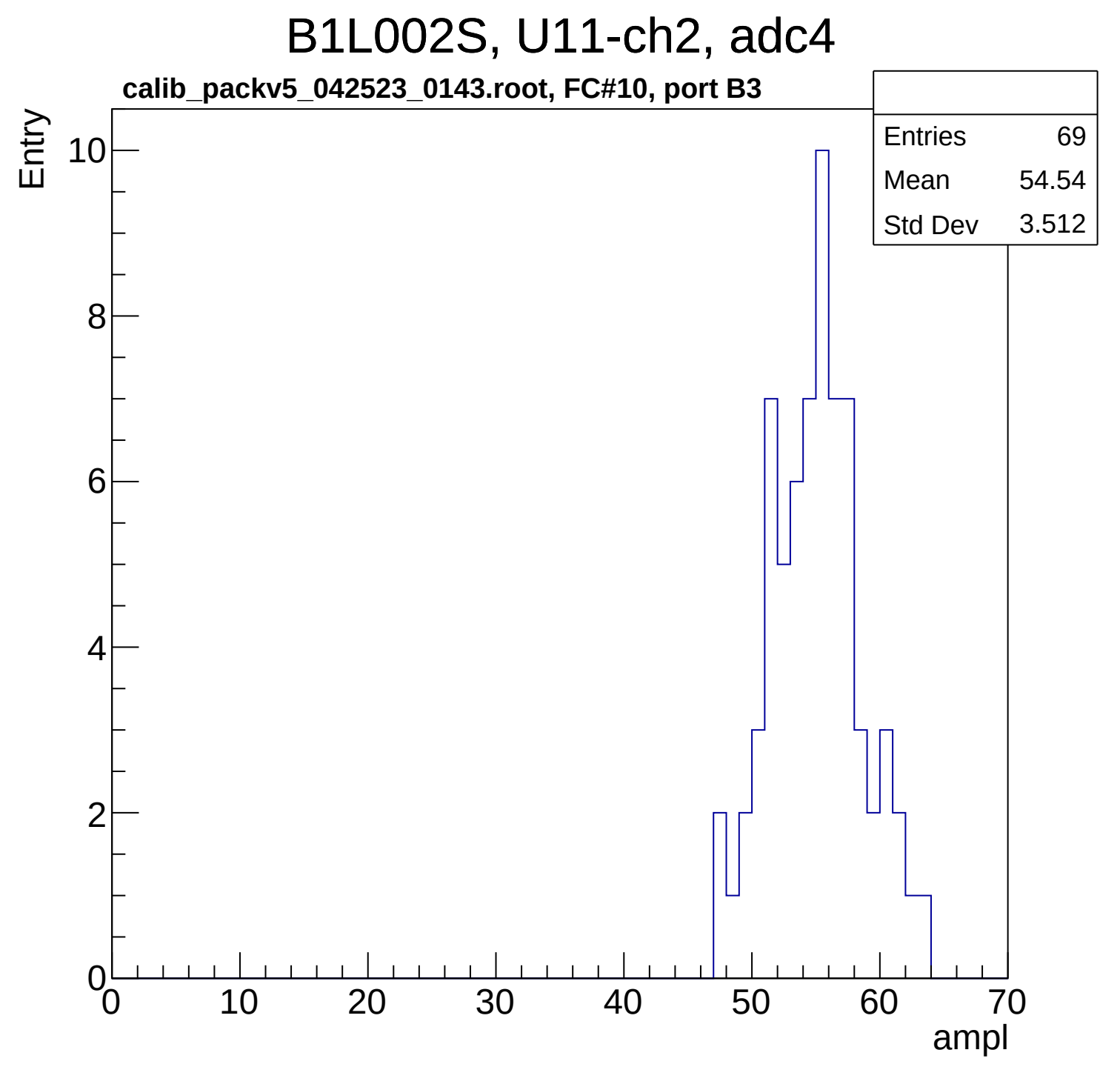
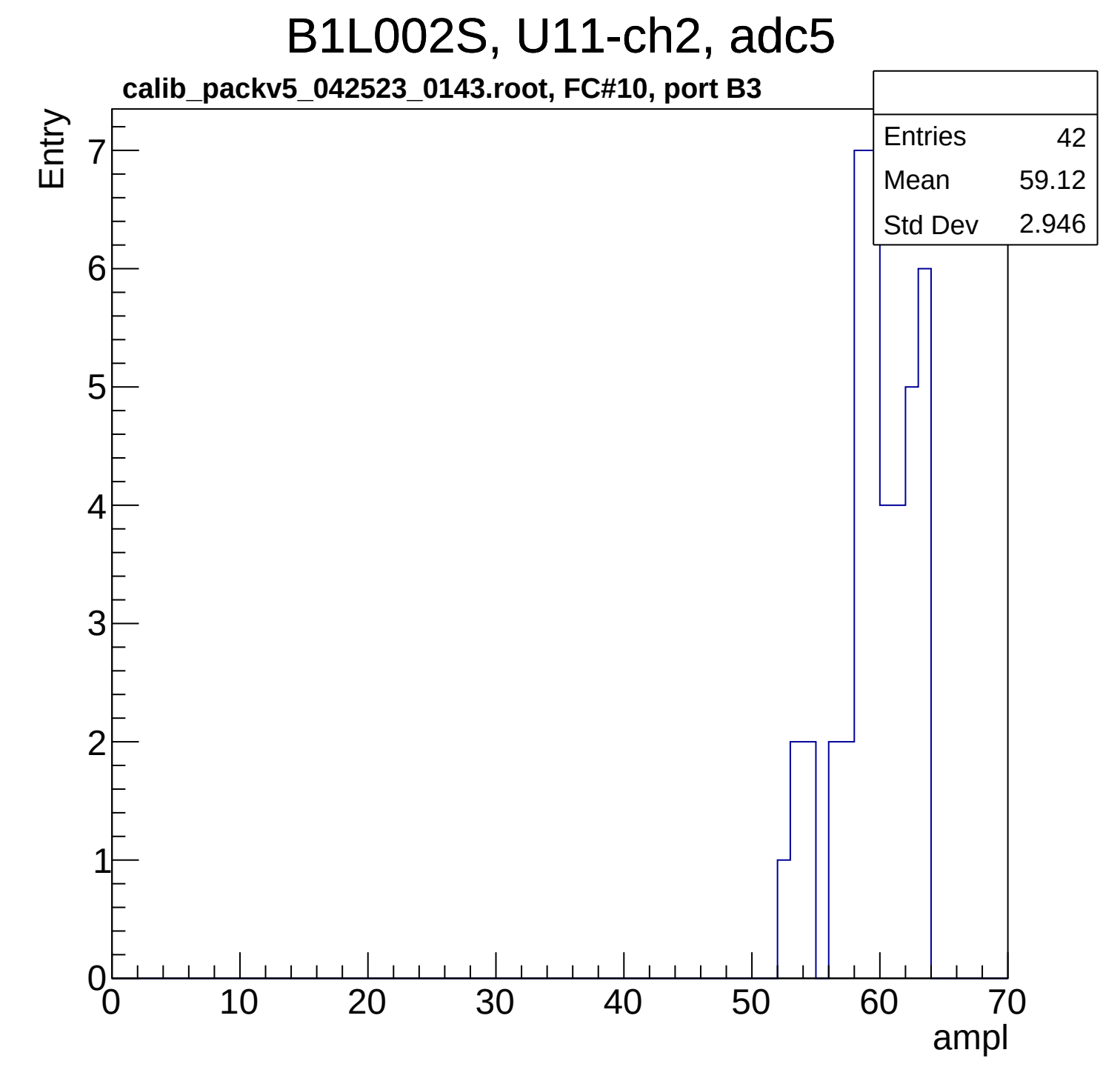
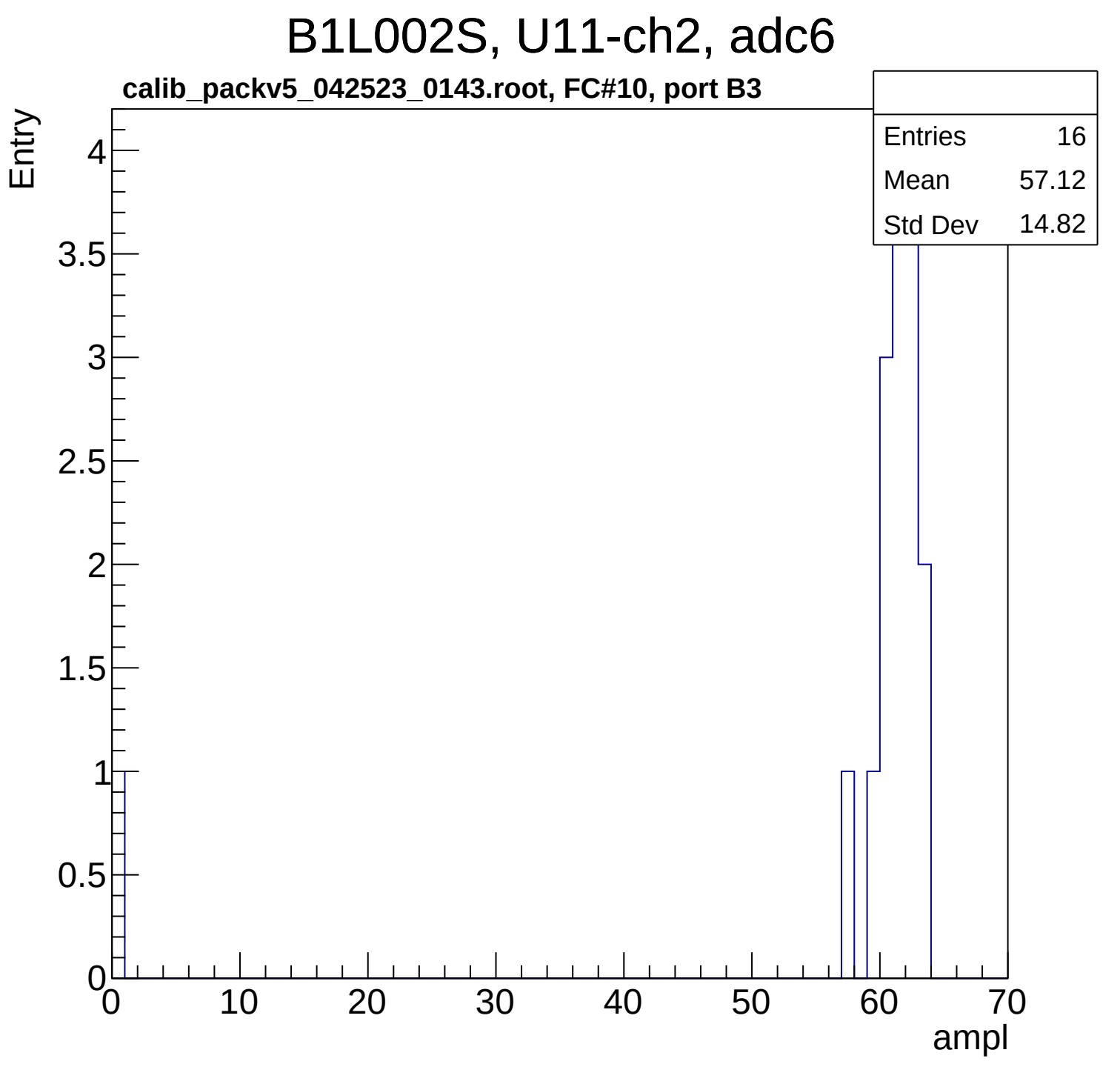
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 54.54
Std Dev 3.512
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.12
Std Dev 2.946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.12
Std Dev 14.82
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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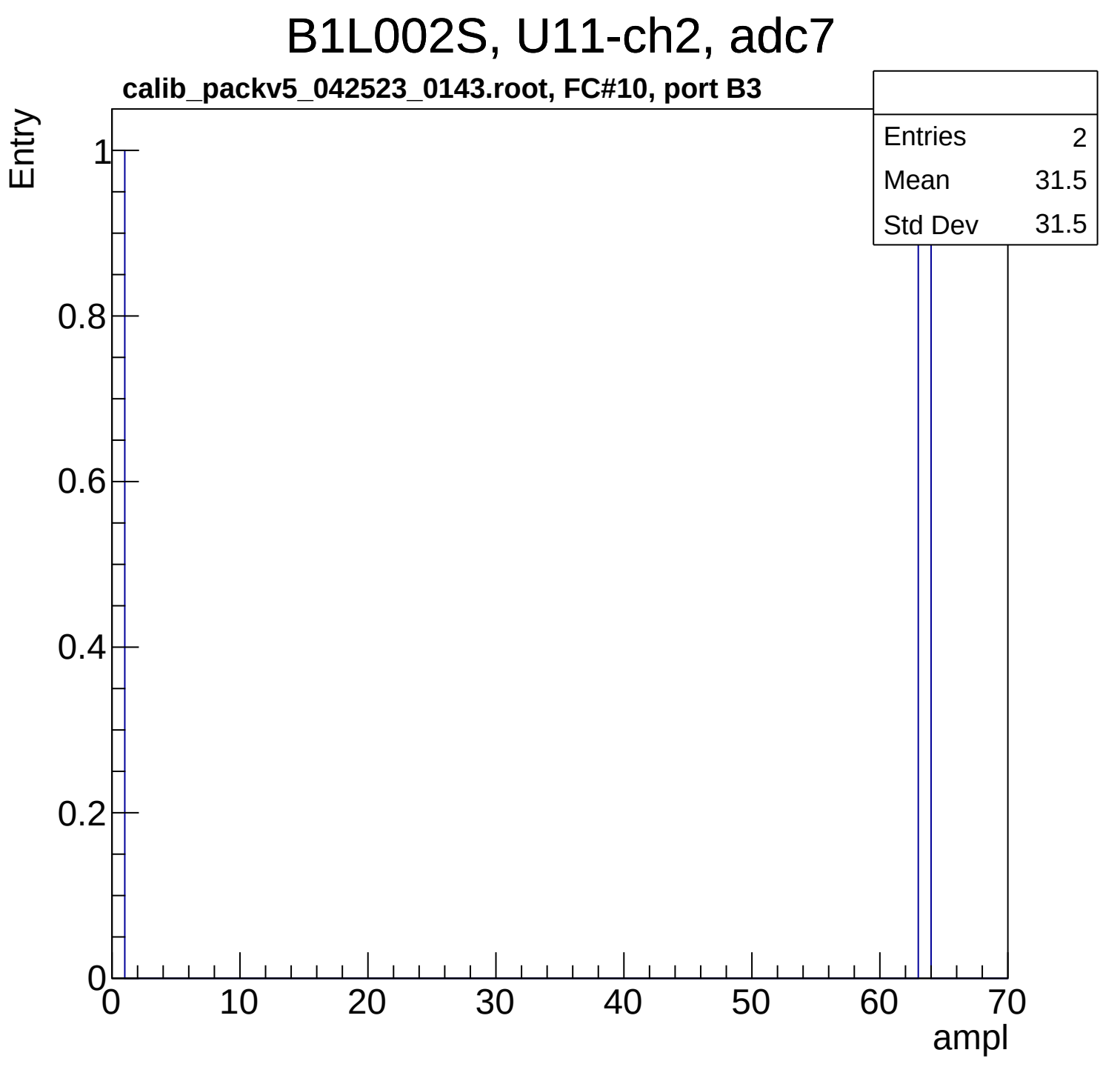
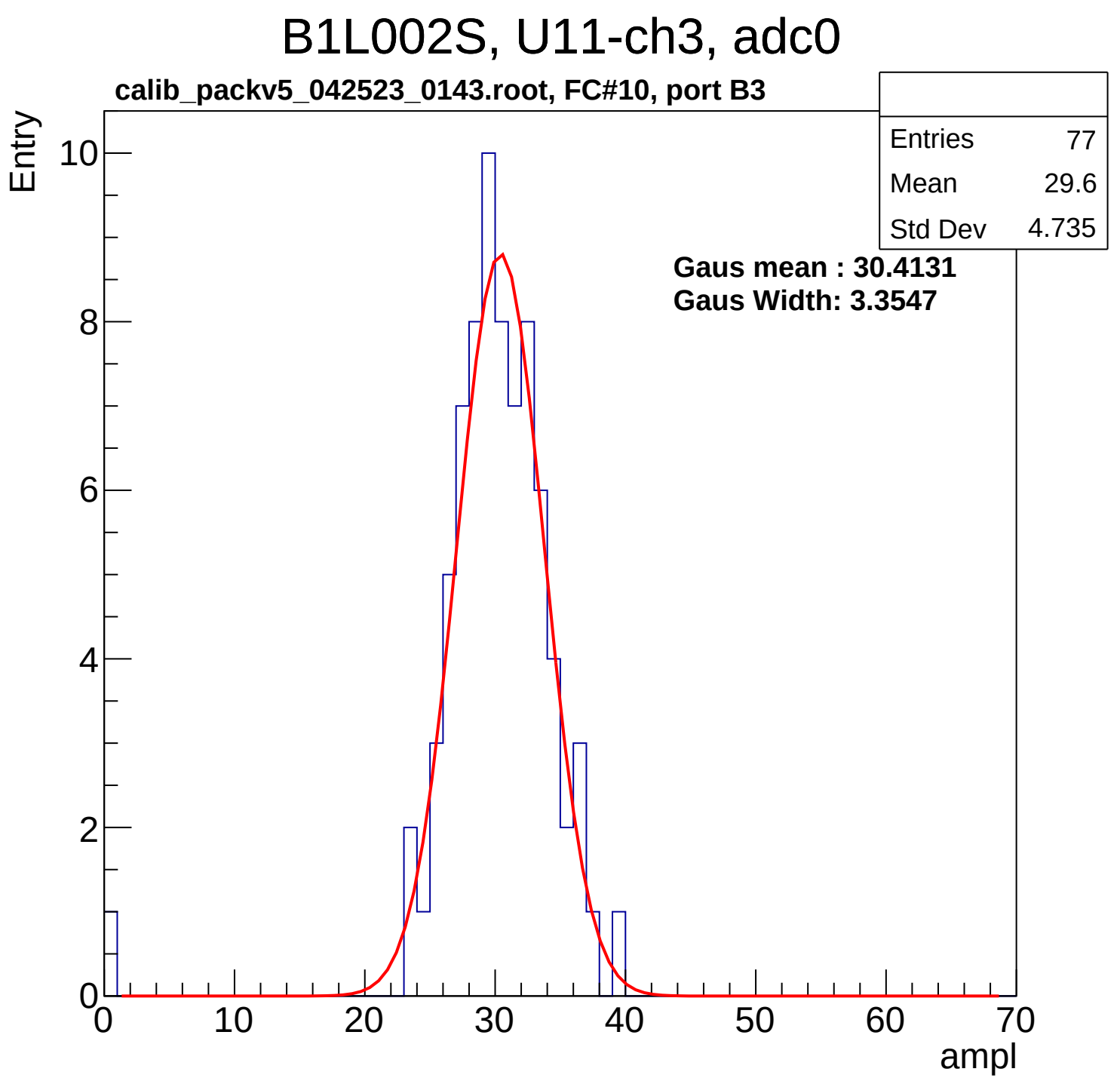

B1L002S, U11-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 29.6
Std Dev 4.735
Gaus mean : 30.4131
Gaus Width: 3.3547
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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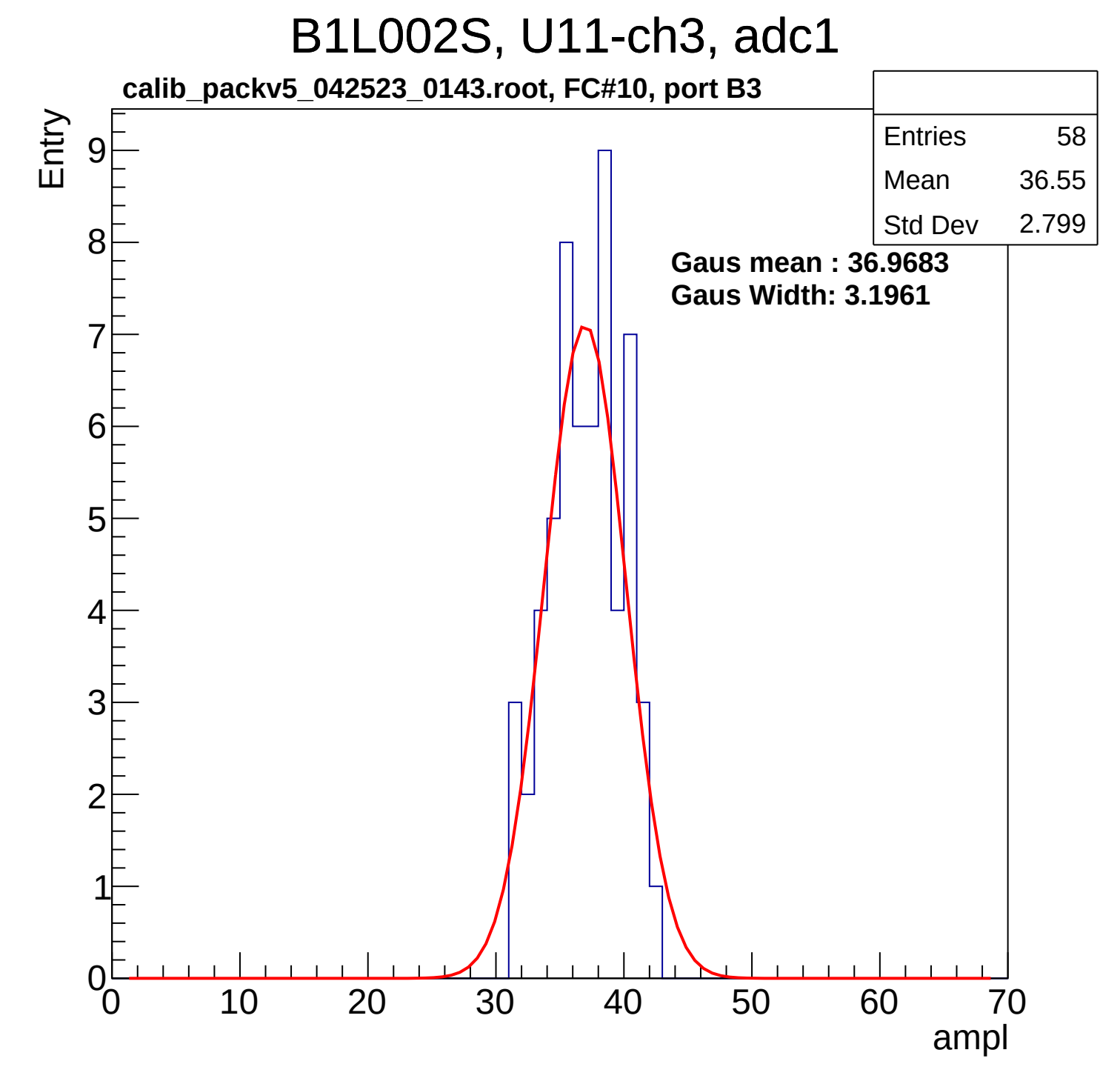
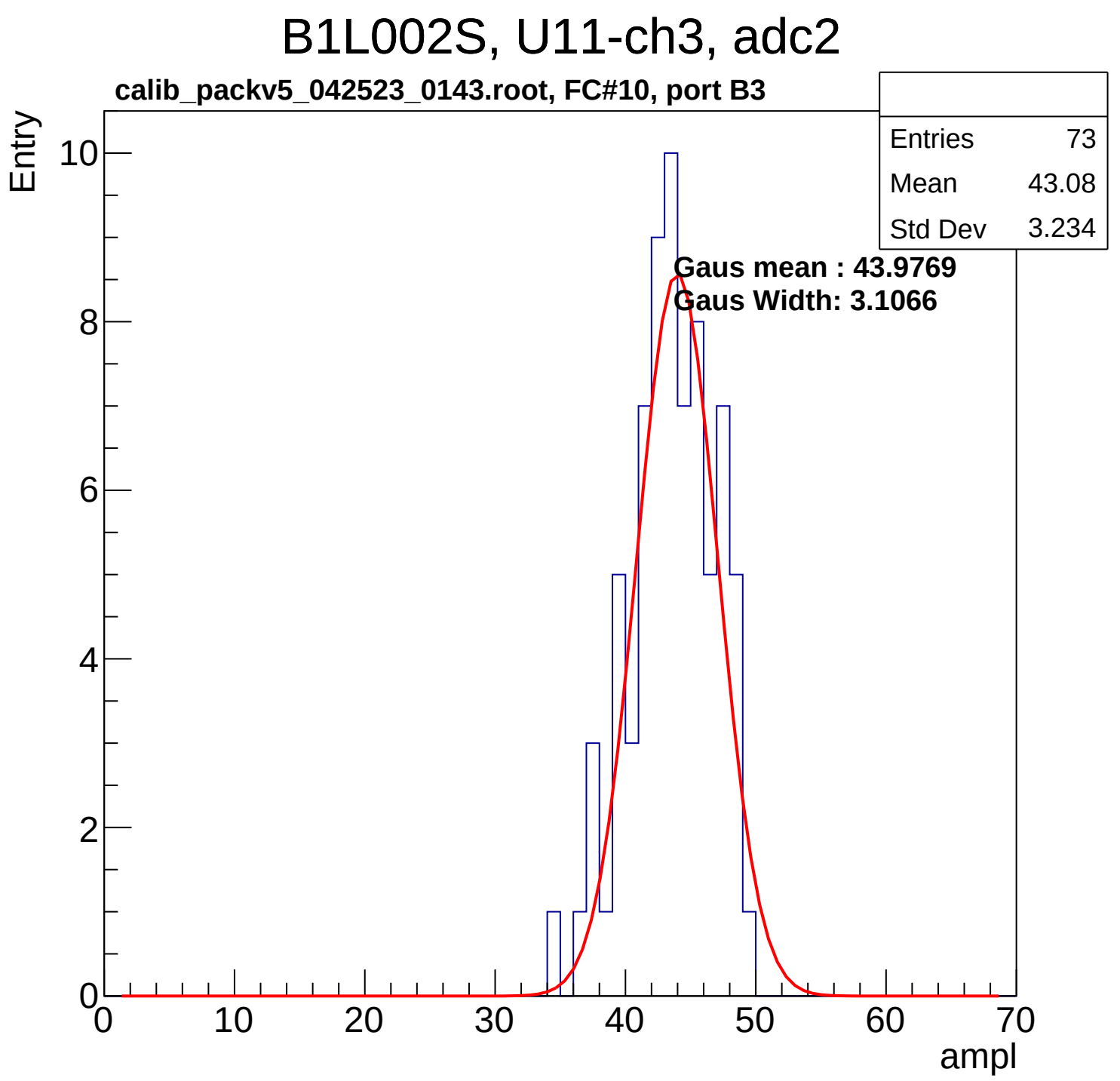
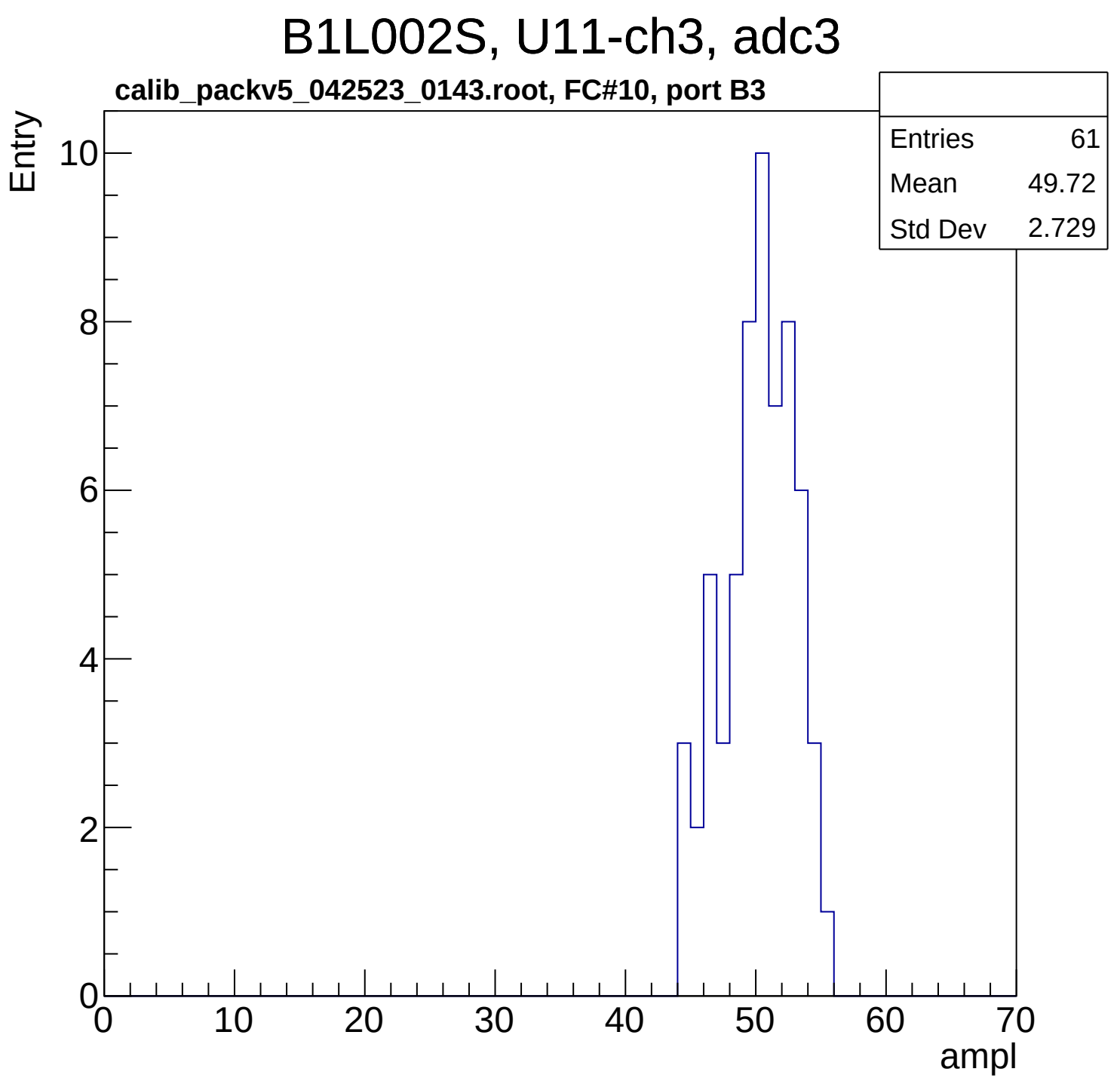
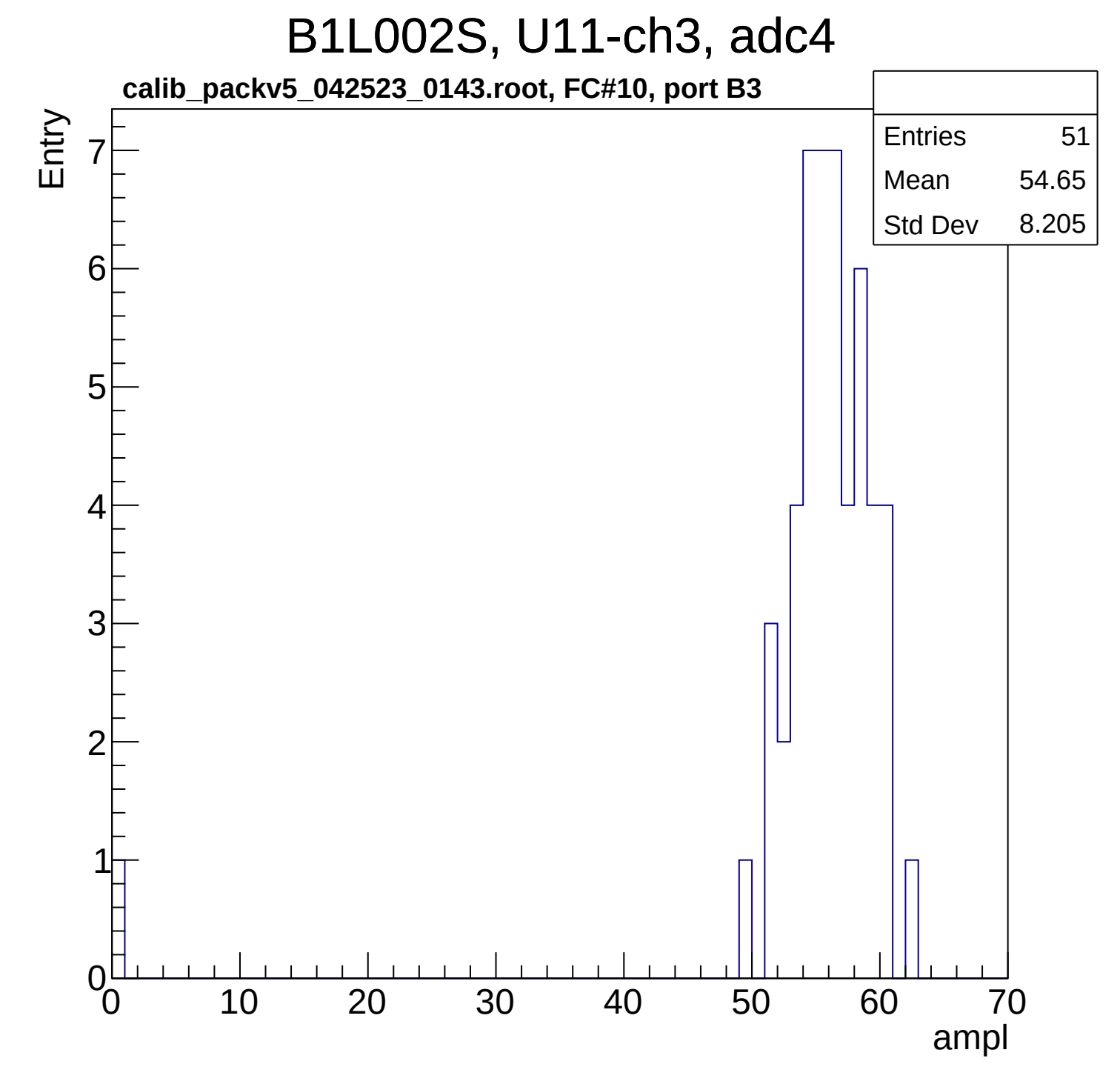

B1L002S, U11-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 36.55
Std Dev 2.799
Gaus mean : 36.9683
Gaus Width: 3.1961
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 43.08
Std Dev 3.234
Gaus mean : 43.9769
Gaus Width: 3.1066
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 49.72
Std Dev 2.729
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.65
Std Dev 8.205
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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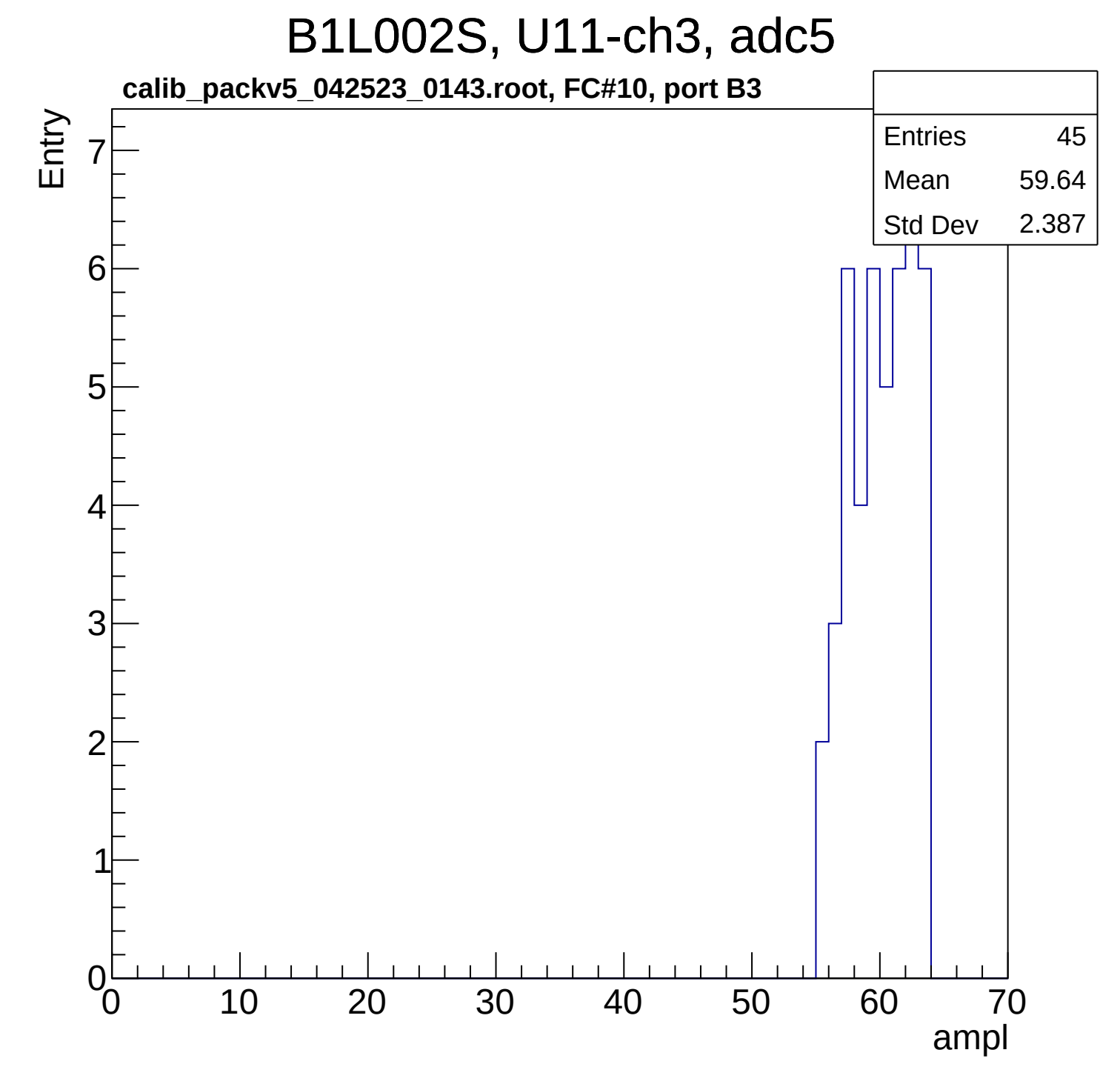
B1L002S, U11-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 59.64
Std Dev 2.387
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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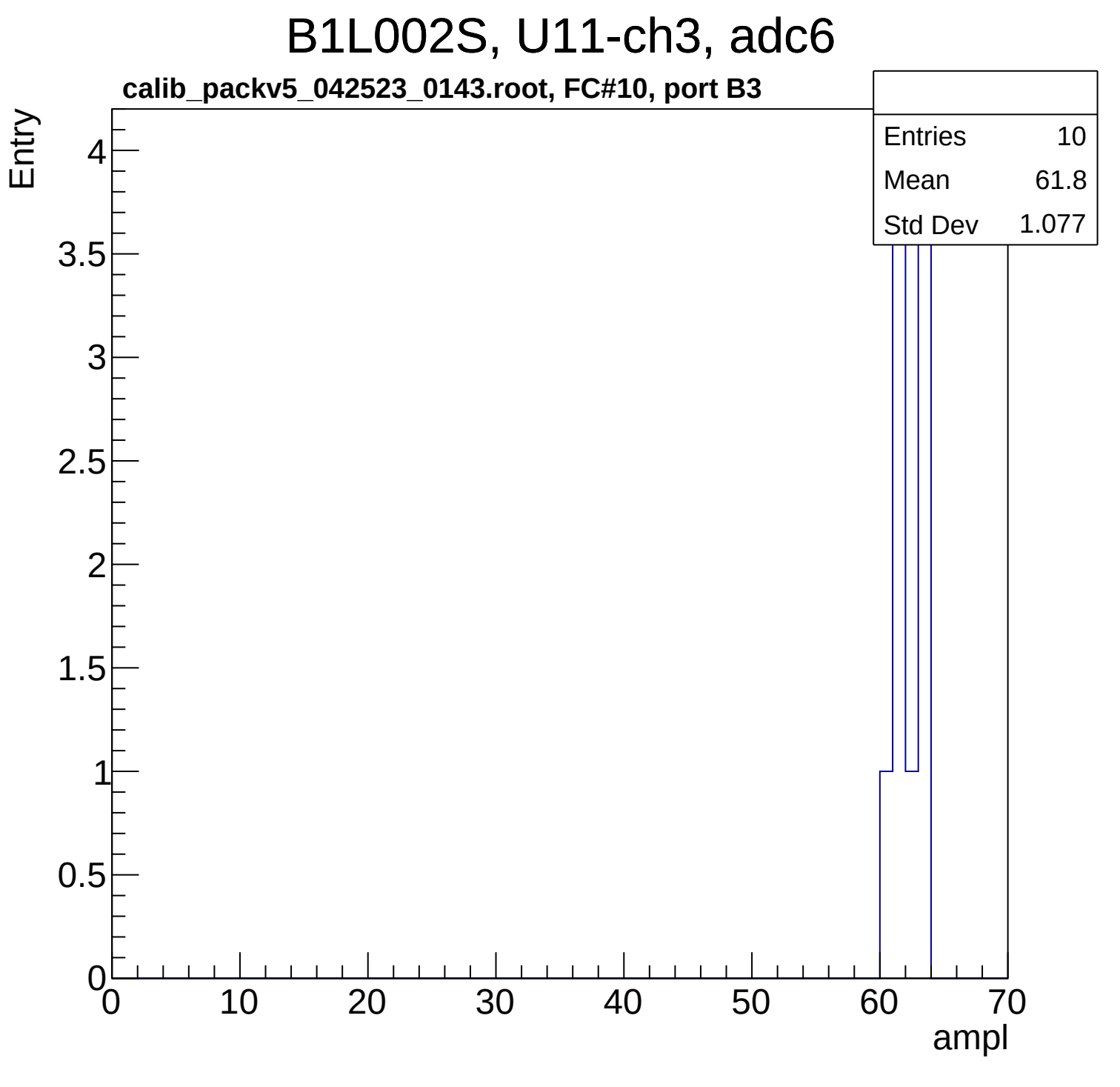
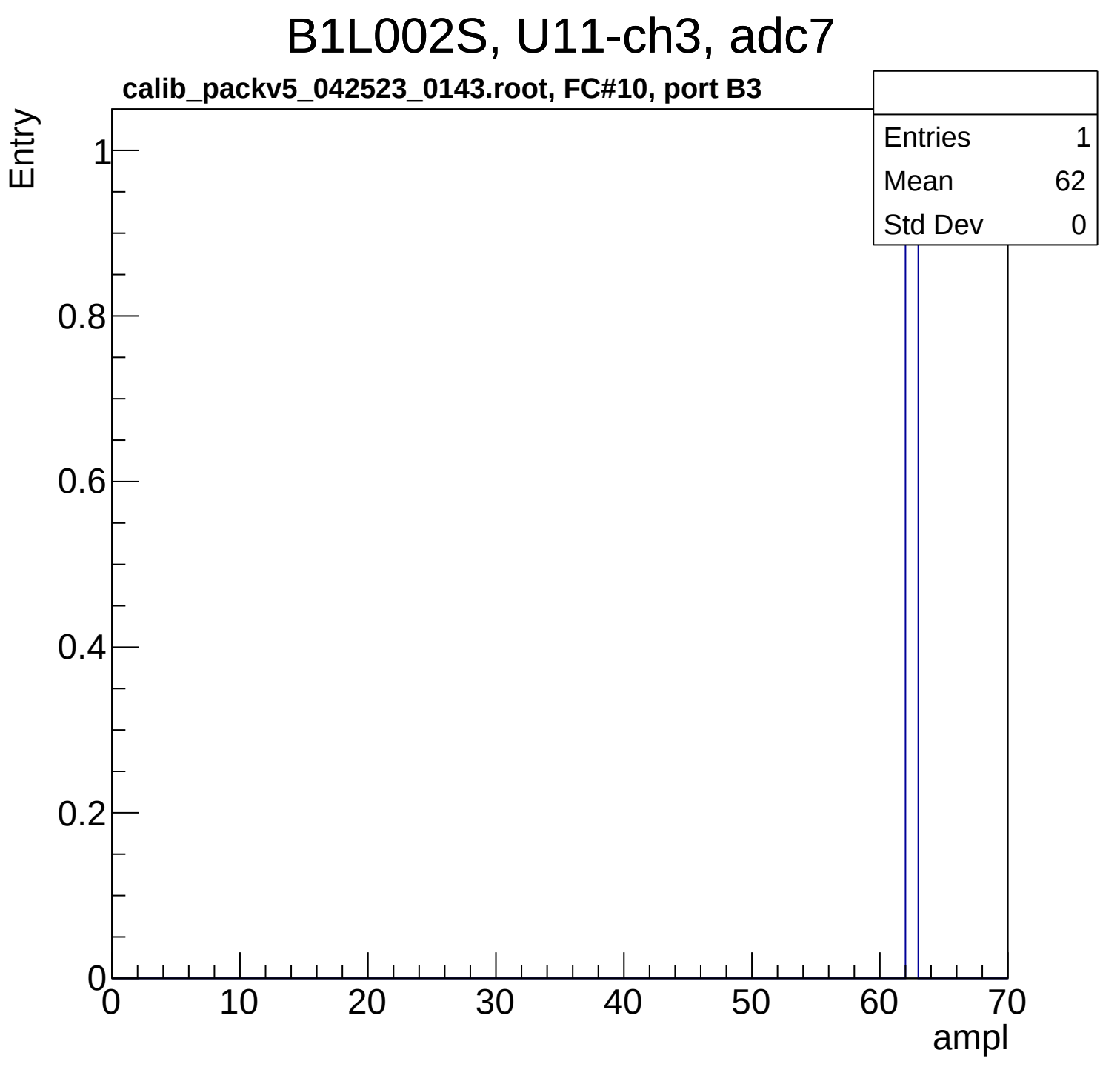
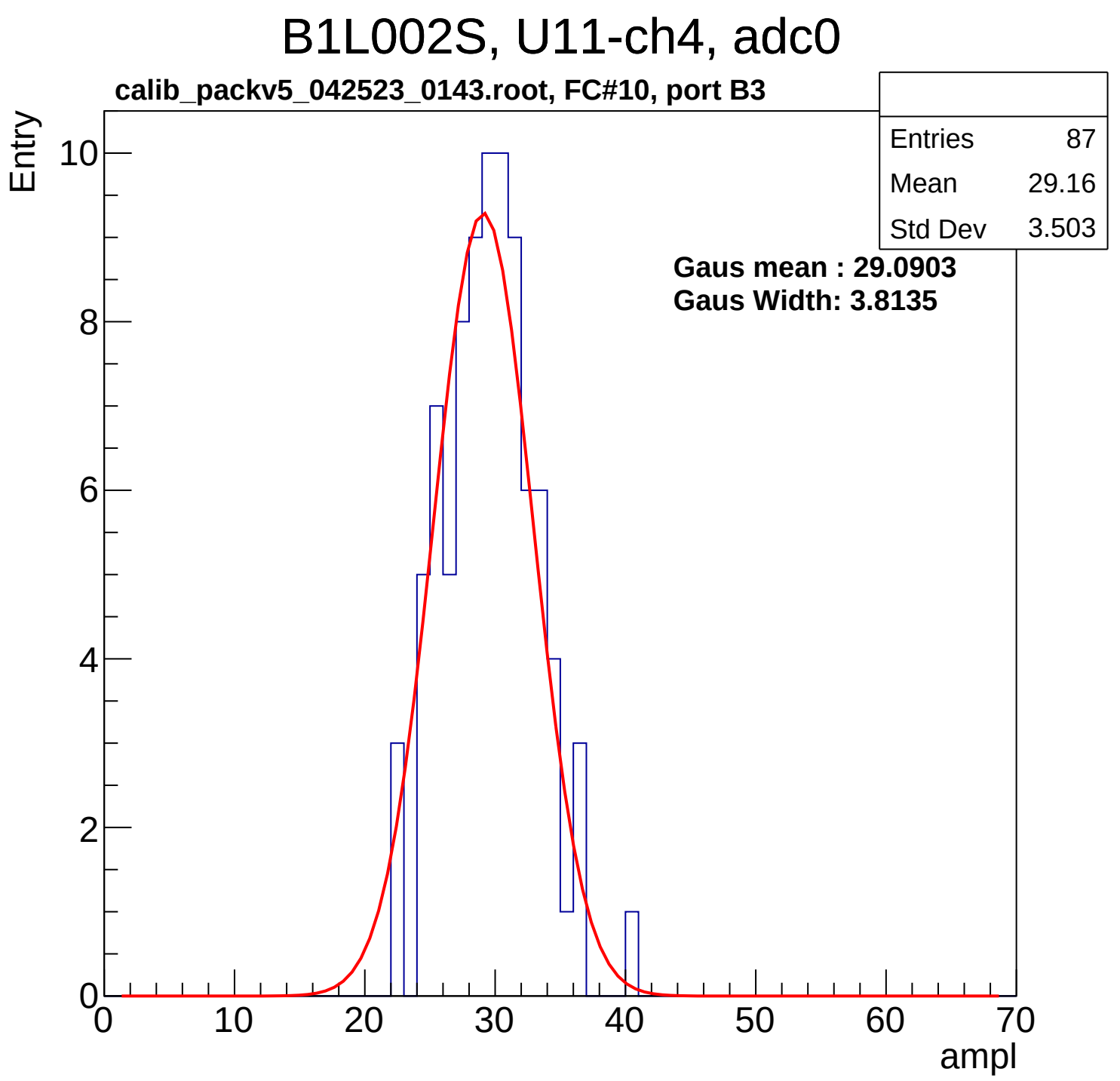
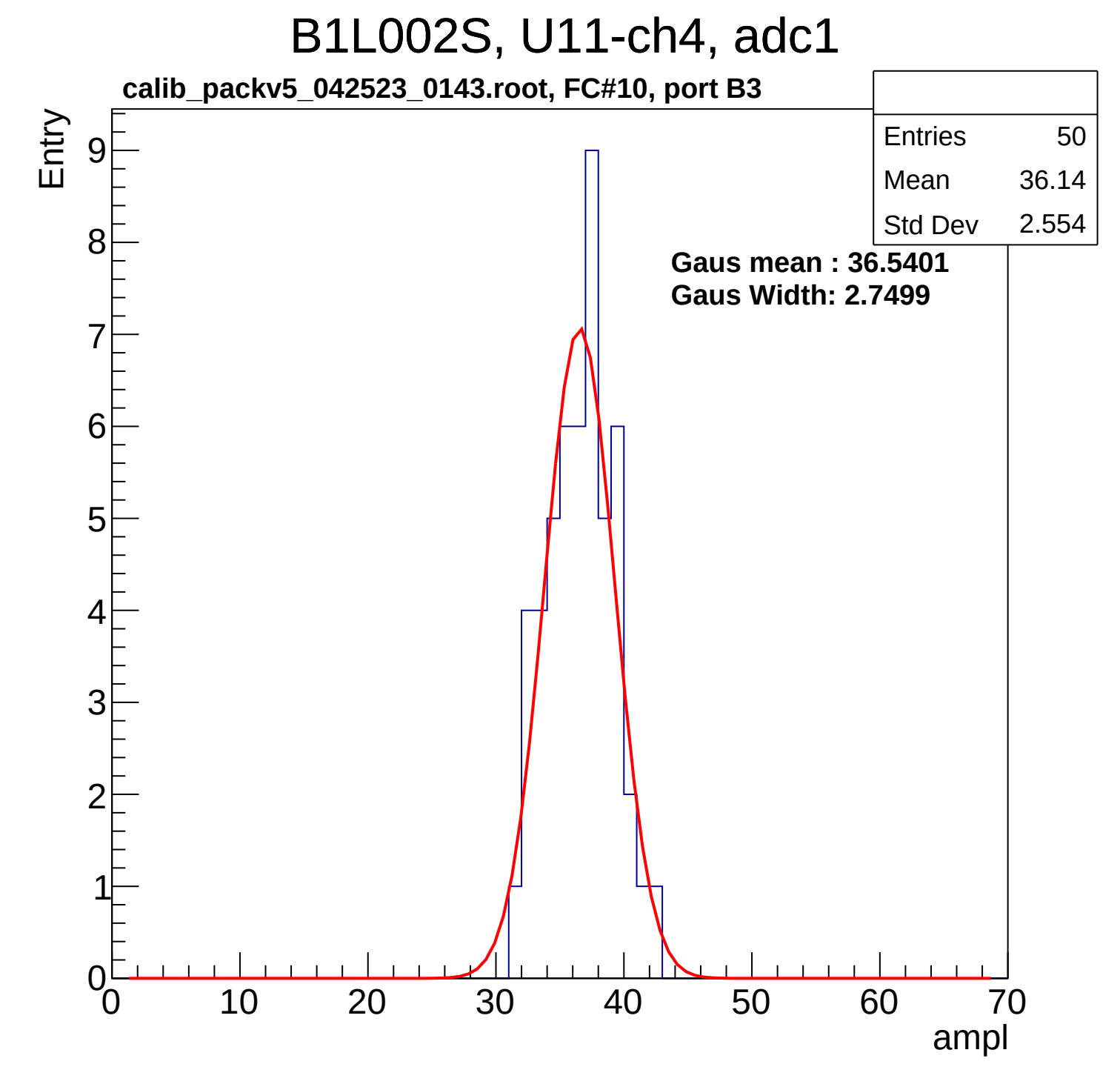

B1L002S, U11-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.8
Std Dev 1.077
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 87
Mean 29.16
Std Dev 3.503
Gaus mean : 29.0903
Gaus Width: 3.8135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 36.14
Std Dev 2.554
Gaus mean : 36.5401
Gaus Width: 2.7499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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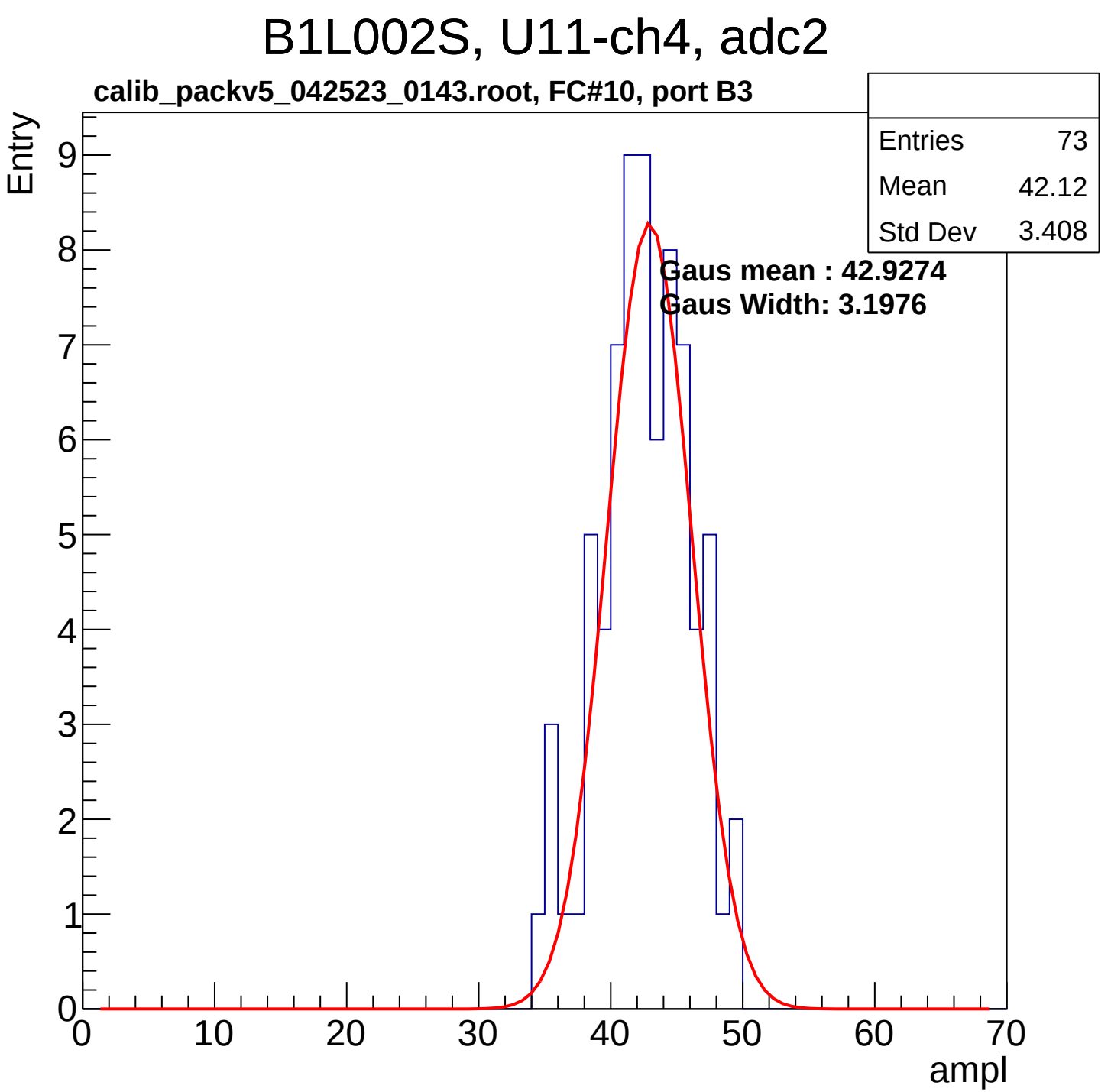
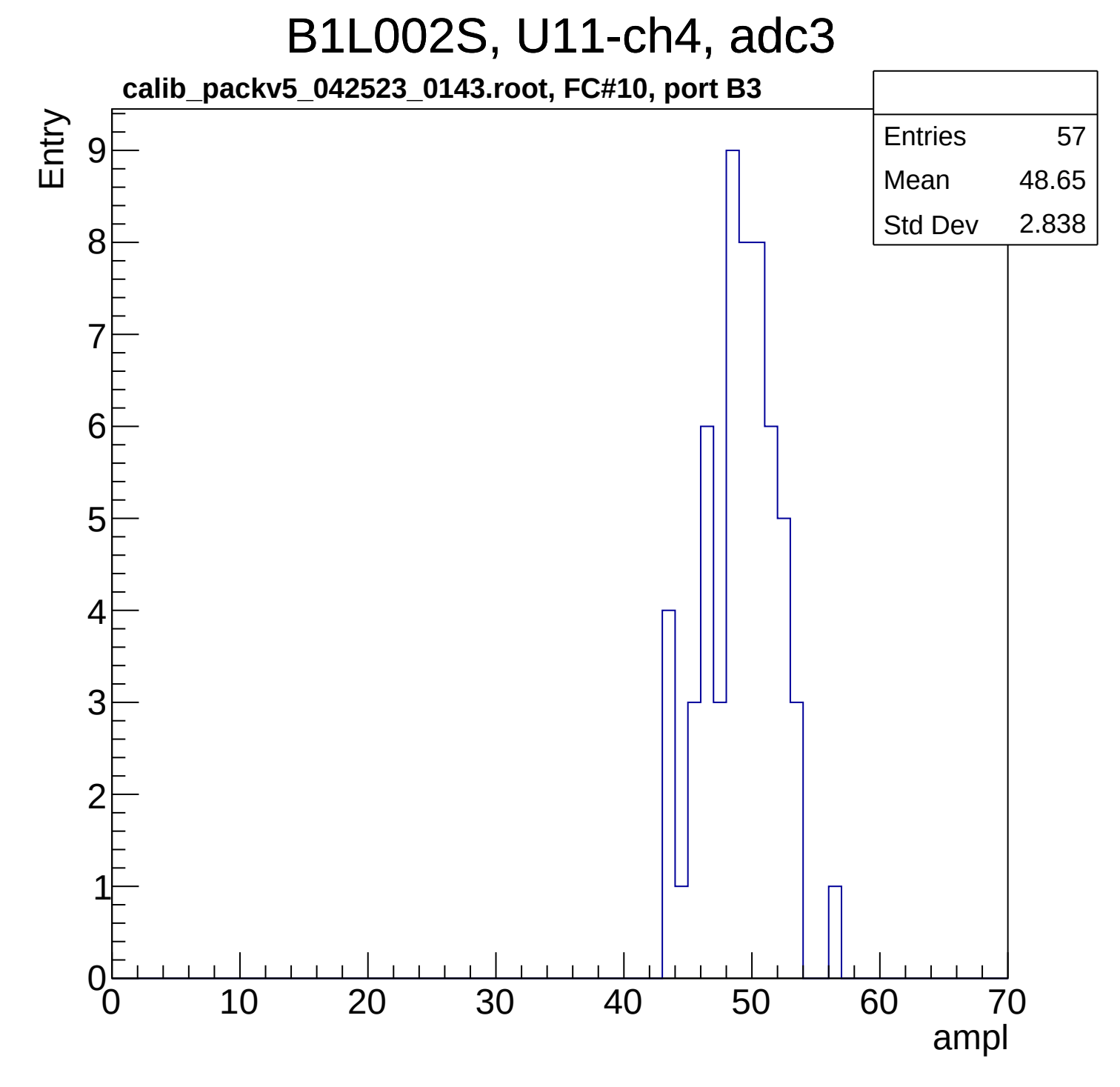

B1L002S, U11-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 42.12
Std Dev 3.408
Gaus mean : 42.9274
Gaus Width: 3.1976
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.65
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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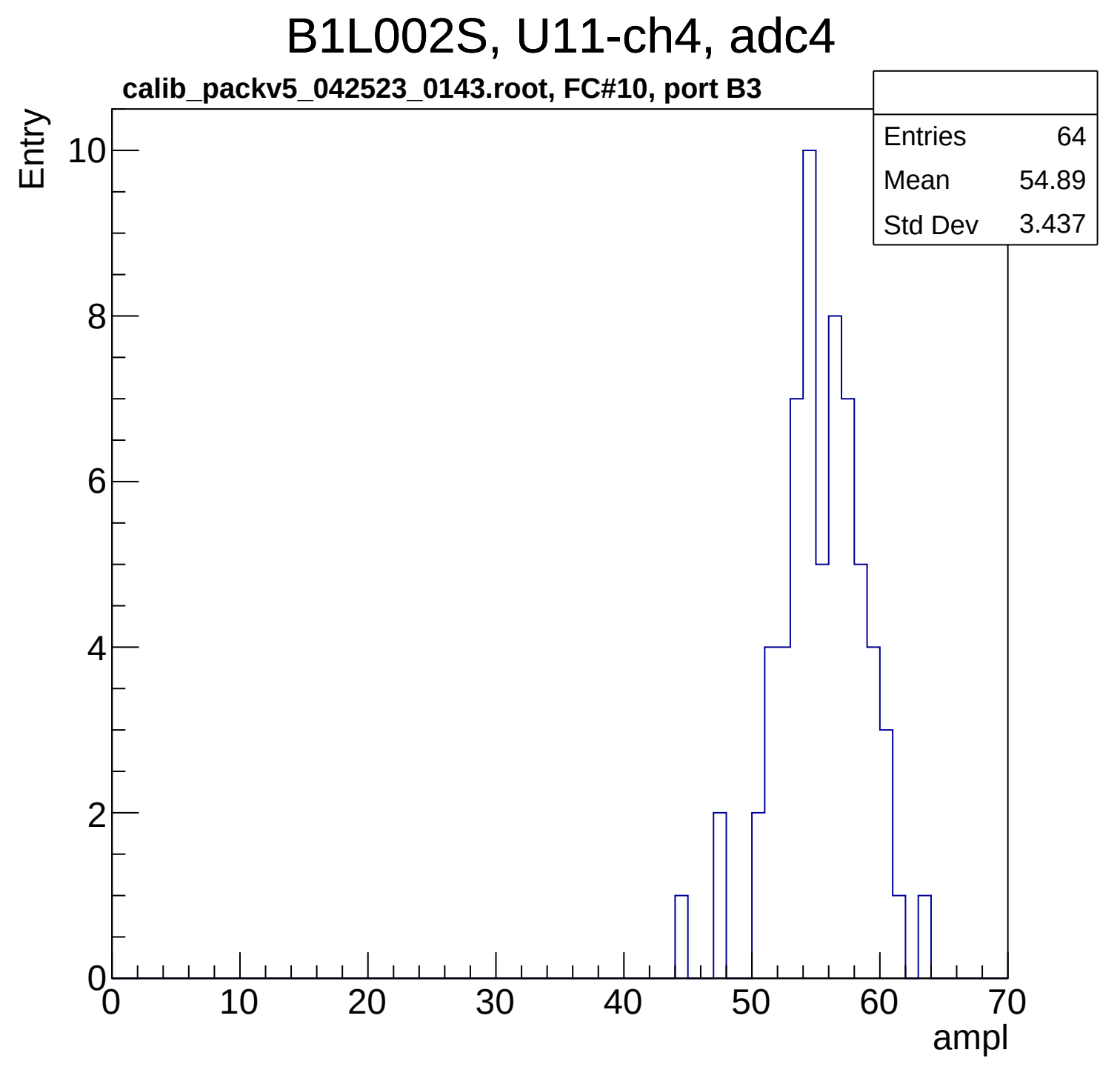
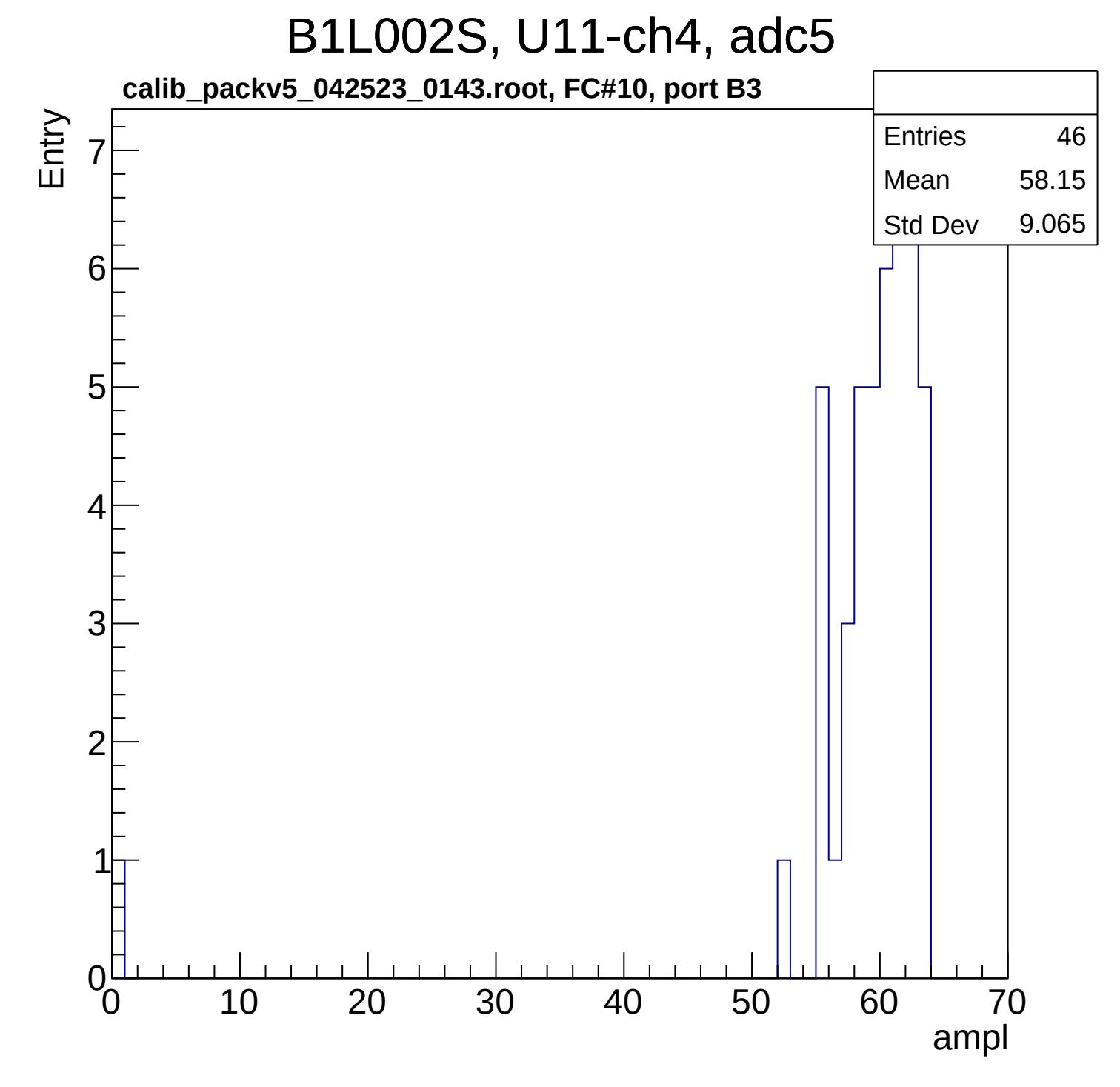
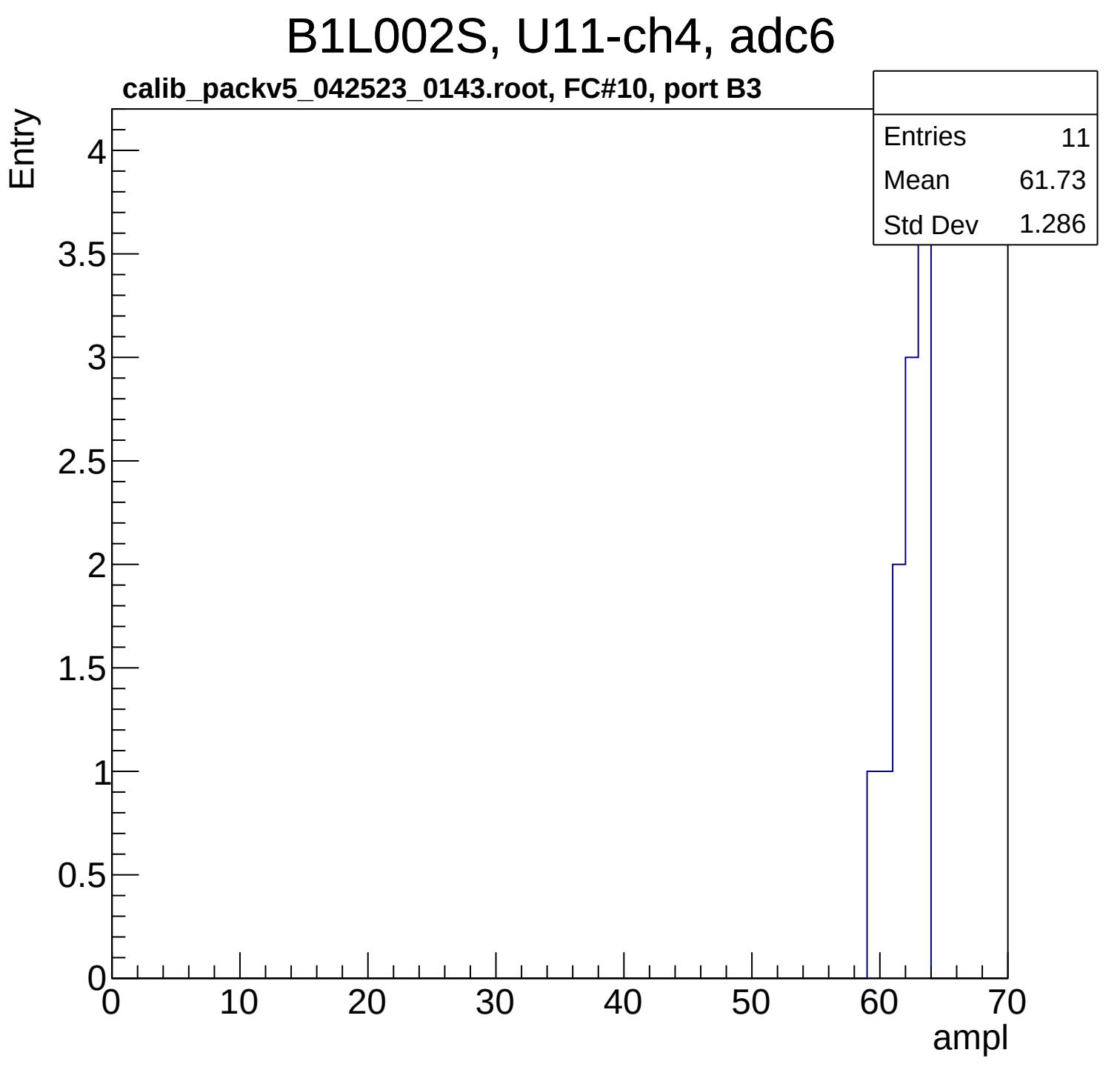

B1L002S, U11-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 54.89
Std Dev 3.437
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.15
Std Dev 9.065
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.73
Std Dev 1.286
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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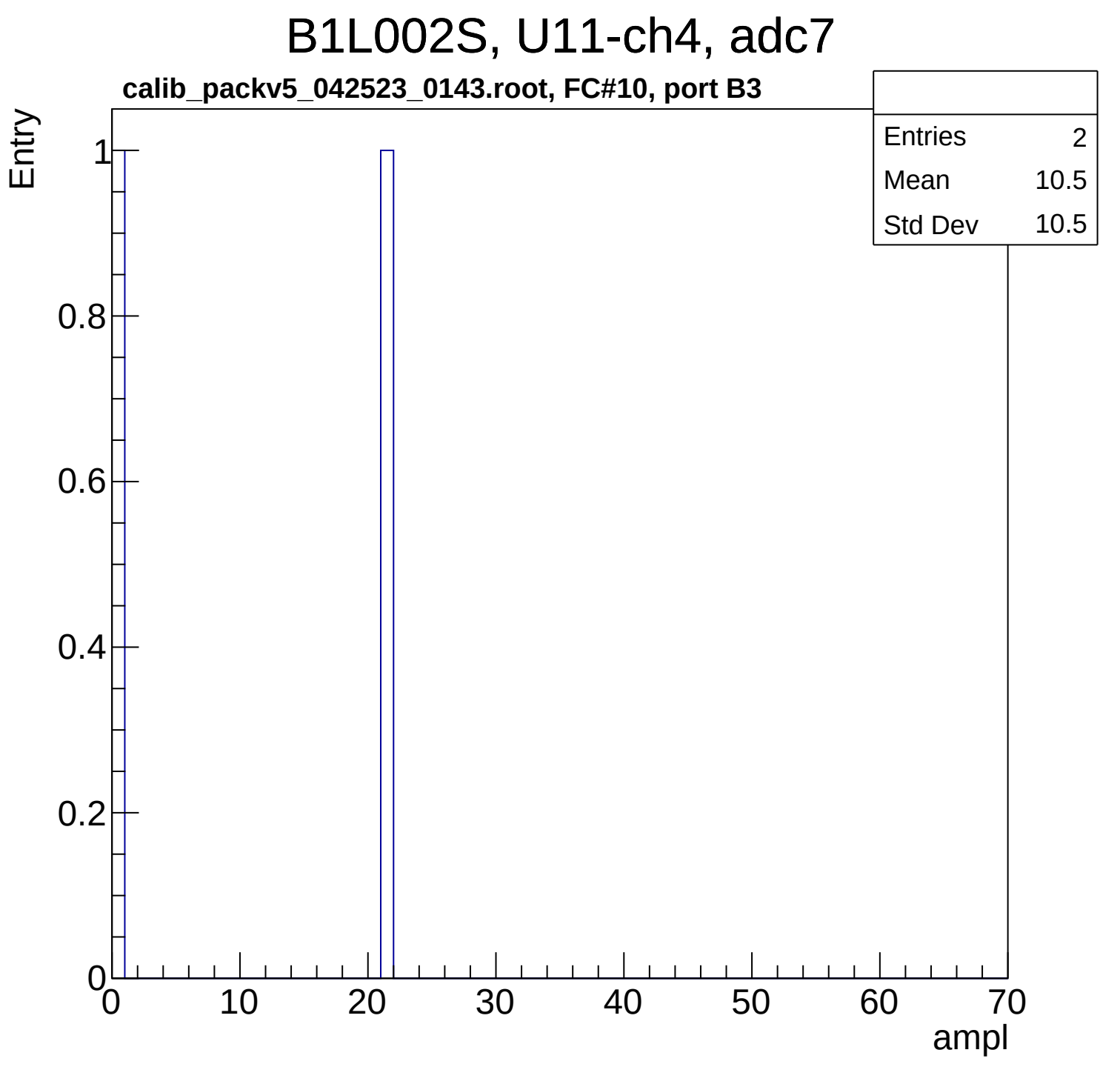
B1L002S, U11-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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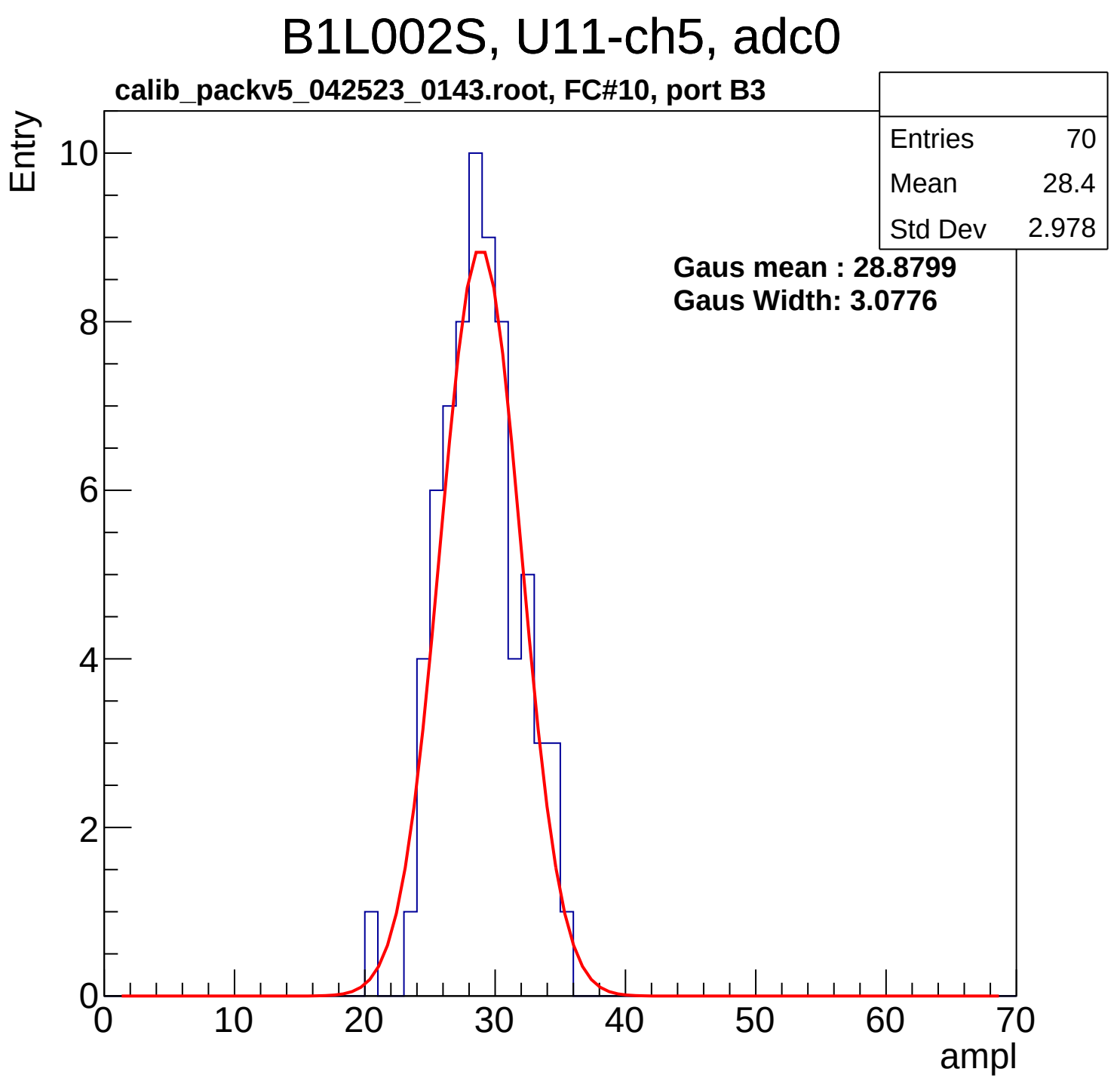
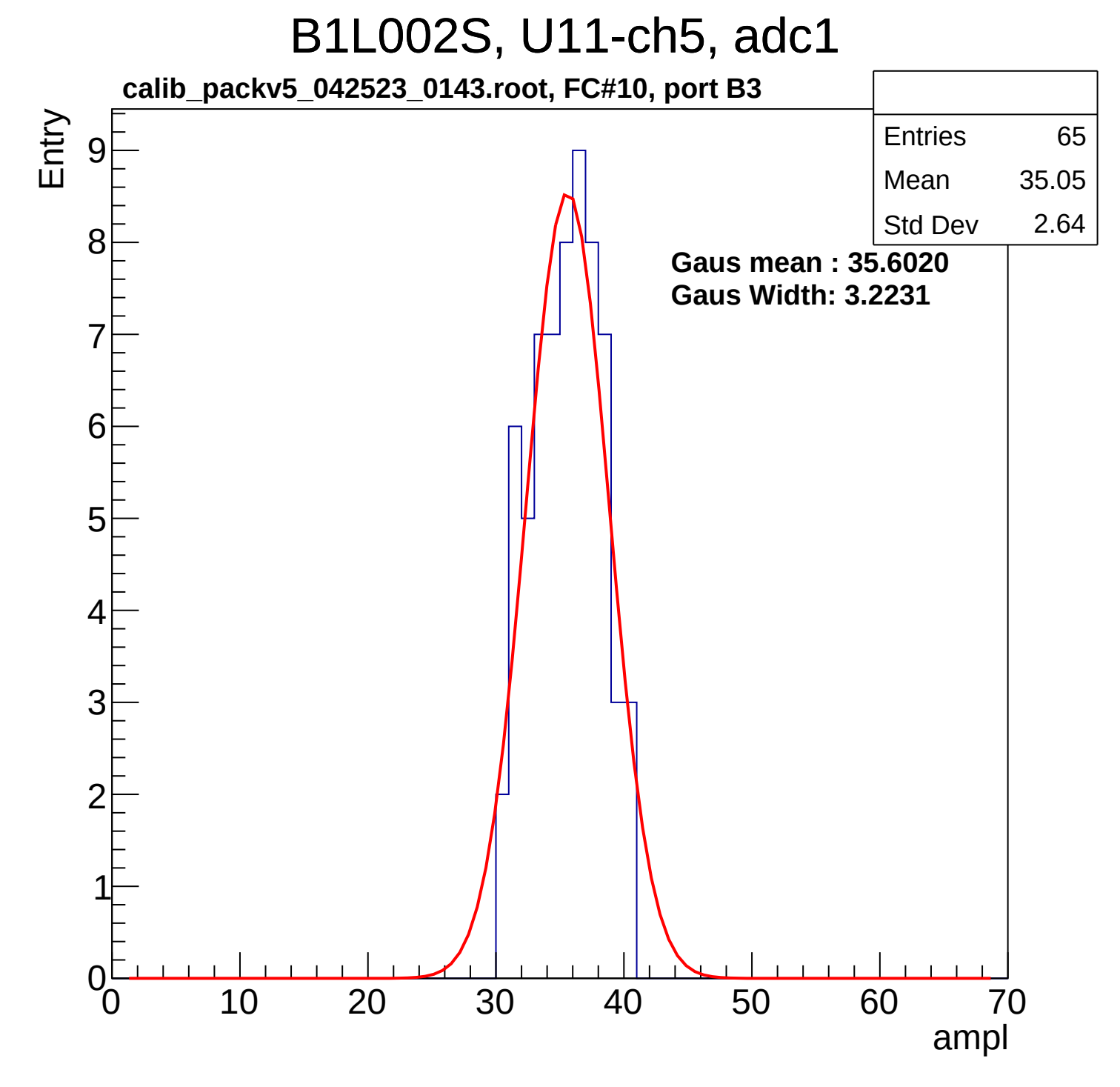
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.4
Std Dev 2.978
Gaus mean : 28.8799
Gaus Width: 3.0776
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.05
Std Dev 2.64
Gaus mean : 35.6020
Gaus Width: 3.2231
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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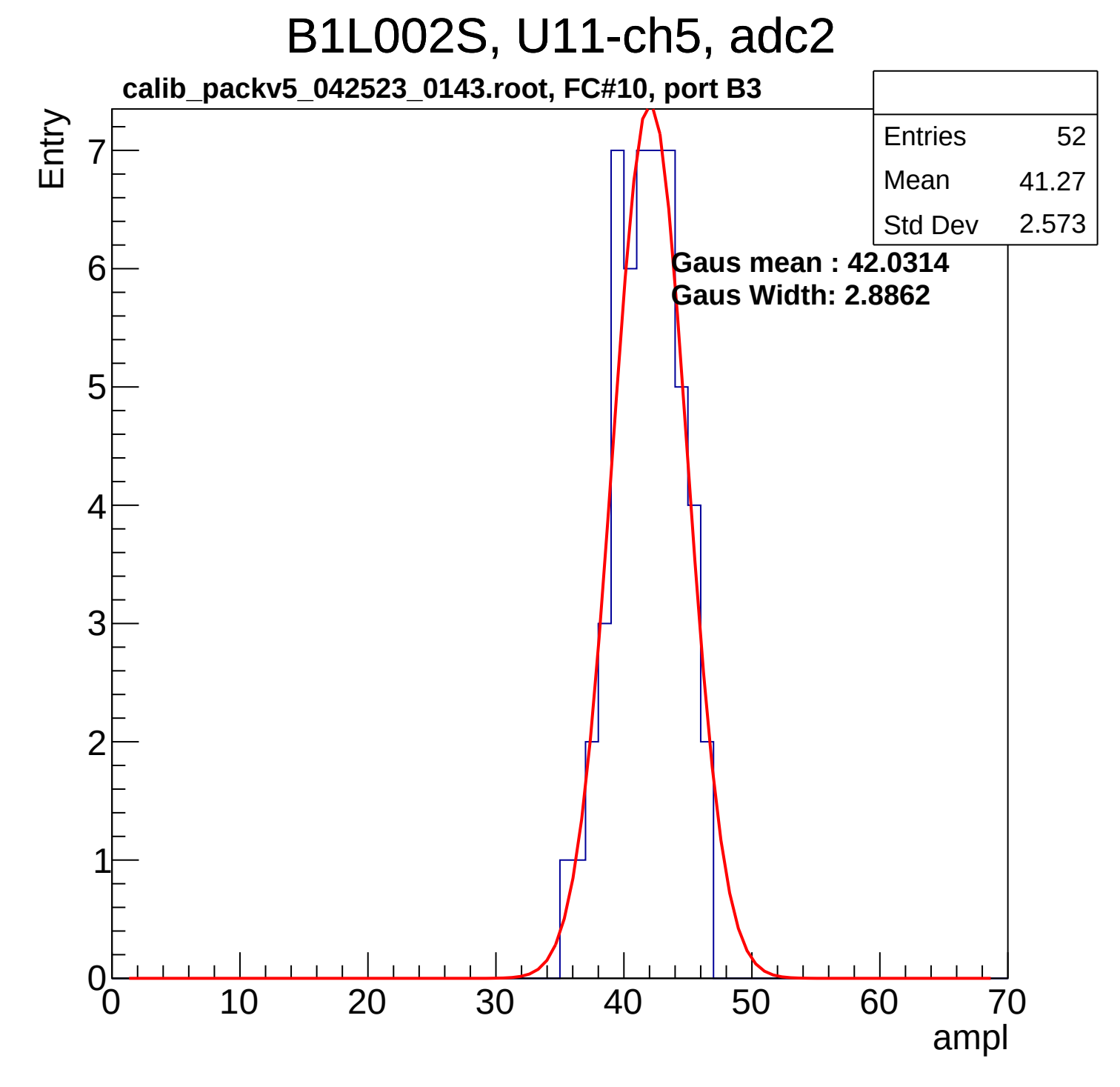
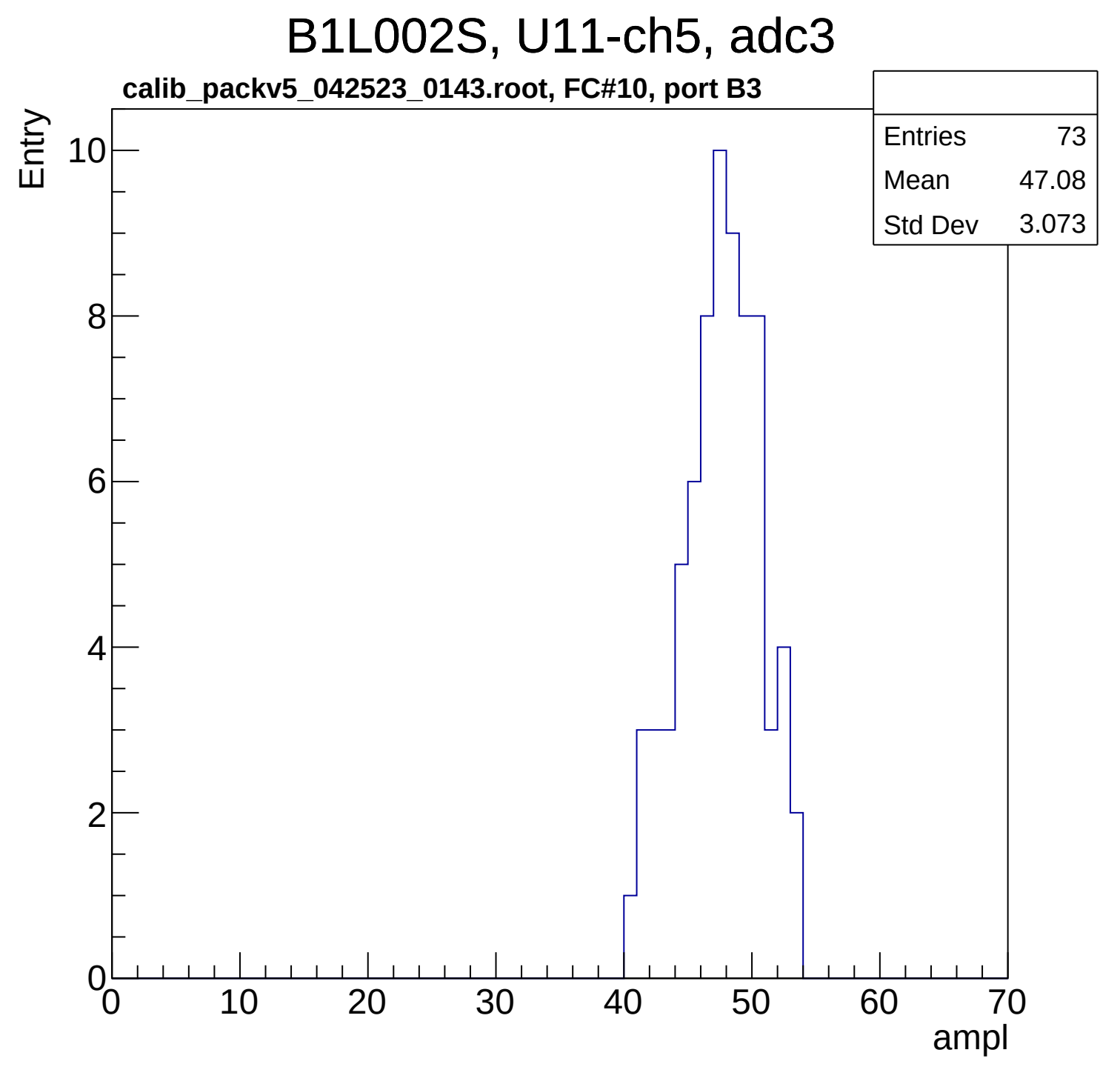
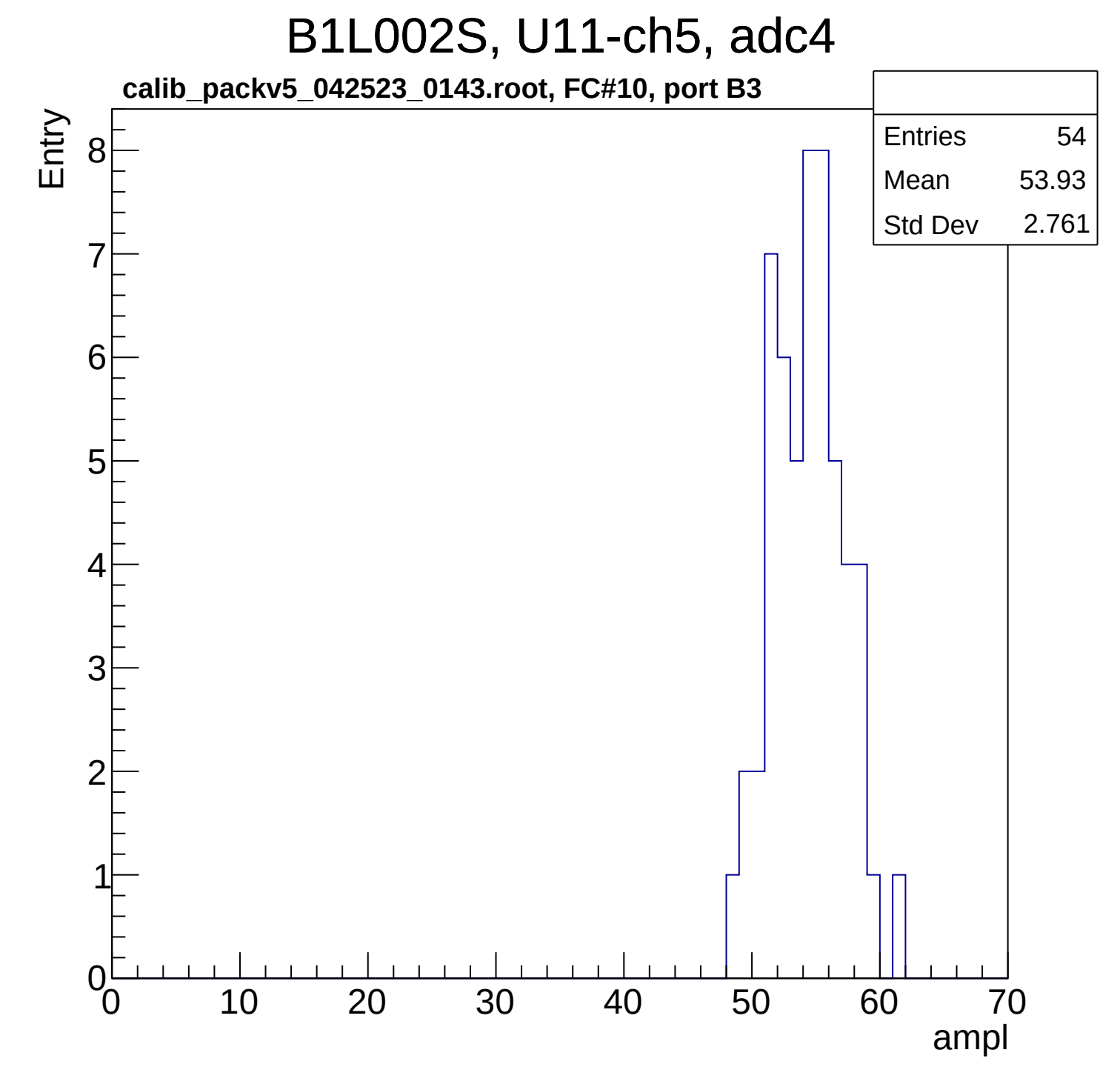

B1L002S, U11-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.27
Std Dev 2.573
Gaus mean : 42.0314
Gaus Width: 2.8862
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 47.08
Std Dev 3.073
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.93
Std Dev 2.761
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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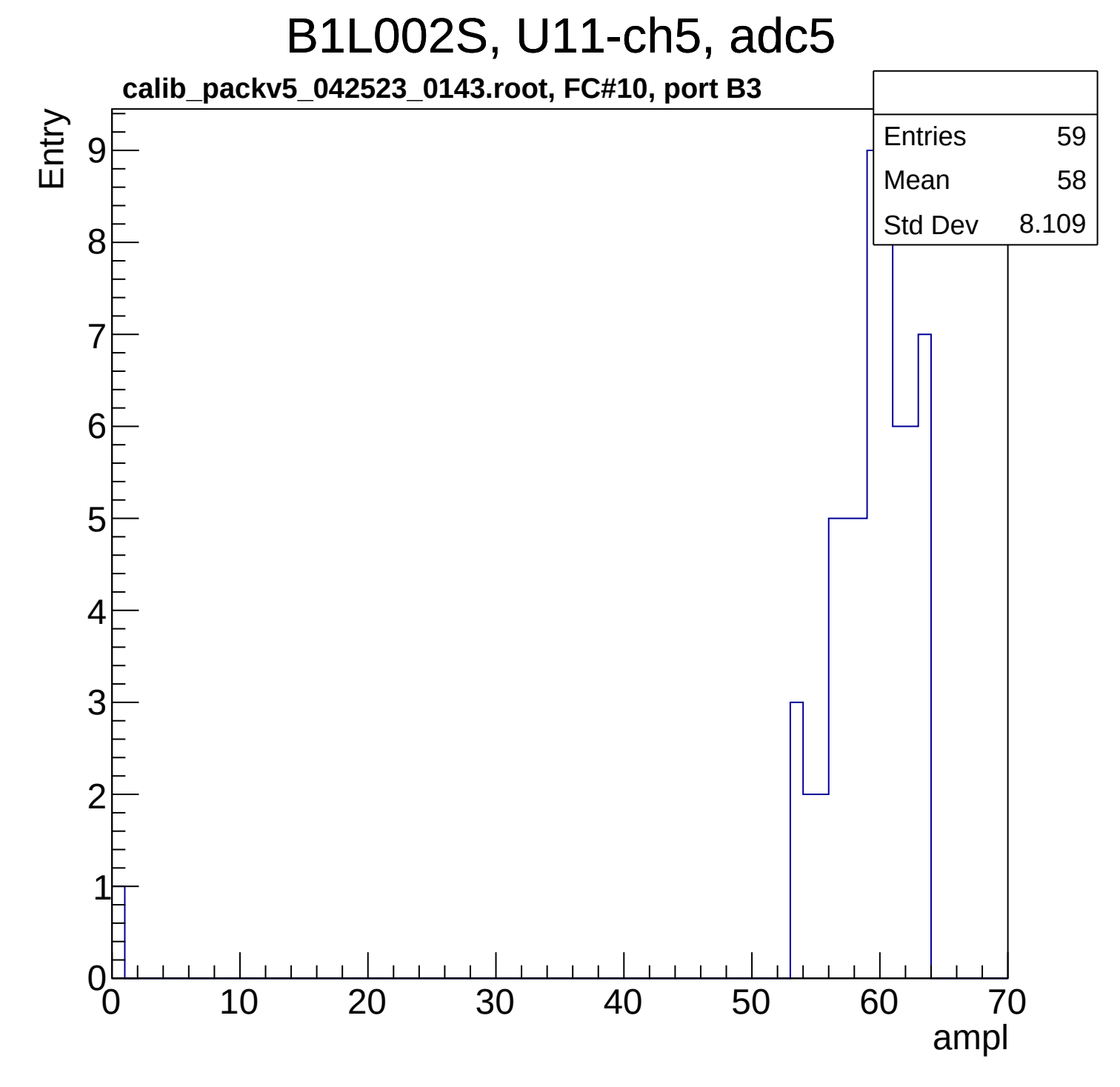
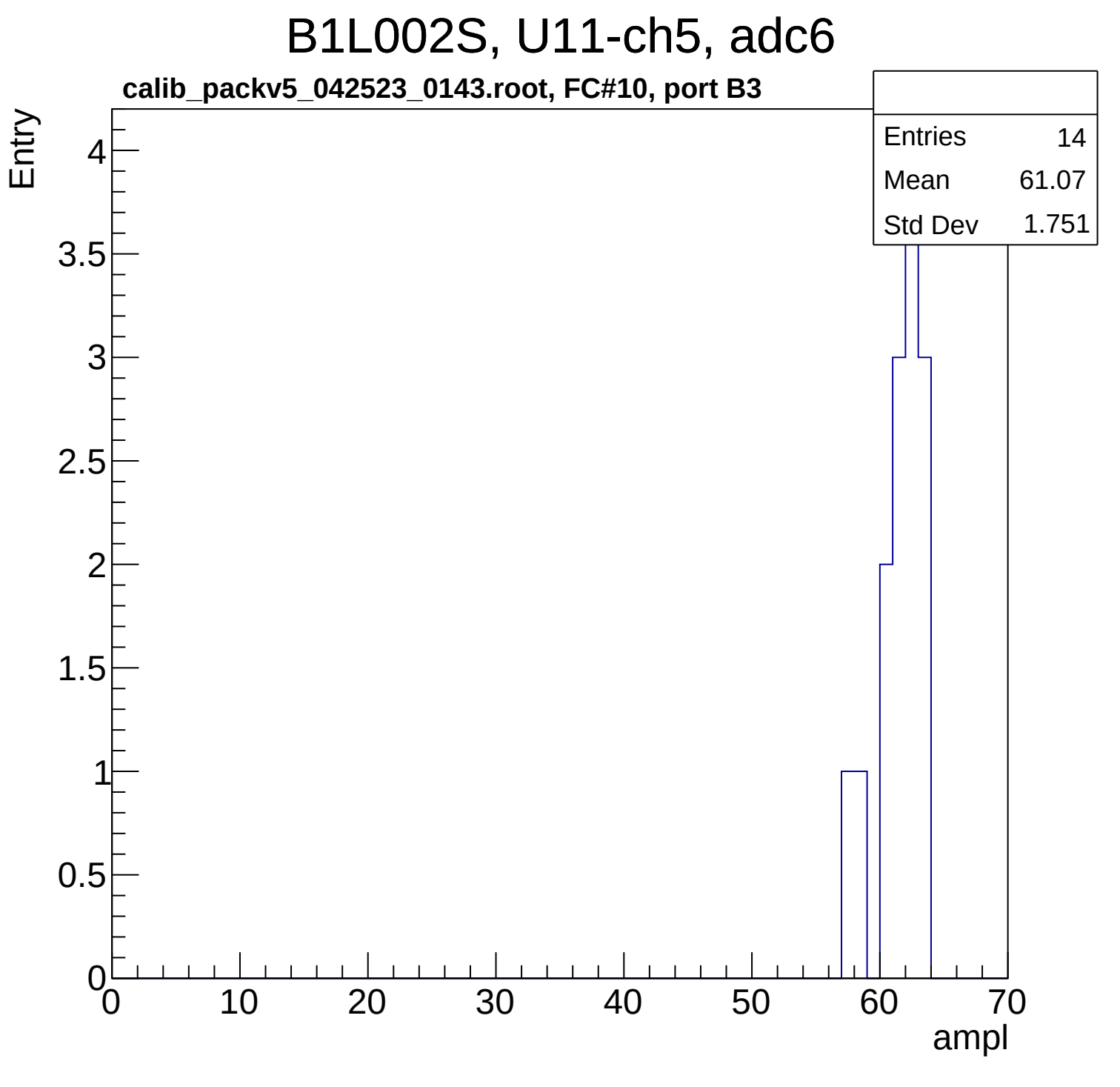

B1L002S, U11-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58
Std Dev 8.109
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.07
Std Dev 1.751
Entry
ampl
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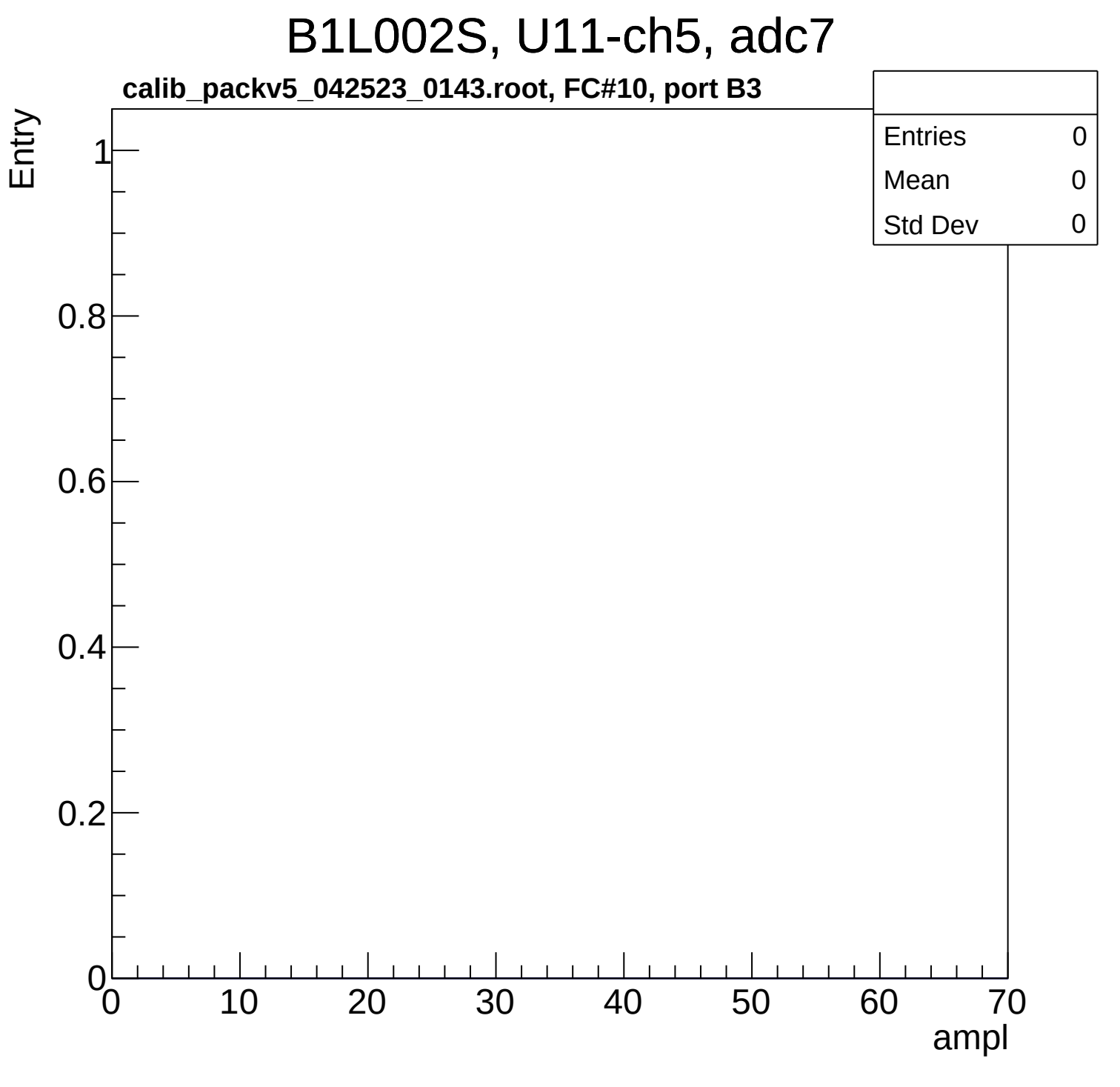
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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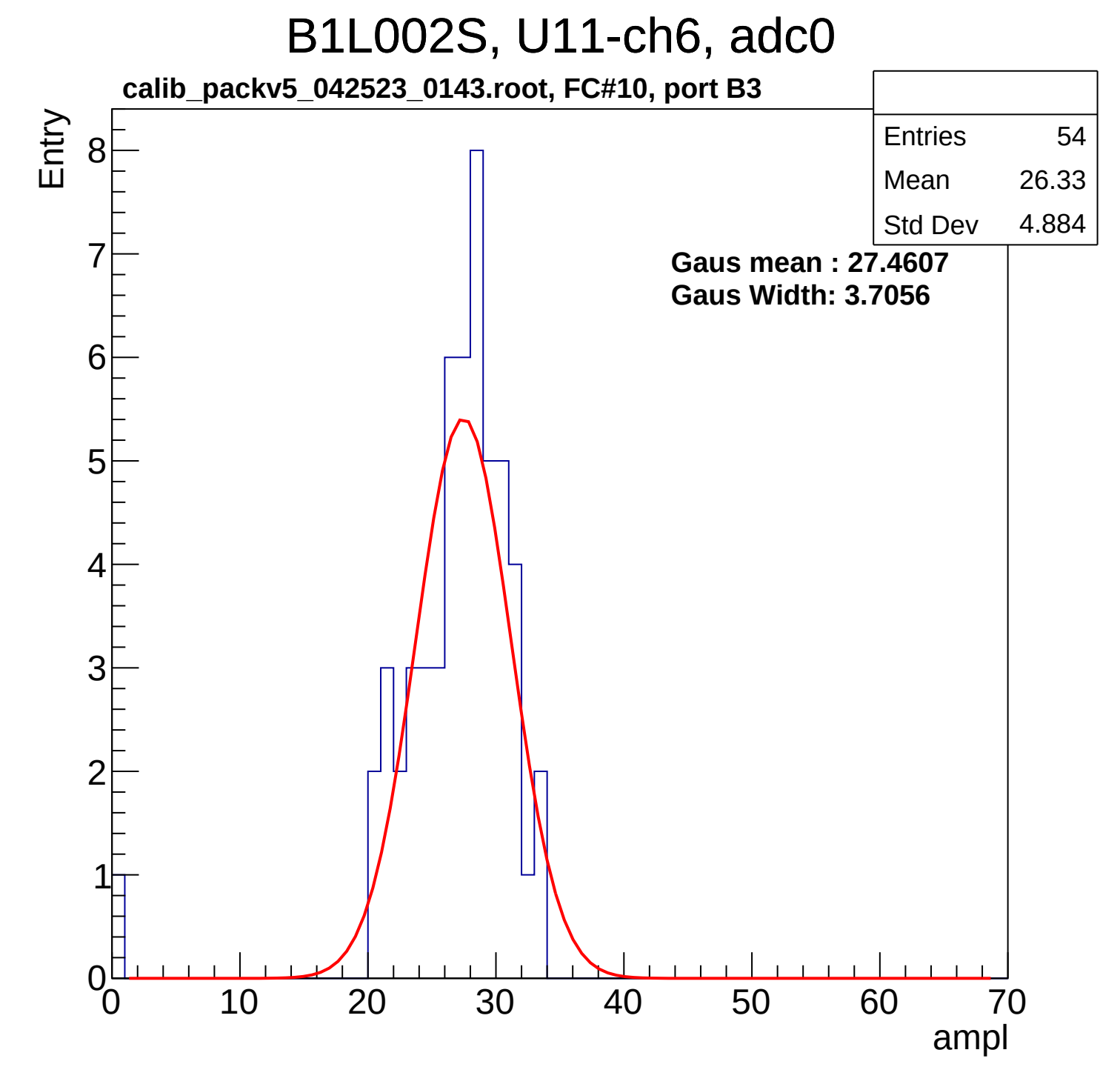
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 26.33
Std Dev 4.884
Gaus mean : 27.4607
Gaus Width: 3.7056
Entry
ampl
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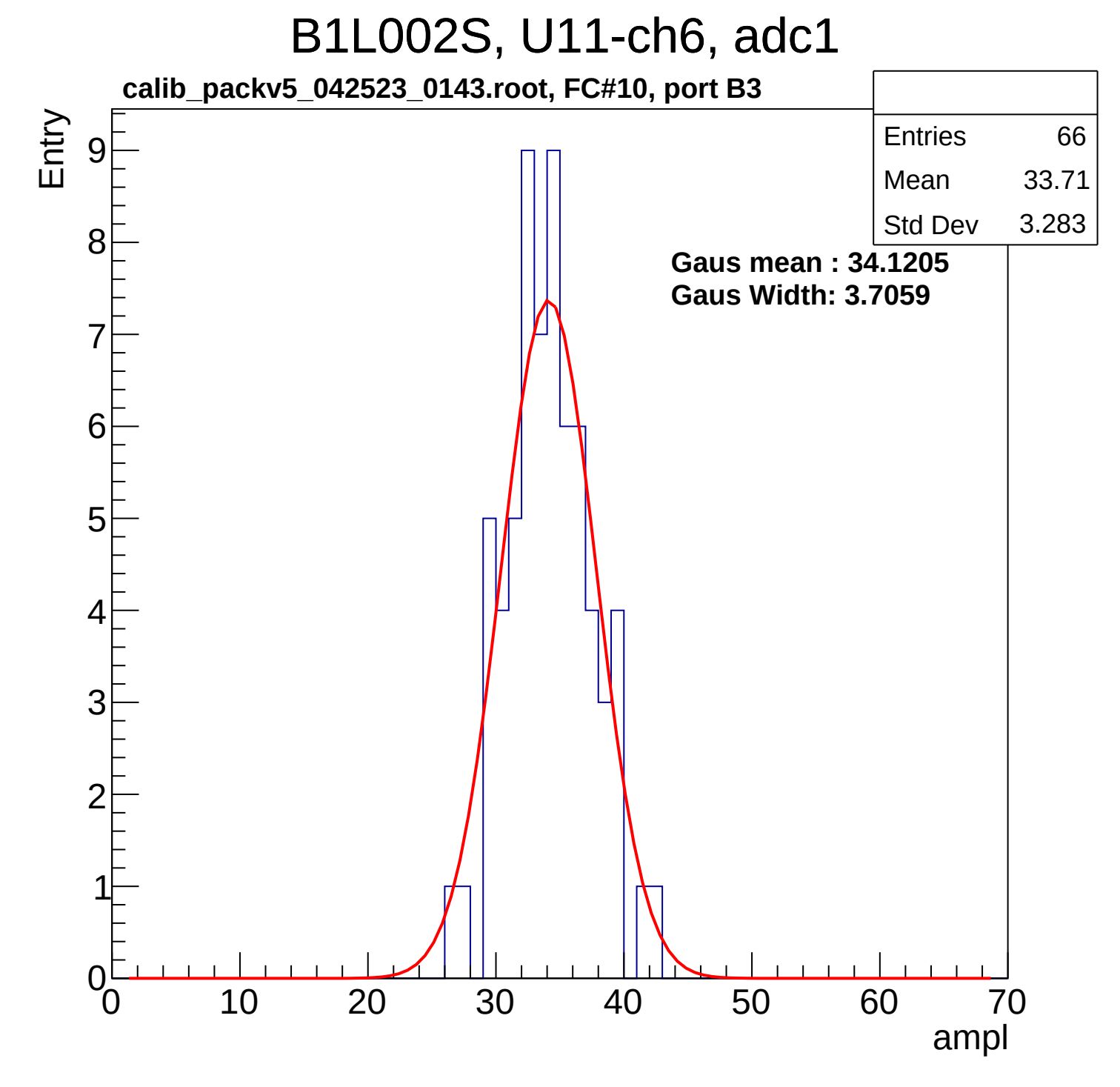
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.71
Std Dev 3.283
Gaus mean : 34.1205
Gaus Width: 3.7059
Entry
ampl
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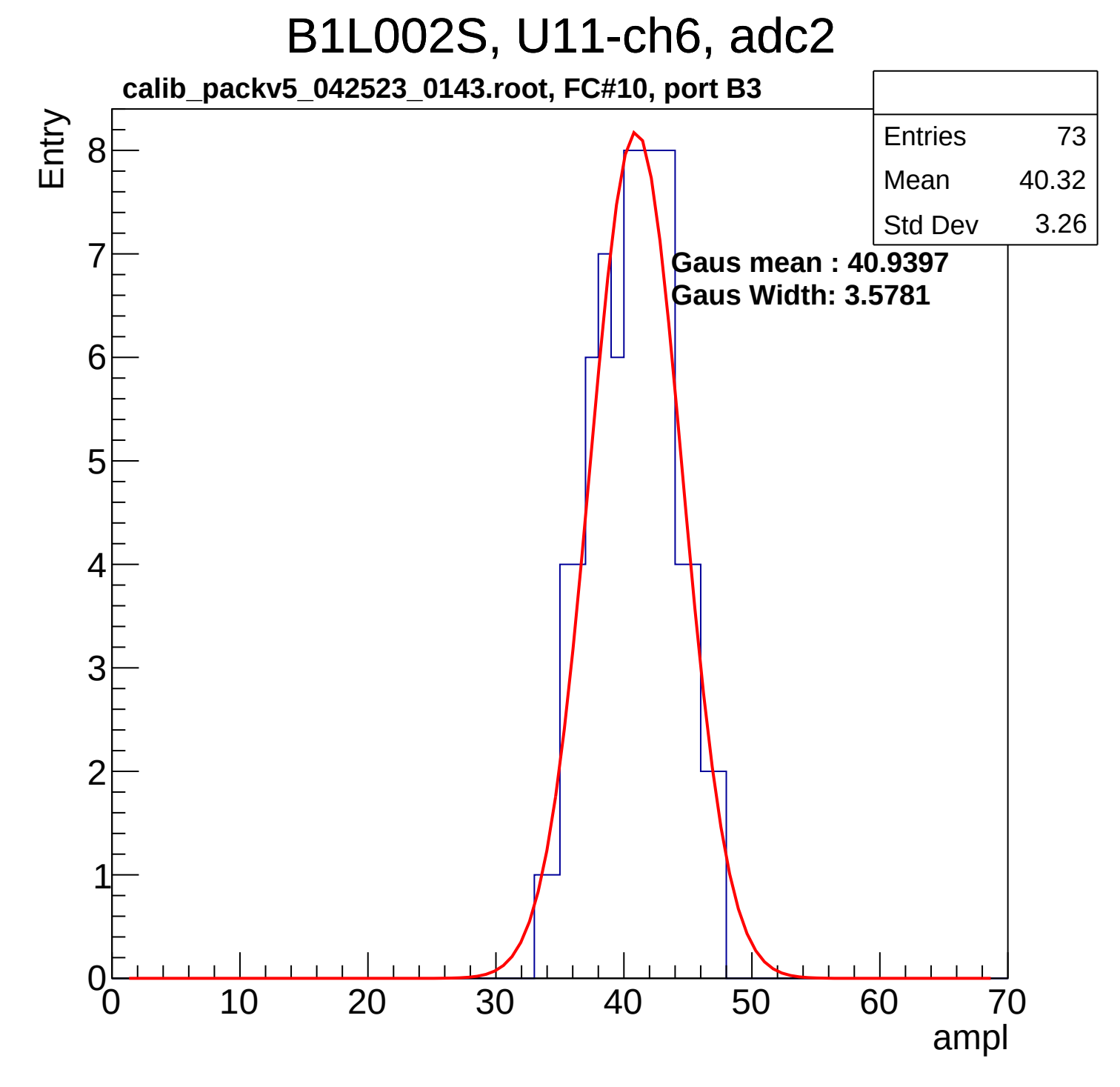
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 40.32
Std Dev 3.26
Gaus mean : 40.9397
Gaus Width: 3.5781
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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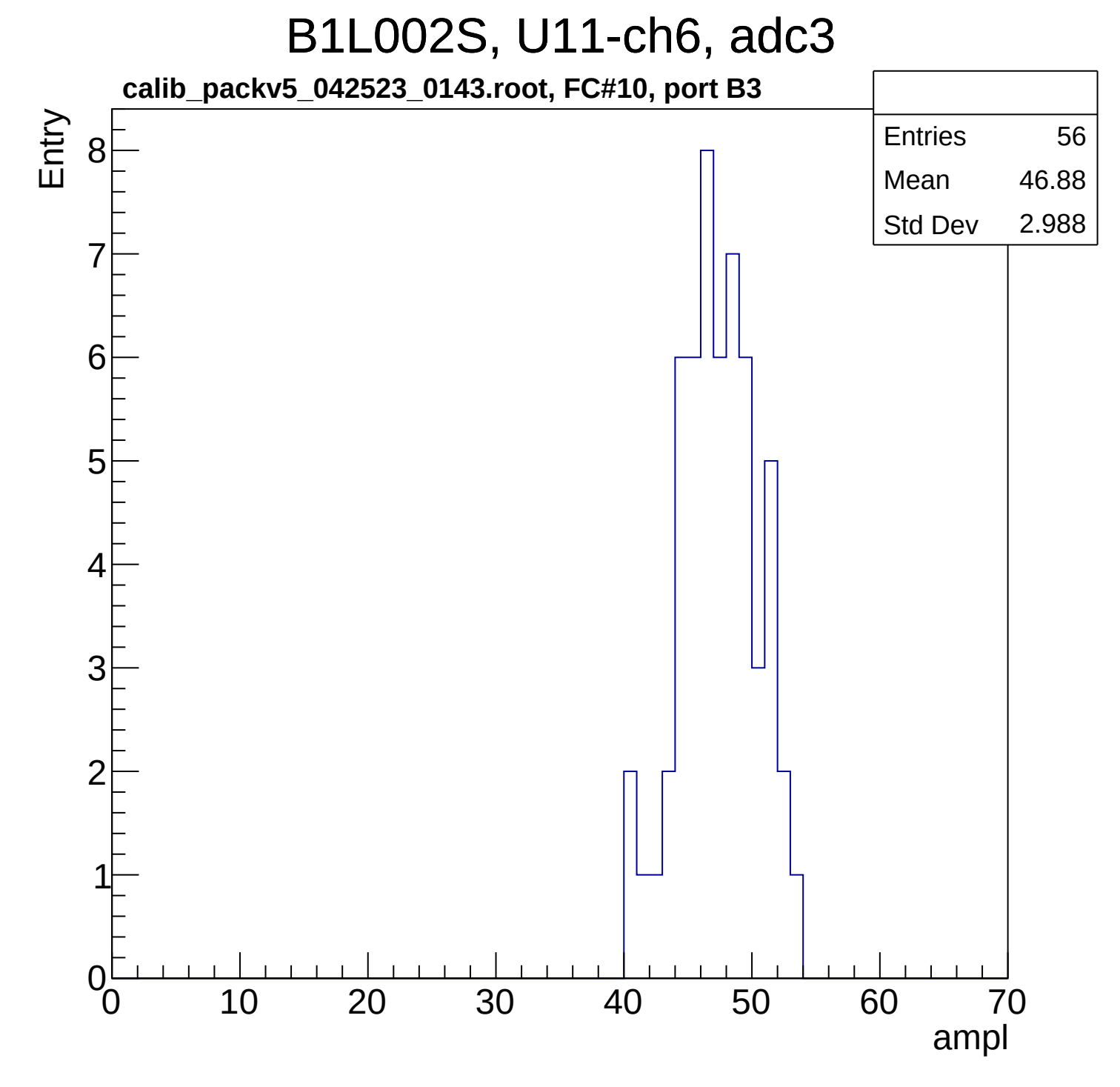
B1L002S, U11-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.88
Std Dev 2.988
Entry
ampl
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3
4
5
6
7
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0
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20
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60
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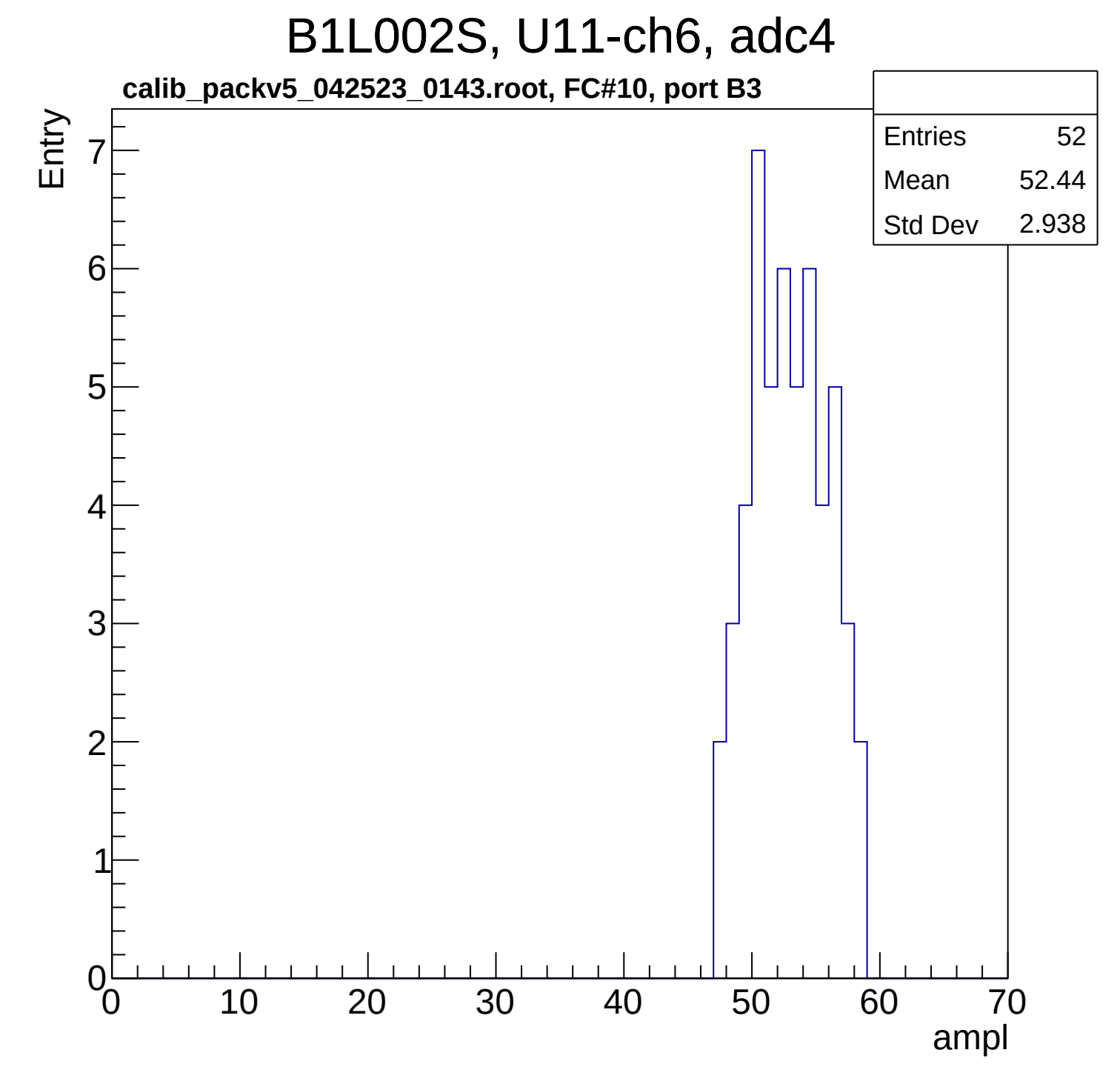
B1L002S, U11-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.44
Std Dev 2.938
Entry
ampl
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1
2
3
4
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0
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20
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40
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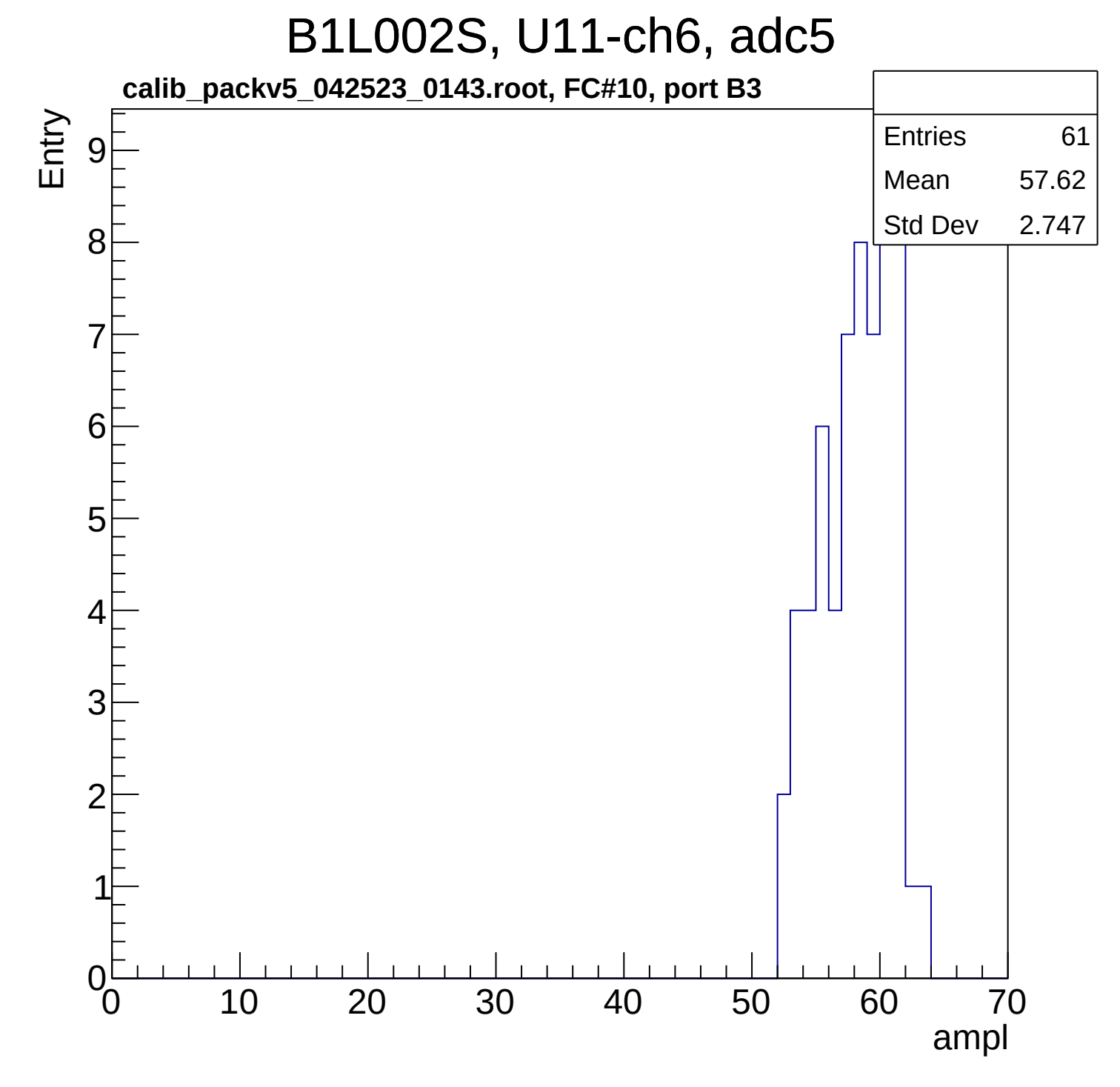
60
70


B1L002S, U11-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.62
Std Dev 2.747
Entry
ampl
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3
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5
6
7
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9
0
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20
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60
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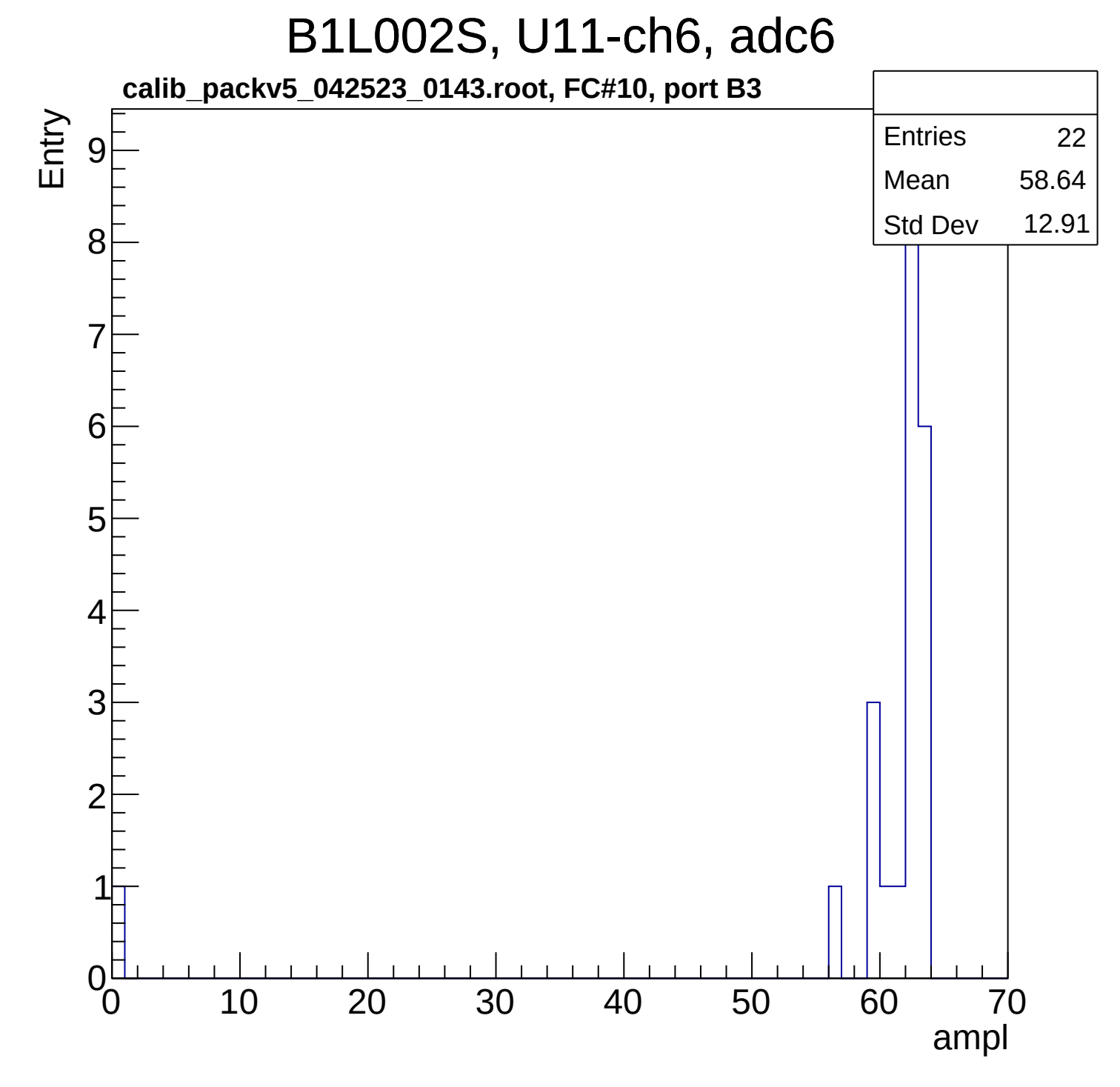
B1L002S, U11-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.64
Std Dev 12.91
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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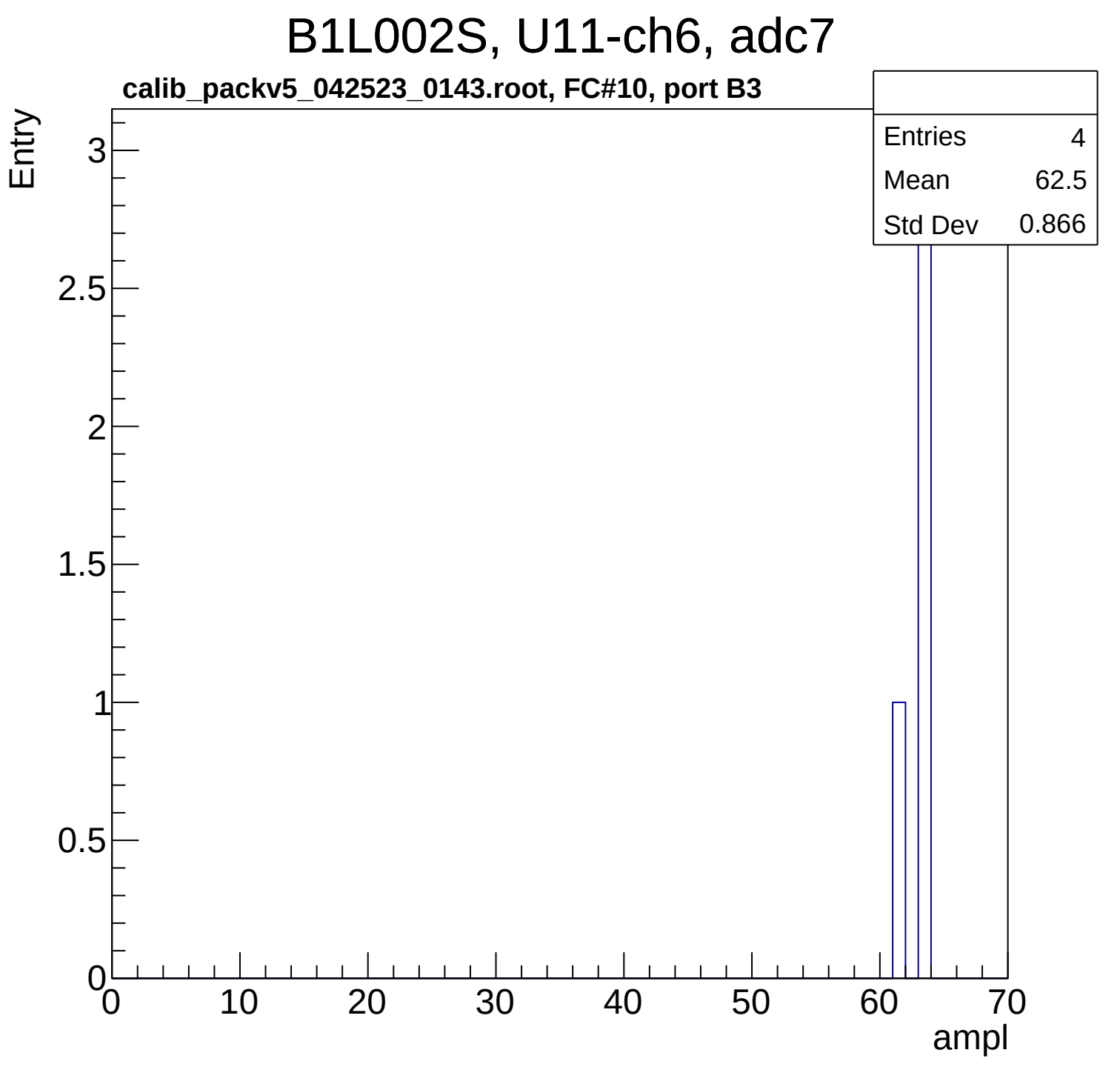
B1L002S, U11-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
30
40
50
60
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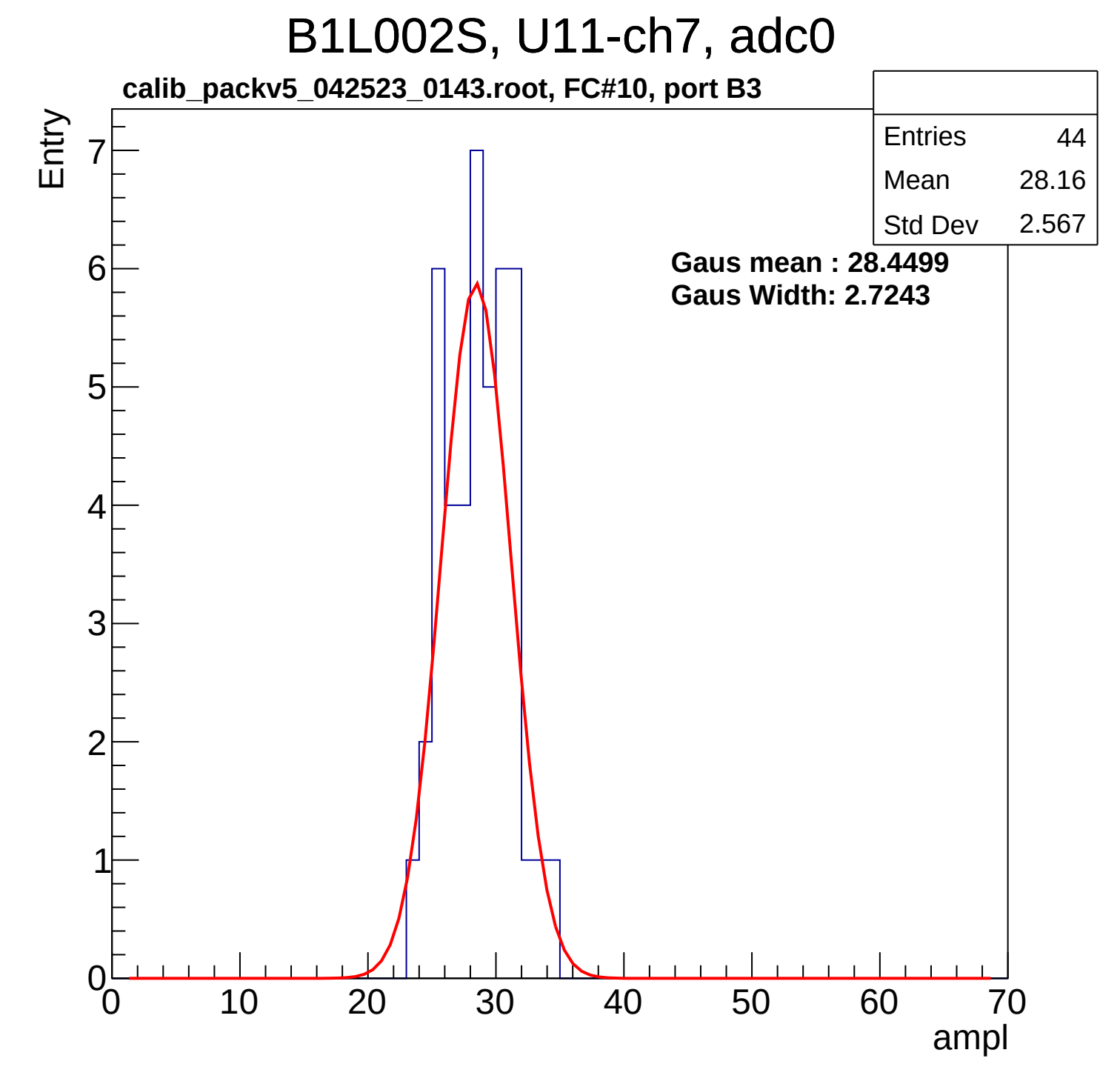
B1L002S, U11-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 28.16
Std Dev 2.567
Gaus mean : 28.4499
Gaus Width: 2.7243
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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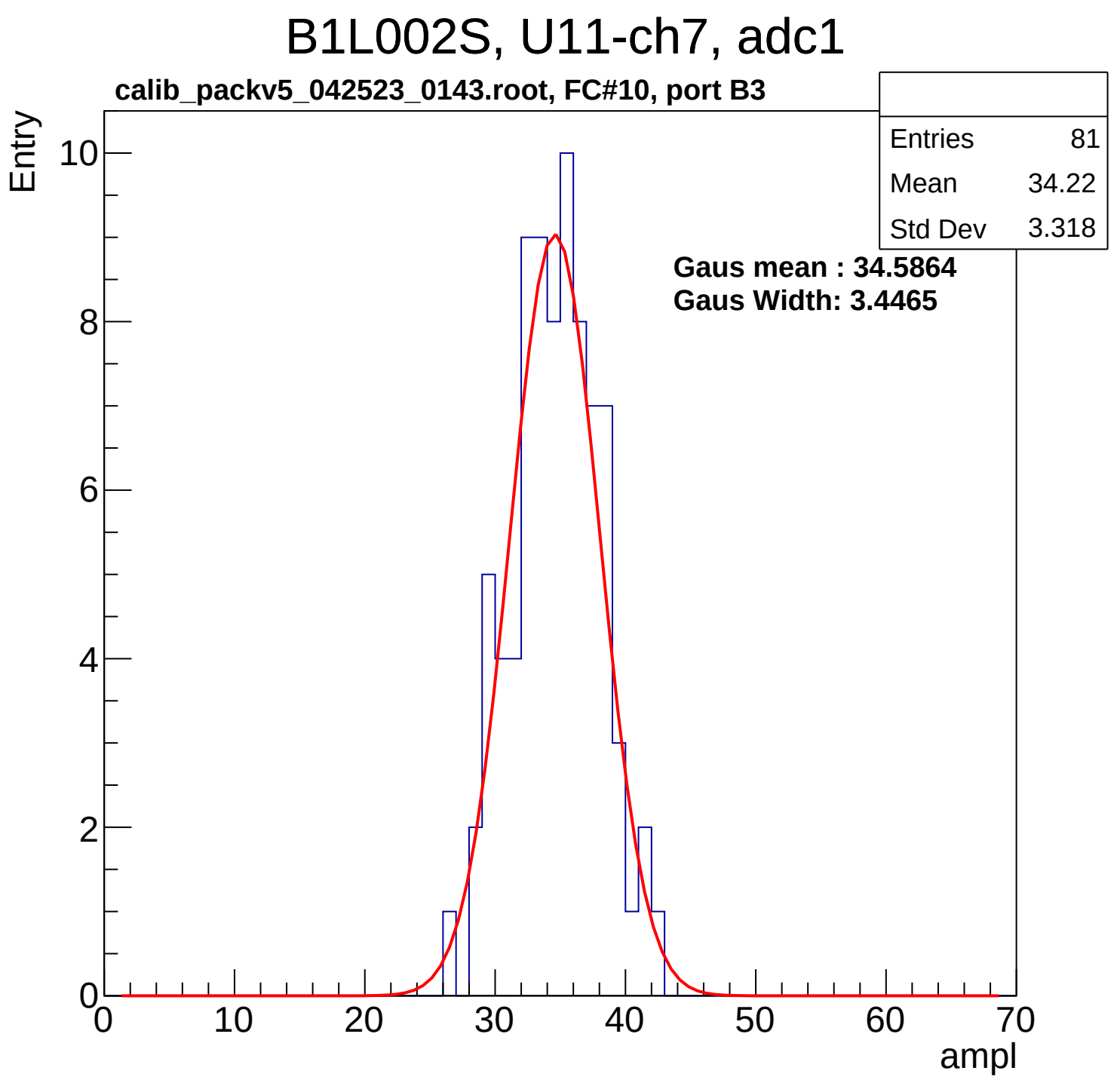
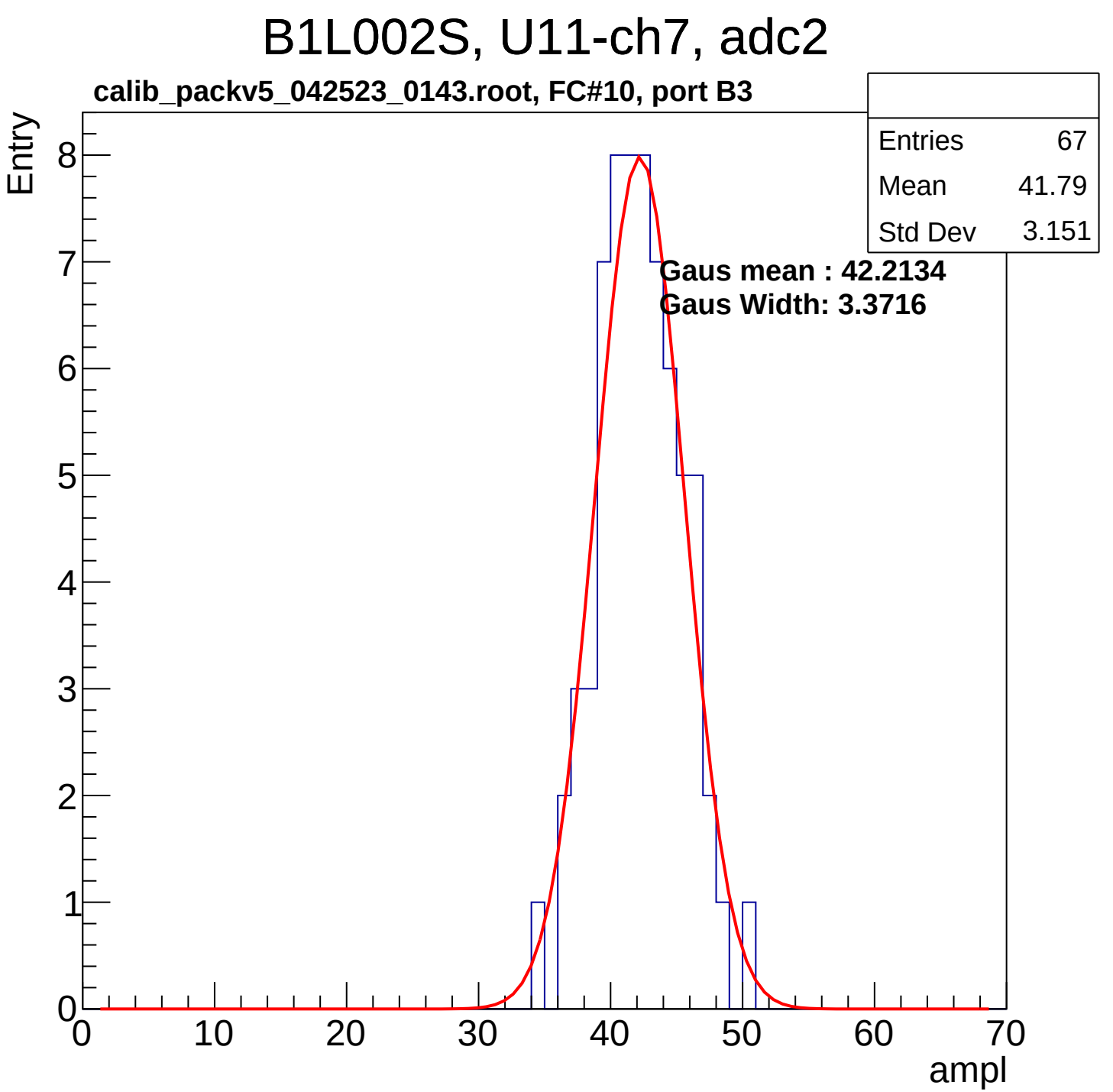

B1L002S, U11-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 34.22
Std Dev 3.318
Gaus mean : 34.5864
Gaus Width: 3.4465
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.79
Std Dev 3.151
Gaus mean : 42.2134
Gaus Width: 3.3716
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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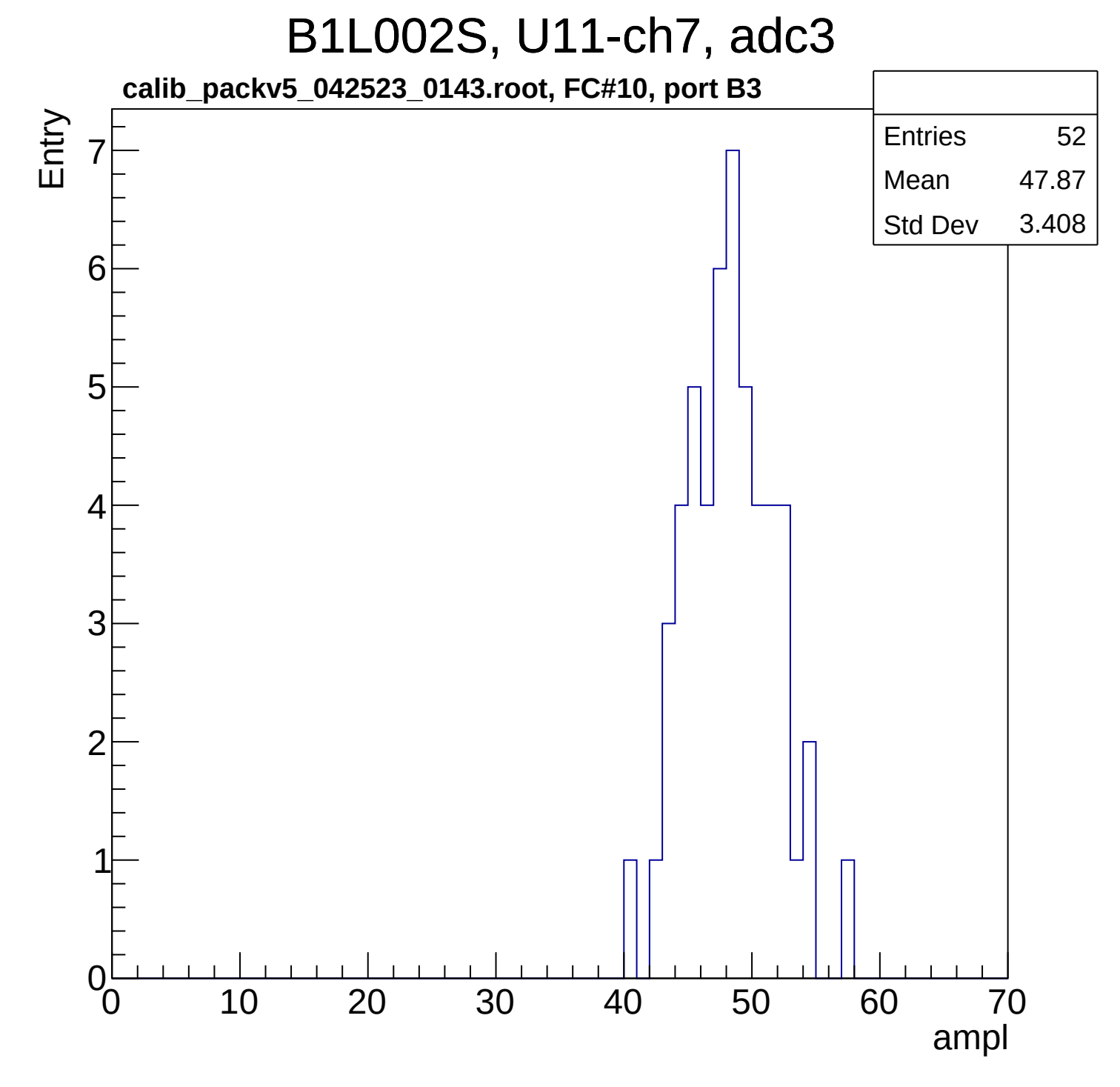
40
50
60
70


B1L002S, U11-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.87
Std Dev 3.408
Entry
ampl
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3
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5
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7
0
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60
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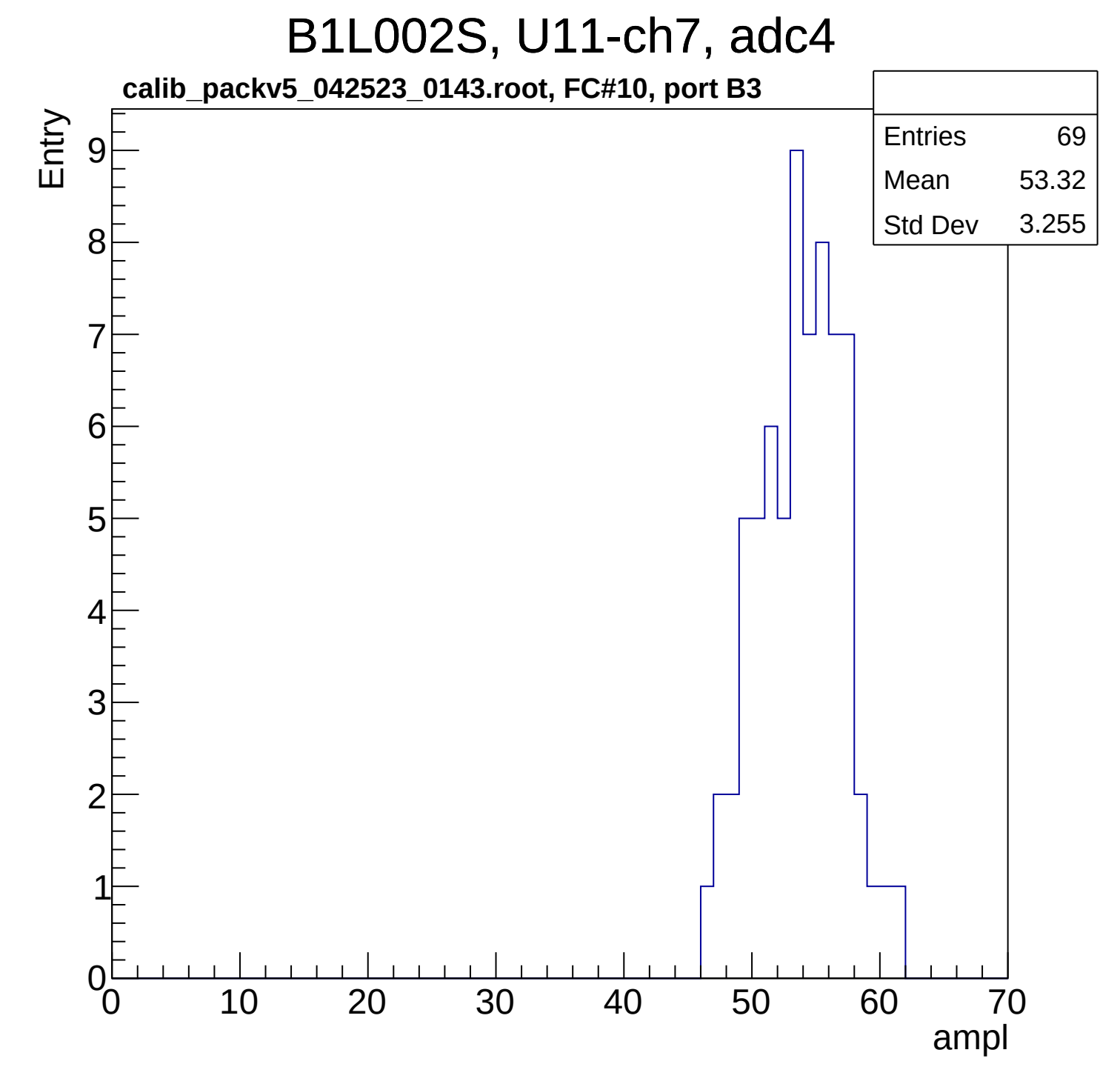
B1L002S, U11-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.32
Std Dev 3.255
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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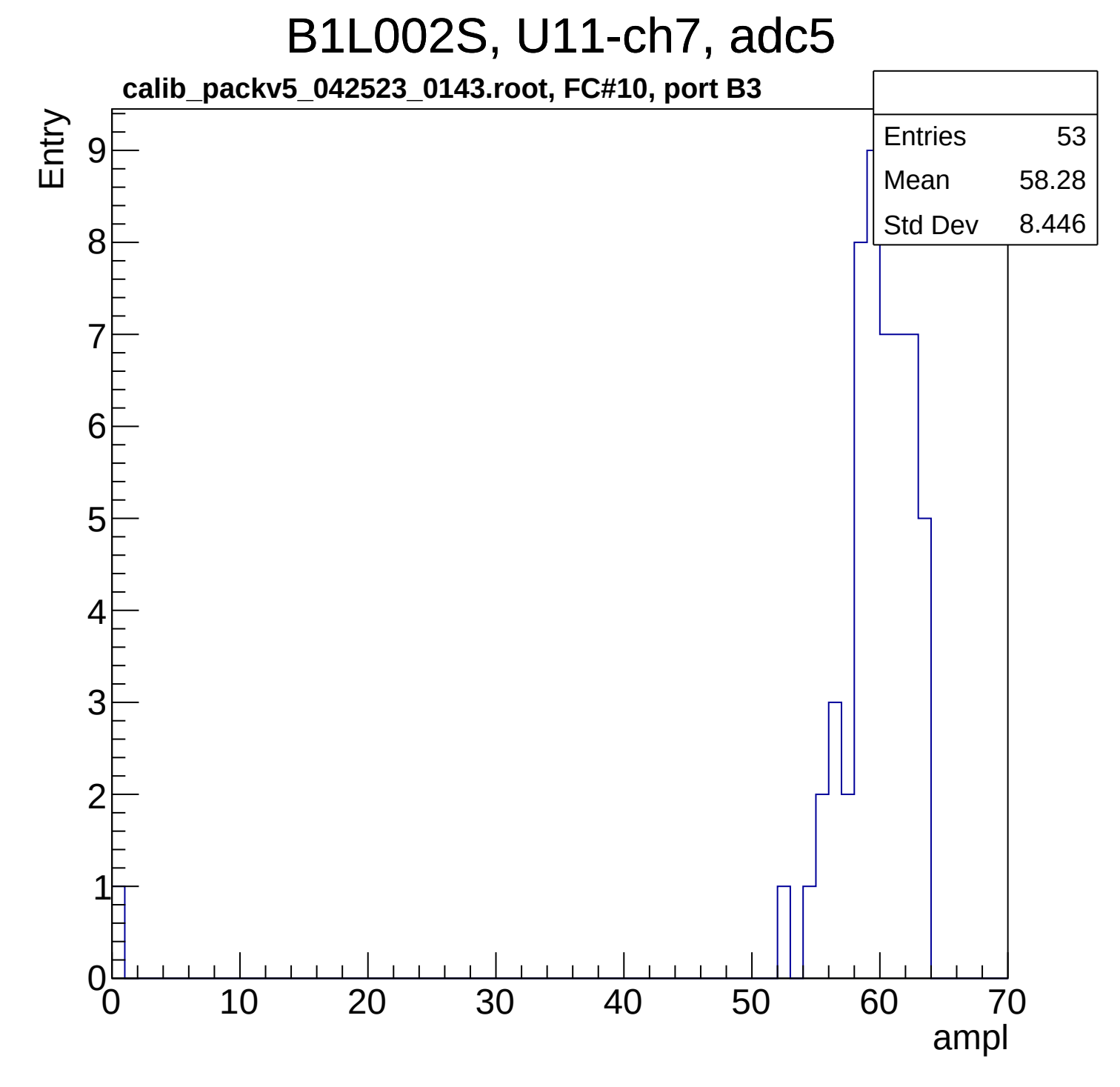
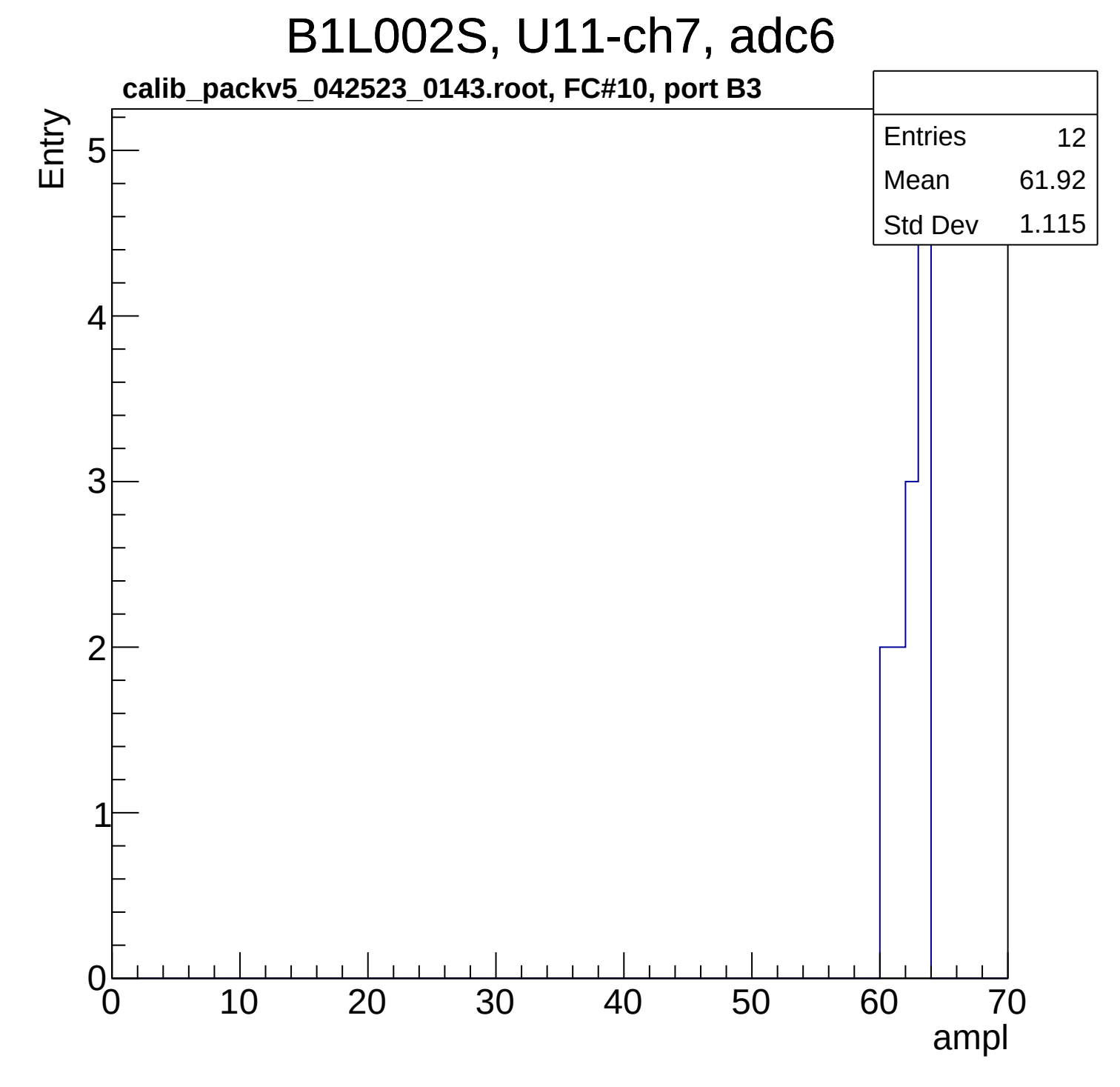

B1L002S, U11-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.28
Std Dev 8.446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.92
Std Dev 1.115
Entry
0
1
2
3
4
5
0
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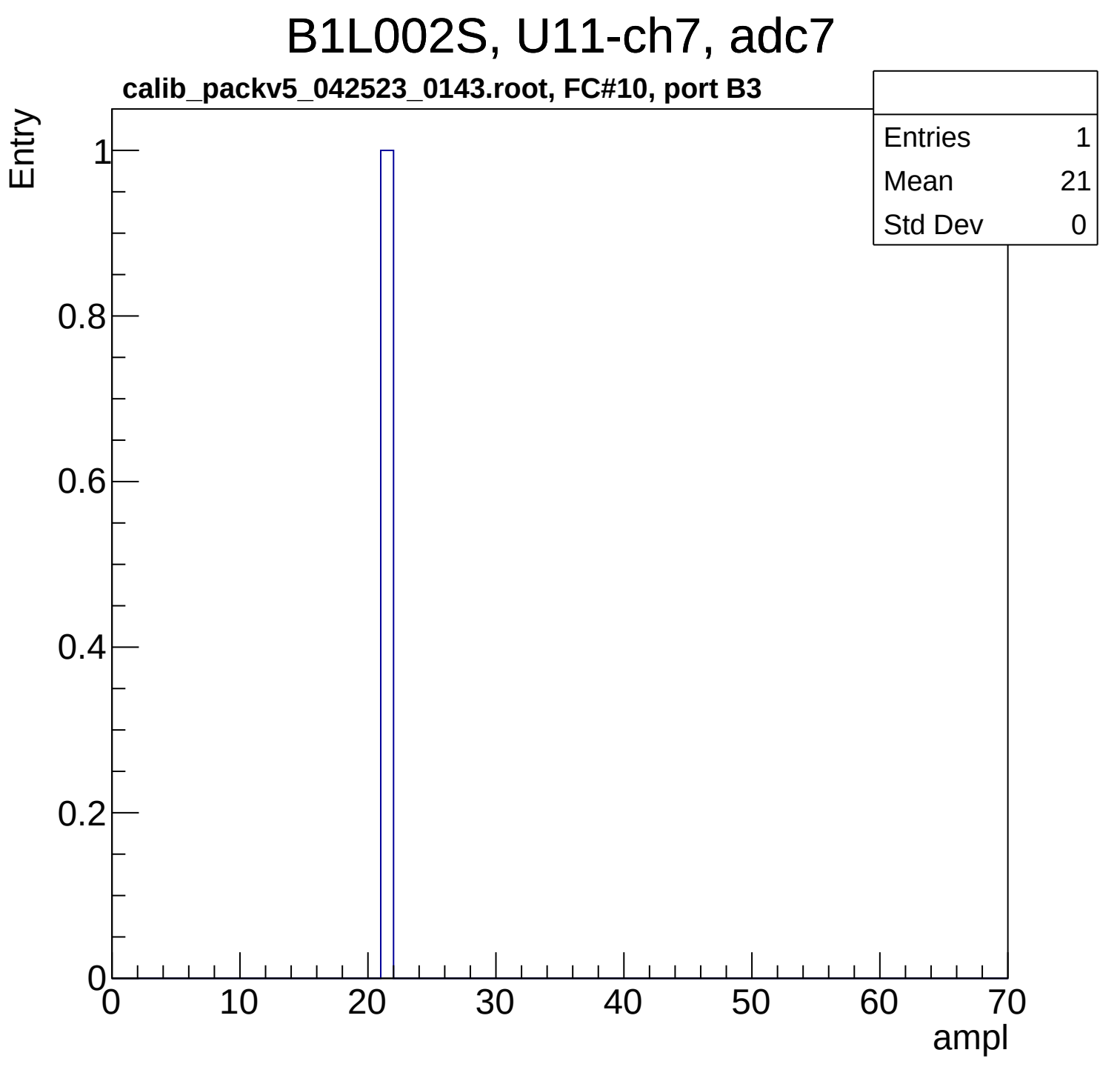
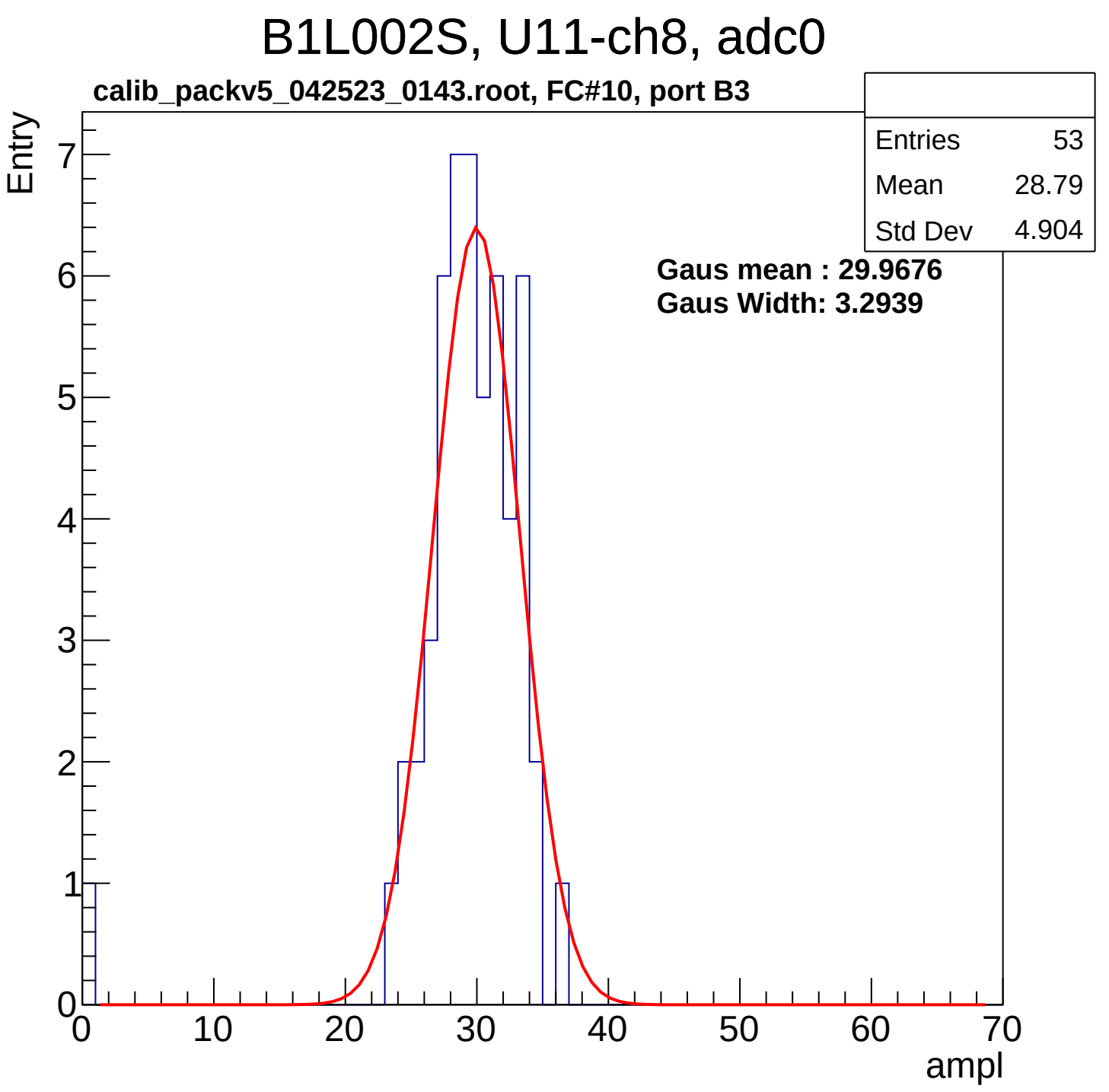
60
70
ampl


B1L002S, U11-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 28.79
Std Dev 4.904
Gaus mean : 29.9676
Gaus Width: 3.2939
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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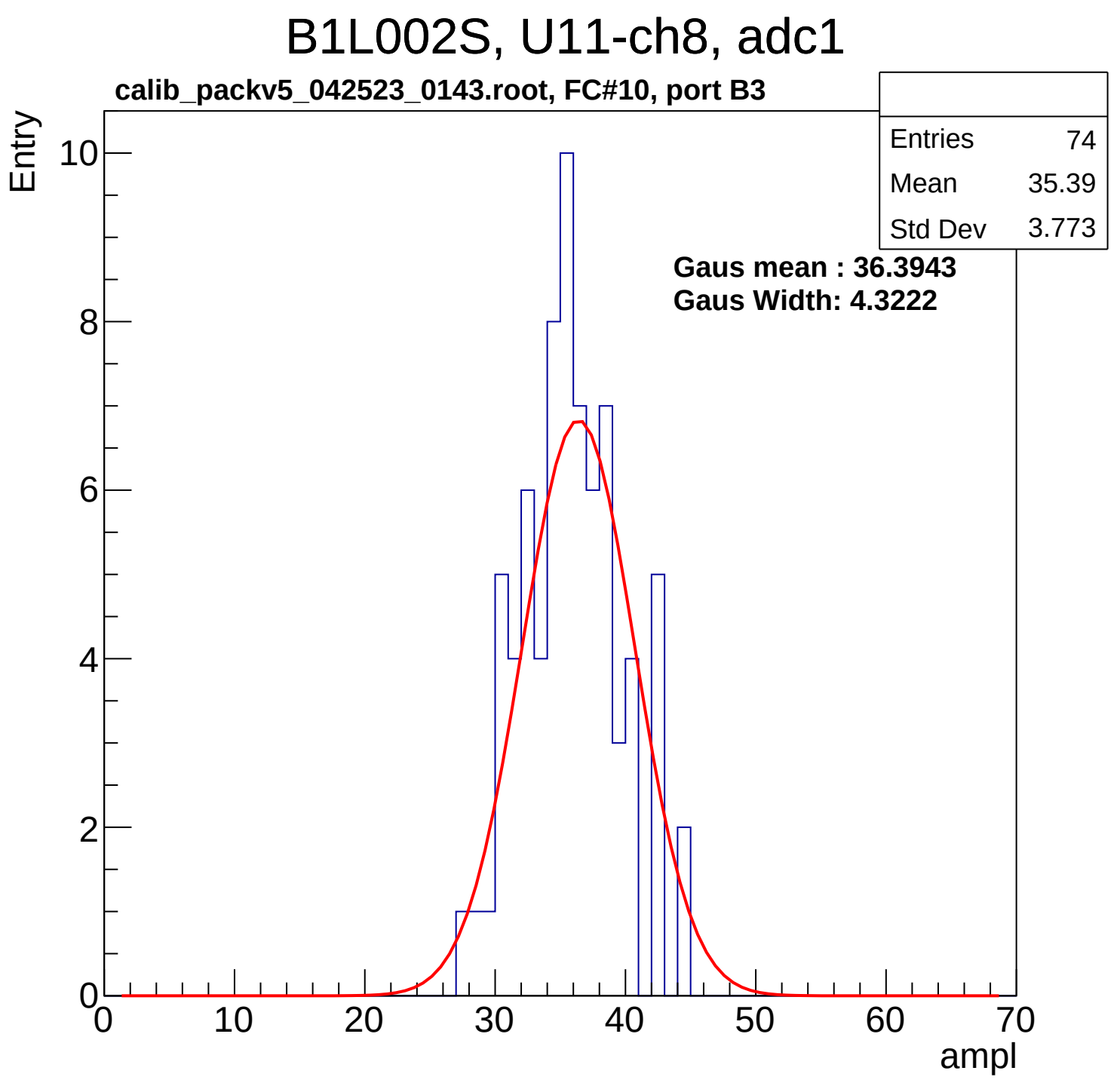
B1L002S, U11-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.39
Std Dev 3.773
Gaus mean : 36.3943
Gaus Width: 4.3222
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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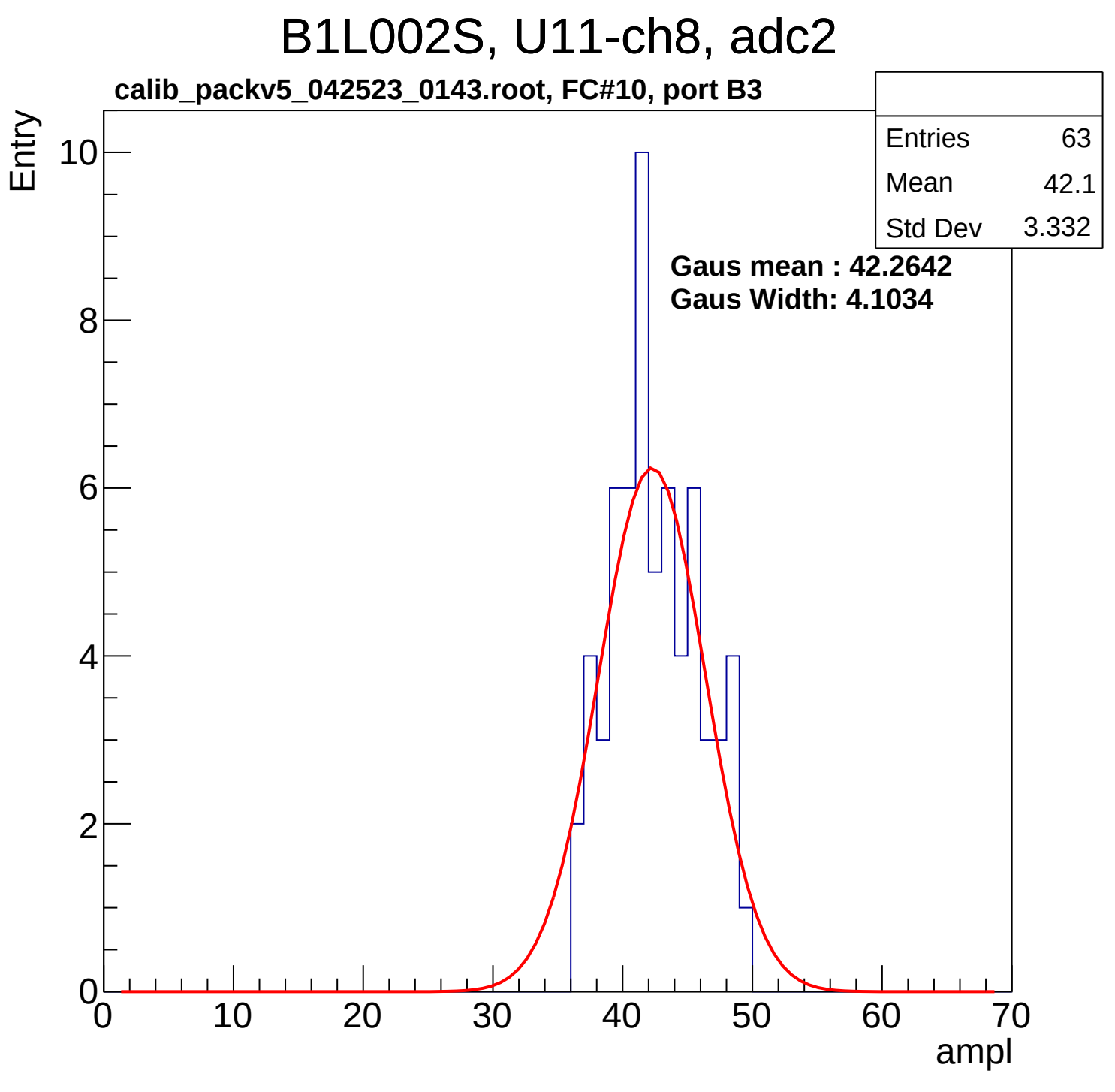
B1L002S, U11-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 42.1
Std Dev 3.332
Gaus mean : 42.2642
Gaus Width: 4.1034
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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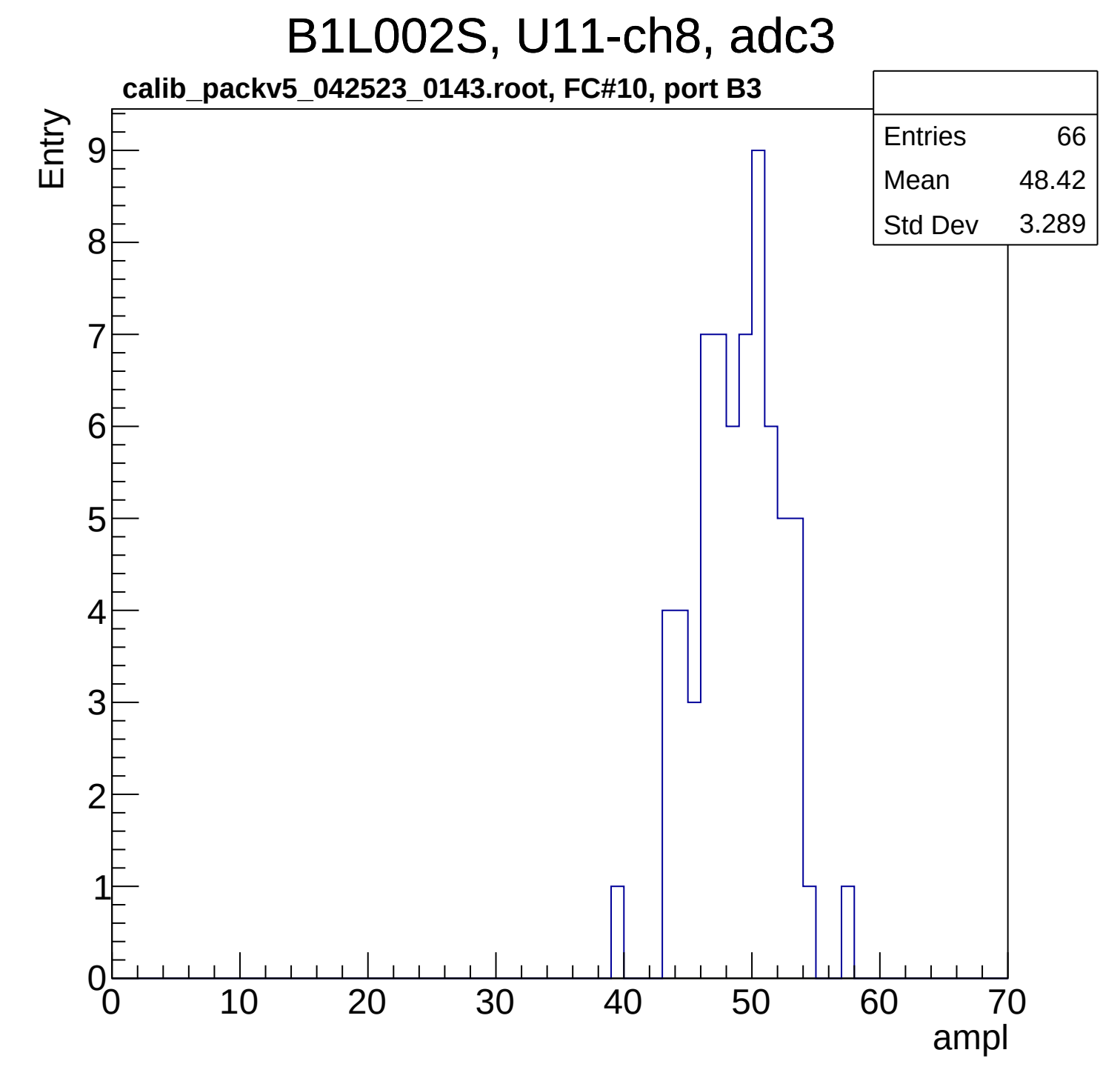
B1L002S, U11-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.42
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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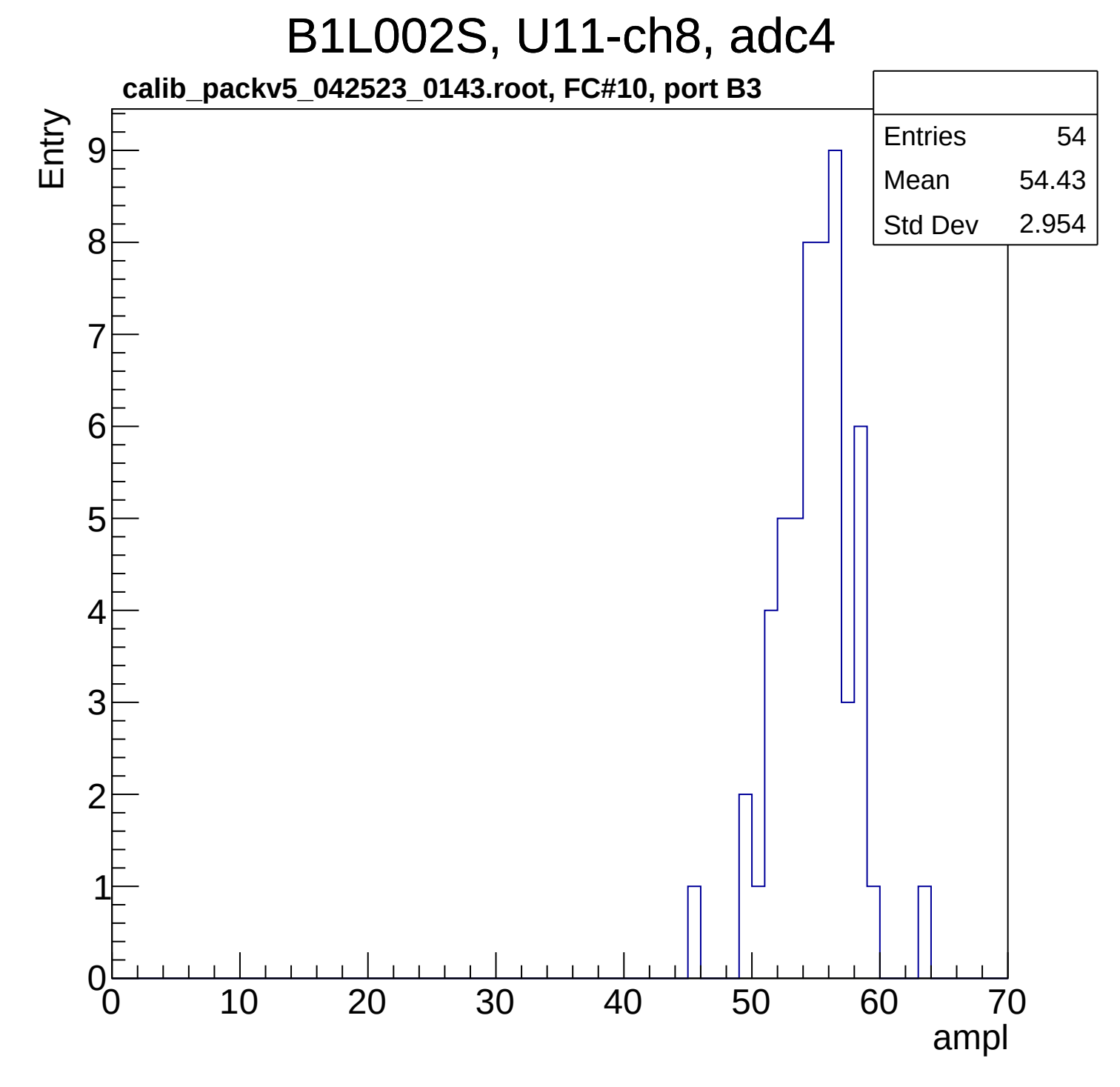
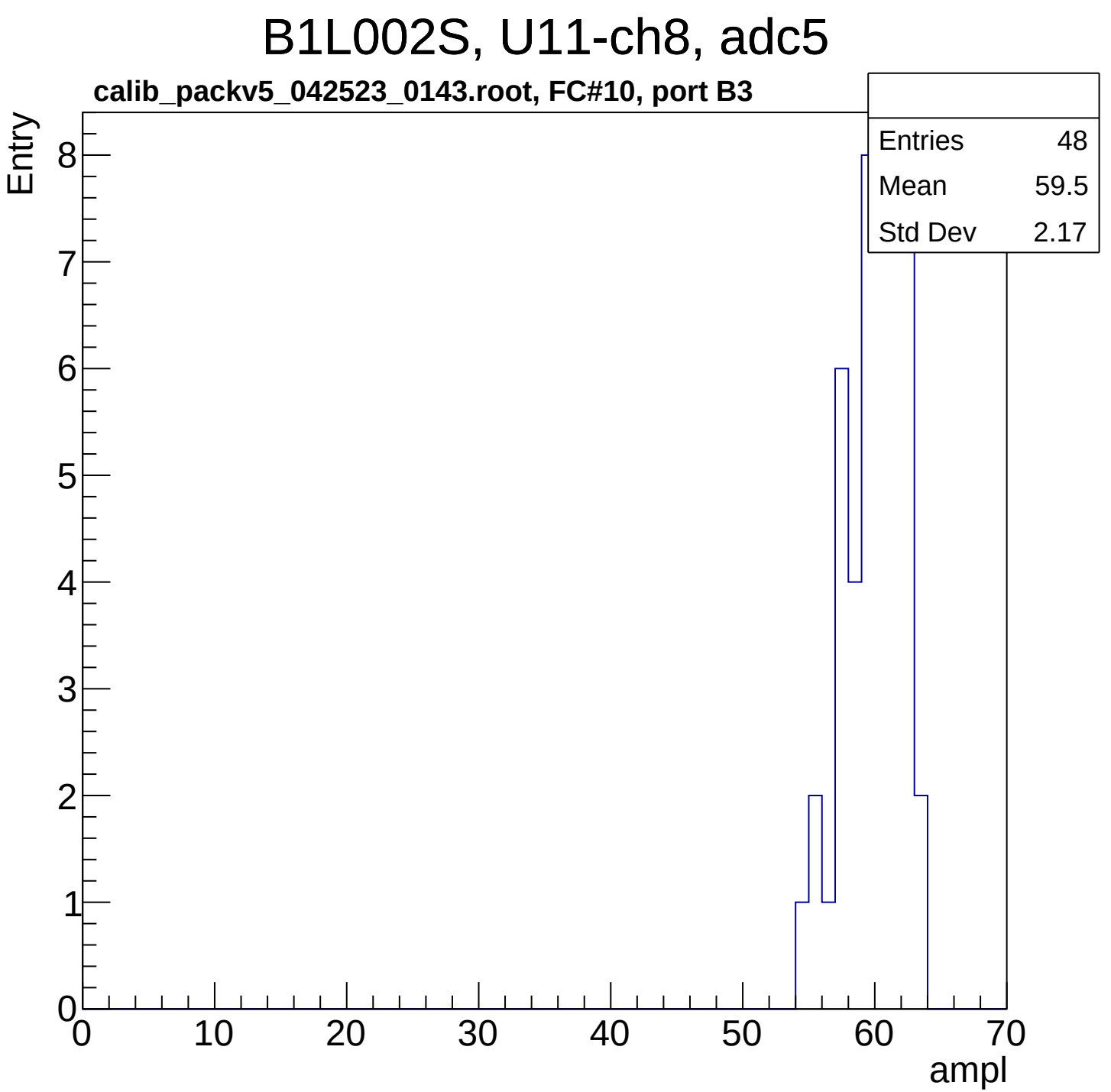
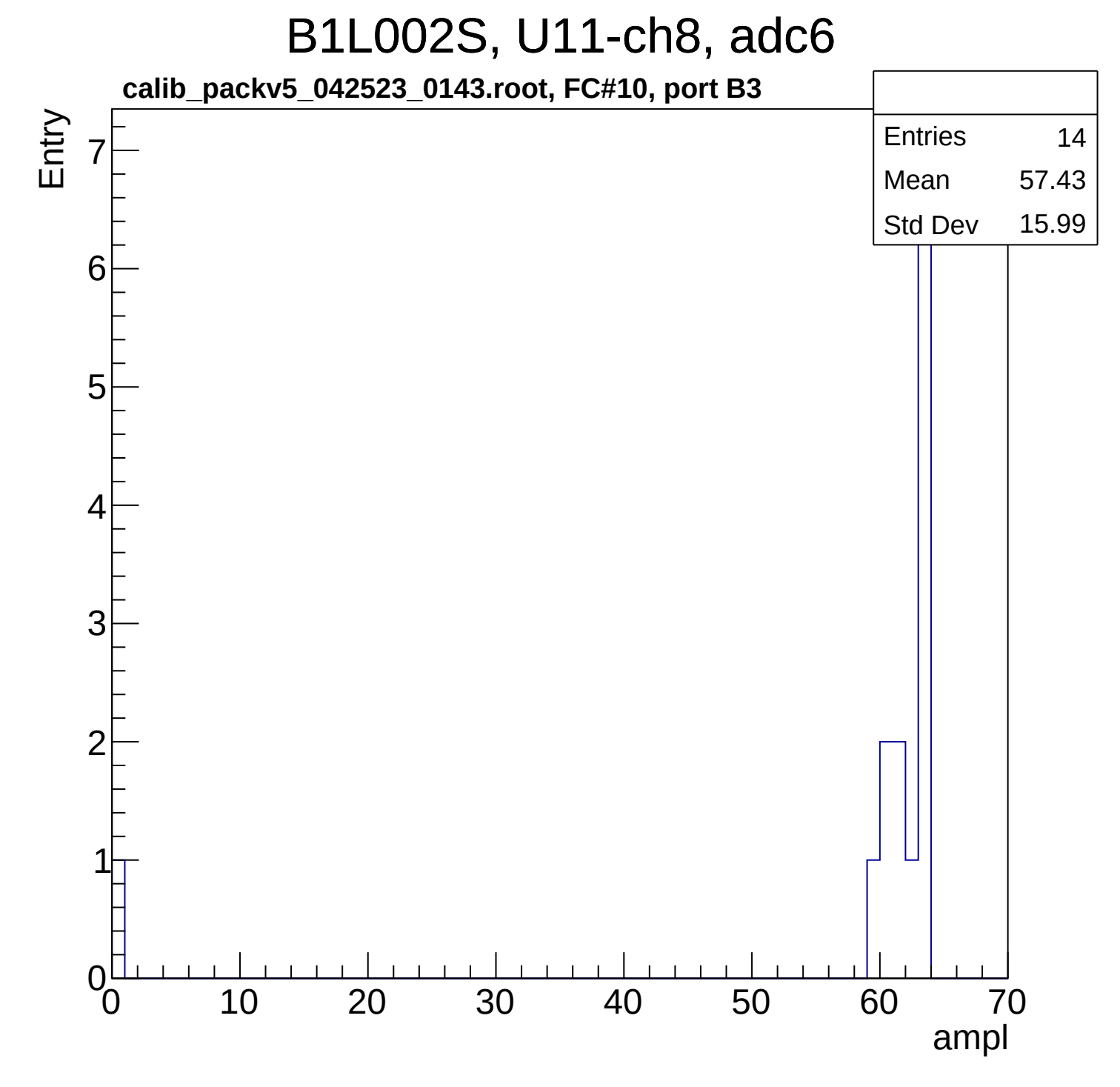
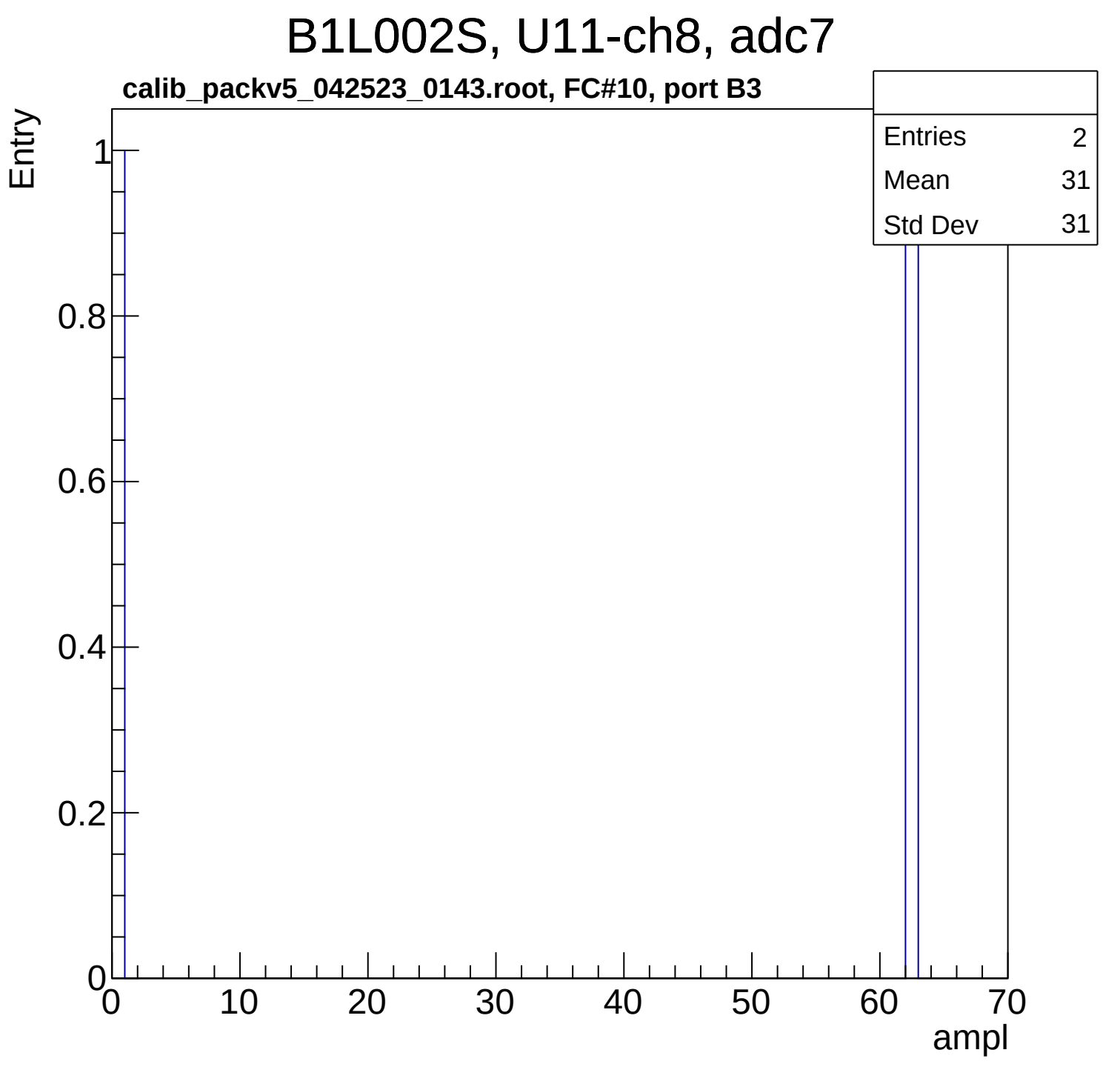
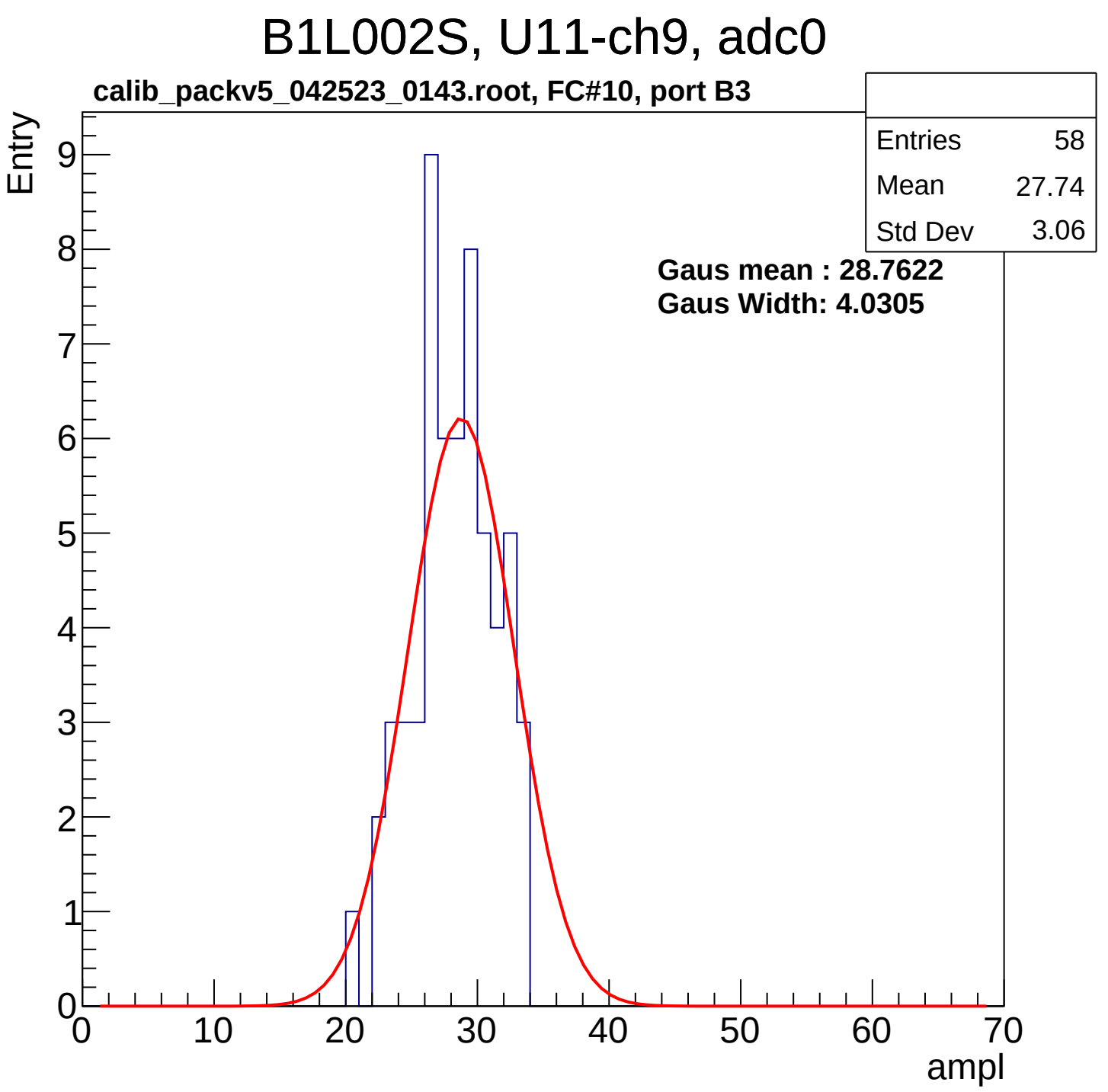
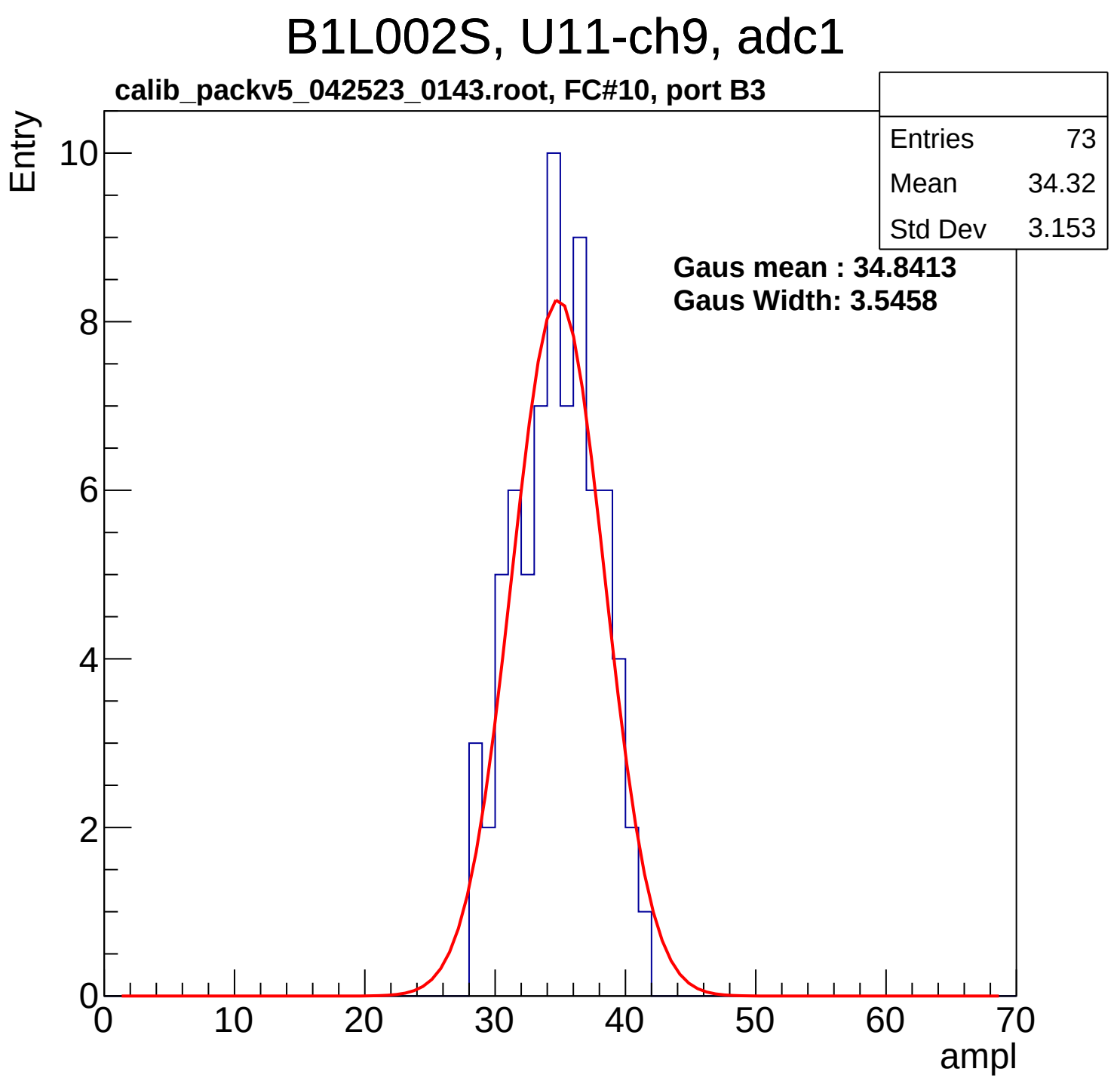

B1L002S, U11-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 54.43
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

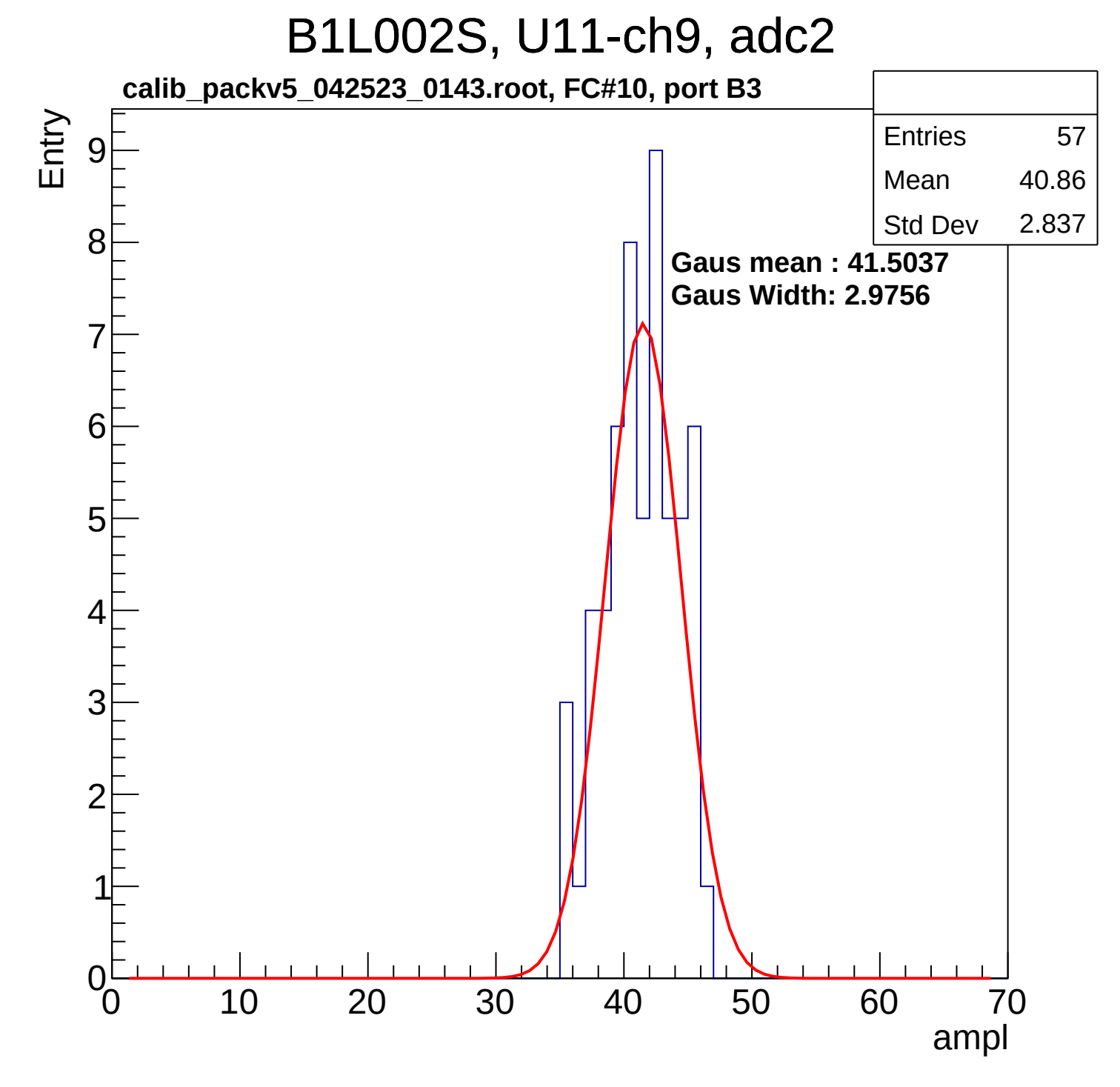

B1L002S, U11-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.5
Std Dev 2.17
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

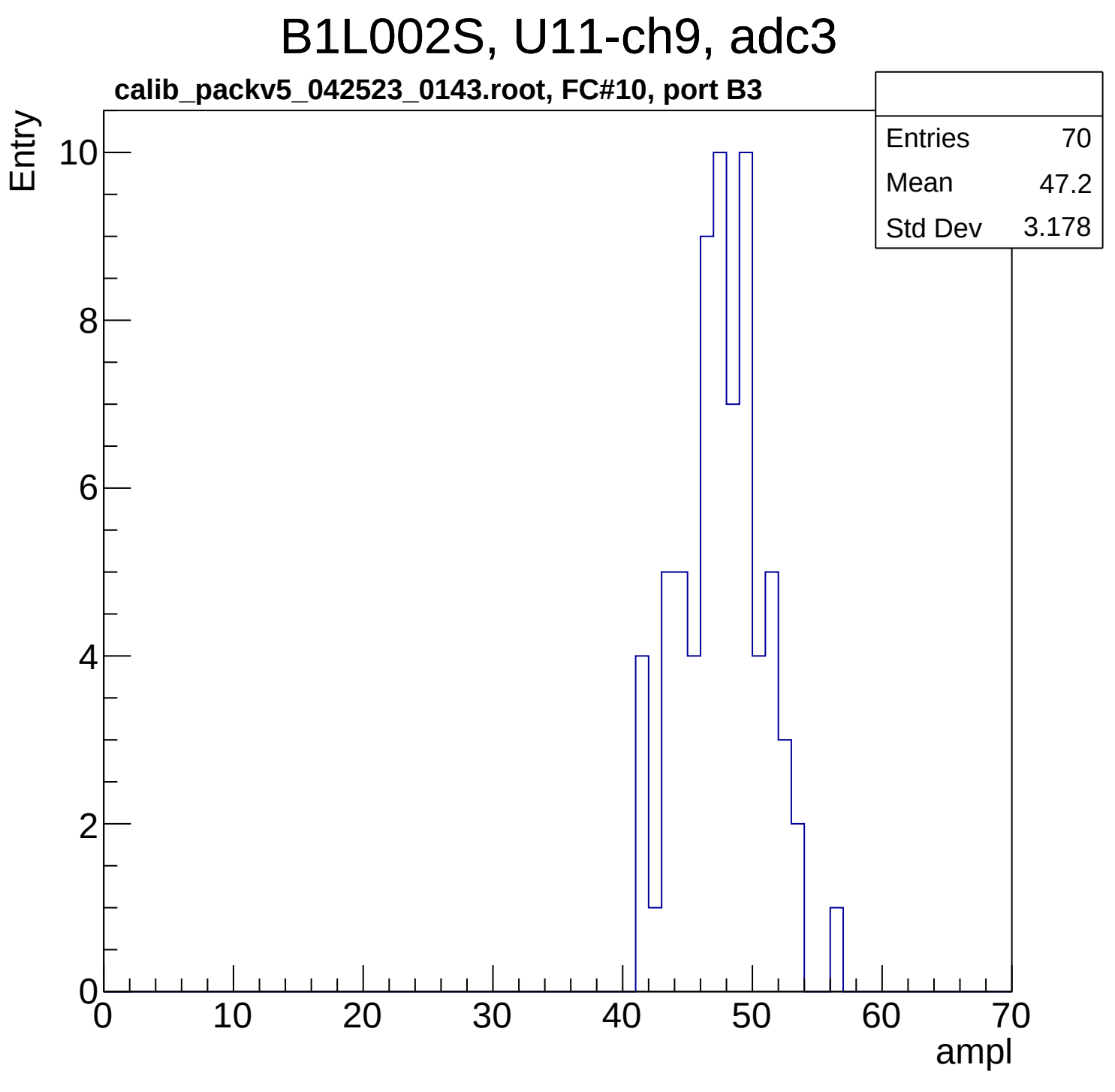

B1L002S, U11-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.43
Std Dev 15.99
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

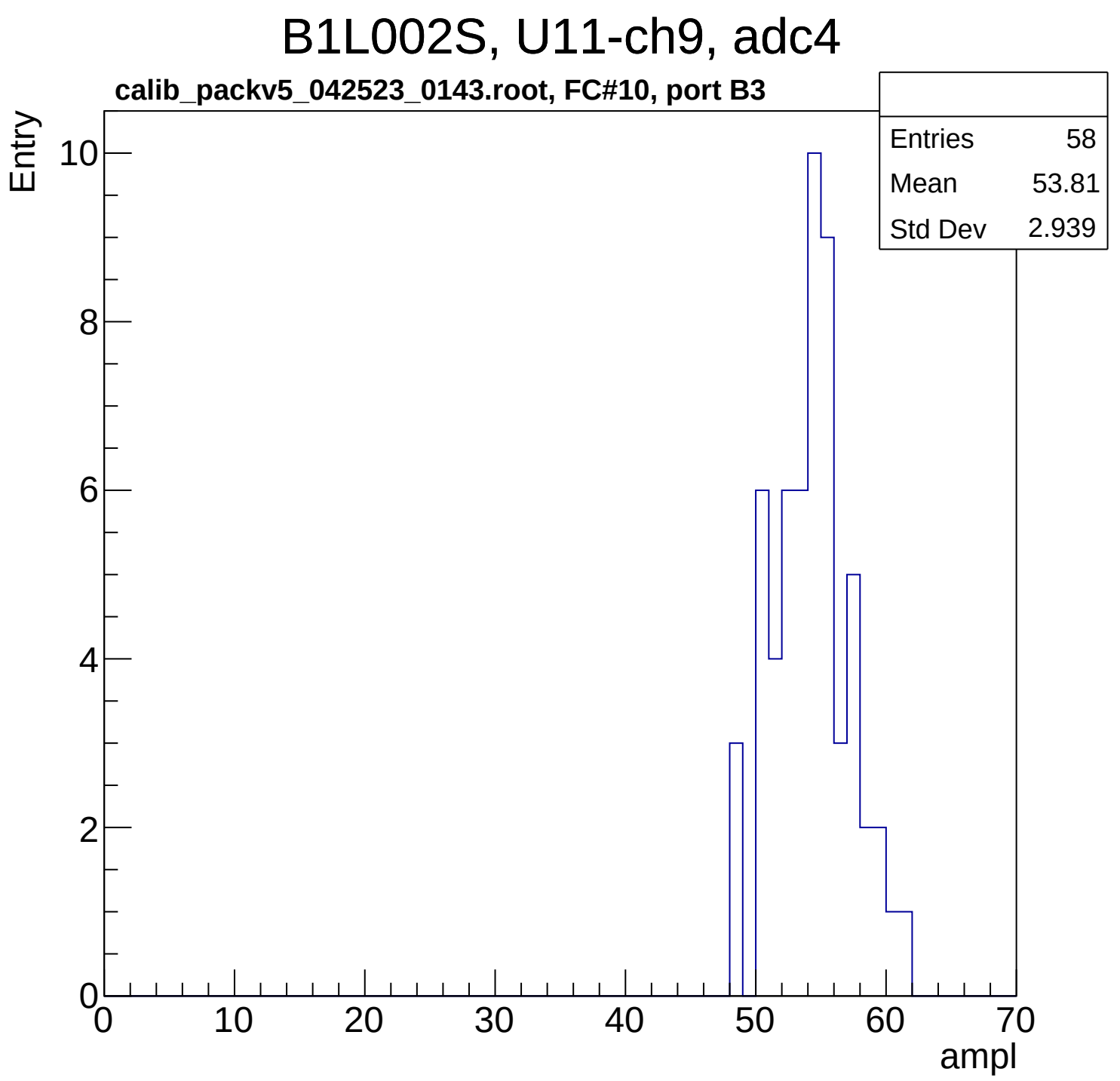

B1L002S, U11-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

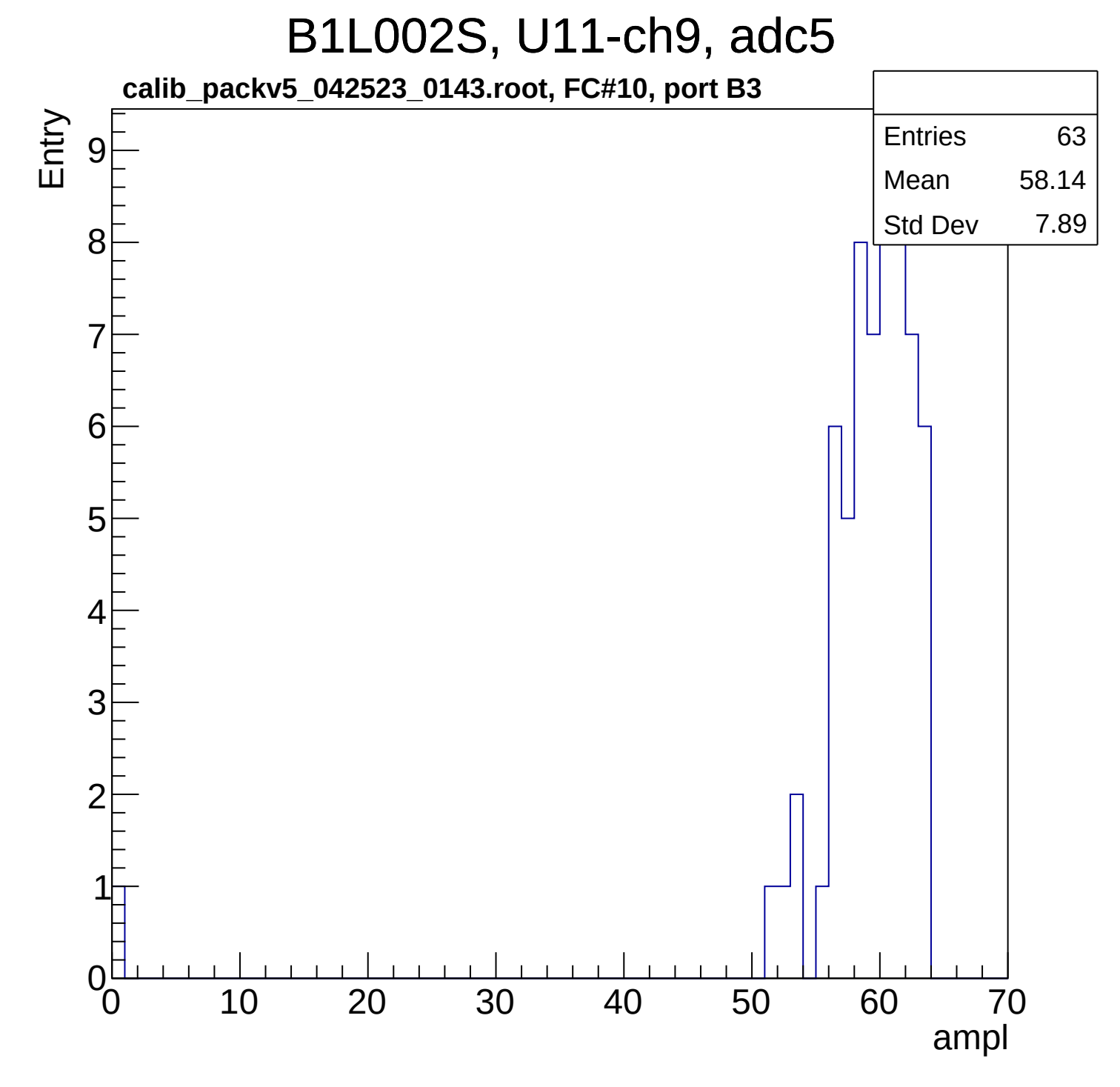

B1L002S, U11-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.74
Std Dev 3.06
Gaus mean : 28.7622
Gaus Width: 4.0305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

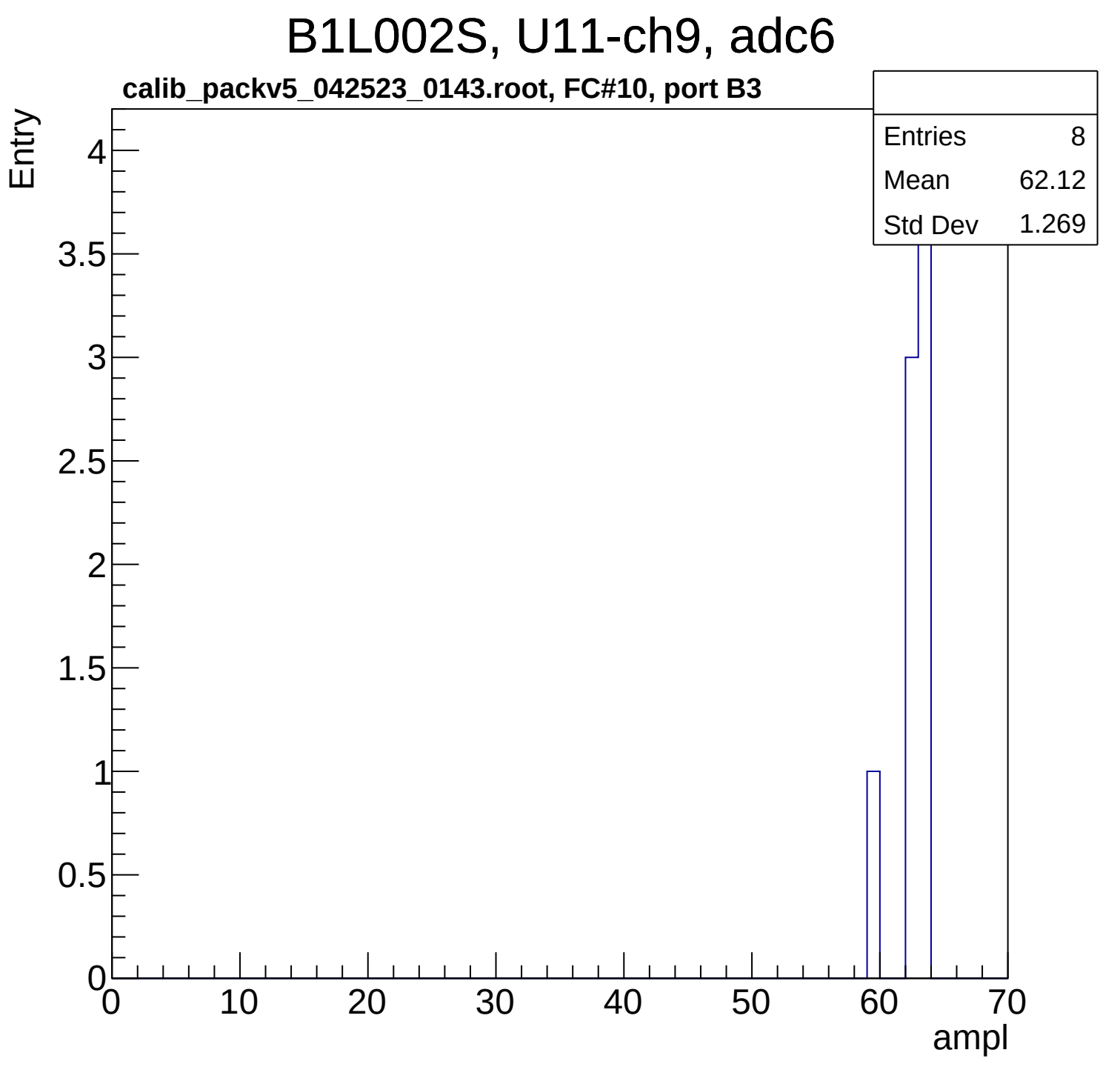

B1L002S, U11-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.32
Std Dev 3.153
Gaus mean : 34.8413
Gaus Width: 3.5458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

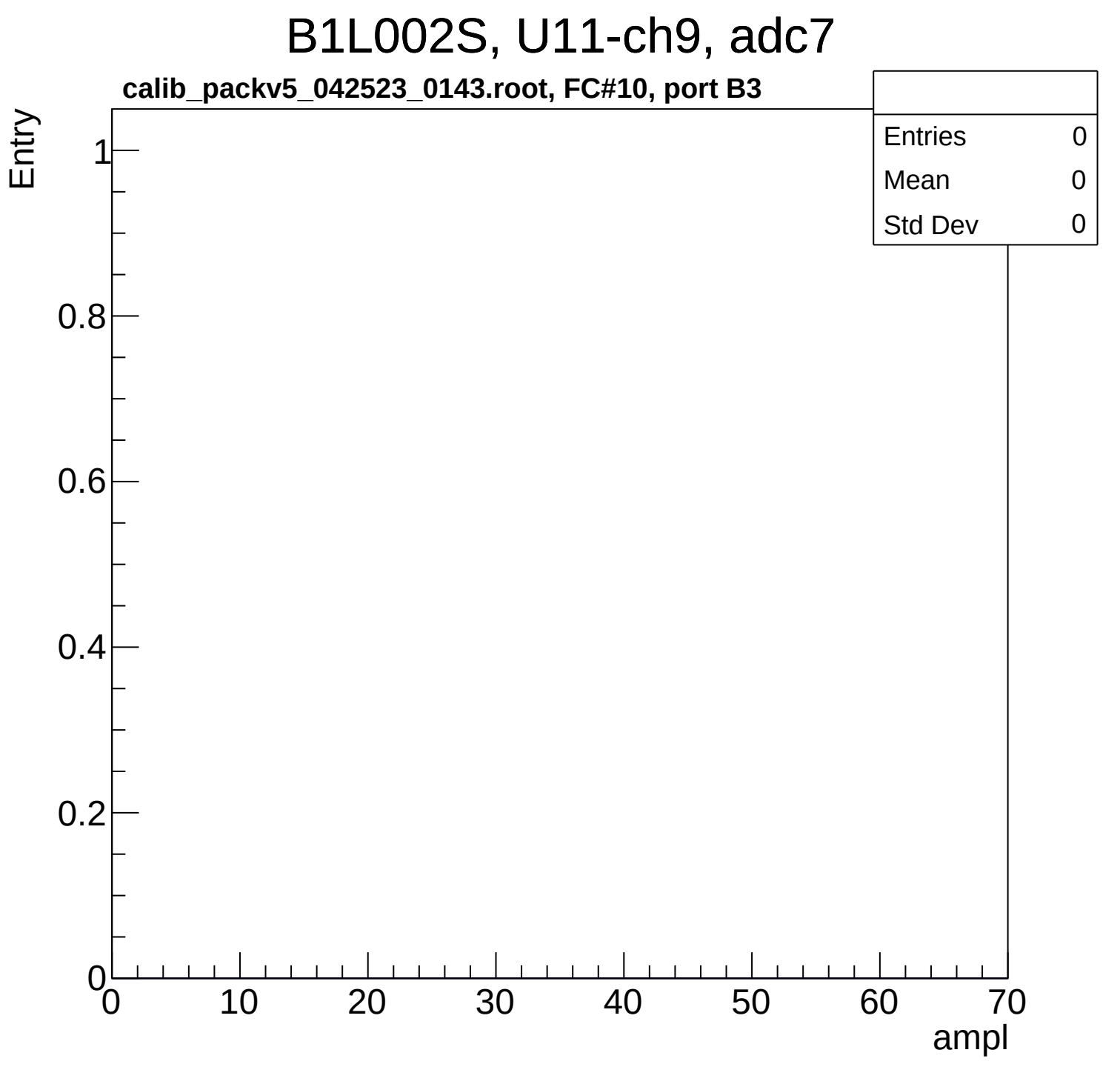

B1L002S, U11-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.86
Std Dev 2.837
Gaus mean : 41.5037
Gaus Width: 2.9756
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

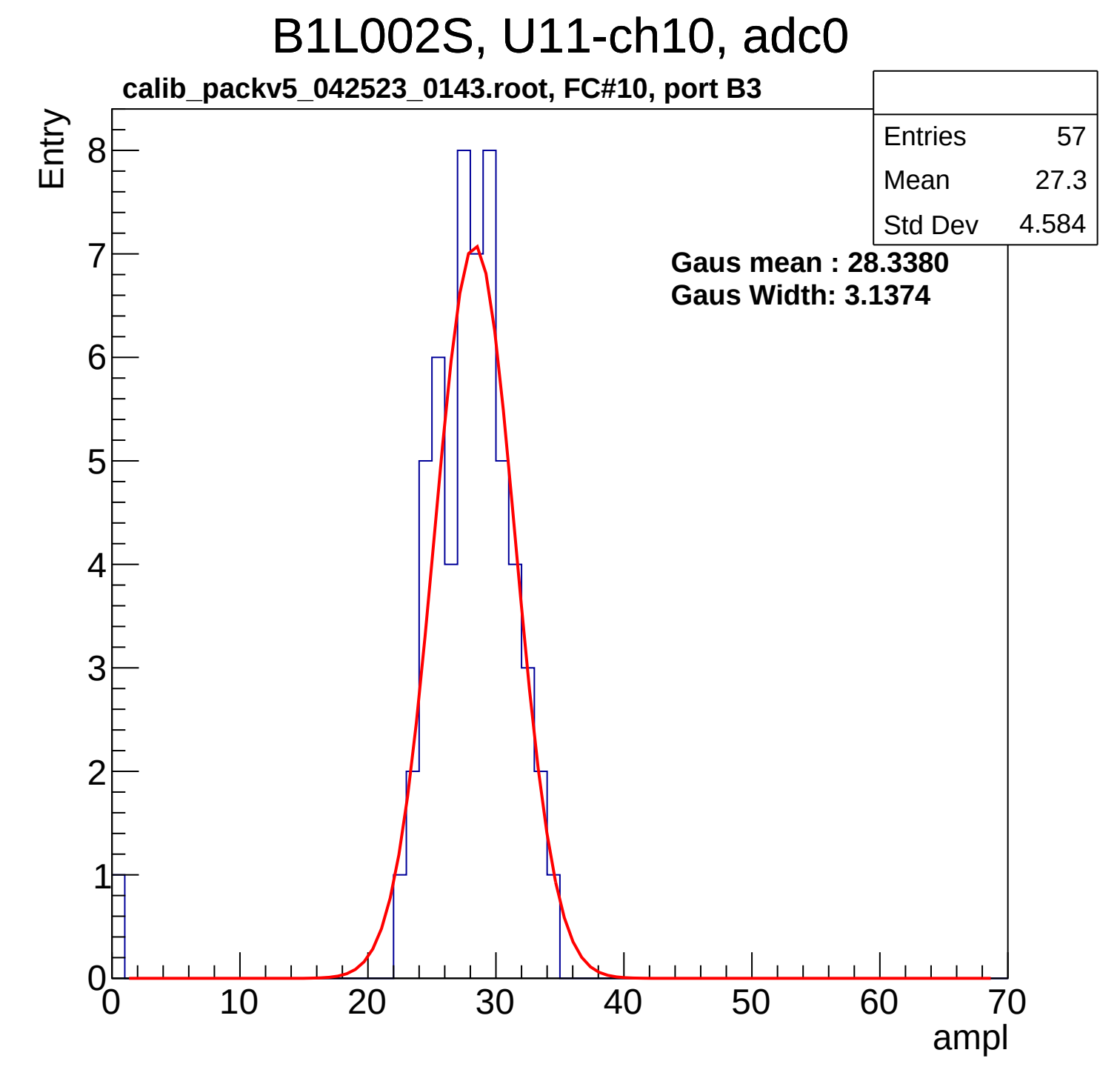

B1L002S, U11-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.2
Std Dev 3.178
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

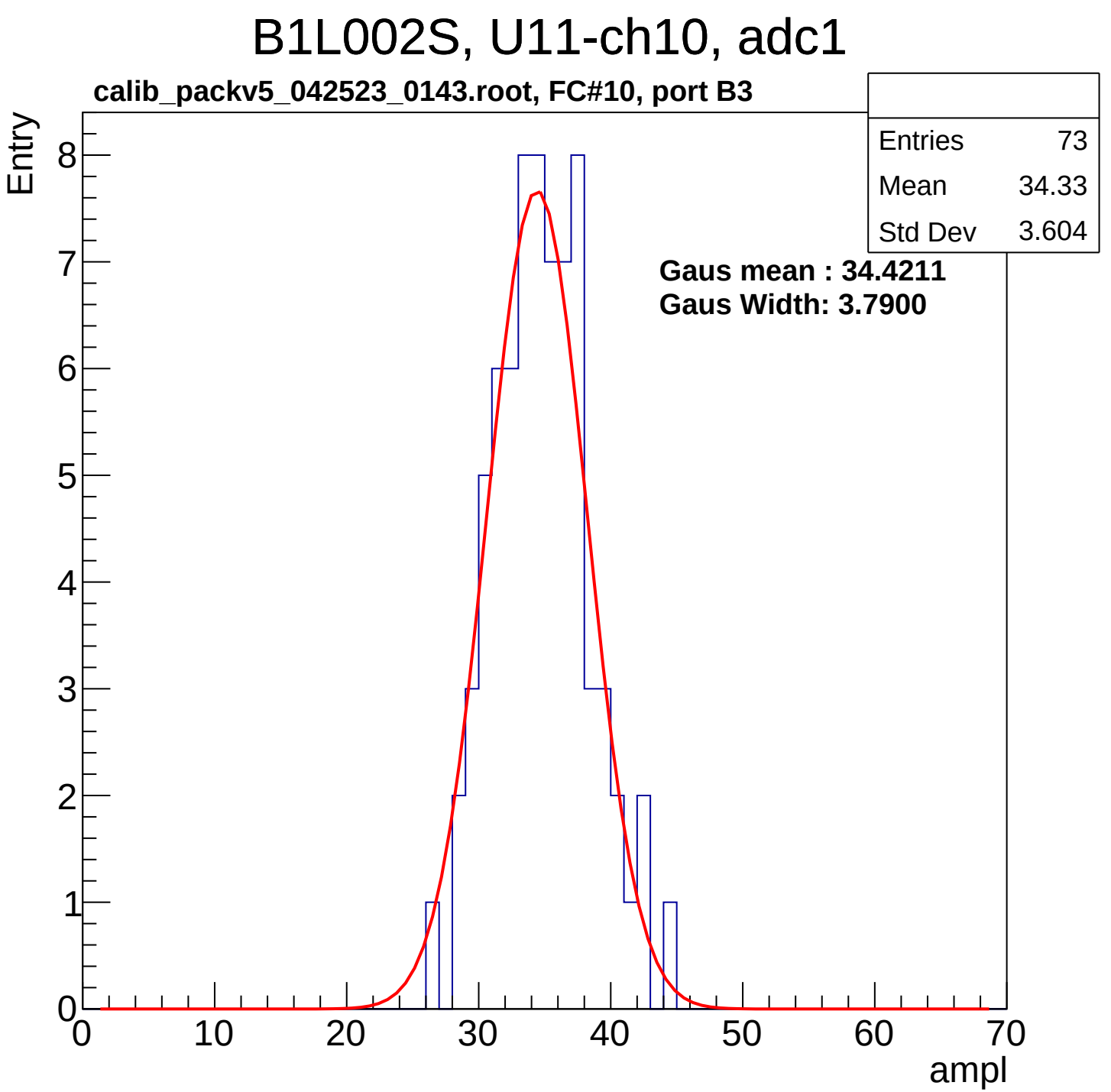

B1L002S, U11-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.81
Std Dev 2.939
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

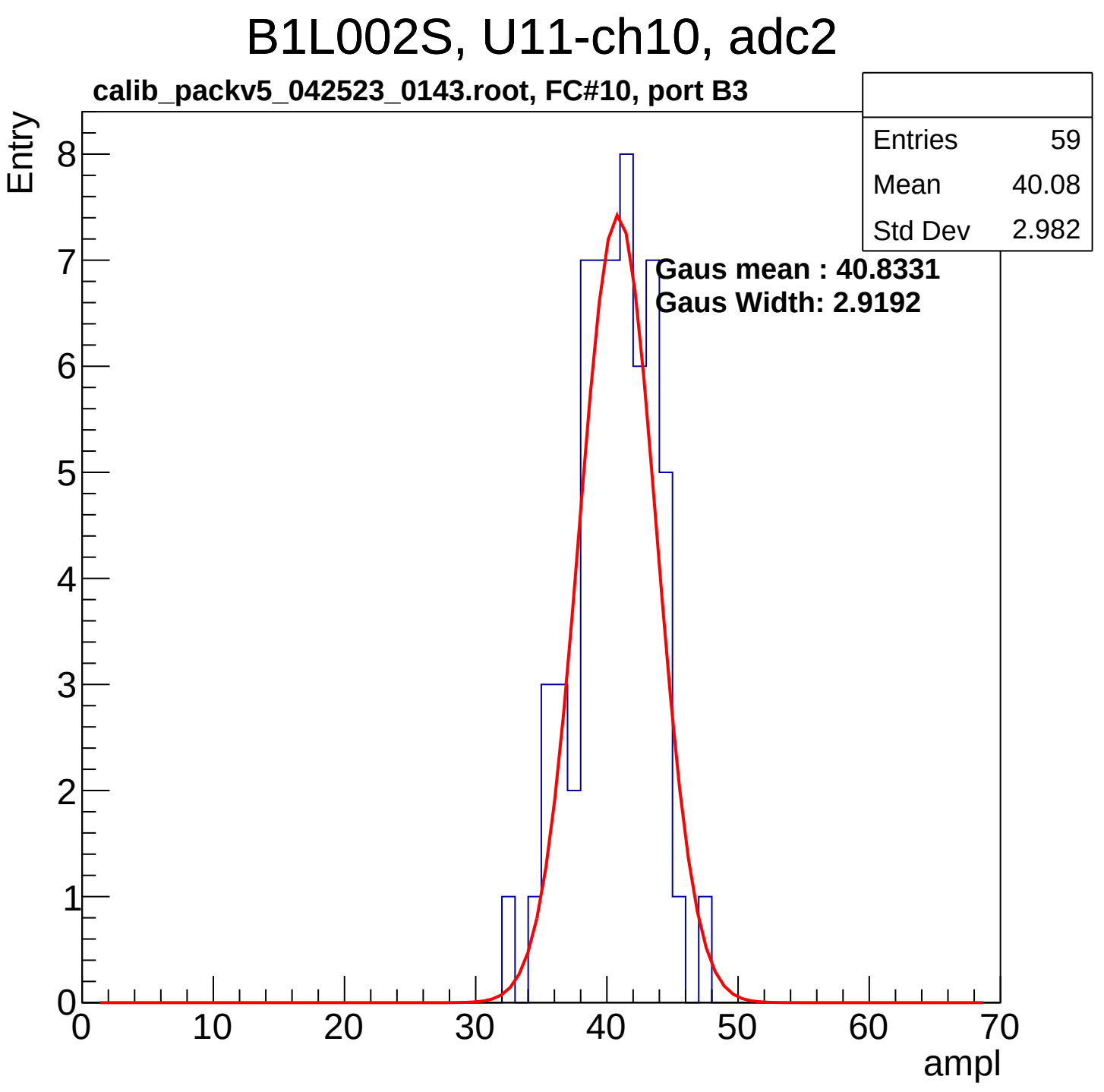

B1L002S, U11-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.14
Std Dev 7.89
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

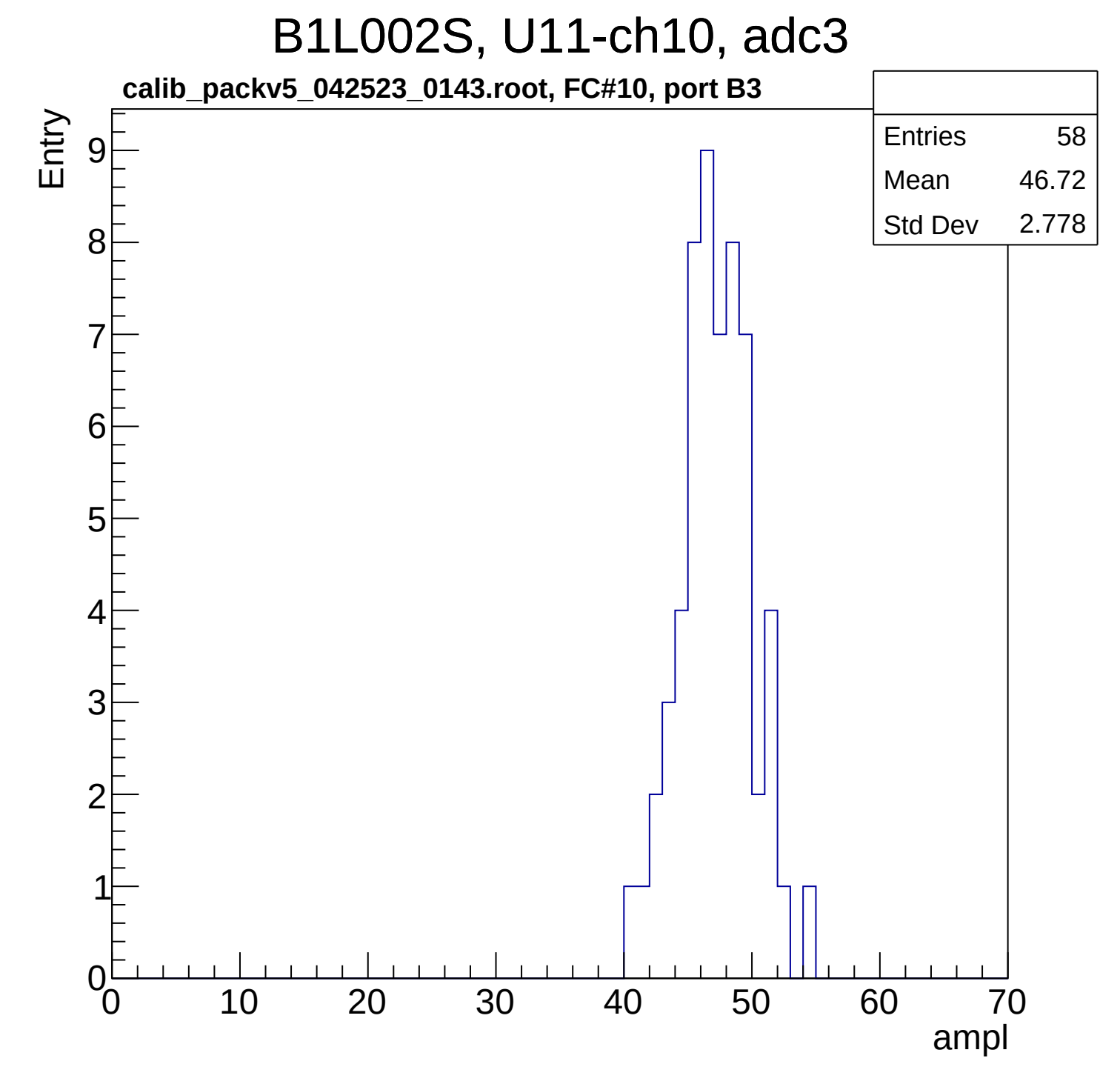

B1L002S, U11-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62.12
Std Dev 1.269
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.3
Std Dev 4.584
Gaus mean : 28.3380
Gaus Width: 3.1374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.33
Std Dev 3.604
Gaus mean : 34.4211
Gaus Width: 3.7900
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.08
Std Dev 2.982
Gaus mean : 40.8331
Gaus Width: 2.9192
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.72
Std Dev 2.778
Entry
ampl
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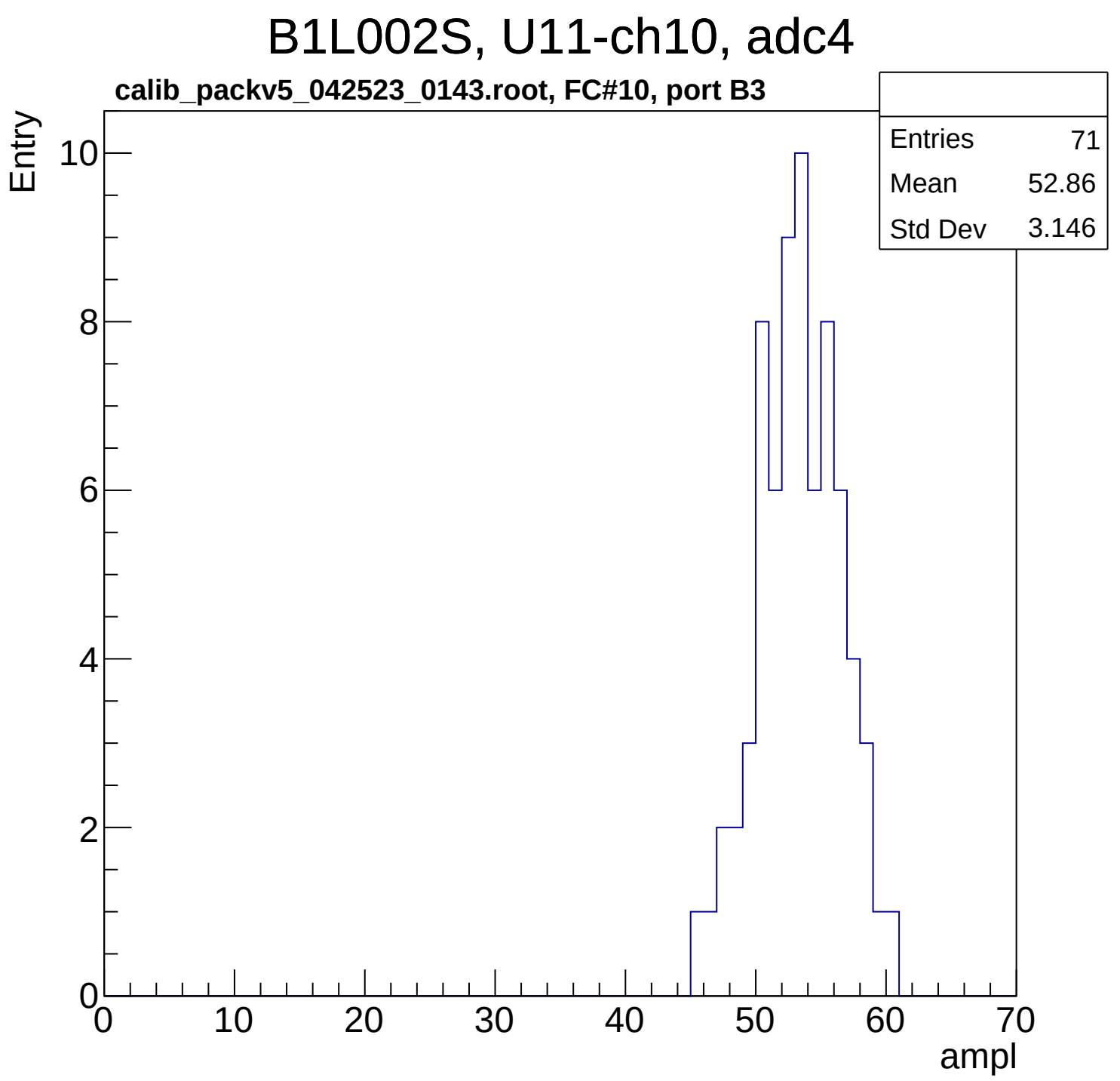
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 52.86
Std Dev 3.146
Entry
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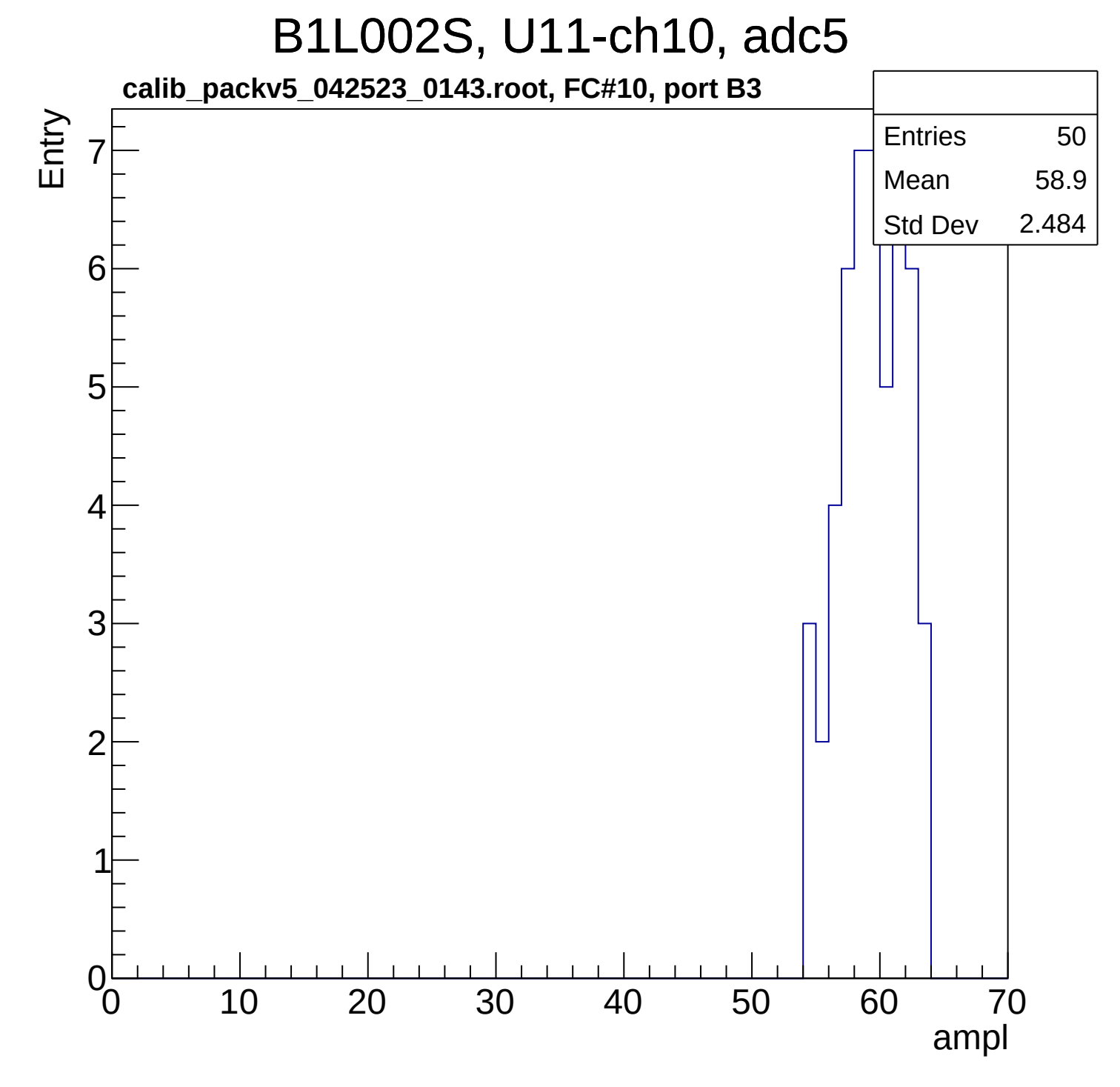
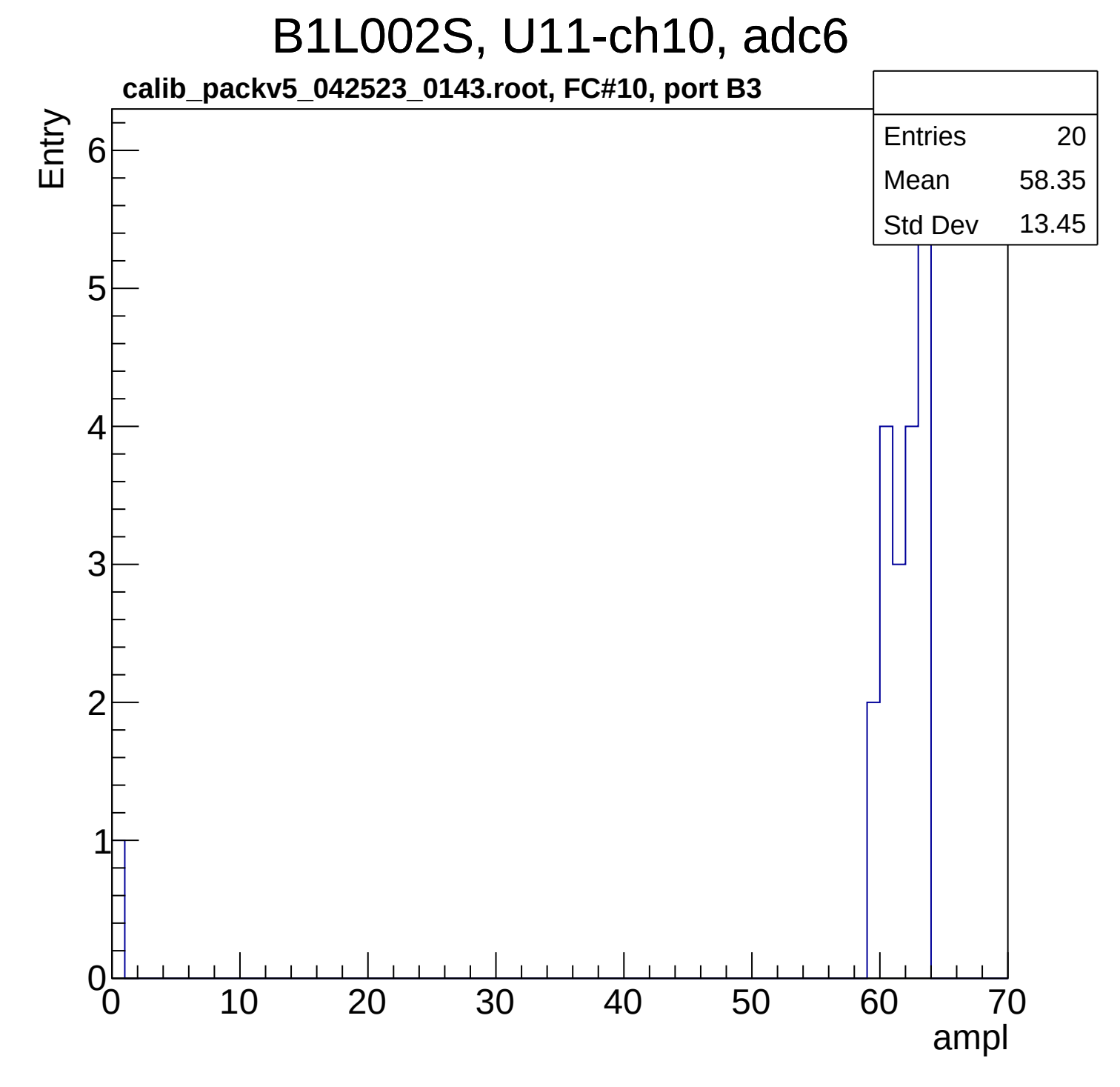
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.9
Std Dev 2.484
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.35
Std Dev 13.45
Entry
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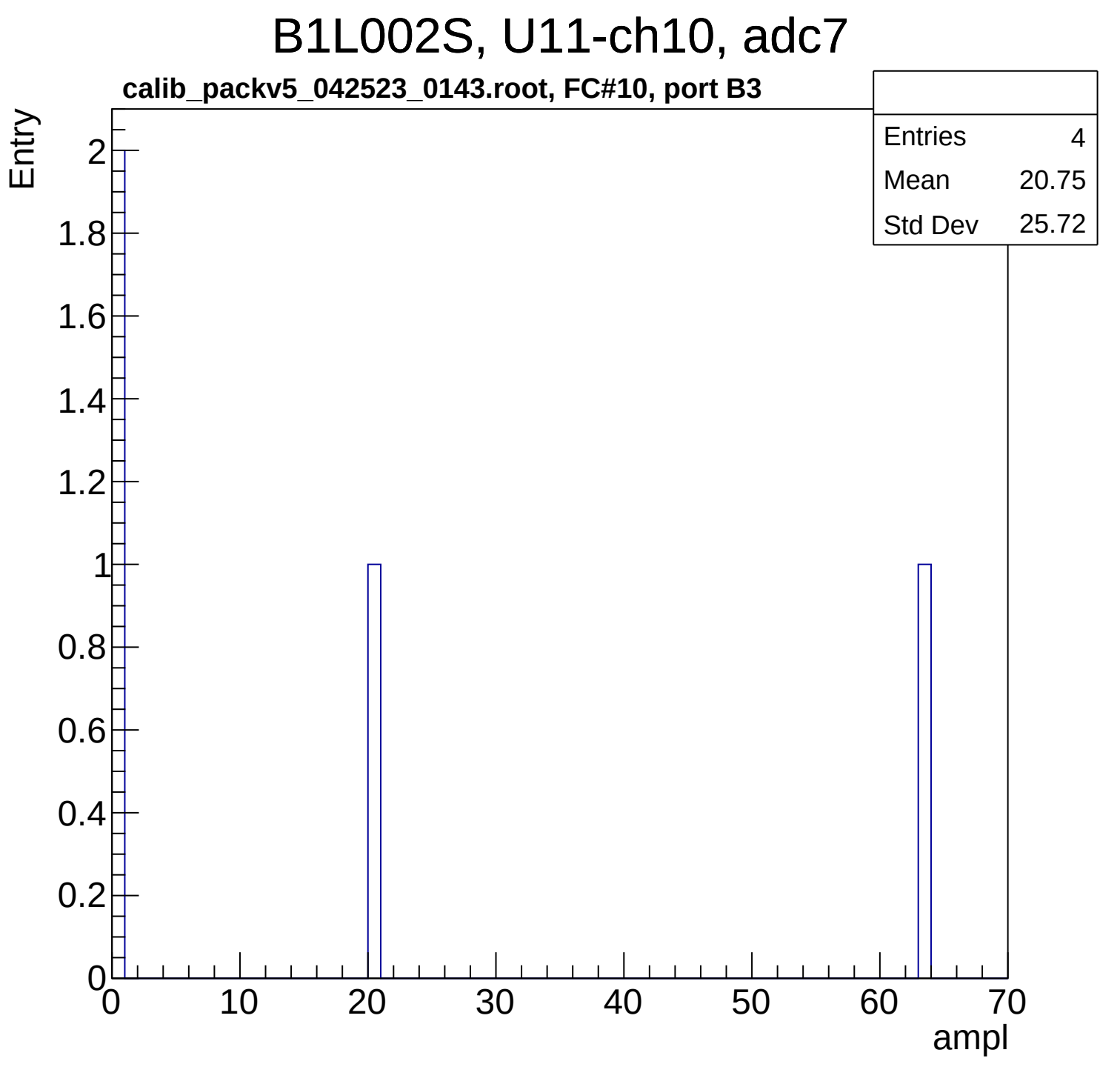
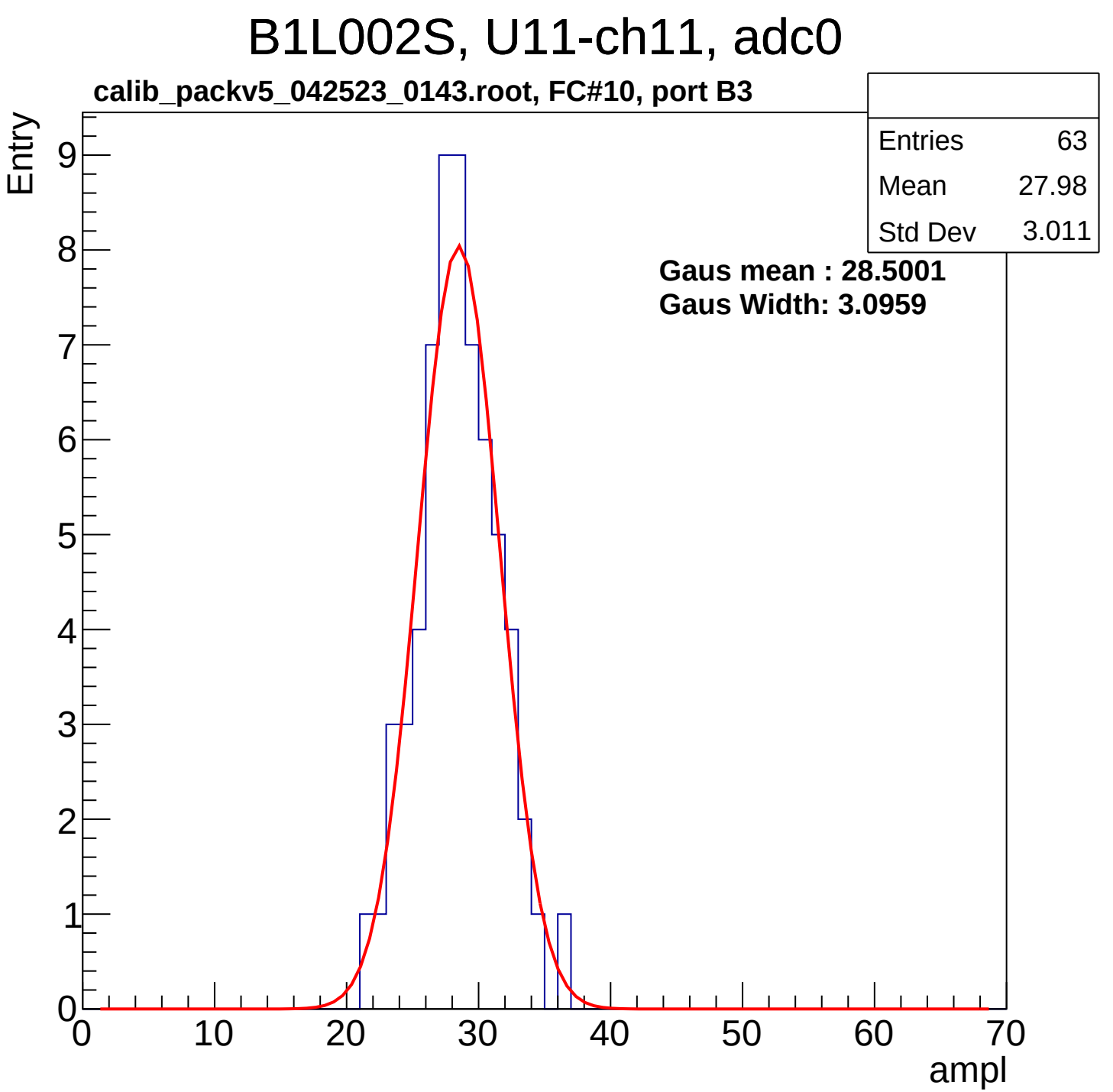
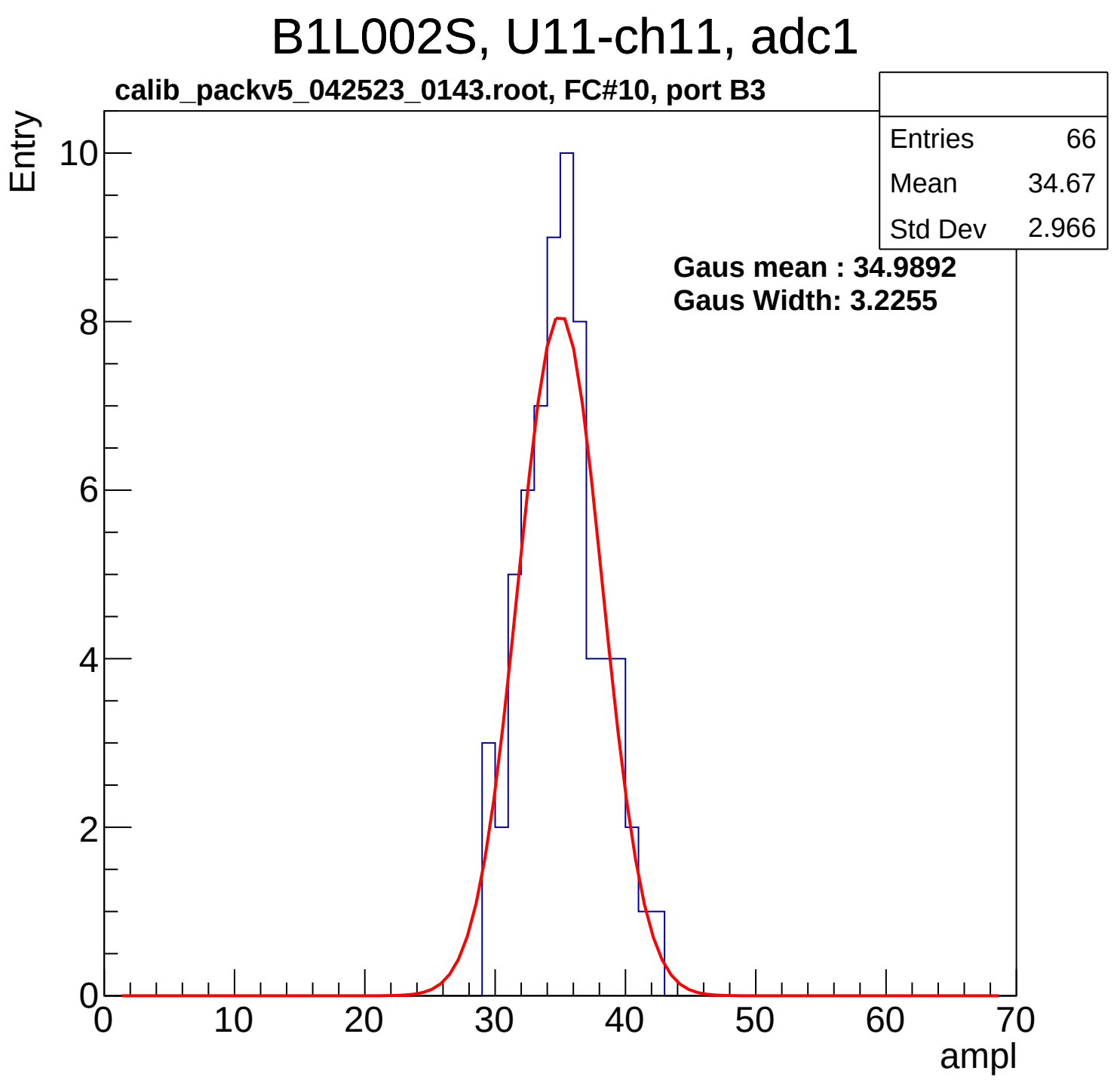
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 20.75
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.98
Std Dev 3.011
Gaus mean : 28.5001
Gaus Width: 3.0959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.67
Std Dev 2.966
Gaus mean : 34.9892
Gaus Width: 3.2255
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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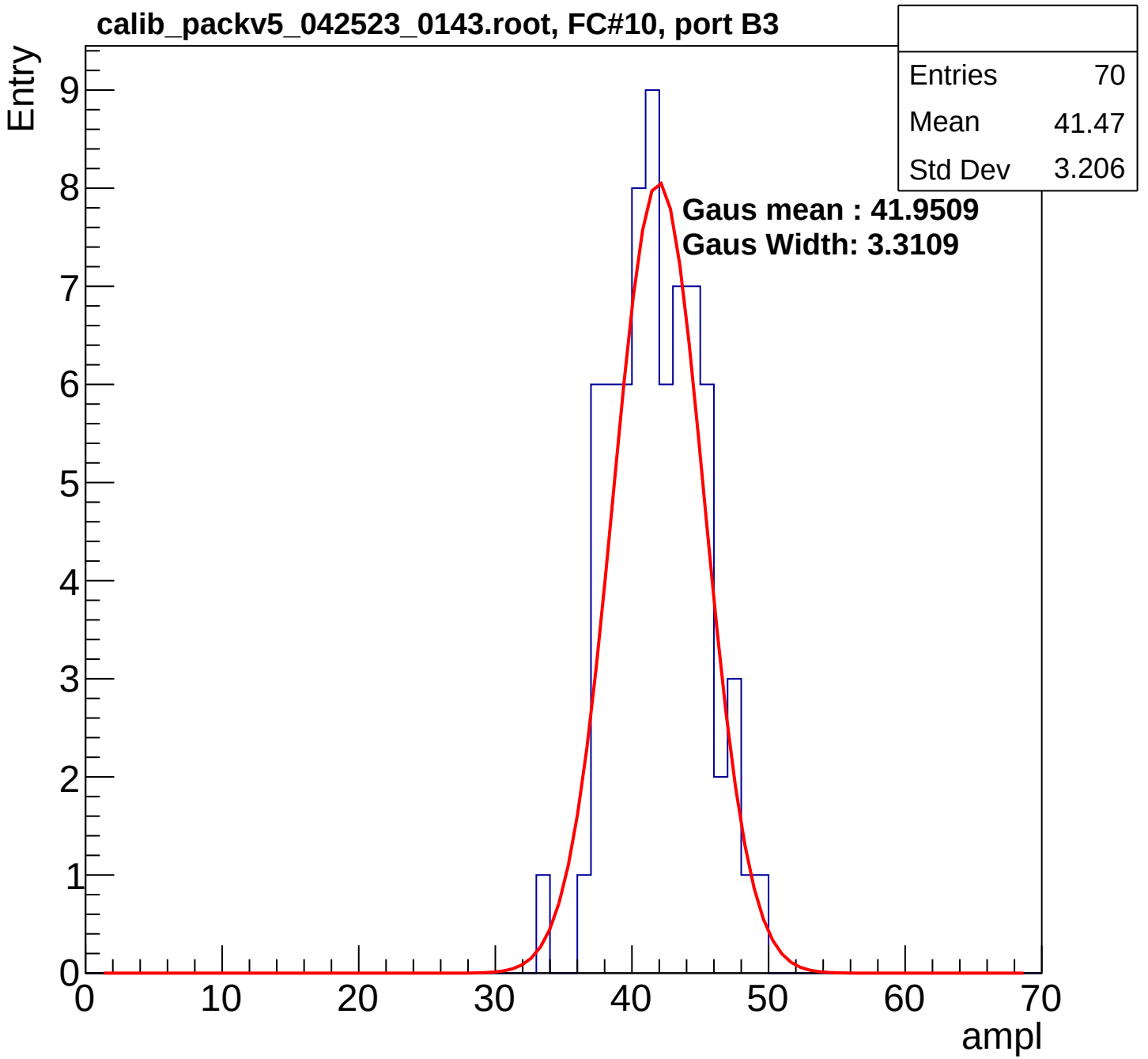
B1L002S, U11-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.47
Std Dev 3.206
Gaus mean : 41.9509
Gaus Width: 3.3109
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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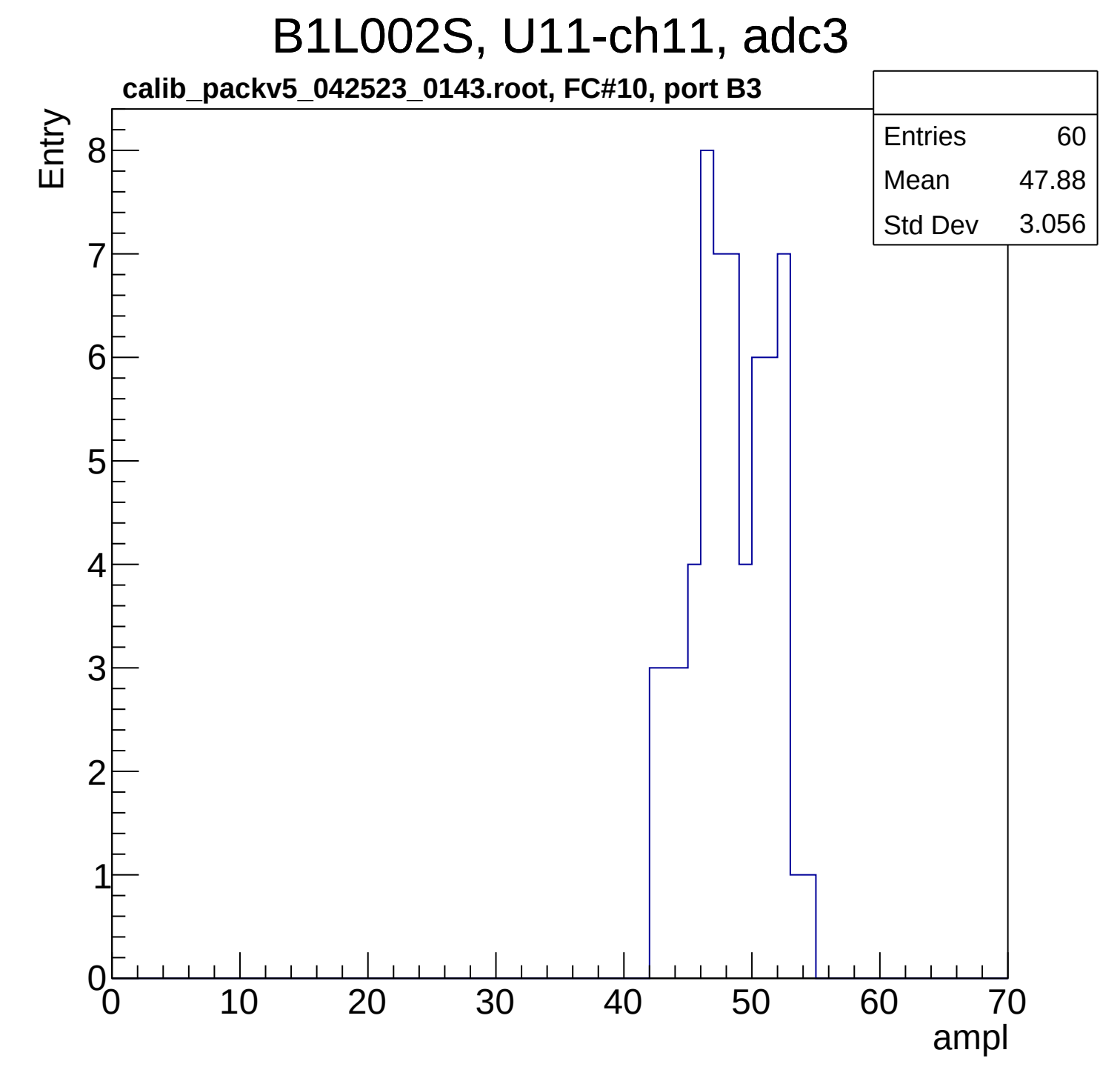
40
50
60
70


B1L002S, U11-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.88
Std Dev 3.056
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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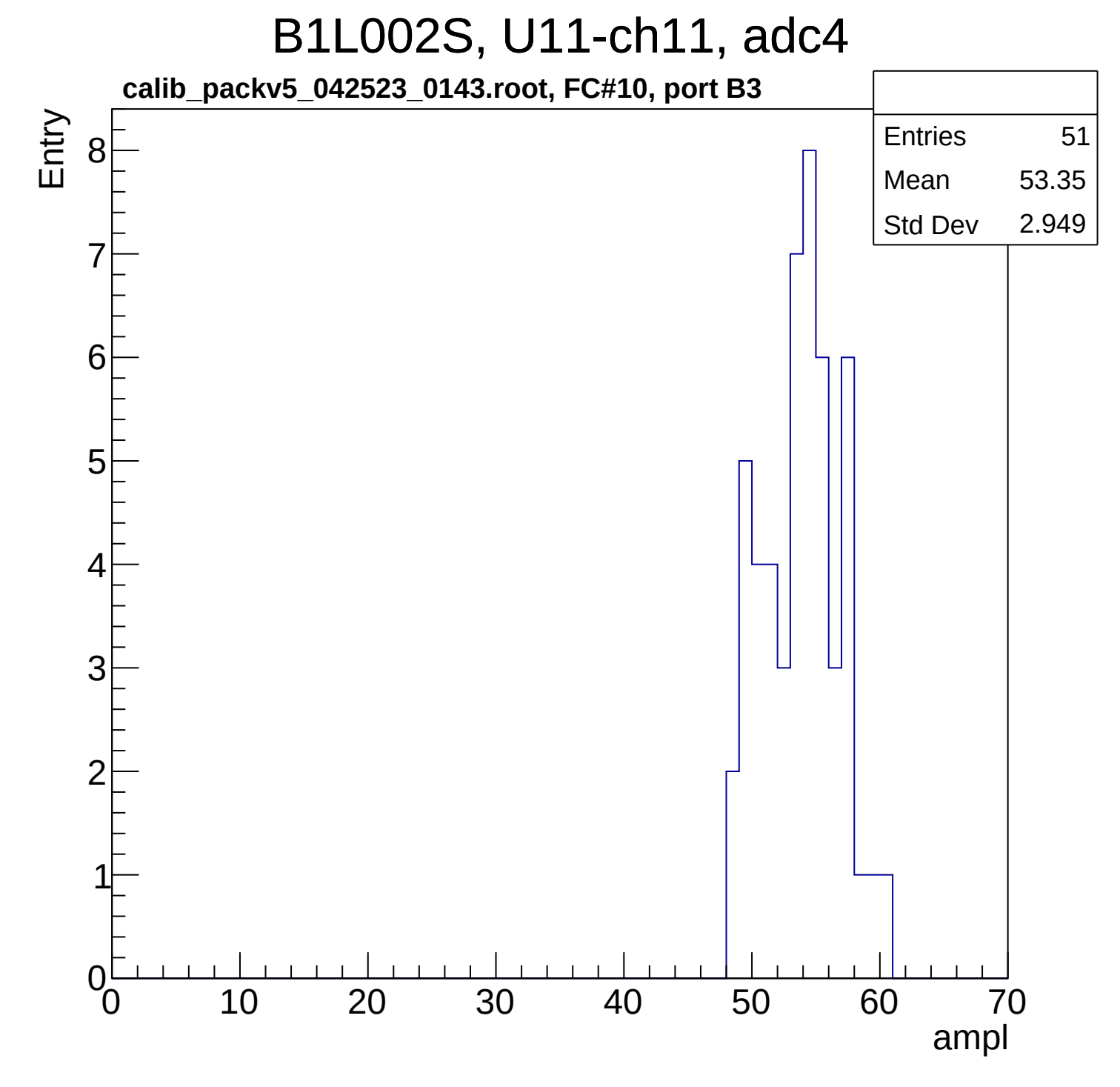
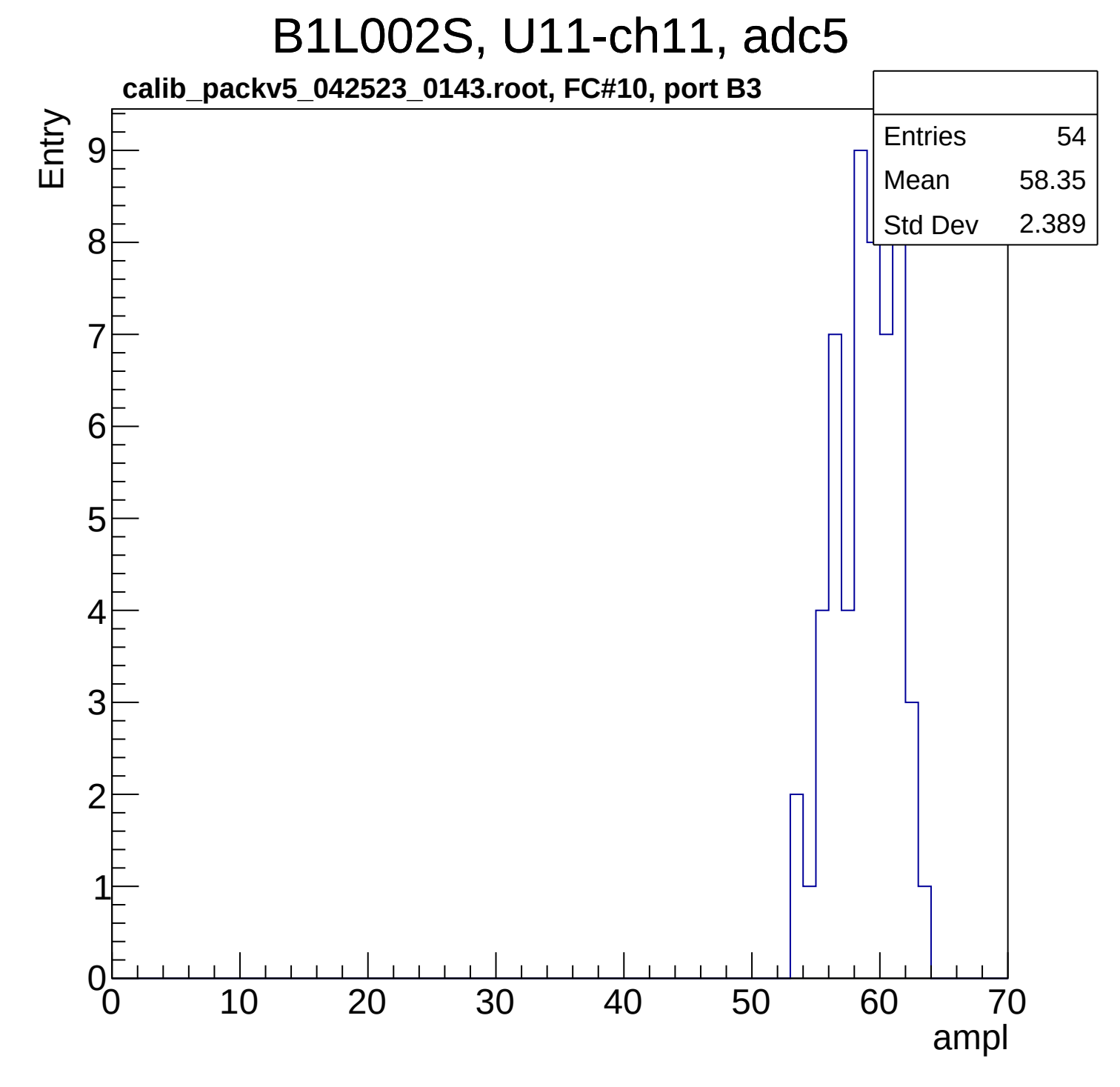
60
70


B1L002S, U11-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.35
Std Dev 2.949
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.35
Std Dev 2.389
Entry
ampl
0
1
2
3
4
5
6
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8
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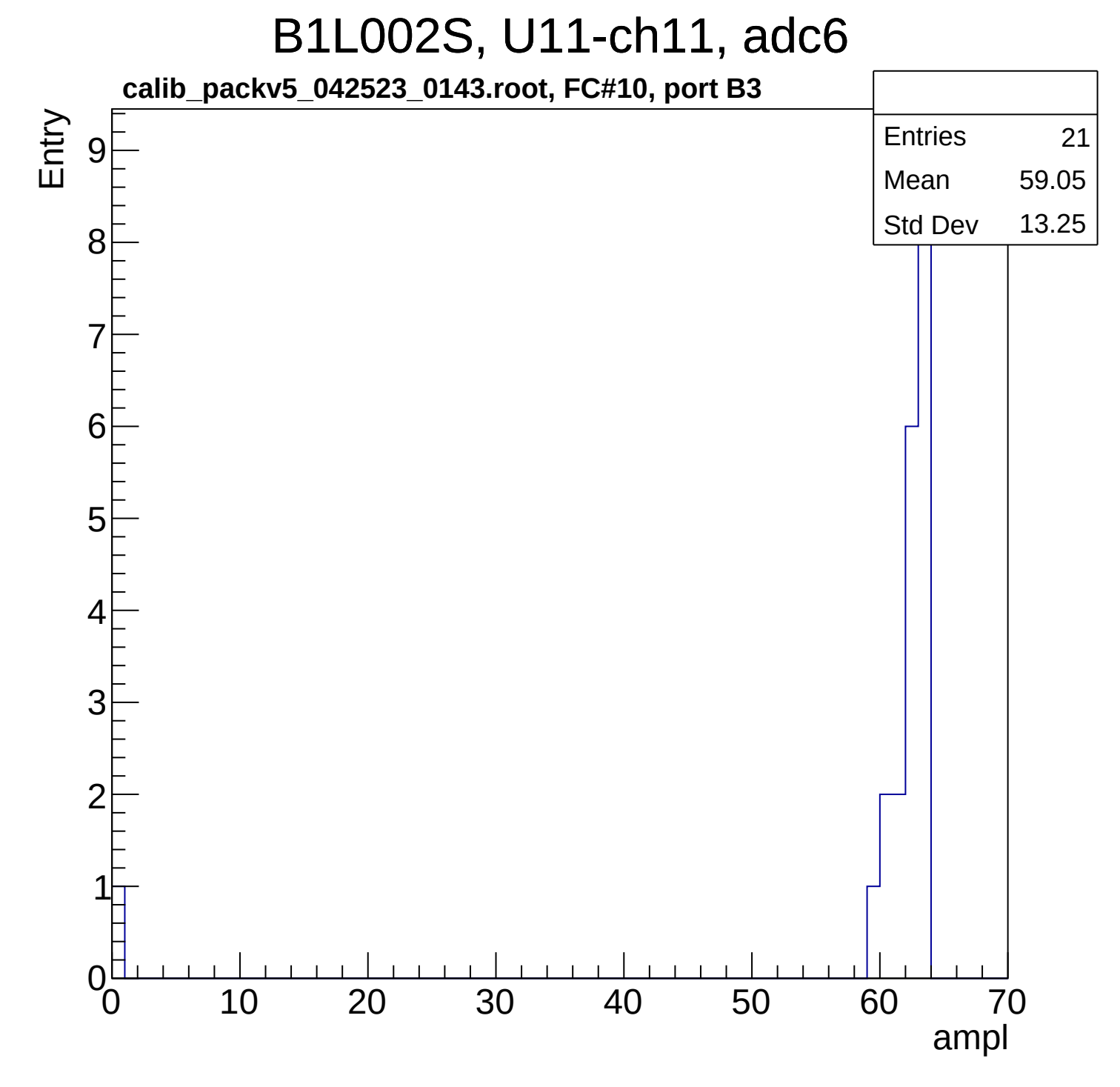
B1L002S, U11-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 59.05
Std Dev 13.25
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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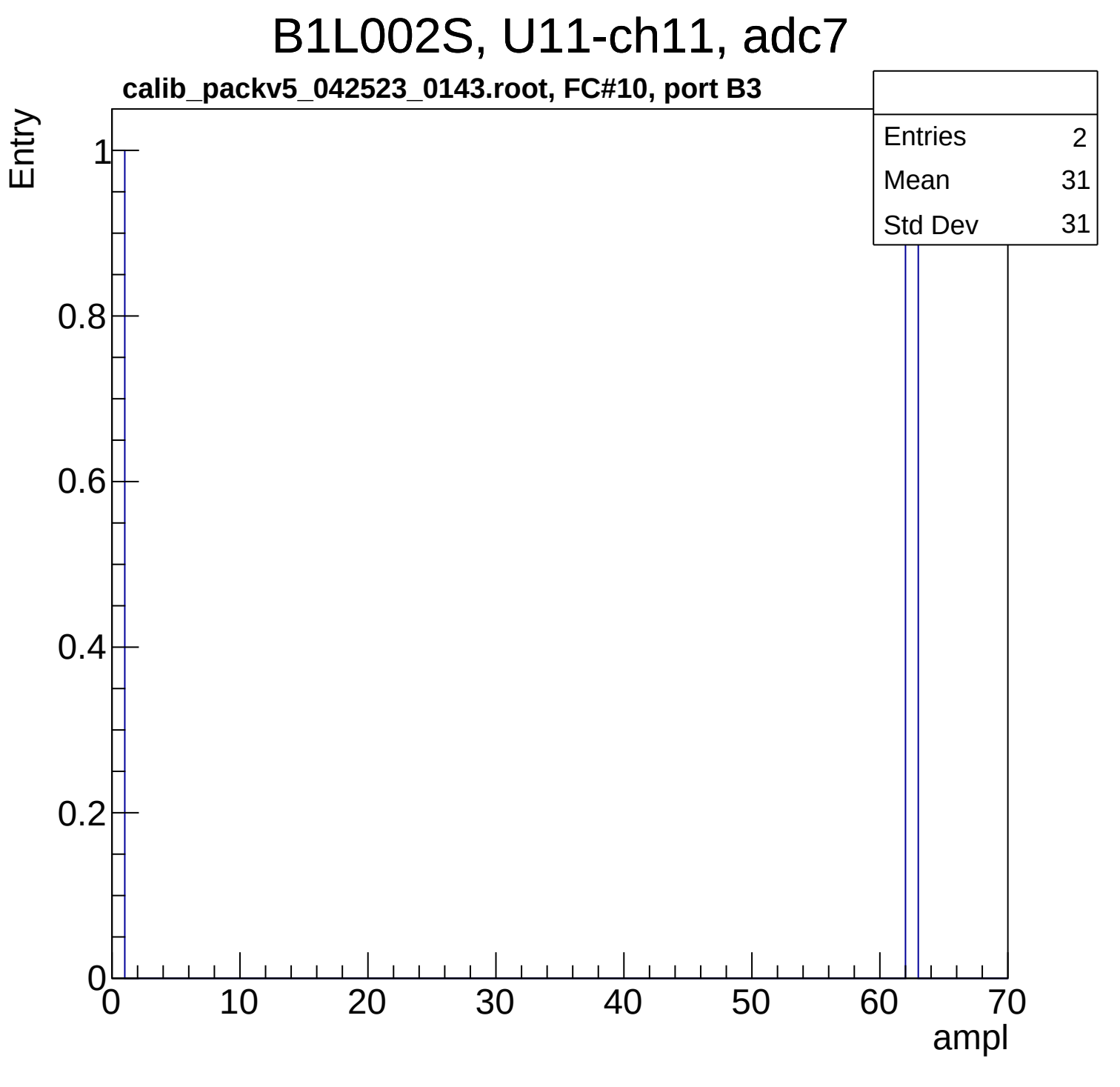
B1L002S, U11-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
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0.8
0.6
0.4
0.2
0
0
10
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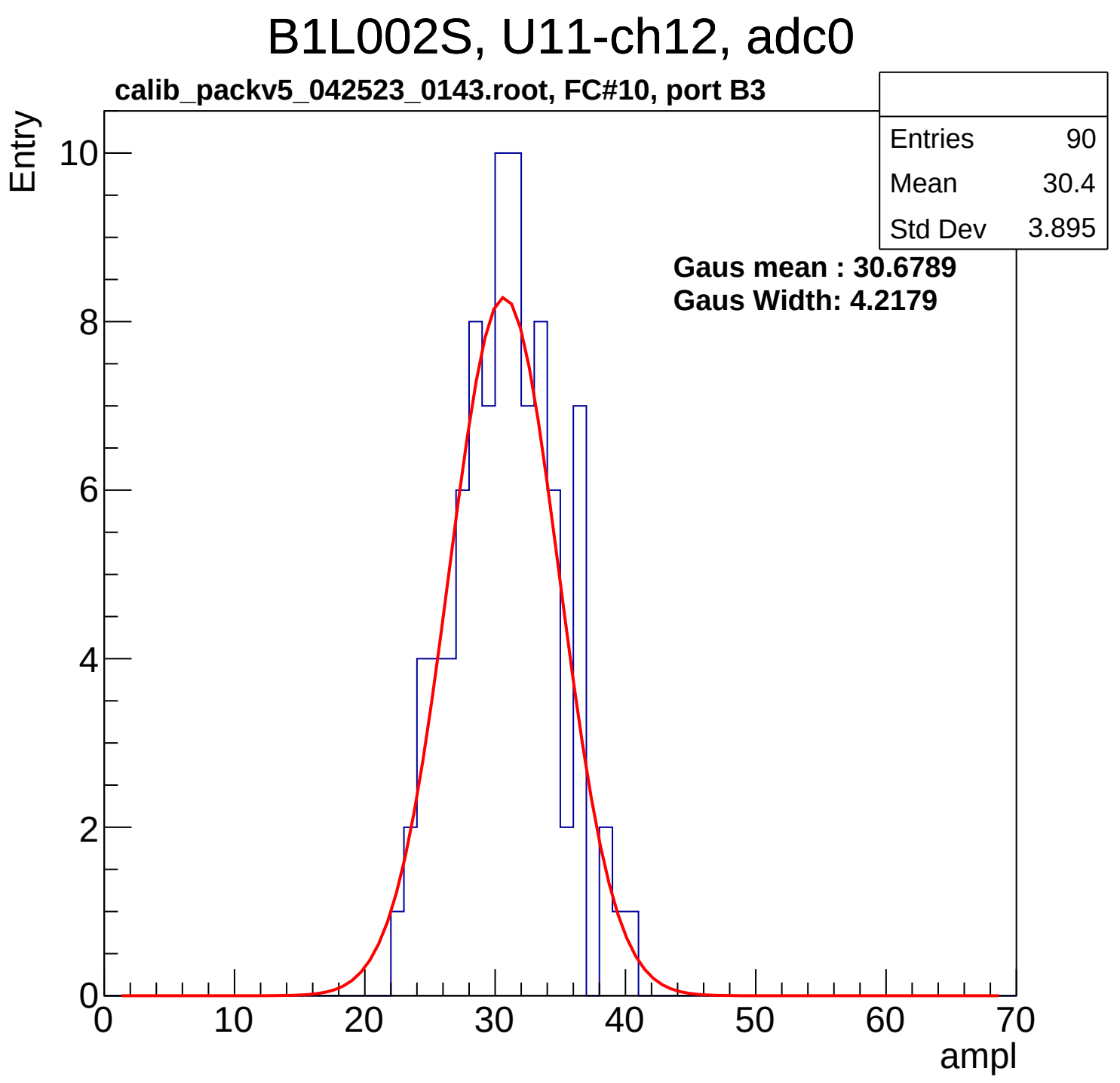
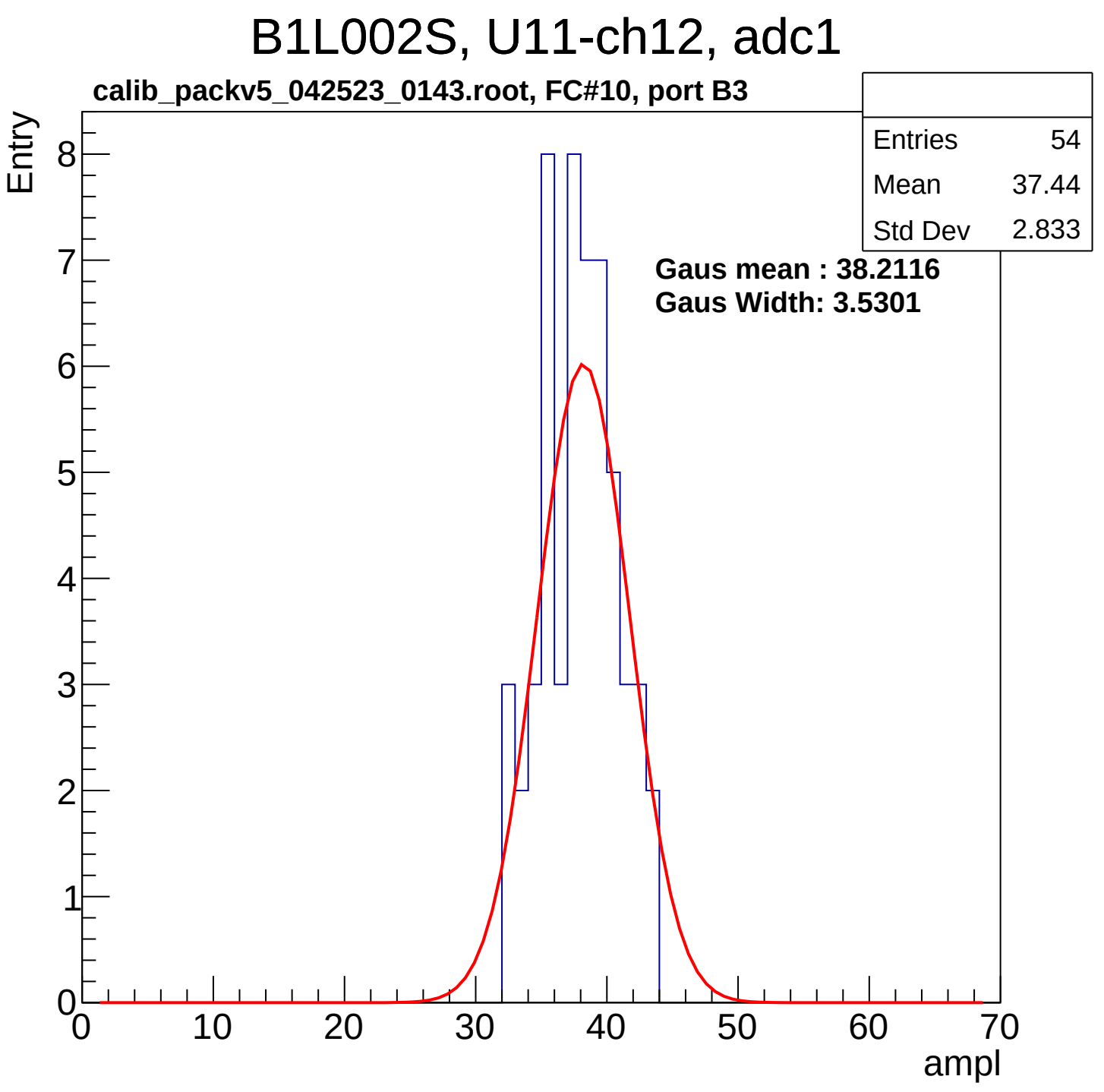
50
60
70
ampl


B1L002S, U11-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 90
Mean 30.4
Std Dev 3.895
Gaus mean : 30.6789
Gaus Width: 4.2179
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 37.44
Std Dev 2.833
Gaus mean : 38.2116
Gaus Width: 3.5301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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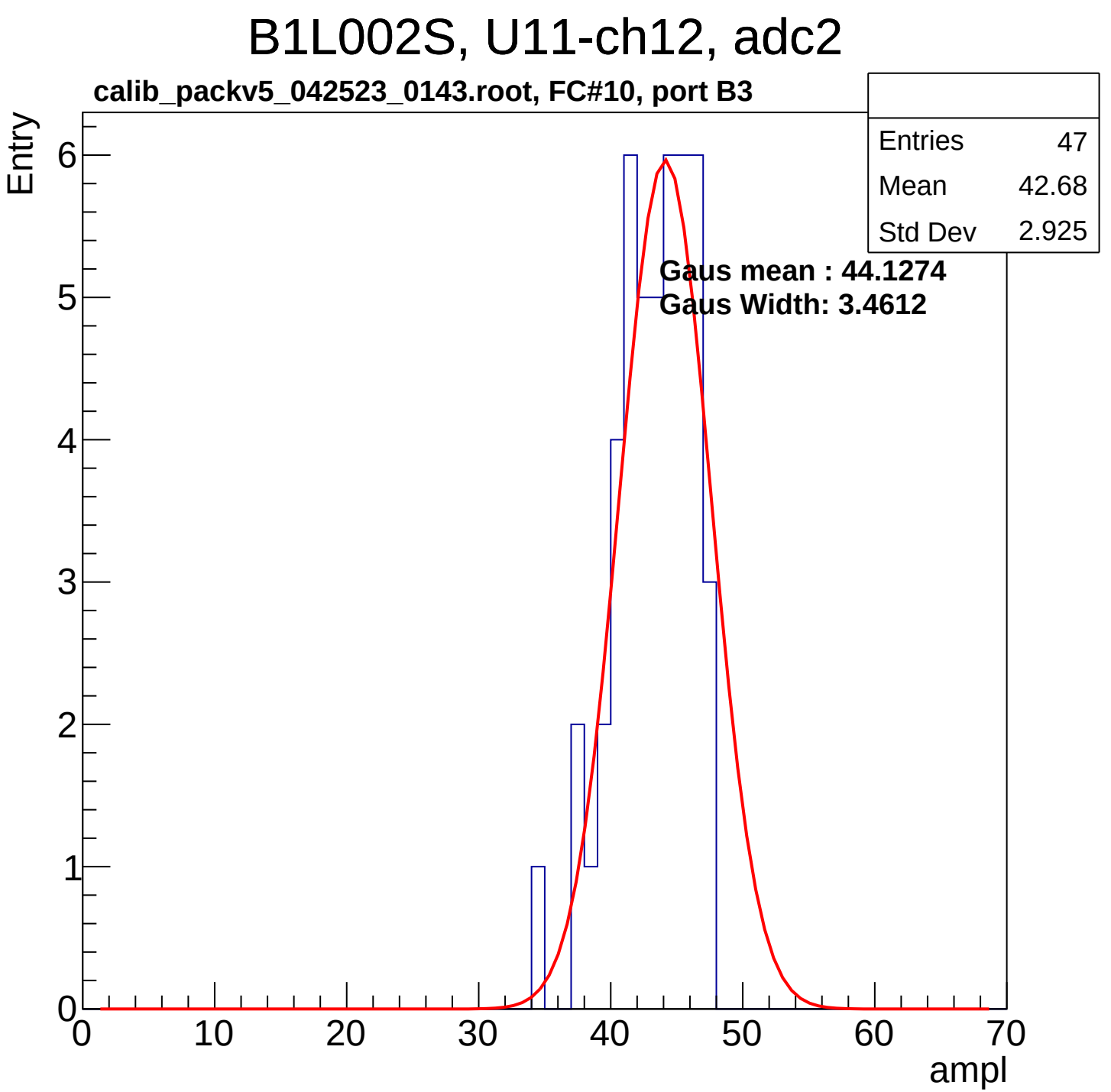
B1L002S, U11-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 42.68
Std Dev 2.925
Gaus mean : 44.1274
Gaus Width: 3.4612
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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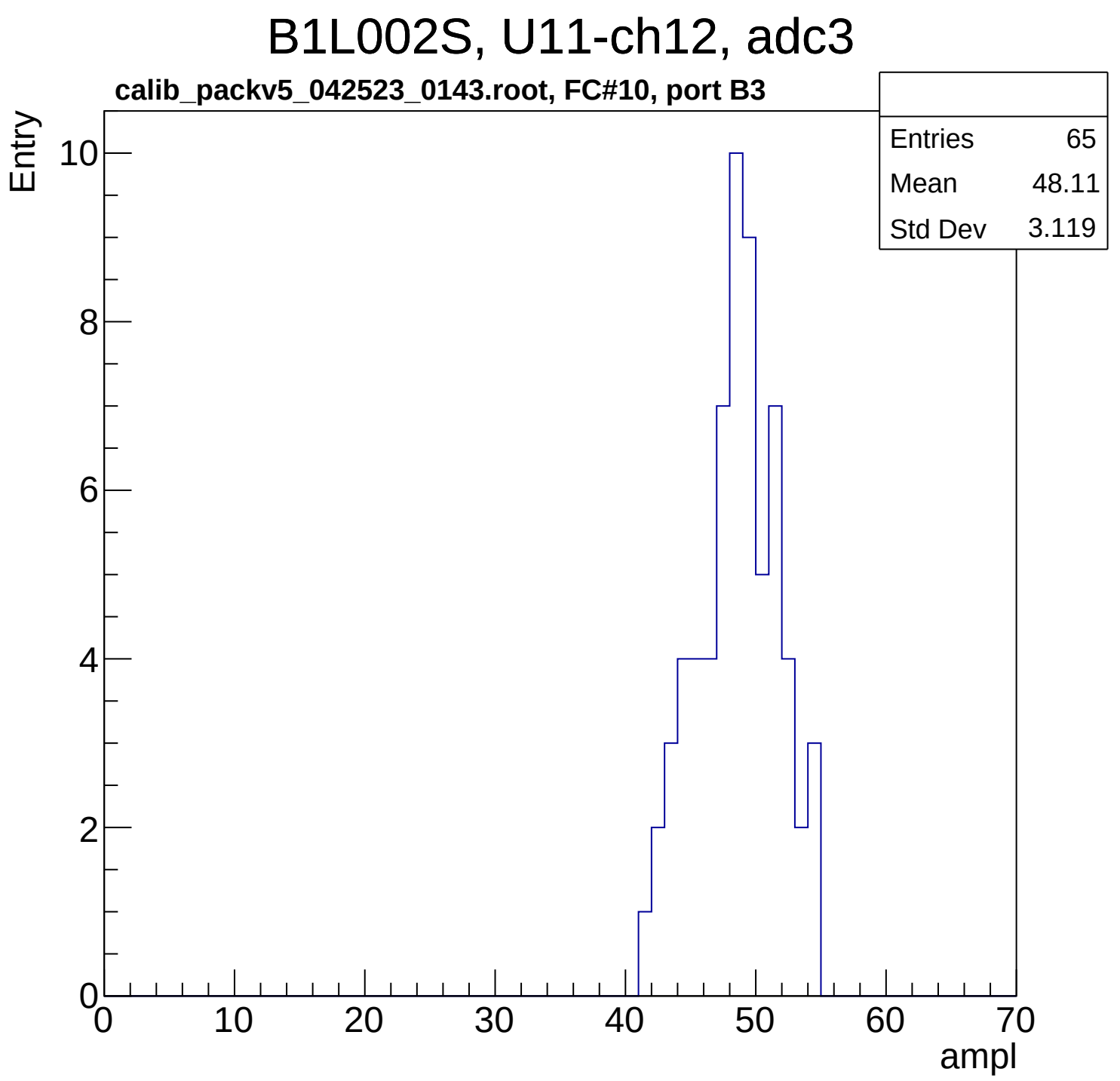
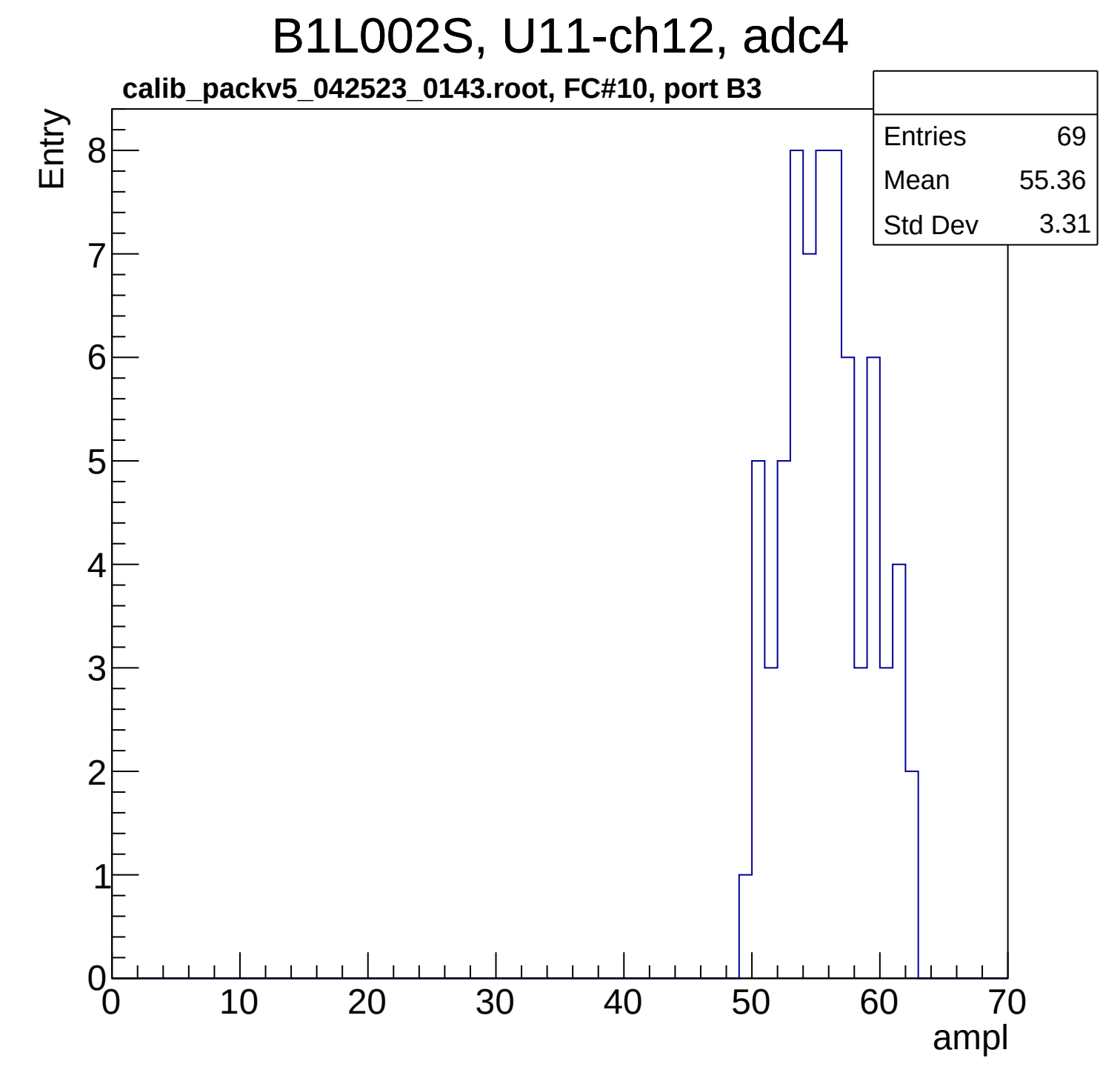

B1L002S, U11-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.11
Std Dev 3.119
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 55.36
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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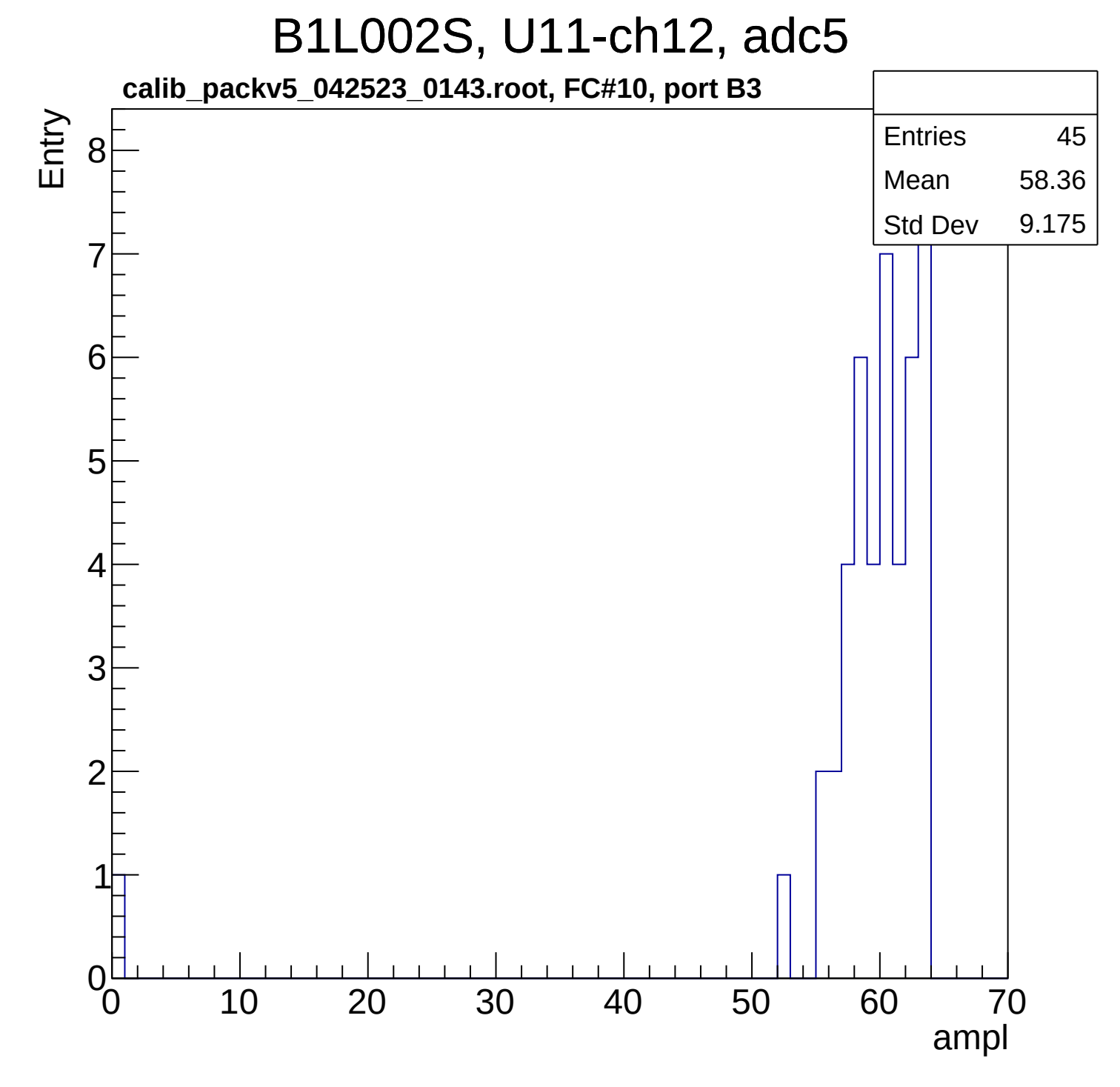
B1L002S, U11-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.36
Std Dev 9.175
Entry
ampl
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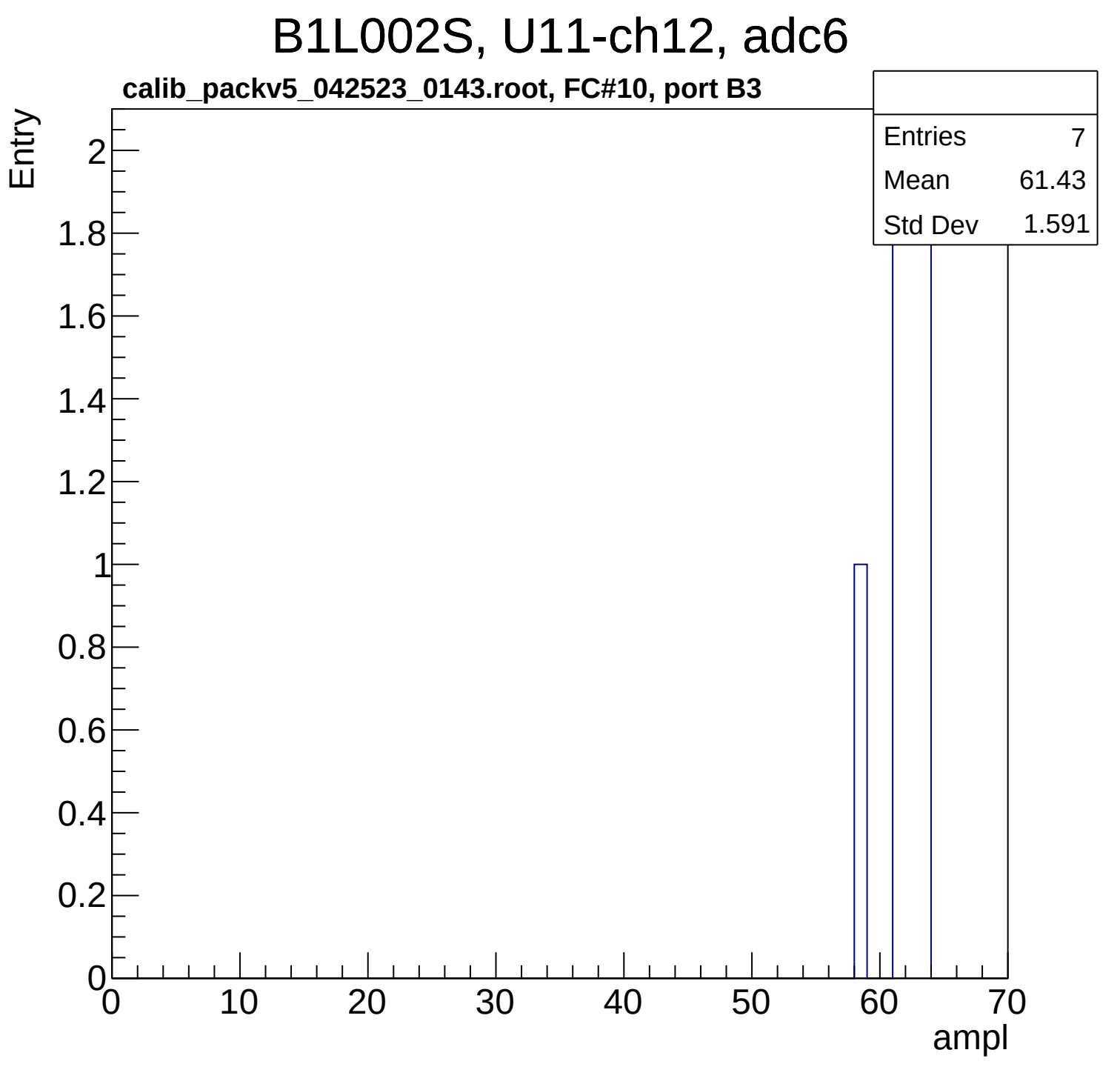
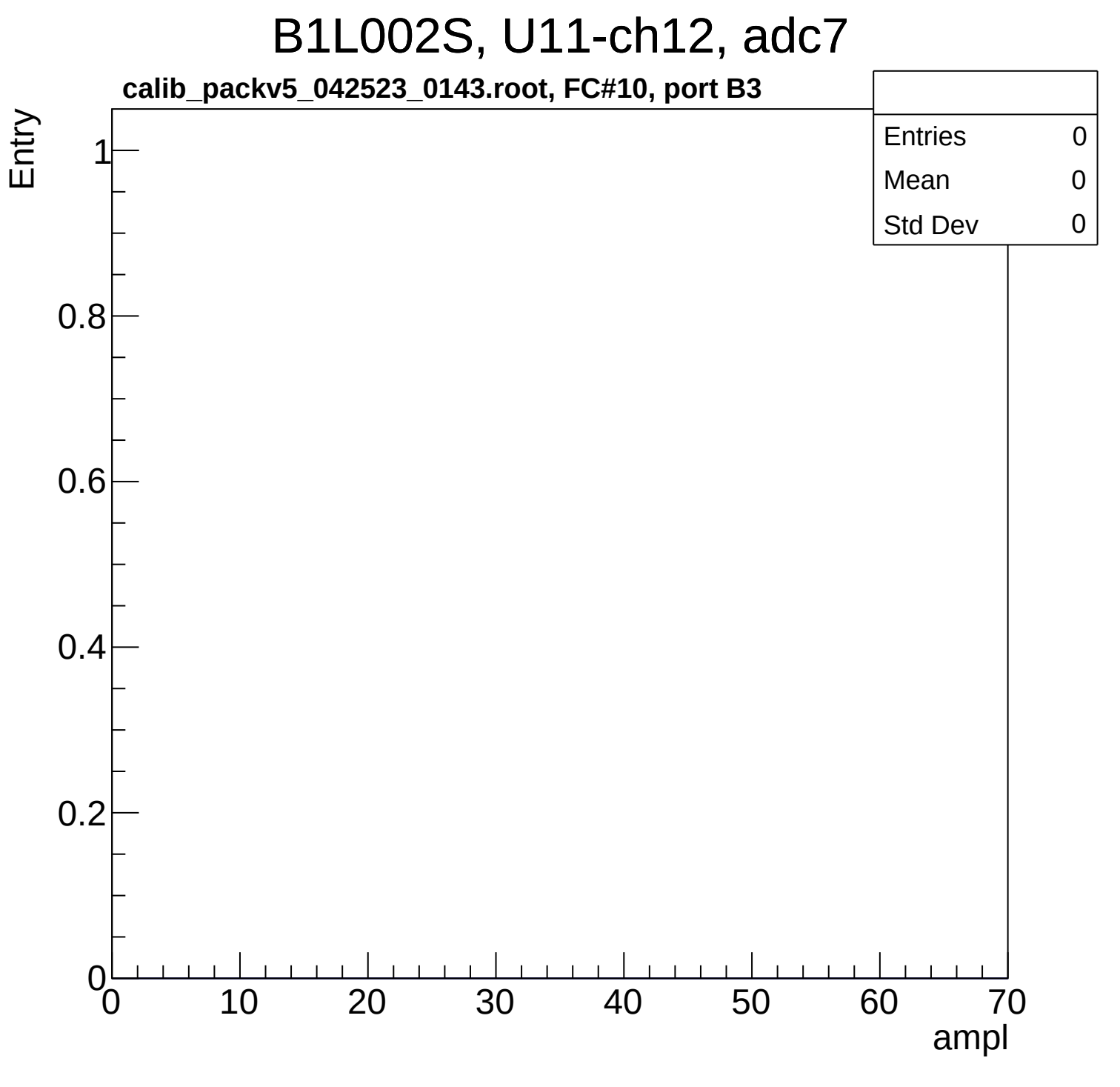
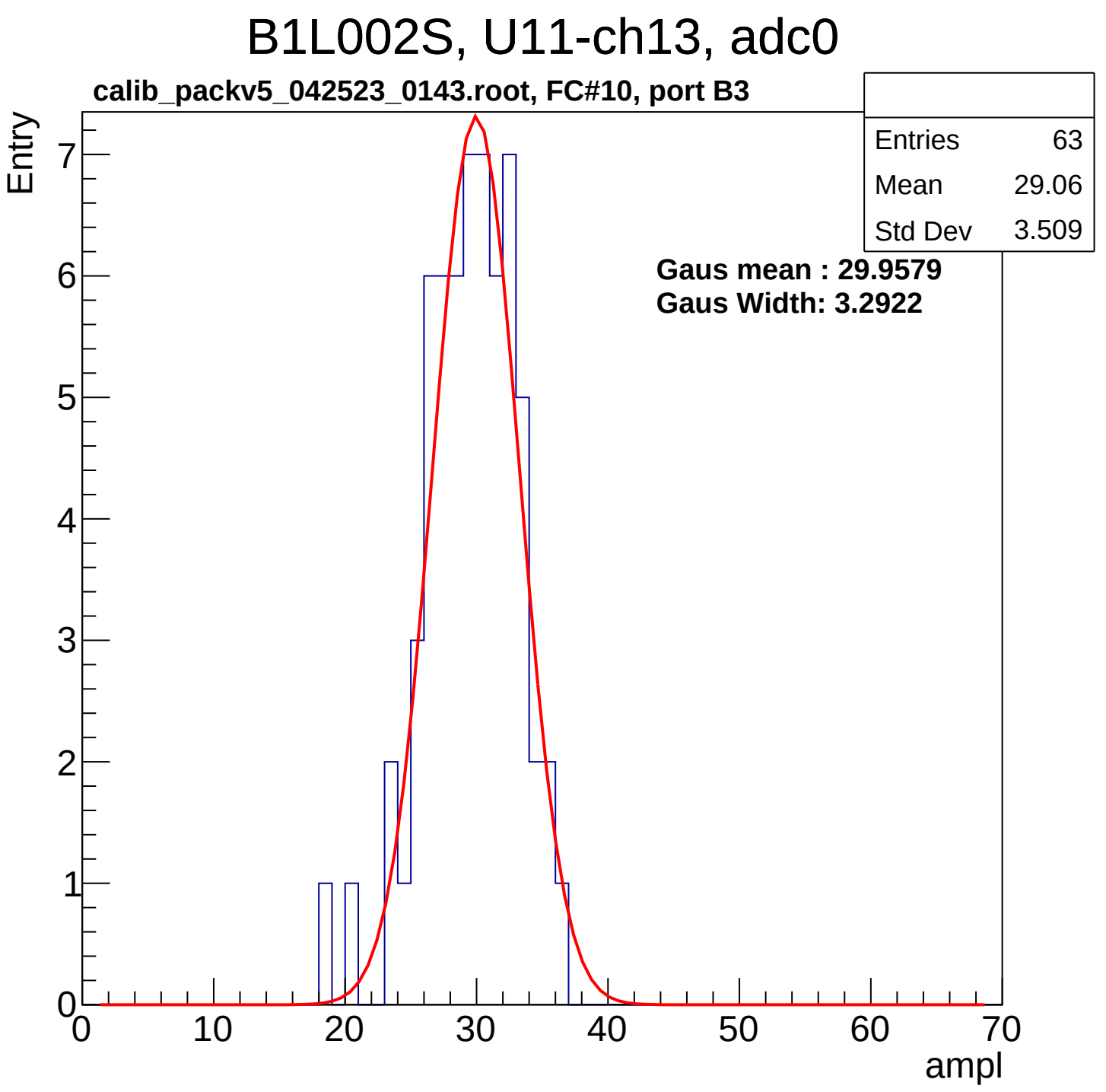
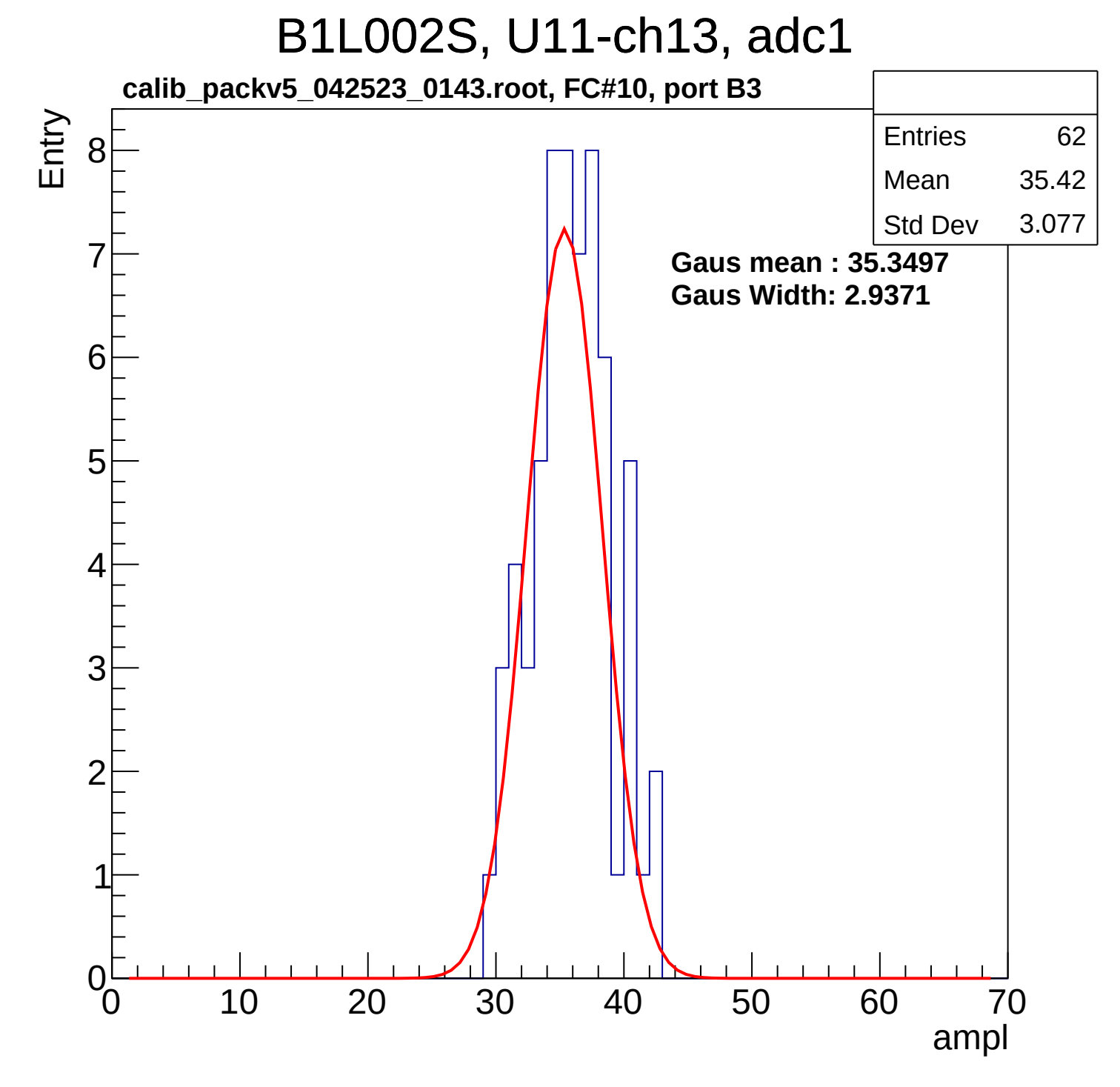
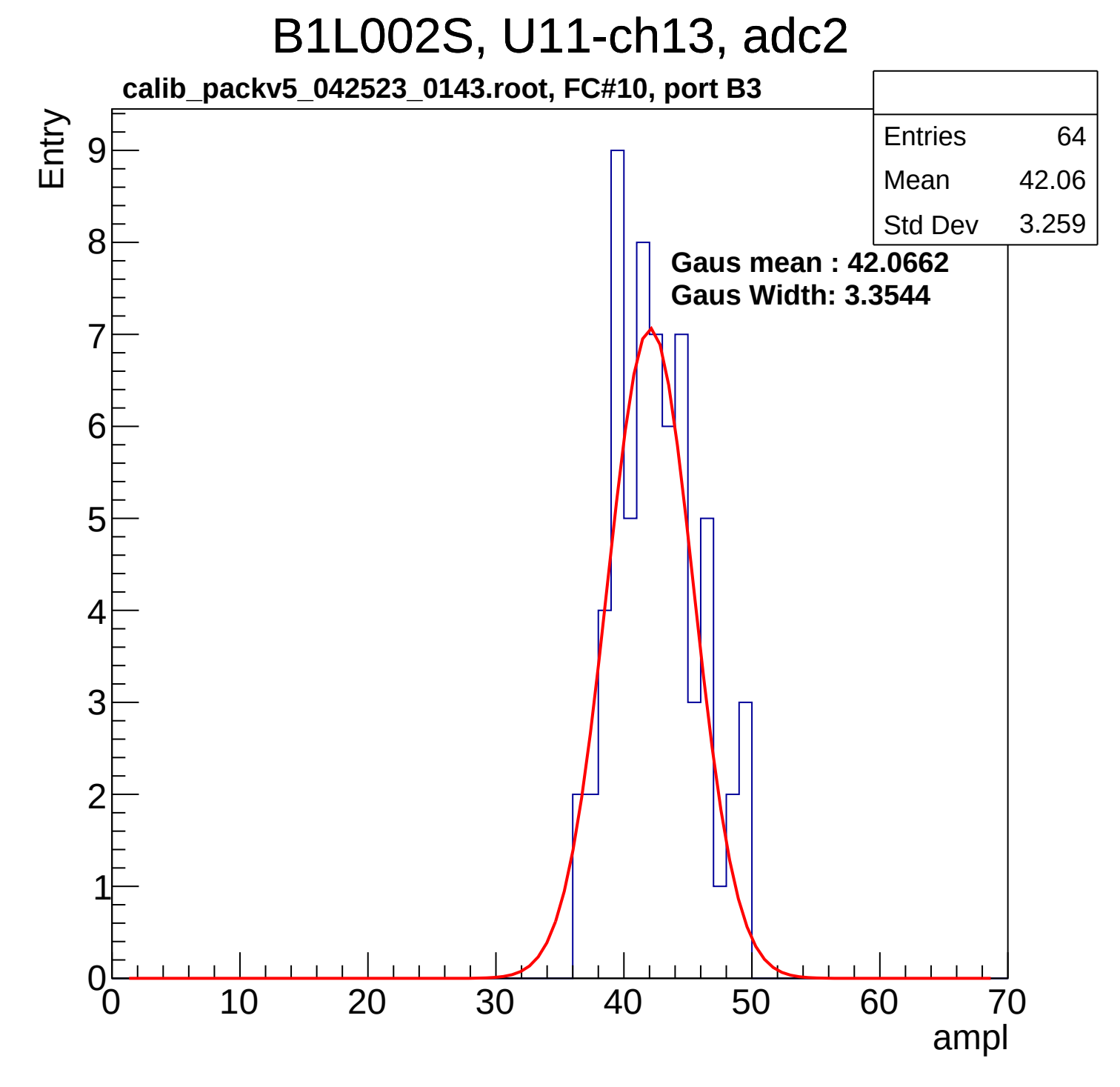
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.43
Std Dev 1.591
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 29.06
Std Dev 3.509
Gaus mean : 29.9579
Gaus Width: 3.2922
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.42
Std Dev 3.077
Gaus mean : 35.3497
Gaus Width: 2.9371
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.06
Std Dev 3.259
Gaus mean : 42.0662
Gaus Width: 3.3544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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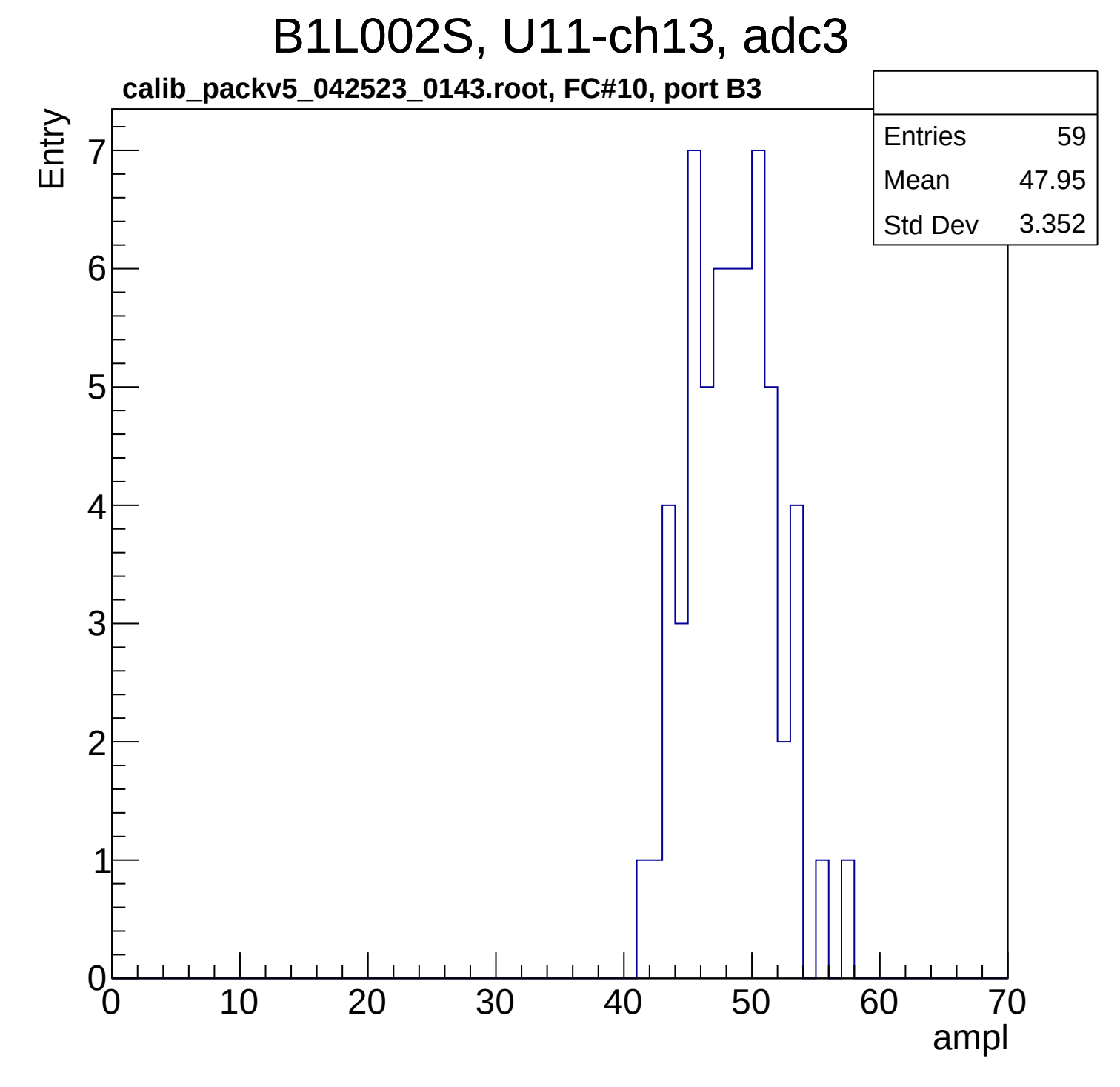
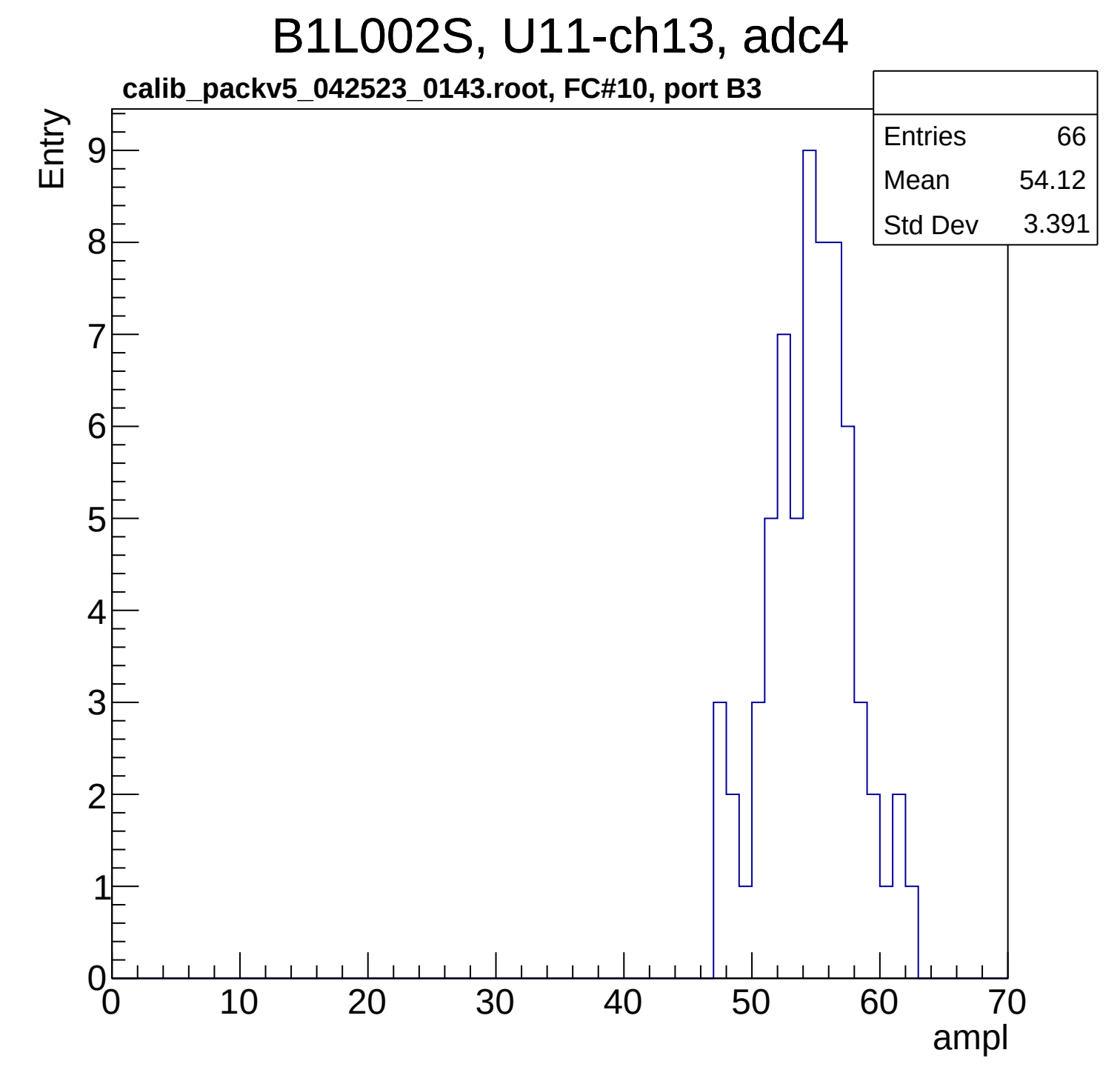

B1L002S, U11-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.95
Std Dev 3.352
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.12
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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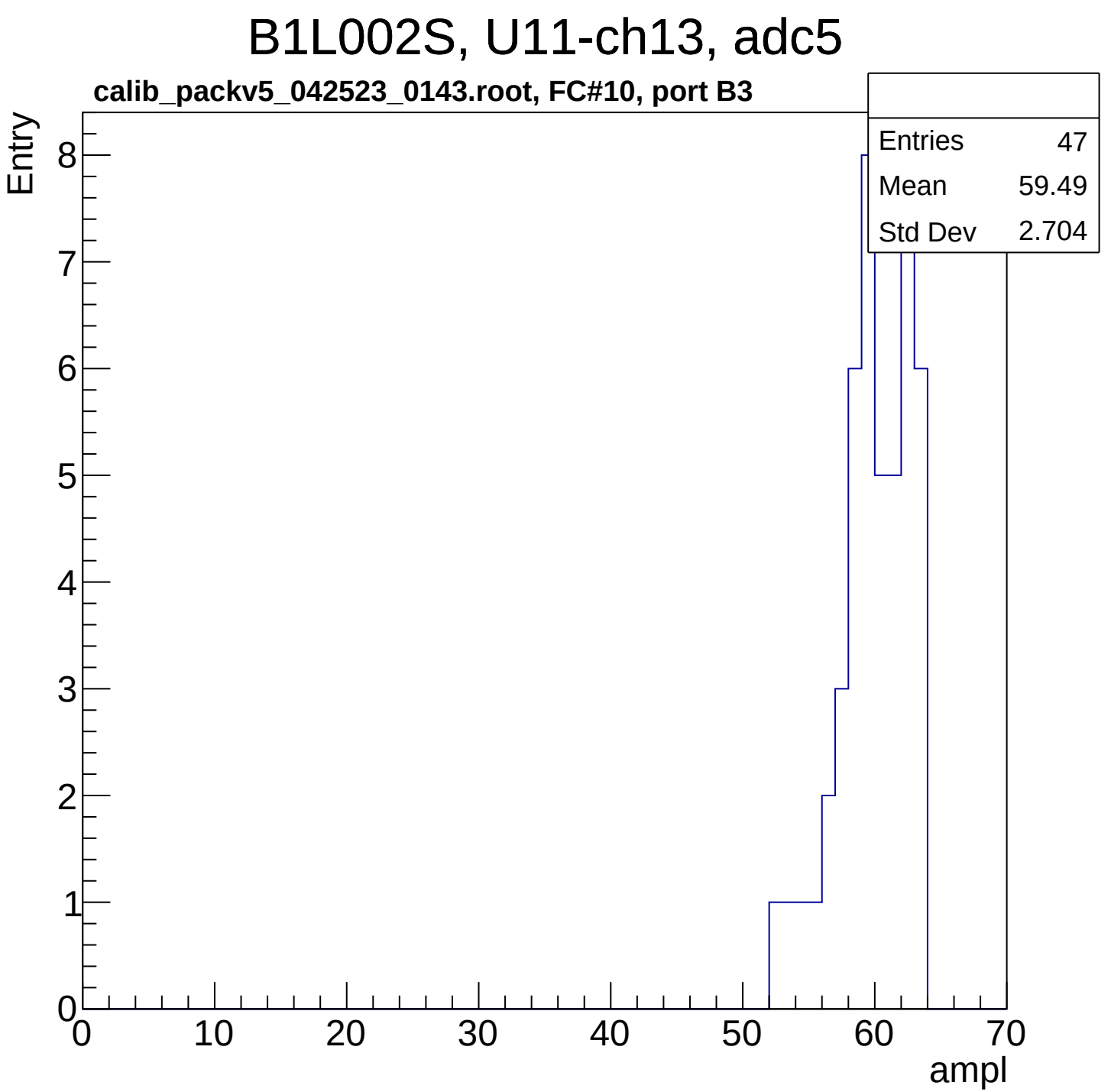
B1L002S, U11-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 59.49
Std Dev 2.704
Entry
ampl
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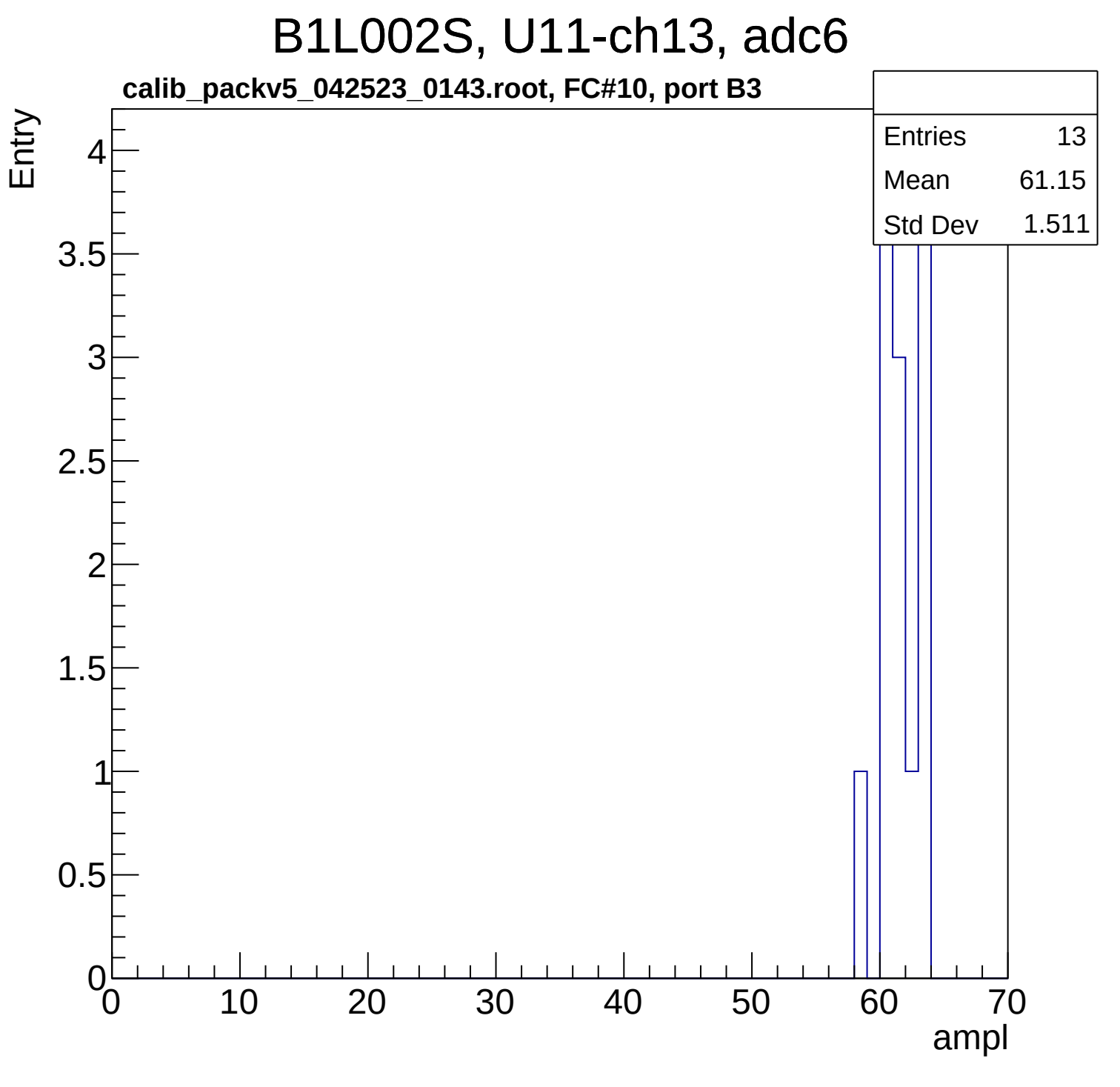
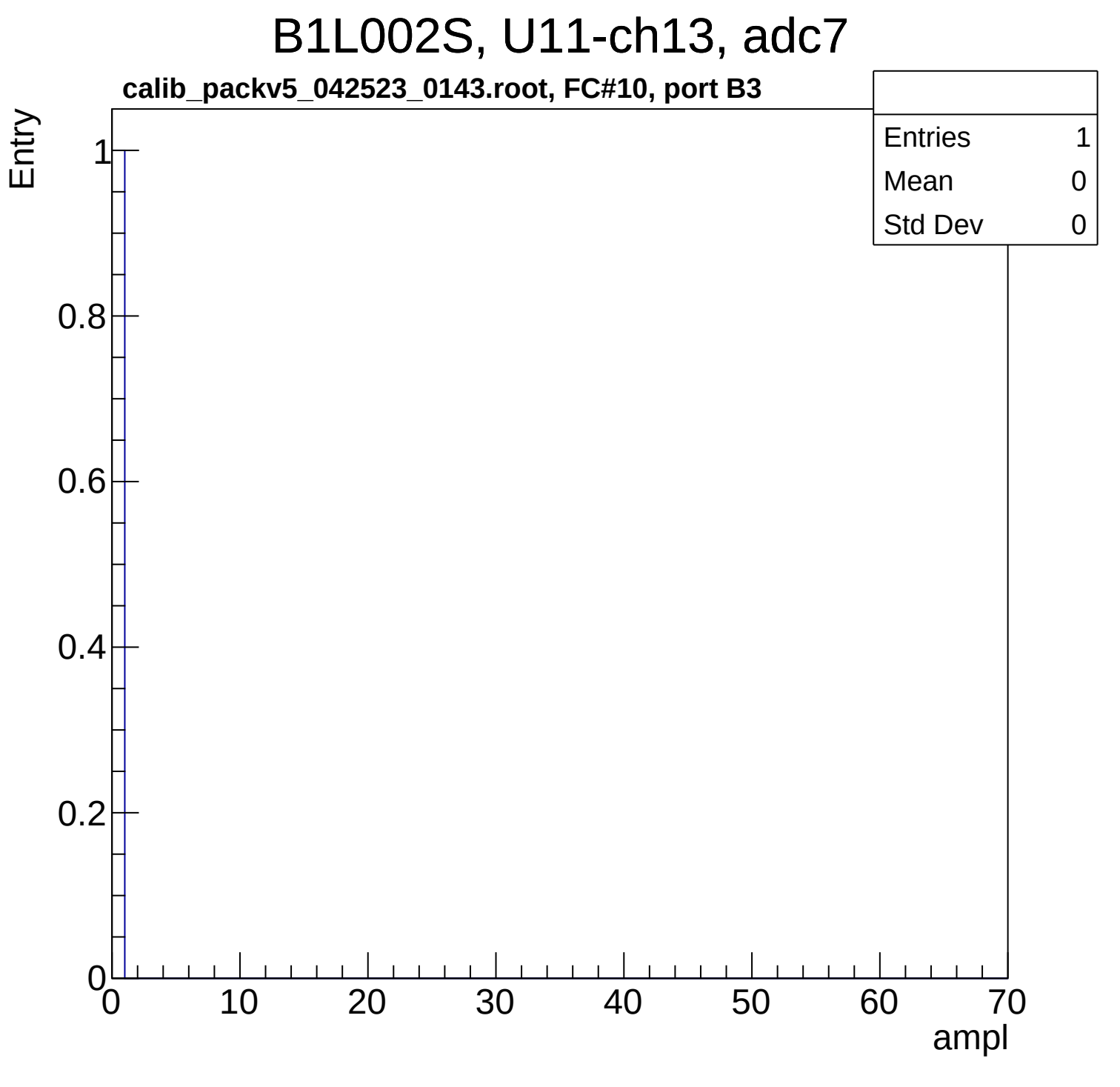
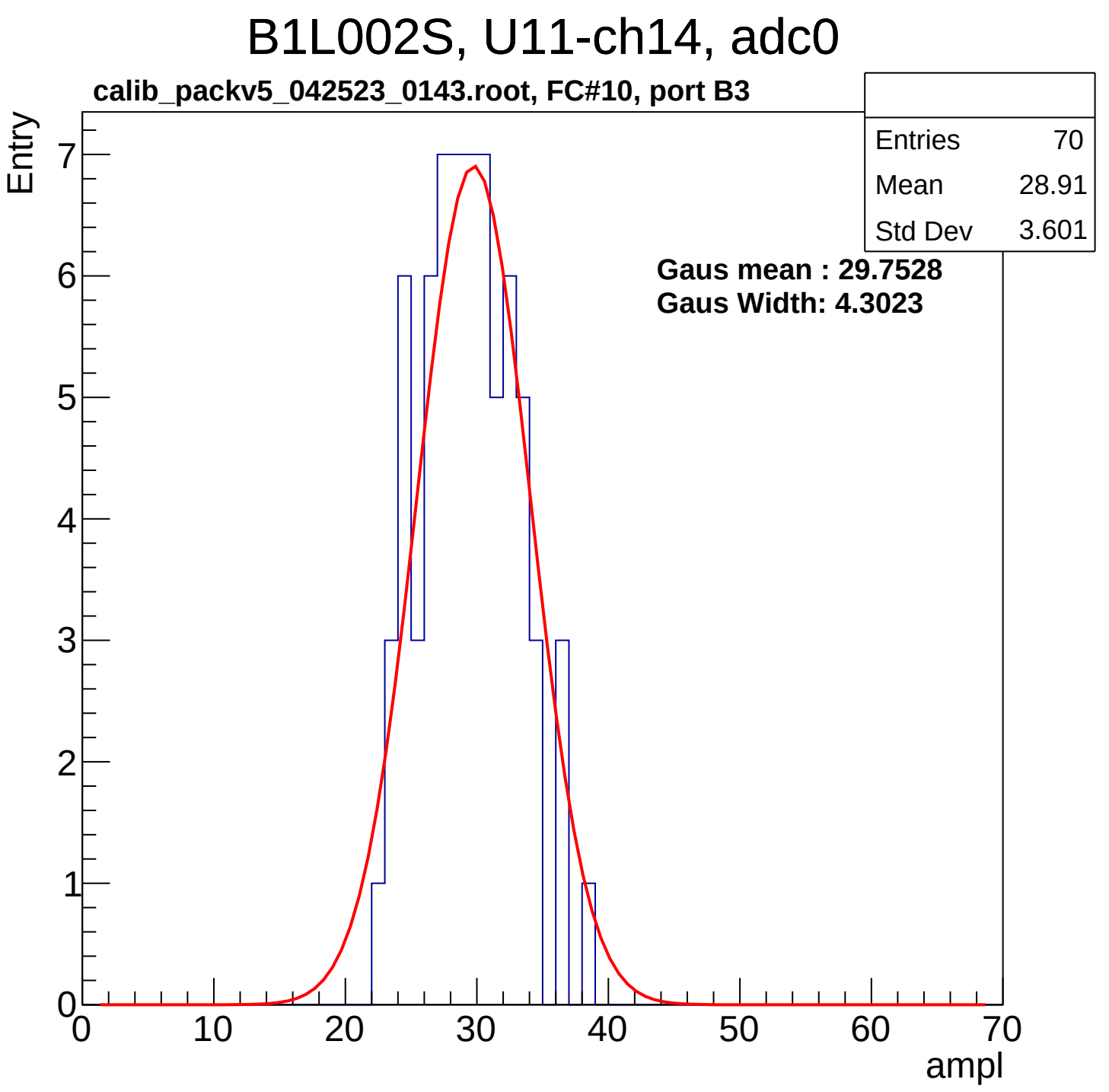
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.15
Std Dev 1.511
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.91
Std Dev 3.601
Gaus mean : 29.7528
Gaus Width: 4.3023
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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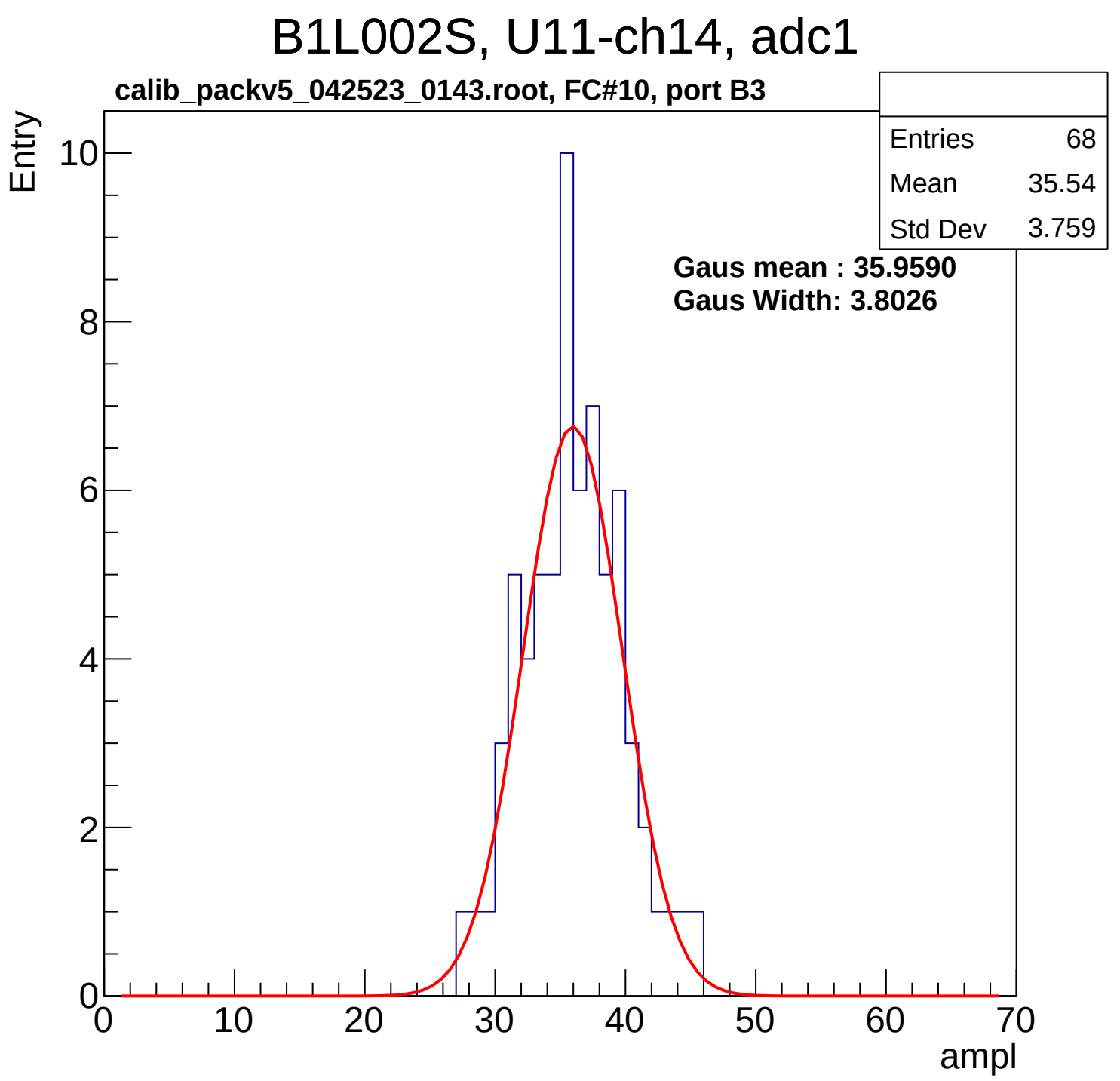
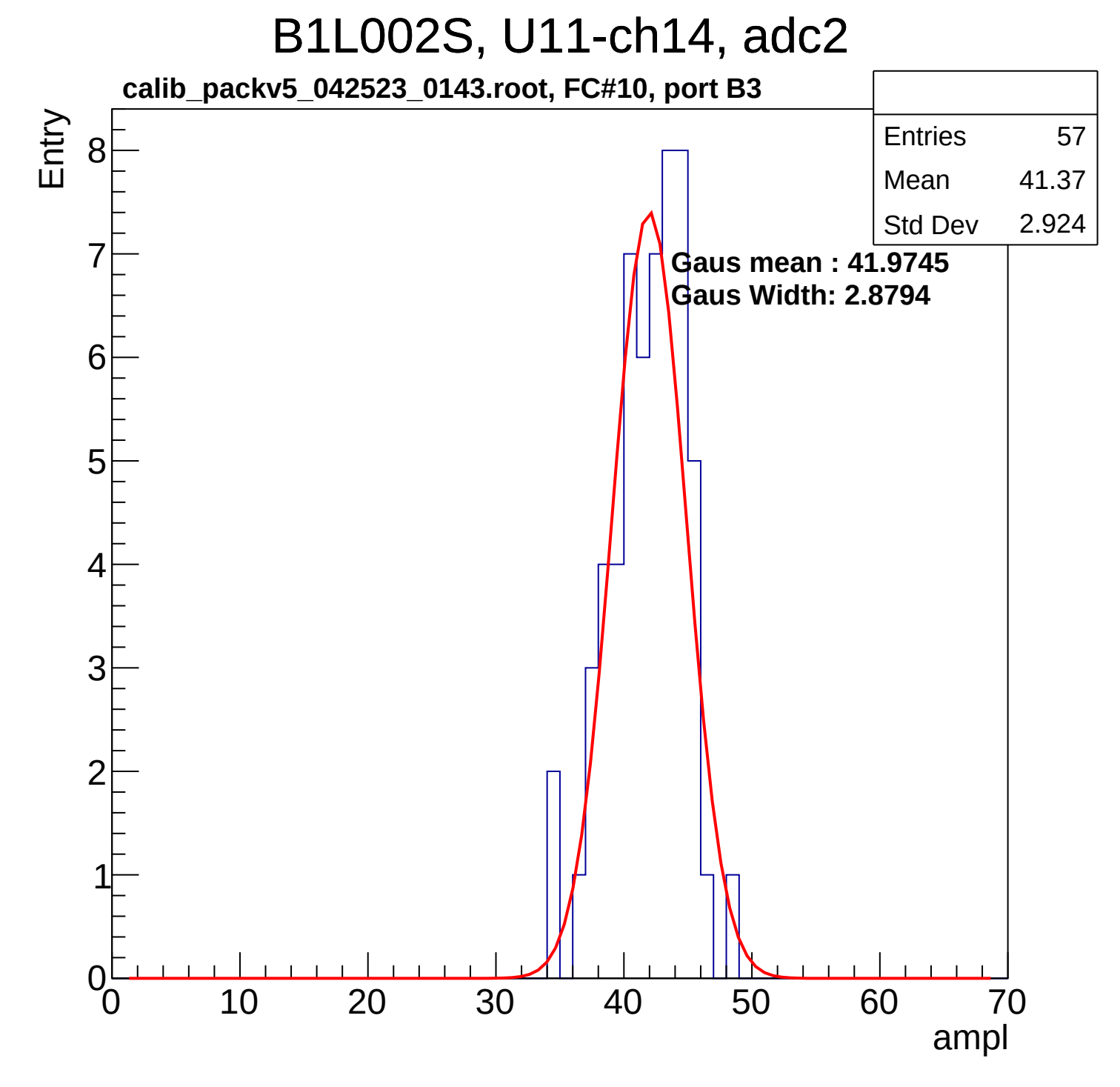

B1L002S, U11-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.54
Std Dev 3.759
Gaus mean : 35.9590
Gaus Width: 3.8026
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.37
Std Dev 2.924
Gaus mean : 41.9745
Gaus Width: 2.8794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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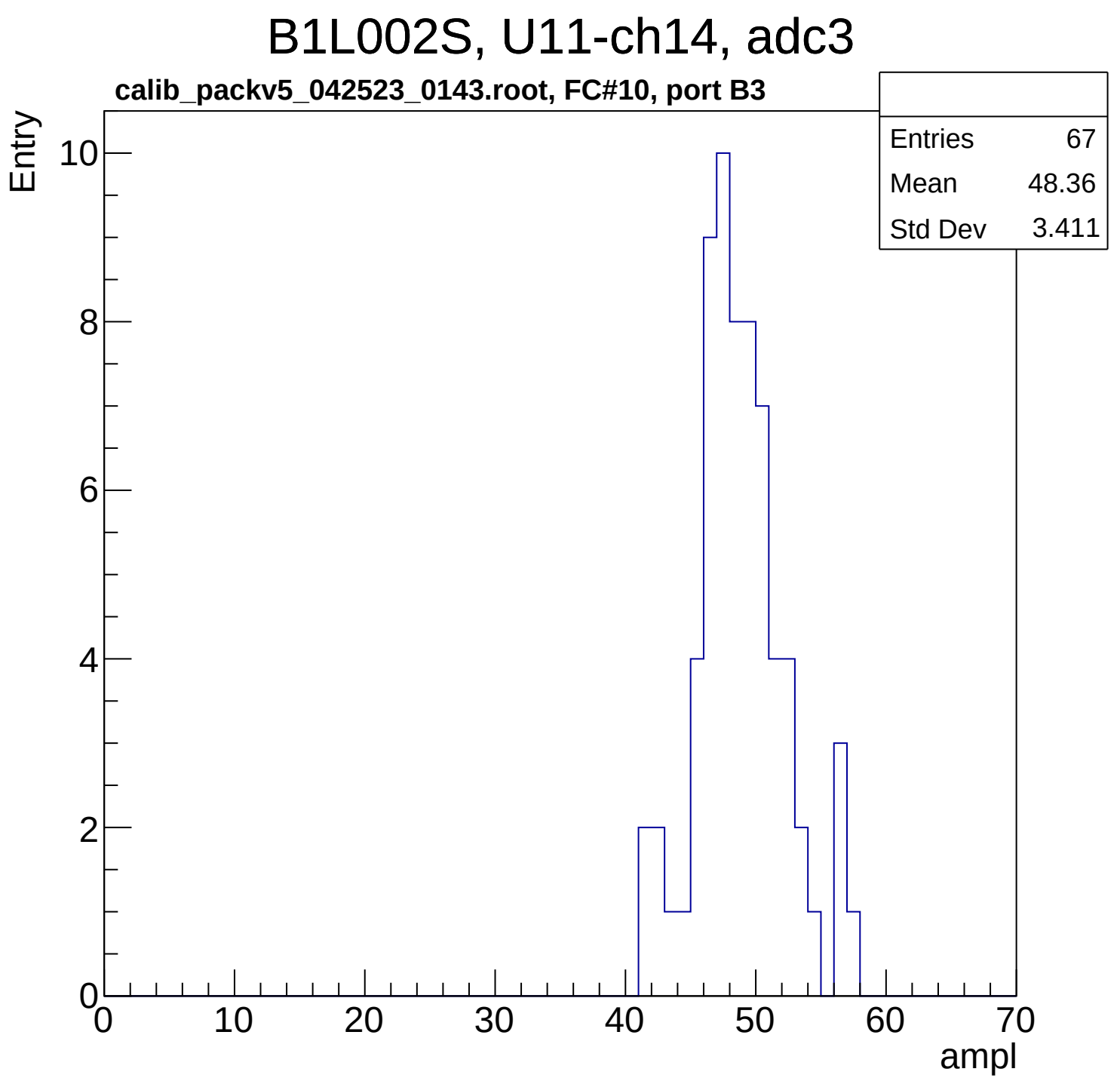
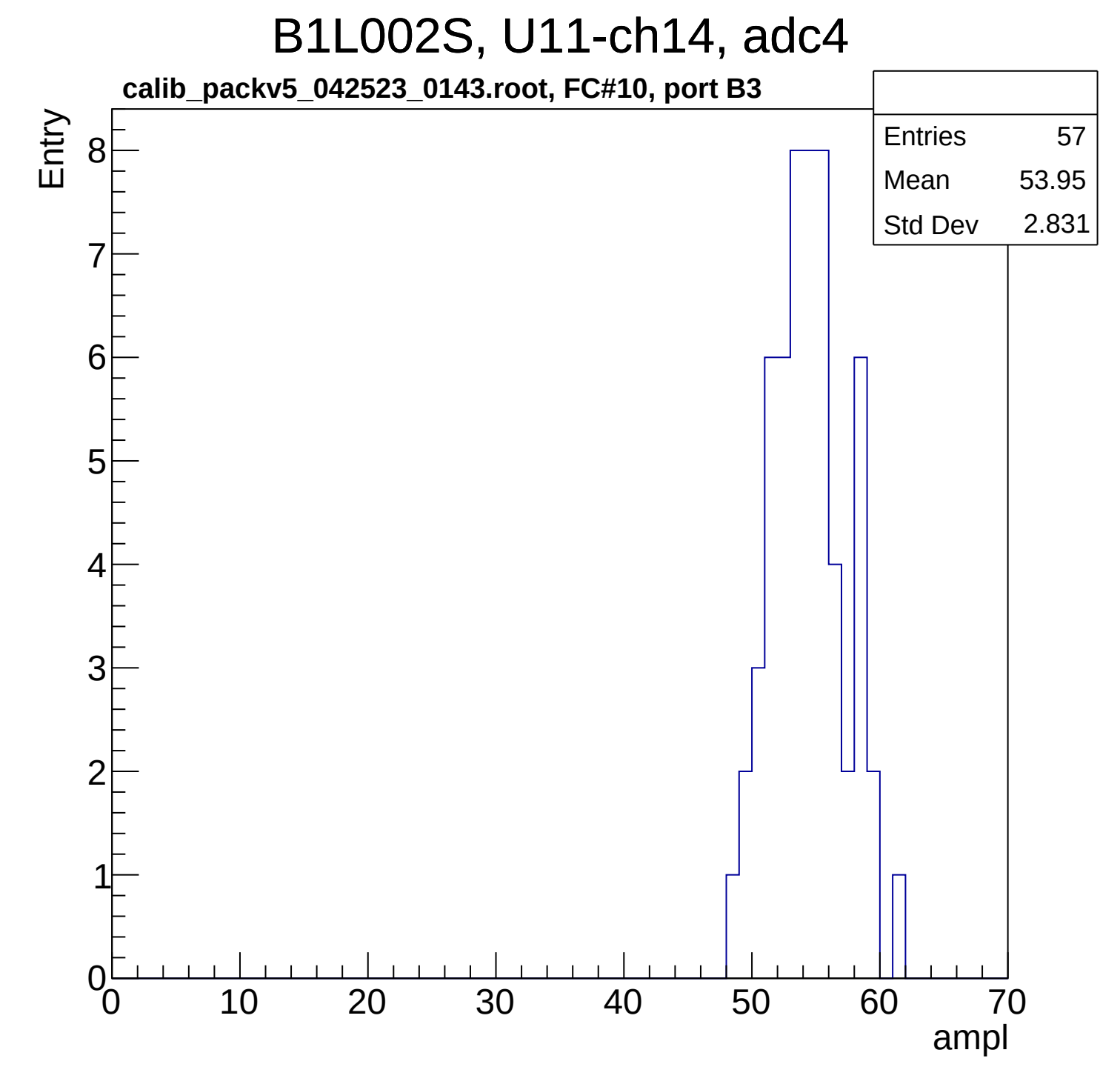

B1L002S, U11-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.36
Std Dev 3.411
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.95
Std Dev 2.831
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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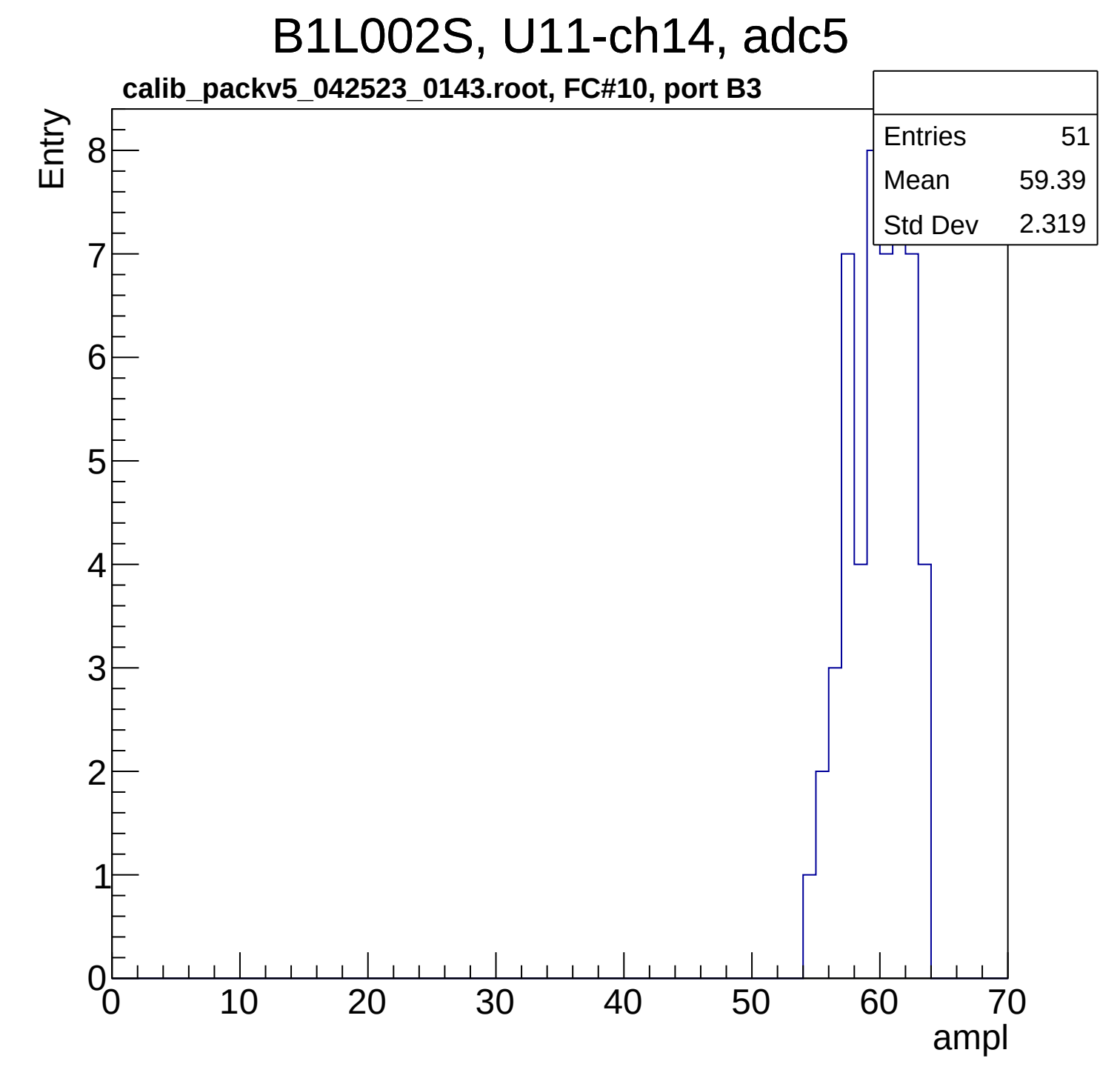
B1L002S, U11-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.39
Std Dev 2.319
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
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60
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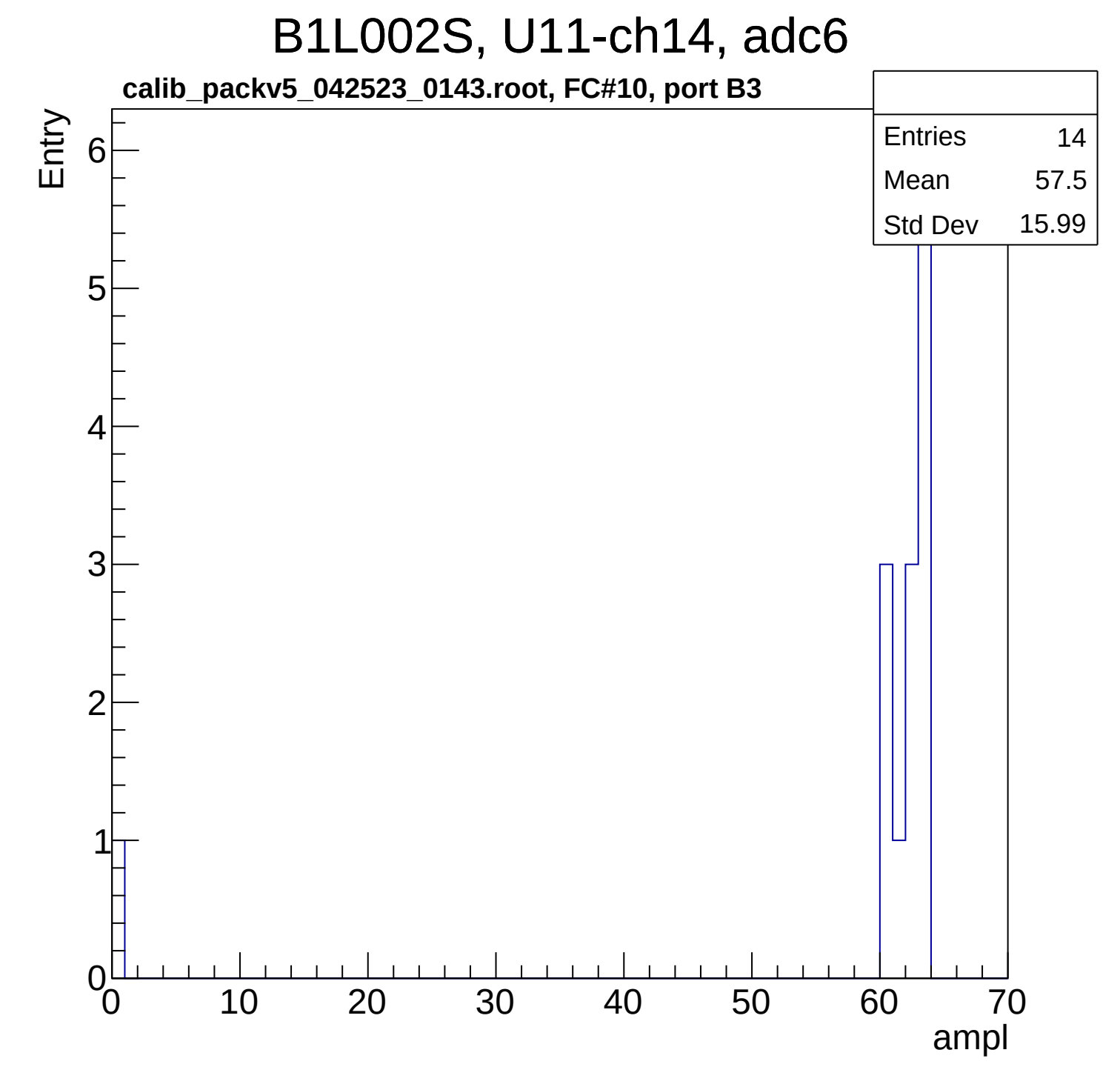
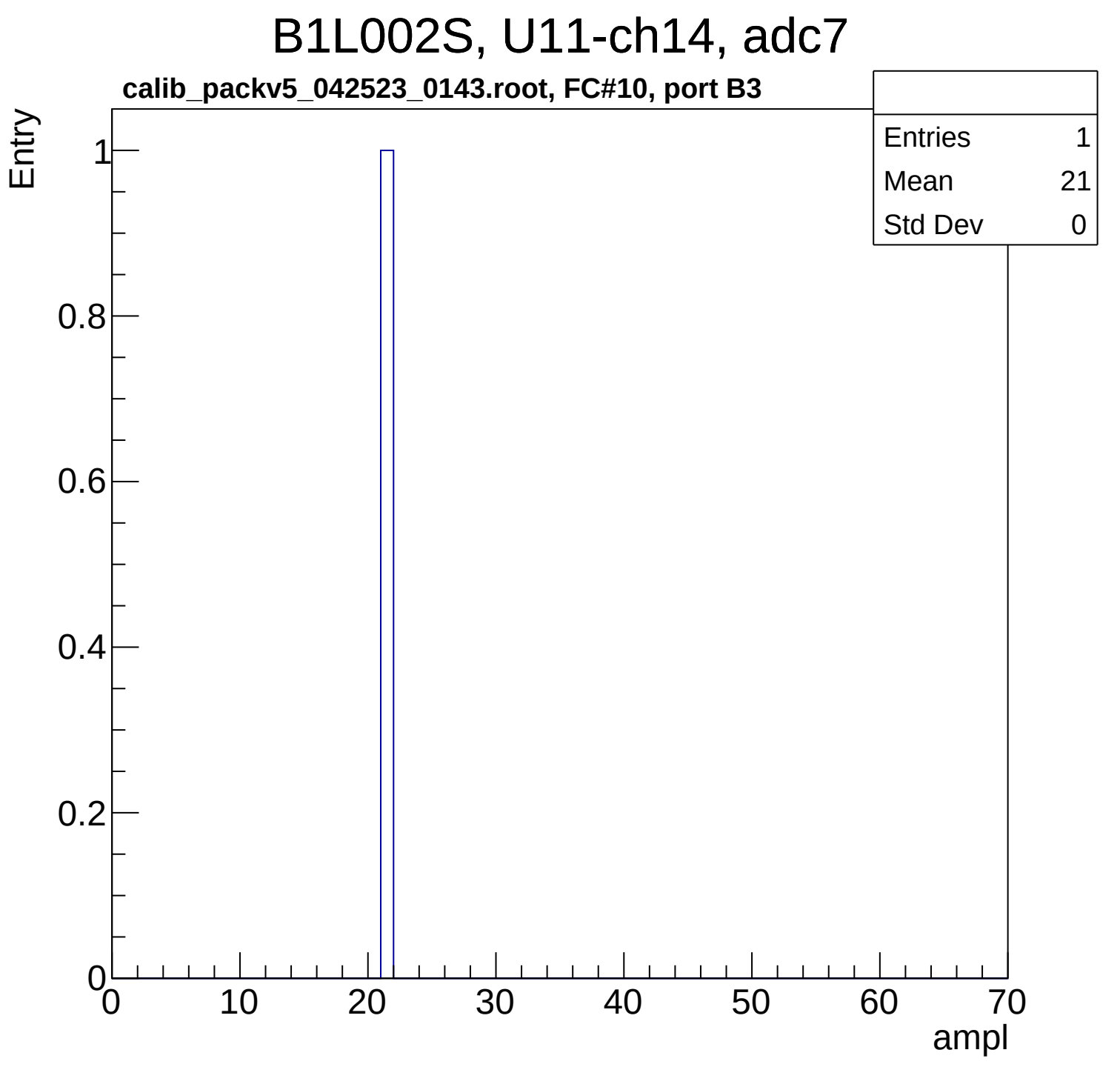
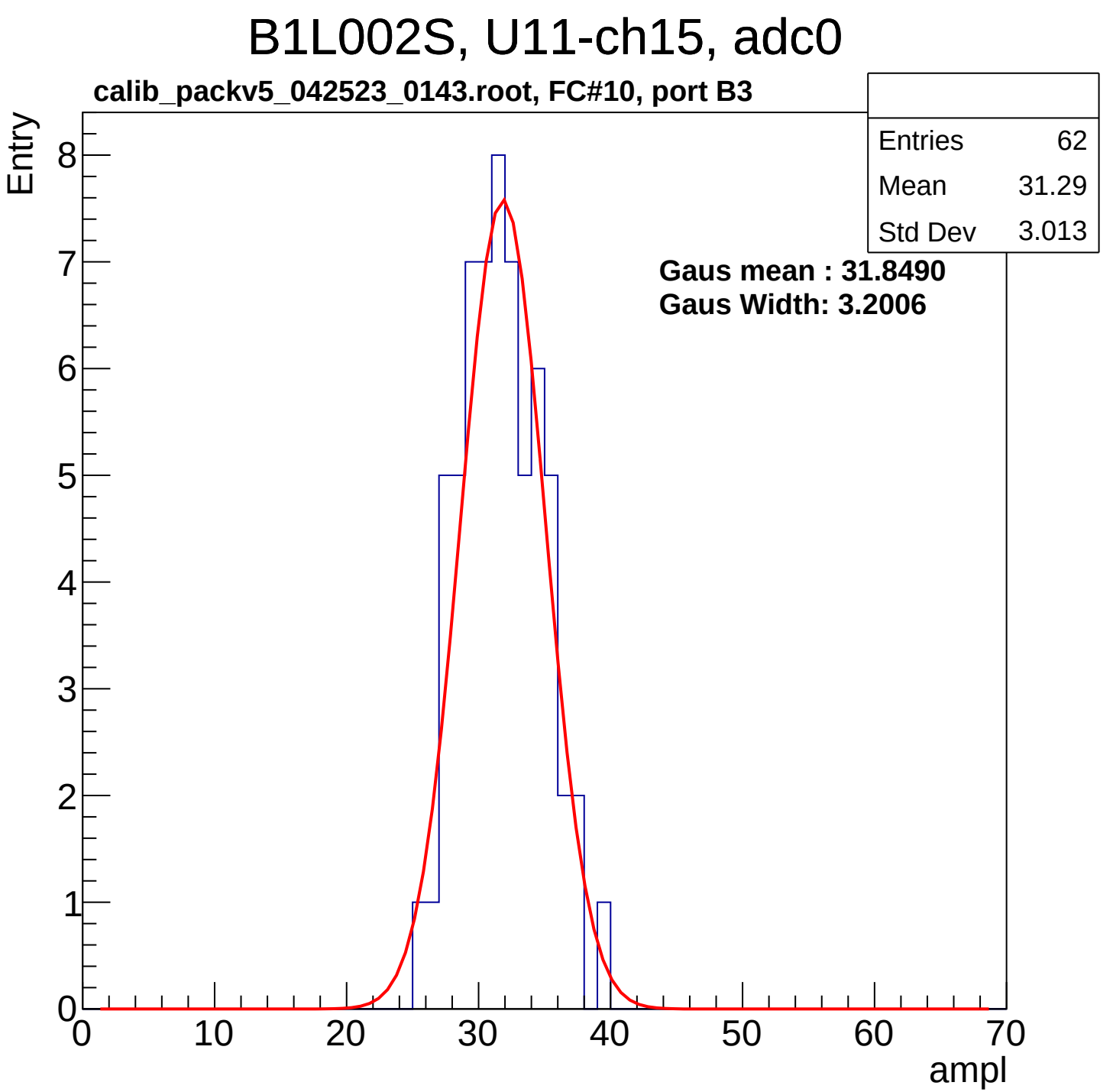
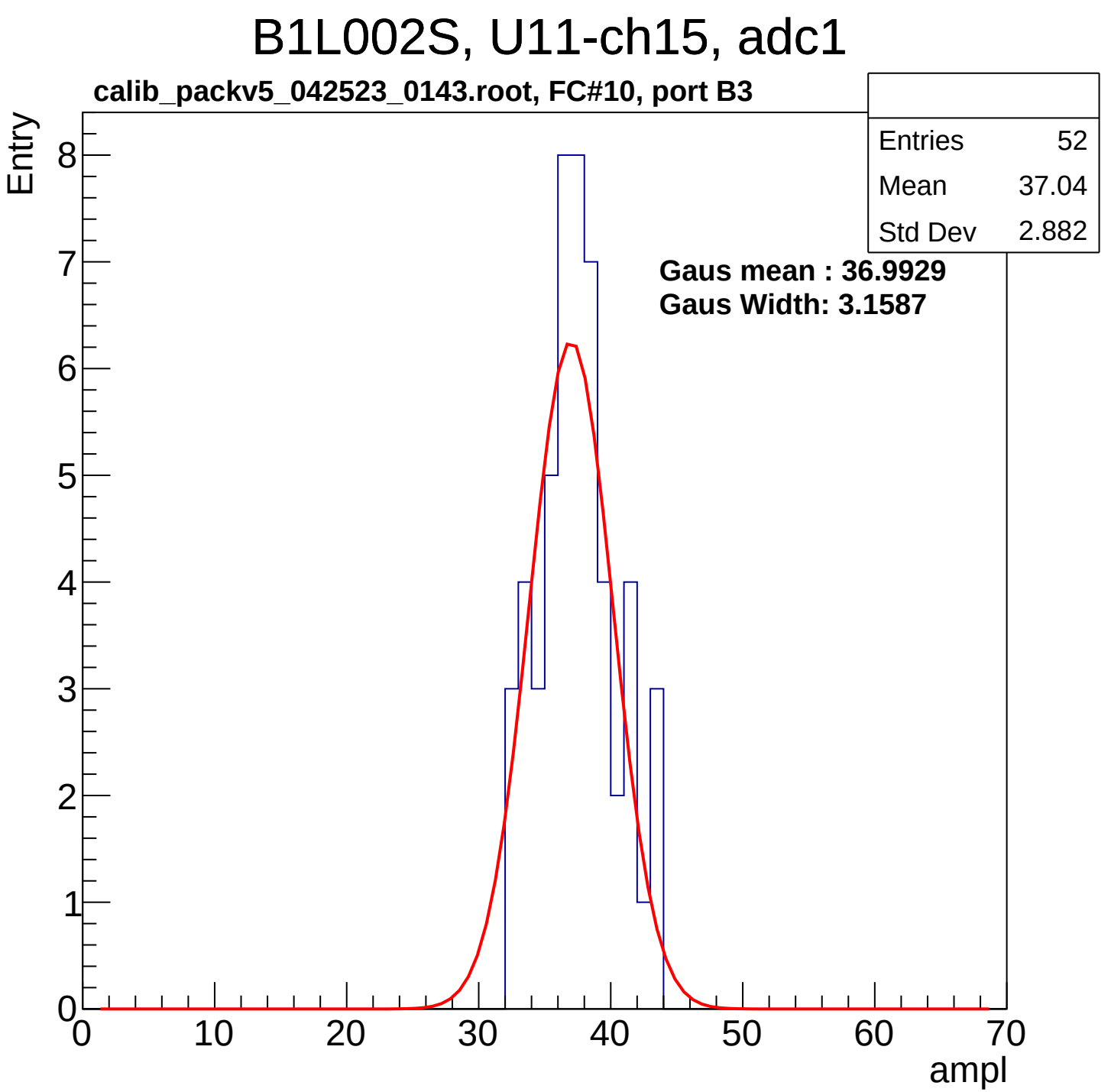

B1L002S, U11-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.5
Std Dev 15.99
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 31.29
Std Dev 3.013
Gaus mean : 31.8490
Gaus Width: 3.2006
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 37.04
Std Dev 2.882
Gaus mean : 36.9929
Gaus Width: 3.1587
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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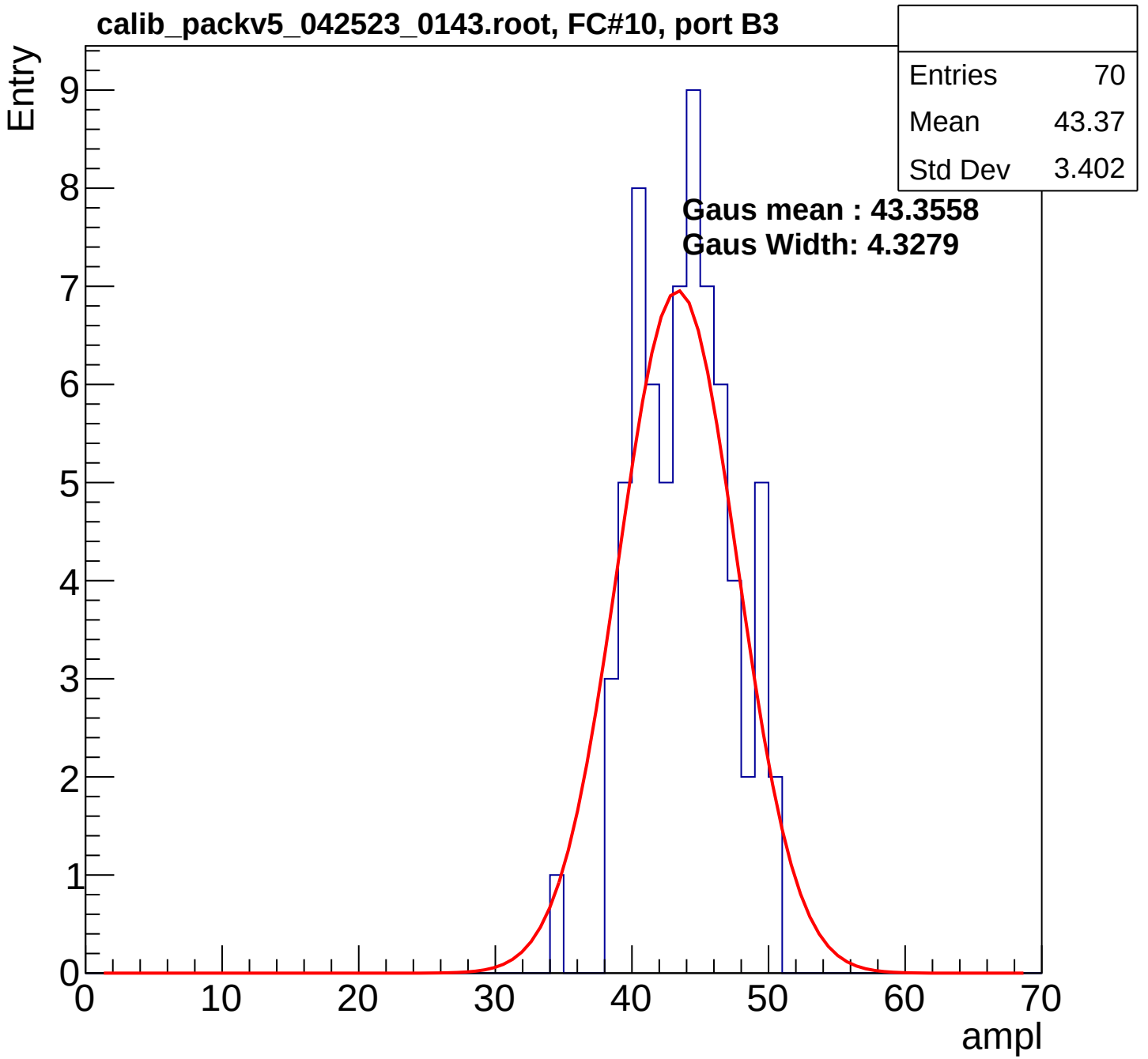
B1L002S, U11-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 43.37
Std Dev 3.402
Gaus mean : 43.3558
Gaus Width: 4.3279
Entry
ampl
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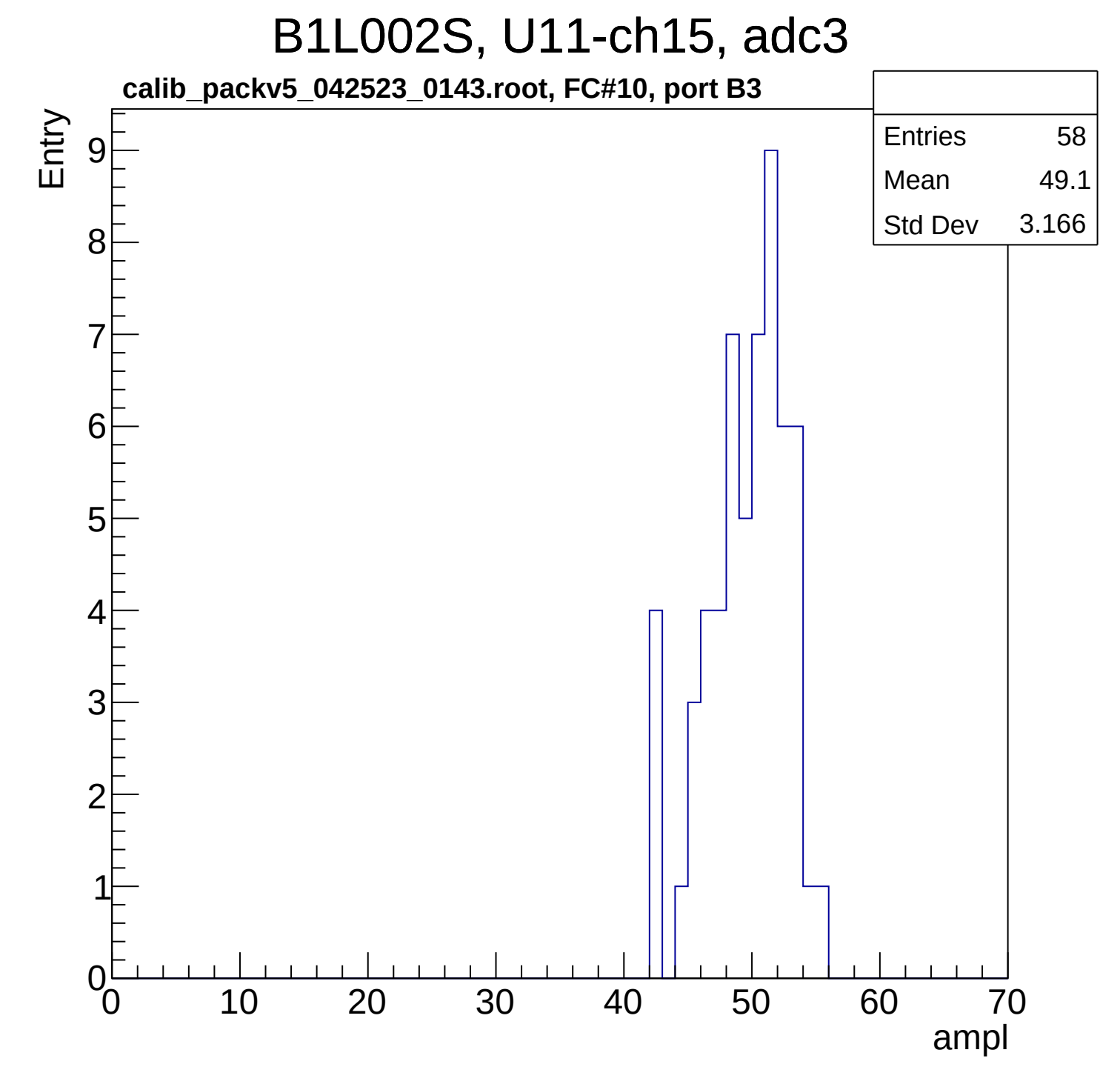
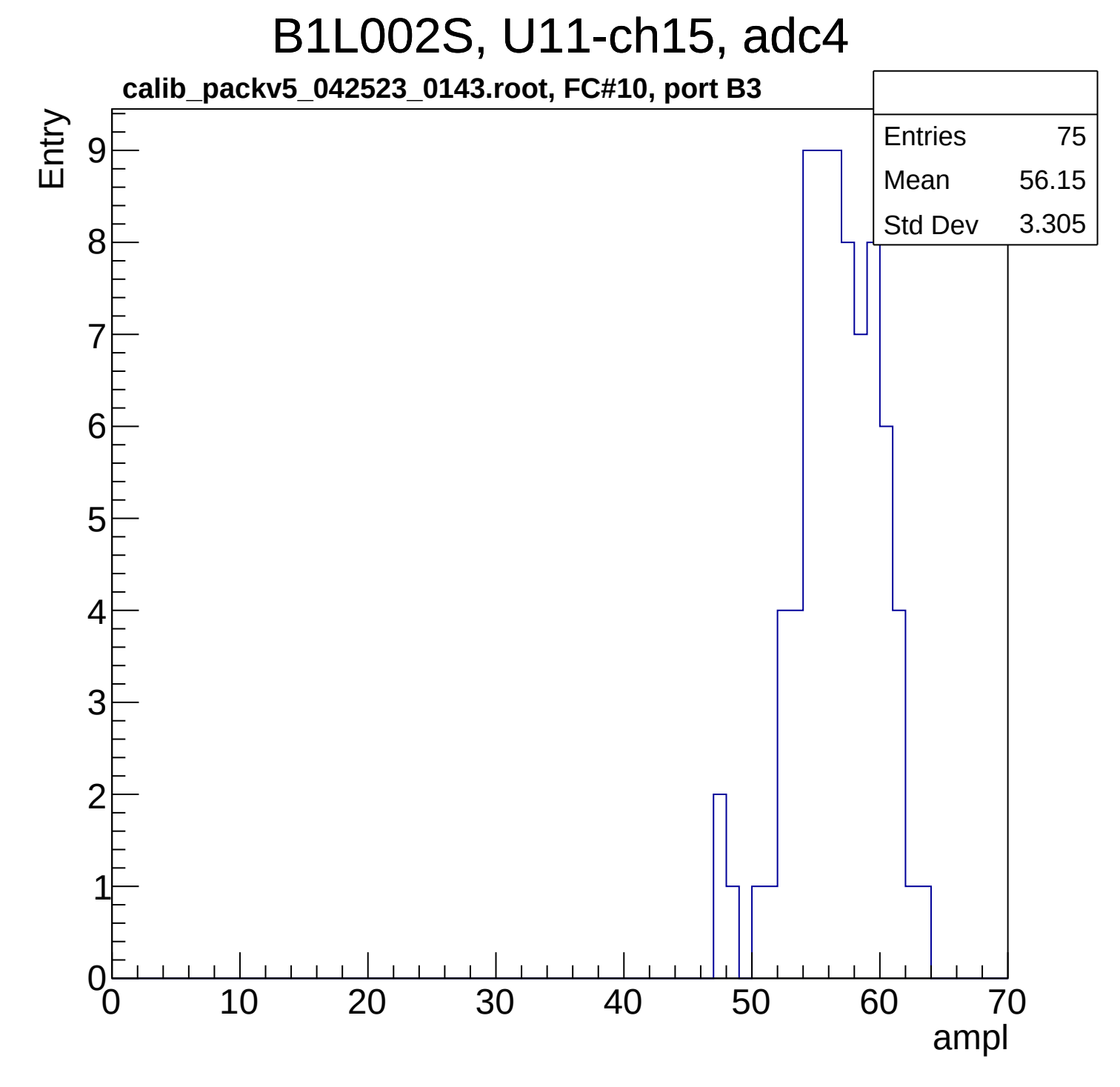
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 49.1
Std Dev 3.166
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 56.15
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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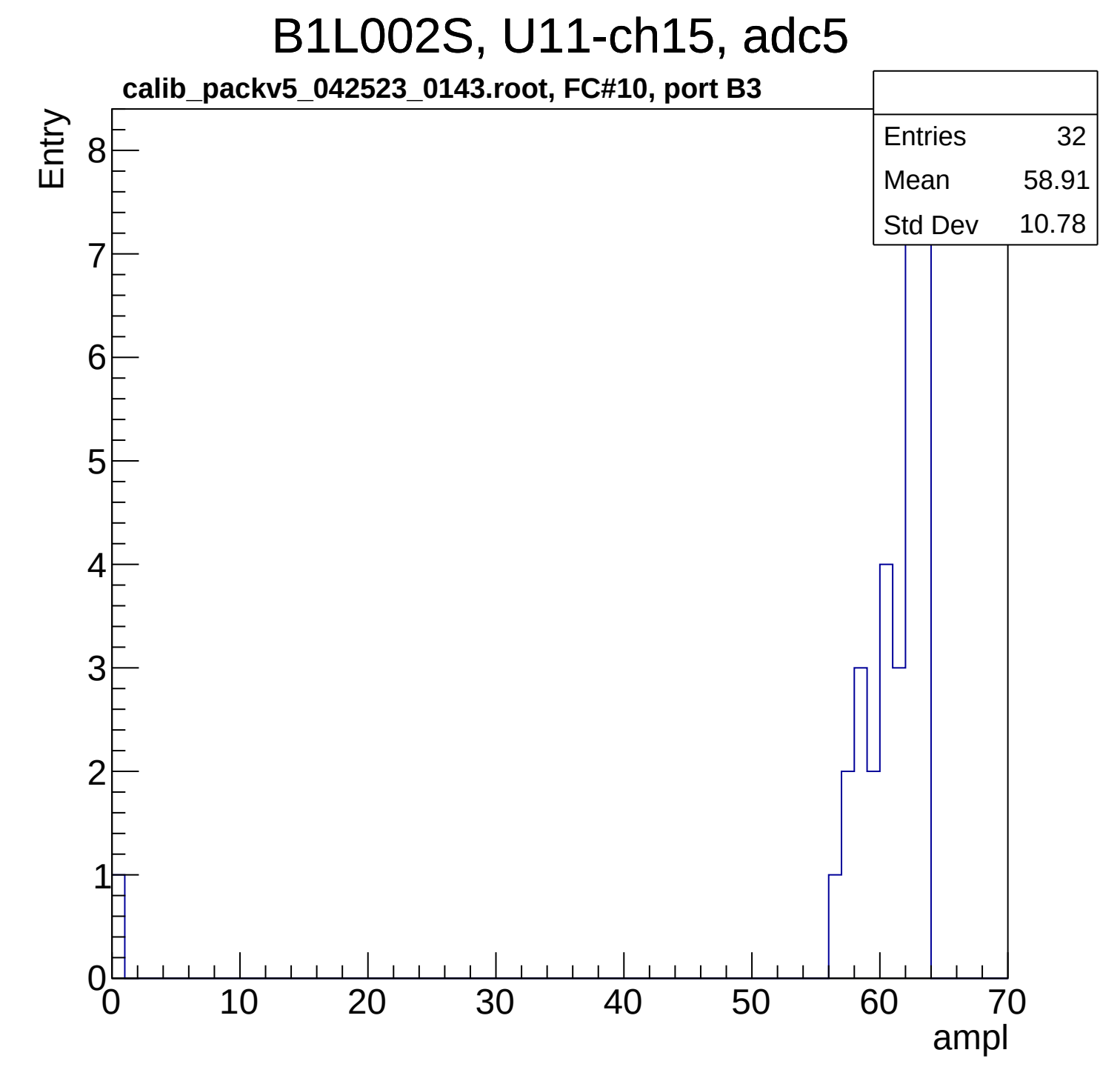
B1L002S, U11-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 58.91
Std Dev 10.78
Entry
ampl
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1
2
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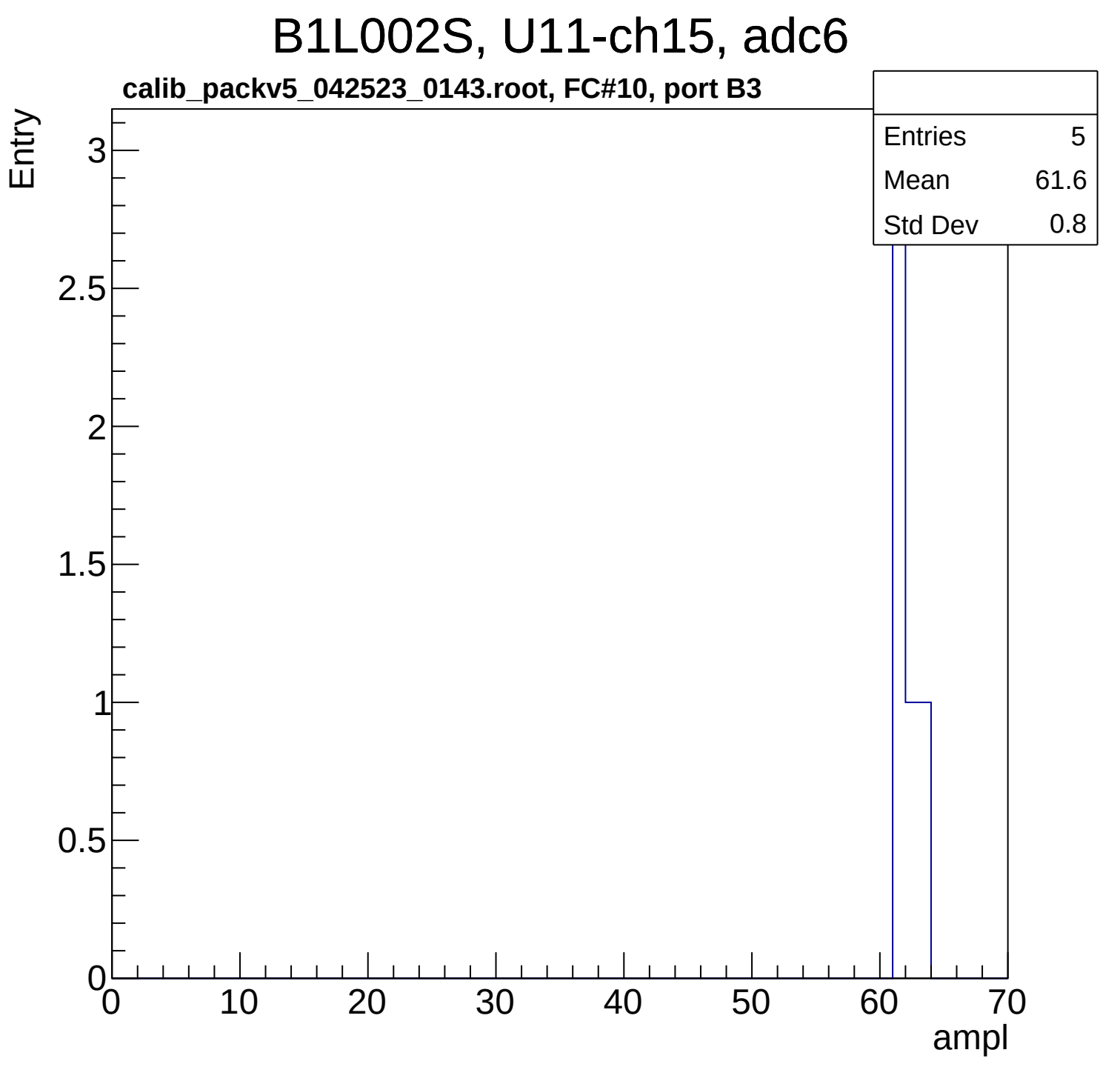
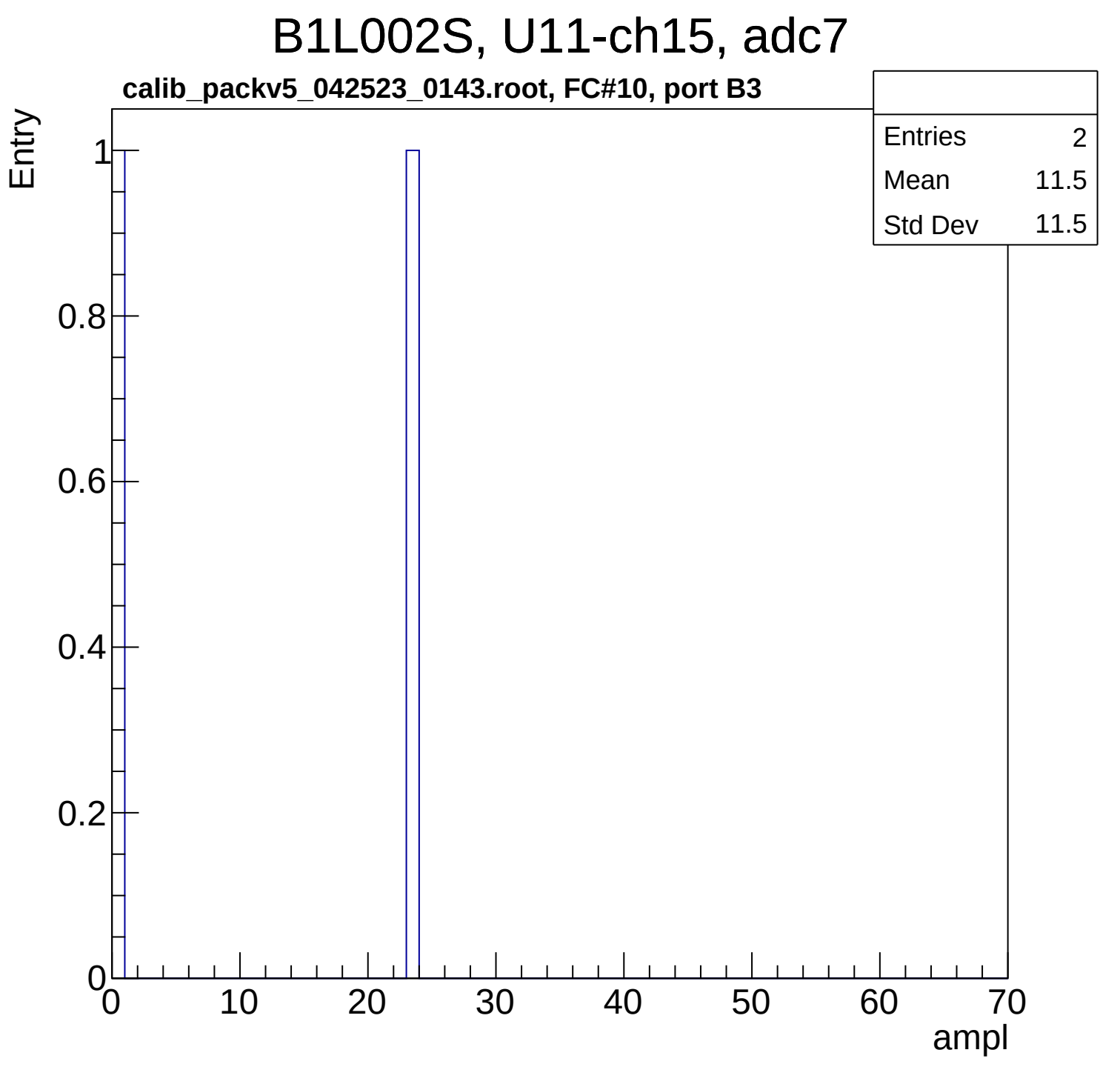

B1L002S, U11-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 61.6
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
0.2
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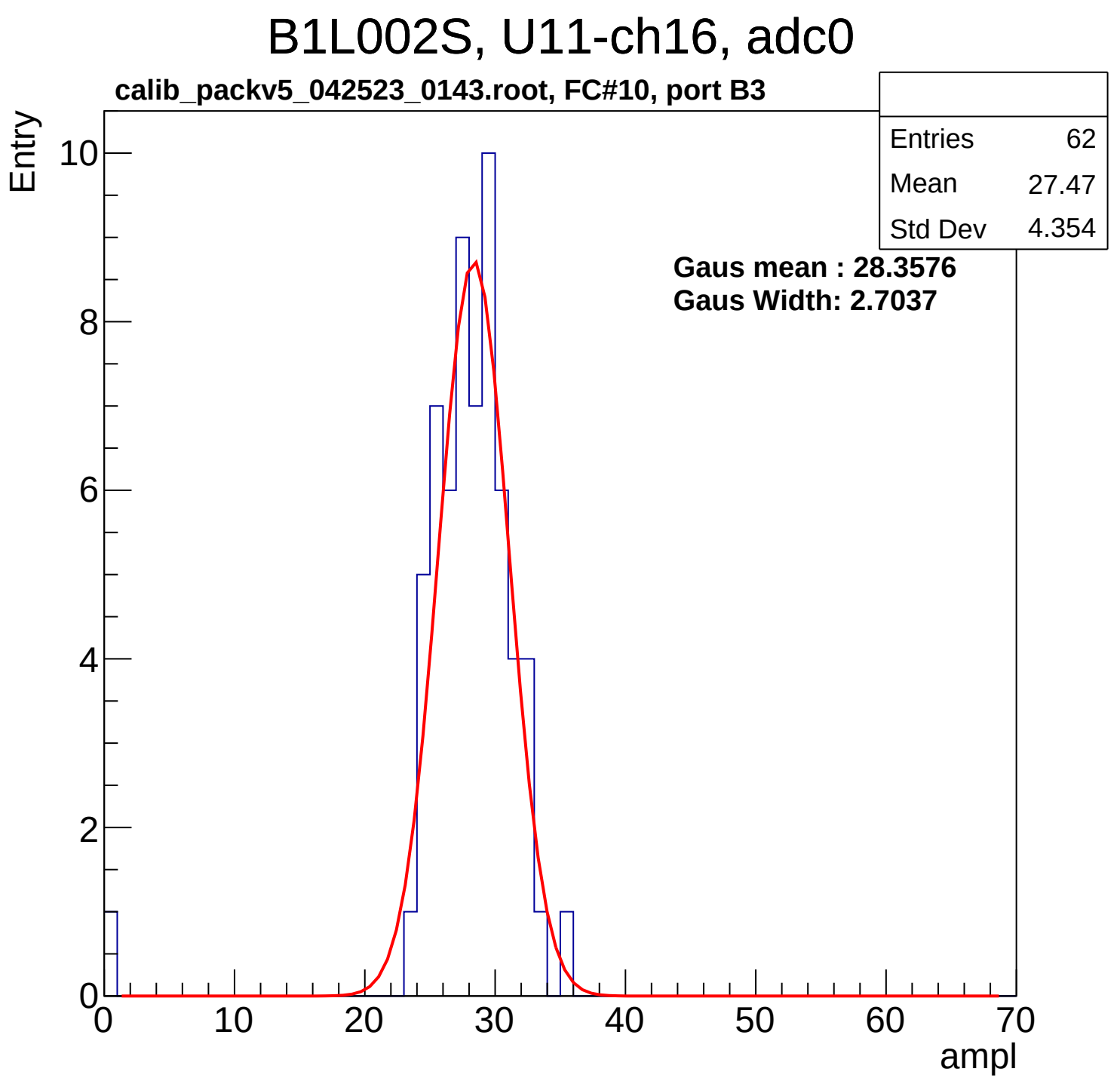
50
60
70
ampl


B1L002S, U11-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.47
Std Dev 4.354
Gaus mean : 28.3576
Gaus Width: 2.7037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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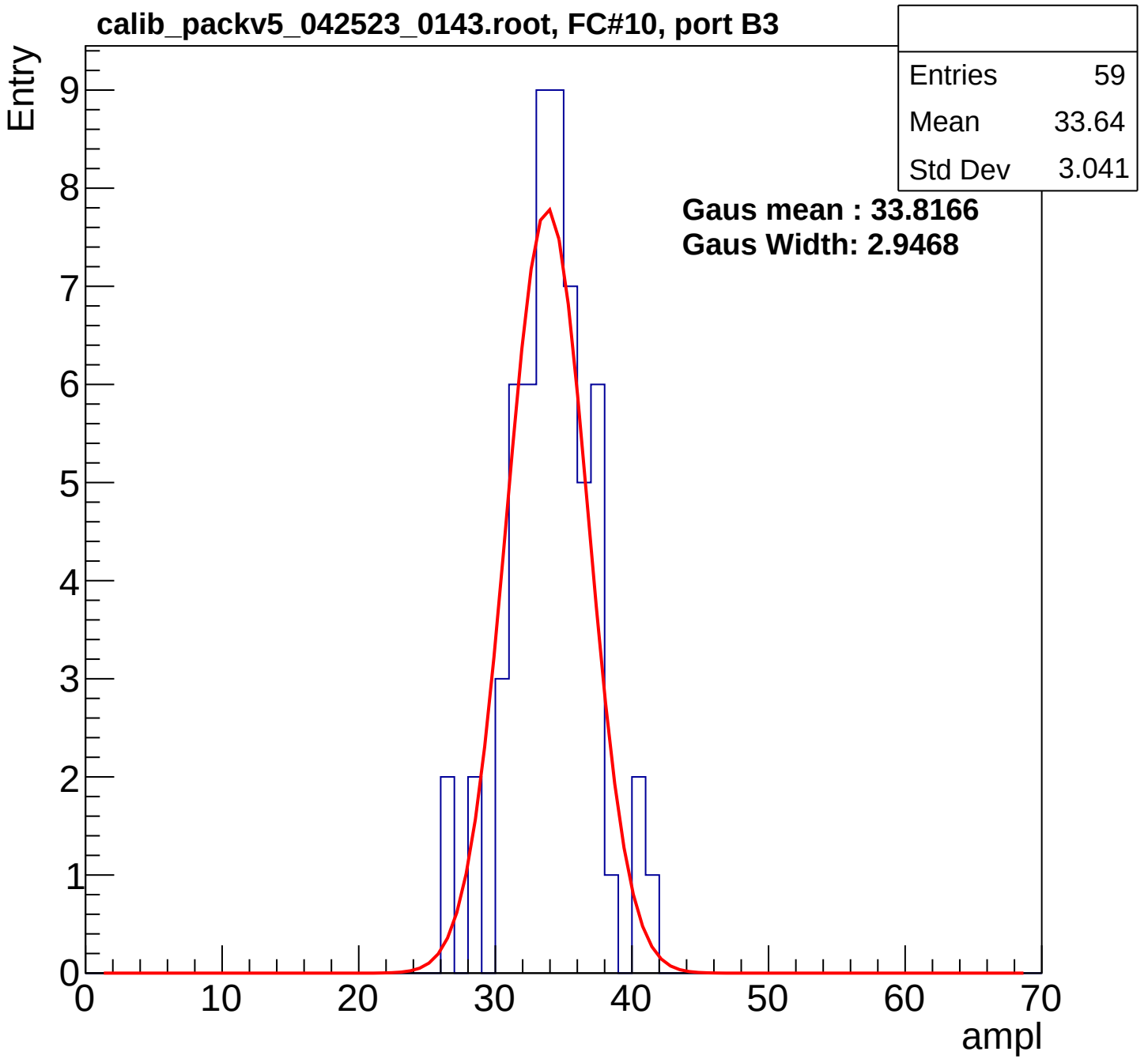
B1L002S, U11-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 33.64
Std Dev 3.041
Gaus mean : 33.8166
Gaus Width: 2.9468
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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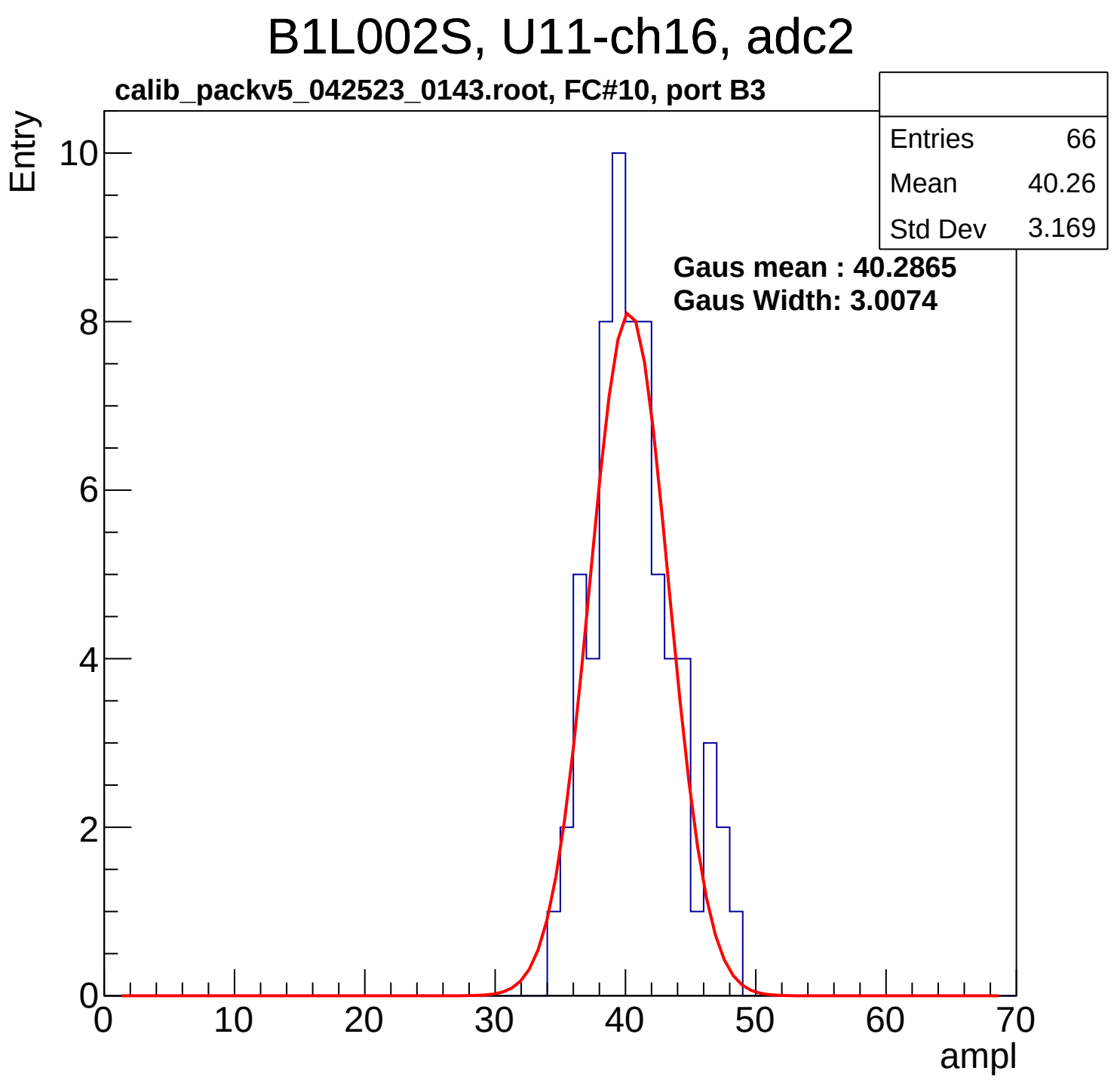
B1L002S, U11-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.26
Std Dev 3.169
Gaus mean : 40.2865
Gaus Width: 3.0074
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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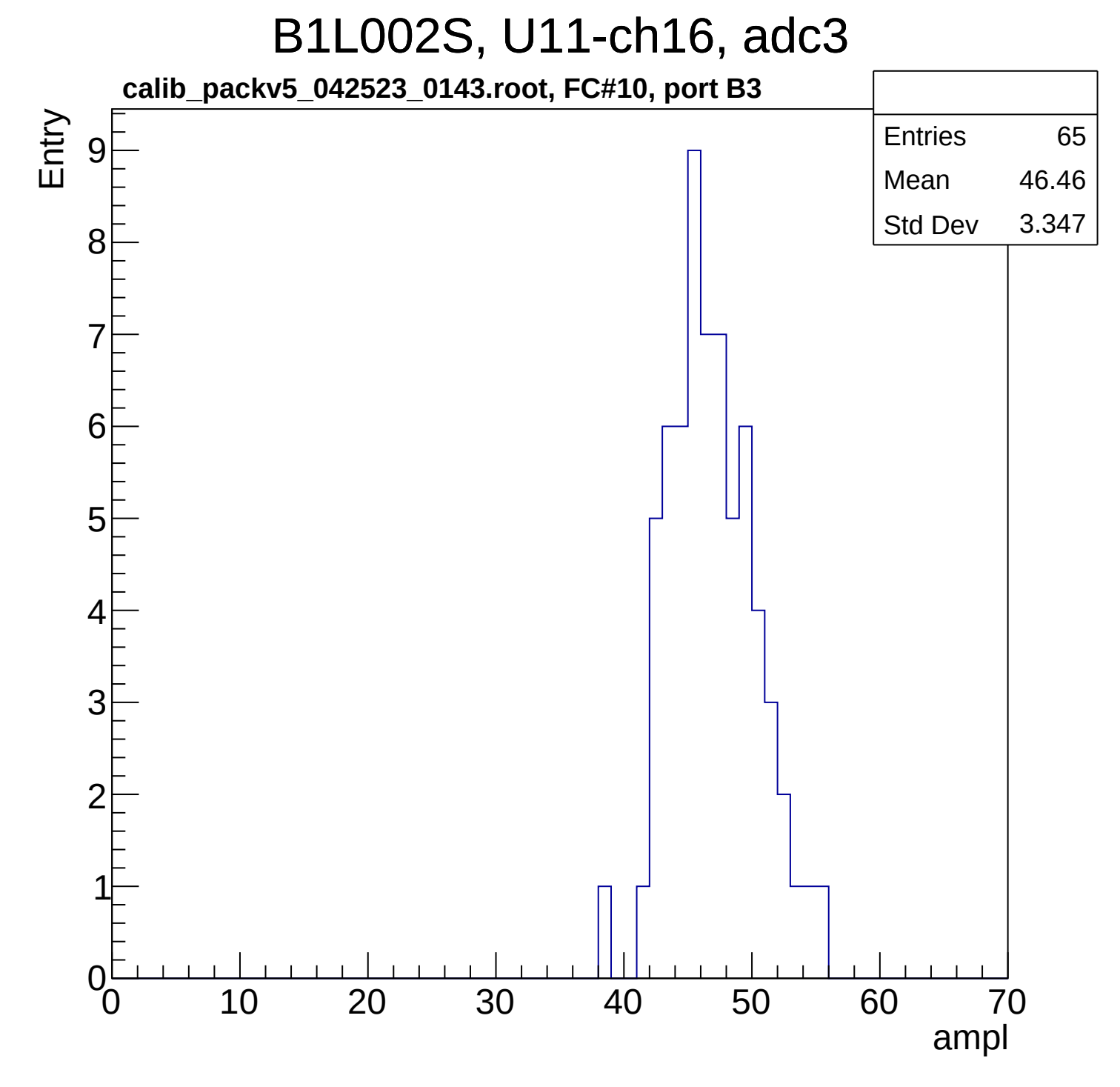
B1L002S, U11-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.46
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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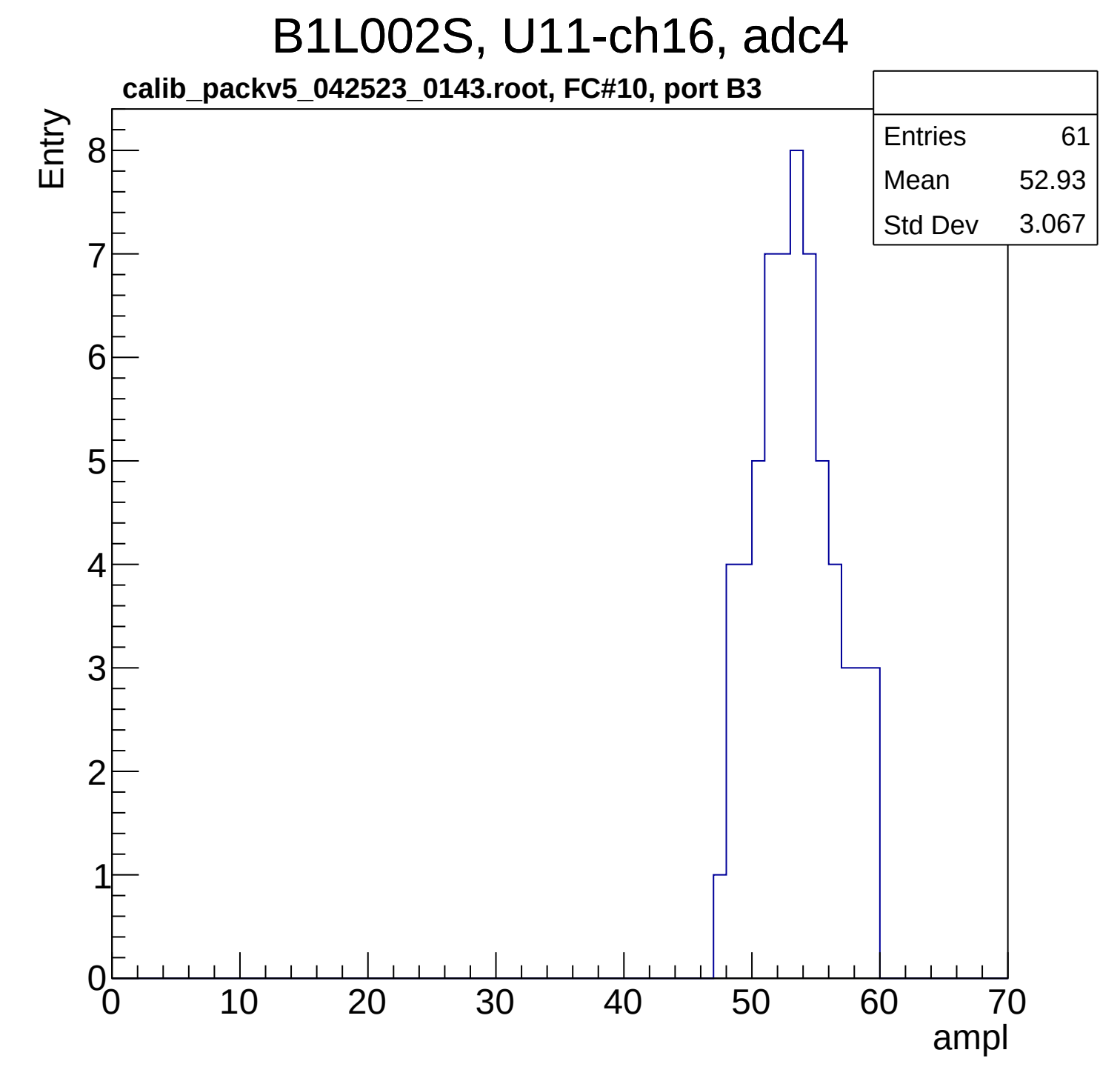
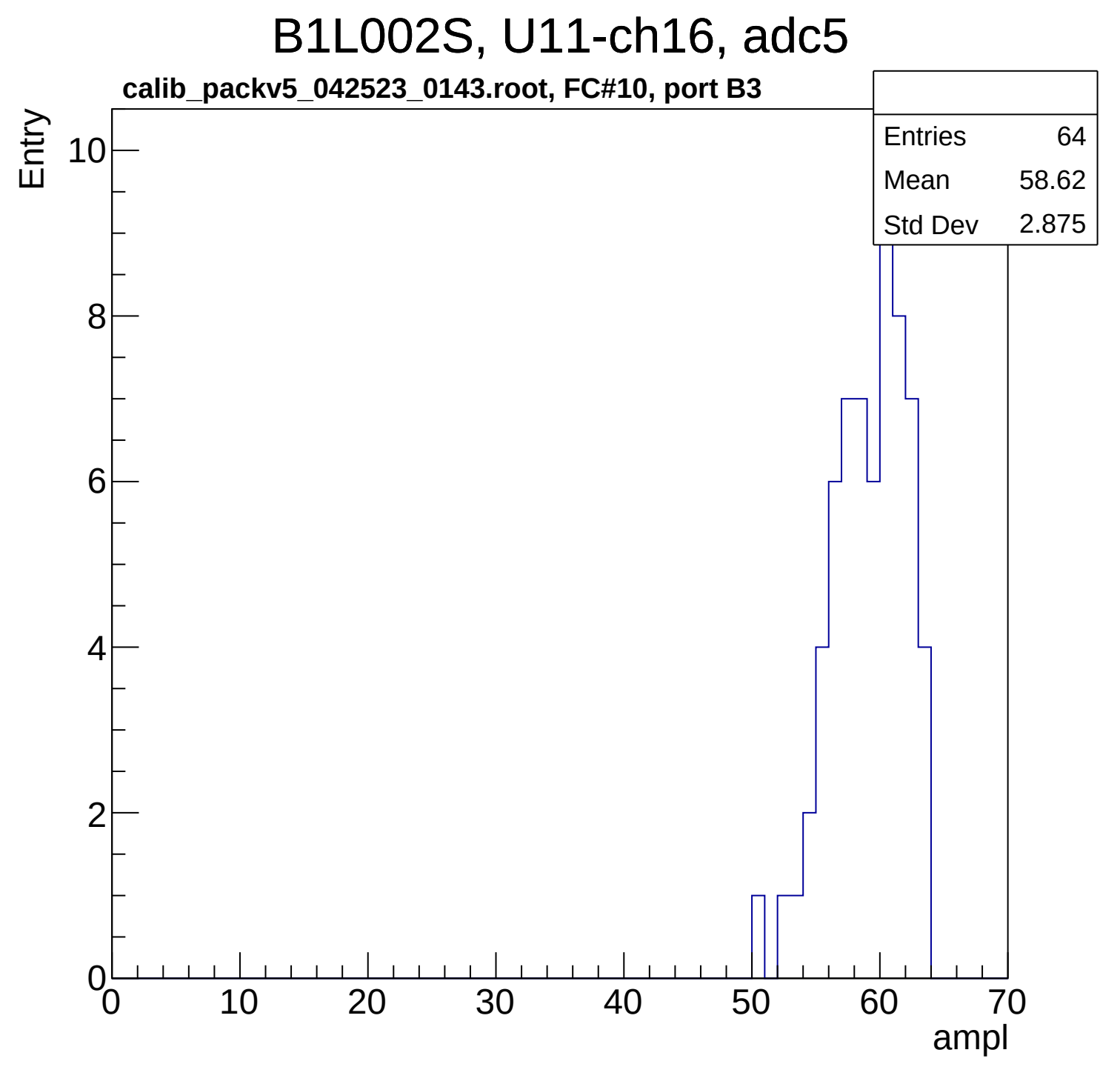
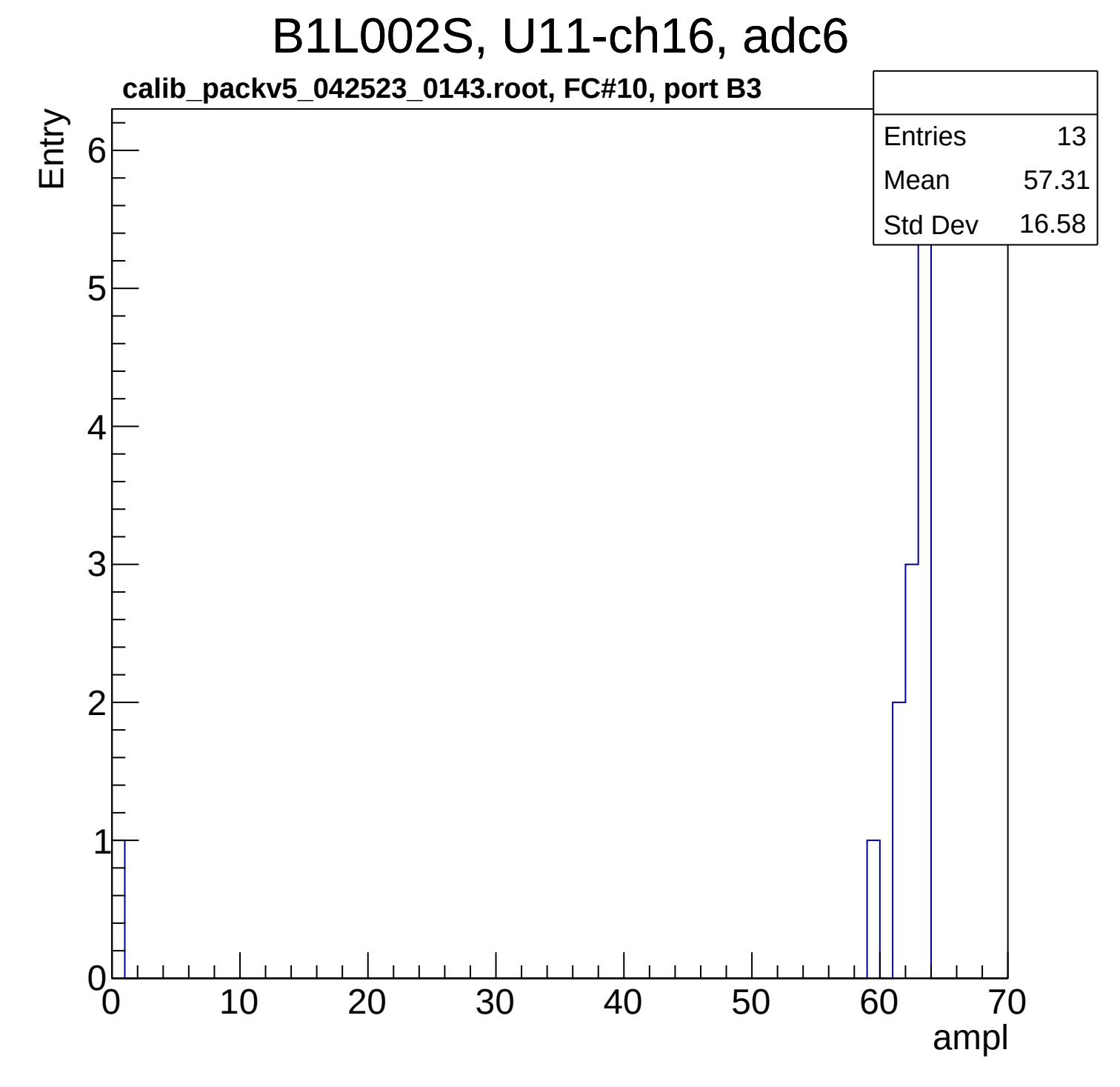
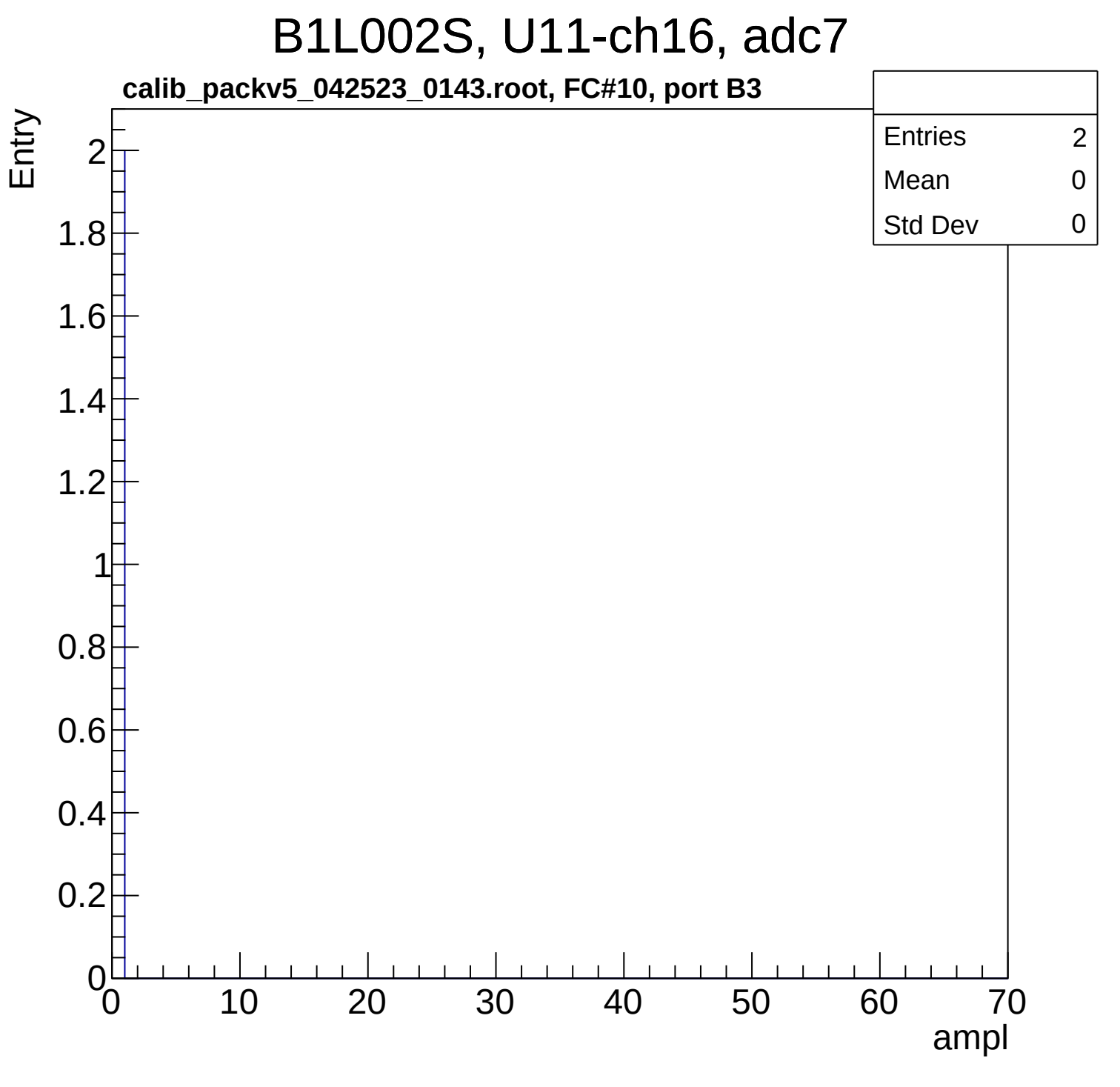
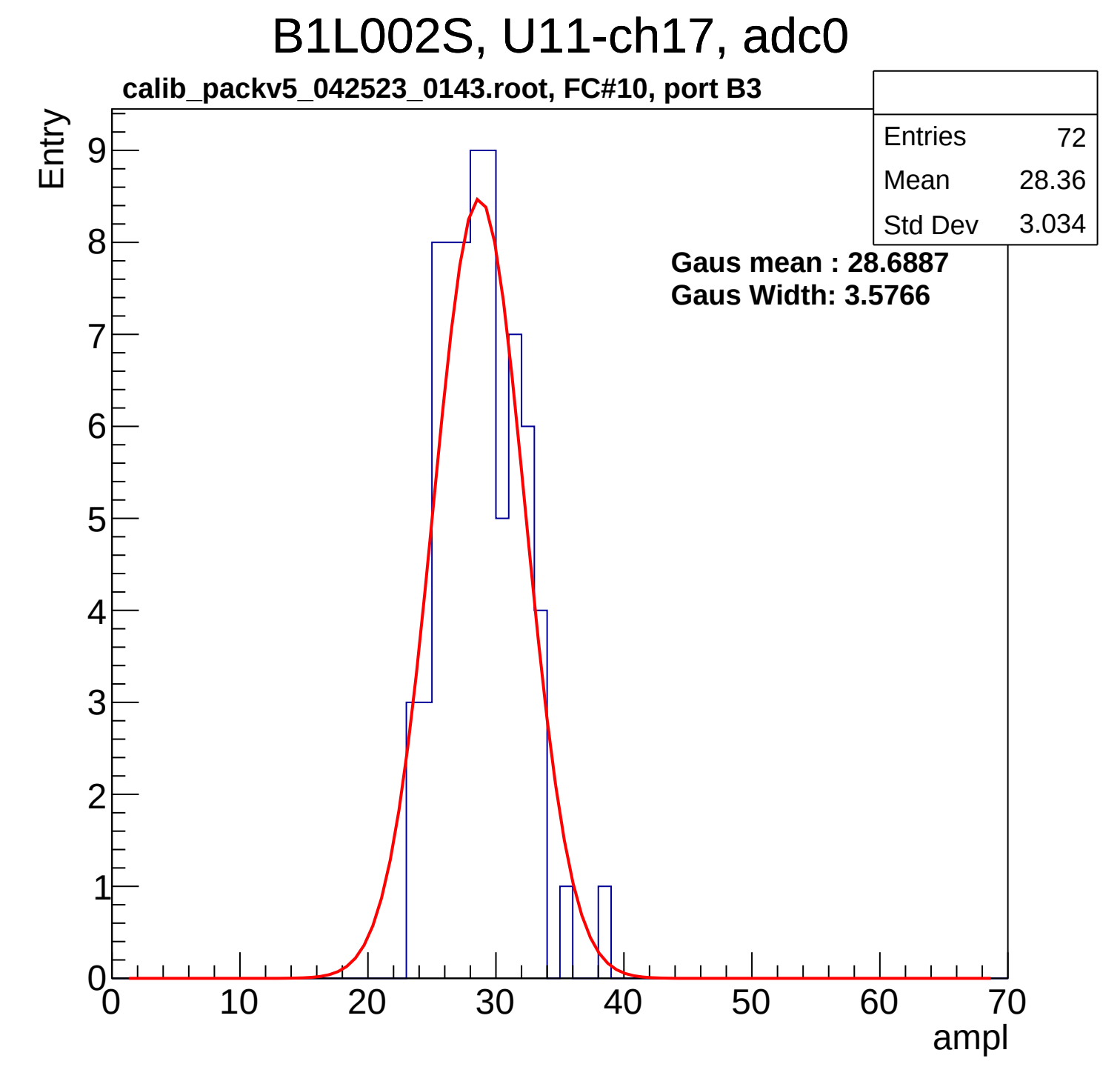

B1L002S, U11-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 52.93
Std Dev 3.067
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 58.62
Std Dev 2.875
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.31
Std Dev 16.58
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.36
Std Dev 3.034
Gaus mean : 28.6887
Gaus Width: 3.5766
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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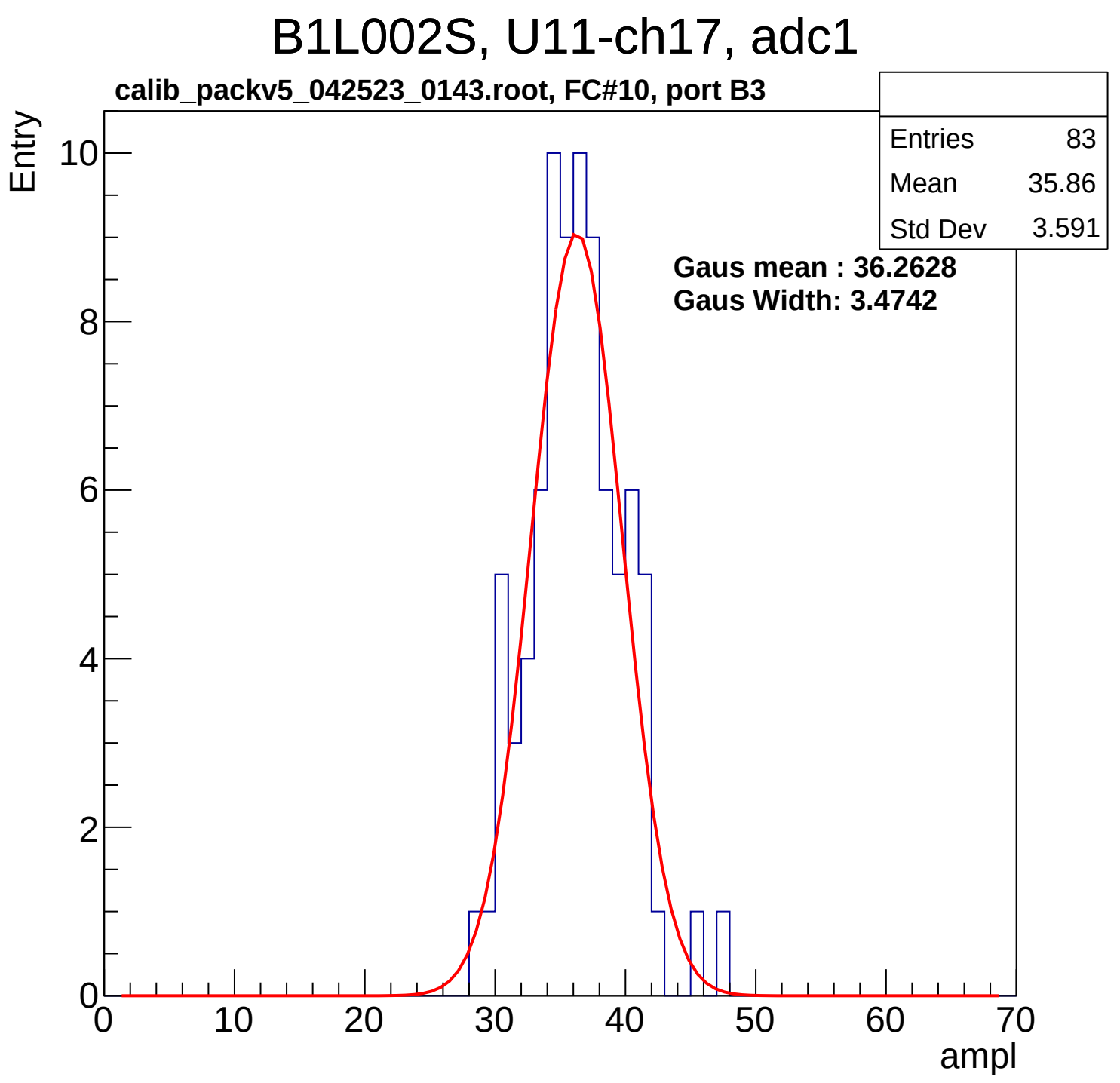
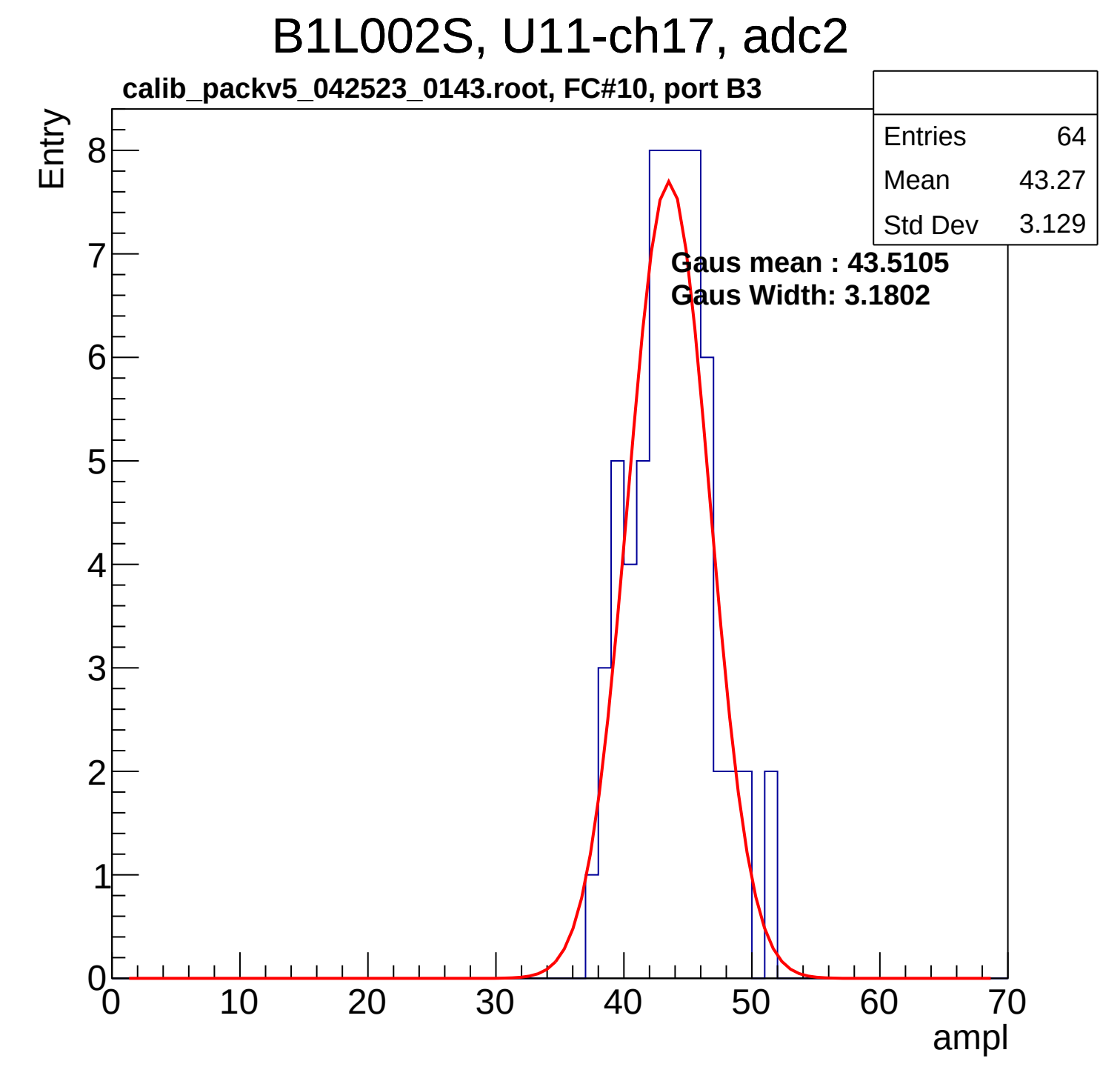

B1L002S, U11-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 35.86
Std Dev 3.591
Gaus mean : 36.2628
Gaus Width: 3.4742
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 43.27
Std Dev 3.129
Gaus mean : 43.5105
Gaus Width: 3.1802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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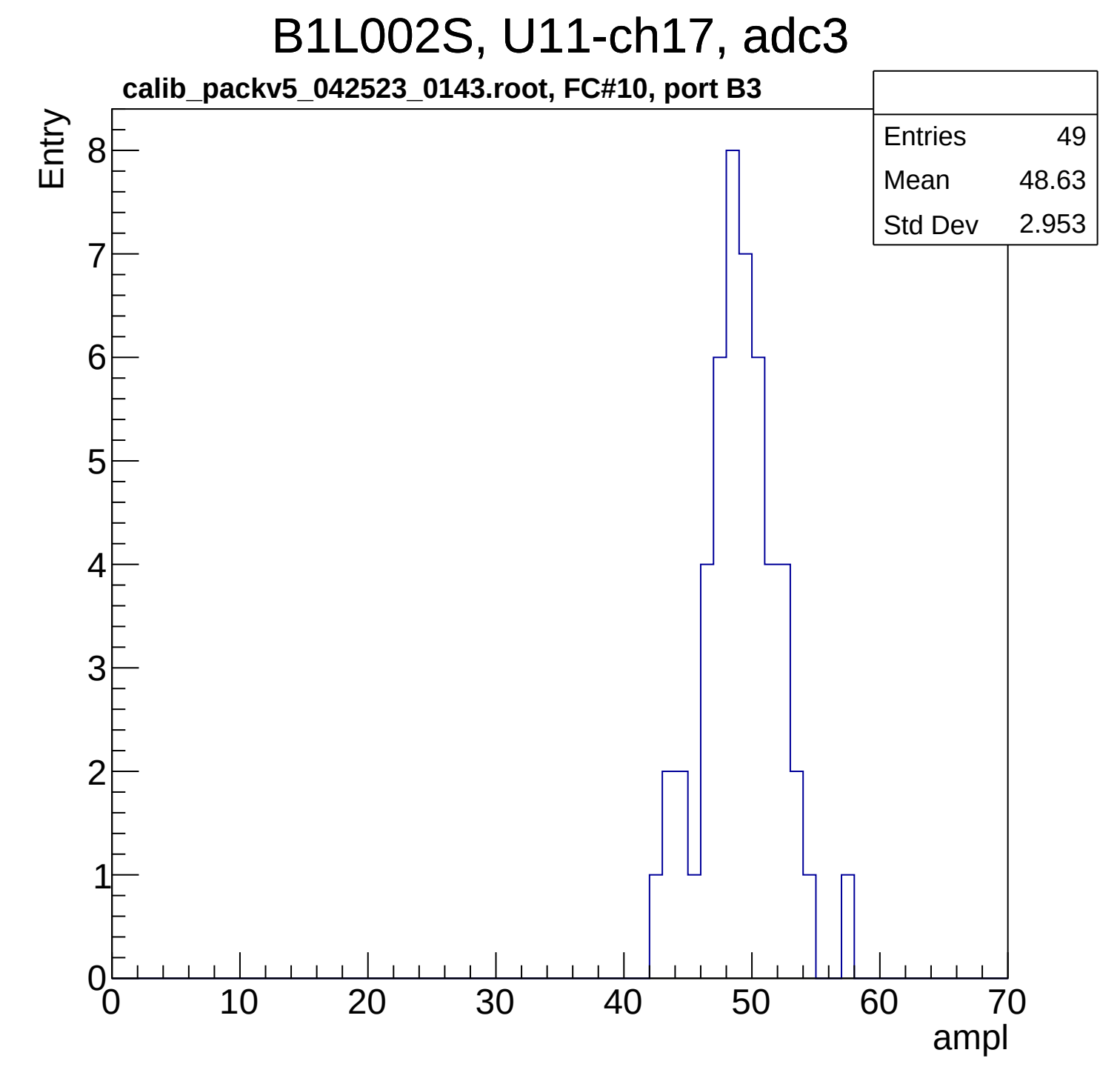
B1L002S, U11-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 48.63
Std Dev 2.953
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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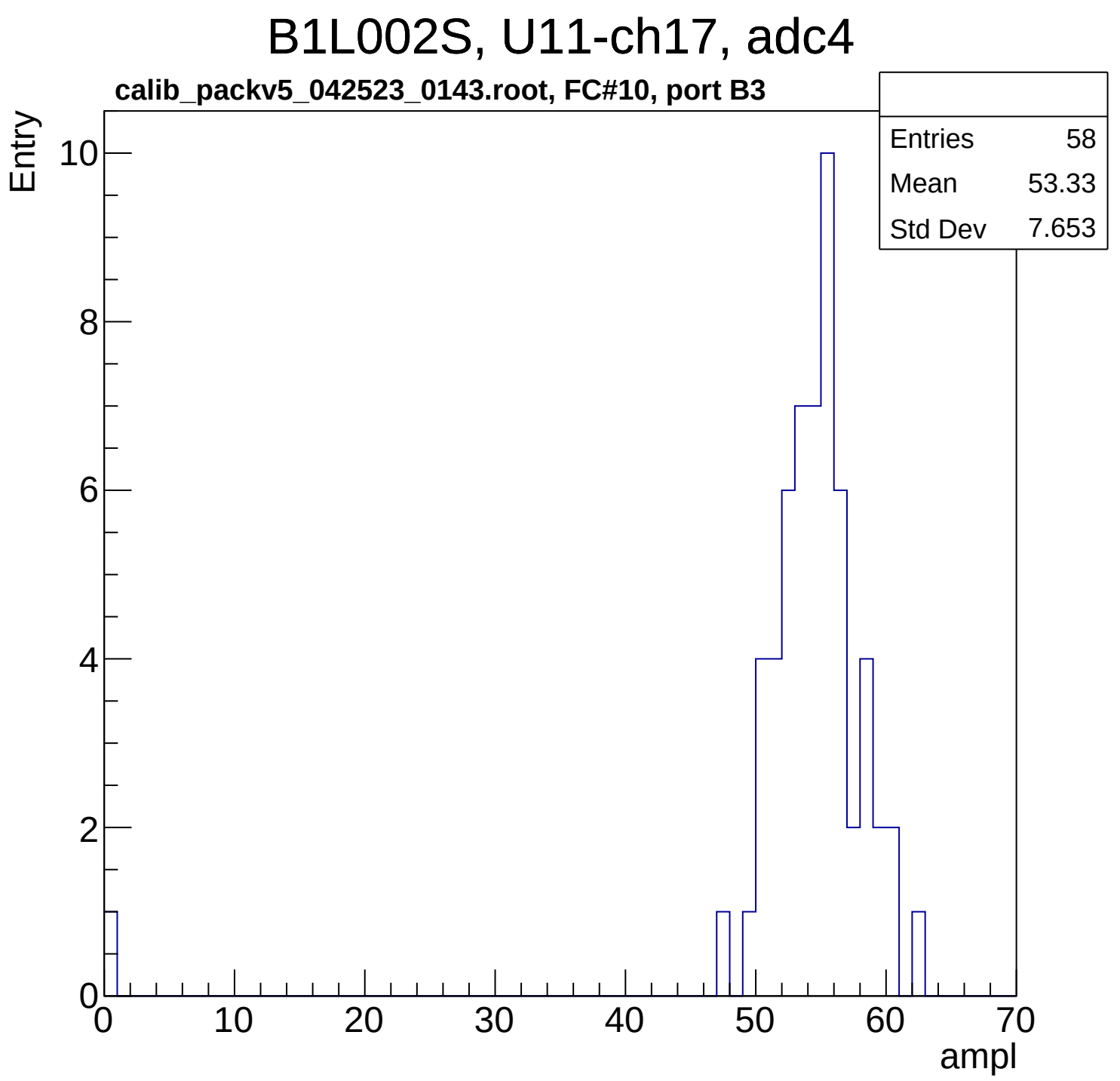
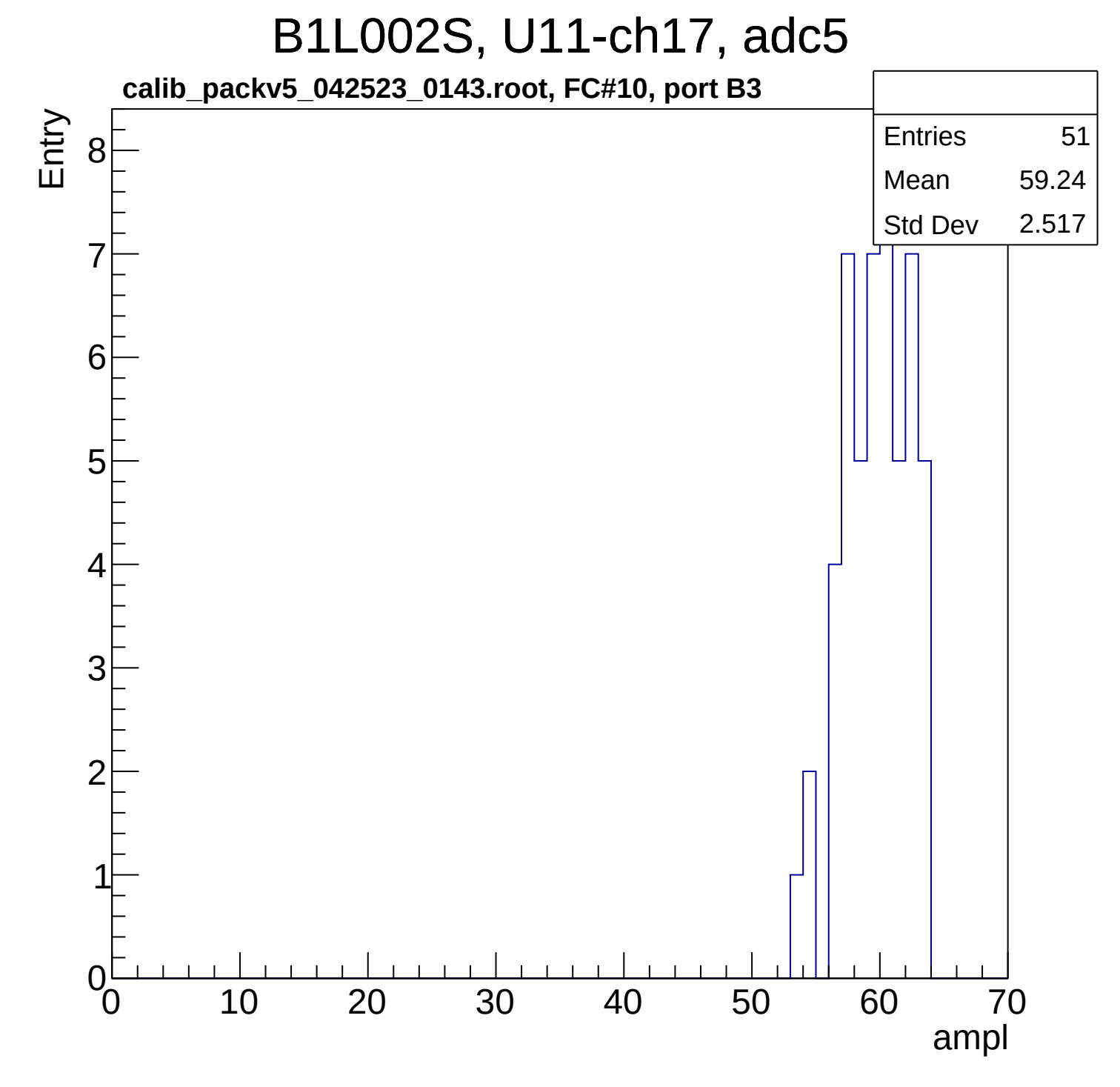

B1L002S, U11-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.33
Std Dev 7.653
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.24
Std Dev 2.517
Entry
ampl
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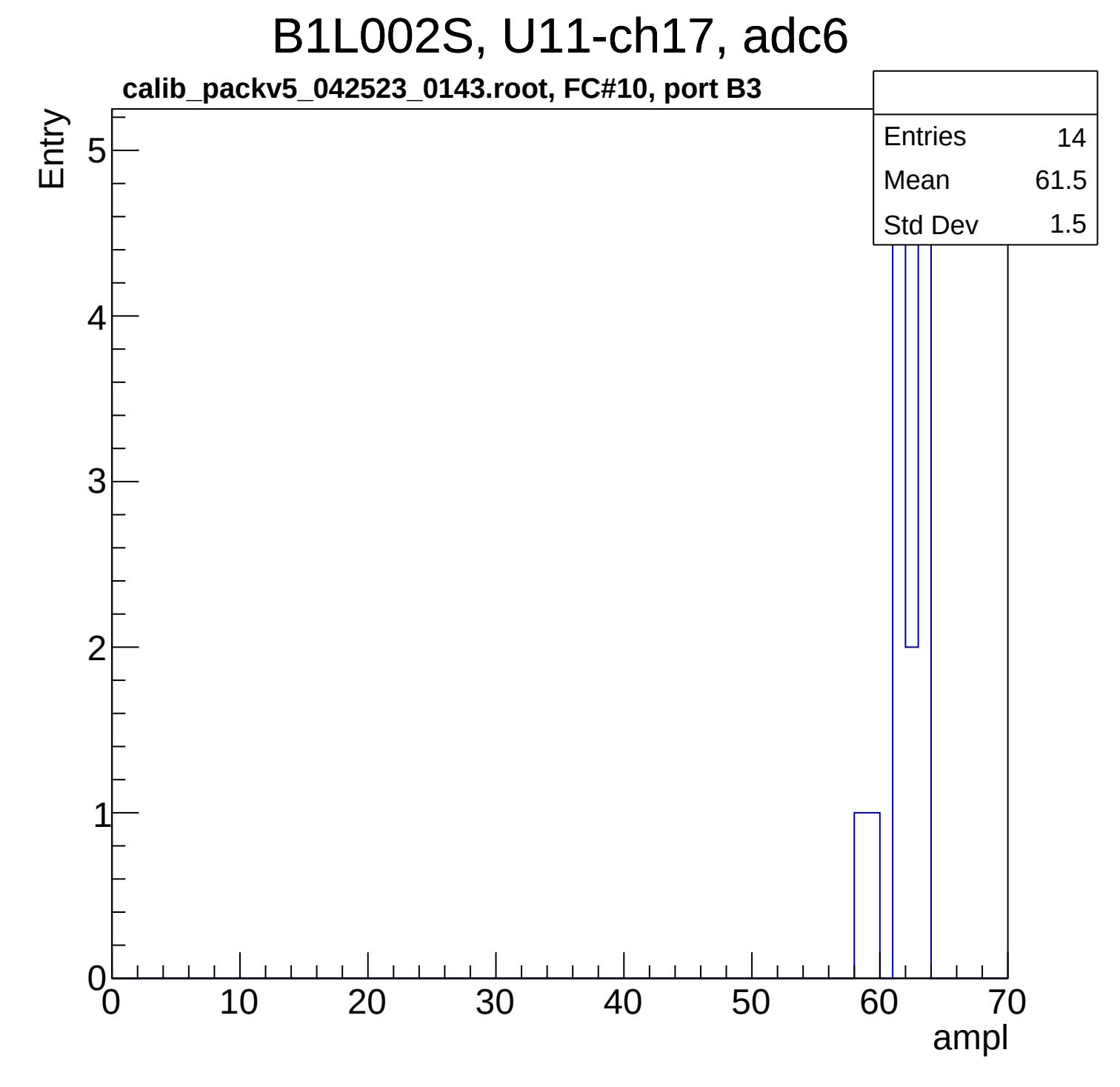
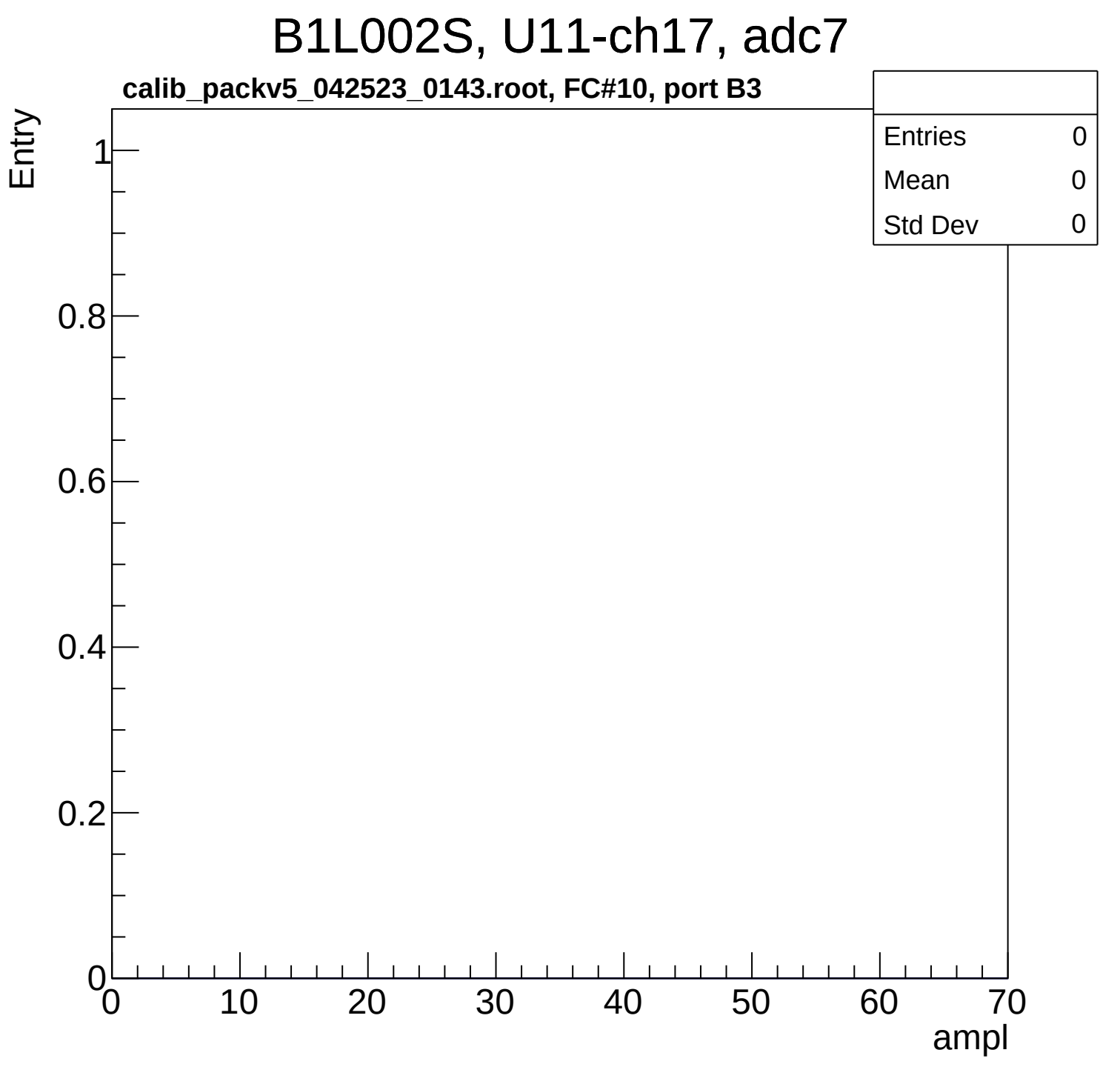
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.5
Std Dev 1.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
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0
0
10
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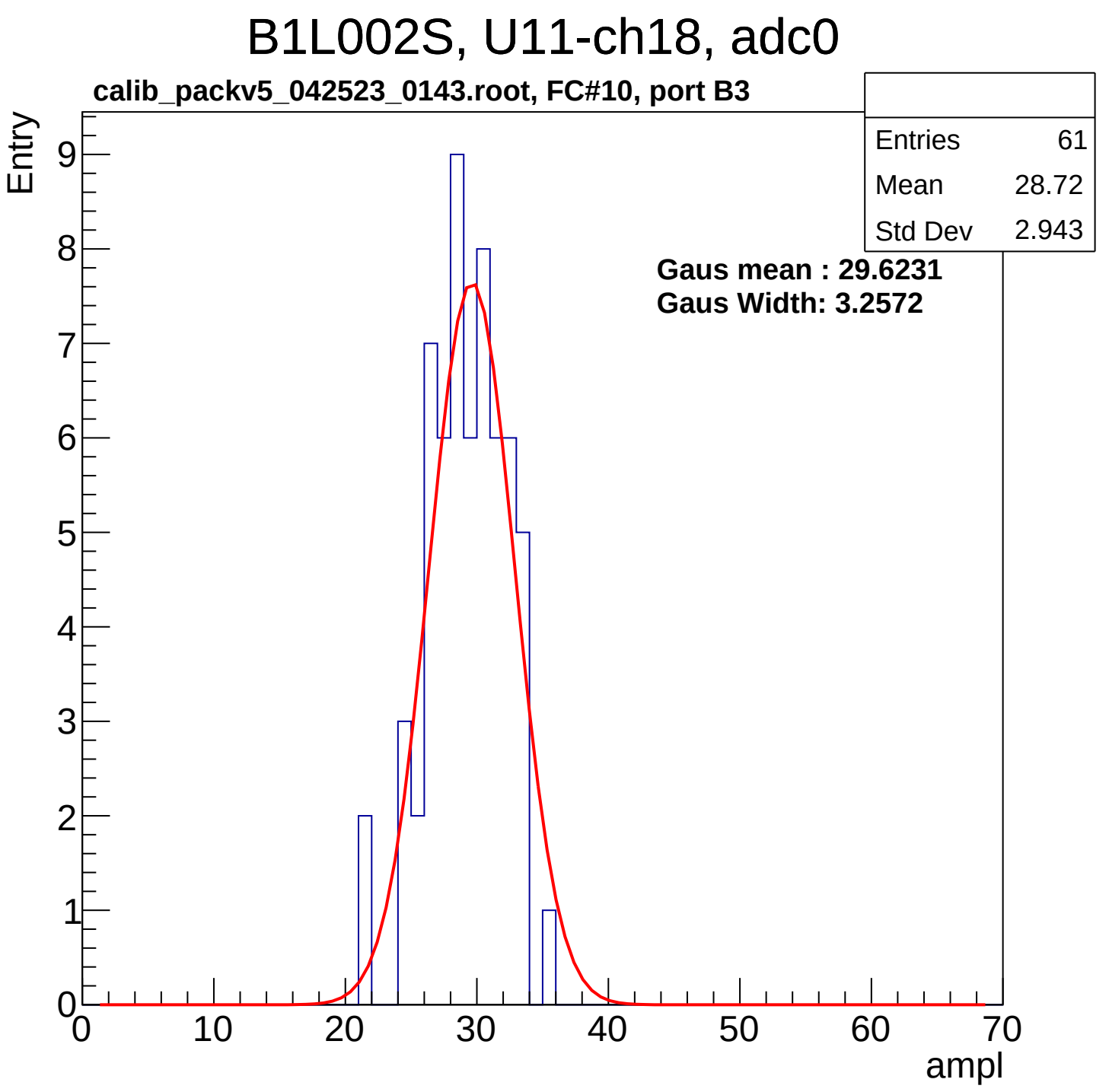
50
60
70
ampl


B1L002S, U11-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.72
Std Dev 2.943
Gaus mean : 29.6231
Gaus Width: 3.2572
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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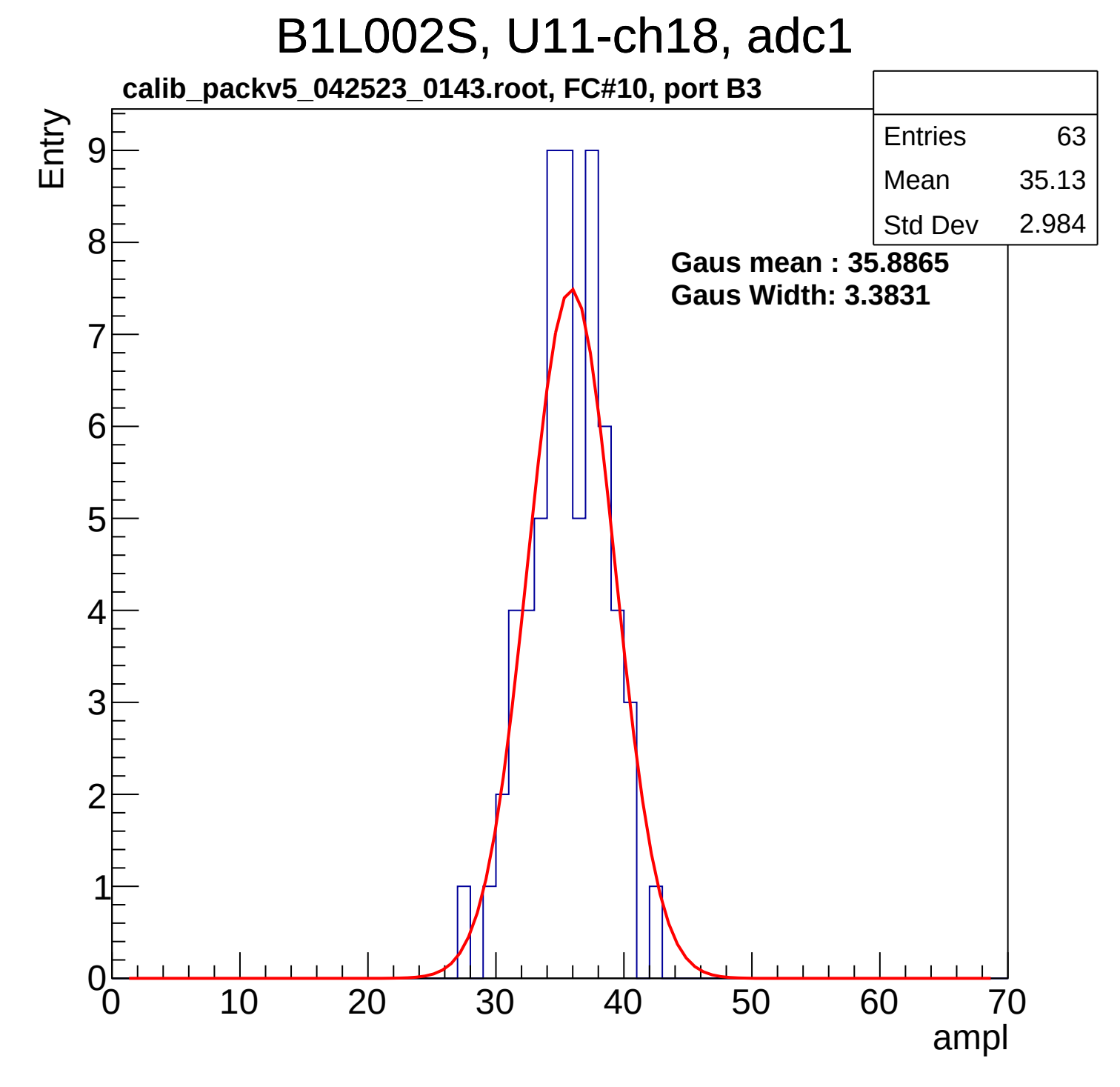
B1L002S, U11-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.13
Std Dev 2.984
Gaus mean : 35.8865
Gaus Width: 3.3831
Entry
ampl
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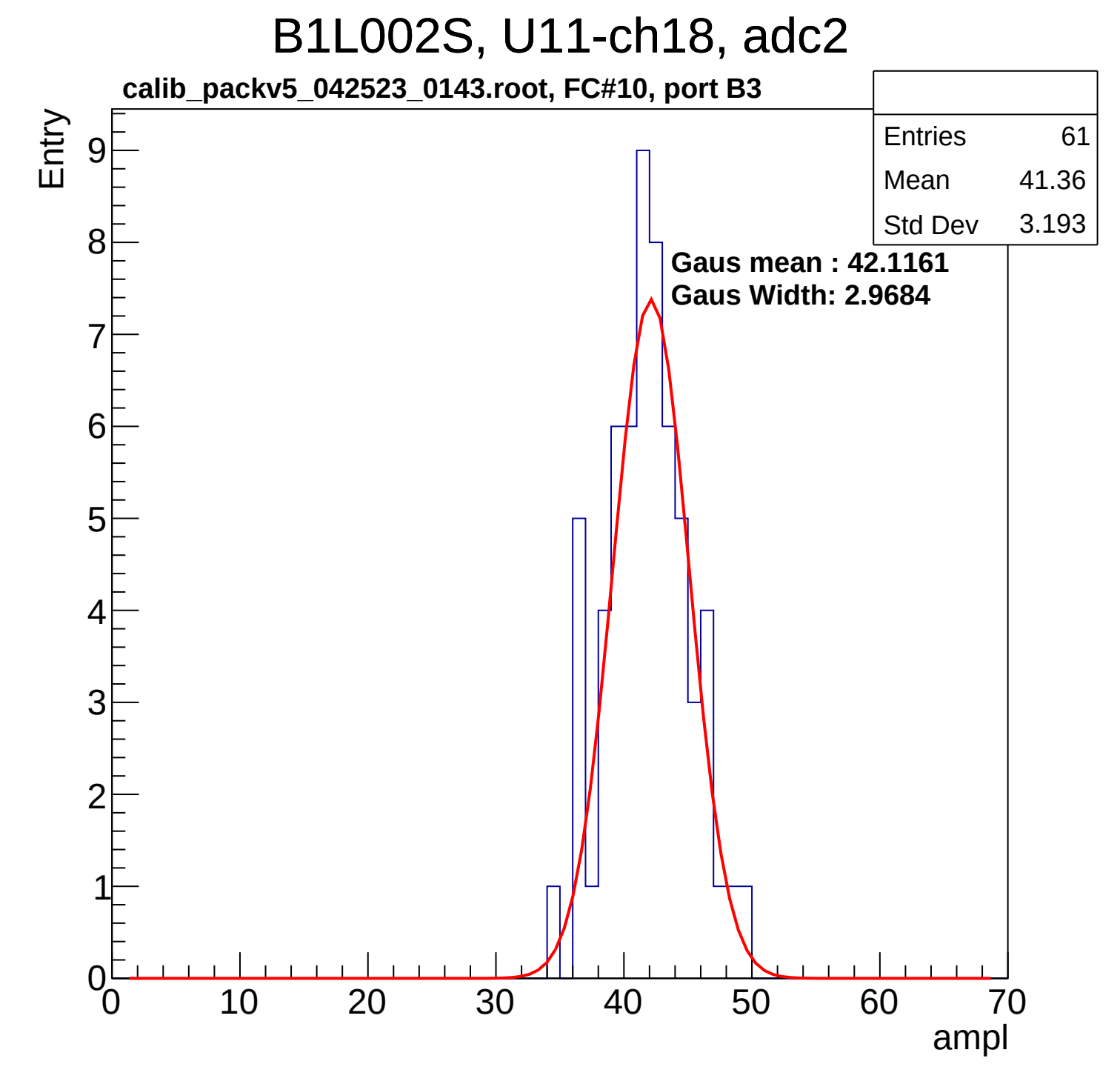
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.36
Std Dev 3.193
Gaus mean : 42.1161
Gaus Width: 2.9684
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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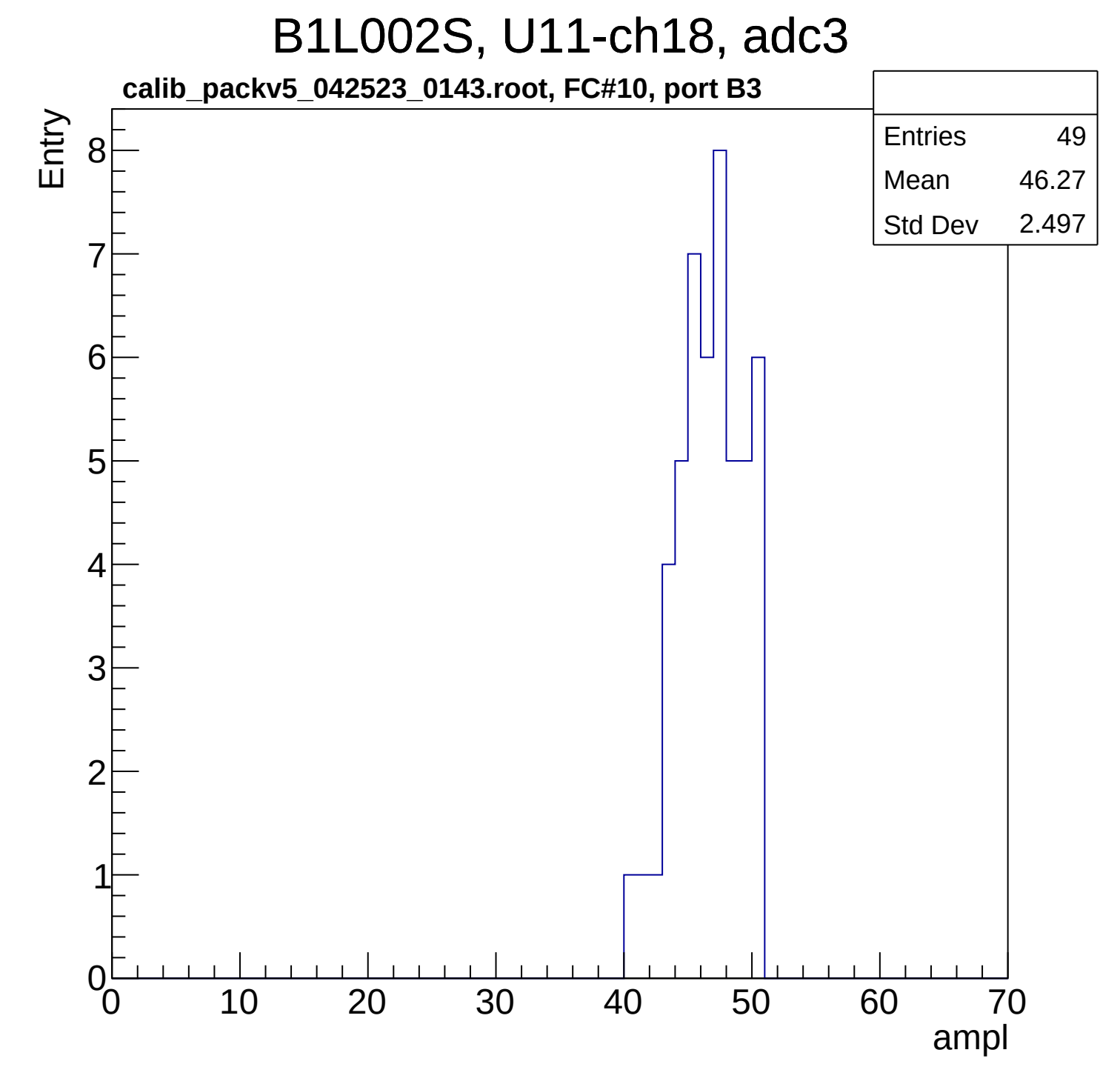
60
70


B1L002S, U11-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 46.27
Std Dev 2.497
Entry
ampl
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2
3
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5
6
7
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0
10
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60
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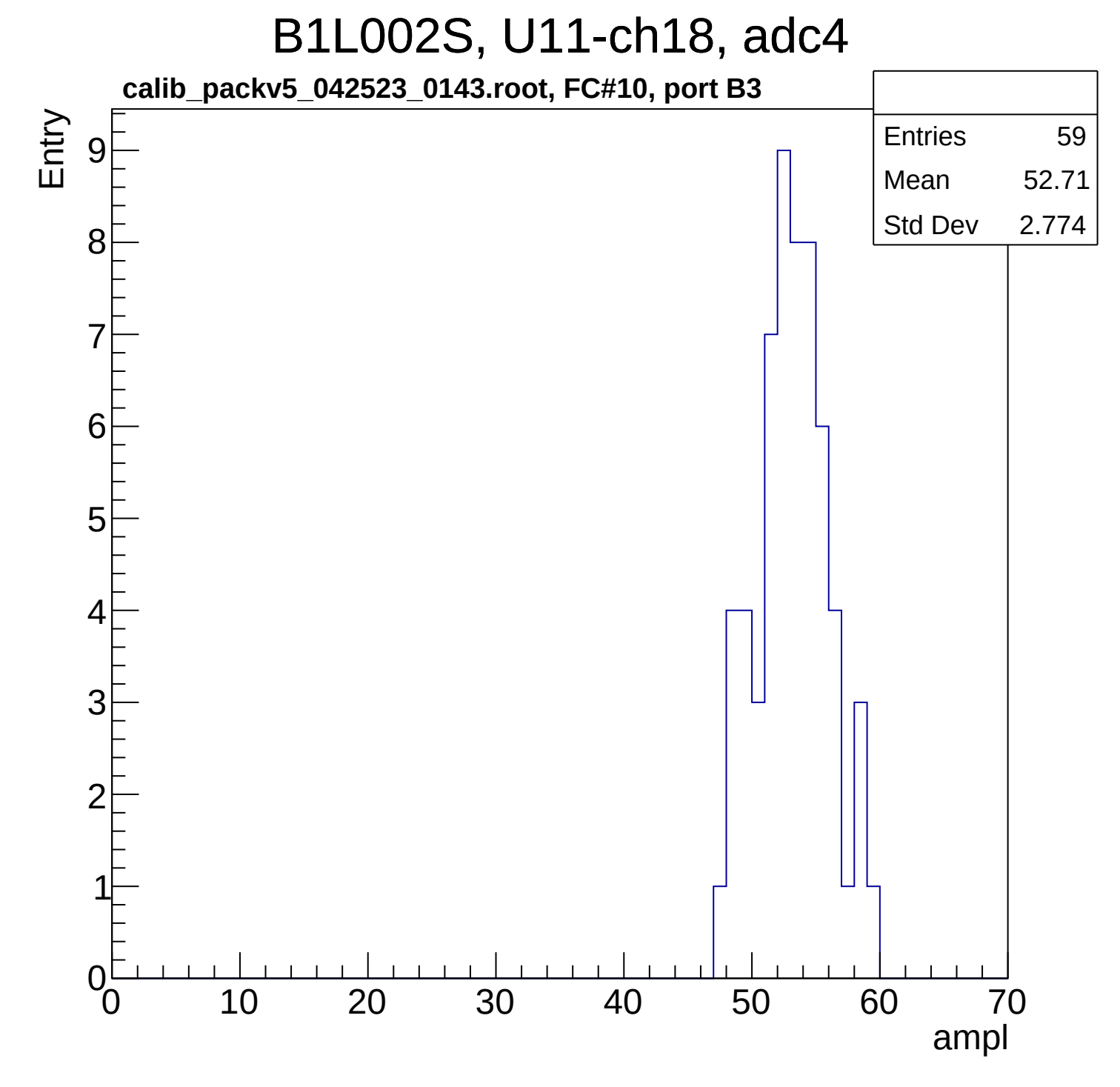
B1L002S, U11-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.71
Std Dev 2.774
Entry
ampl
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4
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6
7
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0
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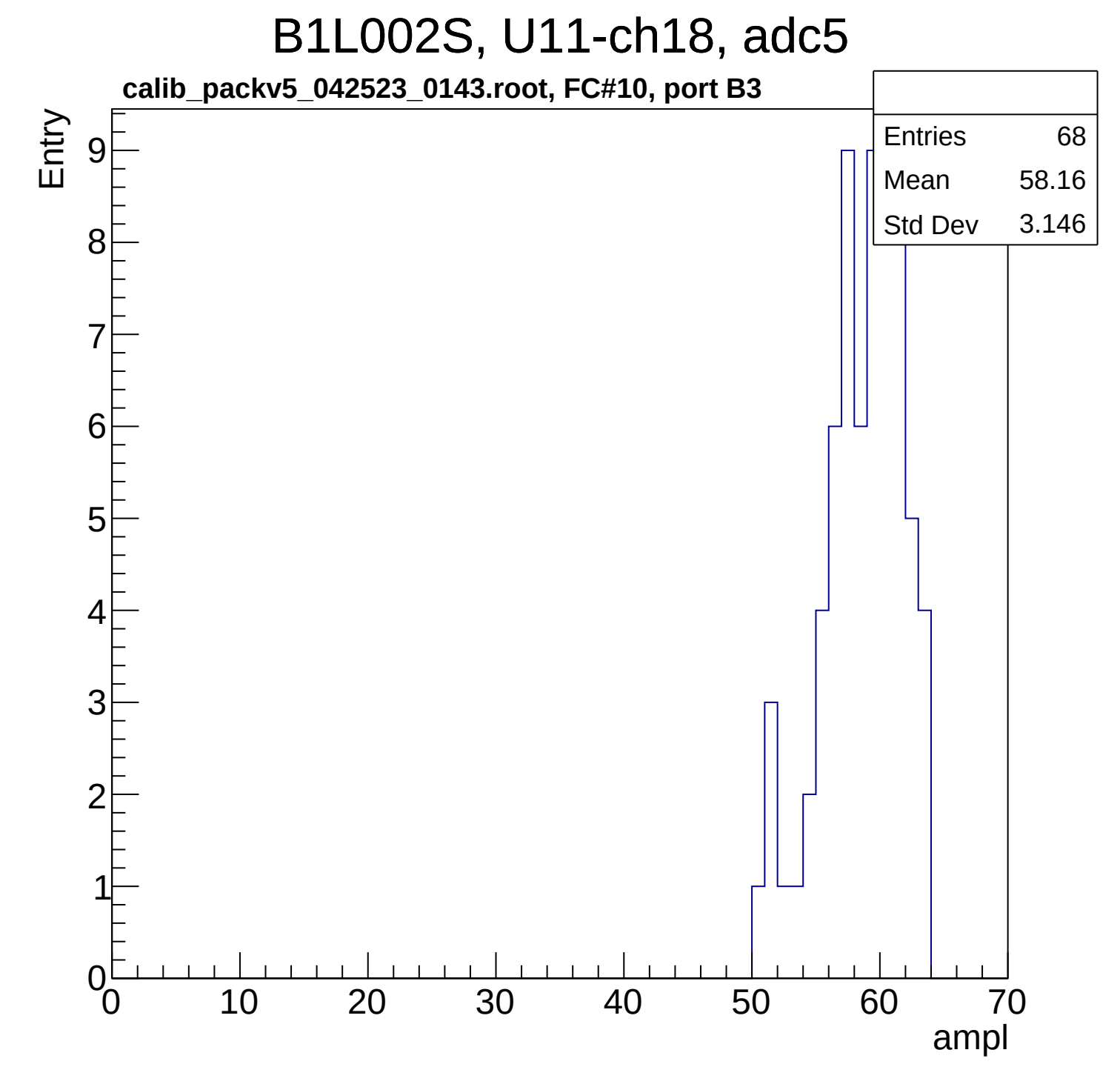
B1L002S, U11-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 58.16
Std Dev 3.146
Entry
ampl
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2
3
4
5
6
7
8
9
0
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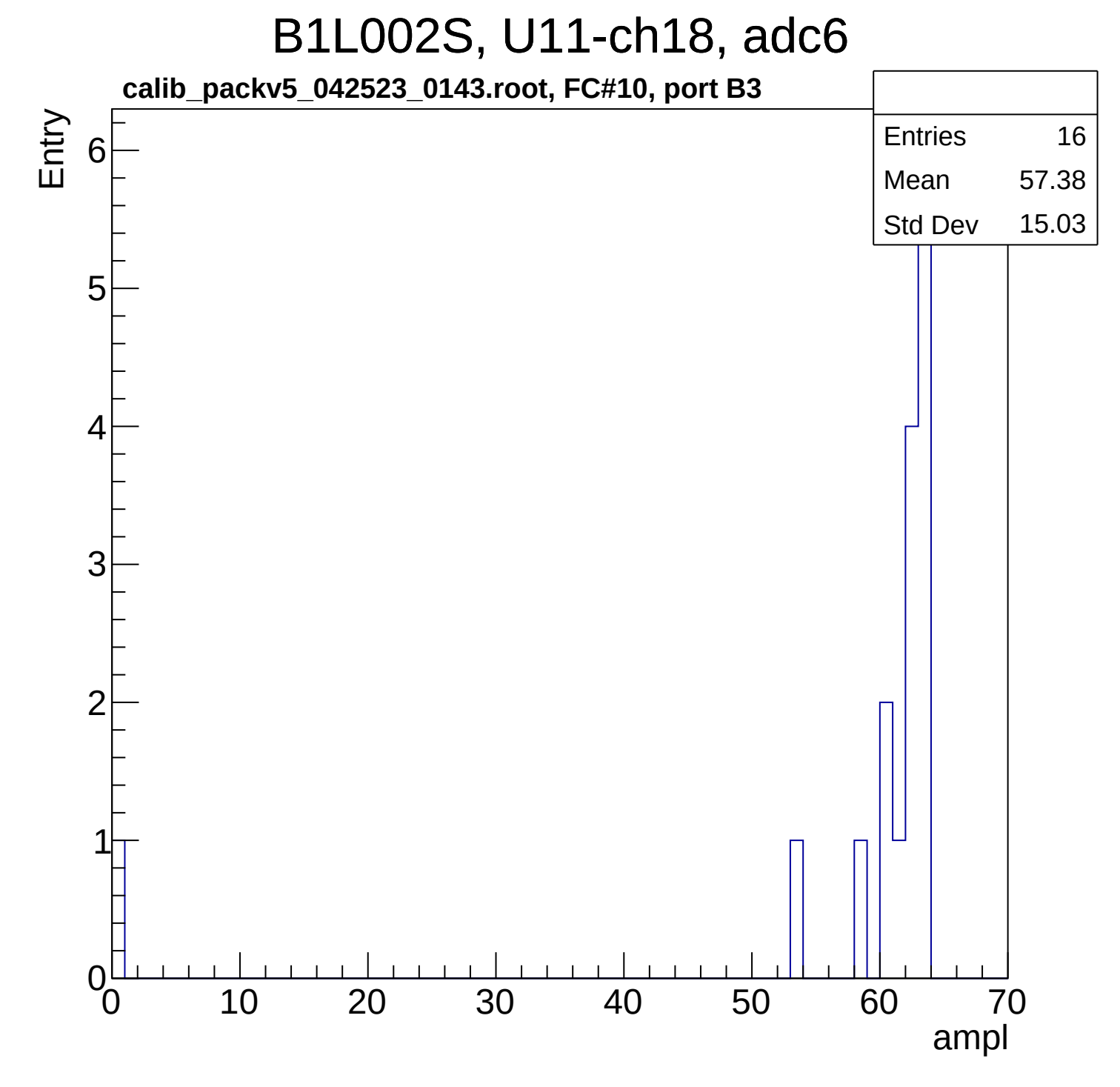
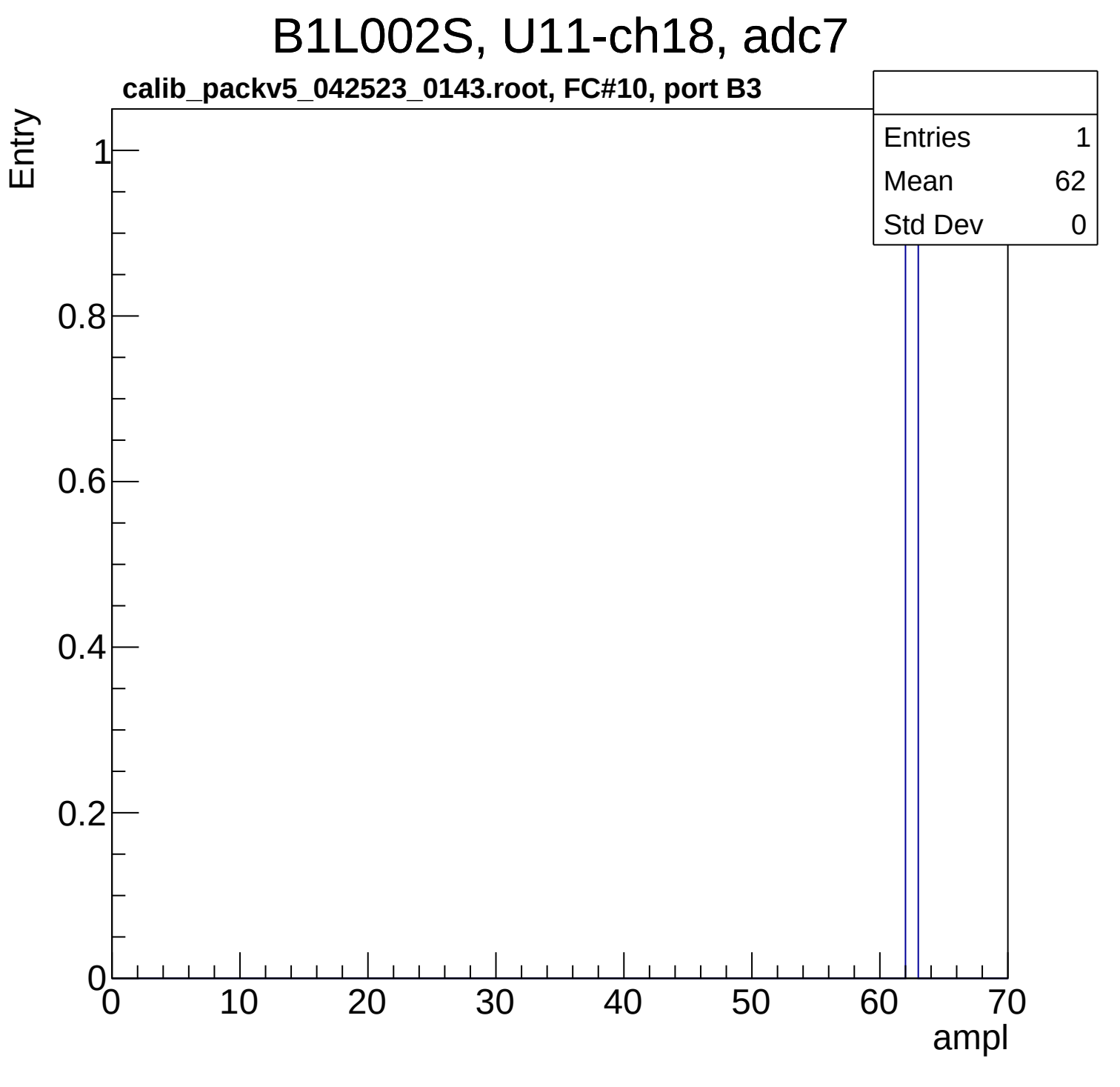

B1L002S, U11-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.38
Std Dev 15.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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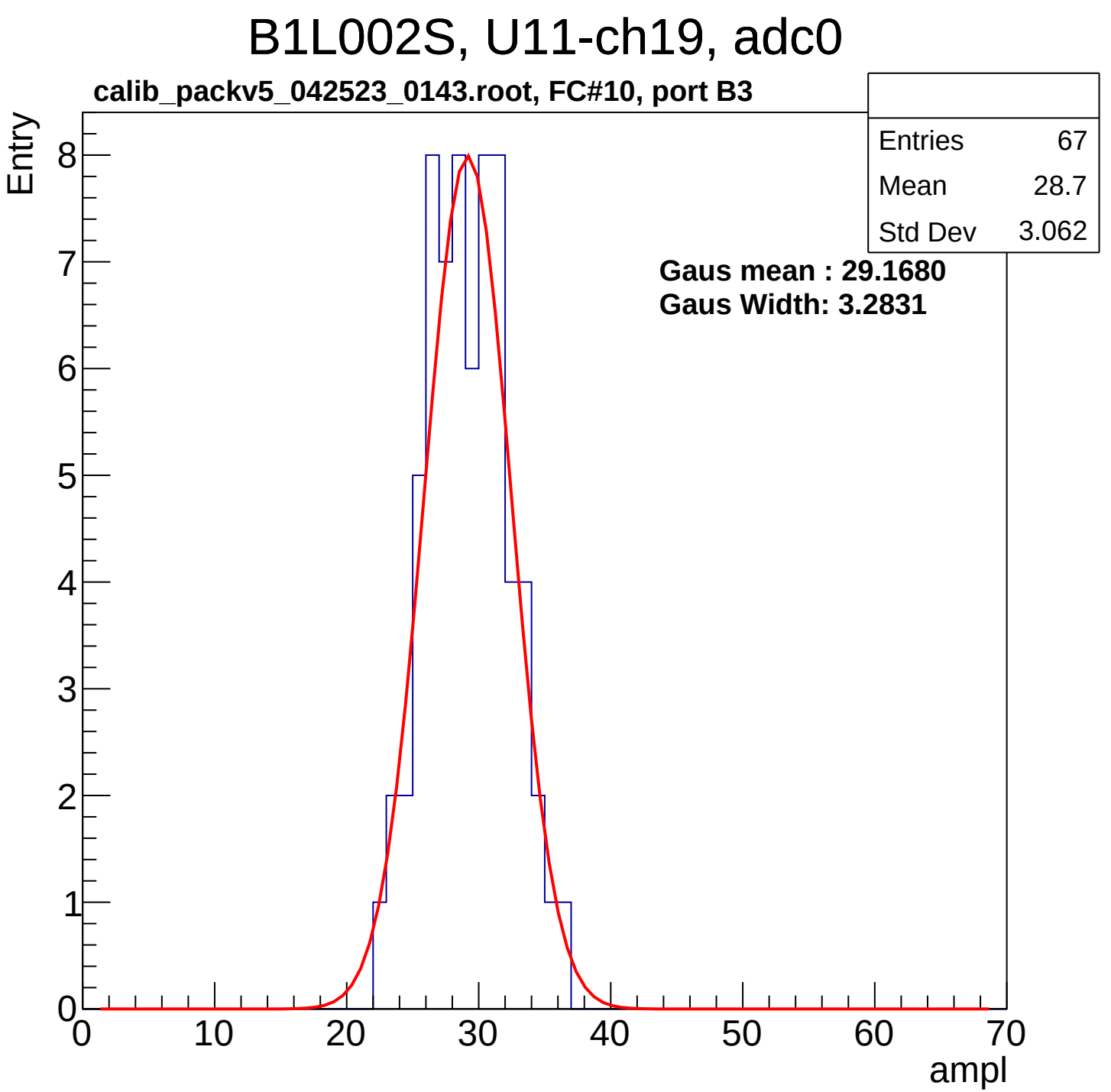
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.7
Std Dev 3.062
Gaus mean : 29.1680
Gaus Width: 3.2831
Entry
ampl
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1
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3
4
5
6
7
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0
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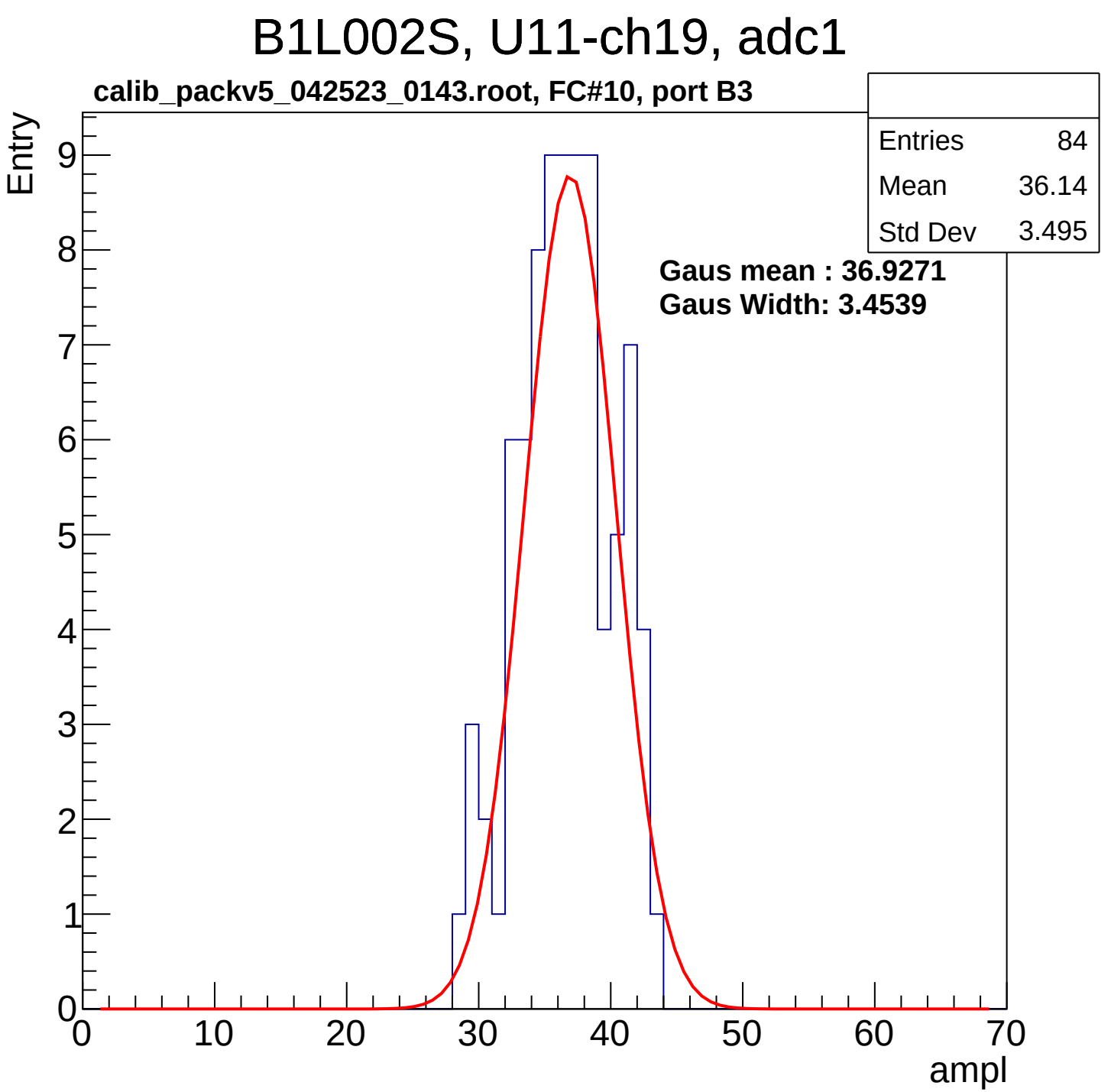
B1L002S, U11-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 36.14
Std Dev 3.495
Gaus mean : 36.9271
Gaus Width: 3.4539
Entry
ampl
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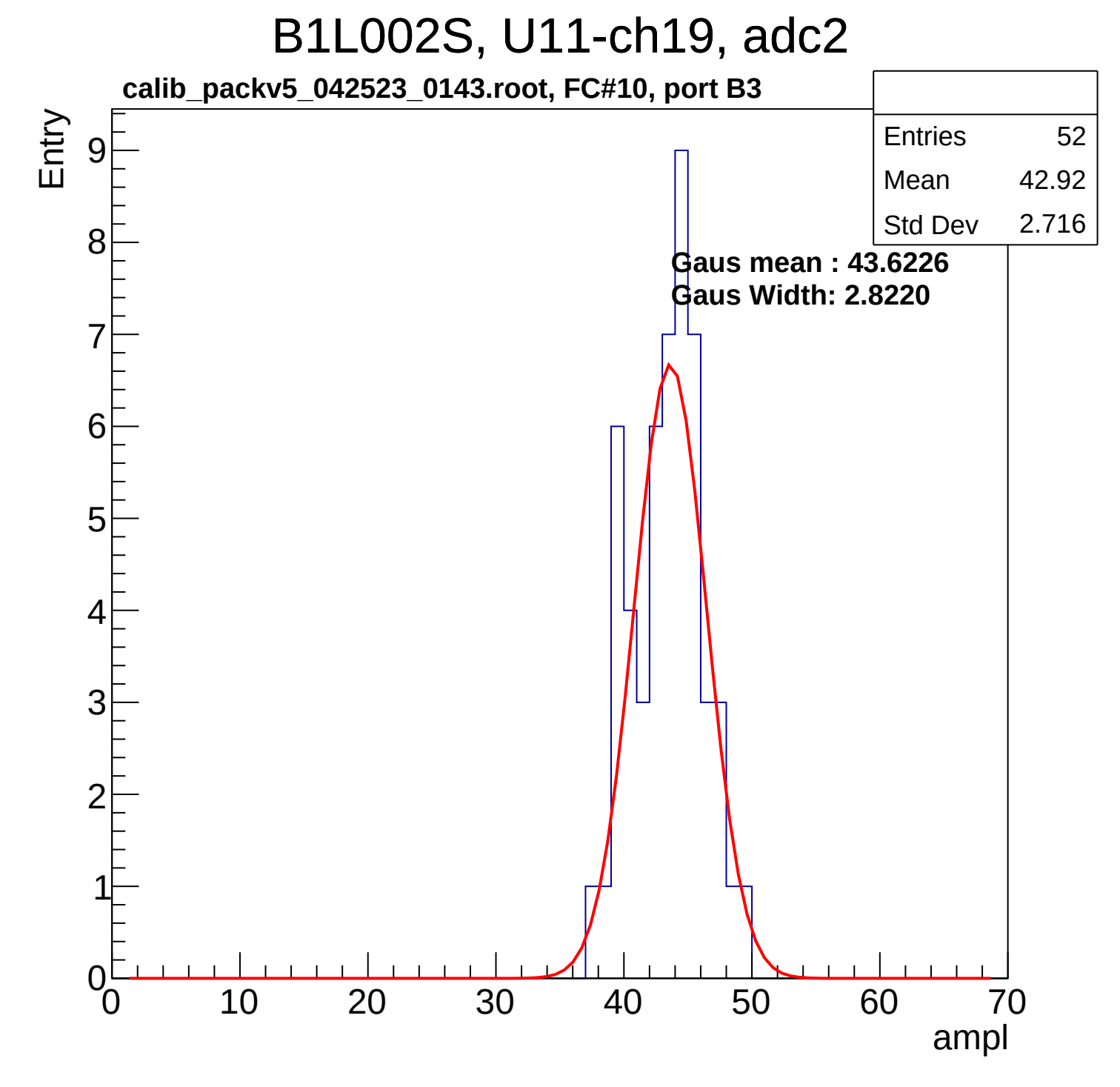
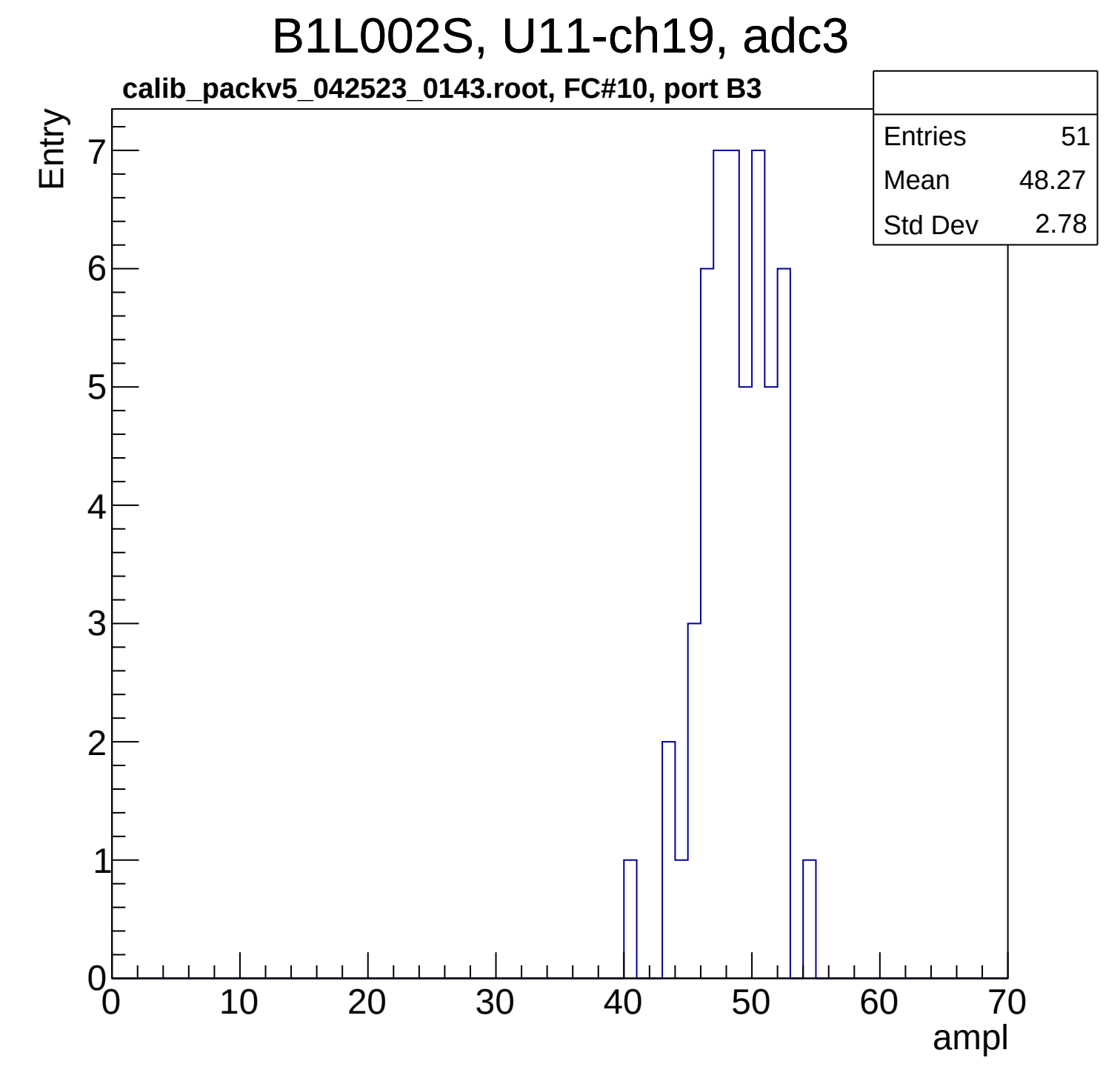
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 42.92
Std Dev 2.716
Gaus mean : 43.6226
Gaus Width: 2.8220
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 48.27
Std Dev 2.78
Entry
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1
2
3
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5
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7
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10
20
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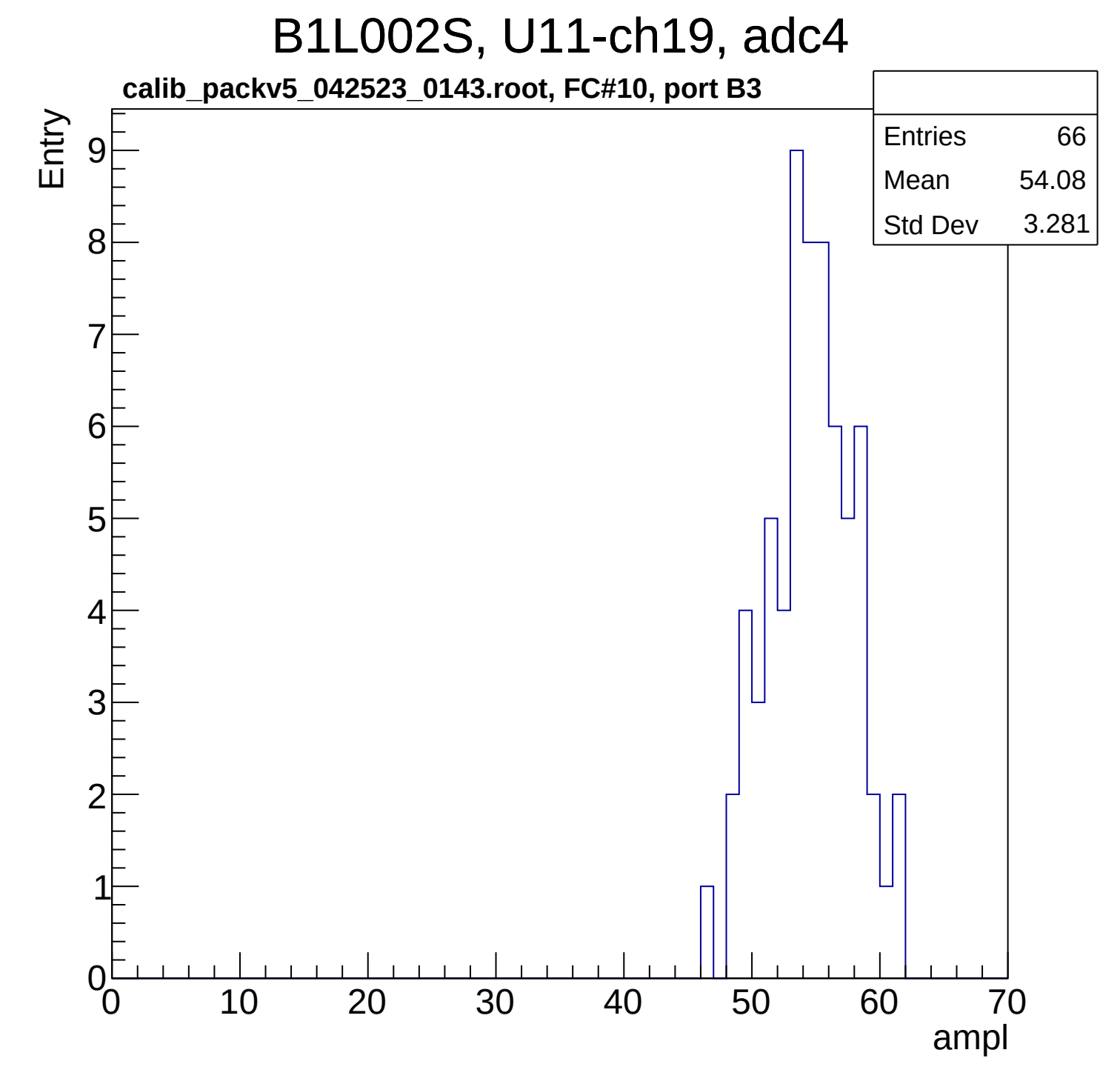
50
60
70
ampl


B1L002S, U11-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.08
Std Dev 3.281
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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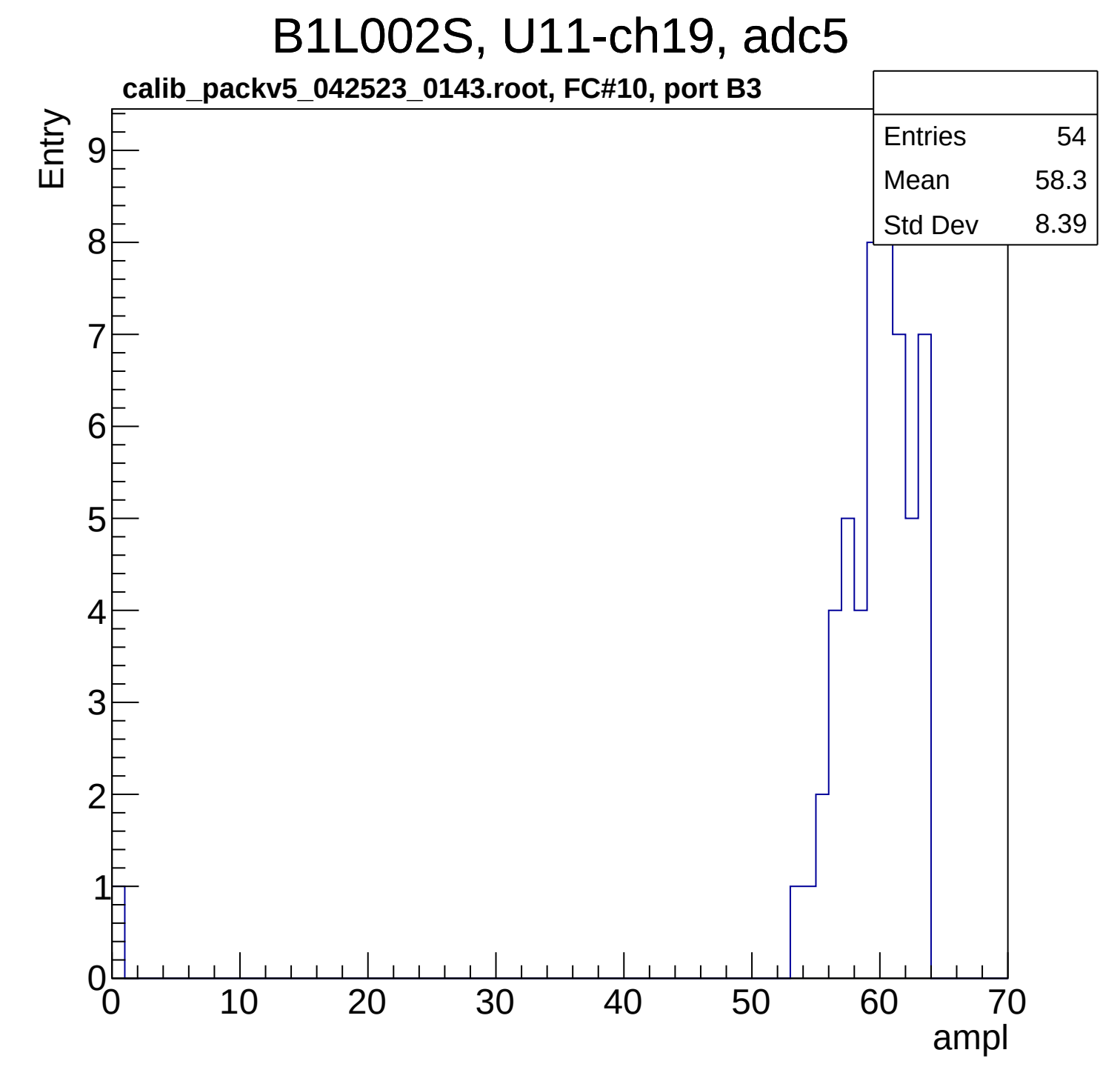
B1L002S, U11-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.3
Std Dev 8.39
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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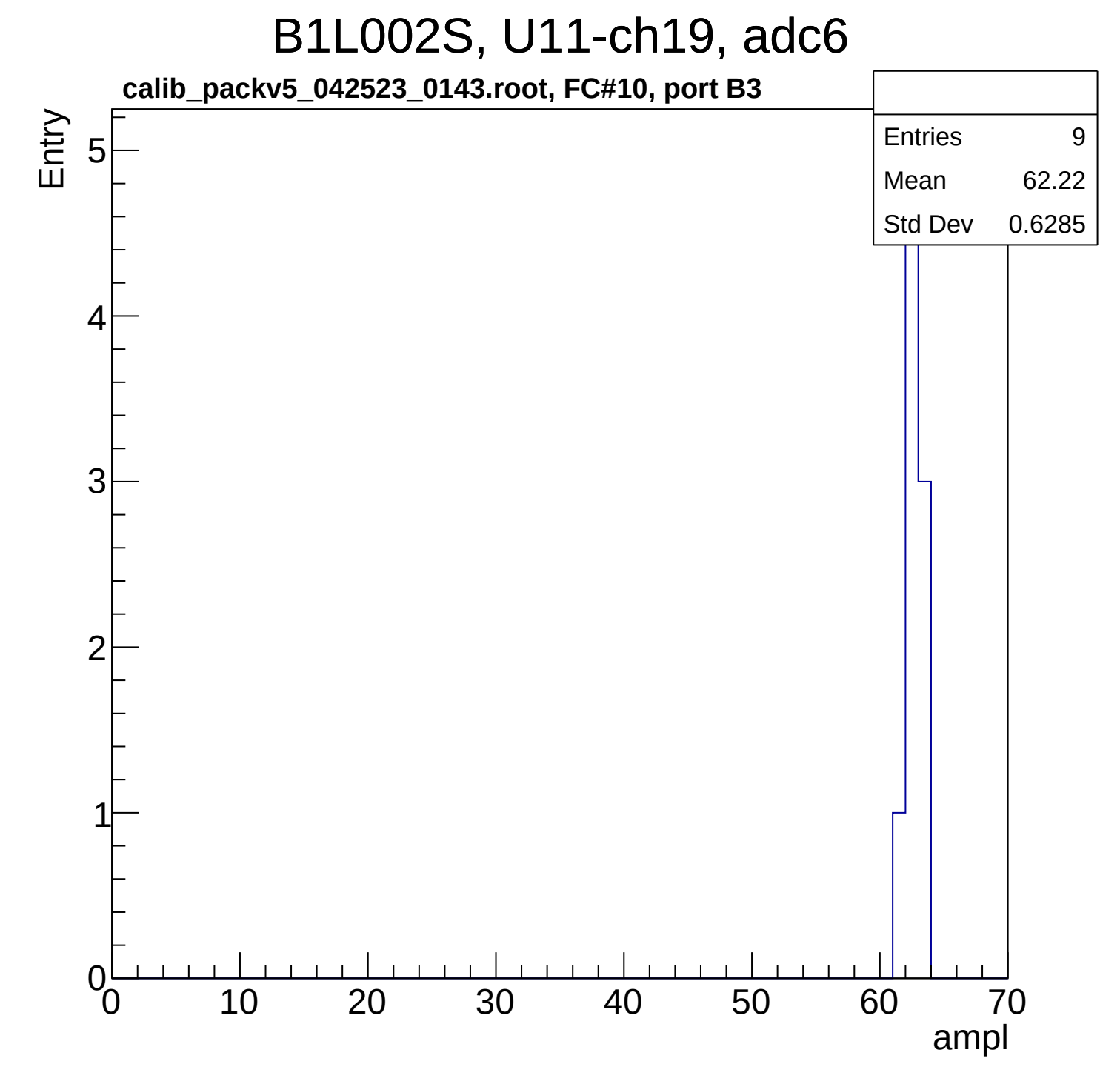
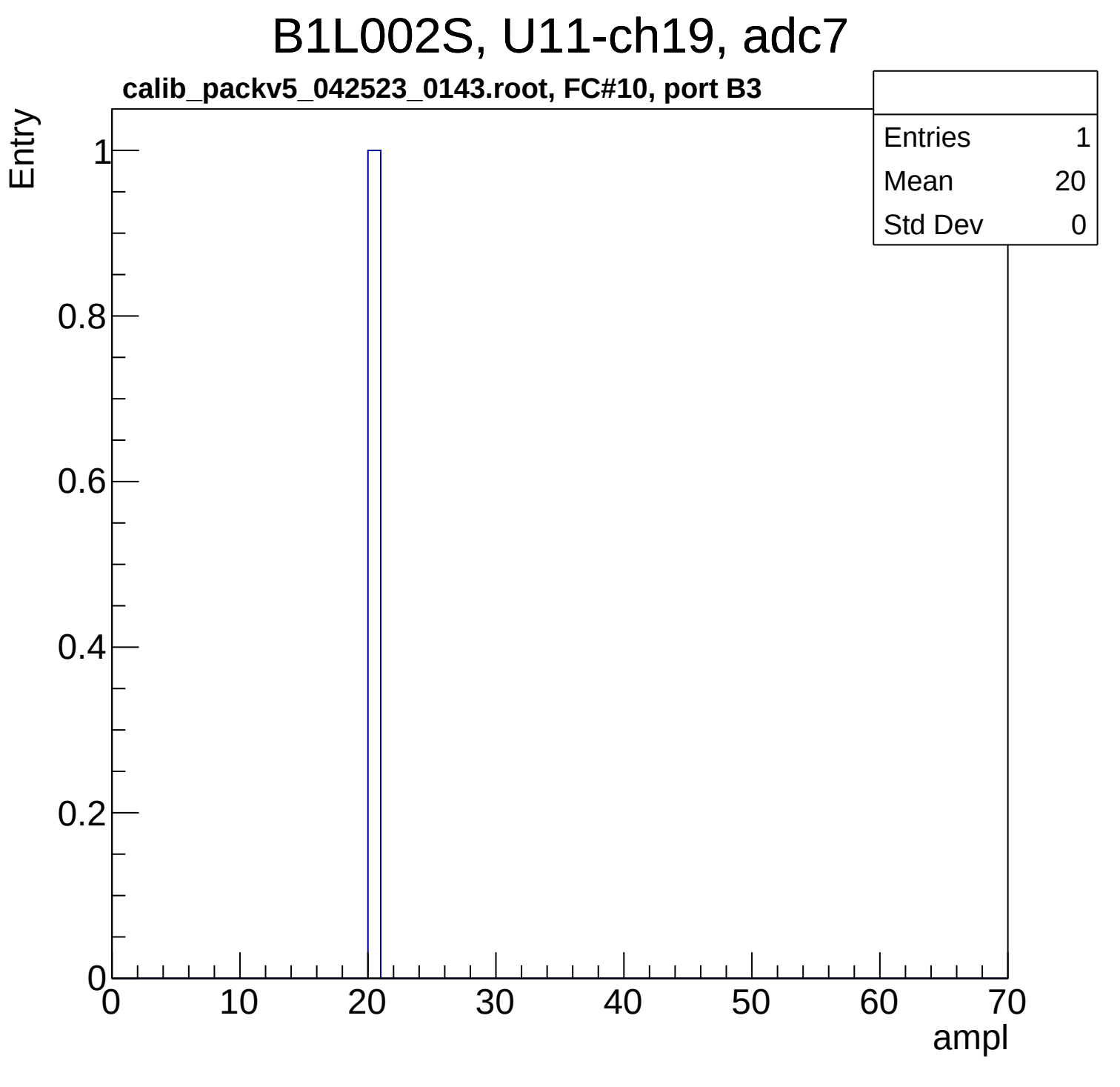
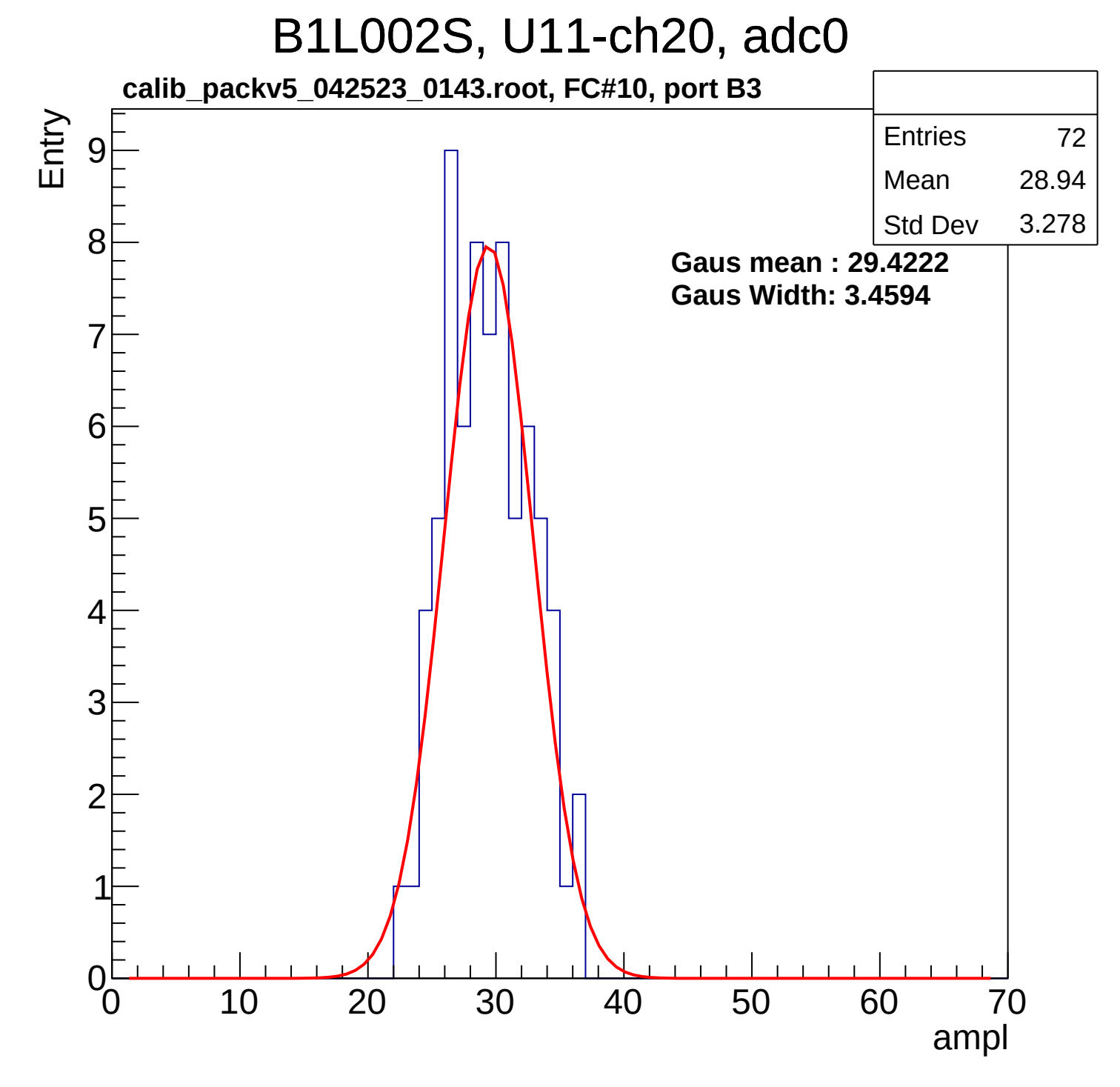

B1L002S, U11-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 62.22
Std Dev 0.6285
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.94
Std Dev 3.278
Gaus mean : 29.4222
Gaus Width: 3.4594
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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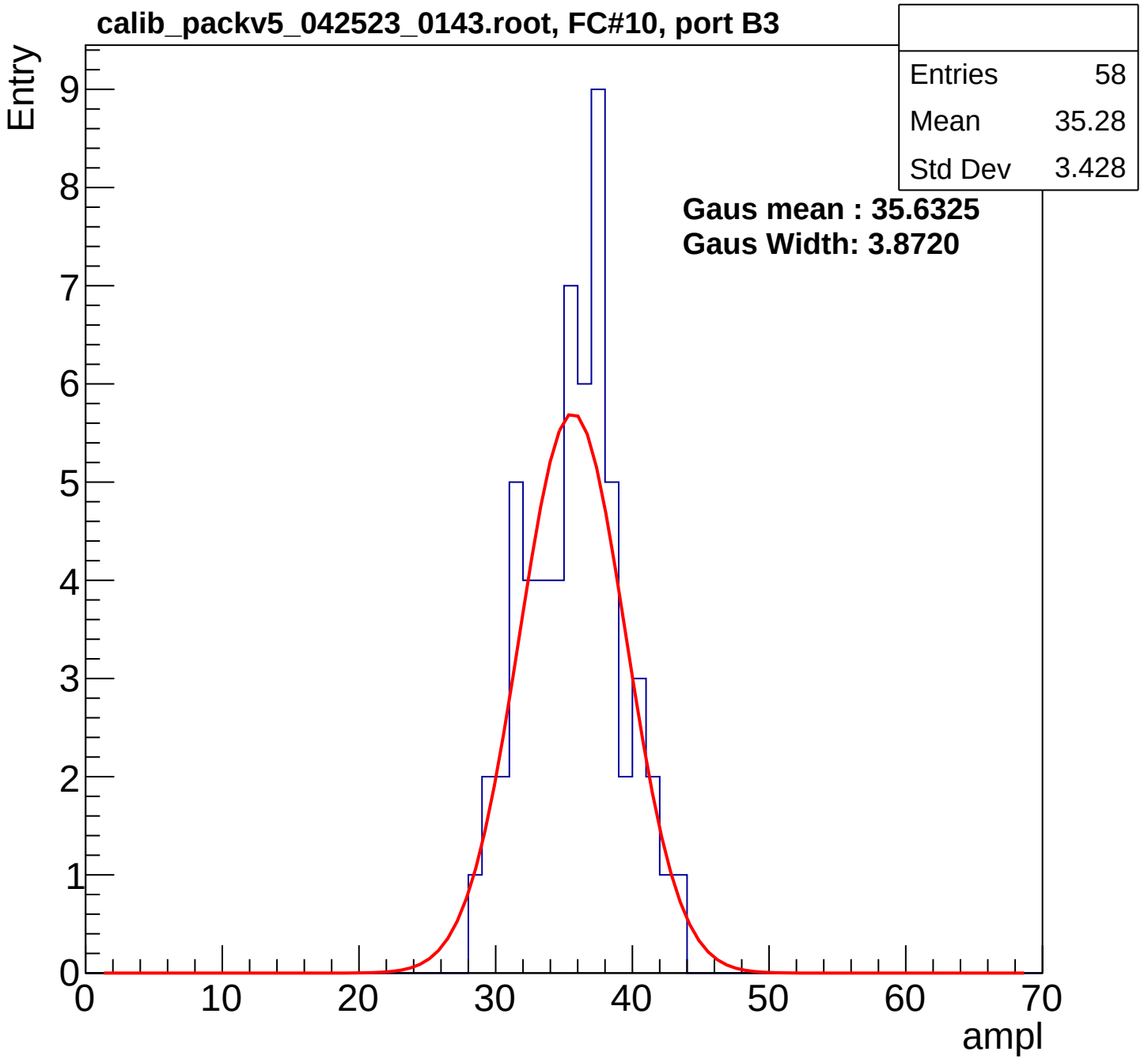
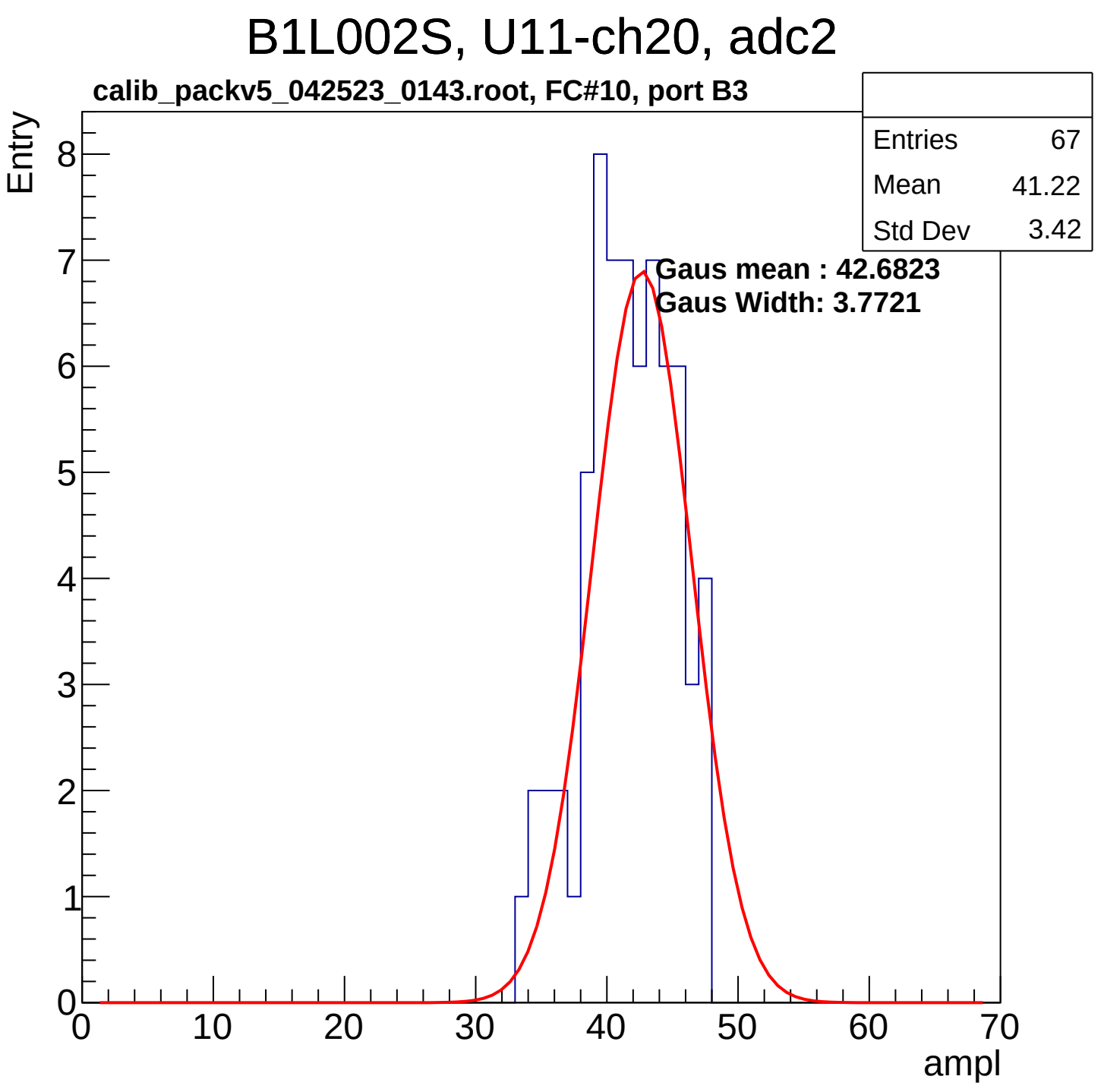

B1L002S, U11-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.28
Std Dev 3.428
Gaus mean : 35.6325
Gaus Width: 3.8720
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.22
Std Dev 3.42
Gaus mean : 42.6823
Gaus Width: 3.7721
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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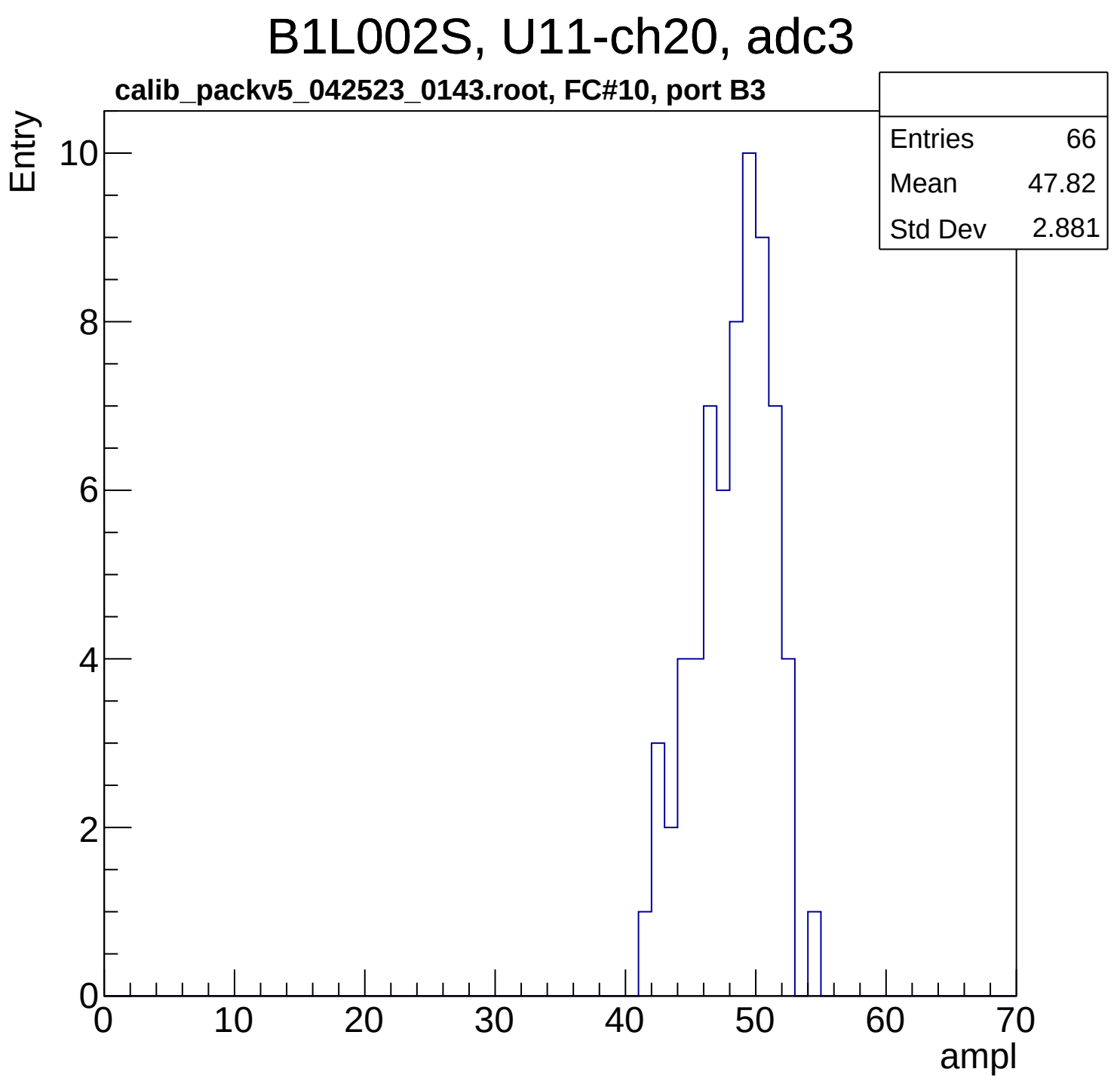
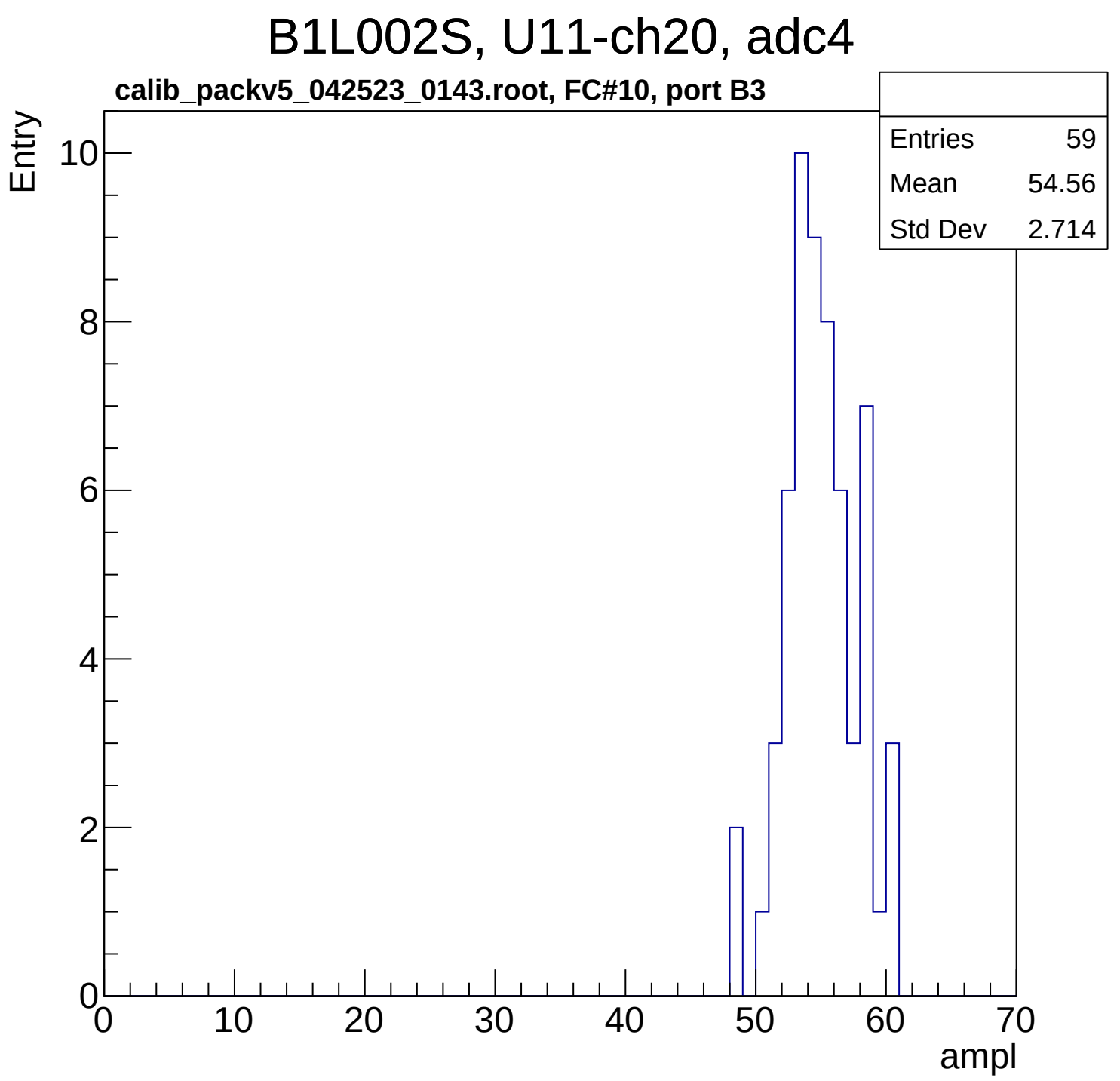
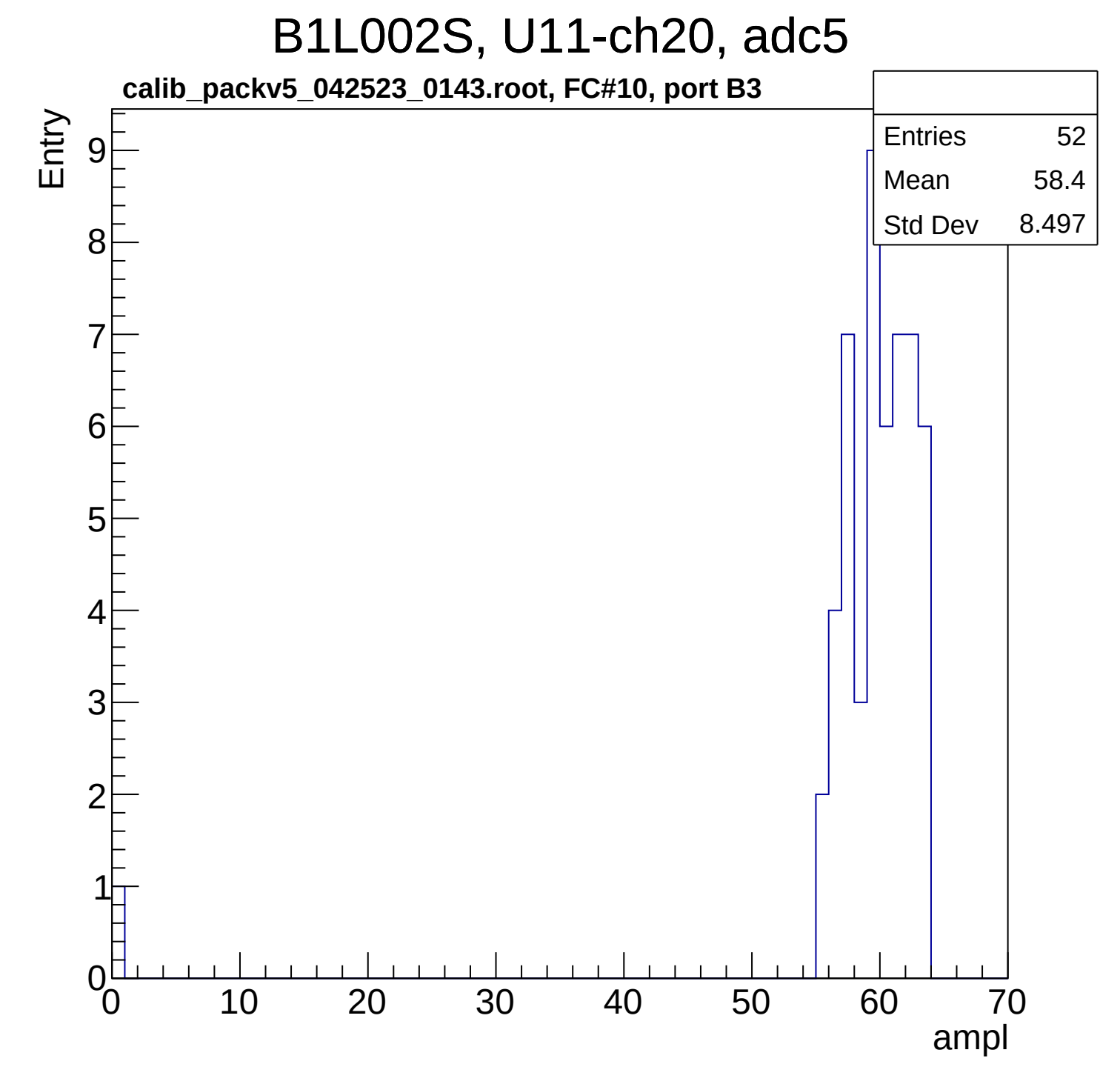

B1L002S, U11-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.82
Std Dev 2.881
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.56
Std Dev 2.714
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.4
Std Dev 8.497
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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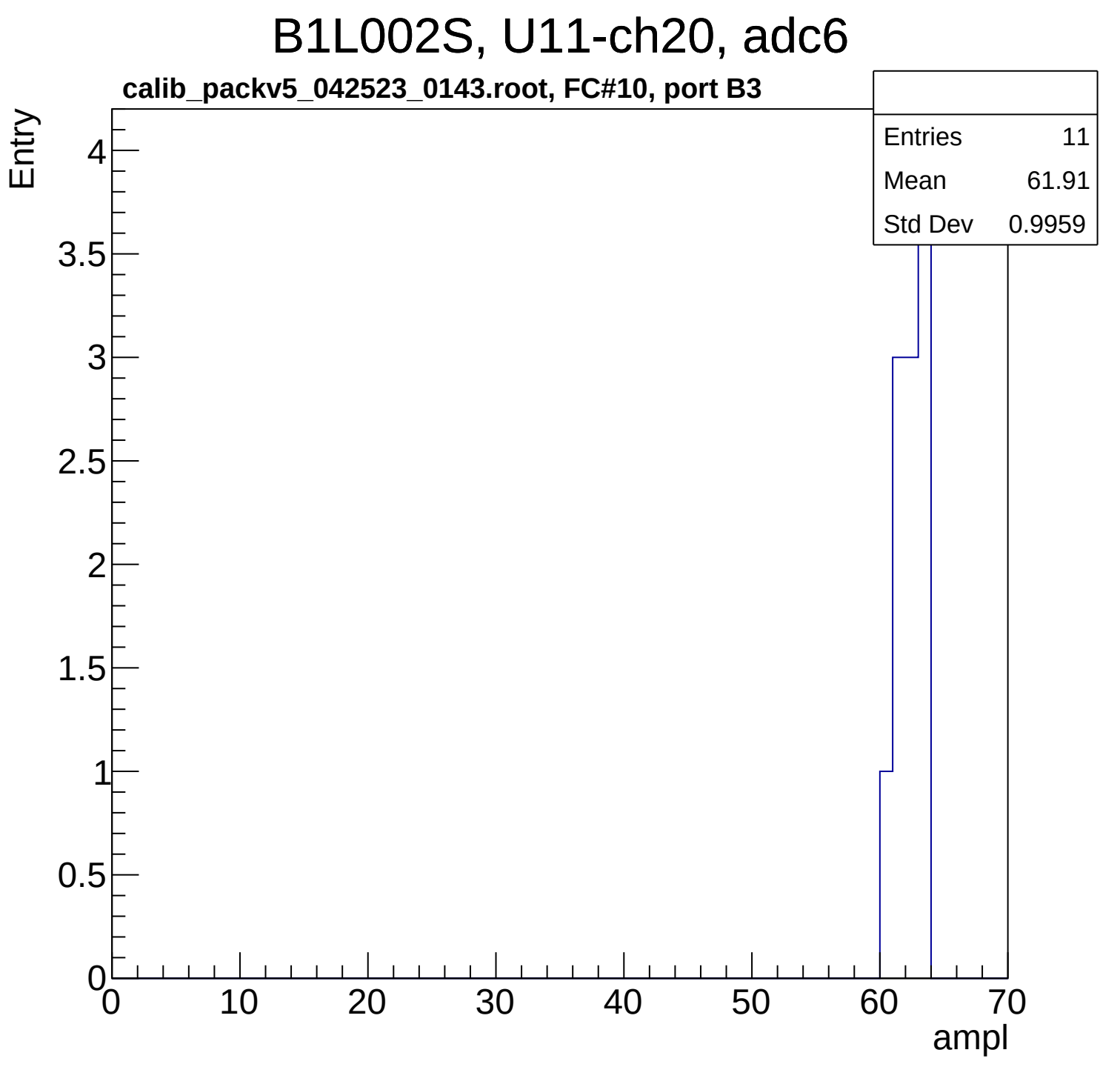
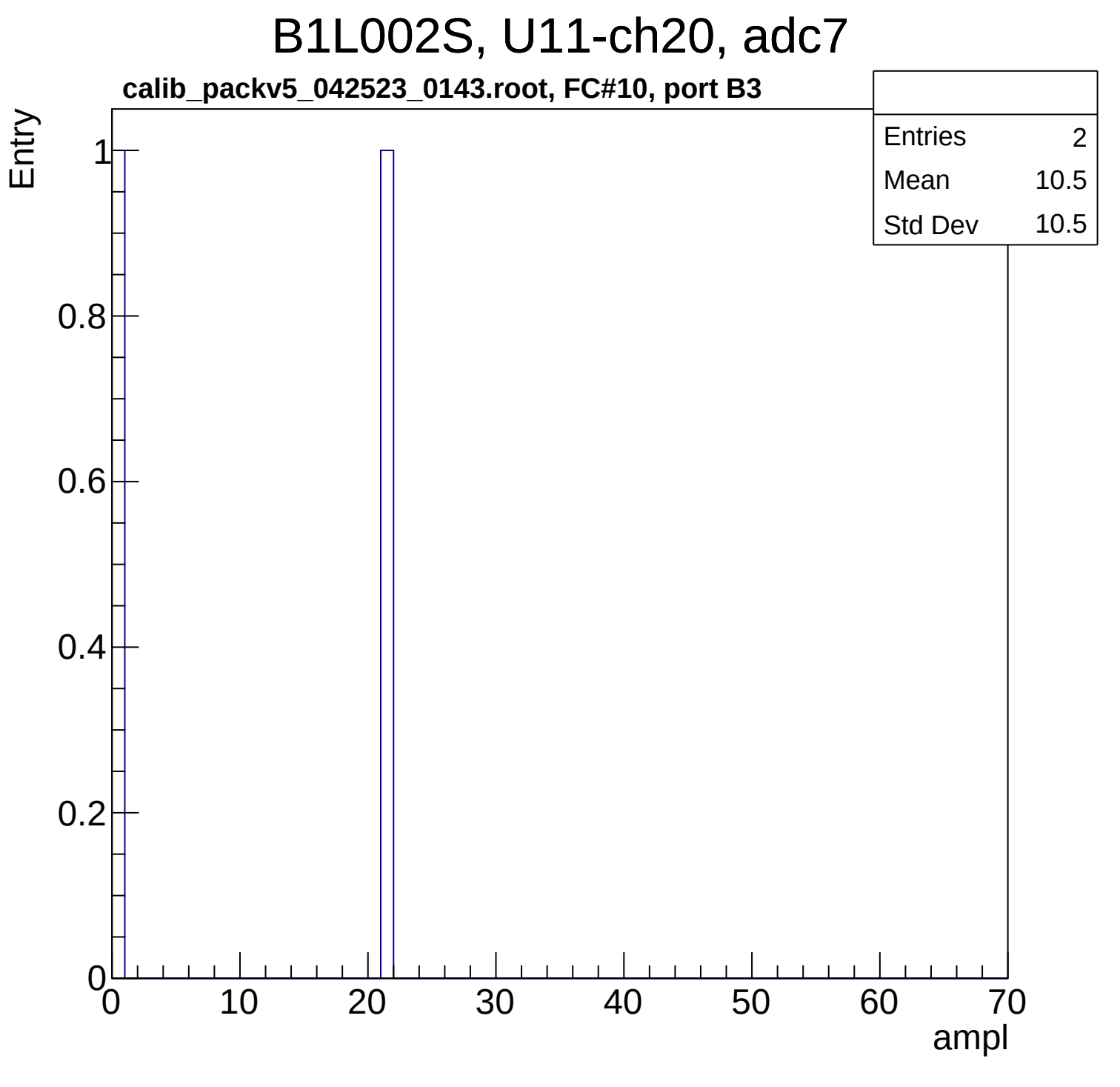
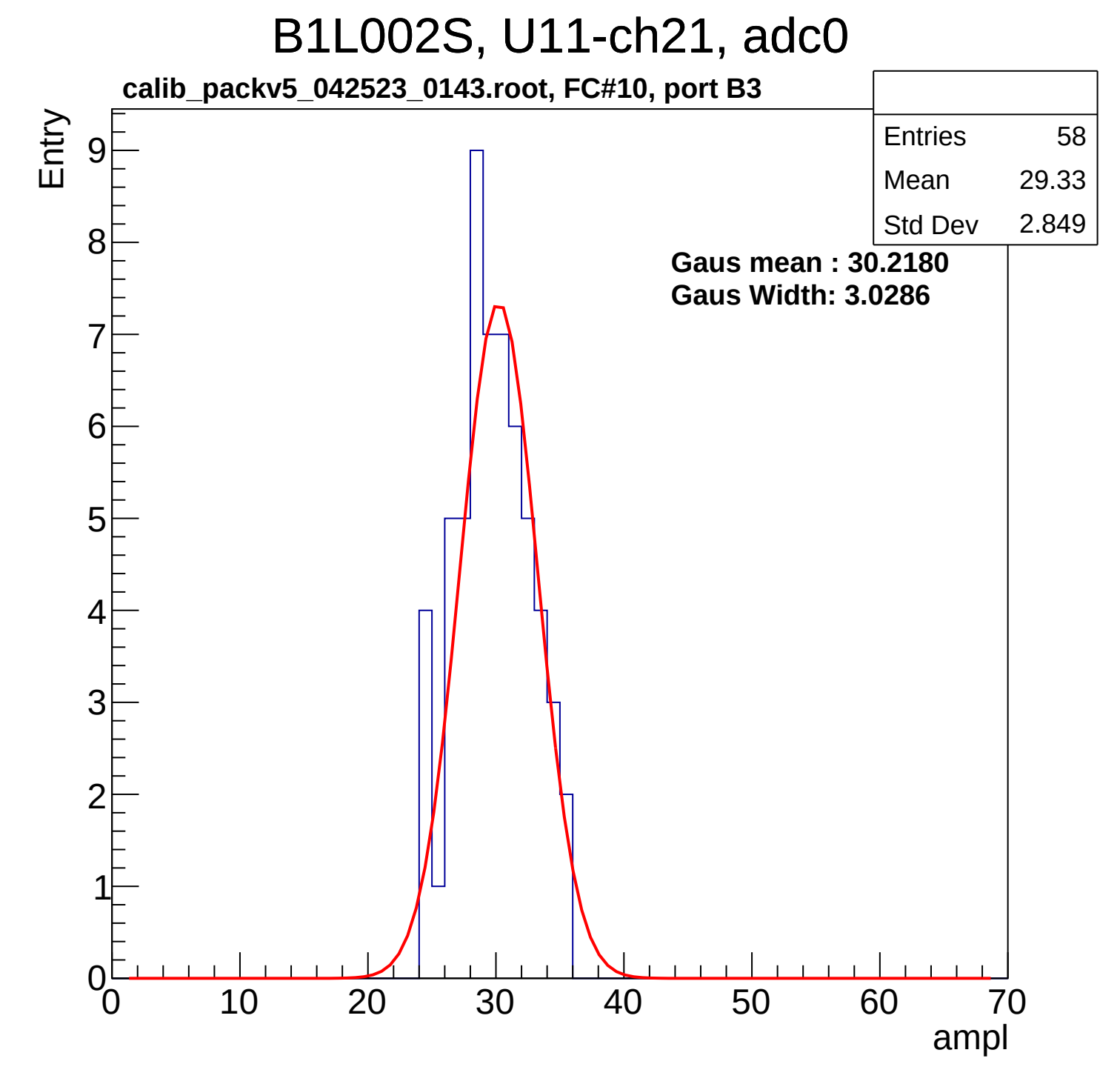
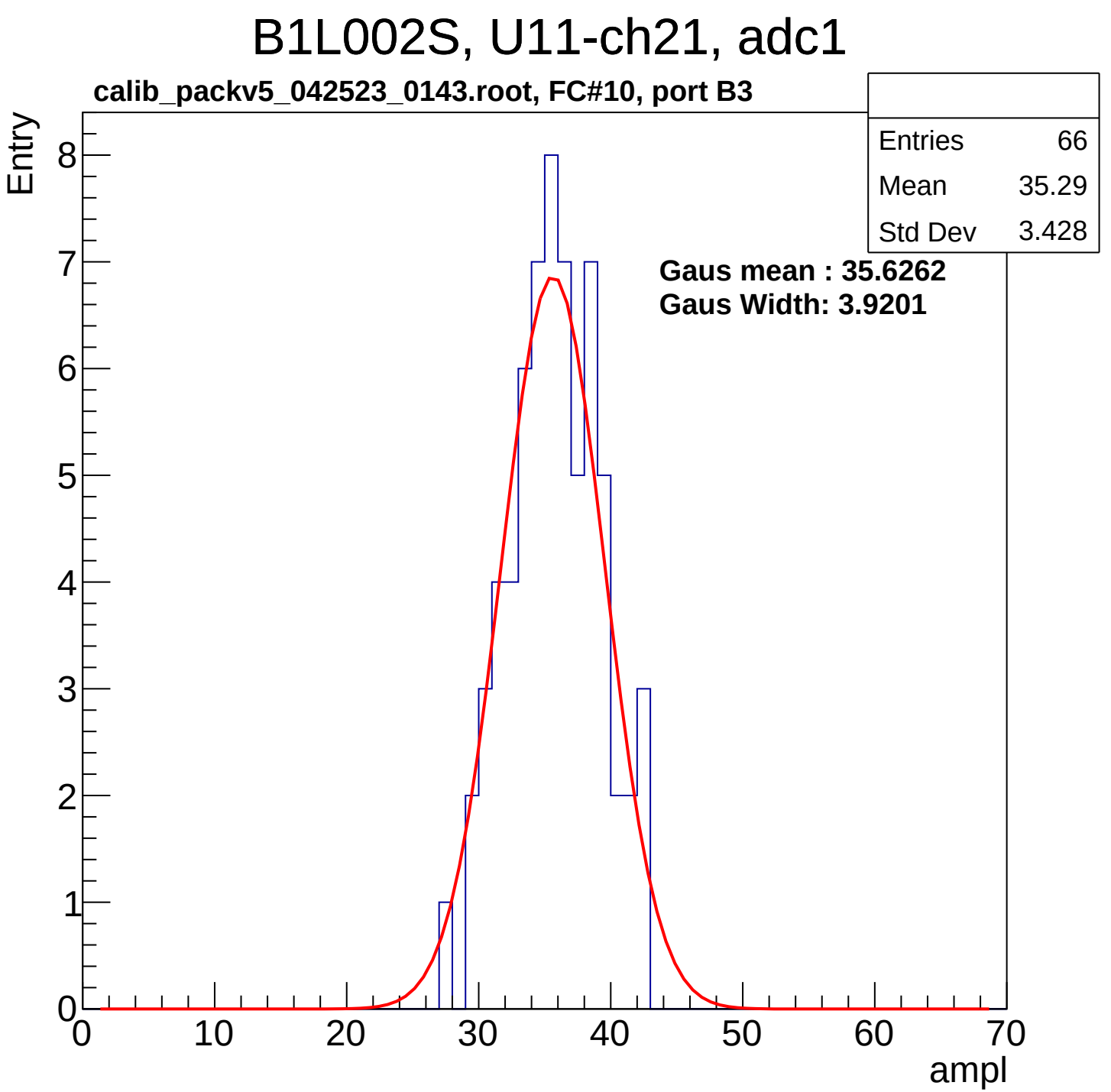
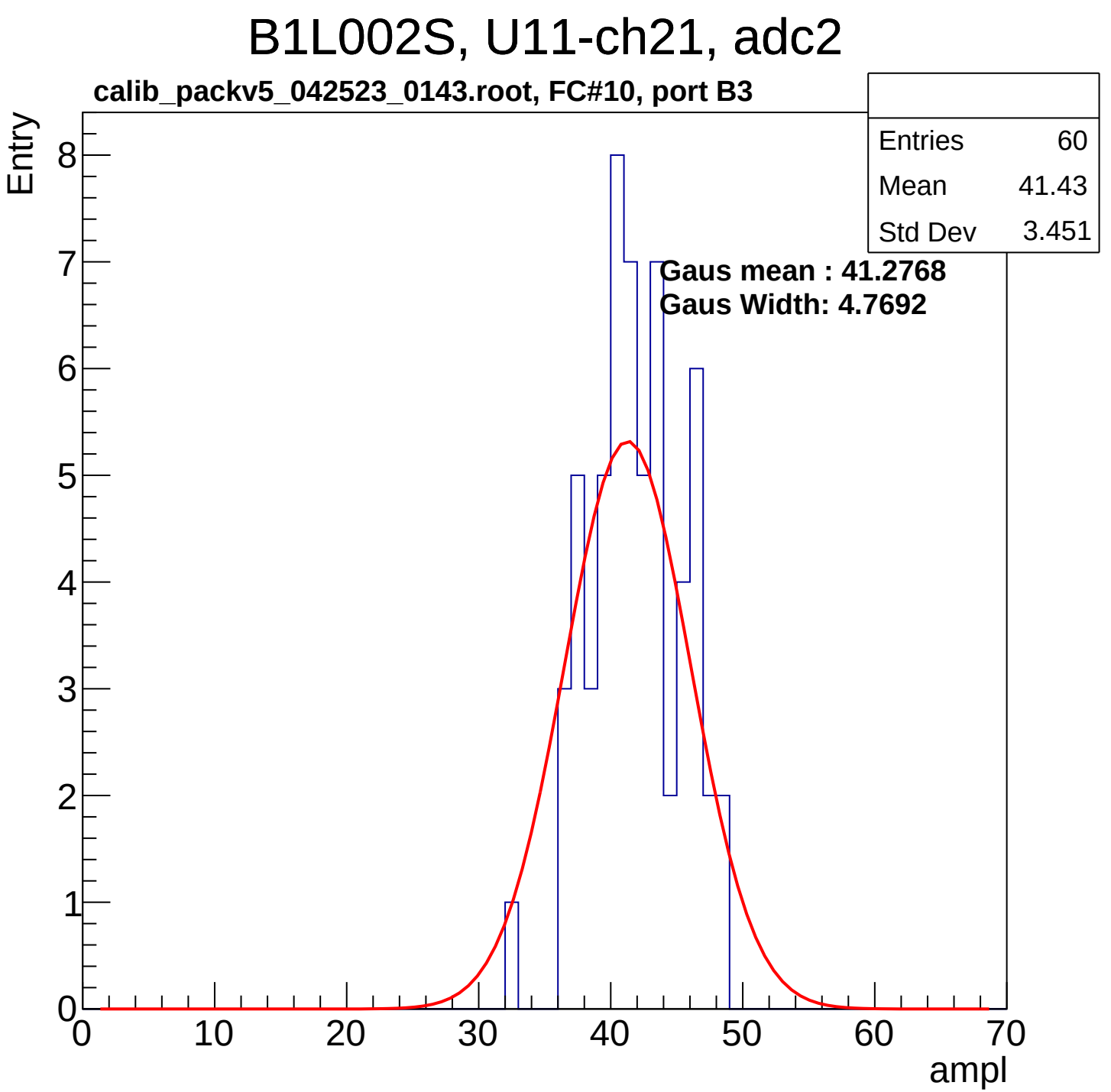
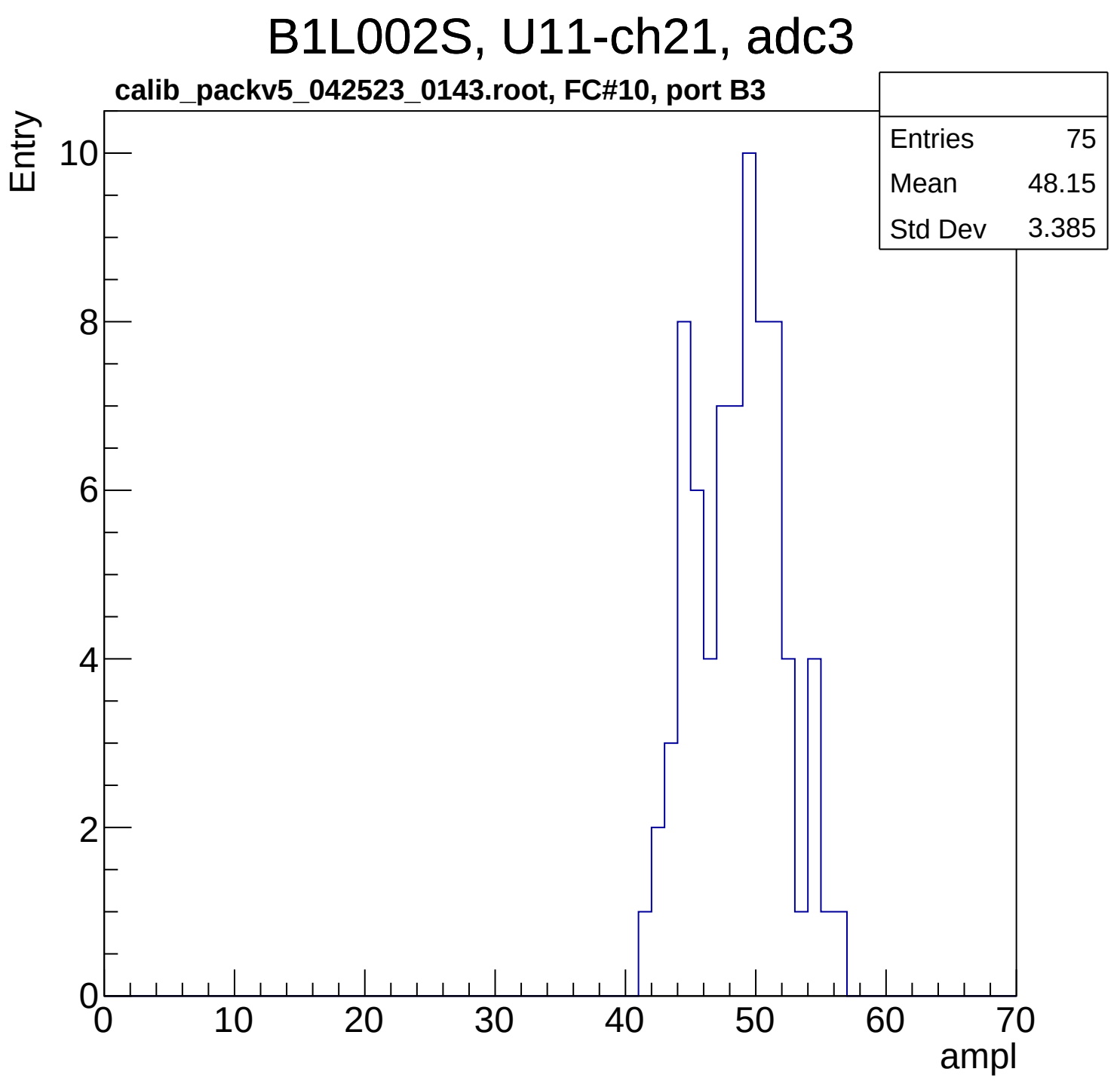

B1L002S, U11-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.91
Std Dev 0.9959
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

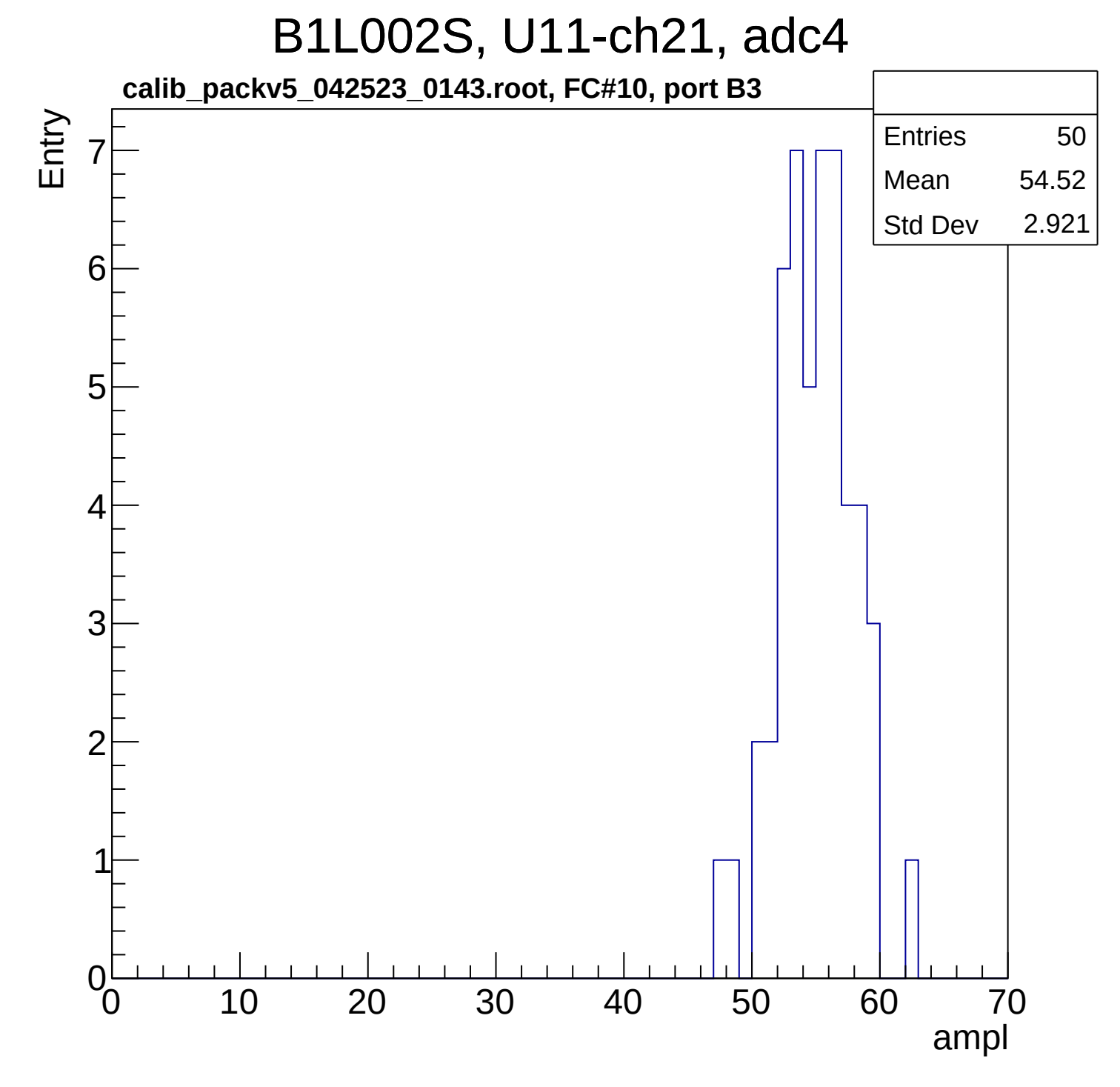

B1L002S, U11-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

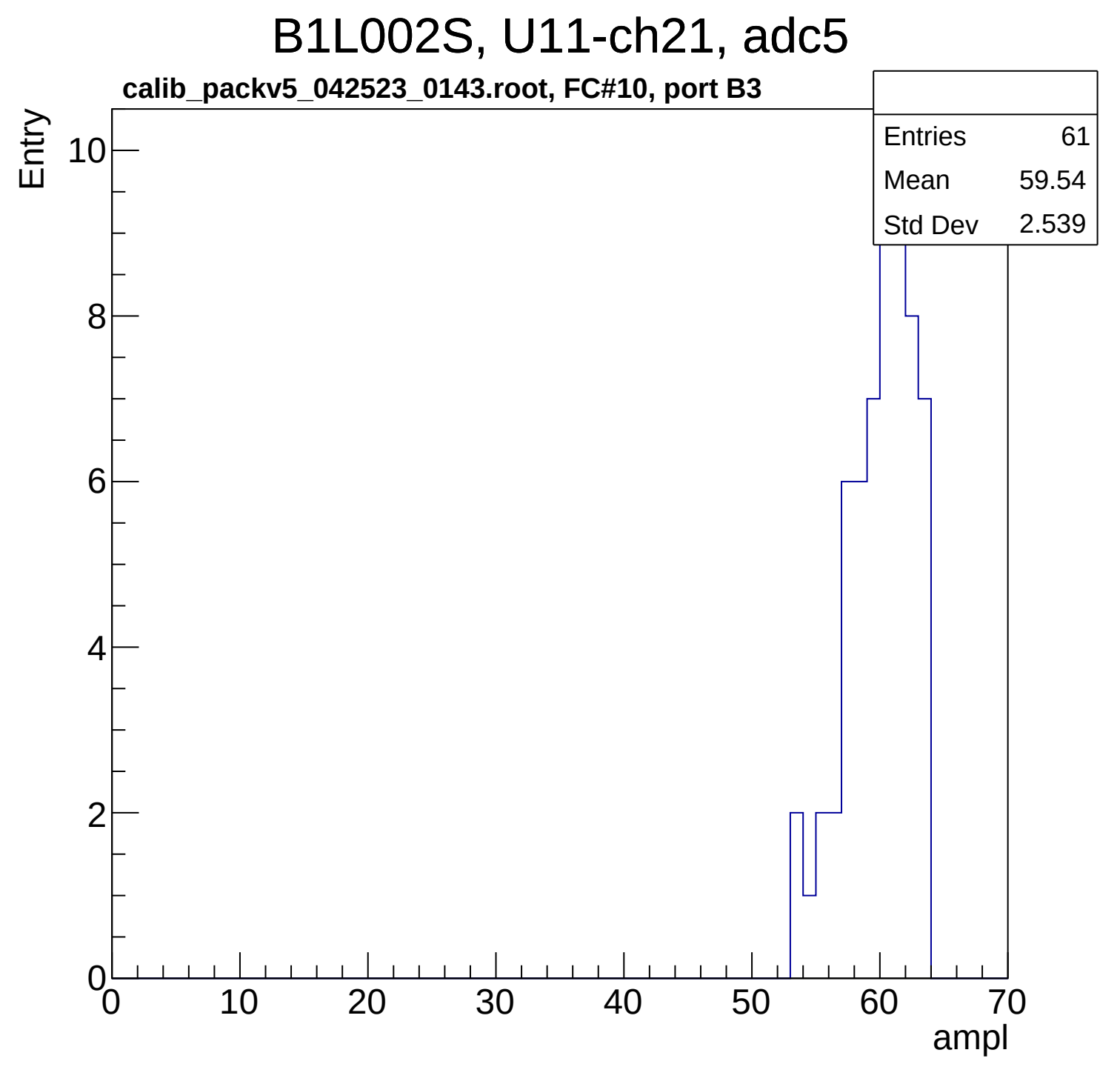

B1L002S, U11-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 29.33
Std Dev 2.849
Gaus mean : 30.2180
Gaus Width: 3.0286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35.29
Std Dev 3.428
Gaus mean : 35.6262
Gaus Width: 3.9201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.43
Std Dev 3.451
Gaus mean : 41.2768
Gaus Width: 4.7692
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 48.15
Std Dev 3.385
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 54.52
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 59.54
Std Dev 2.539
Entry
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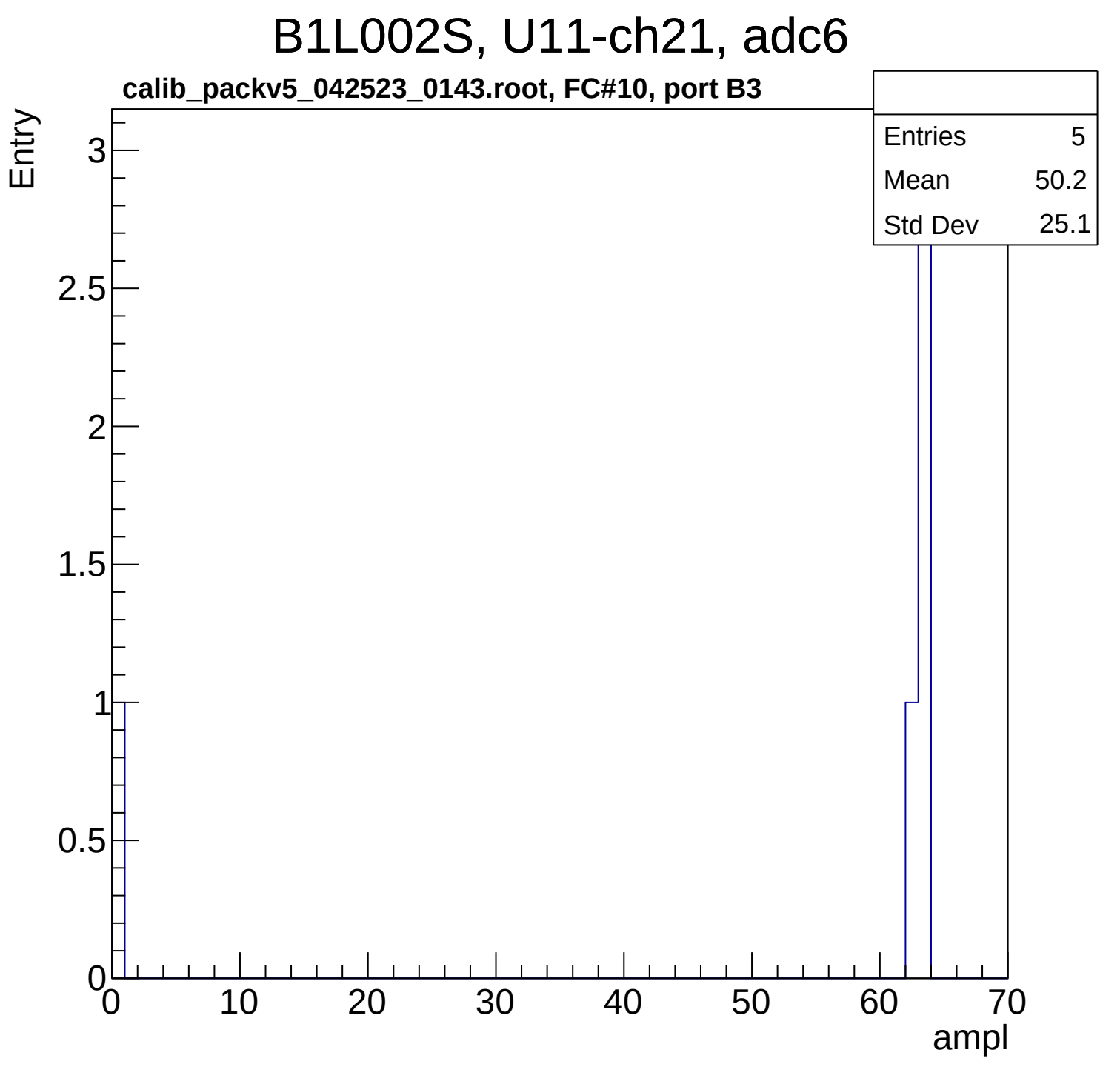
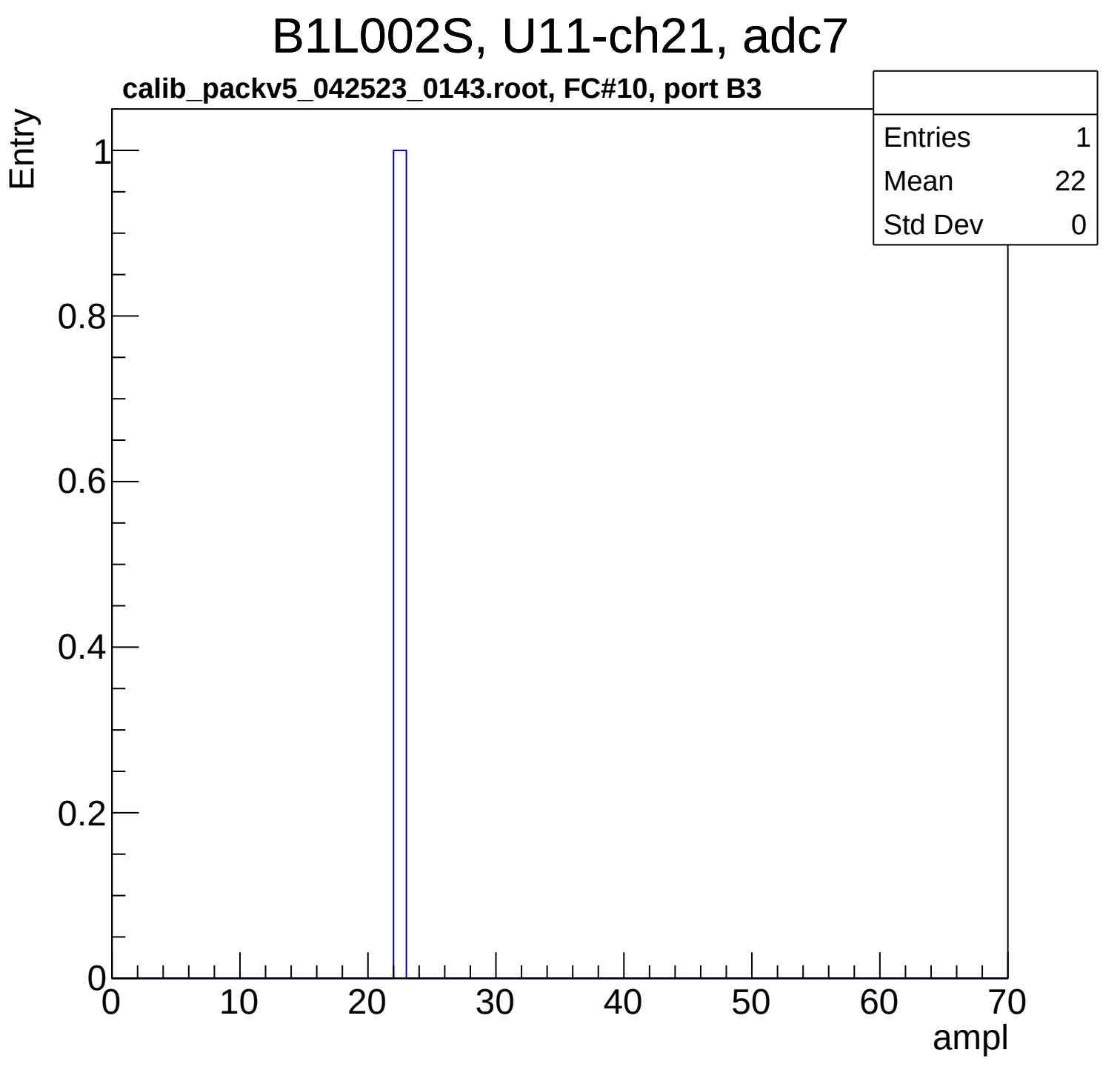
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 50.2
Std Dev 25.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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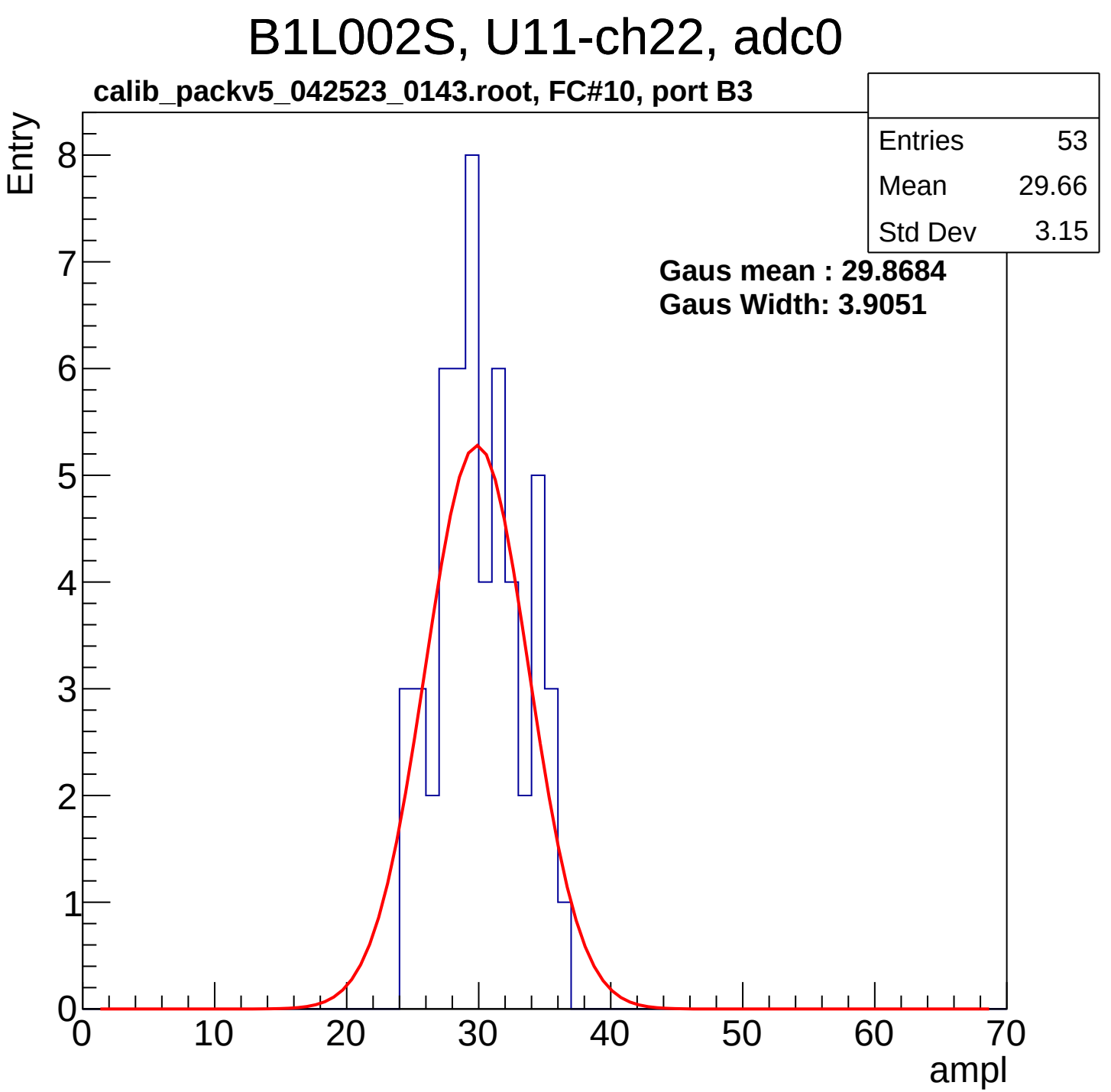
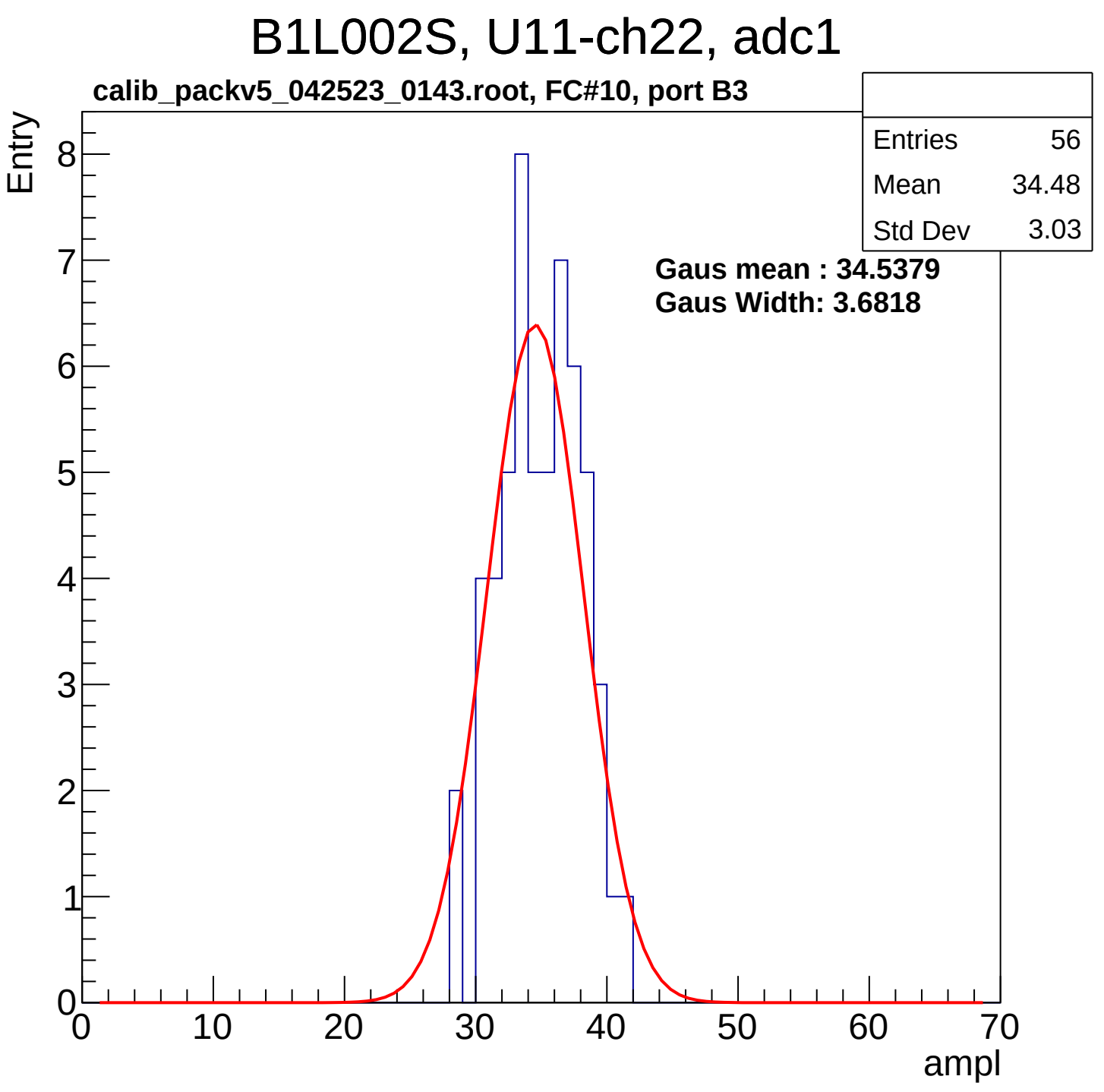
50
60
70
ampl


B1L002S, U11-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 29.66
Std Dev 3.15
Gaus mean : 29.8684
Gaus Width: 3.9051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.48
Std Dev 3.03
Gaus mean : 34.5379
Gaus Width: 3.6818
Entry
ampl
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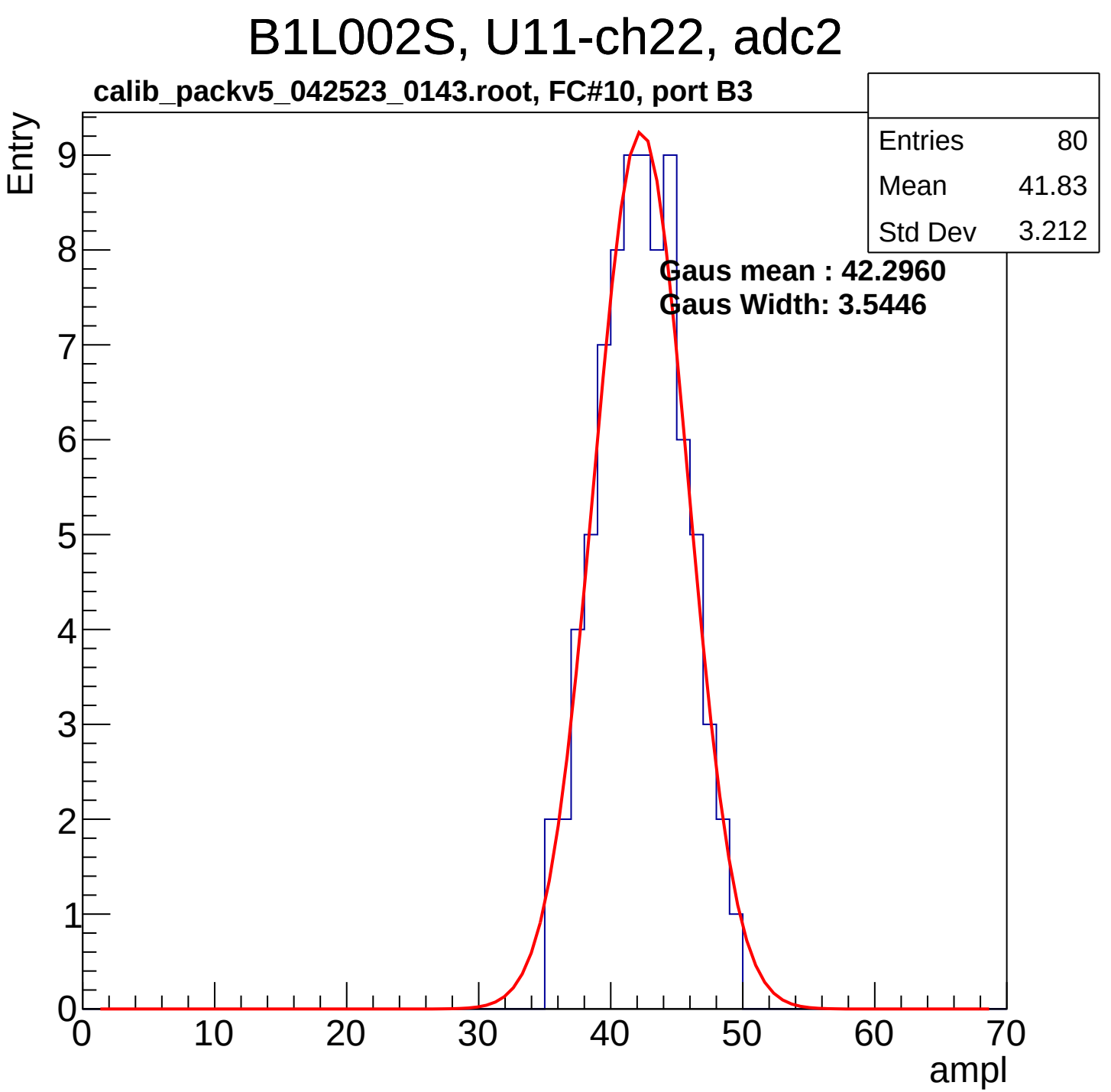
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 41.83
Std Dev 3.212
Gaus mean : 42.2960
Gaus Width: 3.5446
Entry
ampl
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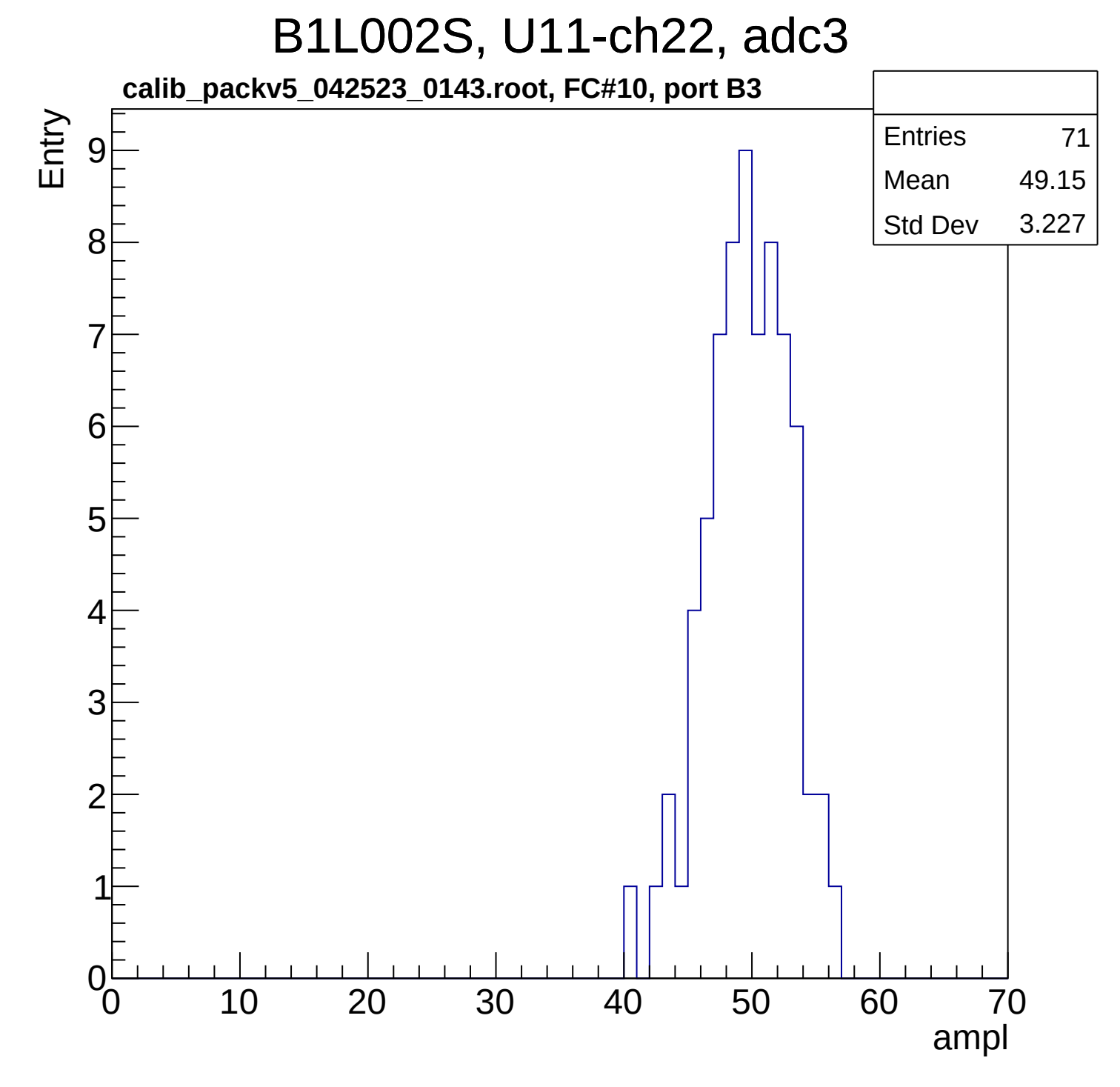
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 49.15
Std Dev 3.227
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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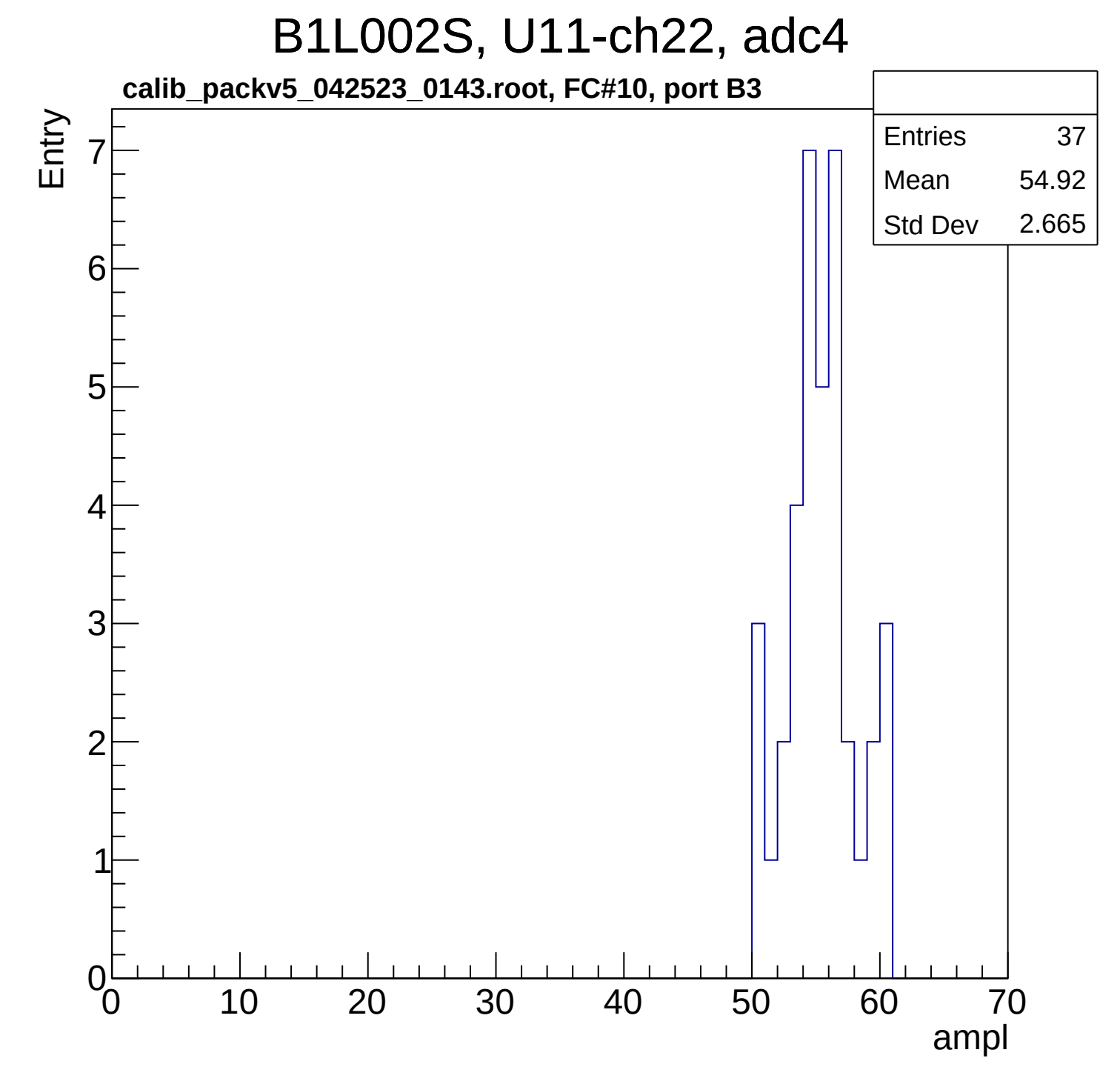
B1L002S, U11-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 54.92
Std Dev 2.665
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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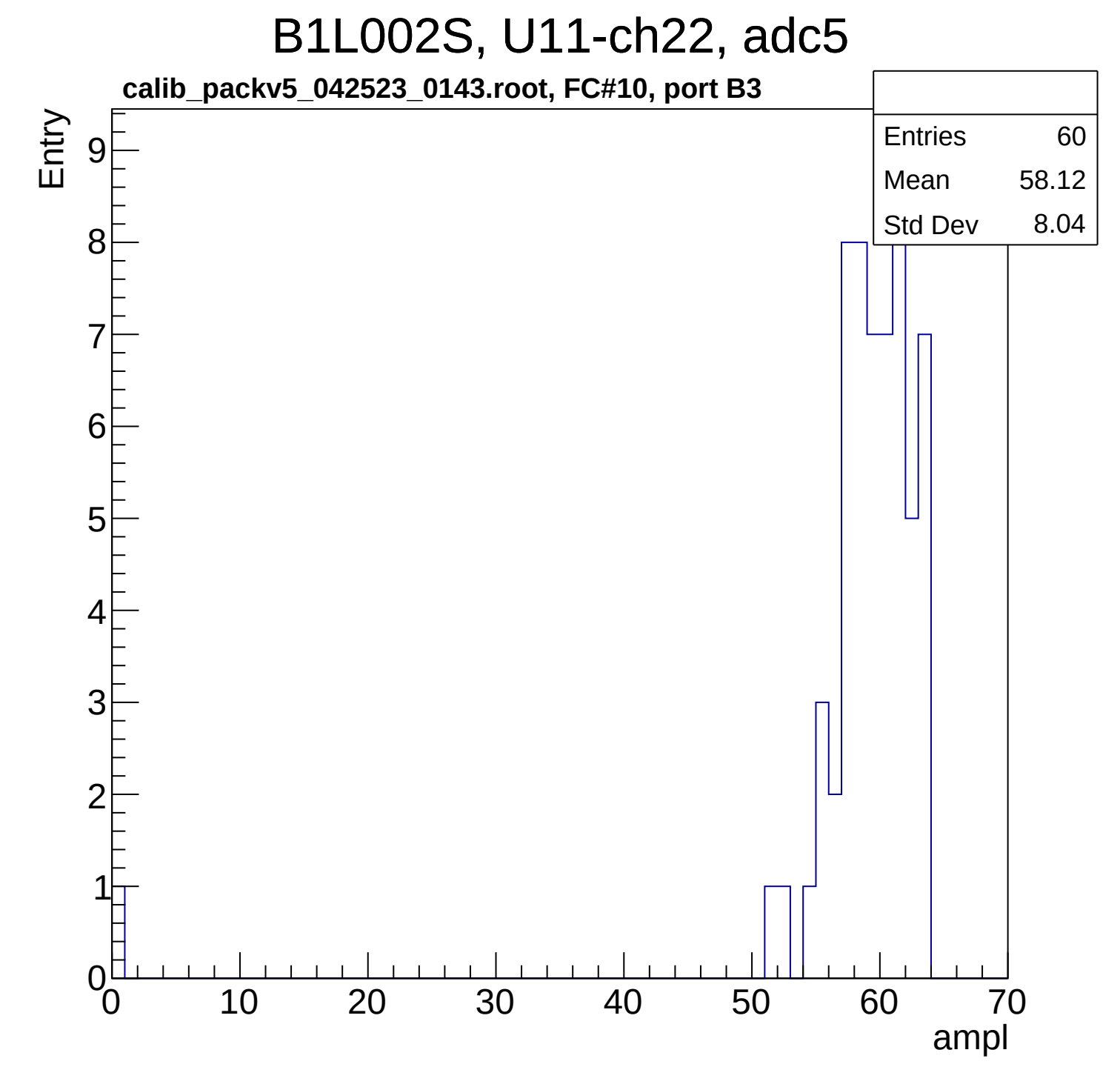
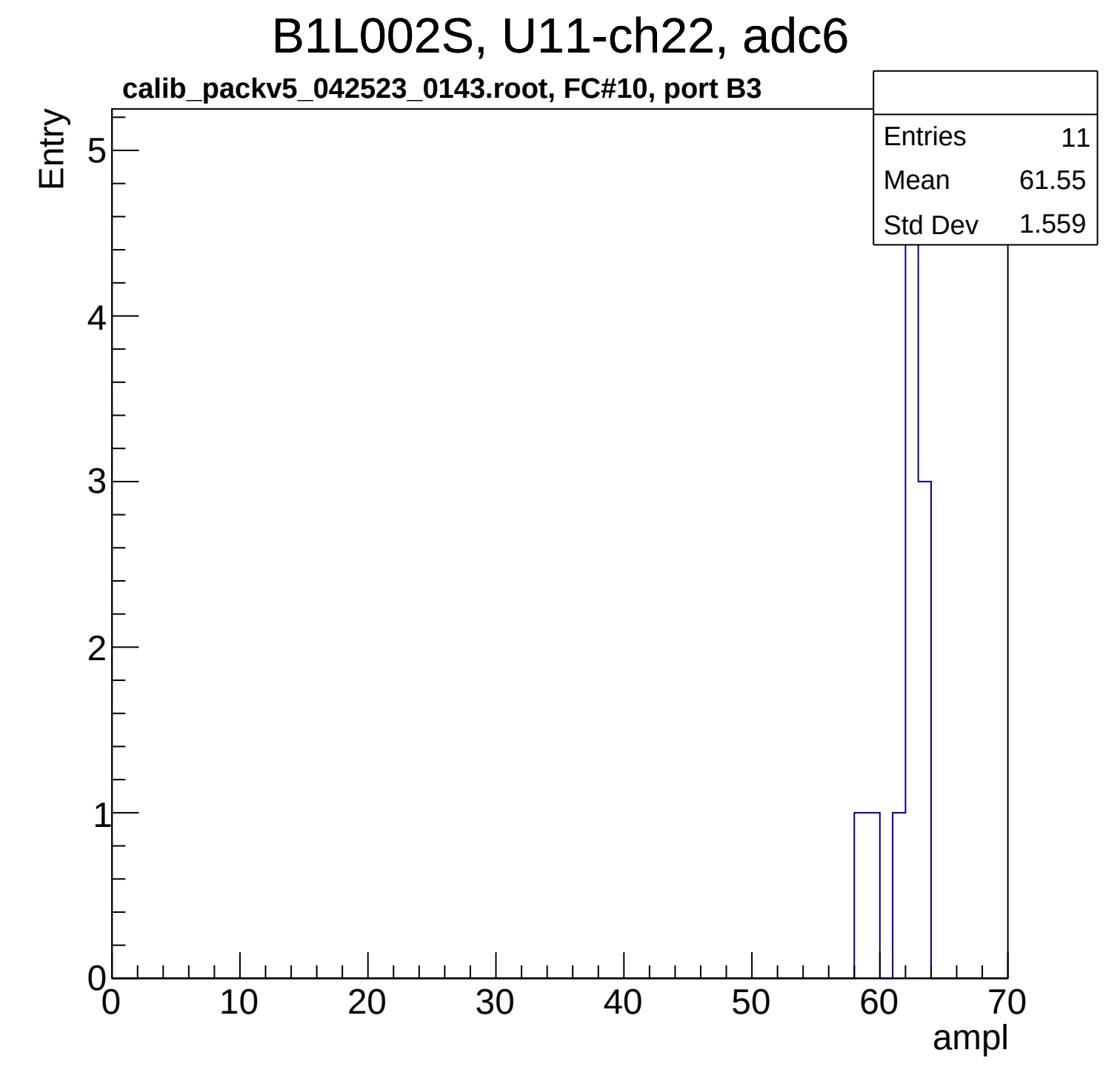
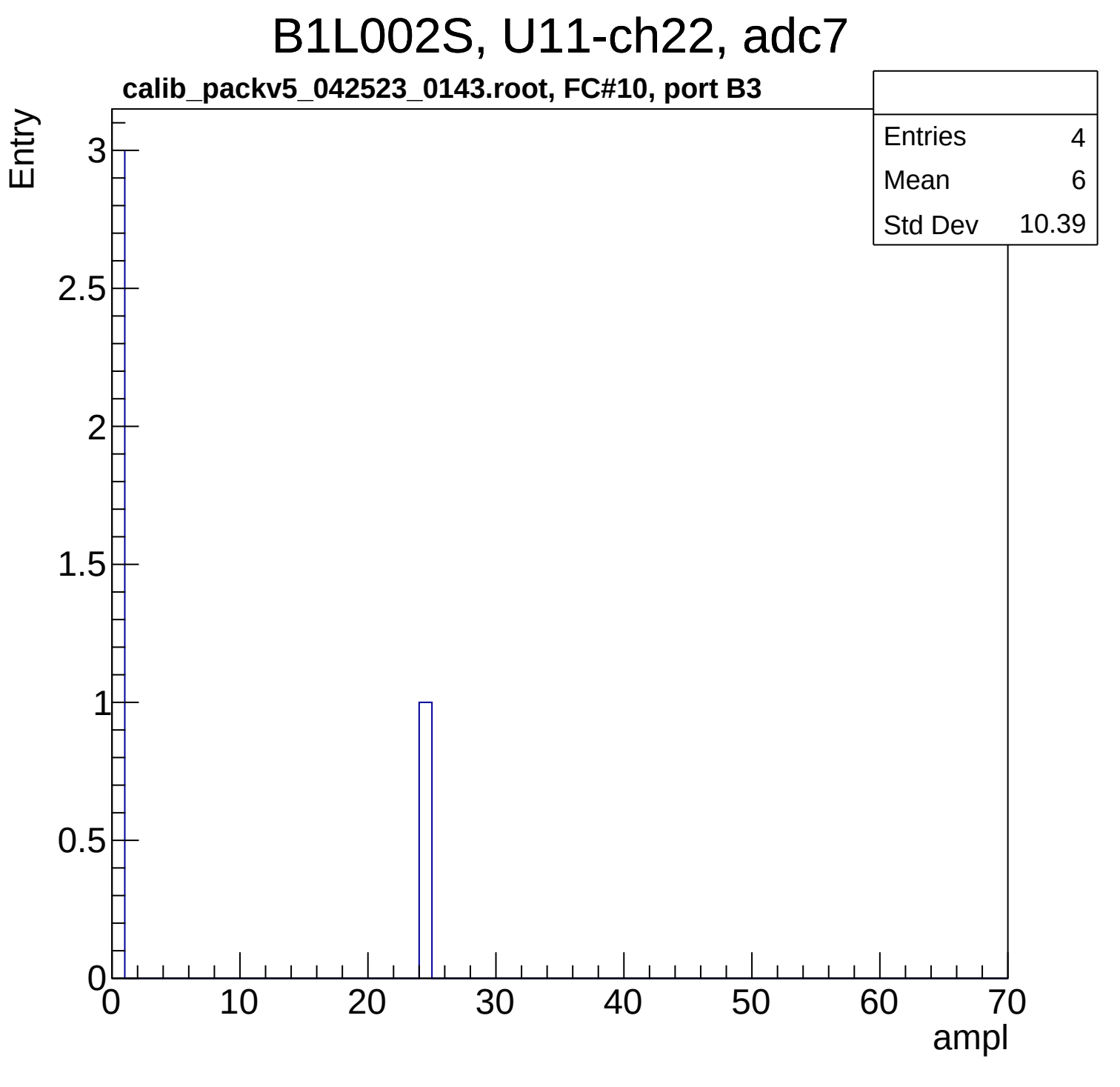

B1L002S, U11-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.12
Std Dev 8.04
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.55
Std Dev 1.559
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 6
Std Dev 10.39
Entry
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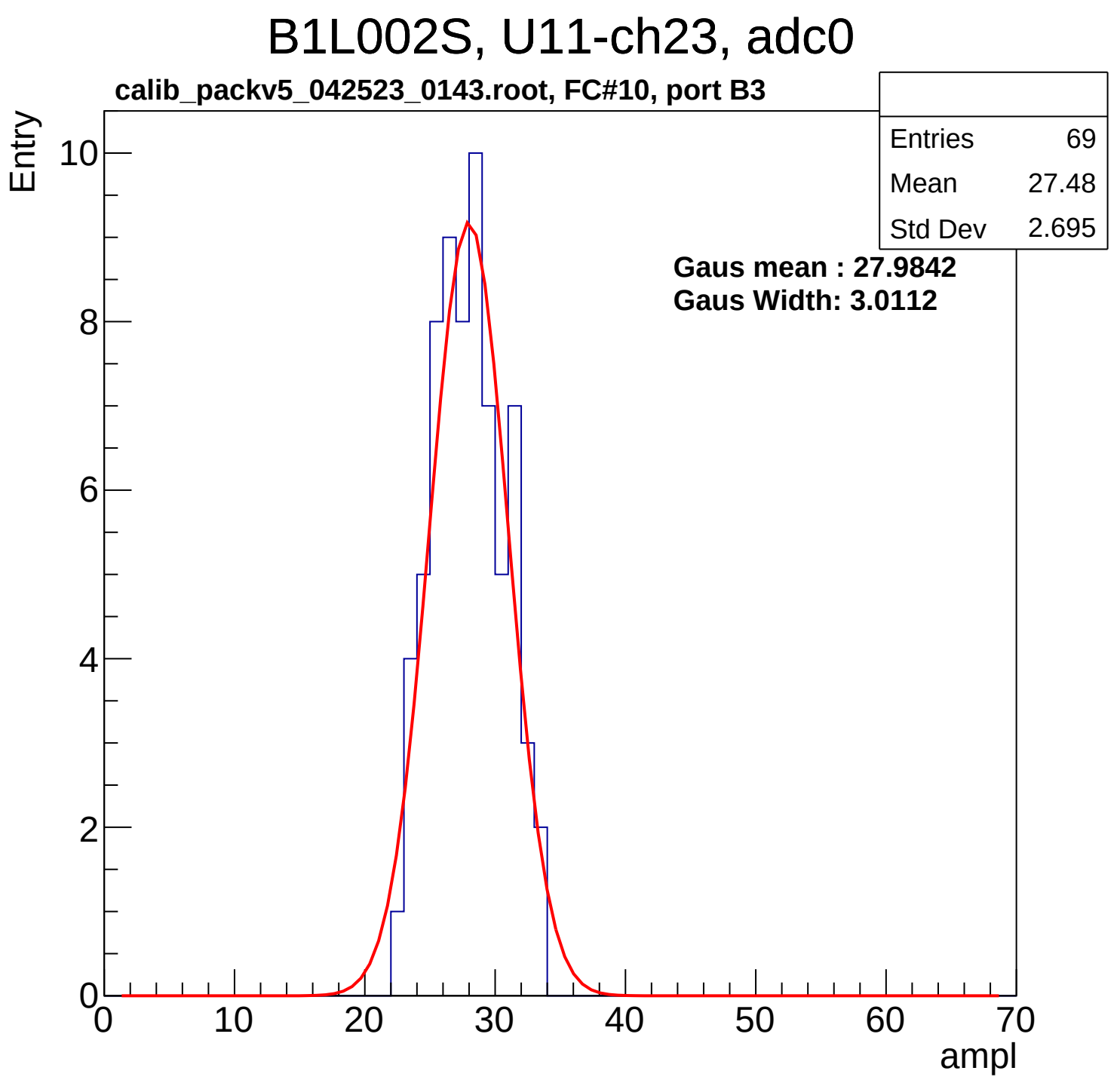
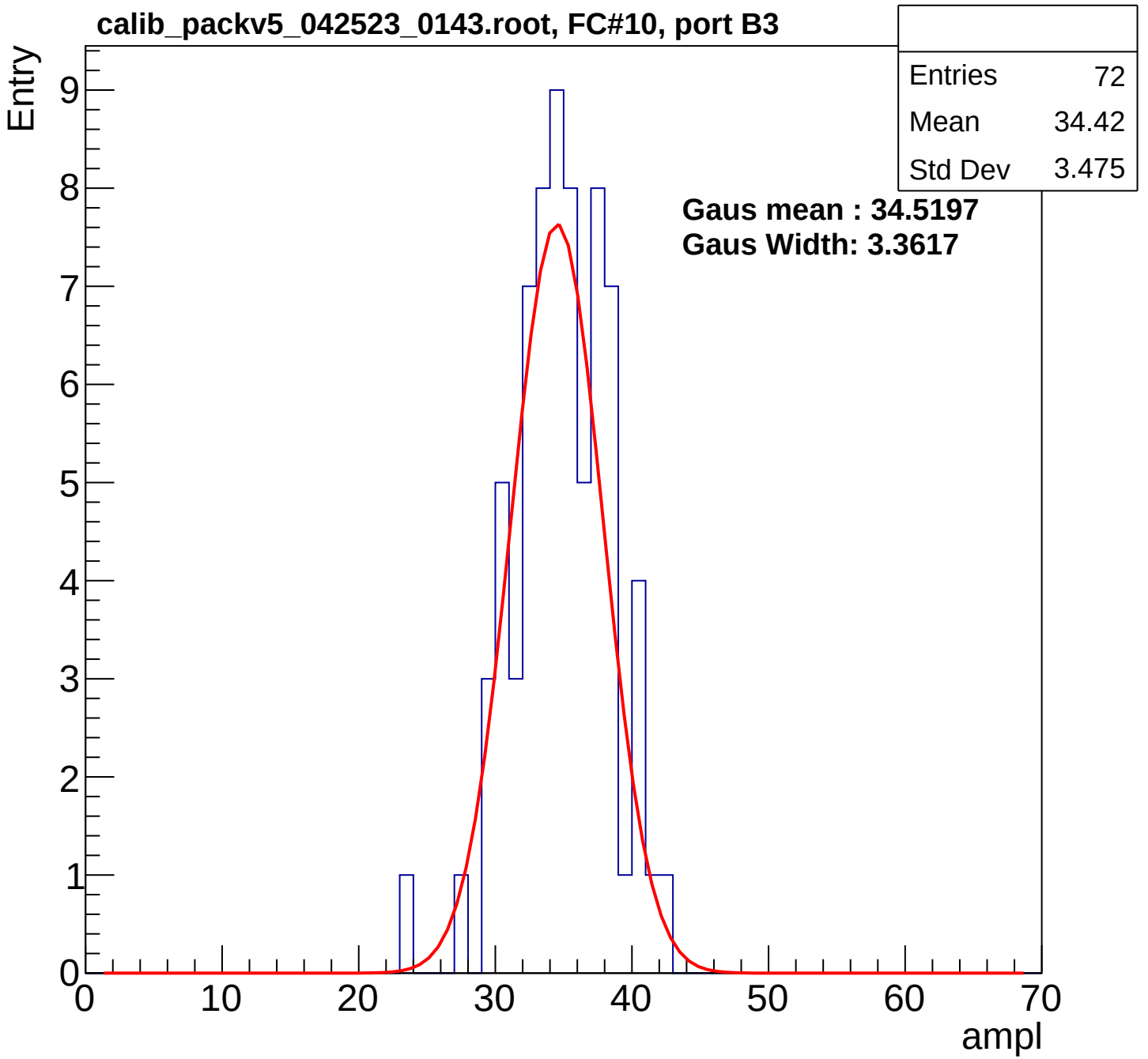
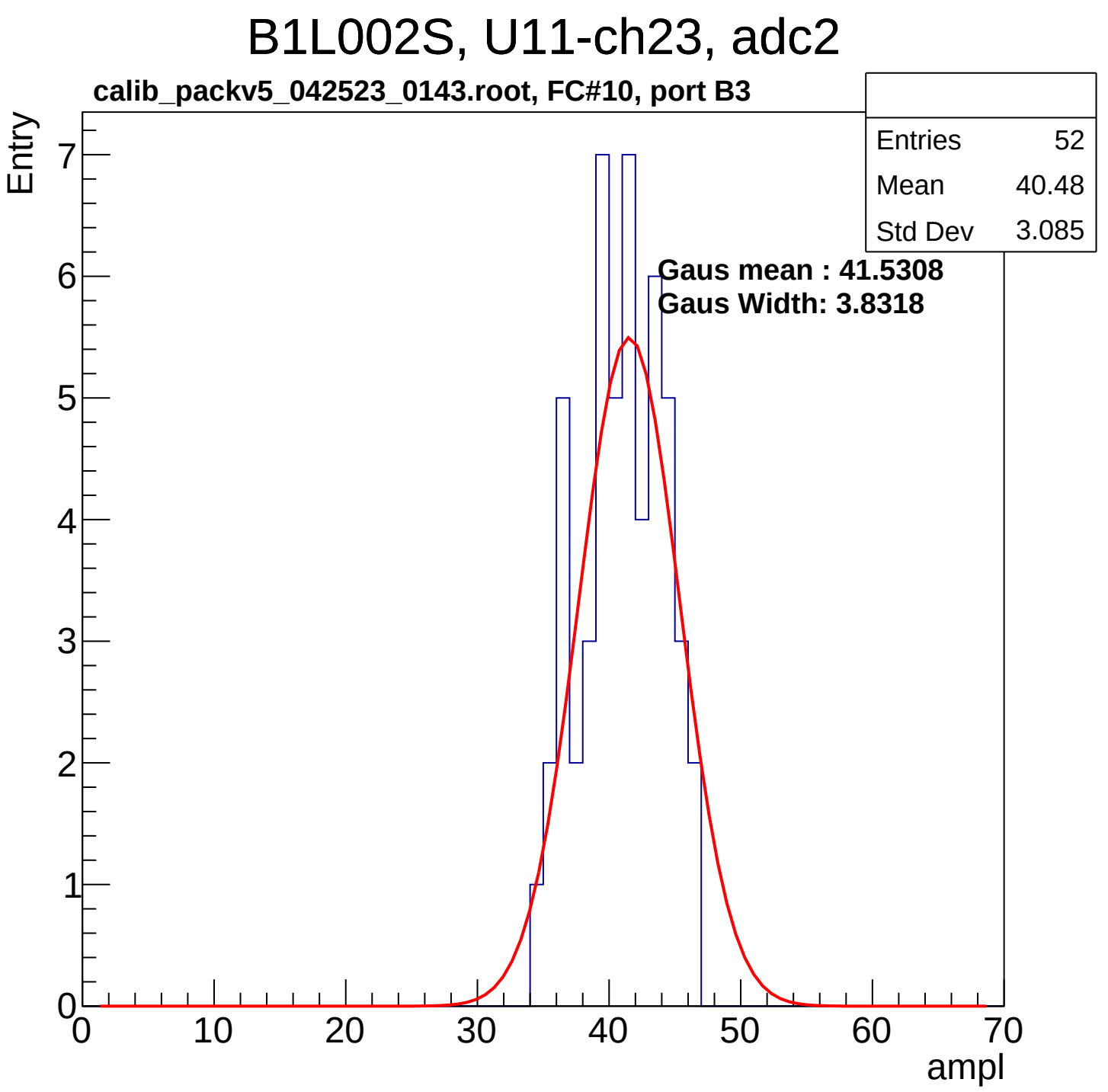
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.48
Std Dev 2.695
Gaus mean : 27.9842
Gaus Width: 3.0112
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.42
Std Dev 3.475
Gaus mean : 34.5197
Gaus Width: 3.3617
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.48
Std Dev 3.085
Gaus mean : 41.5308
Gaus Width: 3.8318
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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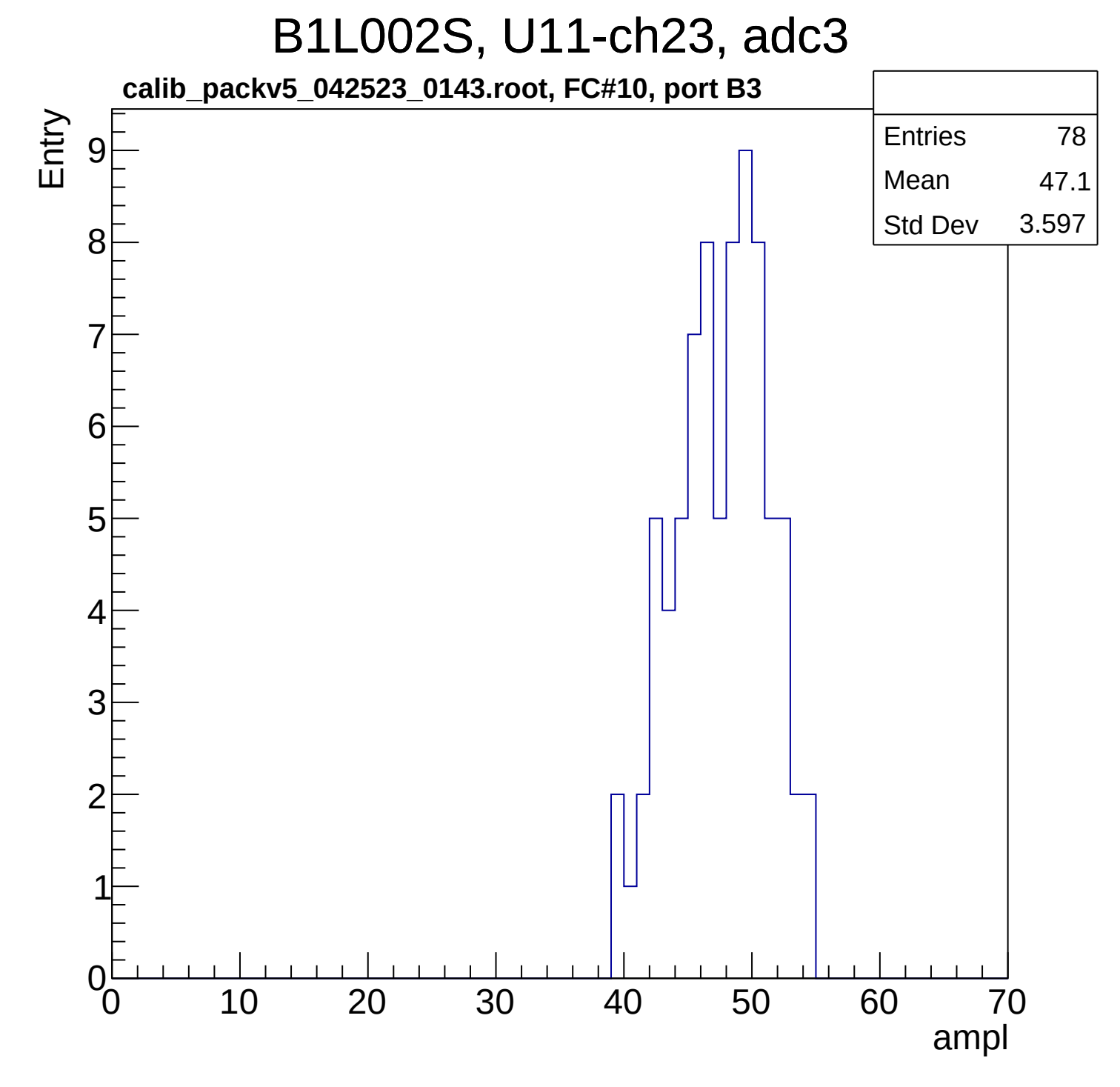
B1L002S, U11-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 47.1
Std Dev 3.597
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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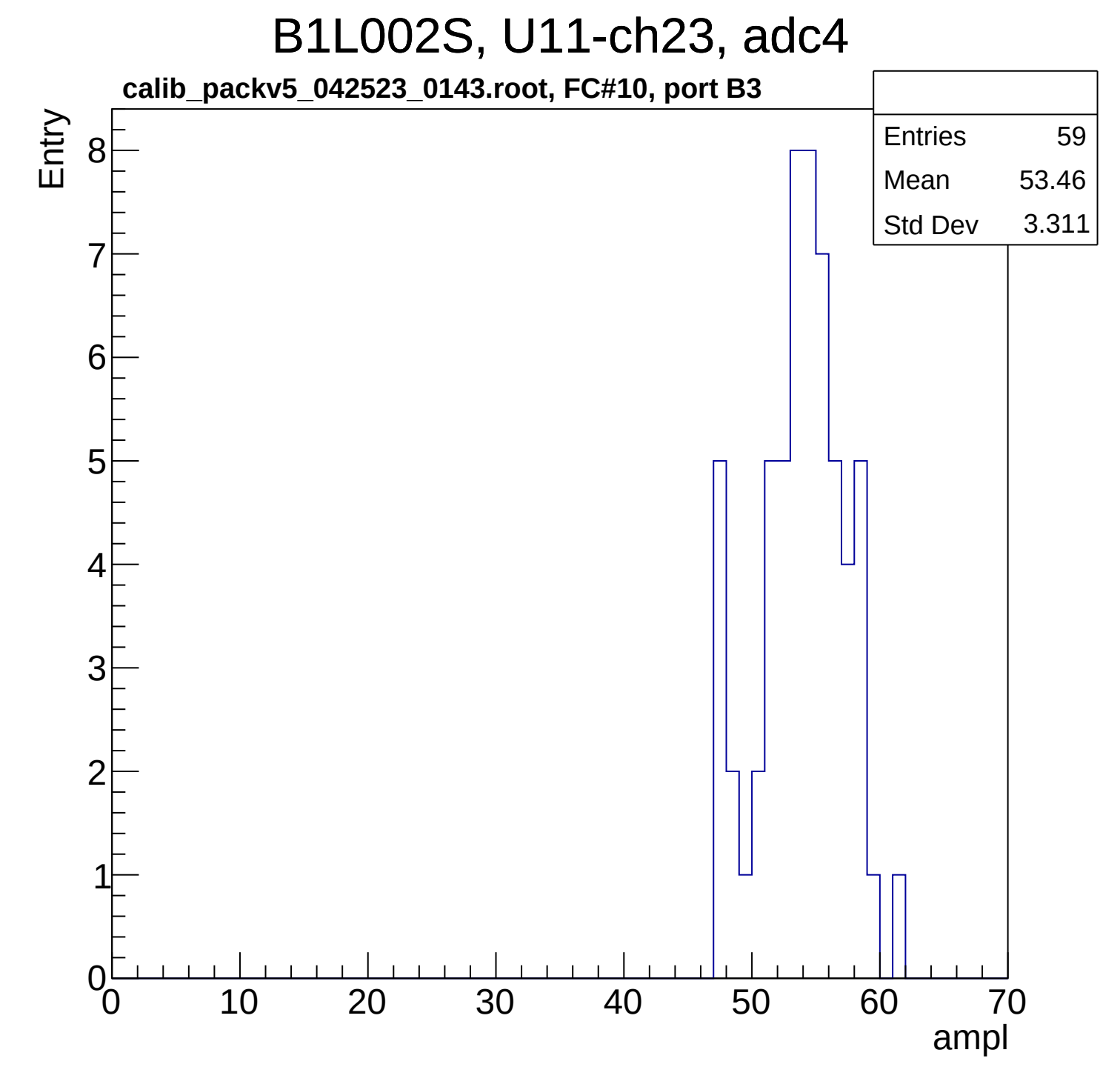
B1L002S, U11-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.46
Std Dev 3.311
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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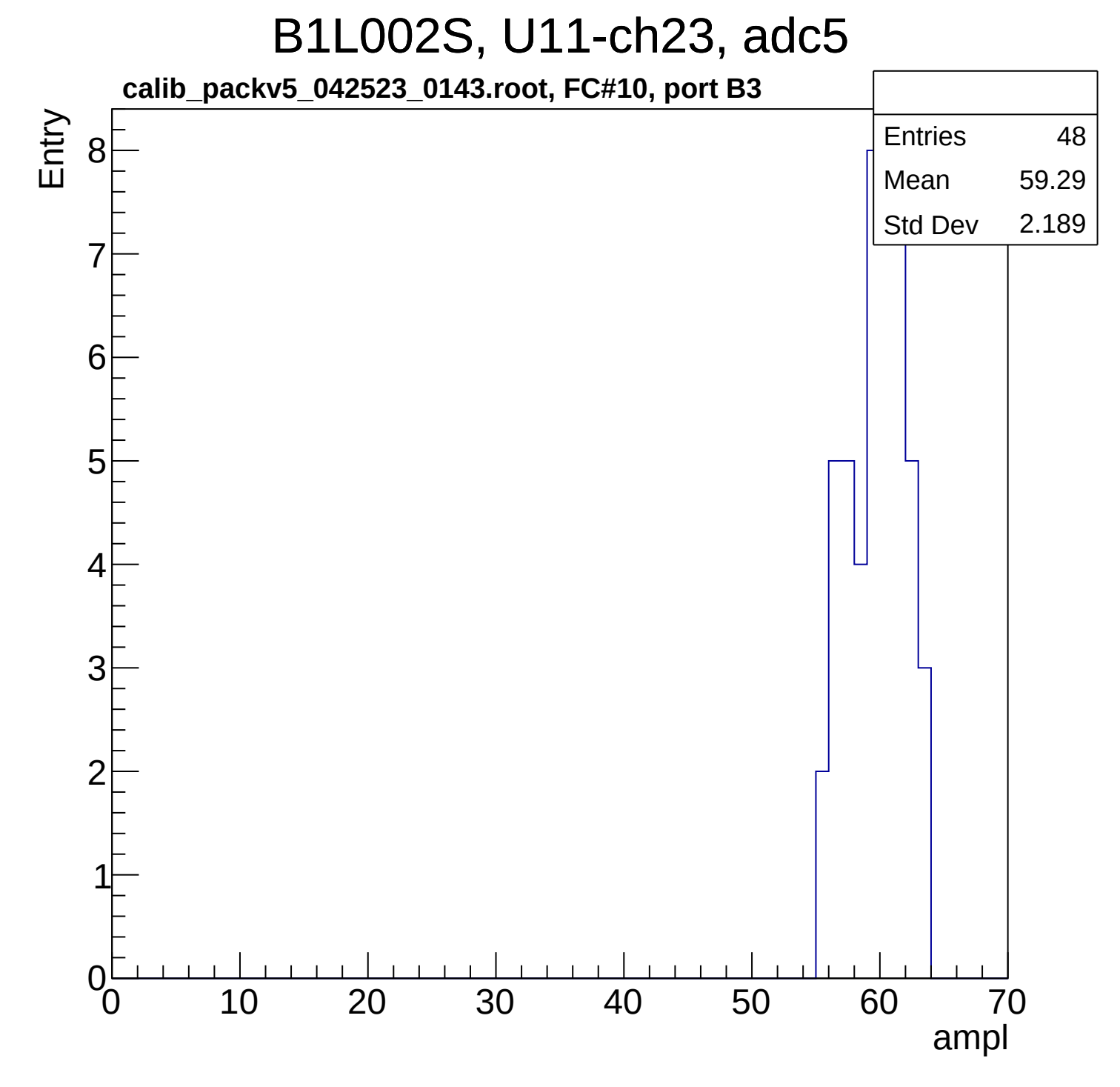
B1L002S, U11-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.29
Std Dev 2.189
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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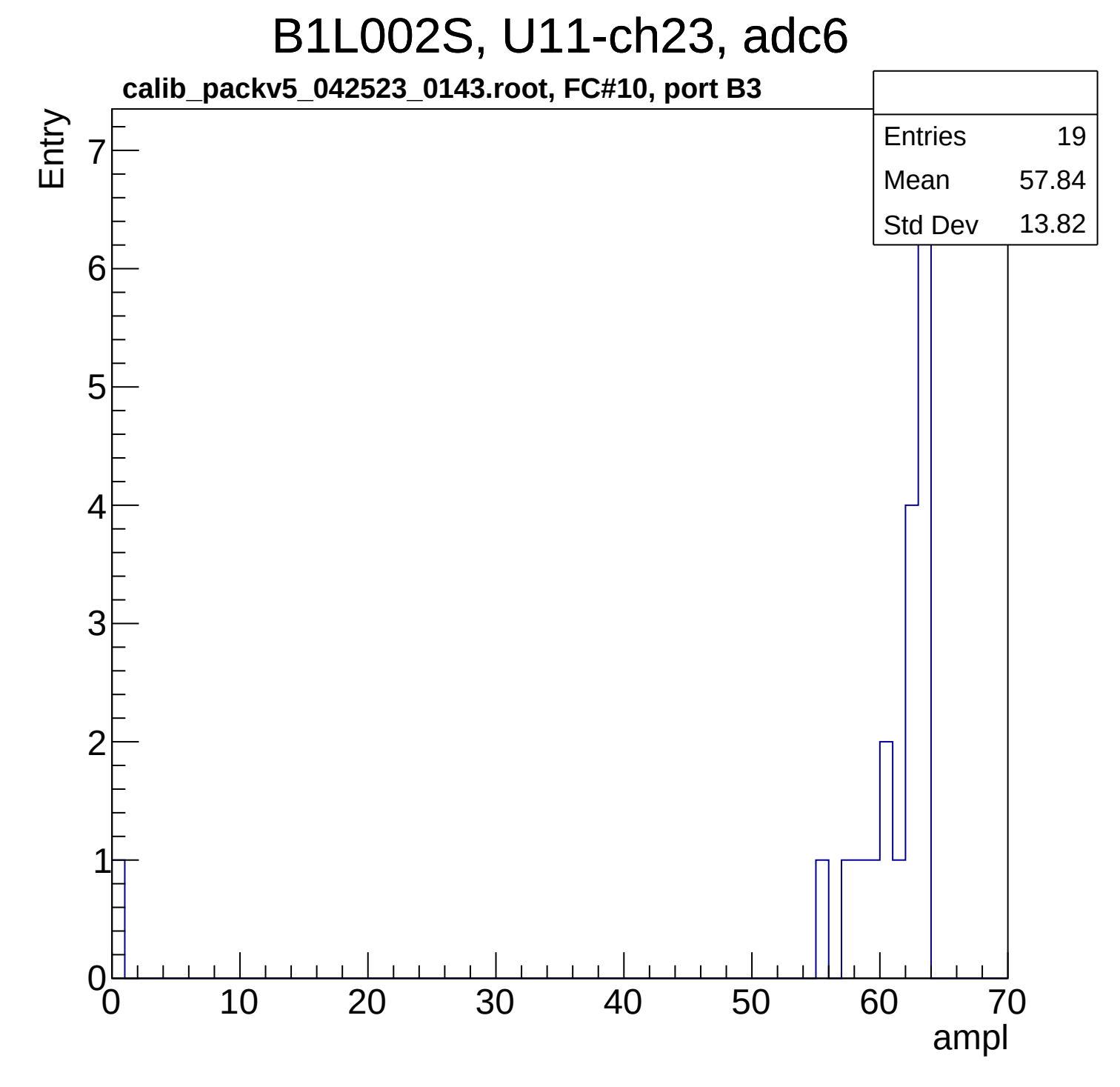
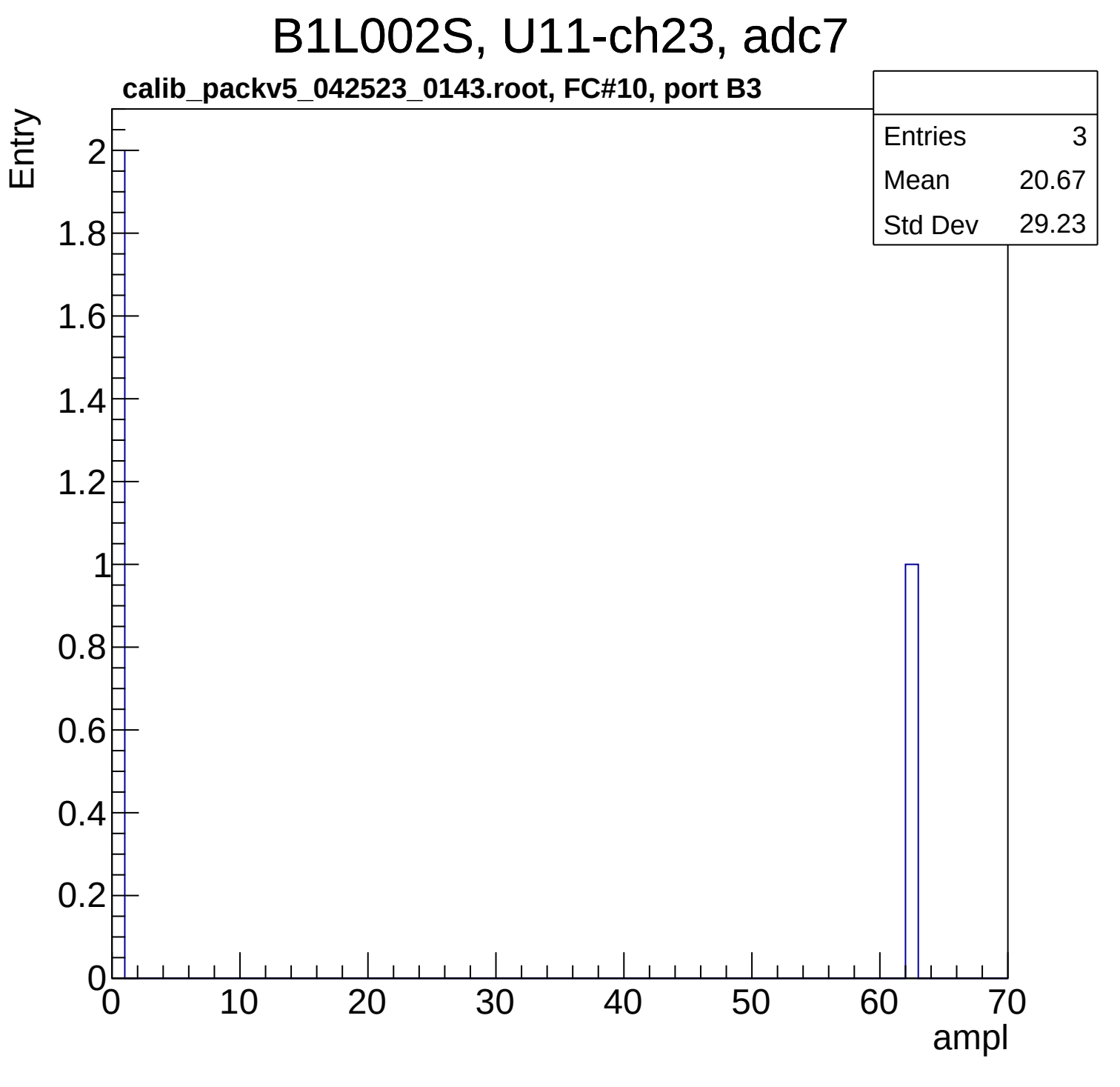

B1L002S, U11-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.84
Std Dev 13.82
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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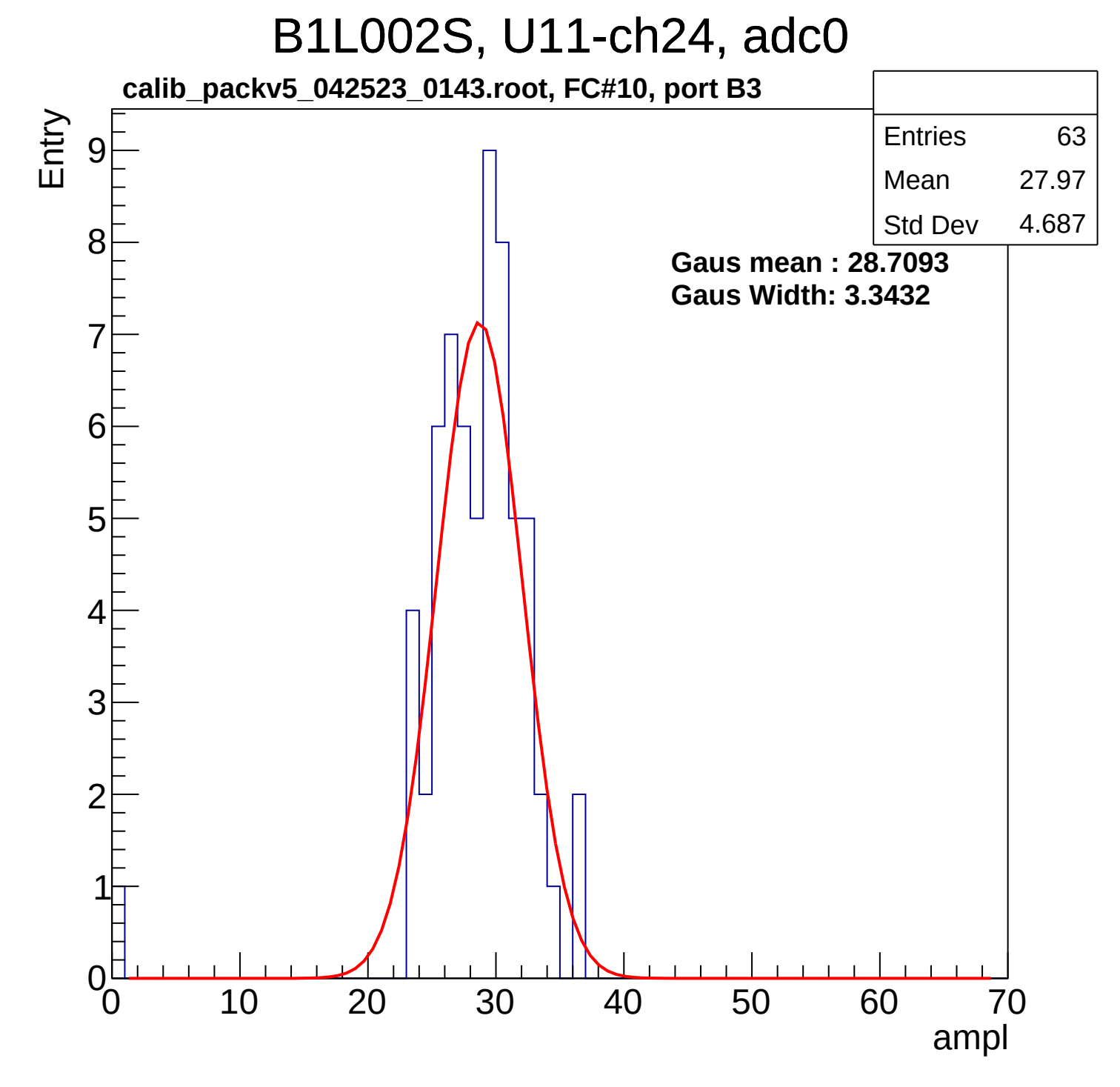
B1L002S, U11-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.97
Std Dev 4.687
Gaus mean : 28.7093
Gaus Width: 3.3432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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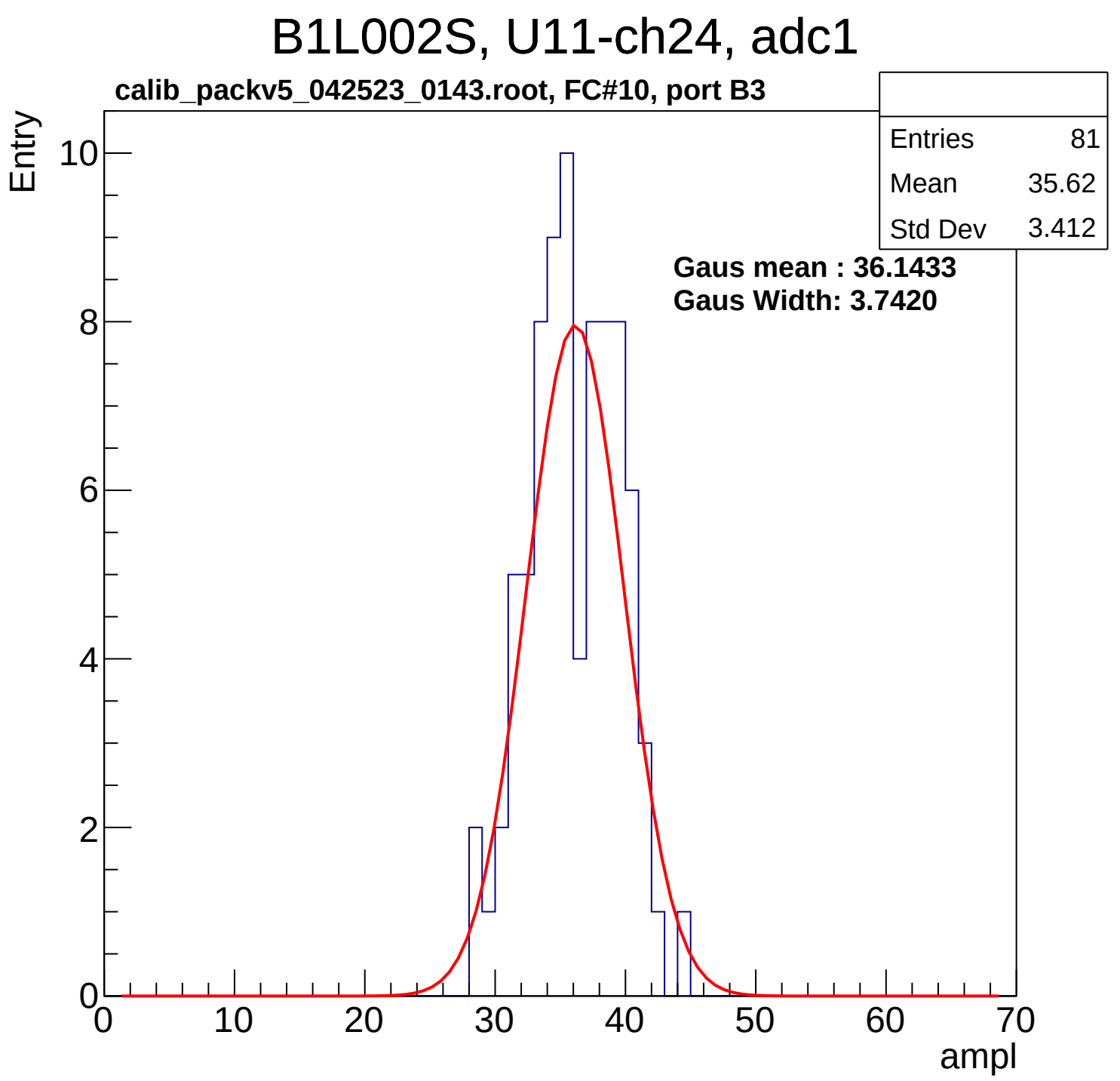
B1L002S, U11-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 35.62
Std Dev 3.412
Gaus mean : 36.1433
Gaus Width: 3.7420
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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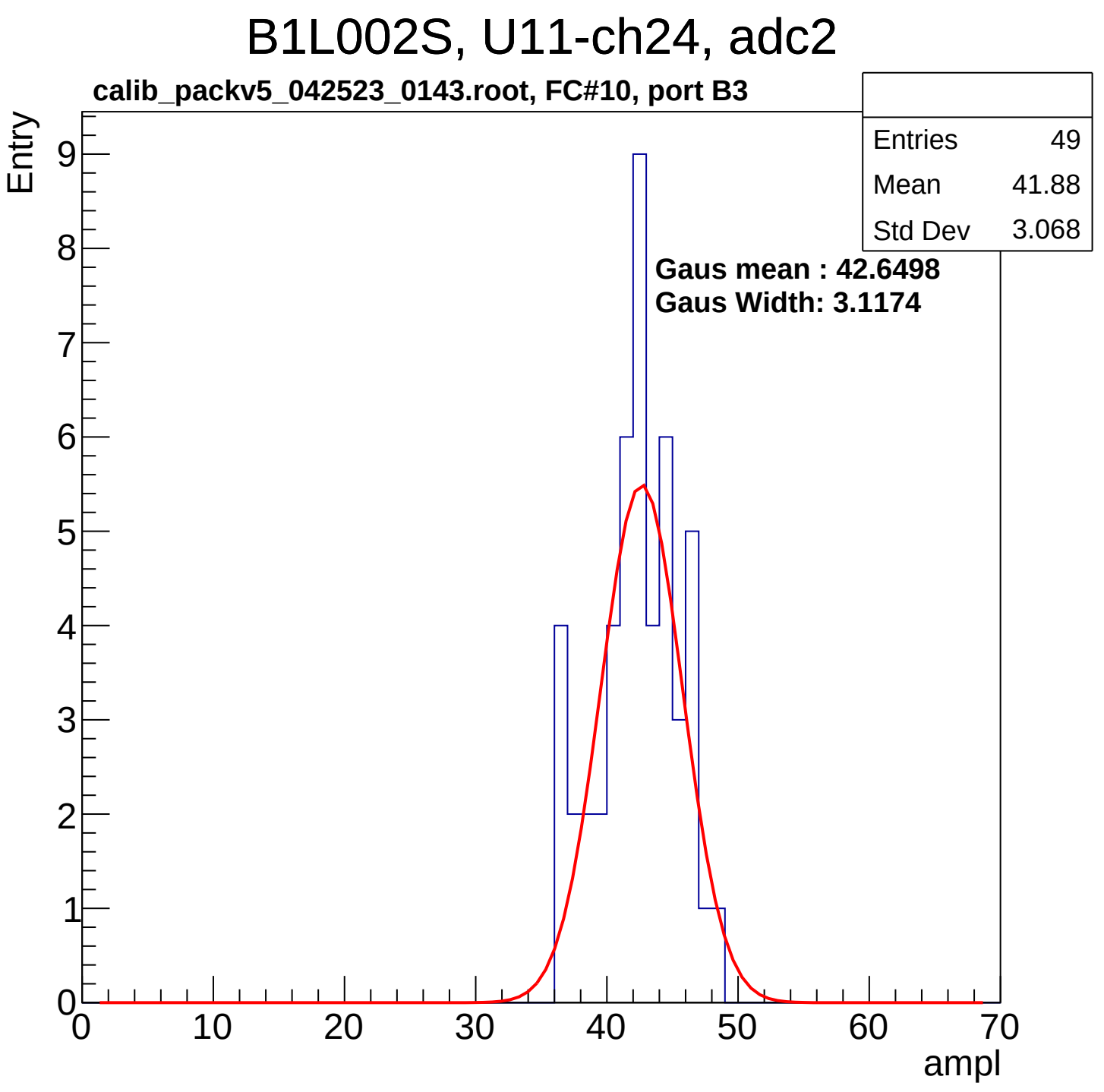
B1L002S, U11-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 41.88
Std Dev 3.068
Gaus mean : 42.6498
Gaus Width: 3.1174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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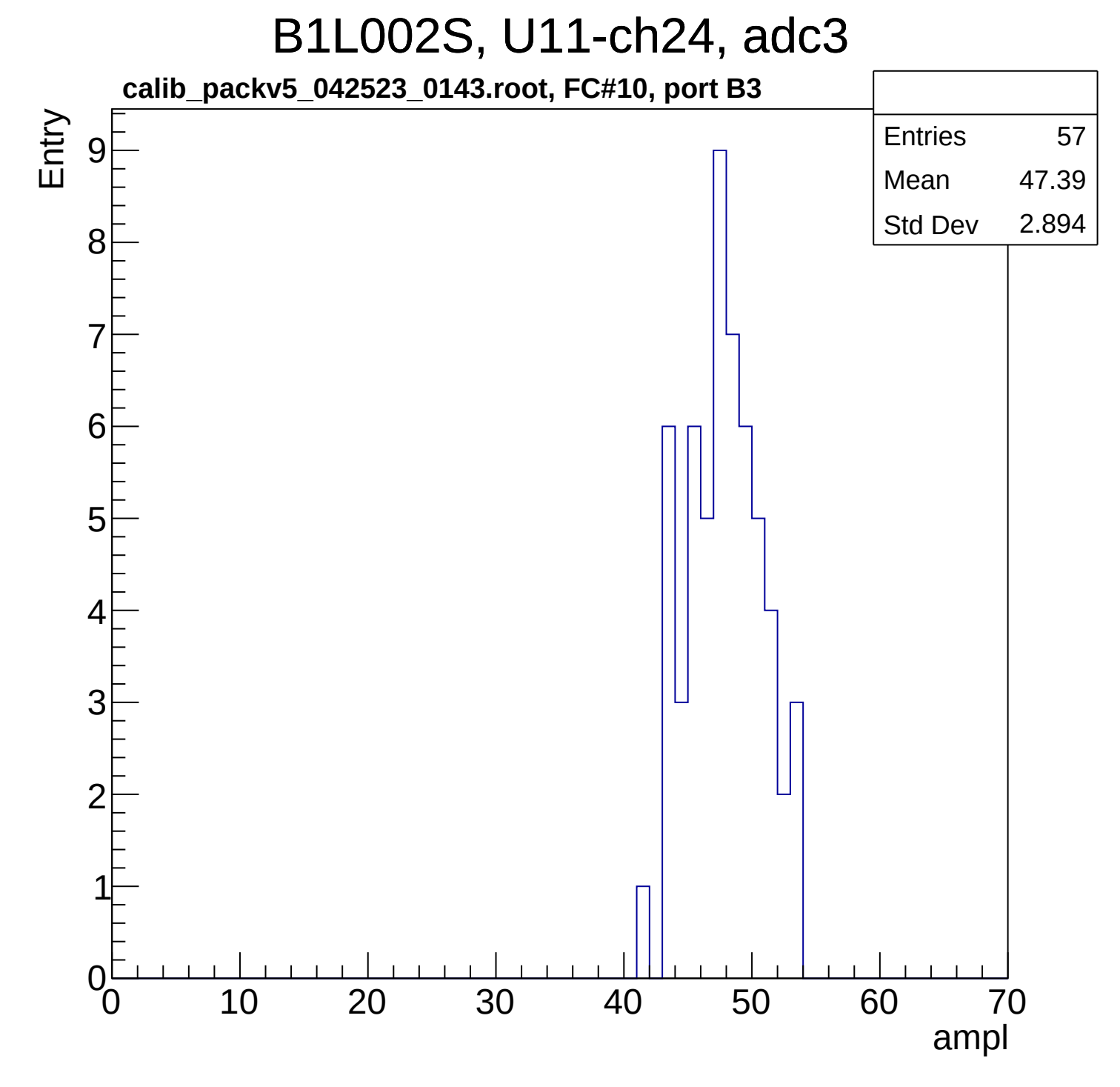
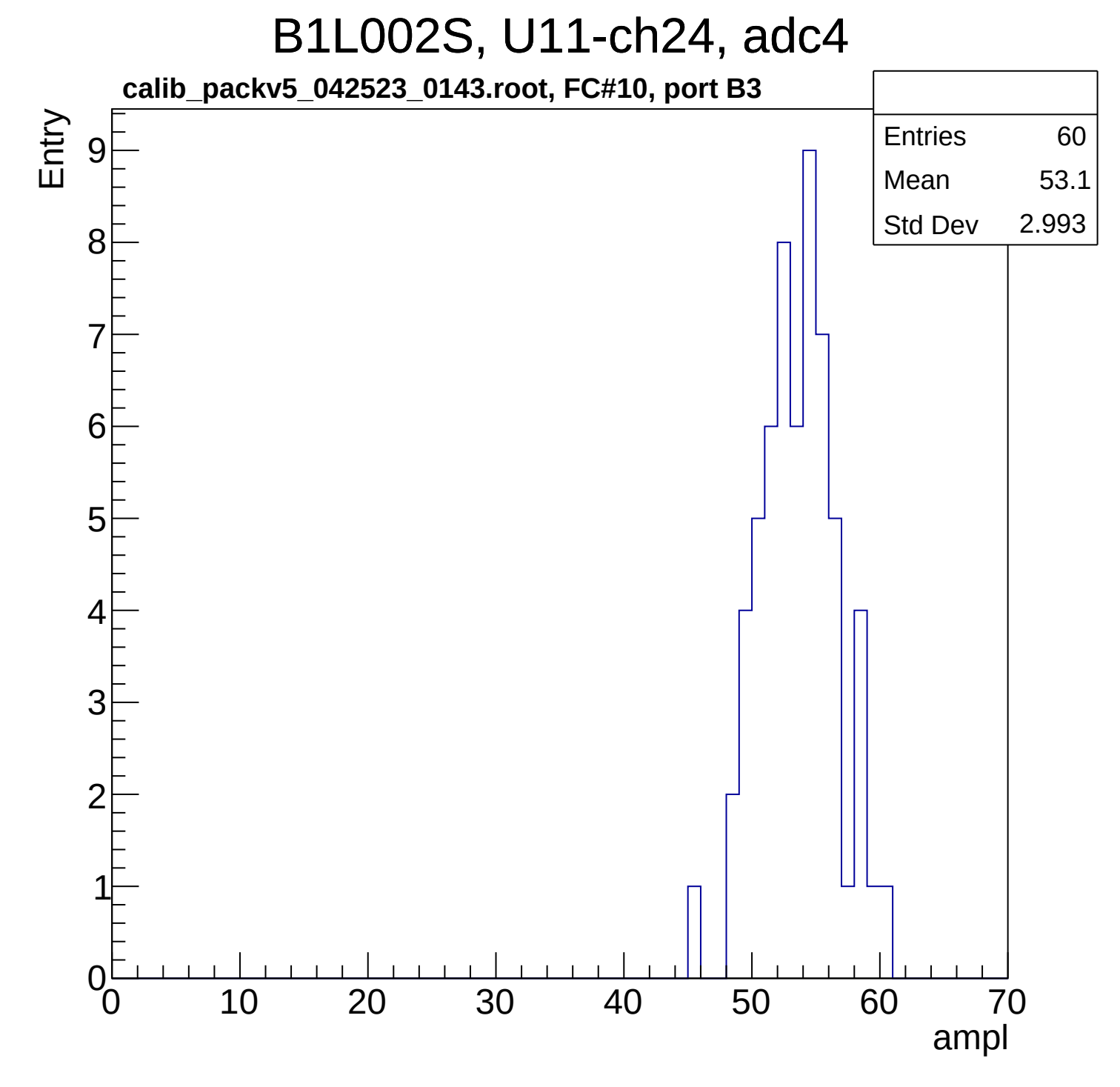
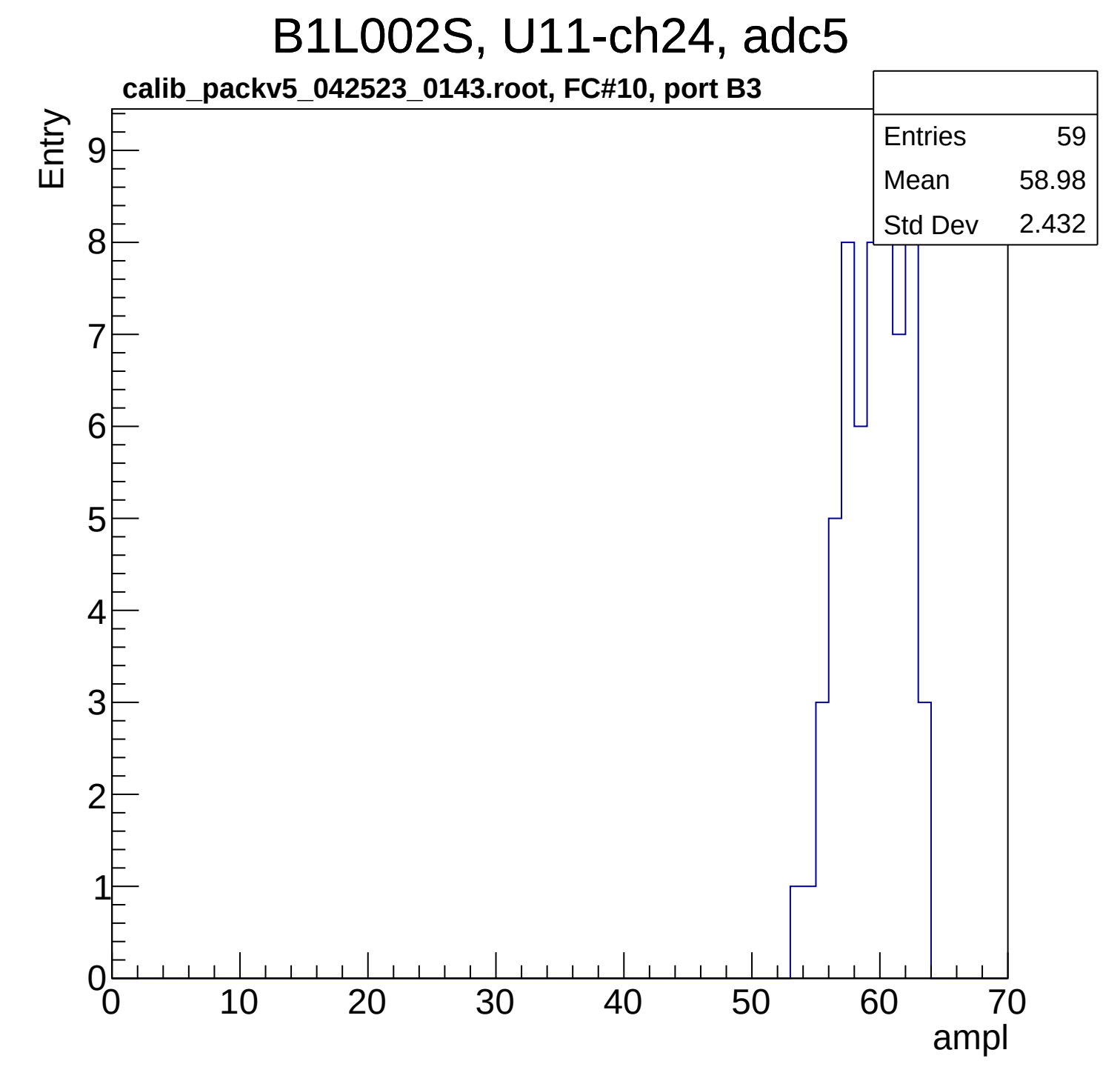

B1L002S, U11-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.39
Std Dev 2.894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.1
Std Dev 2.993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.98
Std Dev 2.432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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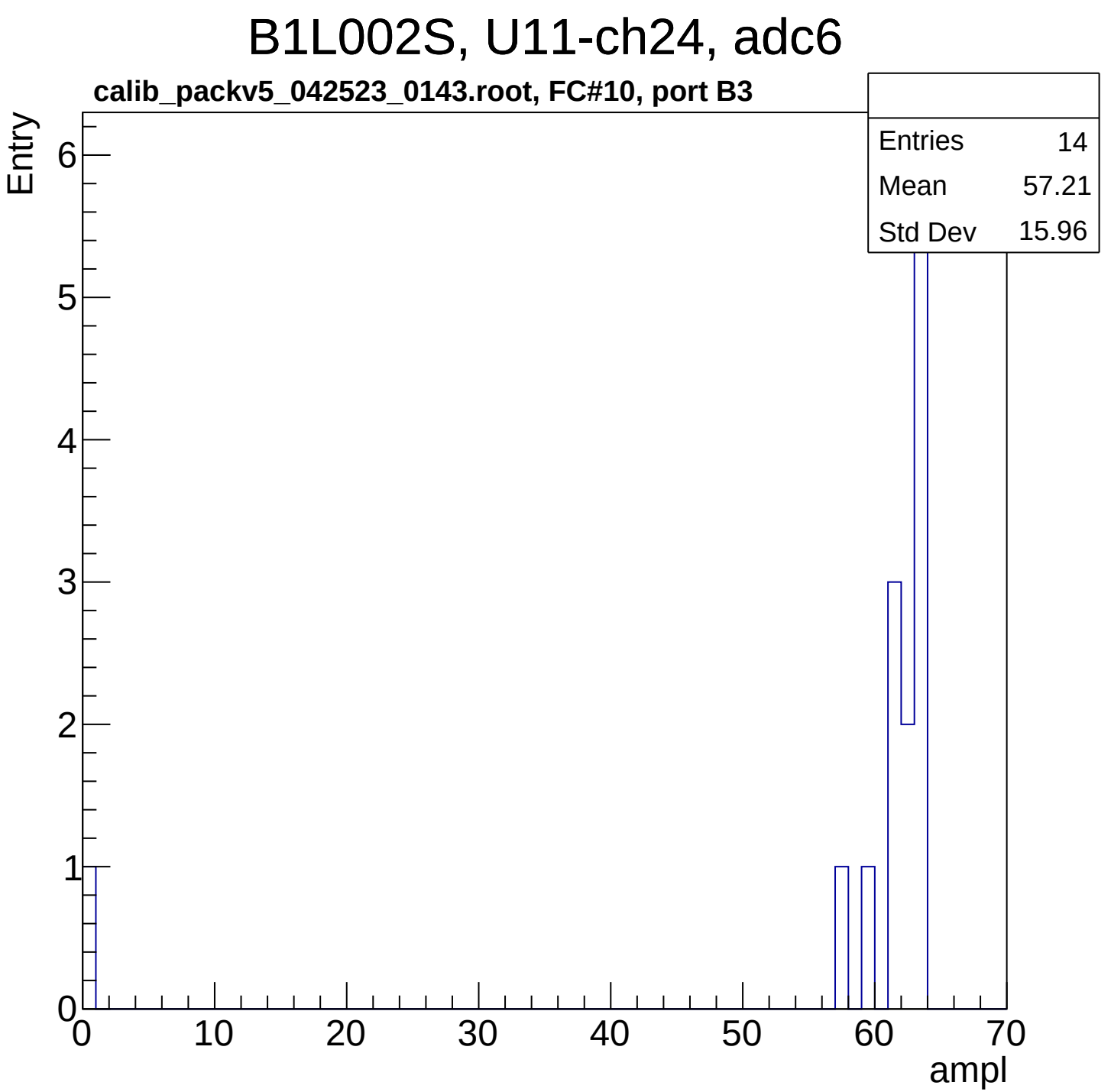
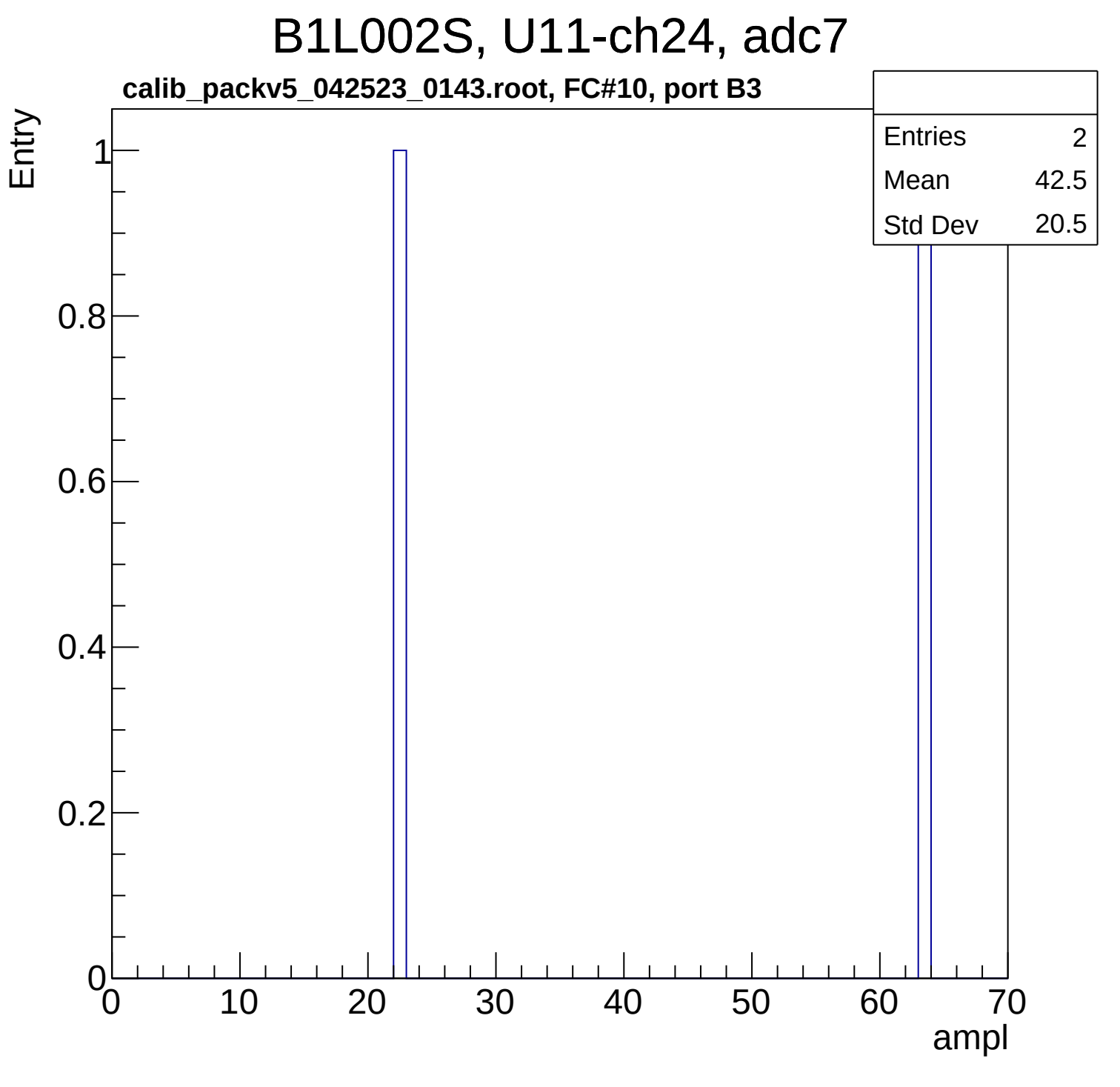
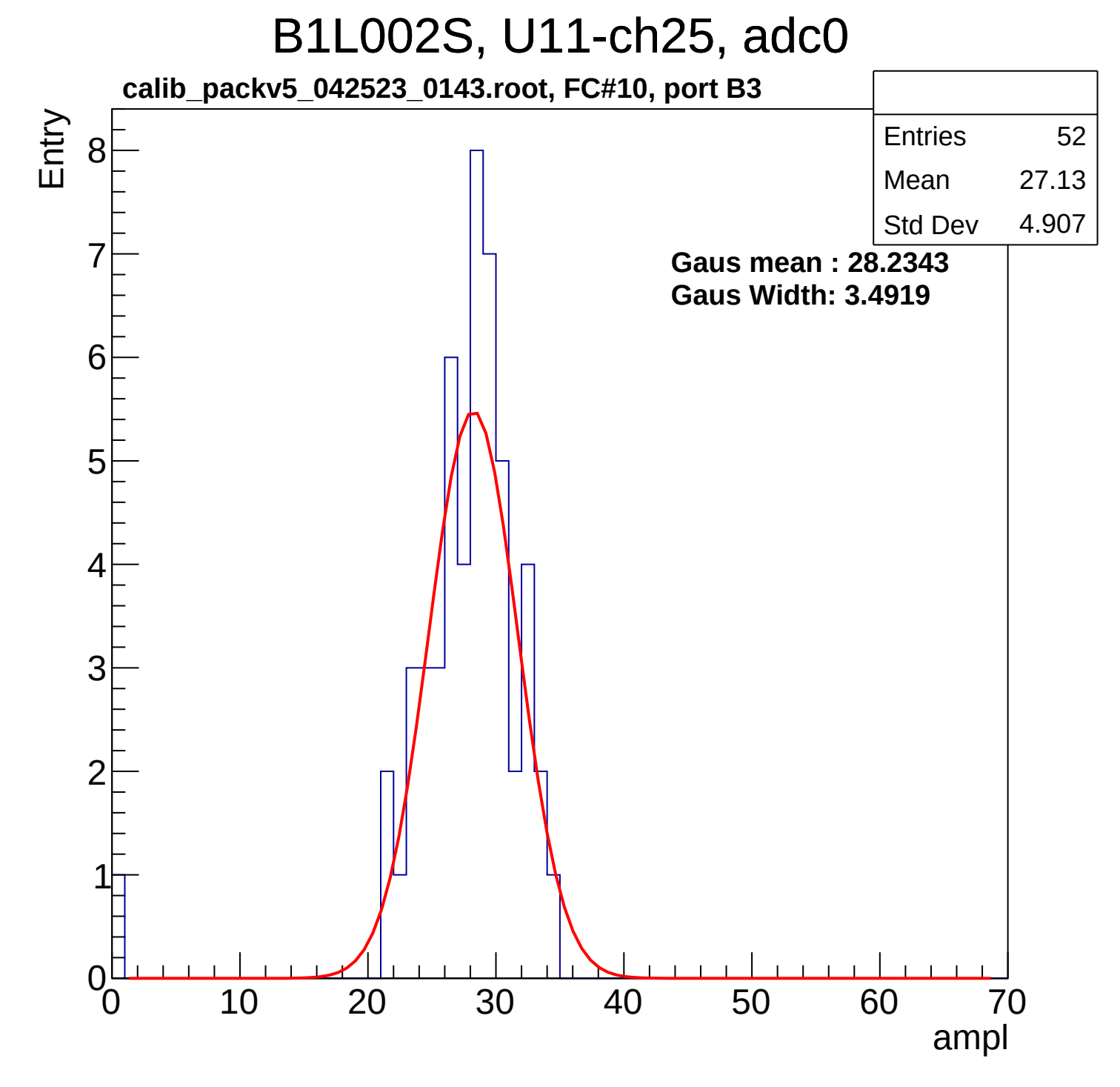
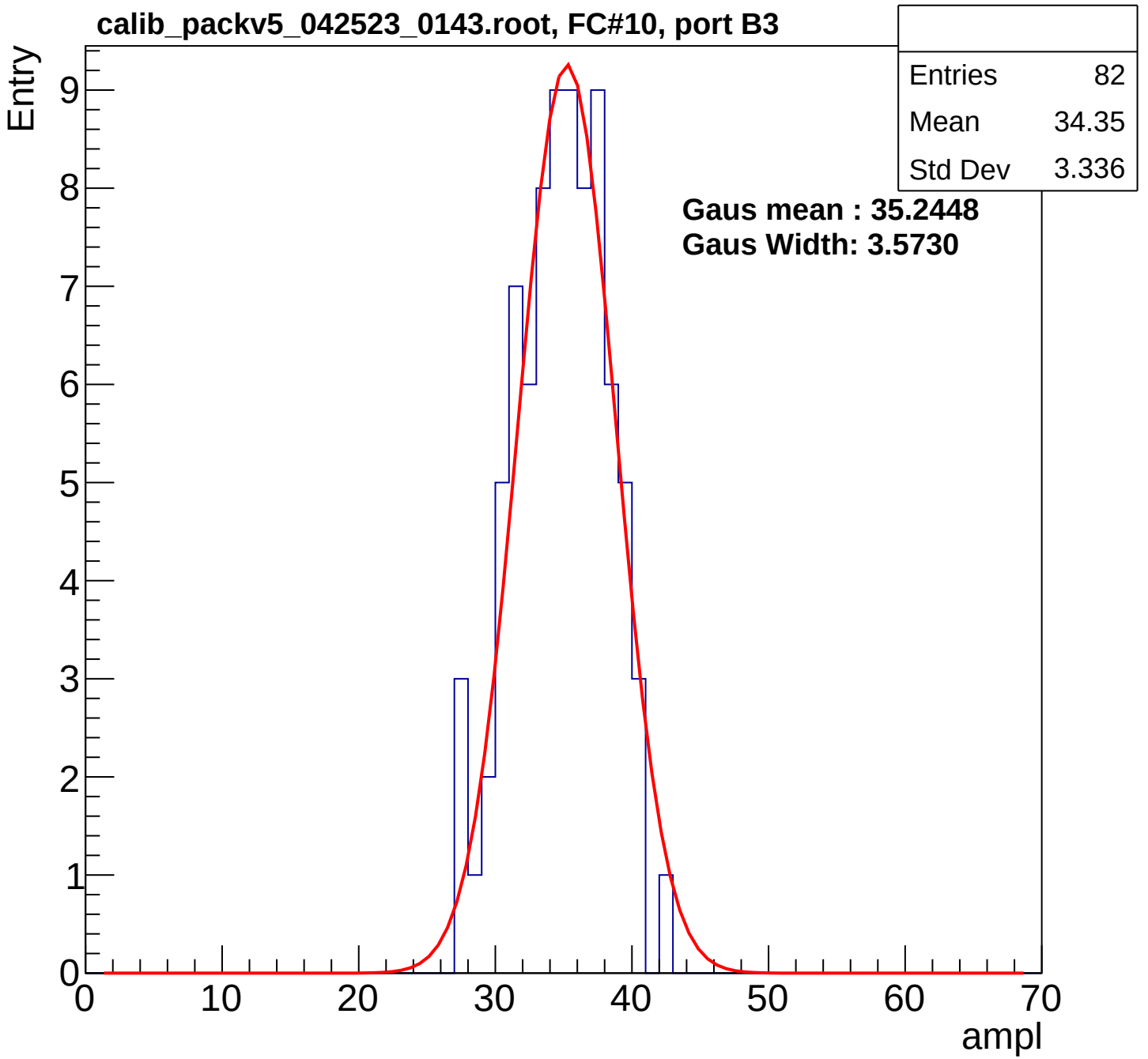
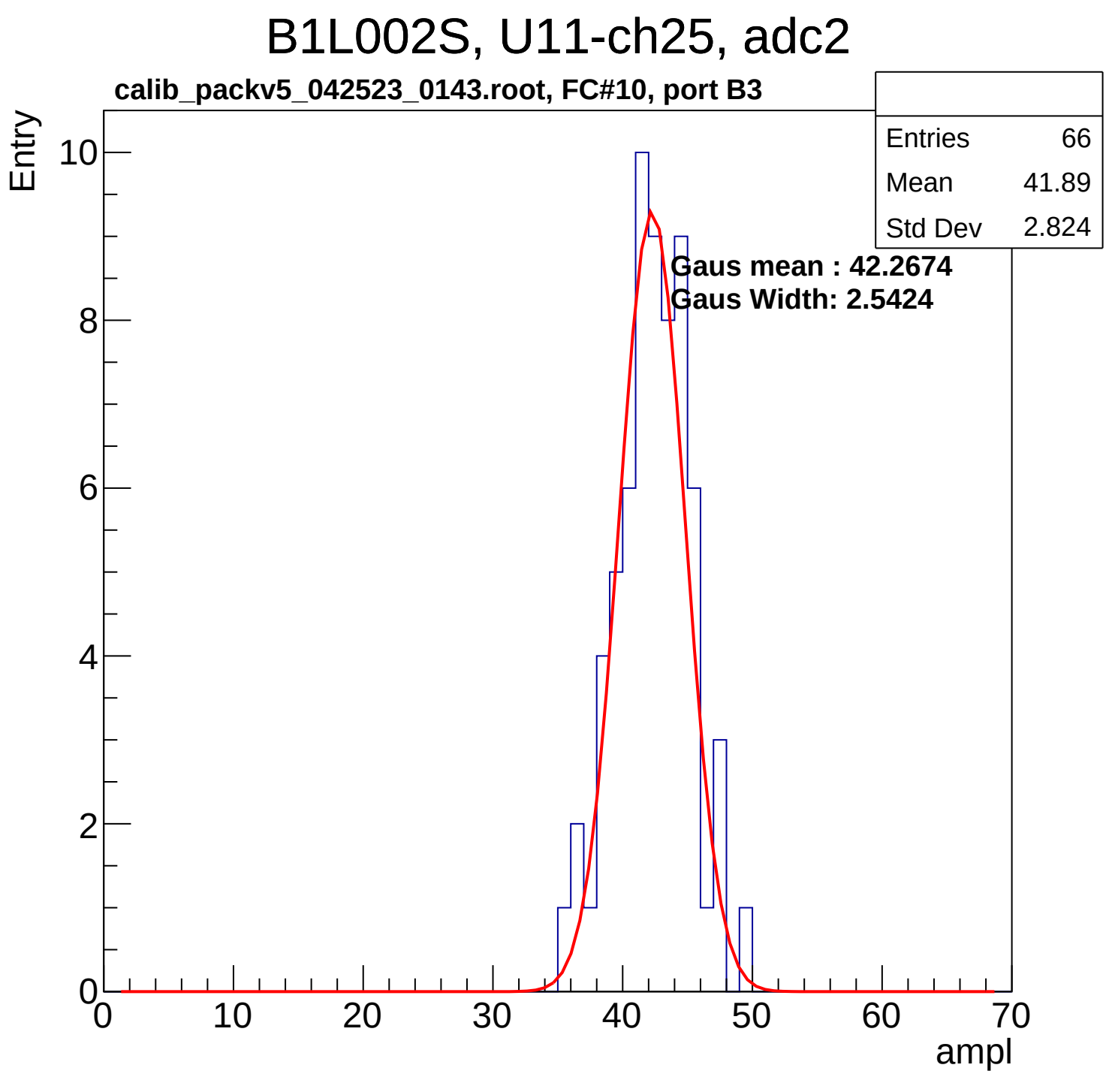
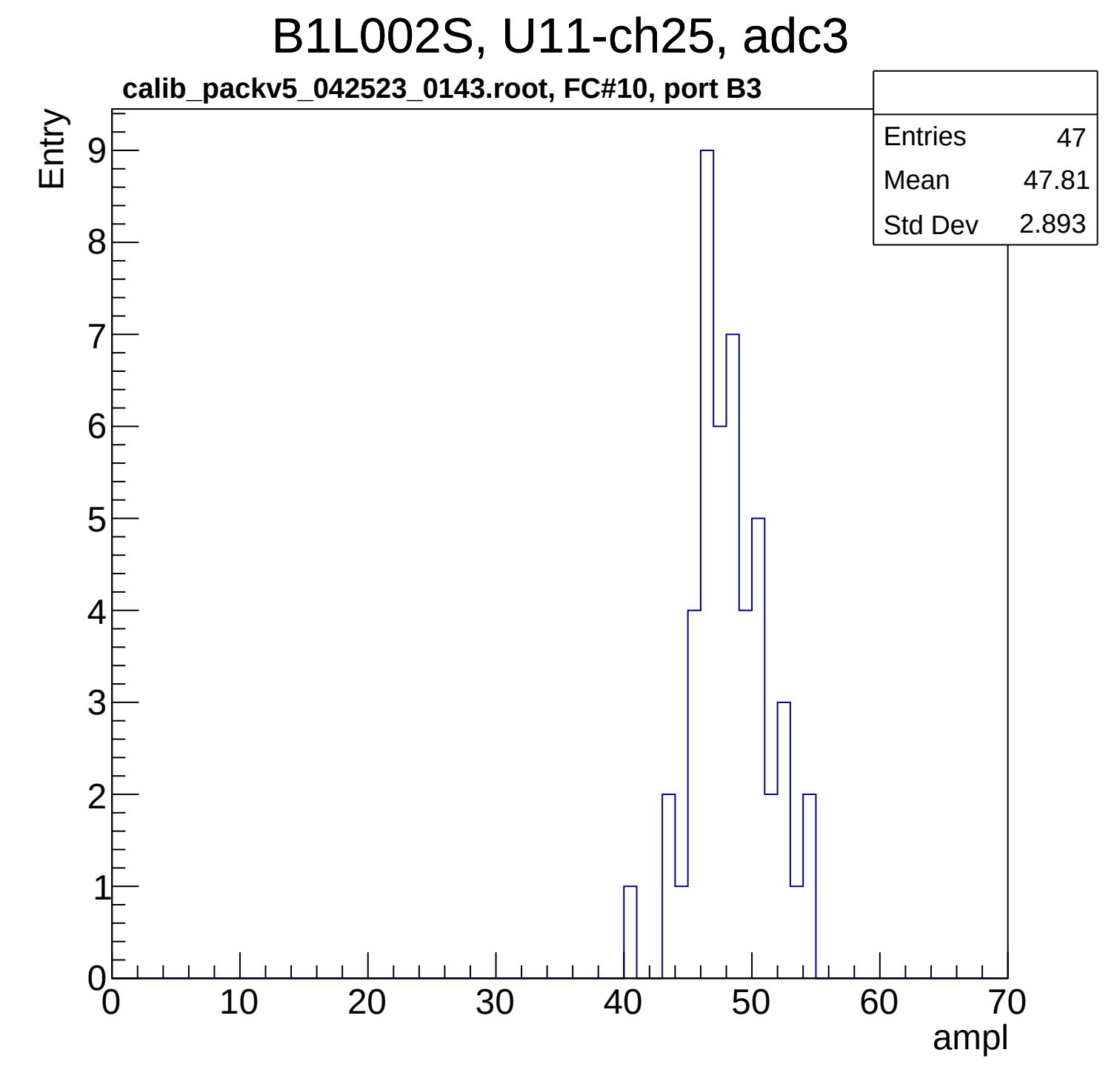

B1L002S, U11-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.21
Std Dev 15.96
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

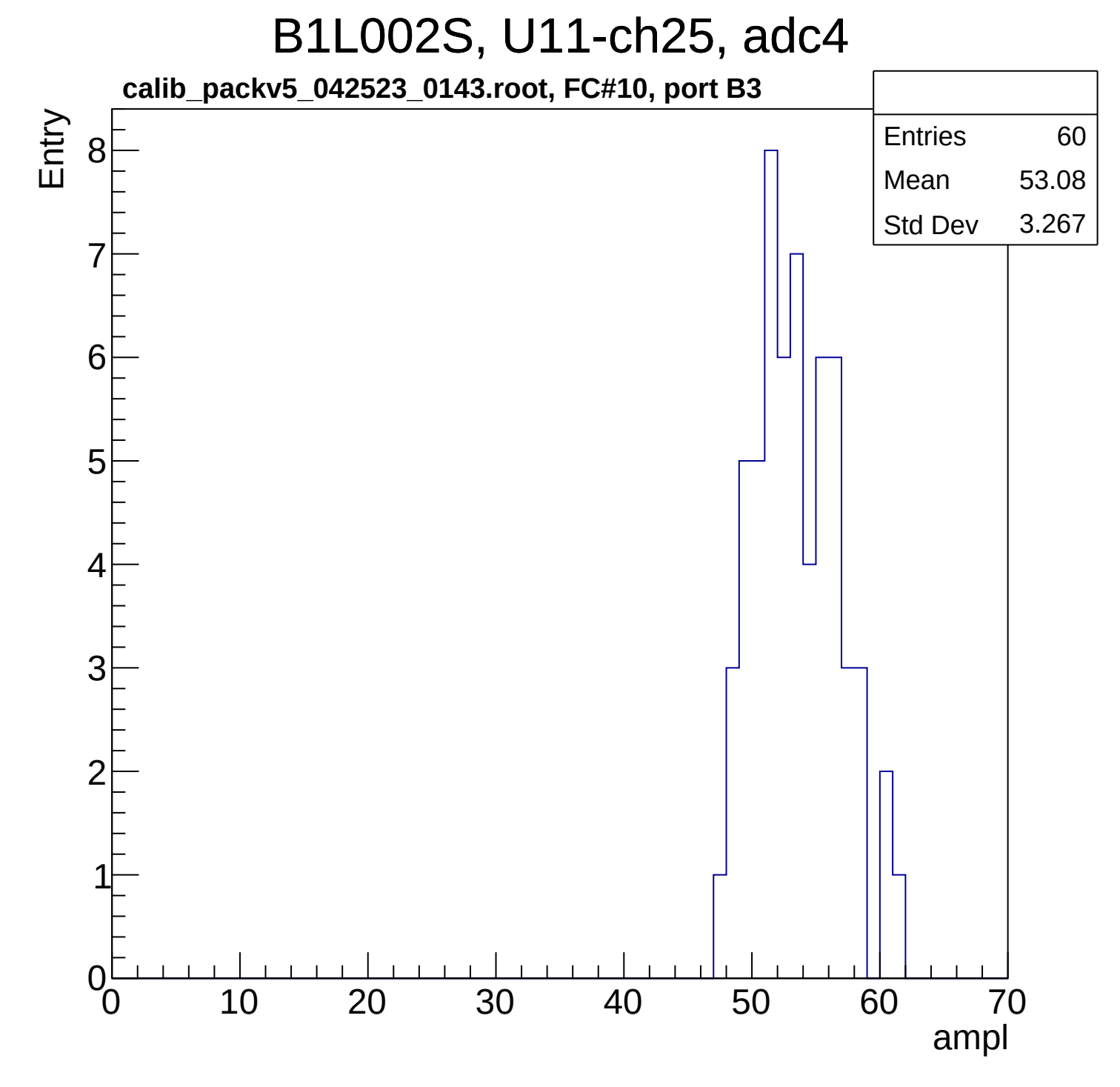

B1L002S, U11-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.13
Std Dev 4.907
Gaus mean : 28.2343
Gaus Width: 3.4919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 34.35
Std Dev 3.336
Gaus mean : 35.2448
Gaus Width: 3.5730
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.89
Std Dev 2.824
Gaus mean : 42.2674
Gaus Width: 2.5424
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 47.81
Std Dev 2.893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.08
Std Dev 3.267
Entry
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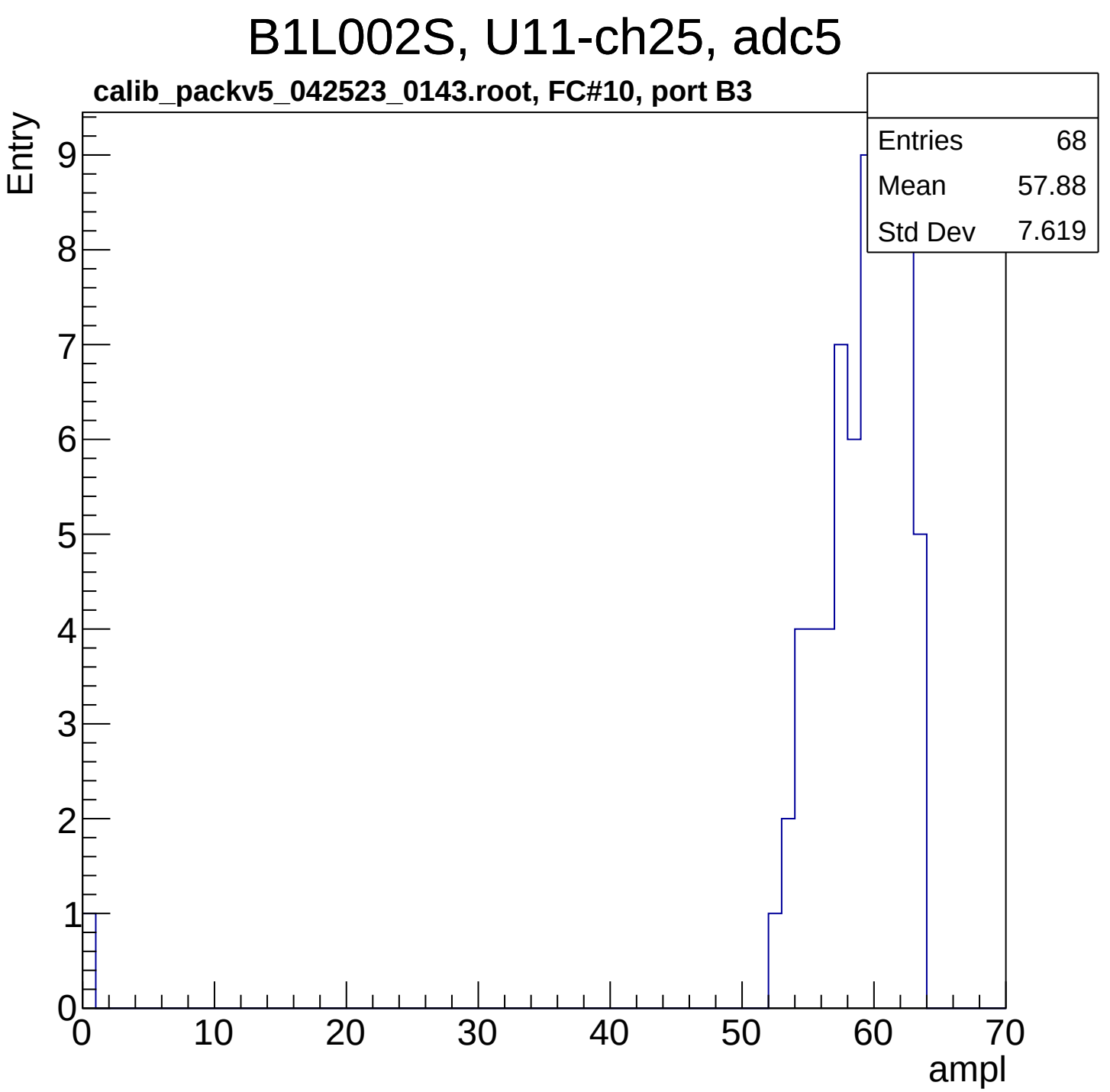
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.88
Std Dev 7.619
Entry
ampl
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3
4
5
6
7
8
9
0
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20
30
40
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60
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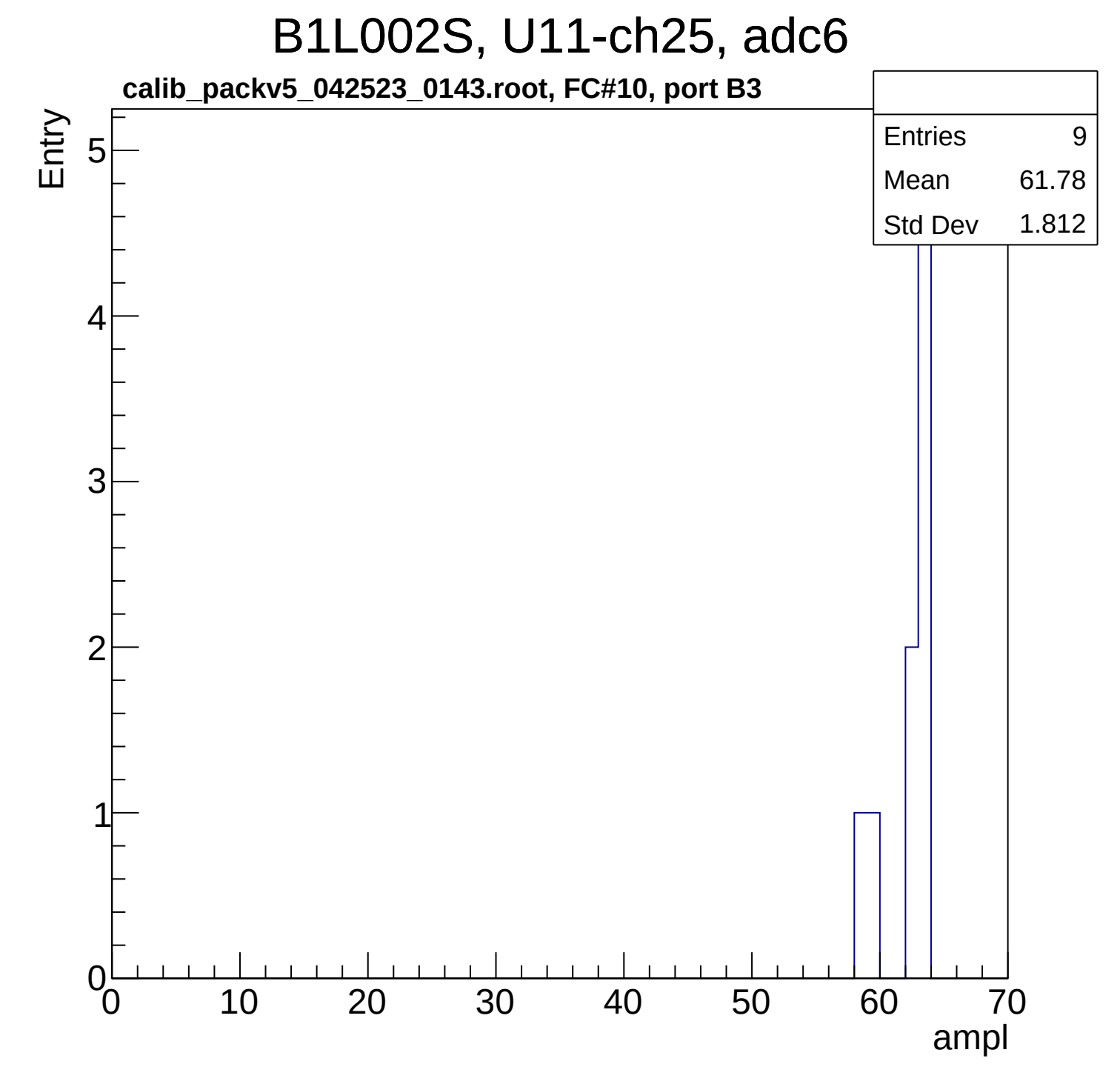
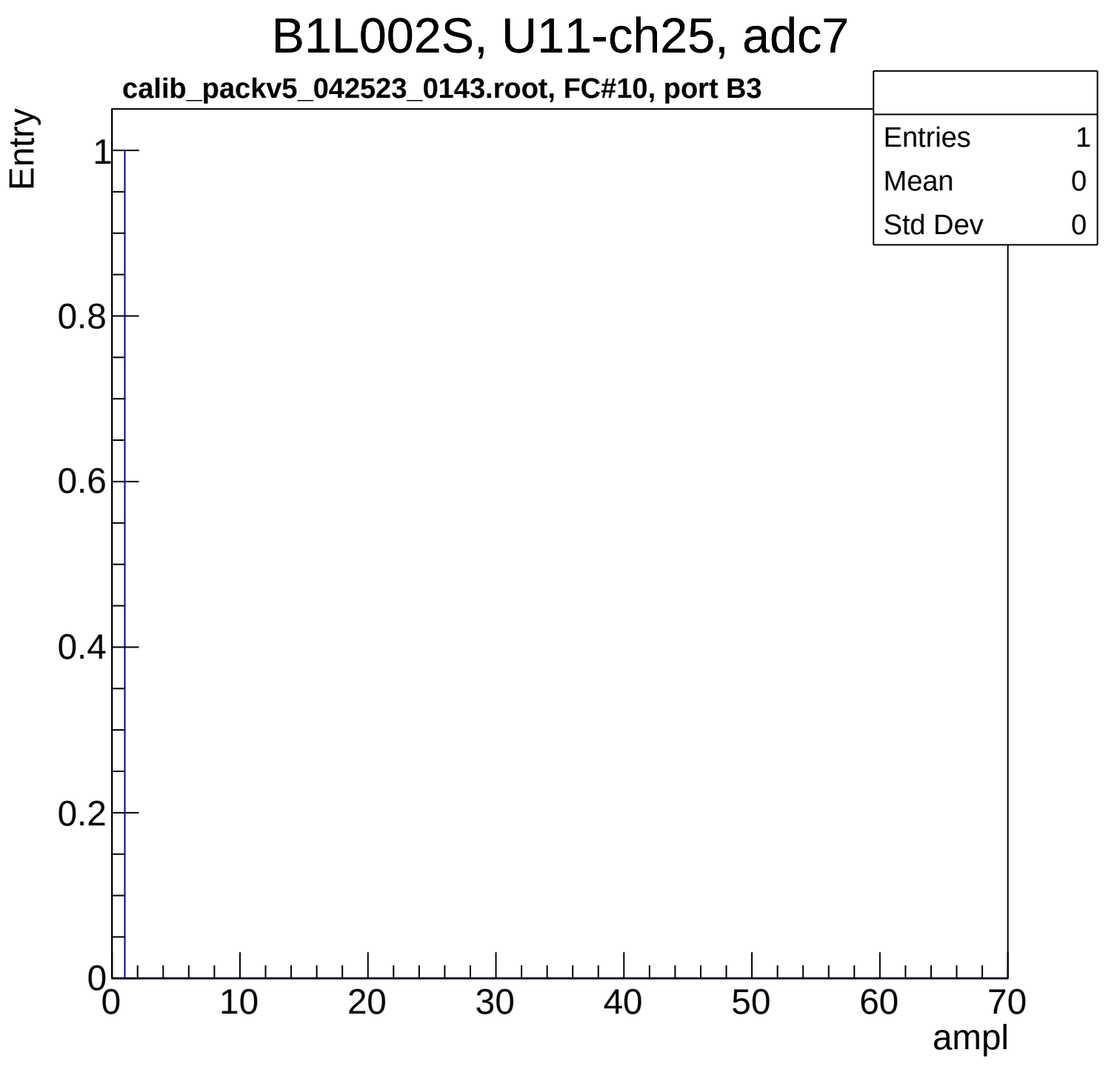
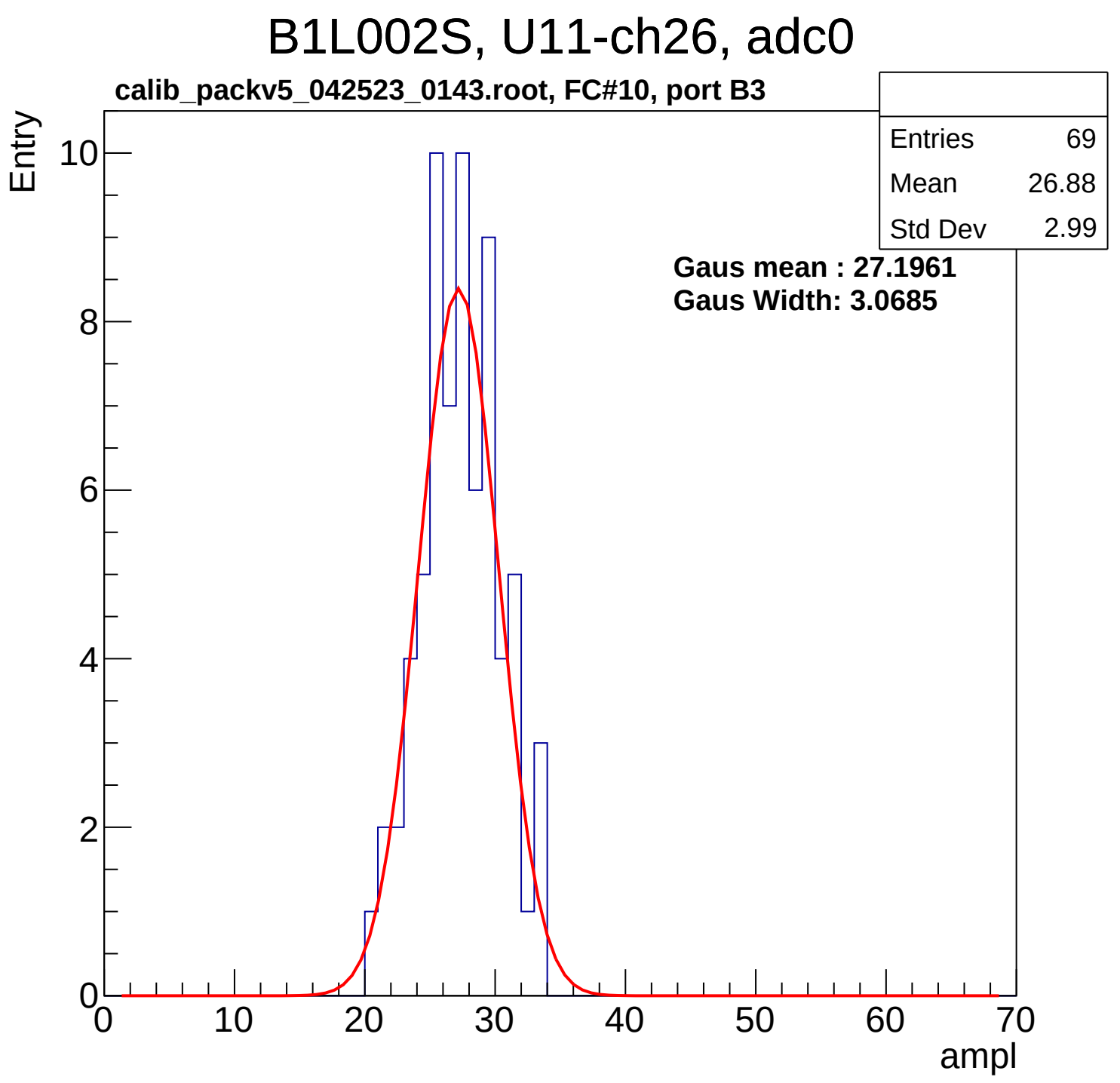
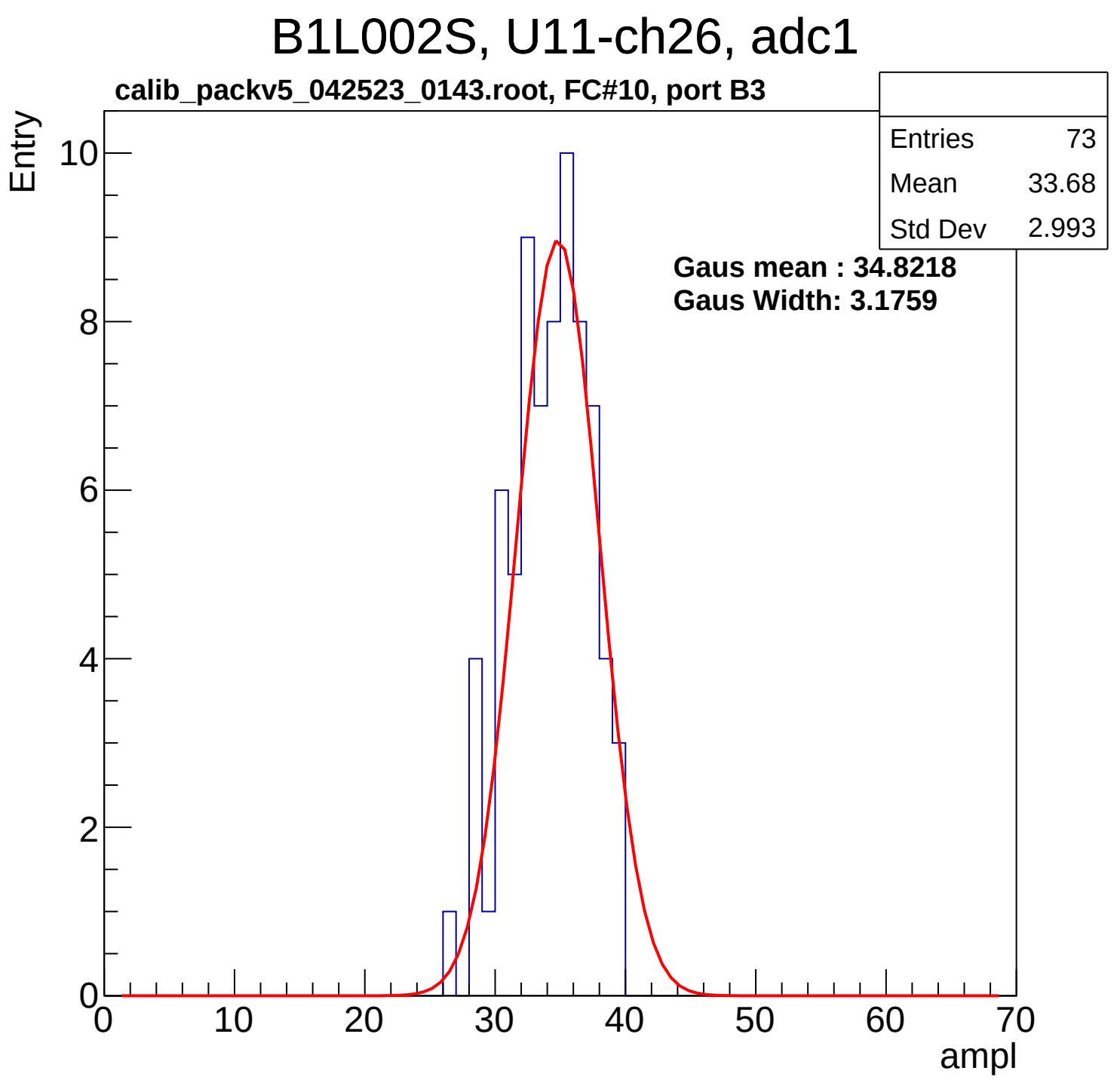
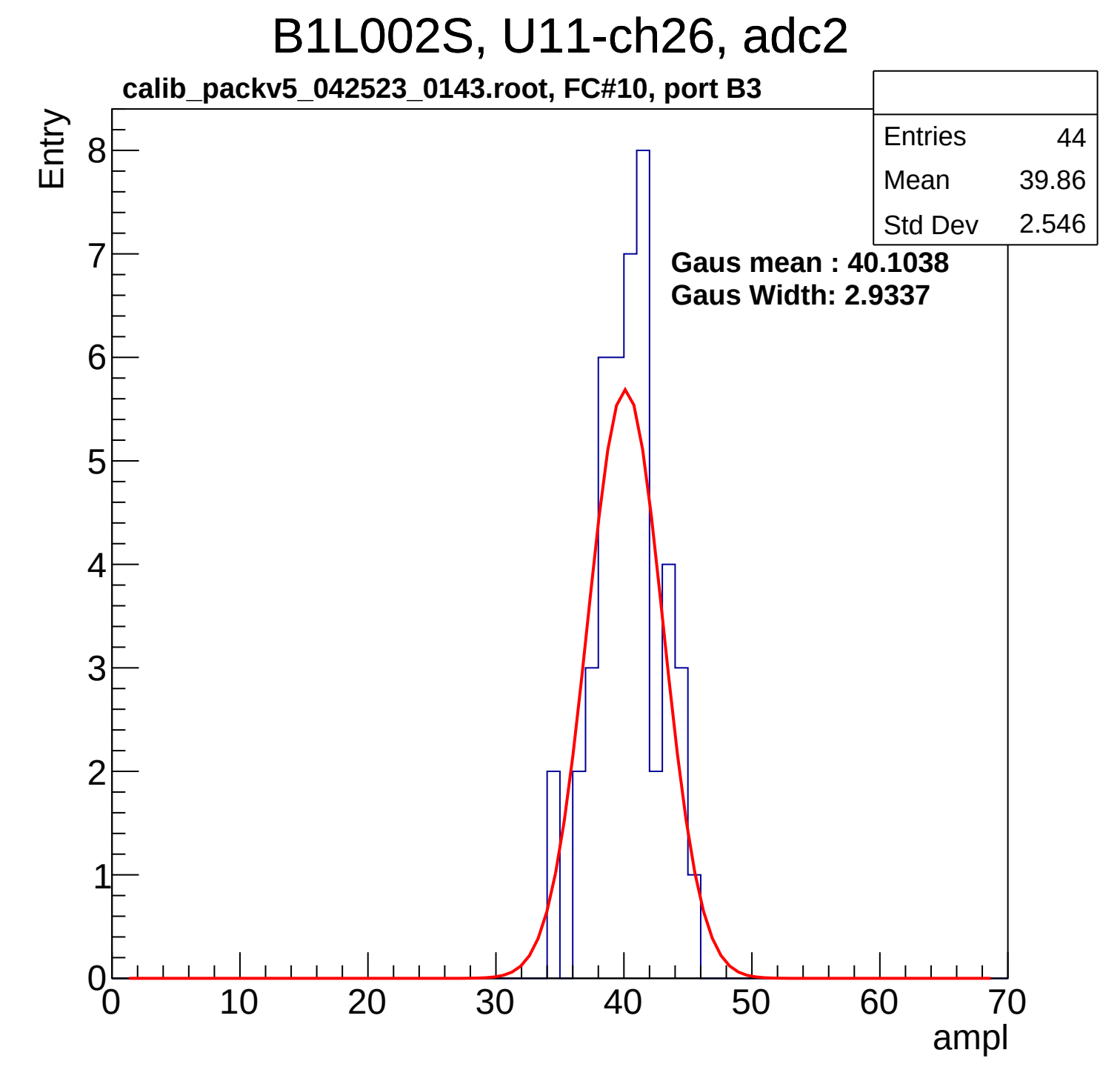

B1L002S, U11-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 61.78
Std Dev 1.812
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 26.88
Std Dev 2.99
Gaus mean : 27.1961
Gaus Width: 3.0685
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 33.68
Std Dev 2.993
Gaus mean : 34.8218
Gaus Width: 3.1759
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 39.86
Std Dev 2.546
Gaus mean : 40.1038
Gaus Width: 2.9337
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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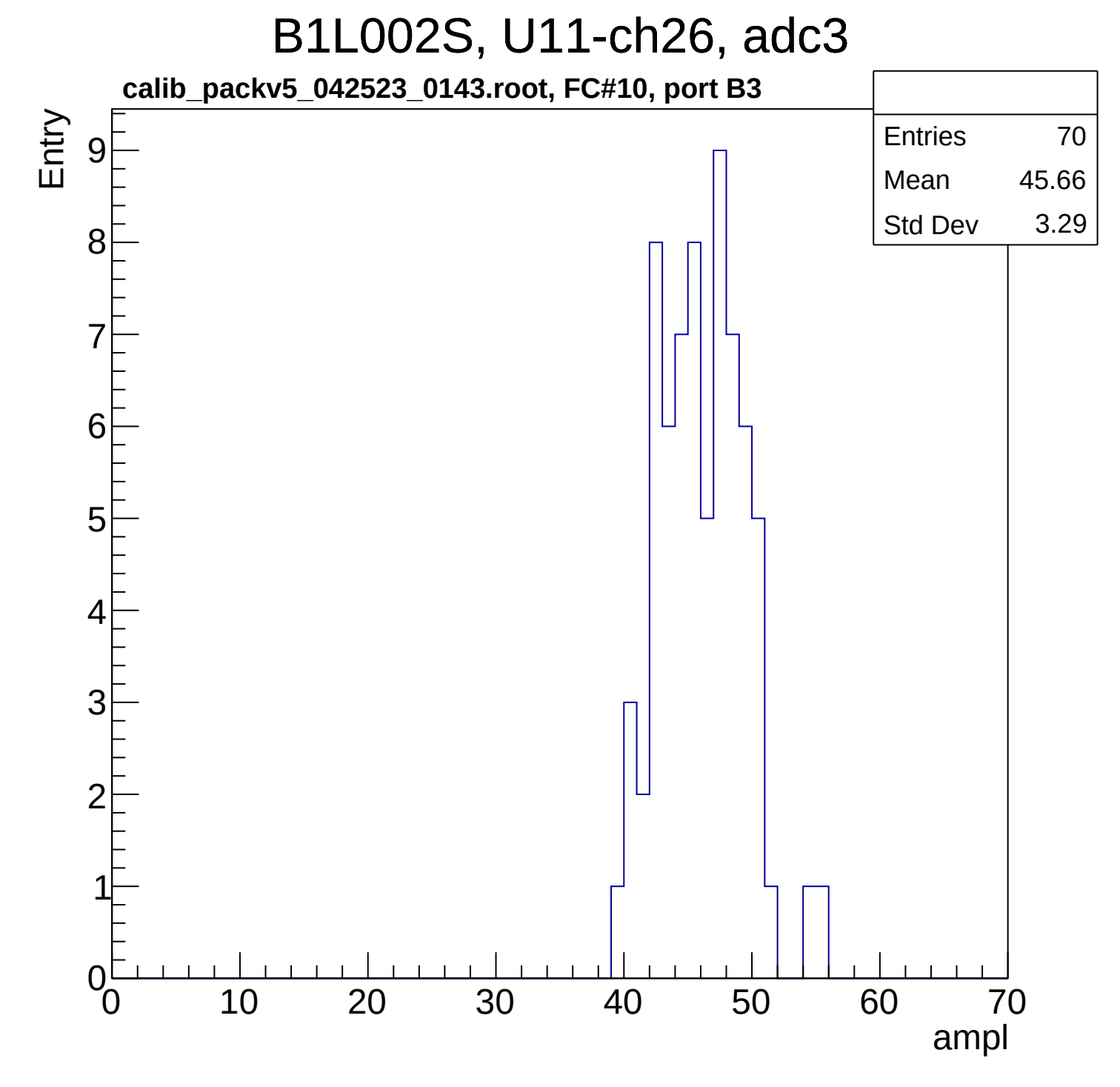
B1L002S, U11-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 45.66
Std Dev 3.29
Entry
ampl
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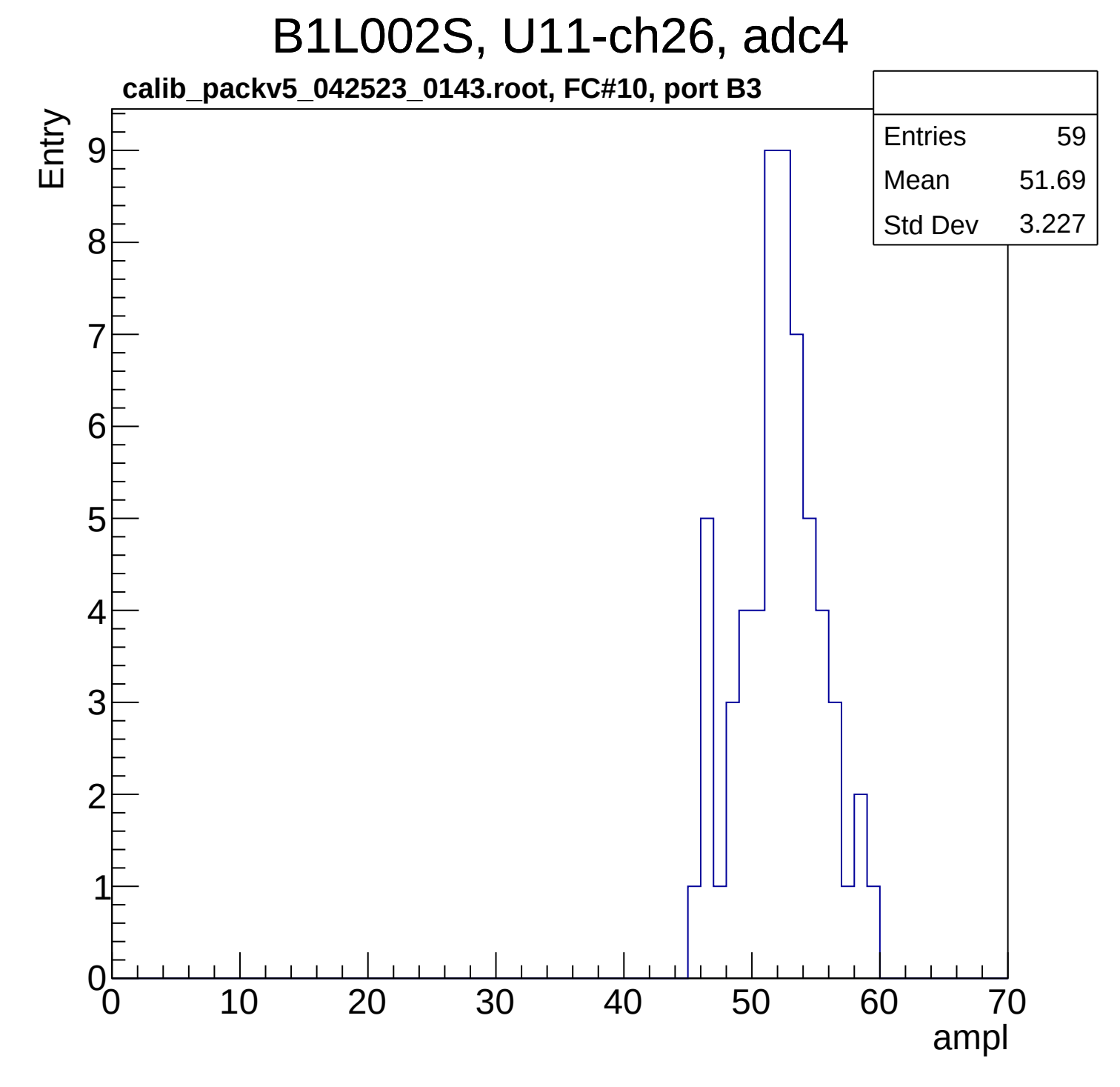
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 51.69
Std Dev 3.227
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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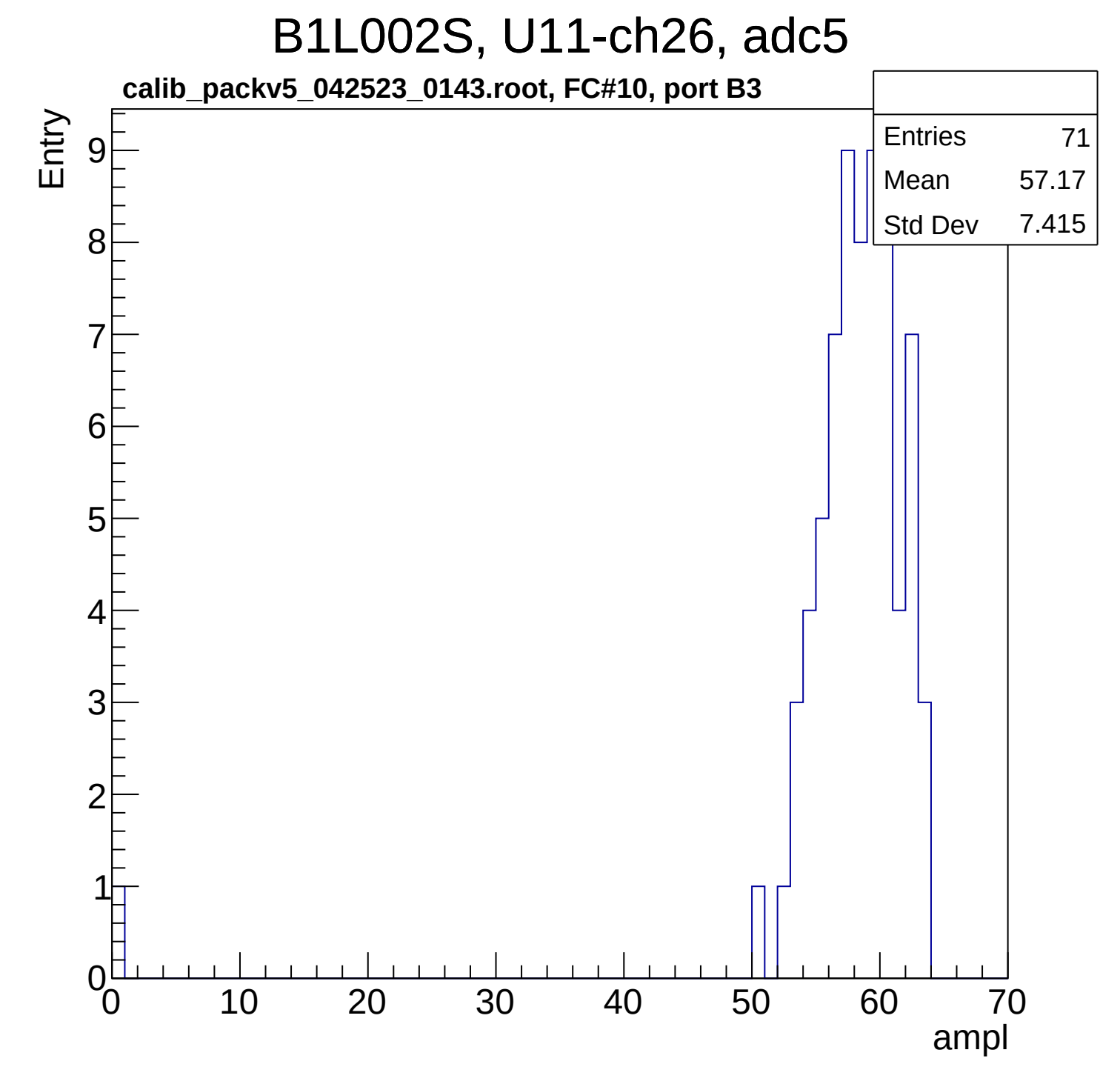
B1L002S, U11-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57.17
Std Dev 7.415
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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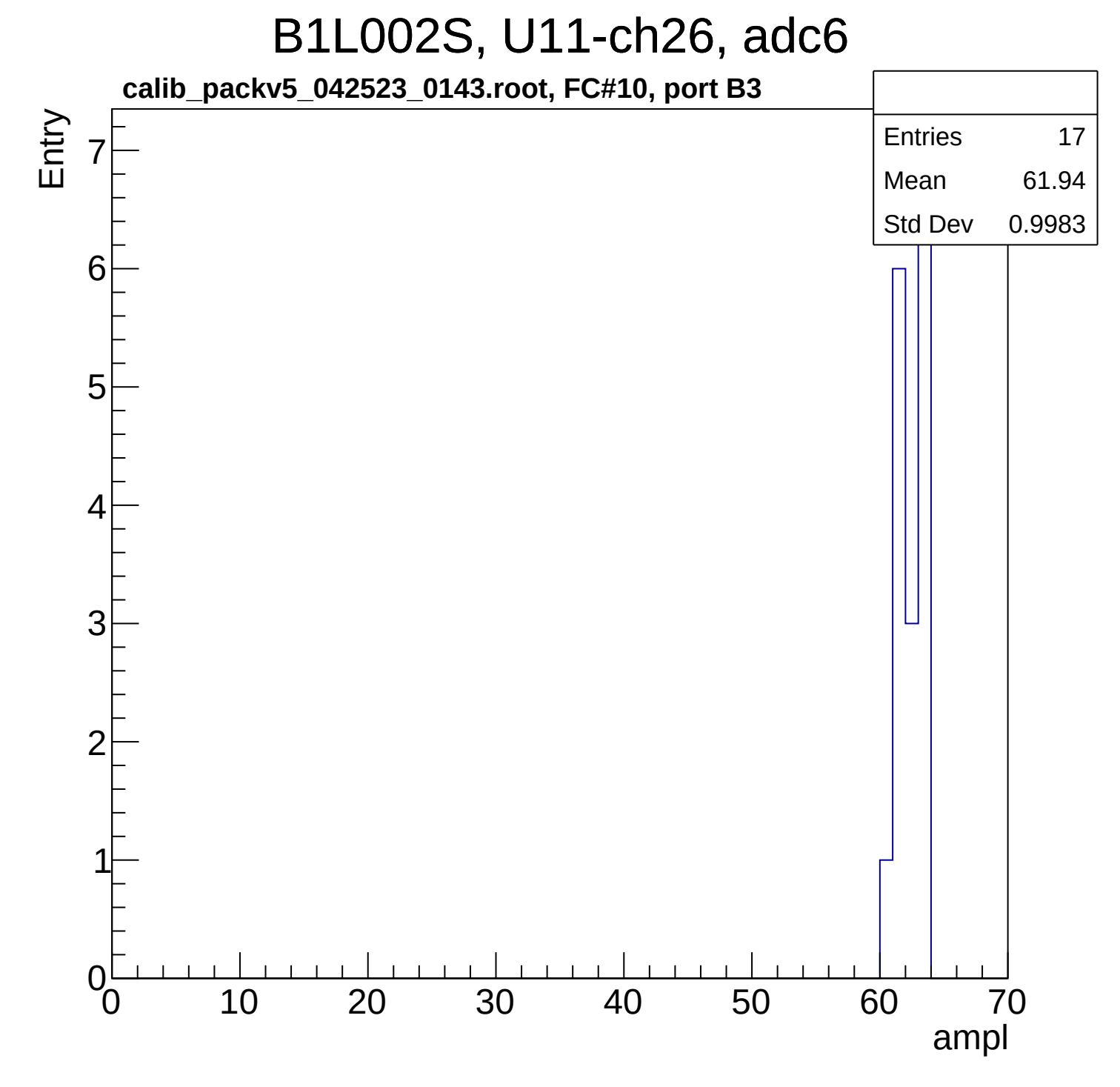
B1L002S, U11-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.94
Std Dev 0.9983
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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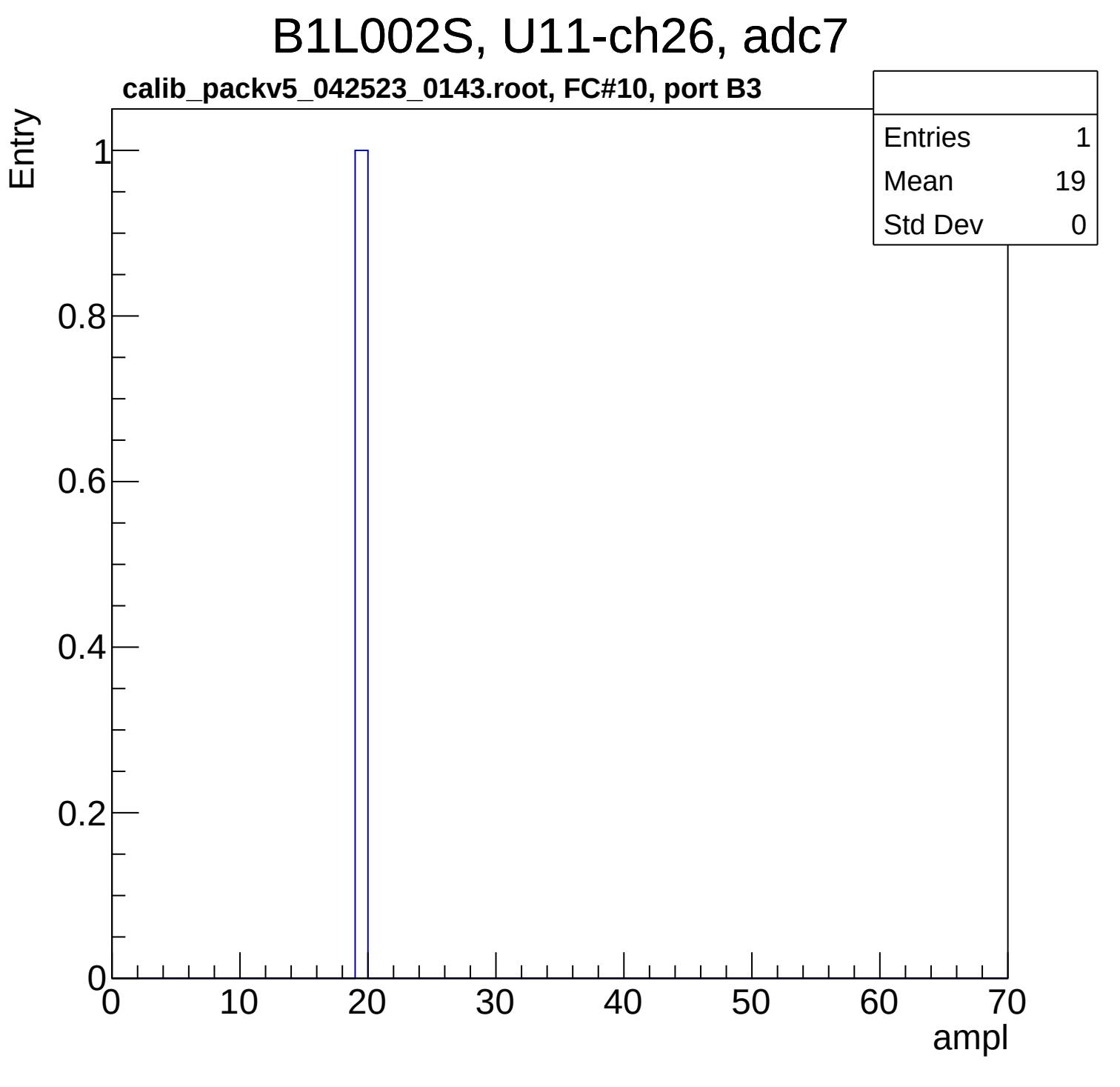
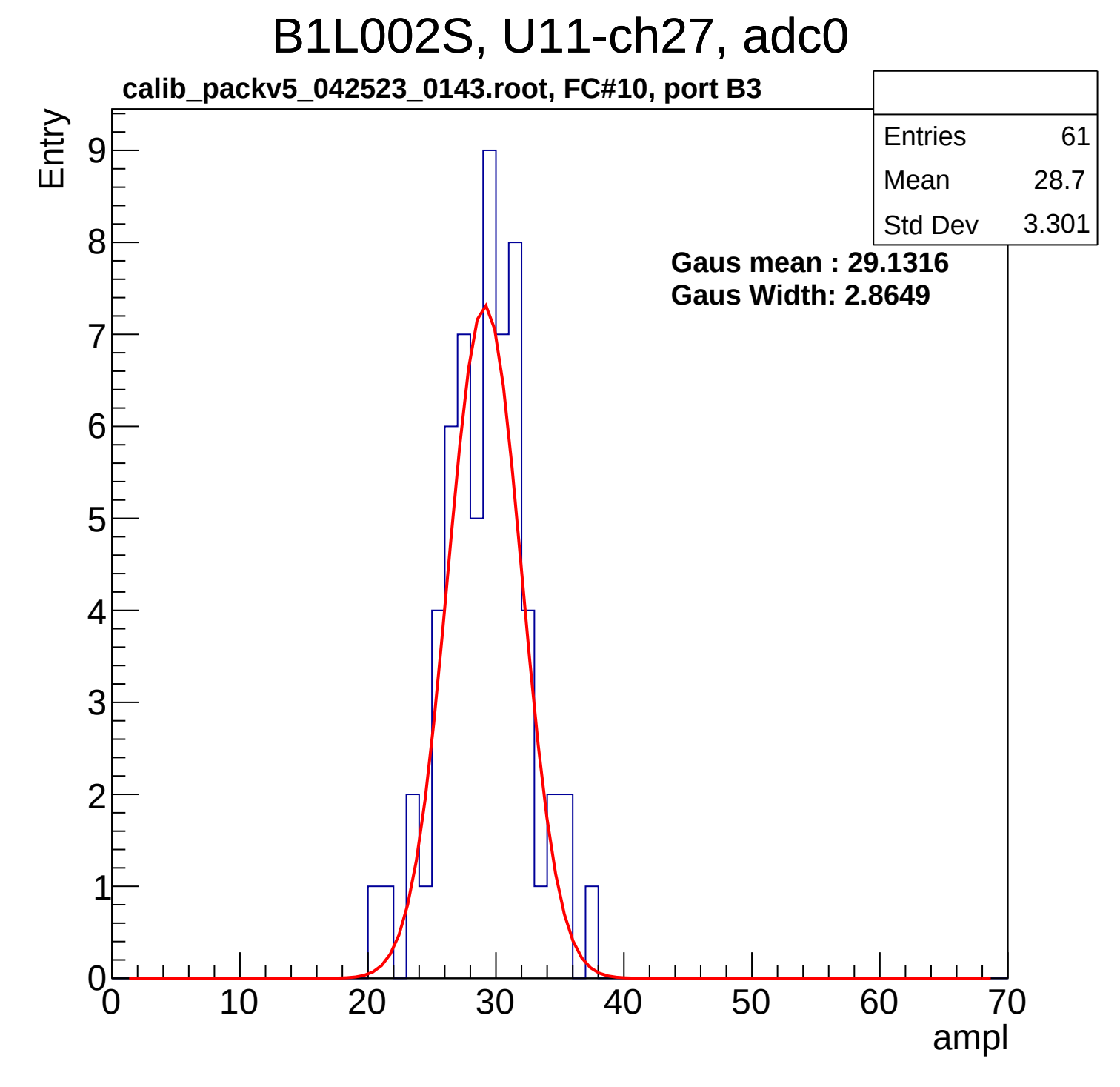
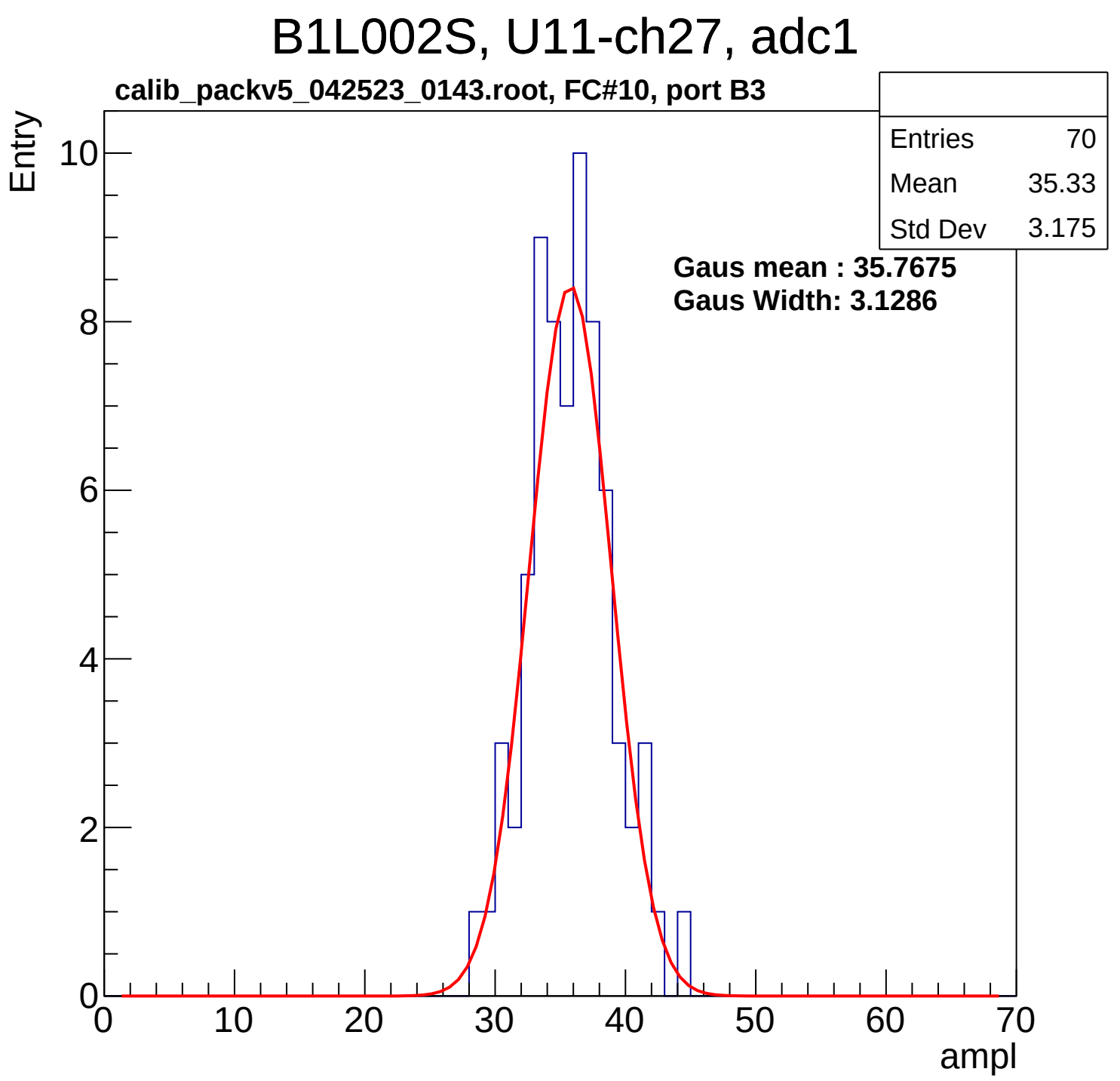

B1L002S, U11-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.7
Std Dev 3.301
Gaus mean : 29.1316
Gaus Width: 2.8649
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.33
Std Dev 3.175
Gaus mean : 35.7675
Gaus Width: 3.1286
Entry
ampl
0
2
4
6
8
10
0
10
20
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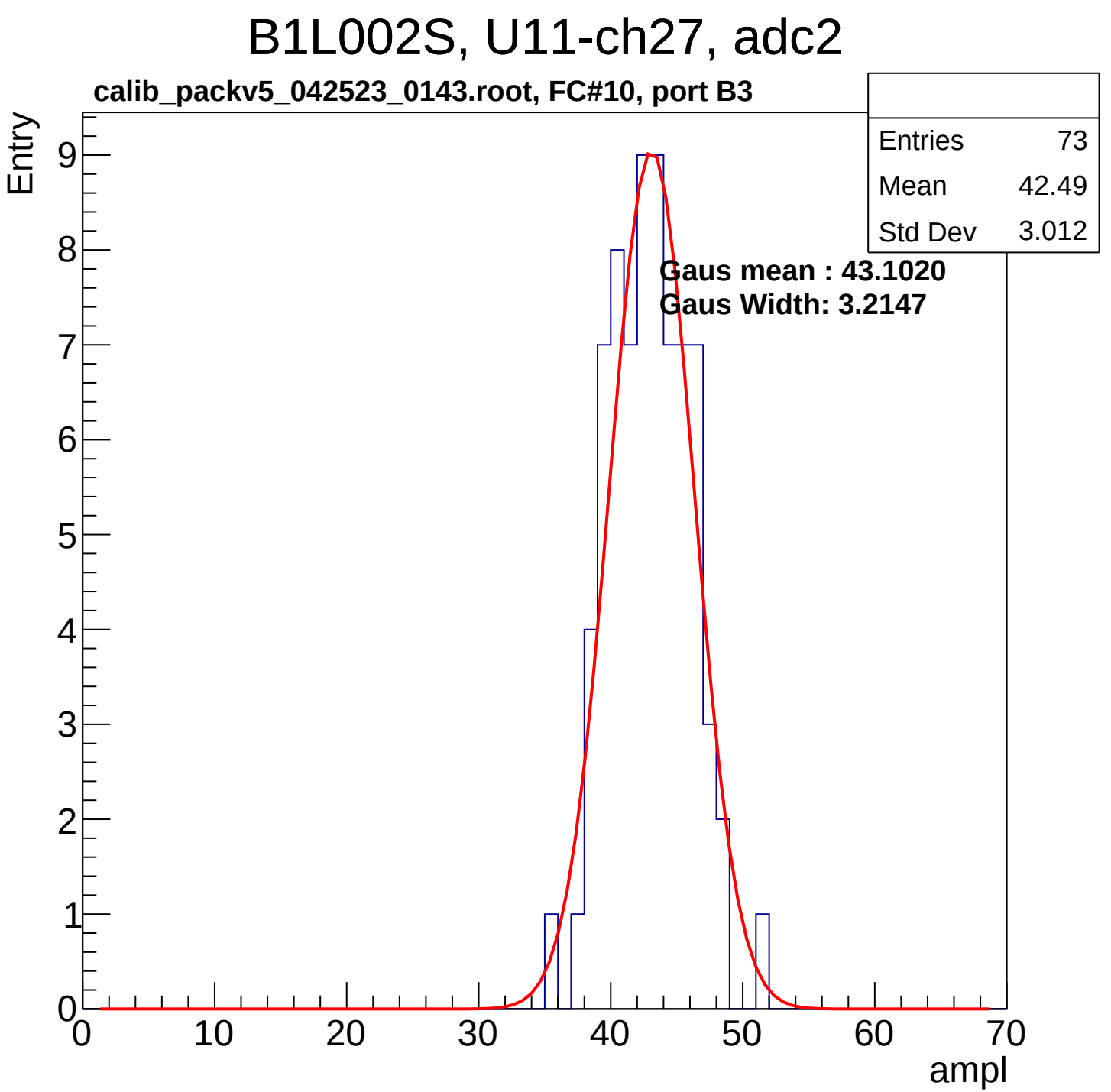
40
50
60
70


B1L002S, U11-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 42.49
Std Dev 3.012
Gaus mean : 43.1020
Gaus Width: 3.2147
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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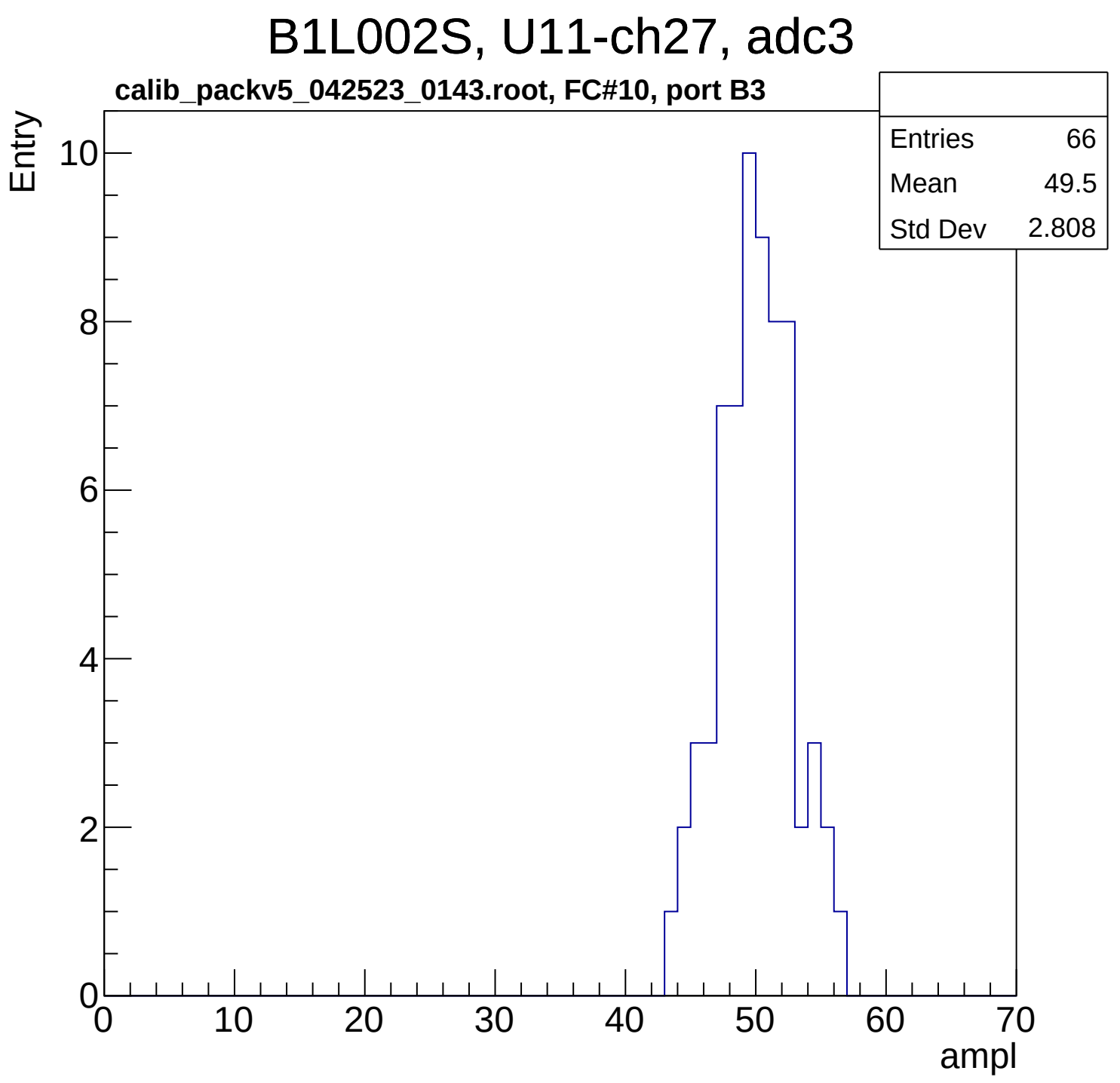
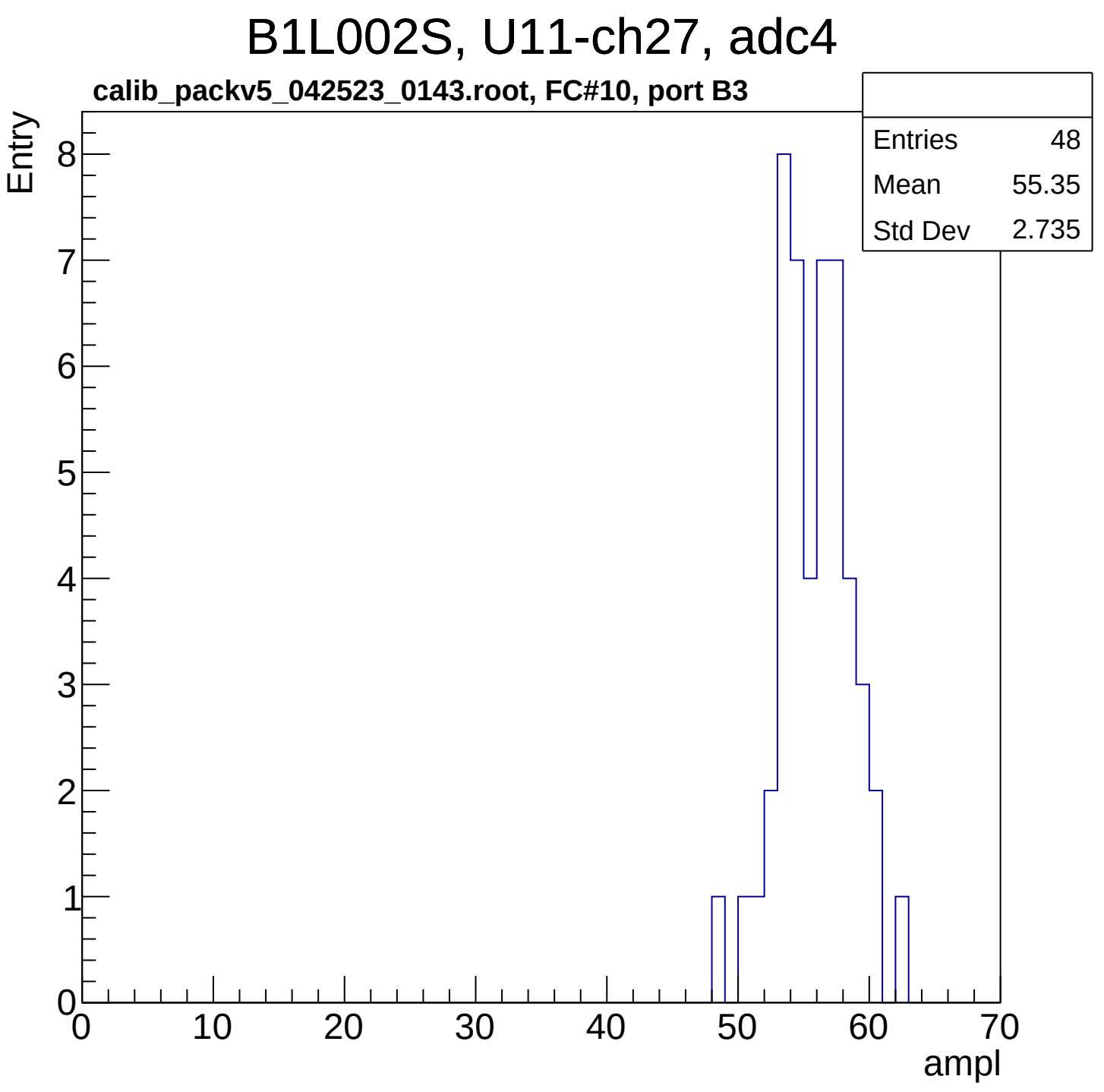
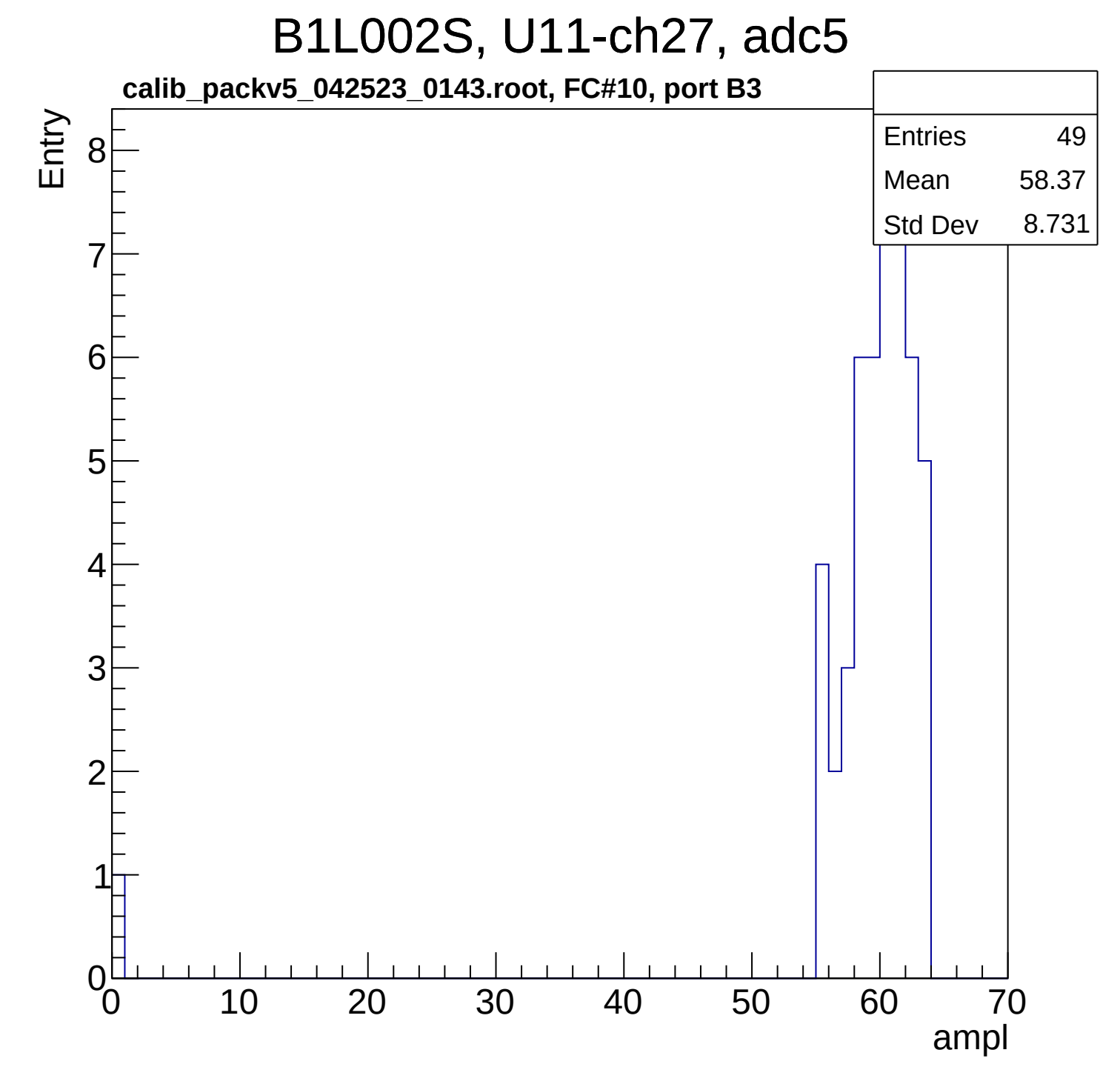

B1L002S, U11-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 49.5
Std Dev 2.808
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 55.35
Std Dev 2.735
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.37
Std Dev 8.731
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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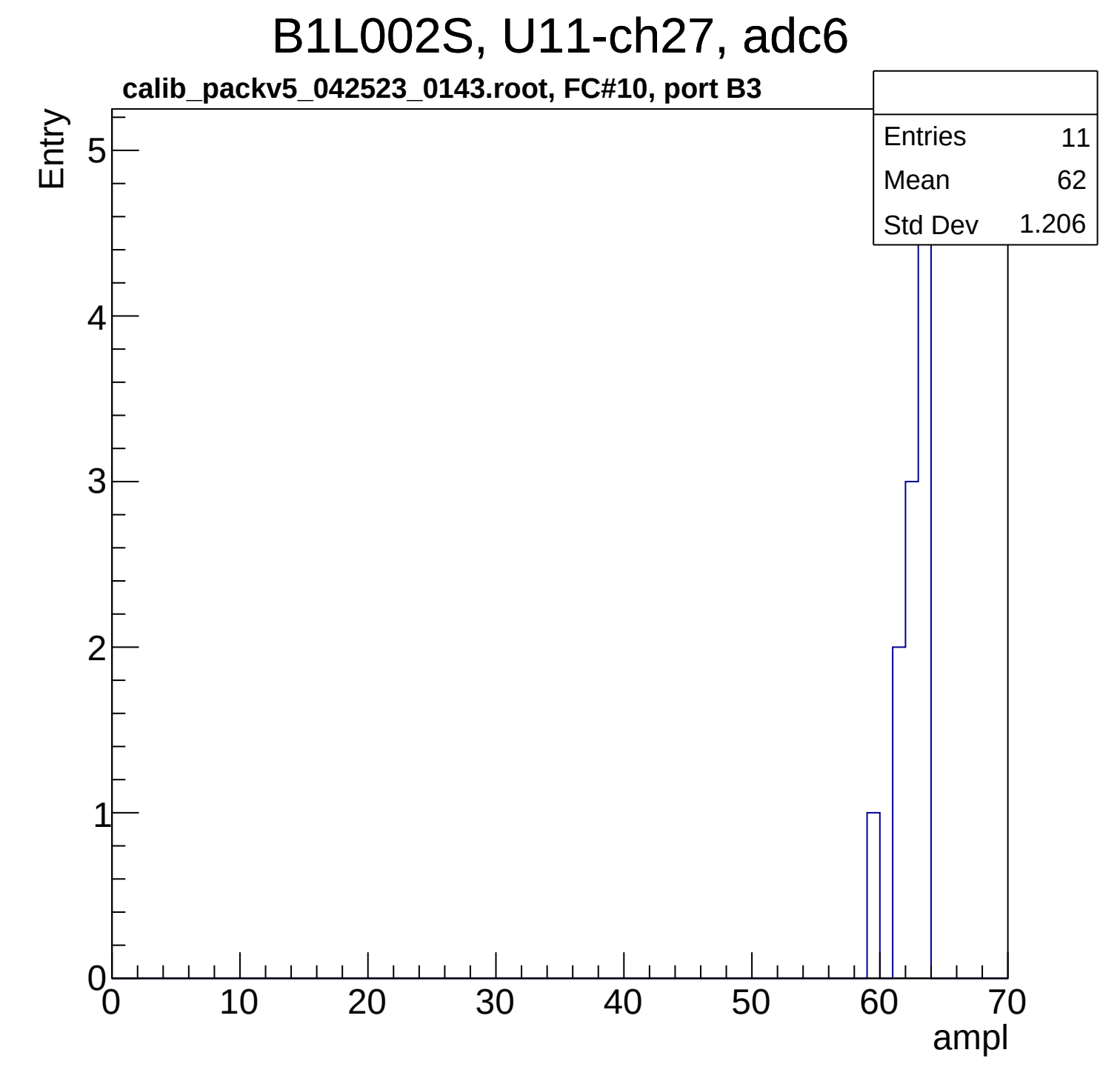
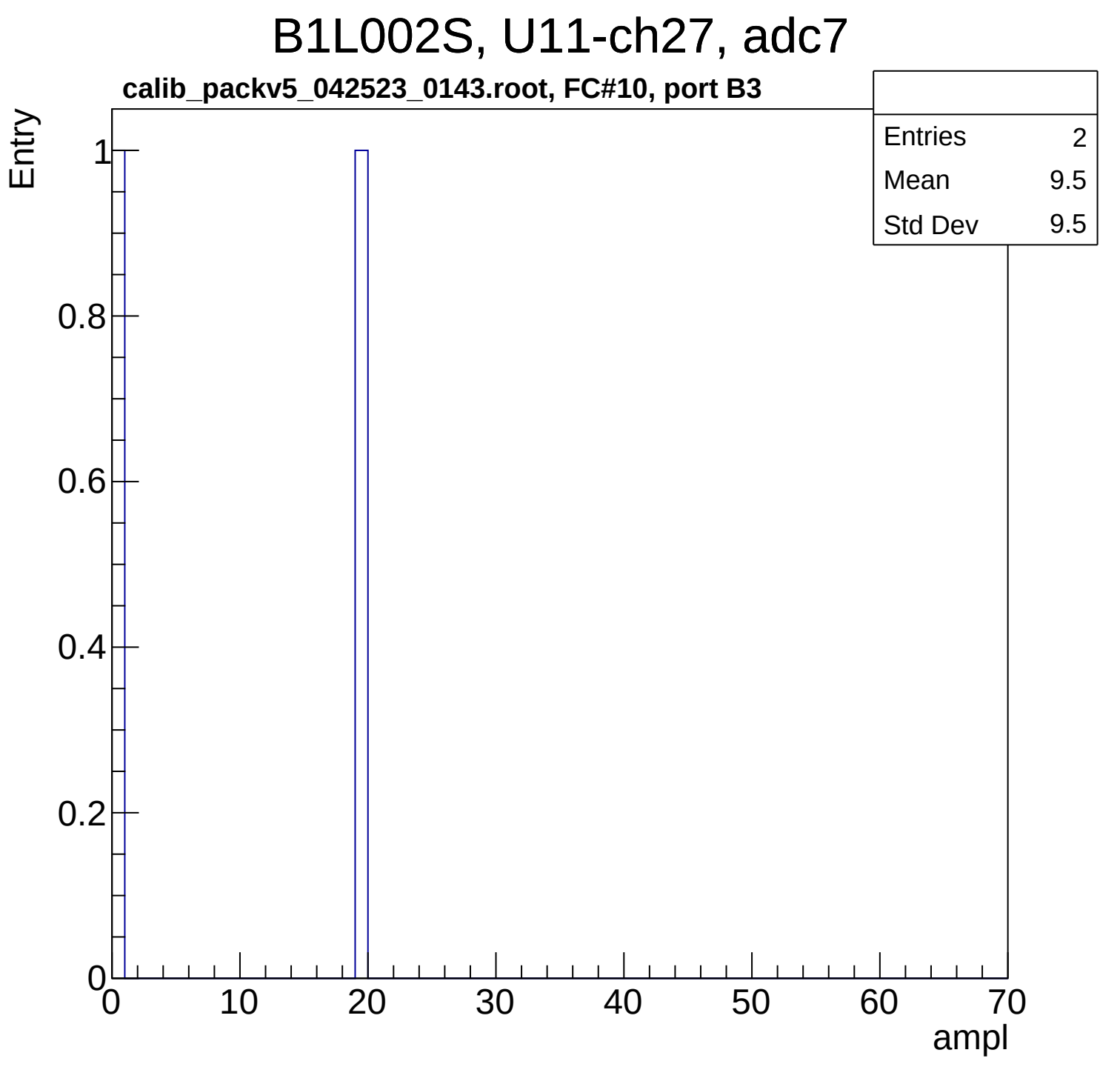
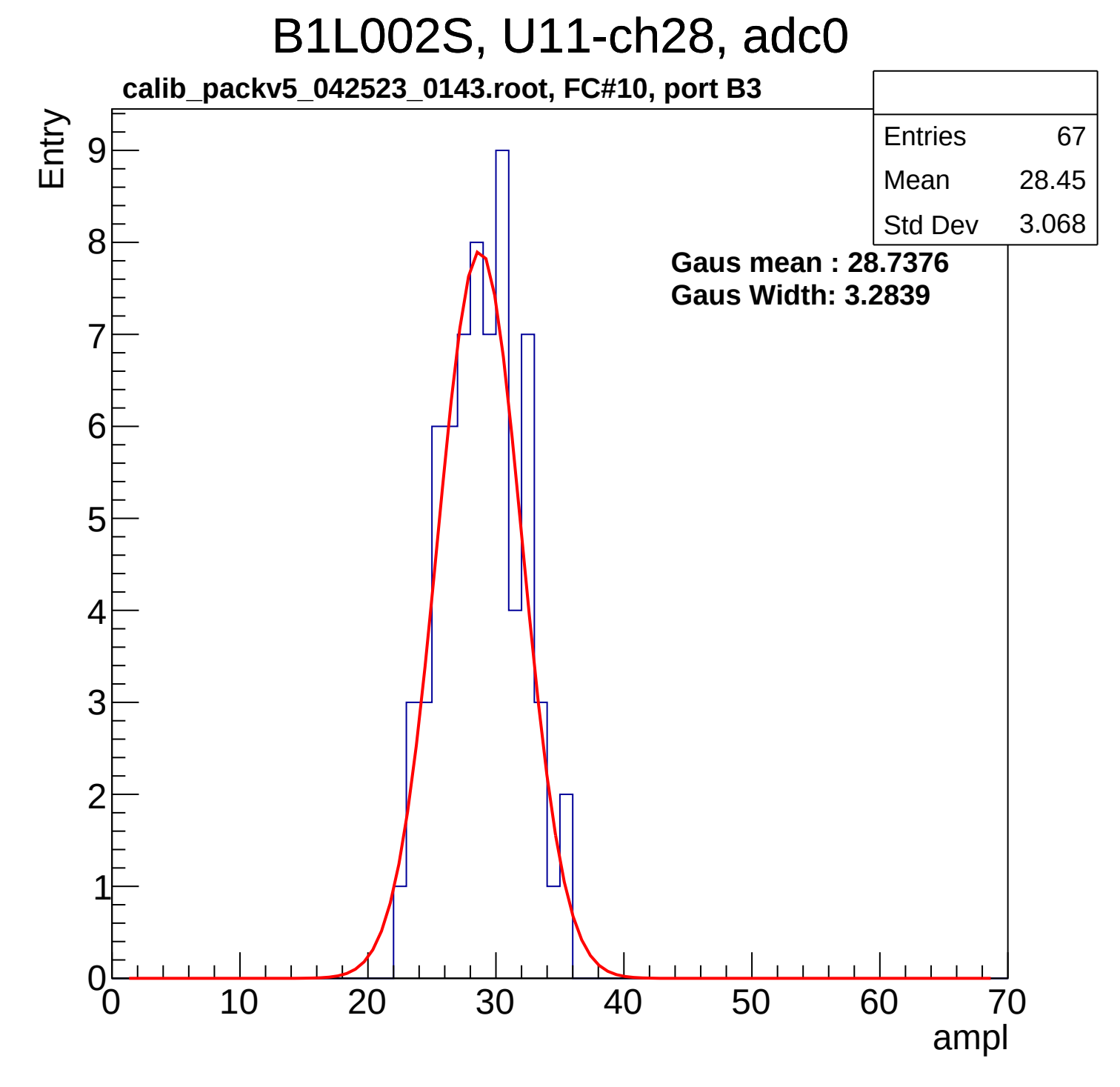

B1L002S, U11-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62
Std Dev 1.206
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.45
Std Dev 3.068
Gaus mean : 28.7376
Gaus Width: 3.2839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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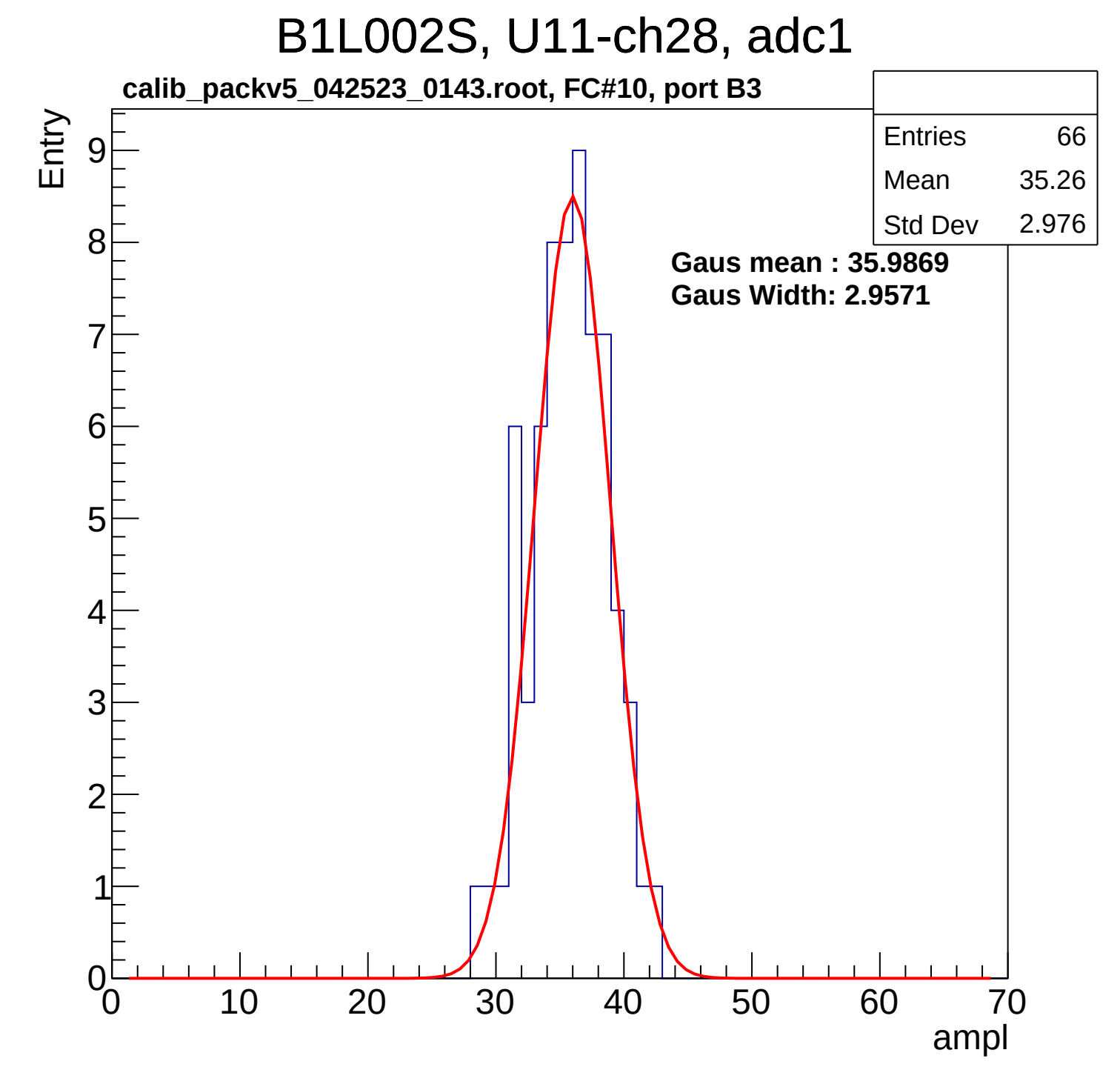
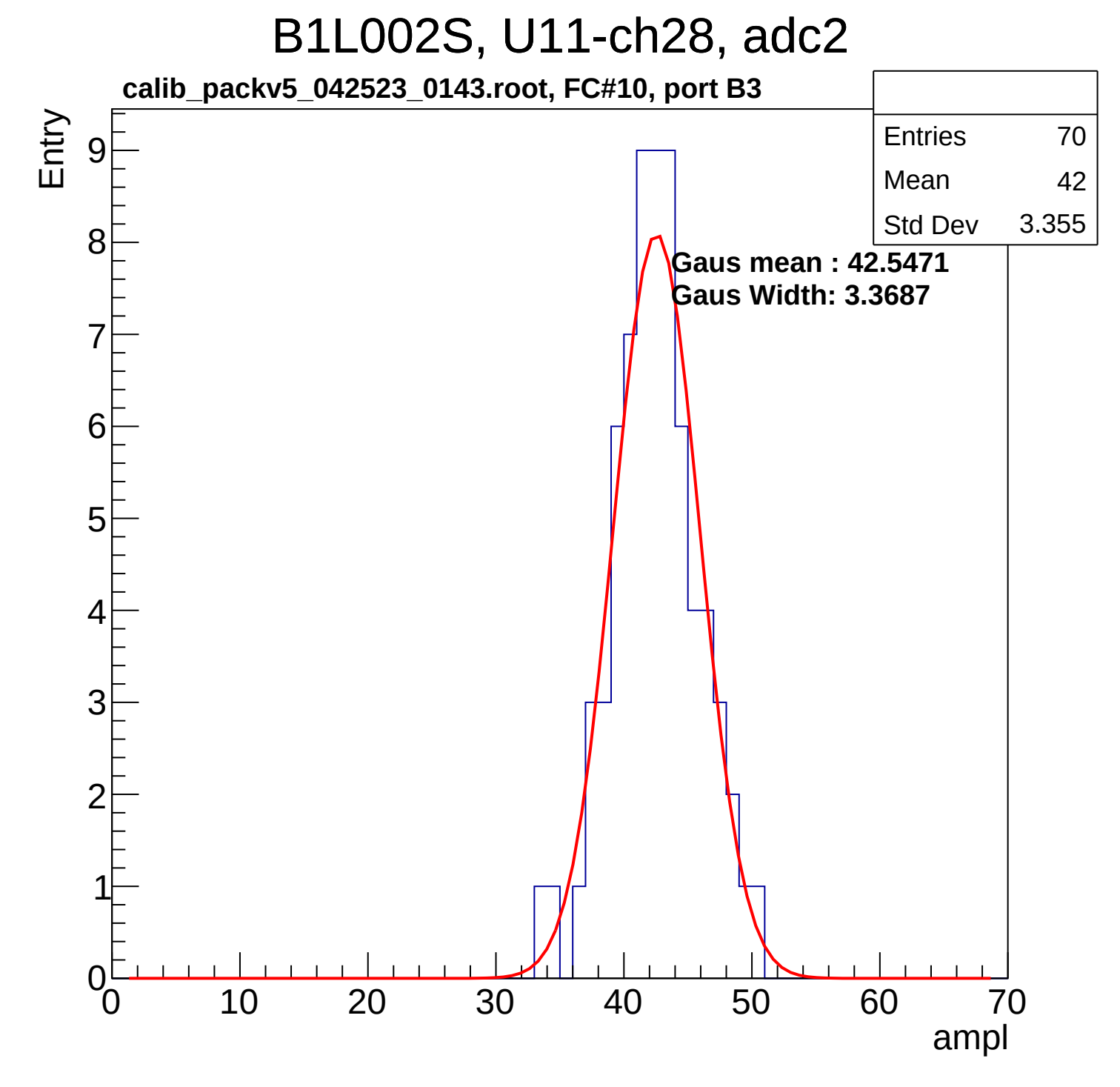
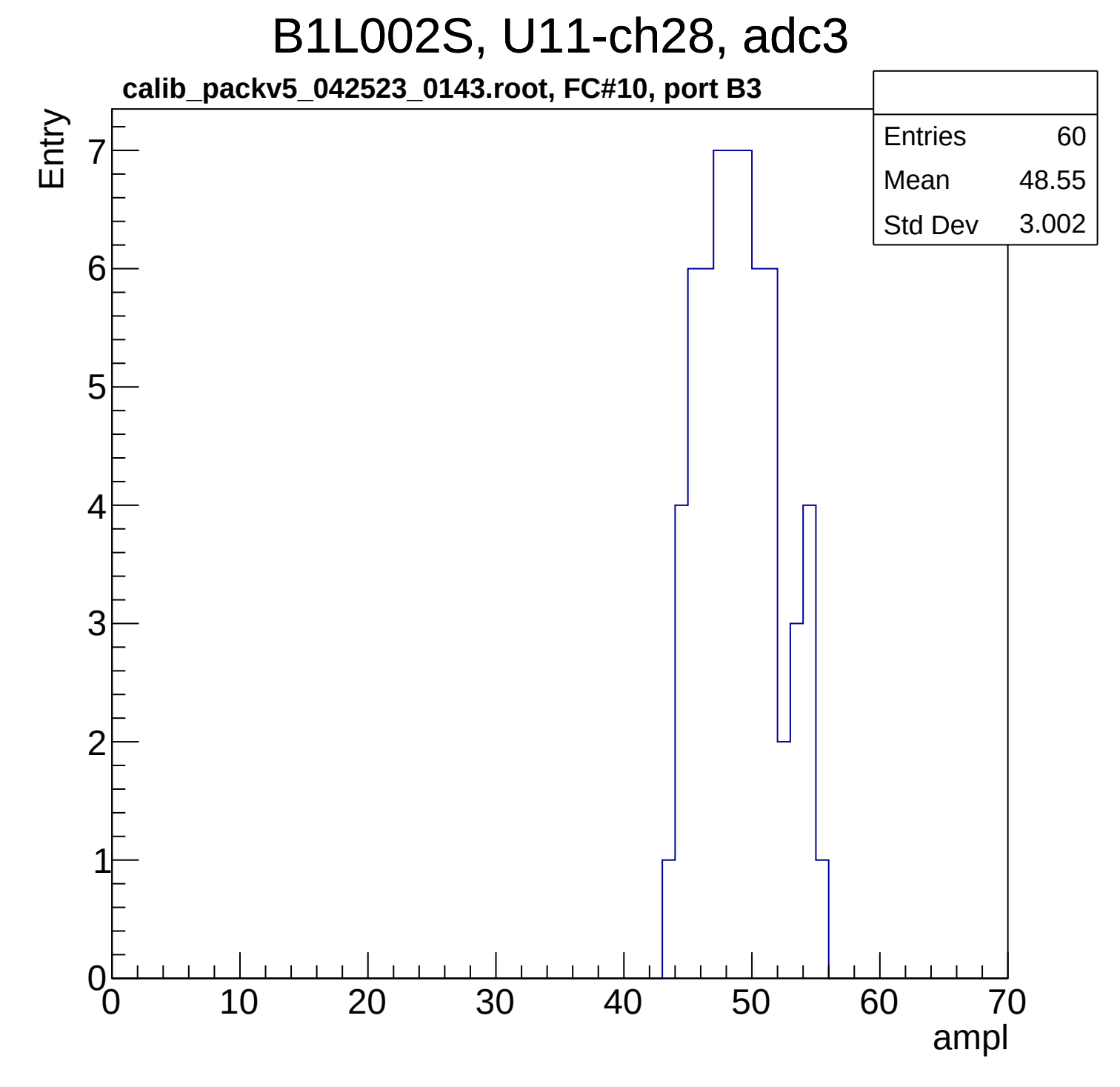
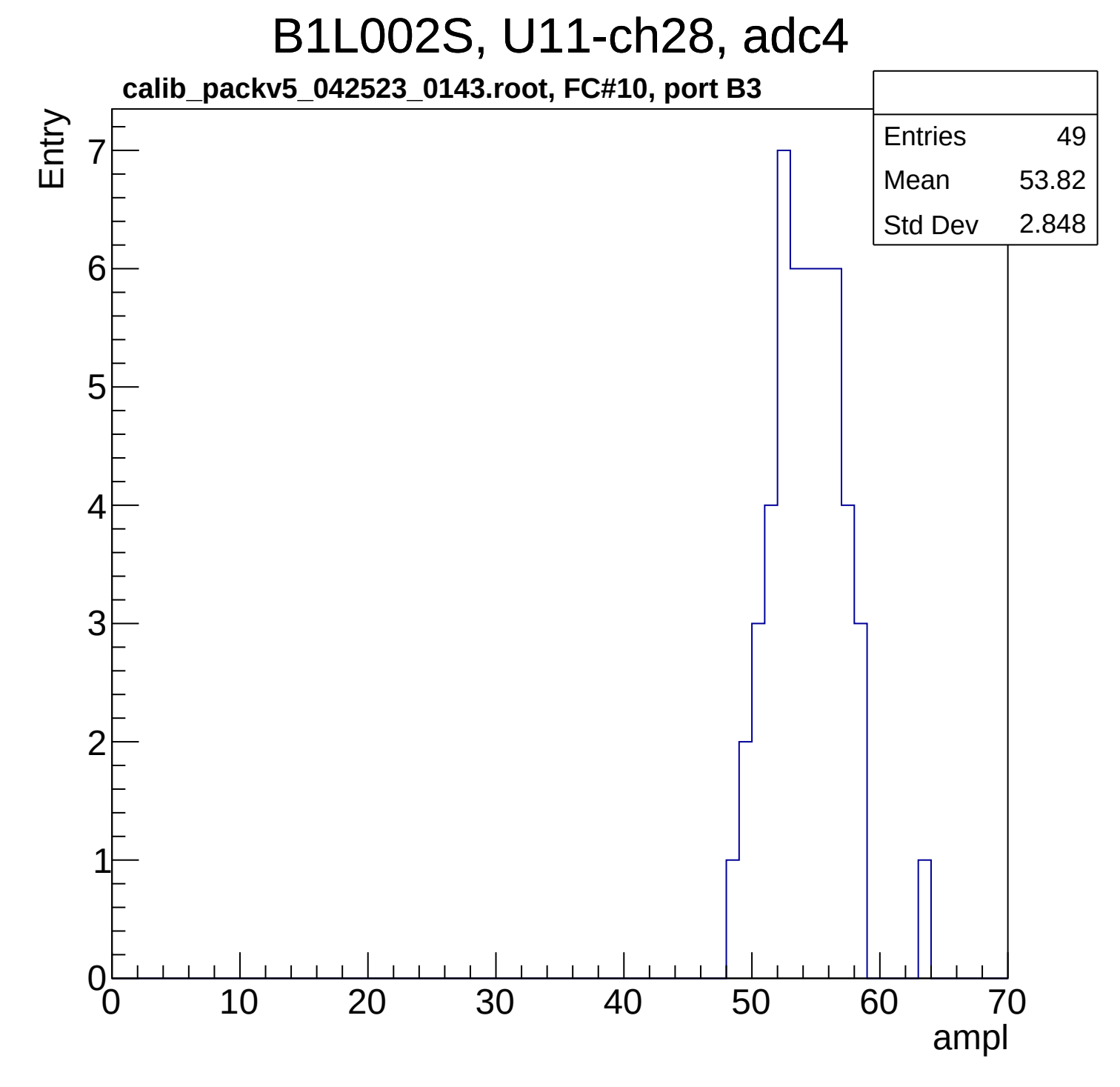
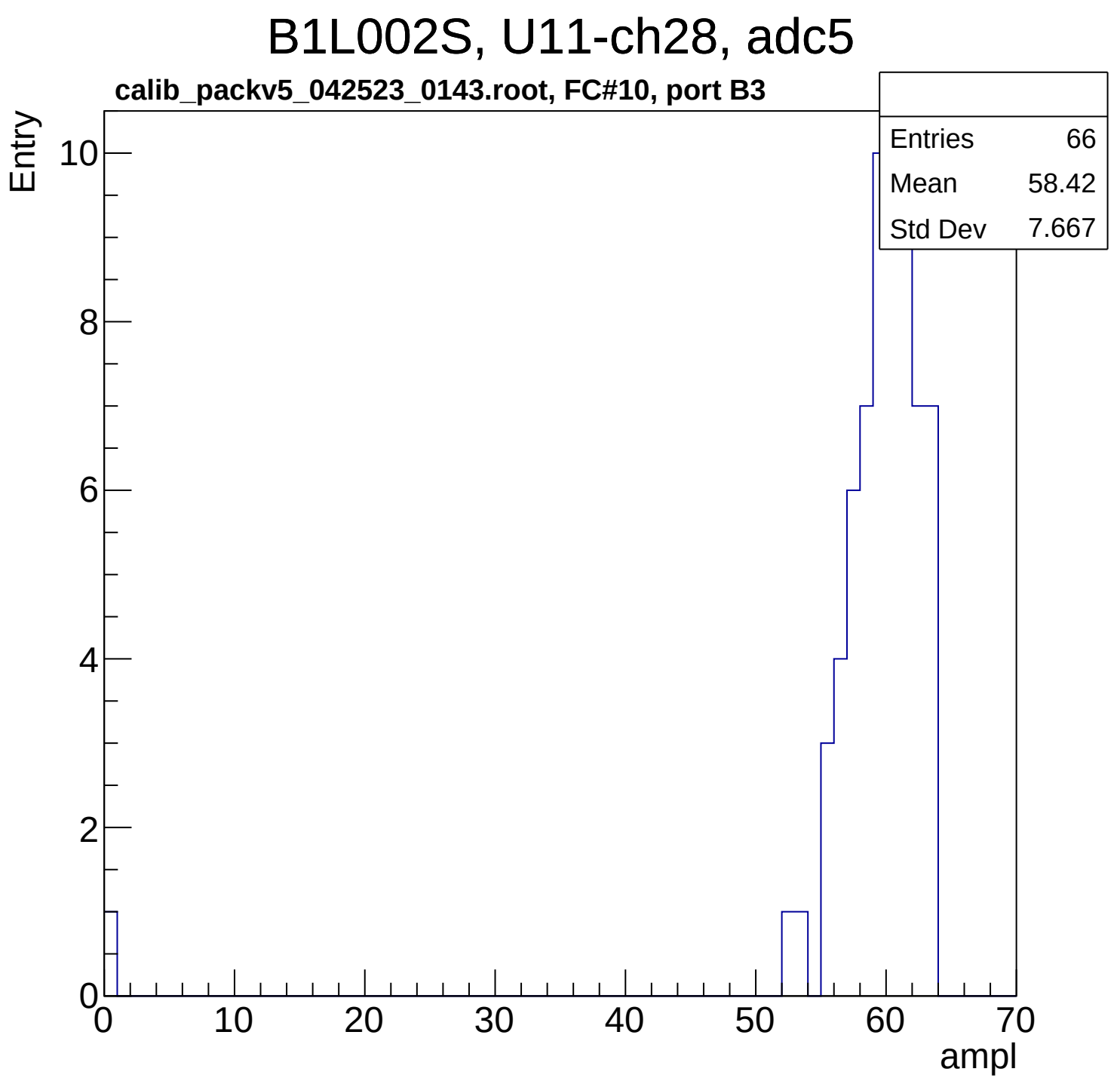
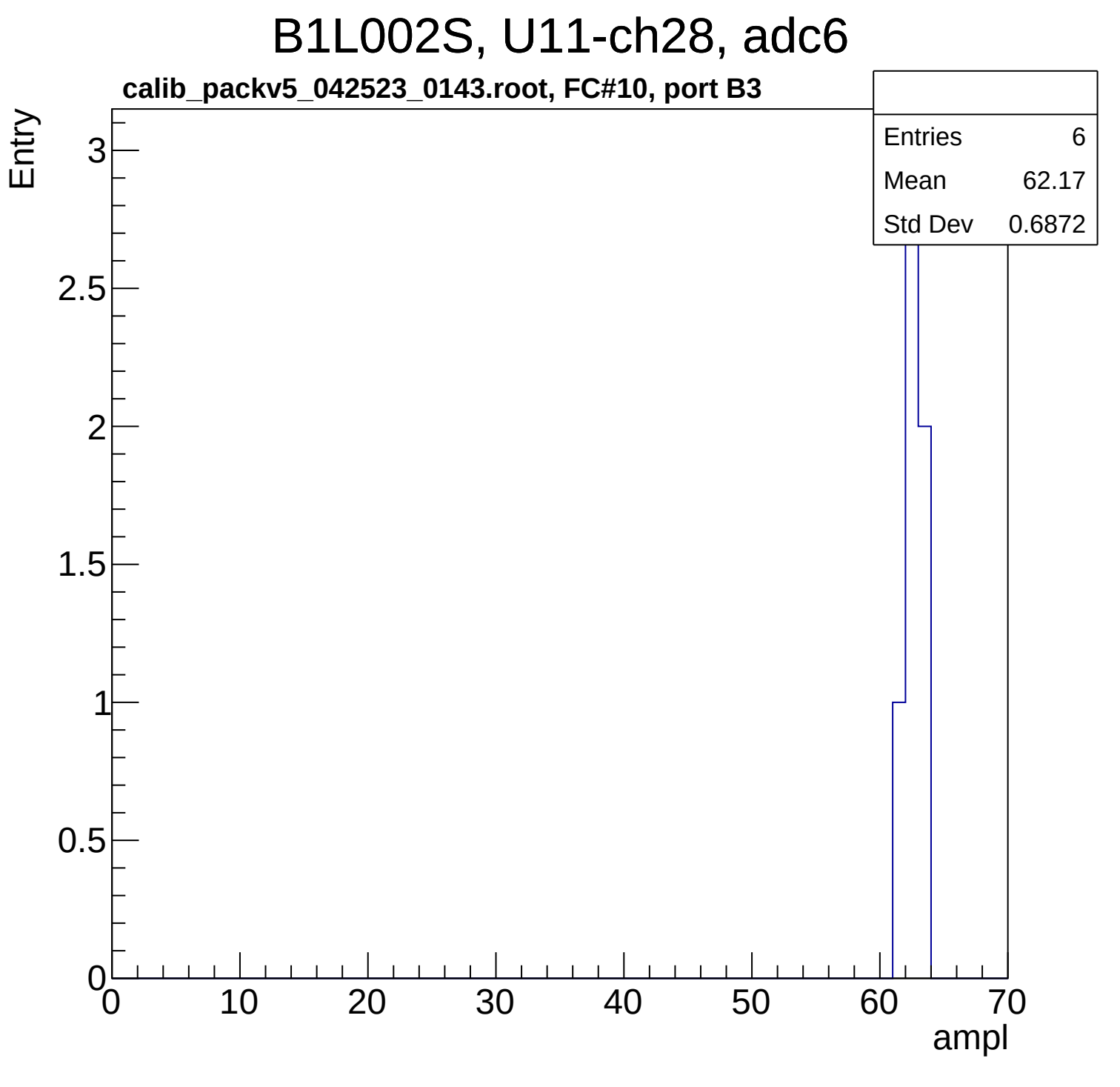

B1L002S, U11-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35.26
Std Dev 2.976
Gaus mean : 35.9869
Gaus Width: 2.9571
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

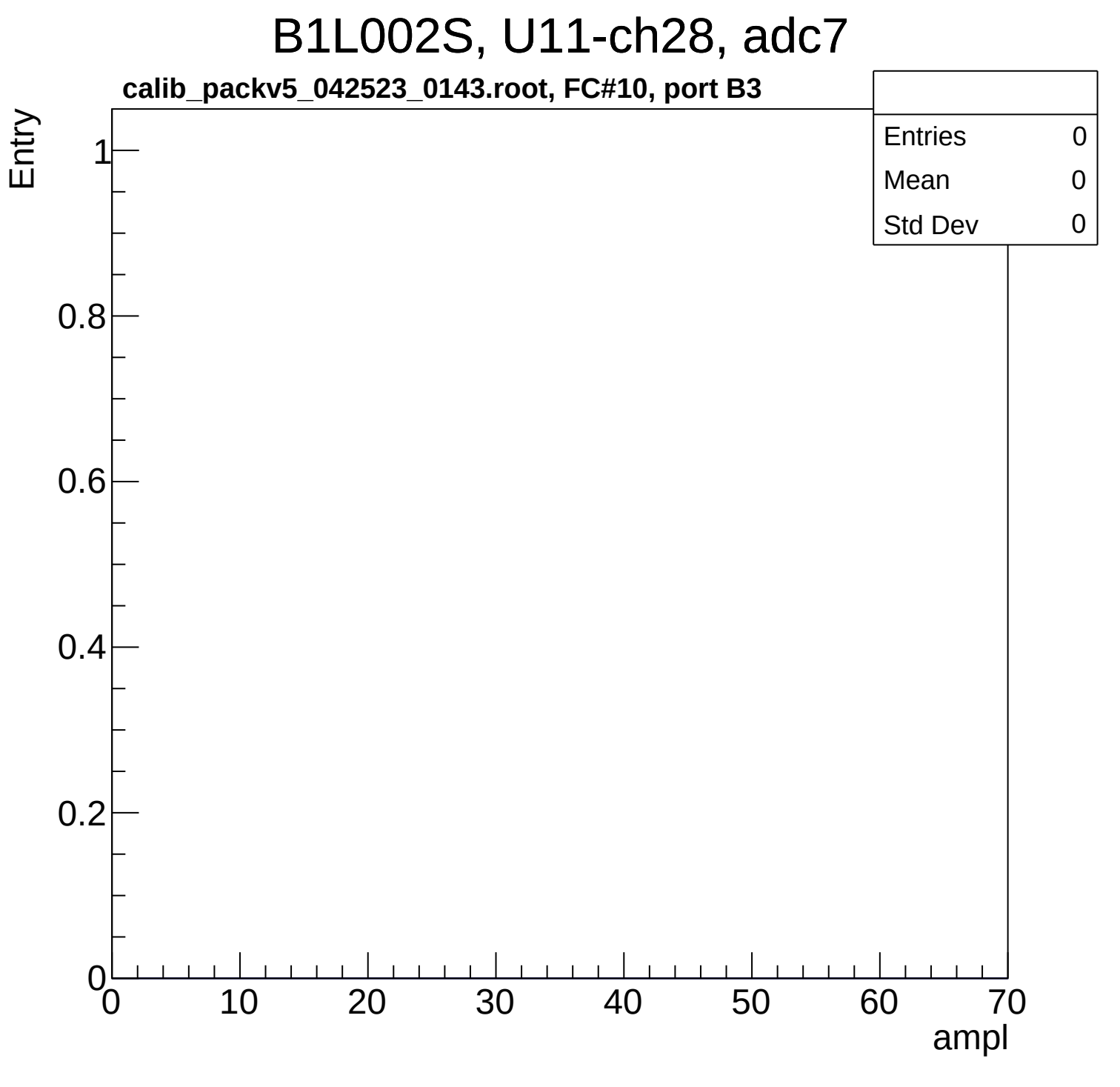

B1L002S, U11-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 42
Std Dev 3.355
Gaus mean : 42.5471
Gaus Width: 3.3687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

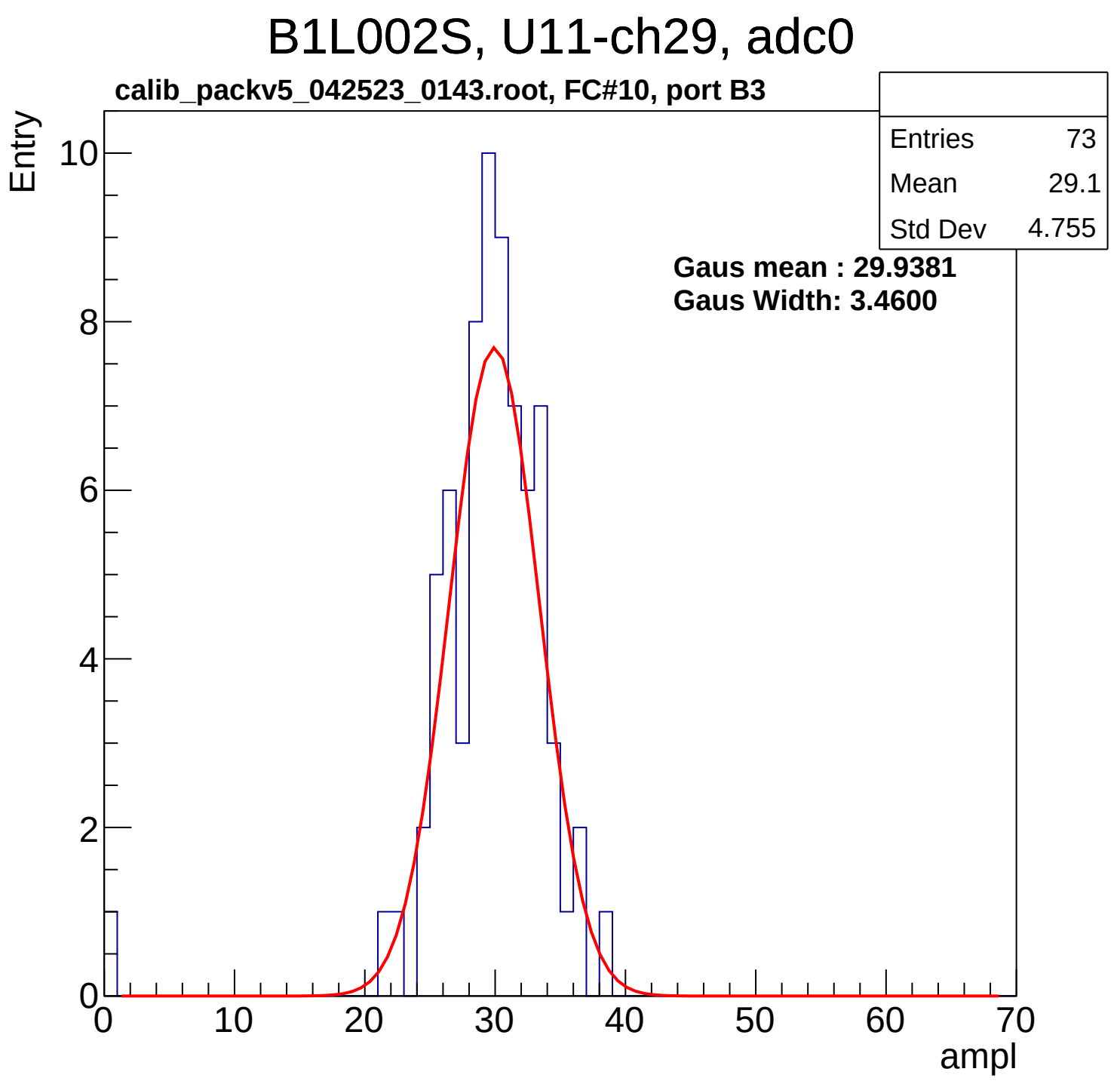

B1L002S, U11-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 48.55
Std Dev 3.002
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

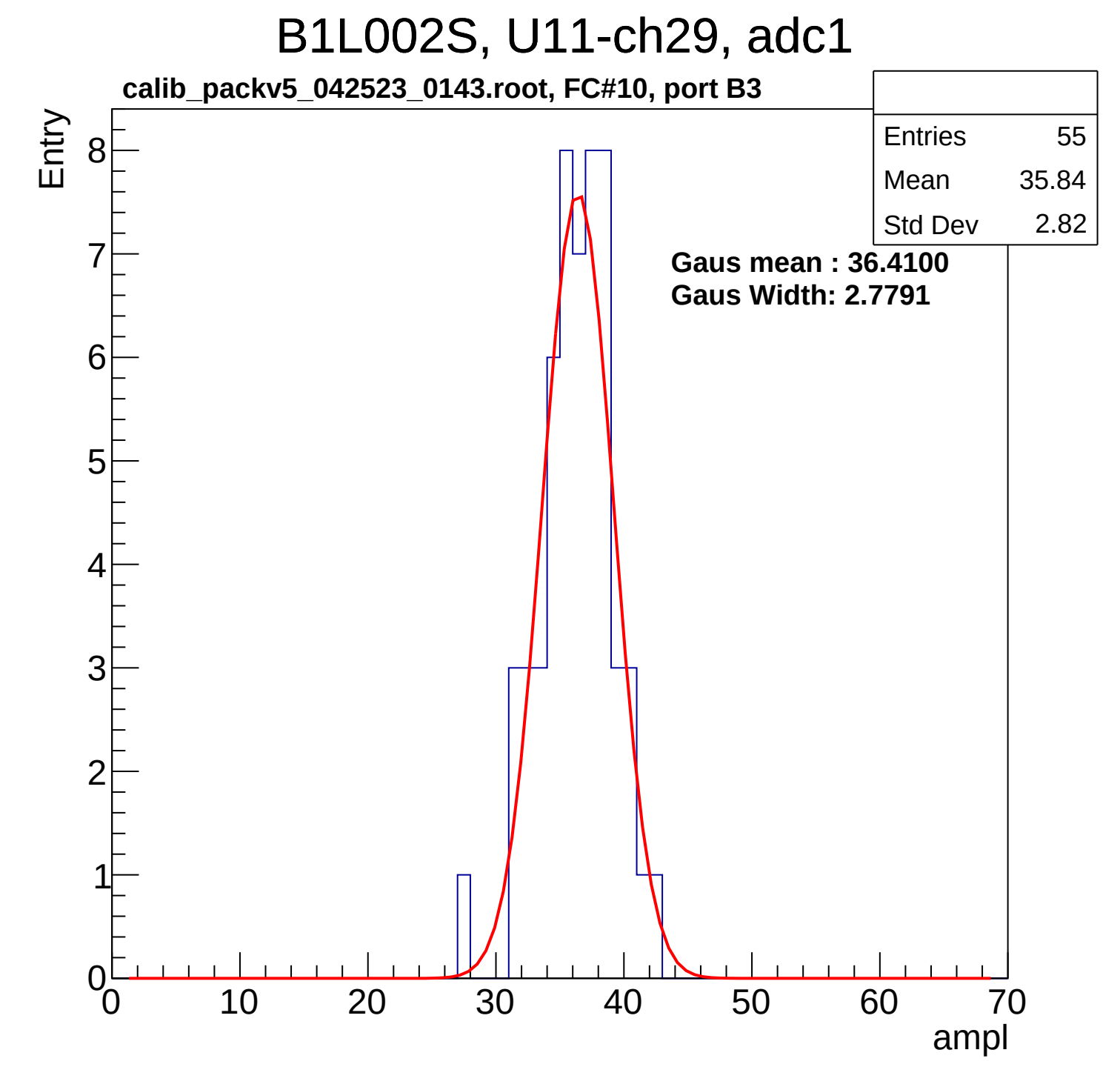

B1L002S, U11-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.82
Std Dev 2.848
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

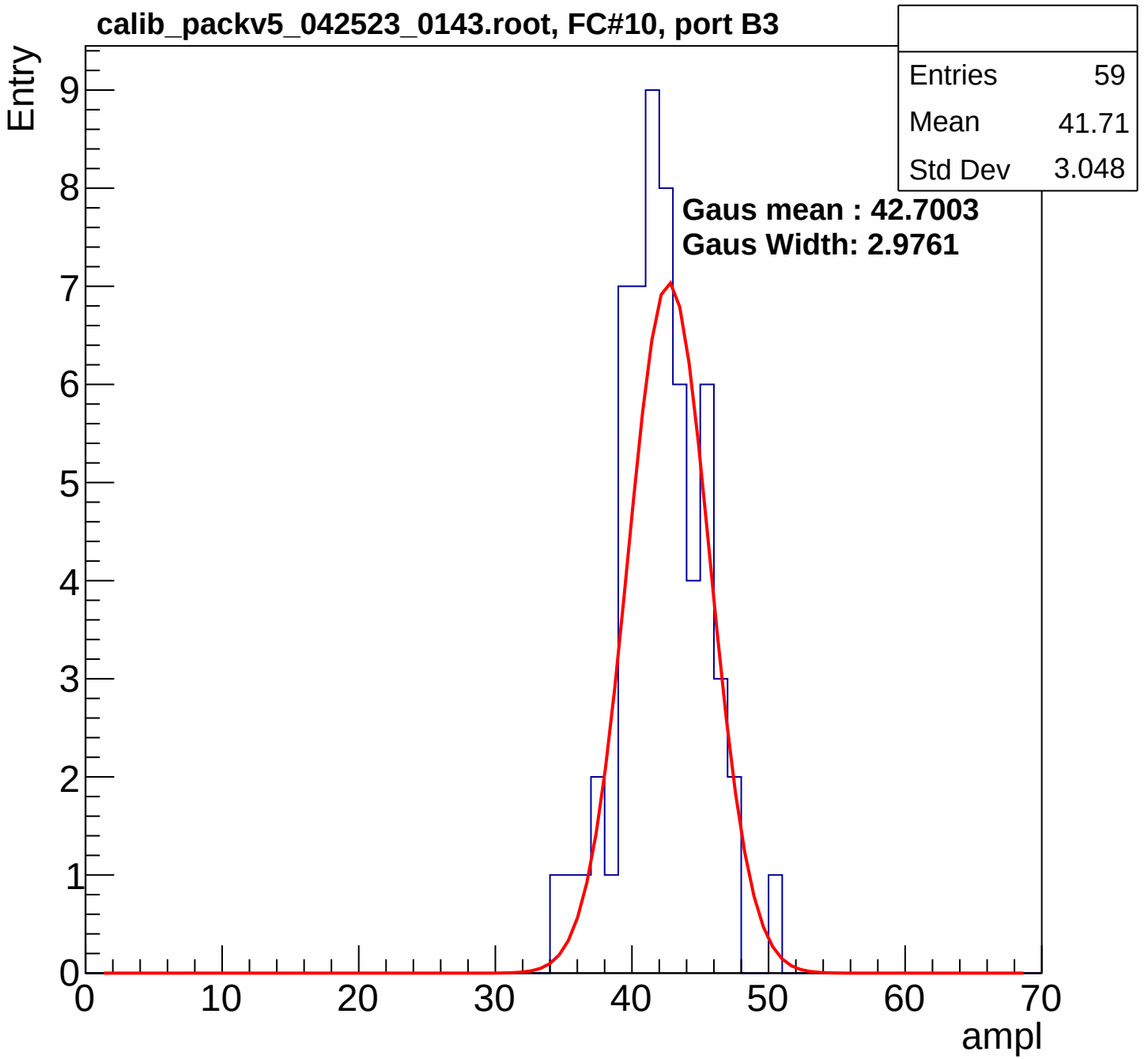

B1L002S, U11-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 58.42
Std Dev 7.667
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.1
Std Dev 4.755
Gaus mean : 29.9381
Gaus Width: 3.4600
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.84
Std Dev 2.82
Gaus mean : 36.4100
Gaus Width: 2.7791
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.71
Std Dev 3.048
Gaus mean : 42.7003
Gaus Width: 2.9761
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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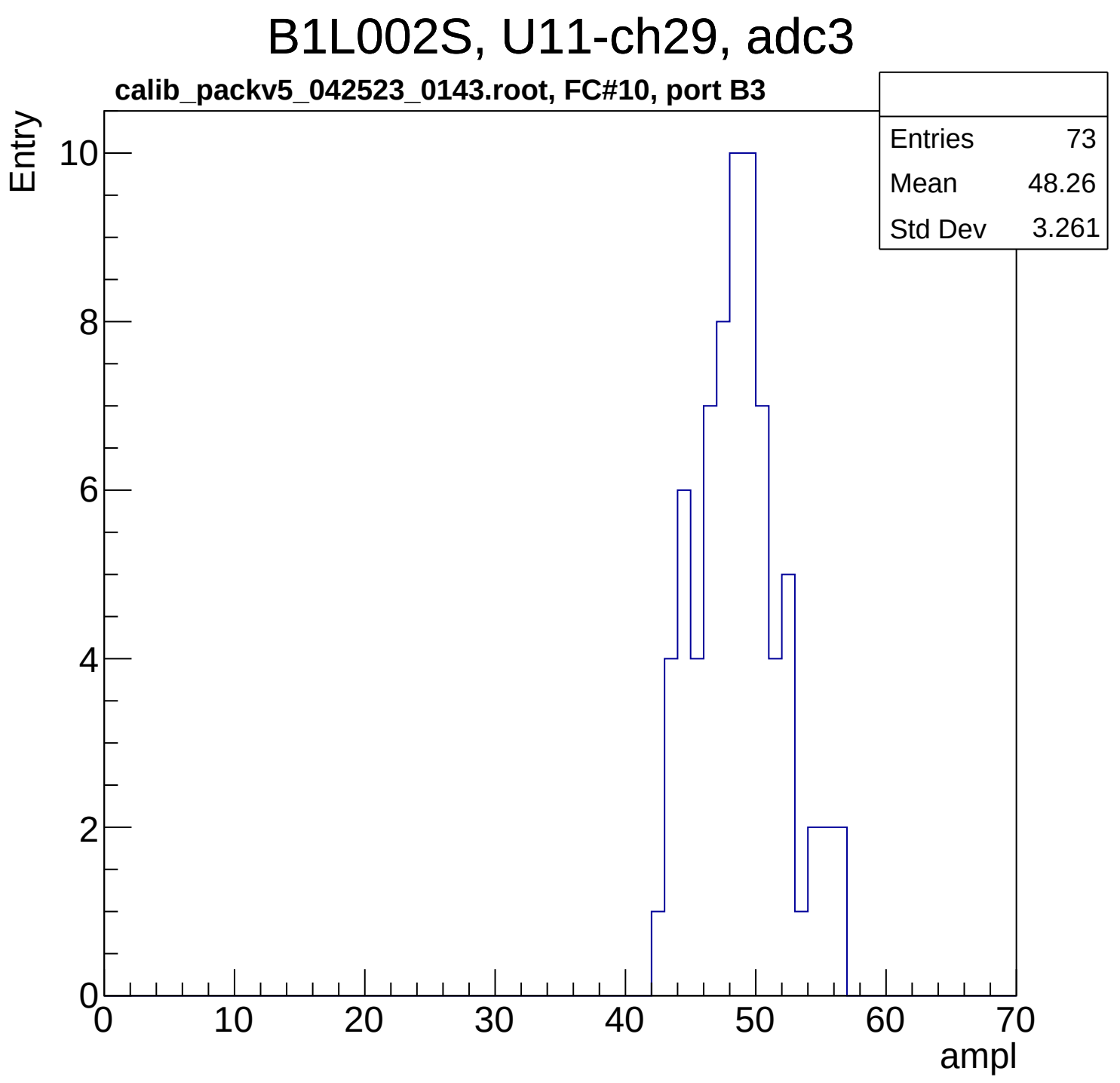
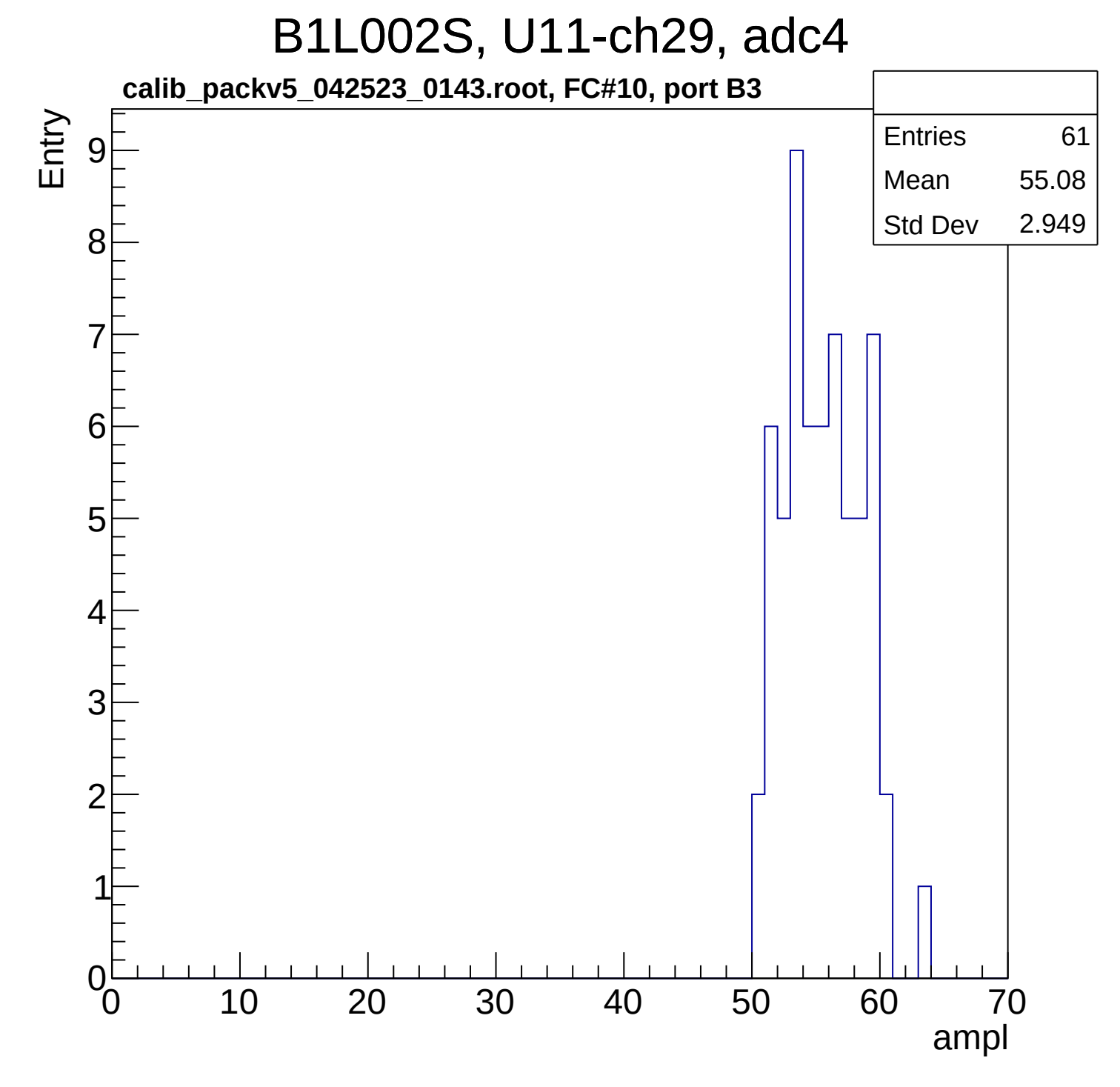

B1L002S, U11-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 48.26
Std Dev 3.261
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 55.08
Std Dev 2.949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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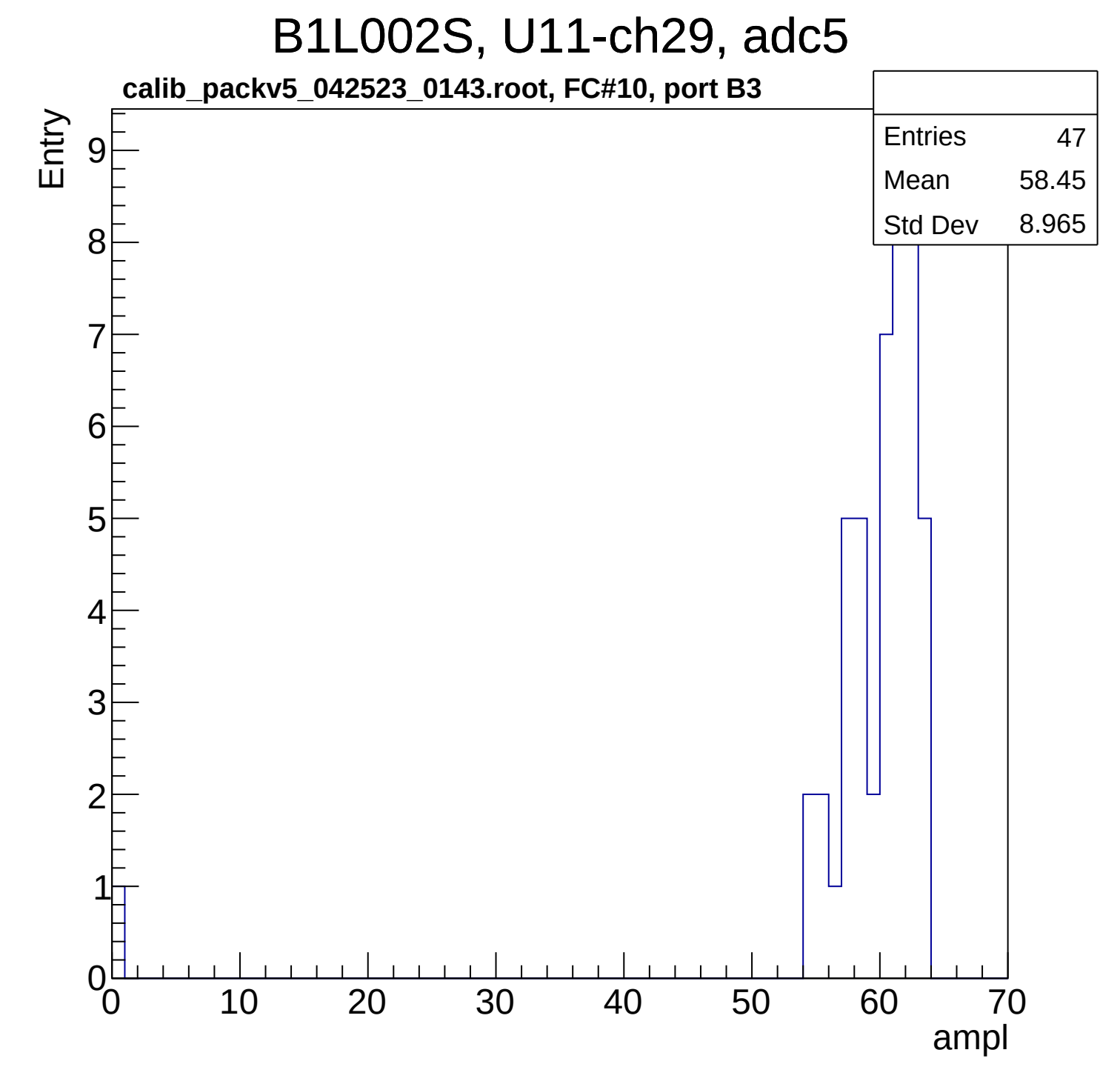
60
70


B1L002S, U11-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.45
Std Dev 8.965
Entry
ampl
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1
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3
4
5
6
7
8
9
0
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40
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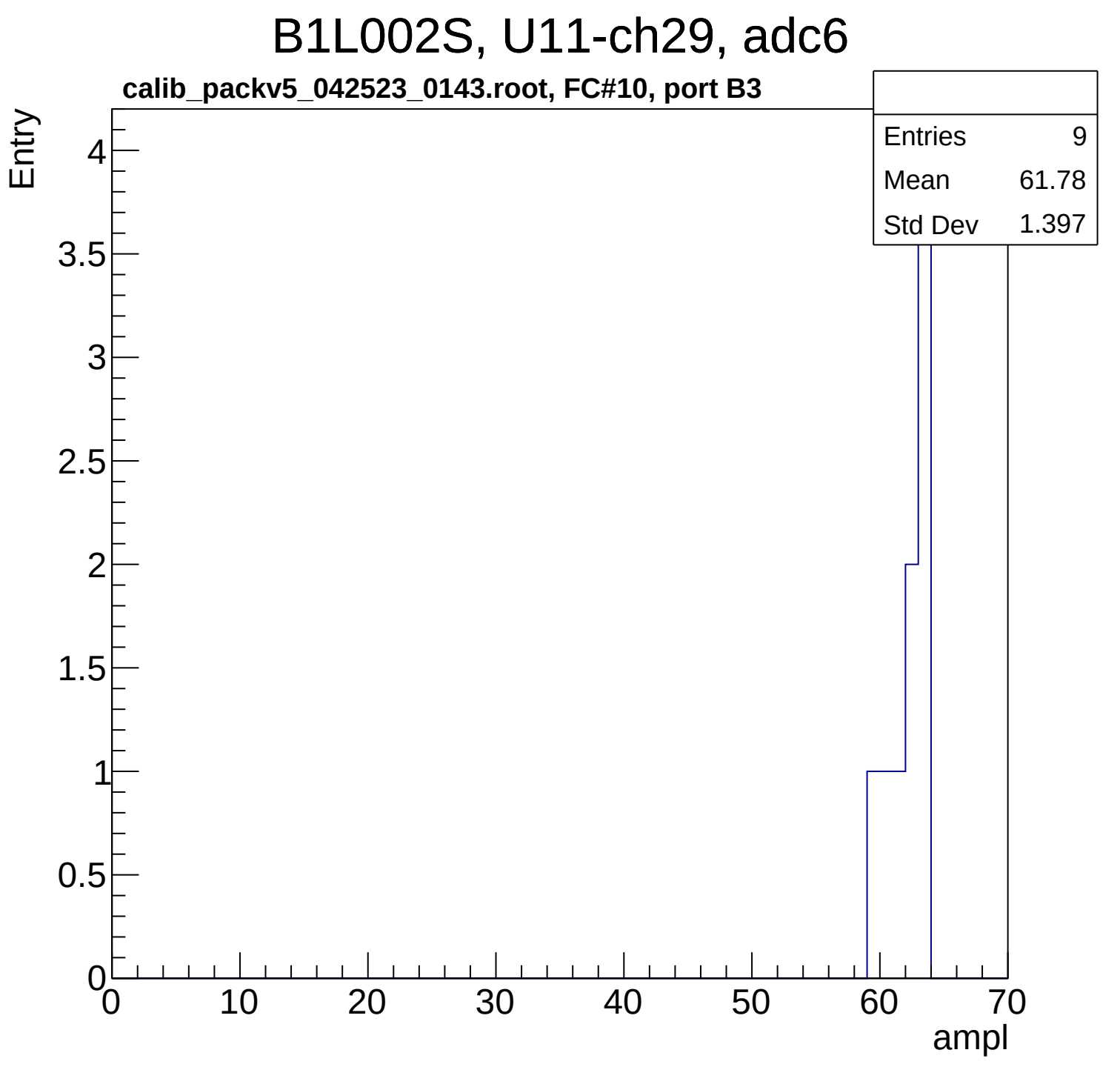
60
70


B1L002S, U11-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 61.78
Std Dev 1.397
Entry
ampl
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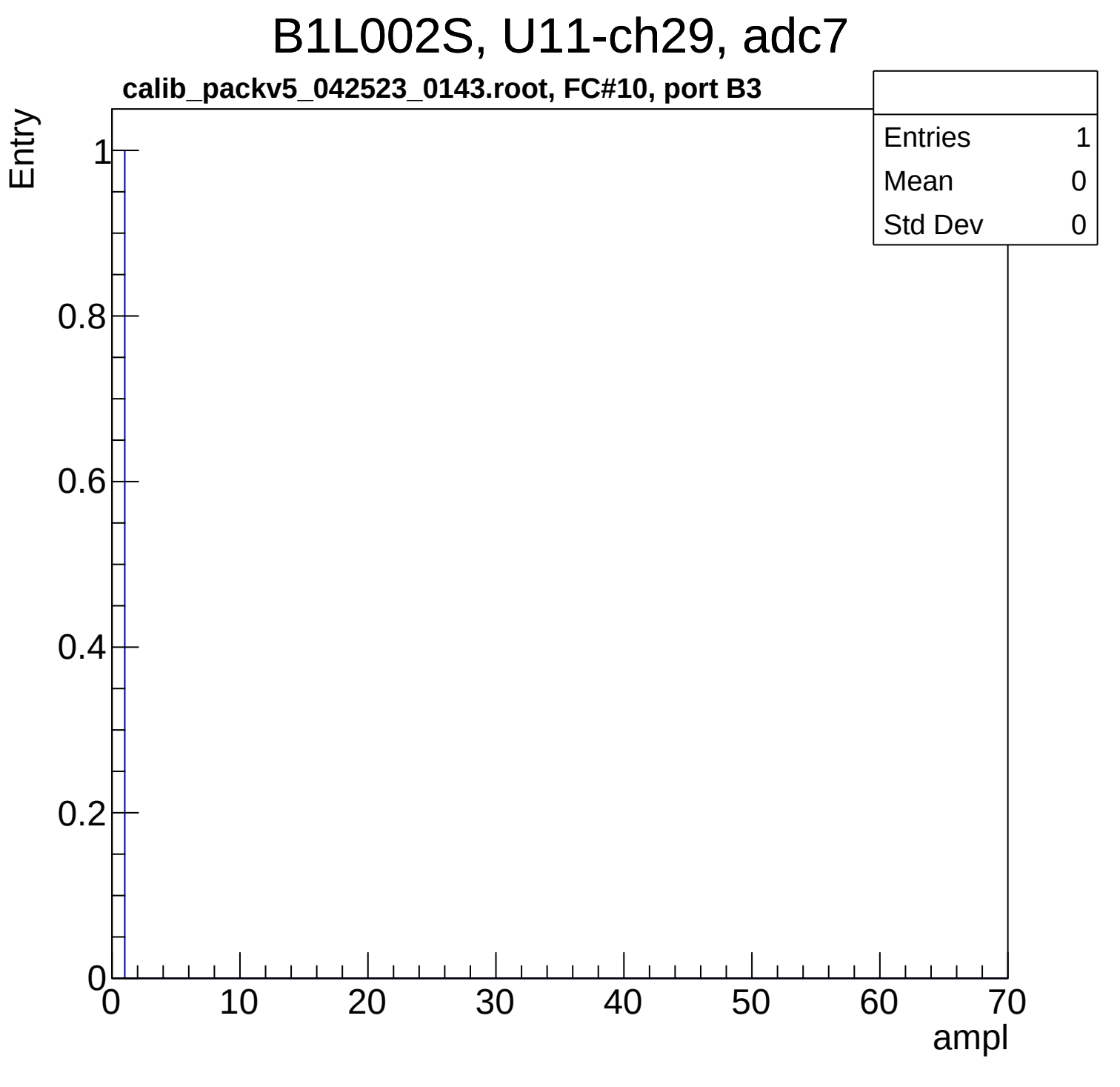
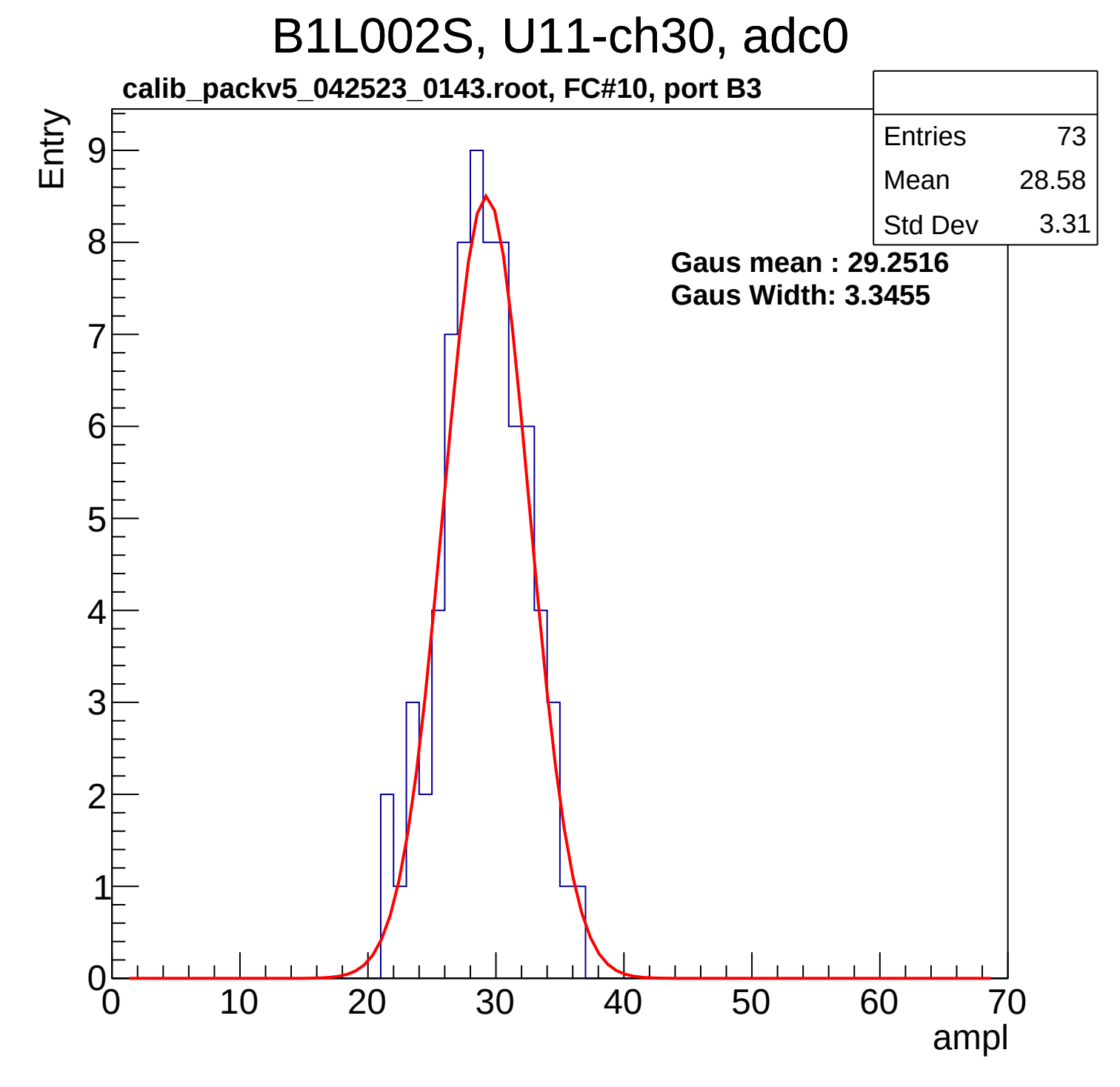
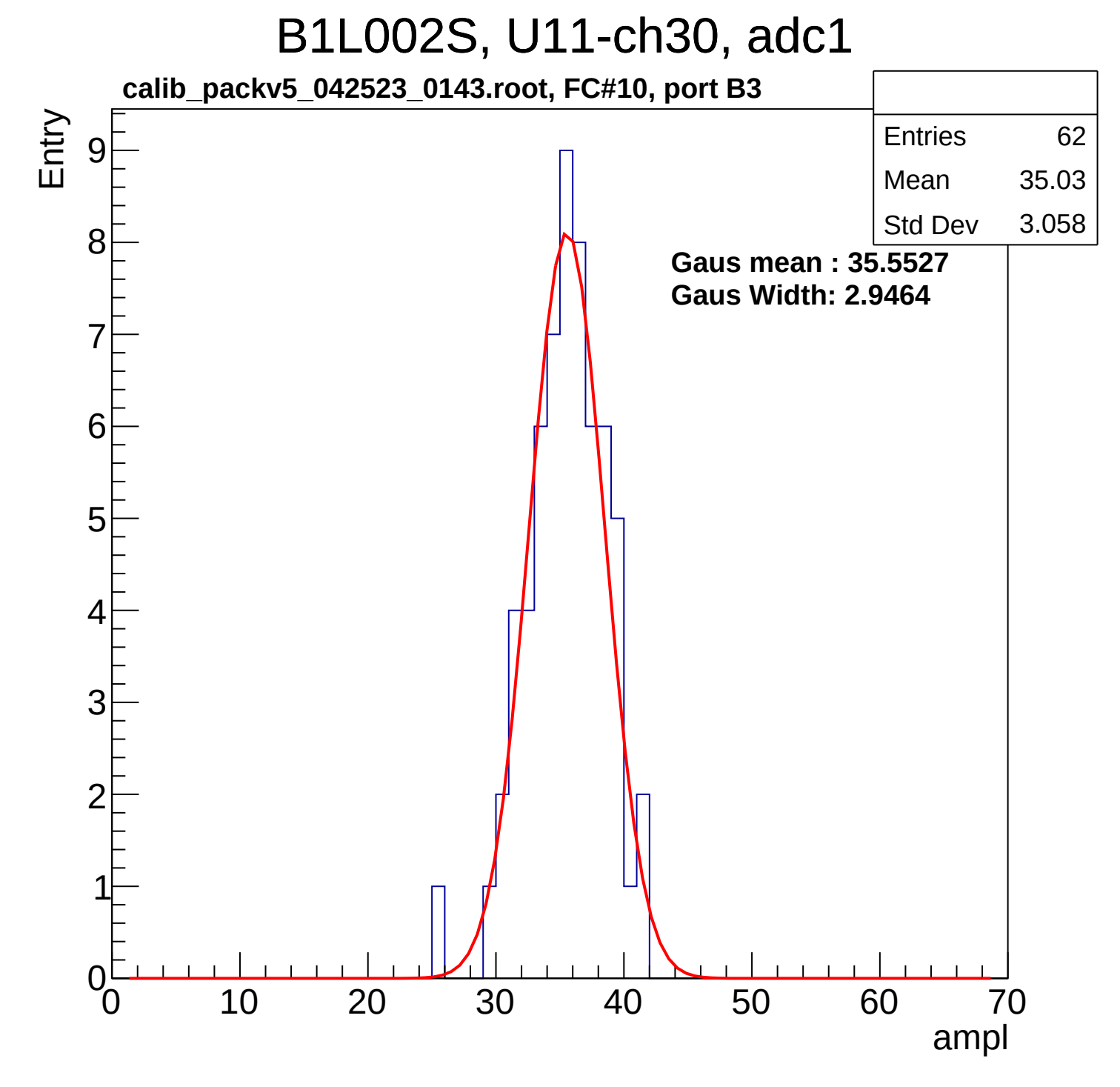
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.58
Std Dev 3.31
Gaus mean : 29.2516
Gaus Width: 3.3455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.03
Std Dev 3.058
Gaus mean : 35.5527
Gaus Width: 2.9464
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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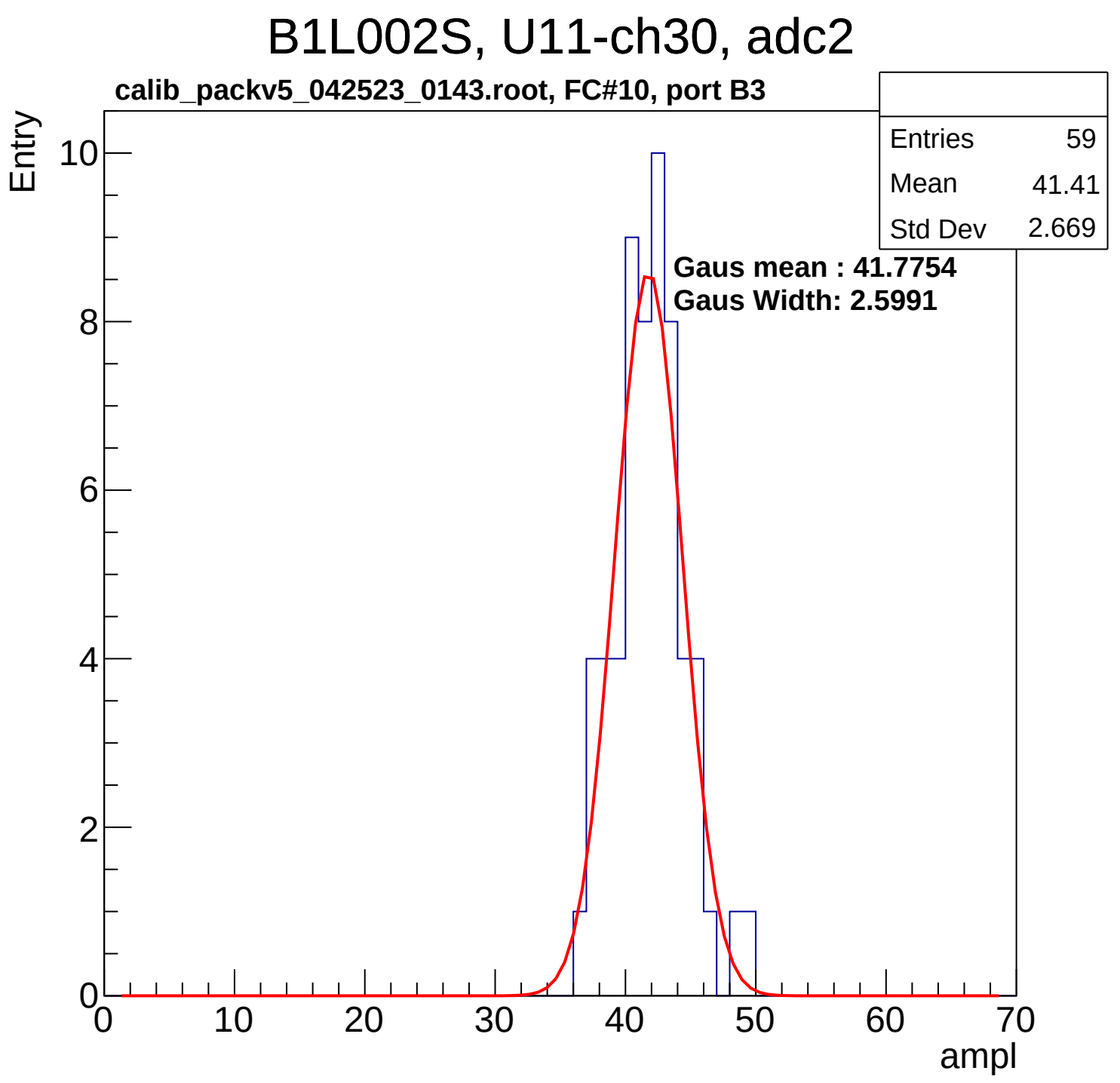
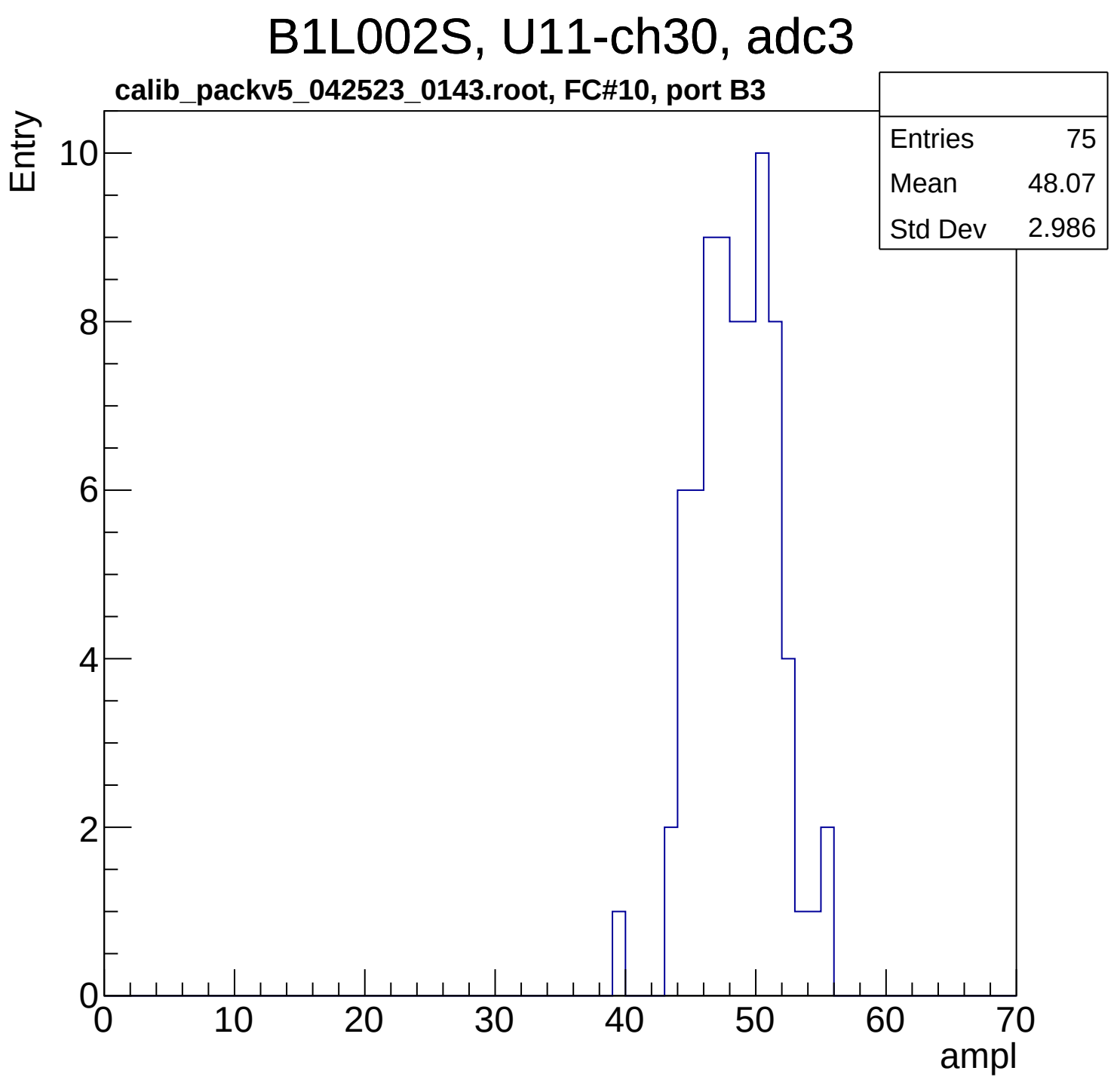
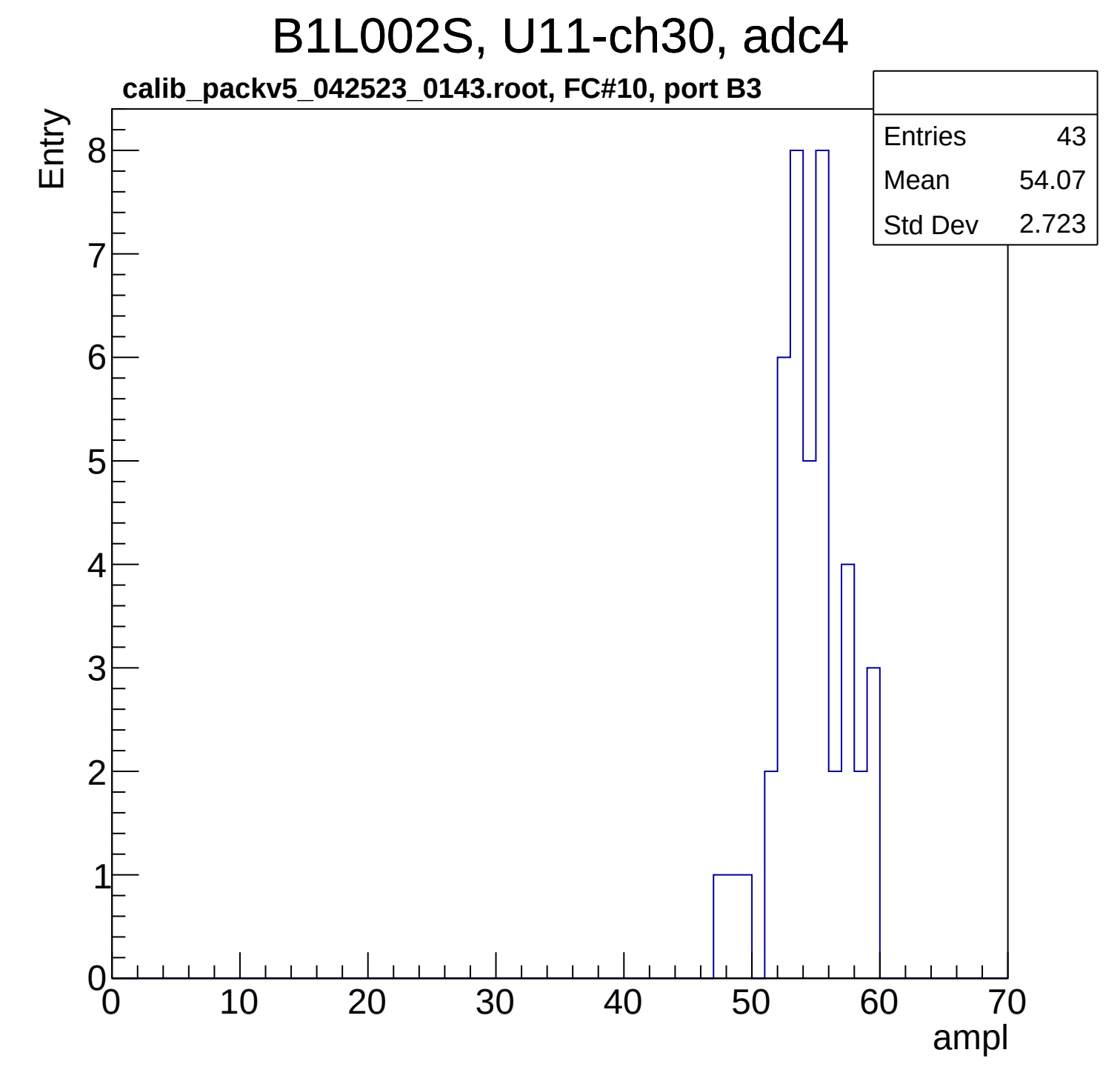
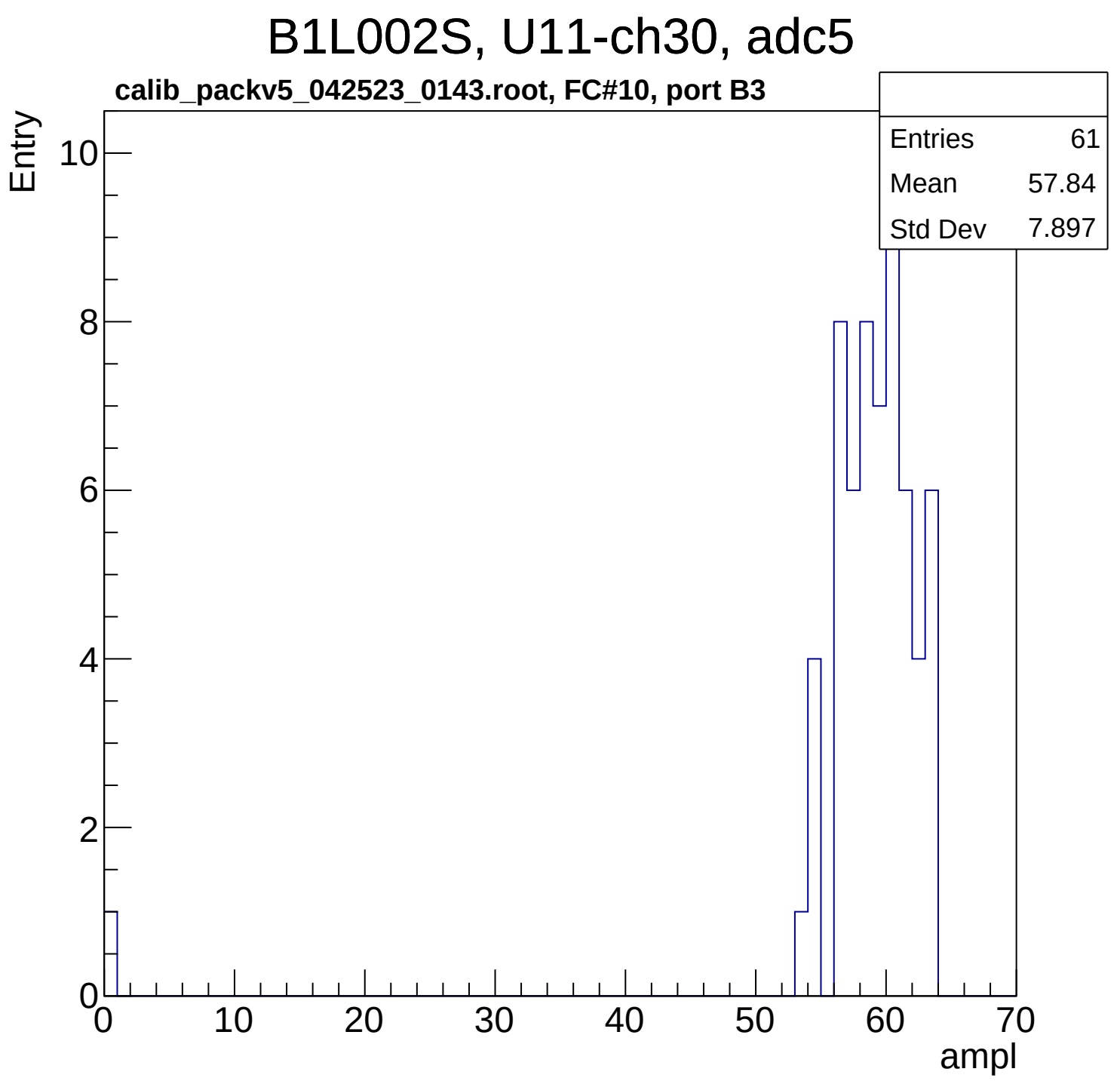

B1L002S, U11-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.41
Std Dev 2.669
Gaus mean : 41.7754
Gaus Width: 2.5991
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 48.07
Std Dev 2.986
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 54.07
Std Dev 2.723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.84
Std Dev 7.897
Entry
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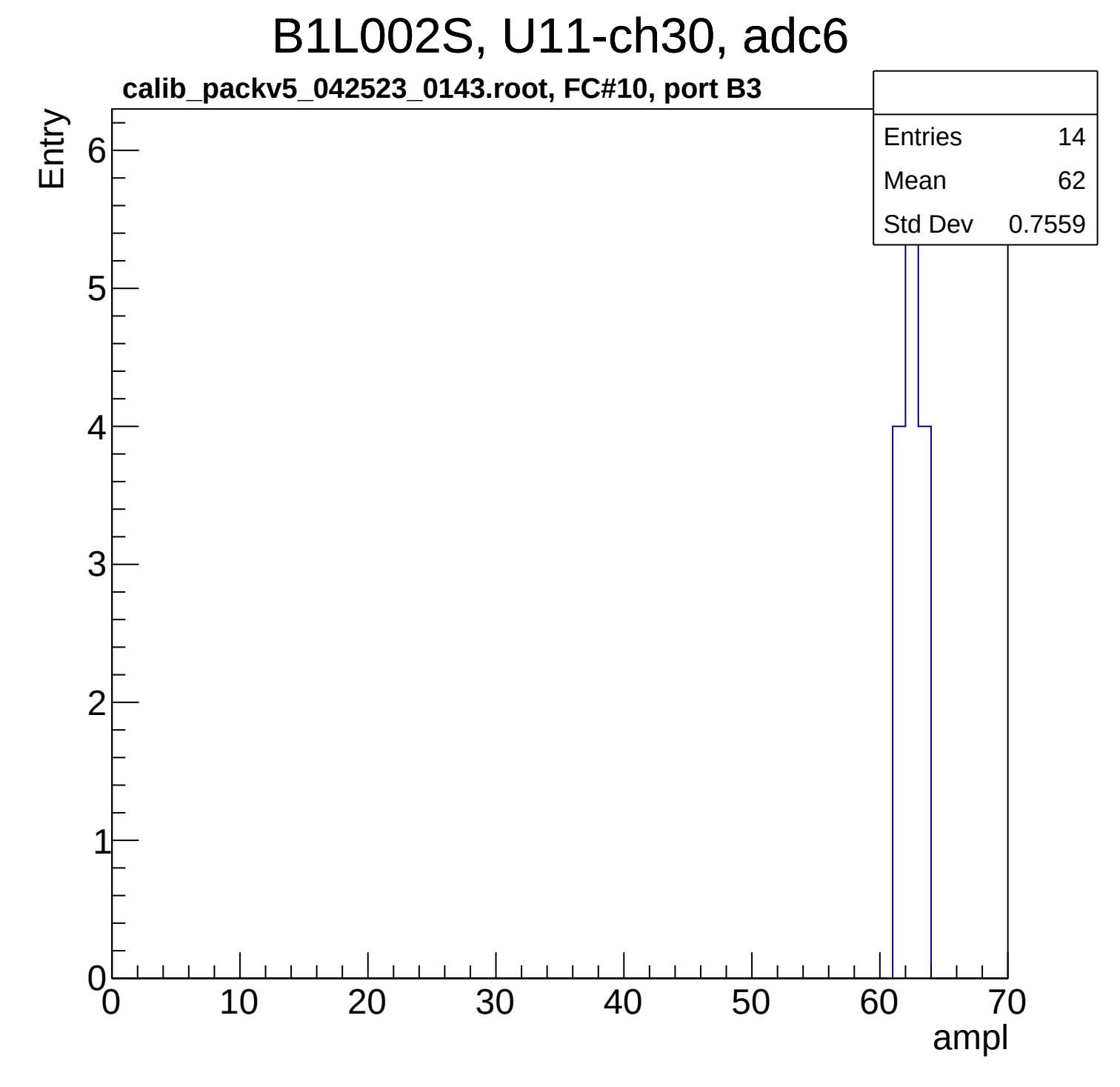
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 62
Std Dev 0.7559
Entry
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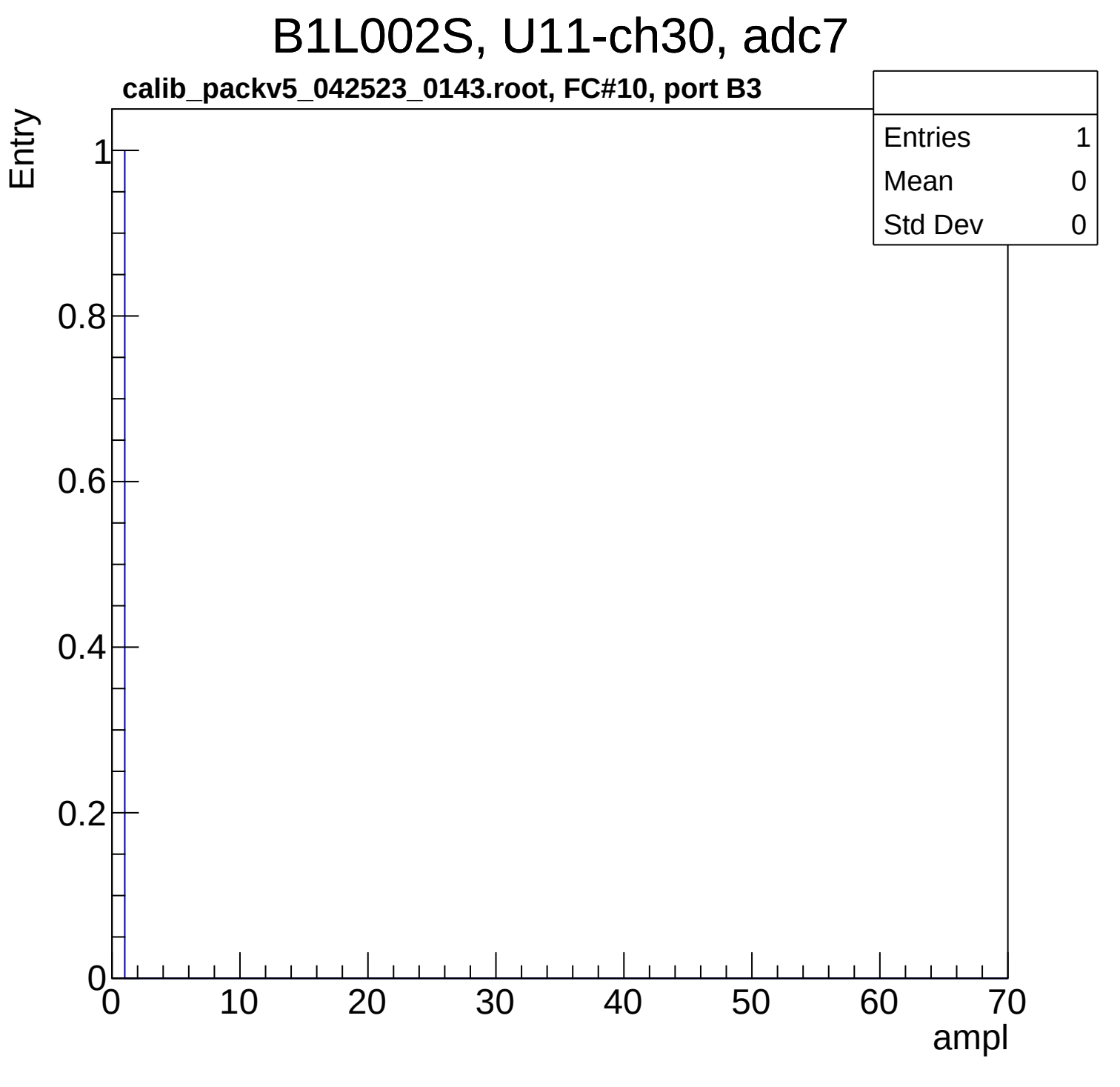
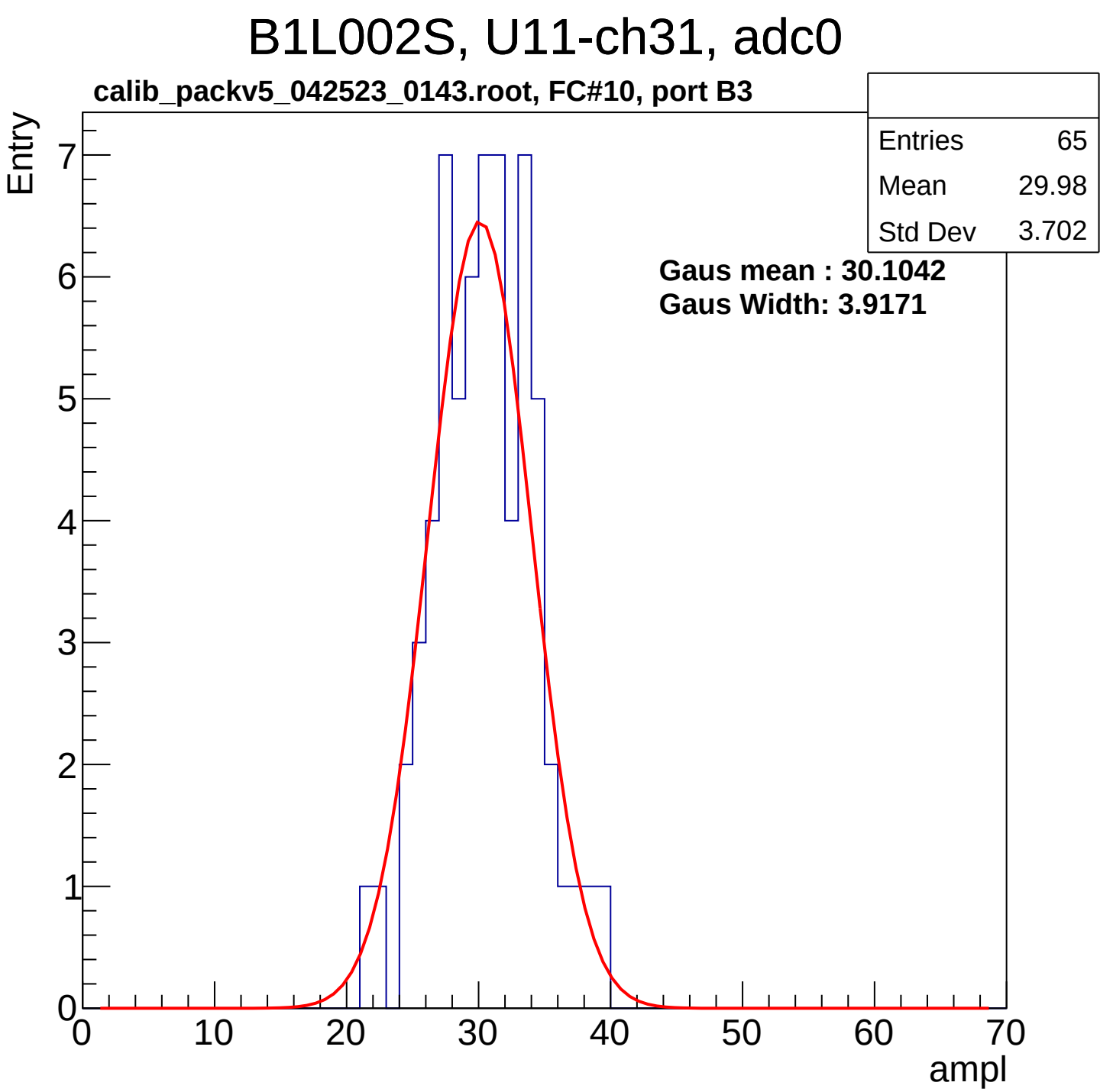
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.98
Std Dev 3.702
Gaus mean : 30.1042
Gaus Width: 3.9171
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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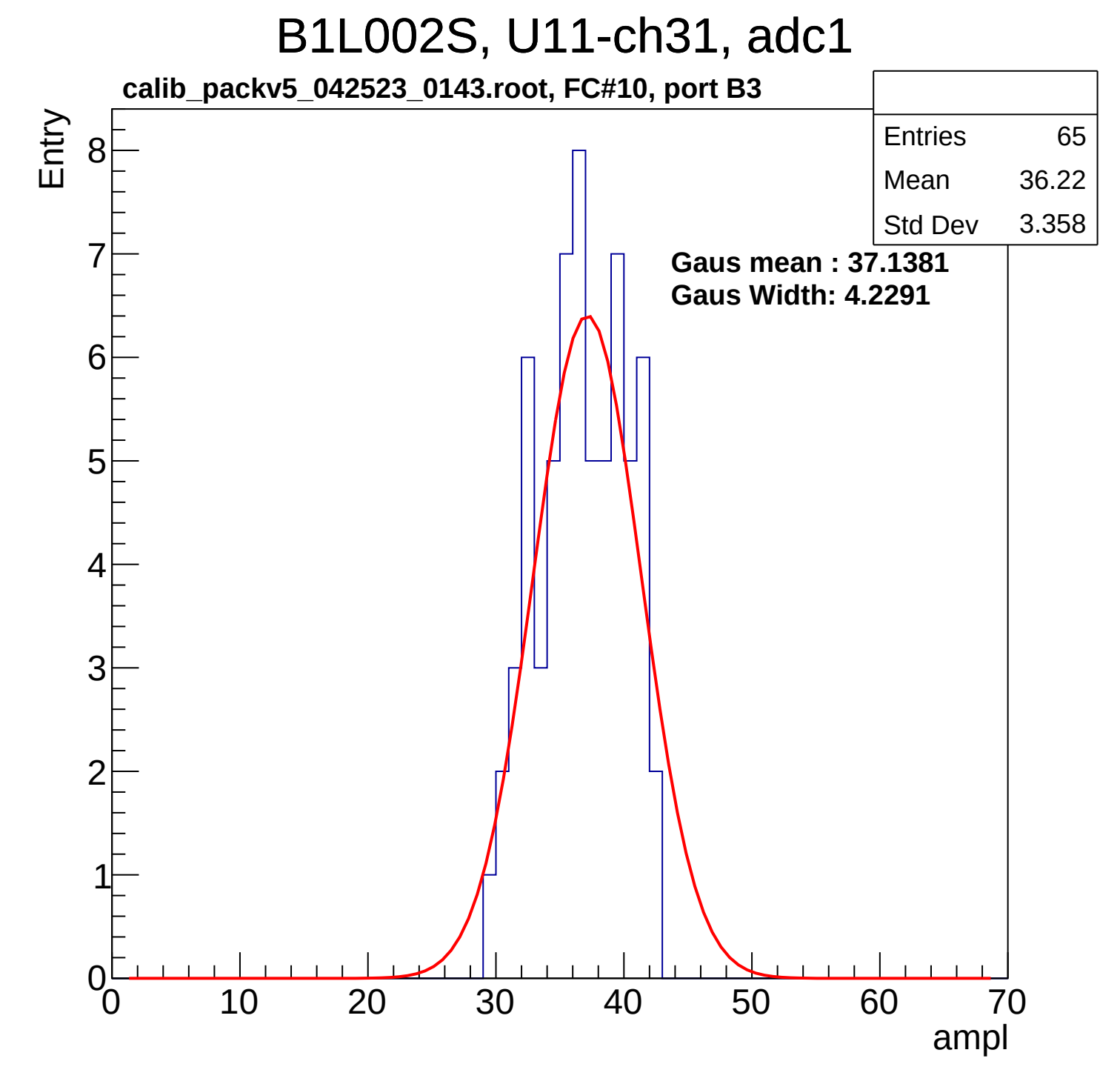
B1L002S, U11-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 36.22
Std Dev 3.358
Gaus mean : 37.1381
Gaus Width: 4.2291
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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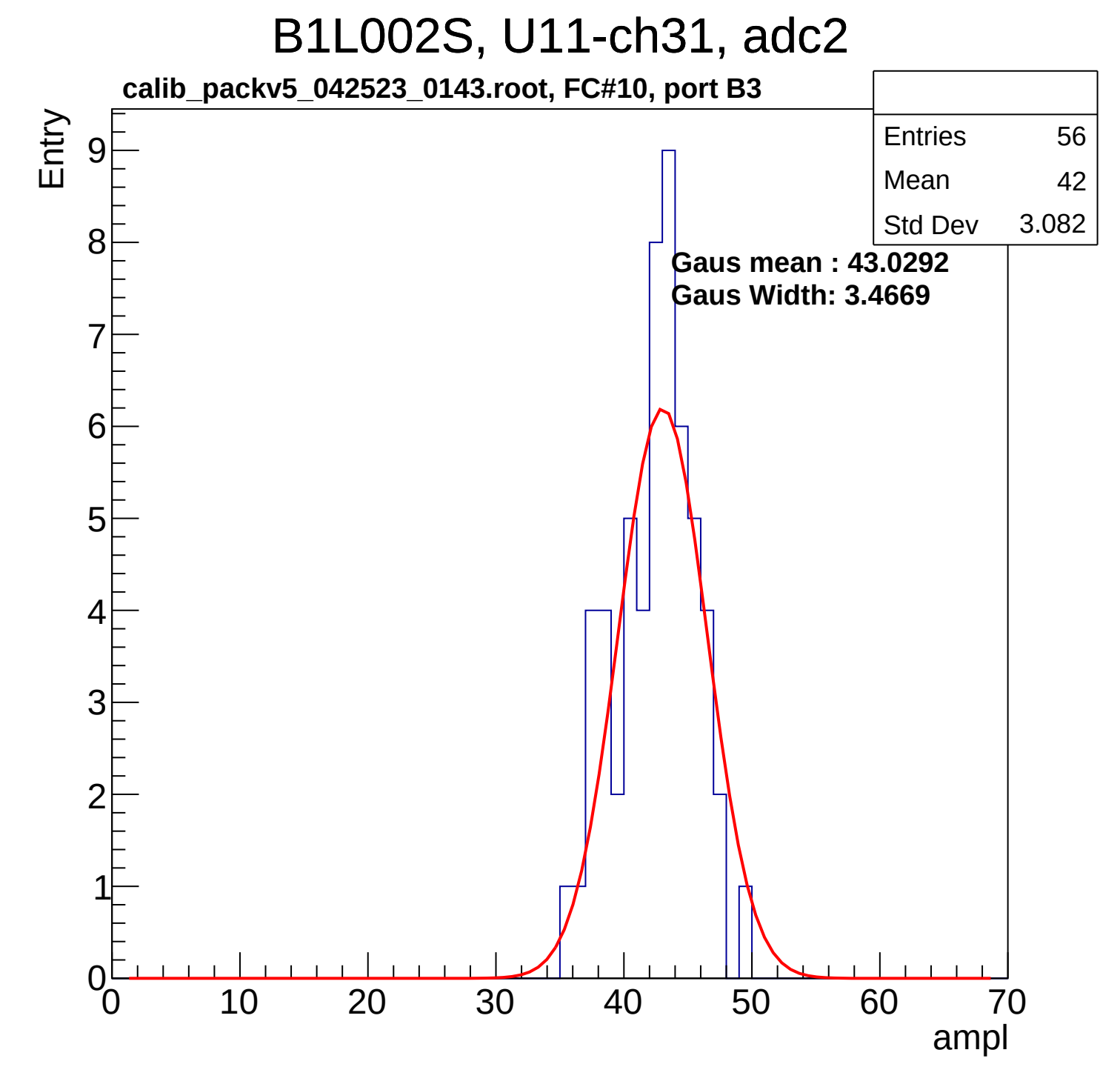
B1L002S, U11-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42
Std Dev 3.082
Gaus mean : 43.0292
Gaus Width: 3.4669
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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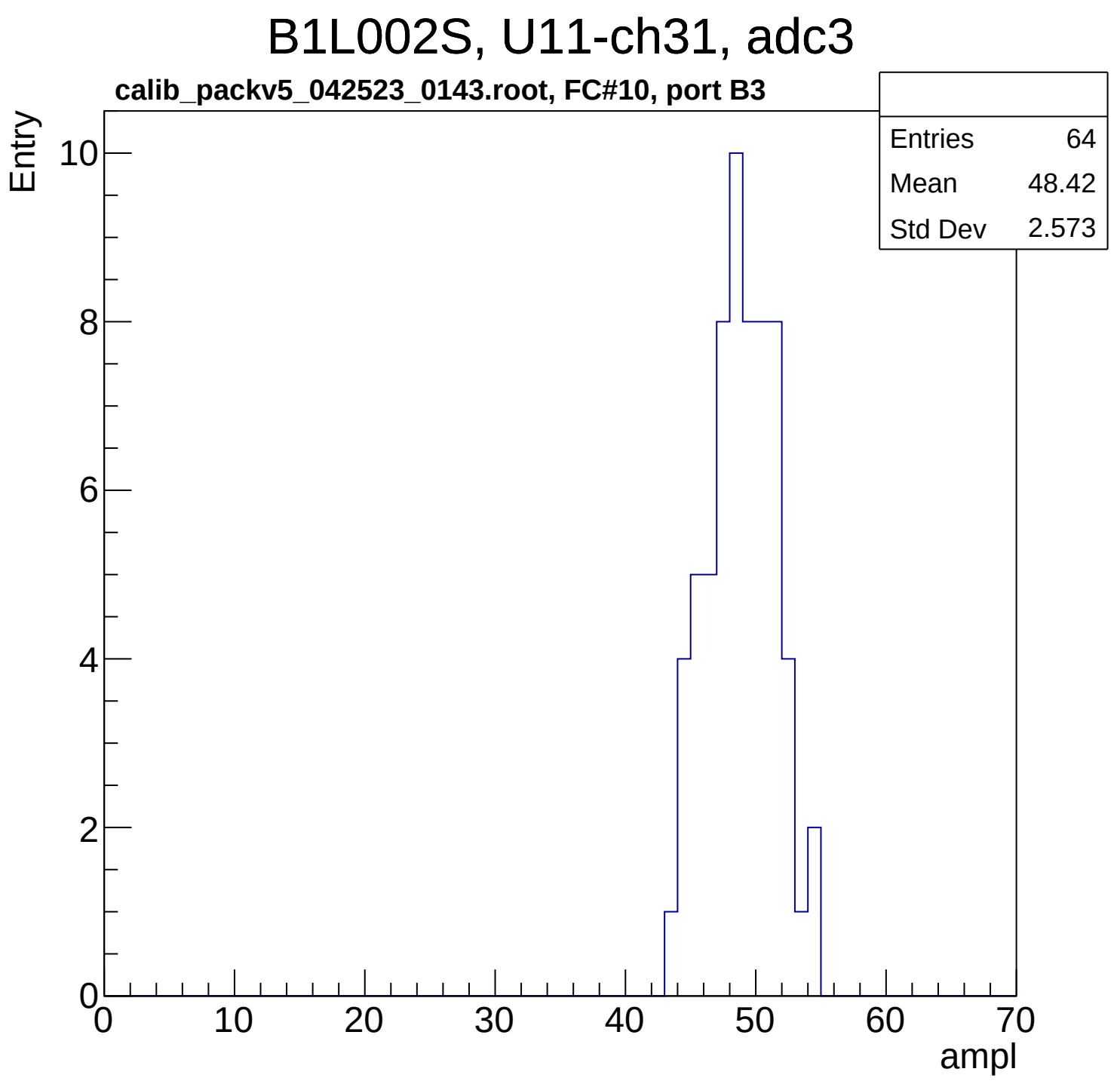
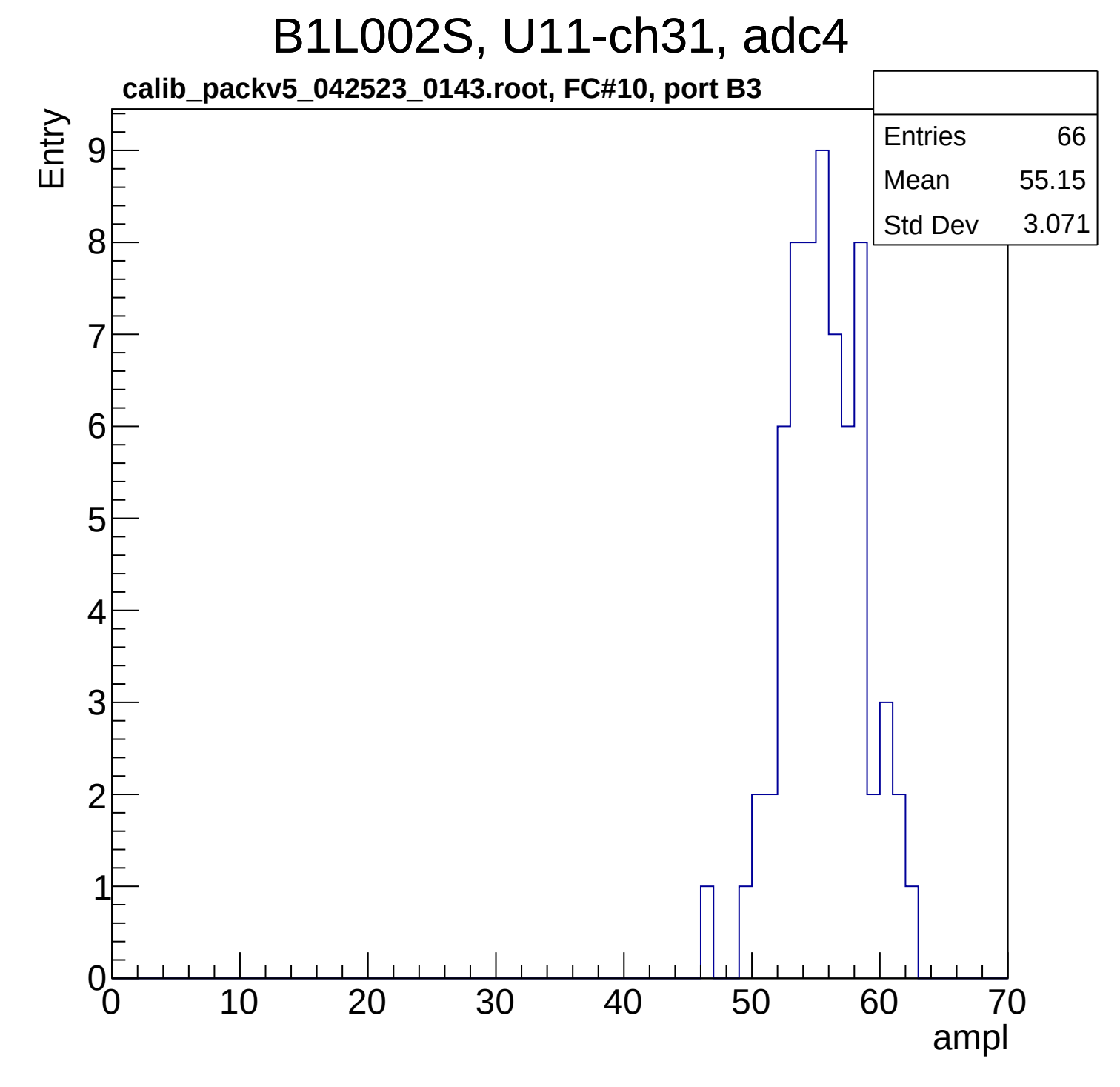

B1L002S, U11-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 48.42
Std Dev 2.573
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 55.15
Std Dev 3.071
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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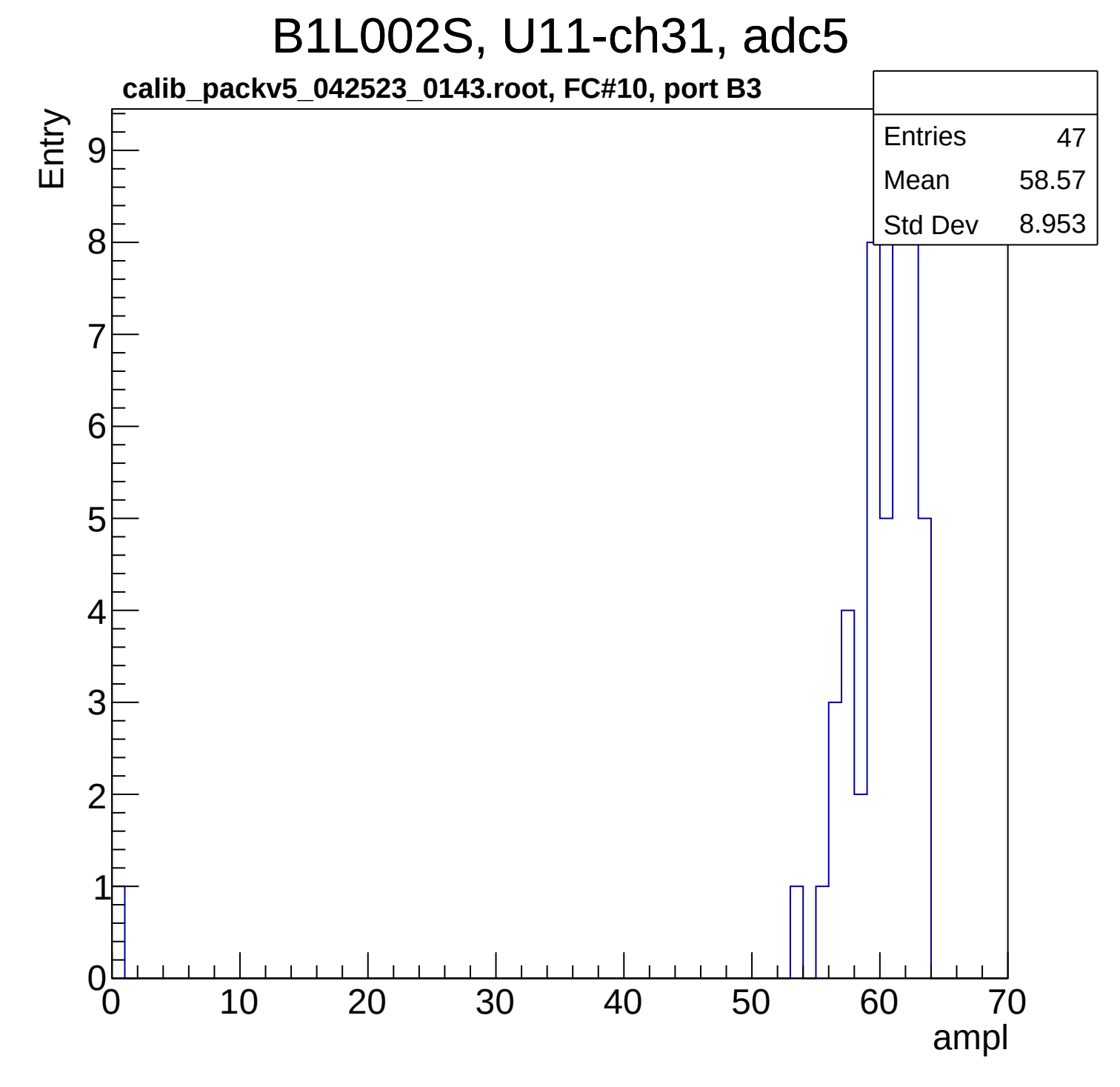
B1L002S, U11-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.57
Std Dev 8.953
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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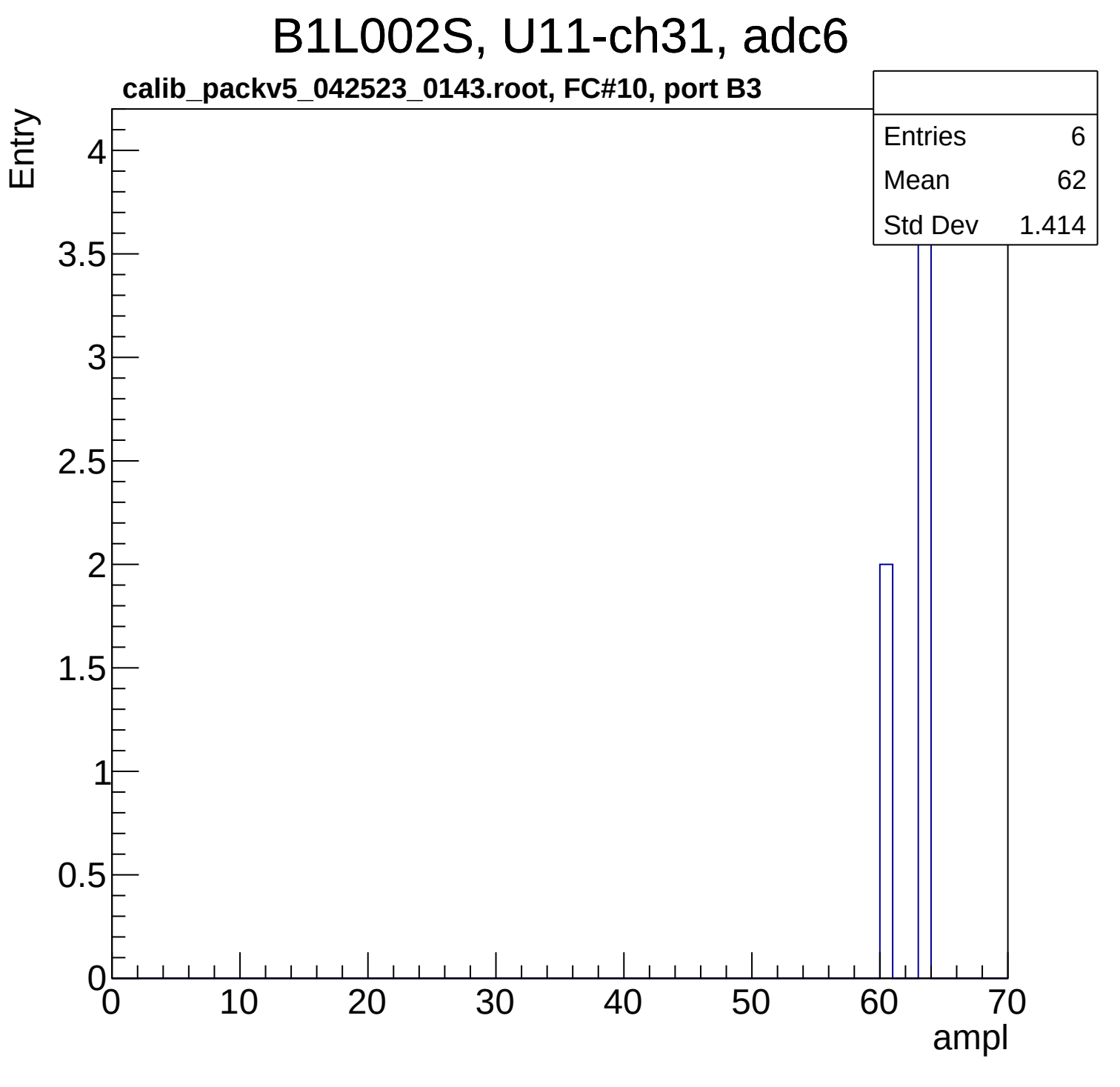
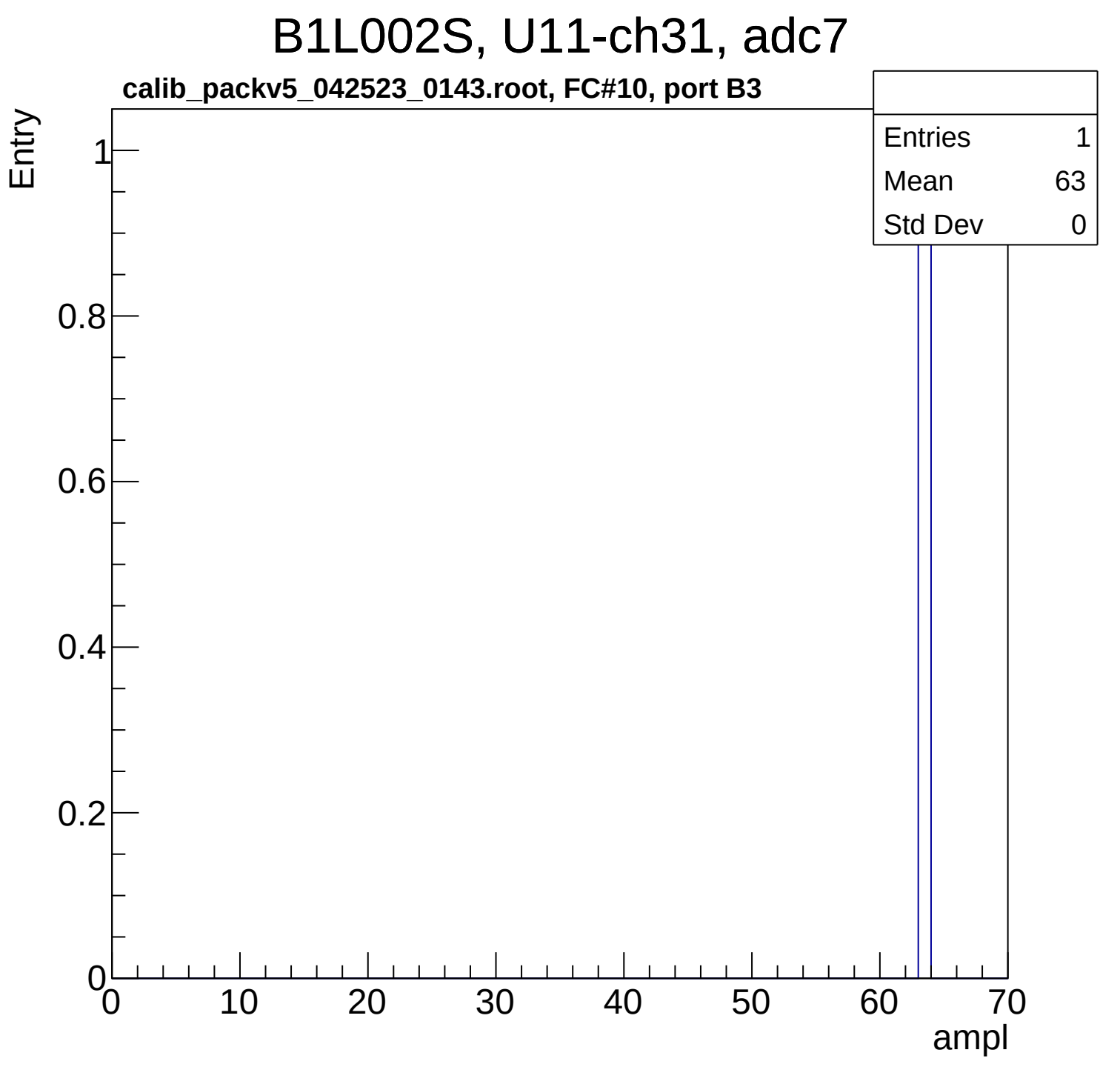
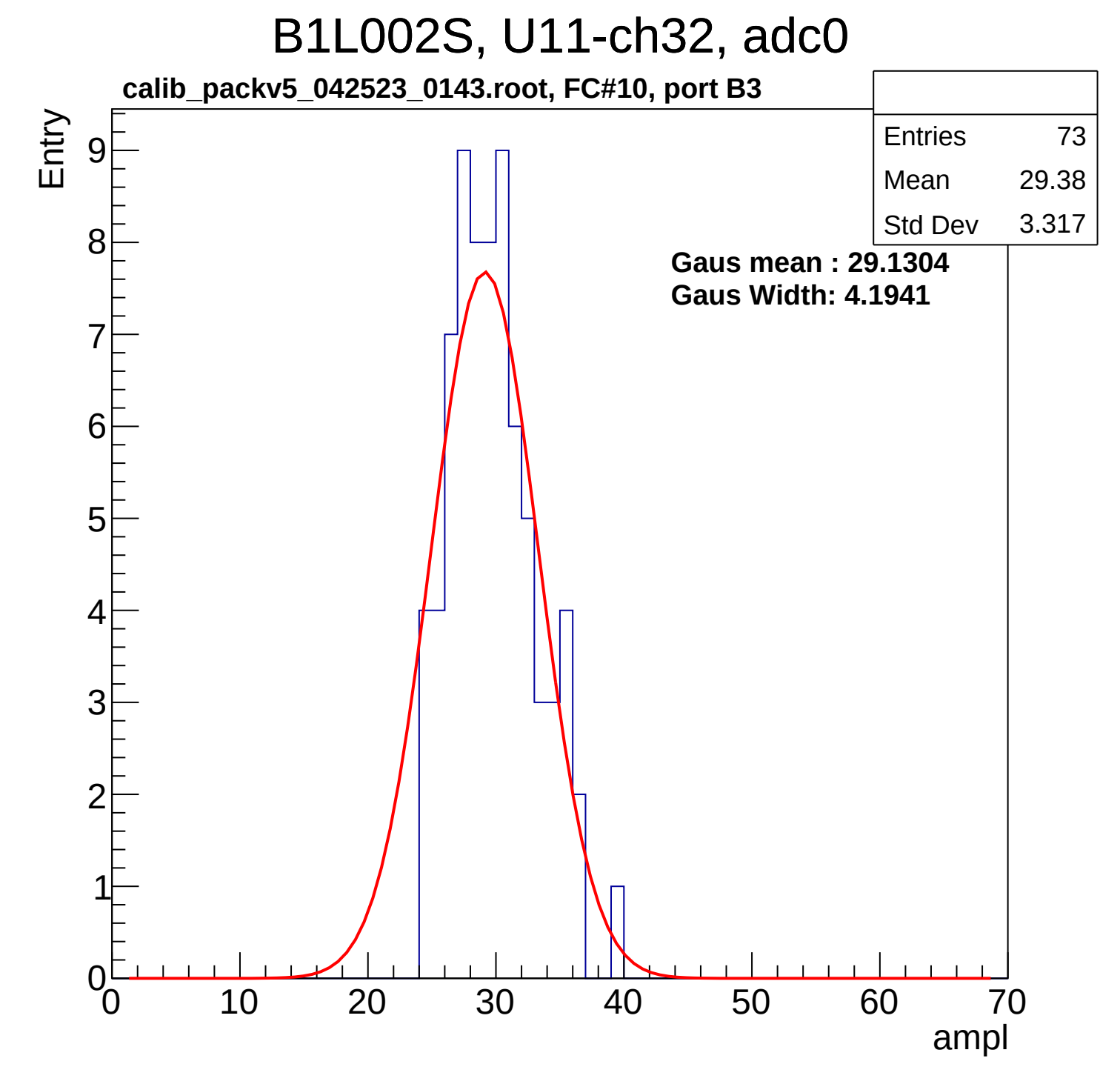
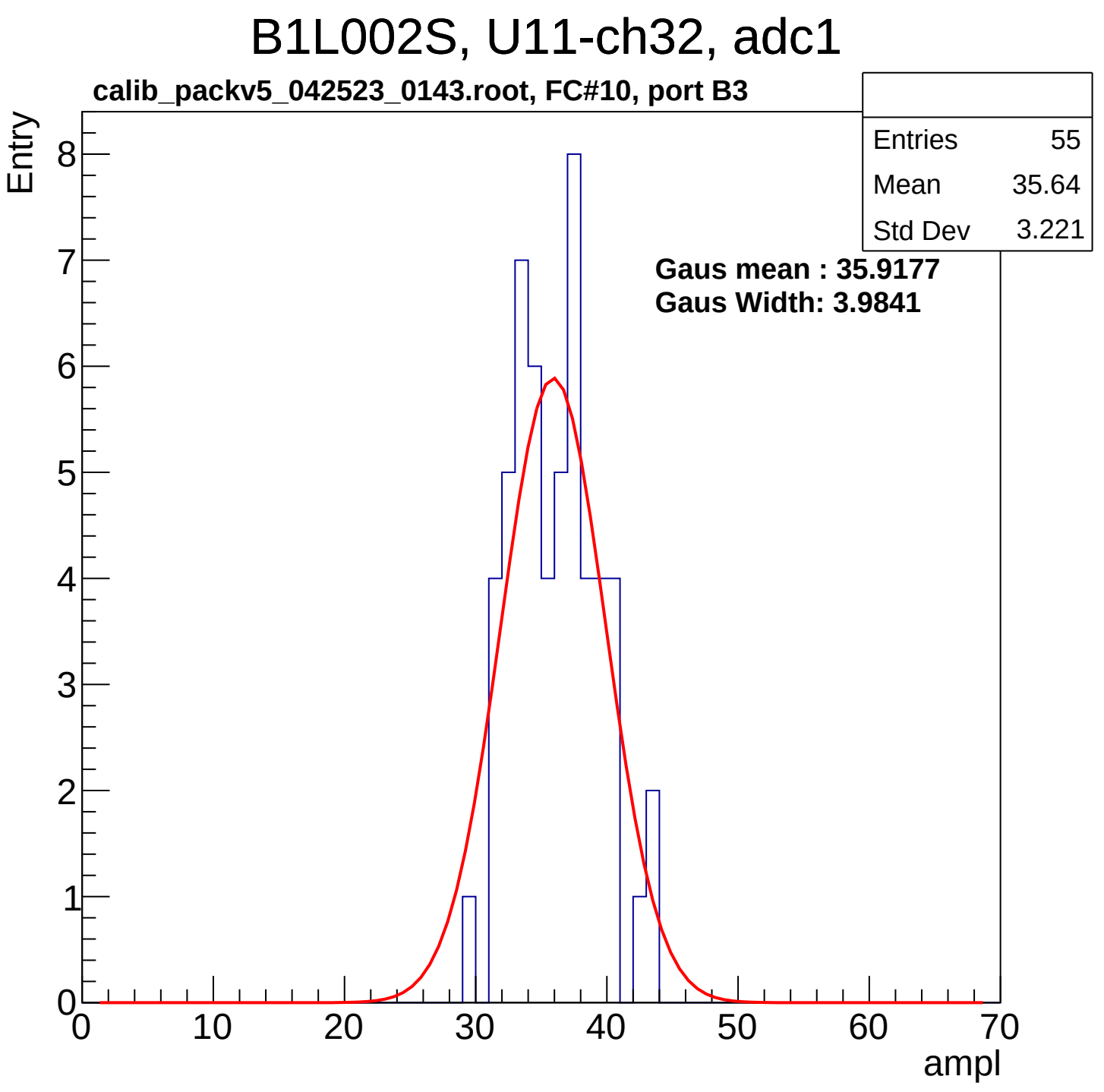
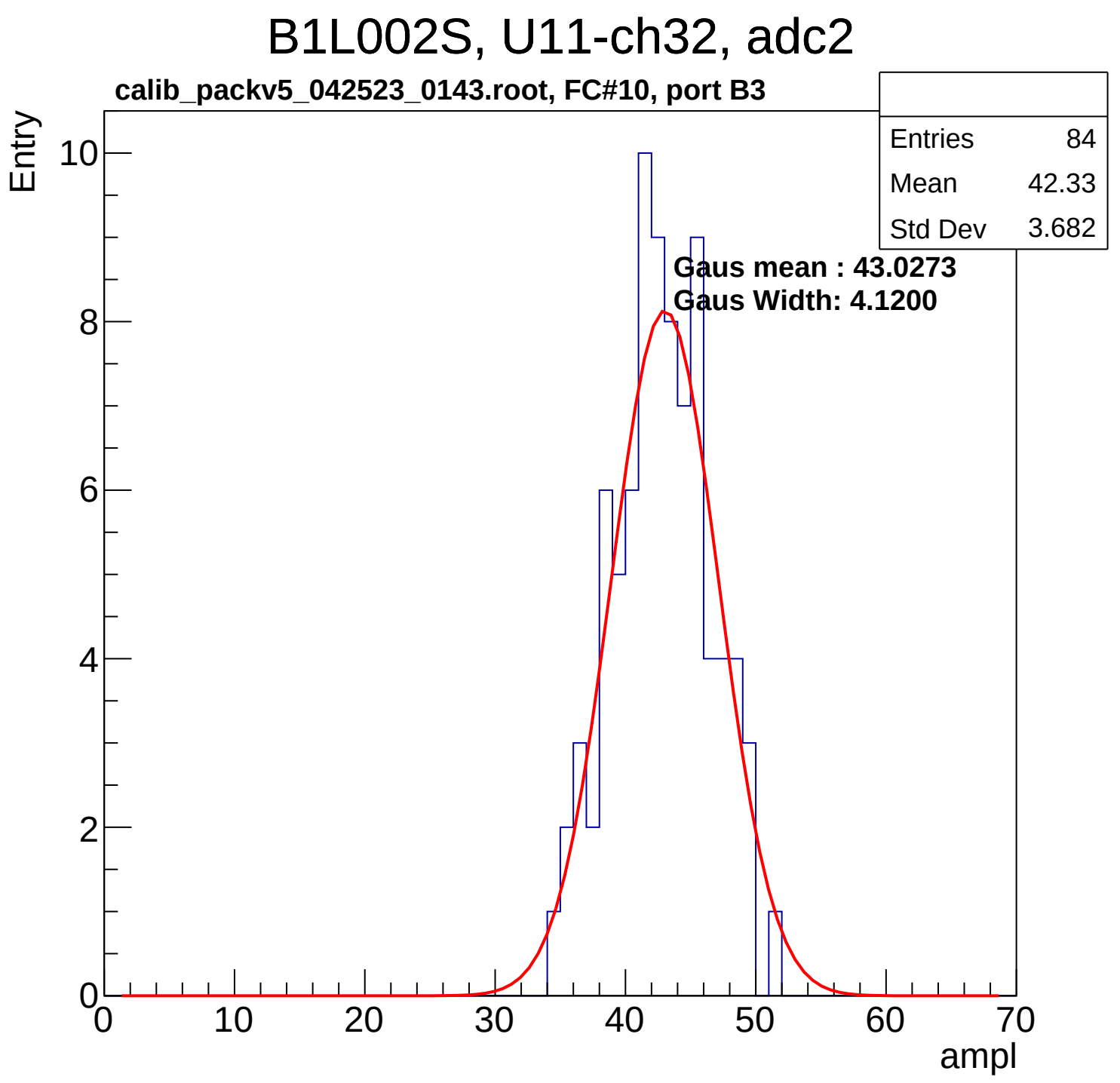
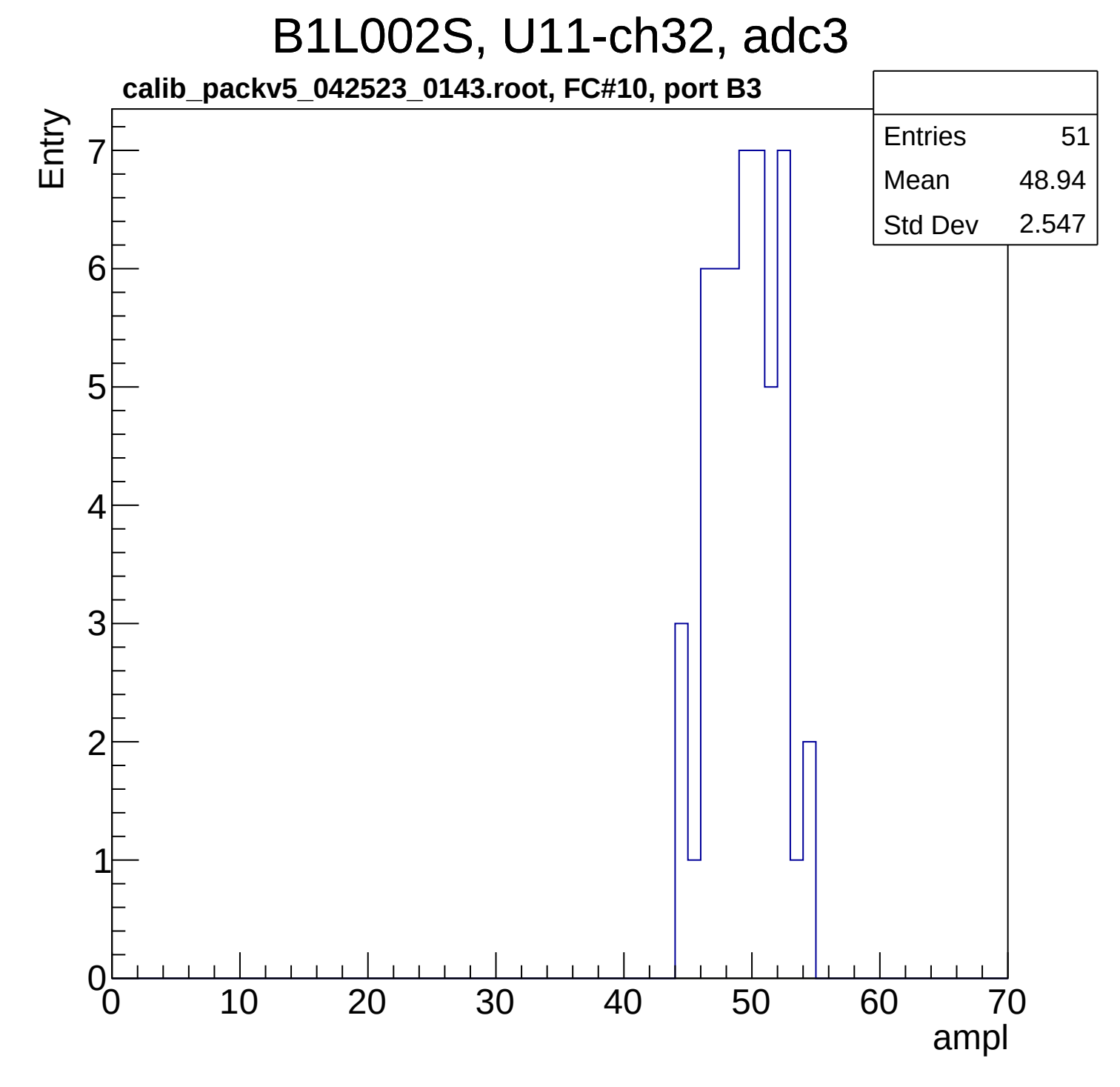

B1L002S, U11-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62
Std Dev 1.414
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

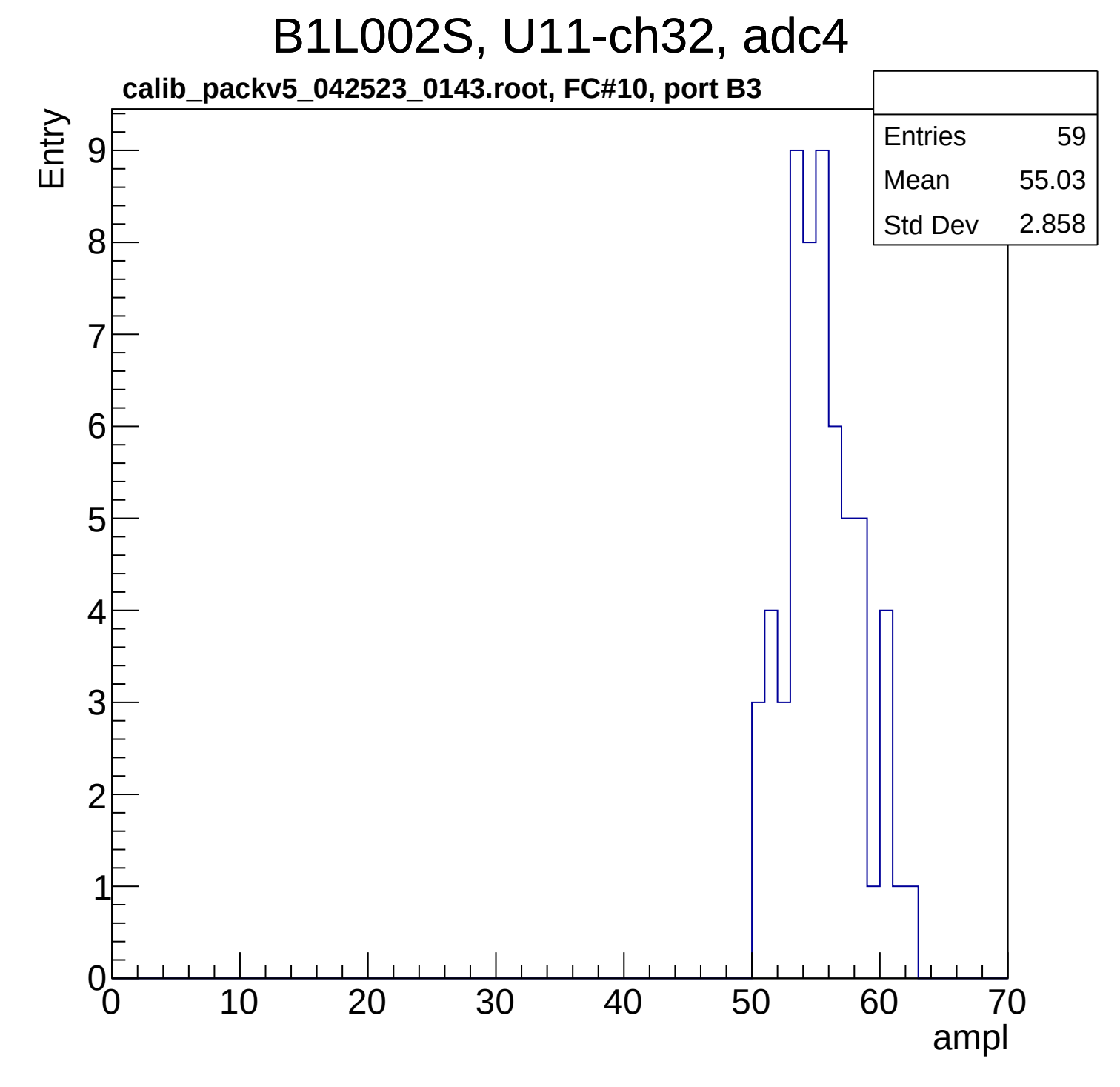

B1L002S, U11-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

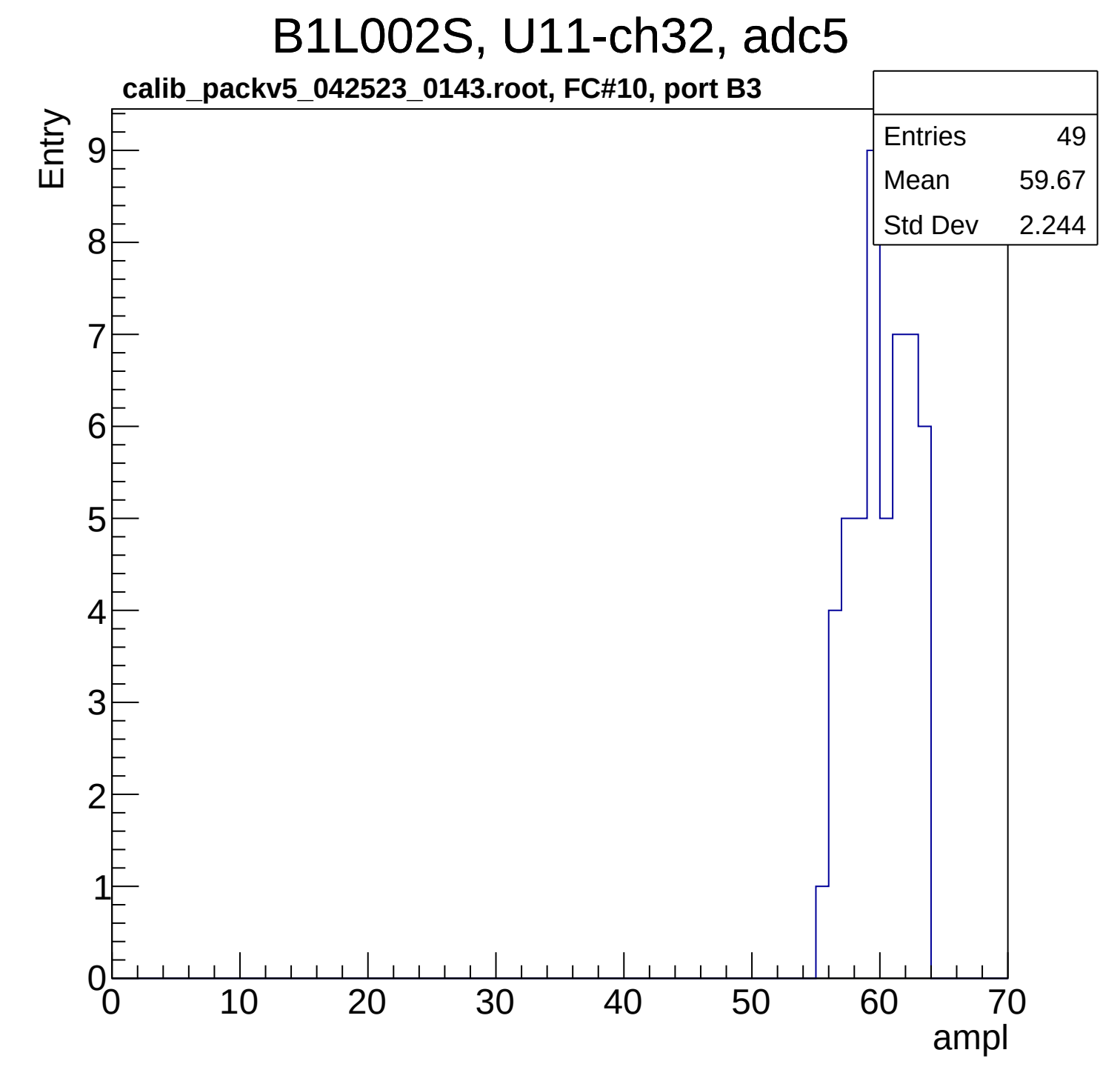

B1L002S, U11-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.38
Std Dev 3.317
Gaus mean : 29.1304
Gaus Width: 4.1941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

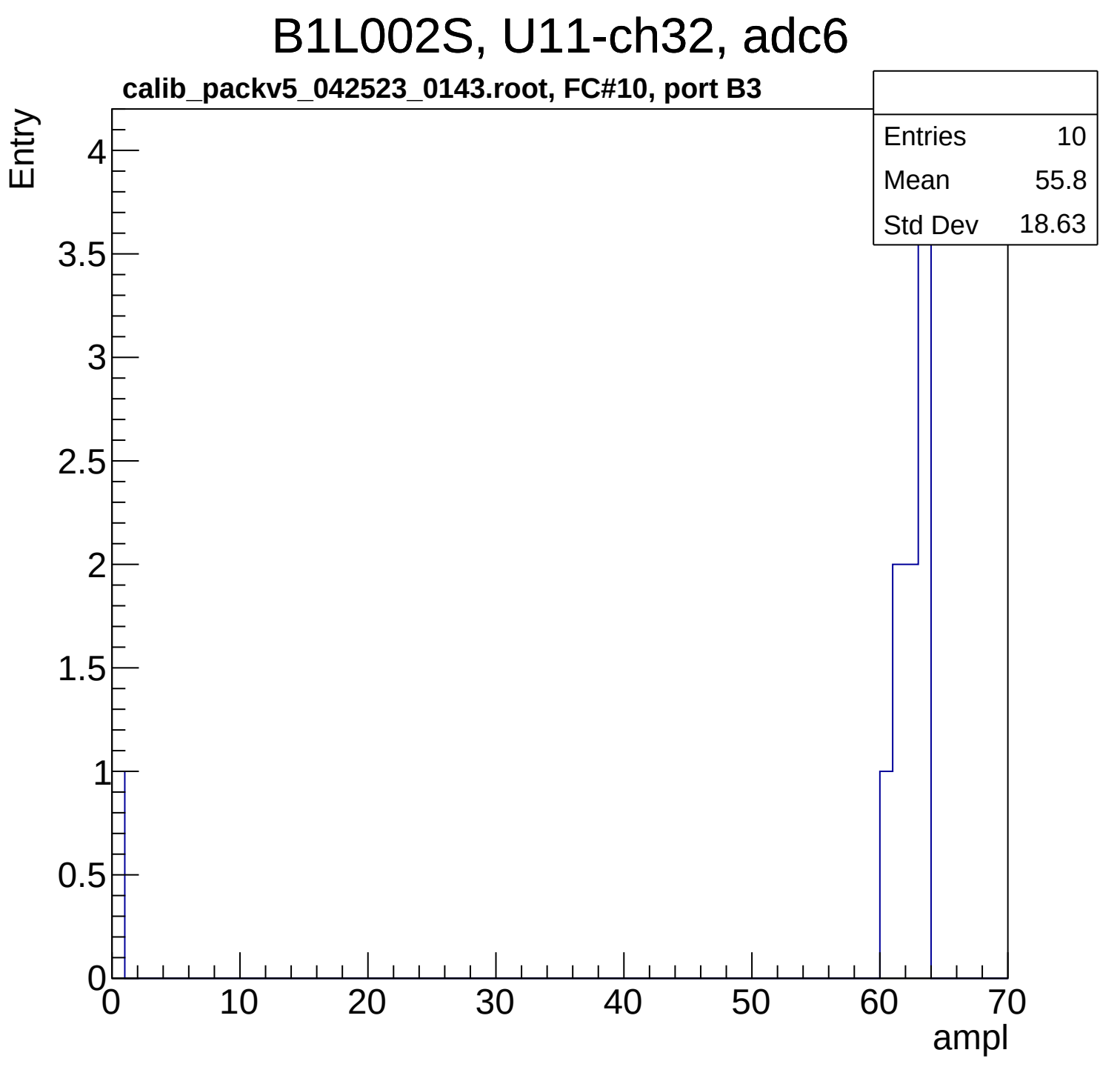

B1L002S, U11-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.64
Std Dev 3.221
Gaus mean : 35.9177
Gaus Width: 3.9841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

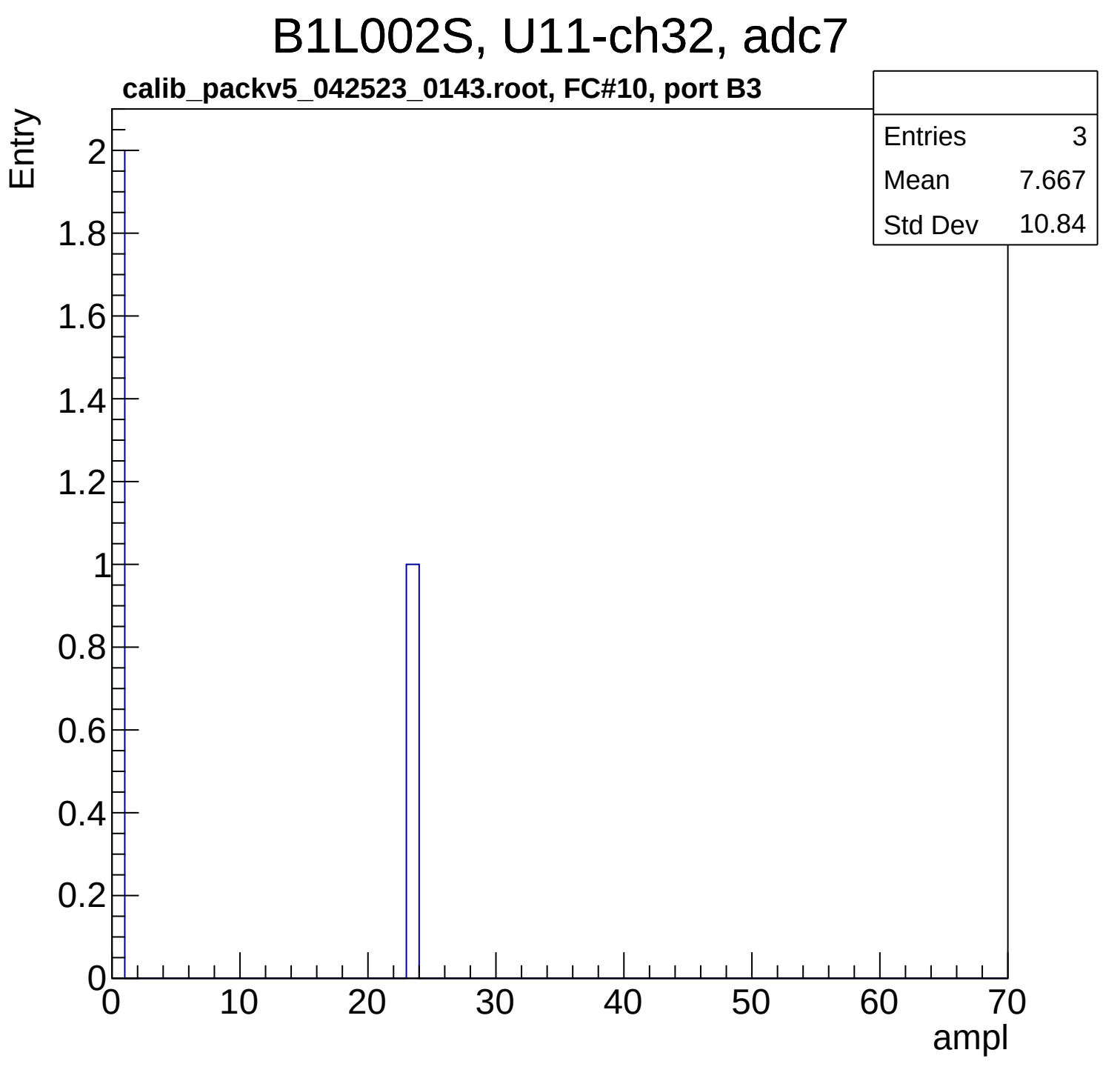

B1L002S, U11-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 42.33
Std Dev 3.682
Gaus mean : 43.0273
Gaus Width: 4.1200
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

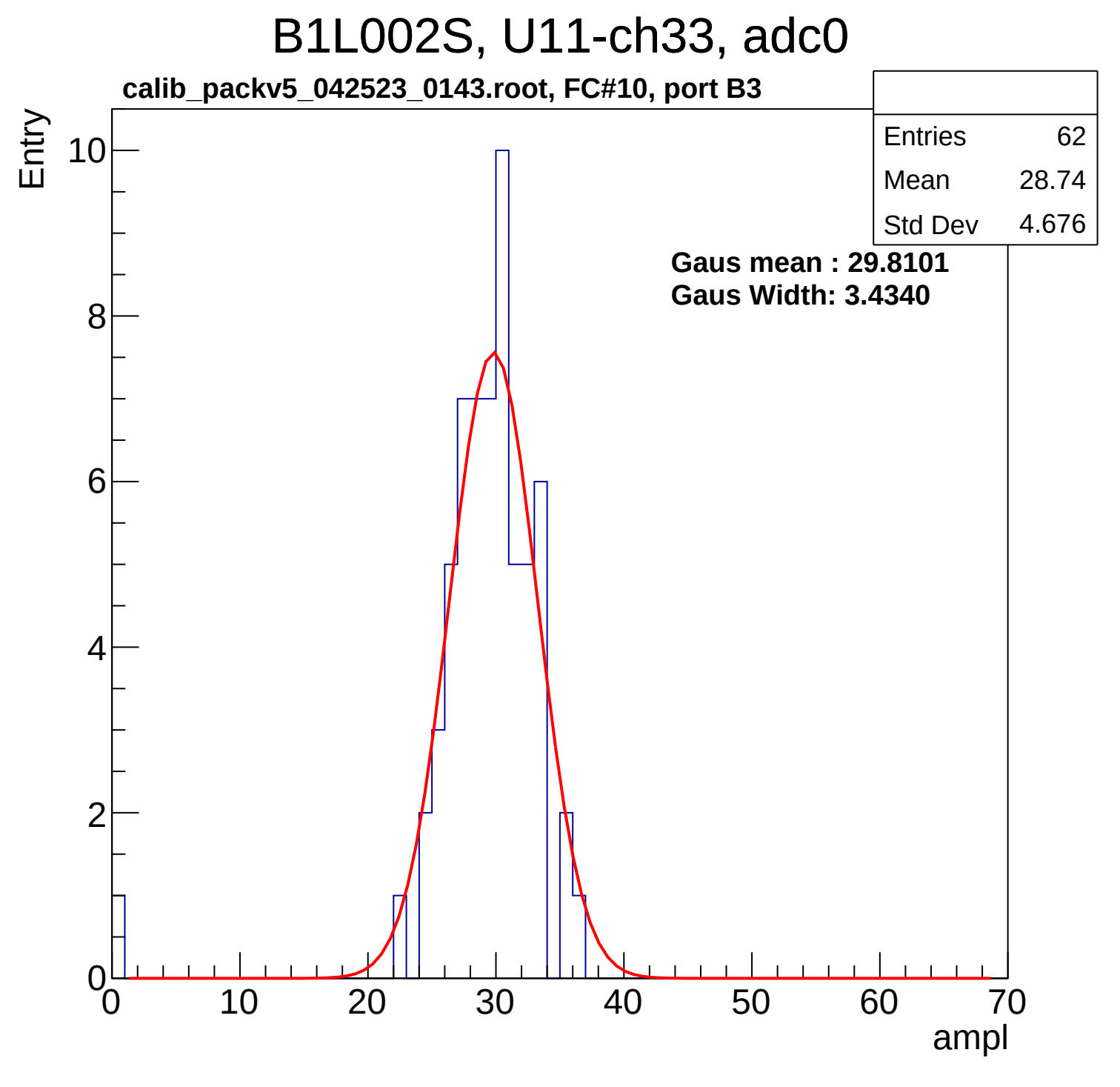

B1L002S, U11-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 48.94
Std Dev 2.547
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

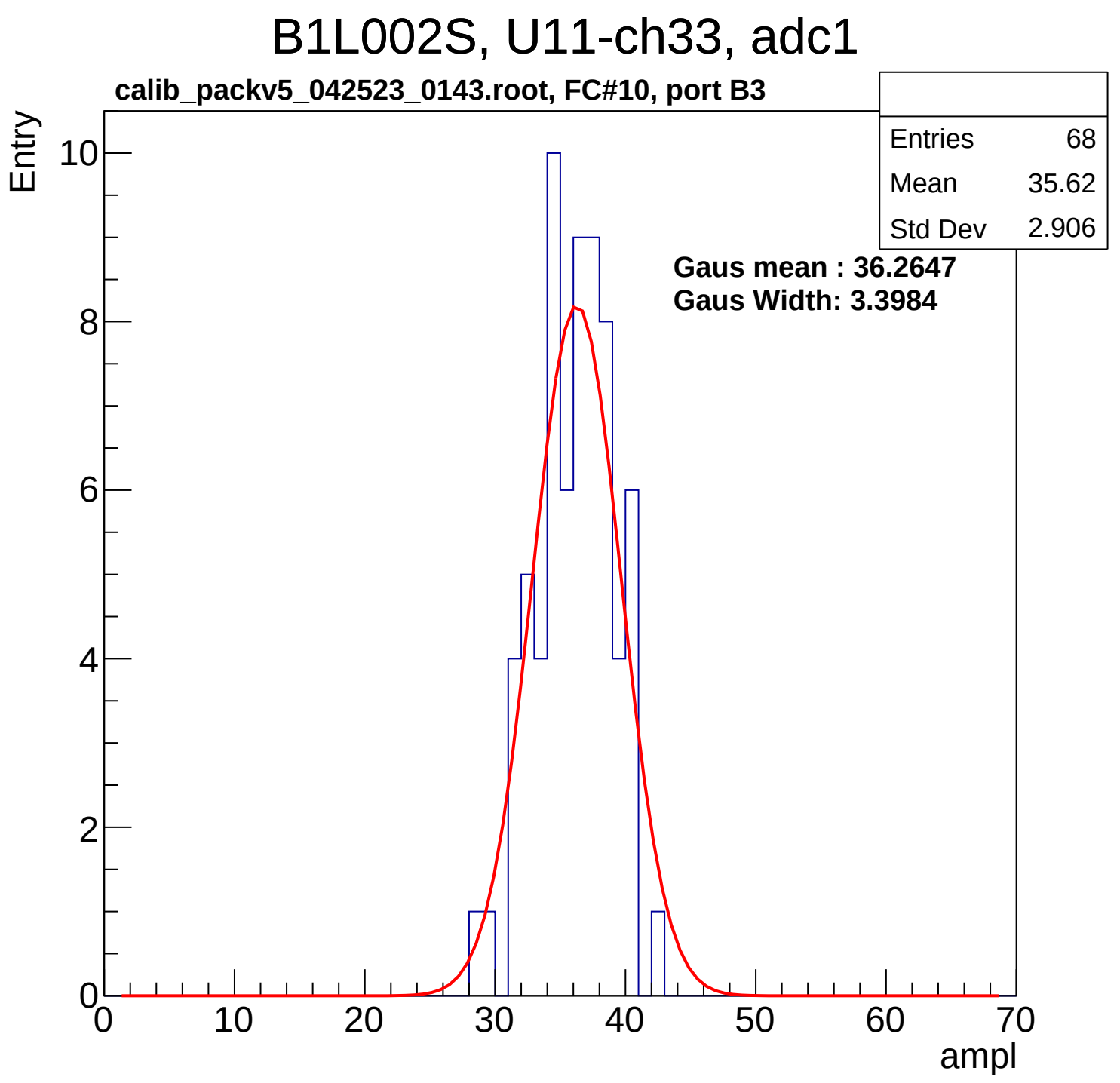

B1L002S, U11-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 55.03
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

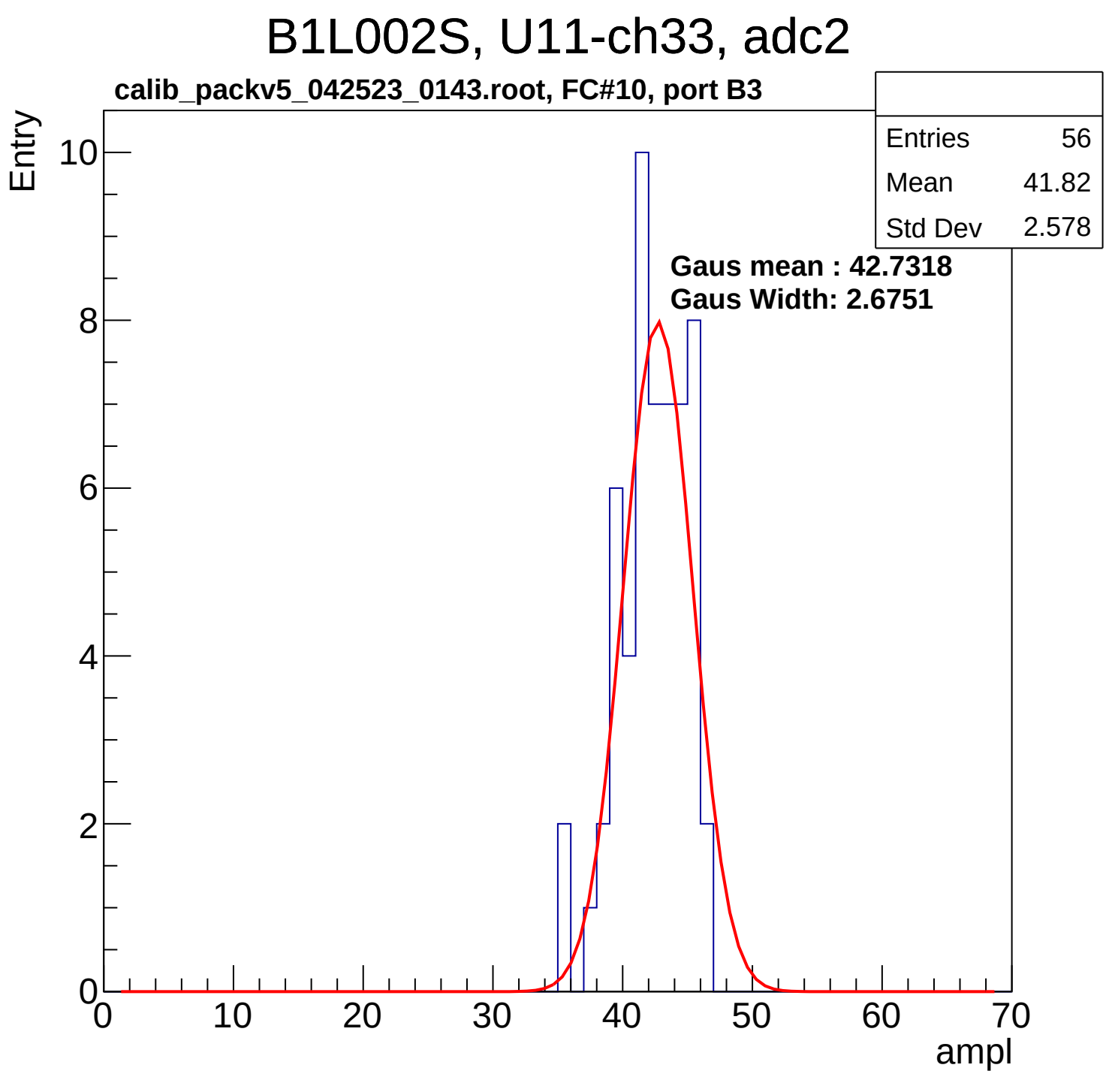

B1L002S, U11-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.67
Std Dev 2.244
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

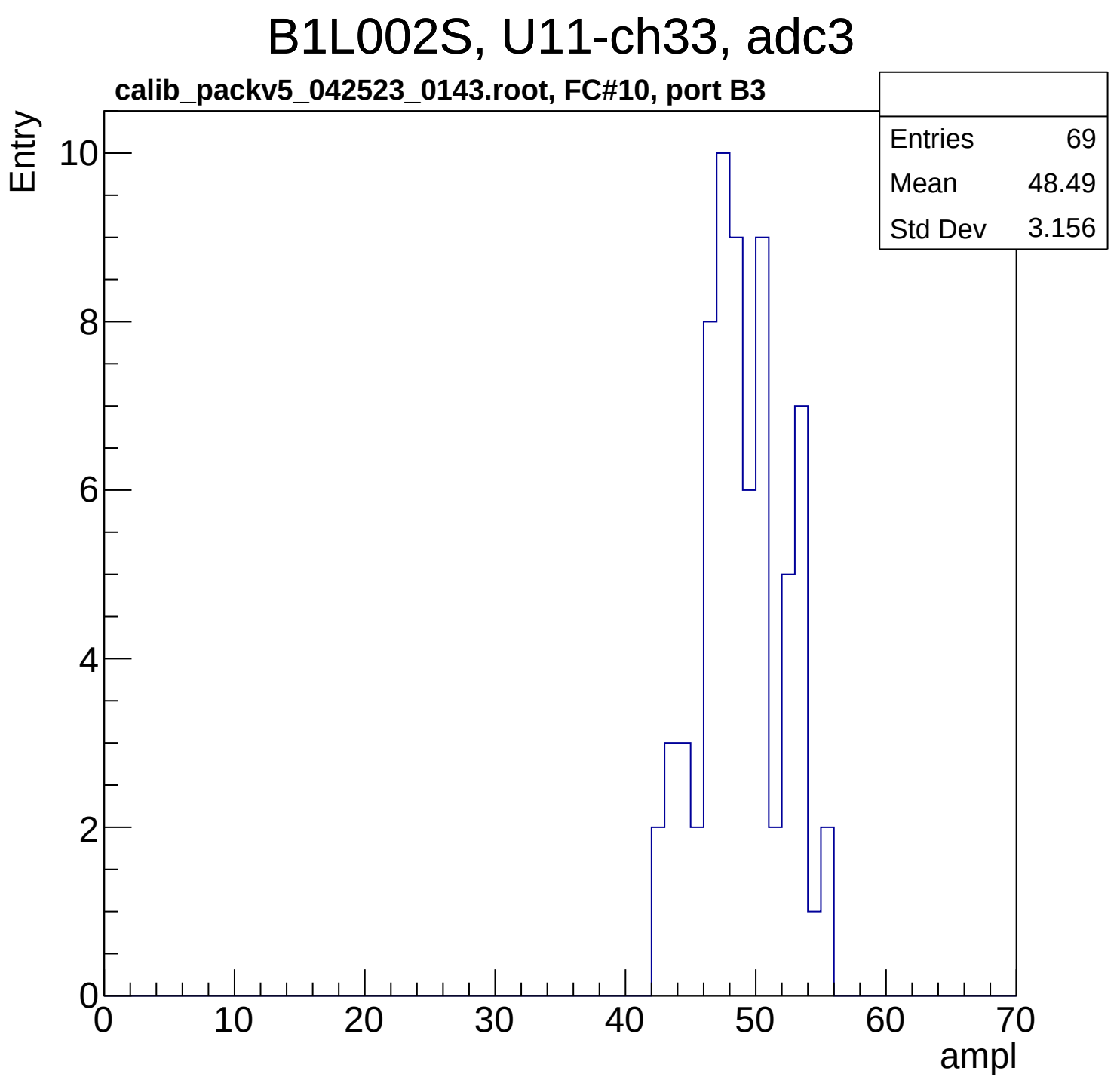

B1L002S, U11-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.8
Std Dev 18.63
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

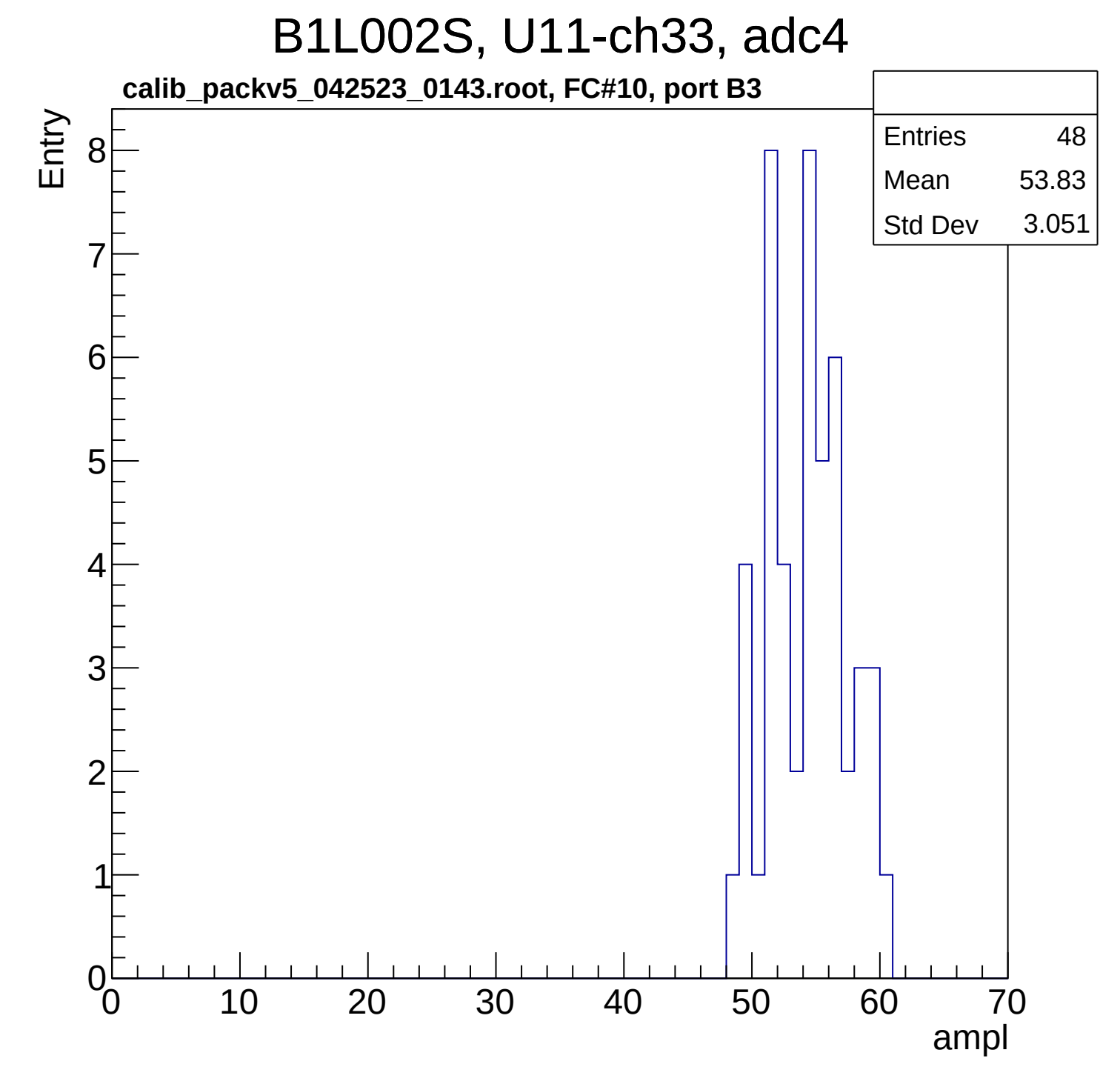

B1L002S, U11-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.74
Std Dev 4.676
Gaus mean : 29.8101
Gaus Width: 3.4340
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.62
Std Dev 2.906
Gaus mean : 36.2647
Gaus Width: 3.3984
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.82
Std Dev 2.578
Gaus mean : 42.7318
Gaus Width: 2.6751
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.49
Std Dev 3.156
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.83
Std Dev 3.051
Entry
ampl
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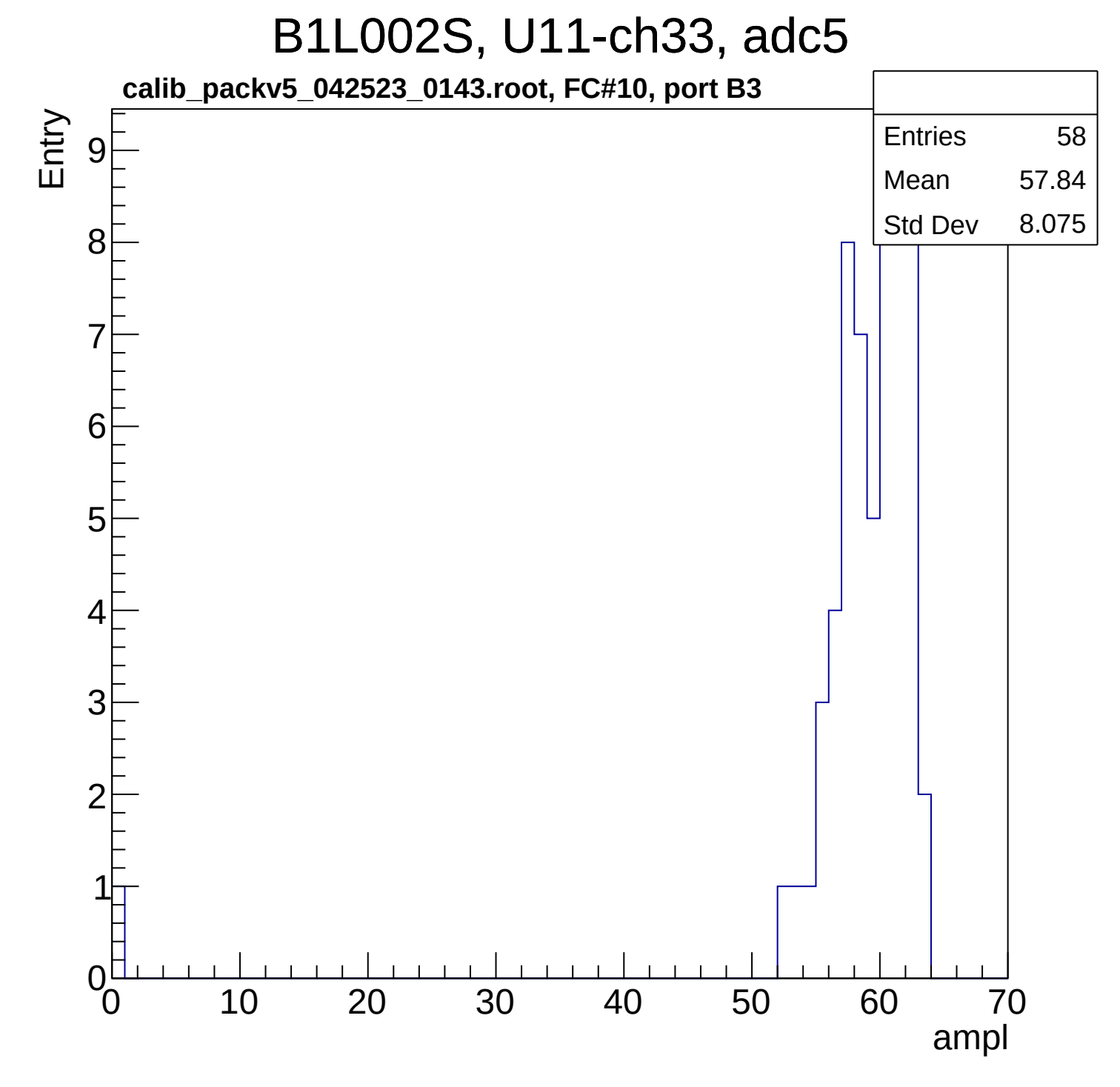
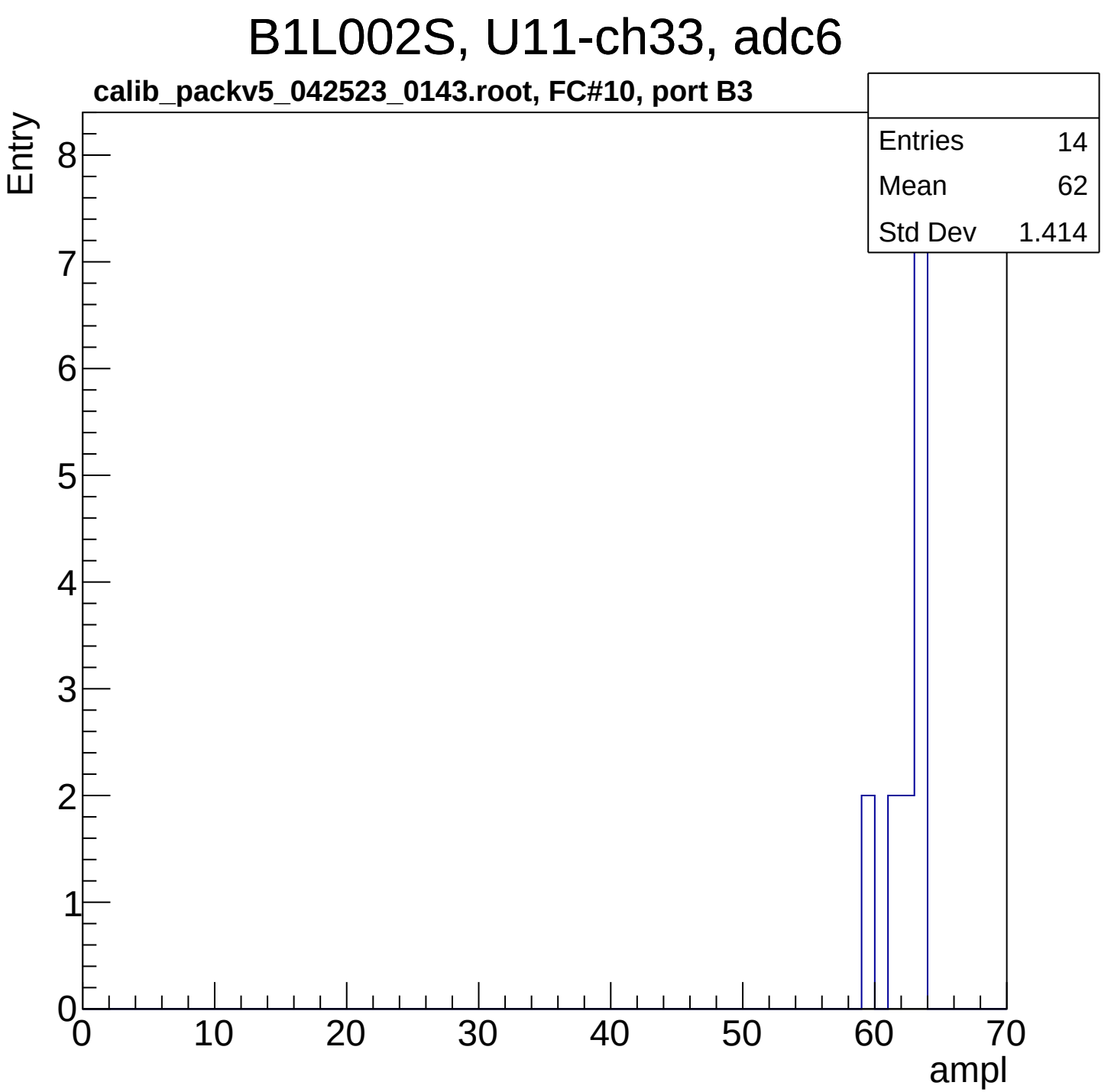
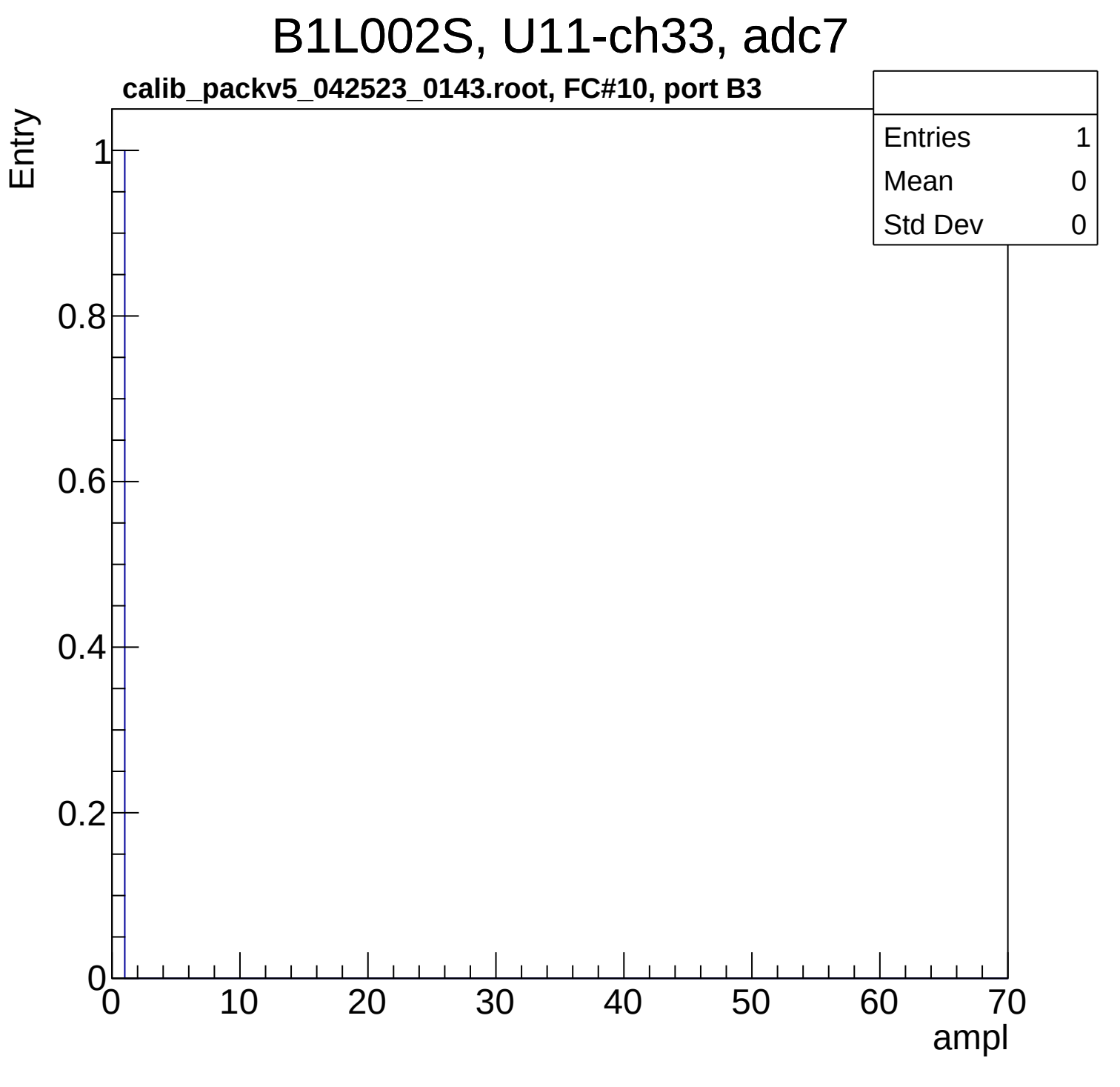
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.84
Std Dev 8.075
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 62
Std Dev 1.414
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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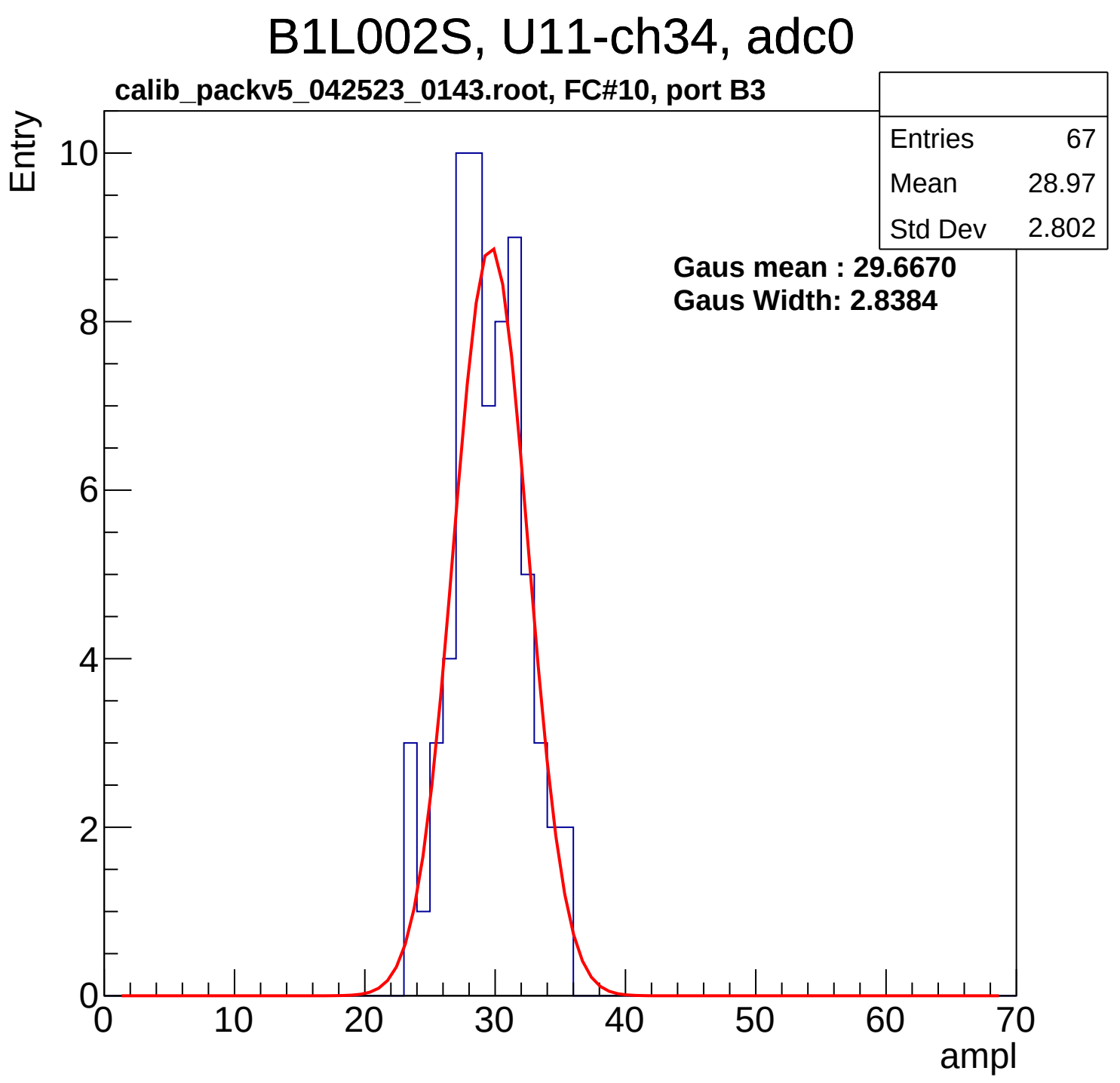
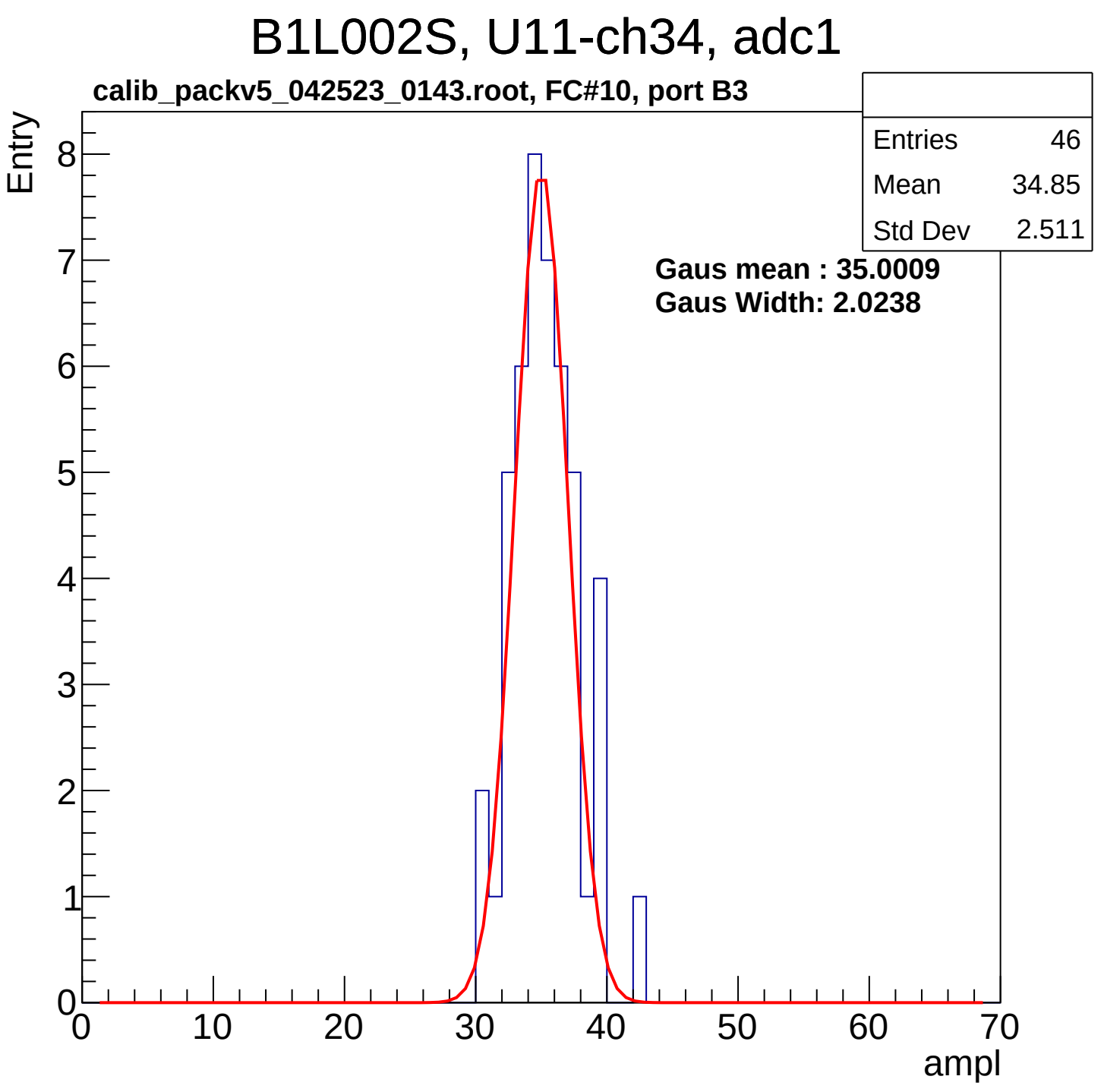
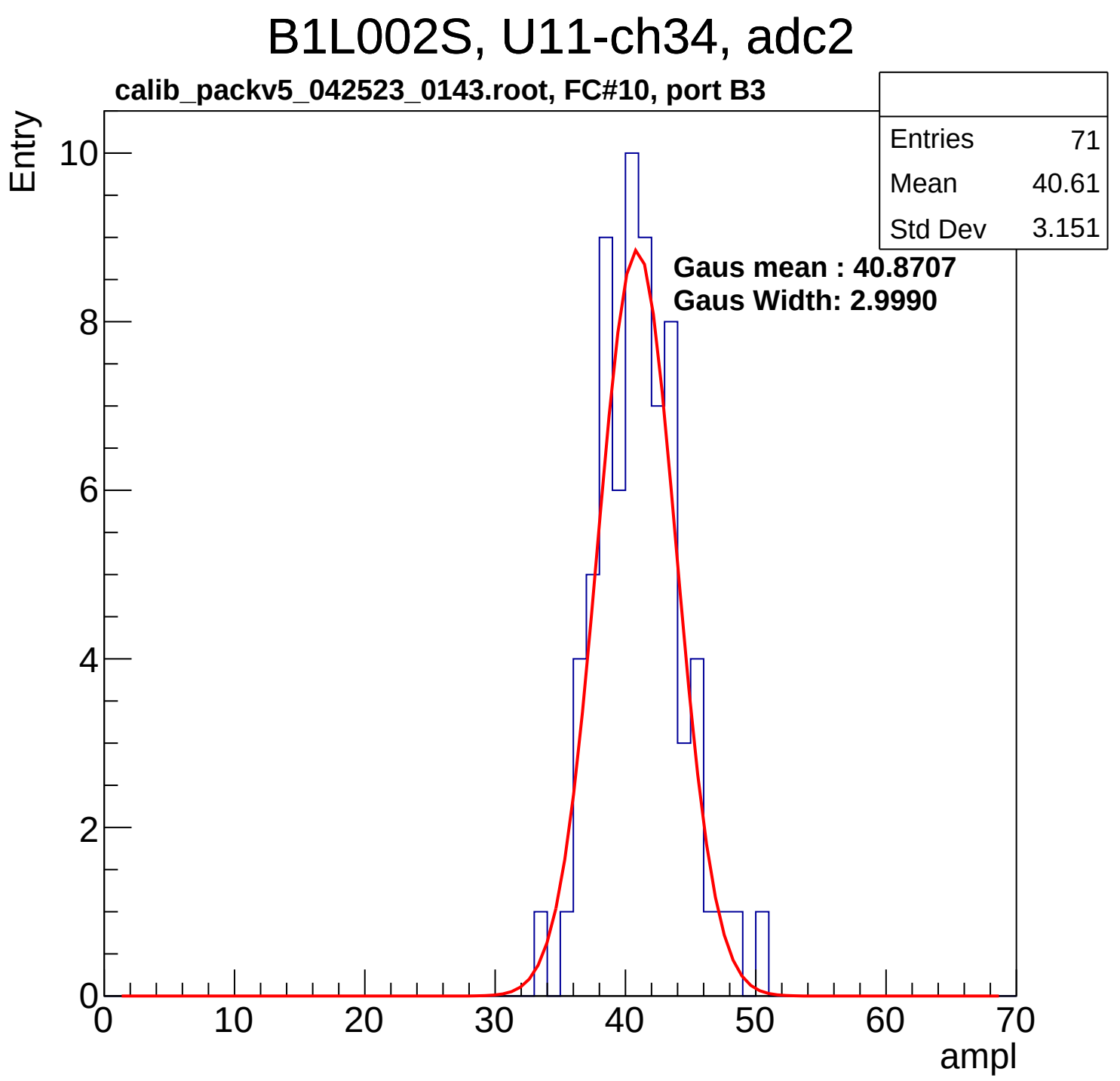
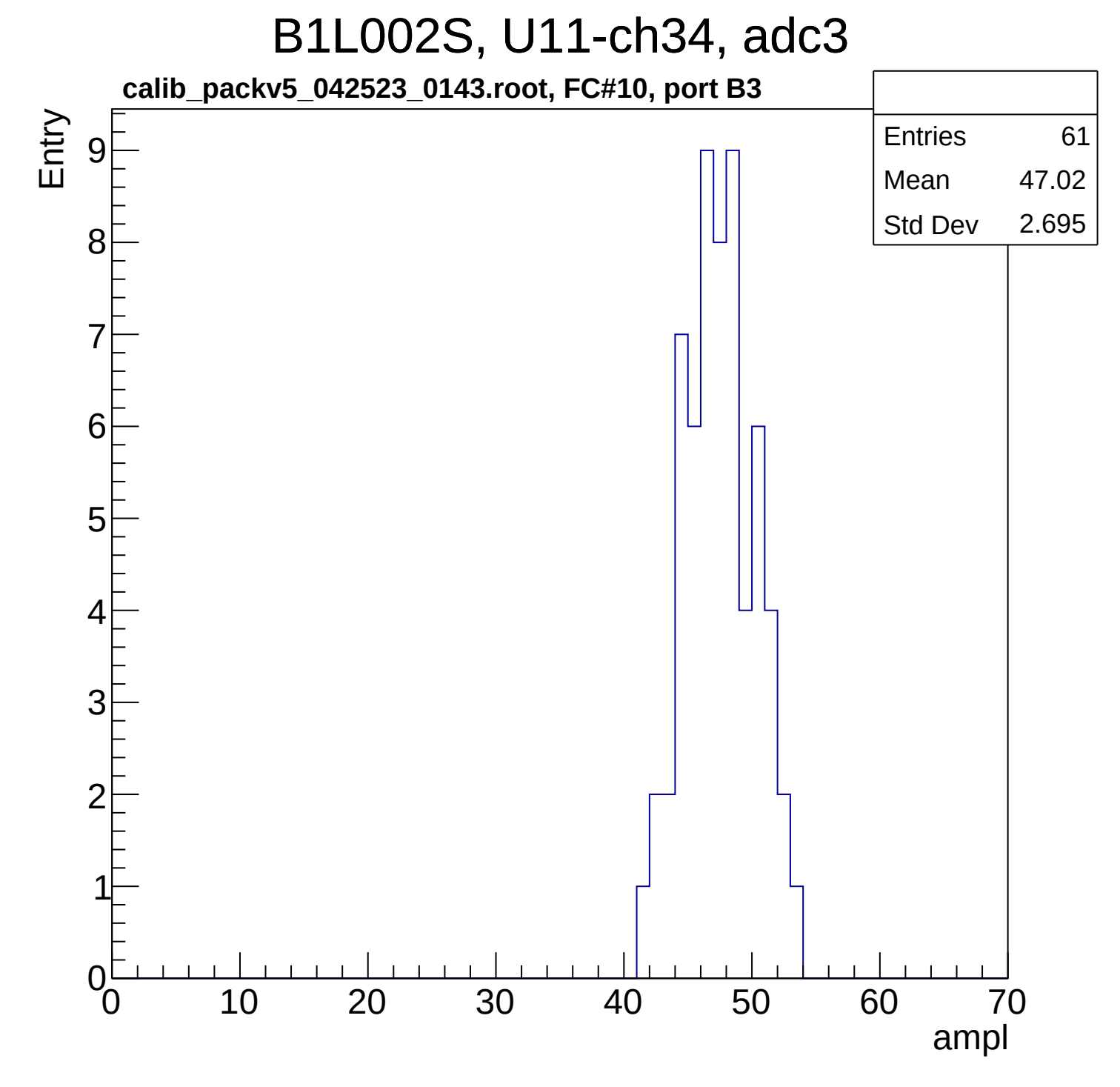
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.97
Std Dev 2.802
Gaus mean : 29.6670
Gaus Width: 2.8384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 34.85
Std Dev 2.511
Gaus mean : 35.0009
Gaus Width: 2.0238
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 40.61
Std Dev 3.151
Gaus mean : 40.8707
Gaus Width: 2.9990
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.02
Std Dev 2.695
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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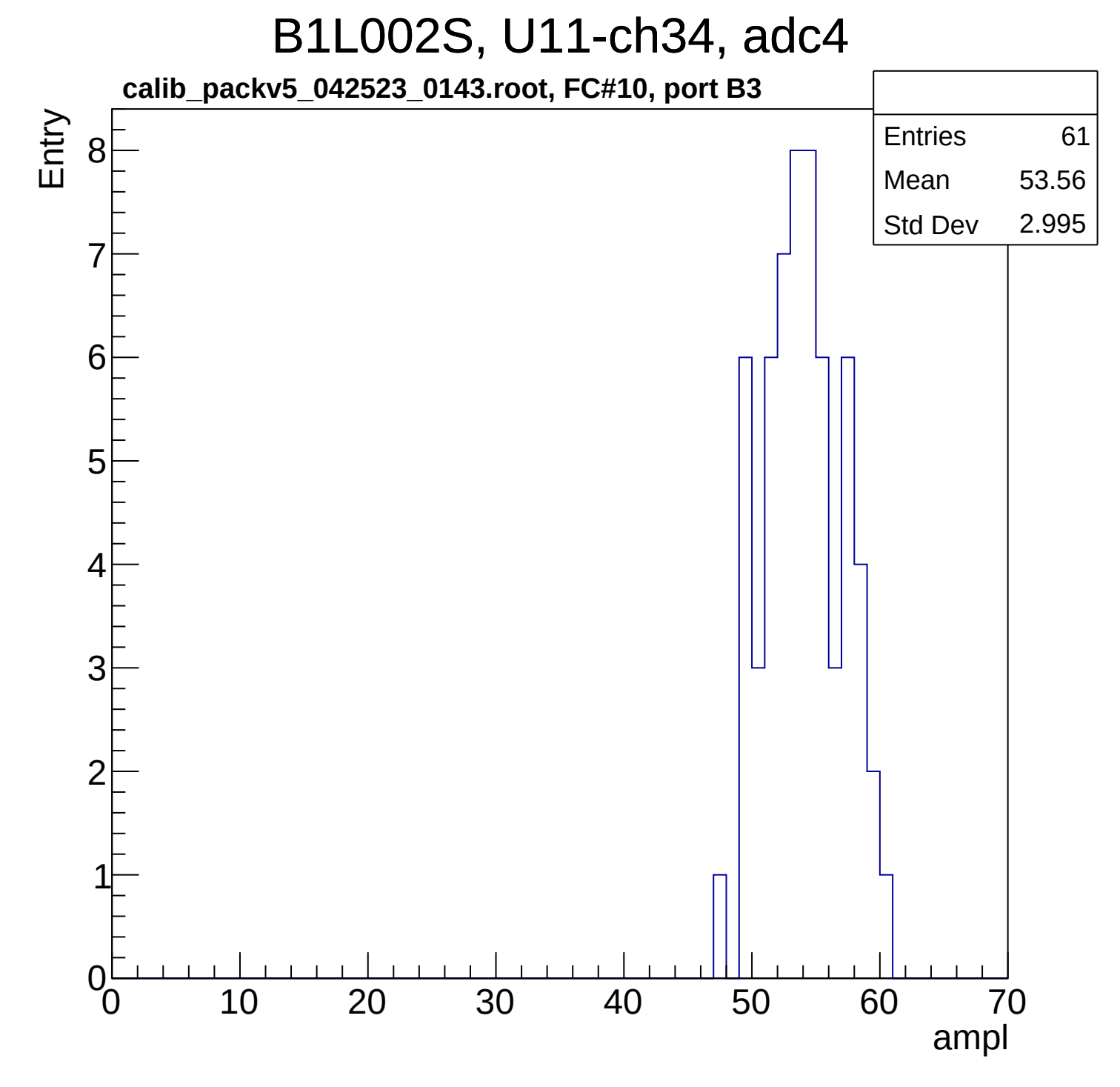
B1L002S, U11-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.56
Std Dev 2.995
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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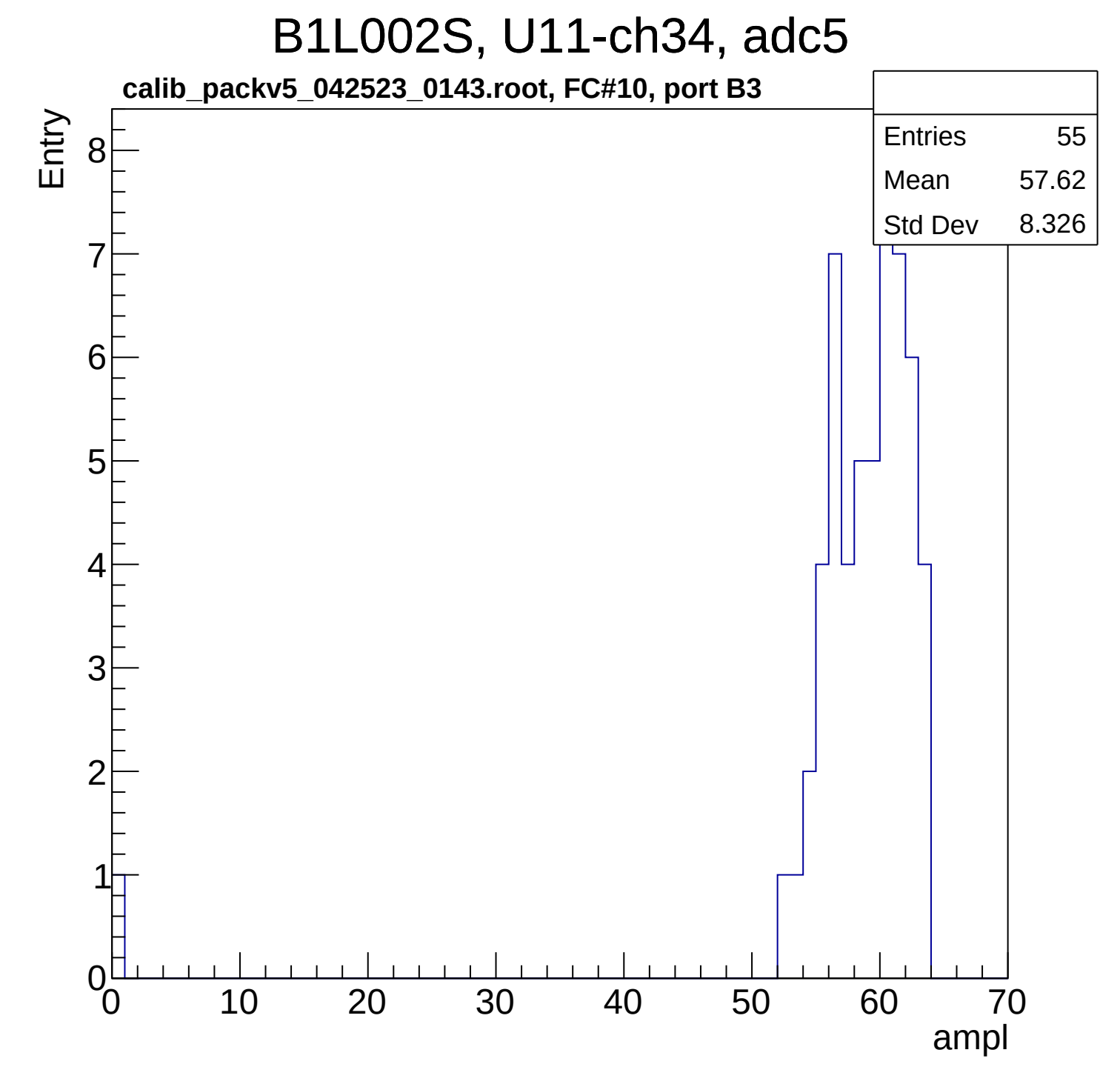
B1L002S, U11-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.62
Std Dev 8.326
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
50
60
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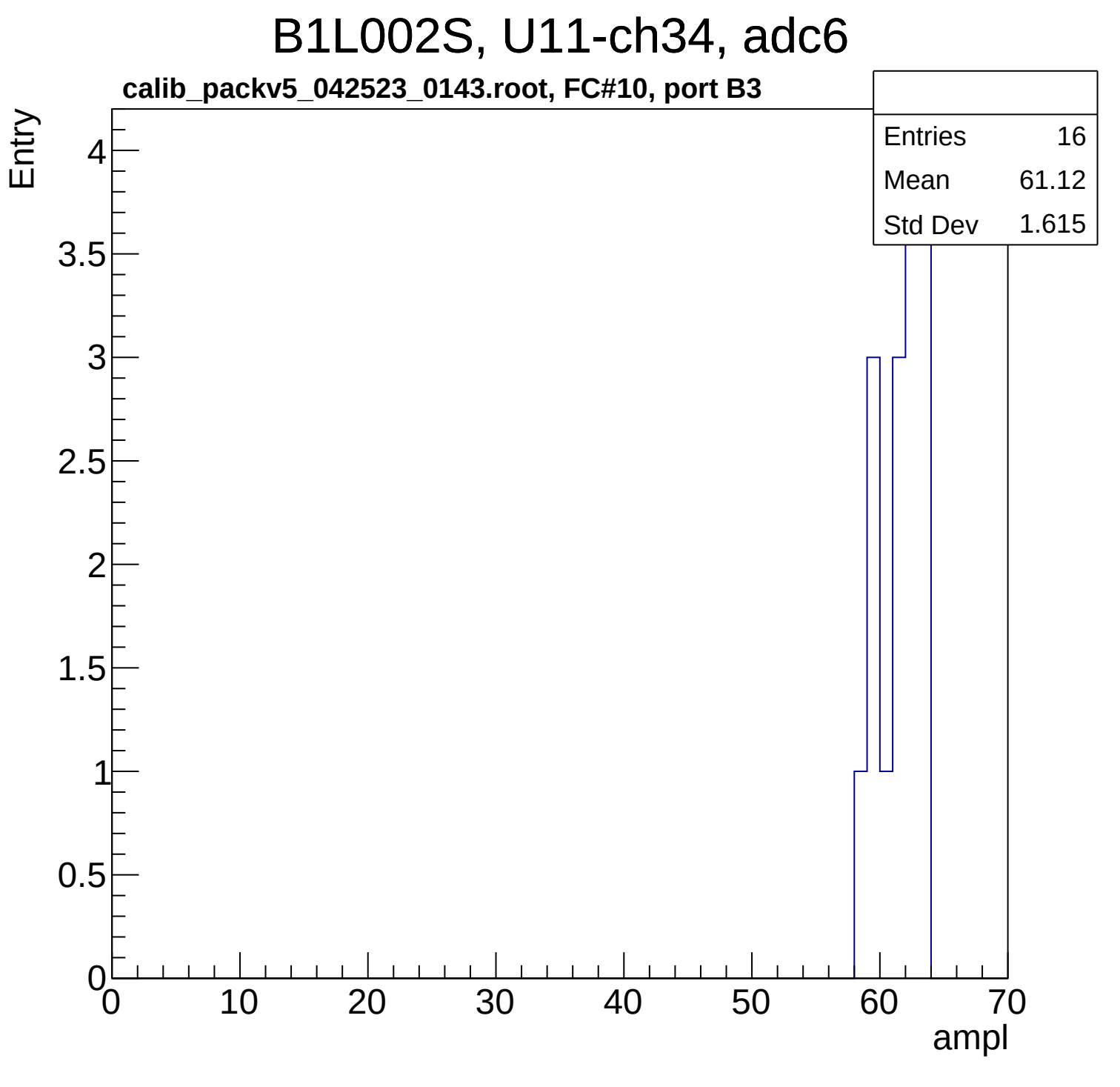
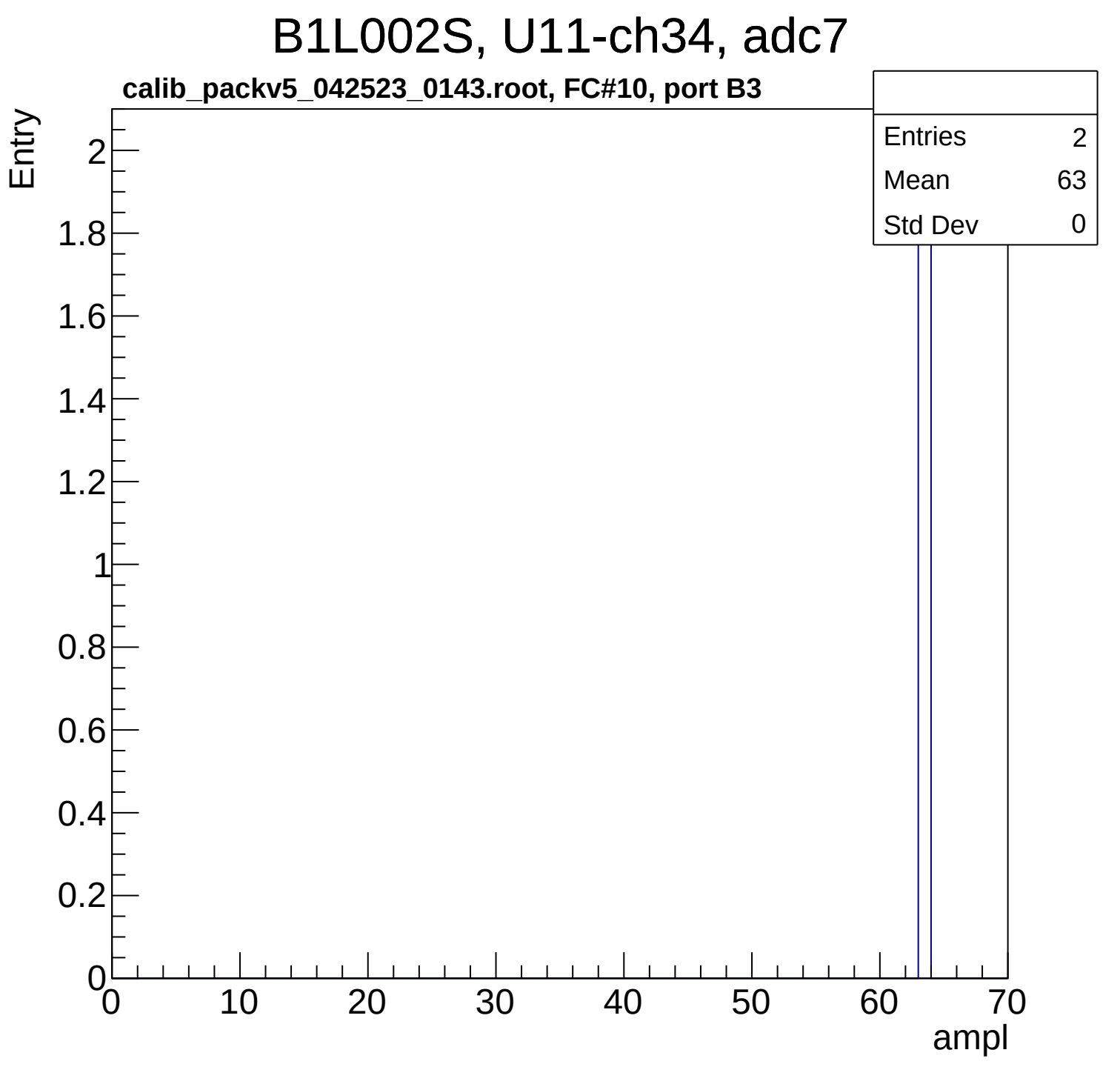

B1L002S, U11-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.12
Std Dev 1.615
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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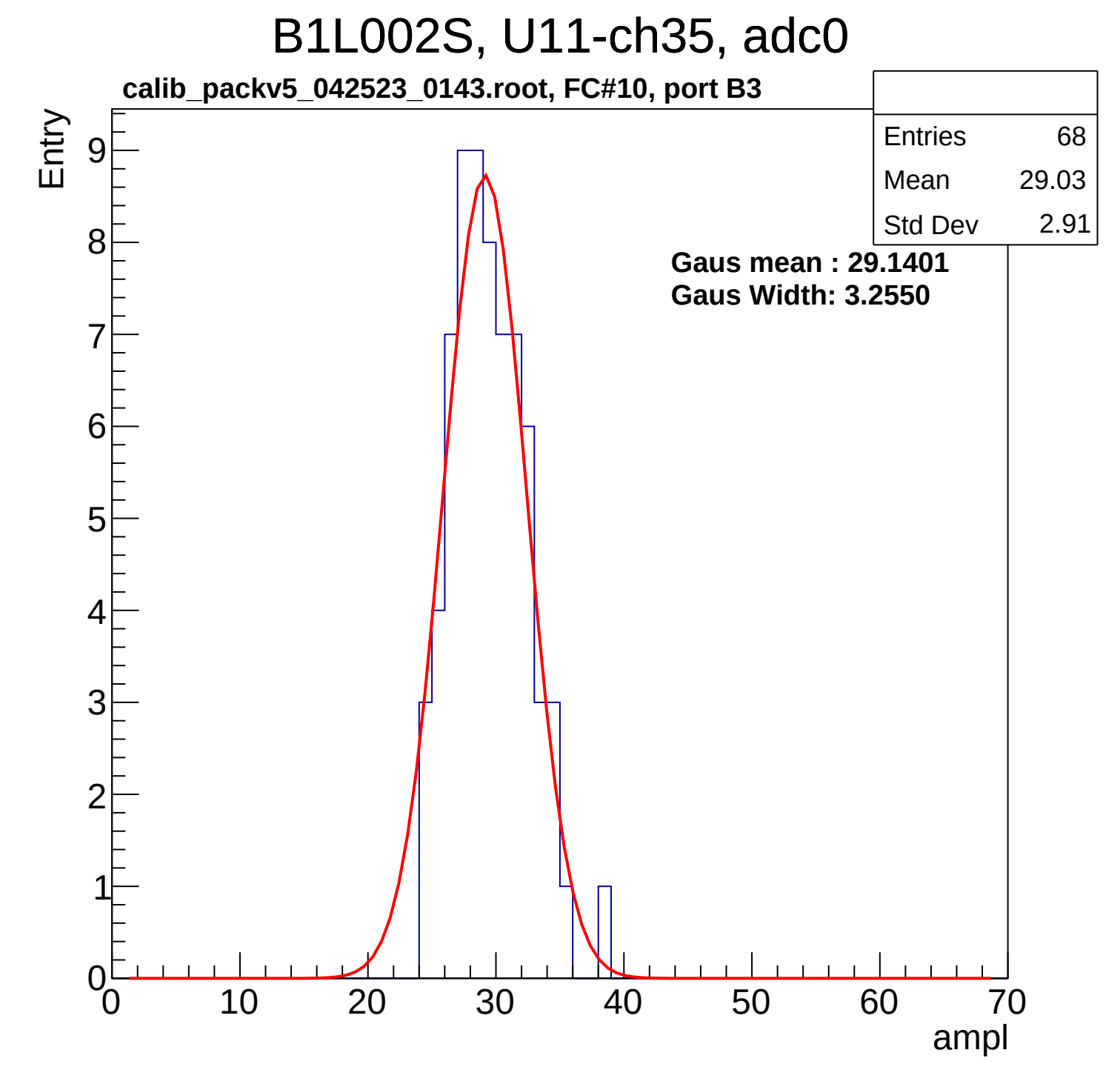
B1L002S, U11-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 29.03
Std Dev 2.91
Gaus mean : 29.1401
Gaus Width: 3.2550
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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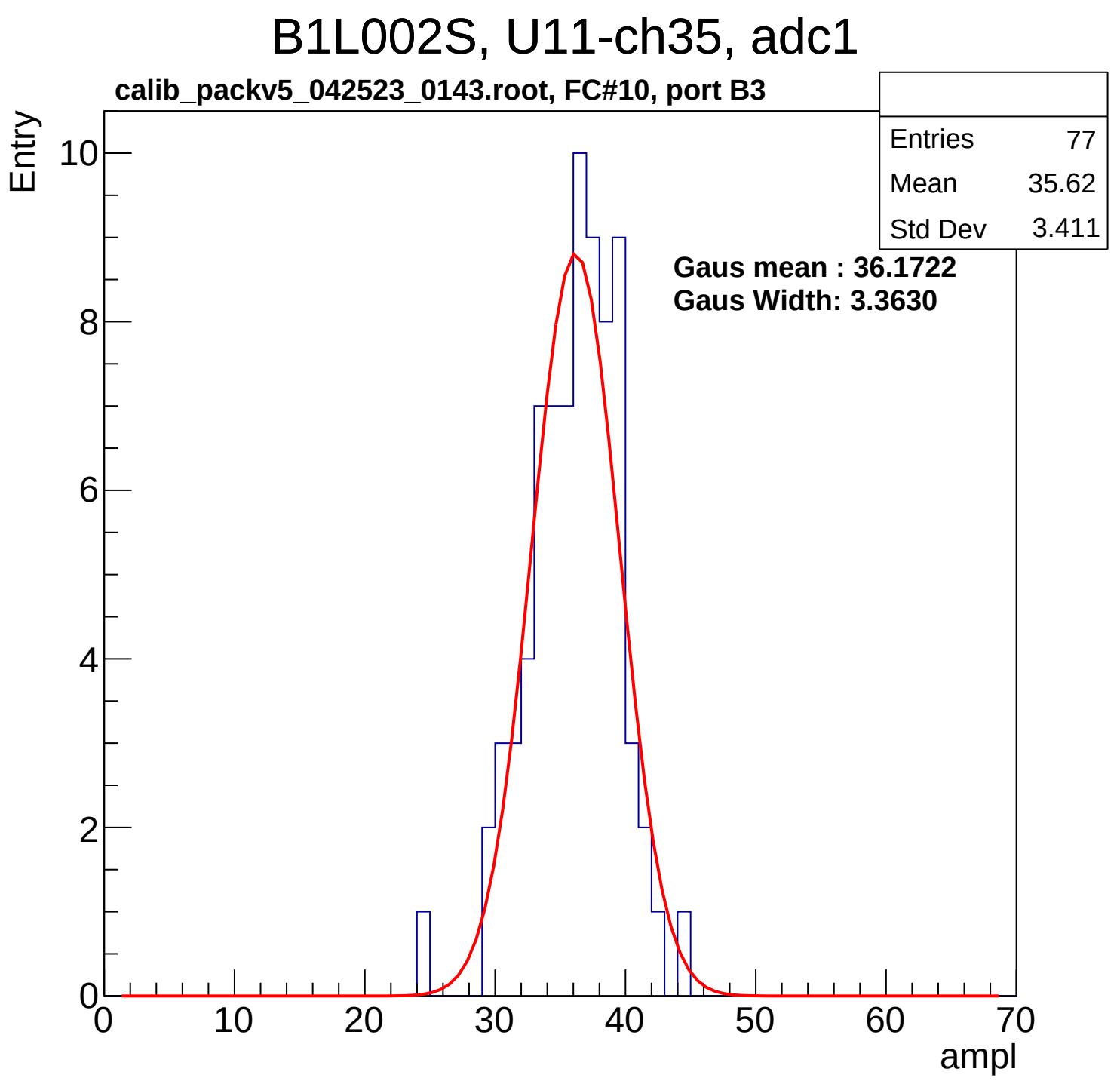
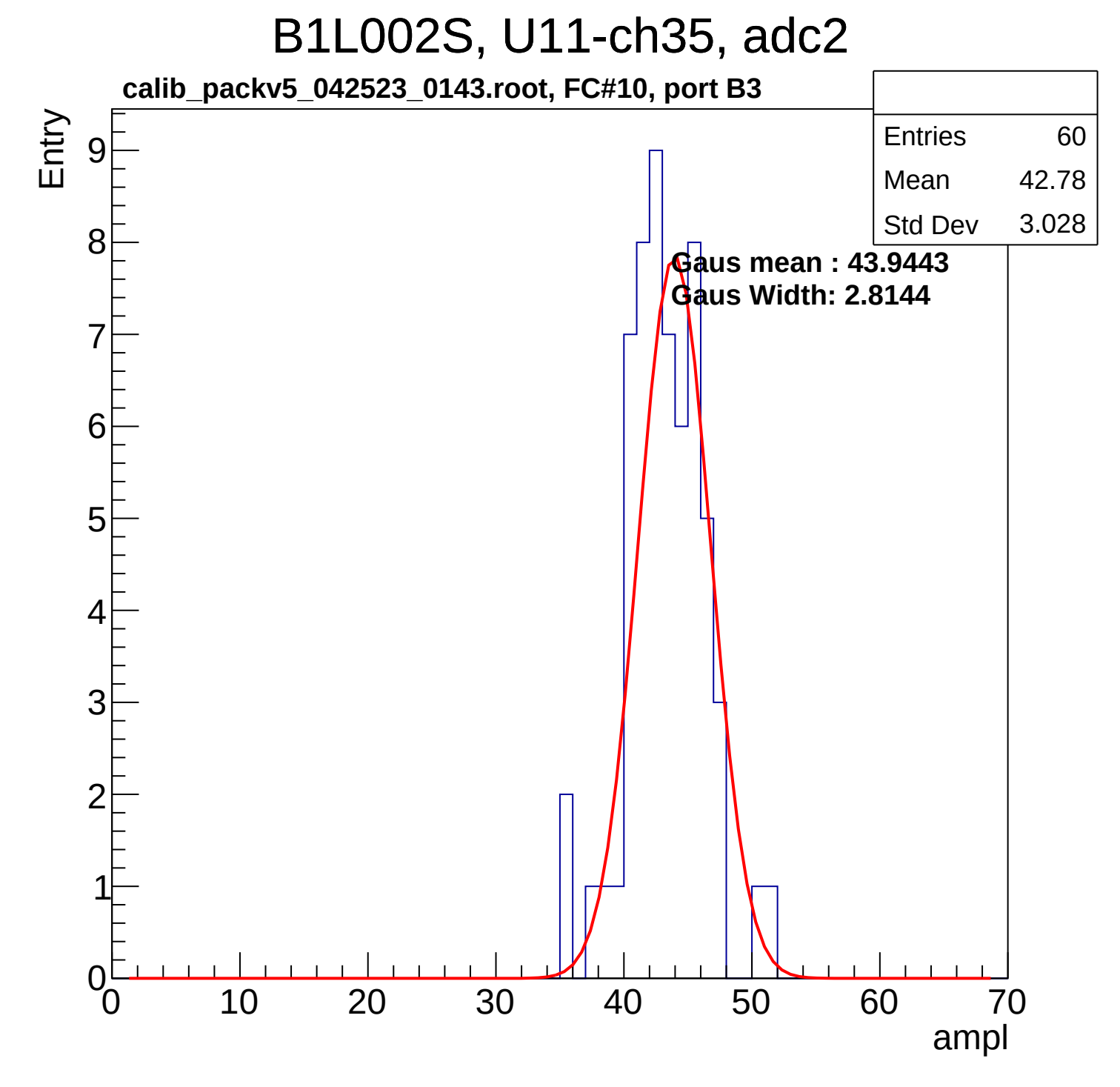

B1L002S, U11-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.62
Std Dev 3.411
Gaus mean : 36.1722
Gaus Width: 3.3630
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.78
Std Dev 3.028
Gaus mean : 43.9443
Gaus Width: 2.8144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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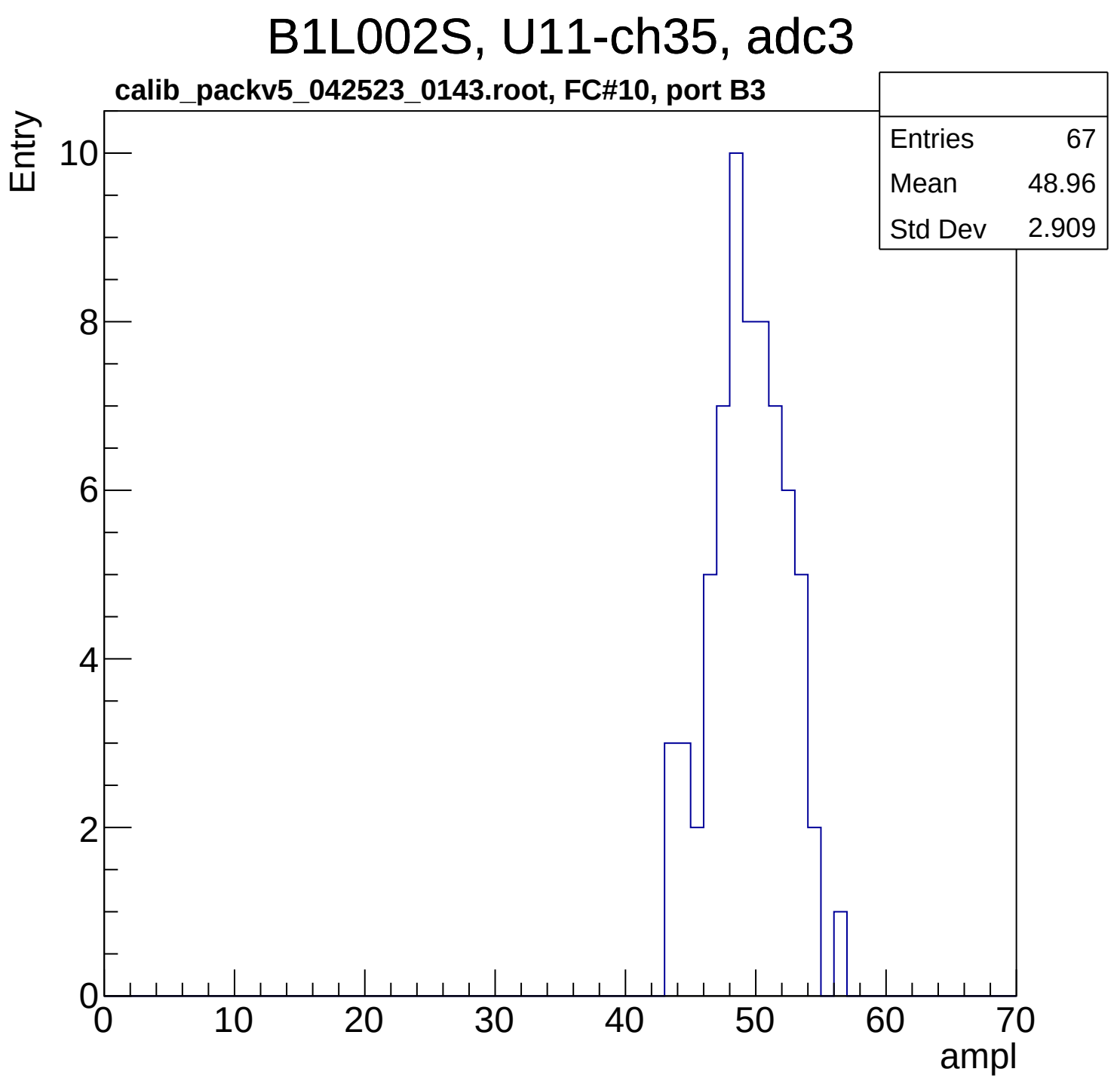
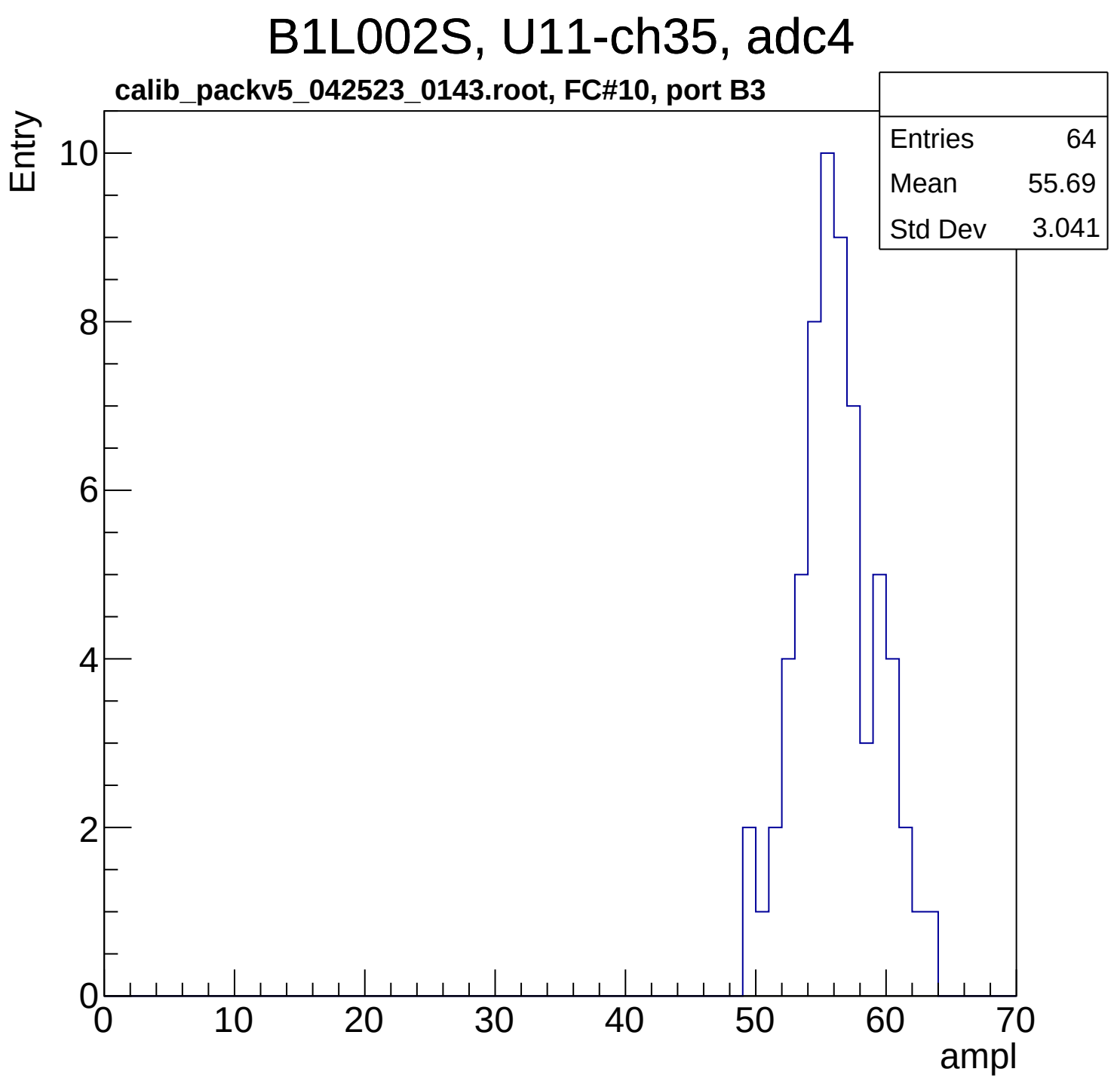
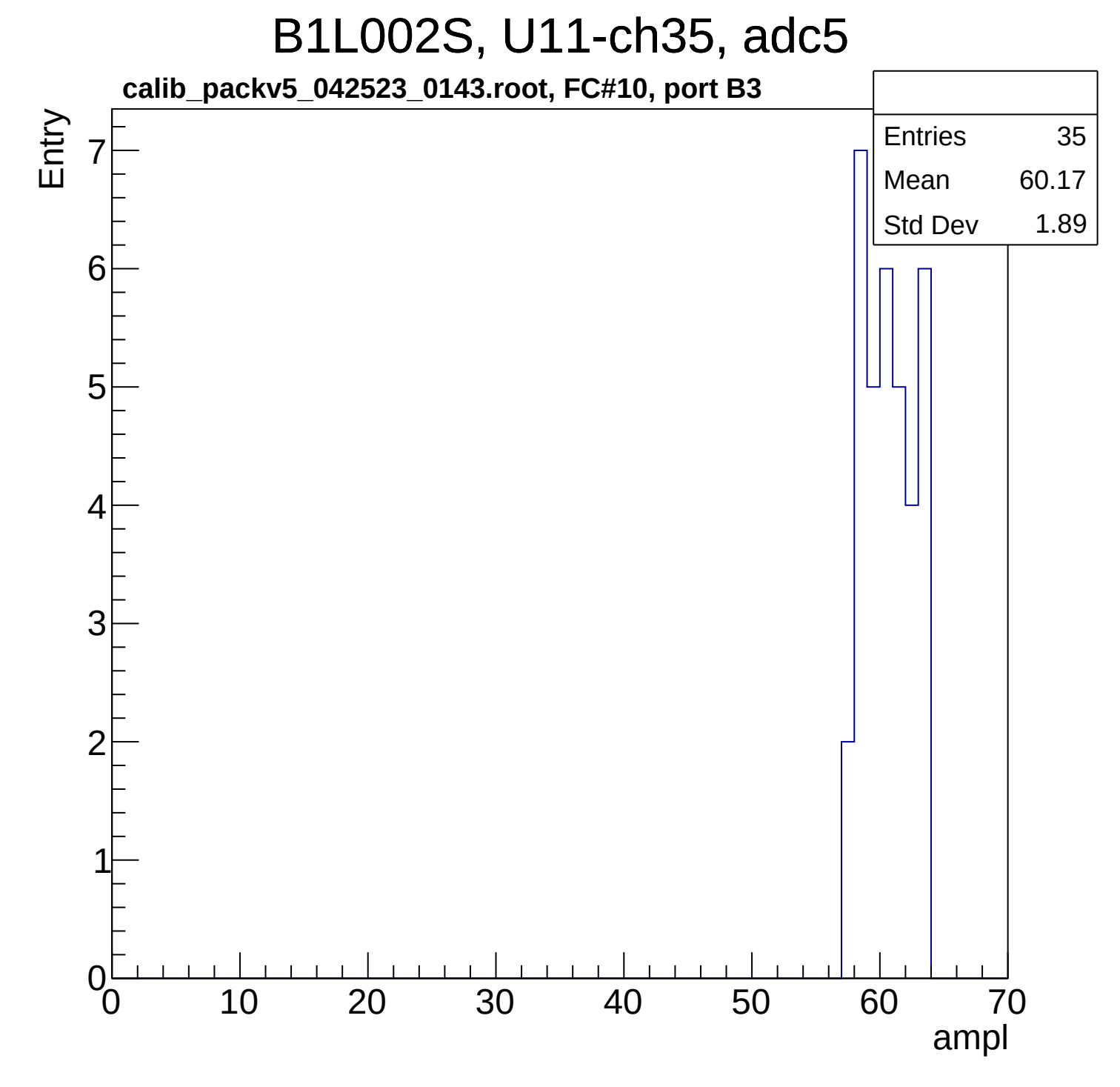

B1L002S, U11-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.96
Std Dev 2.909
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 55.69
Std Dev 3.041
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 60.17
Std Dev 1.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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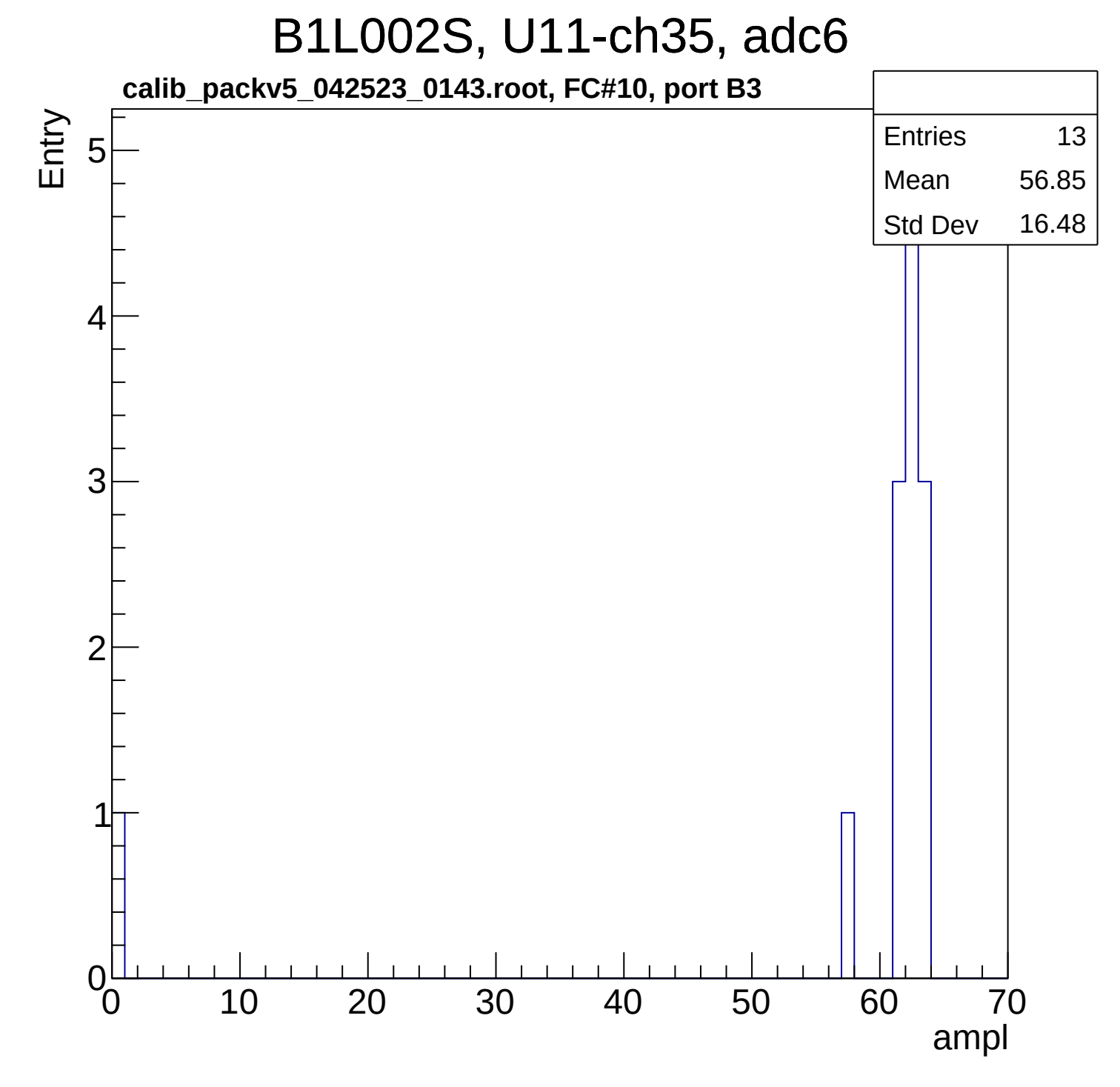
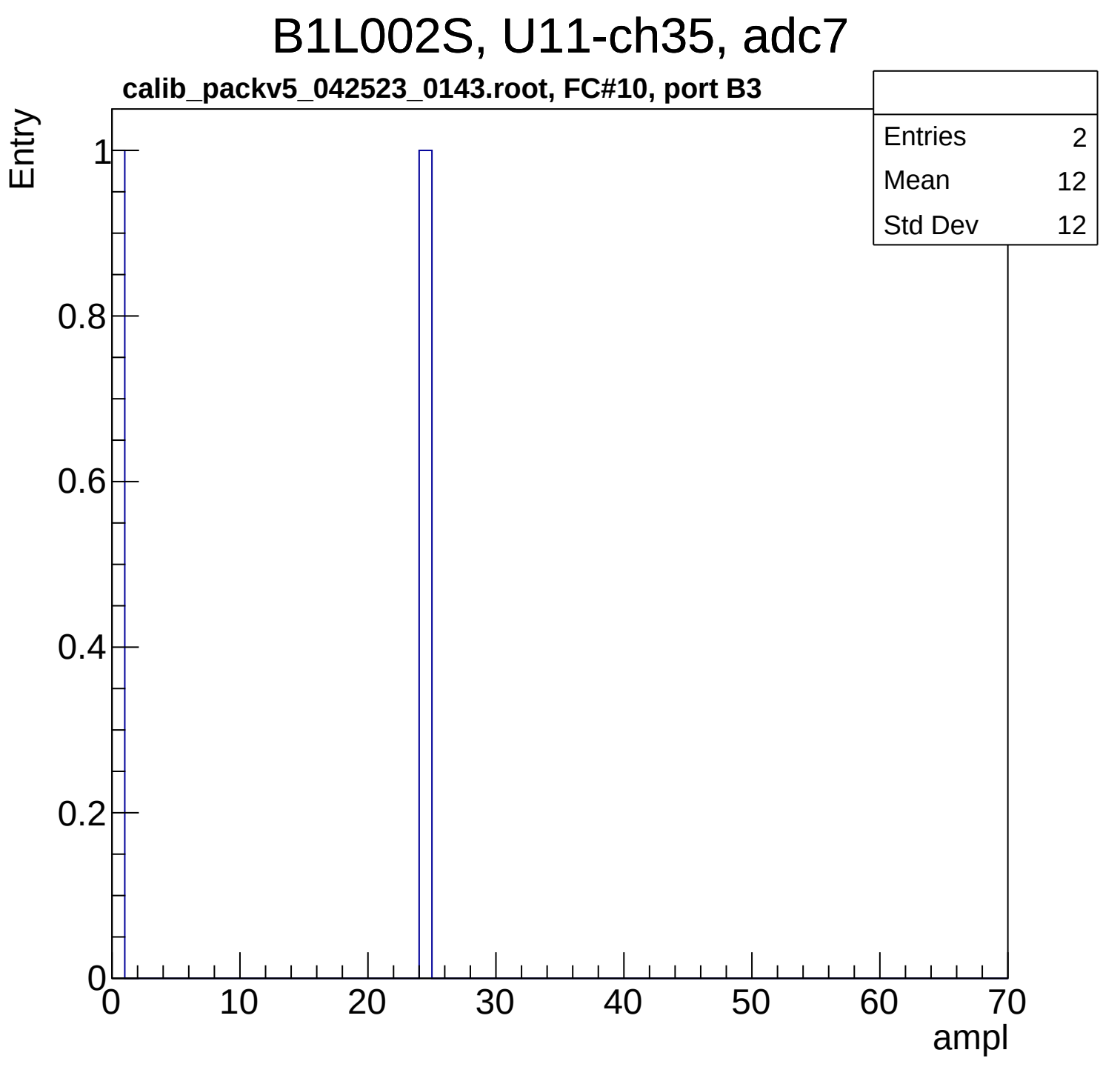
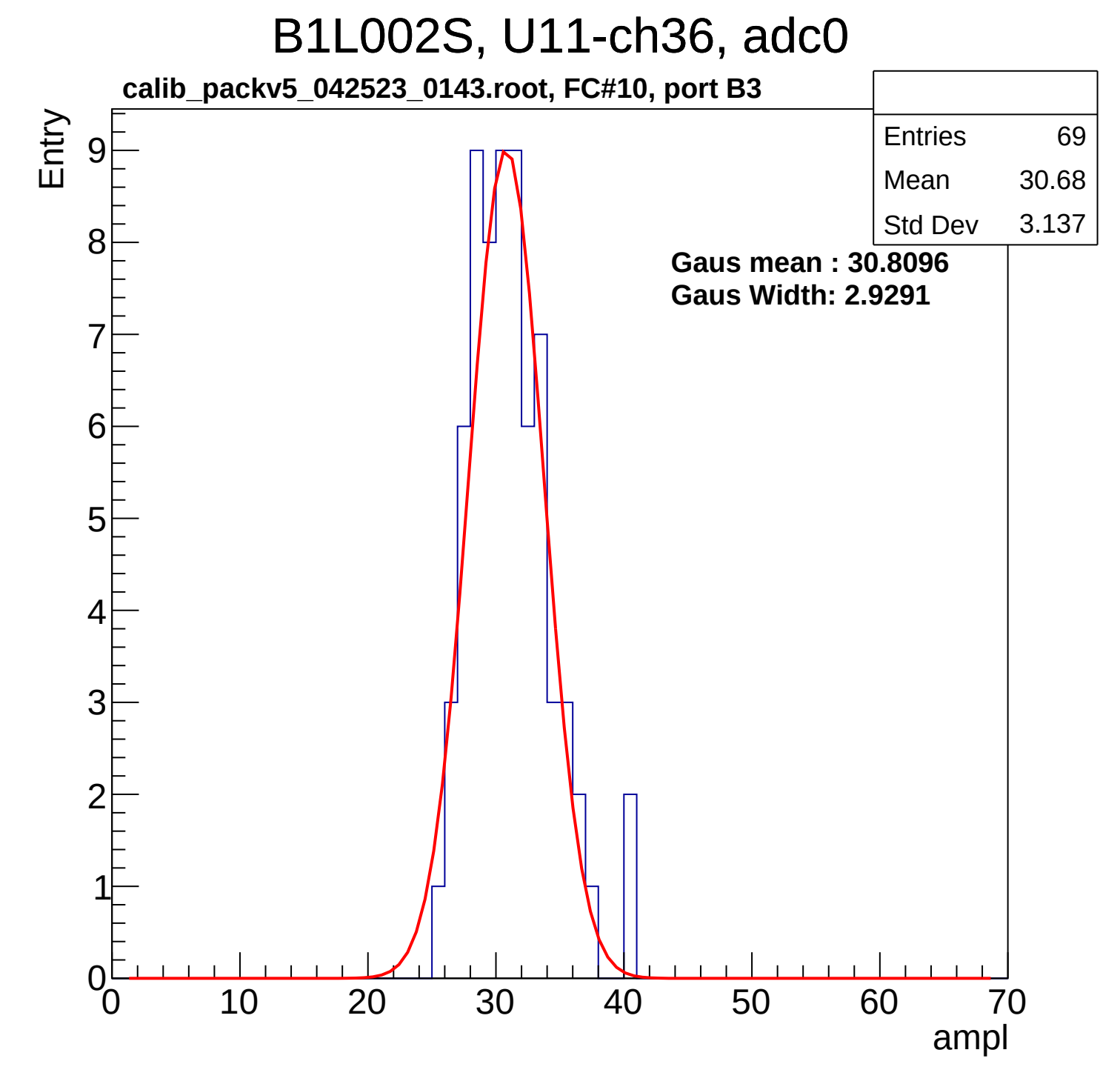
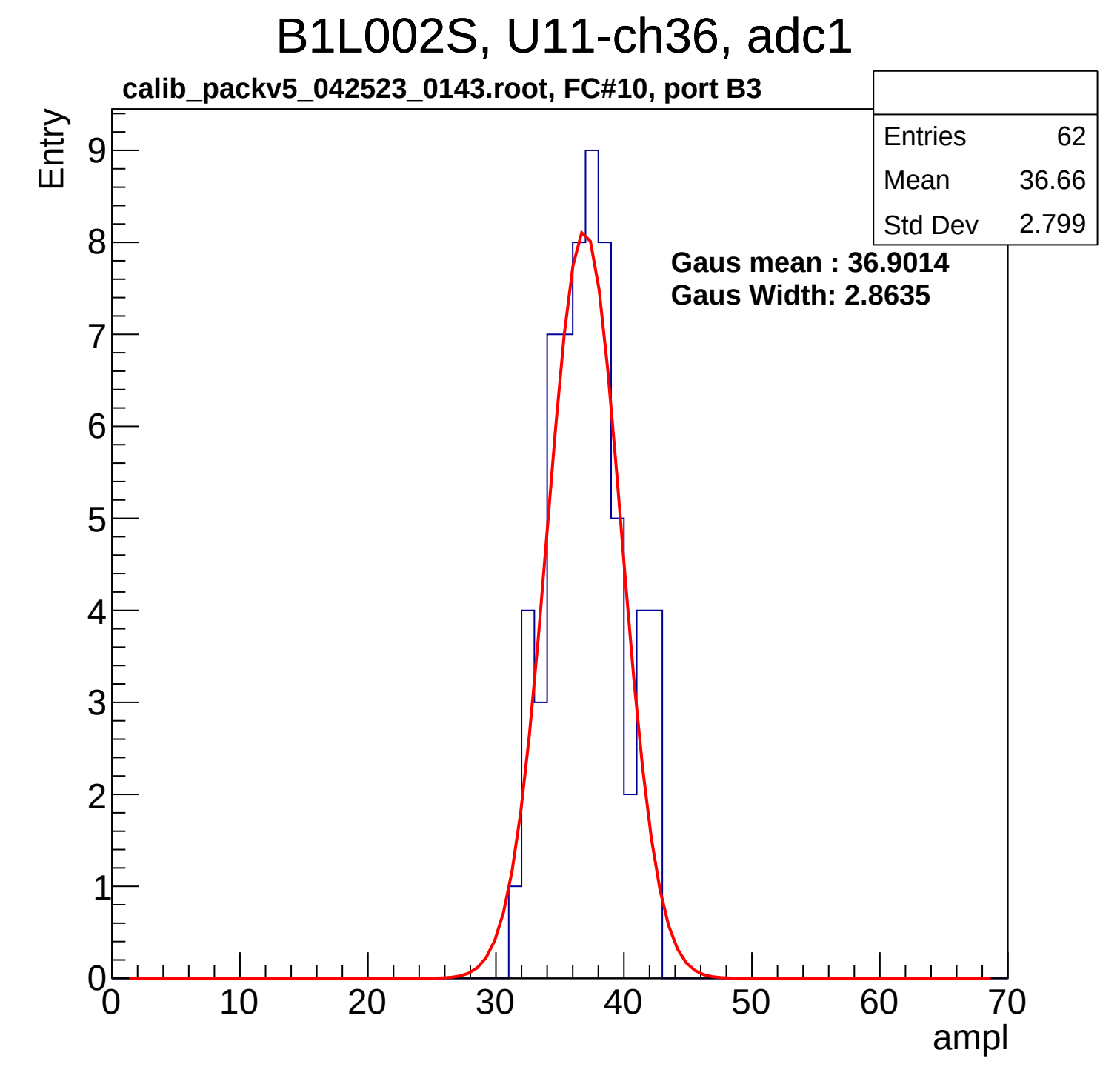
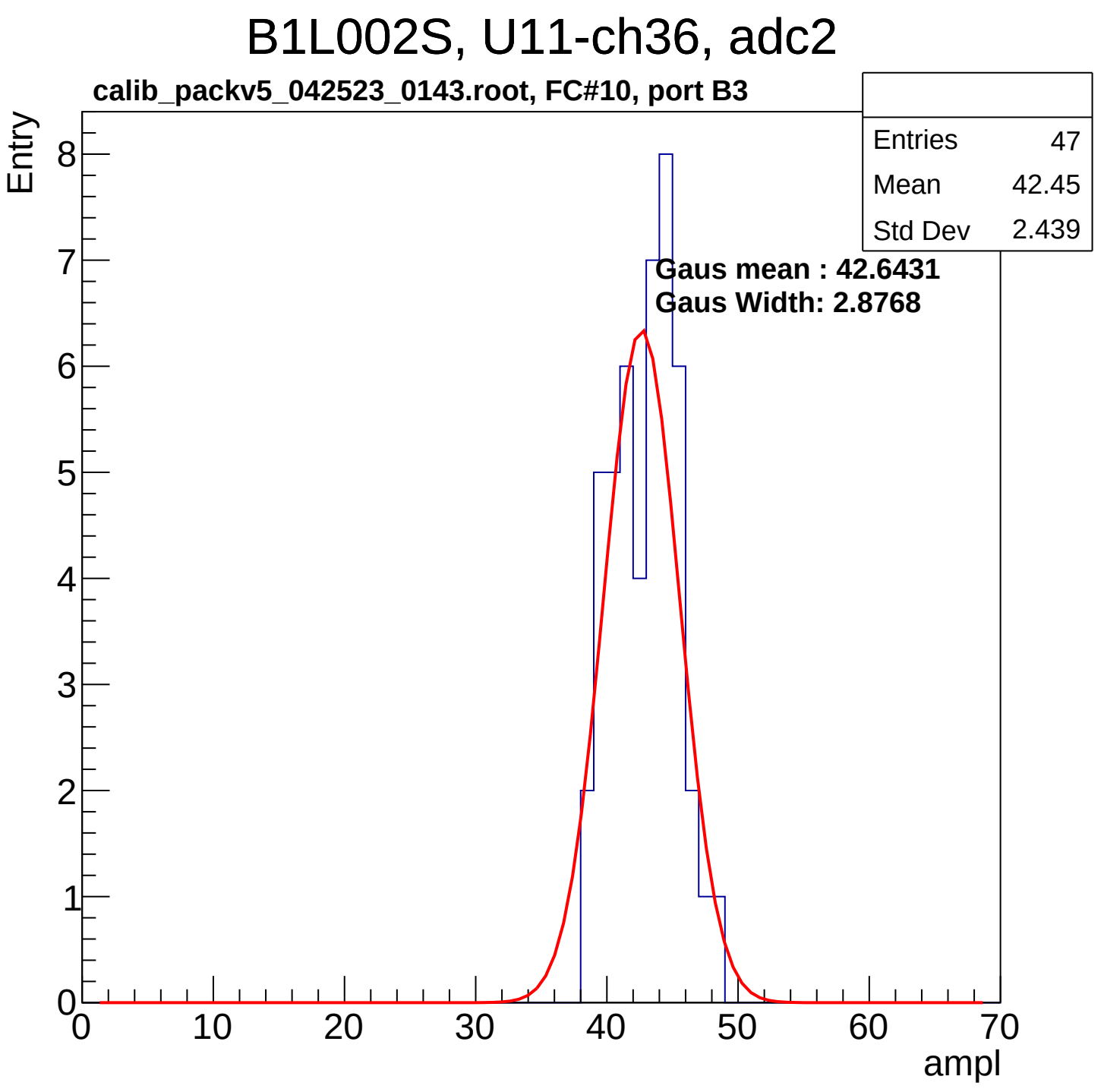

B1L002S, U11-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 56.85
Std Dev 16.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 30.68
Std Dev 3.137
Gaus mean : 30.8096
Gaus Width: 2.9291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 36.66
Std Dev 2.799
Gaus mean : 36.9014
Gaus Width: 2.8635
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 42.45
Std Dev 2.439
Gaus mean : 42.6431
Gaus Width: 2.8768
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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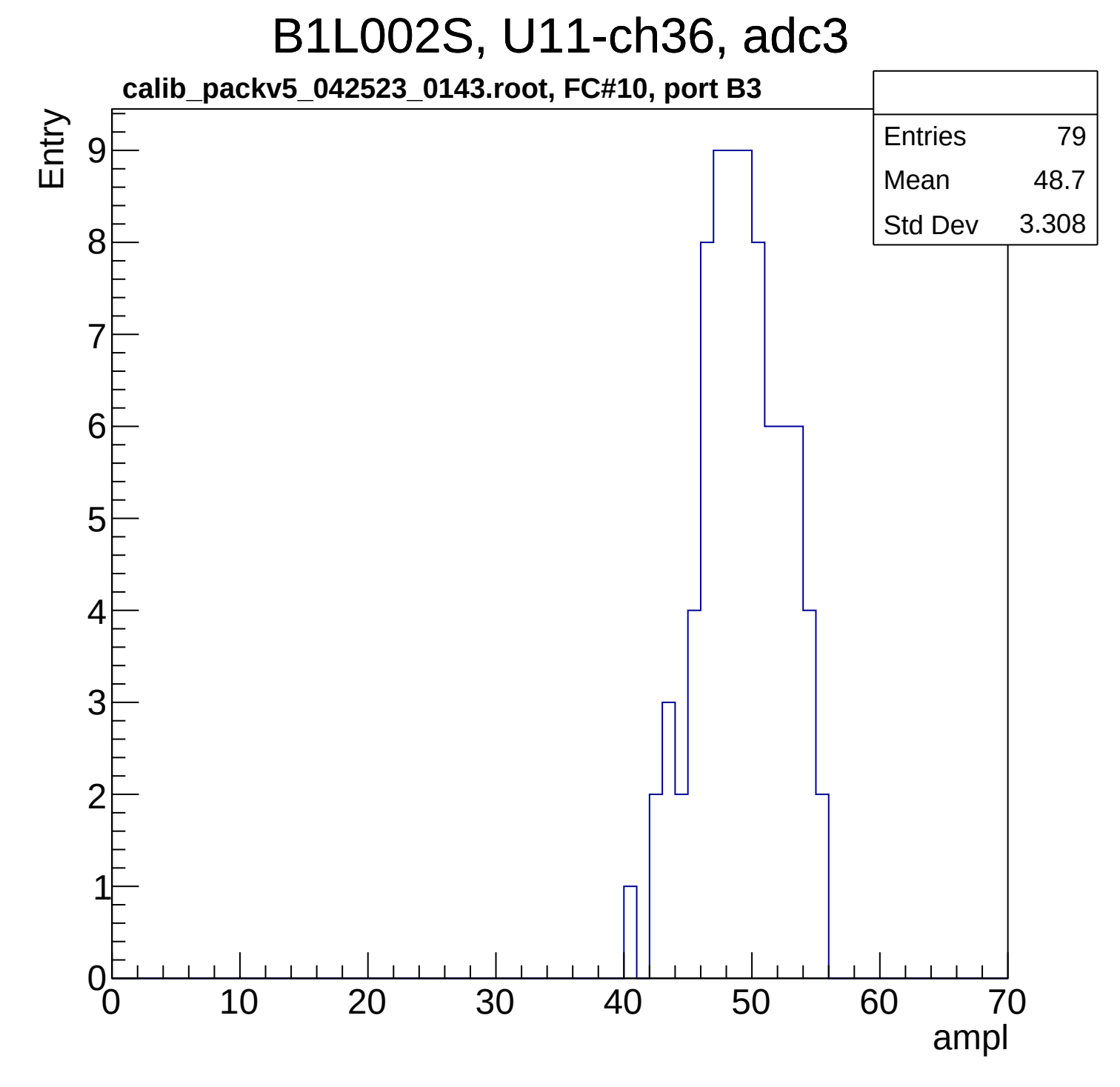
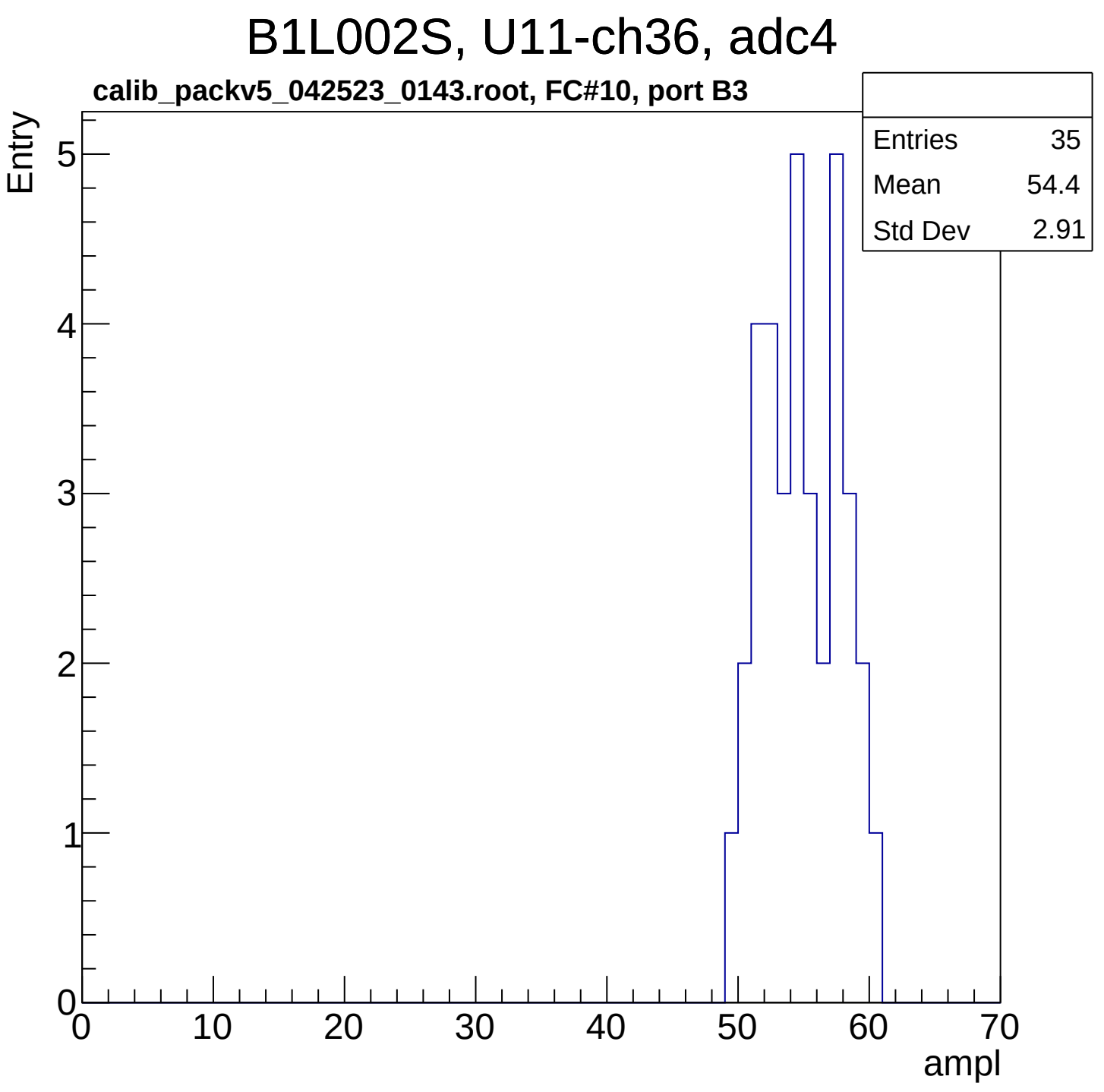
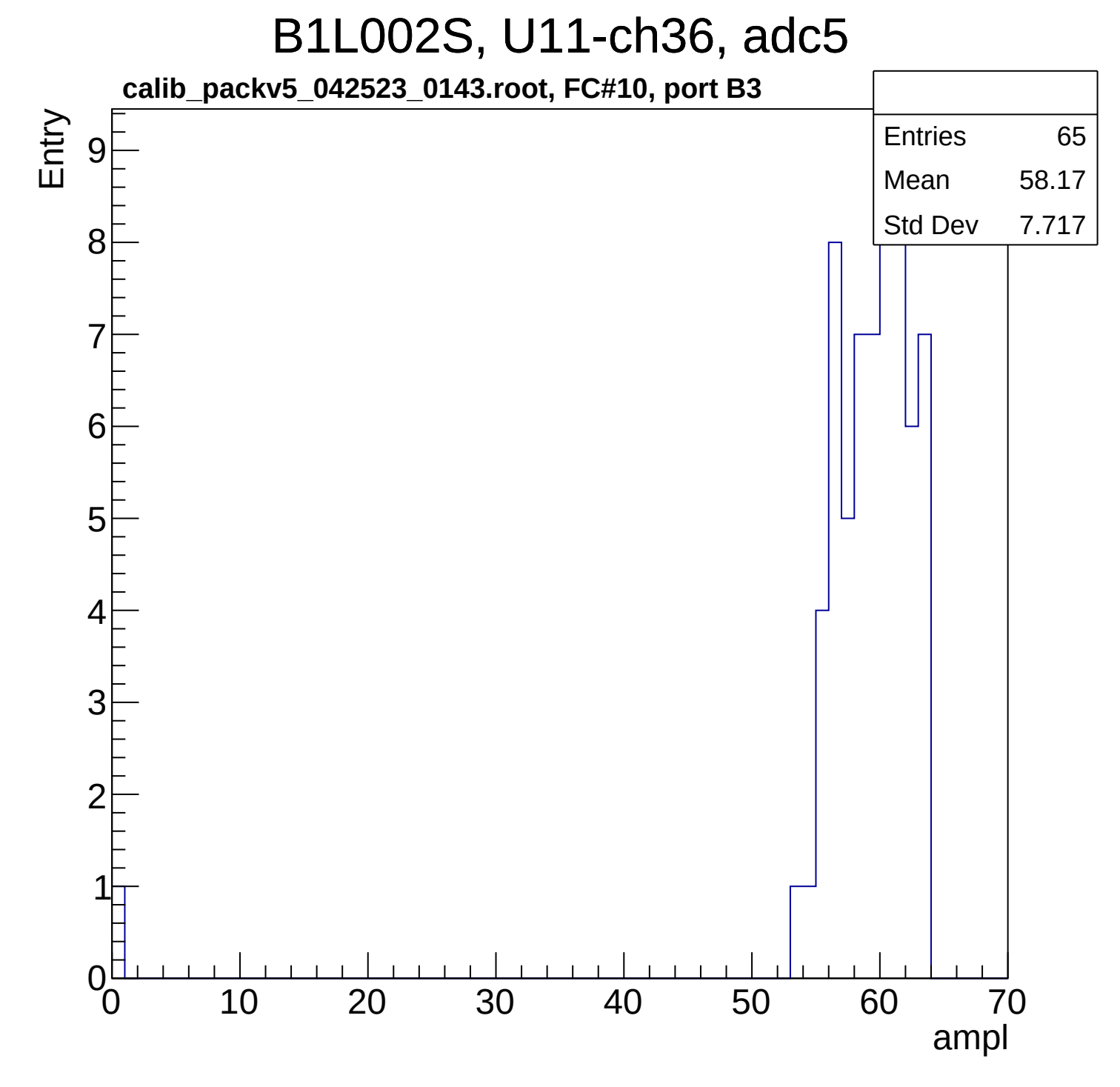
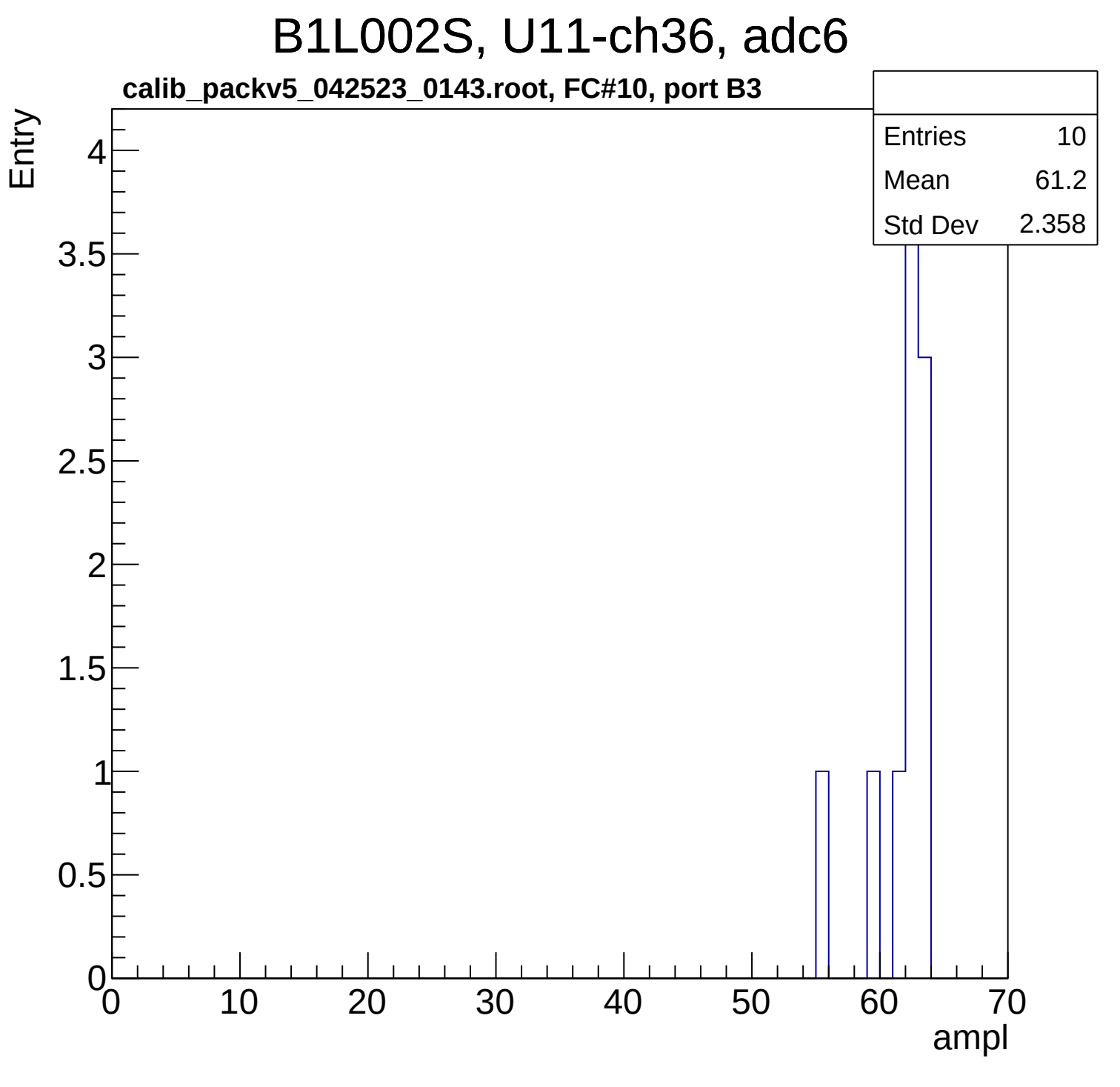
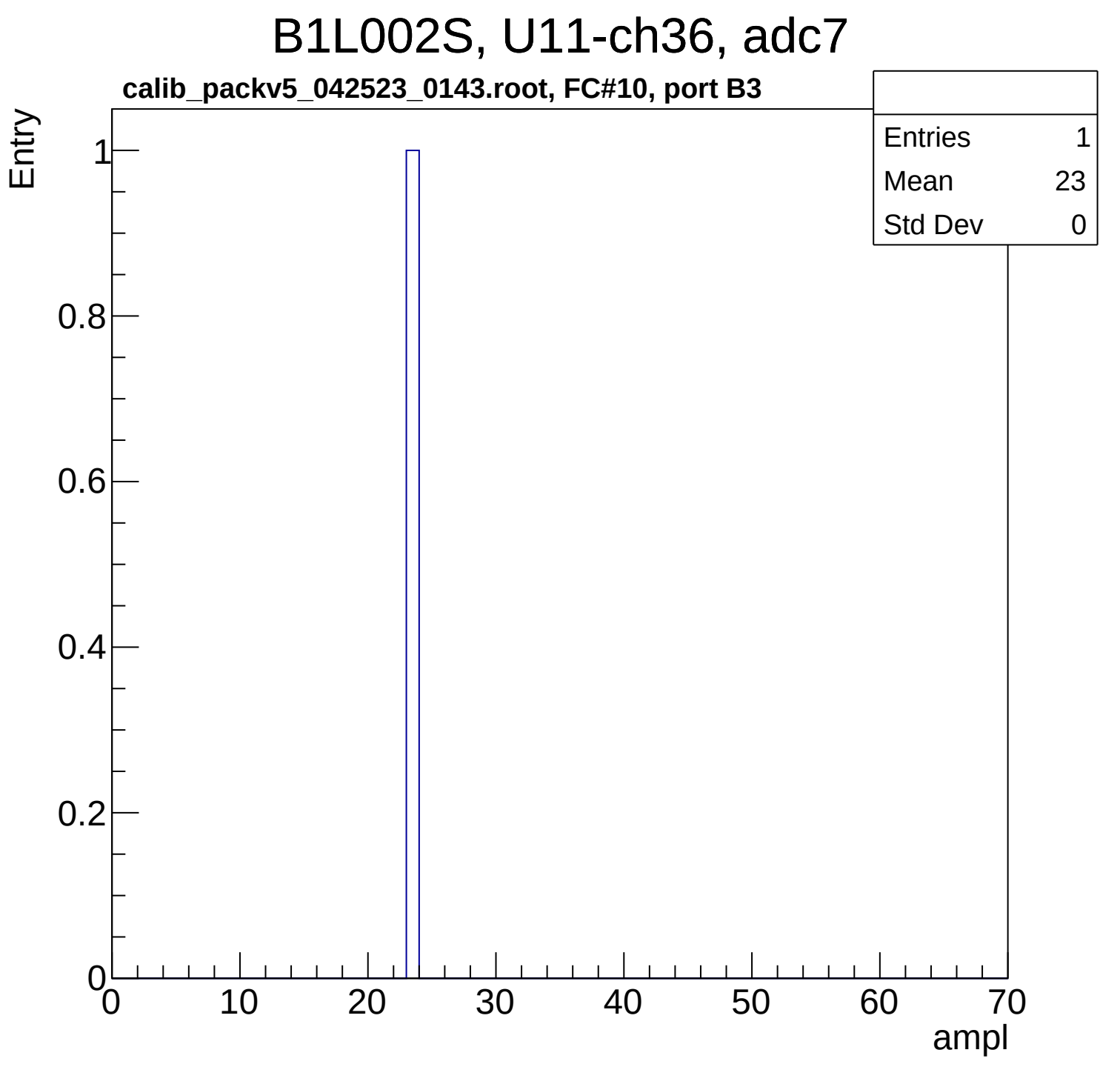
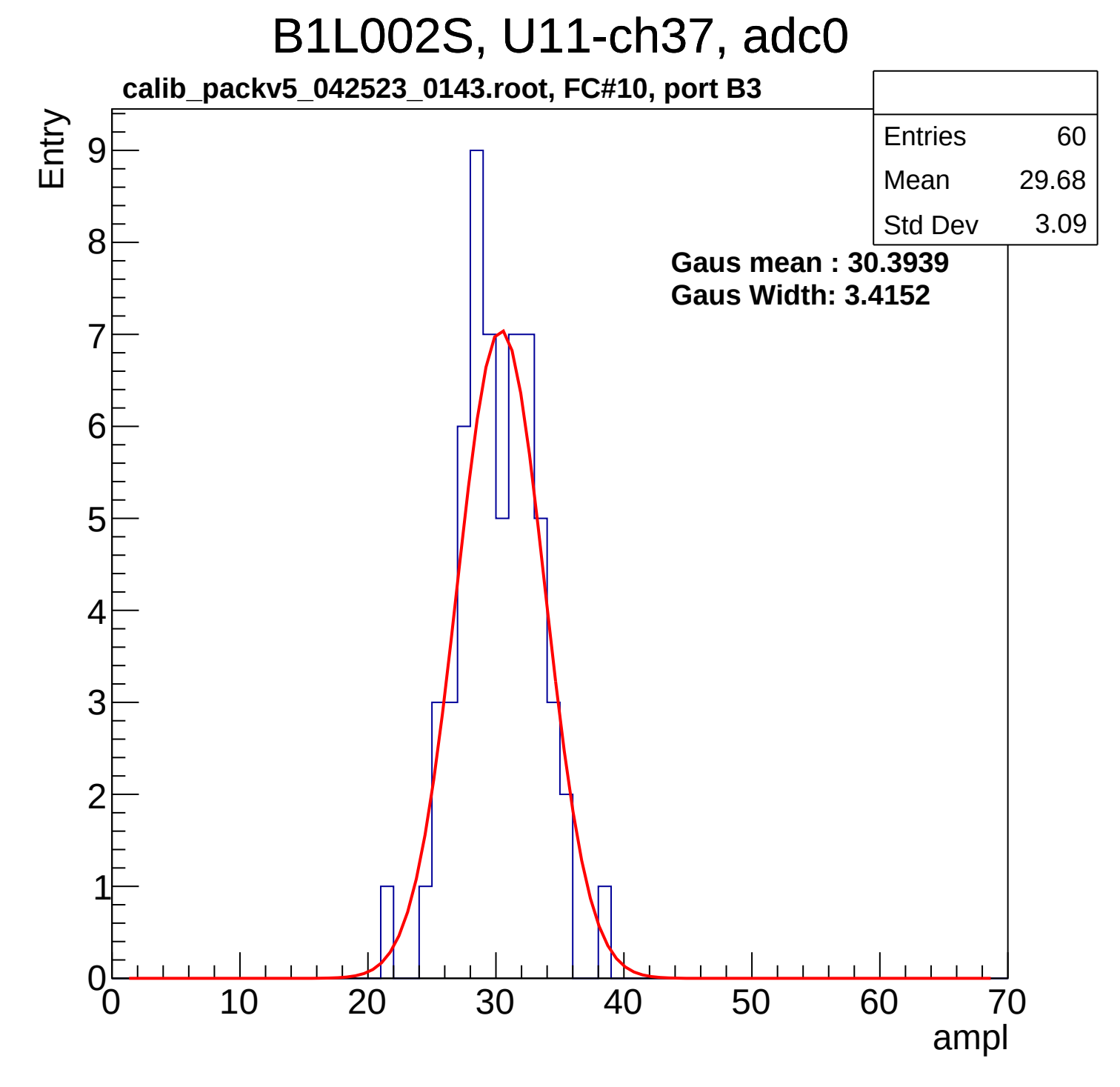

B1L002S, U11-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 48.7
Std Dev 3.308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

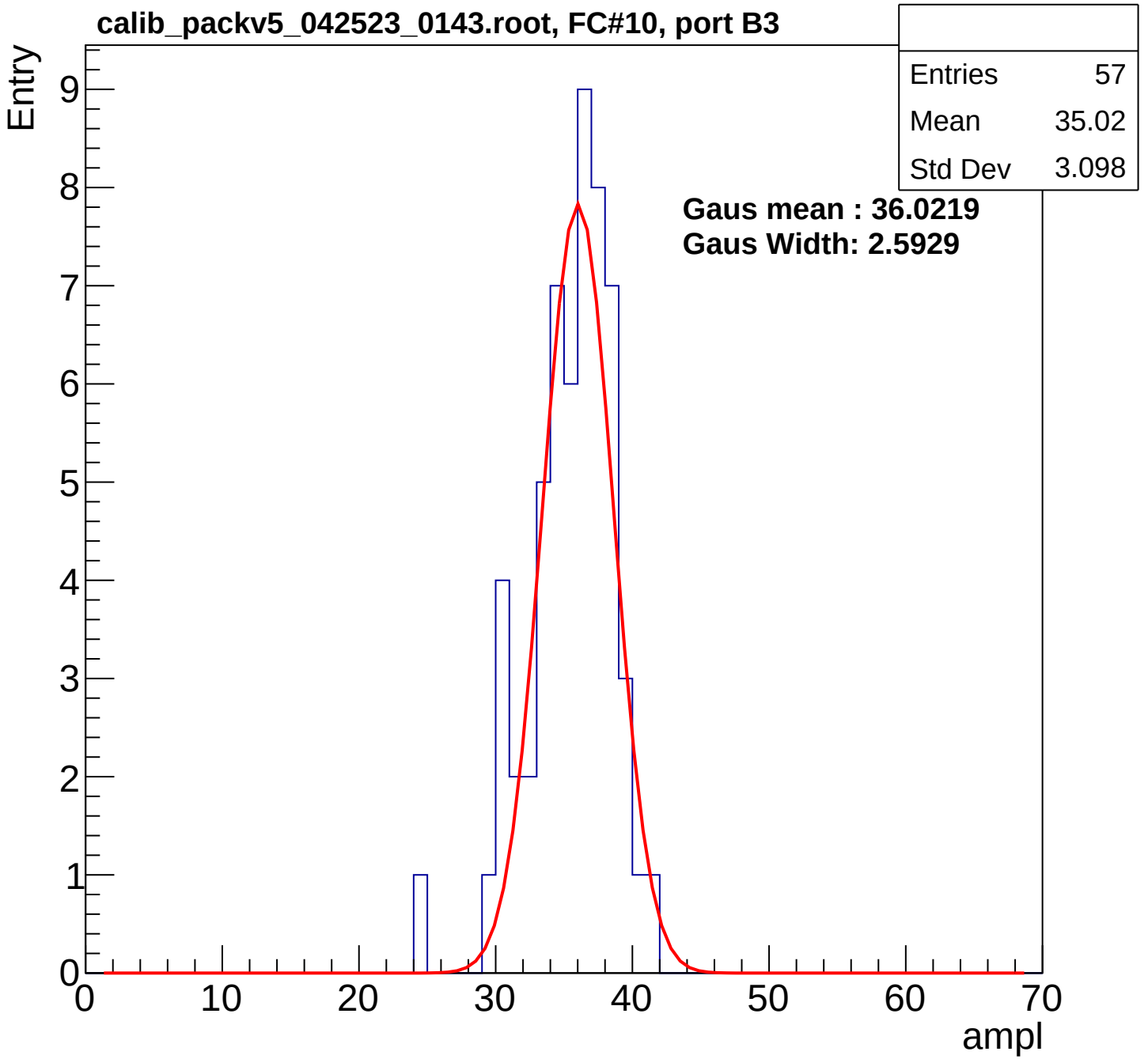

B1L002S, U11-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 54.4
Std Dev 2.91
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

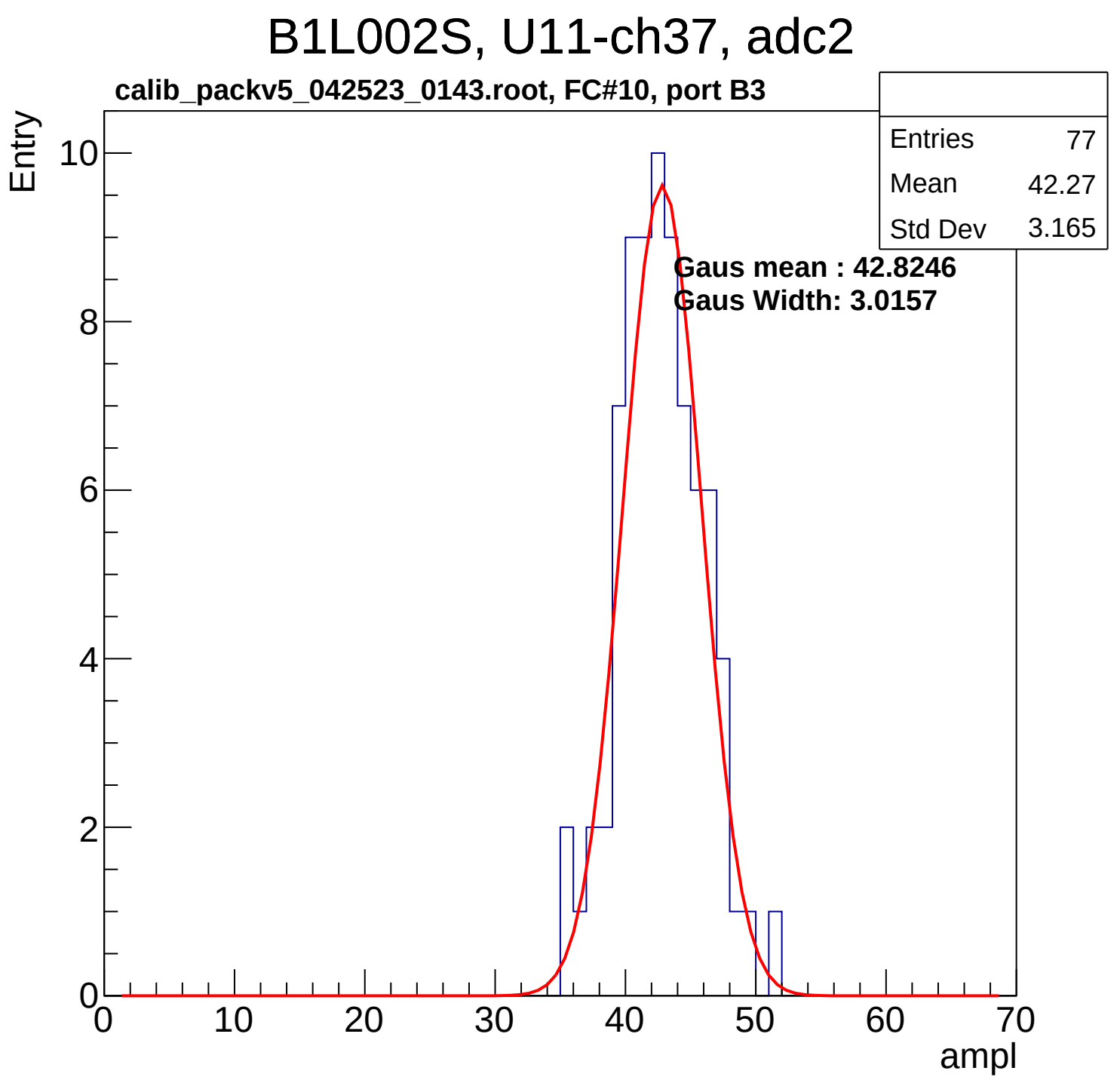

B1L002S, U11-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.17
Std Dev 7.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

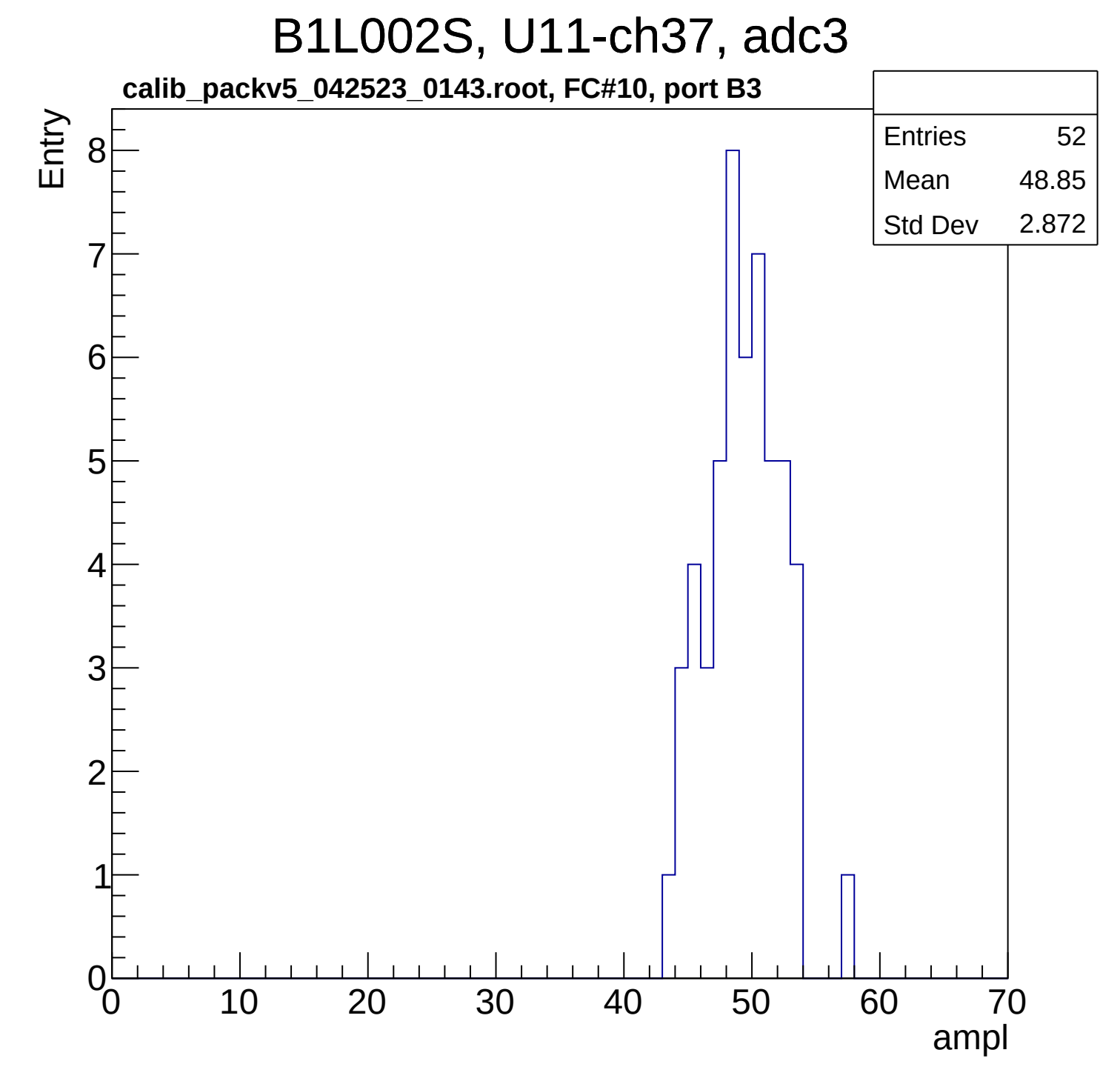

B1L002S, U11-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.2
Std Dev 2.358
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 29.68
Std Dev 3.09
Gaus mean : 30.3939
Gaus Width: 3.4152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.02
Std Dev 3.098
Gaus mean : 36.0219
Gaus Width: 2.5929
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 42.27
Std Dev 3.165
Gaus mean : 42.8246
Gaus Width: 3.0157
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 48.85
Std Dev 2.872
Entry
ampl
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1
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3
4
5
6
7
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0
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60
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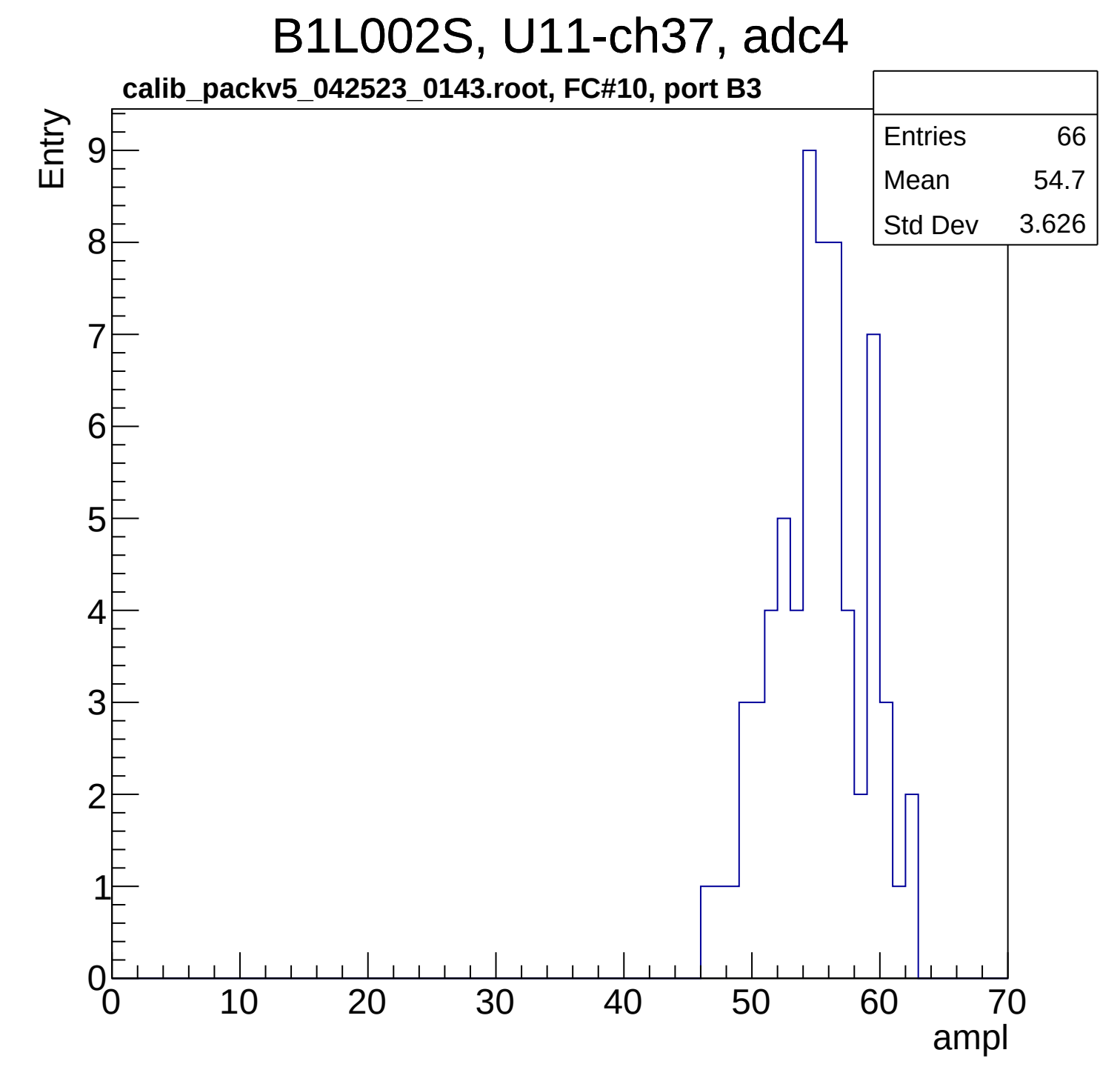
B1L002S, U11-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.7
Std Dev 3.626
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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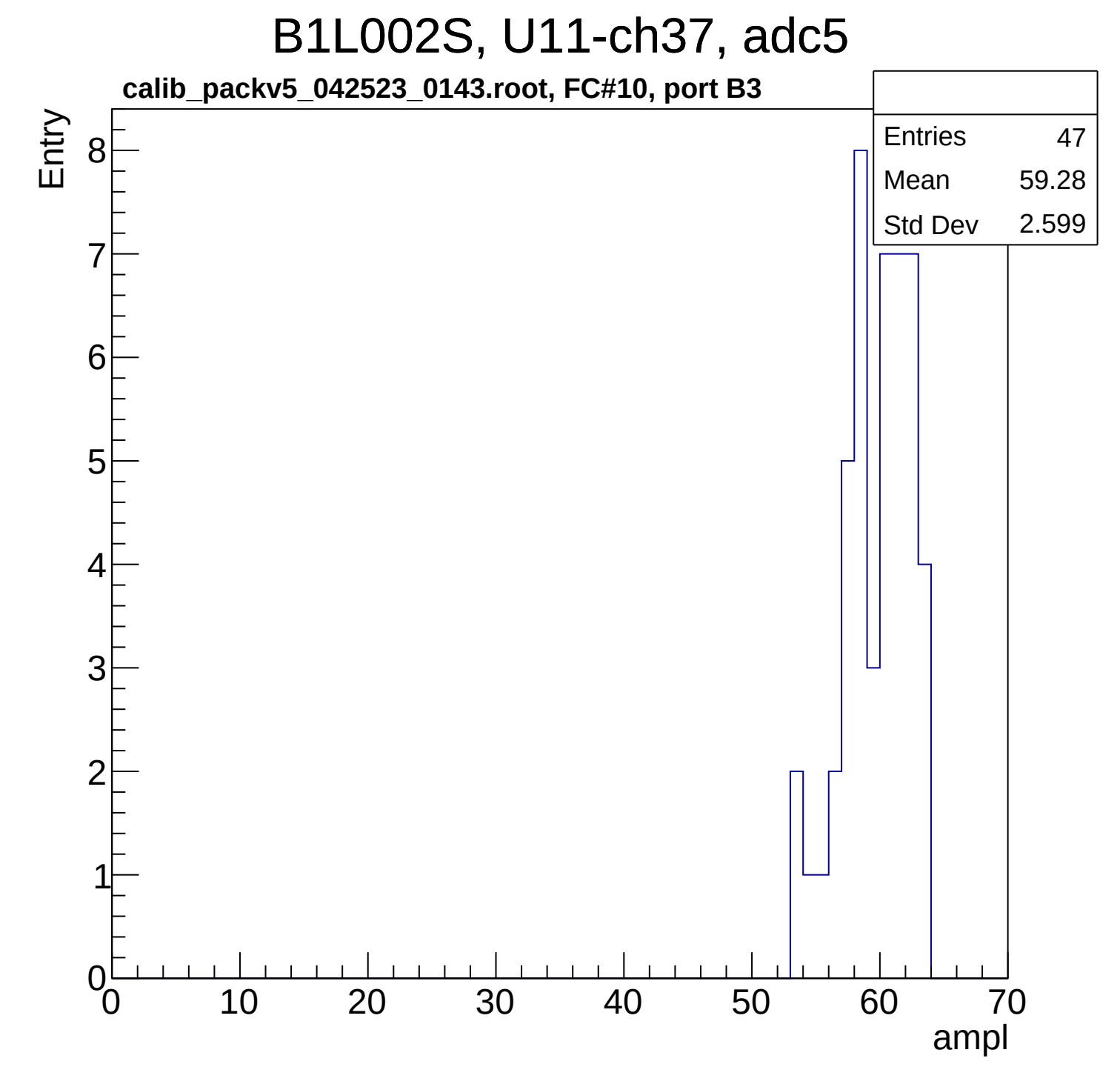
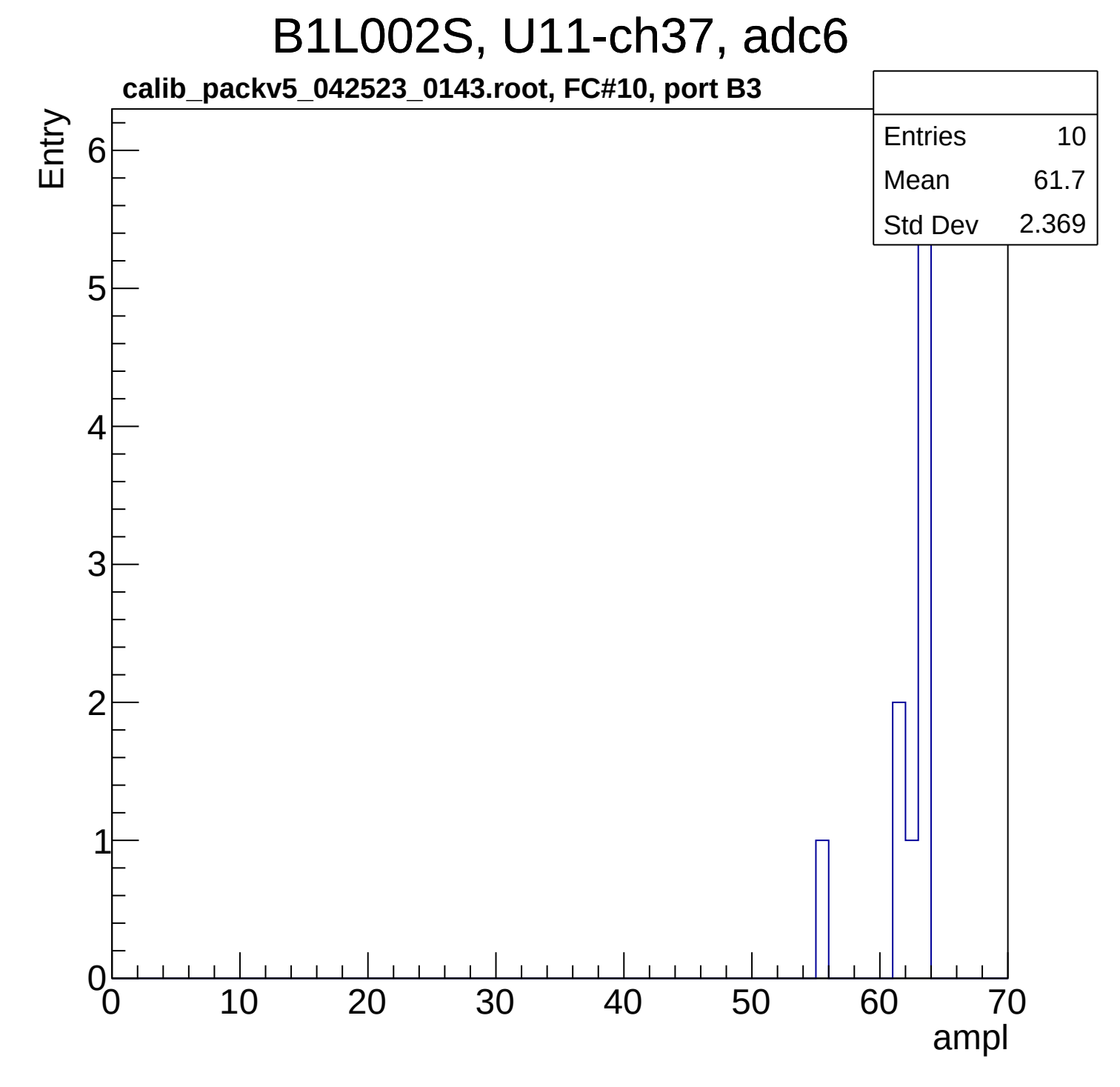
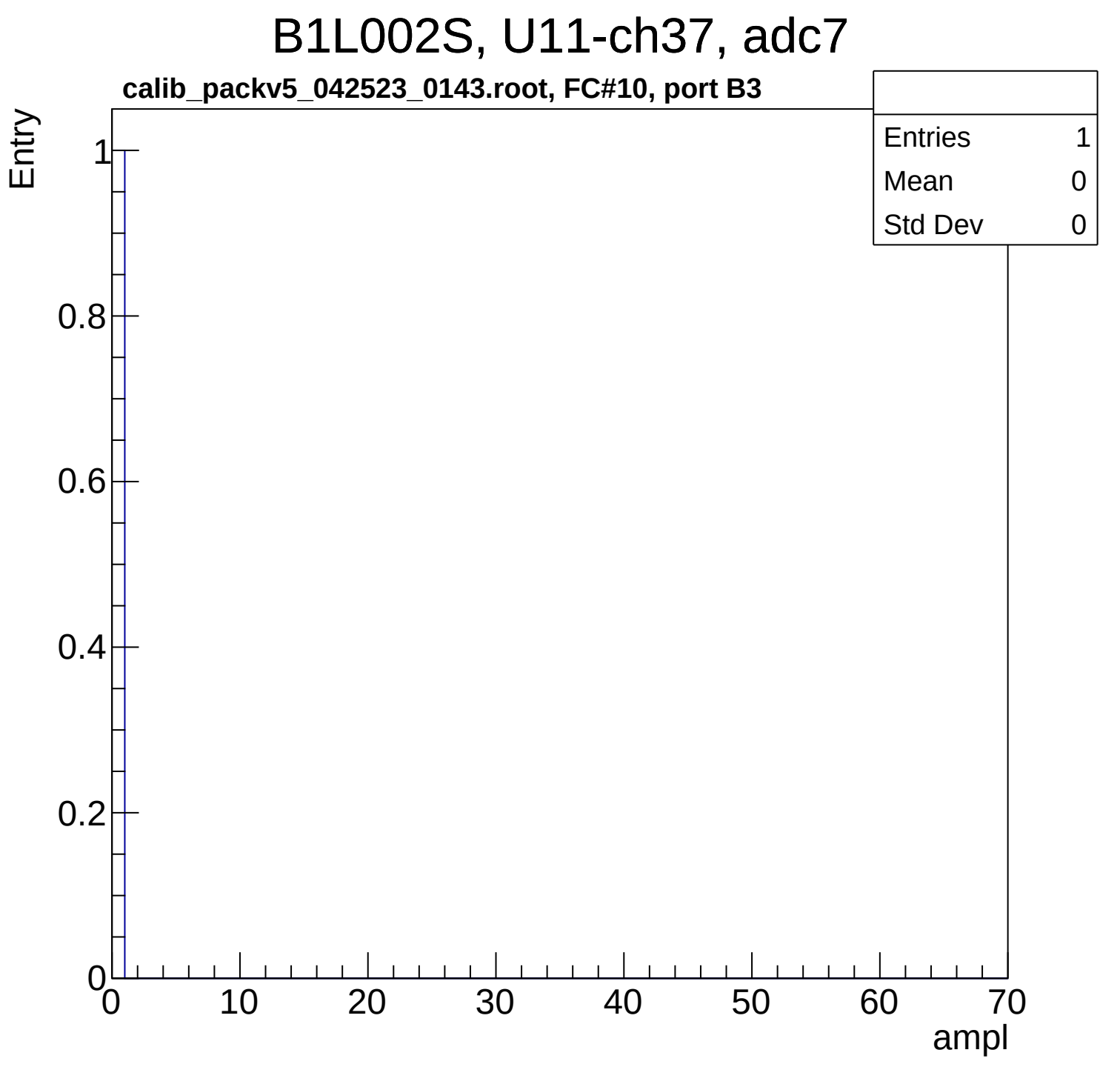

B1L002S, U11-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 59.28
Std Dev 2.599
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.7
Std Dev 2.369
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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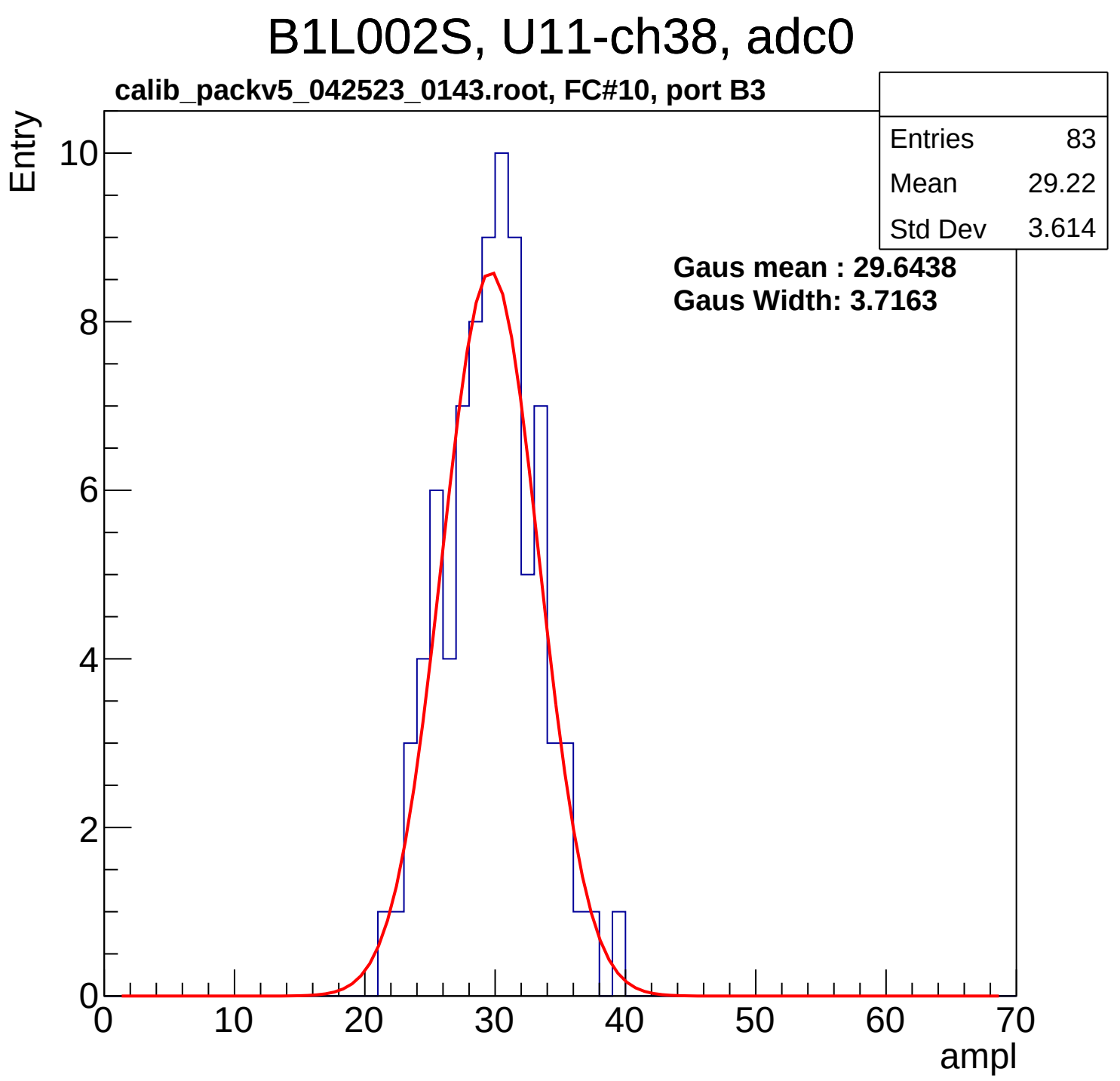
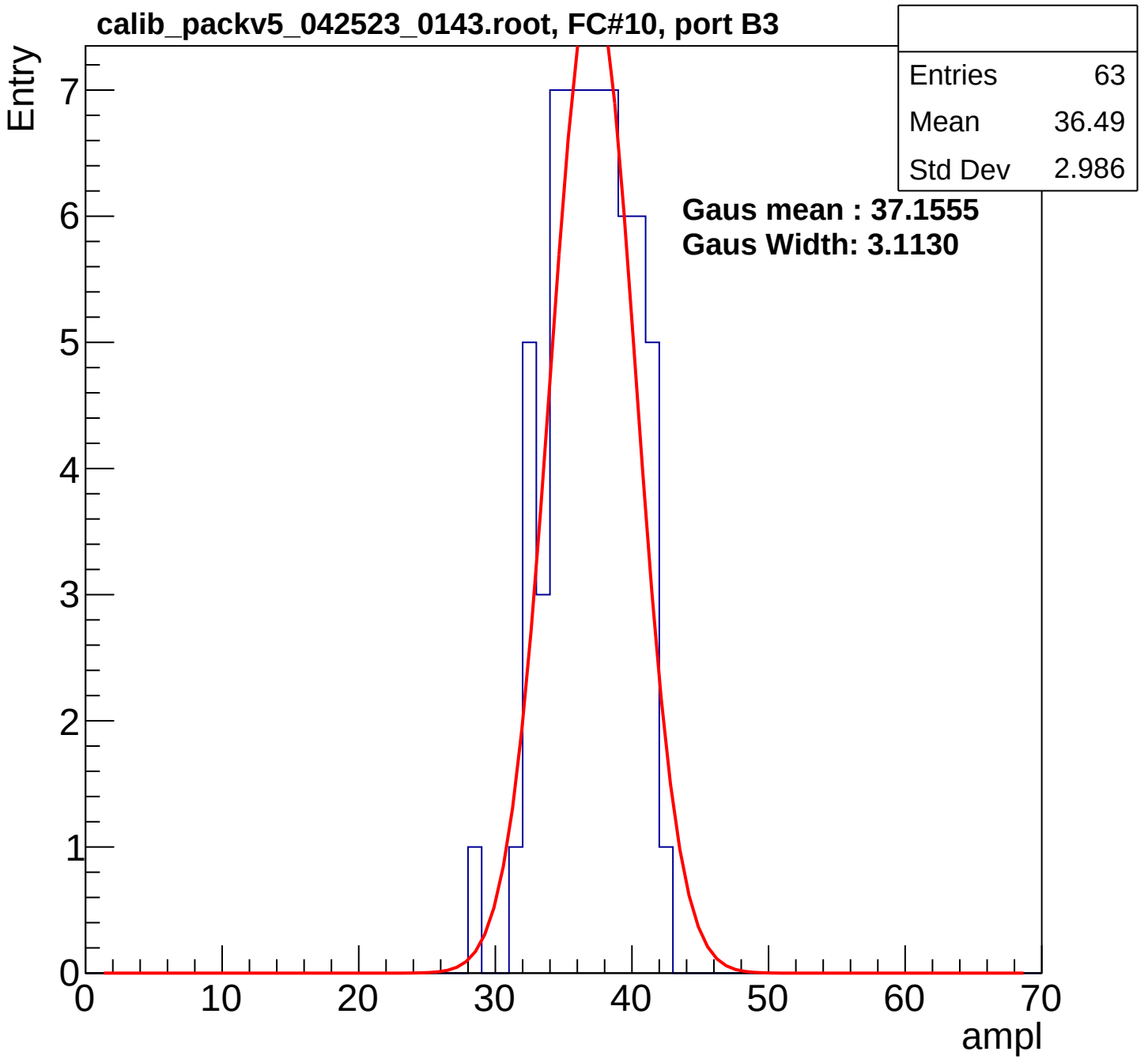
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 29.22
Std Dev 3.614
Gaus mean : 29.6438
Gaus Width: 3.7163
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.49
Std Dev 2.986
Gaus mean : 37.1555
Gaus Width: 3.1130
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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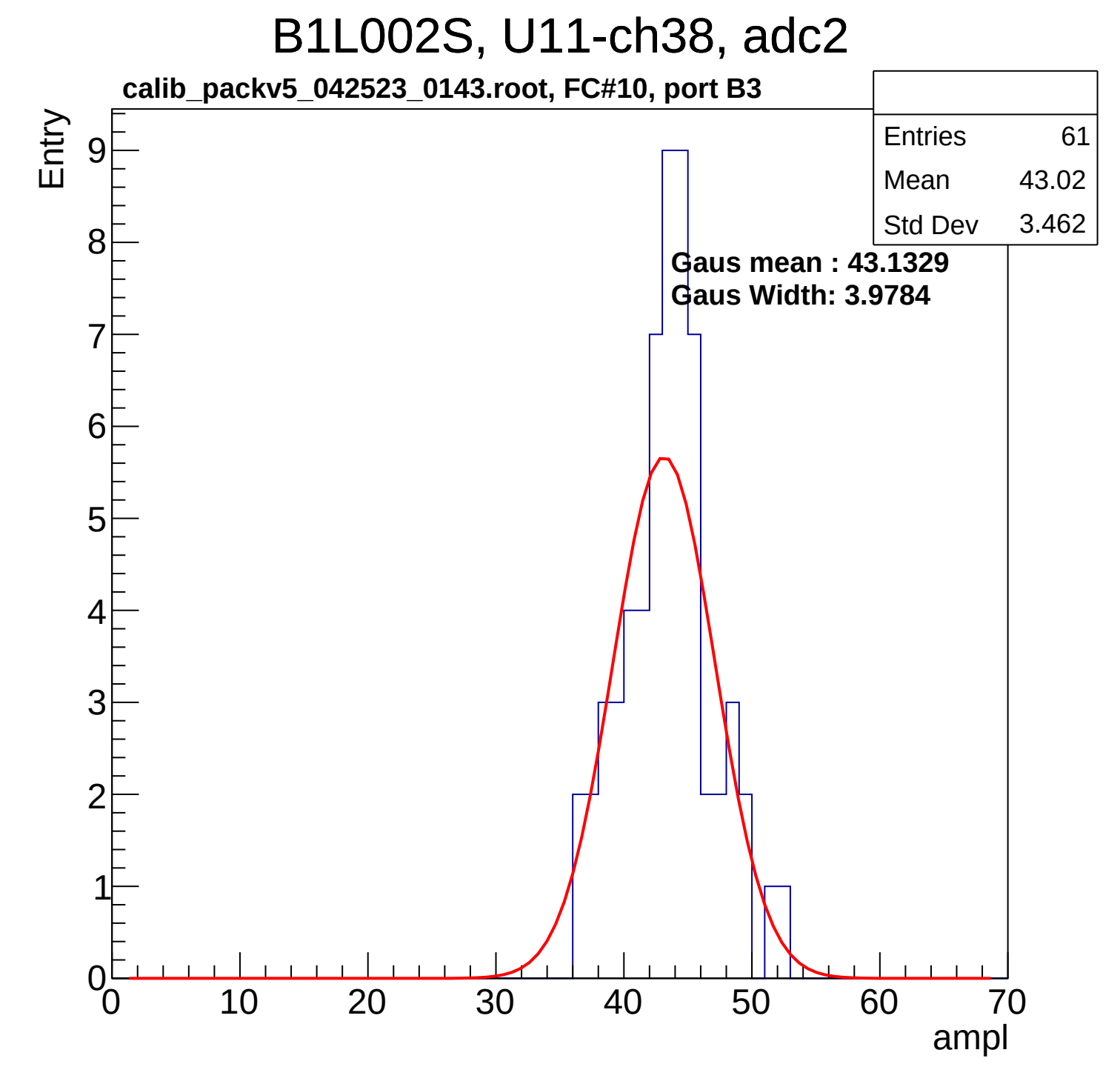
B1L002S, U11-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 43.02
Std Dev 3.462
Gaus mean : 43.1329
Gaus Width: 3.9784
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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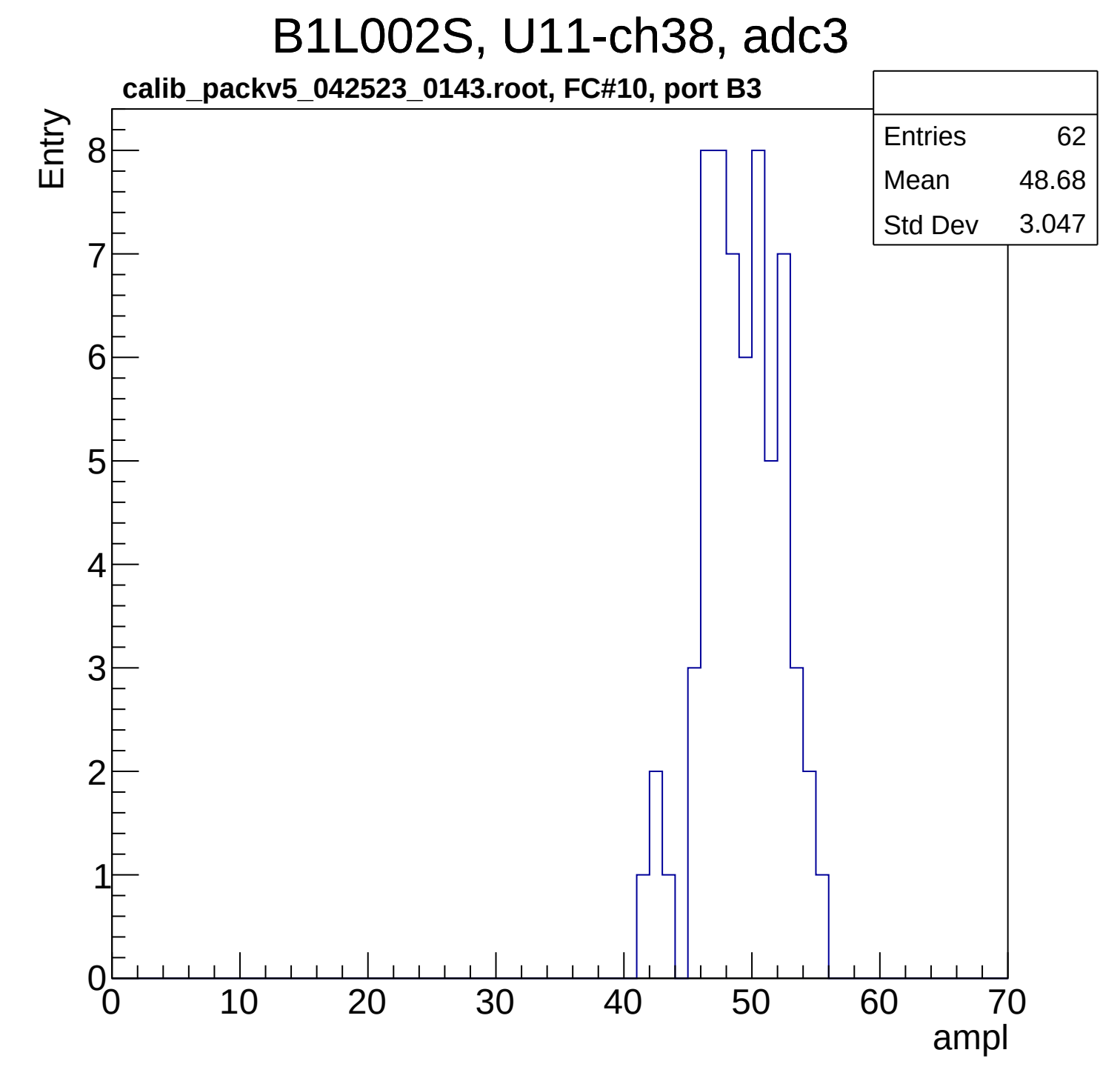
B1L002S, U11-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 48.68
Std Dev 3.047
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
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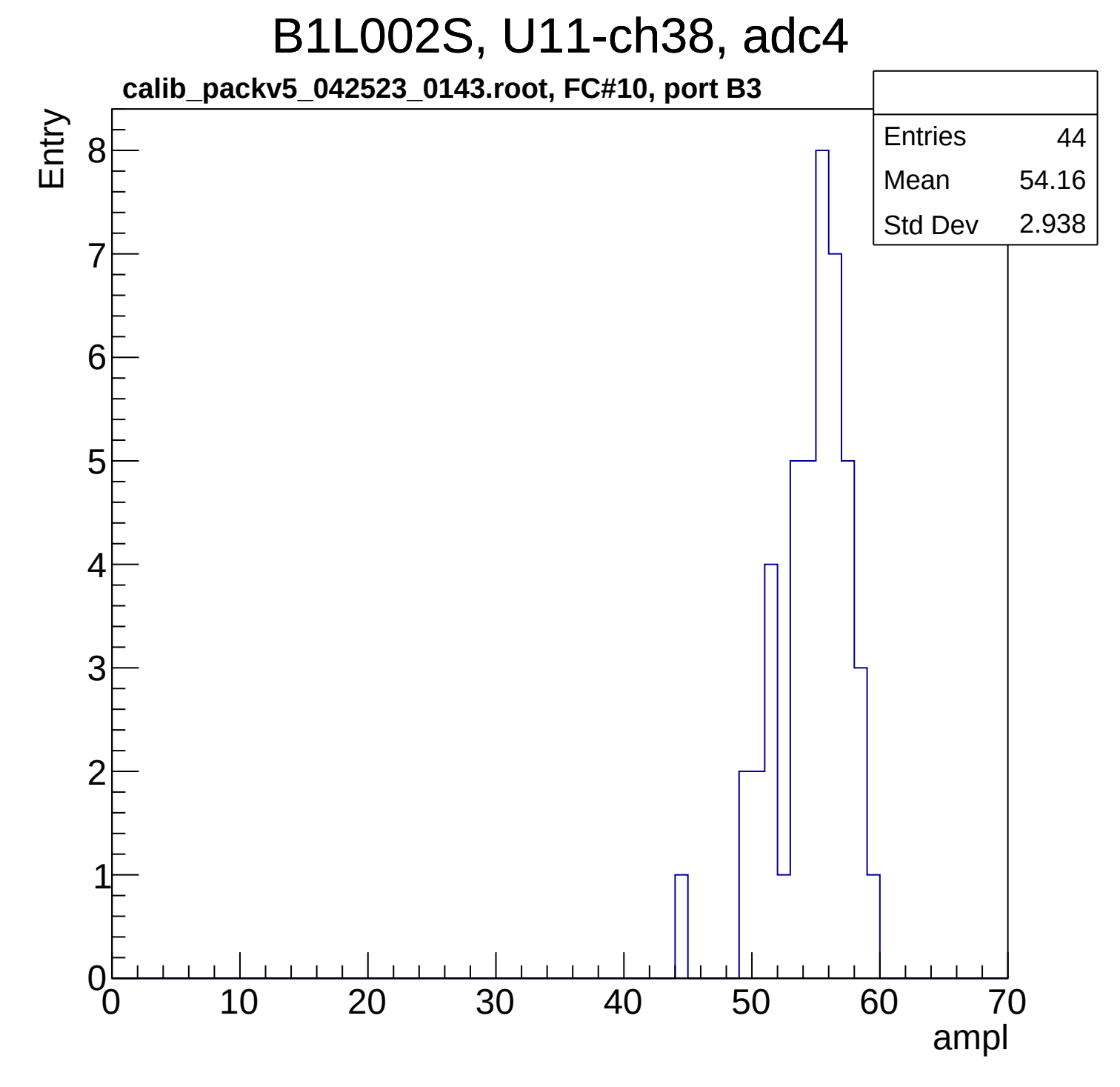
B1L002S, U11-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 54.16
Std Dev 2.938
Entry
ampl
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1
2
3
4
5
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7
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0
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40
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60
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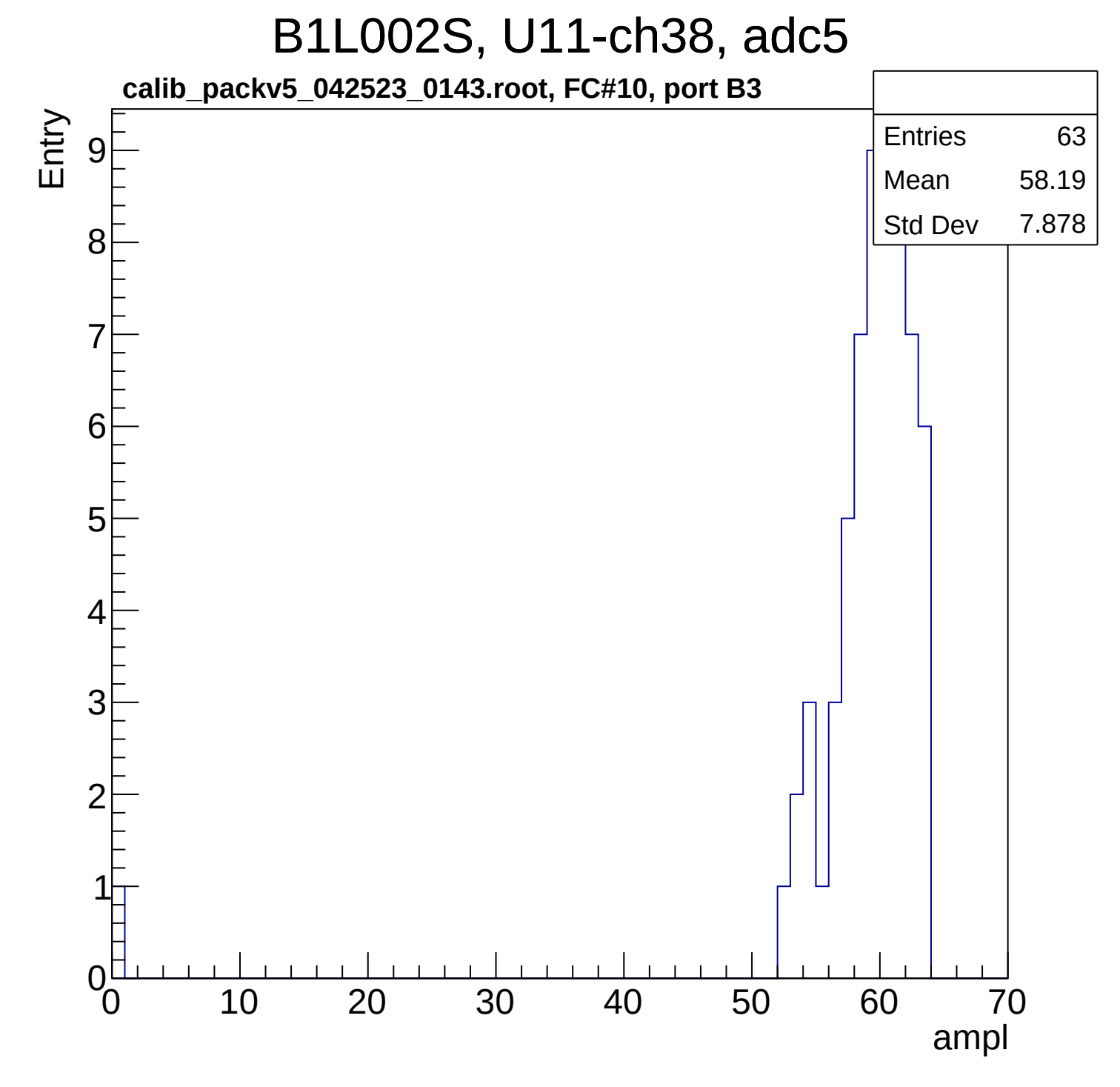
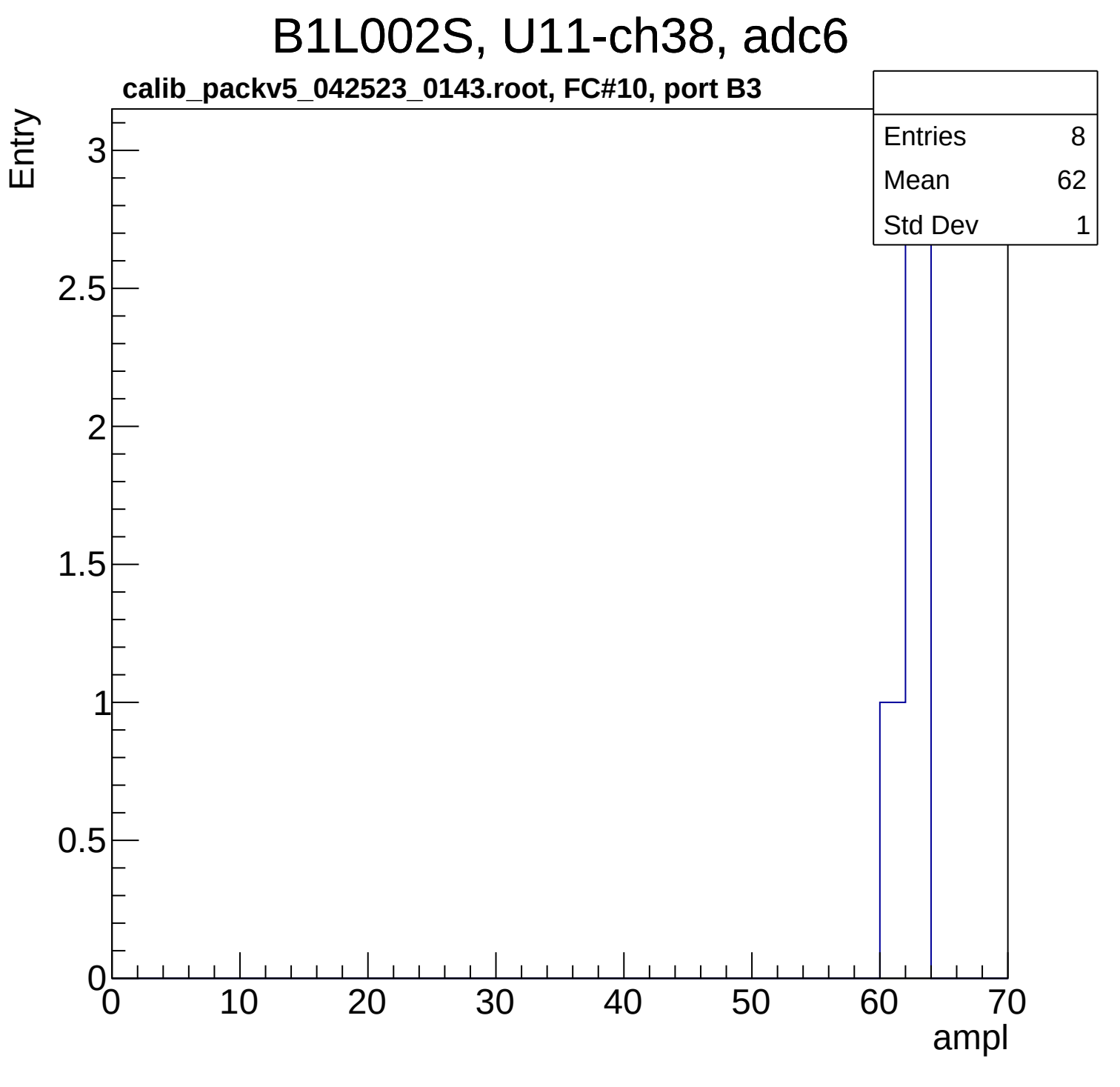

B1L002S, U11-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.19
Std Dev 7.878
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
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0
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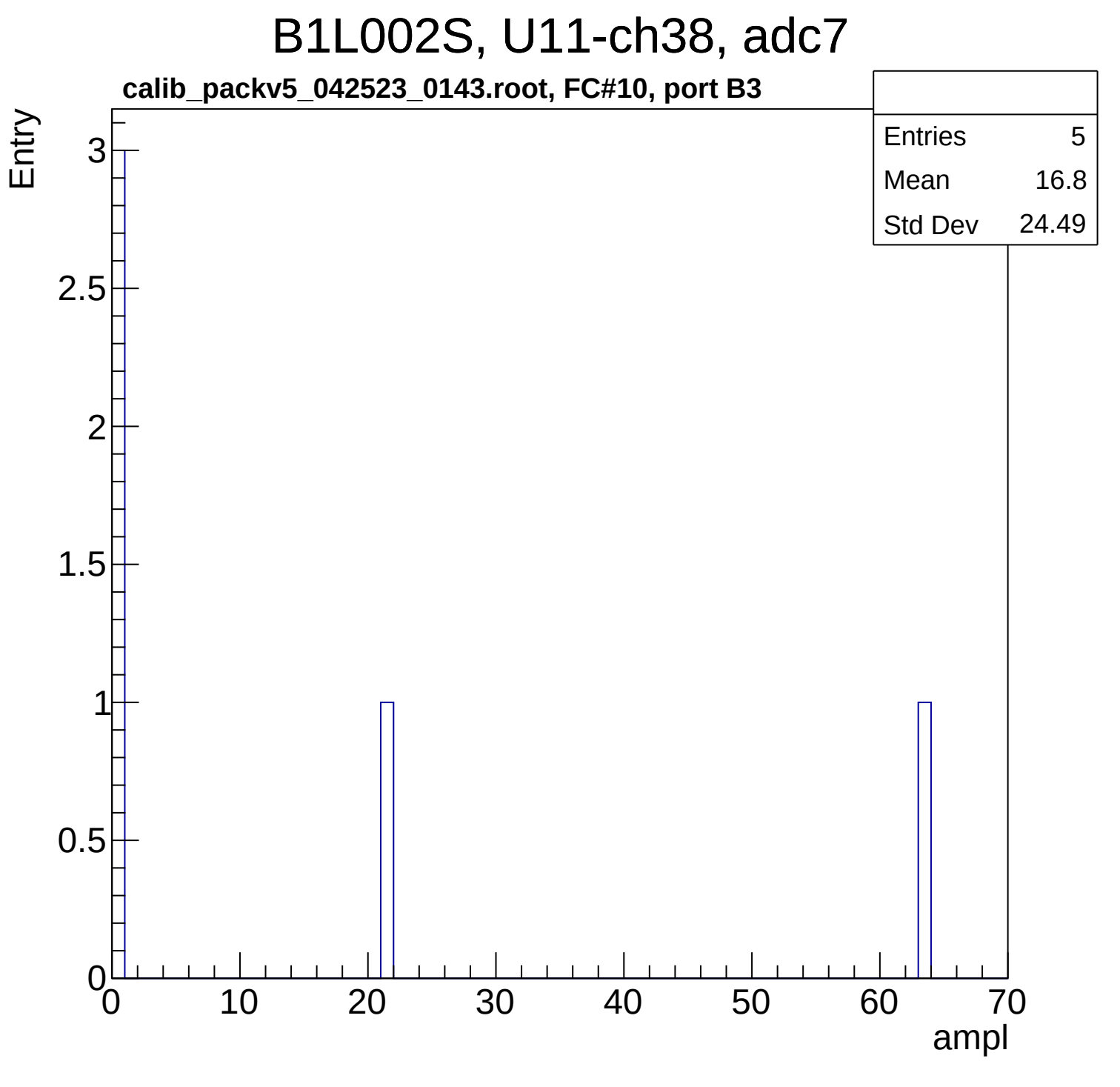
B1L002S, U11-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 16.8
Std Dev 24.49
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
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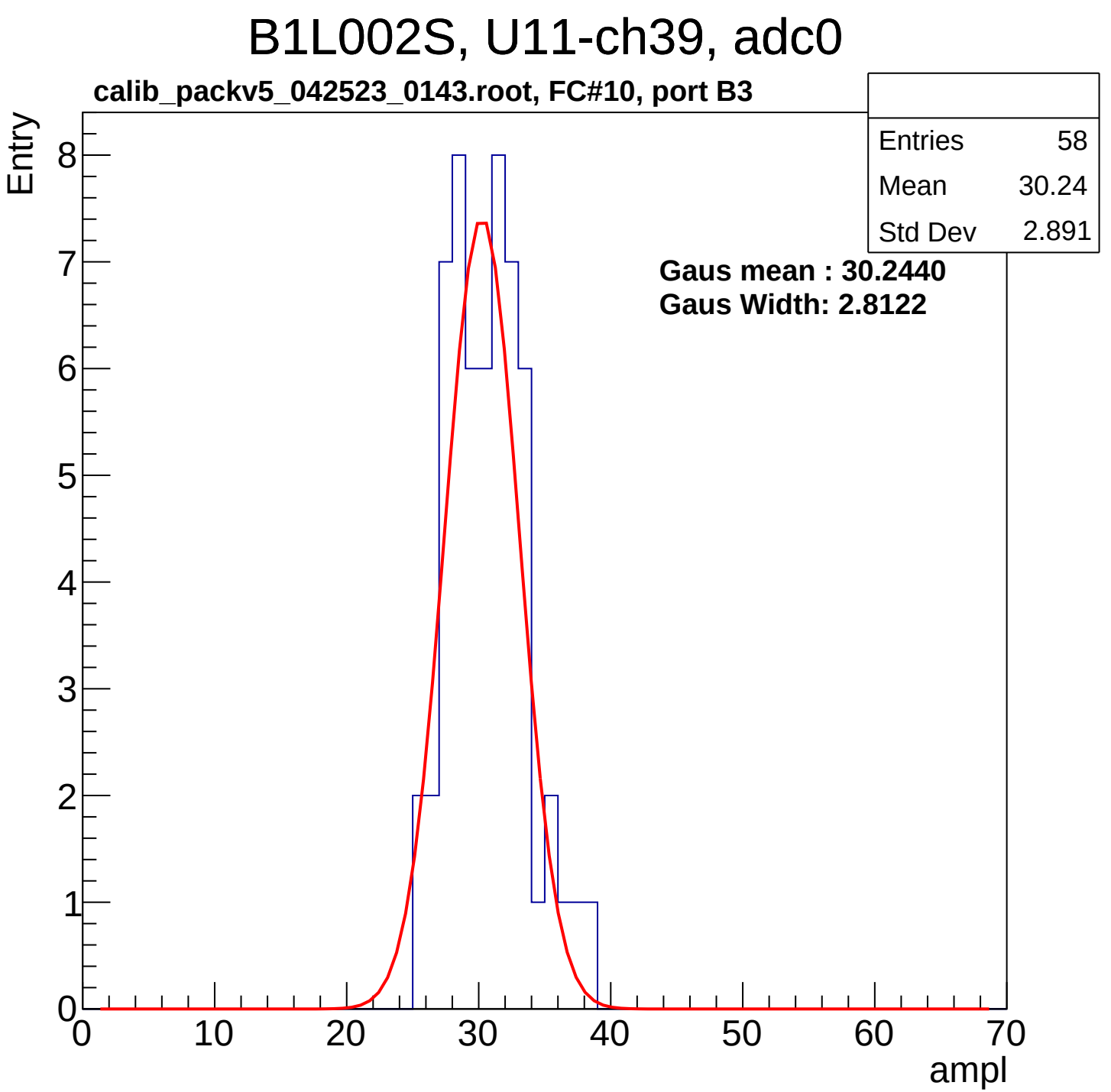
B1L002S, U11-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 30.24
Std Dev 2.891
Gaus mean : 30.2440
Gaus Width: 2.8122
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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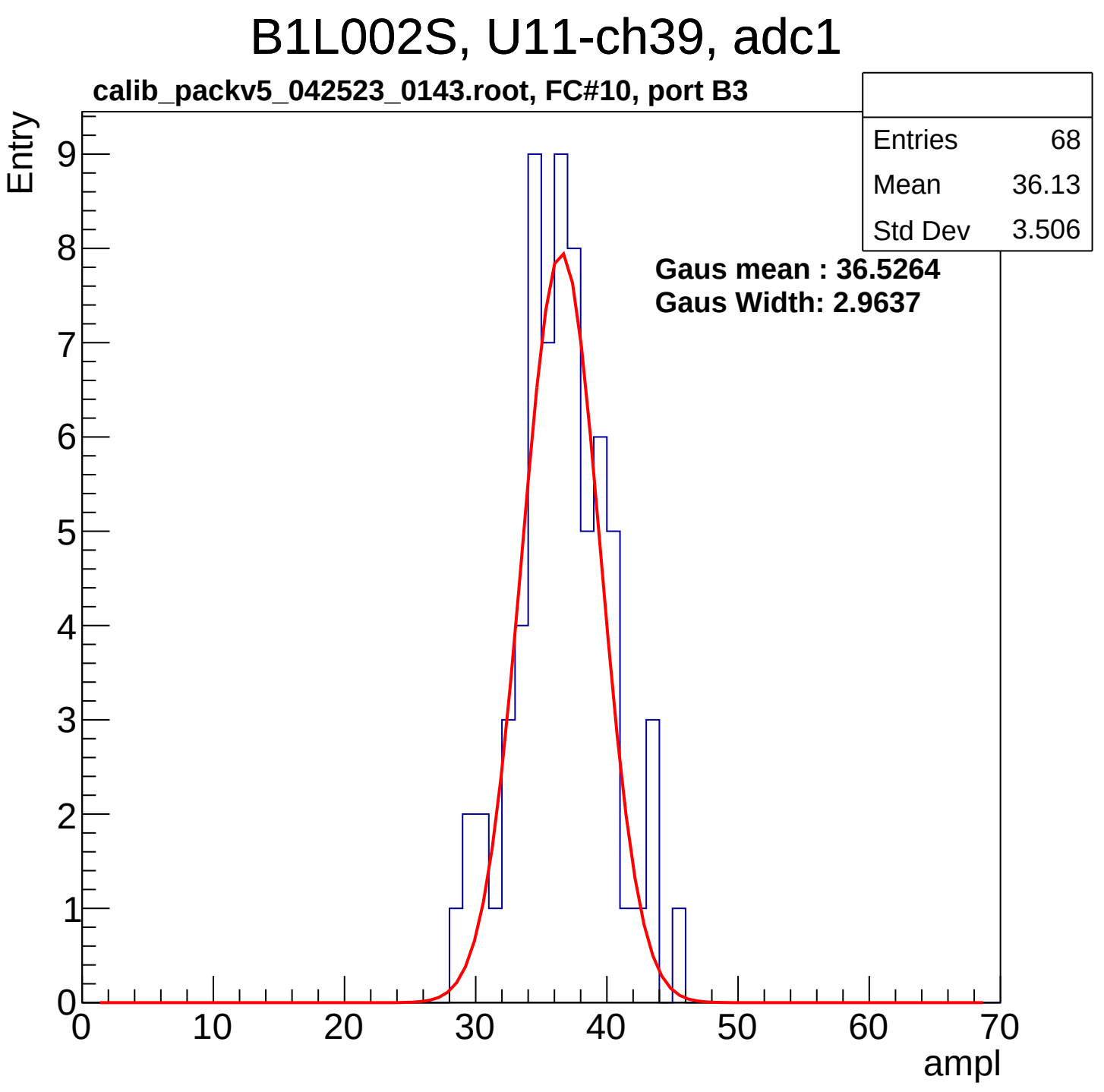
B1L002S, U11-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 36.13
Std Dev 3.506
Gaus mean : 36.5264
Gaus Width: 2.9637
Entry
ampl
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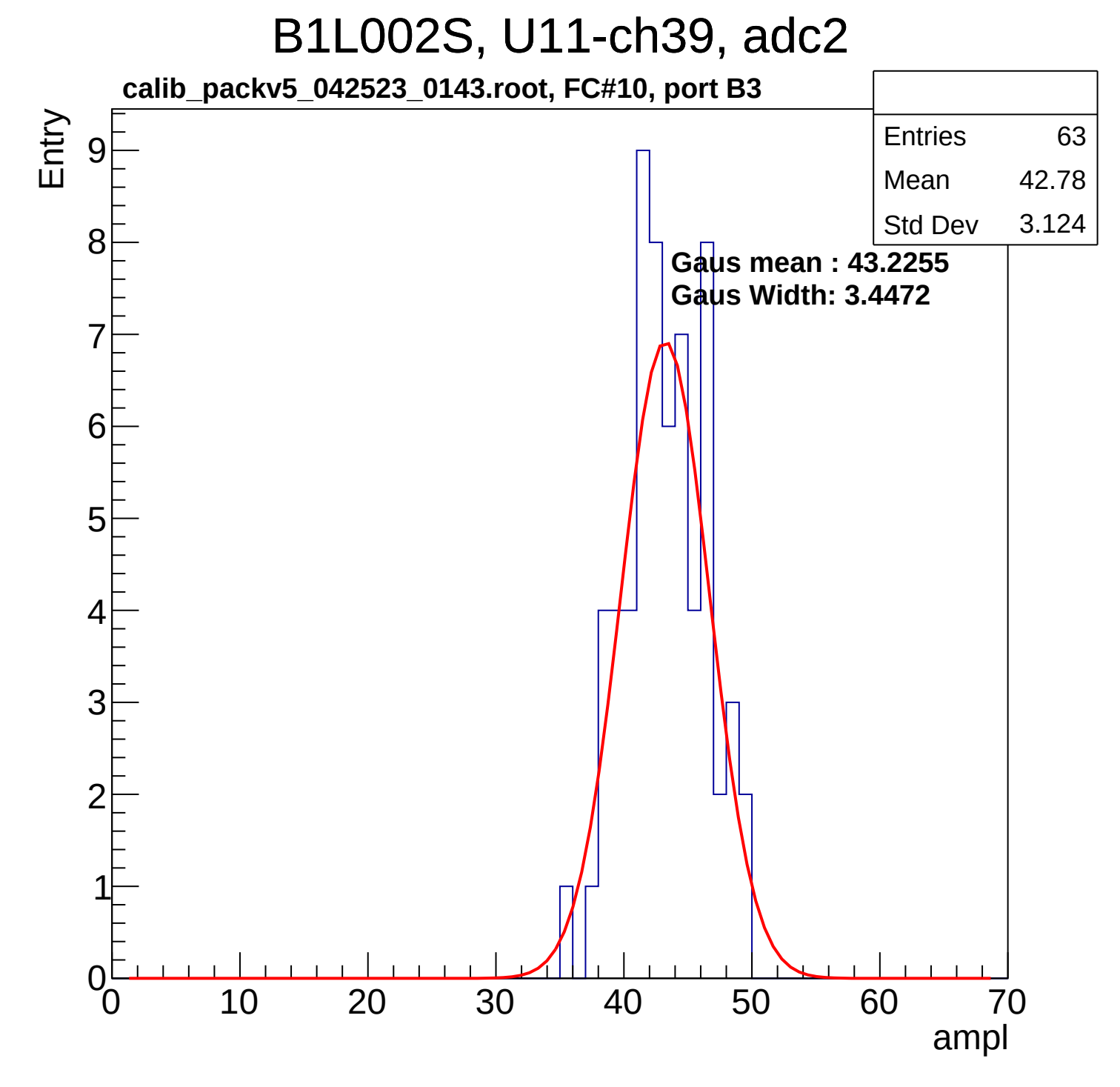
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 42.78
Std Dev 3.124
Gaus mean : 43.2255
Gaus Width: 3.4472
Entry
ampl
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2
3
4
5
6
7
8
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0
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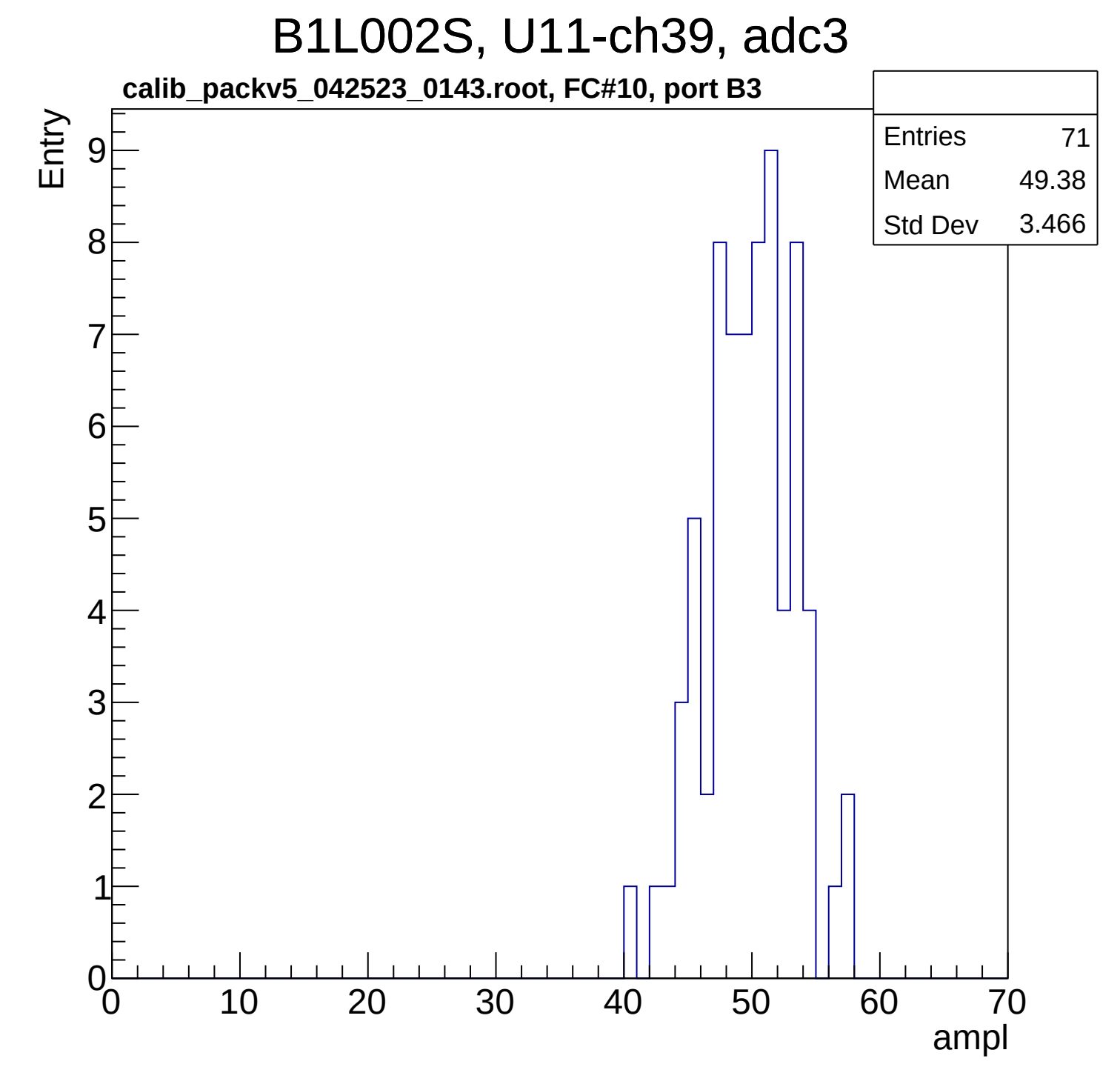
B1L002S, U11-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 49.38
Std Dev 3.466
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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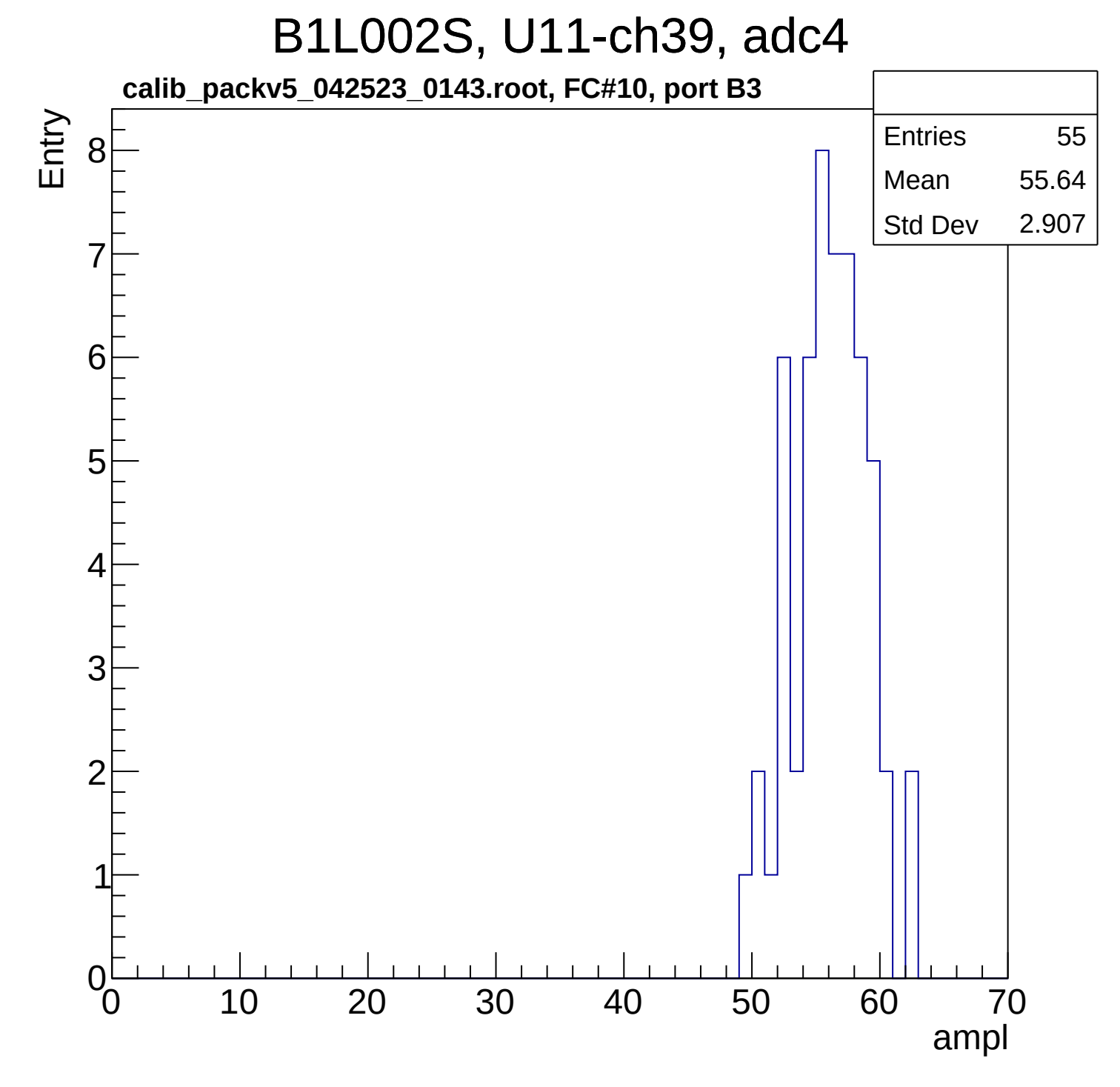
B1L002S, U11-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 55.64
Std Dev 2.907
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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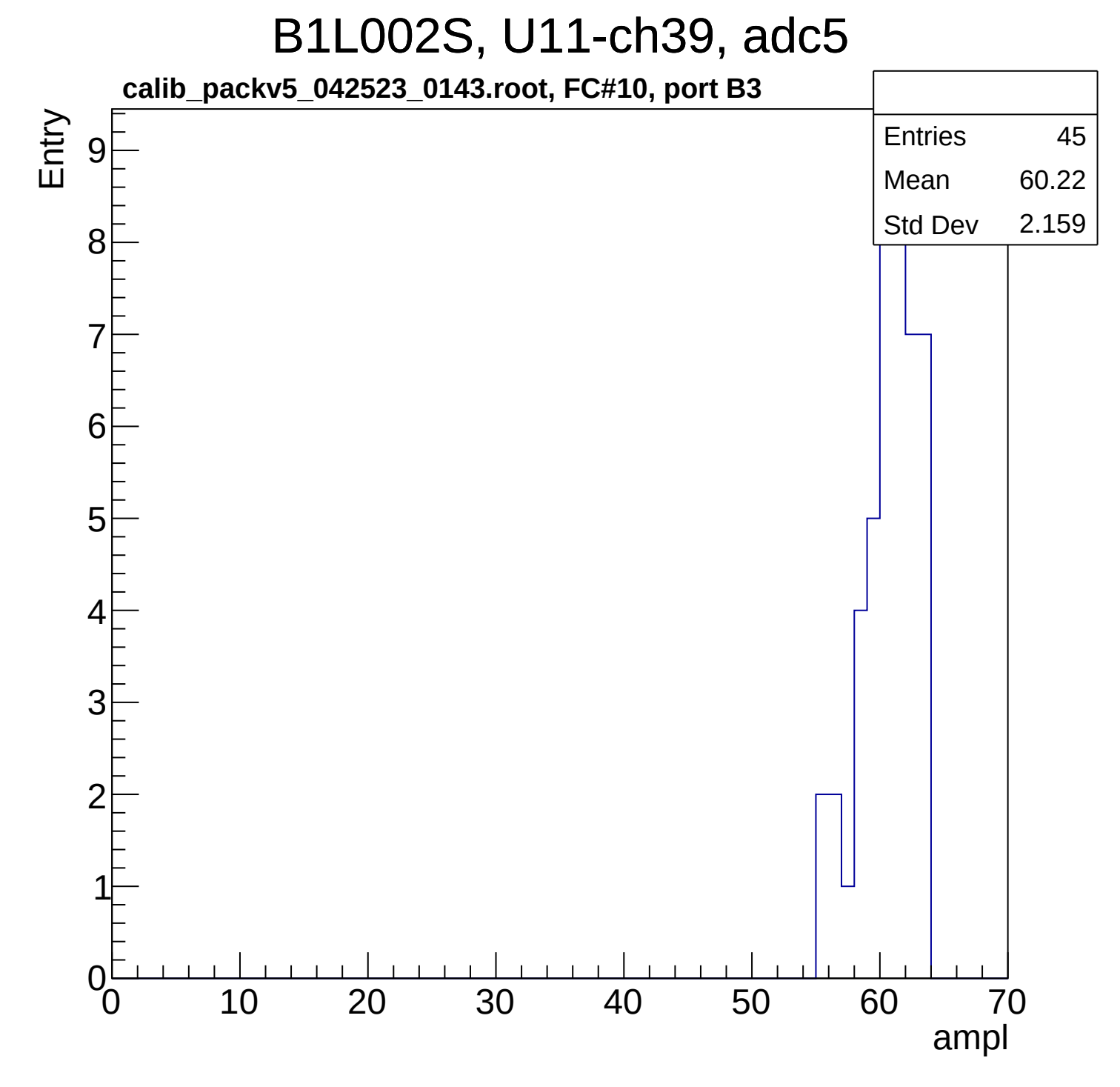
B1L002S, U11-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 60.22
Std Dev 2.159
Entry
ampl
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3
4
5
6
7
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0
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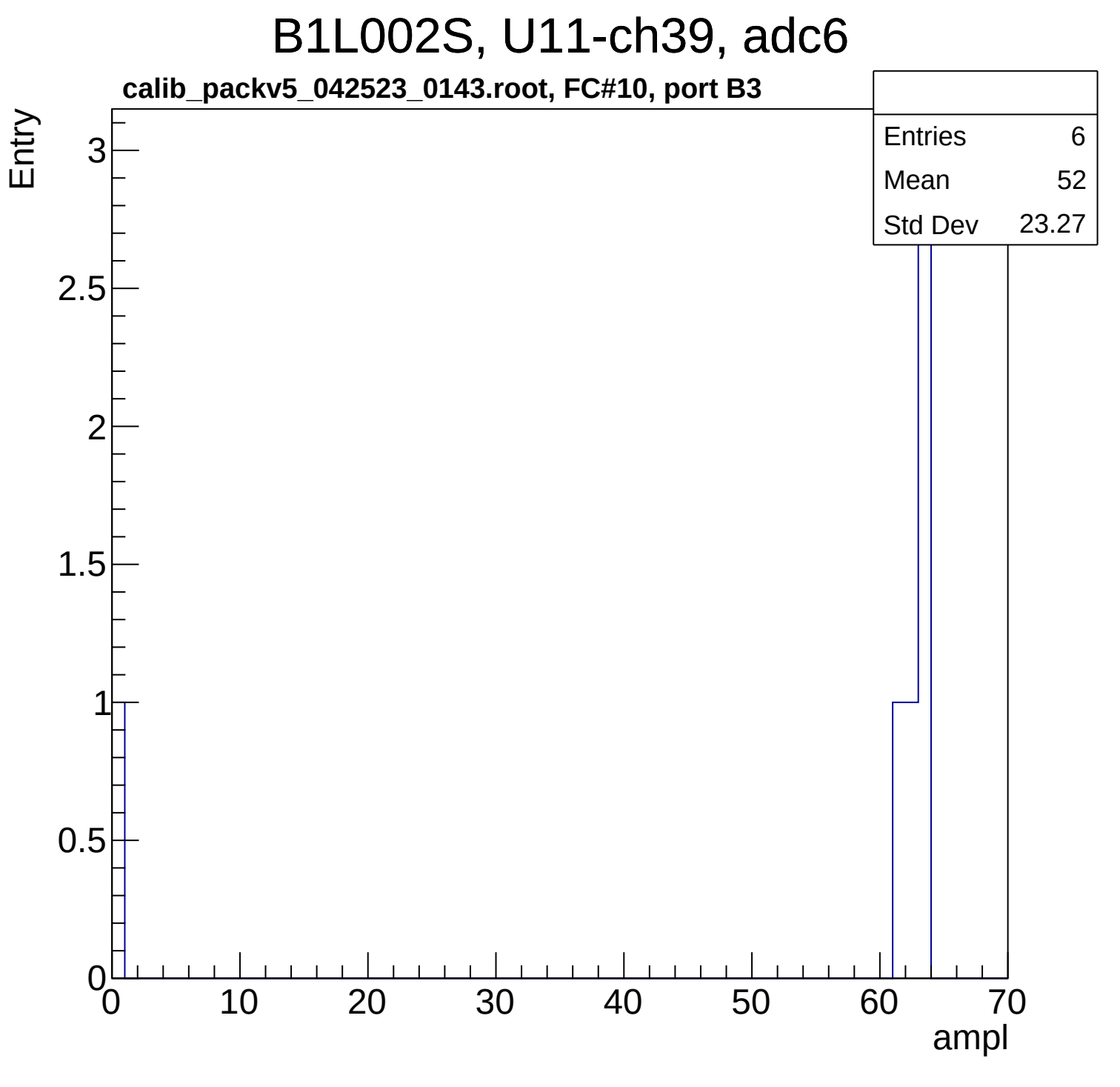
B1L002S, U11-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
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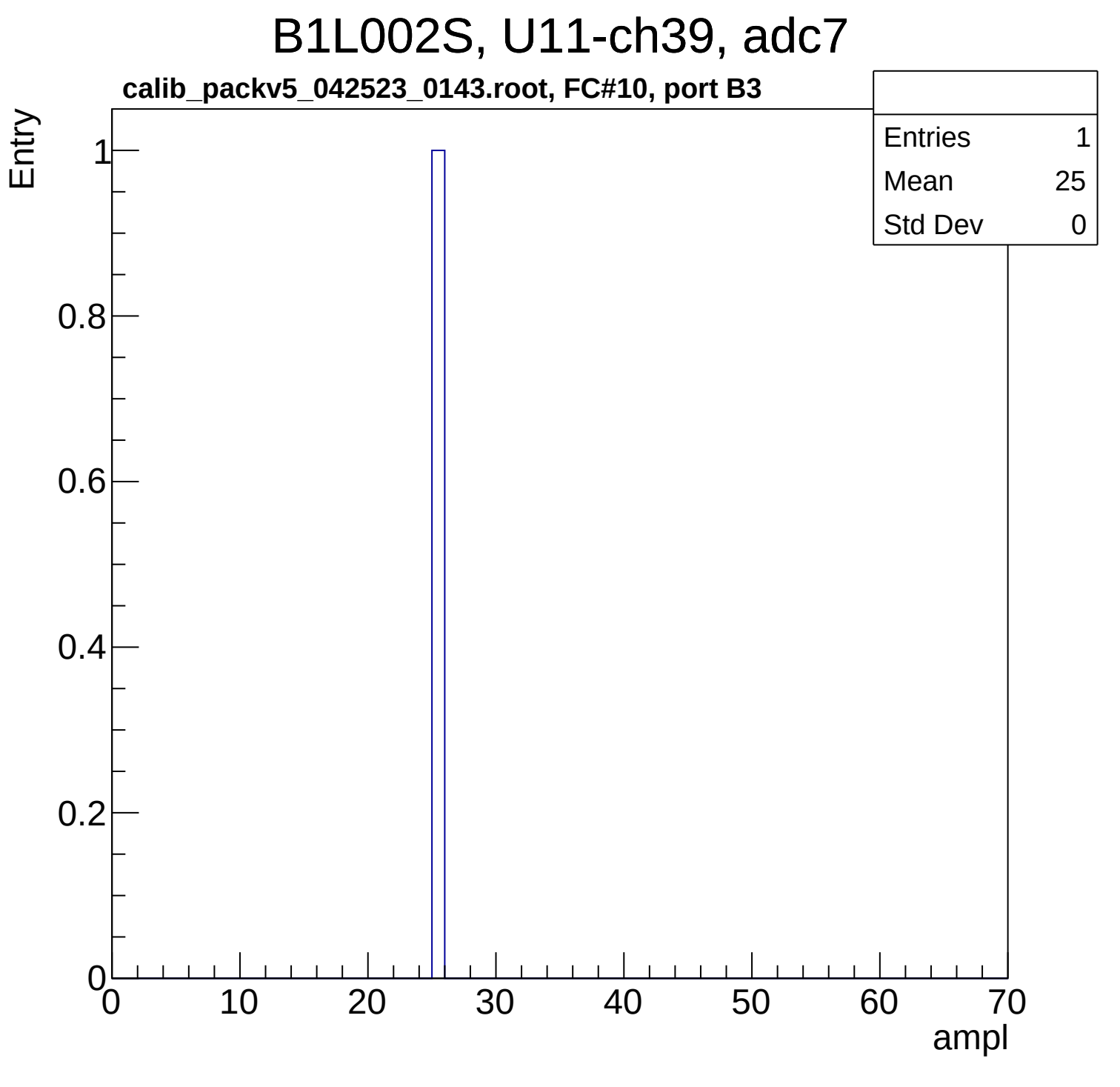
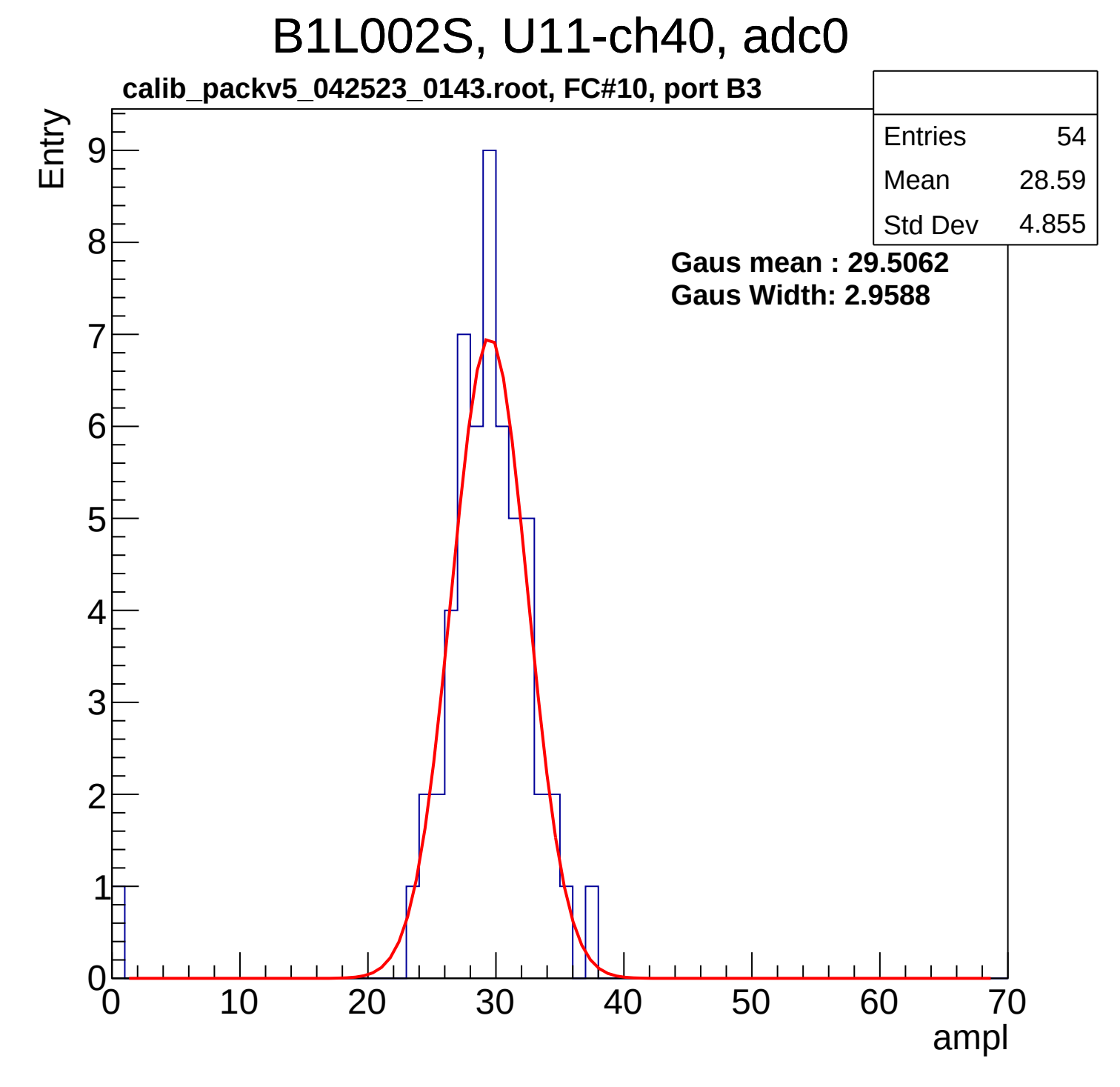
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 28.59
Std Dev 4.855
Gaus mean : 29.5062
Gaus Width: 2.9588
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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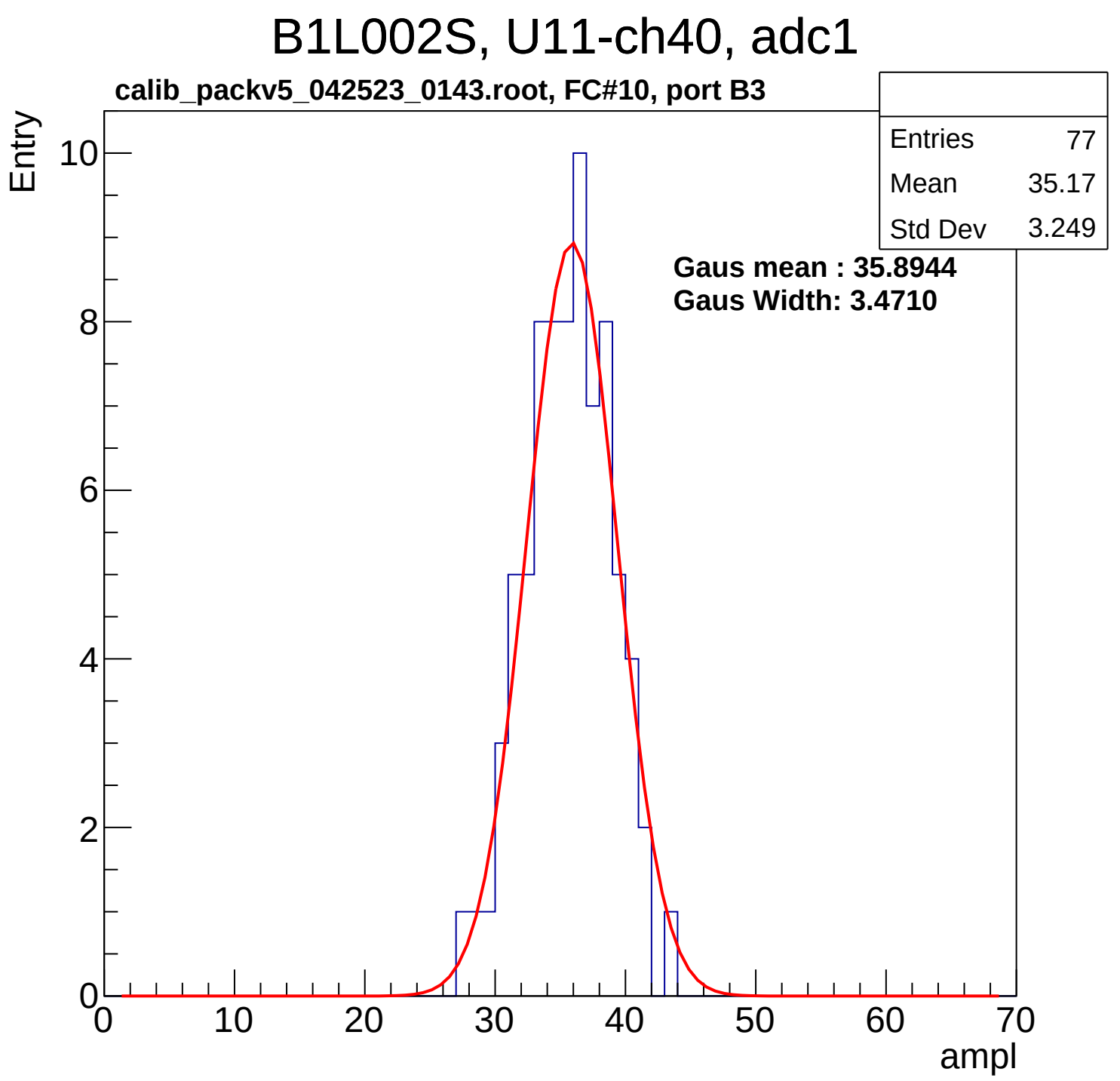
B1L002S, U11-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.17
Std Dev 3.249
Gaus mean : 35.8944
Gaus Width: 3.4710
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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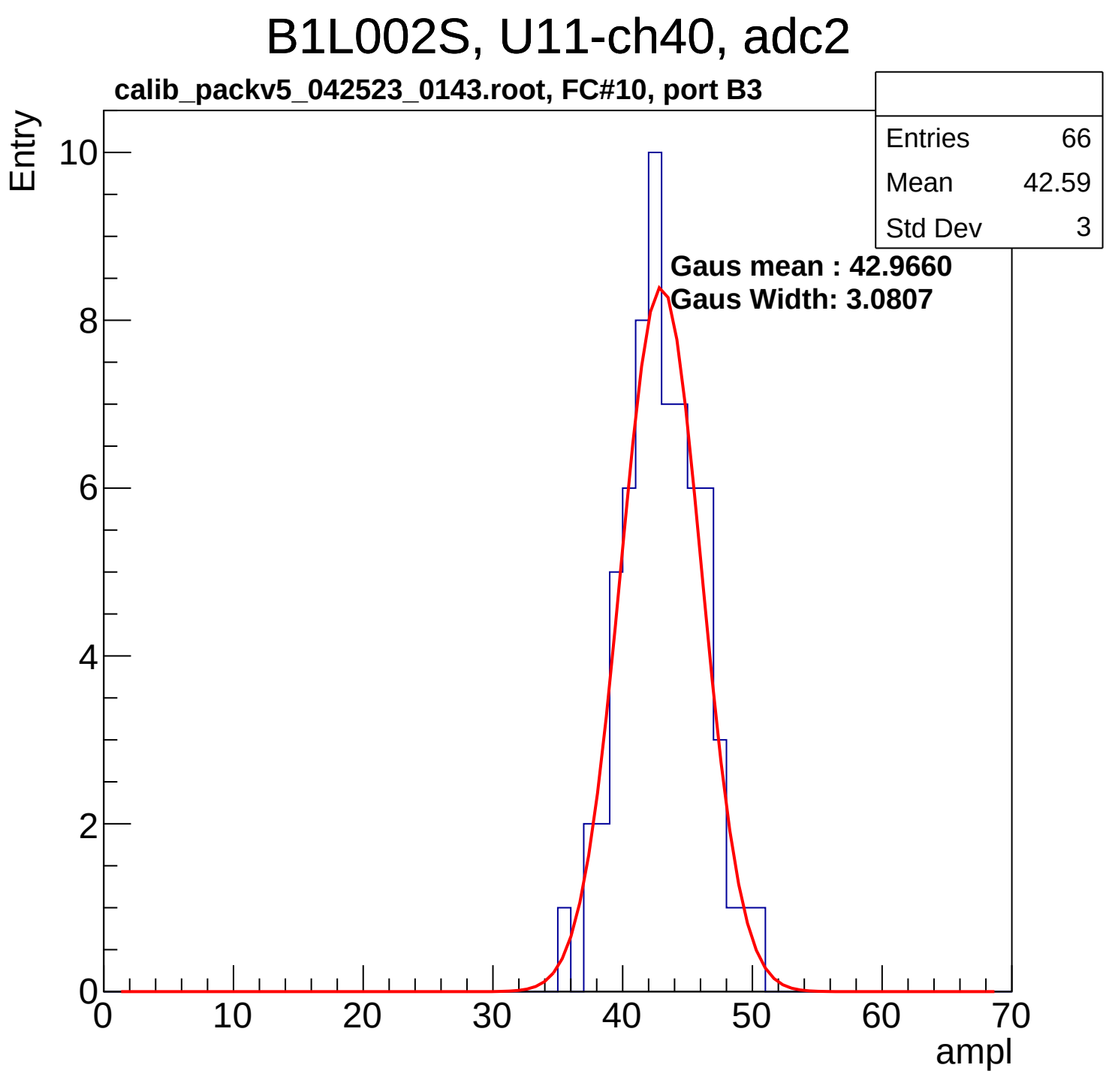
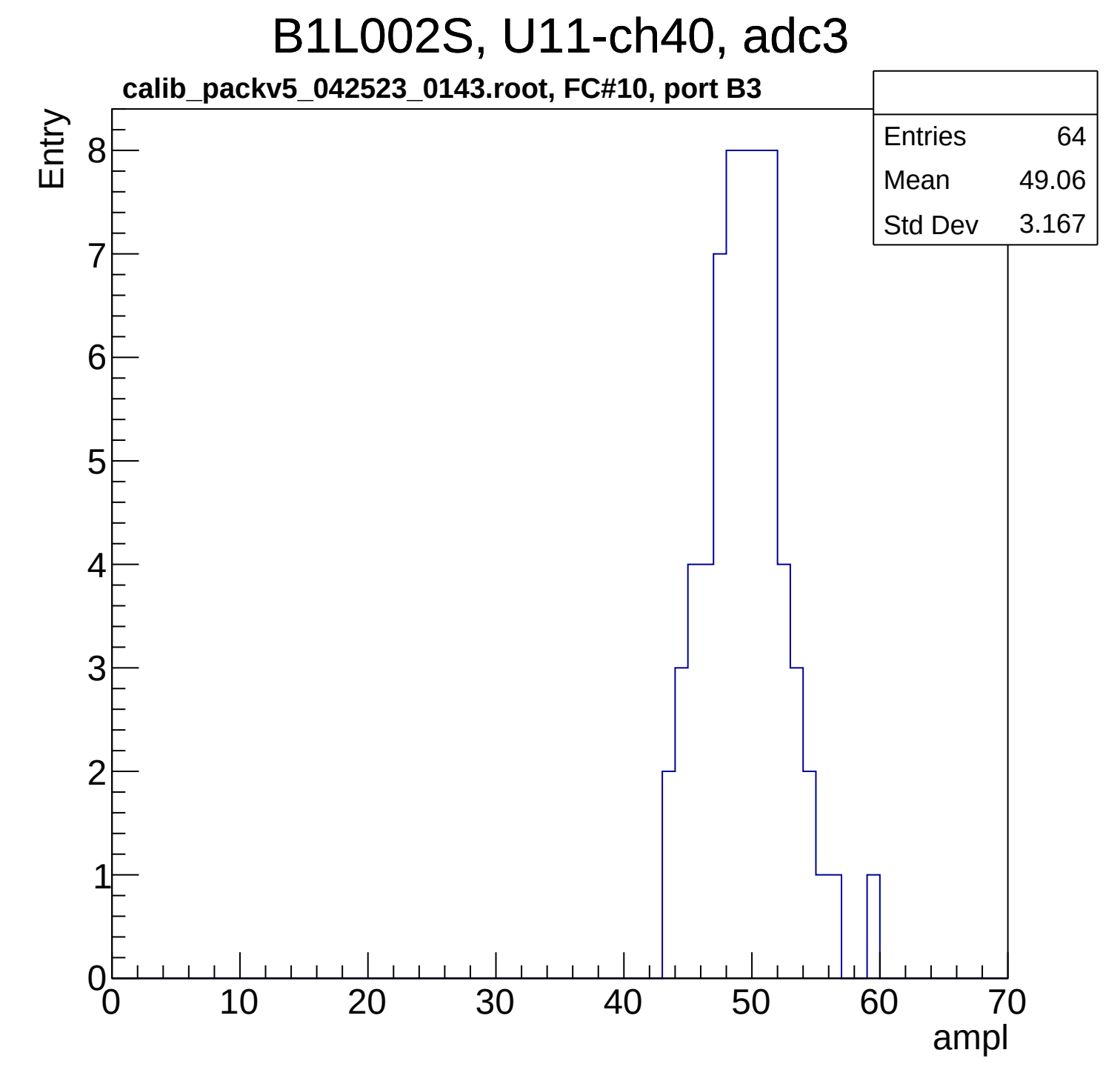

B1L002S, U11-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 42.59
Std Dev 3
Gaus mean : 42.9660
Gaus Width: 3.0807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 49.06
Std Dev 3.167
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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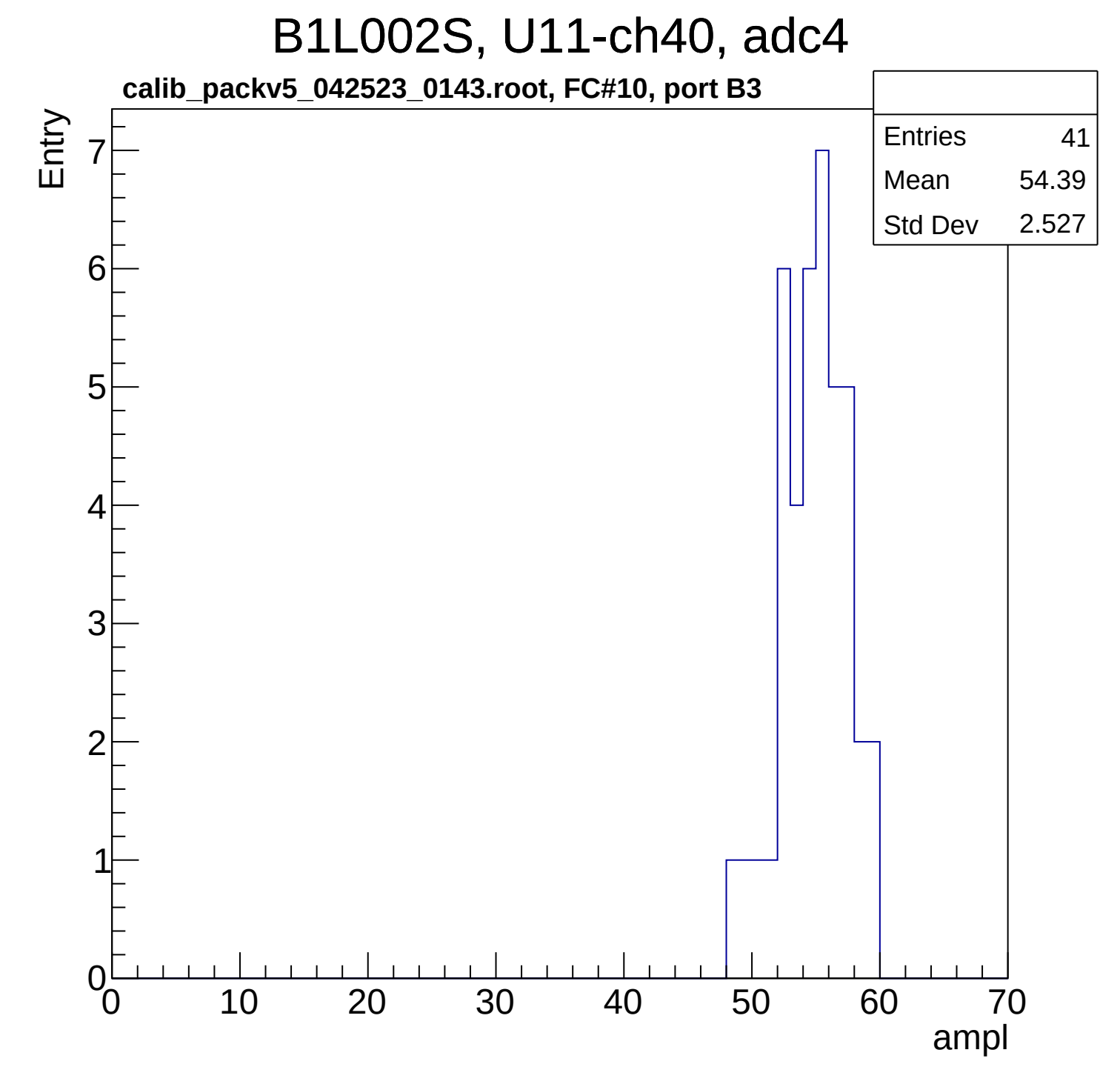
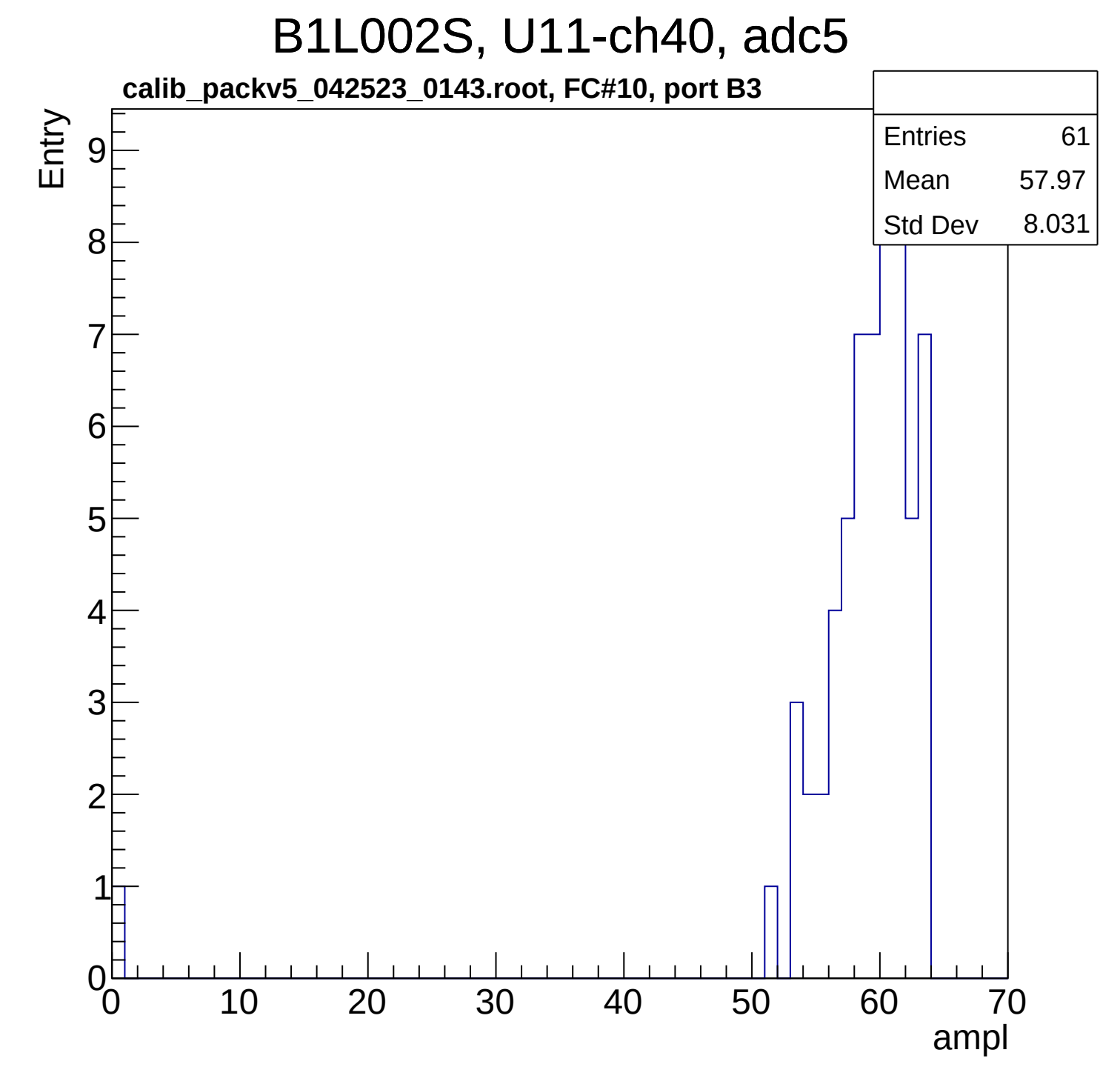
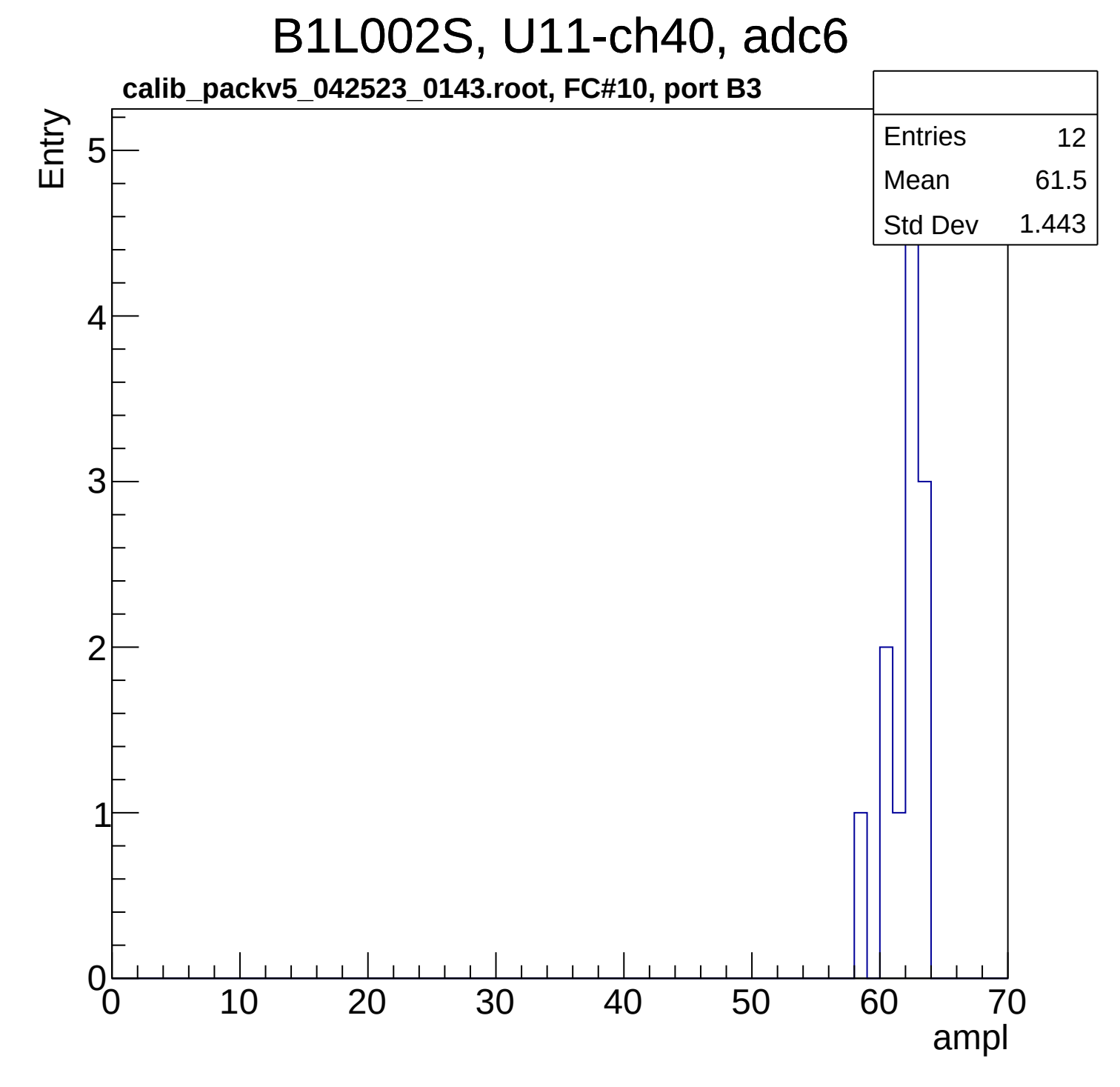
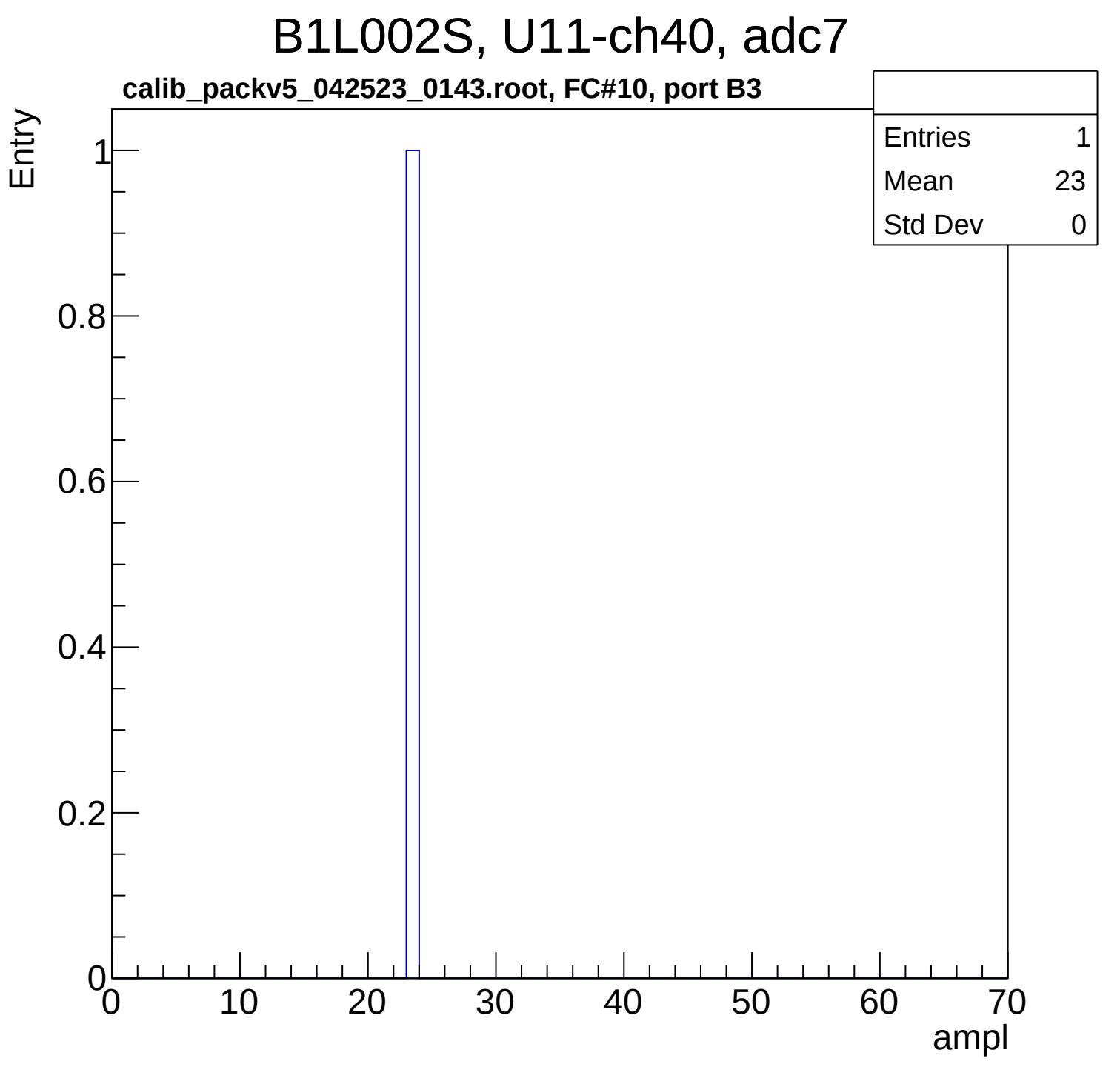

B1L002S, U11-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 54.39
Std Dev 2.527
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.97
Std Dev 8.031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.5
Std Dev 1.443
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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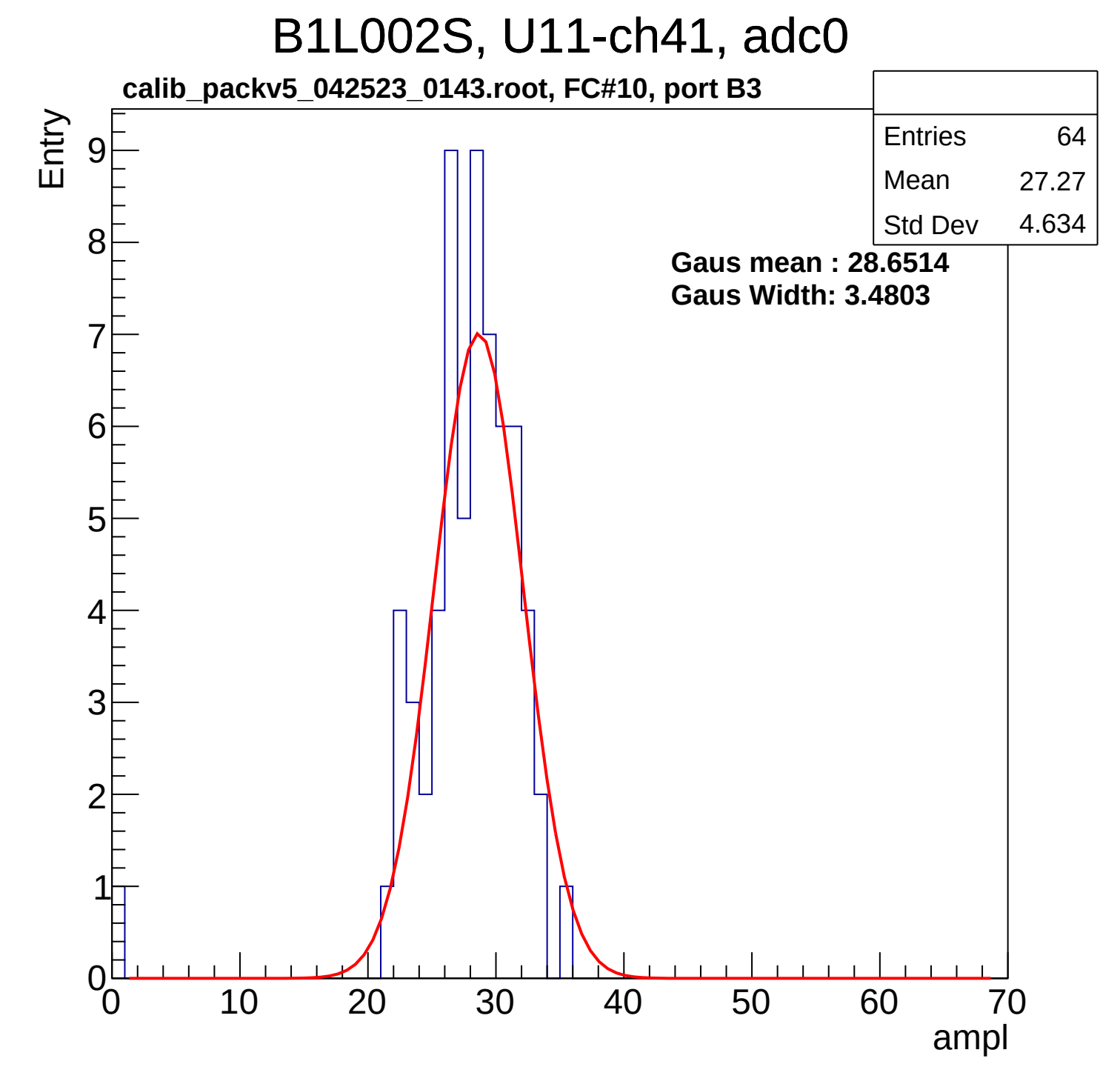
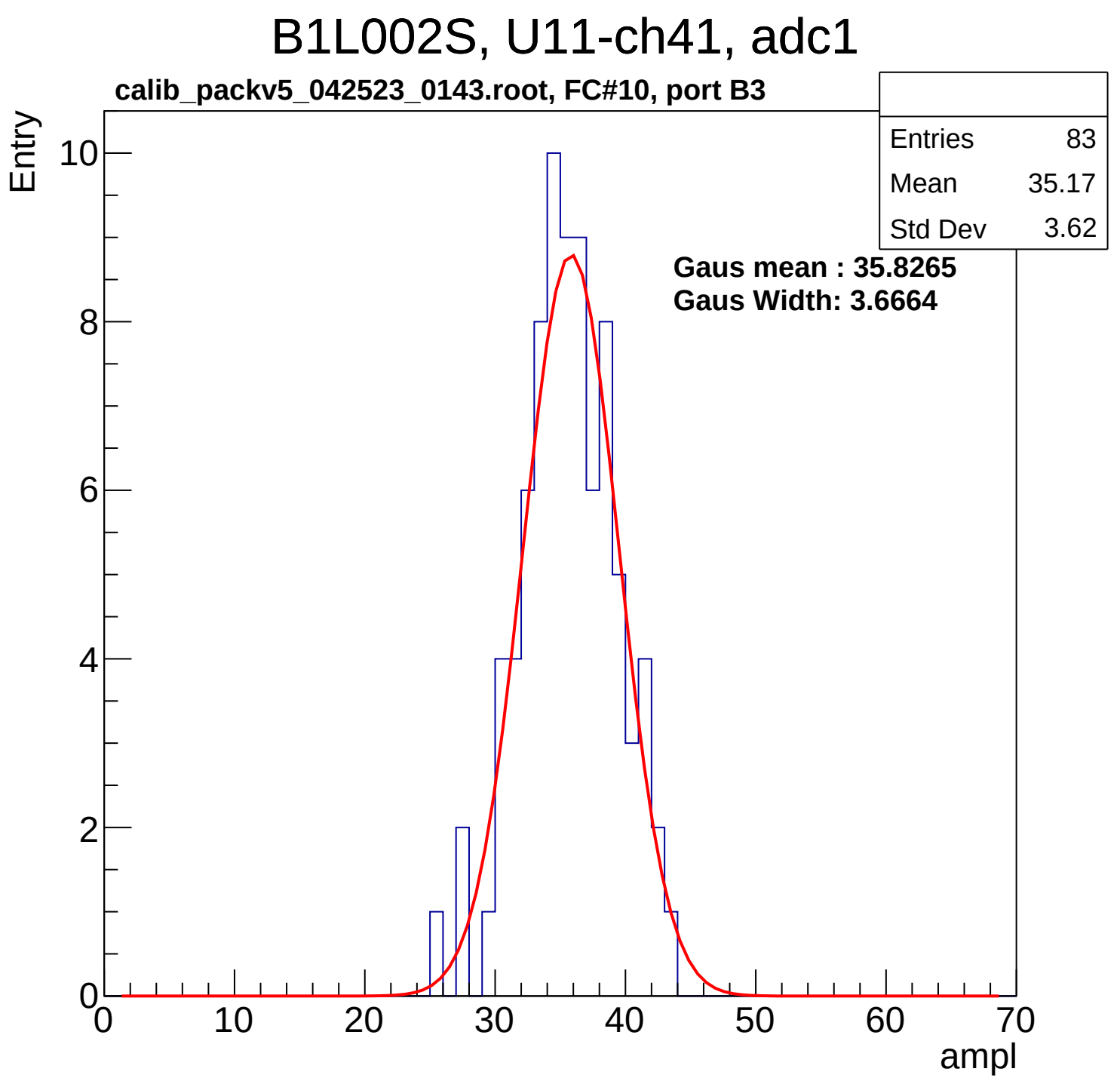
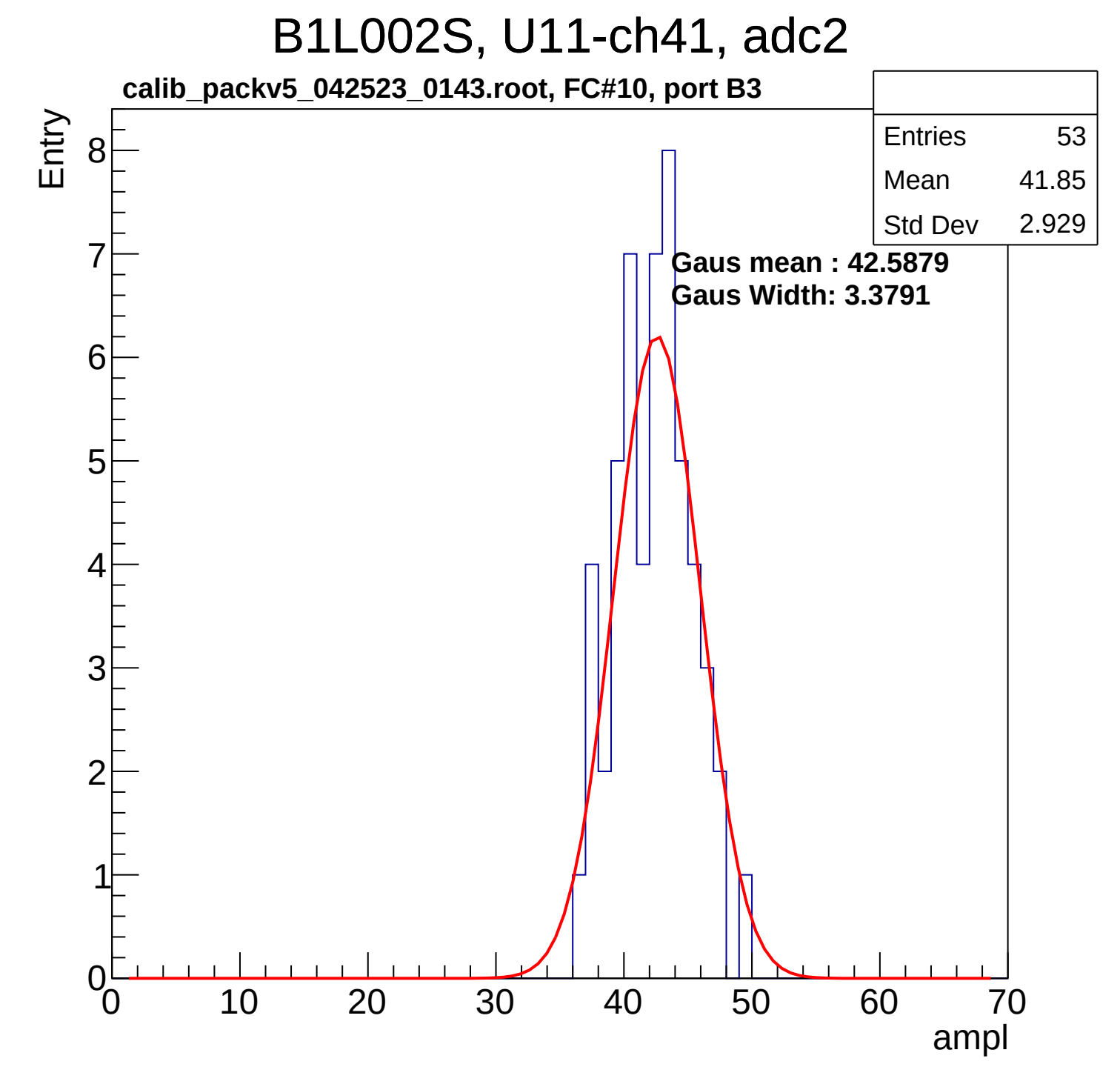
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.27
Std Dev 4.634
Gaus mean : 28.6514
Gaus Width: 3.4803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 35.17
Std Dev 3.62
Gaus mean : 35.8265
Gaus Width: 3.6664
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.85
Std Dev 2.929
Gaus mean : 42.5879
Gaus Width: 3.3791
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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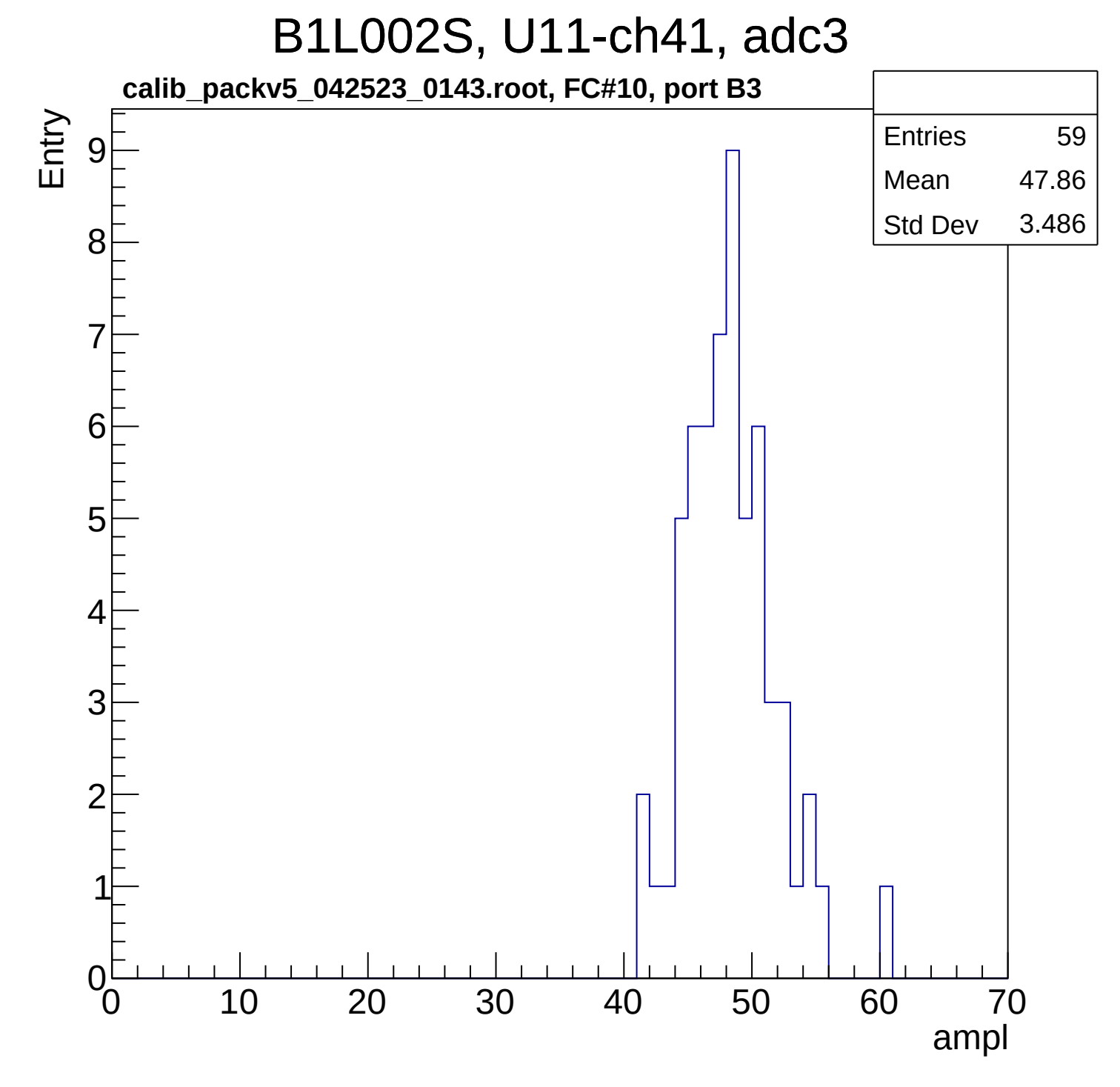
B1L002S, U11-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.86
Std Dev 3.486
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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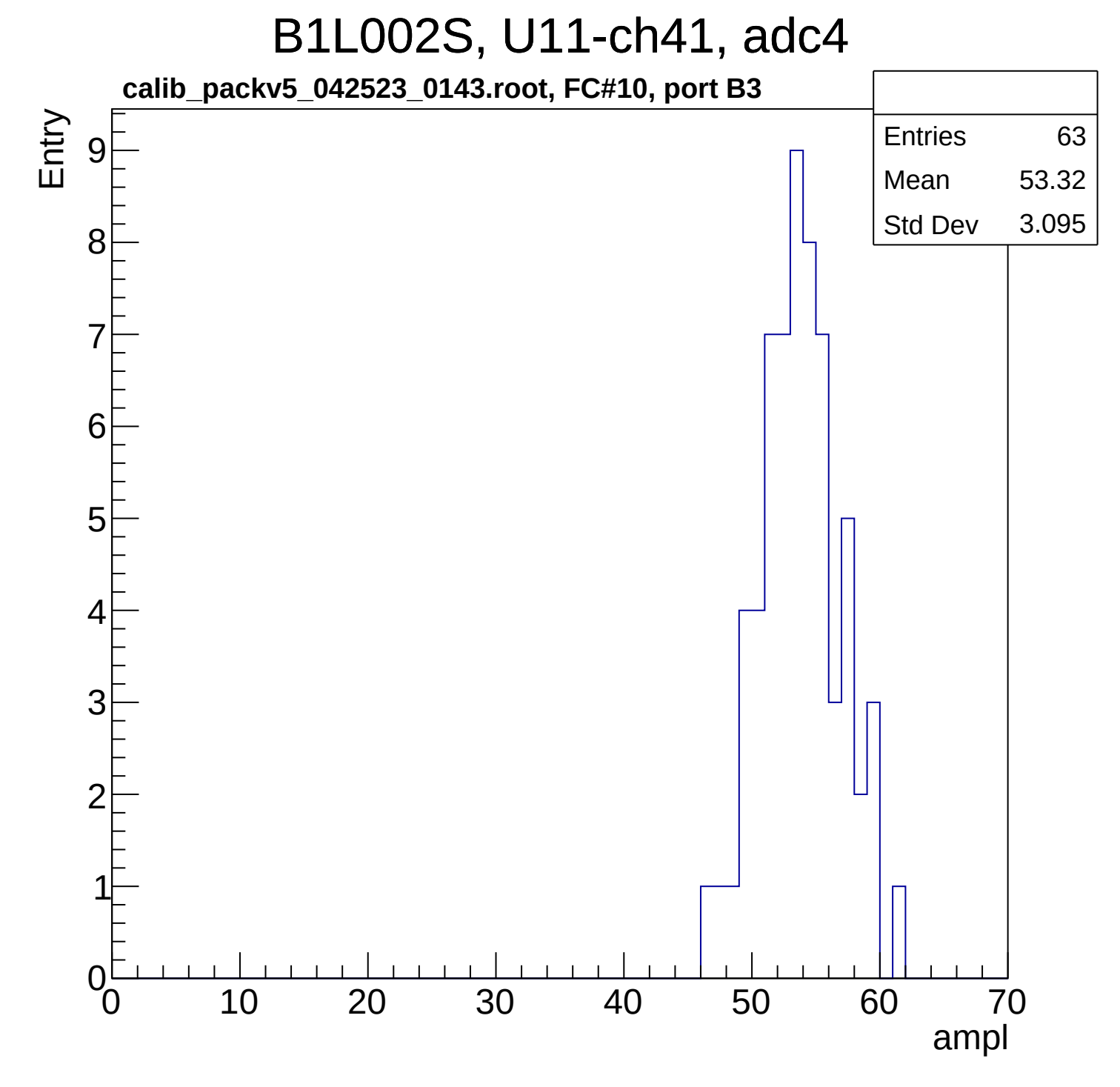
B1L002S, U11-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.32
Std Dev 3.095
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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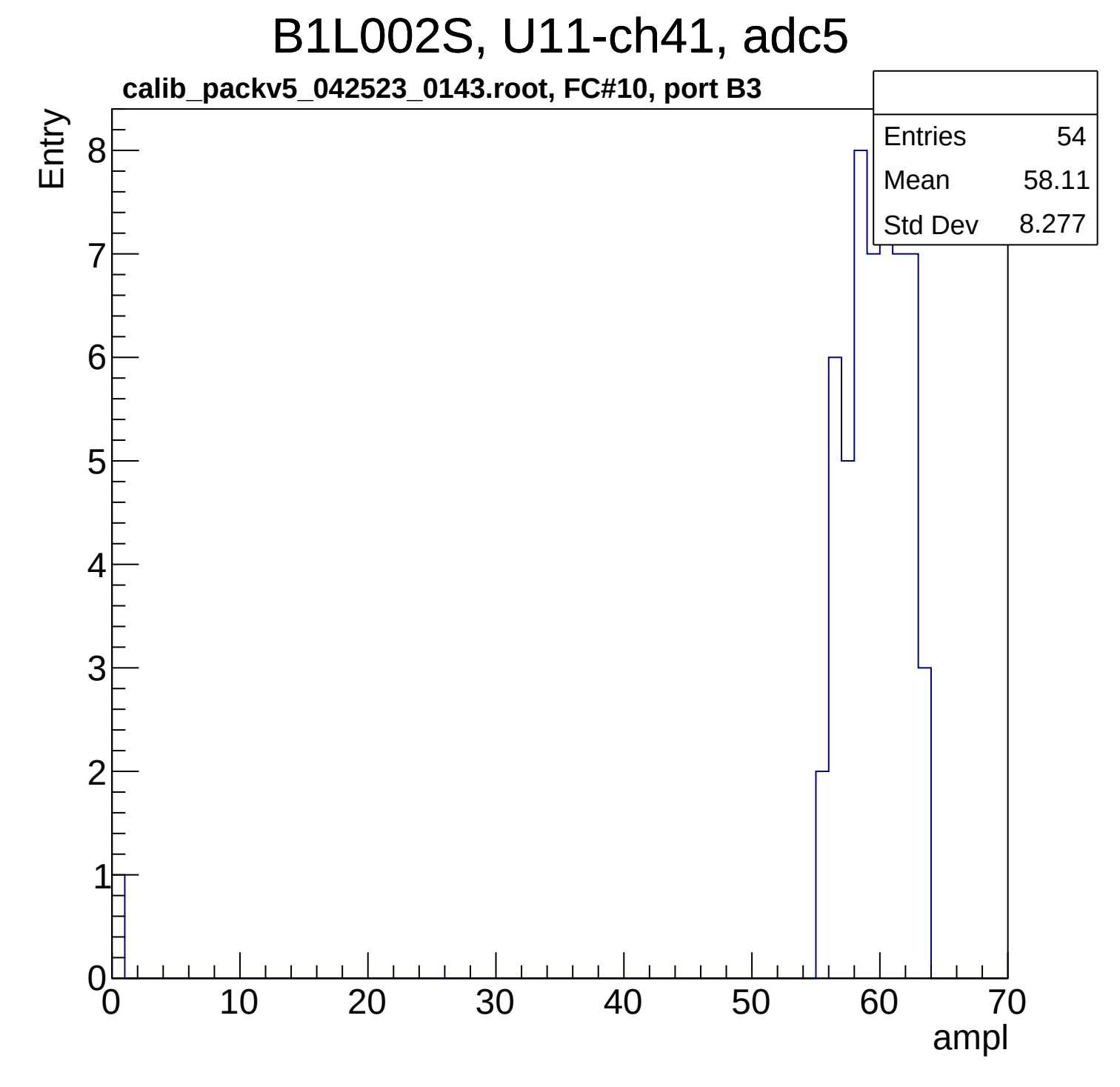
B1L002S, U11-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.11
Std Dev 8.277
Entry
ampl
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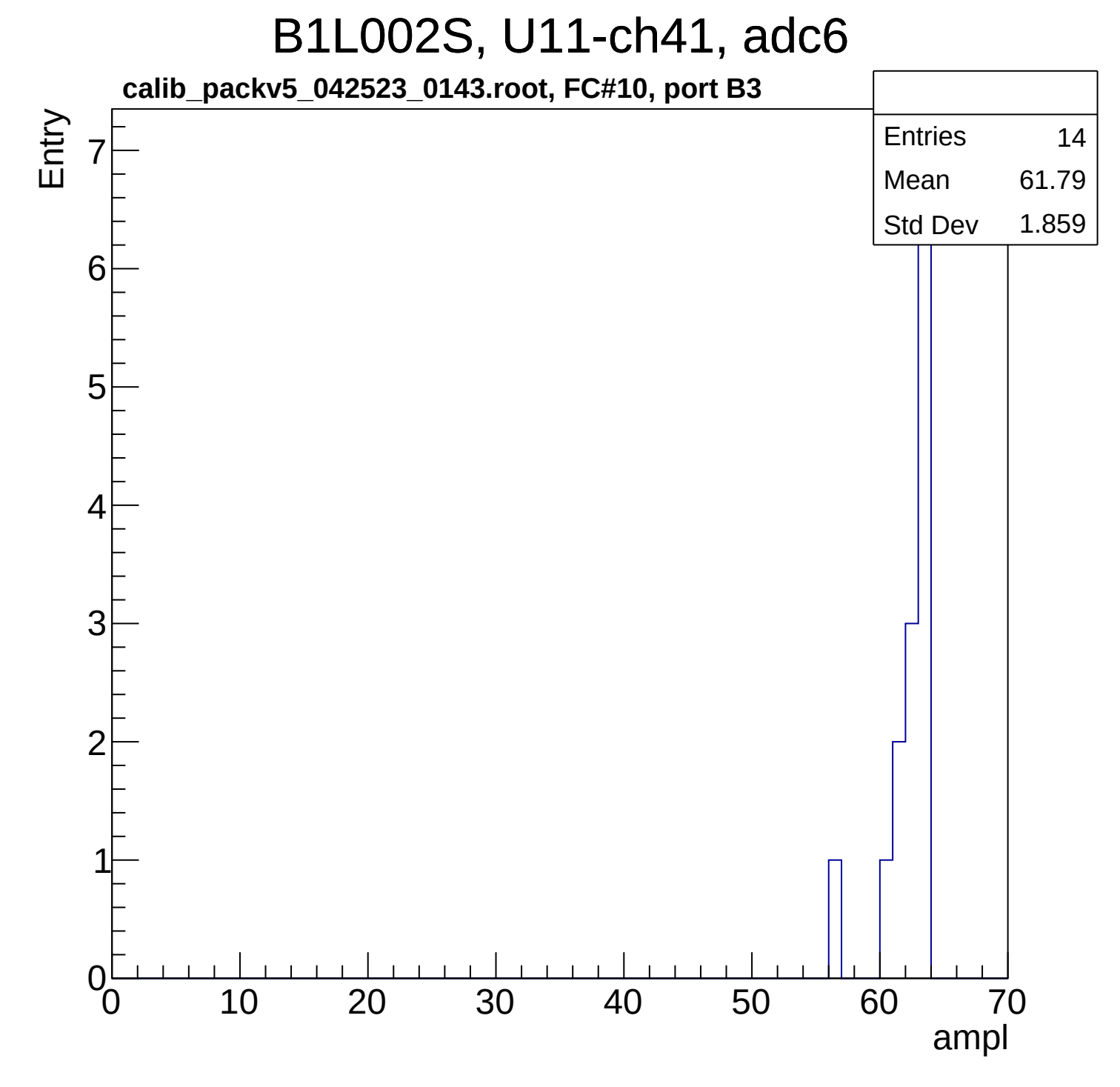
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.79
Std Dev 1.859
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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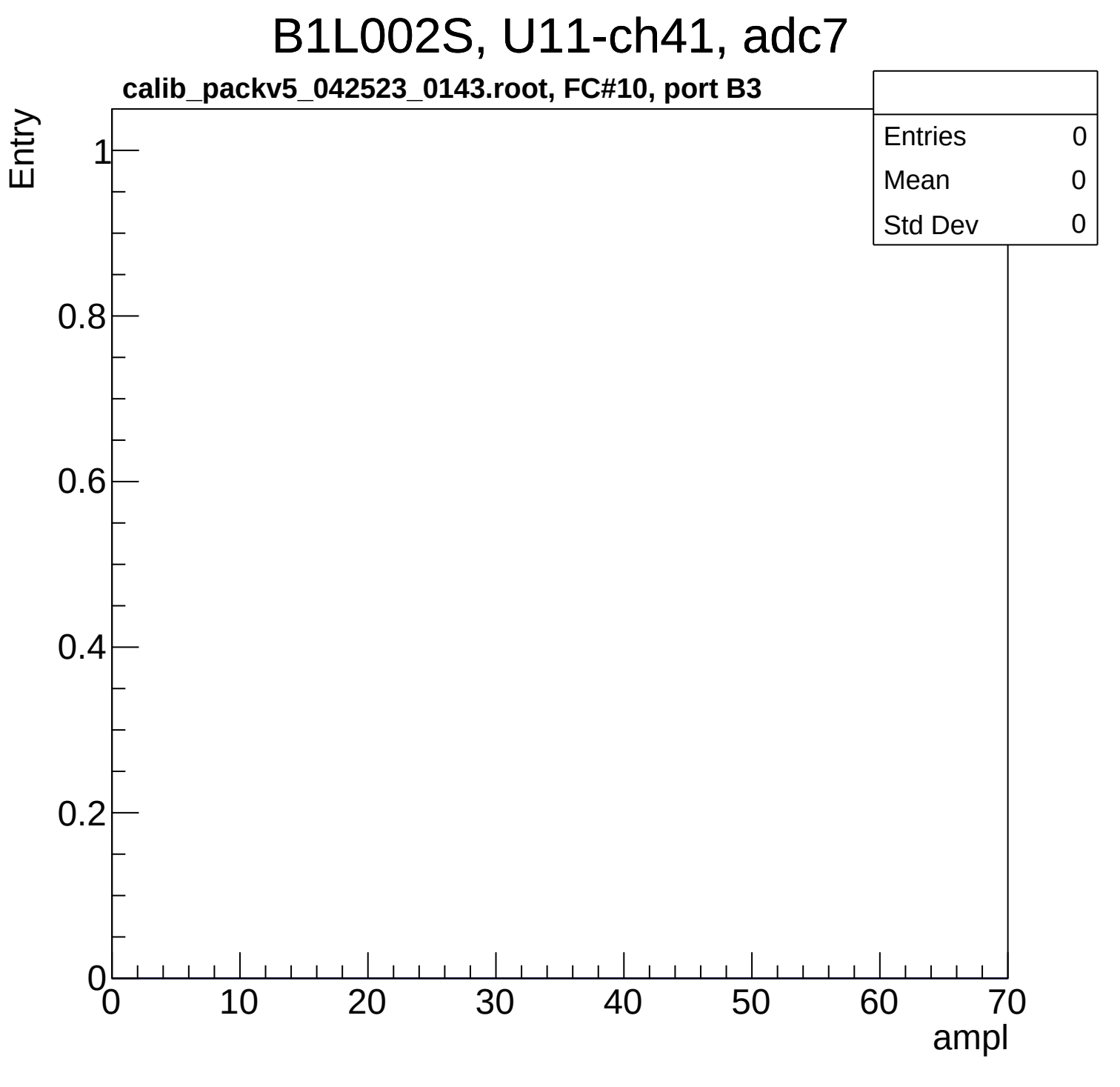
B1L002S, U11-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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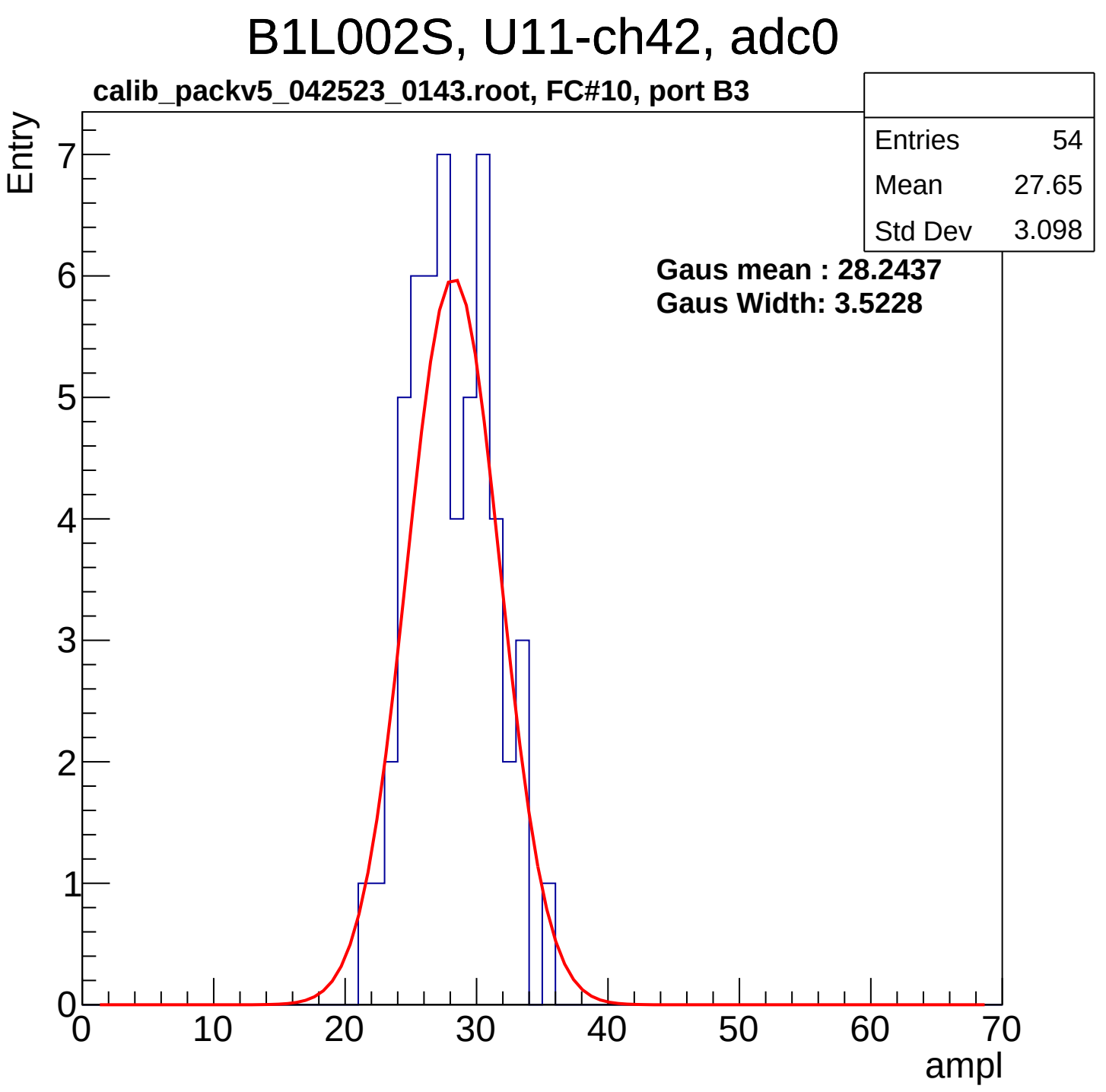
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.65
Std Dev 3.098
Gaus mean : 28.2437
Gaus Width: 3.5228
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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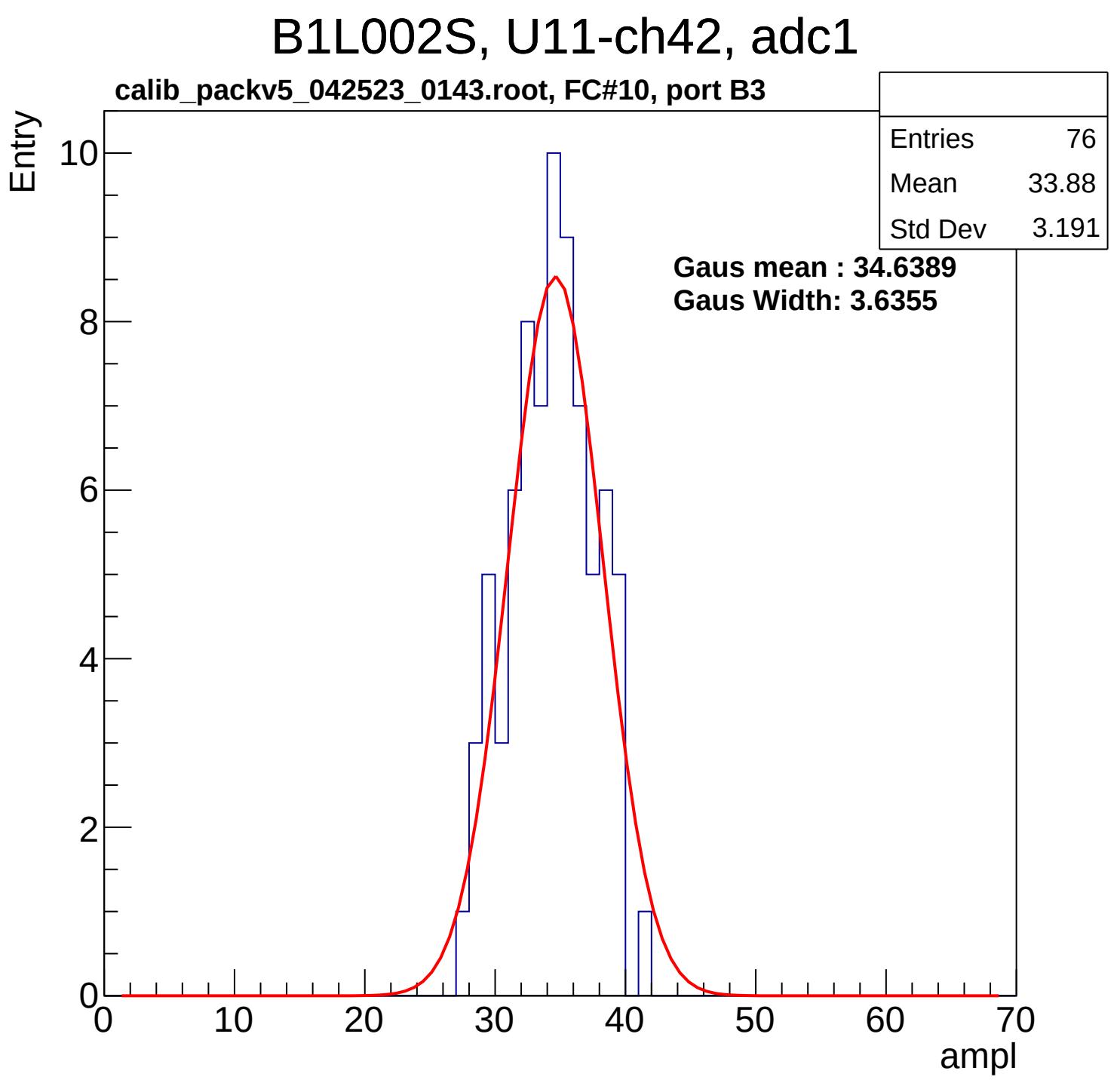
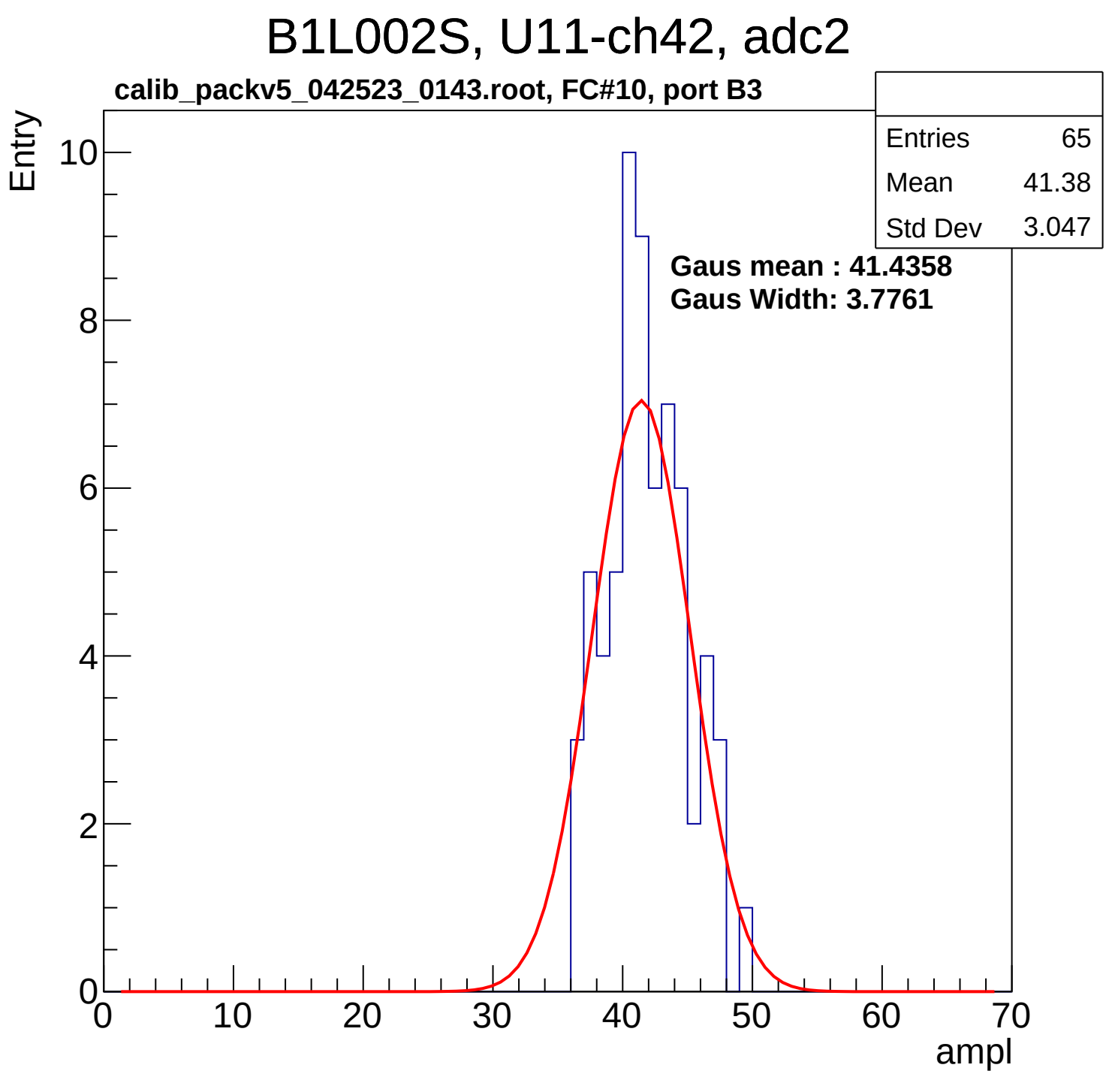

B1L002S, U11-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.88
Std Dev 3.191
Gaus mean : 34.6389
Gaus Width: 3.6355
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.38
Std Dev 3.047
Gaus mean : 41.4358
Gaus Width: 3.7761
Entry
ampl
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4
6
8
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0
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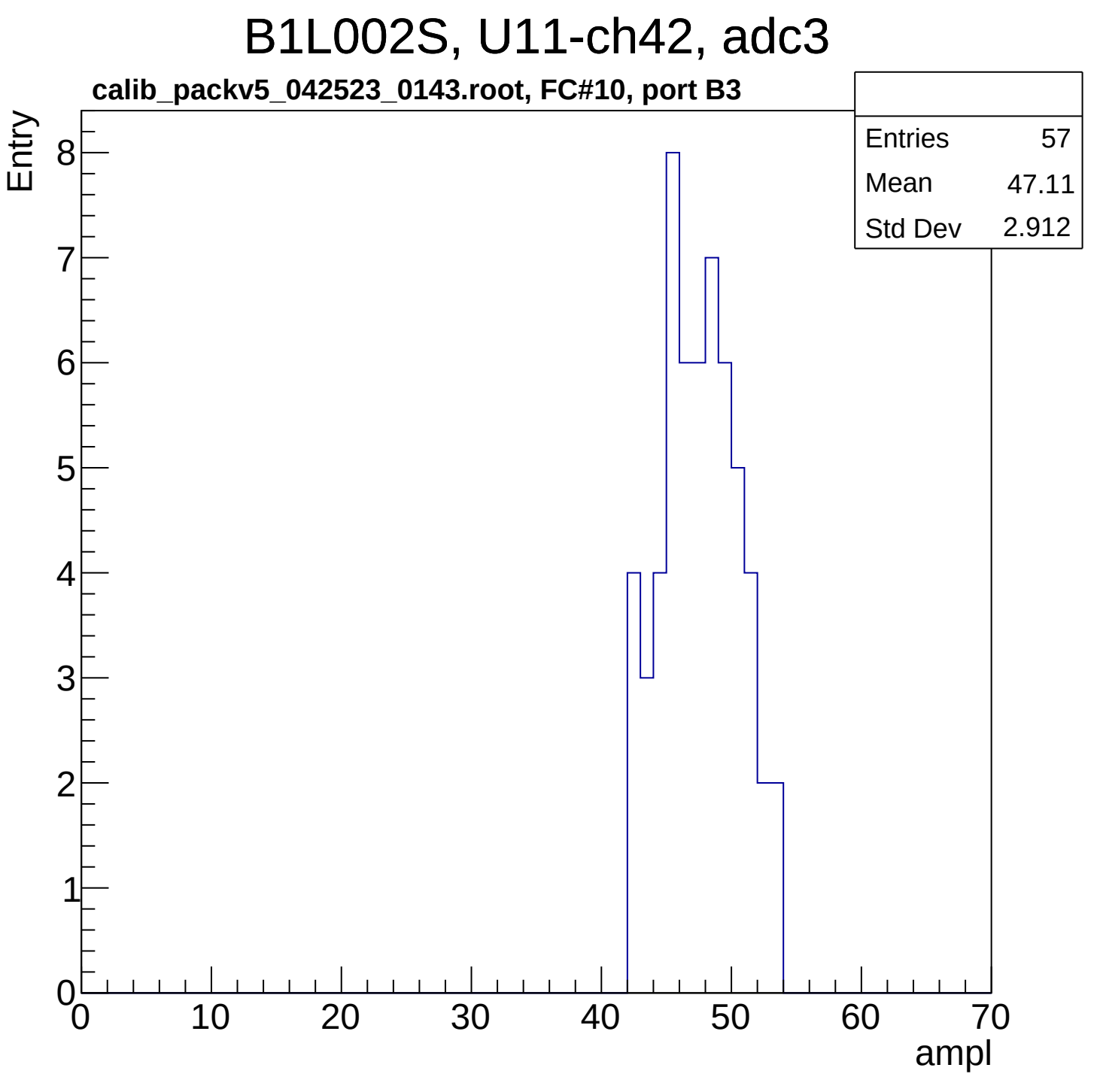
B1L002S, U11-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.11
Std Dev 2.912
Entry
ampl
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4
5
6
7
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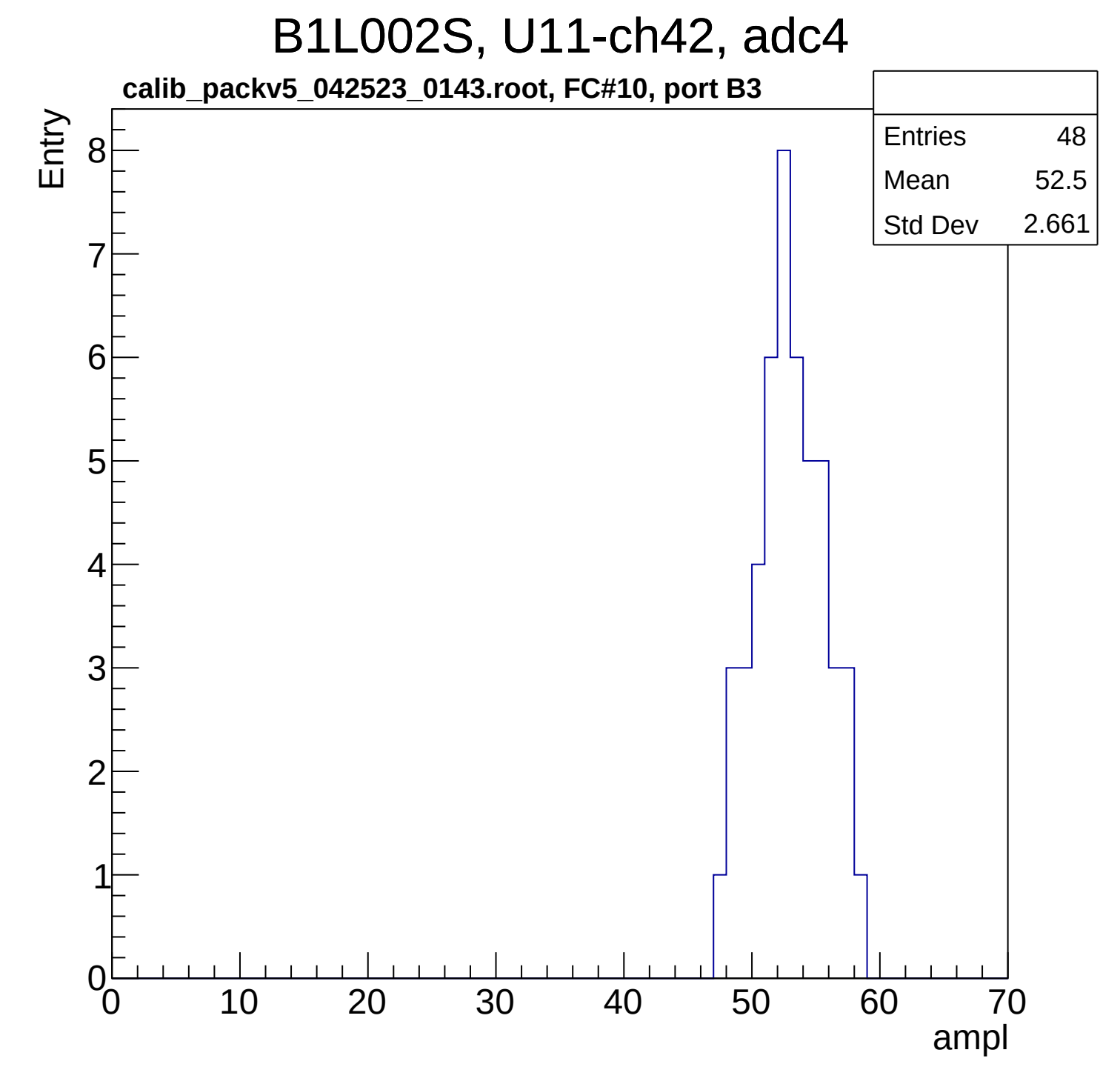
B1L002S, U11-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.5
Std Dev 2.661
Entry
ampl
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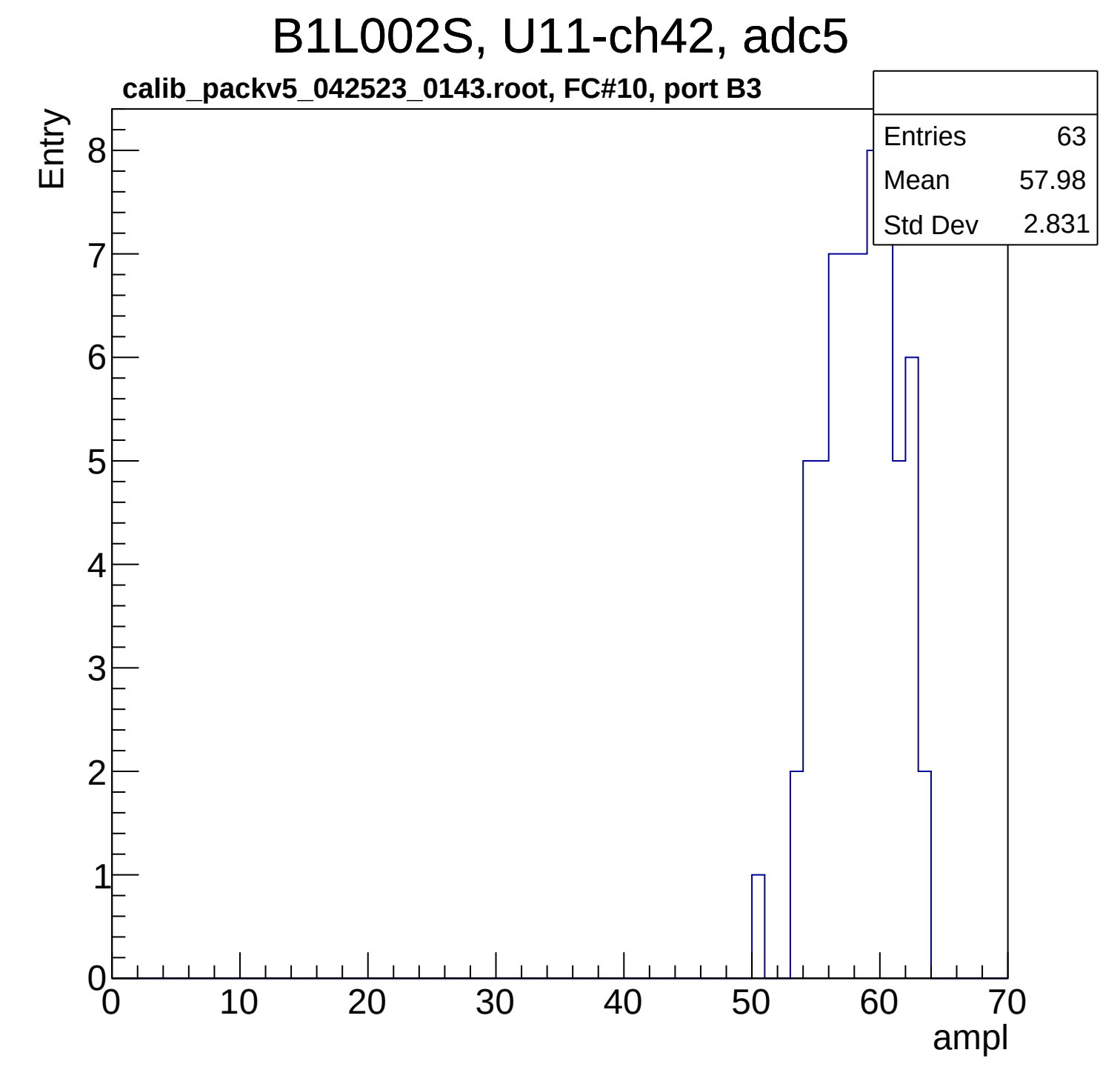
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.98
Std Dev 2.831
Entry
ampl
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2
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4
5
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7
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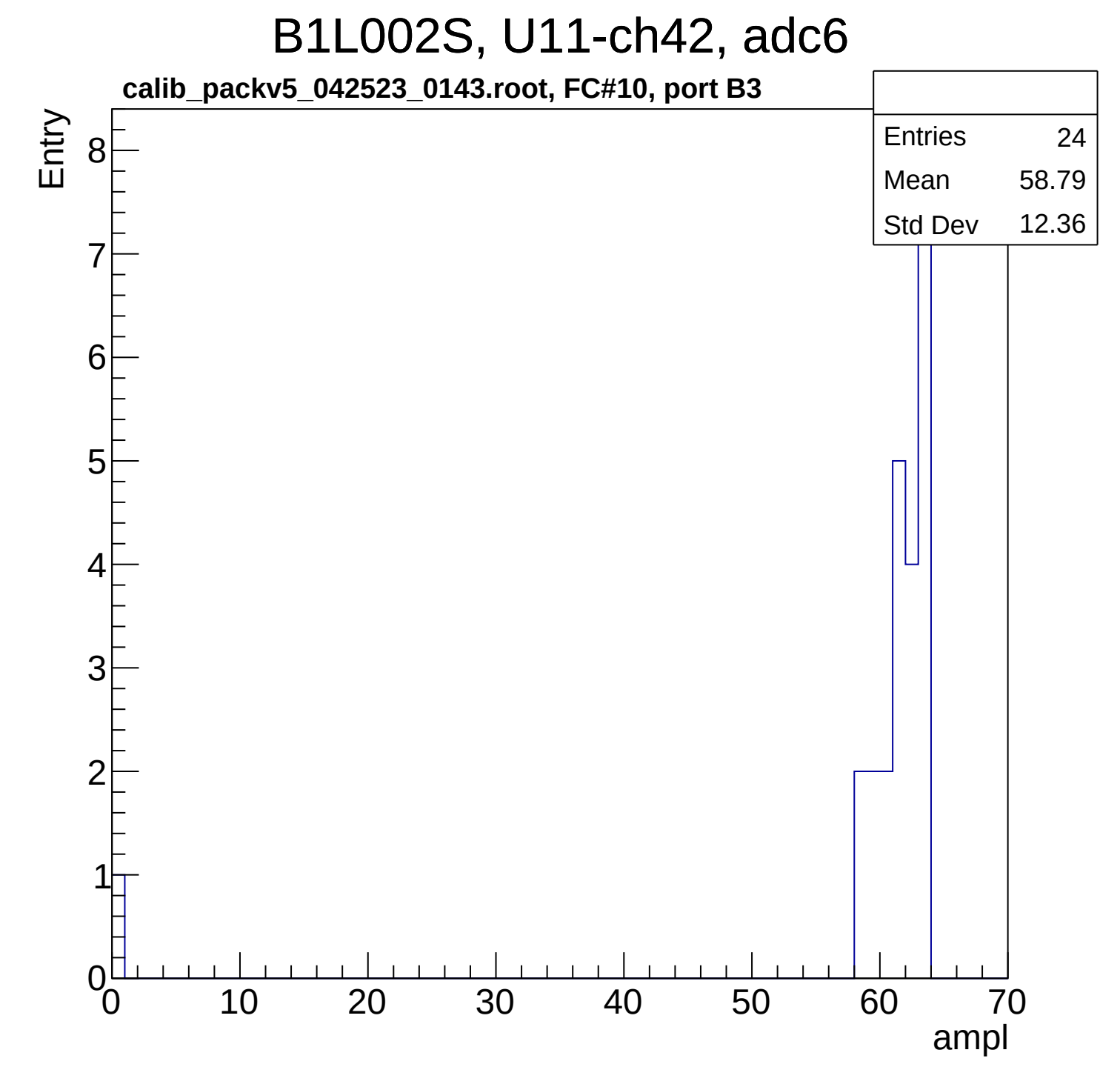
B1L002S, U11-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.79
Std Dev 12.36
Entry
ampl
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1
2
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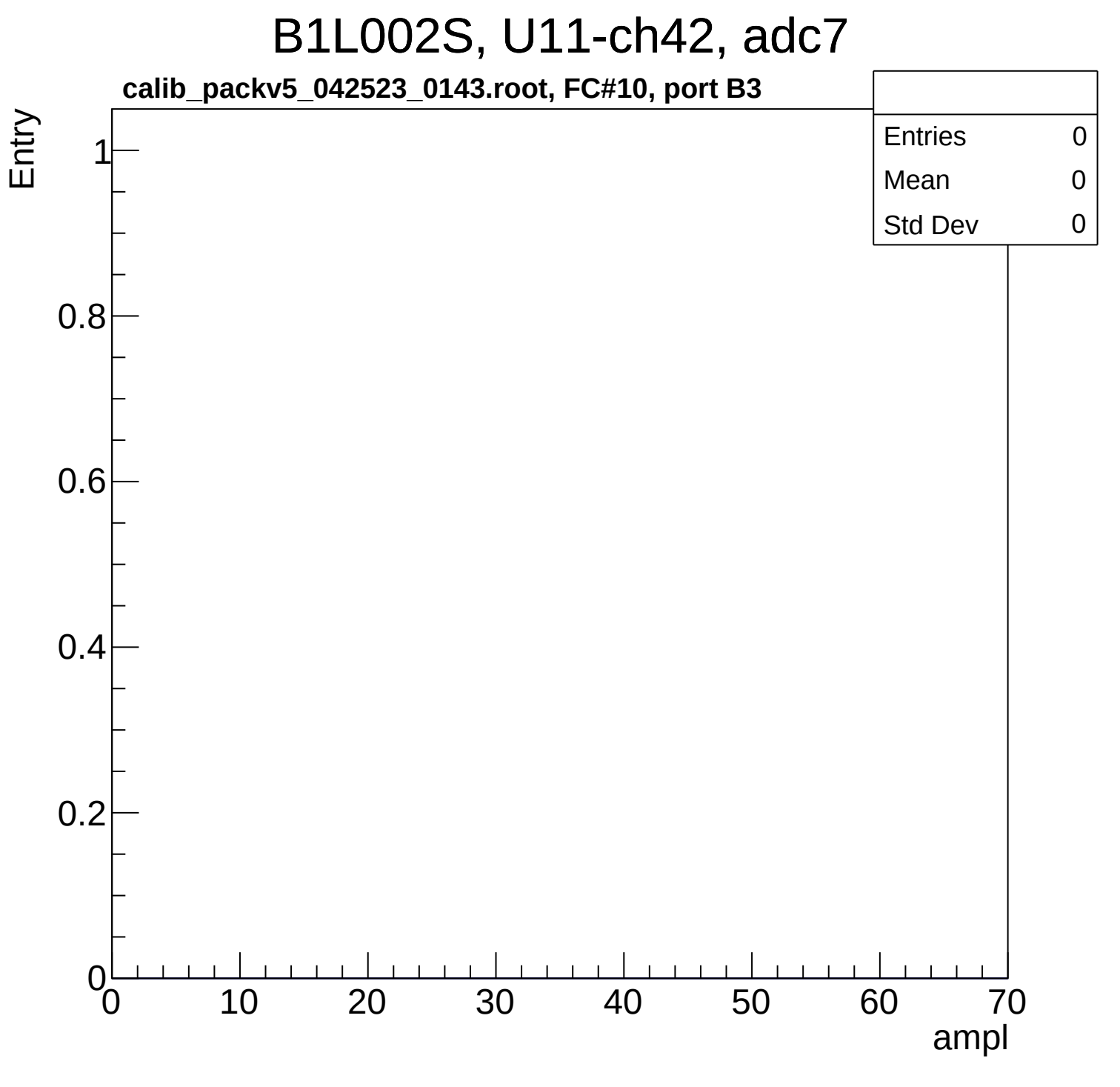
B1L002S, U11-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
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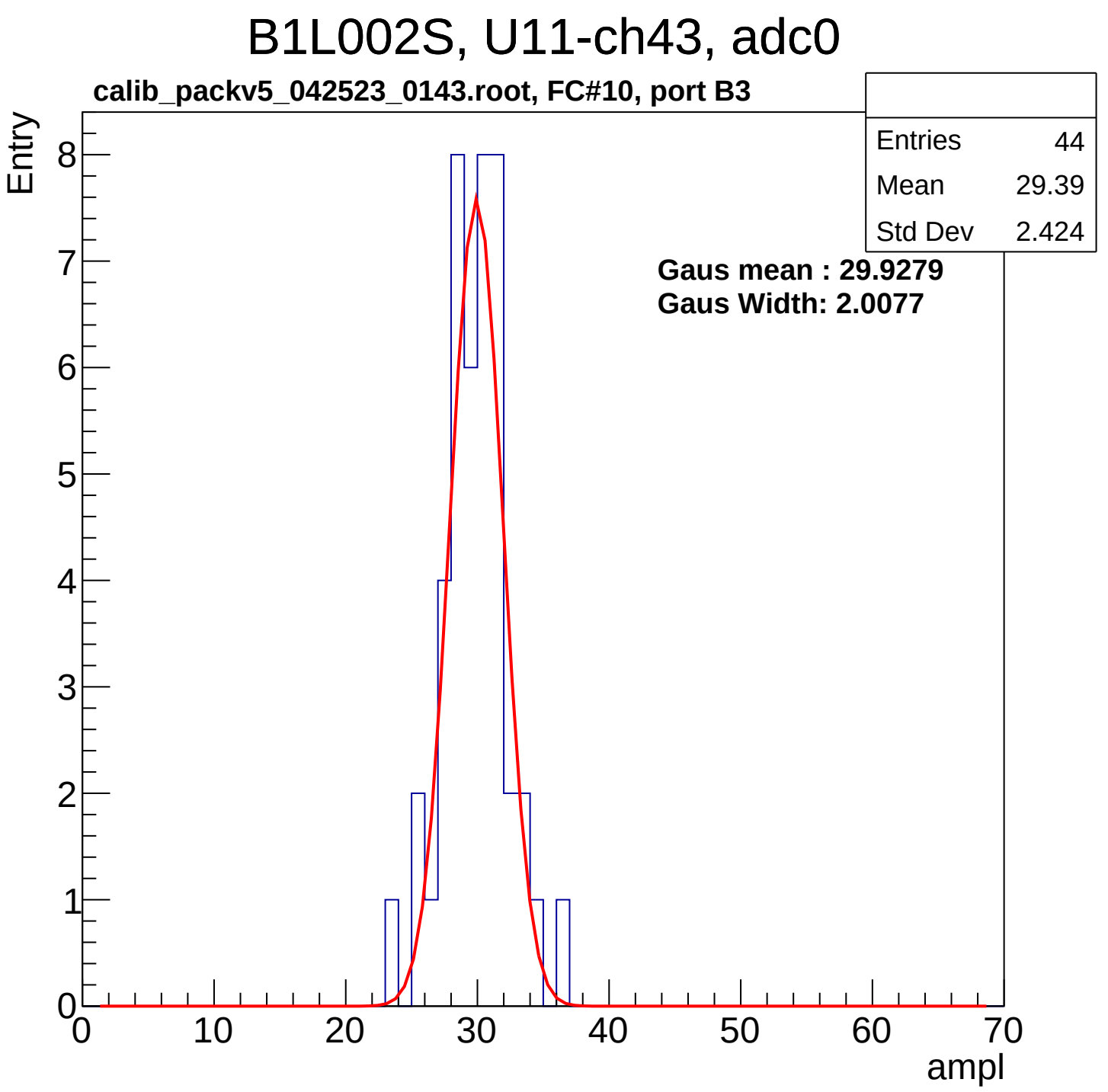
50
60
70
ampl


B1L002S, U11-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 29.39
Std Dev 2.424
Gaus mean : 29.9279
Gaus Width: 2.0077
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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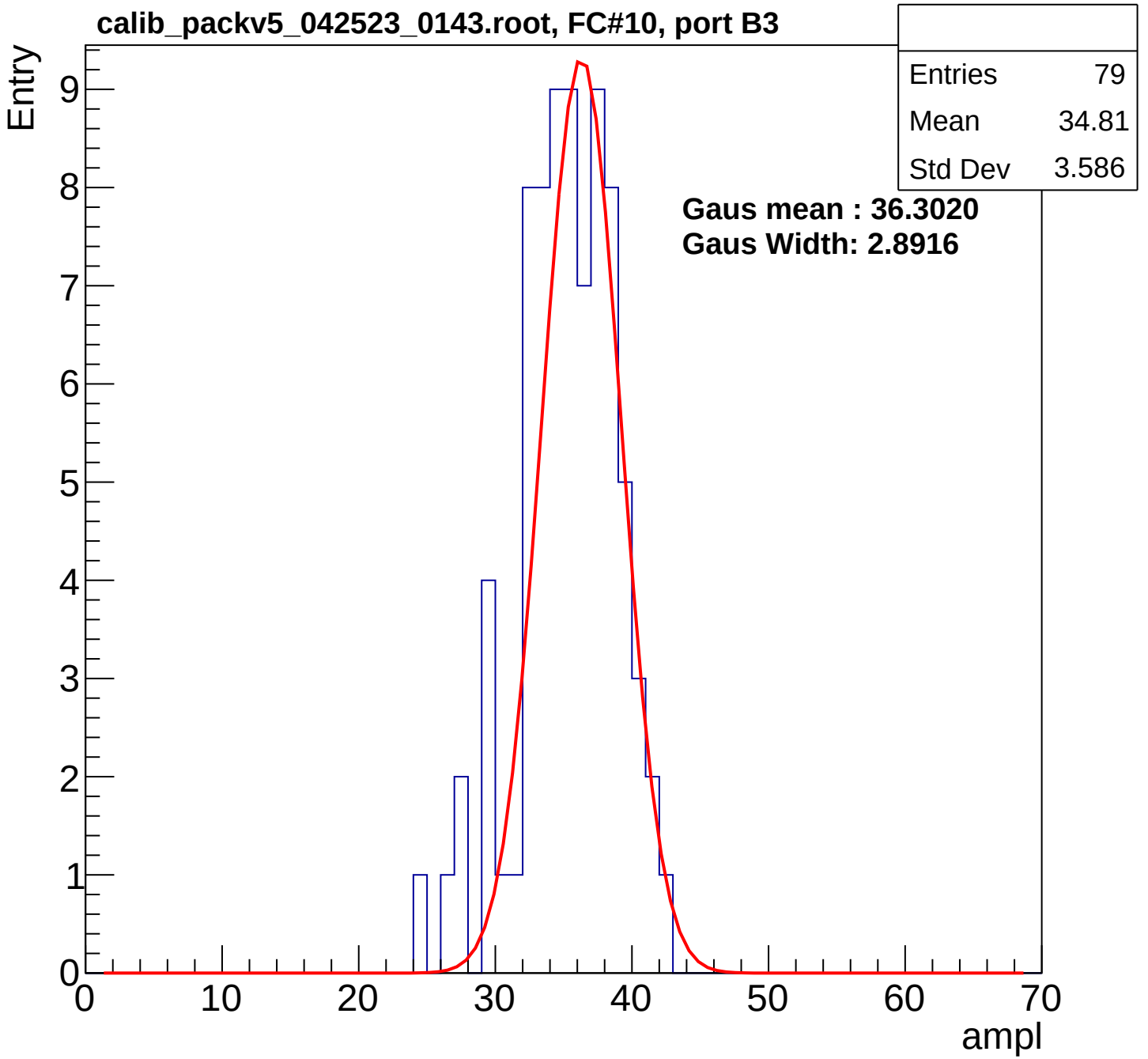
B1L002S, U11-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.81
Std Dev 3.586
Gaus mean : 36.3020
Gaus Width: 2.8916
Entry
ampl
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2
3
4
5
6
7
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0
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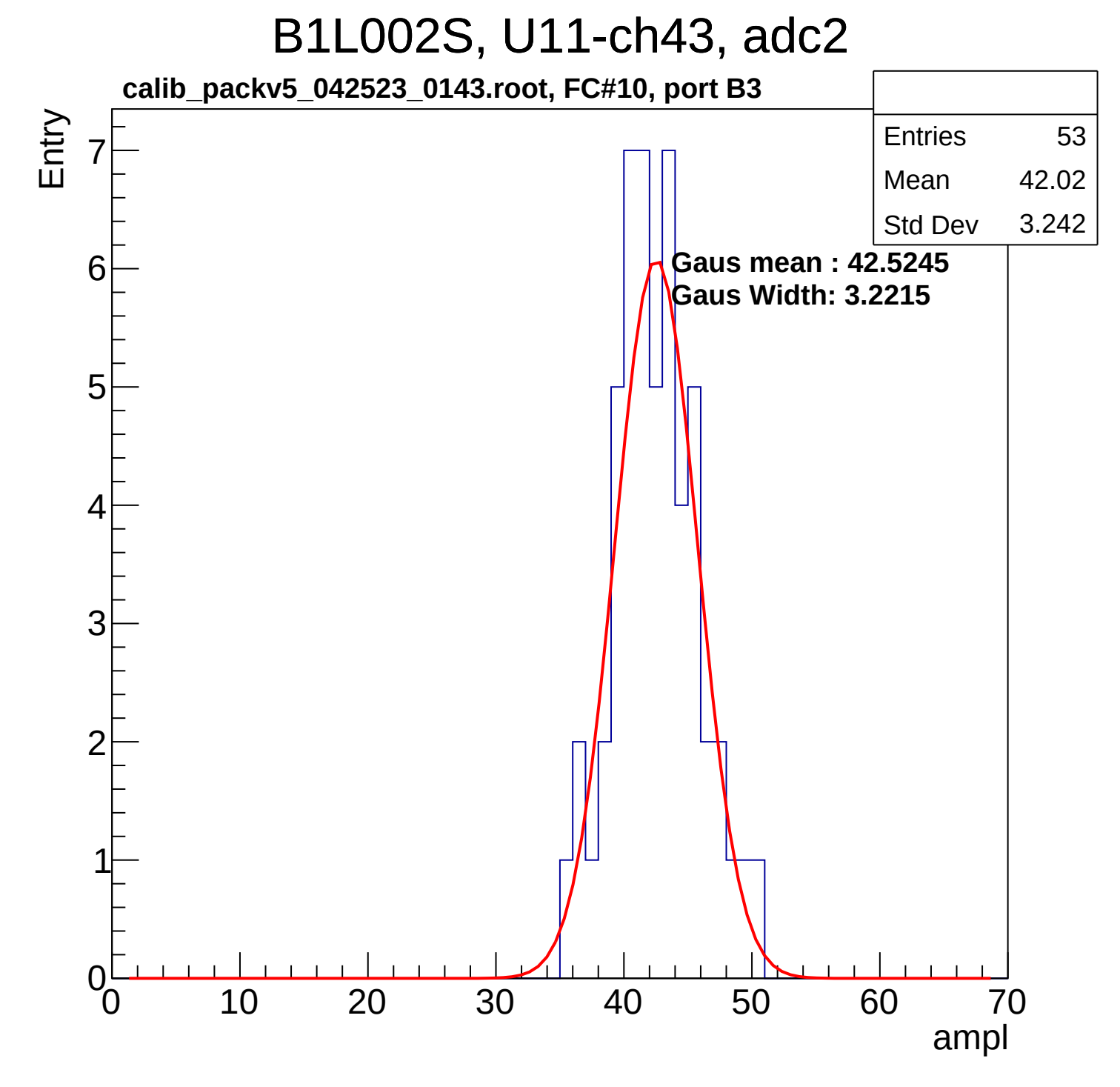
B1L002S, U11-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 42.02
Std Dev 3.242
Gaus mean : 42.5245
Gaus Width: 3.2215
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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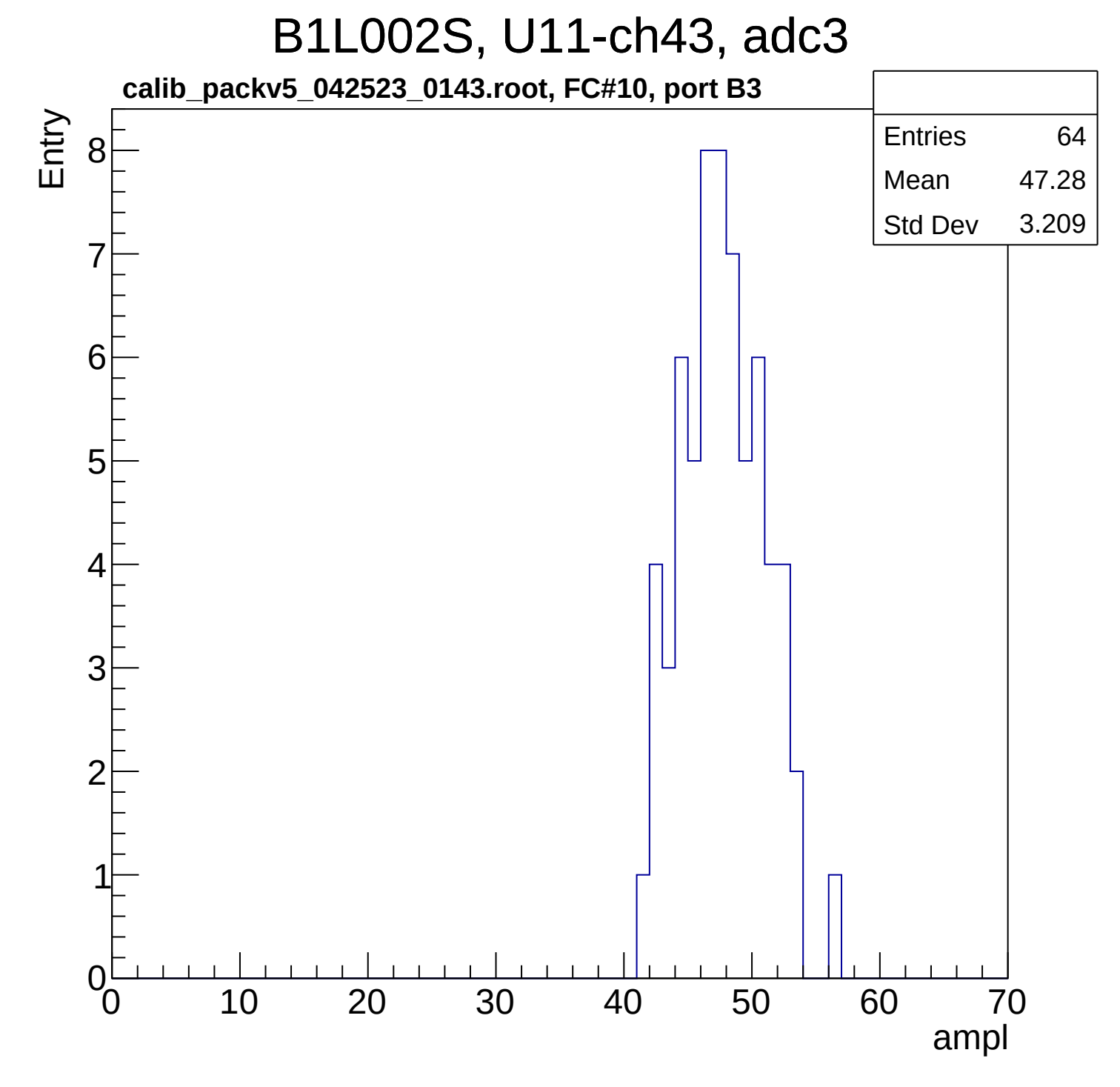
B1L002S, U11-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.28
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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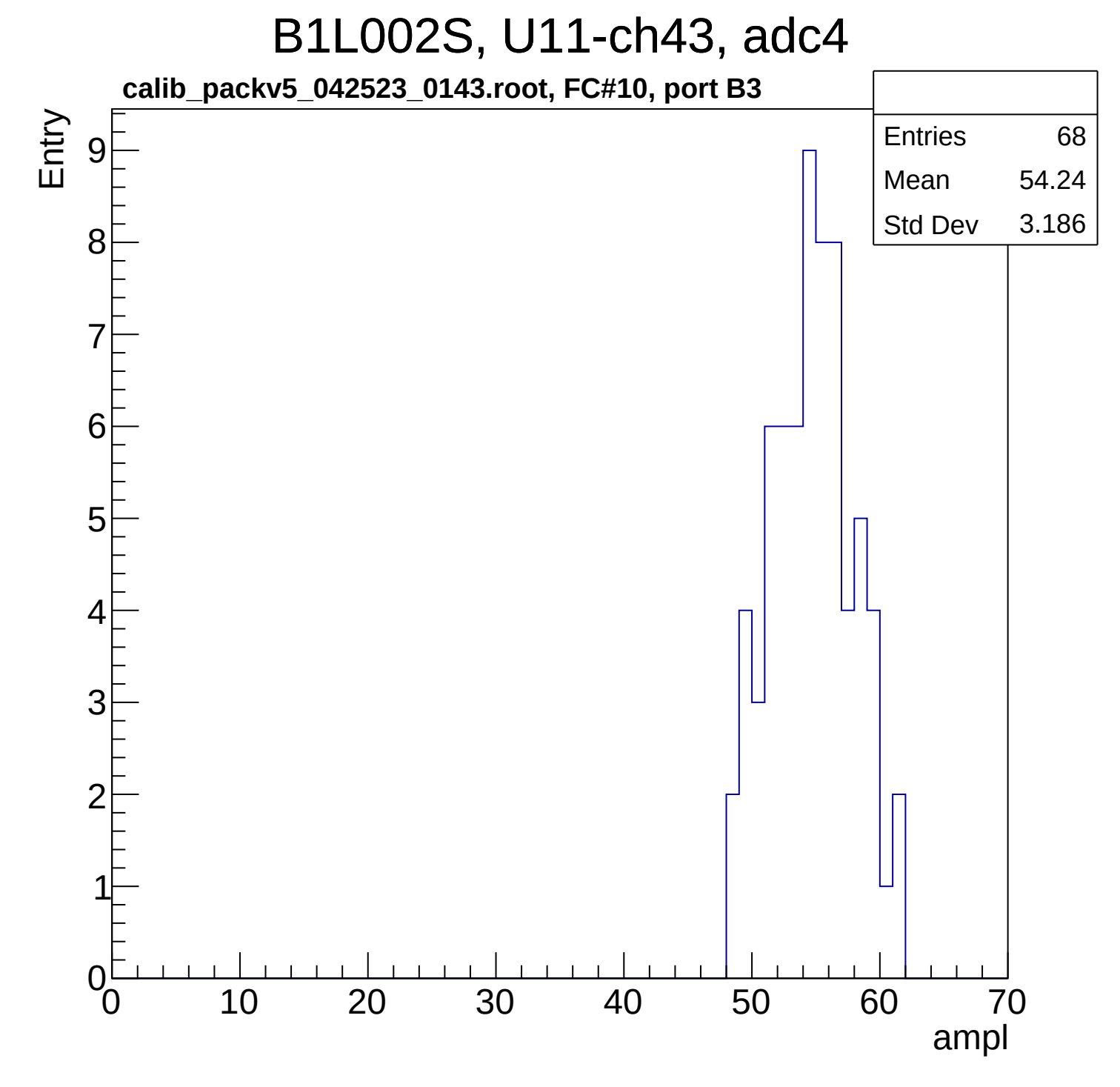
B1L002S, U11-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 54.24
Std Dev 3.186
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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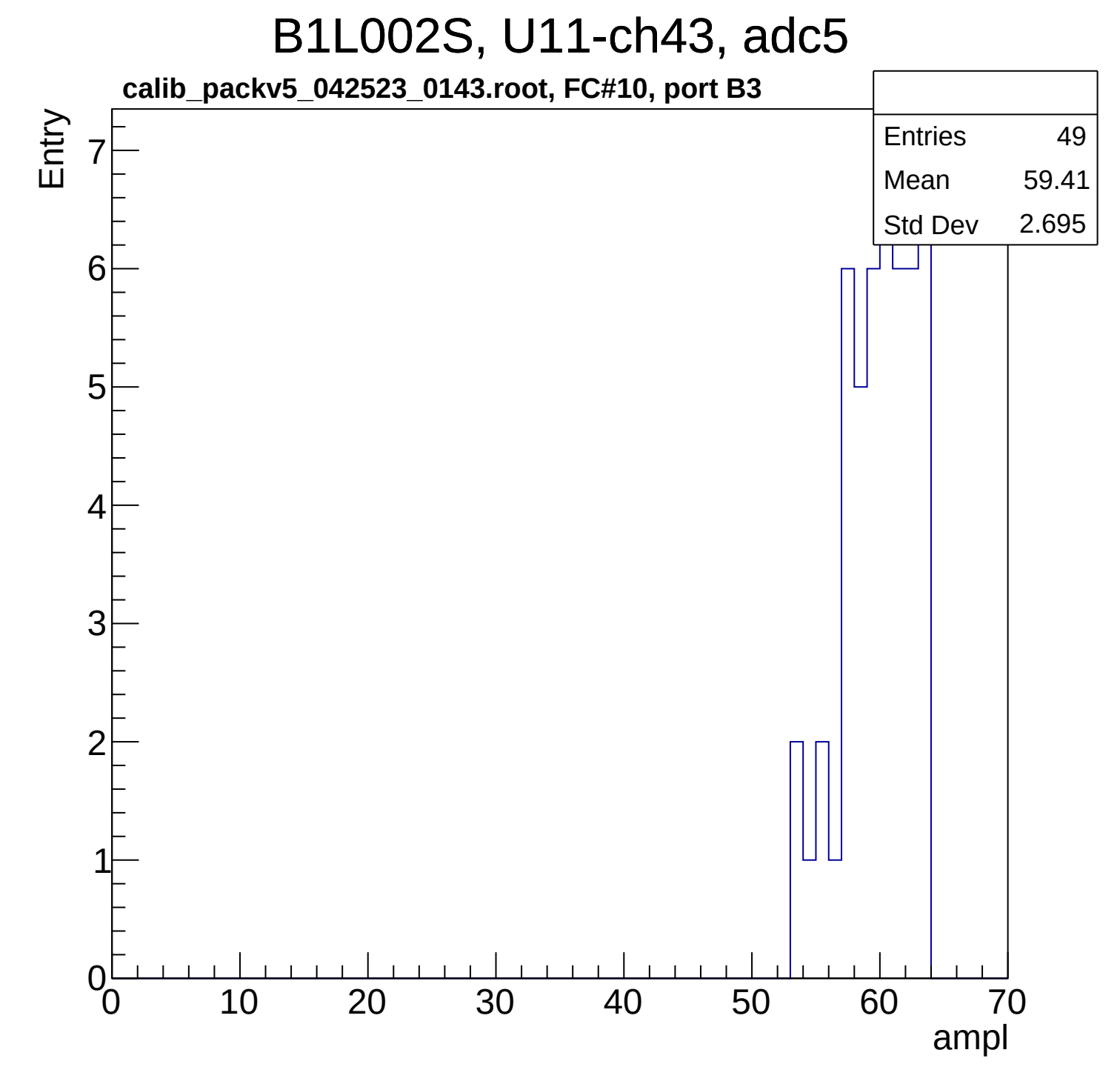
B1L002S, U11-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.41
Std Dev 2.695
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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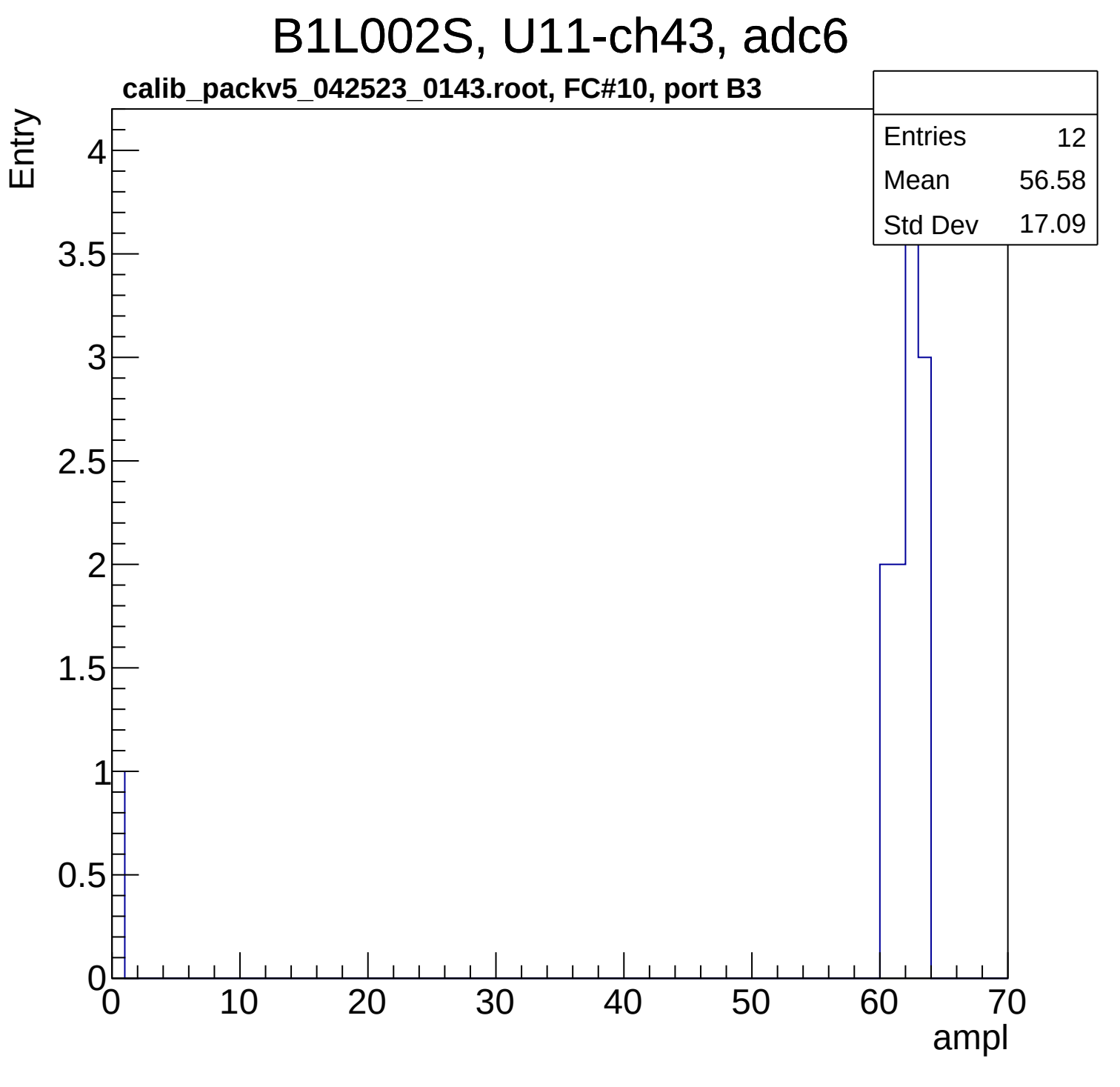
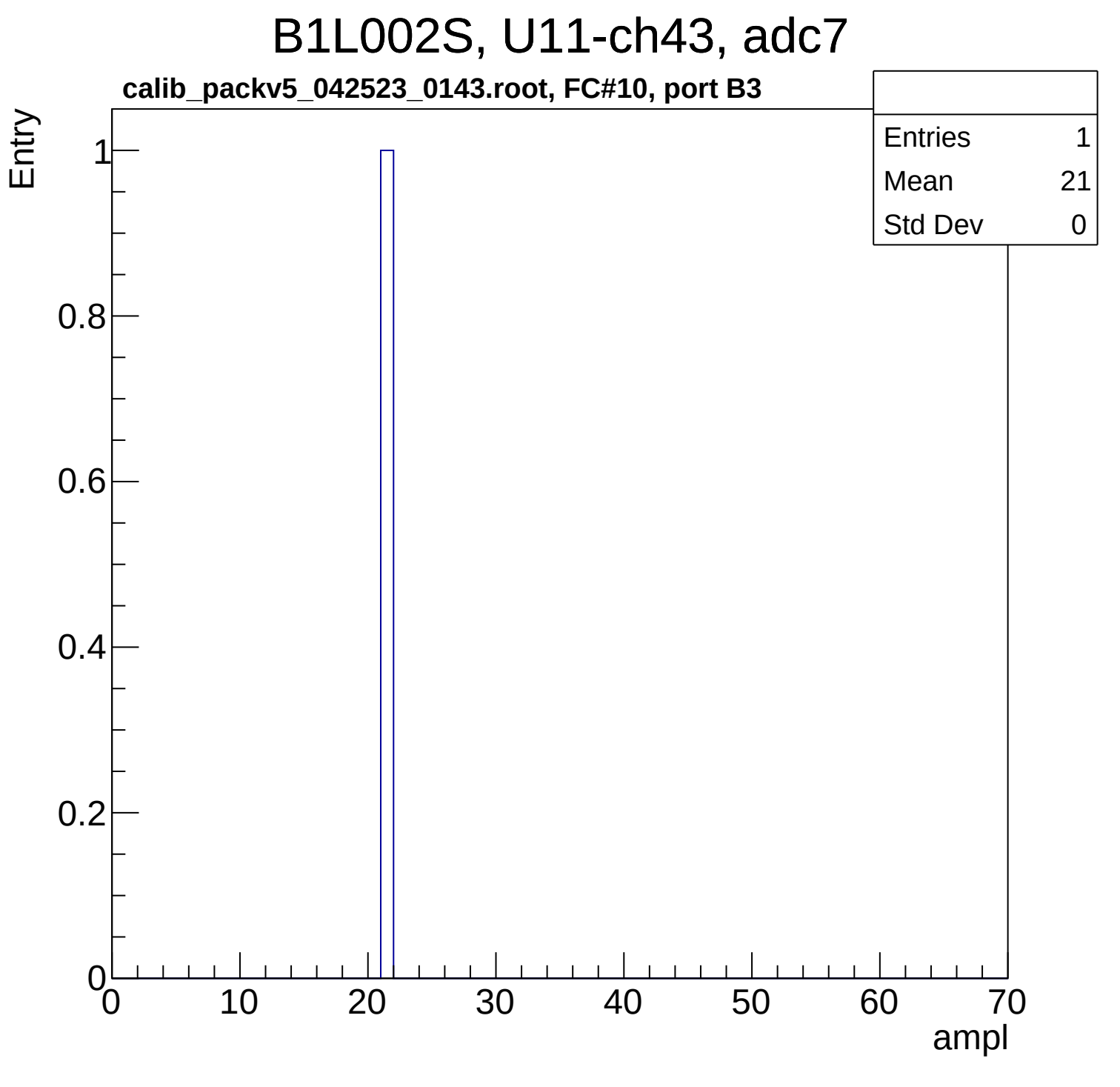

B1L002S, U11-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.58
Std Dev 17.09
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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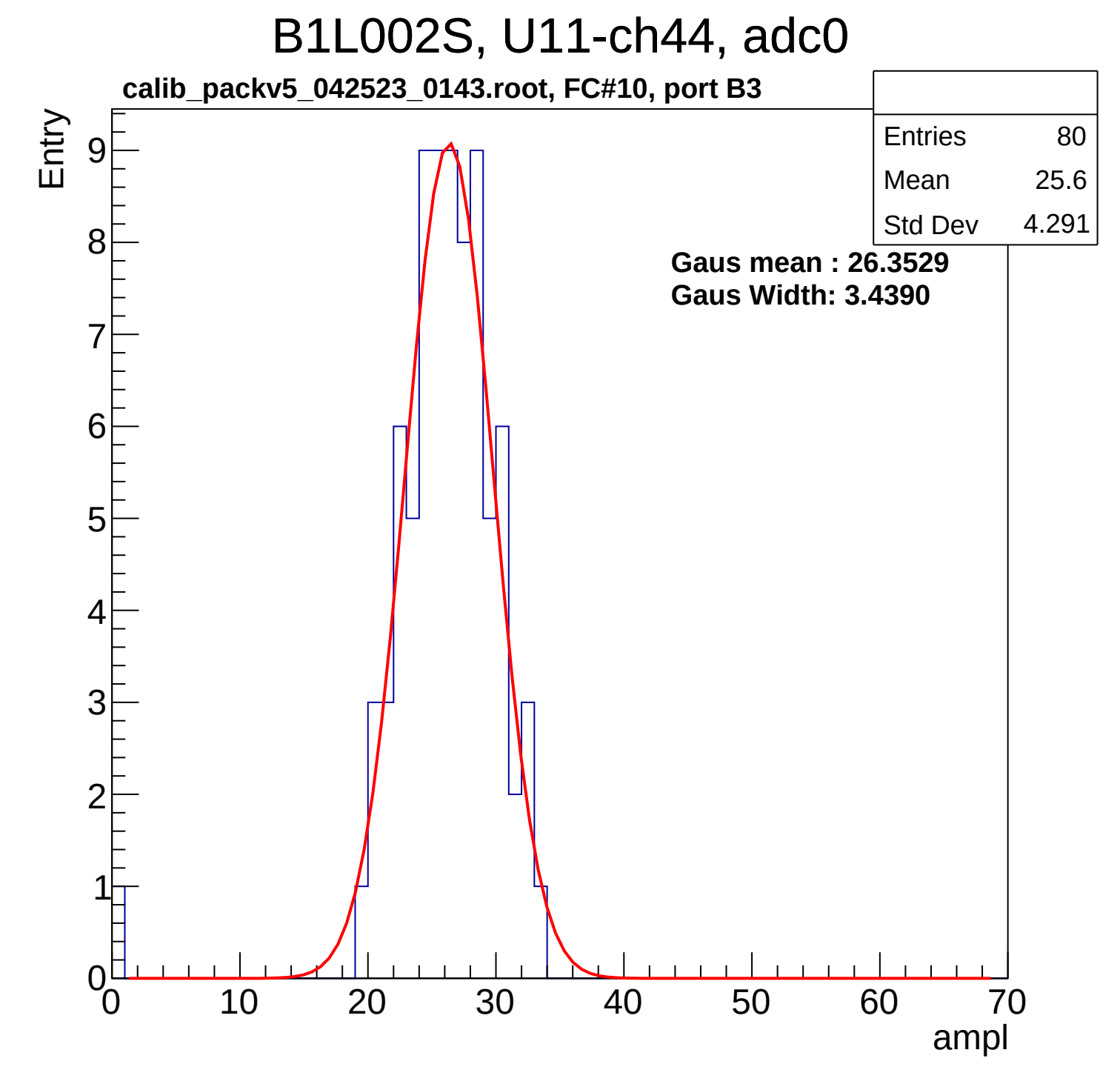
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 25.6
Std Dev 4.291
Gaus mean : 26.3529
Gaus Width: 3.4390
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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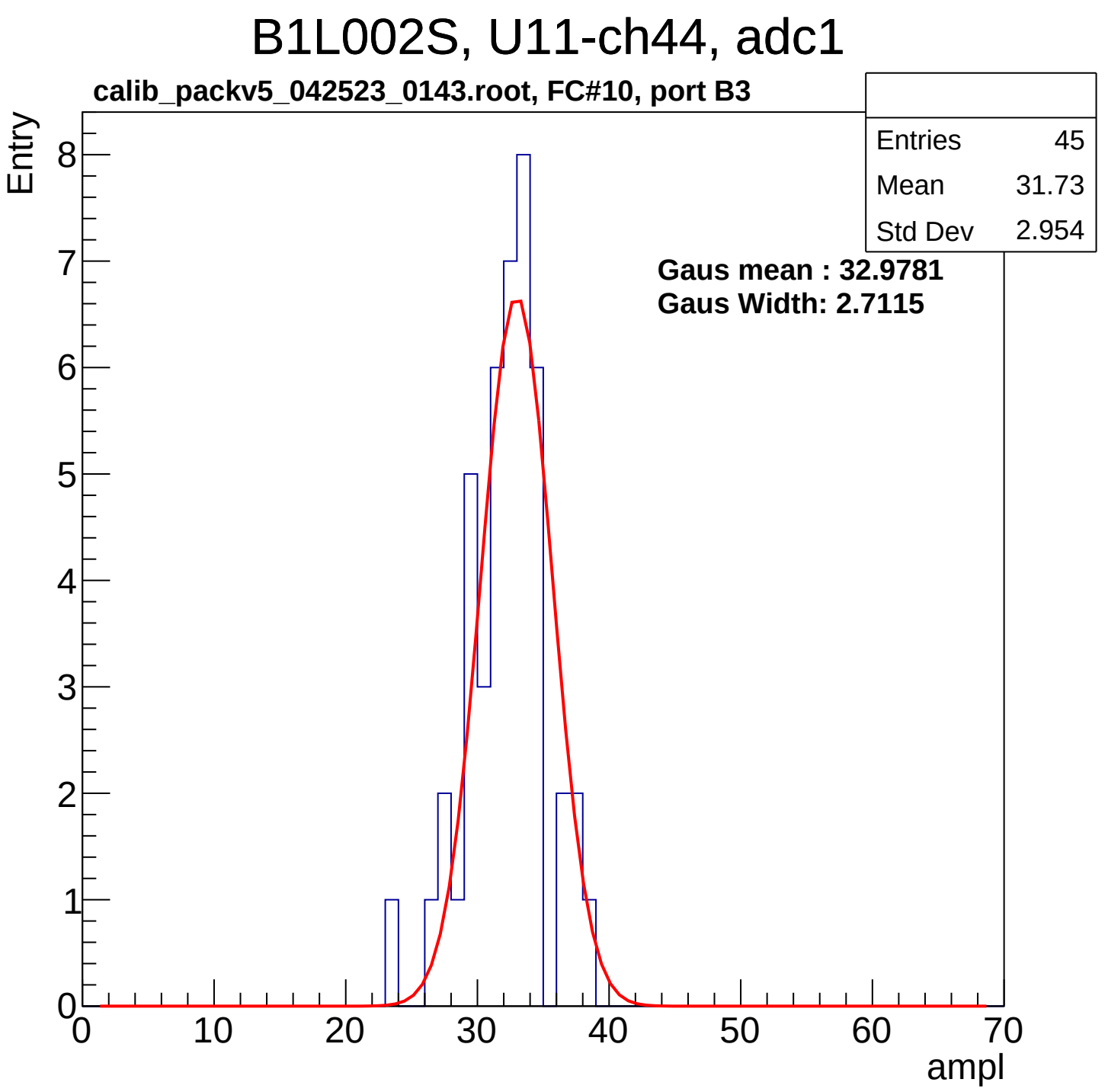
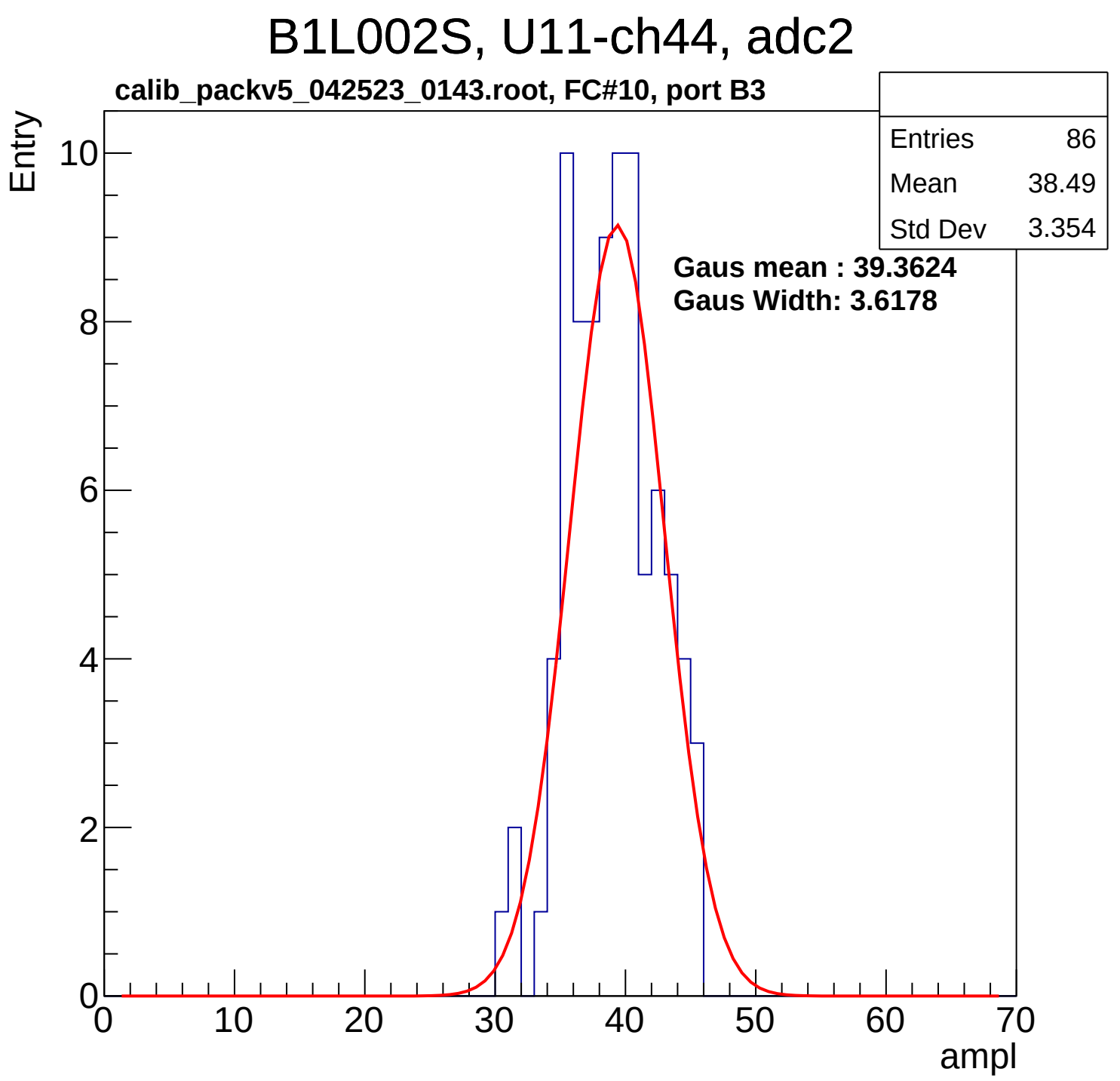
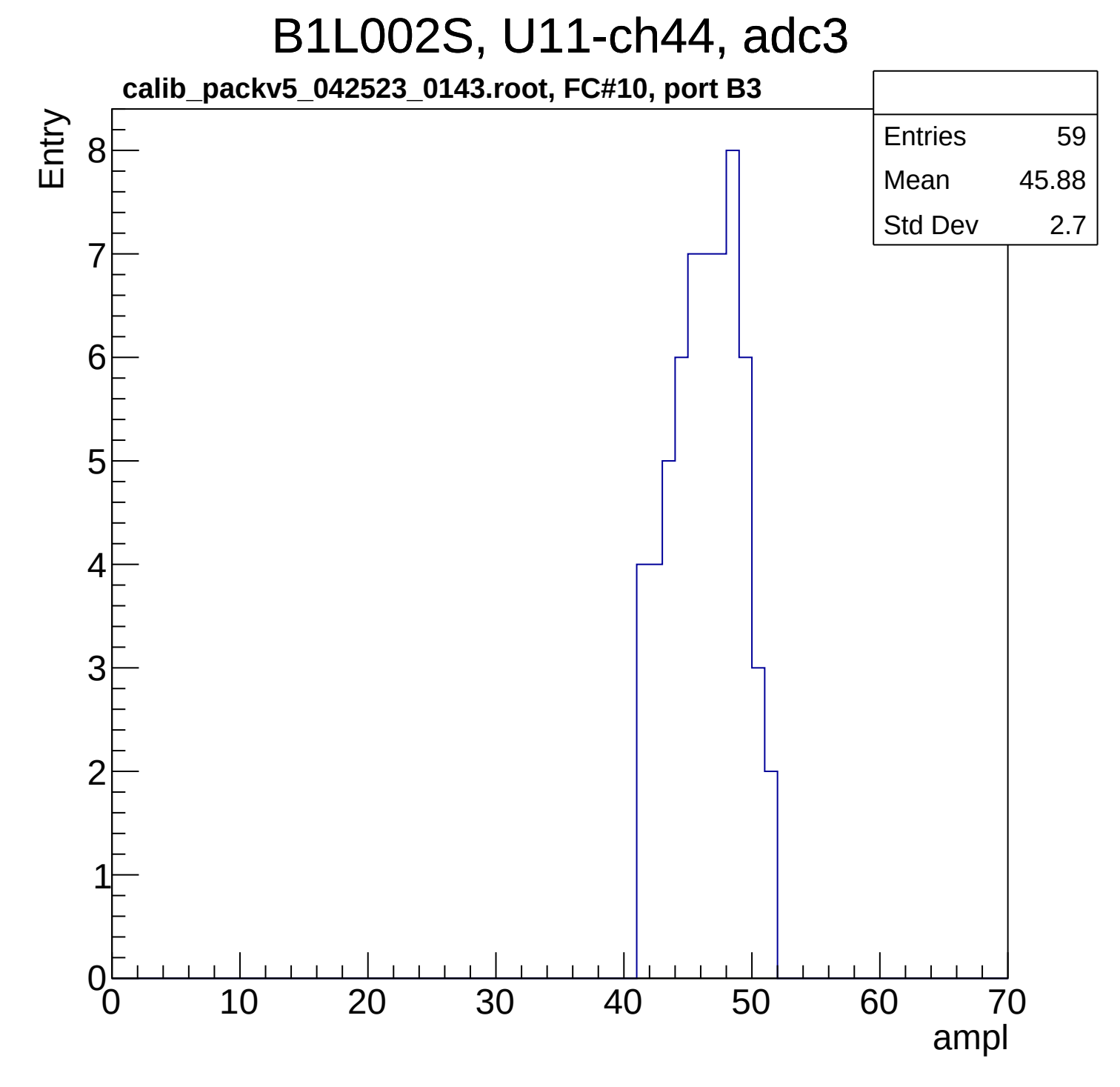

B1L002S, U11-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 31.73
Std Dev 2.954
Gaus mean : 32.9781
Gaus Width: 2.7115
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 38.49
Std Dev 3.354
Gaus mean : 39.3624
Gaus Width: 3.6178
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.88
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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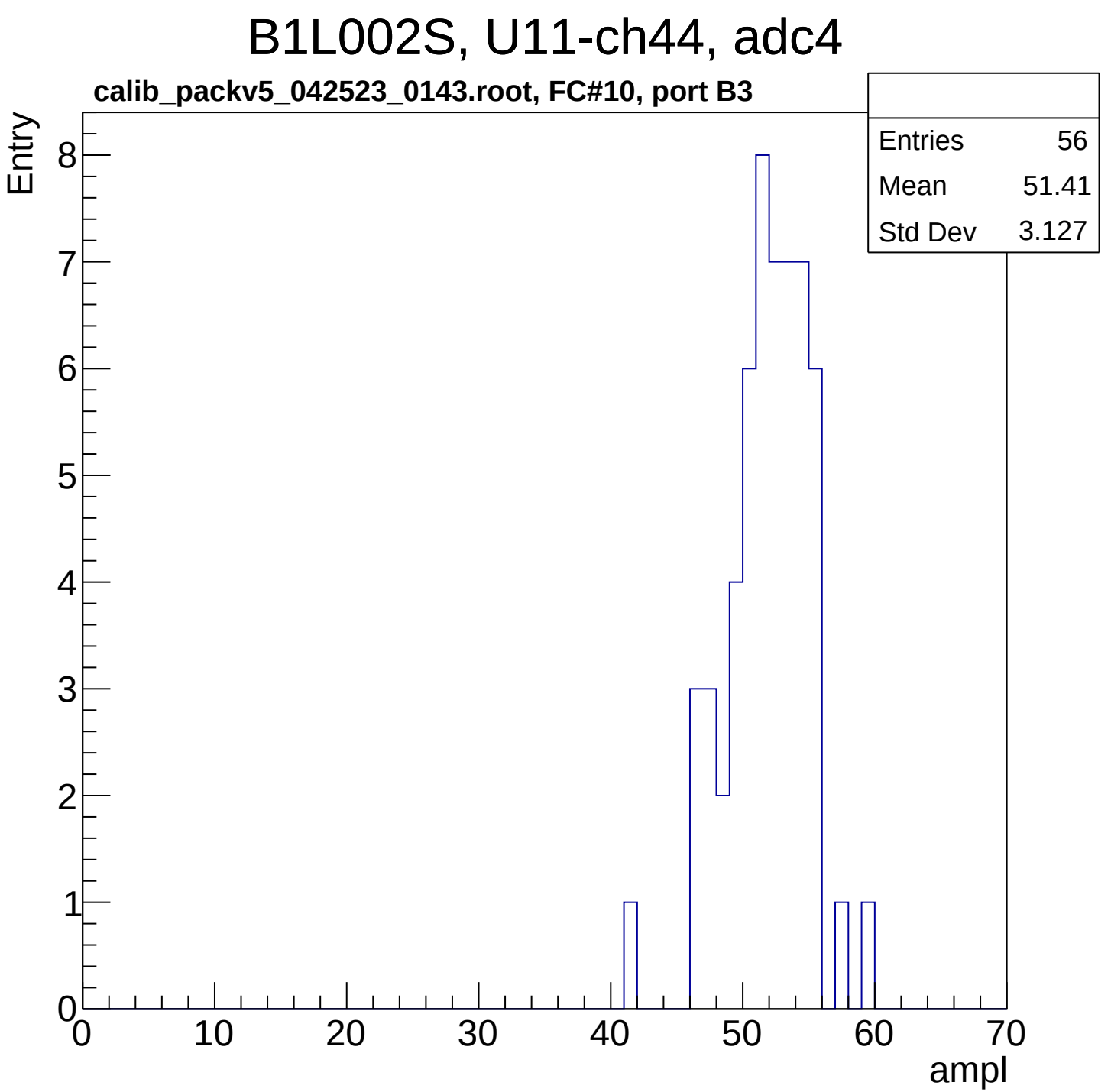
B1L002S, U11-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.41
Std Dev 3.127
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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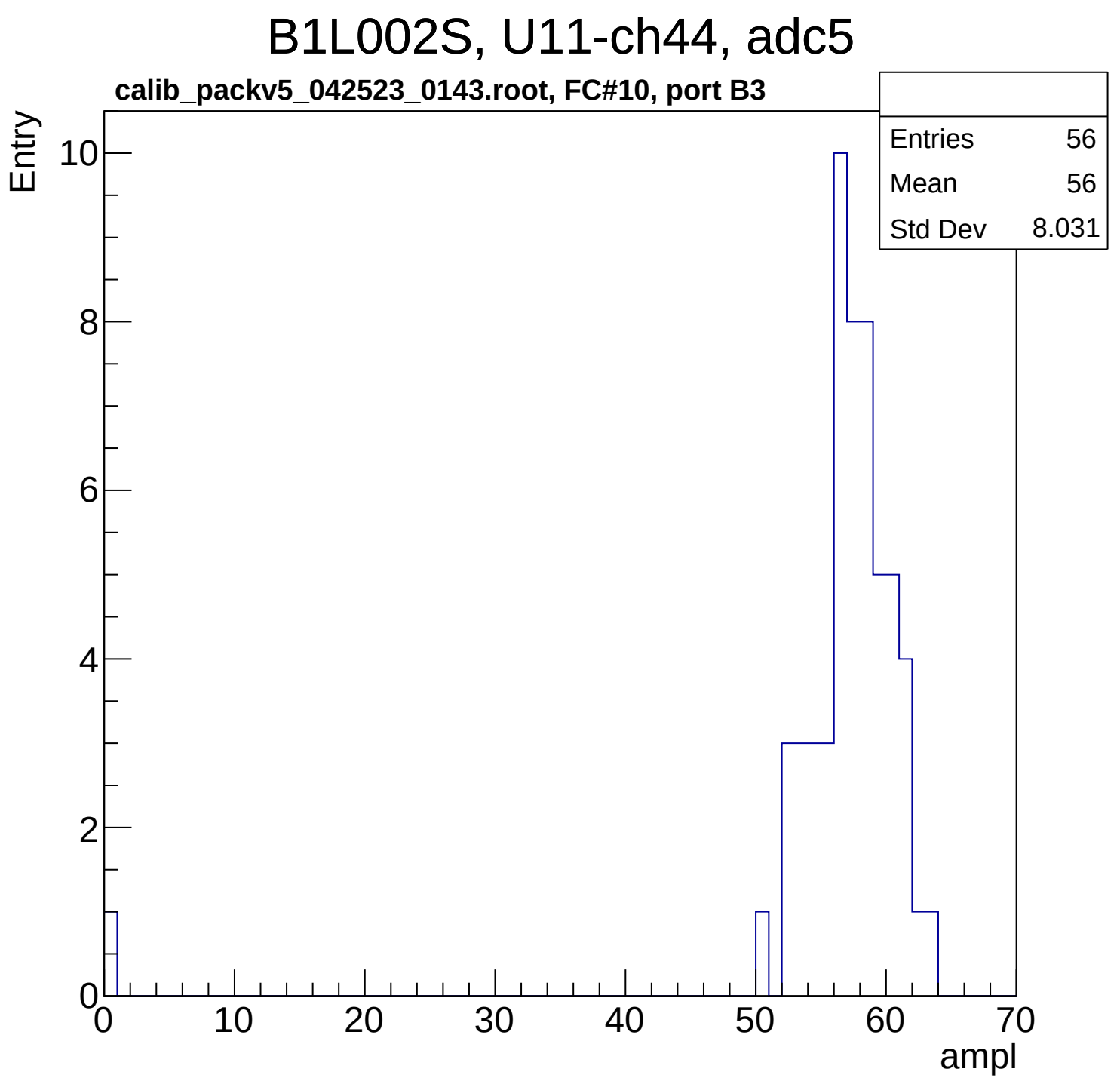
B1L002S, U11-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 56
Std Dev 8.031
Entry
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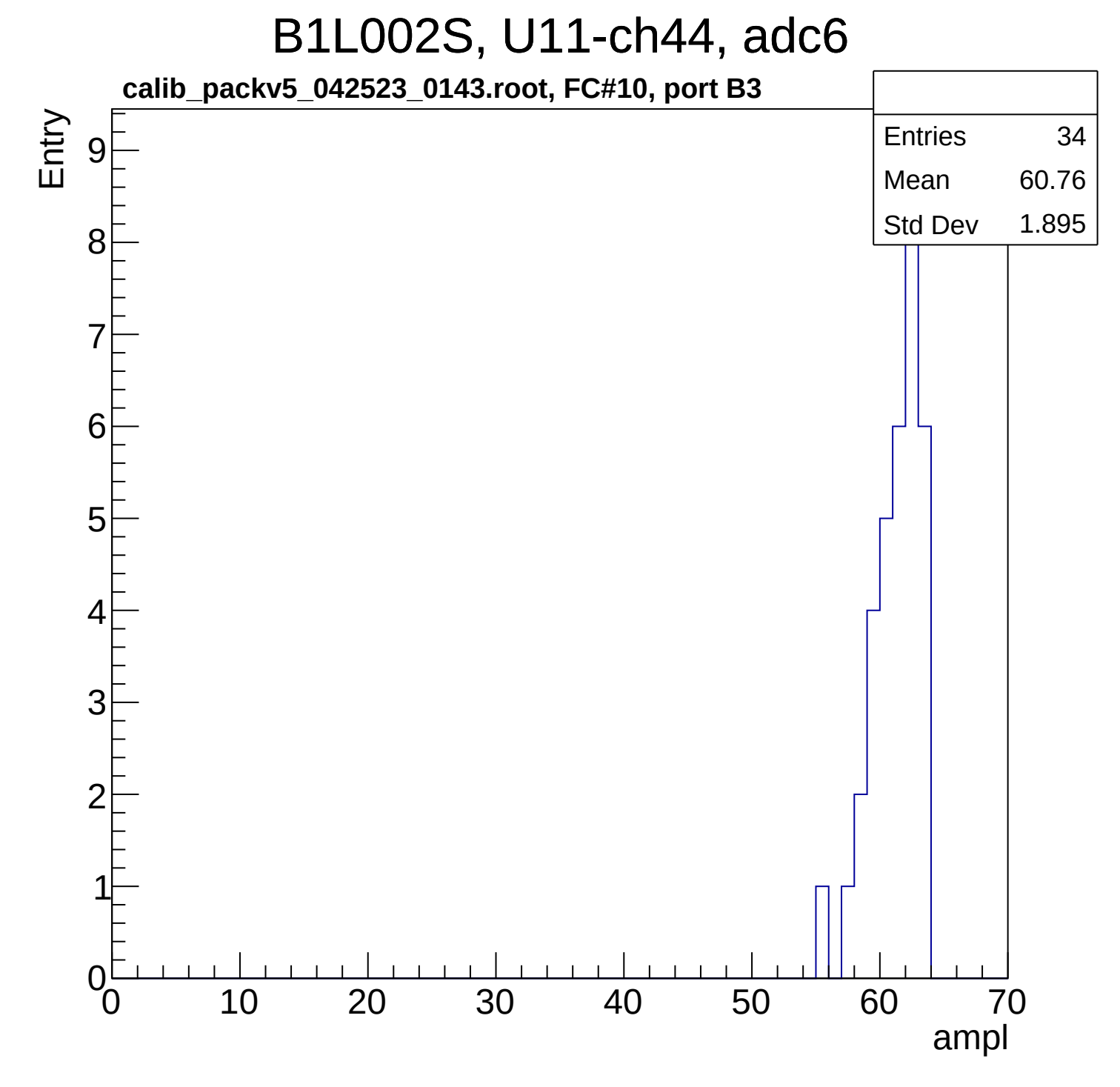
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 60.76
Std Dev 1.895
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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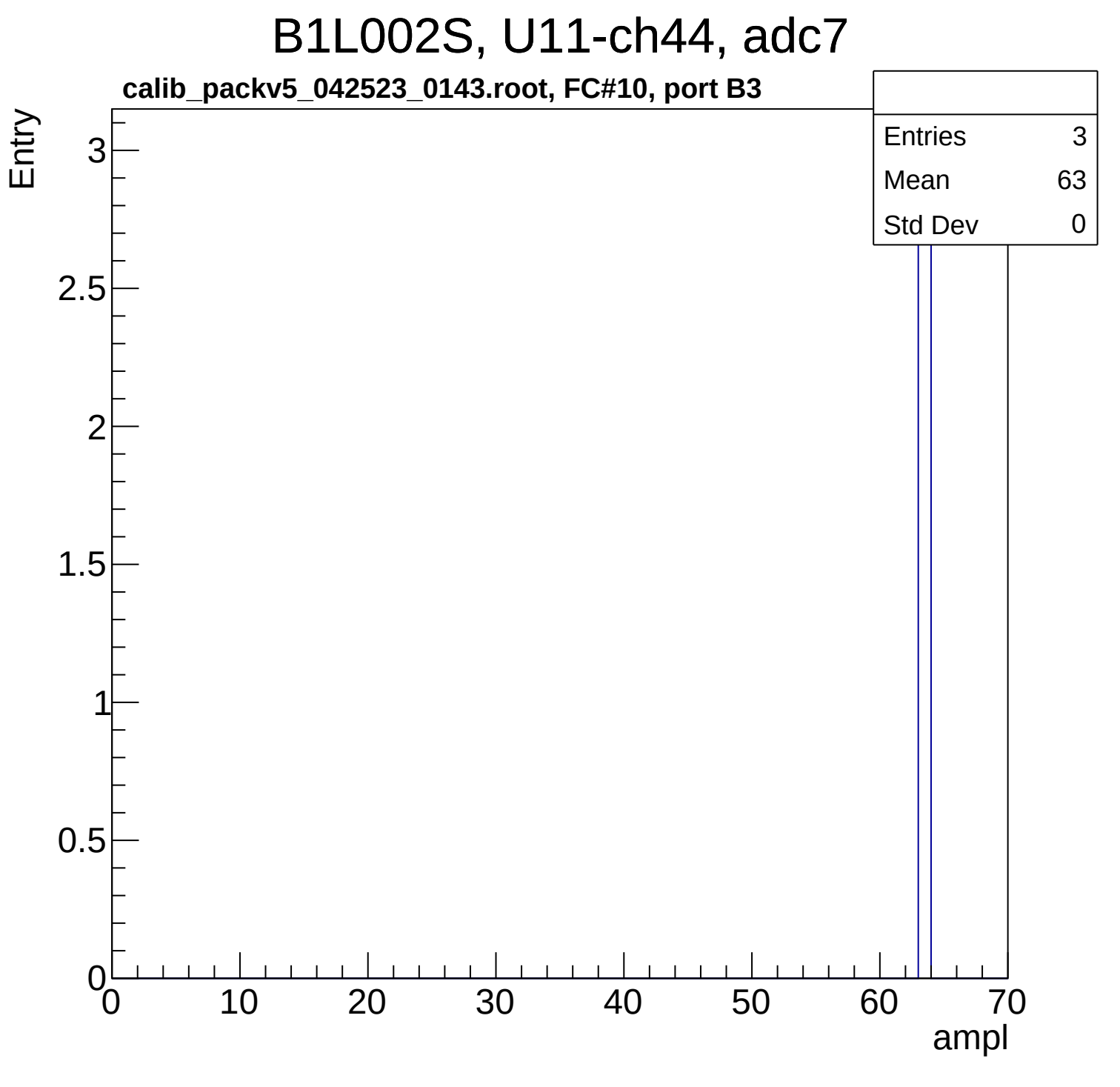
B1L002S, U11-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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1
1.5
2
2.5
3
0
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20
30
40
50
60
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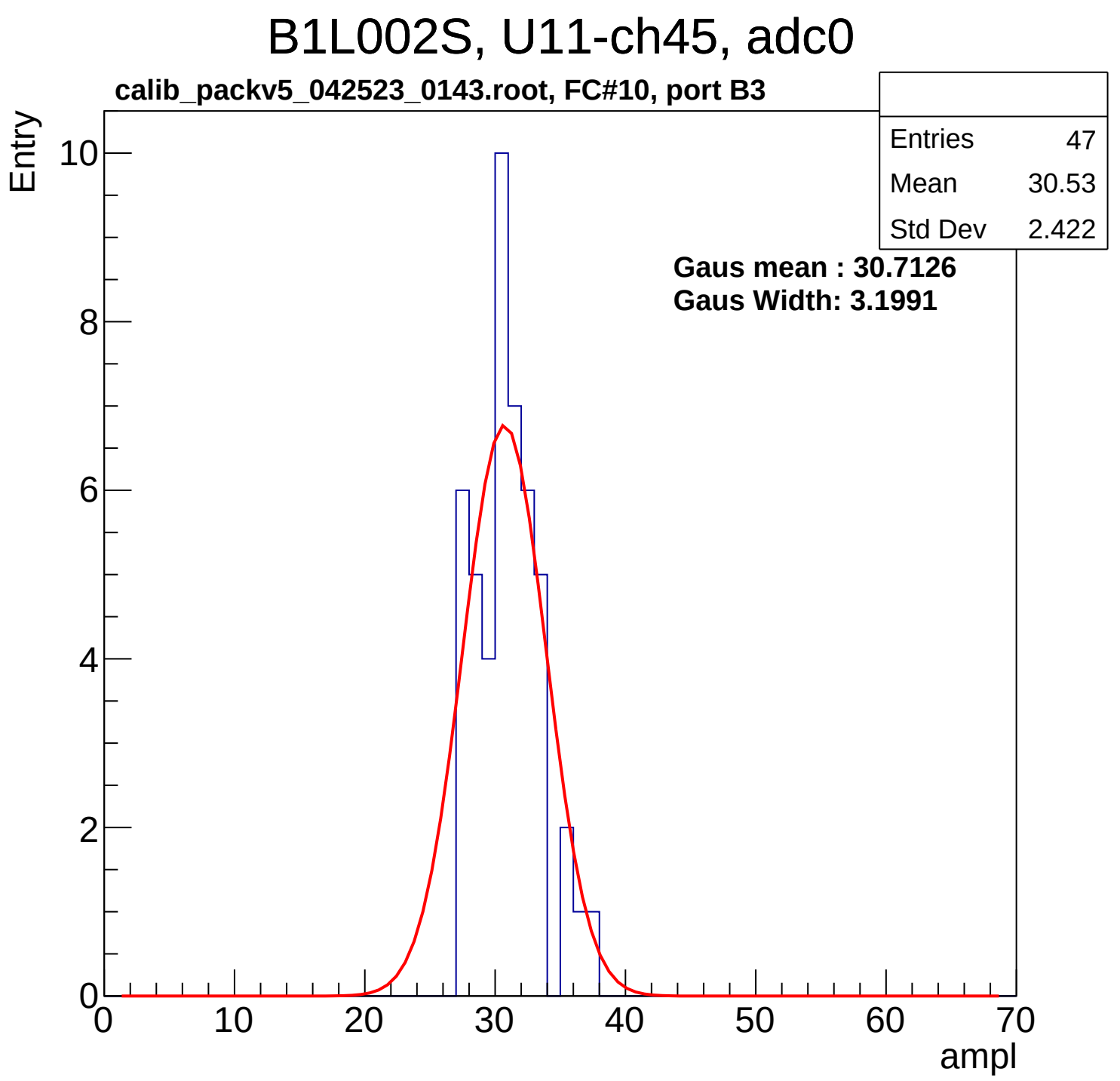
B1L002S, U11-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 30.53
Std Dev 2.422
Gaus mean : 30.7126
Gaus Width: 3.1991
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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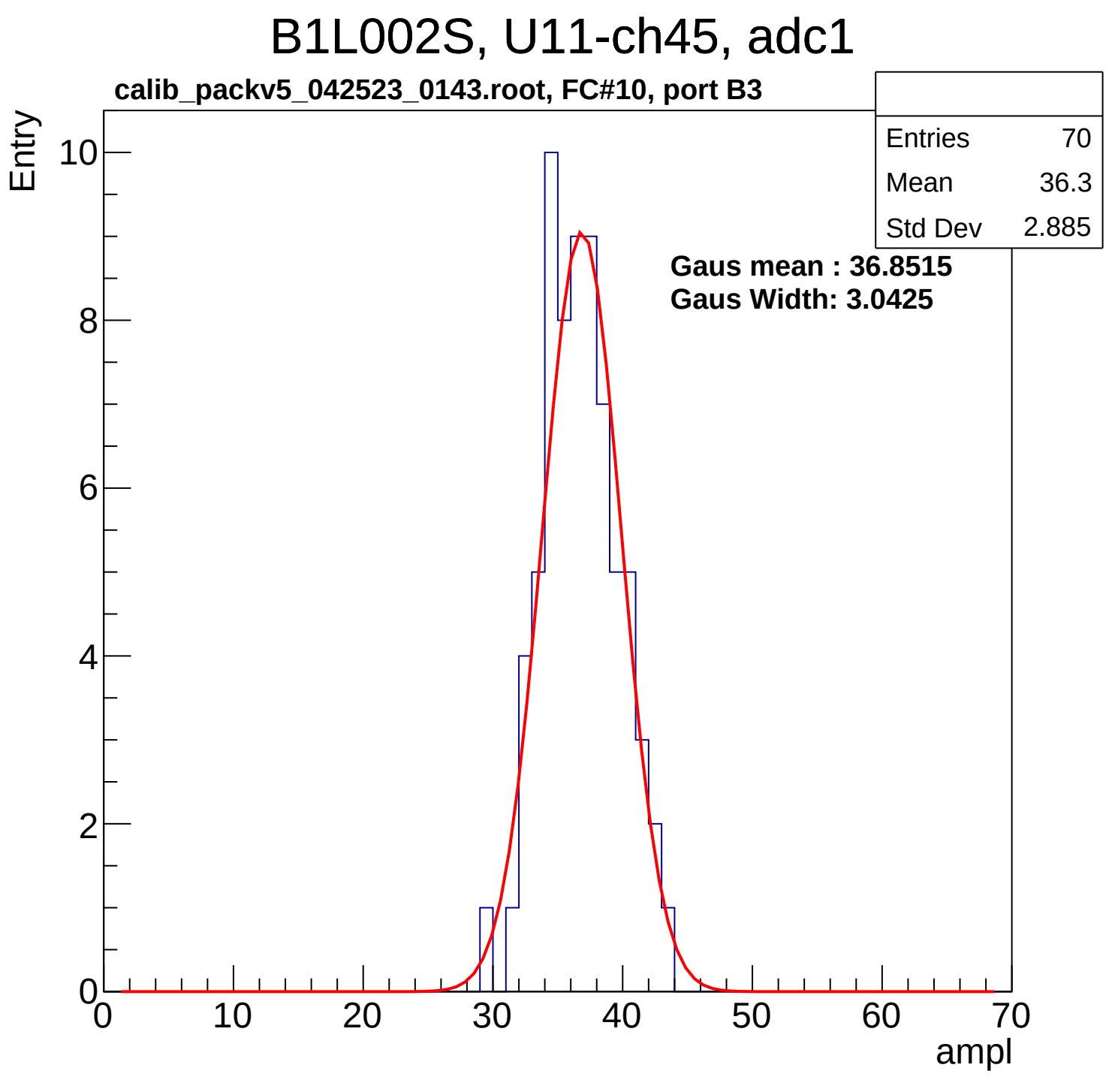
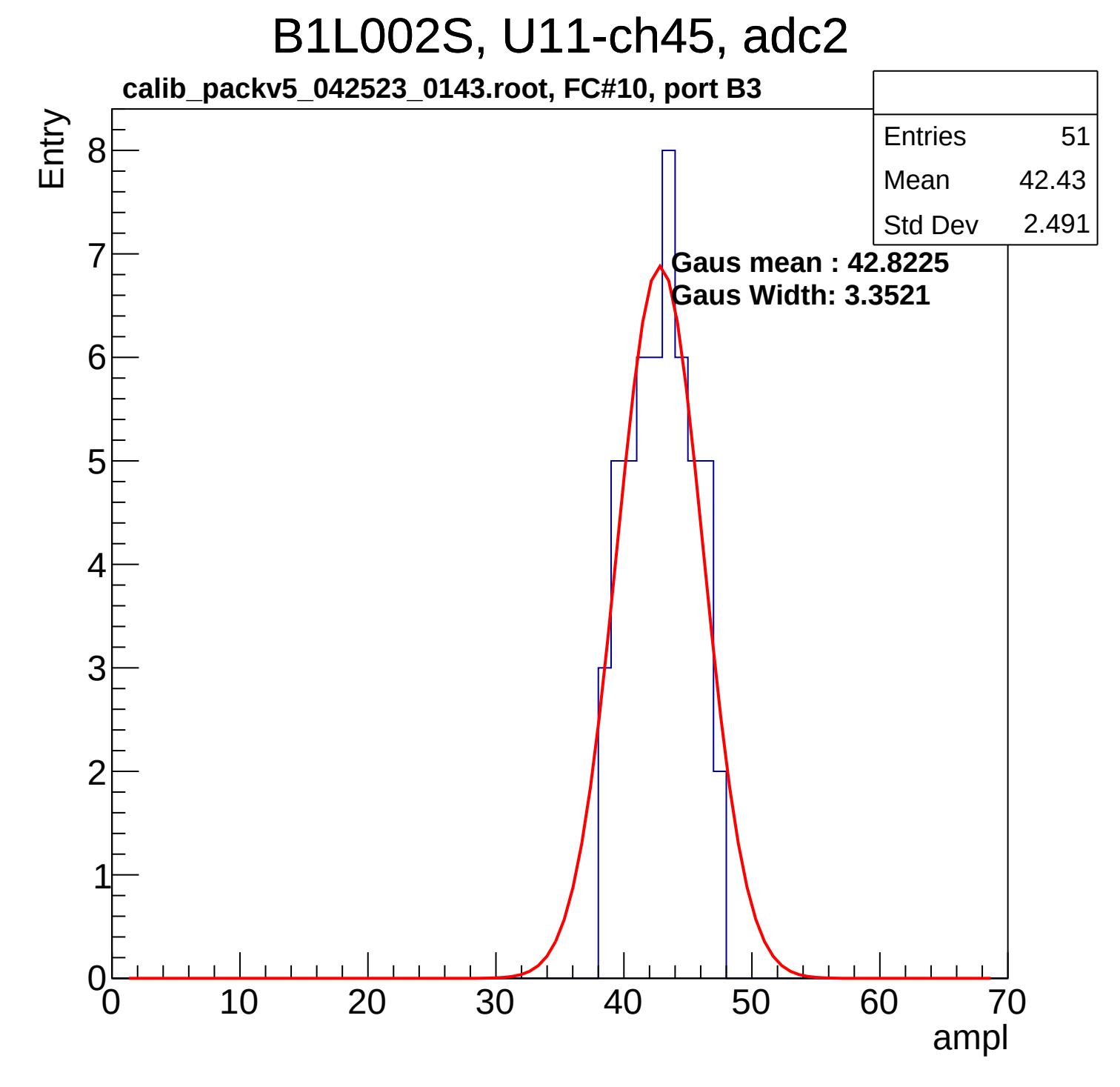

B1L002S, U11-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 36.3
Std Dev 2.885
Gaus mean : 36.8515
Gaus Width: 3.0425
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 42.43
Std Dev 2.491
Gaus mean : 42.8225
Gaus Width: 3.3521
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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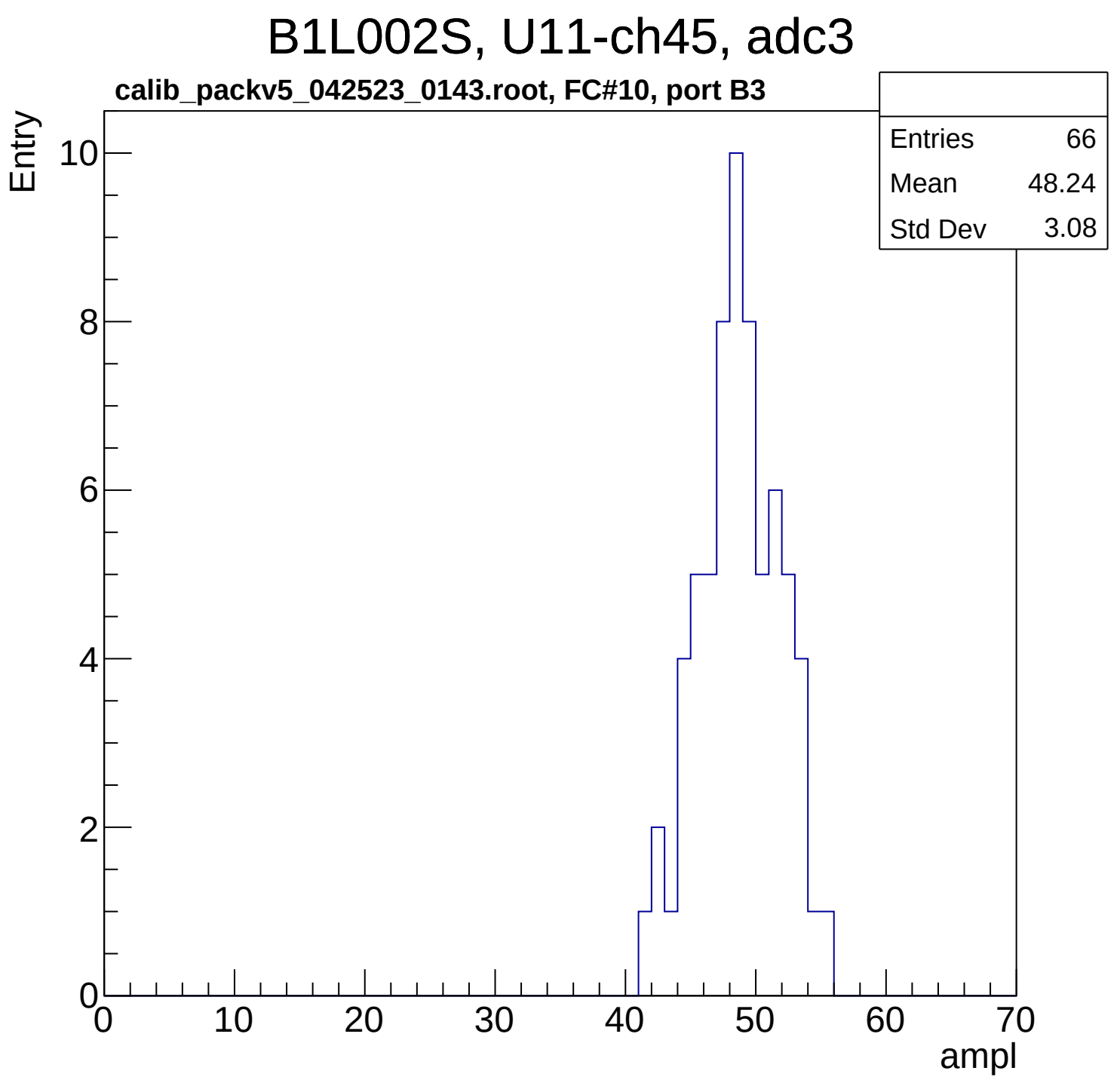
B1L002S, U11-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.24
Std Dev 3.08
Entry
0
2
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6
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10
0
10
20
30
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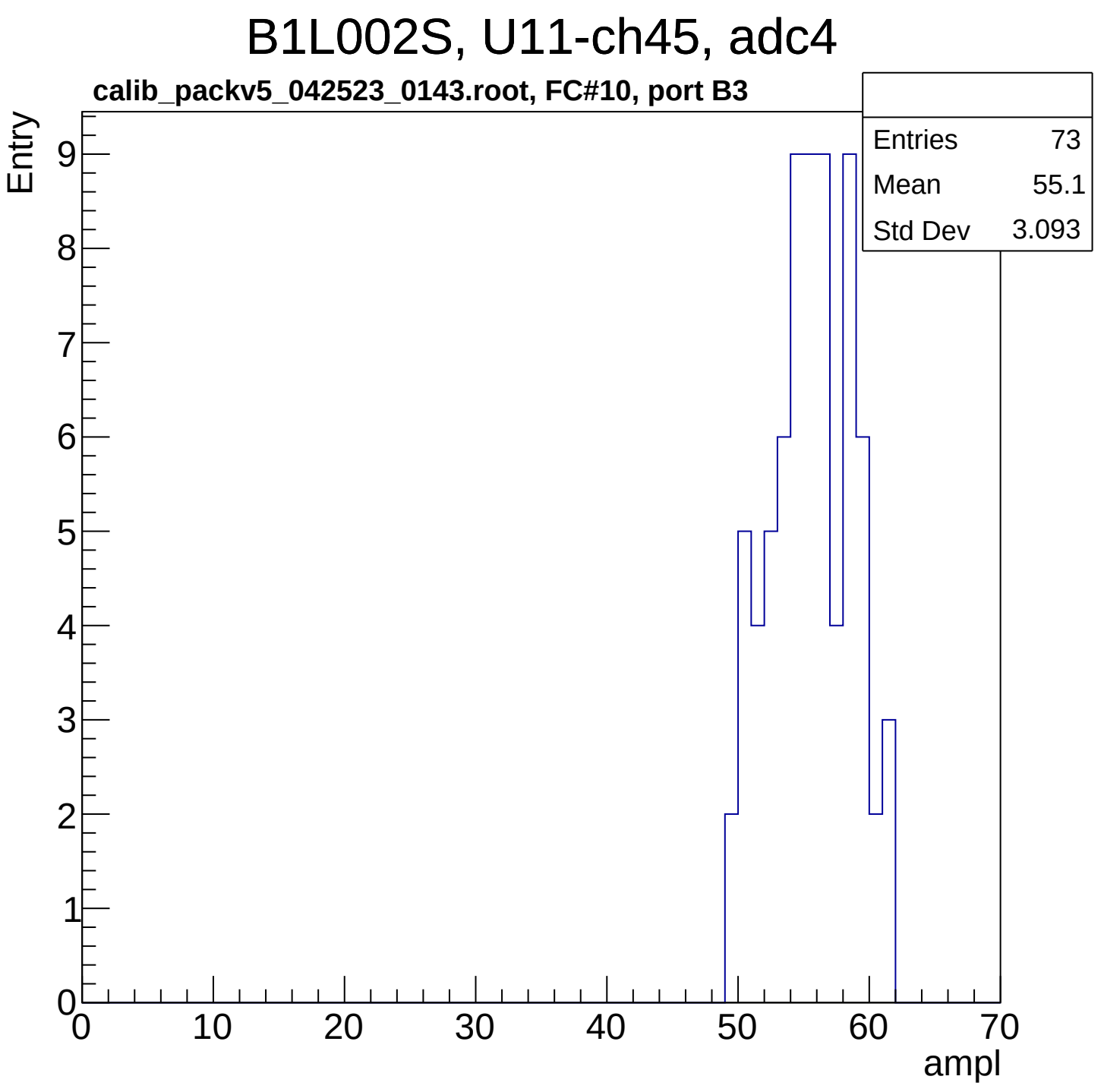
50
60
70
ampl


B1L002S, U11-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 55.1
Std Dev 3.093
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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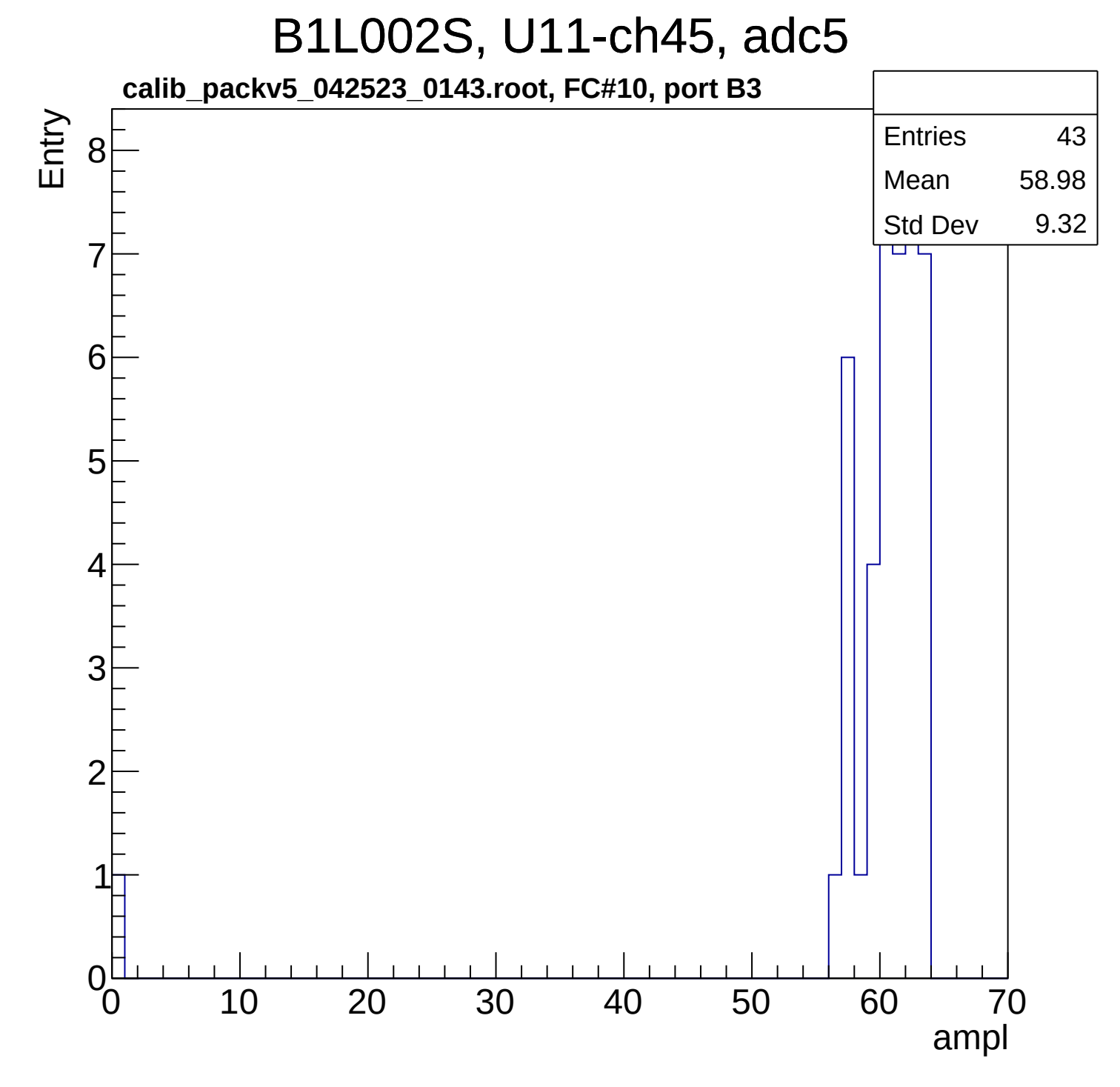
B1L002S, U11-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 58.98
Std Dev 9.32
Entry
ampl
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3
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5
6
7
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0
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20
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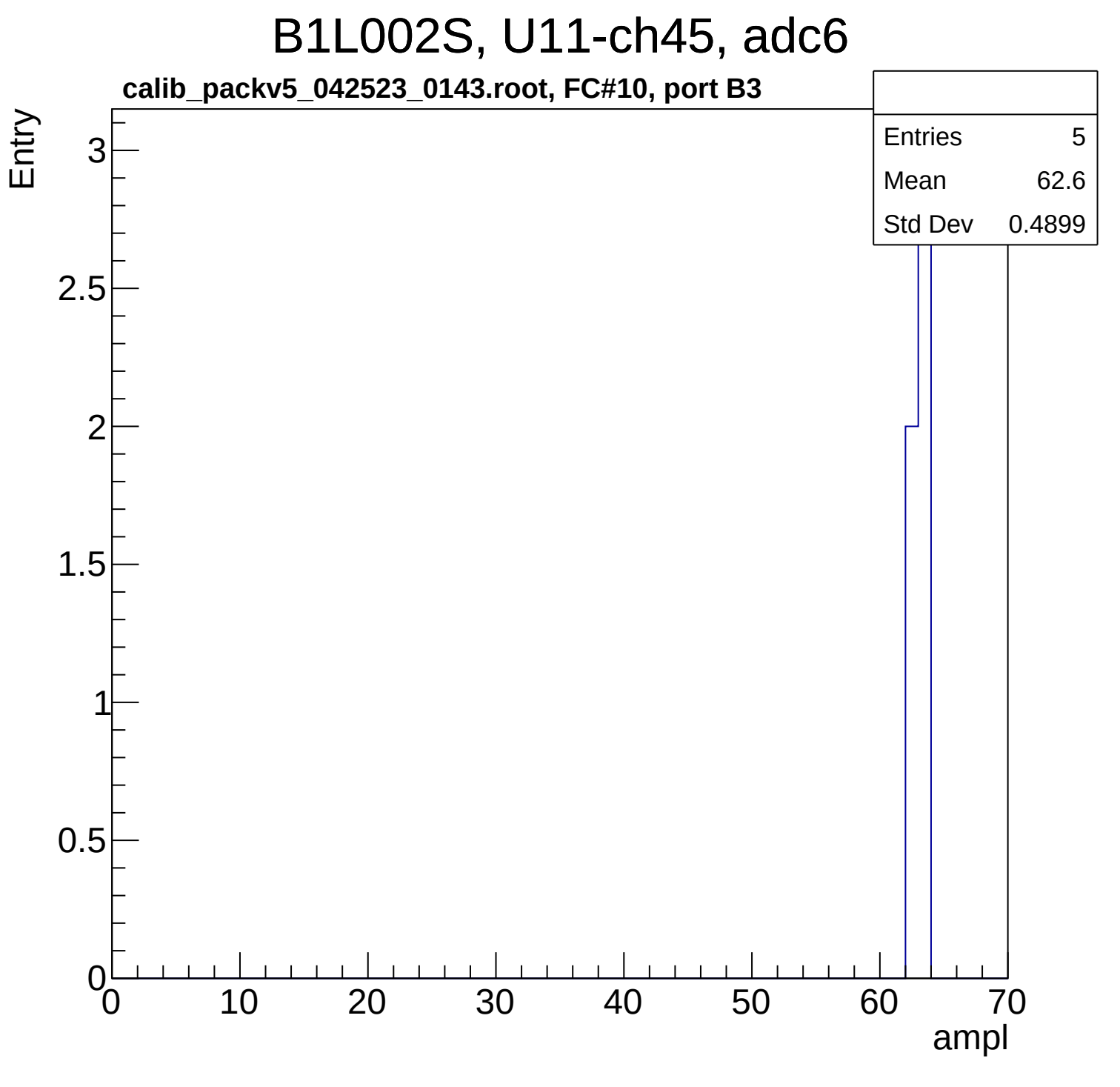
B1L002S, U11-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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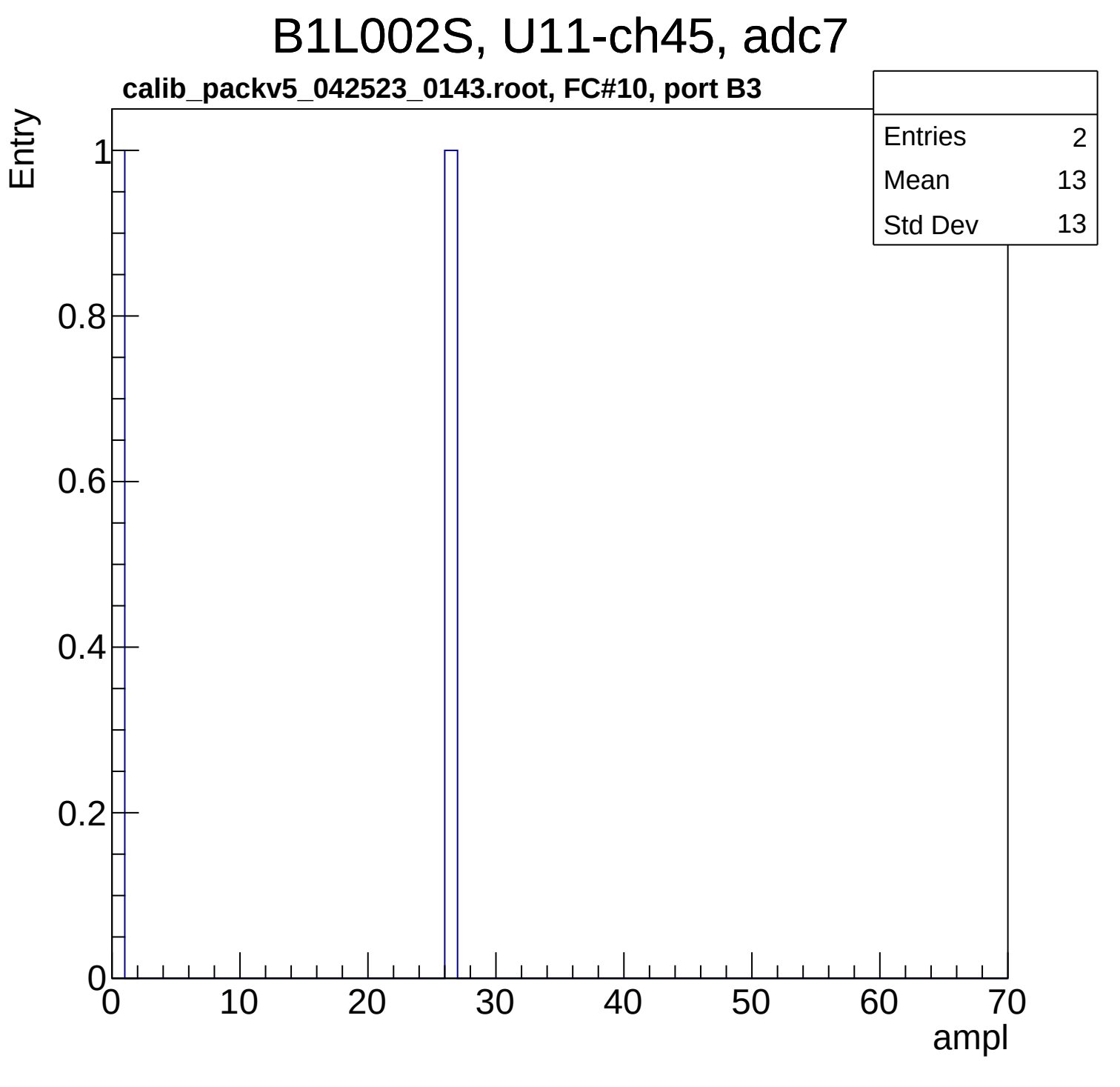
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 13
Std Dev 13
Entry
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0.6
0.4
0.2
0
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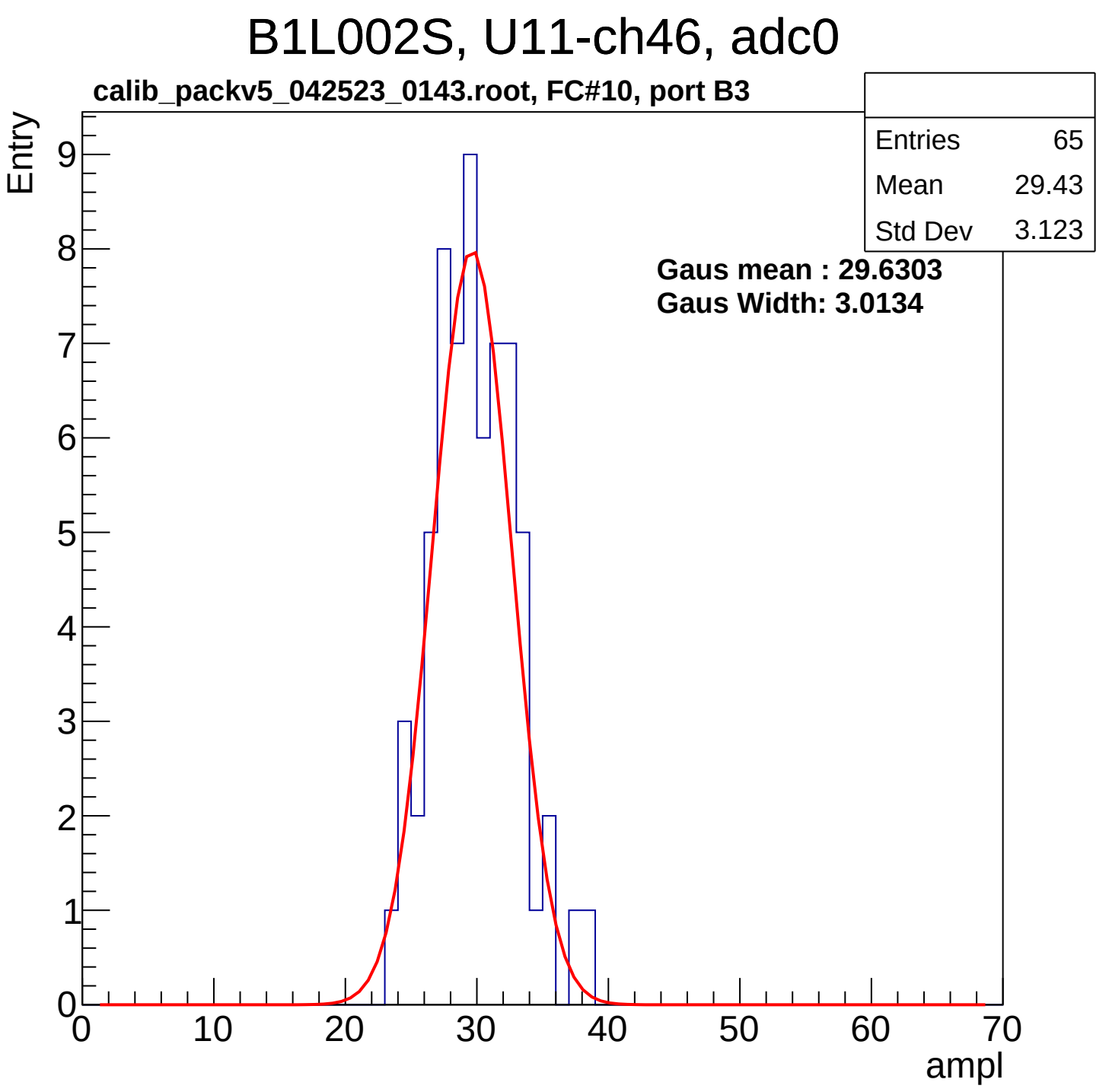
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.43
Std Dev 3.123
Gaus mean : 29.6303
Gaus Width: 3.0134
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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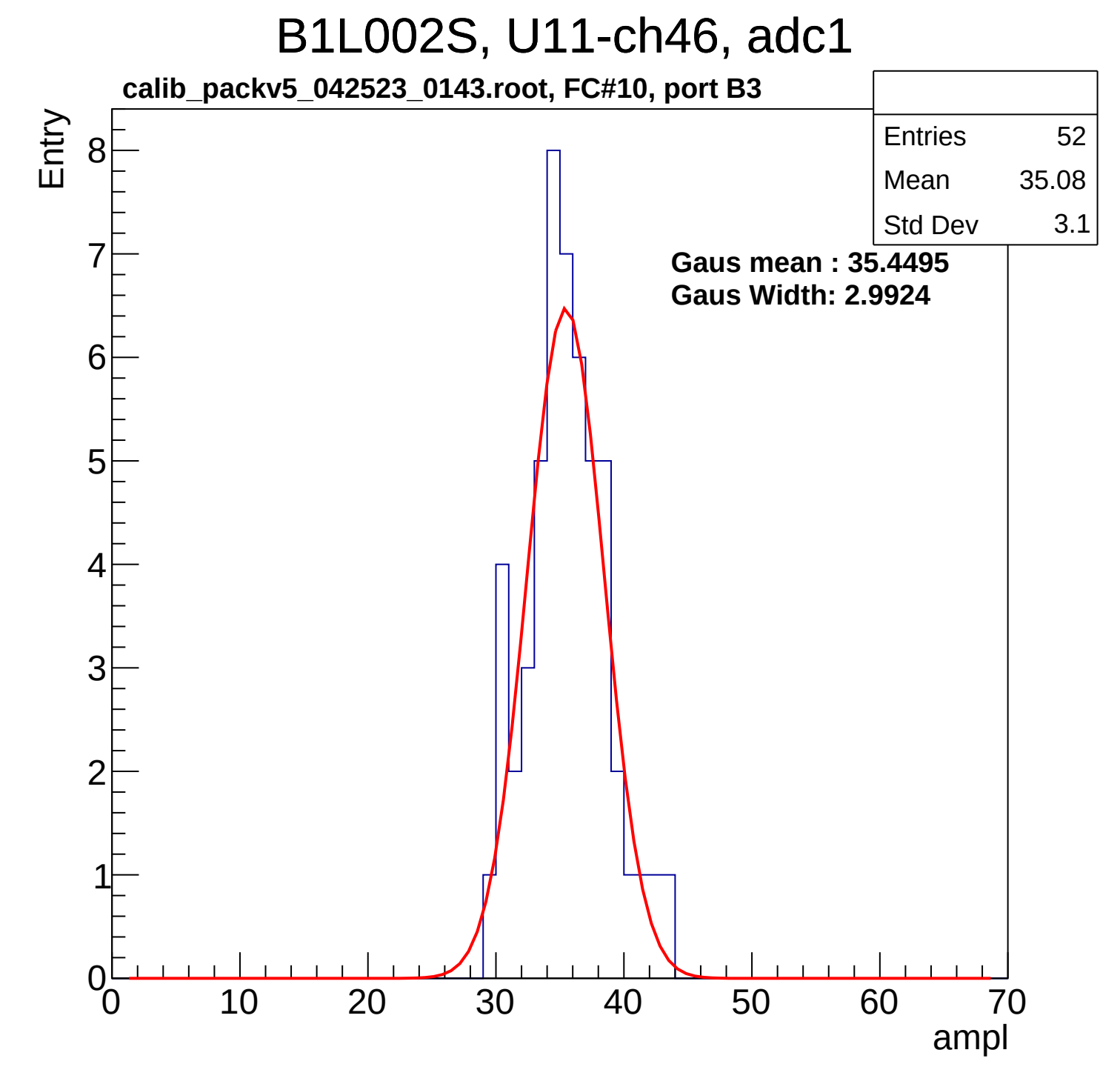
B1L002S, U11-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.08
Std Dev 3.1
Gaus mean : 35.4495
Gaus Width: 2.9924
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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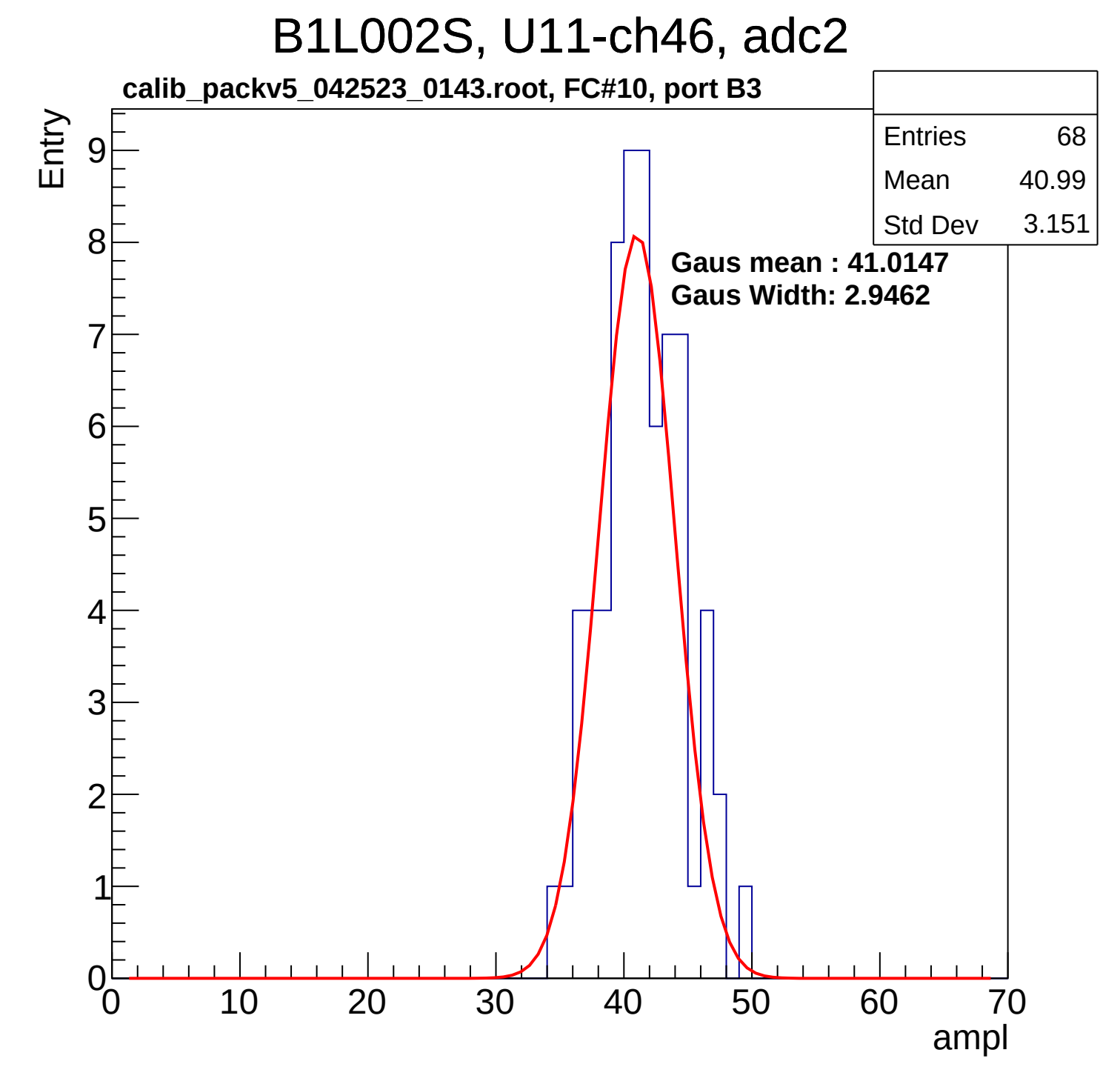
B1L002S, U11-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 40.99
Std Dev 3.151
Gaus mean : 41.0147
Gaus Width: 2.9462
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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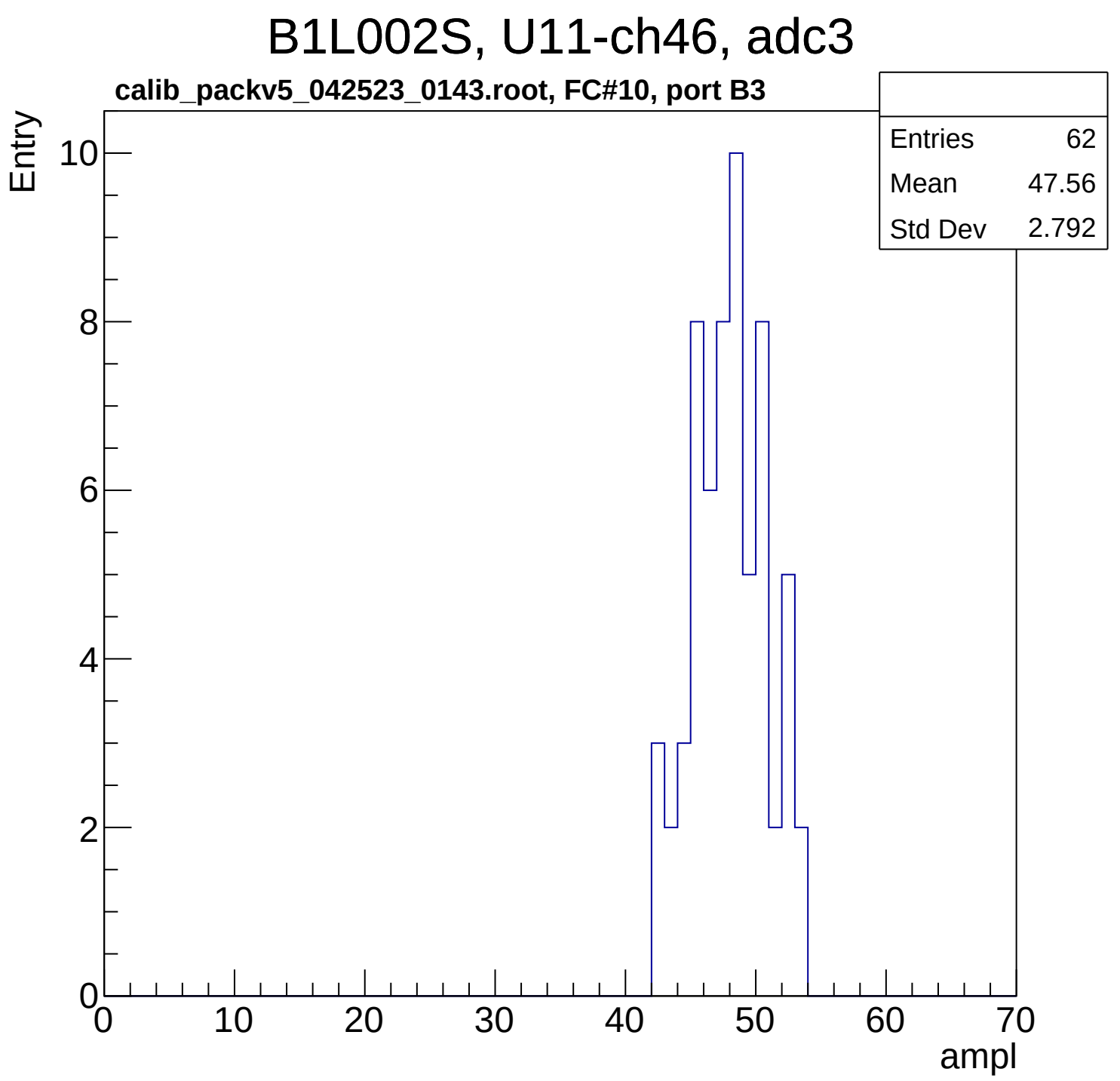
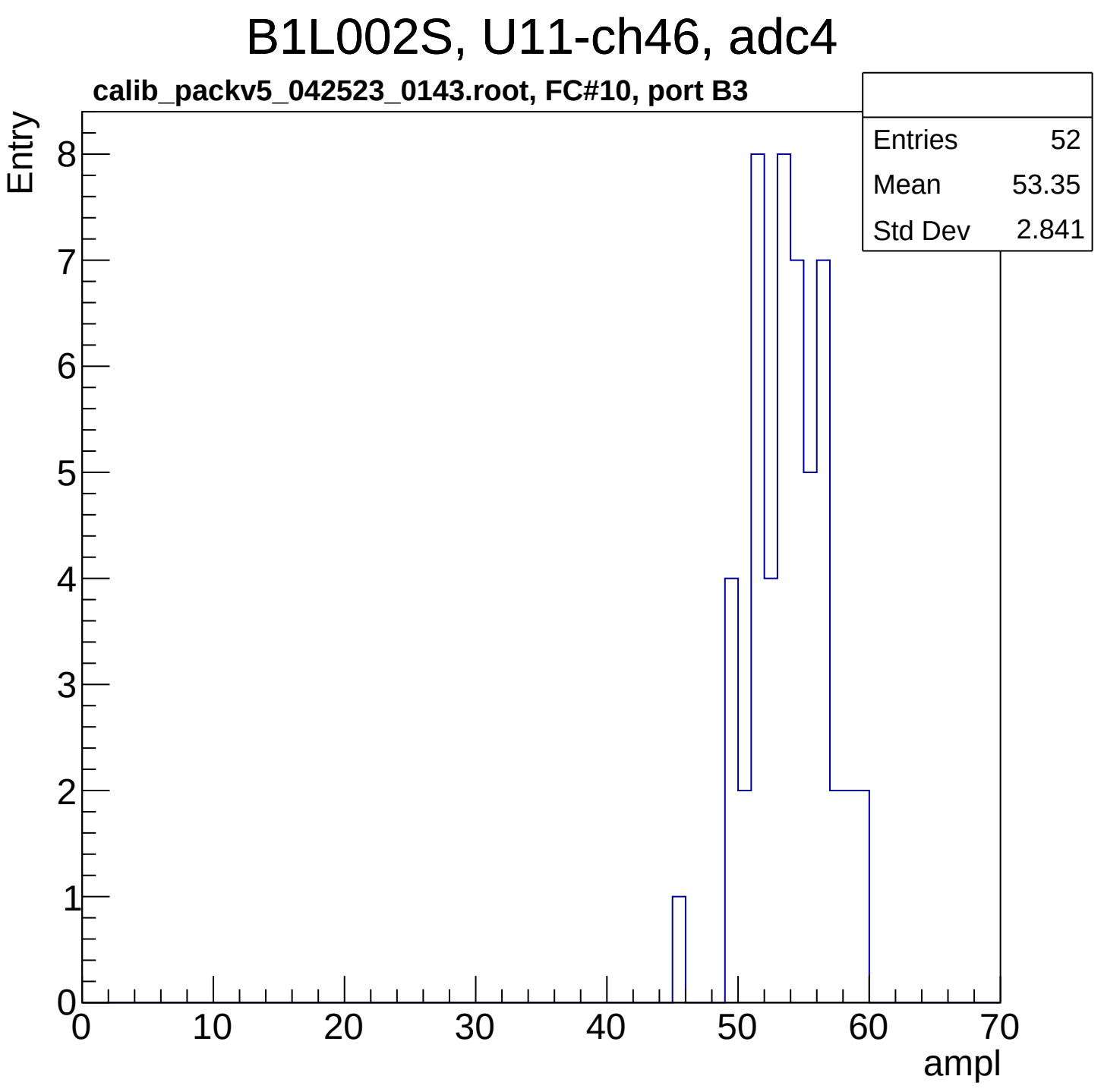

B1L002S, U11-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.56
Std Dev 2.792
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.35
Std Dev 2.841
Entry
ampl
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1
2
3
4
5
6
7
8
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20
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40
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60
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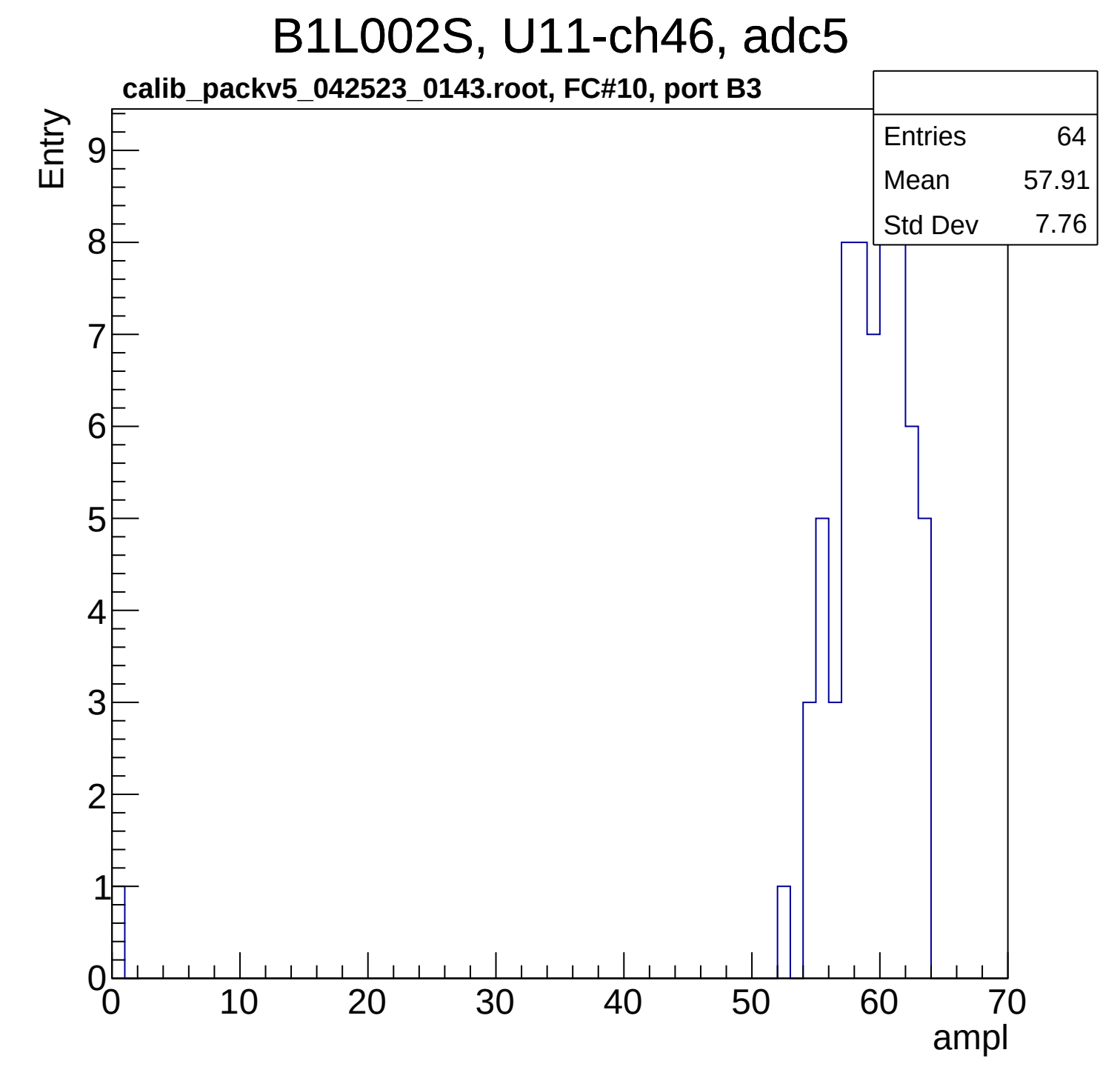
B1L002S, U11-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.91
Std Dev 7.76
Entry
ampl
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3
4
5
6
7
8
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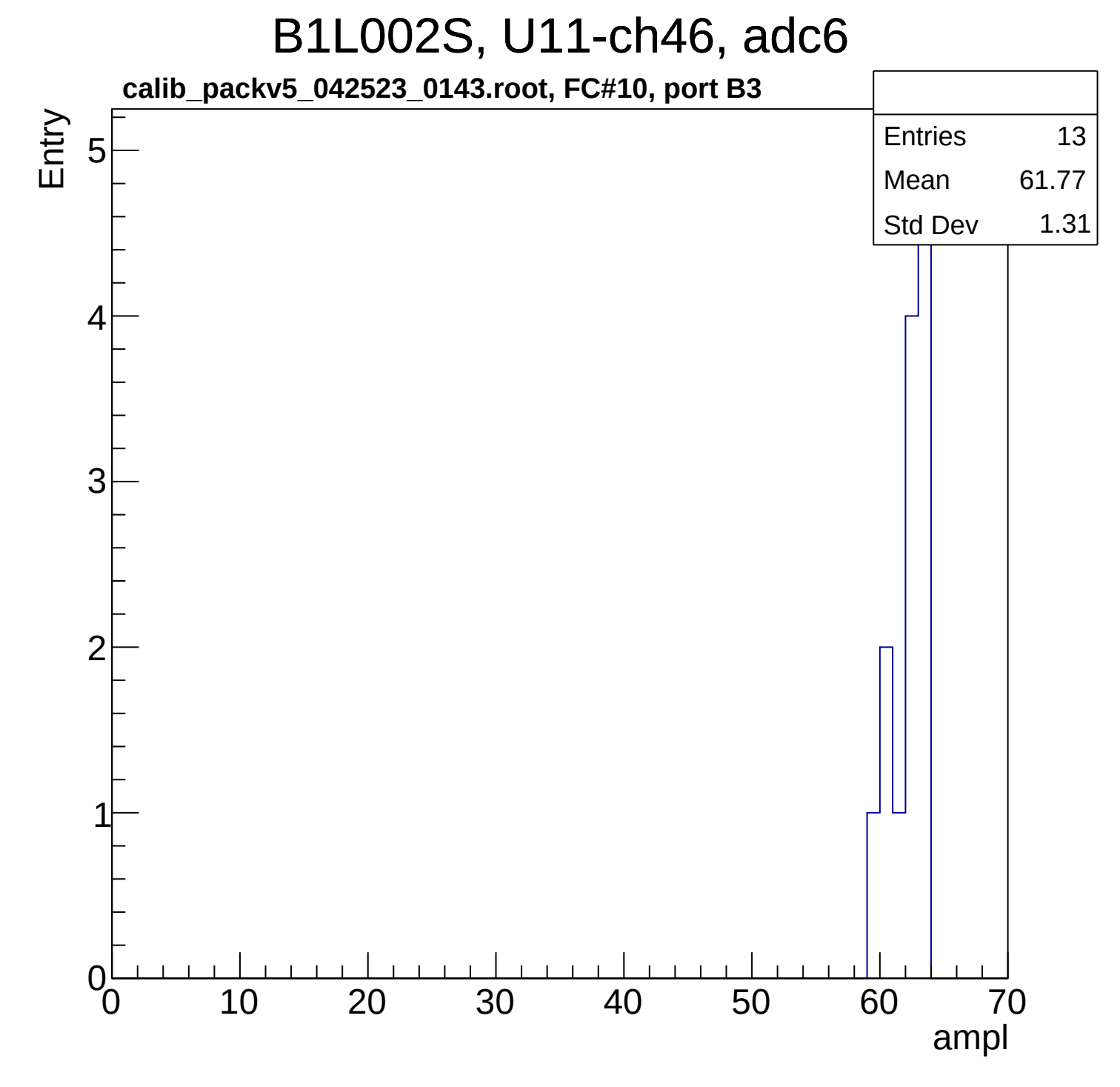
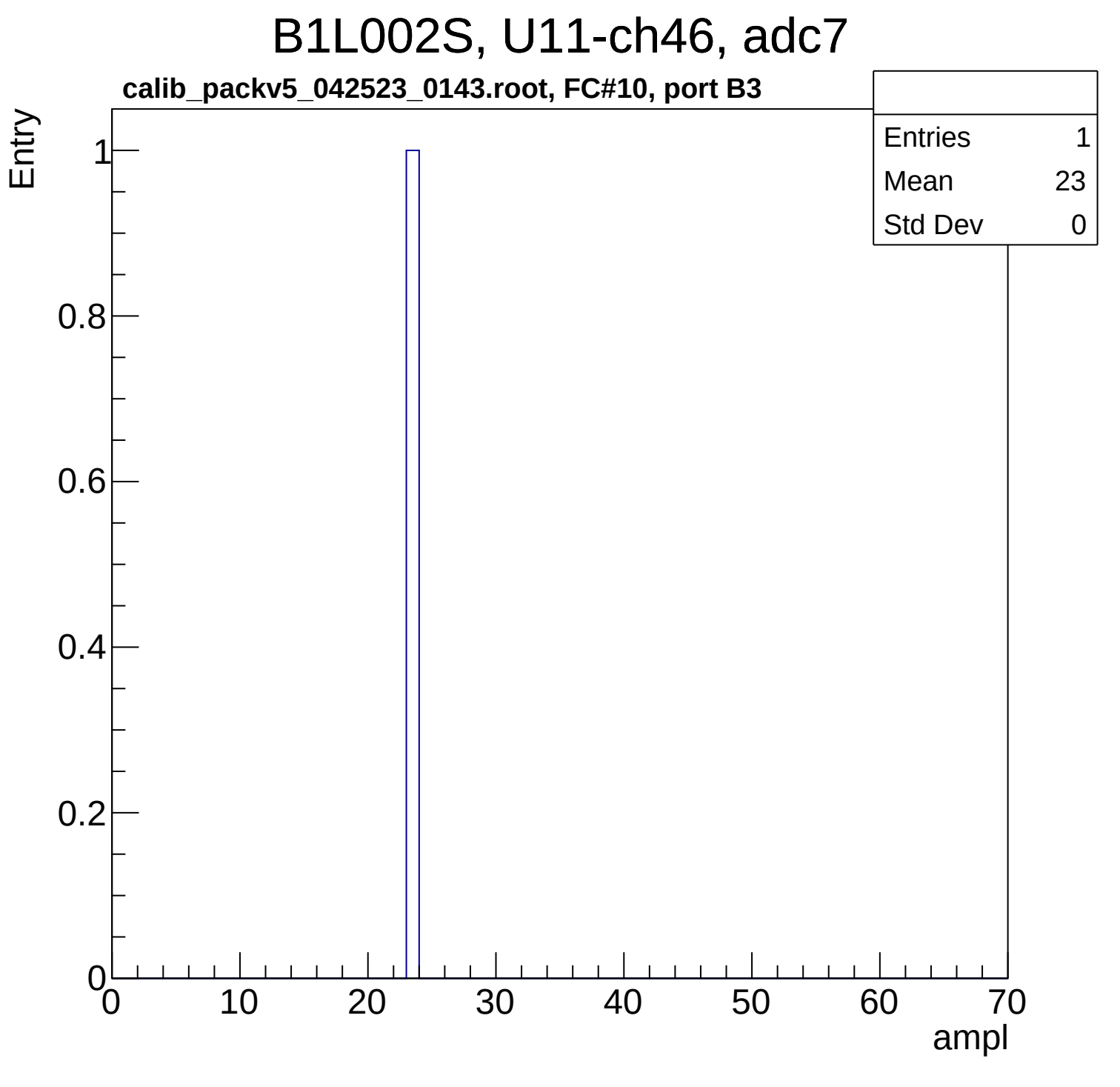

B1L002S, U11-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.77
Std Dev 1.31
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L002S, U11-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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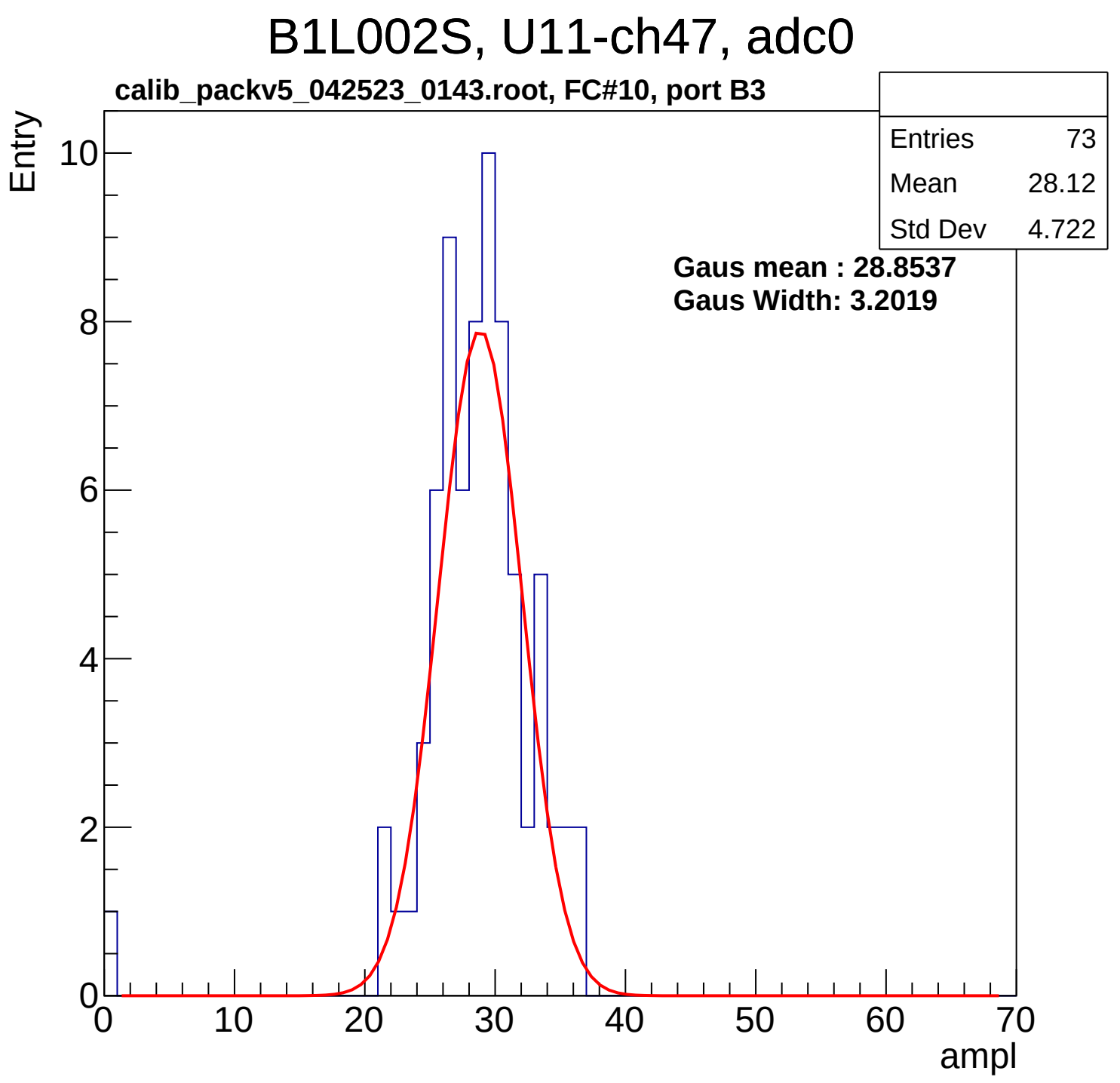
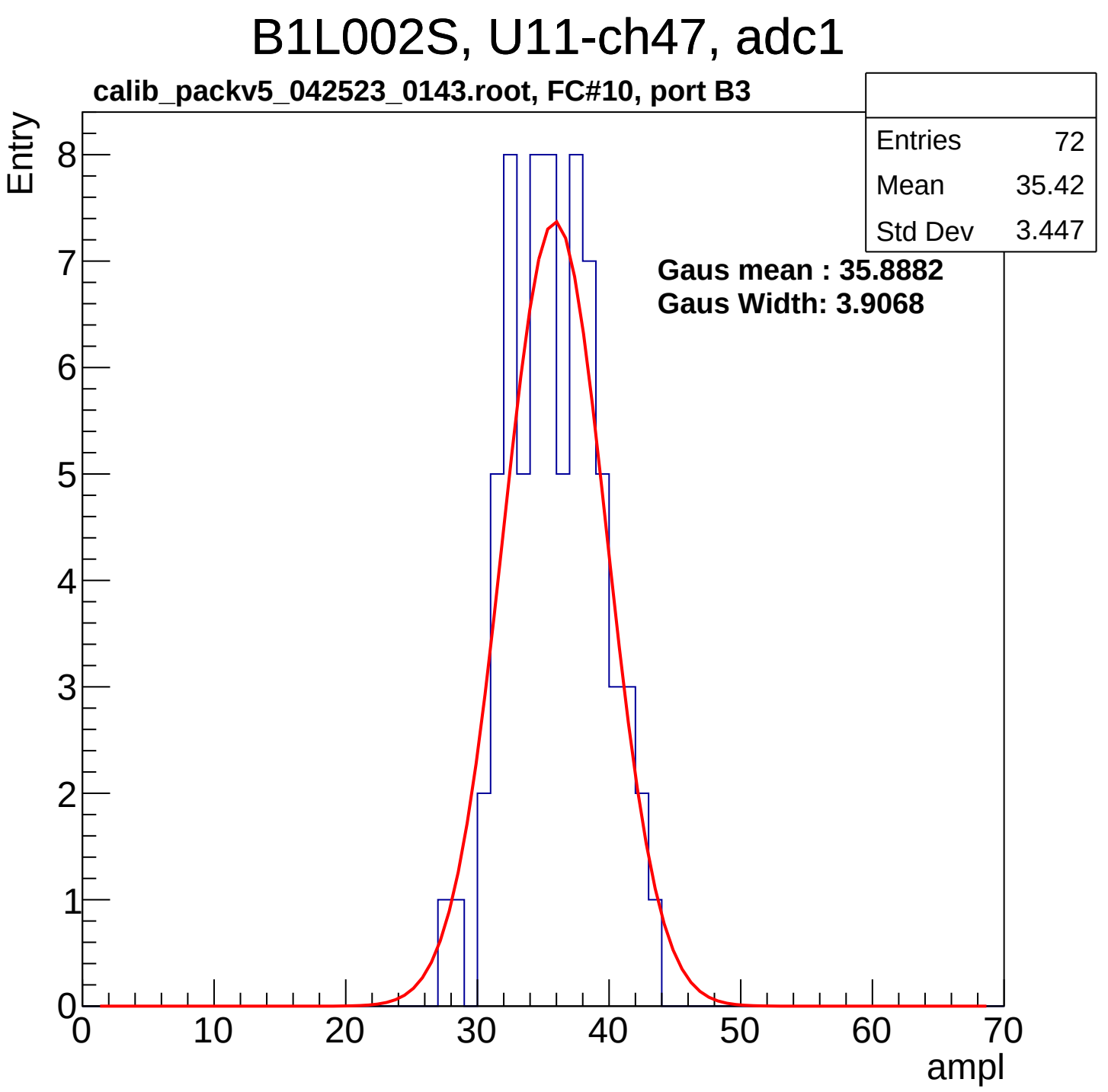
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.12
Std Dev 4.722
Gaus mean : 28.8537
Gaus Width: 3.2019
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.42
Std Dev 3.447
Gaus mean : 35.8882
Gaus Width: 3.9068
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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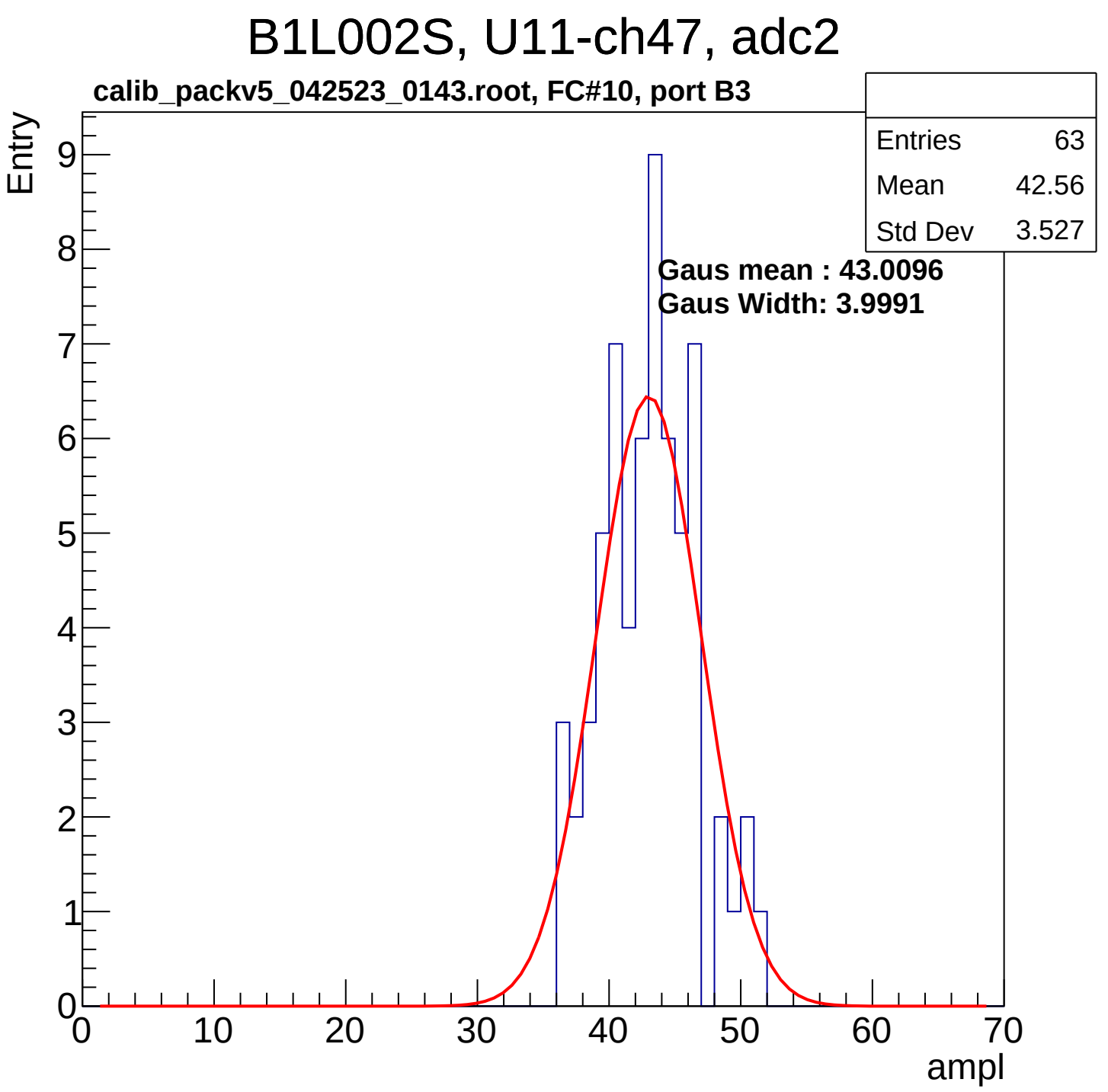
B1L002S, U11-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 42.56
Std Dev 3.527
Gaus mean : 43.0096
Gaus Width: 3.9991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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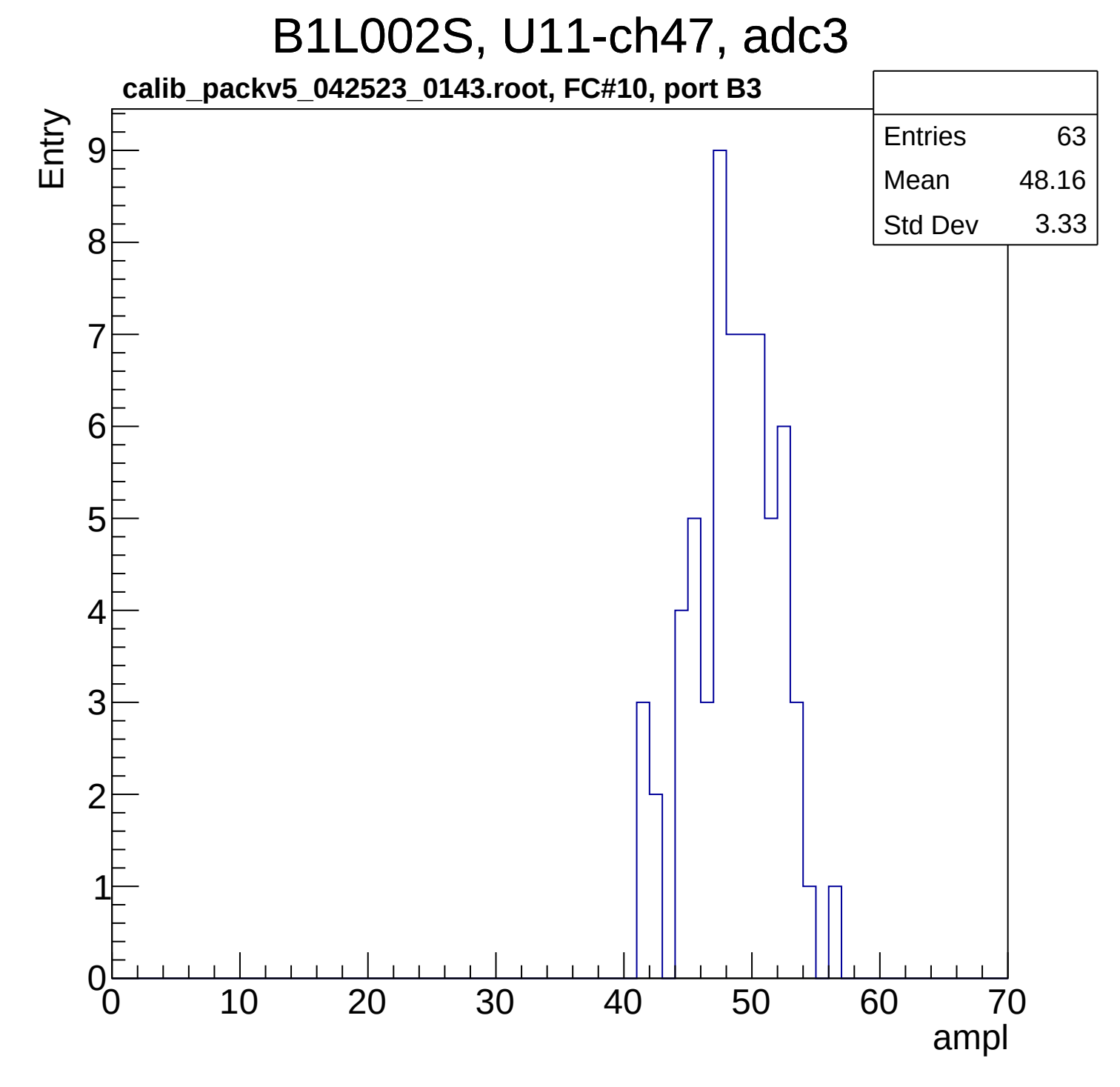
B1L002S, U11-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.16
Std Dev 3.33
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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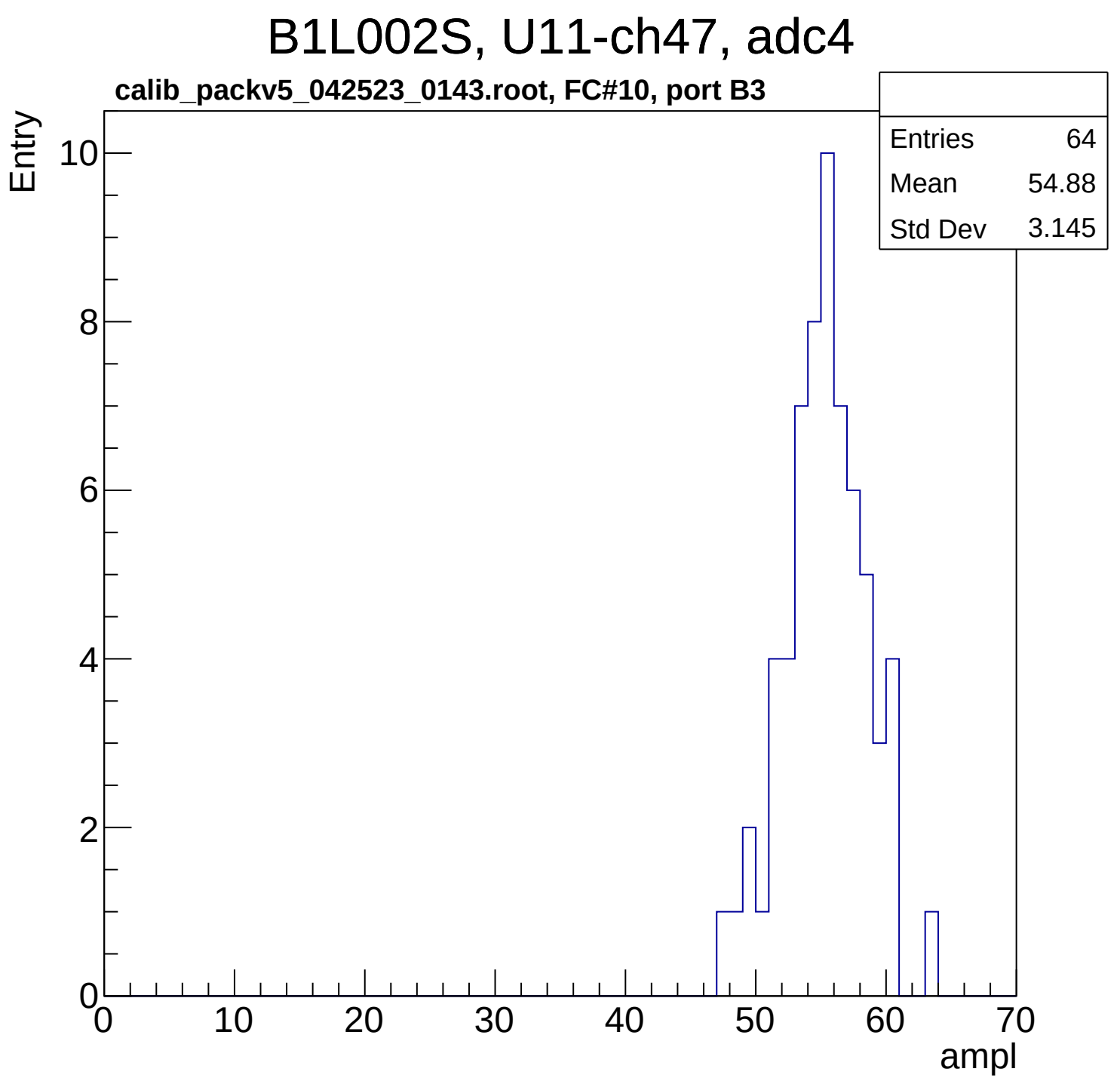
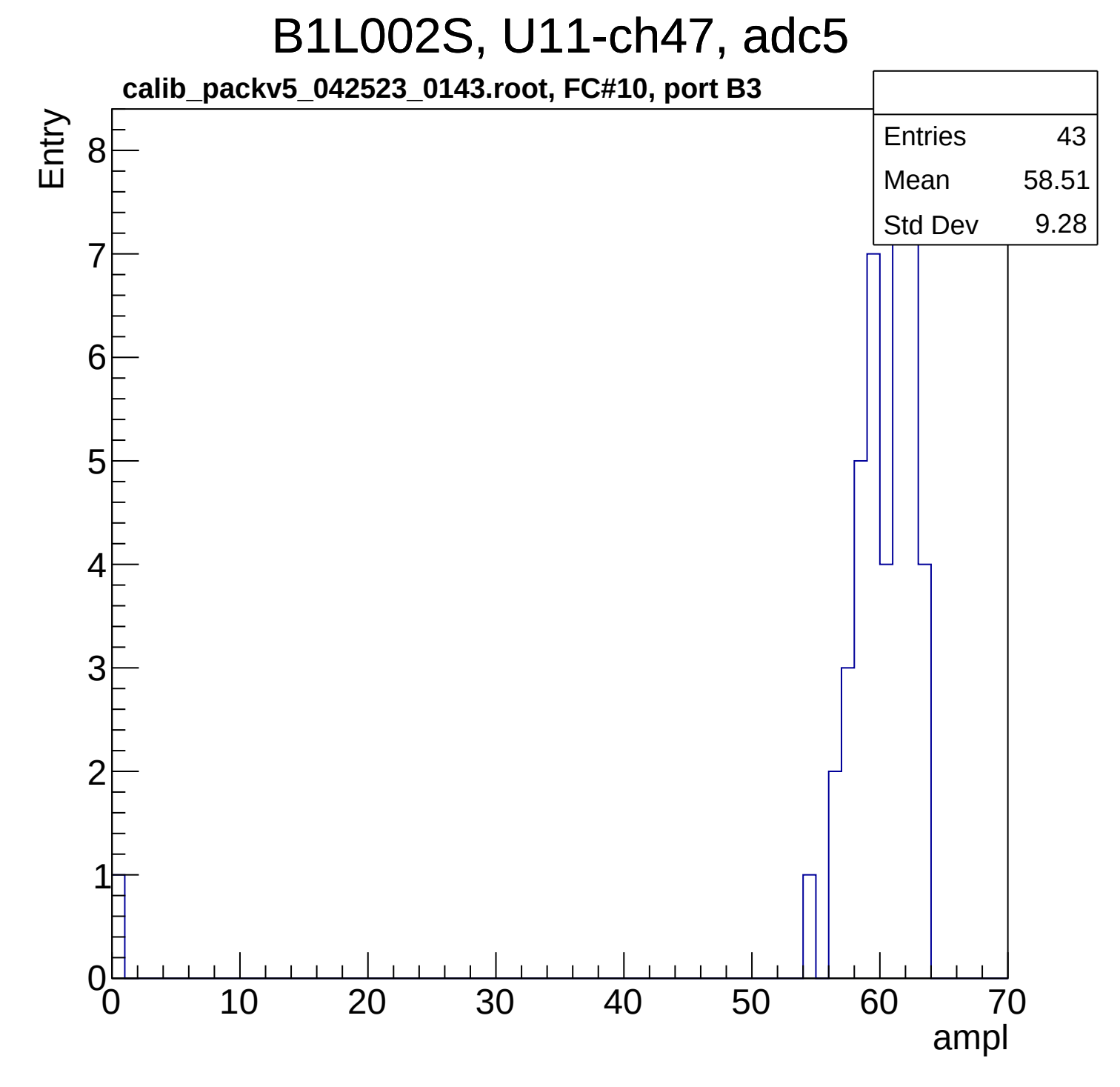

B1L002S, U11-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 54.88
Std Dev 3.145
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 58.51
Std Dev 9.28
Entry
ampl
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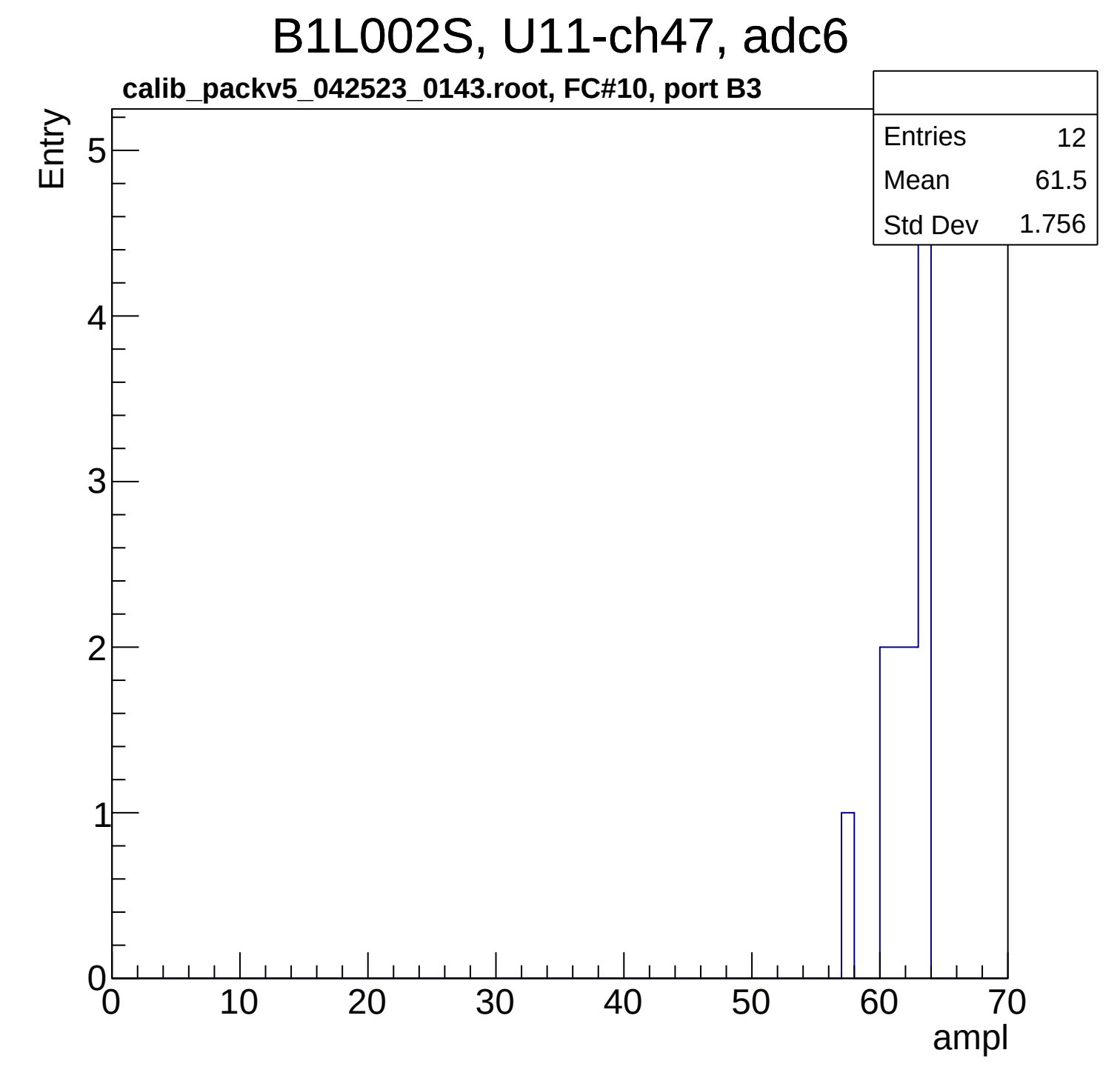
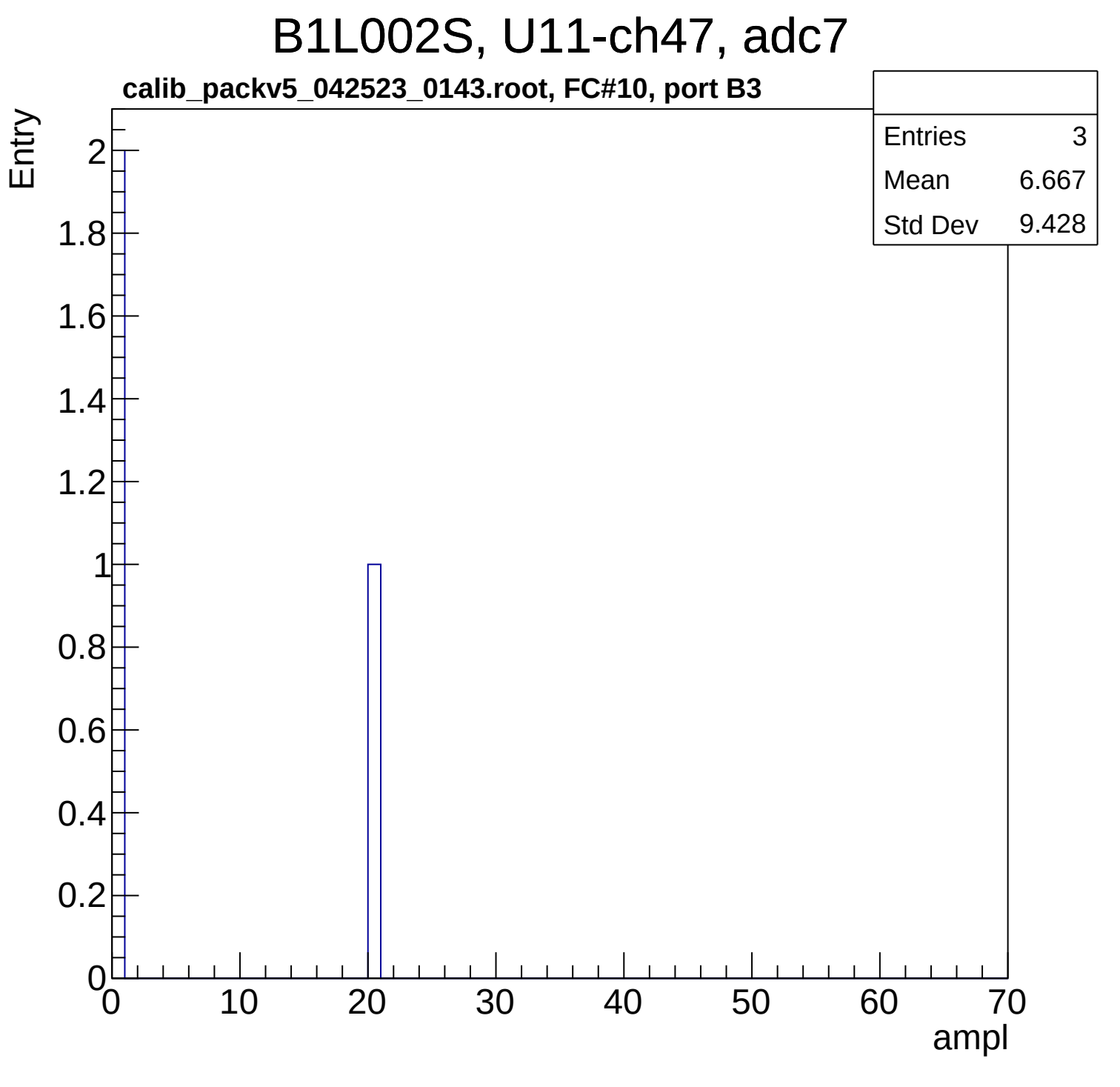
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.5
Std Dev 1.756
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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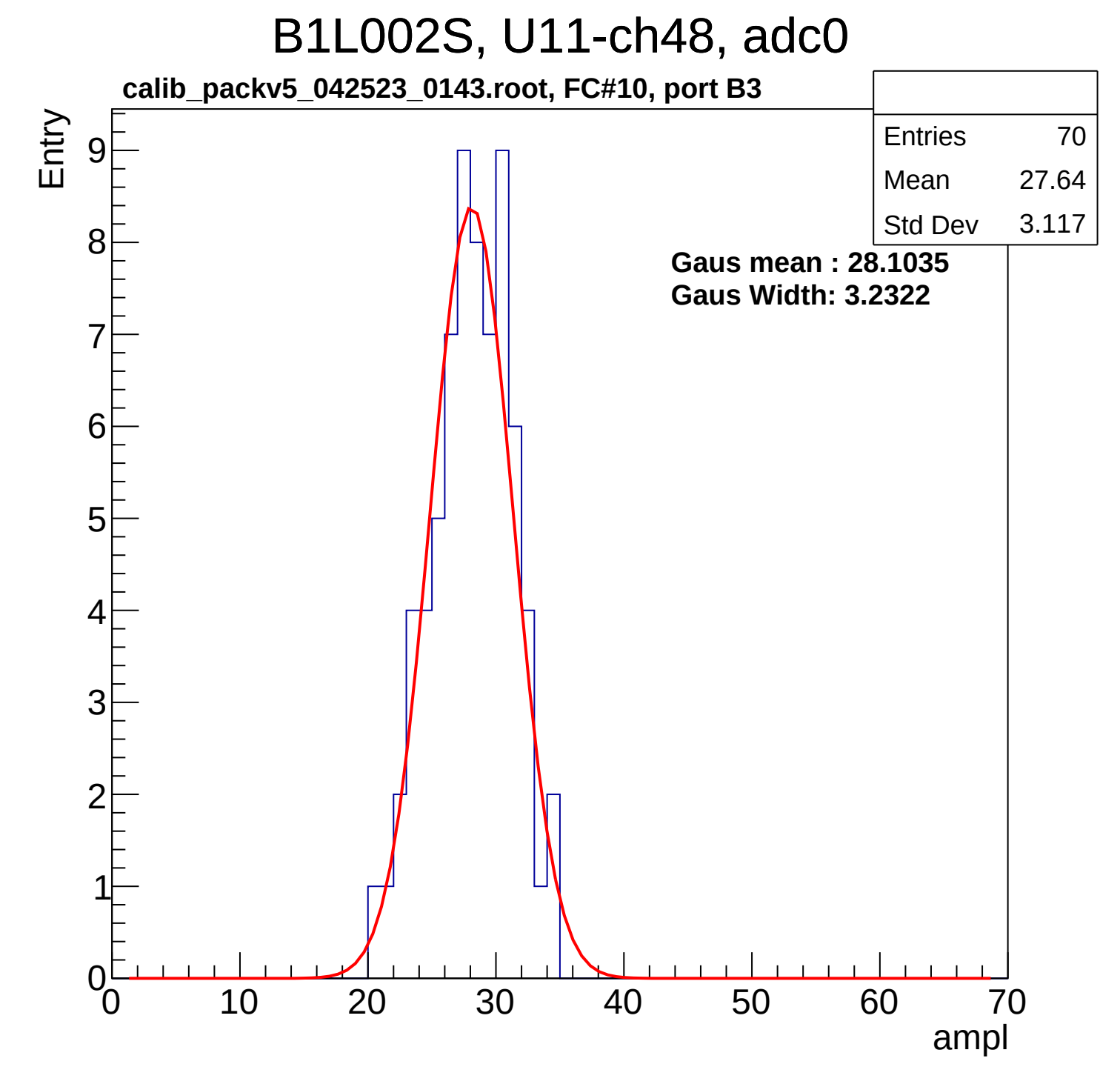
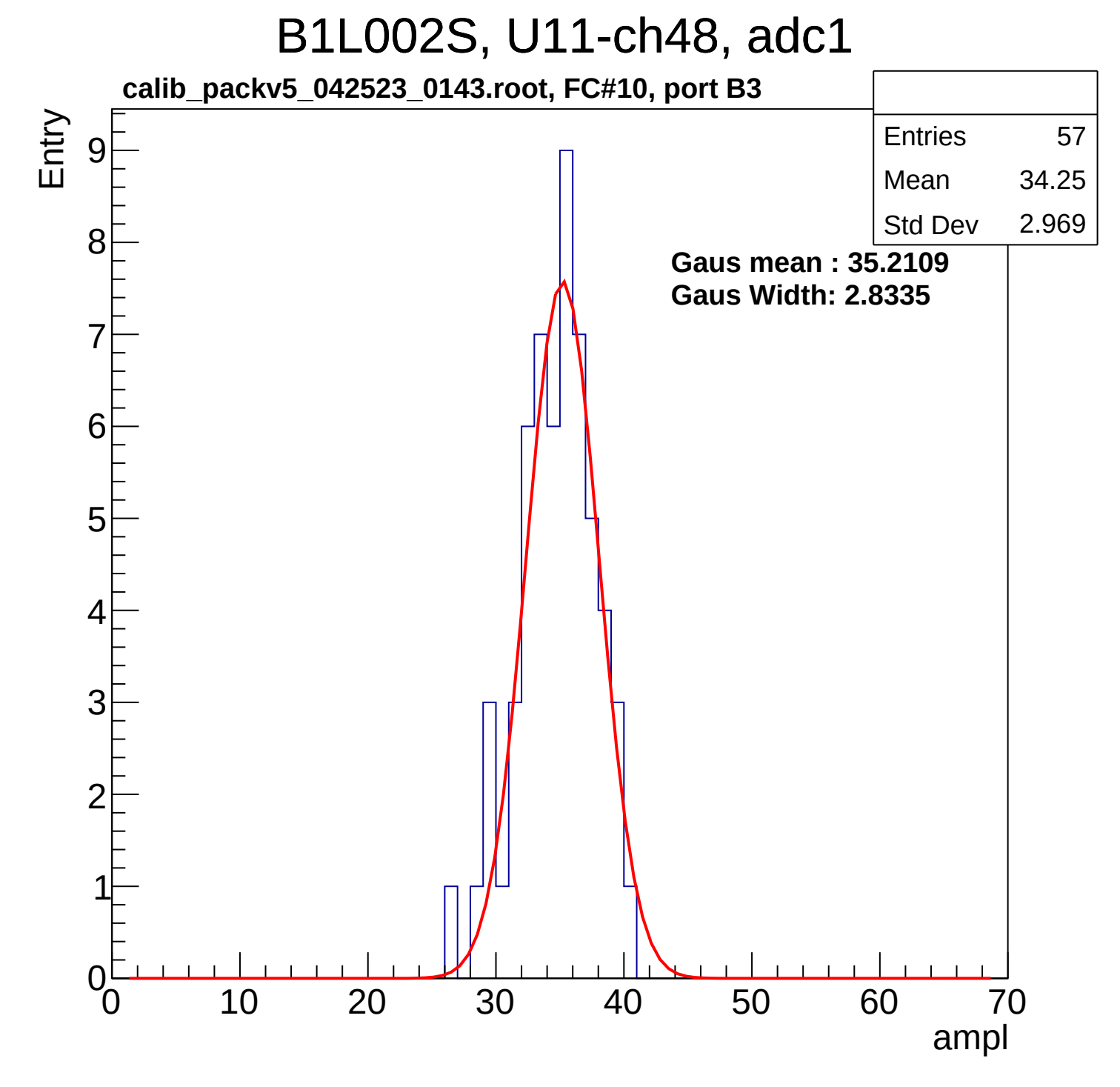
20
30
40
50
60
70


B1L002S, U11-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.64
Std Dev 3.117
Gaus mean : 28.1035
Gaus Width: 3.2322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.25
Std Dev 2.969
Gaus mean : 35.2109
Gaus Width: 2.8335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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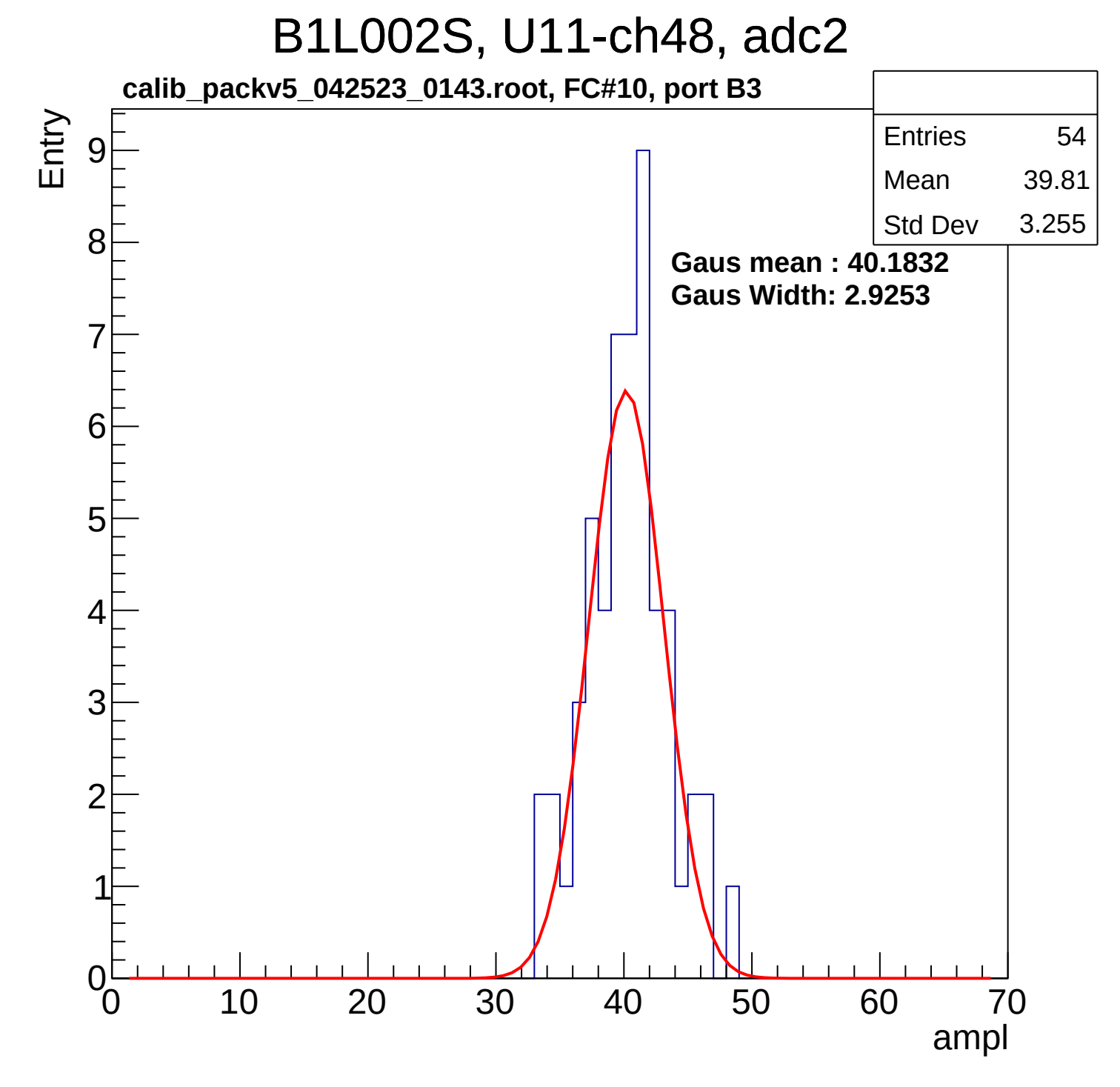
B1L002S, U11-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 39.81
Std Dev 3.255
Gaus mean : 40.1832
Gaus Width: 2.9253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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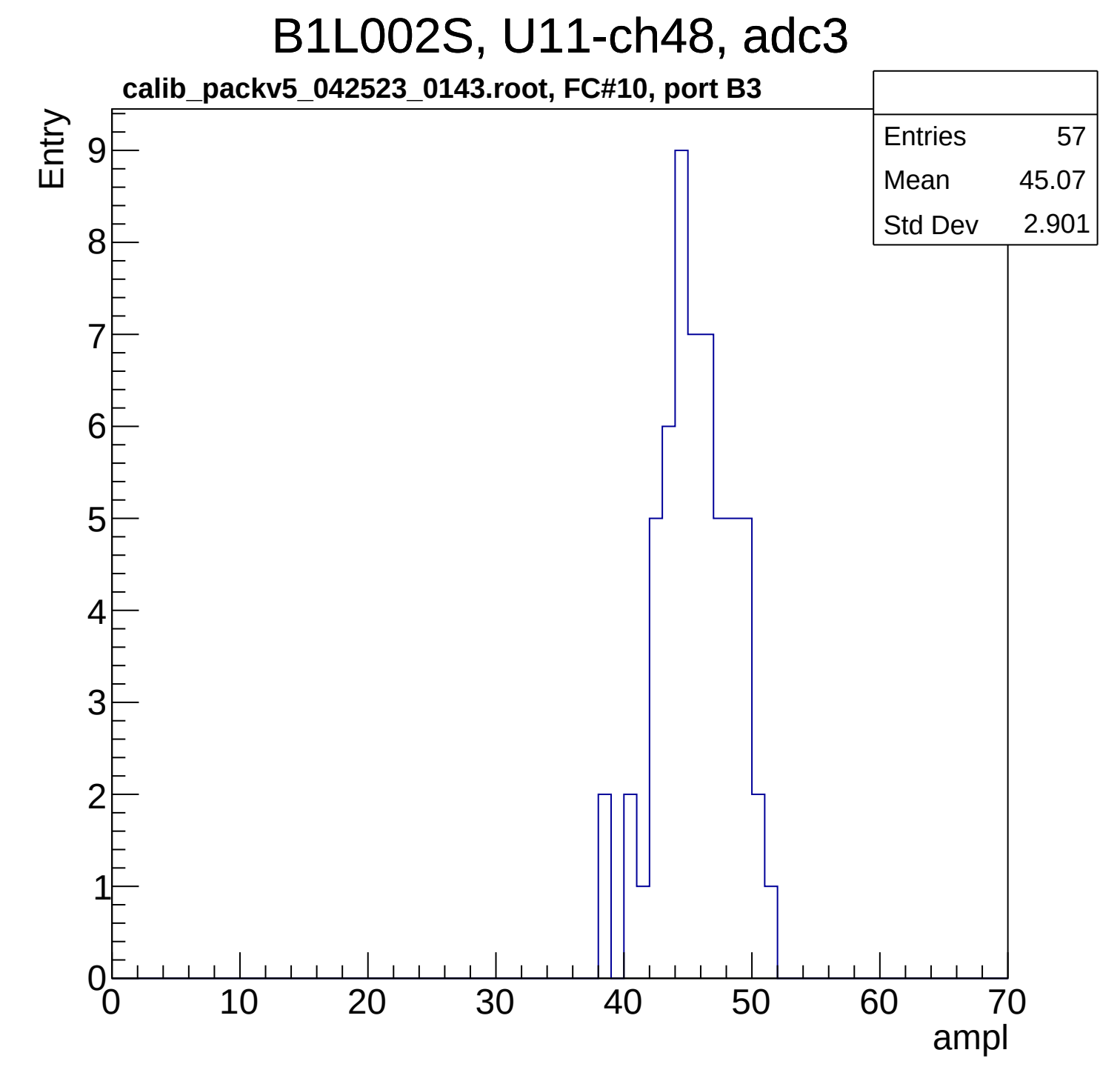
B1L002S, U11-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 45.07
Std Dev 2.901
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
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60
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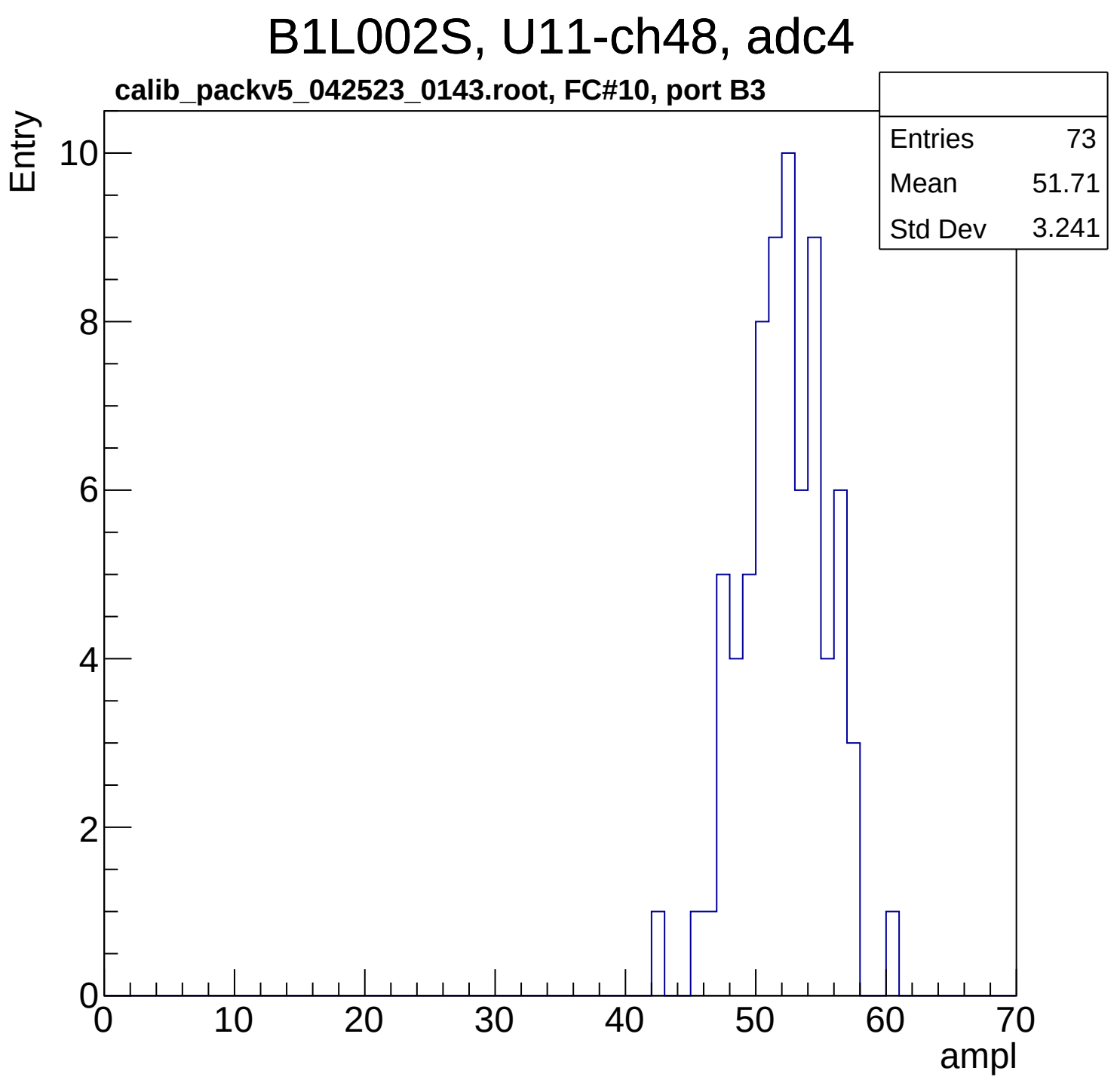
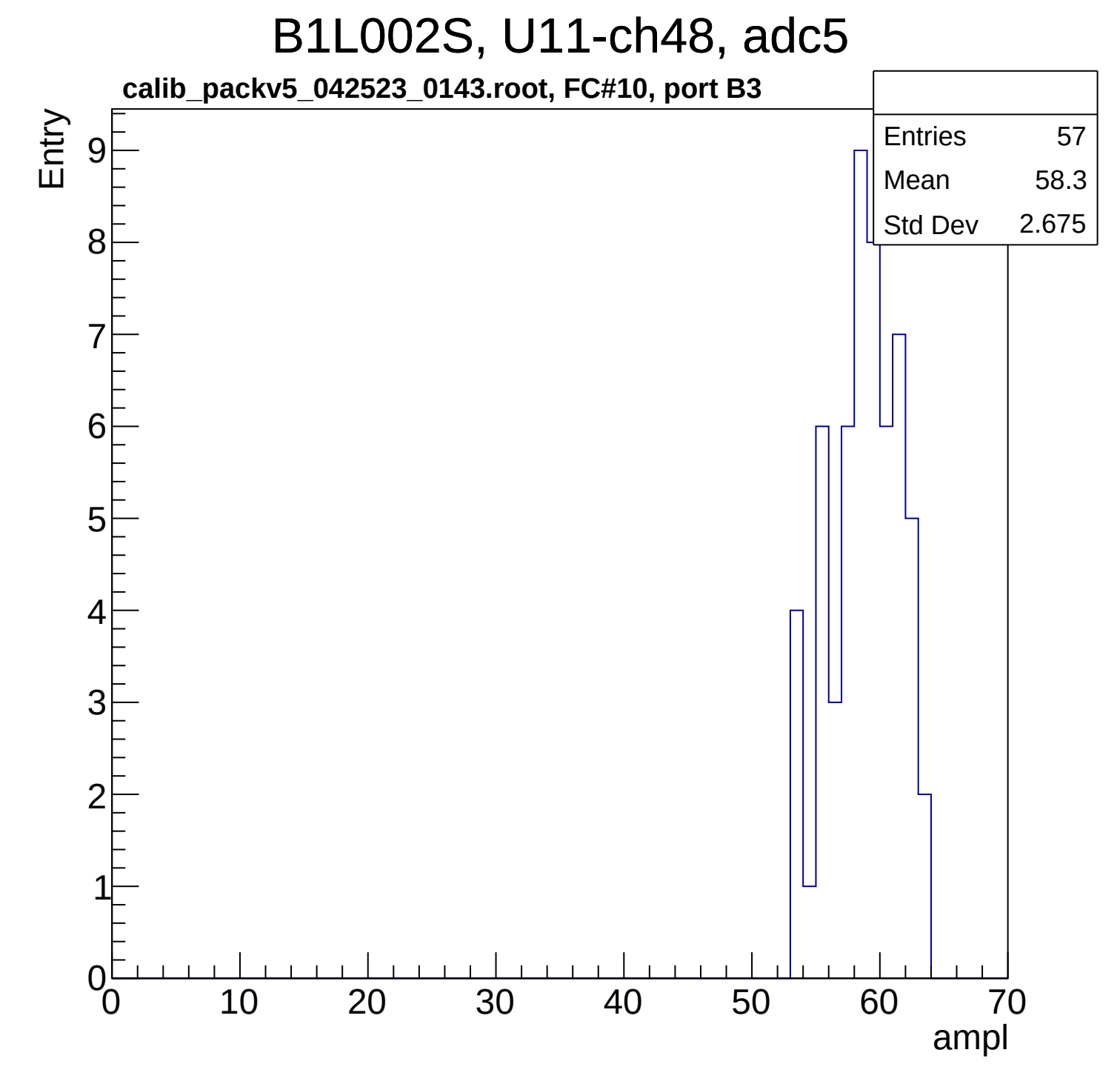

B1L002S, U11-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 51.71
Std Dev 3.241
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.3
Std Dev 2.675
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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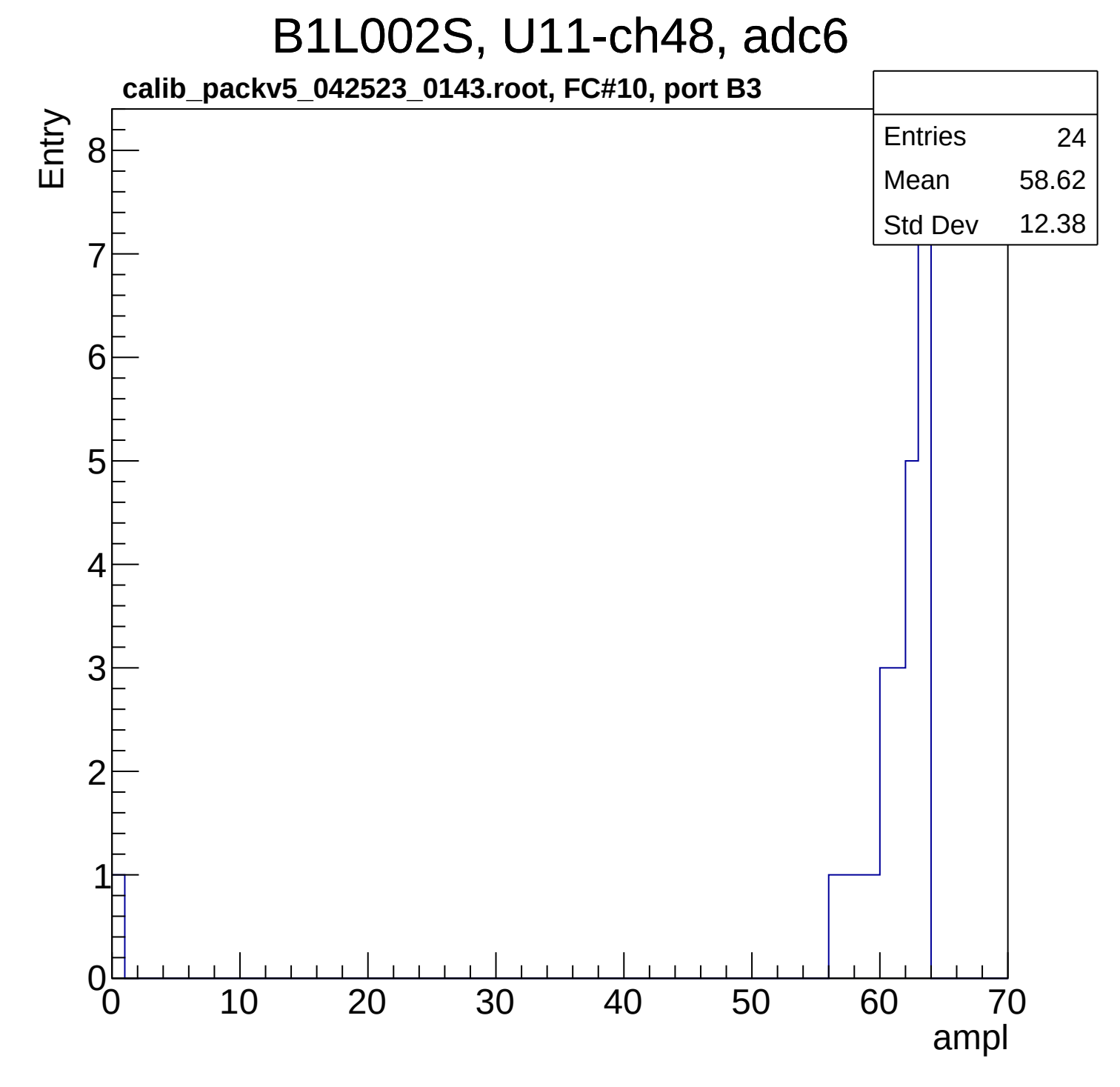
B1L002S, U11-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.62
Std Dev 12.38
Entry
ampl
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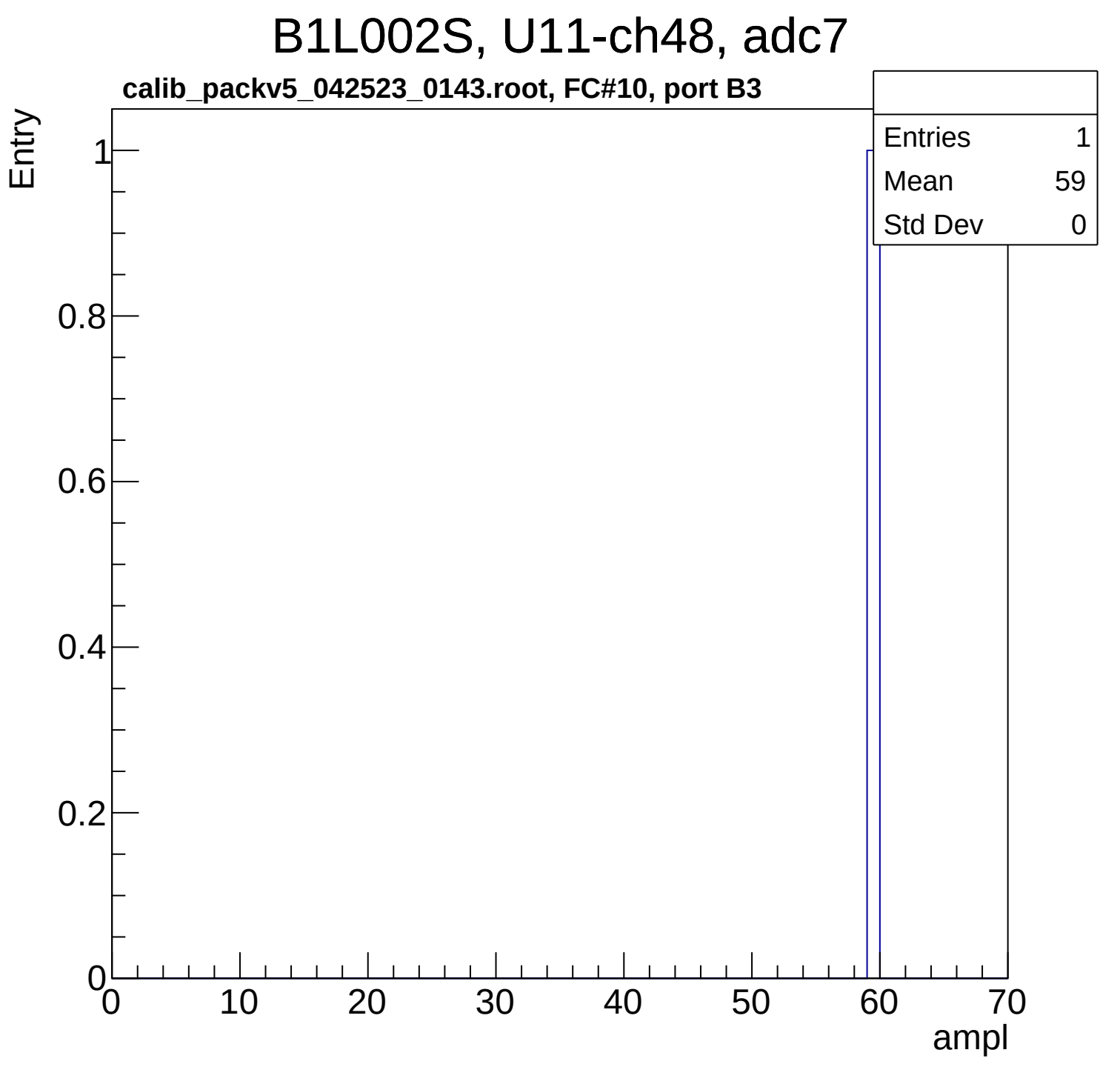
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 59
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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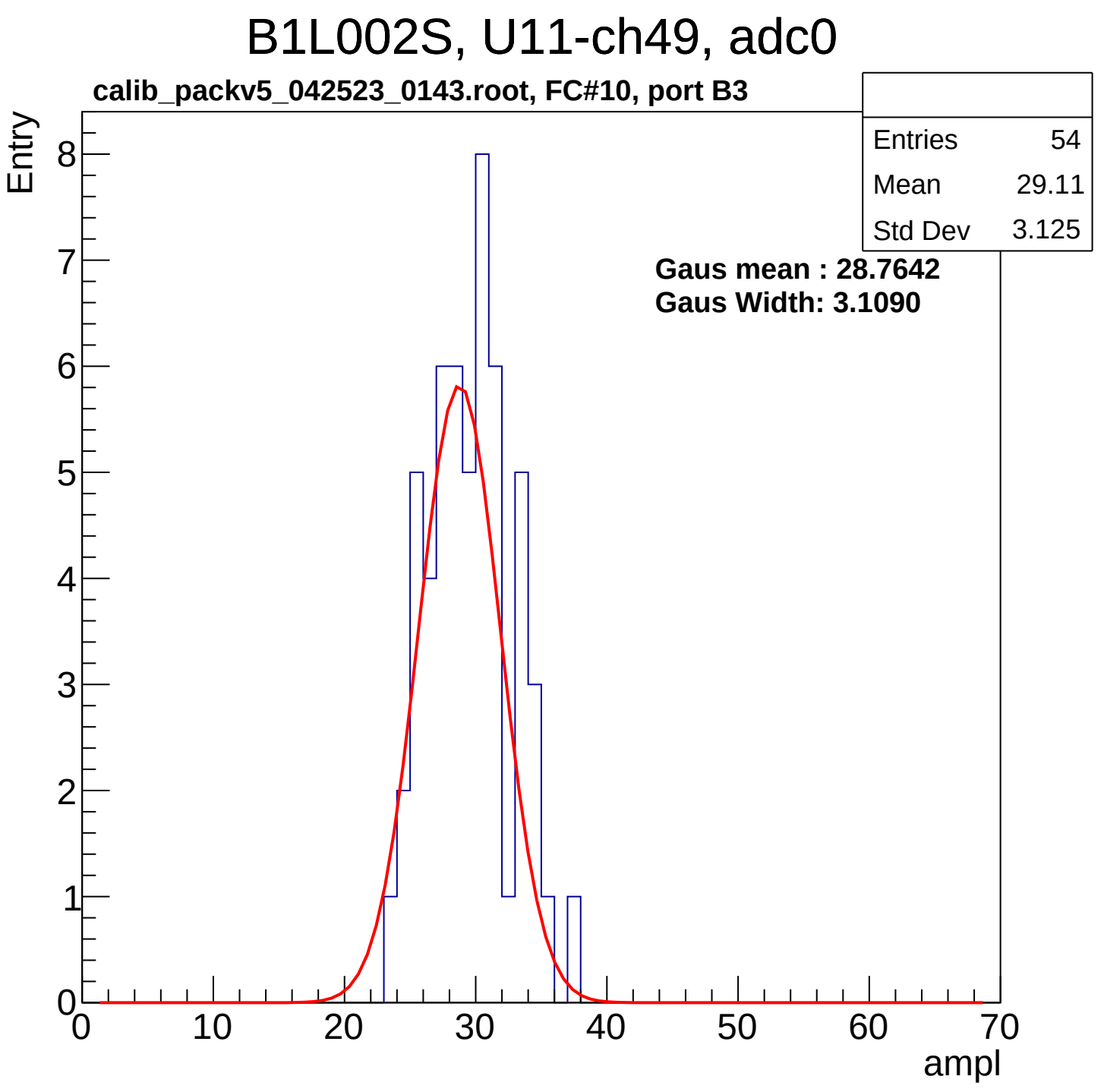
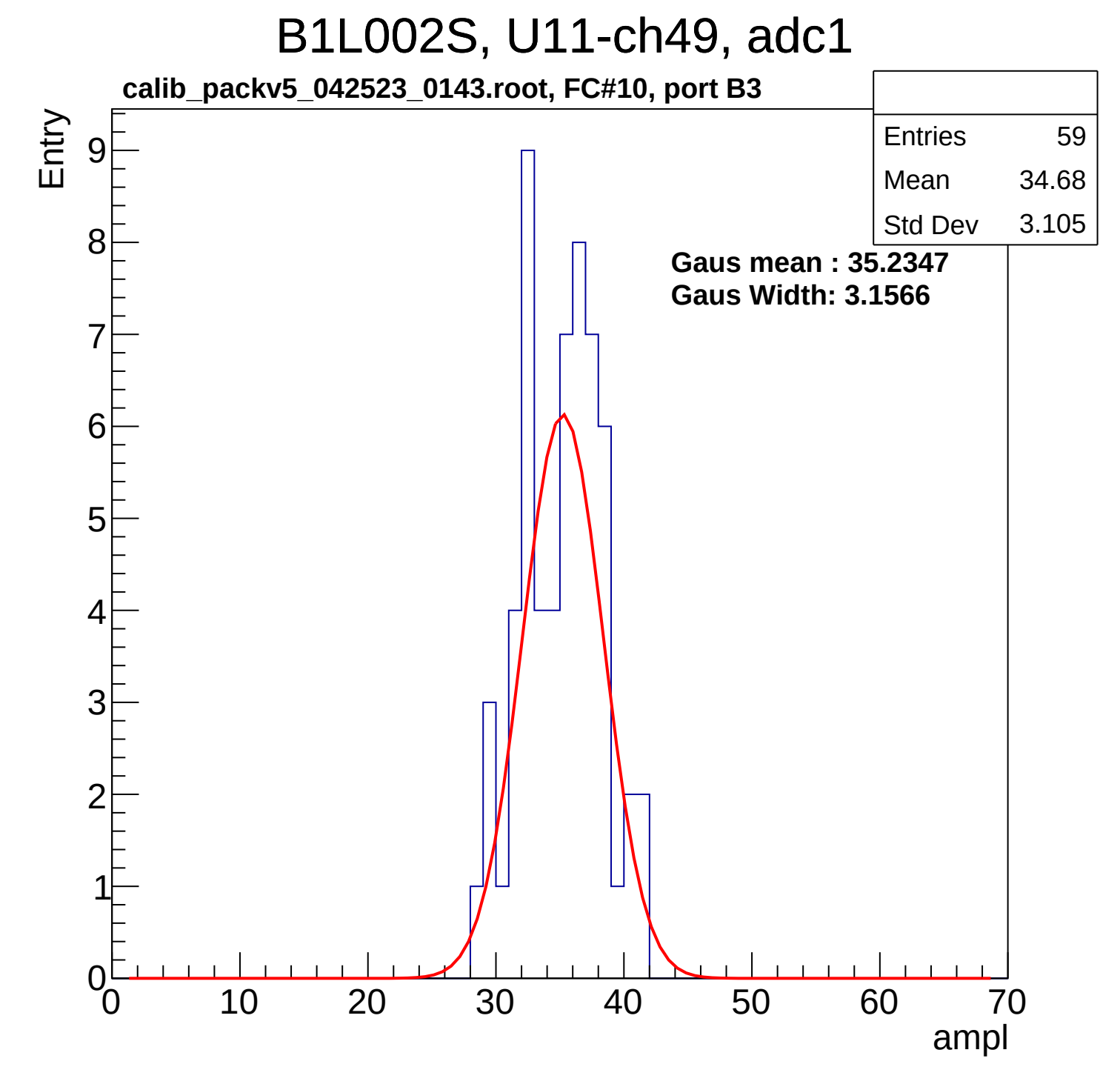
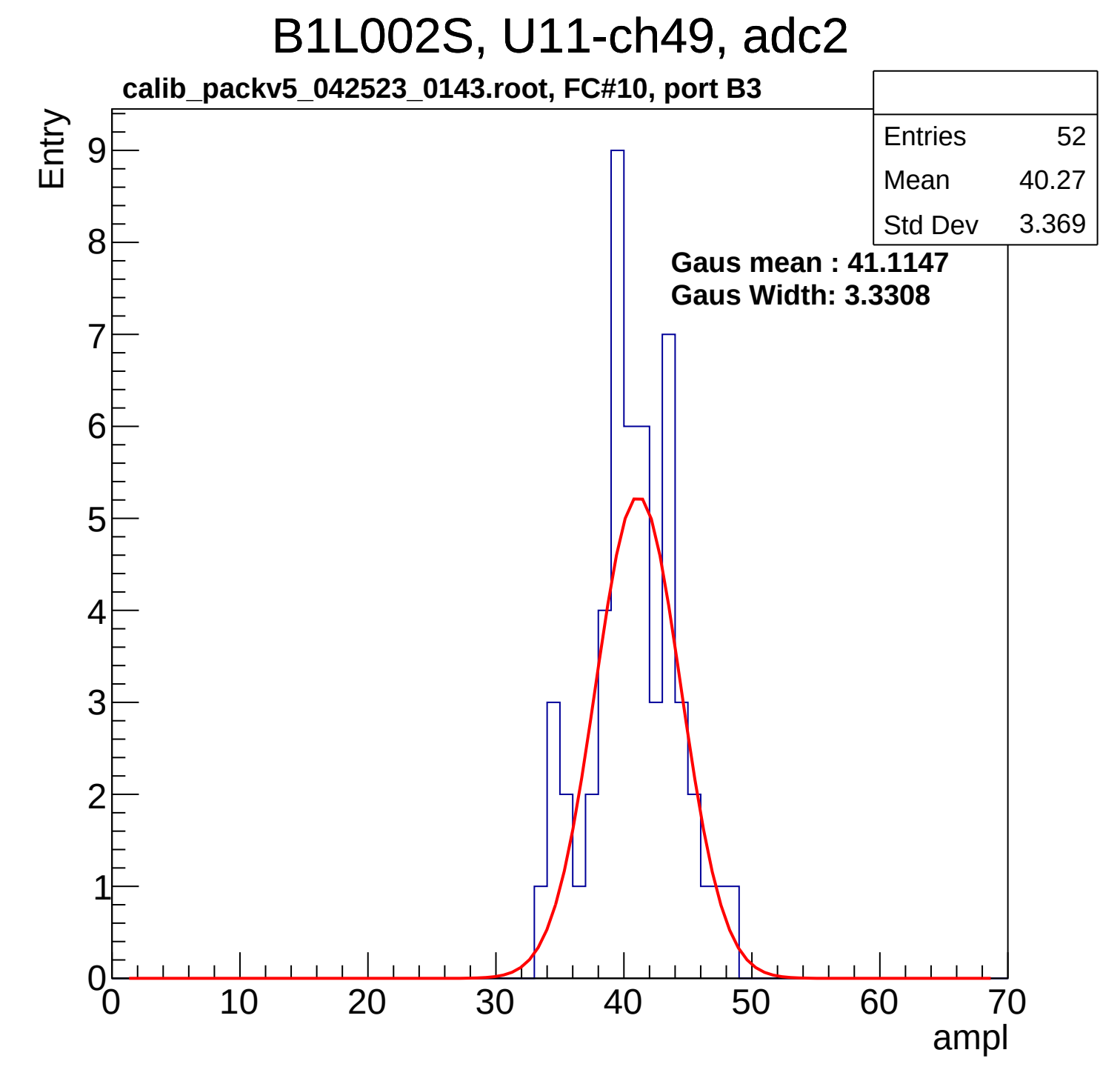
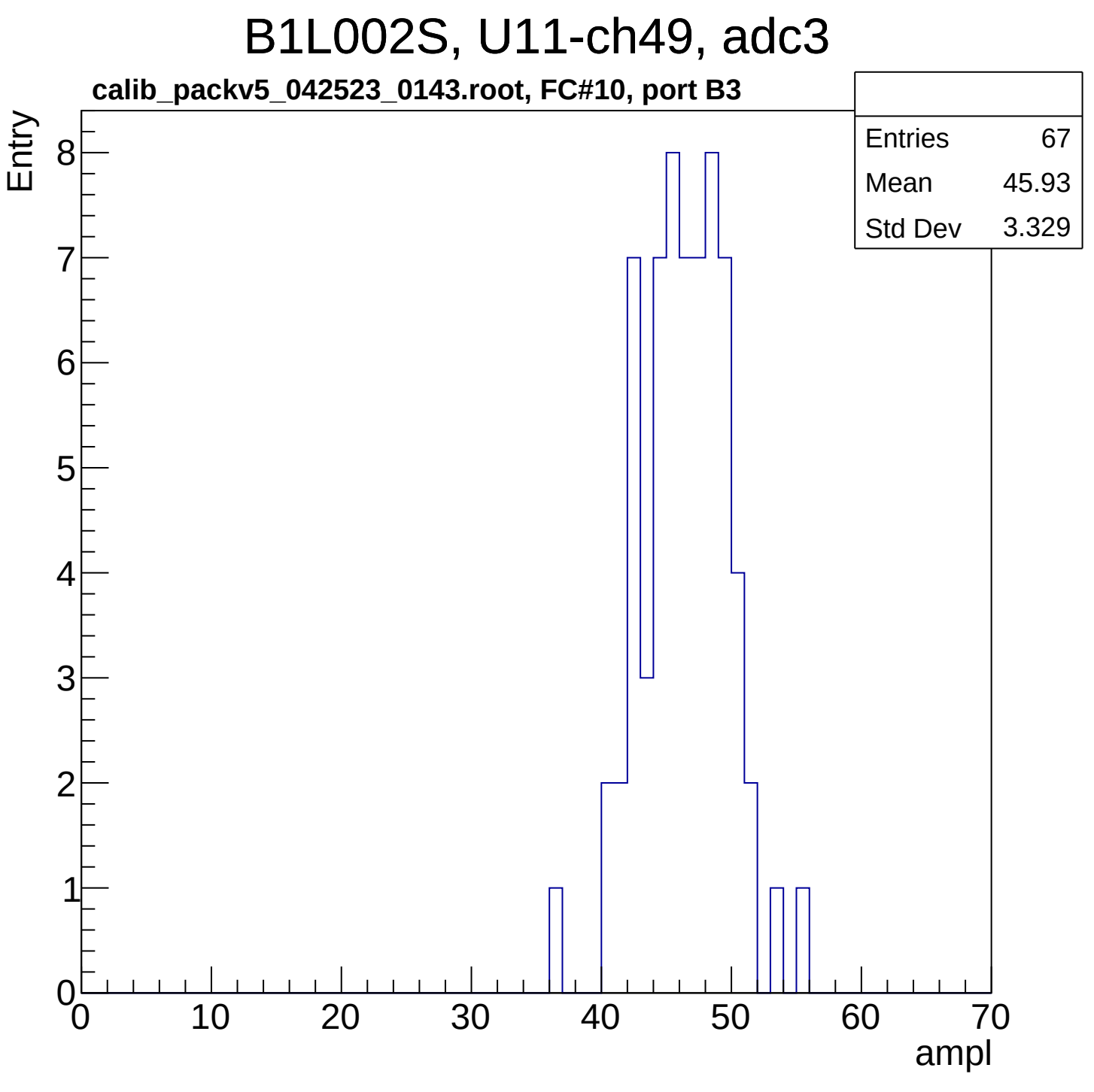
50
60
70
ampl


B1L002S, U11-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 29.11
Std Dev 3.125
Gaus mean : 28.7642
Gaus Width: 3.1090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.68
Std Dev 3.105
Gaus mean : 35.2347
Gaus Width: 3.1566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.27
Std Dev 3.369
Gaus mean : 41.1147
Gaus Width: 3.3308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 45.93
Std Dev 3.329
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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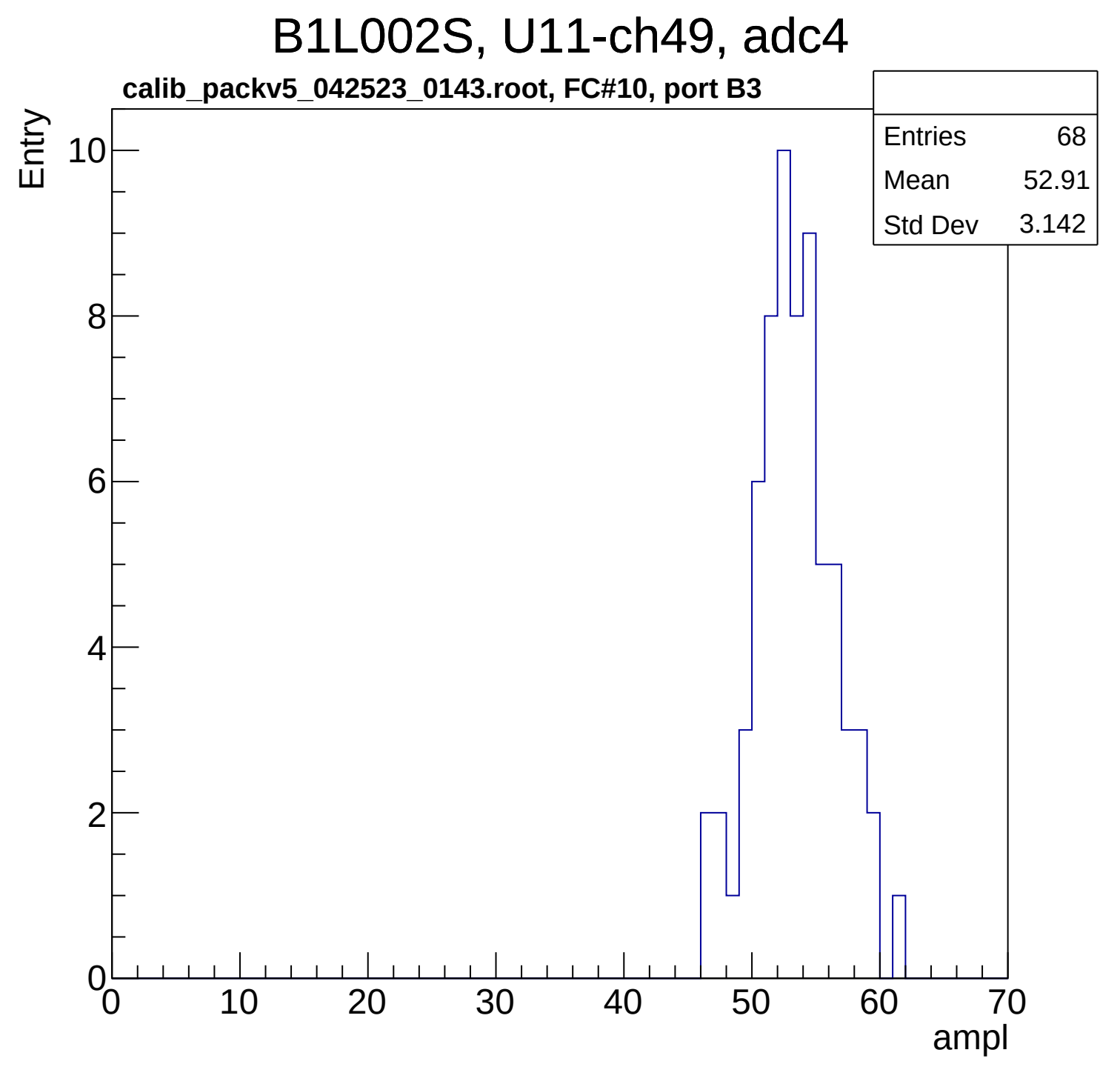
B1L002S, U11-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 52.91
Std Dev 3.142
Entry
ampl
0
2
4
6
8
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0
10
20
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50
60
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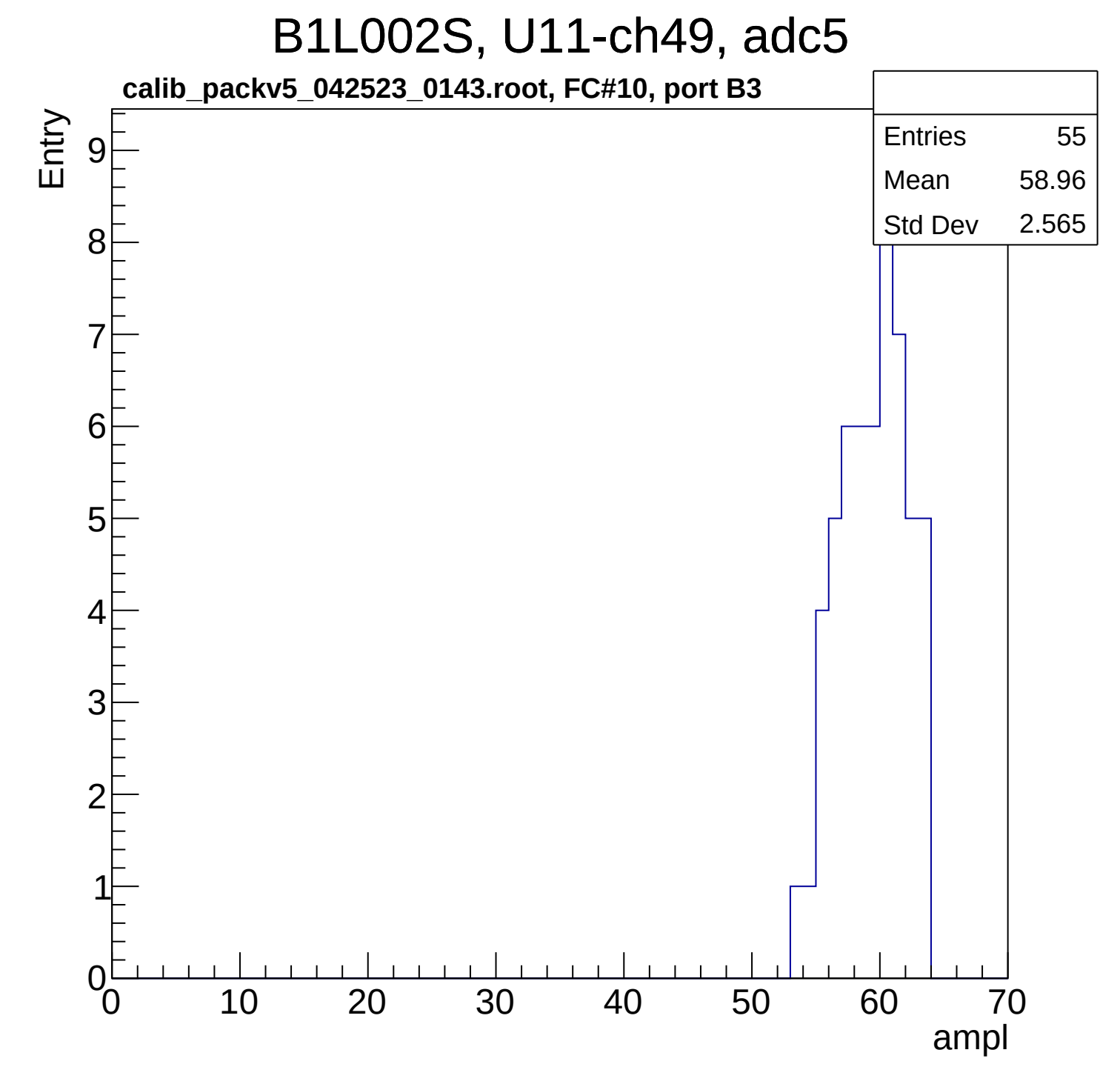
B1L002S, U11-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.96
Std Dev 2.565
Entry
ampl
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1
2
3
4
5
6
7
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9
0
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20
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40
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60
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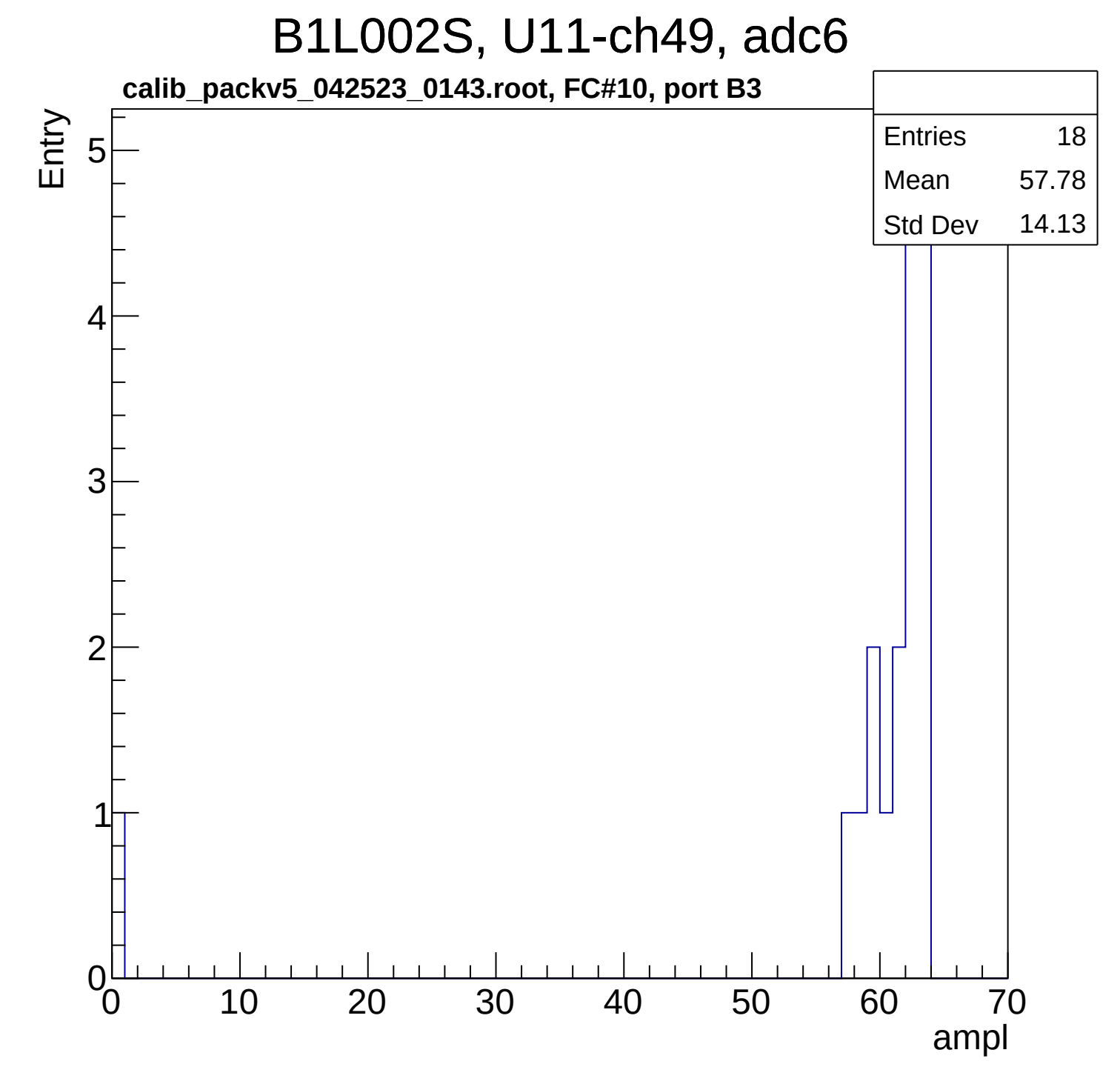
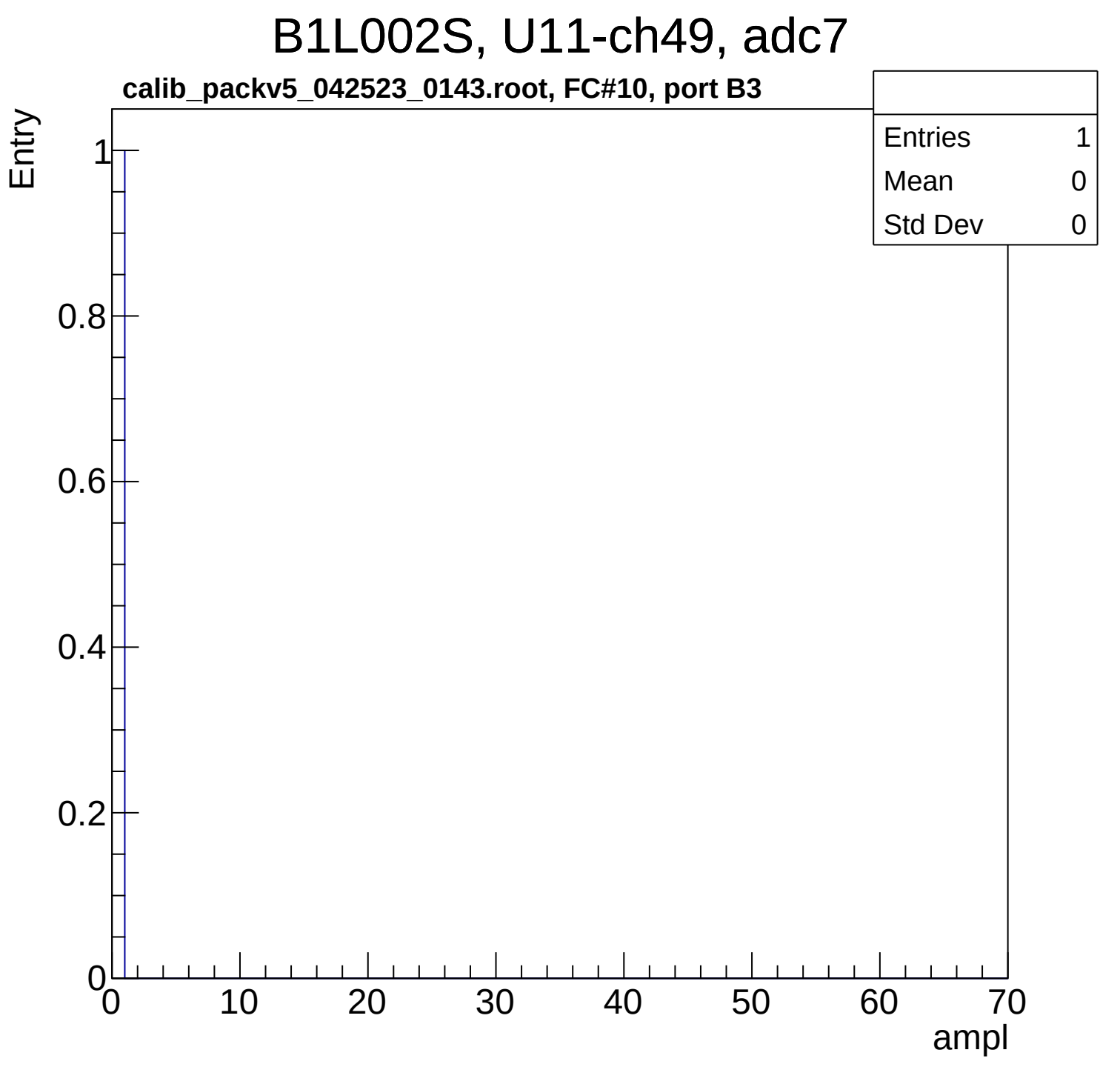

B1L002S, U11-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.78
Std Dev 14.13
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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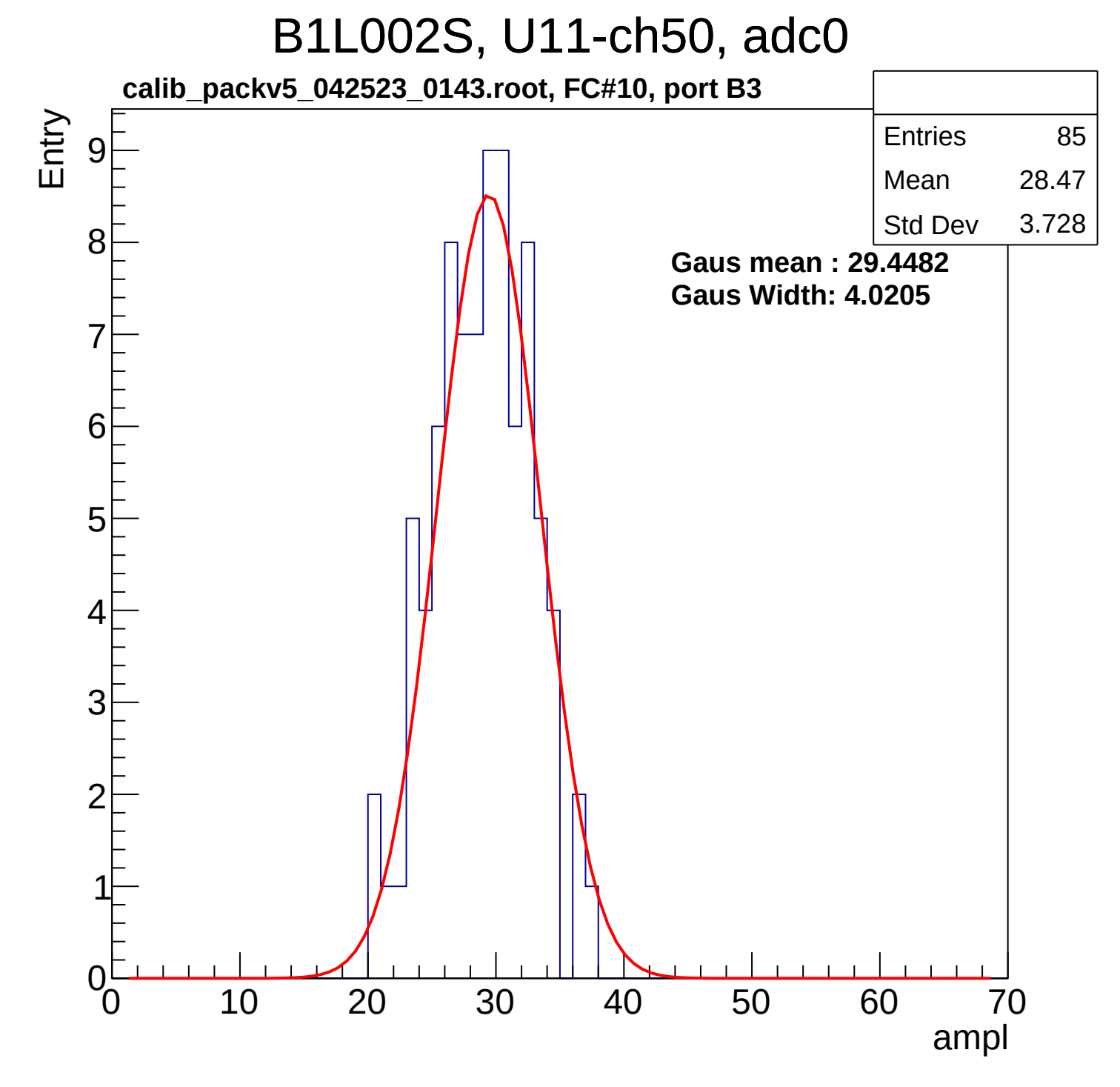
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 28.47
Std Dev 3.728
Gaus mean : 29.4482
Gaus Width: 4.0205
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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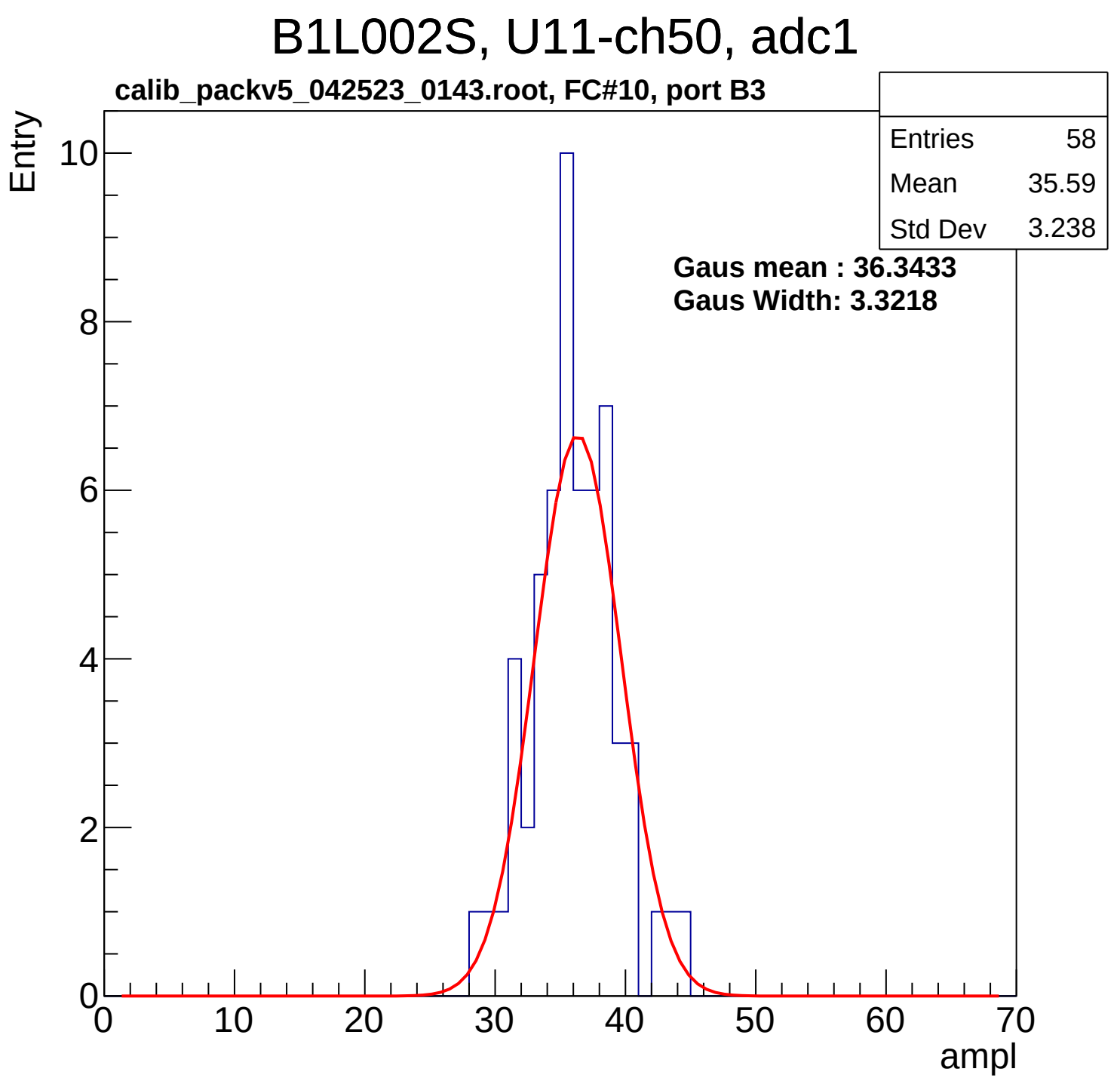
B1L002S, U11-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.59
Std Dev 3.238
Gaus mean : 36.3433
Gaus Width: 3.3218
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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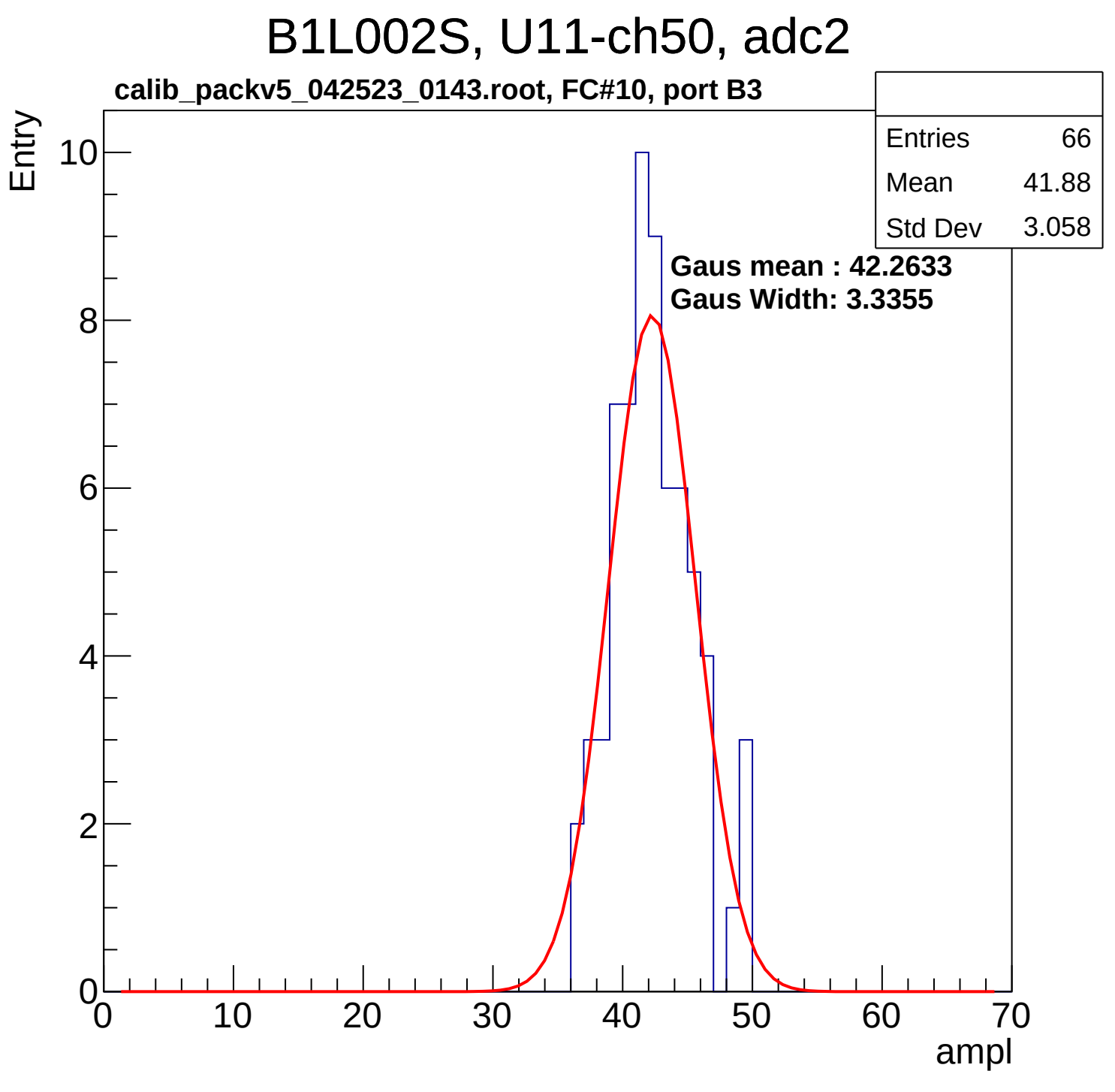
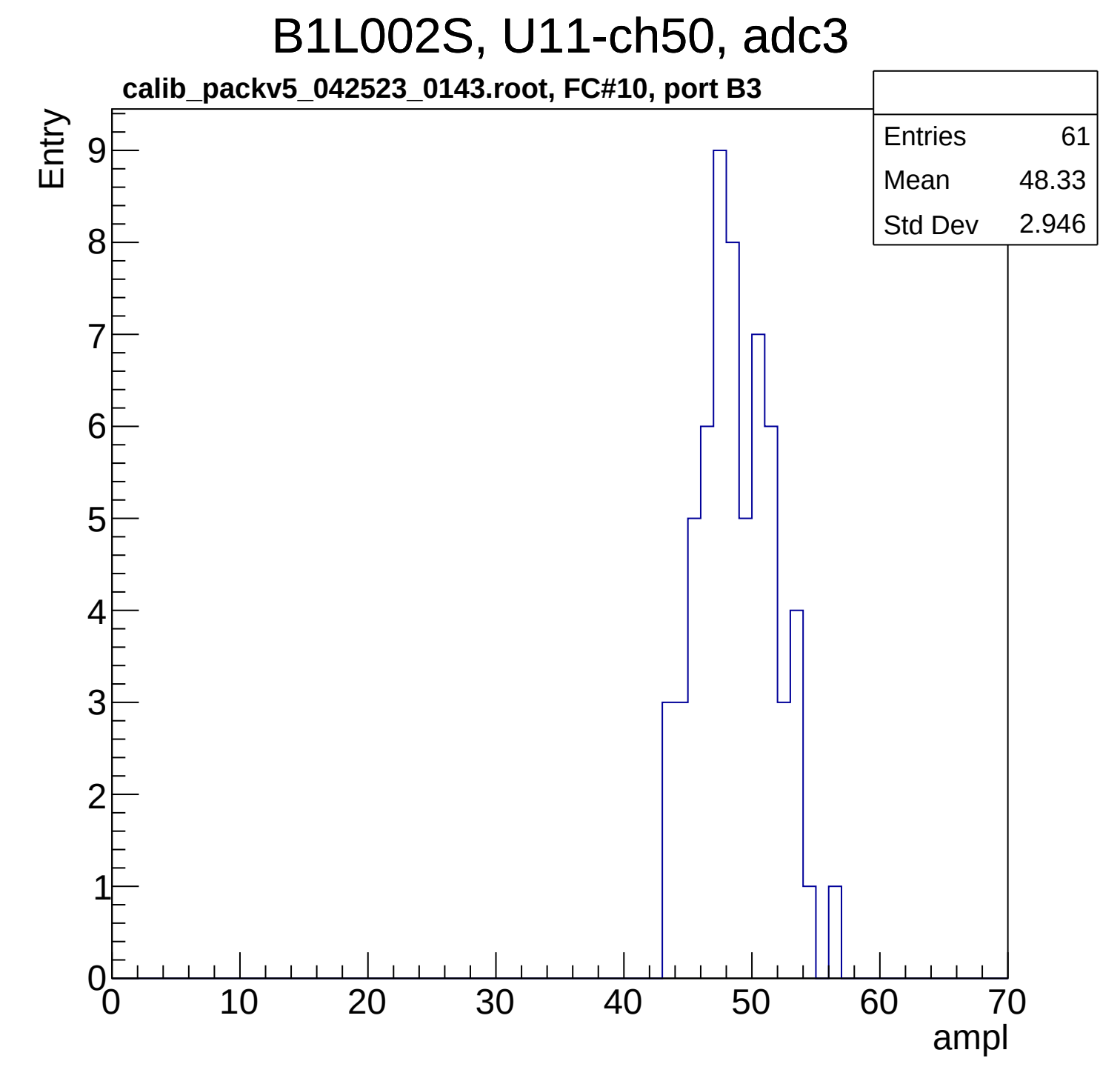

B1L002S, U11-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.88
Std Dev 3.058
Gaus mean : 42.2633
Gaus Width: 3.3355
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.33
Std Dev 2.946
Entry
ampl
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3
4
5
6
7
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0
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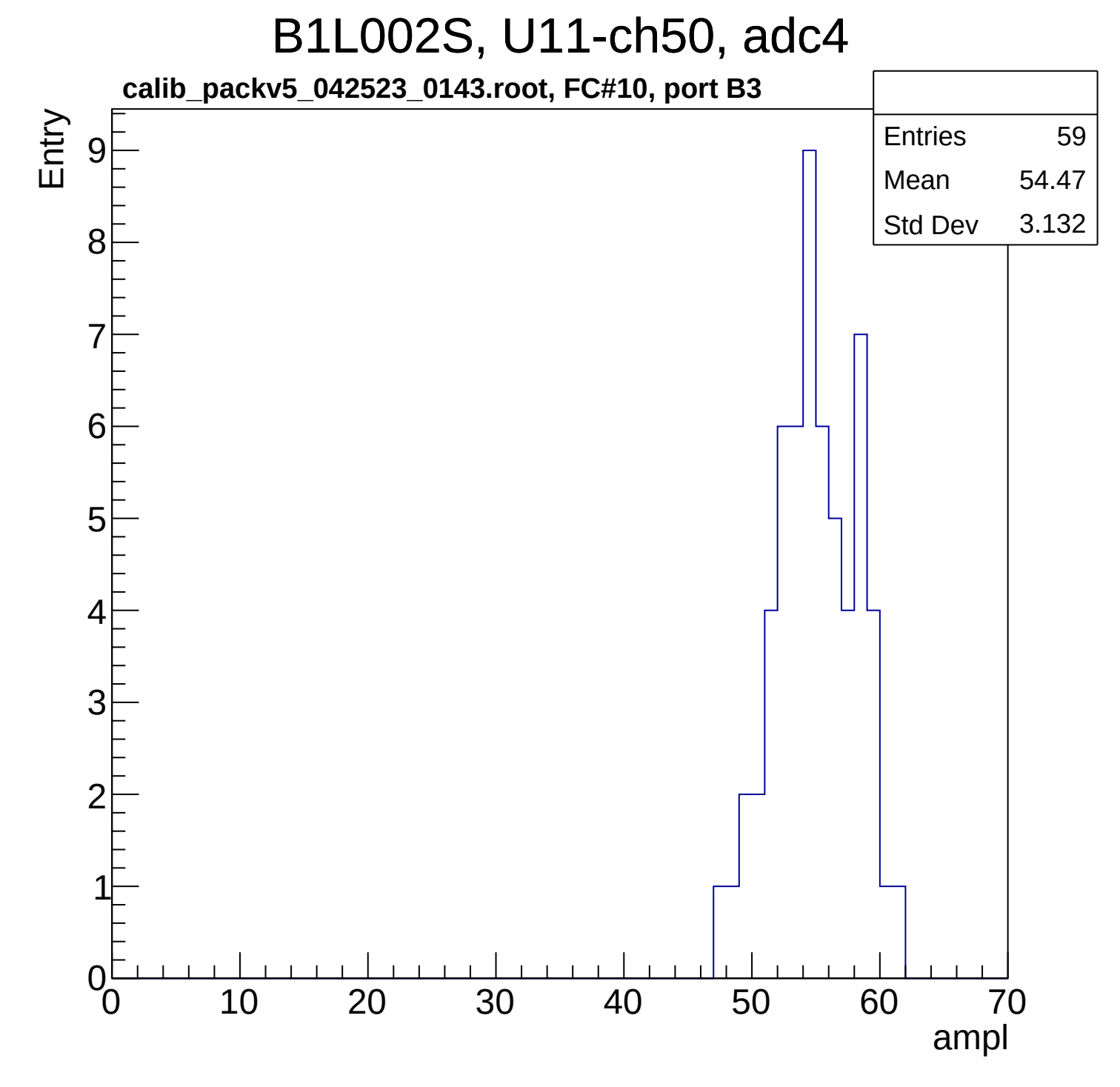
B1L002S, U11-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.47
Std Dev 3.132
Entry
ampl
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3
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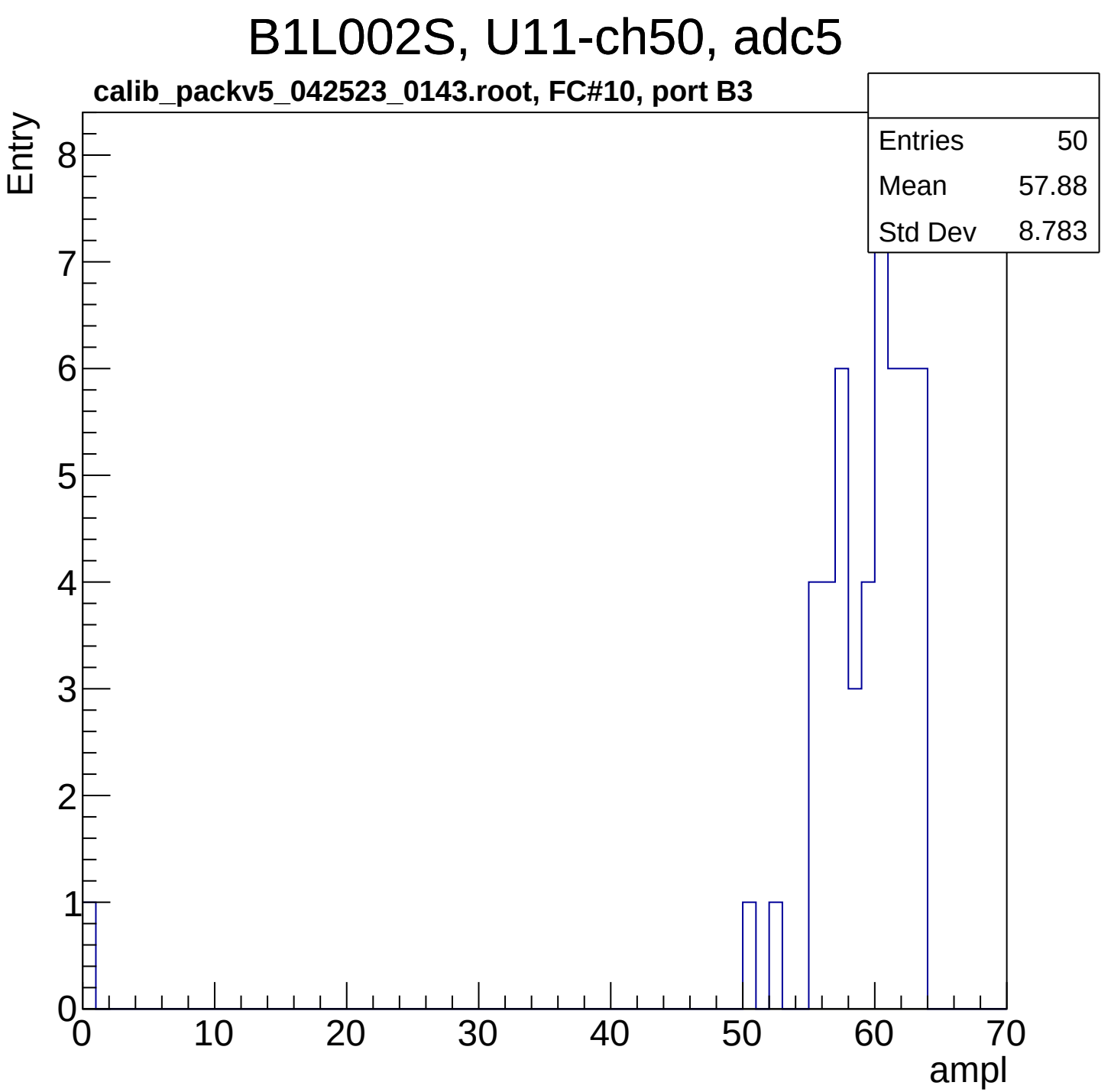
B1L002S, U11-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.88
Std Dev 8.783
Entry
ampl
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2
3
4
5
6
7
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0
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40
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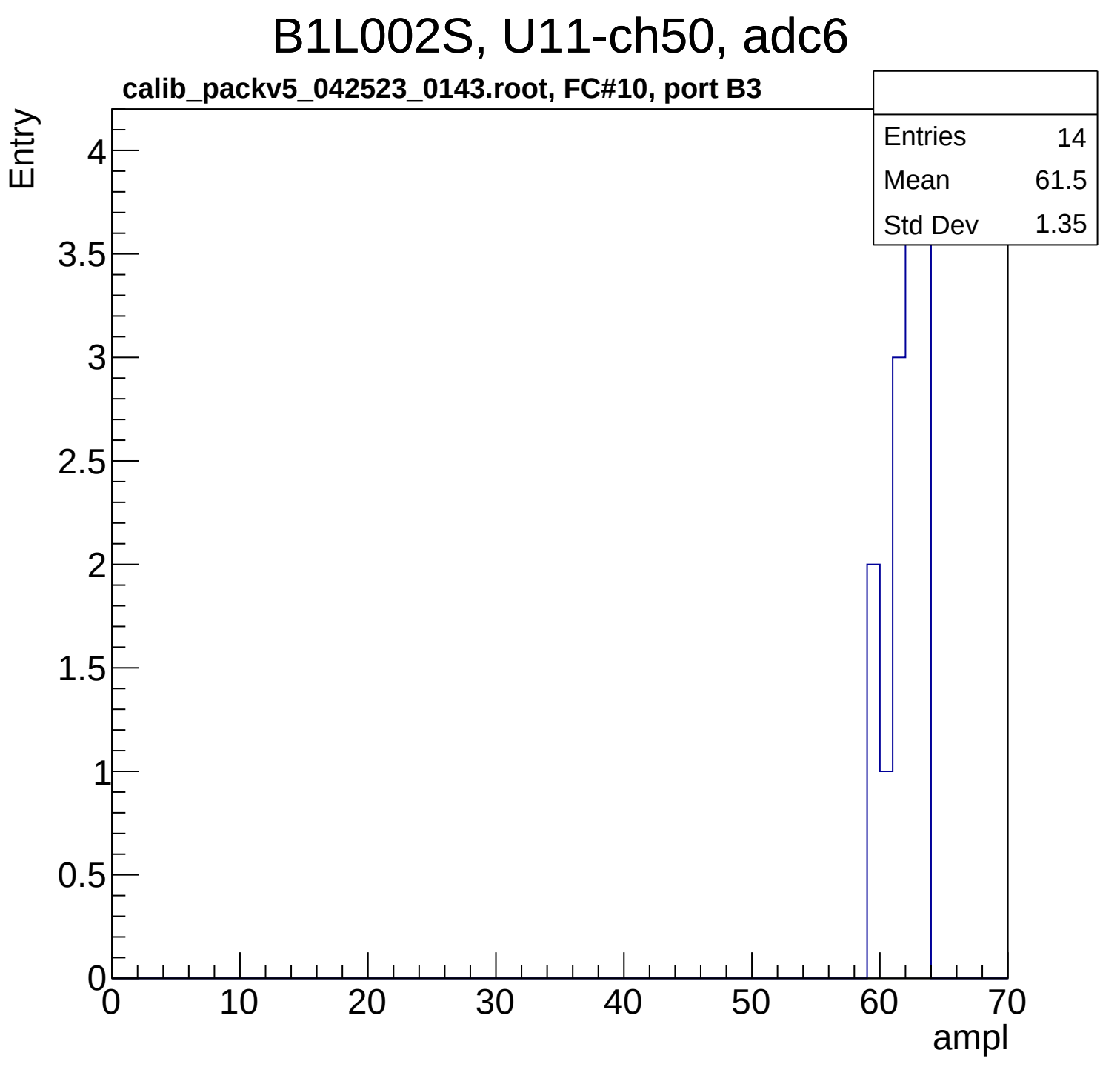
B1L002S, U11-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.5
Std Dev 1.35
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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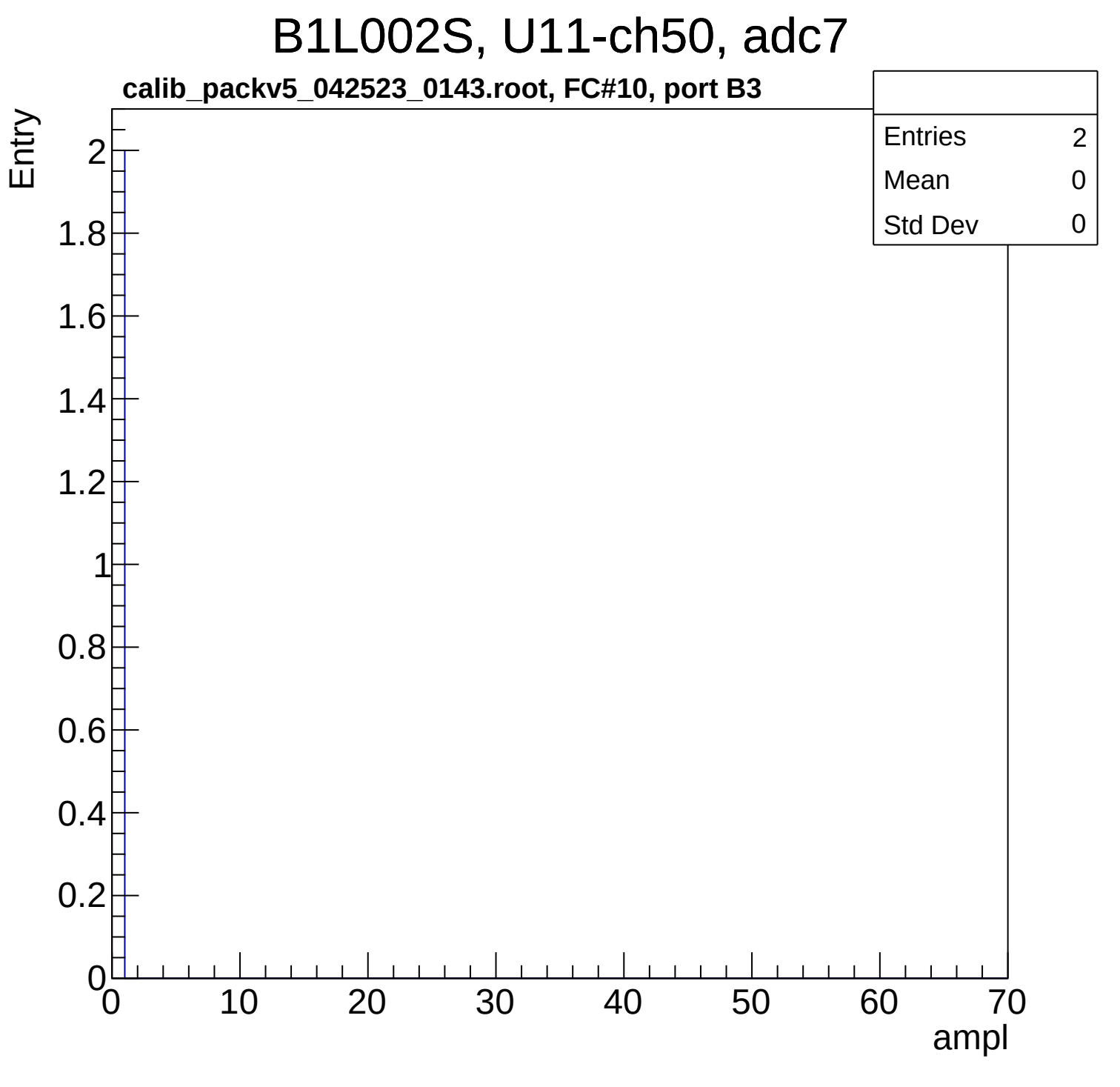
B1L002S, U11-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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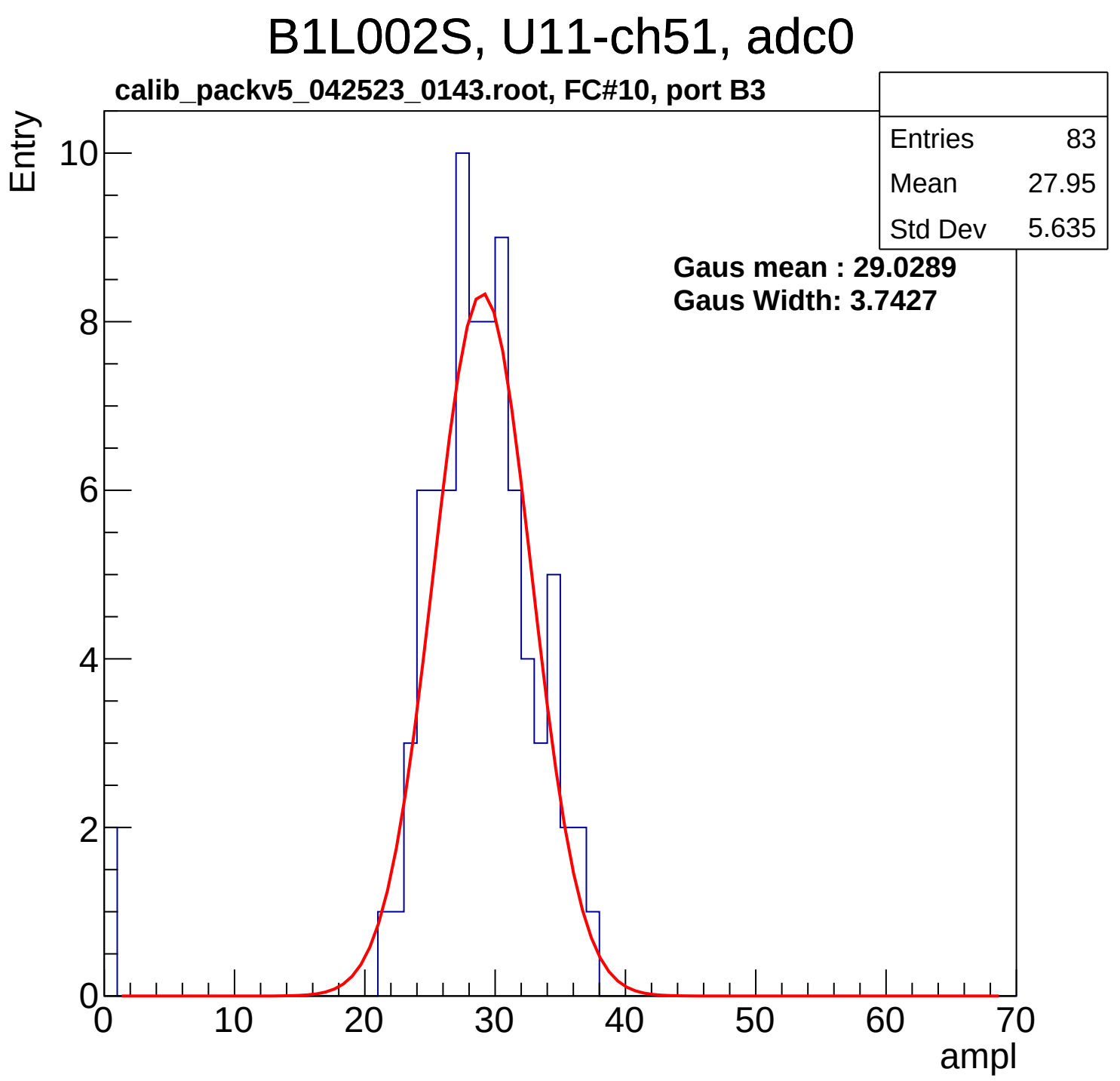
B1L002S, U11-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 27.95
Std Dev 5.635
Gaus mean : 29.0289
Gaus Width: 3.7427
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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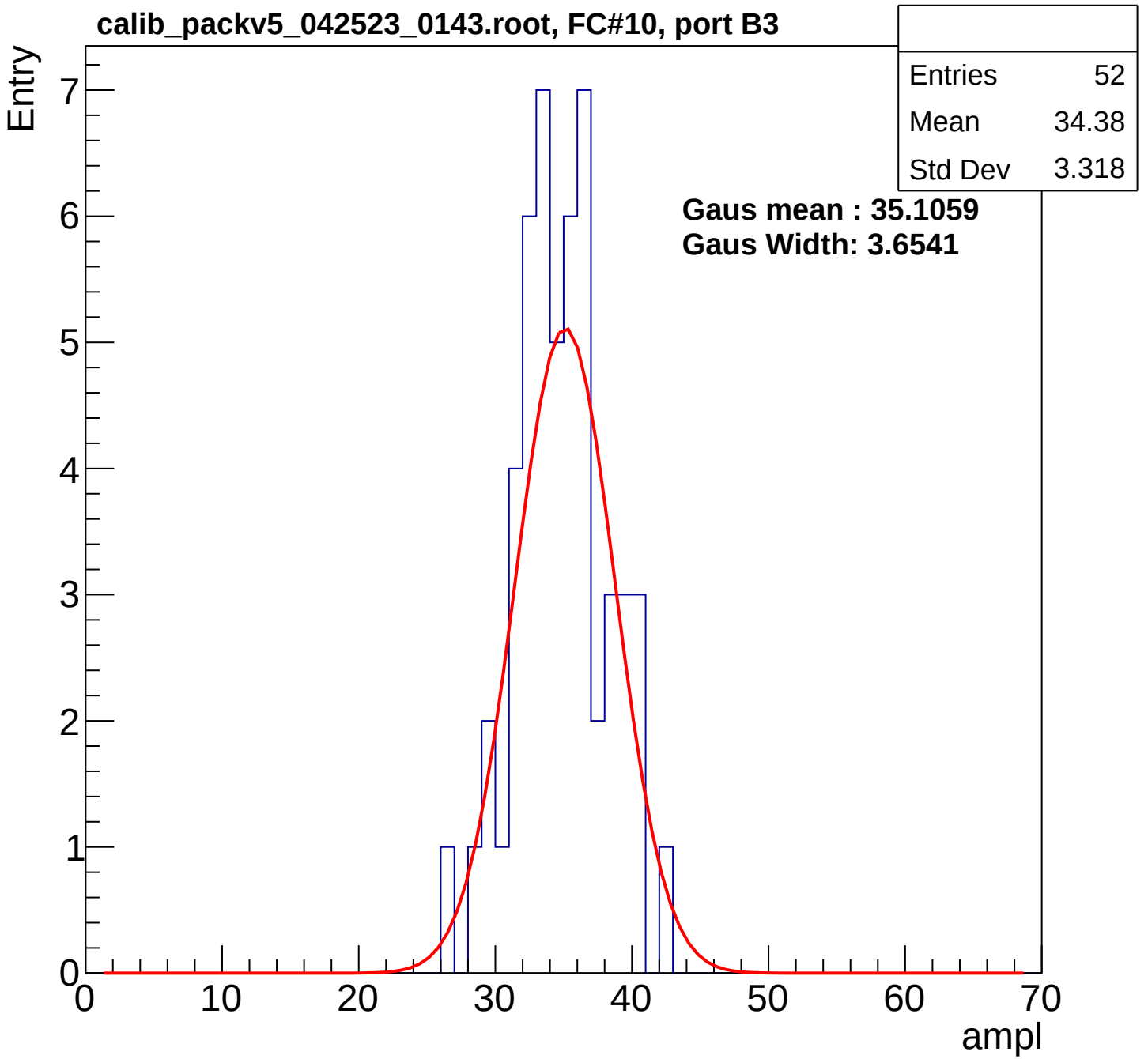
B1L002S, U11-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 34.38
Std Dev 3.318
Gaus mean : 35.1059
Gaus Width: 3.6541
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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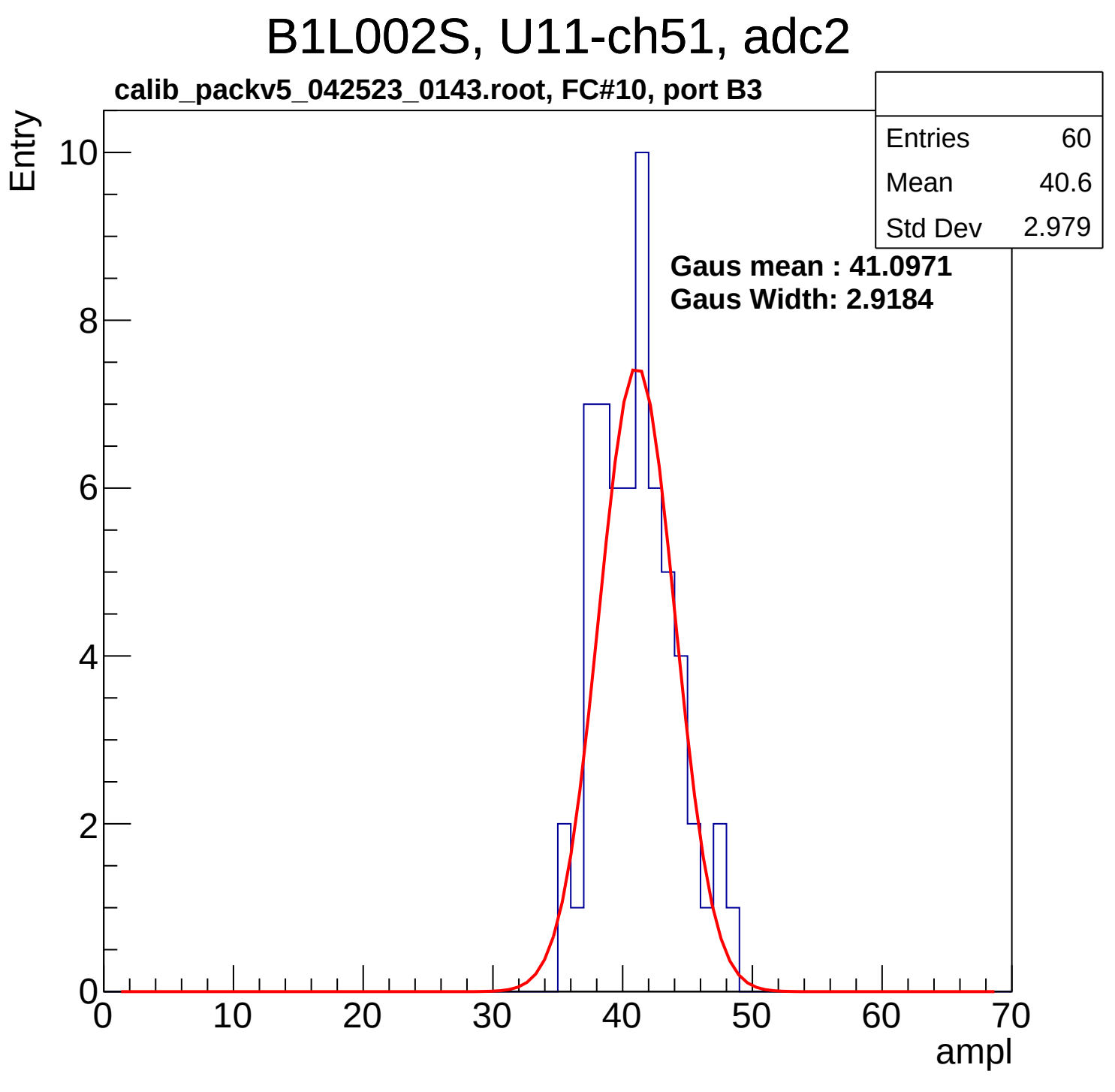
B1L002S, U11-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.6
Std Dev 2.979
Gaus mean : 41.0971
Gaus Width: 2.9184
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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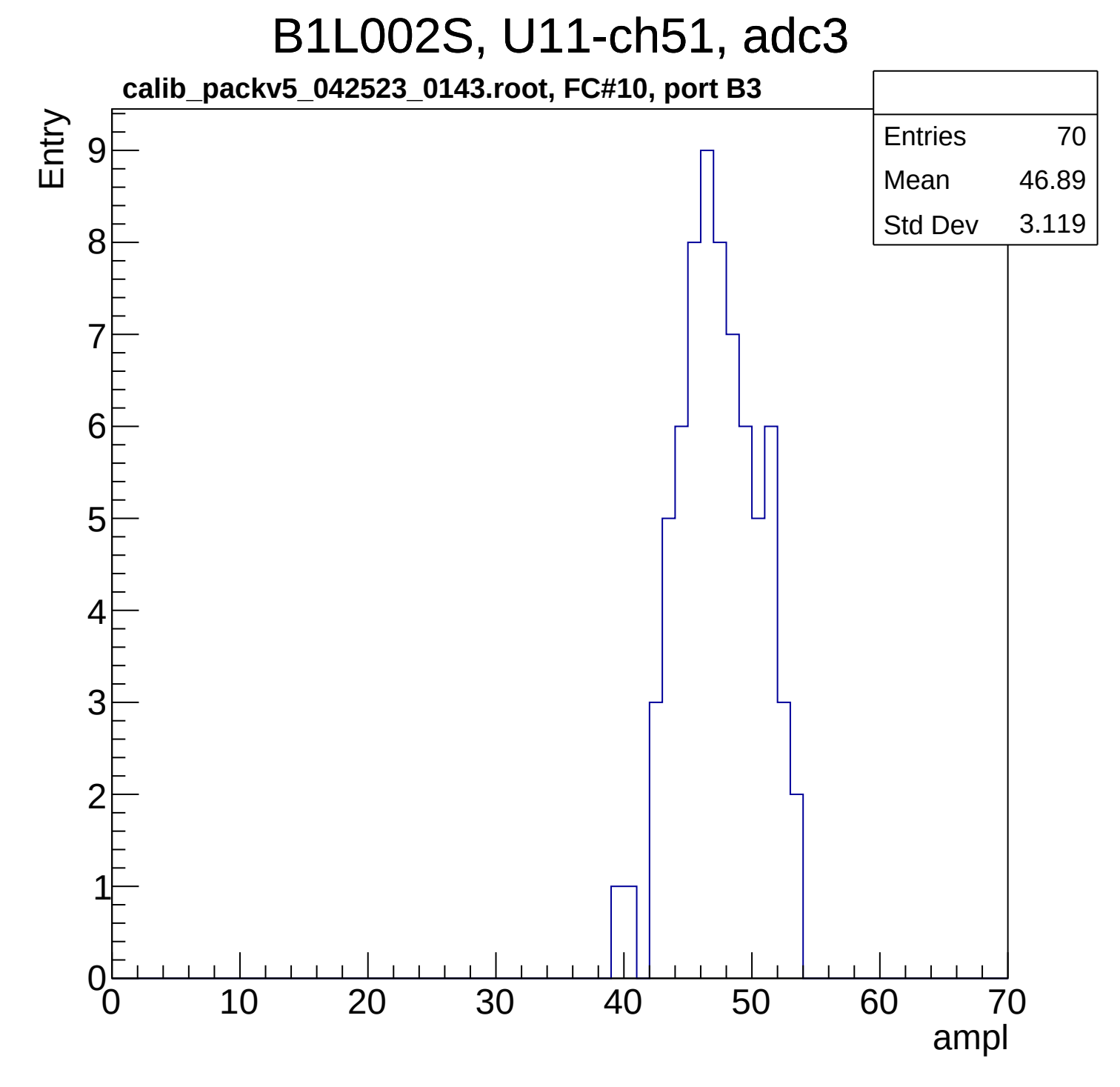
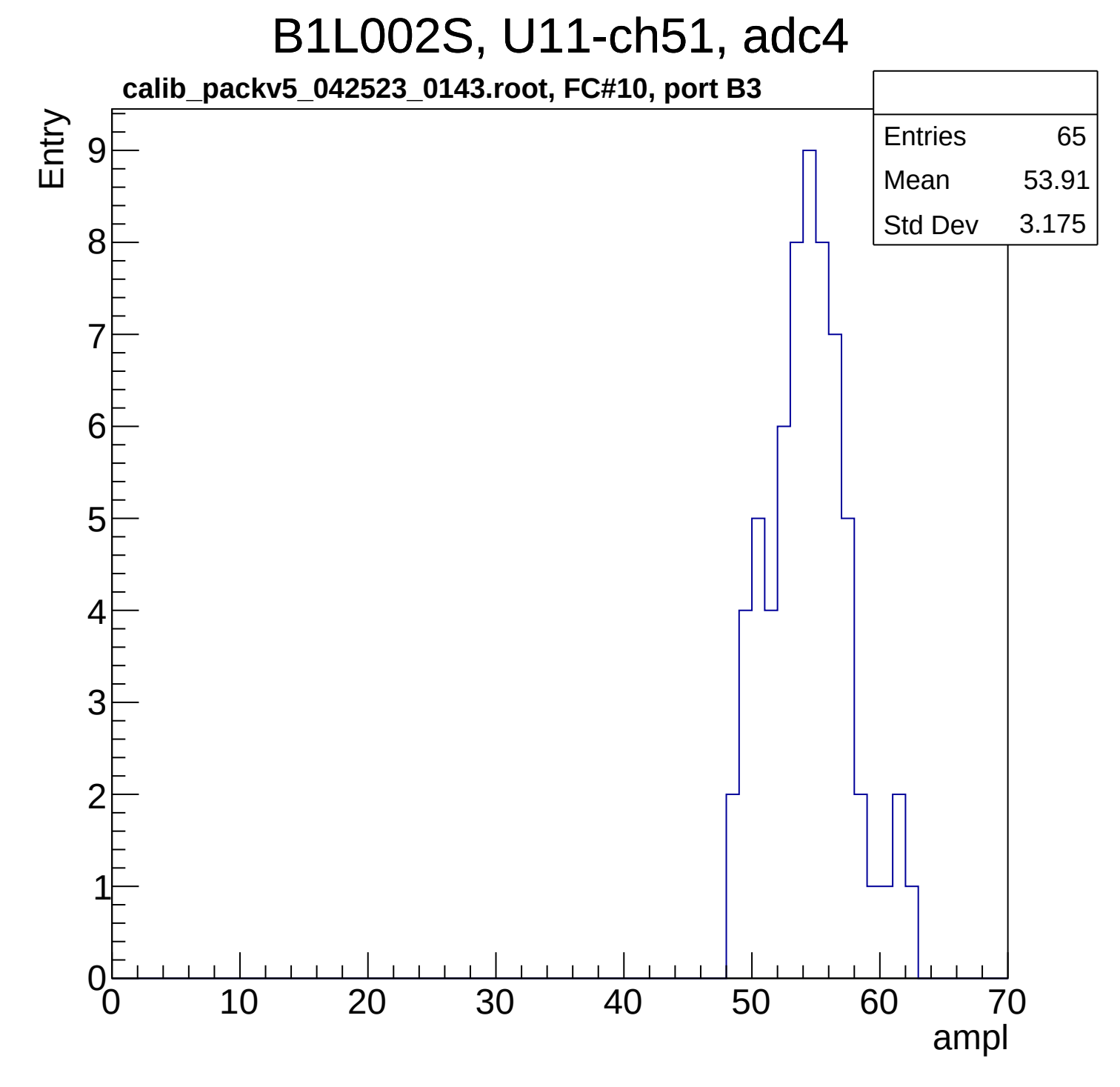
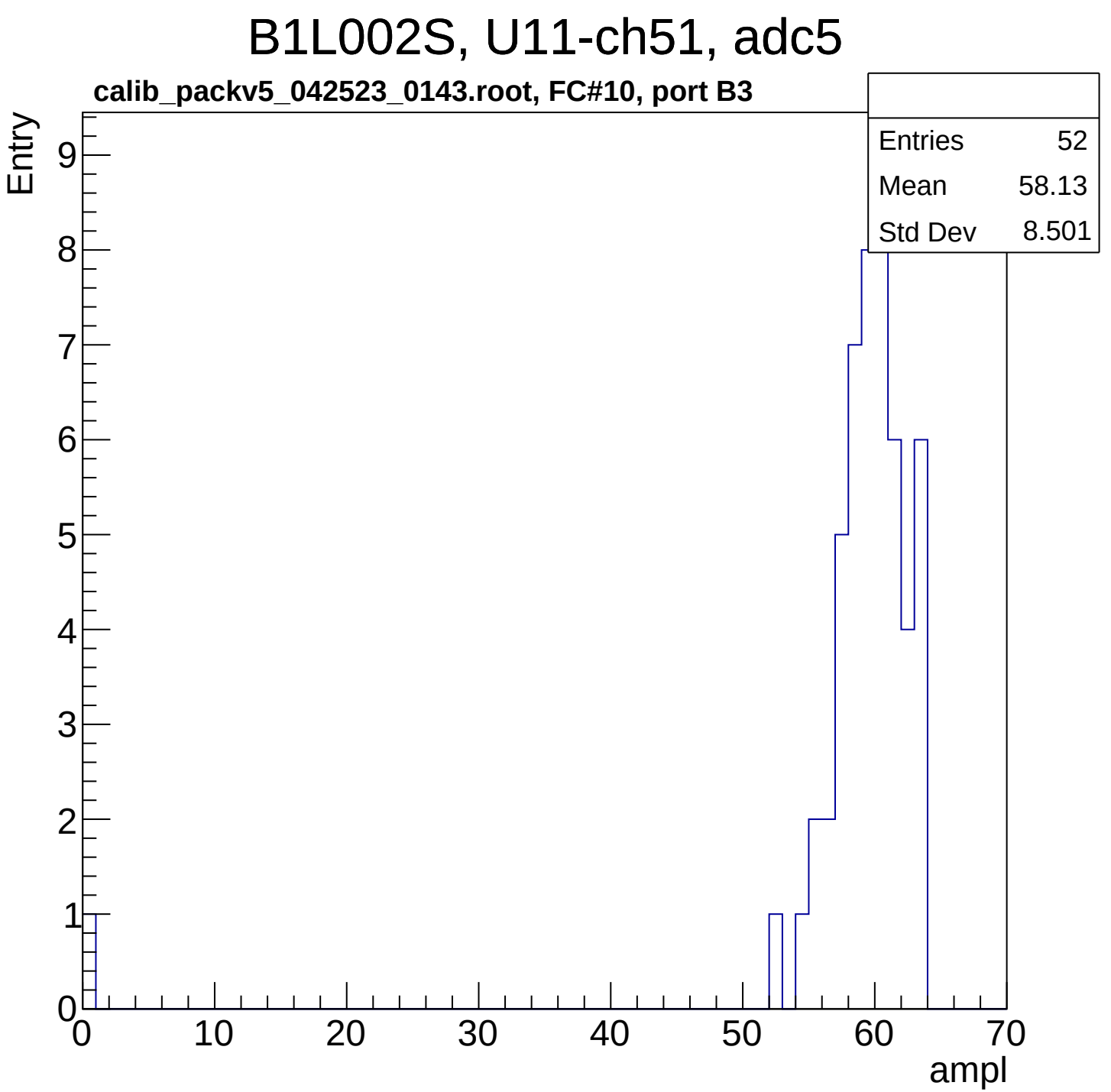

B1L002S, U11-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.89
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.91
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.13
Std Dev 8.501
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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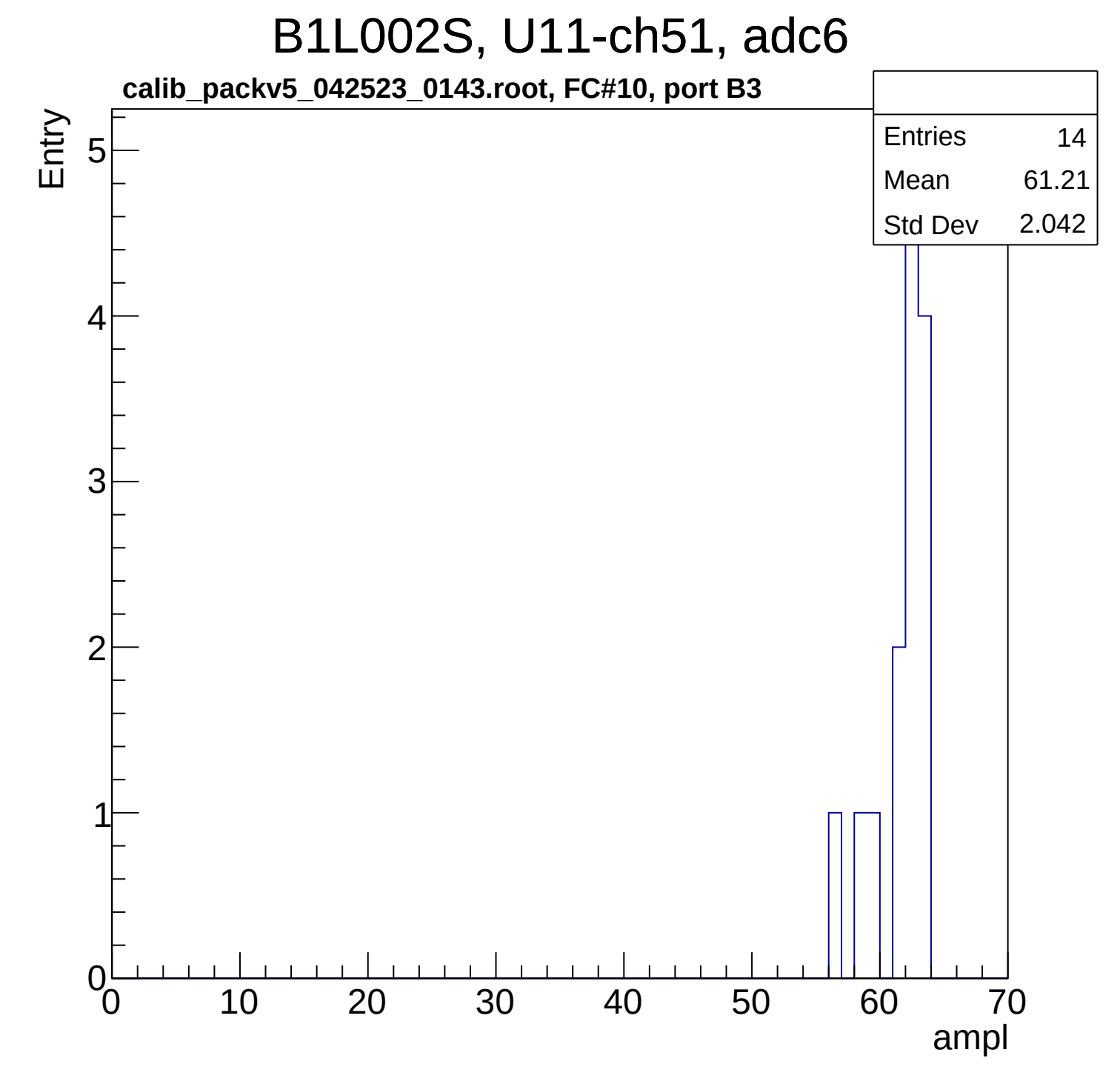
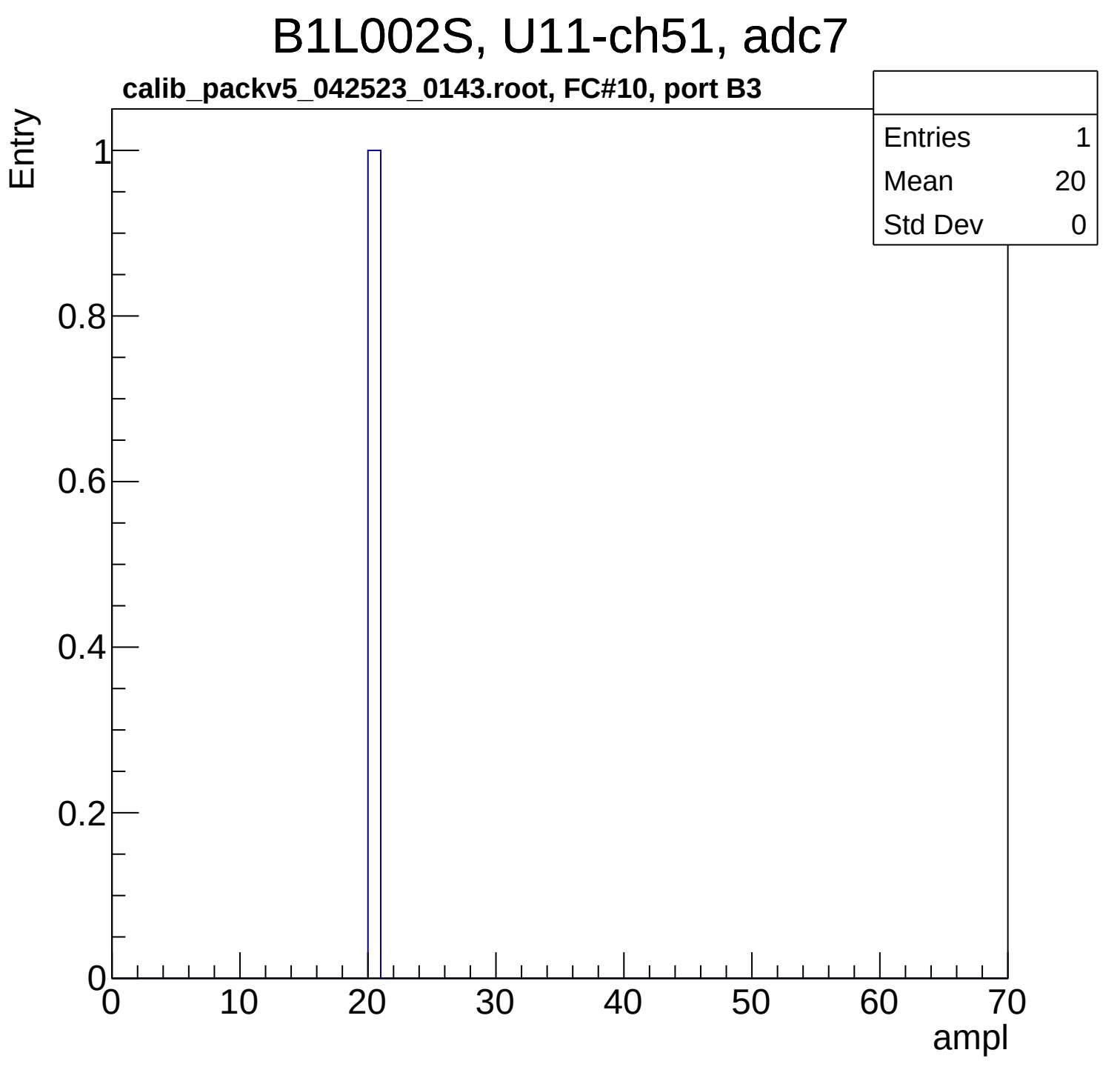

B1L002S, U11-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.21
Std Dev 2.042
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
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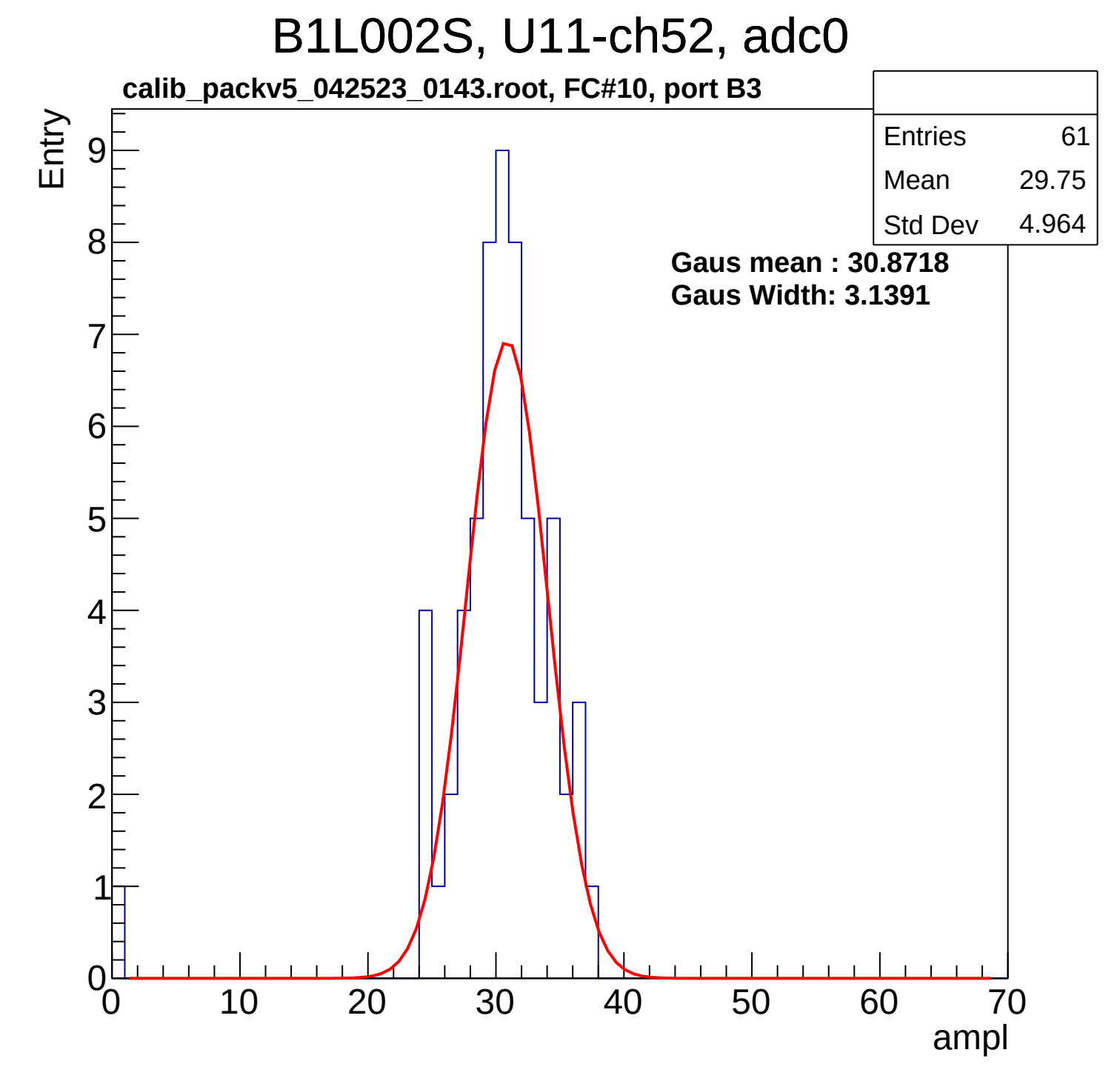
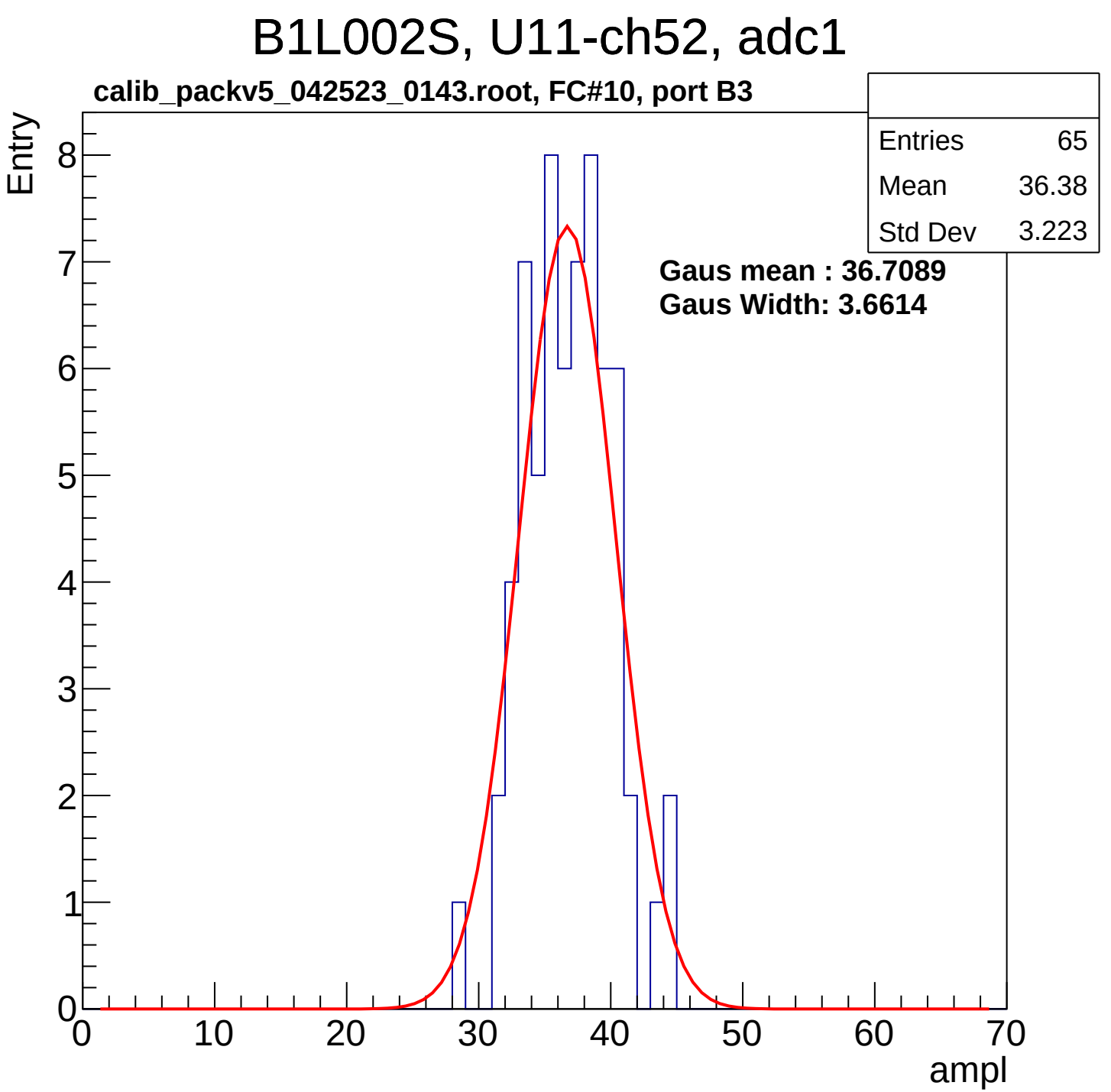
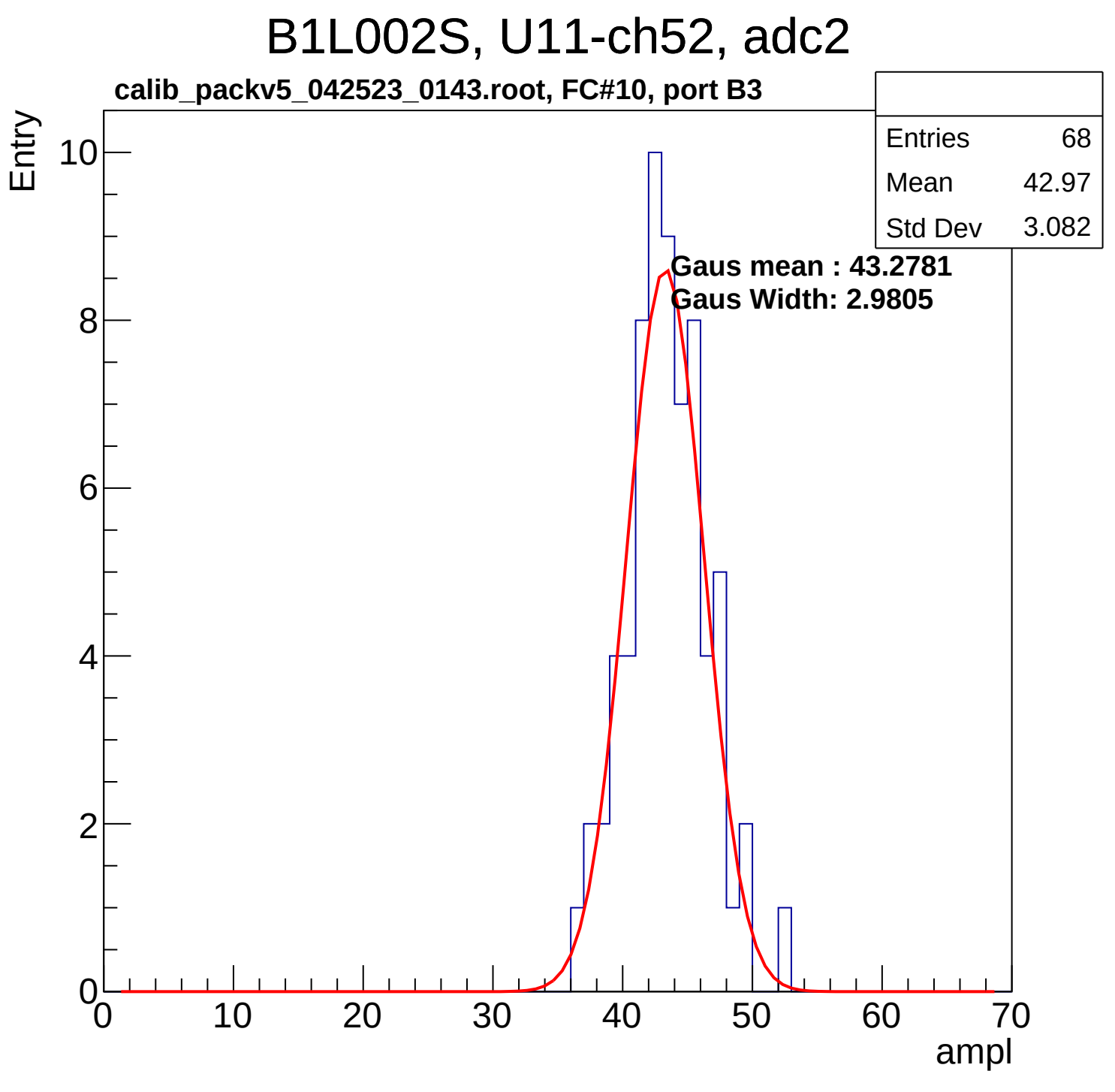
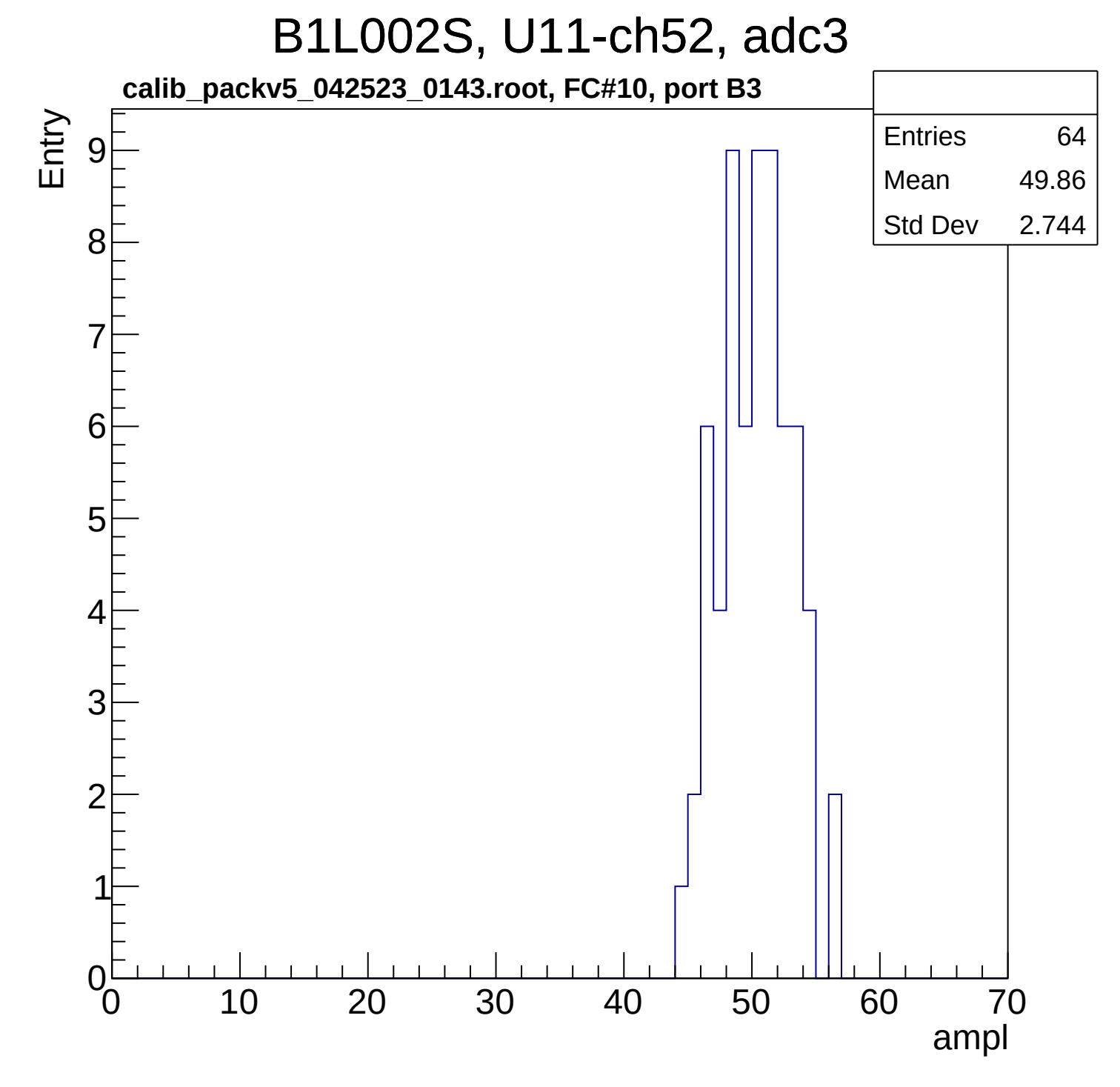
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.75
Std Dev 4.964
Gaus mean : 30.8718
Gaus Width: 3.1391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 36.38
Std Dev 3.223
Gaus mean : 36.7089
Gaus Width: 3.6614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 42.97
Std Dev 3.082
Gaus mean : 43.2781
Gaus Width: 2.9805
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 49.86
Std Dev 2.744
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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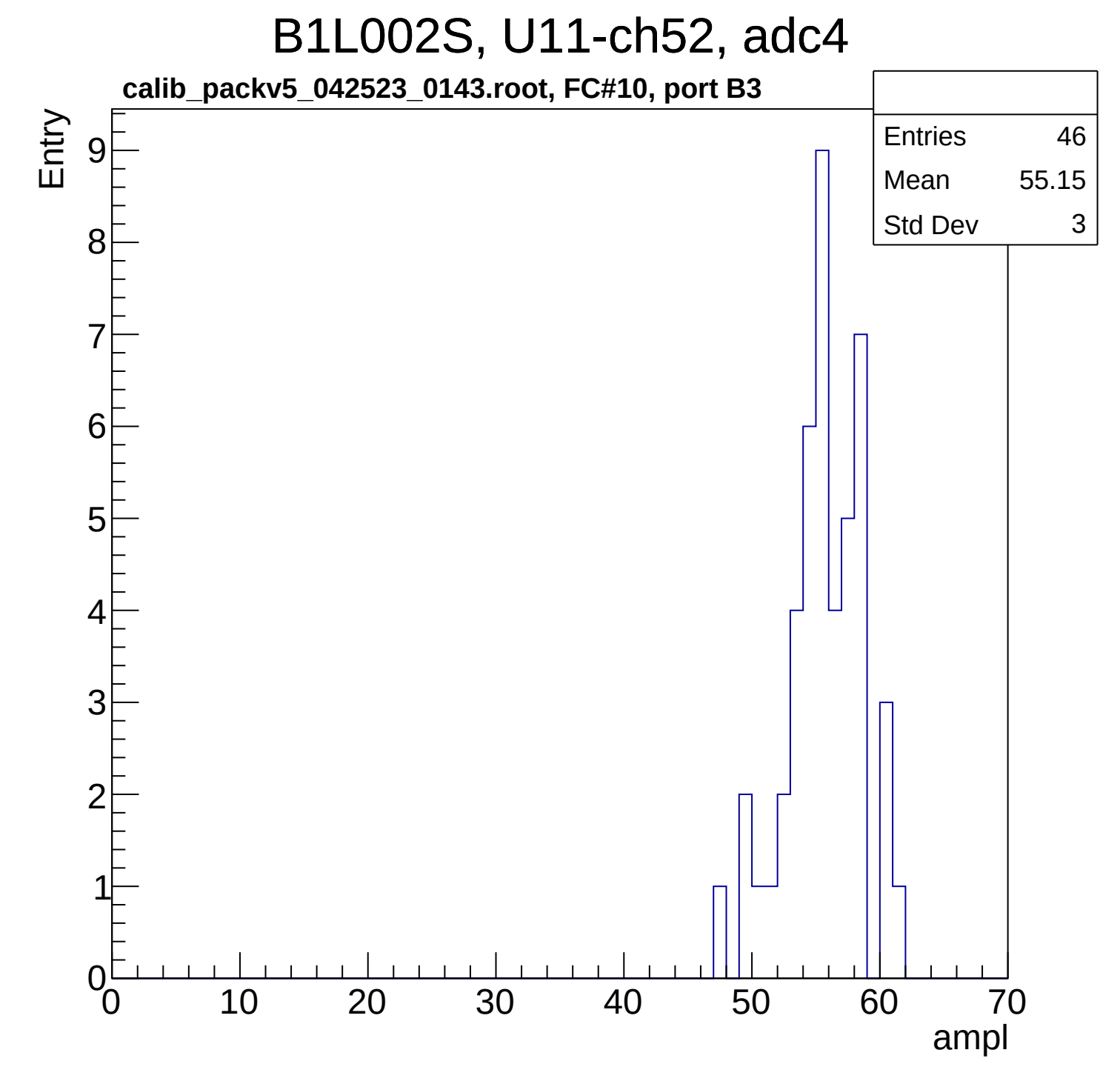
B1L002S, U11-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 55.15
Std Dev 3
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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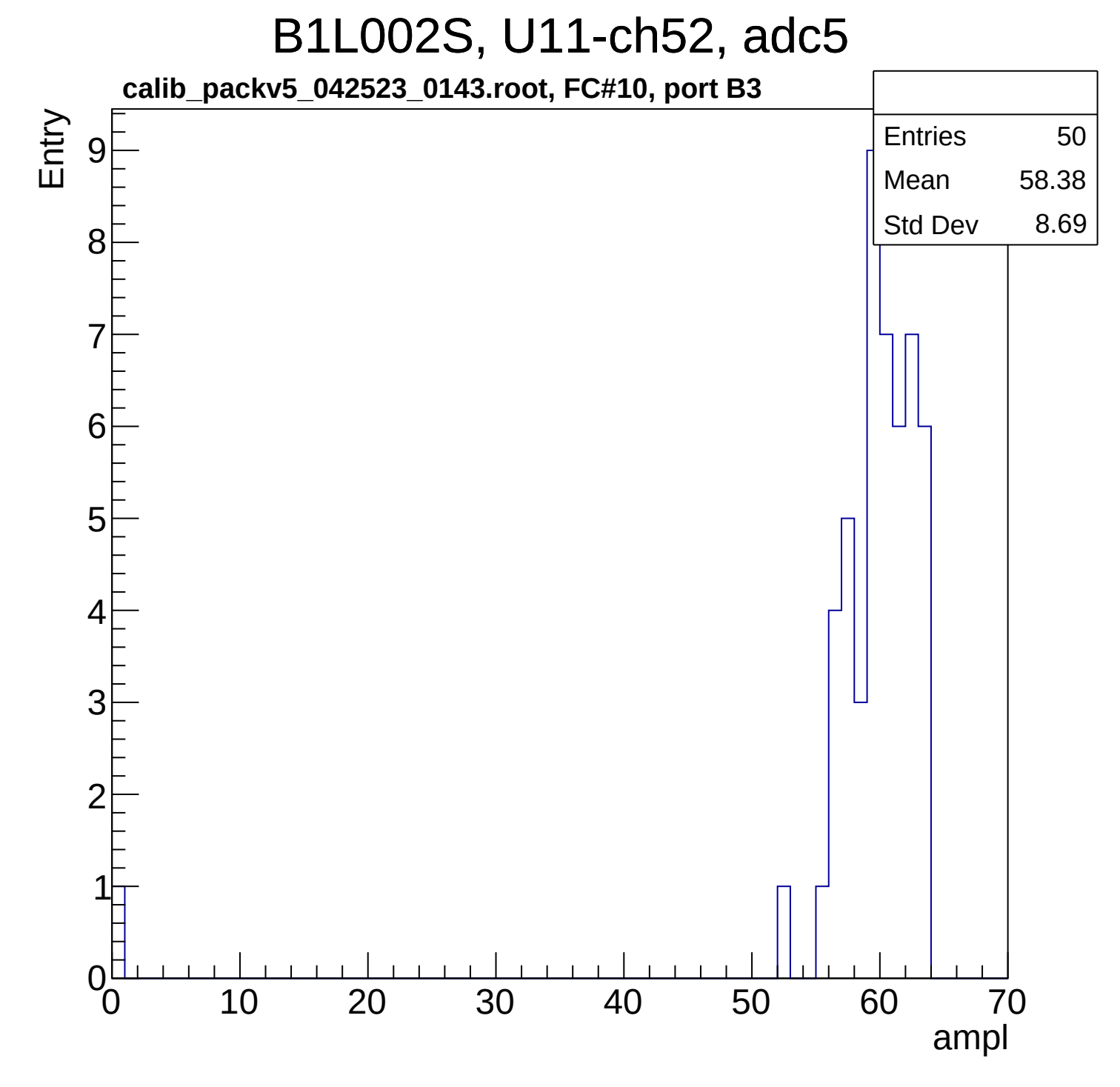
B1L002S, U11-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.38
Std Dev 8.69
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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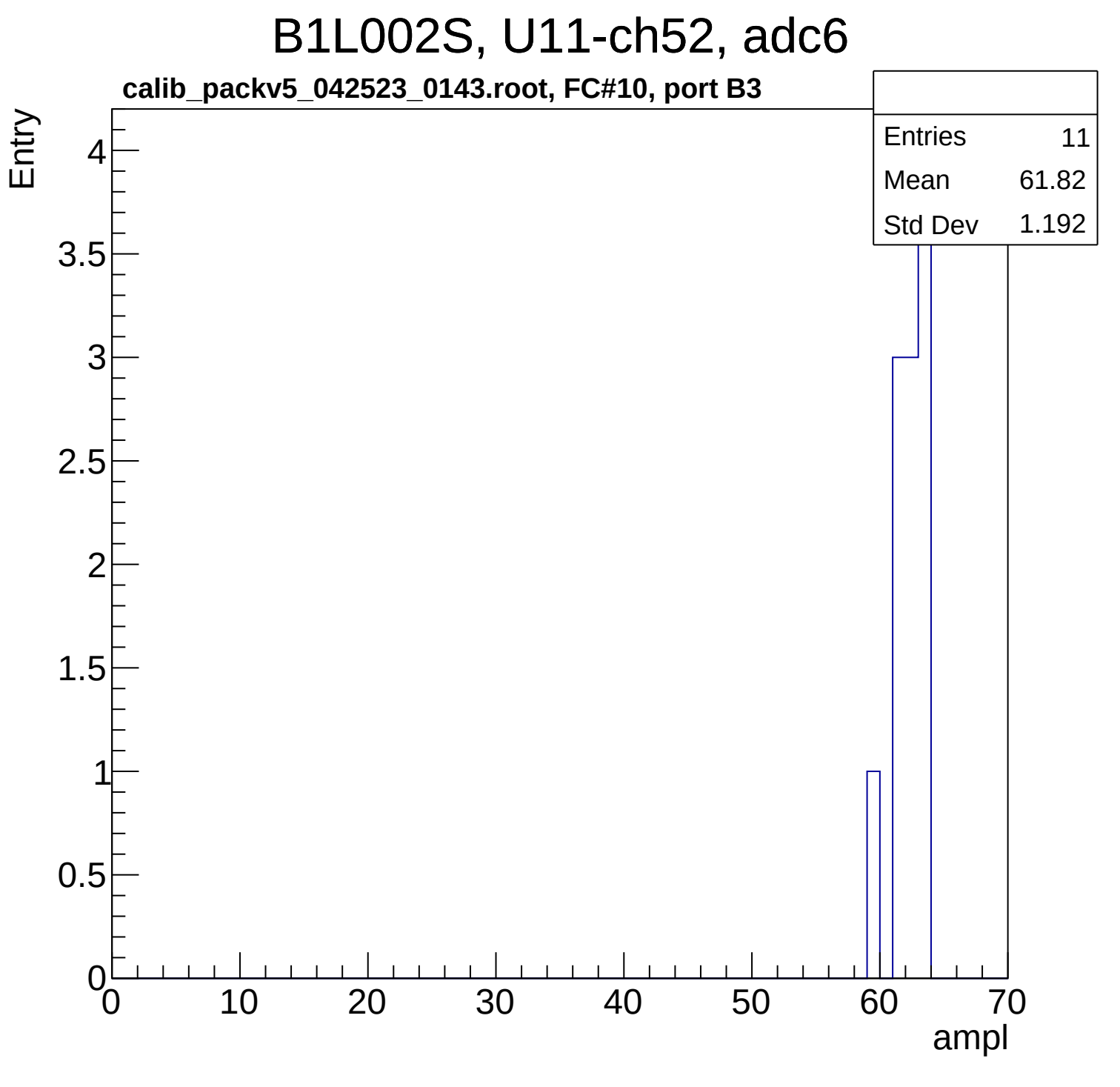
B1L002S, U11-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.82
Std Dev 1.192
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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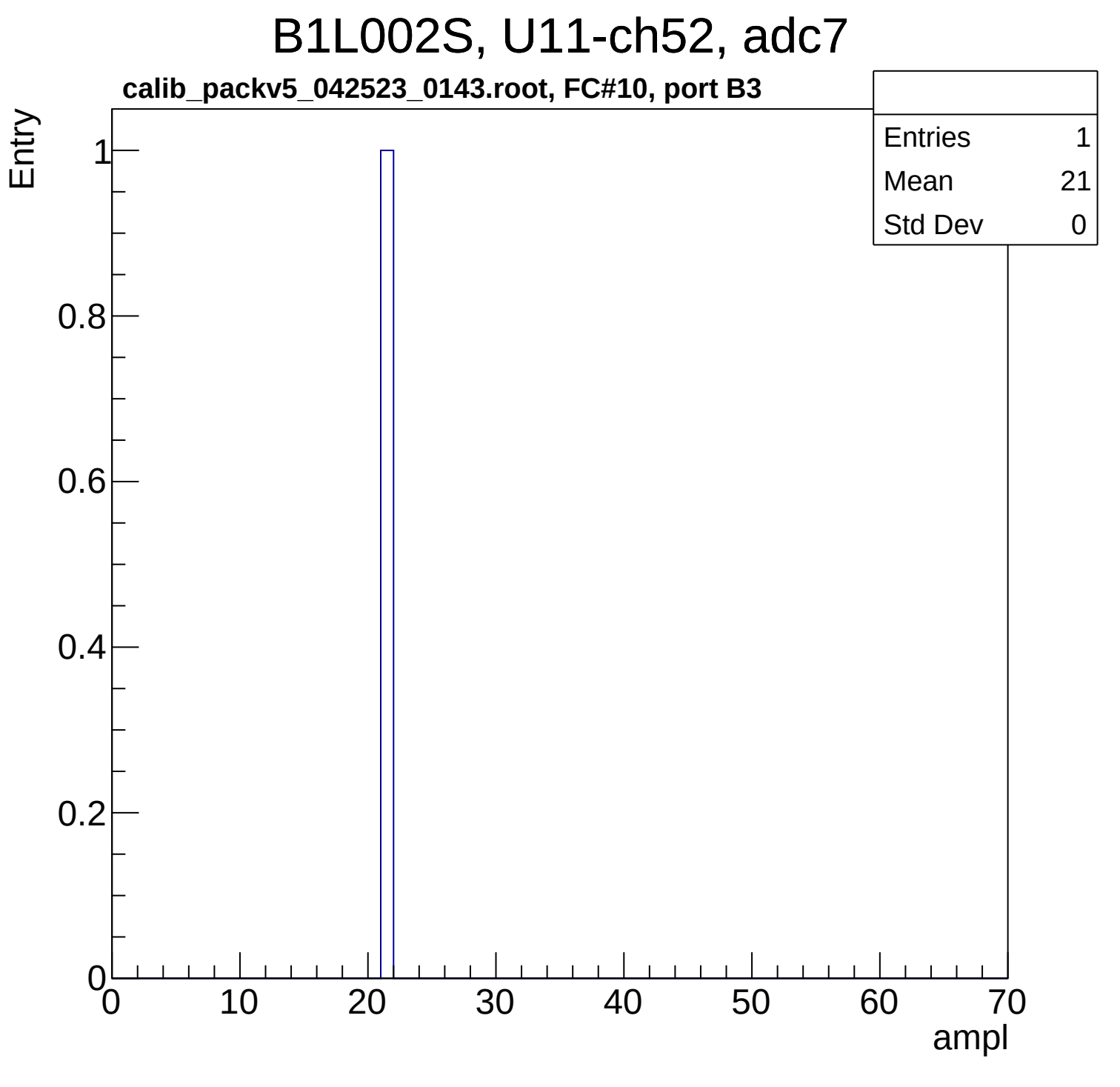
40
50
60
70


B1L002S, U11-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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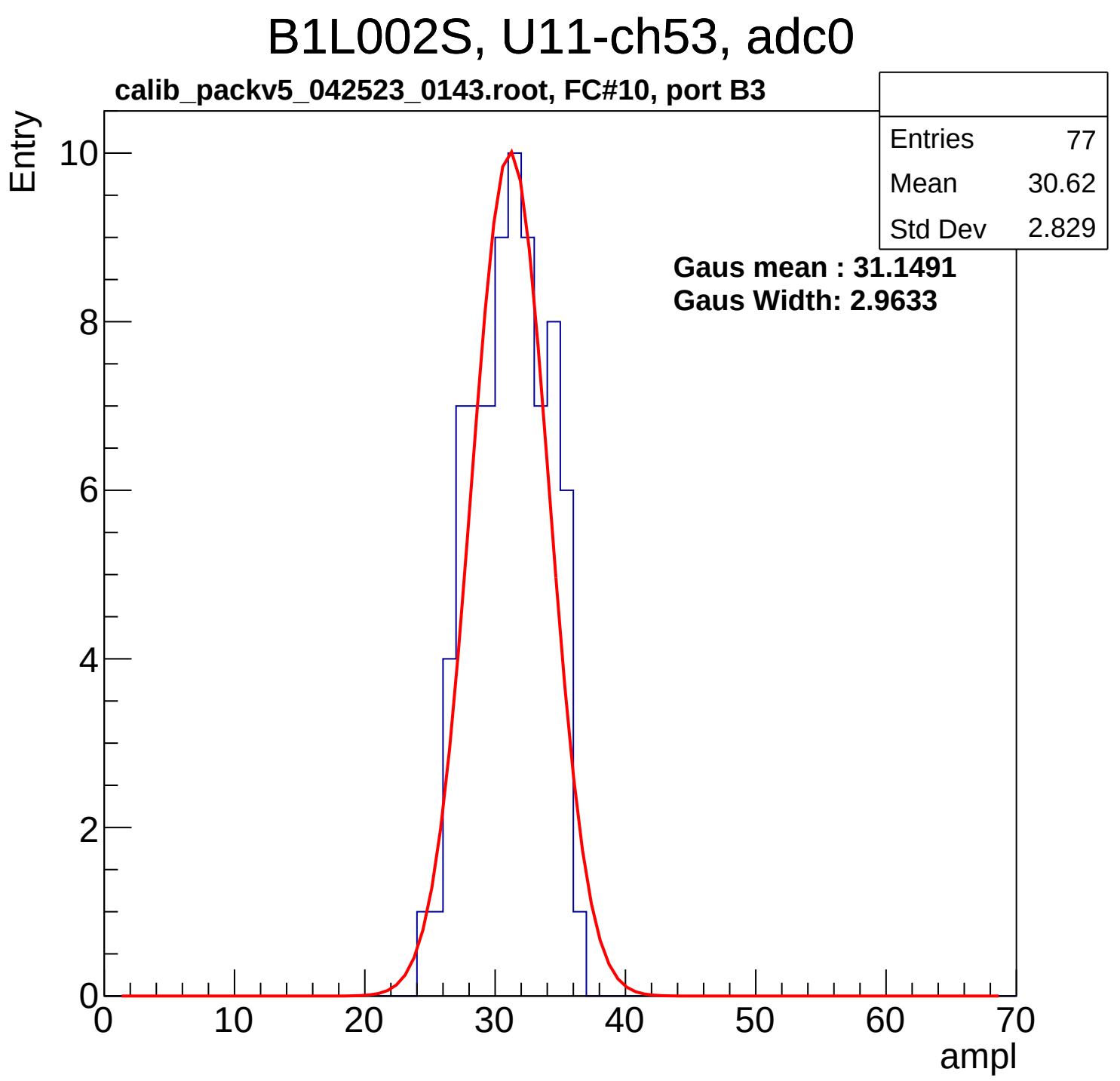
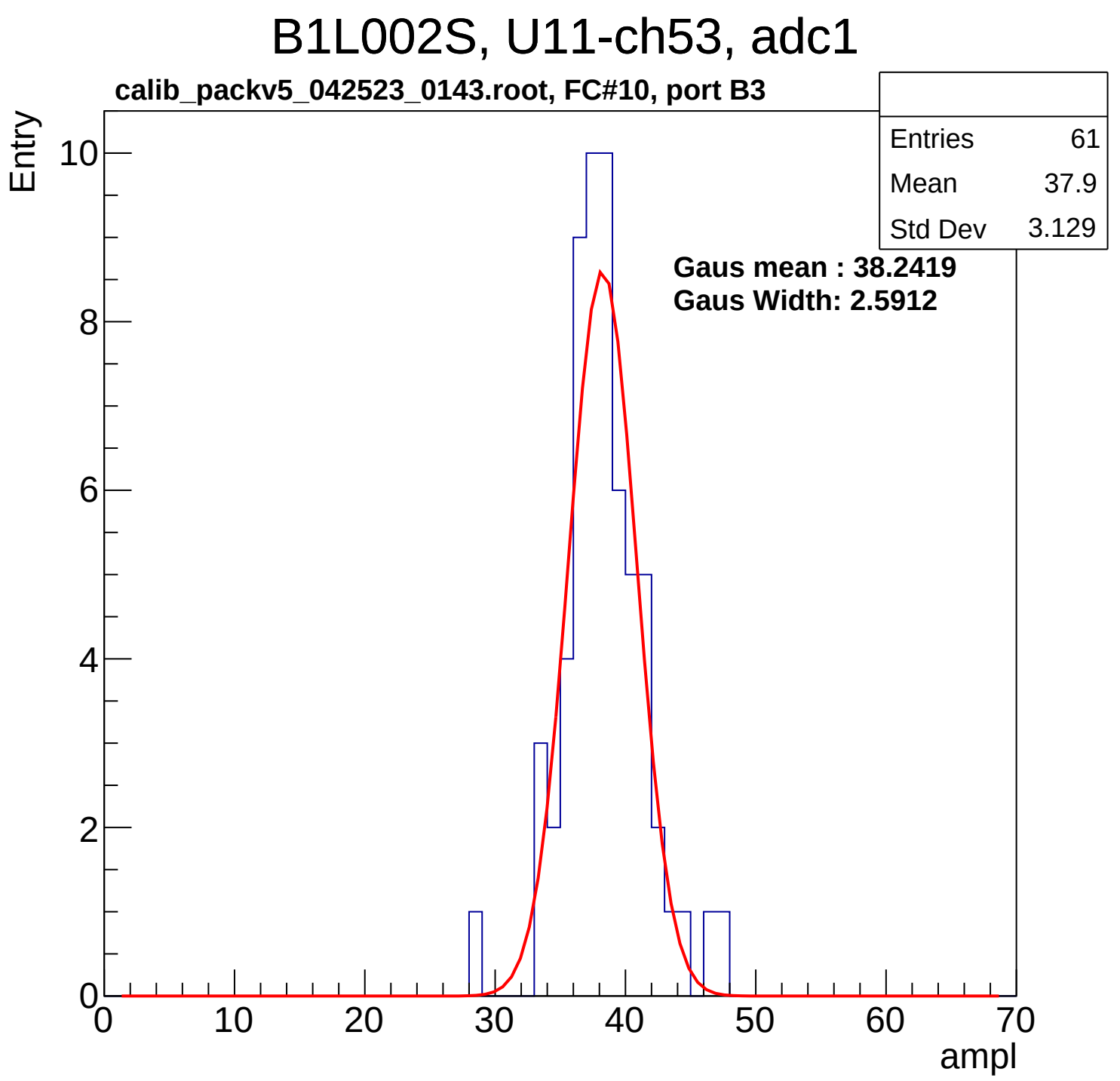
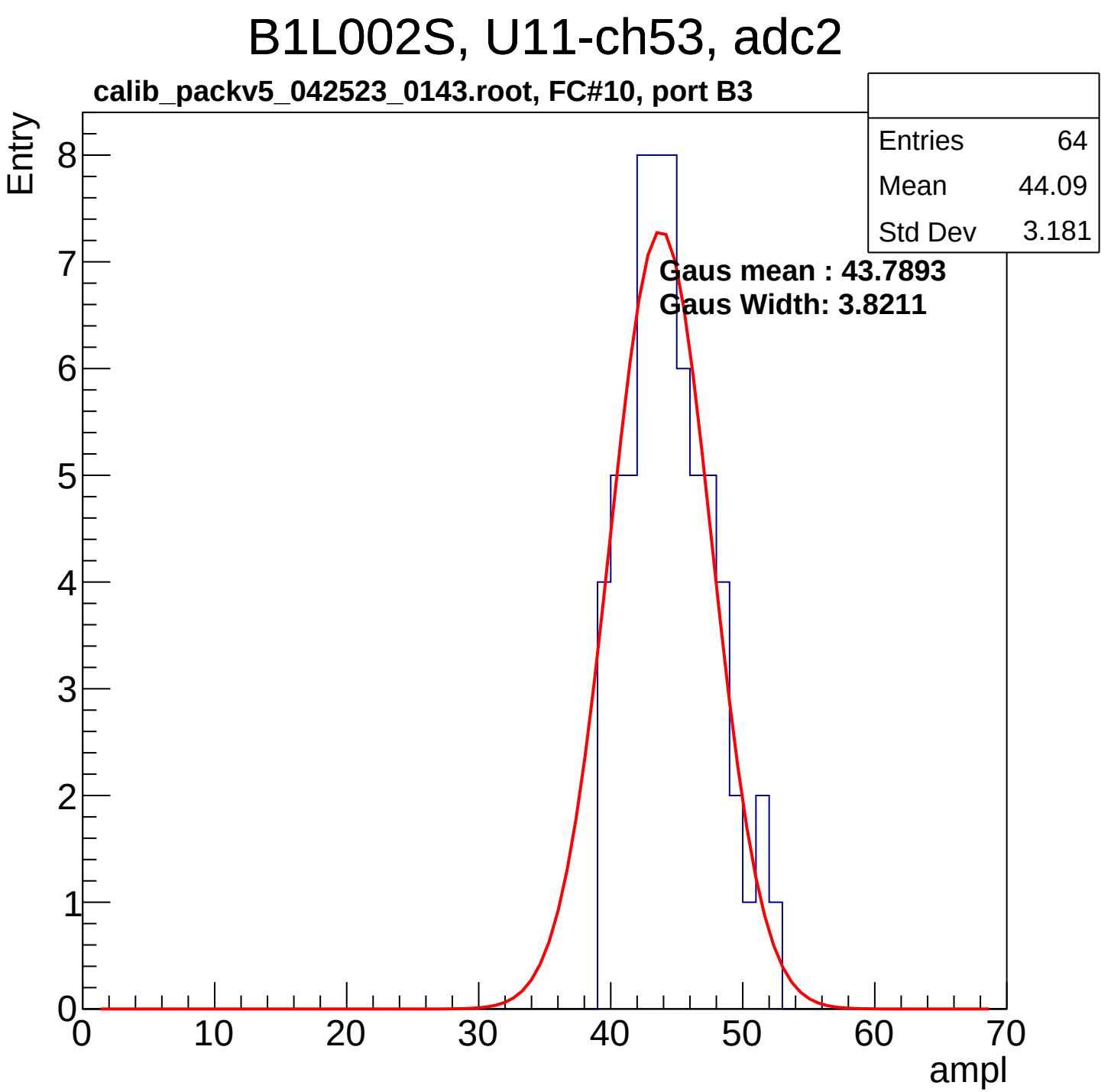
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 30.62
Std Dev 2.829
Gaus mean : 31.1491
Gaus Width: 2.9633
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 37.9
Std Dev 3.129
Gaus mean : 38.2419
Gaus Width: 2.5912
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 44.09
Std Dev 3.181
Gaus mean : 43.7893
Gaus Width: 3.8211
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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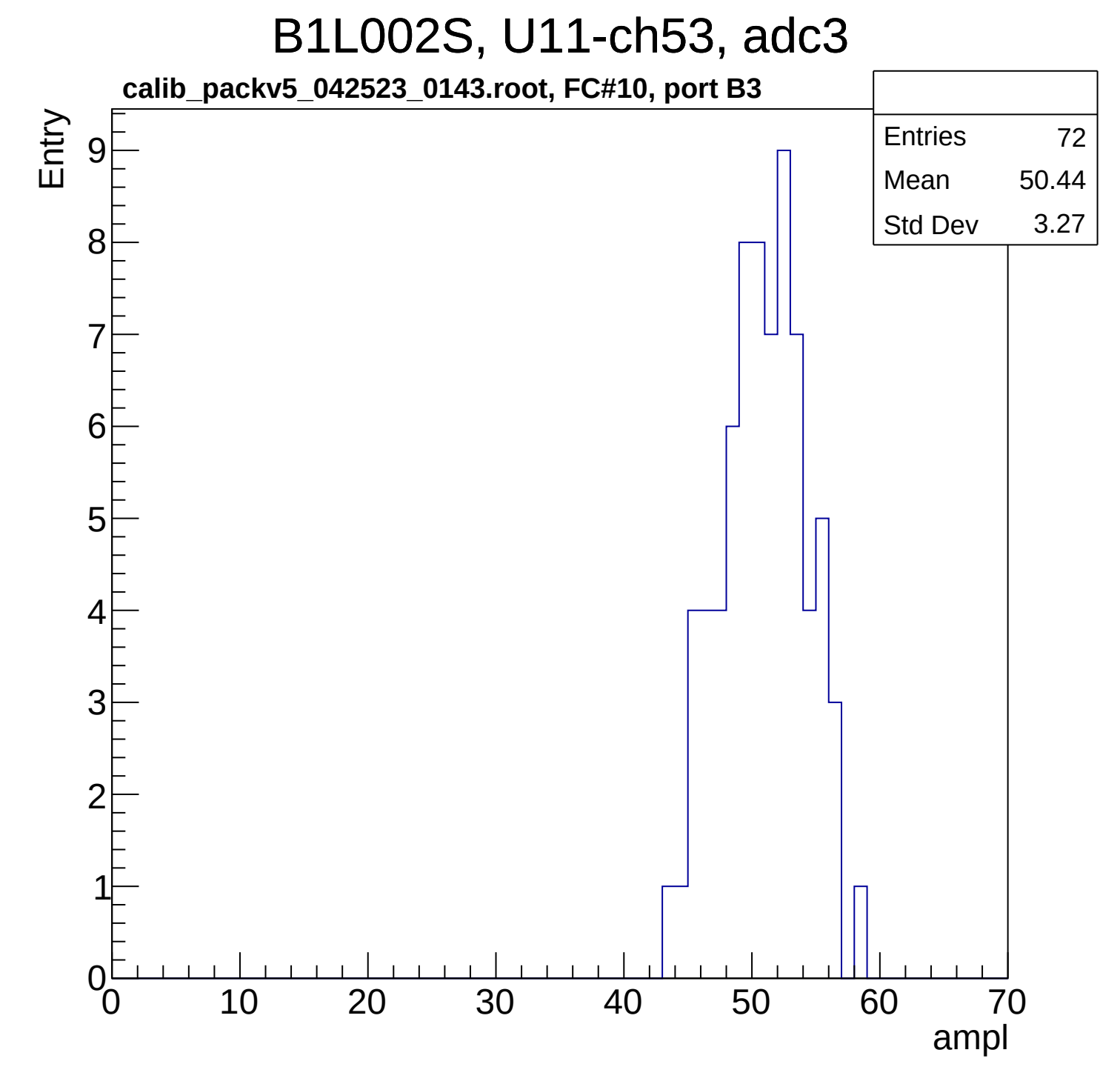
B1L002S, U11-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 50.44
Std Dev 3.27
Entry
ampl
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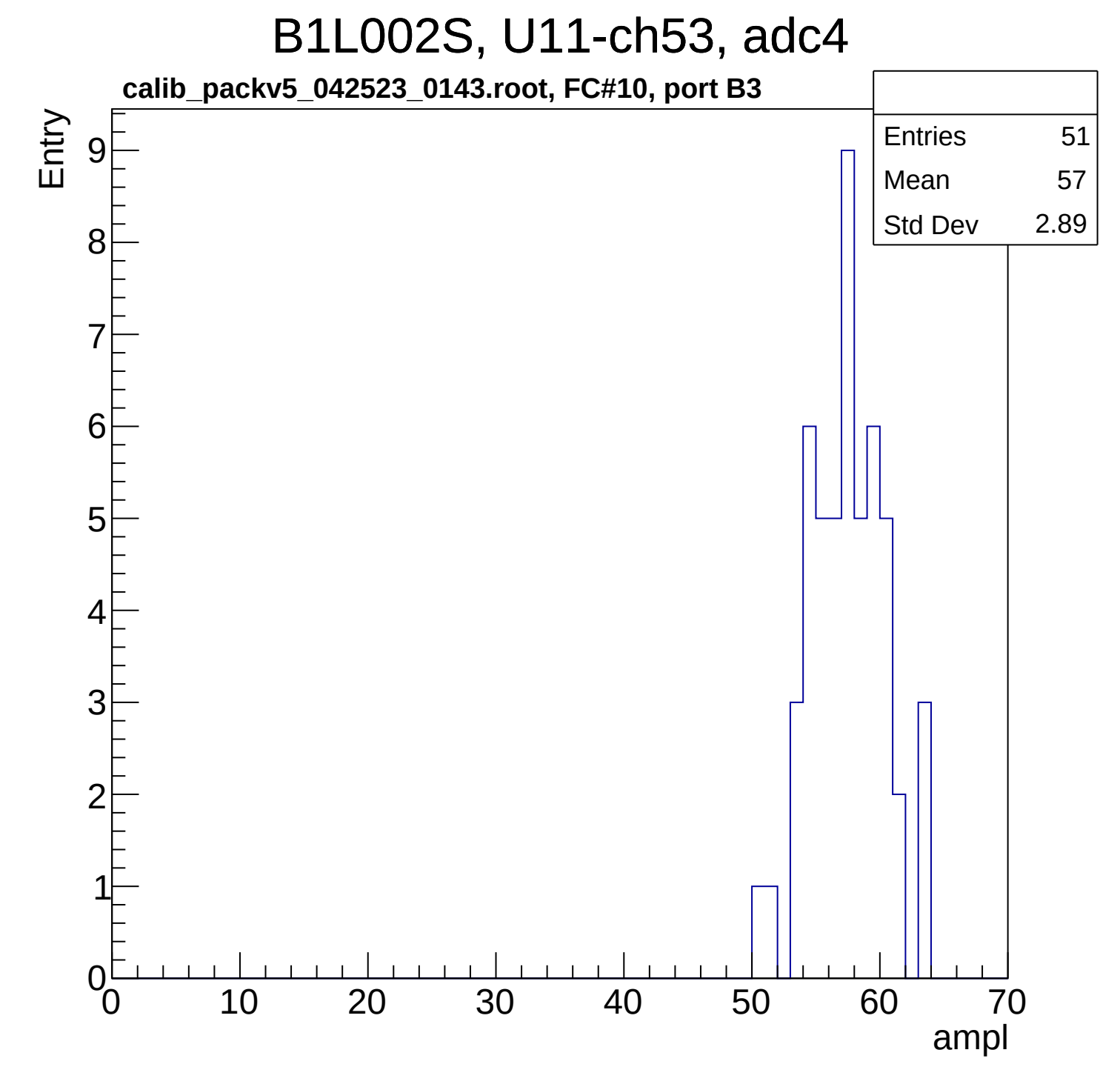
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57
Std Dev 2.89
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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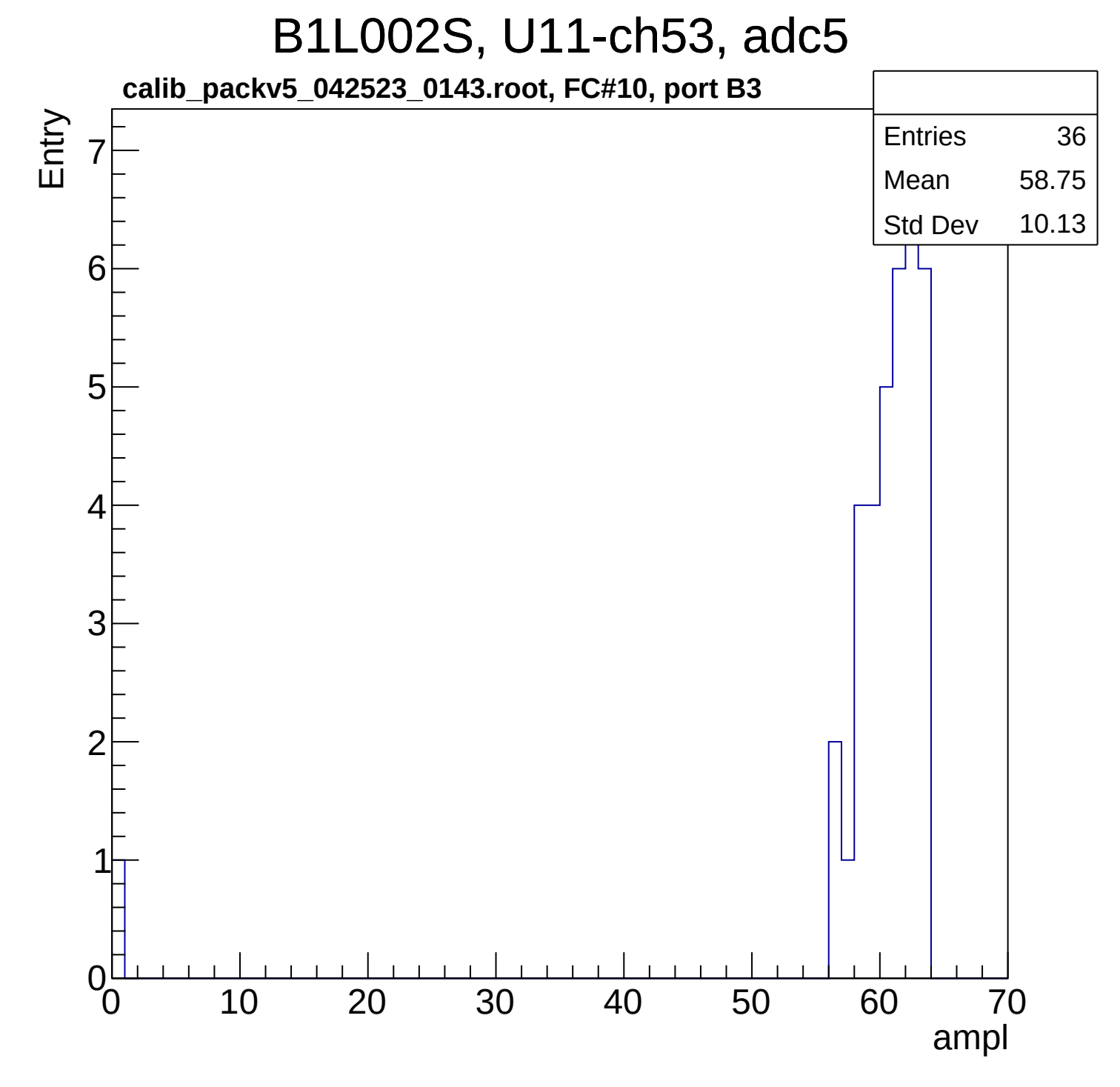
B1L002S, U11-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 58.75
Std Dev 10.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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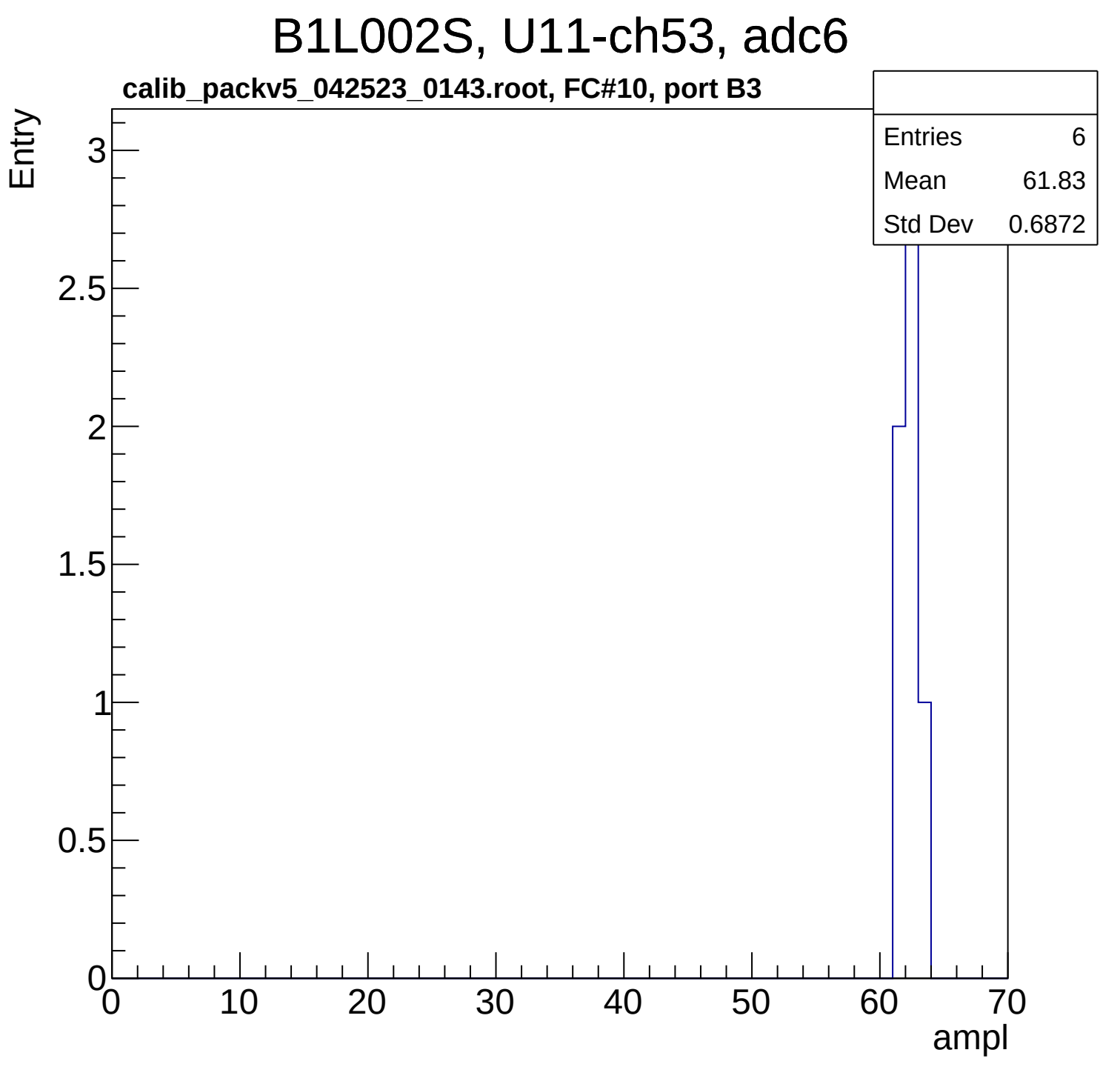
B1L002S, U11-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.83
Std Dev 0.6872
Entry
ampl
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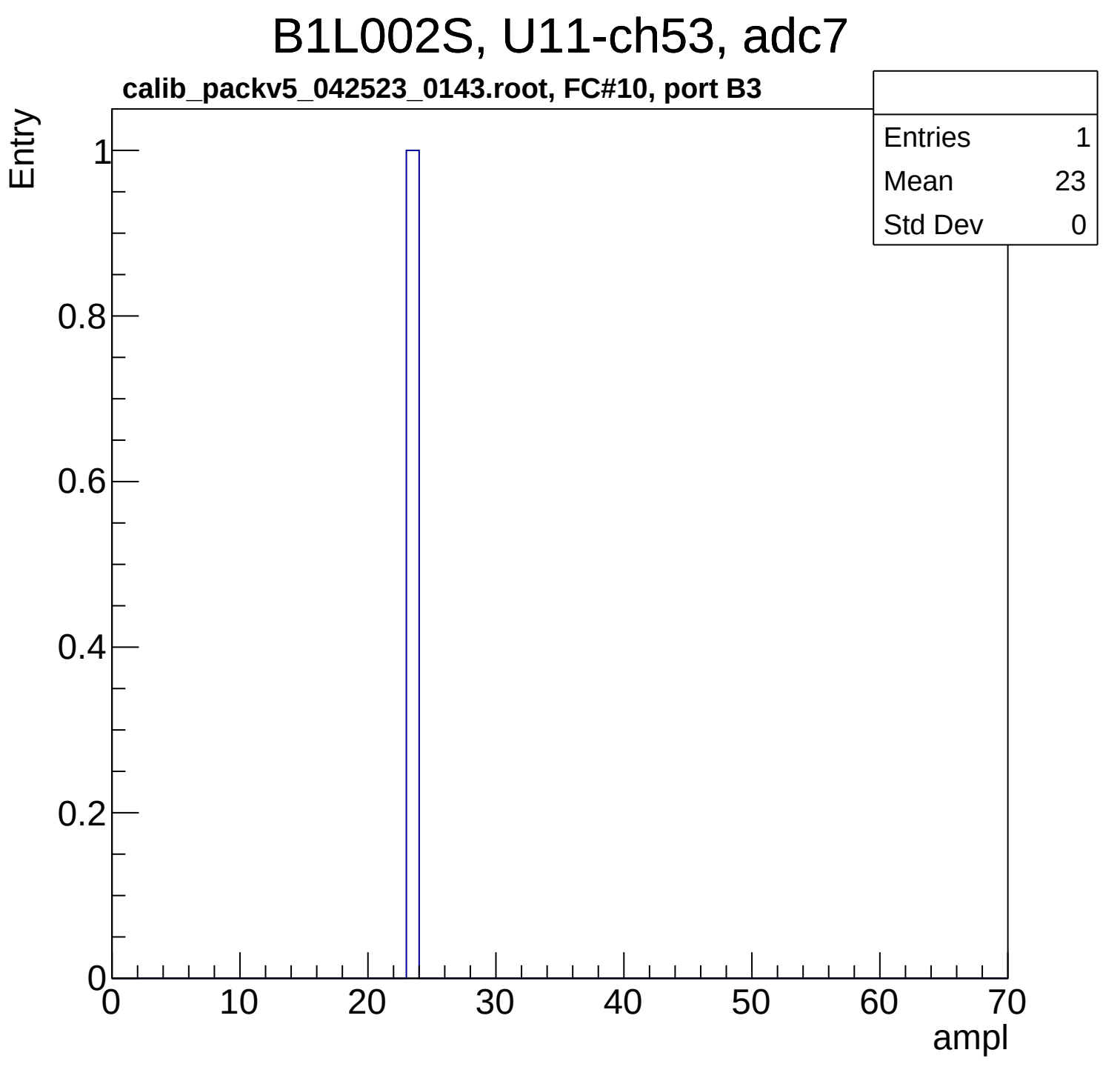
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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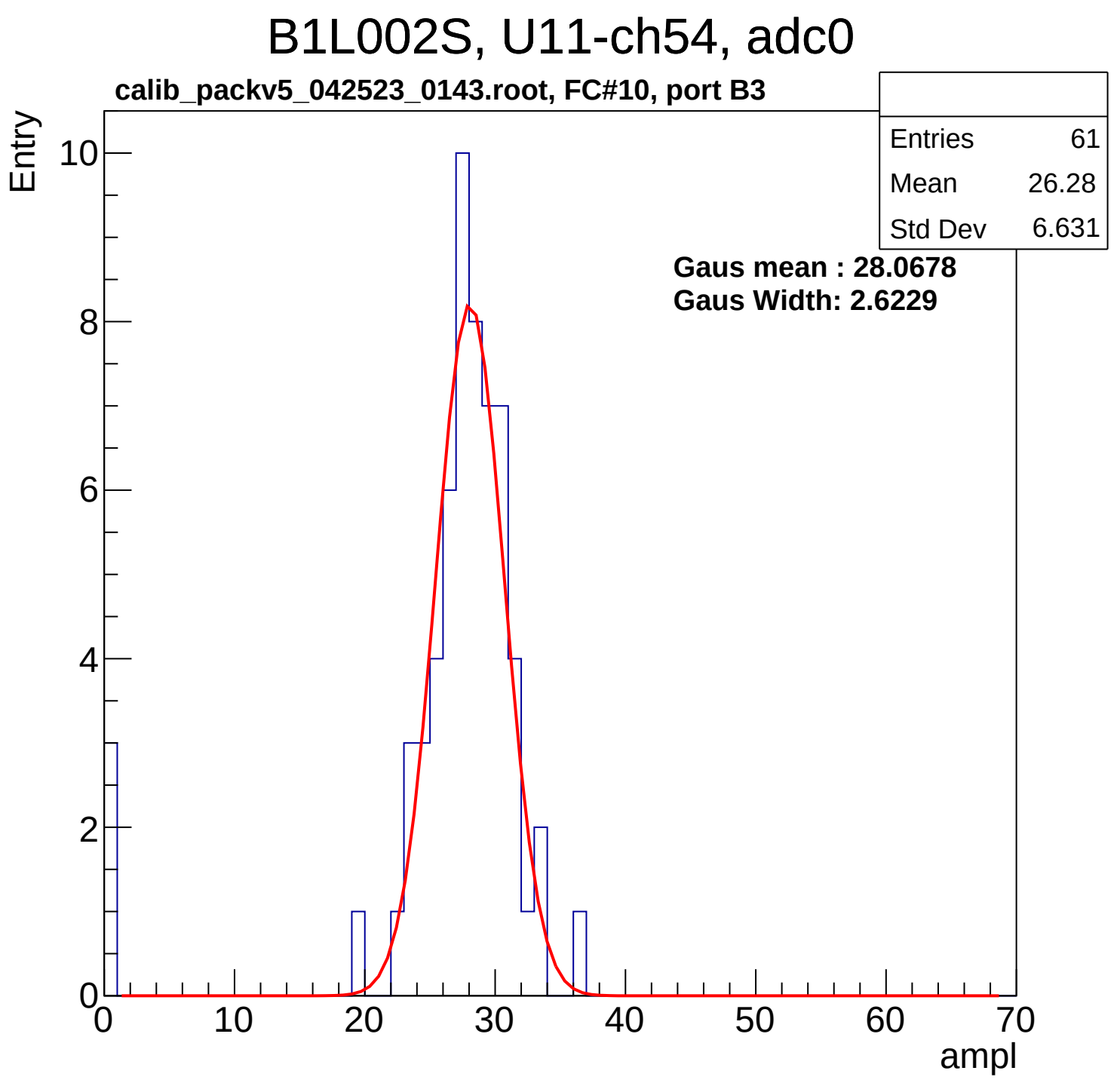
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.28
Std Dev 6.631
Gaus mean : 28.0678
Gaus Width: 2.6229
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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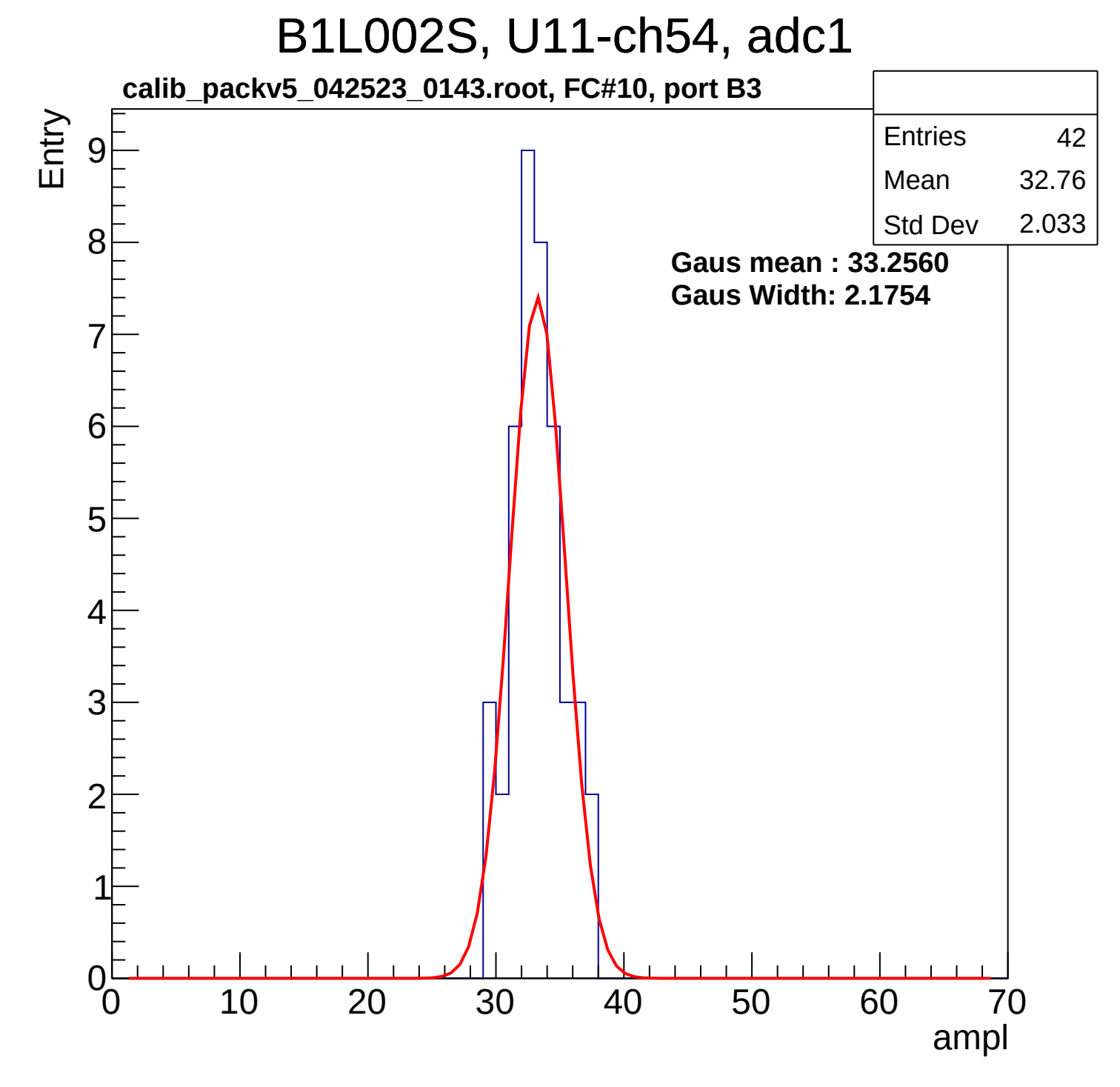
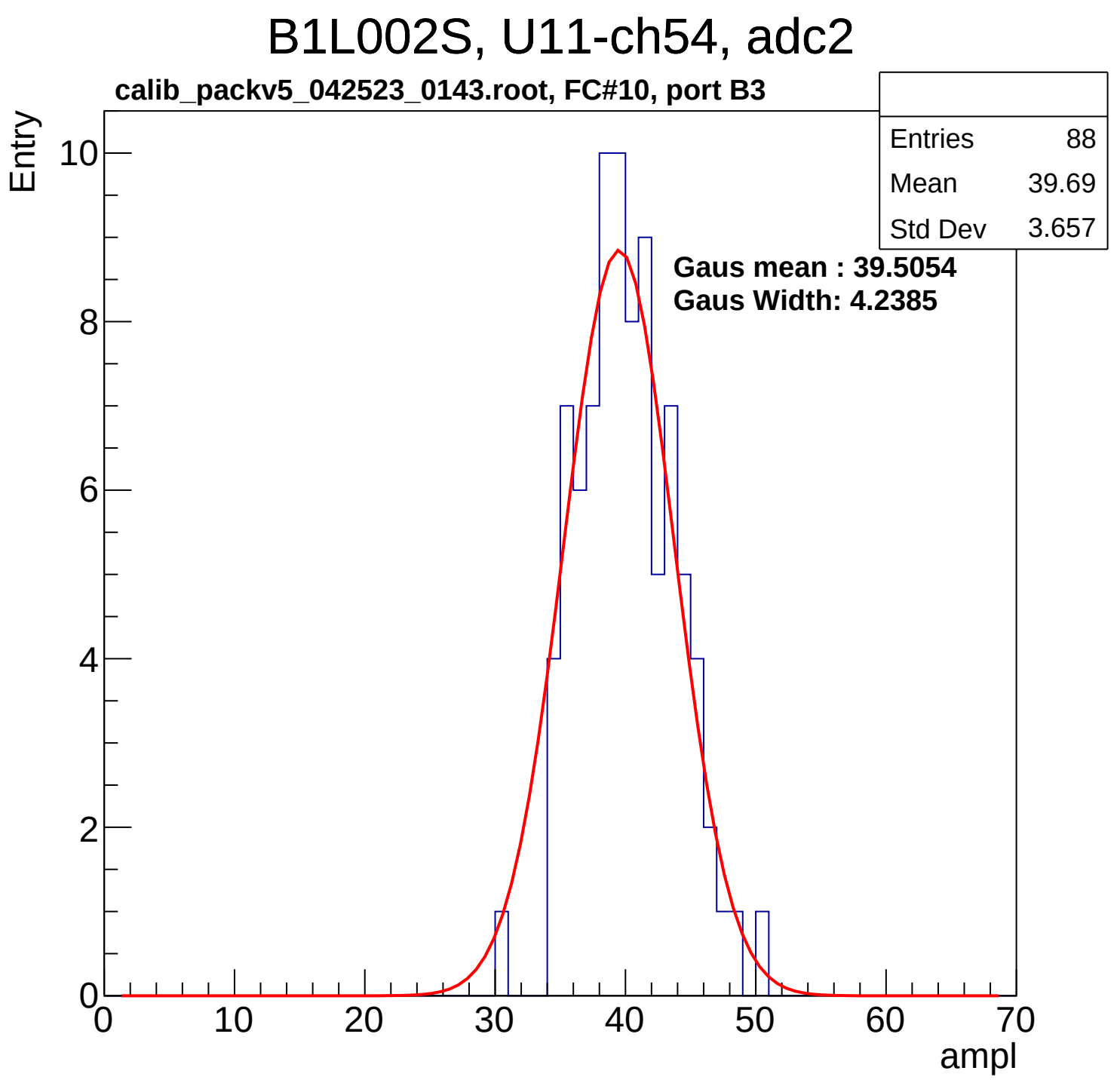

B1L002S, U11-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 32.76
Std Dev 2.033
Gaus mean : 33.2560
Gaus Width: 2.1754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 88
Mean 39.69
Std Dev 3.657
Gaus mean : 39.5054
Gaus Width: 4.2385
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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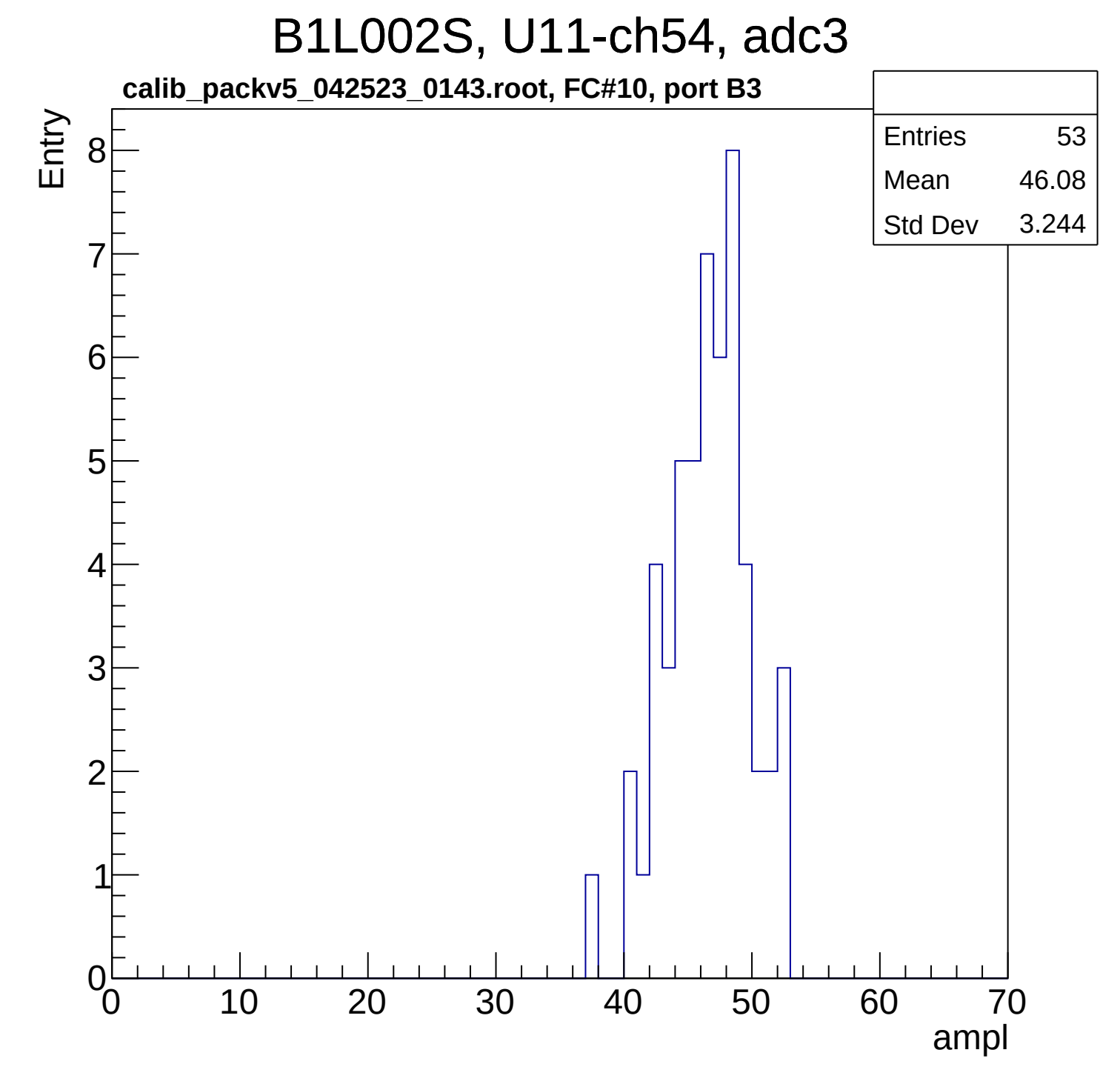
B1L002S, U11-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 46.08
Std Dev 3.244
Entry
ampl
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2
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4
5
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0
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40
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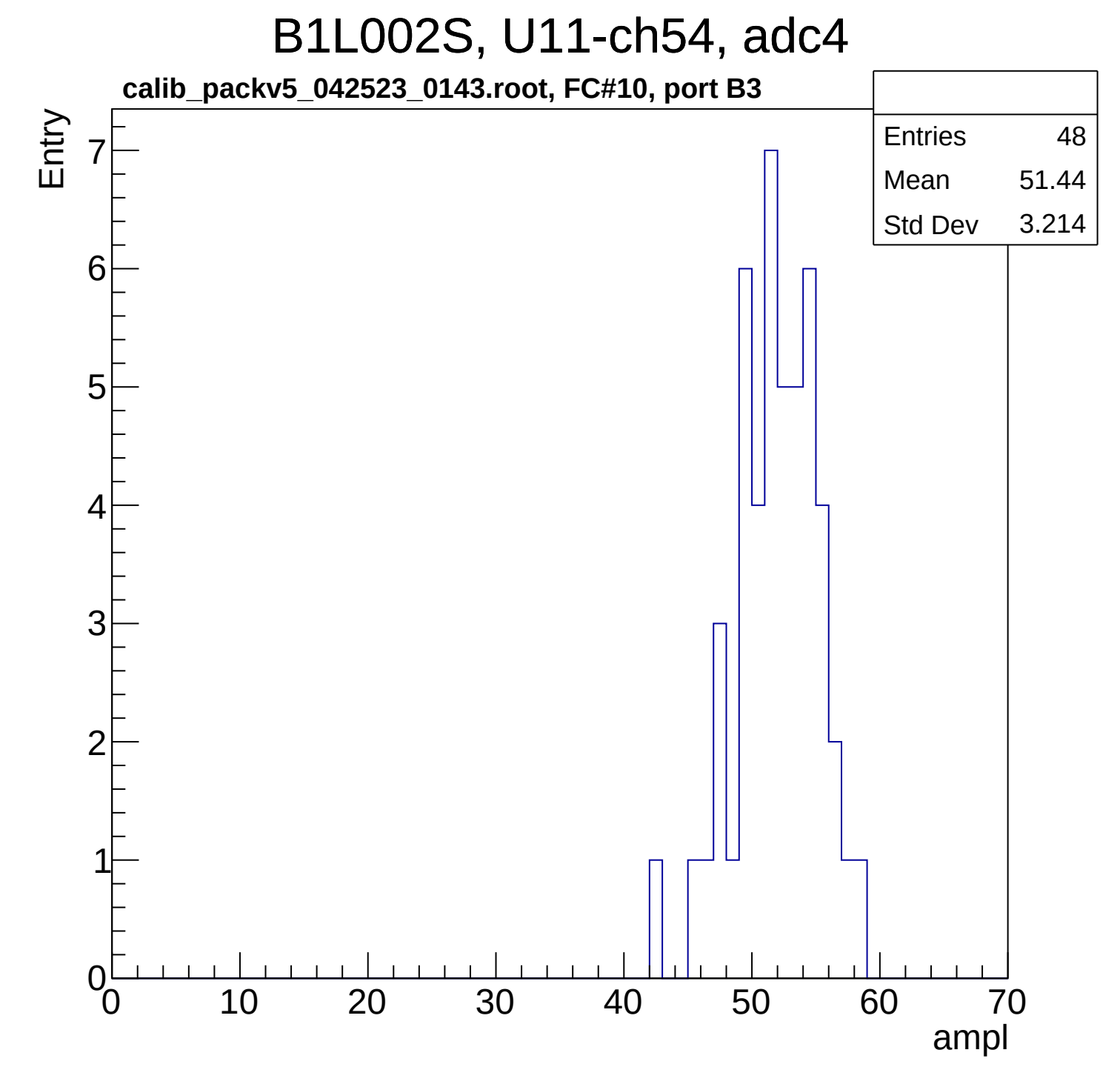
B1L002S, U11-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 51.44
Std Dev 3.214
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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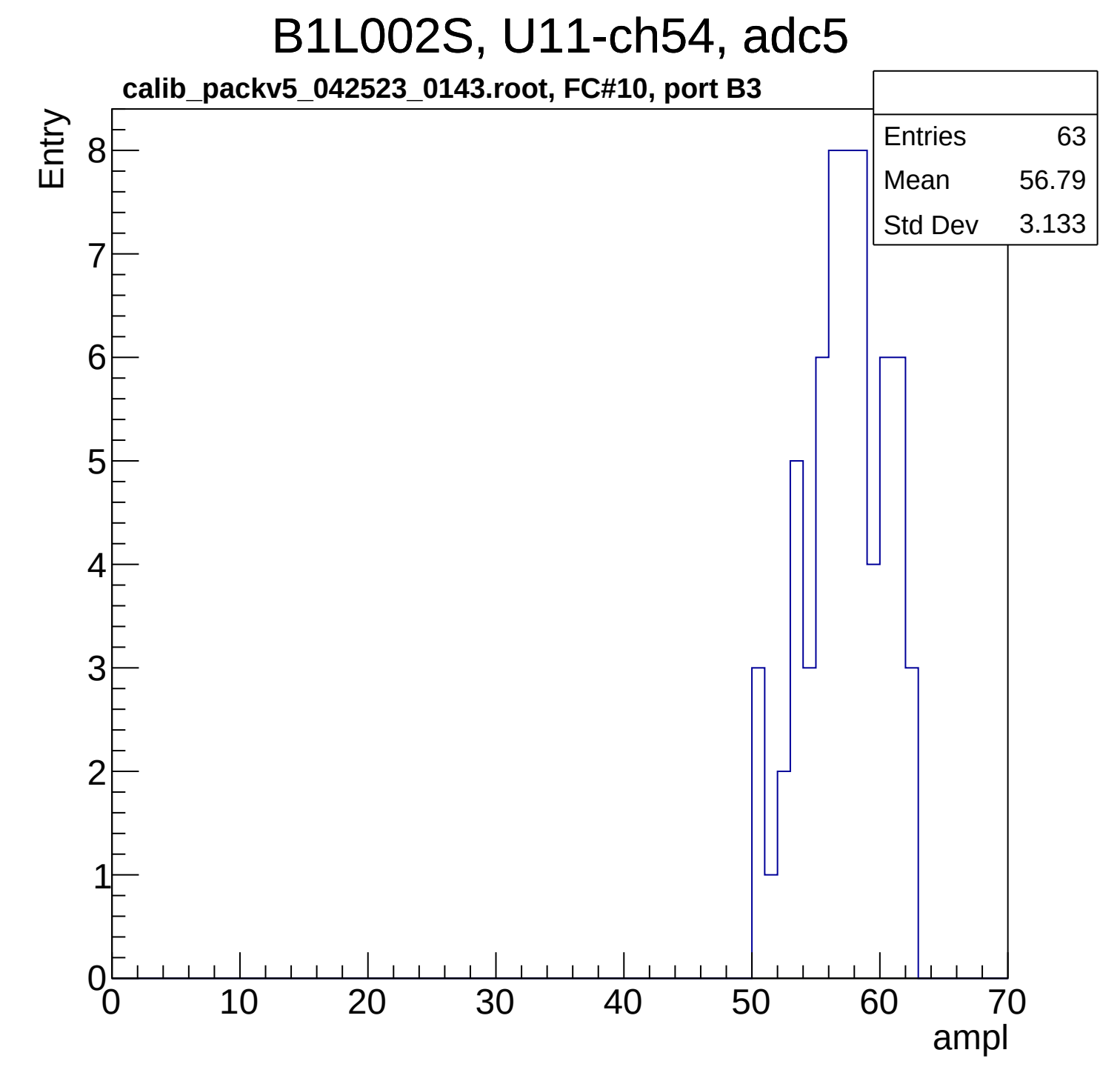
B1L002S, U11-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 56.79
Std Dev 3.133
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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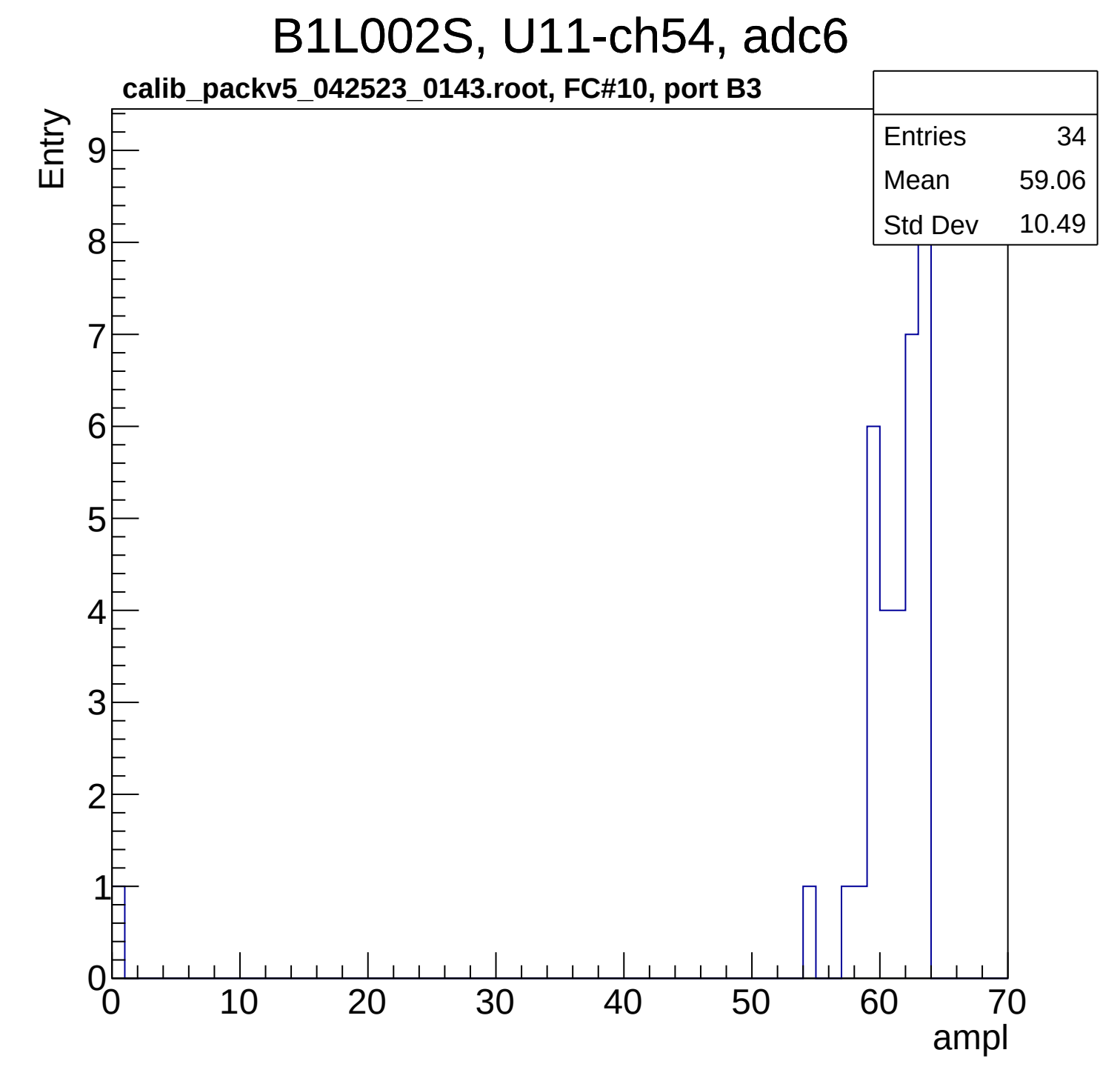
B1L002S, U11-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 59.06
Std Dev 10.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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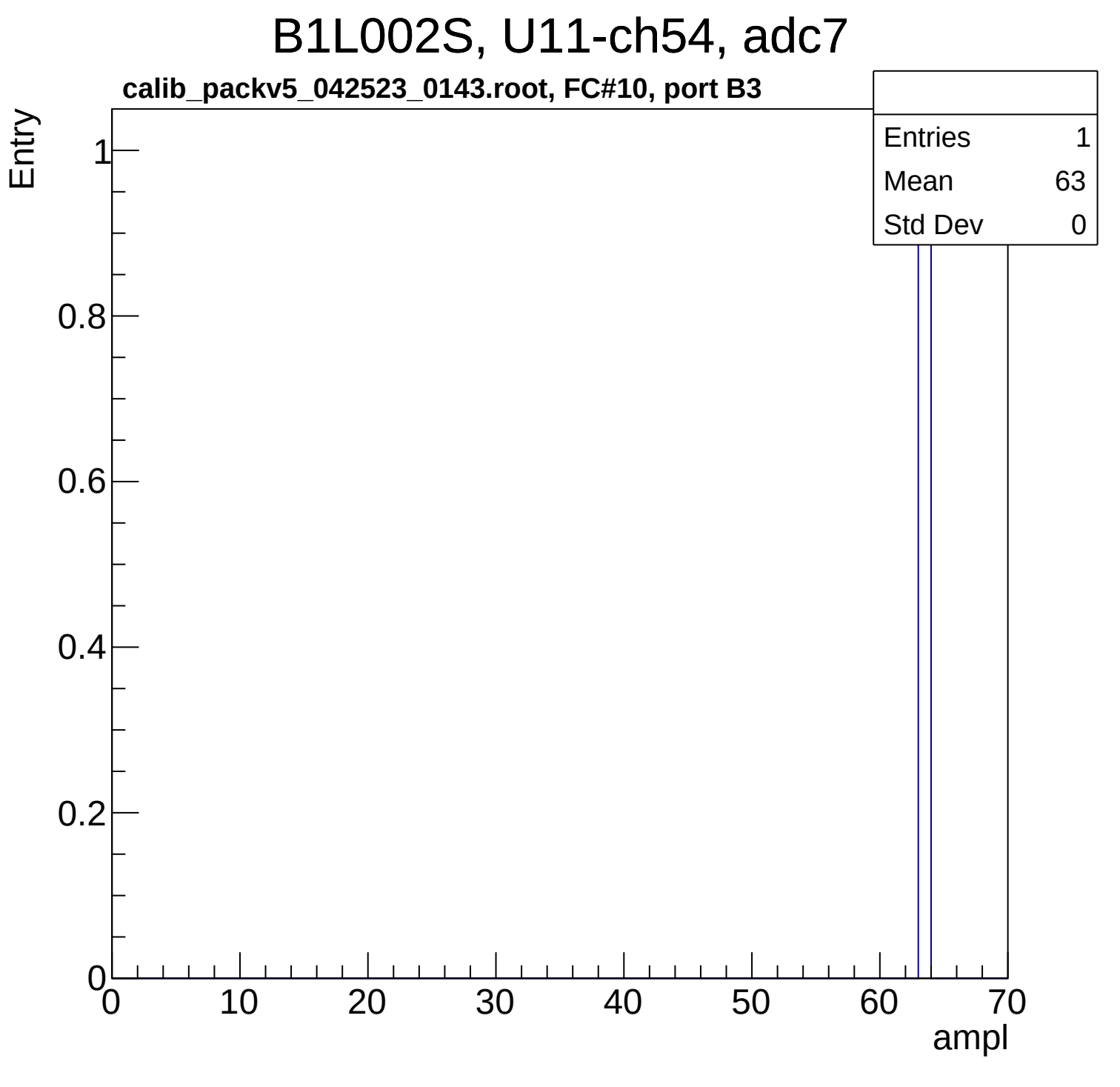
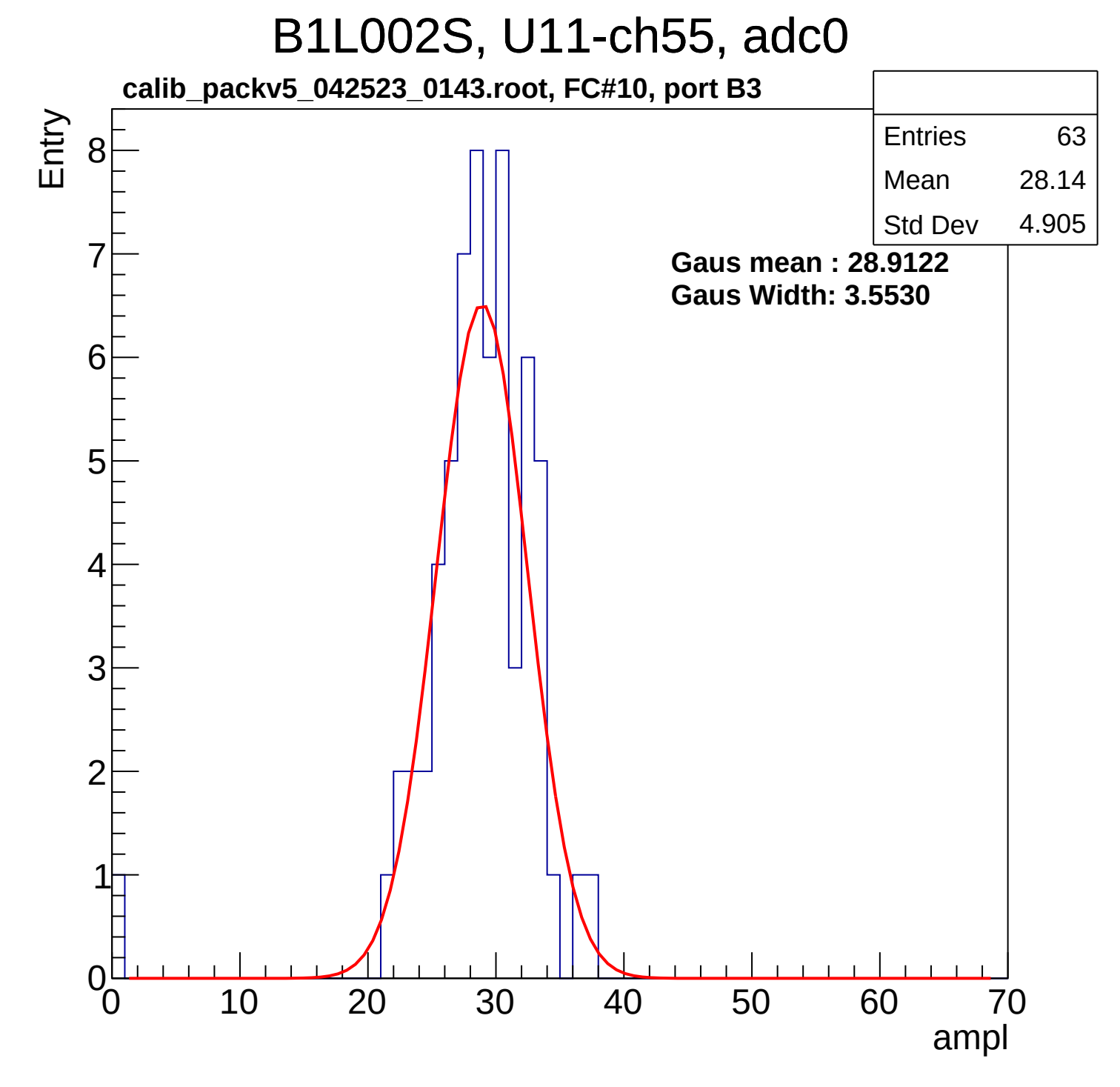

B1L002S, U11-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.14
Std Dev 4.905
Gaus mean : 28.9122
Gaus Width: 3.5530
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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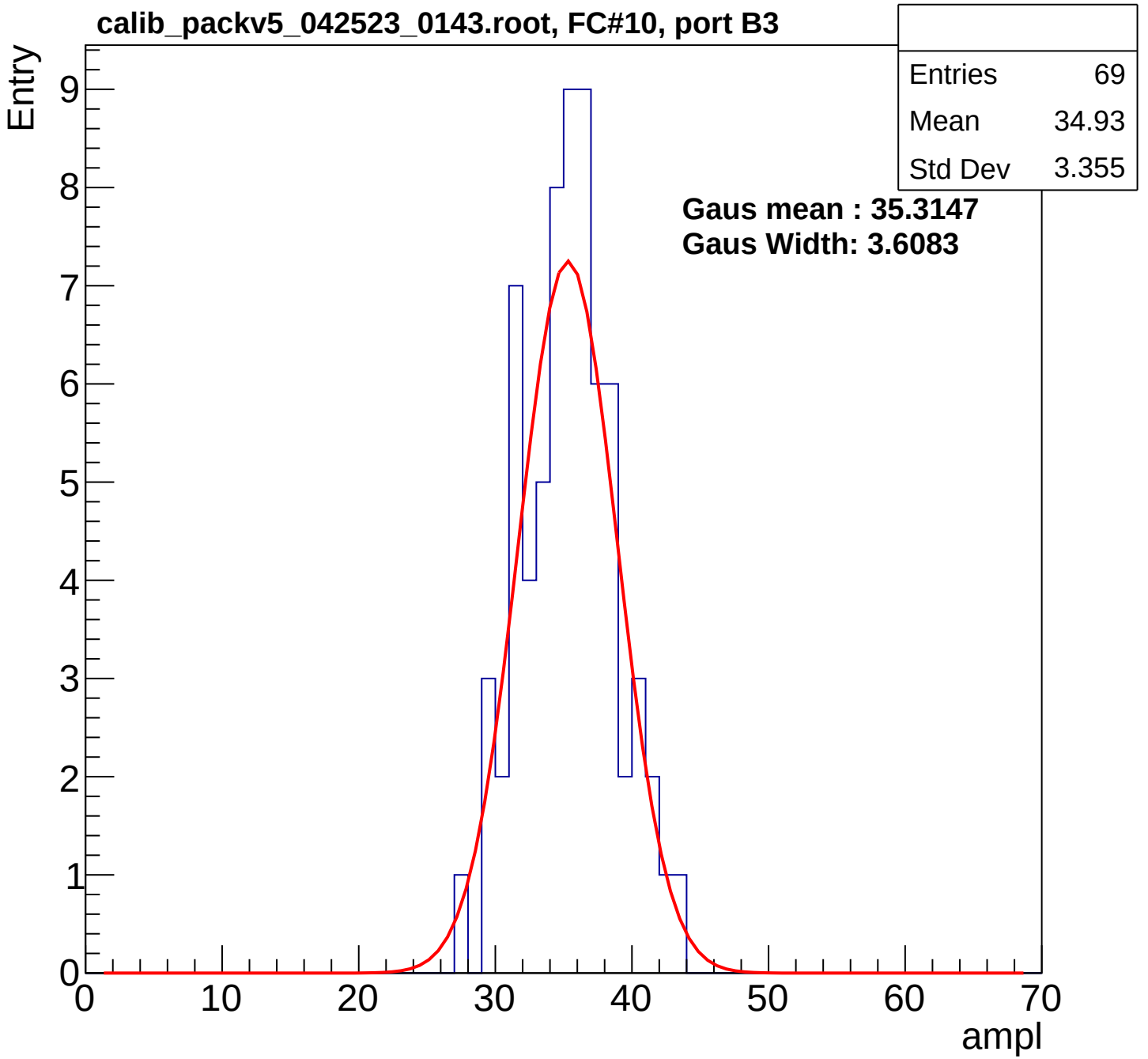
B1L002S, U11-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.93
Std Dev 3.355
Gaus mean : 35.3147
Gaus Width: 3.6083
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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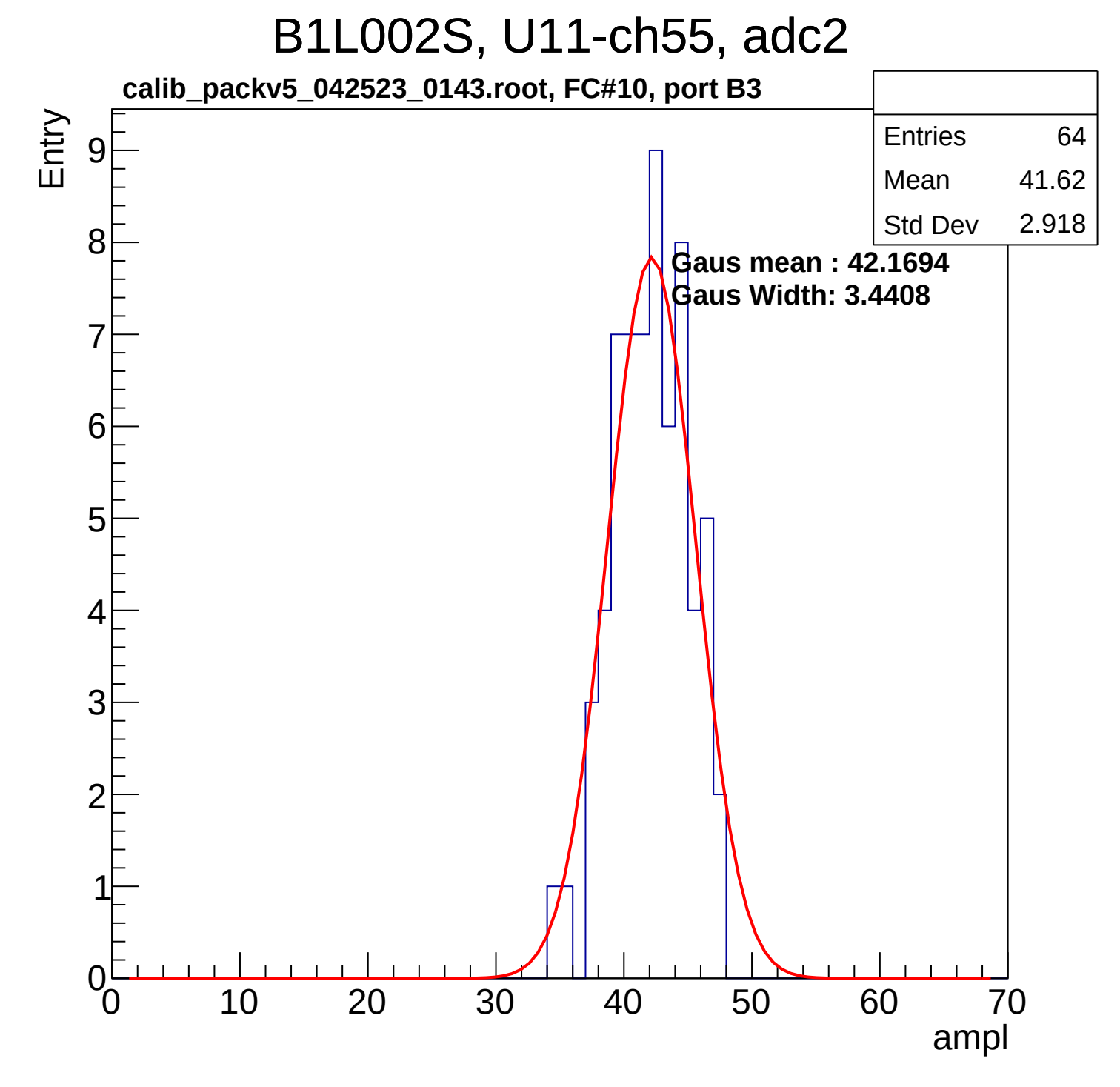
B1L002S, U11-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.62
Std Dev 2.918
Gaus mean : 42.1694
Gaus Width: 3.4408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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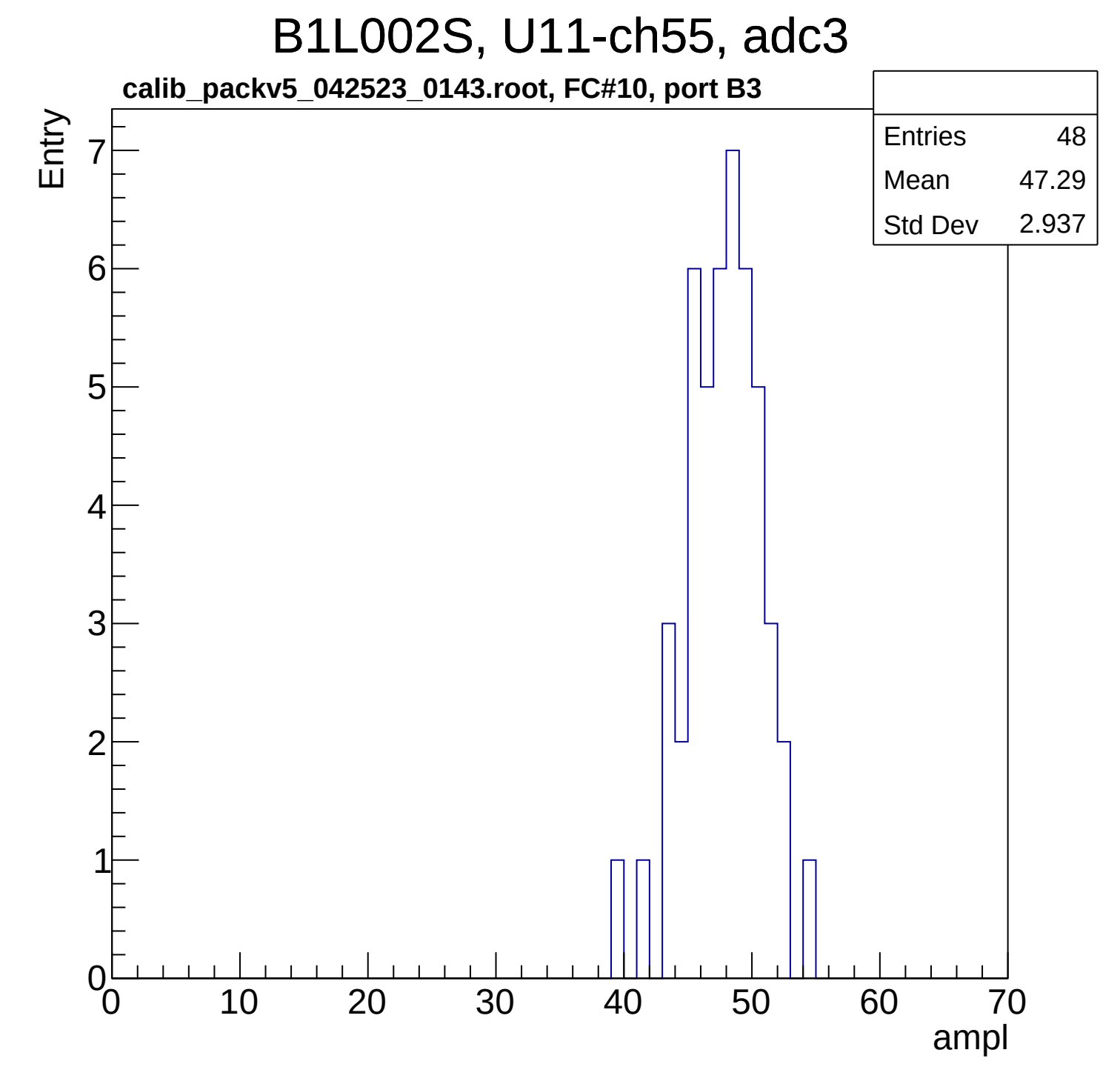
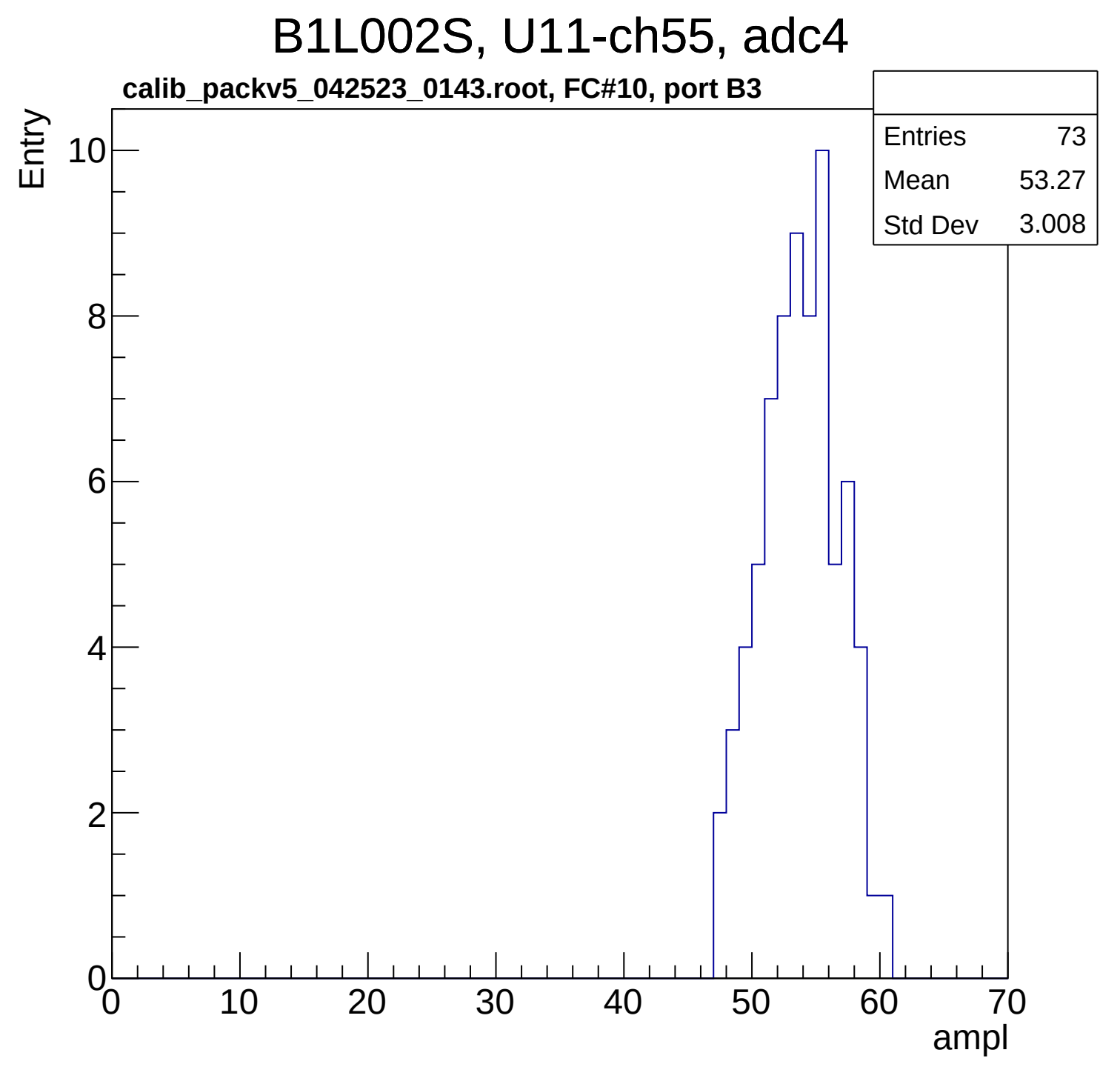
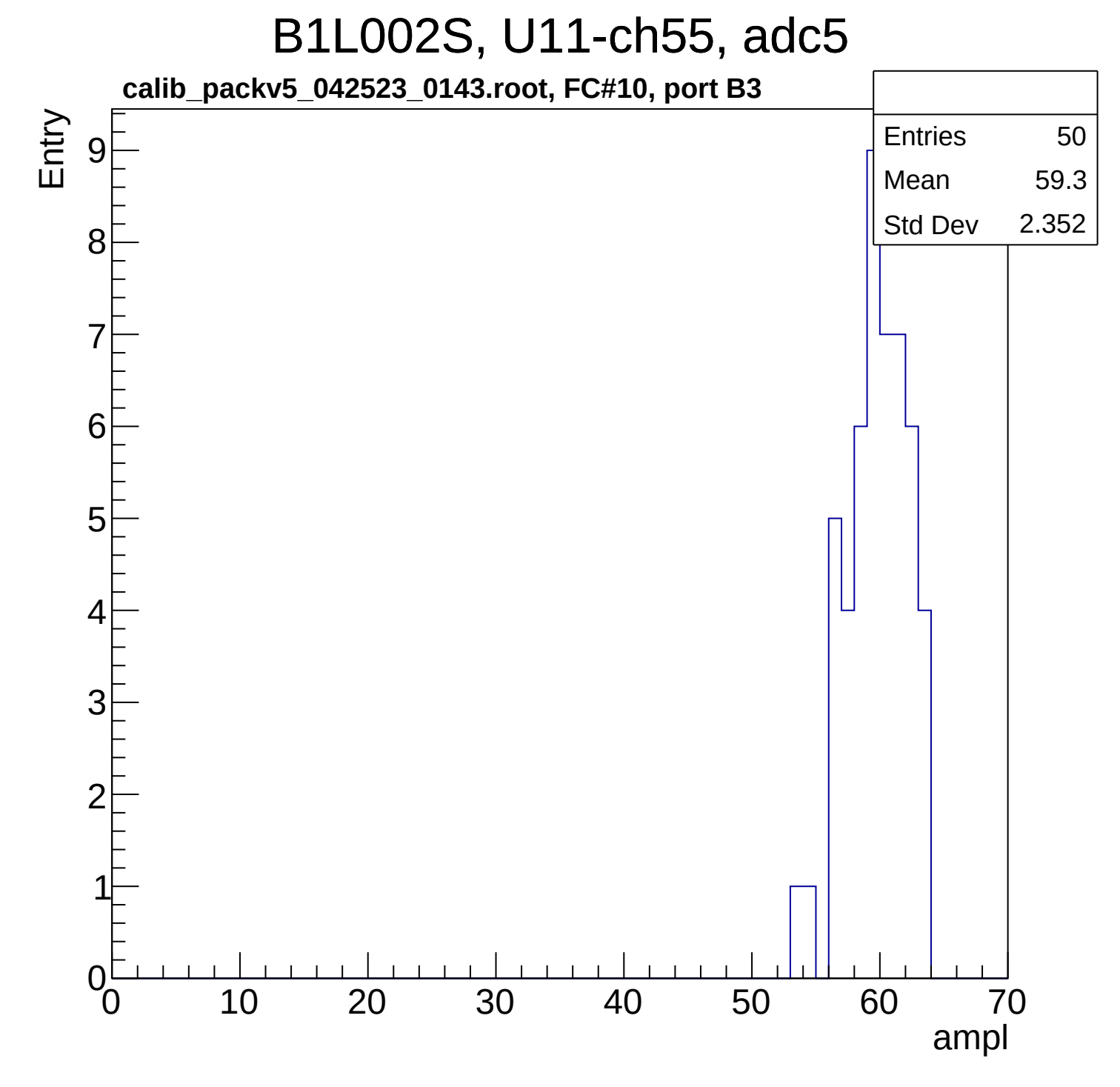

B1L002S, U11-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 47.29
Std Dev 2.937
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 53.27
Std Dev 3.008
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.3
Std Dev 2.352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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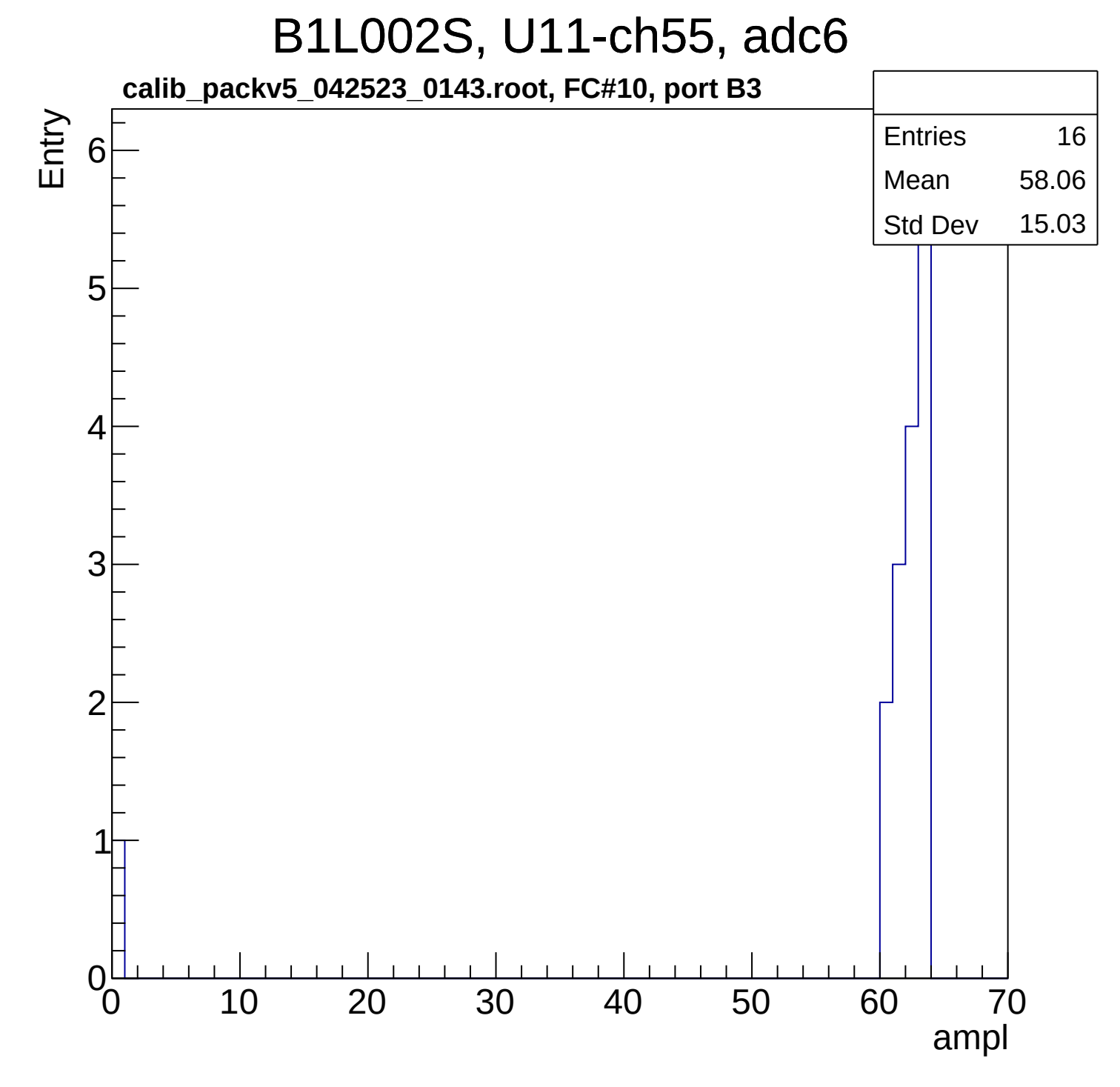
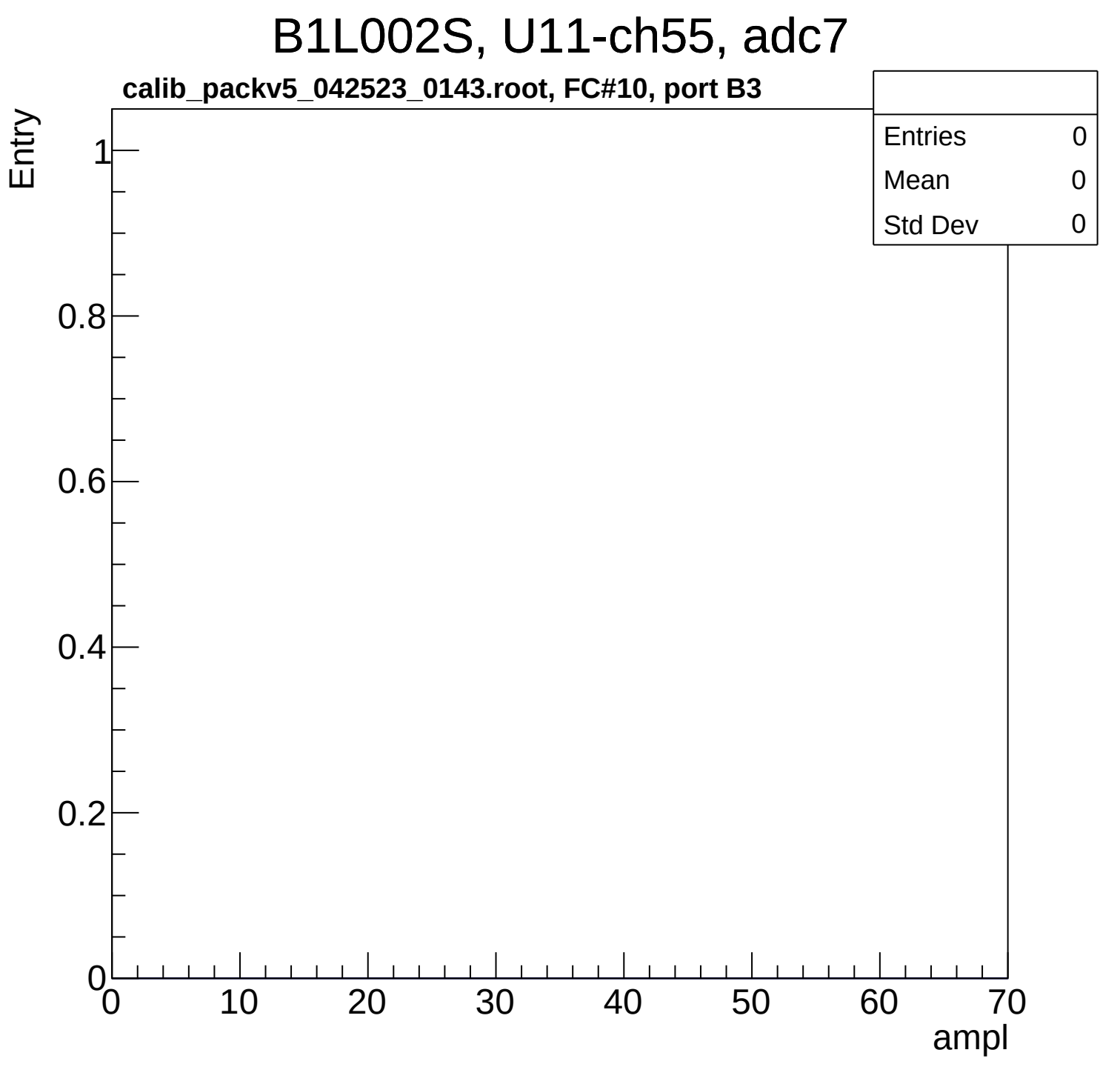
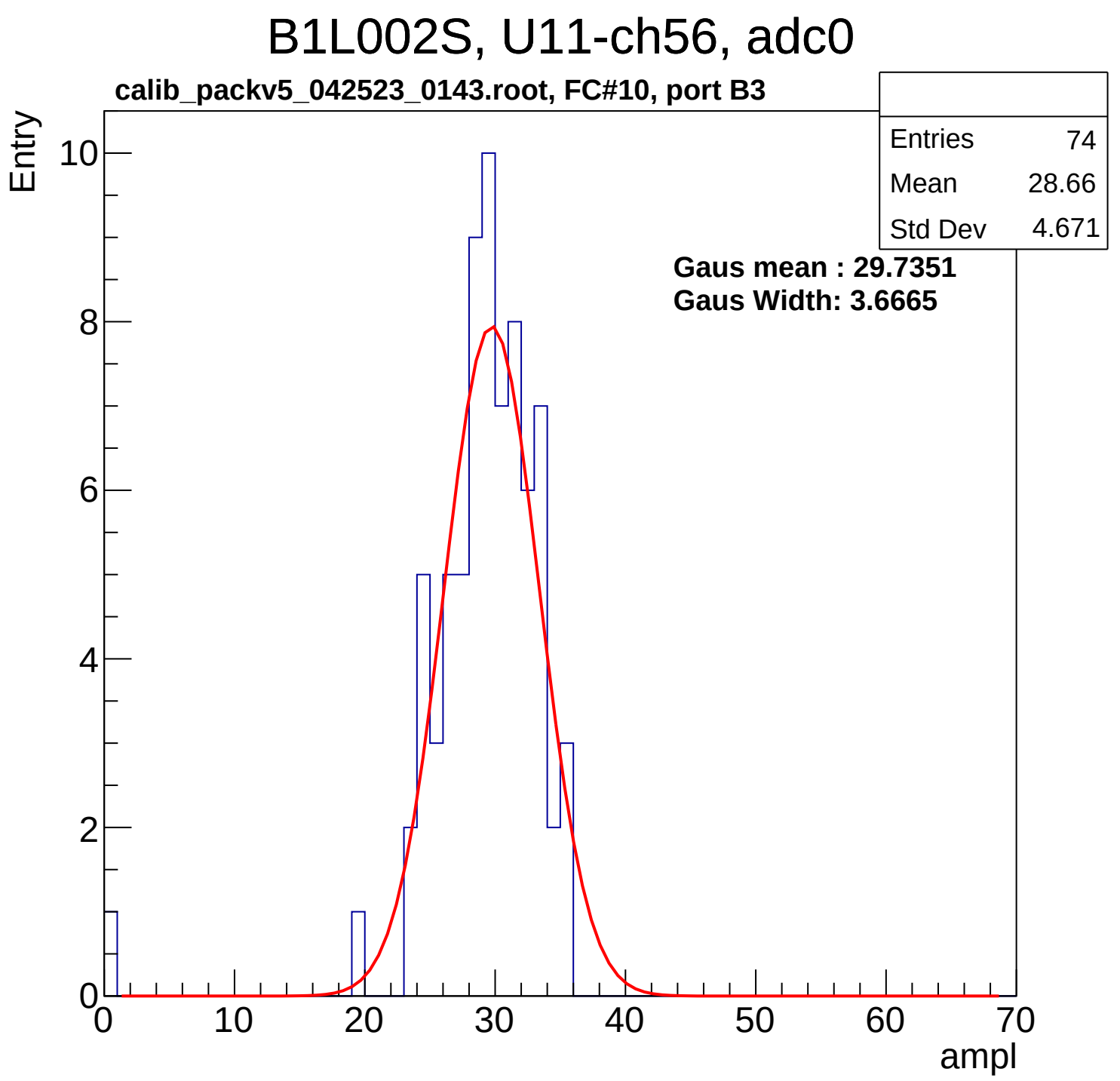
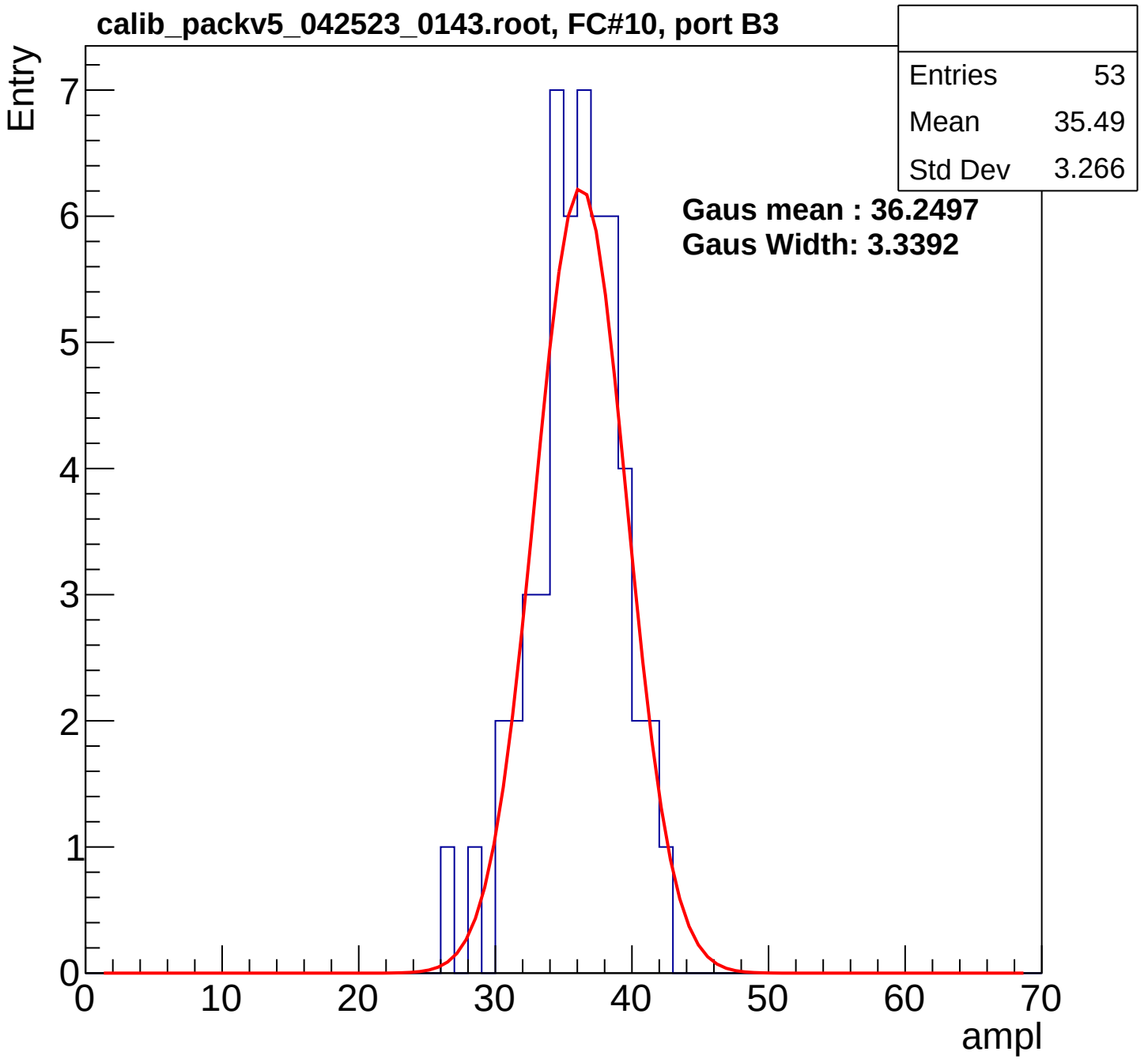
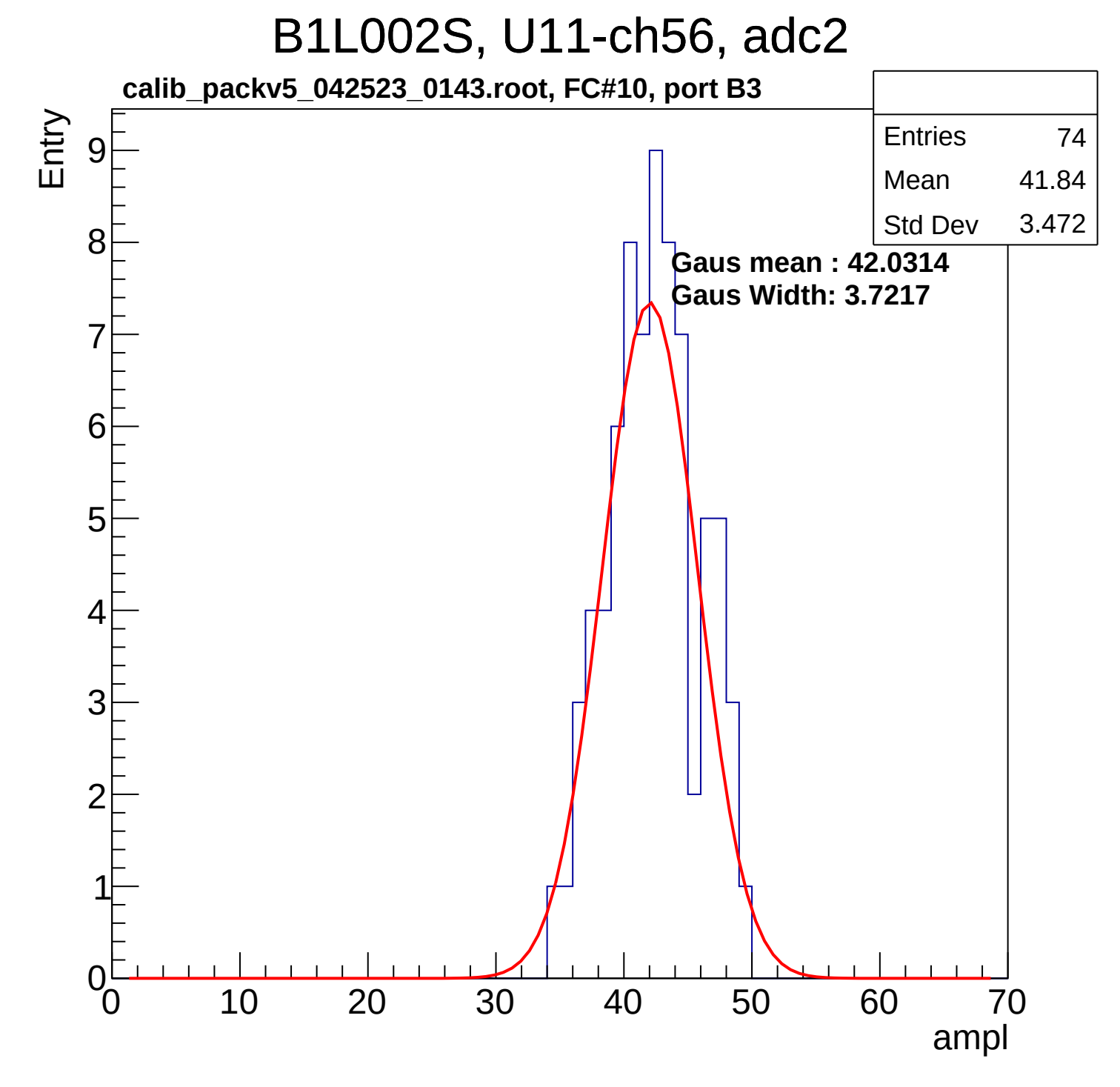

B1L002S, U11-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 58.06
Std Dev 15.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.66
Std Dev 4.671
Gaus mean : 29.7351
Gaus Width: 3.6665
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 35.49
Std Dev 3.266
Gaus mean : 36.2497
Gaus Width: 3.3392
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 41.84
Std Dev 3.472
Gaus mean : 42.0314
Gaus Width: 3.7217
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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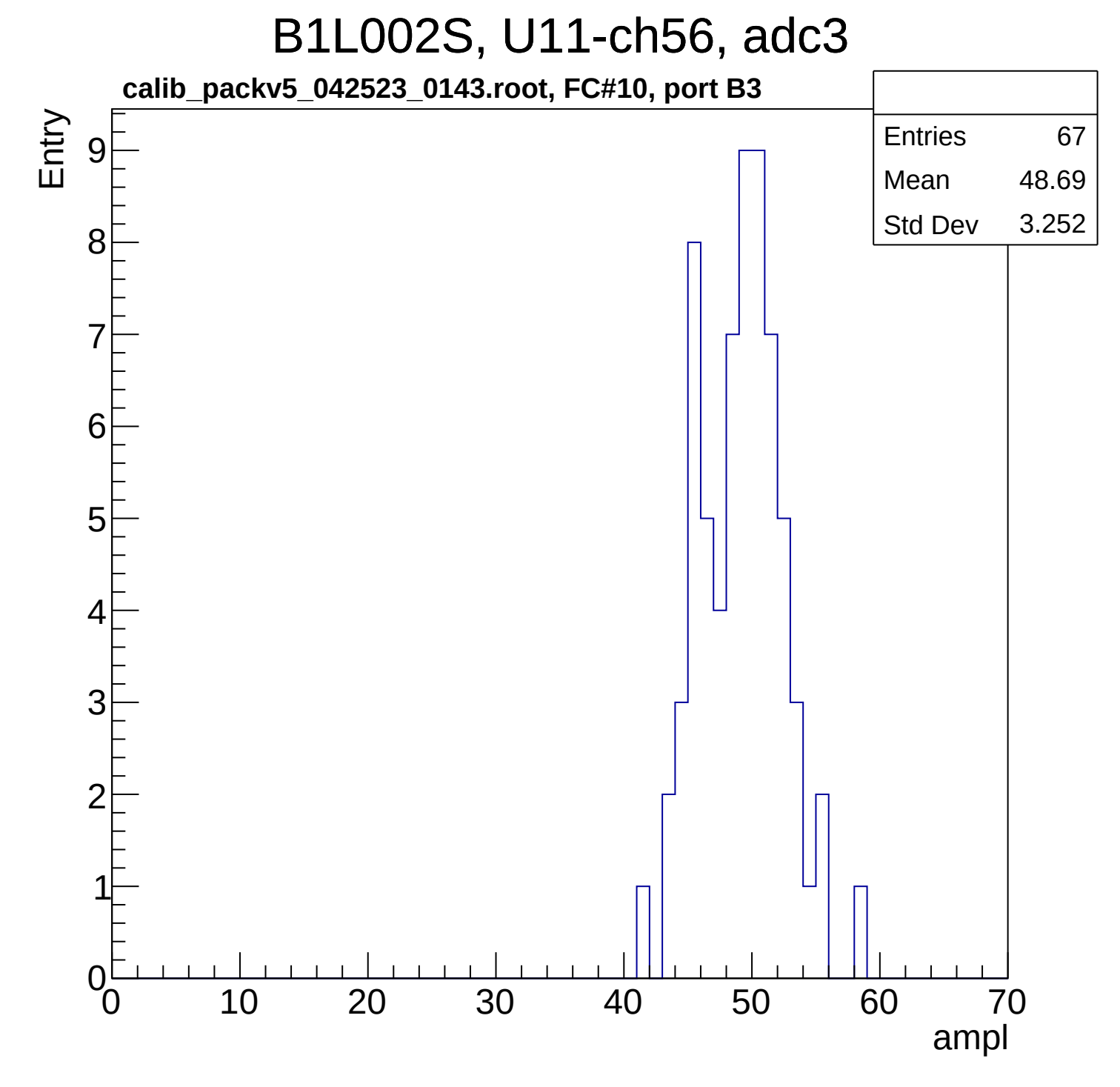
B1L002S, U11-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.69
Std Dev 3.252
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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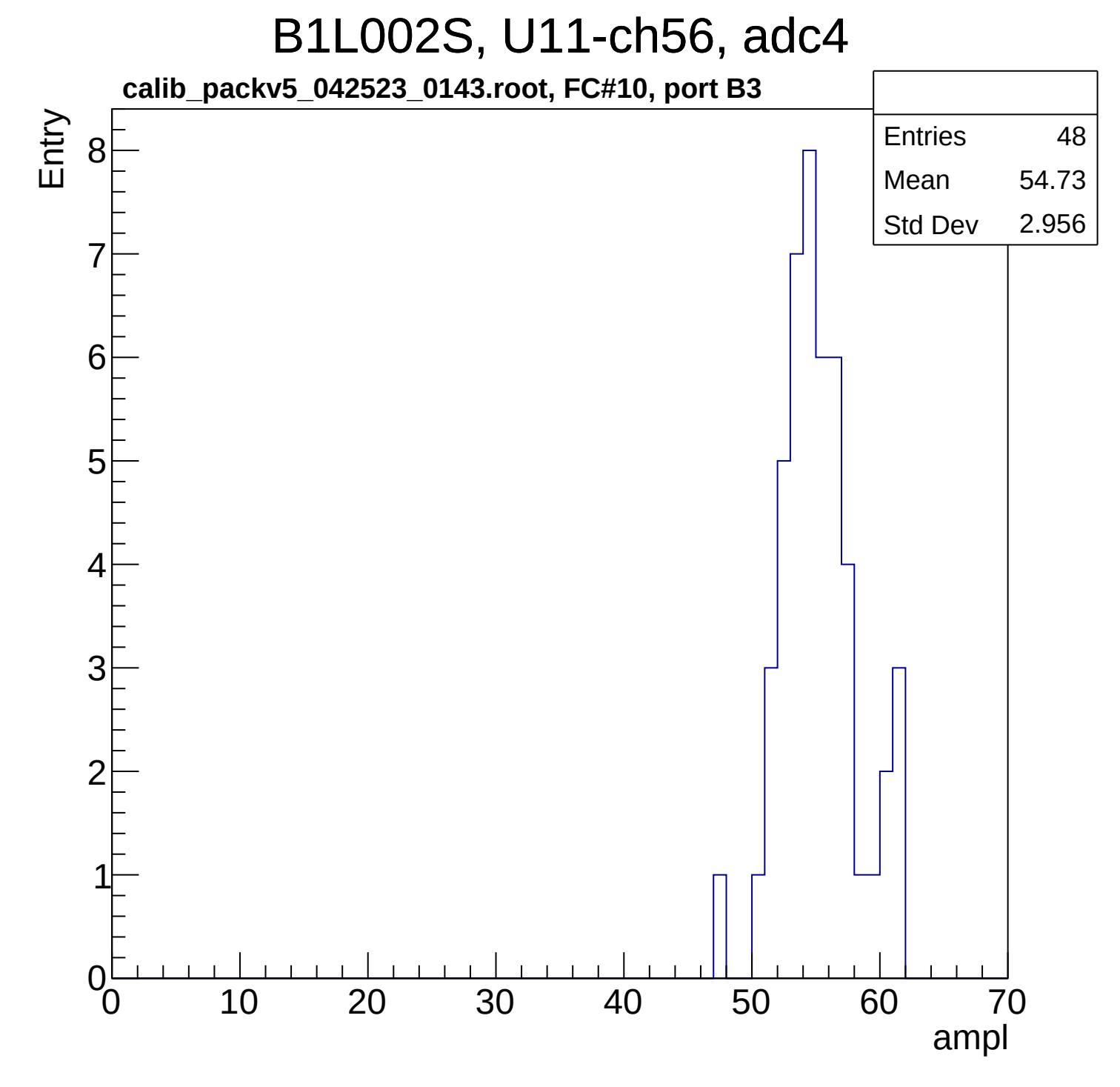
B1L002S, U11-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 54.73
Std Dev 2.956
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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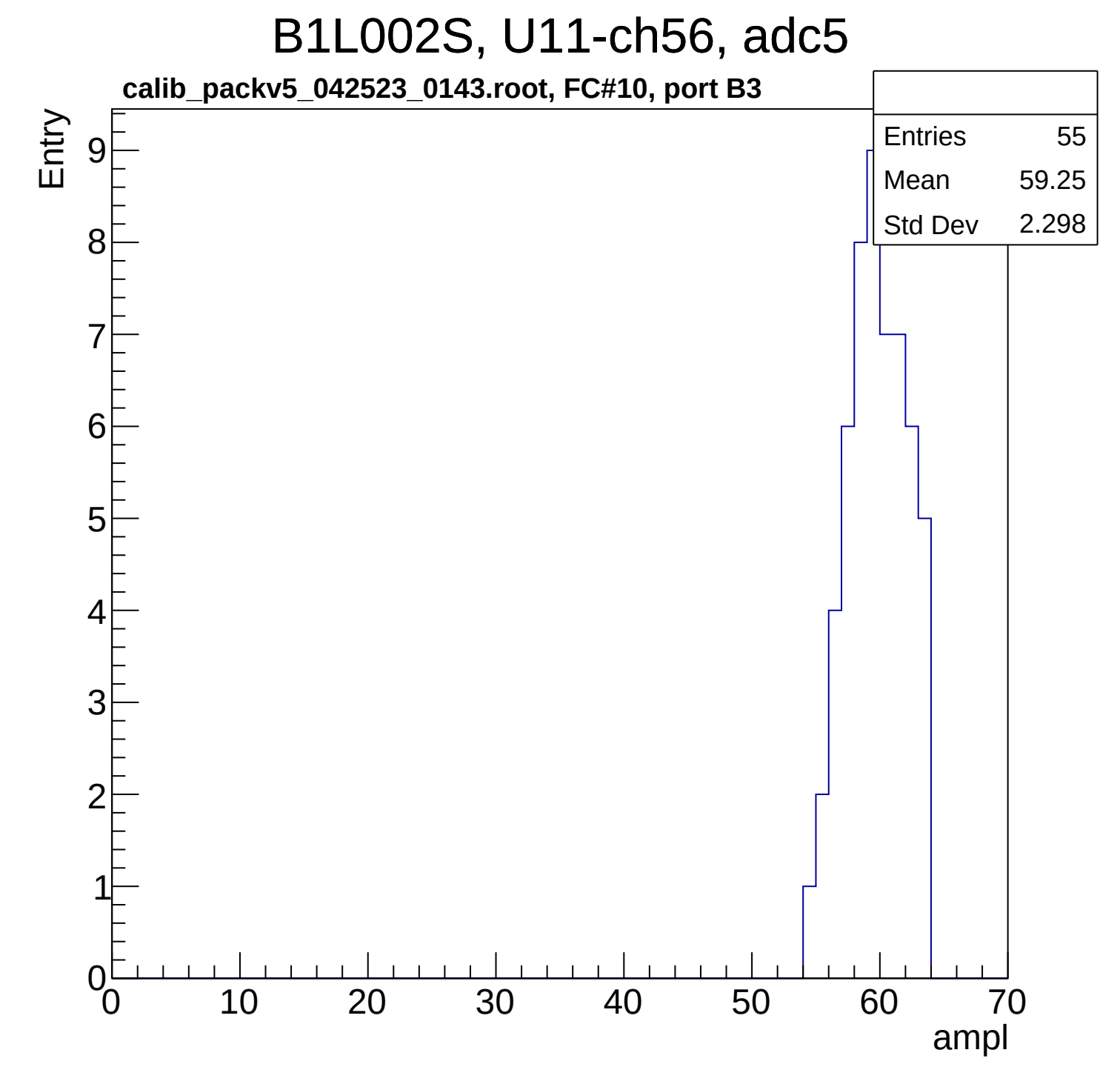
B1L002S, U11-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 59.25
Std Dev 2.298
Entry
ampl
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2
3
4
5
6
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8
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0
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20
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40
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60
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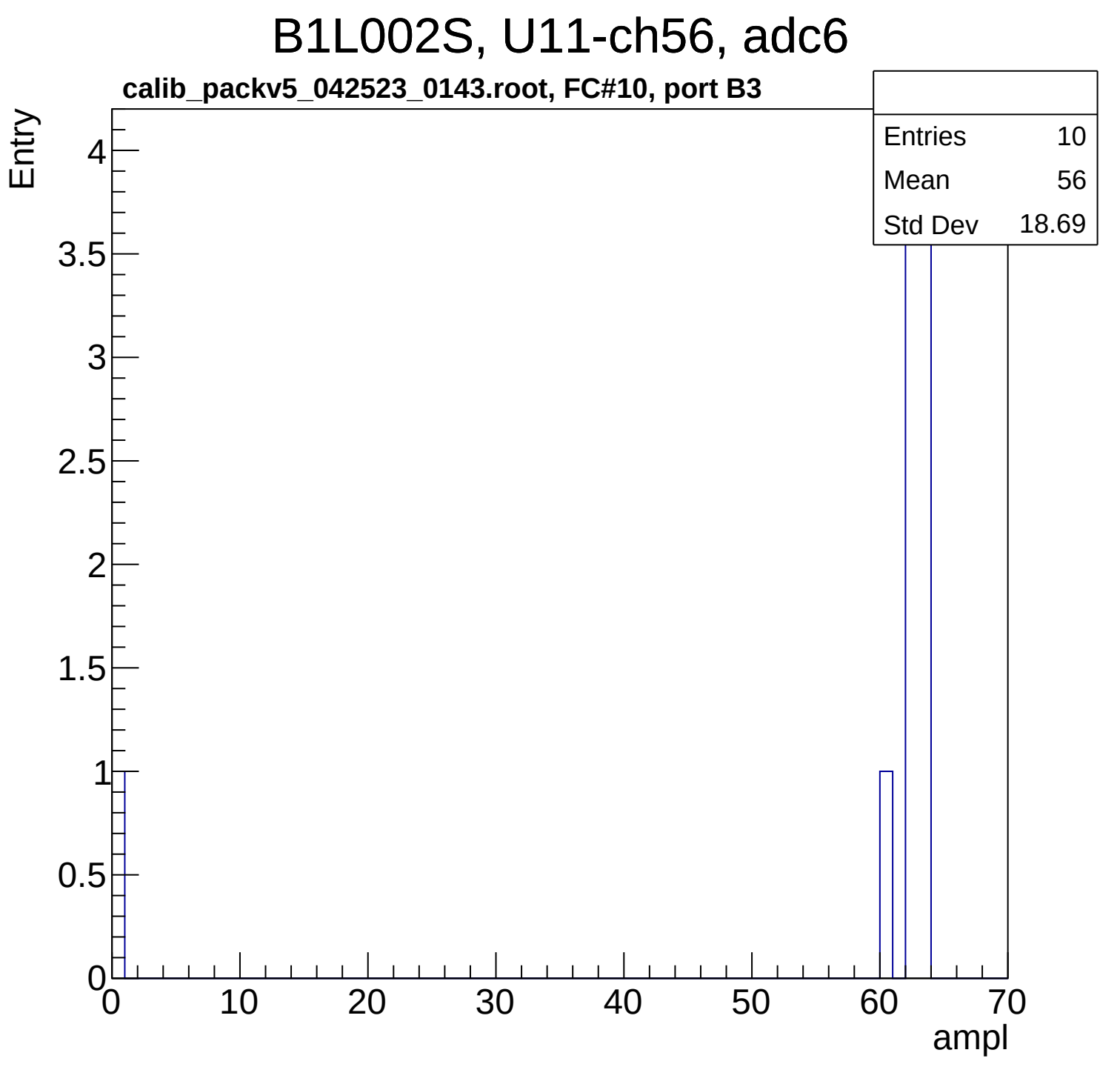
B1L002S, U11-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 56
Std Dev 18.69
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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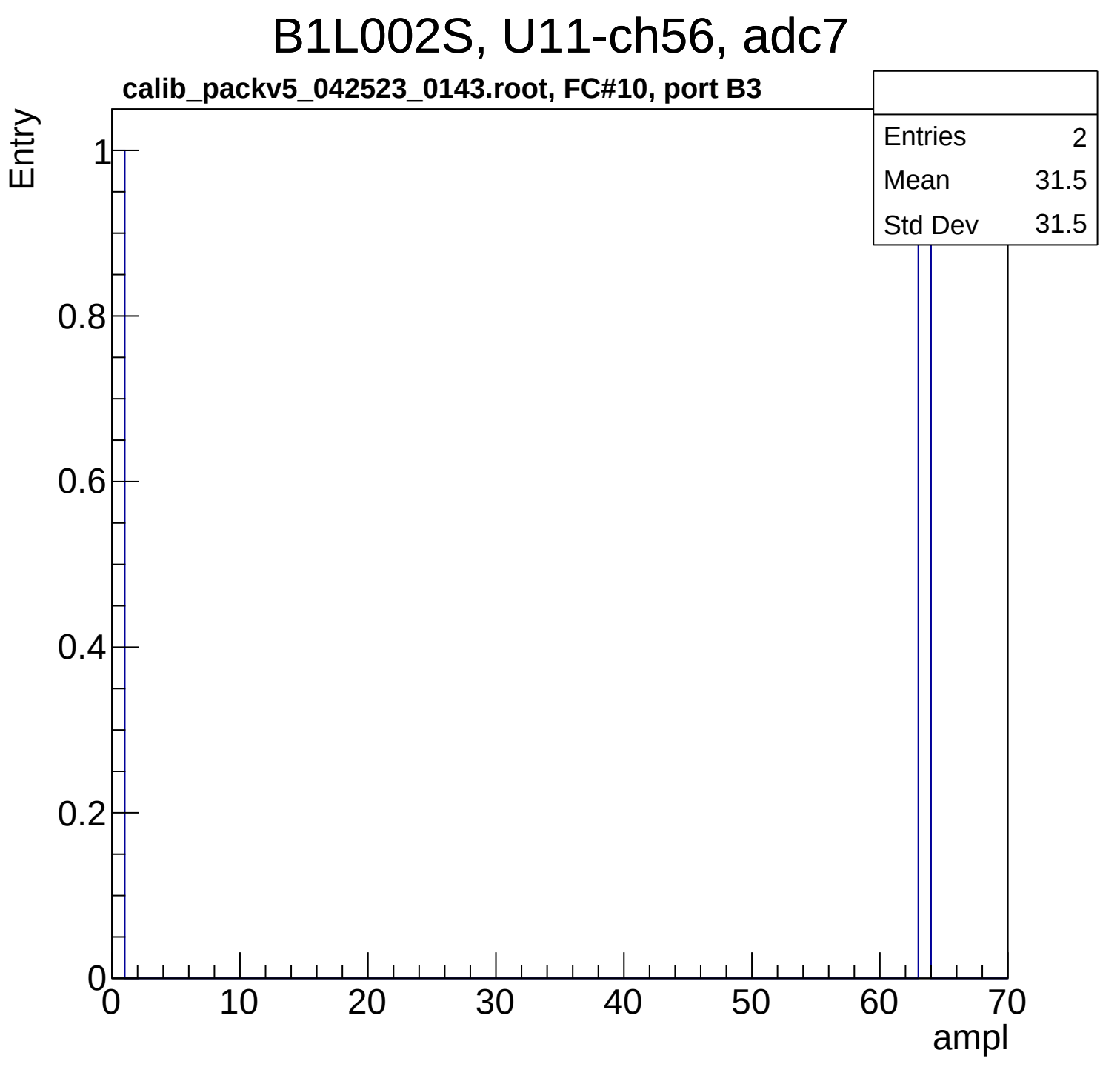
B1L002S, U11-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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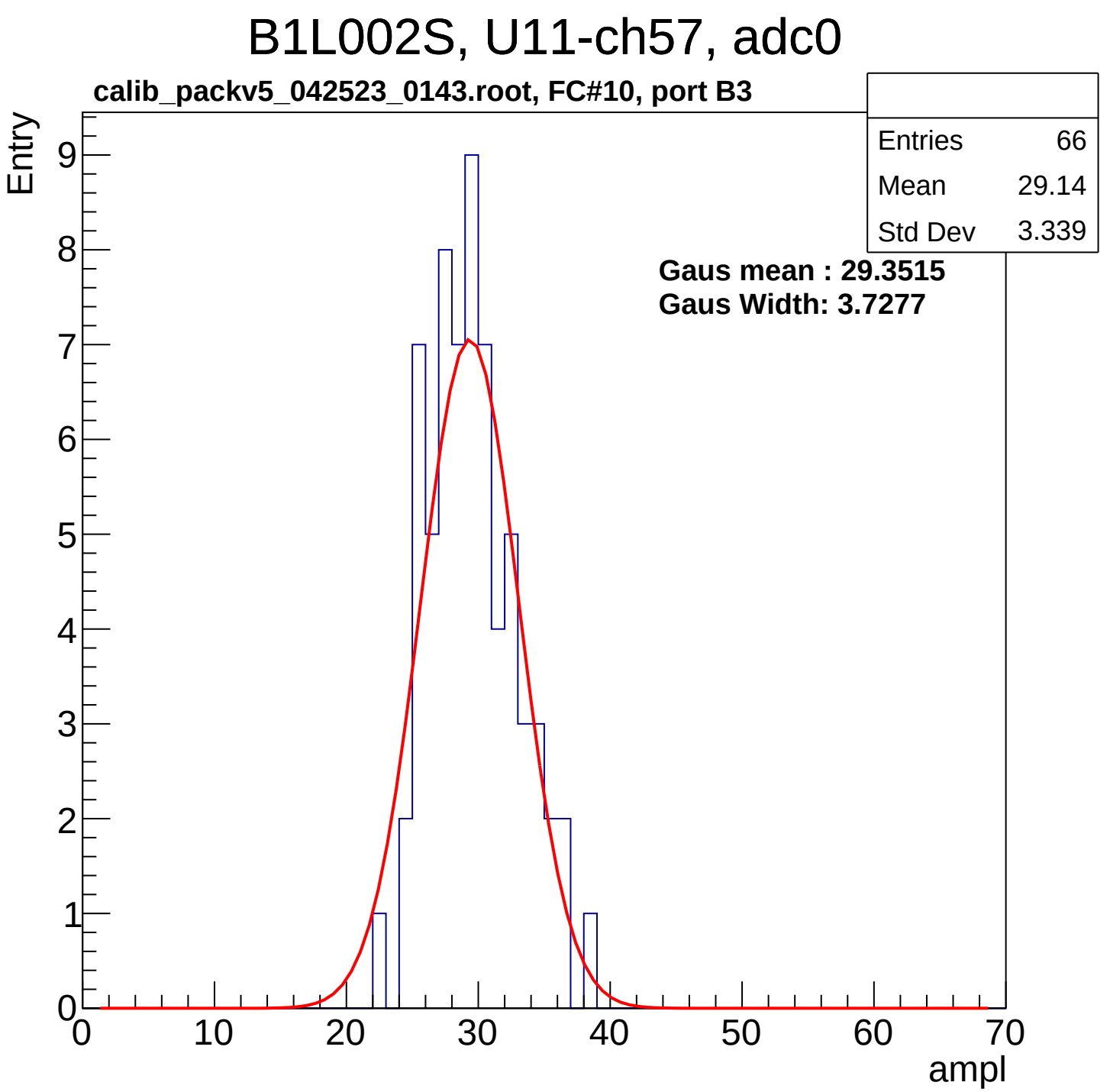
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.14
Std Dev 3.339
Gaus mean : 29.3515
Gaus Width: 3.7277
Entry
ampl
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2
3
4
5
6
7
8
9
0
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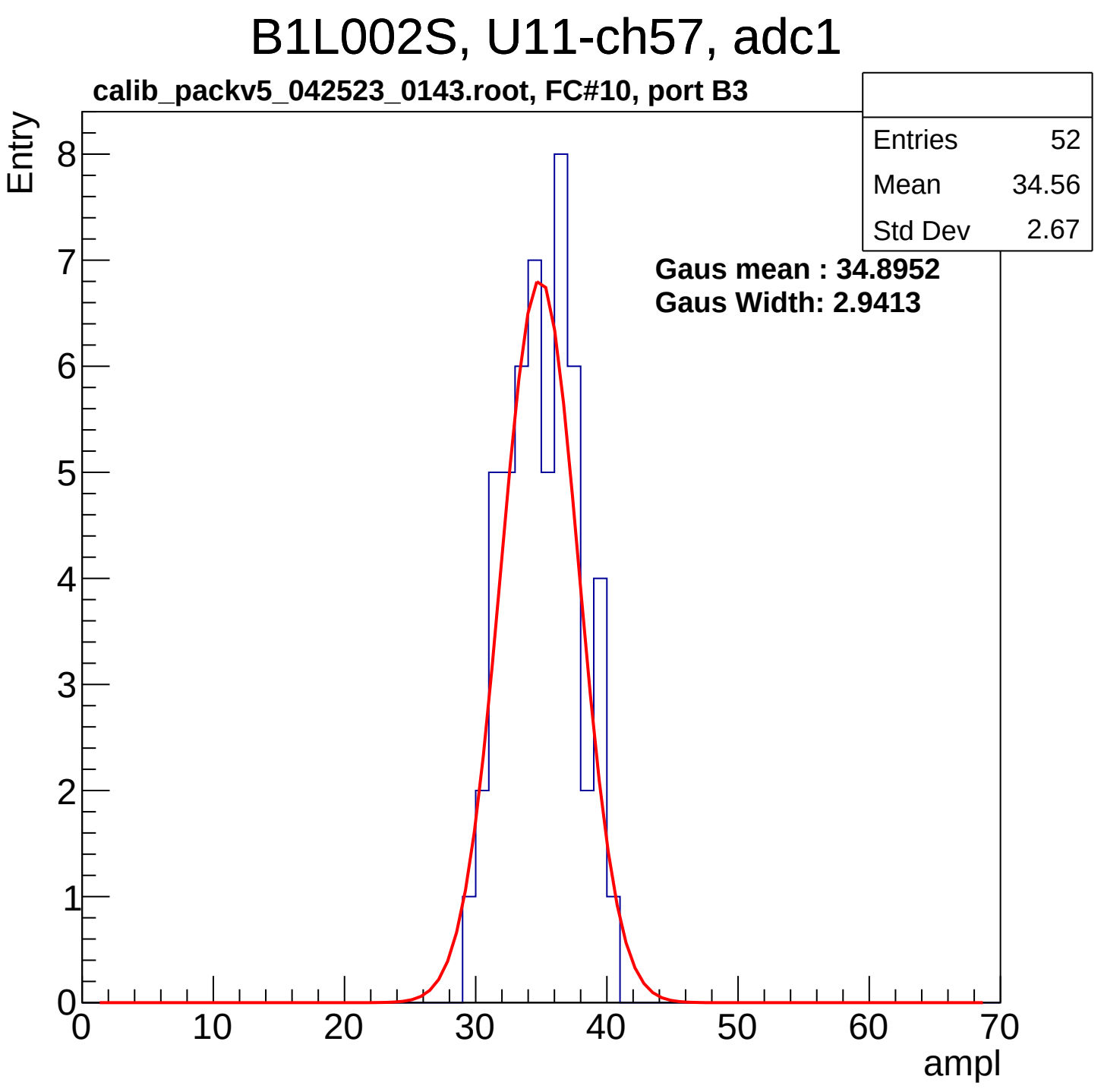
20
30
40
50
60
70


B1L002S, U11-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 34.56
Std Dev 2.67
Gaus mean : 34.8952
Gaus Width: 2.9413
Entry
ampl
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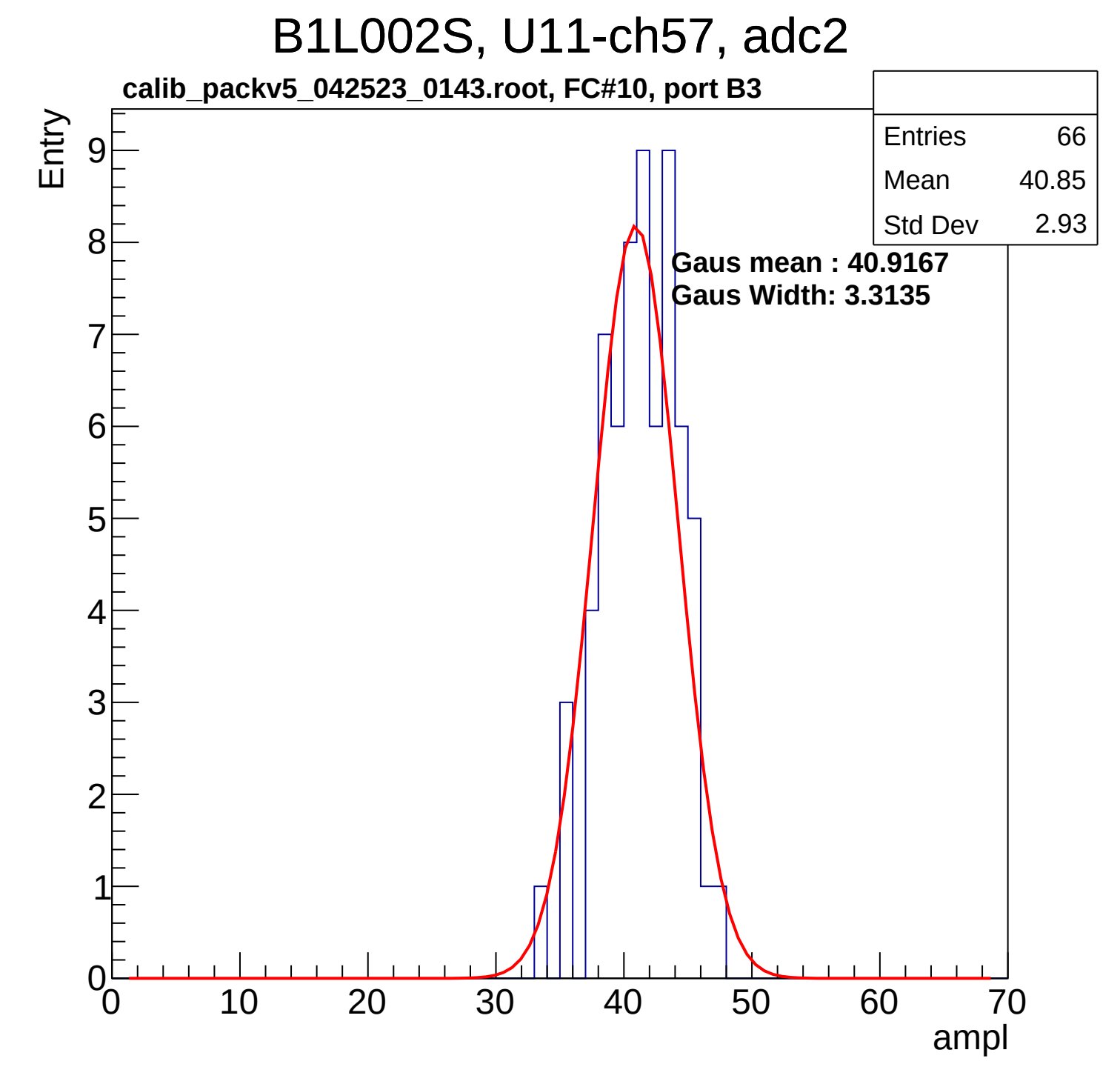
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.85
Std Dev 2.93
Gaus mean : 40.9167
Gaus Width: 3.3135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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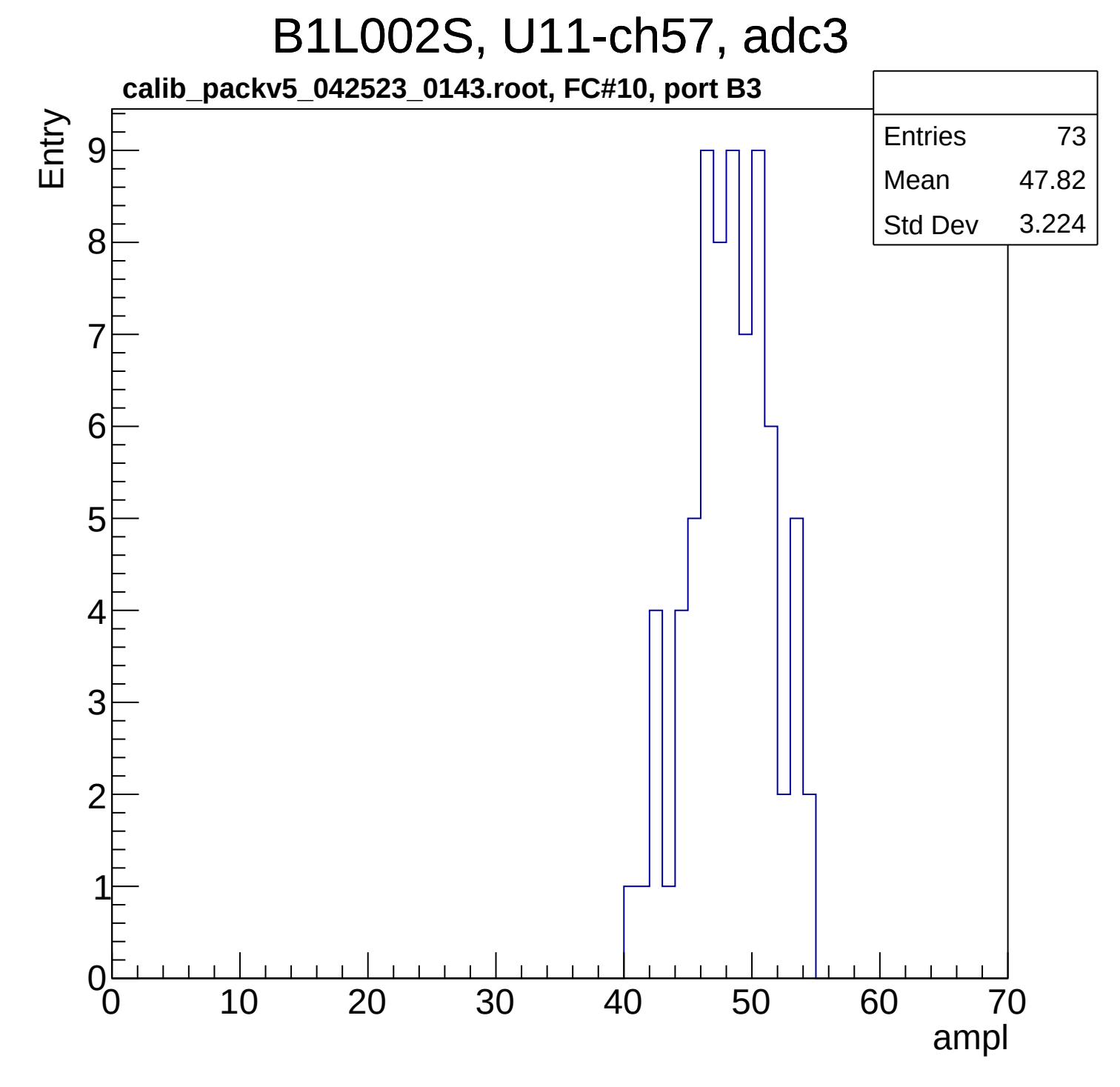
B1L002S, U11-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 47.82
Std Dev 3.224
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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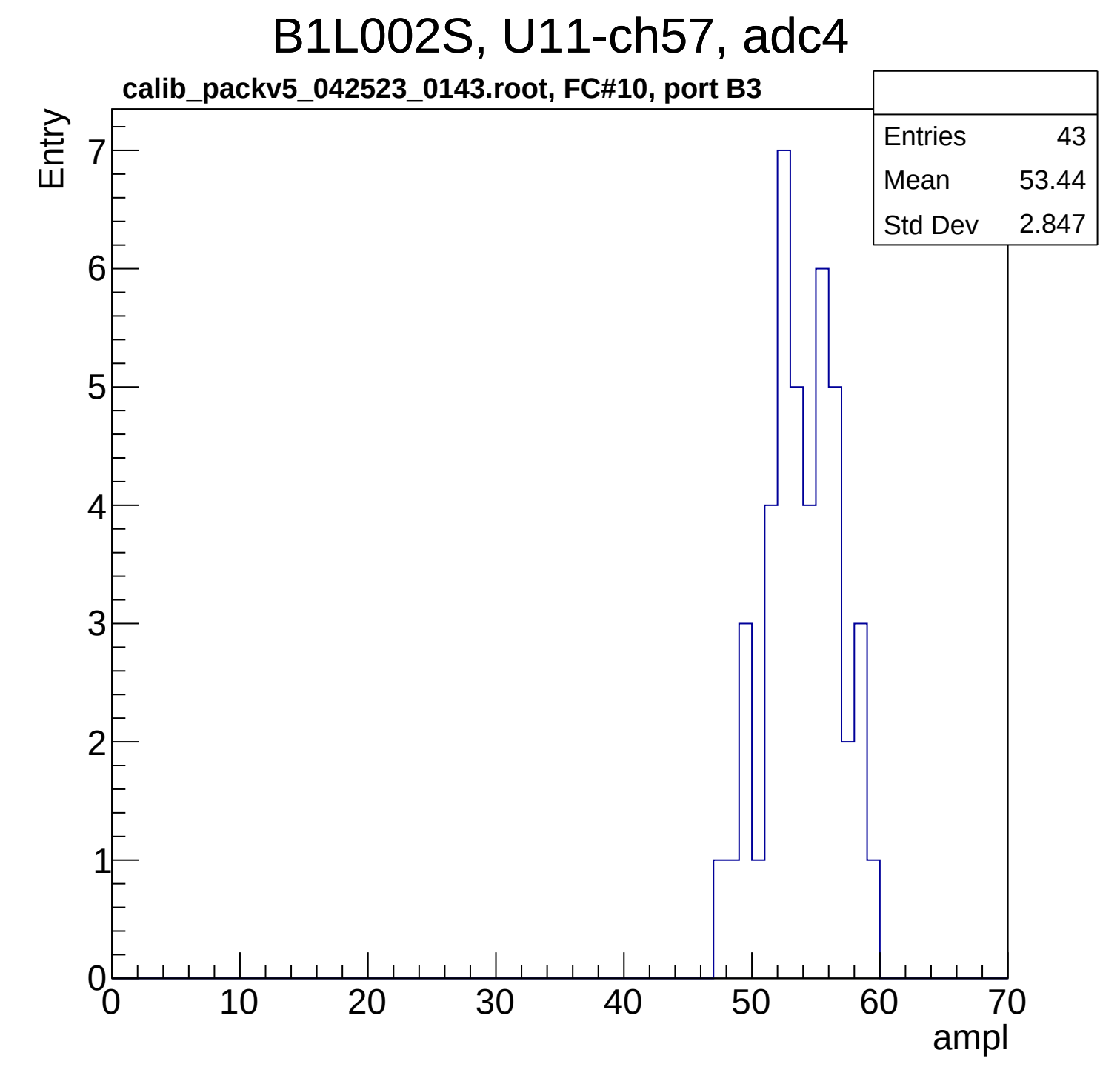
B1L002S, U11-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53.44
Std Dev 2.847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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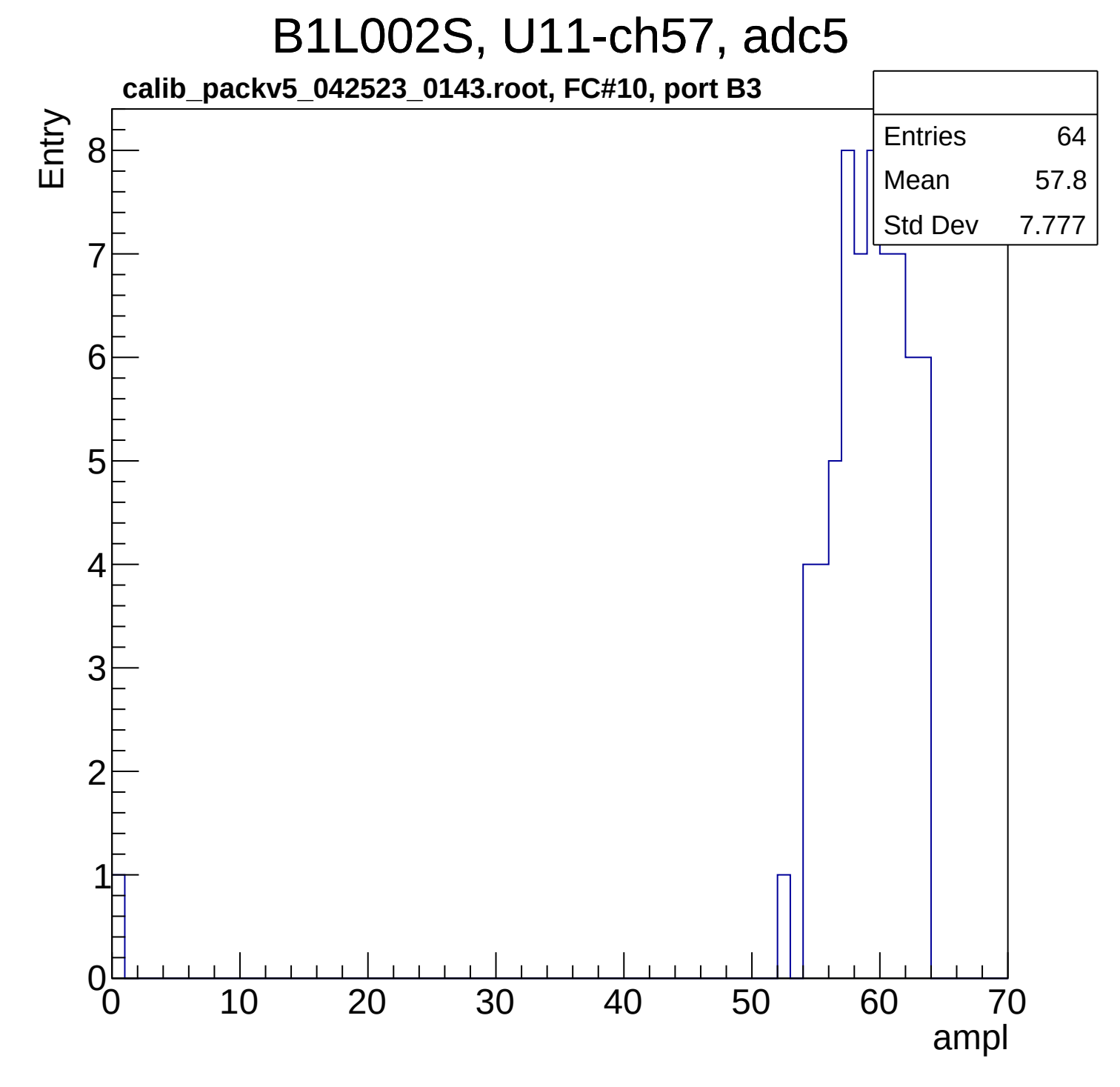
B1L002S, U11-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.8
Std Dev 7.777
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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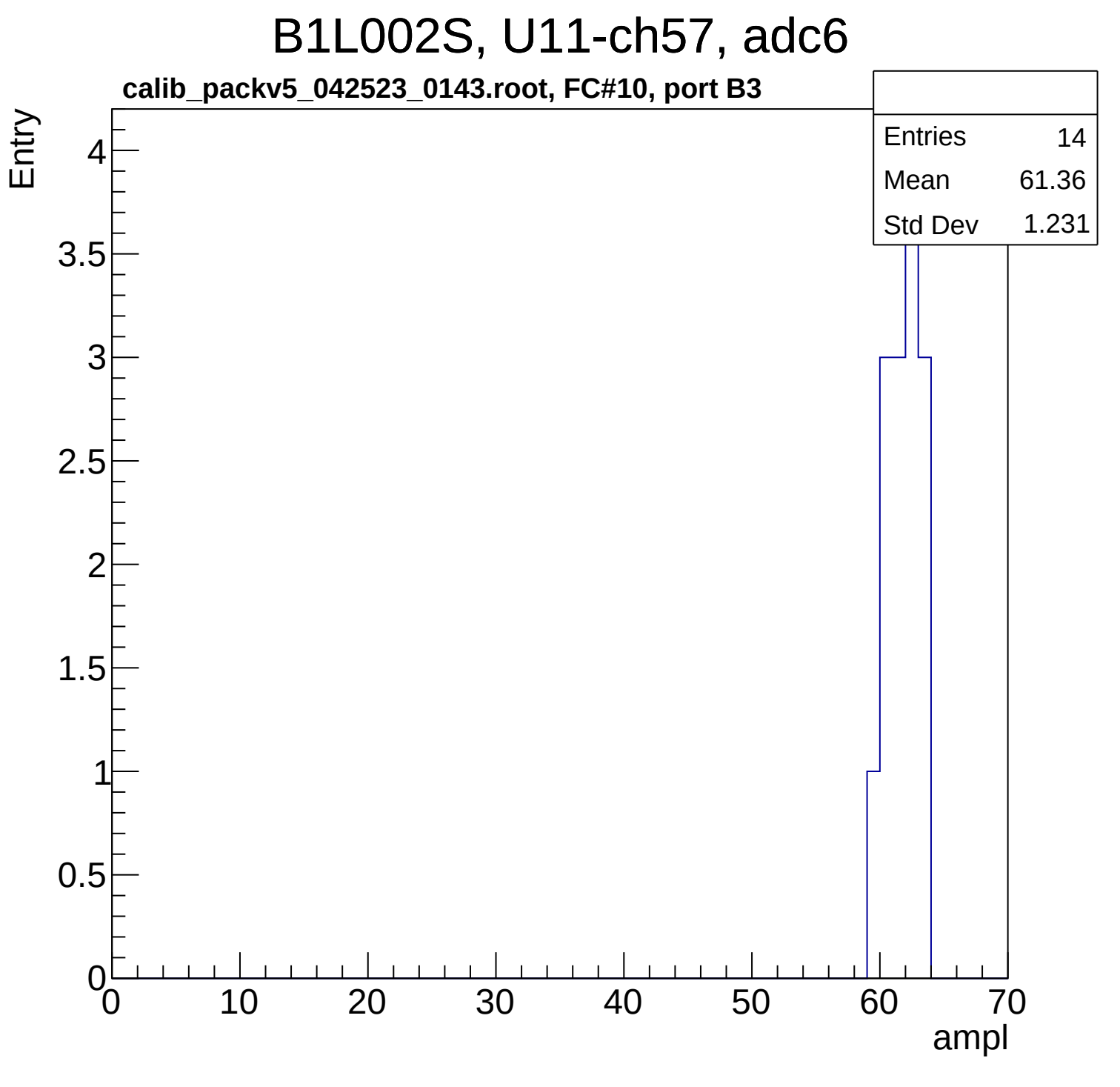
B1L002S, U11-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.36
Std Dev 1.231
Entry
ampl
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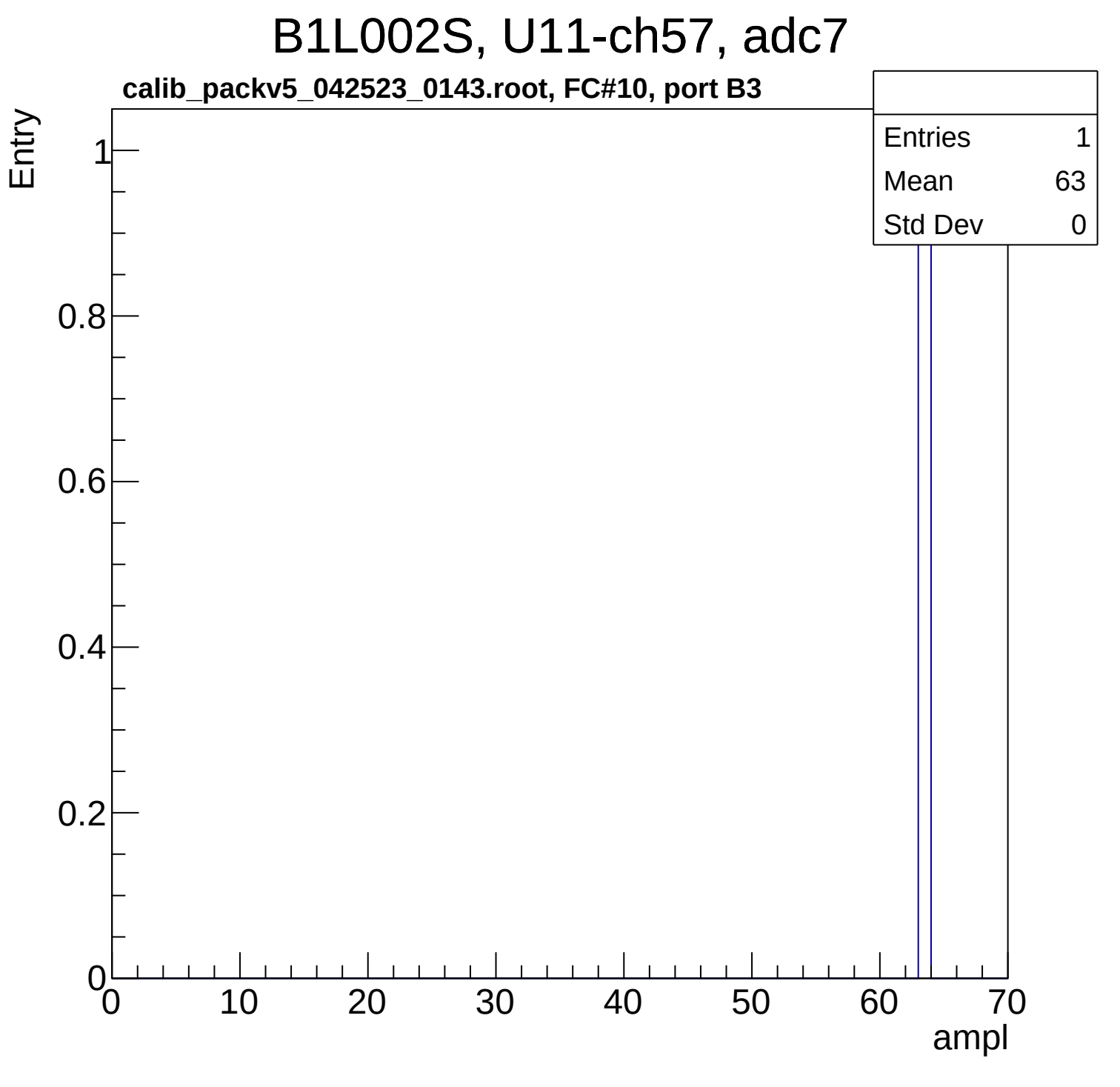
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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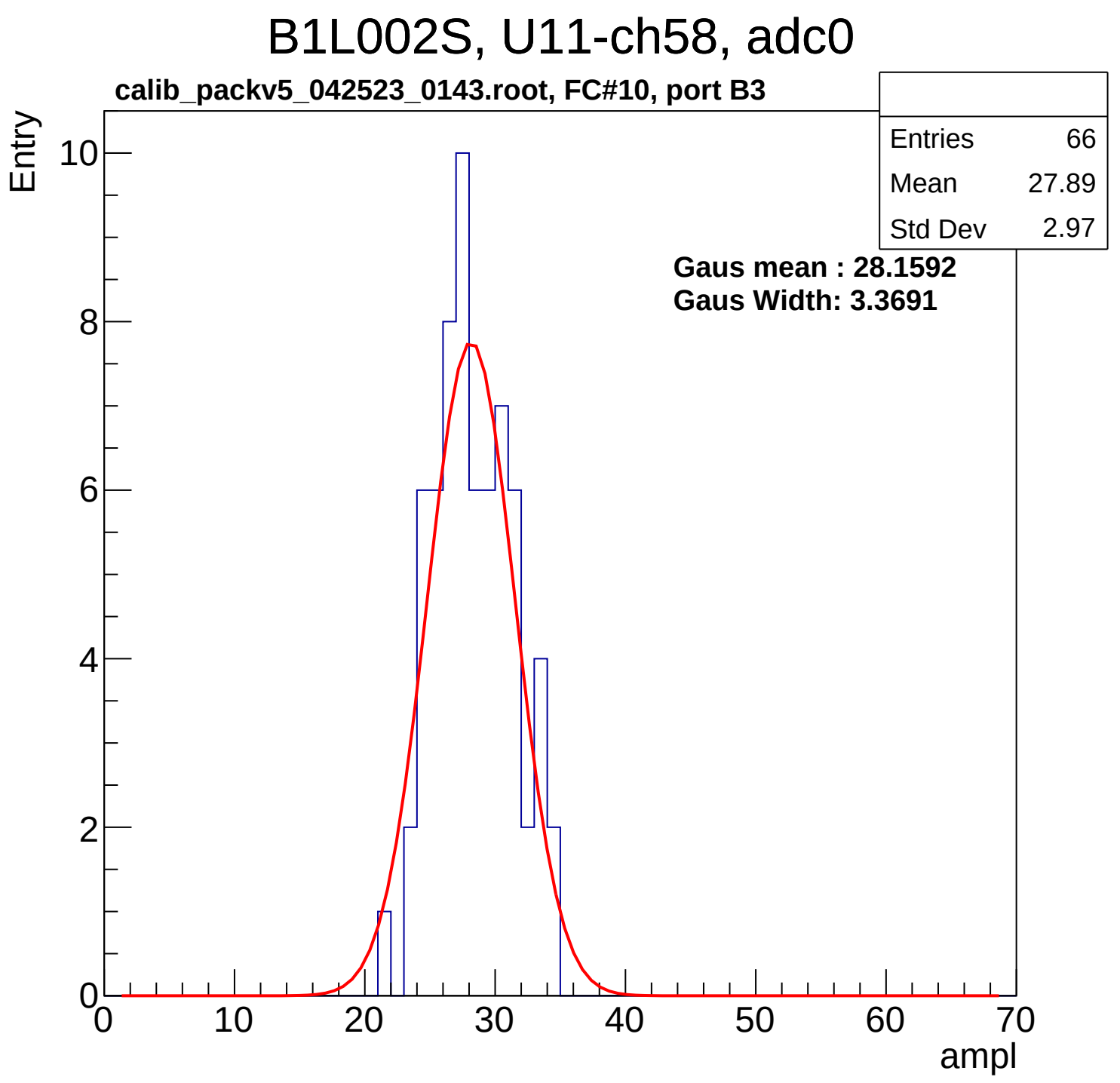
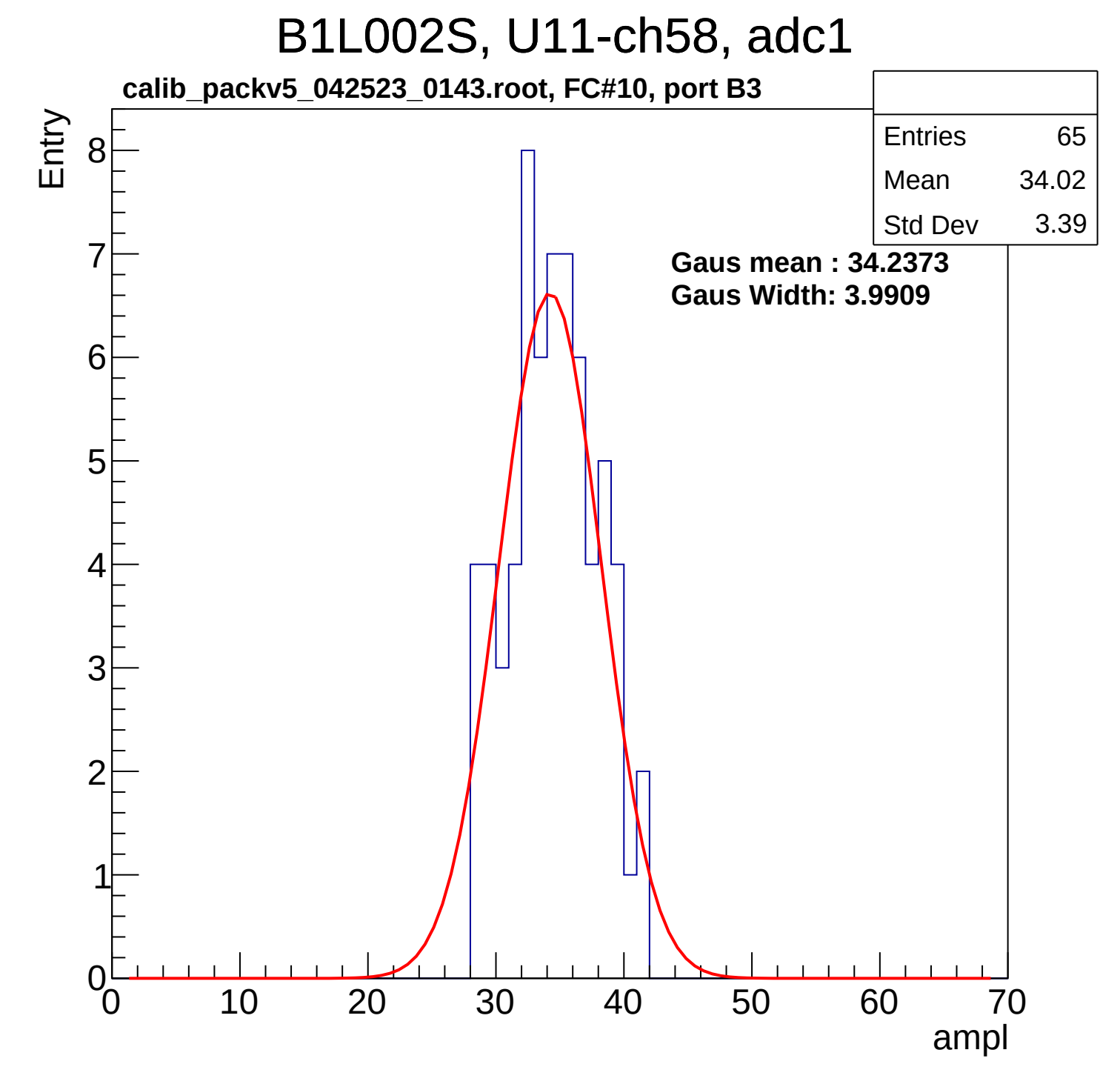
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.89
Std Dev 2.97
Gaus mean : 28.1592
Gaus Width: 3.3691
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.02
Std Dev 3.39
Gaus mean : 34.2373
Gaus Width: 3.9909
Entry
ampl
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5
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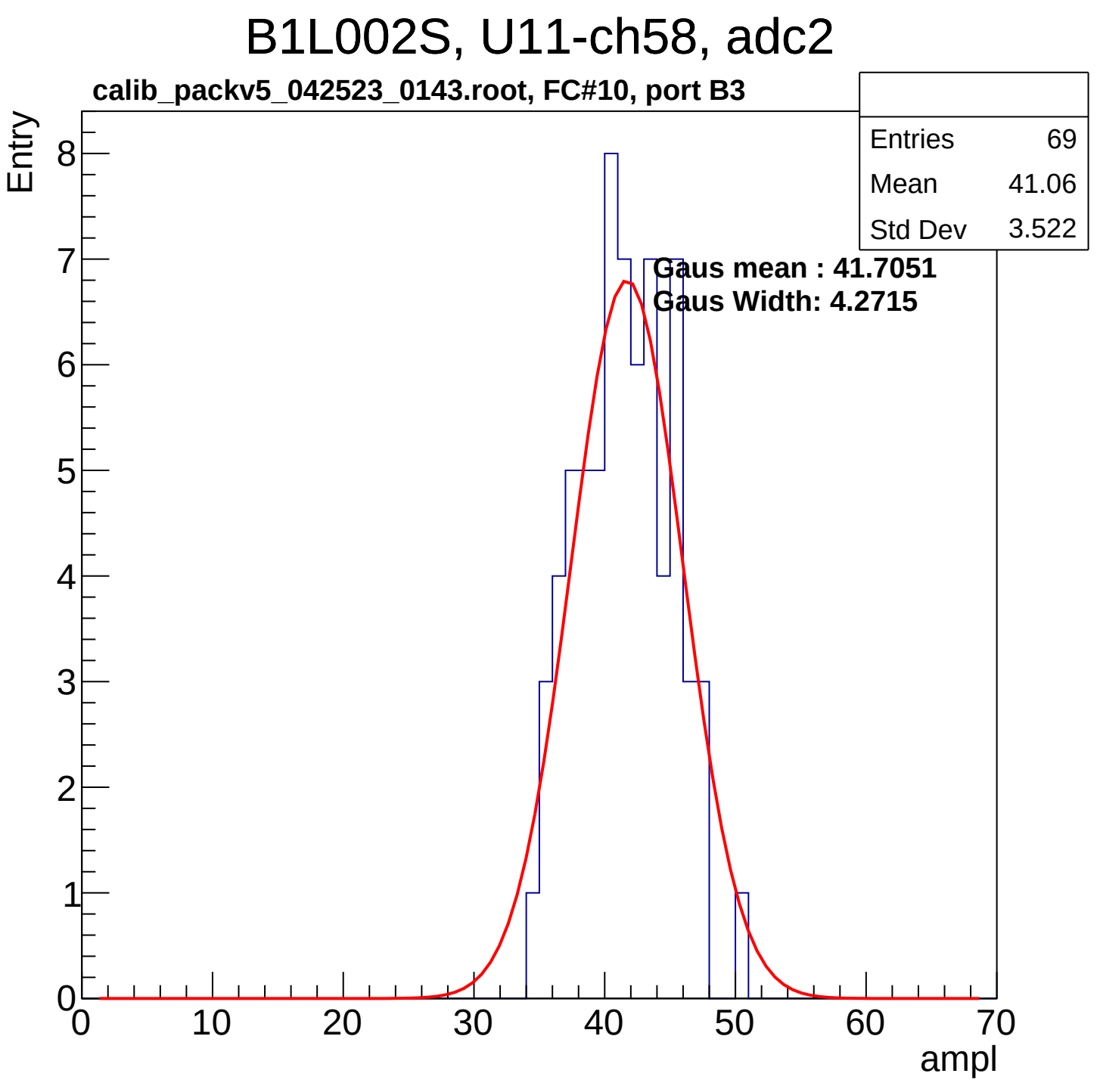
B1L002S, U11-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.06
Std Dev 3.522
Gaus mean : 41.7051
Gaus Width: 4.2715
Entry
ampl
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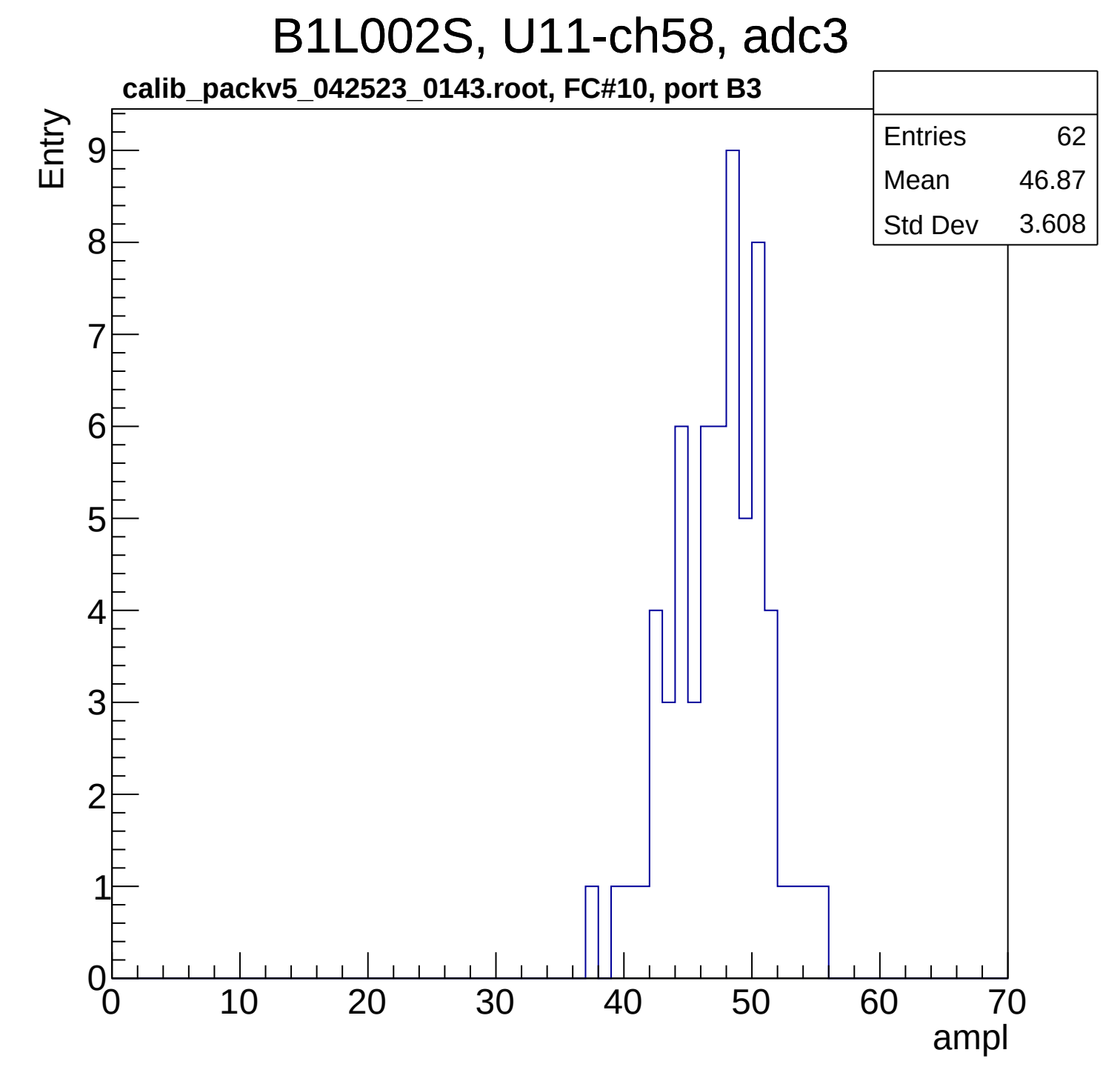
B1L002S, U11-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.87
Std Dev 3.608
Entry
ampl
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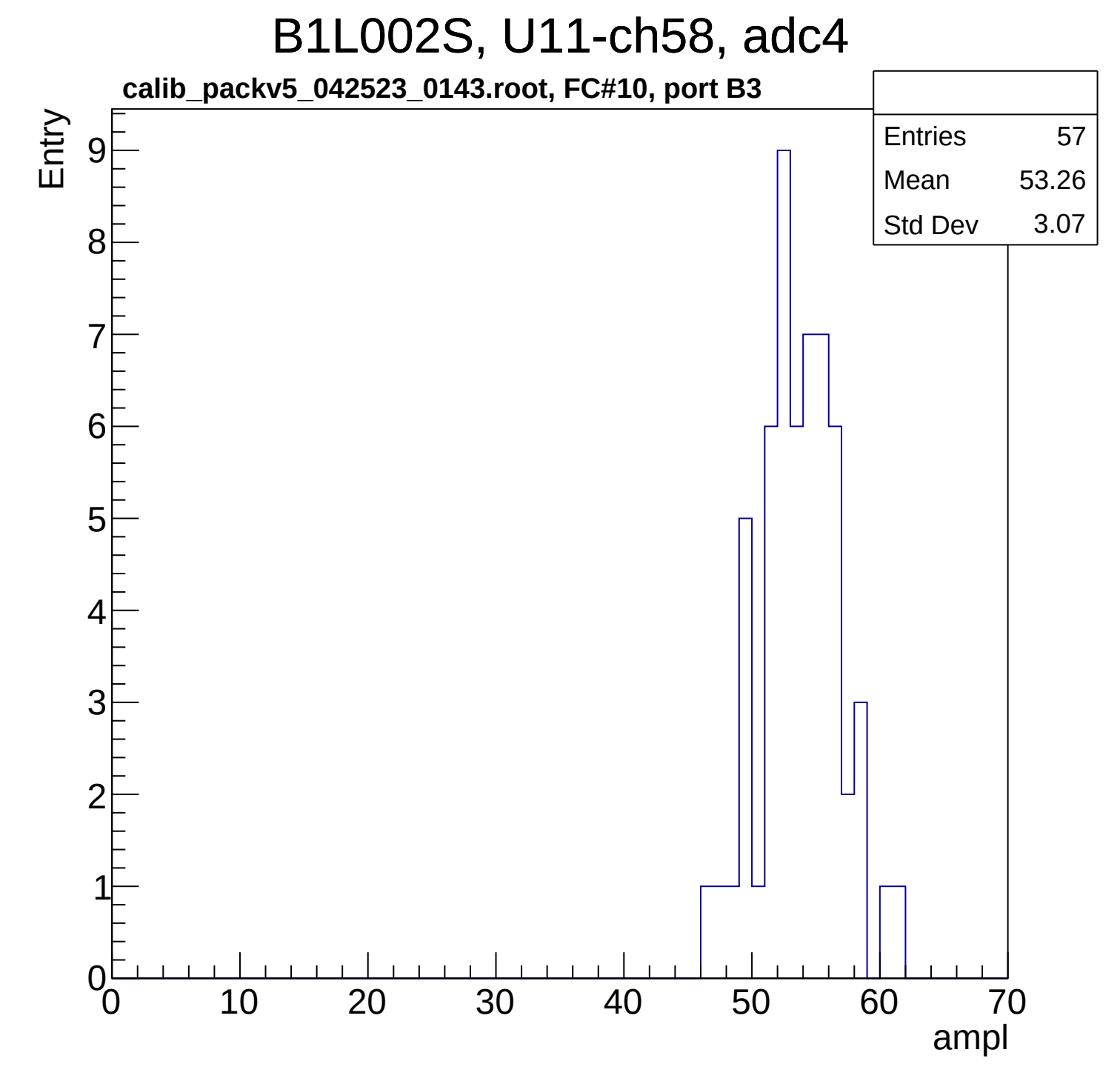
B1L002S, U11-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.26
Std Dev 3.07
Entry
ampl
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3
4
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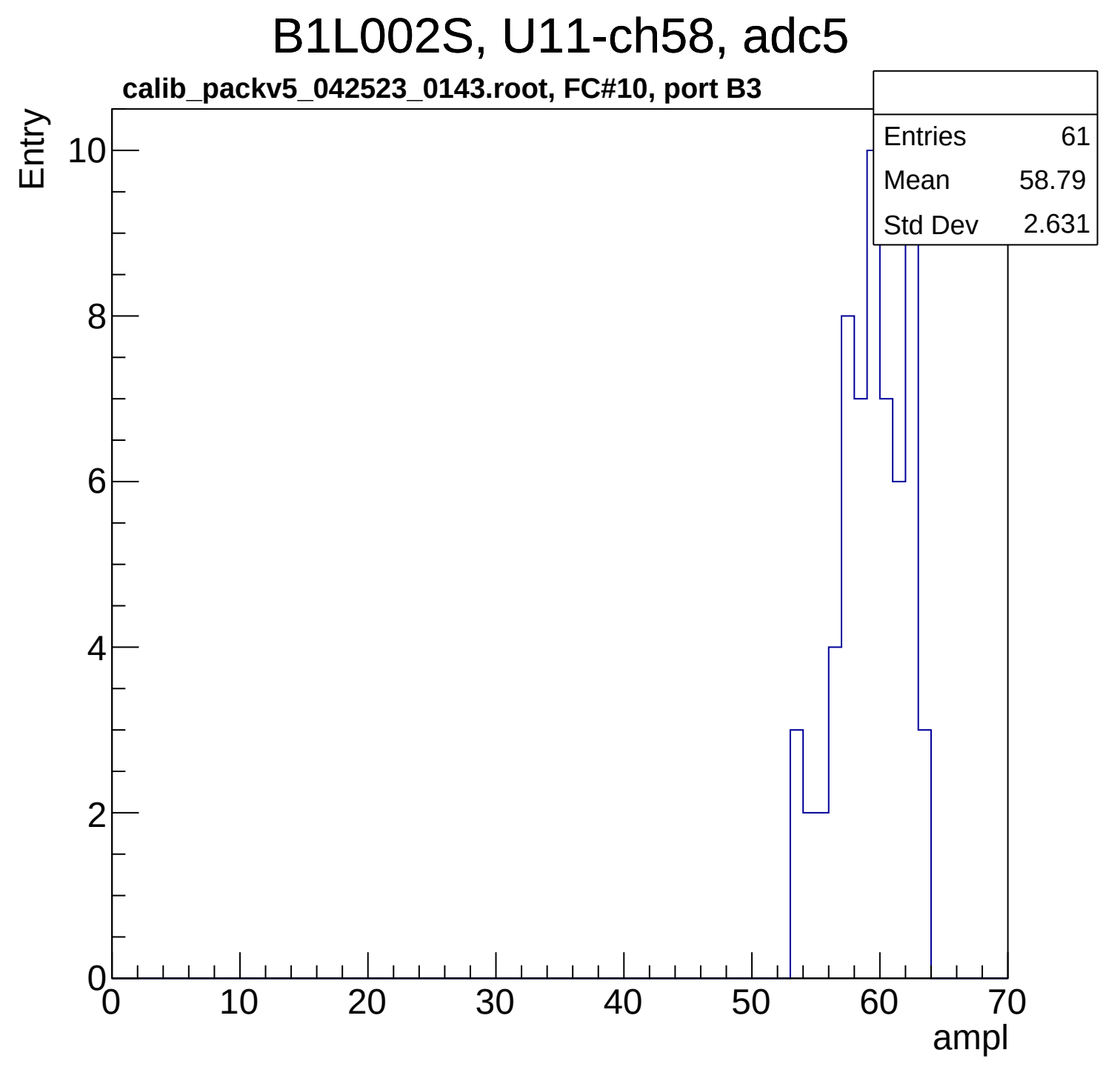
B1L002S, U11-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.79
Std Dev 2.631
Entry
ampl
0
2
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0
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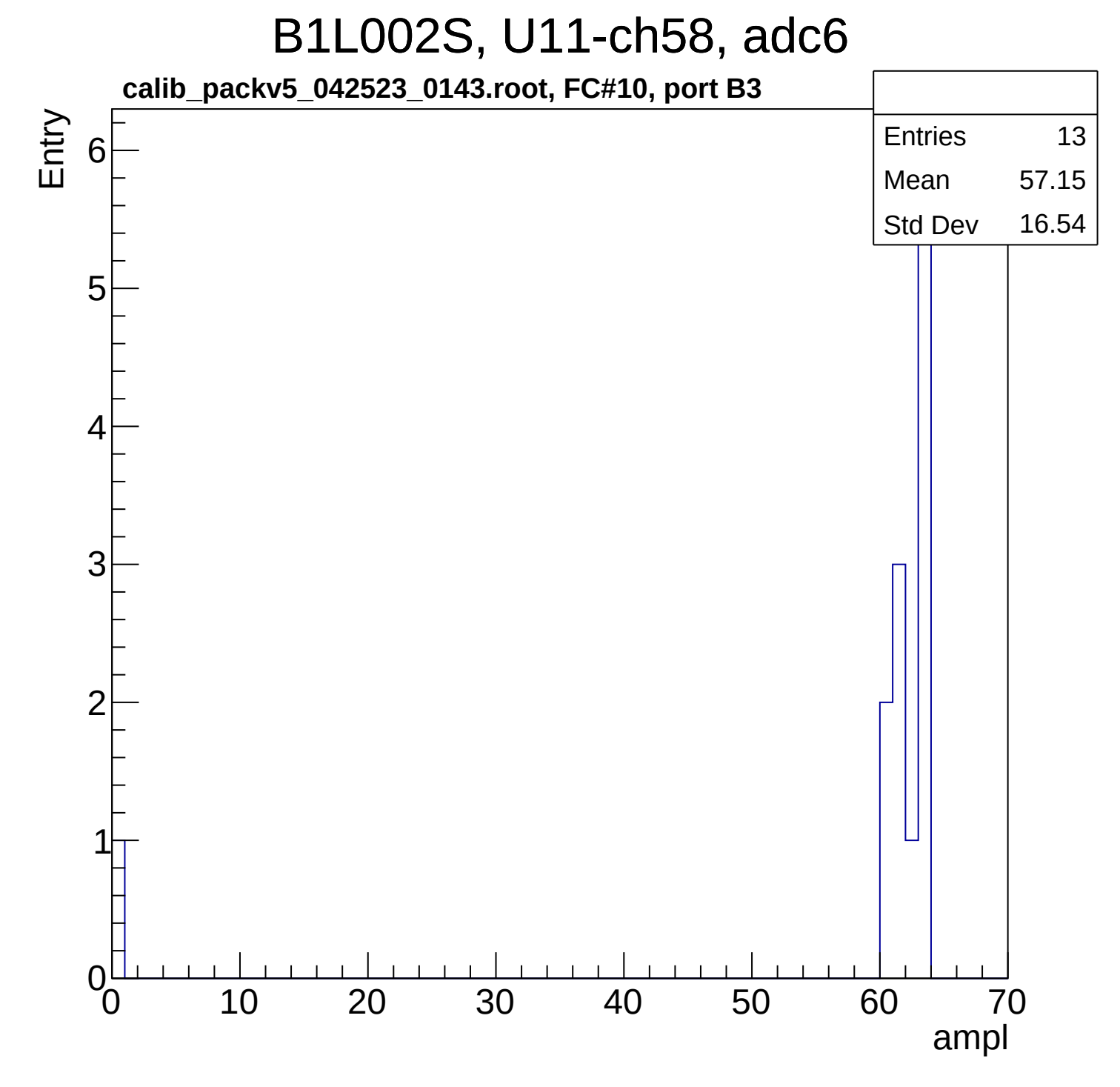
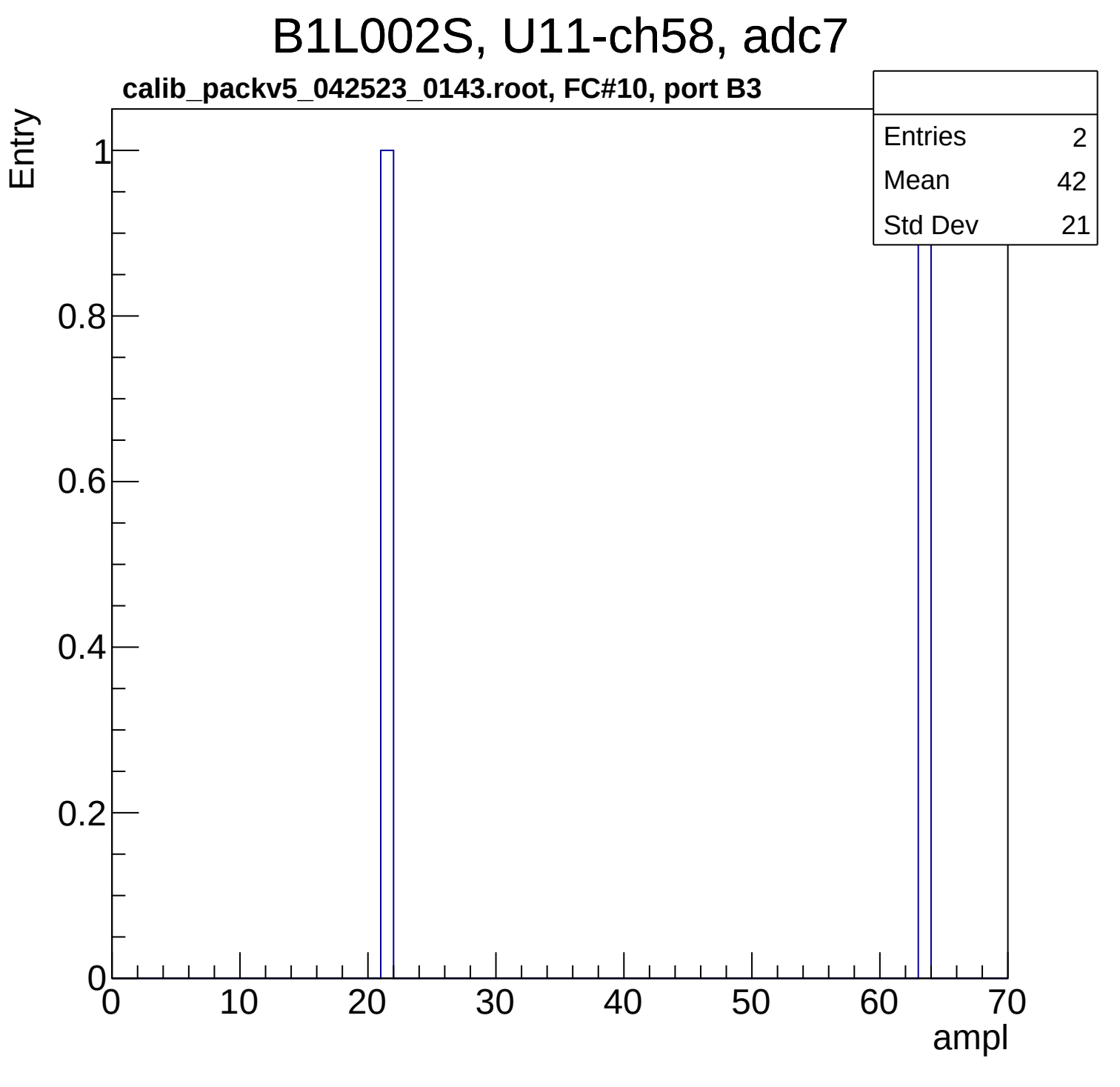

B1L002S, U11-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.15
Std Dev 16.54
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 21
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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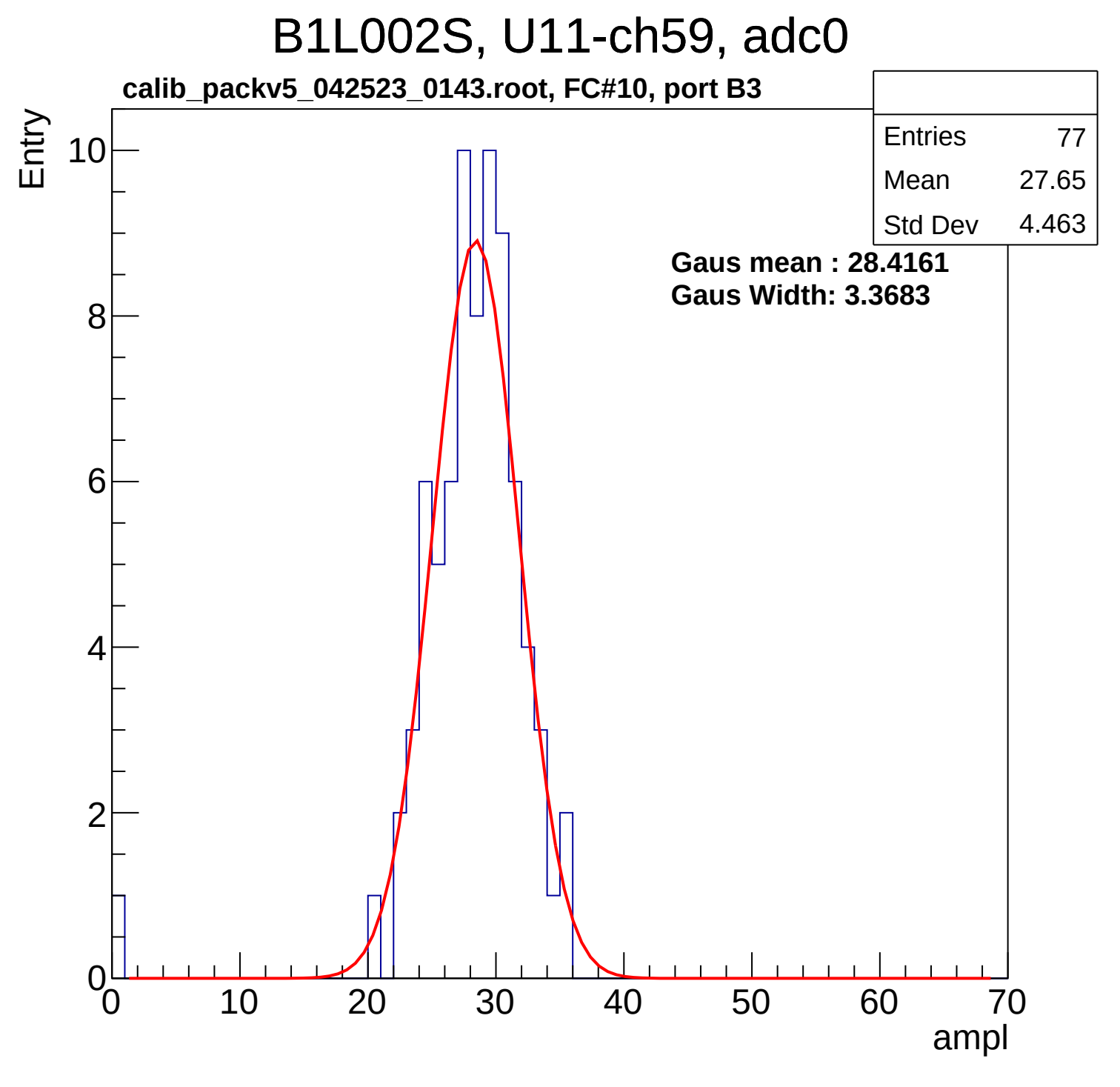
B1L002S, U11-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 27.65
Std Dev 4.463
Gaus mean : 28.4161
Gaus Width: 3.3683
Entry
ampl
0
2
4
6
8
10
0
10
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40
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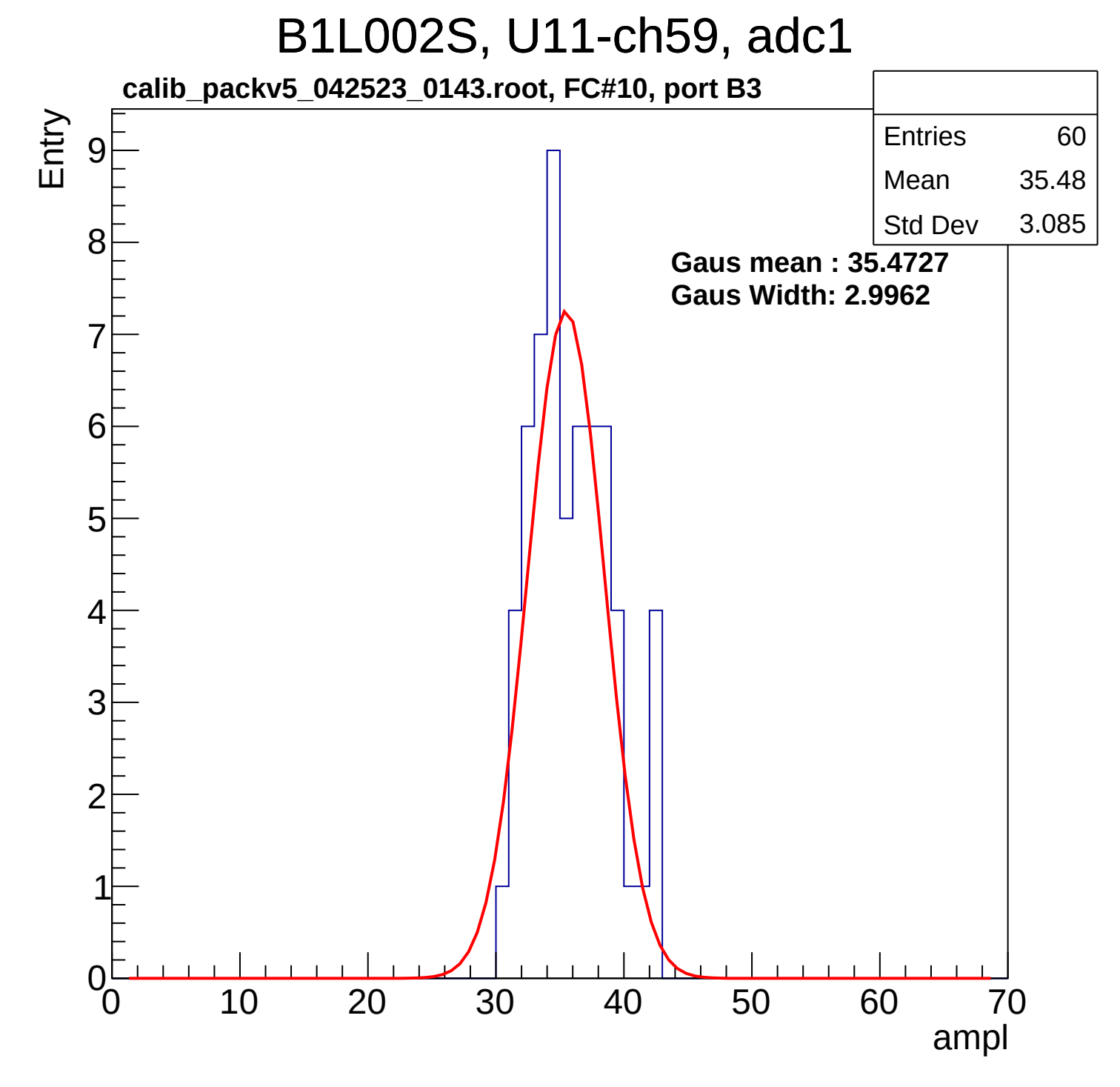
B1L002S, U11-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.48
Std Dev 3.085
Gaus mean : 35.4727
Gaus Width: 2.9962
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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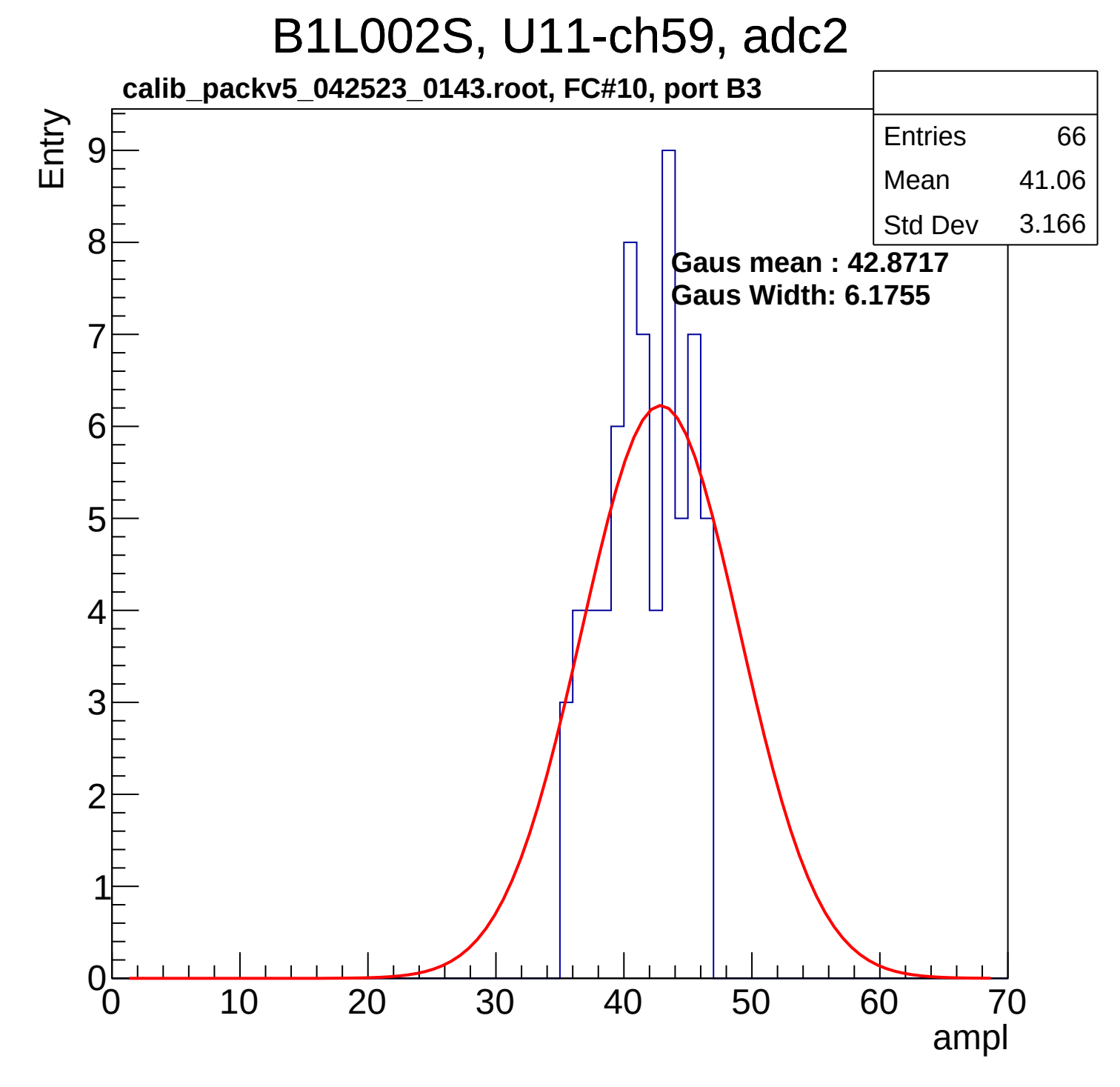
B1L002S, U11-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.06
Std Dev 3.166
Gaus mean : 42.8717
Gaus Width: 6.1755
Entry
ampl
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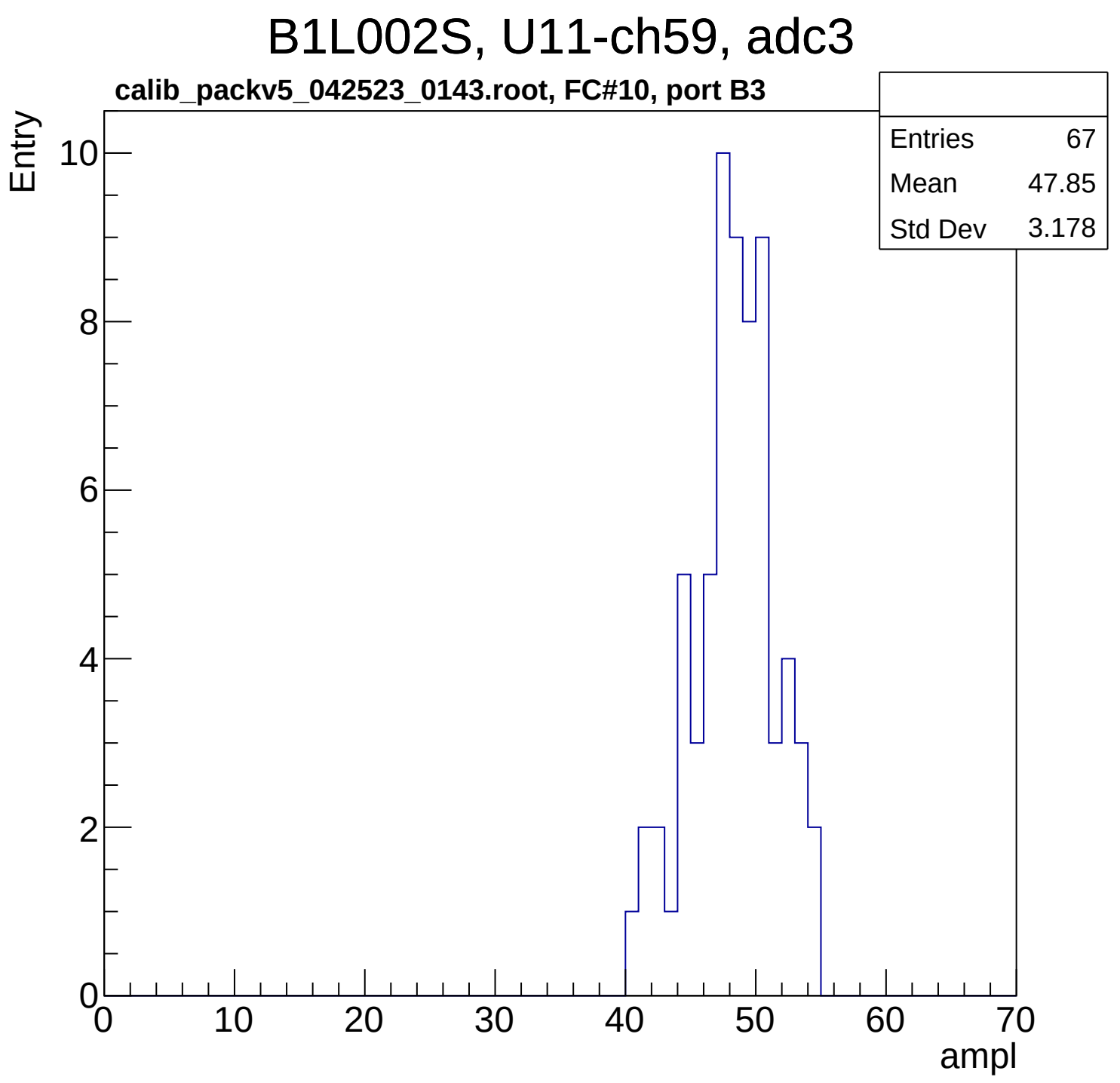
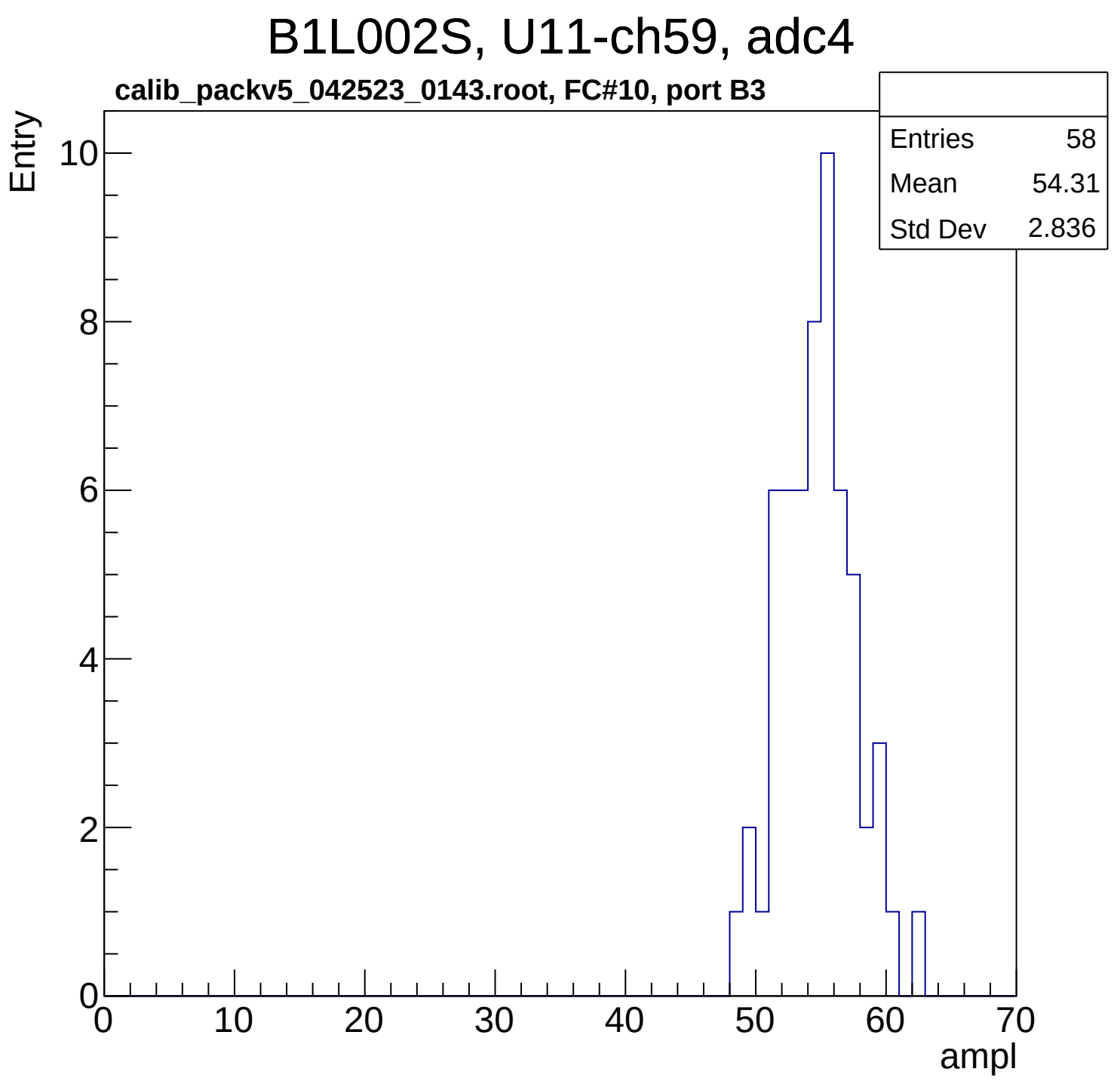
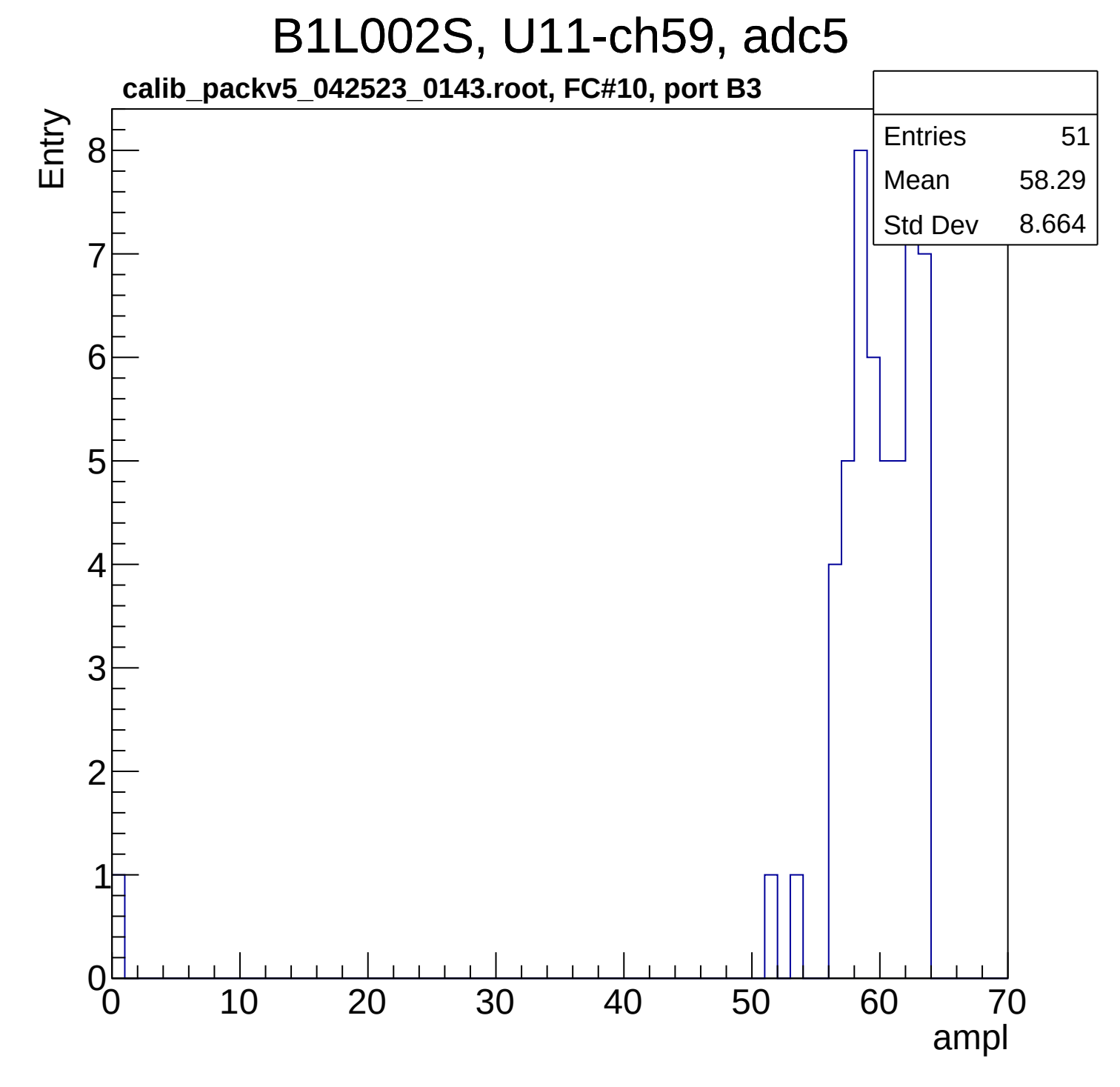
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.85
Std Dev 3.178
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.31
Std Dev 2.836
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.29
Std Dev 8.664
Entry
ampl
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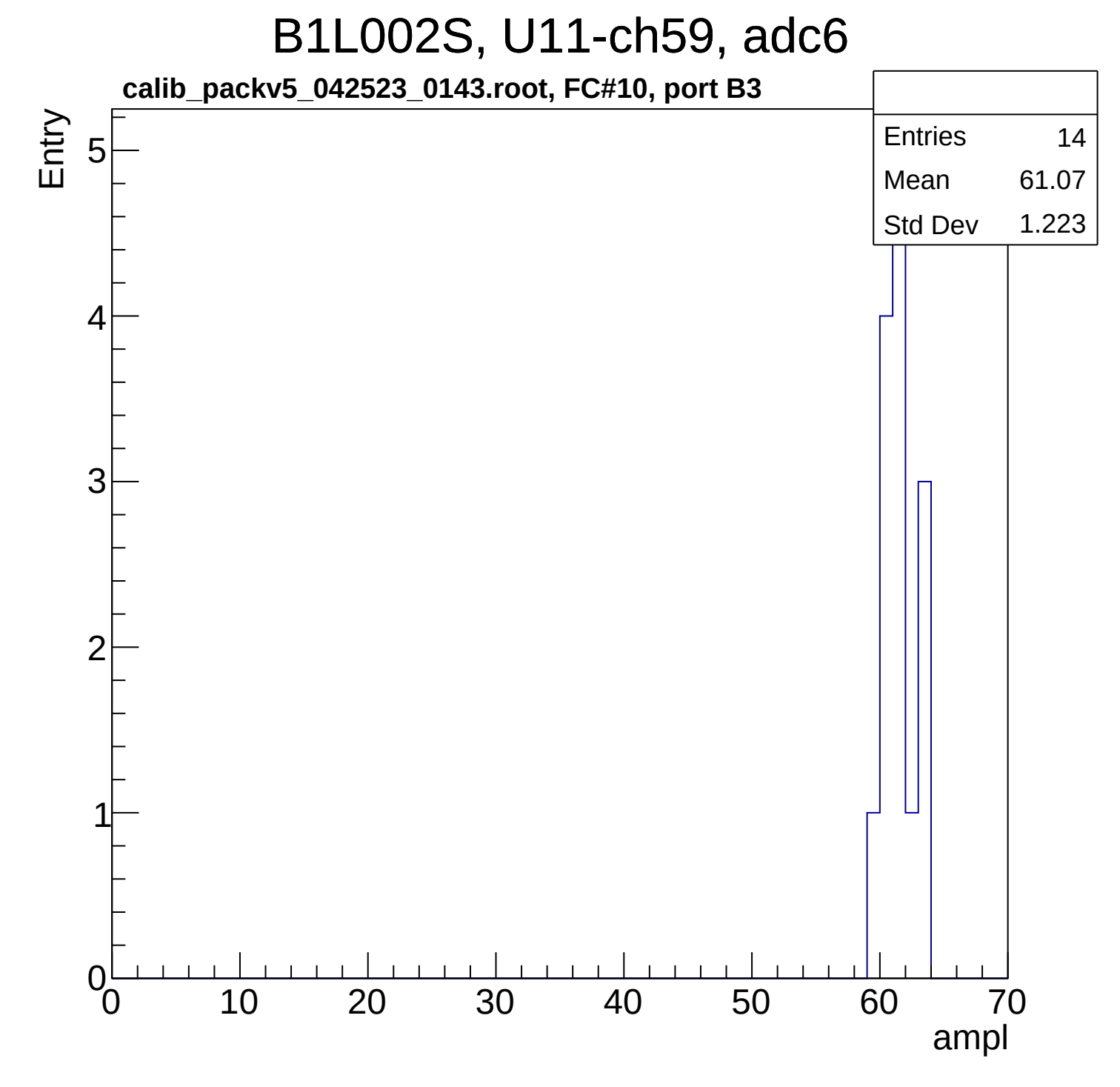
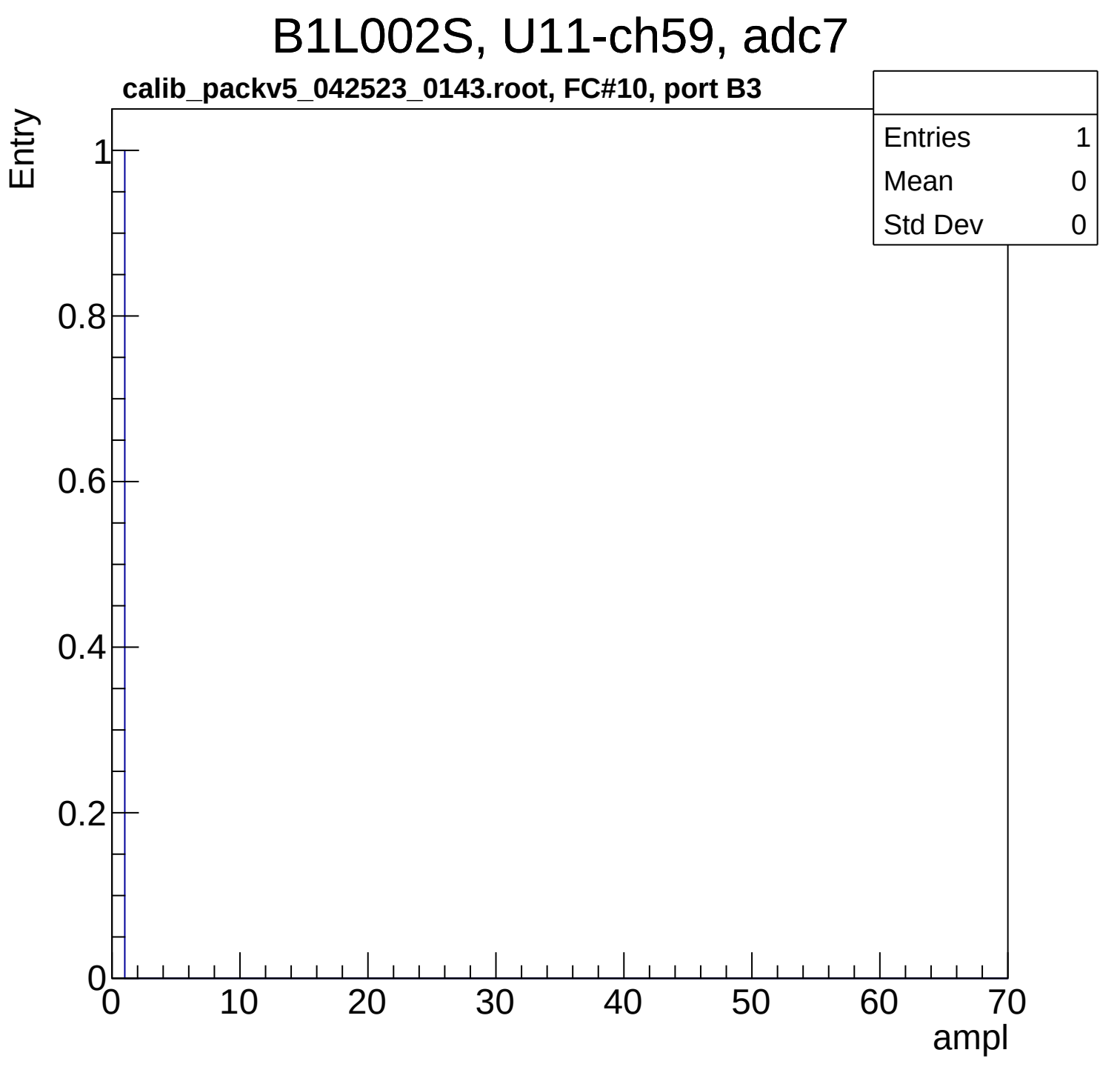
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.07
Std Dev 1.223
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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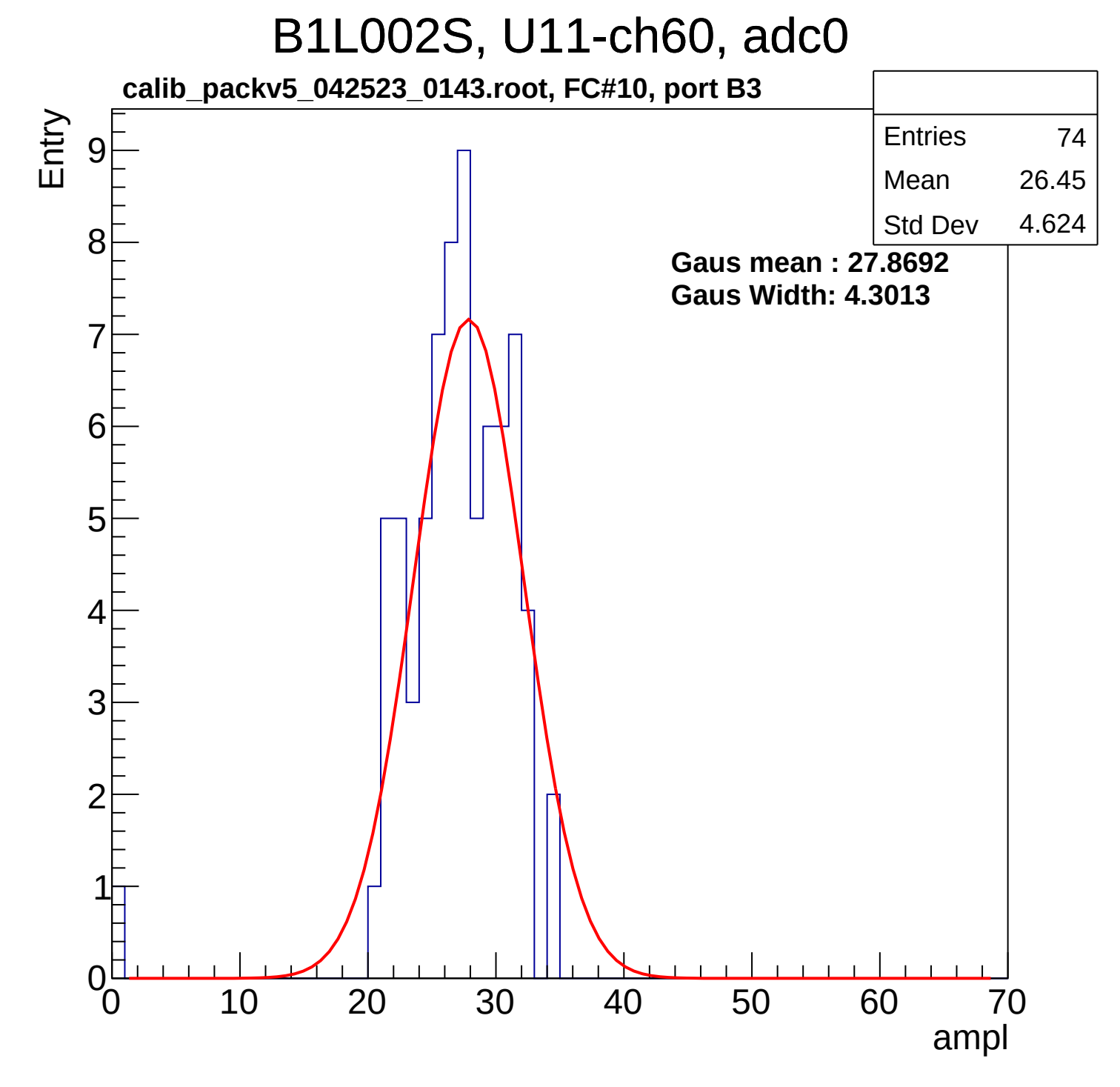
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 26.45
Std Dev 4.624
Gaus mean : 27.8692
Gaus Width: 4.3013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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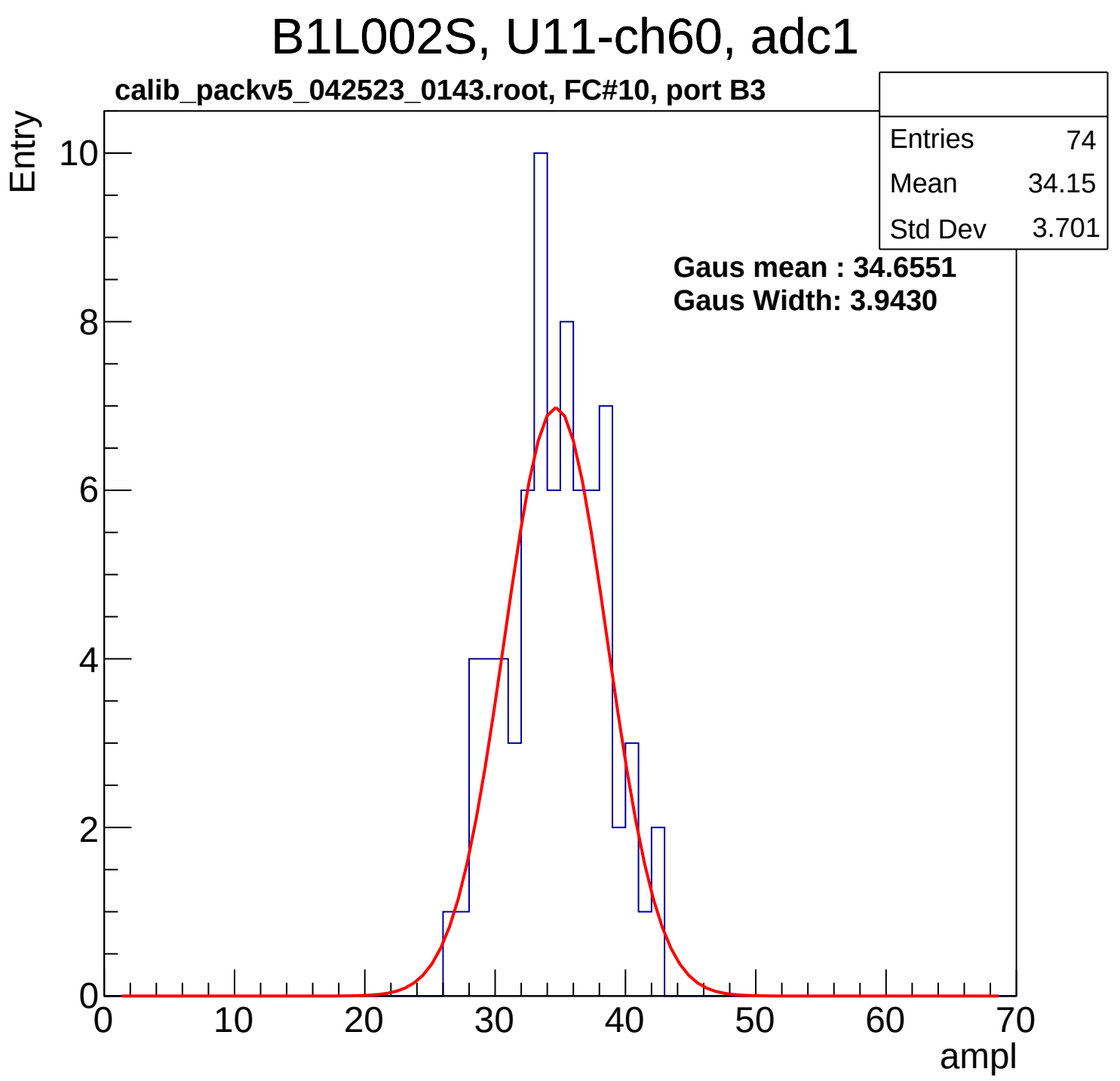
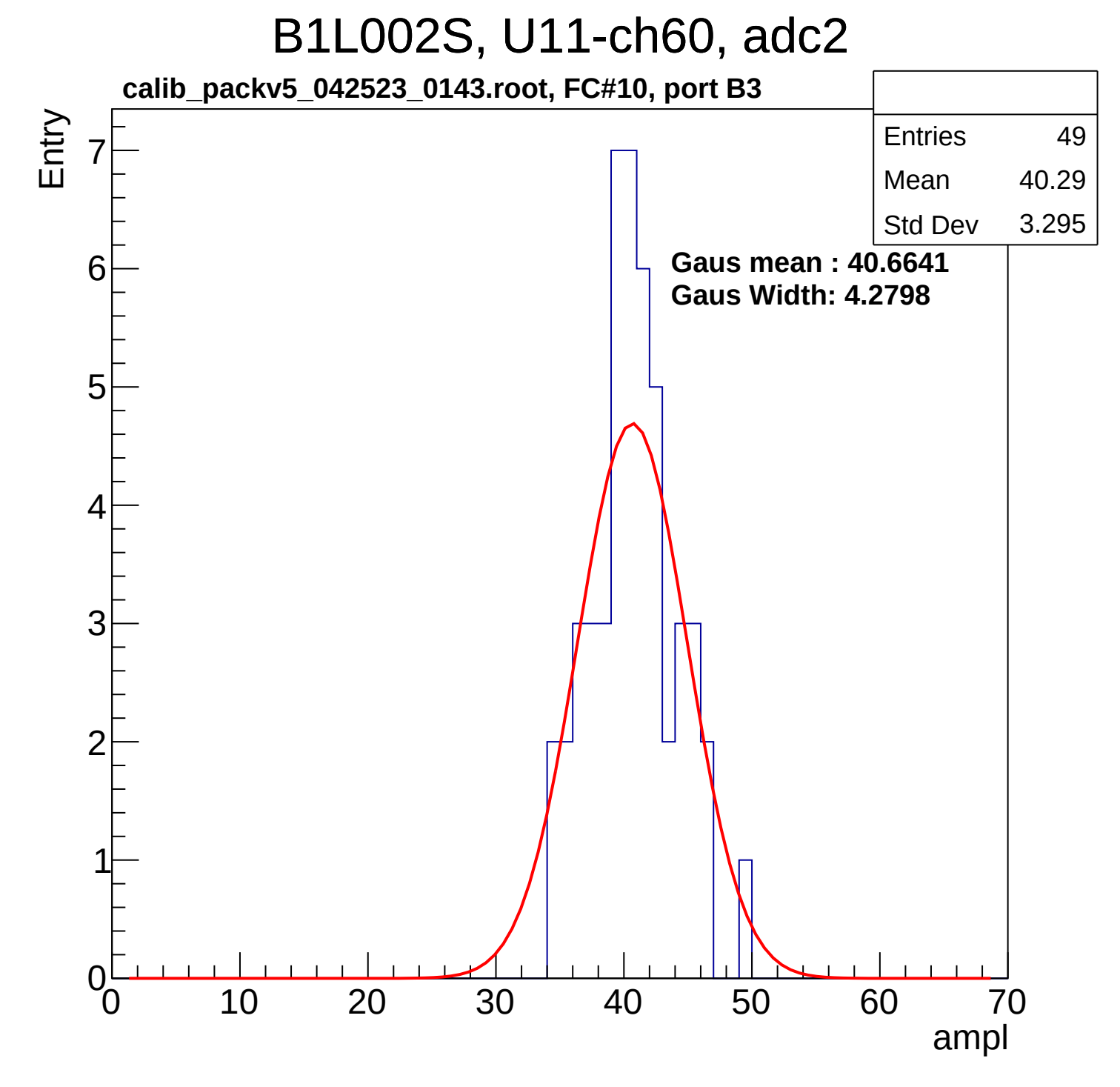
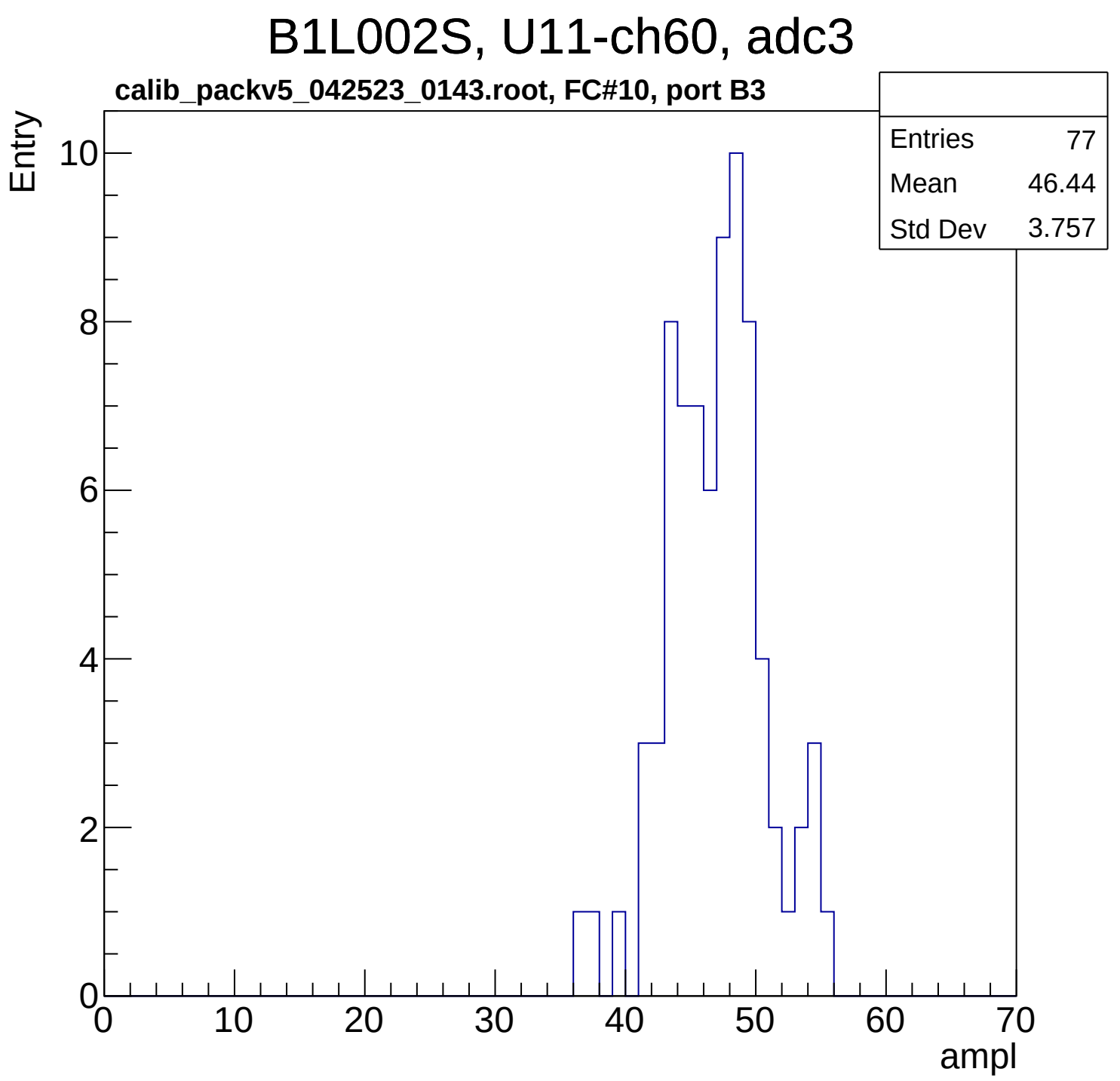
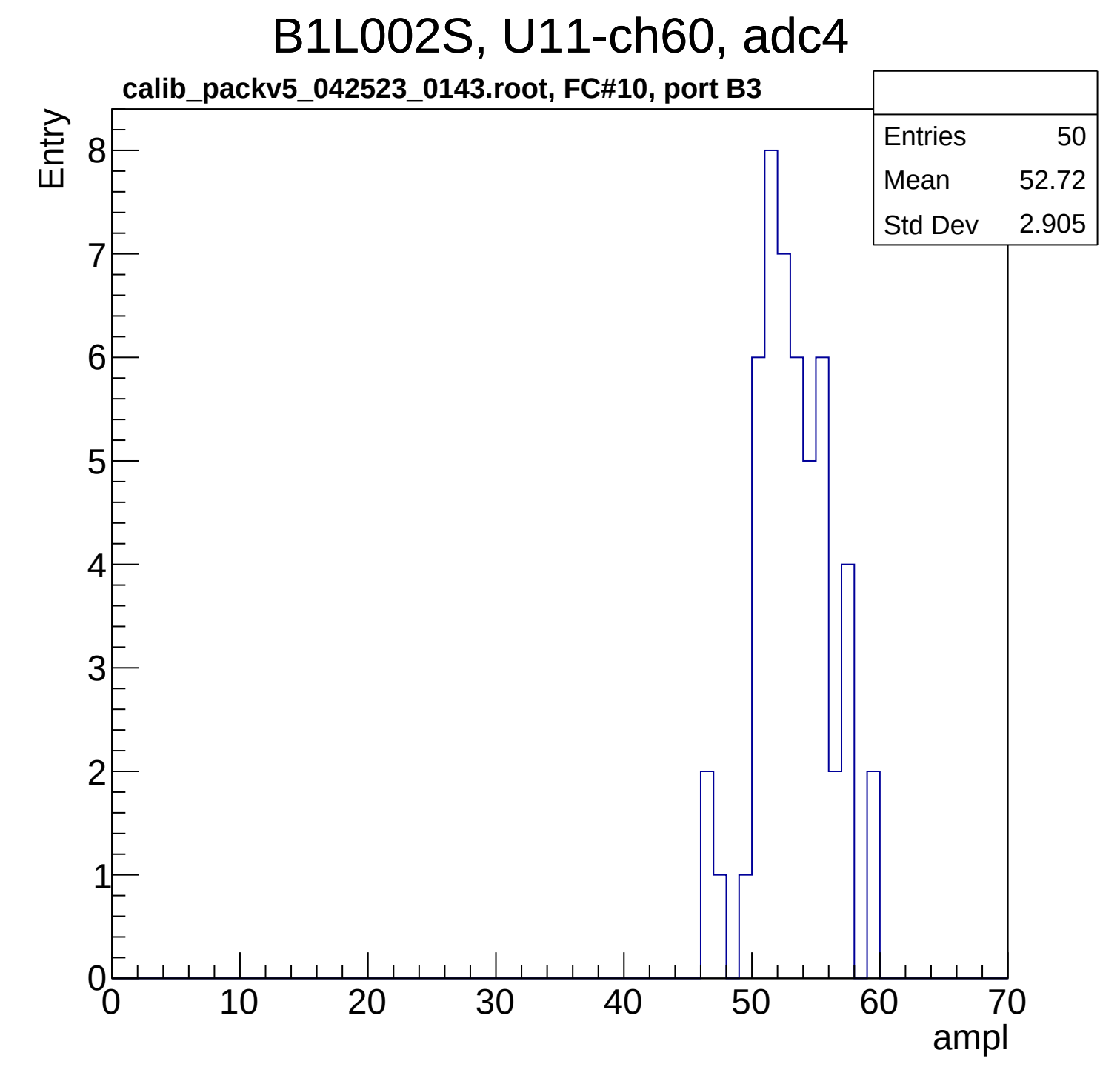

B1L002S, U11-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.15
Std Dev 3.701
Gaus mean : 34.6551
Gaus Width: 3.9430
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.29
Std Dev 3.295
Gaus mean : 40.6641
Gaus Width: 4.2798
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 46.44
Std Dev 3.757
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.72
Std Dev 2.905
Entry
ampl
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2
3
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5
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7
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0
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40
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60
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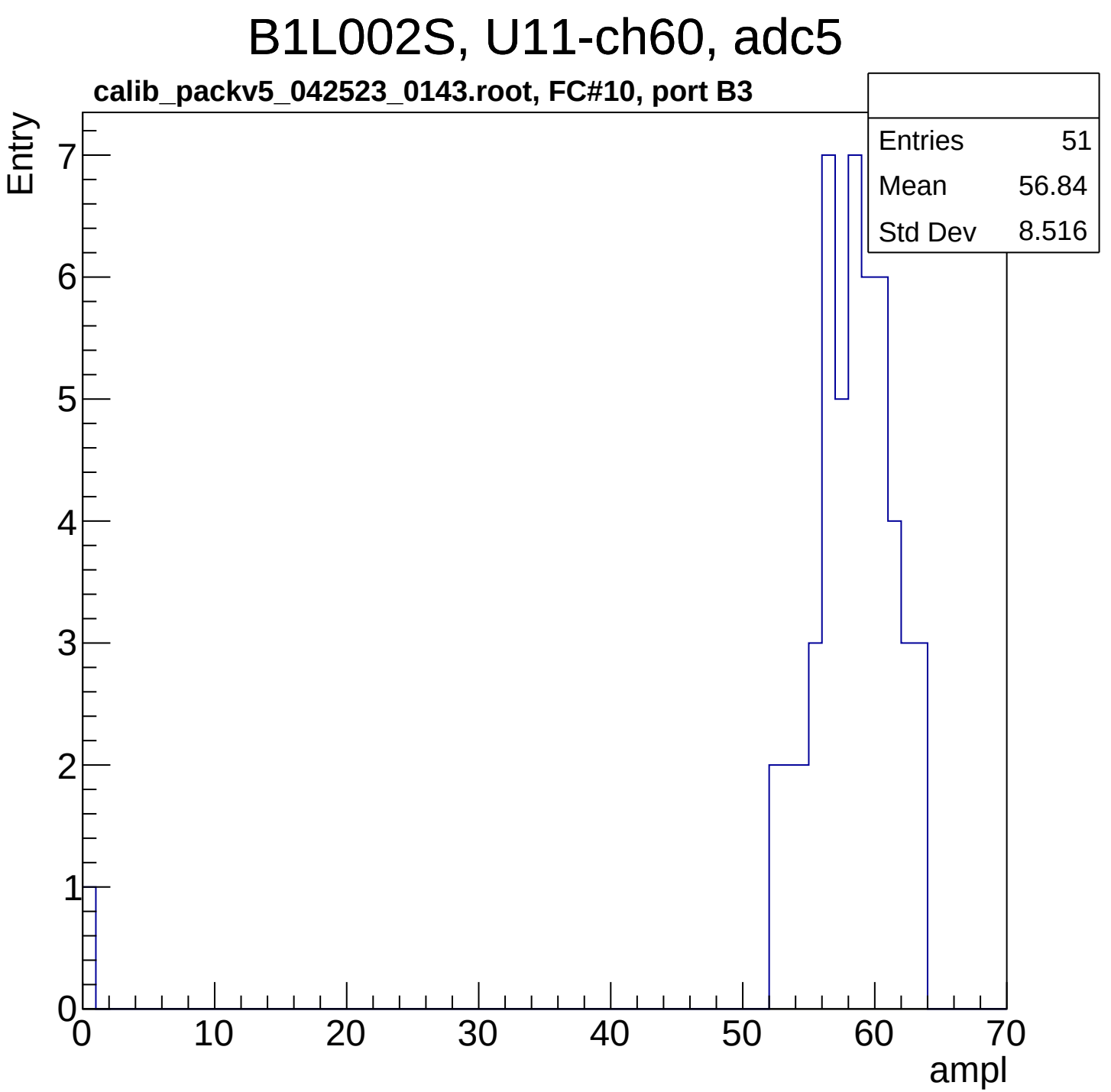
B1L002S, U11-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 56.84
Std Dev 8.516
Entry
ampl
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0
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20
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40
50
60
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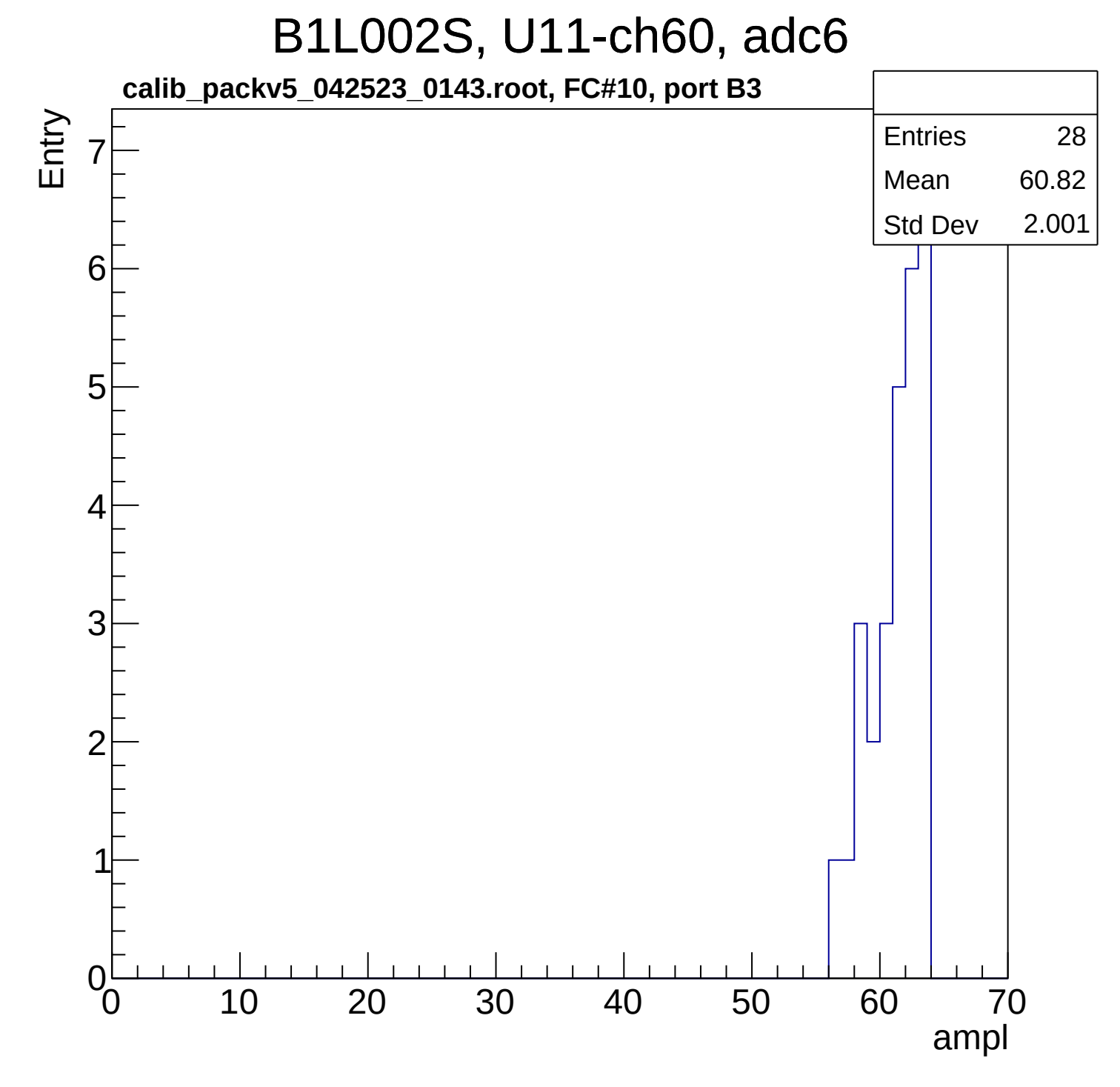
B1L002S, U11-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 60.82
Std Dev 2.001
Entry
ampl
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2
3
4
5
6
7
0
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20
30
40
50
60
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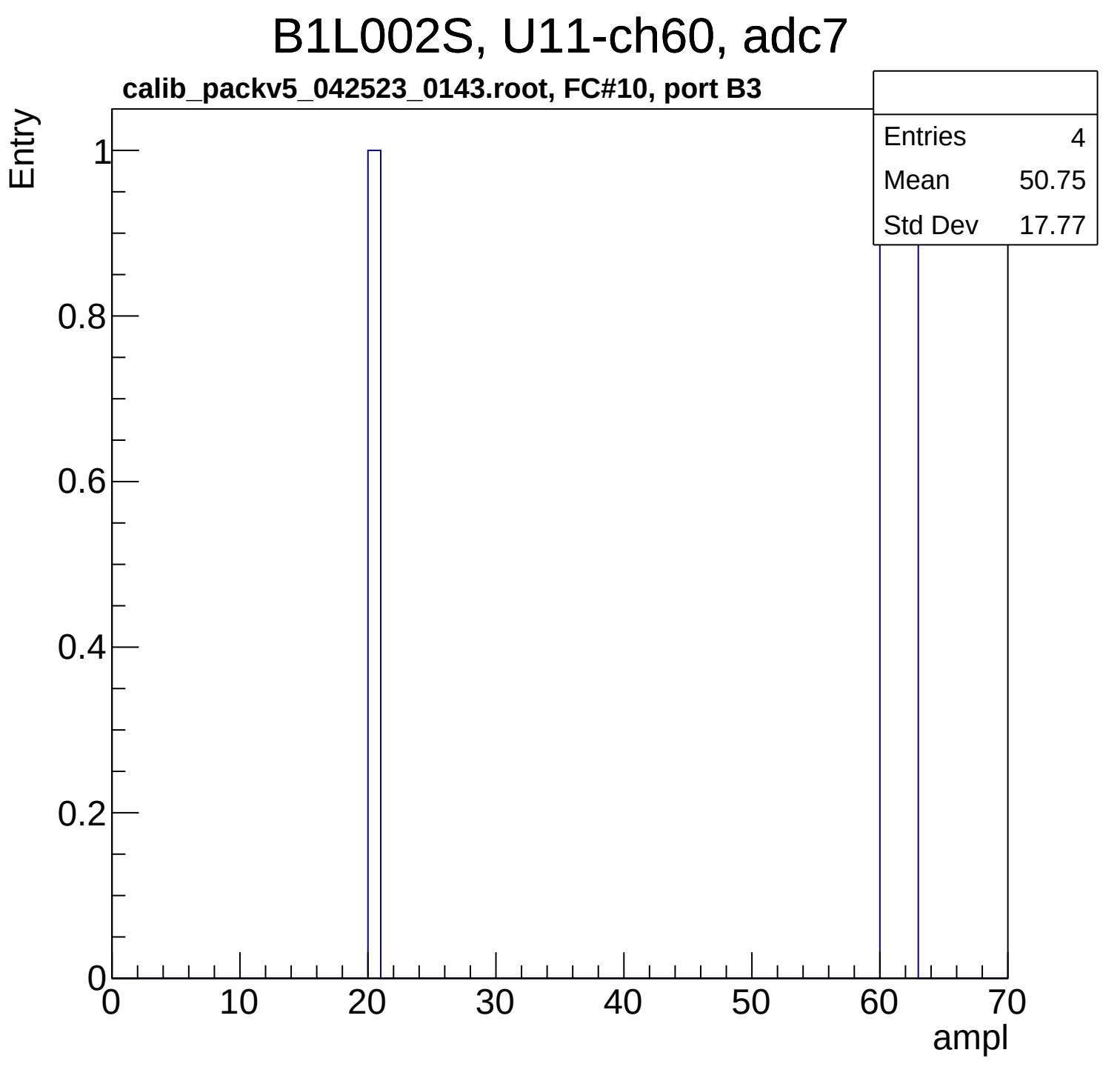
B1L002S, U11-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 50.75
Std Dev 17.77
Entry
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0.6
0.4
0.2
0
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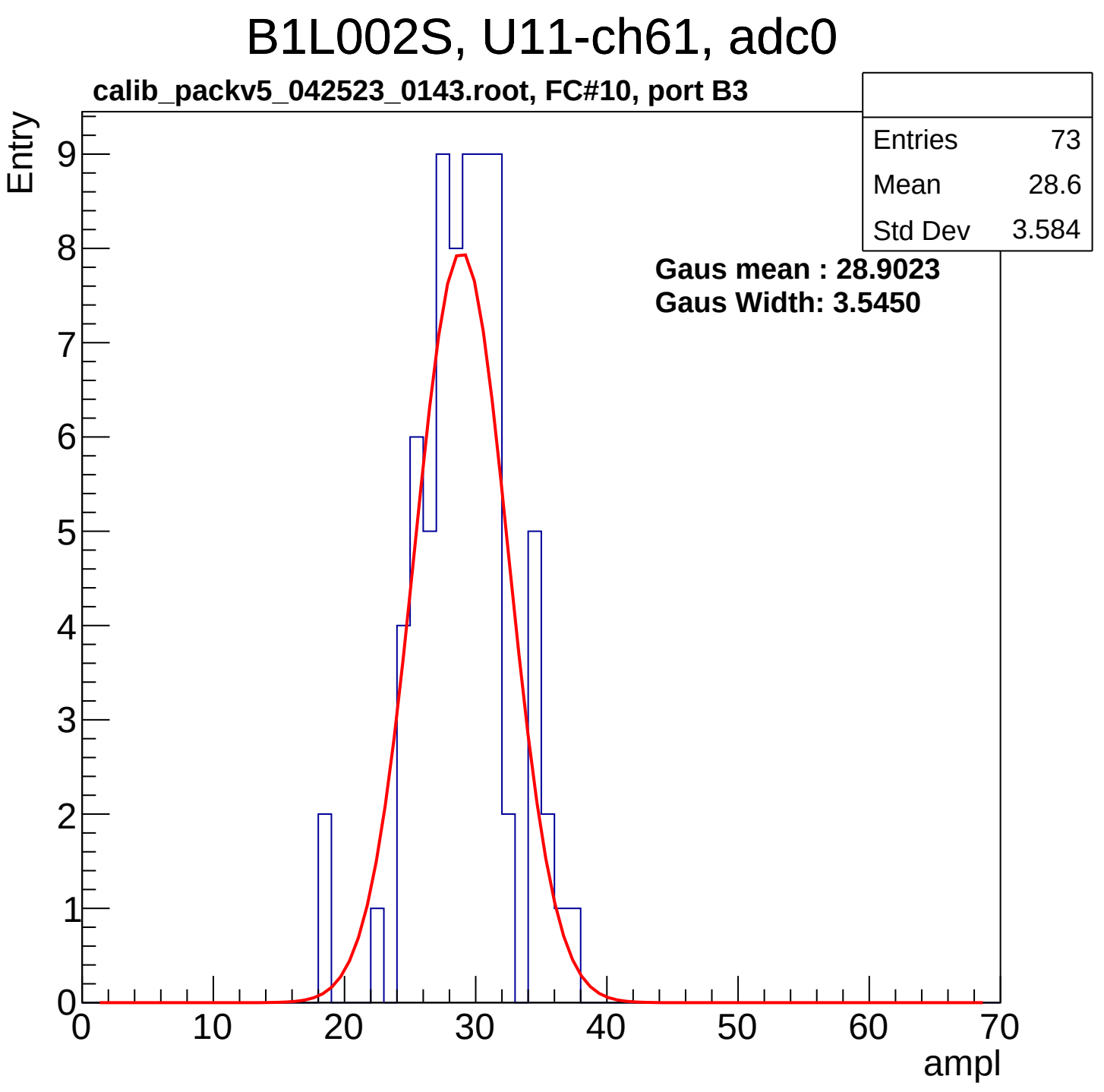
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.6
Std Dev 3.584
Gaus mean : 28.9023
Gaus Width: 3.5450
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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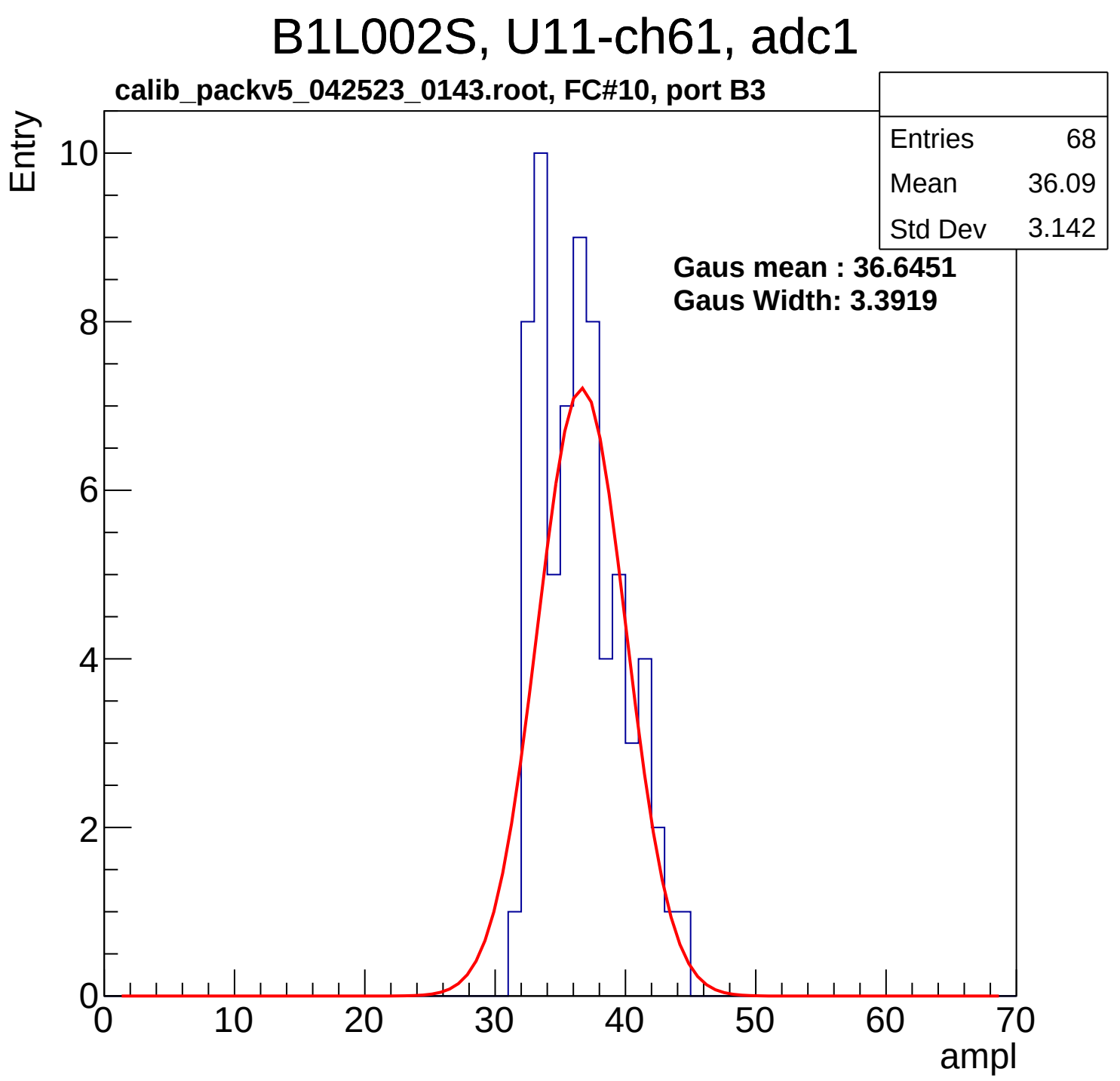
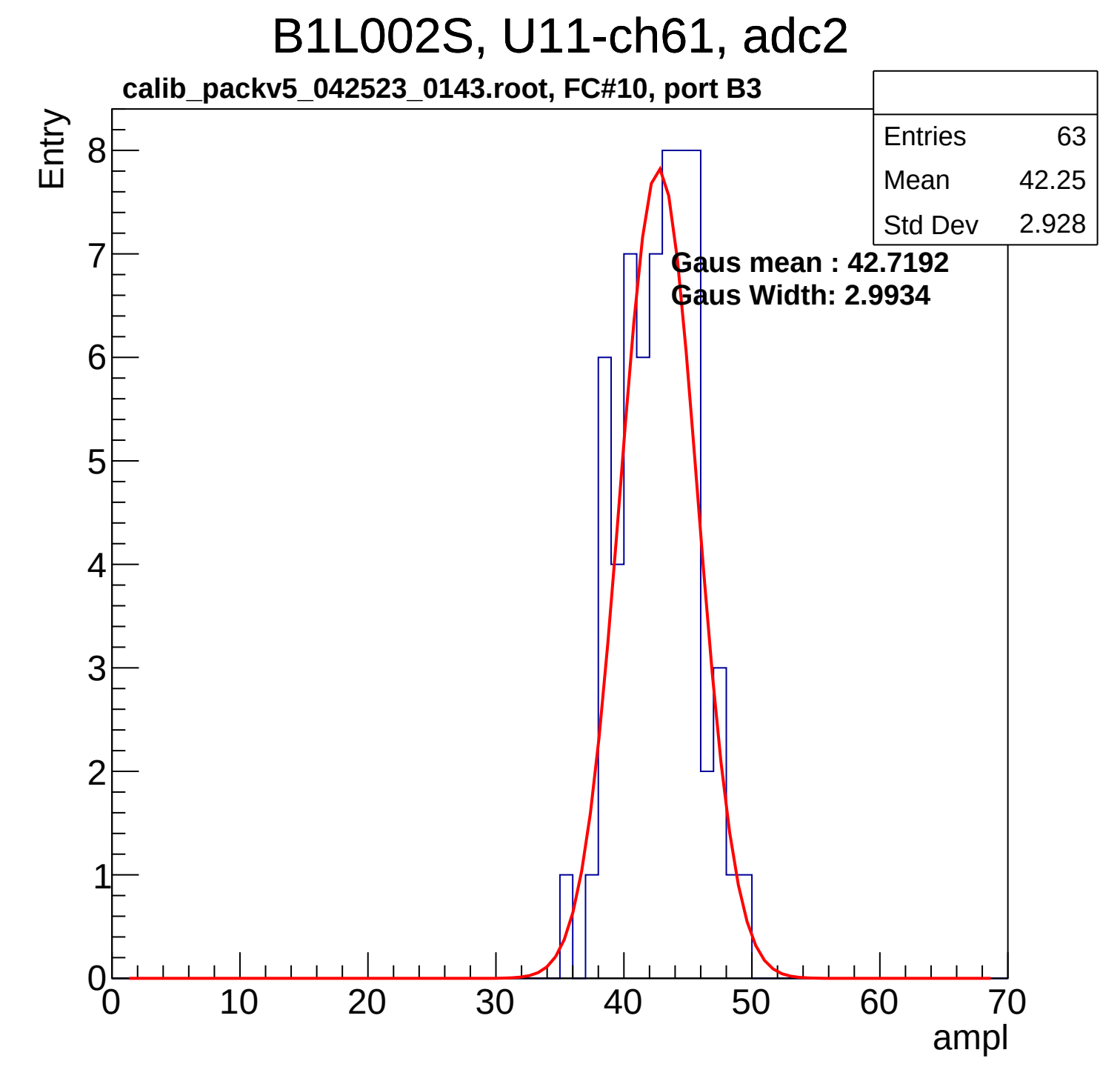
40
50
60
70


B1L002S, U11-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 36.09
Std Dev 3.142
Gaus mean : 36.6451
Gaus Width: 3.3919
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 42.25
Std Dev 2.928
Gaus mean : 42.7192
Gaus Width: 2.9934
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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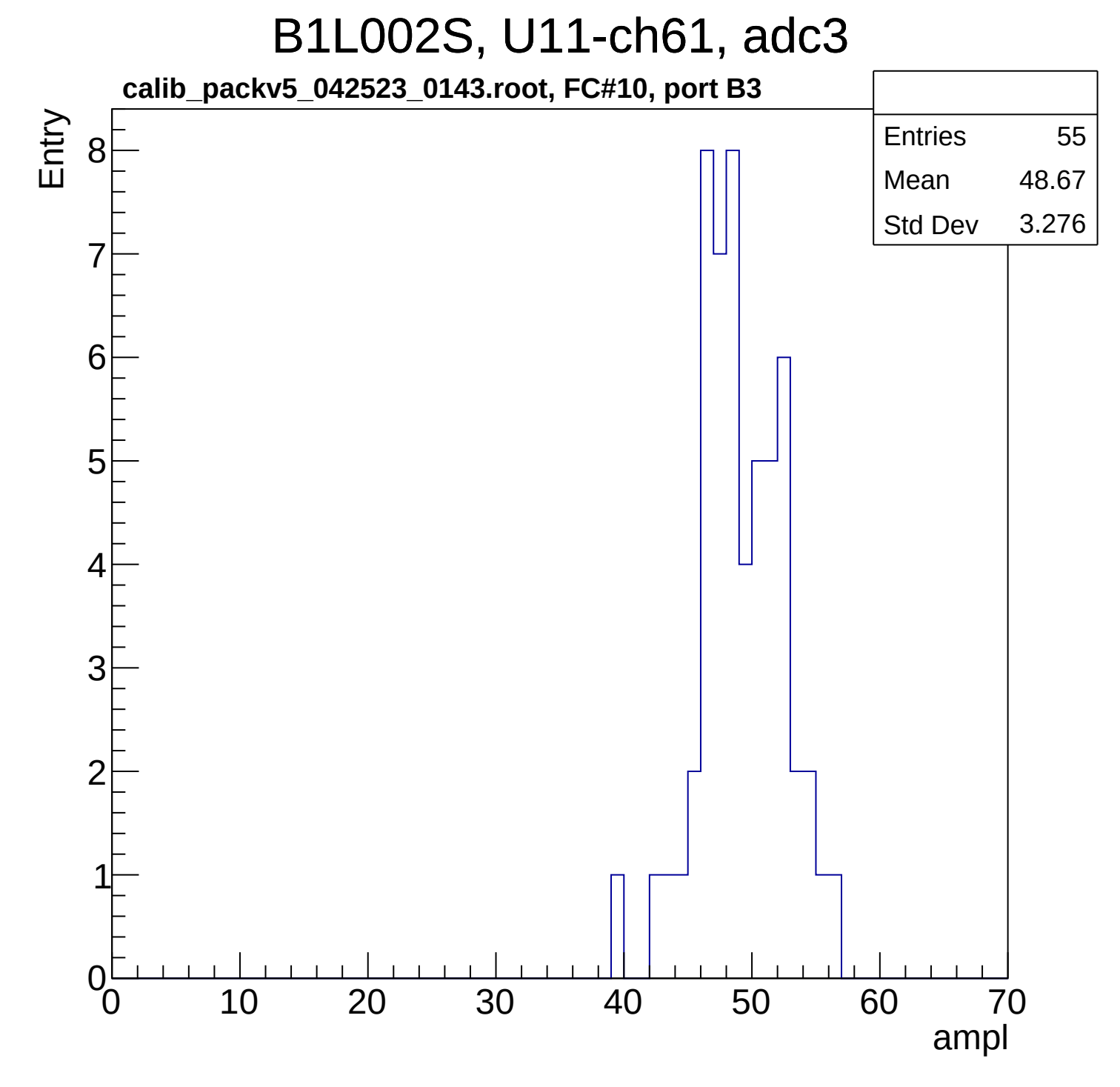
B1L002S, U11-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 48.67
Std Dev 3.276
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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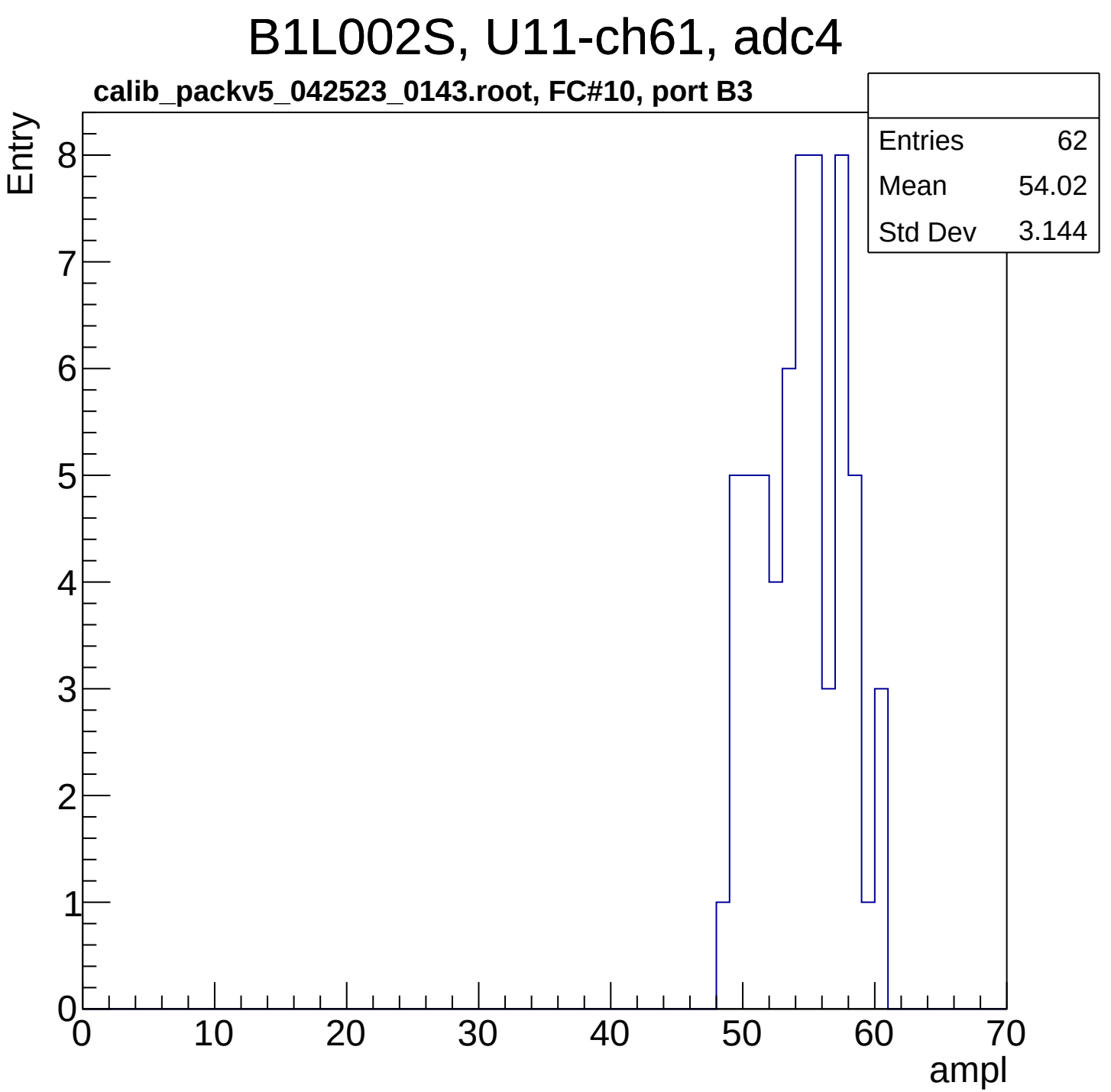
B1L002S, U11-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 54.02
Std Dev 3.144
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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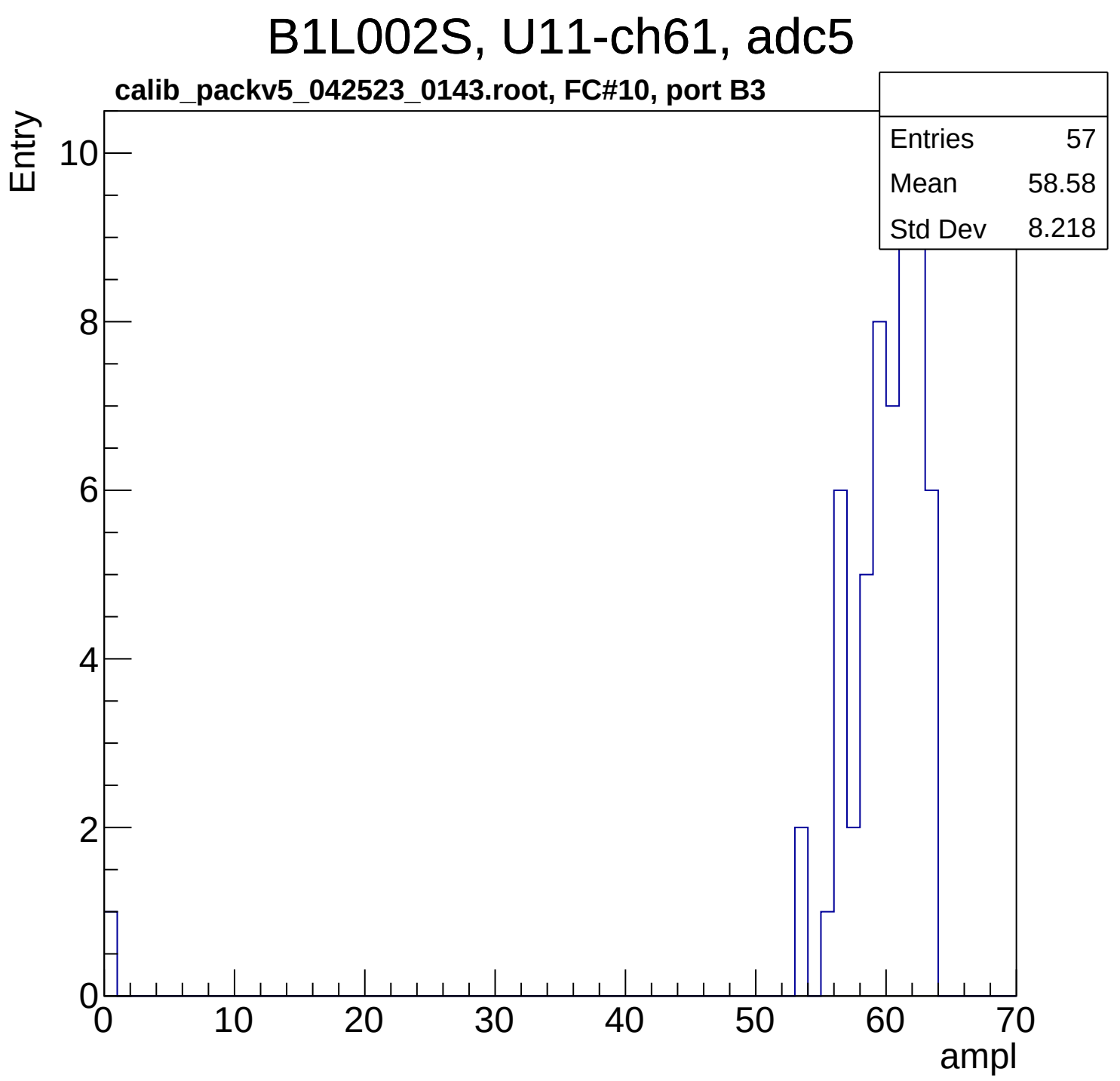
B1L002S, U11-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.58
Std Dev 8.218
Entry
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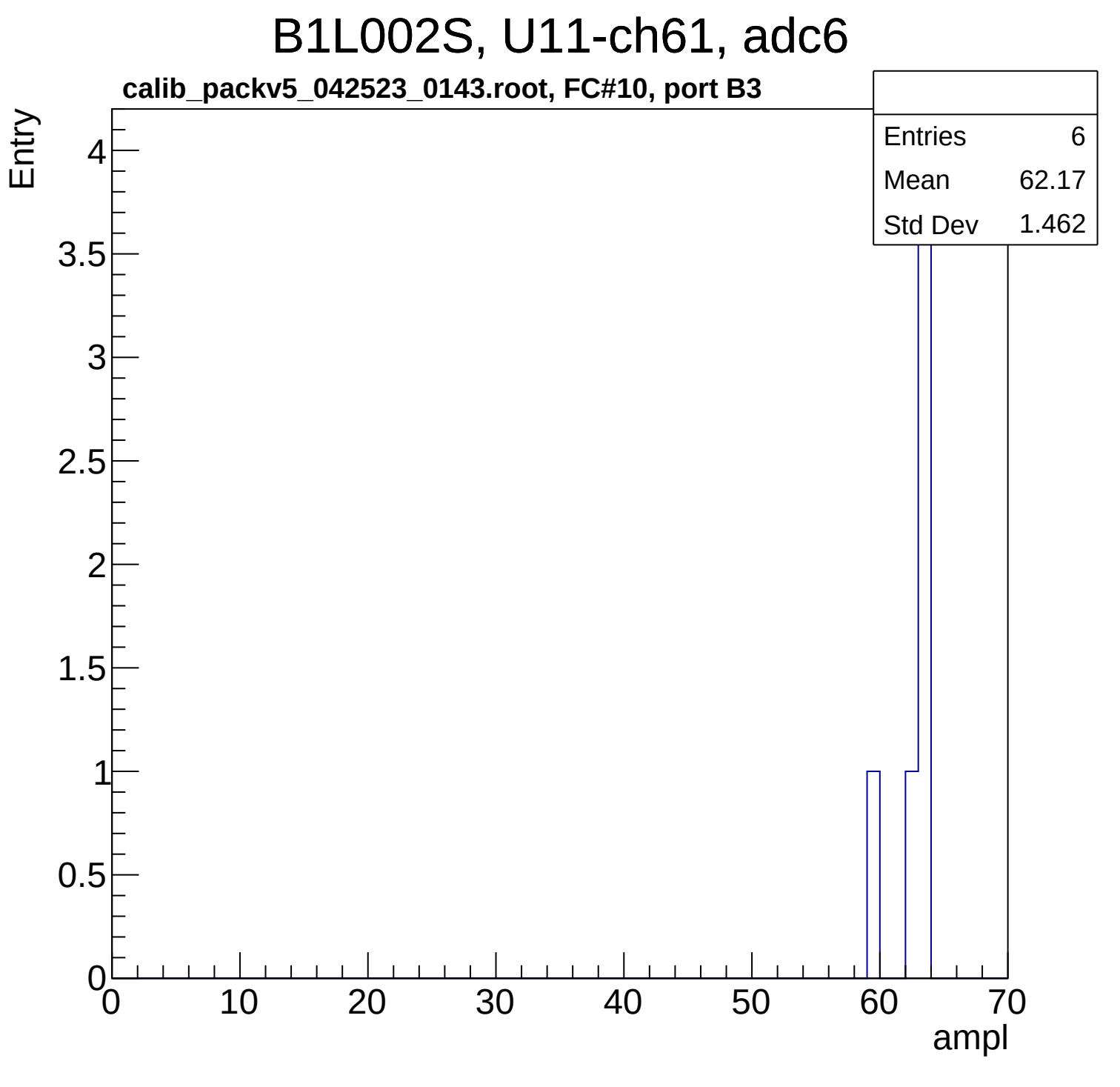
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62.17
Std Dev 1.462
Entry
ampl
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0.5
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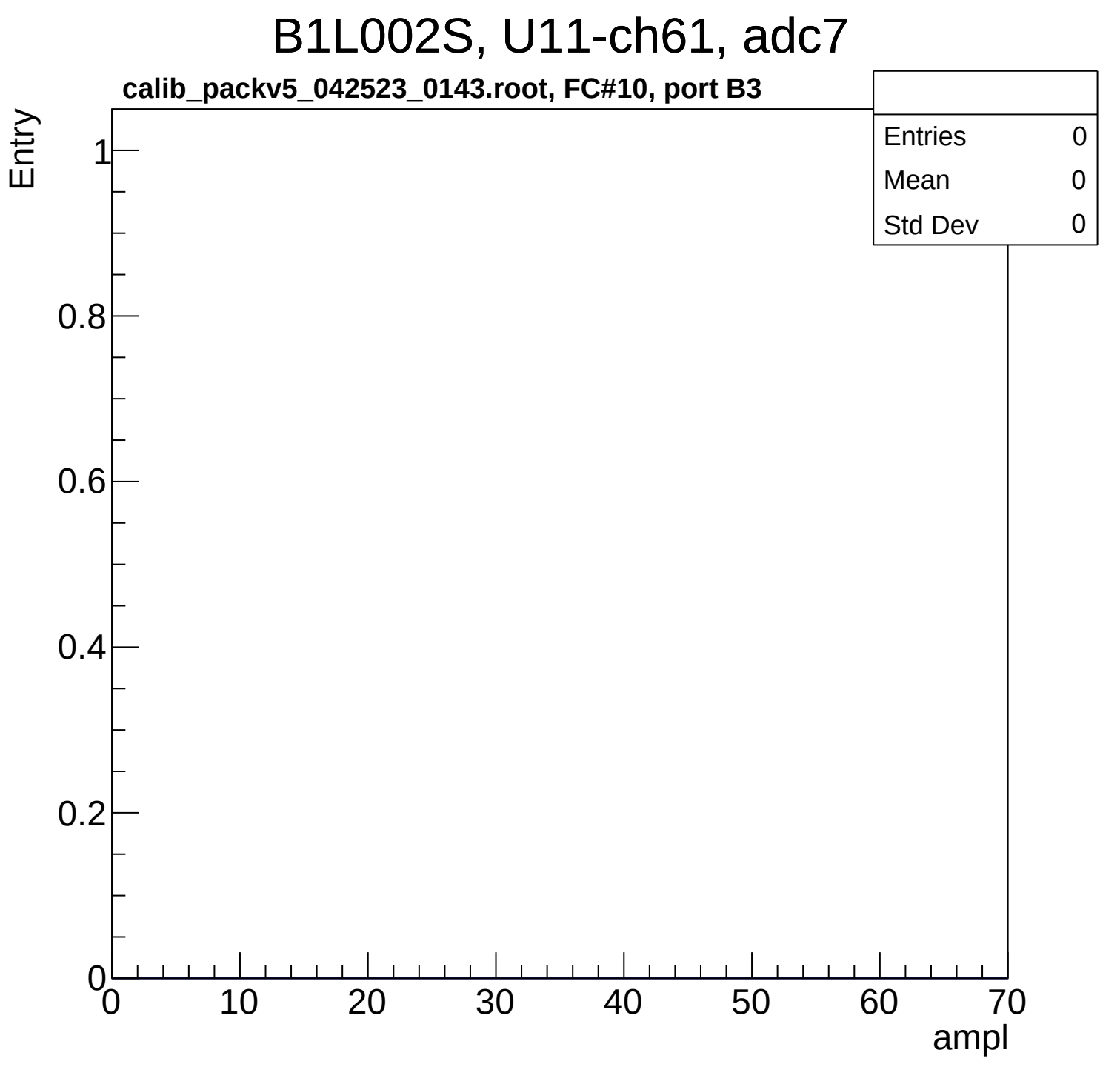
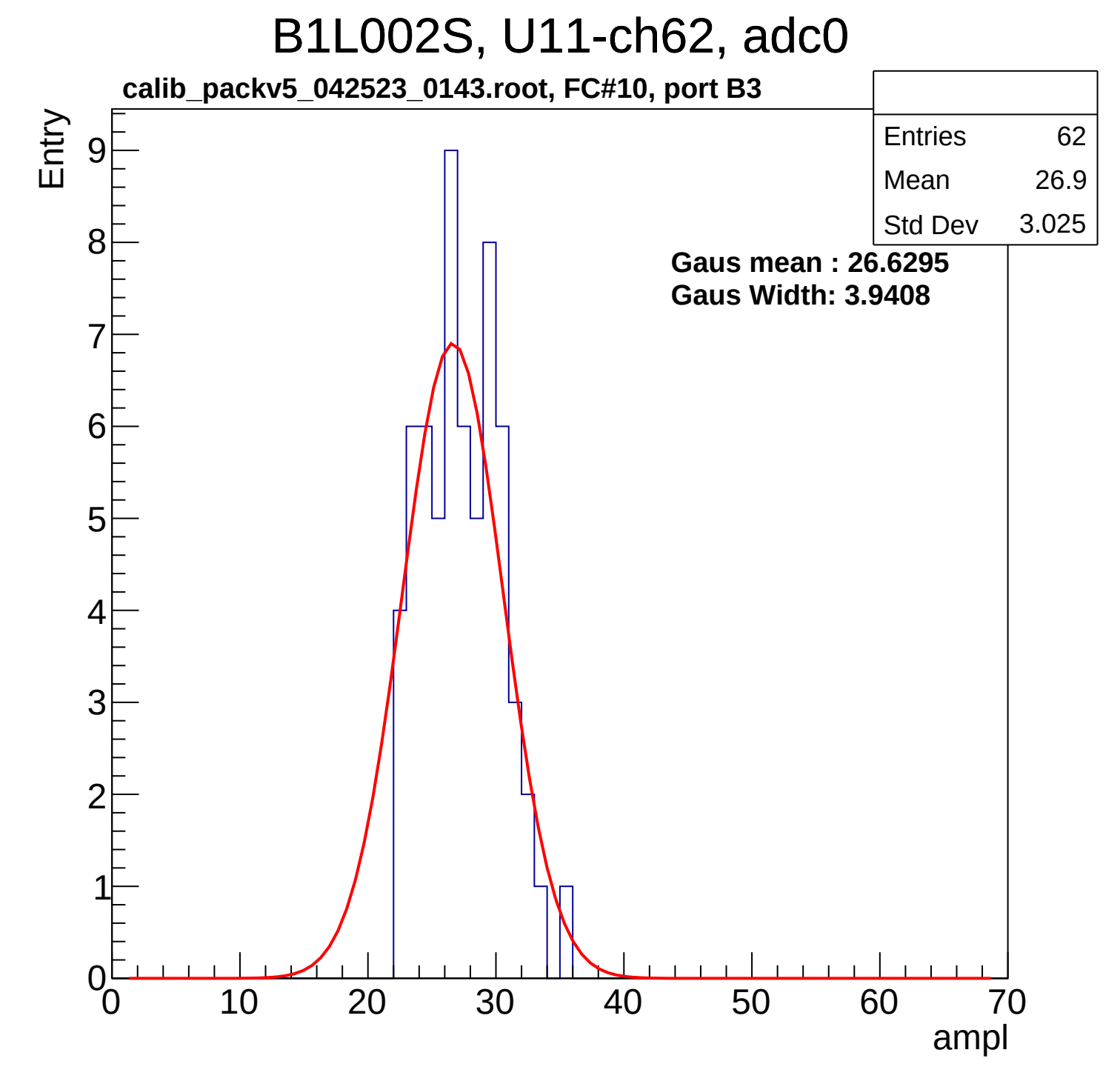
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.9
Std Dev 3.025
Gaus mean : 26.6295
Gaus Width: 3.9408
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
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6
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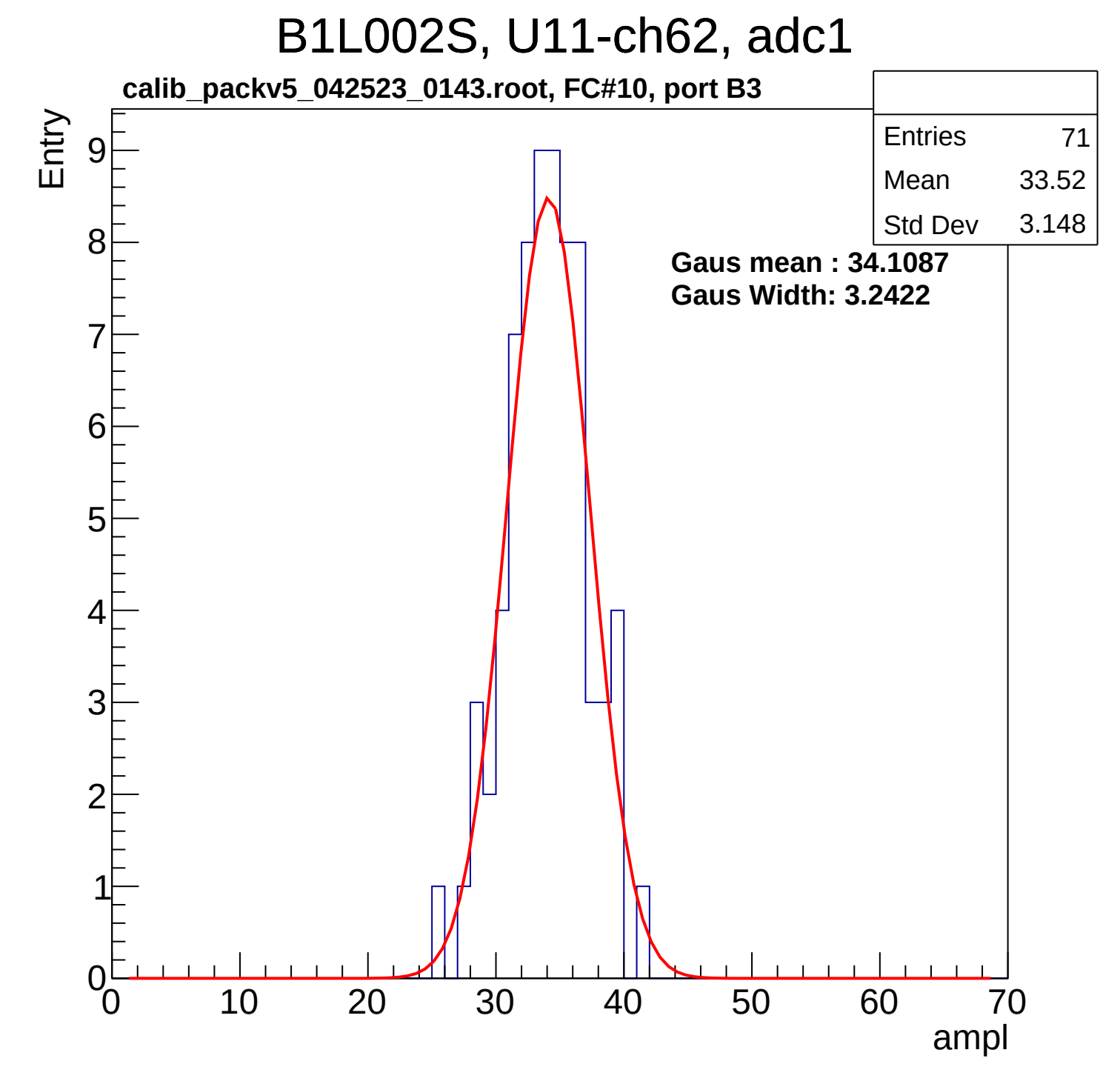
8
9


B1L002S, U11-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.52
Std Dev 3.148
Gaus mean : 34.1087
Gaus Width: 3.2422
Entry
ampl
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3
4
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6
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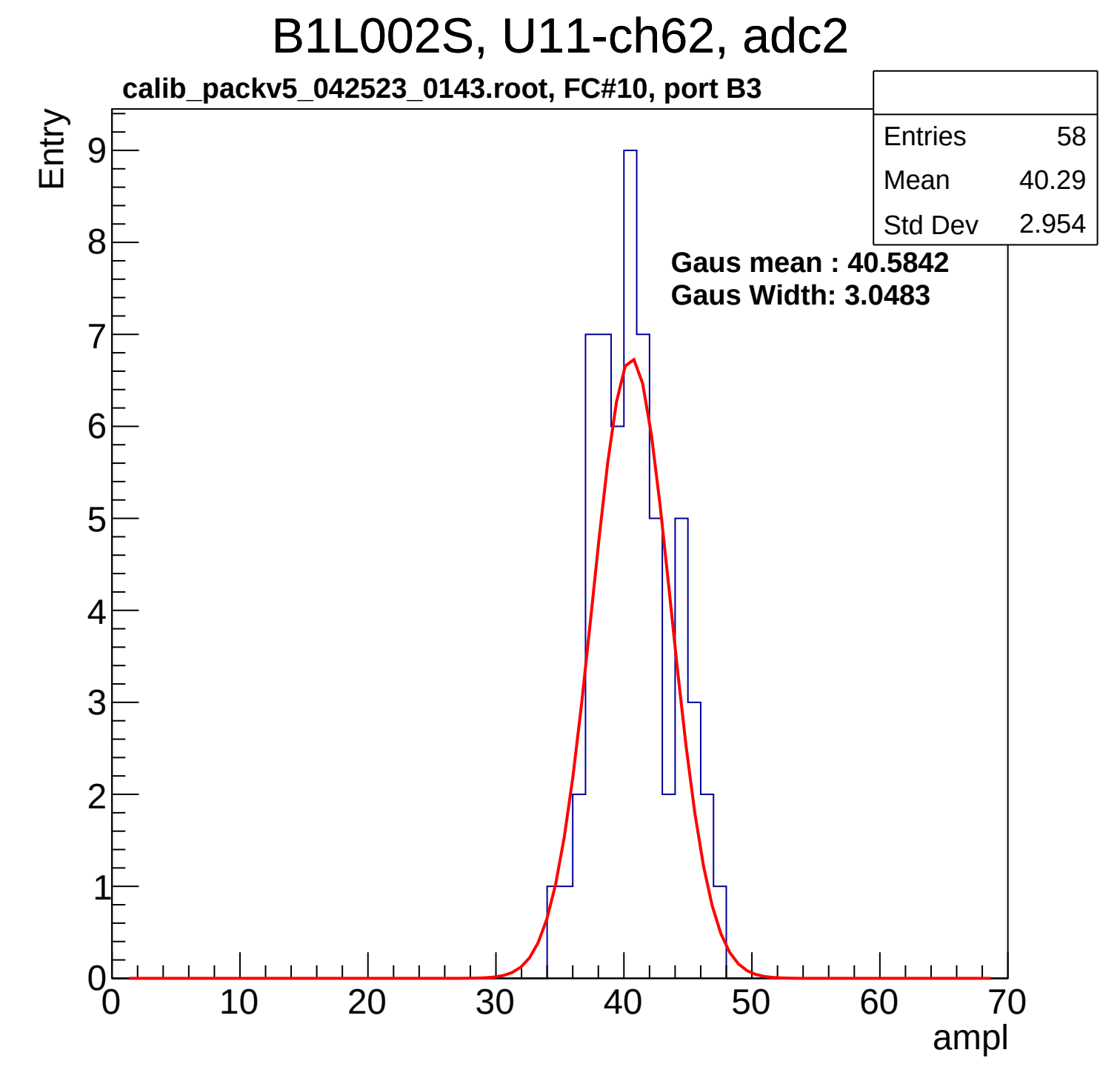
B1L002S, U11-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.29
Std Dev 2.954
Gaus mean : 40.5842
Gaus Width: 3.0483
Entry
ampl
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4
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6
7
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0
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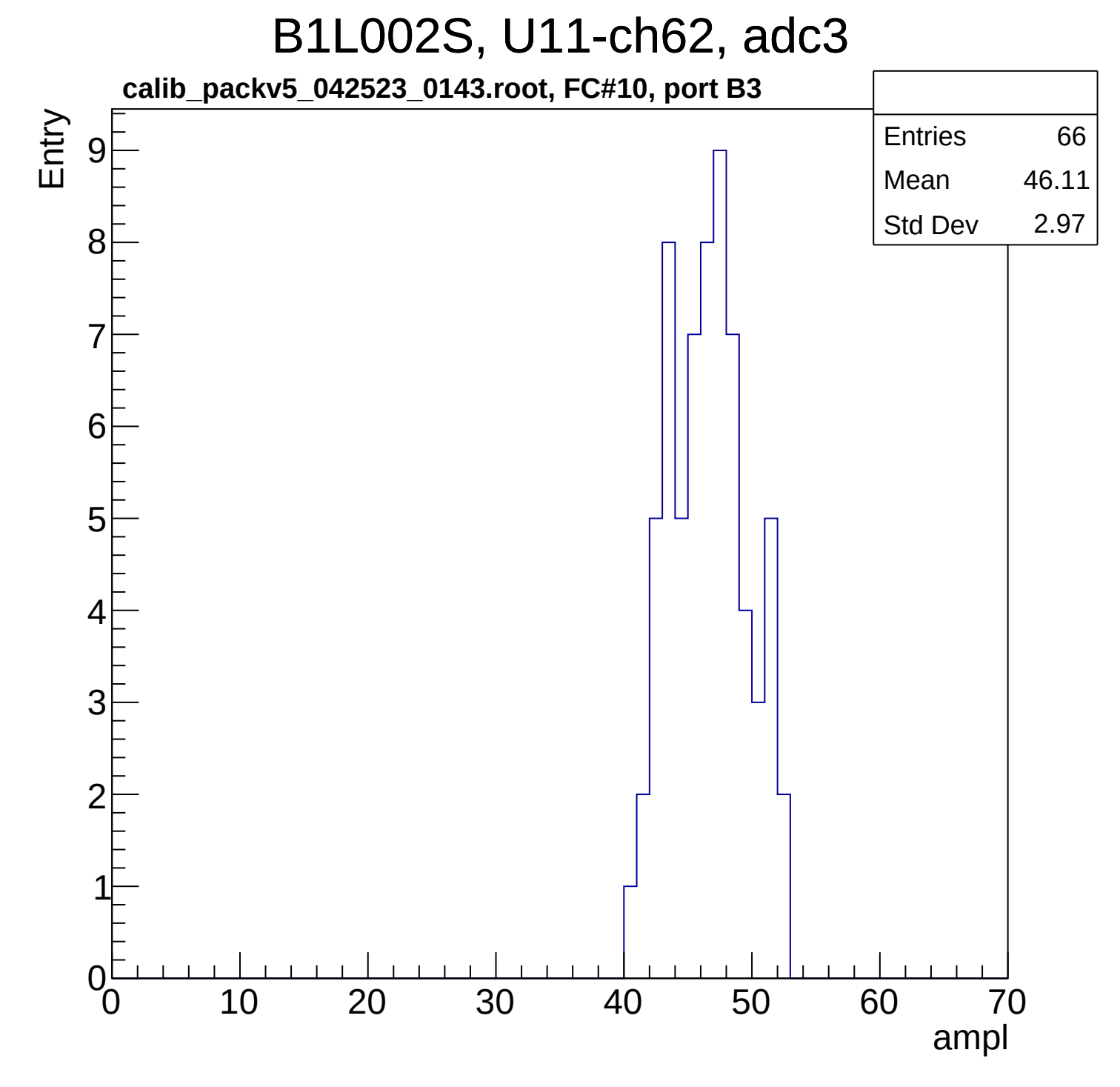
B1L002S, U11-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 46.11
Std Dev 2.97
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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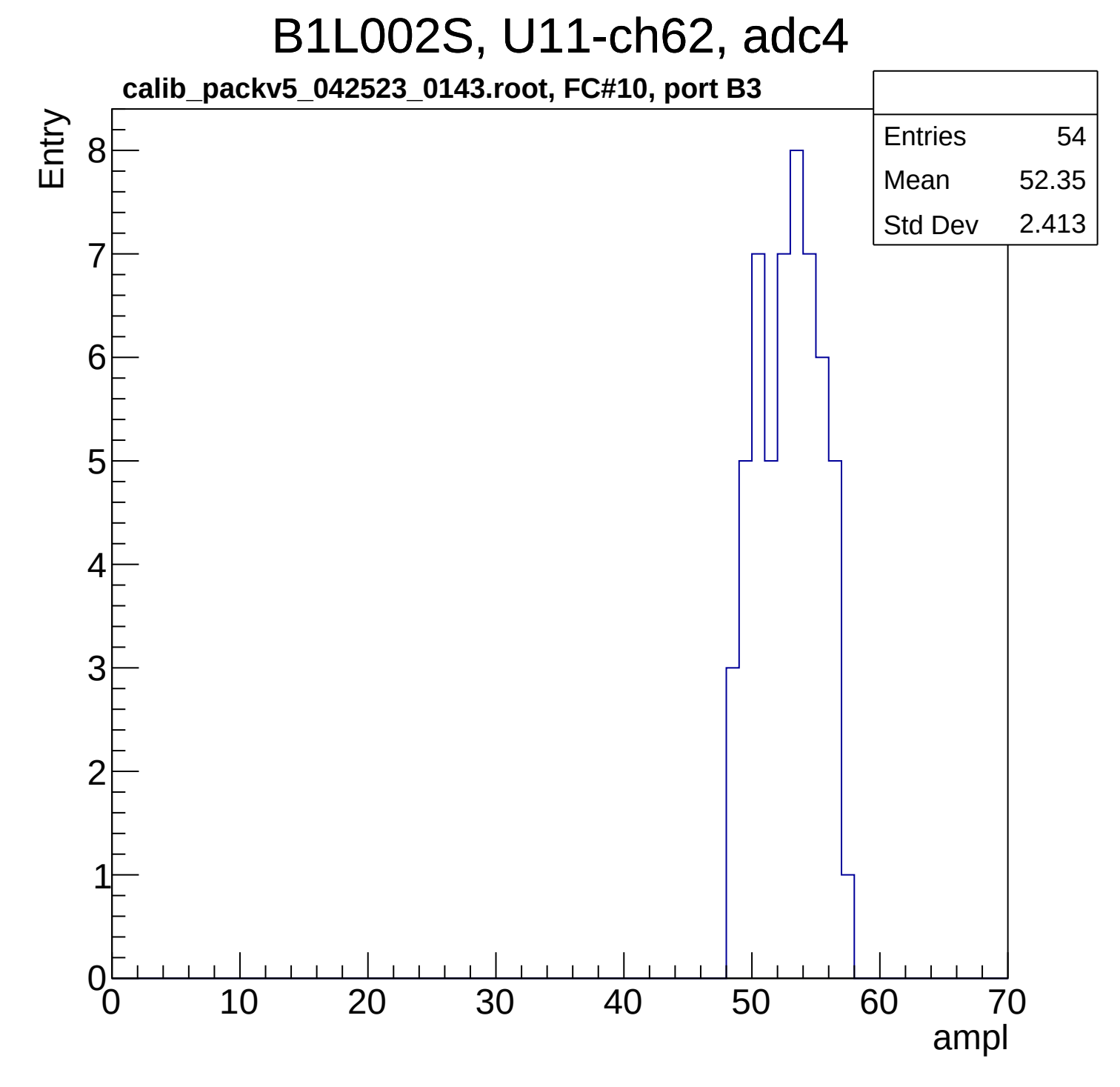
B1L002S, U11-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 52.35
Std Dev 2.413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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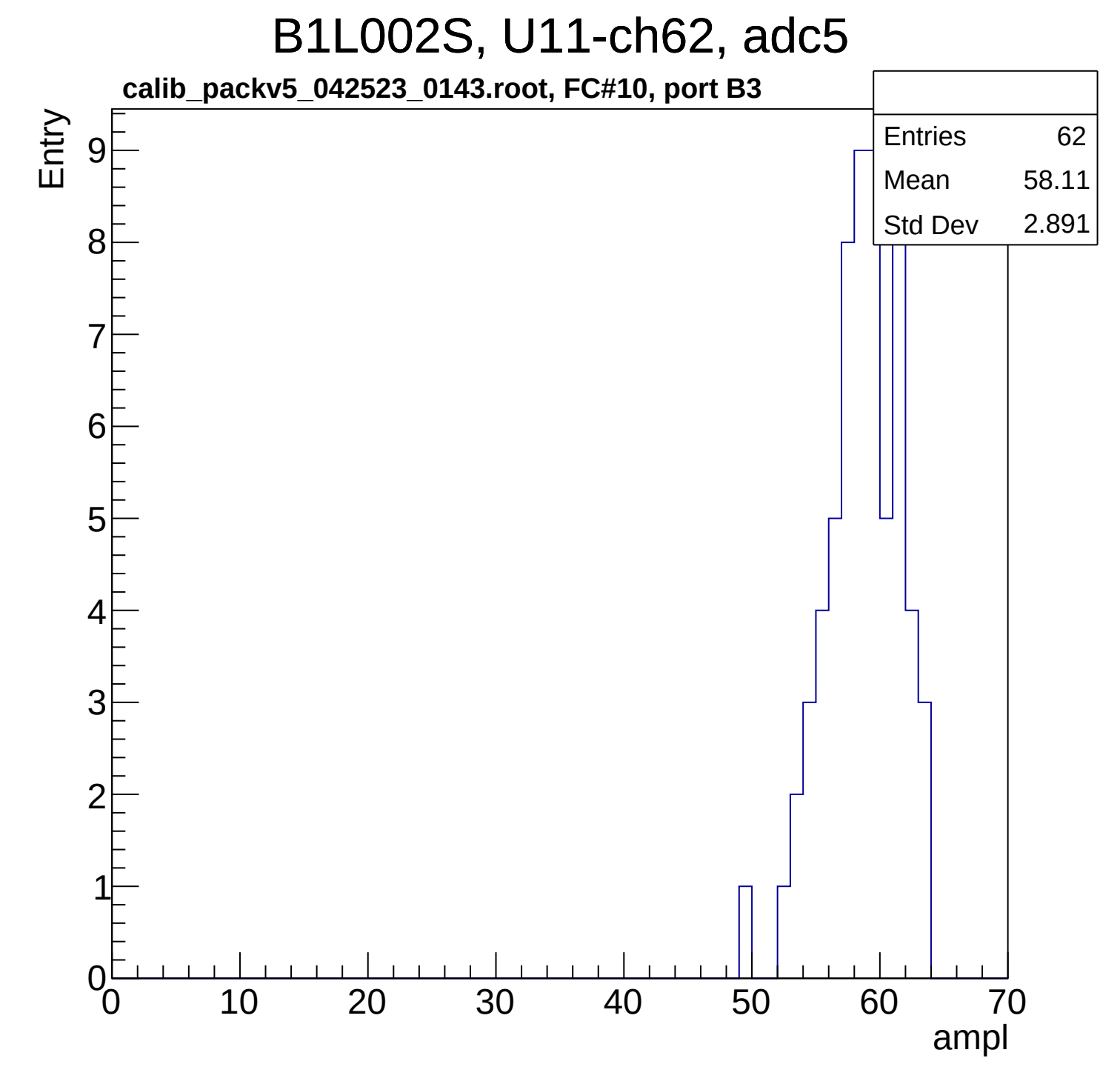
B1L002S, U11-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.11
Std Dev 2.891
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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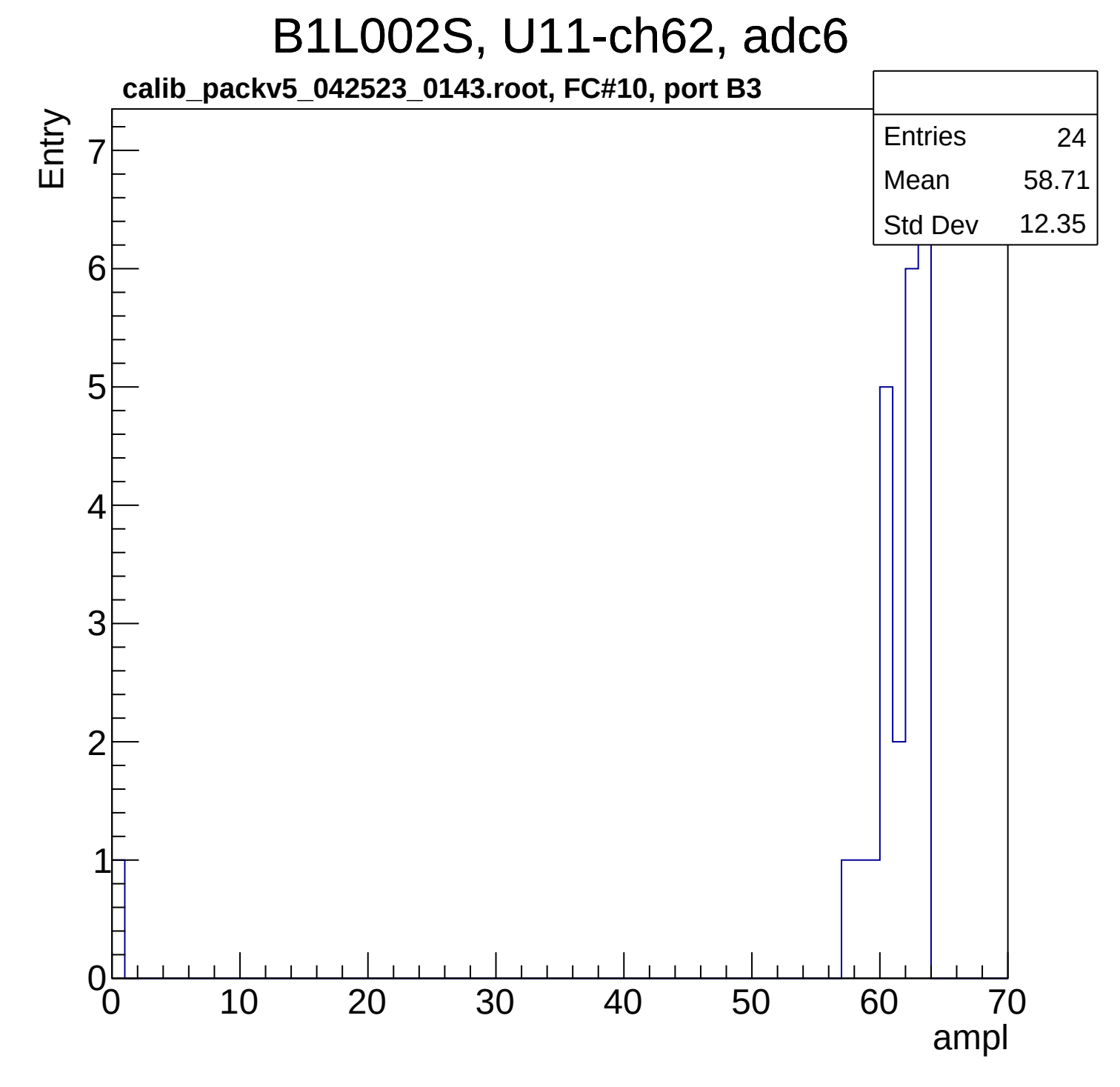
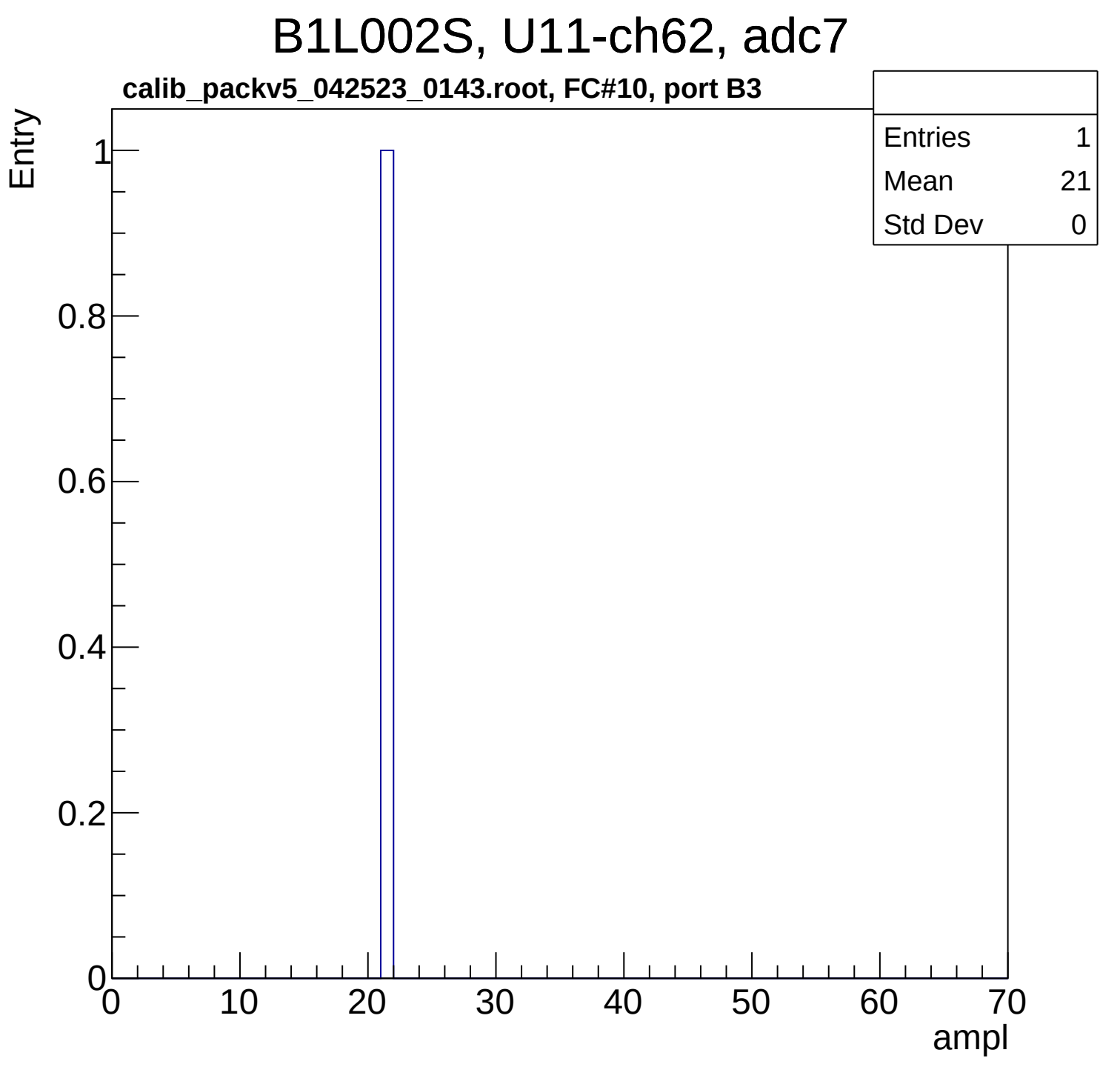
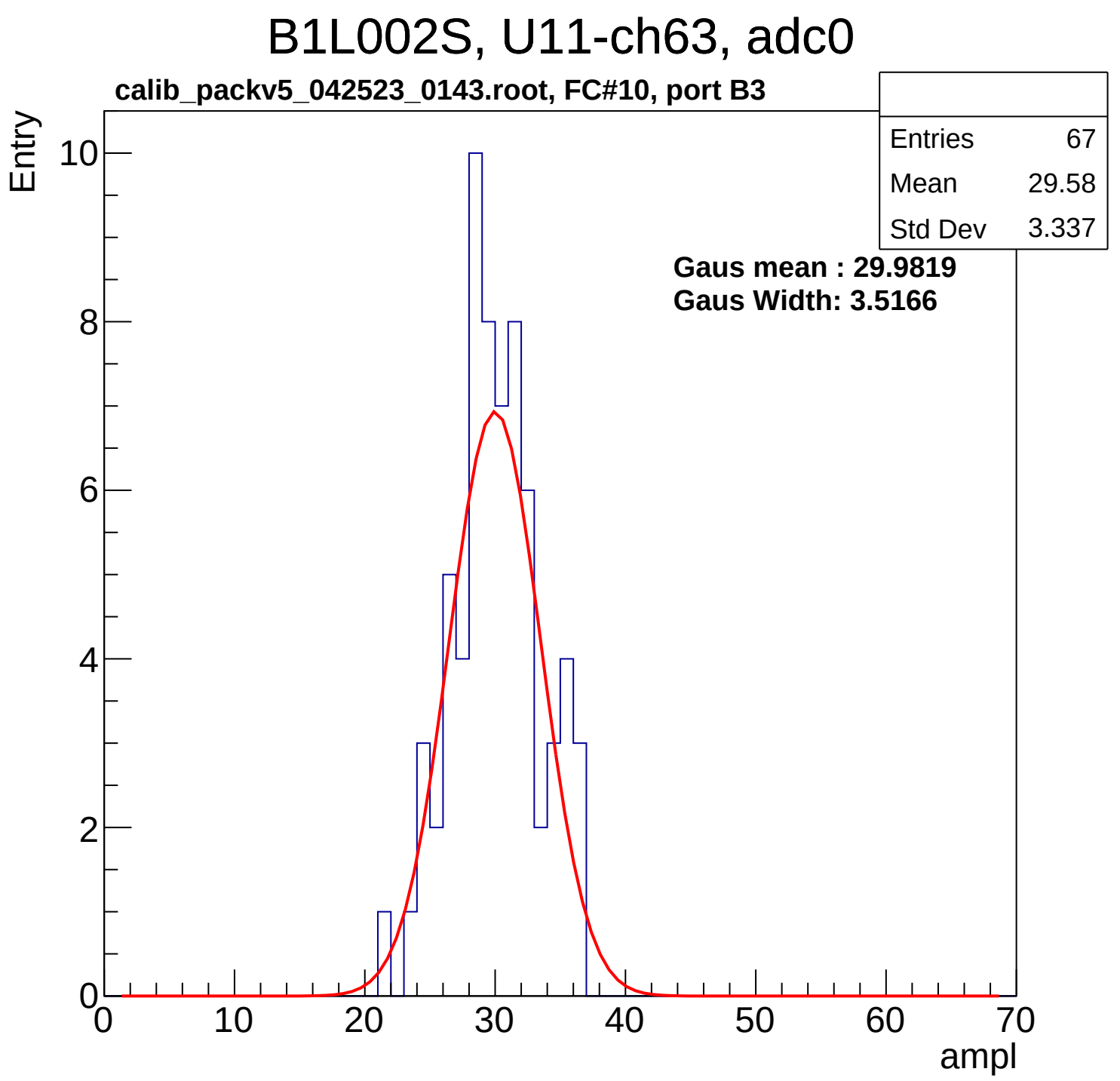
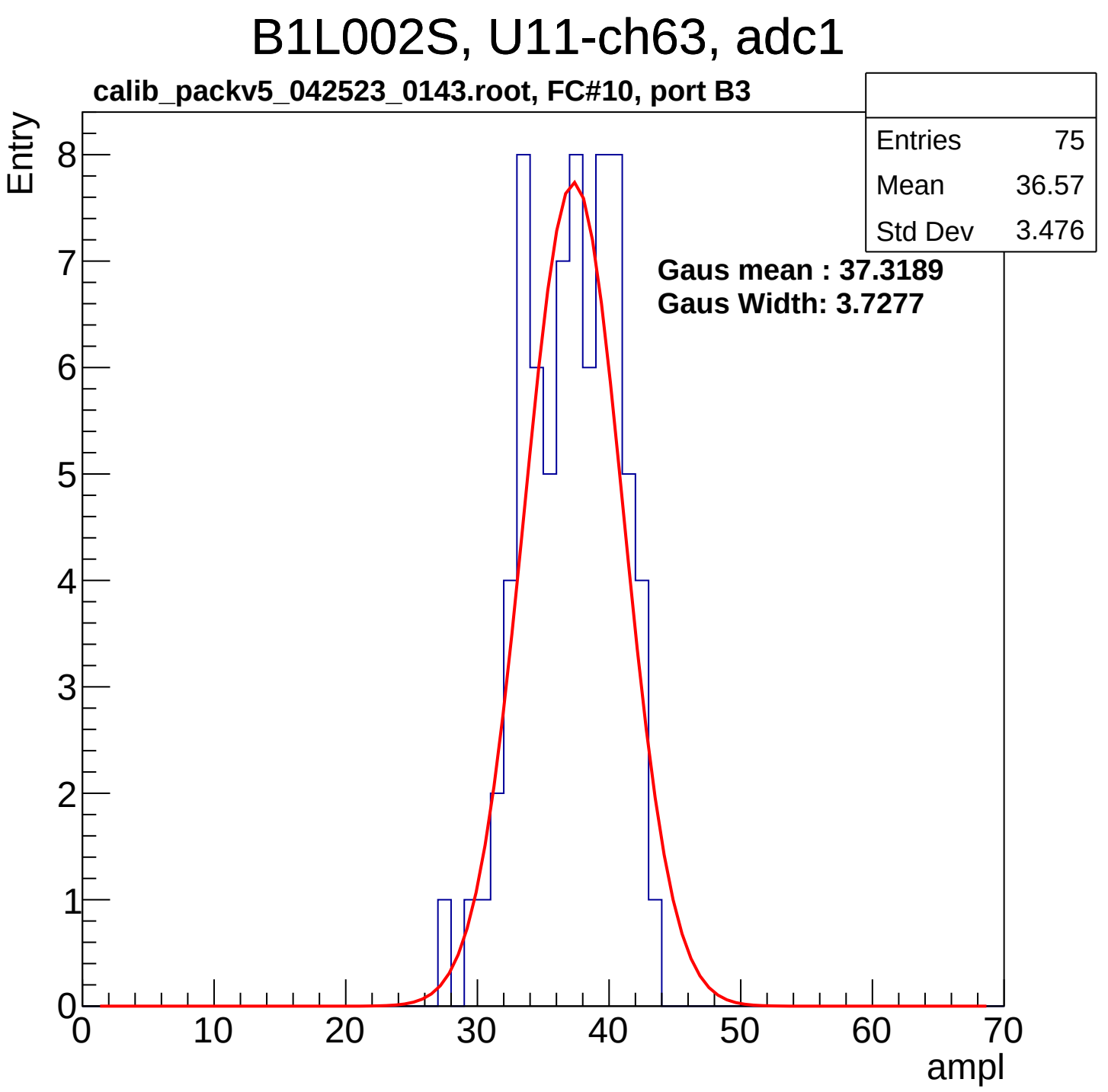

B1L002S, U11-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.71
Std Dev 12.35
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.58
Std Dev 3.337
Gaus mean : 29.9819
Gaus Width: 3.5166
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 36.57
Std Dev 3.476
Gaus mean : 37.3189
Gaus Width: 3.7277
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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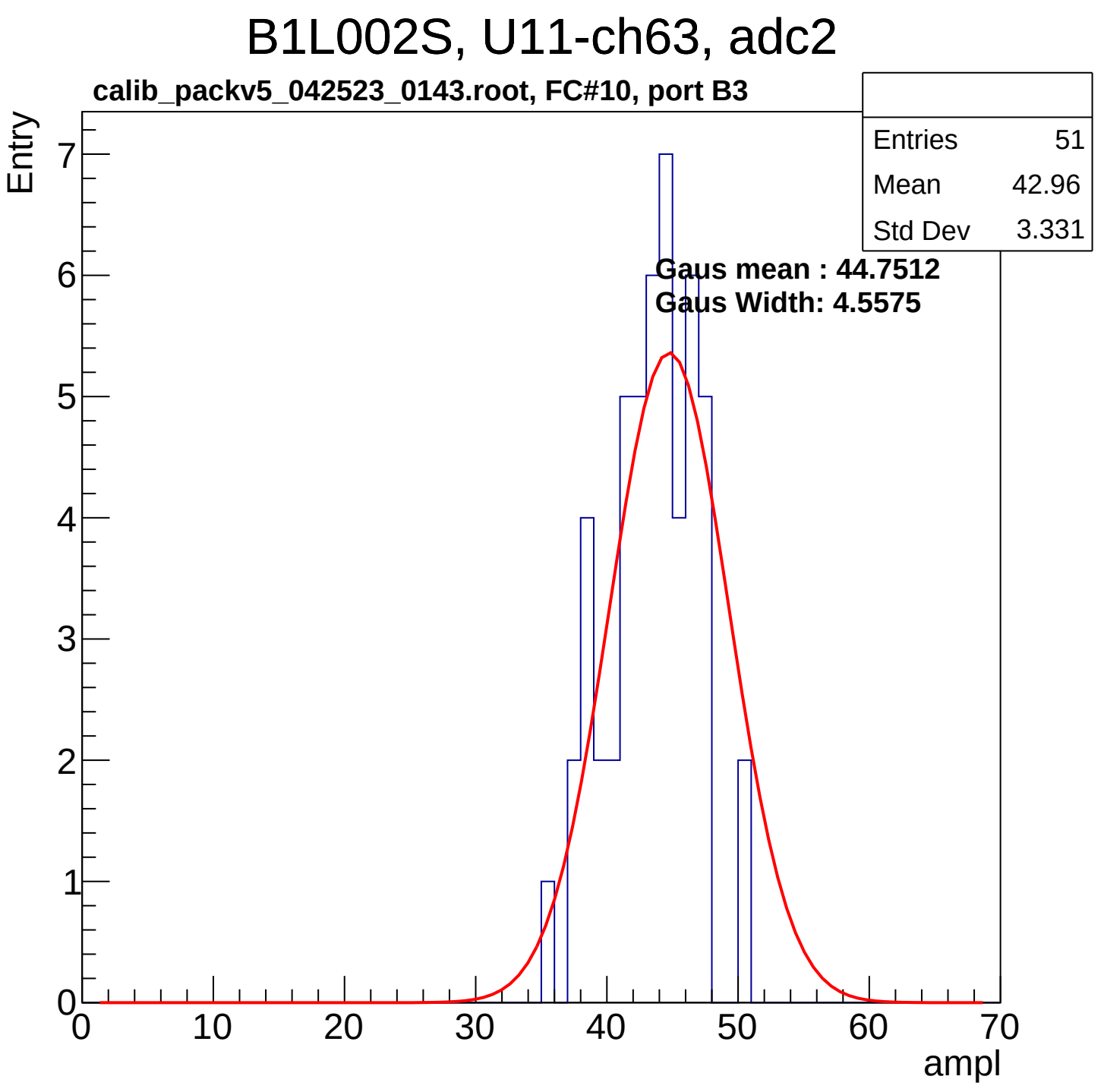
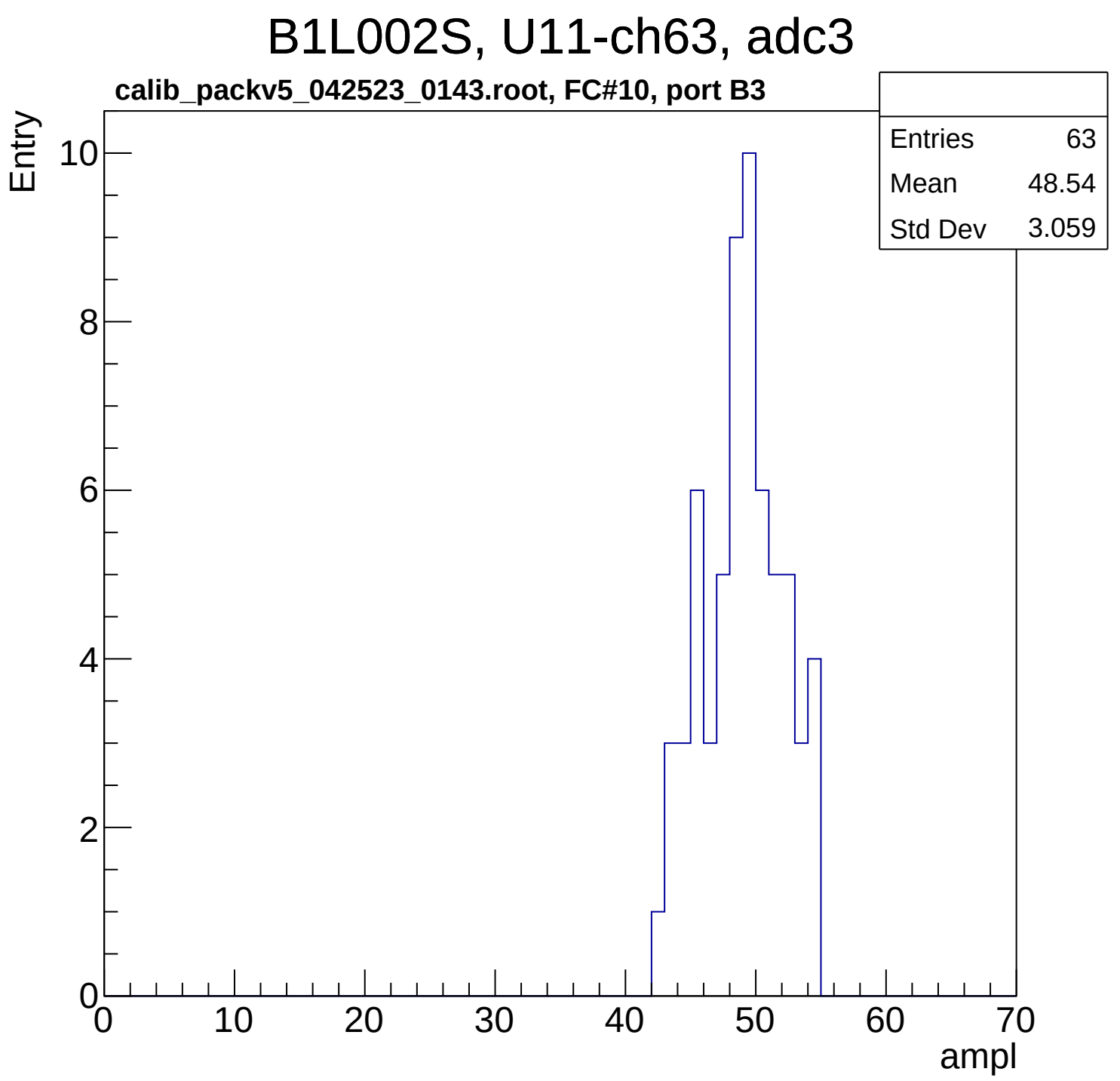
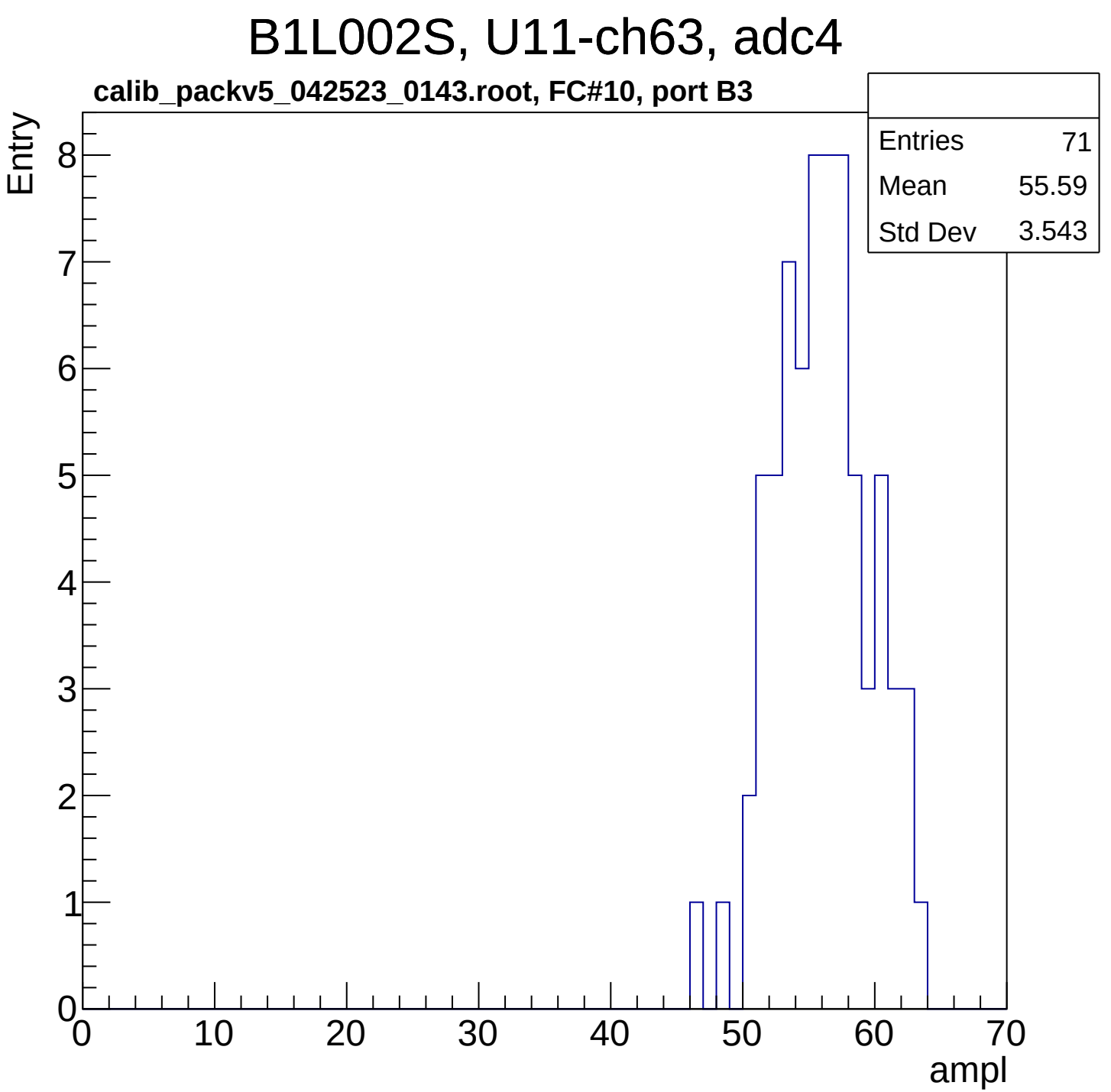
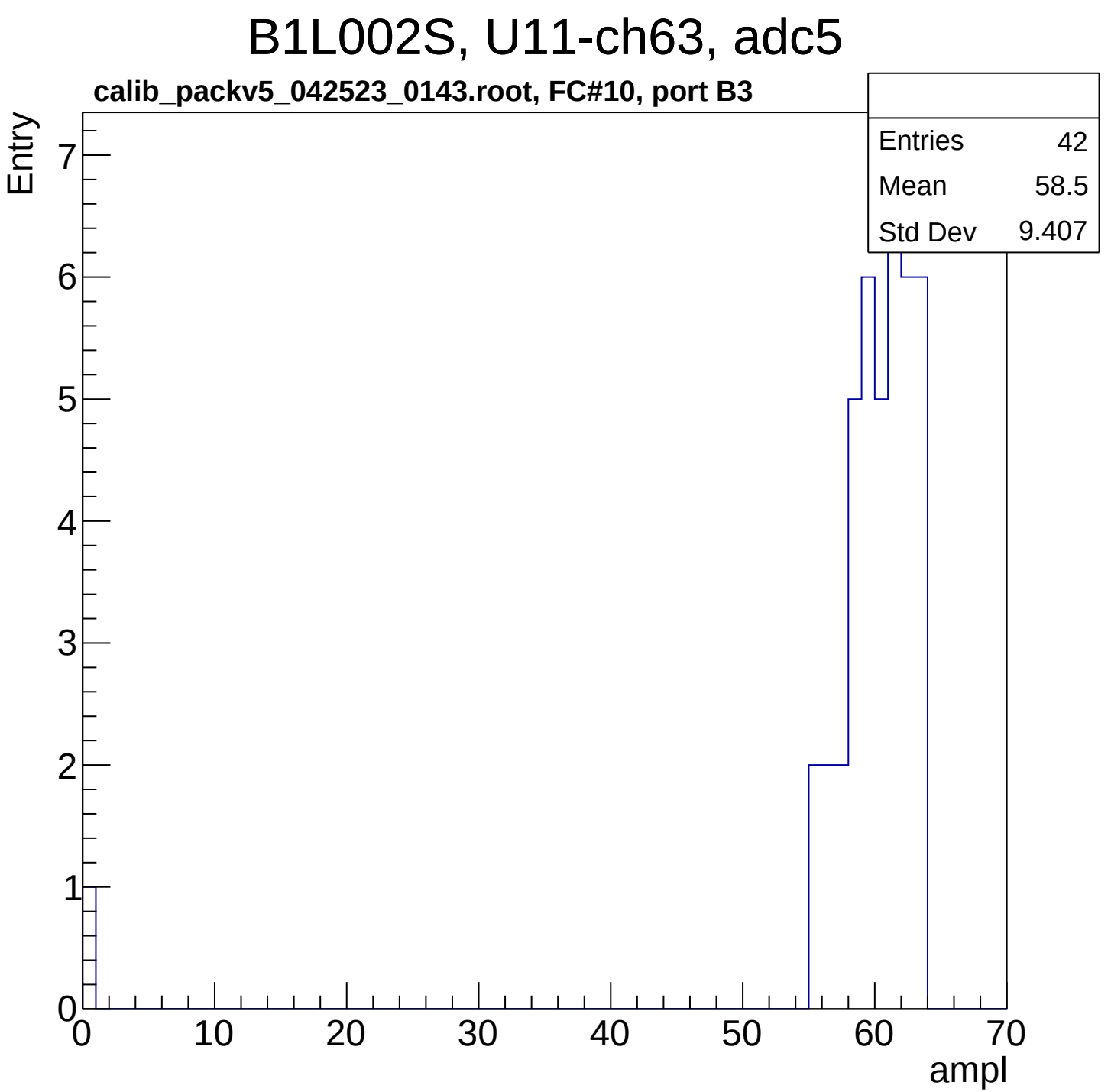
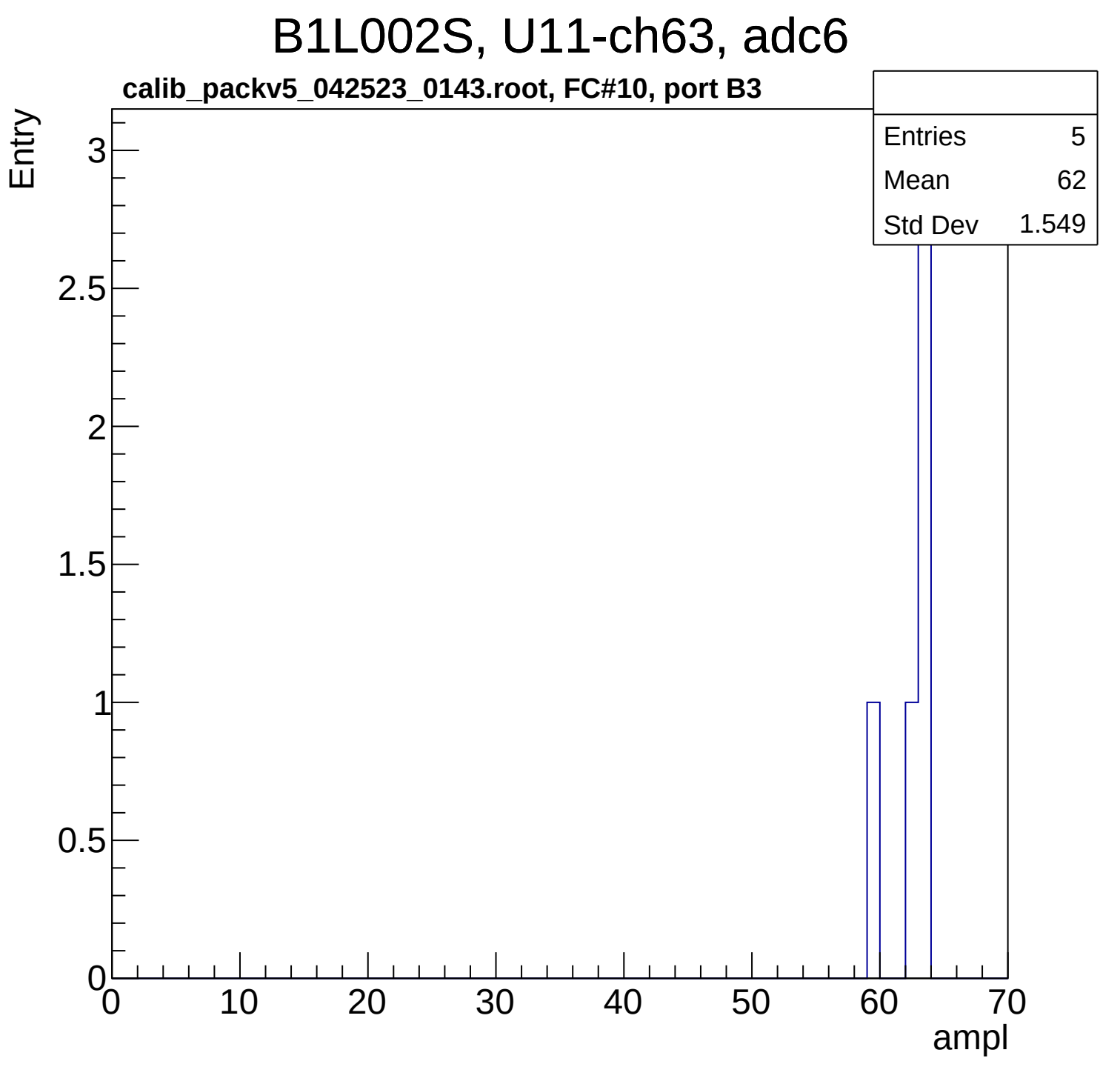
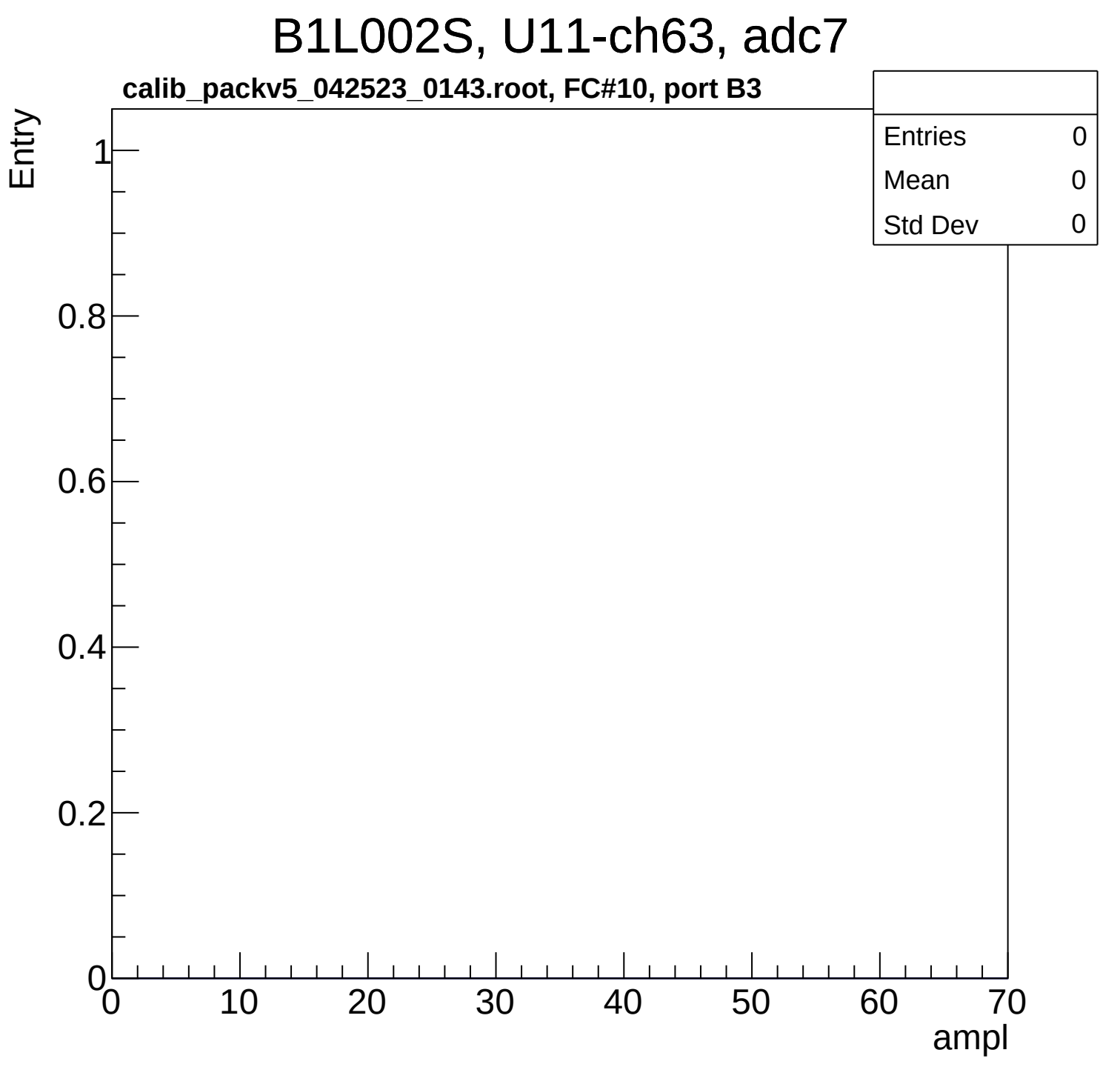

B1L002S, U11-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 42.96
Std Dev 3.331
Gaus mean : 44.7512
Gaus Width: 4.5575
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

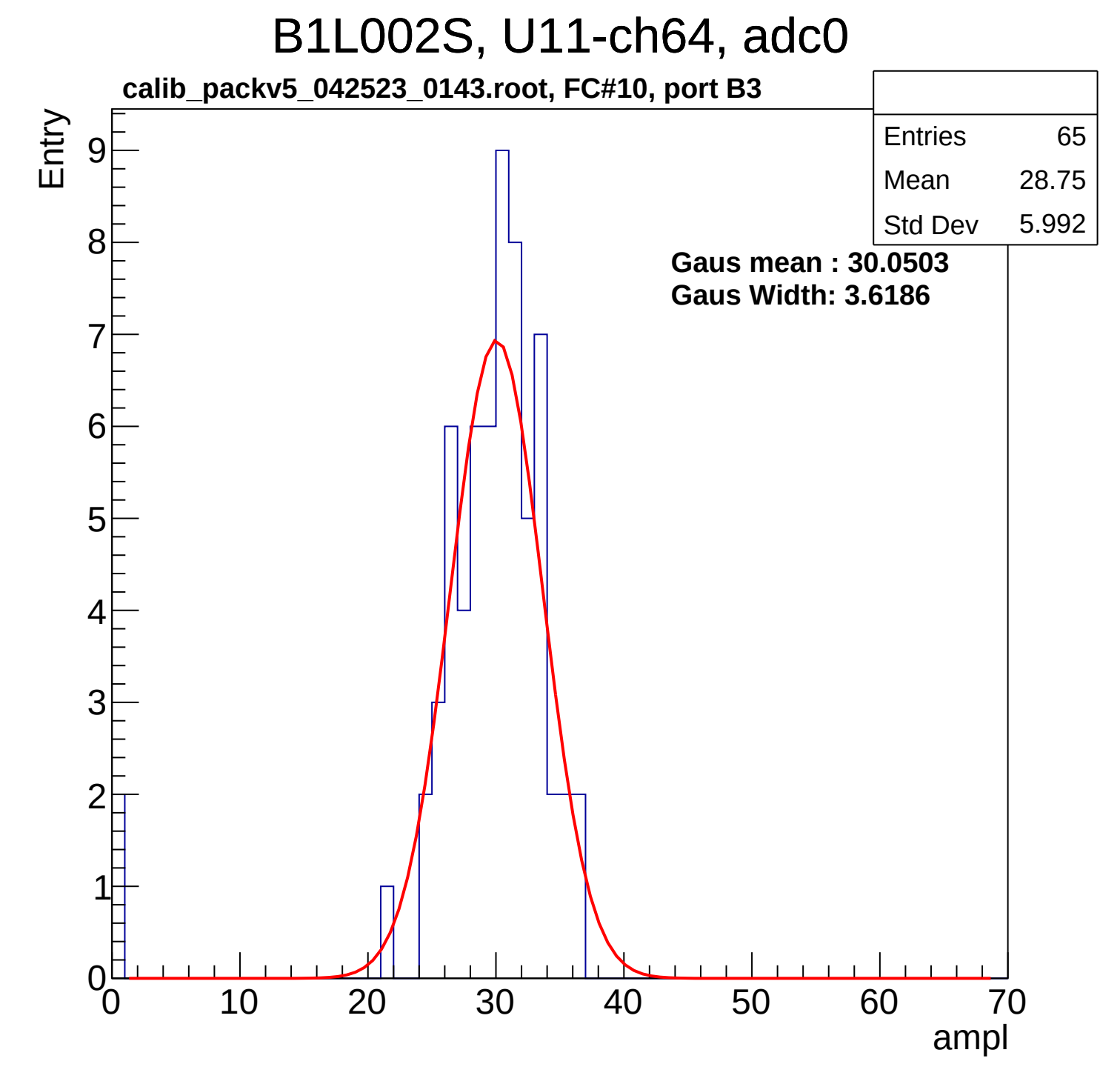

B1L002S, U11-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.54
Std Dev 3.059
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

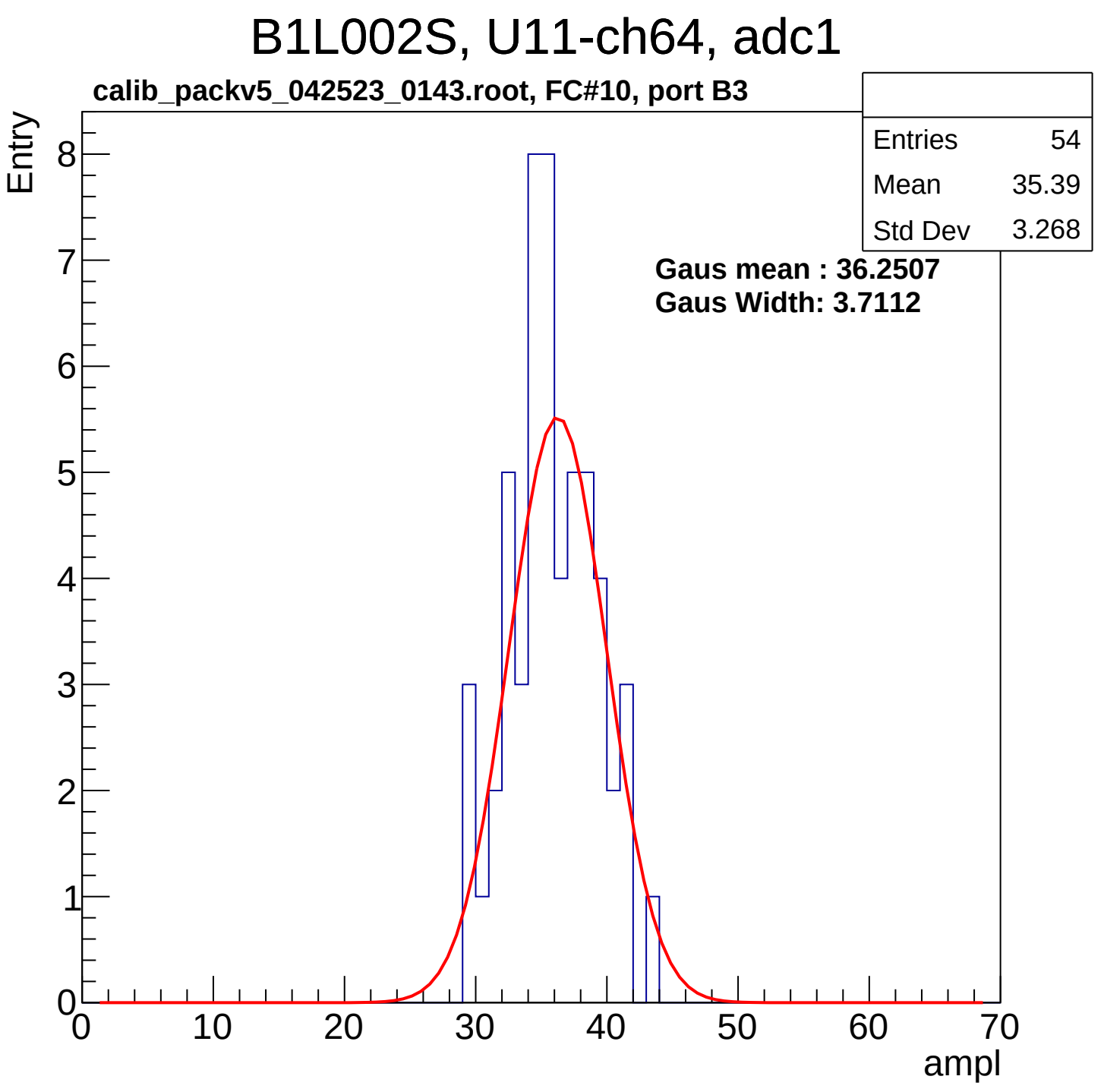

B1L002S, U11-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 55.59
Std Dev 3.543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

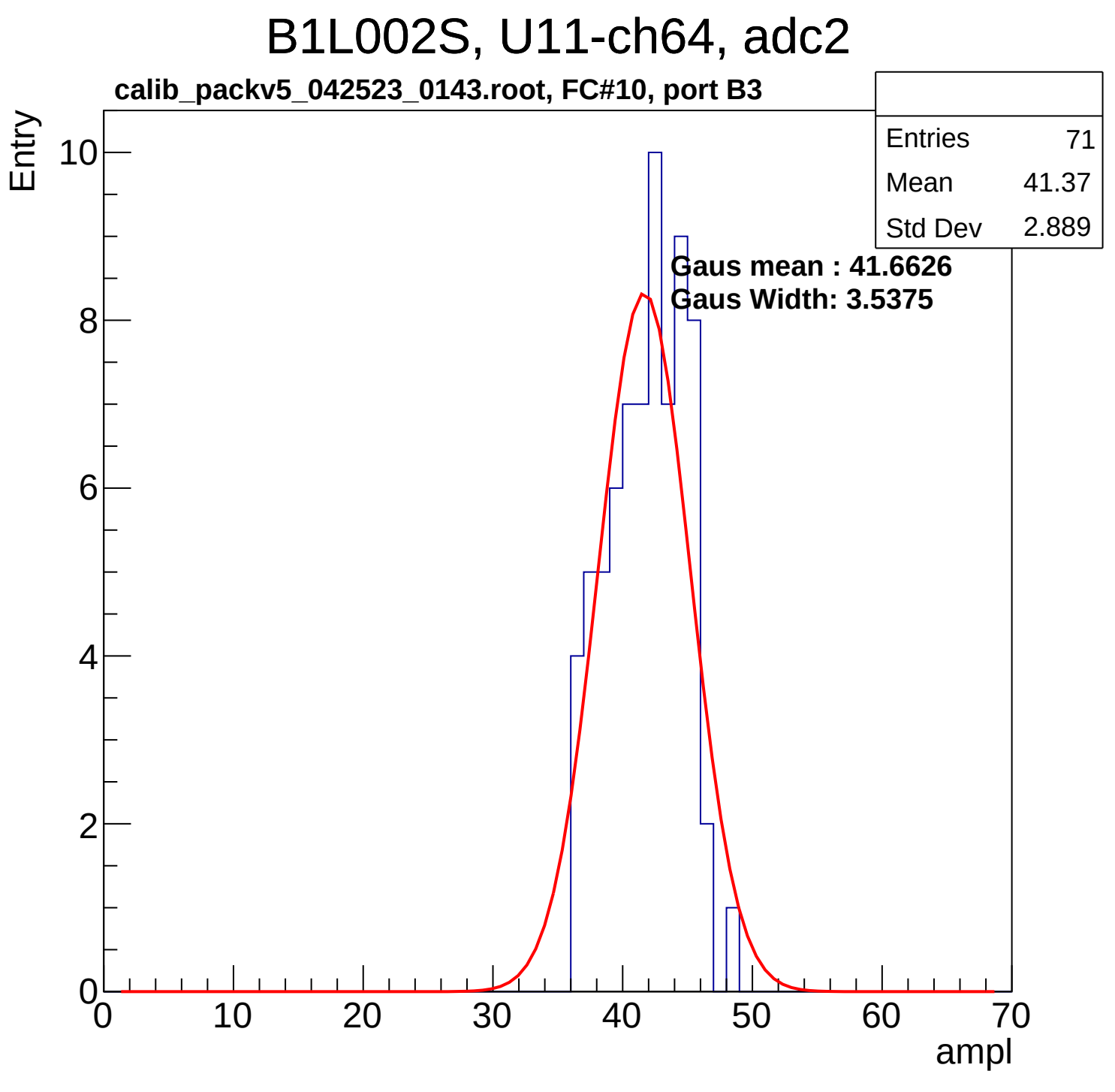

B1L002S, U11-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 58.5
Std Dev 9.407
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 62
Std Dev 1.549
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.75
Std Dev 5.992
Gaus mean : 30.0503
Gaus Width: 3.6186
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 35.39
Std Dev 3.268
Gaus mean : 36.2507
Gaus Width: 3.7112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 41.37
Std Dev 2.889
Gaus mean : 41.6626
Gaus Width: 3.5375
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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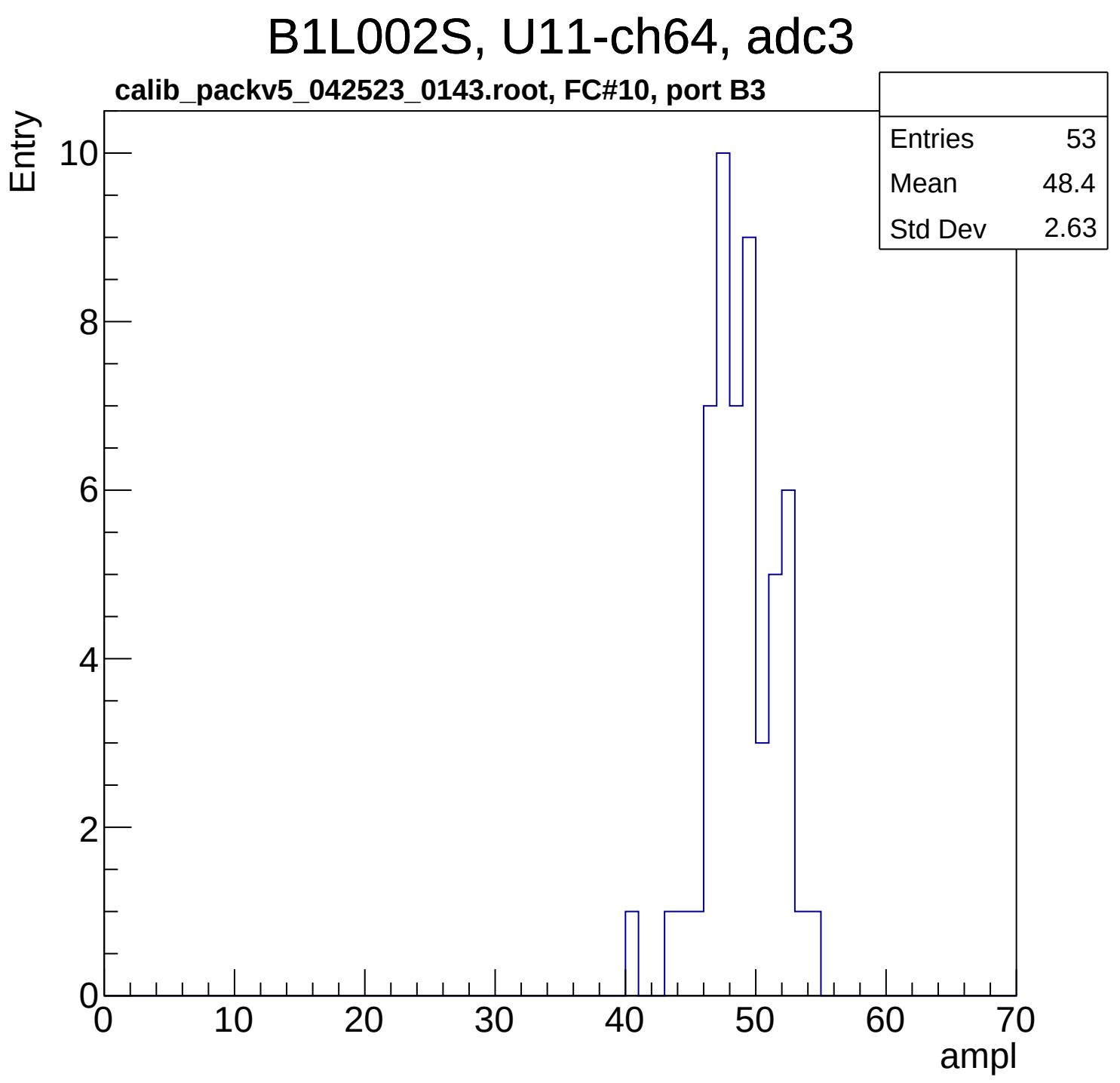
B1L002S, U11-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 48.4
Std Dev 2.63
Entry
0
2
4
6
8
10
0
10
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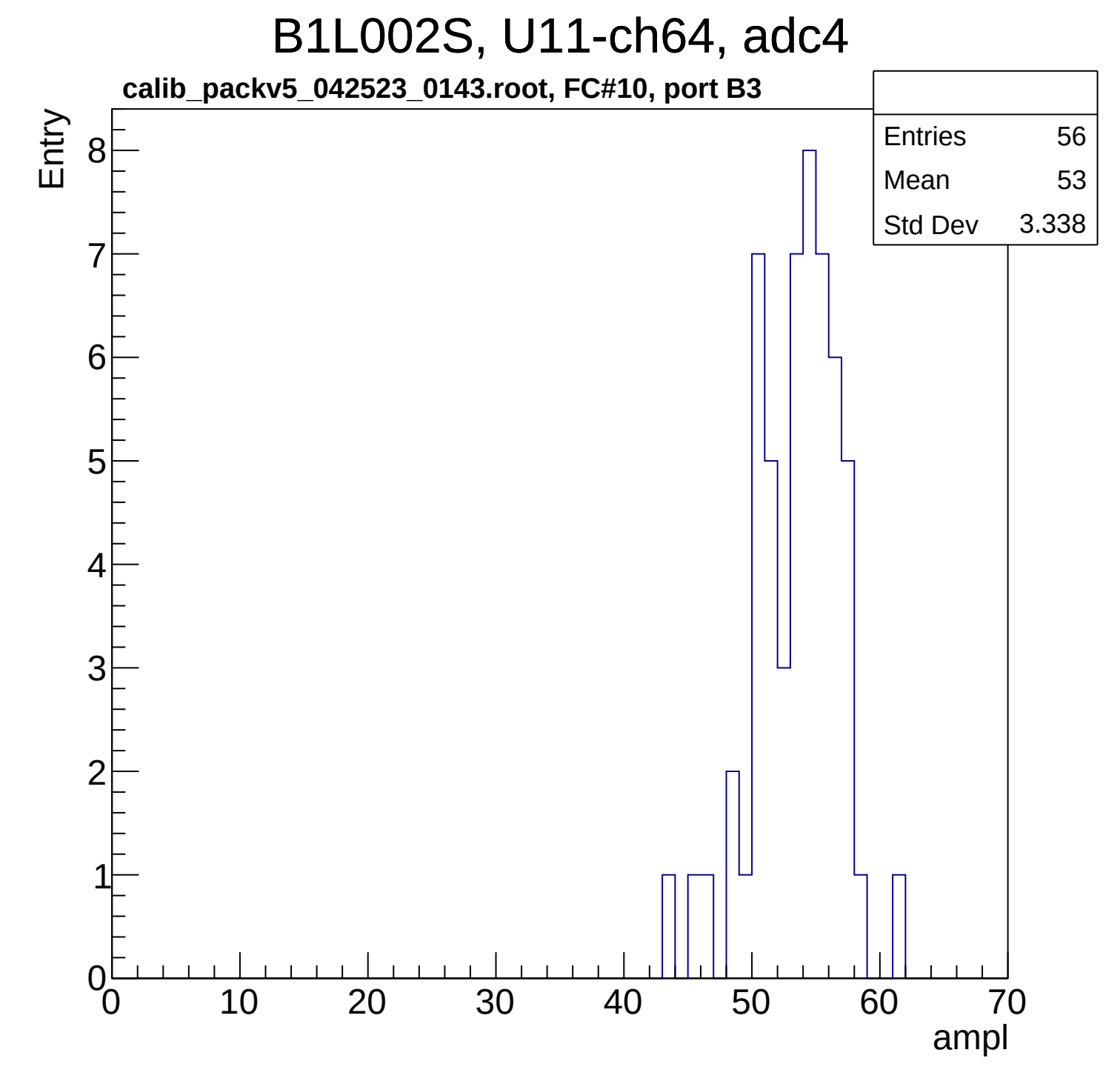
30
40
50
60
70
ampl


B1L002S, U11-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53
Std Dev 3.338
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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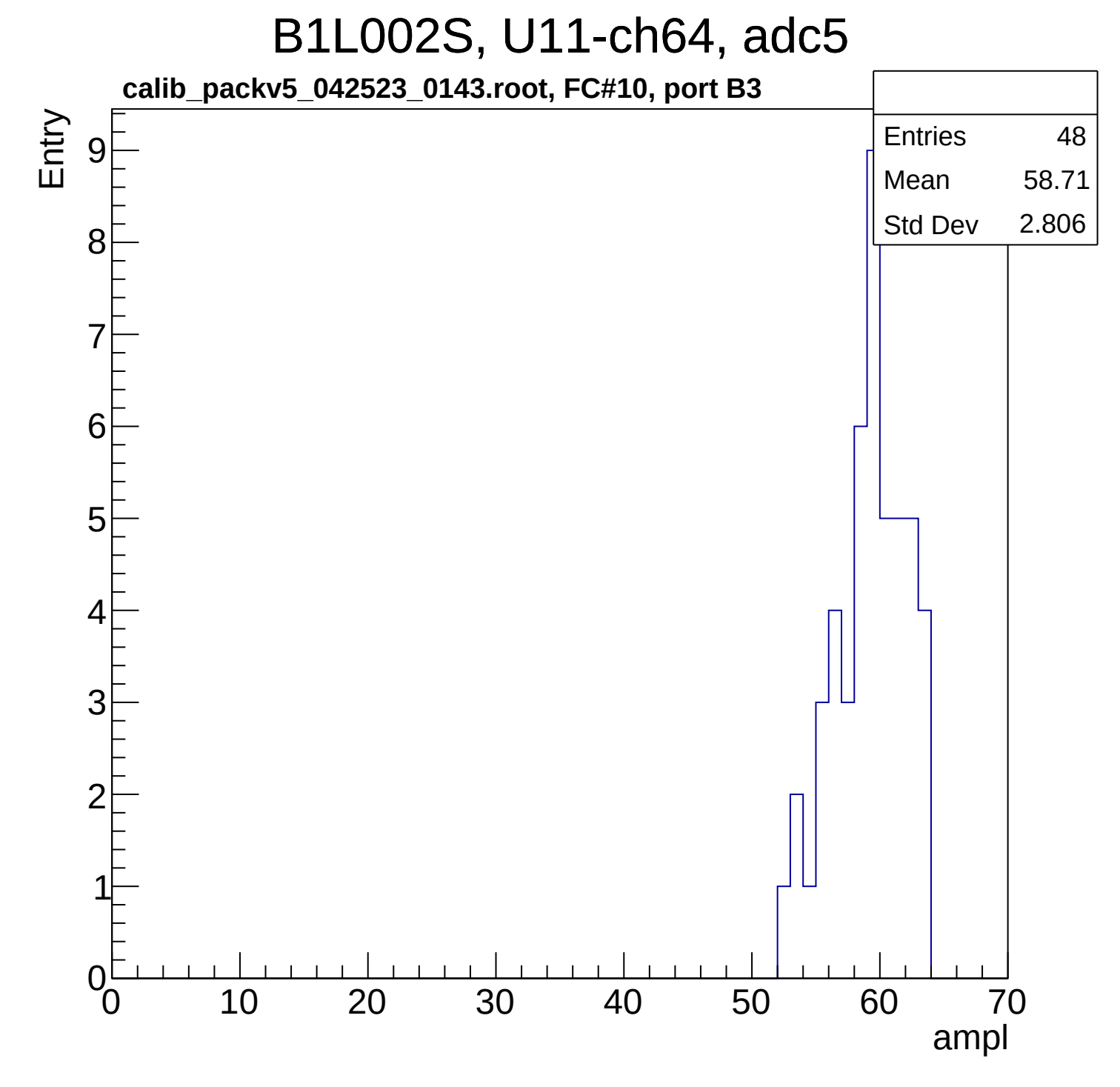
B1L002S, U11-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.71
Std Dev 2.806
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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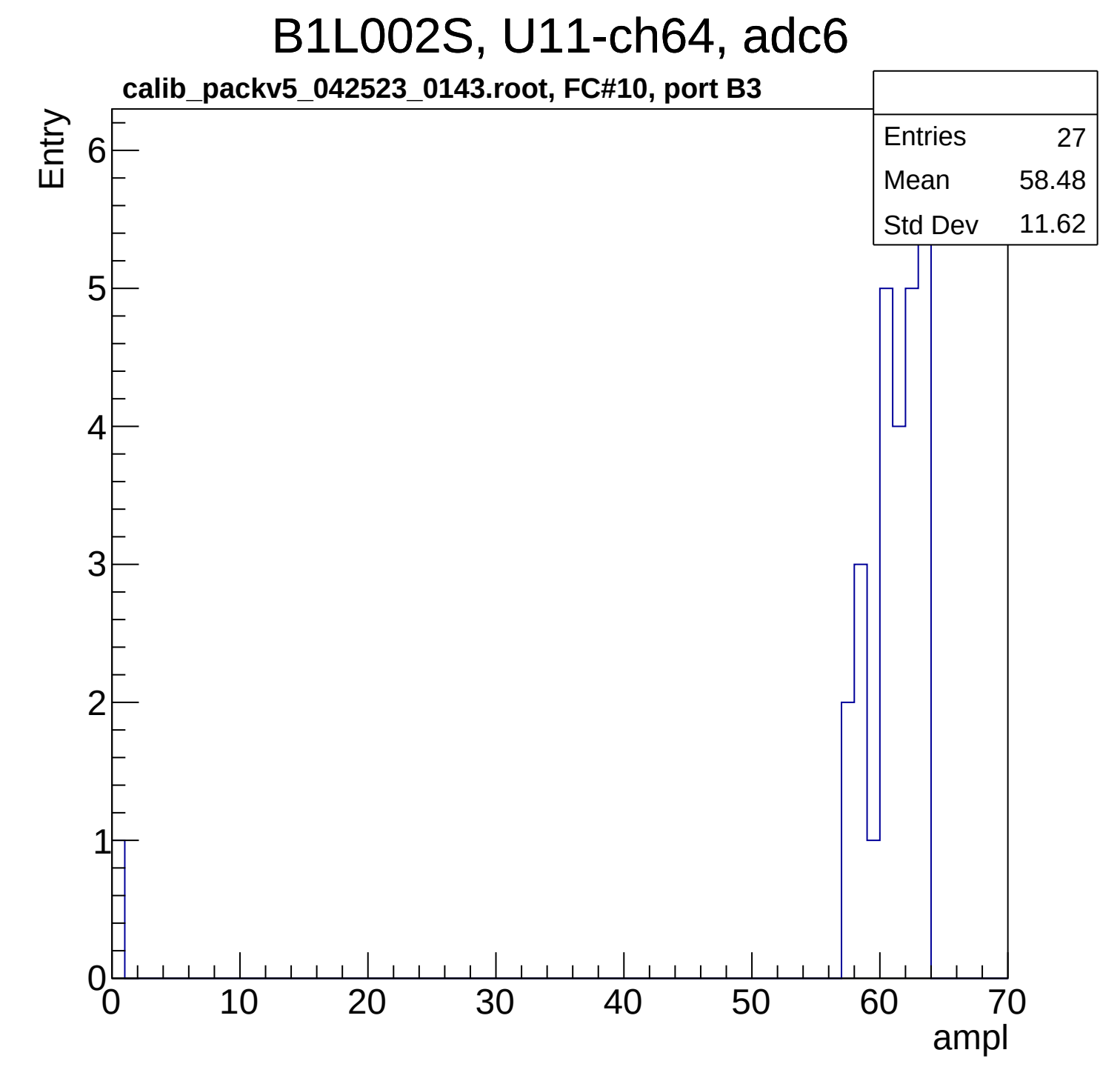
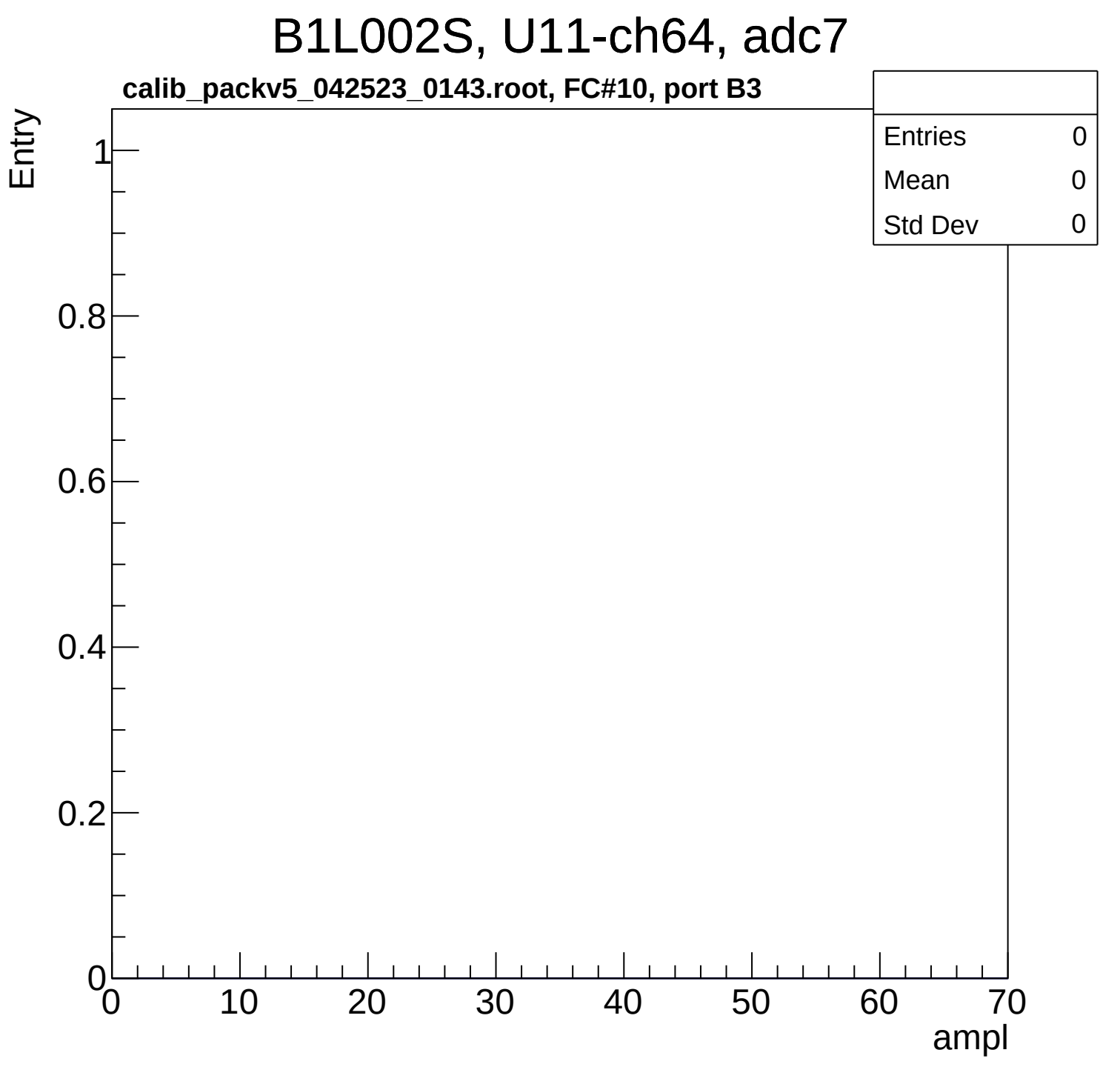
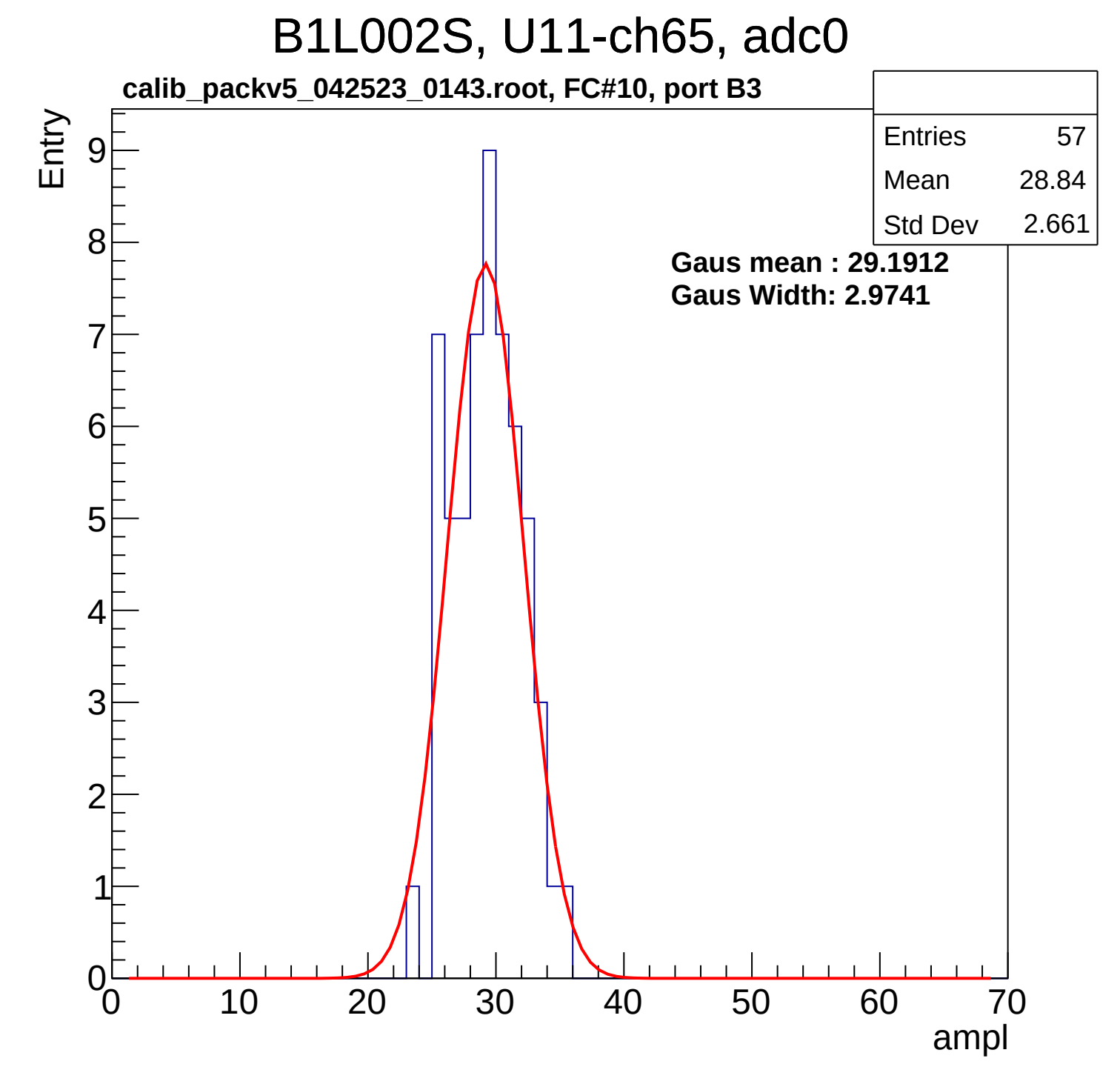

B1L002S, U11-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.48
Std Dev 11.62
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.84
Std Dev 2.661
Gaus mean : 29.1912
Gaus Width: 2.9741
Entry
ampl
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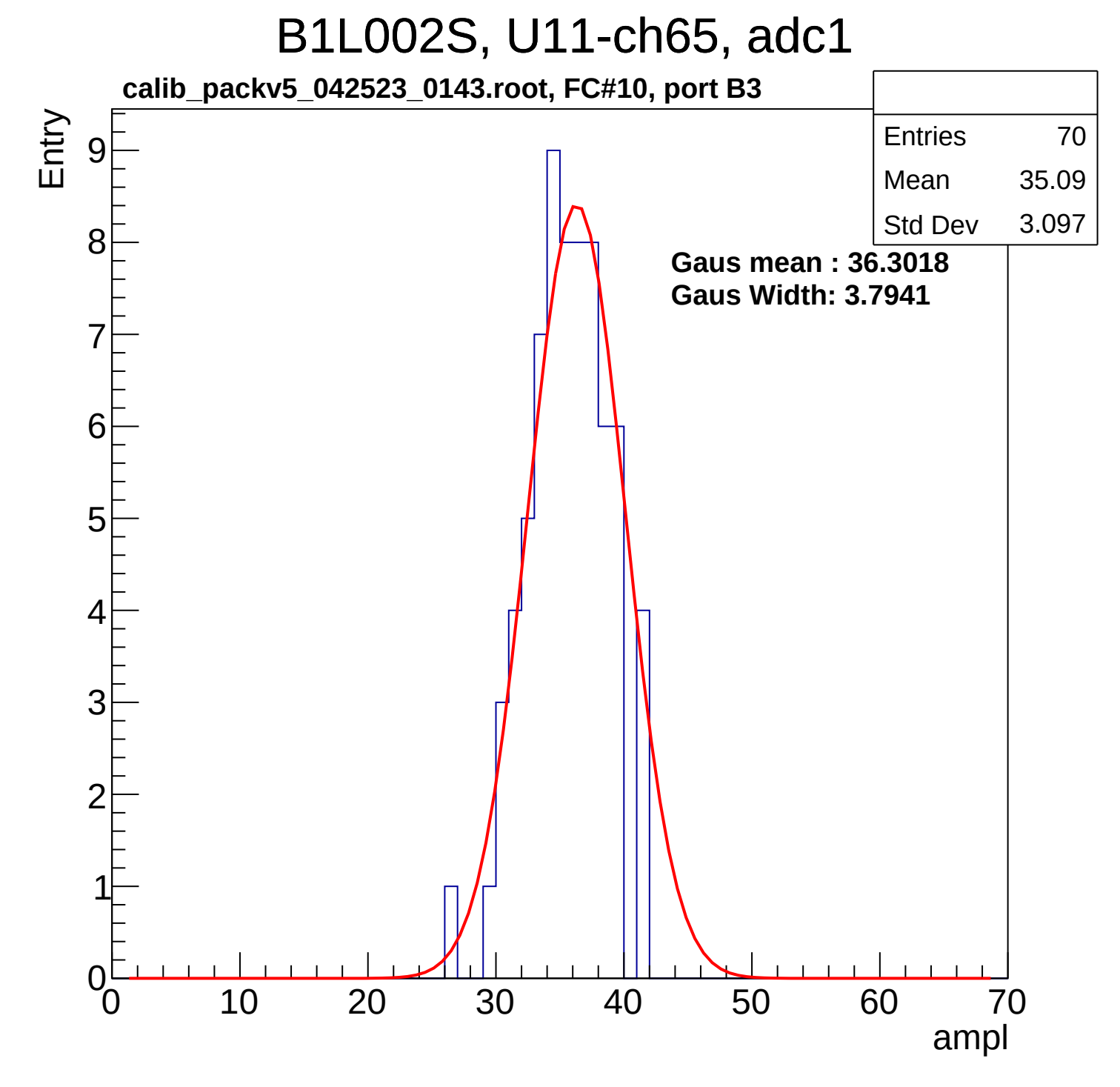
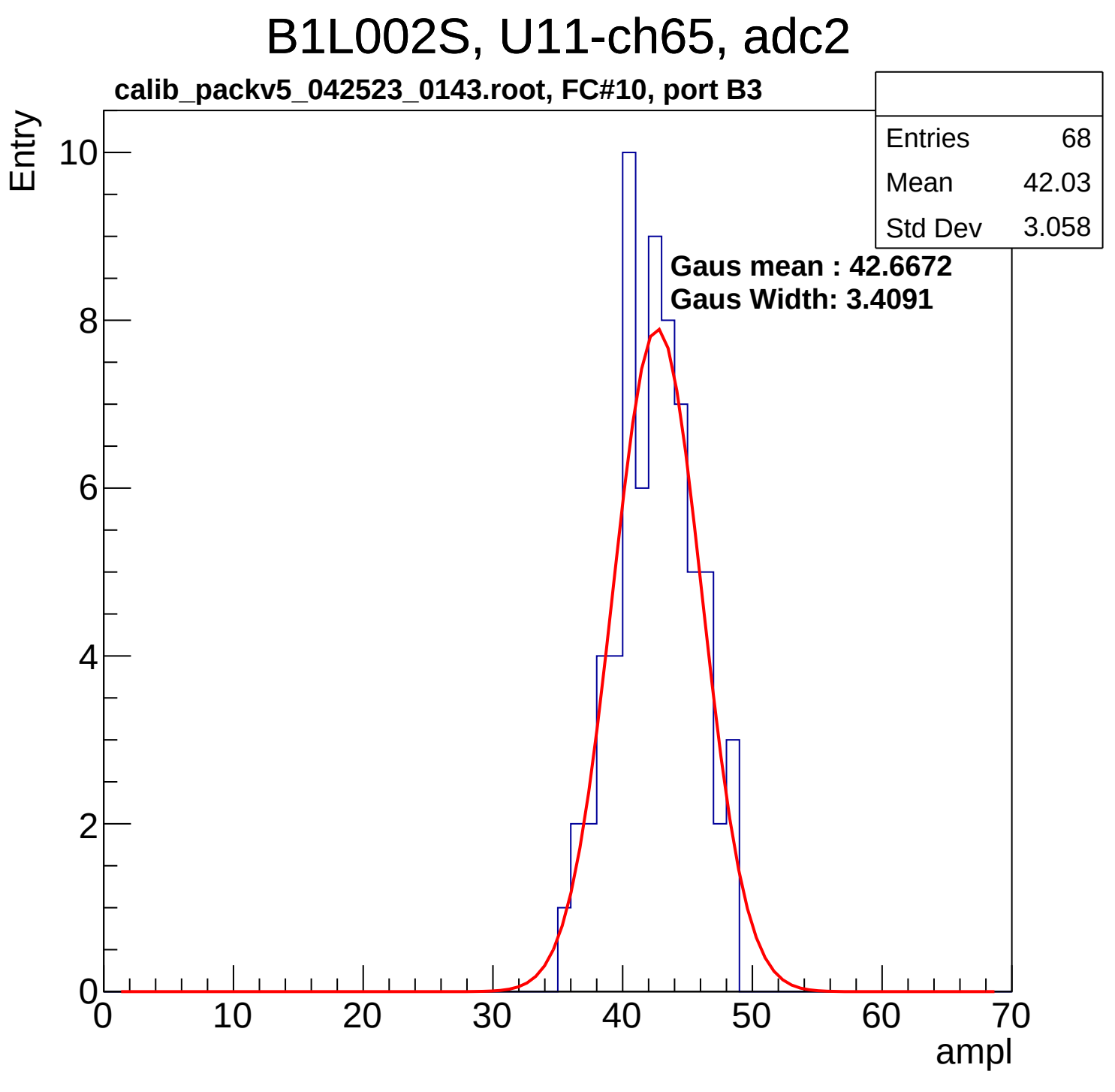
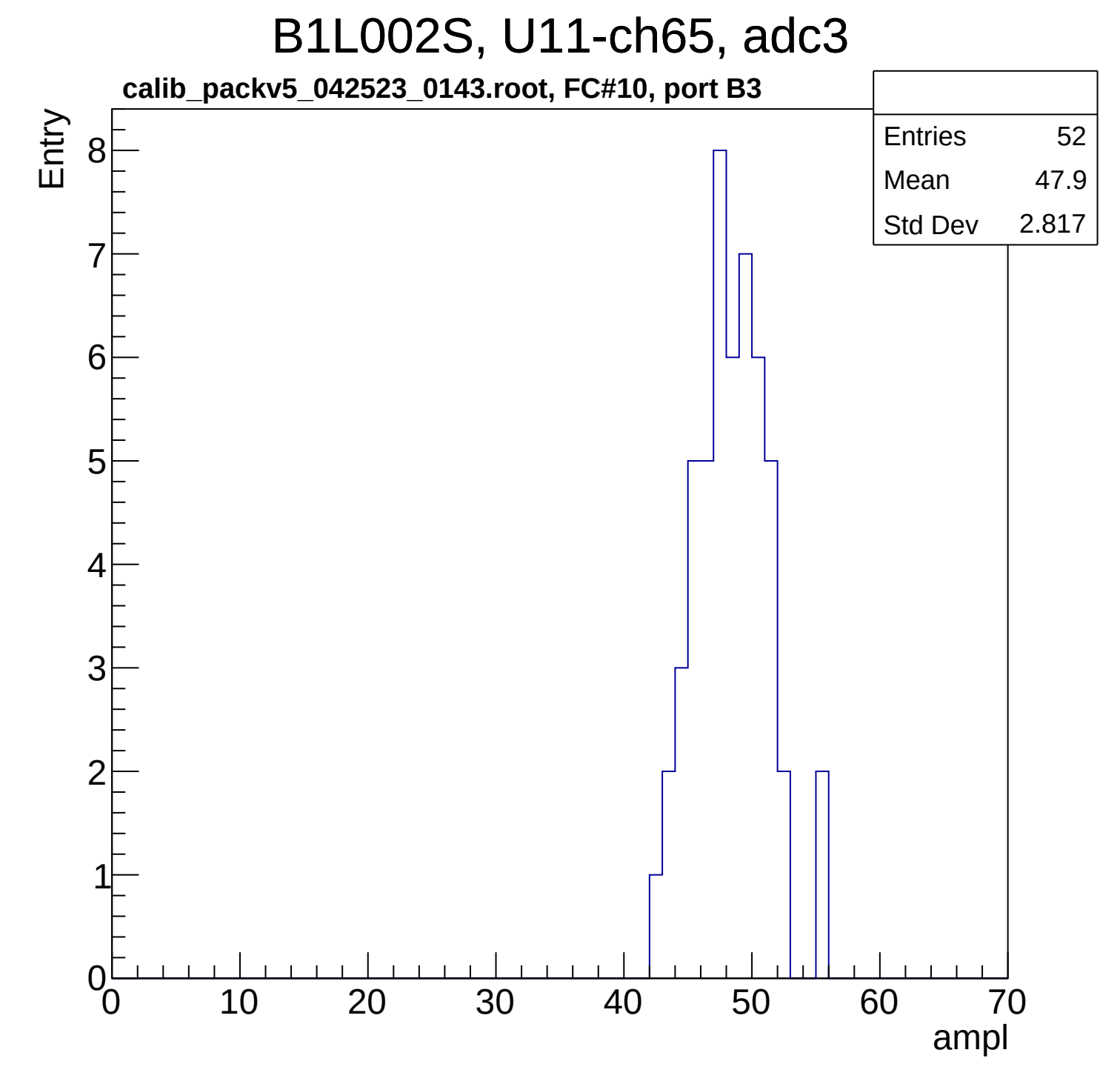
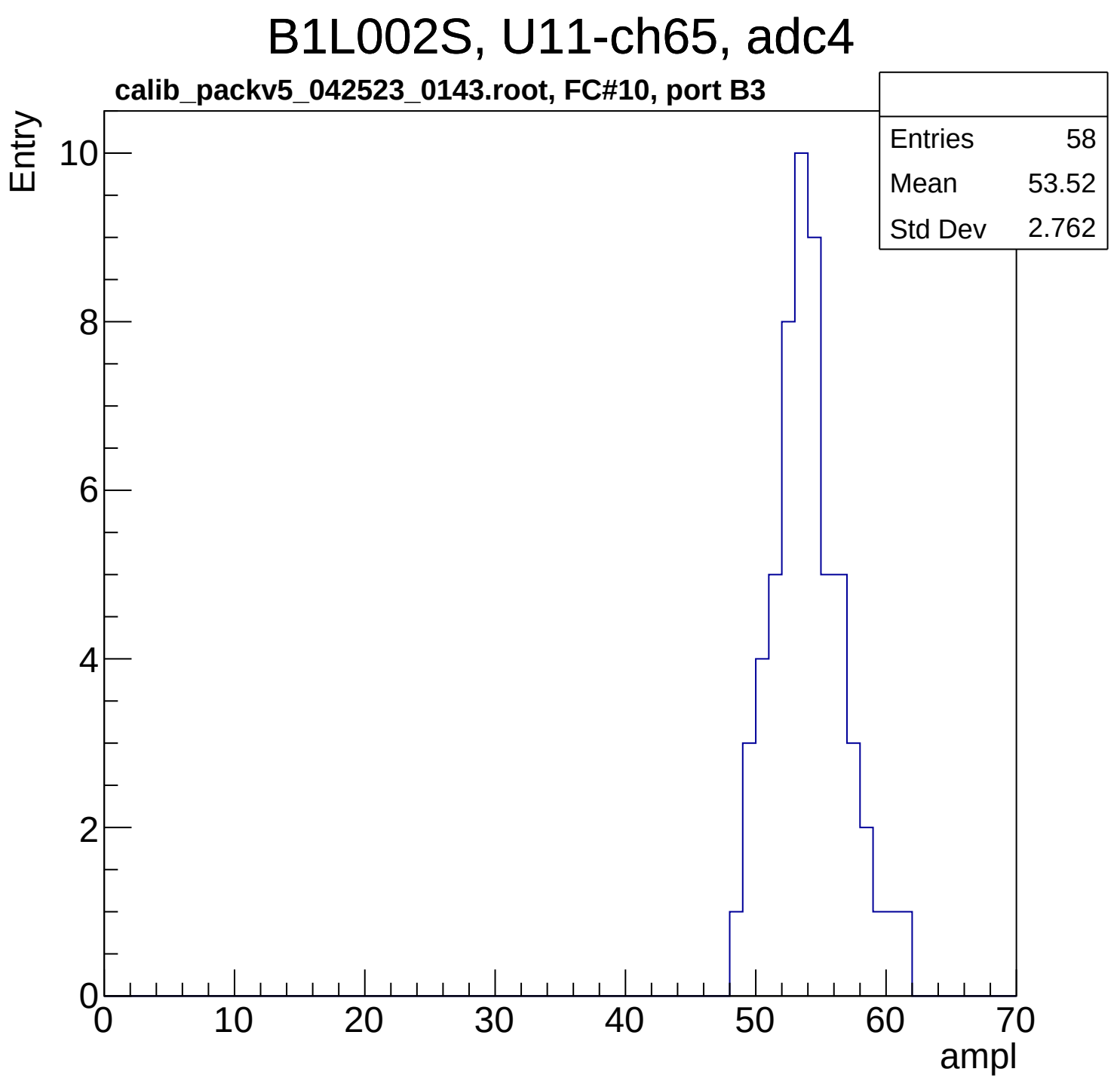
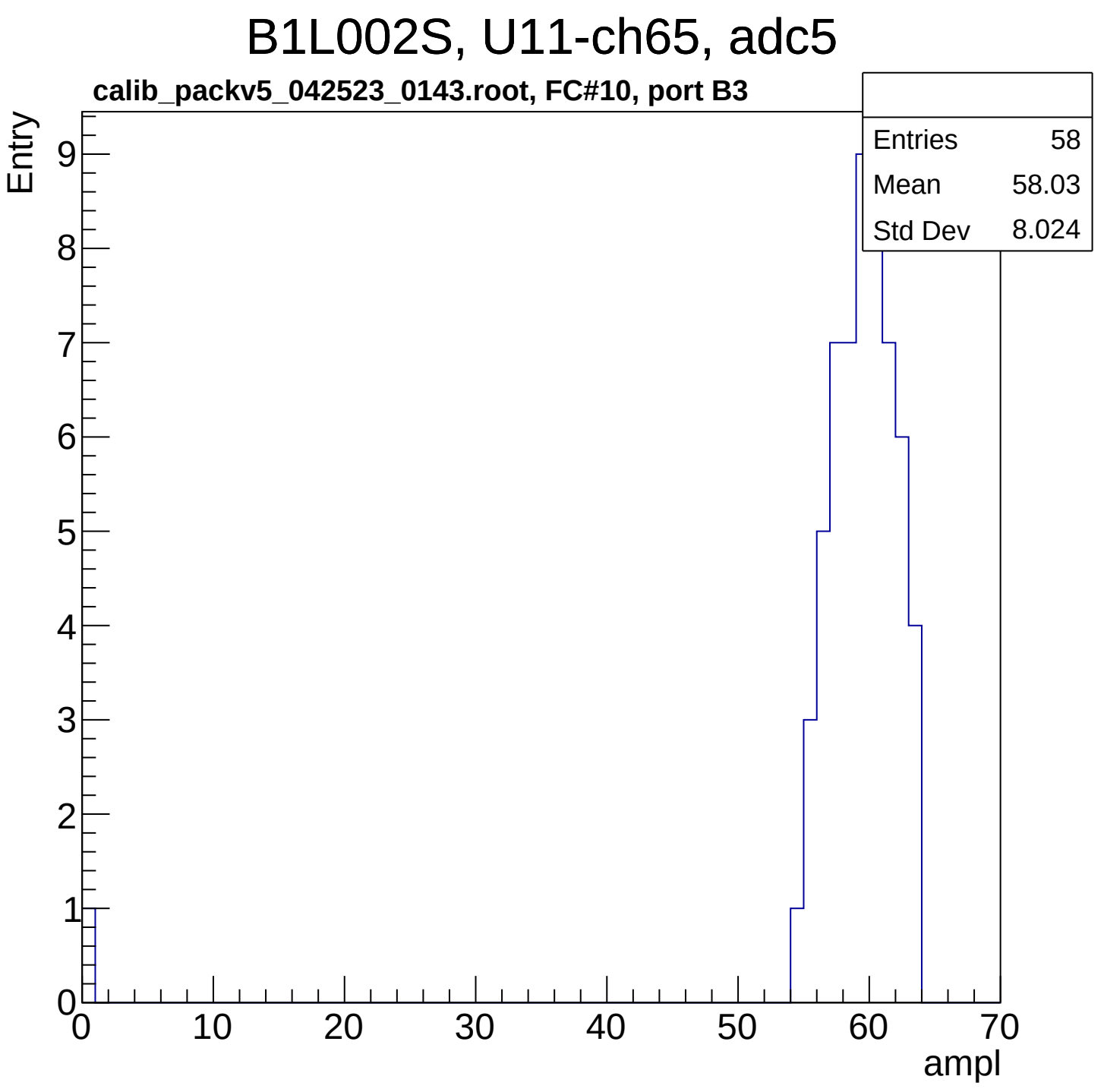
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.09
Std Dev 3.097
Gaus mean : 36.3018
Gaus Width: 3.7941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 42.03
Std Dev 3.058
Gaus mean : 42.6672
Gaus Width: 3.4091
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.9
Std Dev 2.817
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.52
Std Dev 2.762
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.03
Std Dev 8.024
Entry
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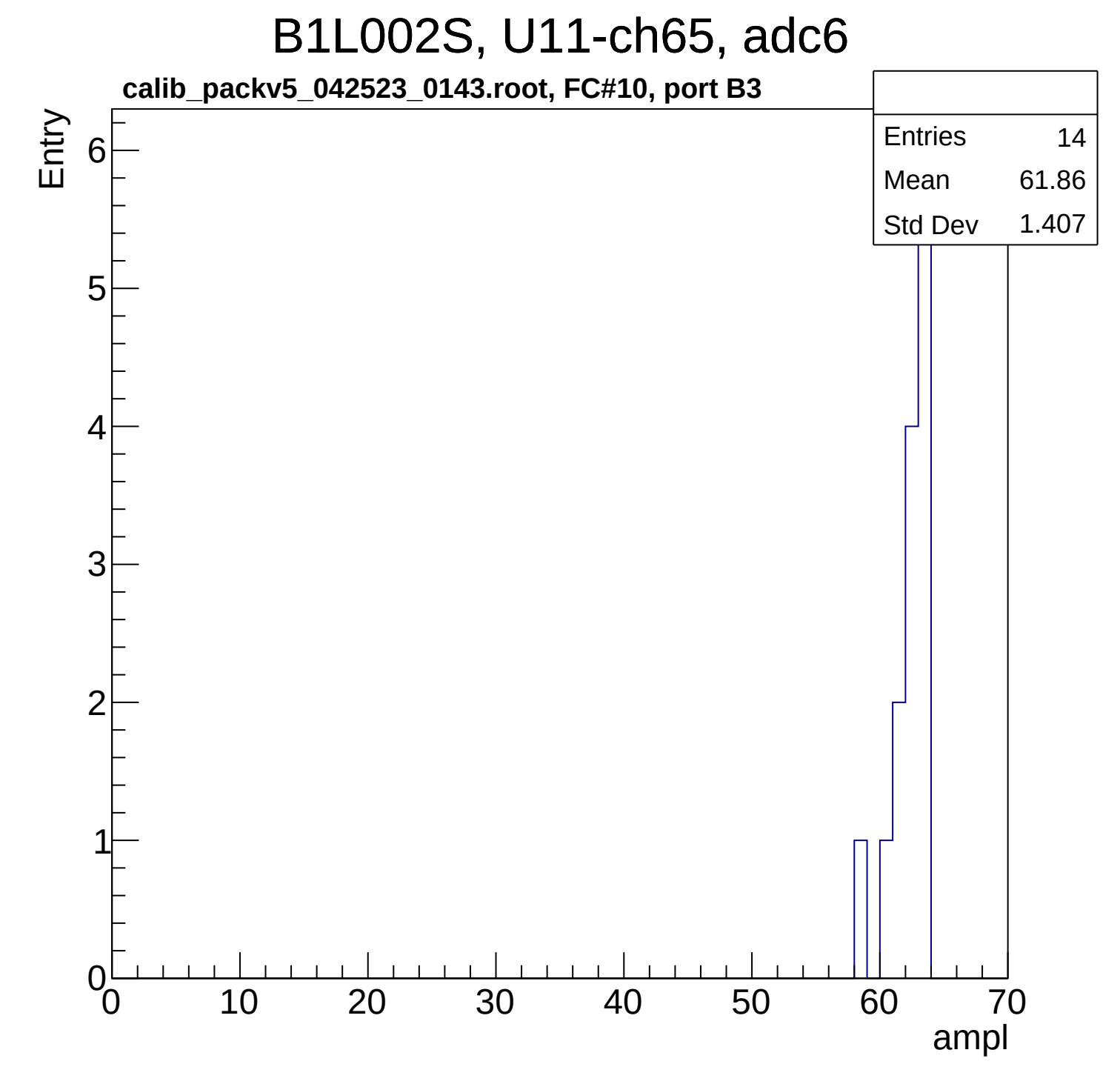
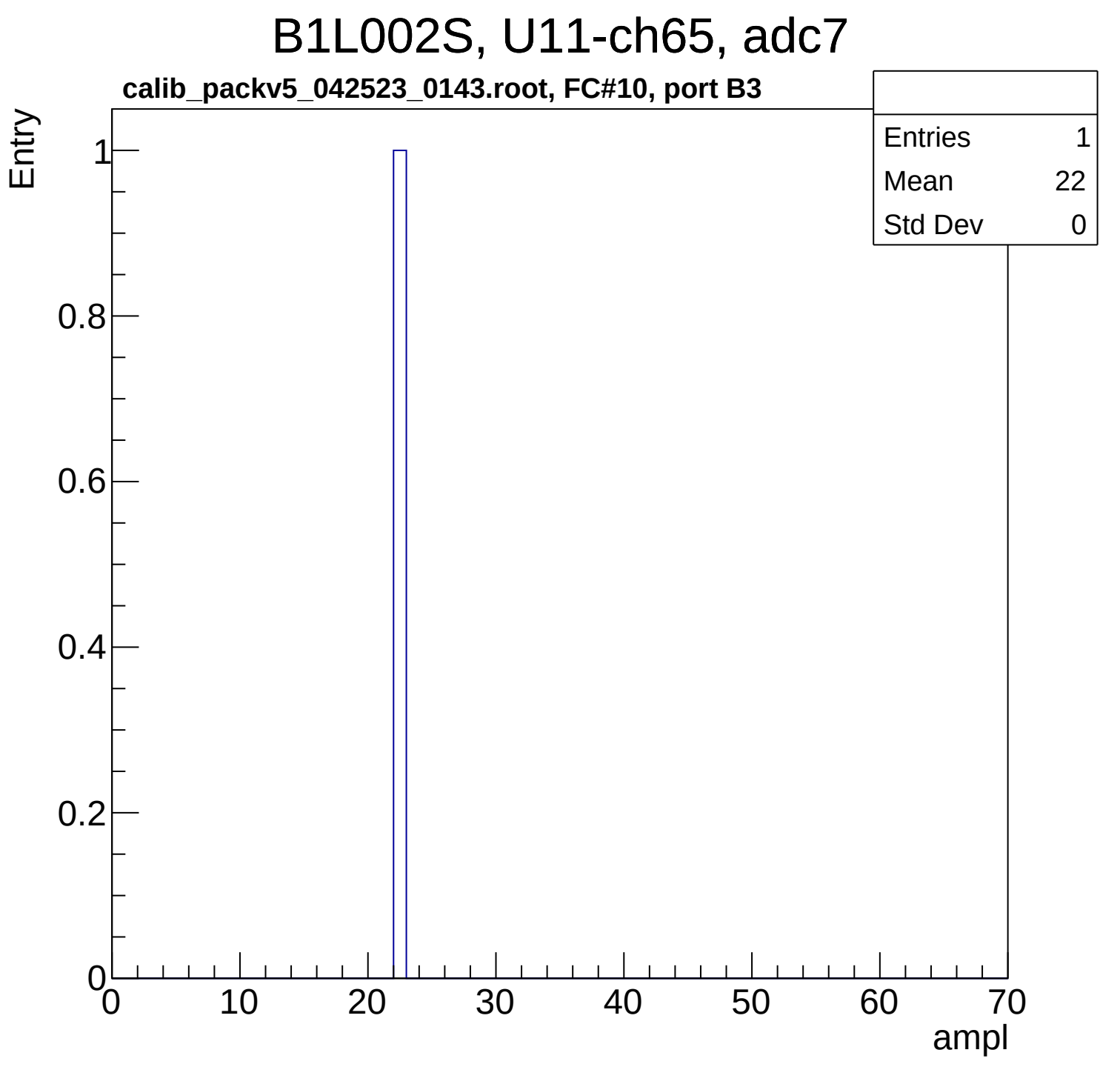
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.86
Std Dev 1.407
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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10
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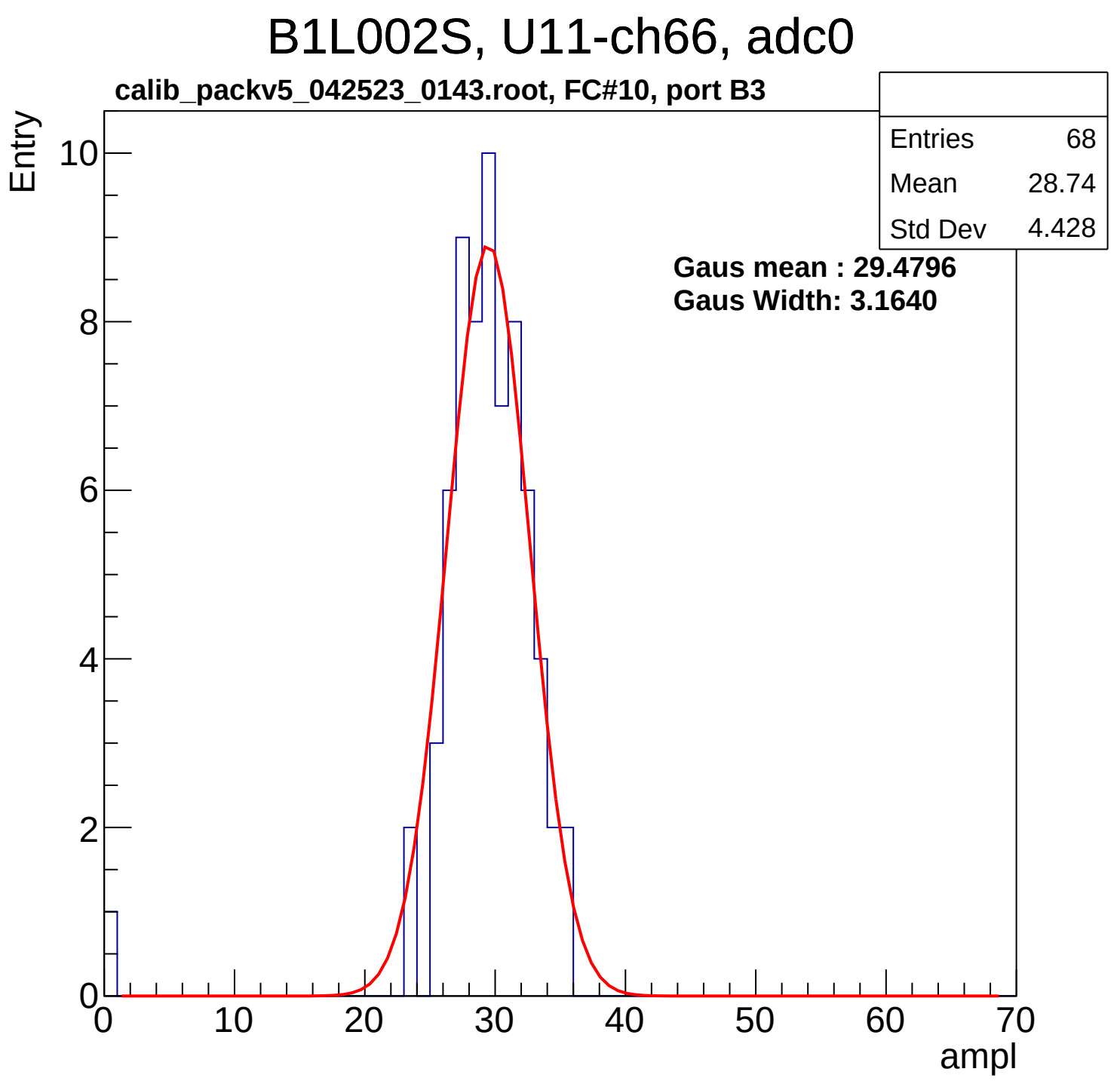
60
70
ampl


B1L002S, U11-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.74
Std Dev 4.428
Gaus mean : 29.4796
Gaus Width: 3.1640
Entry
ampl
0
2
4
6
8
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0
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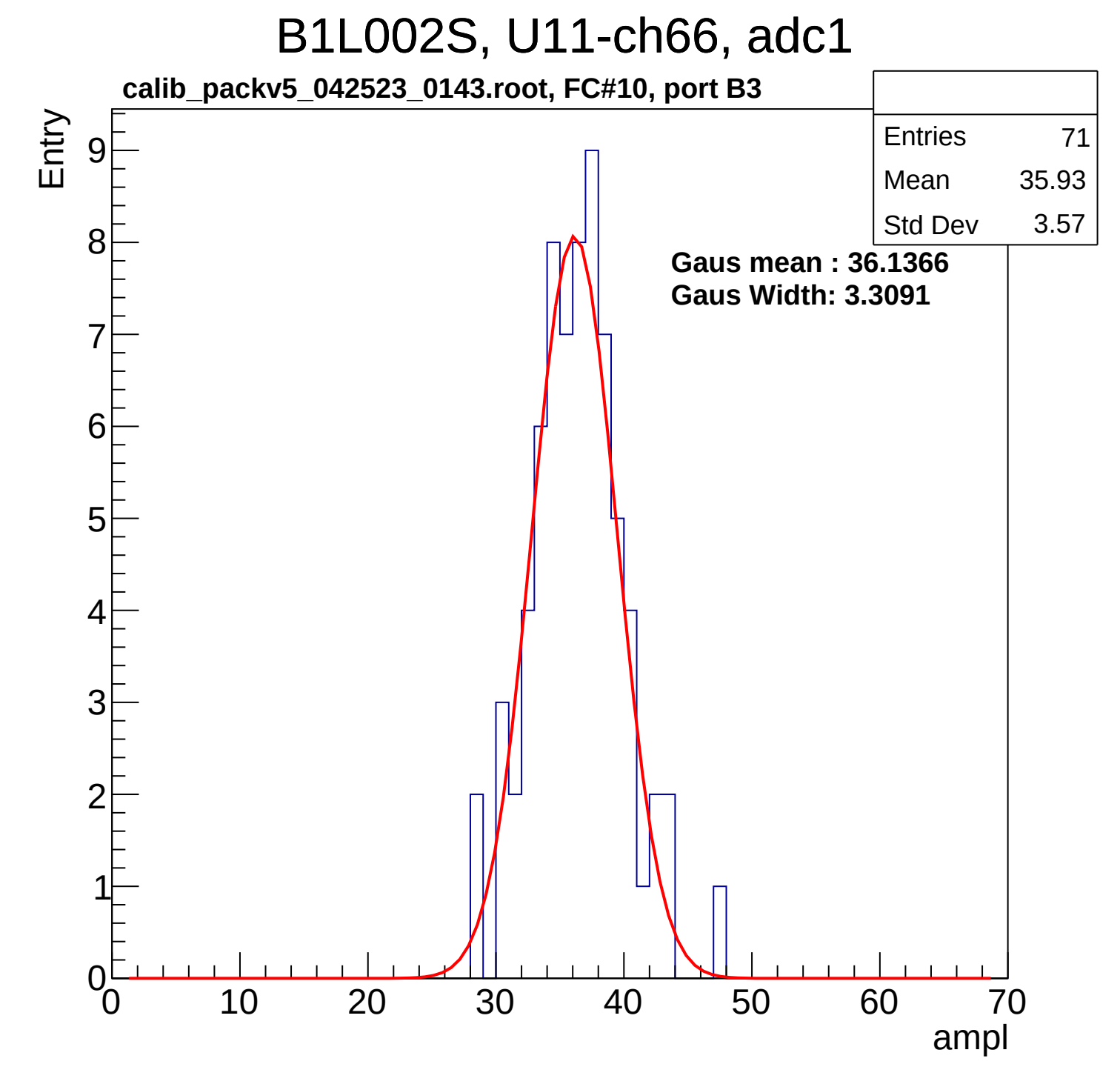
B1L002S, U11-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.93
Std Dev 3.57
Gaus mean : 36.1366
Gaus Width: 3.3091
Entry
ampl
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3
4
5
6
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0
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40
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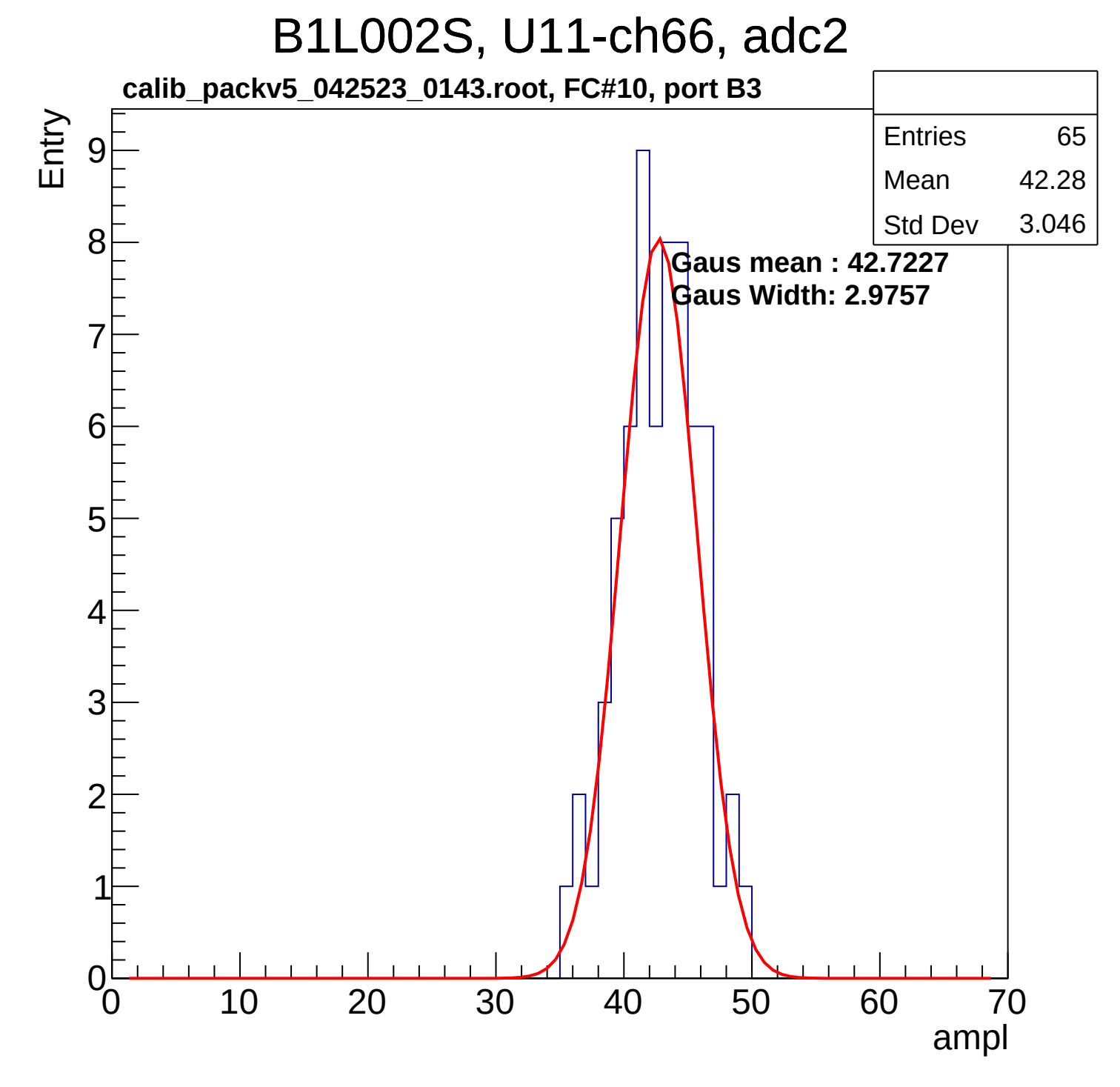
B1L002S, U11-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 42.28
Std Dev 3.046
Gaus mean : 42.7227
Gaus Width: 2.9757
Entry
ampl
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2
3
4
5
6
7
8
9
0
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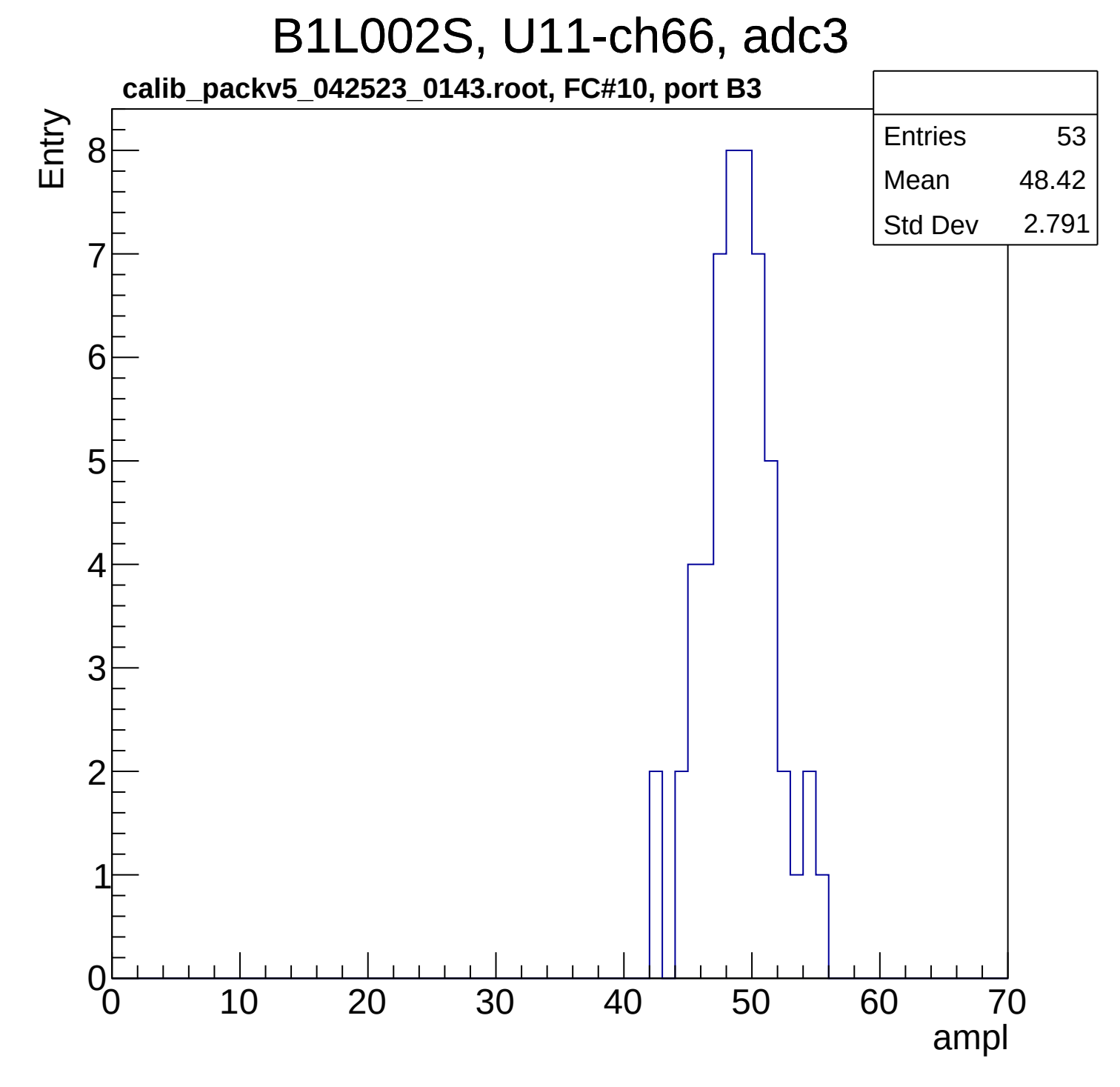
B1L002S, U11-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 48.42
Std Dev 2.791
Entry
ampl
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1
2
3
4
5
6
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0
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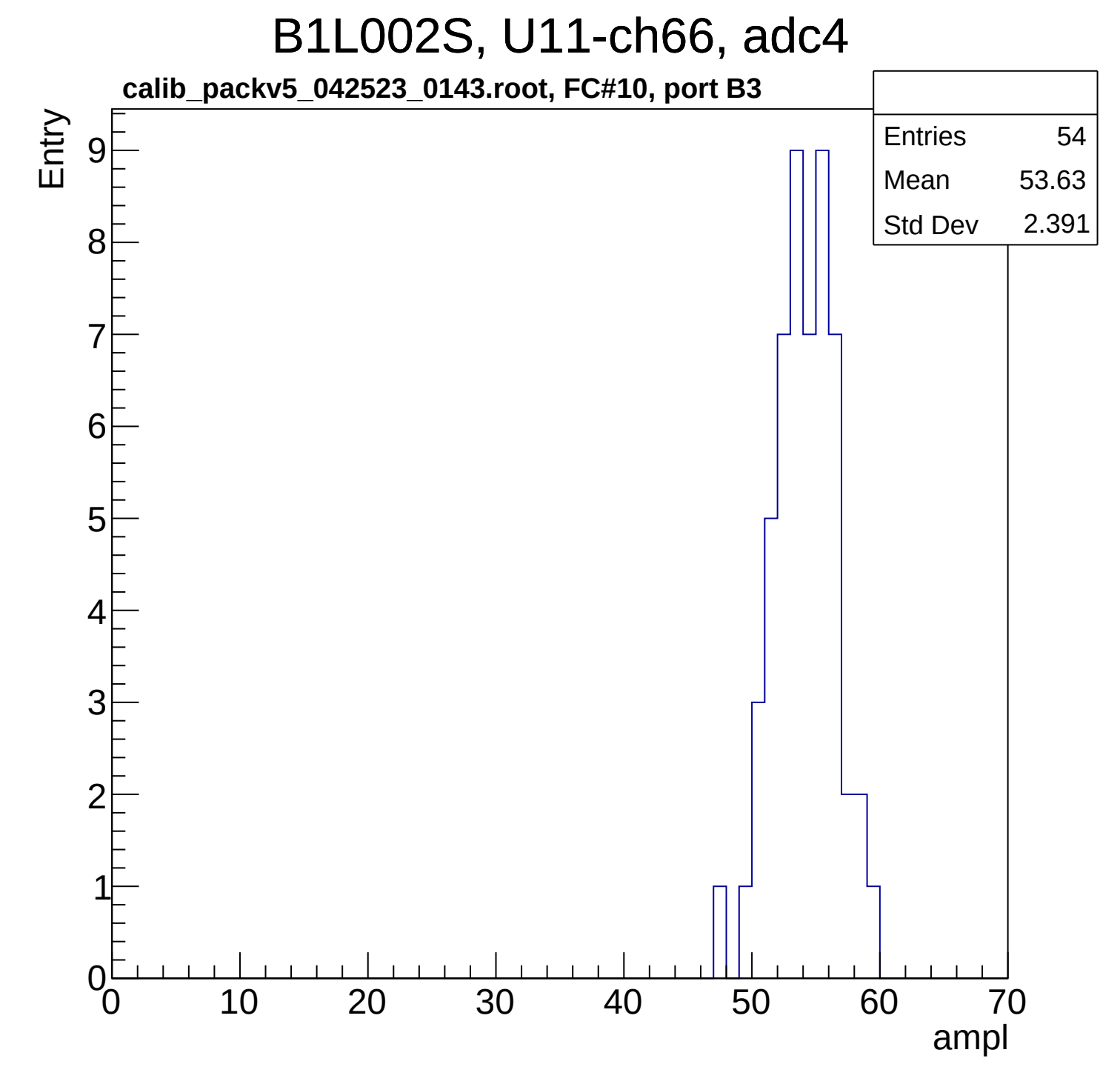
B1L002S, U11-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.63
Std Dev 2.391
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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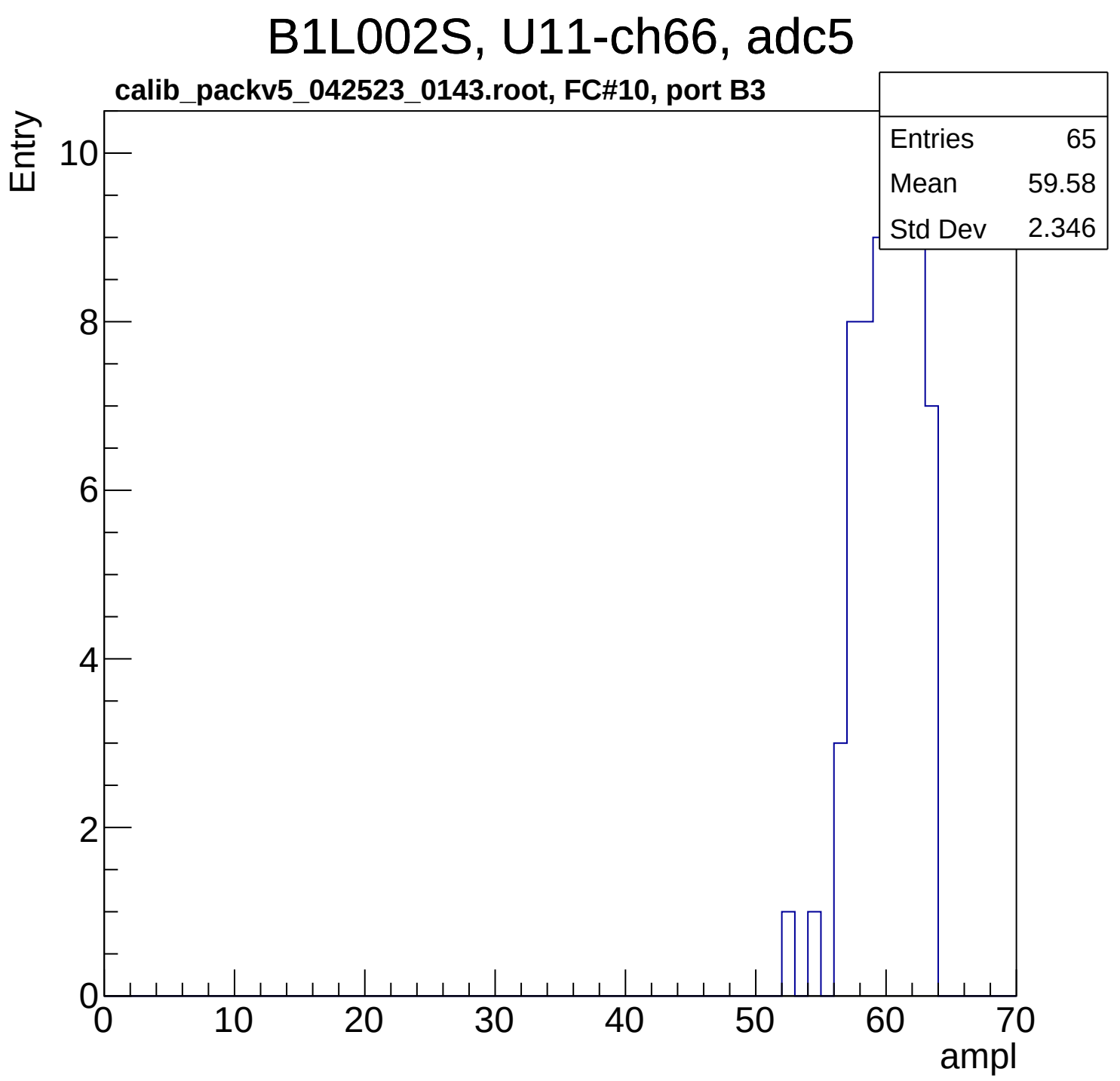
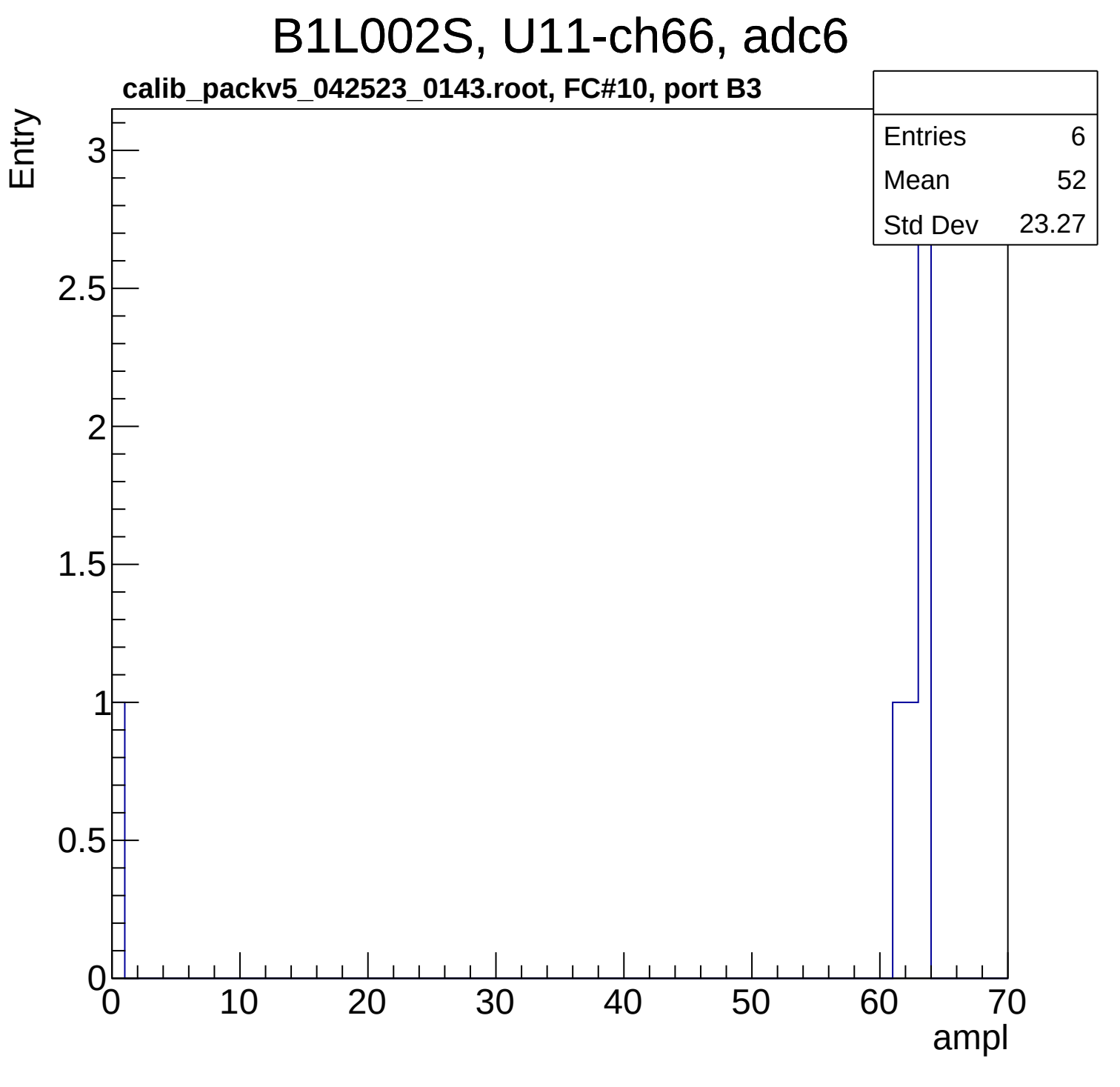

B1L002S, U11-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 59.58
Std Dev 2.346
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
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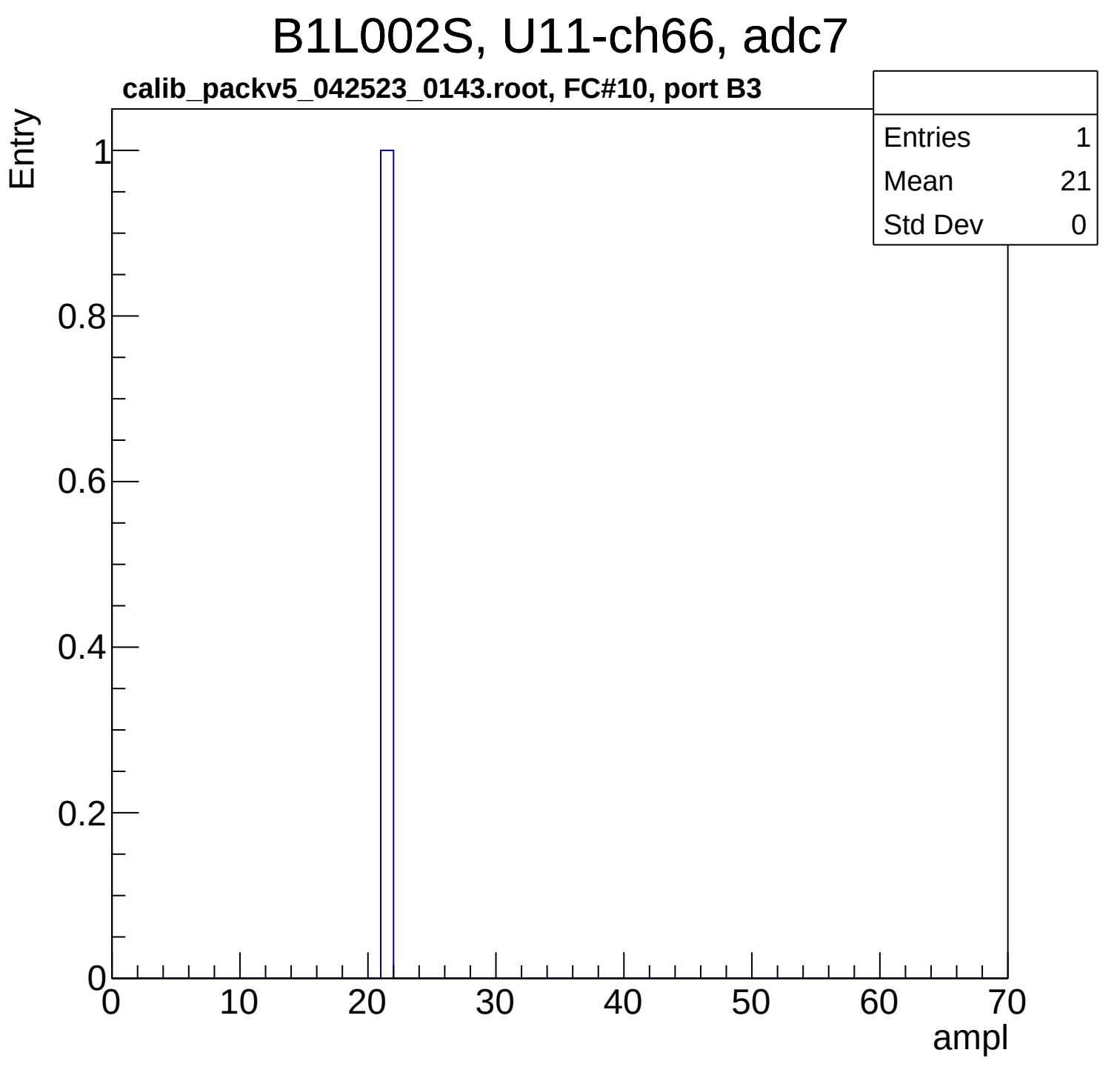
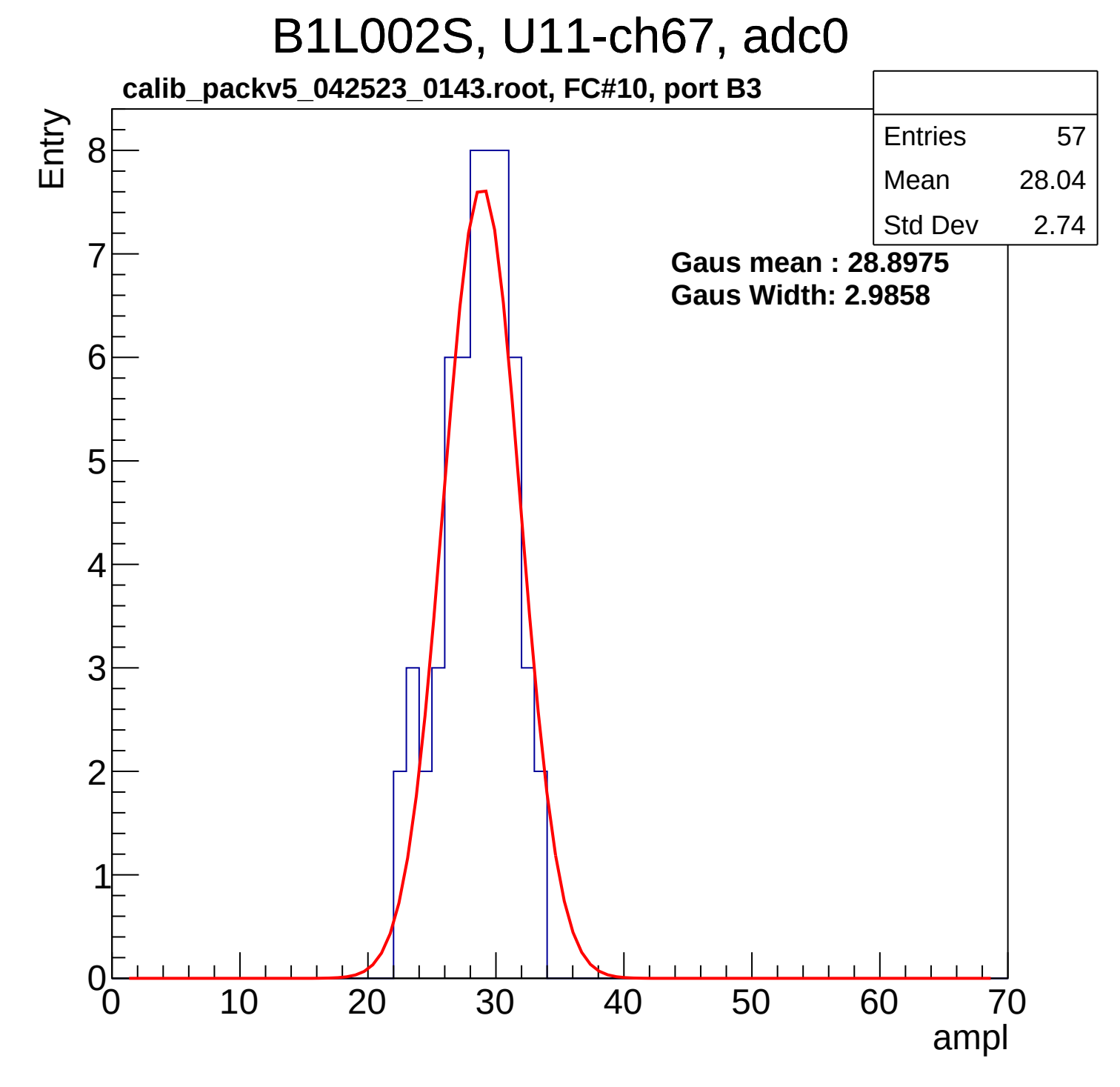
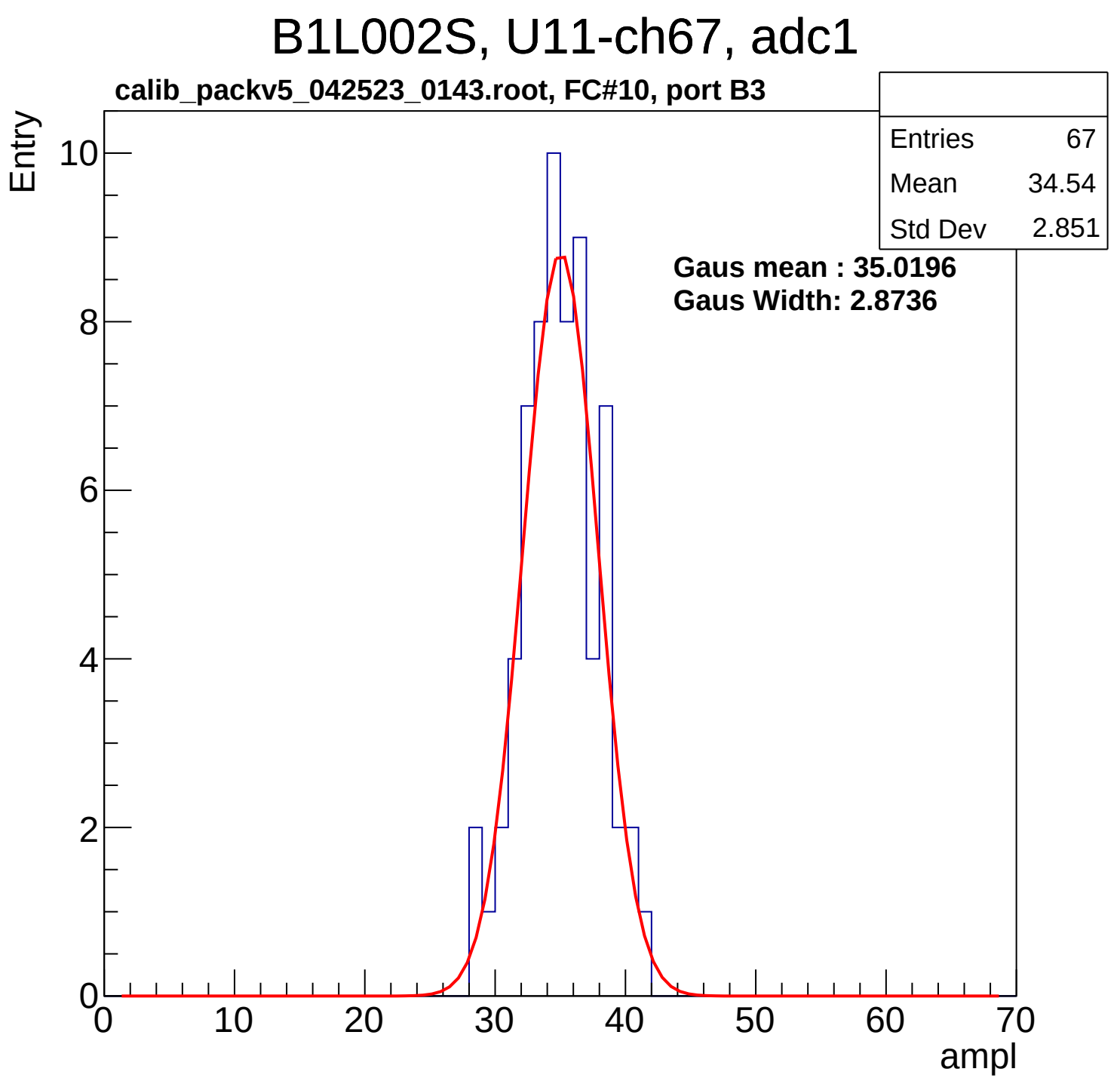
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.04
Std Dev 2.74
Gaus mean : 28.8975
Gaus Width: 2.9858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.54
Std Dev 2.851
Gaus mean : 35.0196
Gaus Width: 2.8736
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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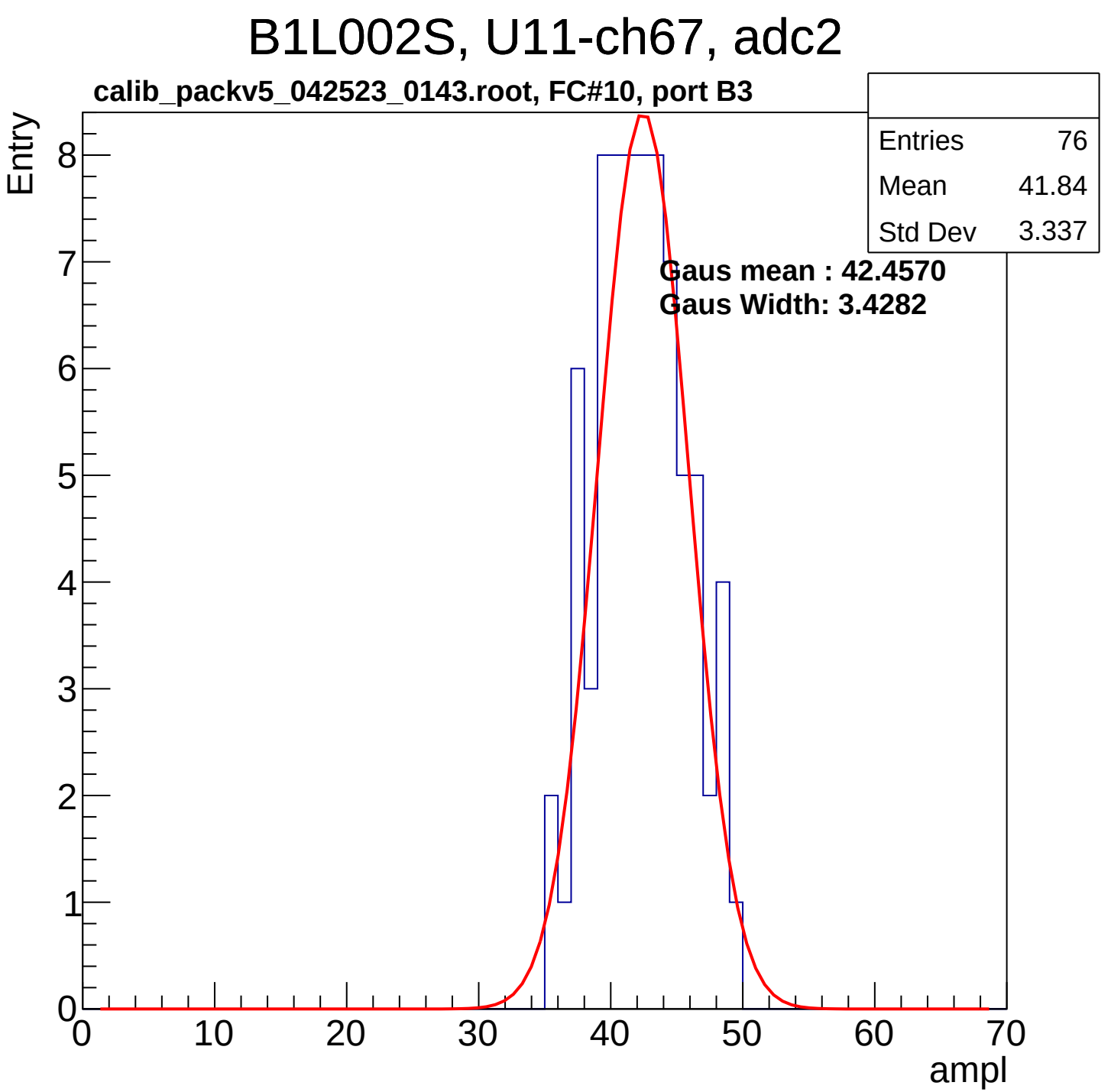
B1L002S, U11-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 41.84
Std Dev 3.337
Gaus mean : 42.4570
Gaus Width: 3.4282
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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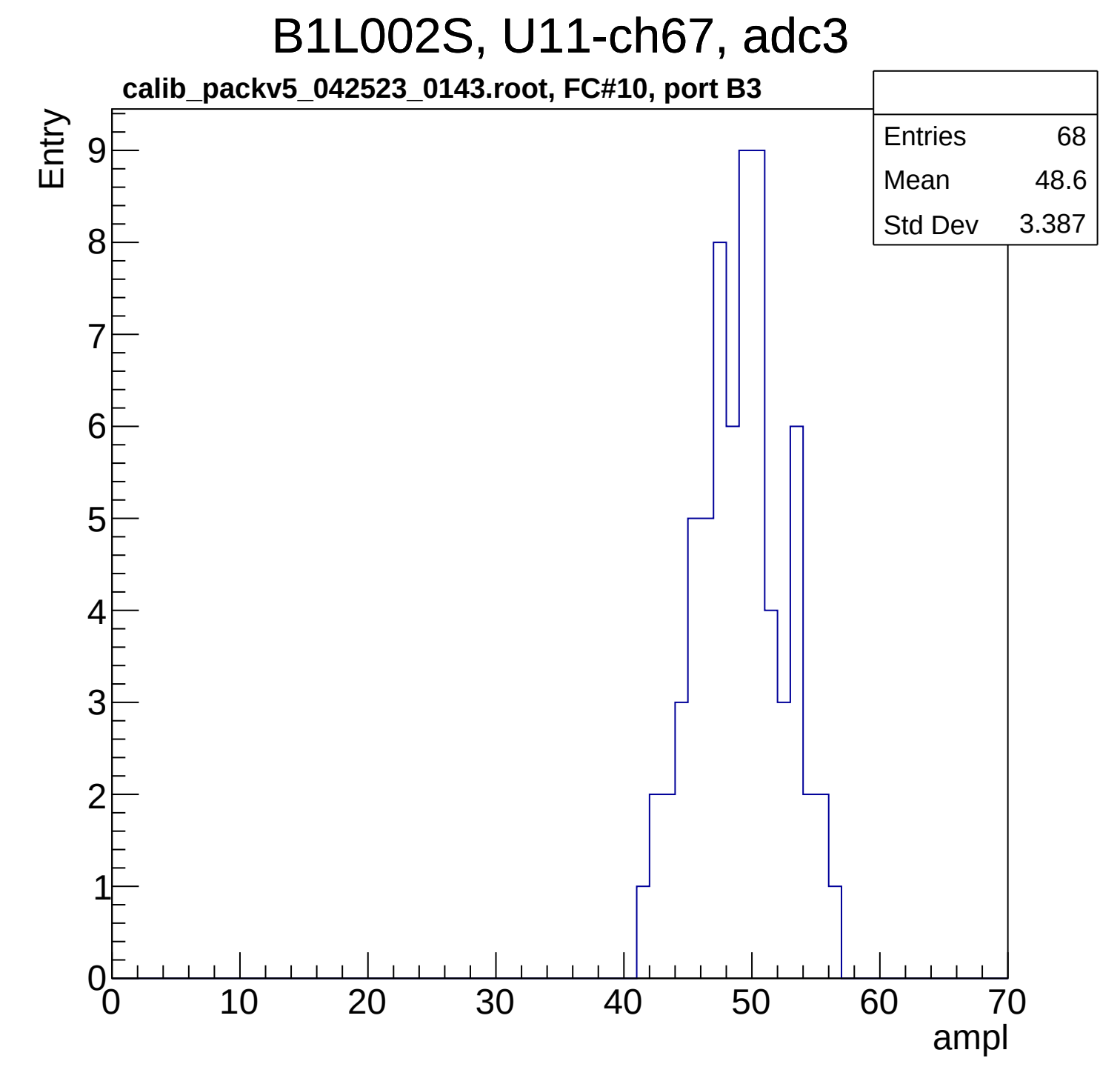
B1L002S, U11-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 48.6
Std Dev 3.387
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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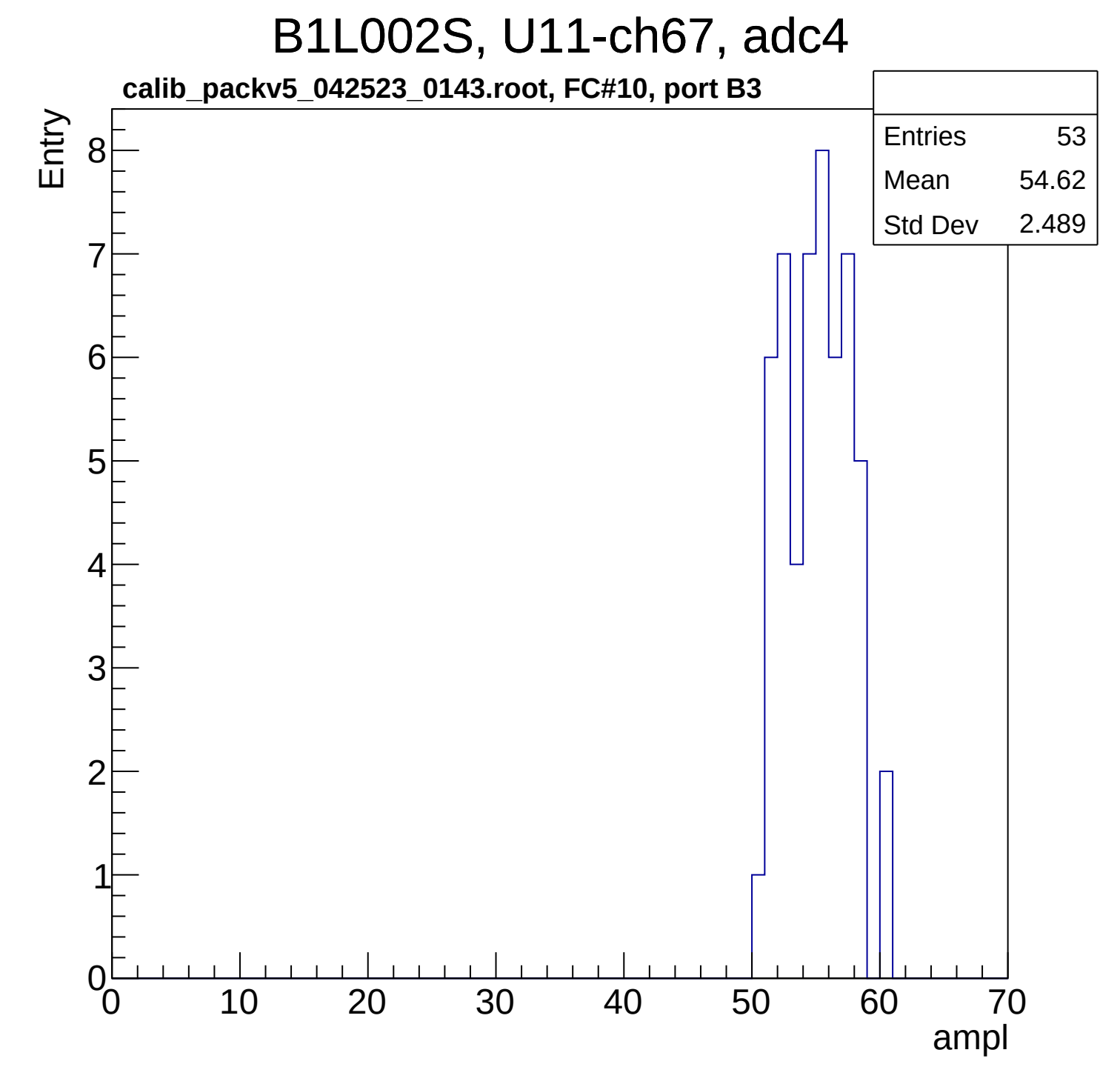
B1L002S, U11-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.62
Std Dev 2.489
Entry
ampl
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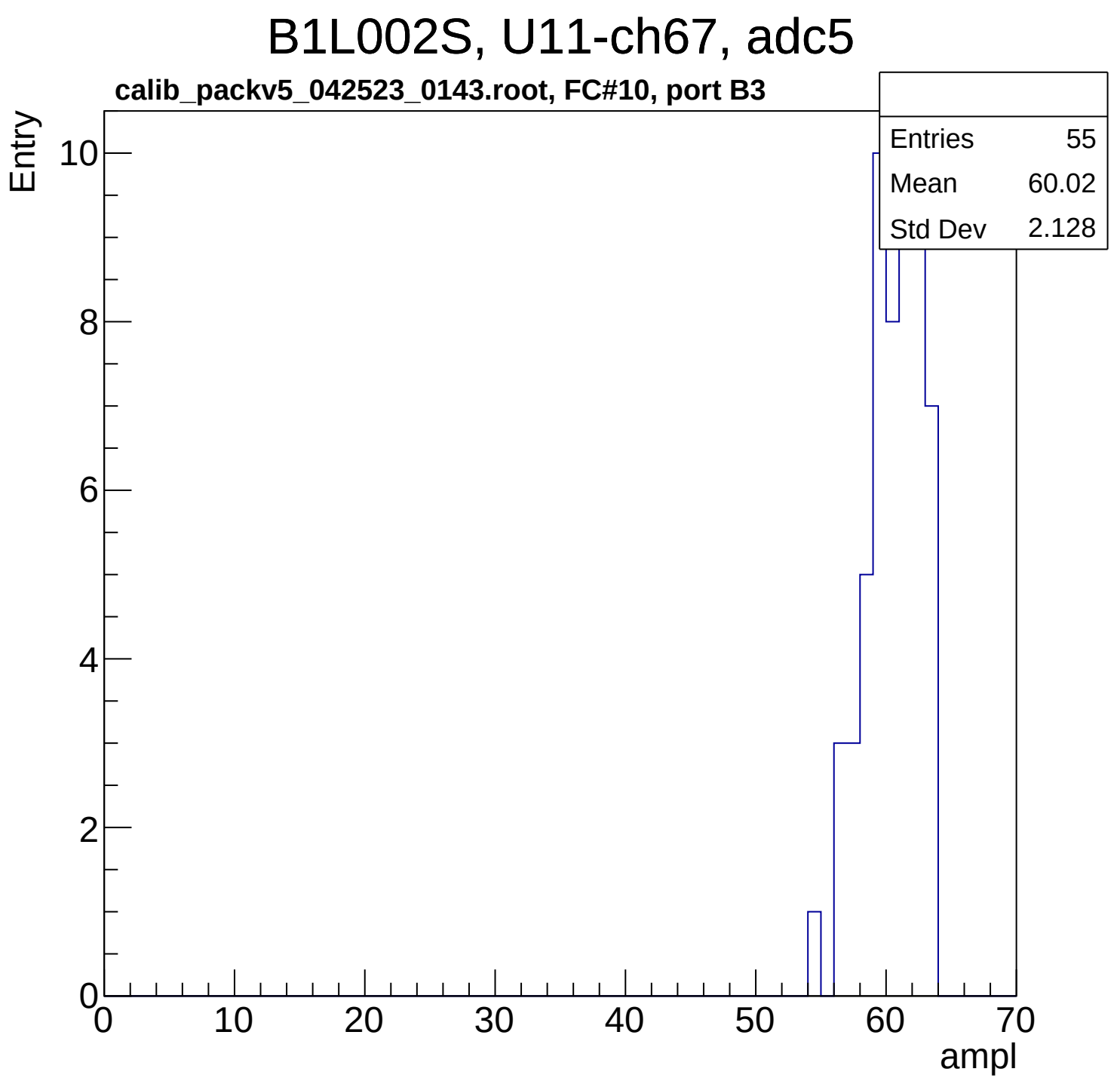
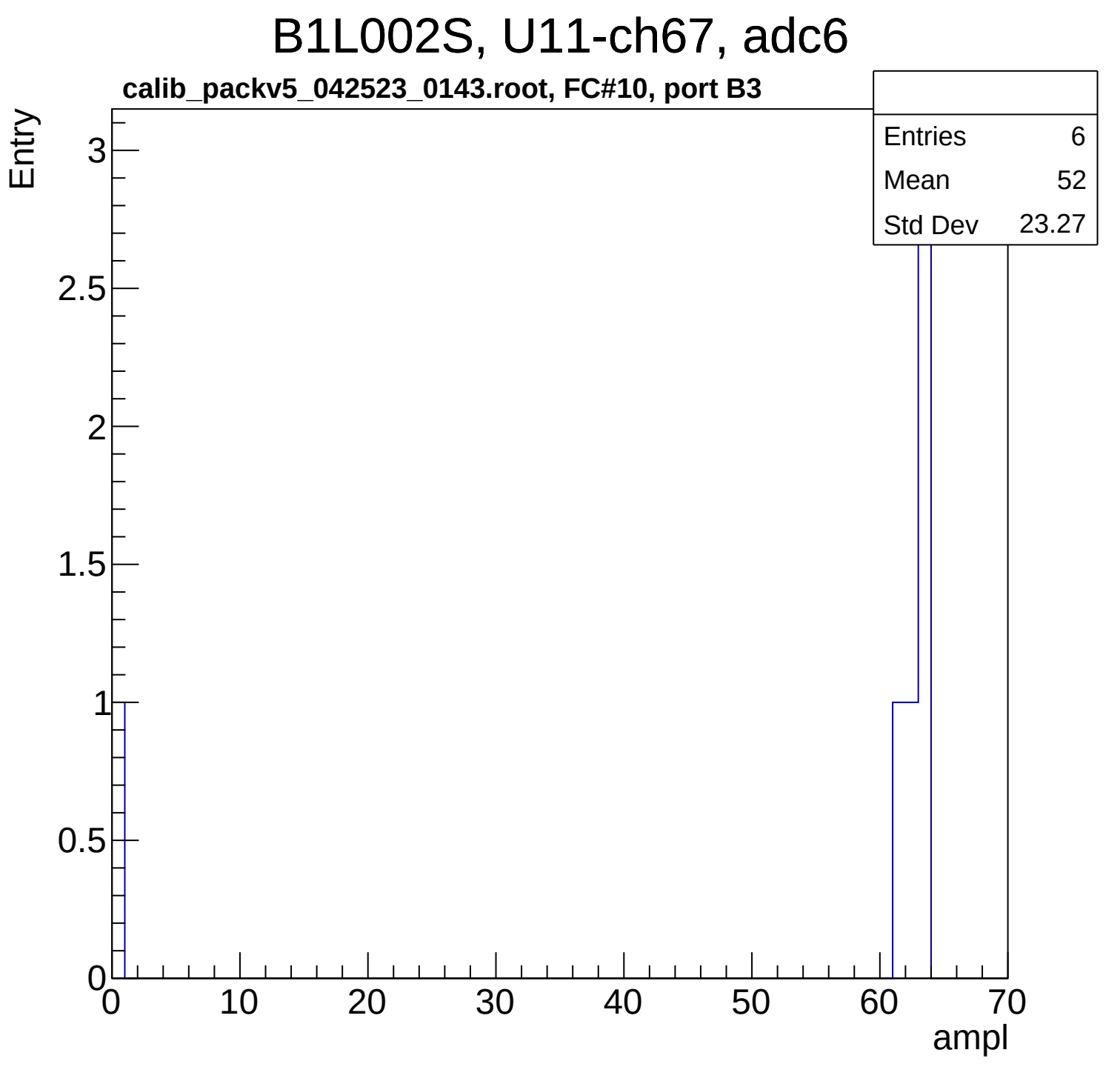
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 60.02
Std Dev 2.128
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
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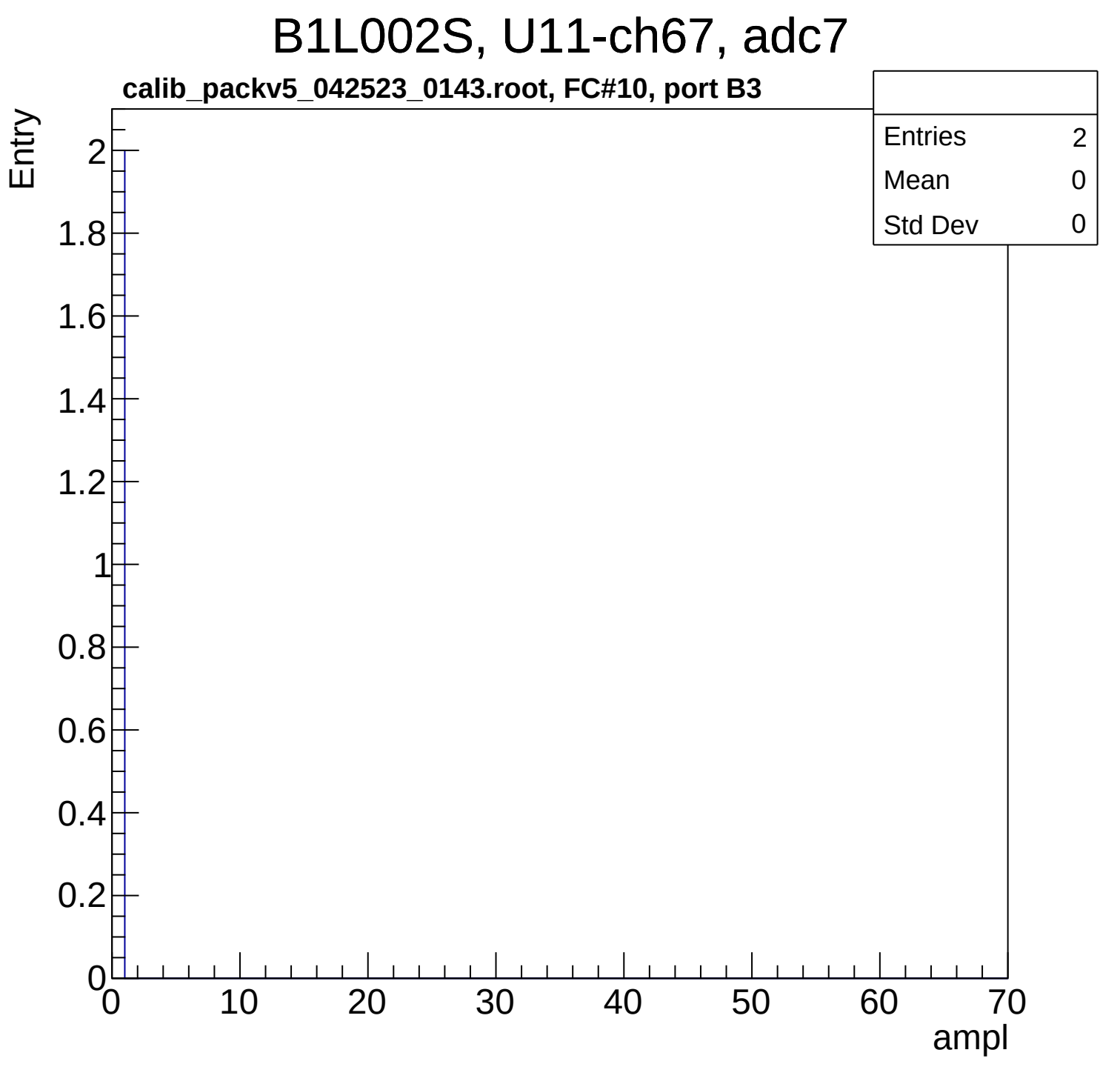
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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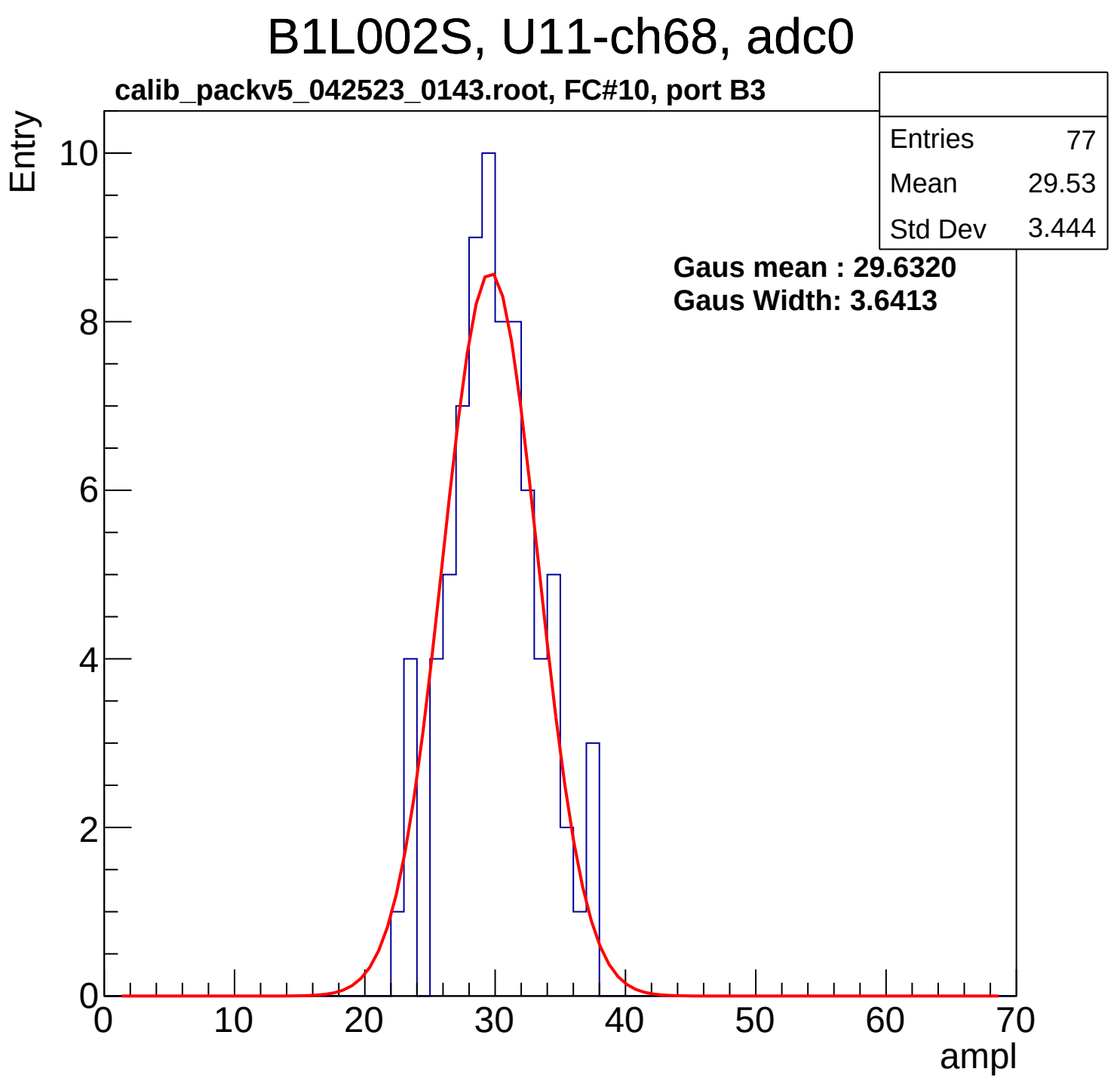
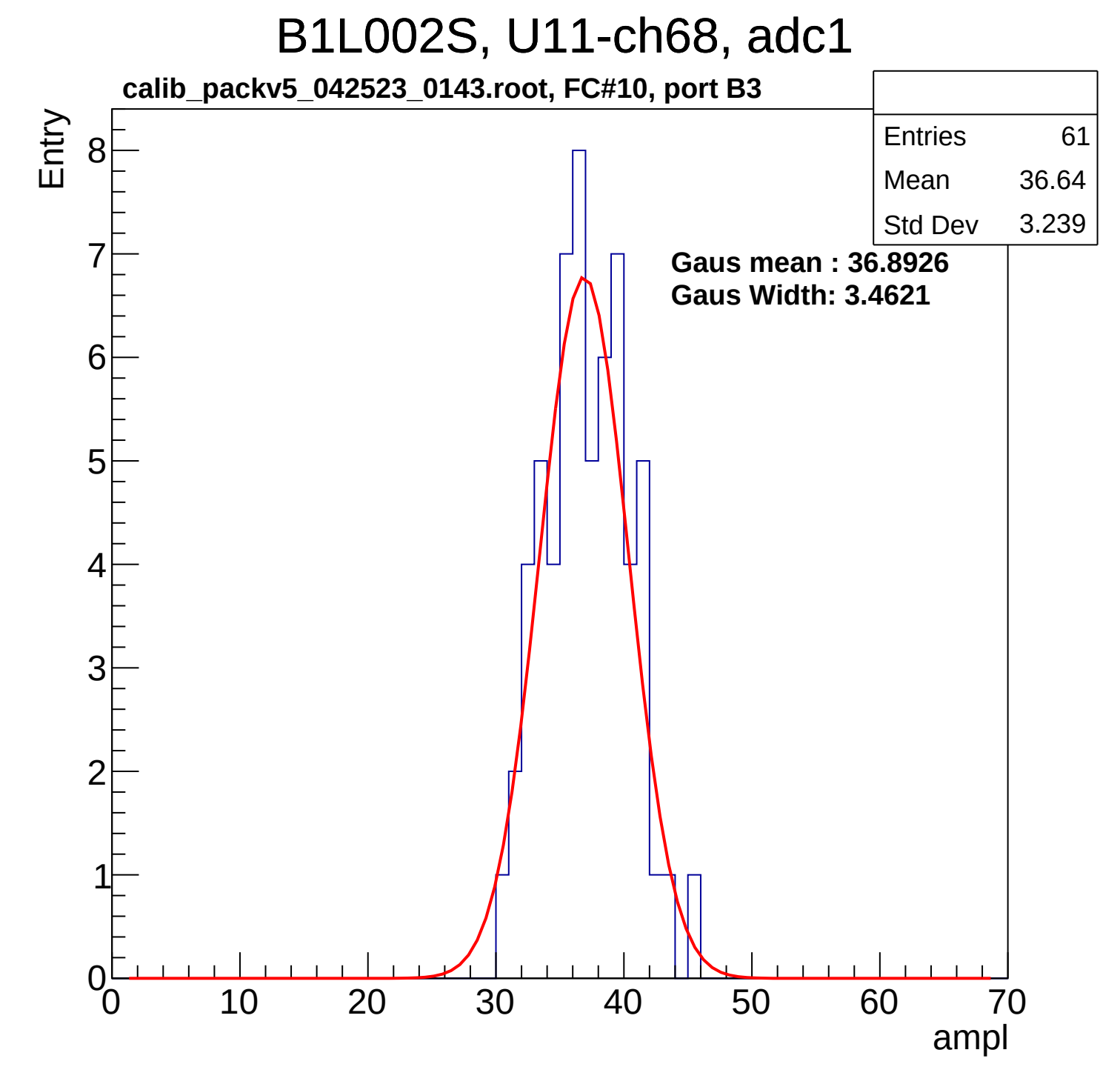

B1L002S, U11-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 29.53
Std Dev 3.444
Gaus mean : 29.6320
Gaus Width: 3.6413
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 36.64
Std Dev 3.239
Gaus mean : 36.8926
Gaus Width: 3.4621
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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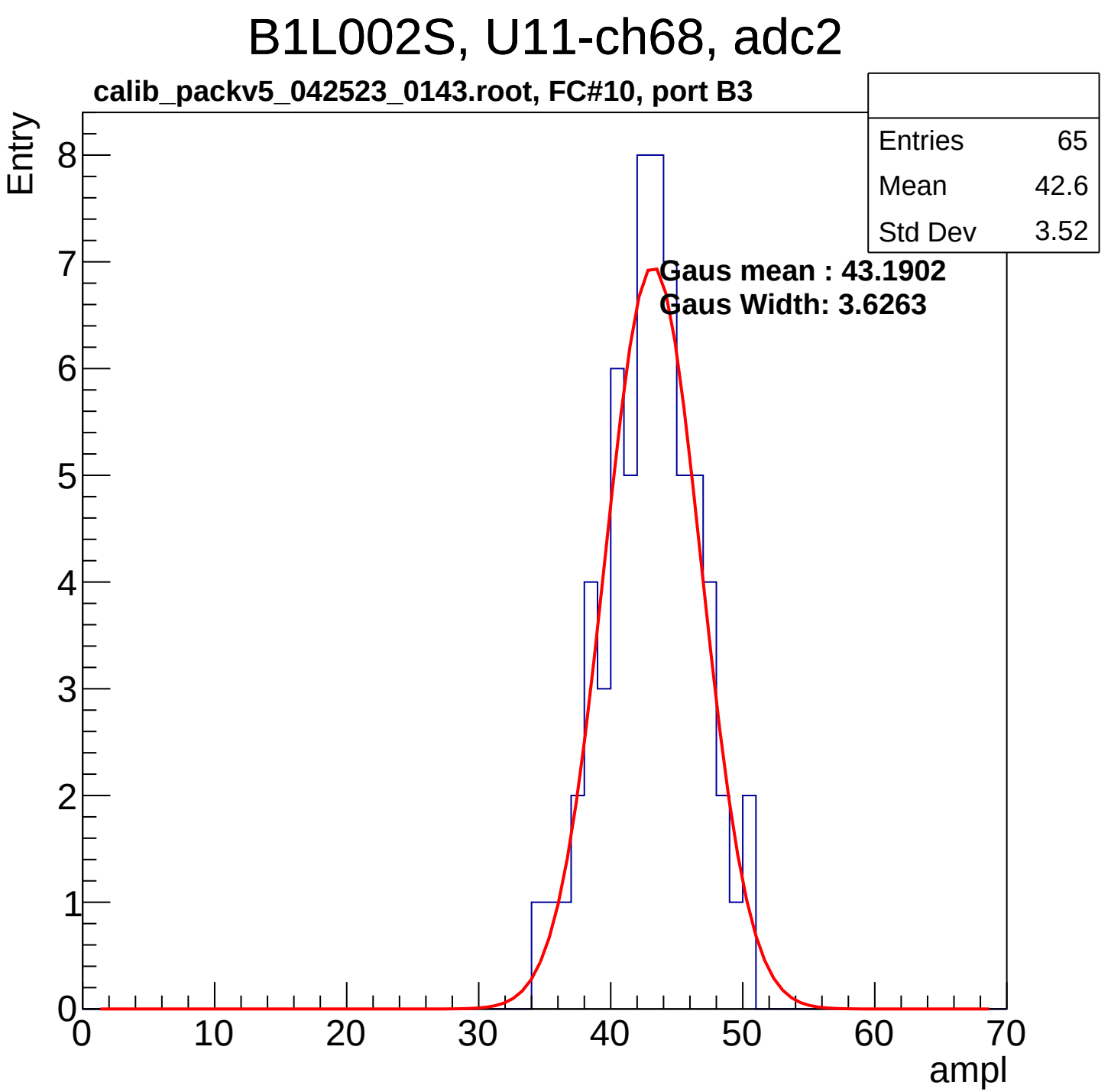
B1L002S, U11-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 42.6
Std Dev 3.52
Gaus mean : 43.1902
Gaus Width: 3.6263
Entry
ampl
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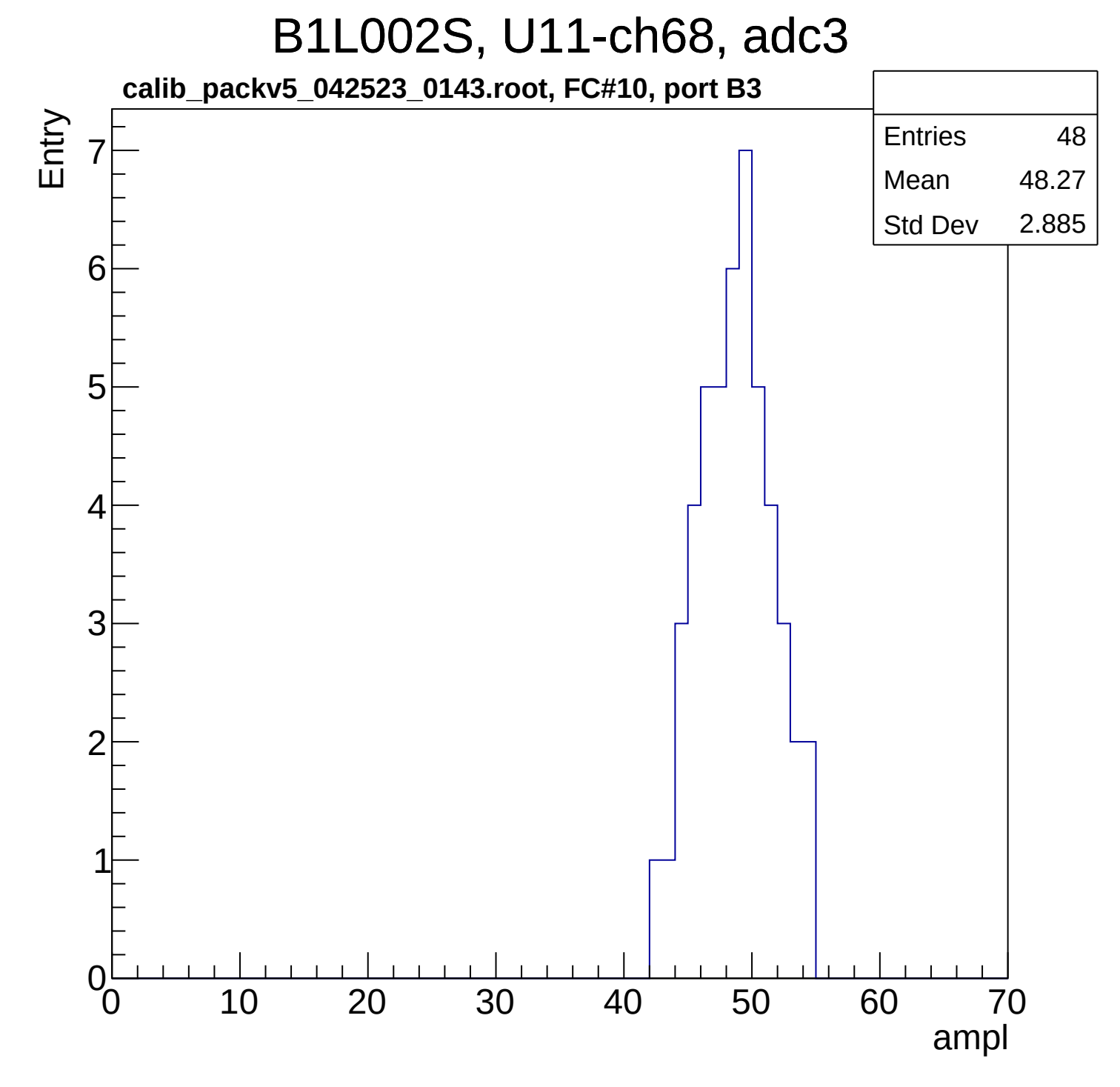
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 48.27
Std Dev 2.885
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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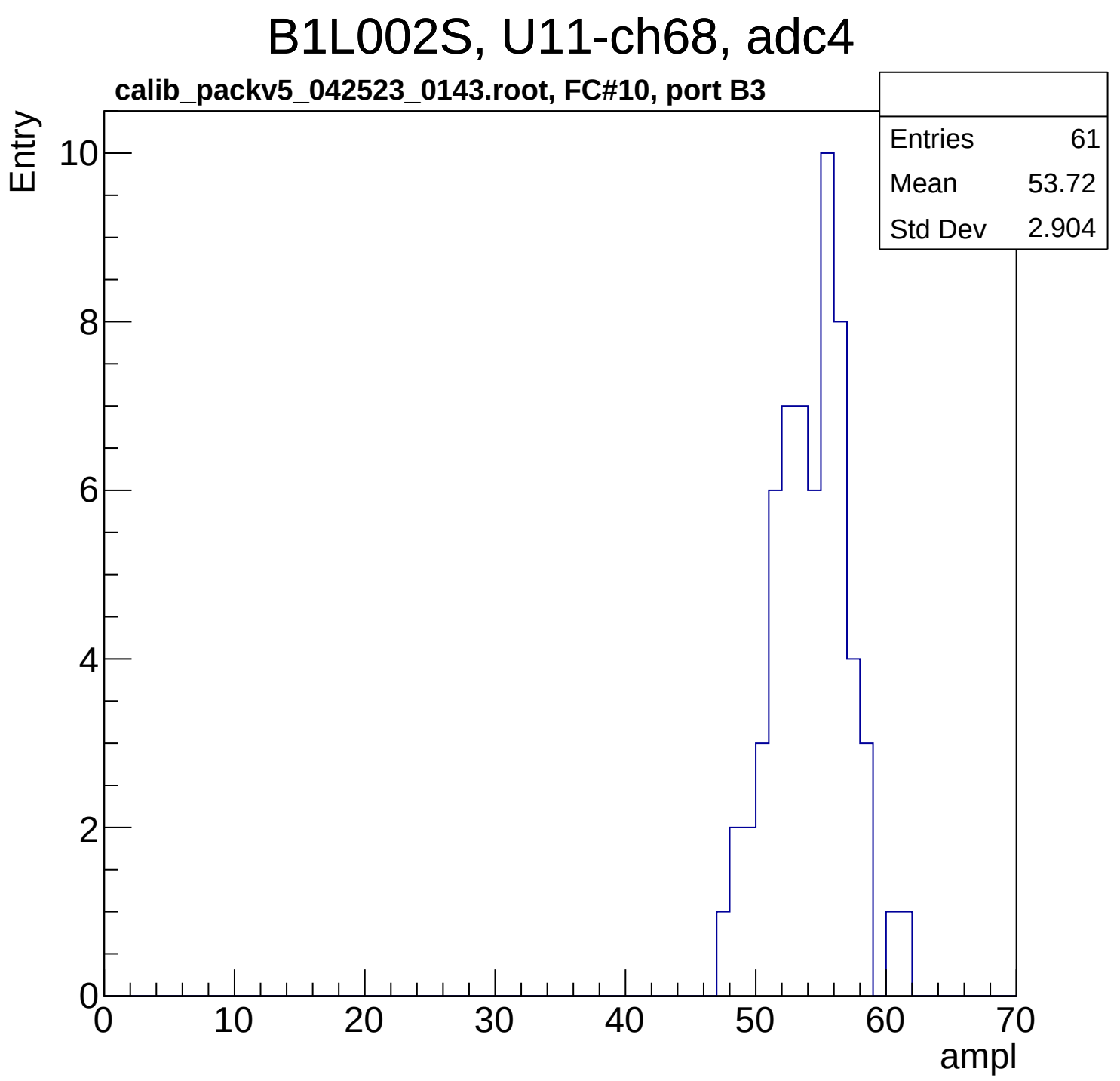
B1L002S, U11-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.72
Std Dev 2.904
Entry
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2
4
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10
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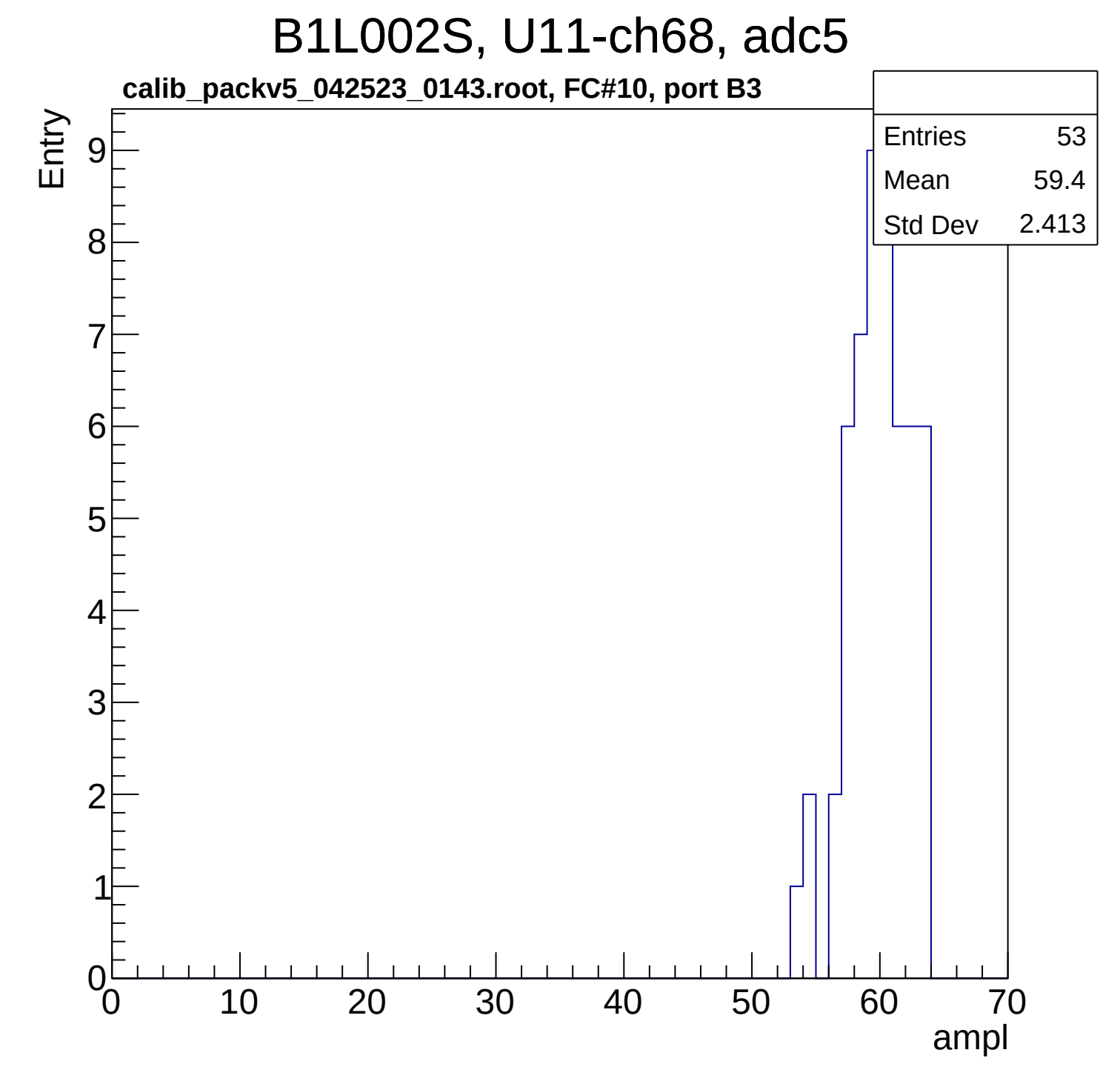
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.4
Std Dev 2.413
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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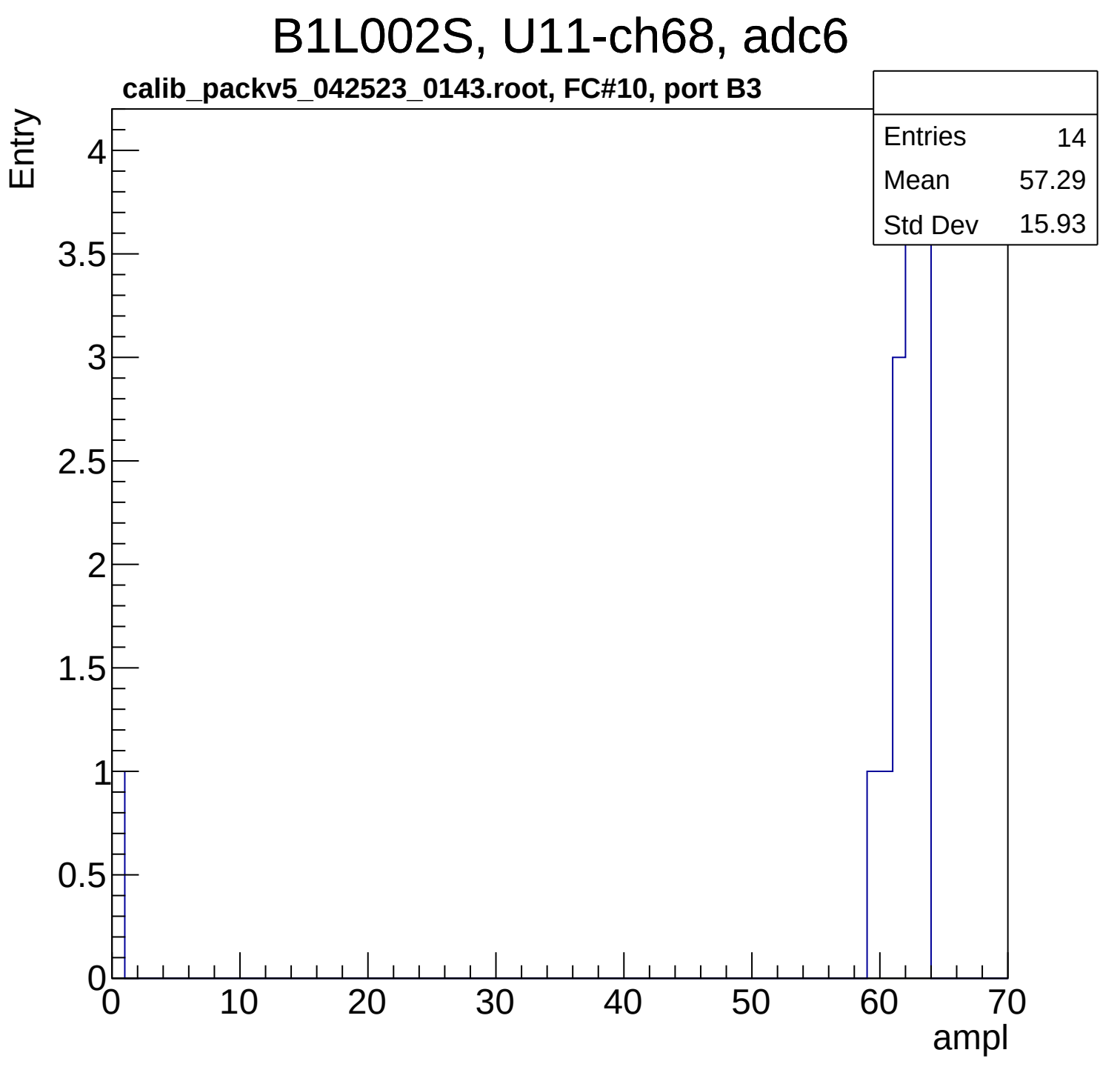
B1L002S, U11-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.29
Std Dev 15.93
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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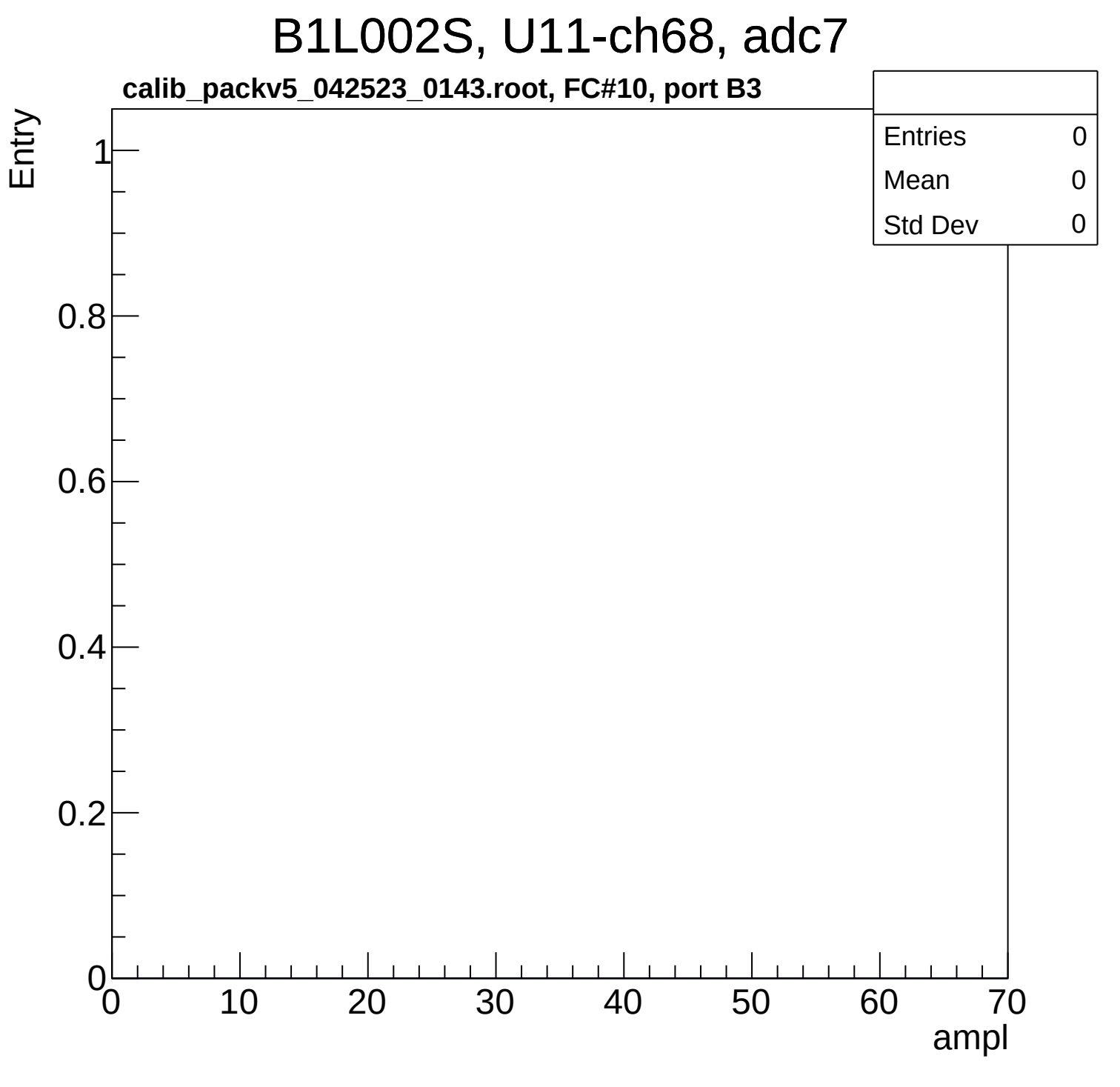
20
30
40
50
60
70


B1L002S, U11-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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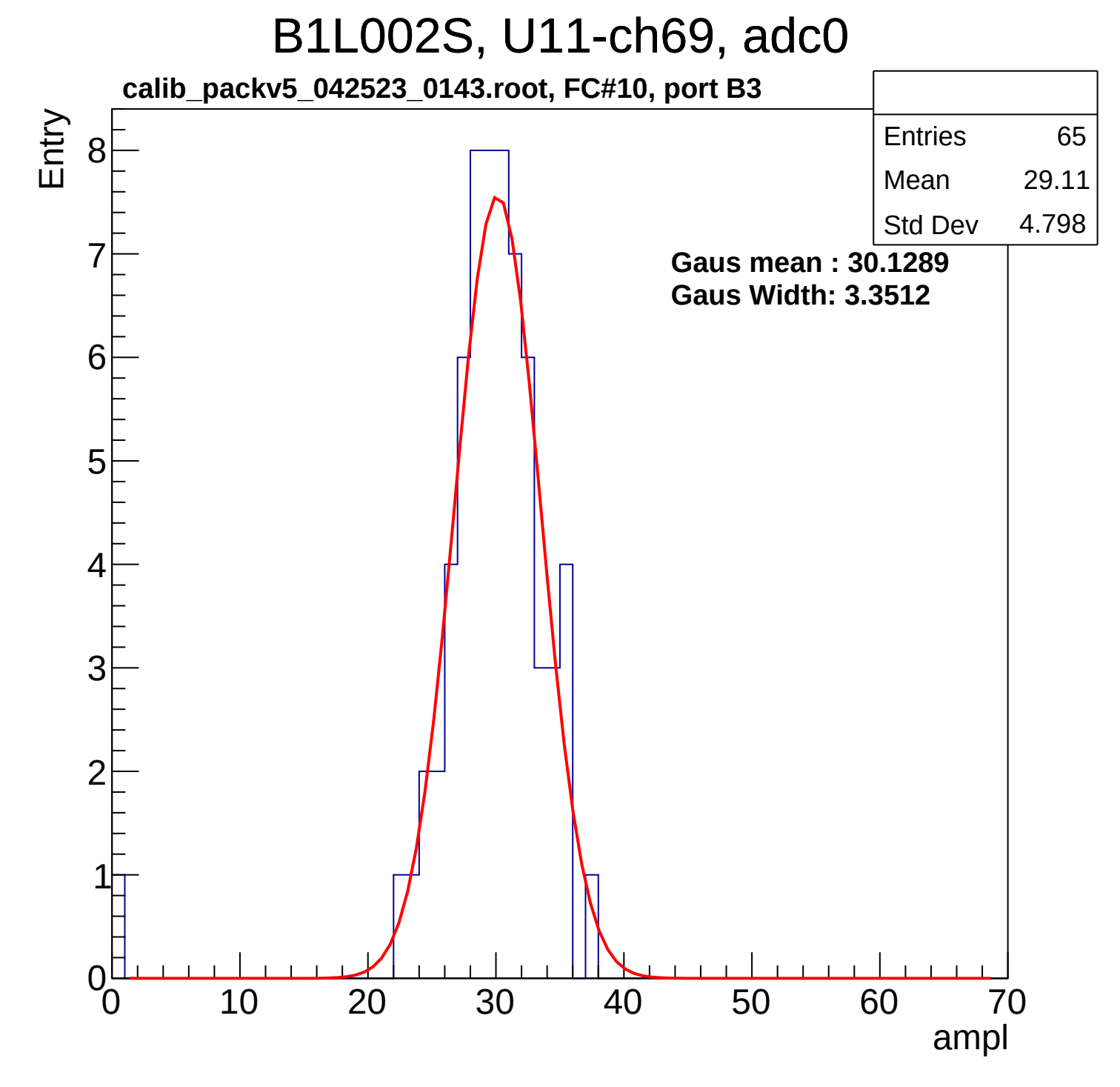
40
50
60
70
ampl


B1L002S, U11-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.11
Std Dev 4.798
Gaus mean : 30.1289
Gaus Width: 3.3512
Entry
ampl
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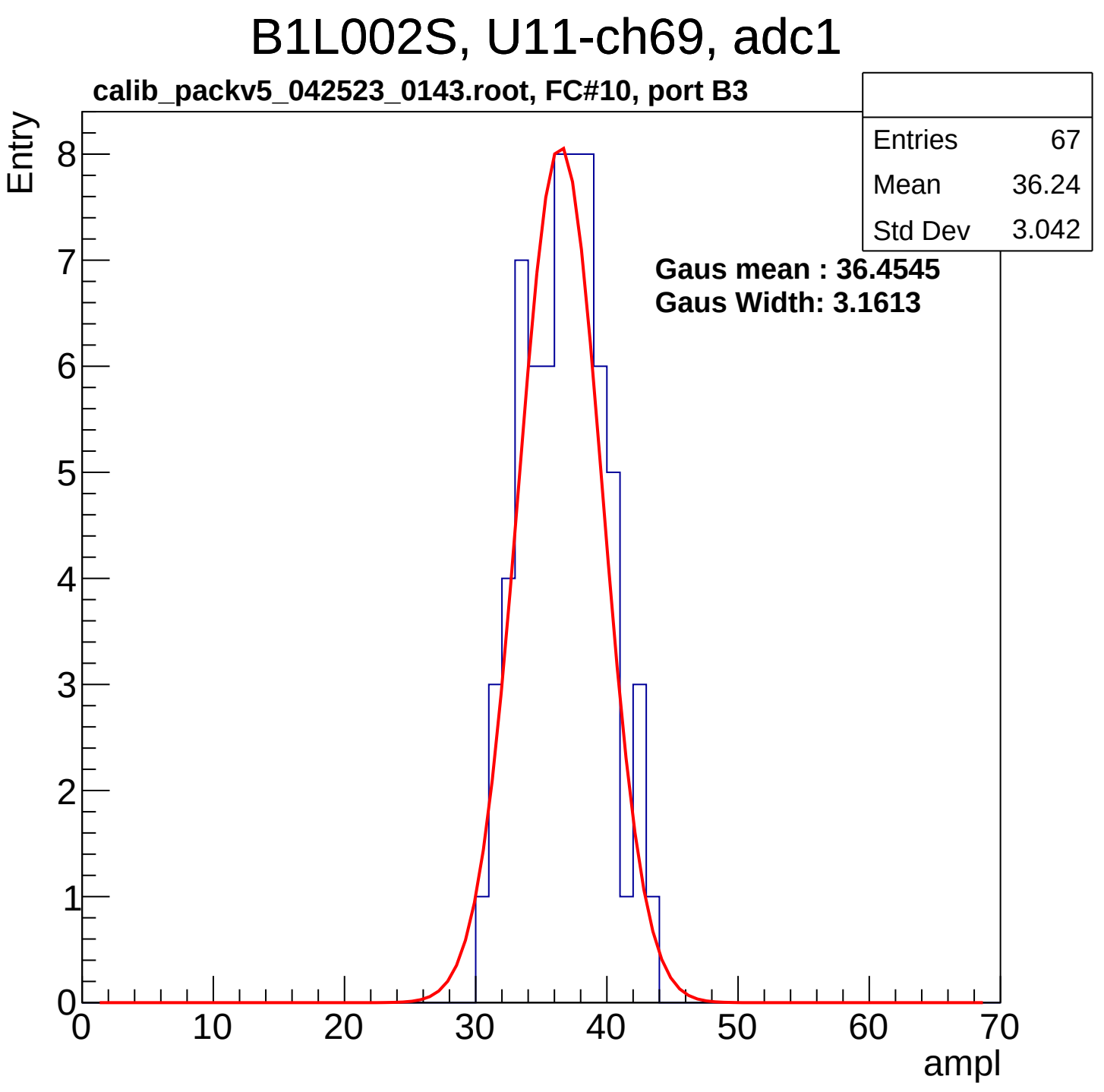
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 36.24
Std Dev 3.042
Gaus mean : 36.4545
Gaus Width: 3.1613
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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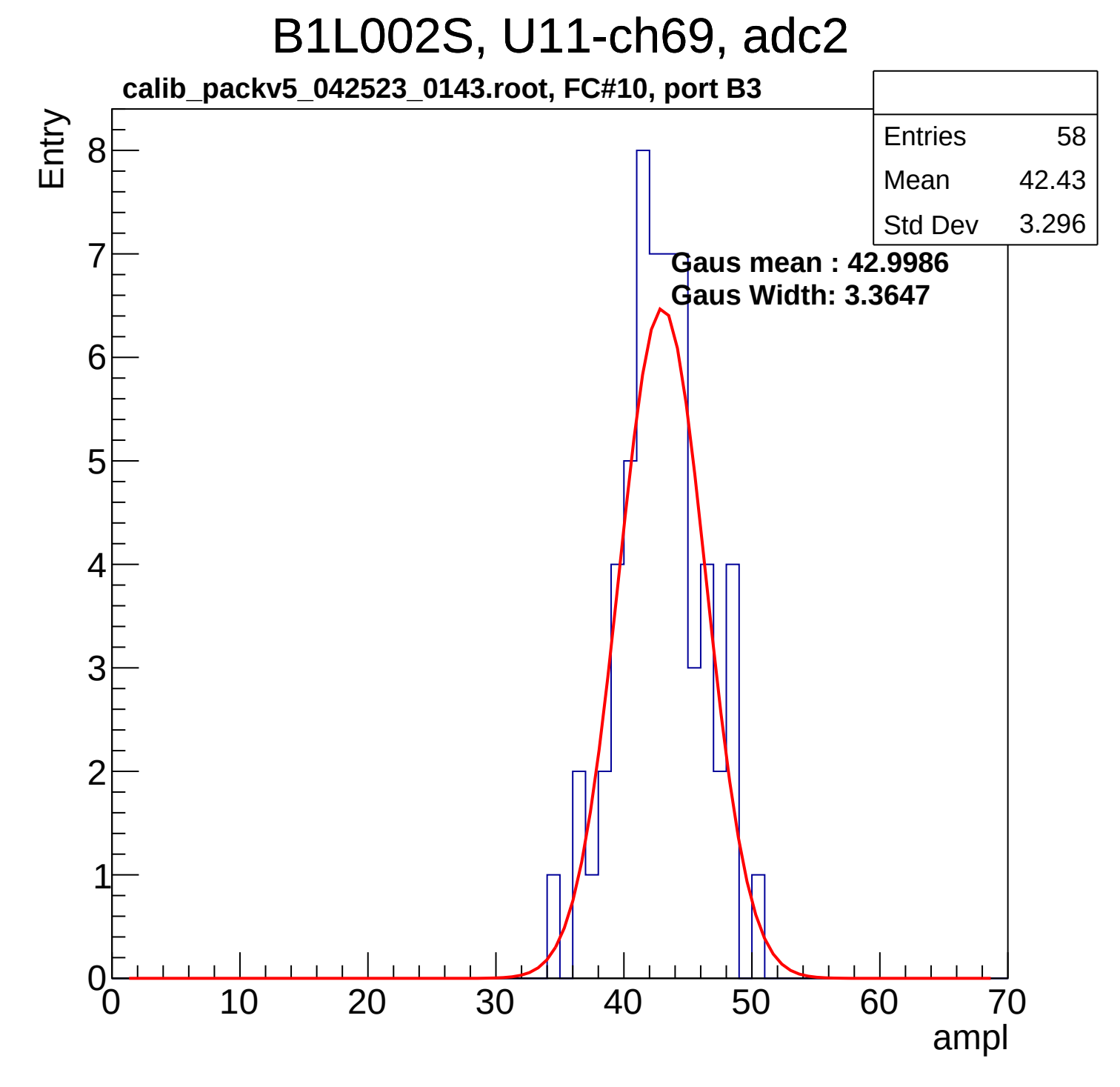
B1L002S, U11-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.43
Std Dev 3.296
Gaus mean : 42.9986
Gaus Width: 3.3647
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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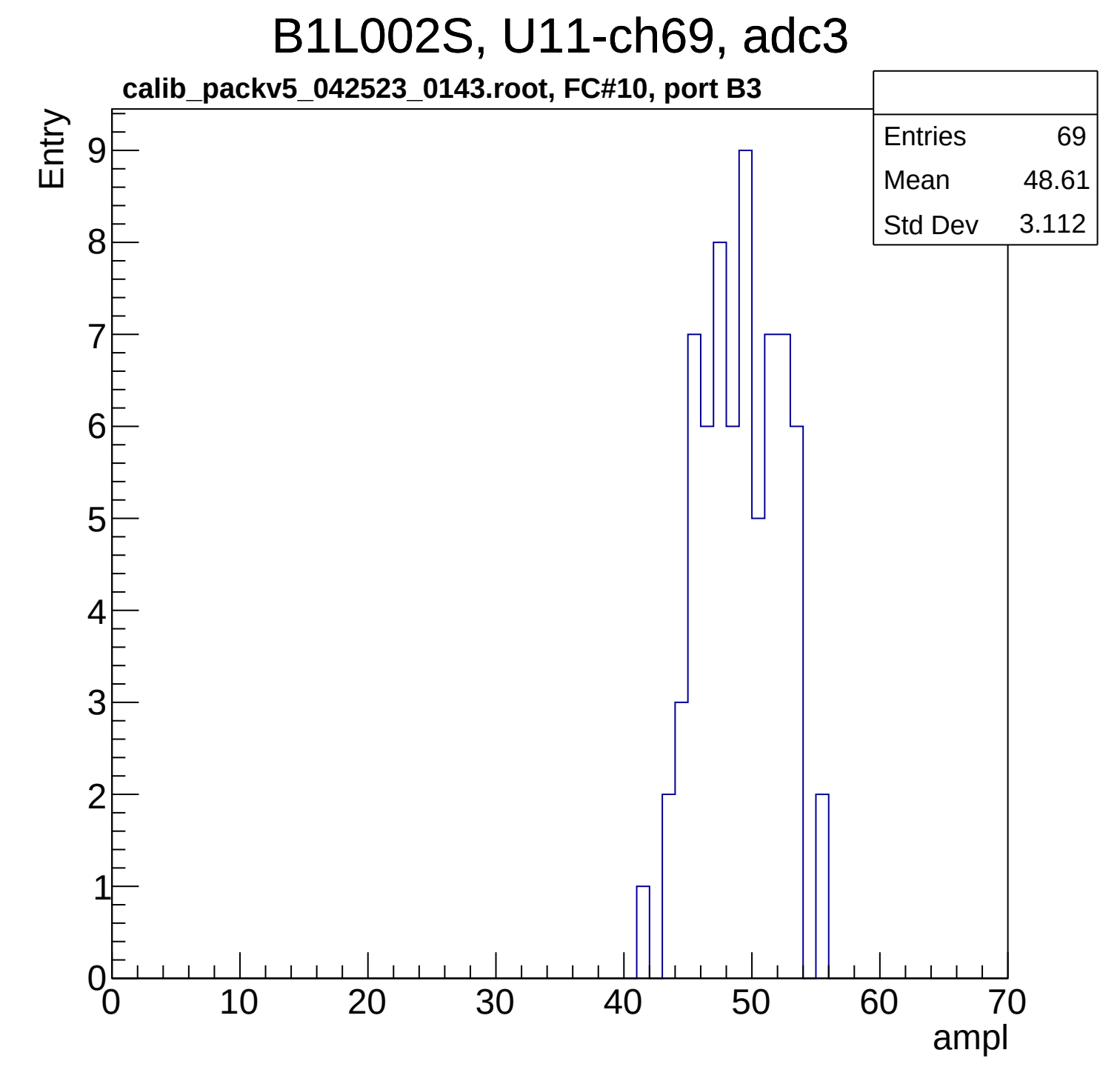
B1L002S, U11-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.61
Std Dev 3.112
Entry
ampl
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3
4
5
6
7
8
9
0
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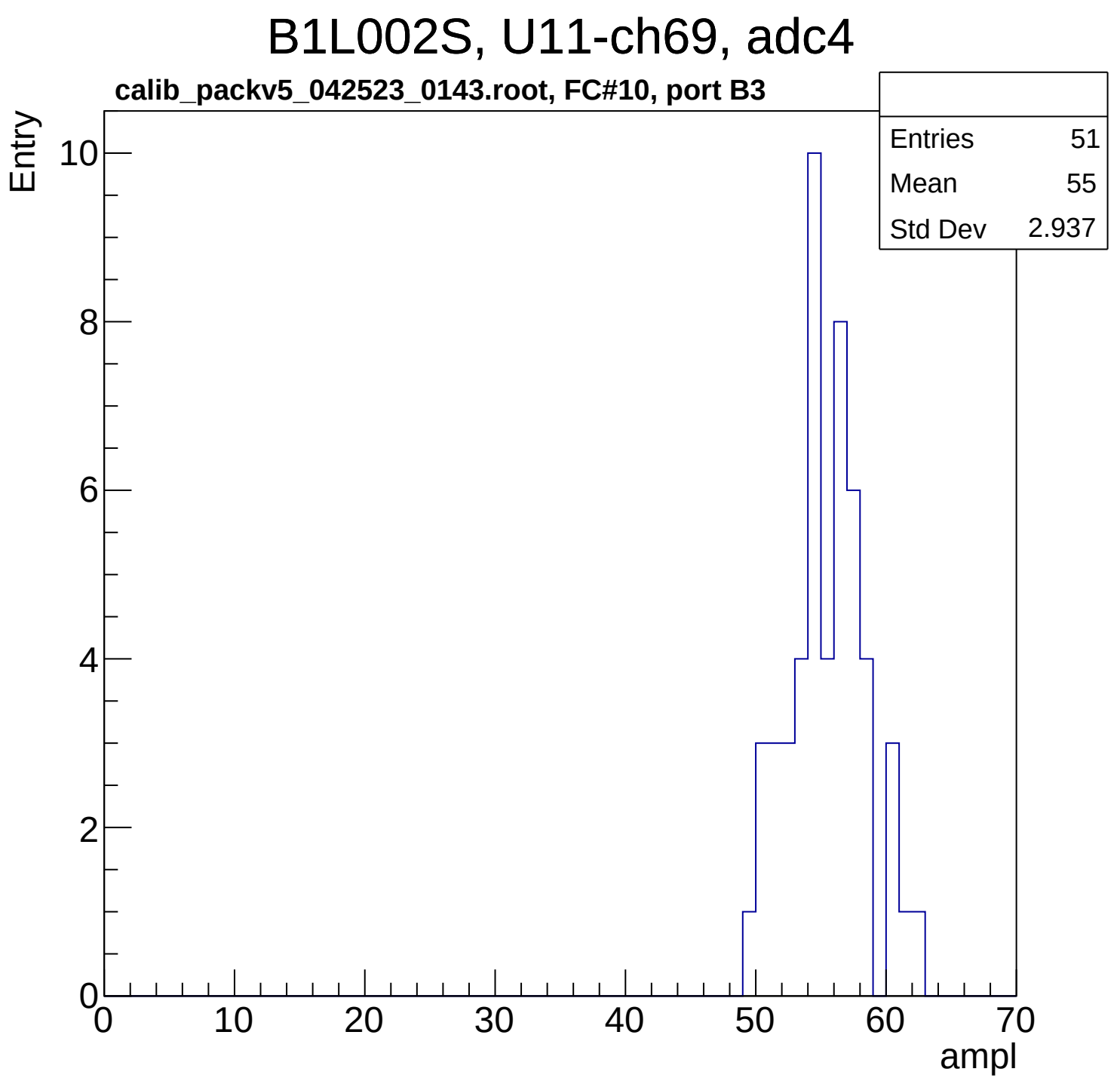
B1L002S, U11-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 55
Std Dev 2.937
Entry
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6
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10
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10
20
30
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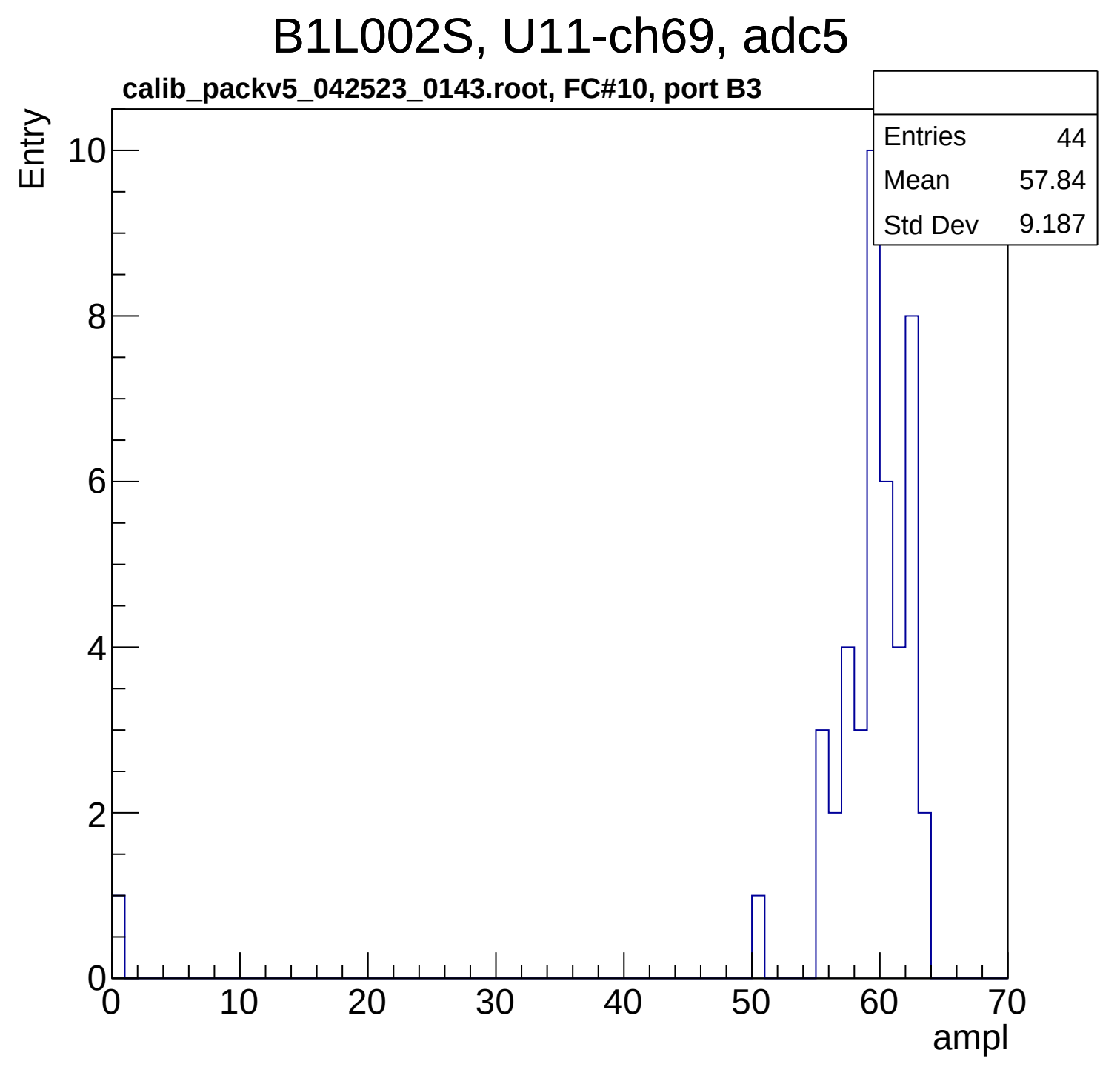
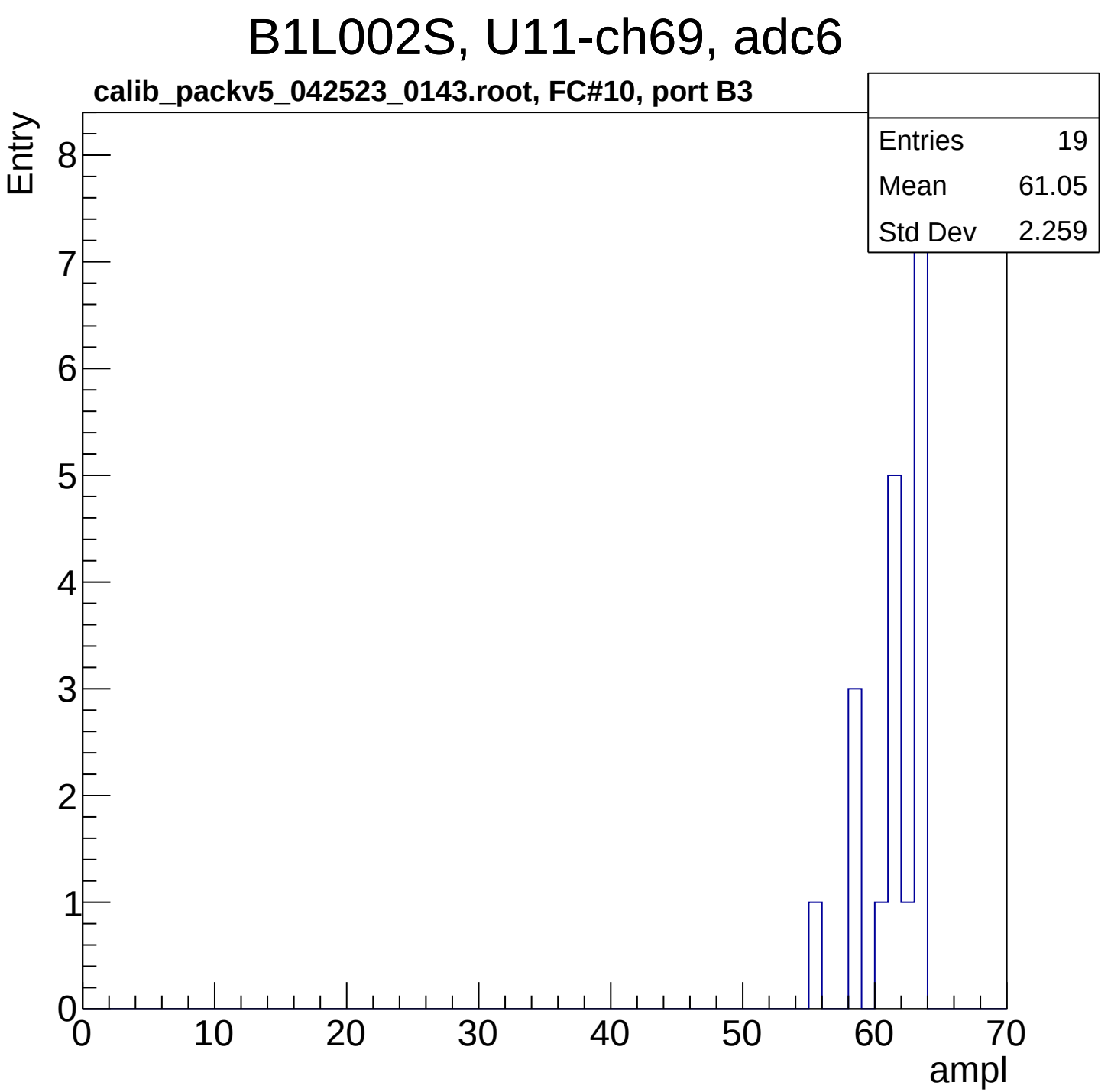
50
60
70
ampl


B1L002S, U11-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.84
Std Dev 9.187
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.05
Std Dev 2.259
Entry
ampl
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2
3
4
5
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7
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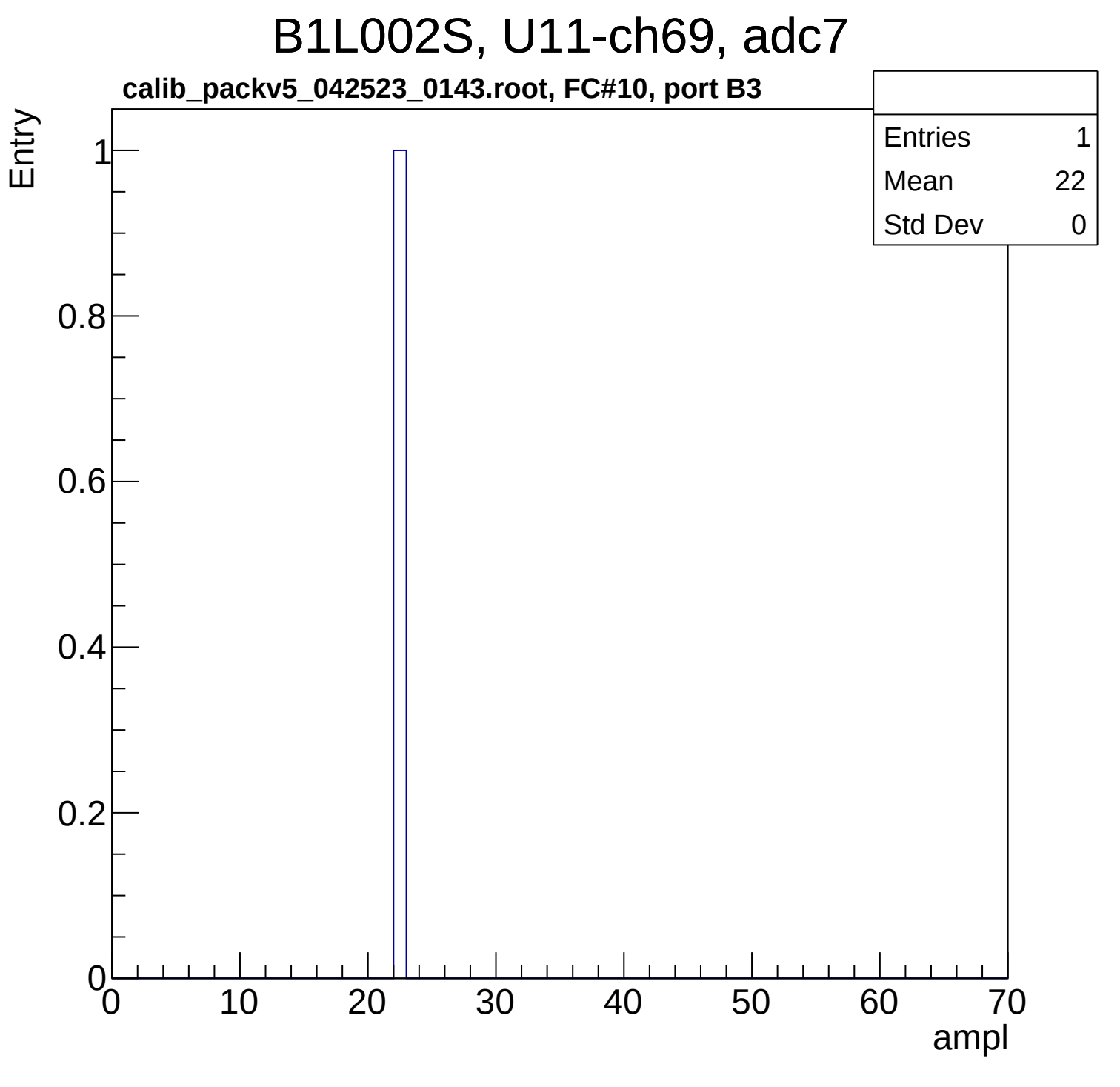
B1L002S, U11-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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10
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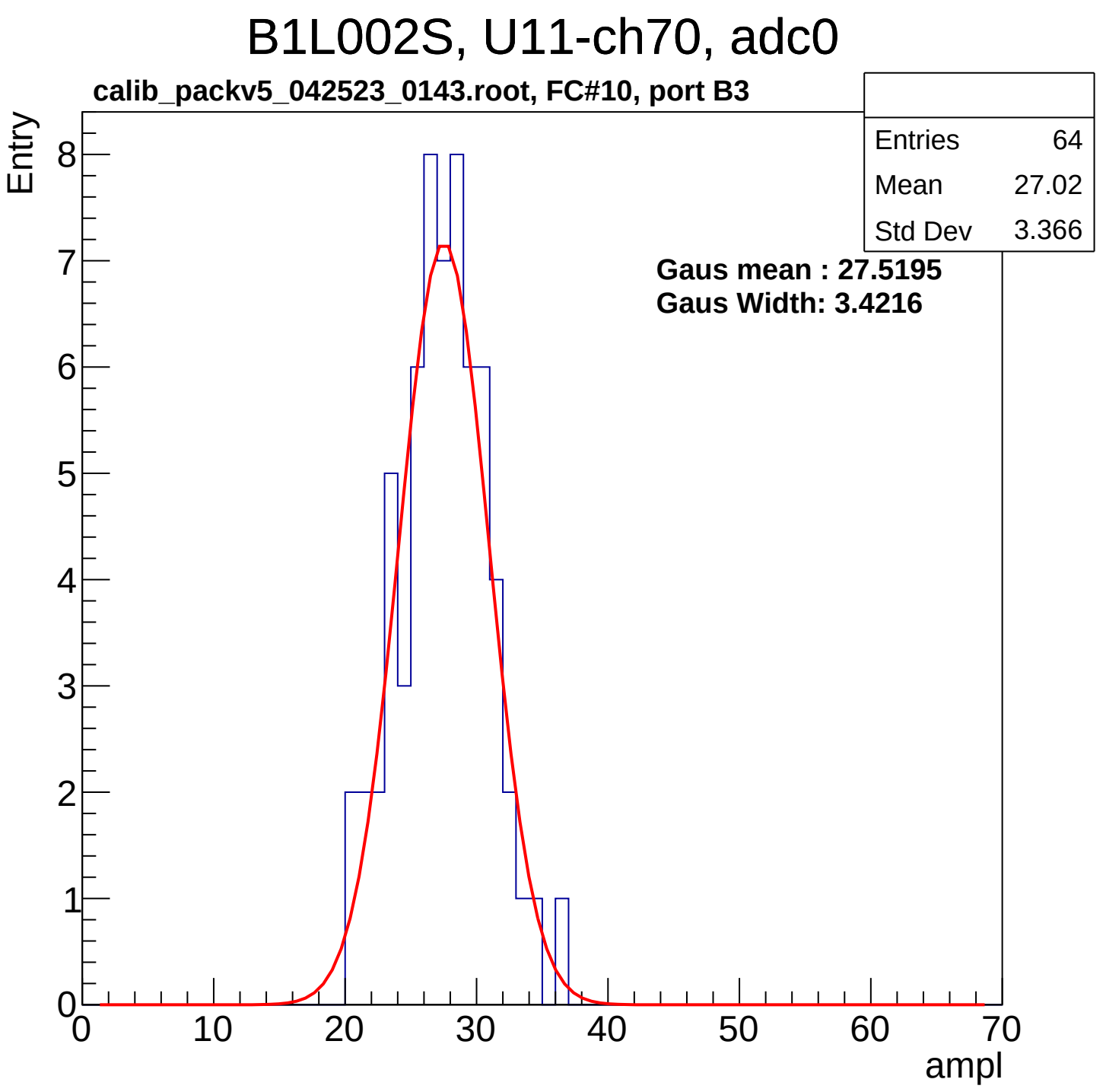
70
ampl


B1L002S, U11-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.02
Std Dev 3.366
Gaus mean : 27.5195
Gaus Width: 3.4216
Entry
ampl
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2
3
4
5
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0
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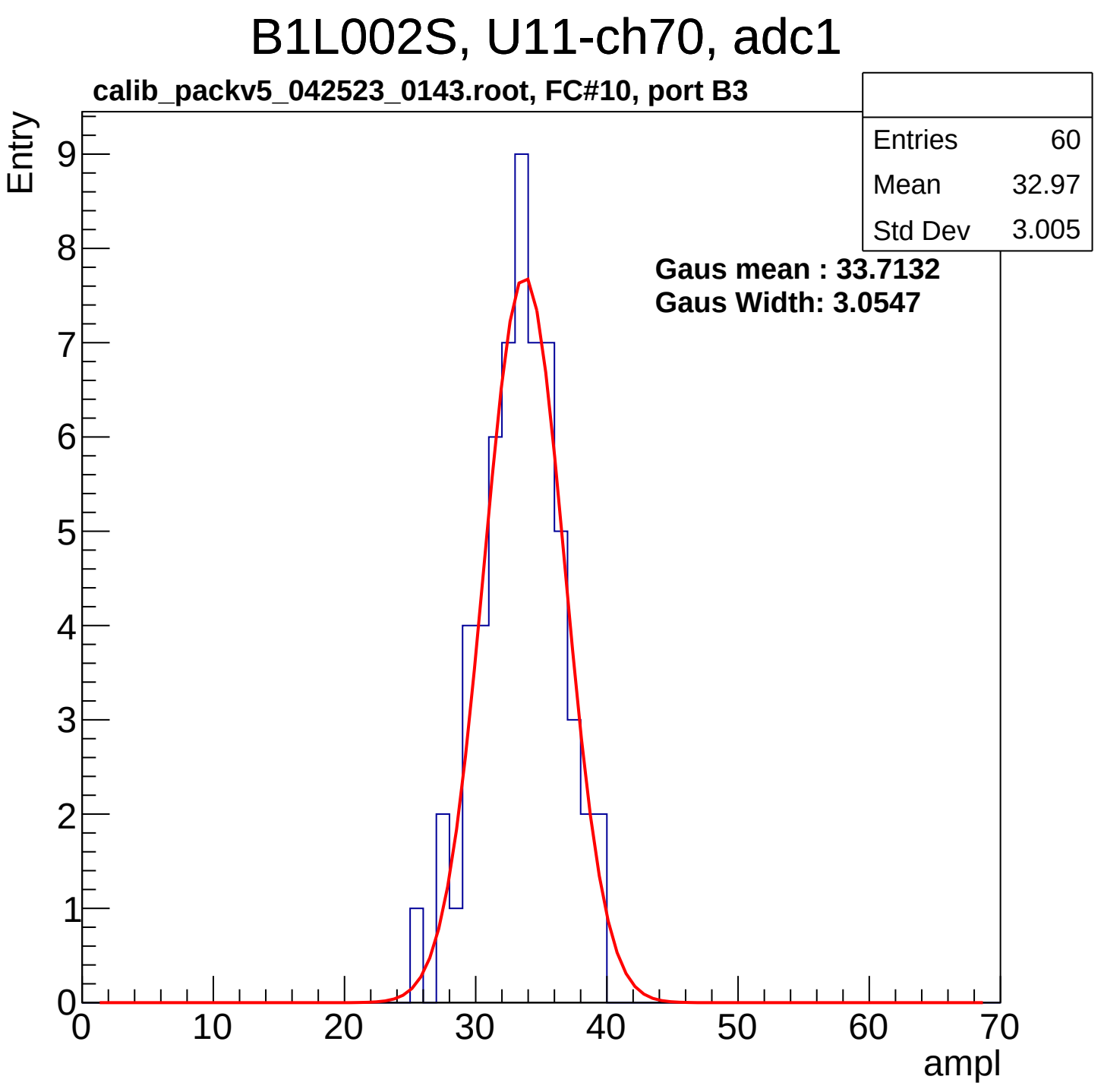
B1L002S, U11-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 32.97
Std Dev 3.005
Gaus mean : 33.7132
Gaus Width: 3.0547
Entry
ampl
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3
4
5
6
7
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0
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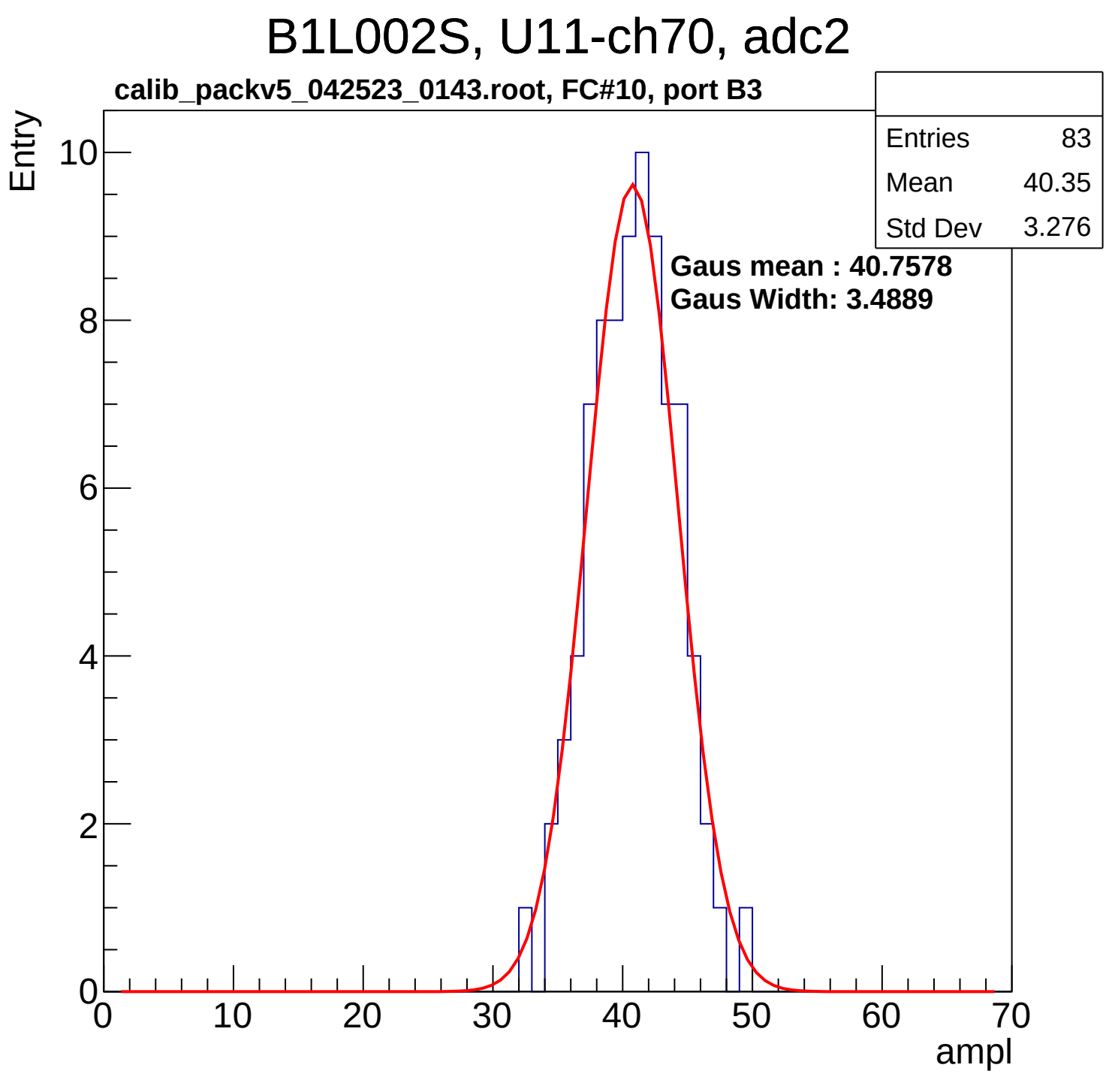
B1L002S, U11-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 40.35
Std Dev 3.276
Gaus mean : 40.7578
Gaus Width: 3.4889
Entry
ampl
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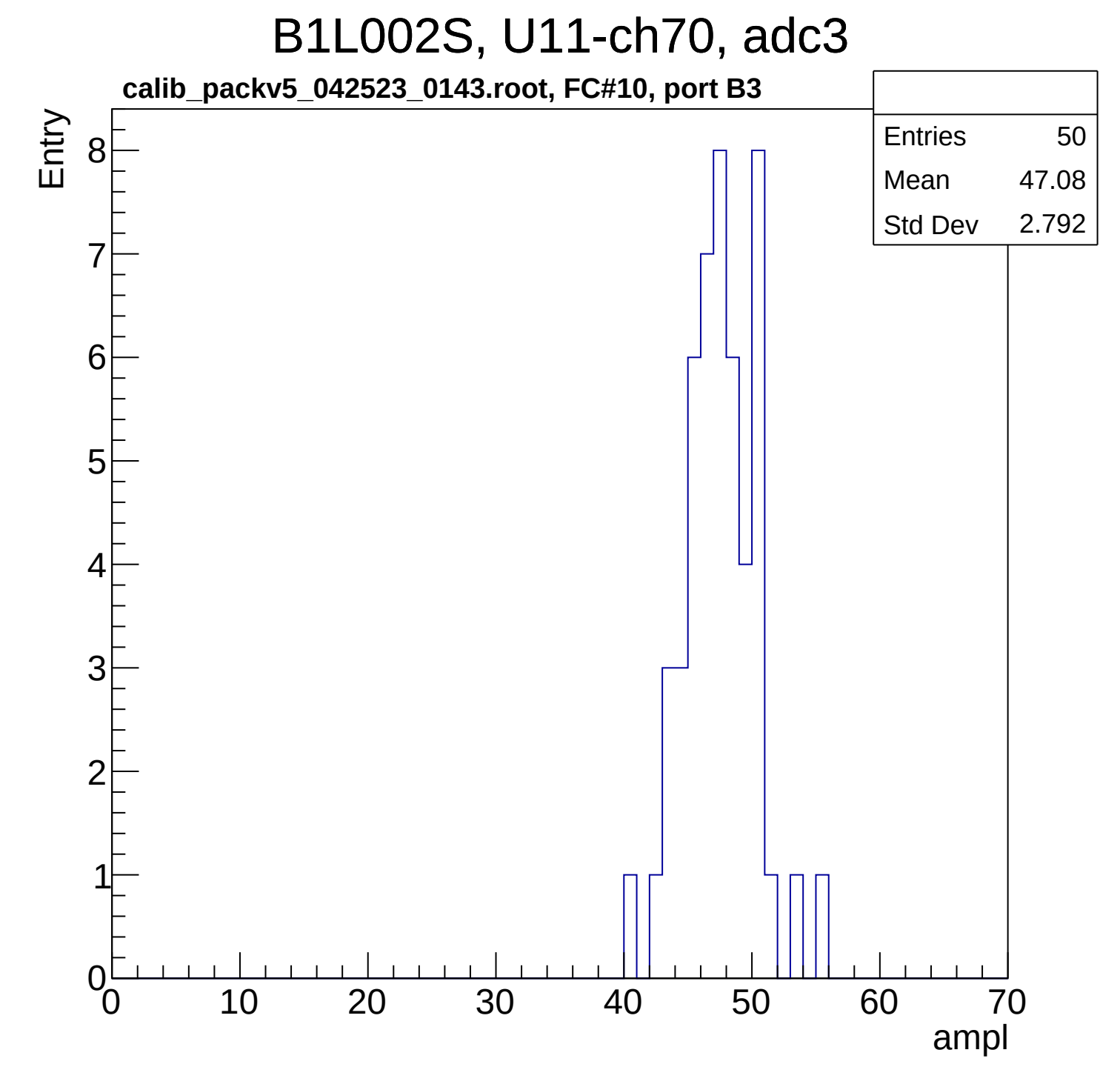
B1L002S, U11-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 47.08
Std Dev 2.792
Entry
ampl
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1
2
3
4
5
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0
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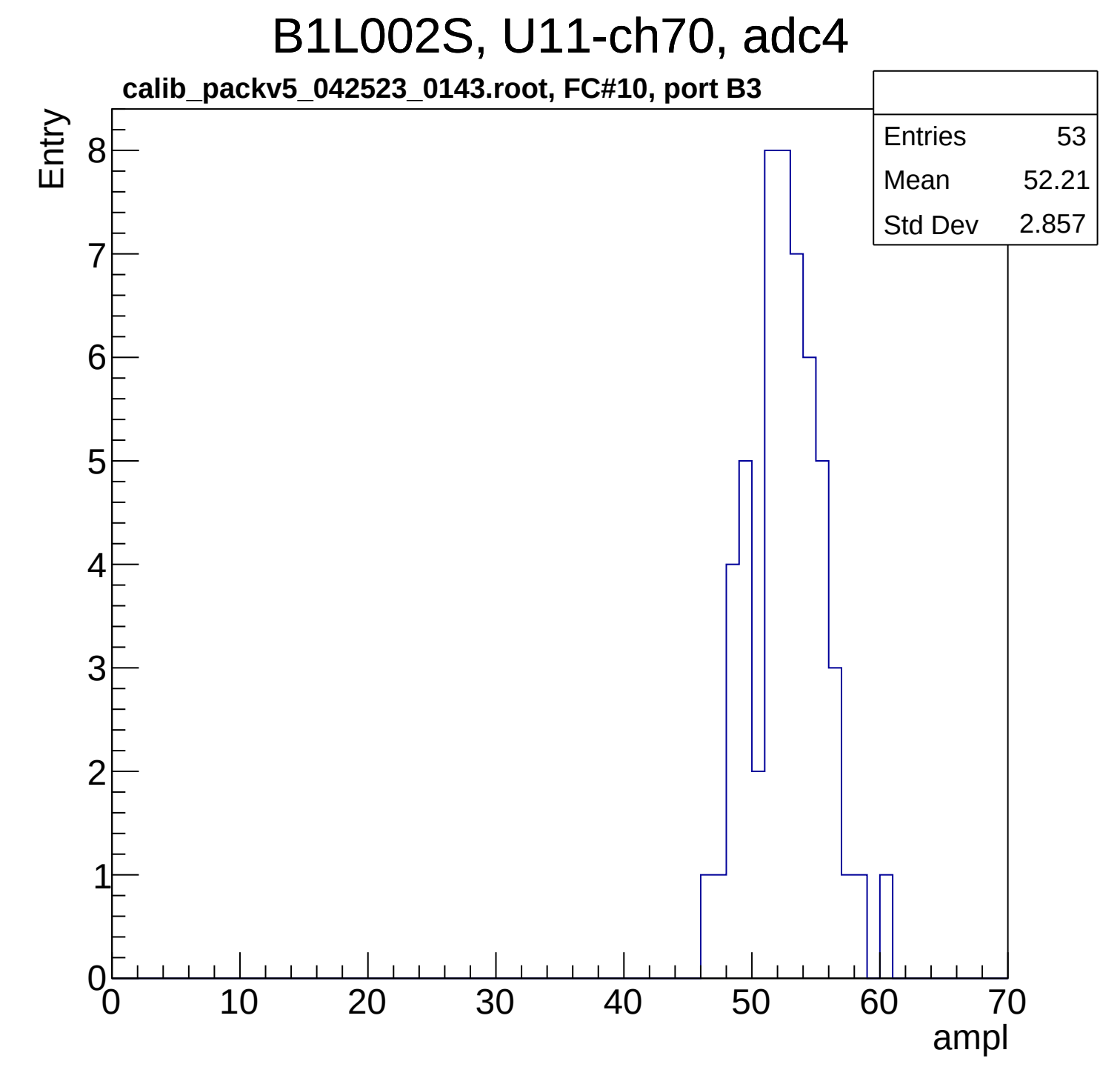
B1L002S, U11-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.21
Std Dev 2.857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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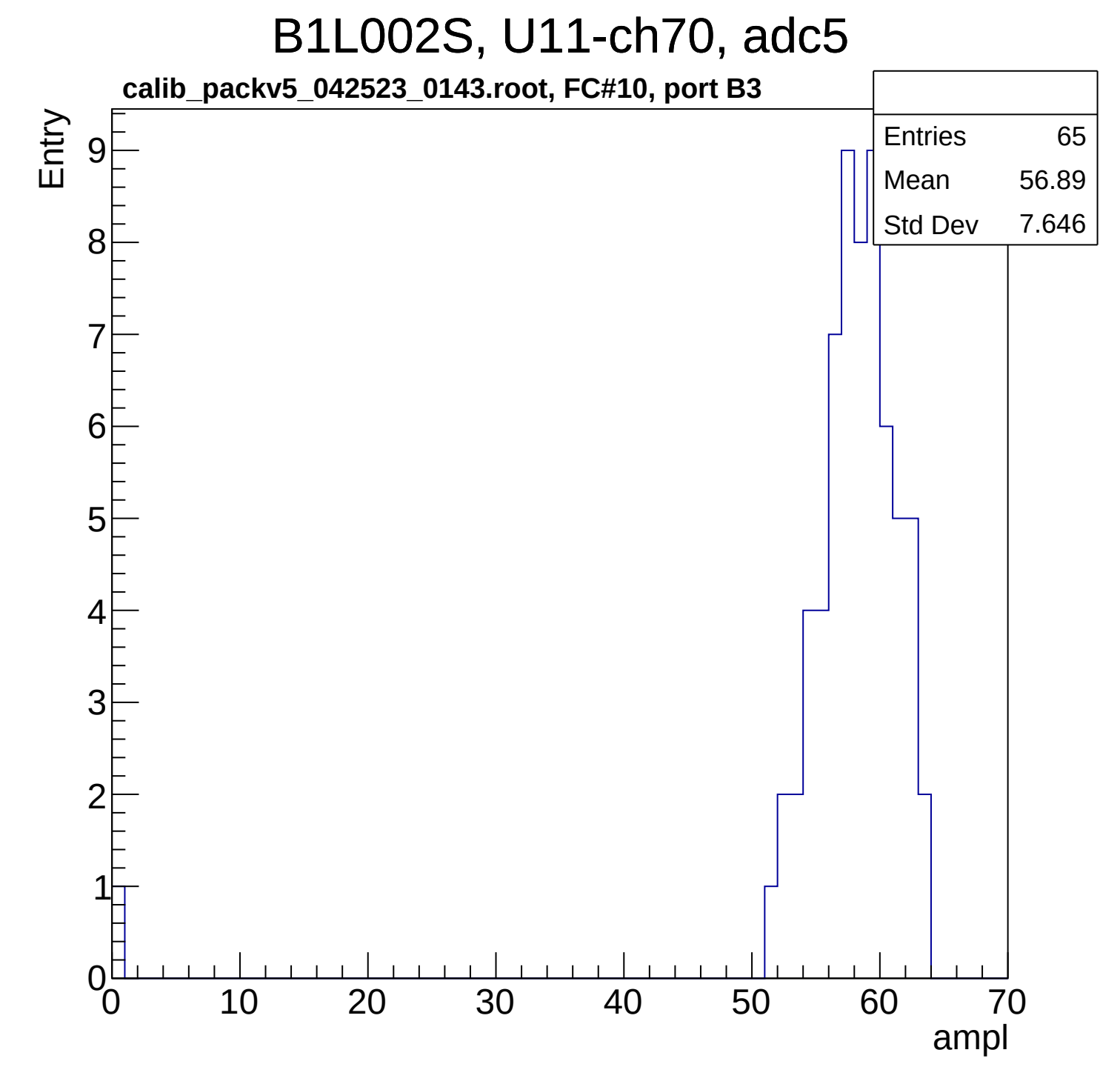
B1L002S, U11-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 56.89
Std Dev 7.646
Entry
ampl
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2
3
4
5
6
7
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0
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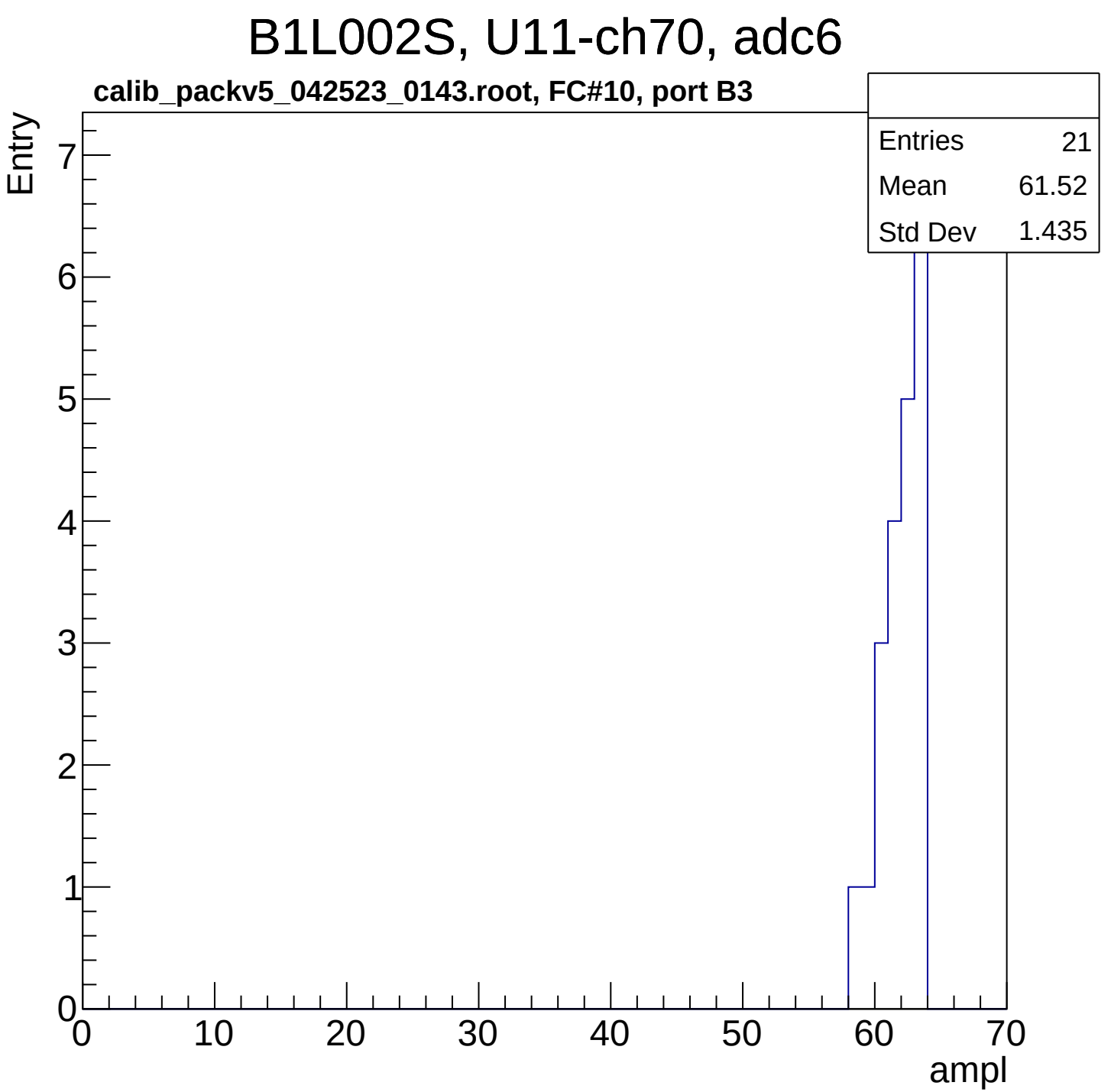
B1L002S, U11-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.52
Std Dev 1.435
Entry
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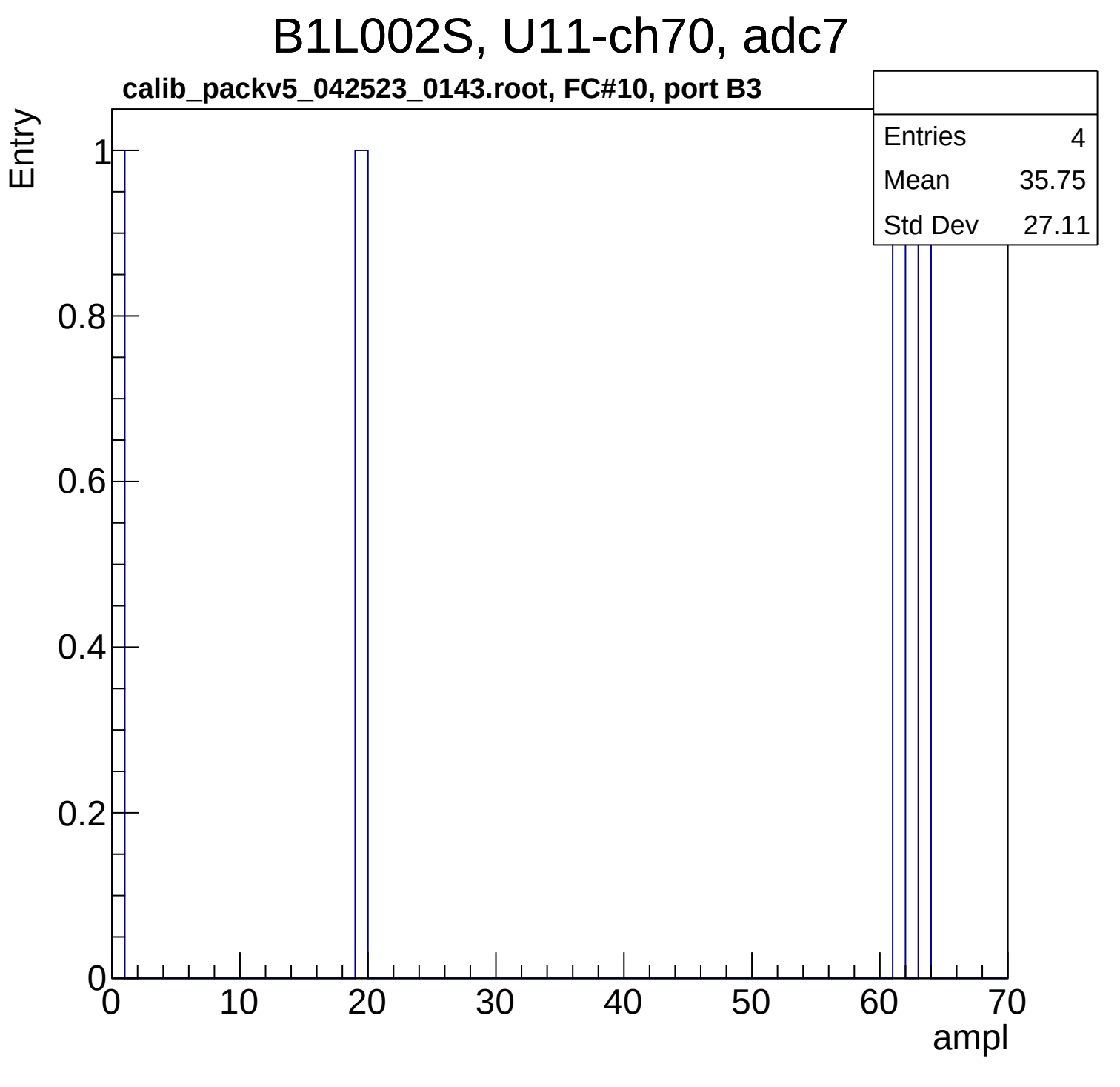
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 35.75
Std Dev 27.11
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
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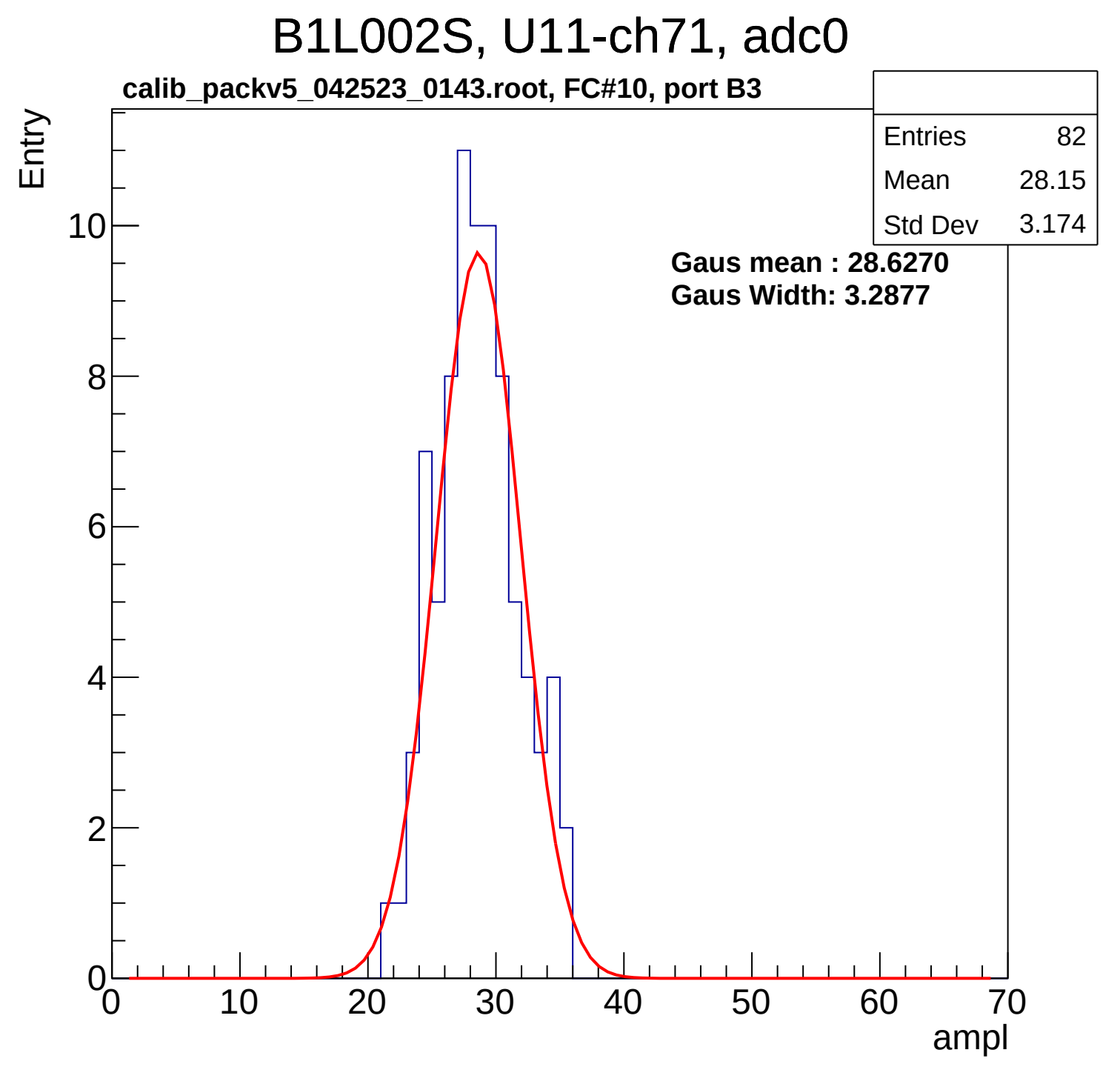
40
50
60
70


B1L002S, U11-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 28.15
Std Dev 3.174
Gaus mean : 28.6270
Gaus Width: 3.2877
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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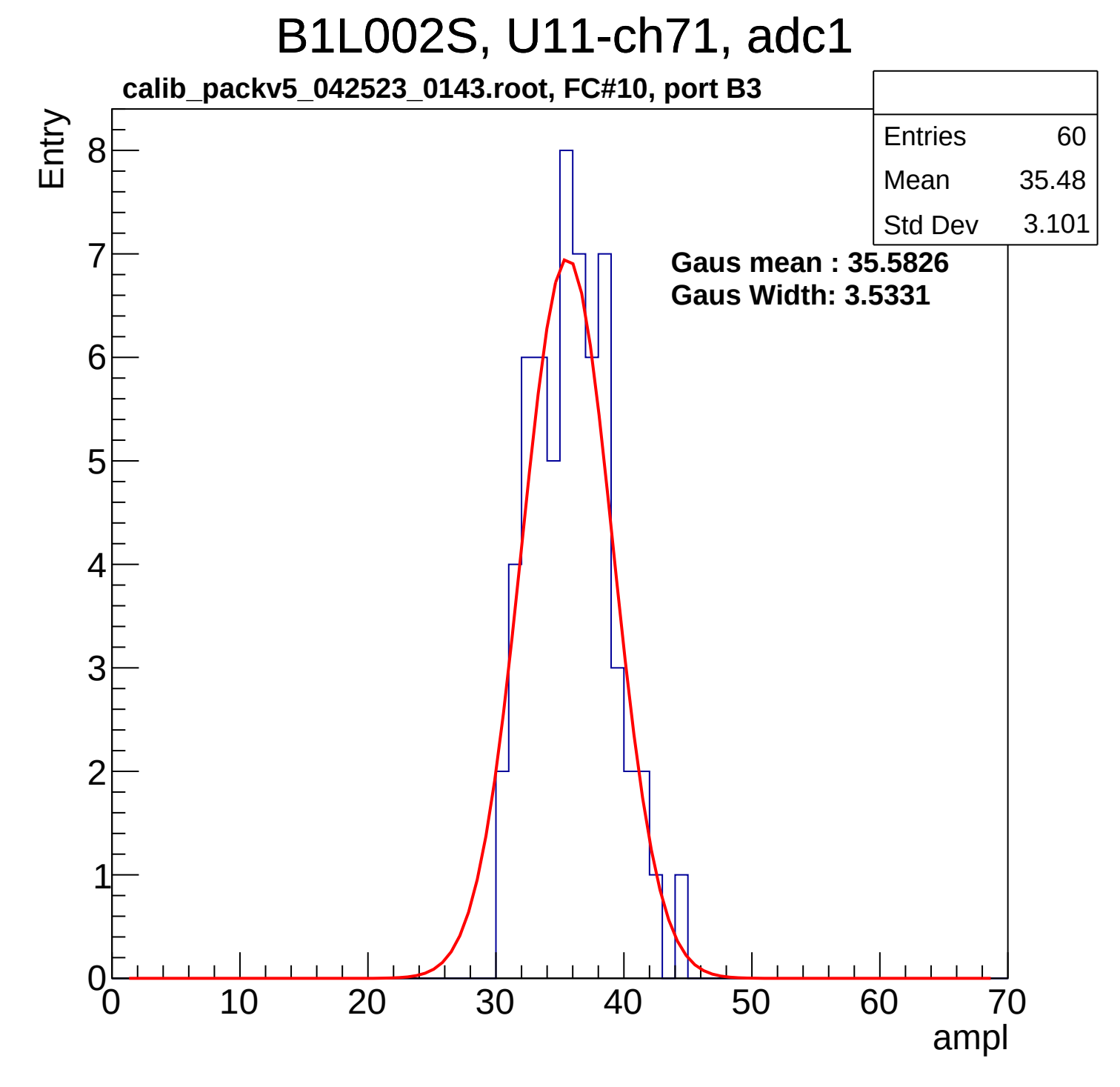
B1L002S, U11-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.48
Std Dev 3.101
Gaus mean : 35.5826
Gaus Width: 3.5331
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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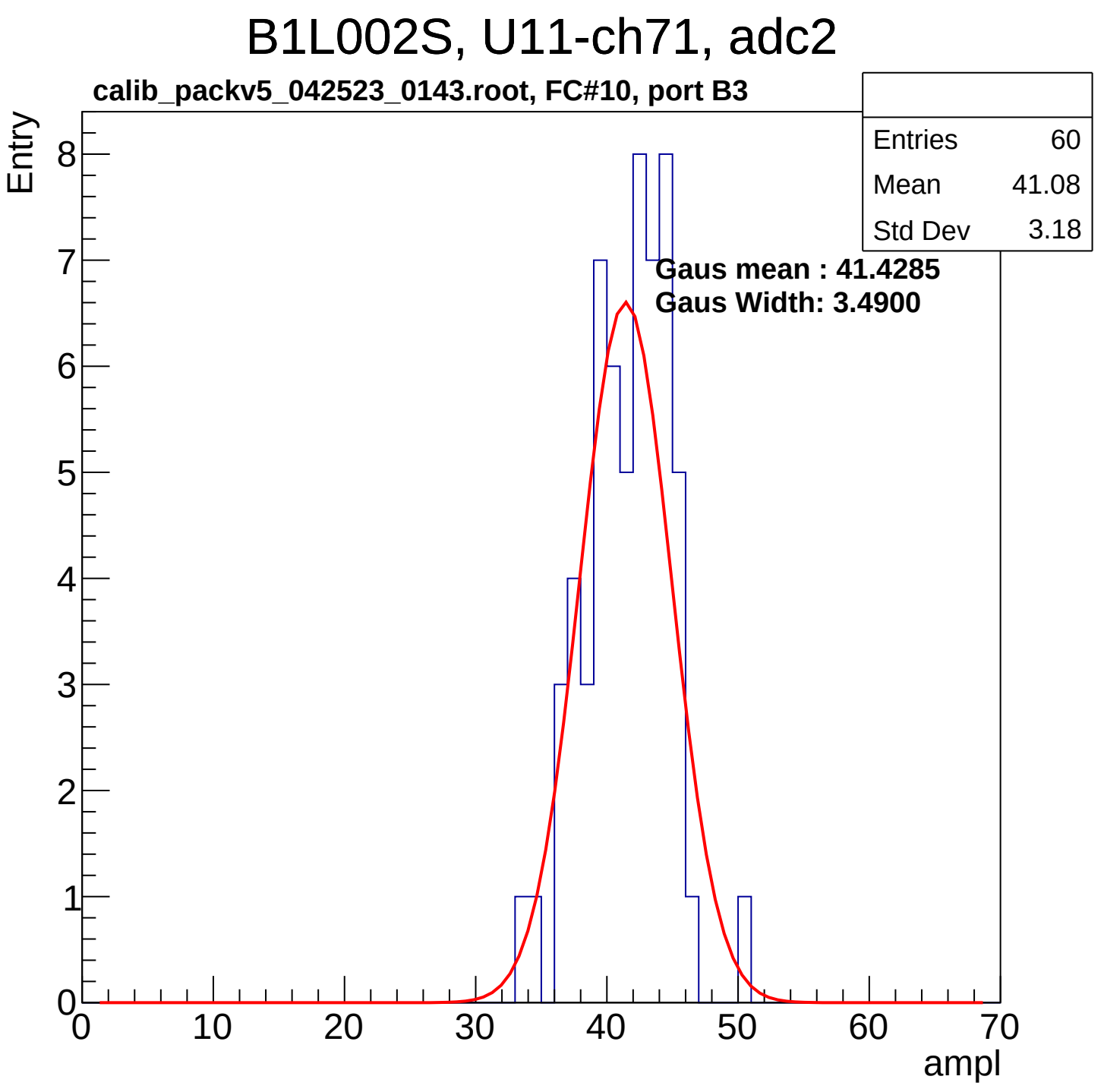
B1L002S, U11-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.08
Std Dev 3.18
Gaus mean : 41.4285
Gaus Width: 3.4900
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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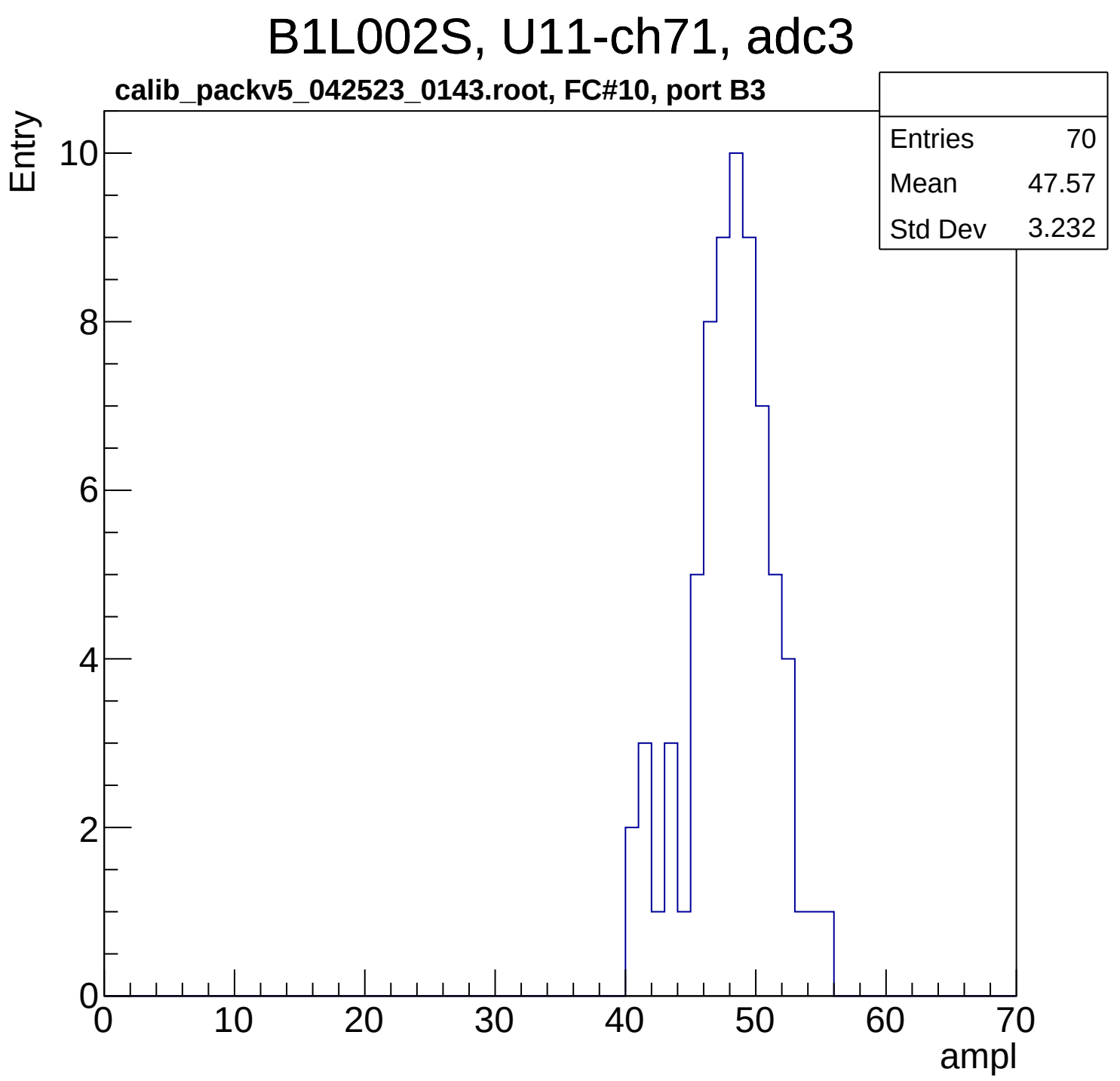
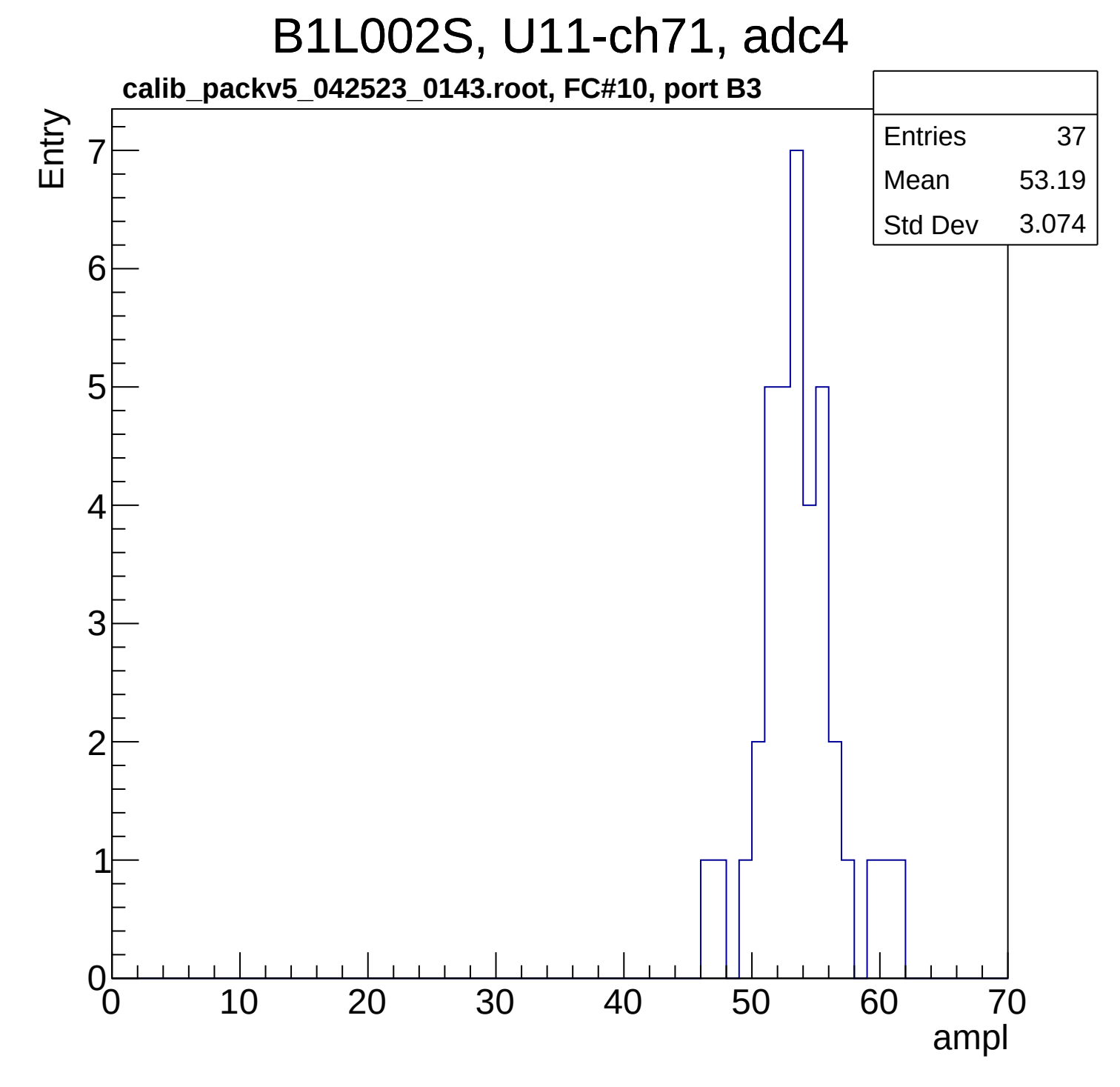

B1L002S, U11-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.57
Std Dev 3.232
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 53.19
Std Dev 3.074
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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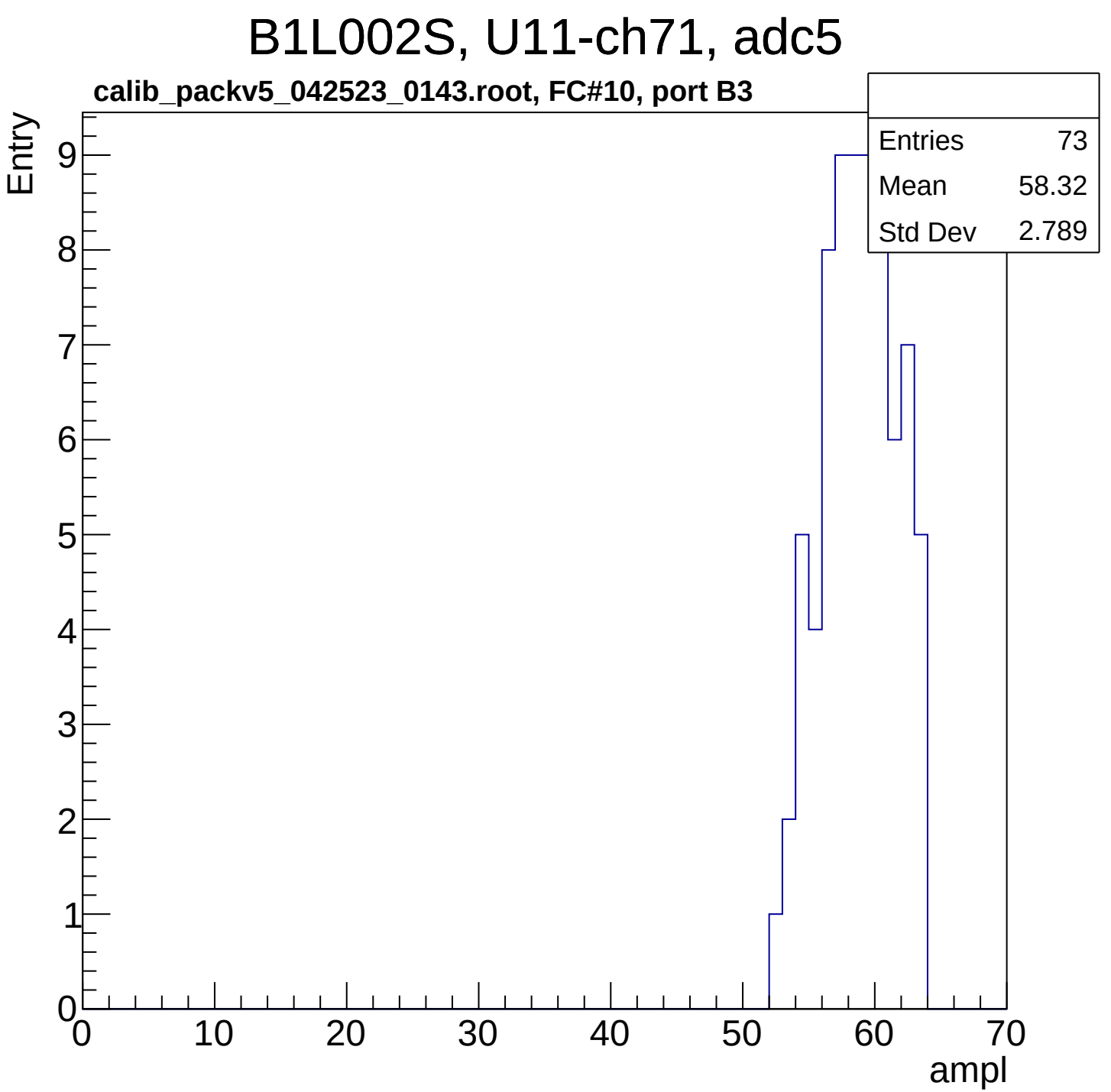
B1L002S, U11-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 58.32
Std Dev 2.789
Entry
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2
3
4
5
6
7
8
9
0
10
20
30
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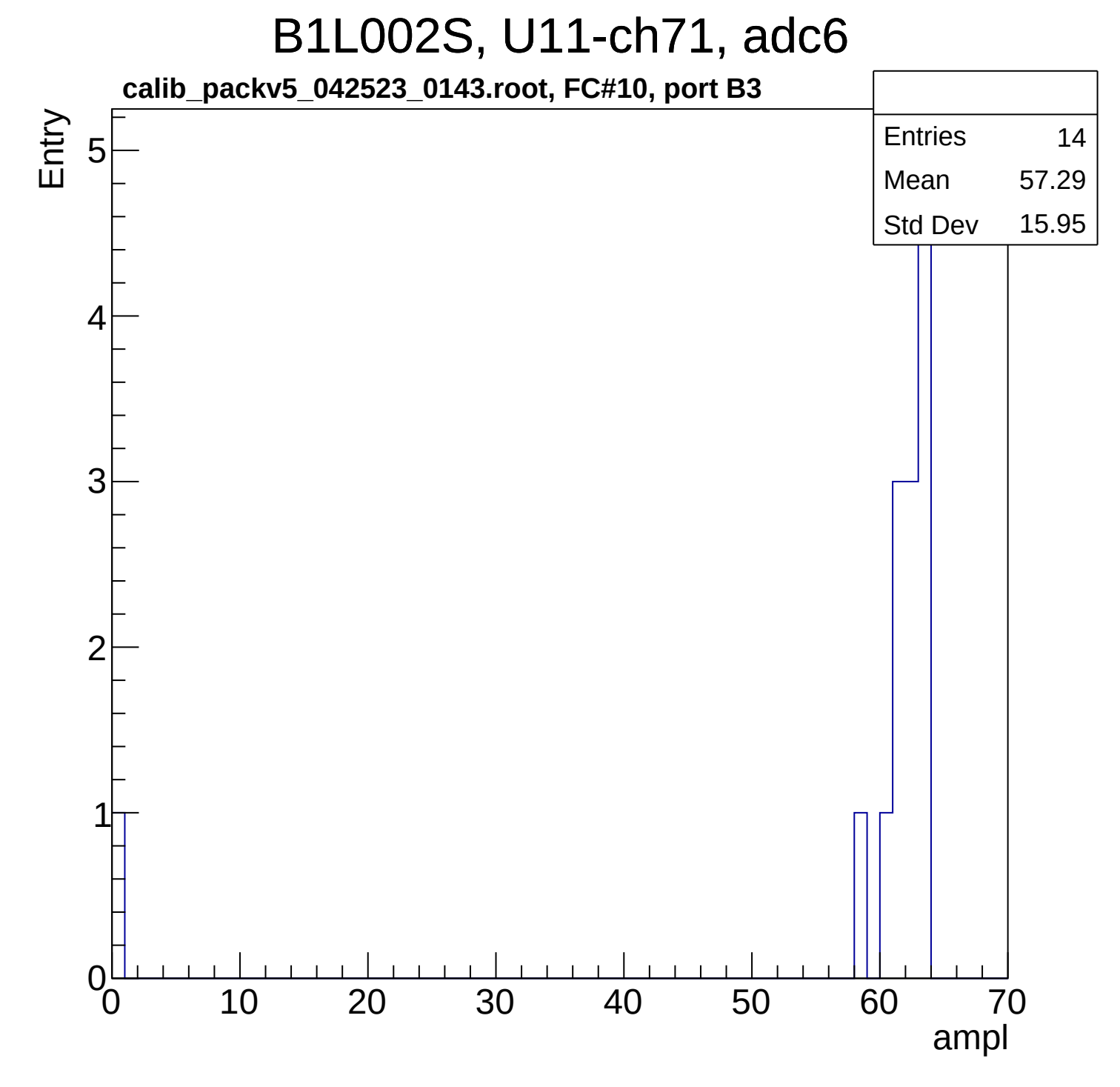
50
60
70
ampl


B1L002S, U11-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.29
Std Dev 15.95
Entry
0
1
2
3
4
5
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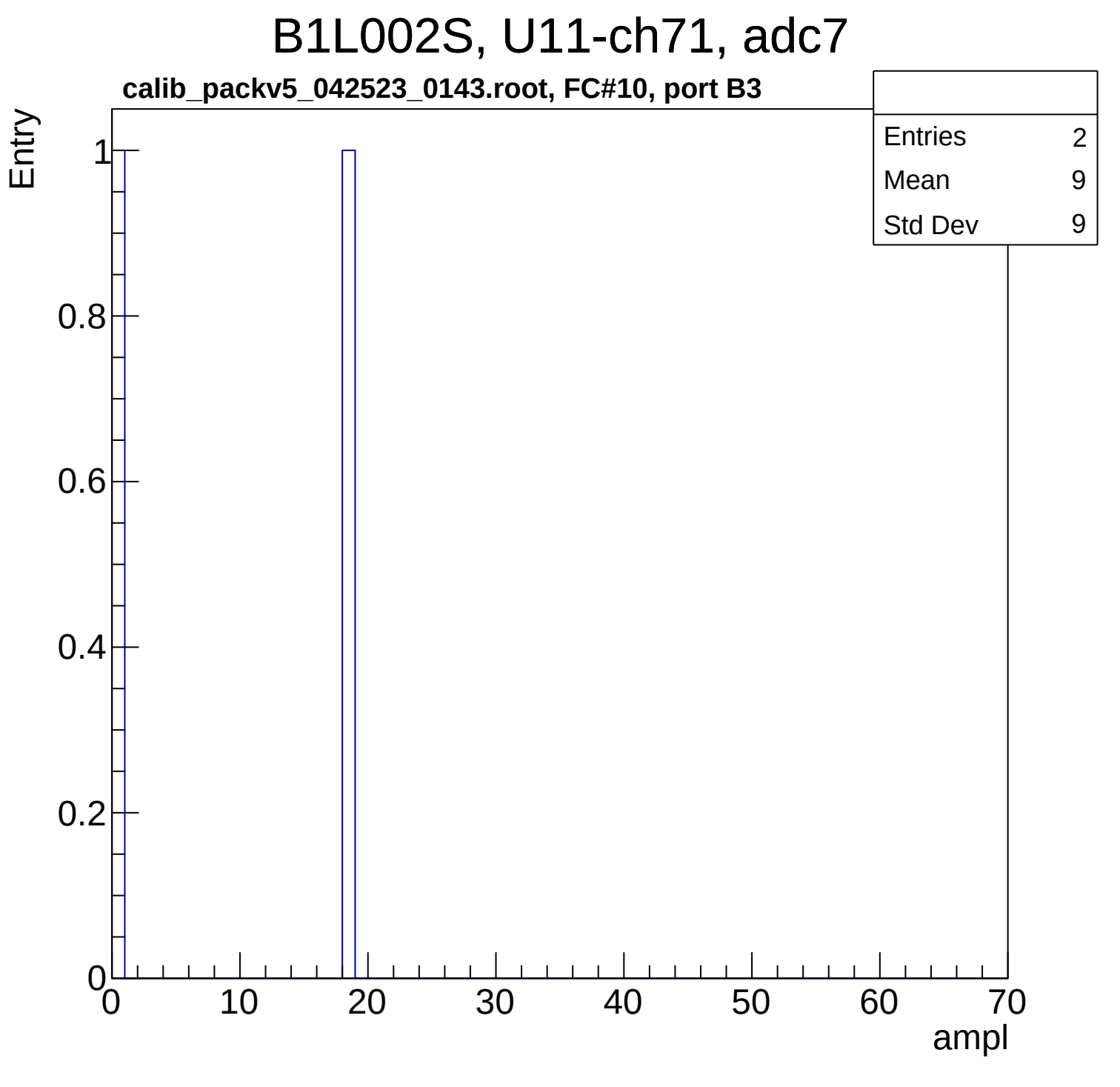
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9
Std Dev 9
Entry
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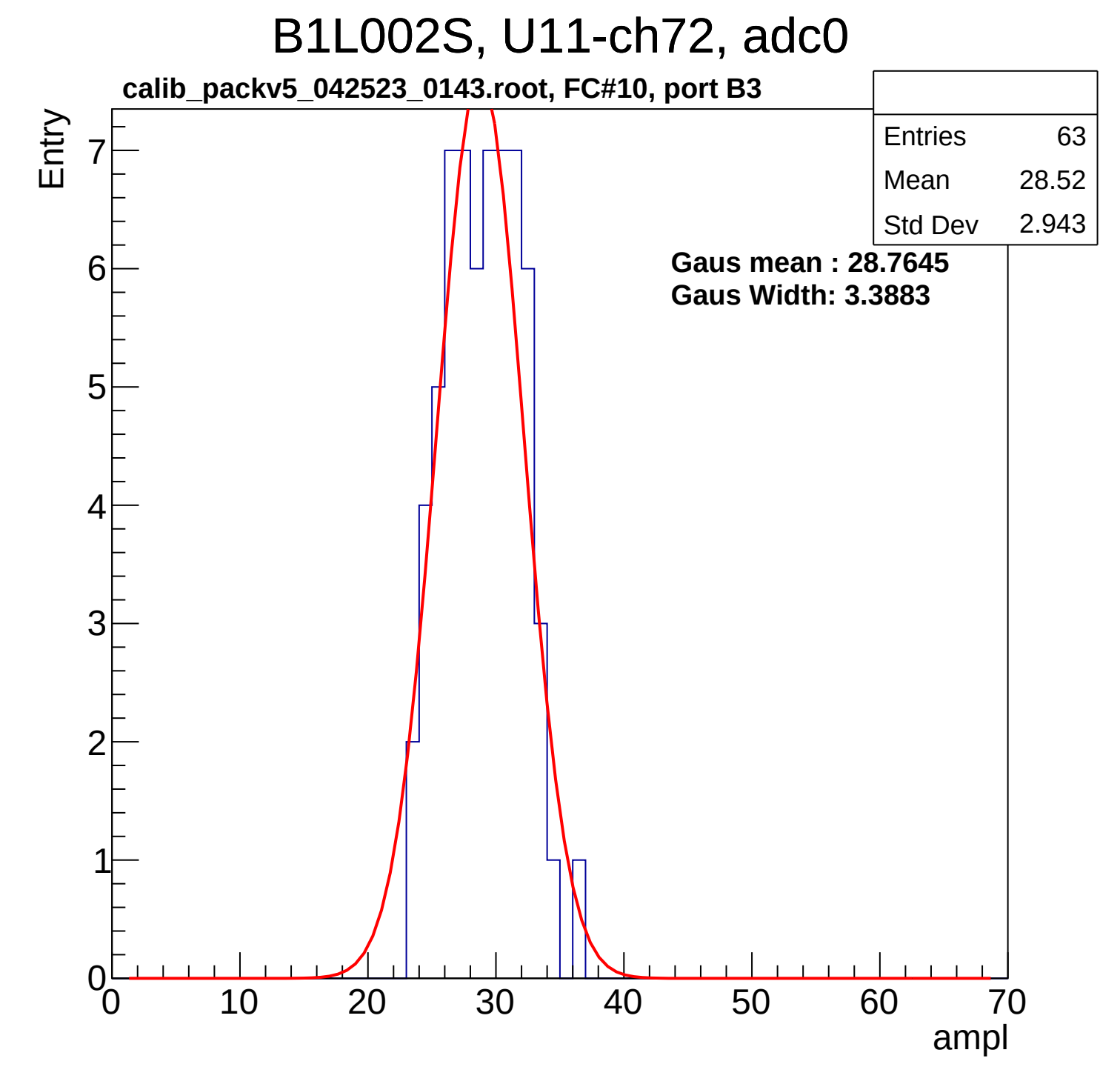
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.52
Std Dev 2.943
Gaus mean : 28.7645
Gaus Width: 3.3883
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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60
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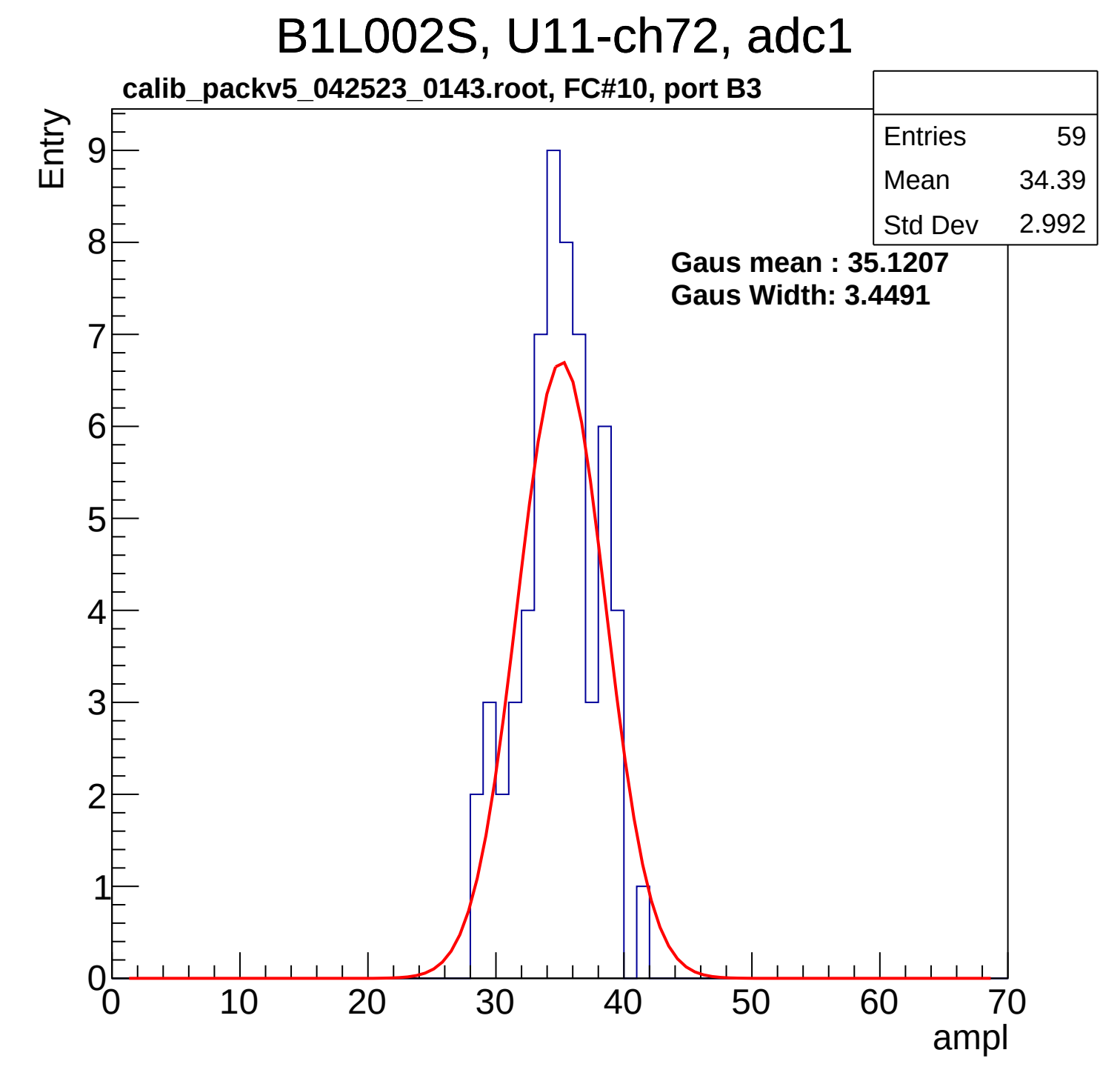
B1L002S, U11-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.39
Std Dev 2.992
Gaus mean : 35.1207
Gaus Width: 3.4491
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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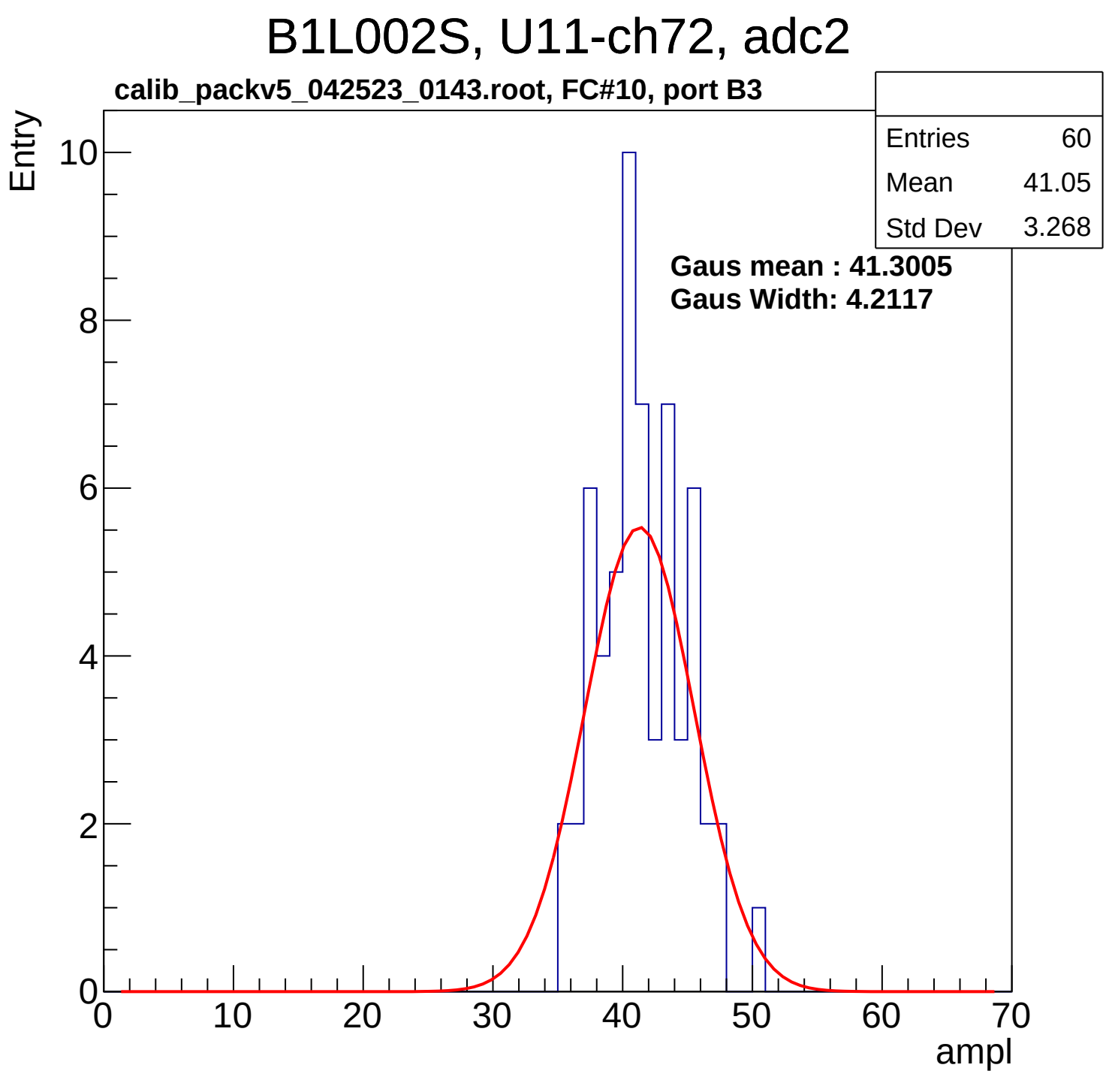
B1L002S, U11-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.05
Std Dev 3.268
Gaus mean : 41.3005
Gaus Width: 4.2117
Entry
ampl
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8
10
0
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20
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60
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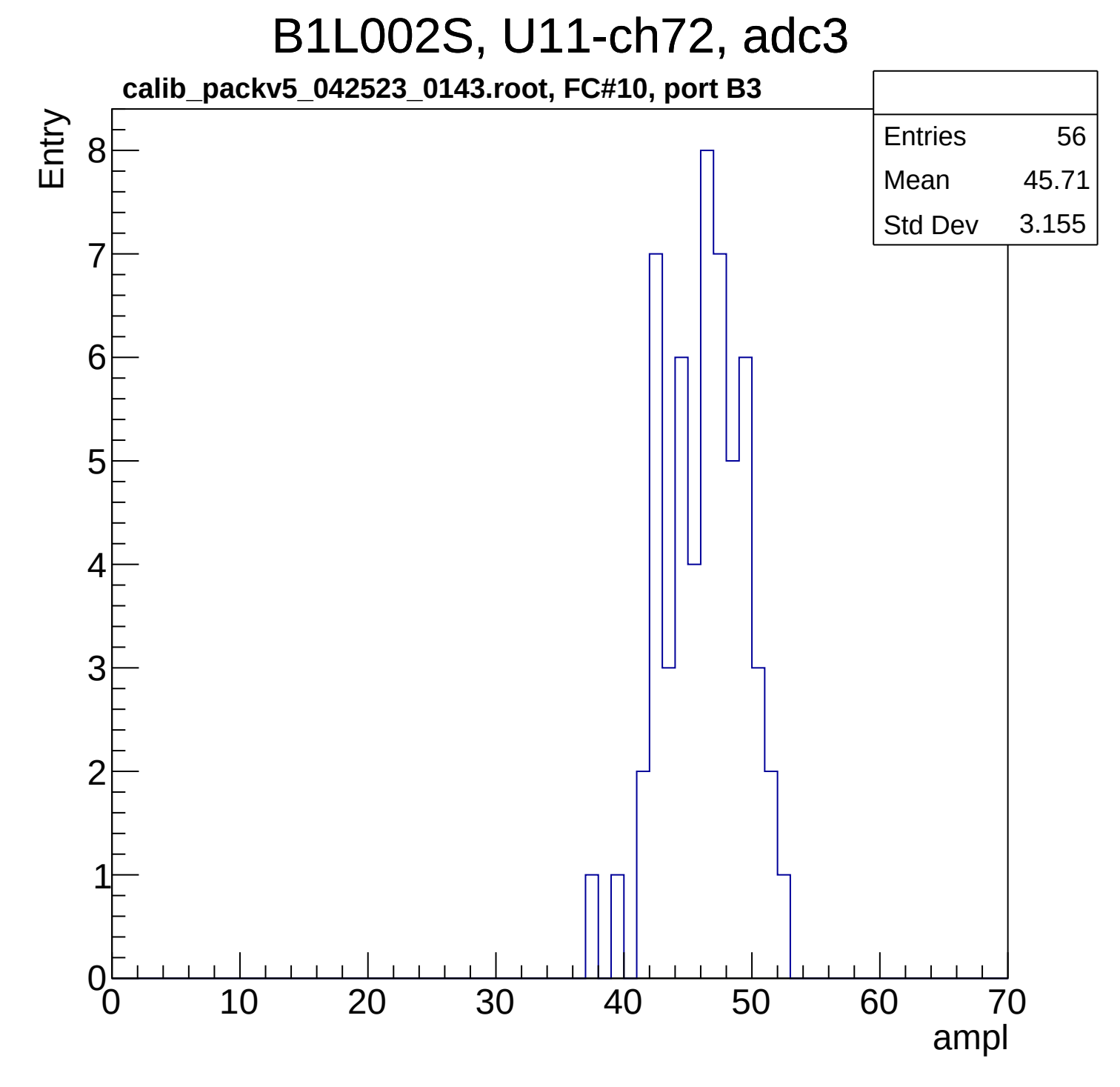
B1L002S, U11-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 45.71
Std Dev 3.155
Entry
ampl
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6
7
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0
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60
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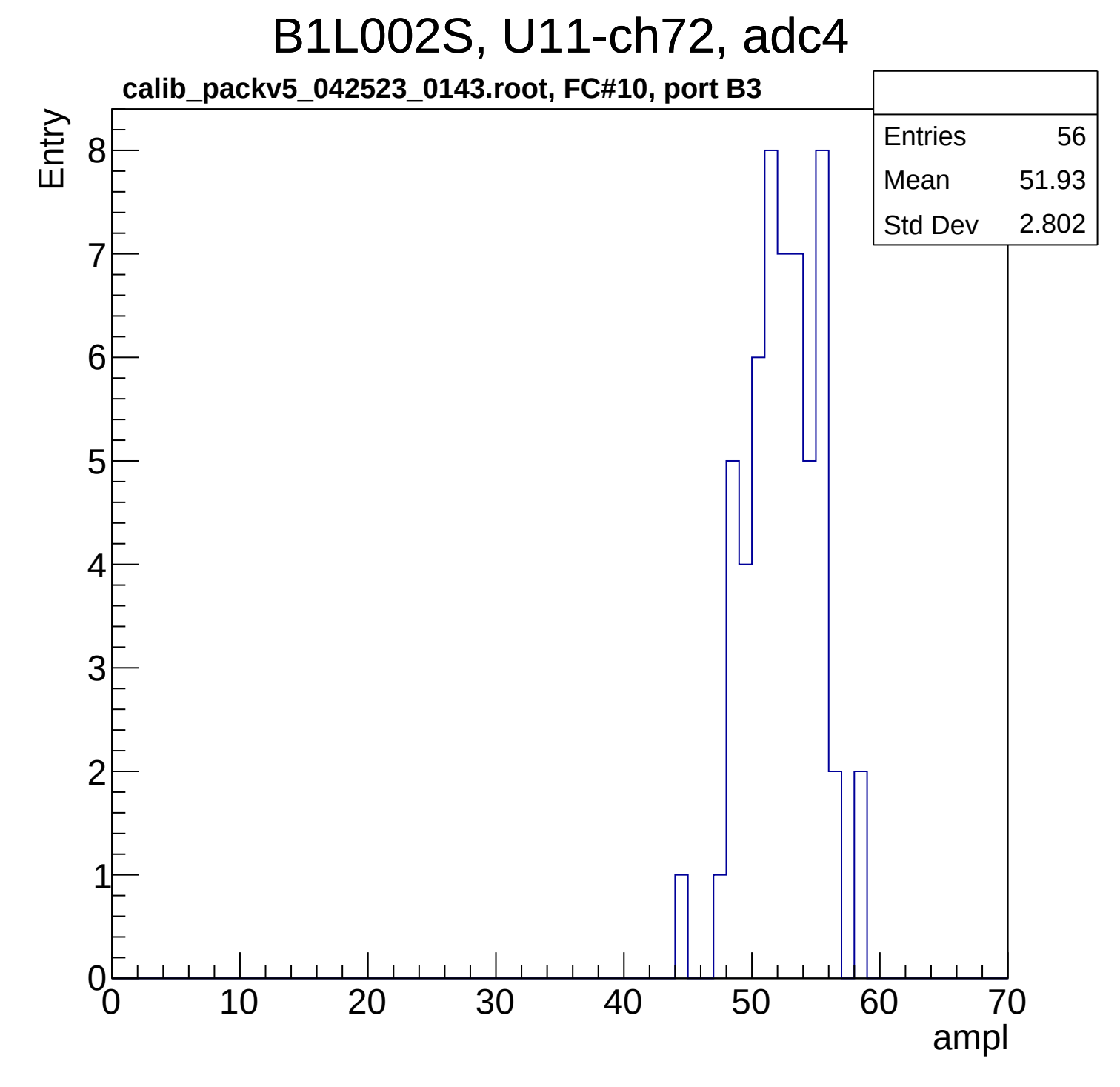
B1L002S, U11-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.93
Std Dev 2.802
Entry
ampl
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7
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0
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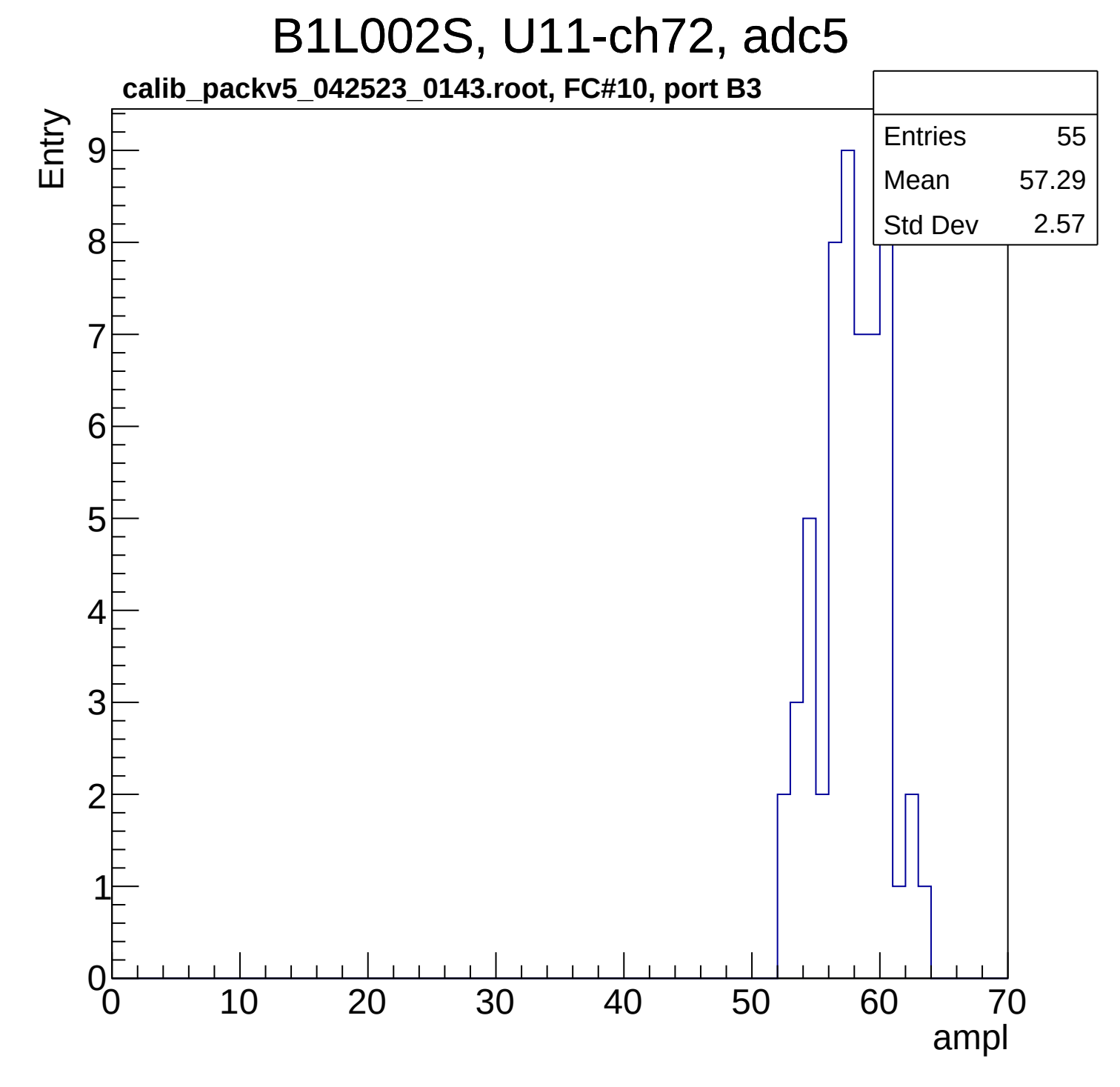
B1L002S, U11-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.29
Std Dev 2.57
Entry
ampl
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6
7
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0
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60
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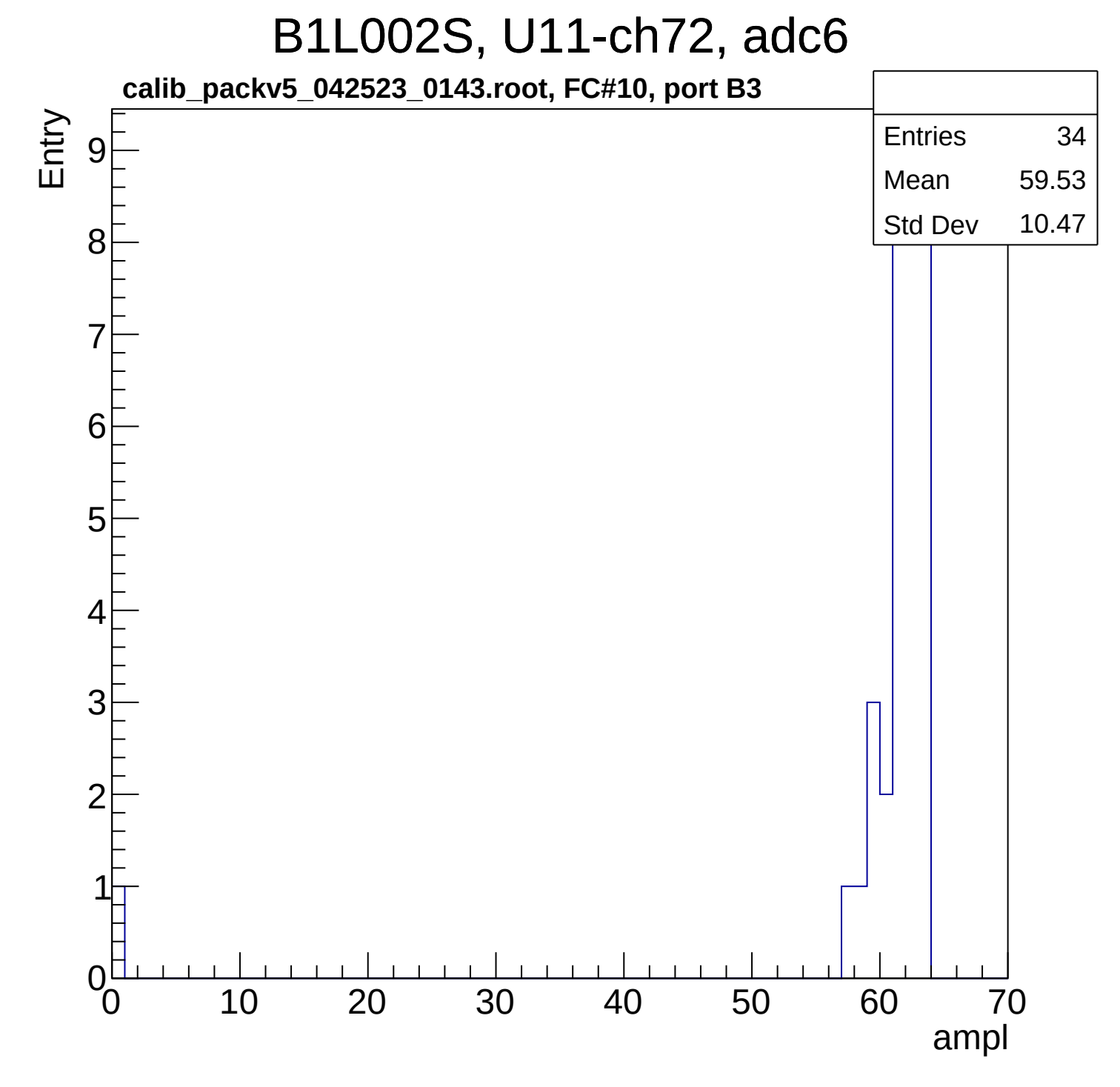
B1L002S, U11-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 59.53
Std Dev 10.47
Entry
ampl
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4
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6
7
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0
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20
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40
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60
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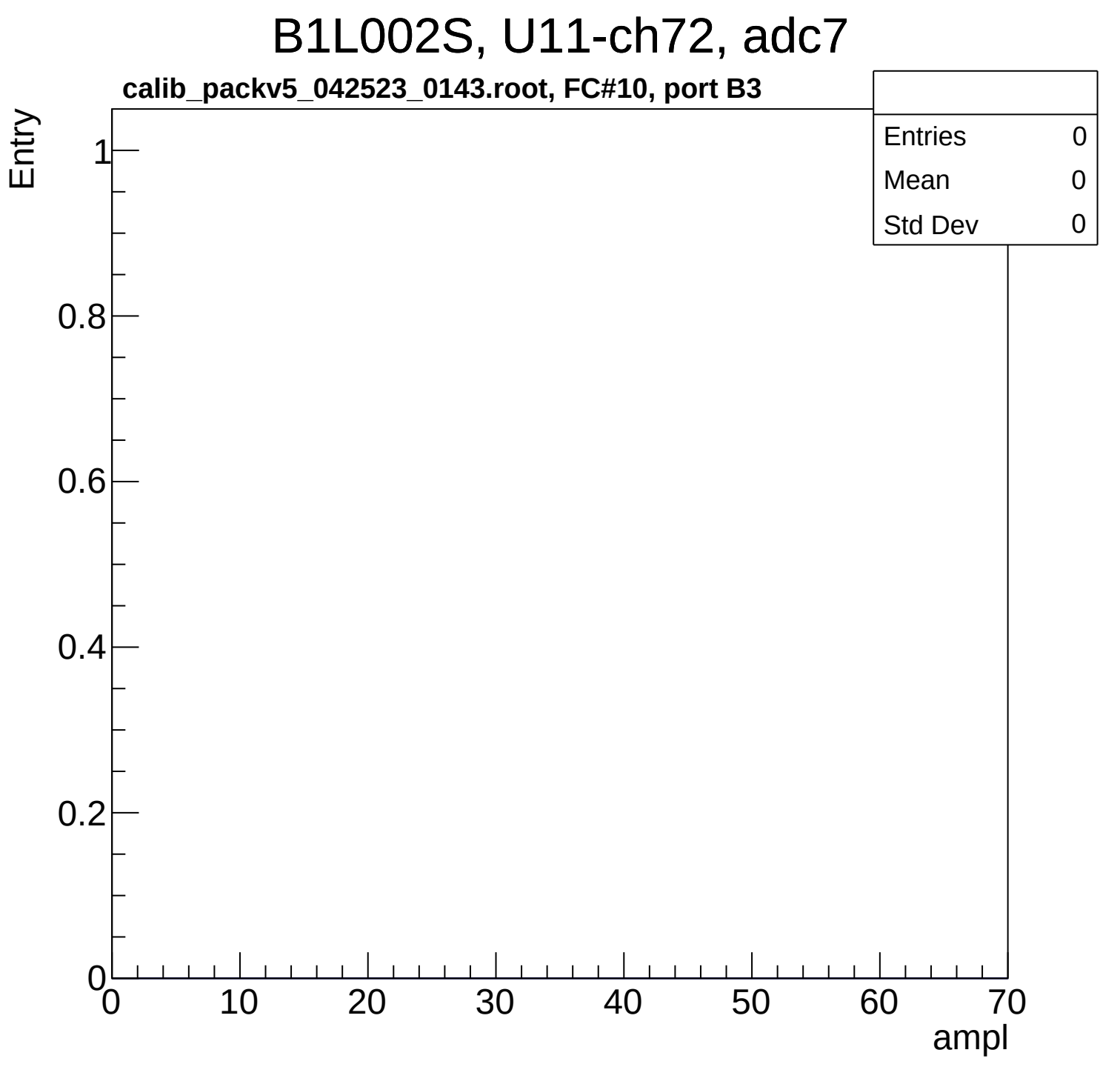
B1L002S, U11-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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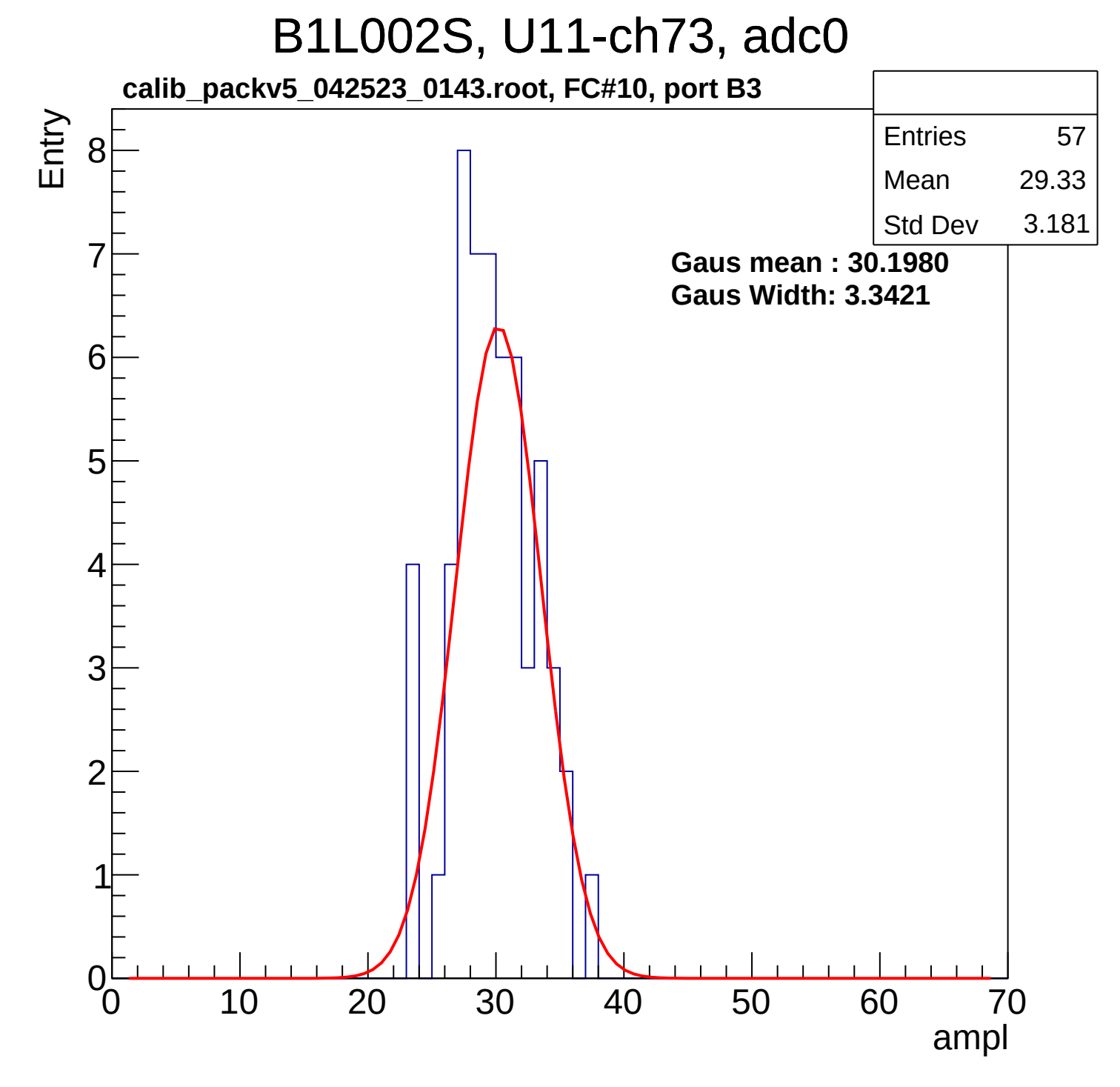
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 29.33
Std Dev 3.181
Gaus mean : 30.1980
Gaus Width: 3.3421
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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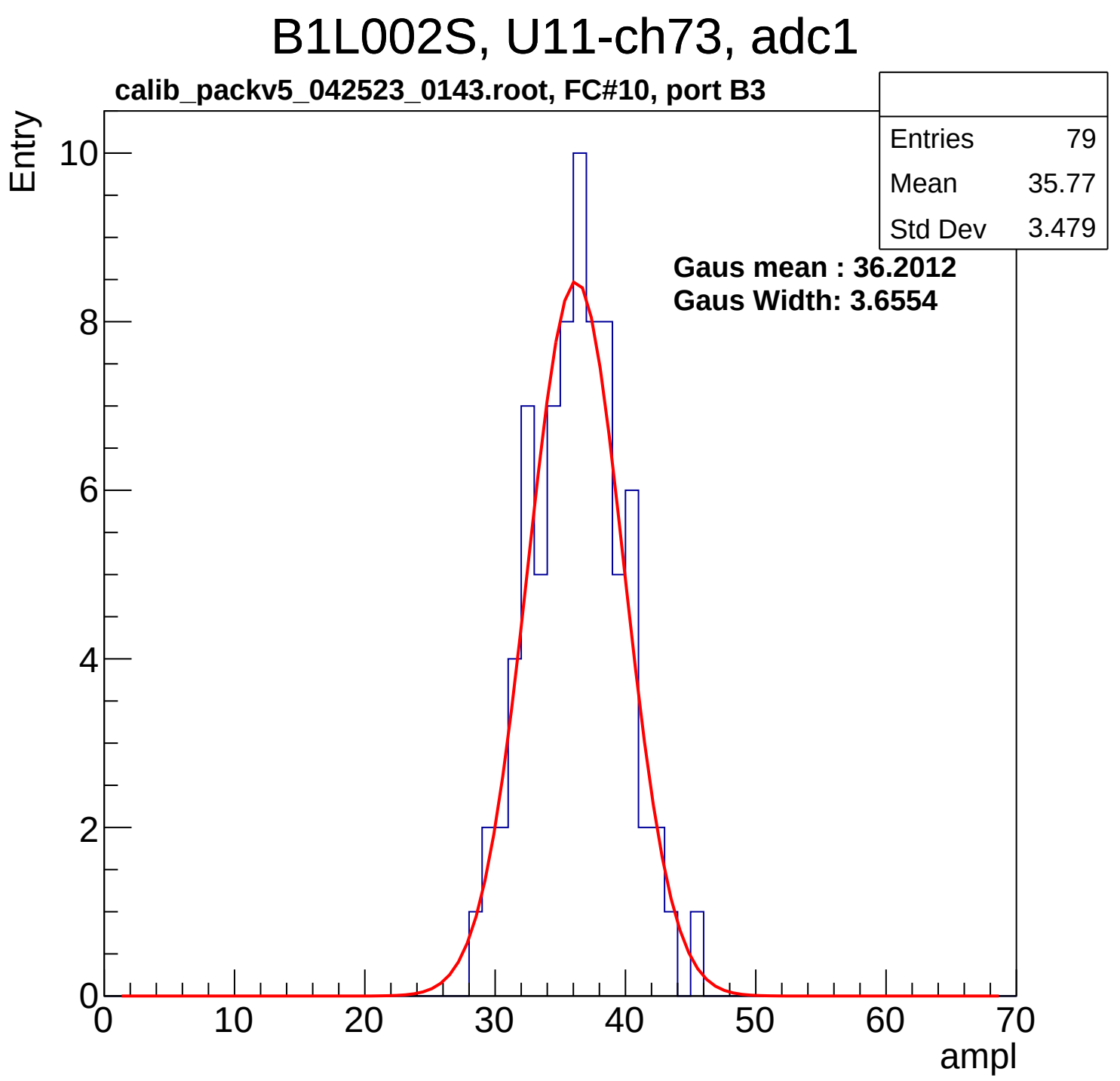
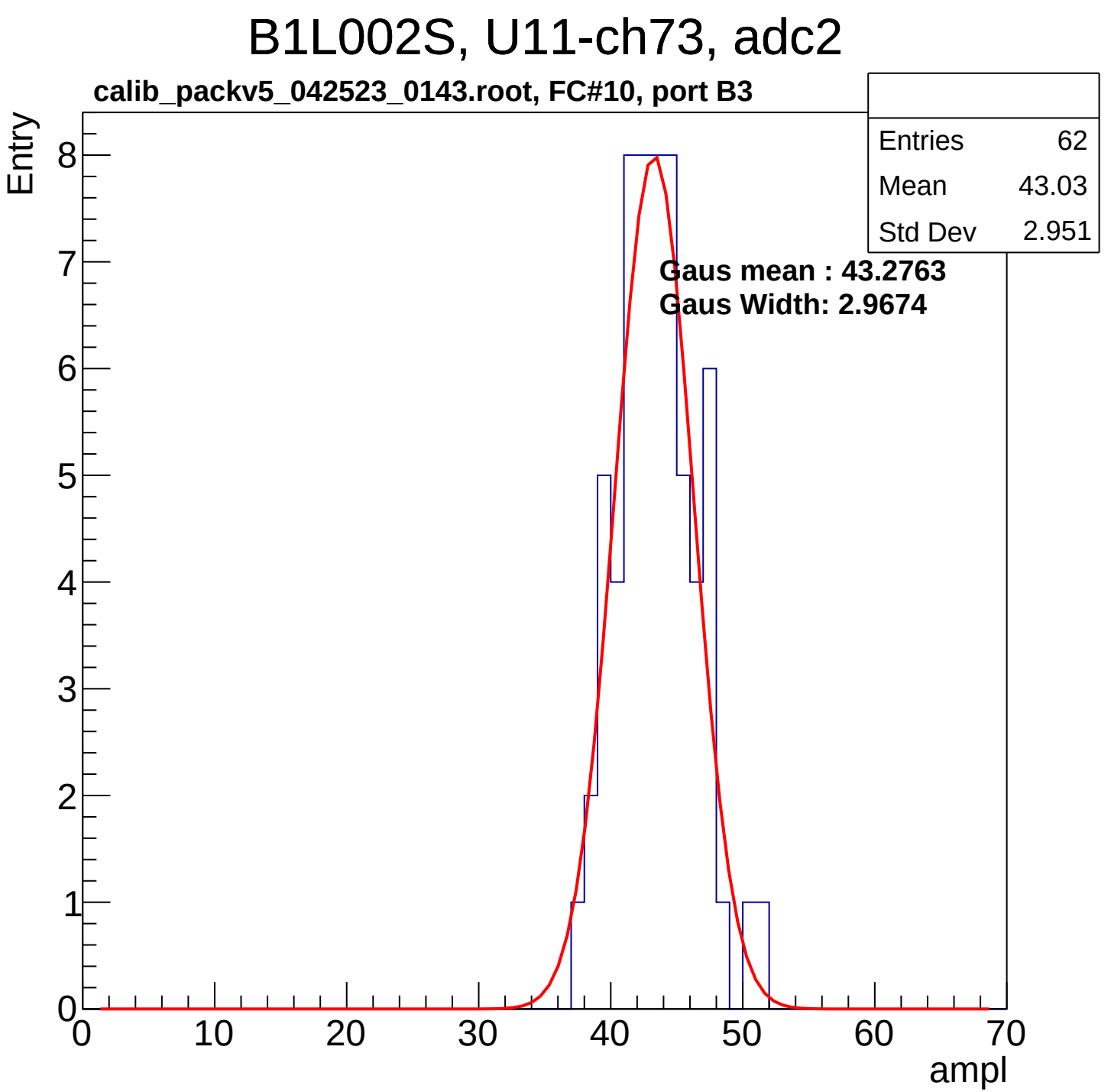

B1L002S, U11-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 35.77
Std Dev 3.479
Gaus mean : 36.2012
Gaus Width: 3.6554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 43.03
Std Dev 2.951
Gaus mean : 43.2763
Gaus Width: 2.9674
Entry
ampl
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3
4
5
6
7
8
0
10
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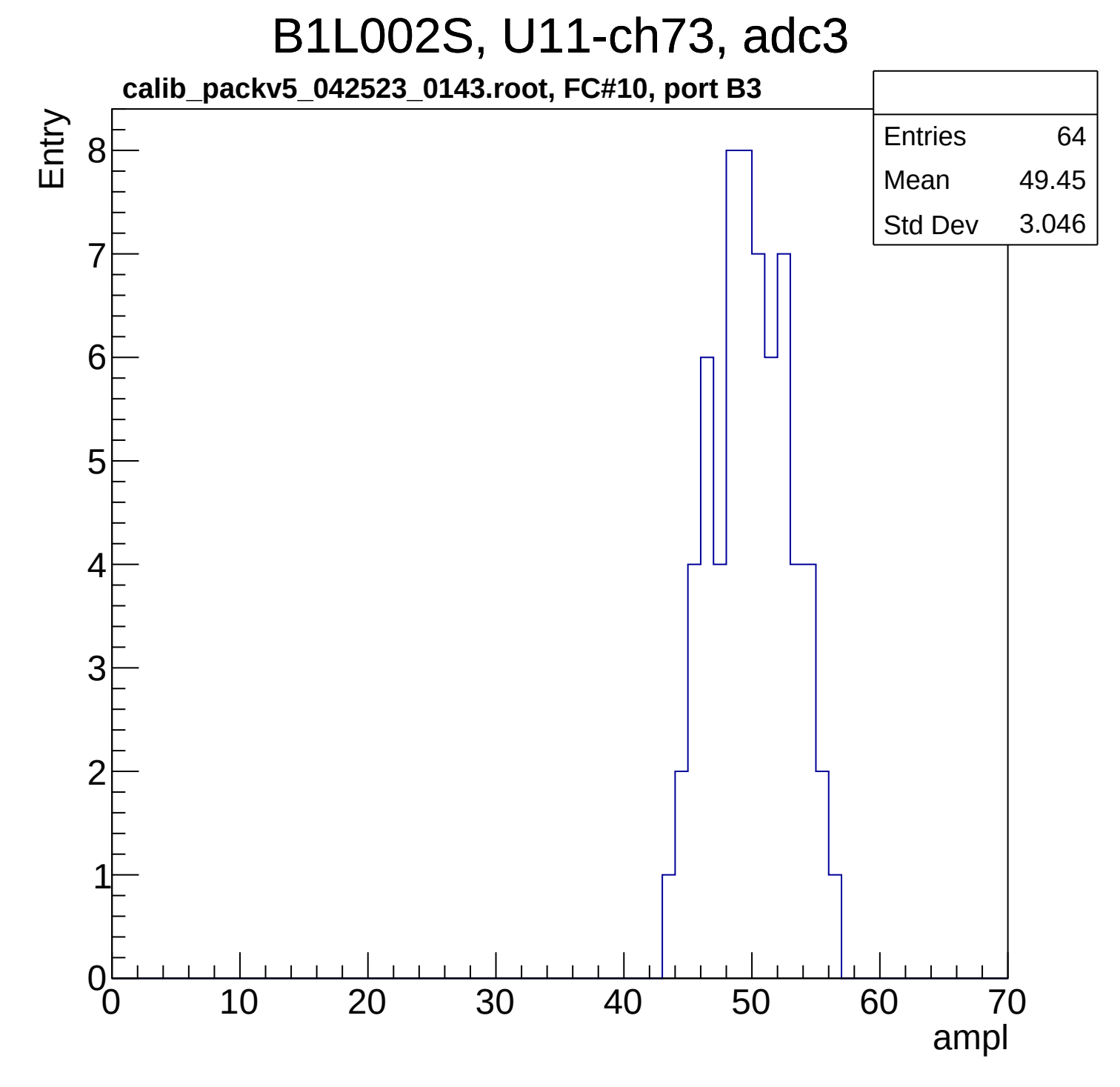
B1L002S, U11-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 49.45
Std Dev 3.046
Entry
ampl
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7
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0
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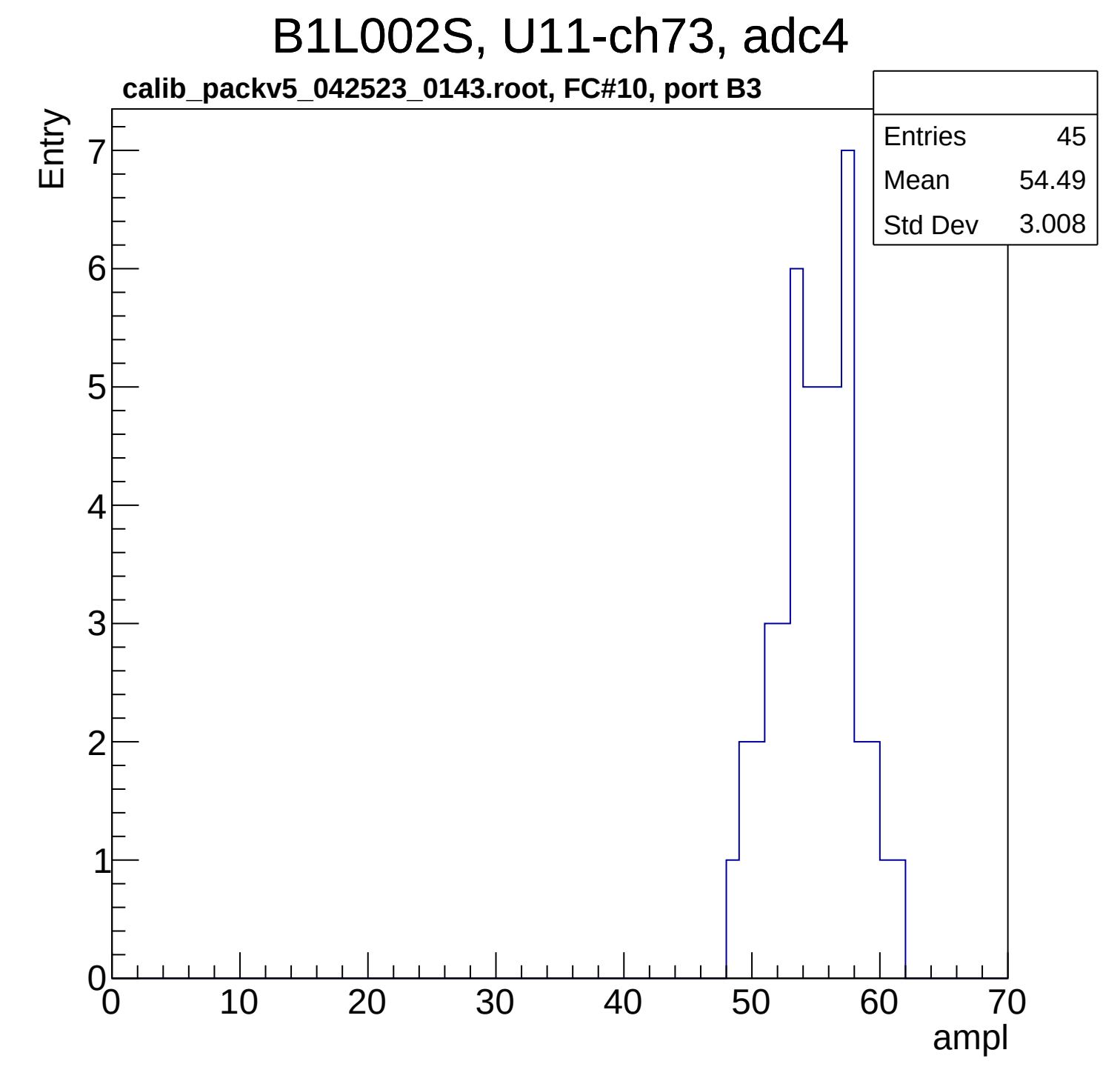
B1L002S, U11-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 54.49
Std Dev 3.008
Entry
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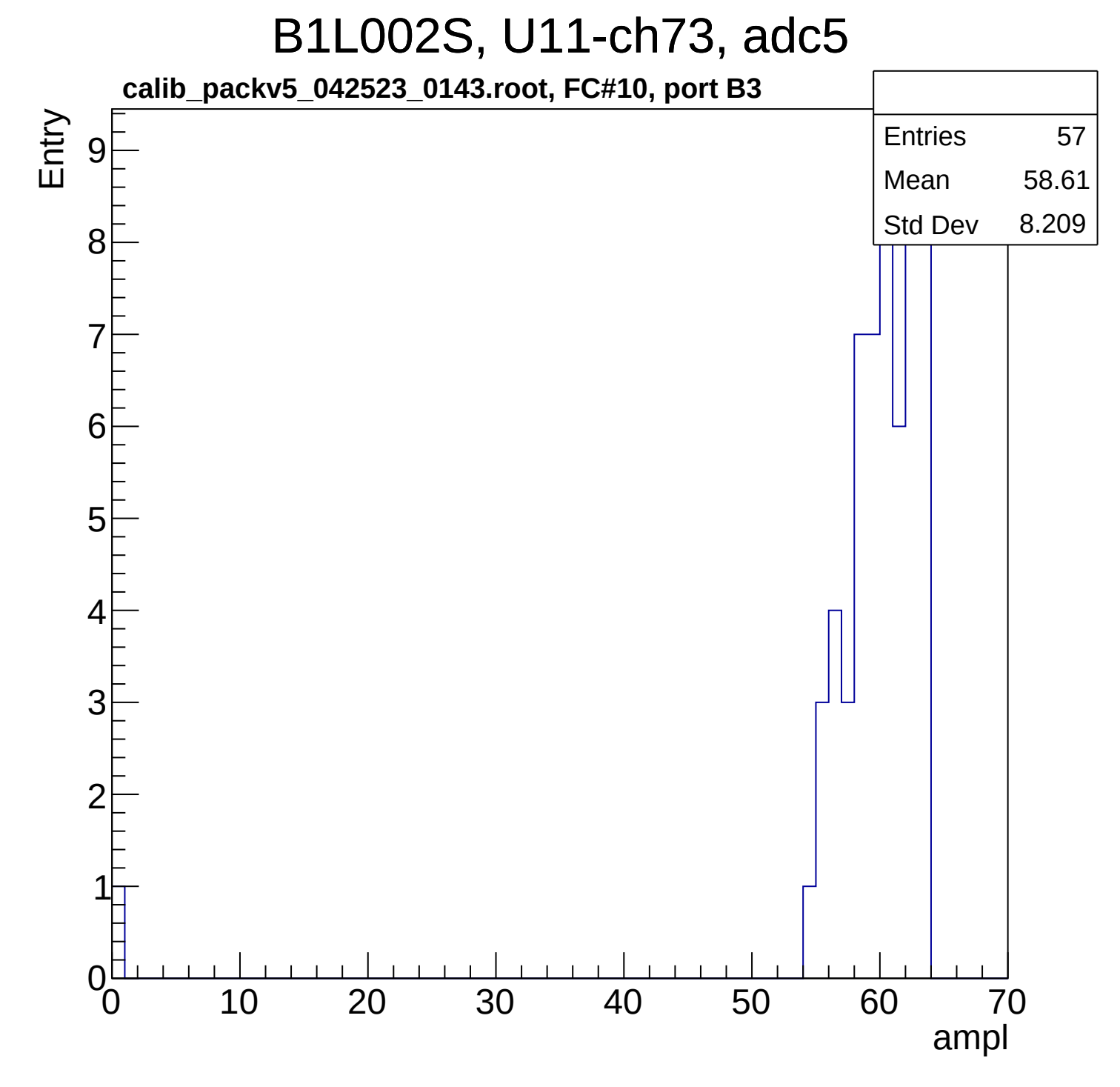
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.61
Std Dev 8.209
Entry
ampl
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2
3
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5
6
7
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0
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40
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60
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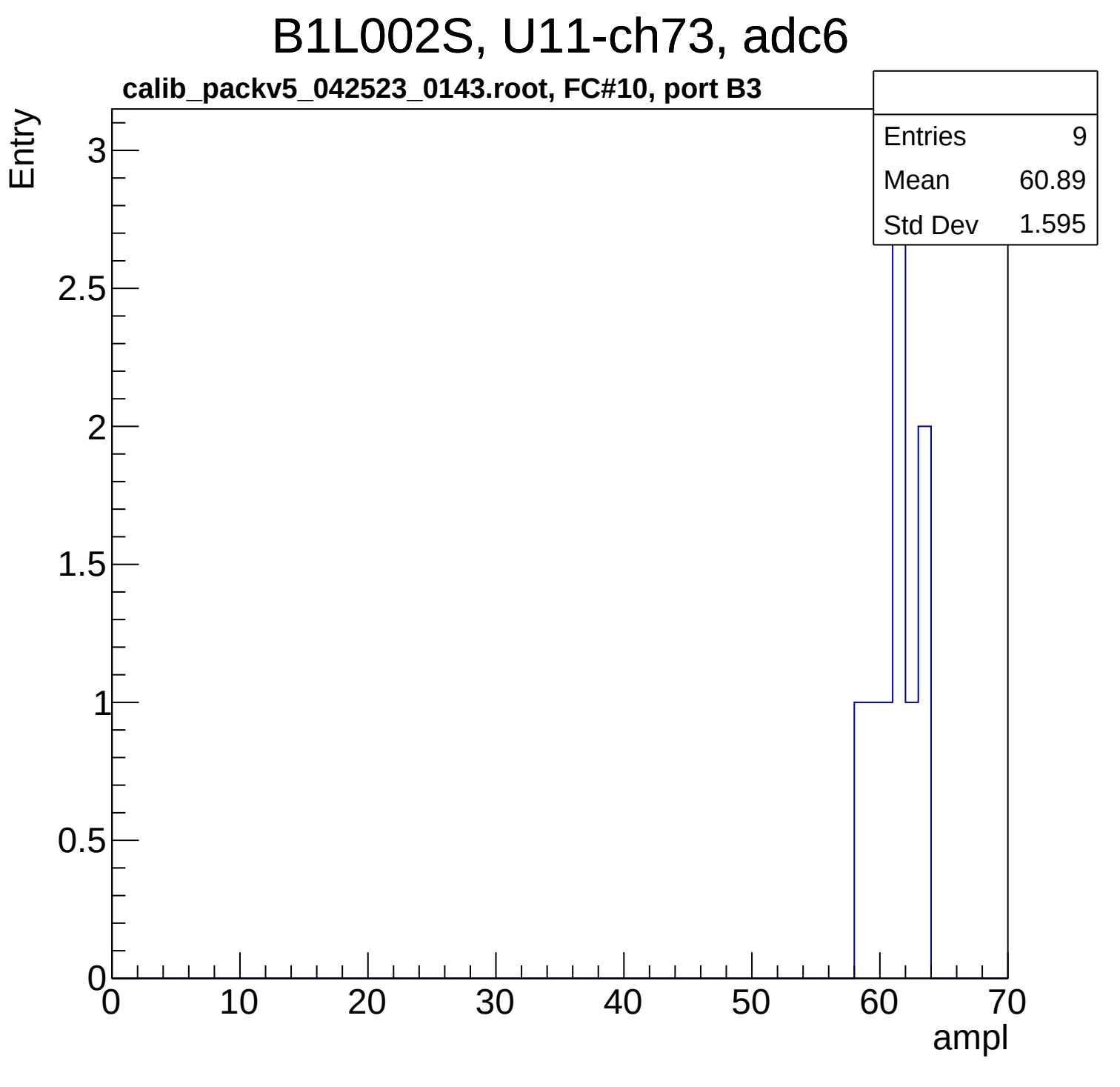
B1L002S, U11-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 60.89
Std Dev 1.595
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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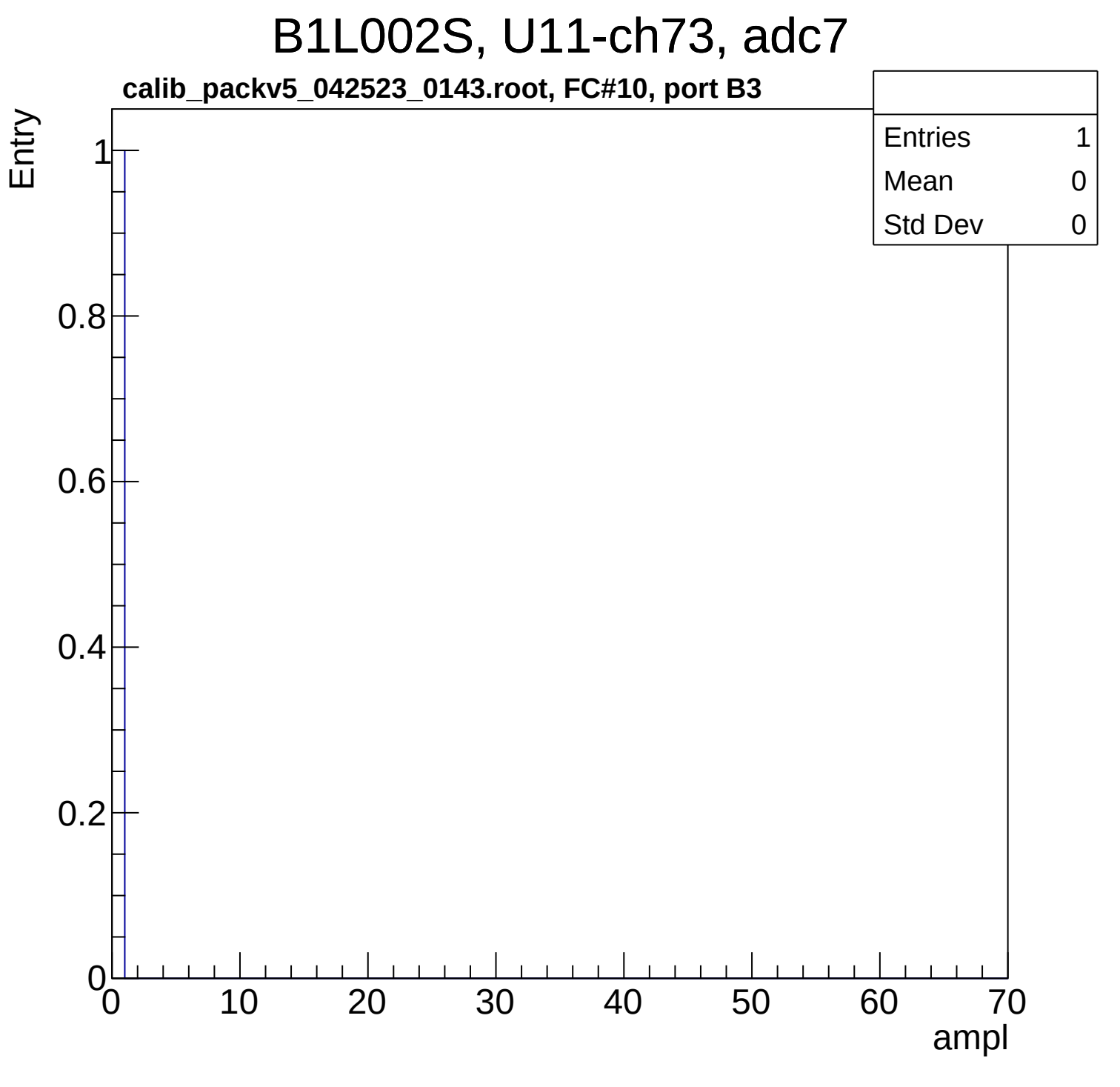
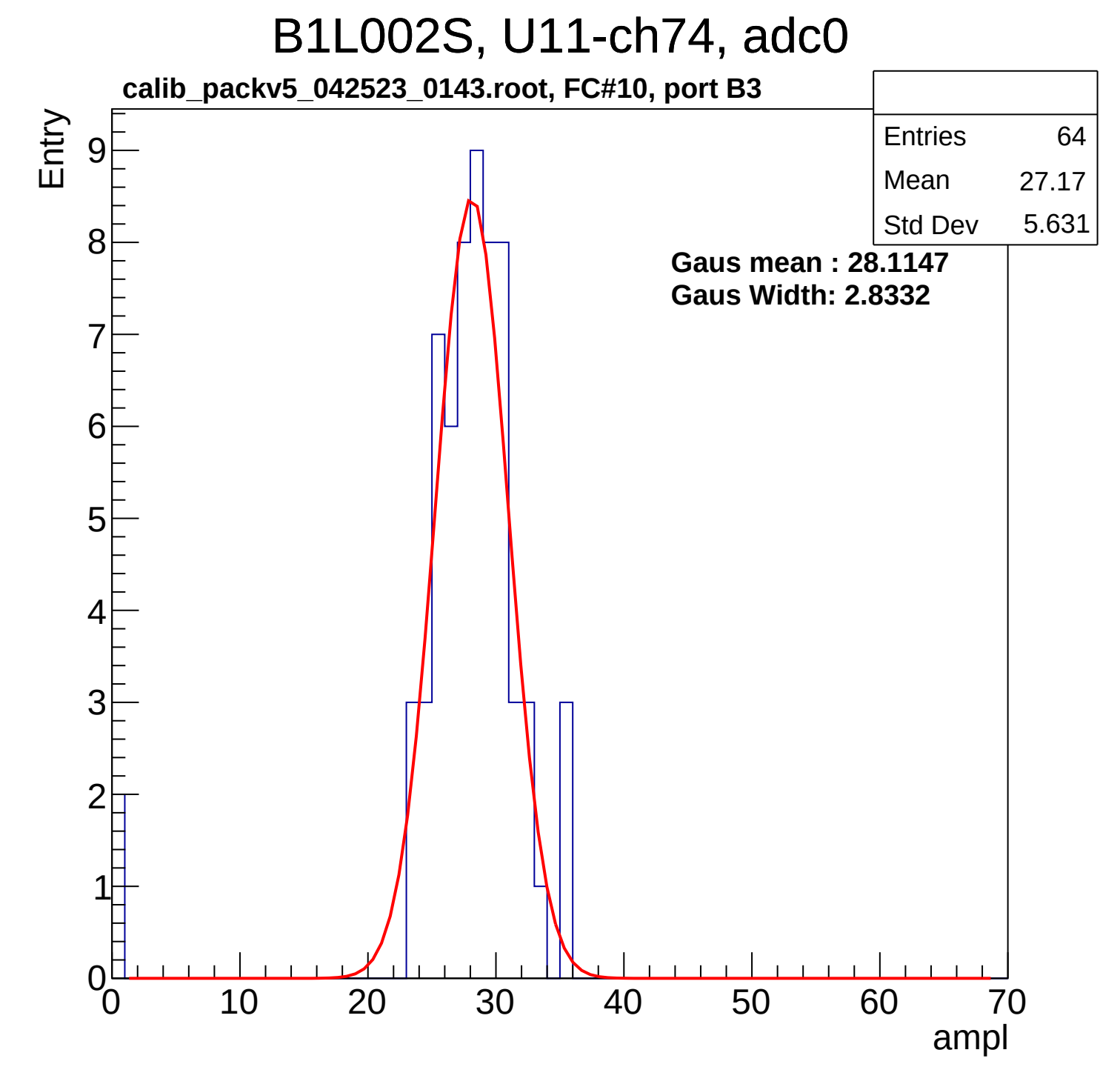

B1L002S, U11-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.17
Std Dev 5.631
Gaus mean : 28.1147
Gaus Width: 2.8332
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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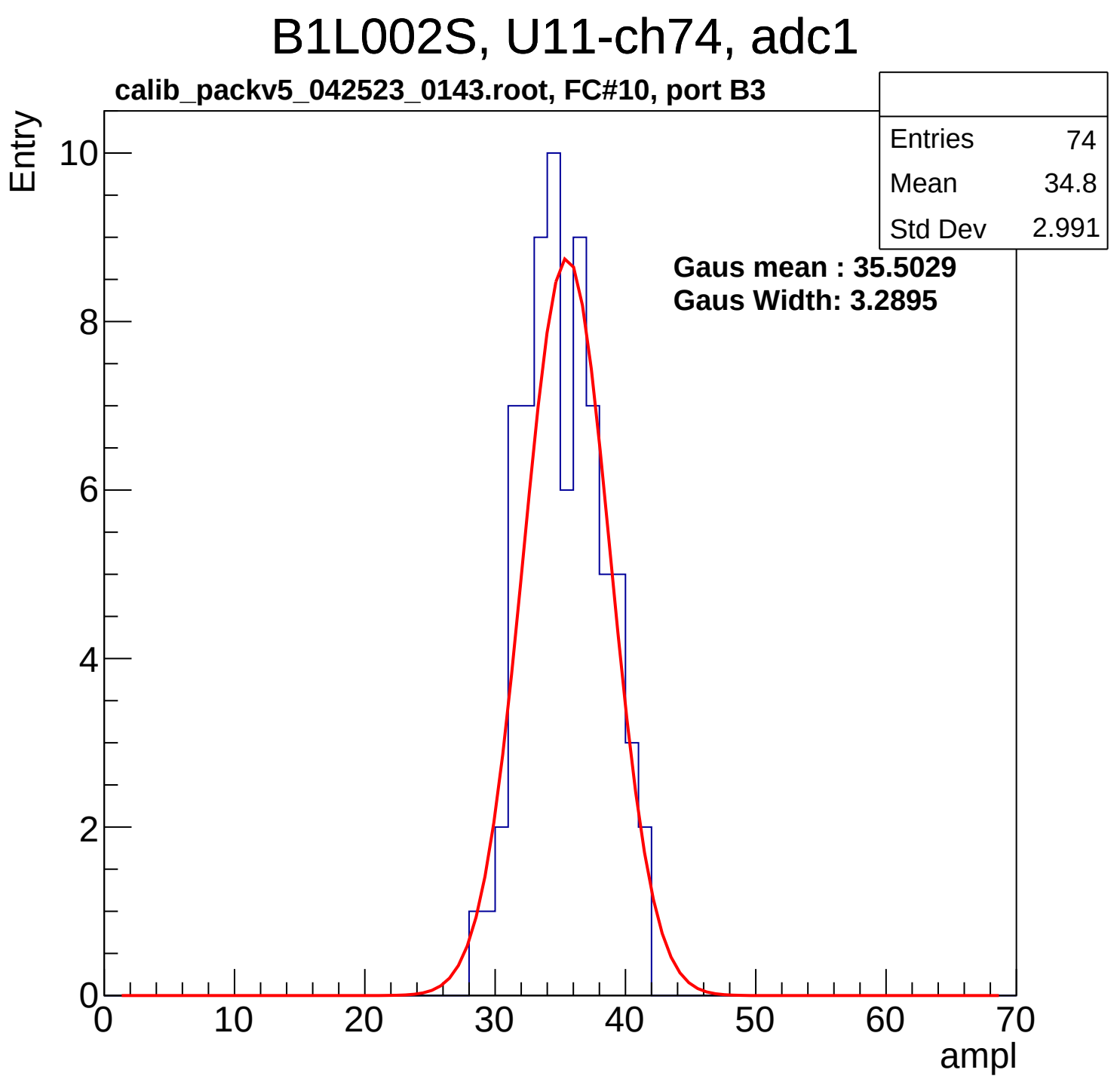
B1L002S, U11-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.8
Std Dev 2.991
Gaus mean : 35.5029
Gaus Width: 3.2895
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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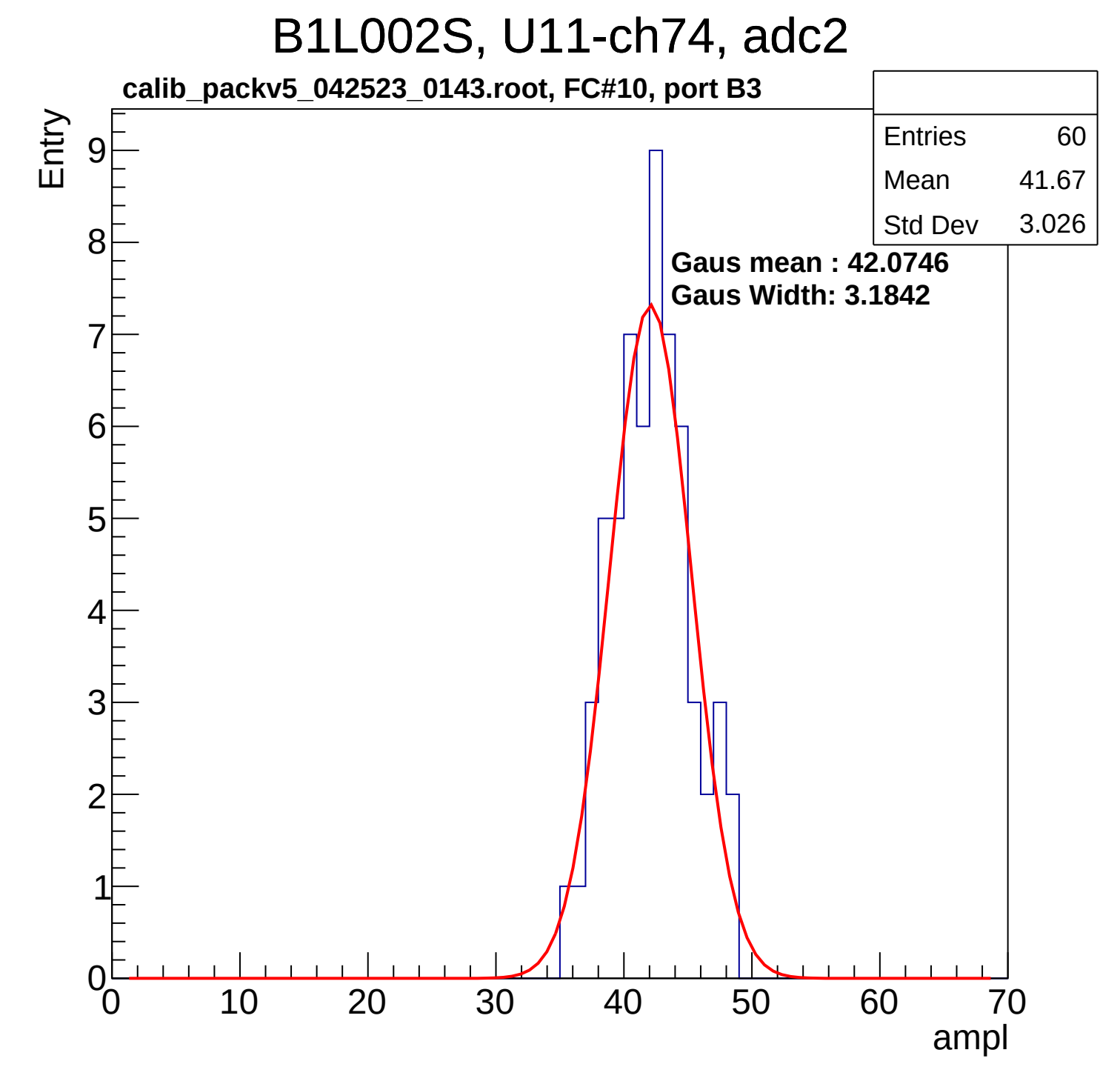
B1L002S, U11-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.67
Std Dev 3.026
Gaus mean : 42.0746
Gaus Width: 3.1842
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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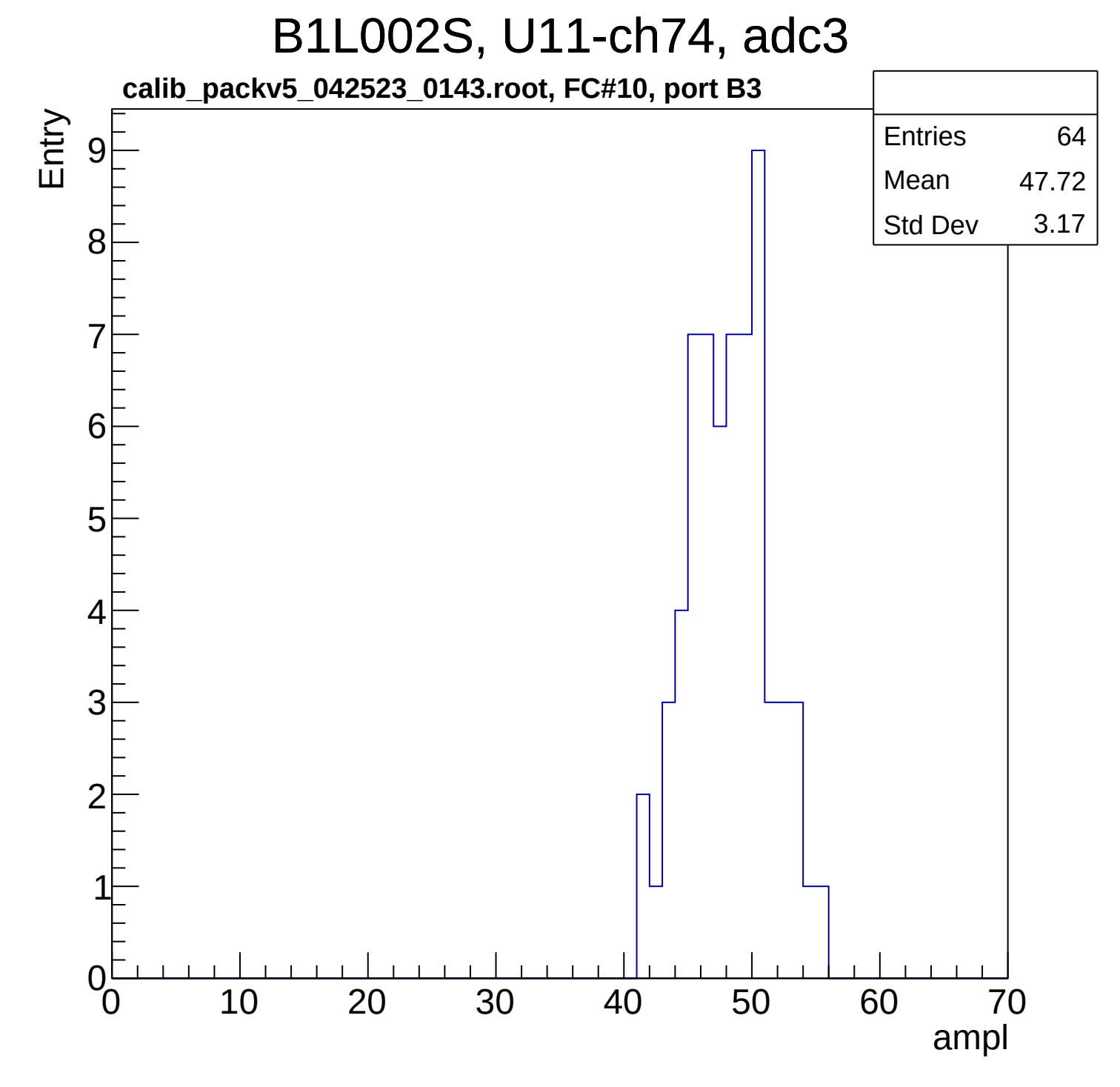
B1L002S, U11-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.72
Std Dev 3.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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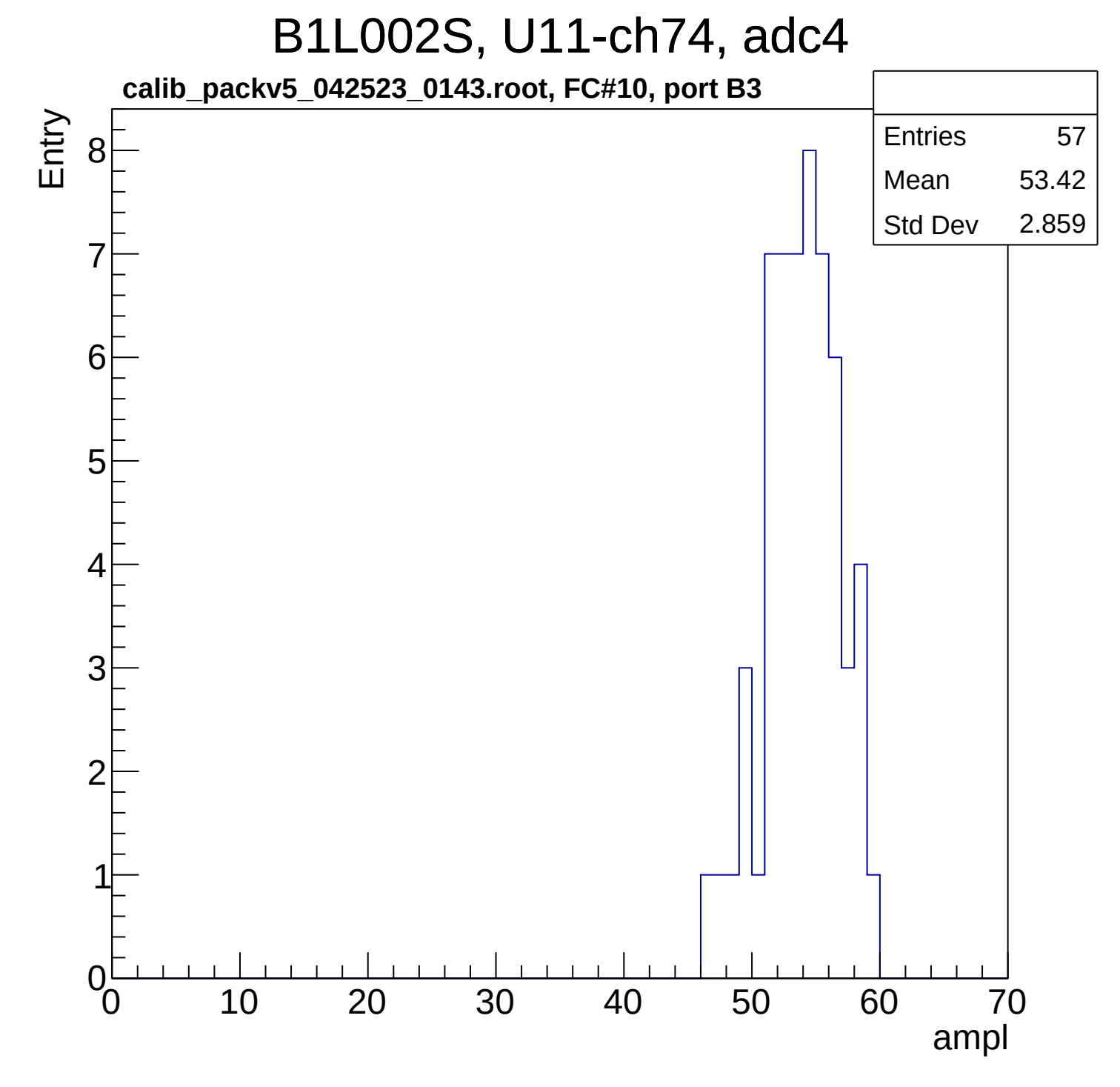
B1L002S, U11-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.42
Std Dev 2.859
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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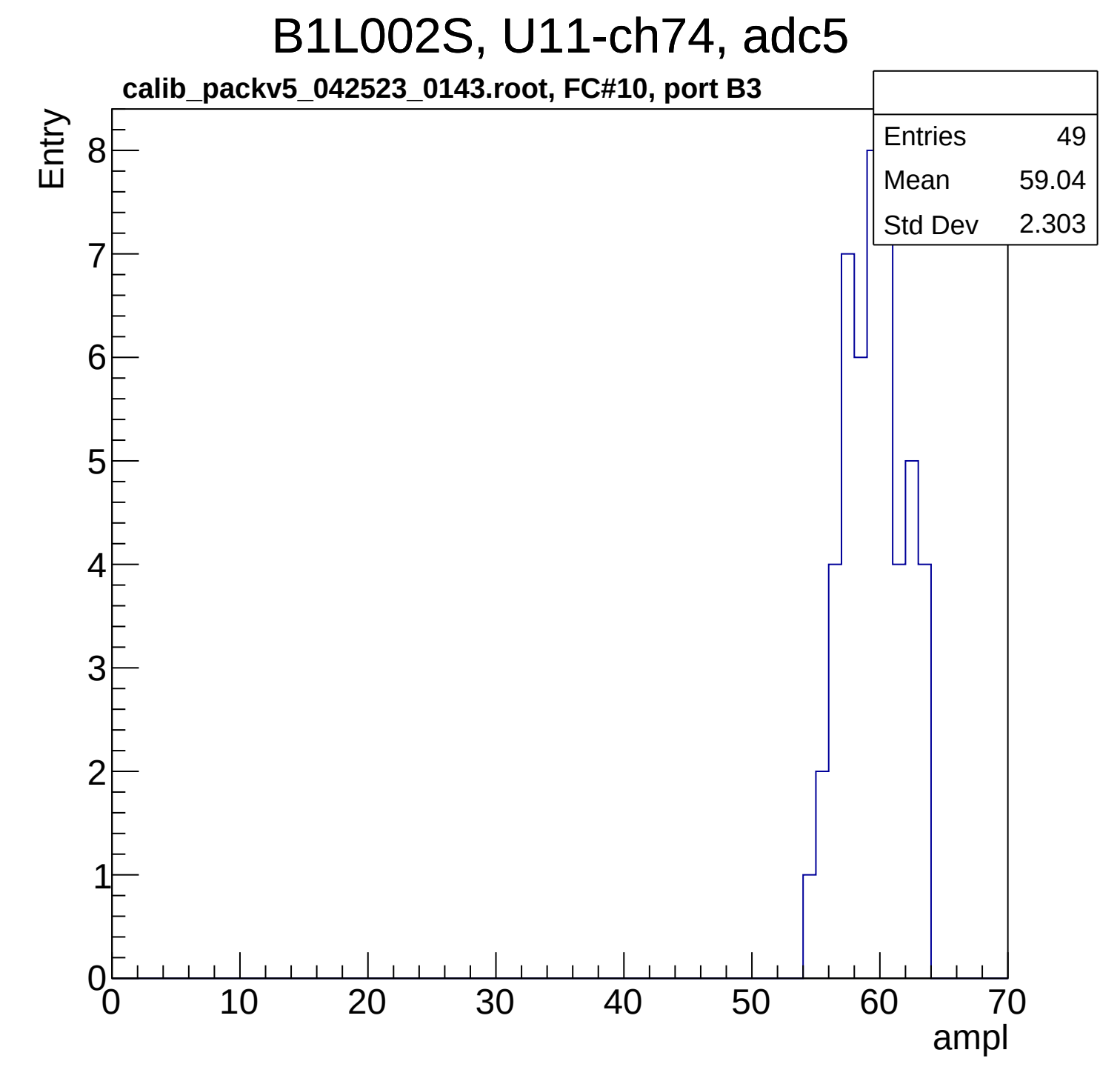
B1L002S, U11-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.04
Std Dev 2.303
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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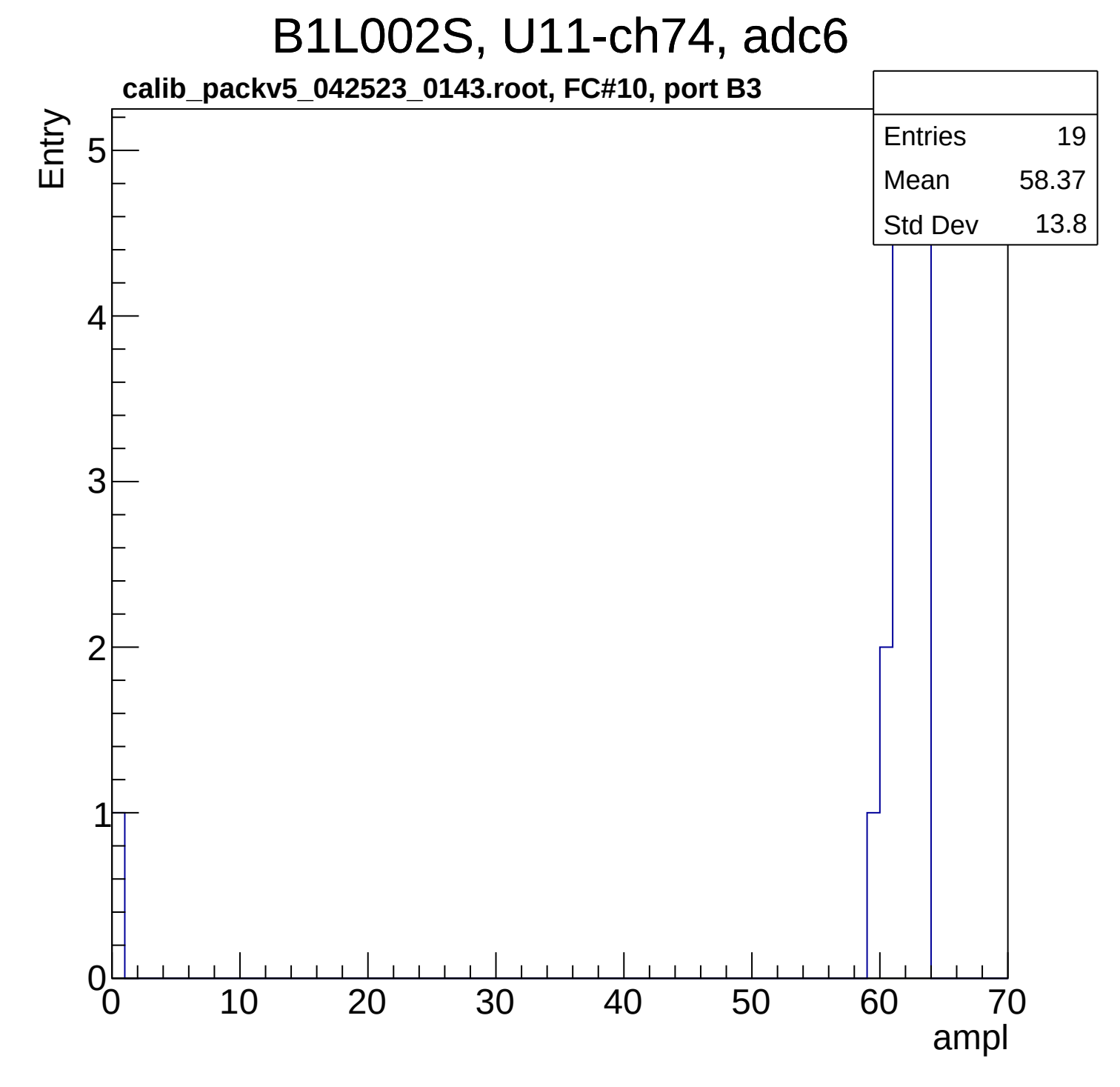
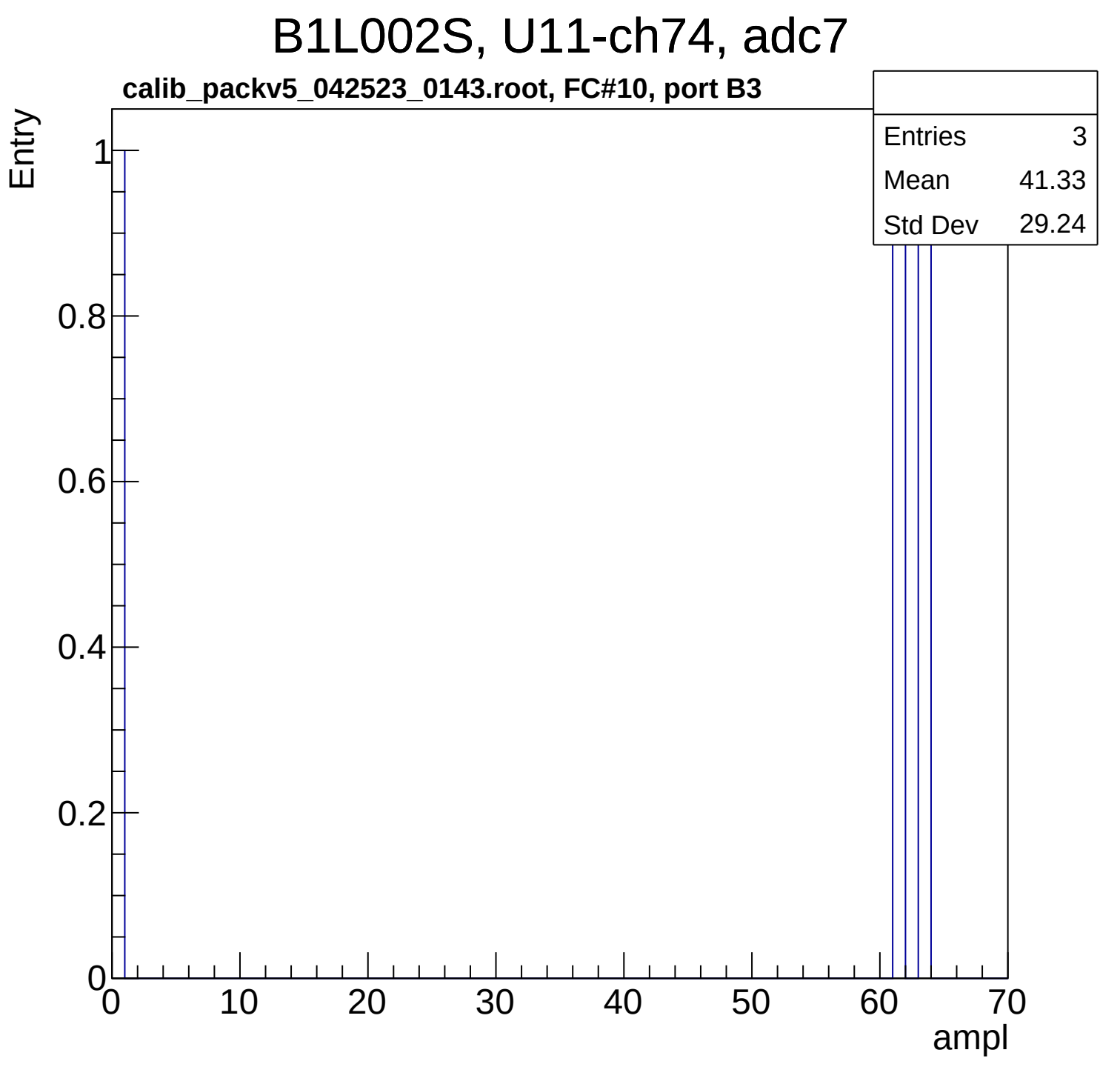

B1L002S, U11-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.37
Std Dev 13.8
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.24
Entry
ampl
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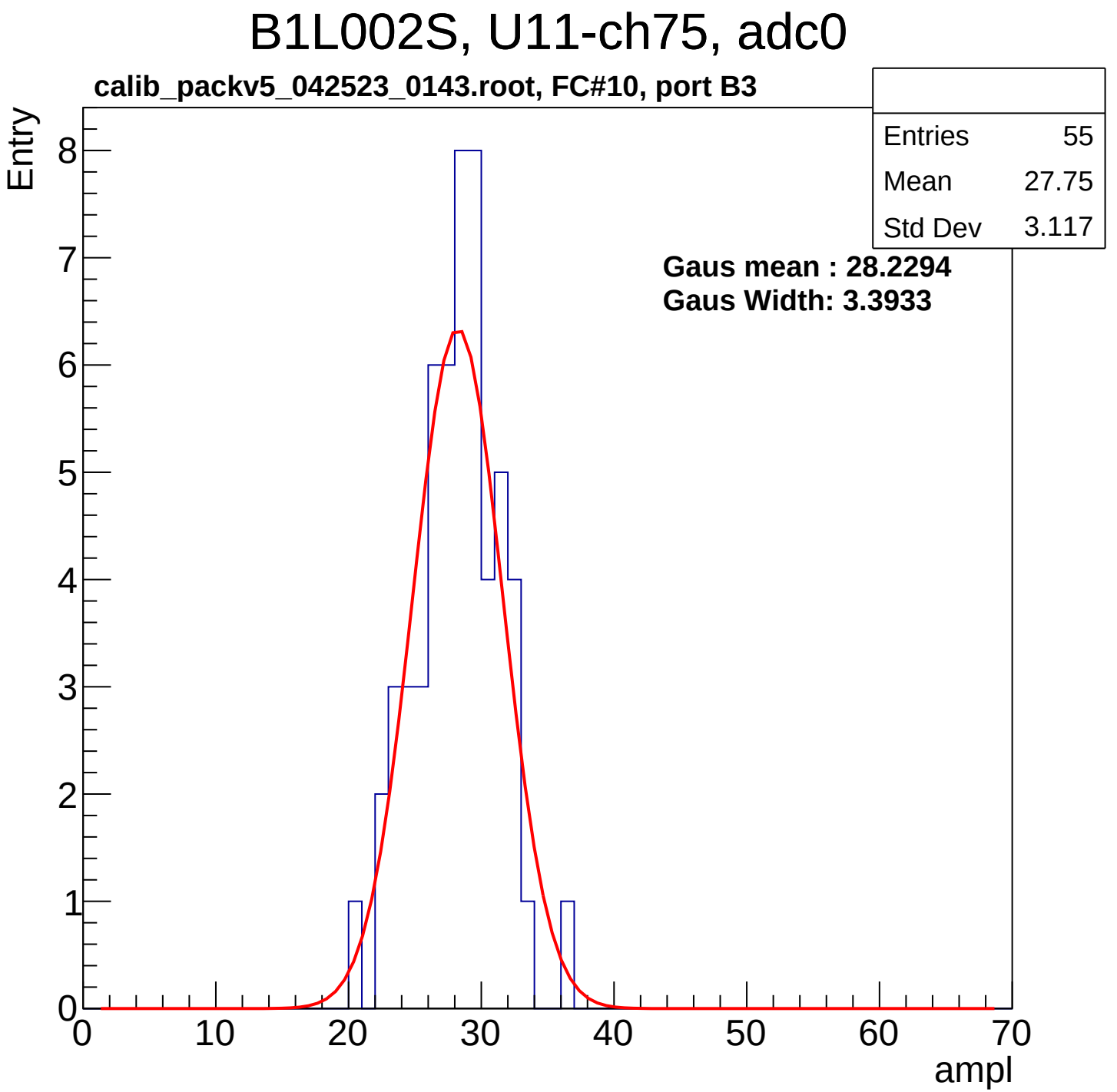
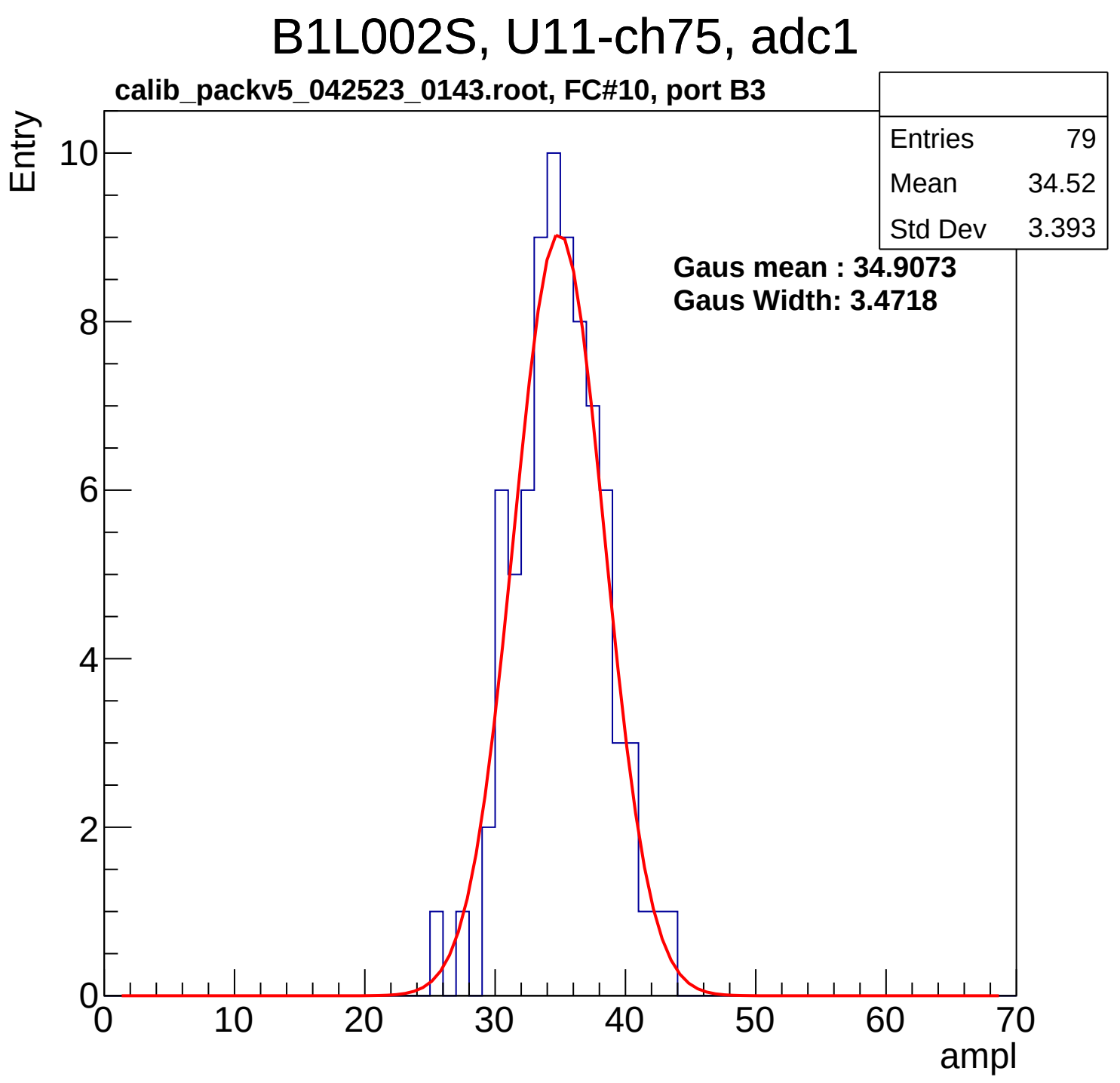
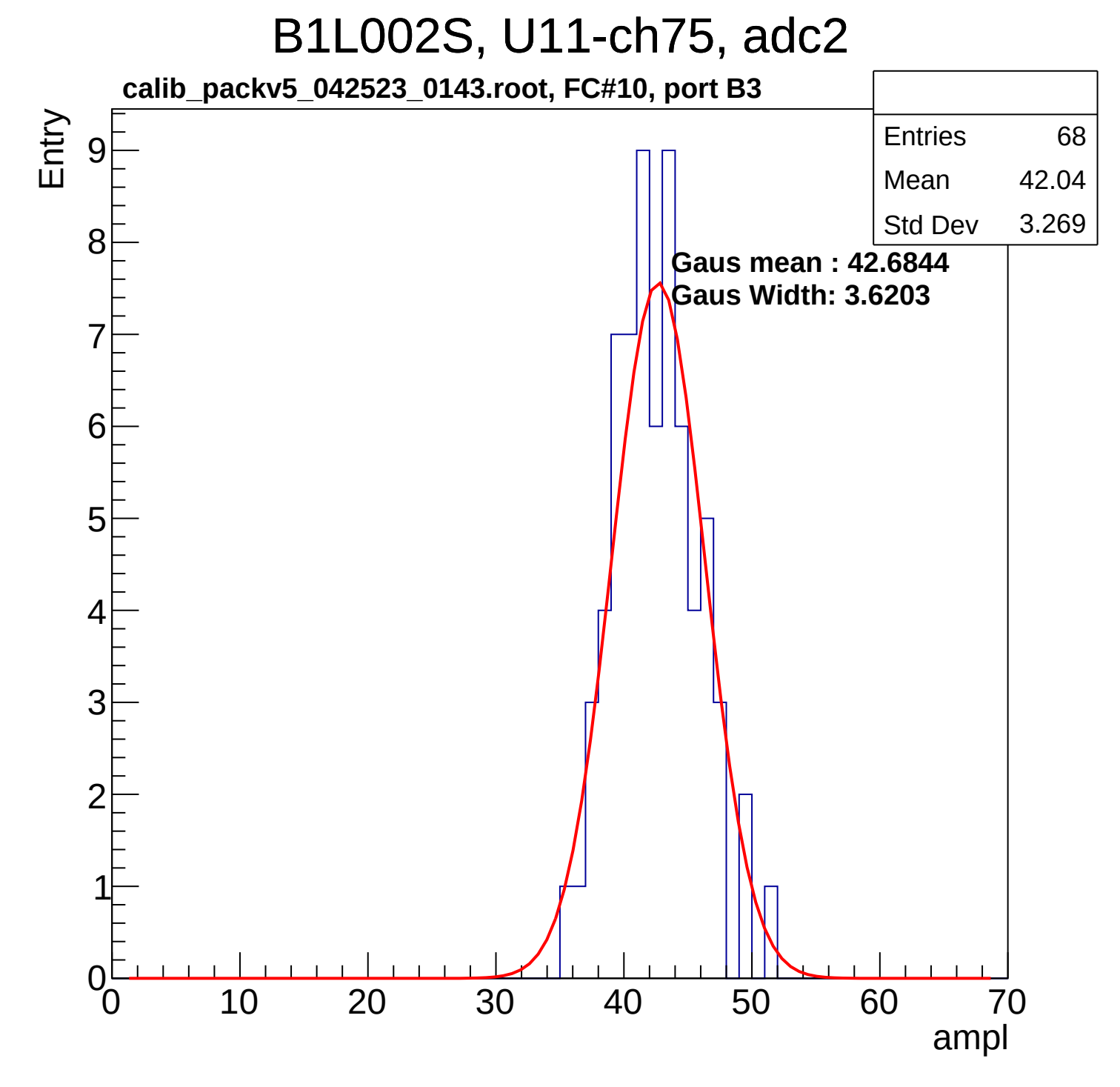
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U11-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 27.75
Std Dev 3.117
Gaus mean : 28.2294
Gaus Width: 3.3933
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.52
Std Dev 3.393
Gaus mean : 34.9073
Gaus Width: 3.4718
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 42.04
Std Dev 3.269
Gaus mean : 42.6844
Gaus Width: 3.6203
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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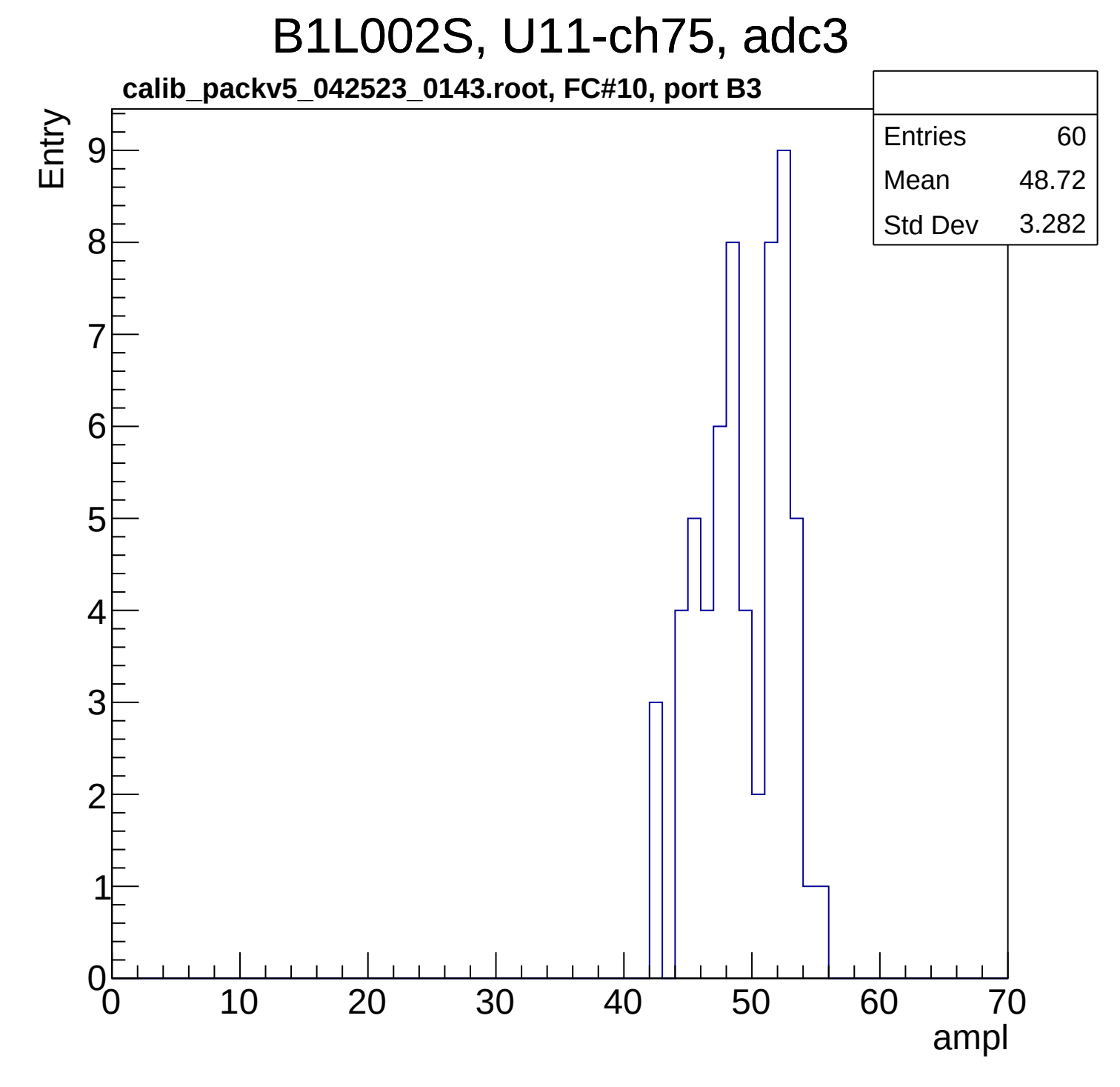
B1L002S, U11-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 48.72
Std Dev 3.282
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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60
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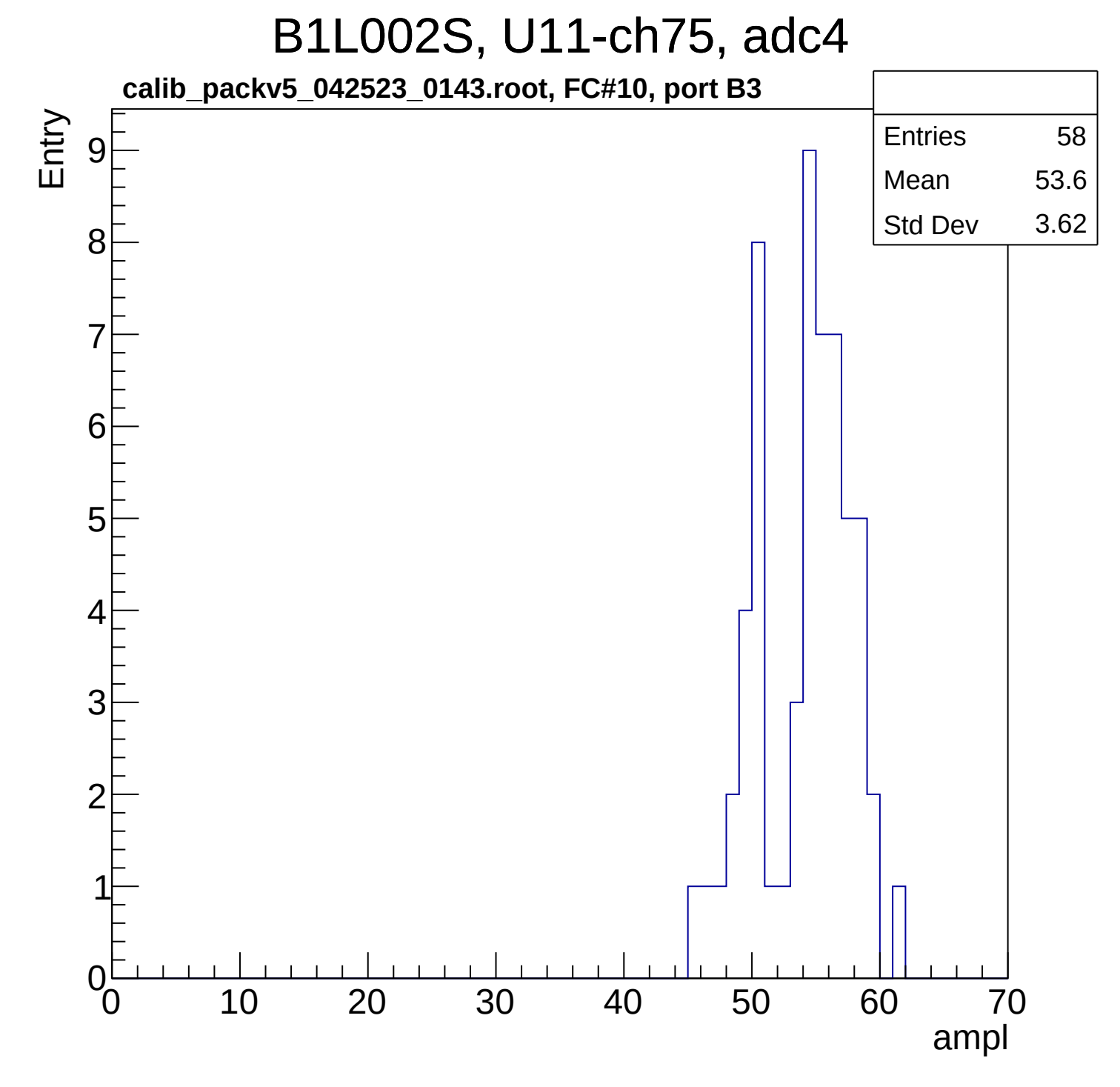
B1L002S, U11-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.6
Std Dev 3.62
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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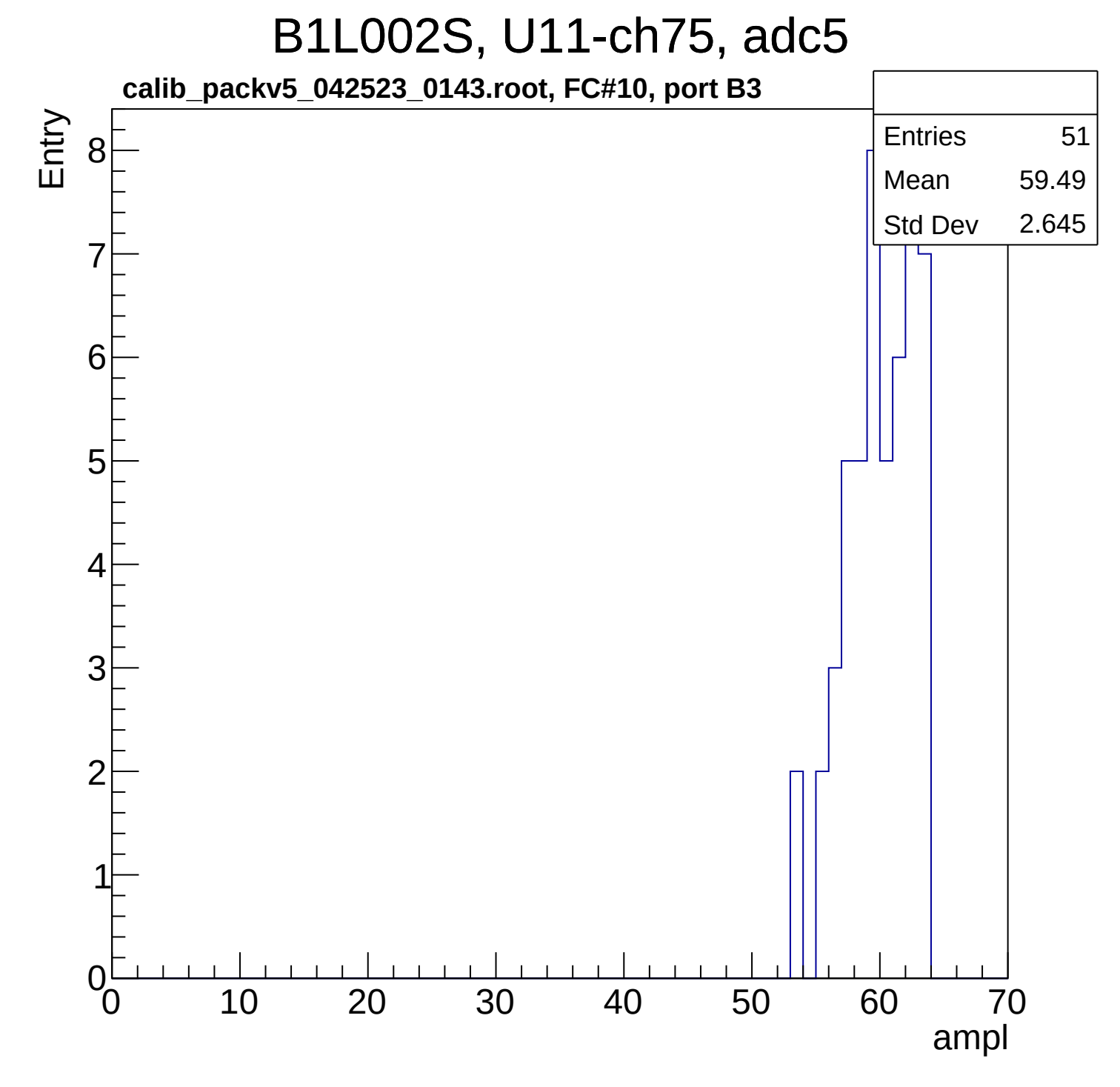
B1L002S, U11-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.49
Std Dev 2.645
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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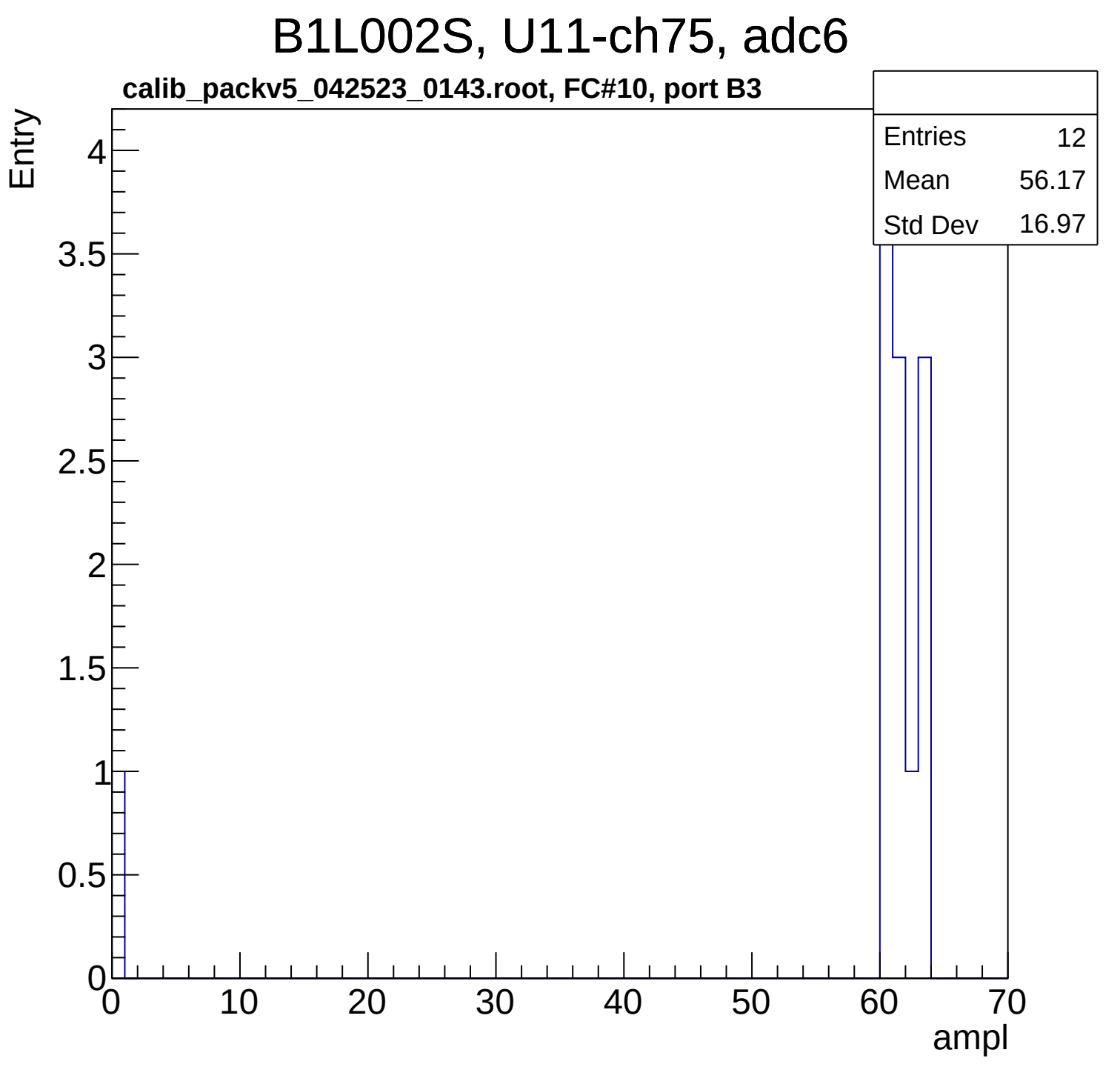
B1L002S, U11-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.17
Std Dev 16.97
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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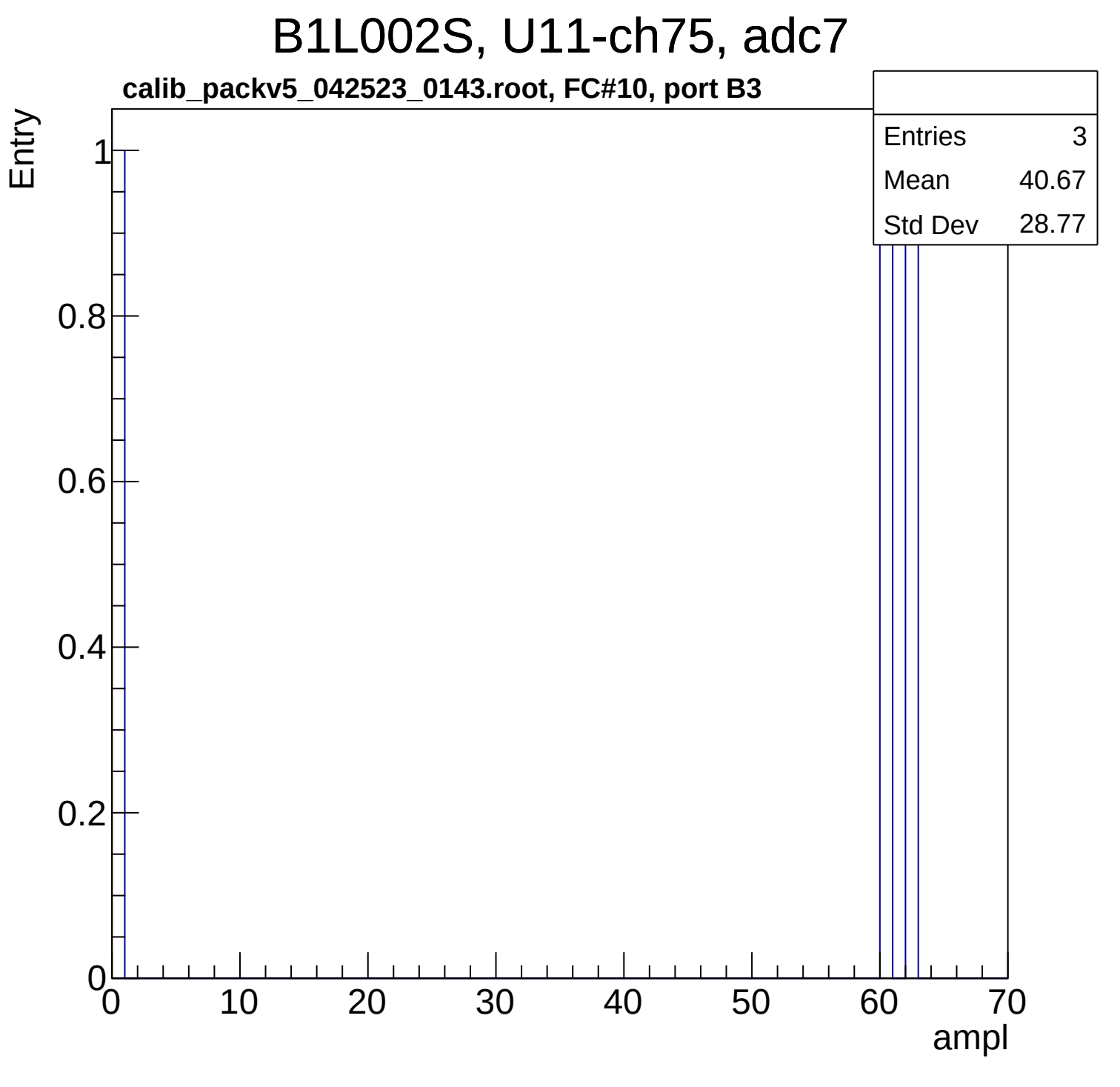
B1L002S, U11-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 40.67
Std Dev 28.77
Entry
ampl
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0.4
0.6
0.8
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0
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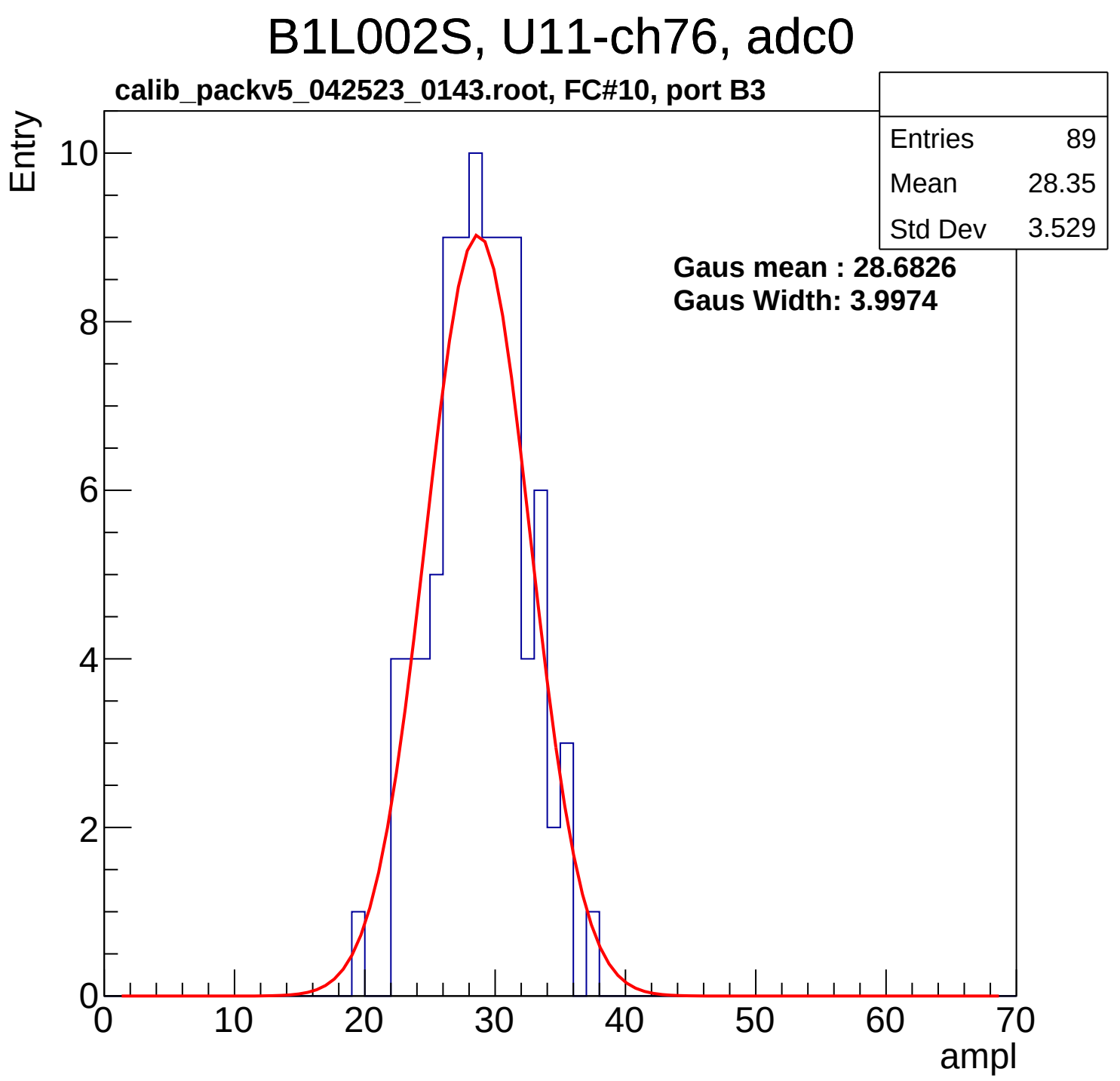
B1L002S, U11-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 89
Mean 28.35
Std Dev 3.529
Gaus mean : 28.6826
Gaus Width: 3.9974
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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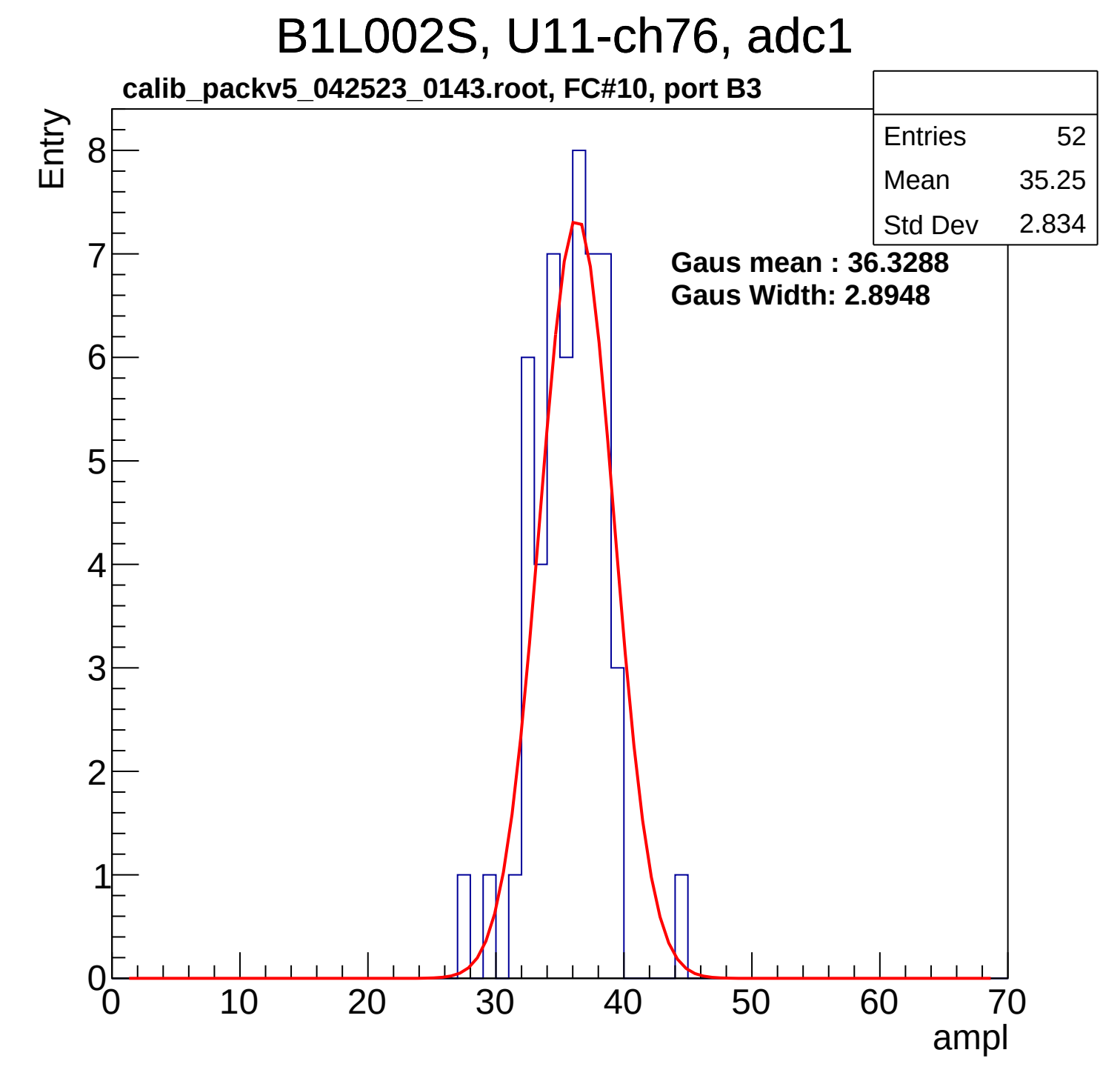
60
70


B1L002S, U11-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.25
Std Dev 2.834
Gaus mean : 36.3288
Gaus Width: 2.8948
Entry
ampl
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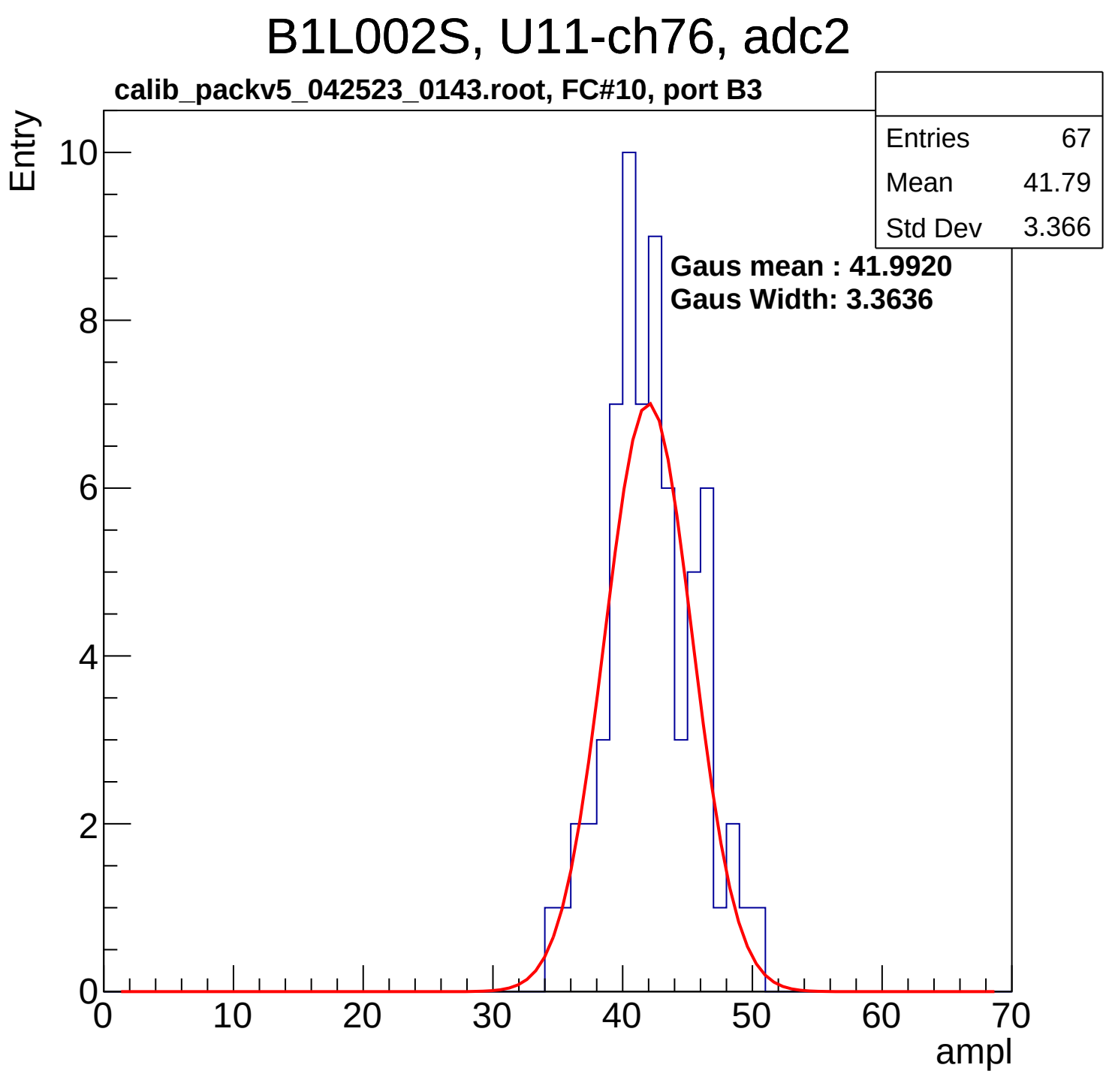
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.79
Std Dev 3.366
Gaus mean : 41.9920
Gaus Width: 3.3636
Entry
ampl
0
2
4
6
8
10
0
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60
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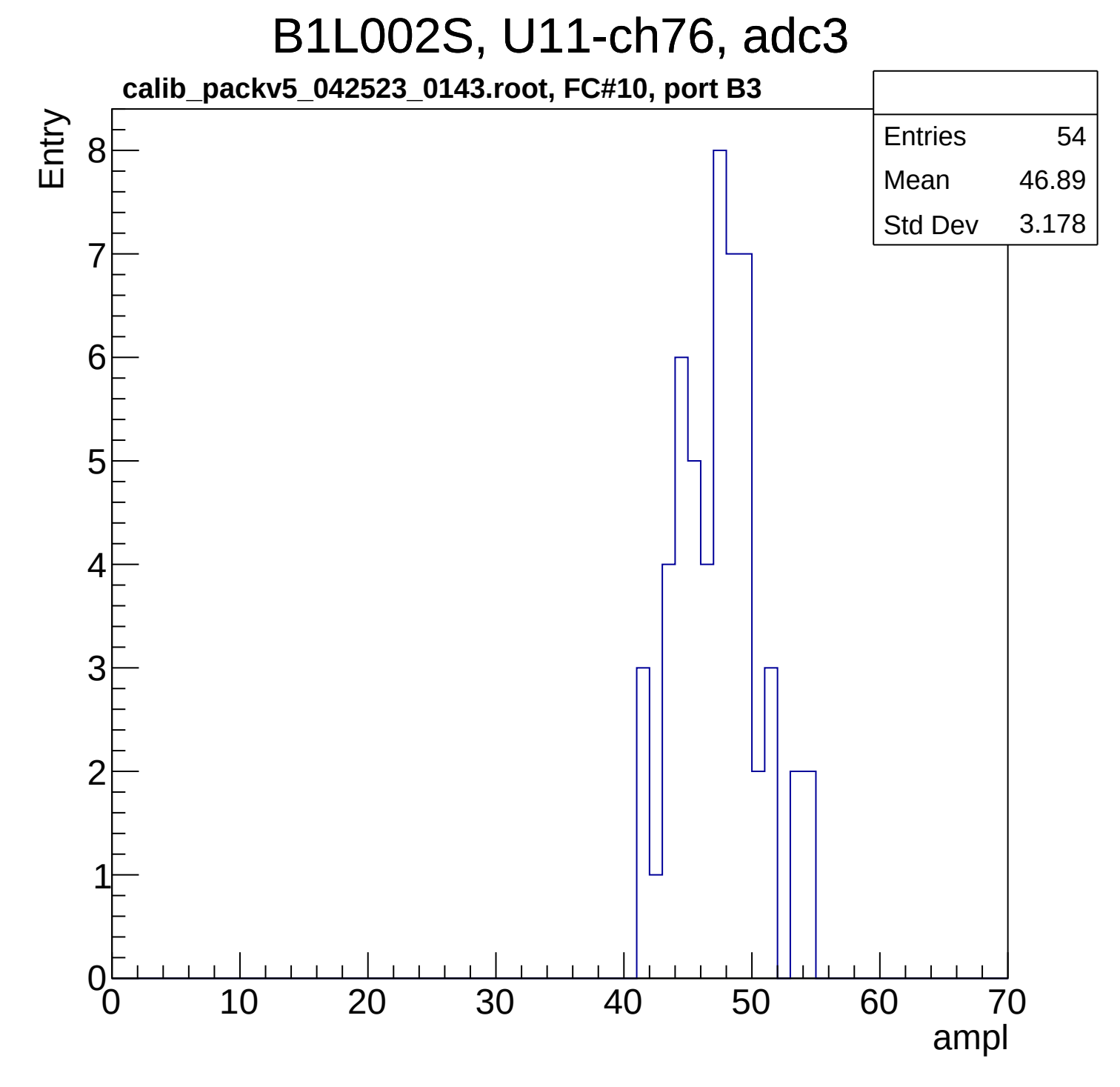
B1L002S, U11-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.89
Std Dev 3.178
Entry
ampl
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4
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6
7
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0
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20
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40
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60
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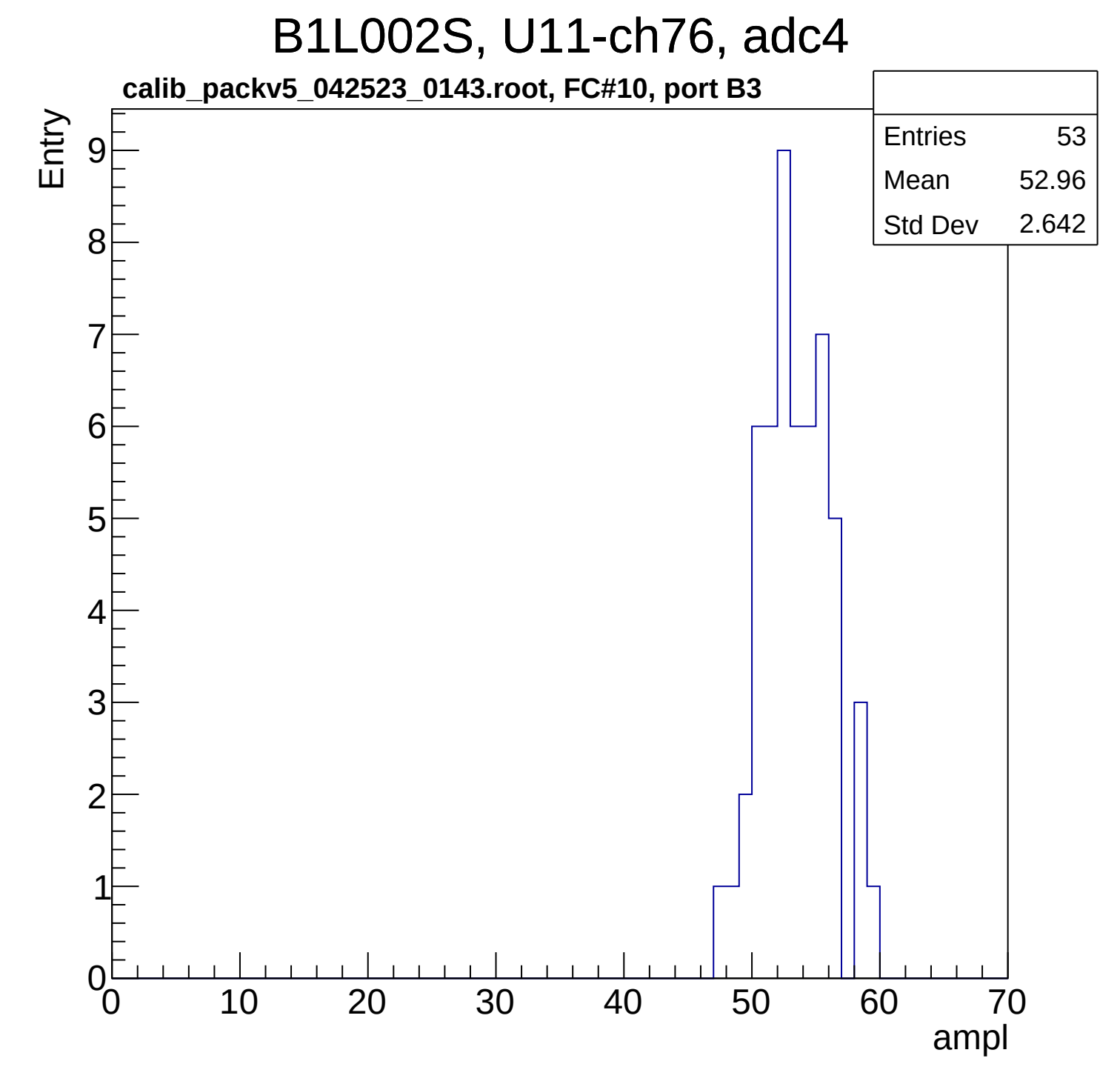
B1L002S, U11-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.96
Std Dev 2.642
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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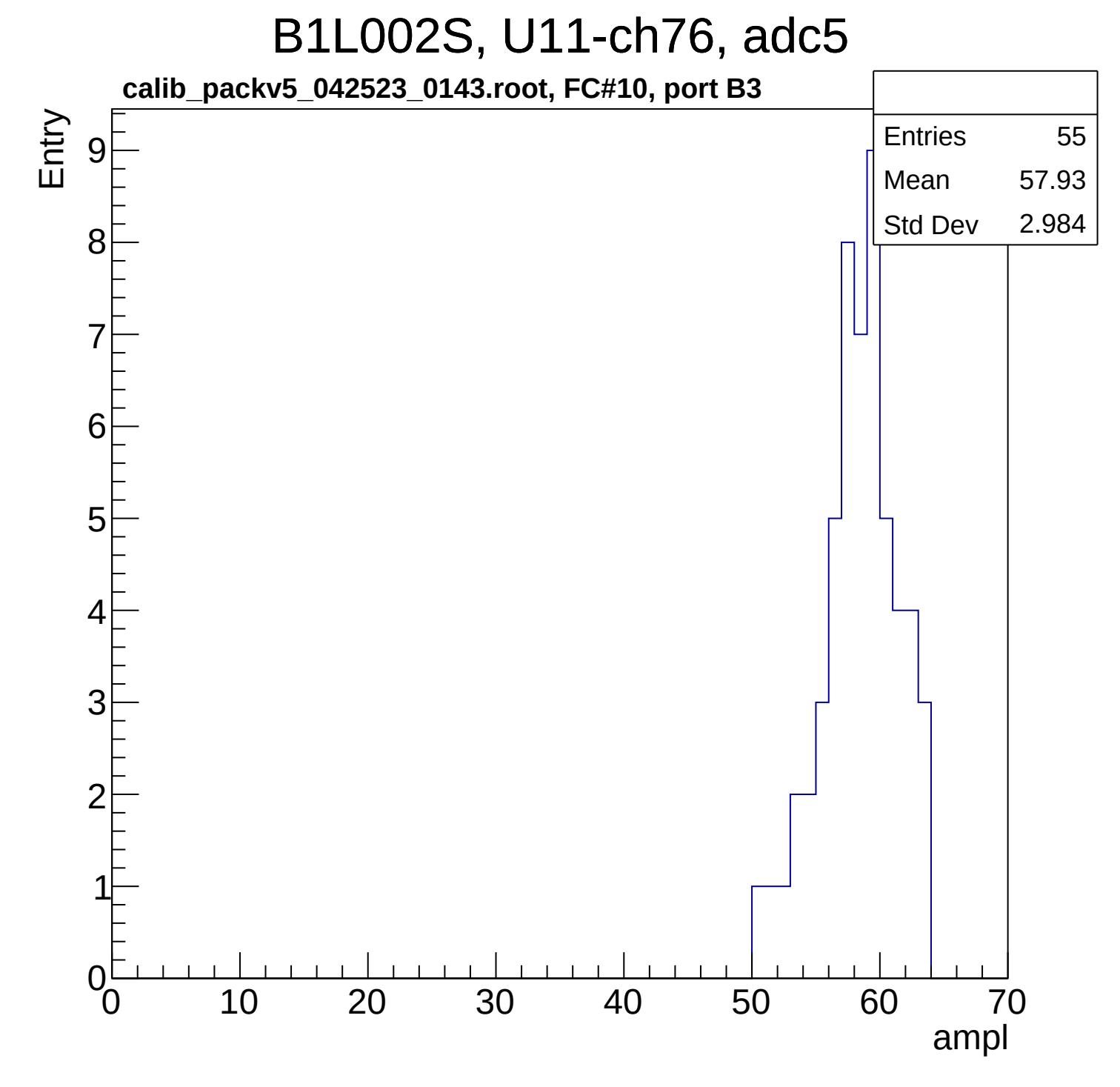
B1L002S, U11-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.93
Std Dev 2.984
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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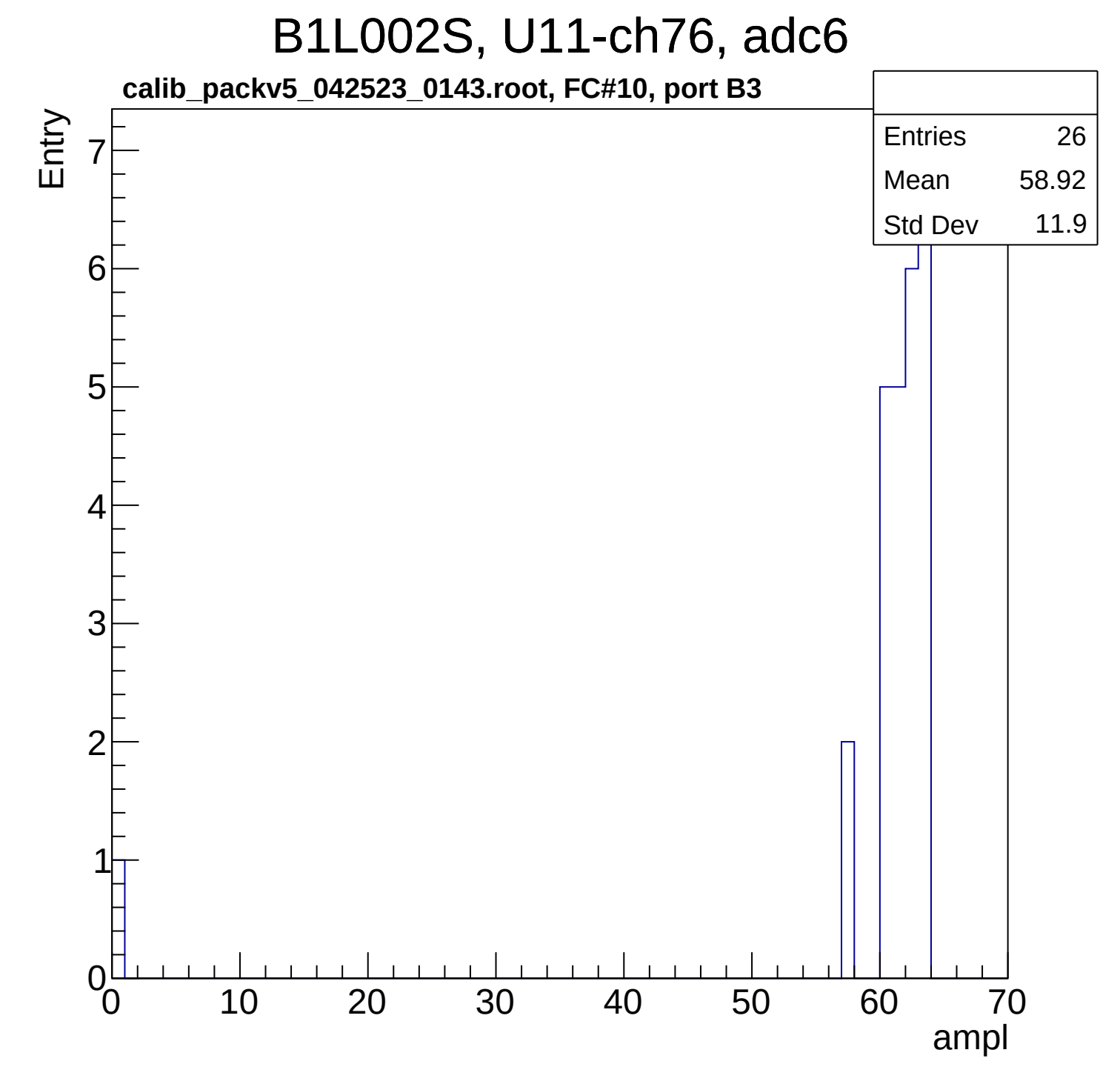
B1L002S, U11-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.92
Std Dev 11.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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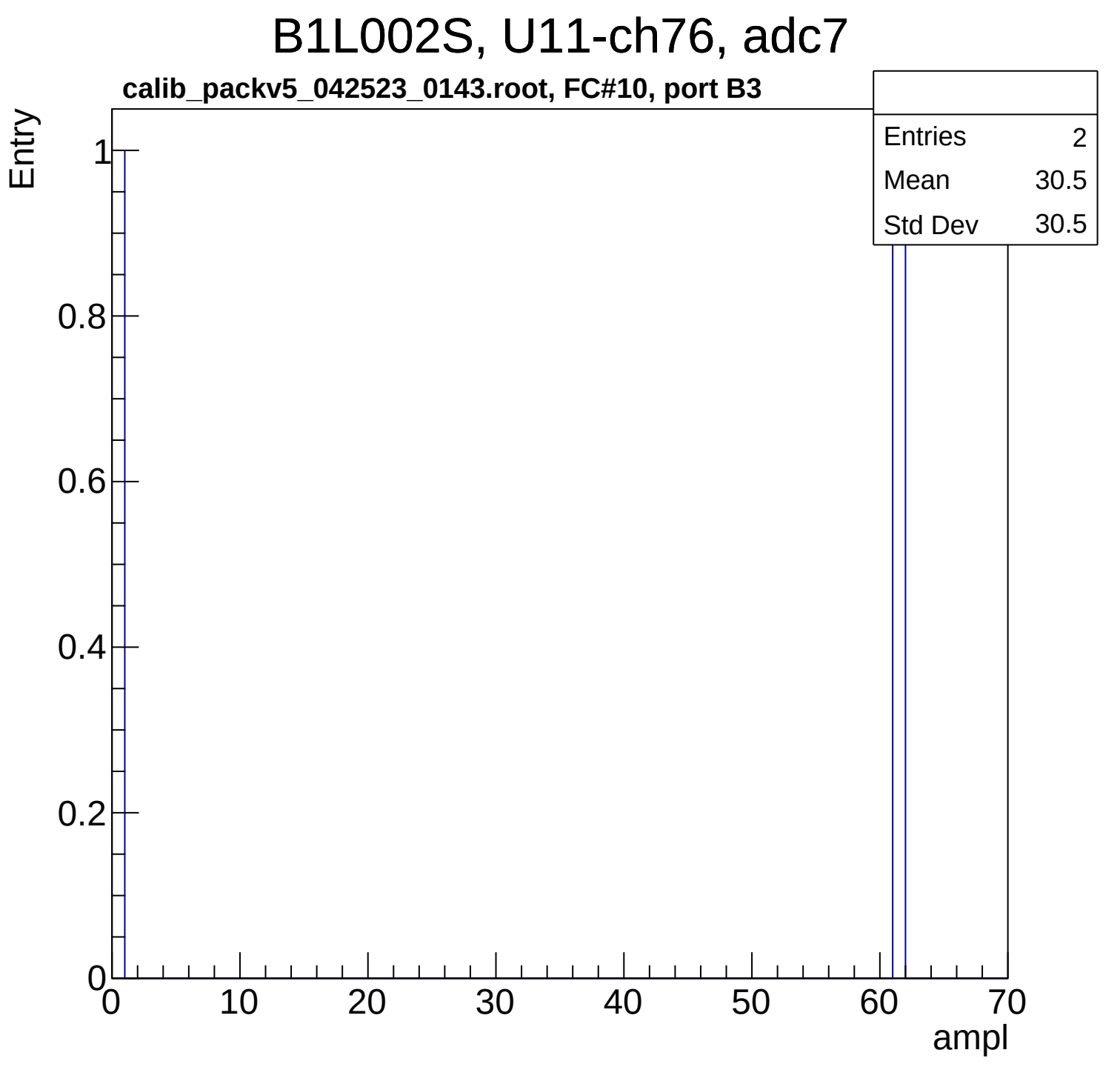
B1L002S, U11-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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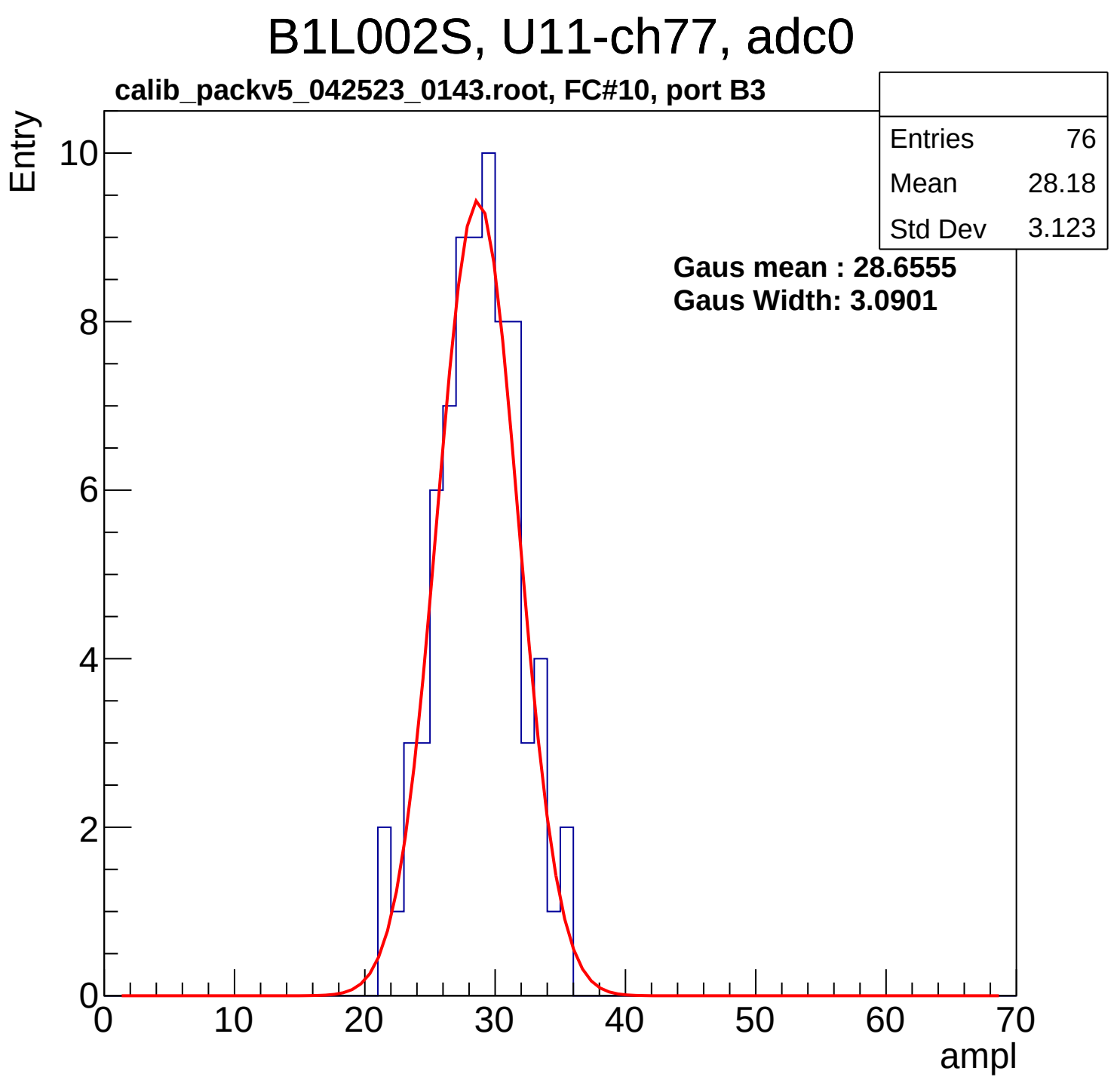
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 28.18
Std Dev 3.123
Gaus mean : 28.6555
Gaus Width: 3.0901
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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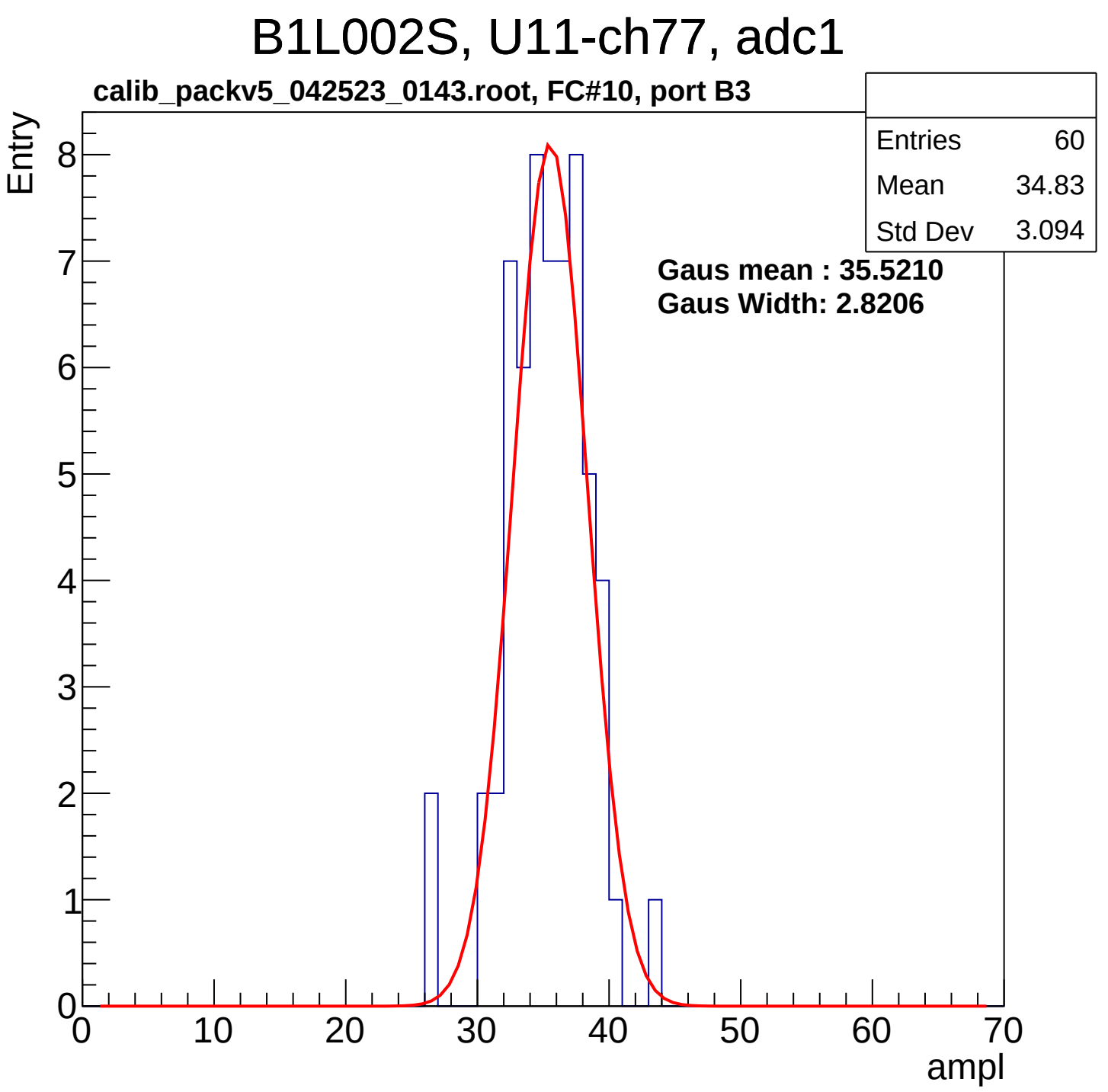
B1L002S, U11-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.83
Std Dev 3.094
Gaus mean : 35.5210
Gaus Width: 2.8206
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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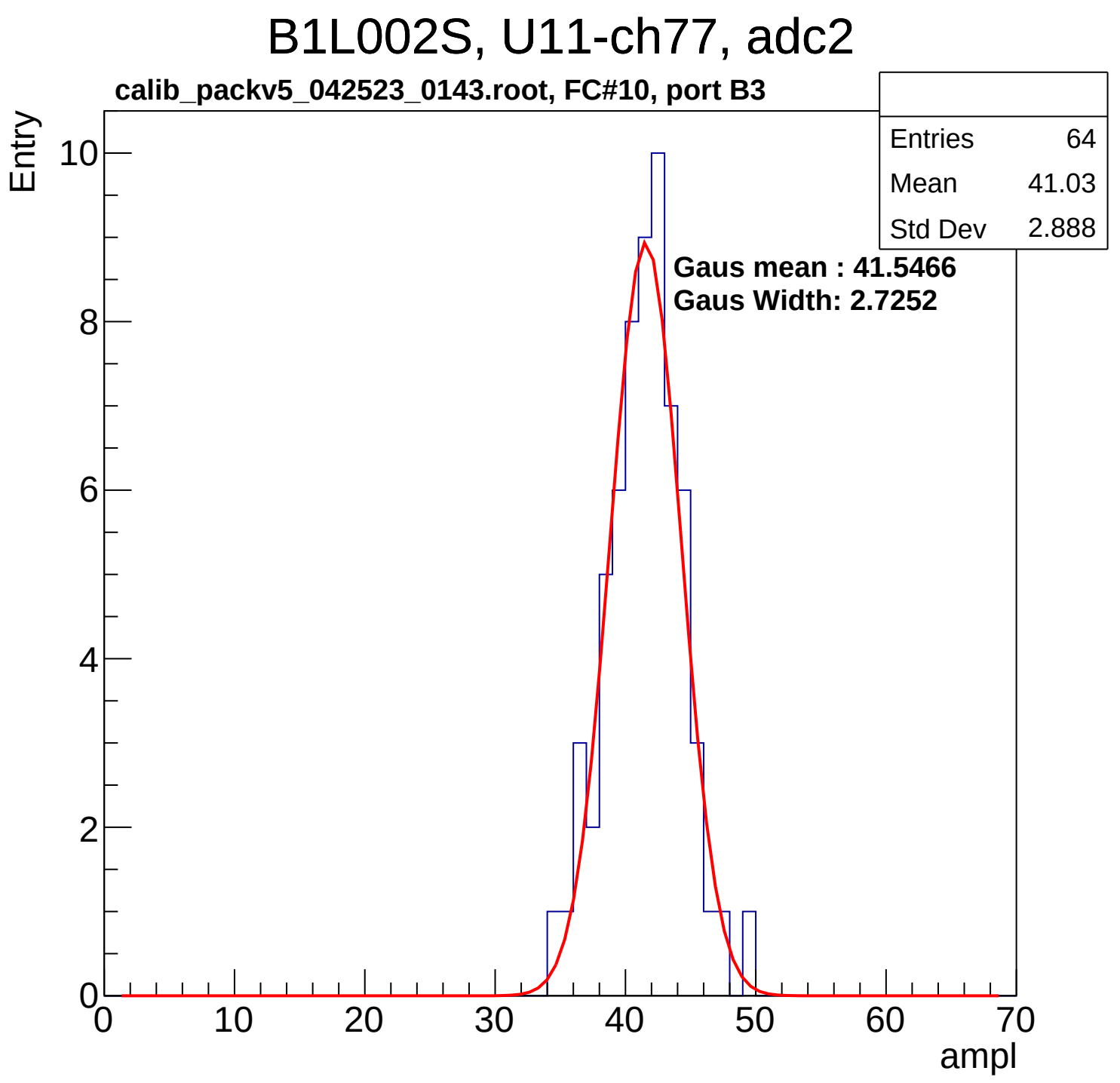
B1L002S, U11-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.03
Std Dev 2.888
Gaus mean : 41.5466
Gaus Width: 2.7252
Entry
ampl
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4
6
8
10
0
10
20
30
40
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60
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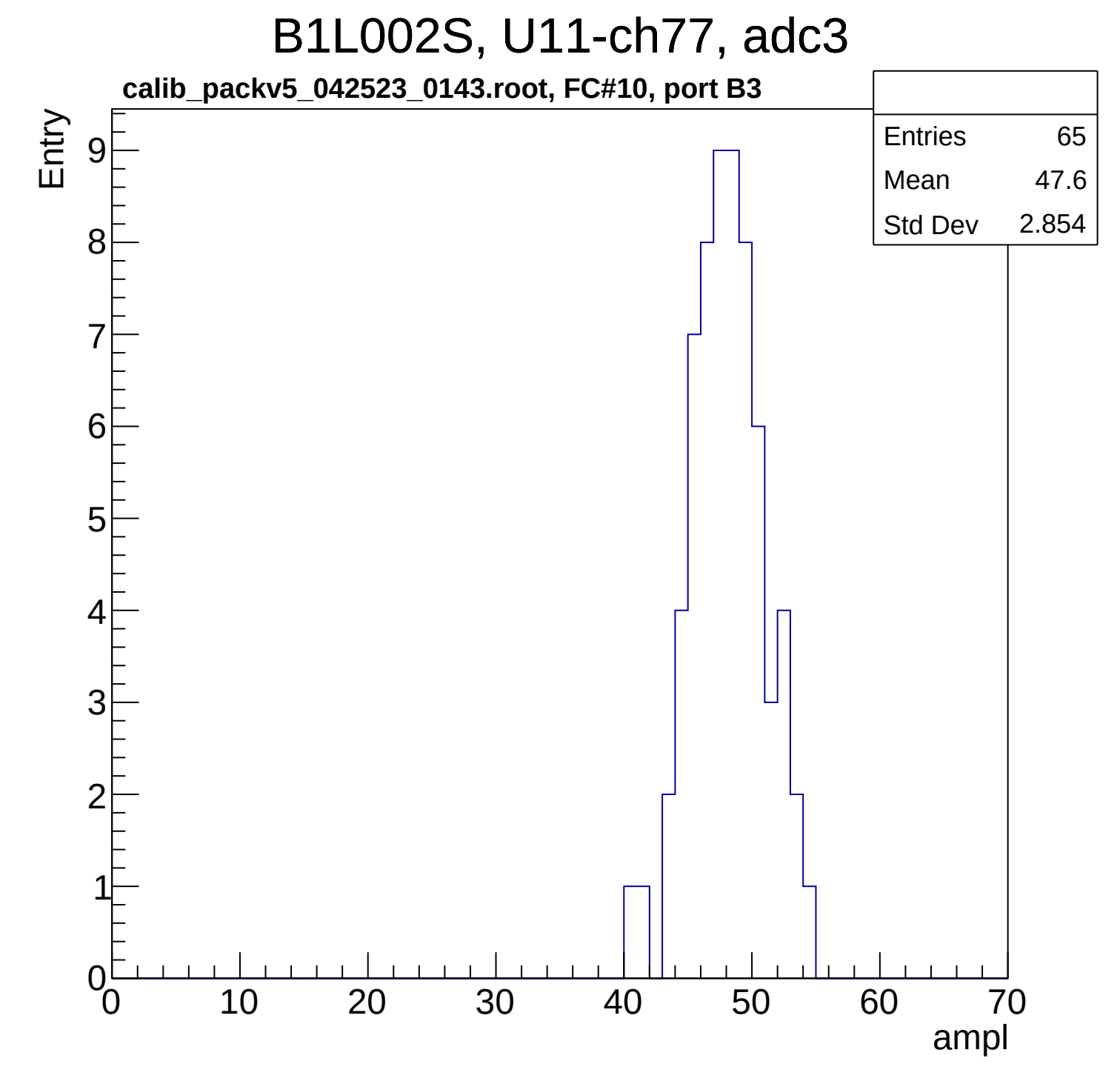
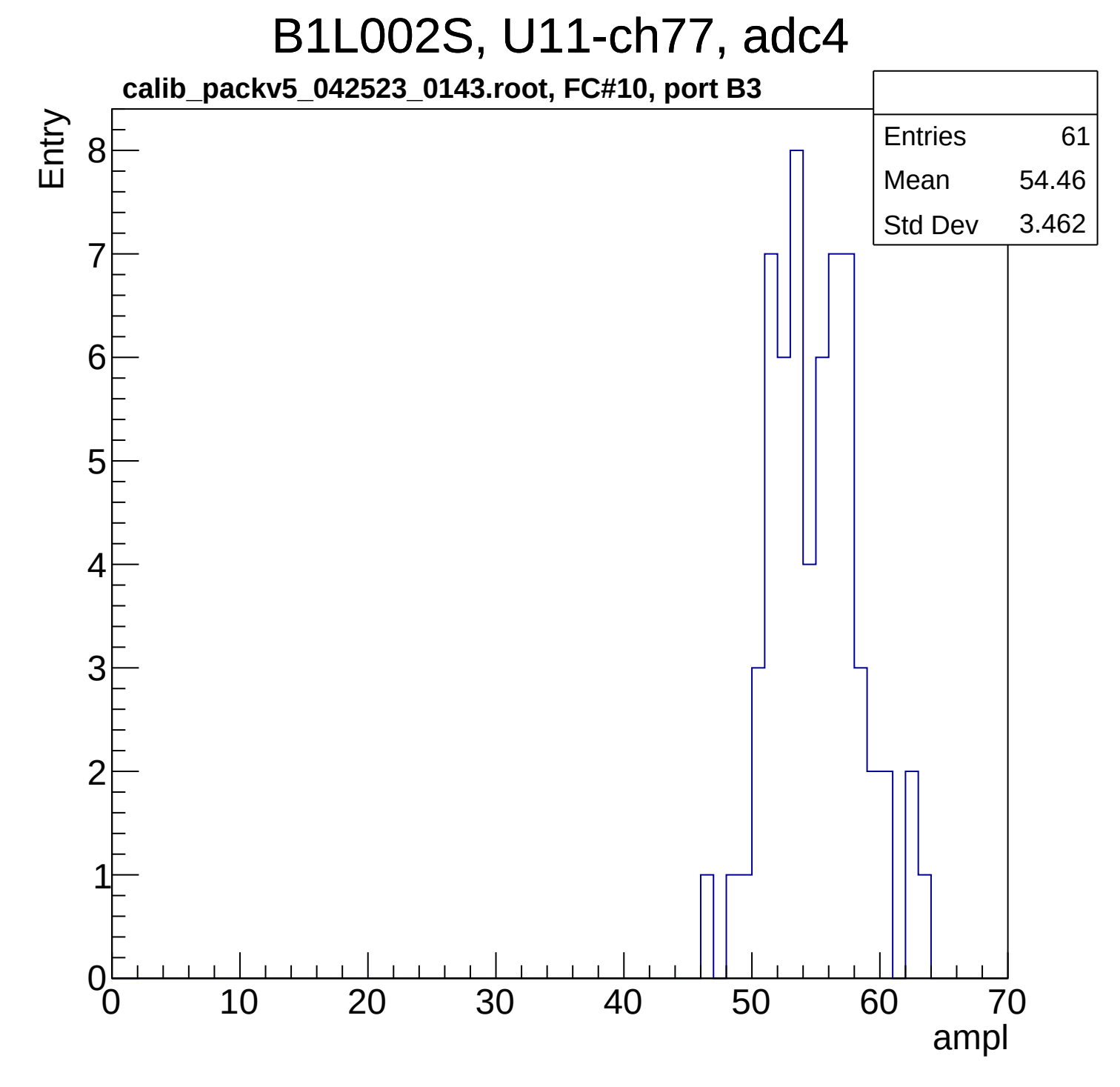

B1L002S, U11-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.6
Std Dev 2.854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.46
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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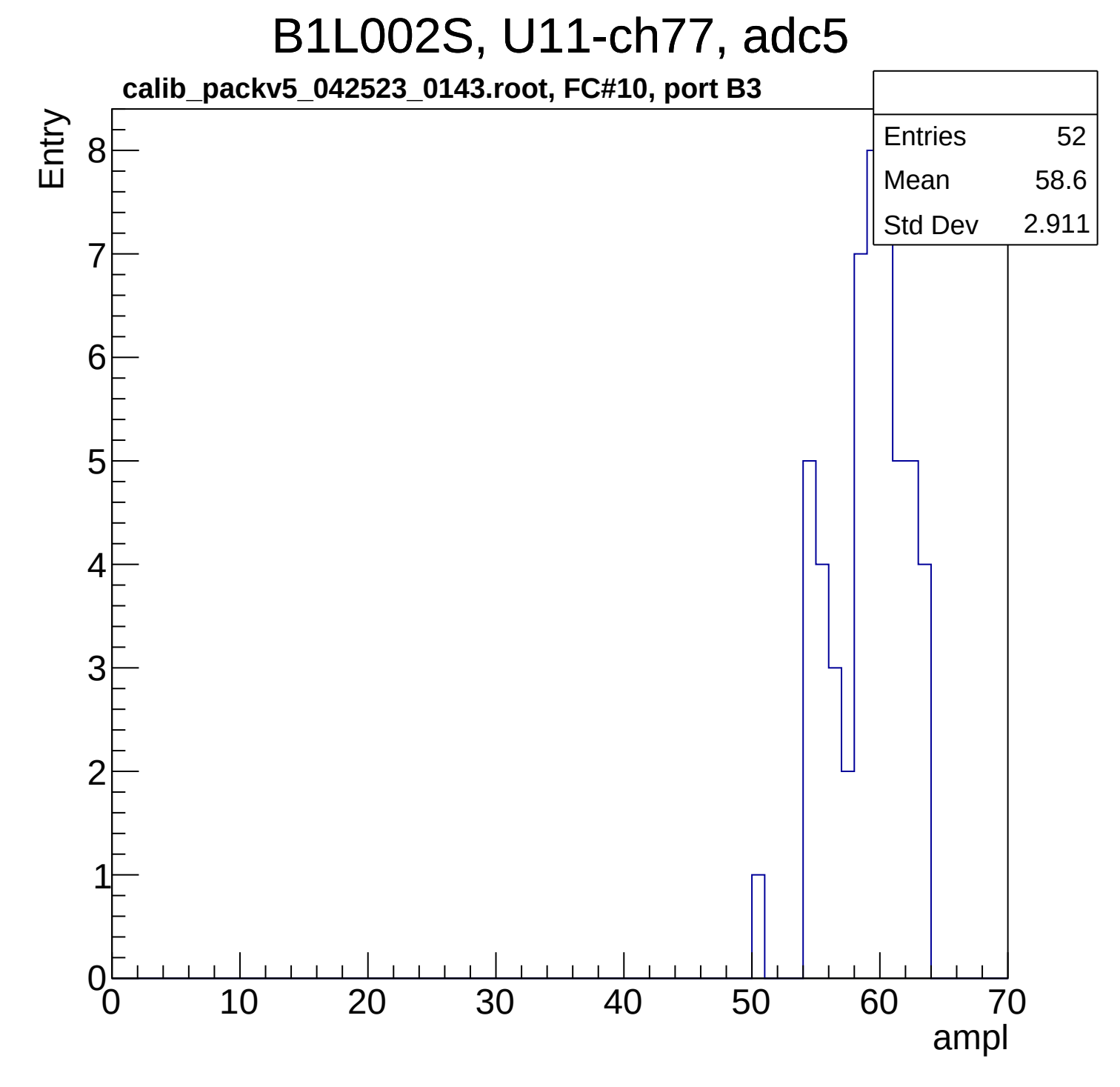
B1L002S, U11-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.6
Std Dev 2.911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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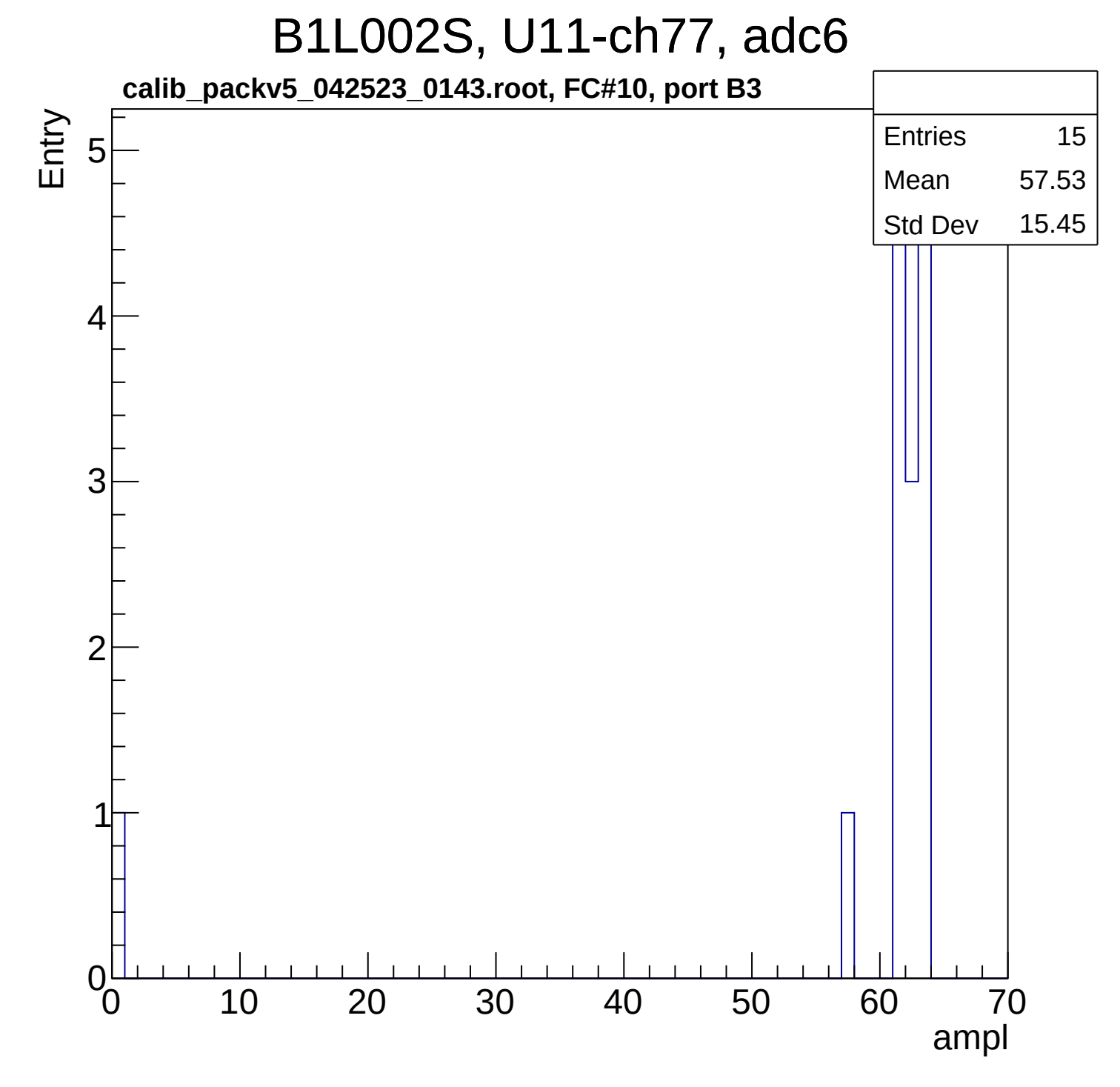
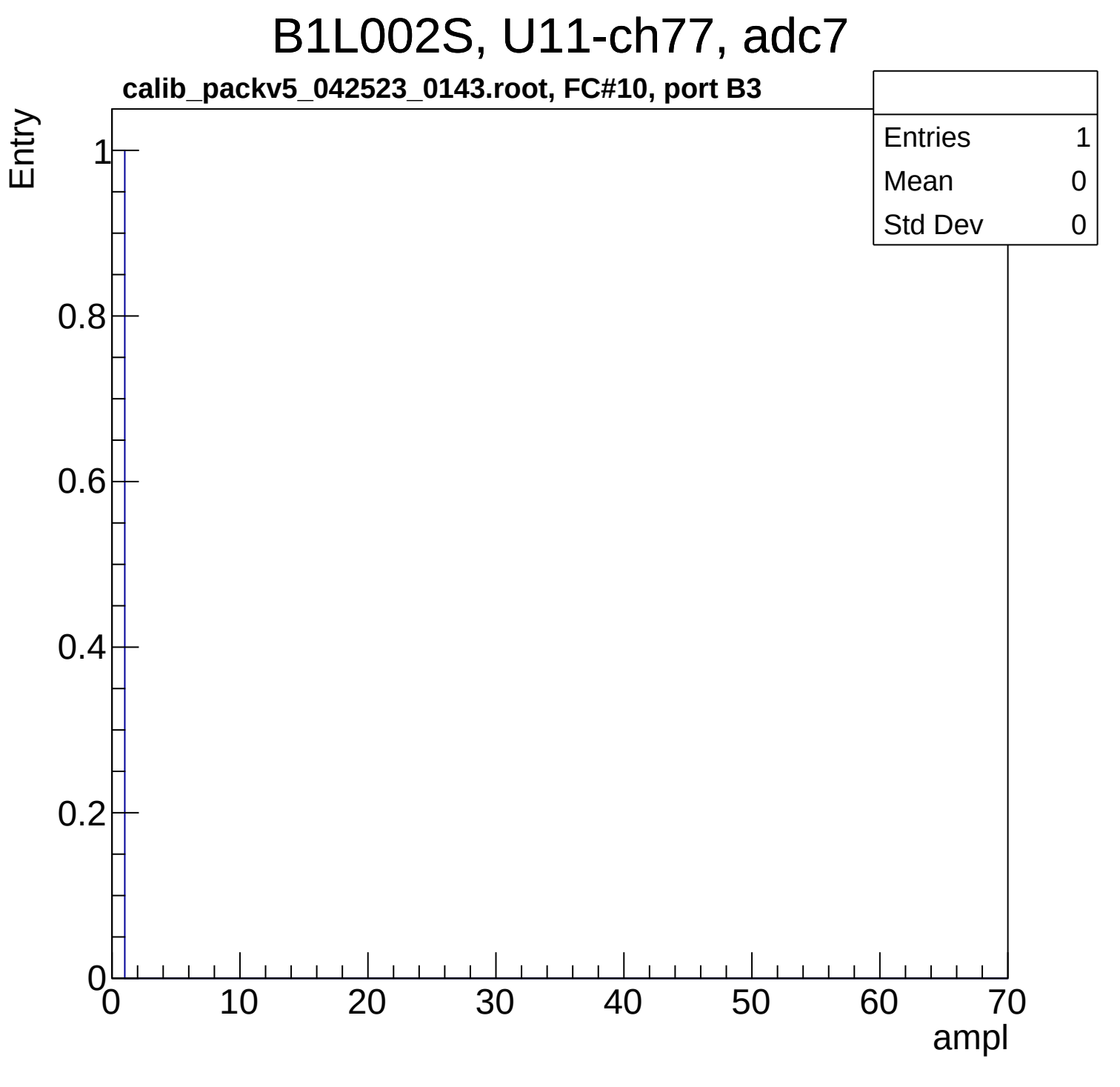
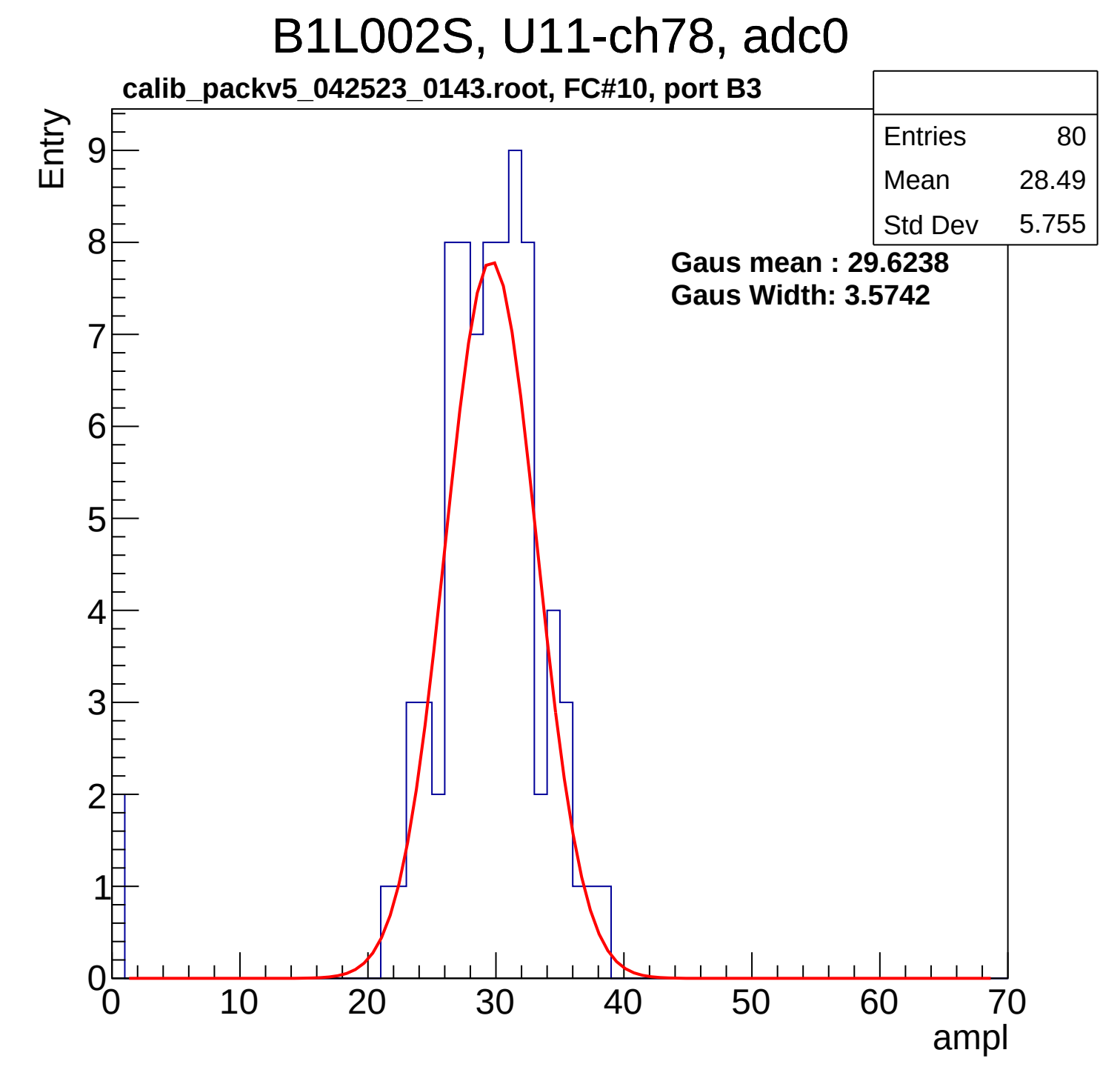
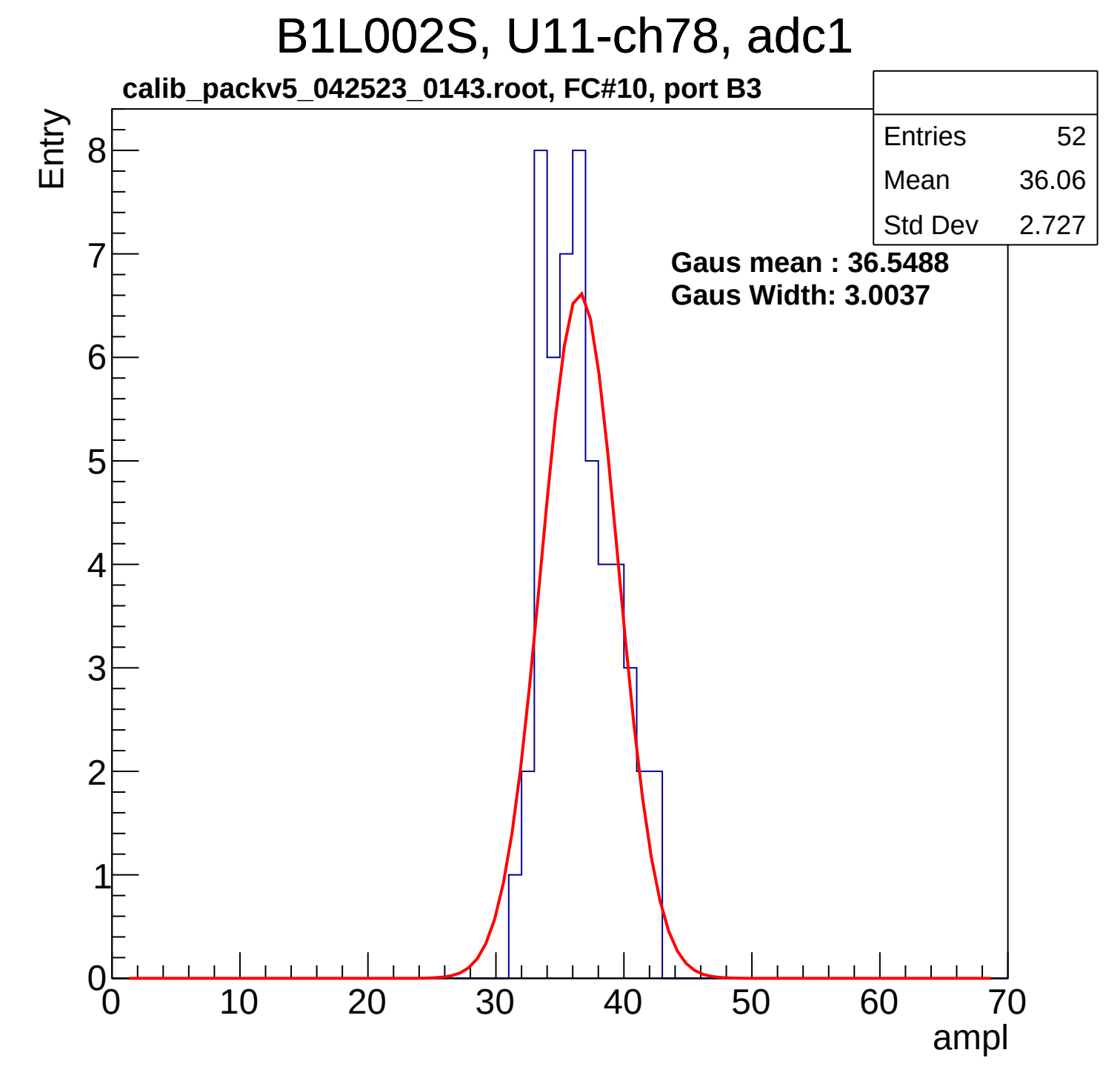
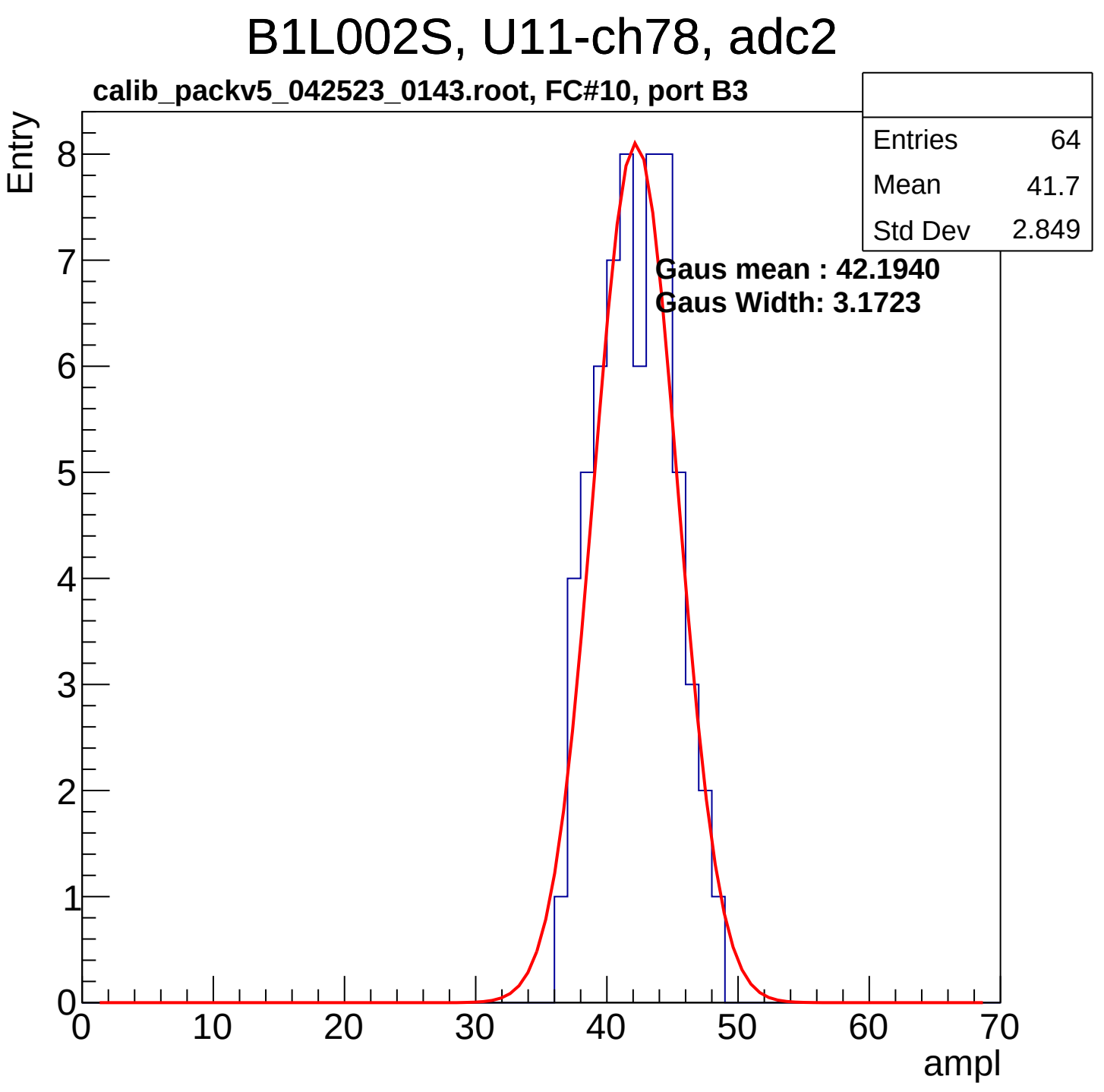
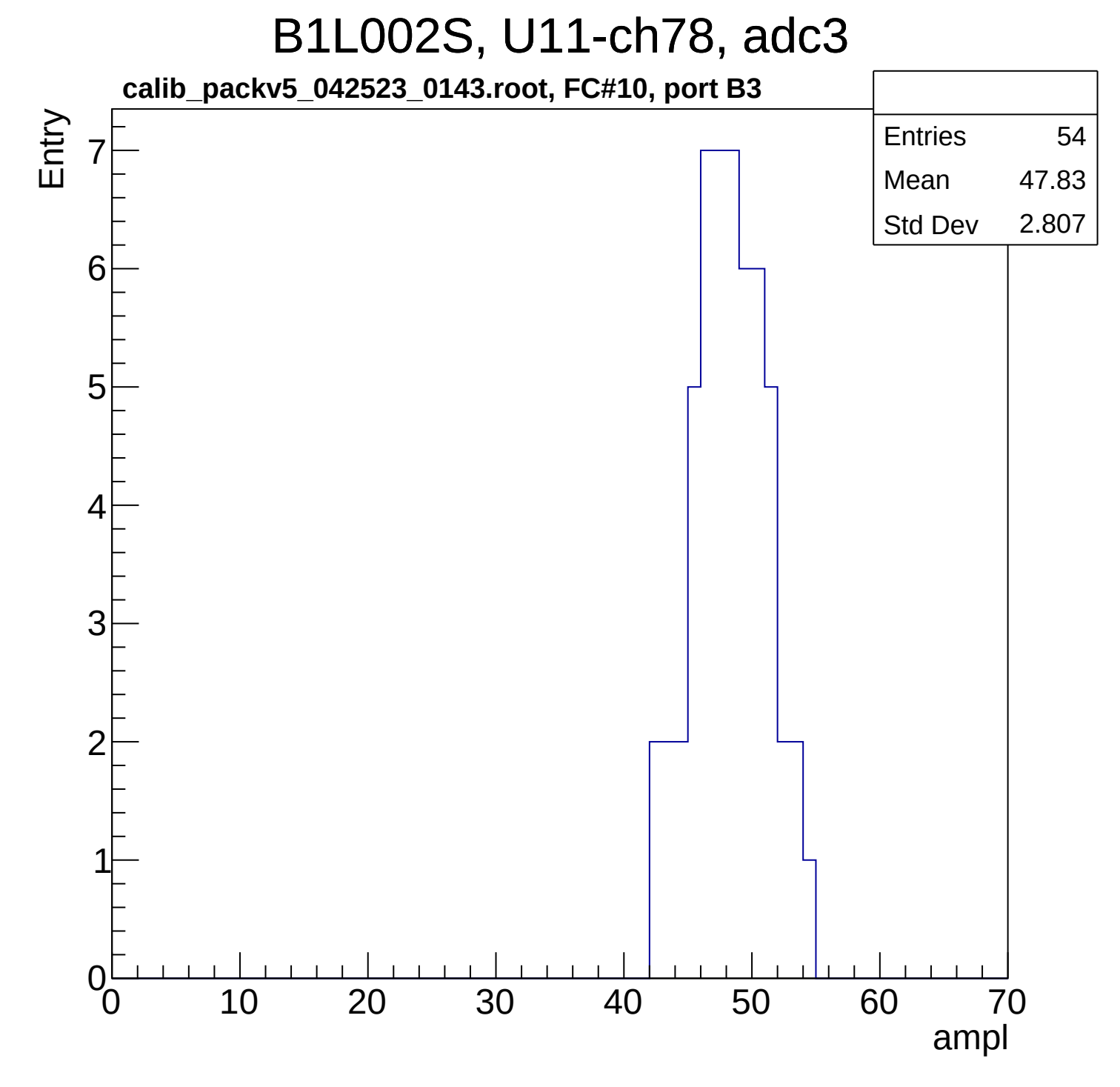

B1L002S, U11-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.53
Std Dev 15.45
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

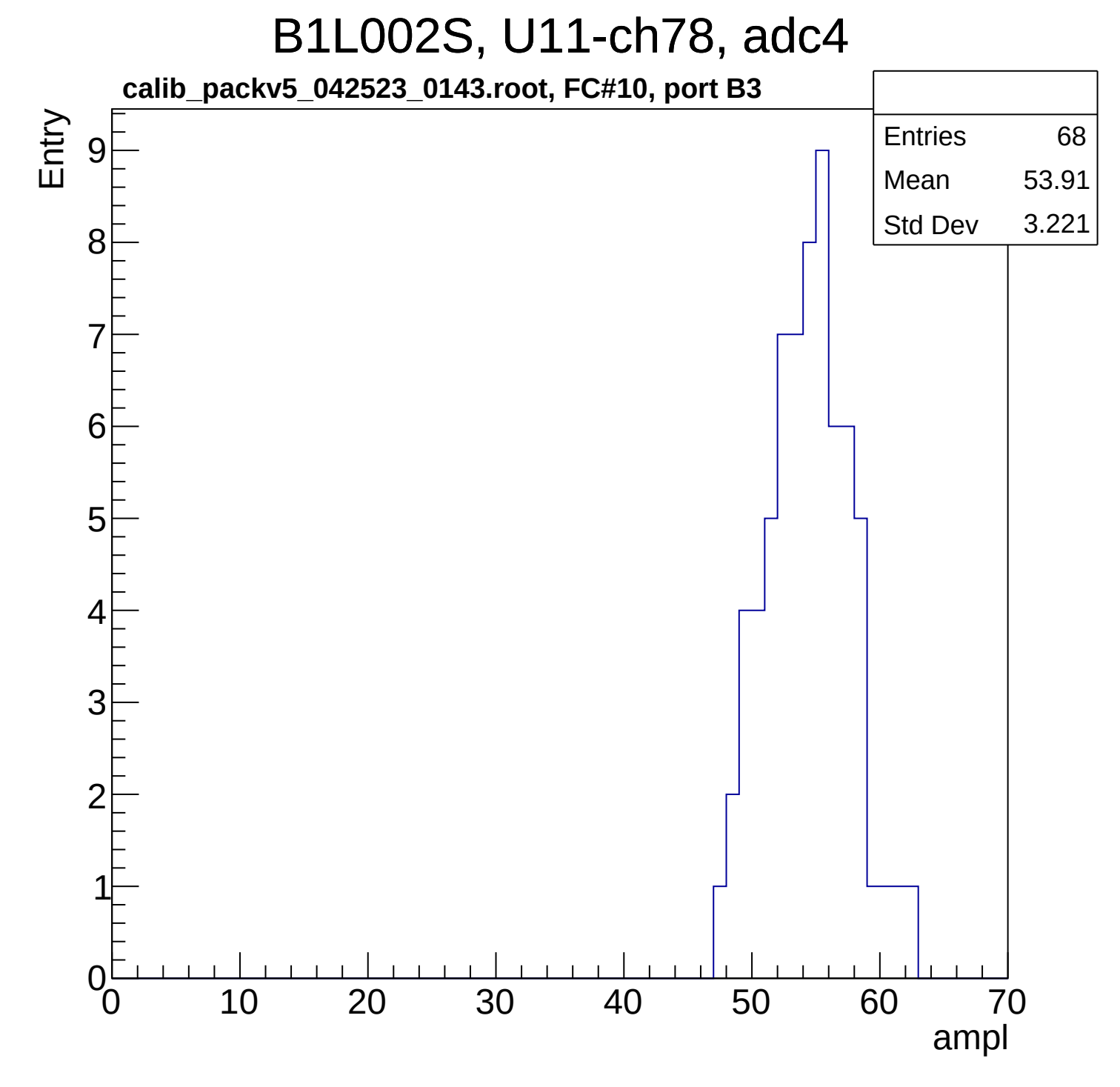

B1L002S, U11-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

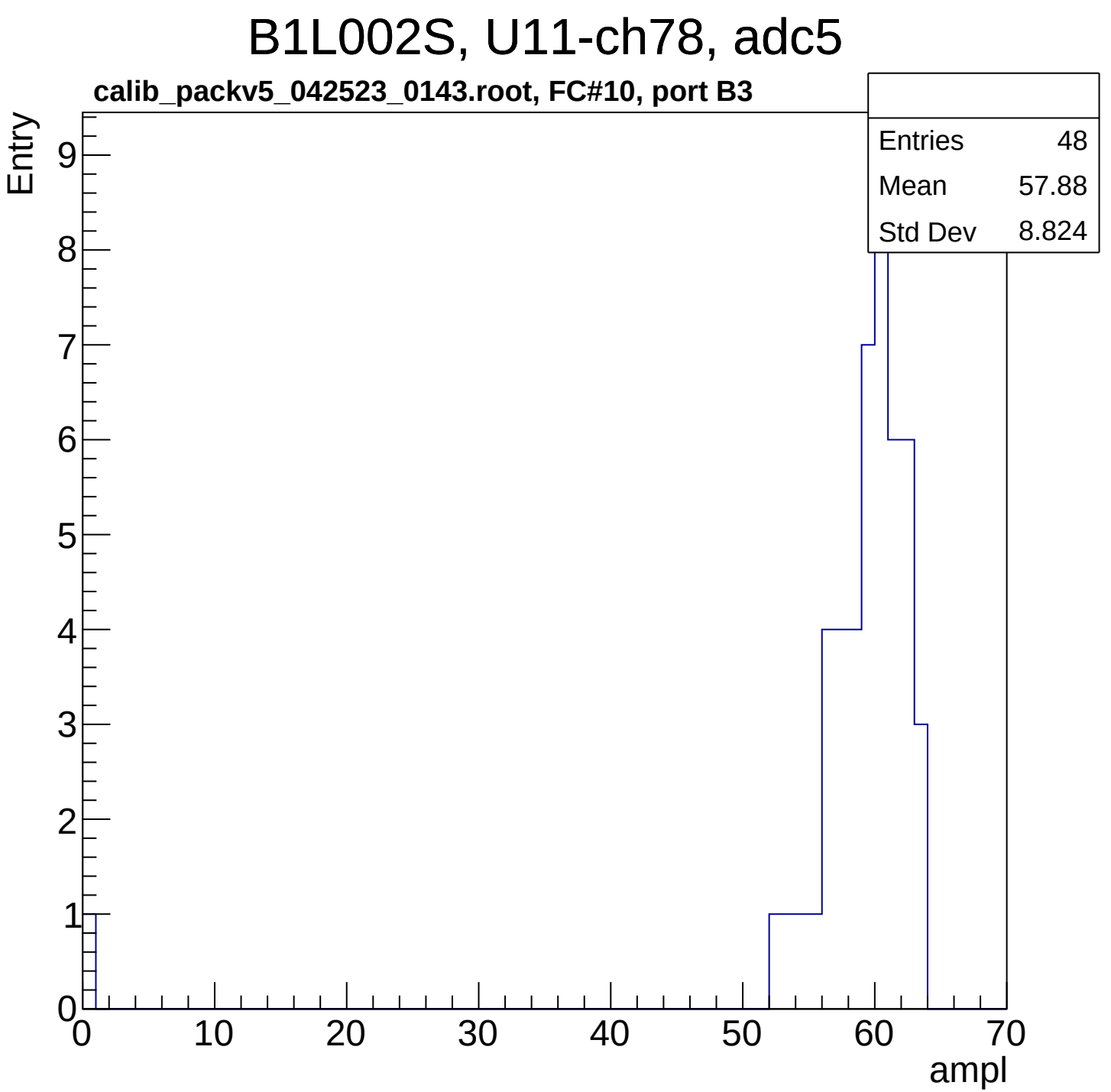

B1L002S, U11-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 28.49
Std Dev 5.755
Gaus mean : 29.6238
Gaus Width: 3.5742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

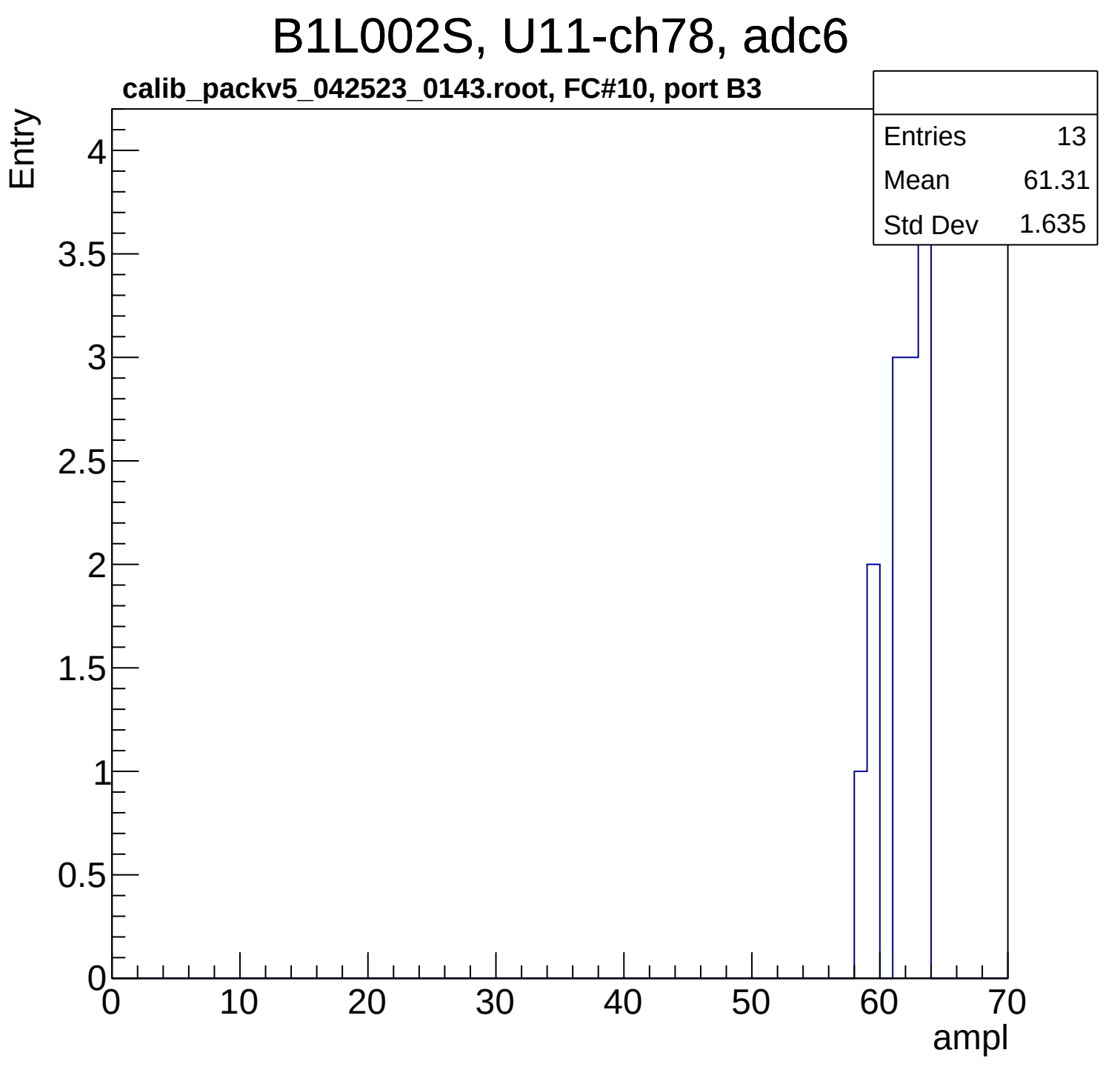

B1L002S, U11-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 36.06
Std Dev 2.727
Gaus mean : 36.5488
Gaus Width: 3.0037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

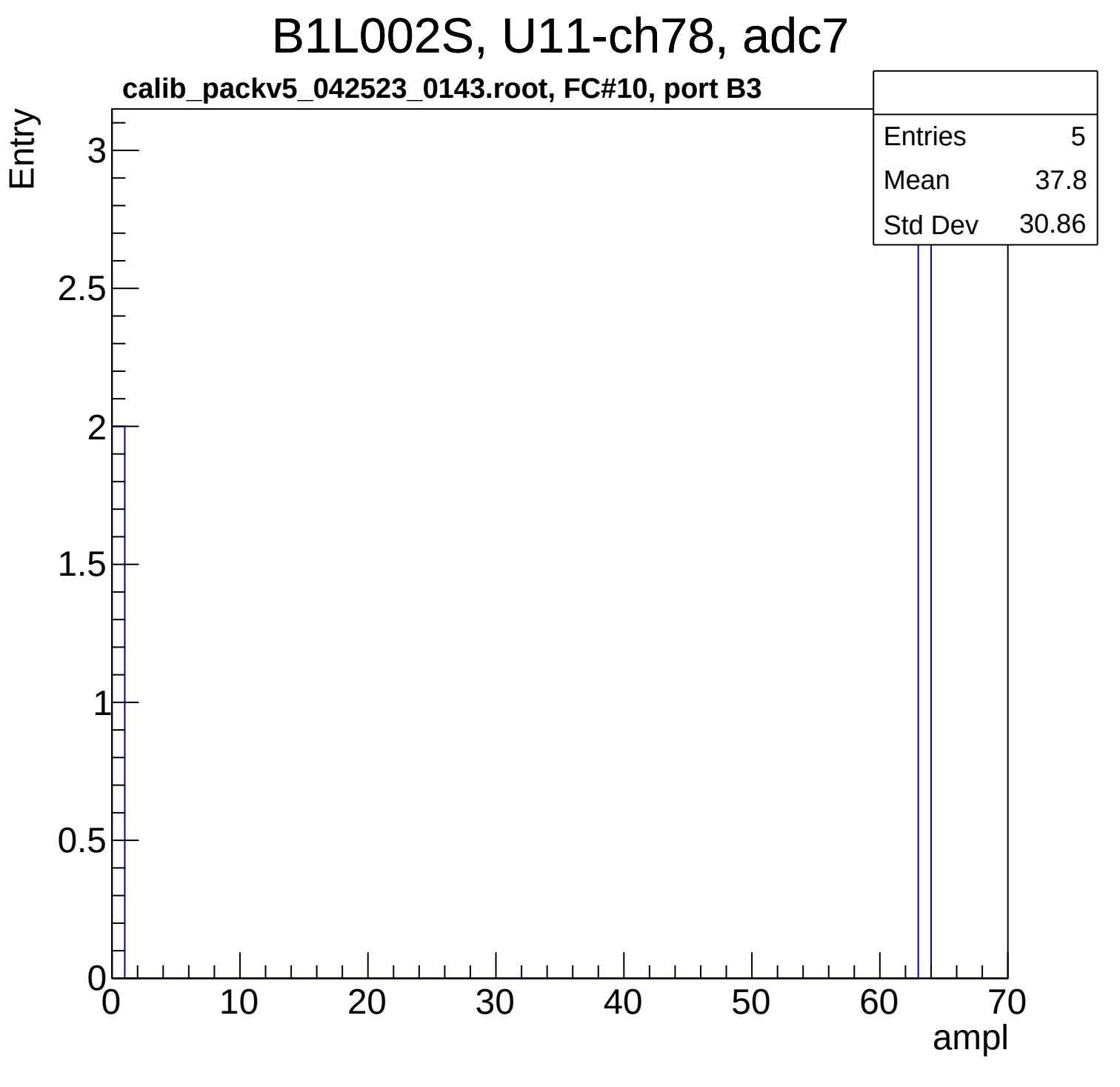

B1L002S, U11-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.7
Std Dev 2.849
Gaus mean : 42.1940
Gaus Width: 3.1723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

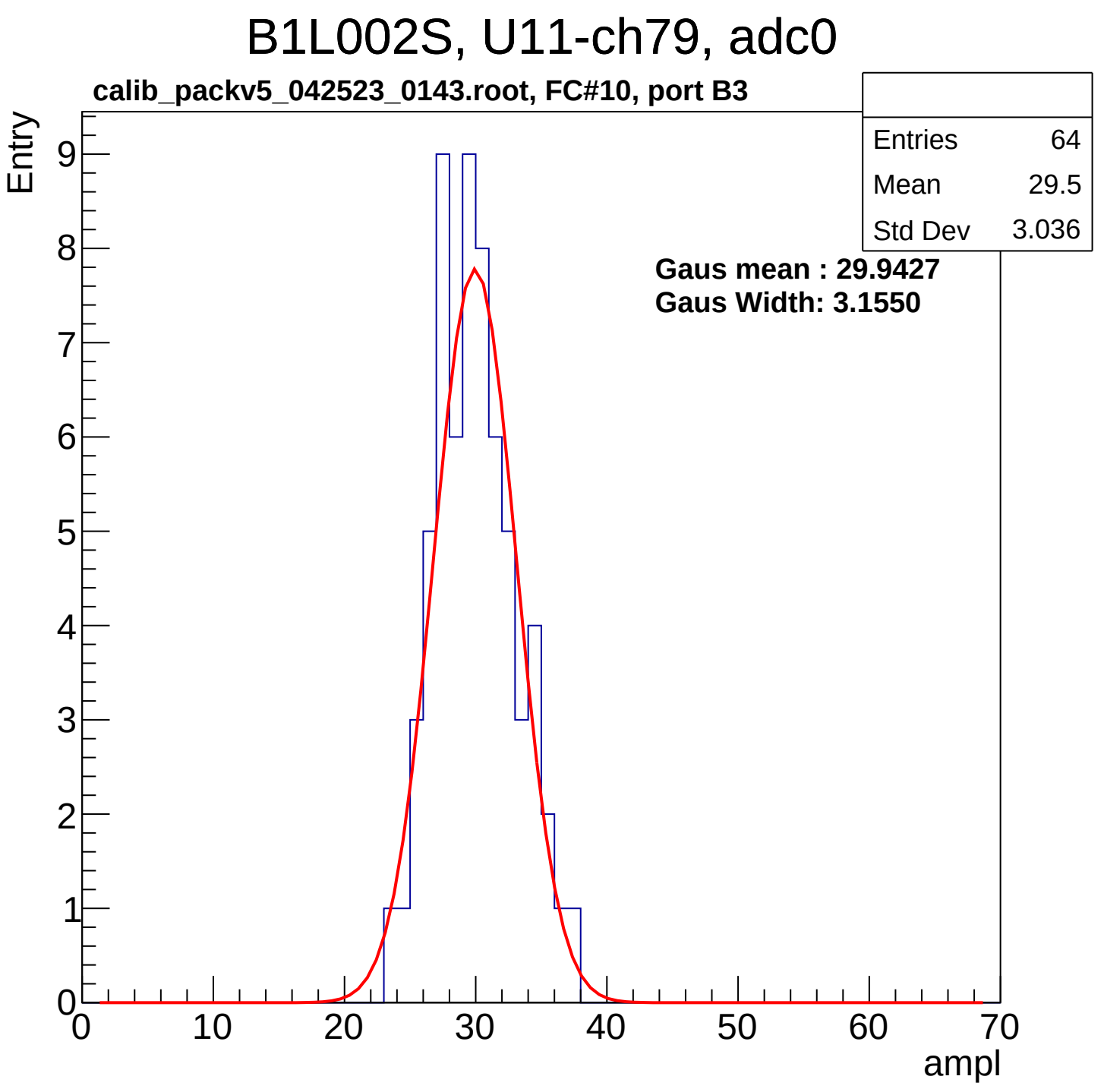

B1L002S, U11-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.83
Std Dev 2.807
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

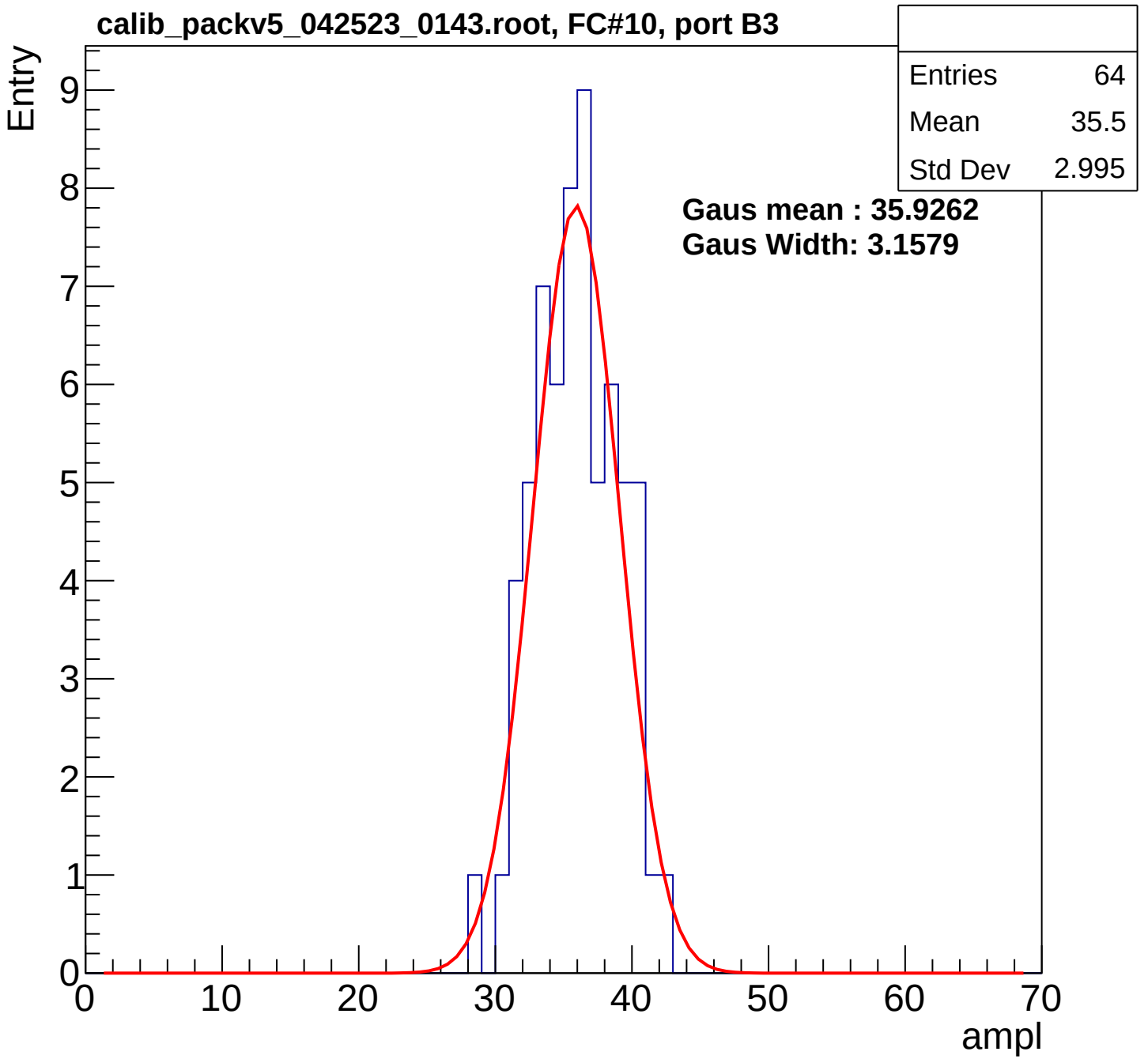

B1L002S, U11-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.91
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

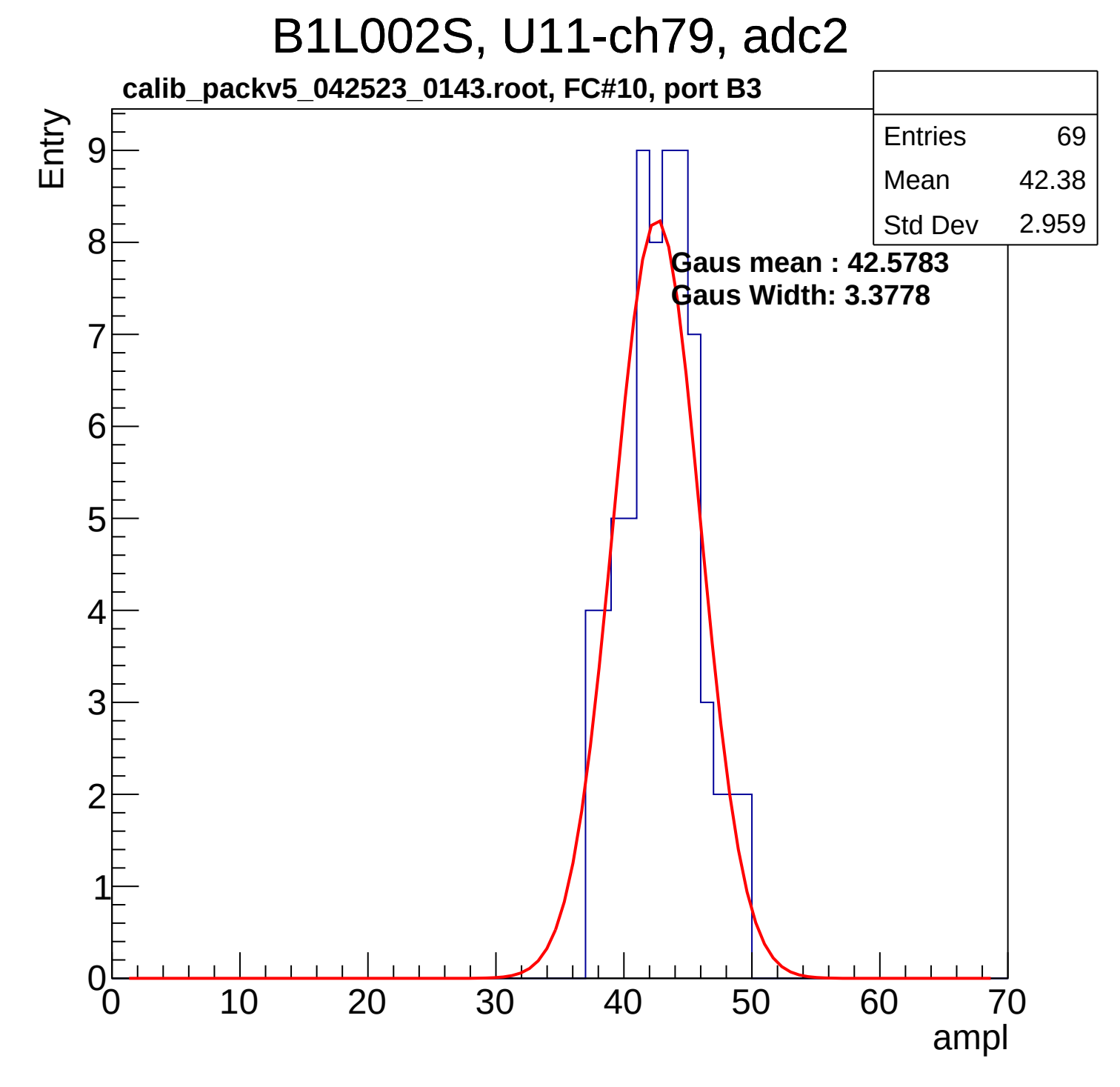

B1L002S, U11-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.88
Std Dev 8.824
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

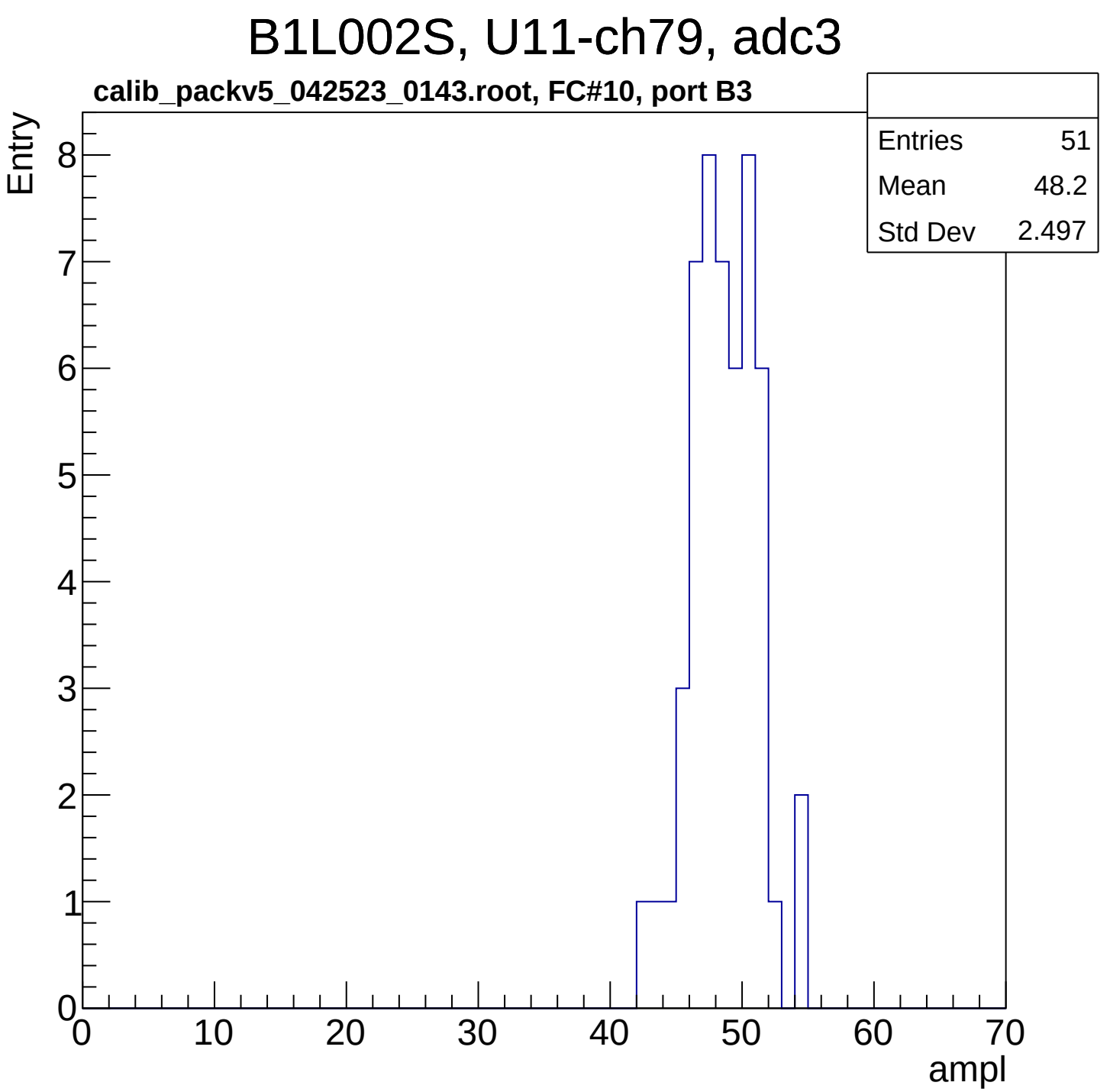

B1L002S, U11-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.31
Std Dev 1.635
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

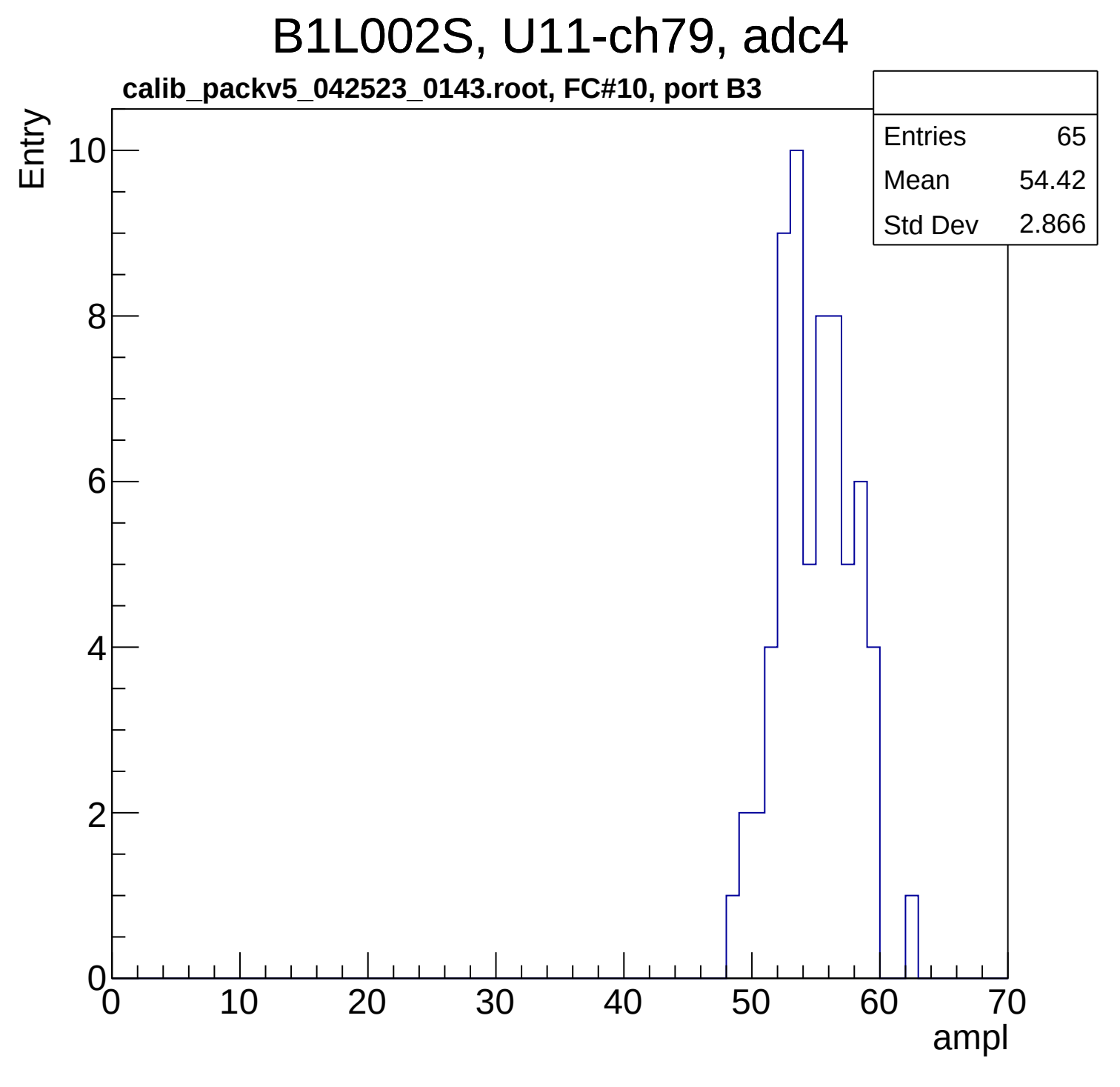

B1L002S, U11-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.8
Std Dev 30.86
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

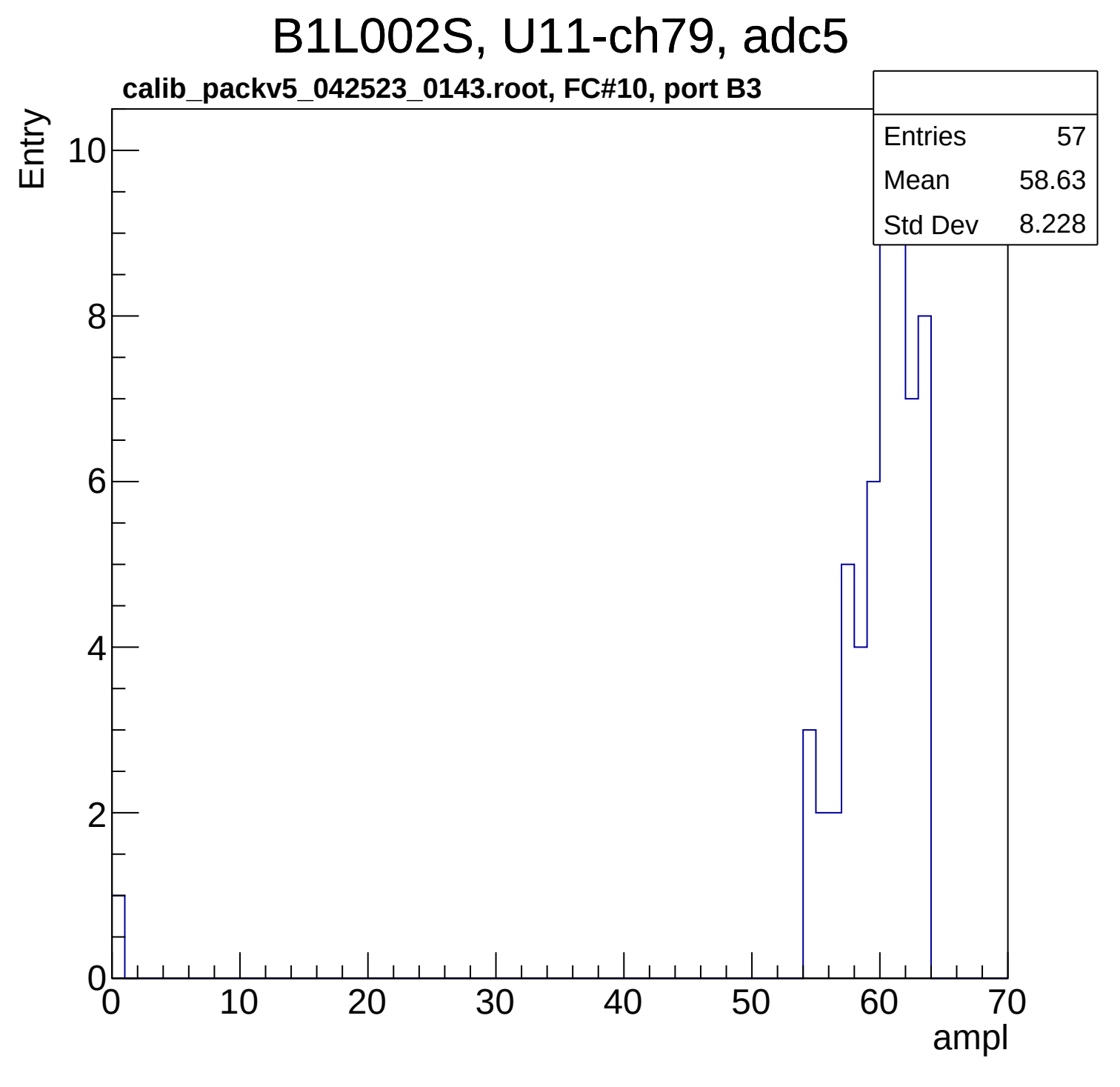

B1L002S, U11-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 29.5
Std Dev 3.036
Gaus mean : 29.9427
Gaus Width: 3.1550
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

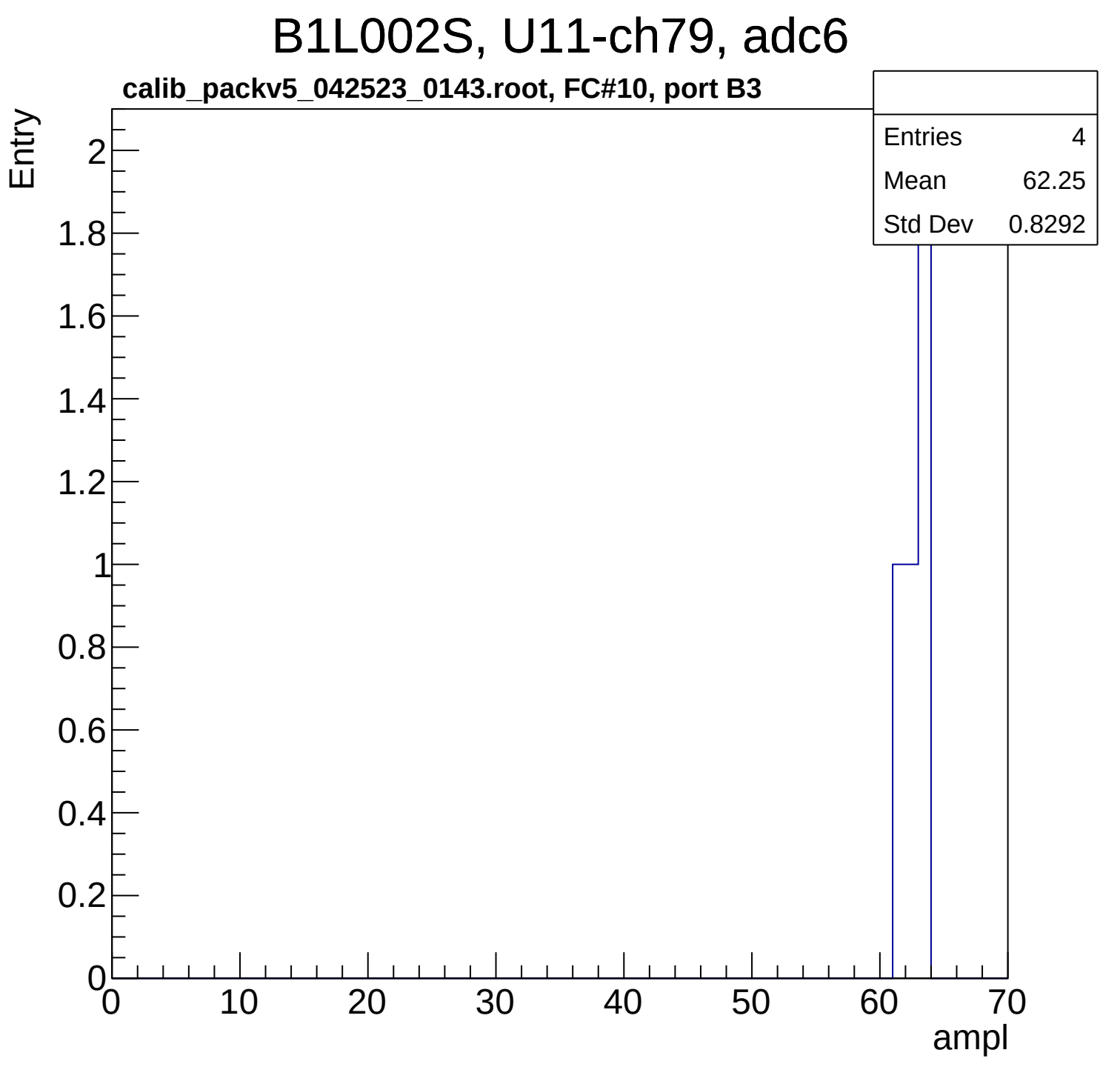

B1L002S, U11-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.5
Std Dev 2.995
Gaus mean : 35.9262
Gaus Width: 3.1579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

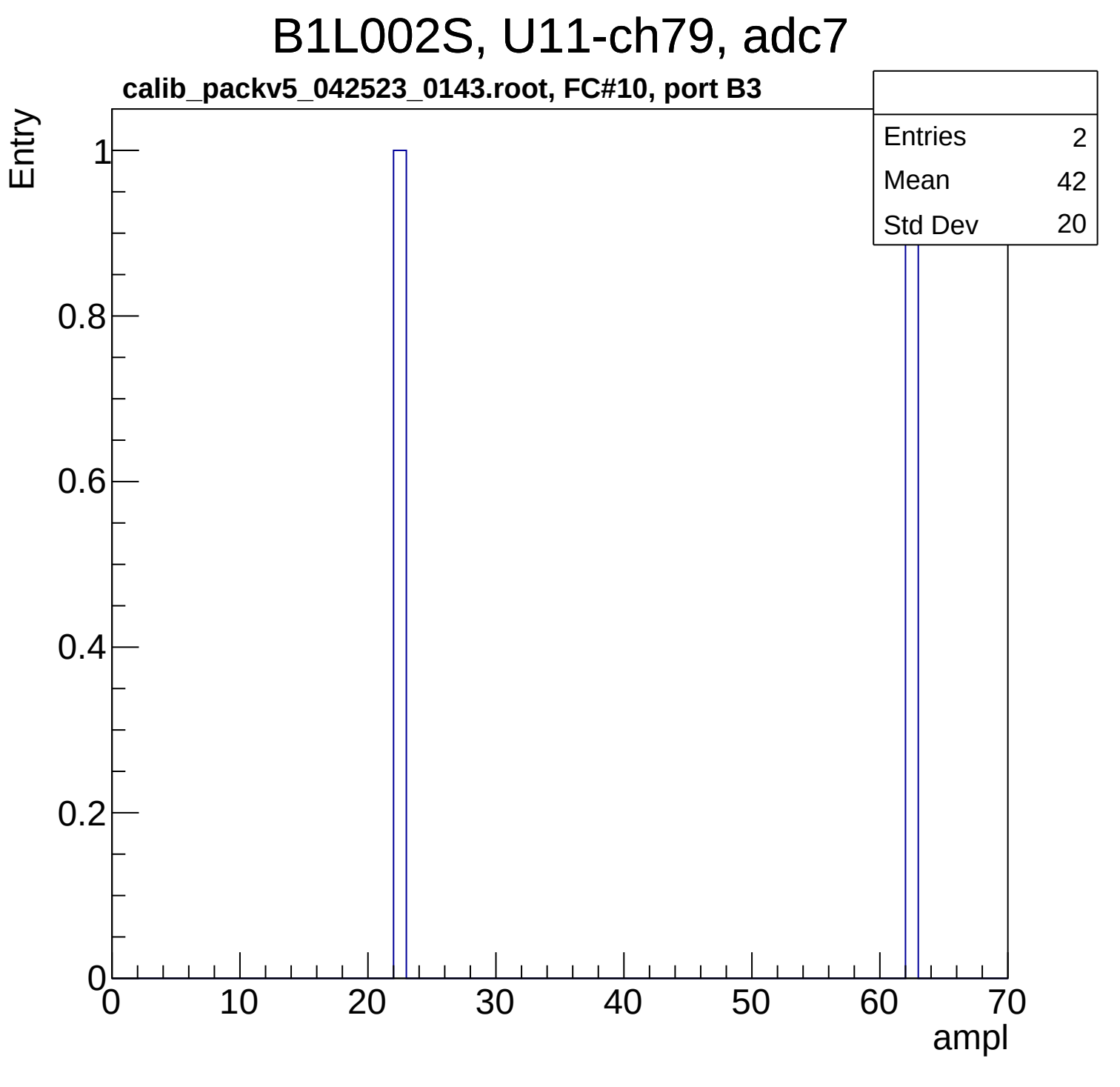

B1L002S, U11-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 42.38
Std Dev 2.959
Gaus mean : 42.5783
Gaus Width: 3.3778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

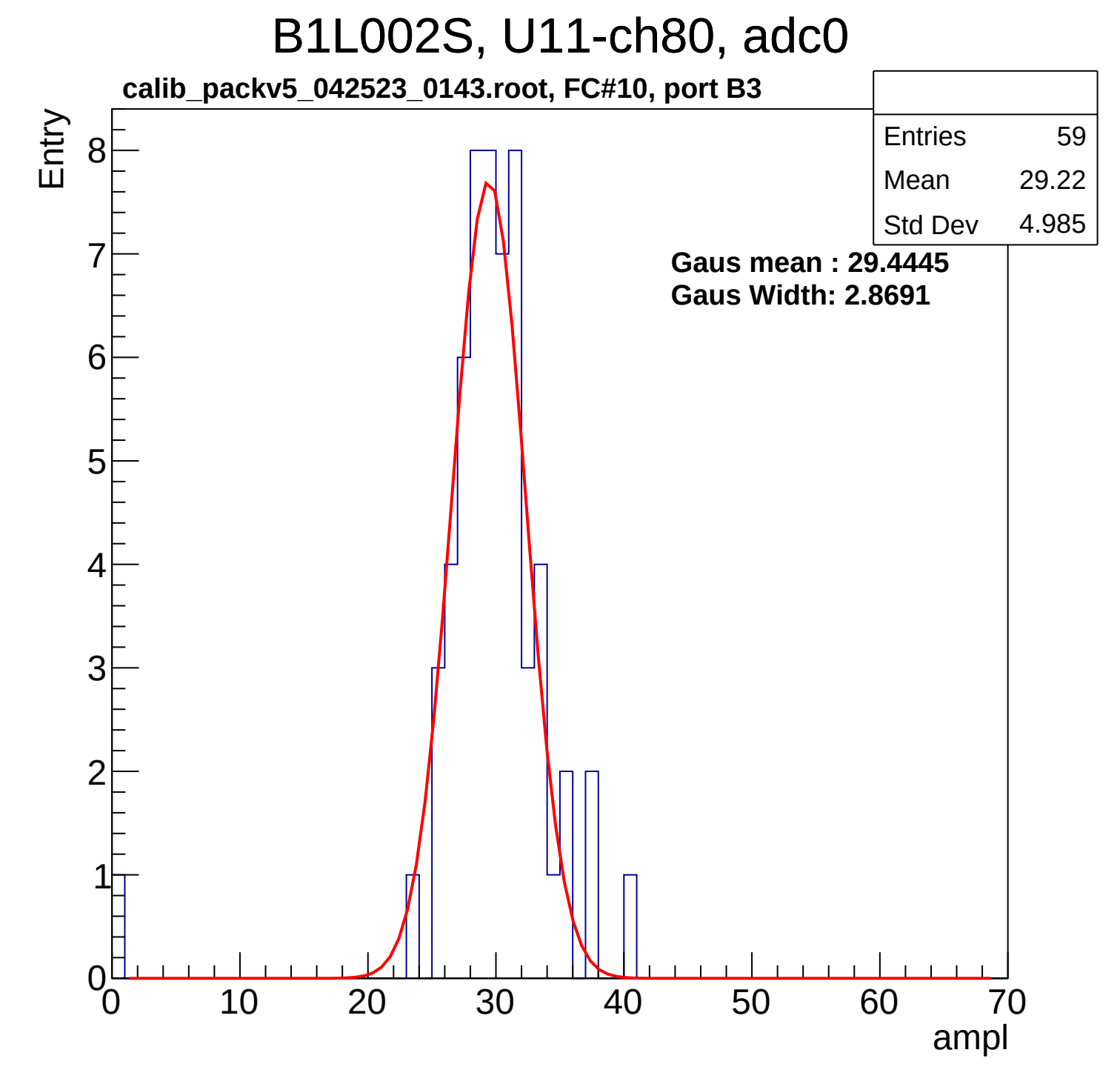

B1L002S, U11-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 48.2
Std Dev 2.497
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

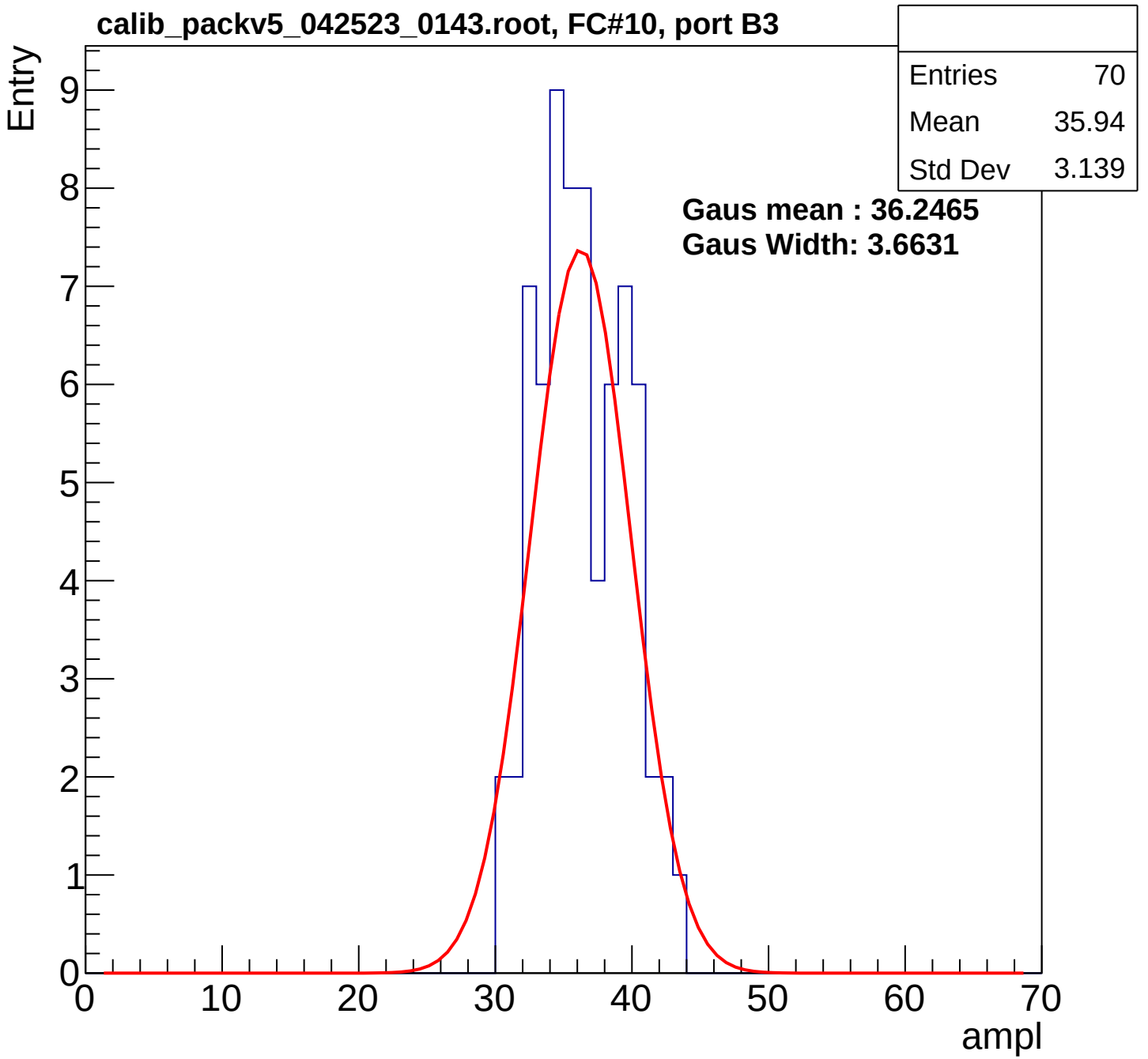

B1L002S, U11-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 54.42
Std Dev 2.866
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

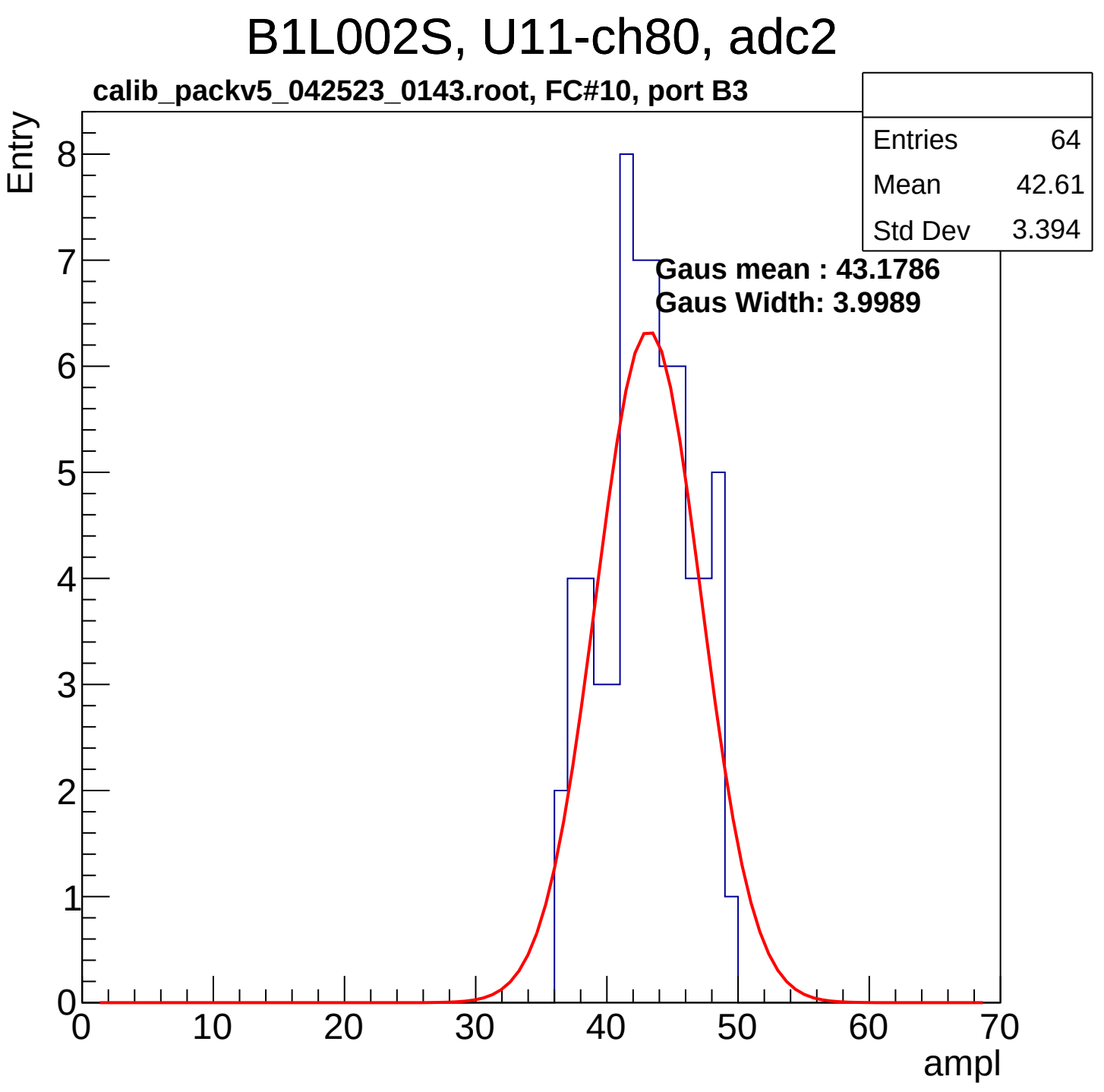

B1L002S, U11-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.63
Std Dev 8.228
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

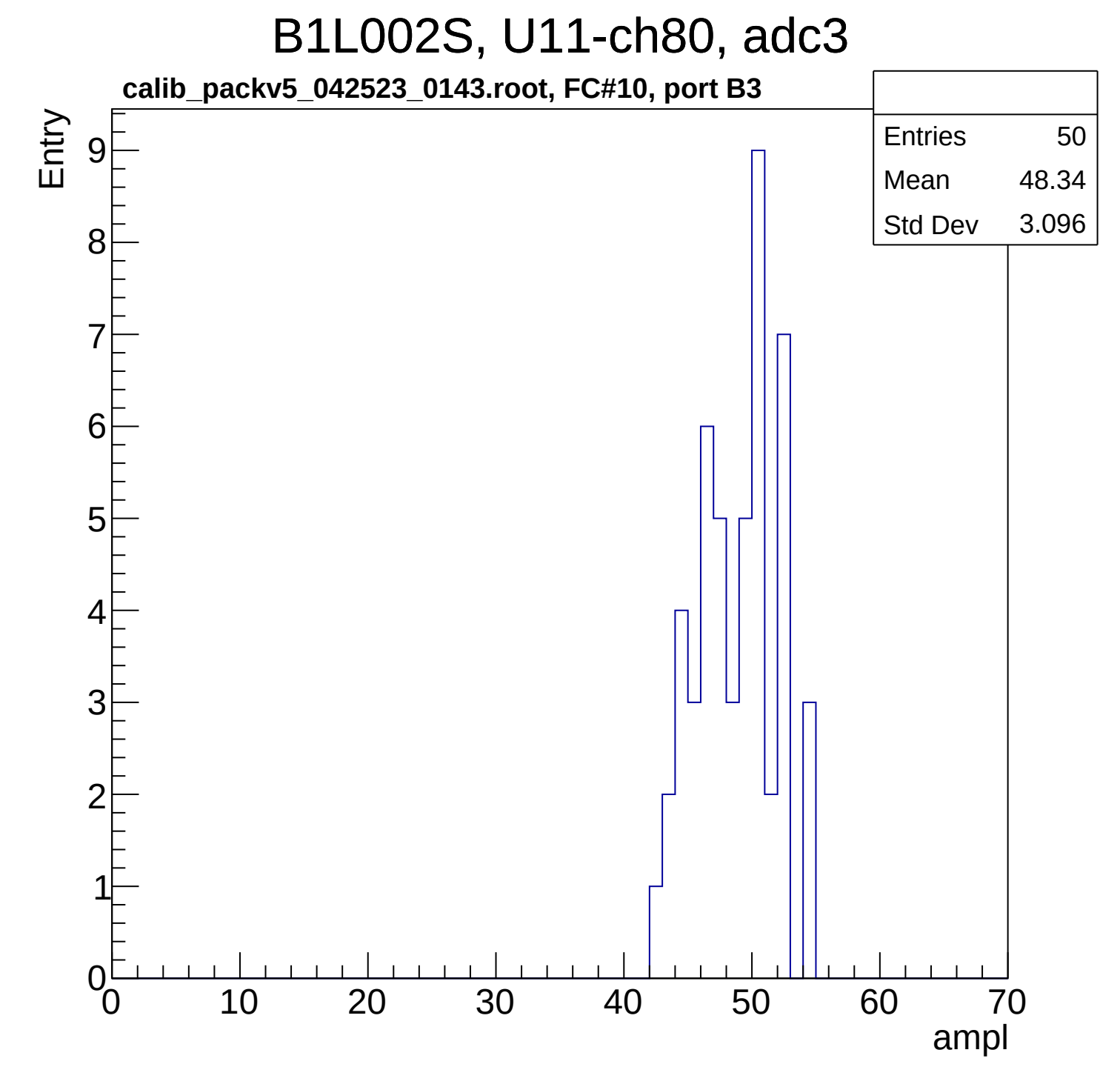

B1L002S, U11-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 20
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 29.22
Std Dev 4.985
Gaus mean : 29.4445
Gaus Width: 2.8691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.94
Std Dev 3.139
Gaus mean : 36.2465
Gaus Width: 3.6631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.61
Std Dev 3.394
Gaus mean : 43.1786
Gaus Width: 3.9989
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 48.34
Std Dev 3.096
Entry
ampl
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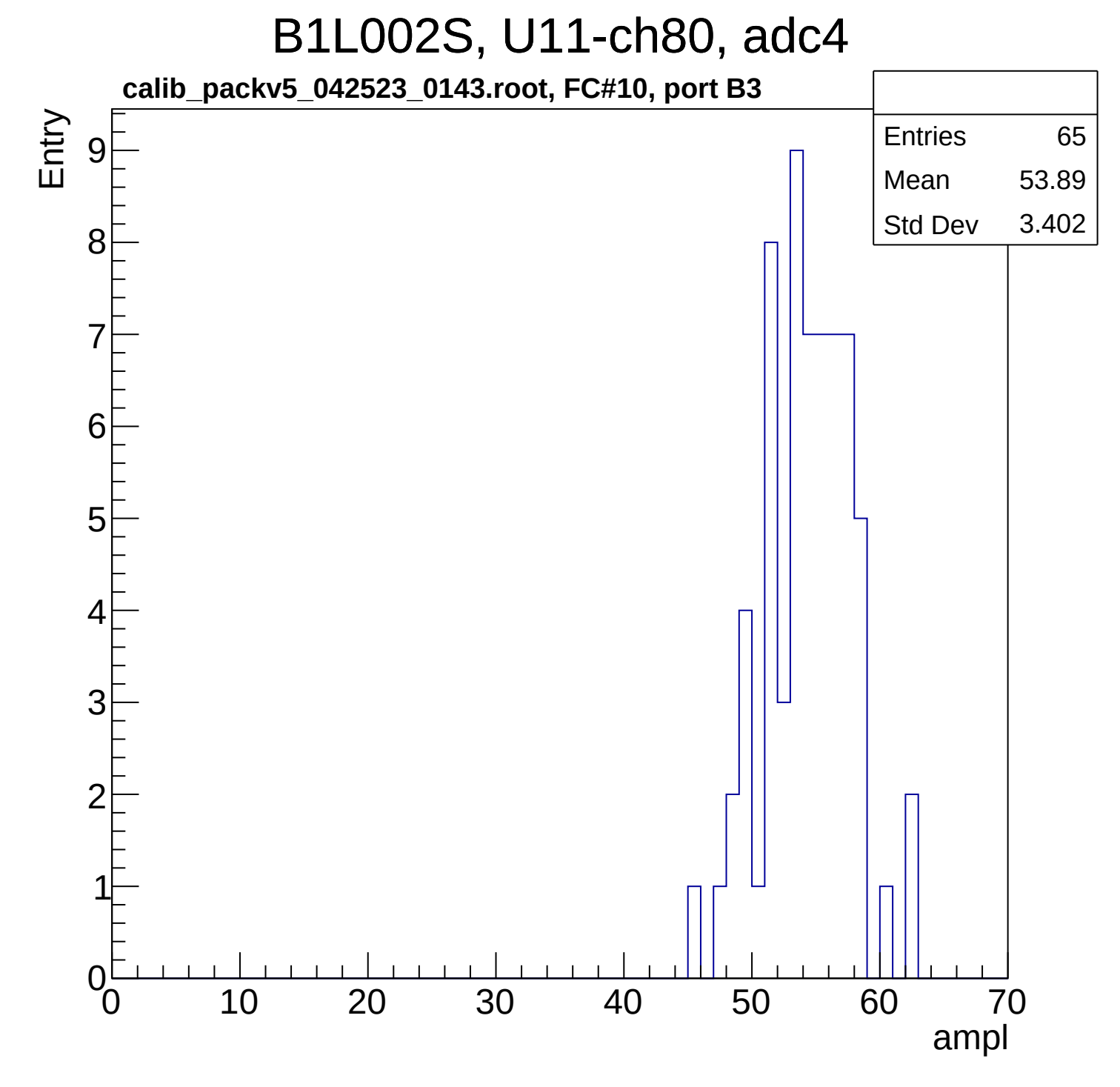
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.89
Std Dev 3.402
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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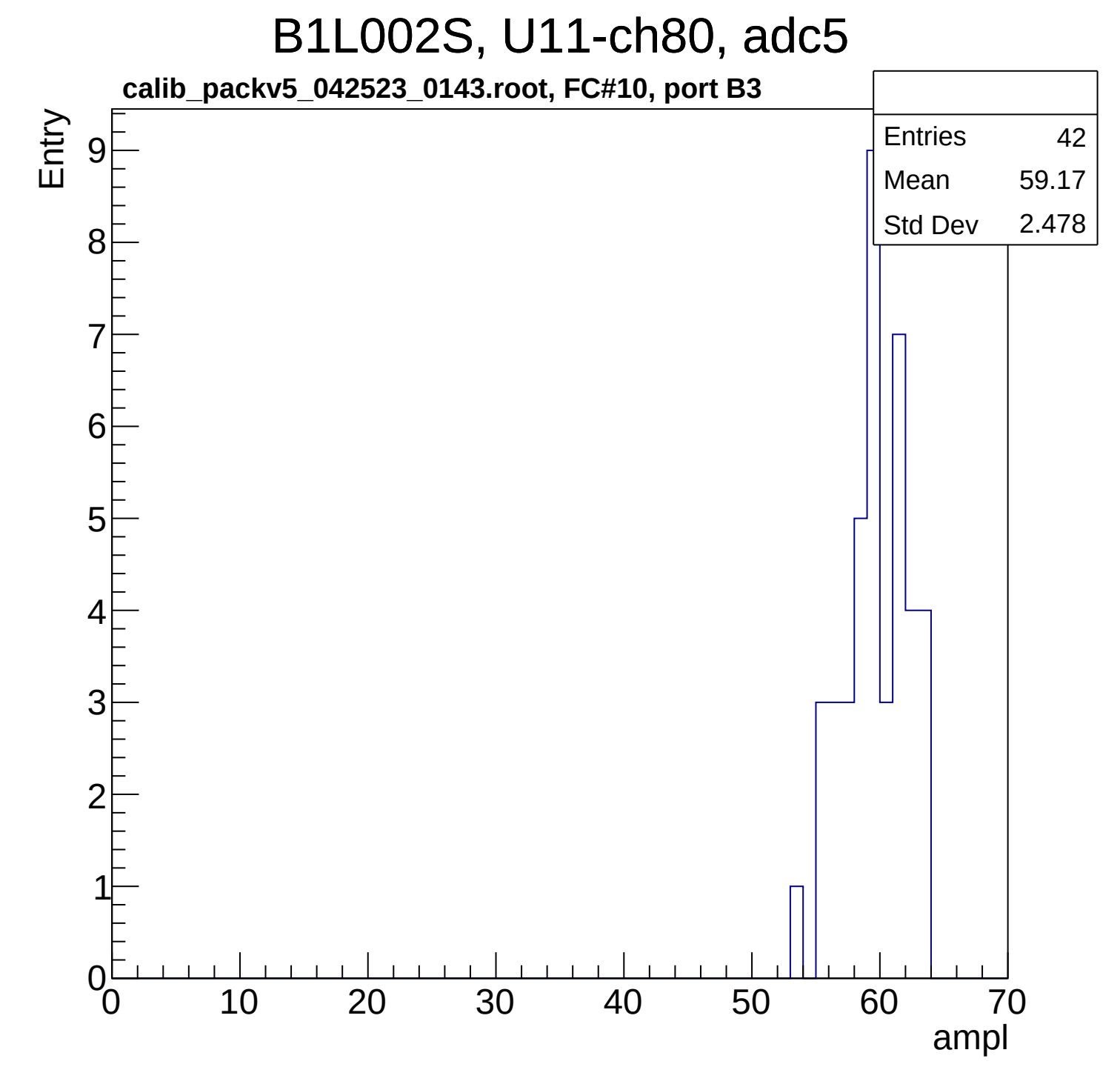
B1L002S, U11-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.17
Std Dev 2.478
Entry
ampl
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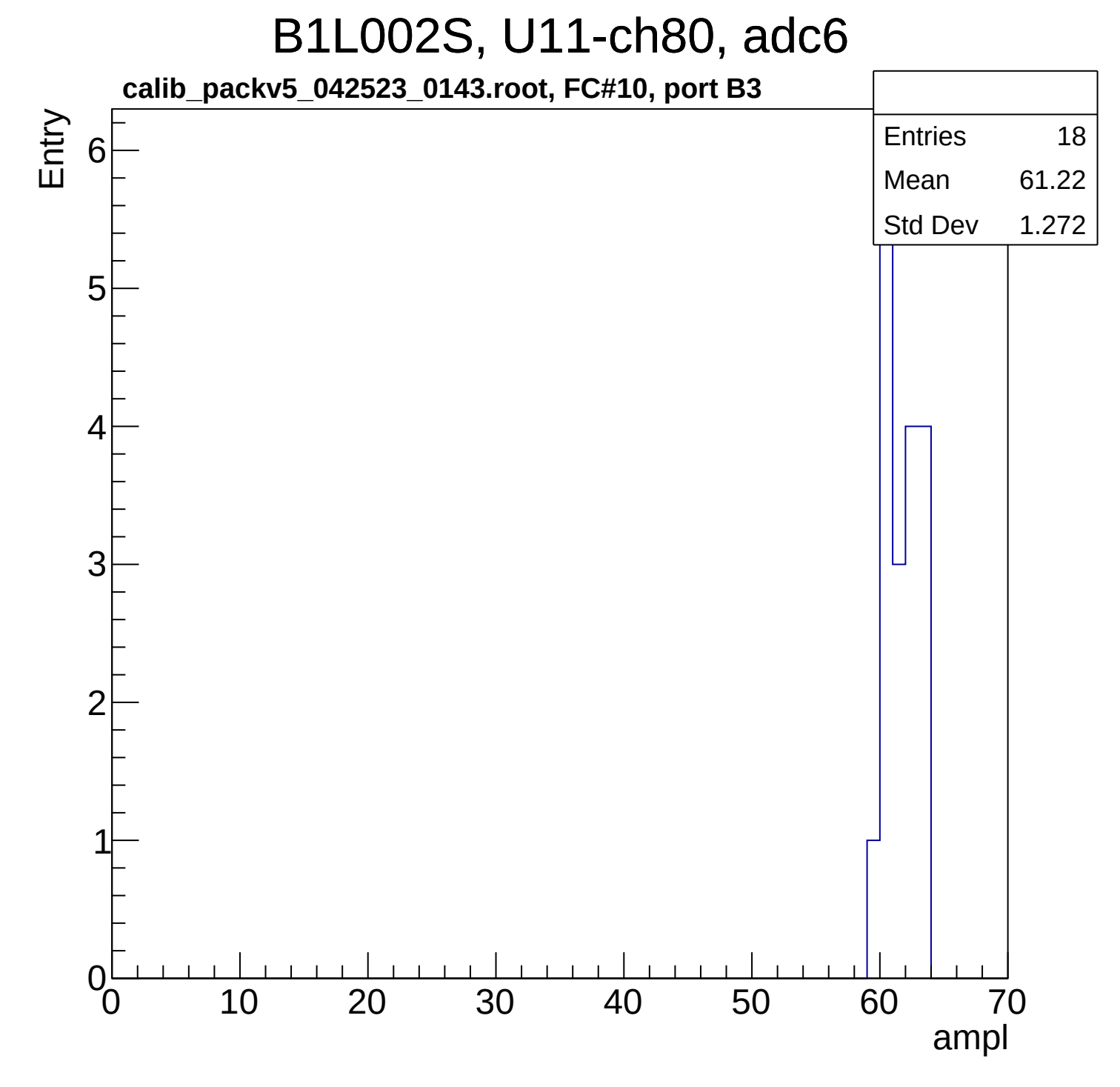
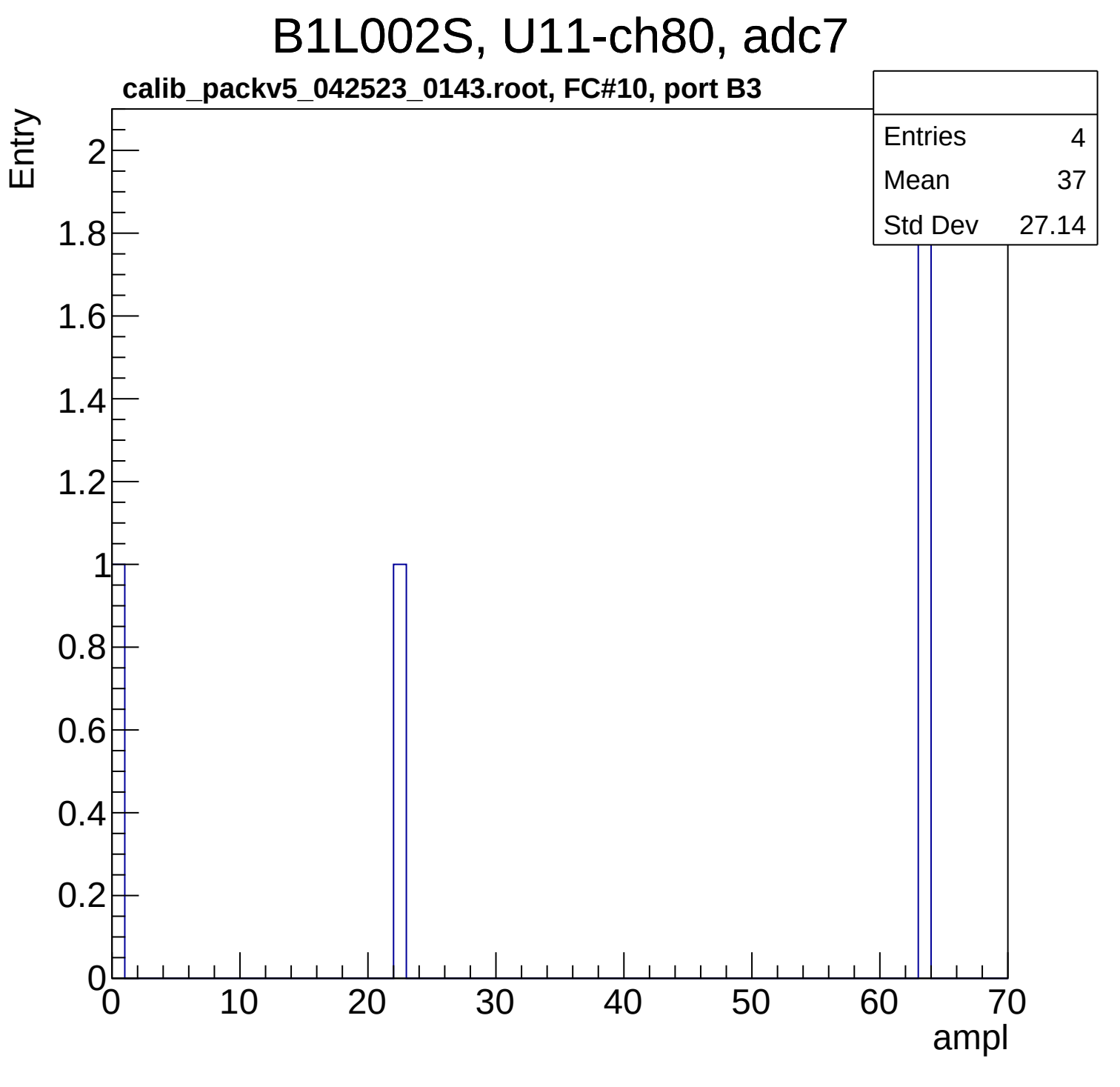
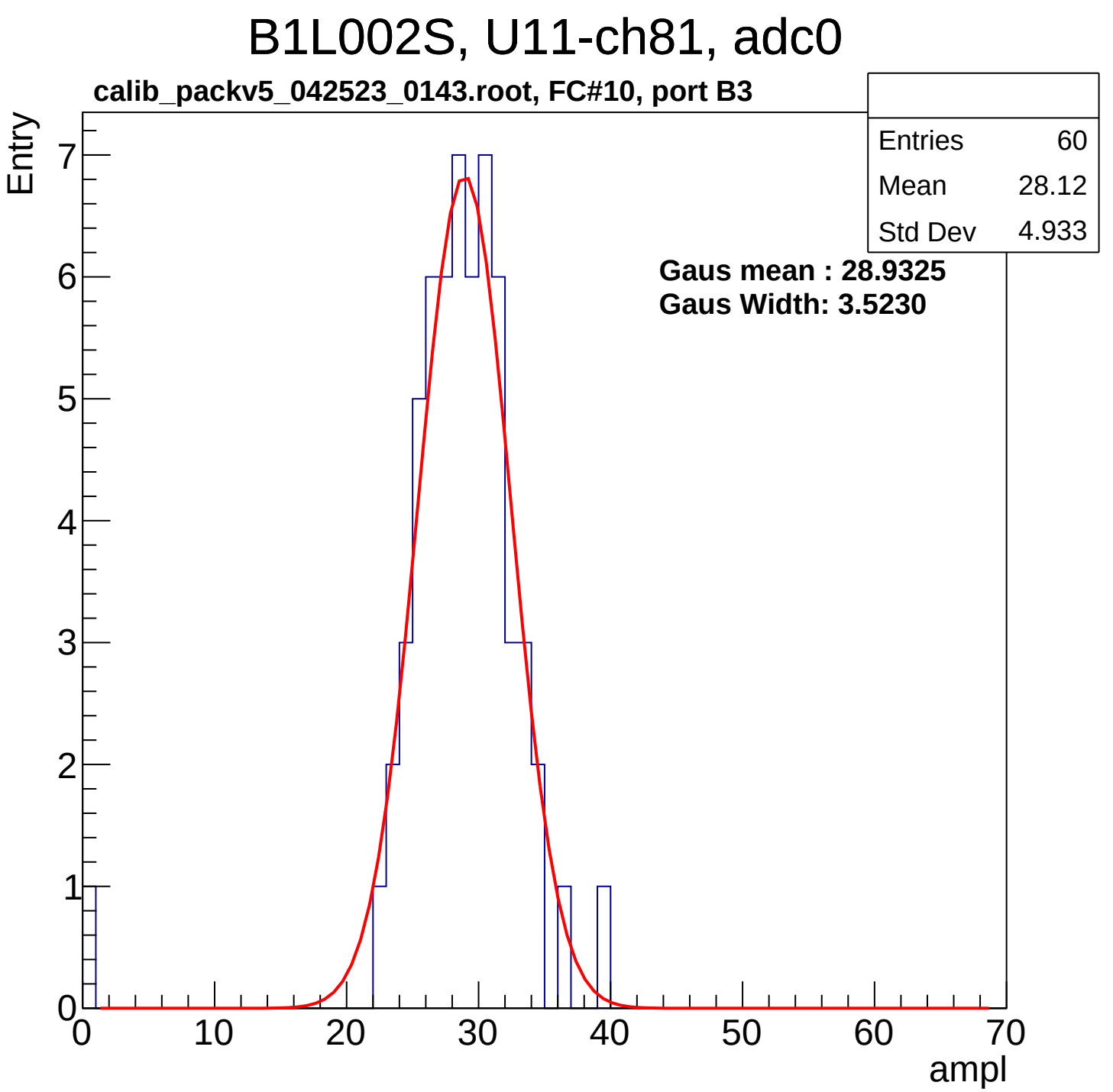
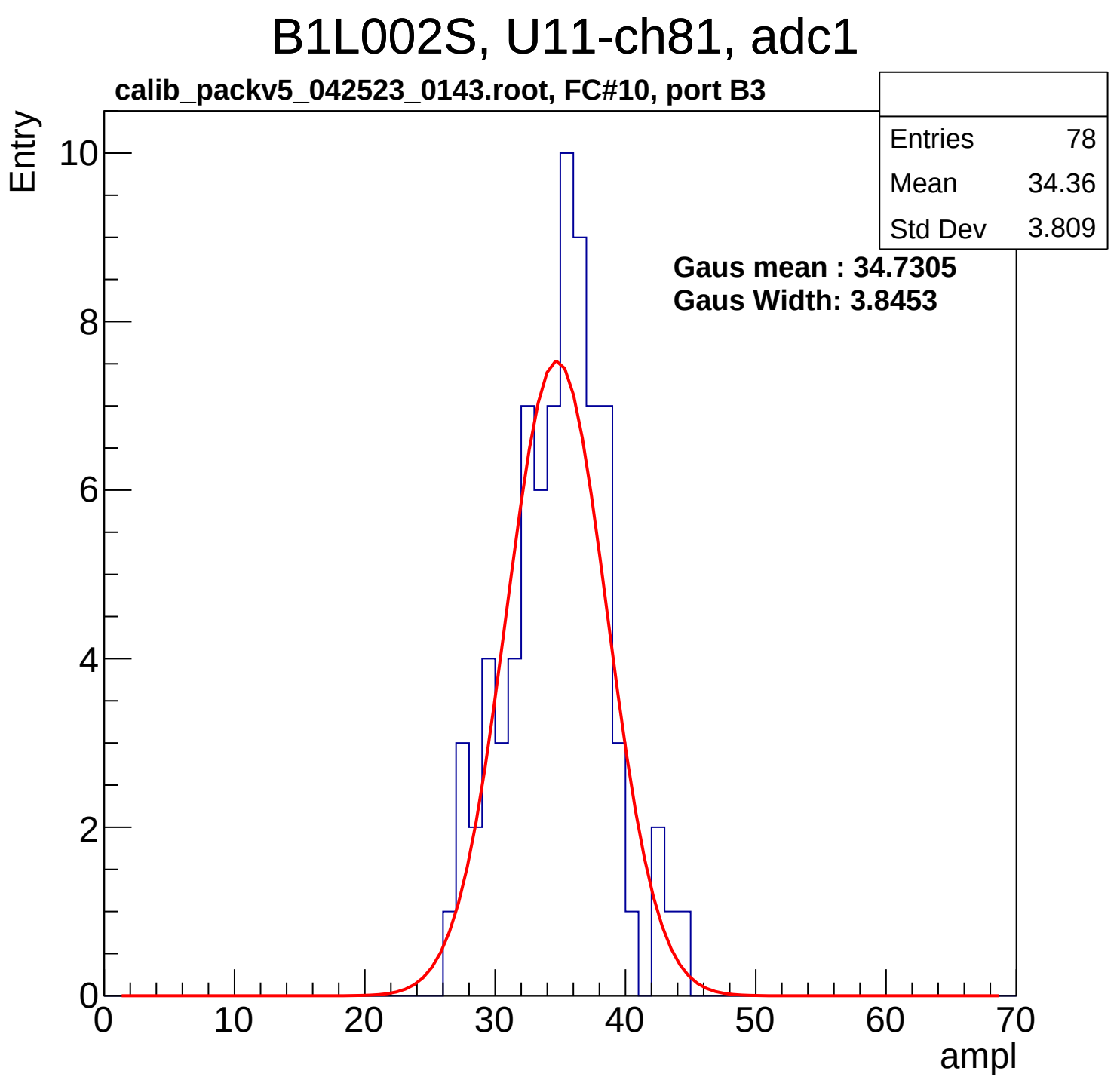
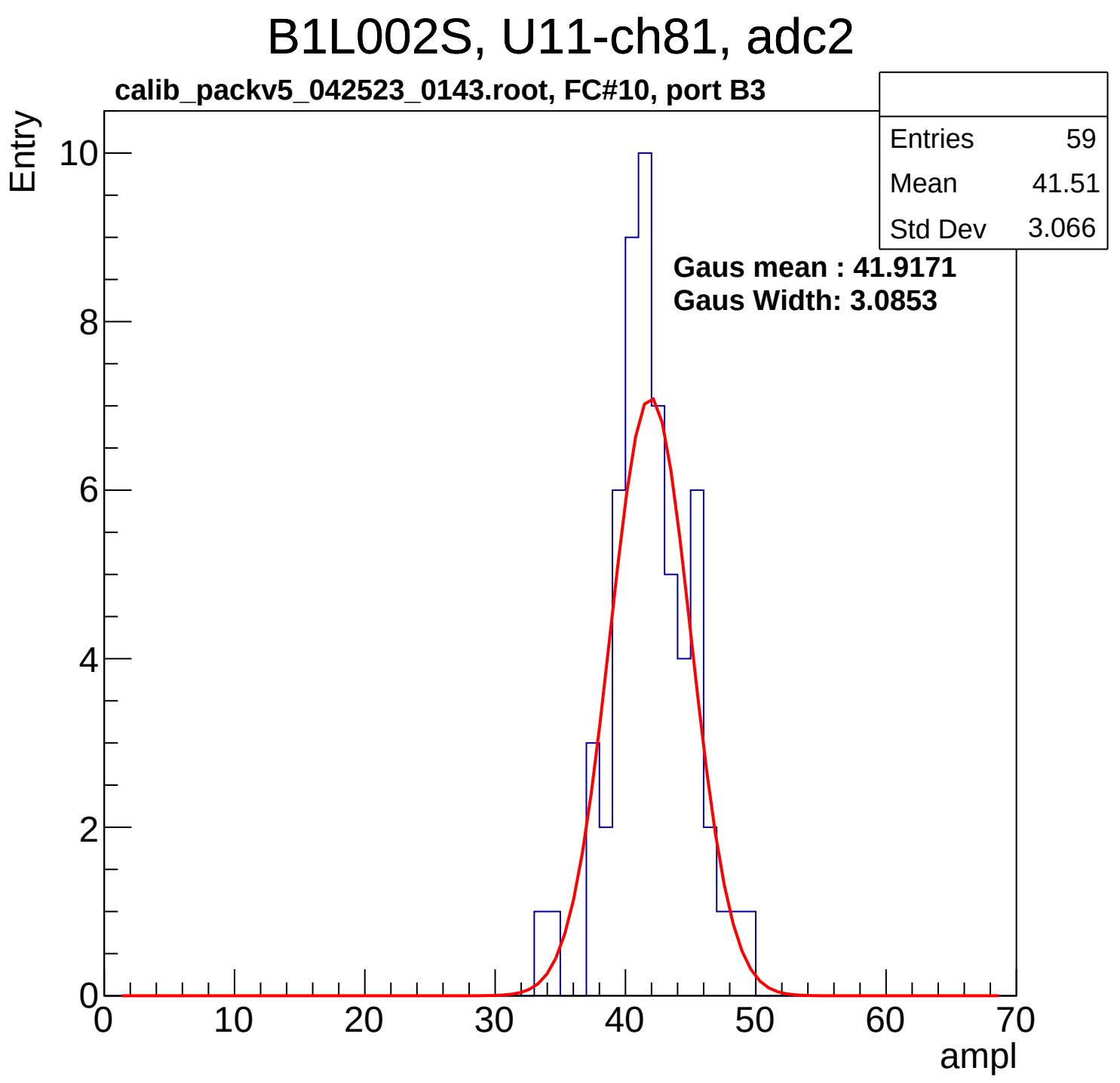
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.22
Std Dev 1.272
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 37
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.12
Std Dev 4.933
Gaus mean : 28.9325
Gaus Width: 3.5230
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 34.36
Std Dev 3.809
Gaus mean : 34.7305
Gaus Width: 3.8453
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.51
Std Dev 3.066
Gaus mean : 41.9171
Gaus Width: 3.0853
Entry
ampl
0
2
4
6
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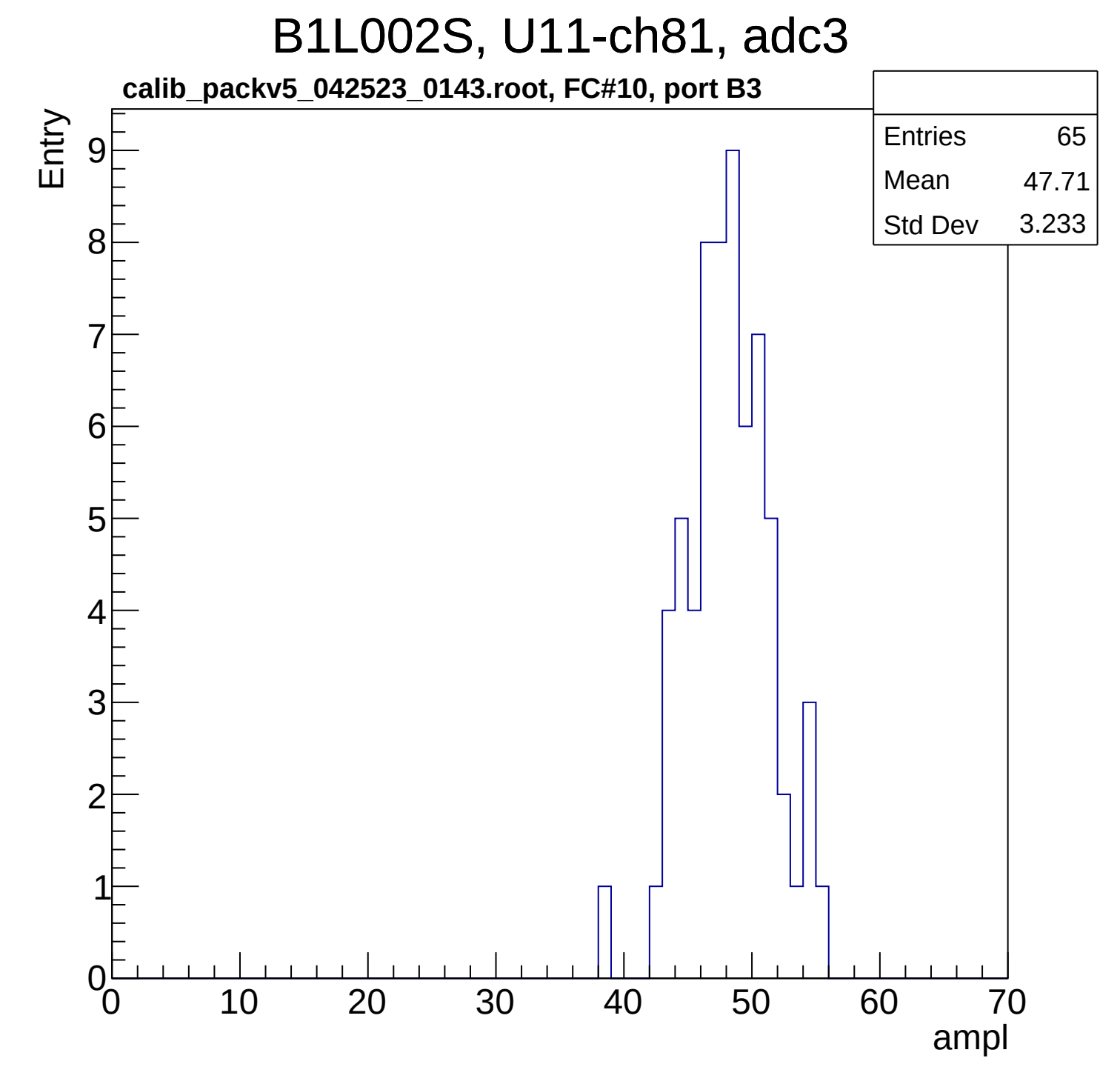
B1L002S, U11-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.71
Std Dev 3.233
Entry
ampl
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3
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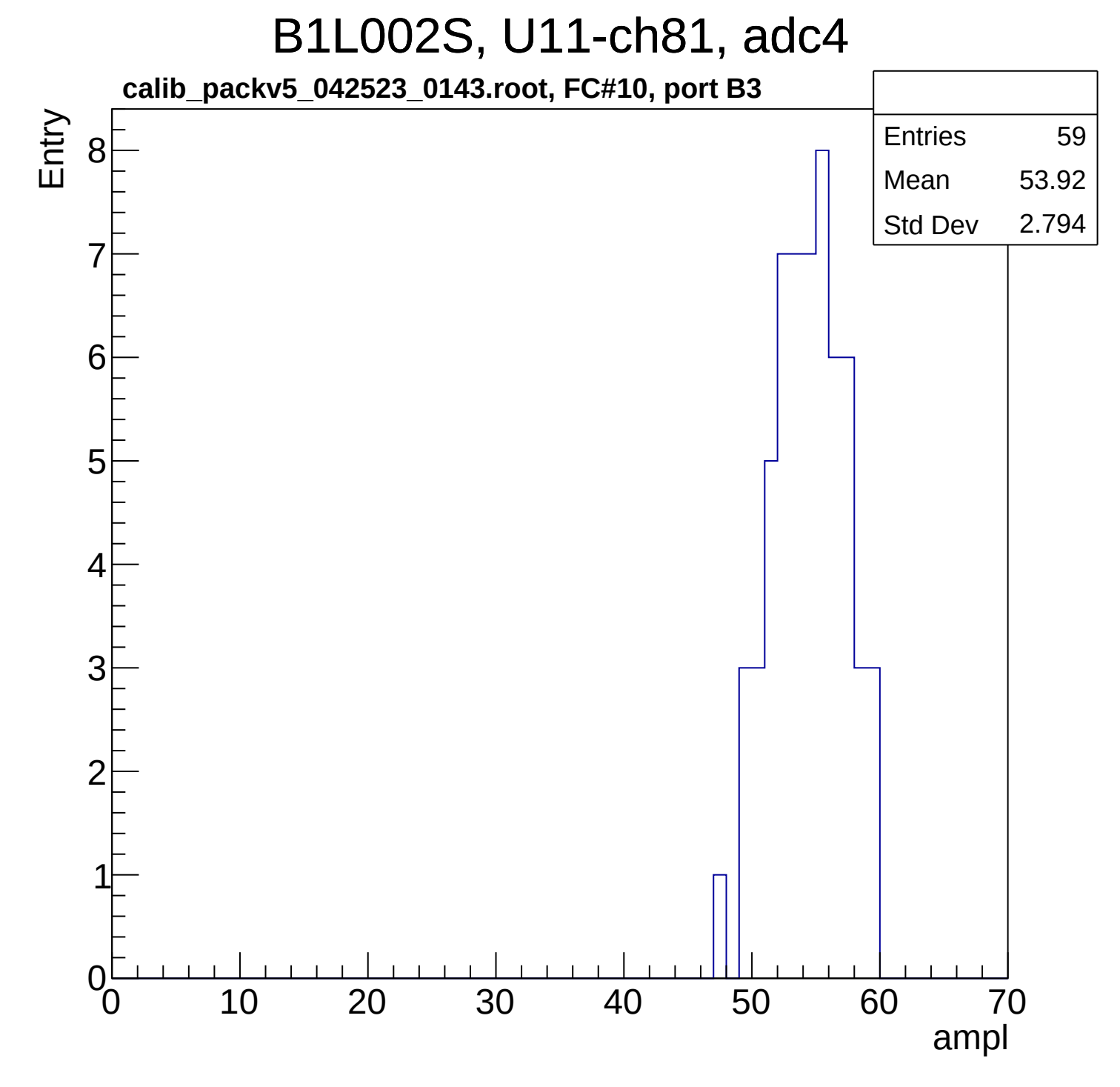
B1L002S, U11-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.92
Std Dev 2.794
Entry
ampl
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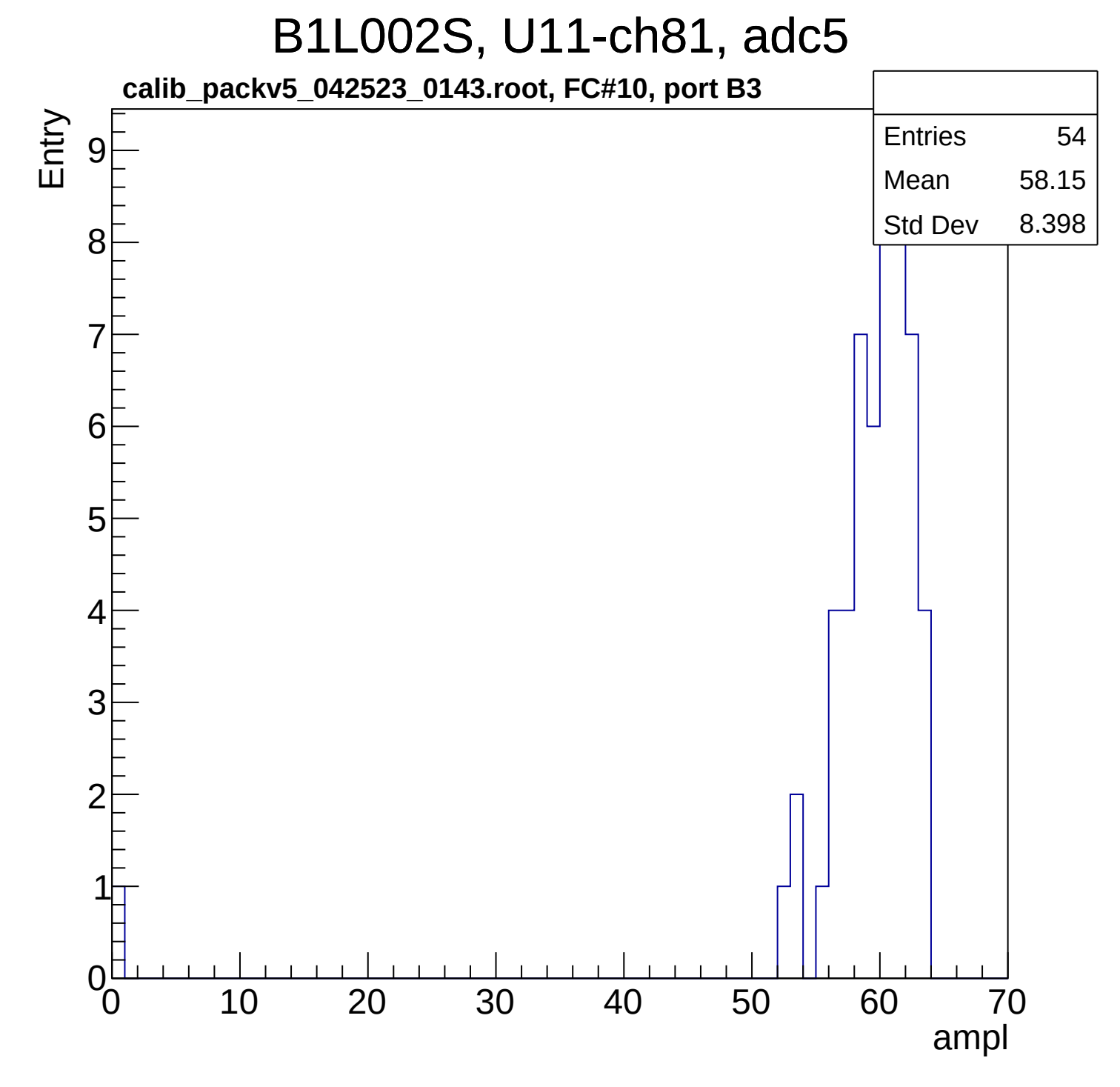
B1L002S, U11-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.15
Std Dev 8.398
Entry
ampl
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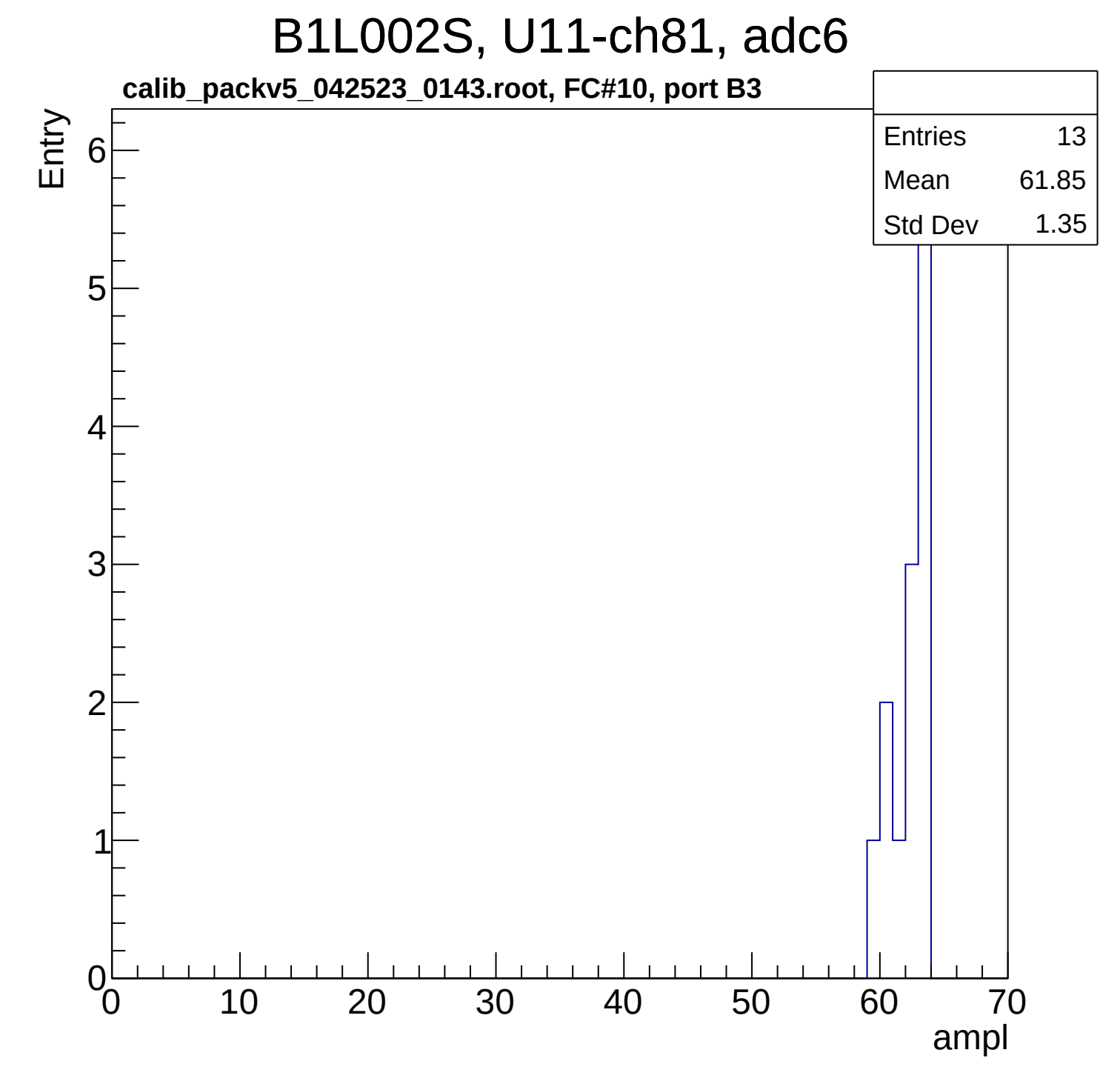
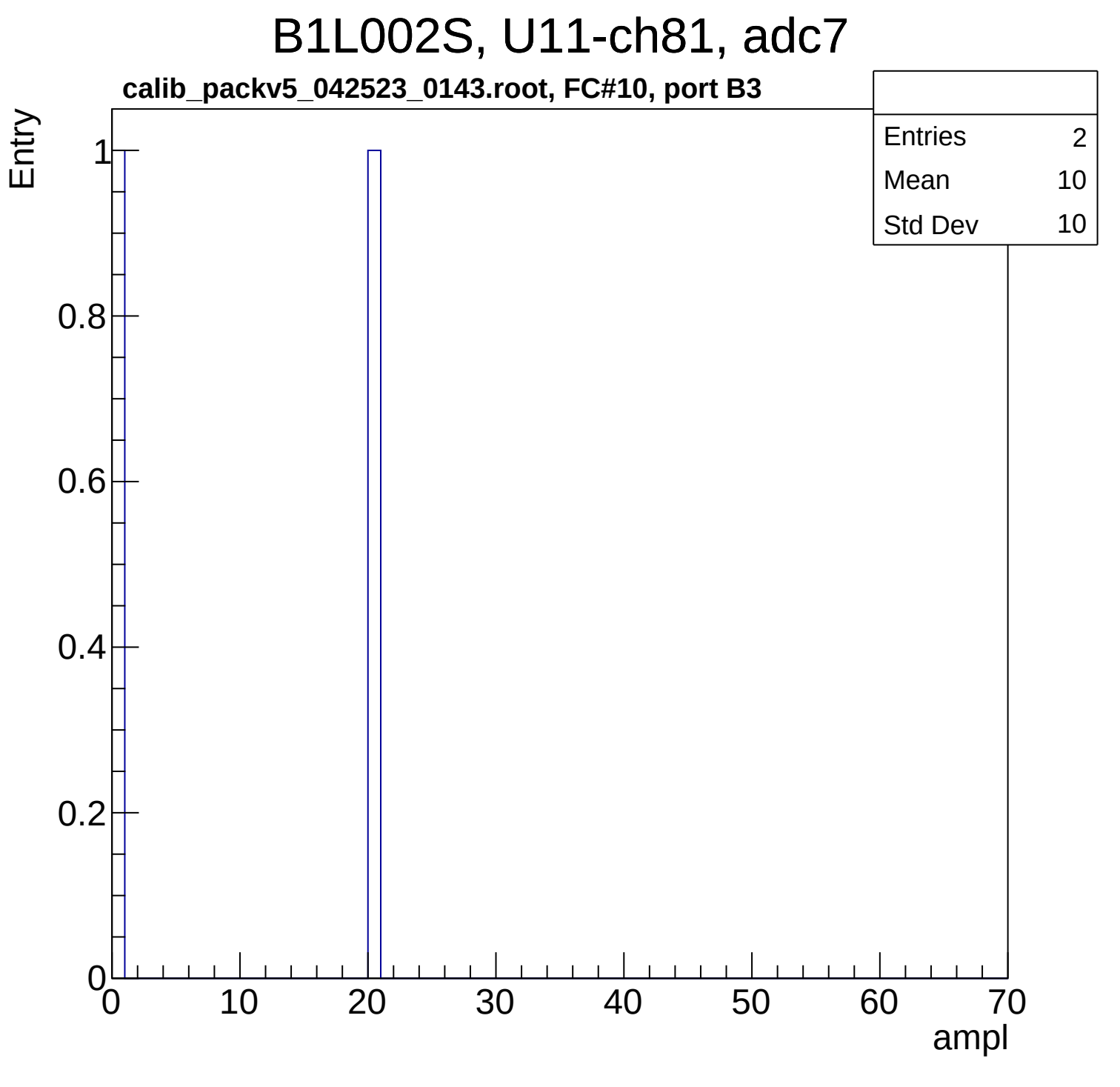

B1L002S, U11-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.85
Std Dev 1.35
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
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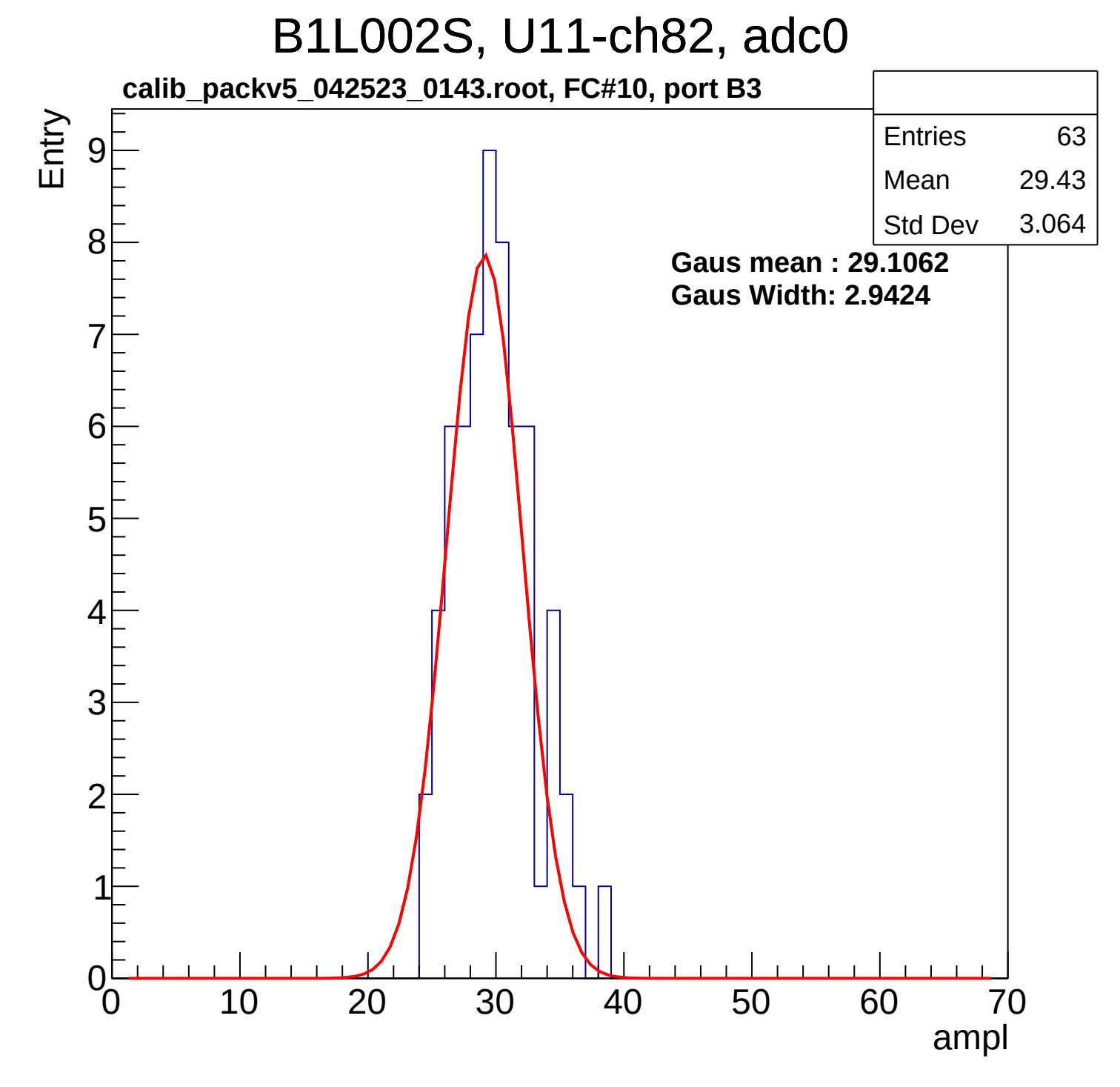
50
60
70
ampl


B1L002S, U11-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 29.43
Std Dev 3.064
Gaus mean : 29.1062
Gaus Width: 2.9424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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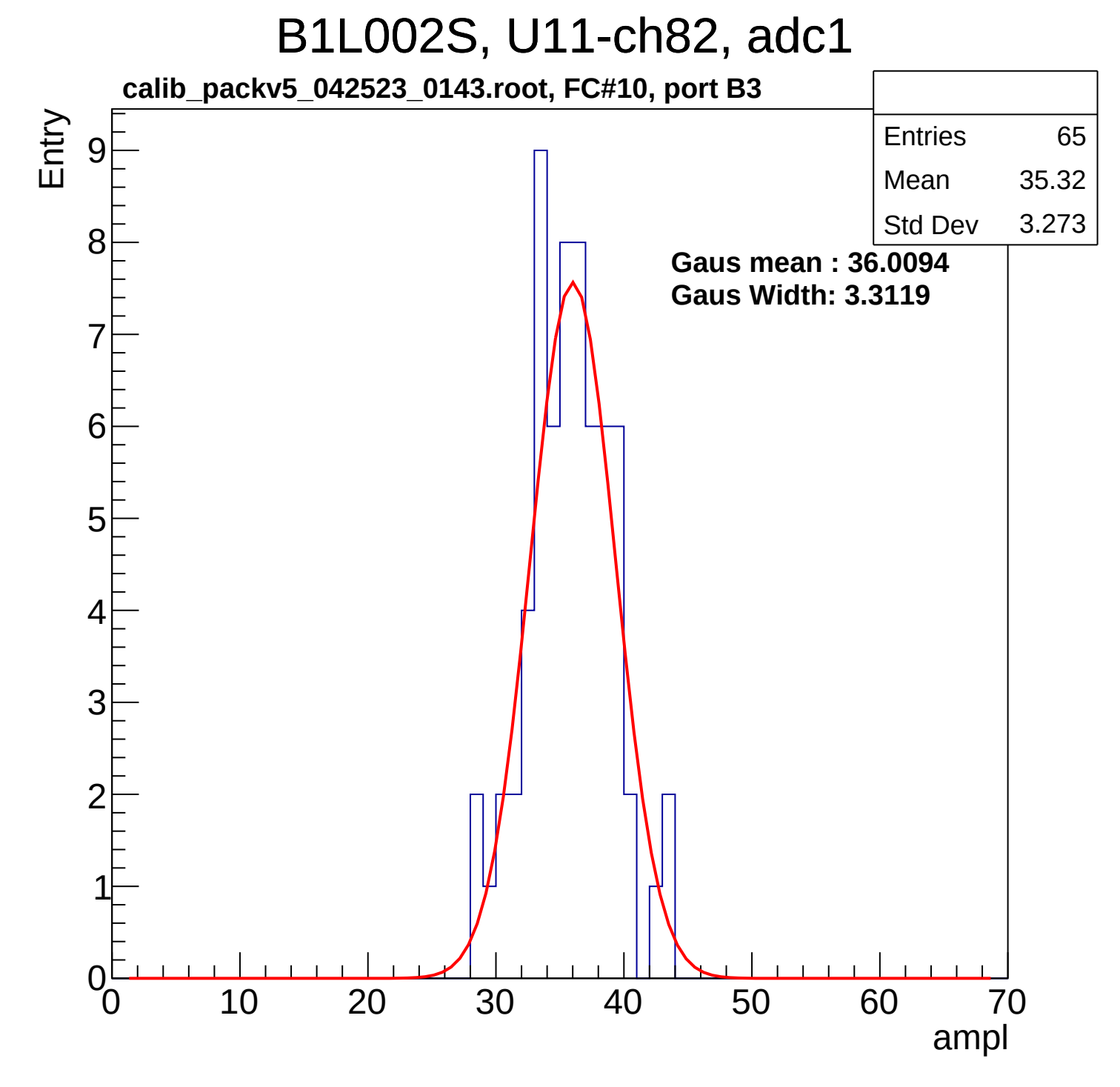
B1L002S, U11-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.32
Std Dev 3.273
Gaus mean : 36.0094
Gaus Width: 3.3119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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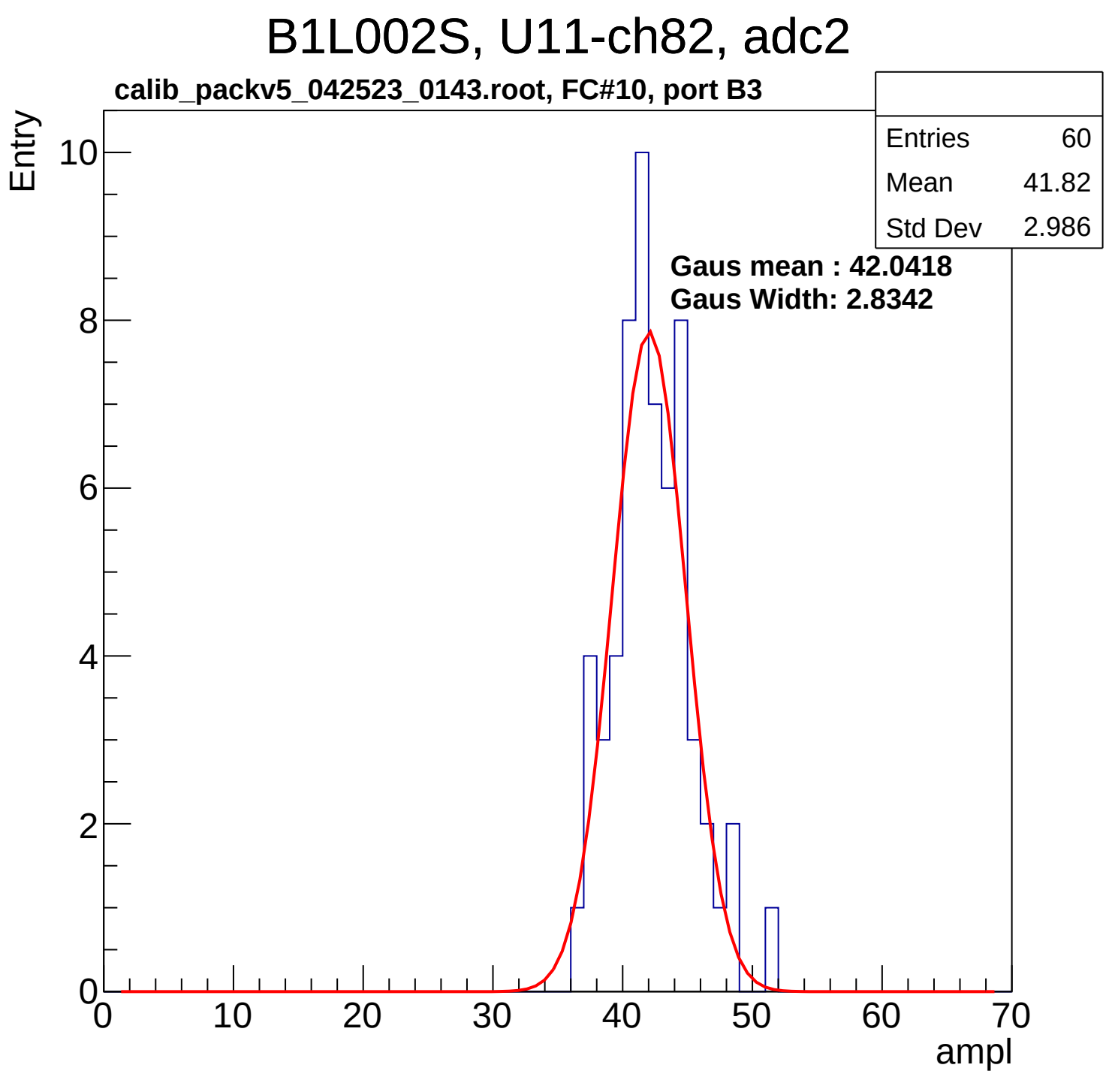
B1L002S, U11-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.82
Std Dev 2.986
Gaus mean : 42.0418
Gaus Width: 2.8342
Entry
ampl
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2
4
6
8
10
0
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20
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40
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60
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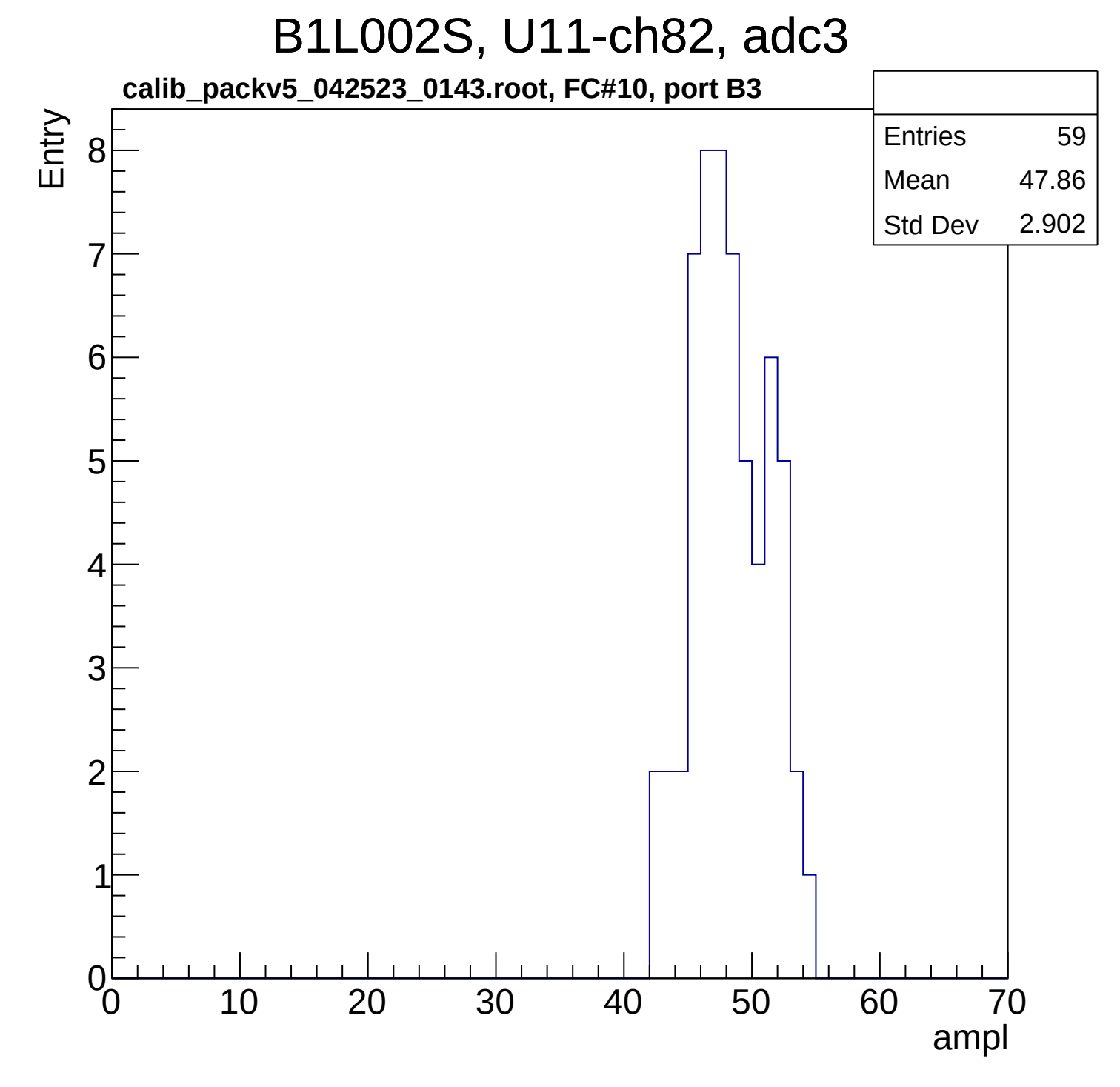
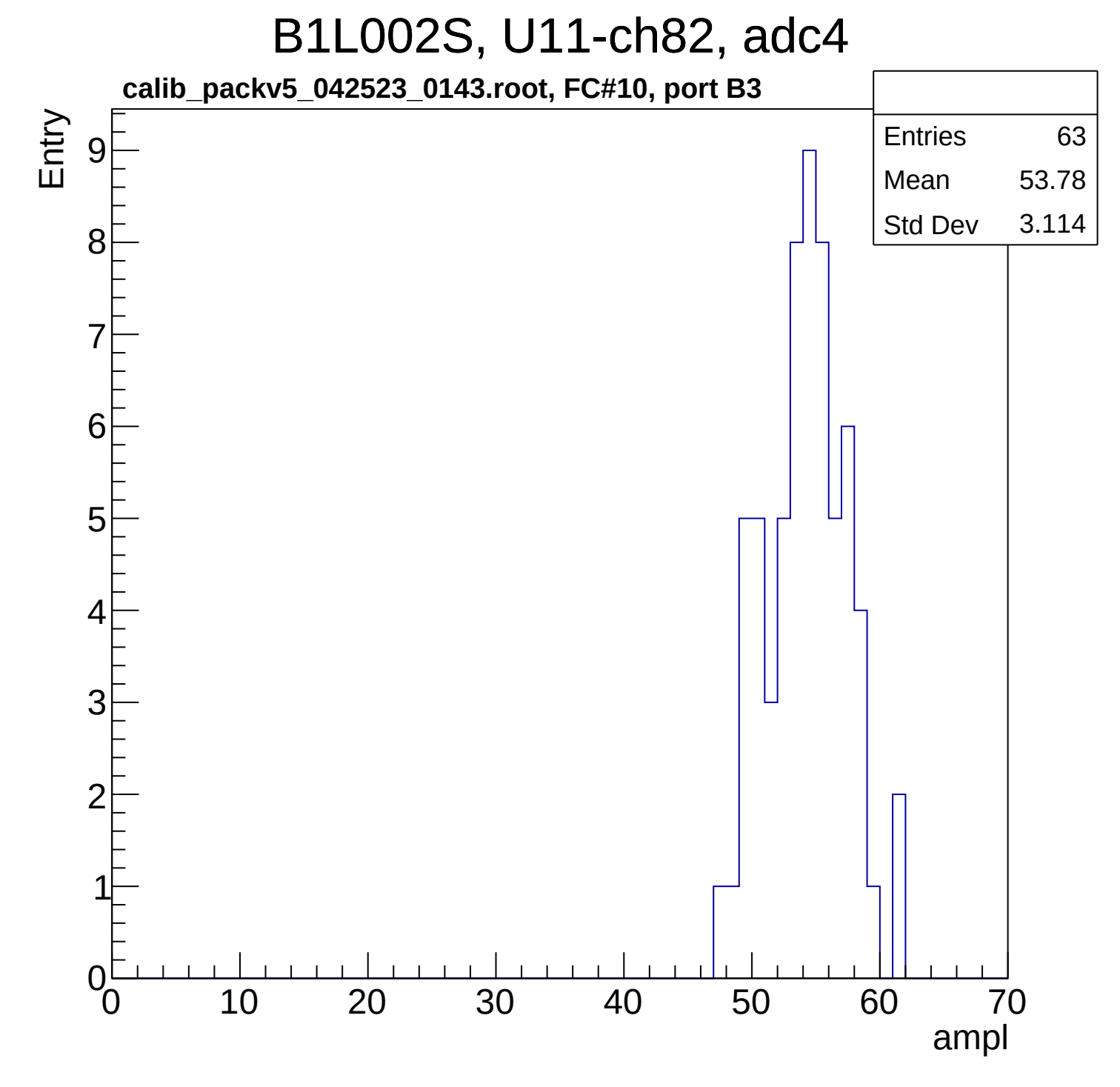

B1L002S, U11-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.86
Std Dev 2.902
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.78
Std Dev 3.114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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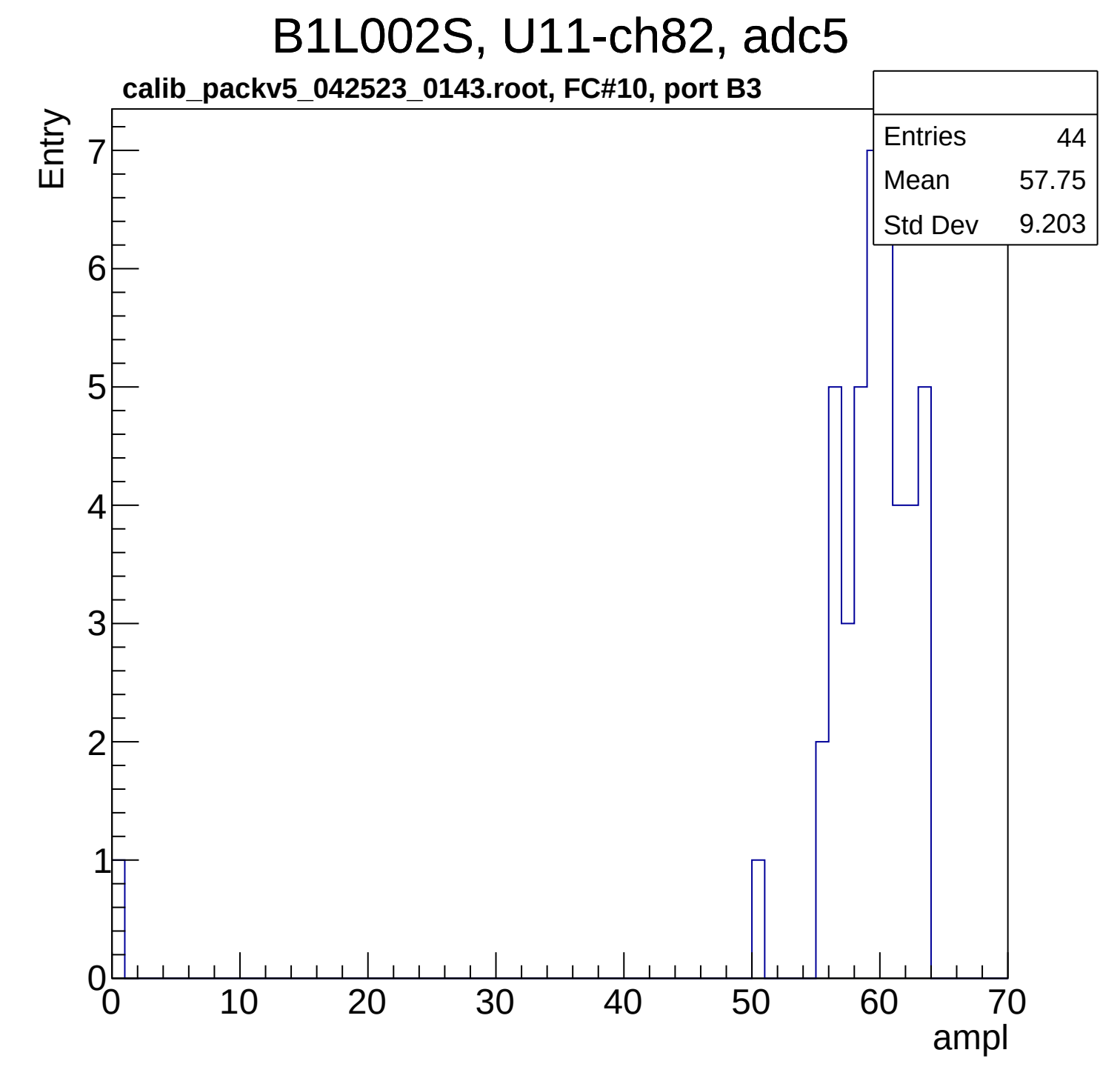
B1L002S, U11-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.75
Std Dev 9.203
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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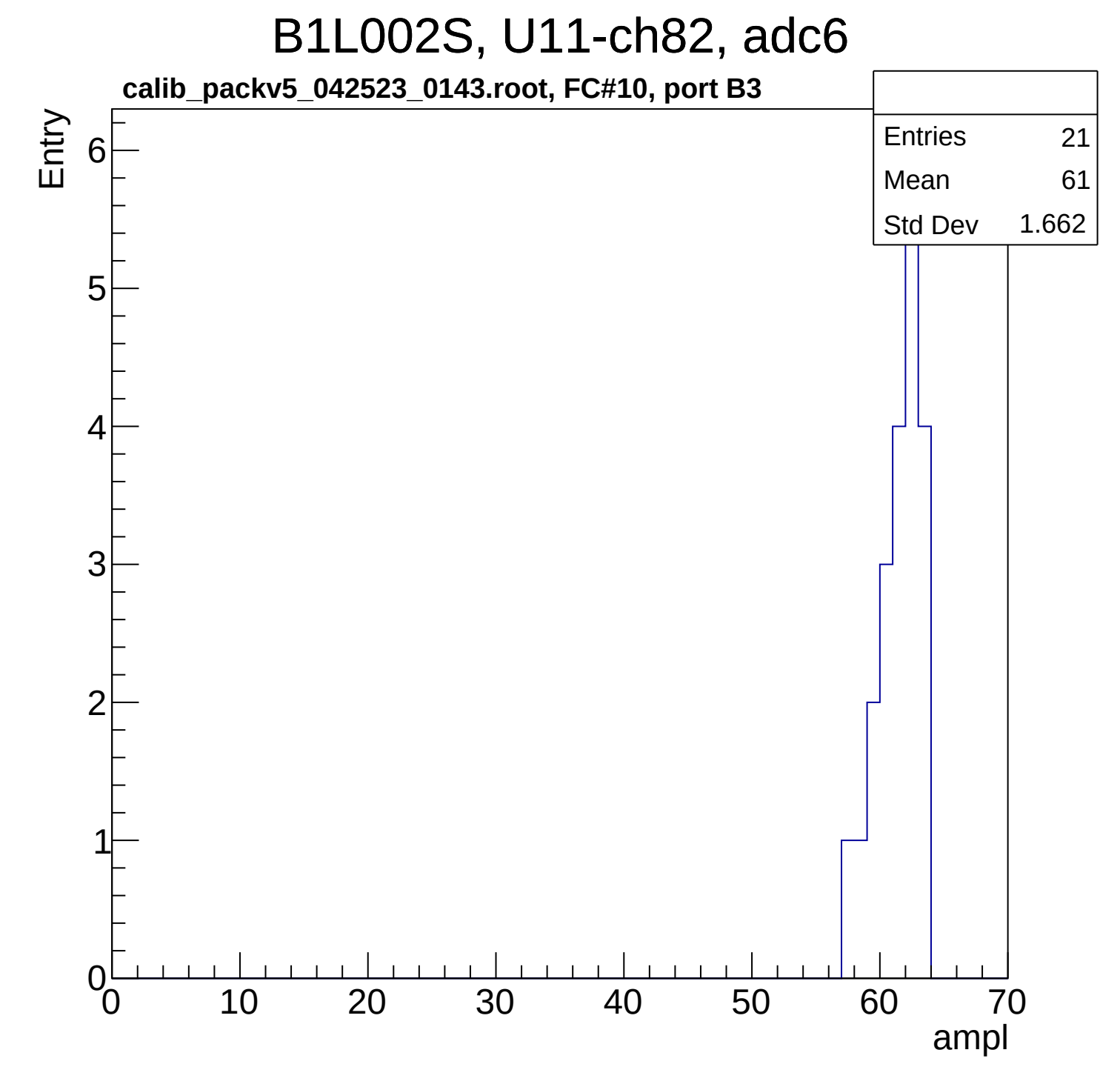
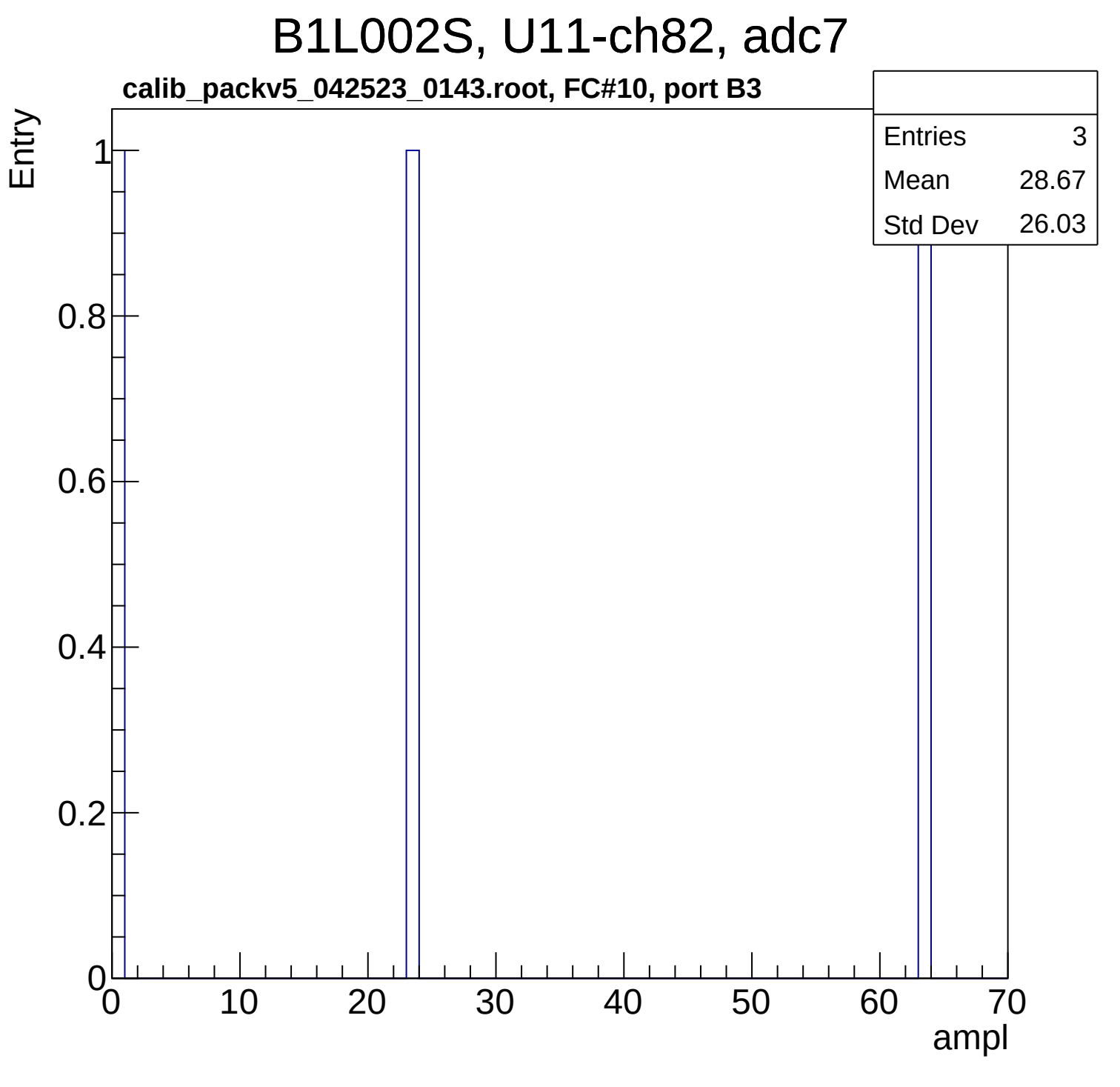

B1L002S, U11-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61
Std Dev 1.662
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
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0.4
0.6
0.8
1
0
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40
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60
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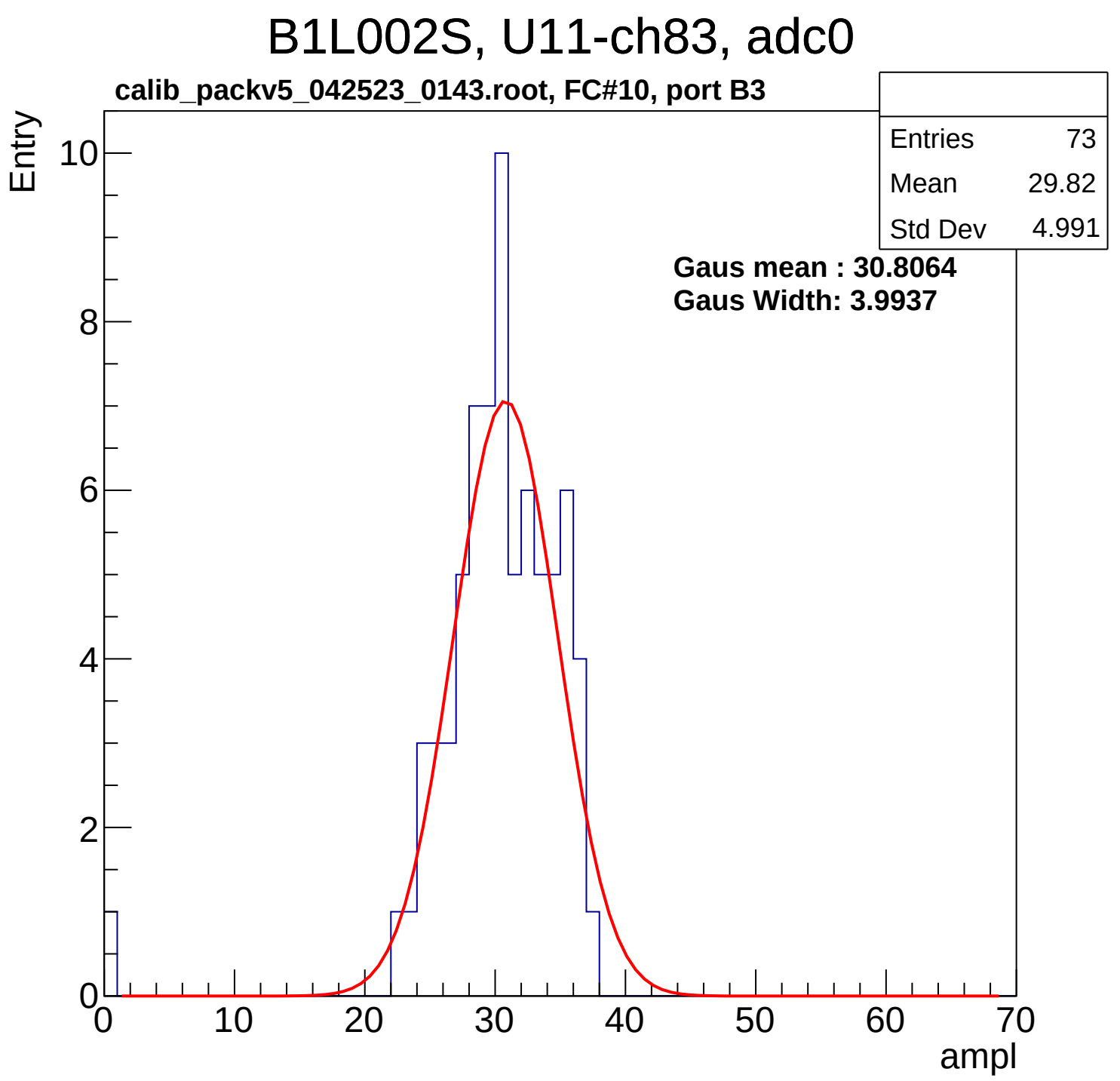
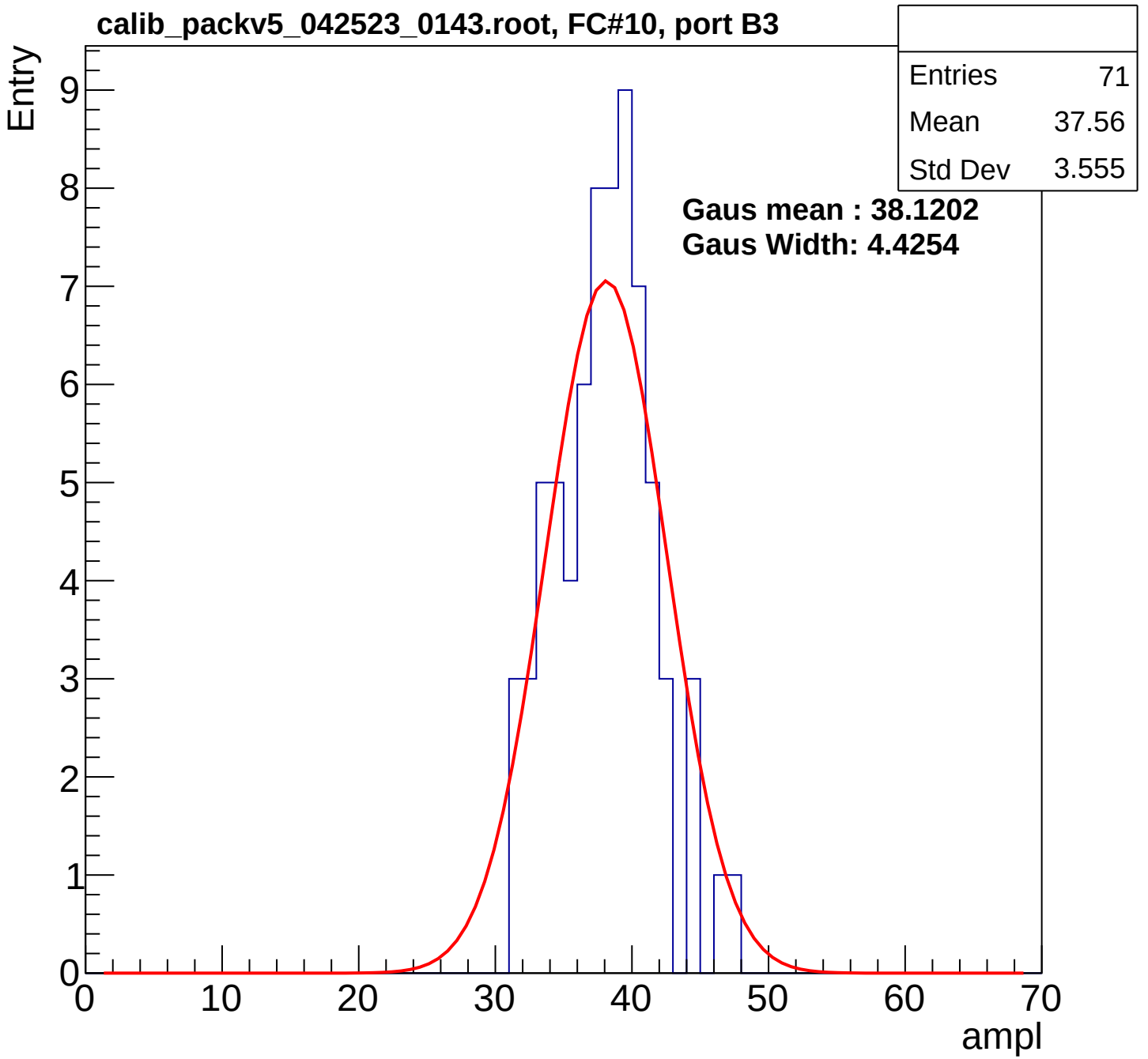
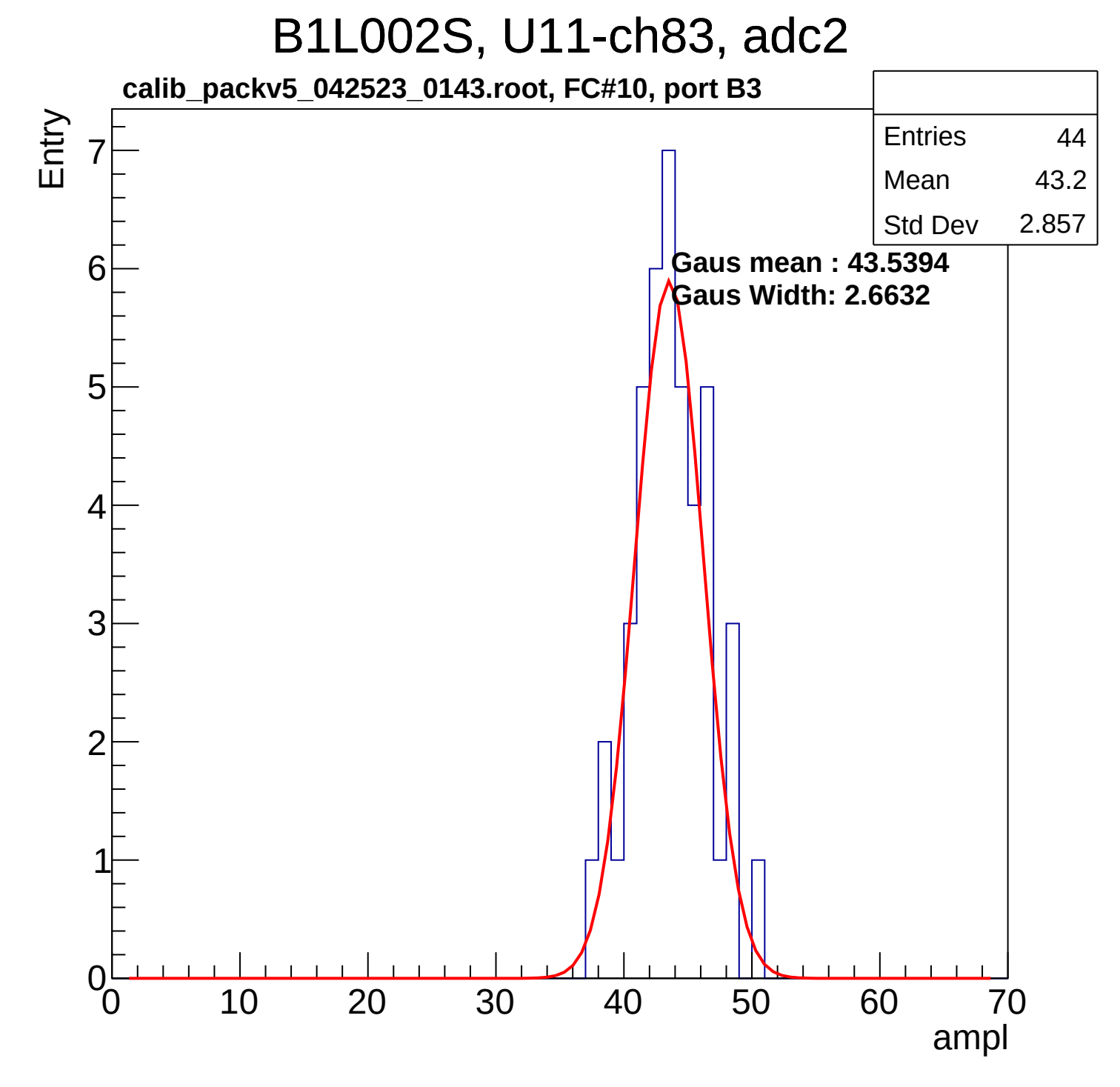
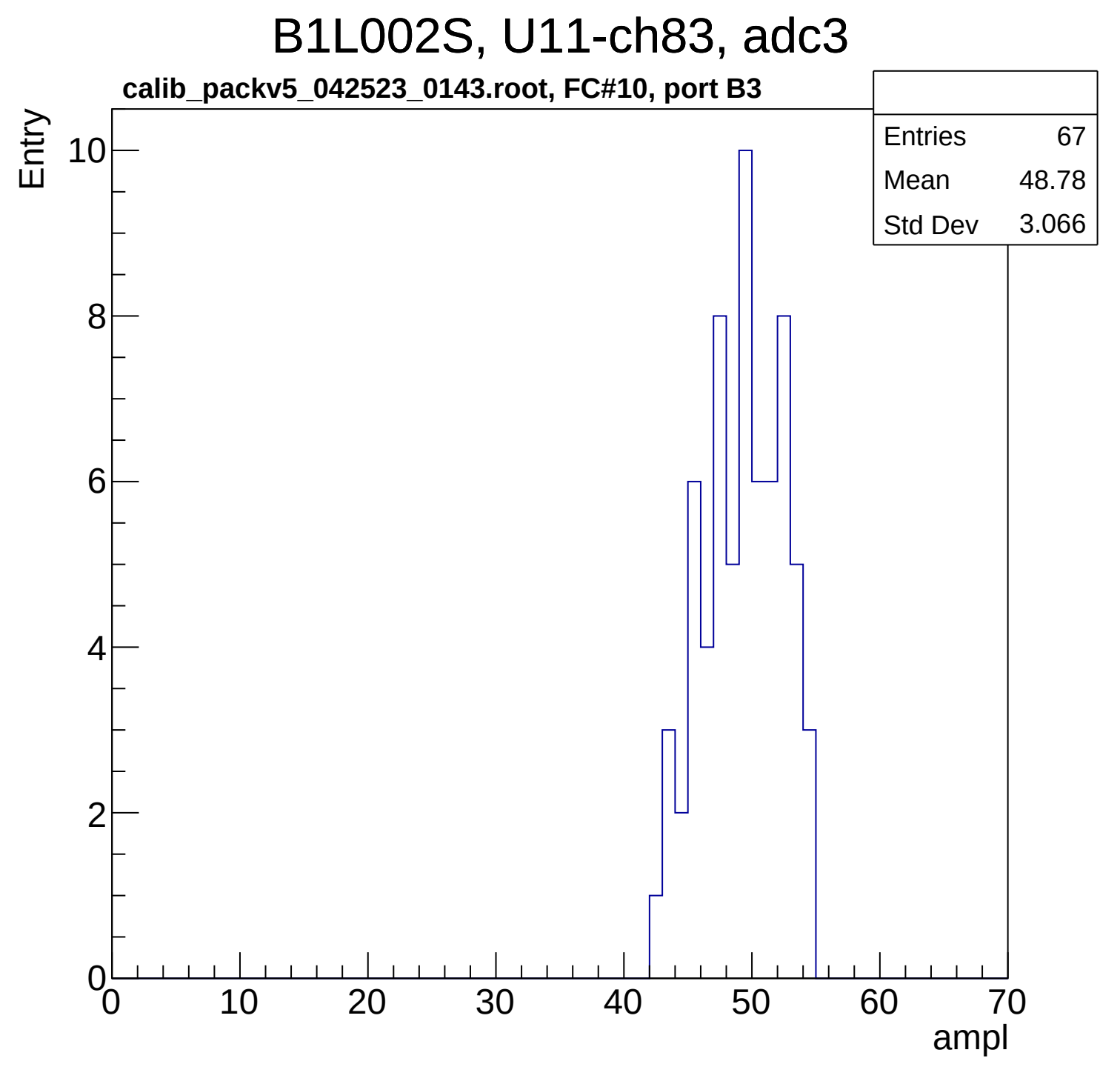
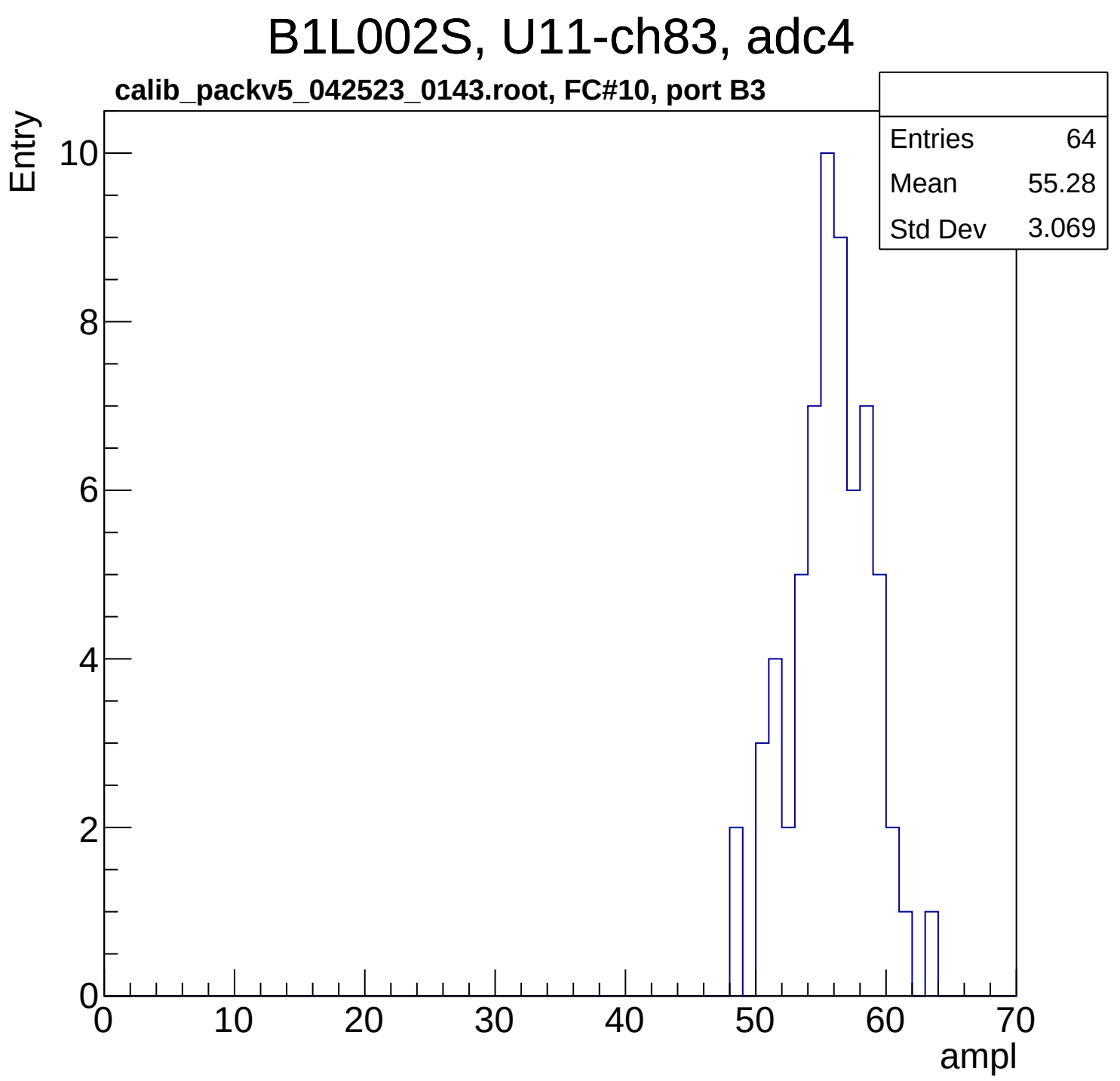

B1L002S, U11-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.82
Std Dev 4.991
Gaus mean : 30.8064
Gaus Width: 3.9937
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 37.56
Std Dev 3.555
Gaus mean : 38.1202
Gaus Width: 4.4254
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 43.2
Std Dev 2.857
Gaus mean : 43.5394
Gaus Width: 2.6632
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.78
Std Dev 3.066
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 55.28
Std Dev 3.069
Entry
ampl
0
2
4
6
8
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0
10
20
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40
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60
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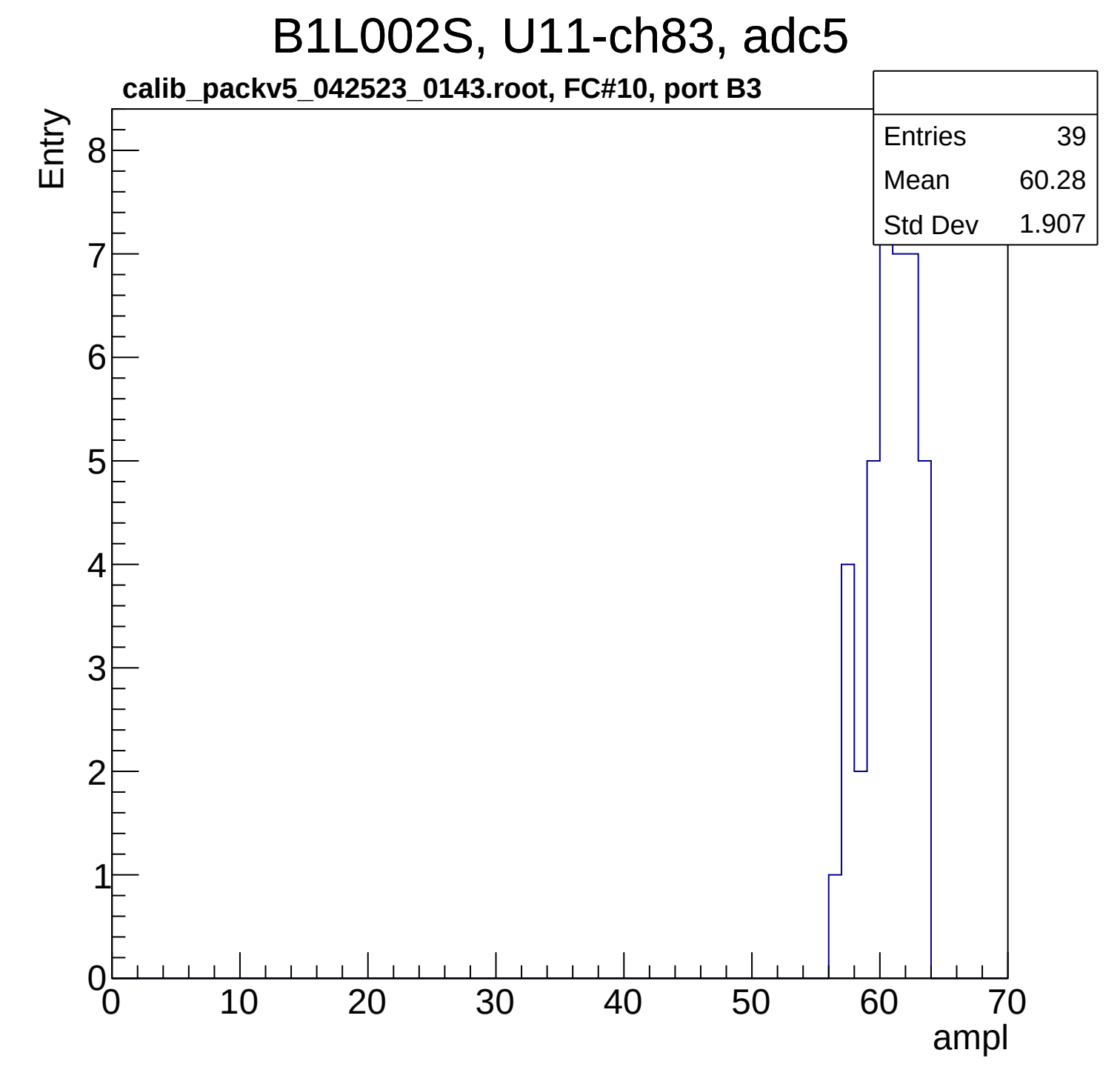
B1L002S, U11-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 60.28
Std Dev 1.907
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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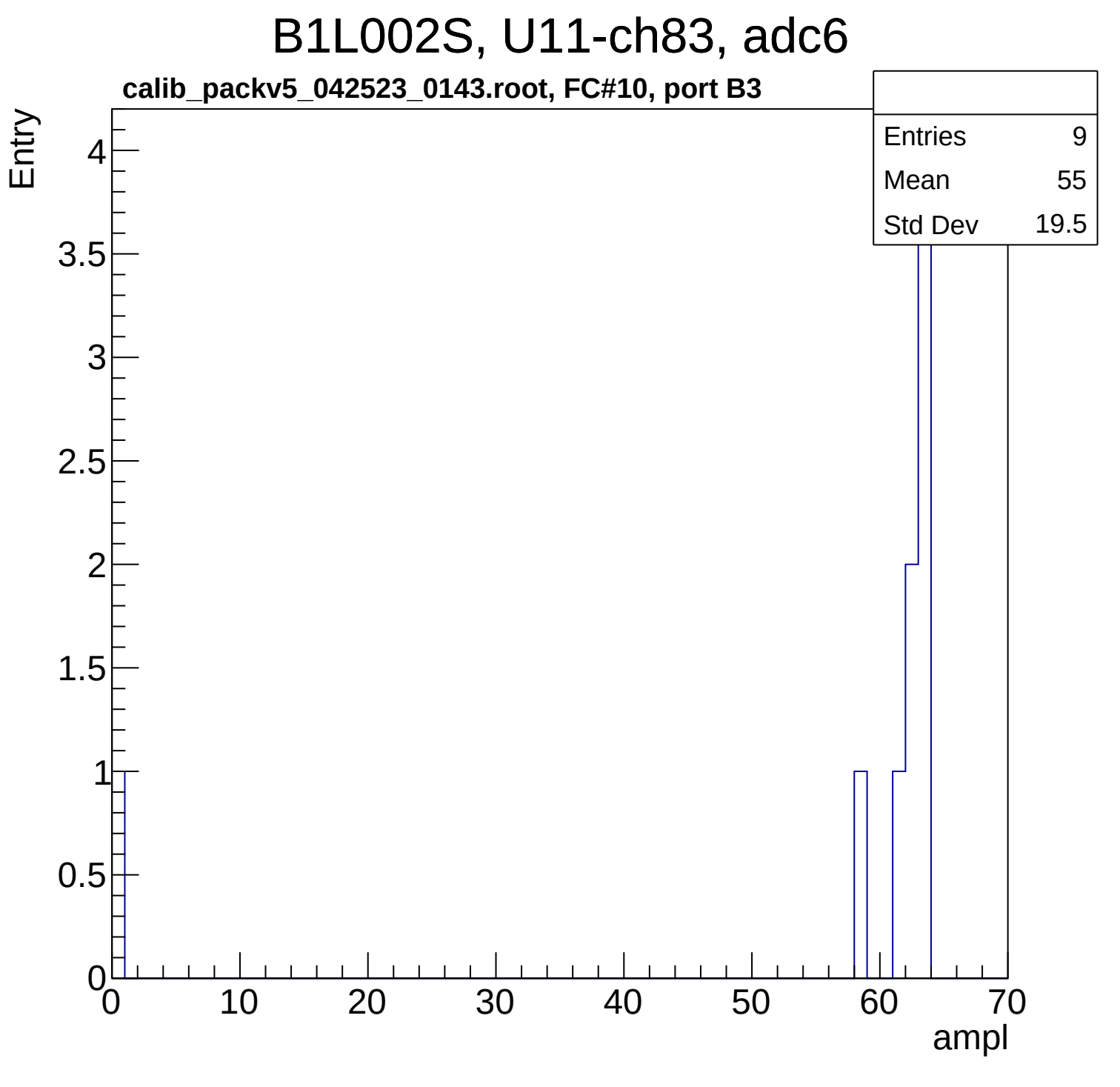
B1L002S, U11-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55
Std Dev 19.5
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
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0
10
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60
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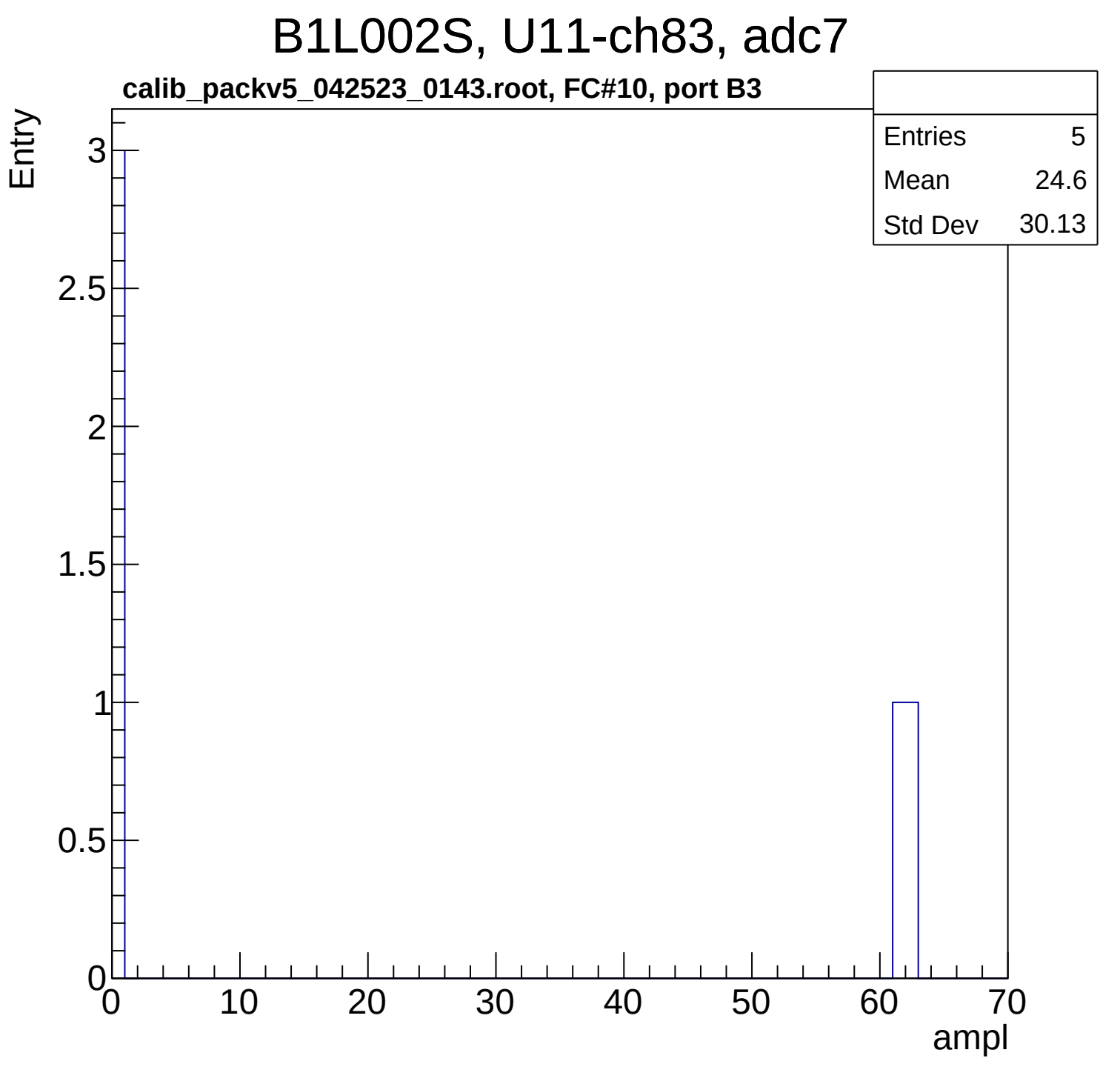
B1L002S, U11-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 24.6
Std Dev 30.13
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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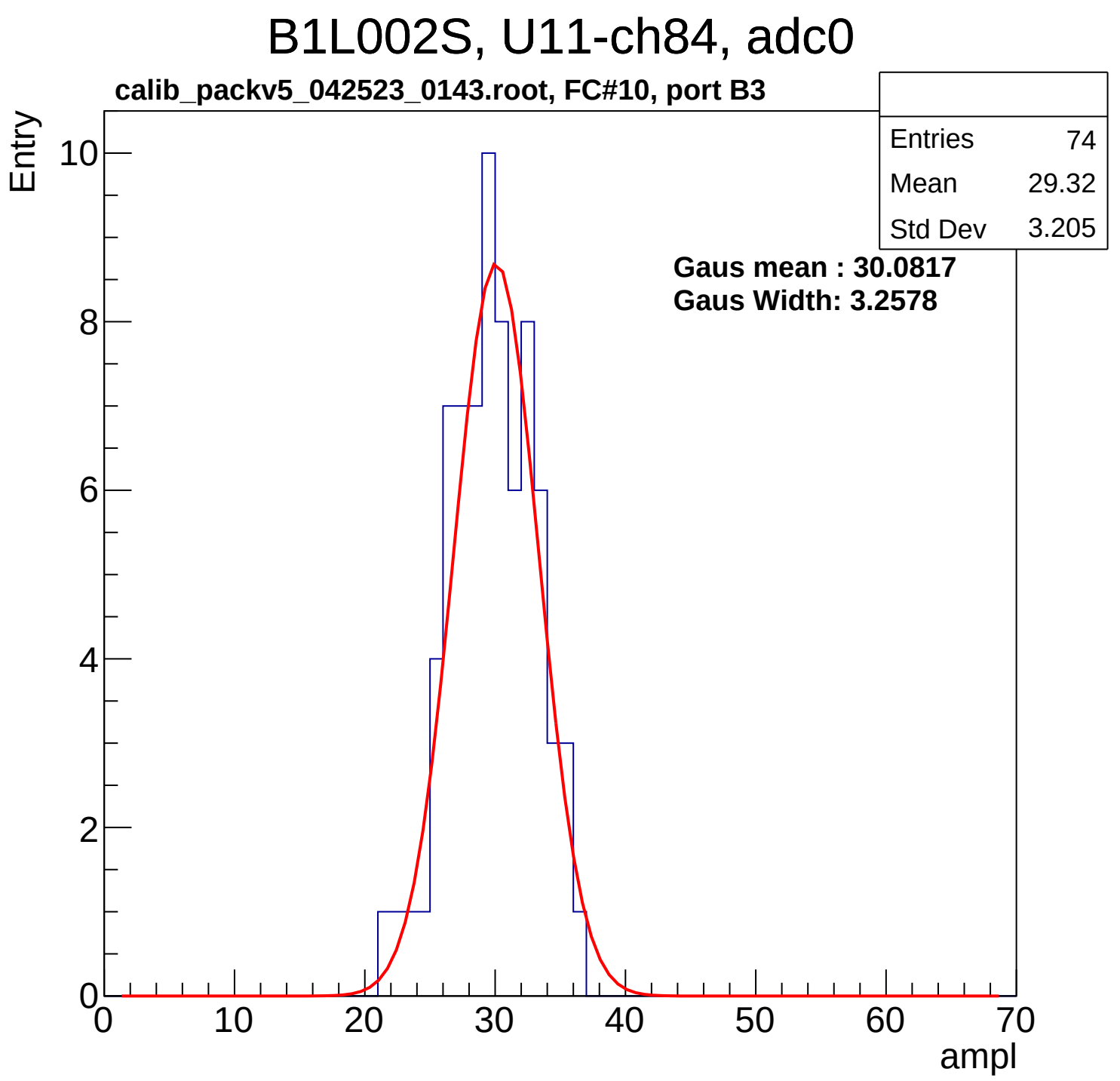
B1L002S, U11-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 29.32
Std Dev 3.205
Gaus mean : 30.0817
Gaus Width: 3.2578
Entry
ampl
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2
4
6
8
10
0
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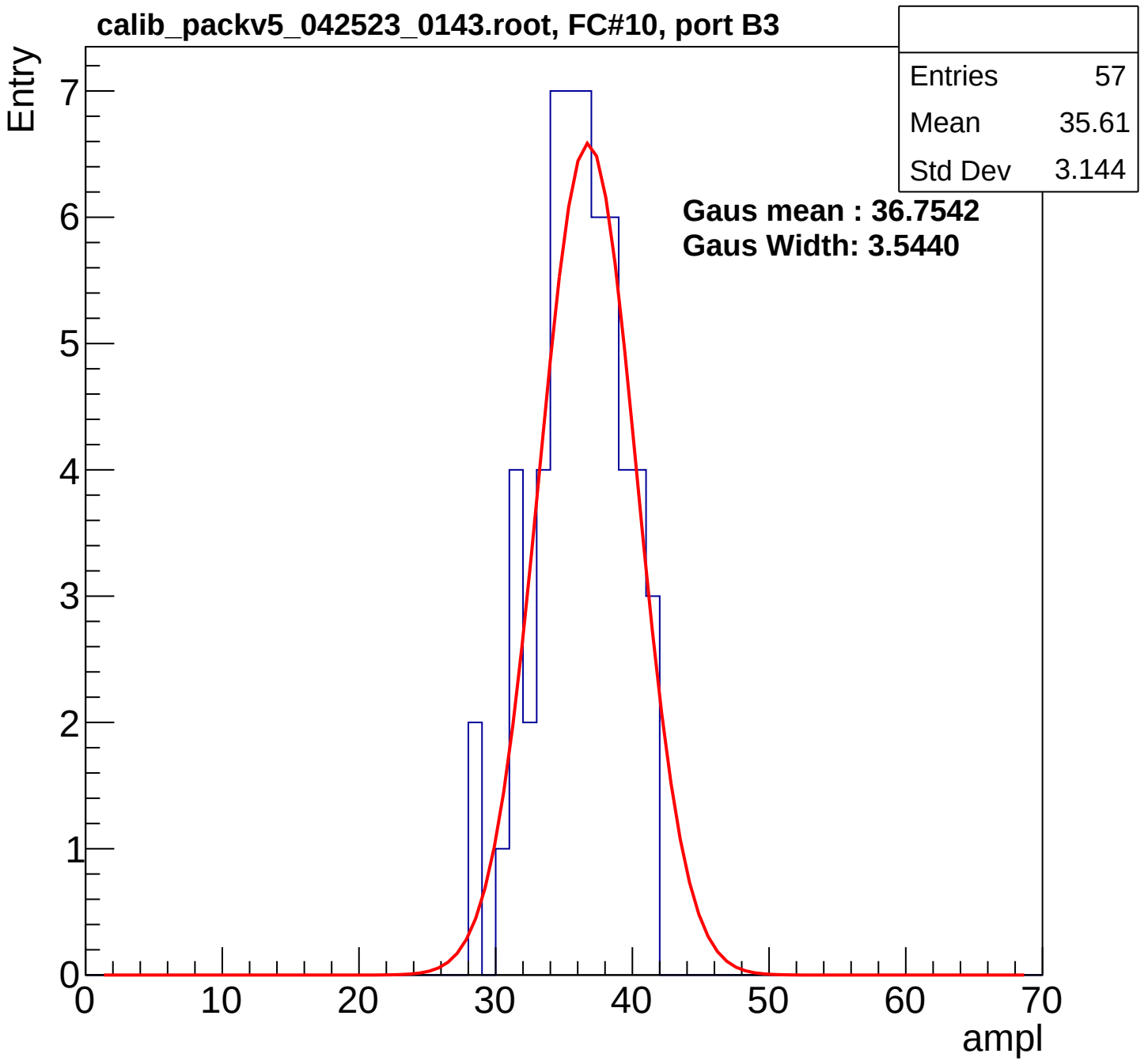
50
60
70


B1L002S, U11-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.61
Std Dev 3.144
Gaus mean : 36.7542
Gaus Width: 3.5440
Entry
ampl
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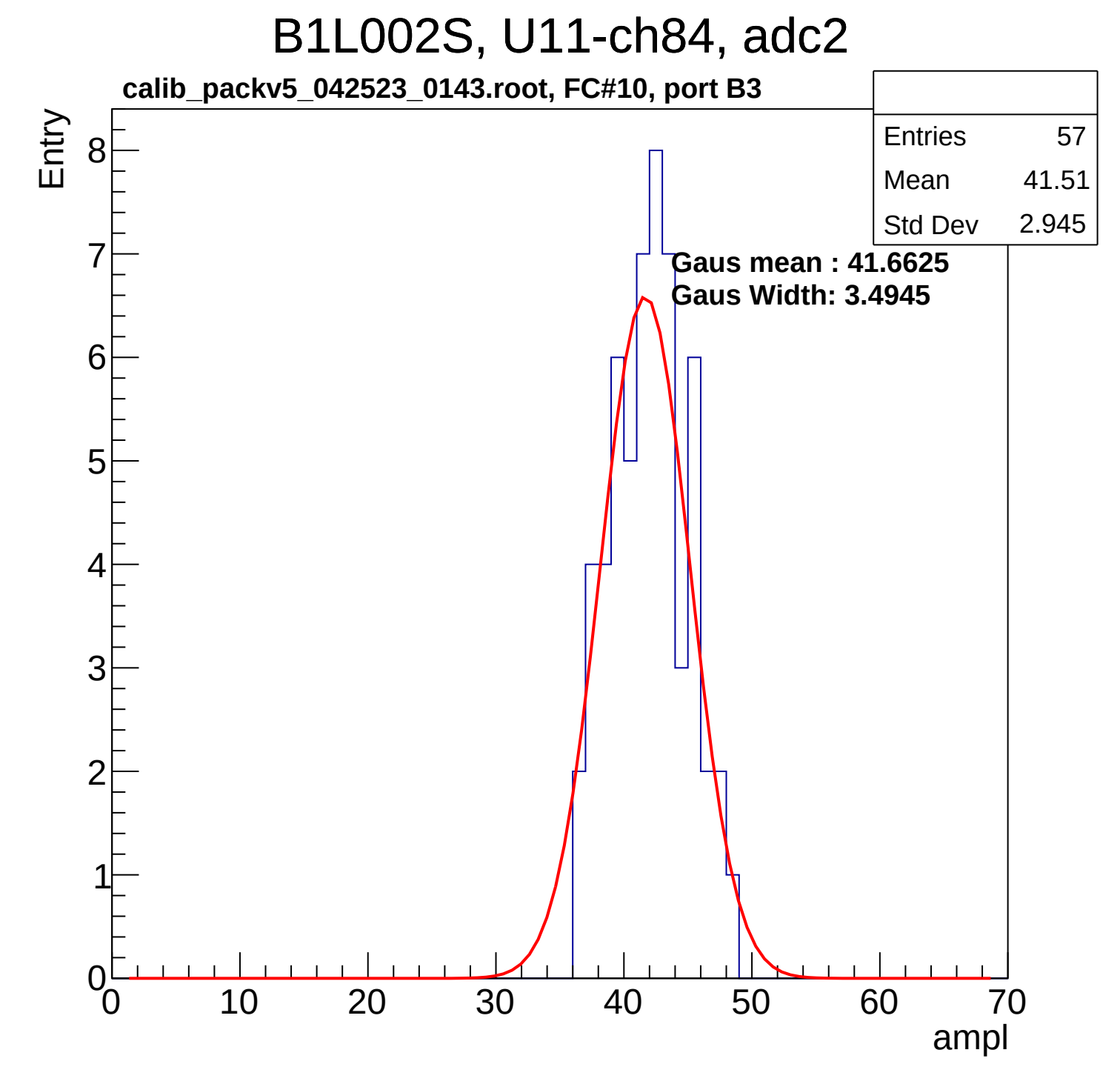
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.51
Std Dev 2.945
Gaus mean : 41.6625
Gaus Width: 3.4945
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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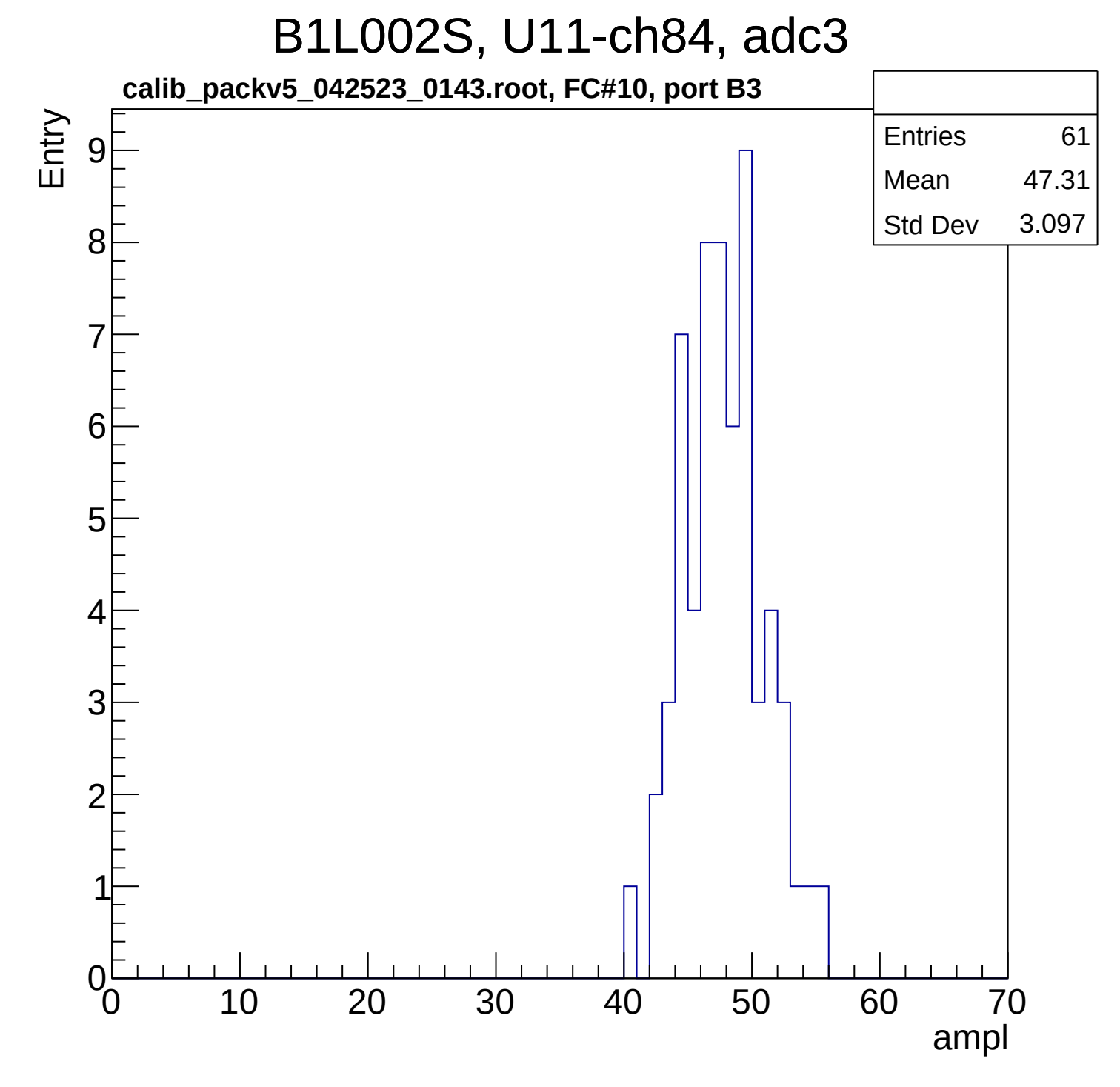
B1L002S, U11-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.31
Std Dev 3.097
Entry
ampl
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6
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0
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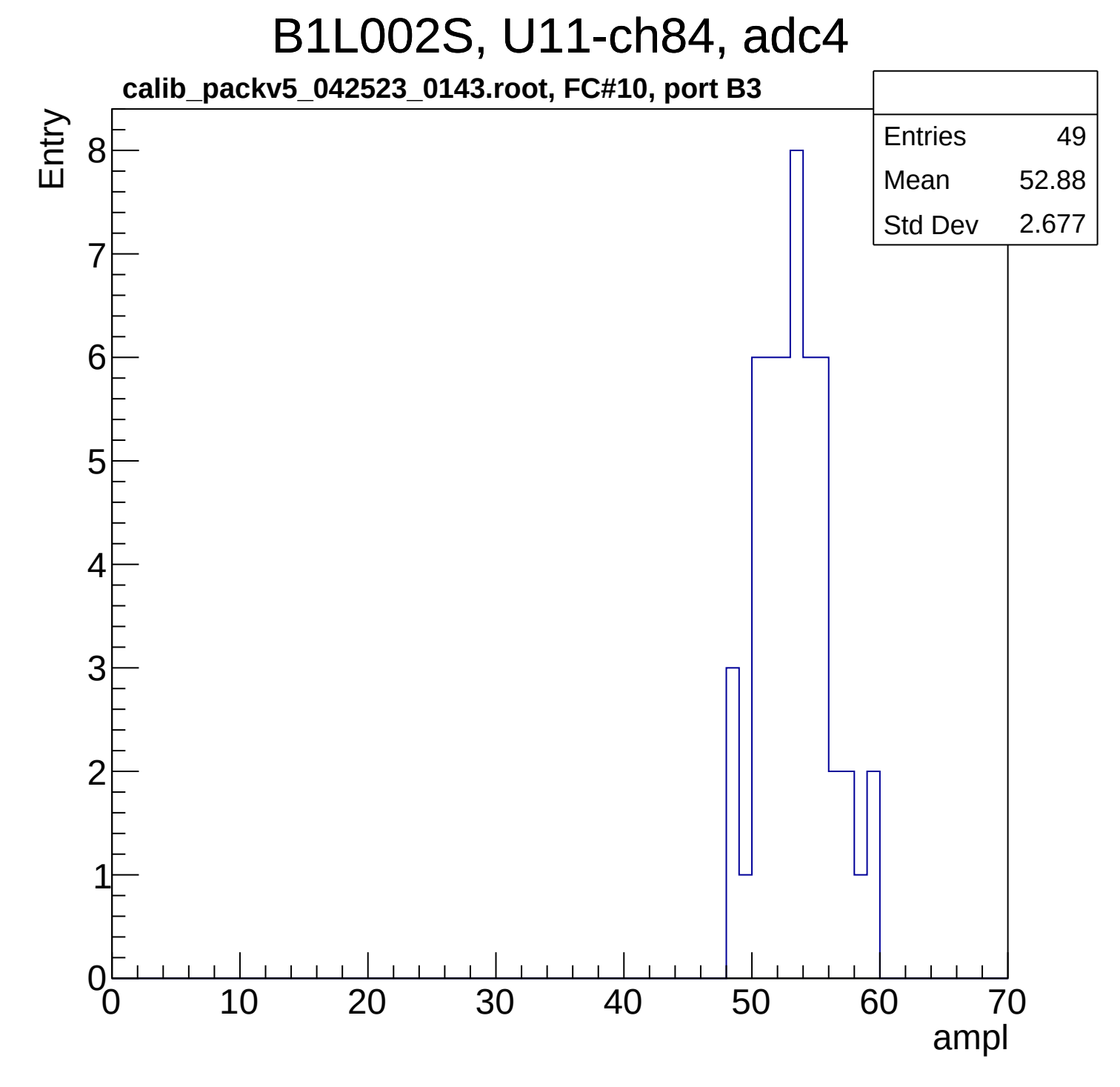
B1L002S, U11-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.88
Std Dev 2.677
Entry
ampl
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4
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6
7
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0
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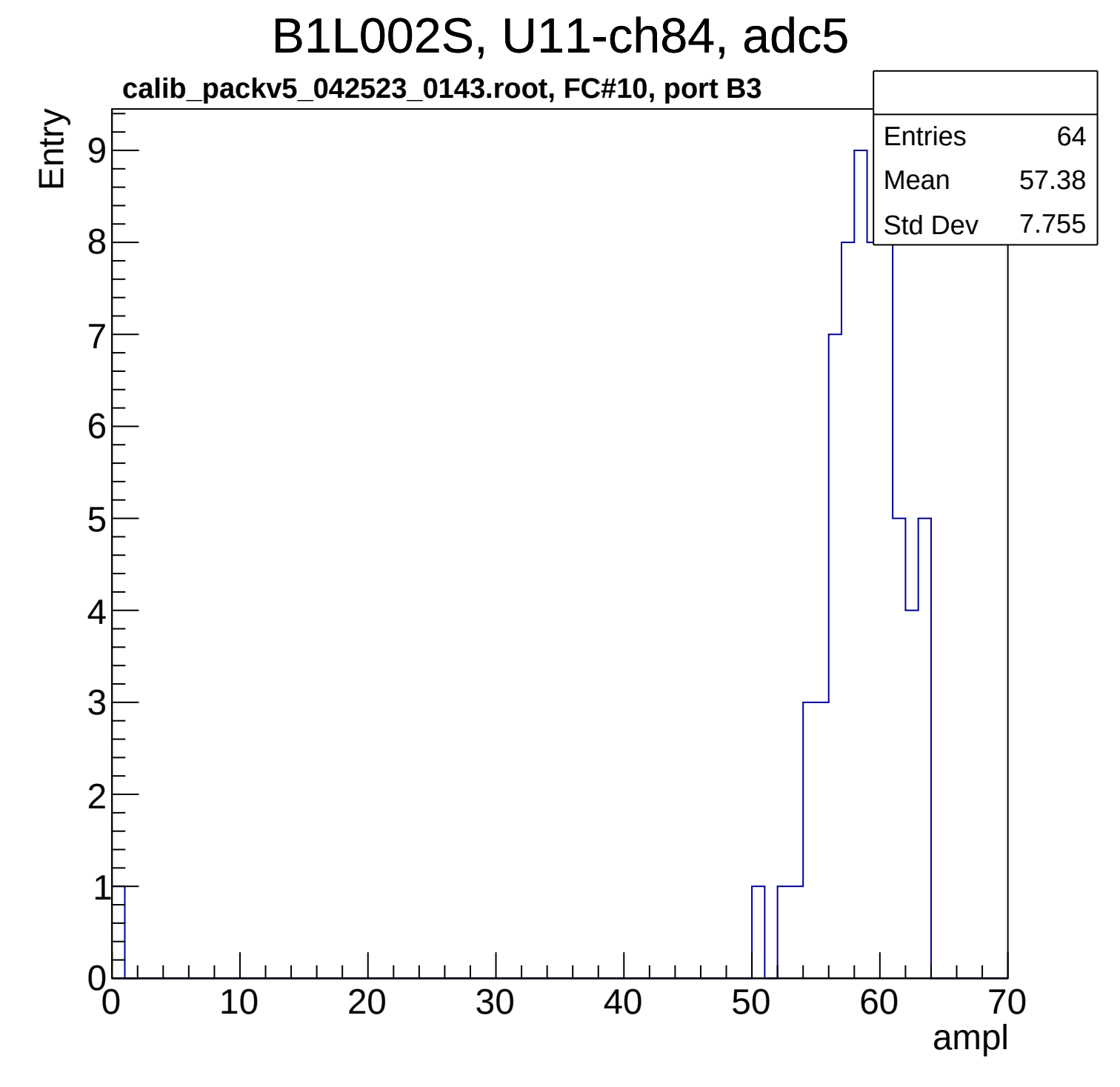
B1L002S, U11-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.38
Std Dev 7.755
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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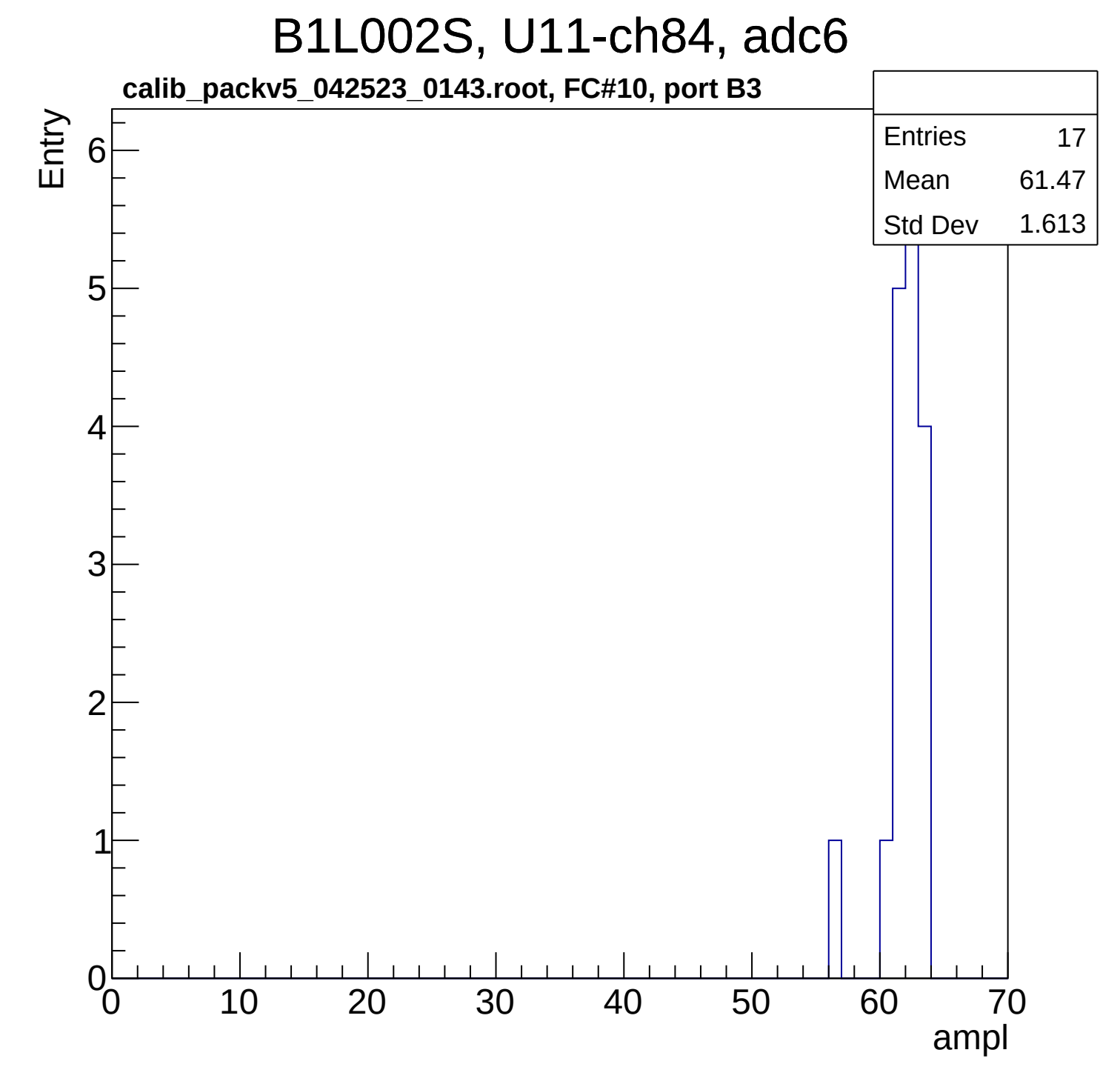
B1L002S, U11-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.47
Std Dev 1.613
Entry
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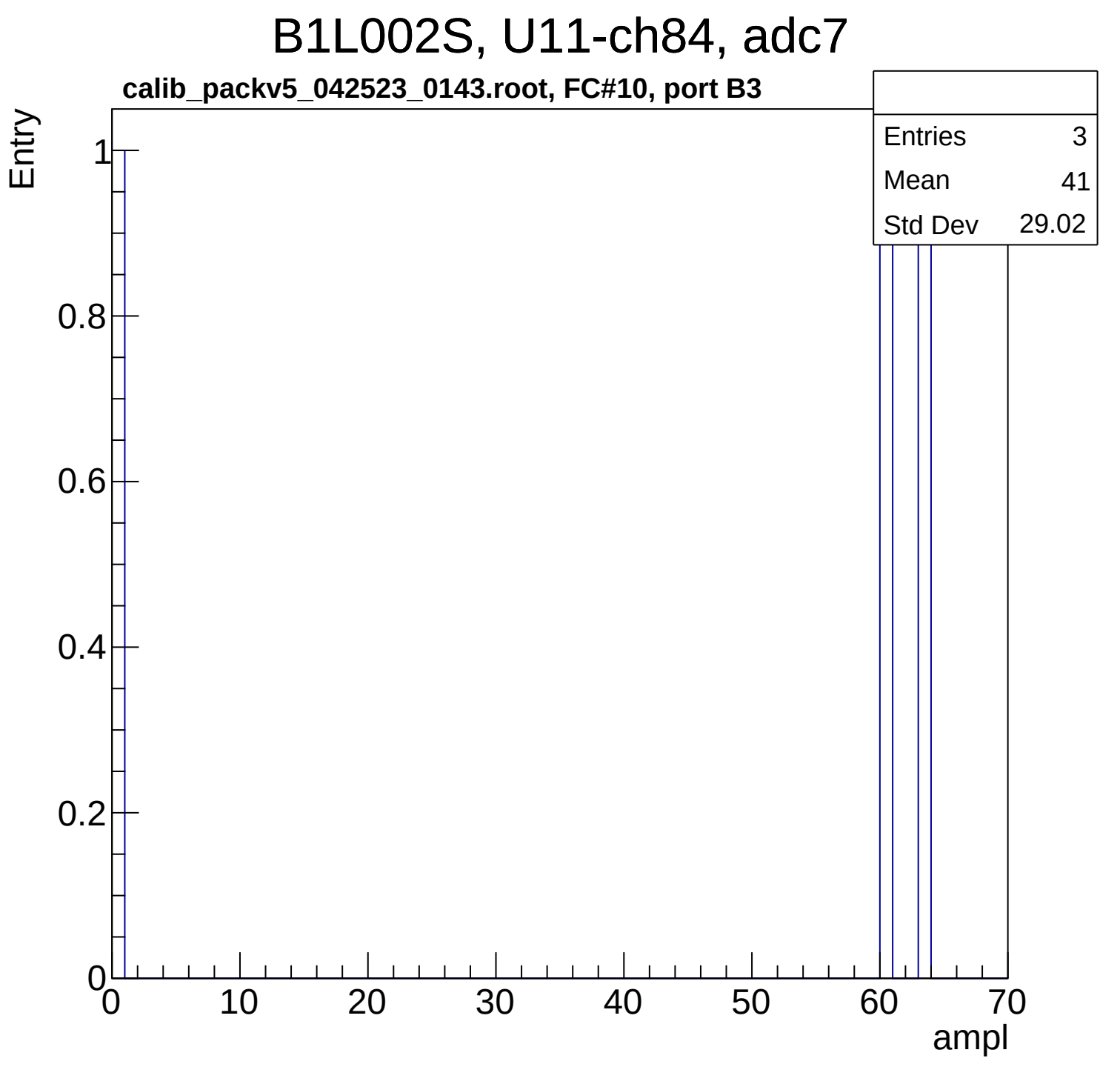
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 29.02
Entry
ampl
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0.4
0.6
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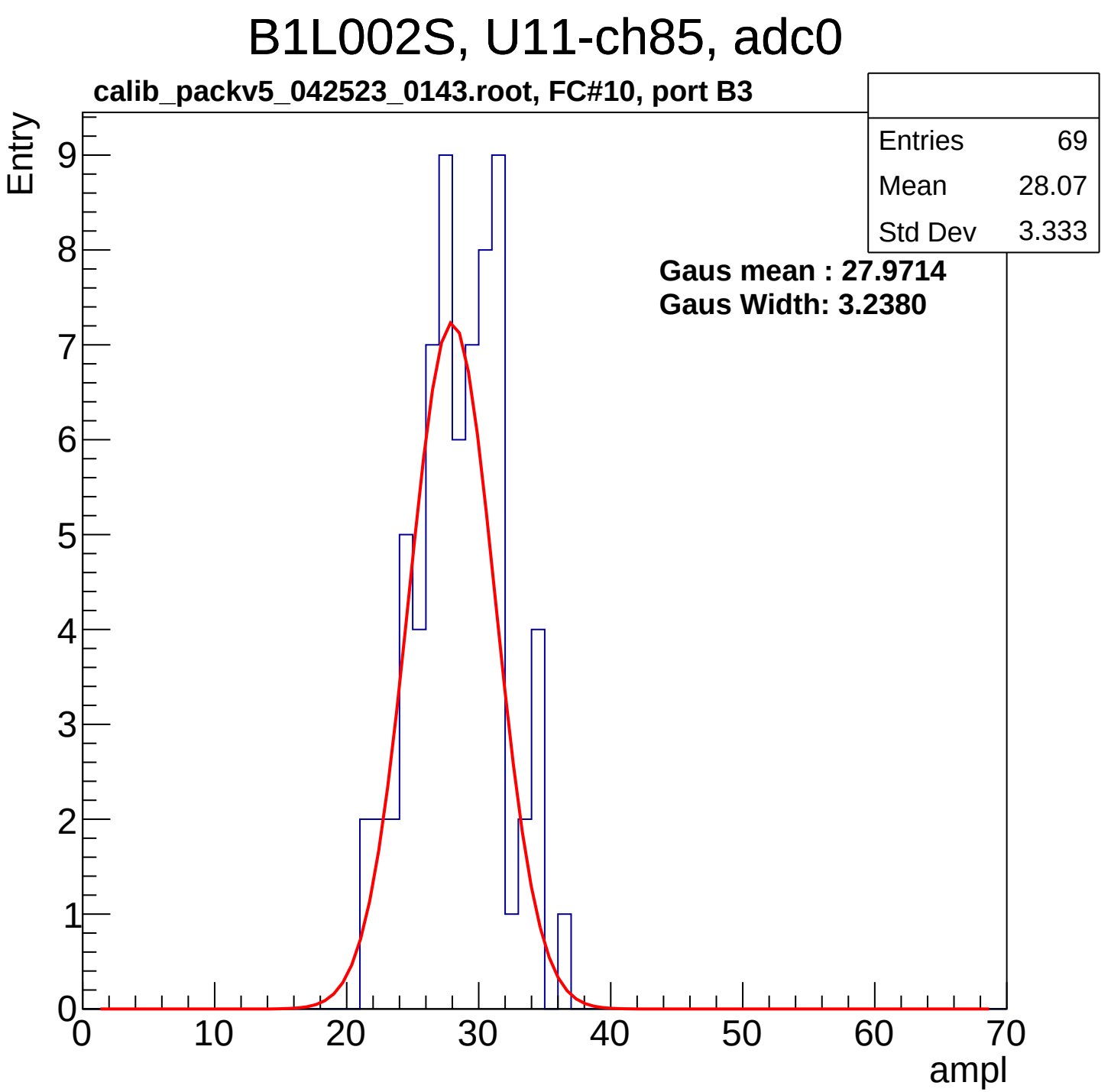
B1L002S, U11-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.07
Std Dev 3.333
Gaus mean : 27.9714
Gaus Width: 3.2380
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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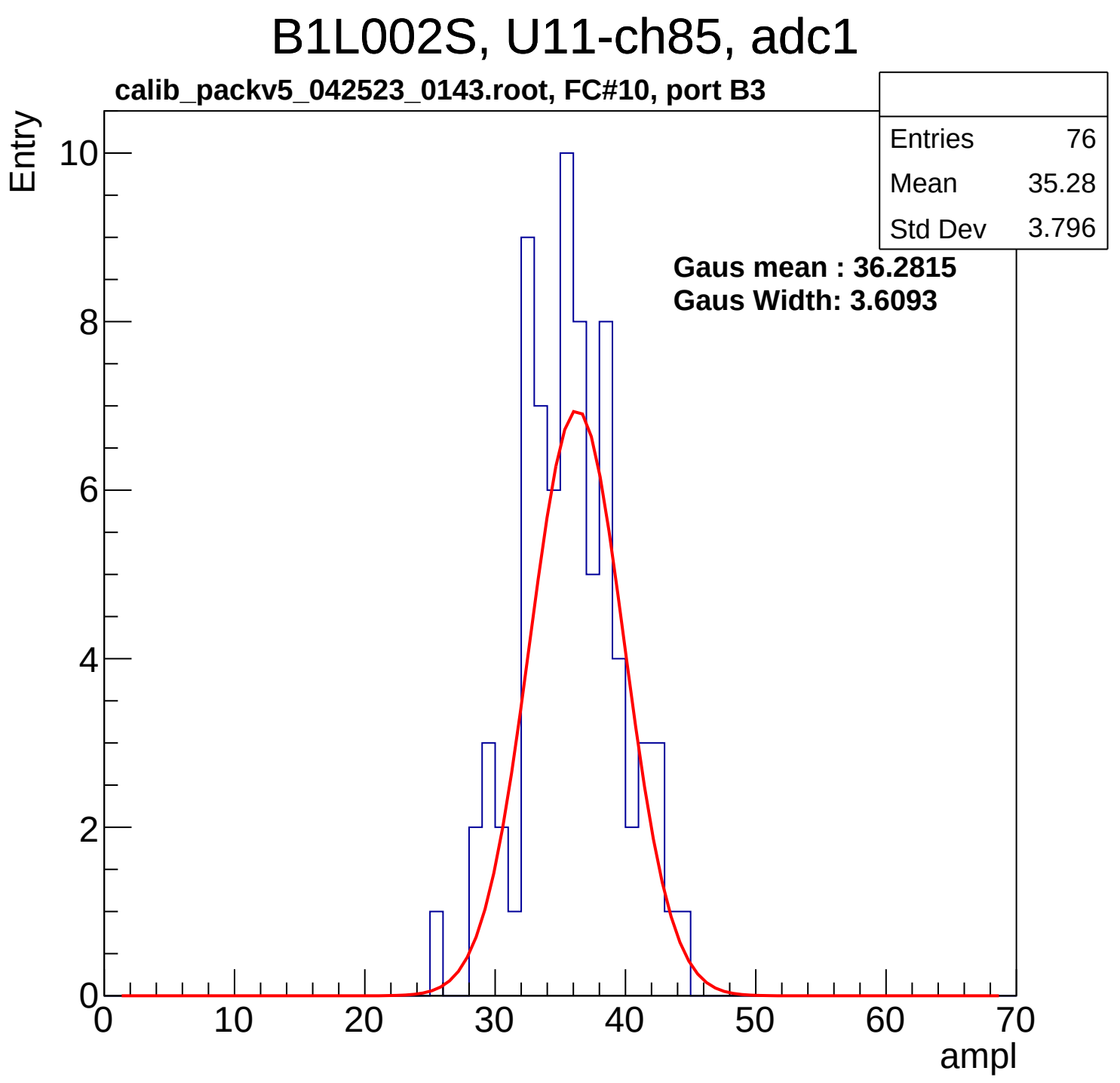
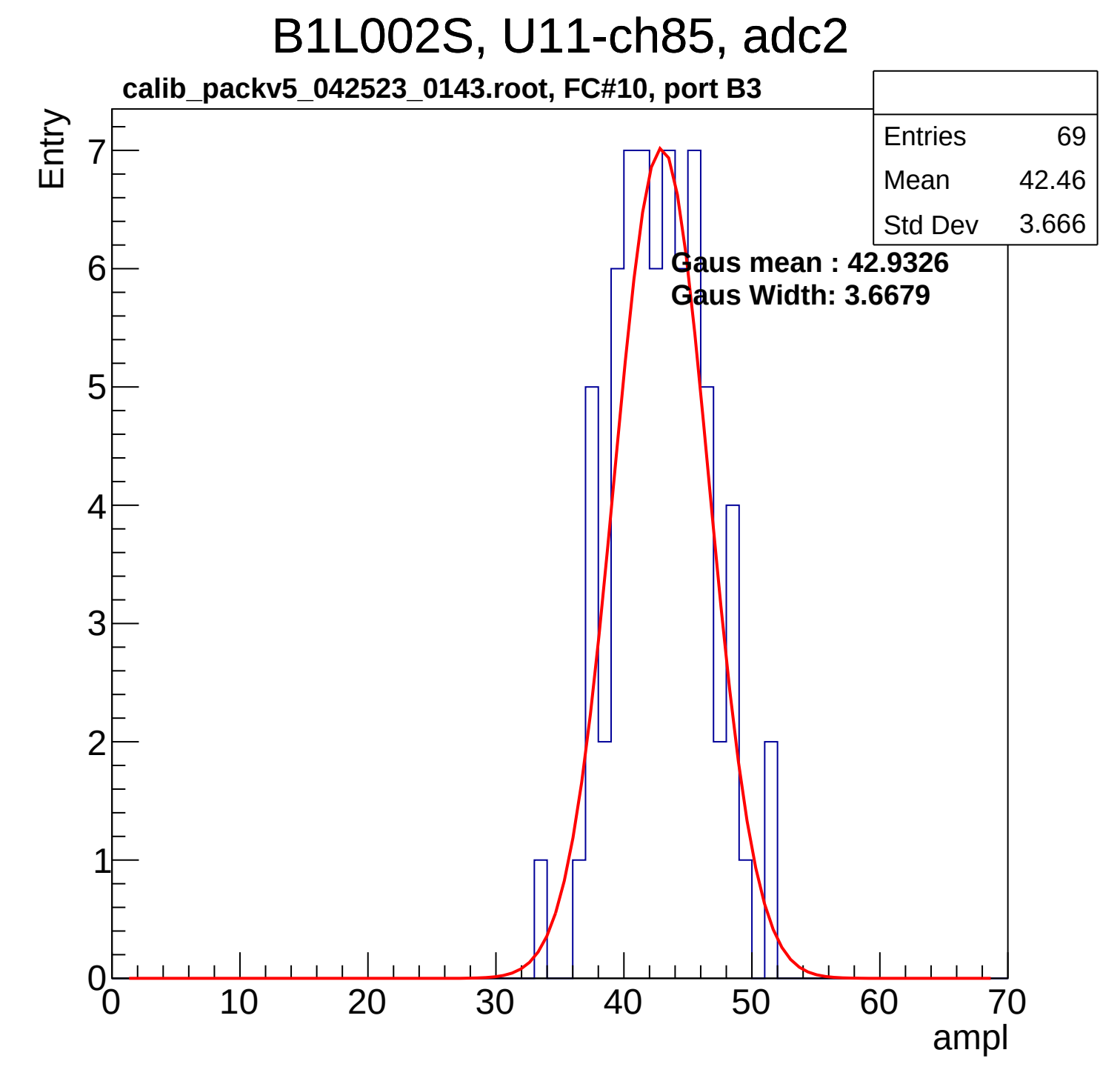

B1L002S, U11-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 35.28
Std Dev 3.796
Gaus mean : 36.2815
Gaus Width: 3.6093
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 42.46
Std Dev 3.666
Gaus mean : 42.9326
Gaus Width: 3.6679
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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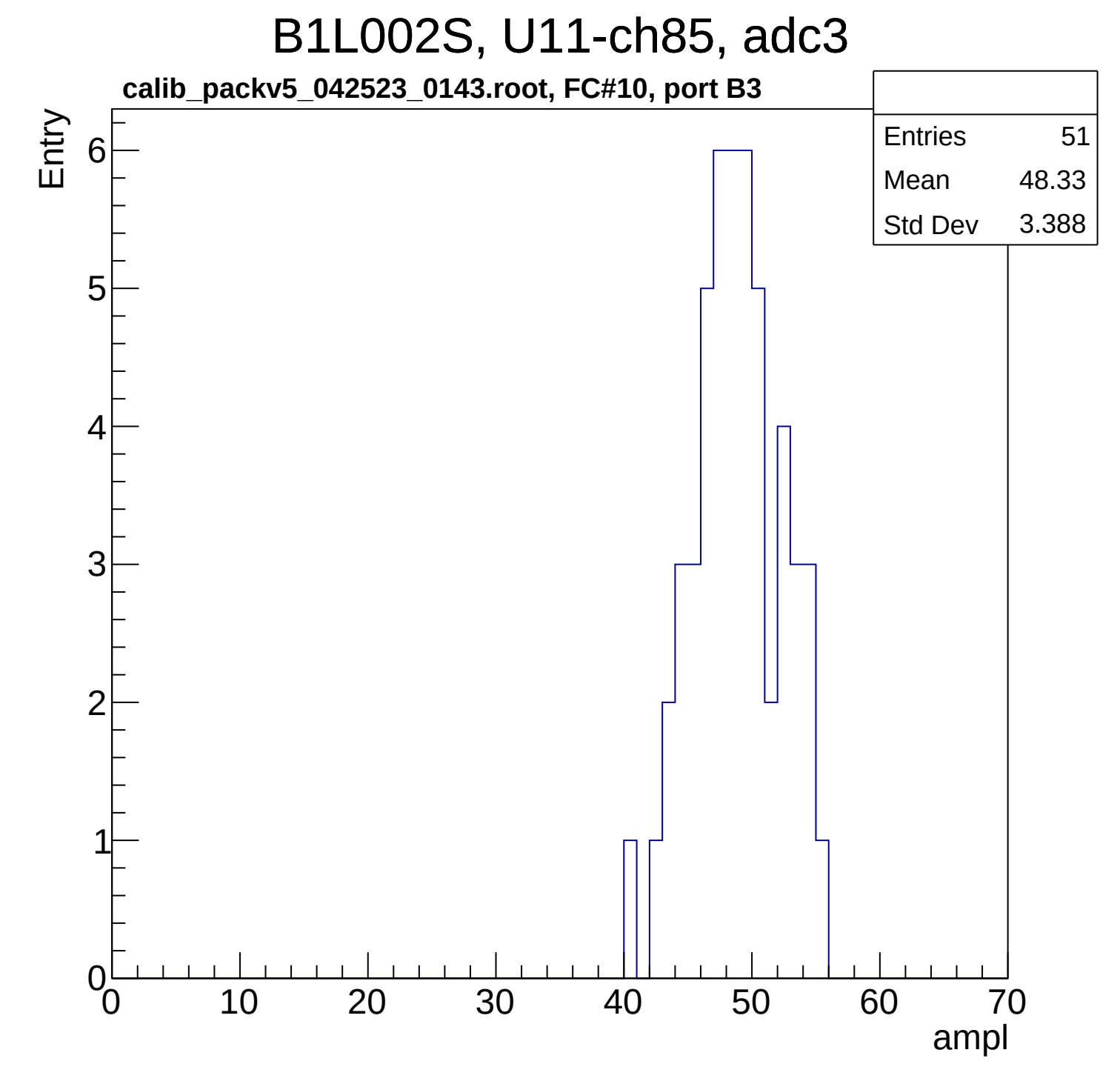
B1L002S, U11-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 48.33
Std Dev 3.388
Entry
0
1
2
3
4
5
6
0
10
20
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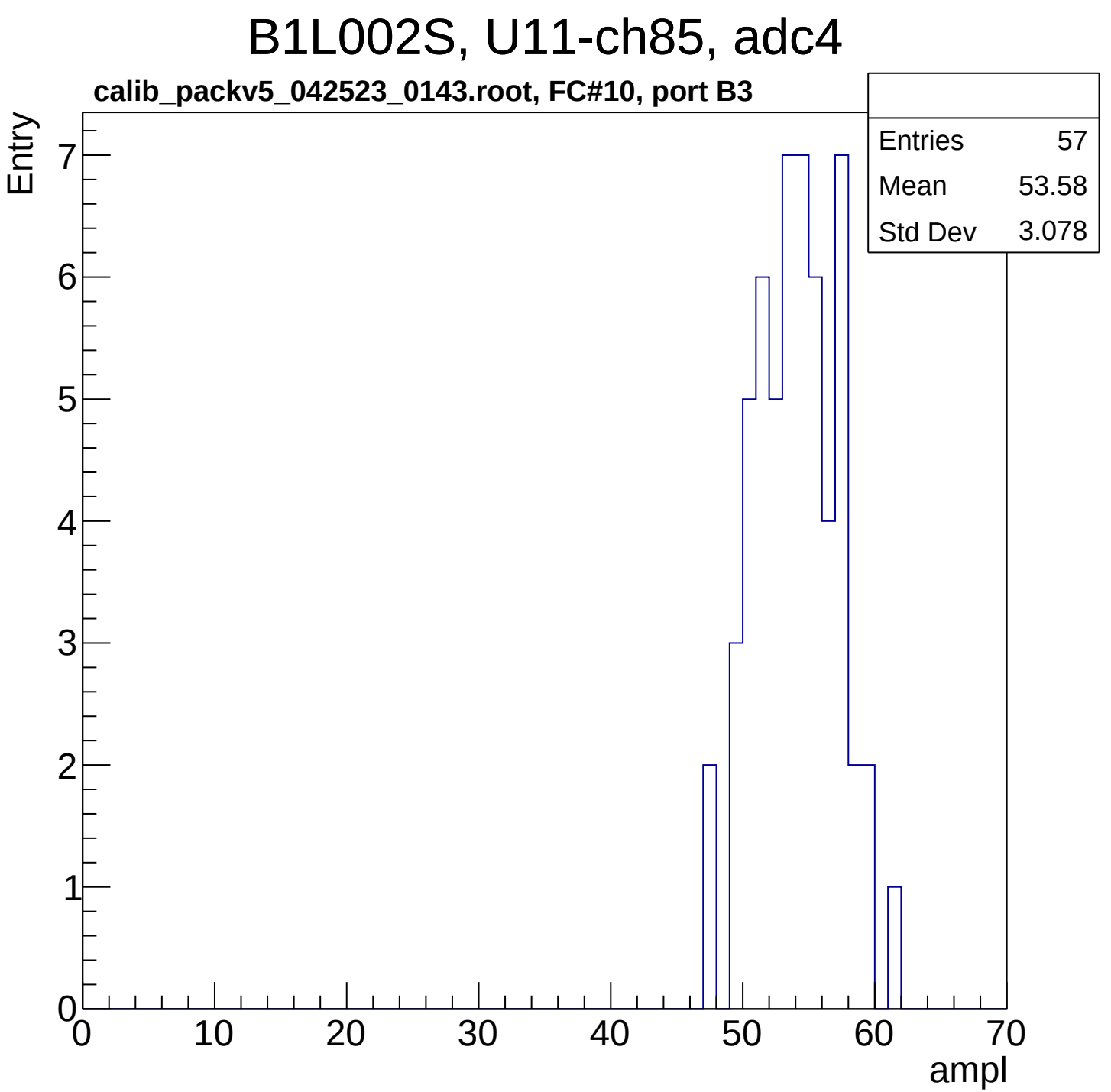
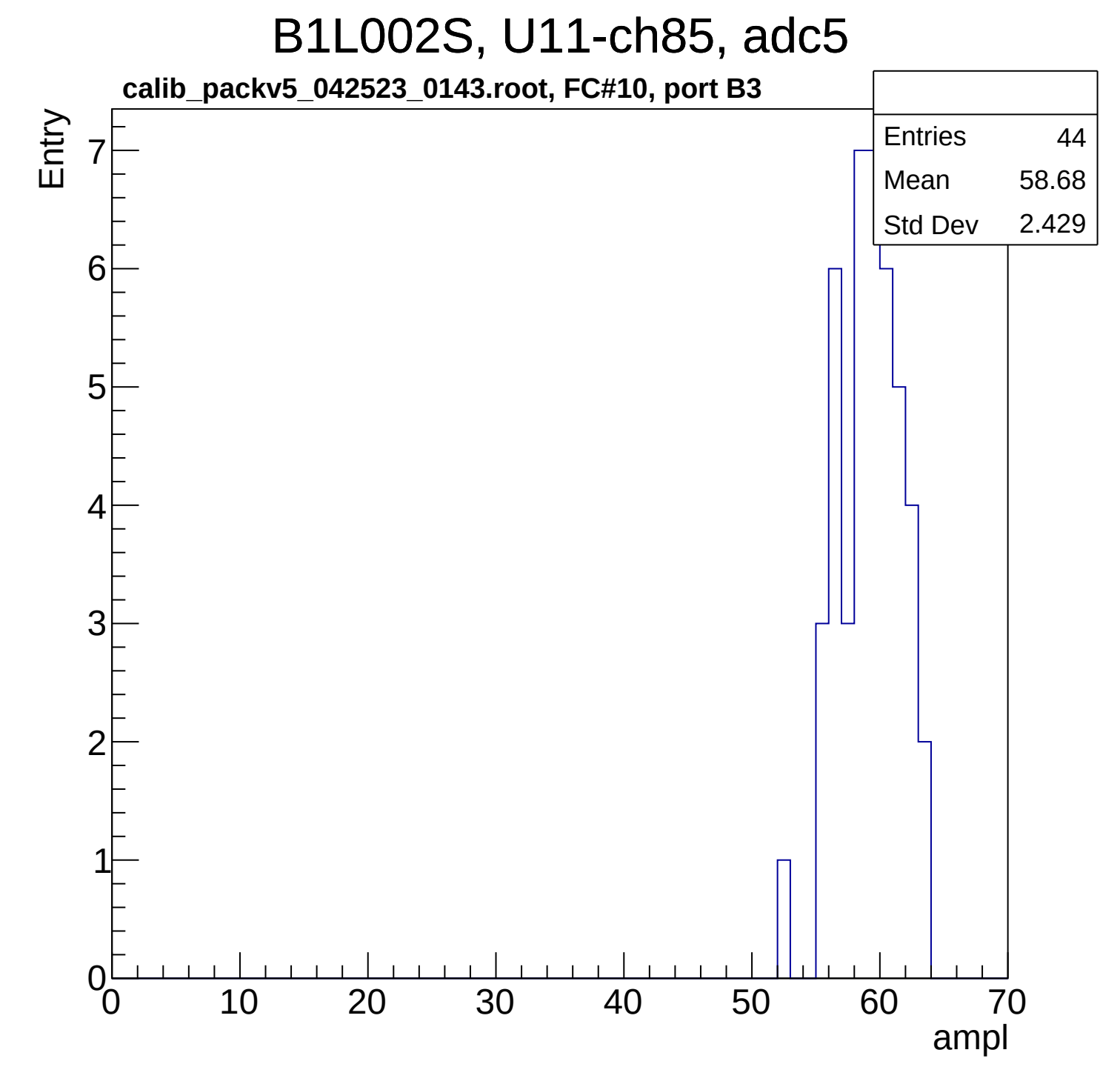
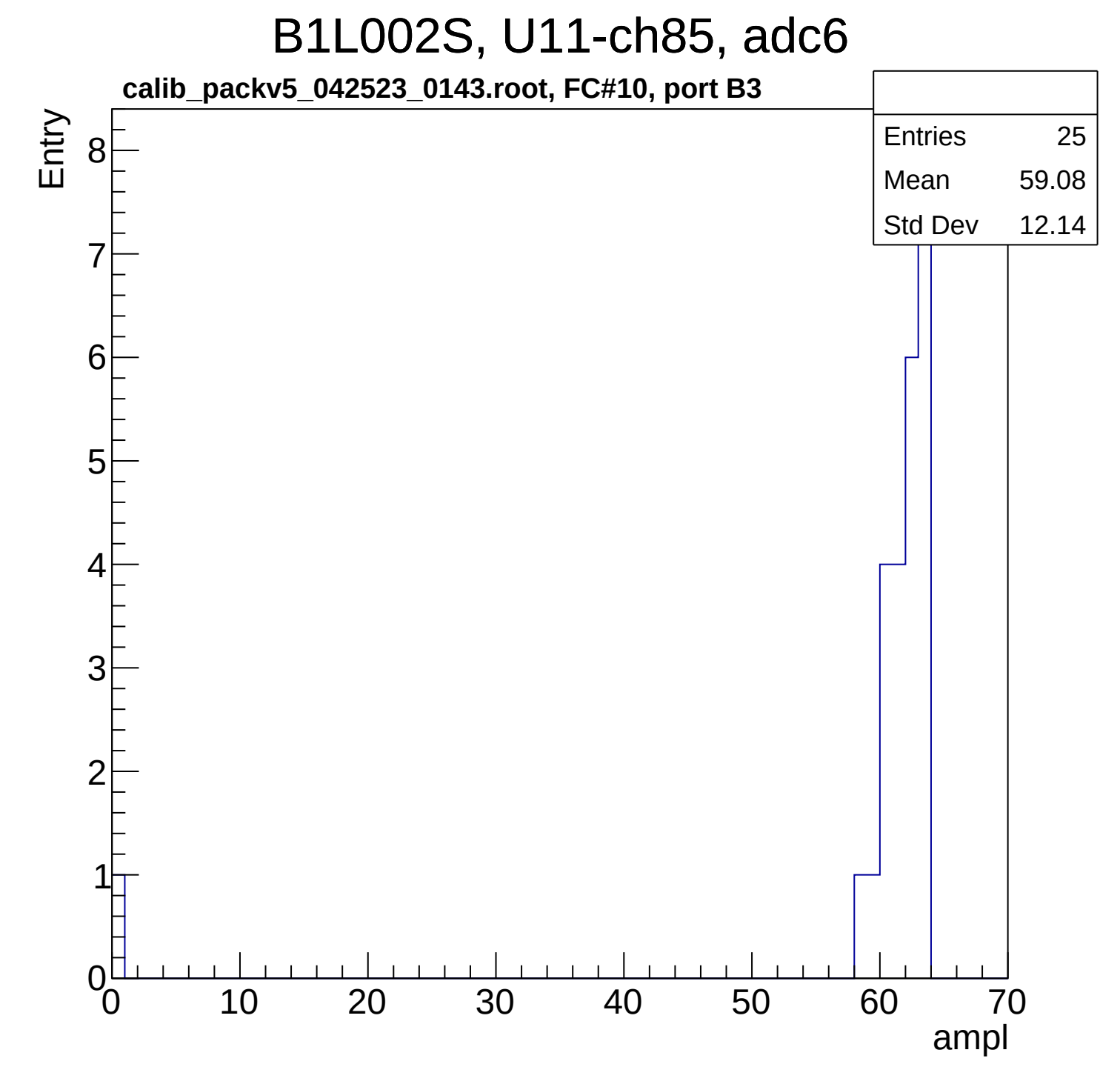
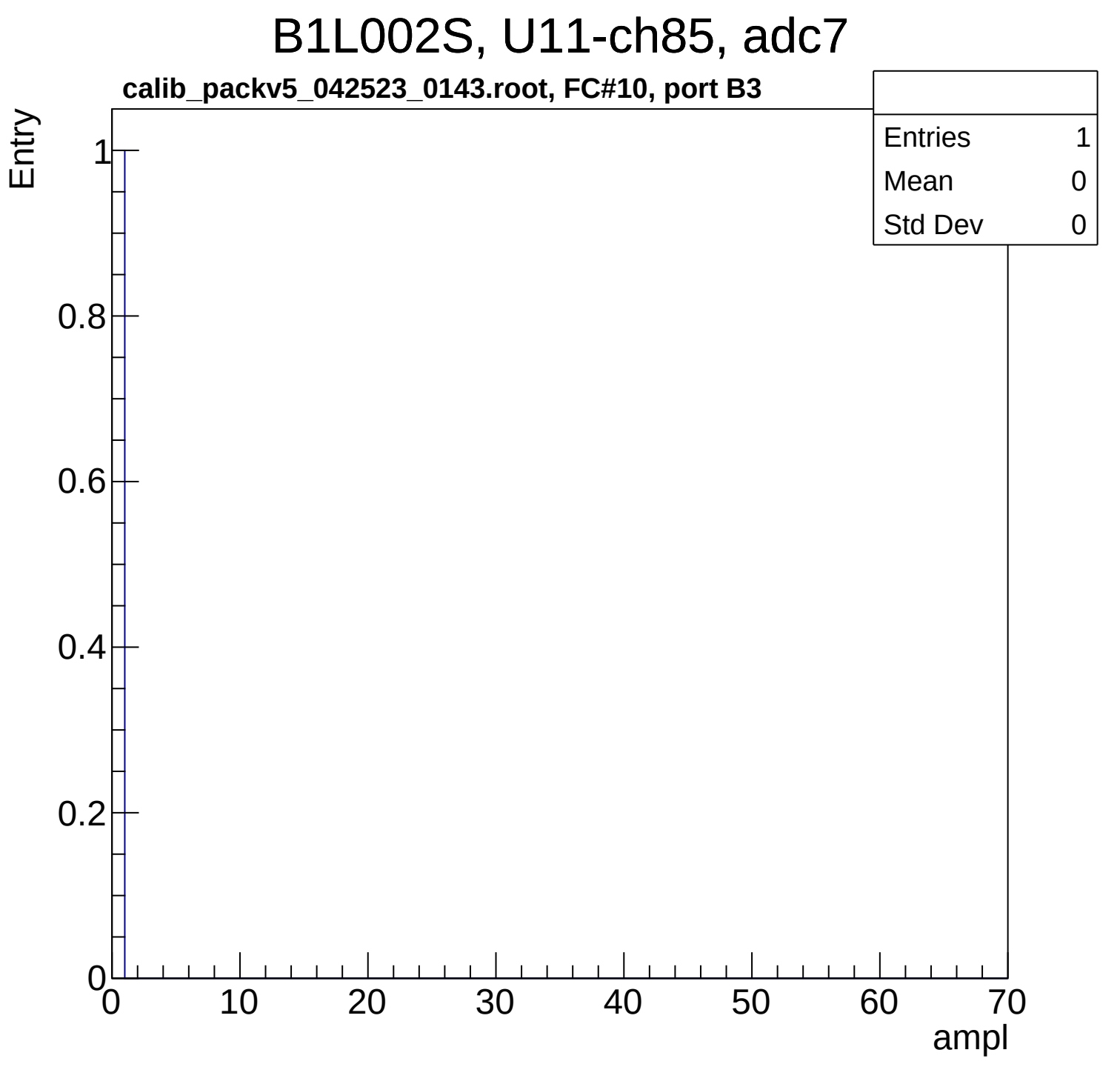
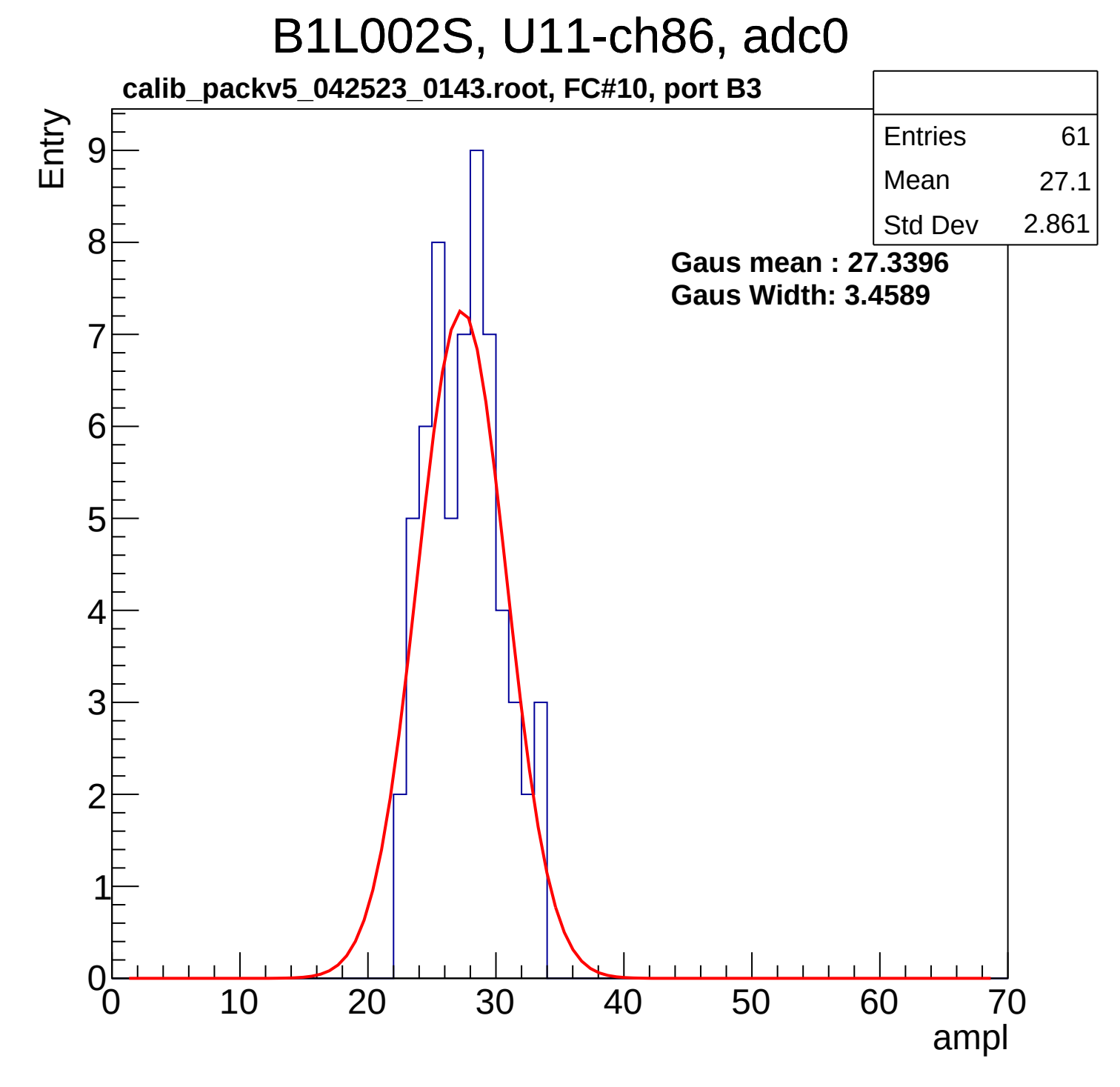
40
50
60
70
ampl

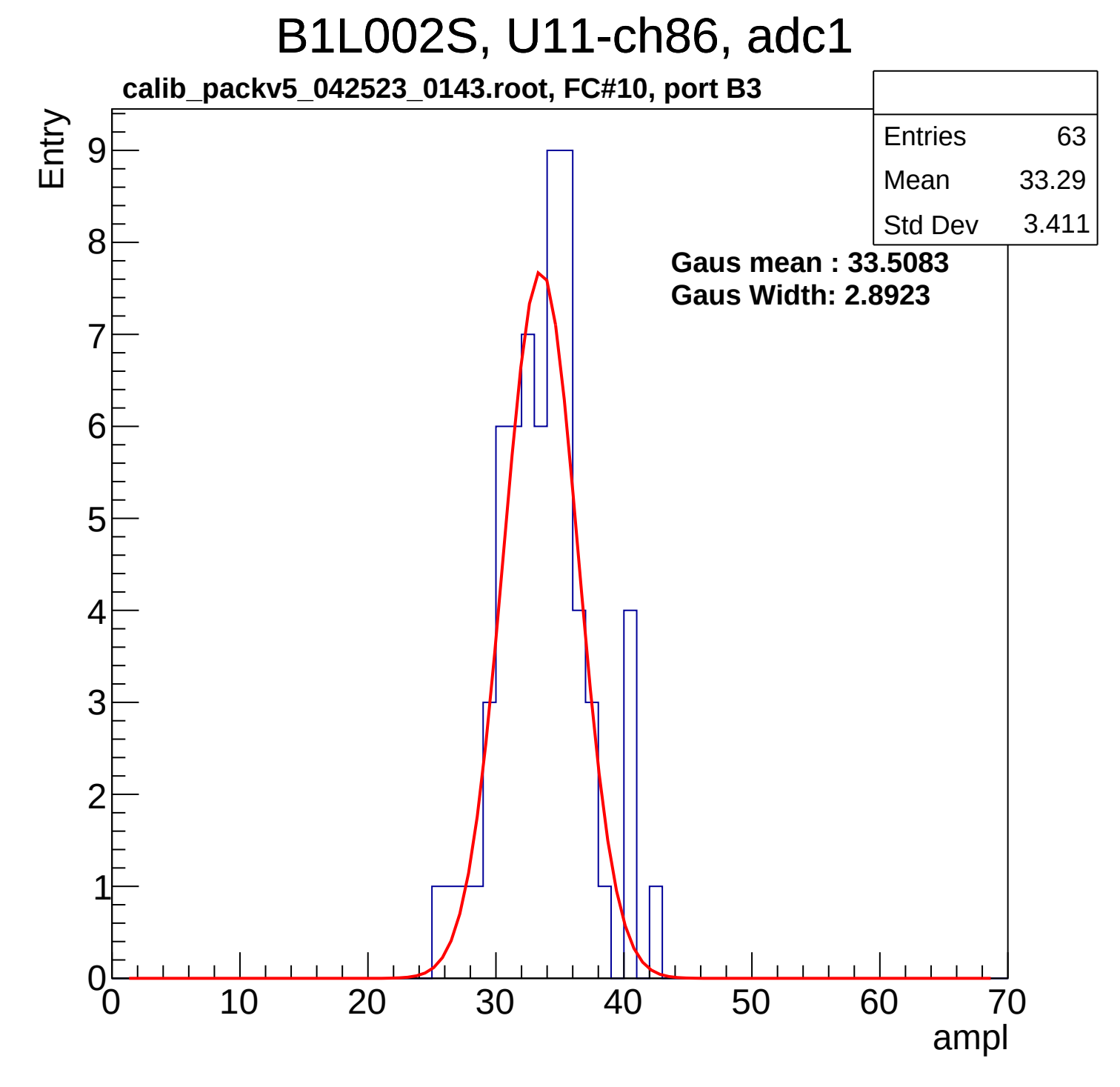

B1L002S, U11-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.58
Std Dev 3.078
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.68
Std Dev 2.429
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 59.08
Std Dev 12.14
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.1
Std Dev 2.861
Gaus mean : 27.3396
Gaus Width: 3.4589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.29
Std Dev 3.411
Gaus mean : 33.5083
Gaus Width: 2.8923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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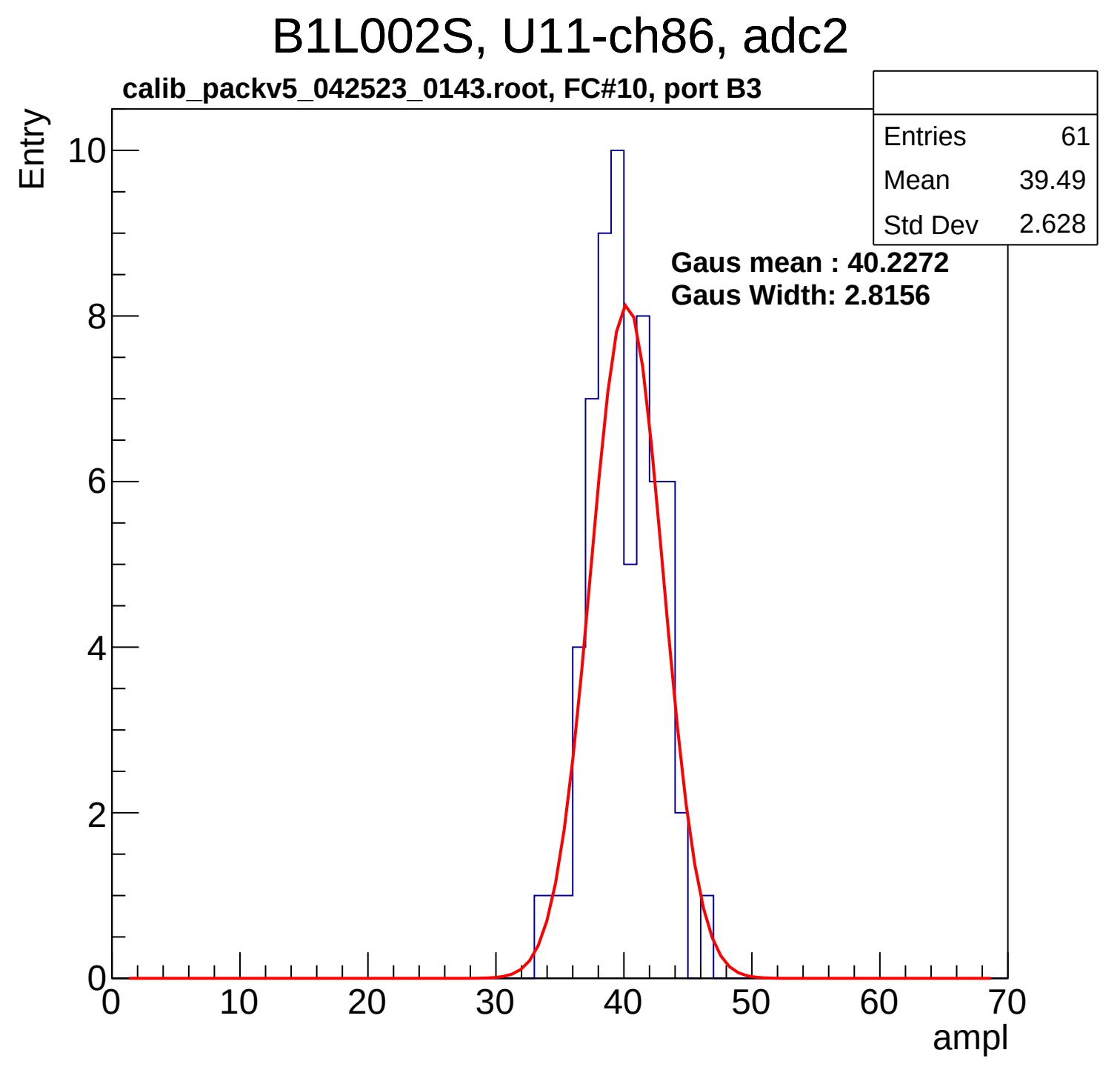
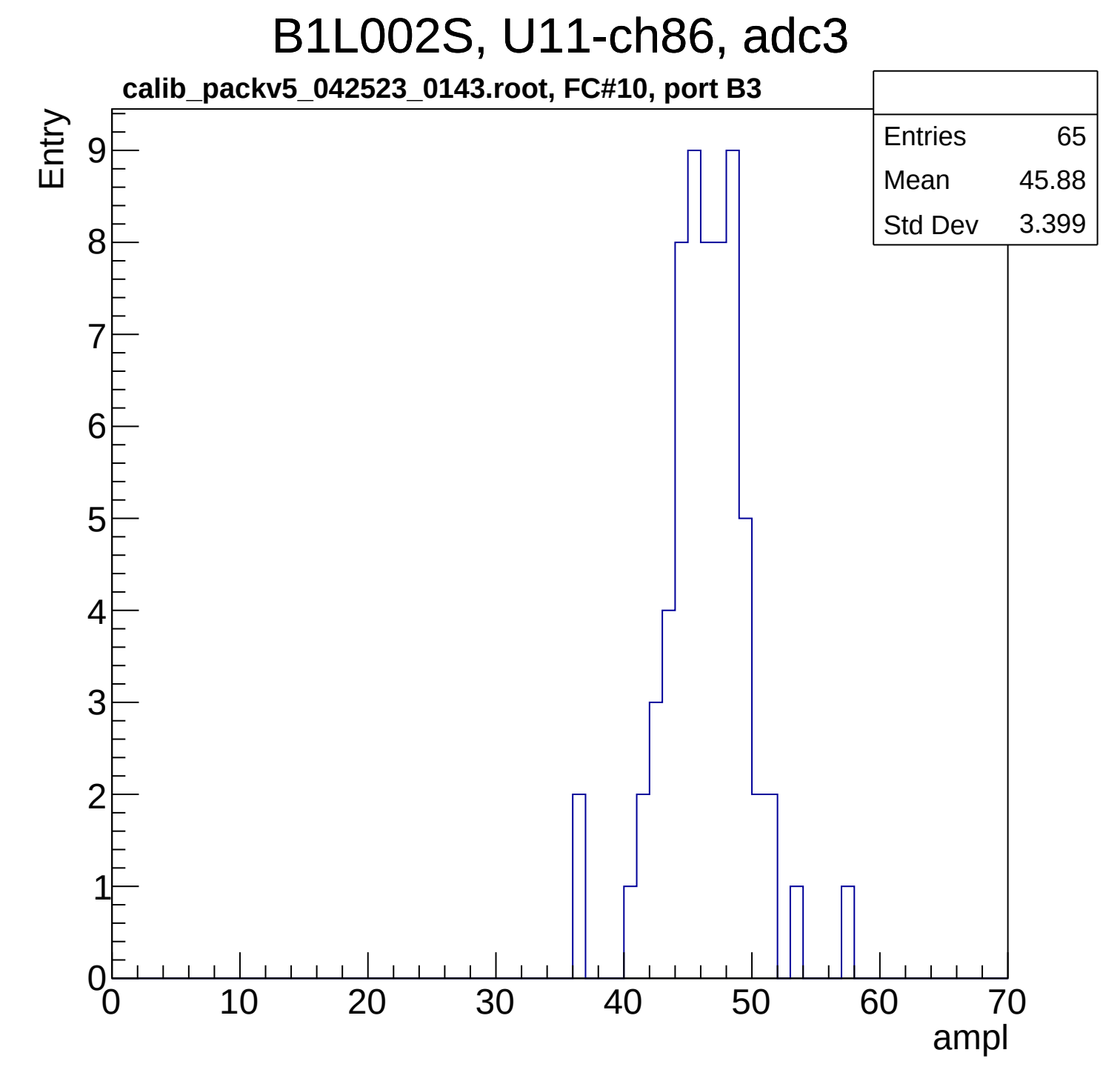
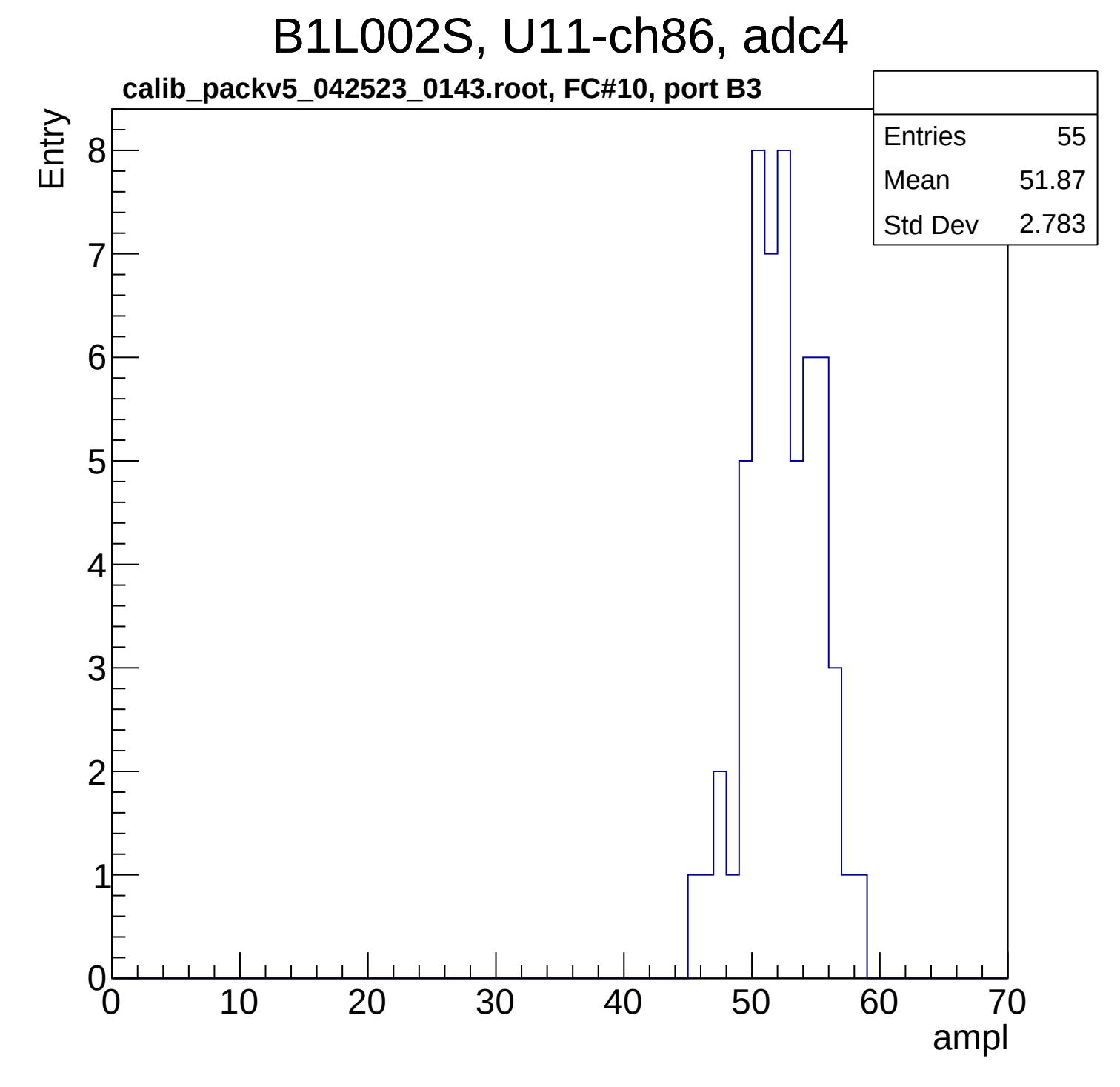
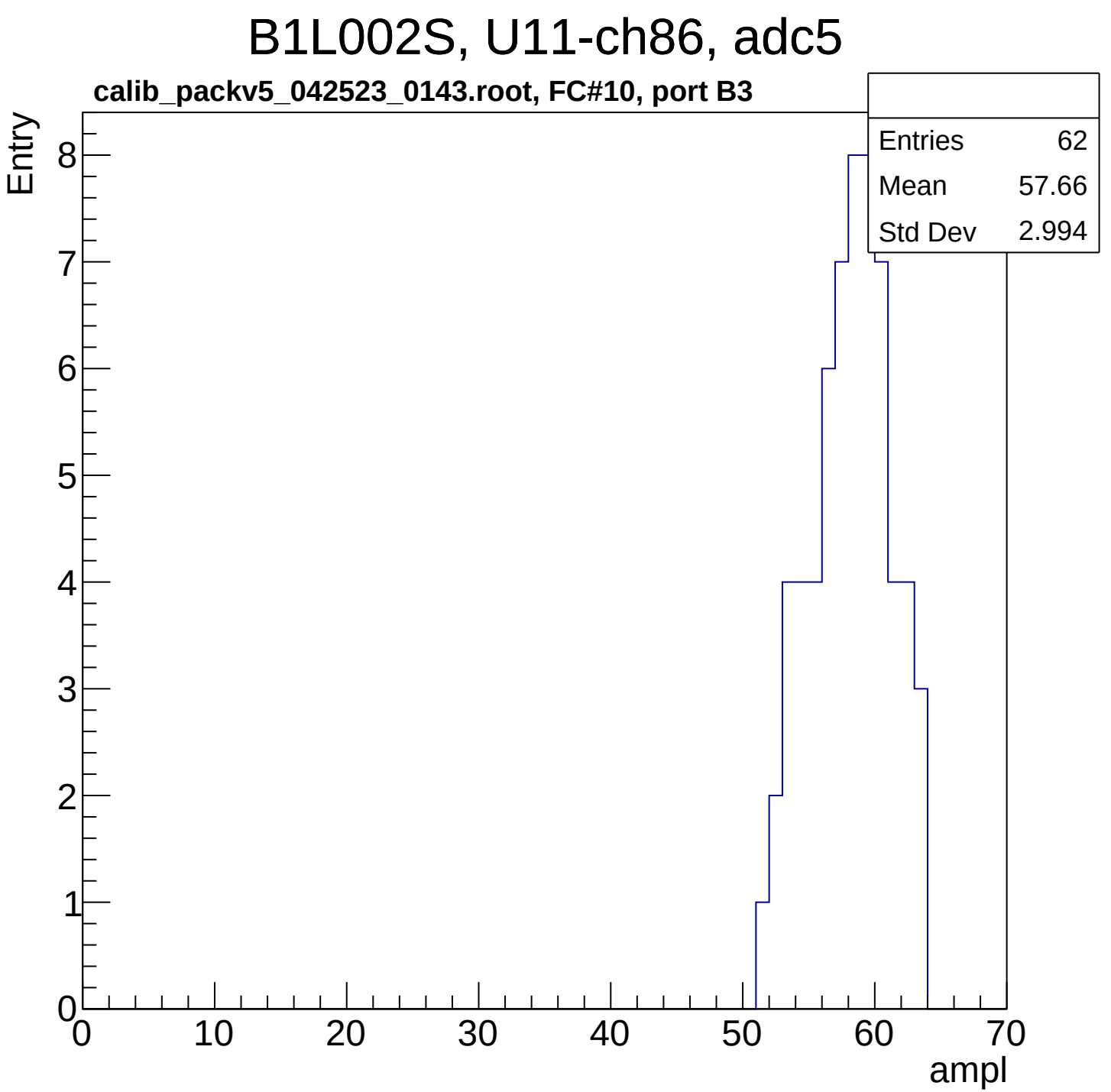
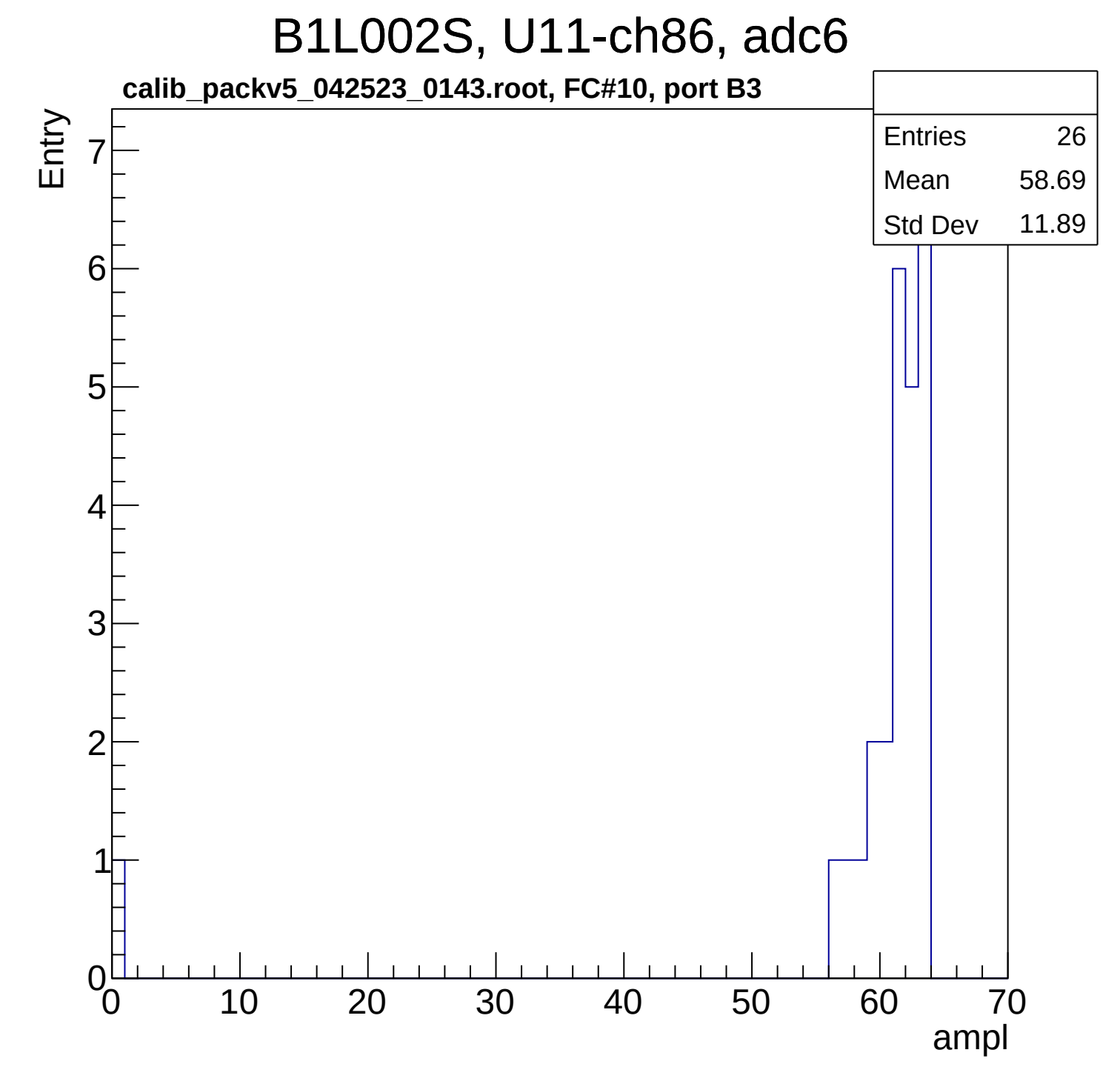
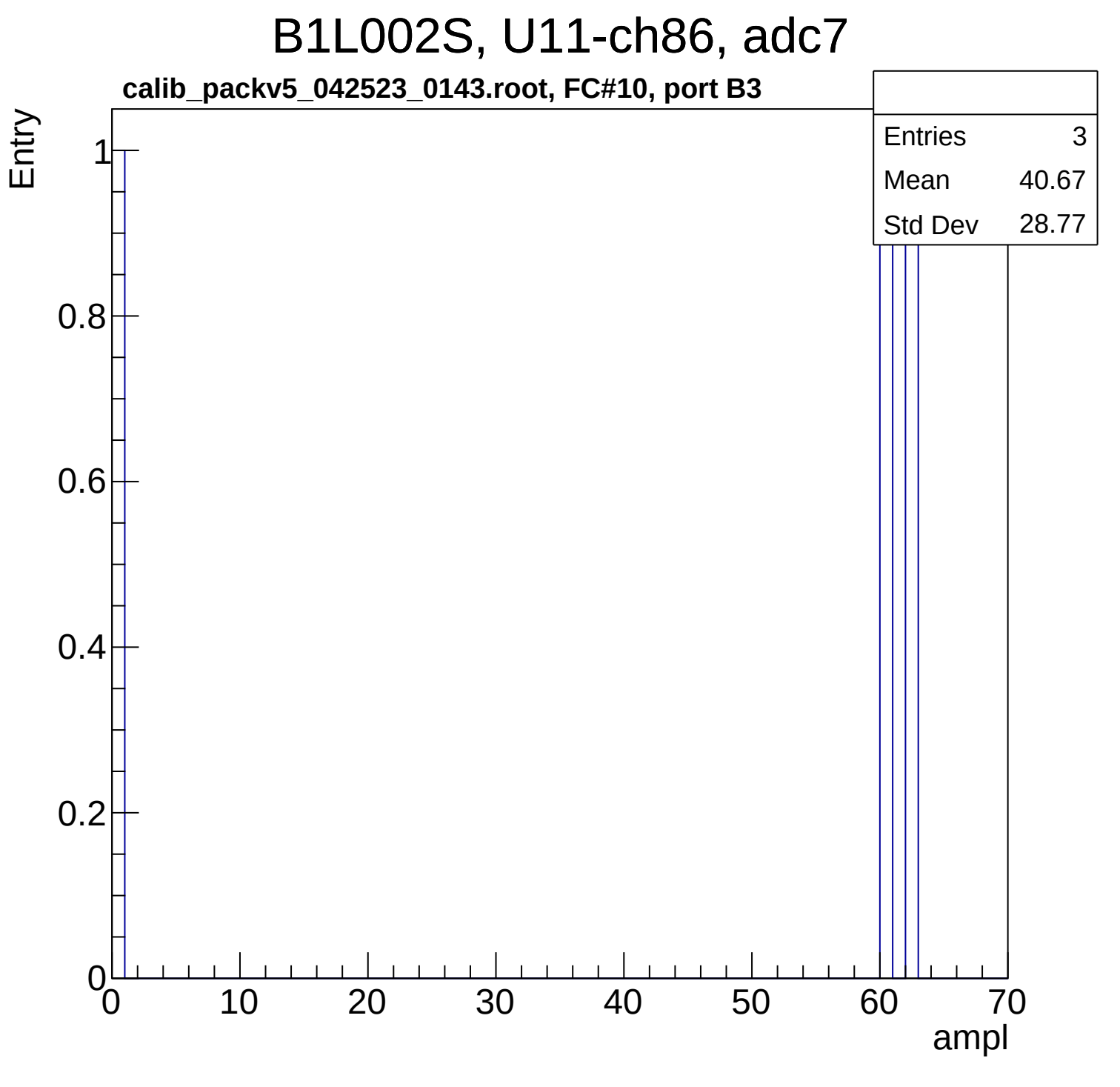

B1L002S, U11-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 39.49
Std Dev 2.628
Gaus mean : 40.2272
Gaus Width: 2.8156
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

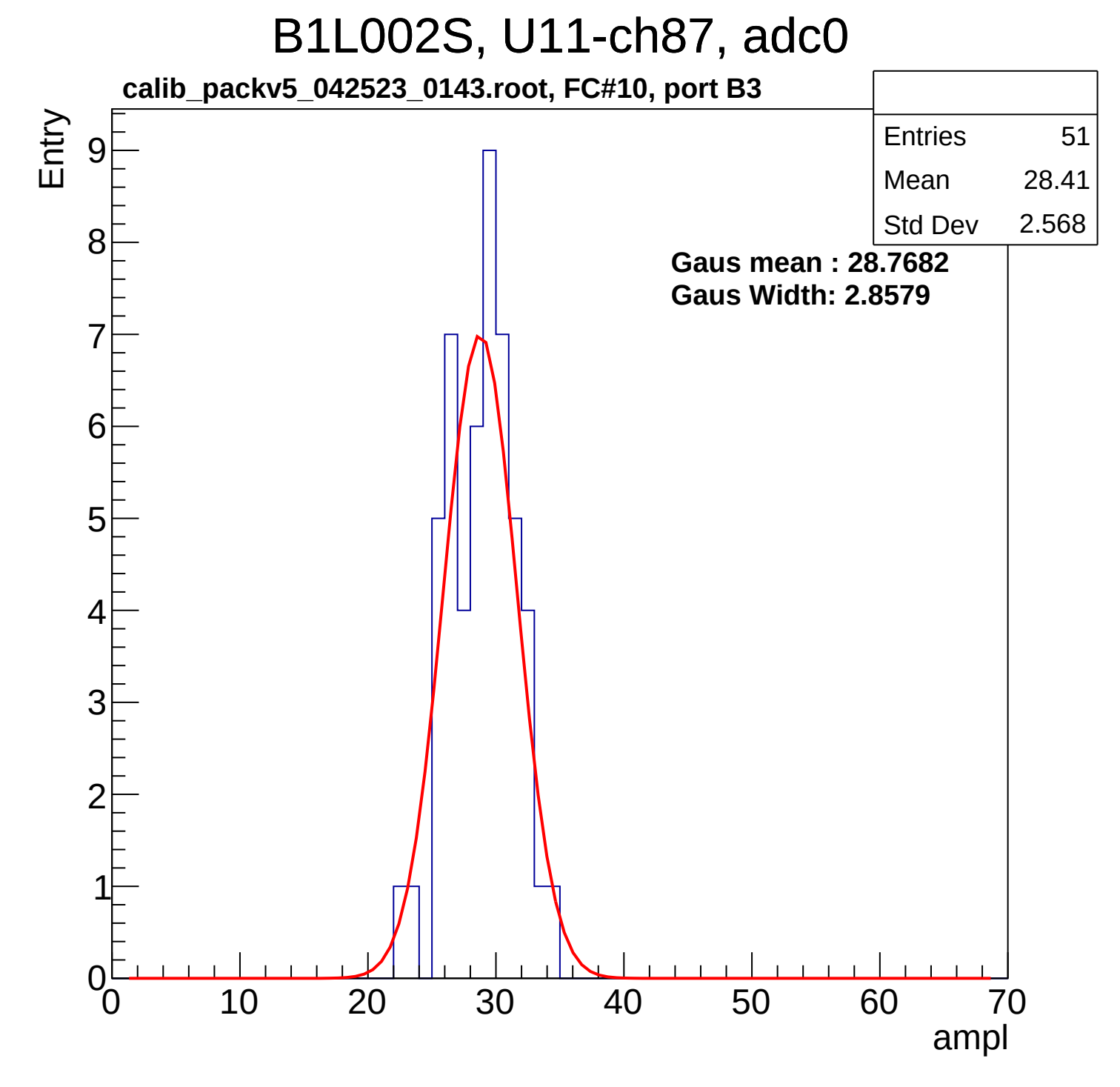

B1L002S, U11-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.88
Std Dev 3.399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

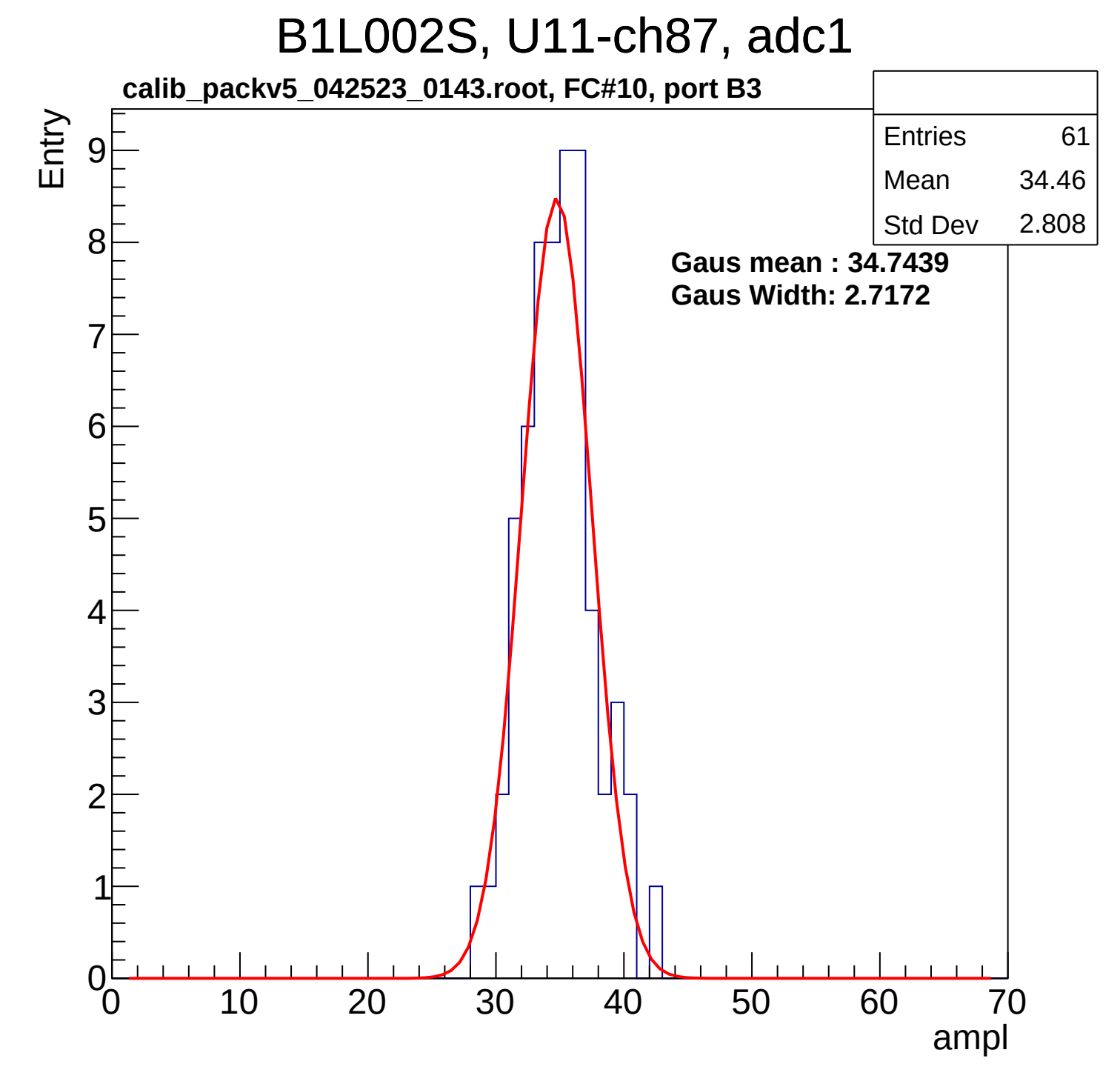

B1L002S, U11-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 51.87
Std Dev 2.783
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

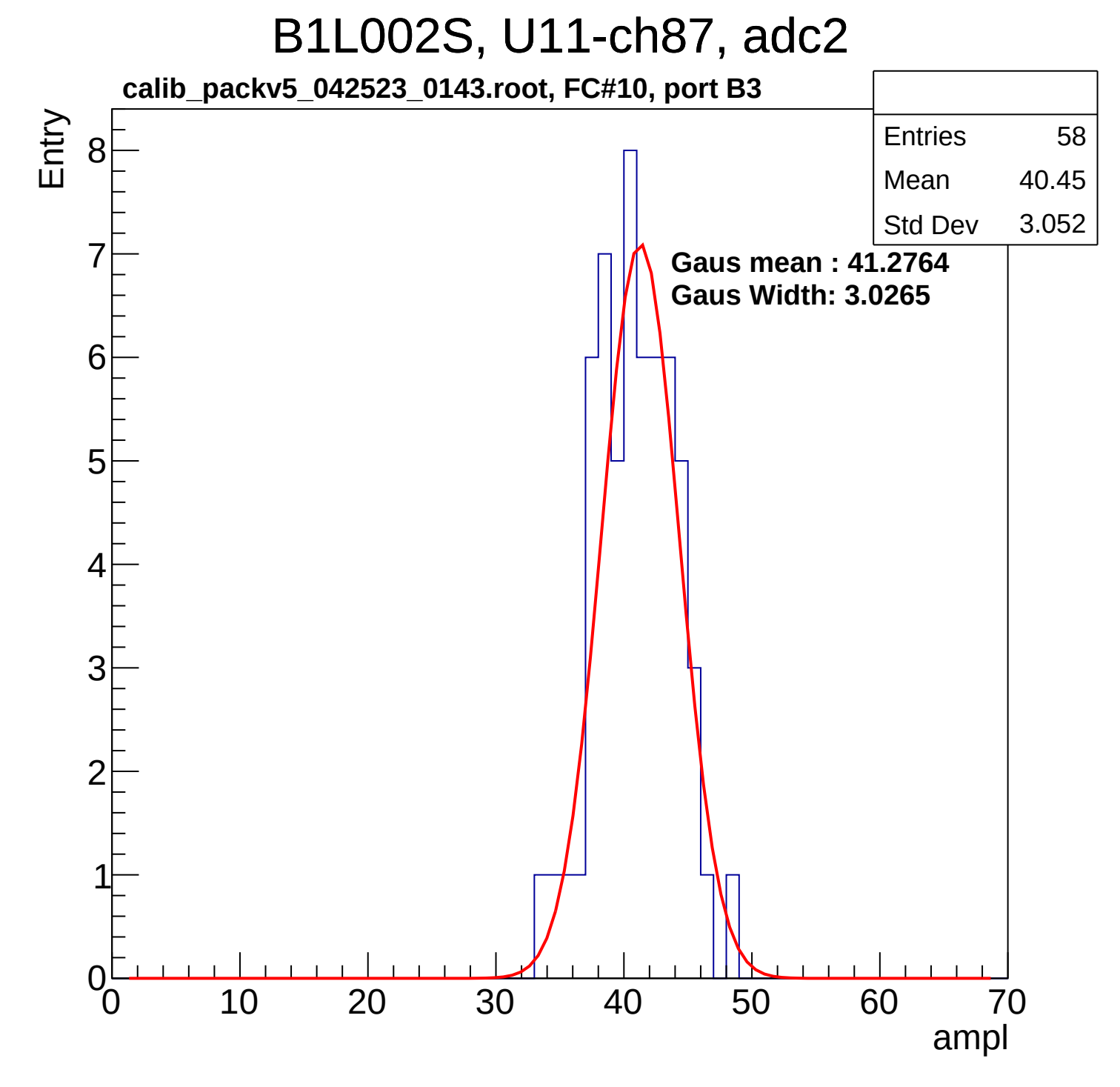

B1L002S, U11-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.66
Std Dev 2.994
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

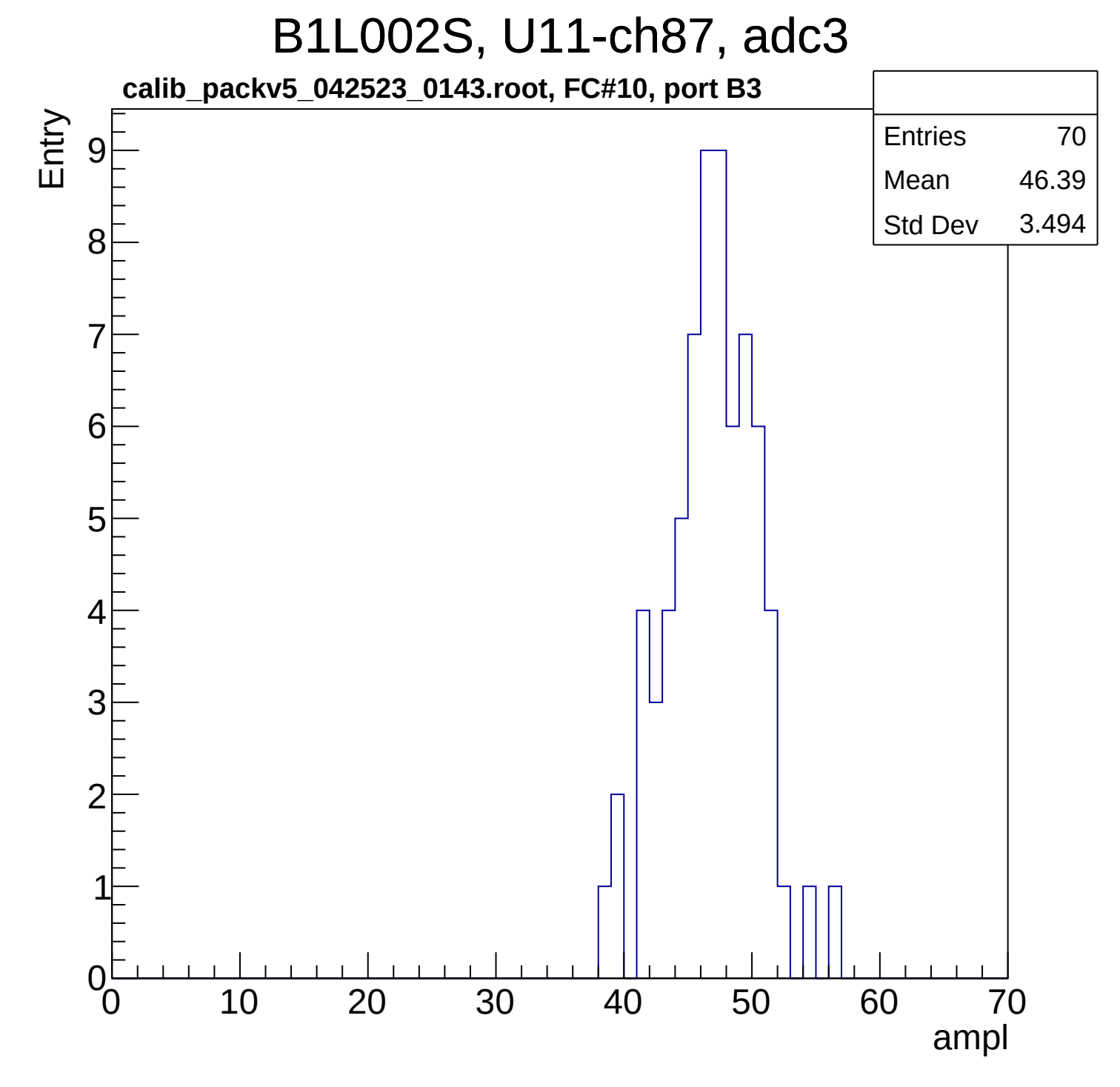

B1L002S, U11-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.69
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

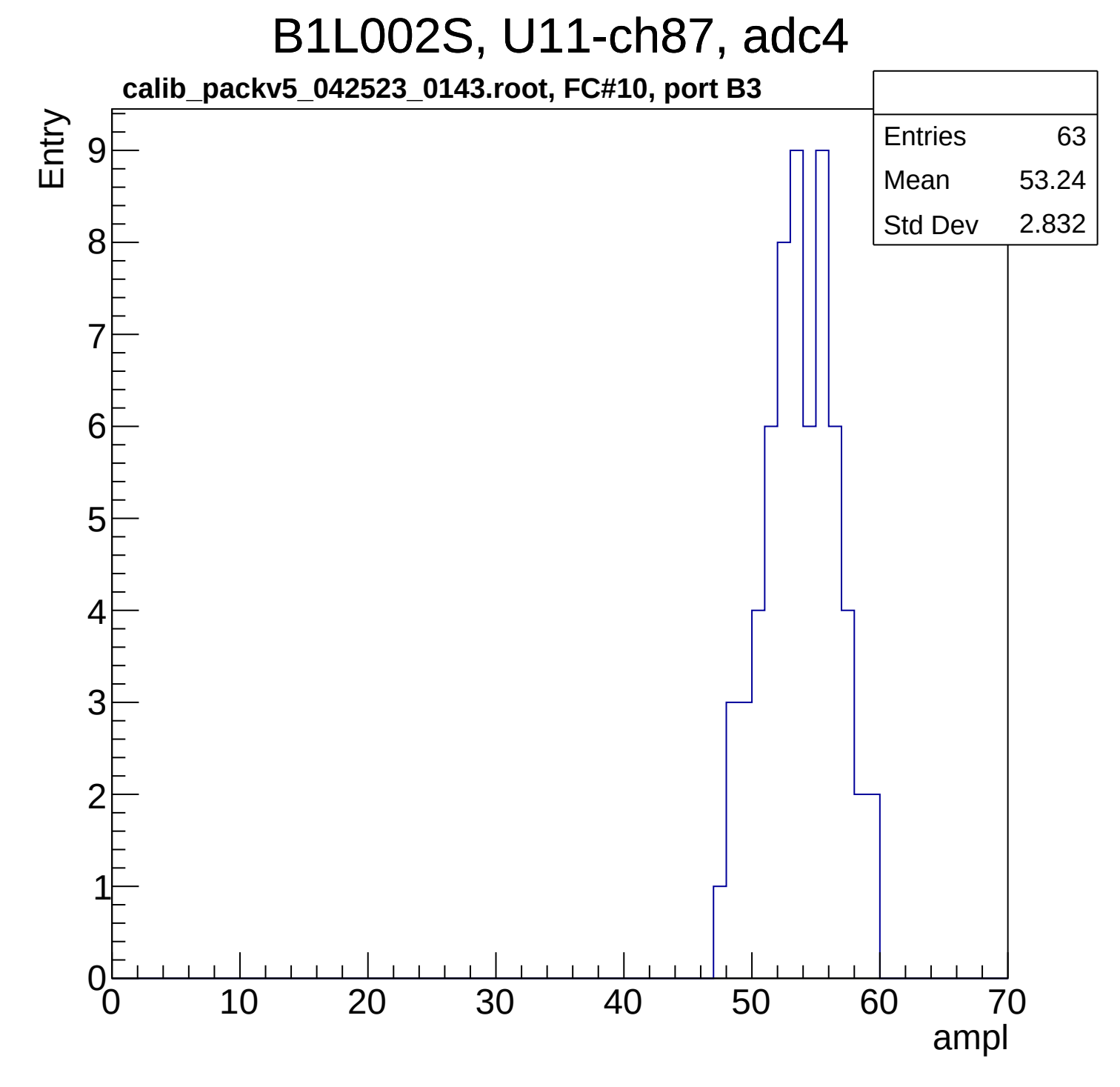

B1L002S, U11-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 40.67
Std Dev 28.77
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U11-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 28.41
Std Dev 2.568
Gaus mean : 28.7682
Gaus Width: 2.8579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.46
Std Dev 2.808
Gaus mean : 34.7439
Gaus Width: 2.7172
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.45
Std Dev 3.052
Gaus mean : 41.2764
Gaus Width: 3.0265
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.39
Std Dev 3.494
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.24
Std Dev 2.832
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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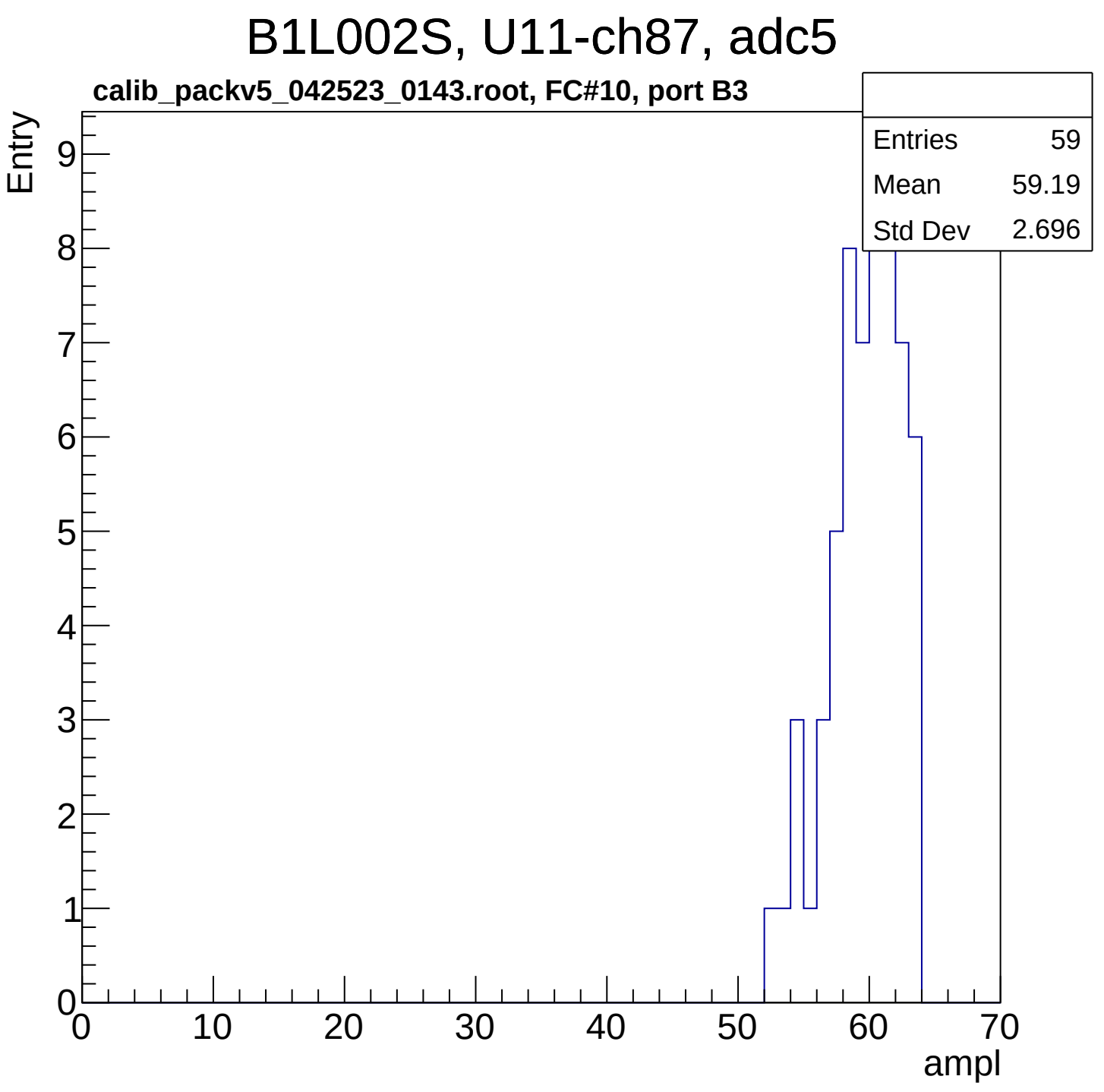
B1L002S, U11-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 59.19
Std Dev 2.696
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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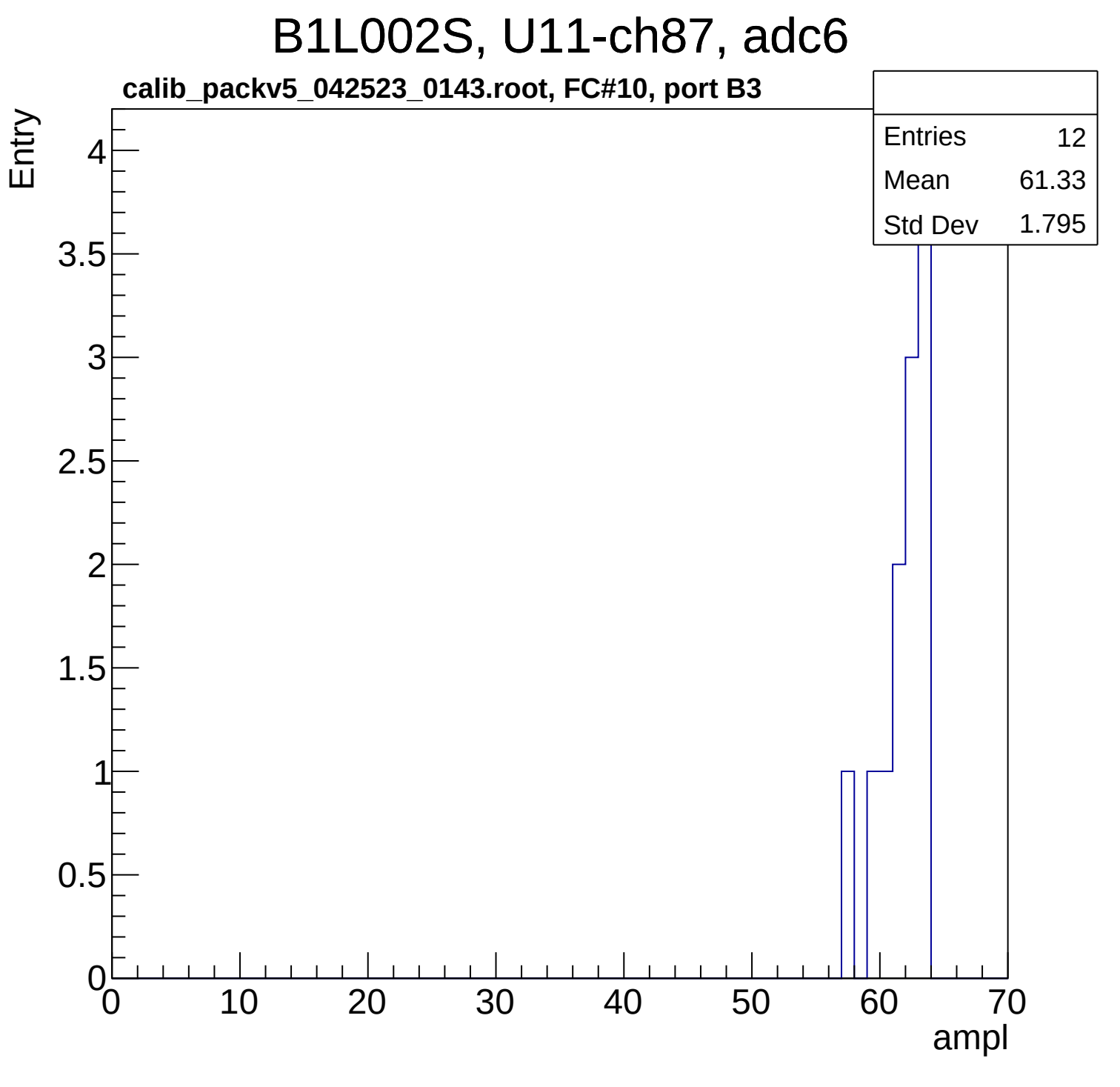
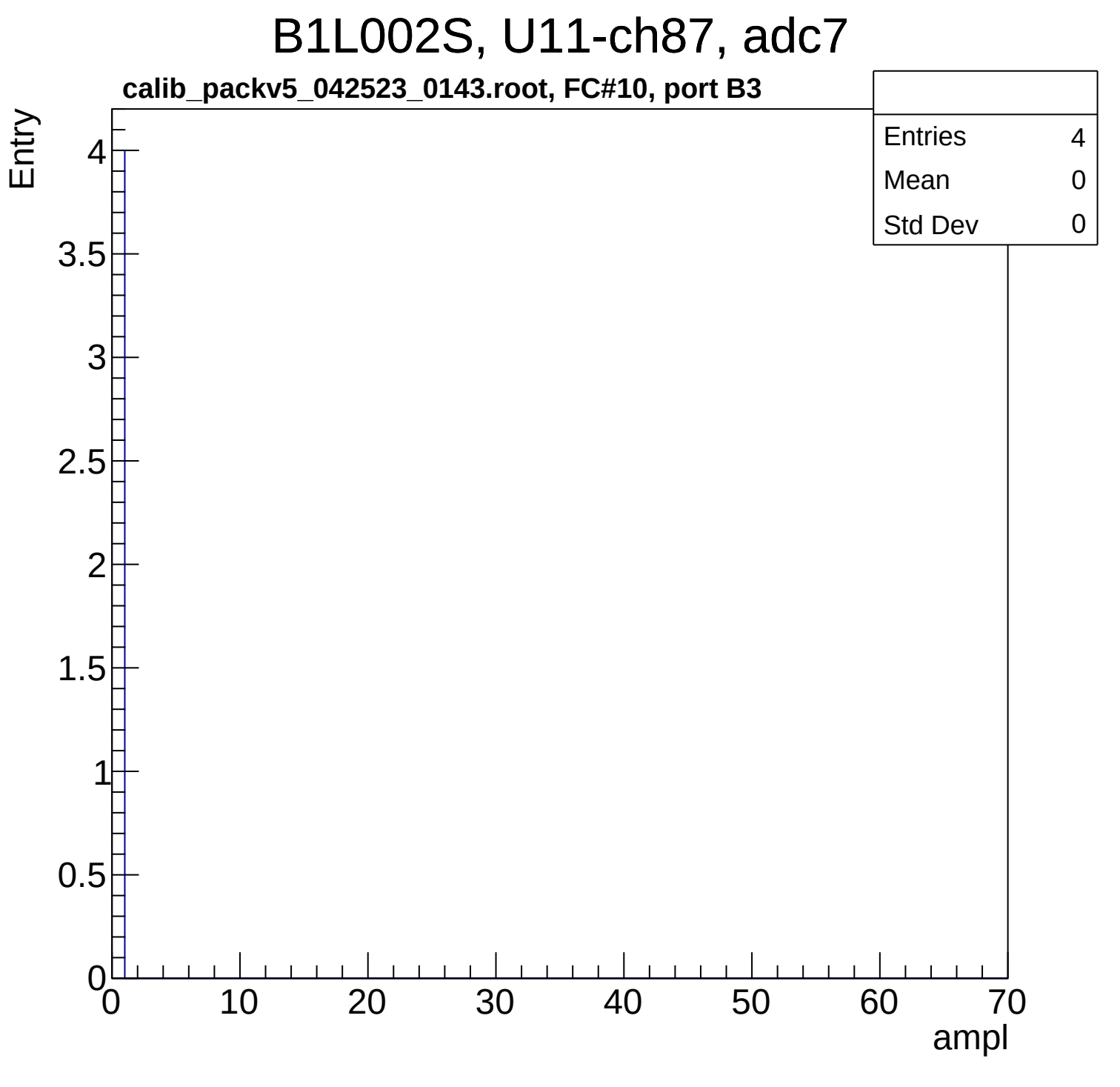
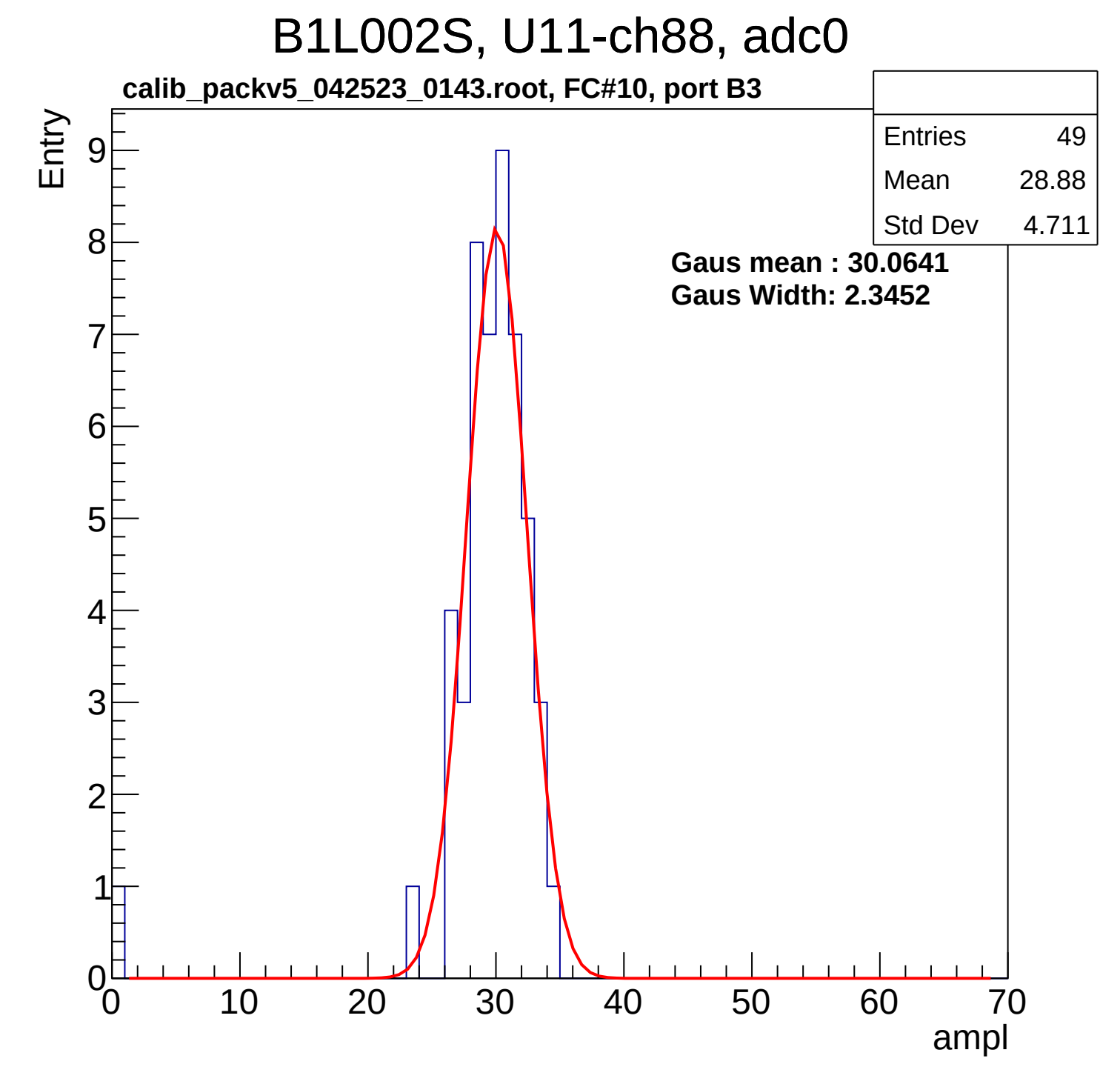
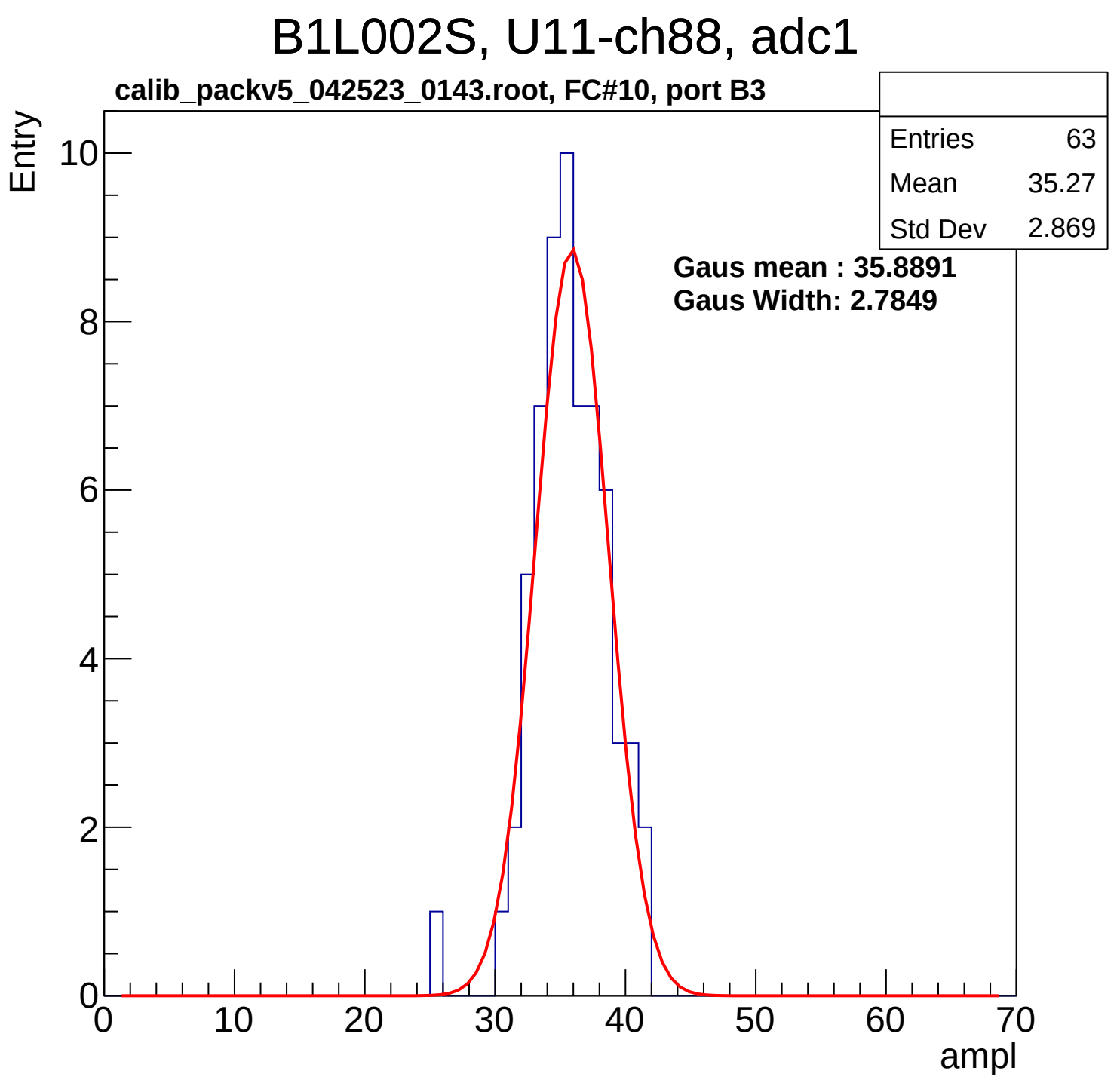
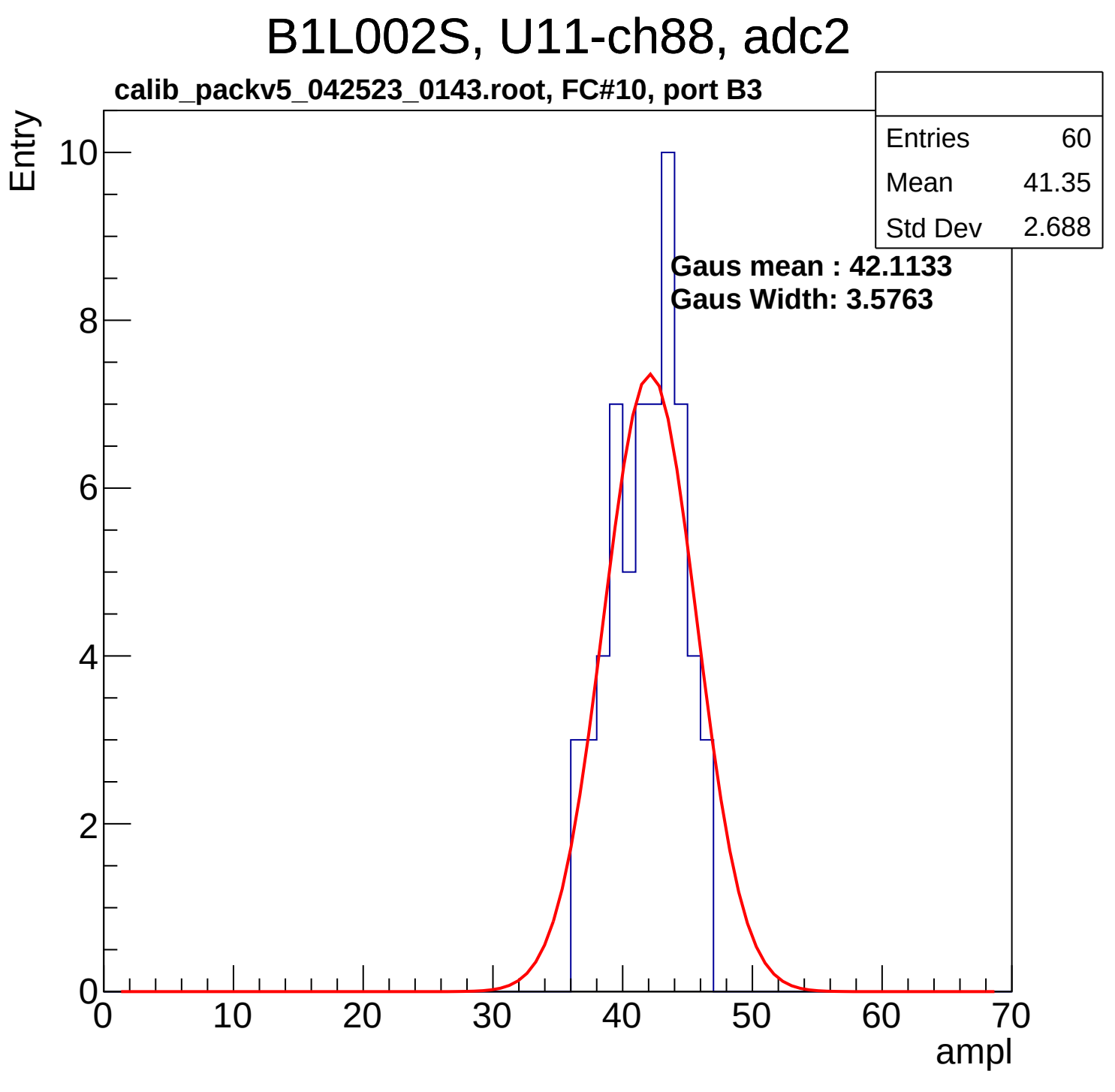
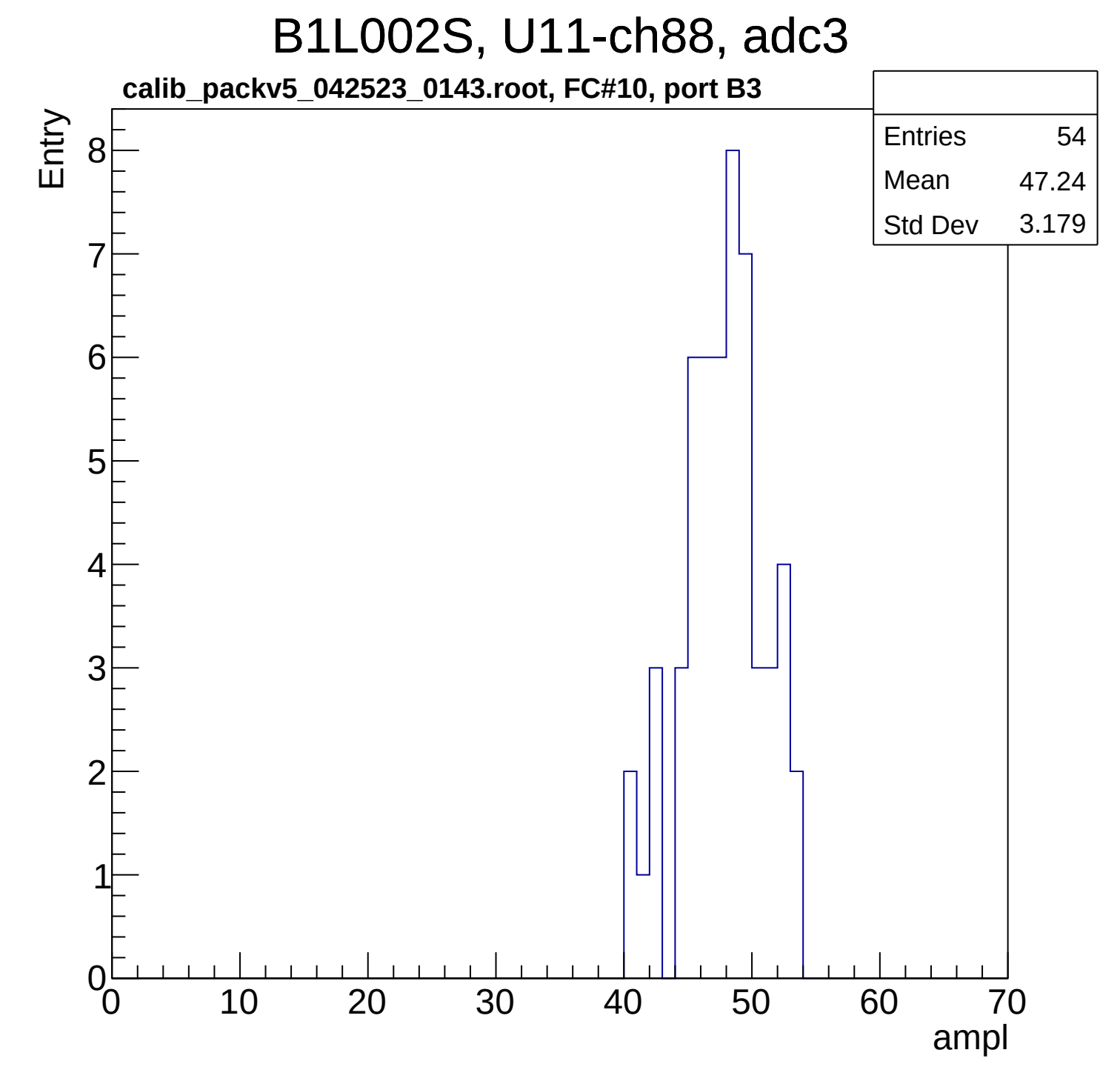

B1L002S, U11-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.33
Std Dev 1.795
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 28.88
Std Dev 4.711
Gaus mean : 30.0641
Gaus Width: 2.3452
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.27
Std Dev 2.869
Gaus mean : 35.8891
Gaus Width: 2.7849
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.35
Std Dev 2.688
Gaus mean : 42.1133
Gaus Width: 3.5763
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.24
Std Dev 3.179
Entry
ampl
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2
3
4
5
6
7
8
0
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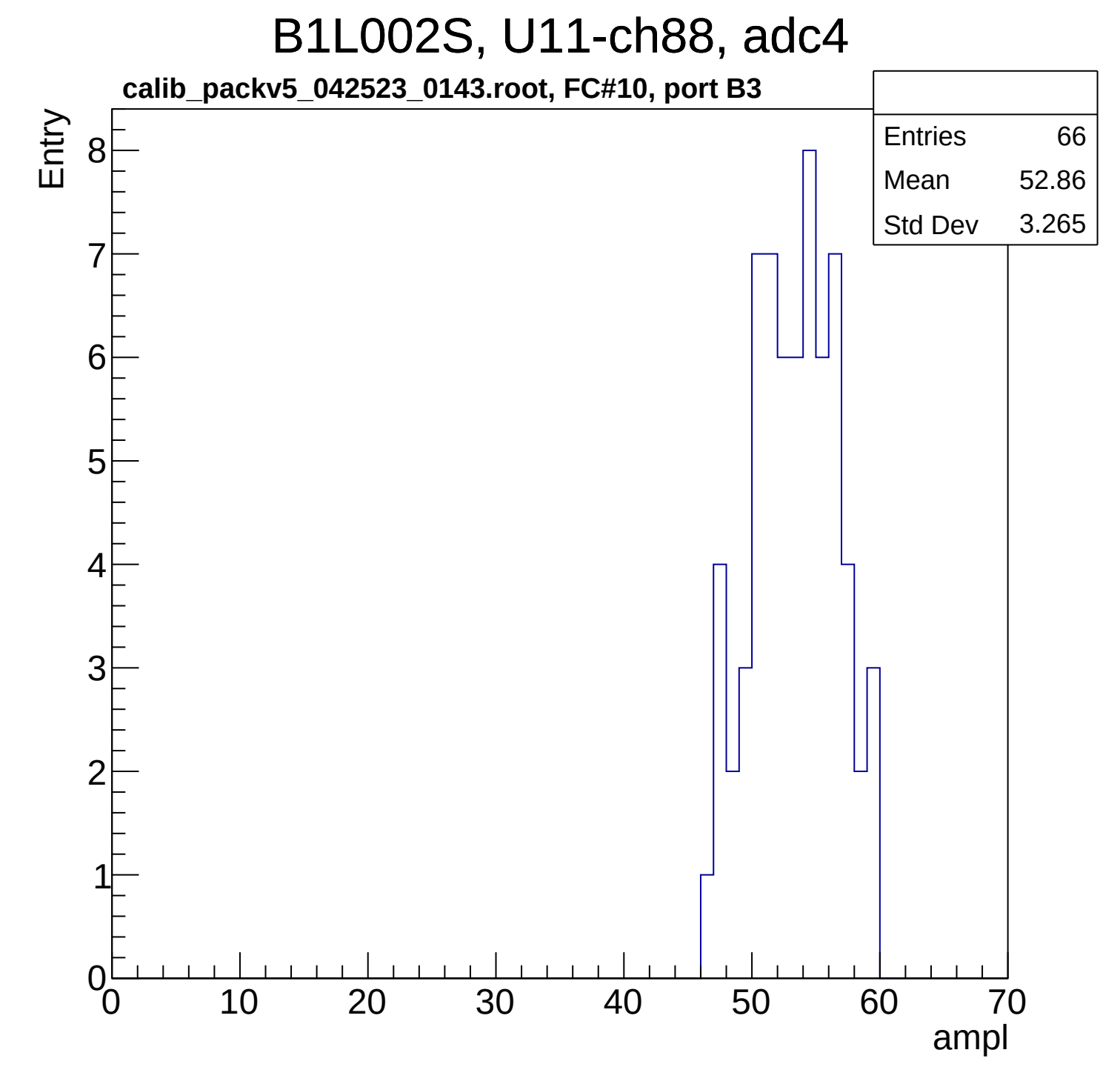
50
60
70


B1L002S, U11-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 52.86
Std Dev 3.265
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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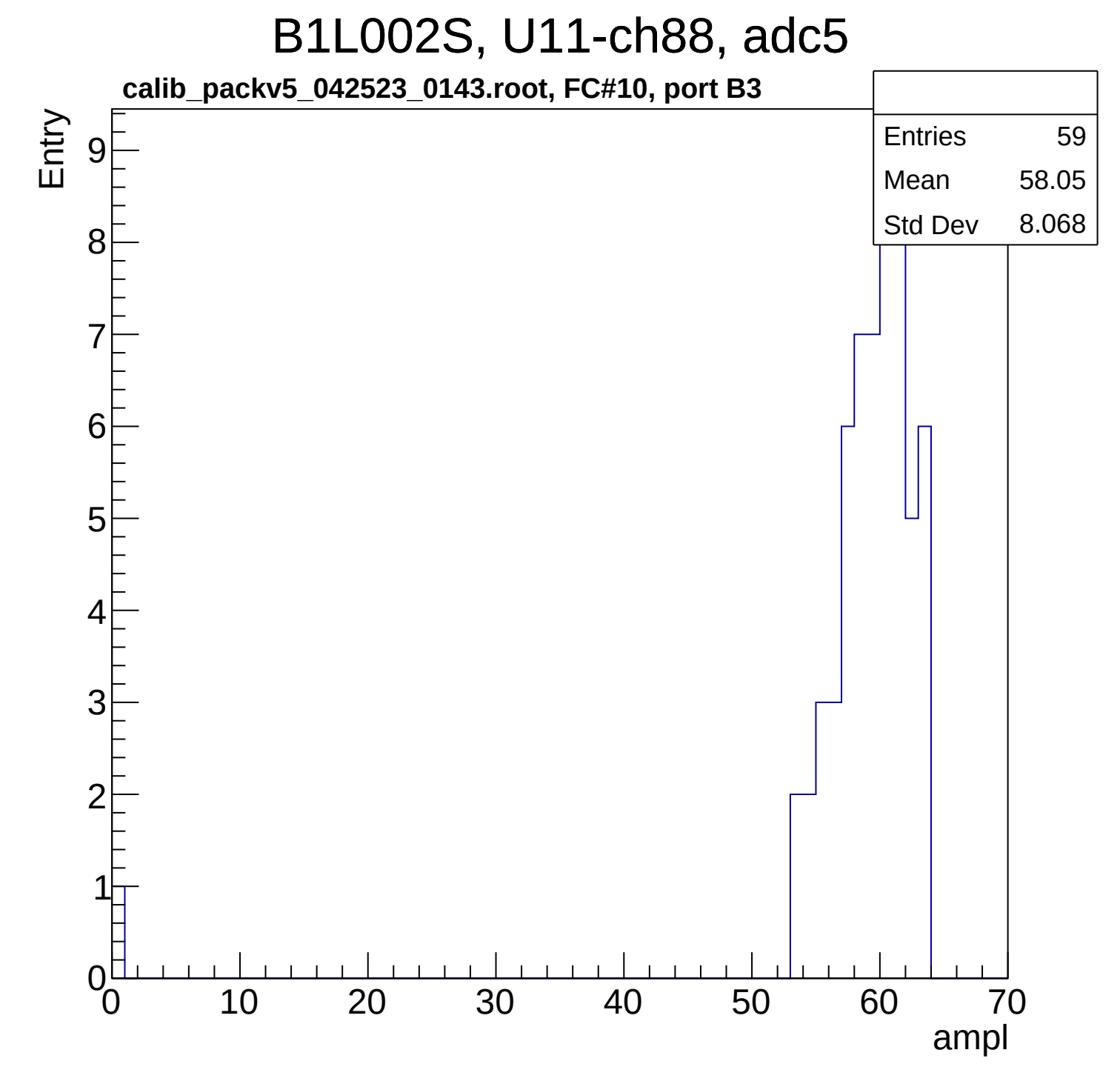
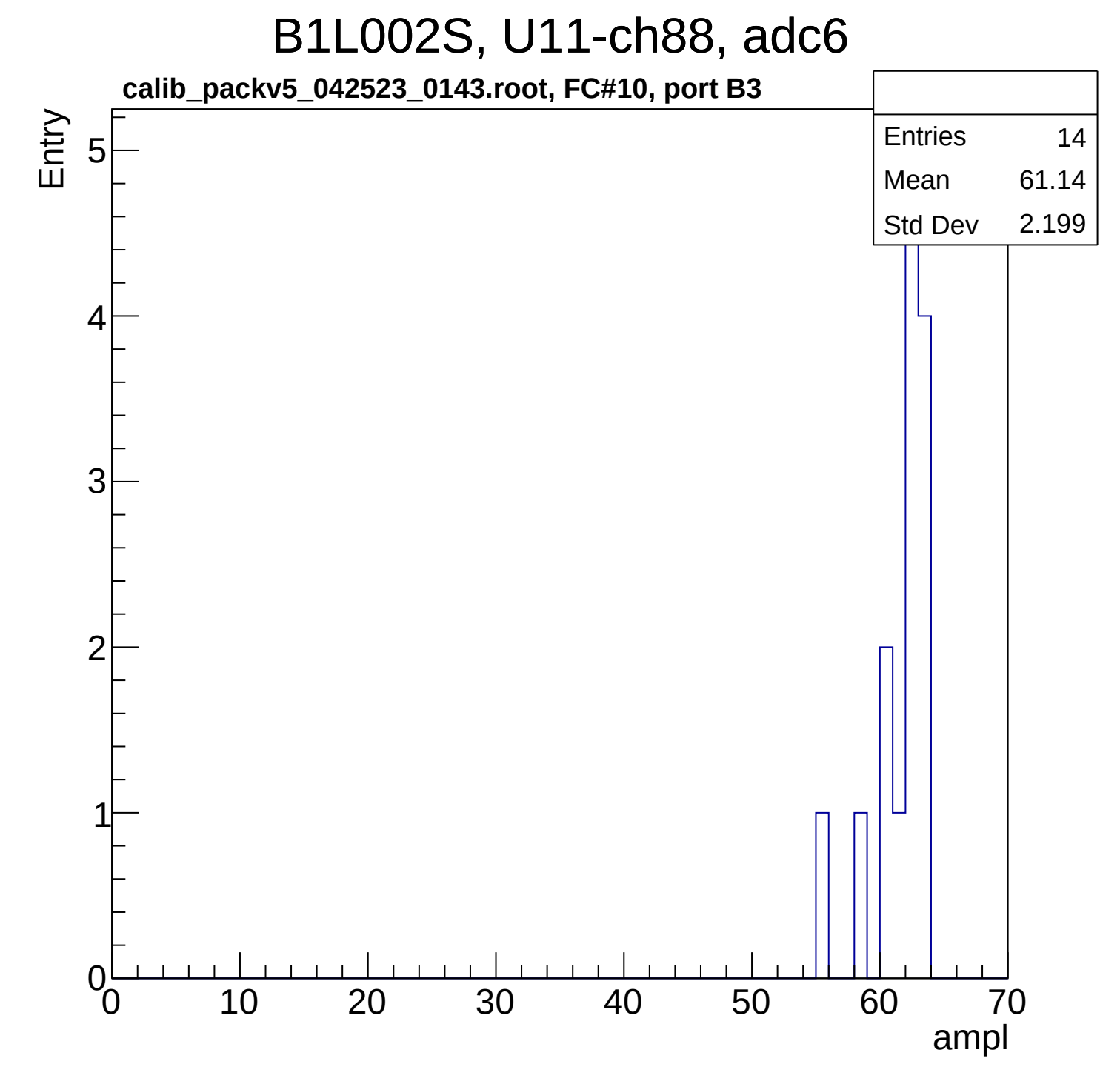
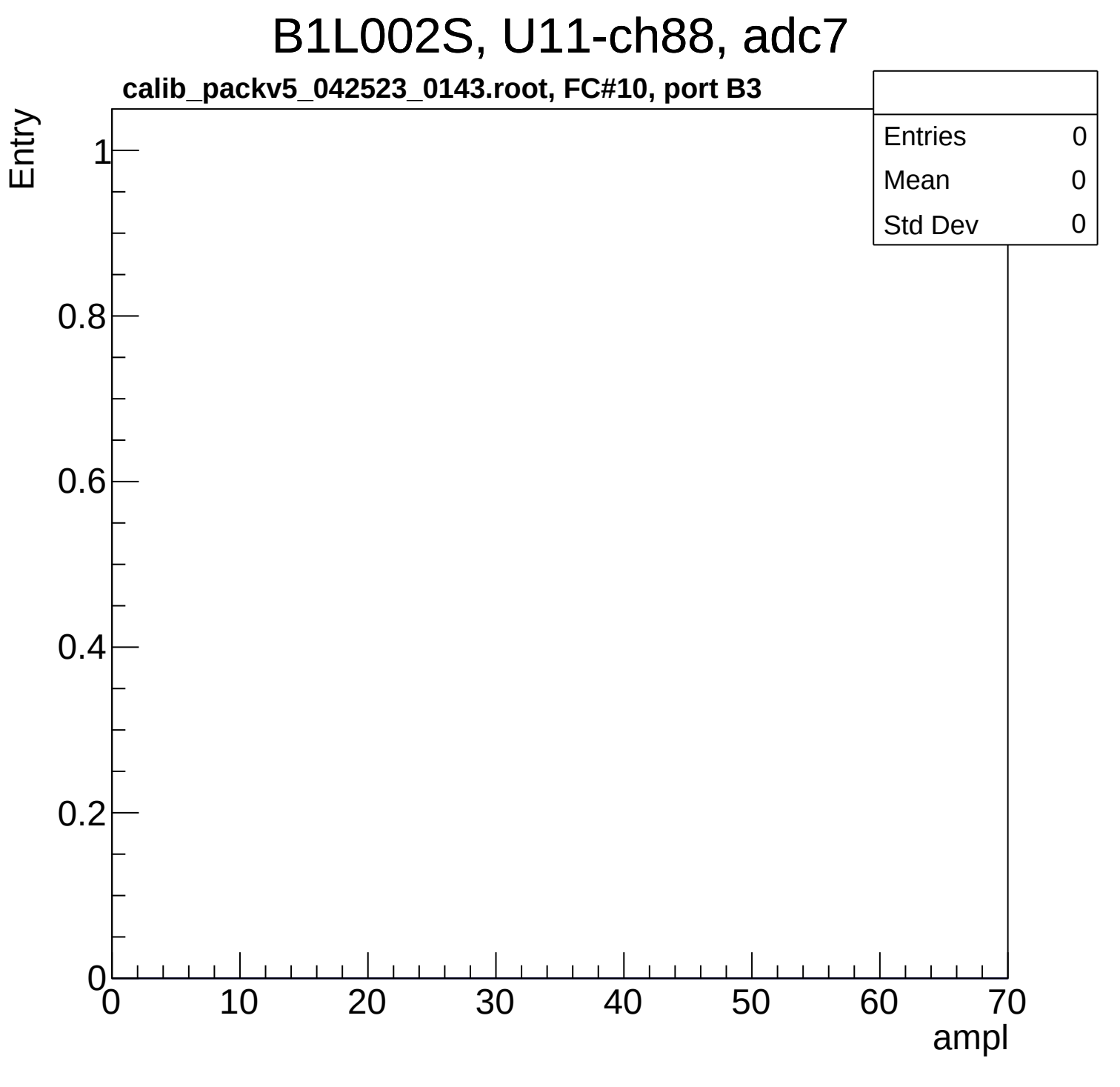

B1L002S, U11-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.05
Std Dev 8.068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.14
Std Dev 2.199
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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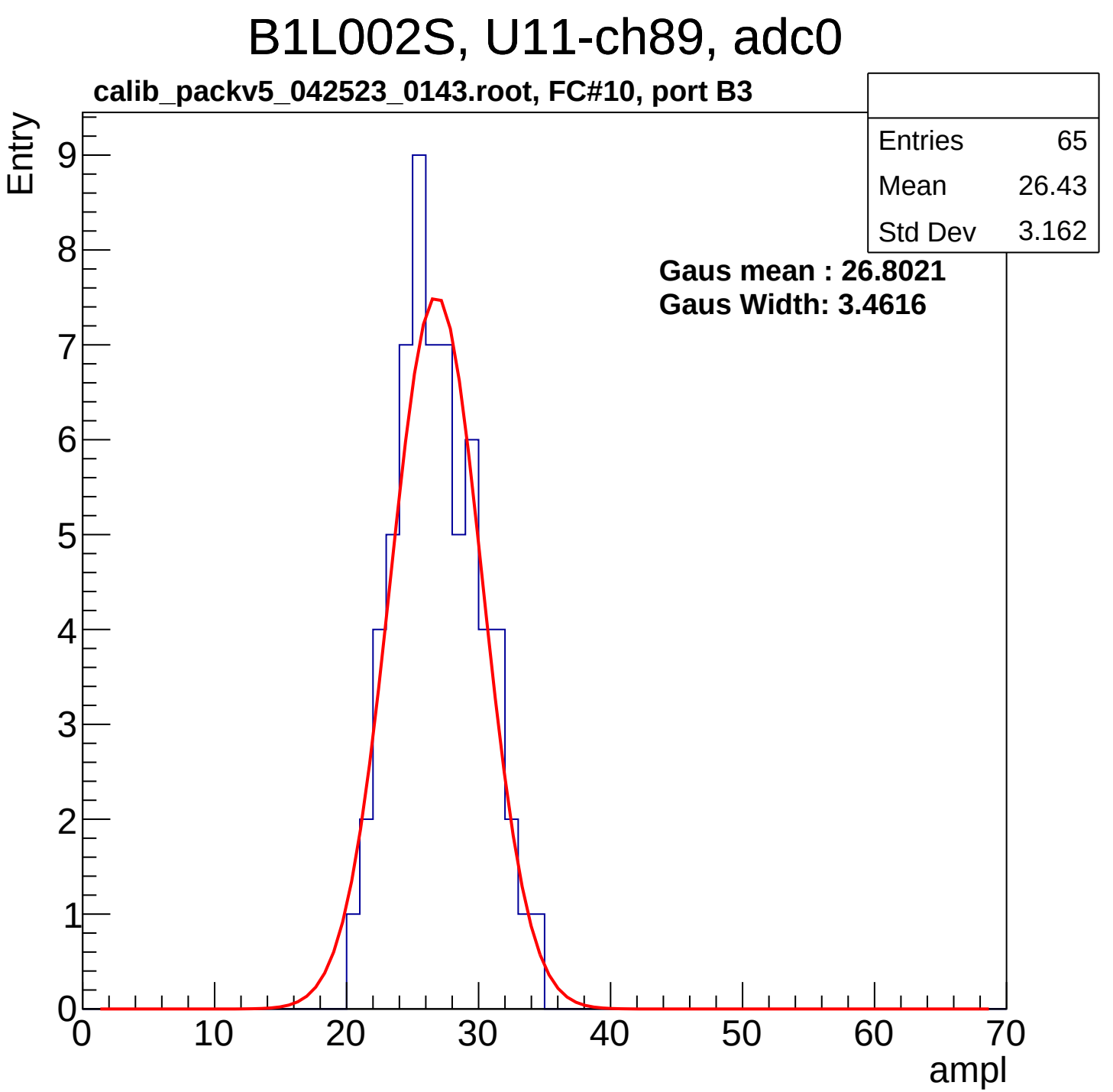
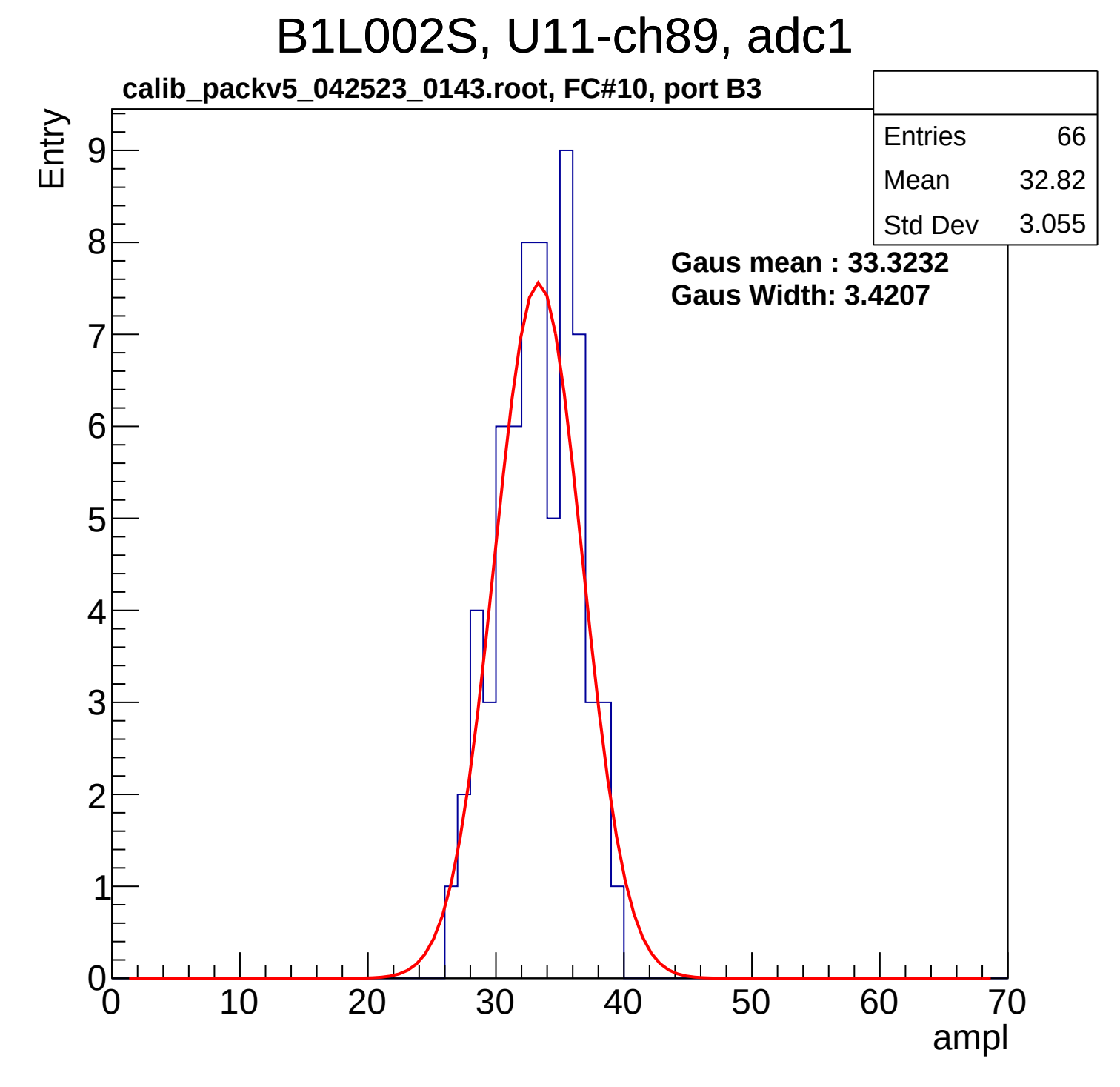
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 26.43
Std Dev 3.162
Gaus mean : 26.8021
Gaus Width: 3.4616
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 32.82
Std Dev 3.055
Gaus mean : 33.3232
Gaus Width: 3.4207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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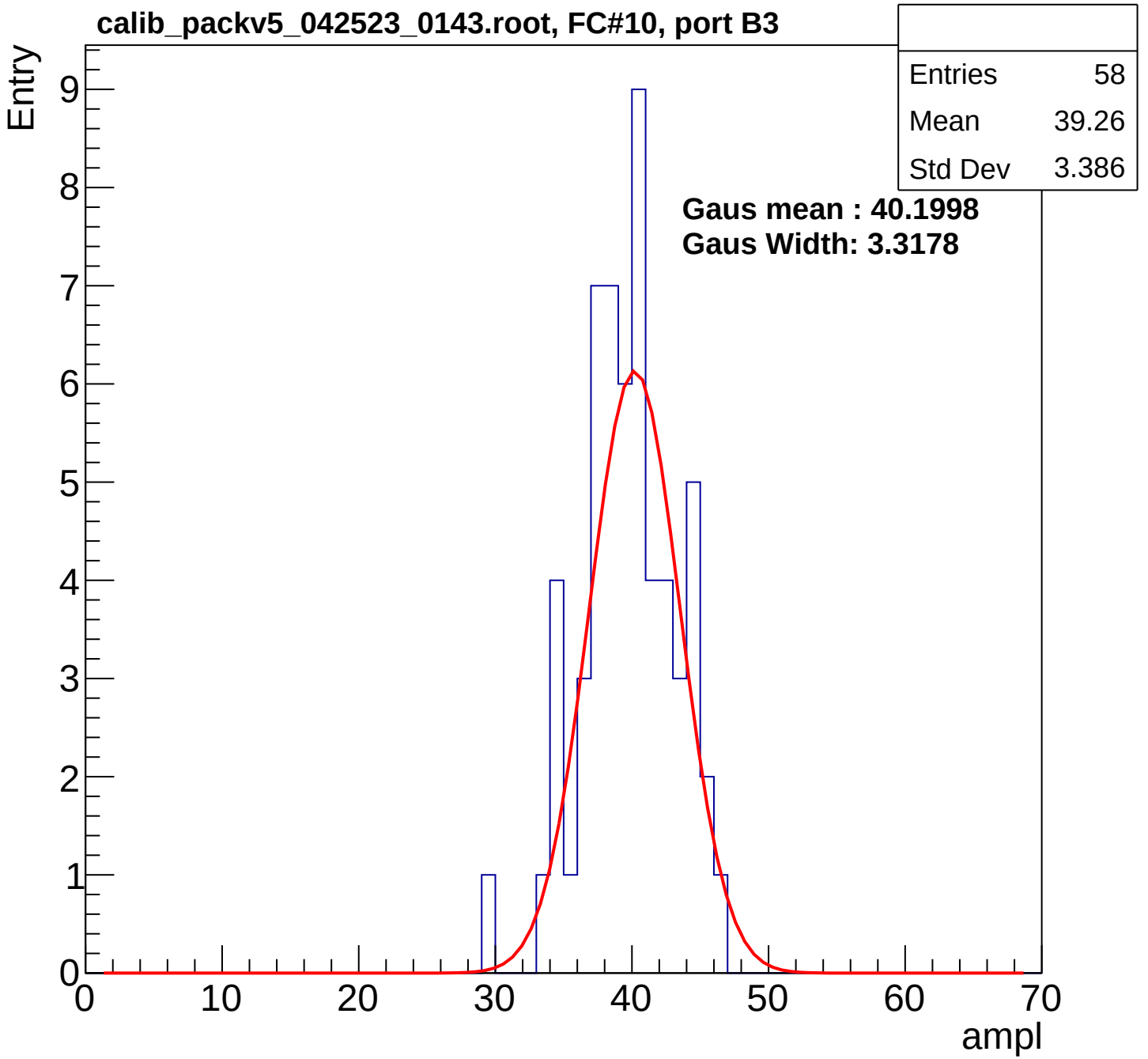
B1L002S, U11-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 39.26
Std Dev 3.386
Gaus mean : 40.1998
Gaus Width: 3.3178
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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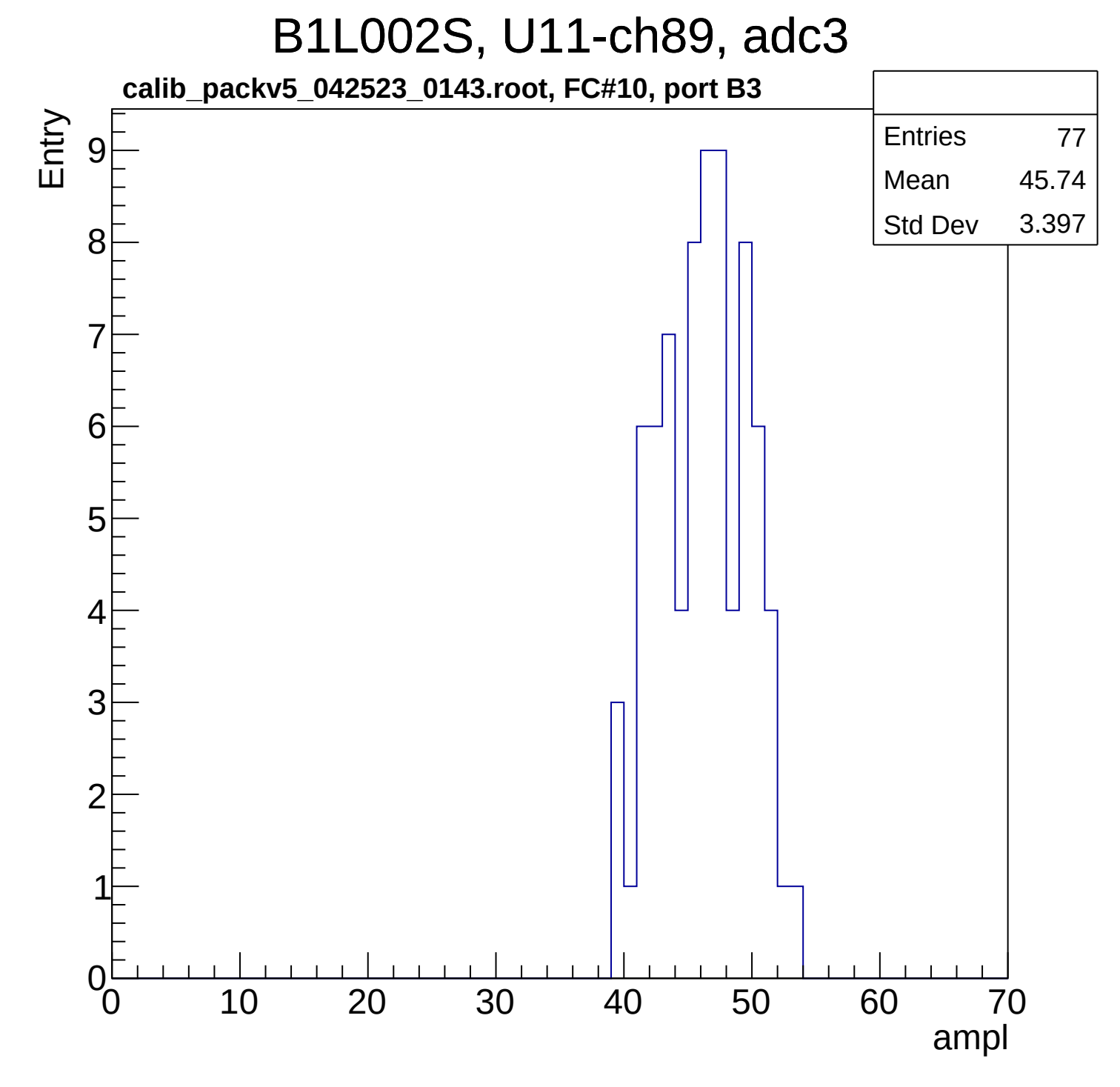
B1L002S, U11-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 45.74
Std Dev 3.397
Entry
ampl
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3
4
5
6
7
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0
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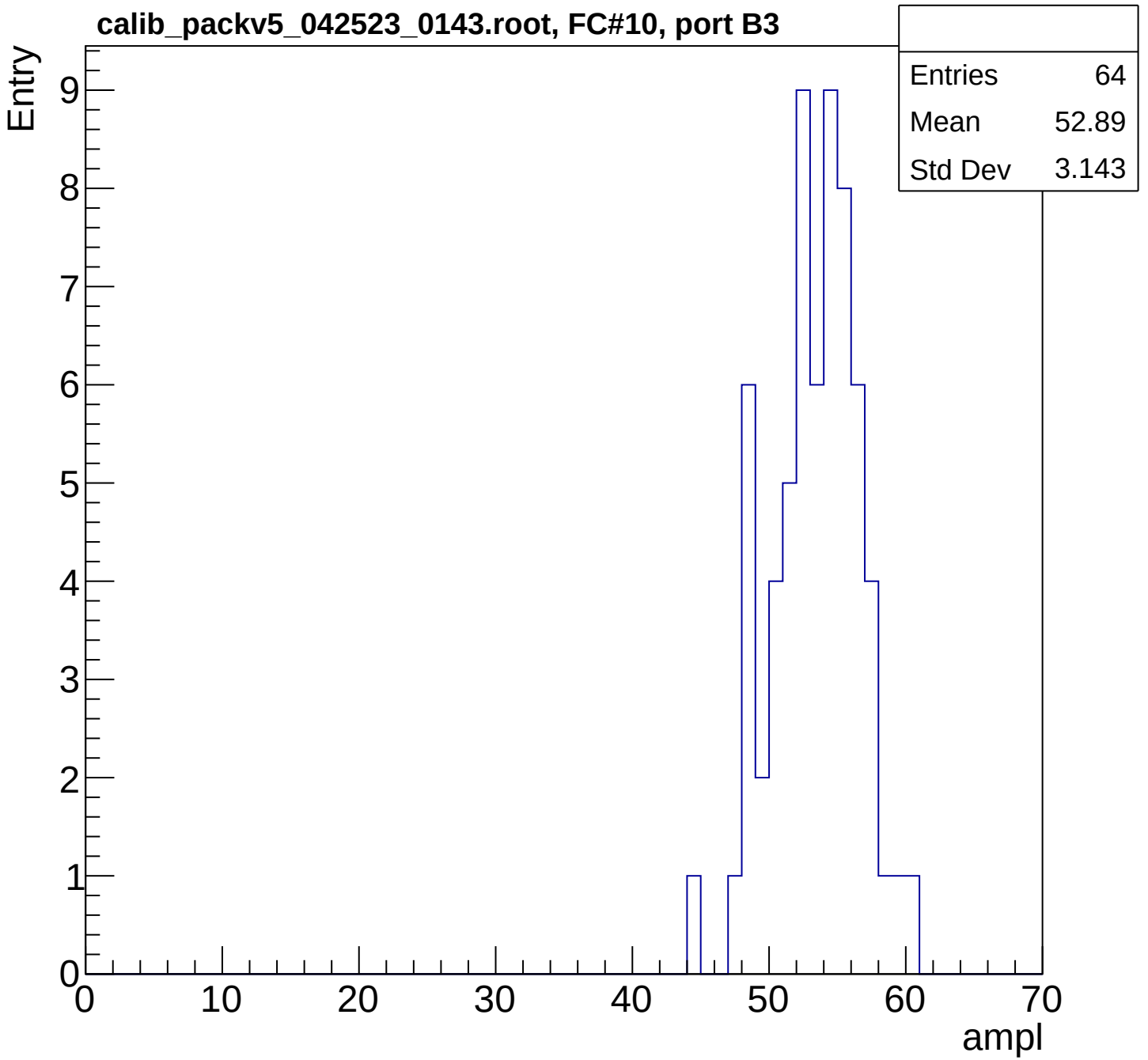
B1L002S, U11-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.89
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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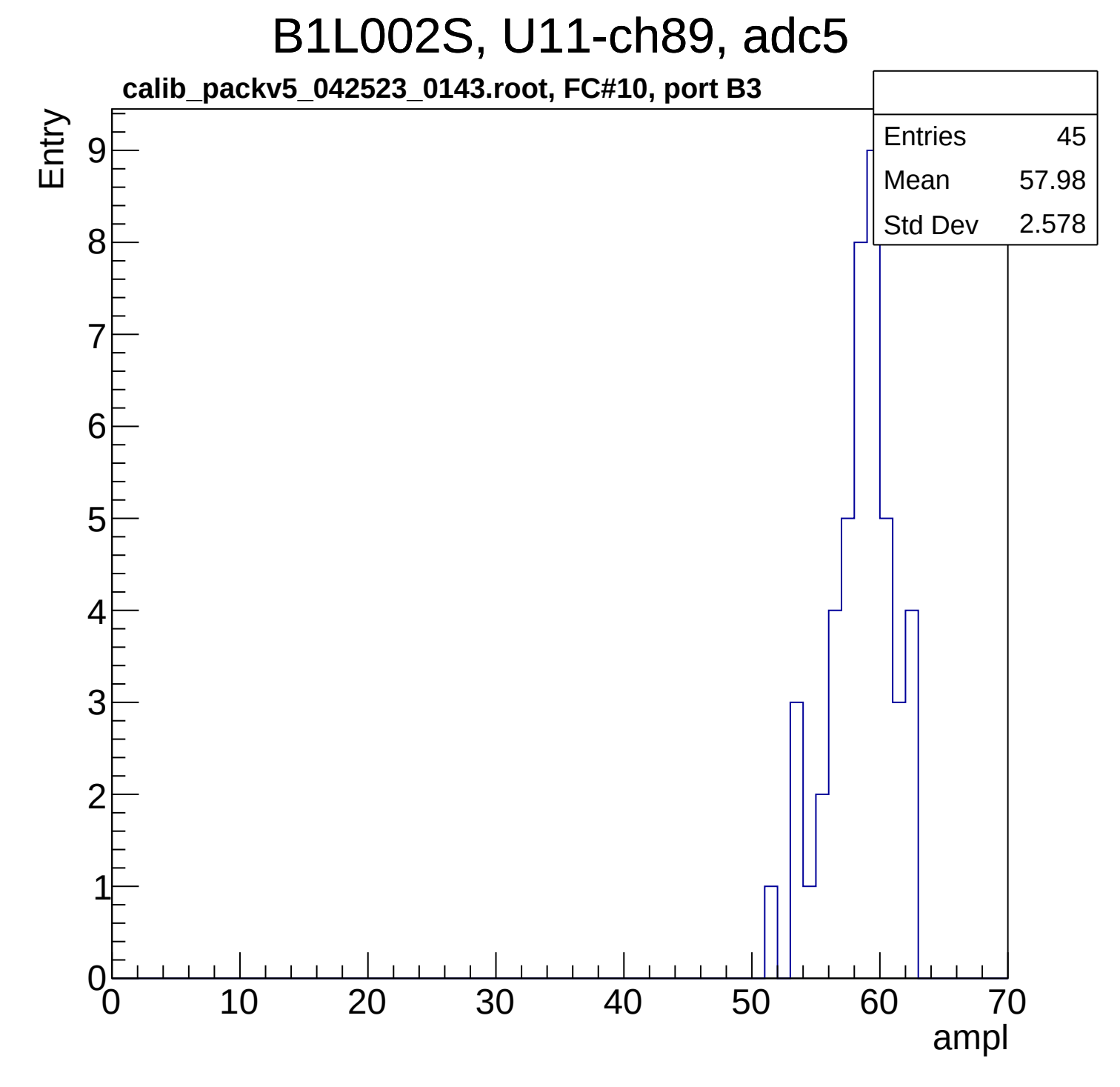
B1L002S, U11-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 57.98
Std Dev 2.578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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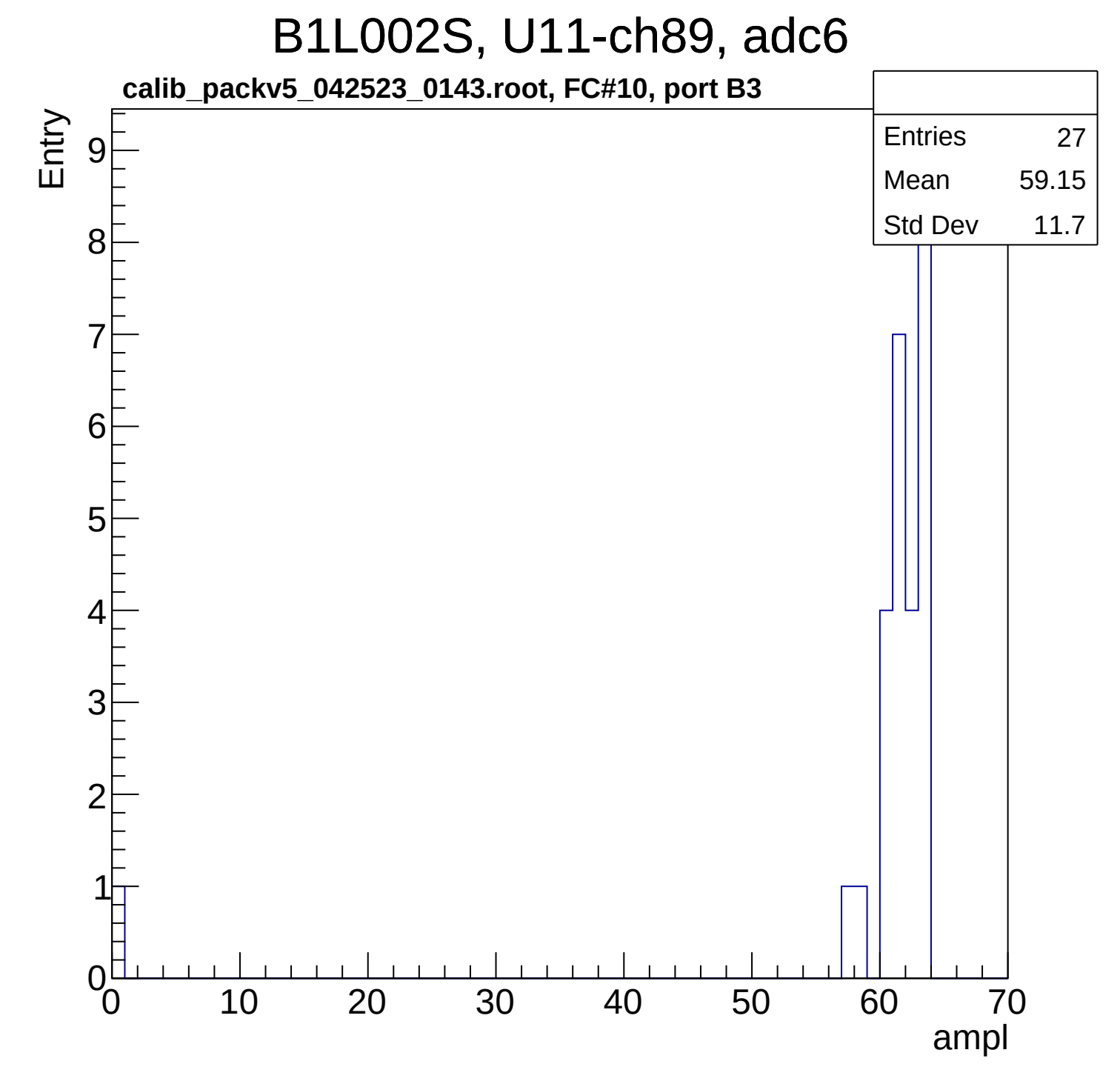
B1L002S, U11-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59.15
Std Dev 11.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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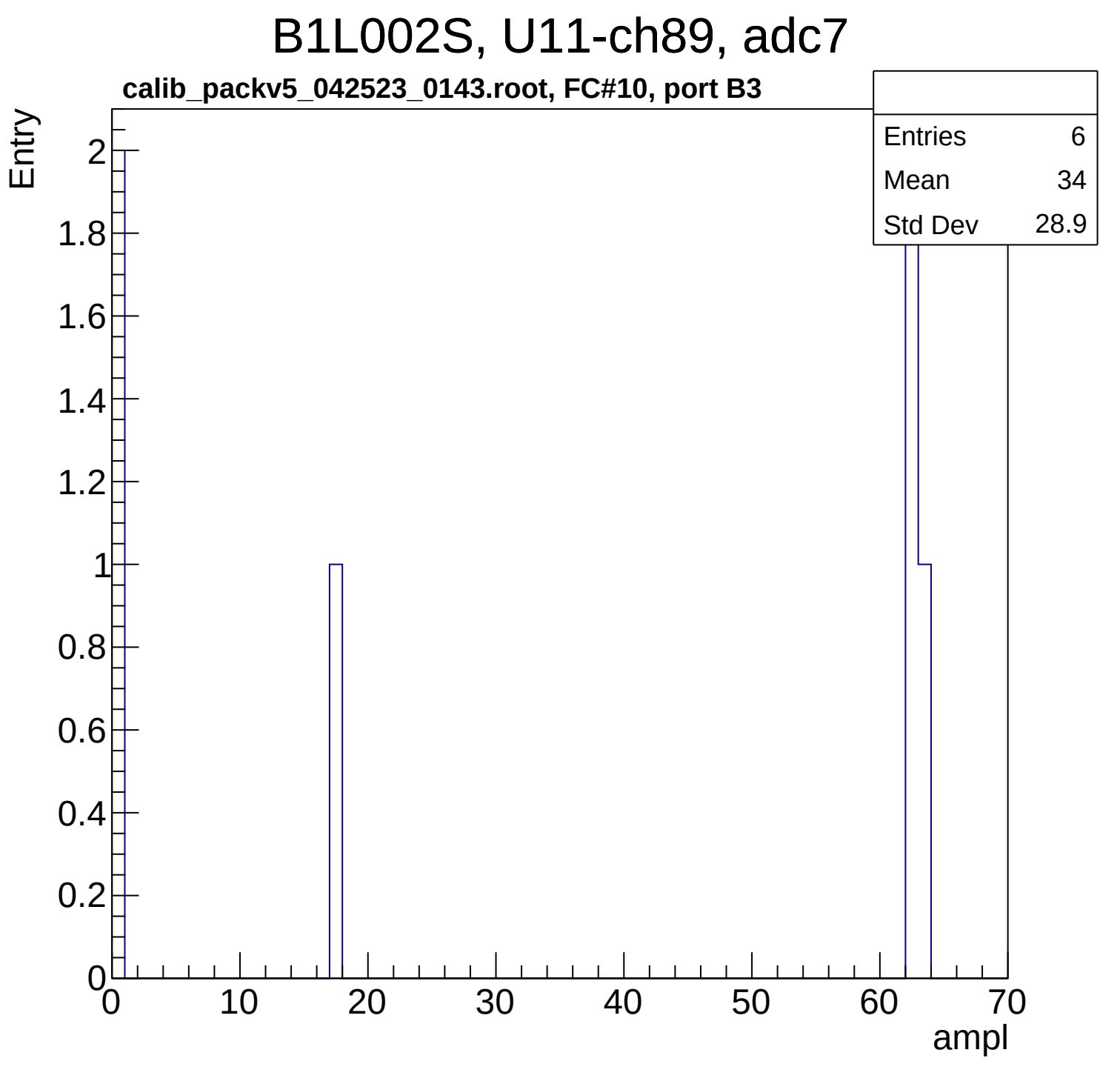
B1L002S, U11-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 34
Std Dev 28.9
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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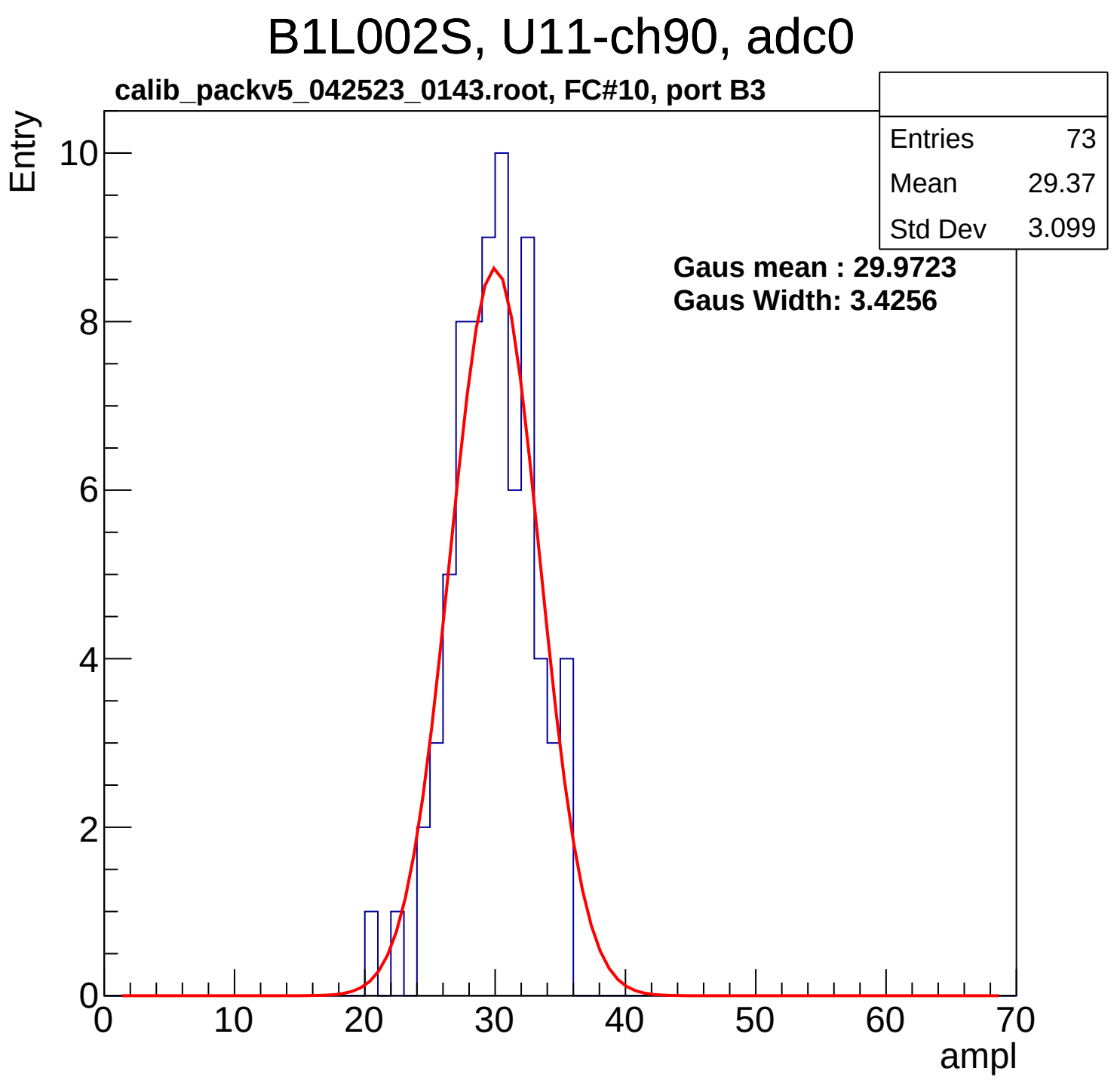
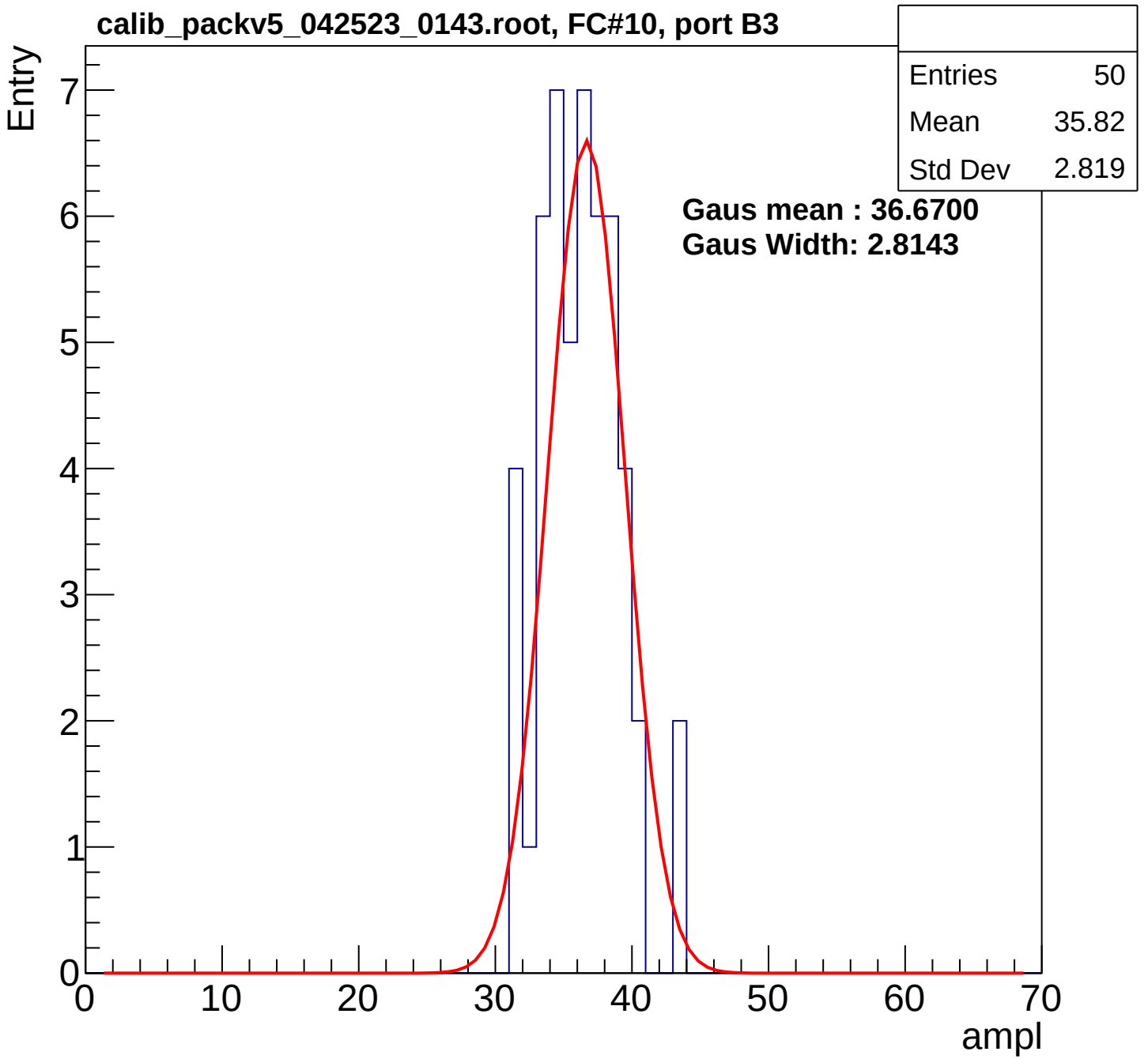
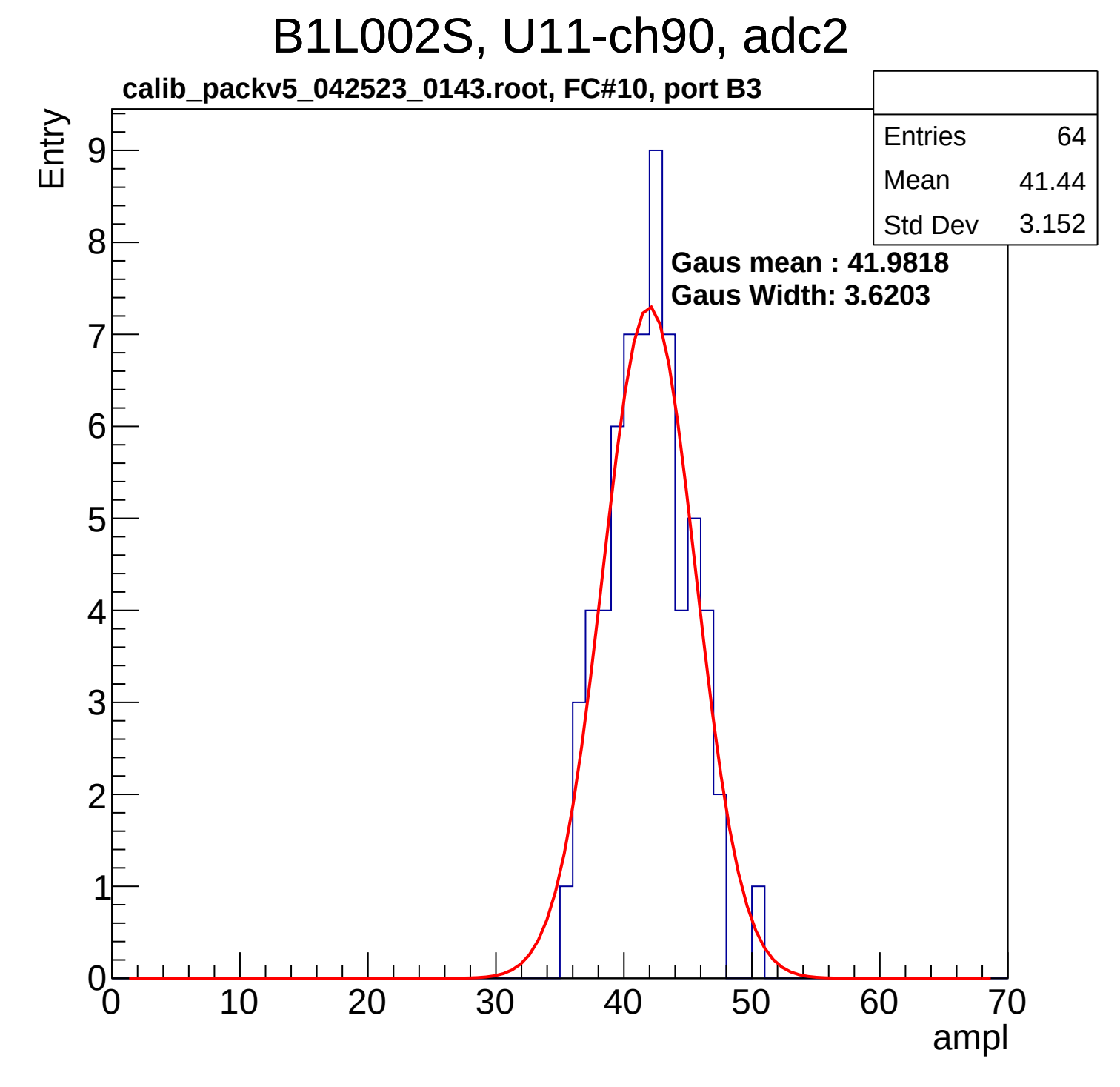

B1L002S, U11-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.37
Std Dev 3.099
Gaus mean : 29.9723
Gaus Width: 3.4256
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 35.82
Std Dev 2.819
Gaus mean : 36.6700
Gaus Width: 2.8143
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.44
Std Dev 3.152
Gaus mean : 41.9818
Gaus Width: 3.6203
Entry
ampl
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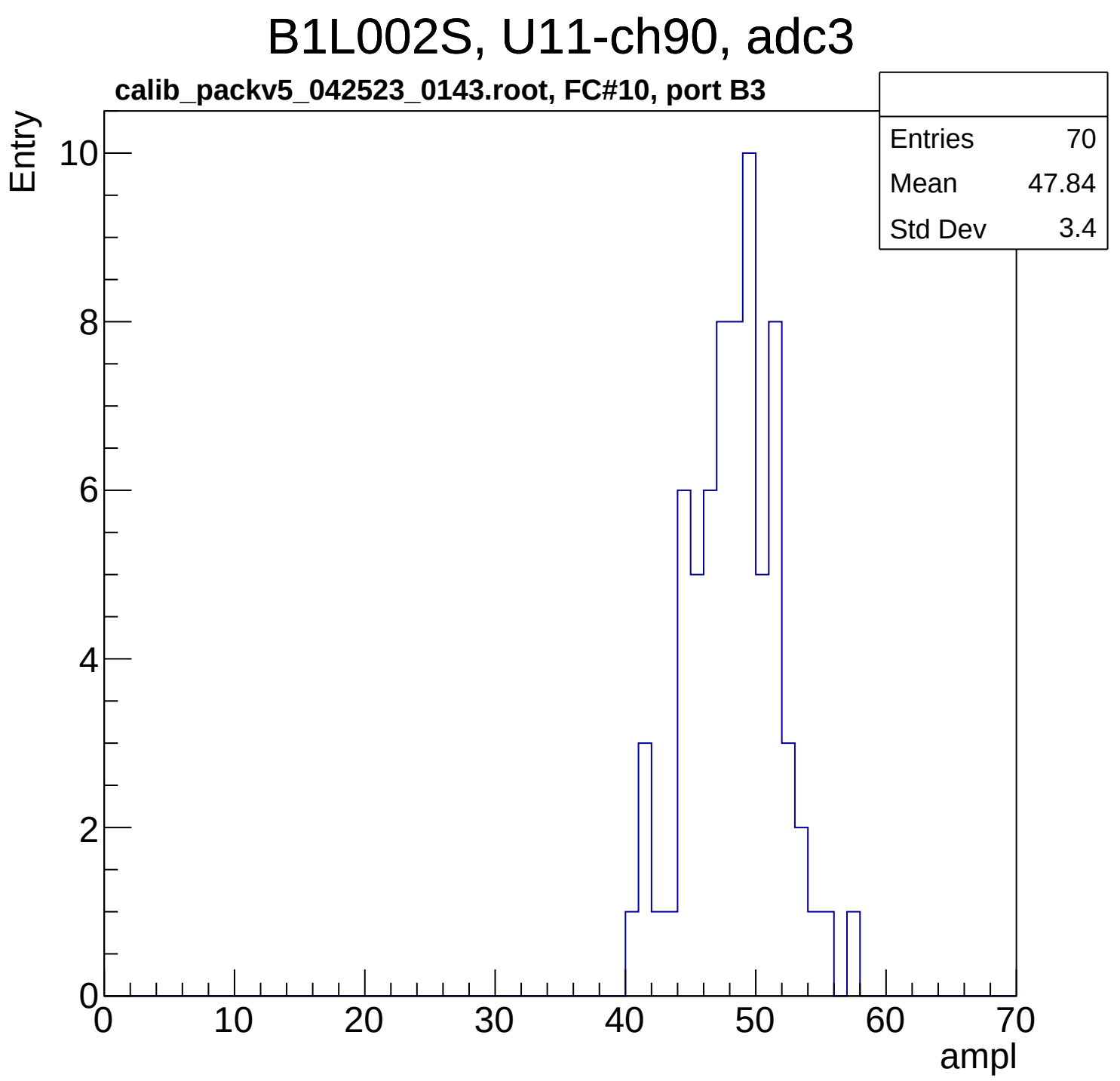
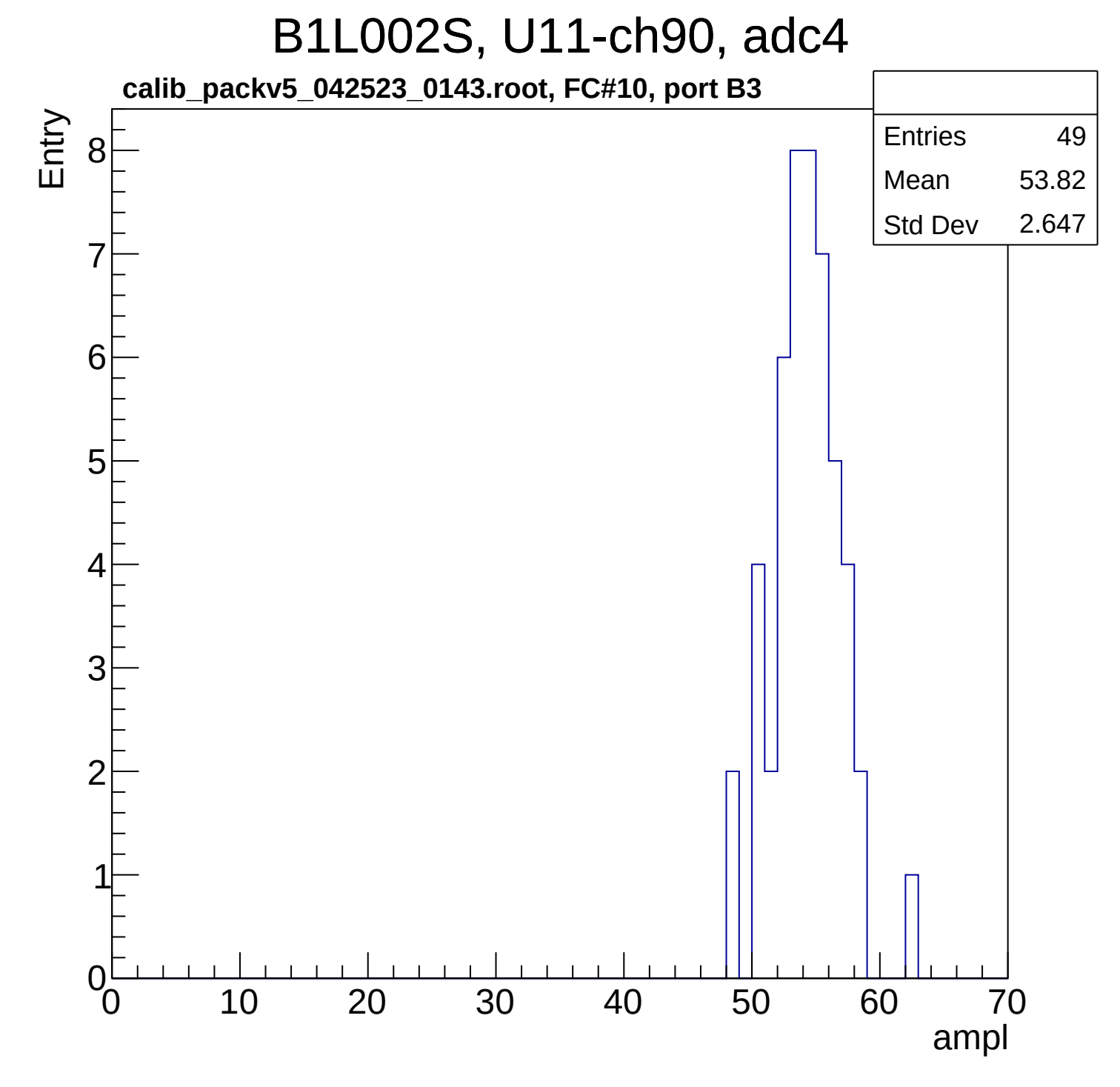
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.84
Std Dev 3.4
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.82
Std Dev 2.647
Entry
ampl
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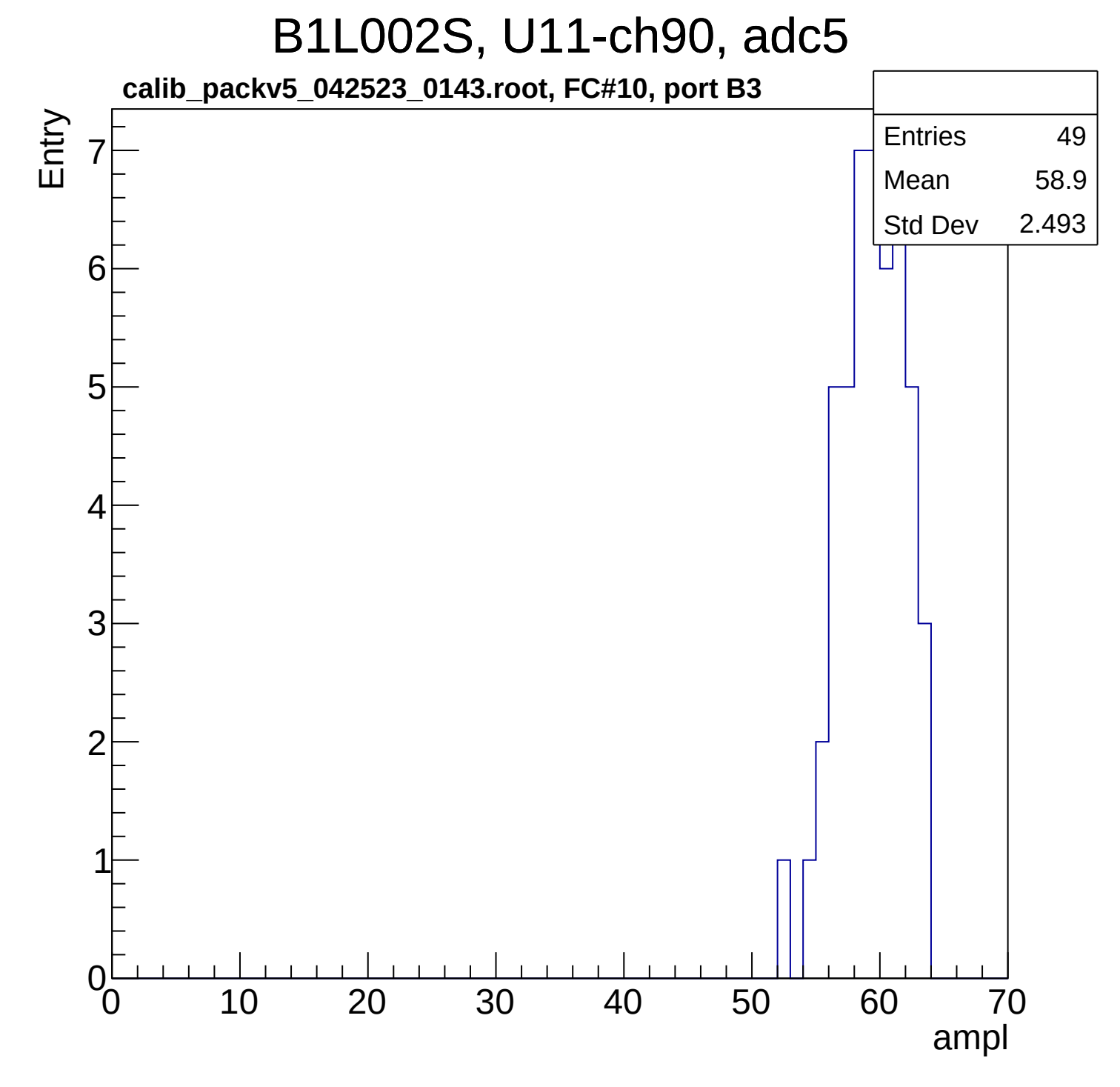
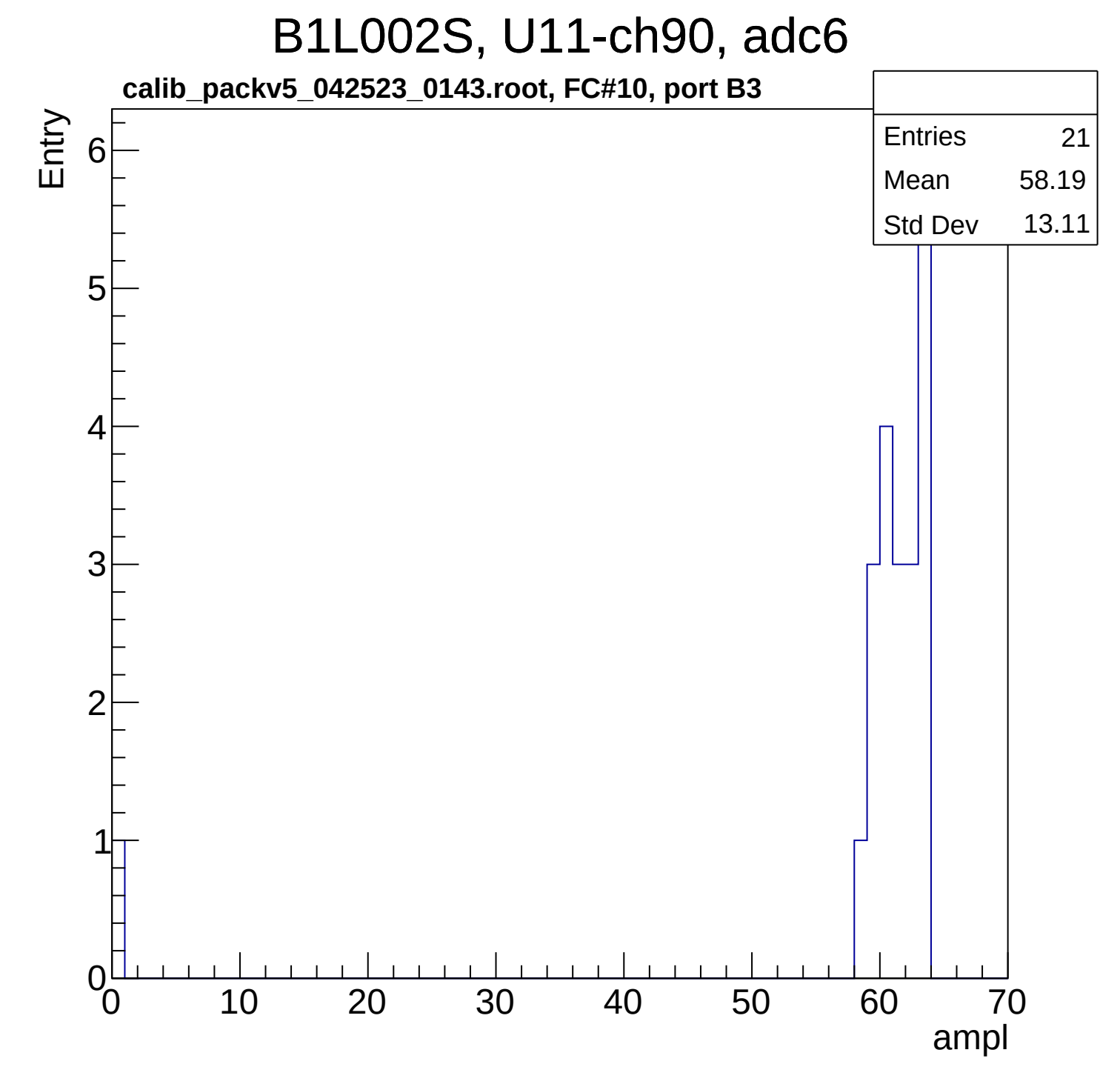
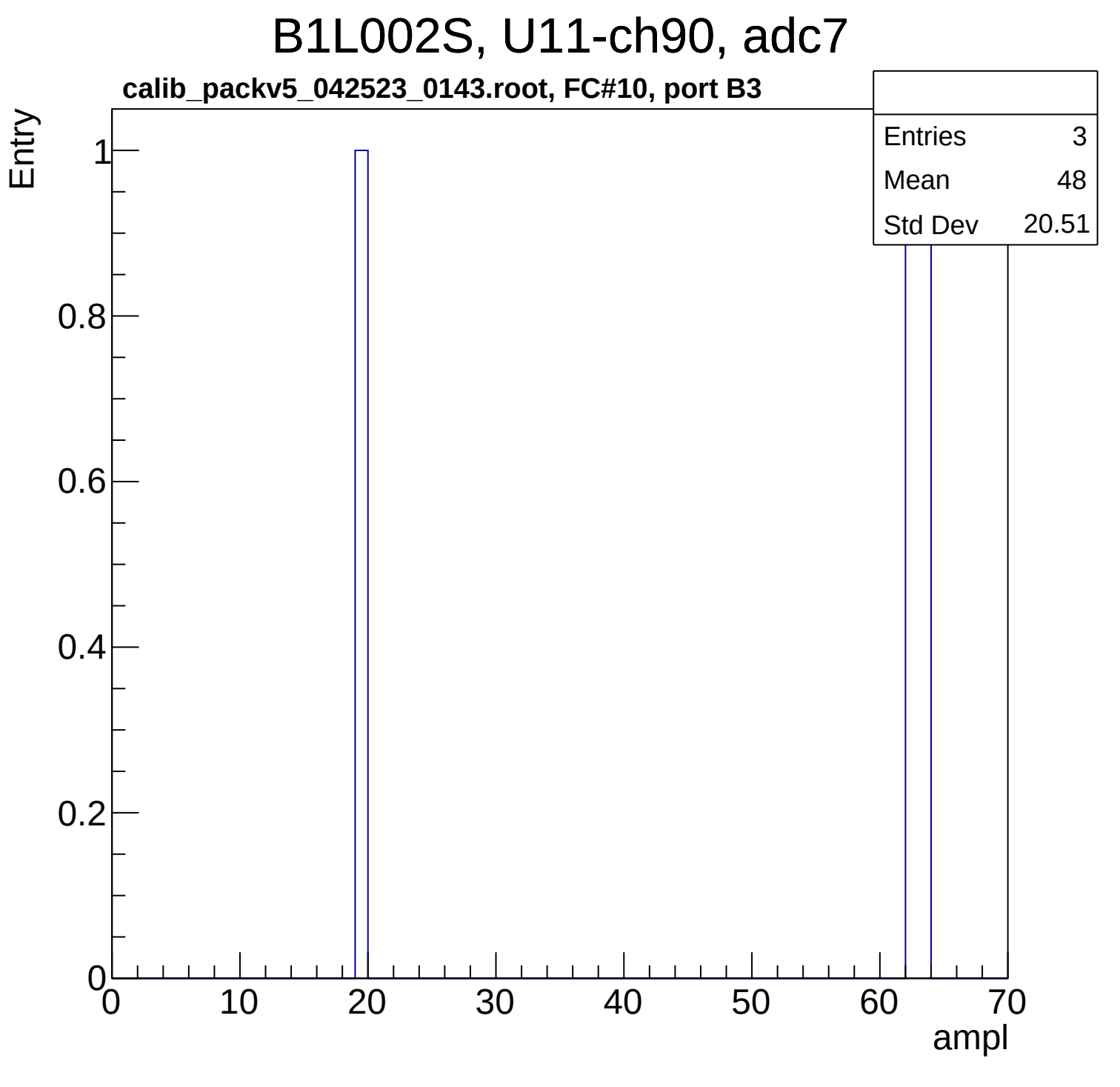
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.9
Std Dev 2.493
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.19
Std Dev 13.11
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48
Std Dev 20.51
Entry
ampl
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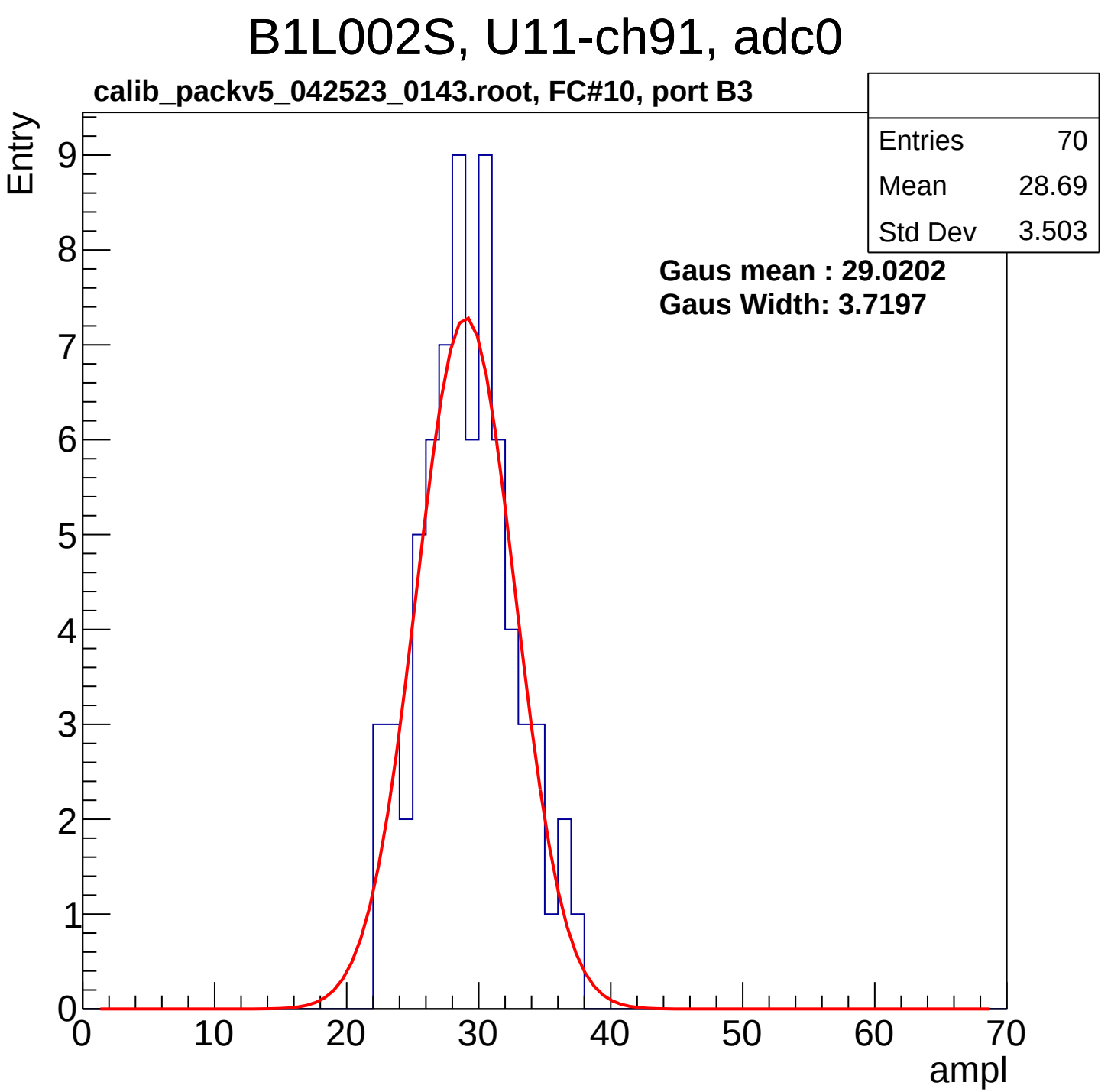
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U11-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.69
Std Dev 3.503
Gaus mean : 29.0202
Gaus Width: 3.7197
Entry
ampl
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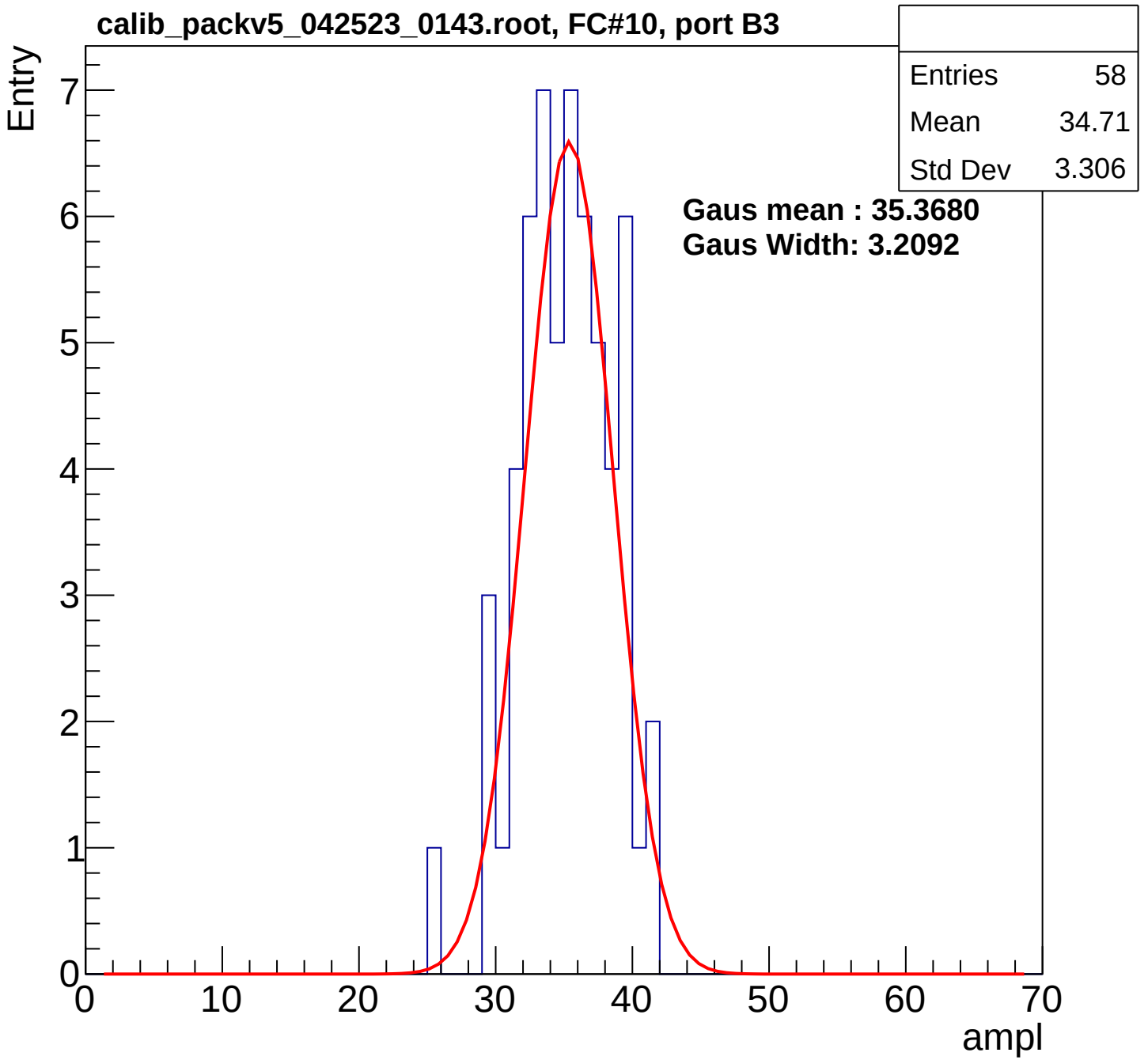
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.71
Std Dev 3.306
Gaus mean : 35.3680
Gaus Width: 3.2092
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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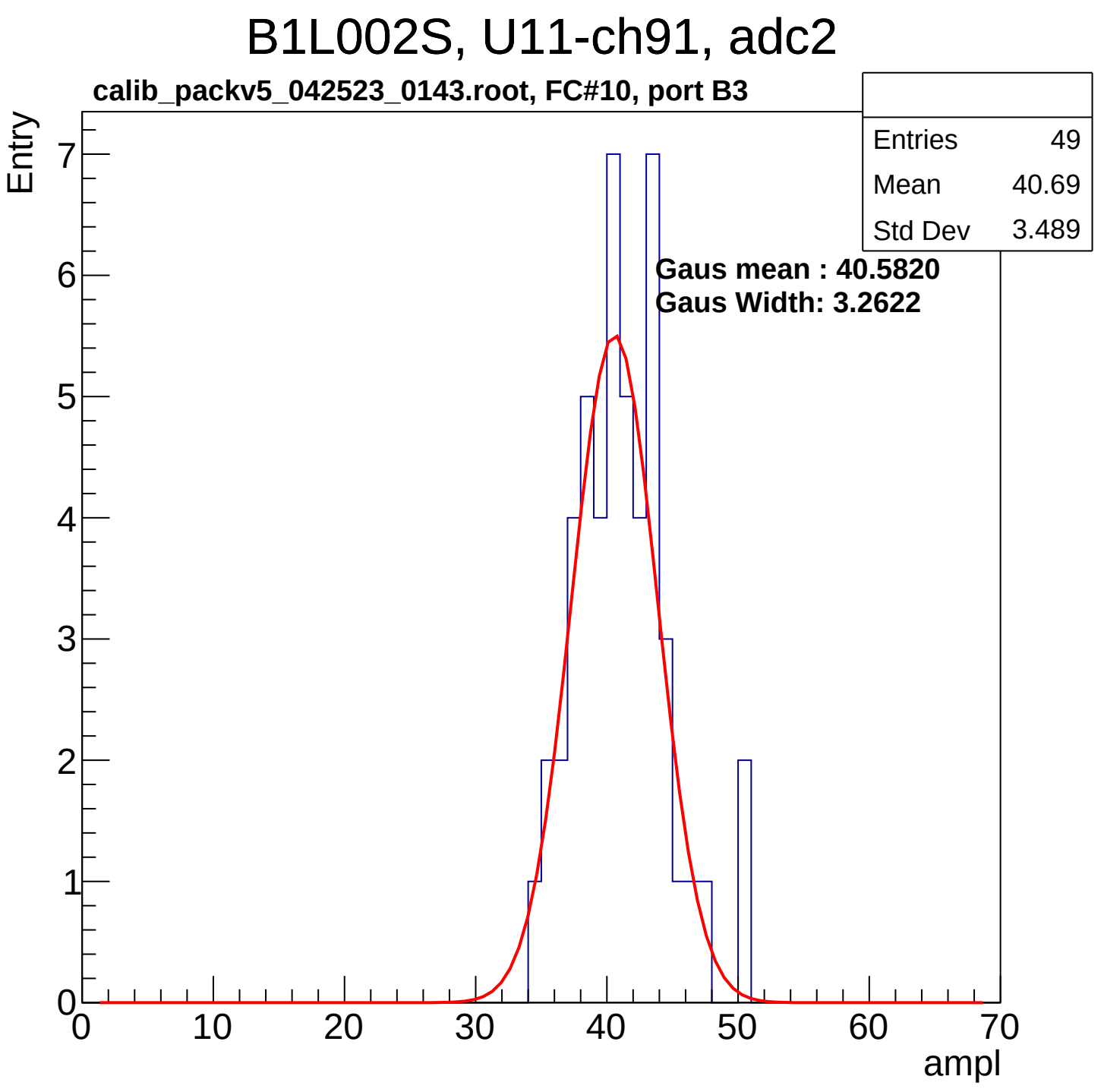
B1L002S, U11-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.69
Std Dev 3.489
Gaus mean : 40.5820
Gaus Width: 3.2622
Entry
ampl
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1
2
3
4
5
6
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0
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40
50
60
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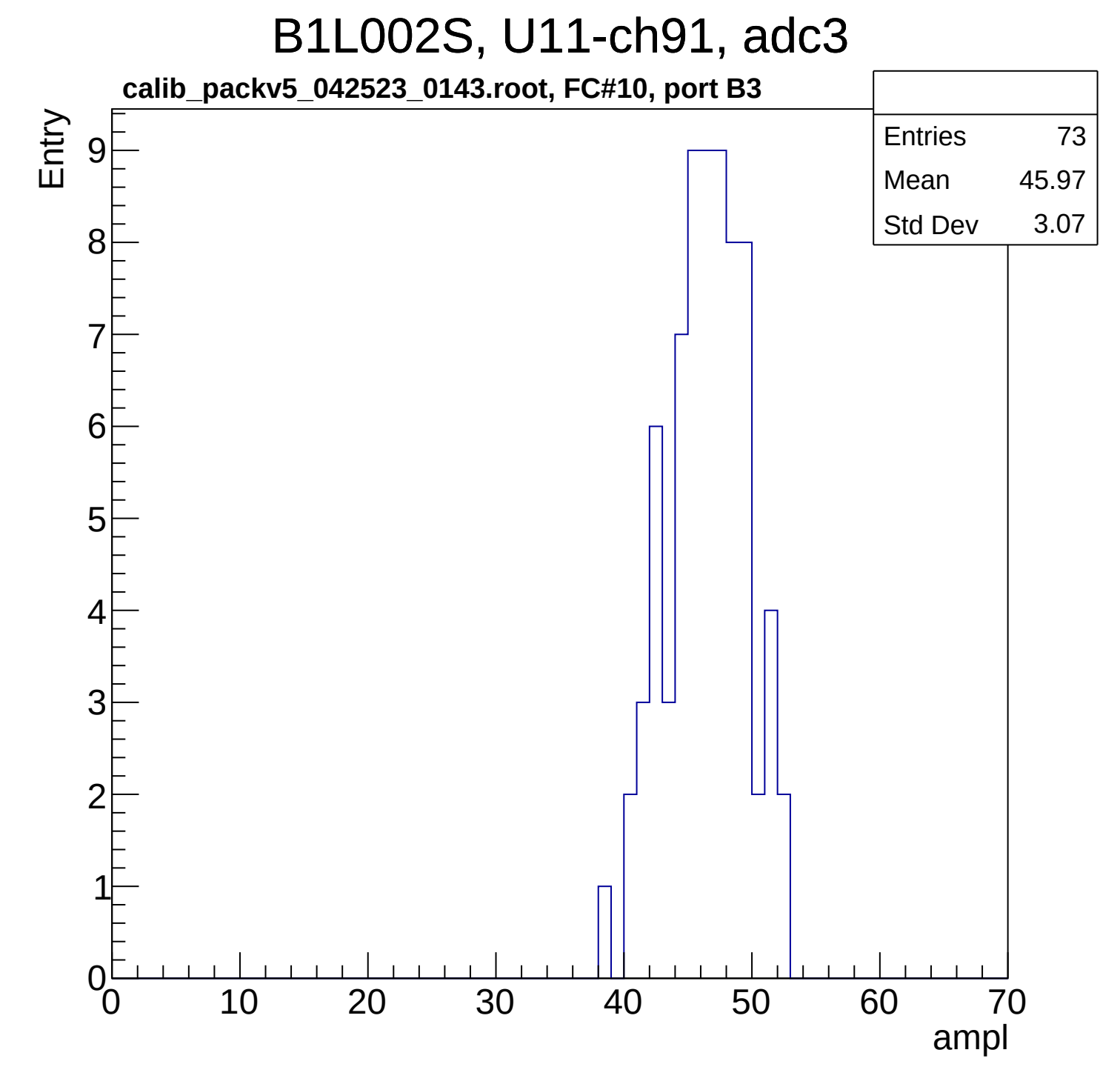
B1L002S, U11-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 45.97
Std Dev 3.07
Entry
ampl
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1
2
3
4
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7
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0
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60
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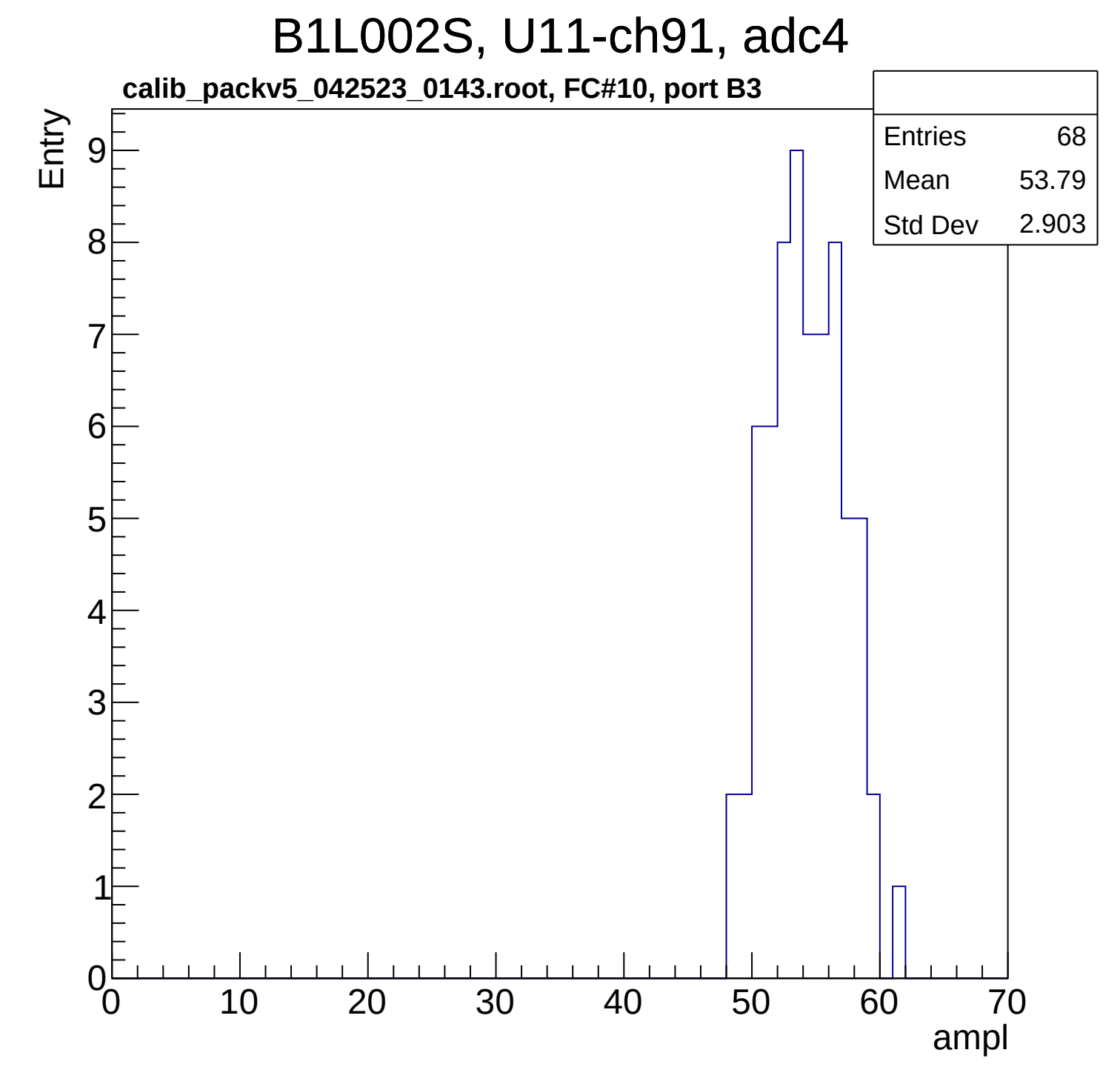
B1L002S, U11-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.79
Std Dev 2.903
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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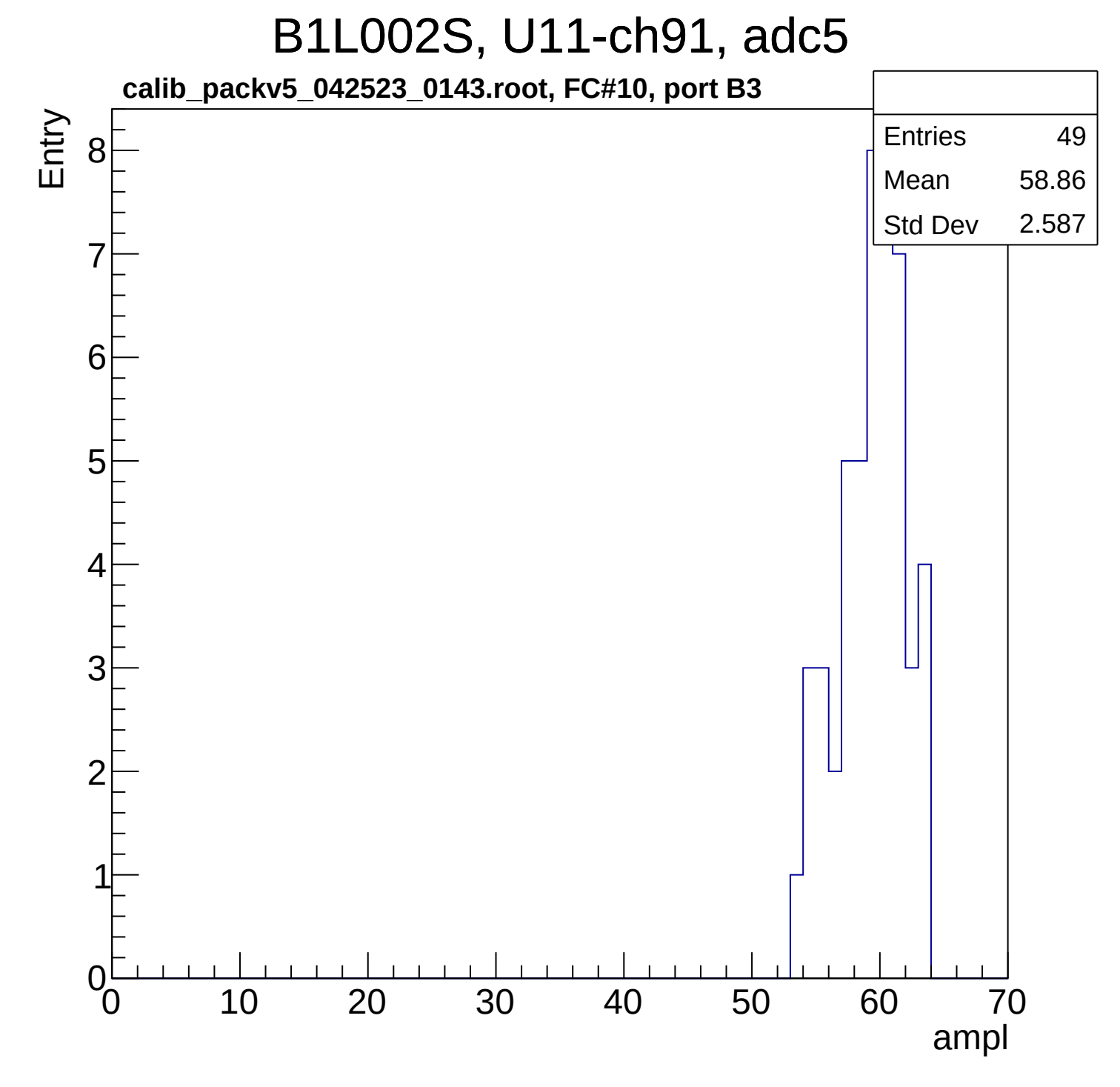
B1L002S, U11-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.86
Std Dev 2.587
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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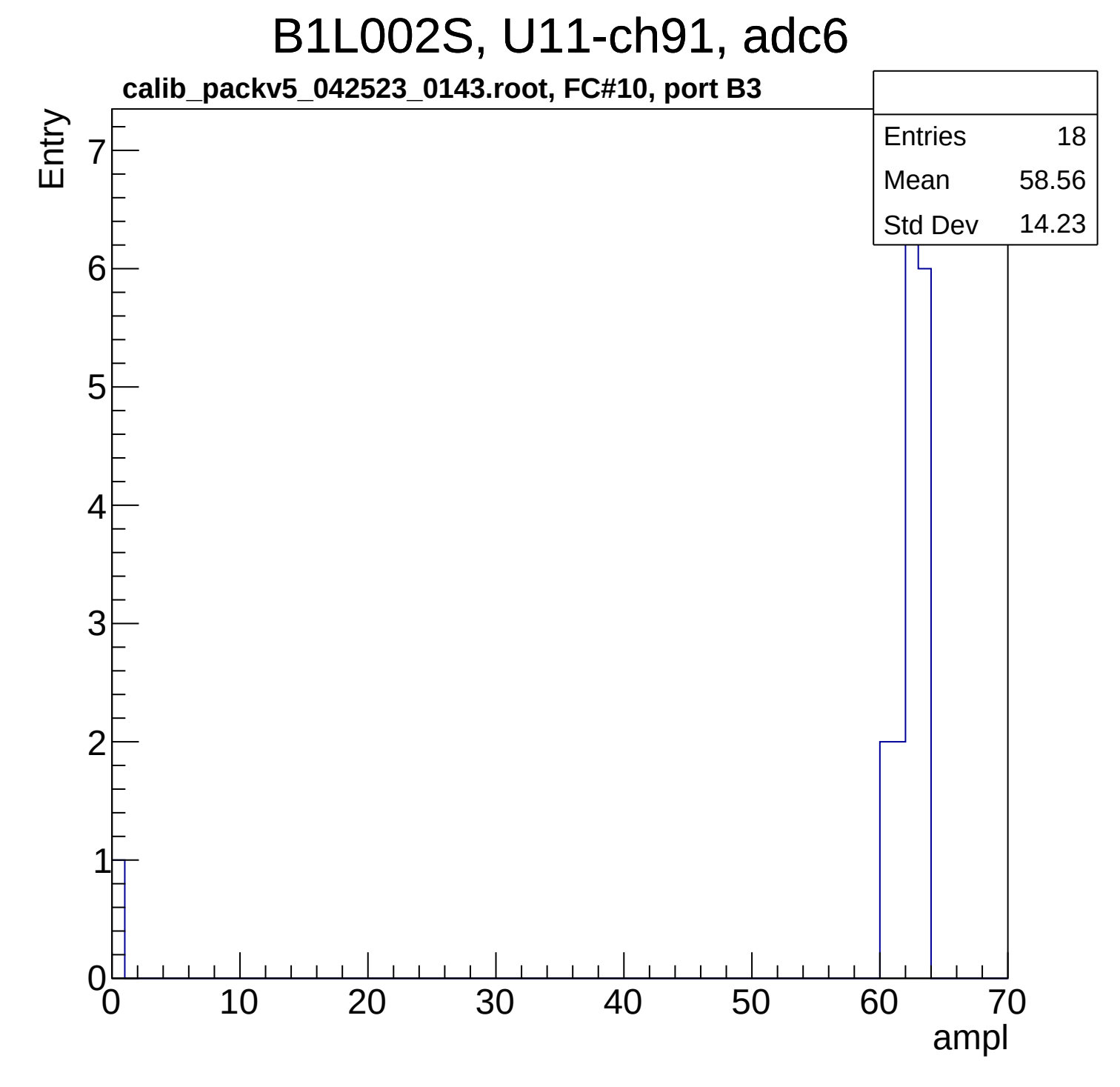
40
50
60
70


B1L002S, U11-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.56
Std Dev 14.23
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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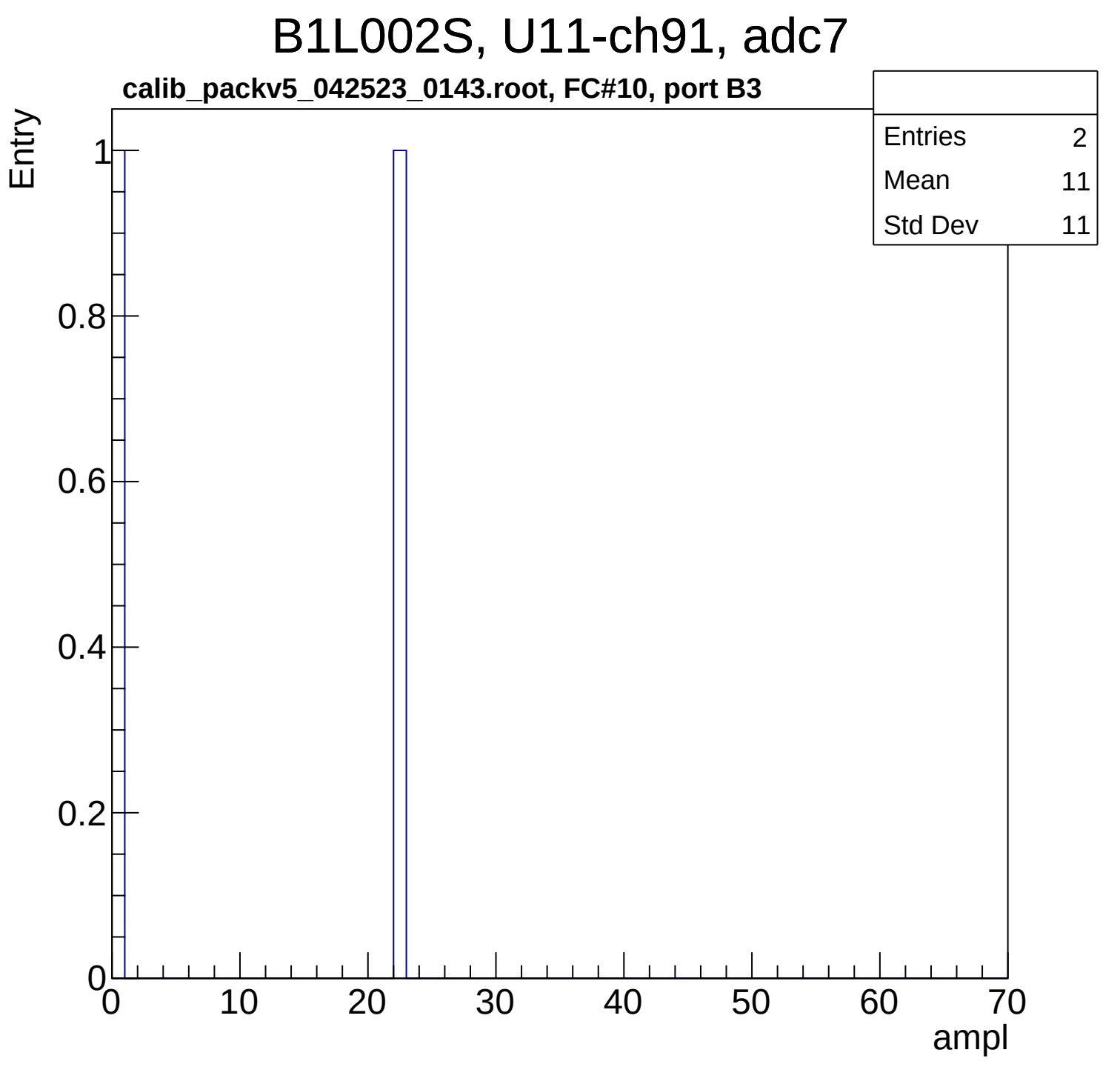
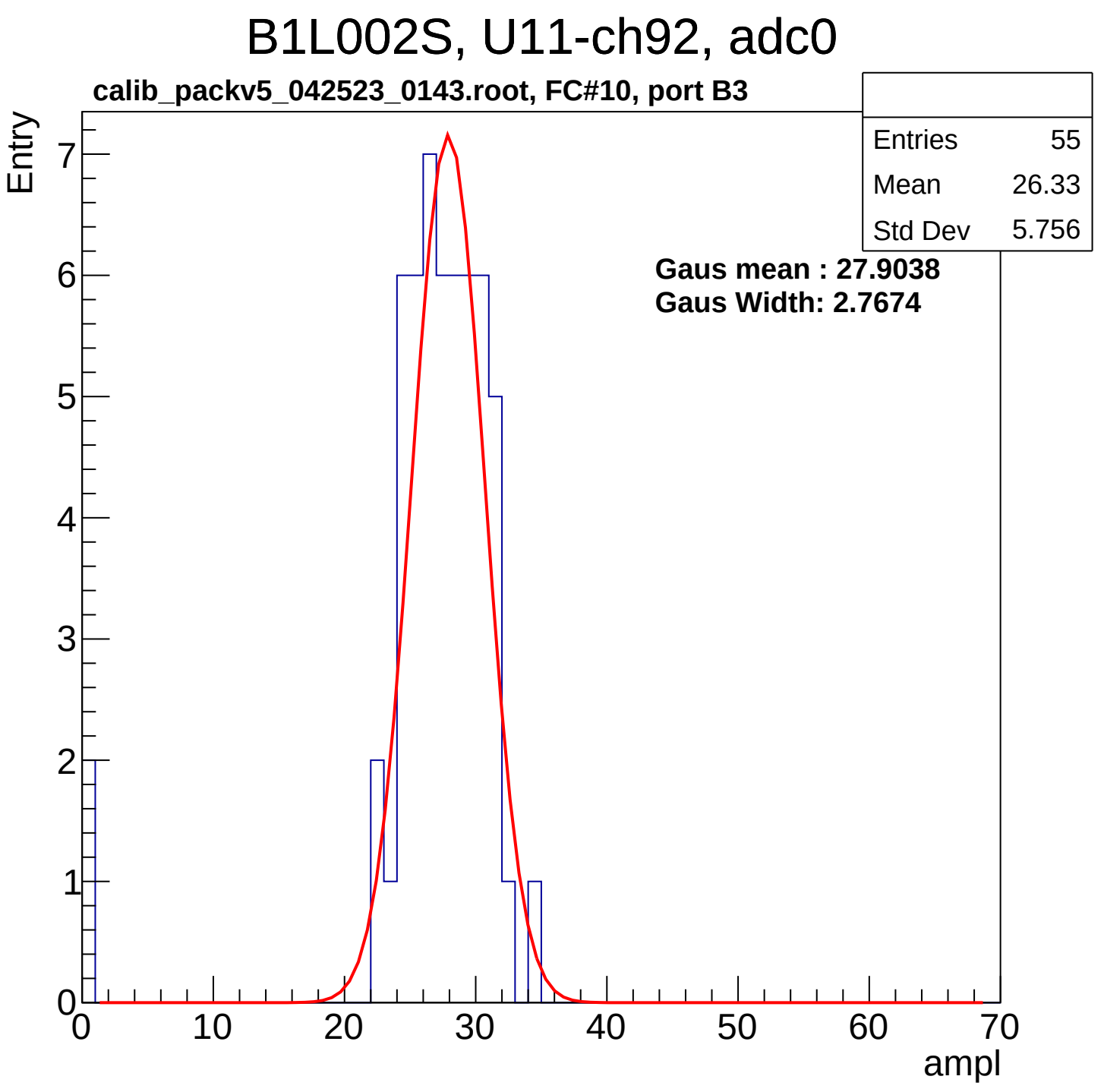
60
70


B1L002S, U11-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 26.33
Std Dev 5.756
Gaus mean : 27.9038
Gaus Width: 2.7674
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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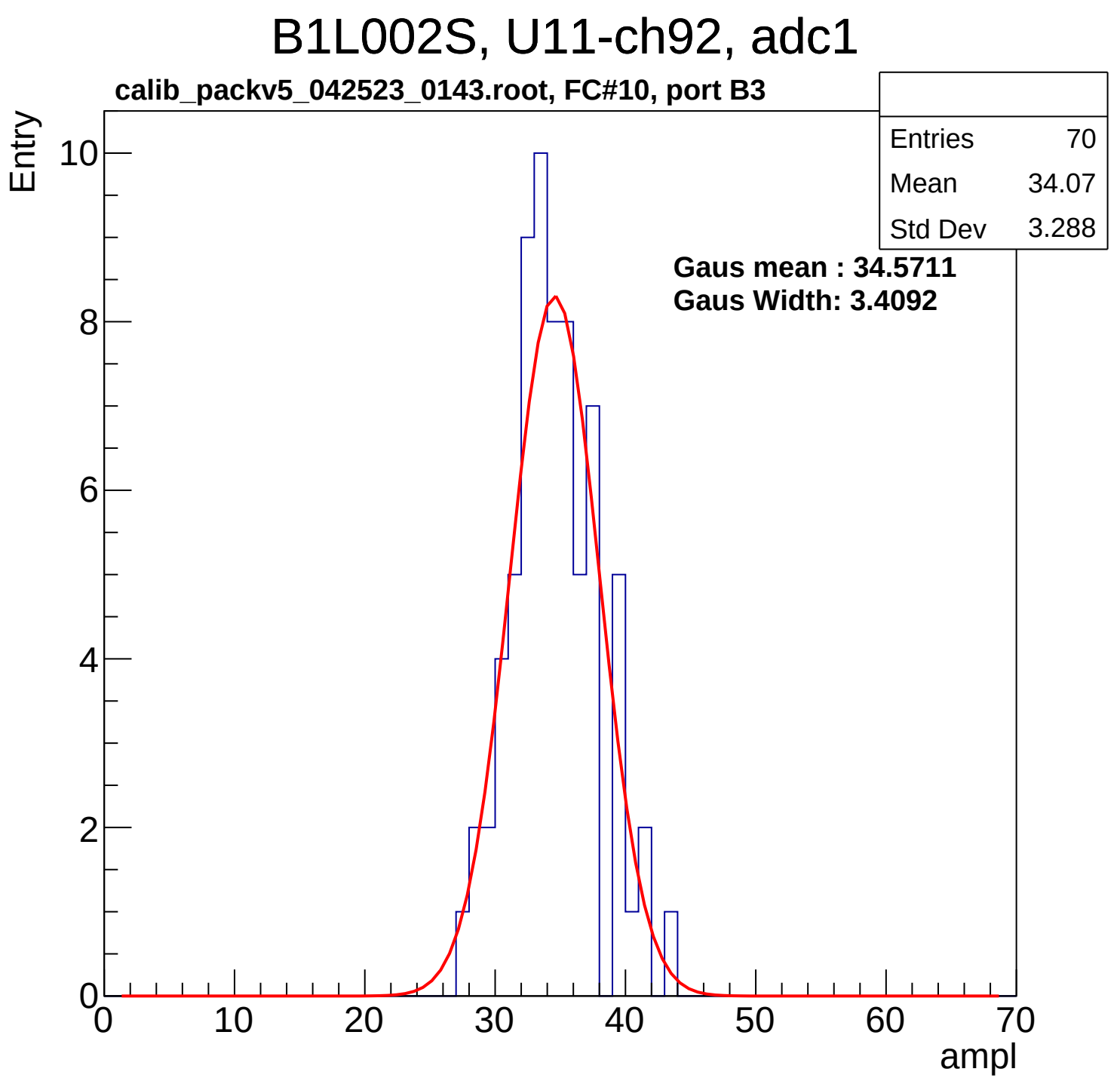
60
70


B1L002S, U11-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.07
Std Dev 3.288
Gaus mean : 34.5711
Gaus Width: 3.4092
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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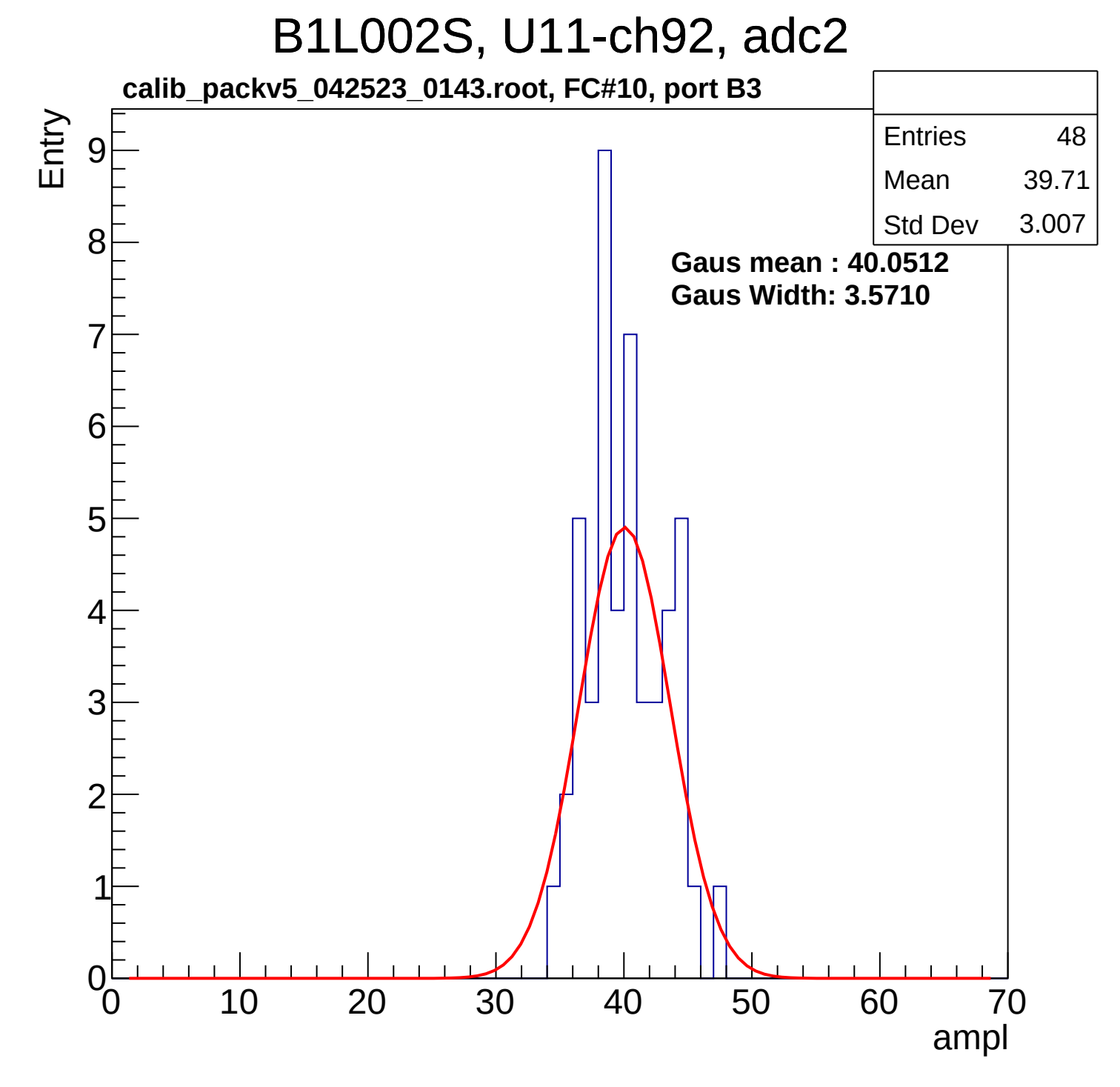
B1L002S, U11-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 39.71
Std Dev 3.007
Gaus mean : 40.0512
Gaus Width: 3.5710
Entry
ampl
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2
3
4
5
6
7
8
9
0
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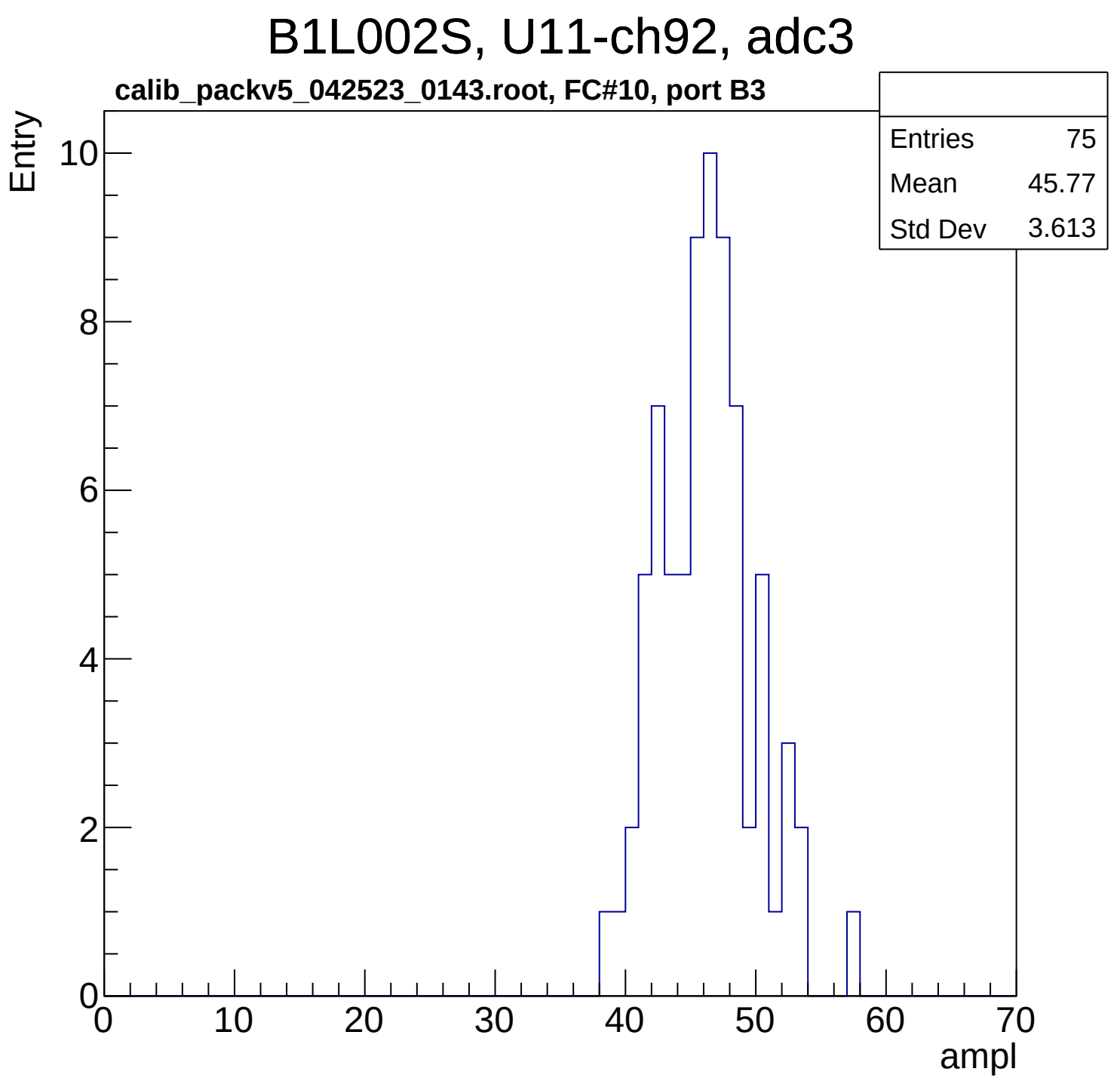
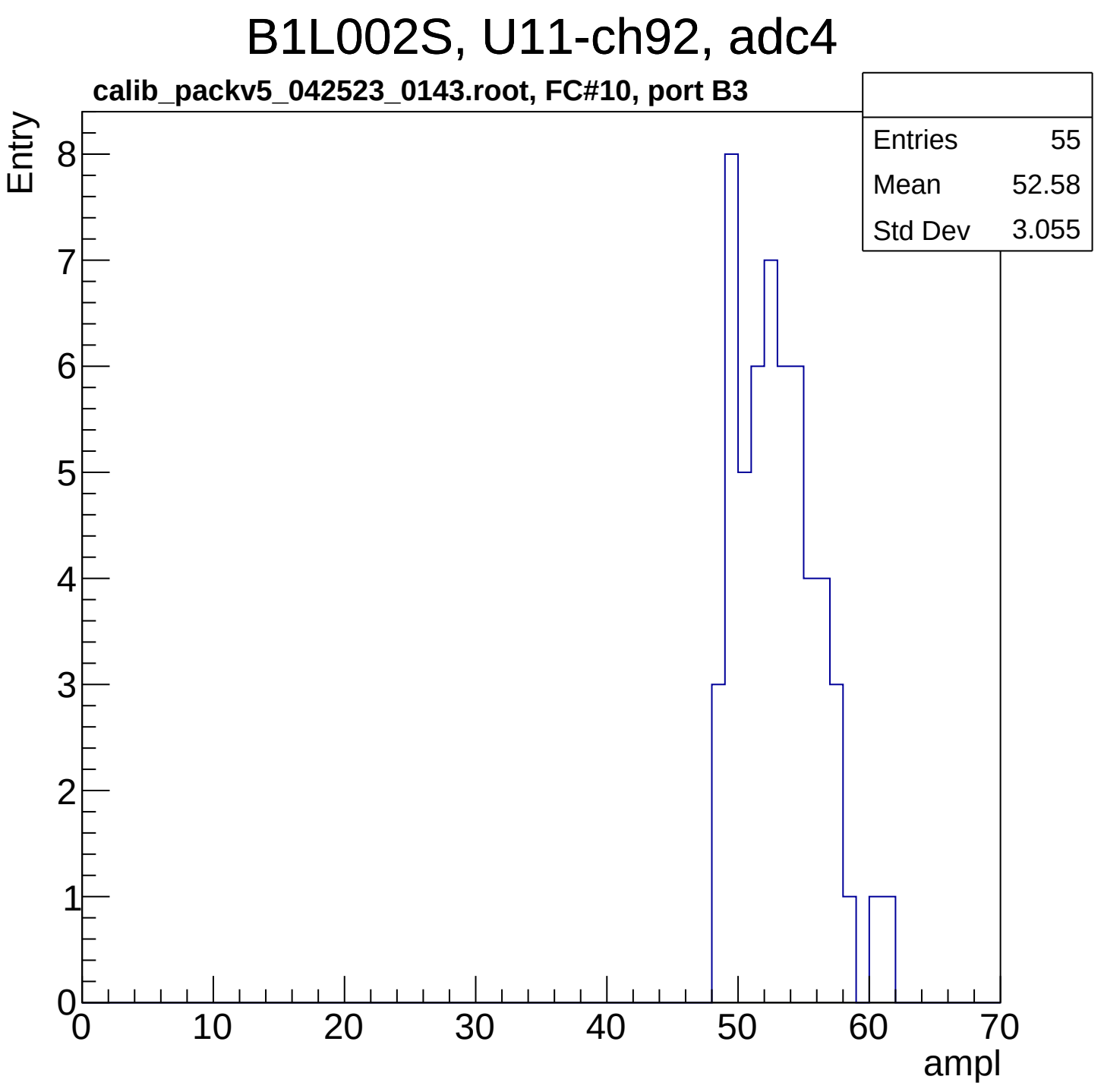
20
30
40
50
60
70


B1L002S, U11-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 45.77
Std Dev 3.613
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.58
Std Dev 3.055
Entry
0
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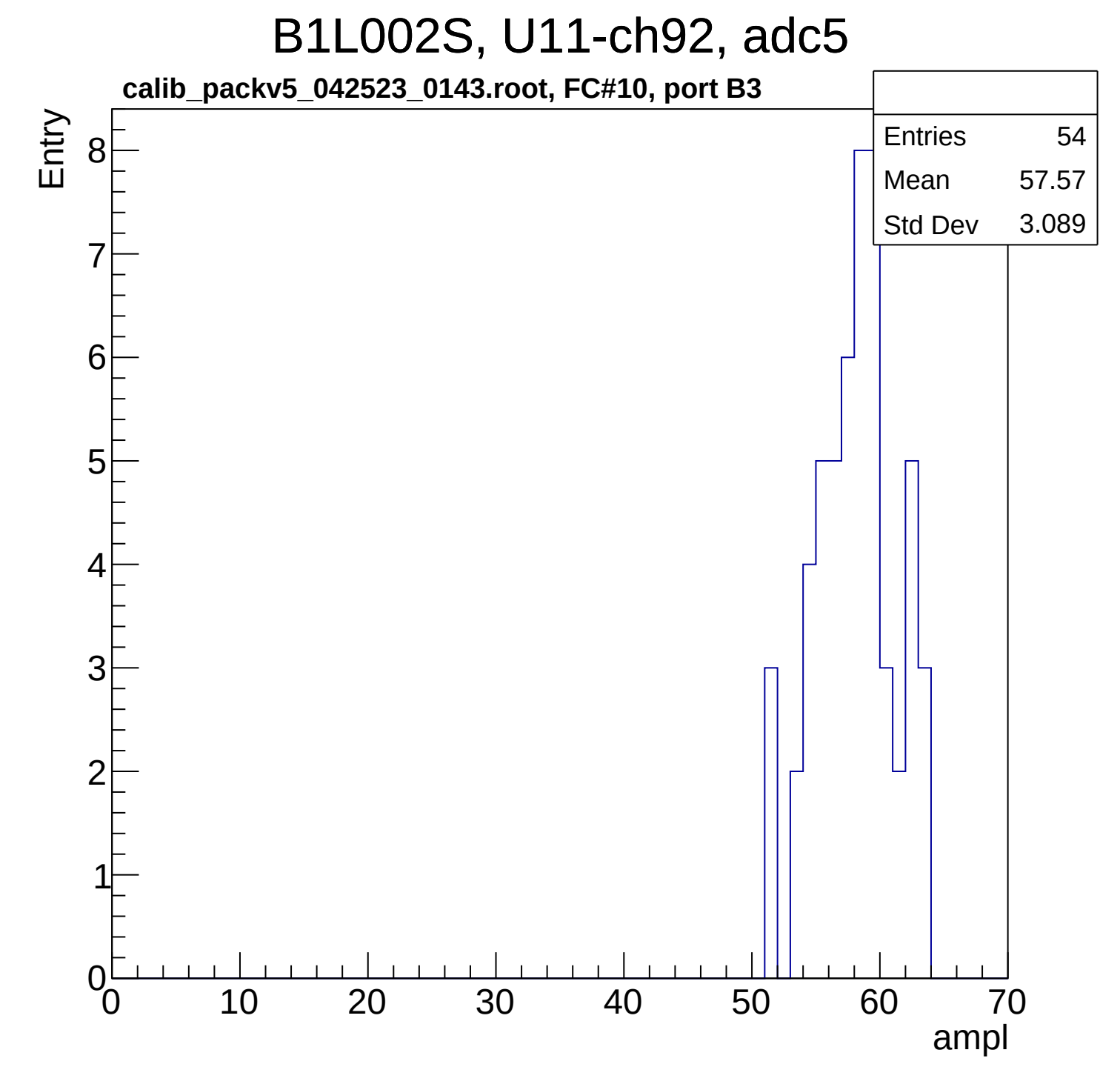
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.57
Std Dev 3.089
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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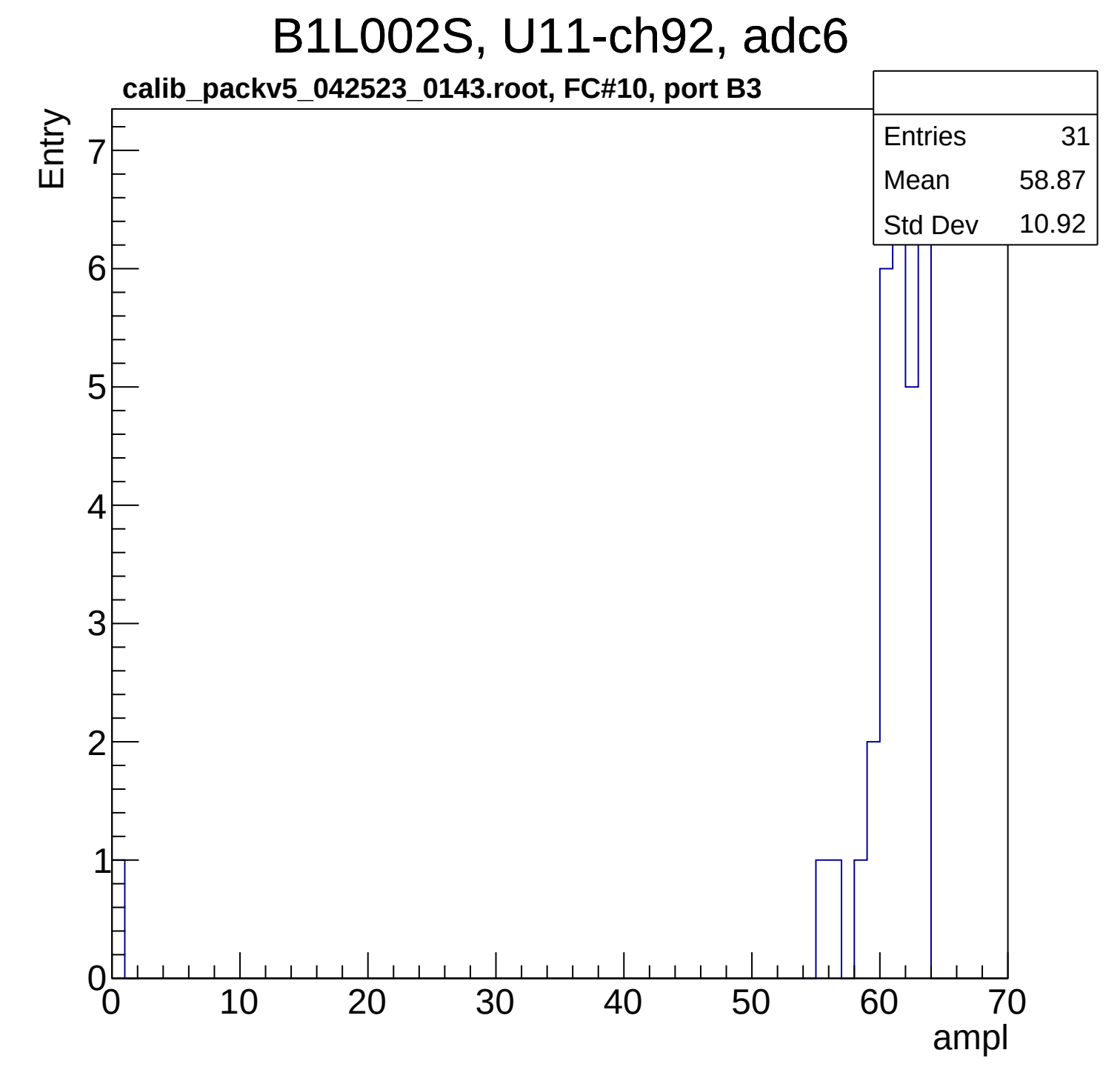
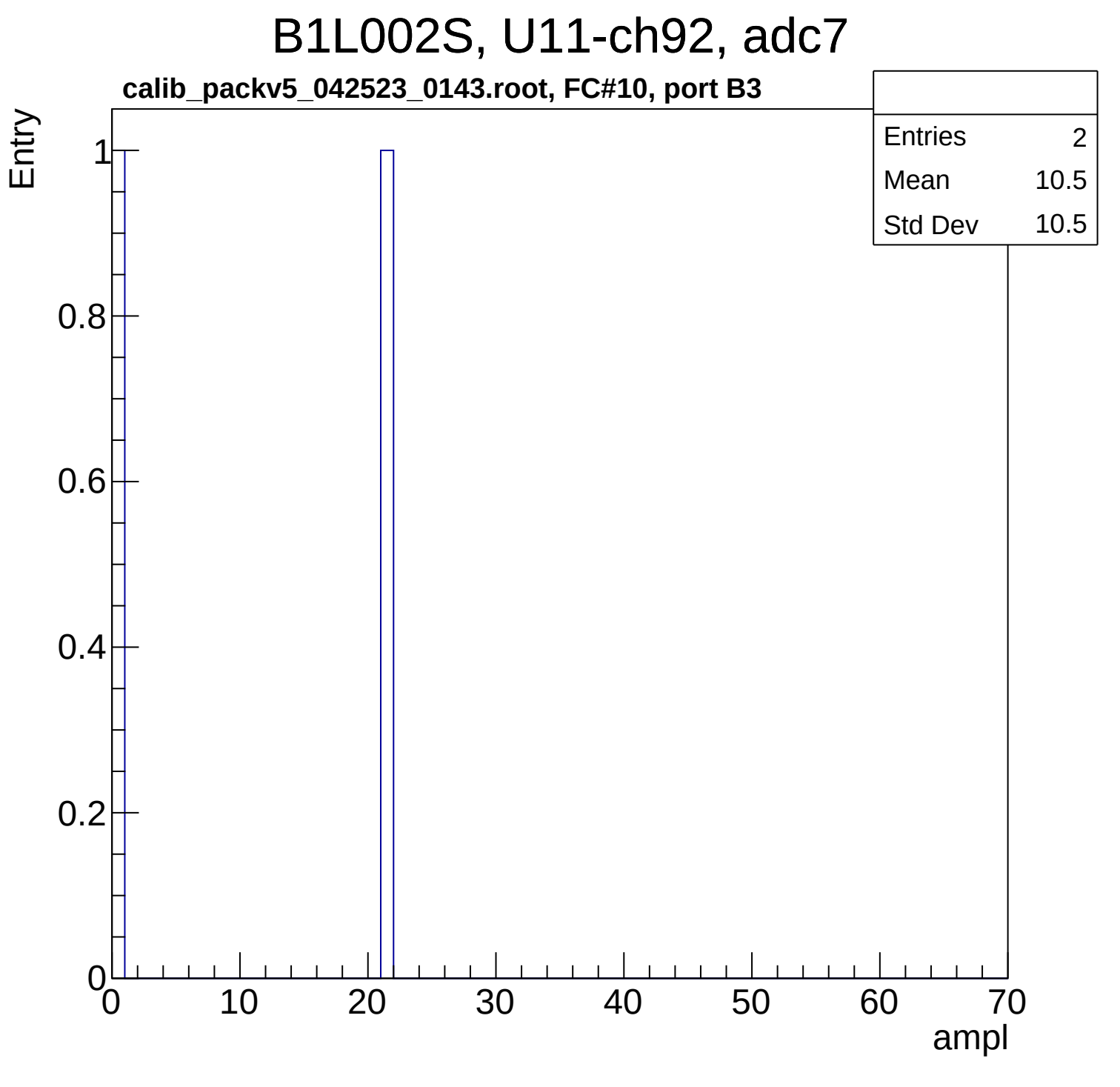
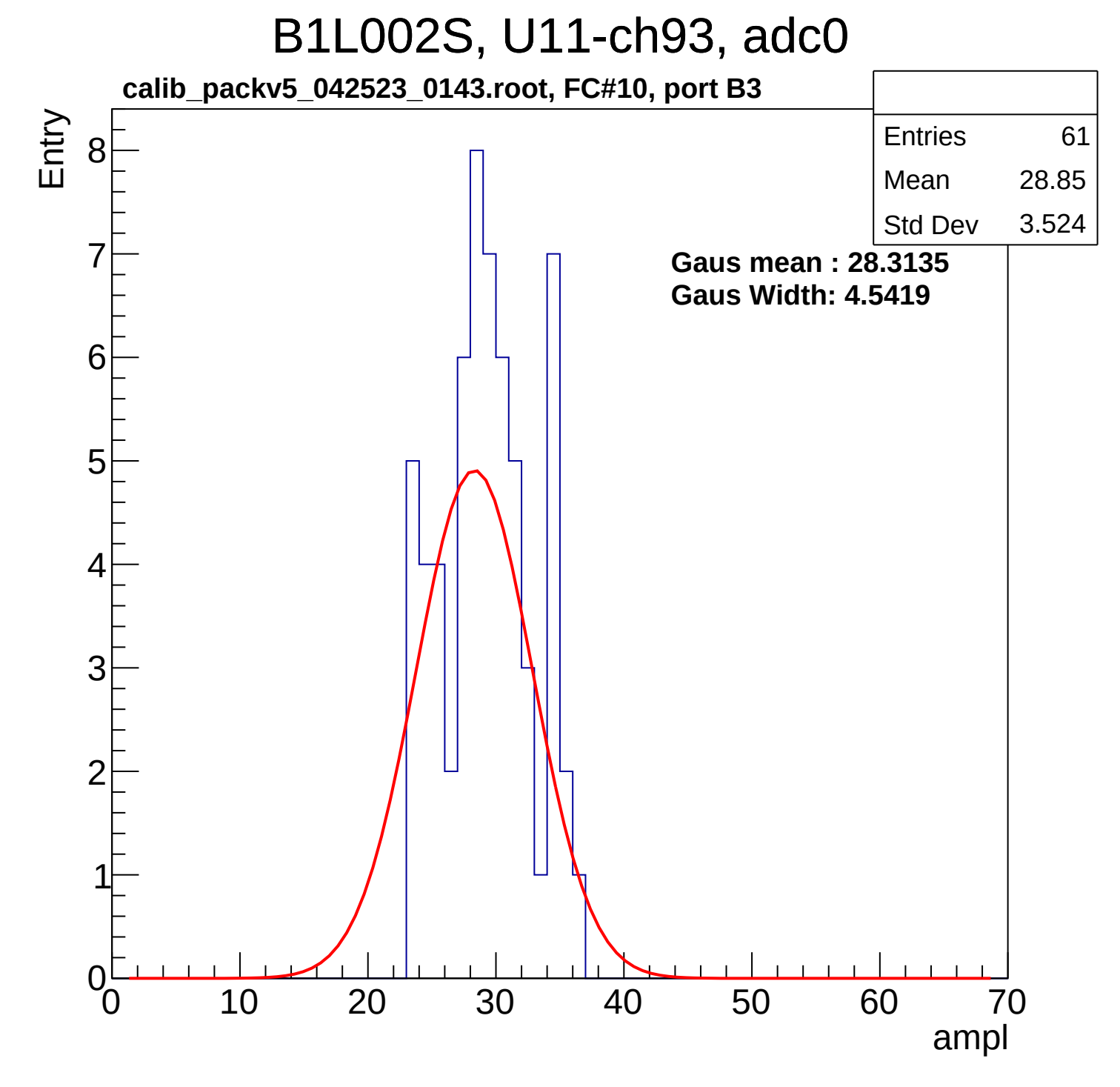

B1L002S, U11-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.87
Std Dev 10.92
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.85
Std Dev 3.524
Gaus mean : 28.3135
Gaus Width: 4.5419
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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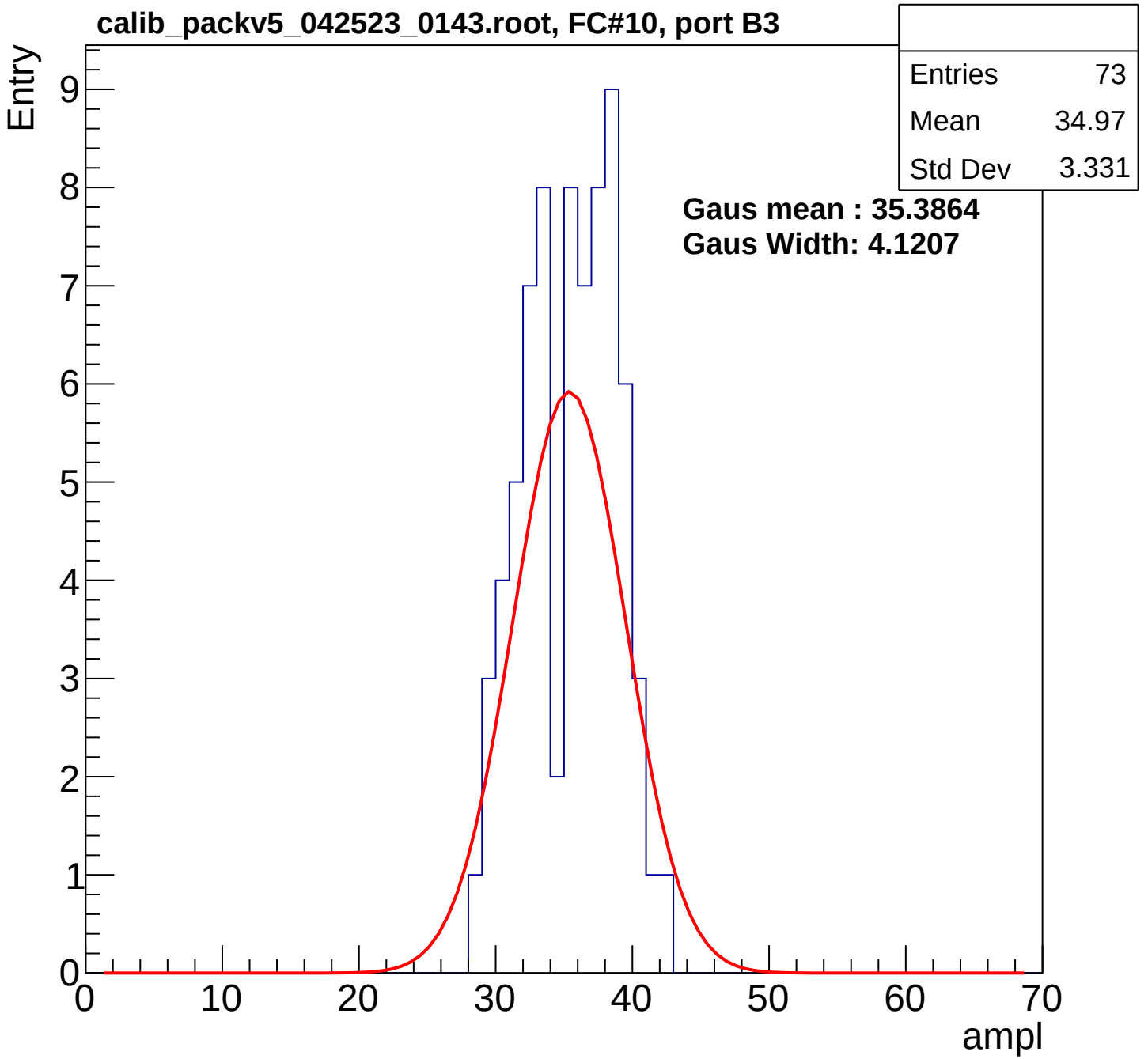
B1L002S, U11-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.97
Std Dev 3.331
Gaus mean : 35.3864
Gaus Width: 4.1207
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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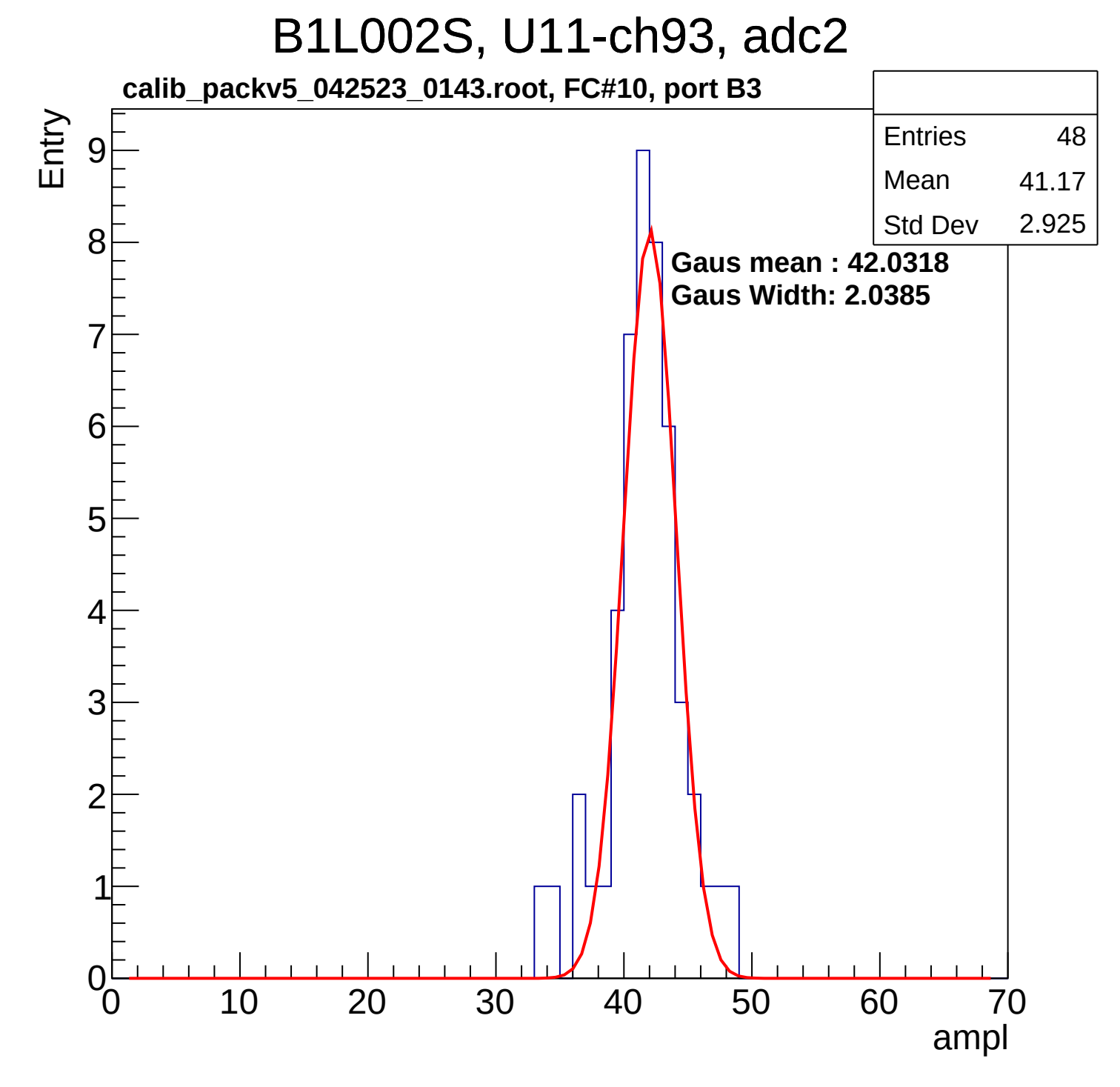
B1L002S, U11-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.17
Std Dev 2.925
Gaus mean : 42.0318
Gaus Width: 2.0385
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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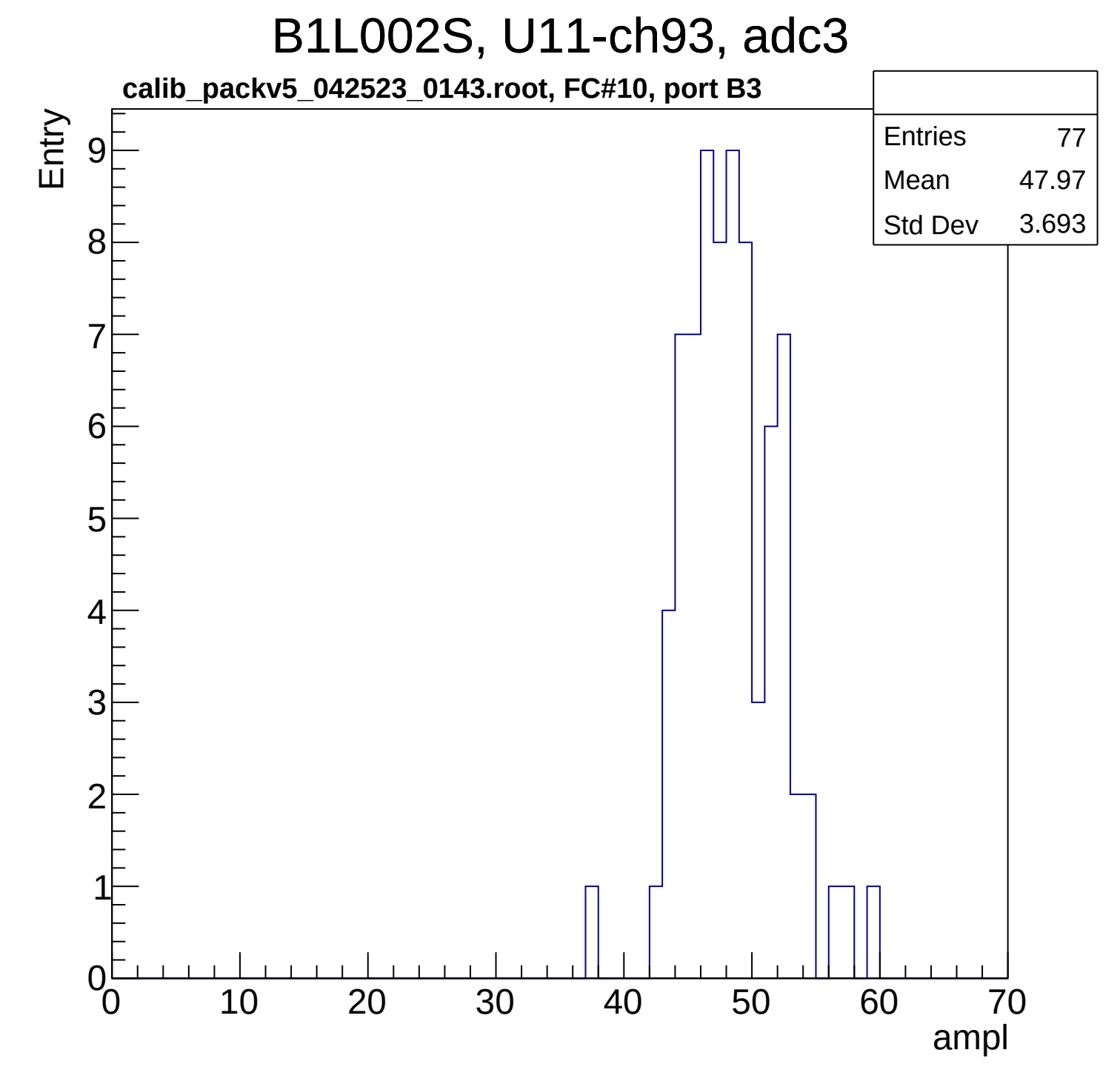
B1L002S, U11-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 47.97
Std Dev 3.693
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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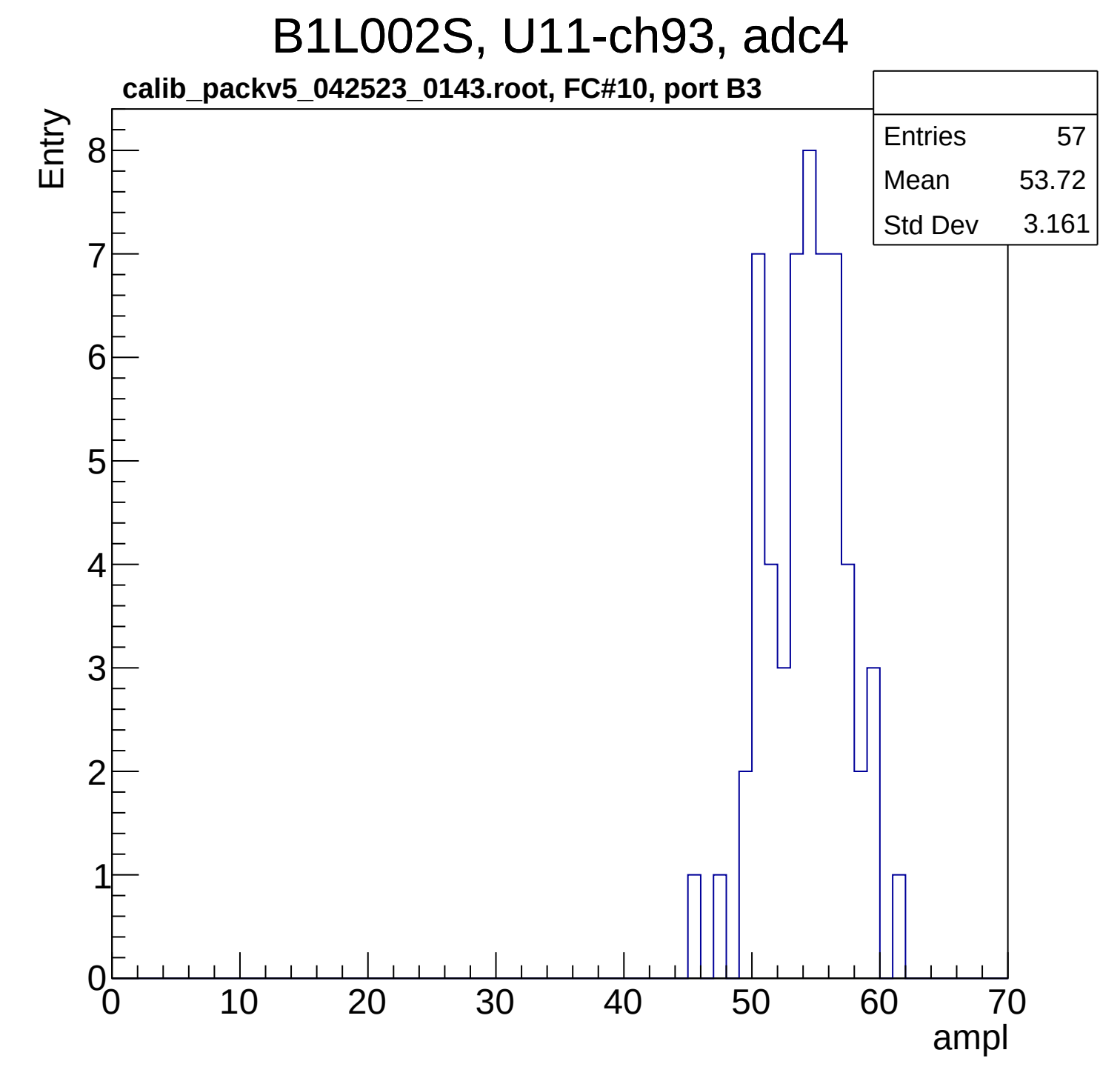
B1L002S, U11-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.72
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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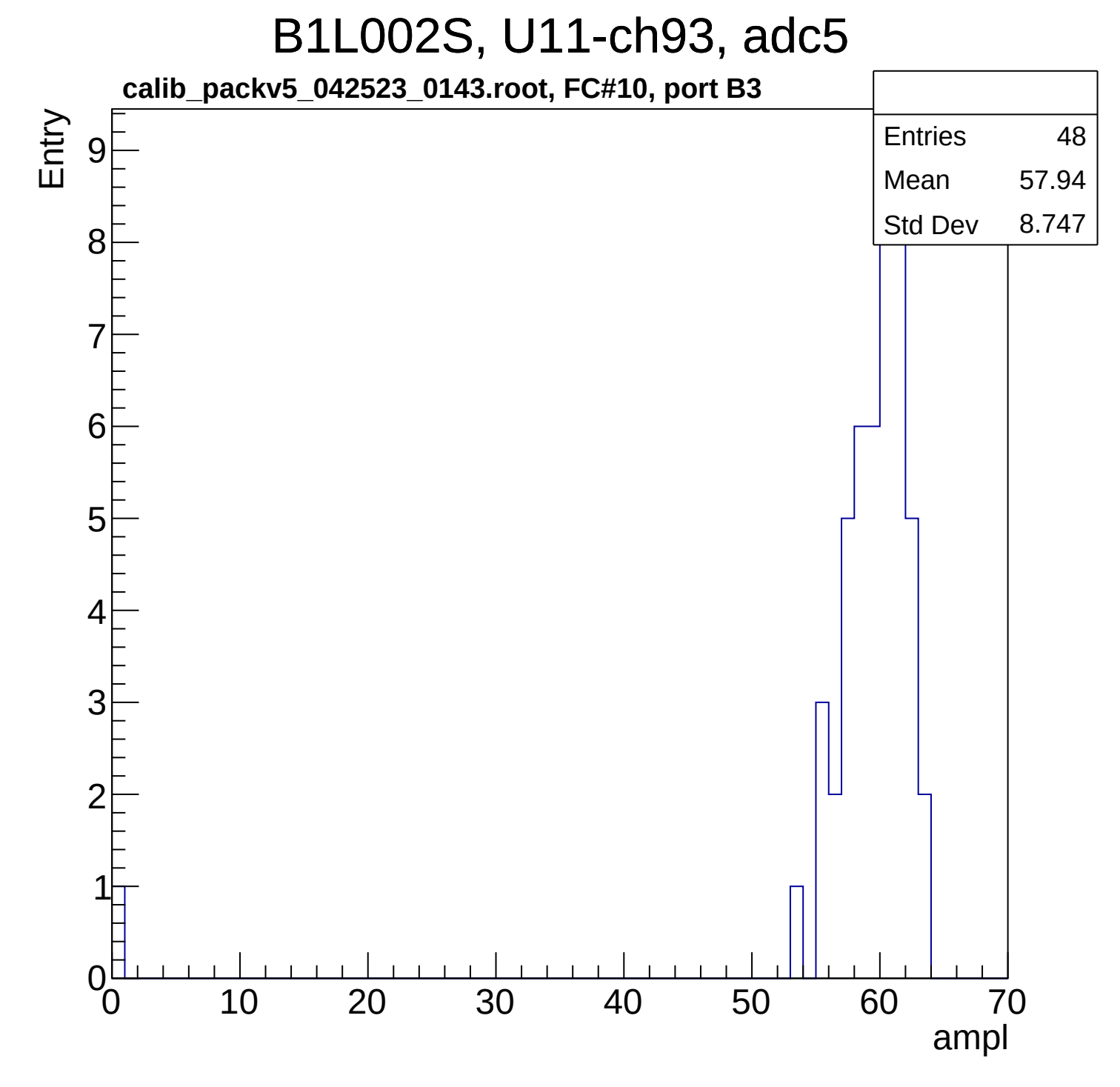
B1L002S, U11-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.94
Std Dev 8.747
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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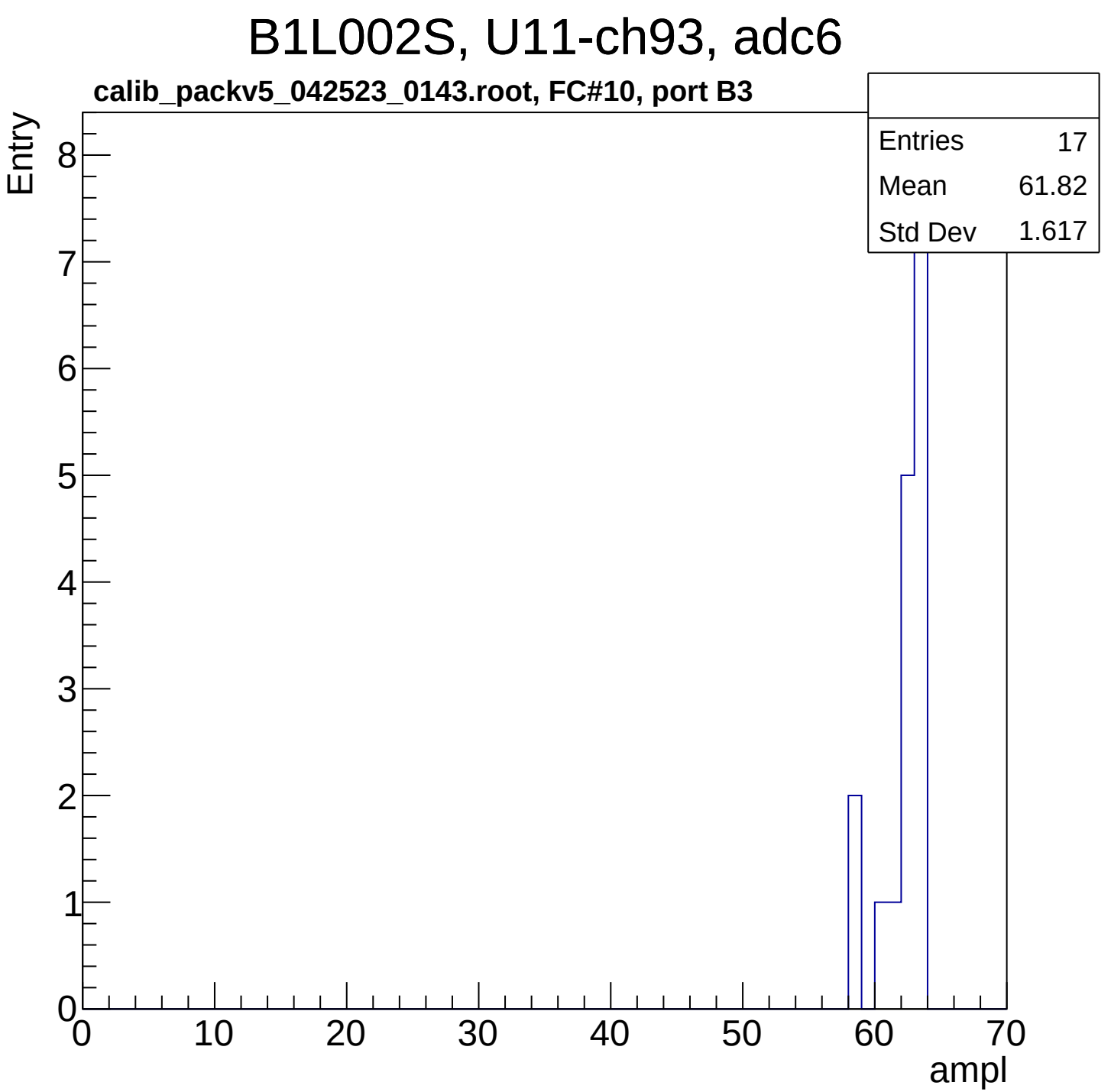
B1L002S, U11-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.82
Std Dev 1.617
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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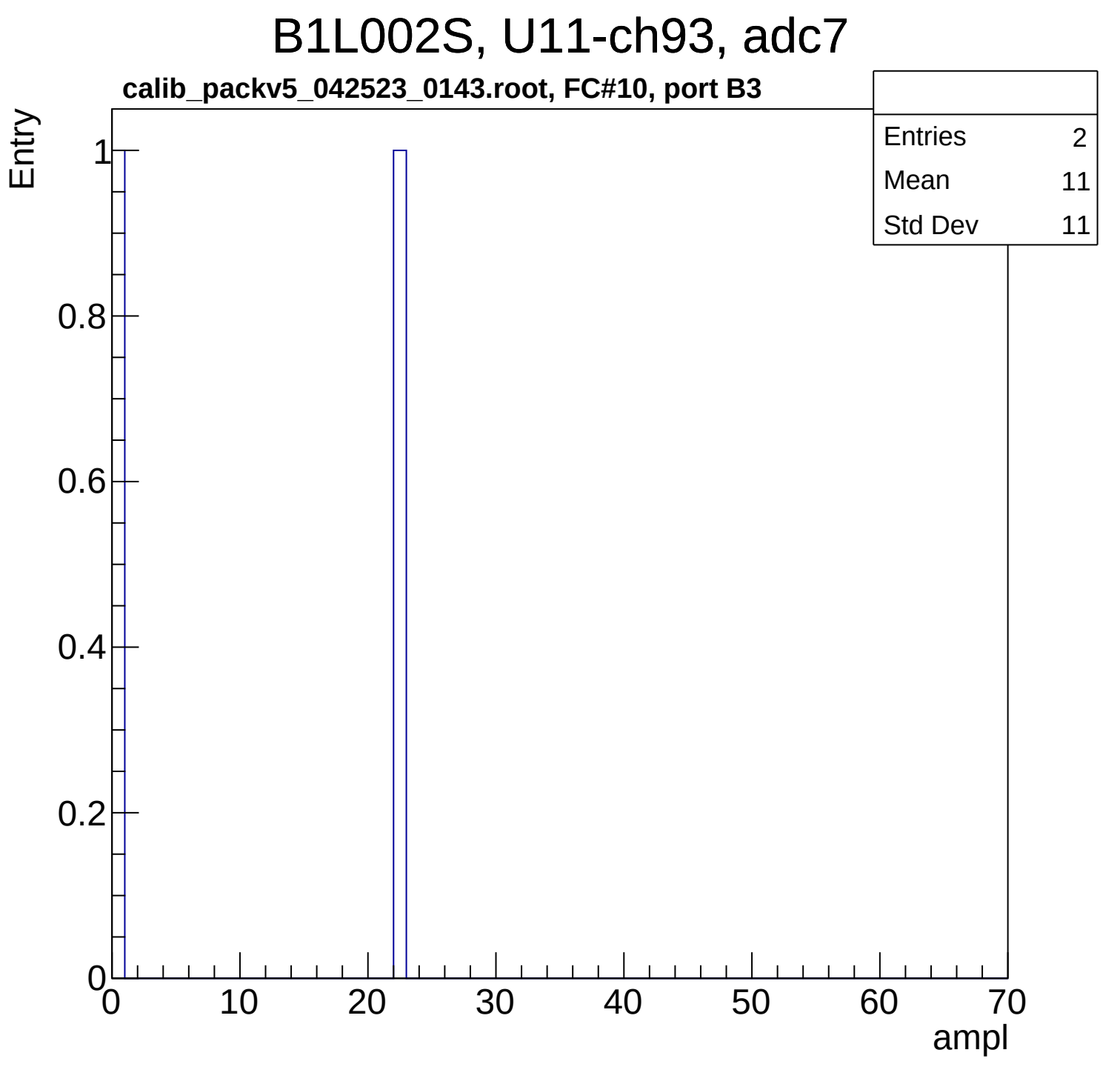
B1L002S, U11-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
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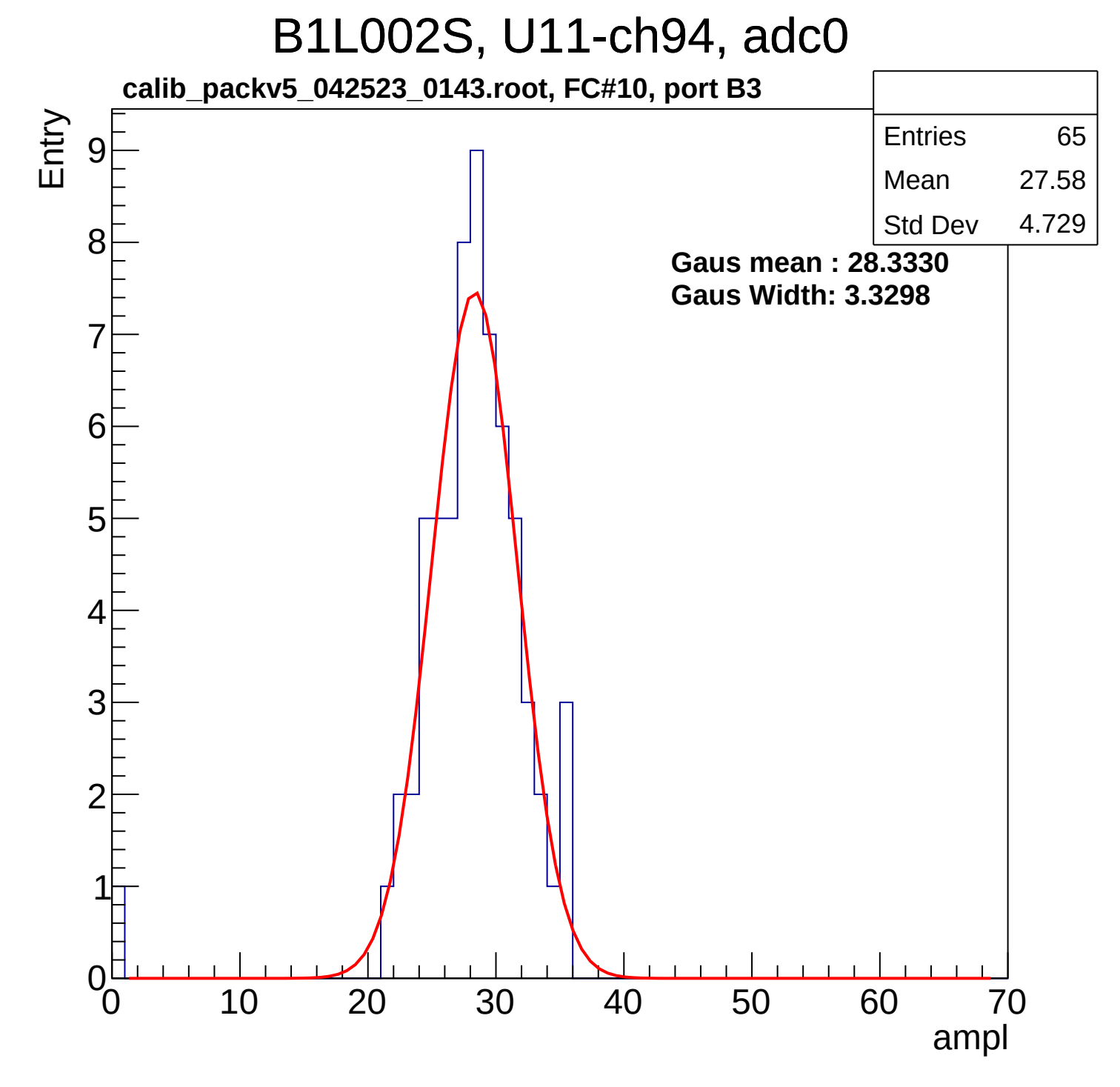
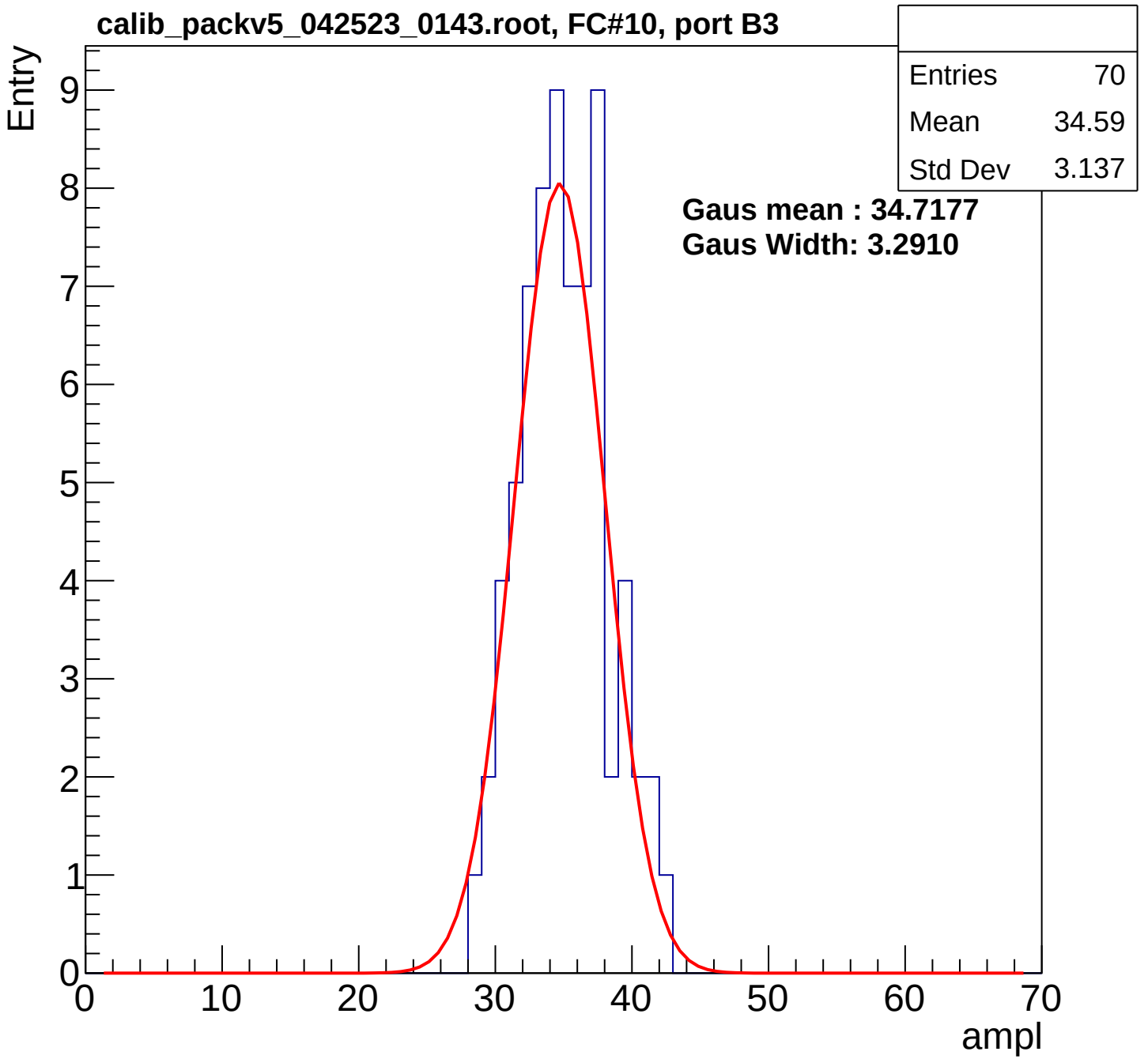
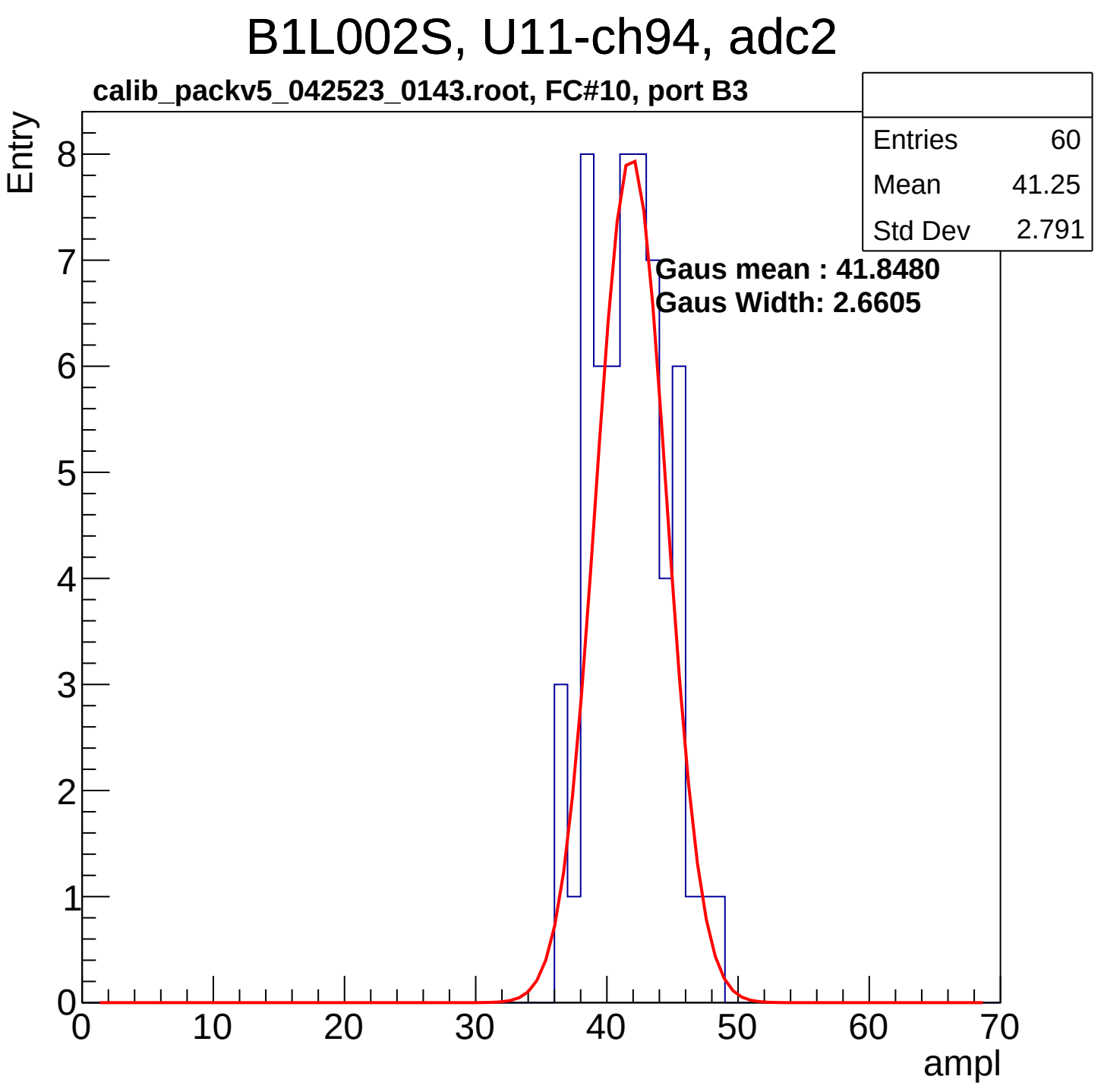
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.58
Std Dev 4.729
Gaus mean : 28.3330
Gaus Width: 3.3298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.59
Std Dev 3.137
Gaus mean : 34.7177
Gaus Width: 3.2910
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.25
Std Dev 2.791
Gaus mean : 41.8480
Gaus Width: 2.6605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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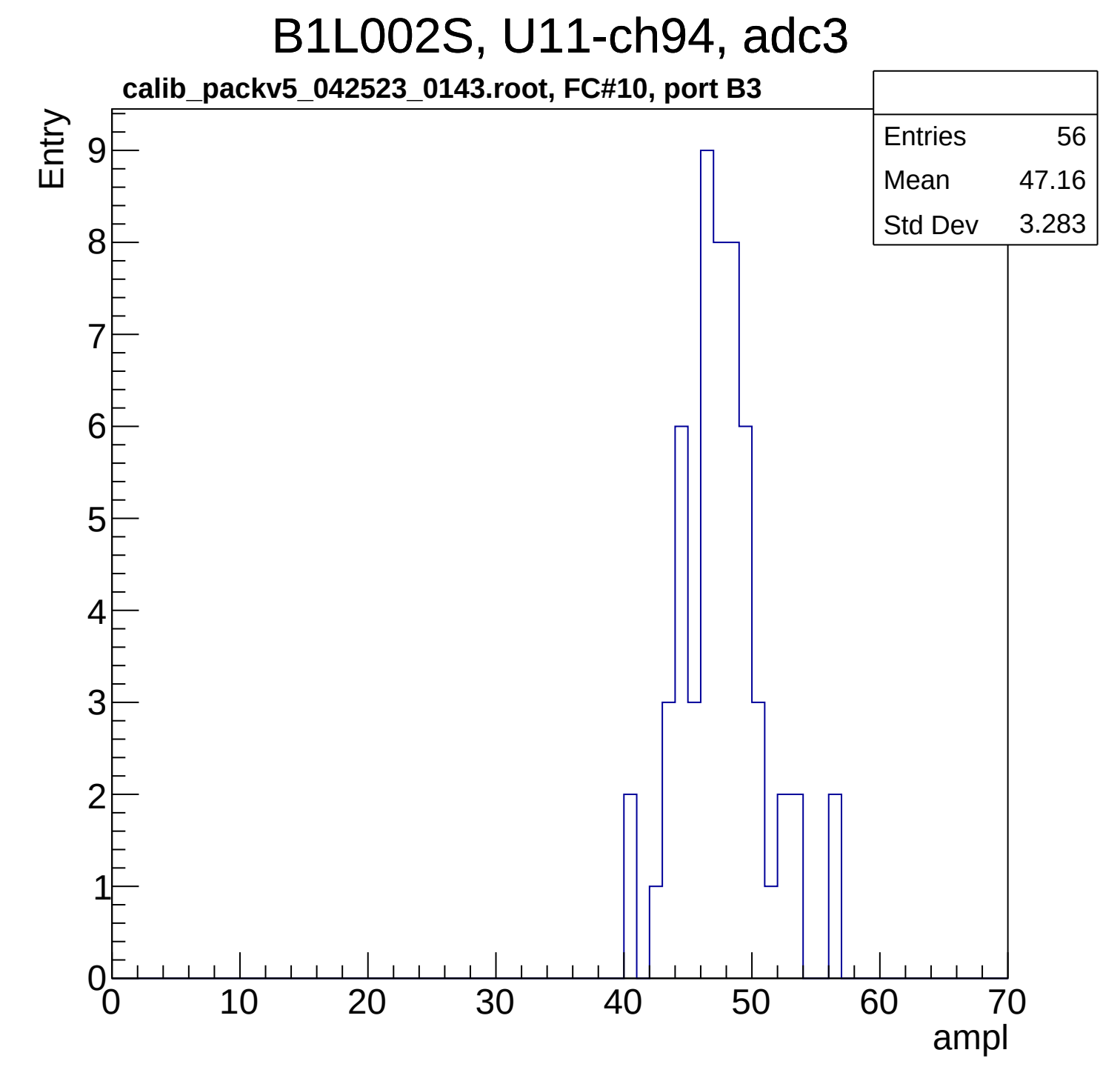
B1L002S, U11-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.16
Std Dev 3.283
Entry
ampl
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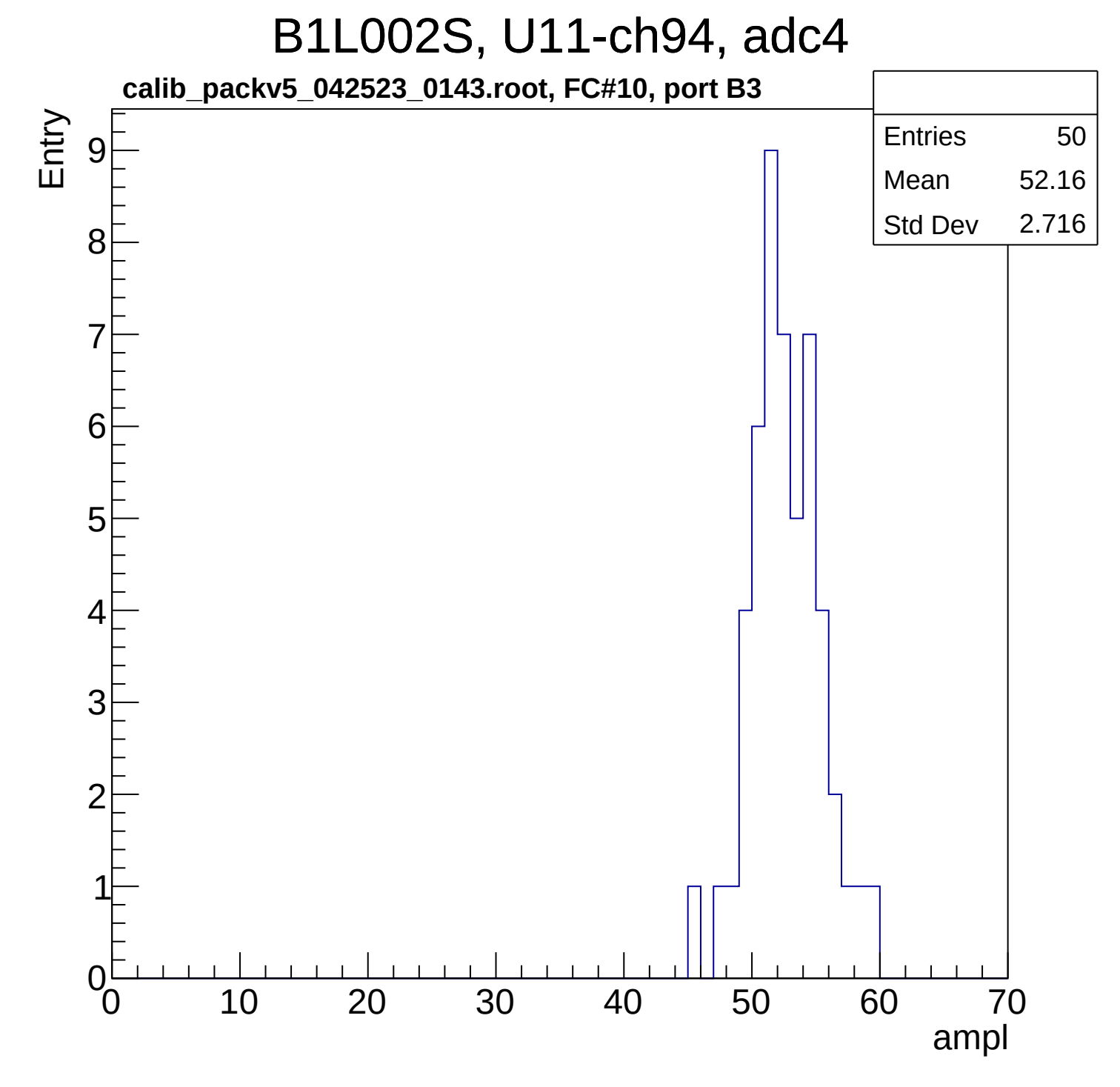
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.16
Std Dev 2.716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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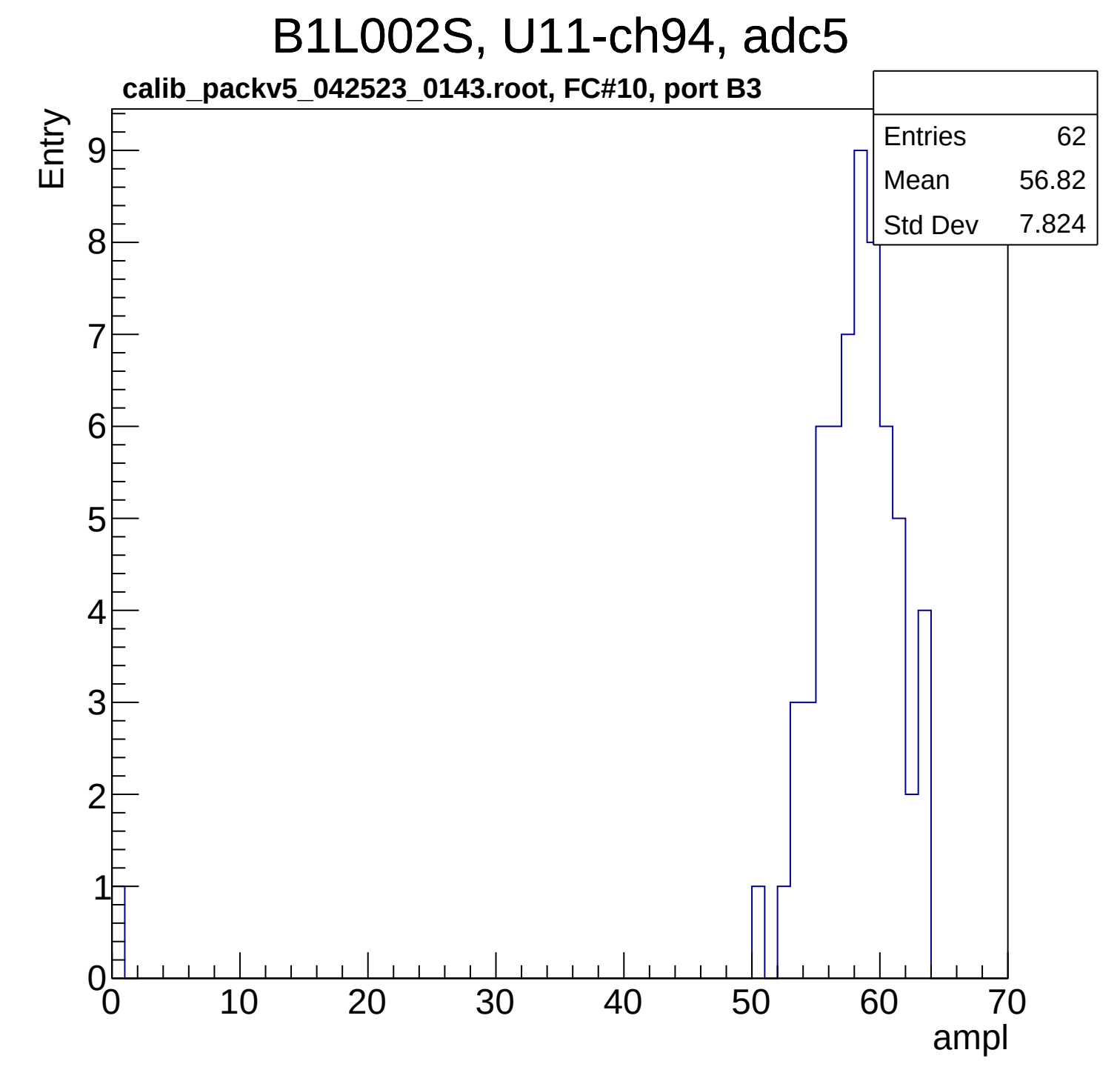
B1L002S, U11-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.82
Std Dev 7.824
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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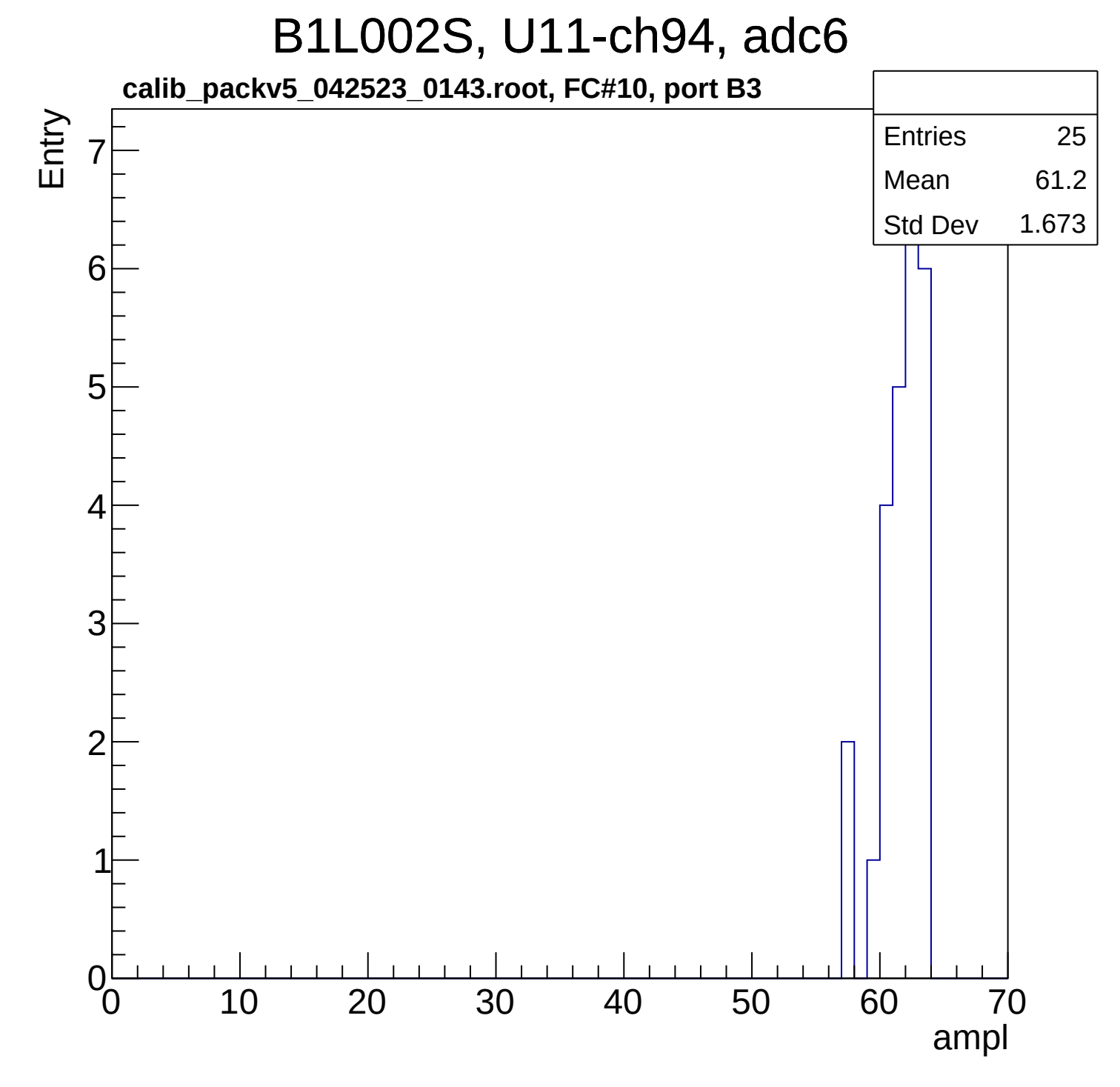
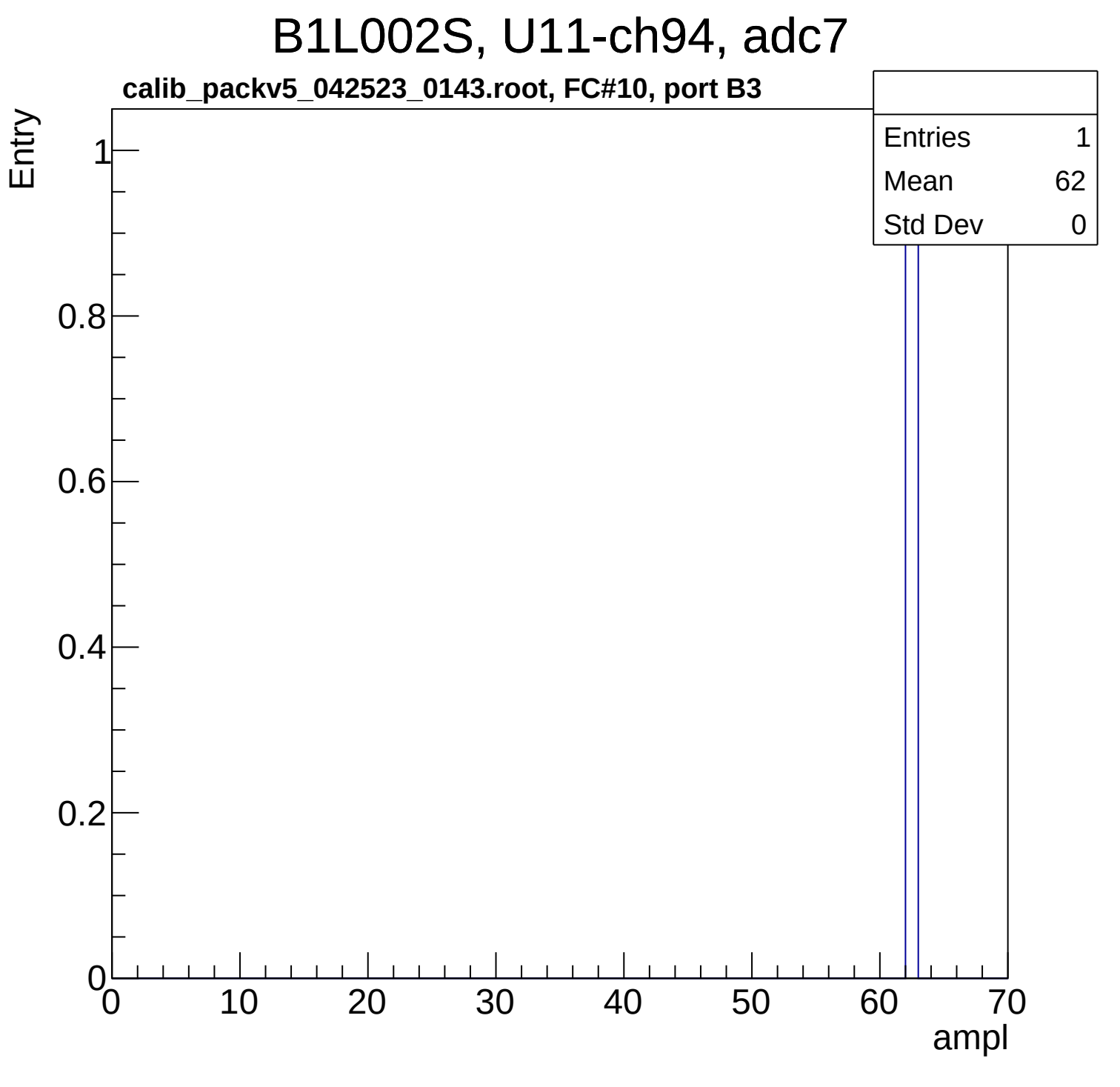

B1L002S, U11-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 61.2
Std Dev 1.673
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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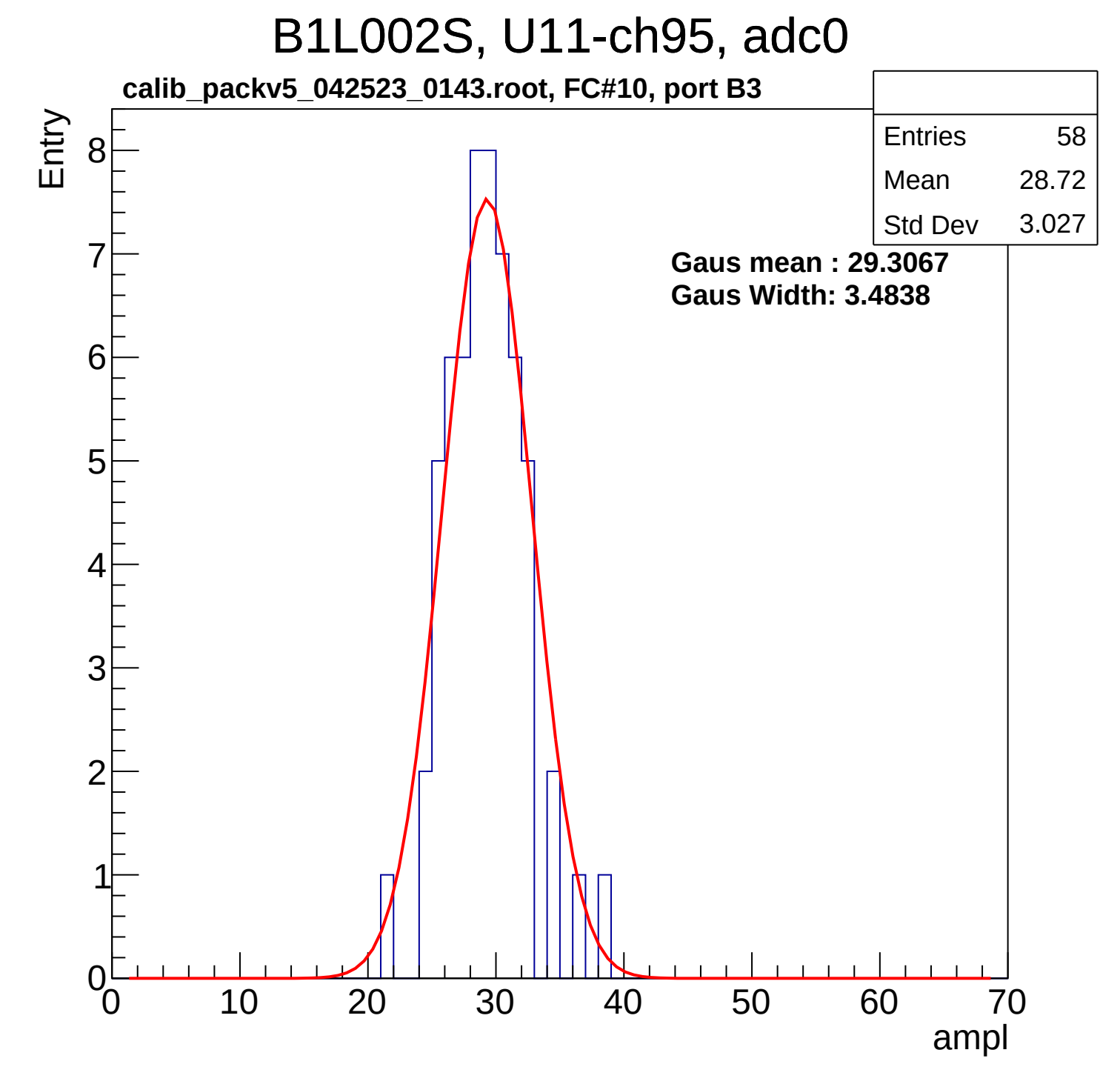
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28.72
Std Dev 3.027
Gaus mean : 29.3067
Gaus Width: 3.4838
Entry
ampl
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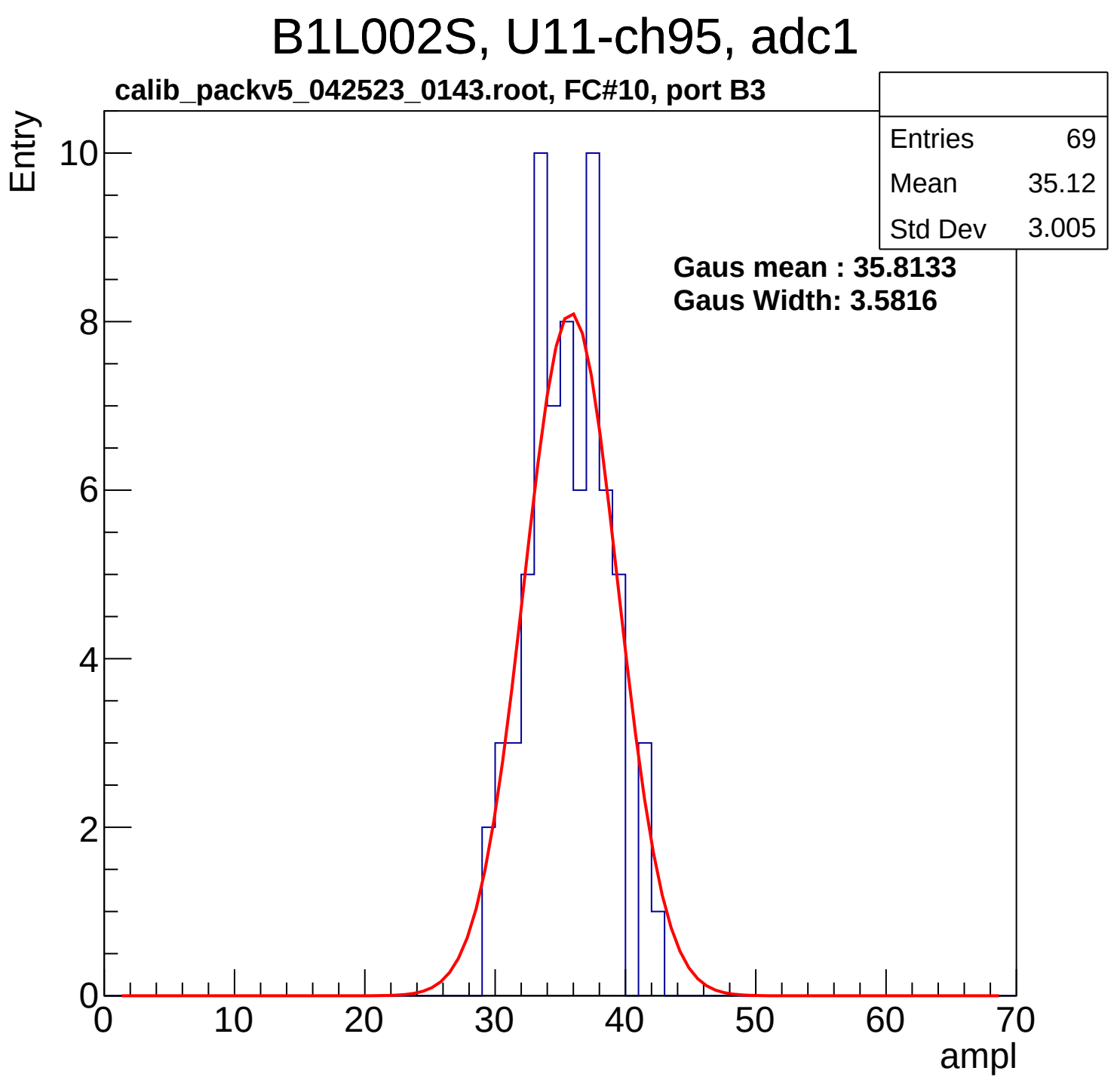
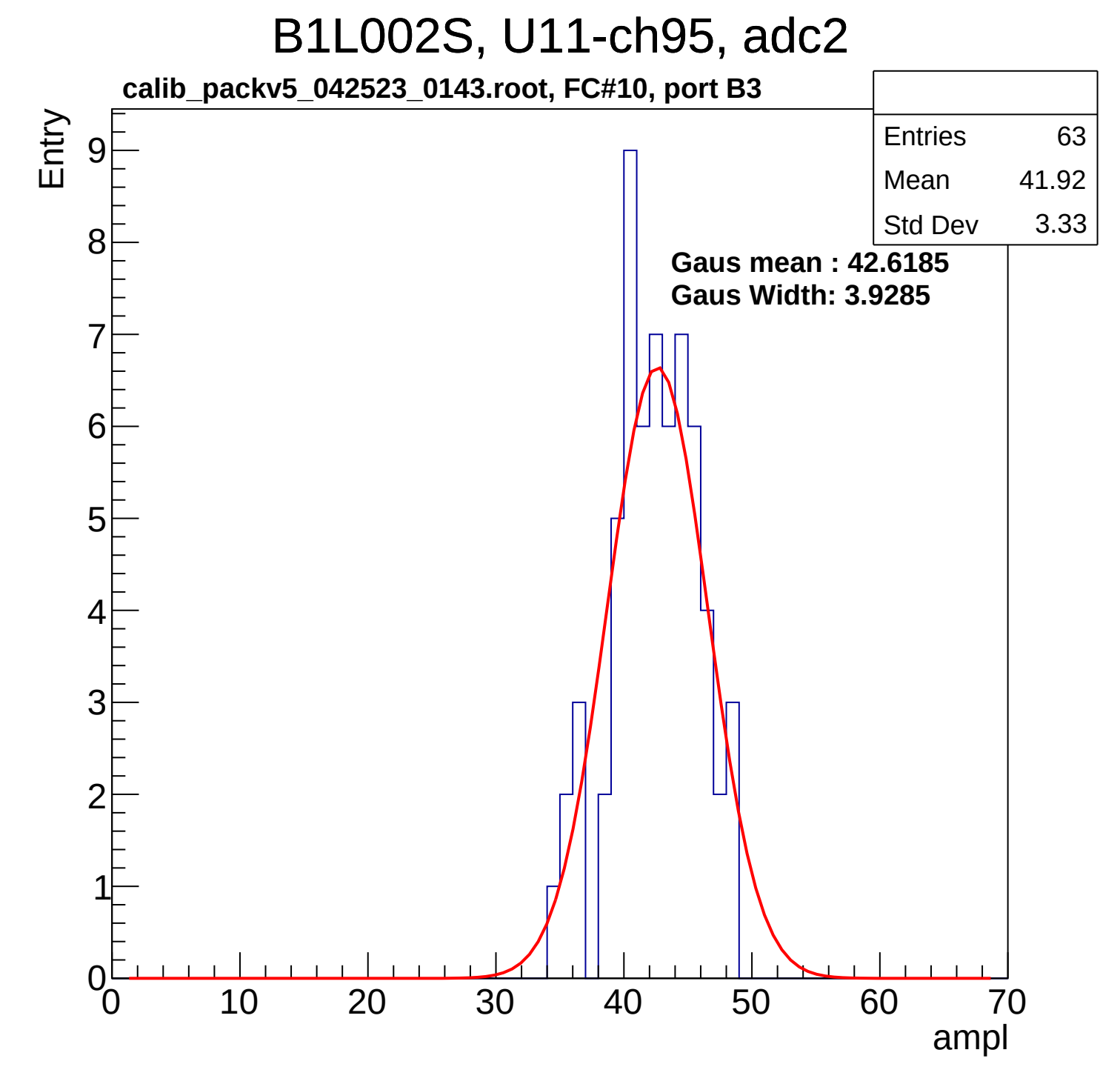
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.12
Std Dev 3.005
Gaus mean : 35.8133
Gaus Width: 3.5816
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.92
Std Dev 3.33
Gaus mean : 42.6185
Gaus Width: 3.9285
Entry
ampl
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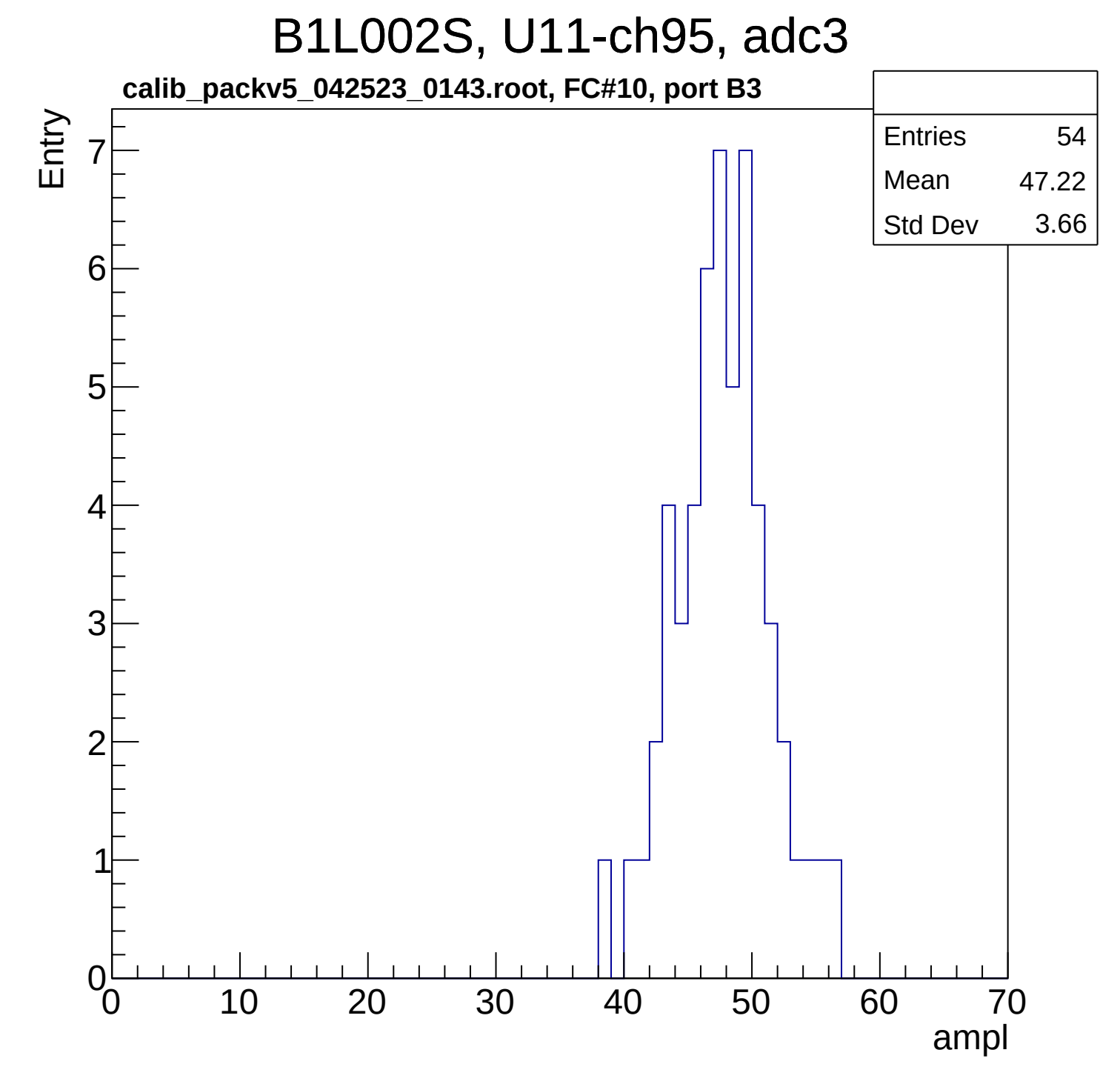
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.22
Std Dev 3.66
Entry
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3
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5
6
7
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10
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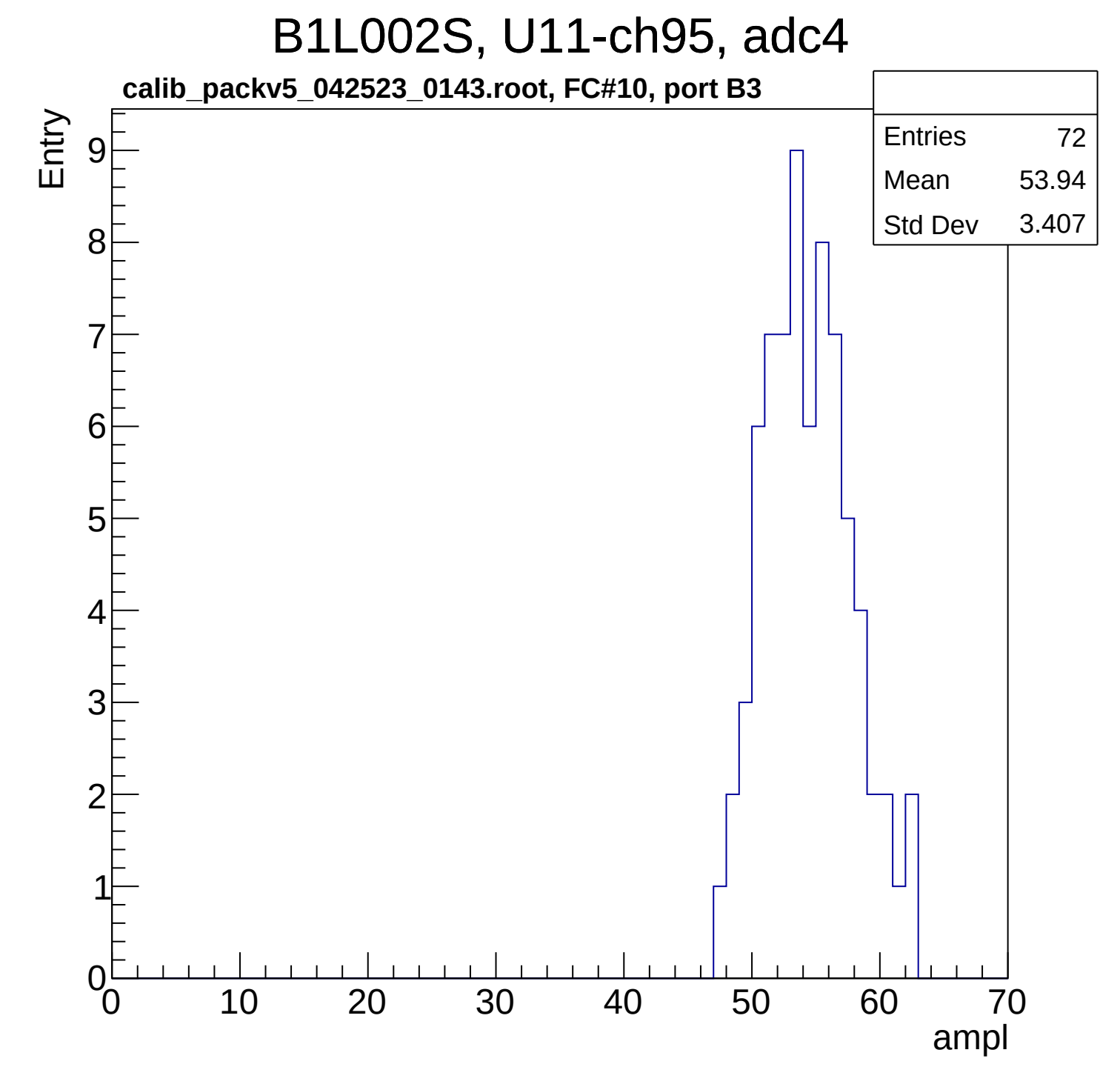
30
40
50
60
70
ampl


B1L002S, U11-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 53.94
Std Dev 3.407
Entry
ampl
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3
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5
6
7
8
9
0
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60
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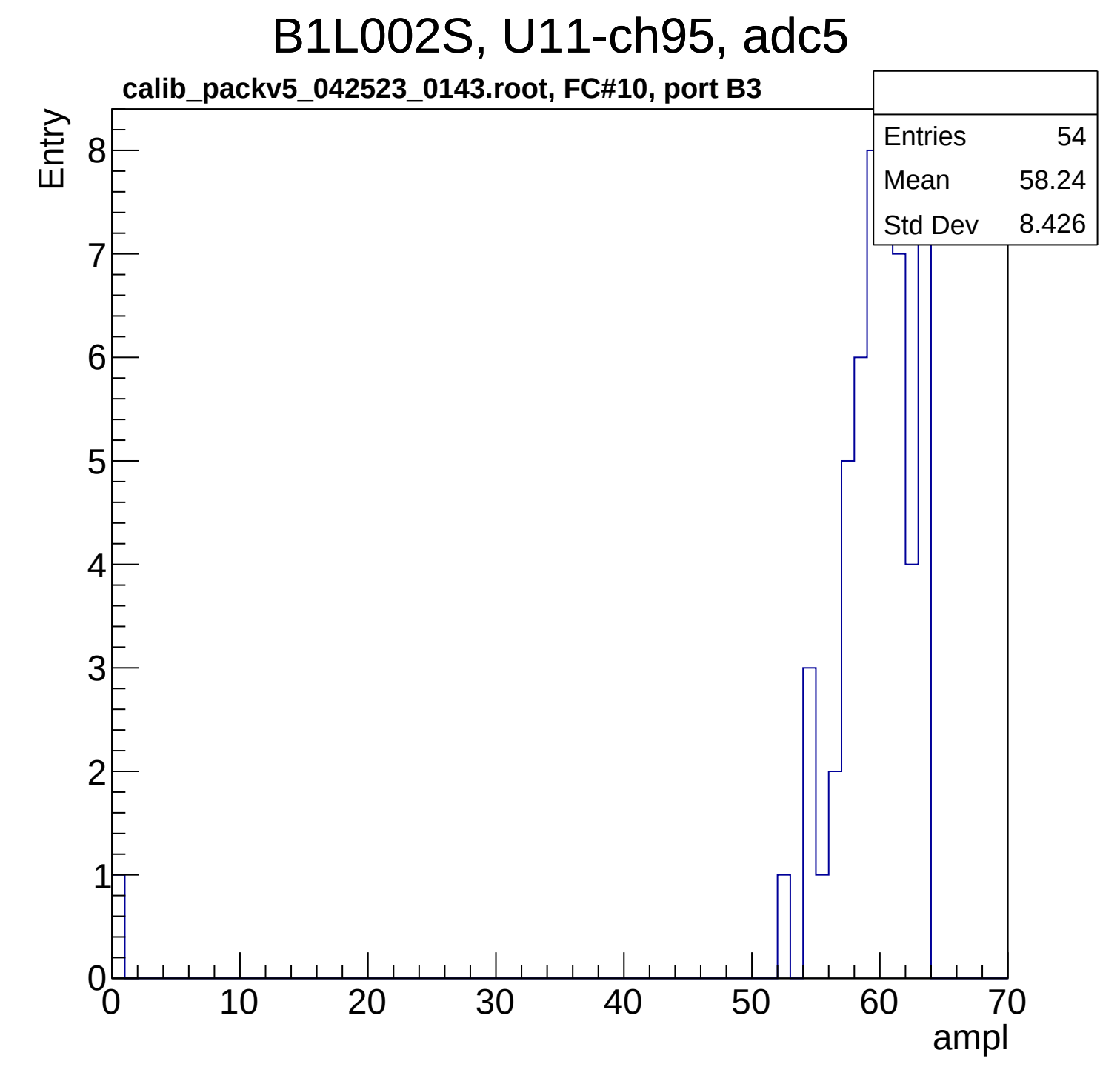
B1L002S, U11-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.24
Std Dev 8.426
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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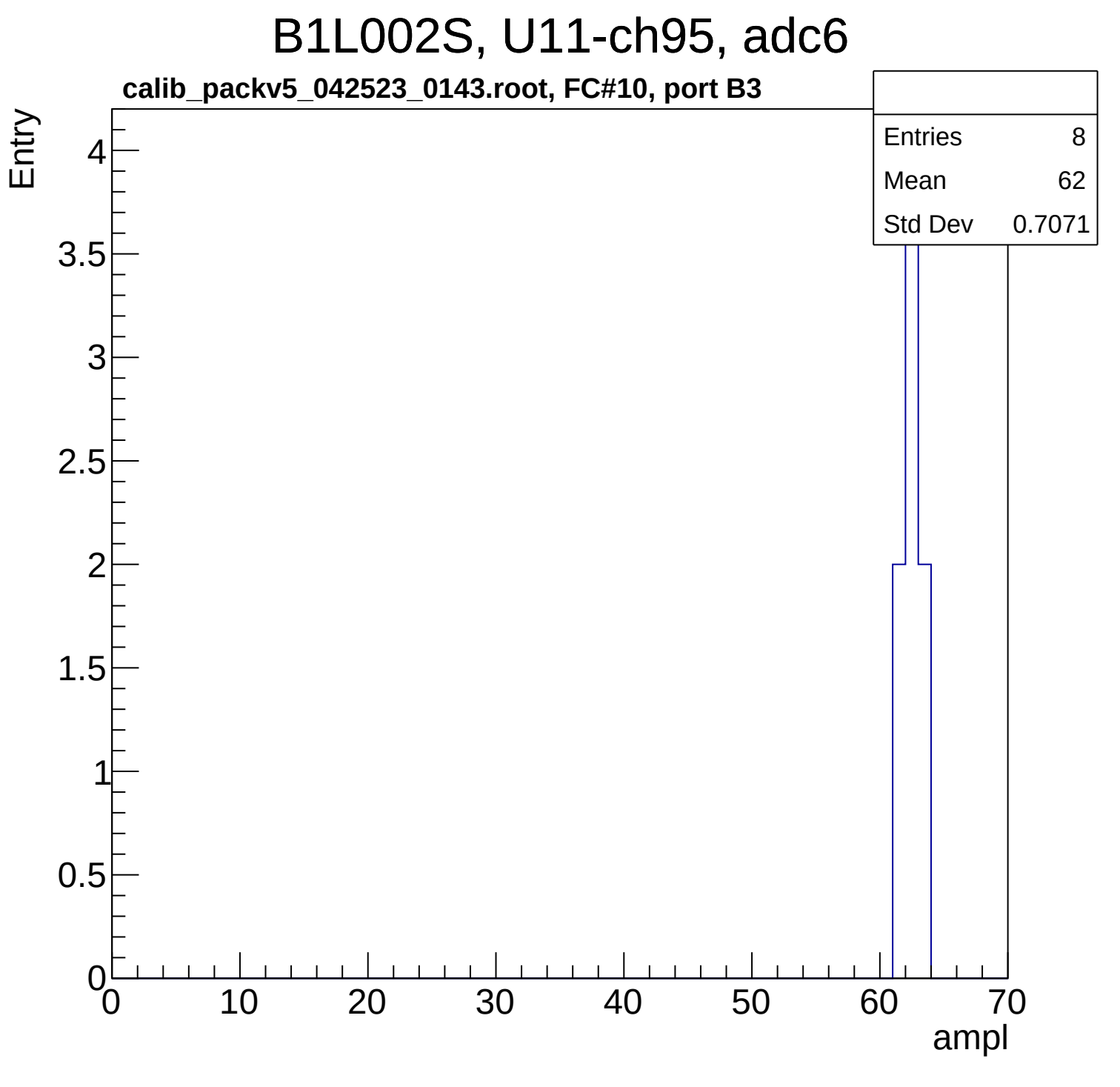
B1L002S, U11-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62
Std Dev 0.7071
Entry
ampl
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1.5
2
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3
3.5
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0
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60
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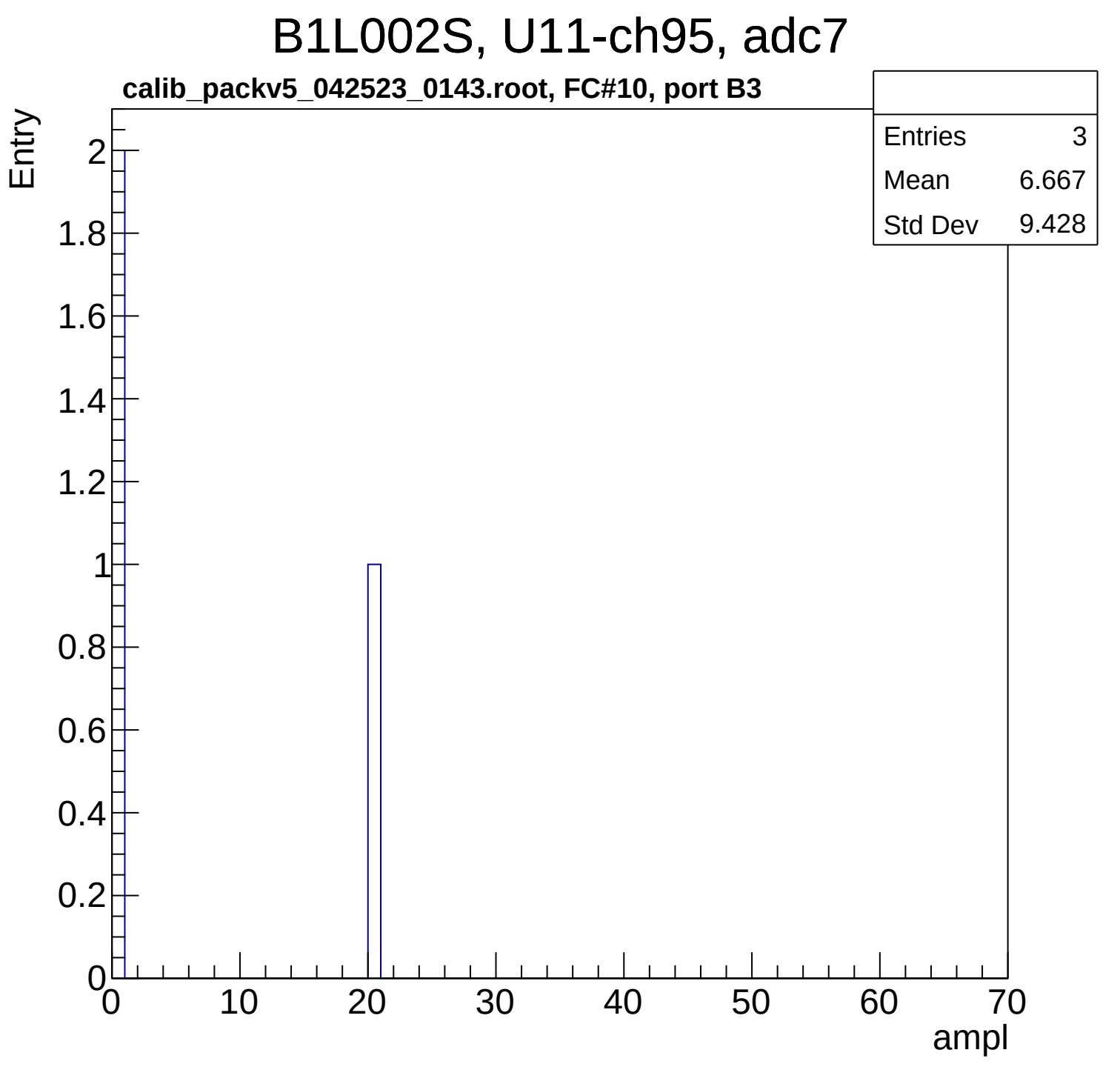
B1L002S, U11-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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60
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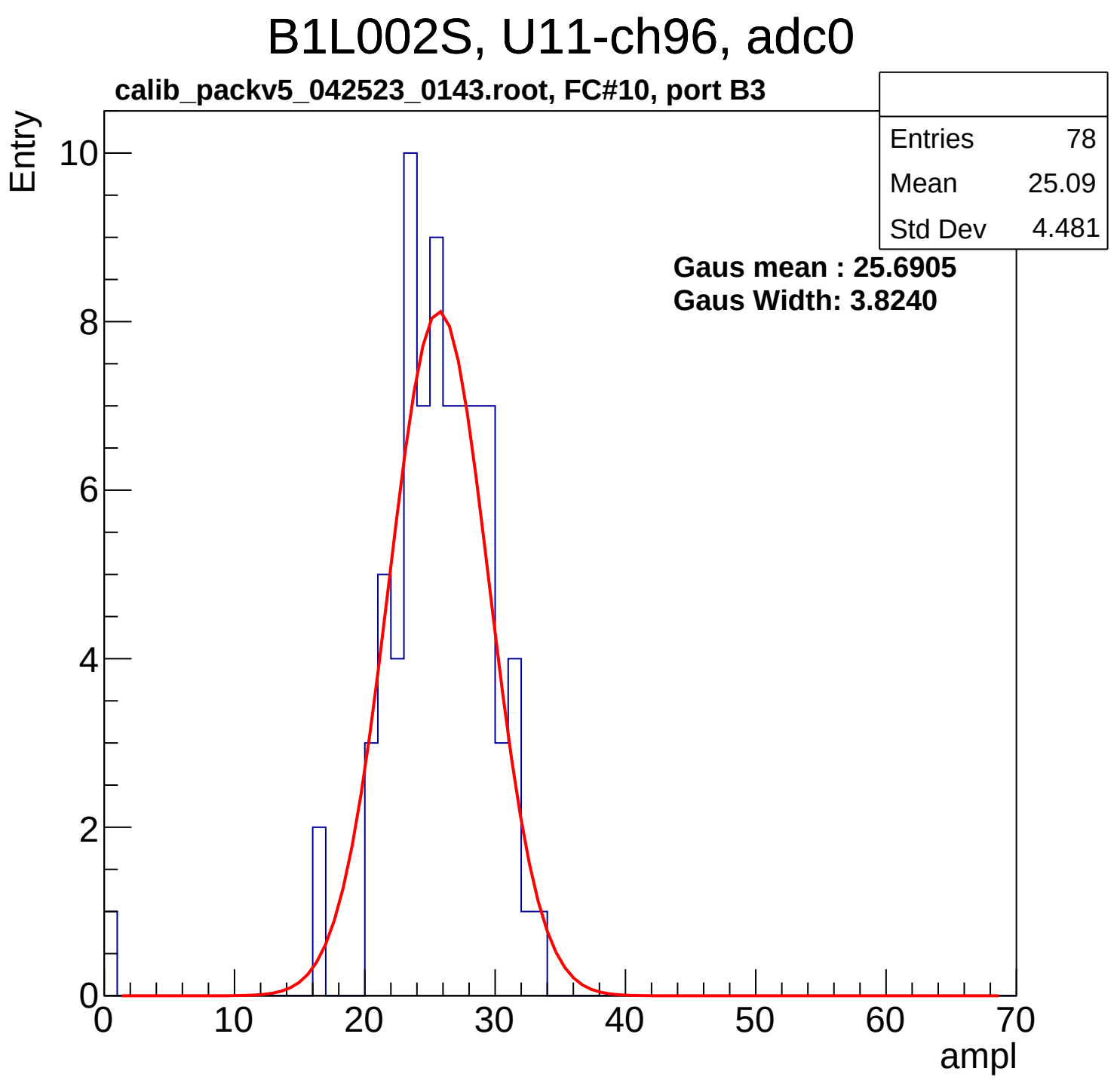
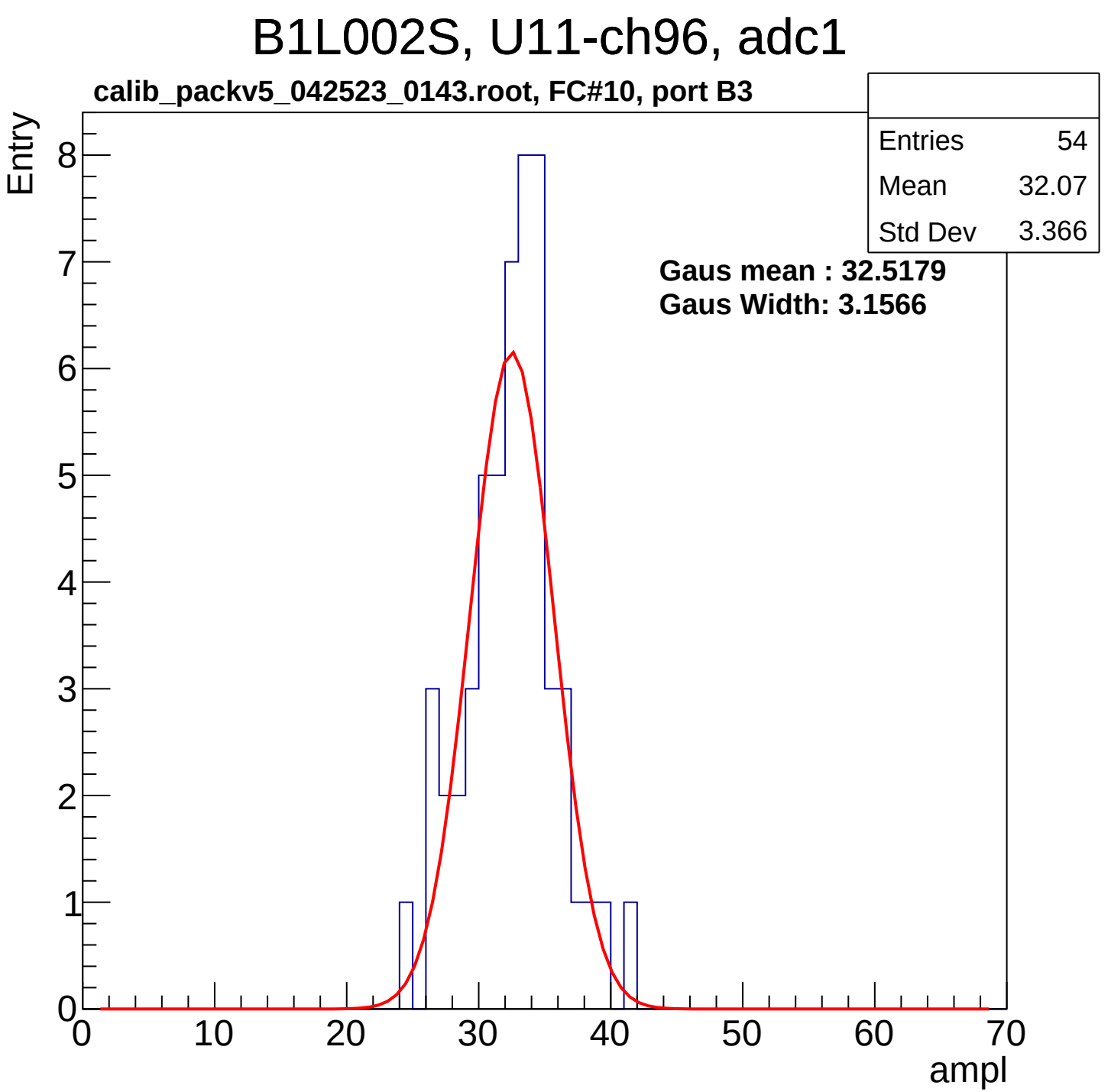

B1L002S, U11-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 25.09
Std Dev 4.481
Gaus mean : 25.6905
Gaus Width: 3.8240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 32.07
Std Dev 3.366
Gaus mean : 32.5179
Gaus Width: 3.1566
Entry
ampl
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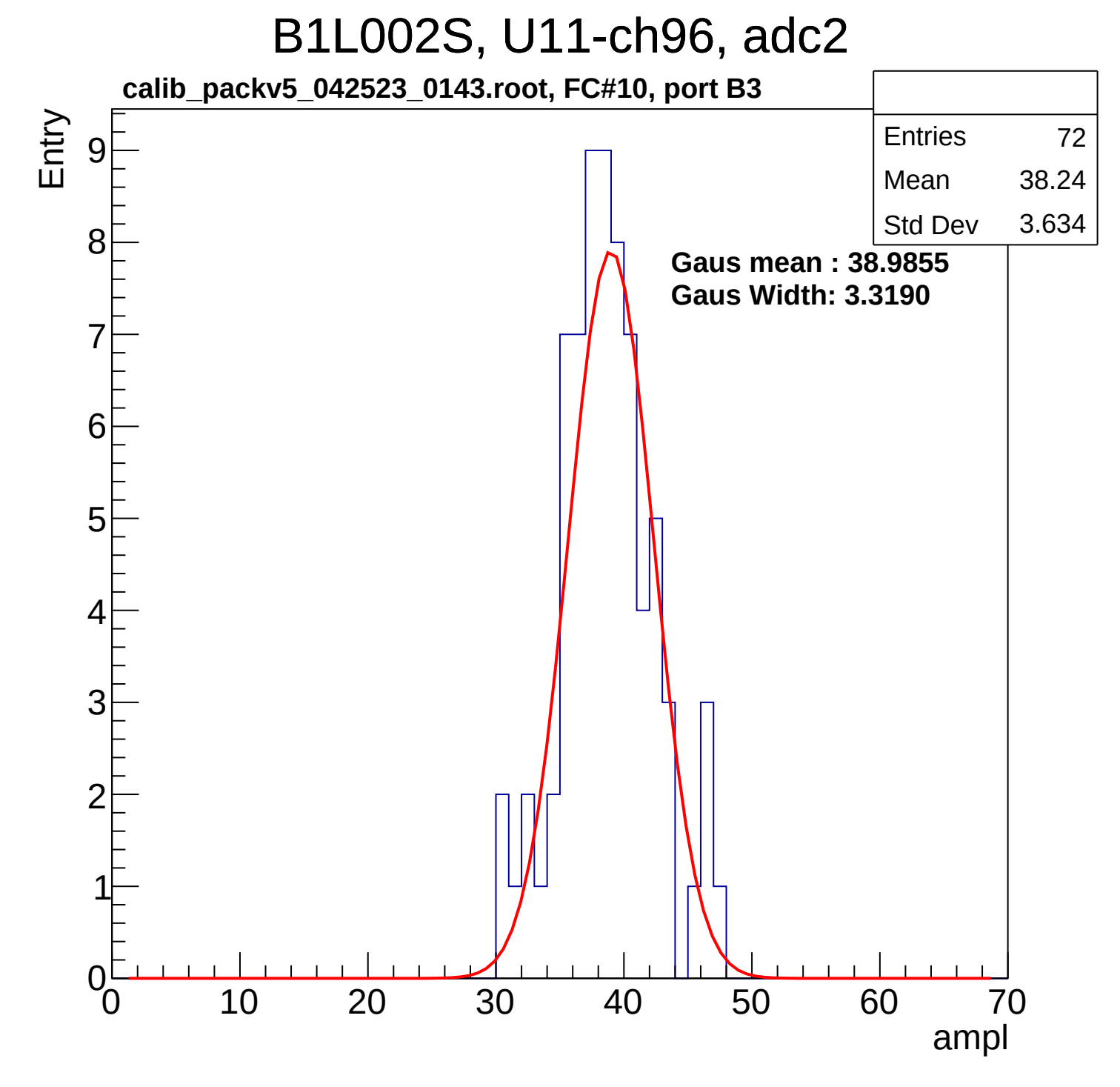
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 38.24
Std Dev 3.634
Gaus mean : 38.9855
Gaus Width: 3.3190
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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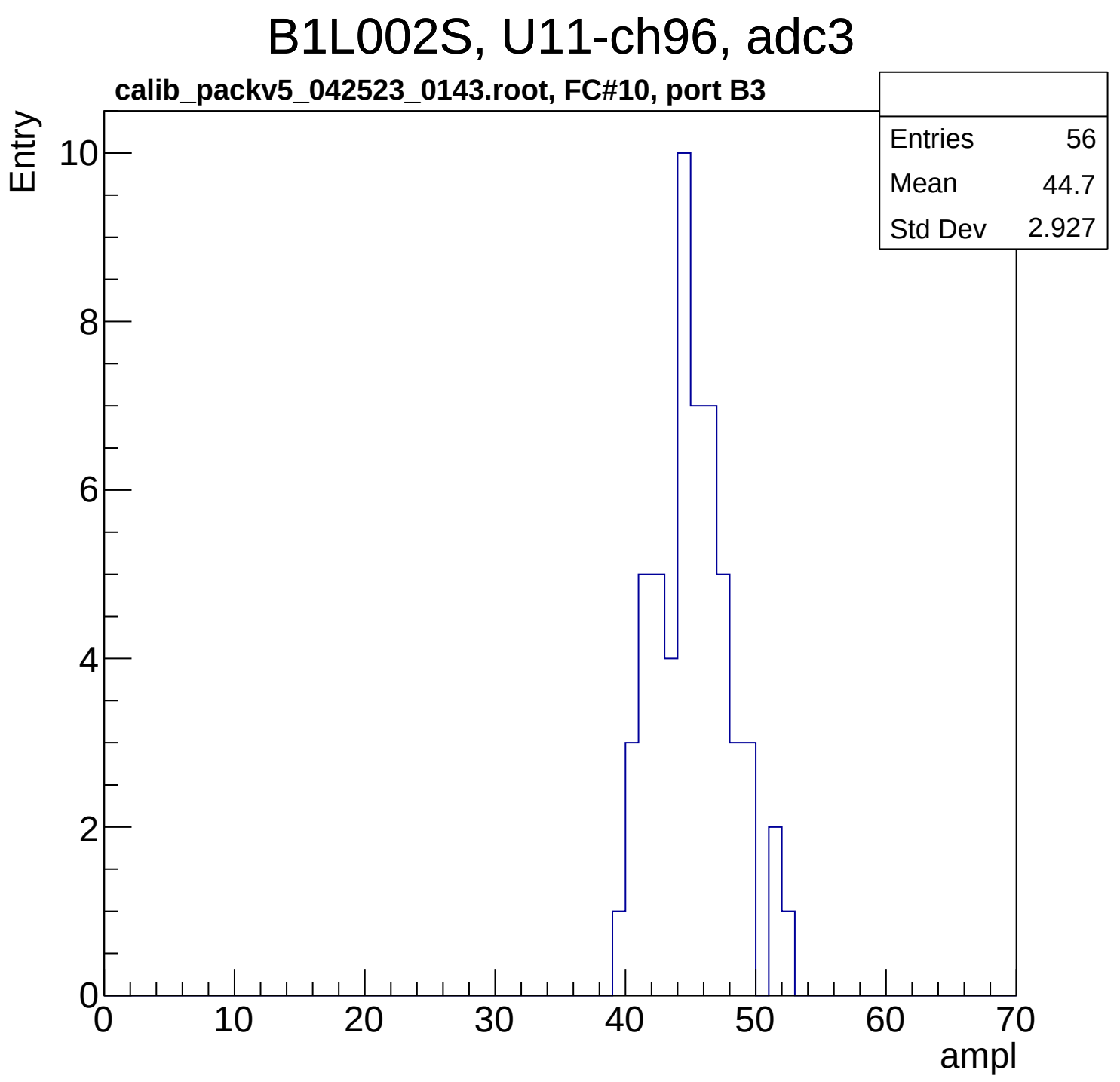
B1L002S, U11-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 44.7
Std Dev 2.927
Entry
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2
4
6
8
10
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10
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30
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50
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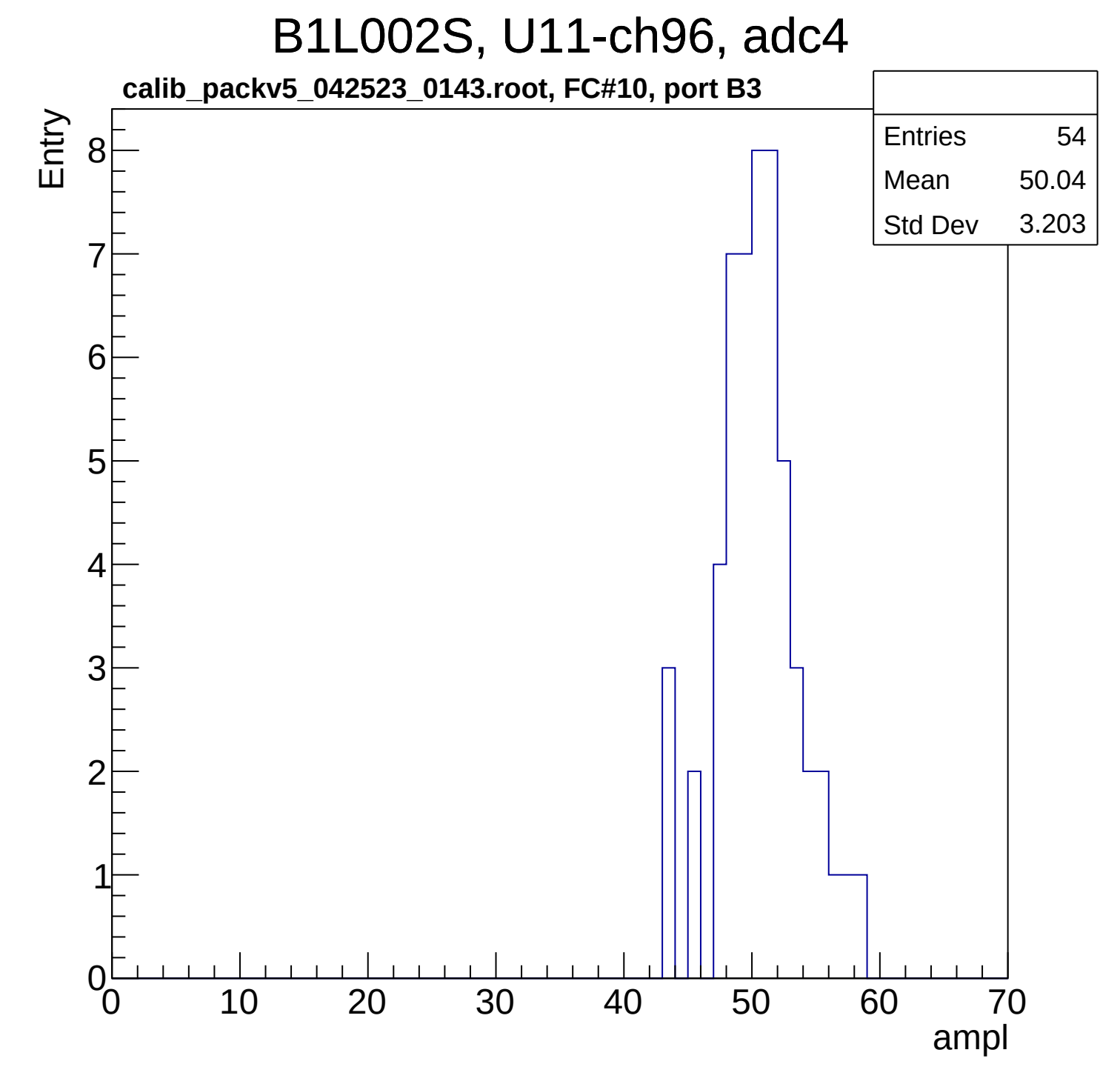
70
ampl


B1L002S, U11-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 50.04
Std Dev 3.203
Entry
ampl
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2
3
4
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50
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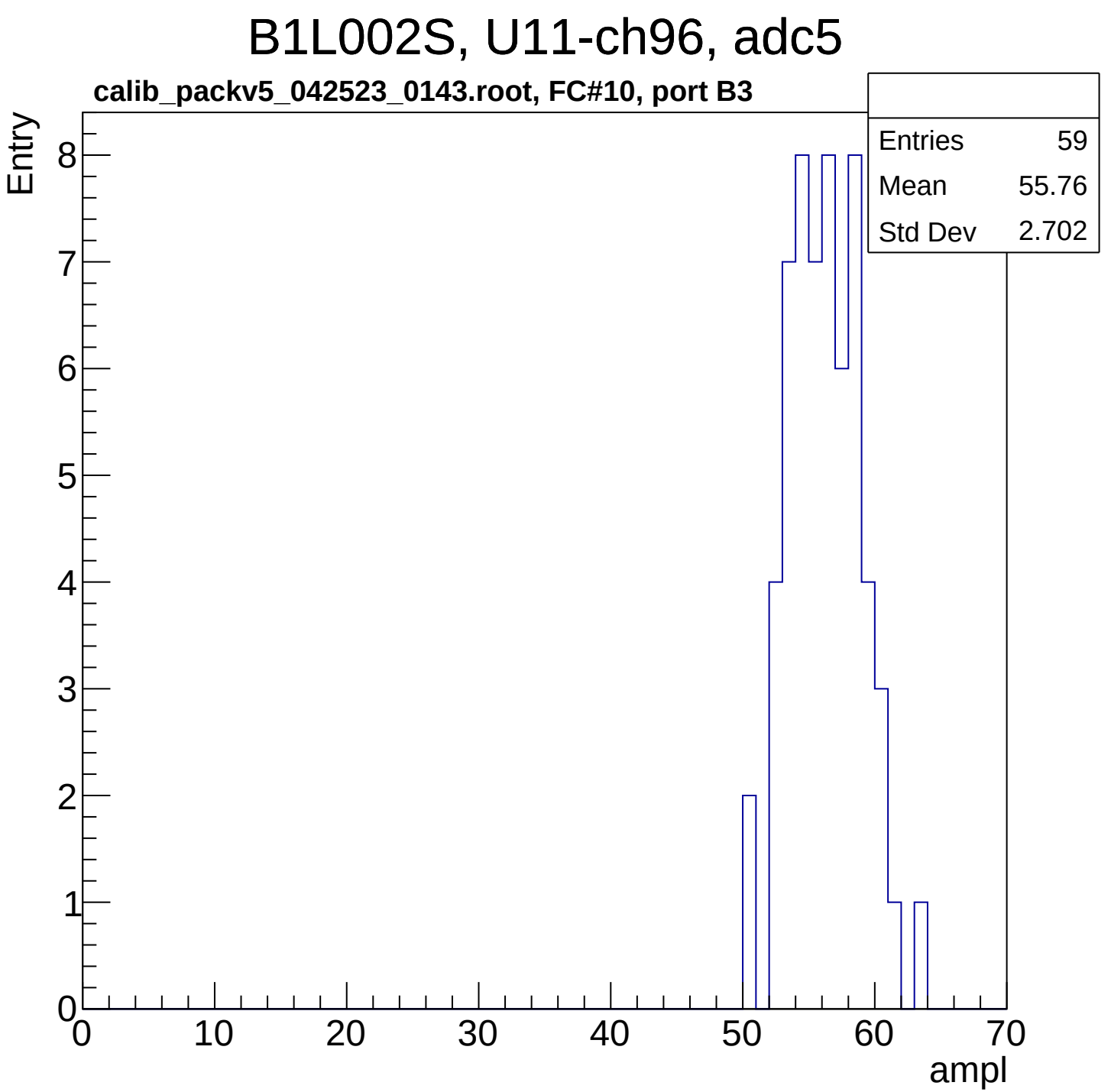
B1L002S, U11-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 55.76
Std Dev 2.702
Entry
ampl
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7
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0
10
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60
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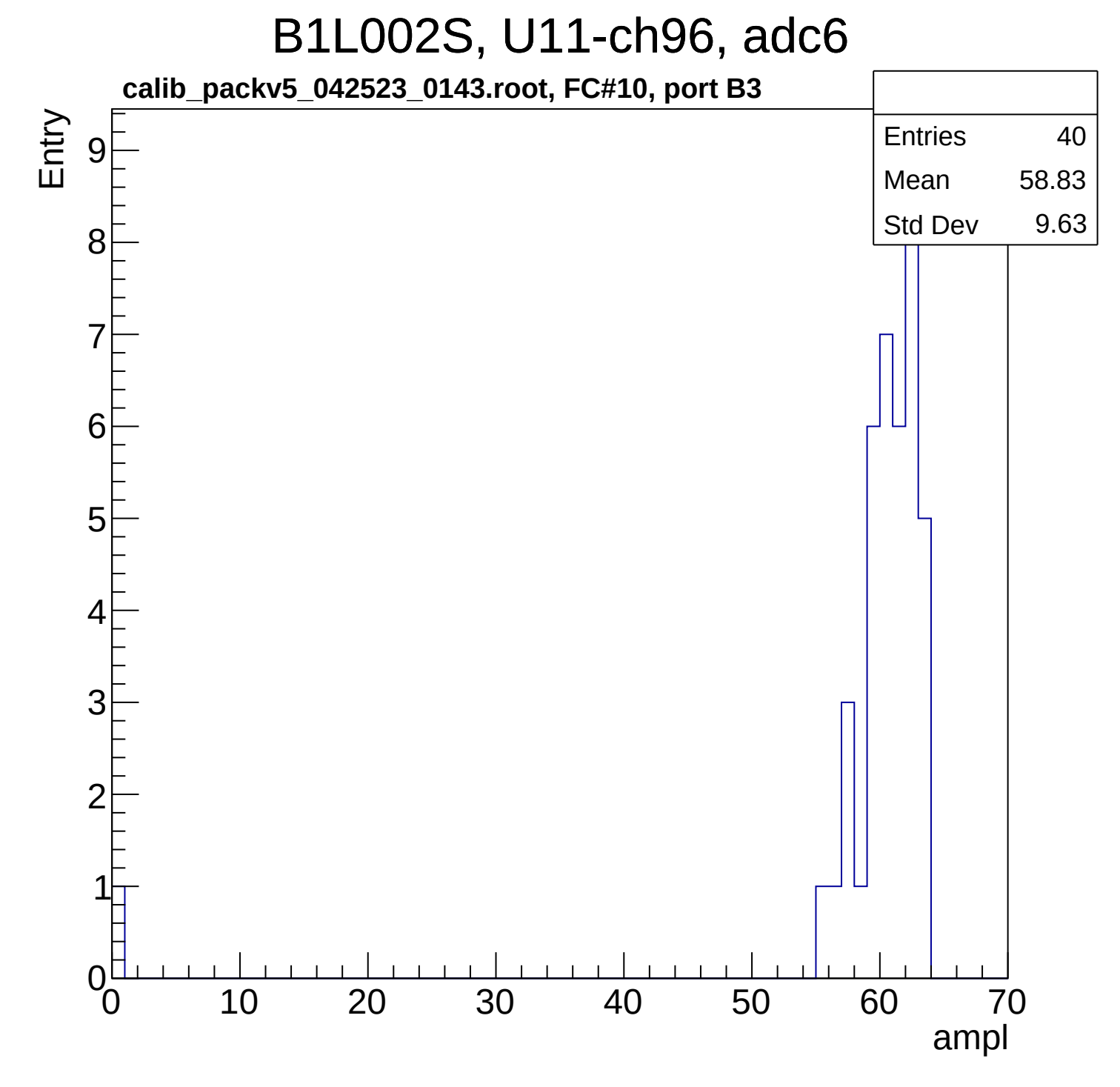
B1L002S, U11-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 58.83
Std Dev 9.63
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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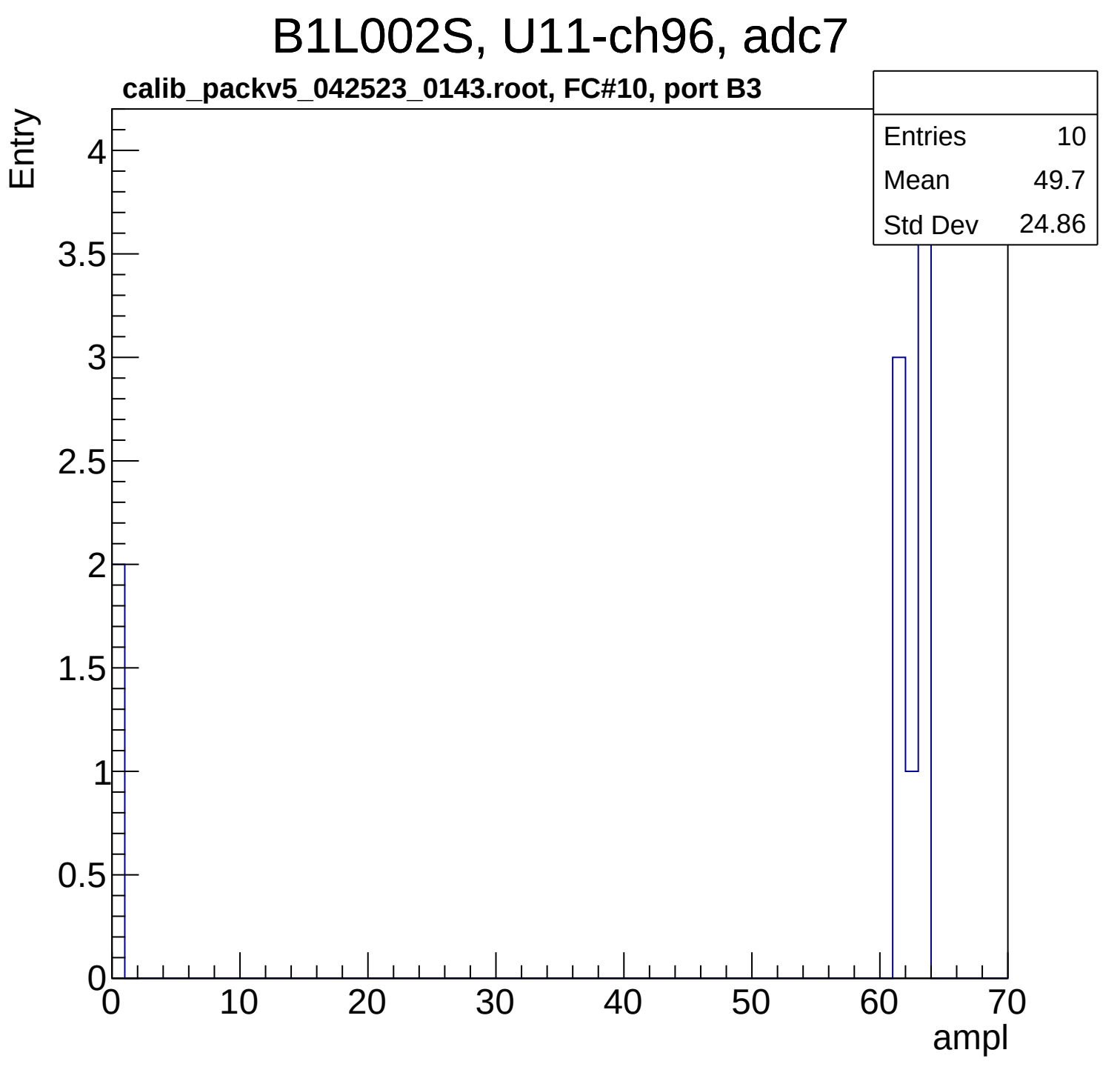
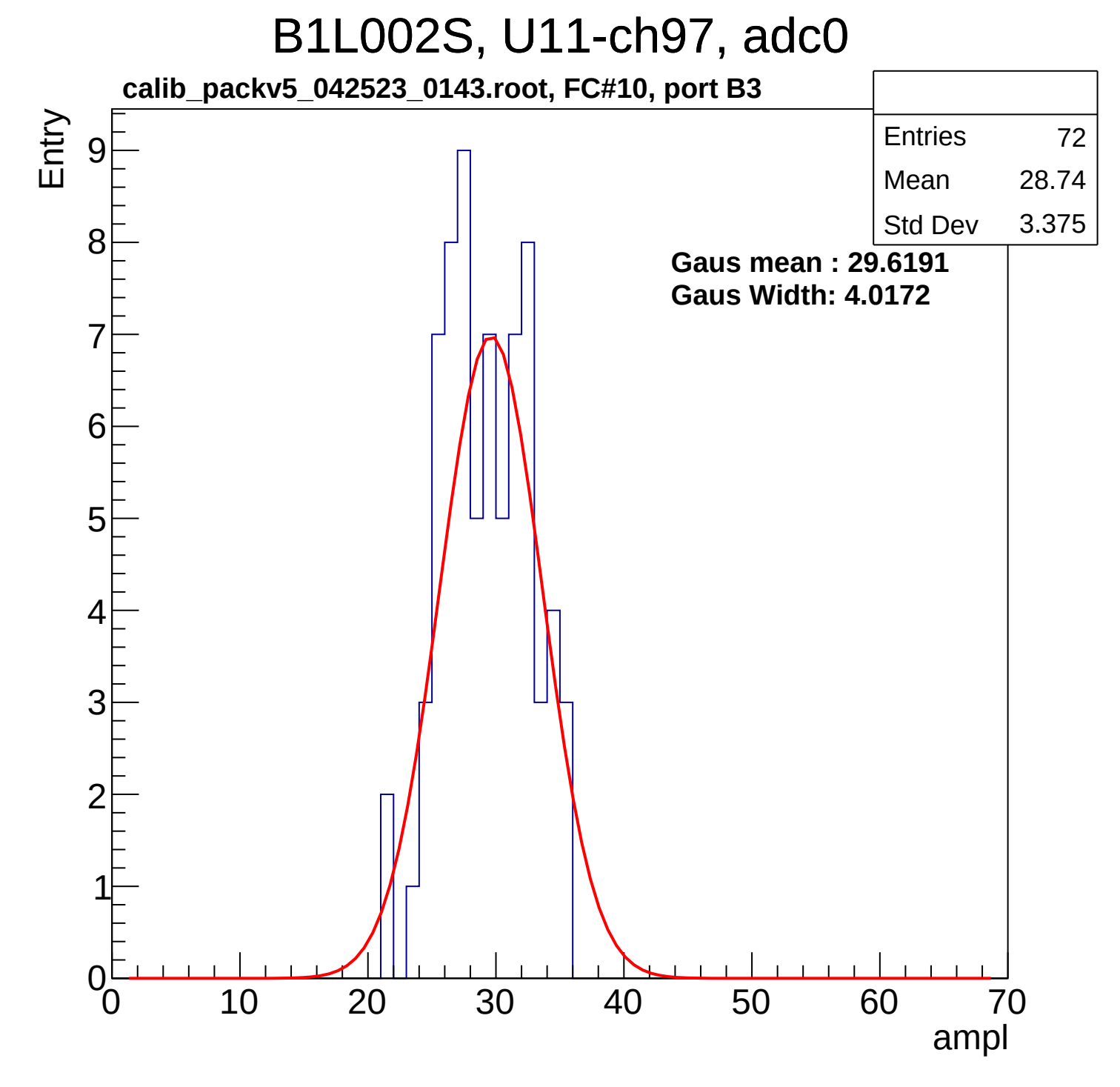
40
50
60
70


B1L002S, U11-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 49.7
Std Dev 24.86
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.74
Std Dev 3.375
Gaus mean : 29.6191
Gaus Width: 4.0172
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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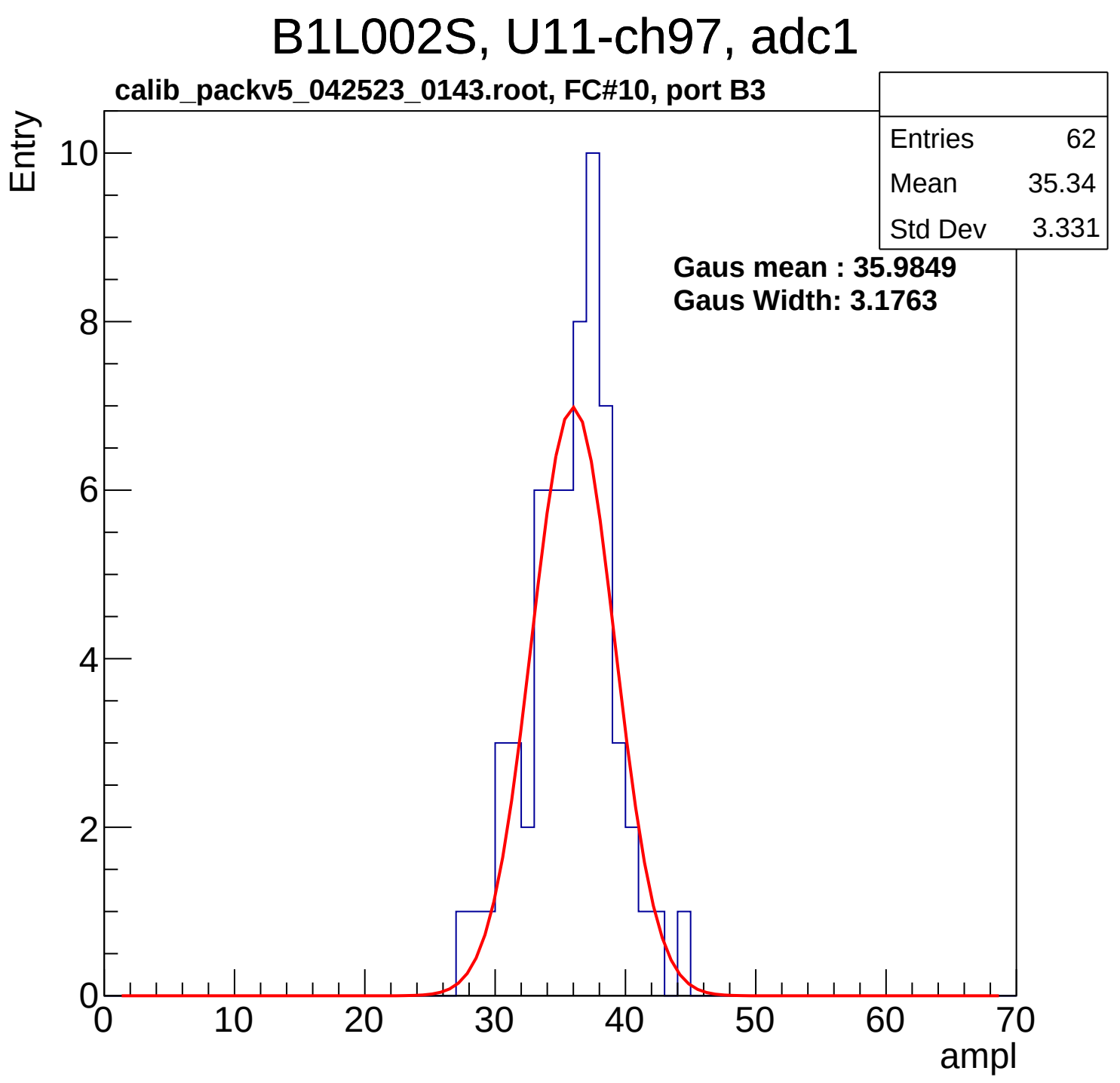
B1L002S, U11-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.34
Std Dev 3.331
Gaus mean : 35.9849
Gaus Width: 3.1763
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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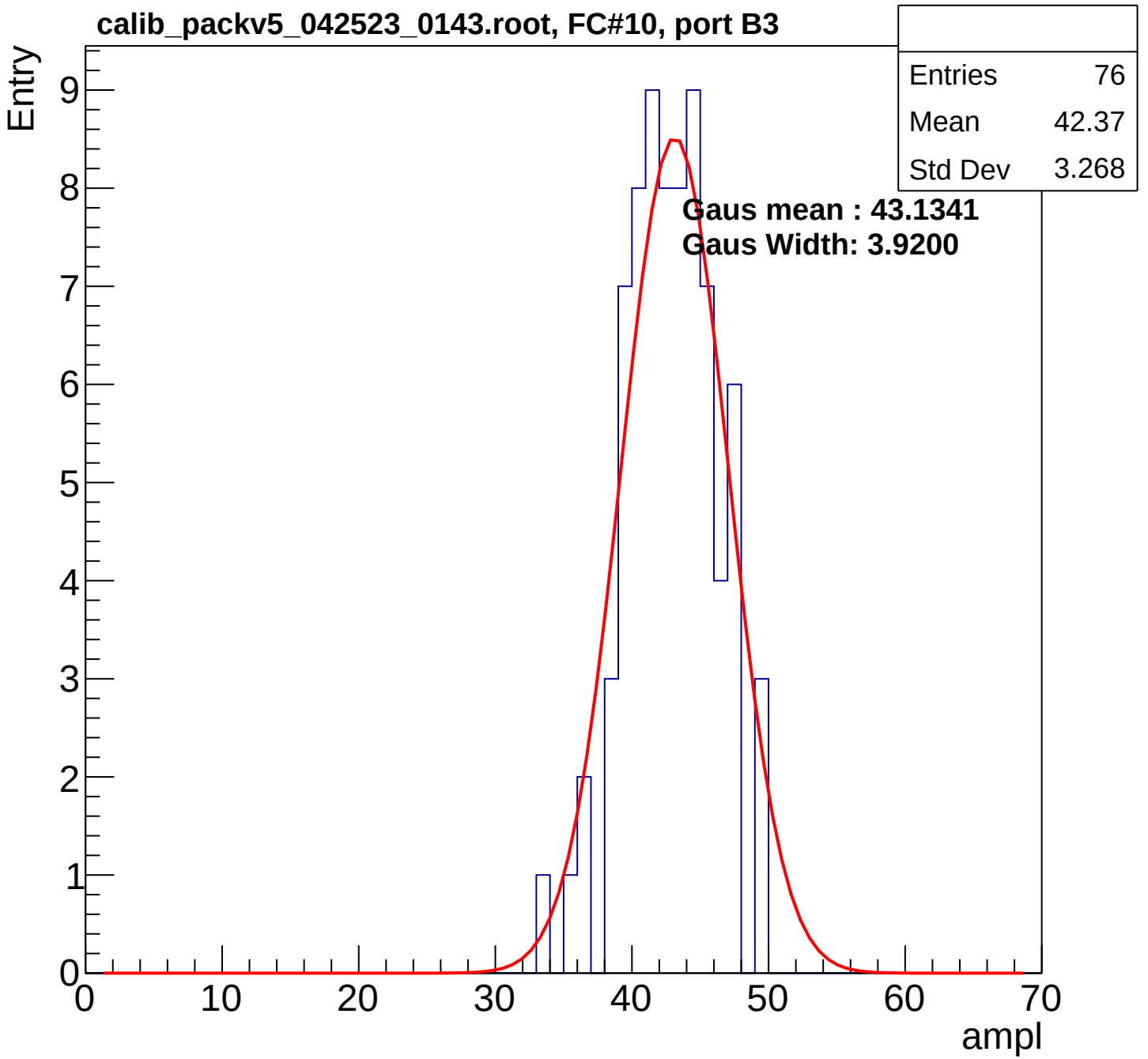
B1L002S, U11-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 42.37
Std Dev 3.268
Gaus mean : 43.1341
Gaus Width: 3.9200
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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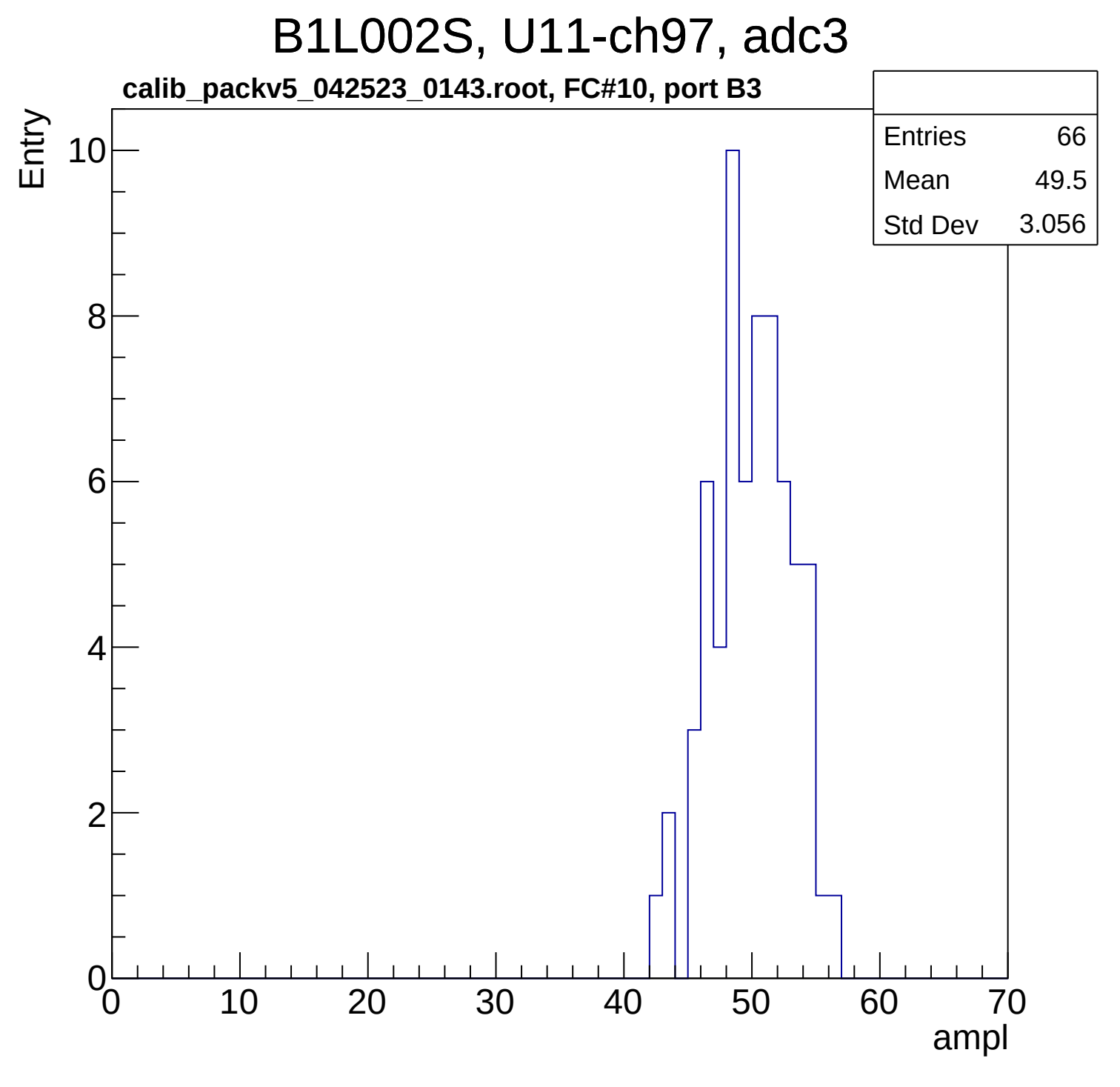
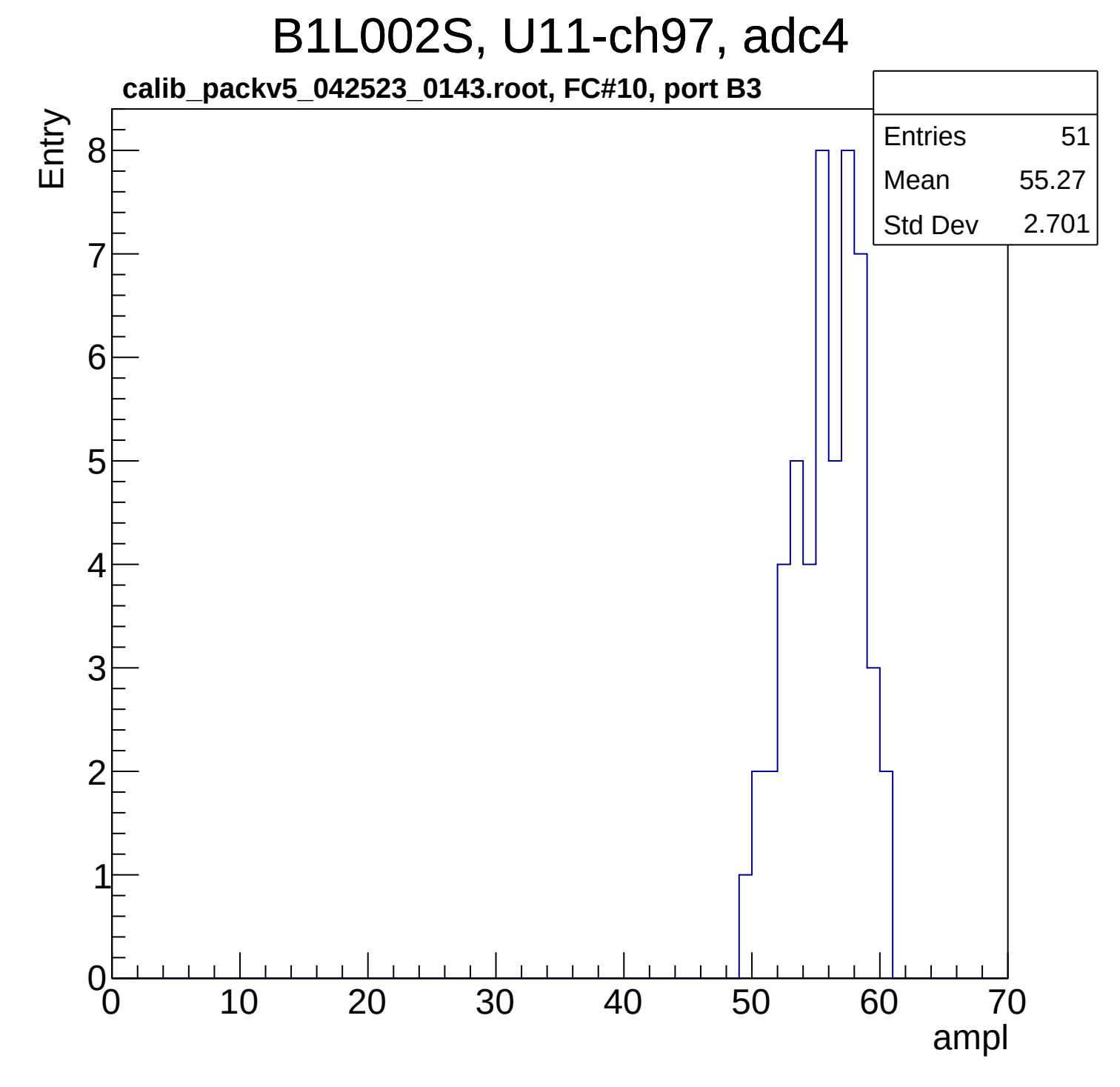

B1L002S, U11-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 49.5
Std Dev 3.056
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 55.27
Std Dev 2.701
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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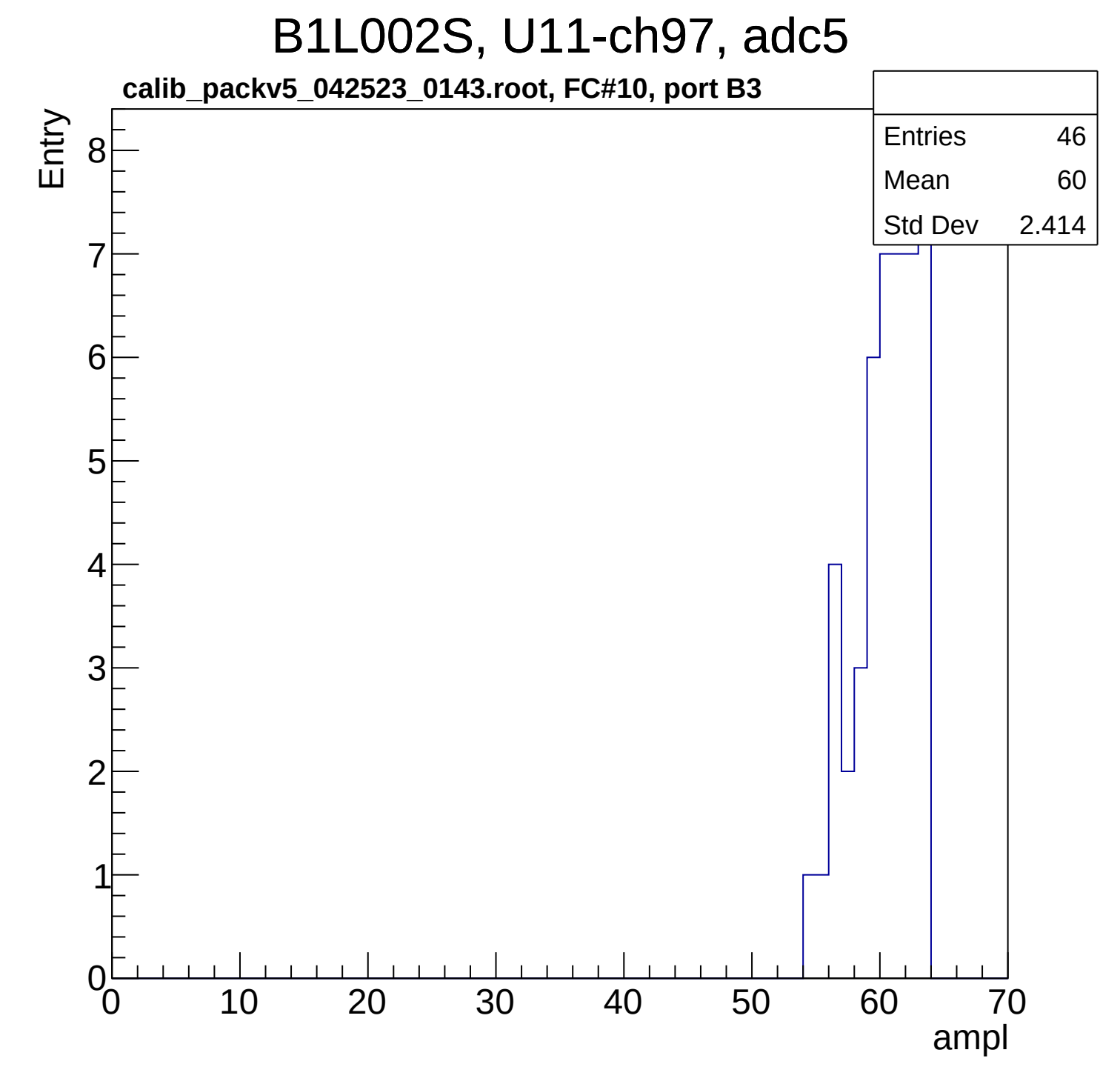
B1L002S, U11-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 60
Std Dev 2.414
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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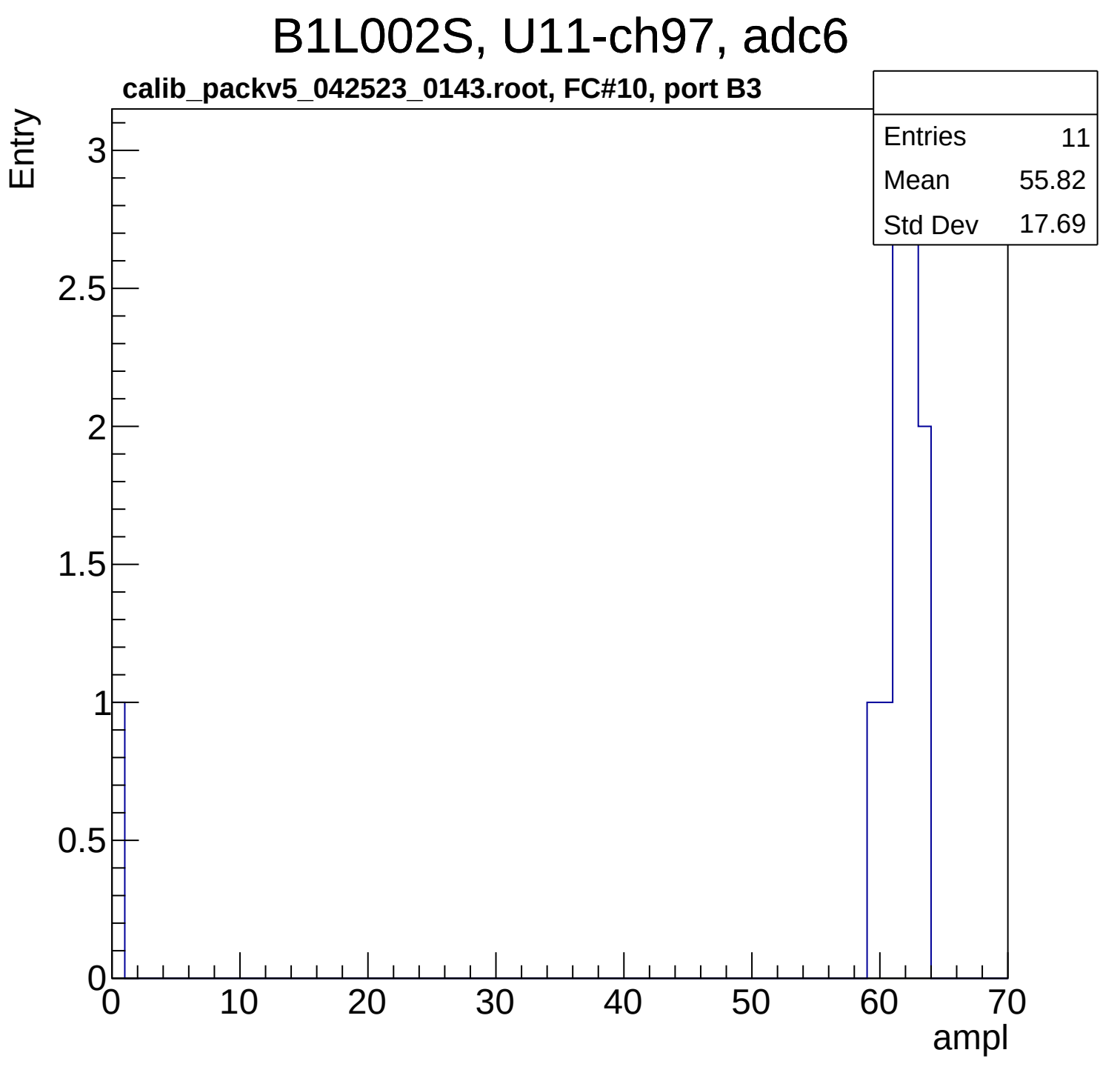
40
50
60
70


B1L002S, U11-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 55.82
Std Dev 17.69
Entry
ampl
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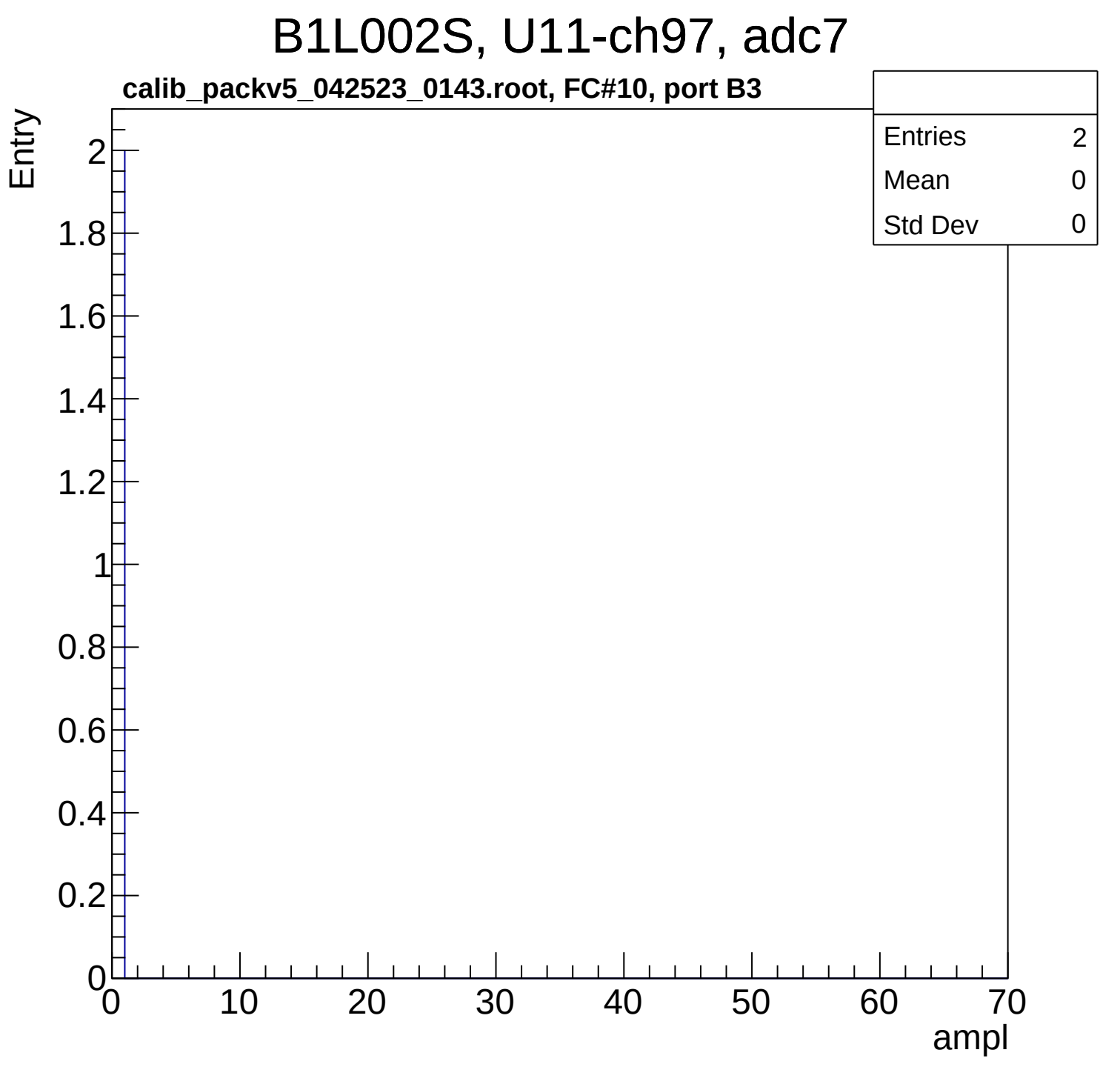
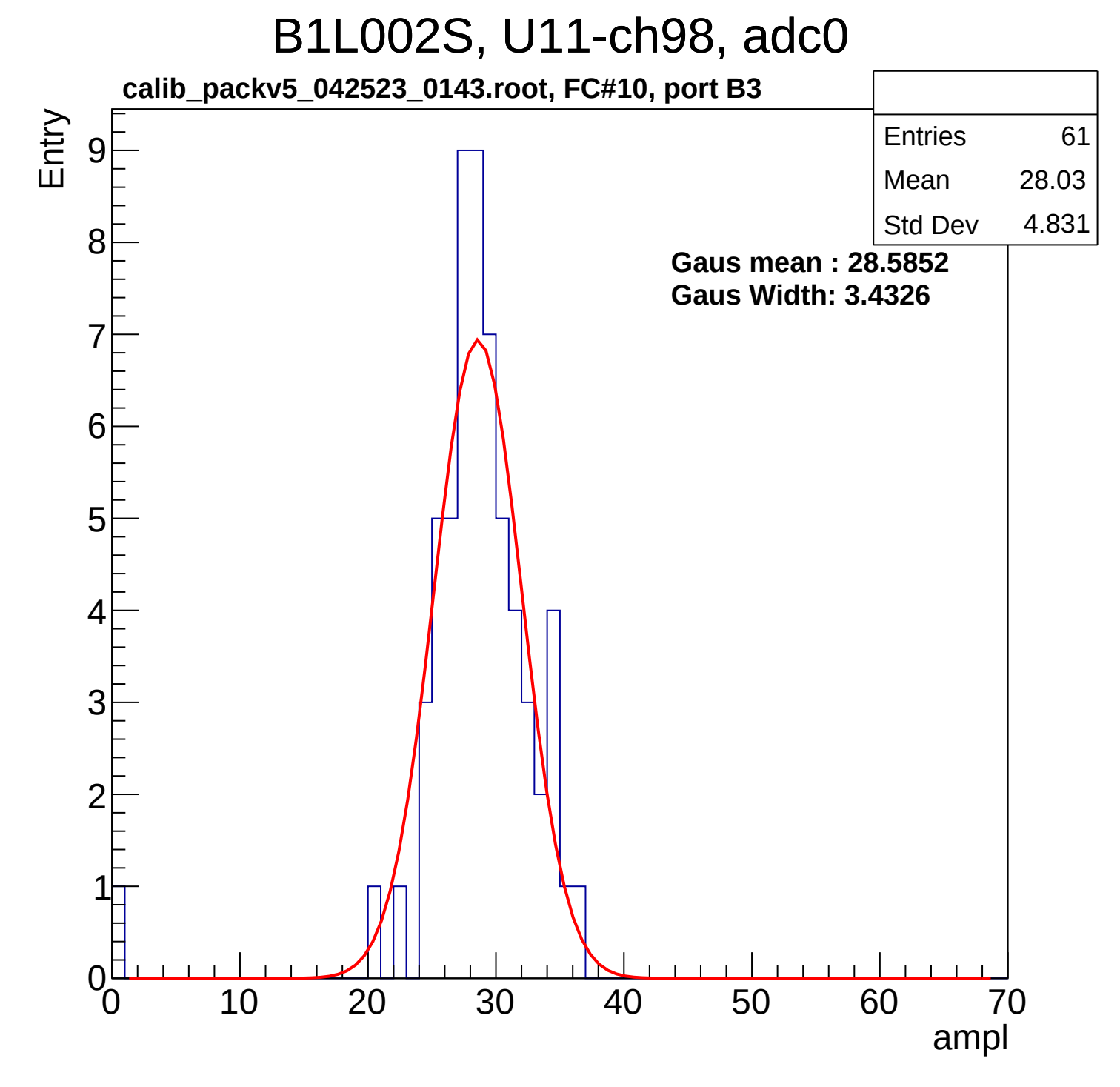
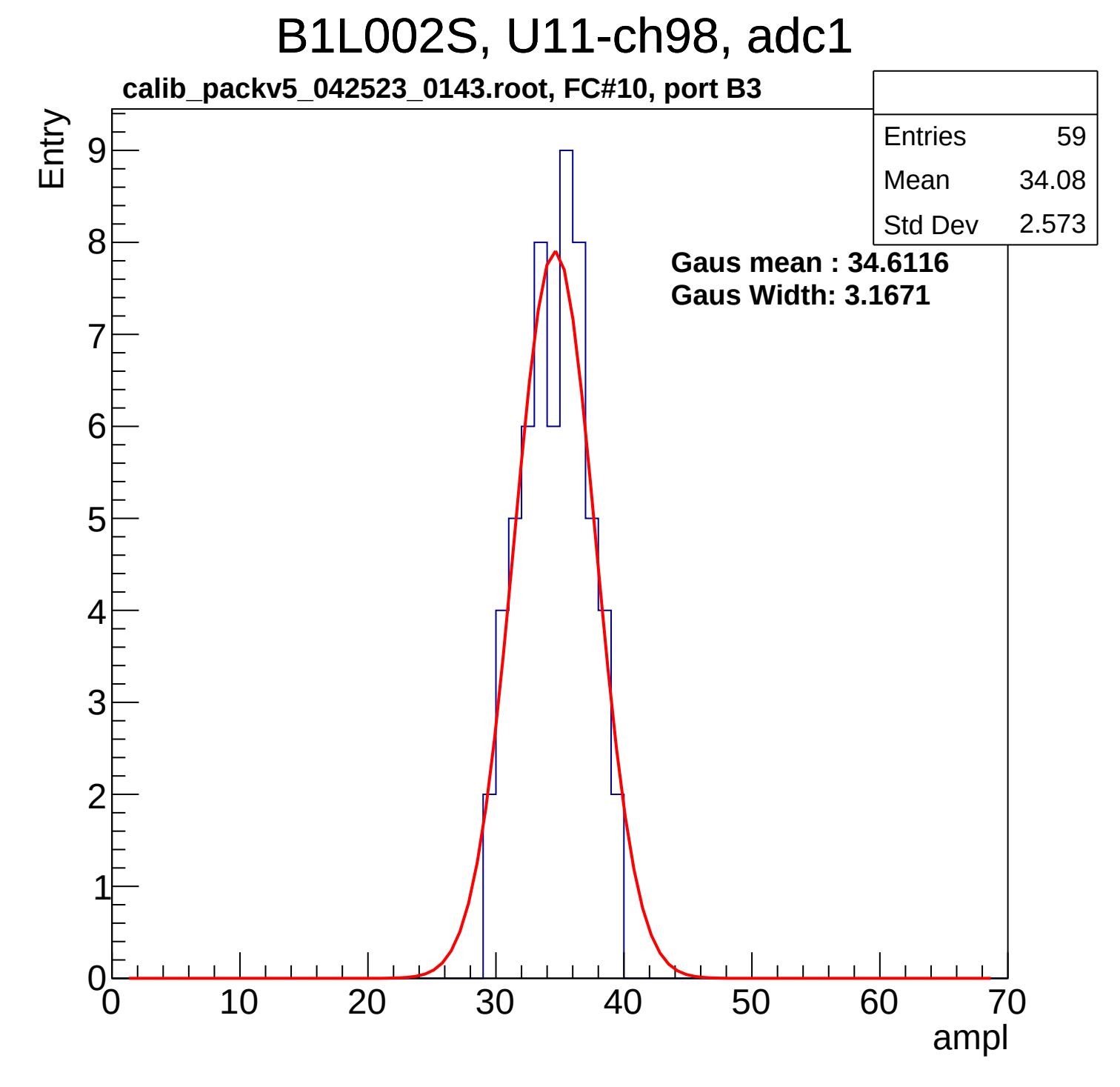
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.03
Std Dev 4.831
Gaus mean : 28.5852
Gaus Width: 3.4326
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.08
Std Dev 2.573
Gaus mean : 34.6116
Gaus Width: 3.1671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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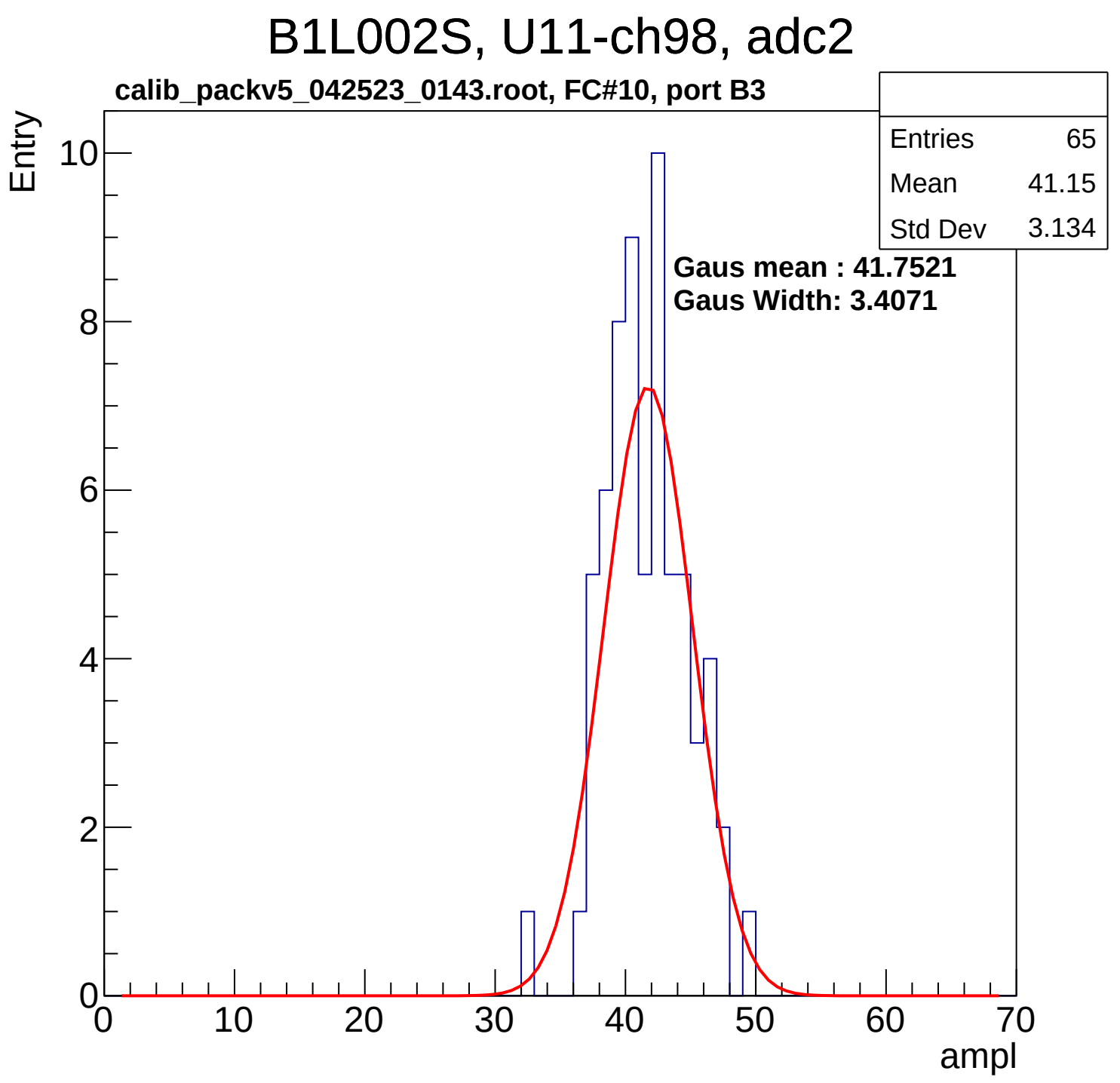
B1L002S, U11-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.15
Std Dev 3.134
Gaus mean : 41.7521
Gaus Width: 3.4071
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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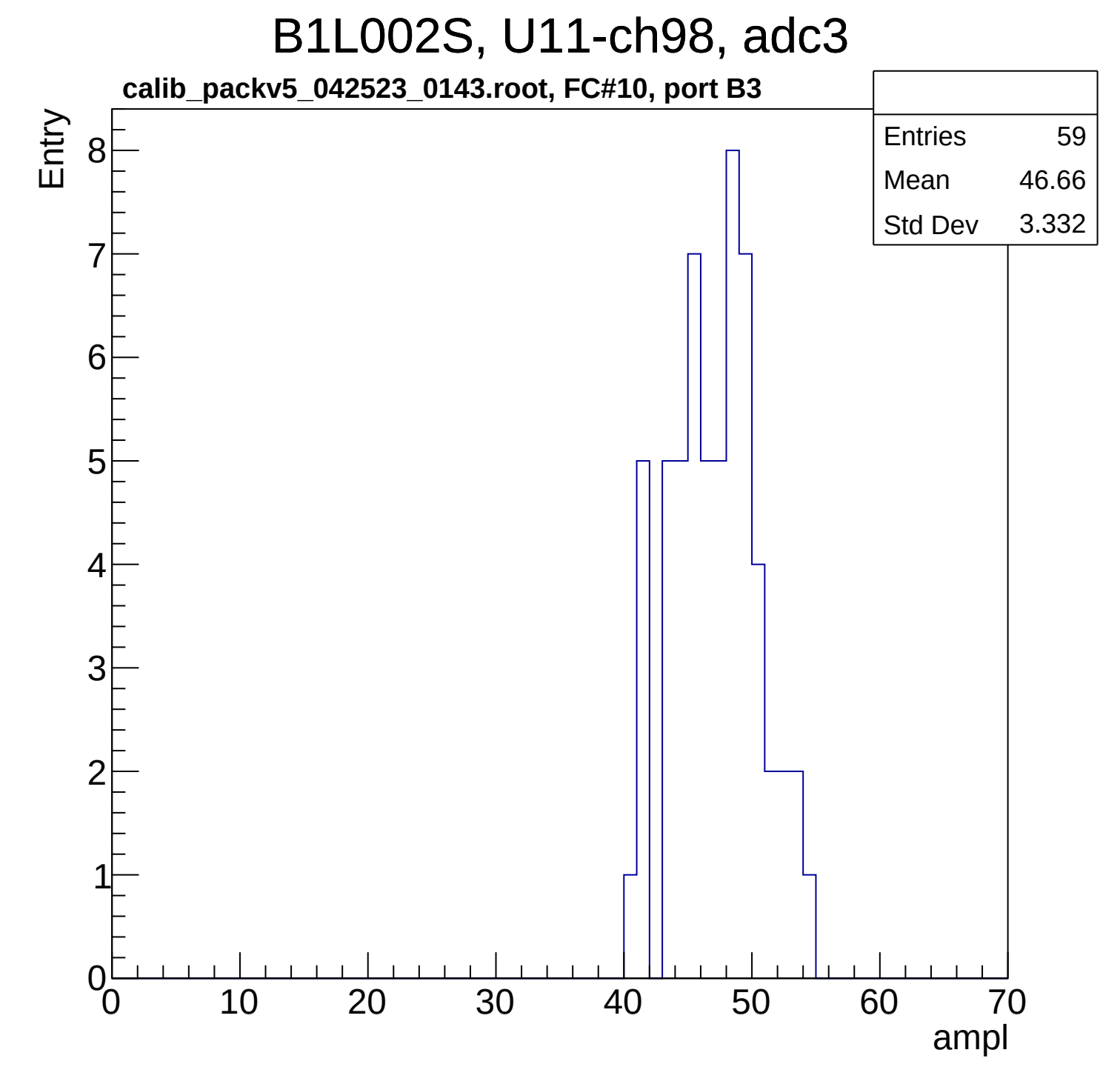
B1L002S, U11-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.66
Std Dev 3.332
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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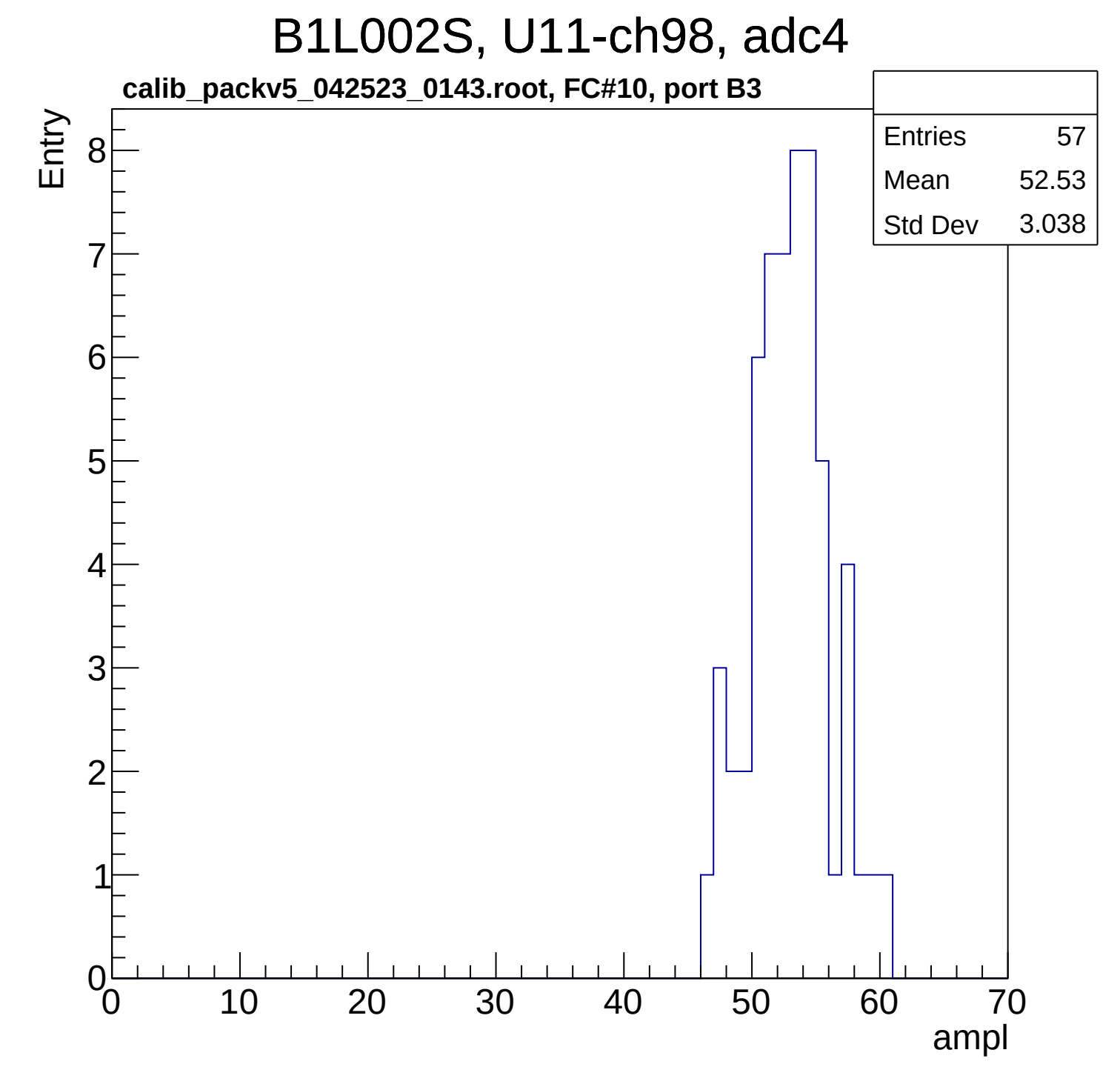
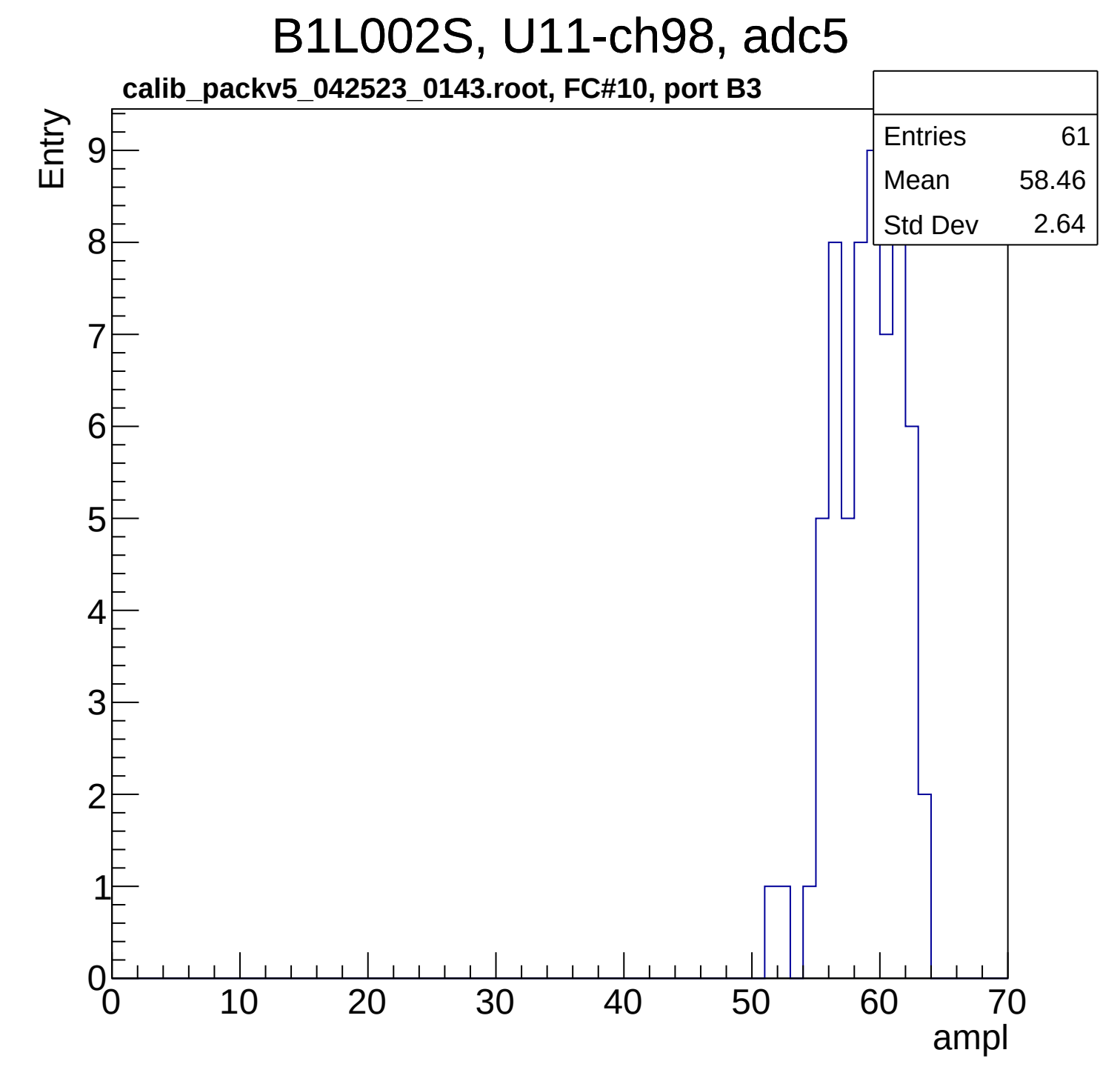
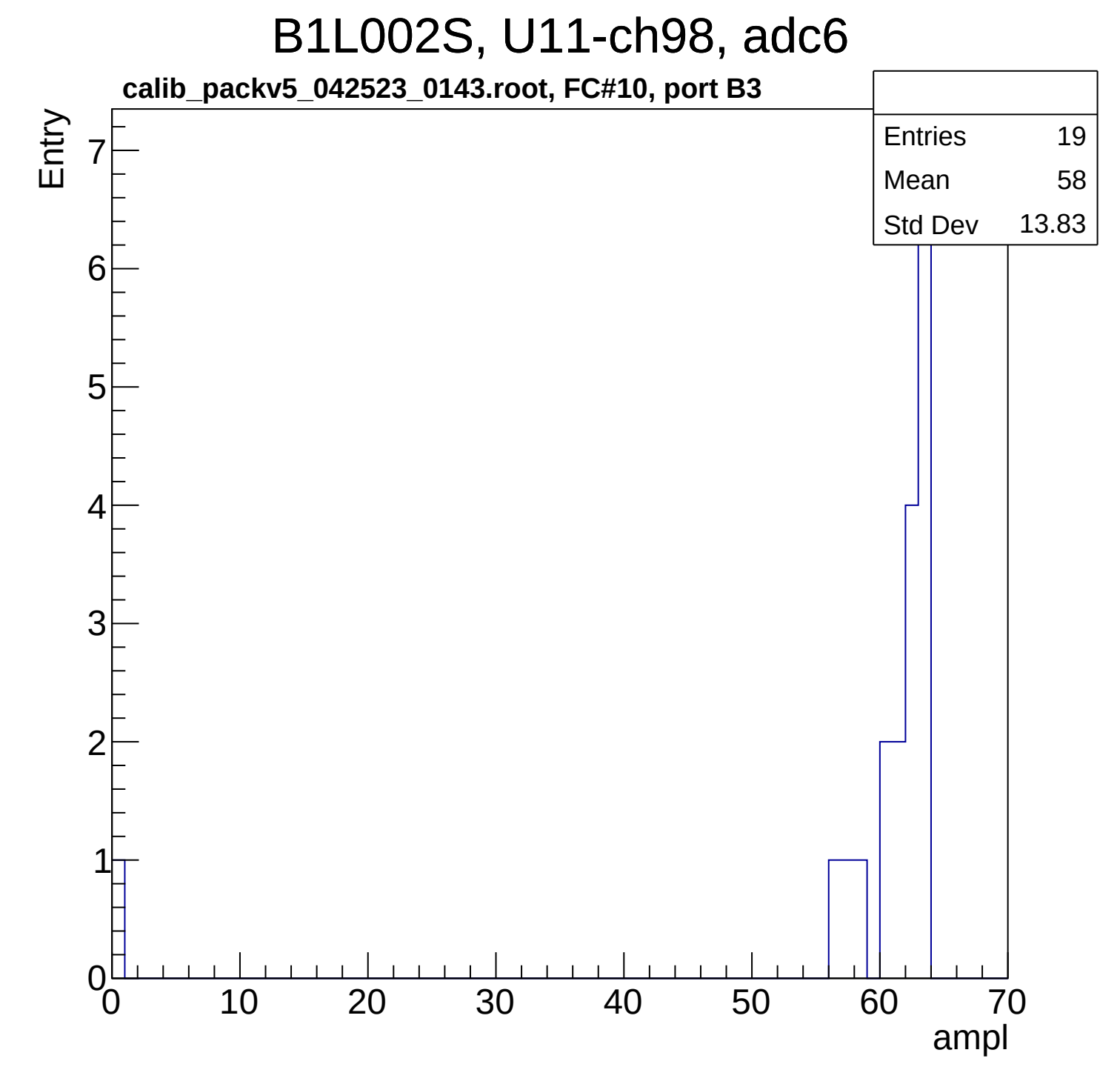
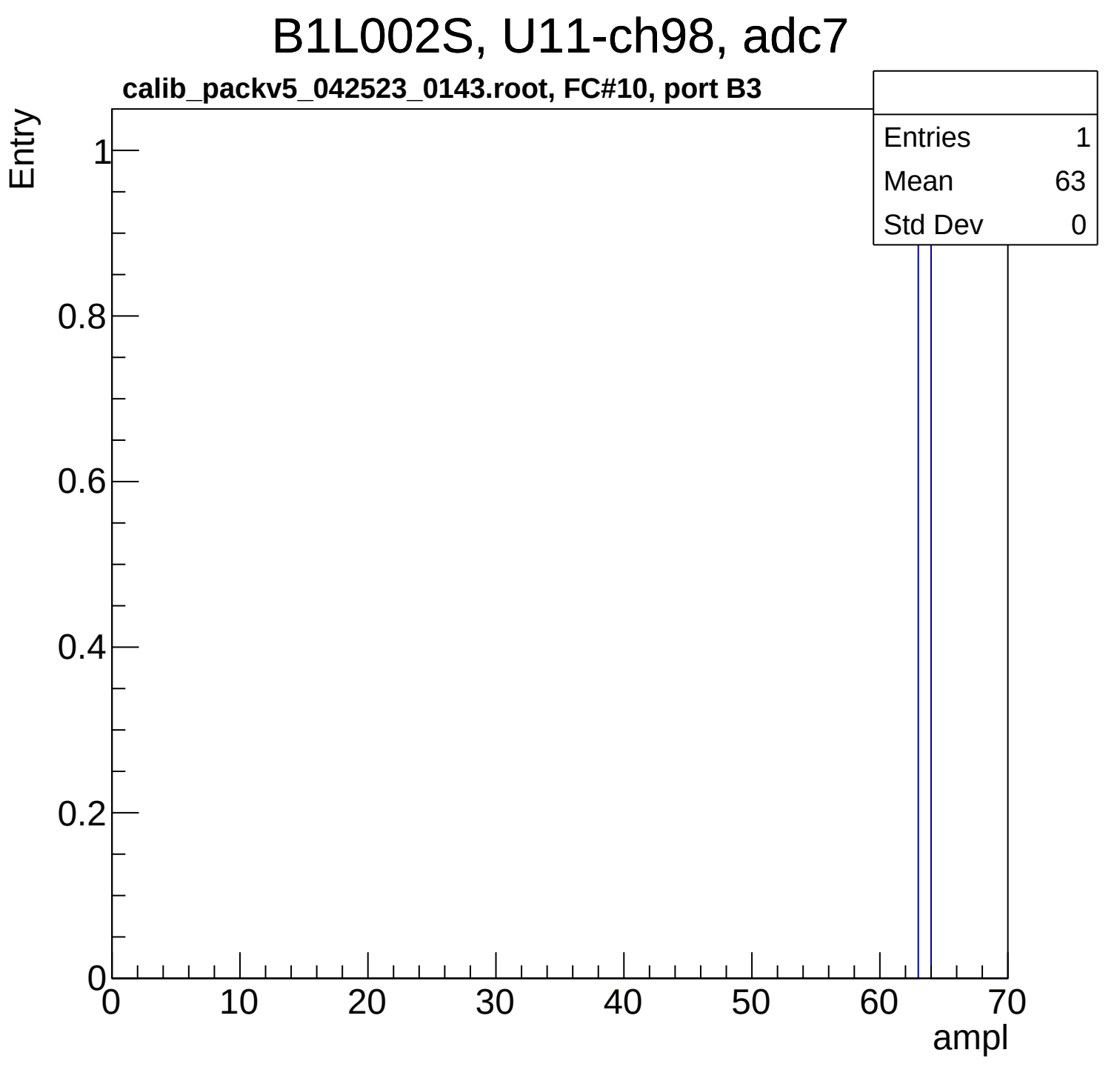
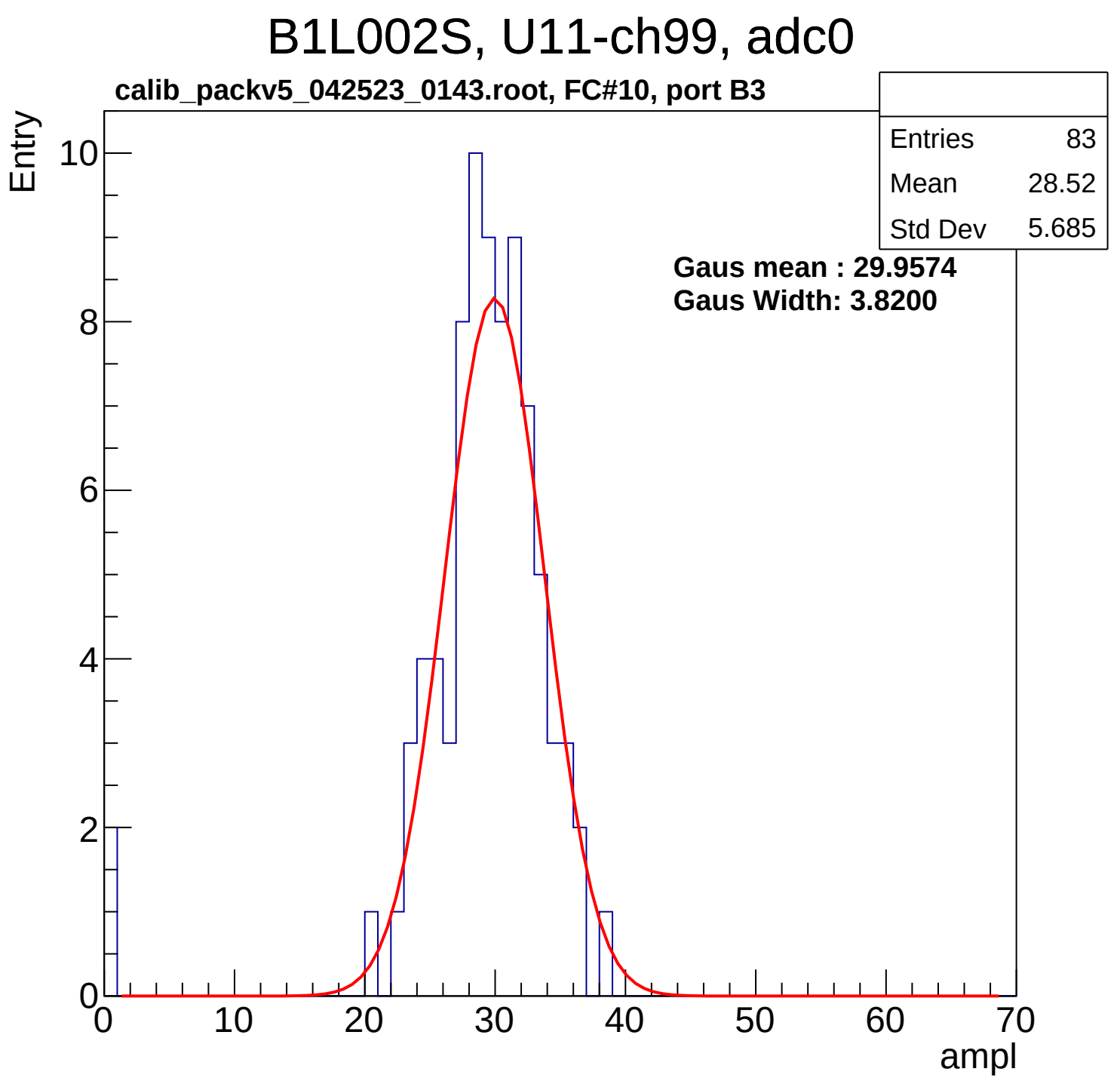
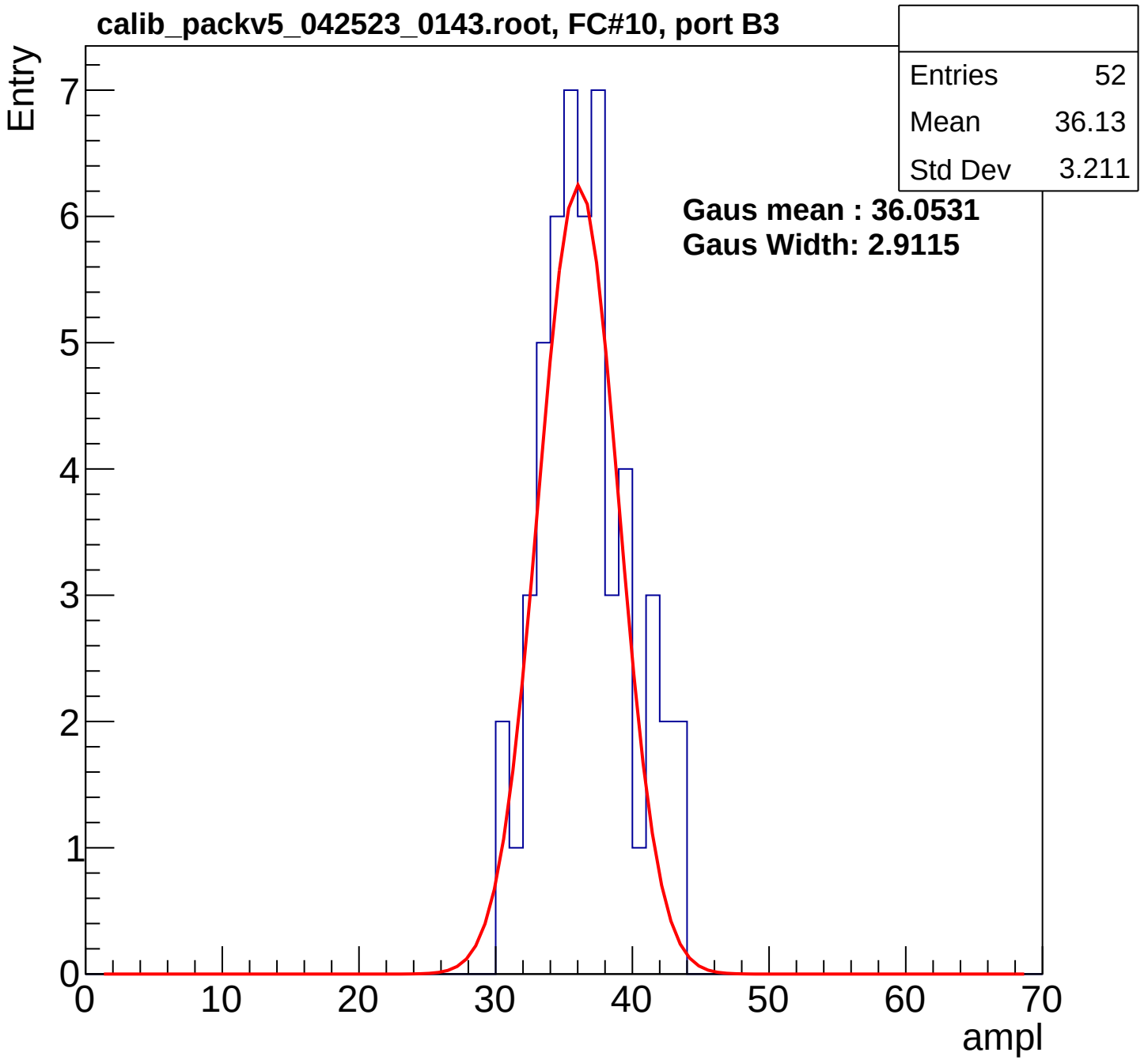

B1L002S, U11-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.53
Std Dev 3.038
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

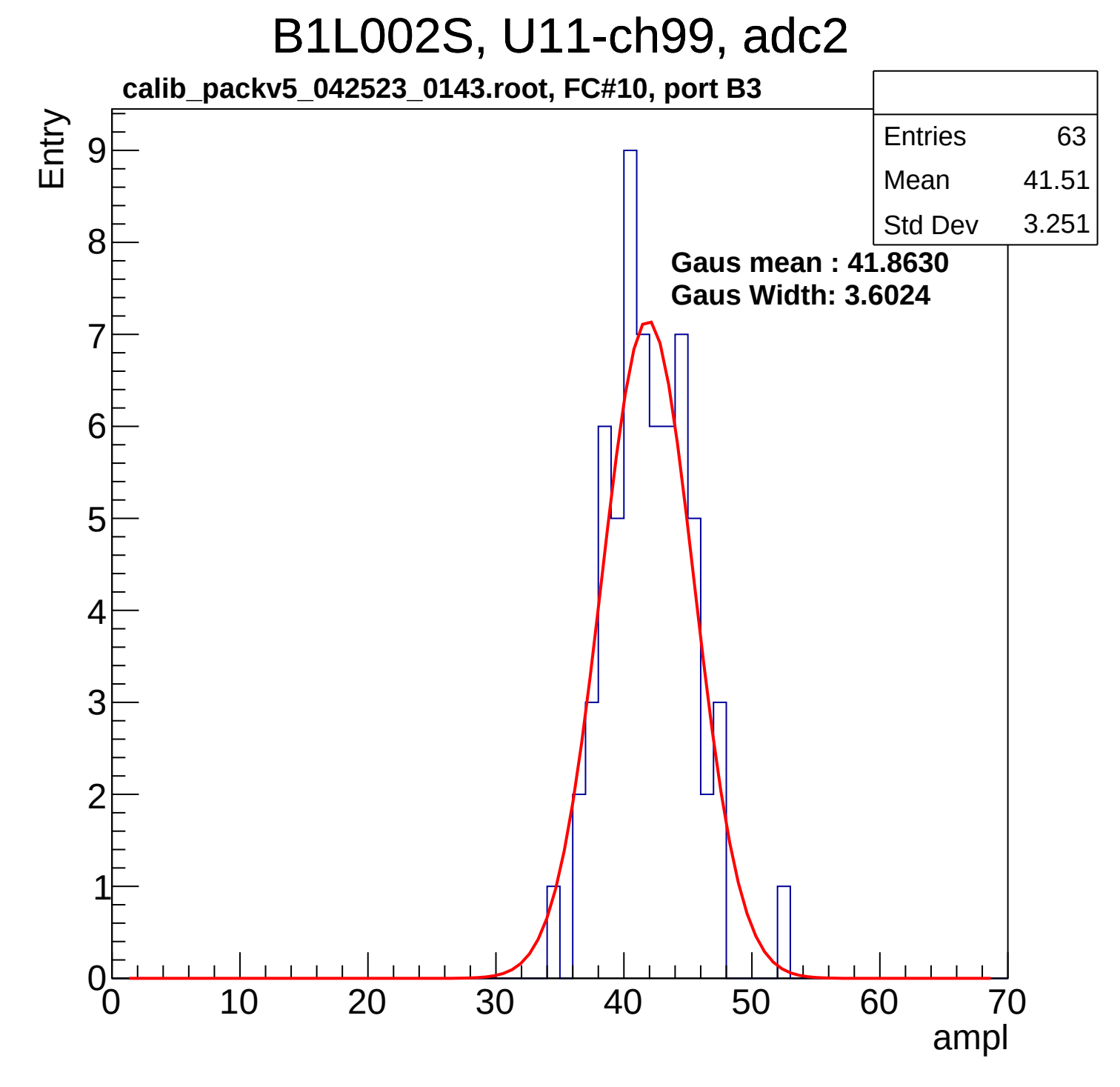

B1L002S, U11-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.46
Std Dev 2.64
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58
Std Dev 13.83
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 28.52
Std Dev 5.685
Gaus mean : 29.9574
Gaus Width: 3.8200
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 36.13
Std Dev 3.211
Gaus mean : 36.0531
Gaus Width: 2.9115
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.51
Std Dev 3.251
Gaus mean : 41.8630
Gaus Width: 3.6024
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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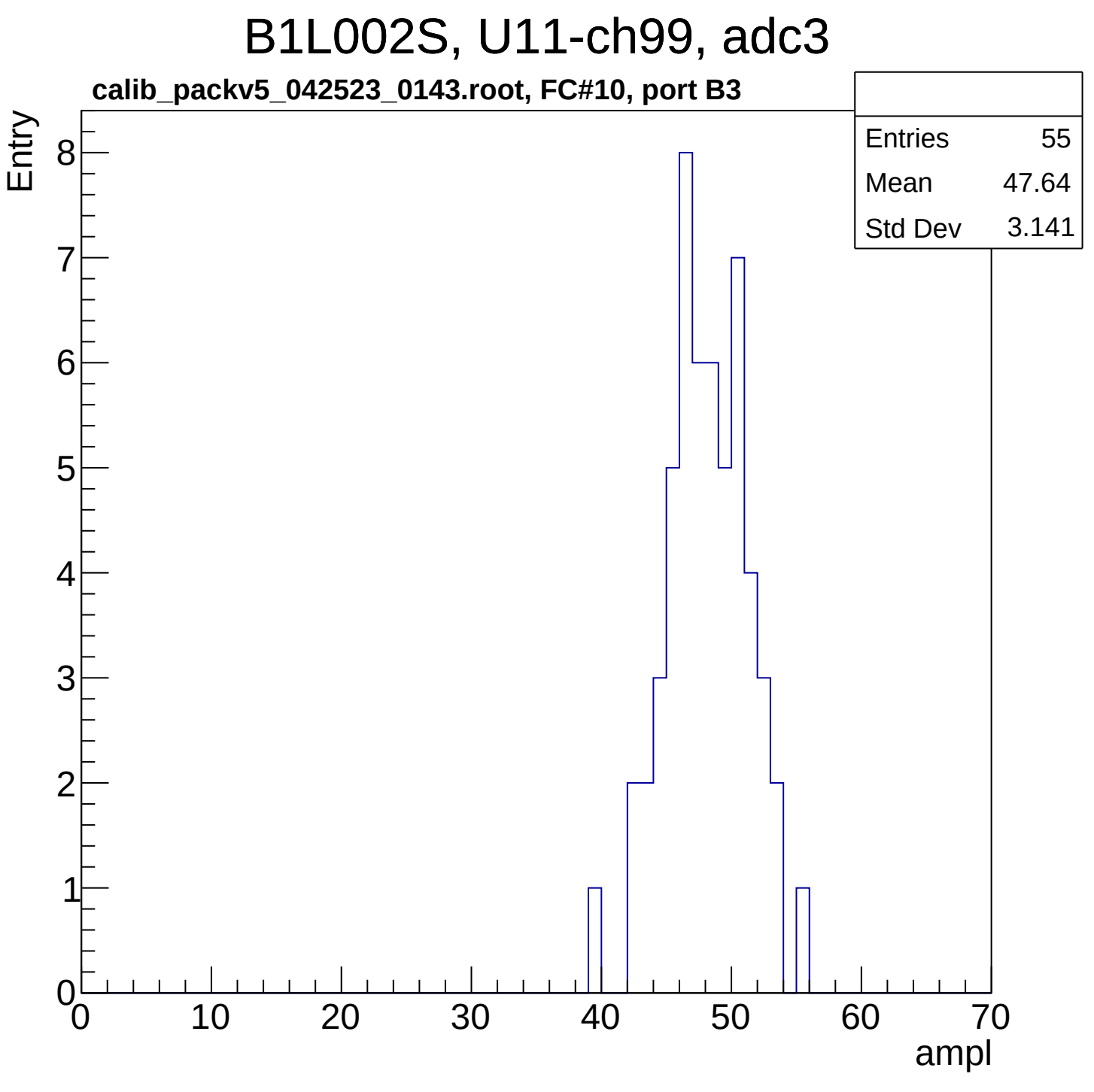
B1L002S, U11-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.64
Std Dev 3.141
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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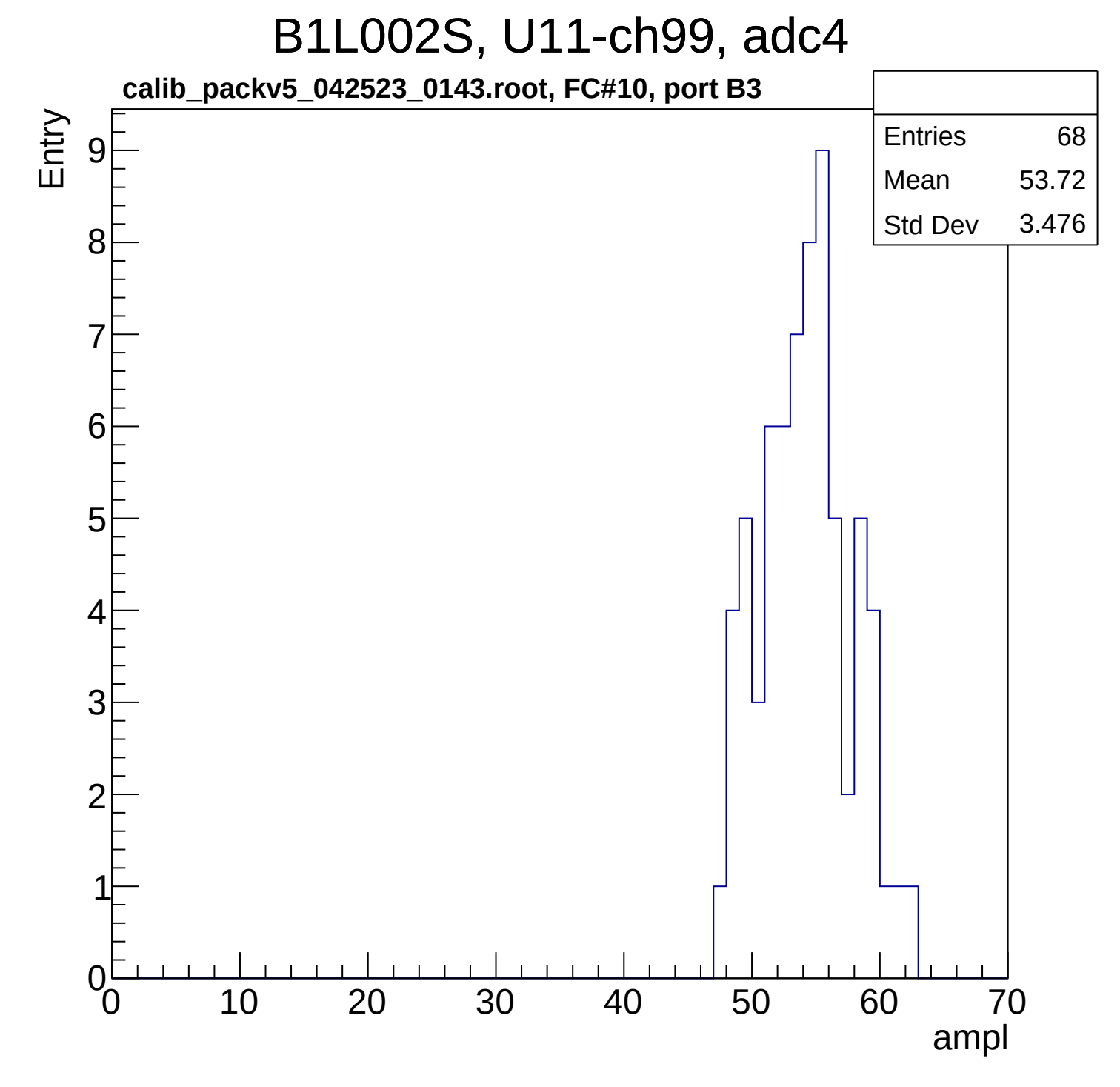
B1L002S, U11-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 53.72
Std Dev 3.476
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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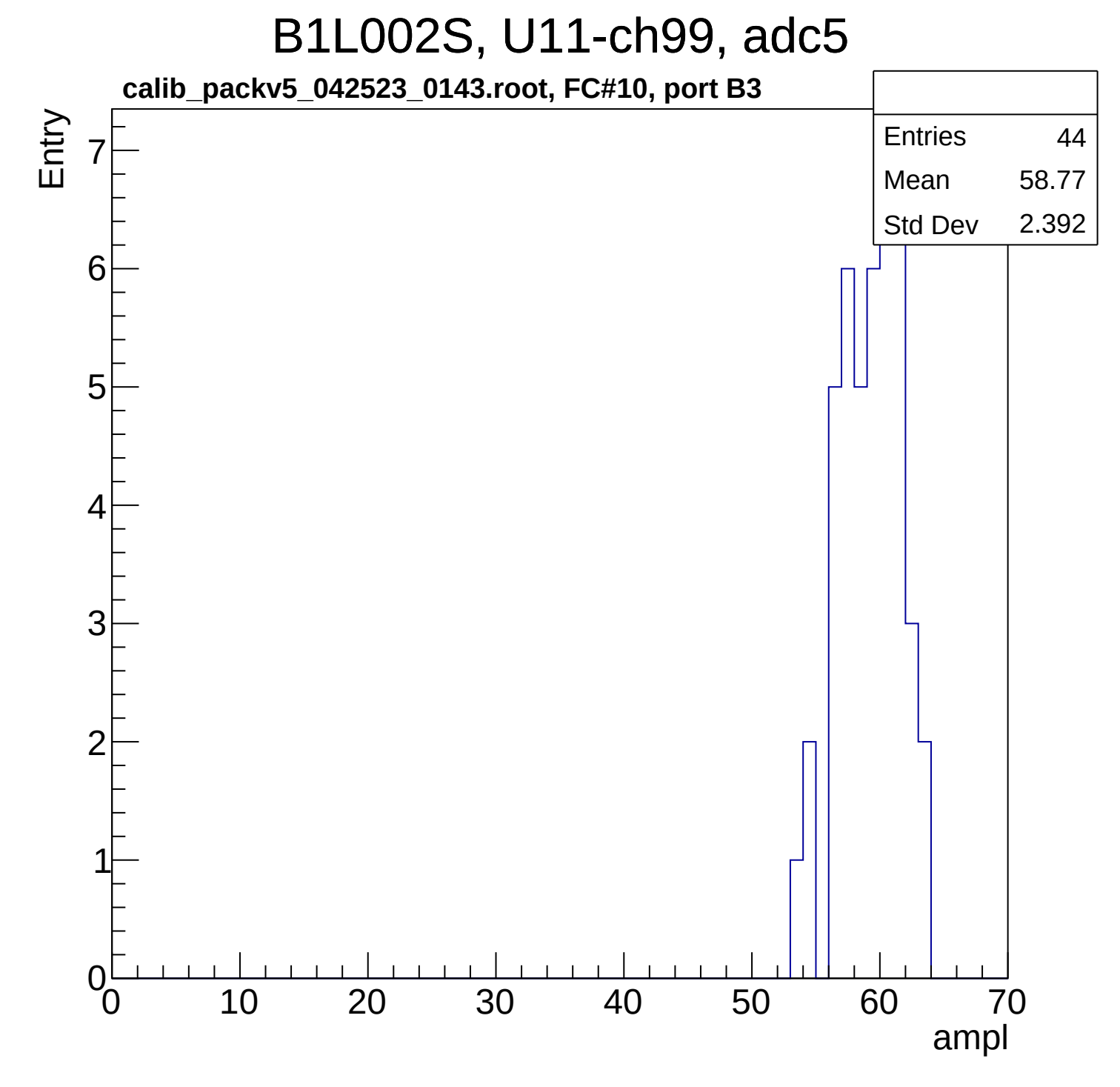
B1L002S, U11-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.77
Std Dev 2.392
Entry
ampl
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3
4
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6
7
0
10
20
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40
50
60
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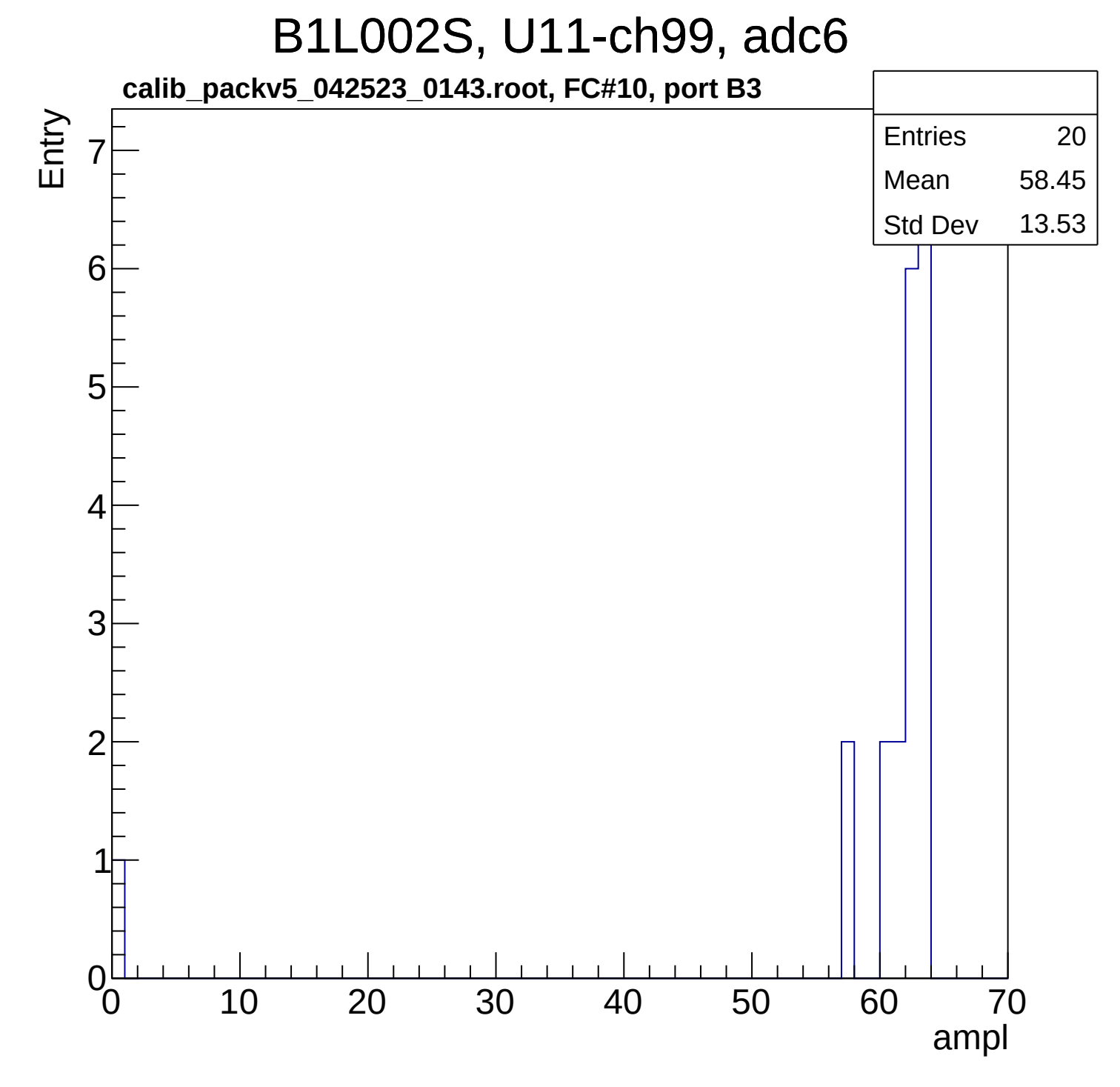
B1L002S, U11-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.45
Std Dev 13.53
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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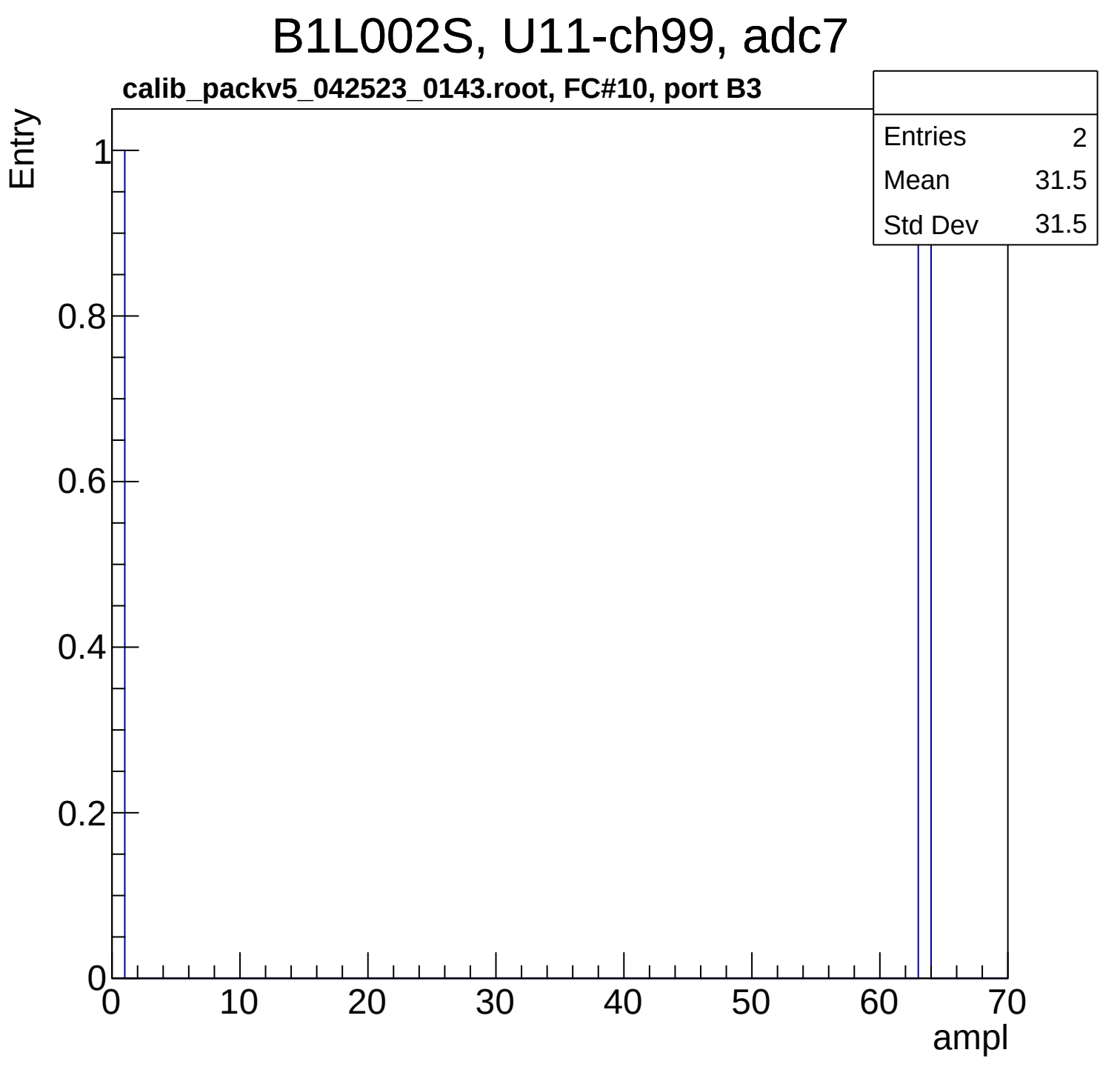
B1L002S, U11-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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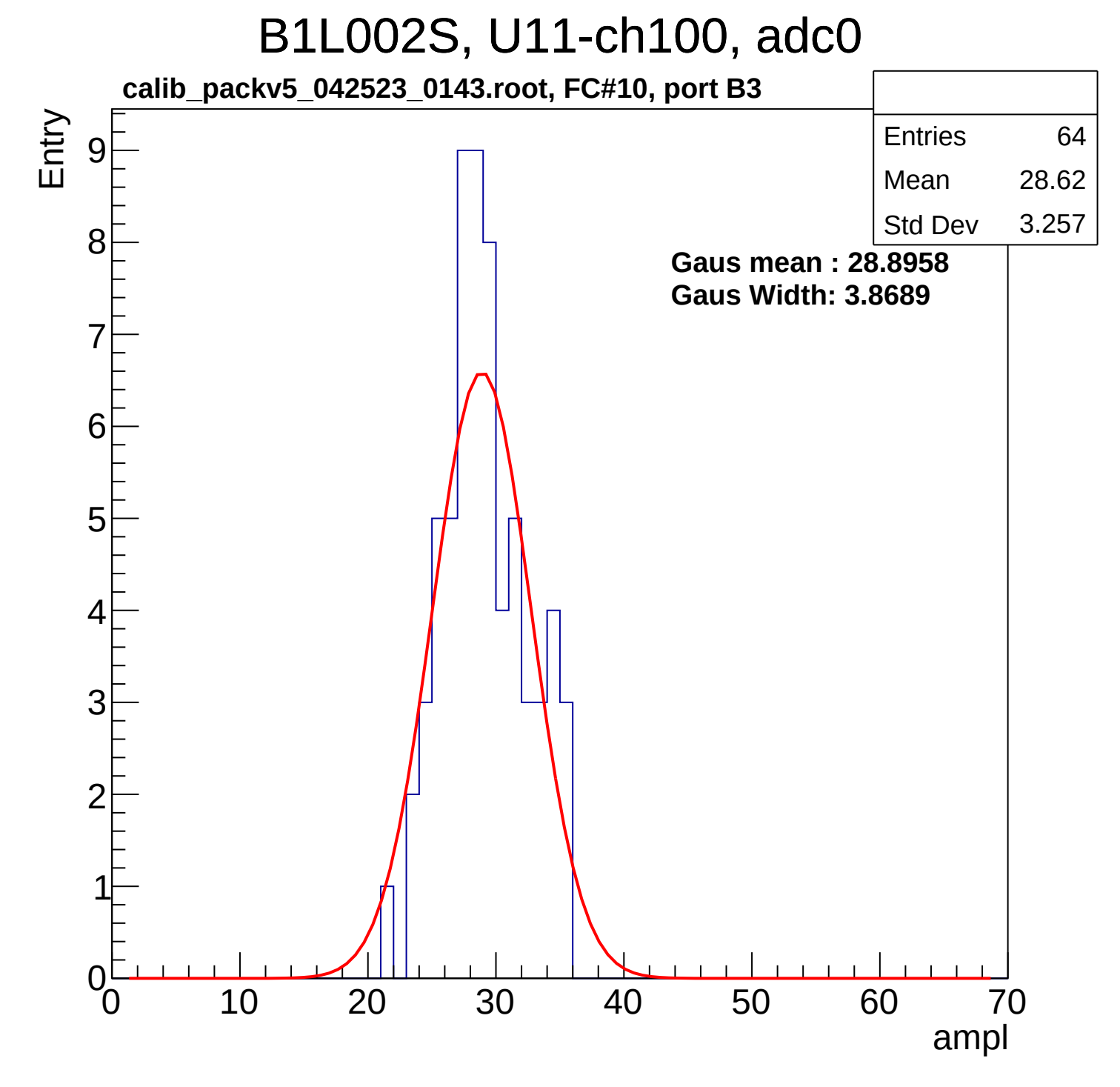
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.62
Std Dev 3.257
Gaus mean : 28.8958
Gaus Width: 3.8689
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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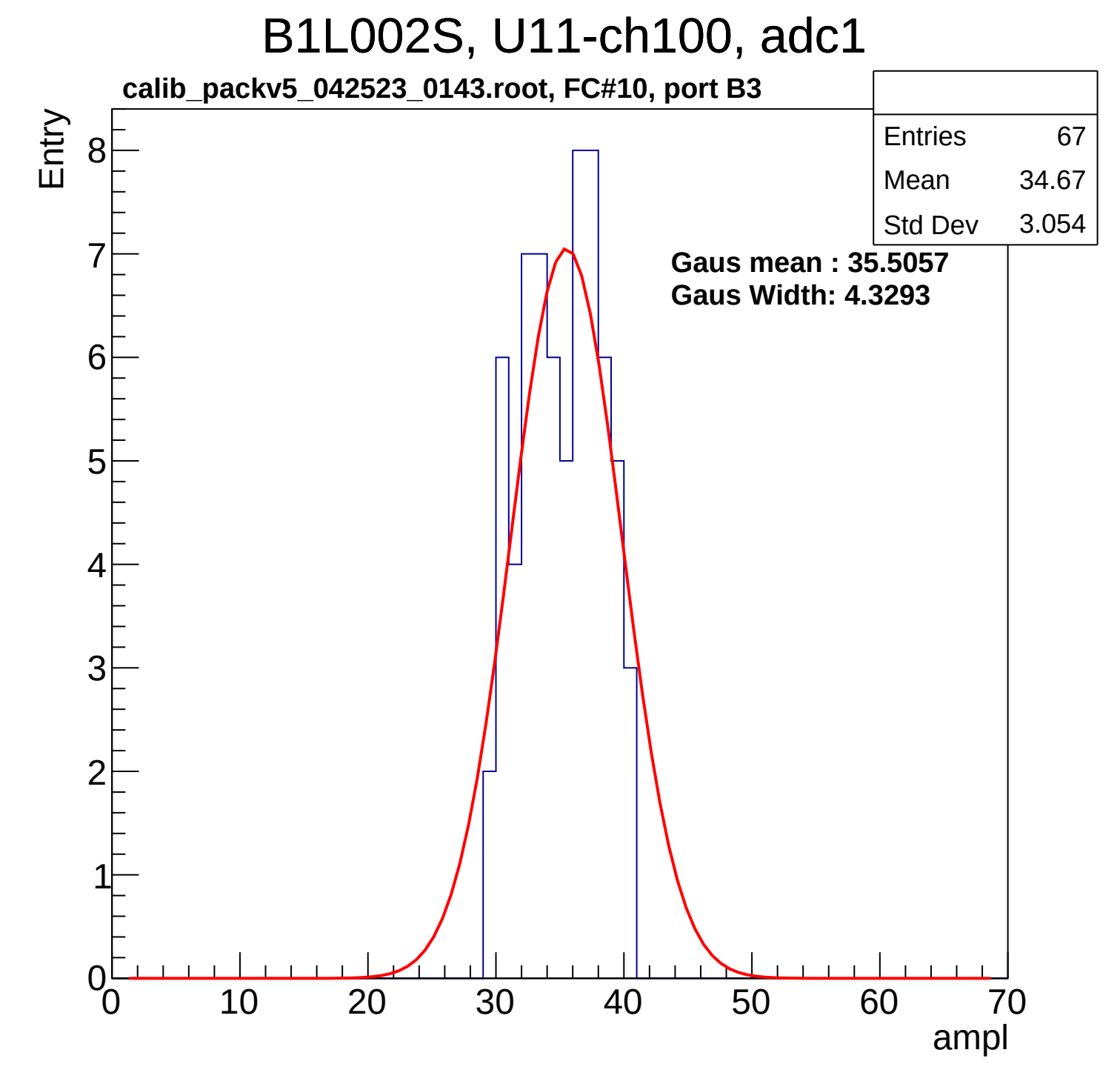
B1L002S, U11-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.67
Std Dev 3.054
Gaus mean : 35.5057
Gaus Width: 4.3293
Entry
ampl
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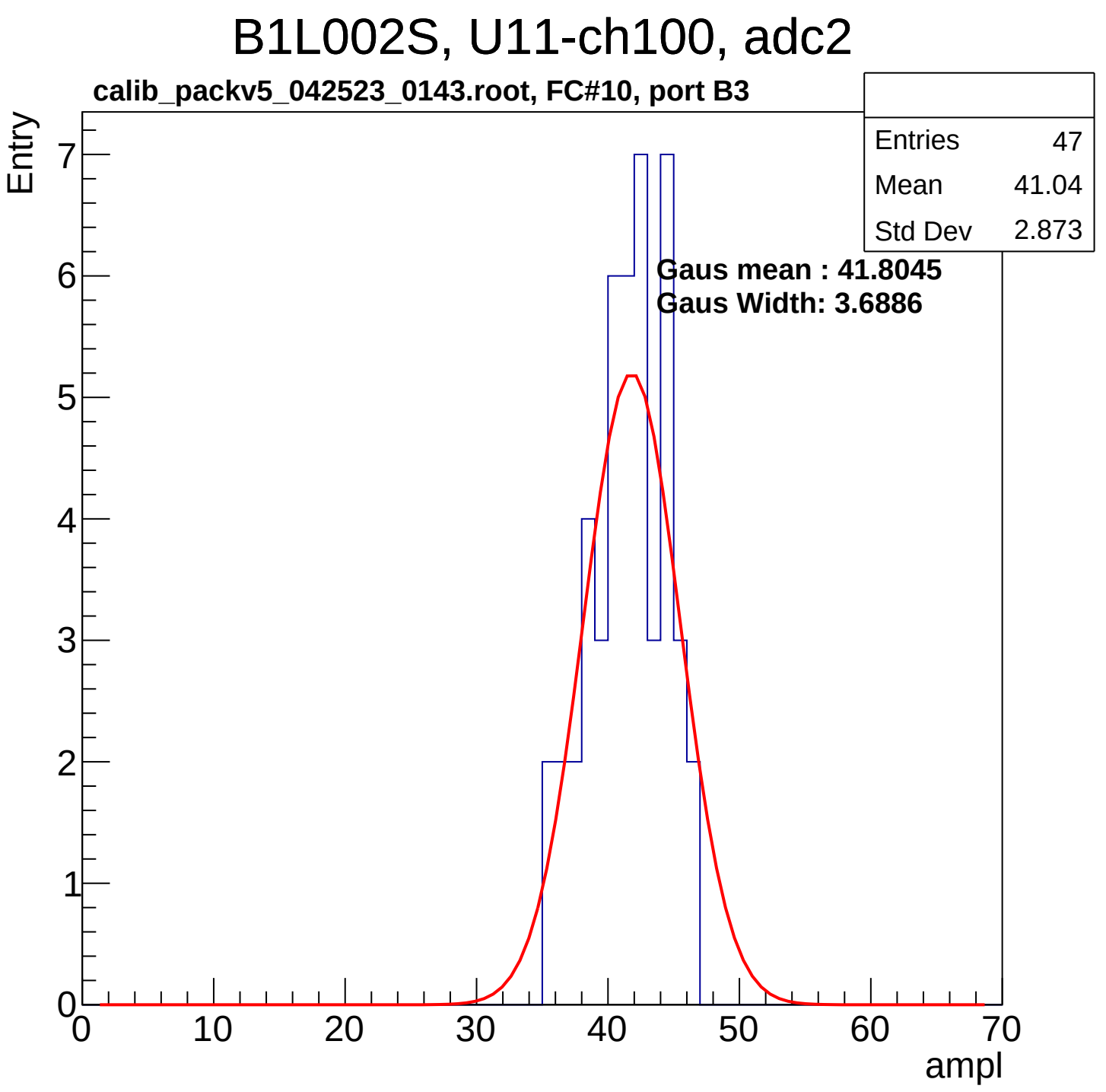
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 41.04
Std Dev 2.873
Gaus mean : 41.8045
Gaus Width: 3.6886
Entry
ampl
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2
3
4
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6
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0
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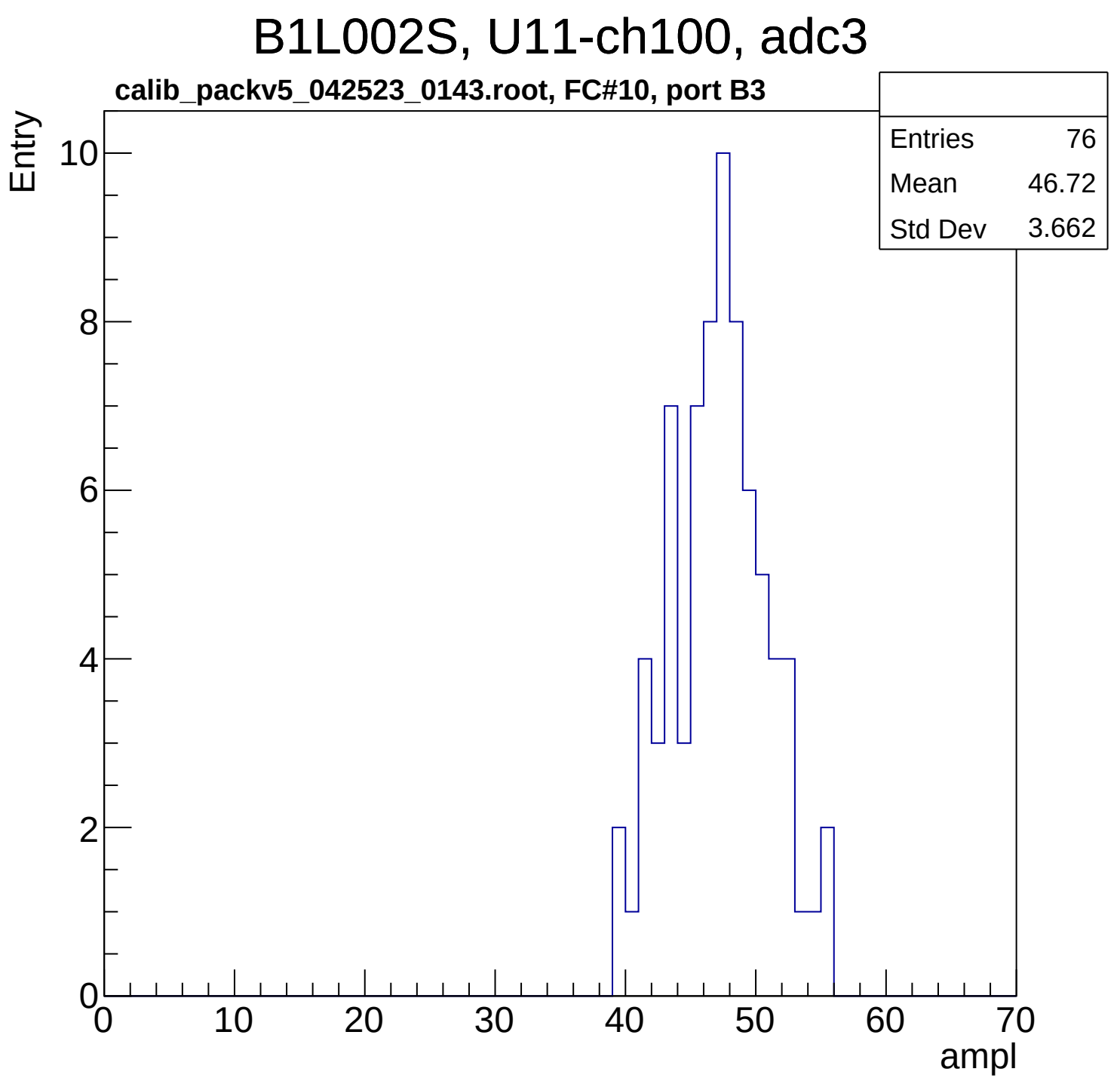
B1L002S, U11-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 46.72
Std Dev 3.662
Entry
ampl
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4
6
8
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0
10
20
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40
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60
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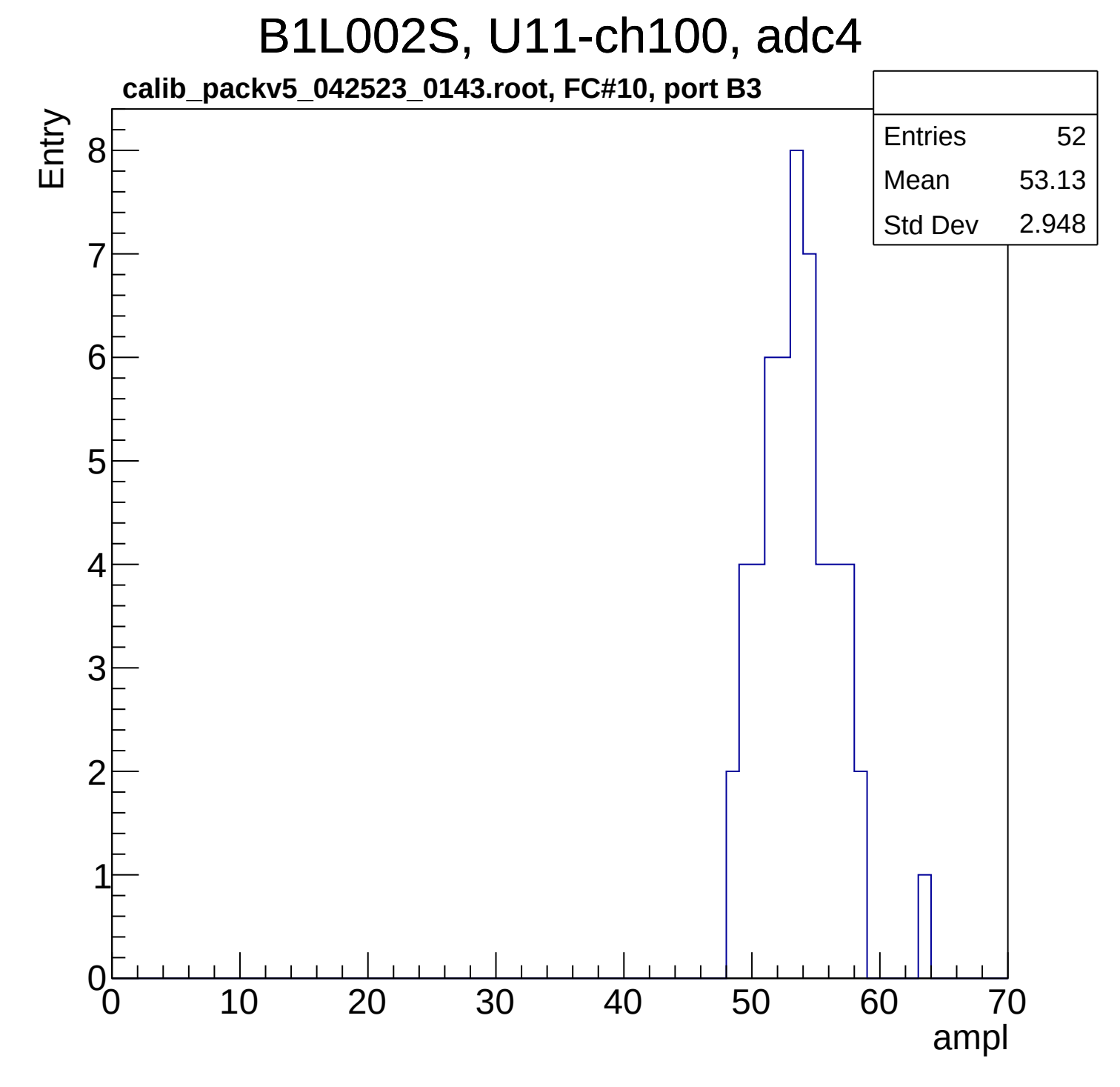
B1L002S, U11-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.13
Std Dev 2.948
Entry
ampl
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2
3
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5
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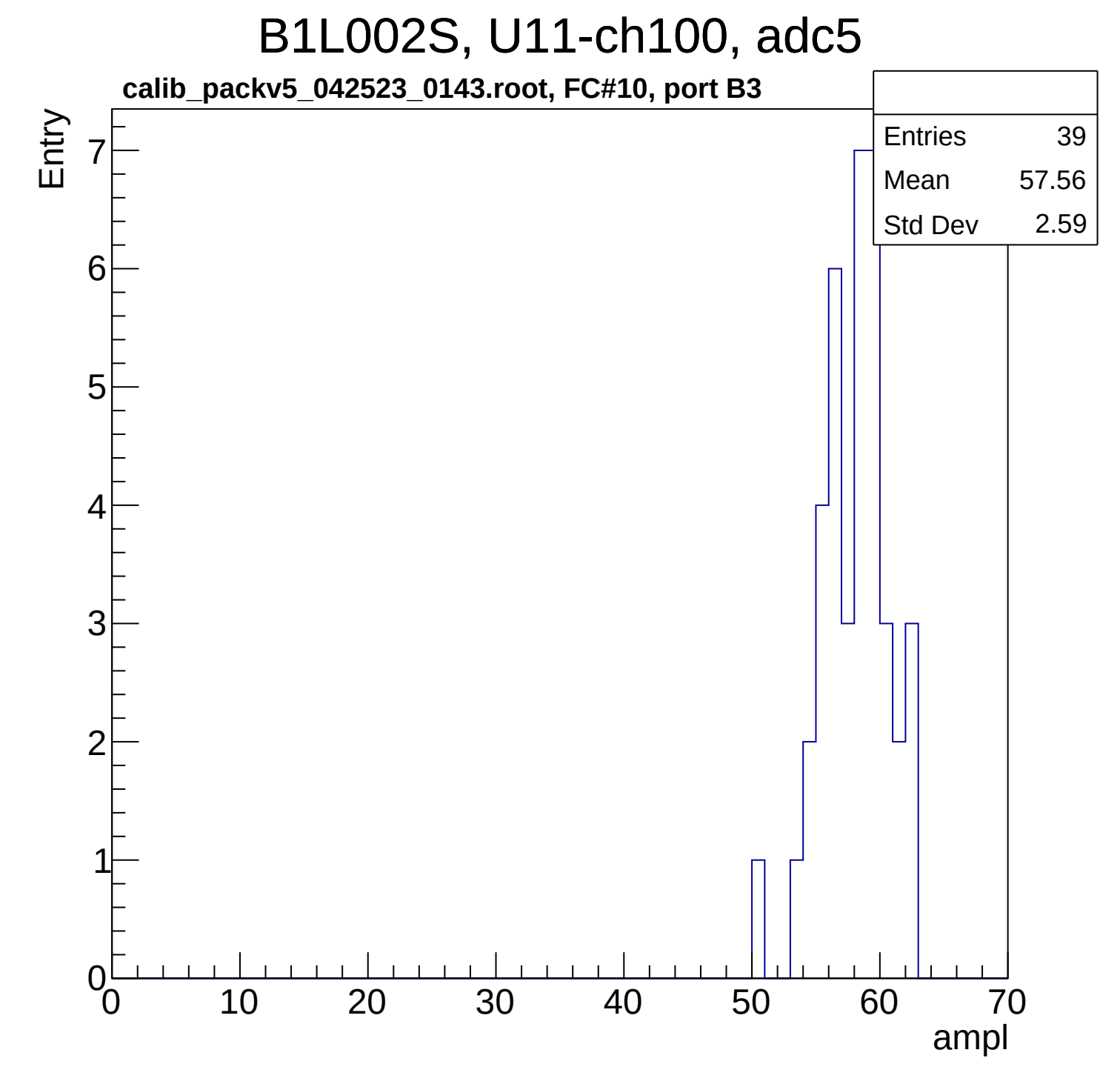
B1L002S, U11-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 57.56
Std Dev 2.59
Entry
ampl
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1
2
3
4
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0
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40
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60
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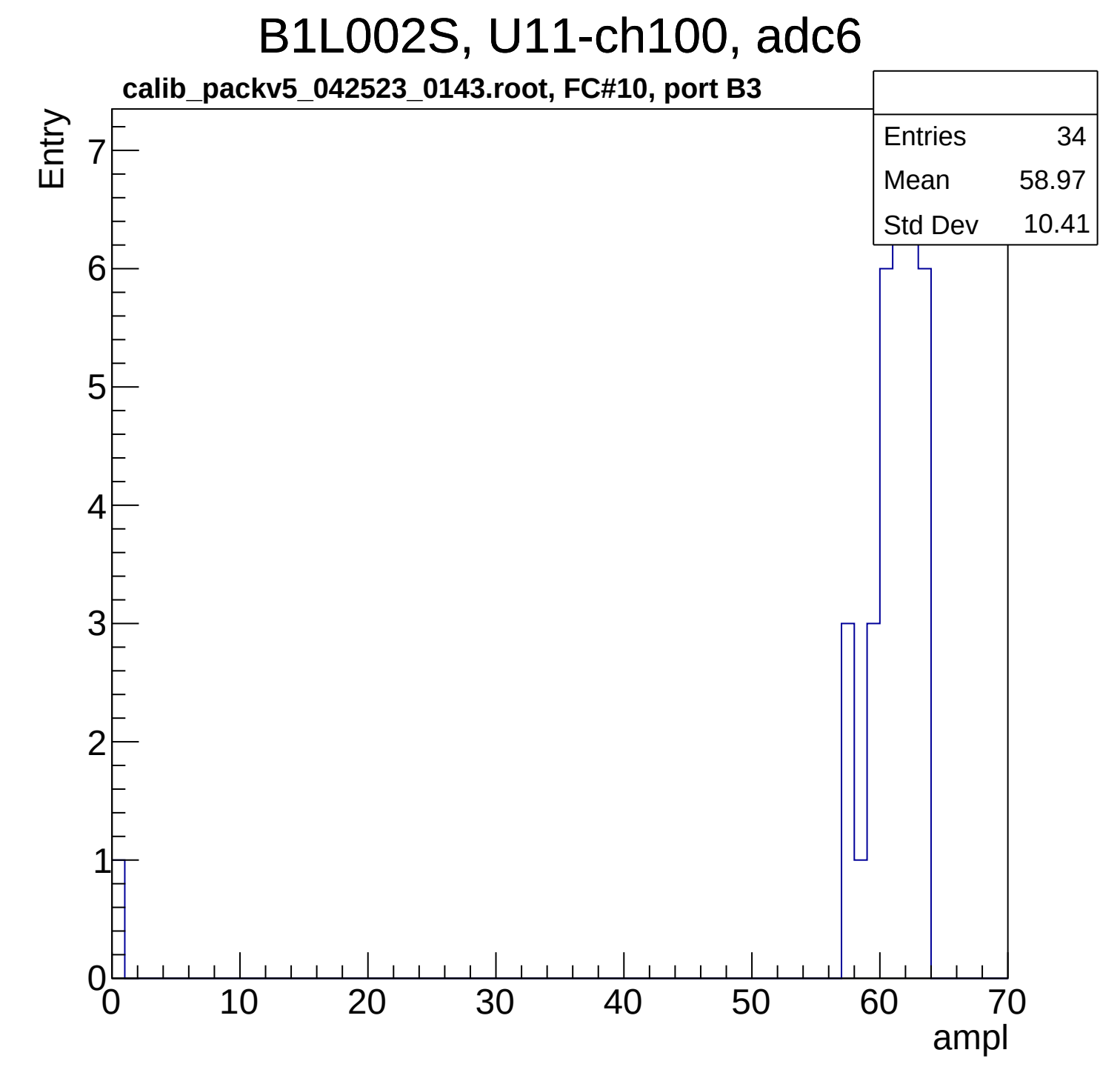
B1L002S, U11-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.97
Std Dev 10.41
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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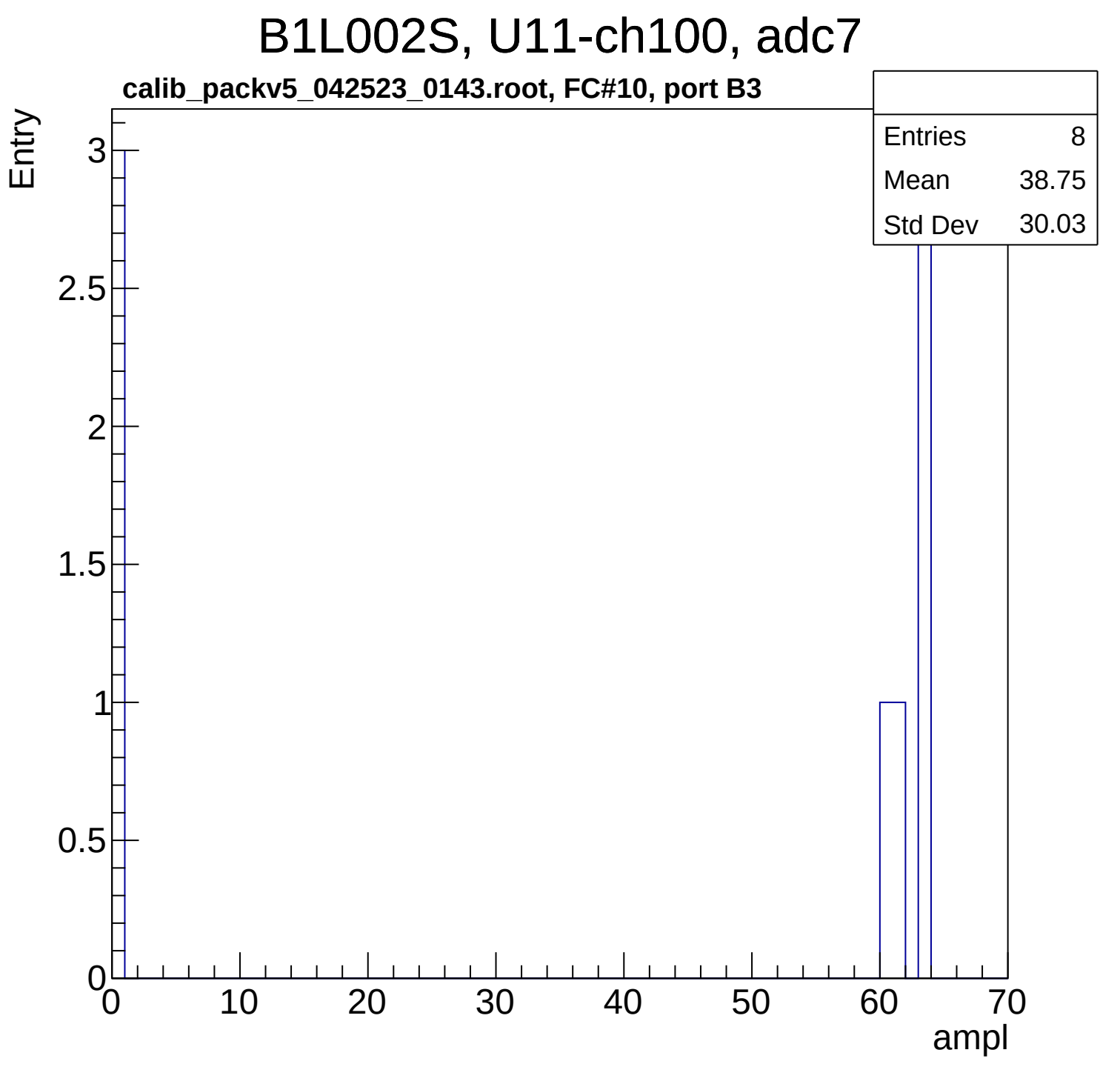
B1L002S, U11-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 38.75
Std Dev 30.03
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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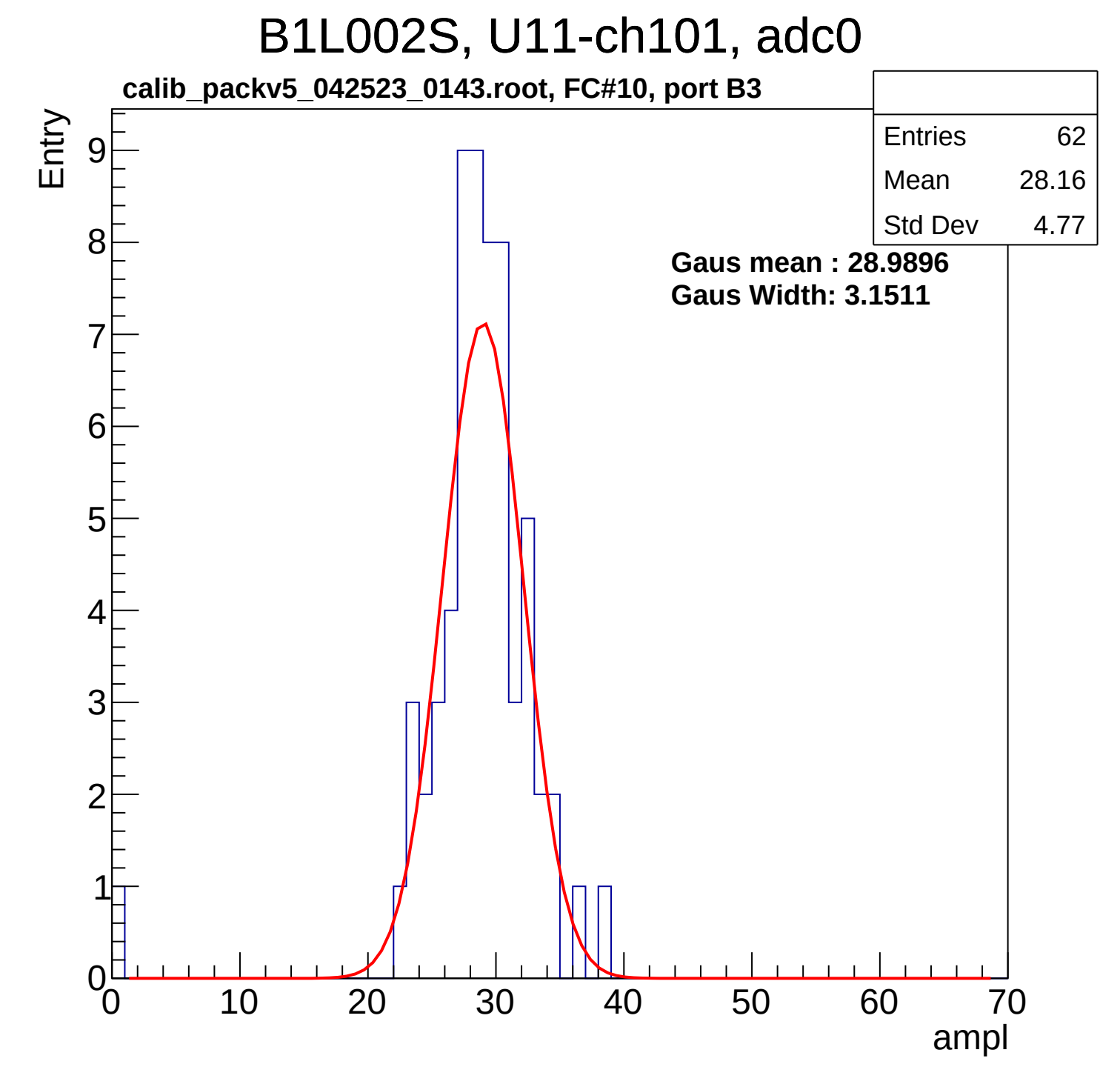
B1L002S, U11-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.16
Std Dev 4.77
Gaus mean : 28.9896
Gaus Width: 3.1511
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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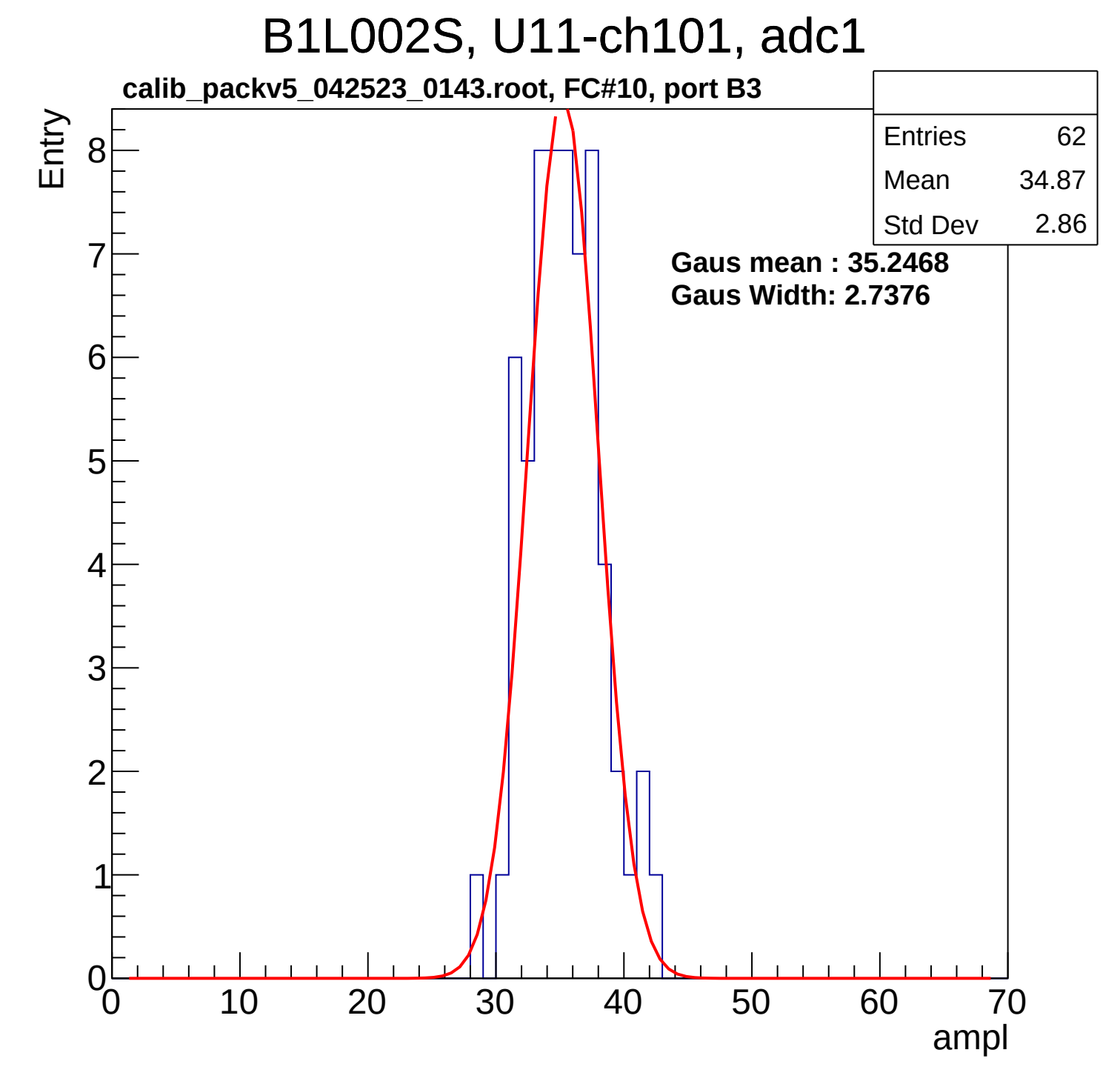
B1L002S, U11-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.87
Std Dev 2.86
Gaus mean : 35.2468
Gaus Width: 2.7376
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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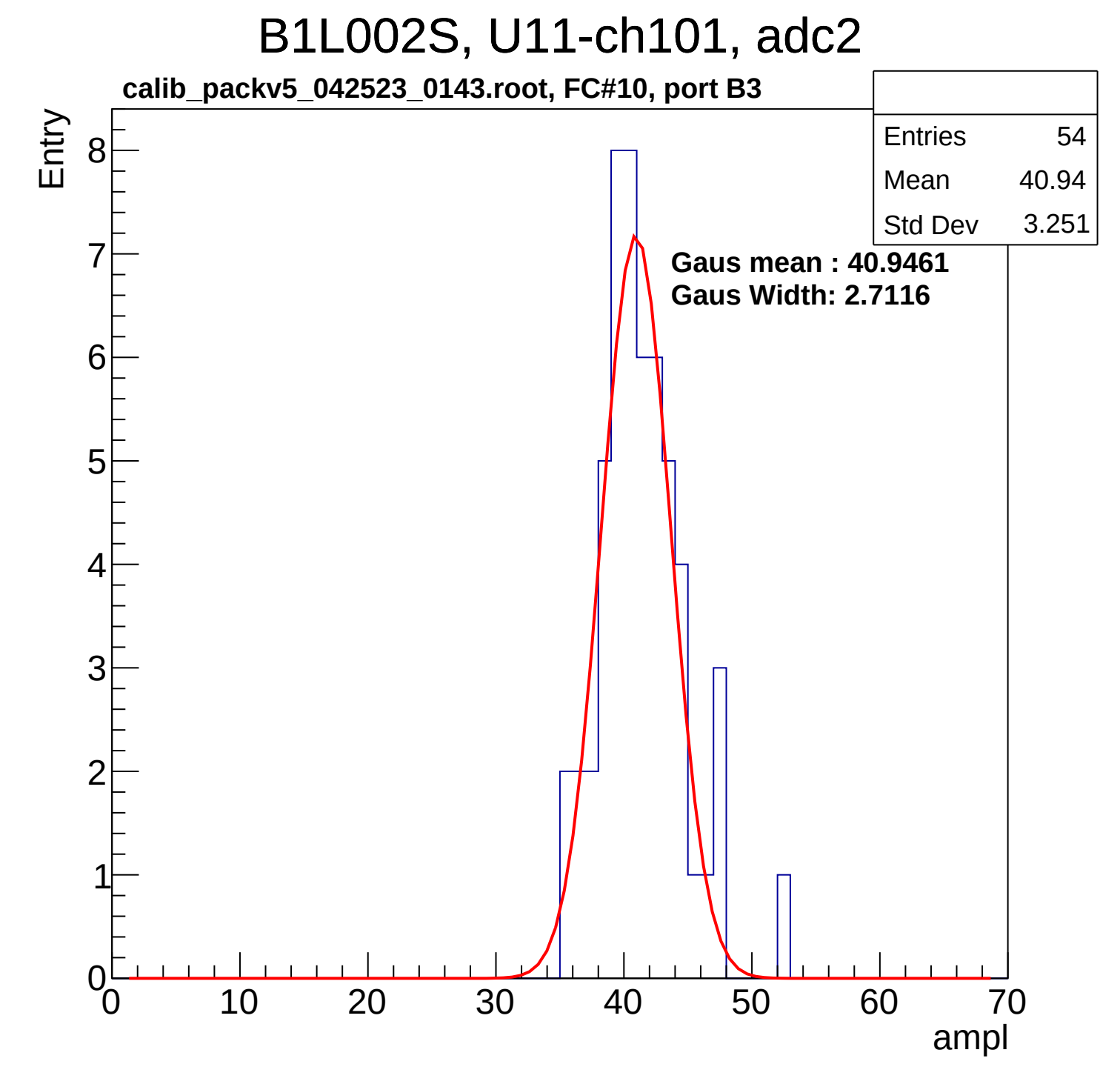
B1L002S, U11-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.94
Std Dev 3.251
Gaus mean : 40.9461
Gaus Width: 2.7116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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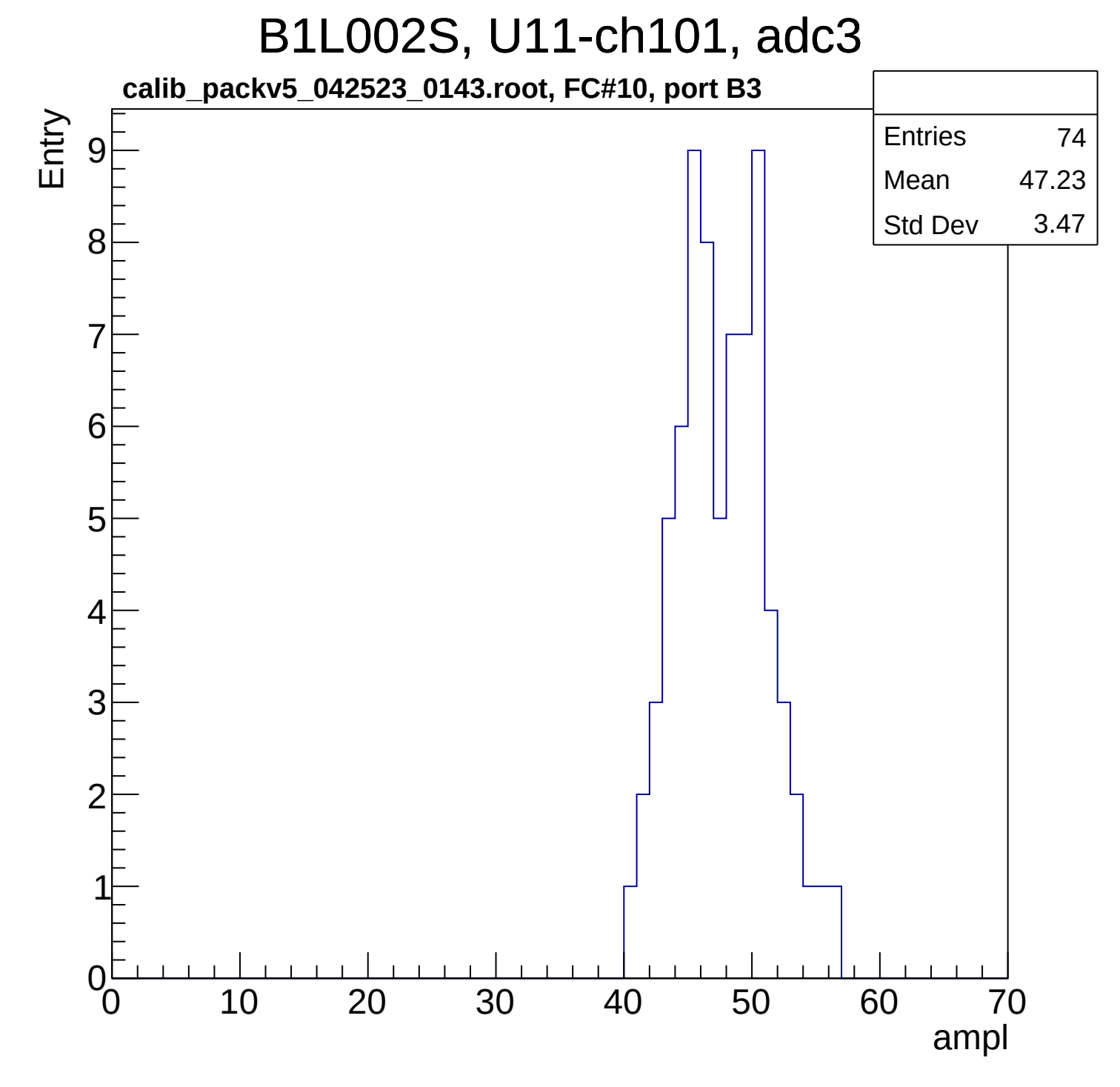
B1L002S, U11-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 47.23
Std Dev 3.47
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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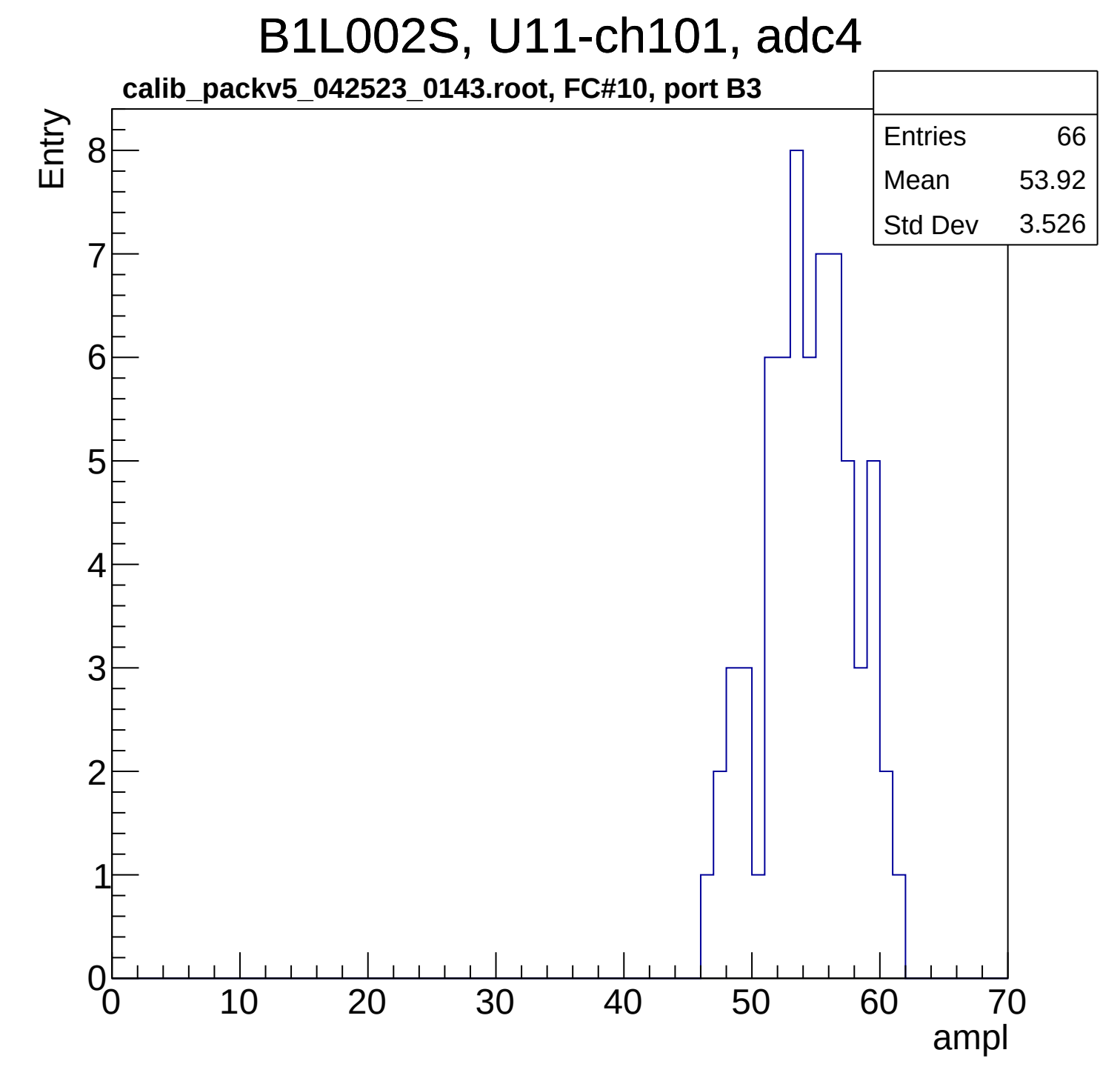
B1L002S, U11-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 53.92
Std Dev 3.526
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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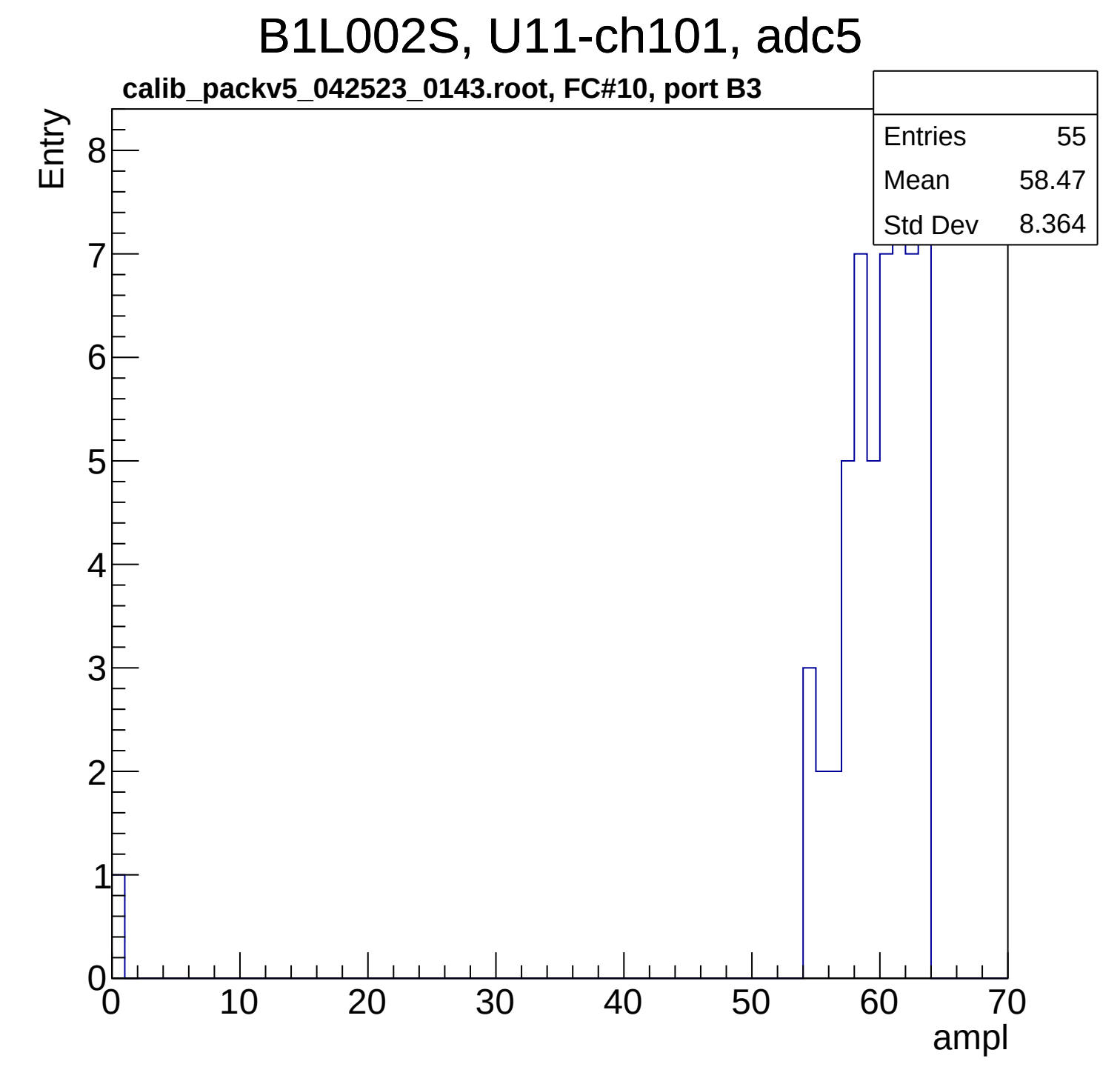
B1L002S, U11-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.47
Std Dev 8.364
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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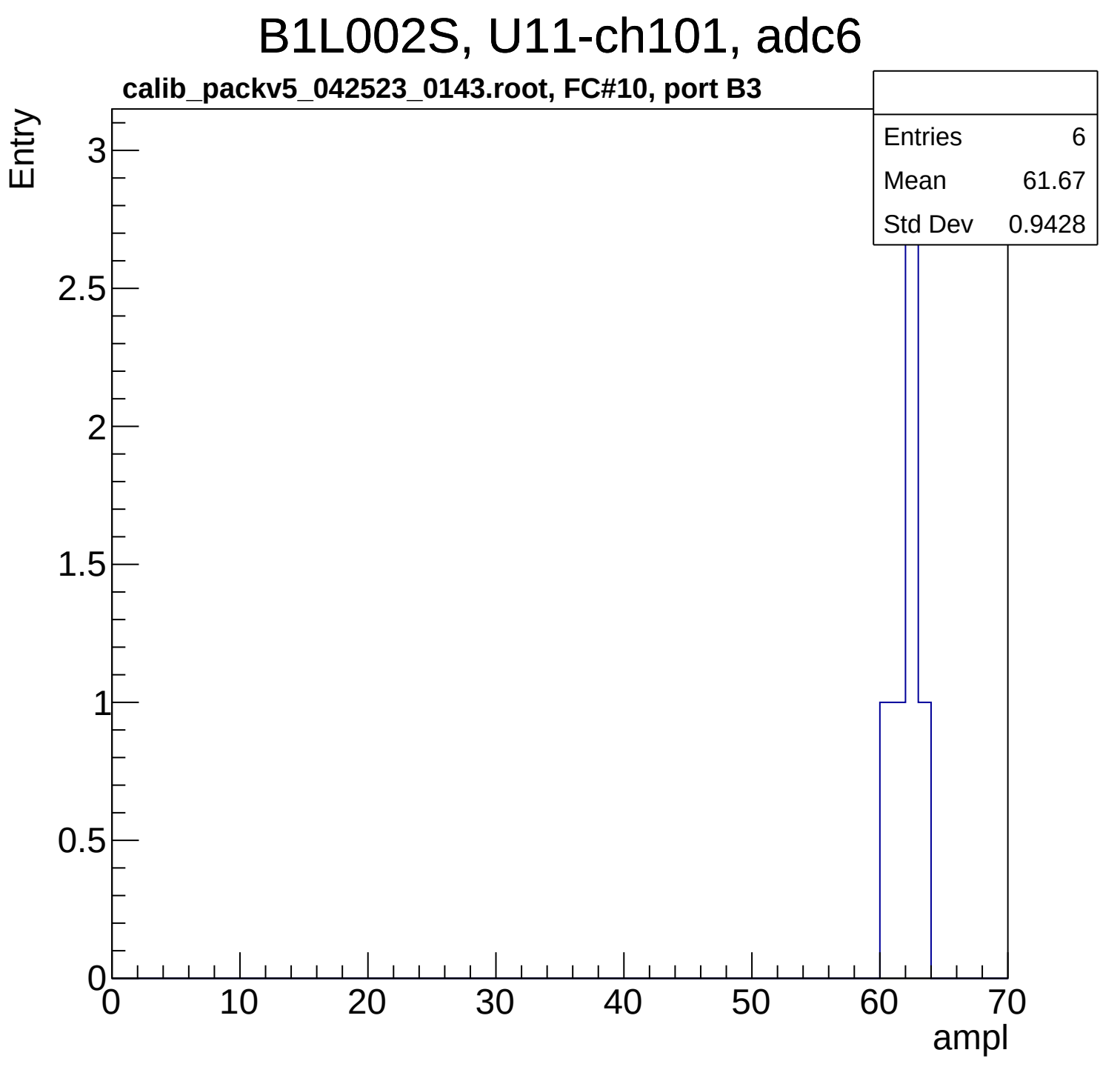
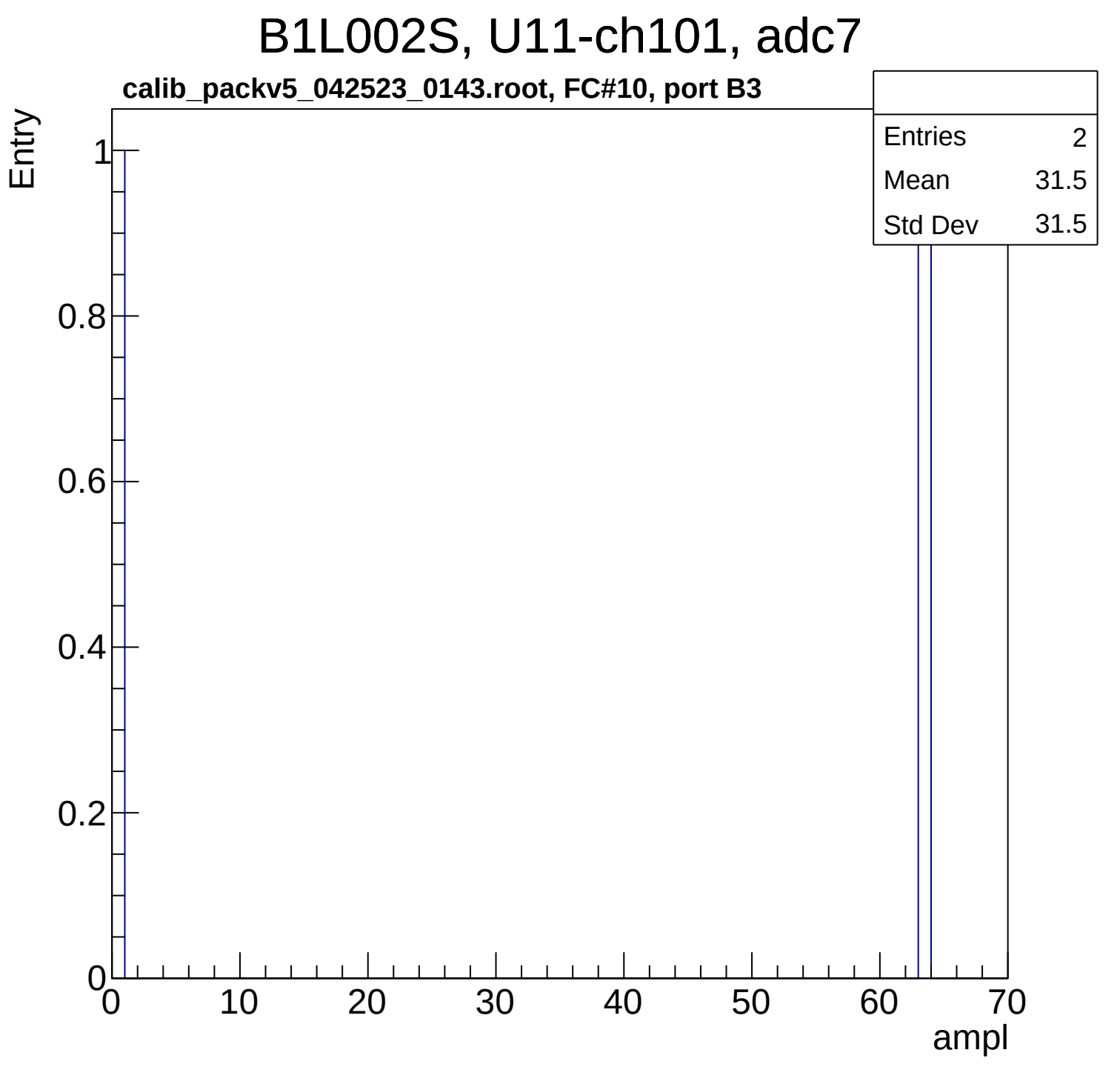
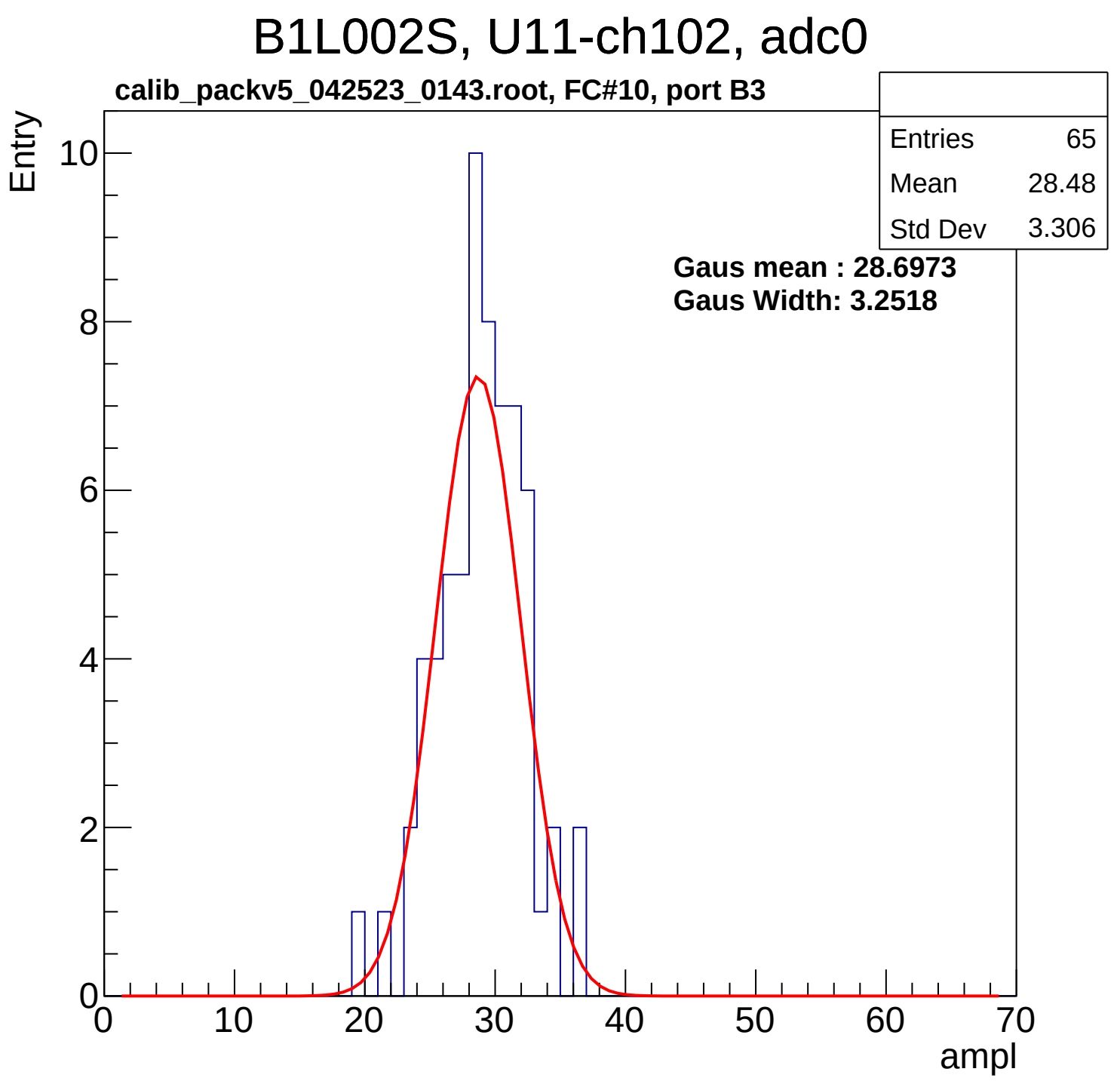
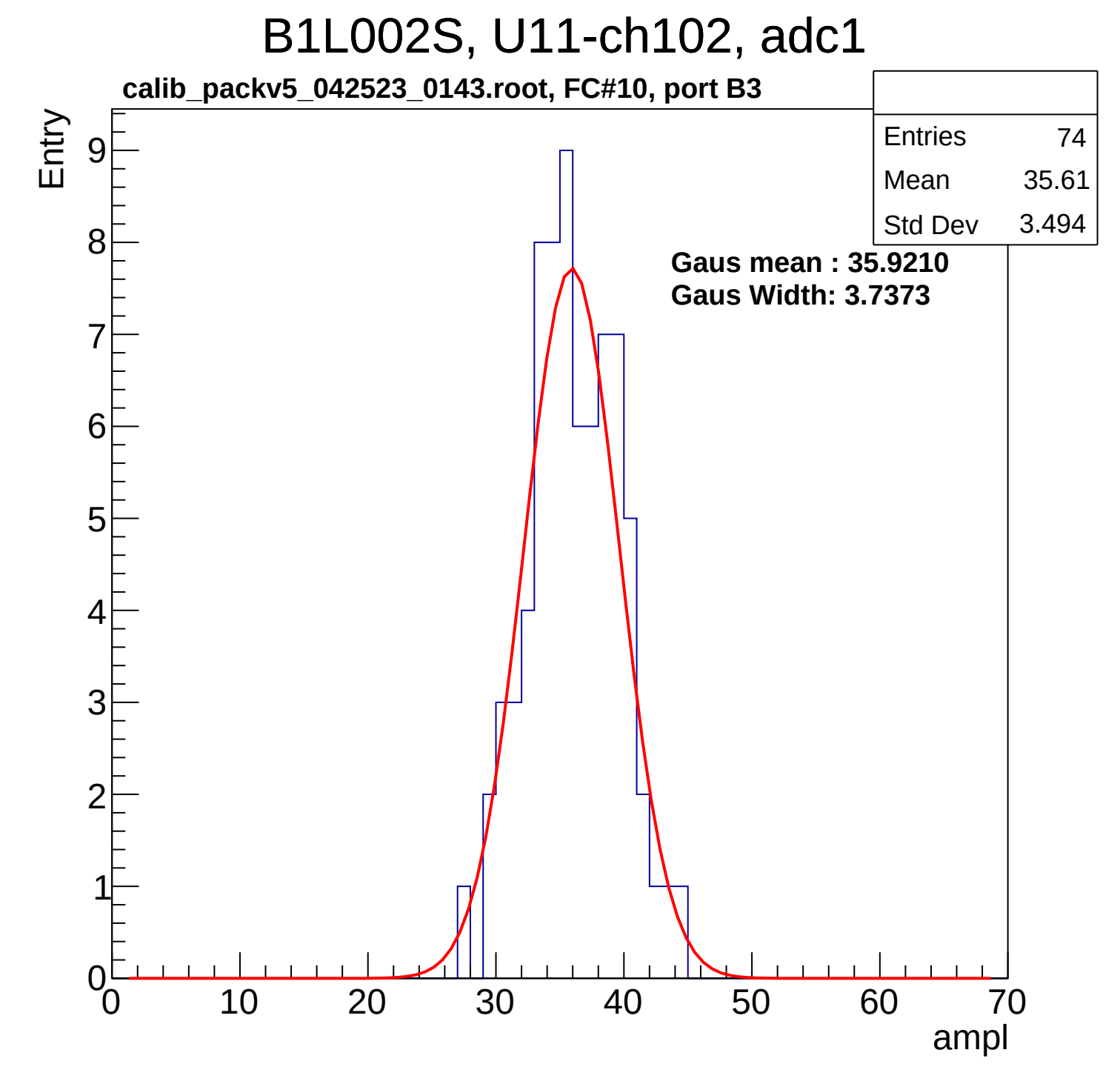
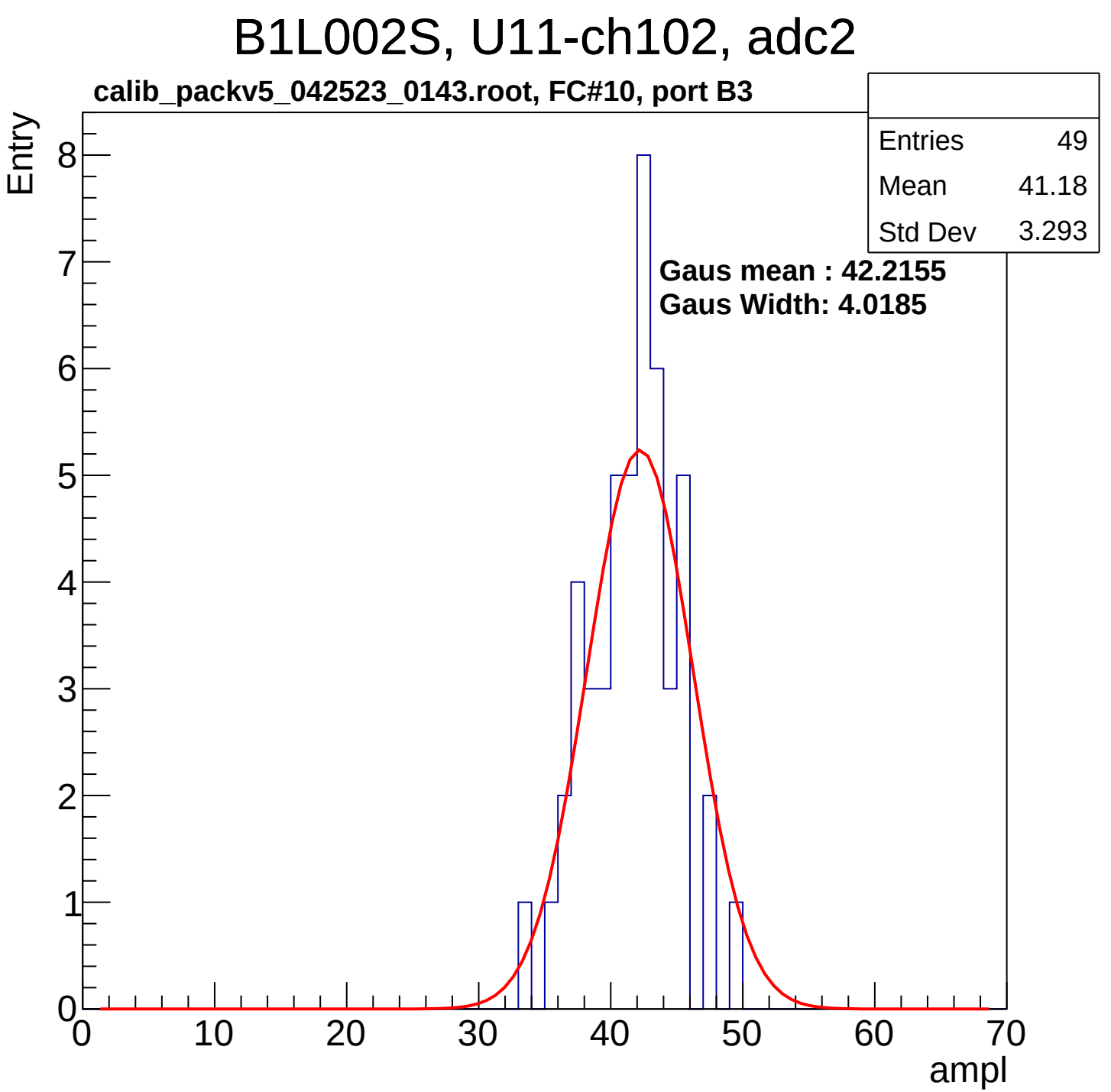

B1L002S, U11-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.48
Std Dev 3.306
Gaus mean : 28.6973
Gaus Width: 3.2518
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.61
Std Dev 3.494
Gaus mean : 35.9210
Gaus Width: 3.7373
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 41.18
Std Dev 3.293
Gaus mean : 42.2155
Gaus Width: 4.0185
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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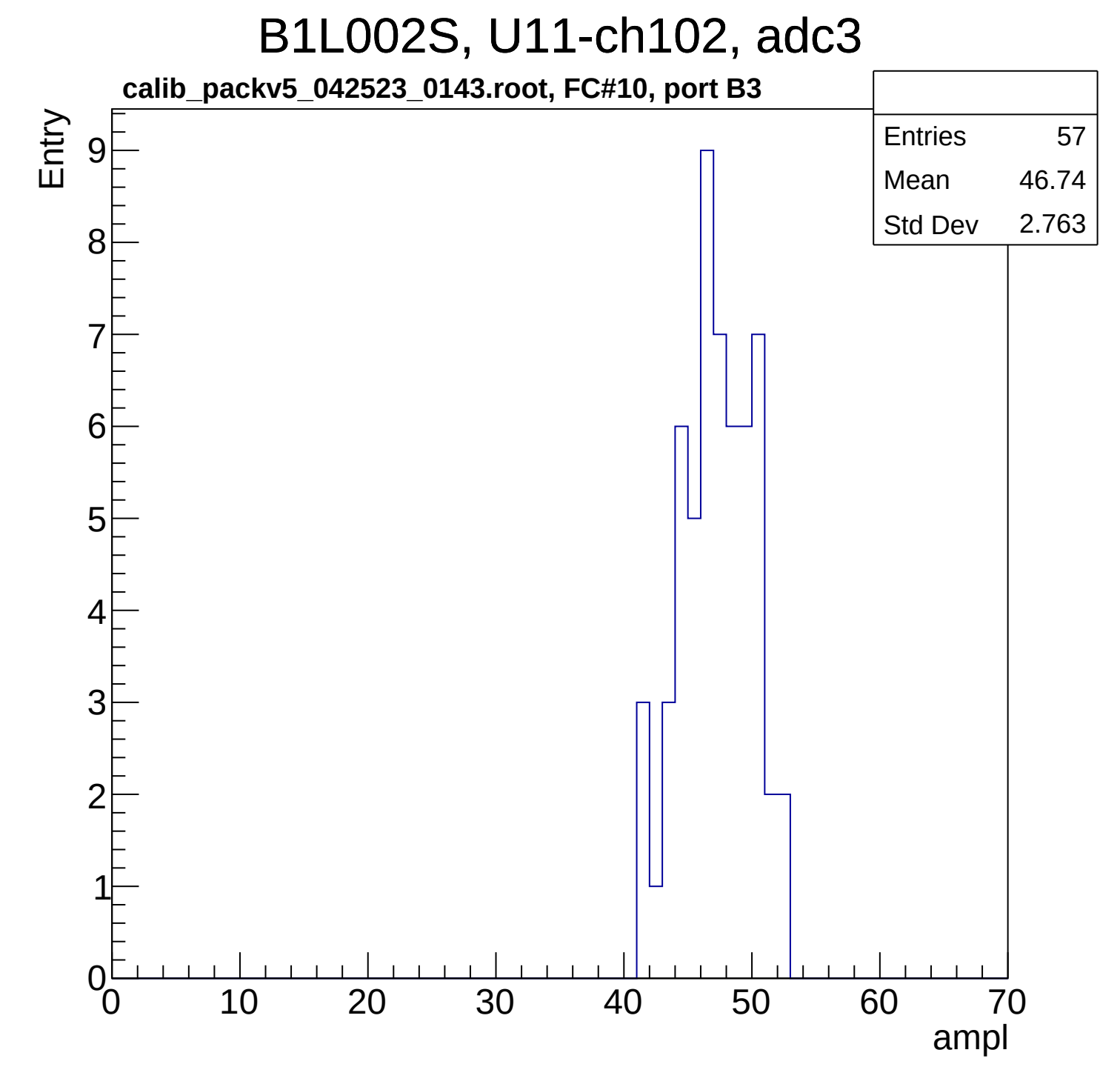
B1L002S, U11-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 46.74
Std Dev 2.763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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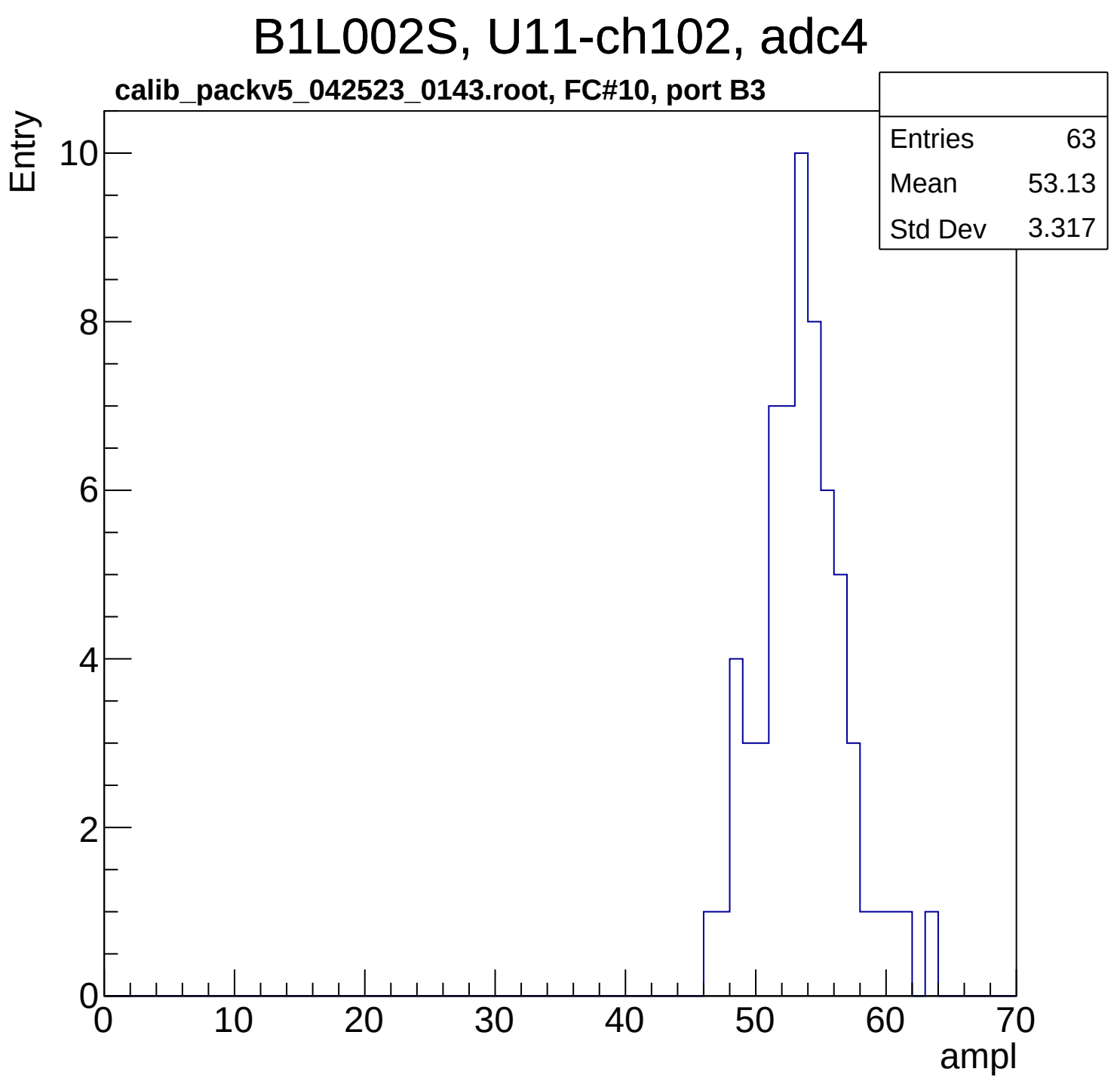
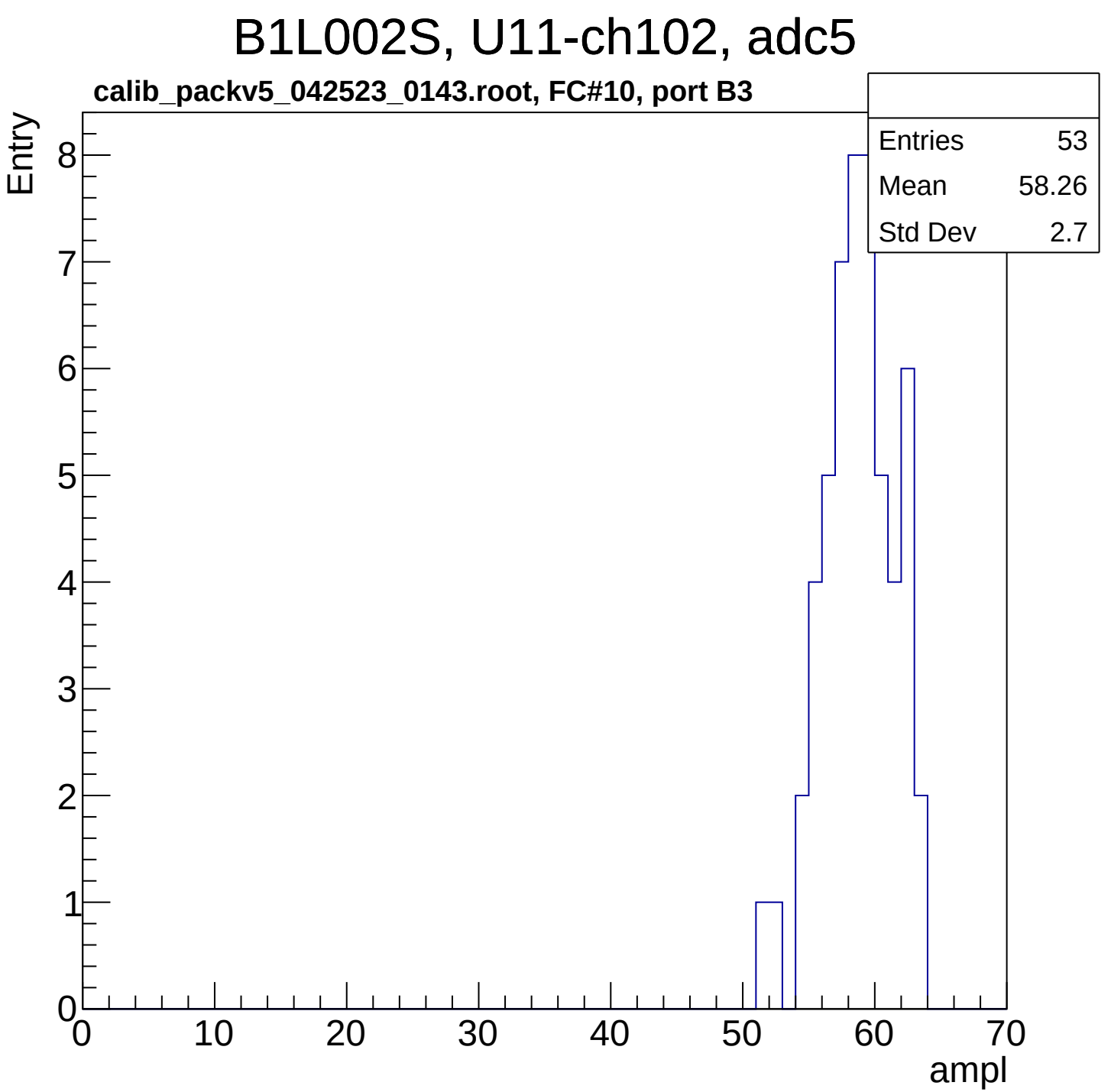

B1L002S, U11-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.13
Std Dev 3.317
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.26
Std Dev 2.7
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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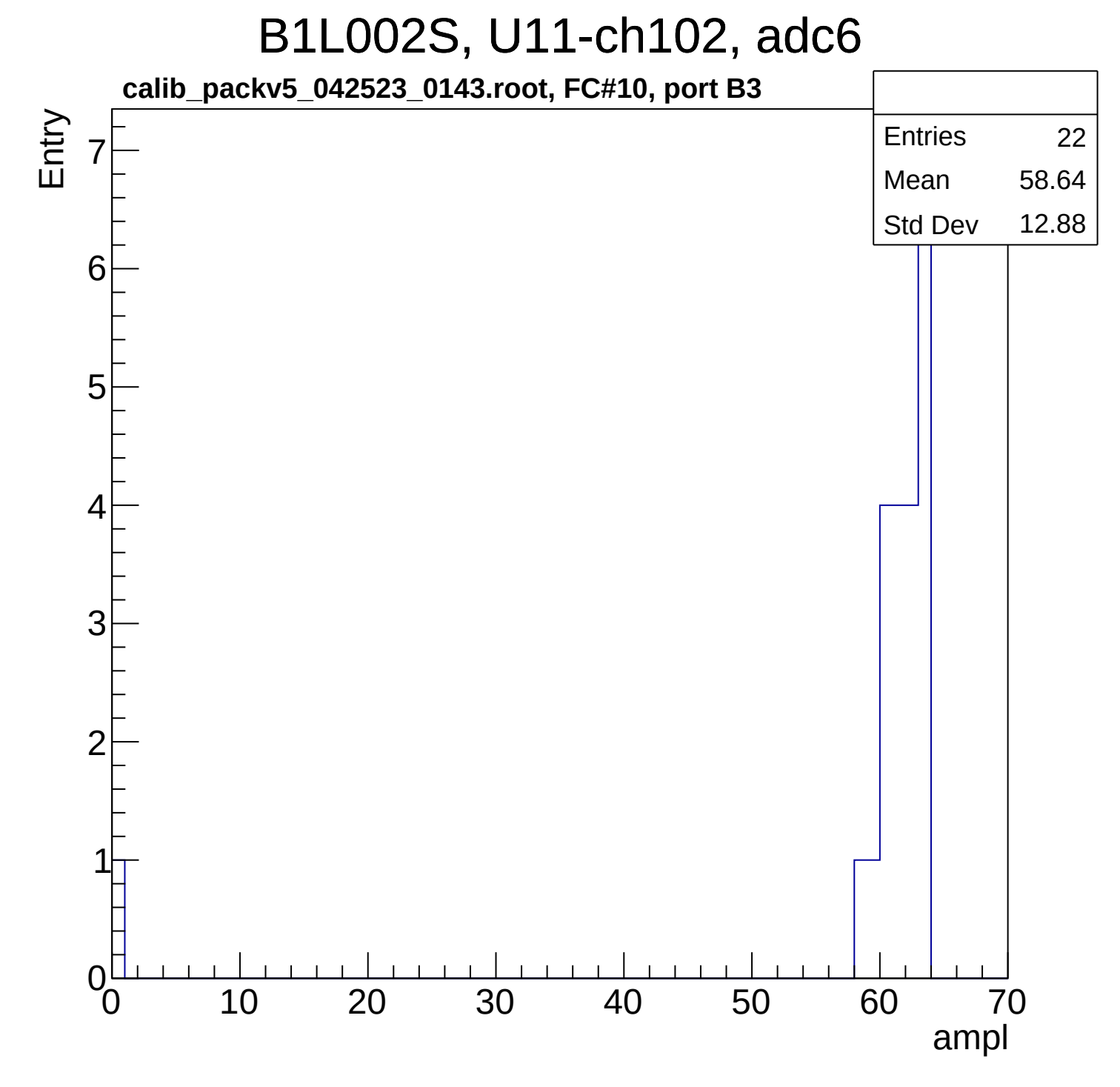
B1L002S, U11-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.64
Std Dev 12.88
Entry
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1
2
3
4
5
6
7
0
10
20
30
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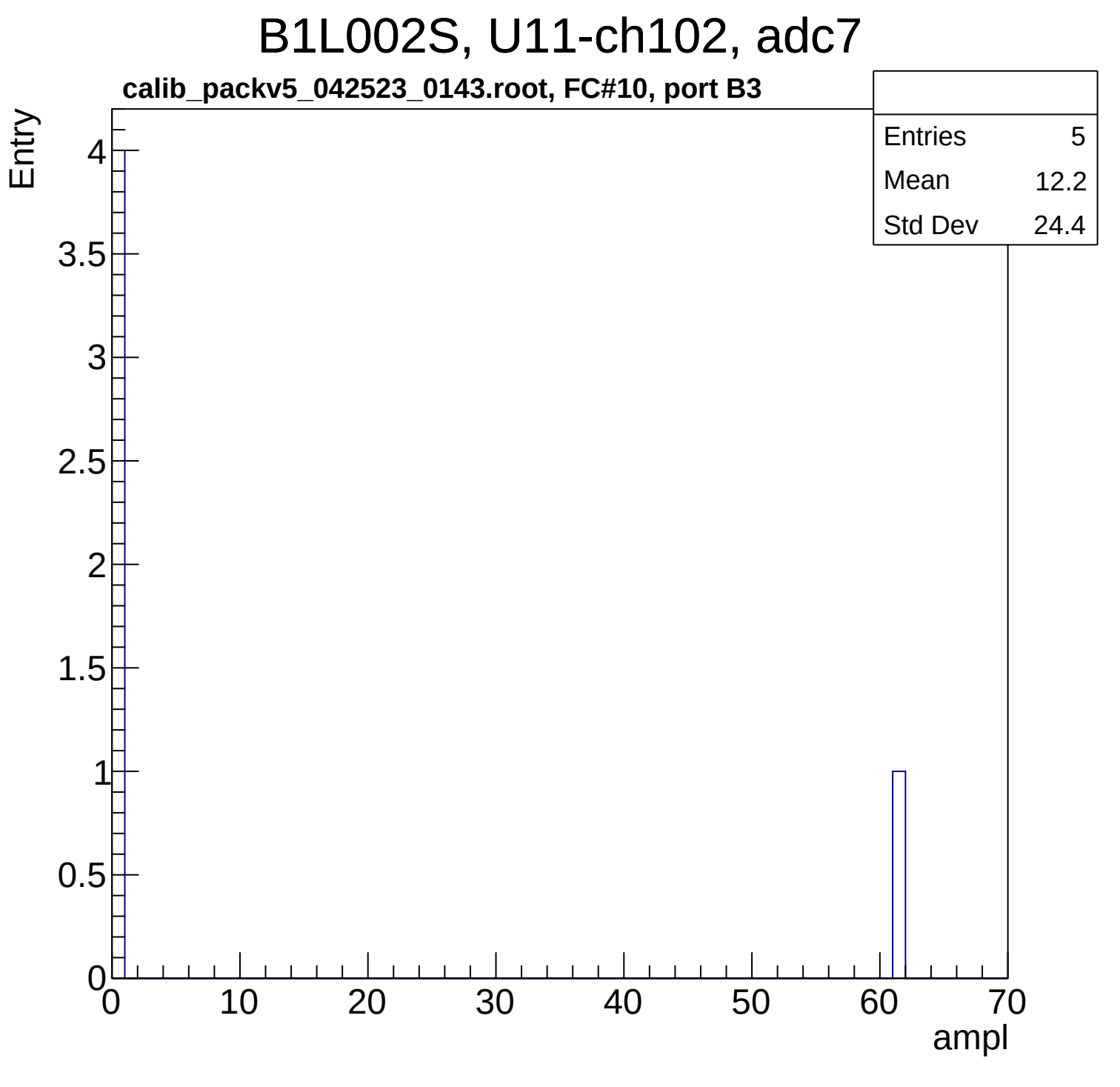
50
60
70
ampl


B1L002S, U11-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 12.2
Std Dev 24.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
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30
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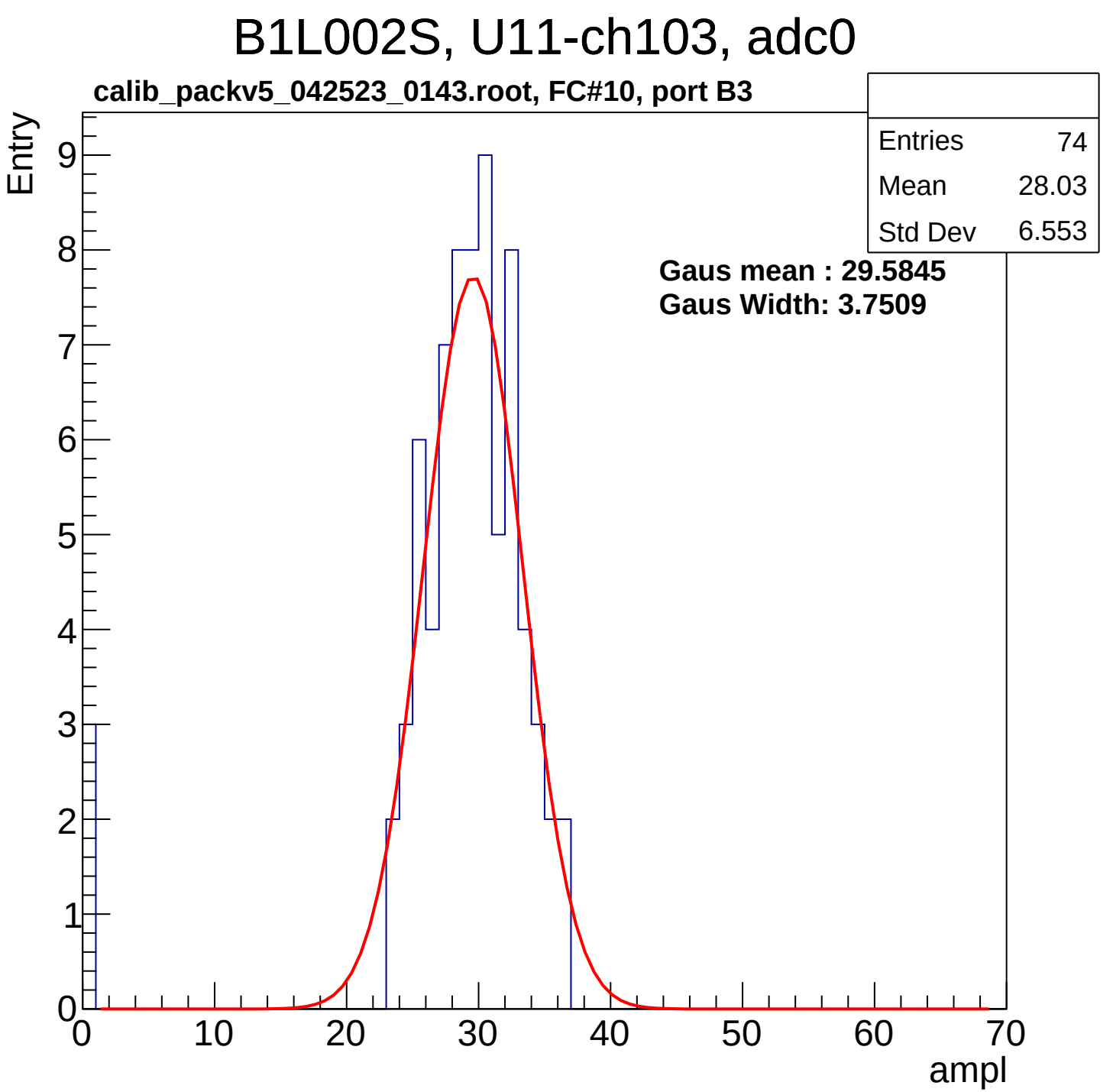
50
60
70
ampl


B1L002S, U11-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.03
Std Dev 6.553
Gaus mean : 29.5845
Gaus Width: 3.7509
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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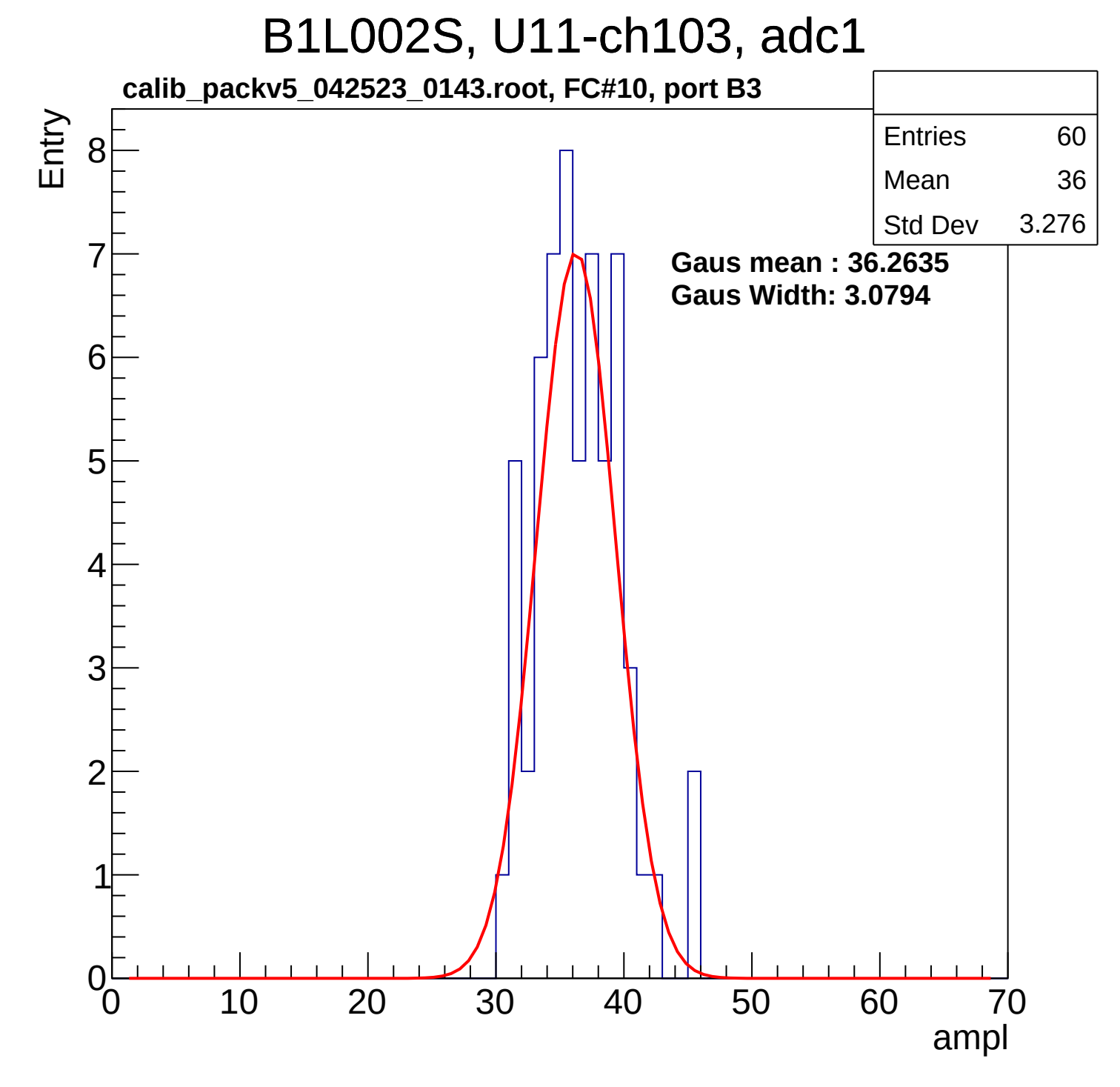
B1L002S, U11-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 36
Std Dev 3.276
Gaus mean : 36.2635
Gaus Width: 3.0794
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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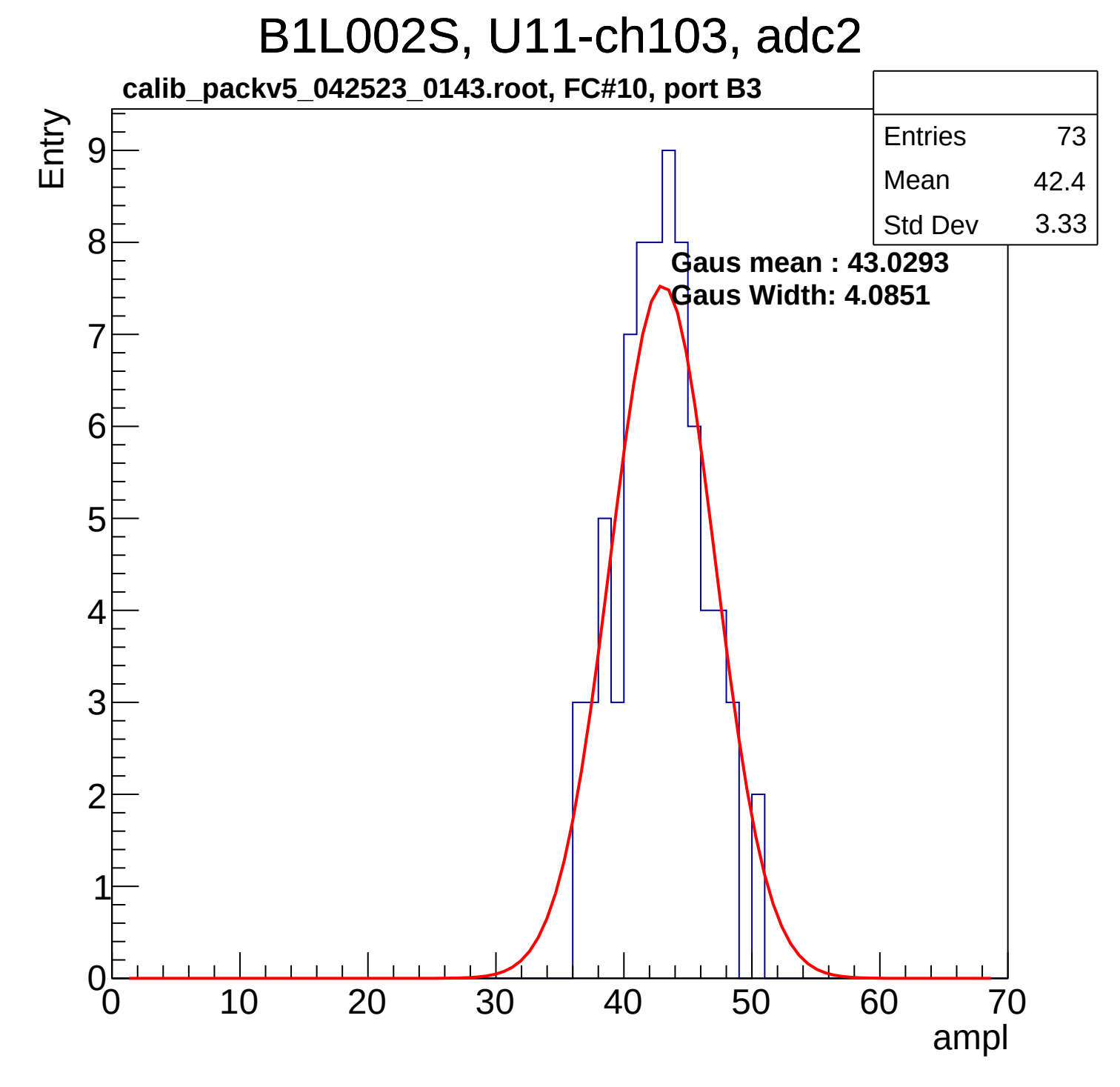
B1L002S, U11-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 42.4
Std Dev 3.33
Gaus mean : 43.0293
Gaus Width: 4.0851
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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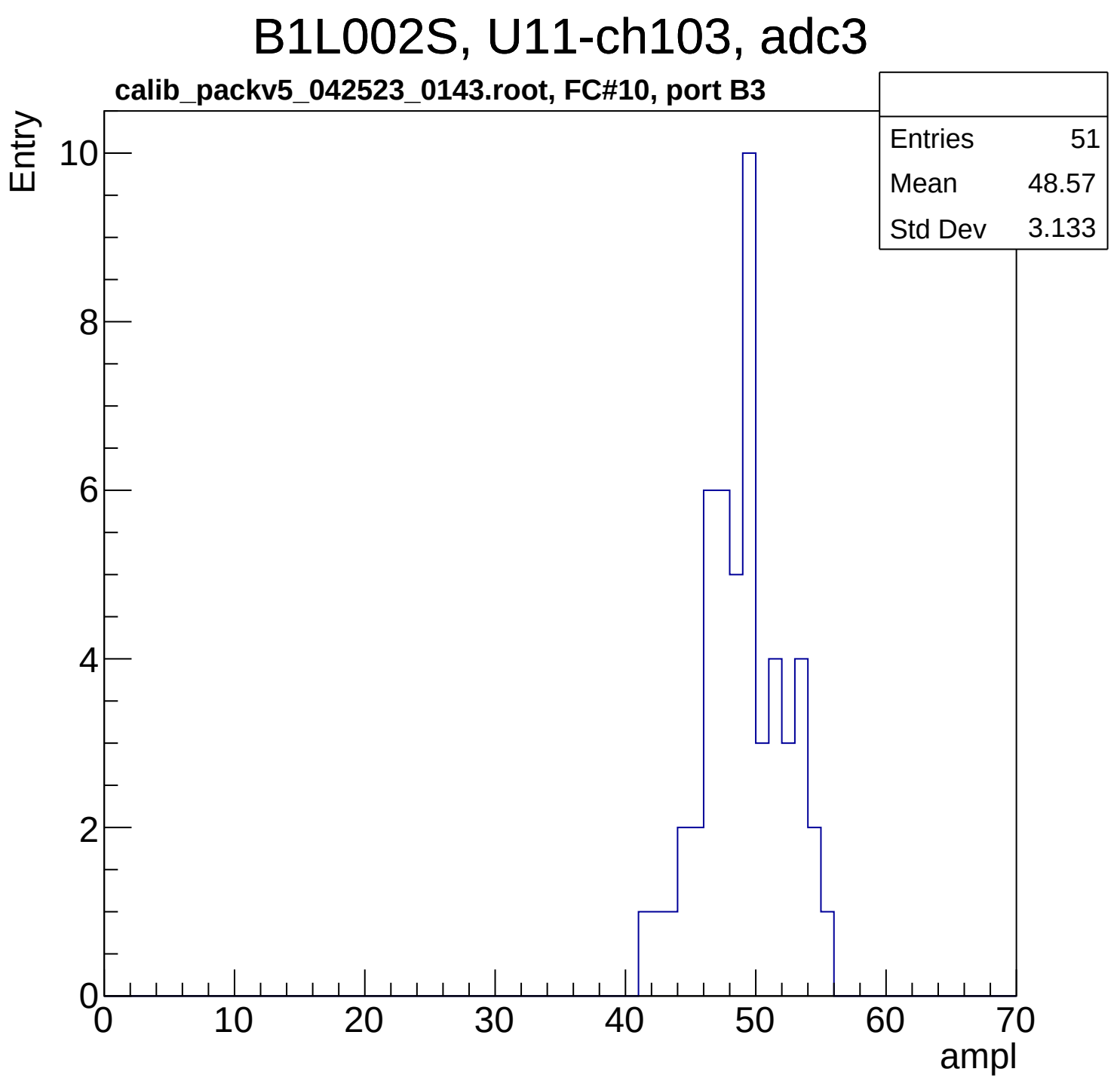
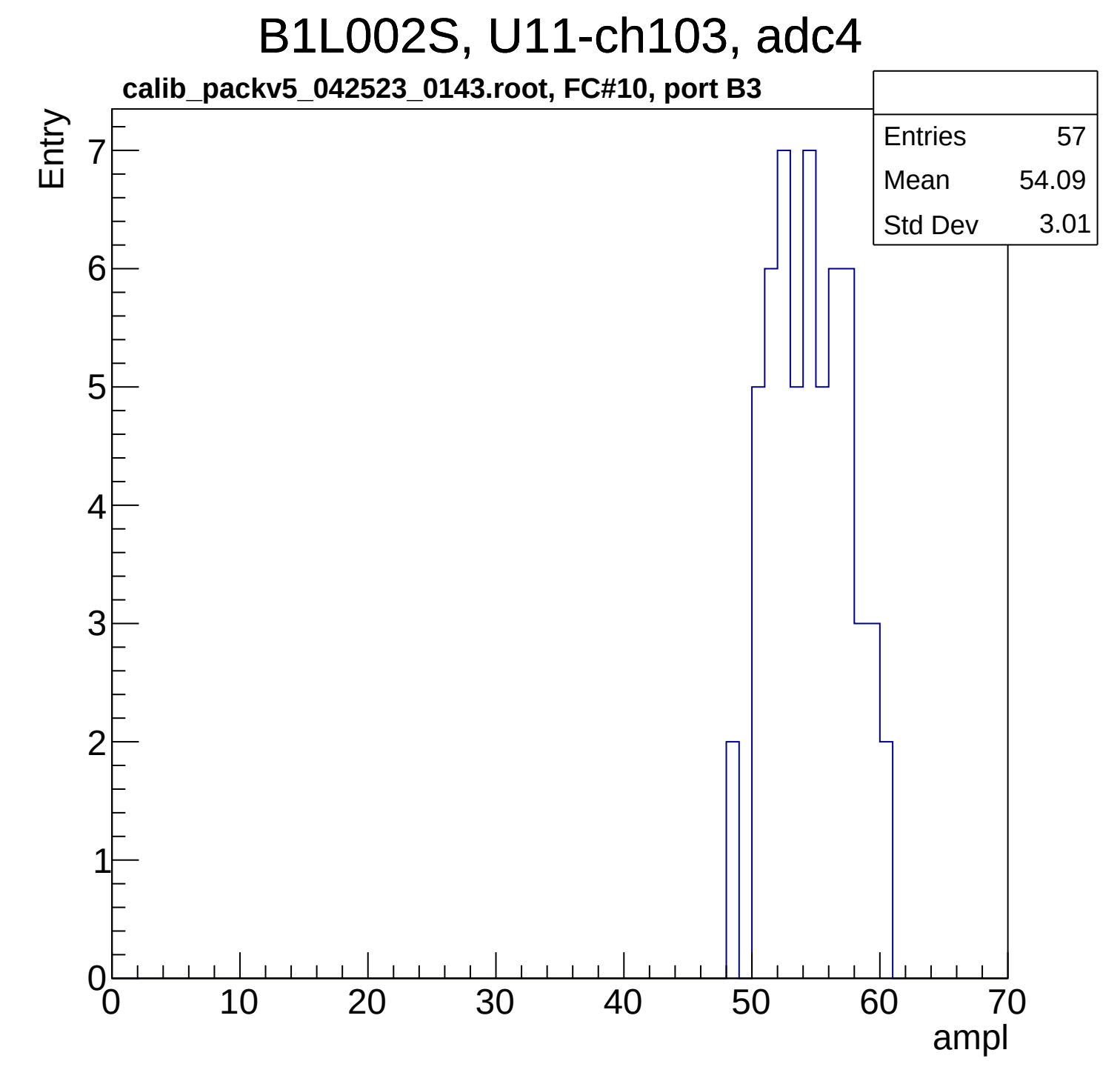

B1L002S, U11-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 48.57
Std Dev 3.133
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 54.09
Std Dev 3.01
Entry
ampl
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3
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0
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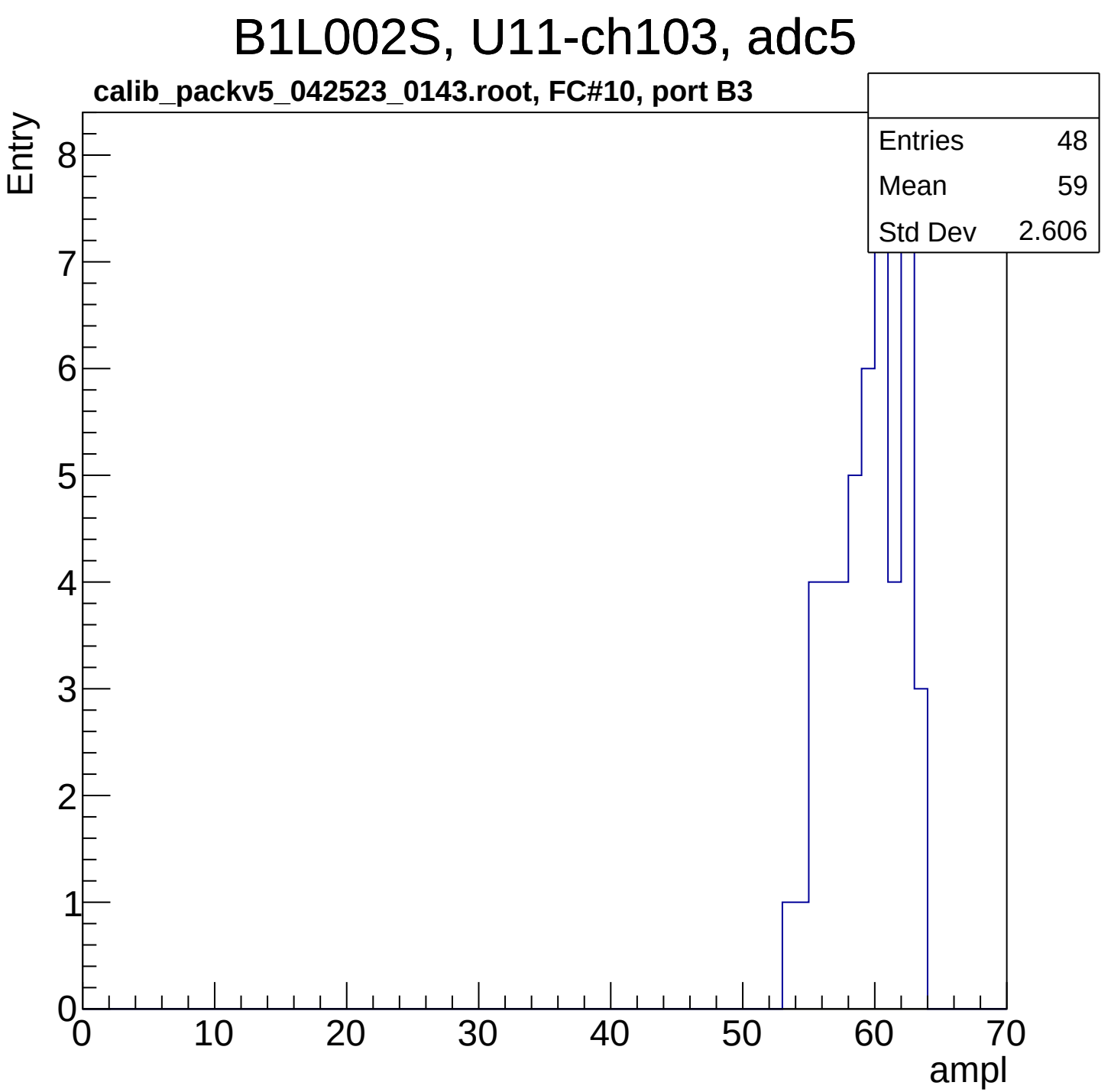
B1L002S, U11-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59
Std Dev 2.606
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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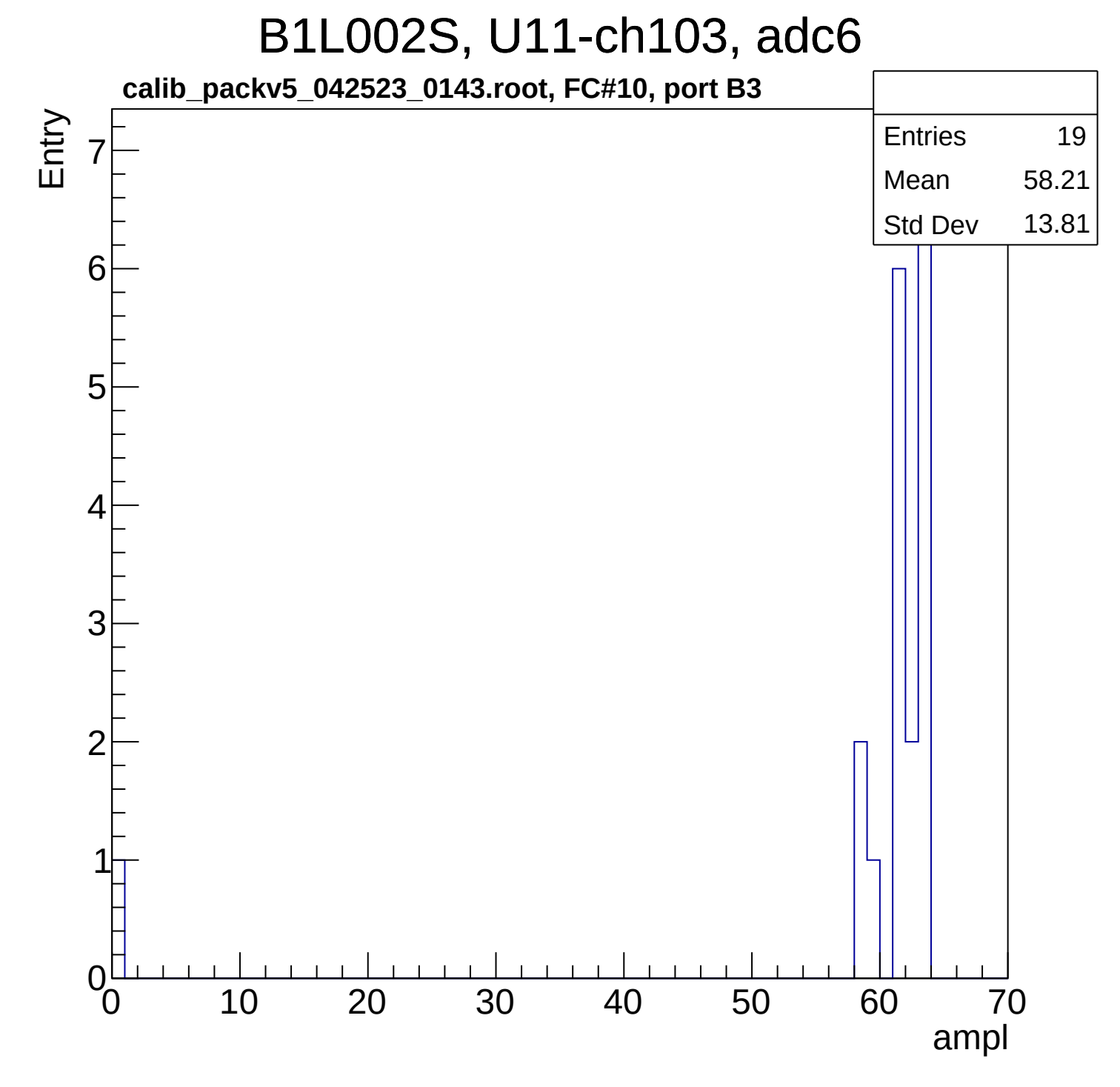
B1L002S, U11-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.21
Std Dev 13.81
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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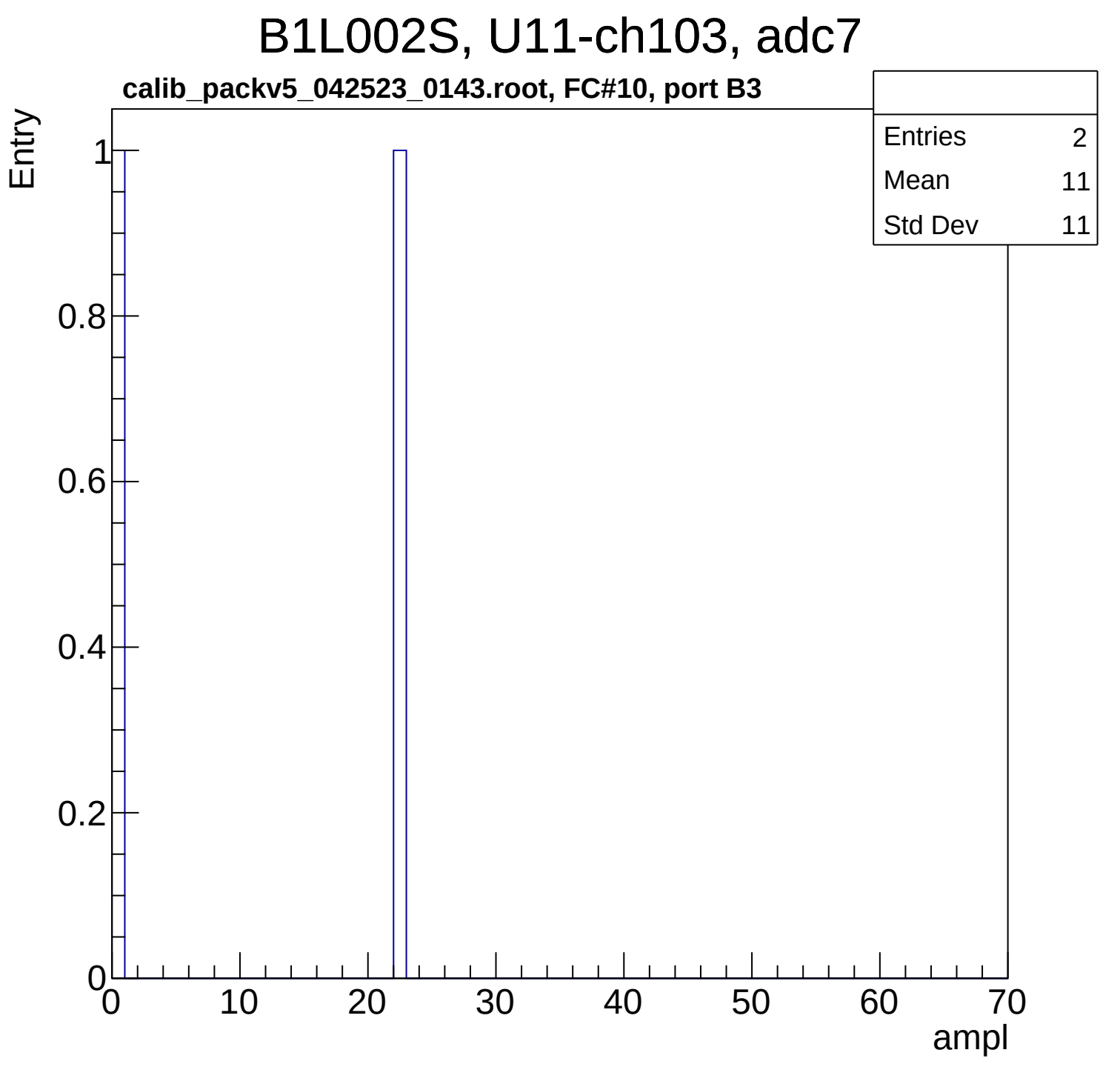
B1L002S, U11-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
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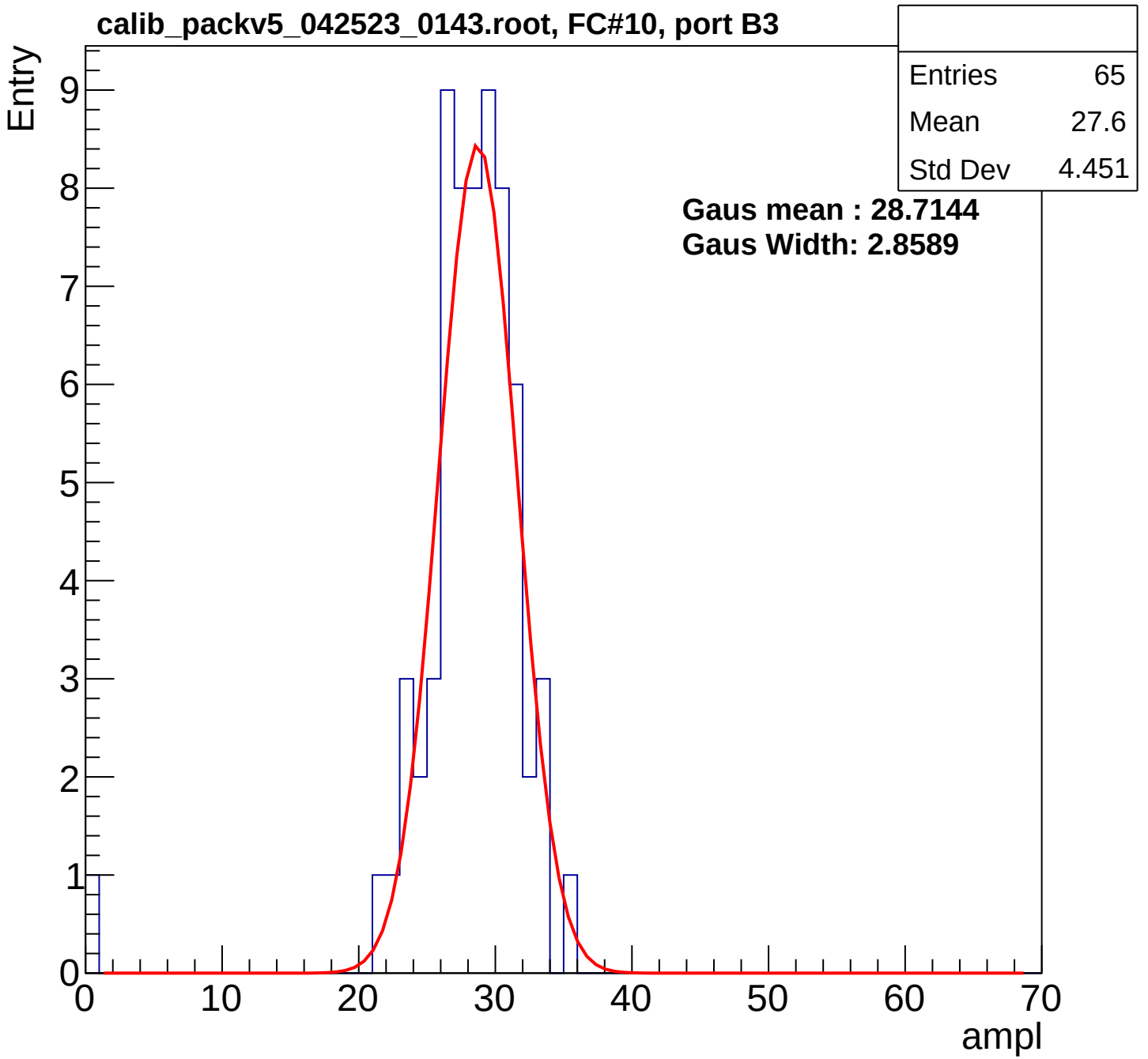
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.6
Std Dev 4.451
Gaus mean : 28.7144
Gaus Width: 2.8589
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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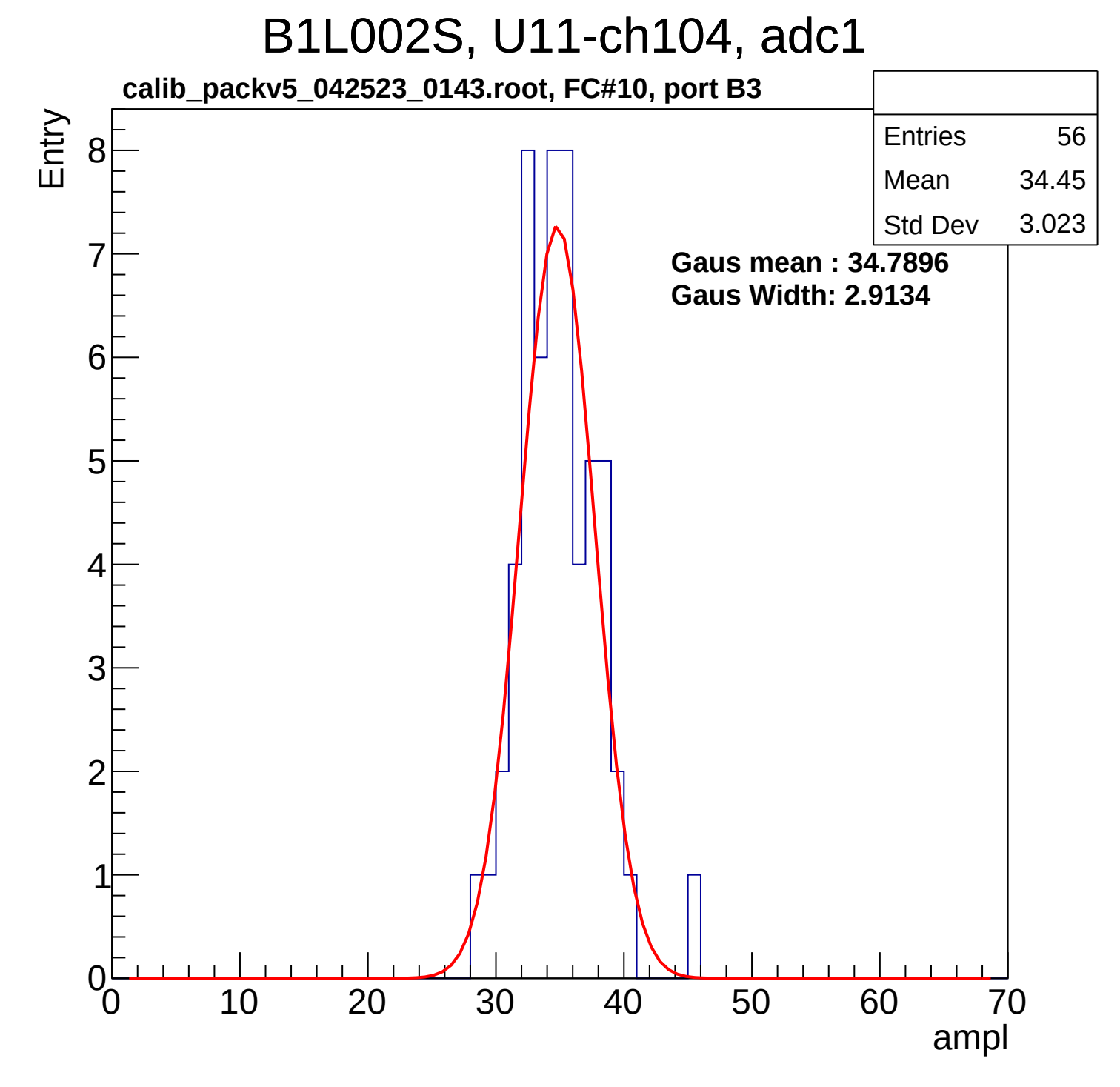
B1L002S, U11-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.45
Std Dev 3.023
Gaus mean : 34.7896
Gaus Width: 2.9134
Entry
ampl
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2
3
4
5
6
7
8
0
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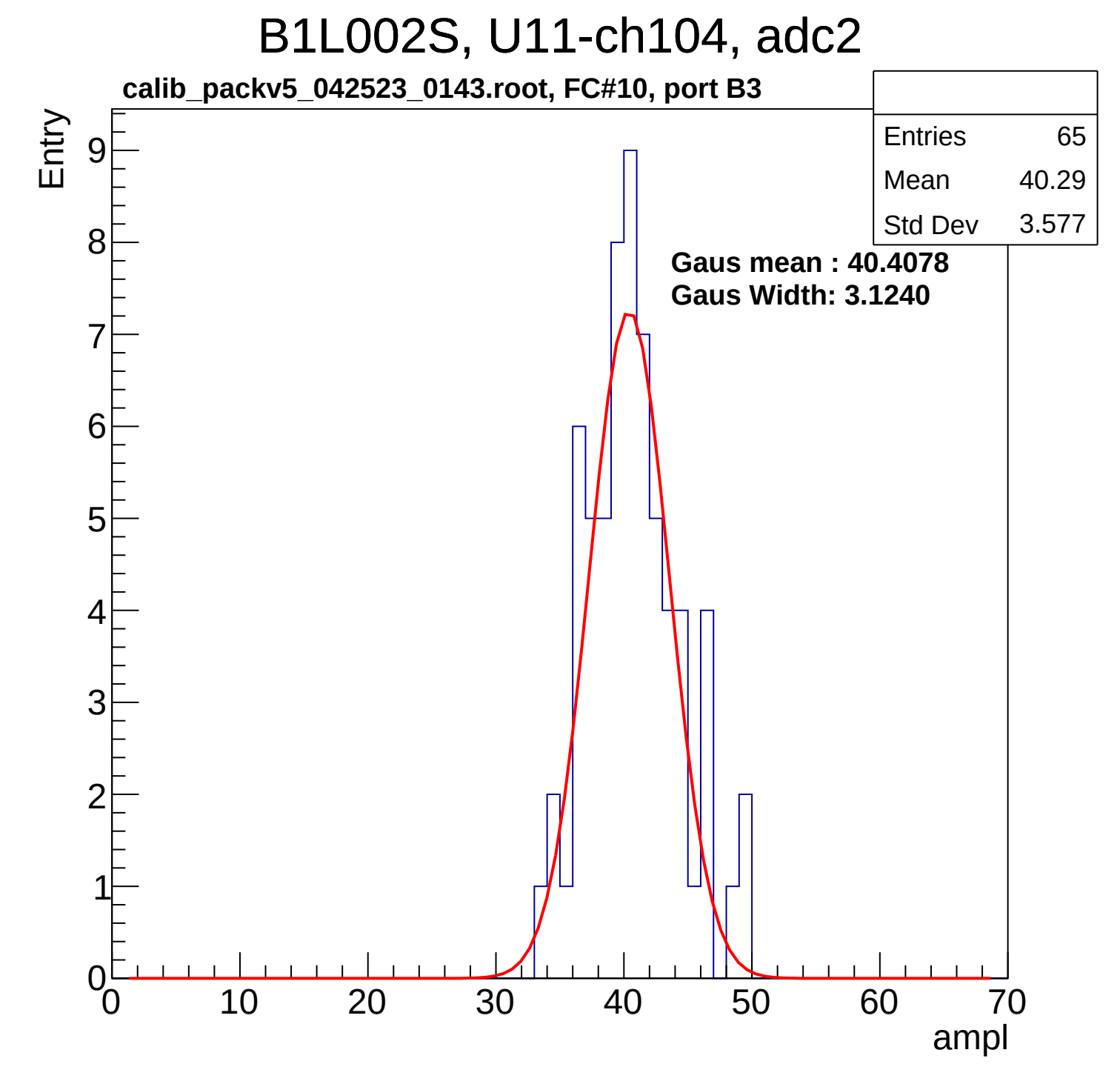
B1L002S, U11-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.29
Std Dev 3.577
Gaus mean : 40.4078
Gaus Width: 3.1240
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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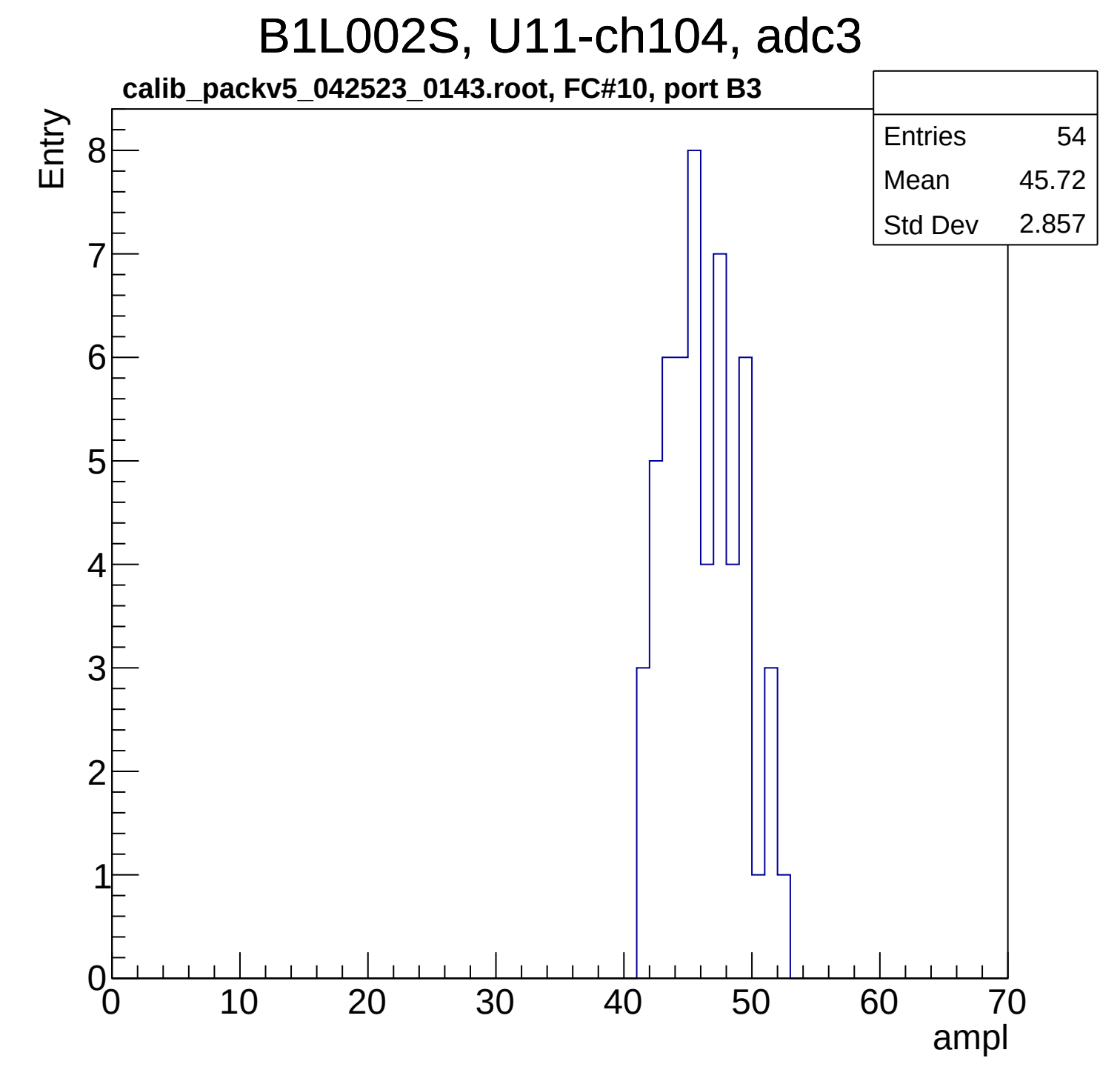
B1L002S, U11-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 45.72
Std Dev 2.857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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60
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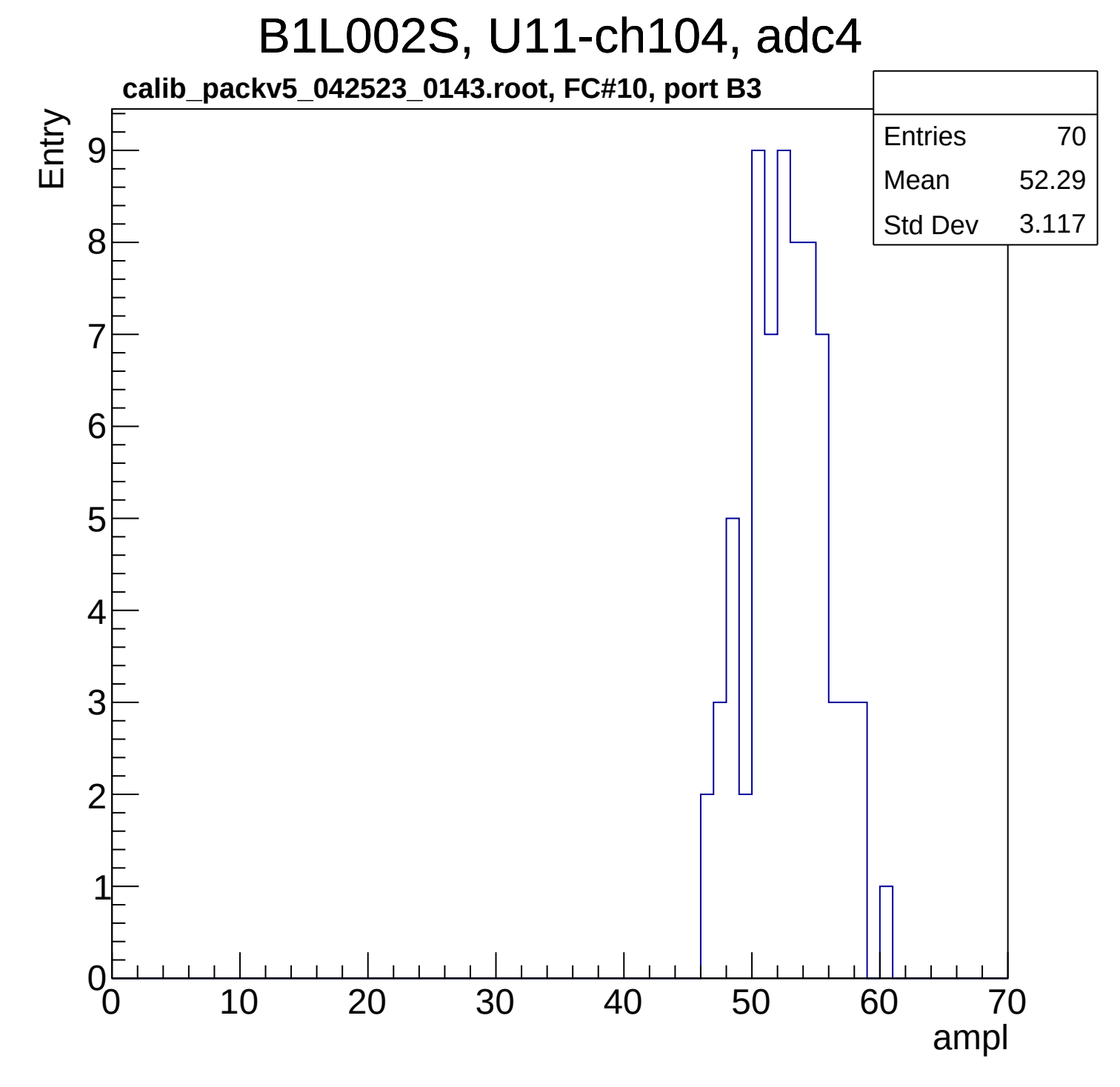
B1L002S, U11-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 52.29
Std Dev 3.117
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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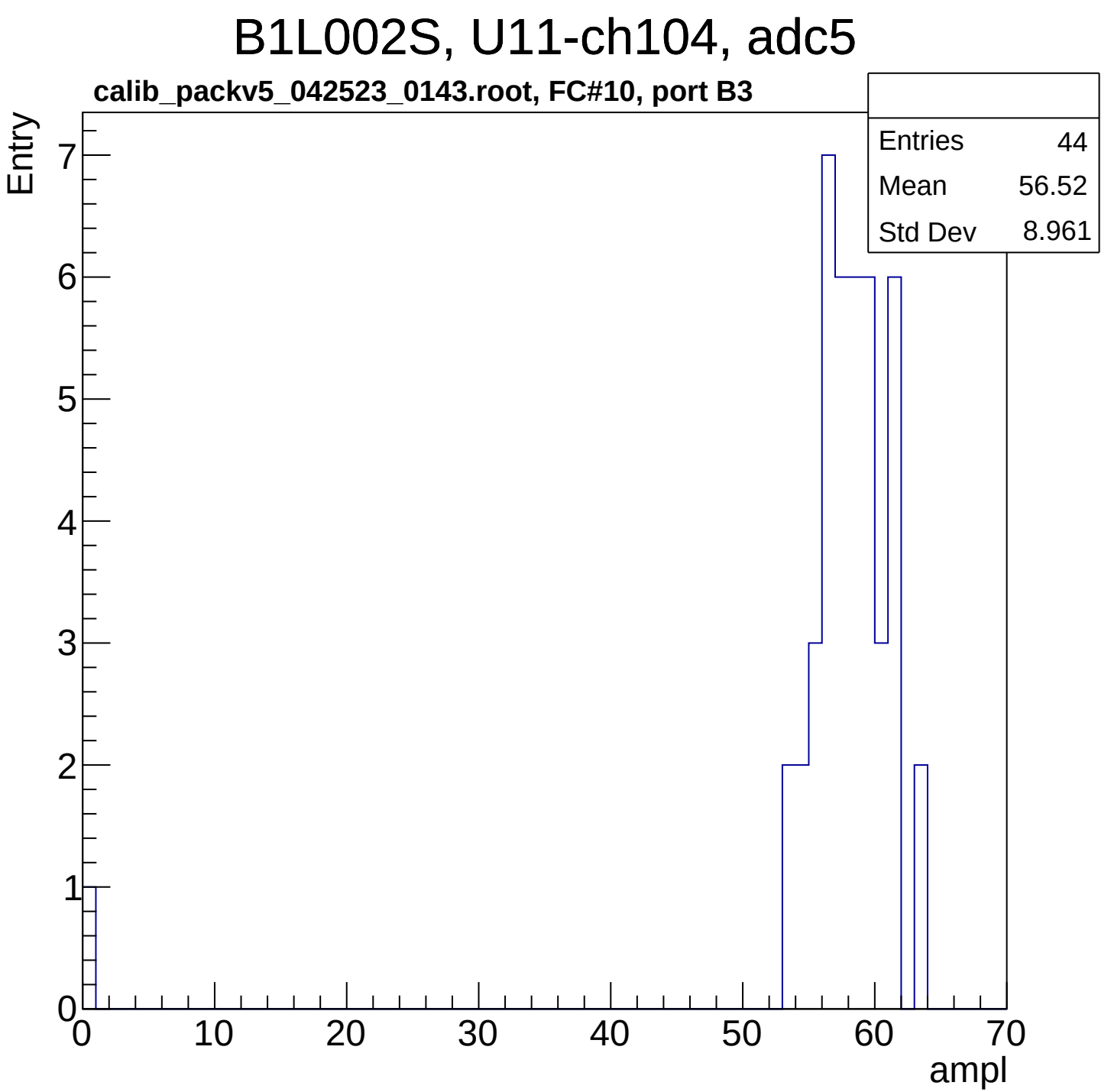
B1L002S, U11-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 56.52
Std Dev 8.961
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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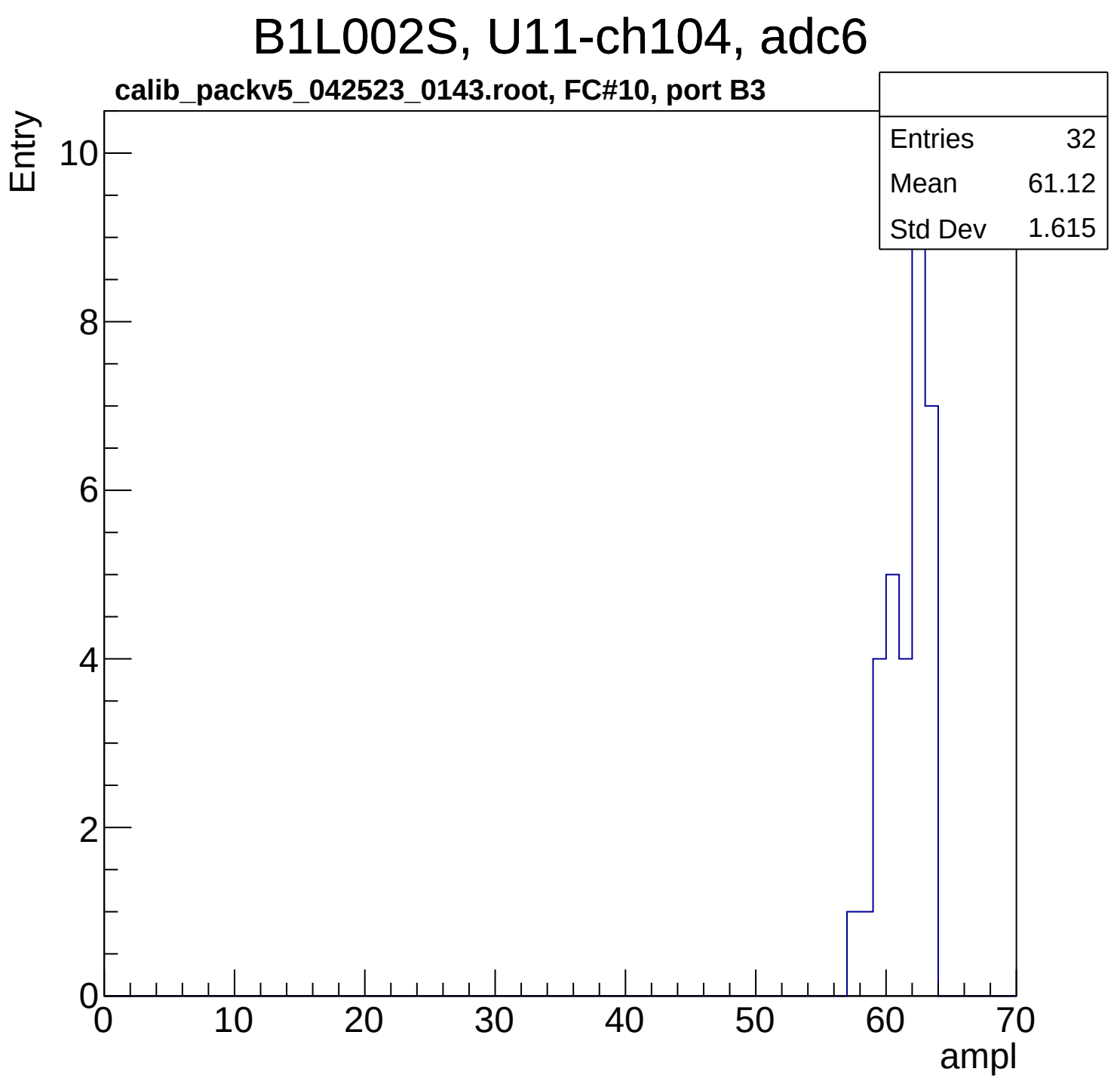
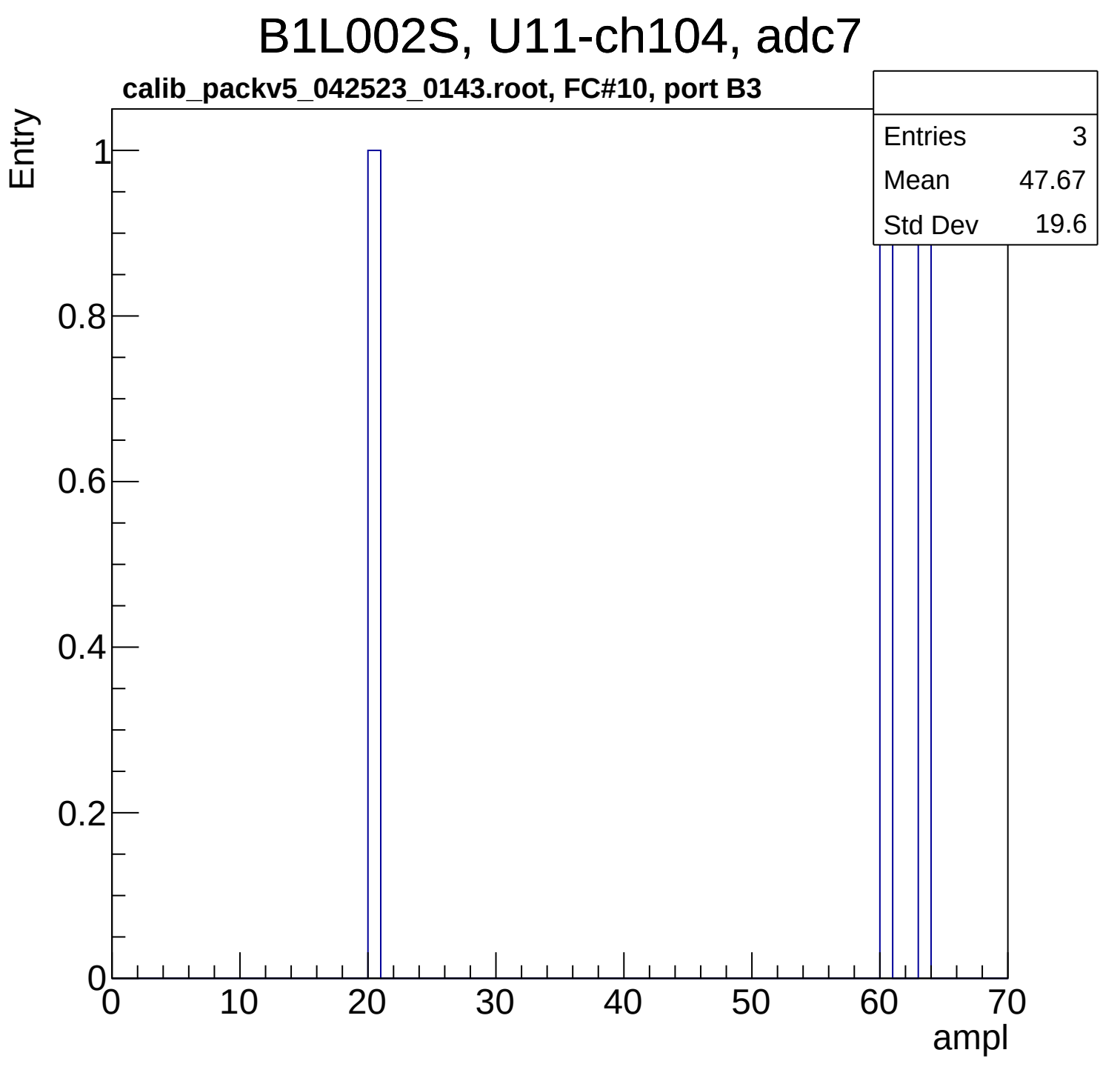

B1L002S, U11-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 61.12
Std Dev 1.615
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 47.67
Std Dev 19.6
Entry
ampl
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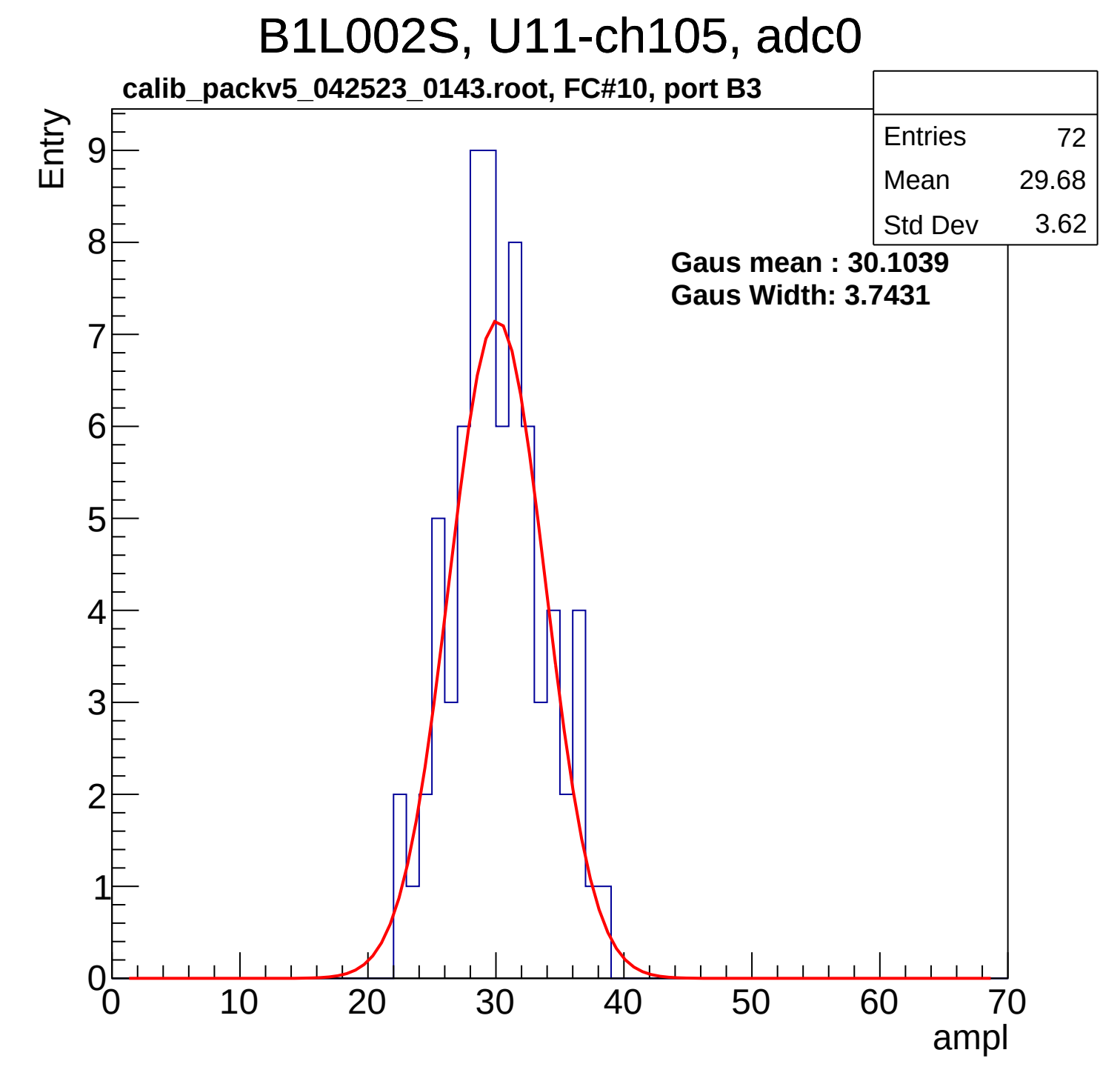
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U11-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 29.68
Std Dev 3.62
Gaus mean : 30.1039
Gaus Width: 3.7431
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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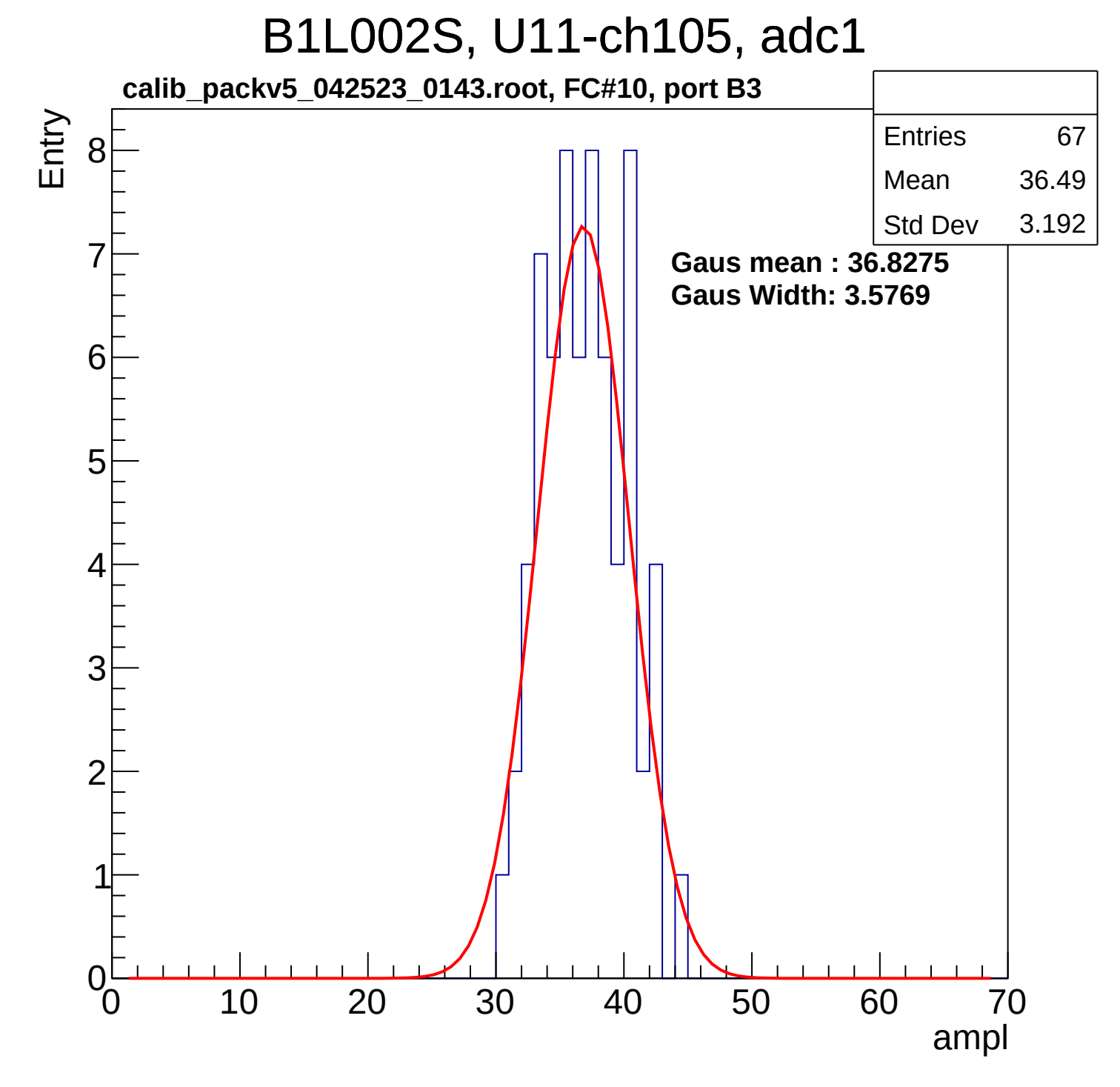
B1L002S, U11-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 36.49
Std Dev 3.192
Gaus mean : 36.8275
Gaus Width: 3.5769
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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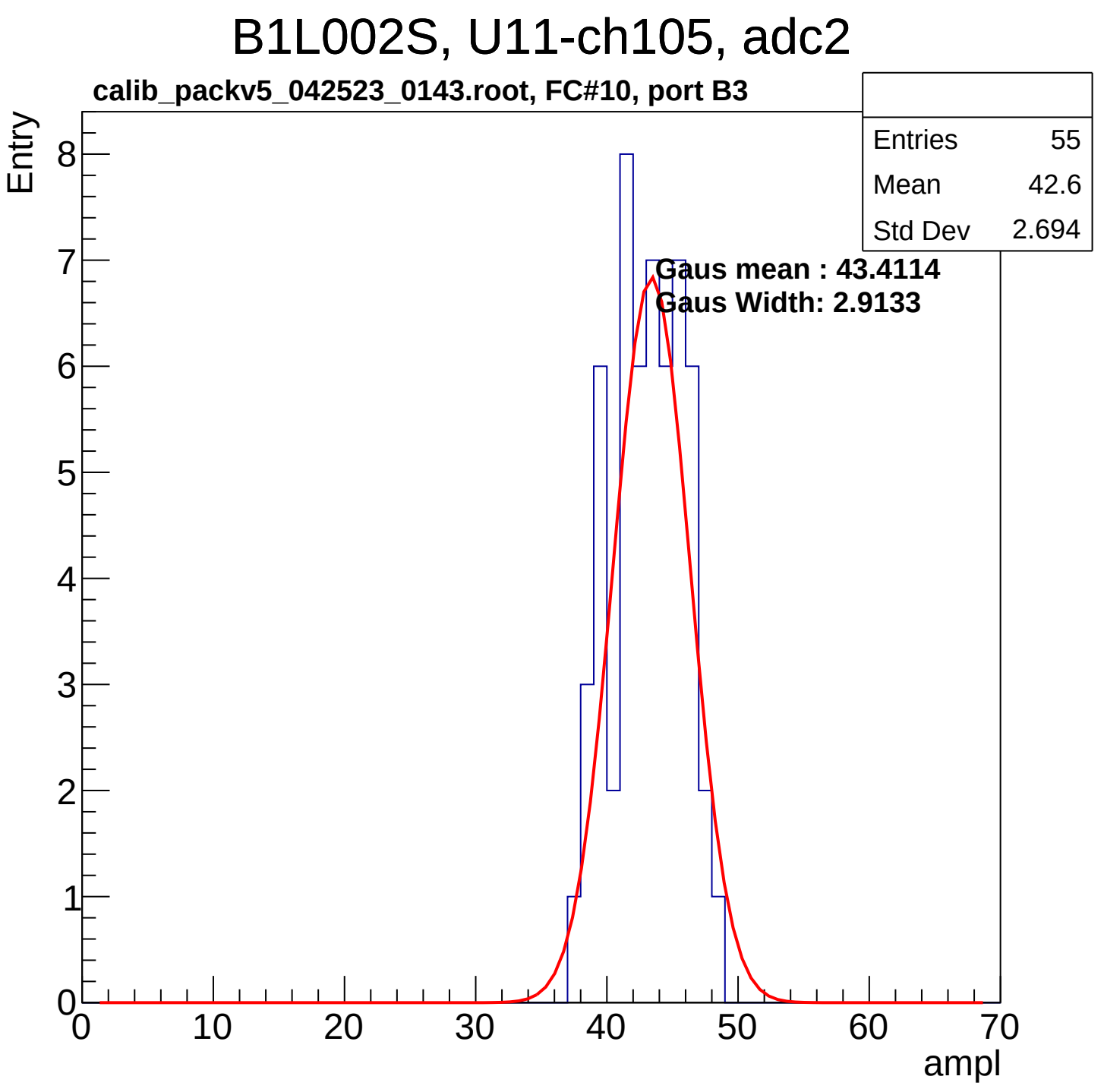
B1L002S, U11-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 42.6
Std Dev 2.694
Gaus mean : 43.4114
Gaus Width: 2.9133
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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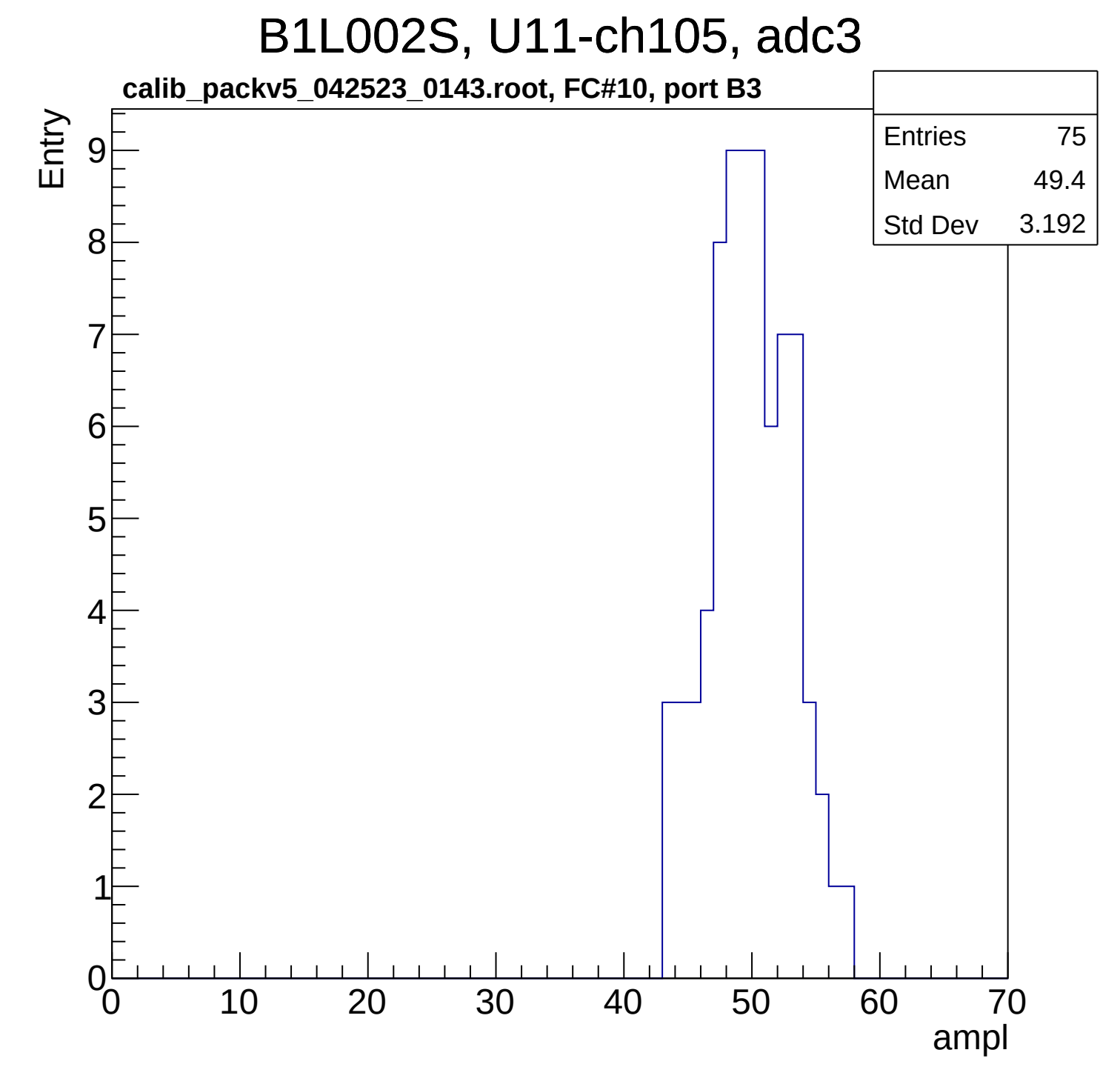
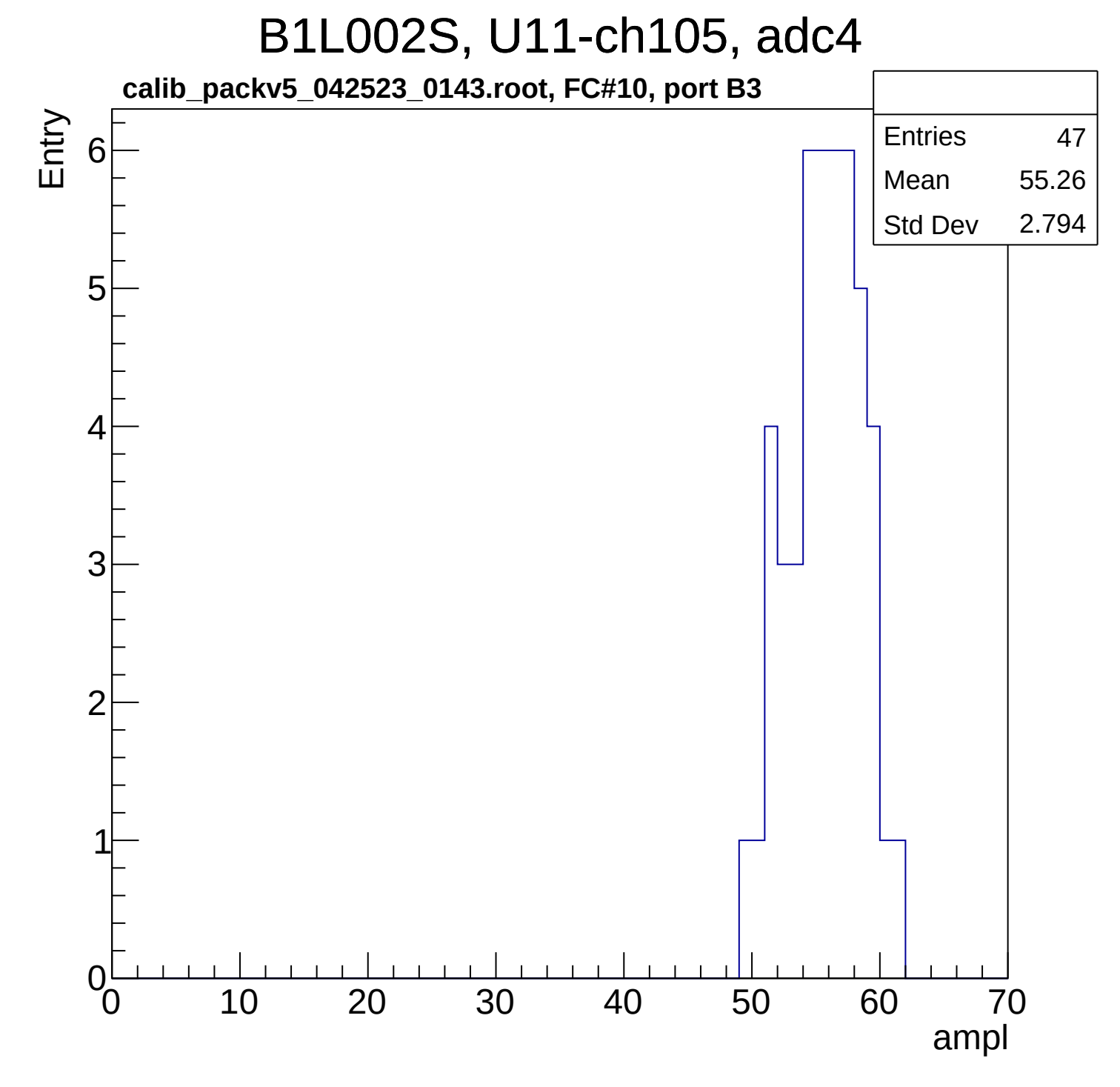
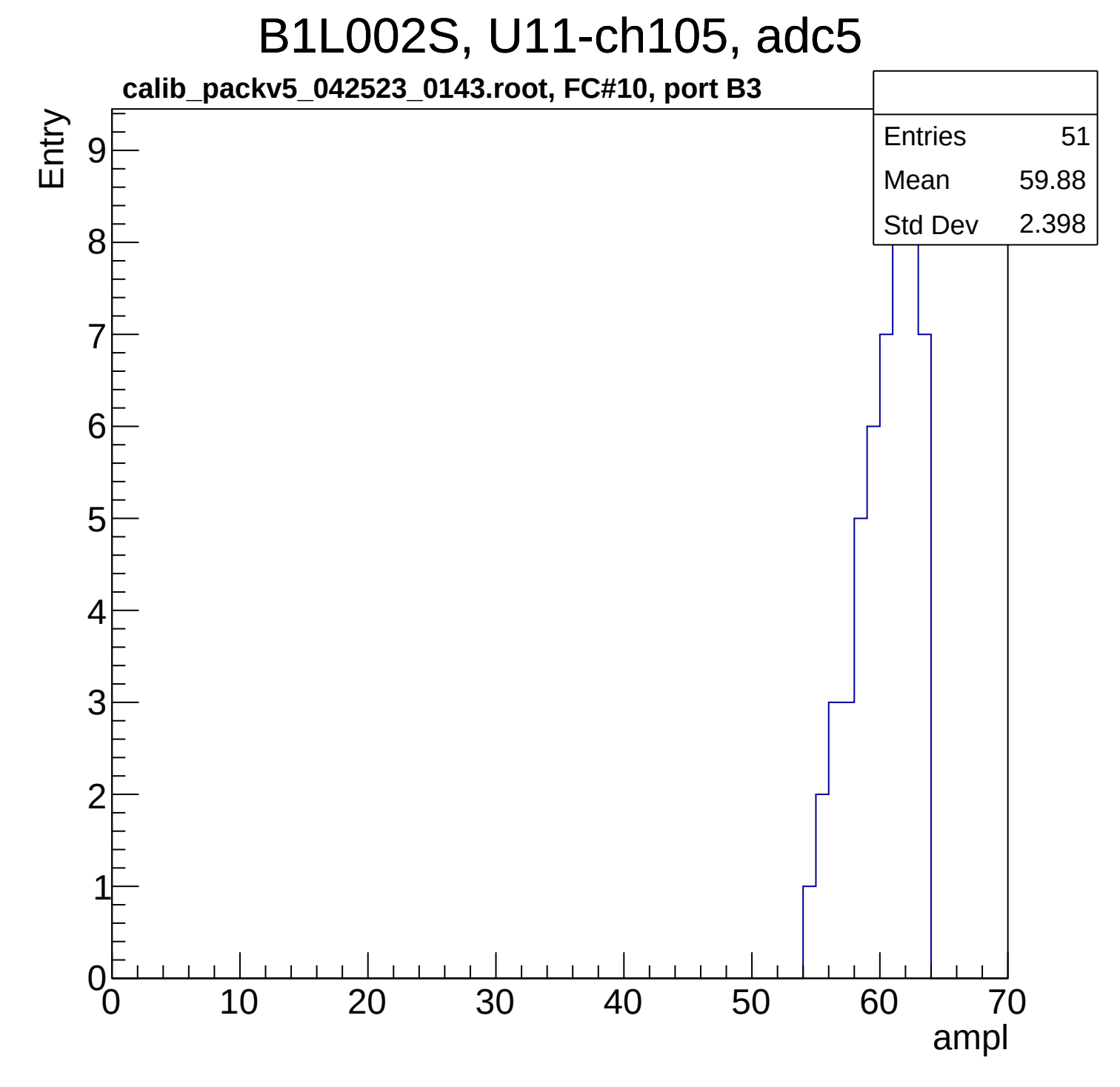

B1L002S, U11-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 49.4
Std Dev 3.192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 55.26
Std Dev 2.794
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.88
Std Dev 2.398
Entry
ampl
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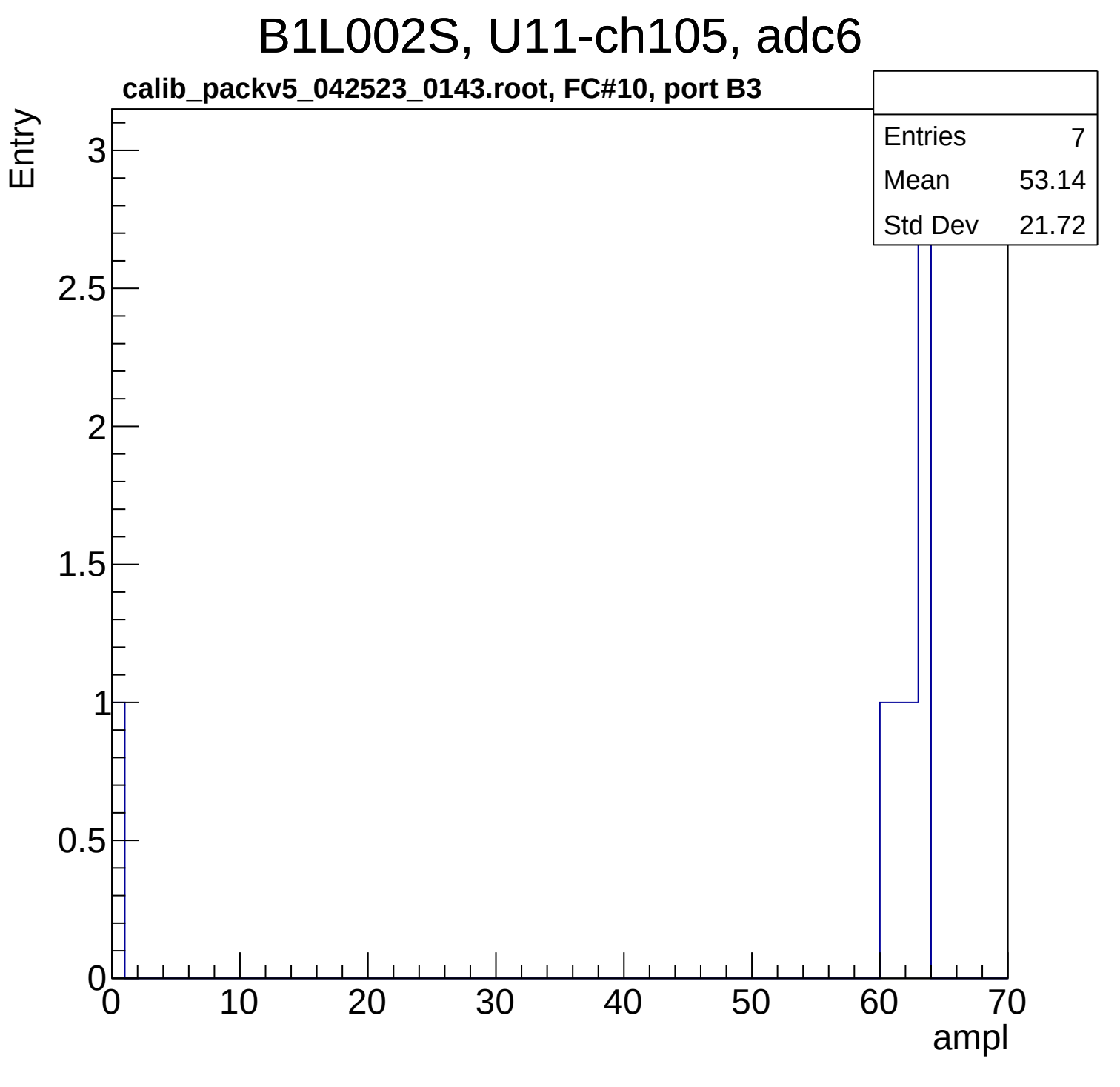
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53.14
Std Dev 21.72
Entry
ampl
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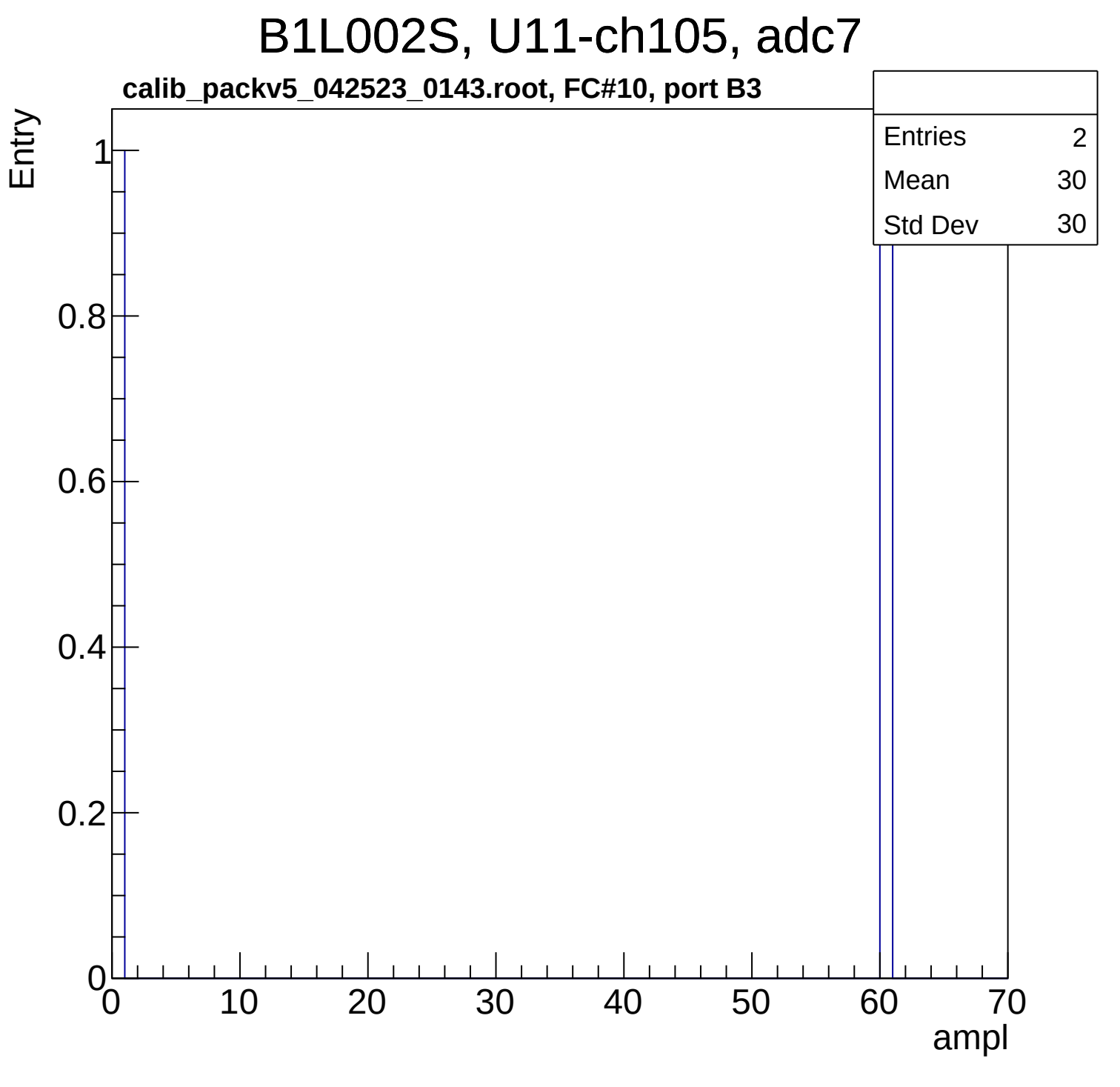
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U11-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30
Std Dev 30
Entry
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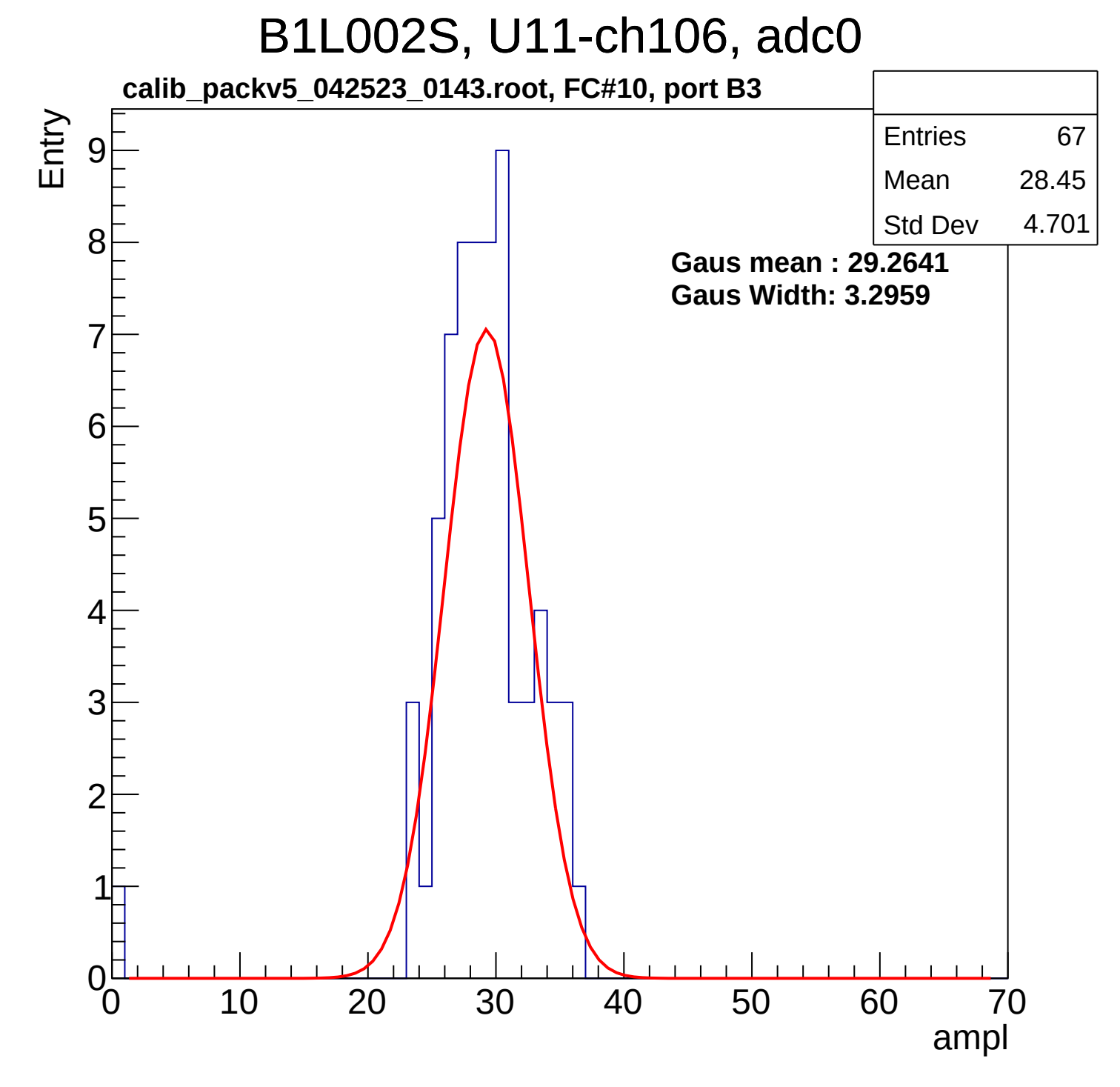
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.45
Std Dev 4.701
Gaus mean : 29.2641
Gaus Width: 3.2959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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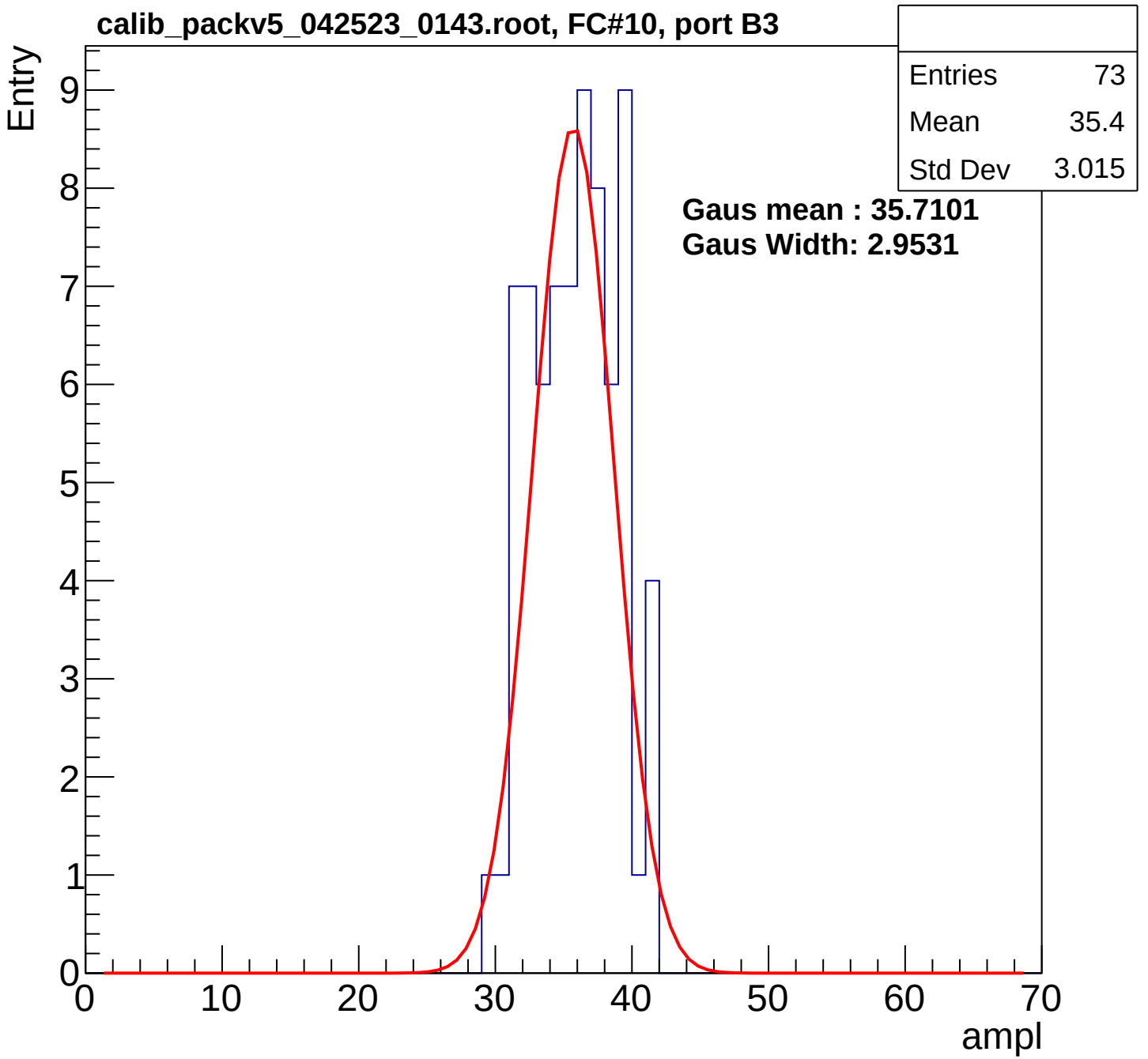
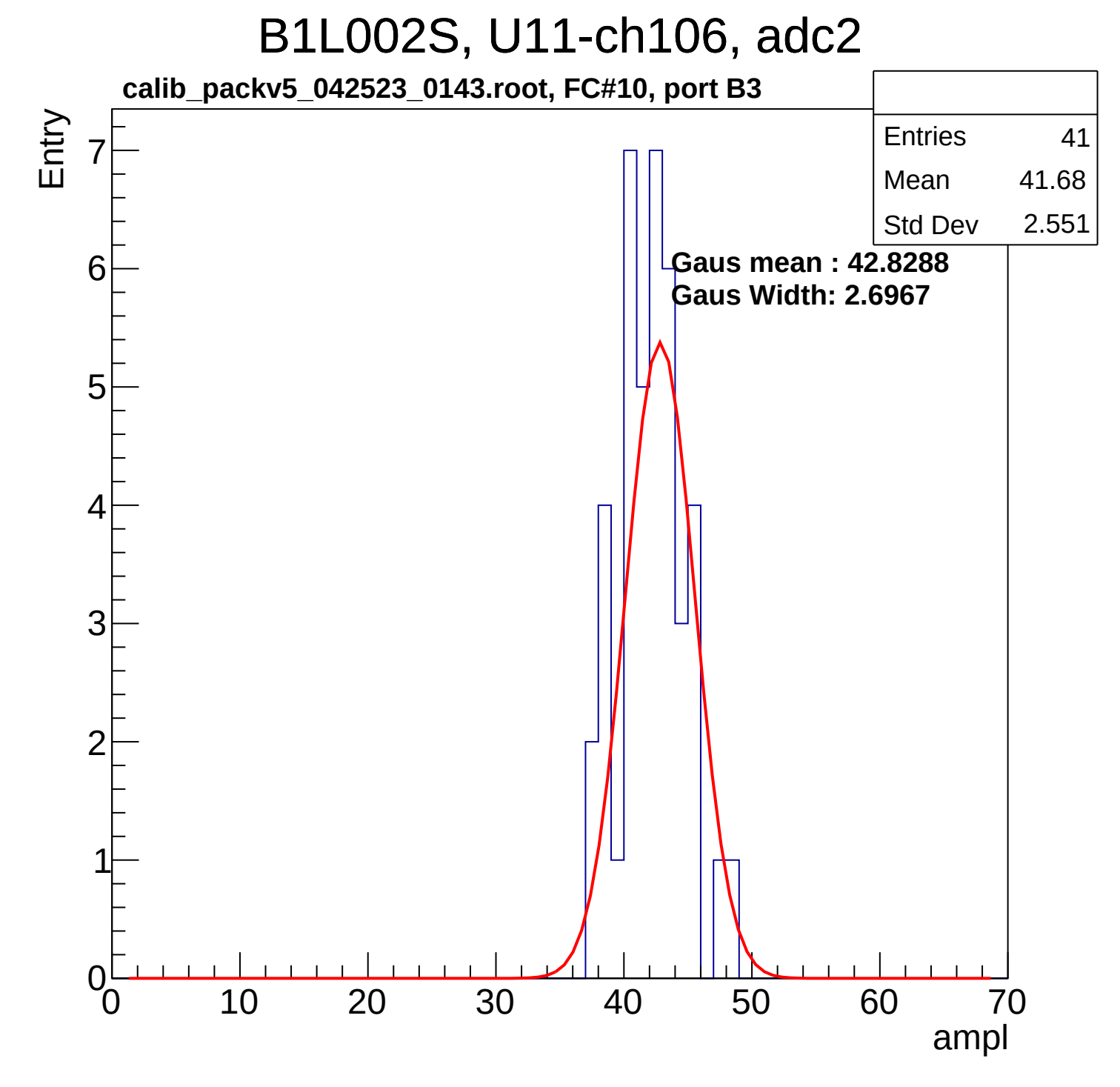

B1L002S, U11-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.4
Std Dev 3.015
Gaus mean : 35.7101
Gaus Width: 2.9531
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 41.68
Std Dev 2.551
Gaus mean : 42.8288
Gaus Width: 2.6967
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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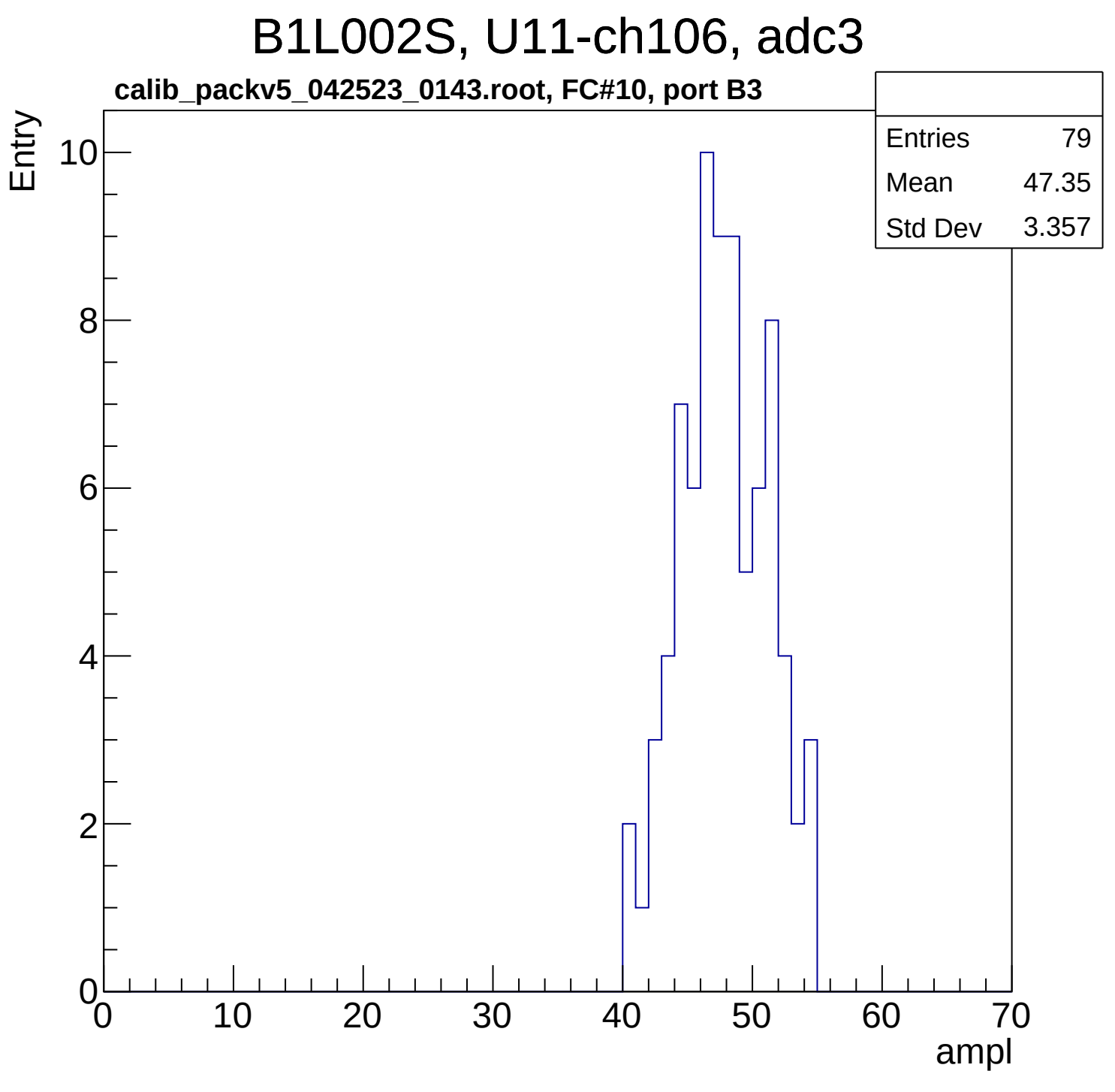
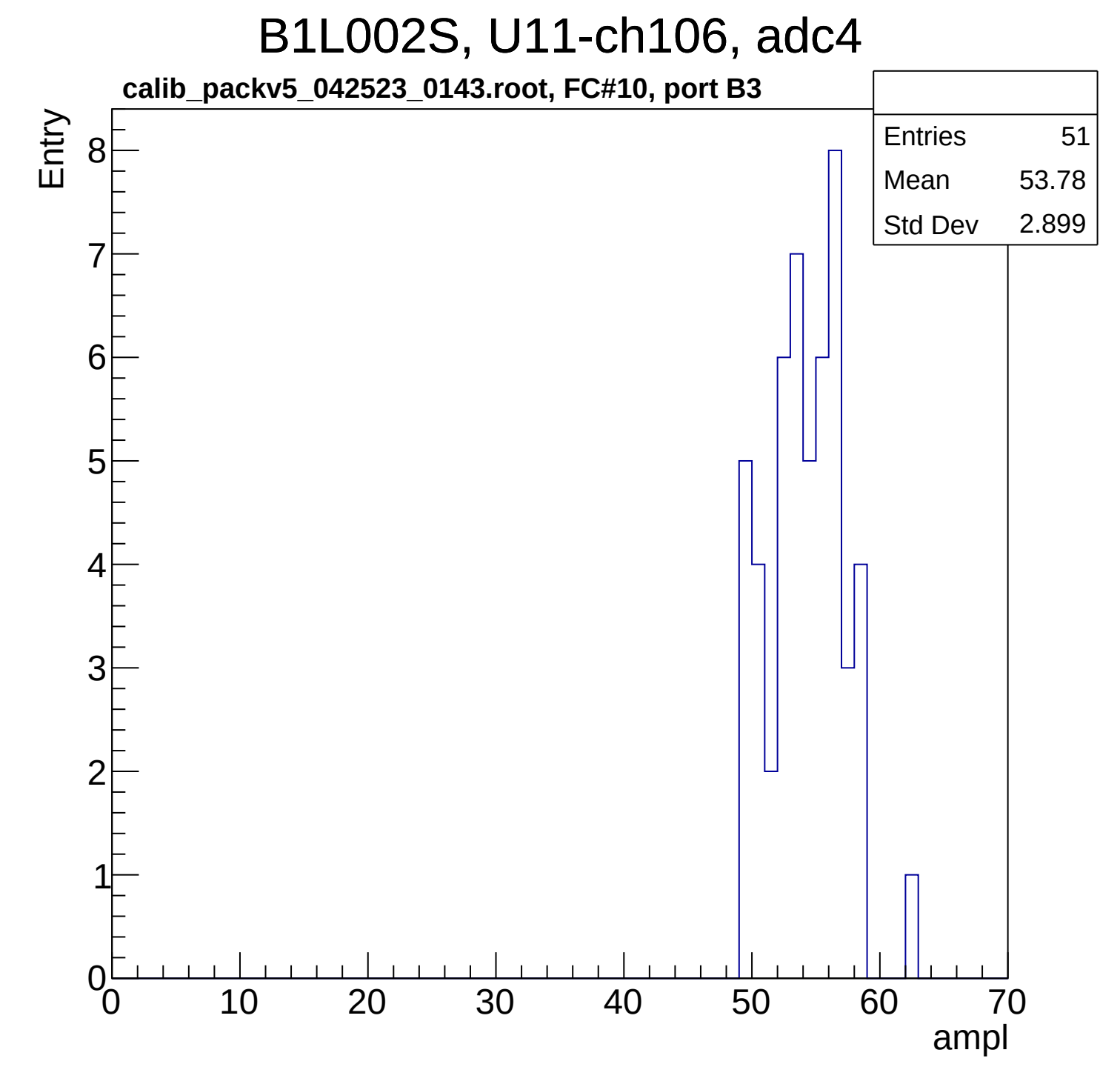

B1L002S, U11-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 47.35
Std Dev 3.357
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.78
Std Dev 2.899
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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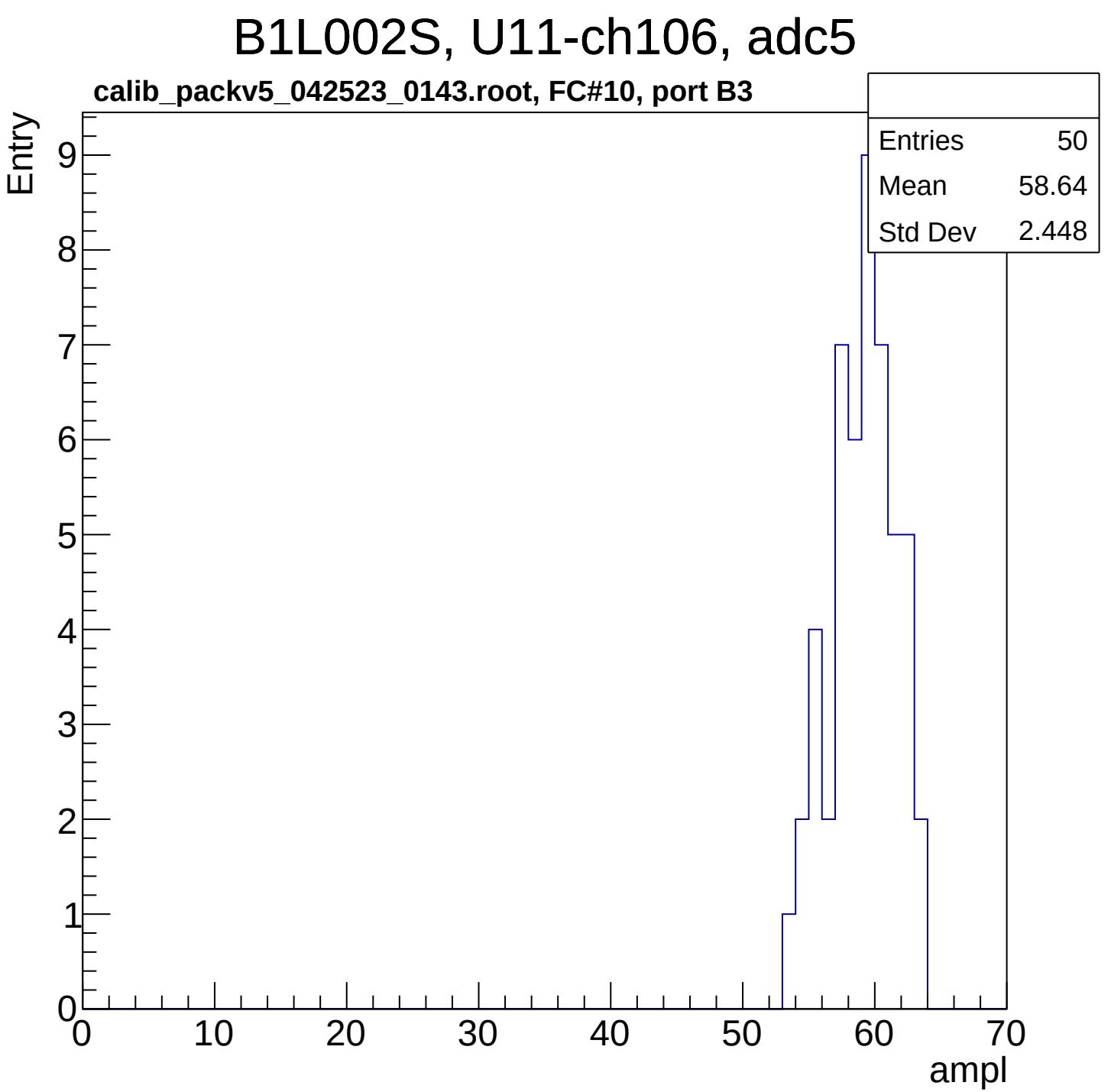
B1L002S, U11-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.64
Std Dev 2.448
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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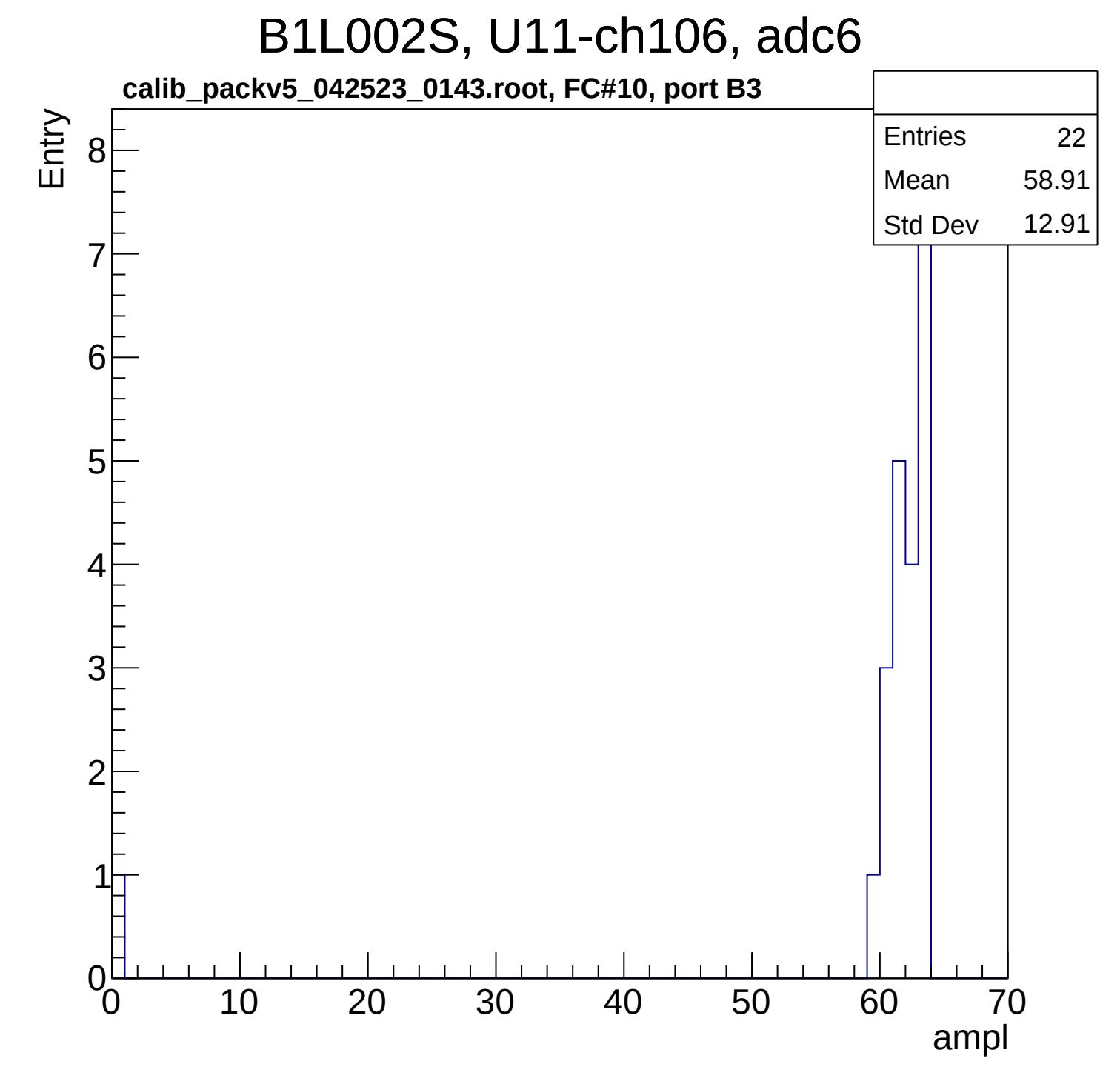
B1L002S, U11-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.91
Std Dev 12.91
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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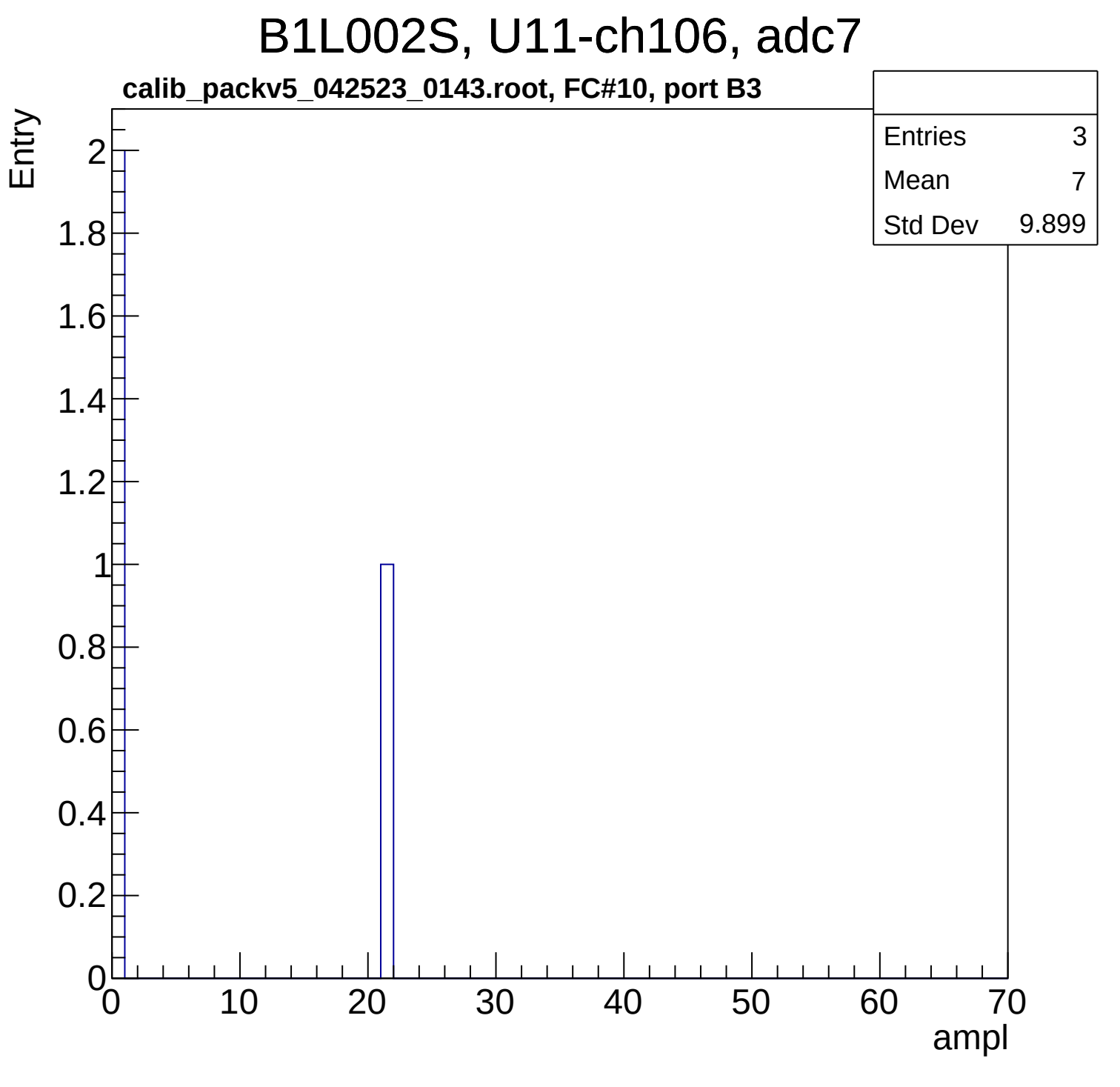
B1L002S, U11-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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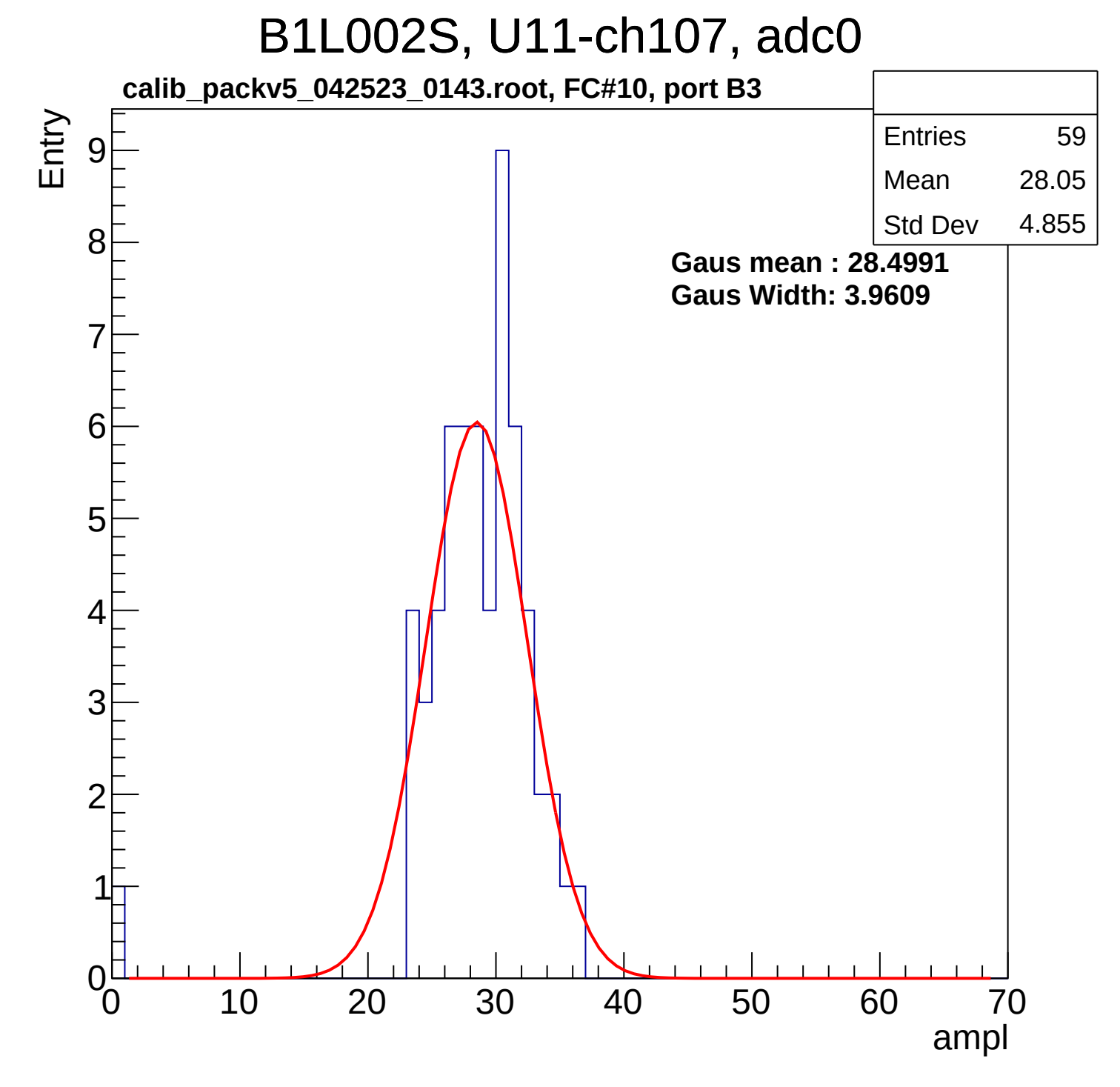
B1L002S, U11-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.05
Std Dev 4.855
Gaus mean : 28.4991
Gaus Width: 3.9609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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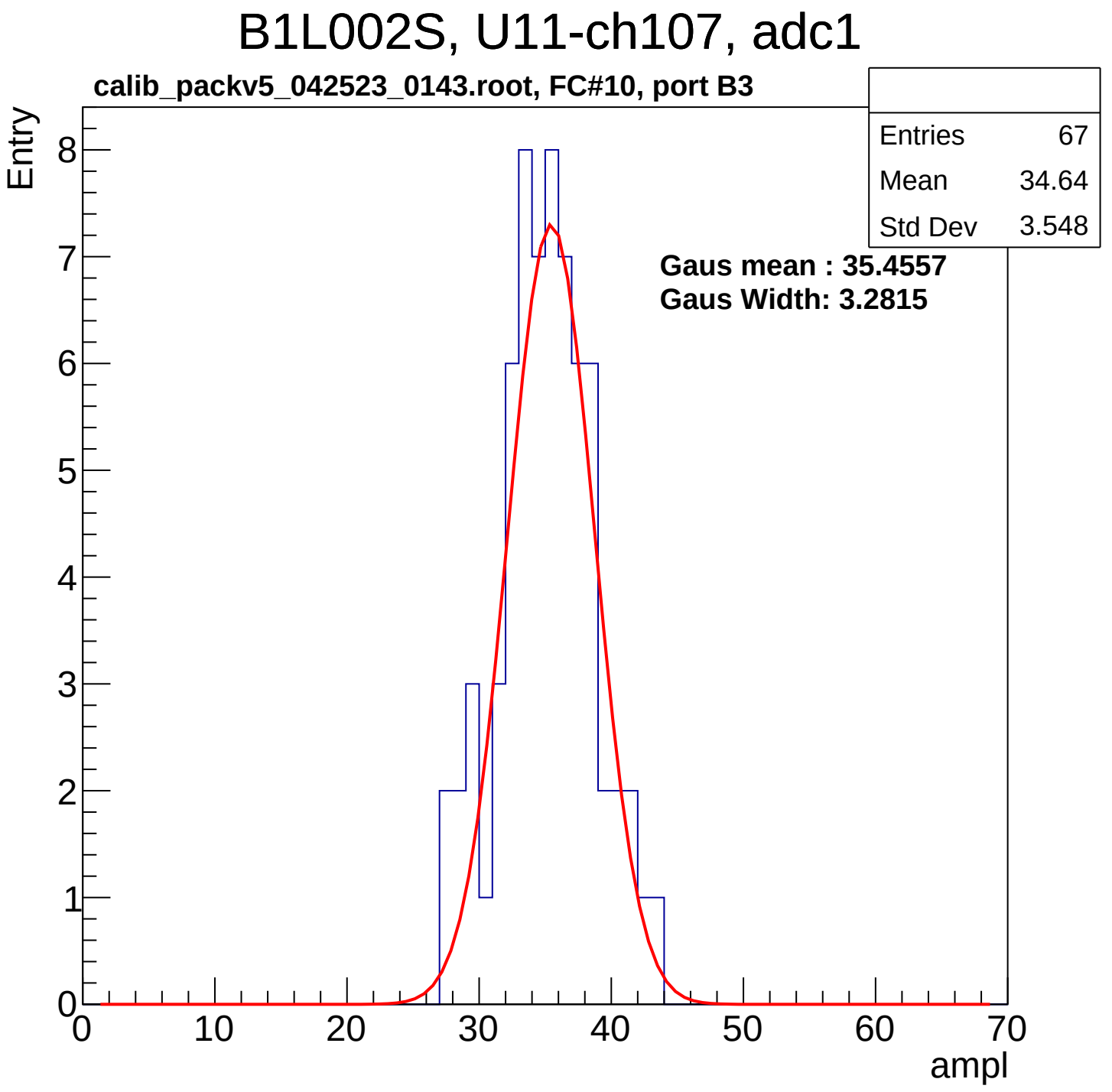
B1L002S, U11-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.64
Std Dev 3.548
Gaus mean : 35.4557
Gaus Width: 3.2815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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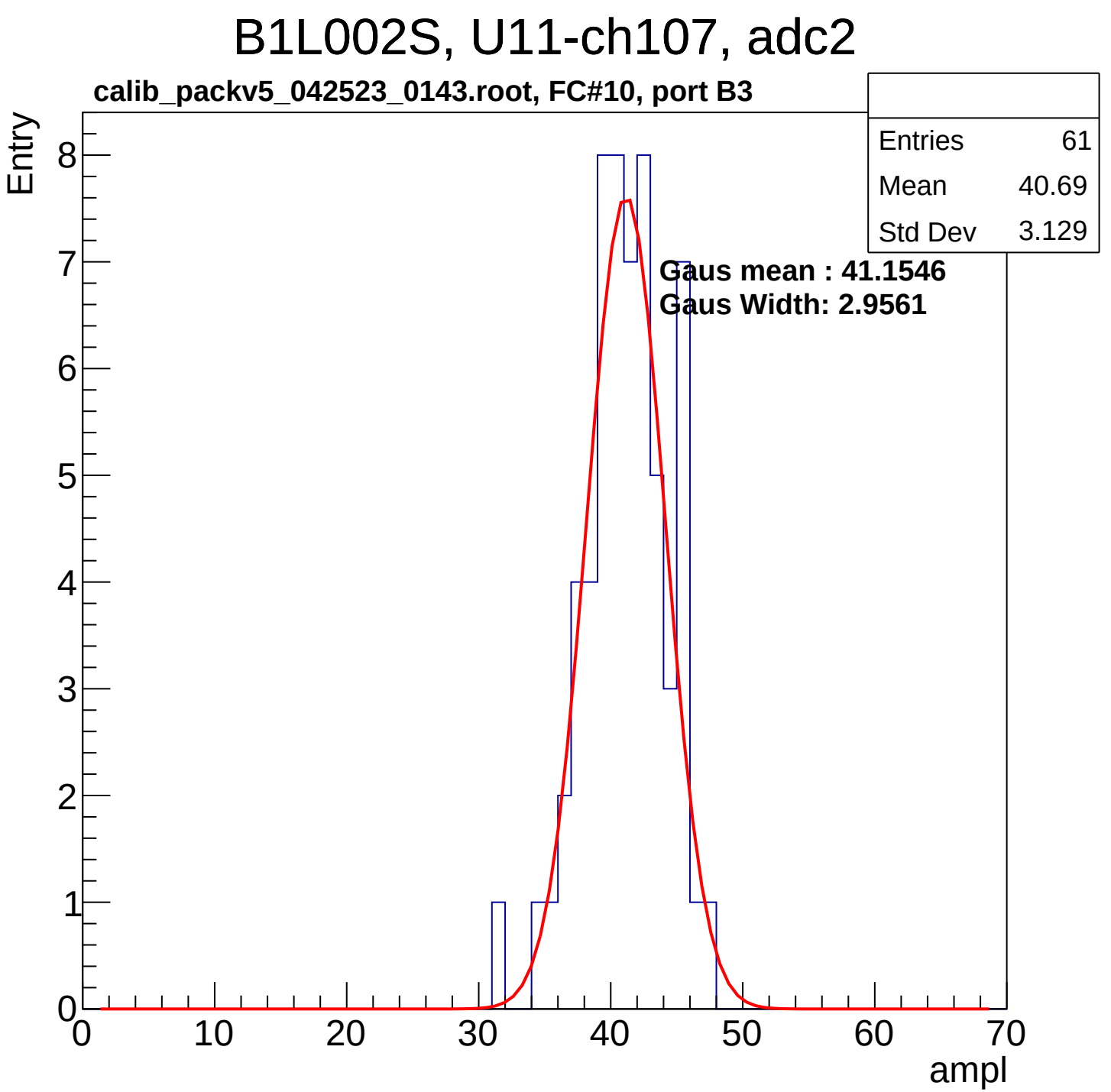
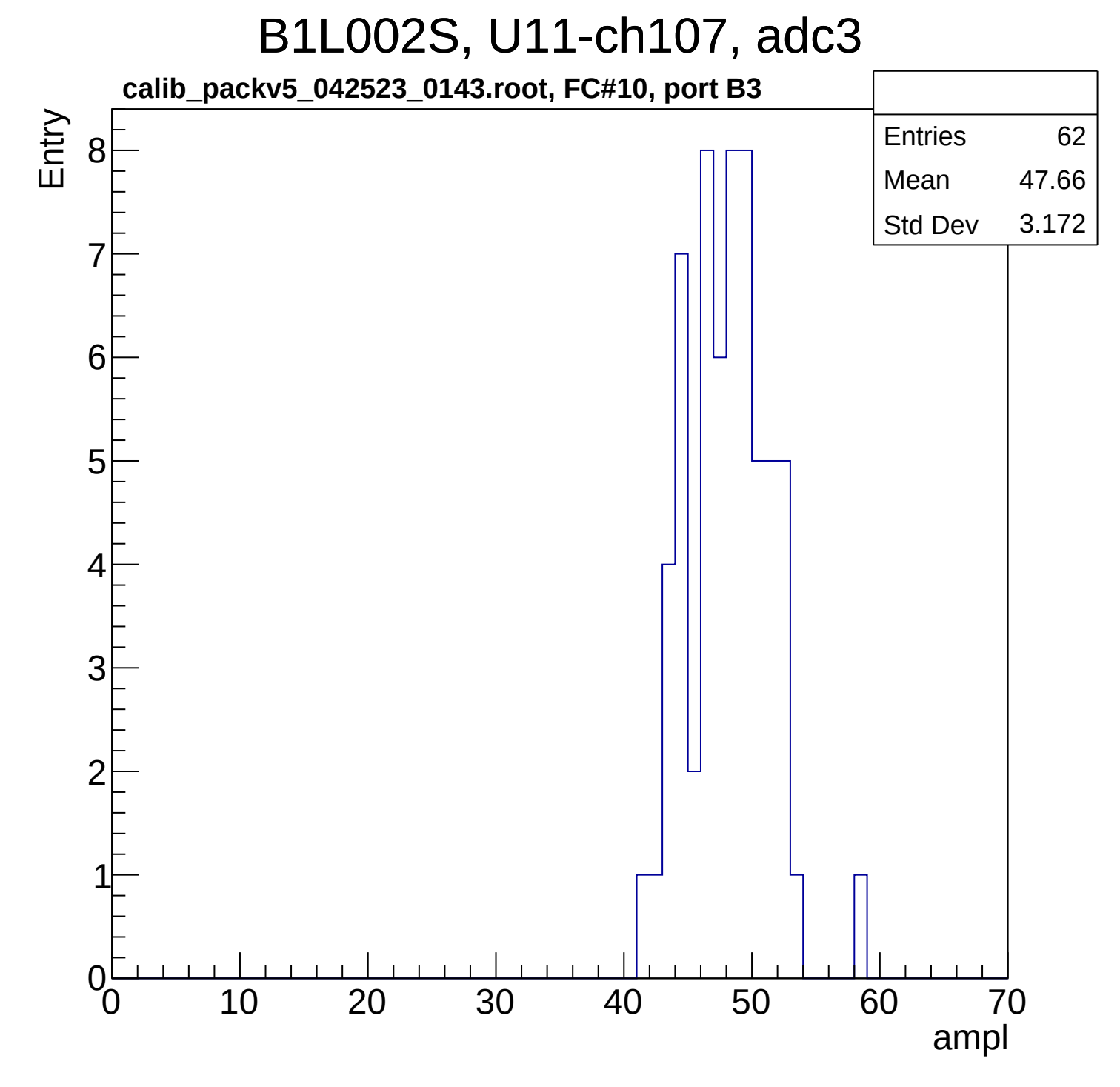

B1L002S, U11-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.69
Std Dev 3.129
Gaus mean : 41.1546
Gaus Width: 2.9561
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.66
Std Dev 3.172
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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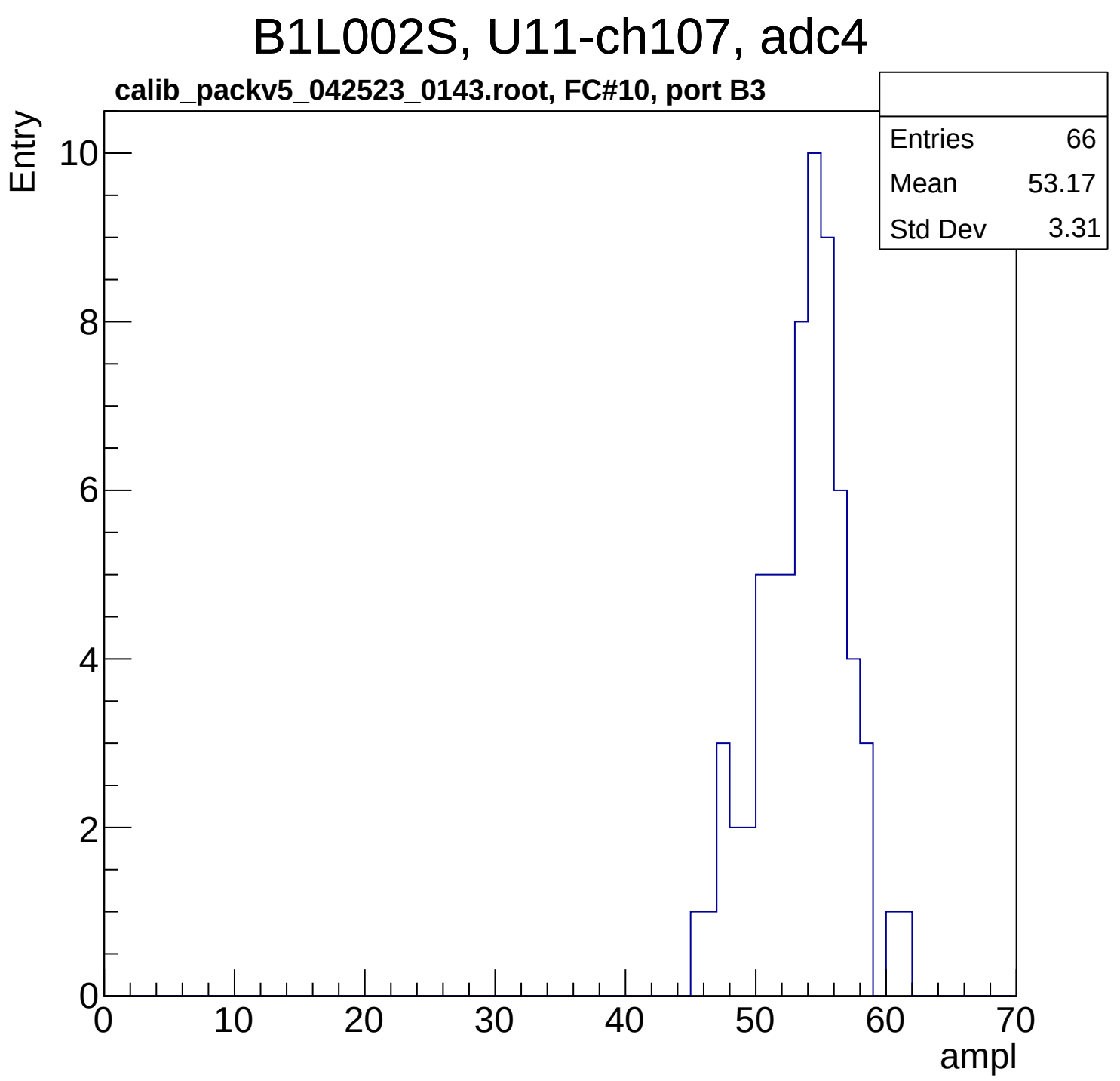
B1L002S, U11-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 53.17
Std Dev 3.31
Entry
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2
4
6
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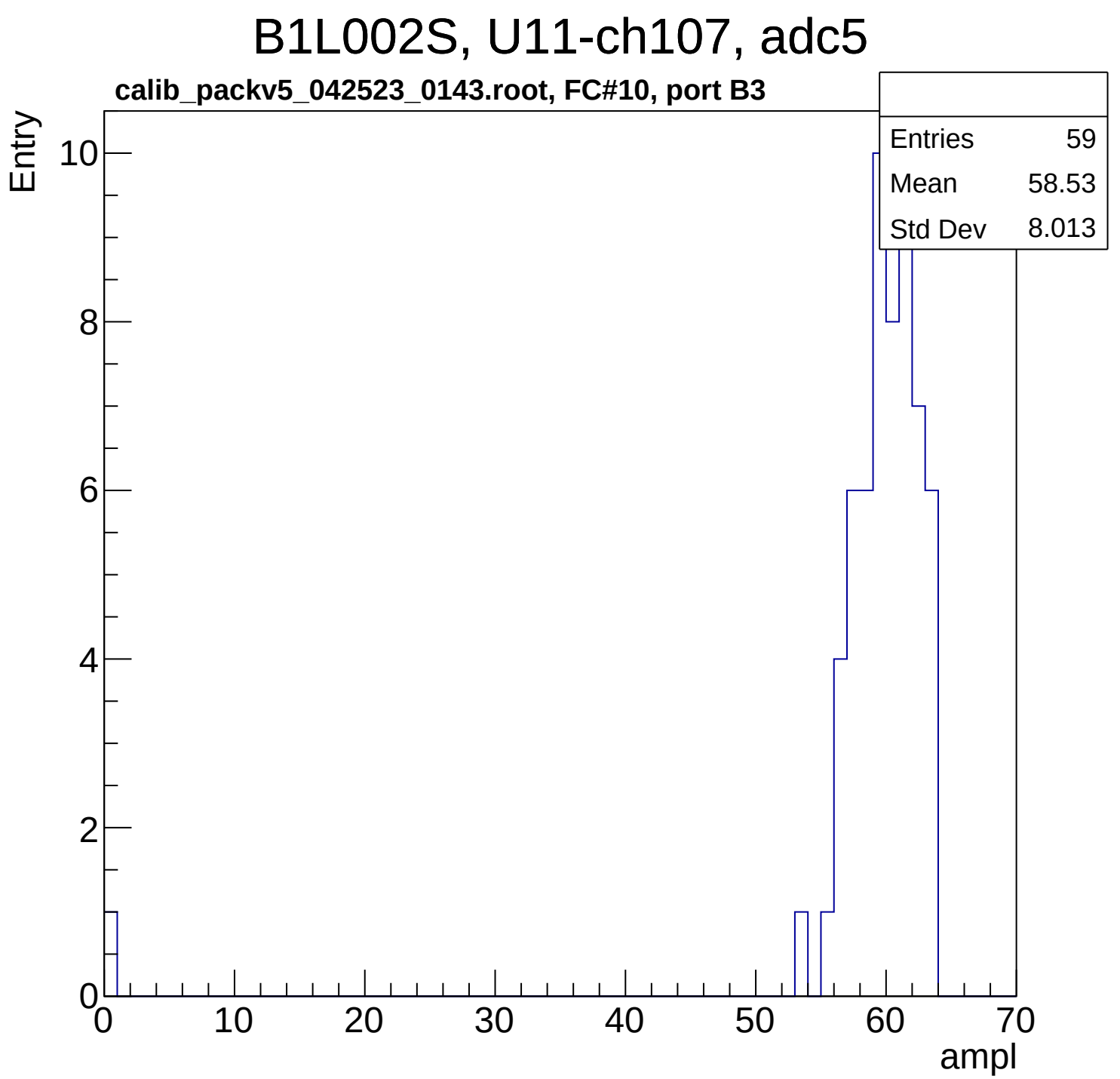
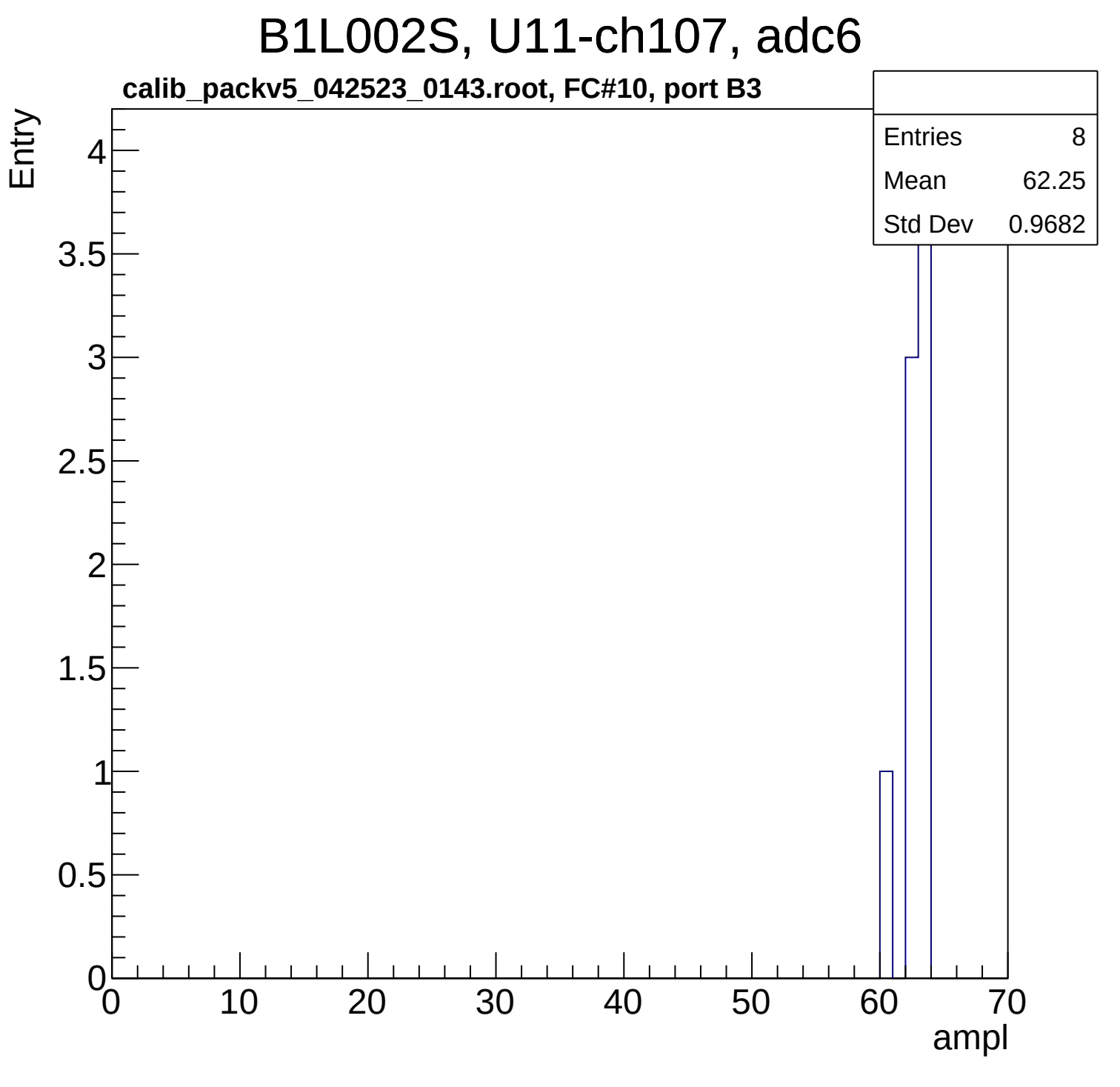
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.53
Std Dev 8.013
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62.25
Std Dev 0.9682
Entry
ampl
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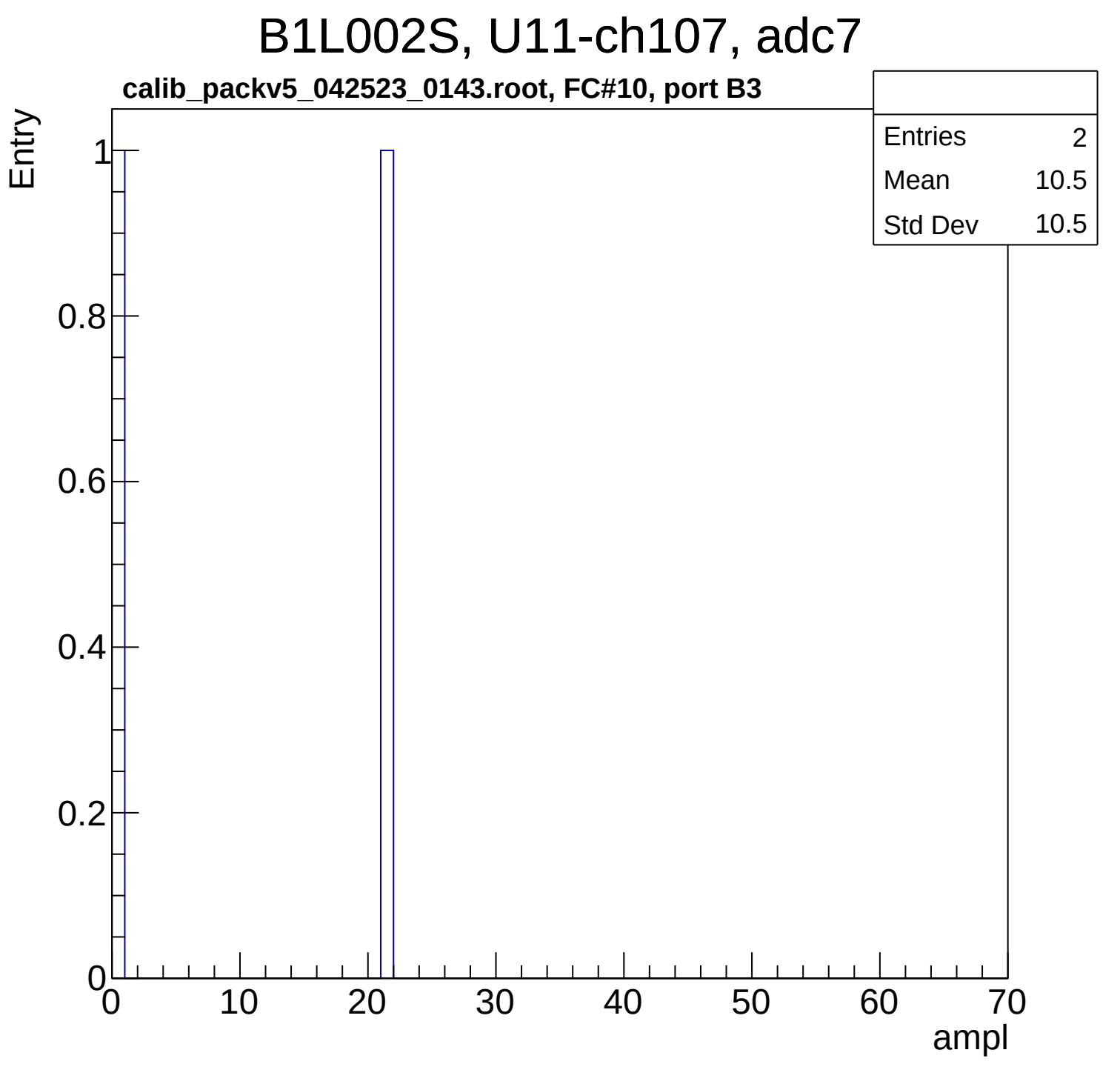
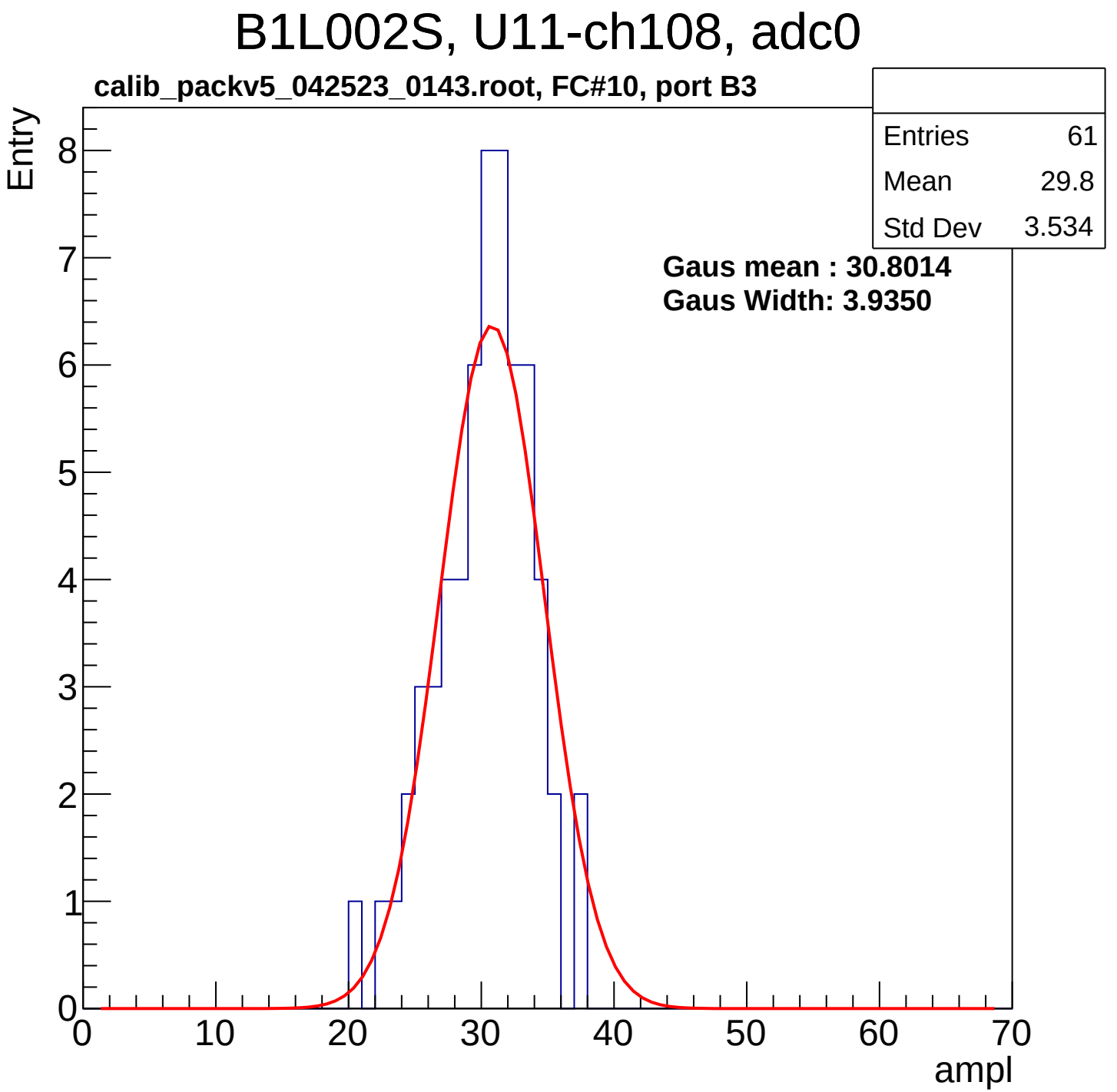
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.8
Std Dev 3.534
Gaus mean : 30.8014
Gaus Width: 3.9350
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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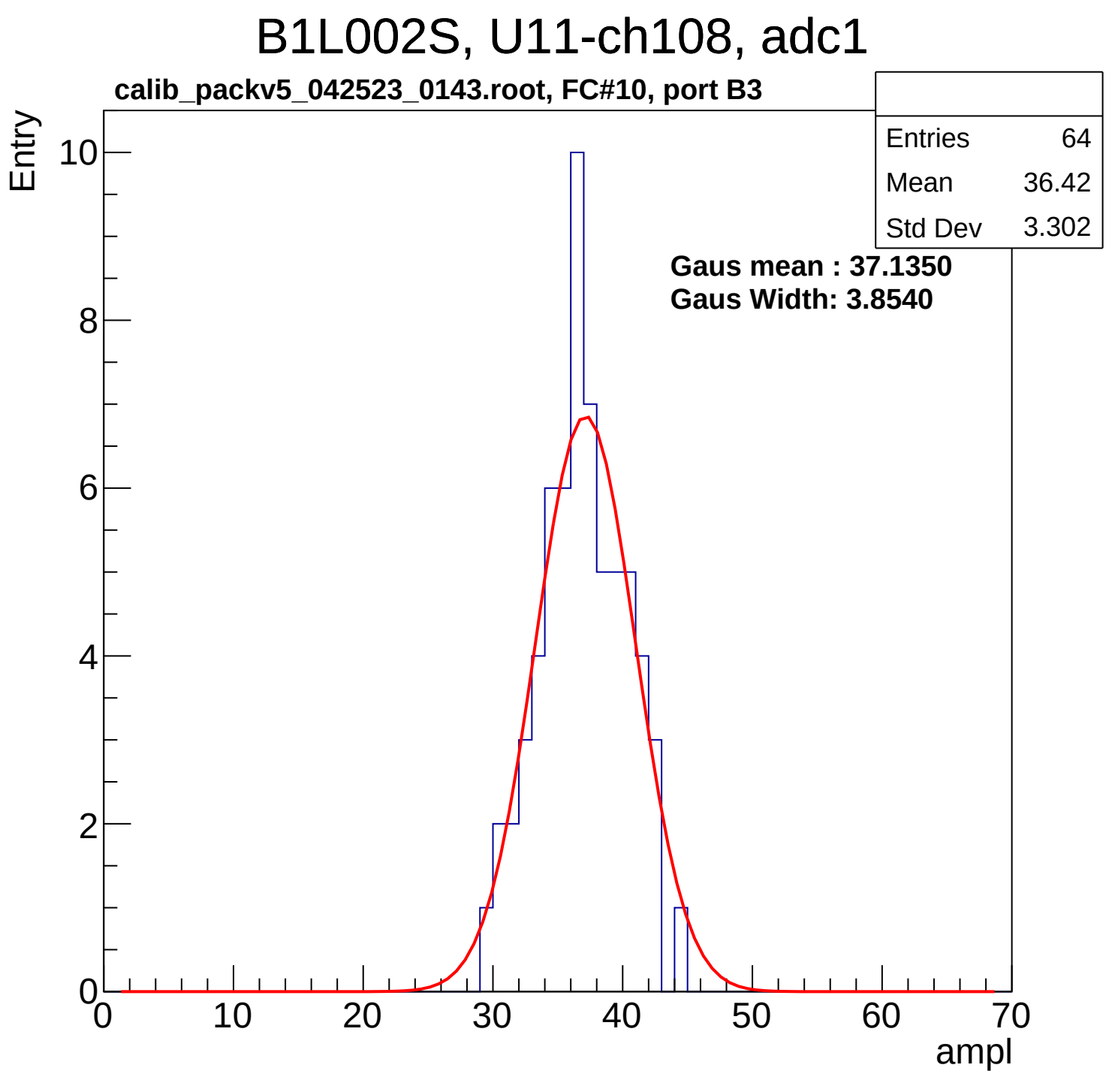
B1L002S, U11-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 36.42
Std Dev 3.302
Gaus mean : 37.1350
Gaus Width: 3.8540
Entry
ampl
0
2
4
6
8
10
0
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30
40
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60
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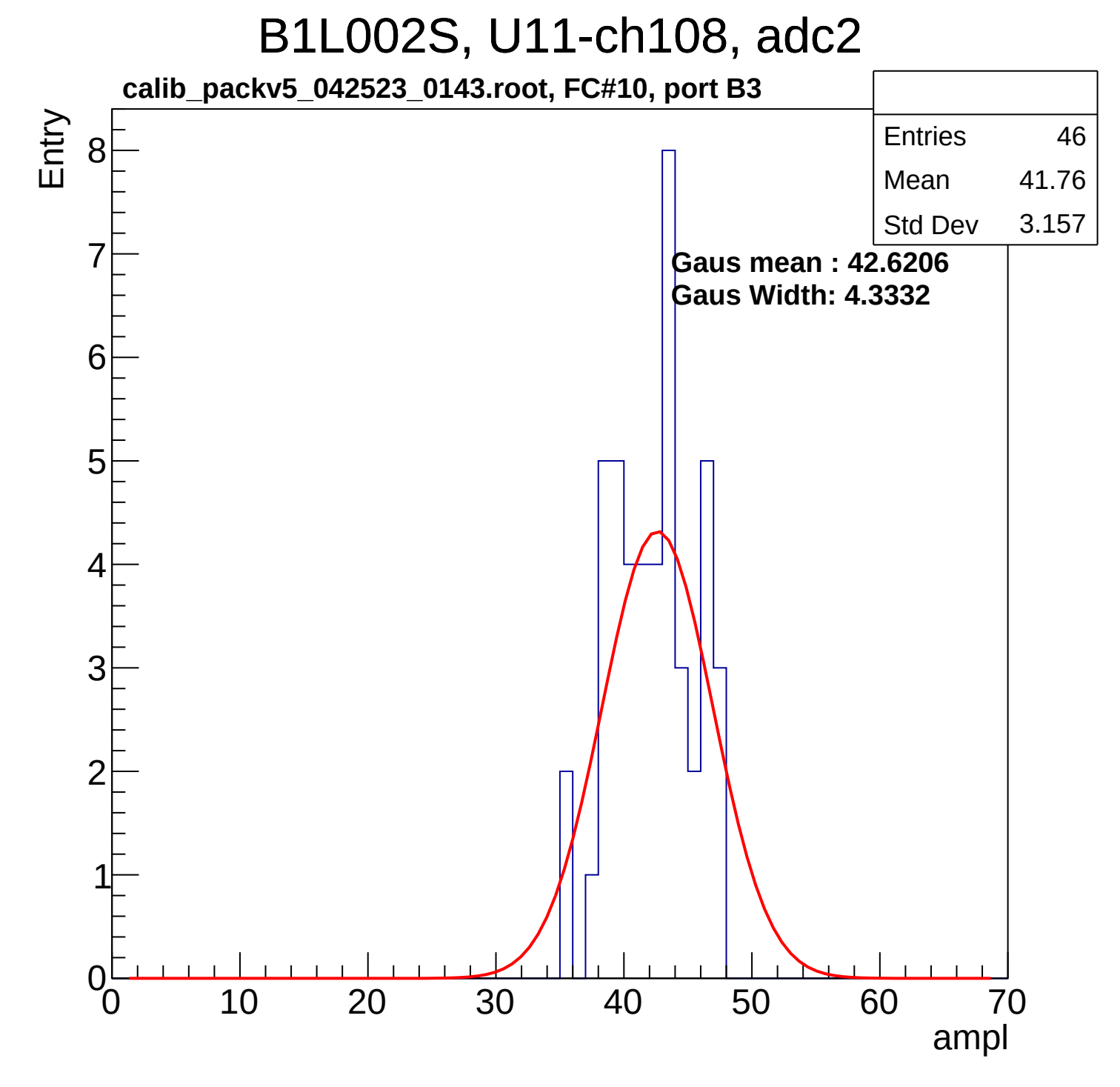
B1L002S, U11-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 41.76
Std Dev 3.157
Gaus mean : 42.6206
Gaus Width: 4.3332
Entry
ampl
0
1
2
3
4
5
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7
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0
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60
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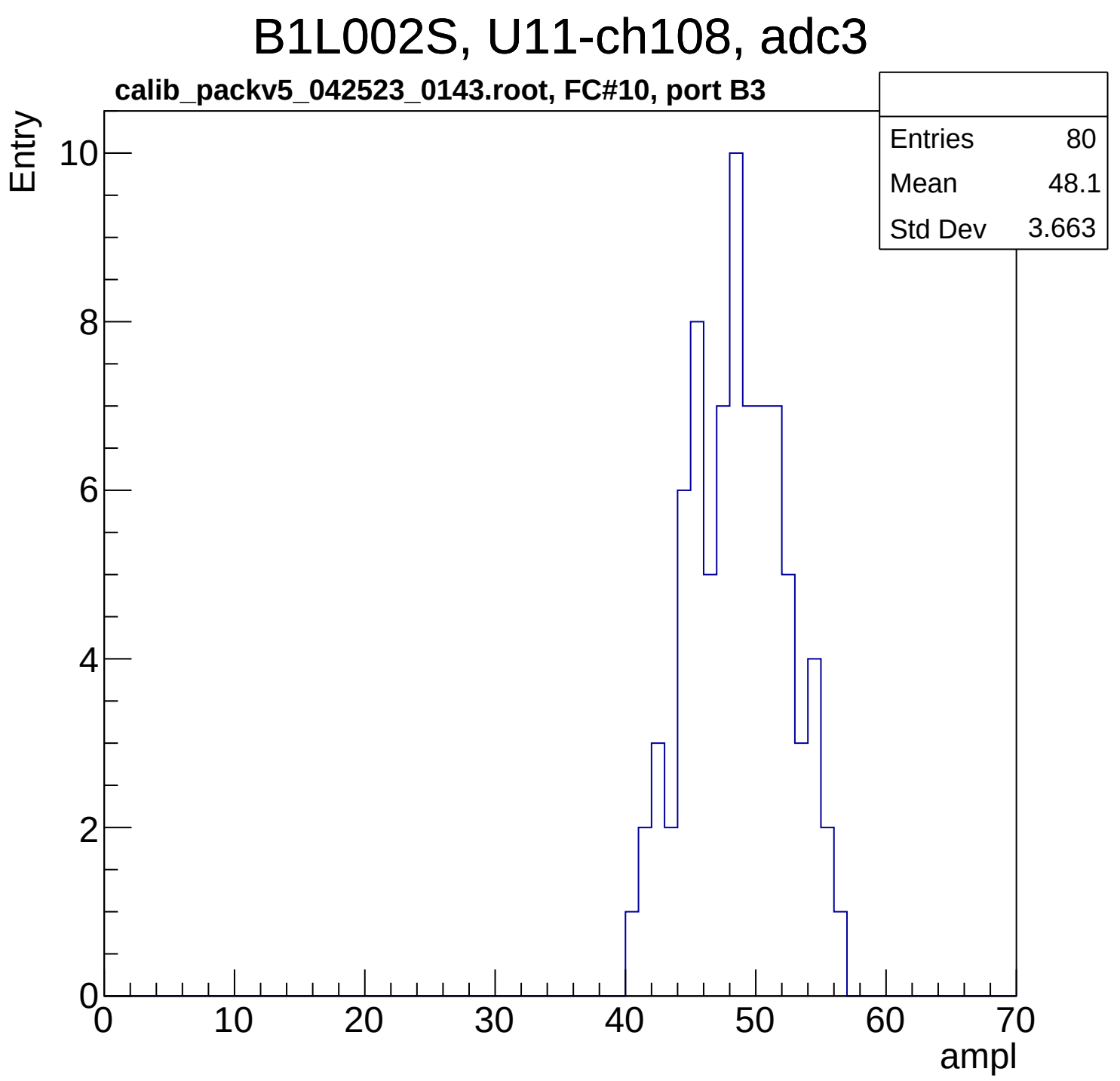
B1L002S, U11-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 48.1
Std Dev 3.663
Entry
0
2
4
6
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10
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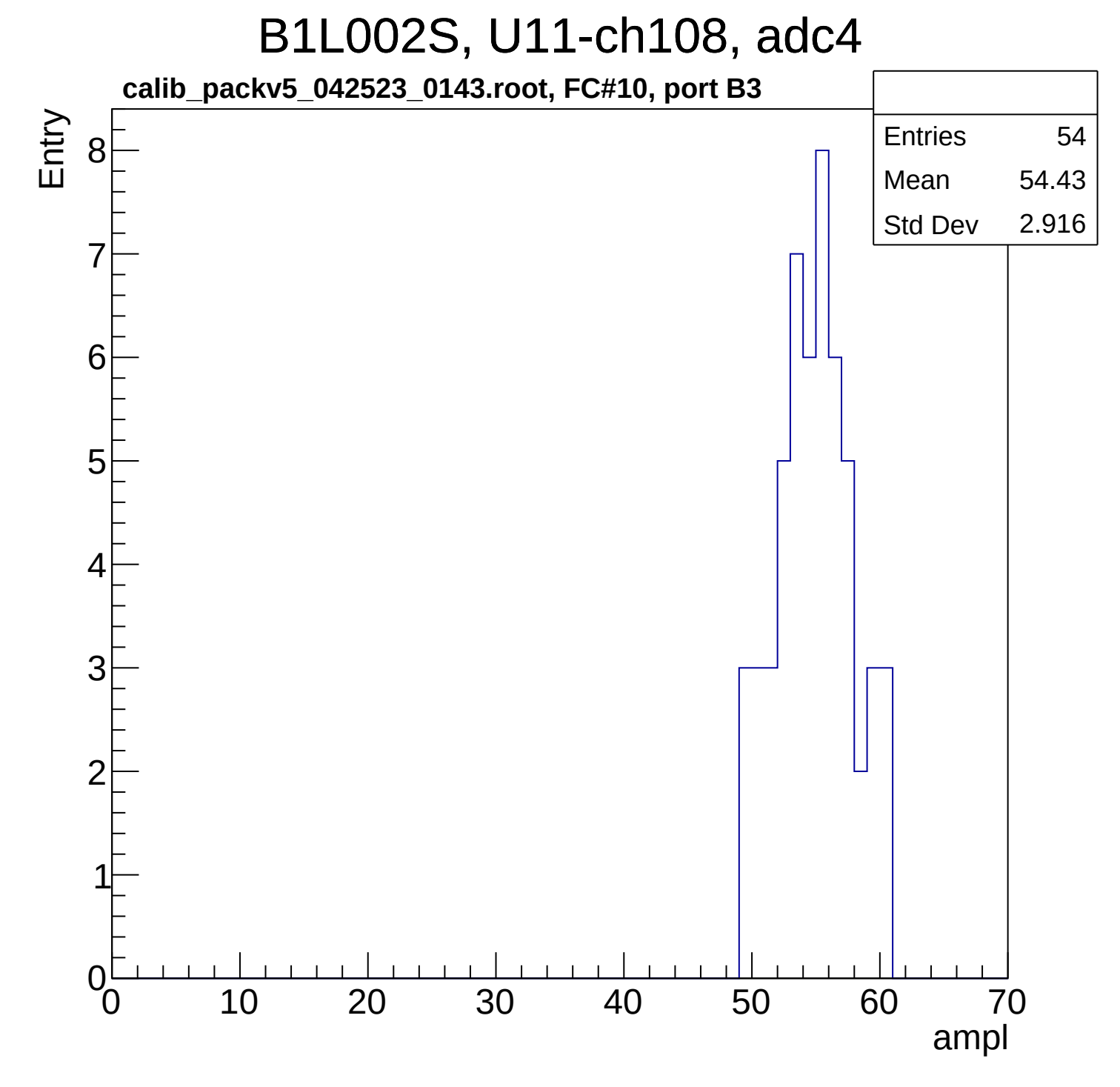
70
ampl


B1L002S, U11-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 54.43
Std Dev 2.916
Entry
ampl
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1
2
3
4
5
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0
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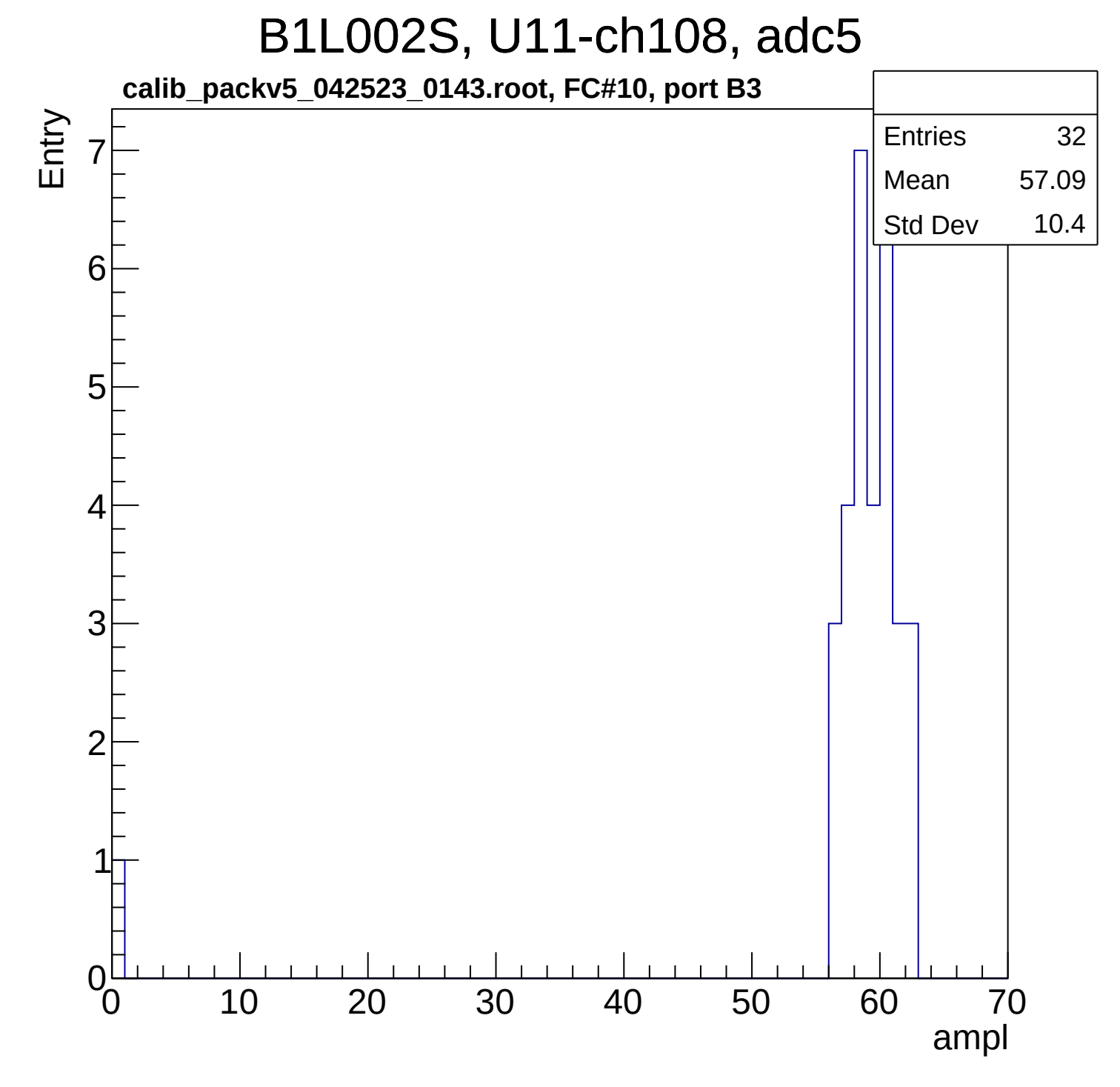
B1L002S, U11-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 57.09
Std Dev 10.4
Entry
ampl
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1
2
3
4
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0
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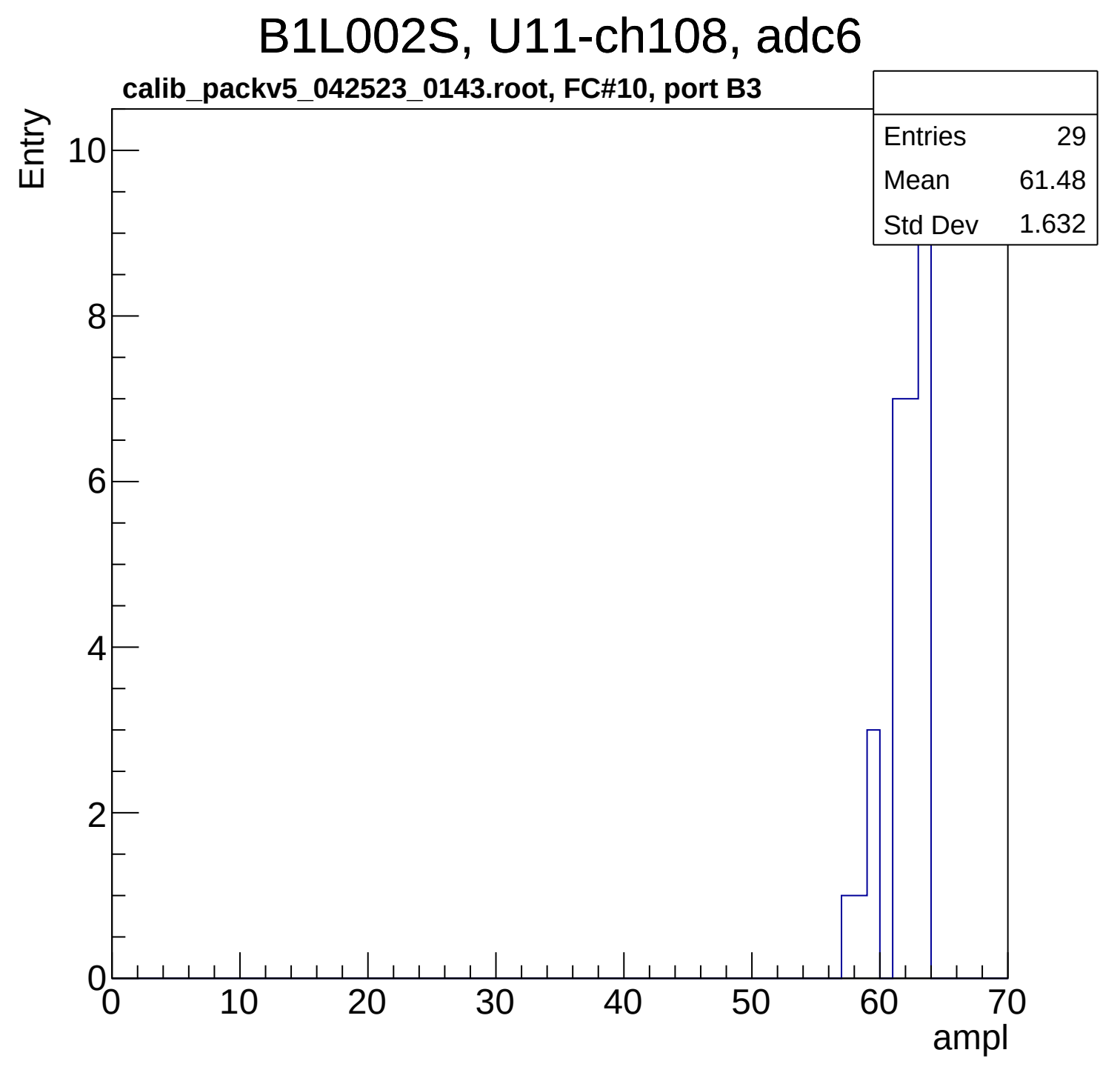
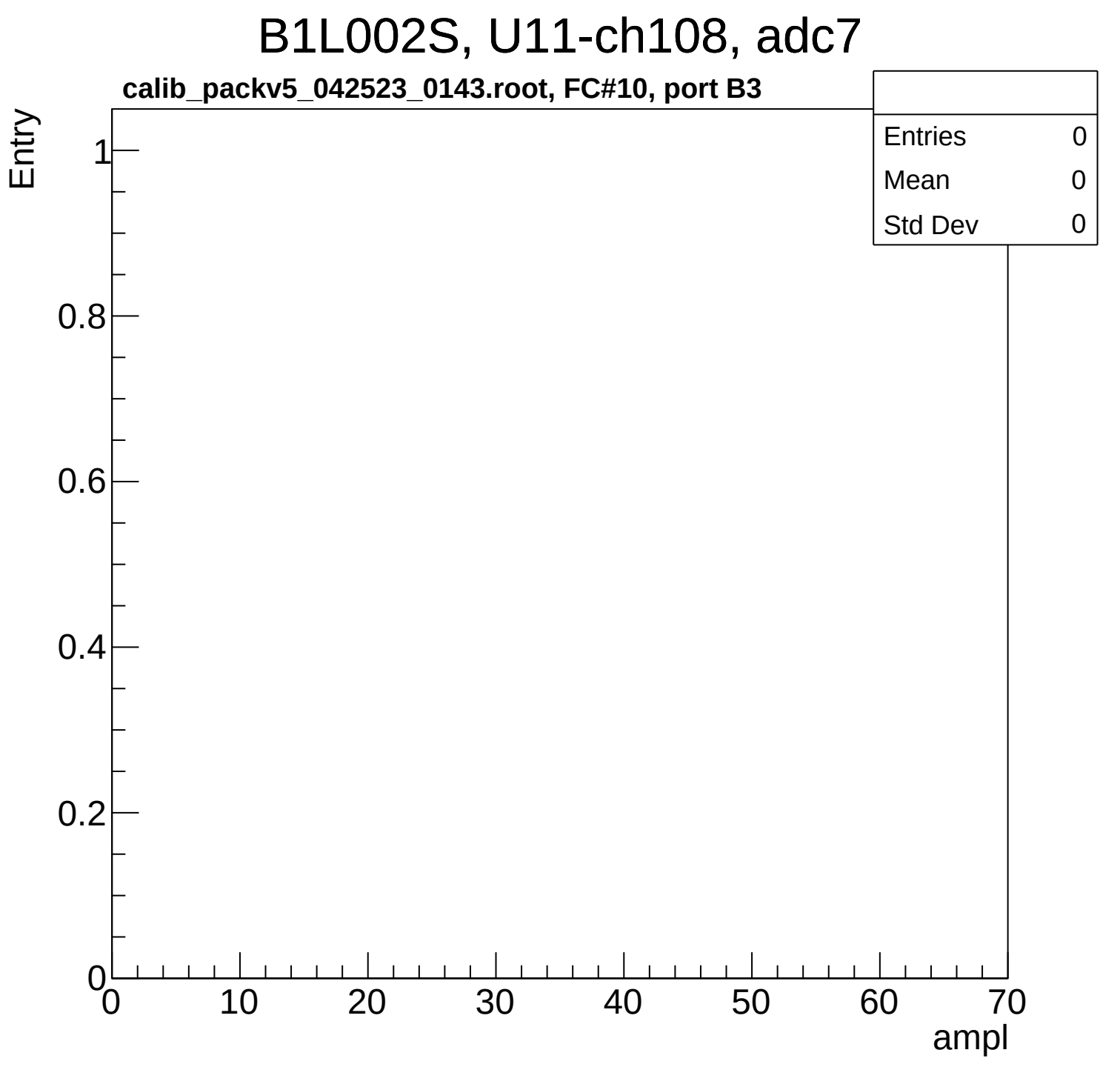

B1L002S, U11-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 61.48
Std Dev 1.632
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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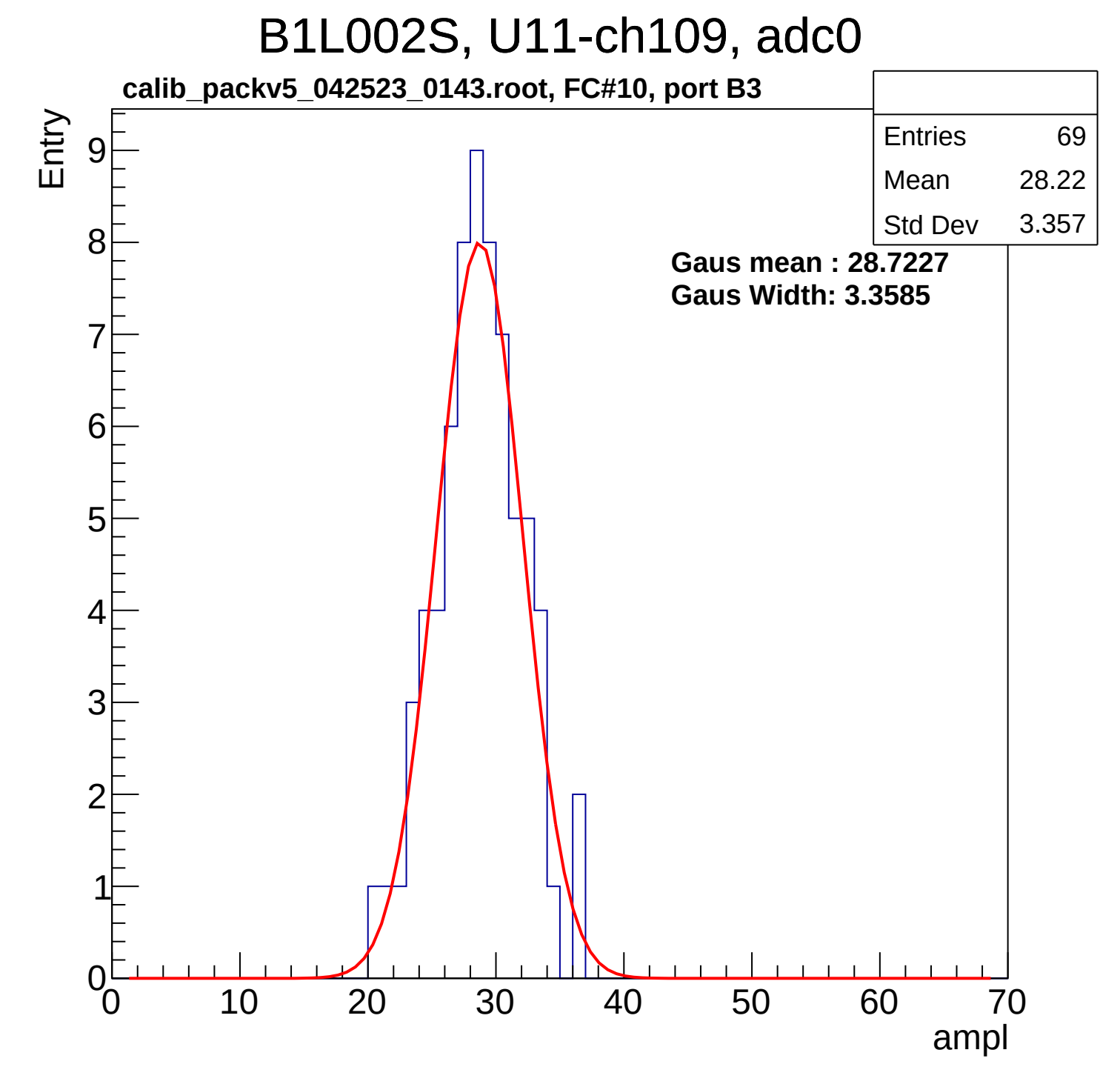
30
40
50
60
70
ampl


B1L002S, U11-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.22
Std Dev 3.357
Gaus mean : 28.7227
Gaus Width: 3.3585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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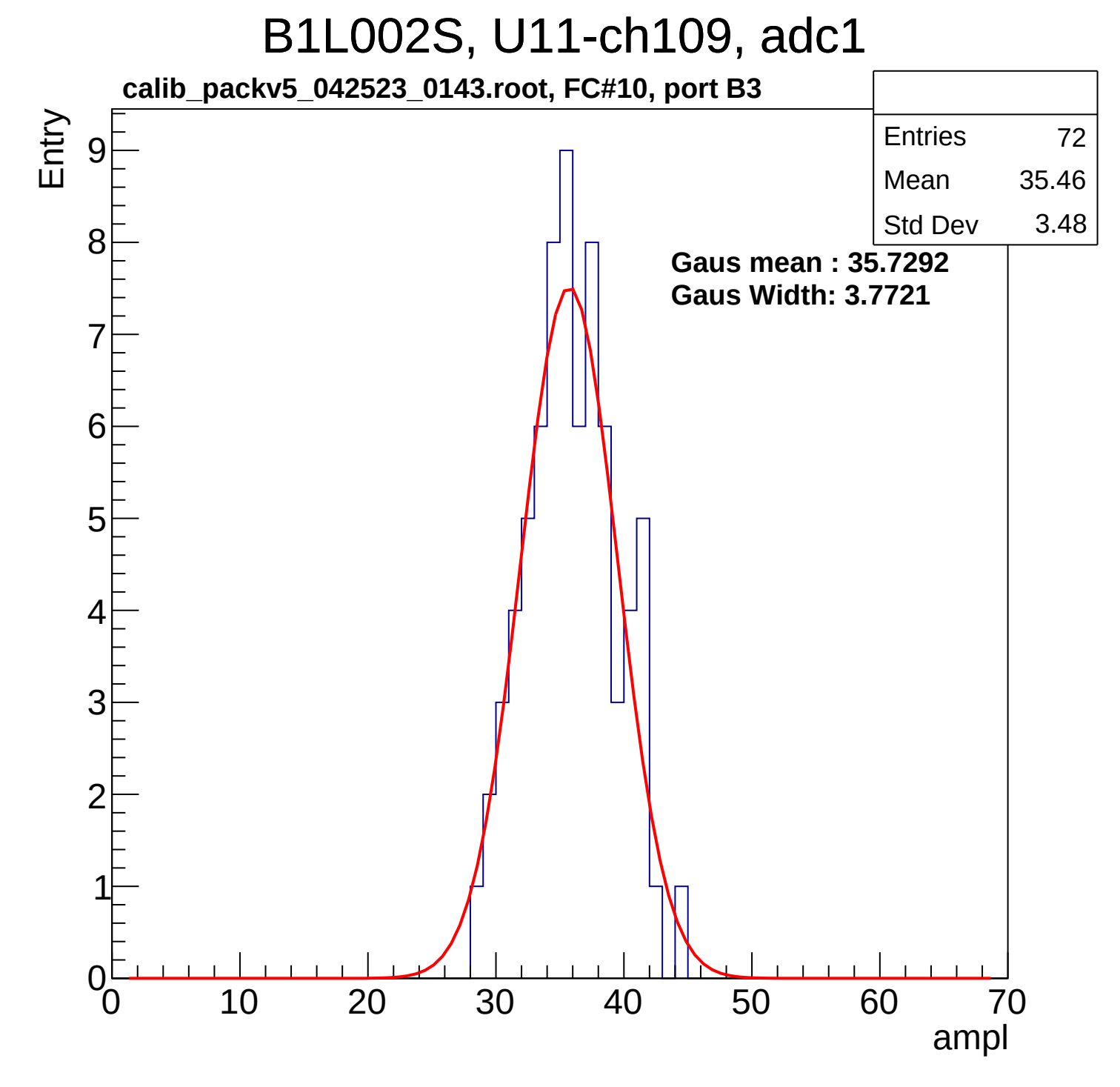
B1L002S, U11-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.46
Std Dev 3.48
Gaus mean : 35.7292
Gaus Width: 3.7721
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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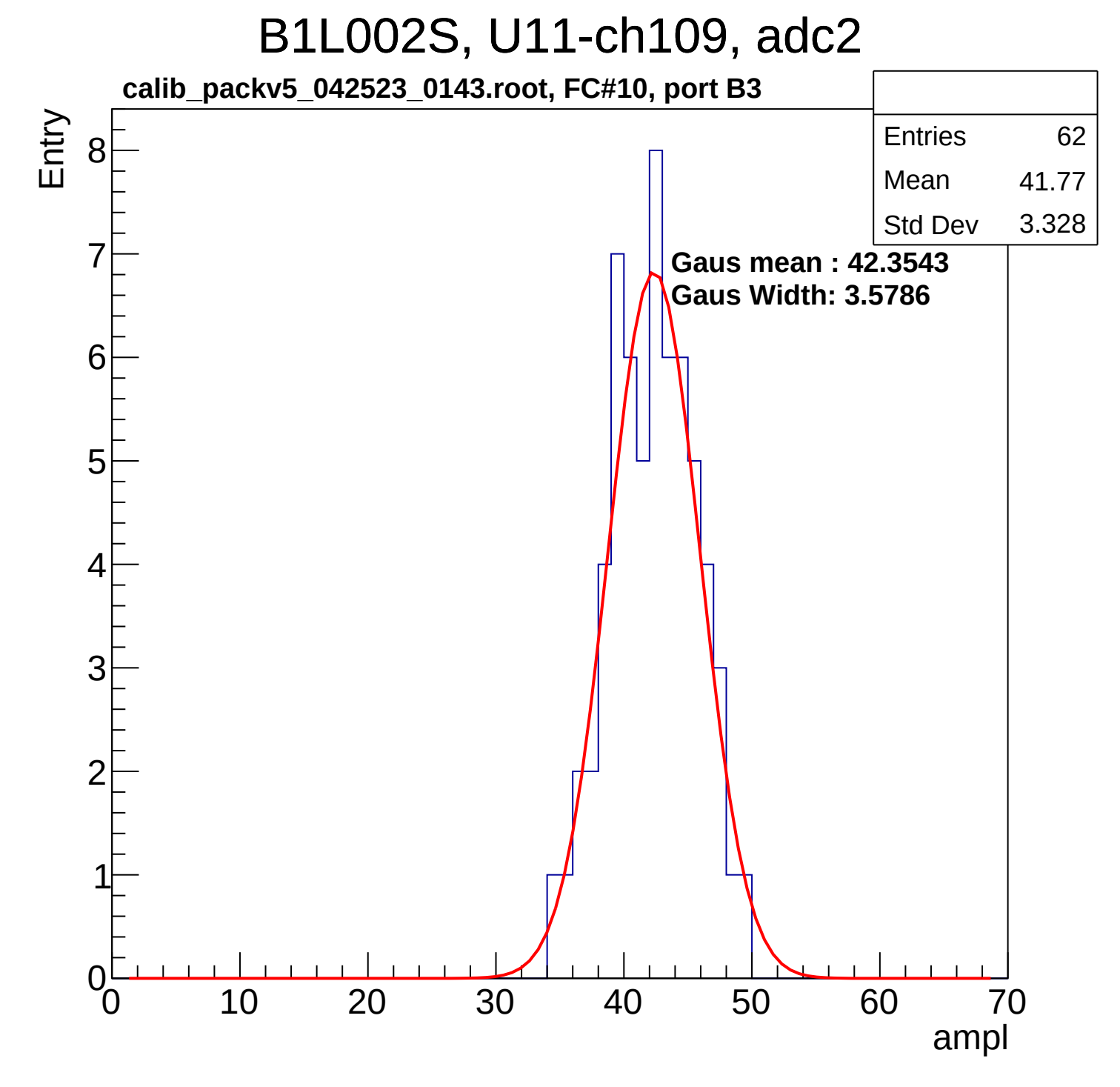
B1L002S, U11-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.77
Std Dev 3.328
Gaus mean : 42.3543
Gaus Width: 3.5786
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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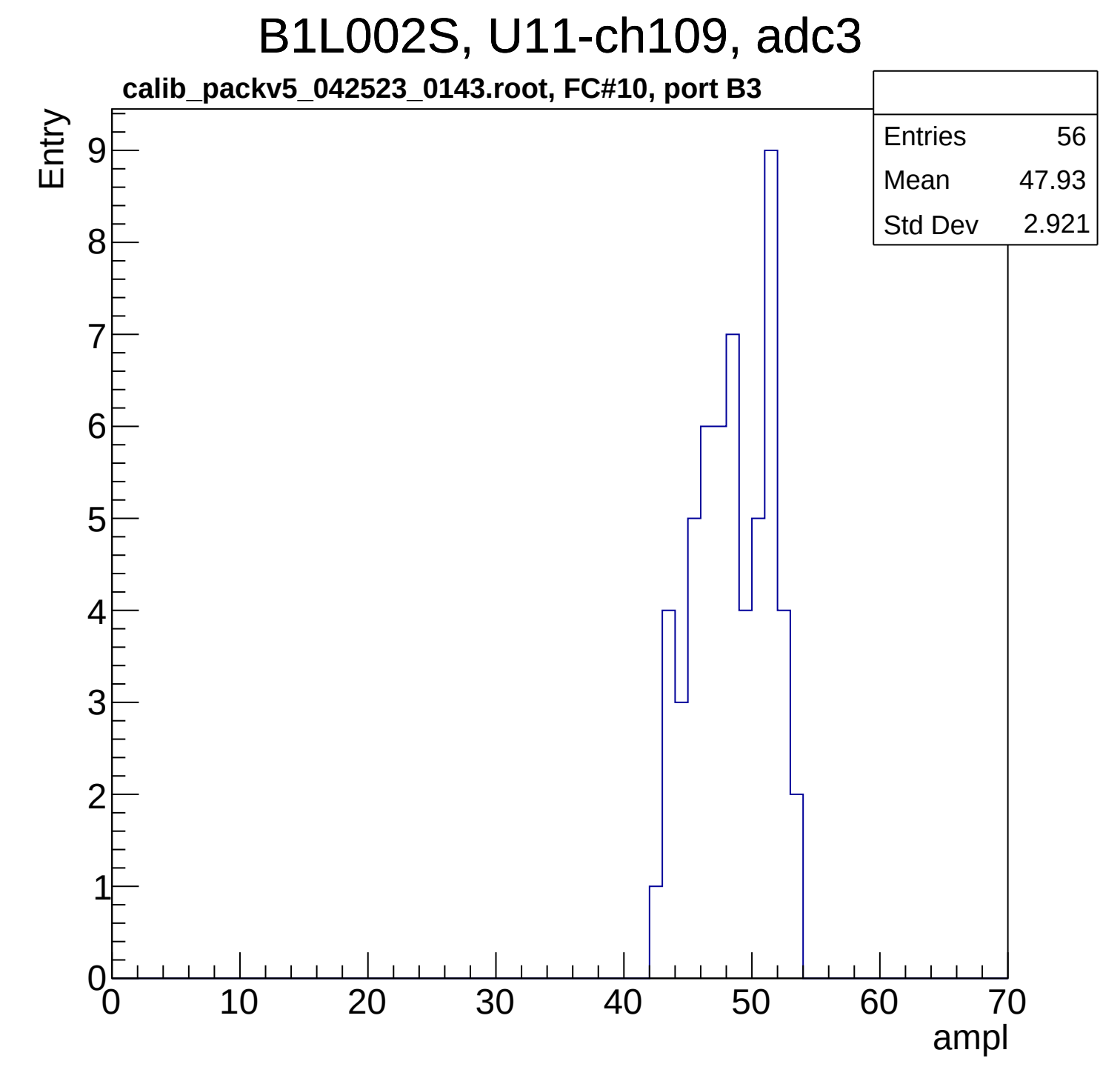
B1L002S, U11-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.93
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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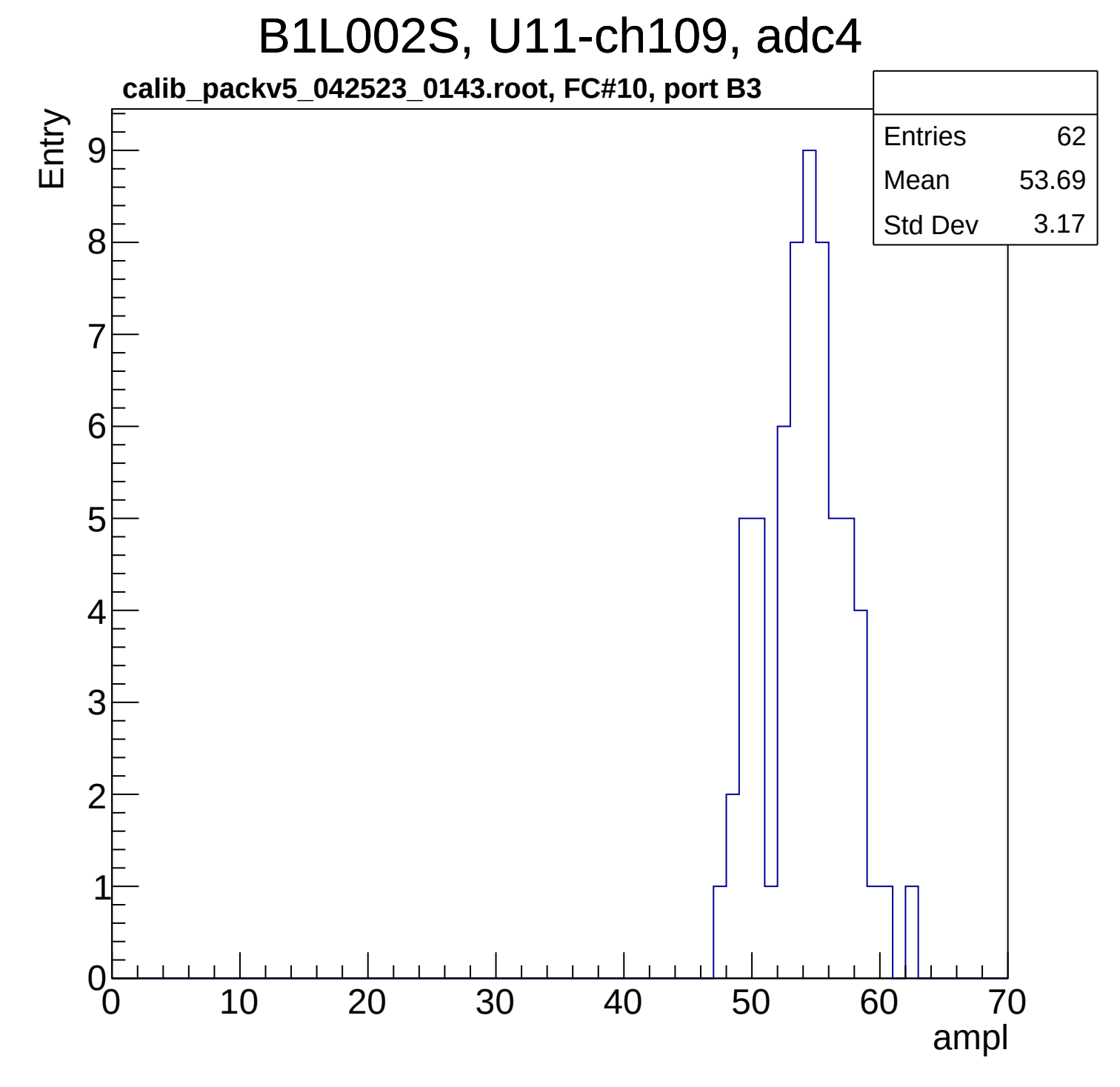
B1L002S, U11-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 53.69
Std Dev 3.17
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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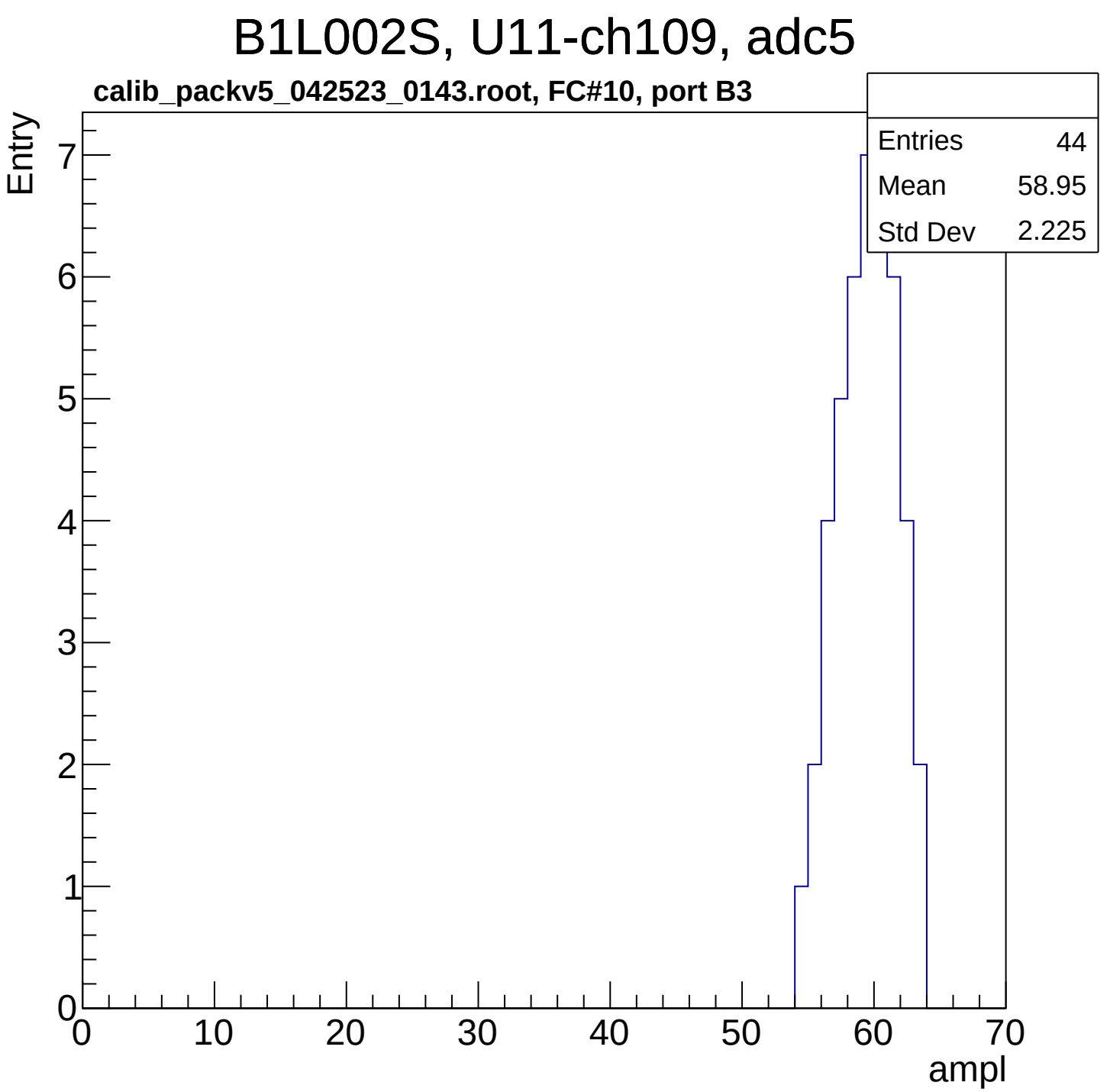
B1L002S, U11-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.95
Std Dev 2.225
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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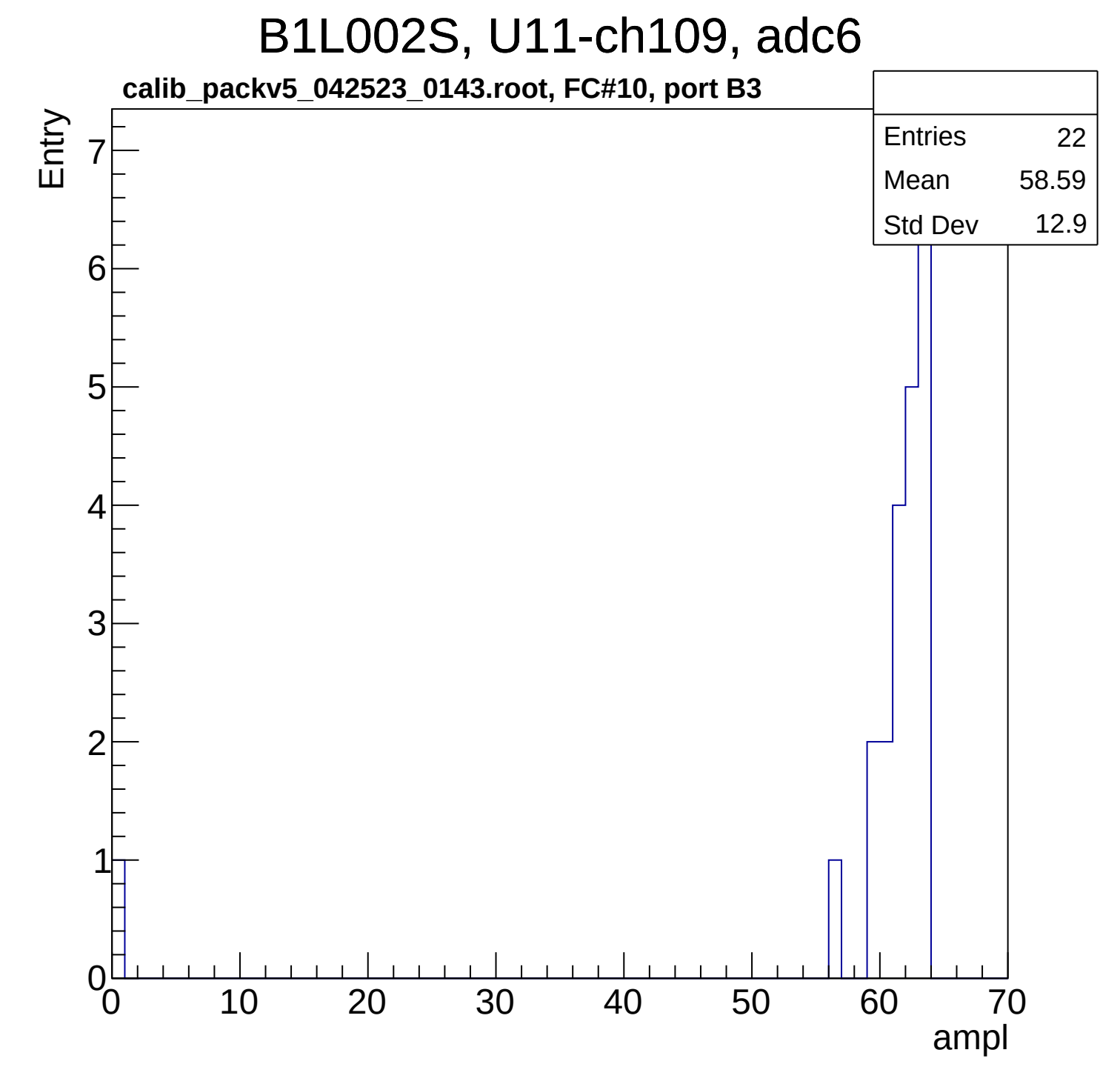
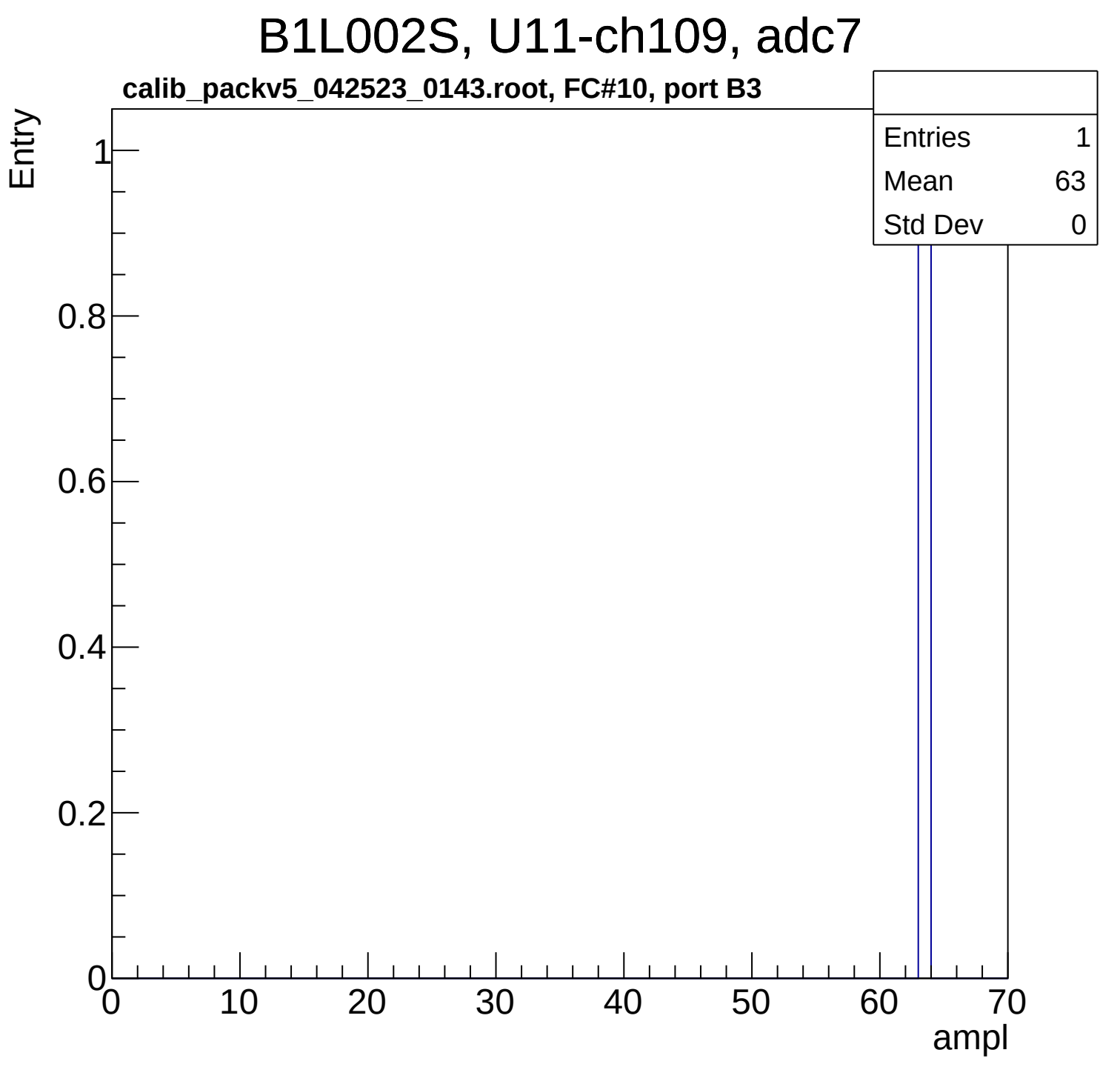

B1L002S, U11-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.59
Std Dev 12.9
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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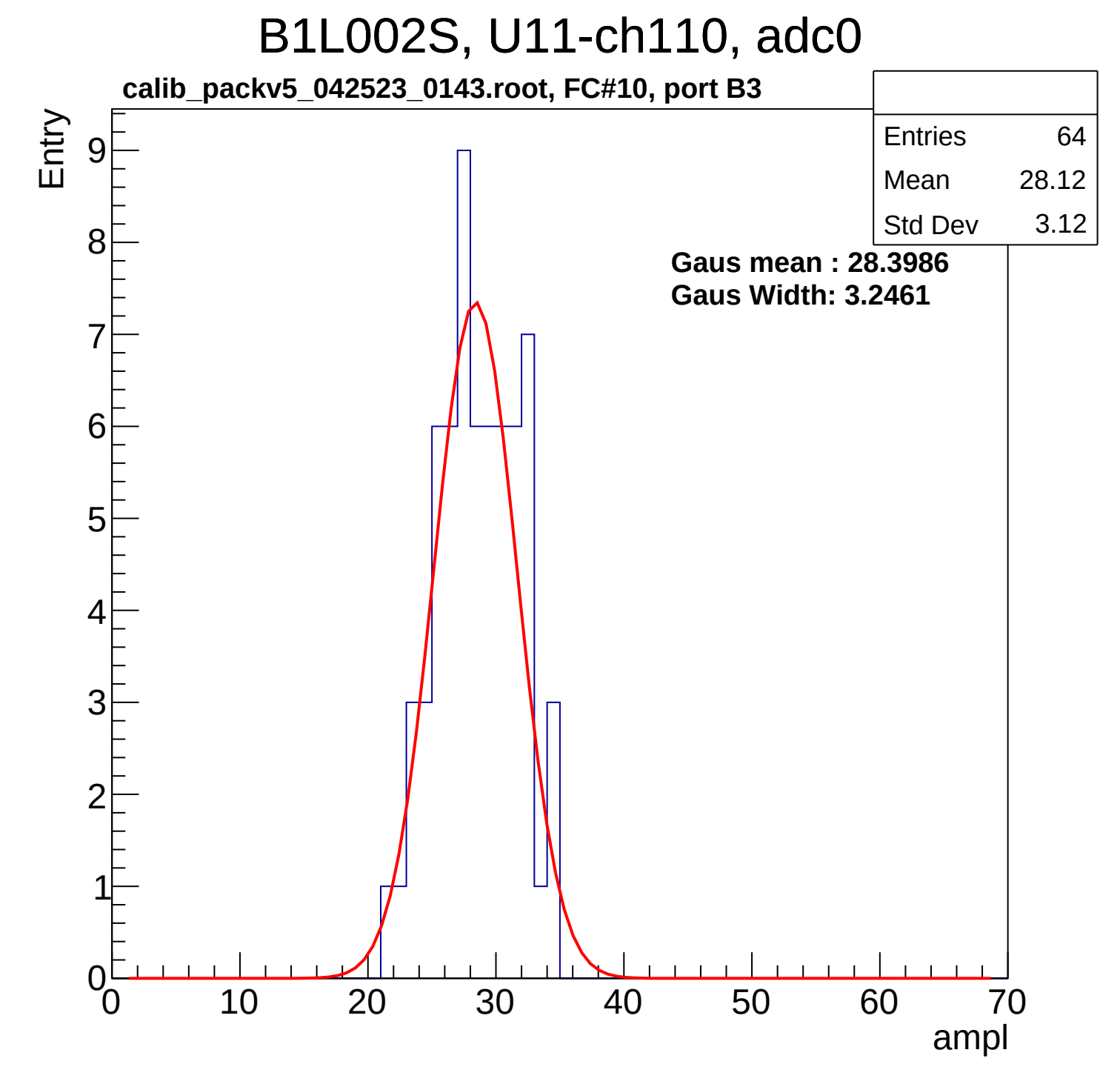
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.12
Std Dev 3.12
Gaus mean : 28.3986
Gaus Width: 3.2461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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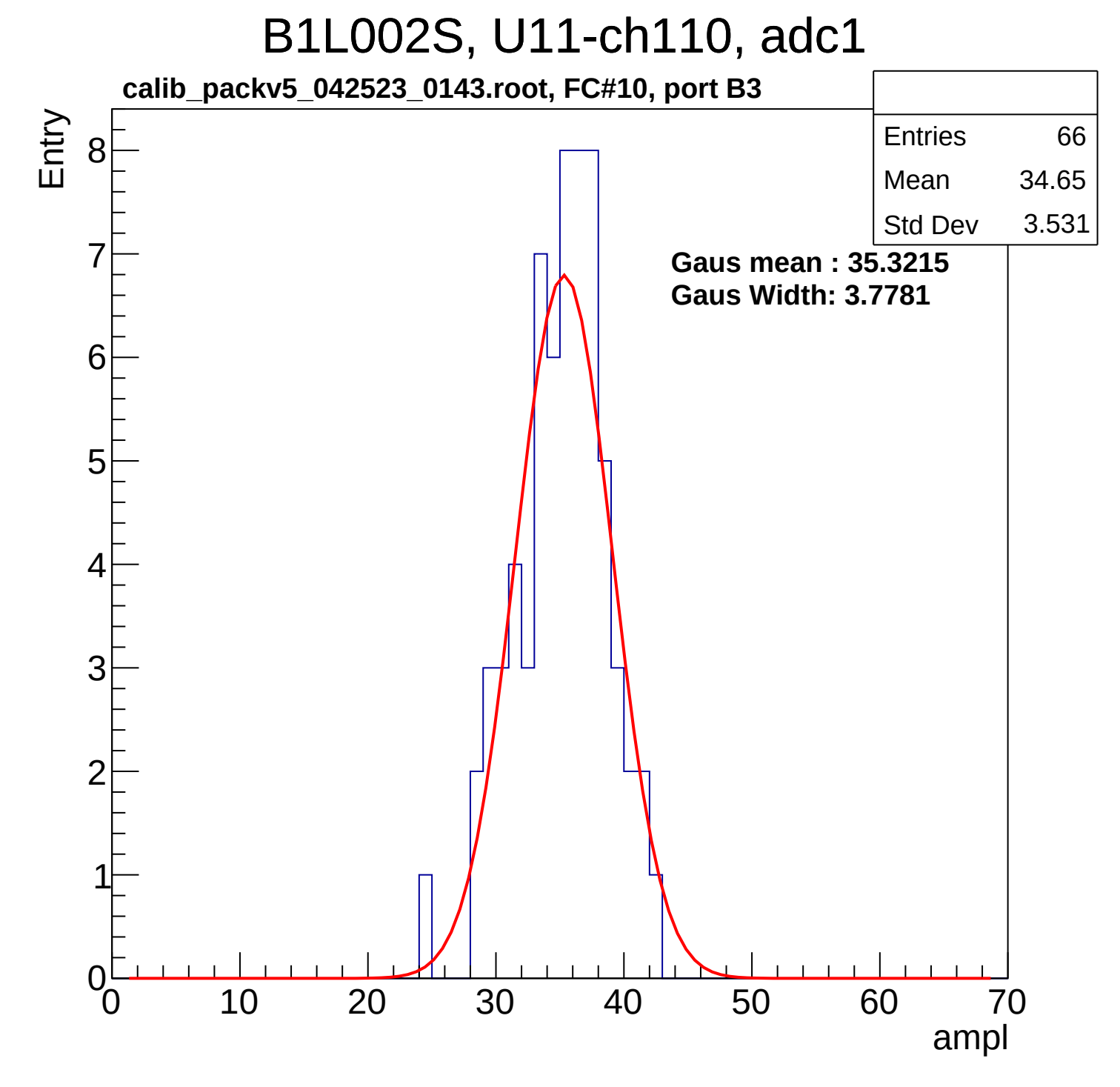
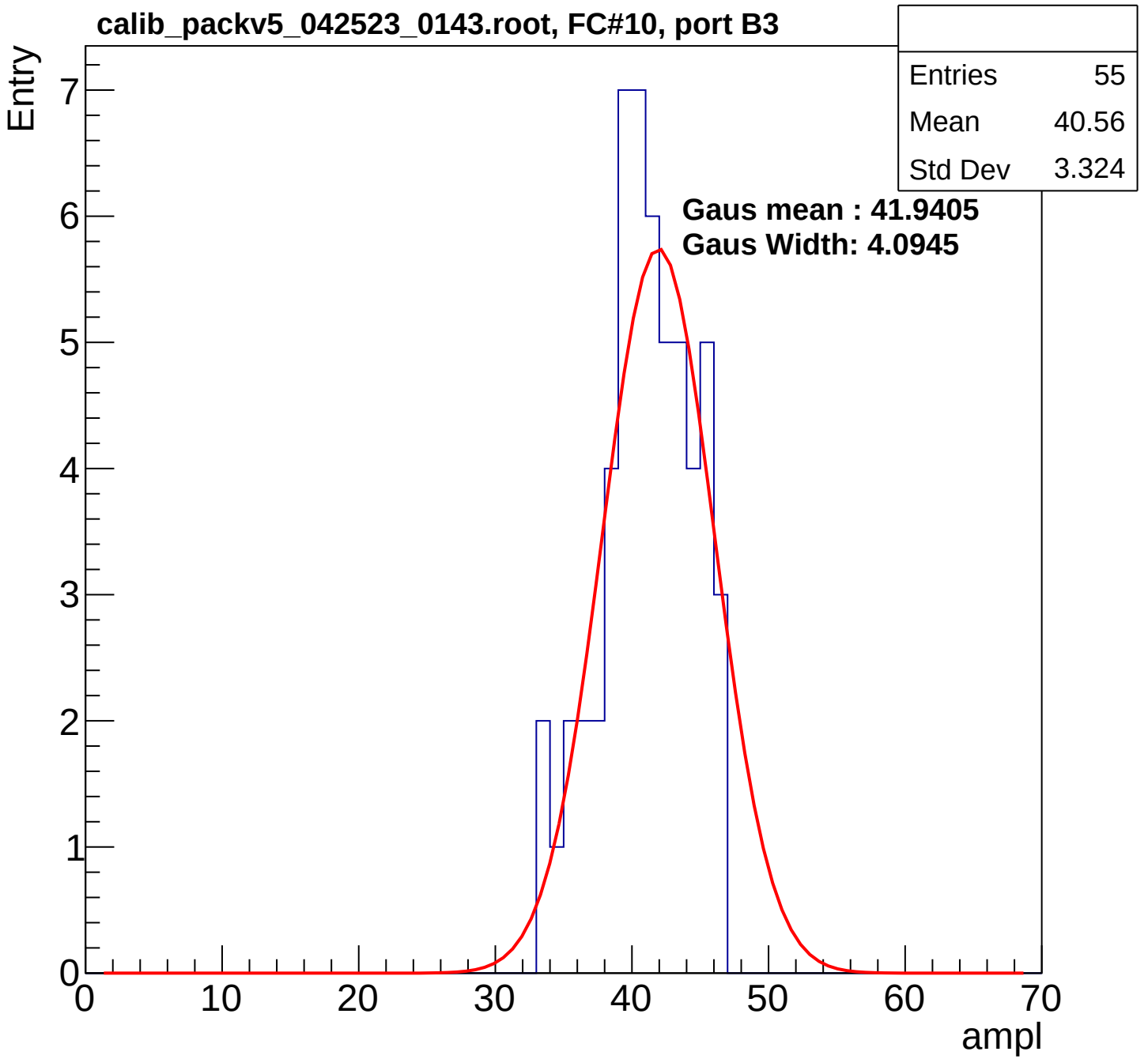
40
50
60
70


B1L002S, U11-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.65
Std Dev 3.531
Gaus mean : 35.3215
Gaus Width: 3.7781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.56
Std Dev 3.324
Gaus mean : 41.9405
Gaus Width: 4.0945
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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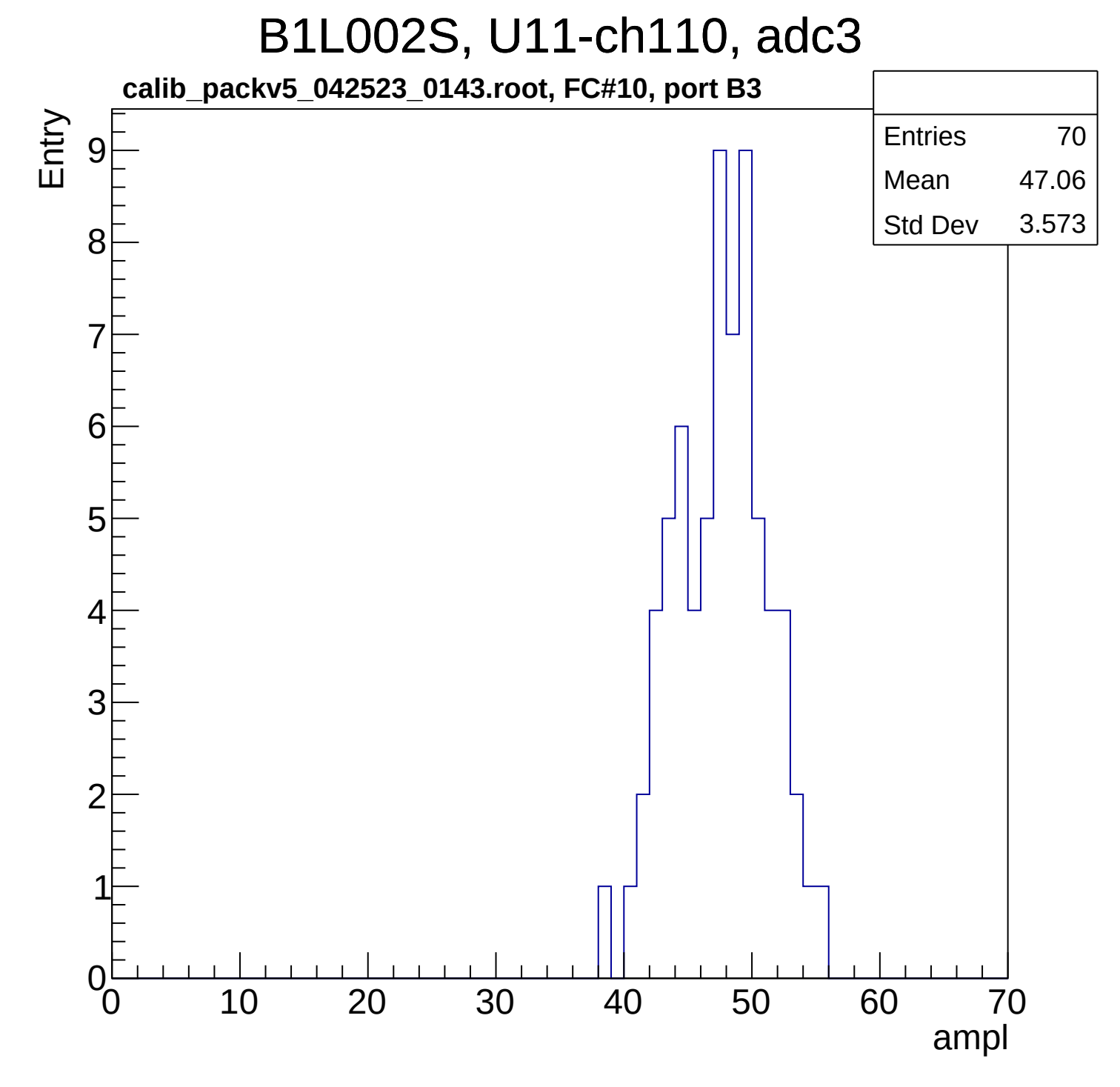
B1L002S, U11-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.06
Std Dev 3.573
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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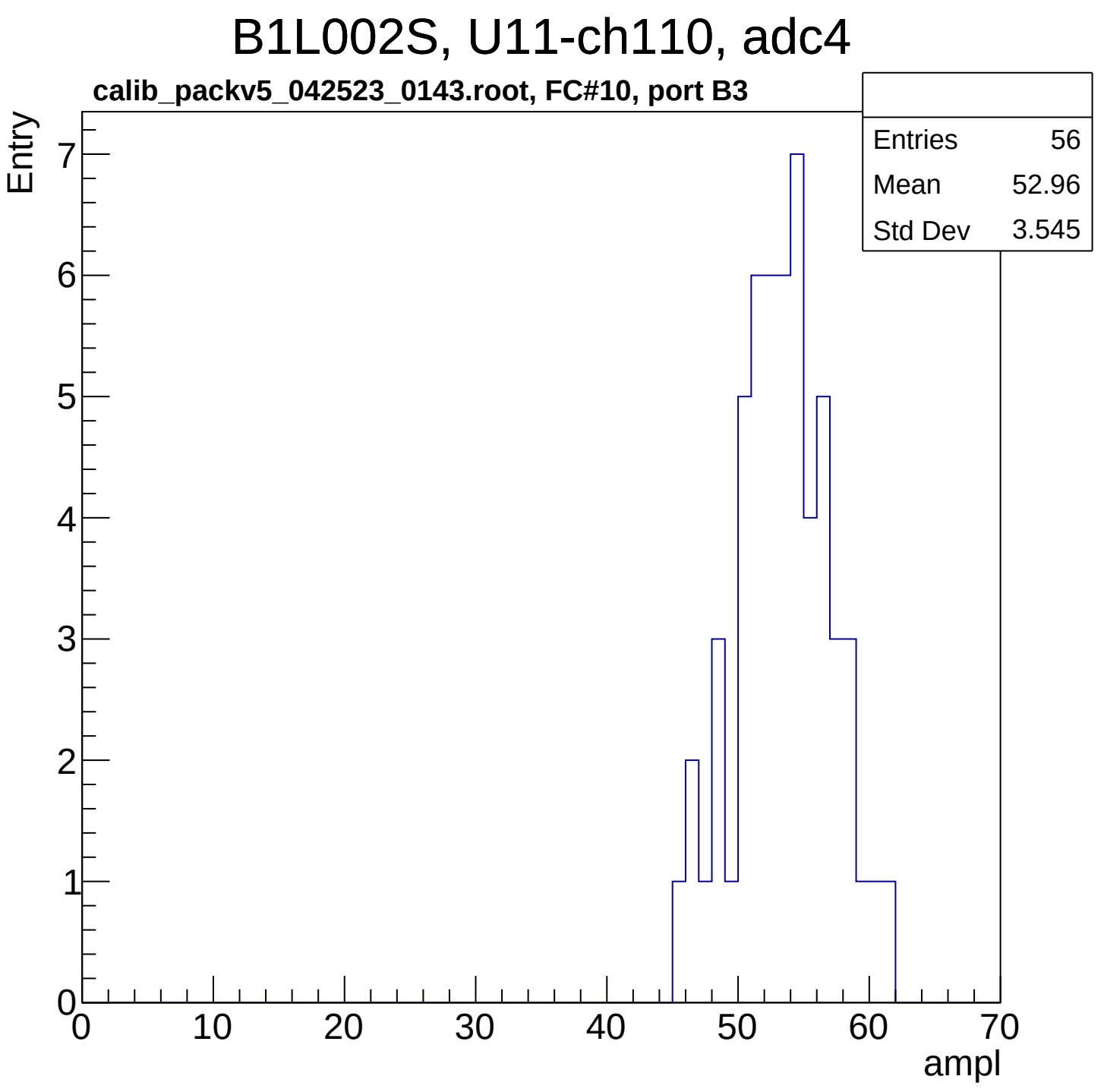
B1L002S, U11-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.96
Std Dev 3.545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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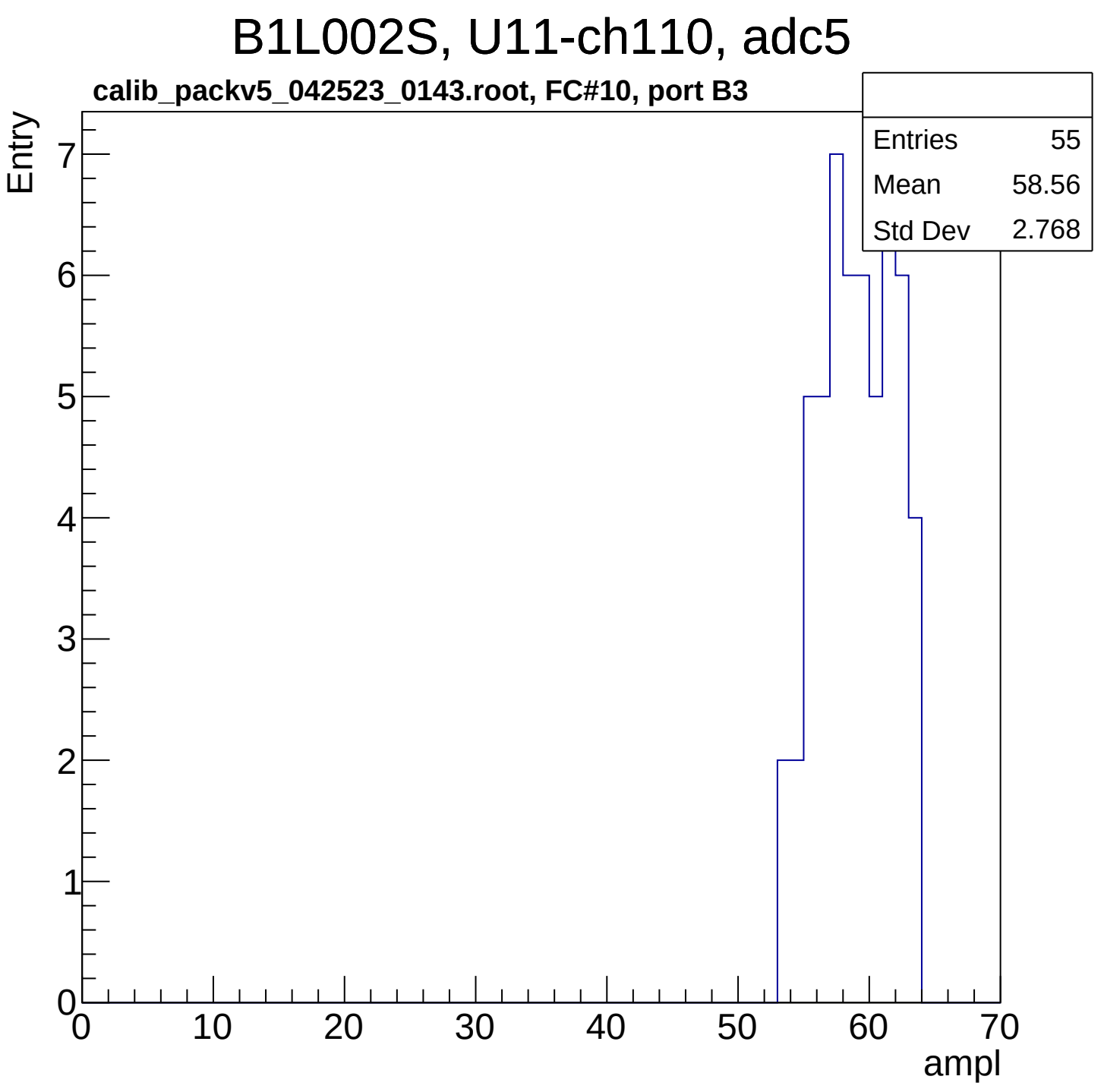
B1L002S, U11-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.56
Std Dev 2.768
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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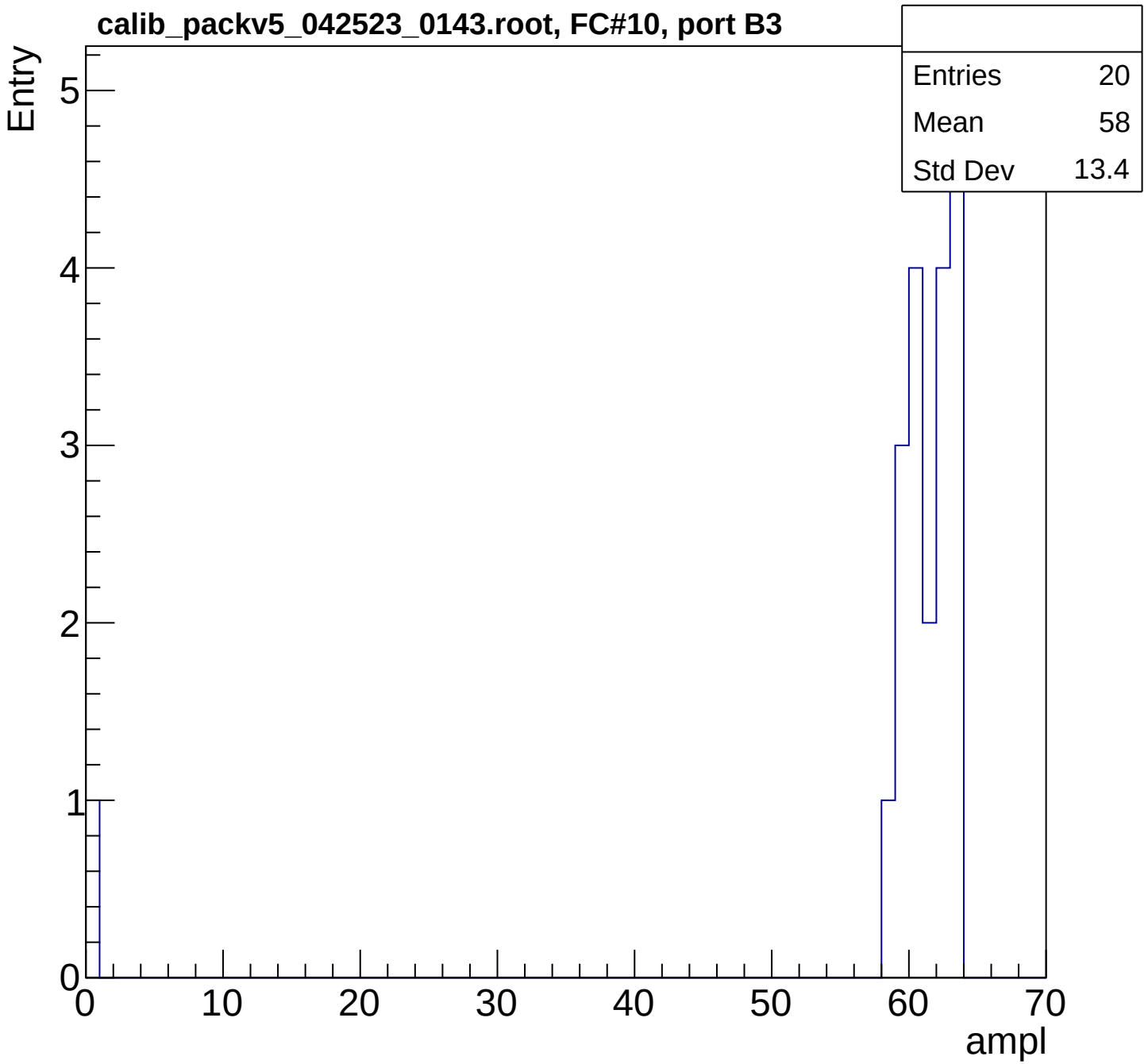
B1L002S, U11-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58
Std Dev 13.4
Entry
0
1
2
3
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5
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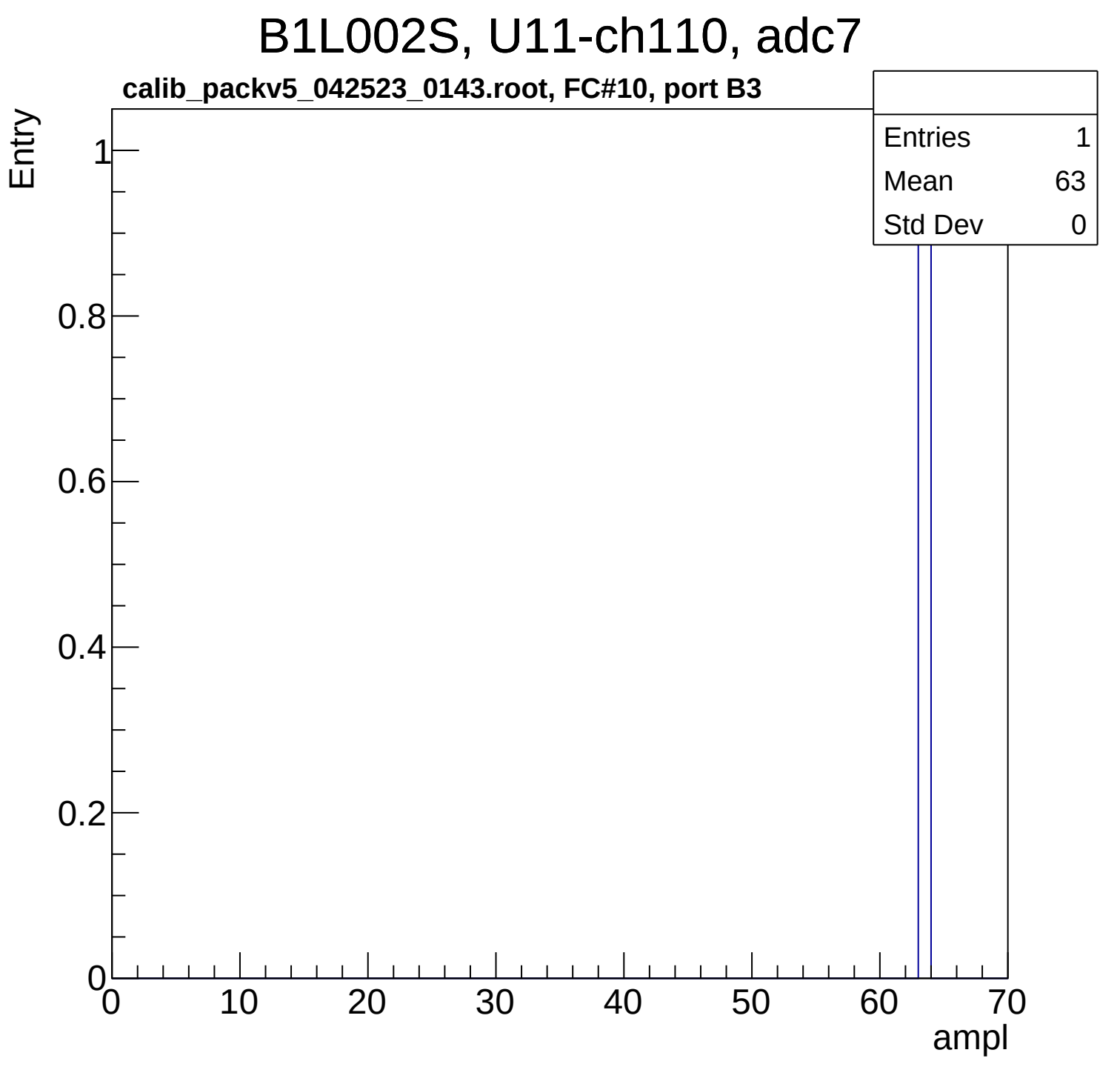
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.4
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0
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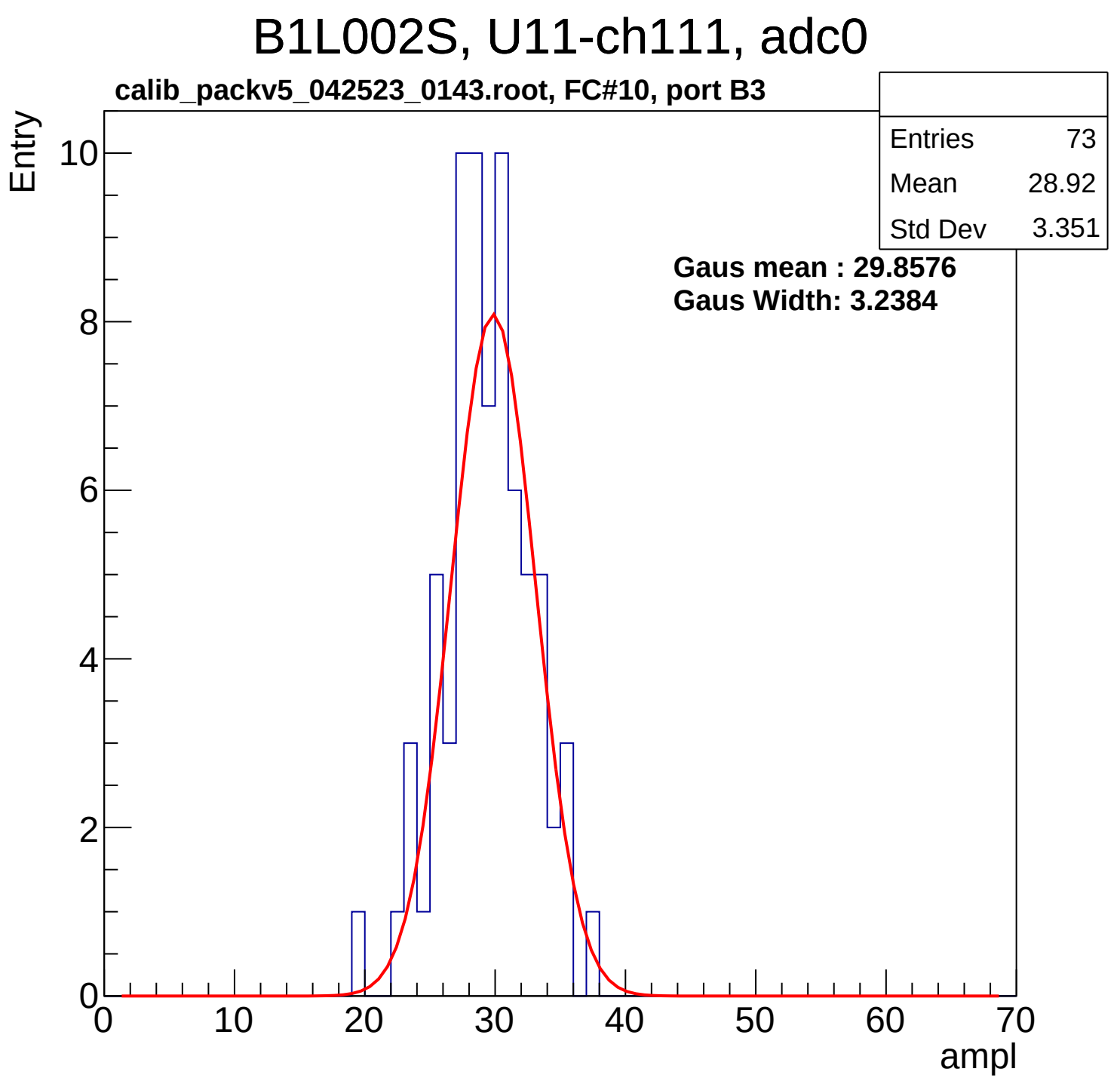
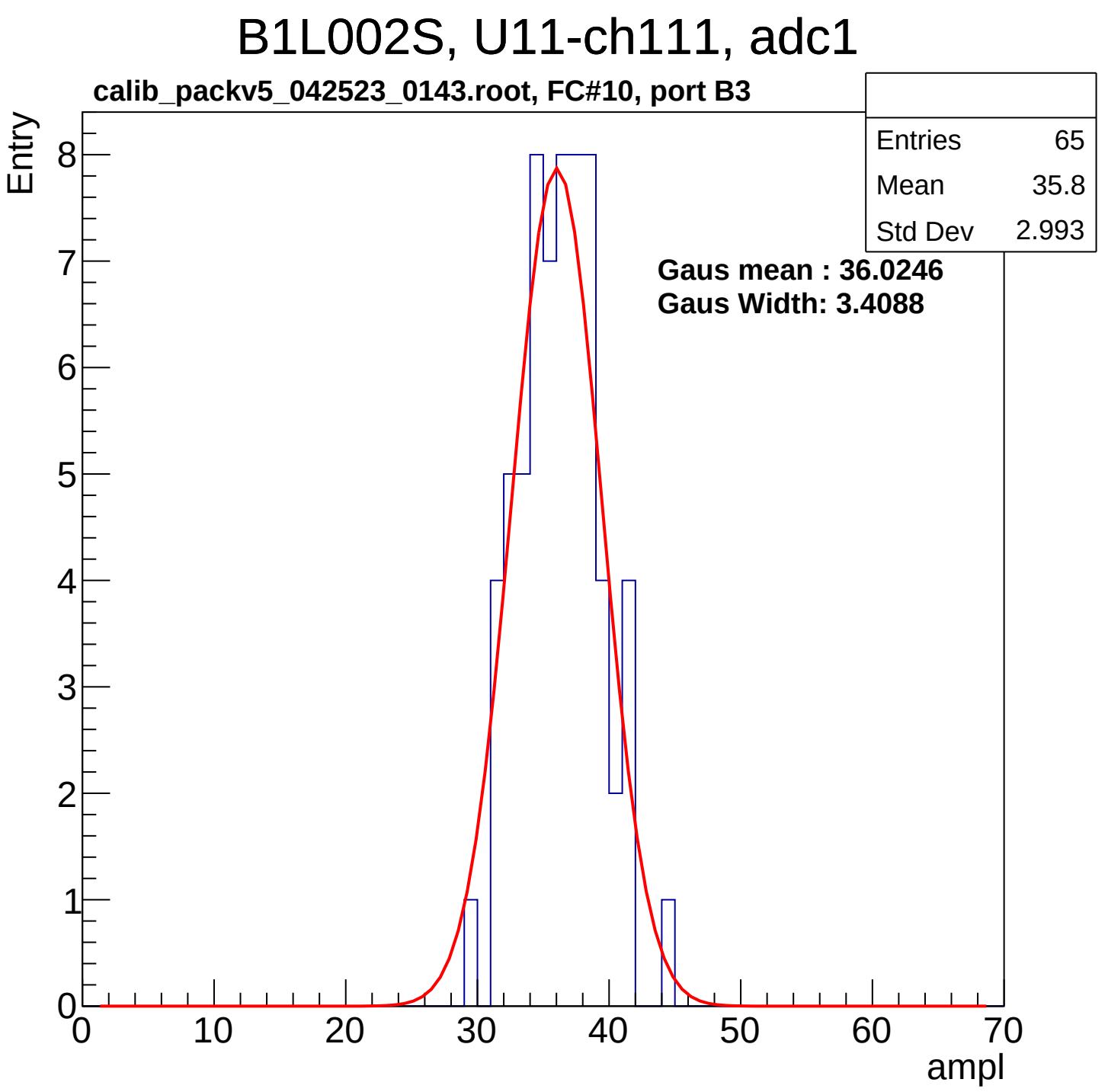
60
70
ampl


B1L002S, U11-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.92
Std Dev 3.351
Gaus mean : 29.8576
Gaus Width: 3.2384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.8
Std Dev 2.993
Gaus mean : 36.0246
Gaus Width: 3.4088
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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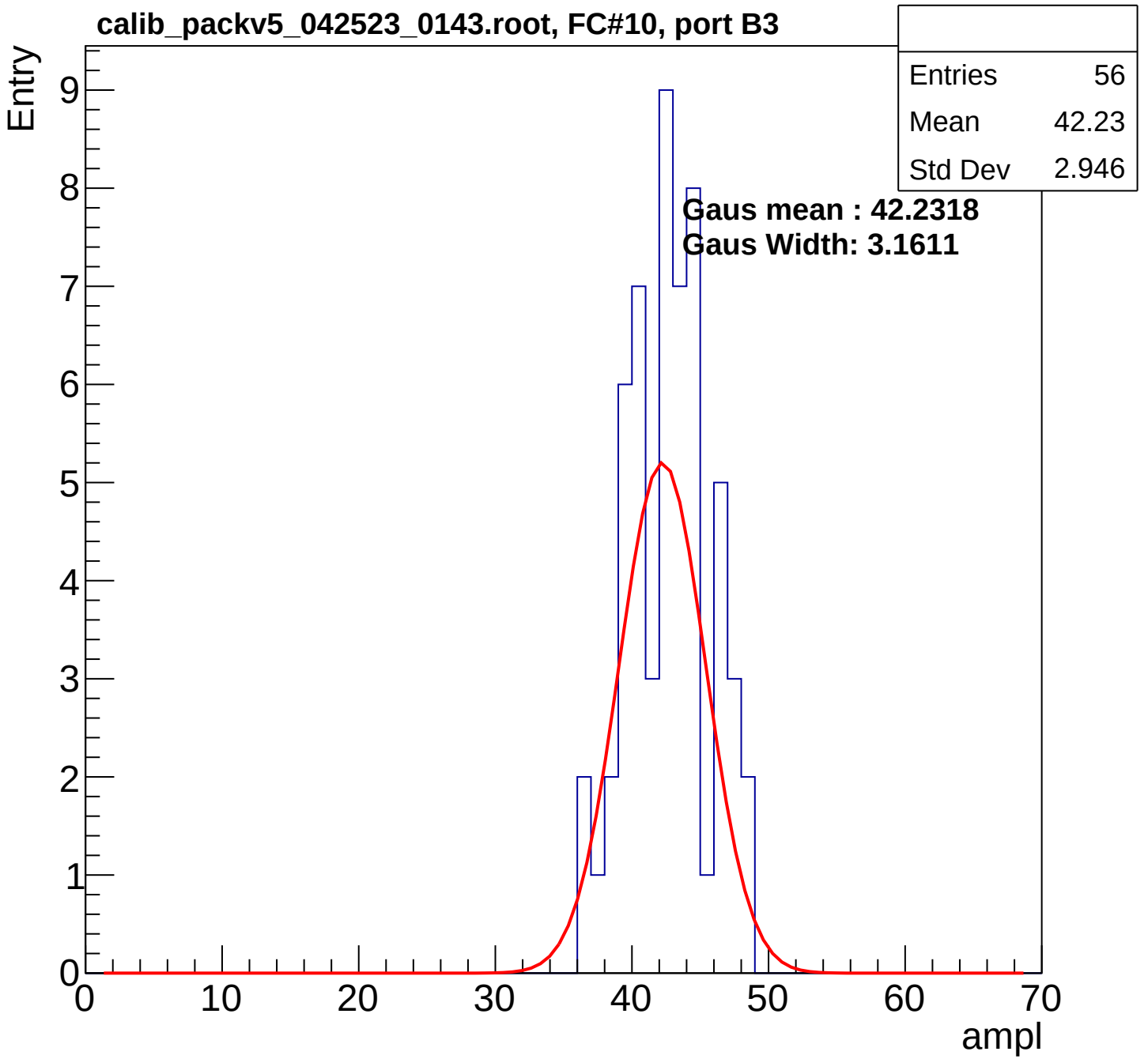
B1L002S, U11-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 42.23
Std Dev 2.946
Gaus mean : 42.2318
Gaus Width: 3.1611
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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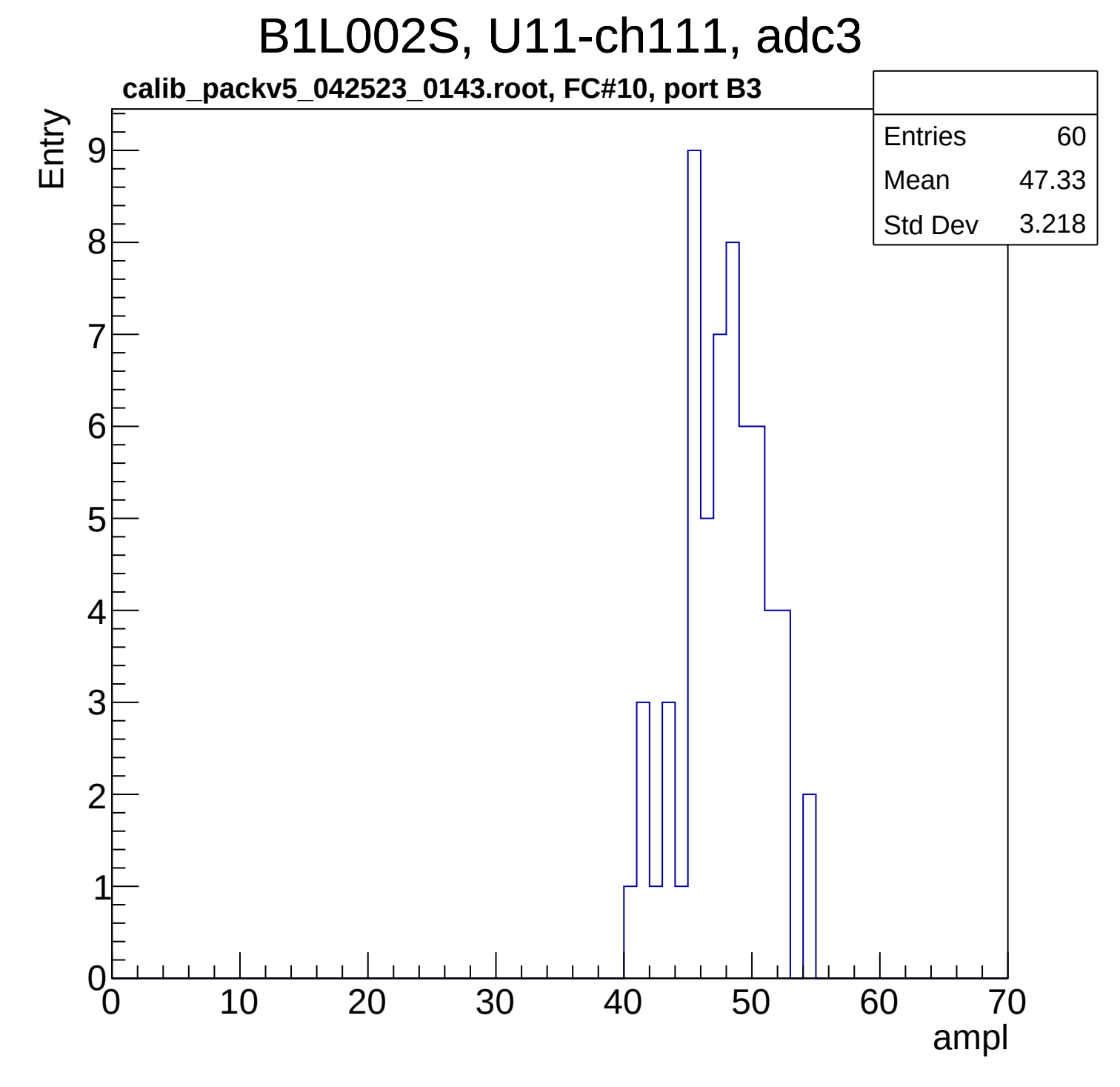
B1L002S, U11-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.33
Std Dev 3.218
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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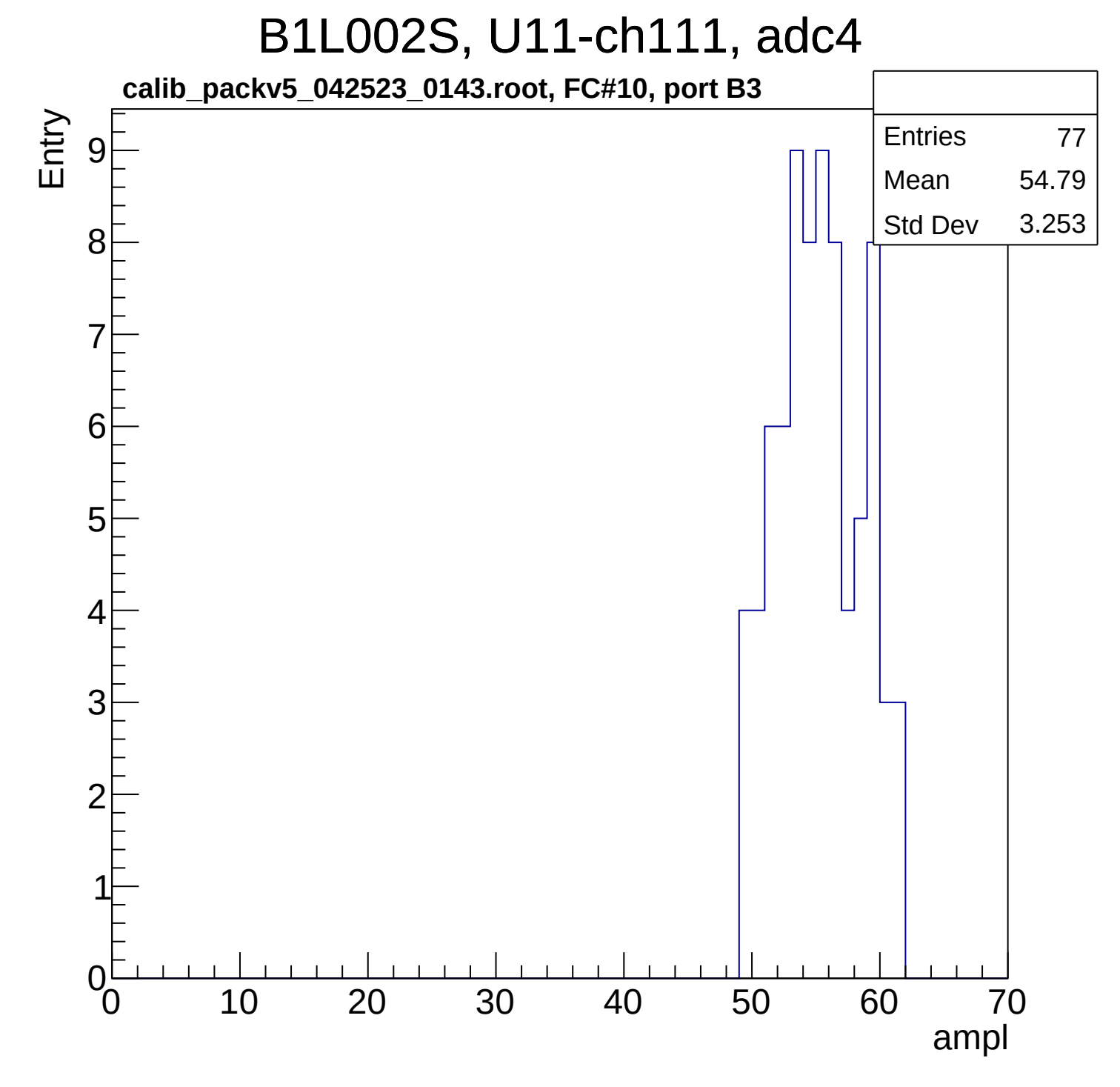
B1L002S, U11-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 54.79
Std Dev 3.253
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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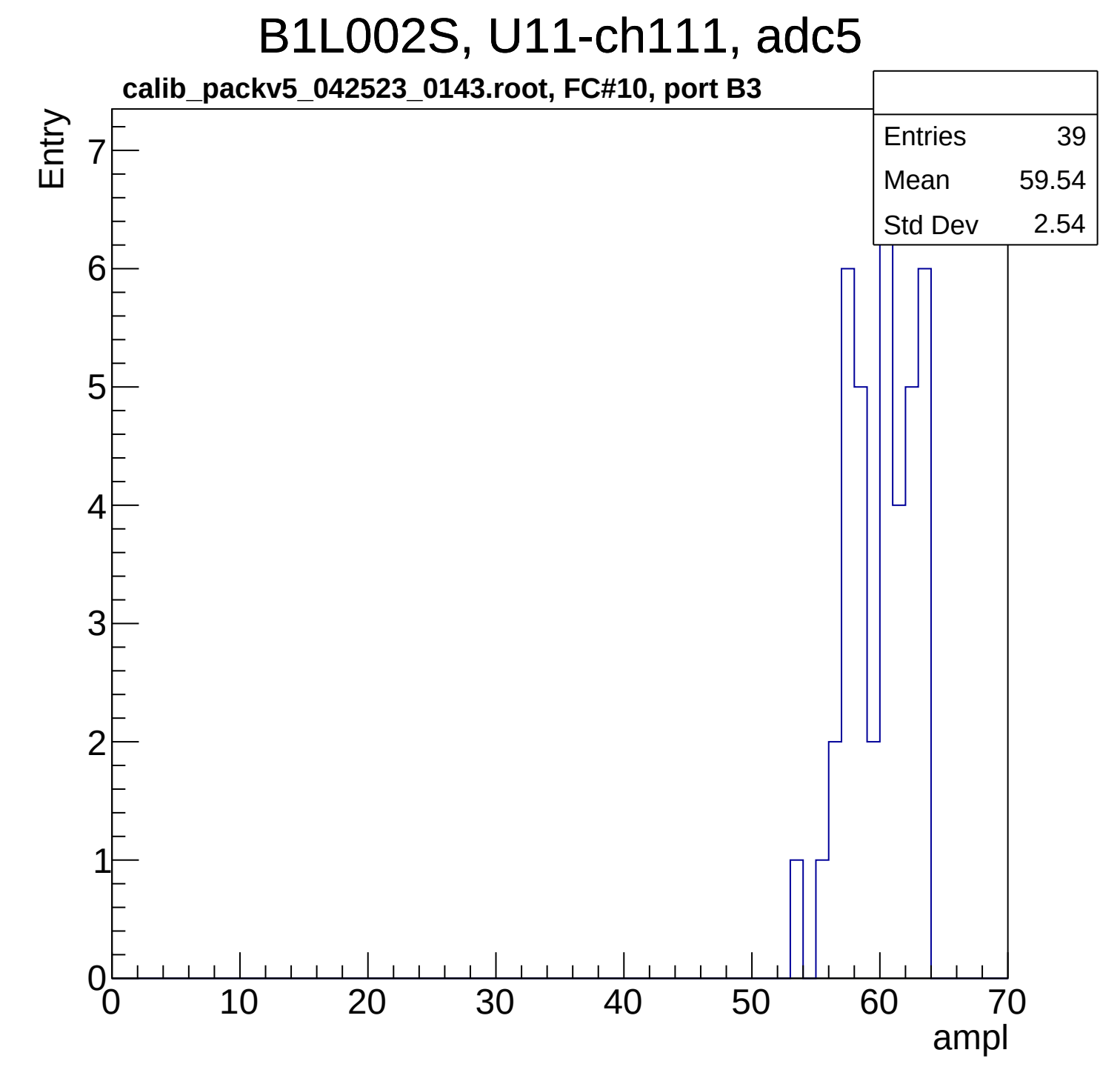
B1L002S, U11-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 59.54
Std Dev 2.54
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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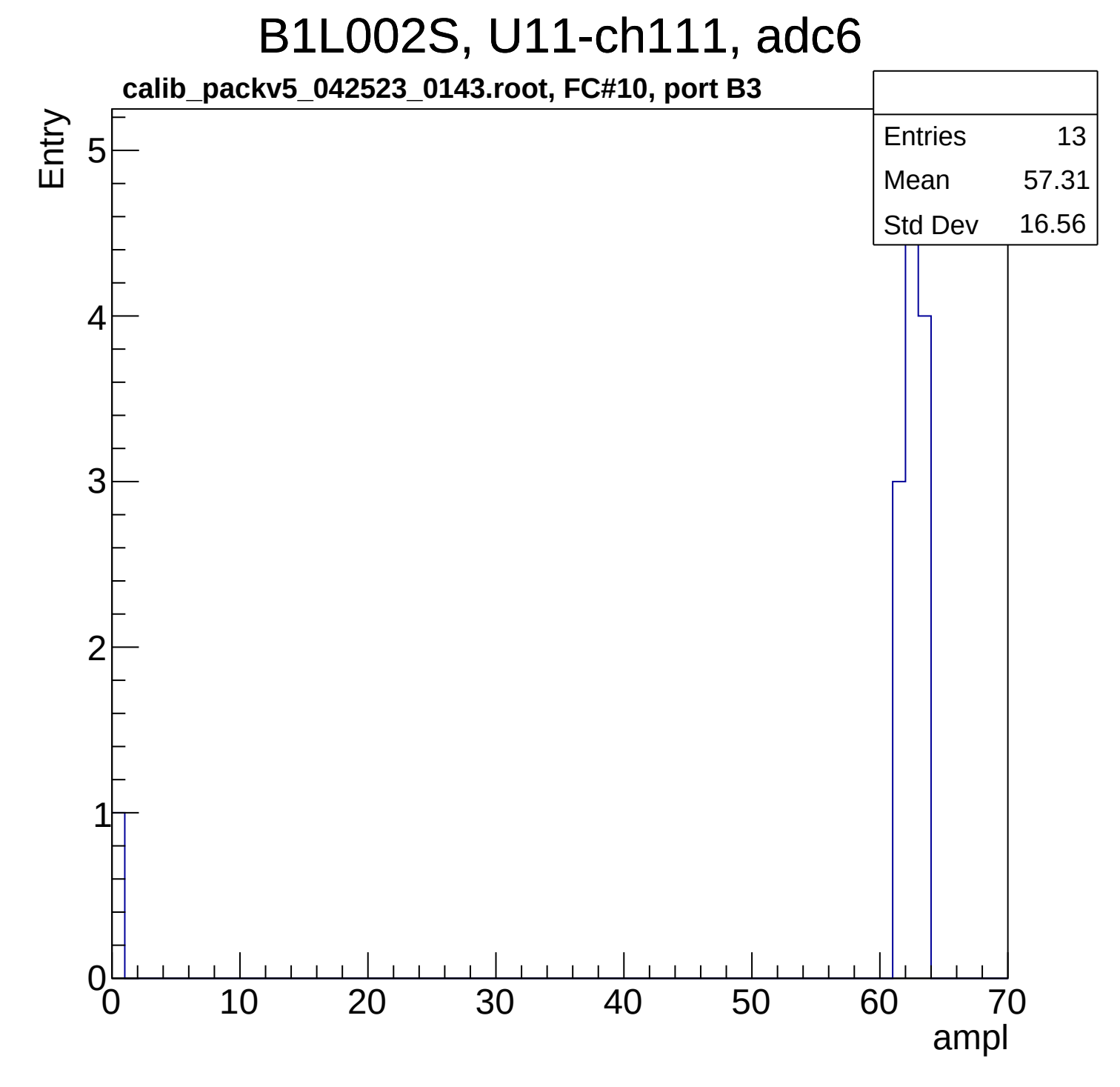
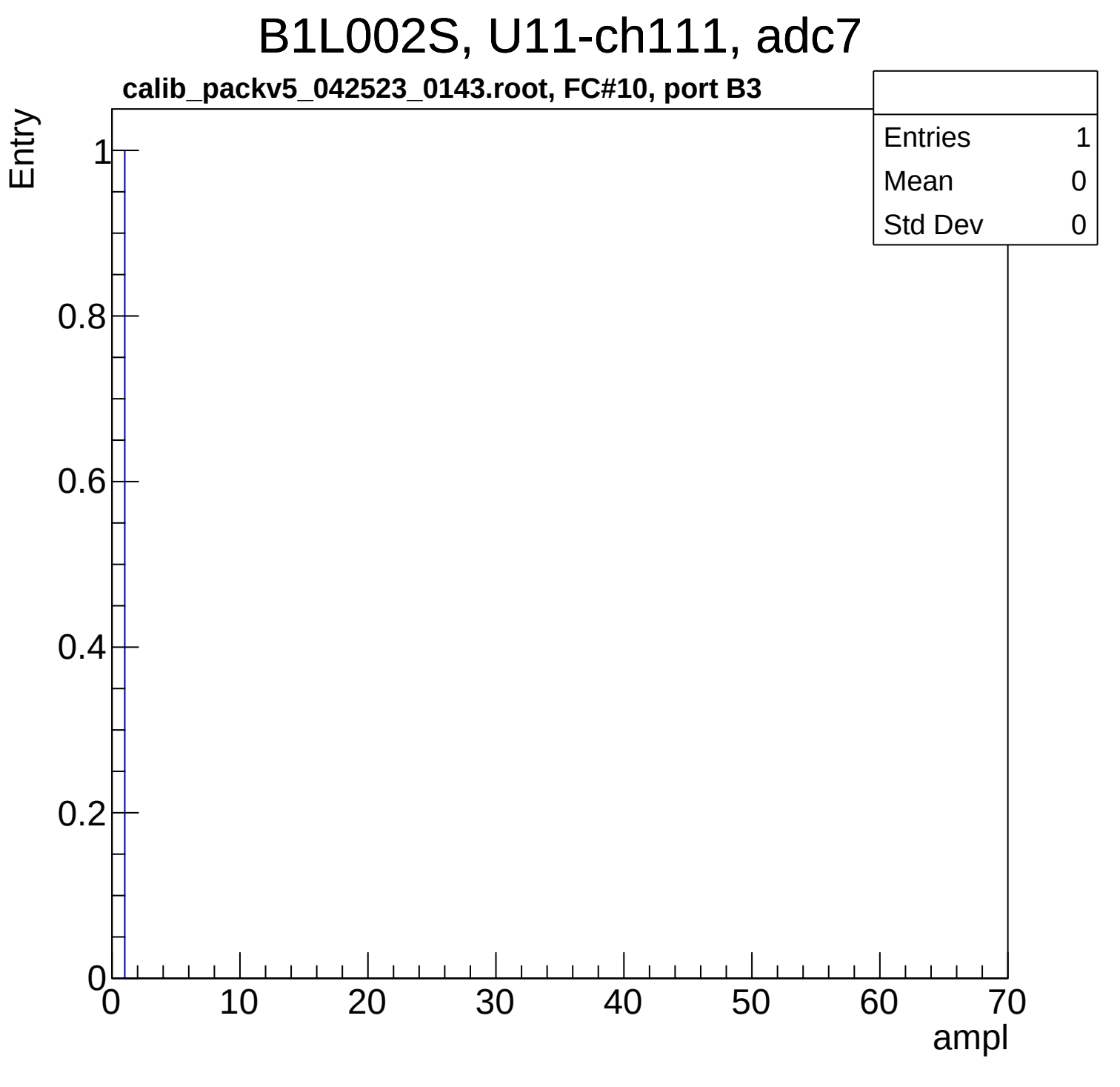

B1L002S, U11-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.31
Std Dev 16.56
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
40
50
60
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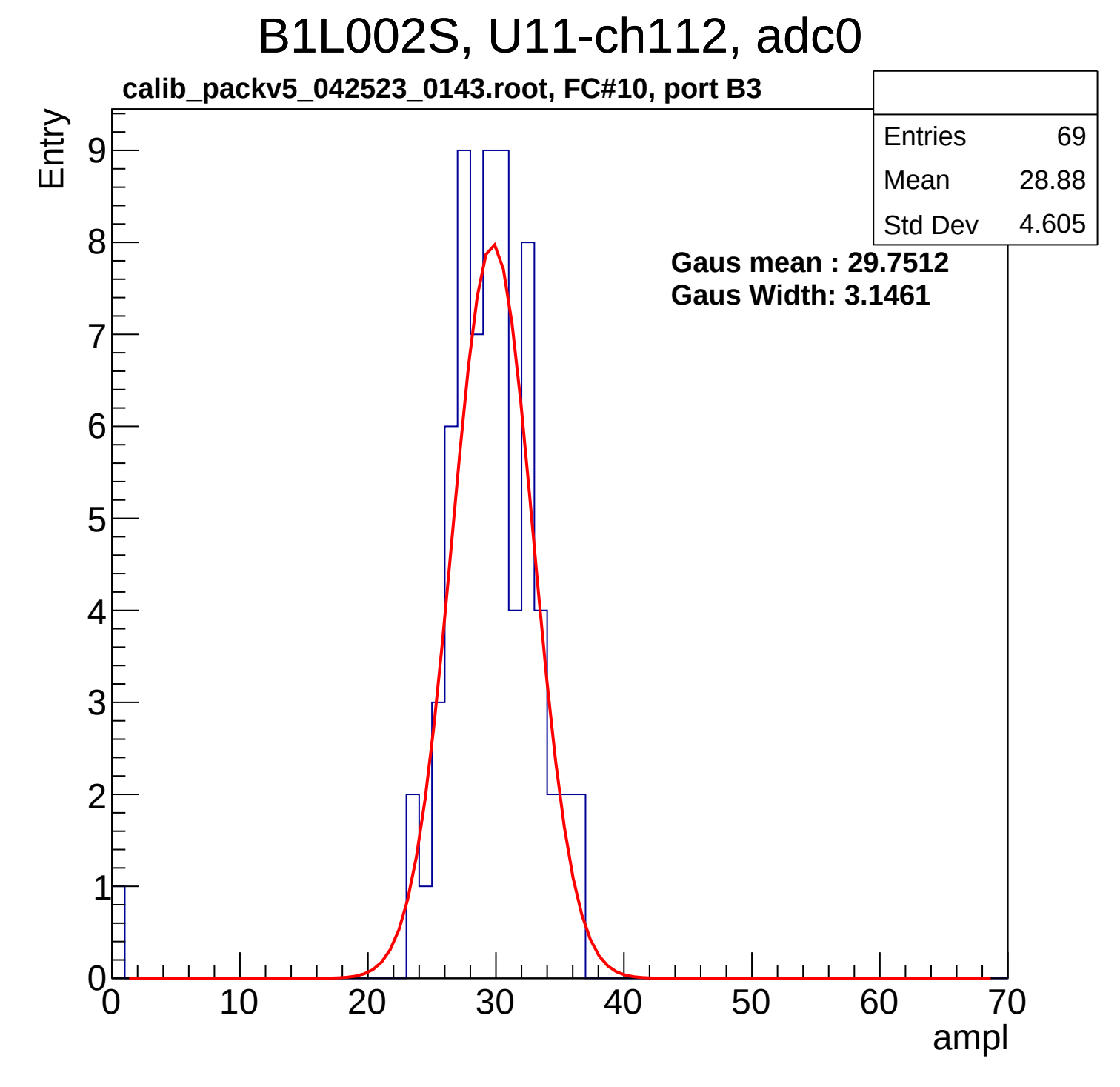
ampl


B1L002S, U11-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.88
Std Dev 4.605
Gaus mean : 29.7512
Gaus Width: 3.1461
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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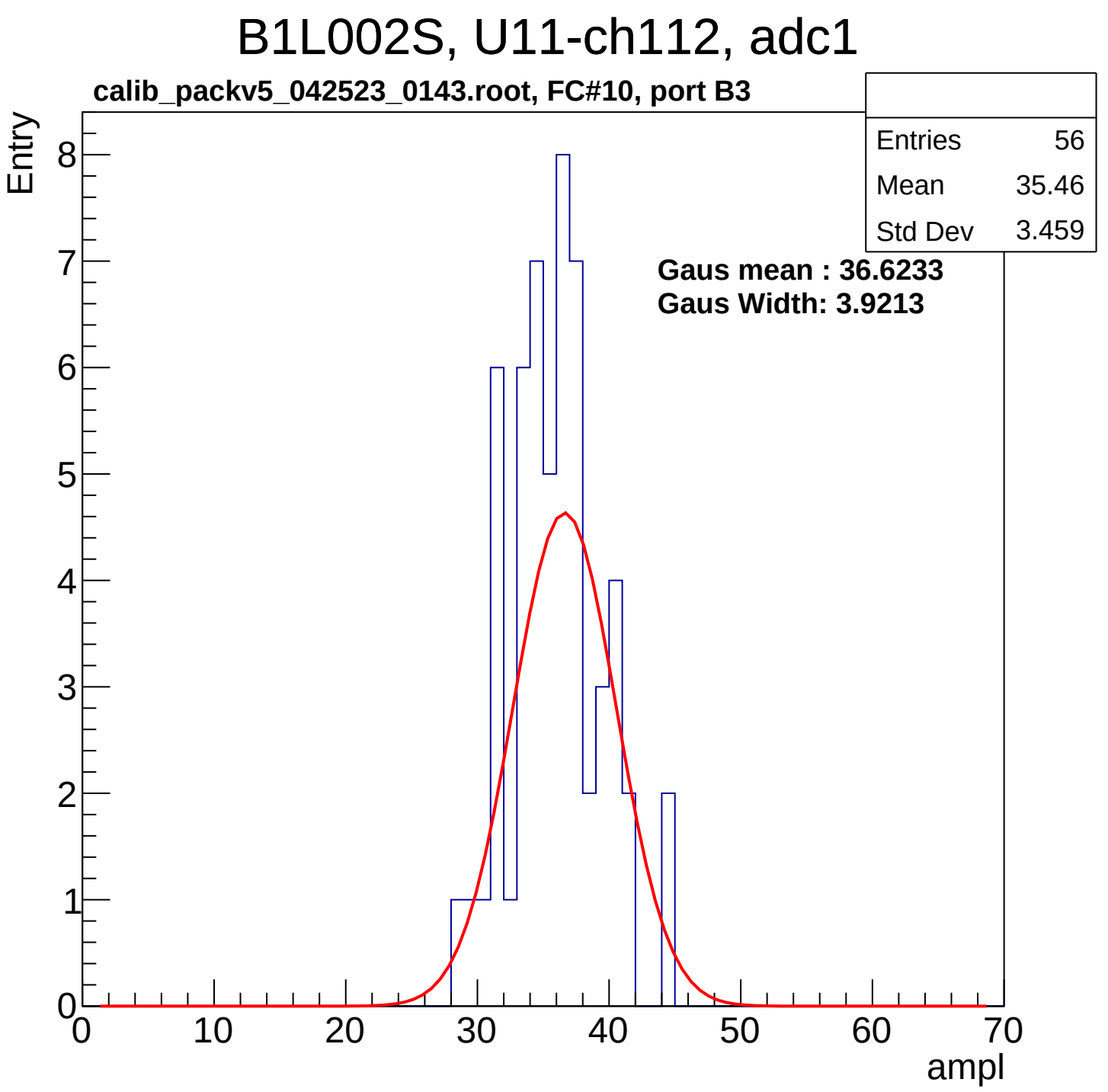
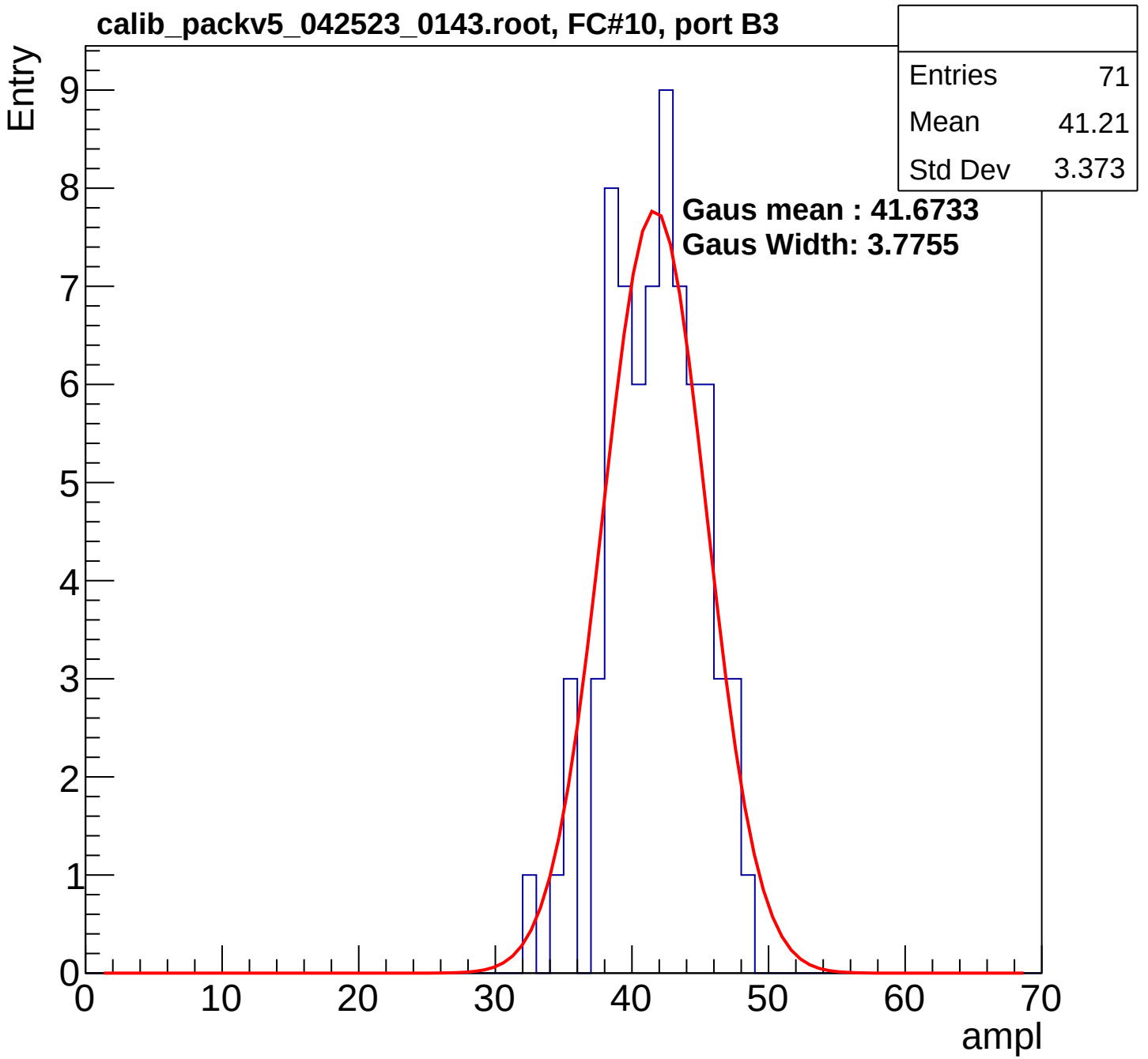
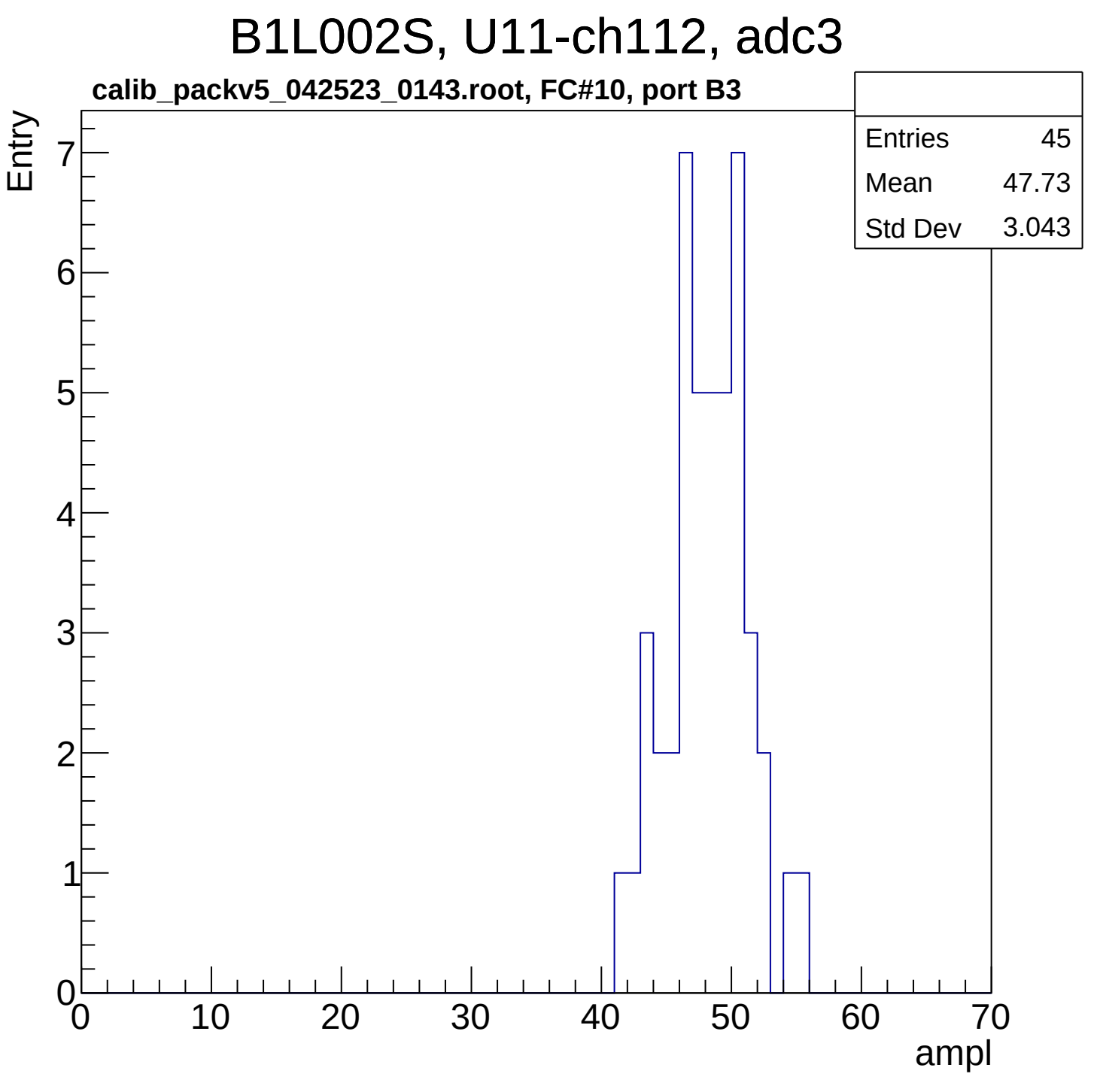

B1L002S, U11-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 35.46
Std Dev 3.459
Gaus mean : 36.6233
Gaus Width: 3.9213
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 41.21
Std Dev 3.373
Gaus mean : 41.6733
Gaus Width: 3.7755
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 47.73
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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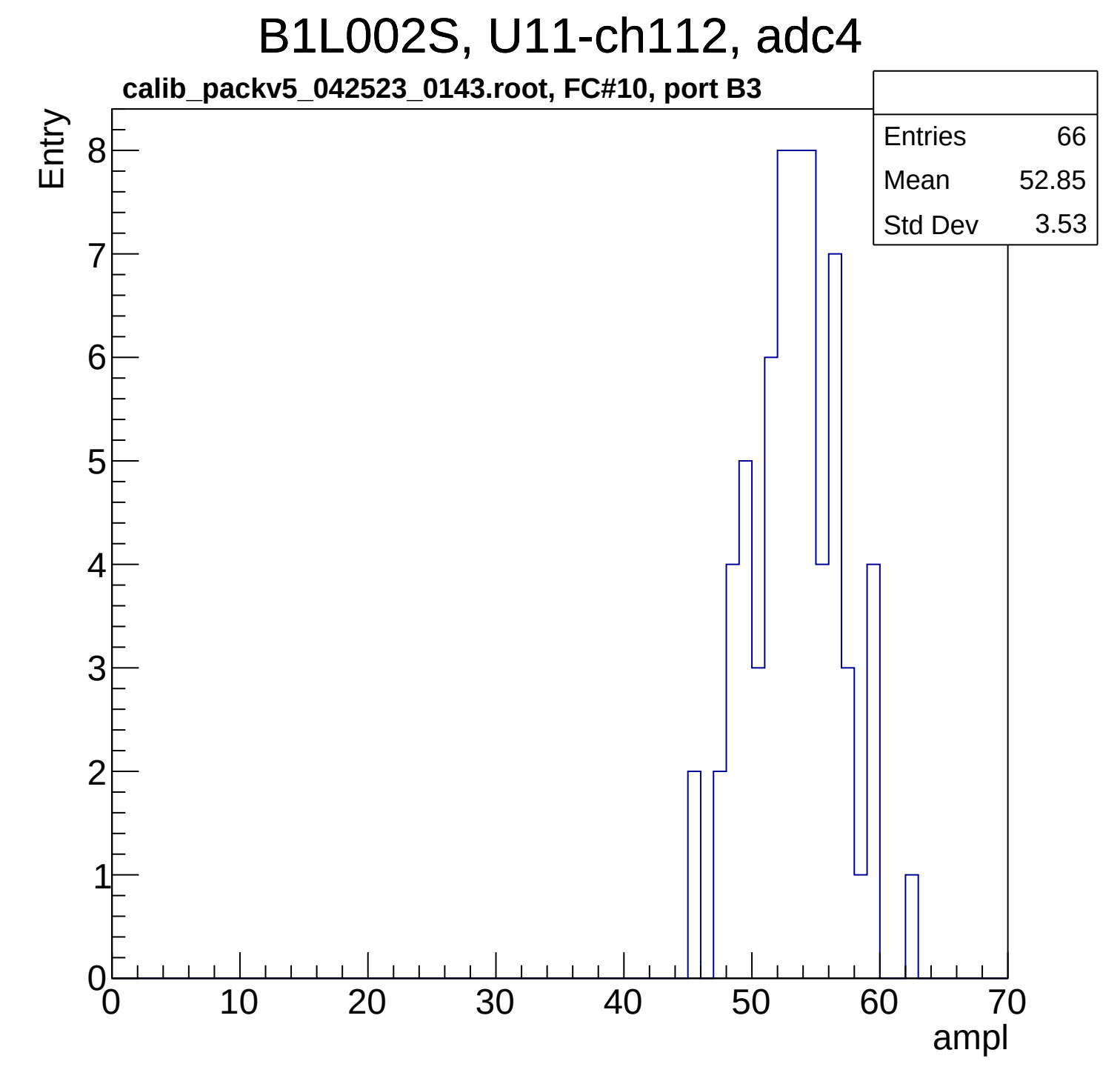
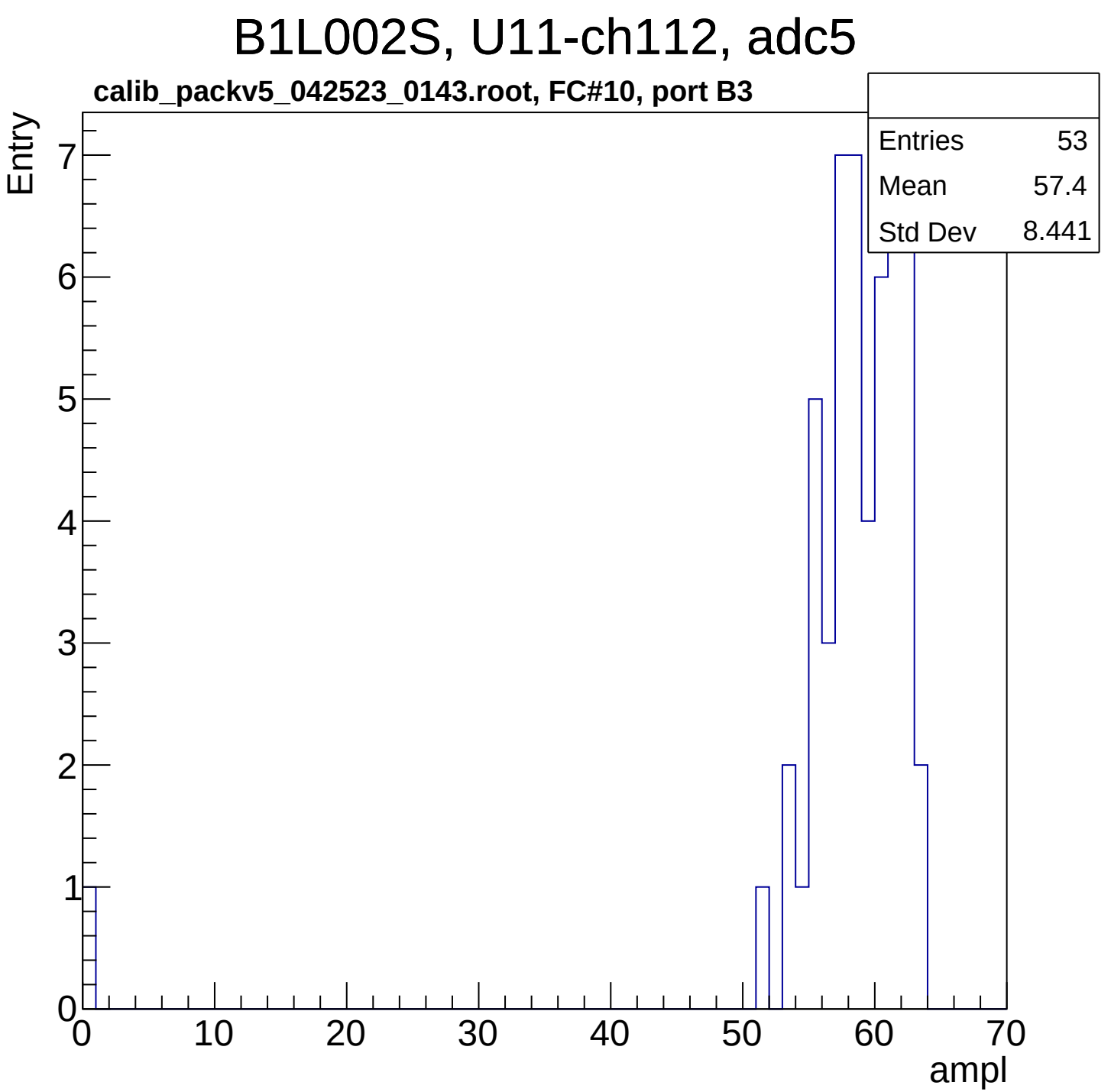
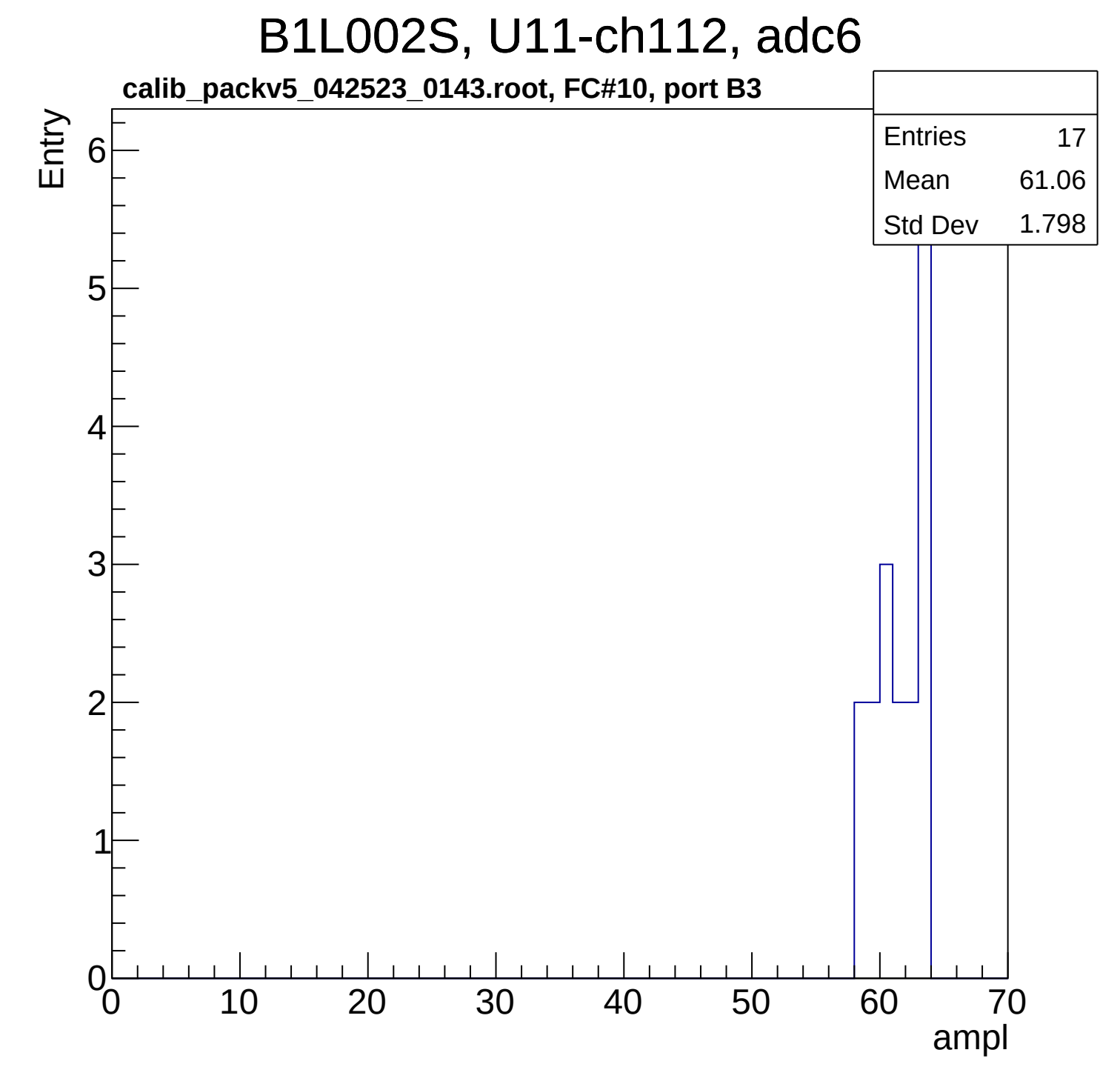
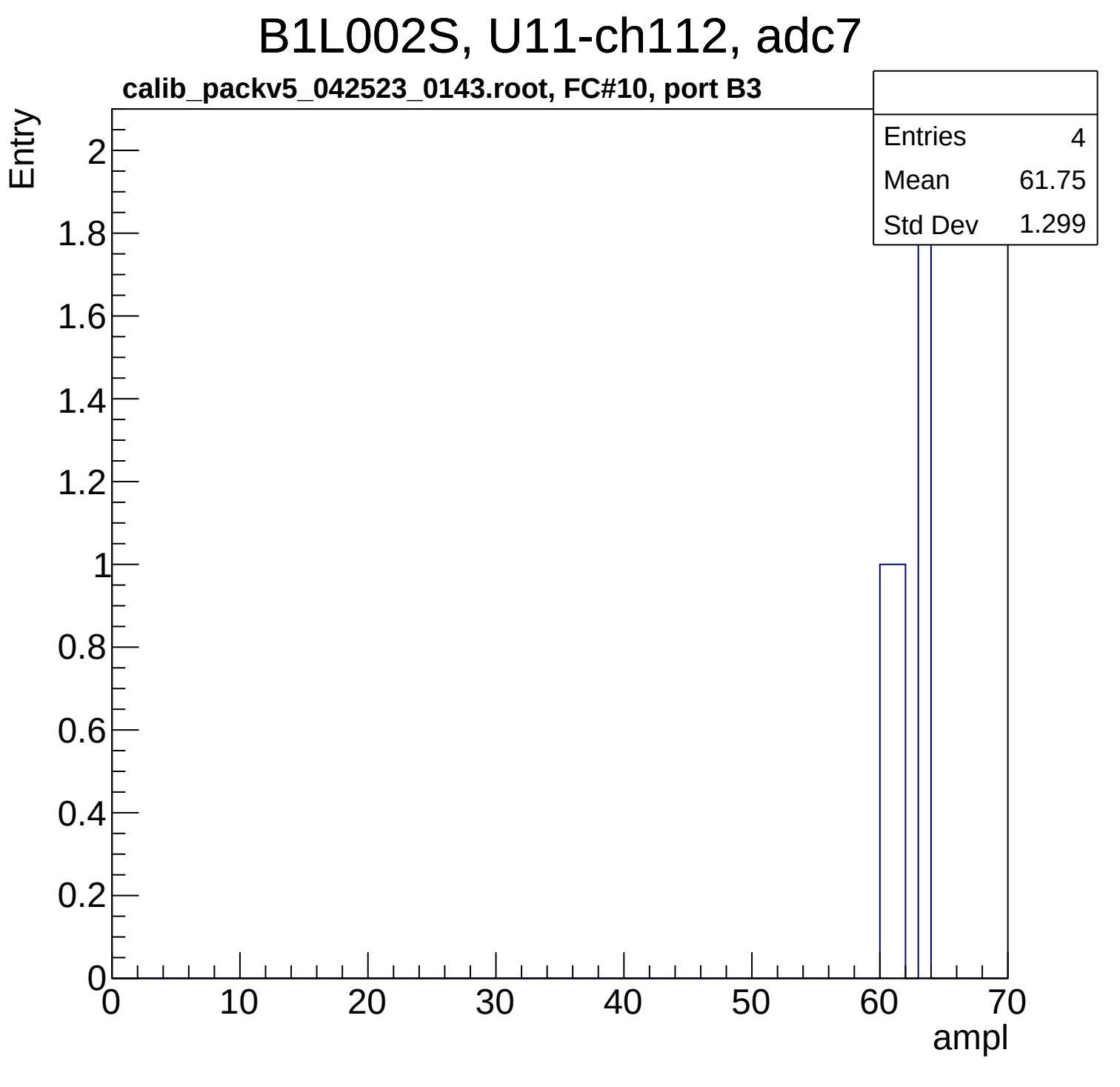
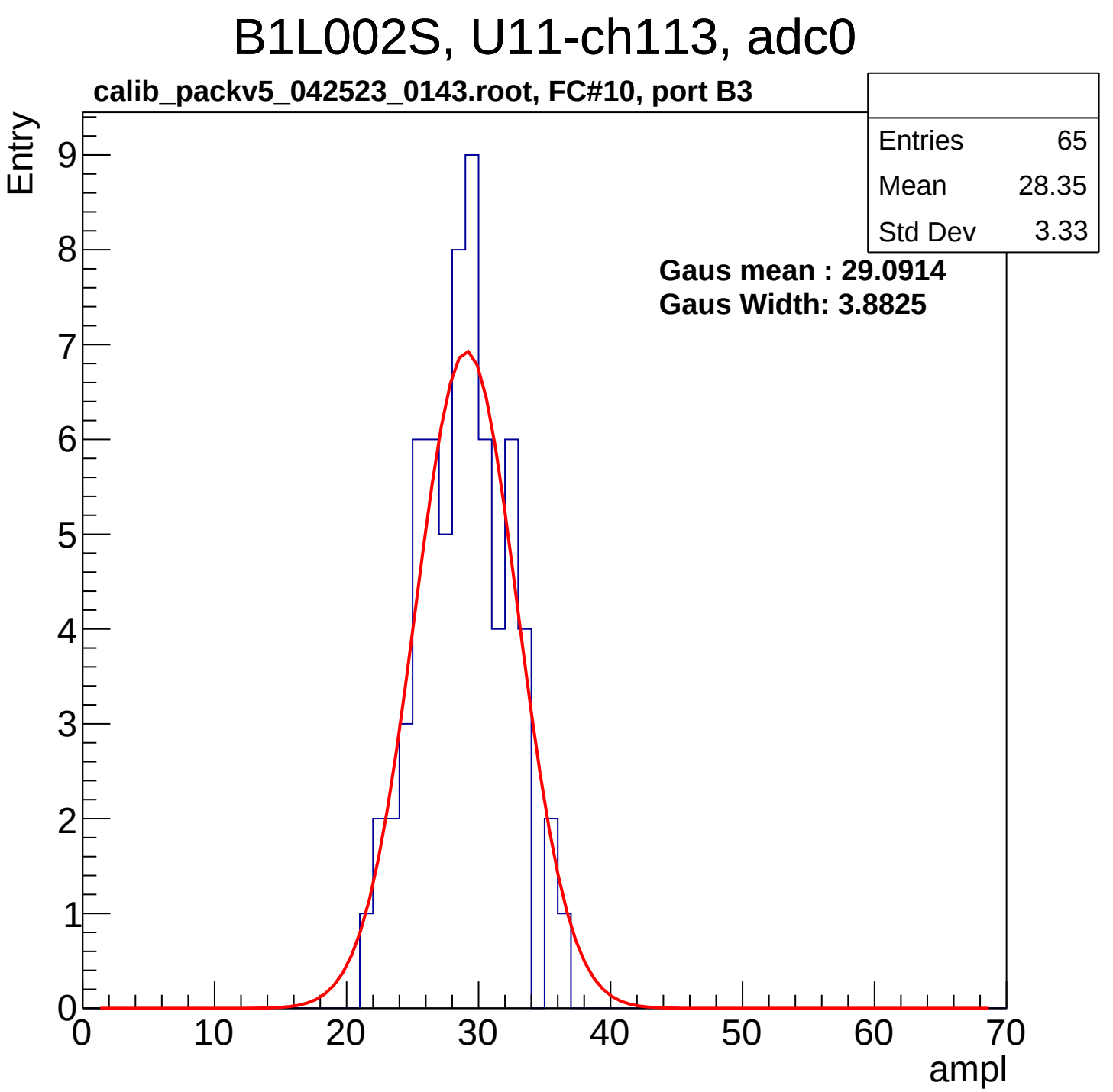
40
50
60
70

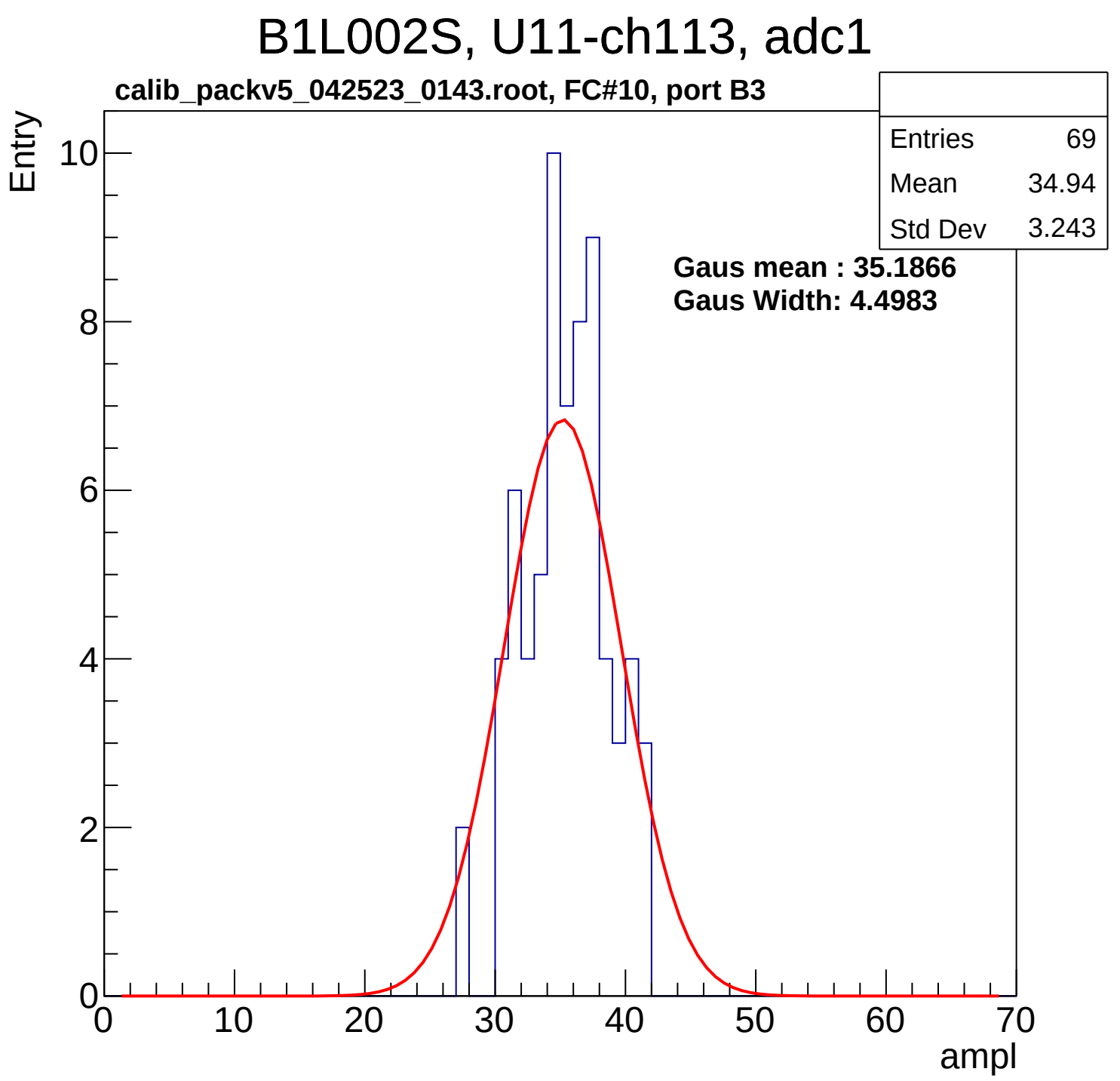

B1L002S, U11-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 52.85
Std Dev 3.53
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

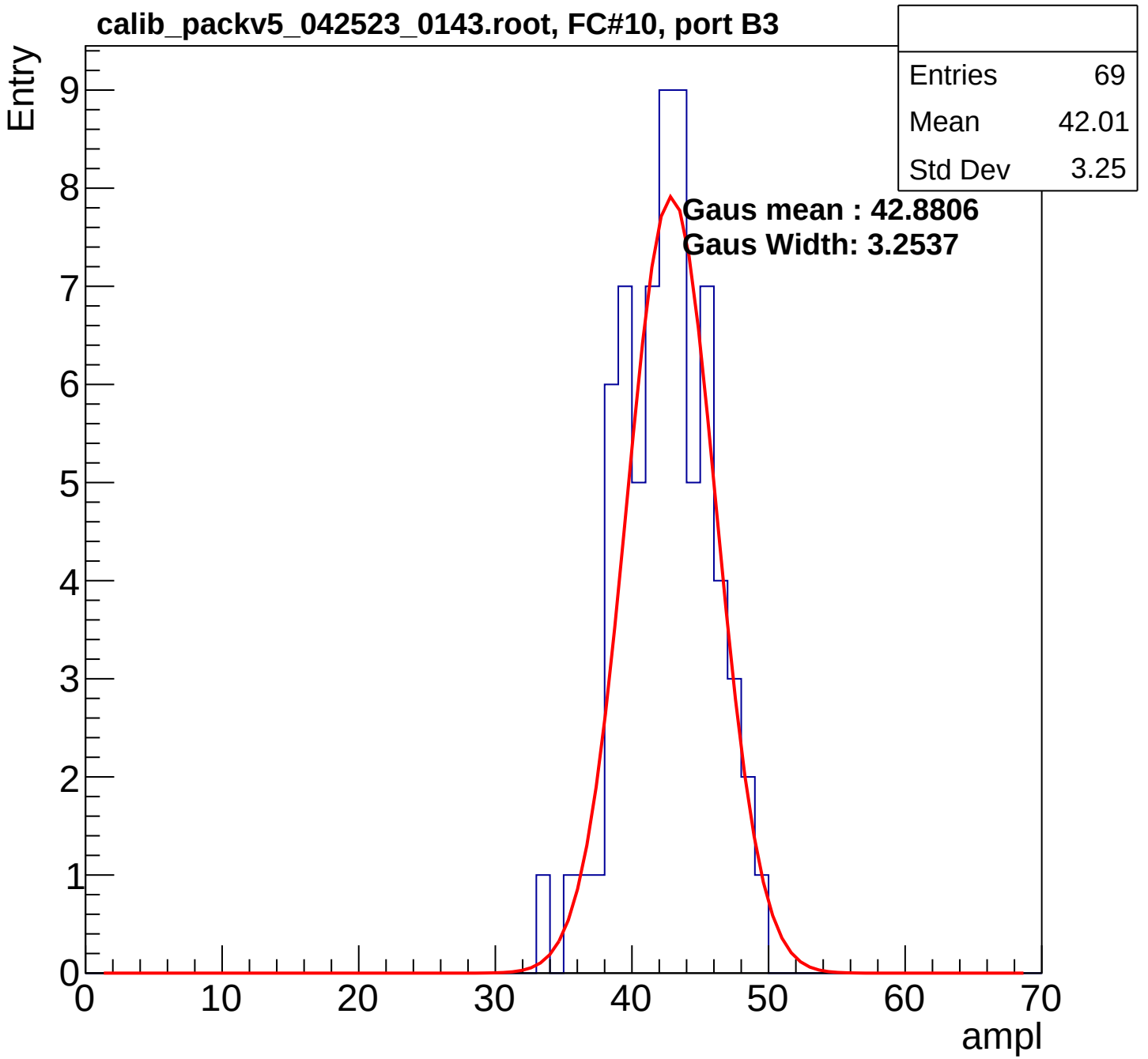

B1L002S, U11-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.4
Std Dev 8.441
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

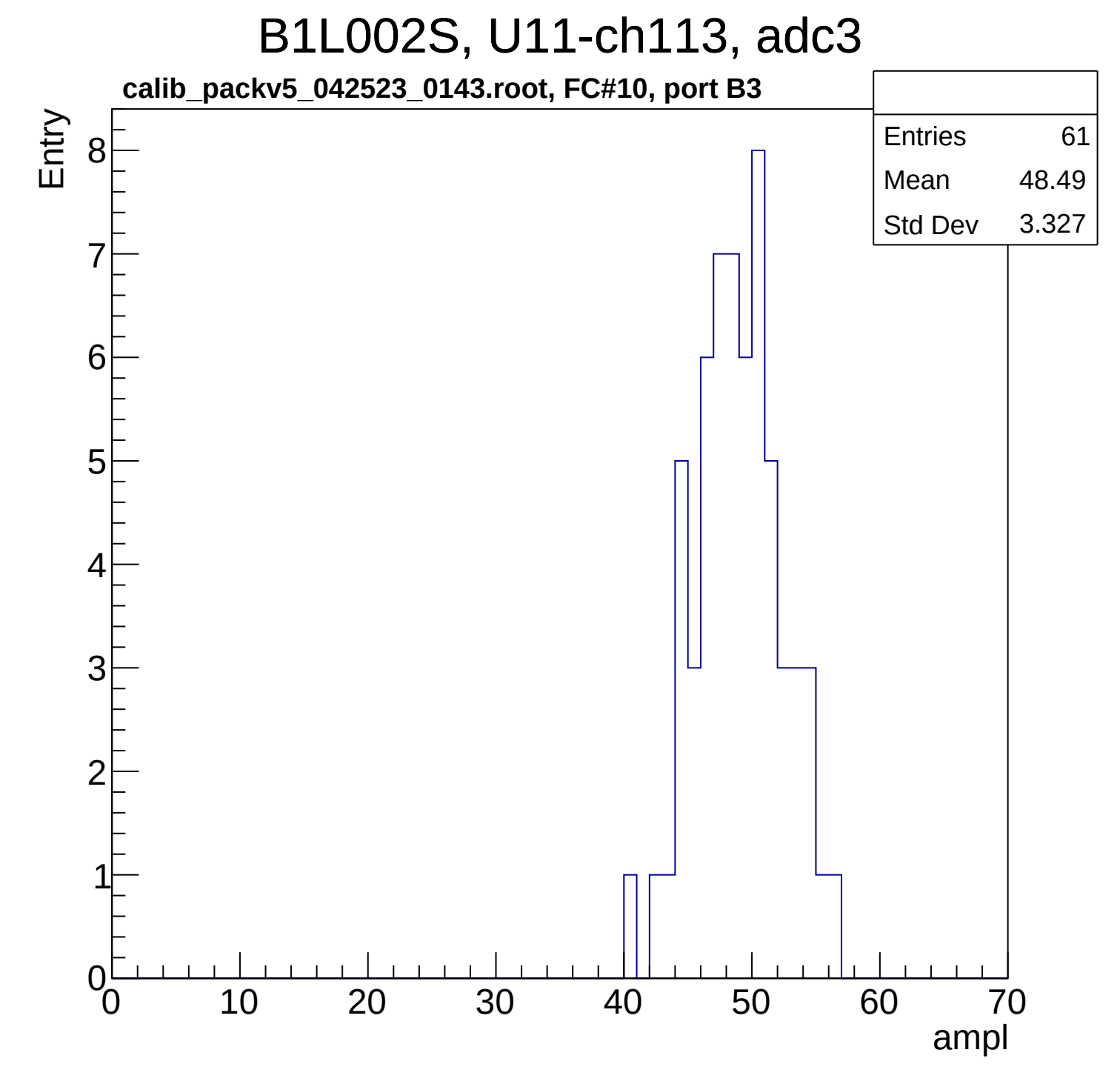

B1L002S, U11-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.06
Std Dev 1.798
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

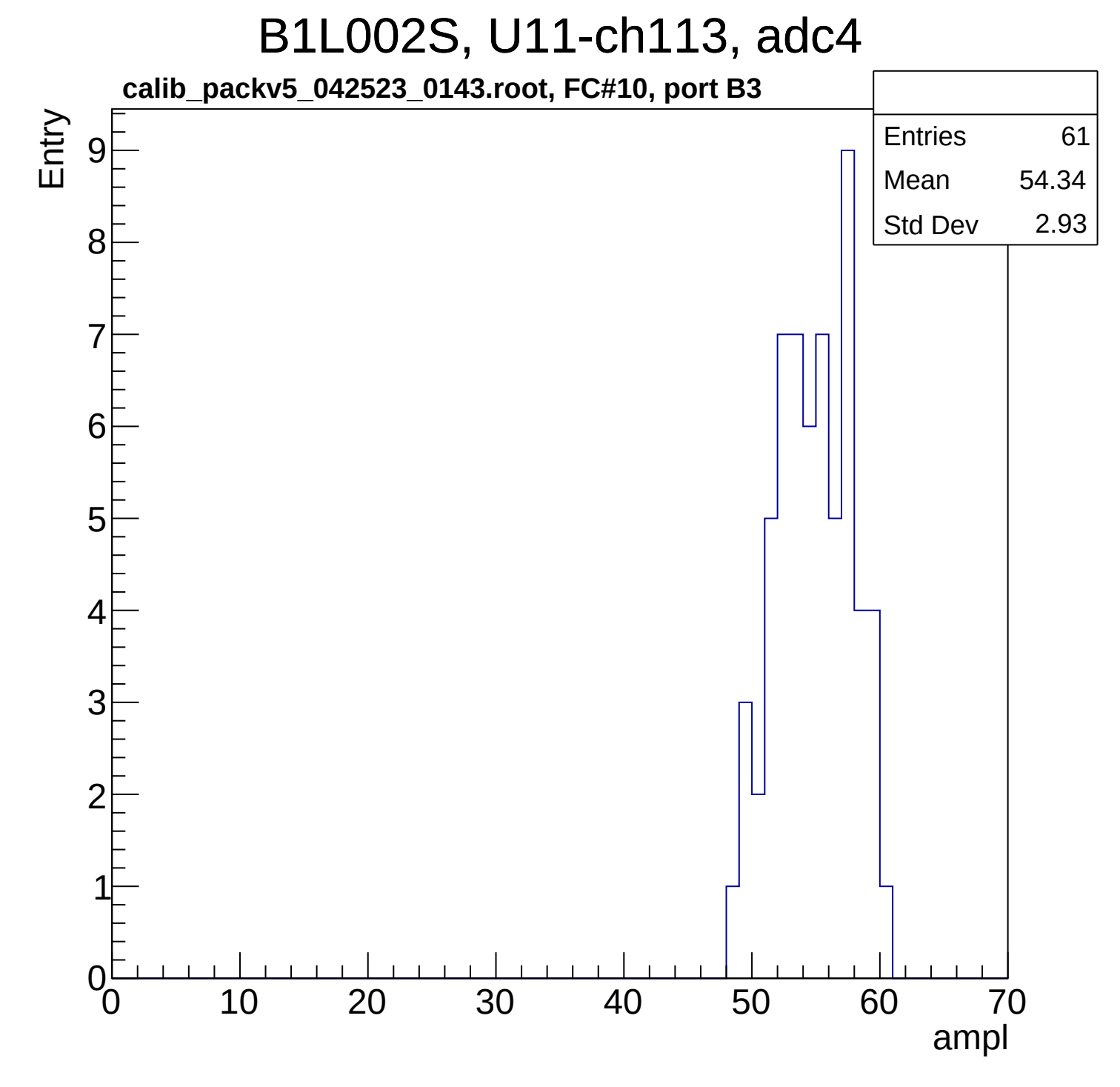

B1L002S, U11-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

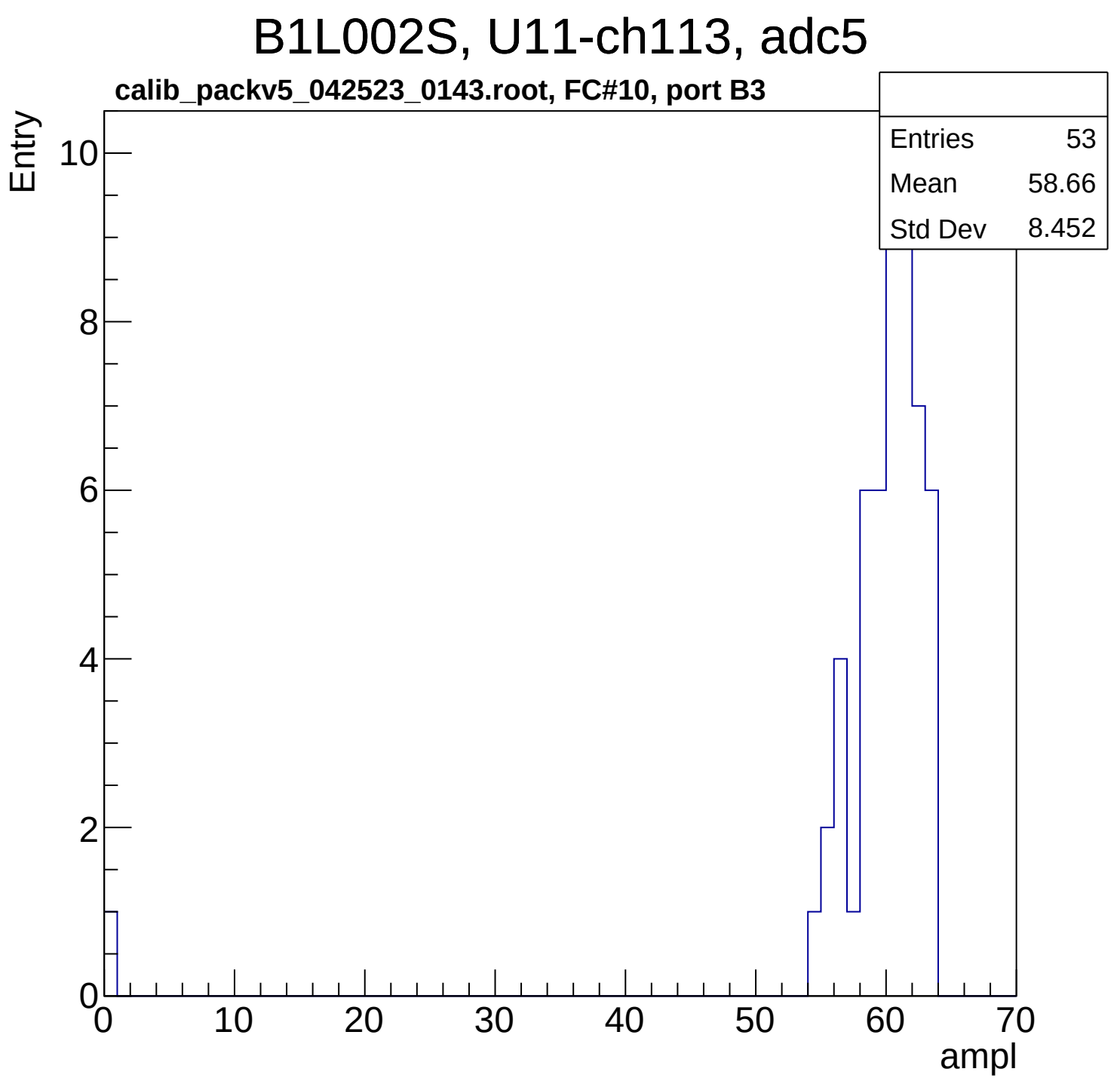

B1L002S, U11-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.35
Std Dev 3.33
Gaus mean : 29.0914
Gaus Width: 3.8825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

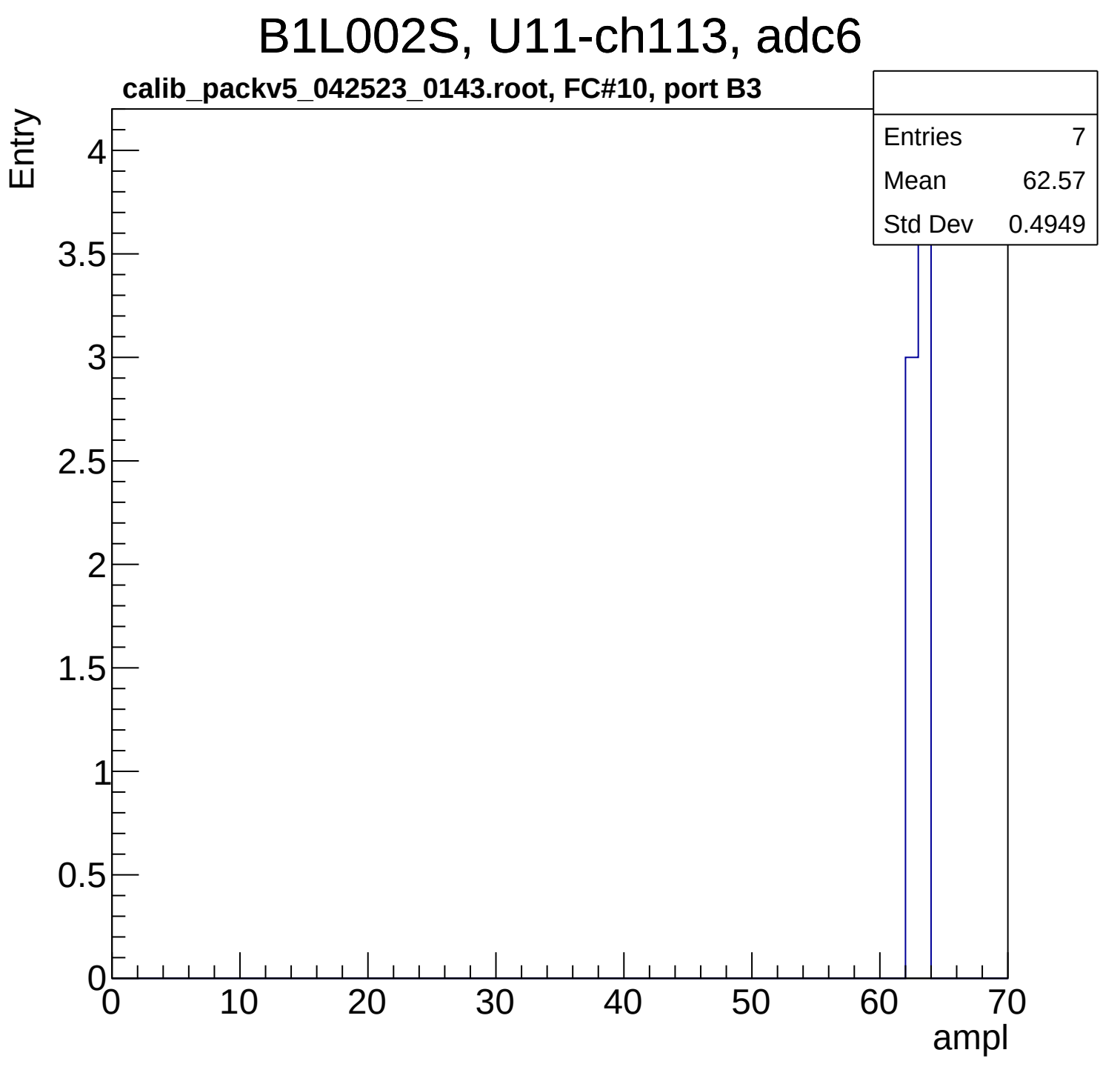

B1L002S, U11-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.94
Std Dev 3.243
Gaus mean : 35.1866
Gaus Width: 4.4983
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

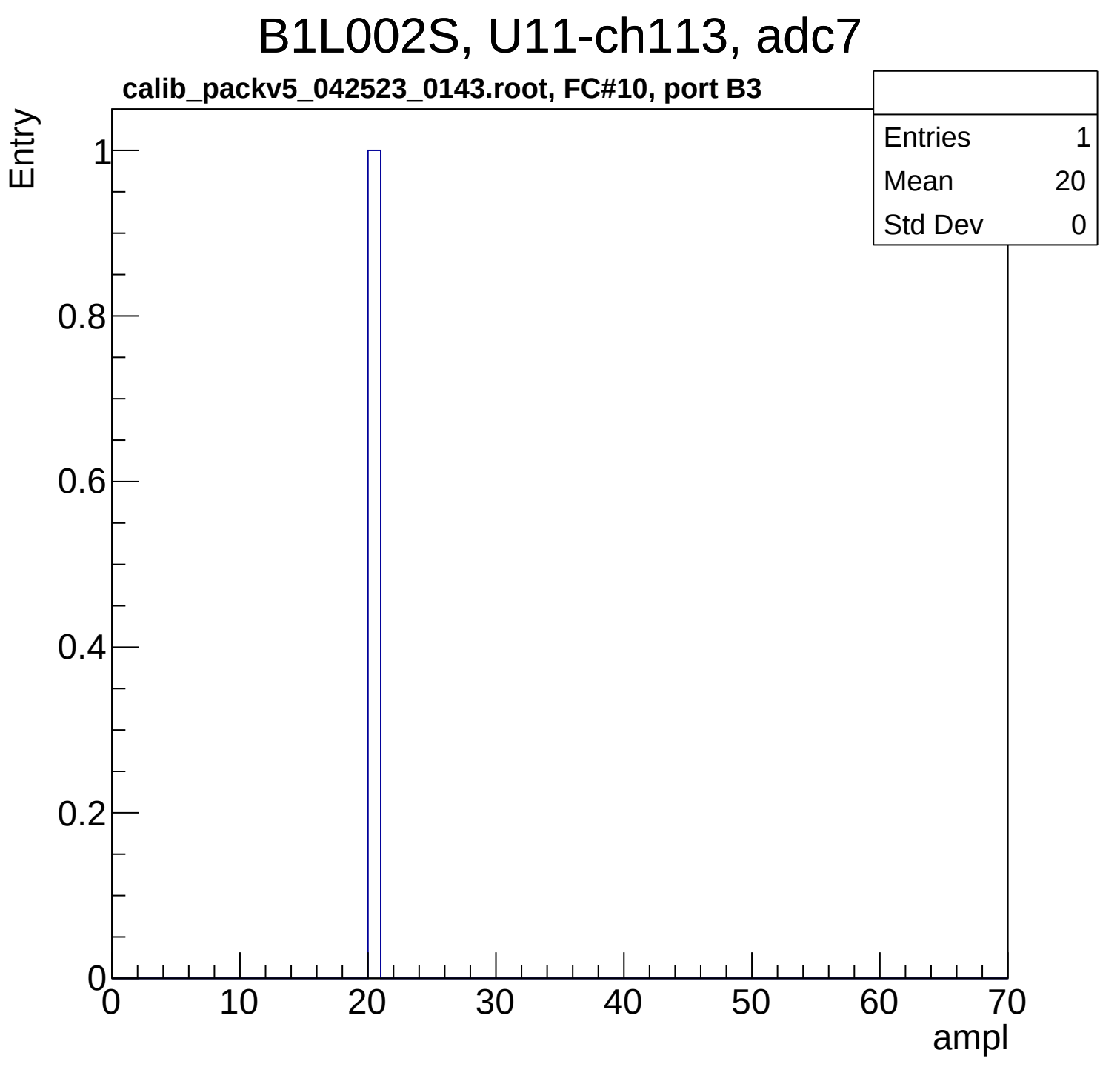

B1L002S, U11-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 42.01
Std Dev 3.25
Gaus mean : 42.8806
Gaus Width: 3.2537
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

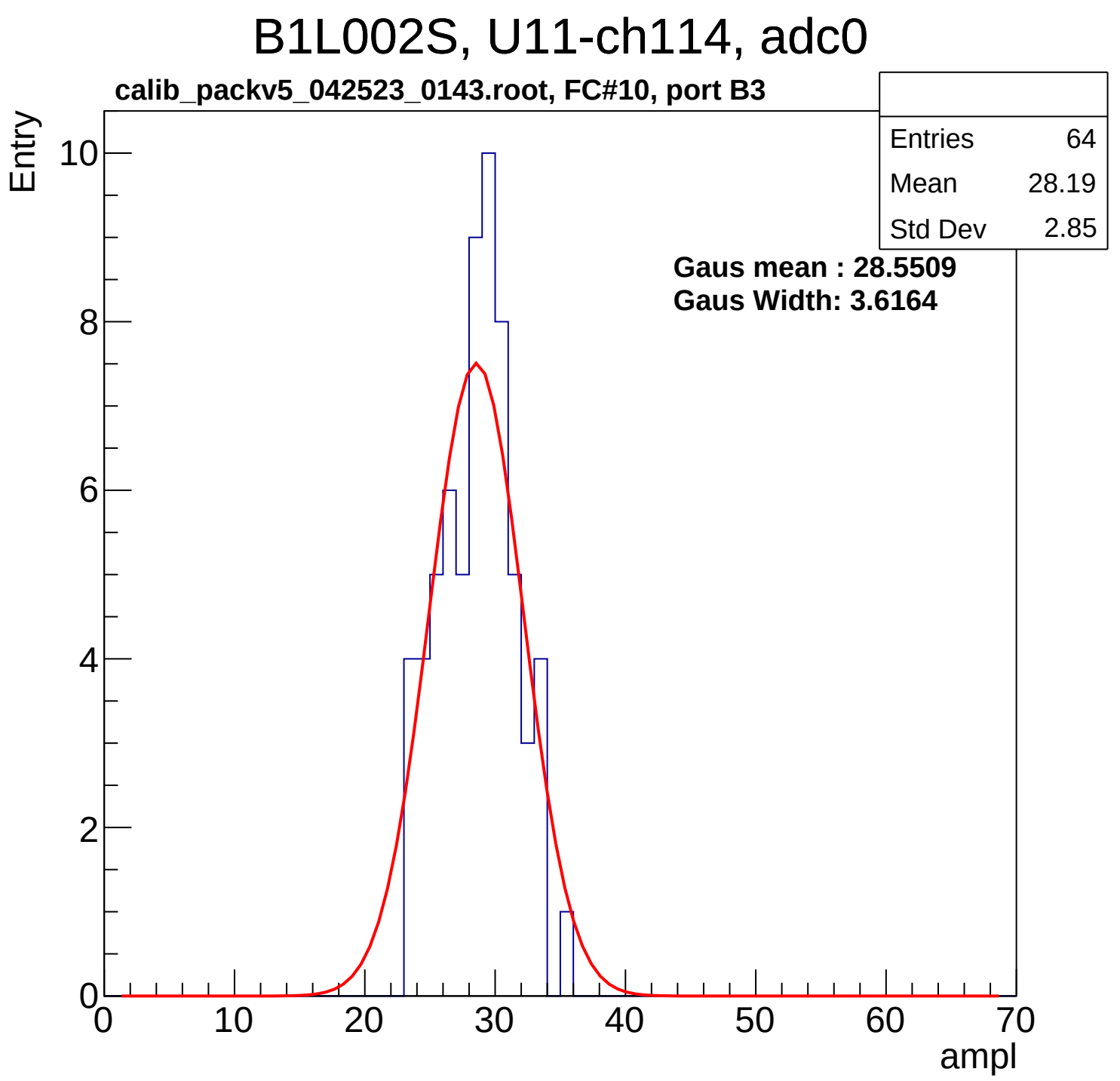

B1L002S, U11-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.49
Std Dev 3.327
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

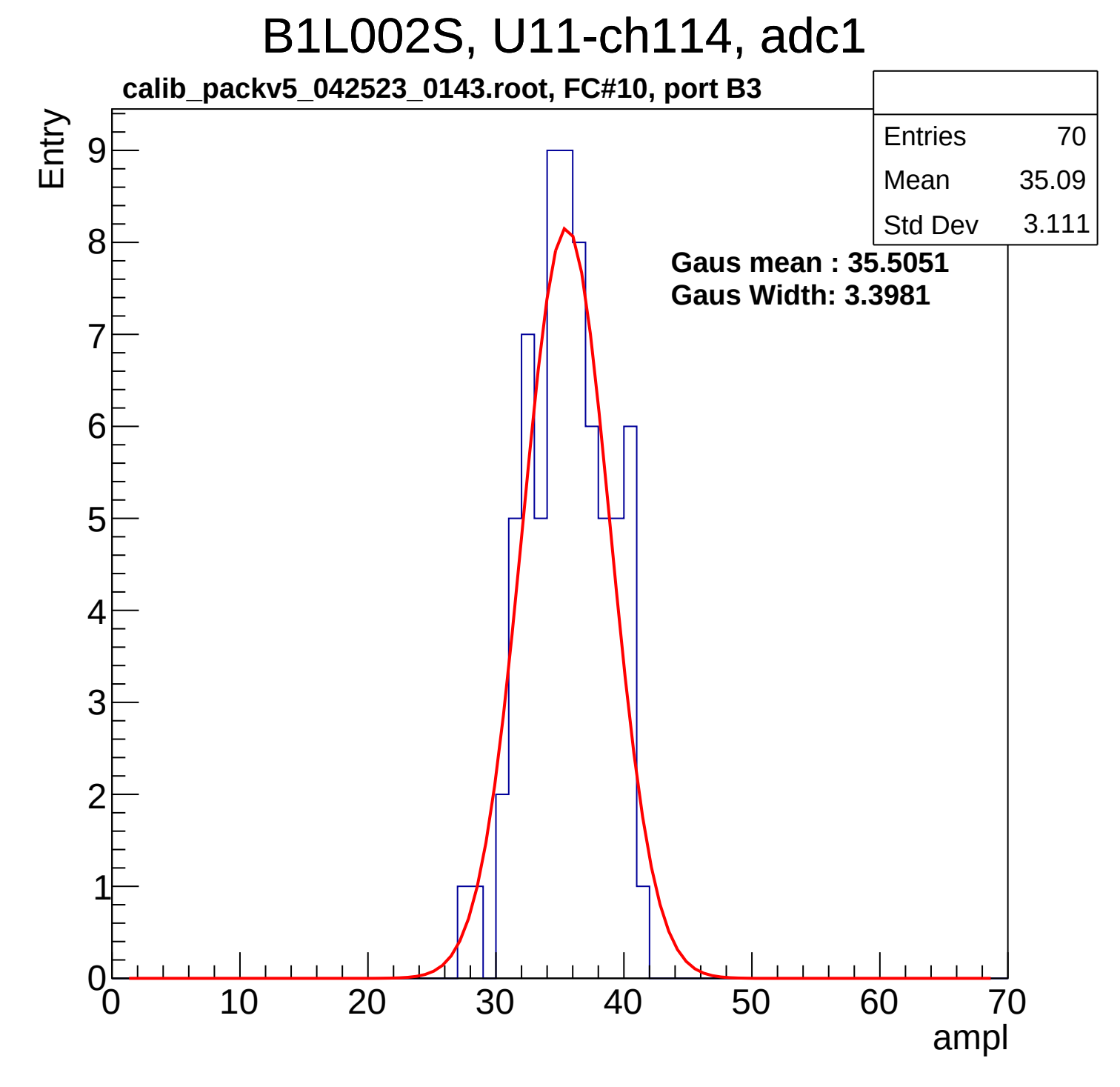

B1L002S, U11-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.34
Std Dev 2.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

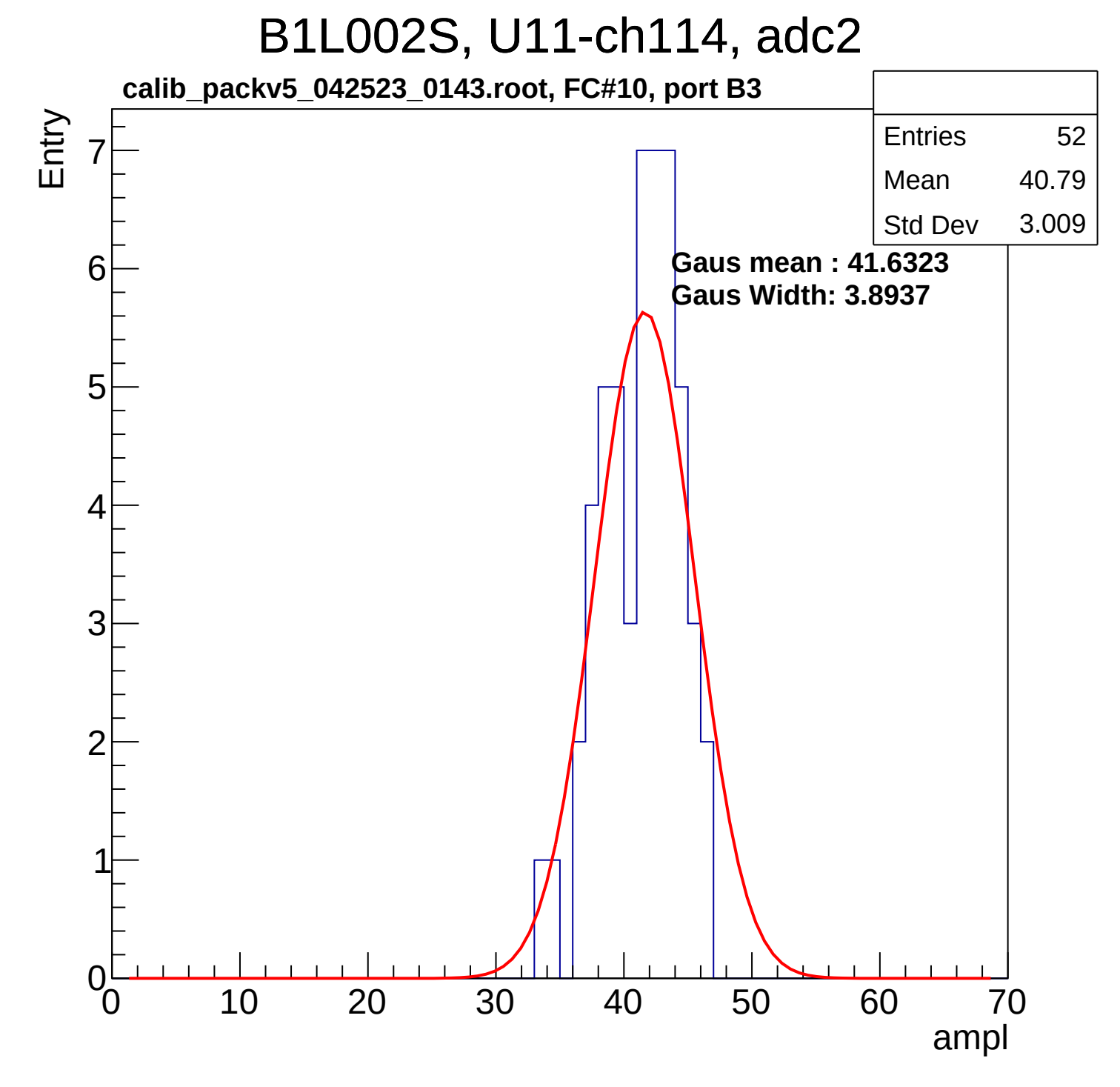

B1L002S, U11-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.66
Std Dev 8.452
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

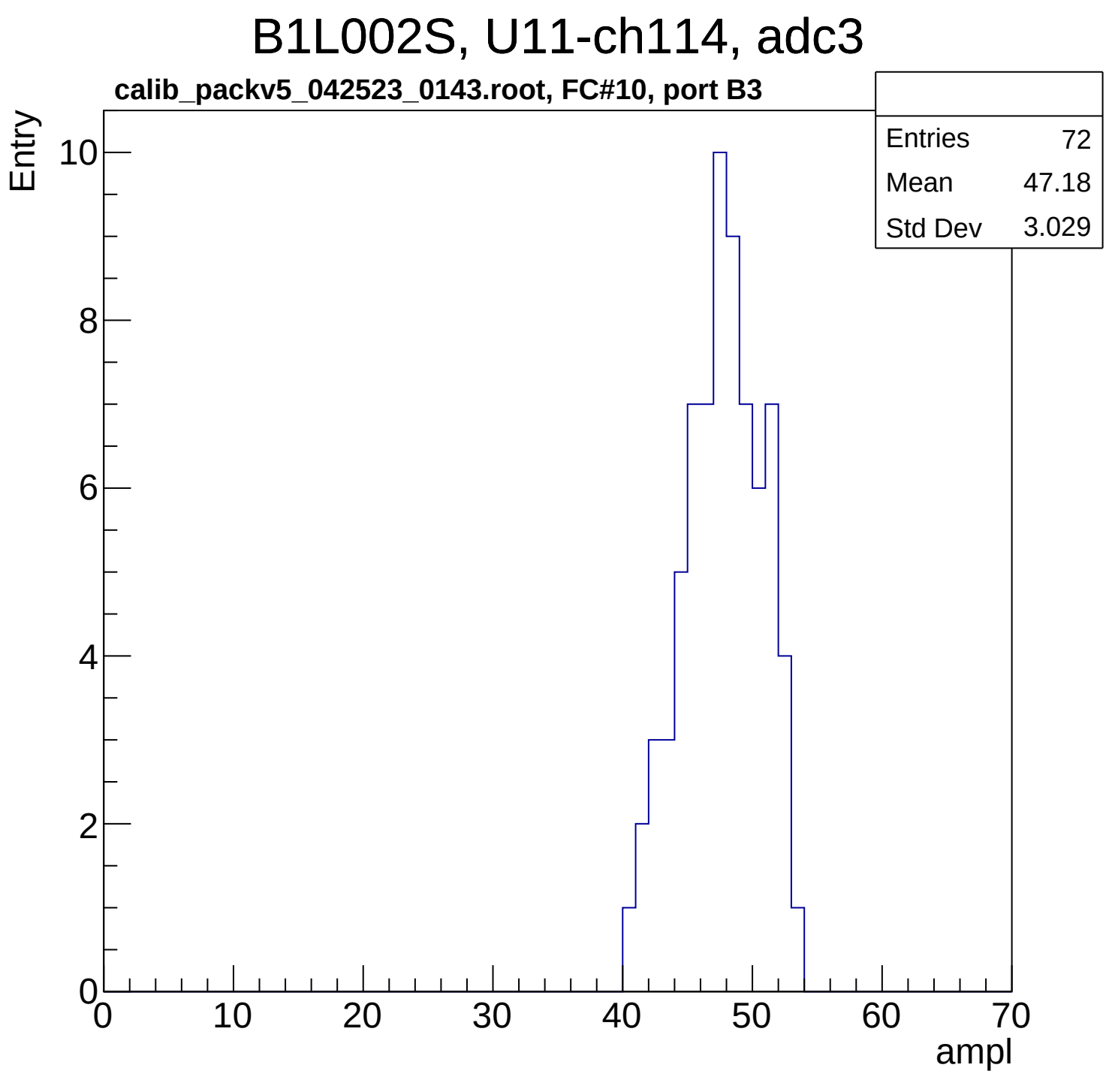

B1L002S, U11-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

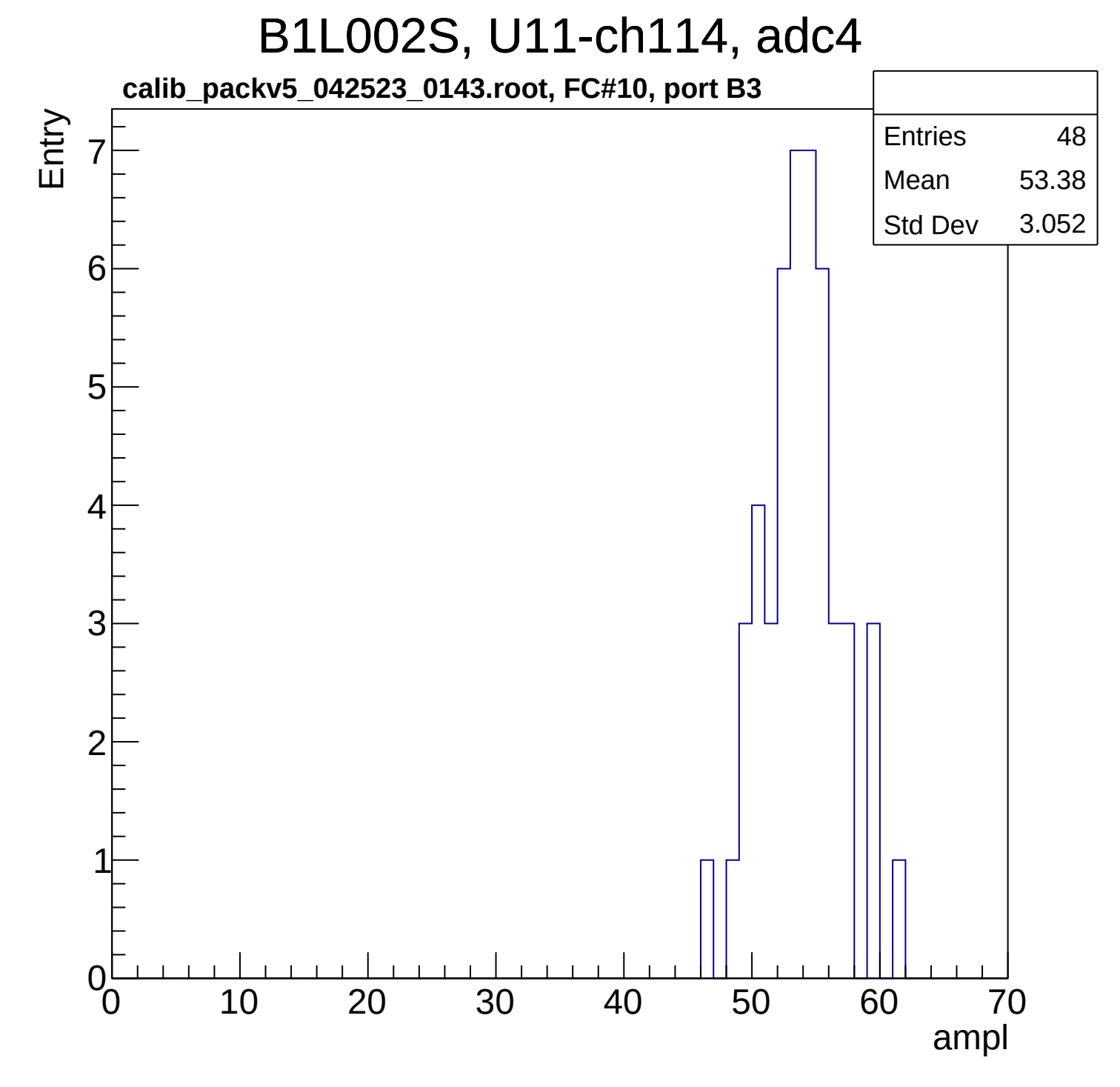

B1L002S, U11-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

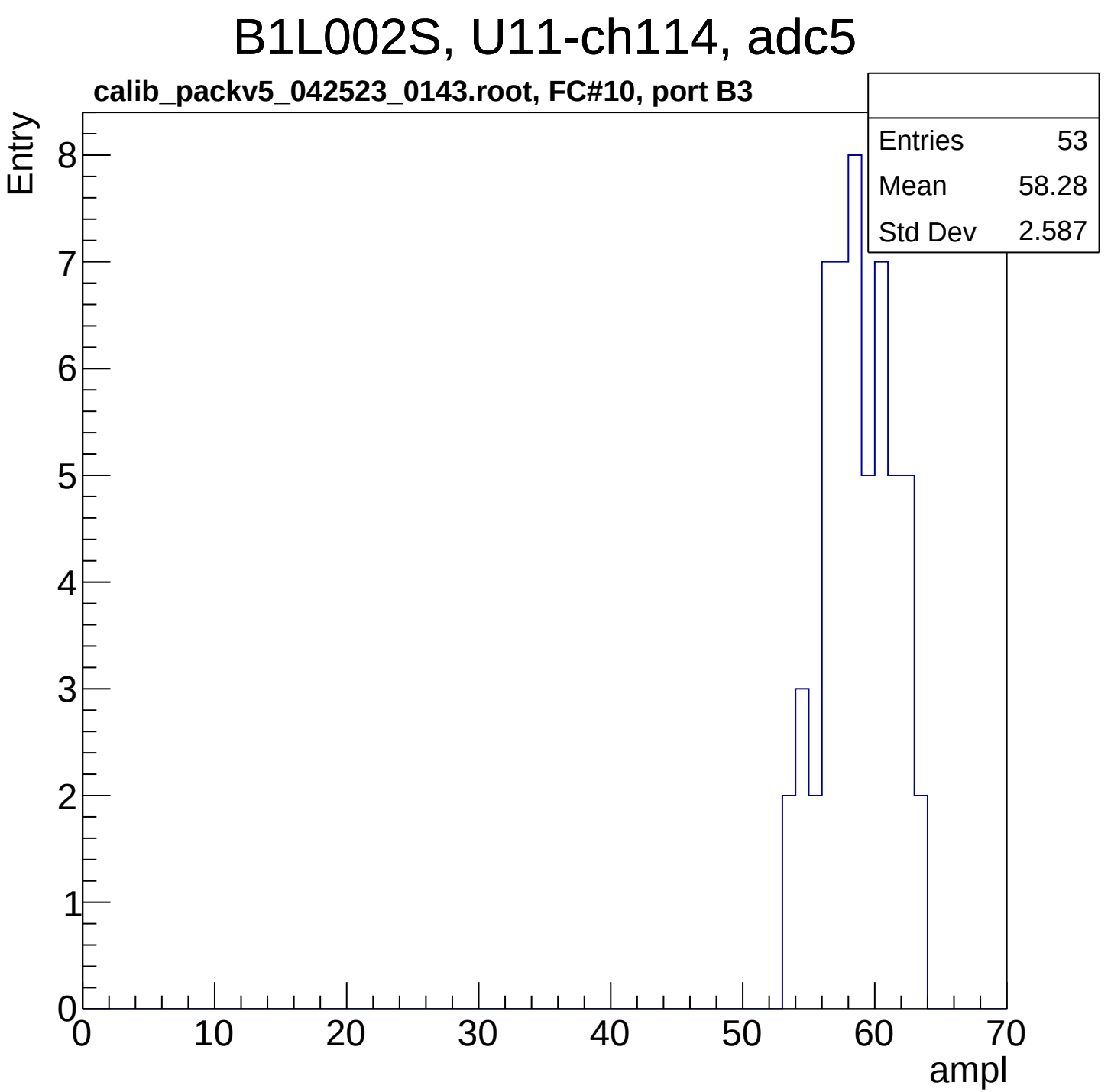

B1L002S, U11-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.19
Std Dev 2.85
Gaus mean : 28.5509
Gaus Width: 3.6164
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

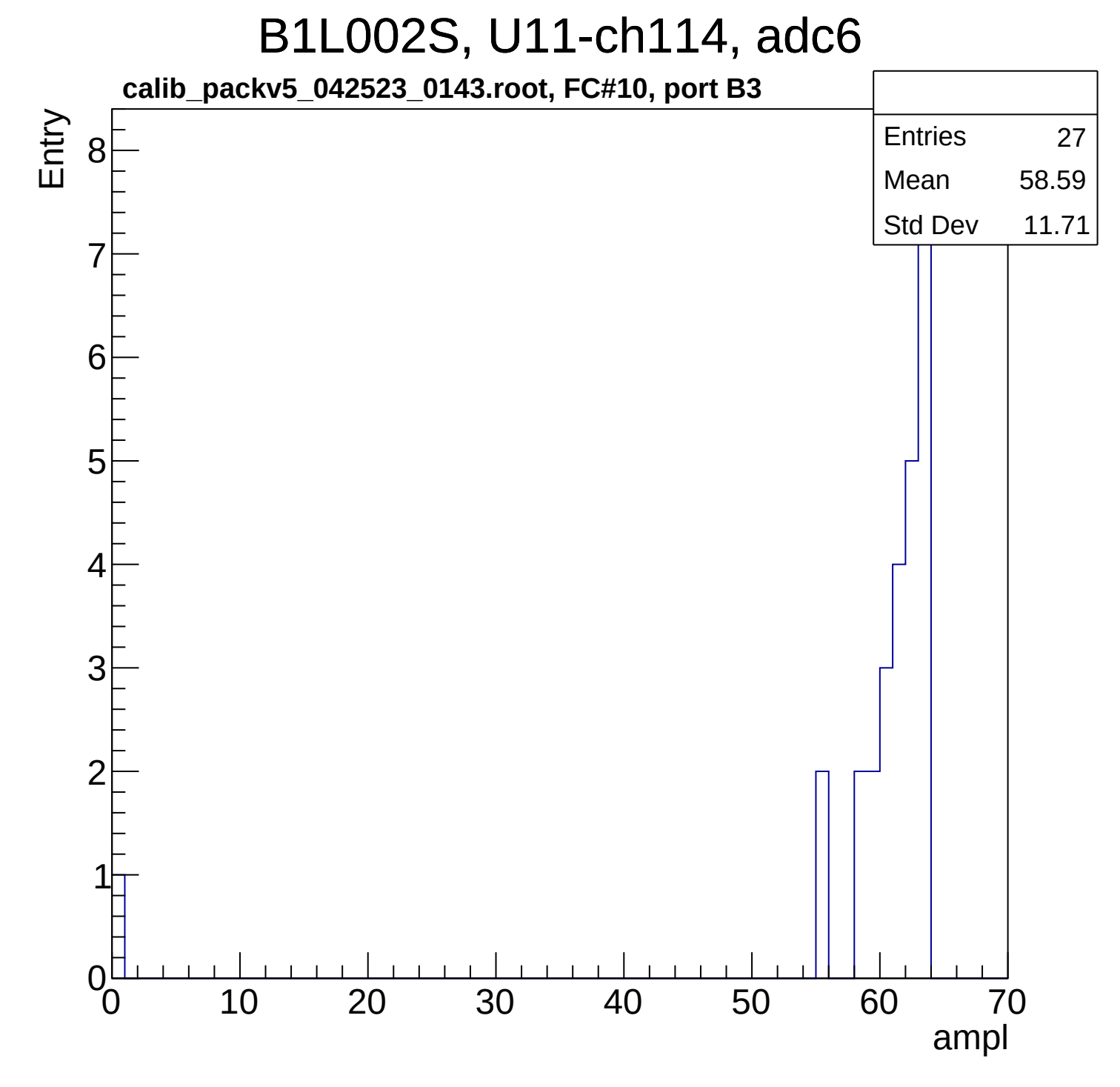

B1L002S, U11-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.09
Std Dev 3.111
Gaus mean : 35.5051
Gaus Width: 3.3981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.79
Std Dev 3.009
Gaus mean : 41.6323
Gaus Width: 3.8937
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 47.18
Std Dev 3.029
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.38
Std Dev 3.052
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.28
Std Dev 2.587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.59
Std Dev 11.71
Entry
ampl
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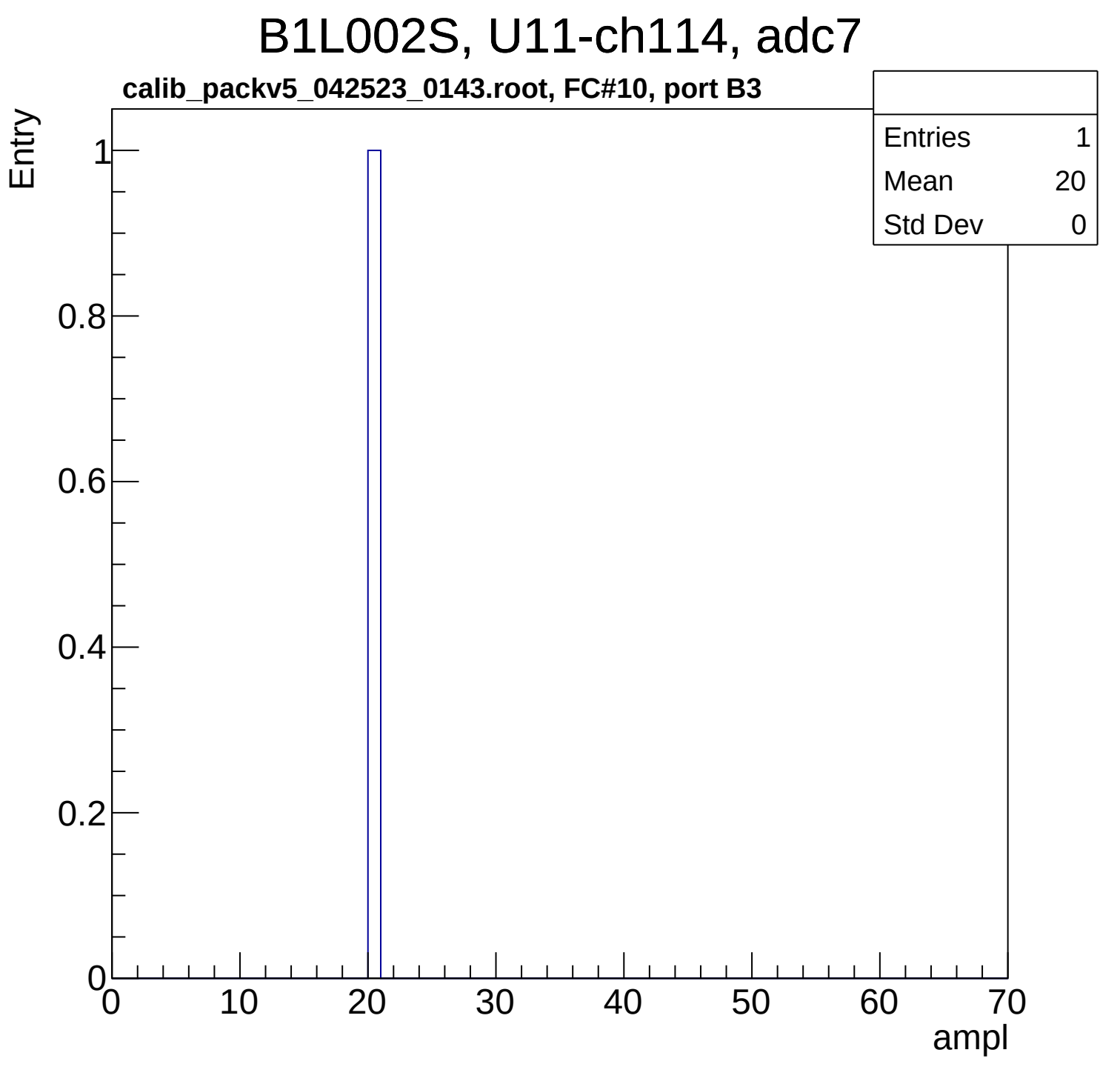
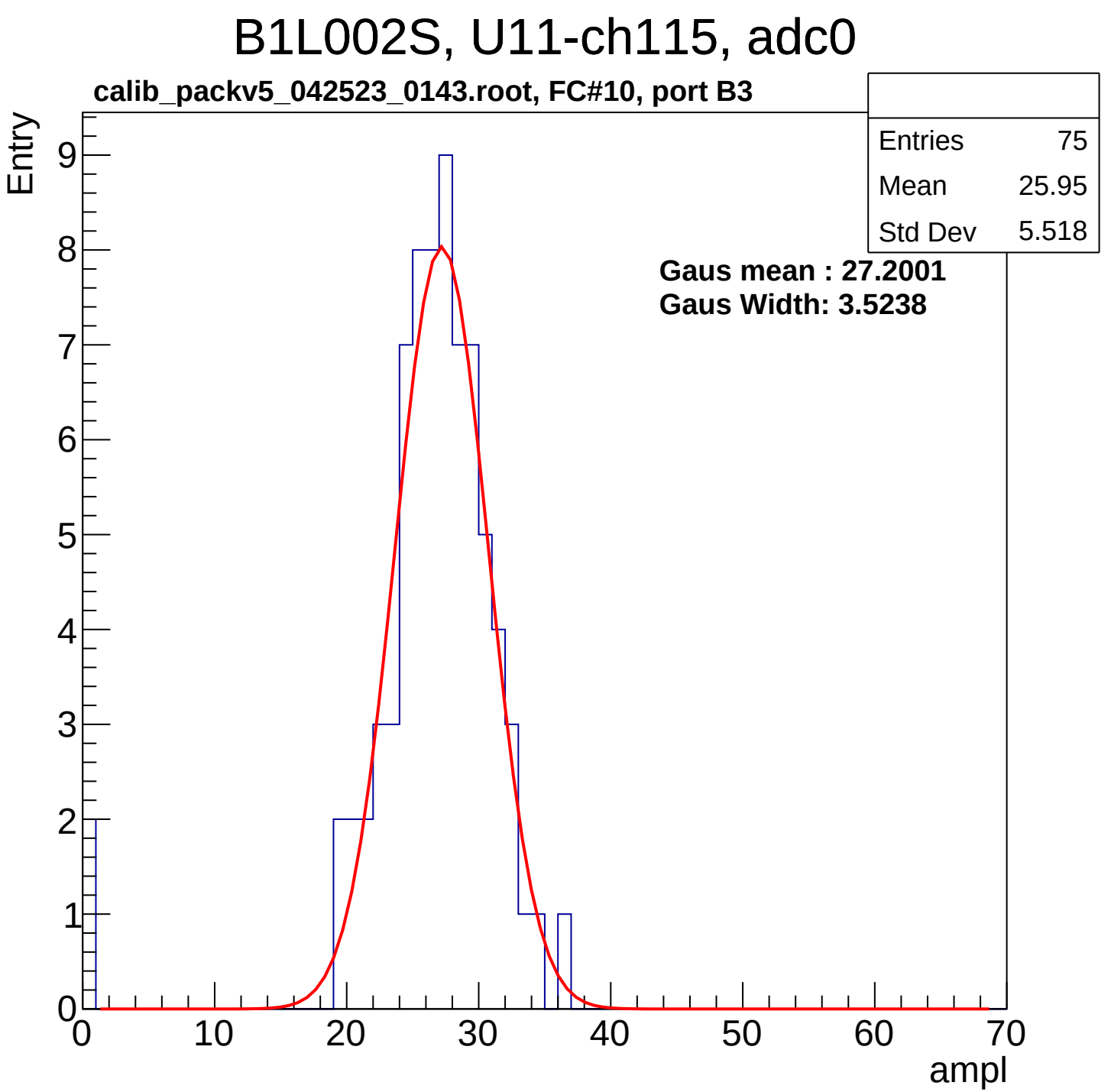
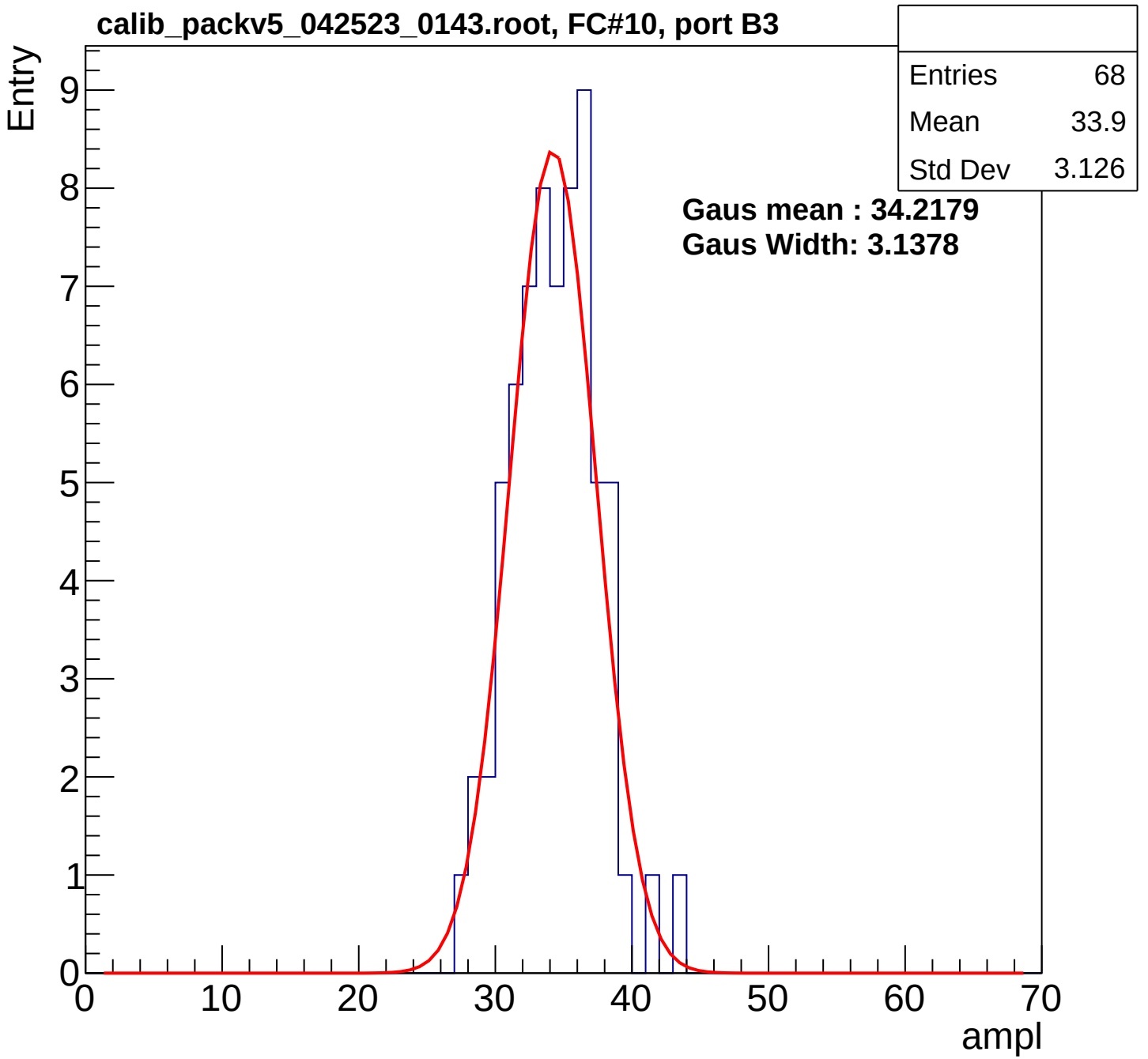
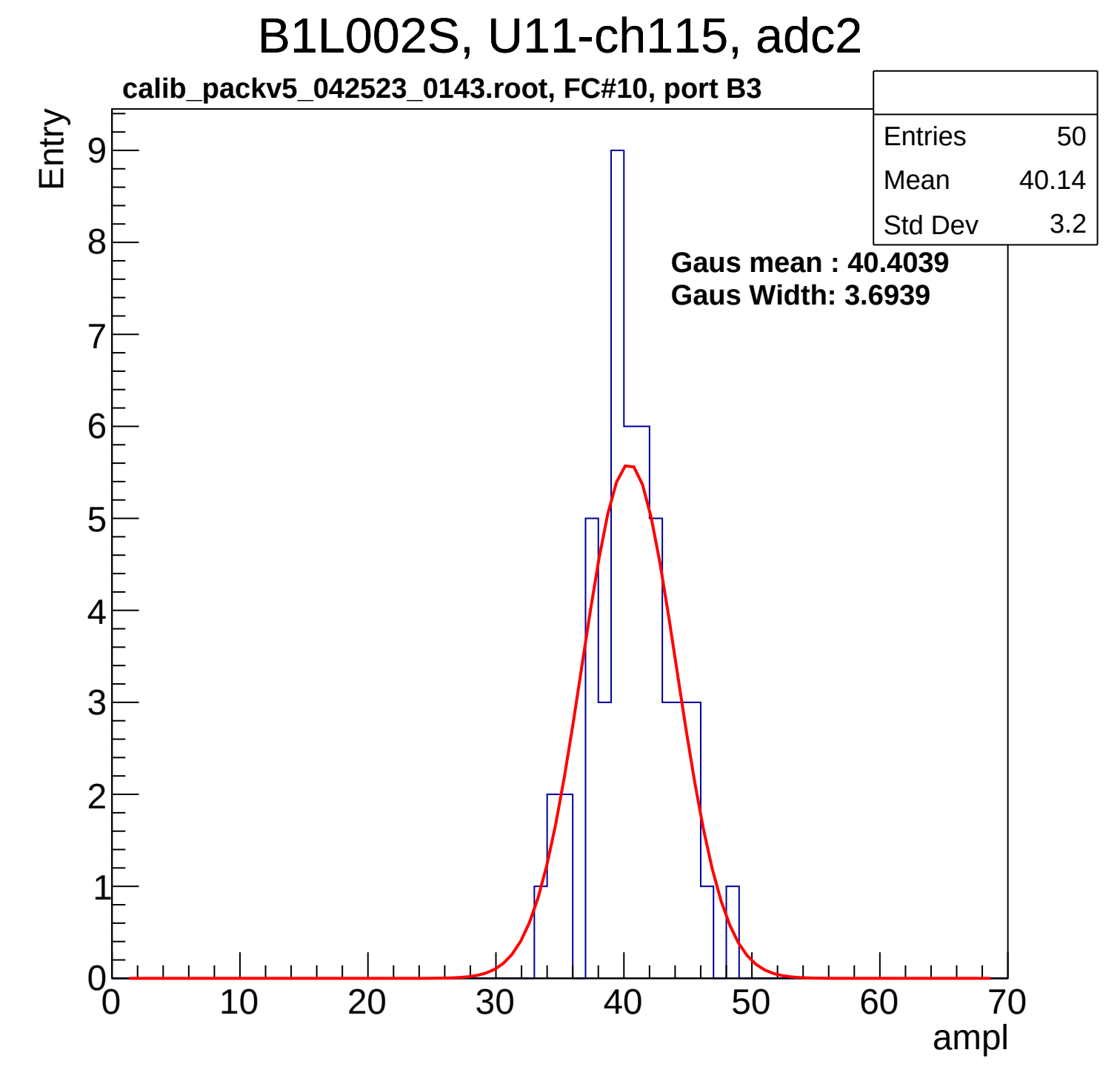
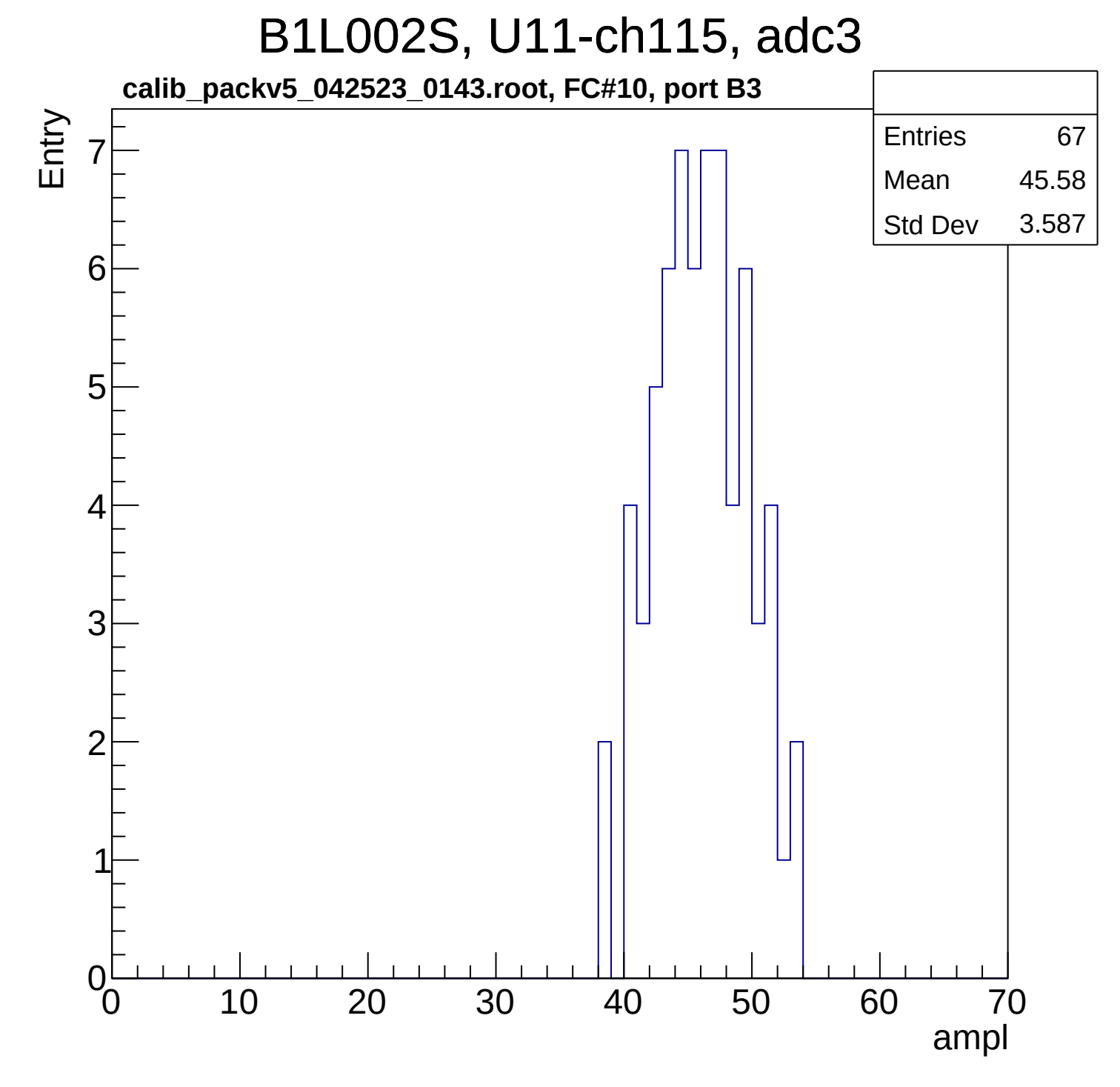
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

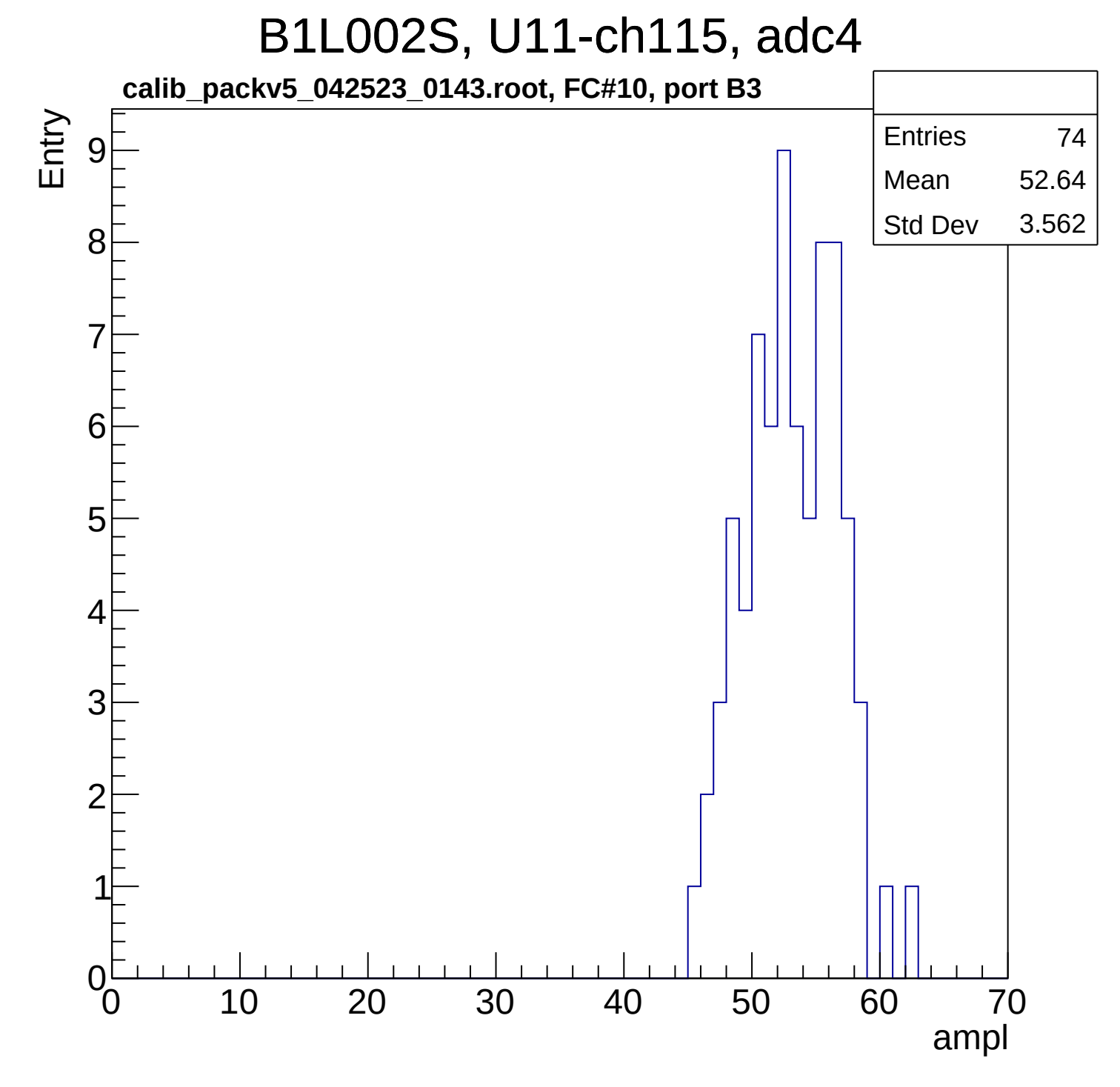

B1L002S, U11-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

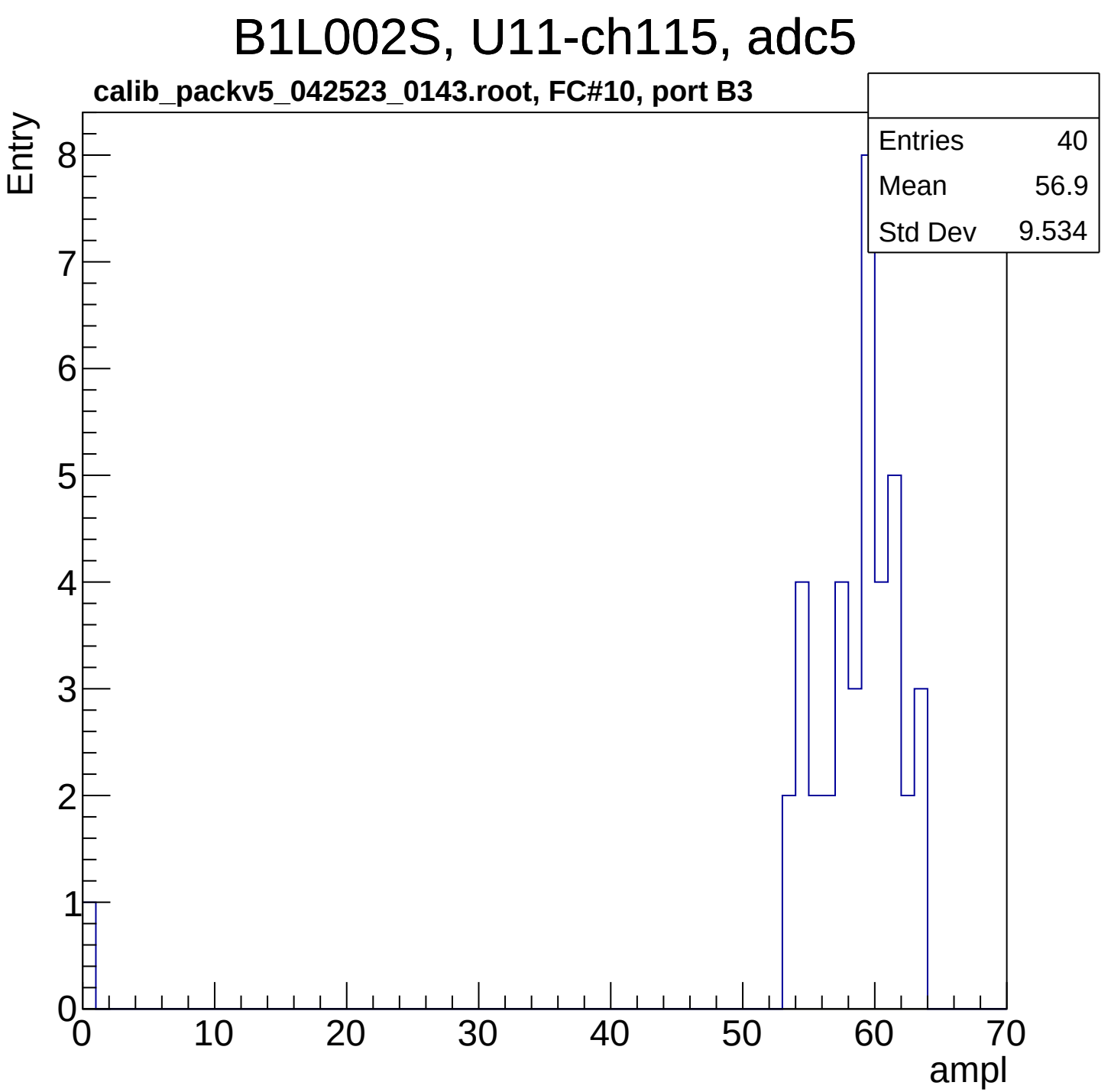

B1L002S, U11-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 25.95
Std Dev 5.518
Gaus mean : 27.2001
Gaus Width: 3.5238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

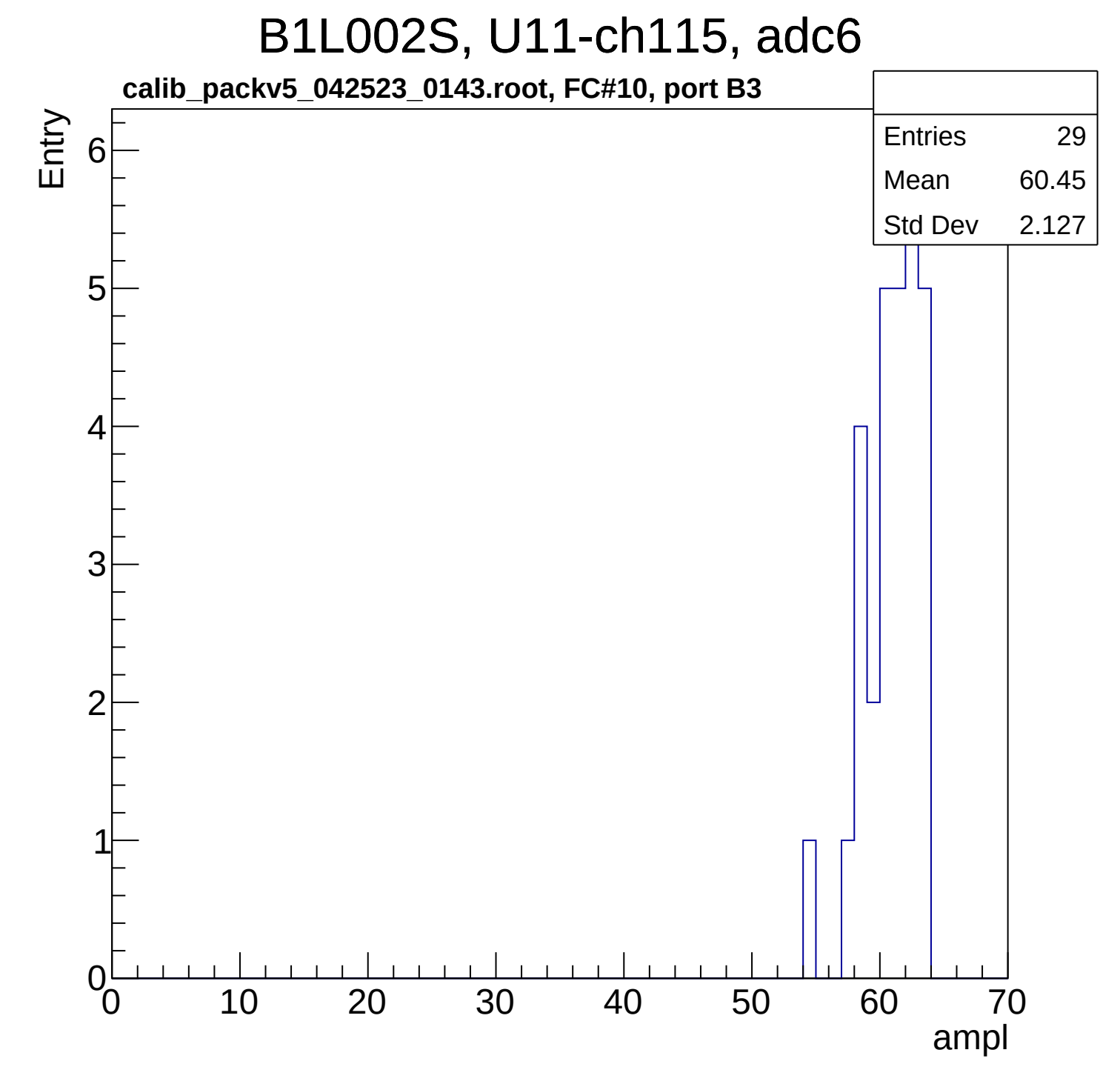

B1L002S, U11-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.9
Std Dev 3.126
Gaus mean : 34.2179
Gaus Width: 3.1378
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

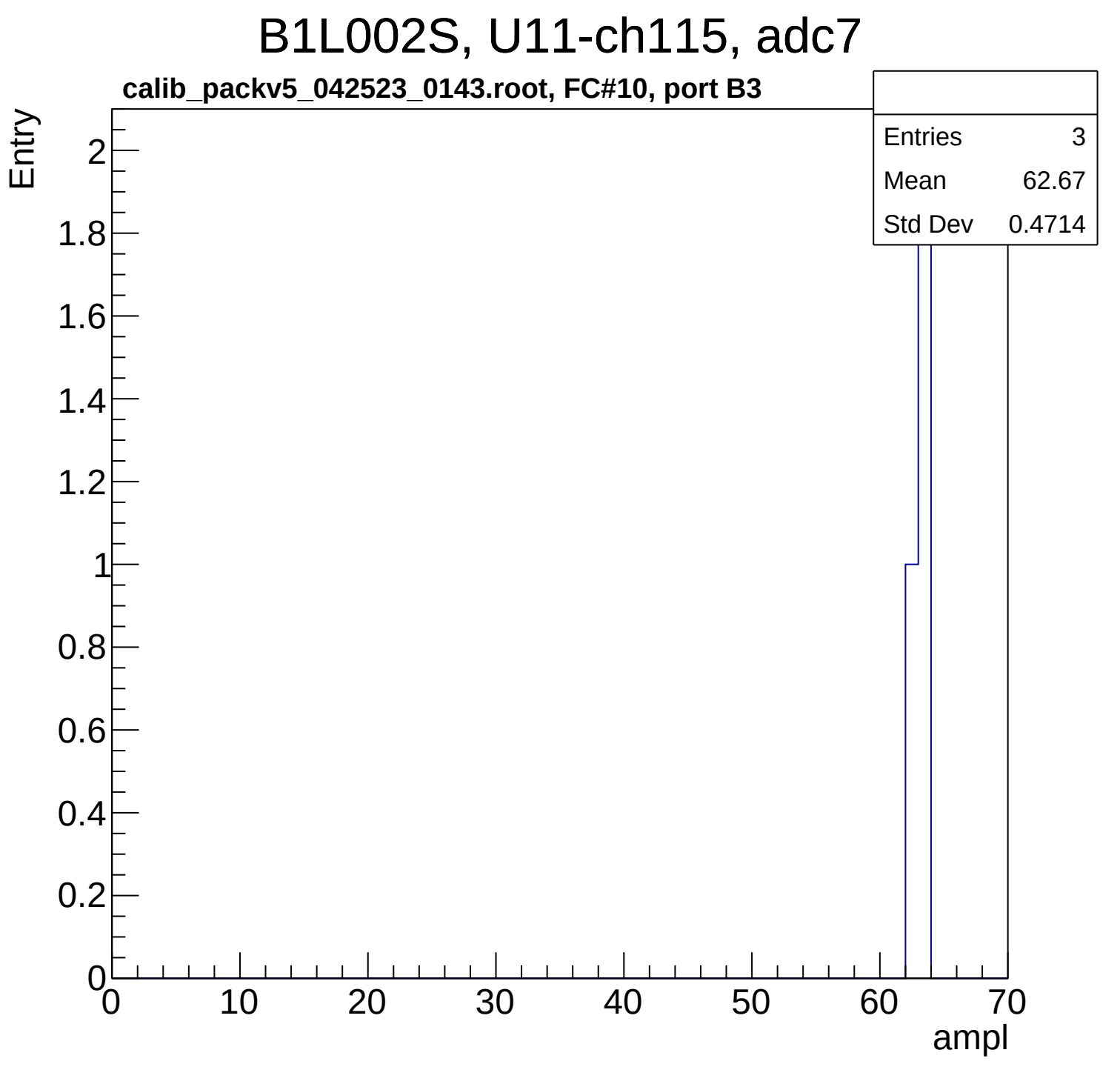

B1L002S, U11-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.14
Std Dev 3.2
Gaus mean : 40.4039
Gaus Width: 3.6939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

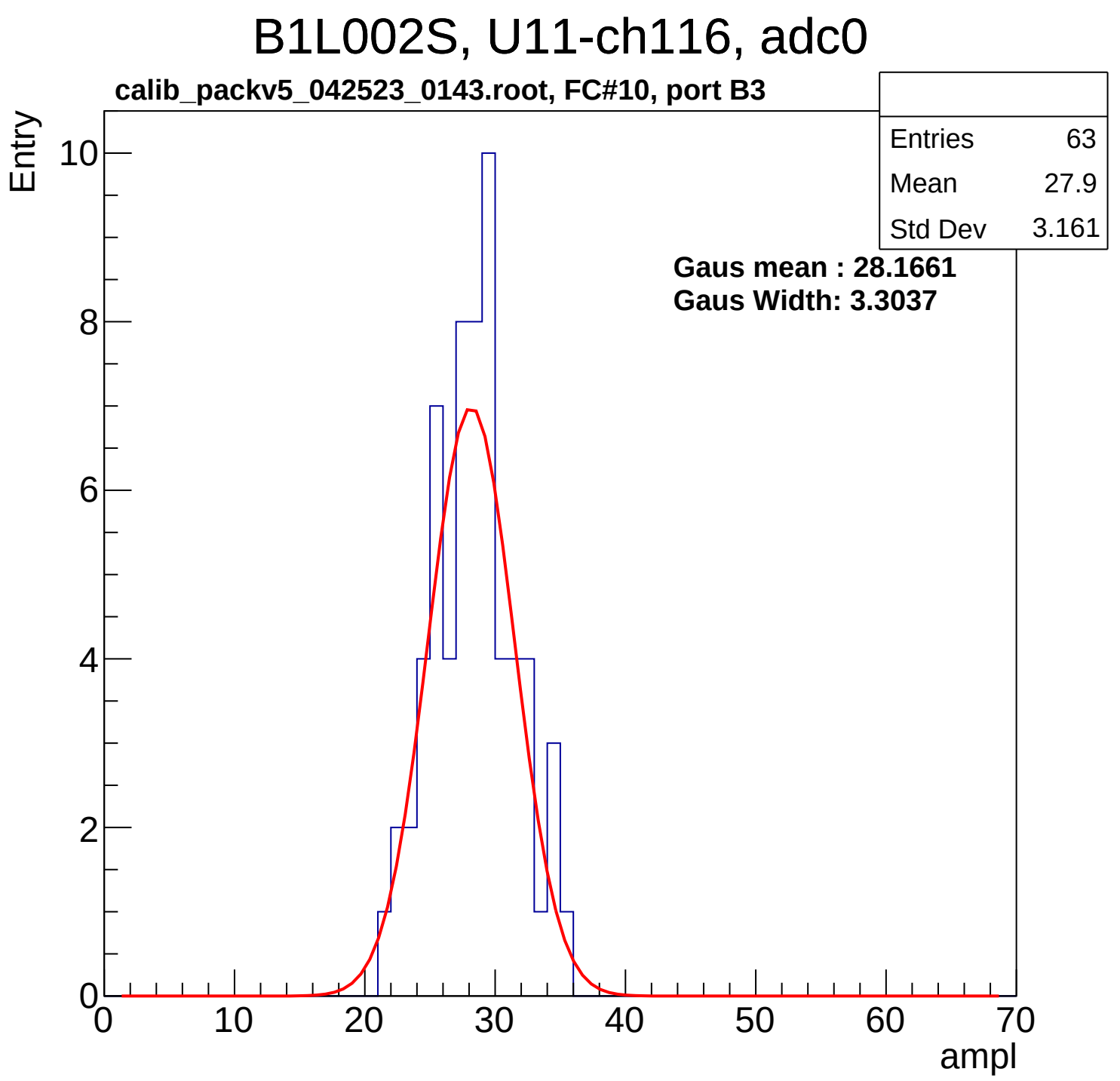

B1L002S, U11-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 45.58
Std Dev 3.587
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

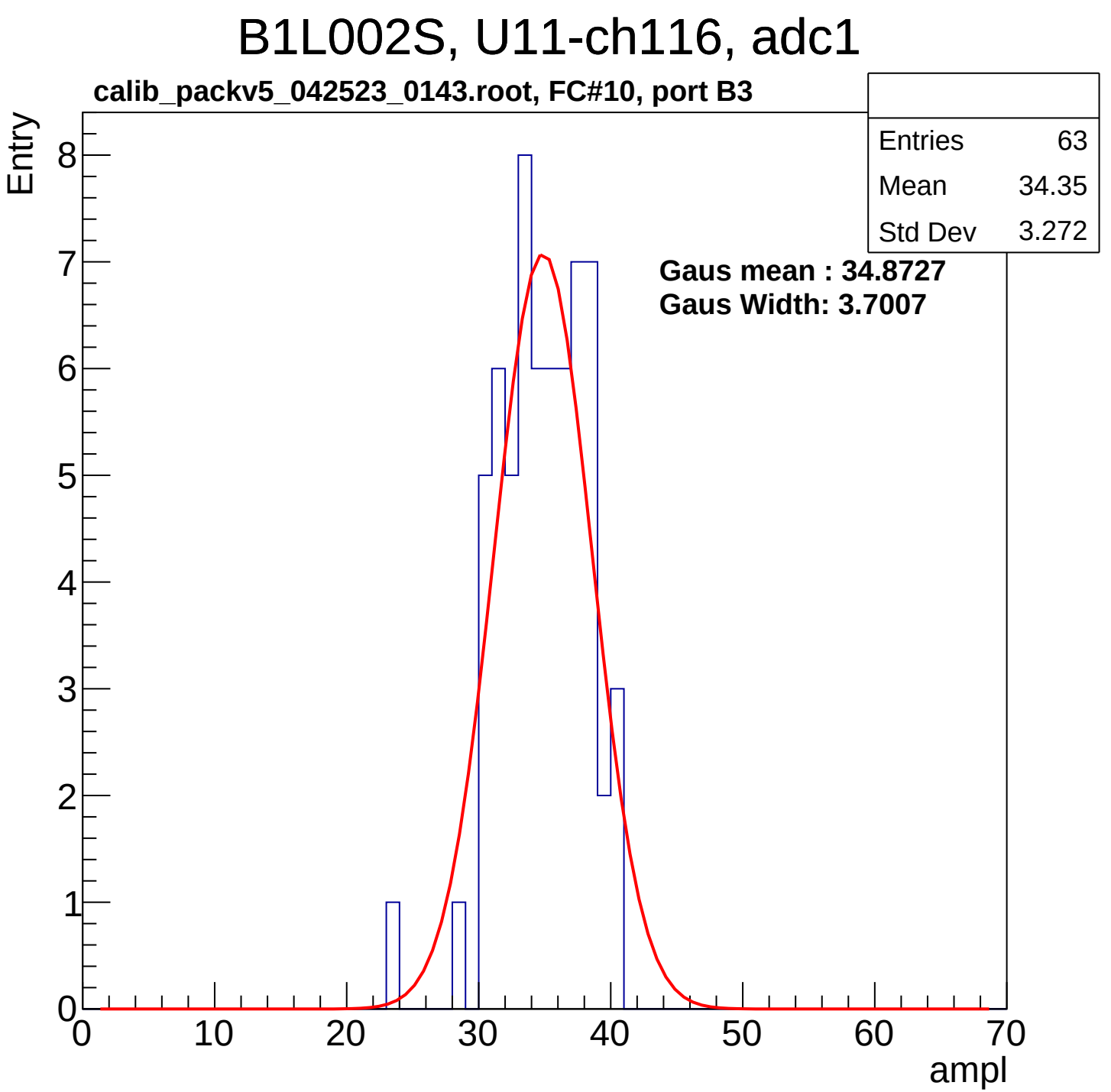

B1L002S, U11-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 52.64
Std Dev 3.562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 56.9
Std Dev 9.534
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 60.45
Std Dev 2.127
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.9
Std Dev 3.161
Gaus mean : 28.1661
Gaus Width: 3.3037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.35
Std Dev 3.272
Gaus mean : 34.8727
Gaus Width: 3.7007
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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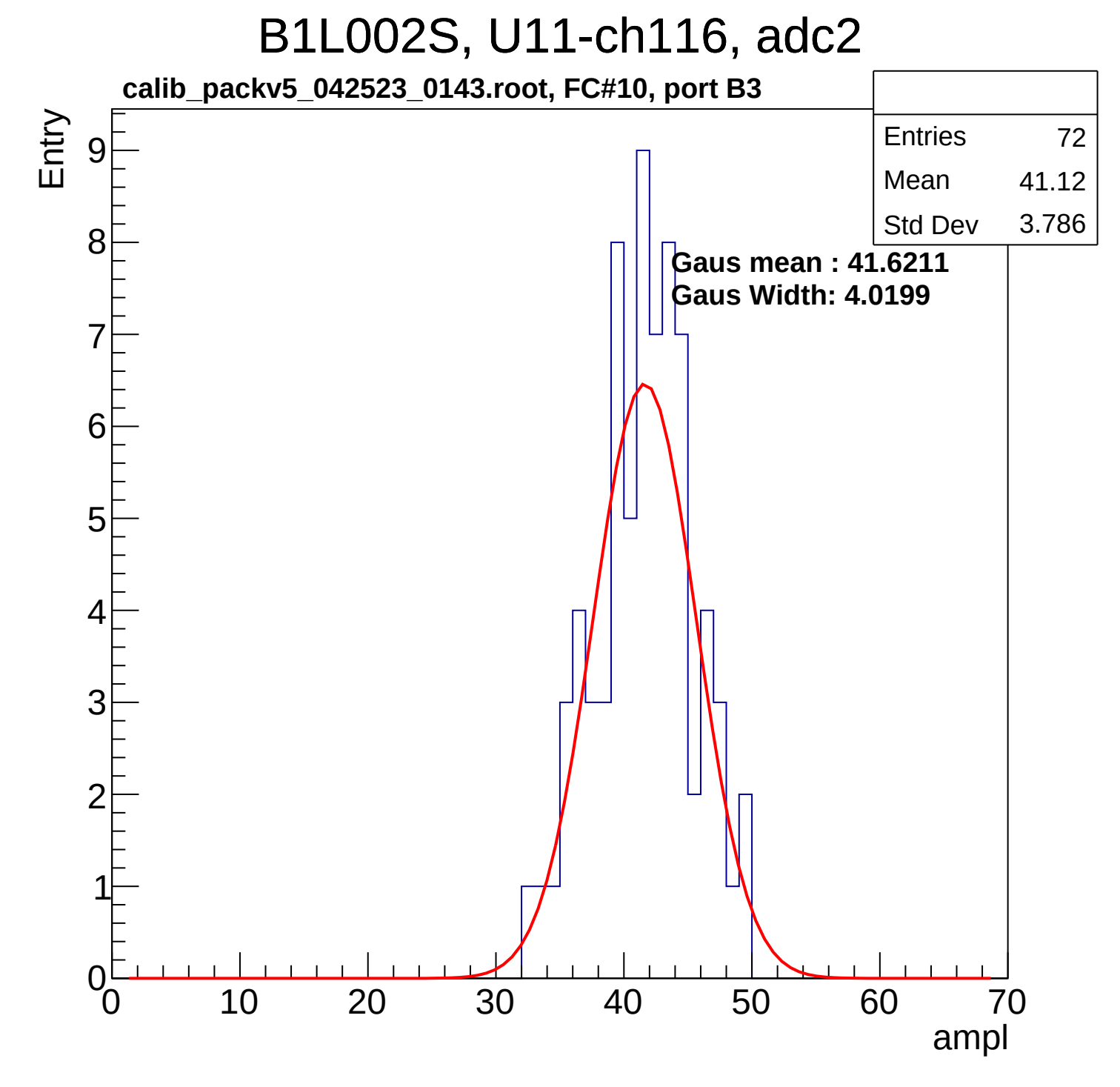
B1L002S, U11-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 41.12
Std Dev 3.786
Gaus mean : 41.6211
Gaus Width: 4.0199
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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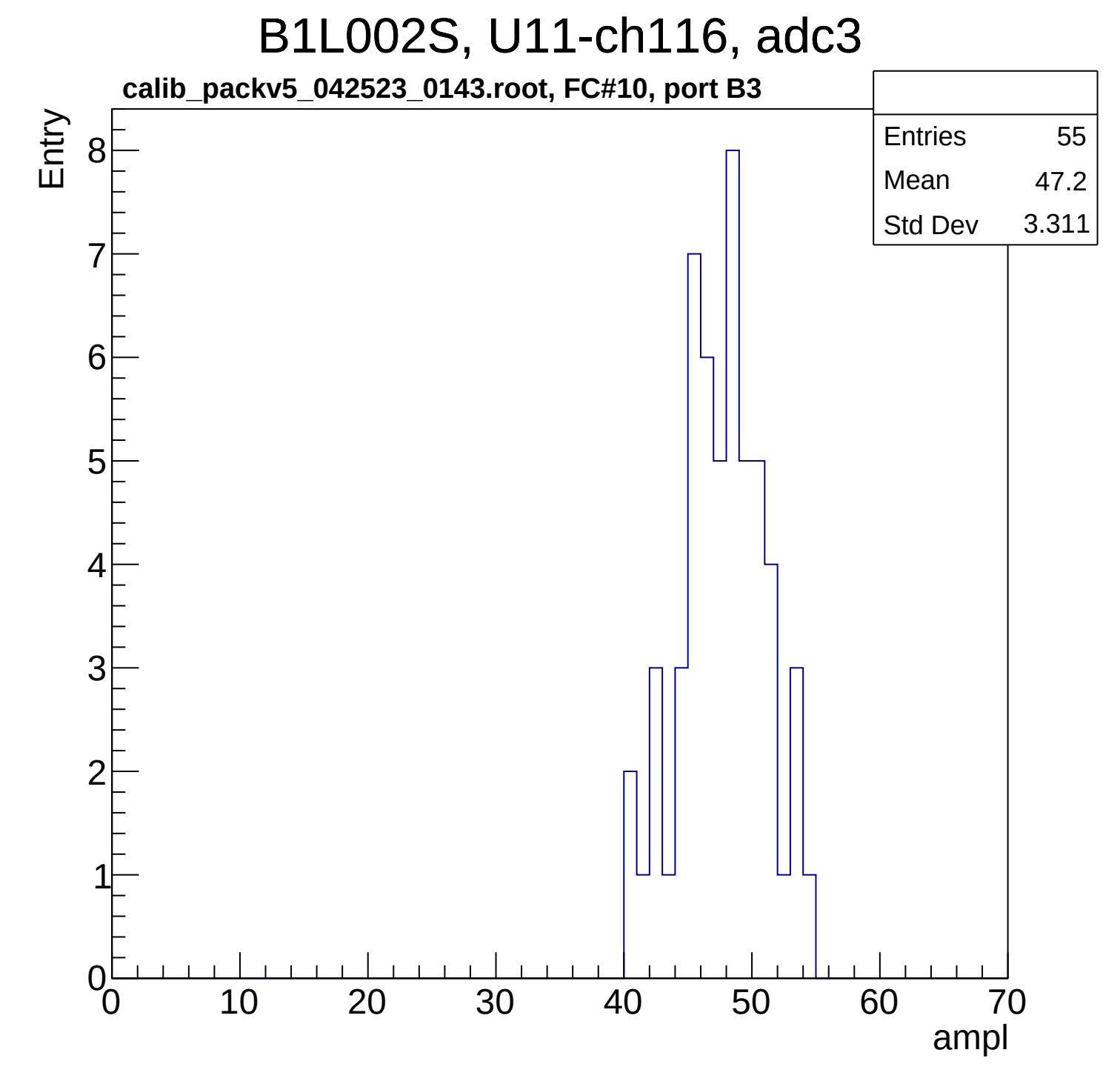
B1L002S, U11-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.2
Std Dev 3.311
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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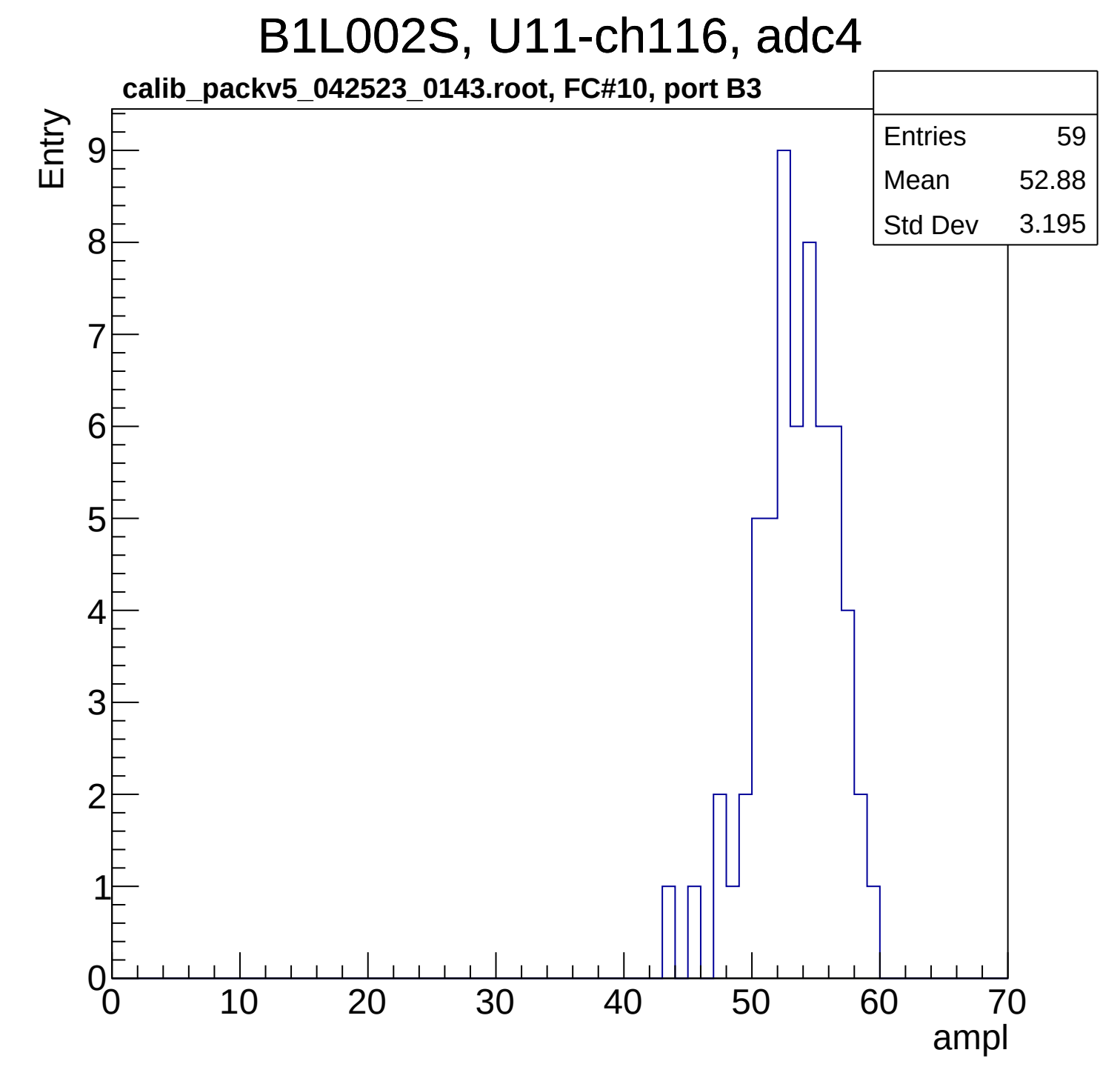
B1L002S, U11-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.88
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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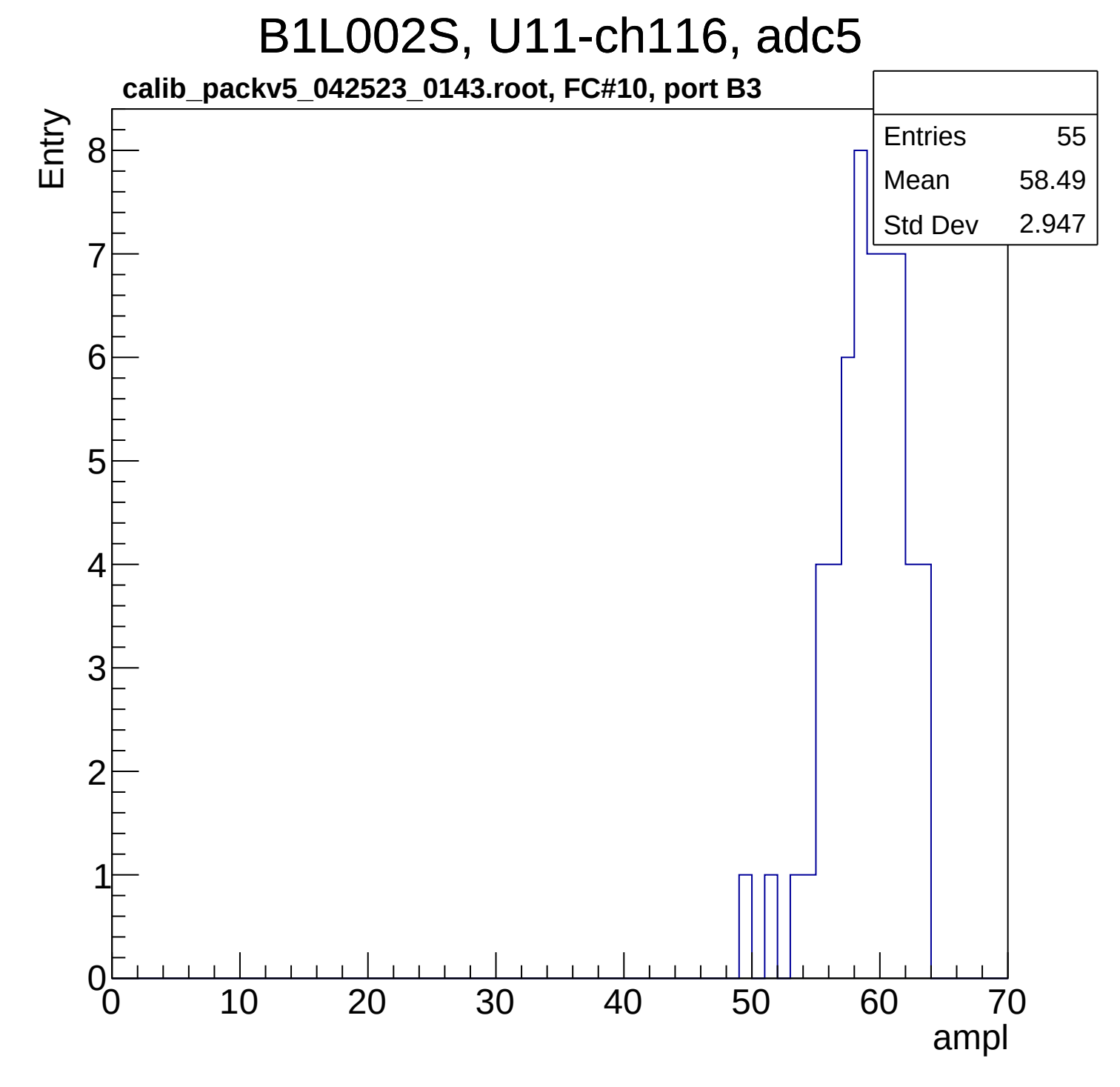
B1L002S, U11-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.49
Std Dev 2.947
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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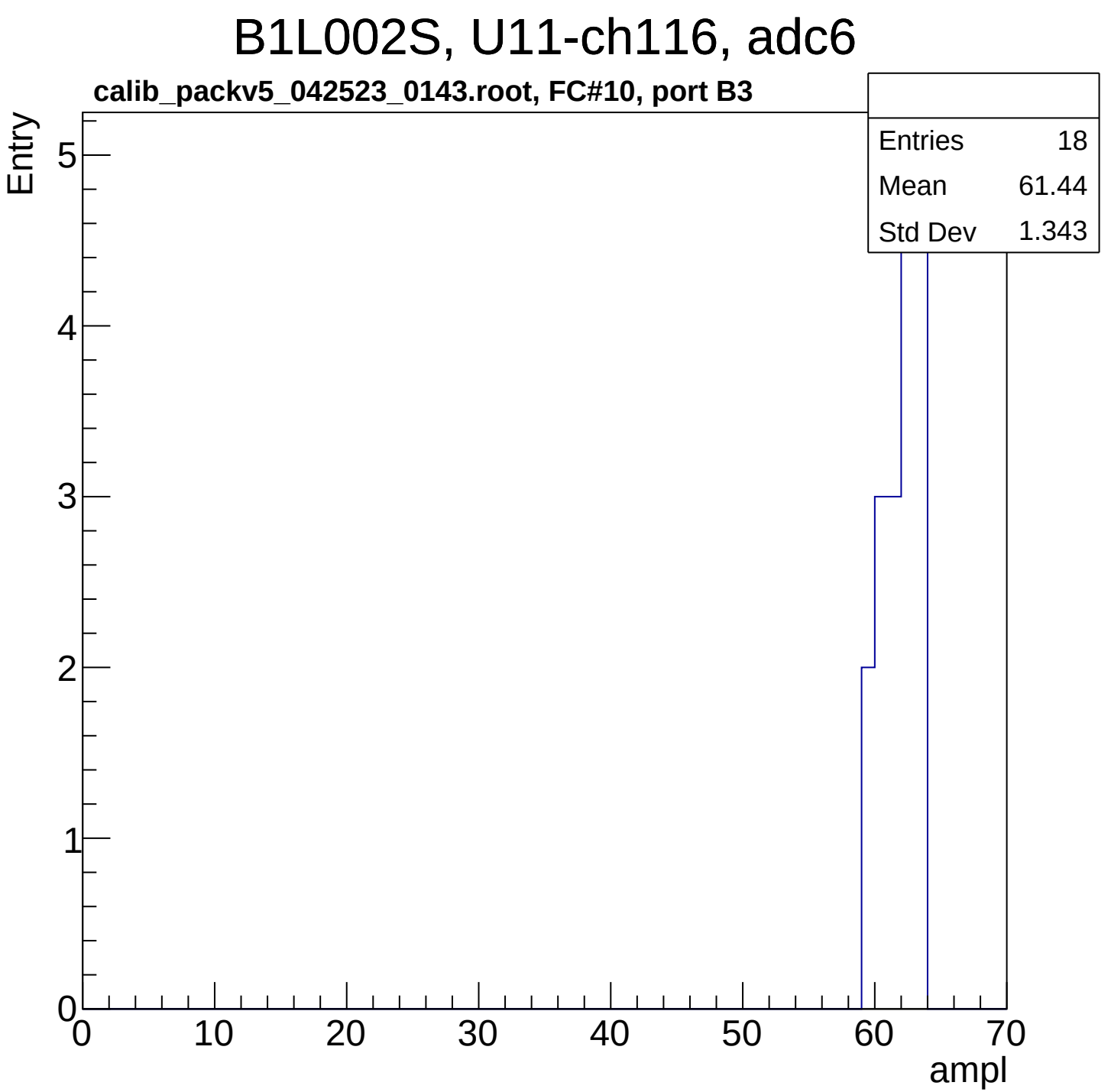
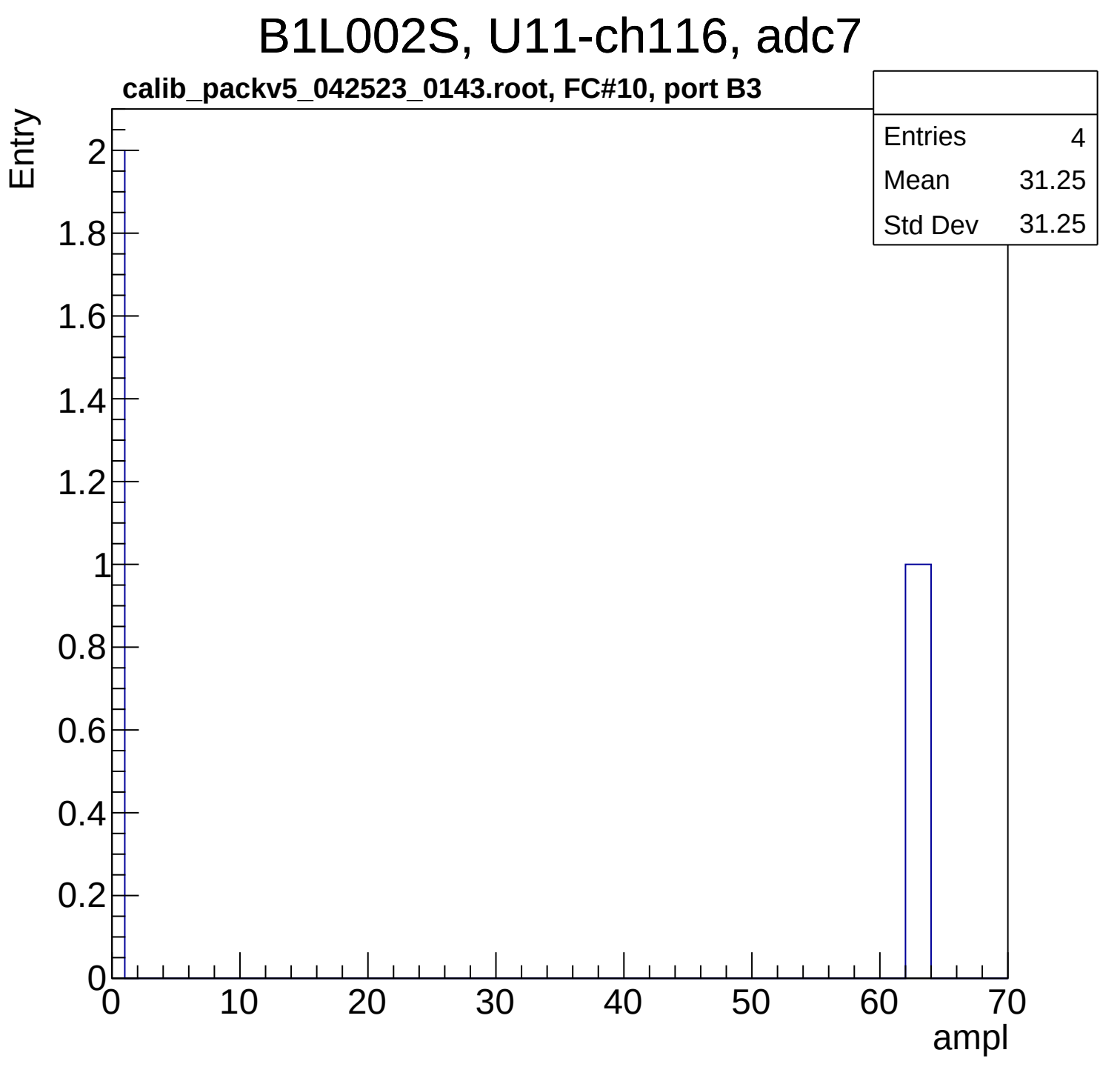

B1L002S, U11-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.44
Std Dev 1.343
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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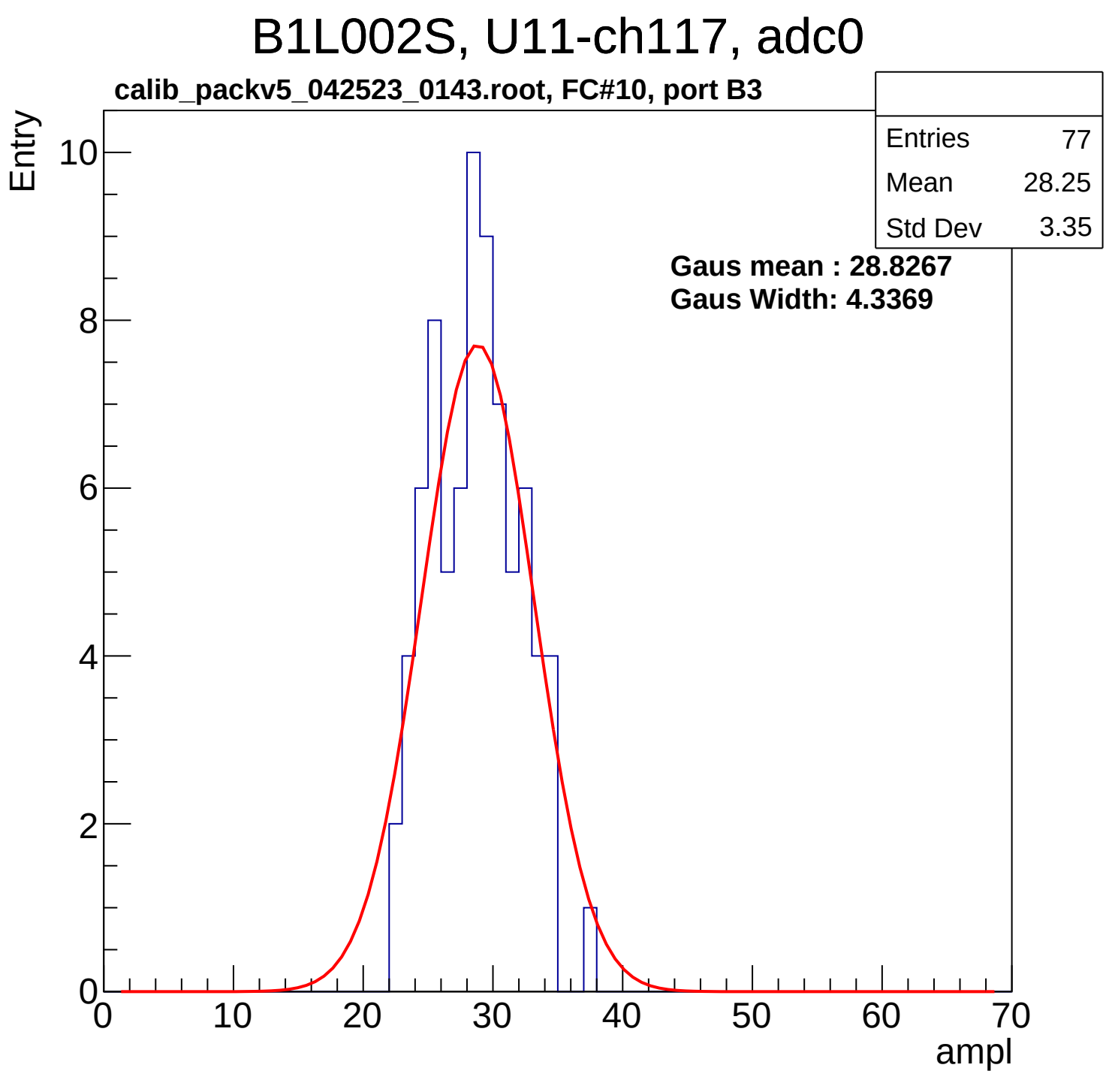
B1L002S, U11-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.25
Std Dev 3.35
Gaus mean : 28.8267
Gaus Width: 4.3369
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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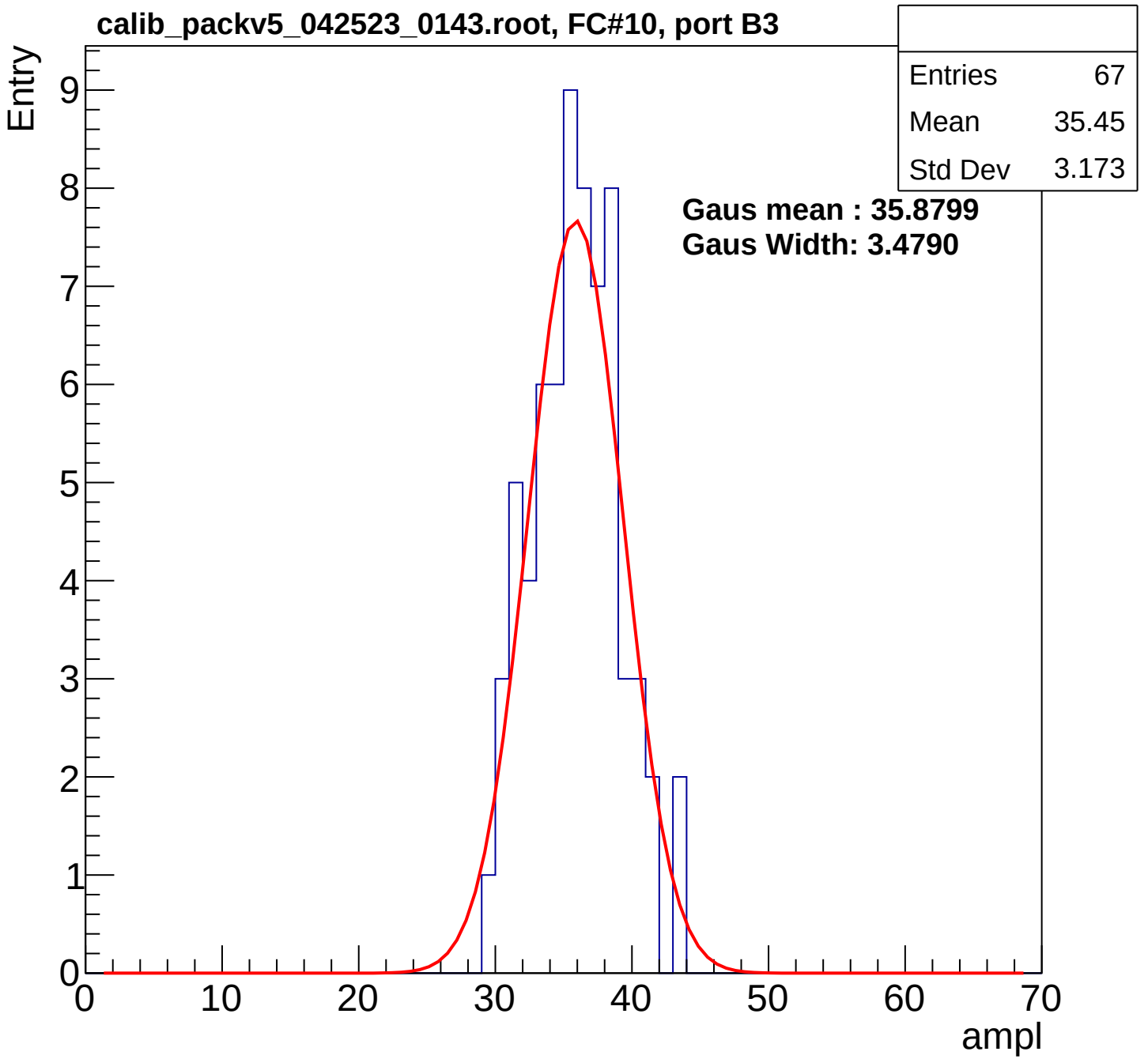
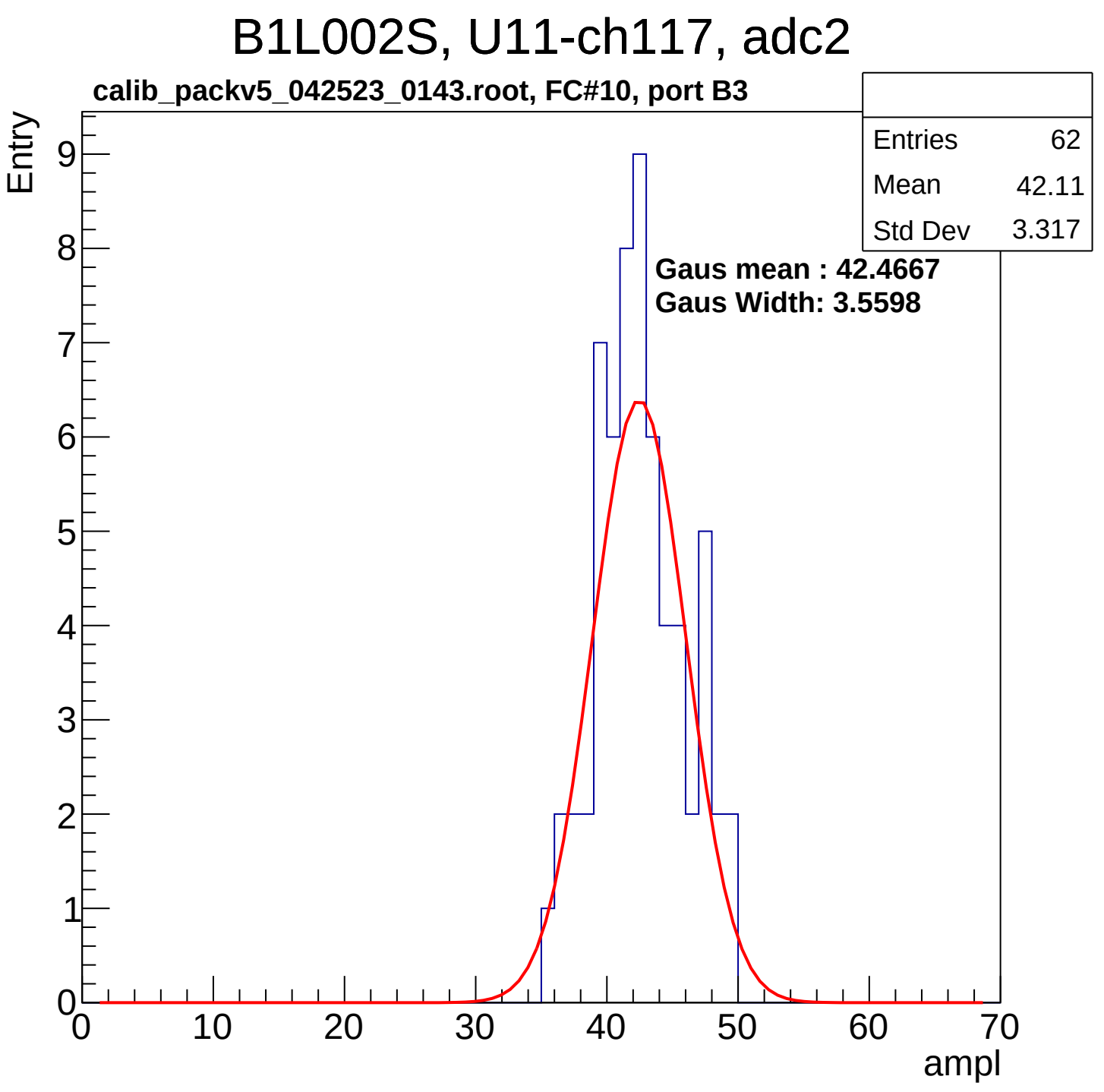
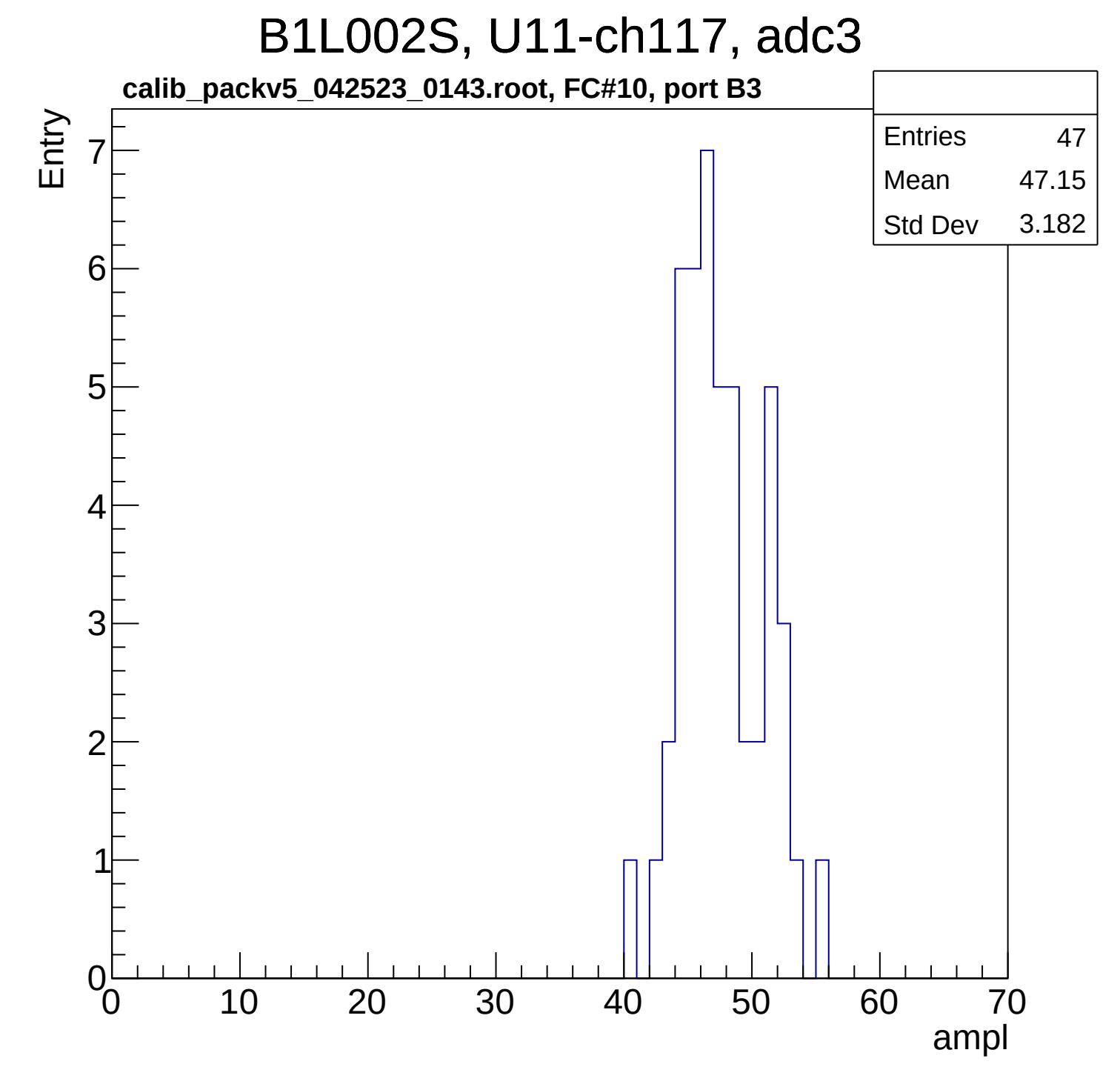
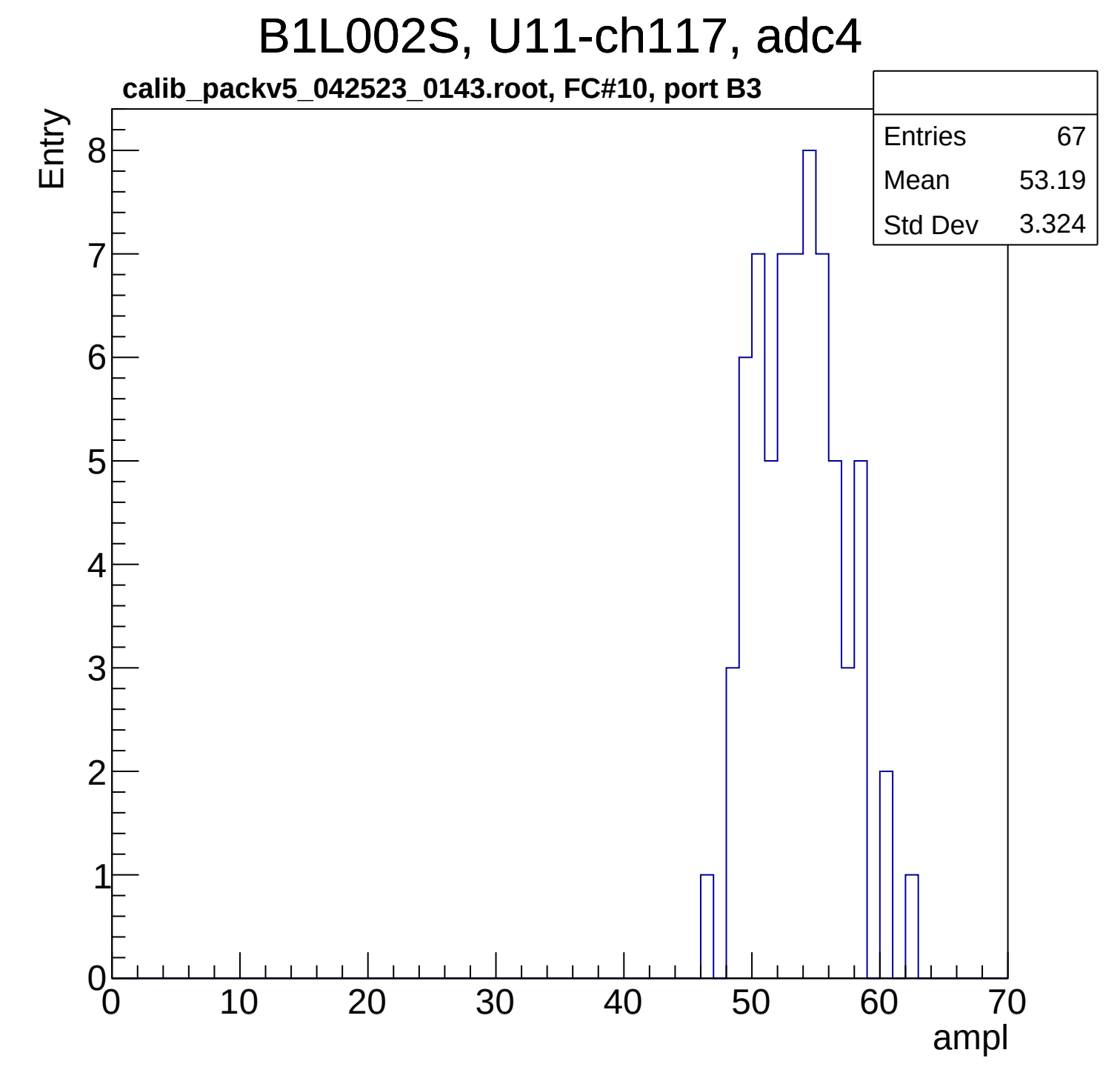

B1L002S, U11-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.45
Std Dev 3.173
Gaus mean : 35.8799
Gaus Width: 3.4790
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.11
Std Dev 3.317
Gaus mean : 42.4667
Gaus Width: 3.5598
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 47.15
Std Dev 3.182
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.19
Std Dev 3.324
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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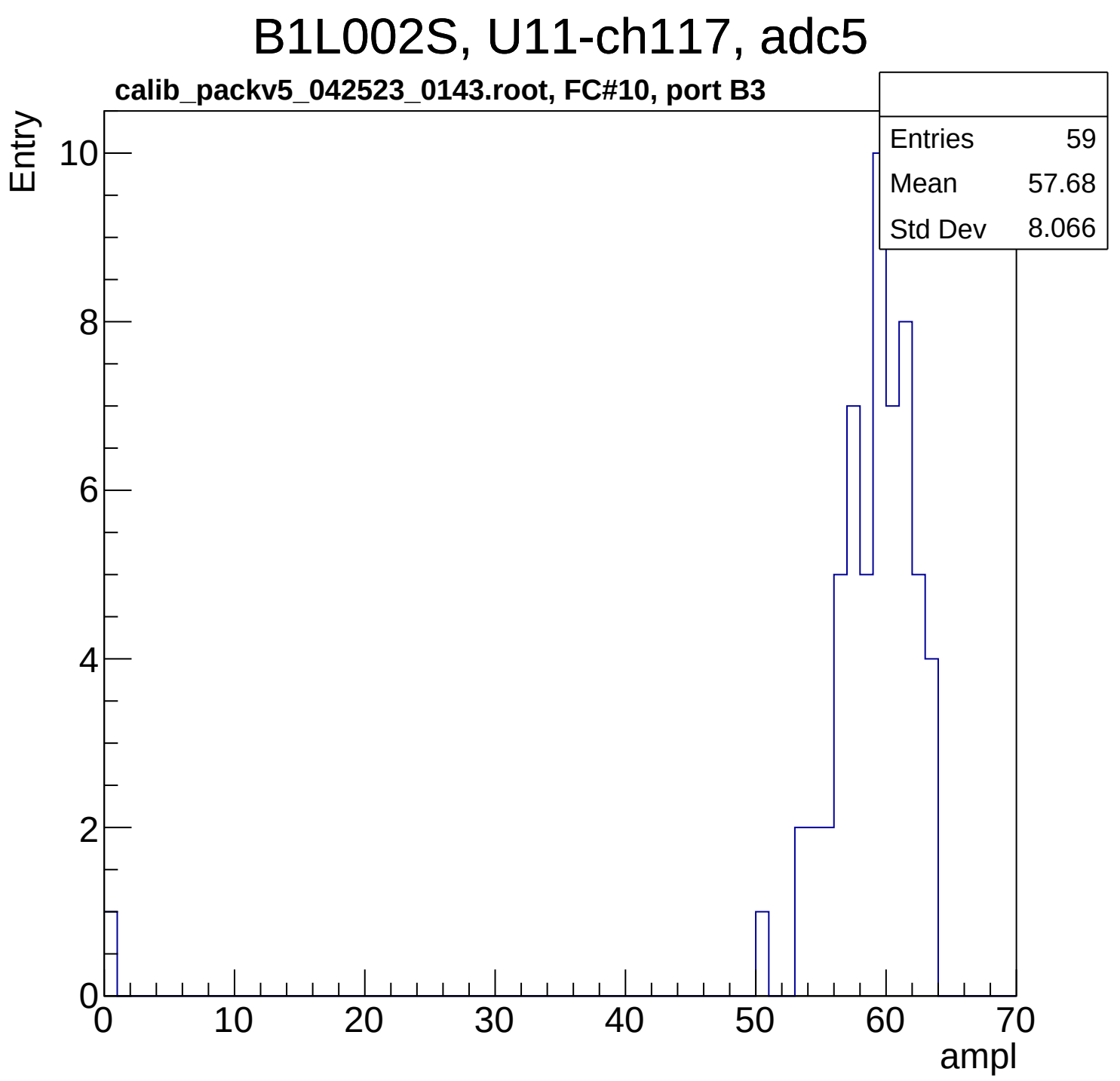
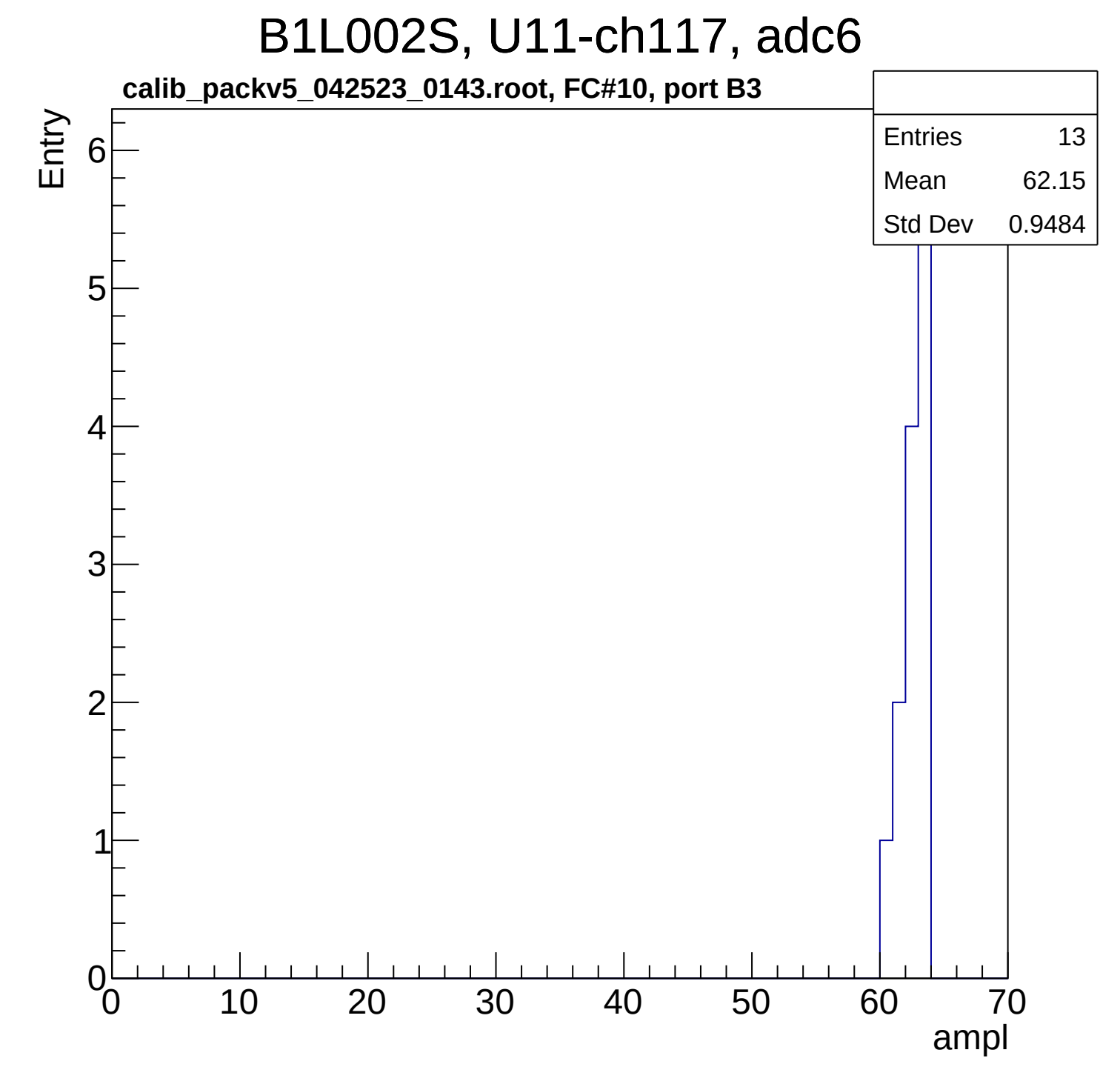
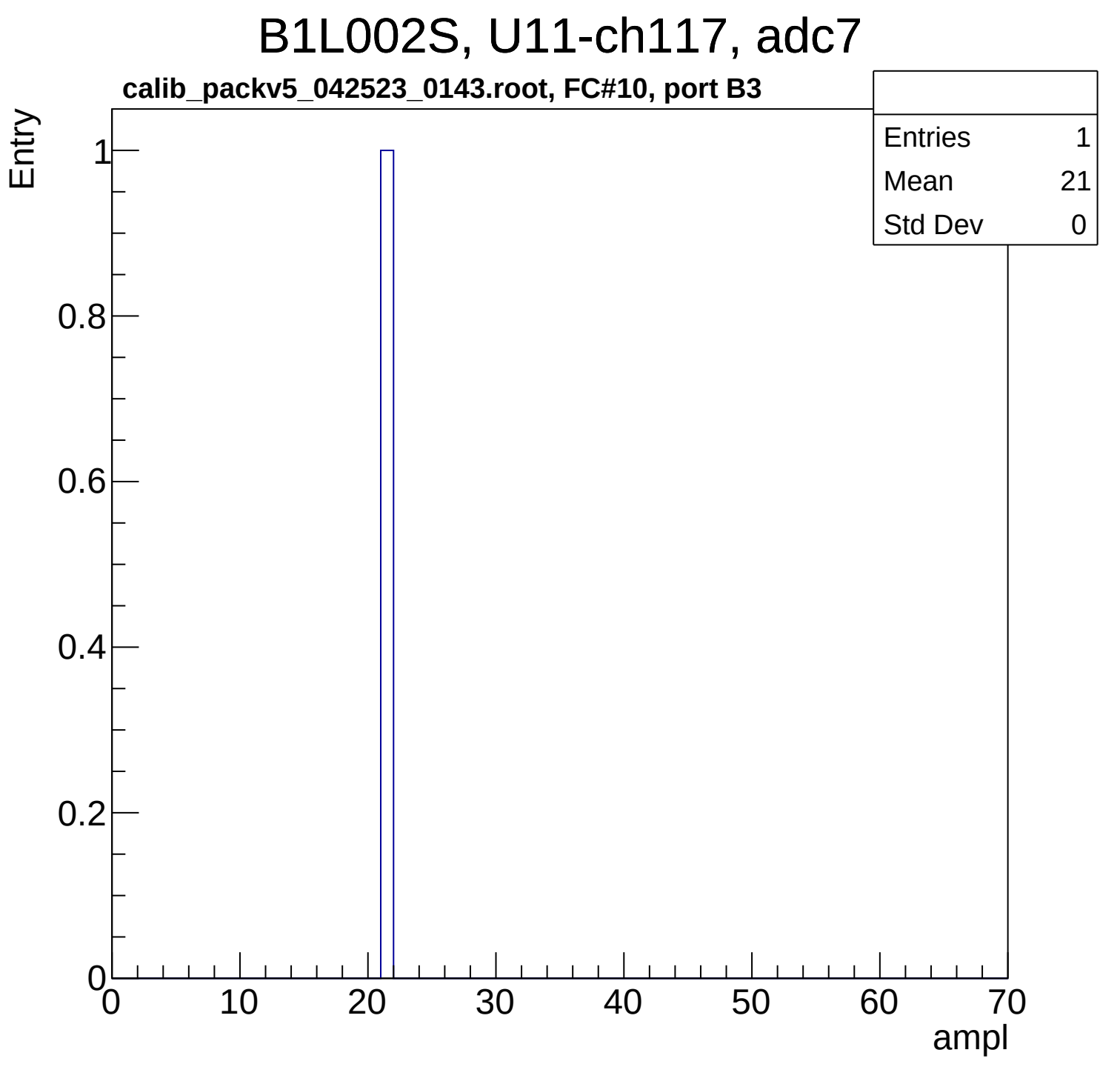
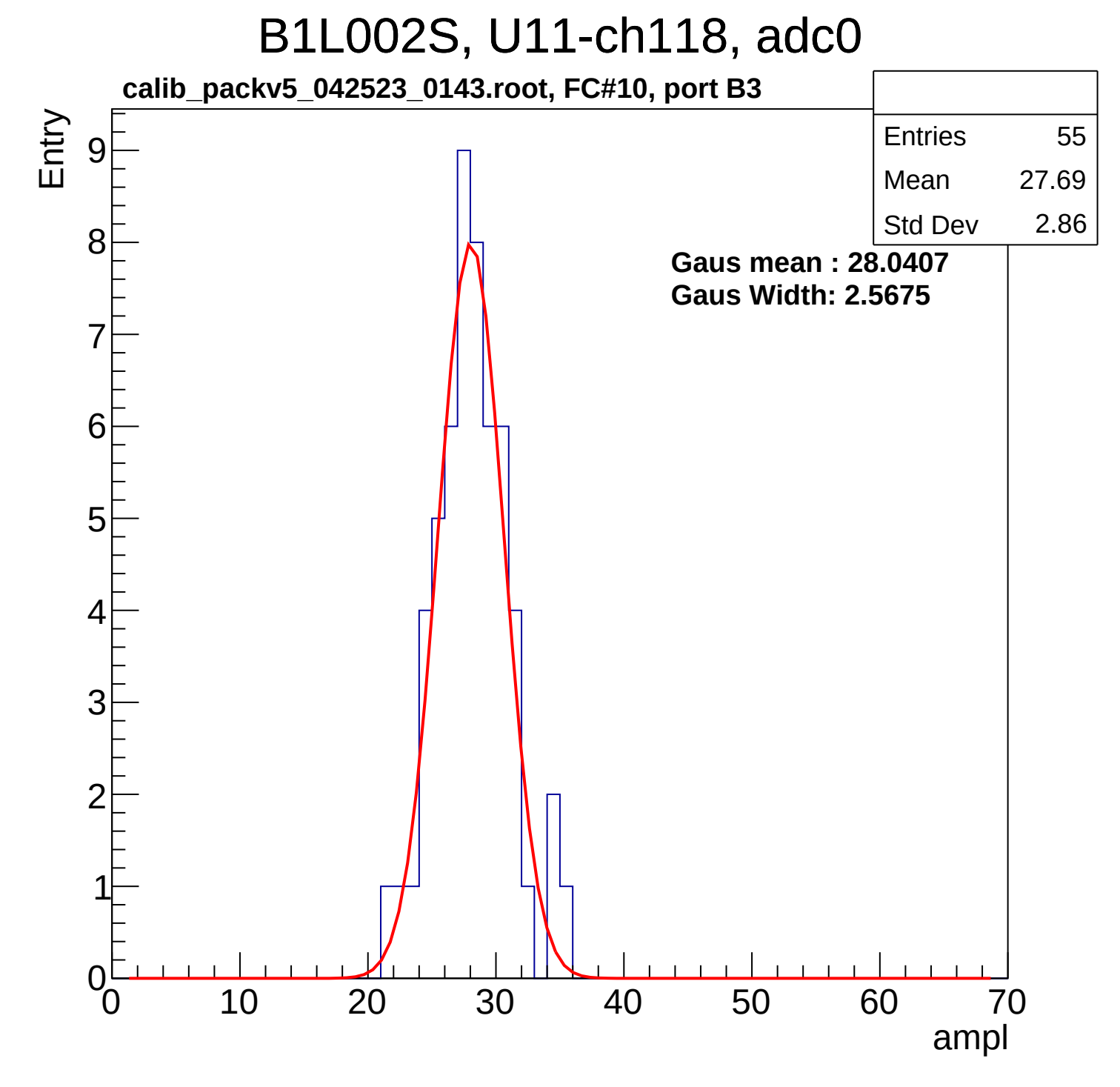
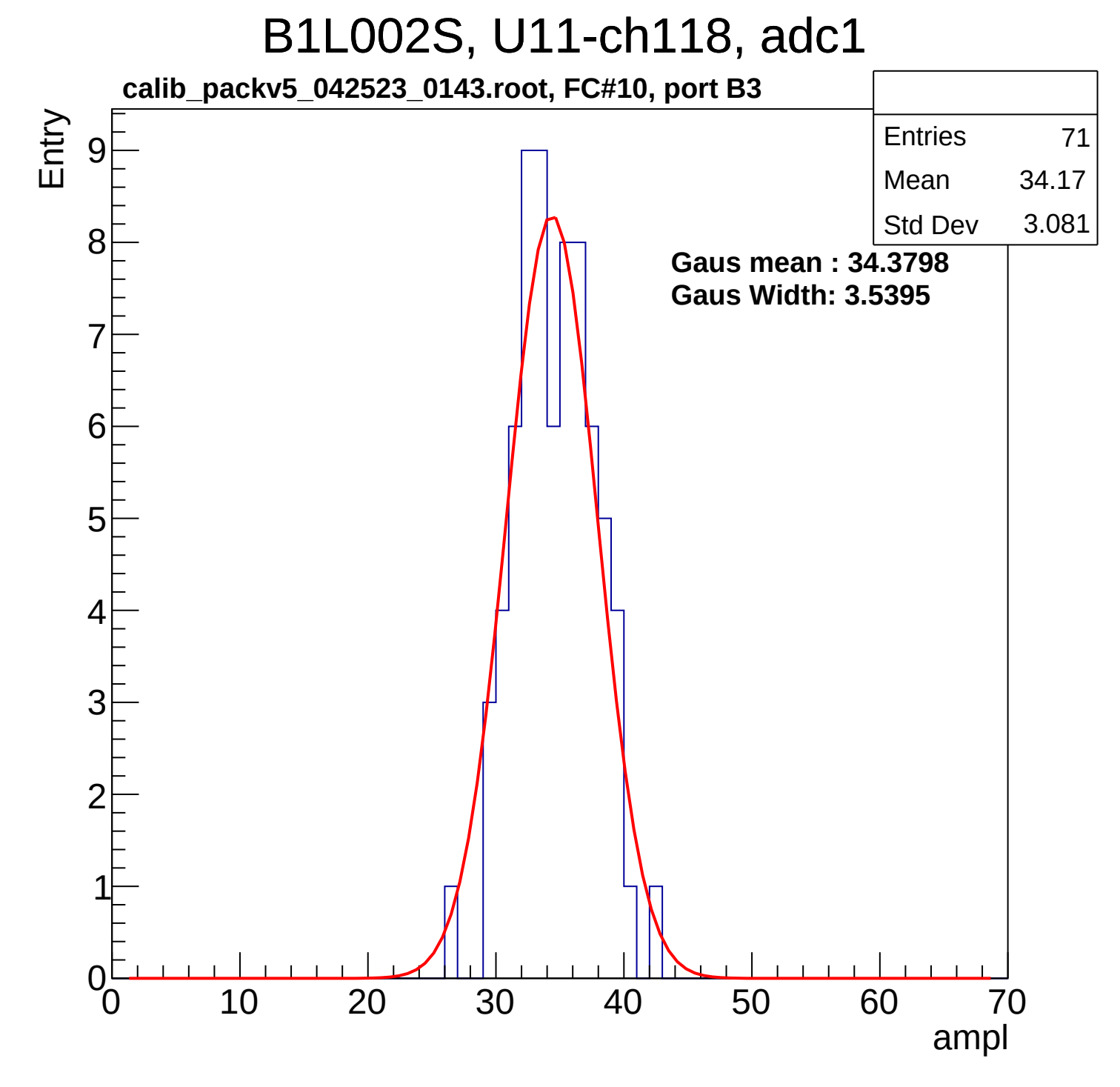
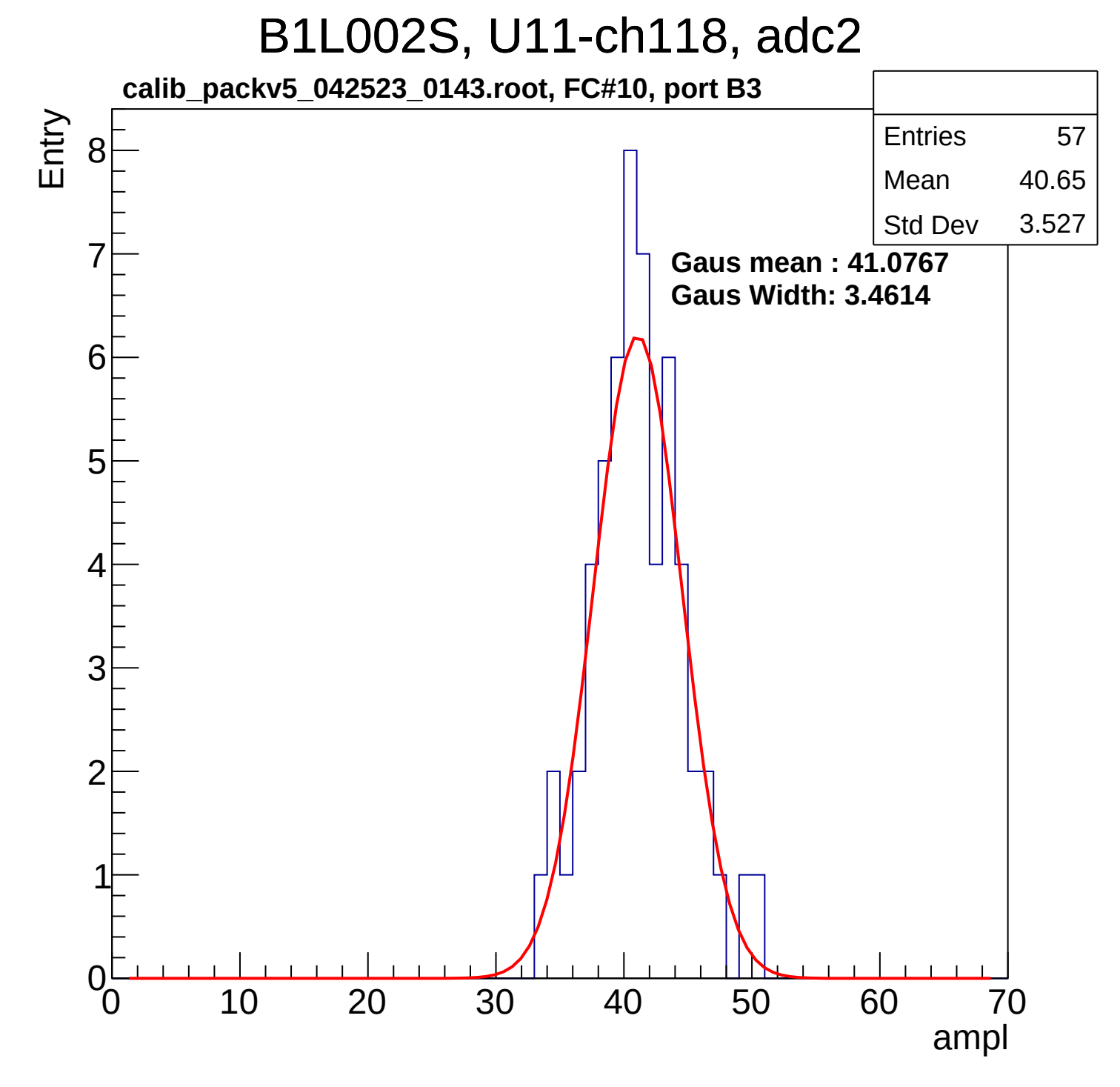

B1L002S, U11-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.68
Std Dev 8.066
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

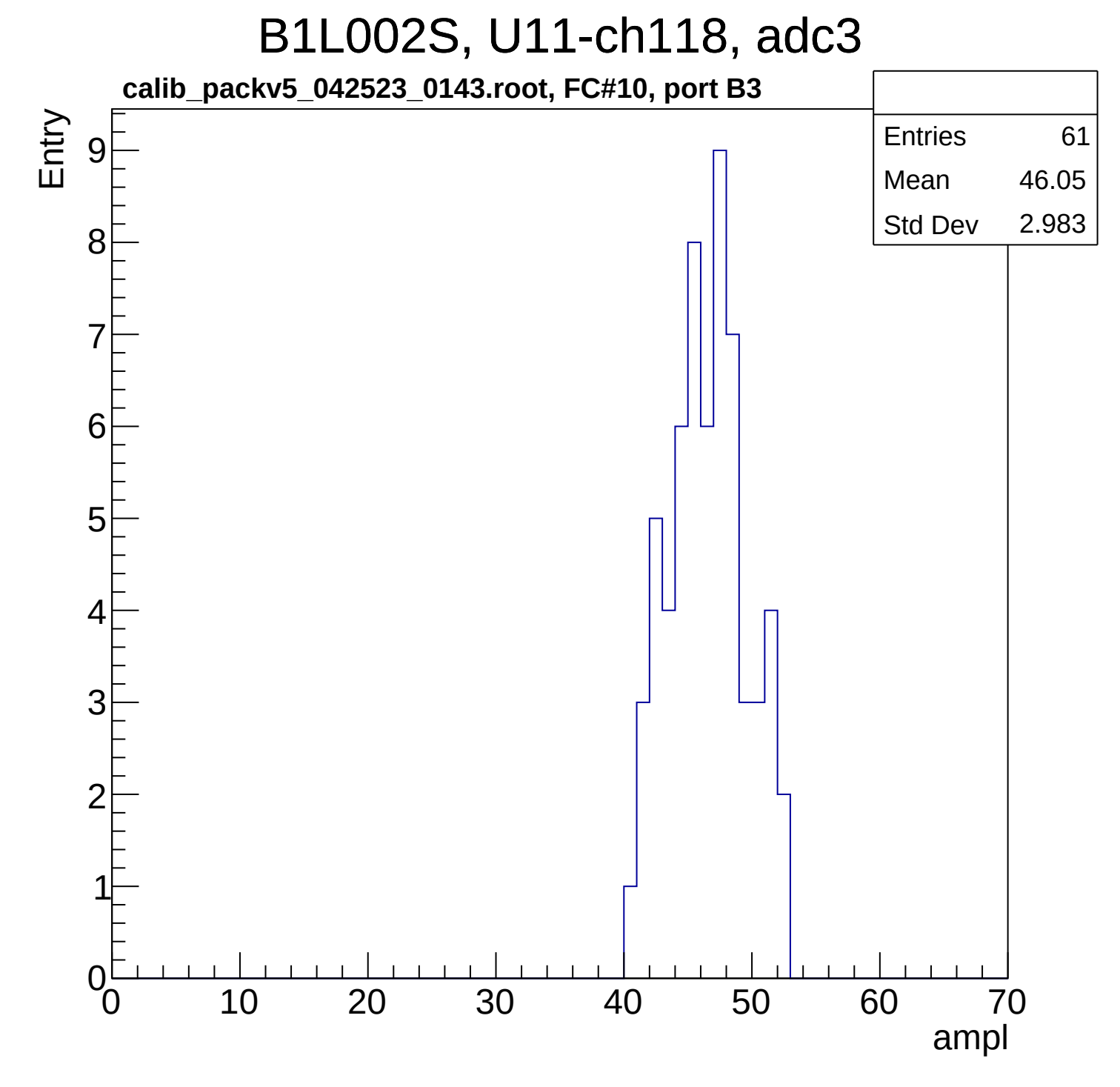

B1L002S, U11-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 62.15
Std Dev 0.9484
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 27.69
Std Dev 2.86
Gaus mean : 28.0407
Gaus Width: 2.5675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.17
Std Dev 3.081
Gaus mean : 34.3798
Gaus Width: 3.5395
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.65
Std Dev 3.527
Gaus mean : 41.0767
Gaus Width: 3.4614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.05
Std Dev 2.983
Entry
ampl
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5
6
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0
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20
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60
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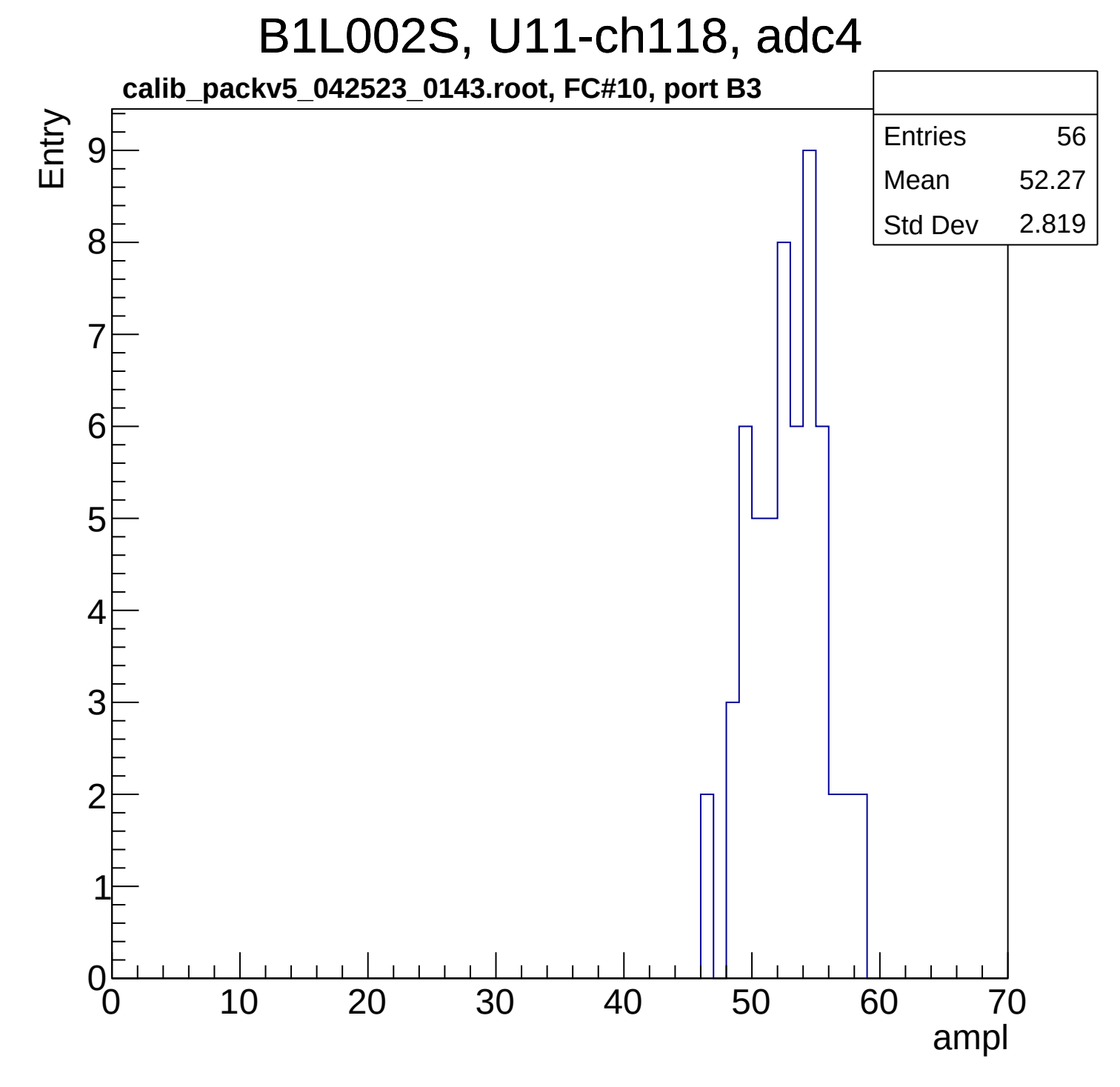
B1L002S, U11-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.27
Std Dev 2.819
Entry
ampl
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3
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0
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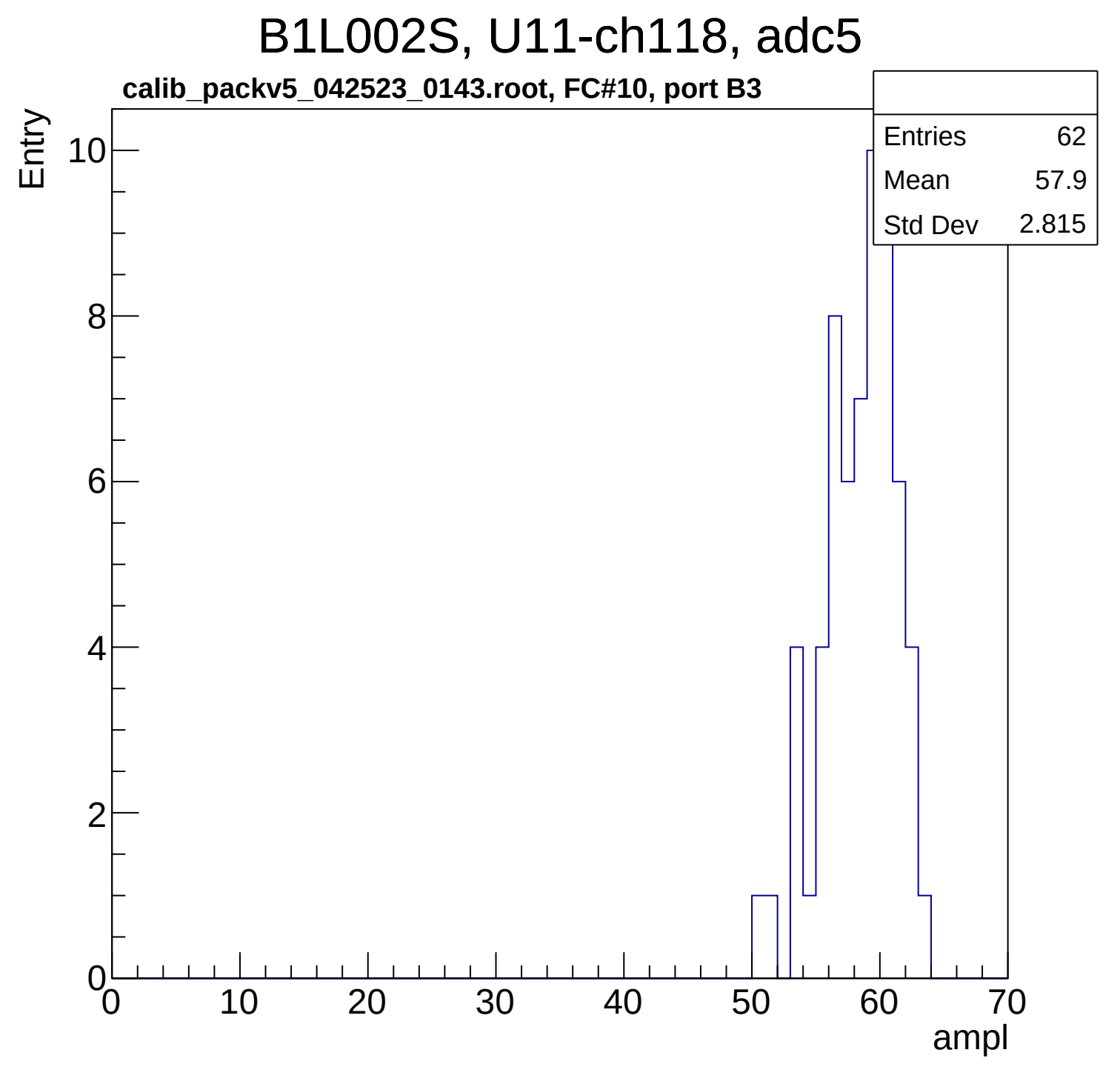
B1L002S, U11-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.9
Std Dev 2.815
Entry
ampl
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2
4
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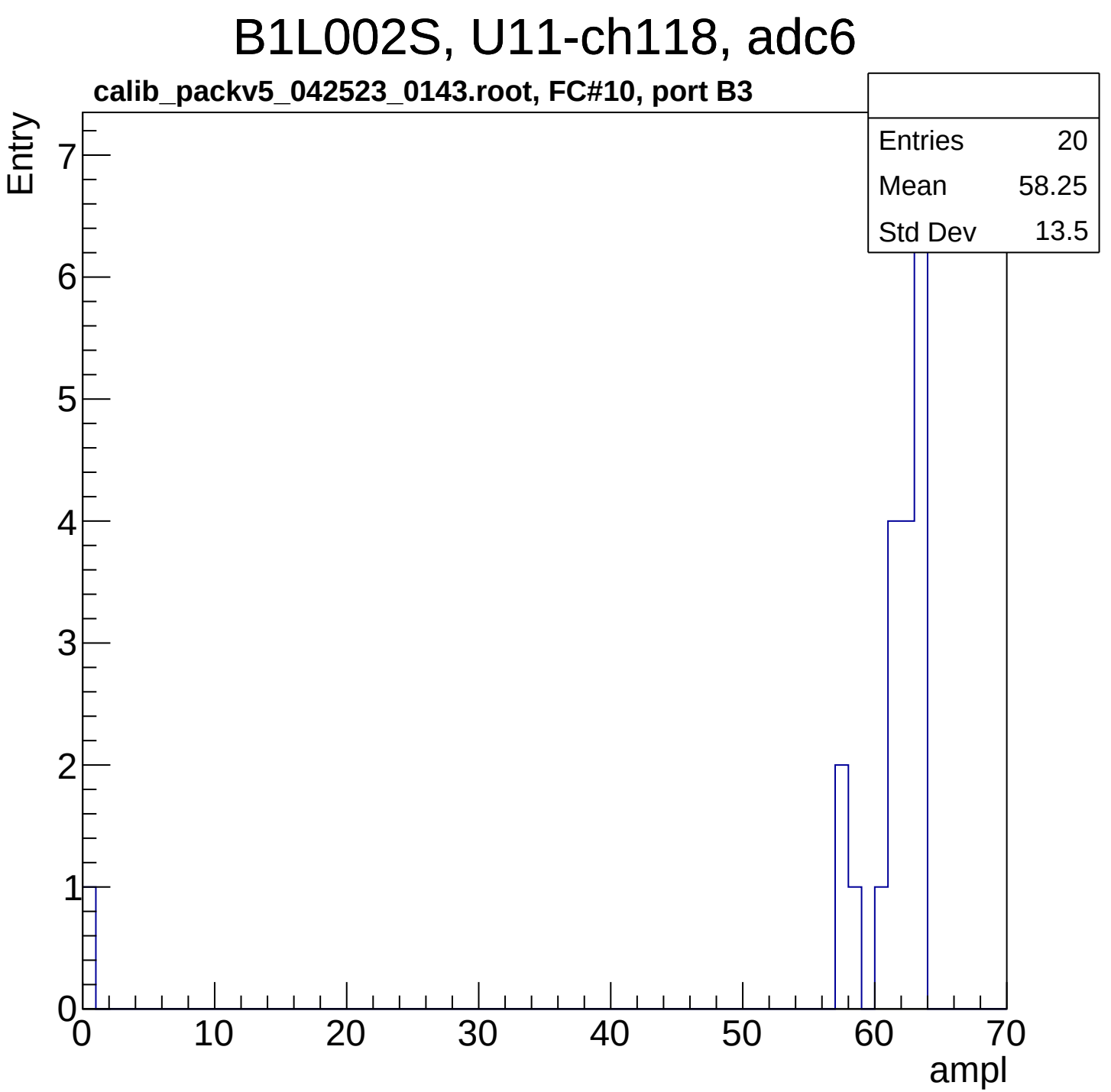
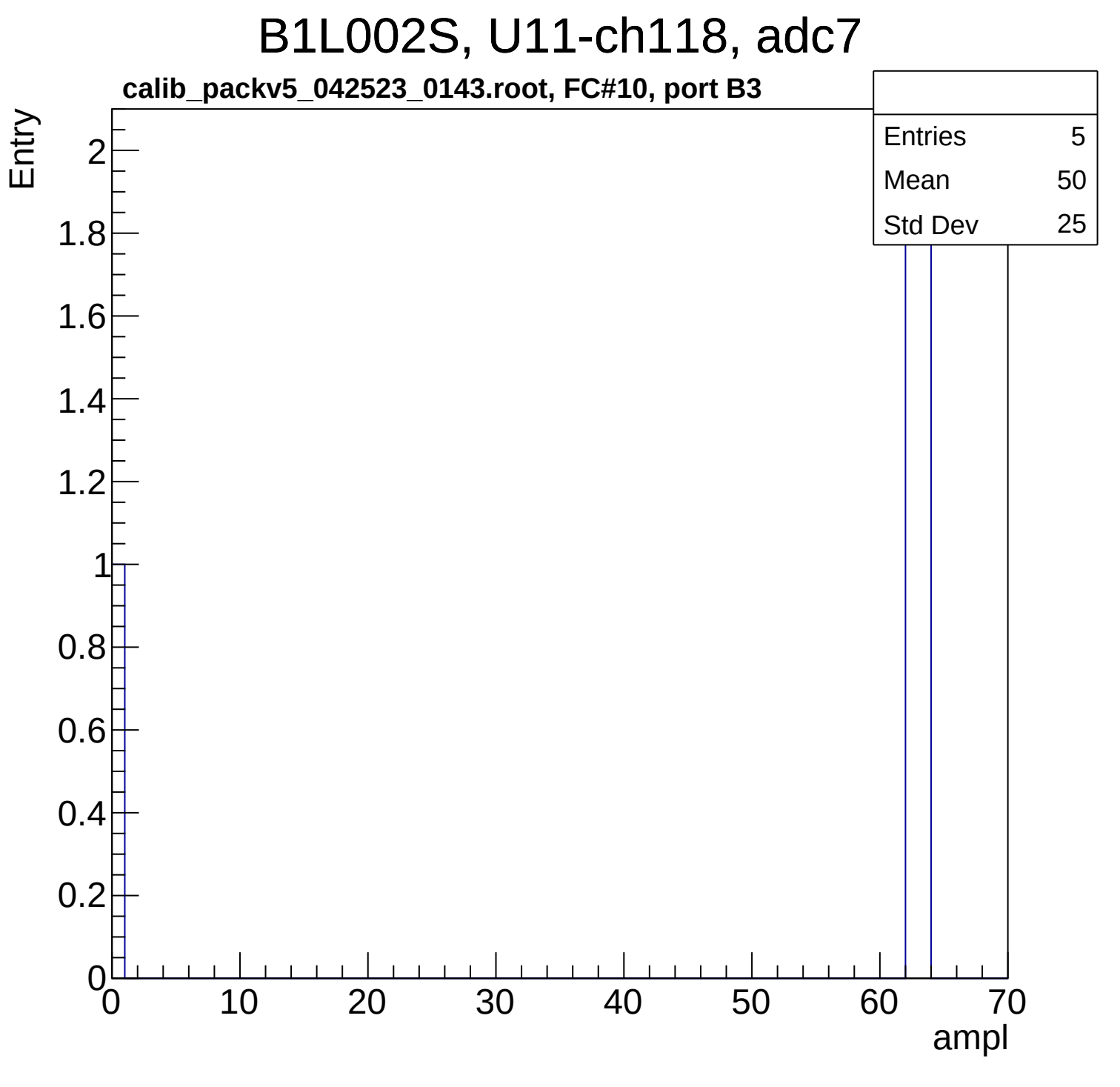
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.25
Std Dev 13.5
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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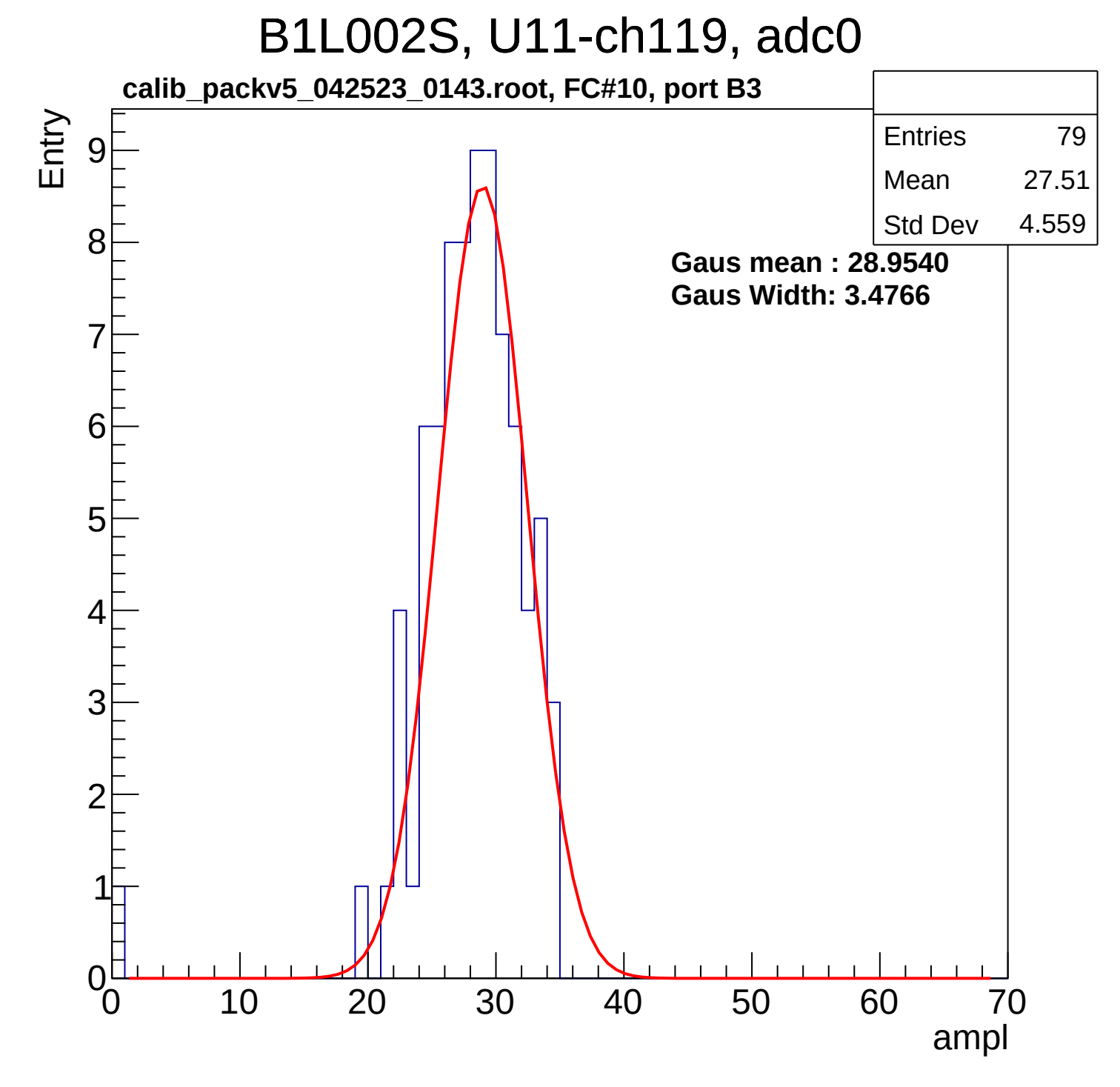
B1L002S, U11-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.51
Std Dev 4.559
Gaus mean : 28.9540
Gaus Width: 3.4766
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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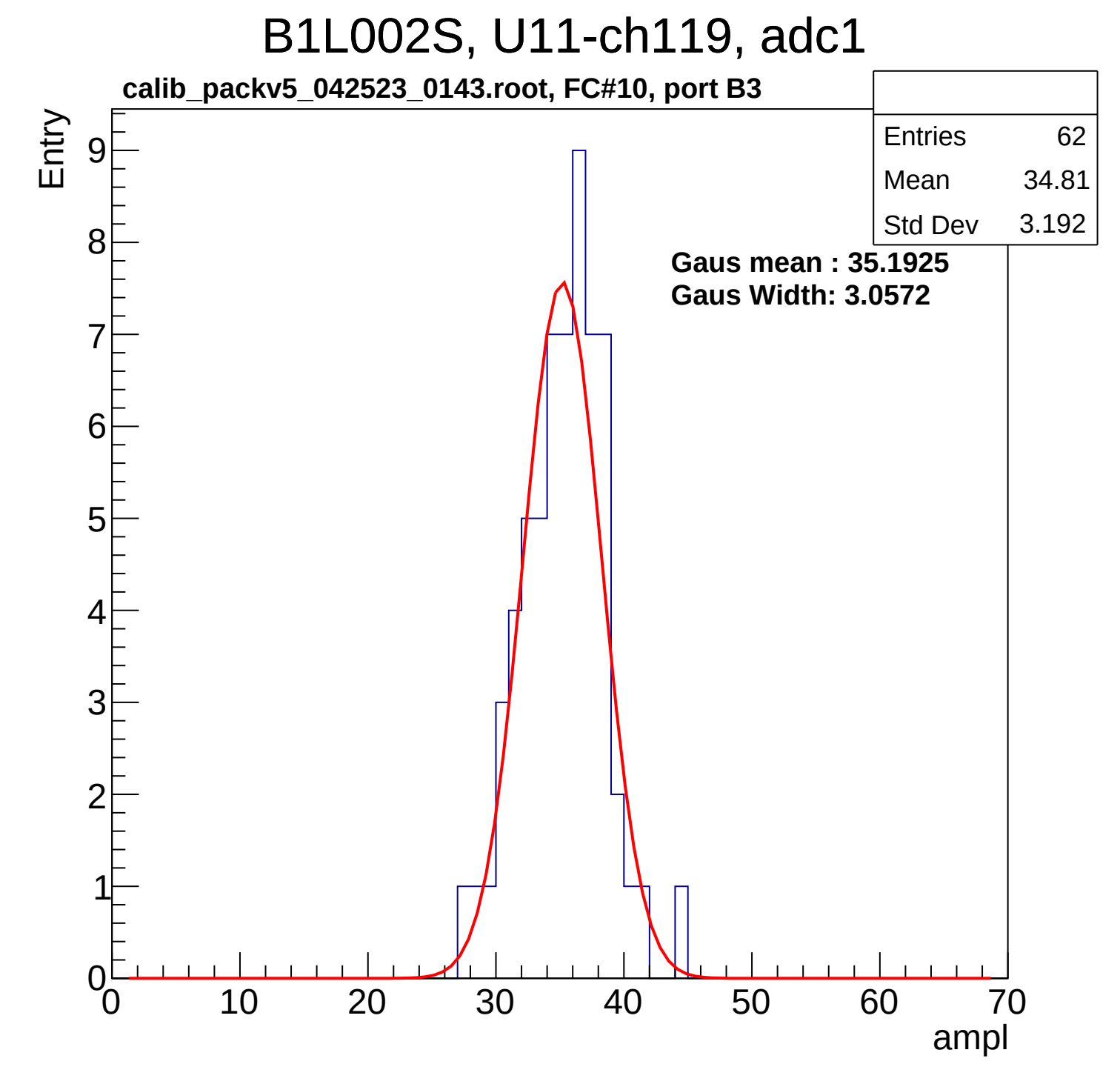
B1L002S, U11-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.81
Std Dev 3.192
Gaus mean : 35.1925
Gaus Width: 3.0572
Entry
ampl
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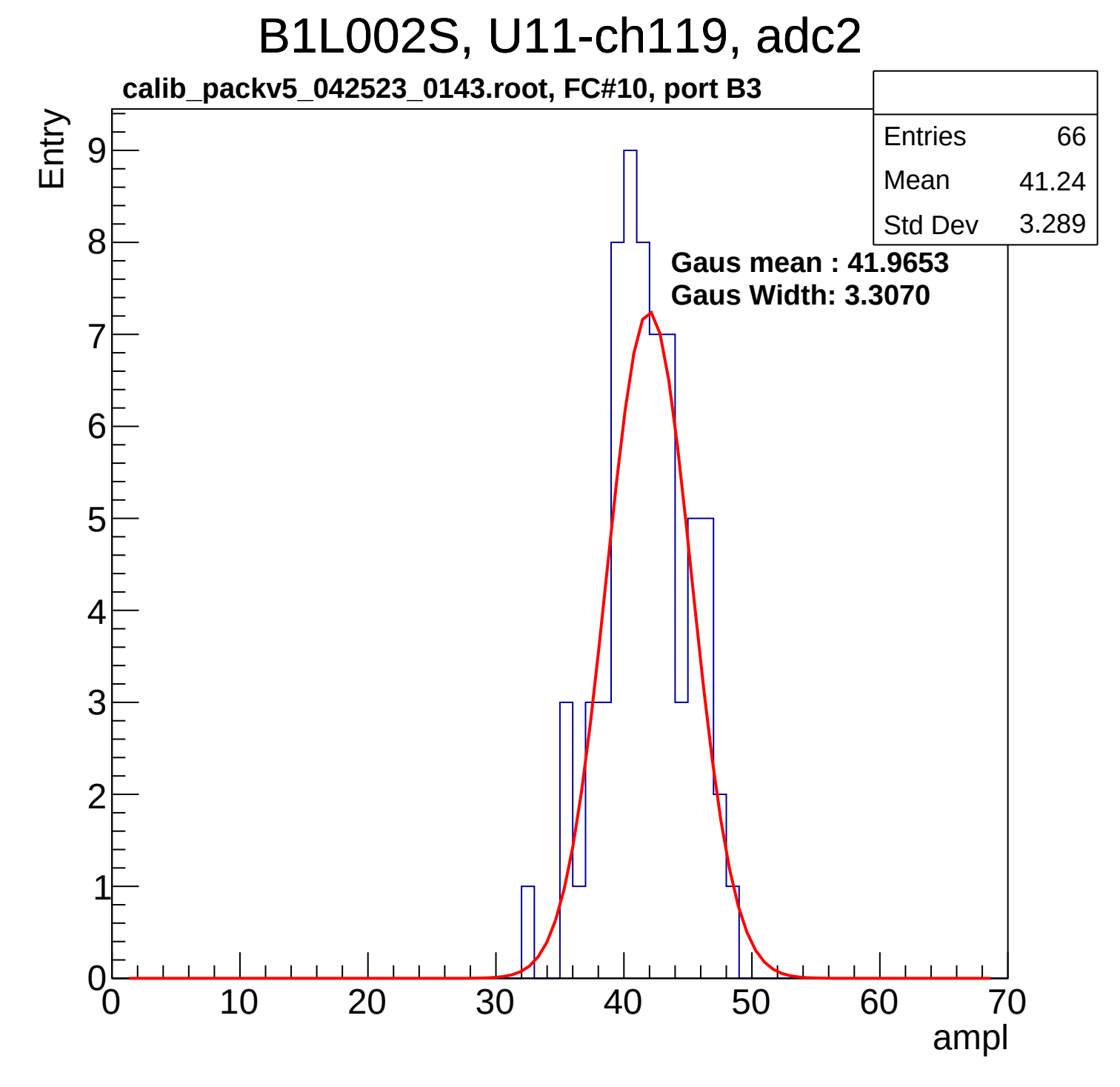
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.24
Std Dev 3.289
Gaus mean : 41.9653
Gaus Width: 3.3070
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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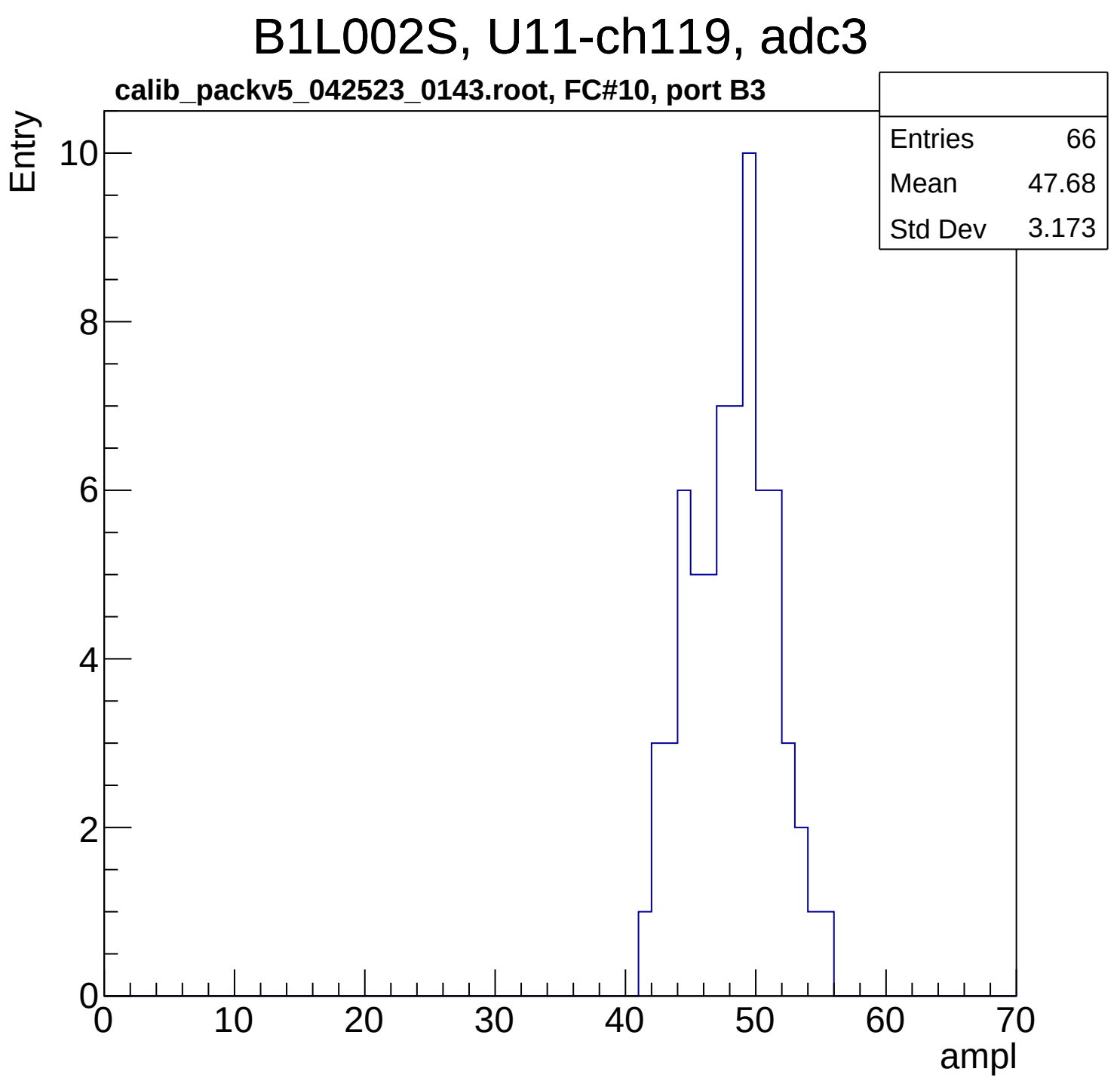
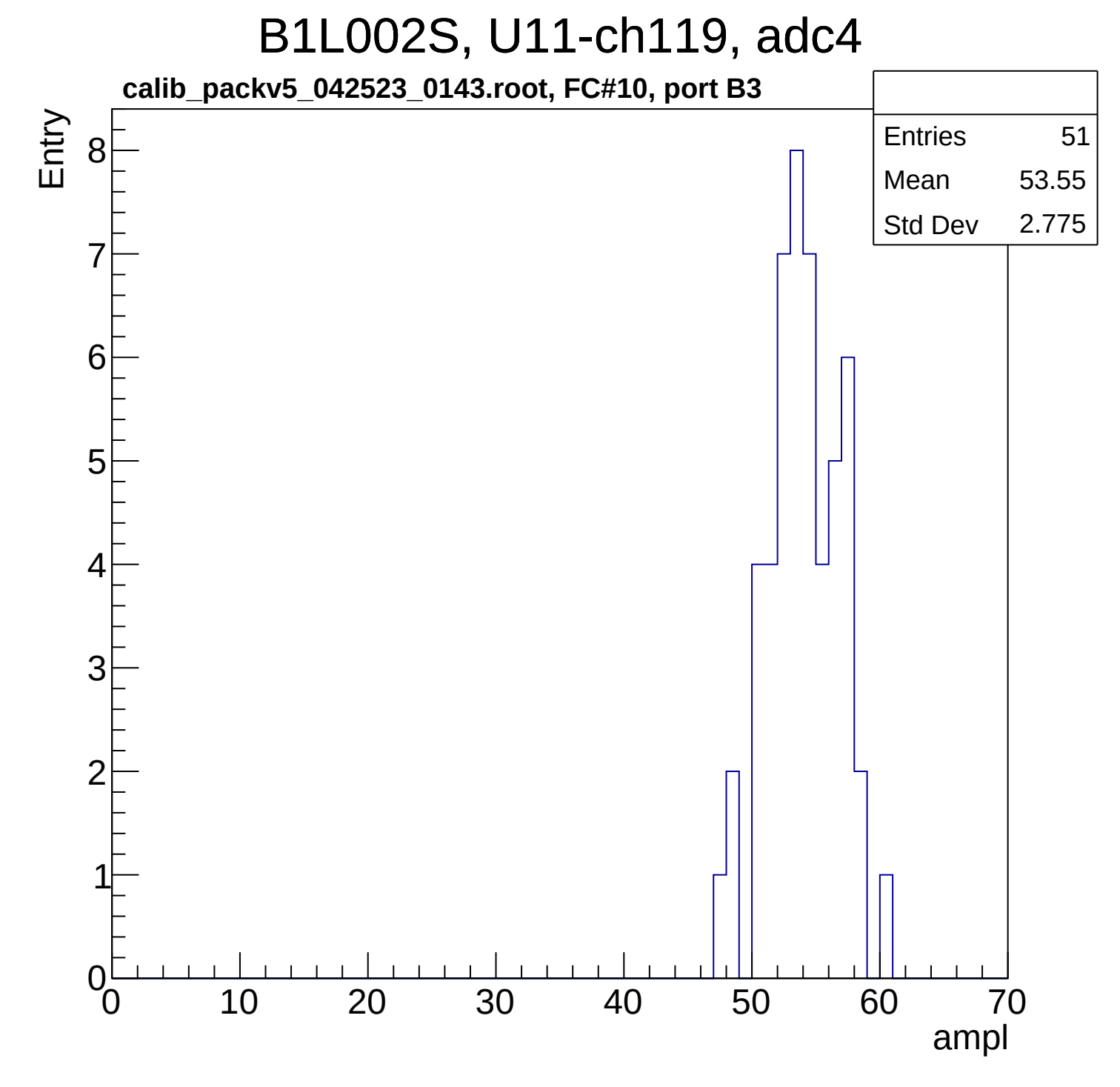

B1L002S, U11-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.68
Std Dev 3.173
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.55
Std Dev 2.775
Entry
ampl
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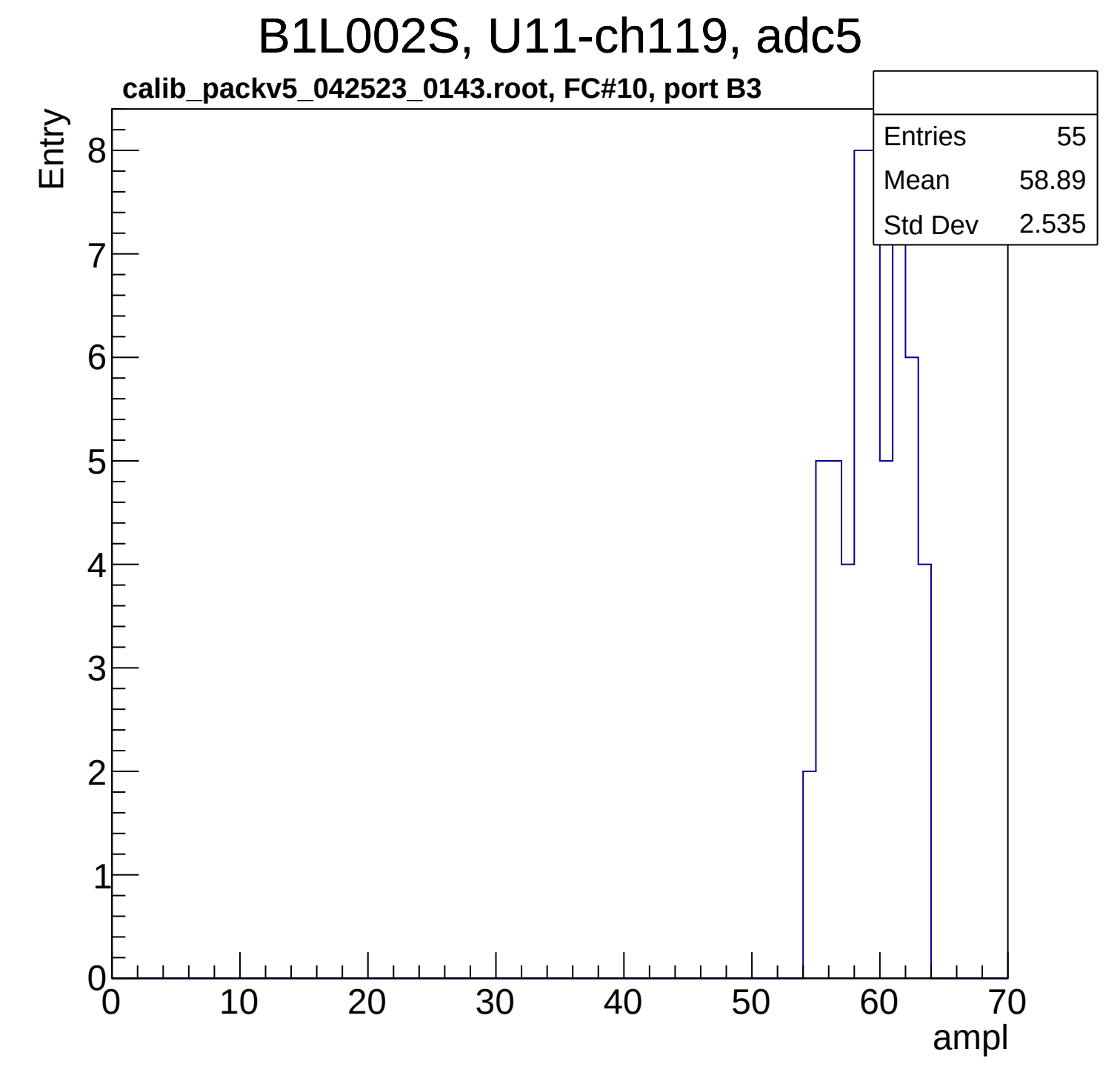
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.89
Std Dev 2.535
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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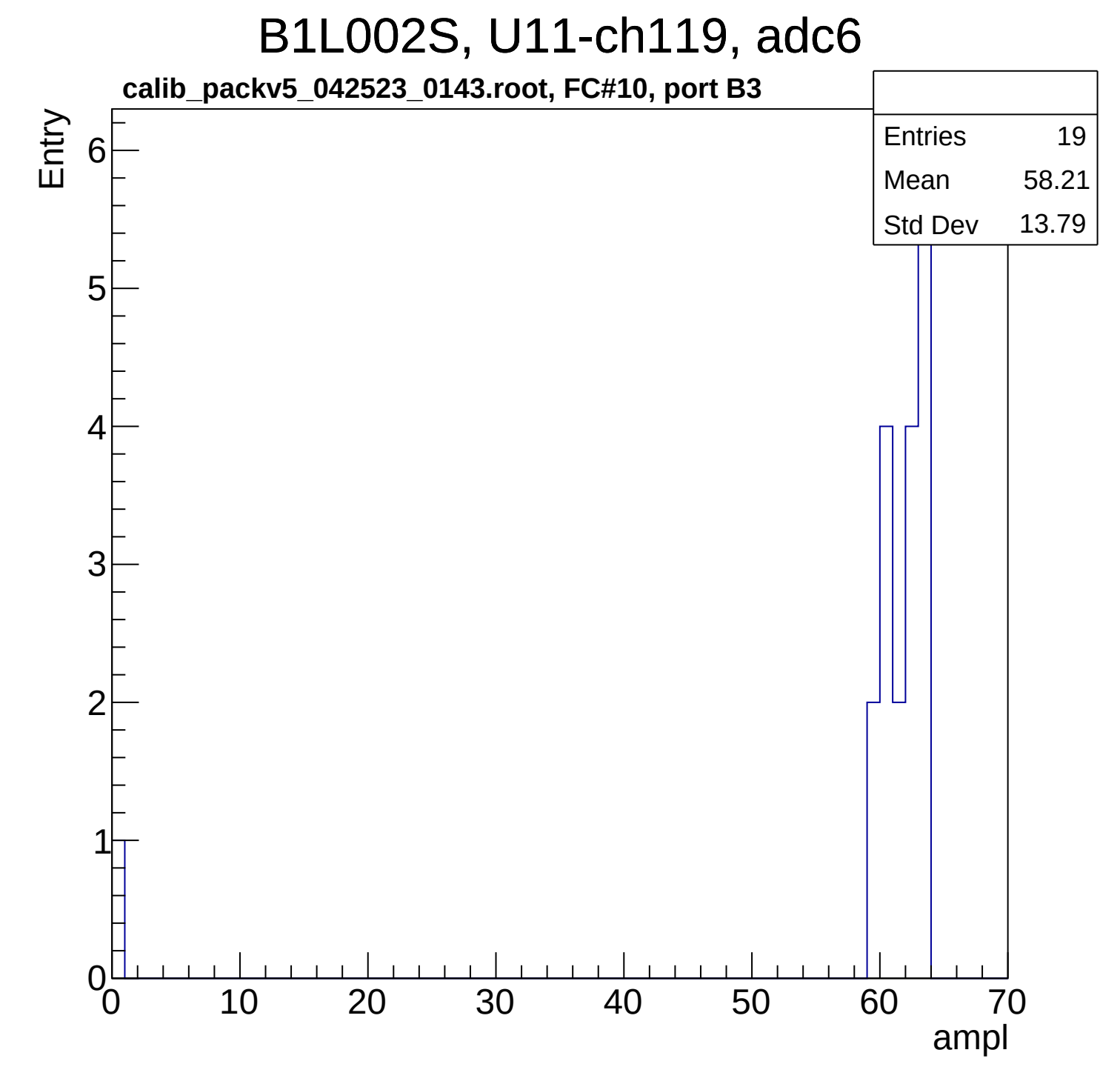
B1L002S, U11-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.21
Std Dev 13.79
Entry
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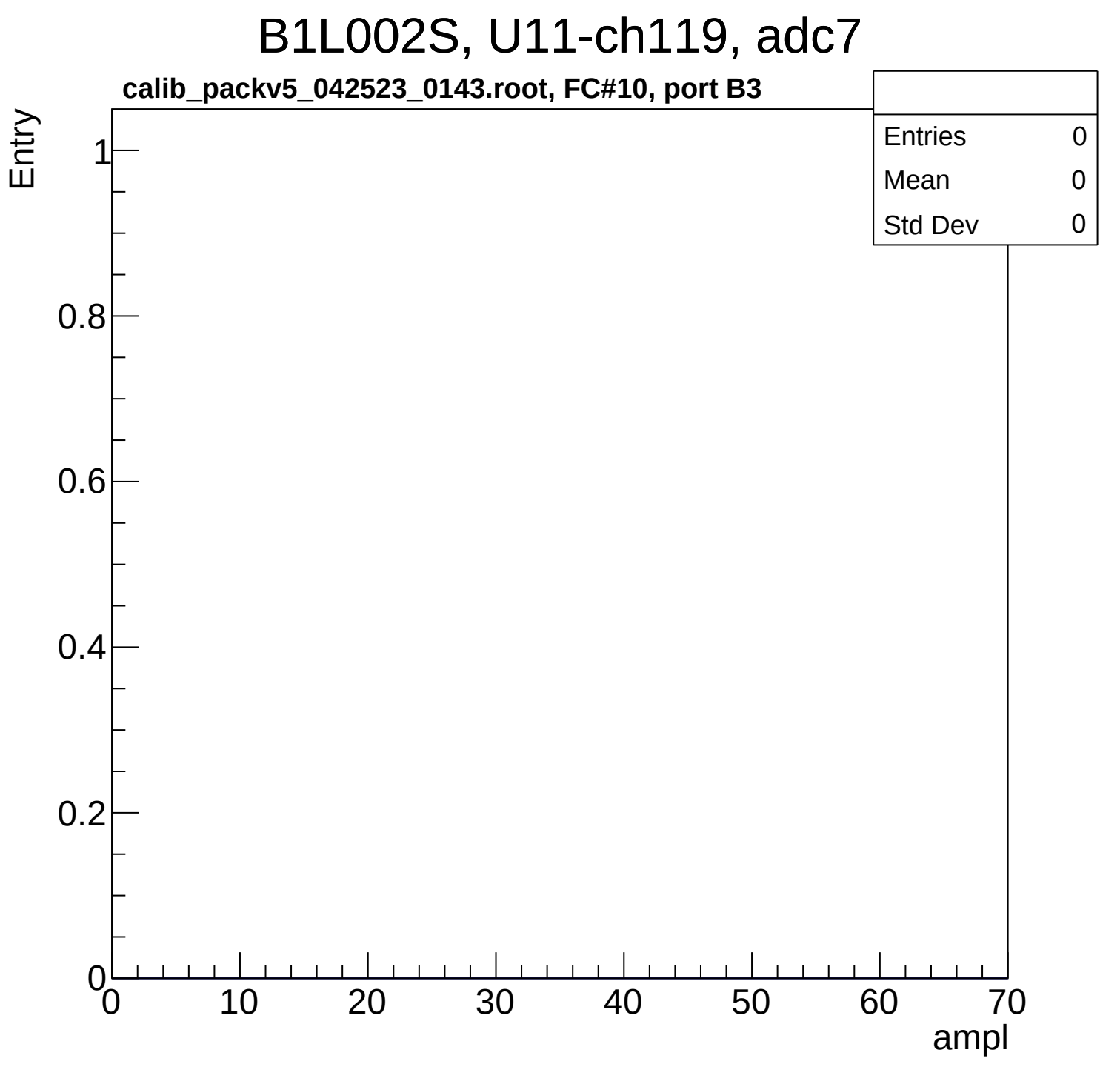
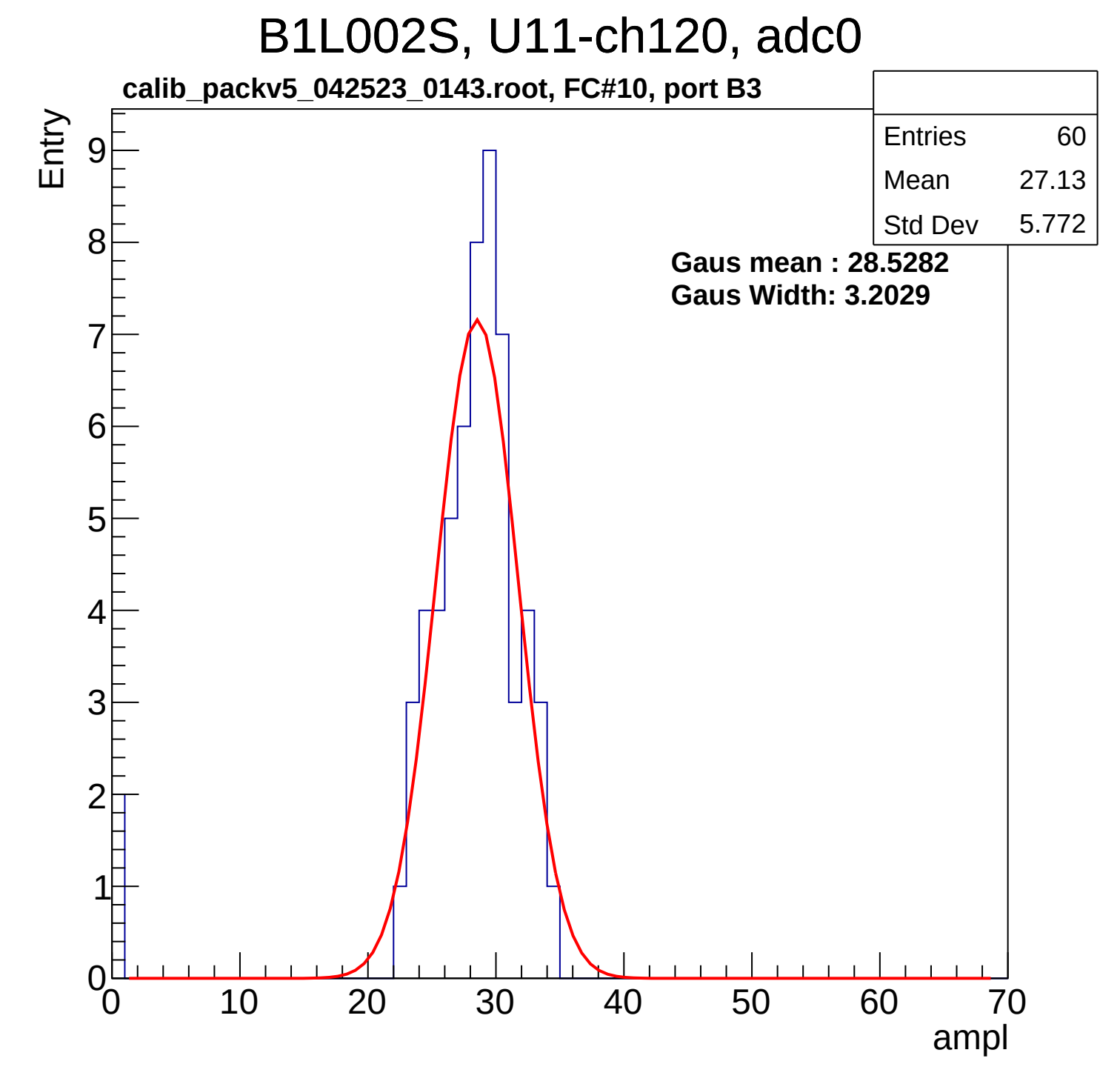
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.13
Std Dev 5.772
Gaus mean : 28.5282
Gaus Width: 3.2029
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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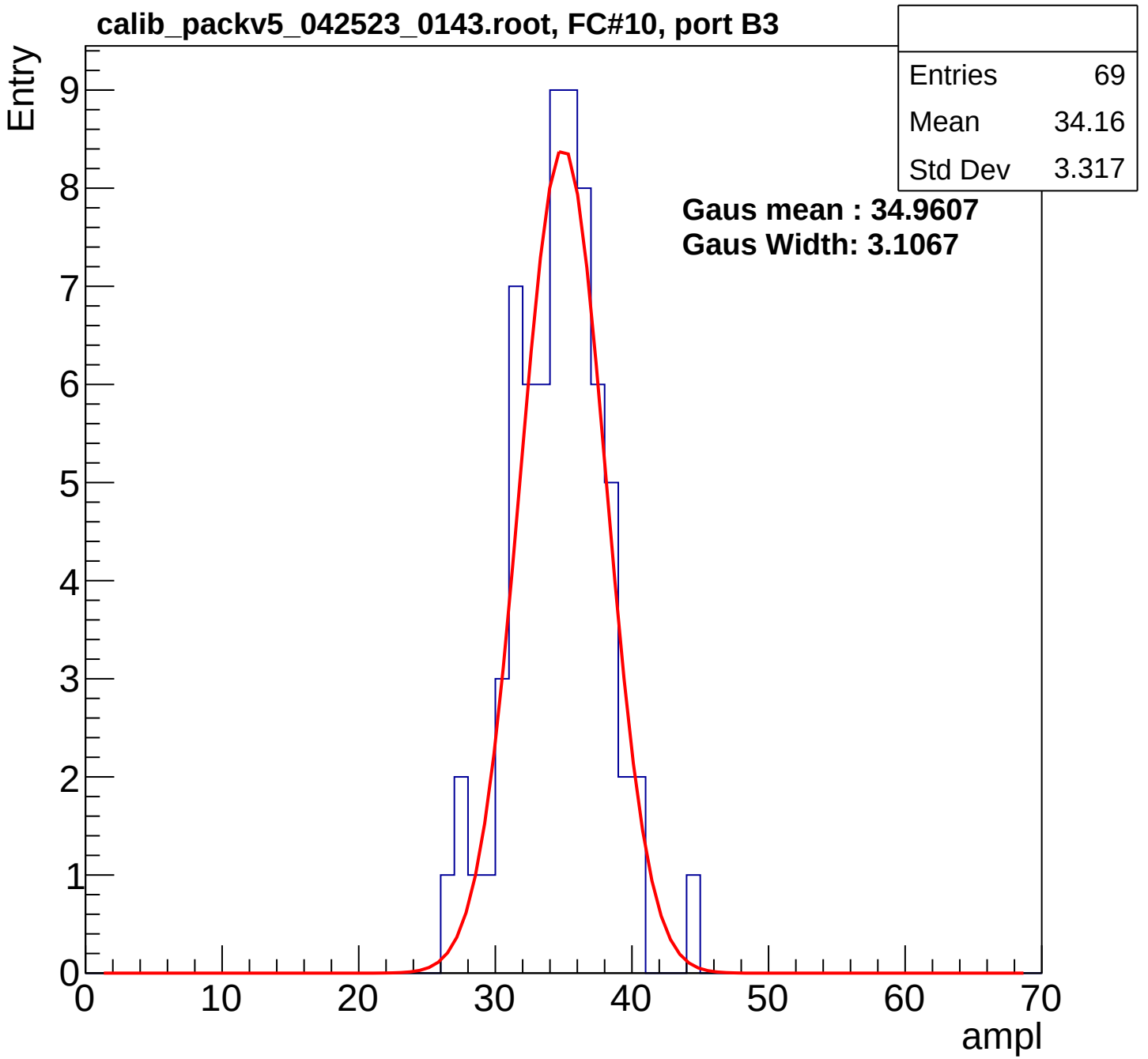
B1L002S, U11-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.16
Std Dev 3.317
Gaus mean : 34.9607
Gaus Width: 3.1067
Entry
ampl
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2
3
4
5
6
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8
9
0
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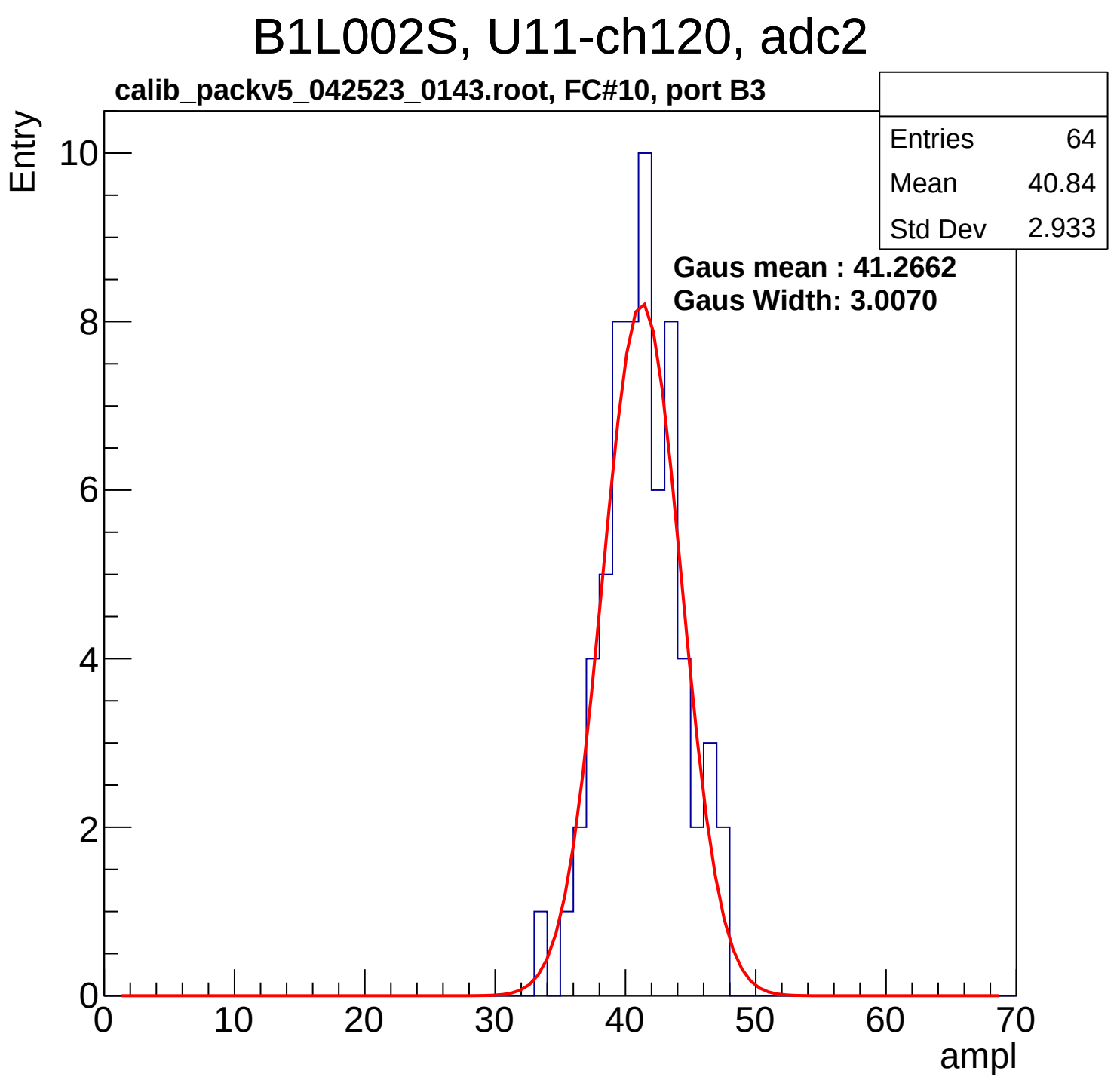
B1L002S, U11-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.84
Std Dev 2.933
Gaus mean : 41.2662
Gaus Width: 3.0070
Entry
ampl
0
2
4
6
8
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0
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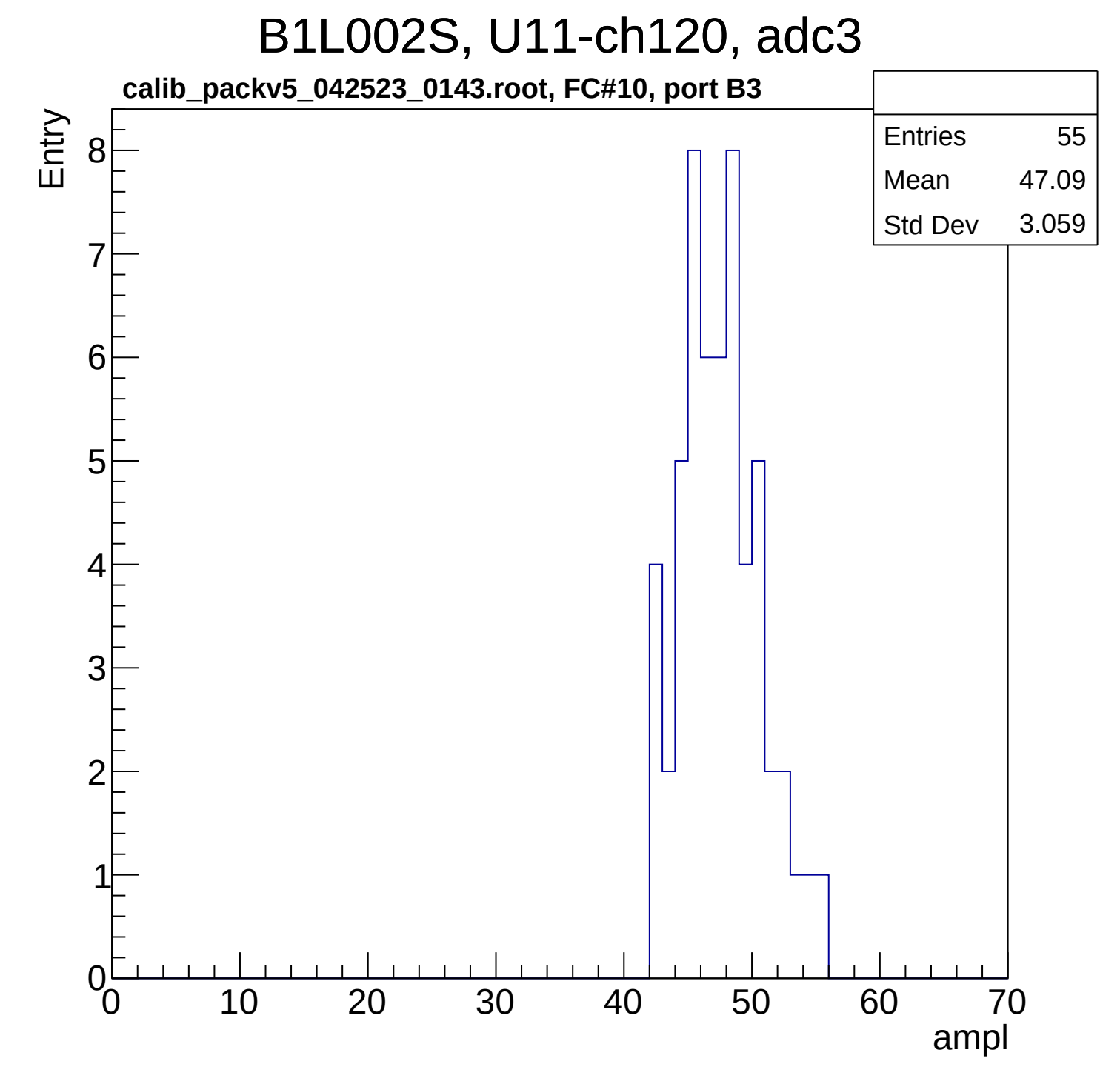
B1L002S, U11-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.09
Std Dev 3.059
Entry
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2
3
4
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6
7
8
0
10
20
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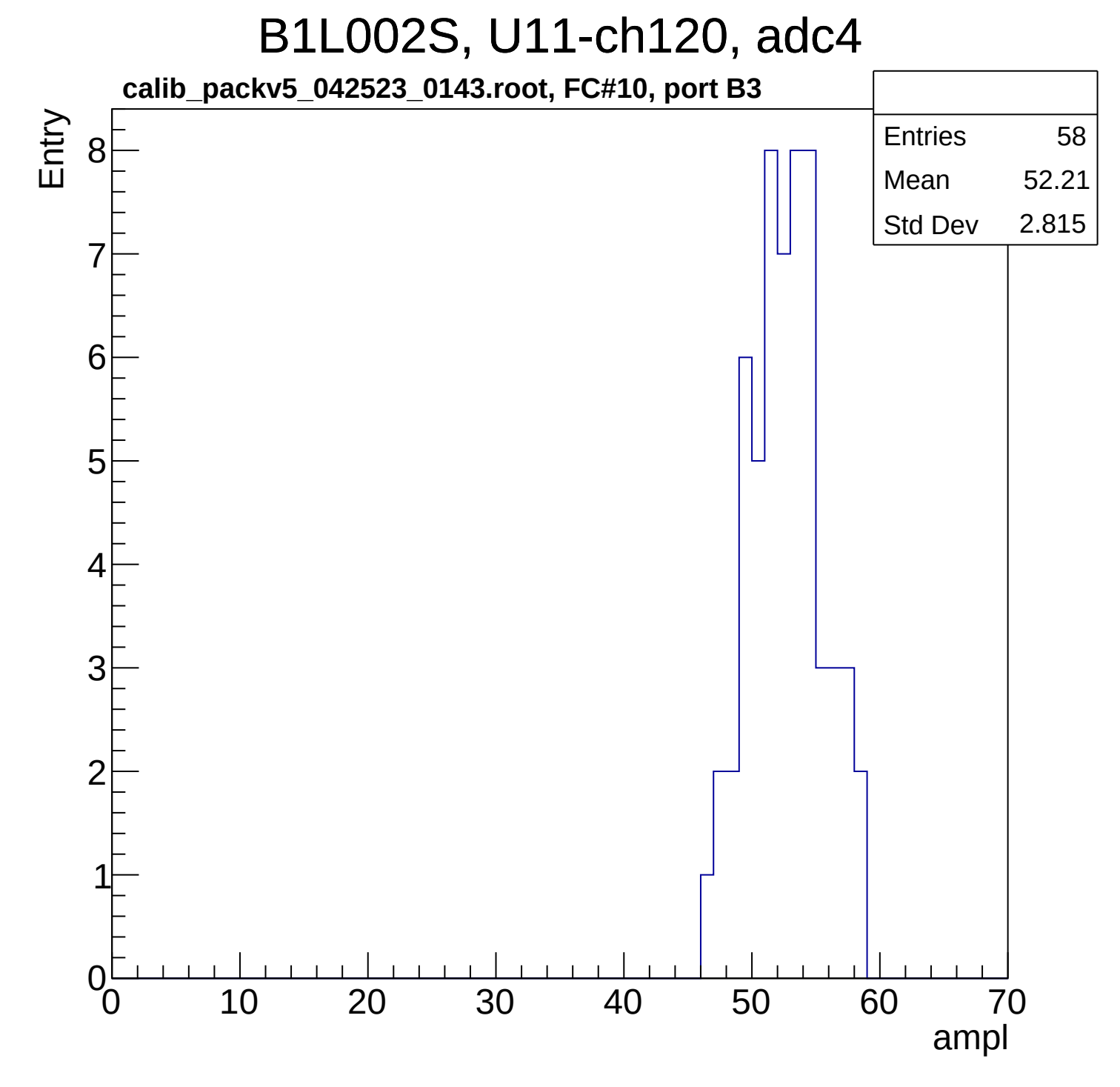
50
60
70
ampl


B1L002S, U11-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.21
Std Dev 2.815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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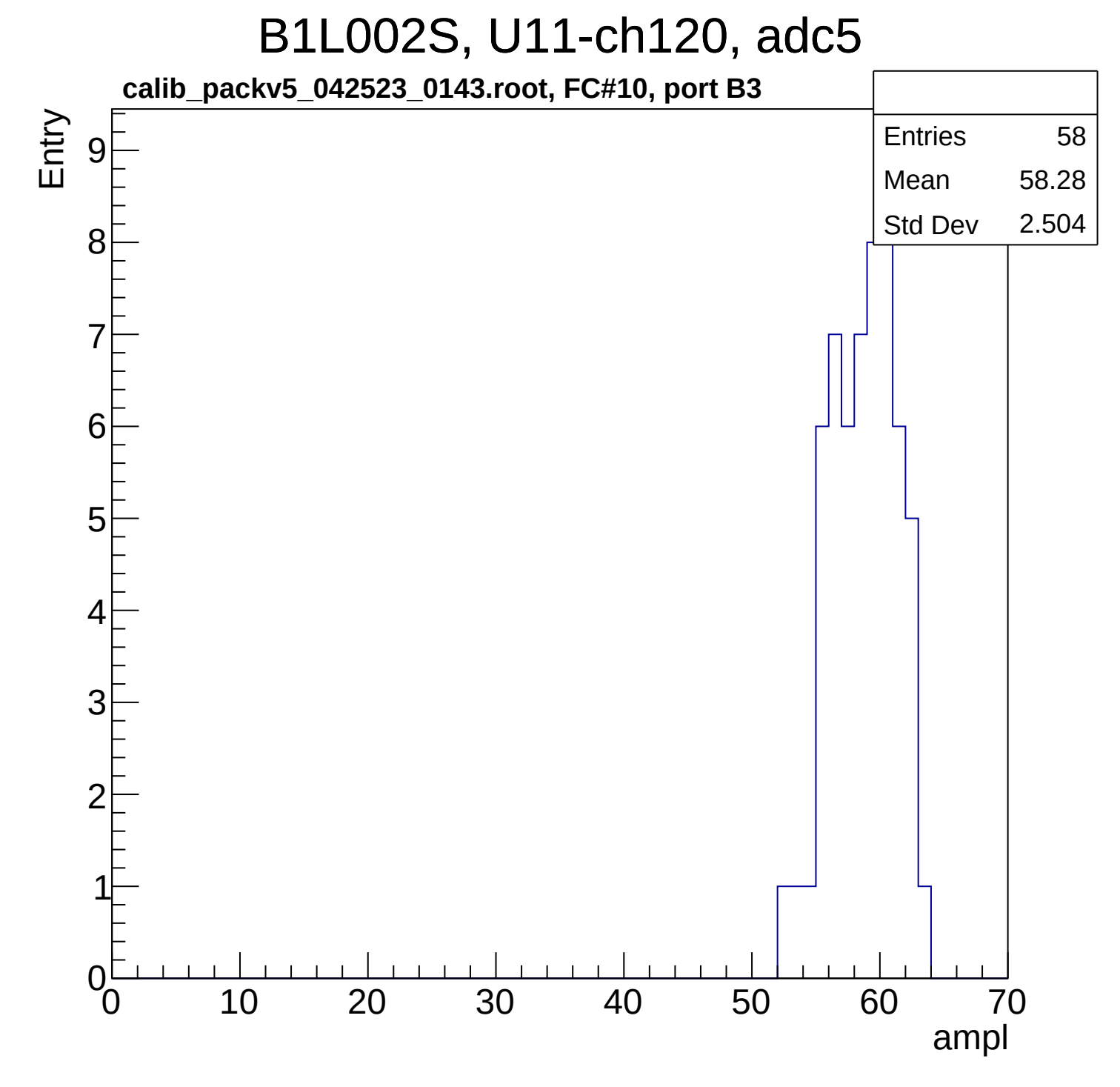
B1L002S, U11-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.28
Std Dev 2.504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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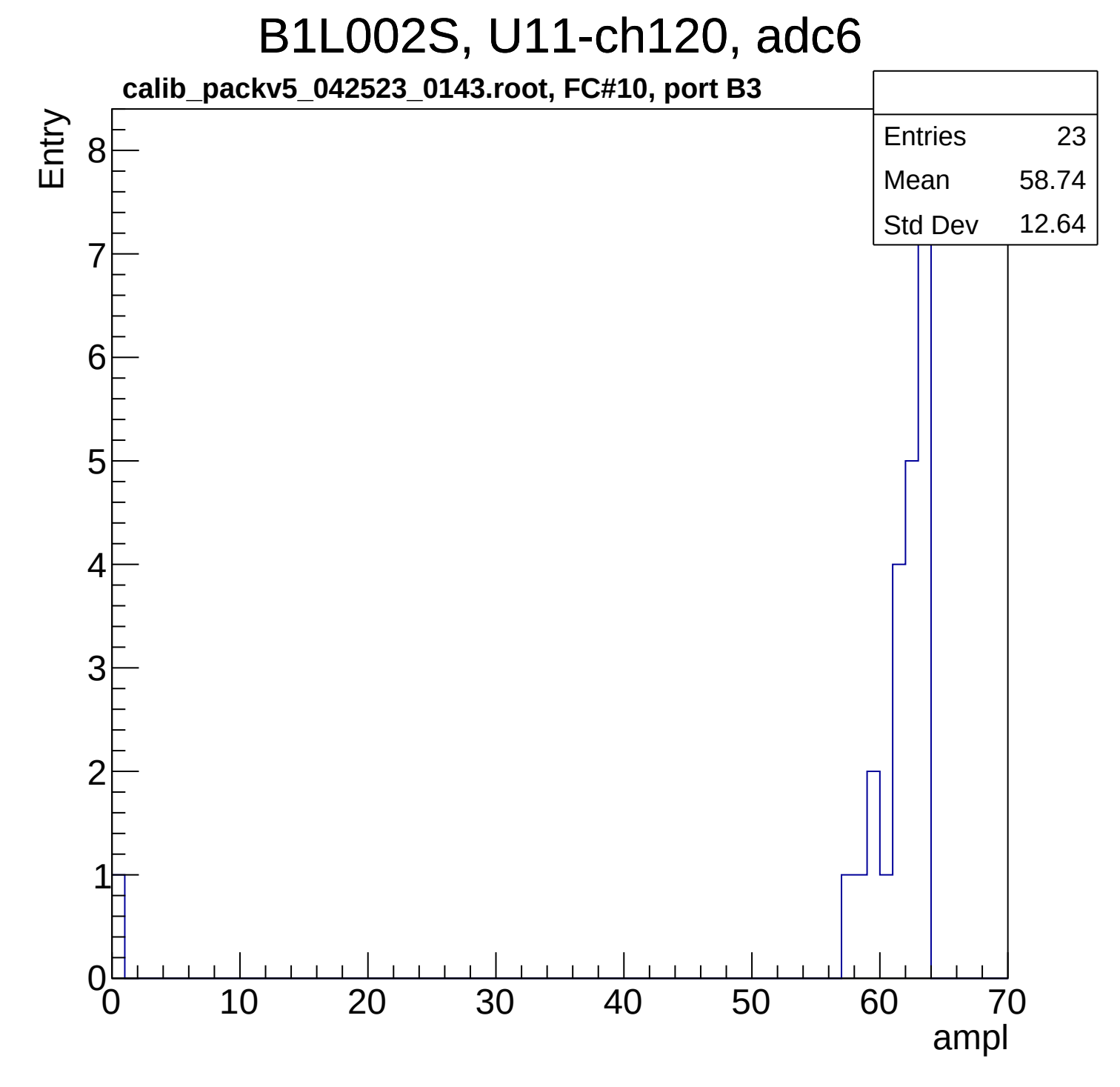
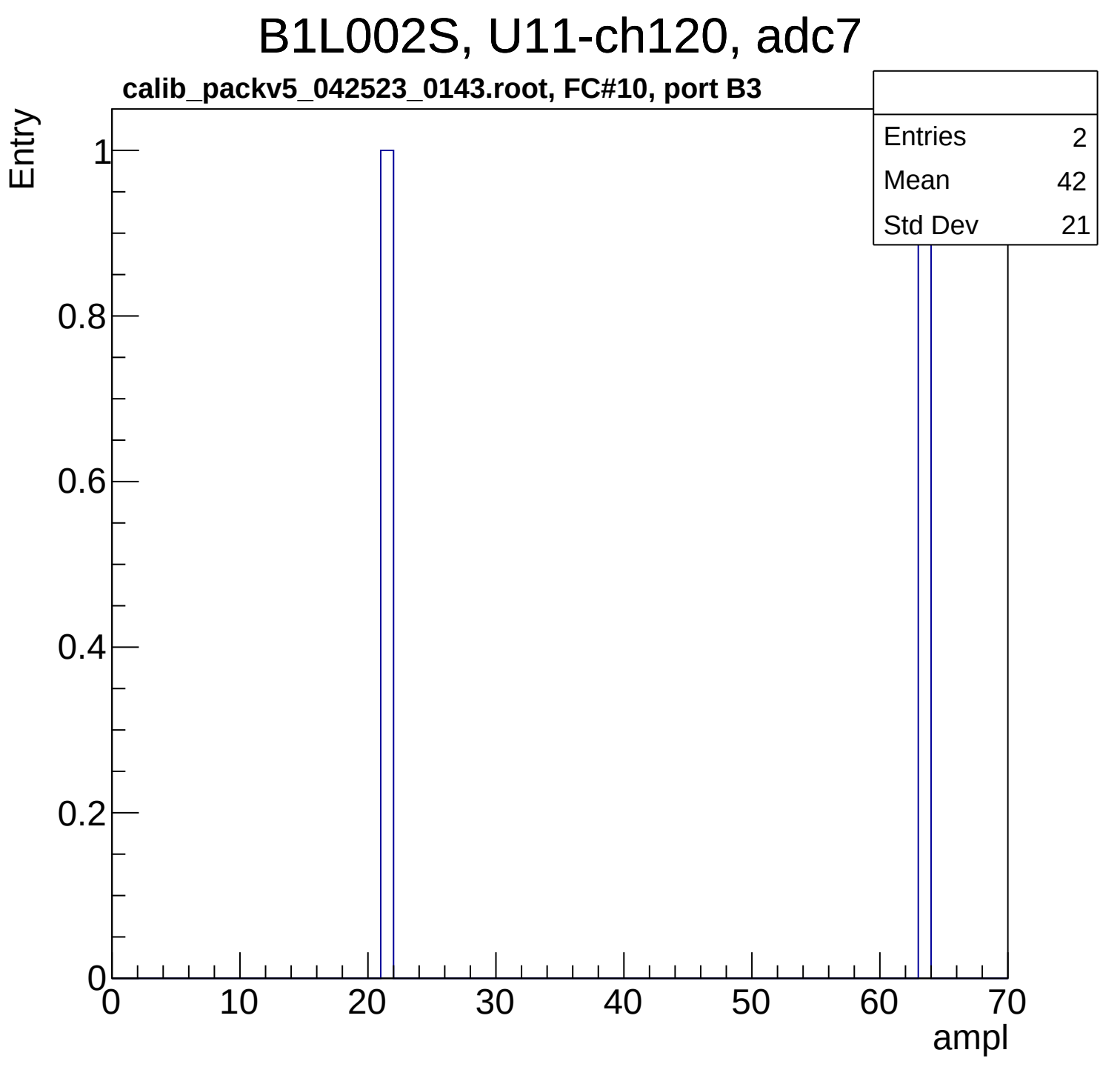
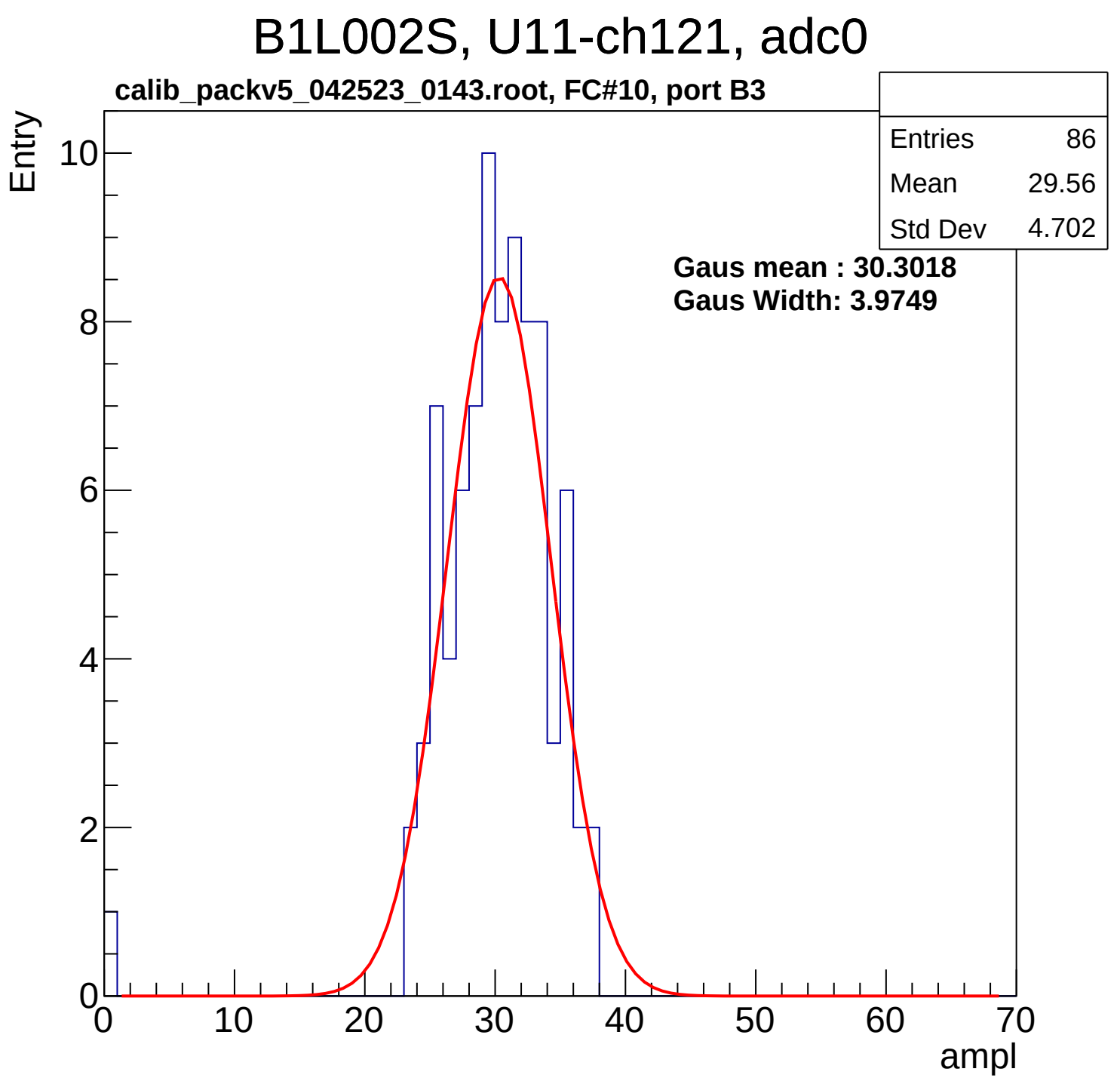
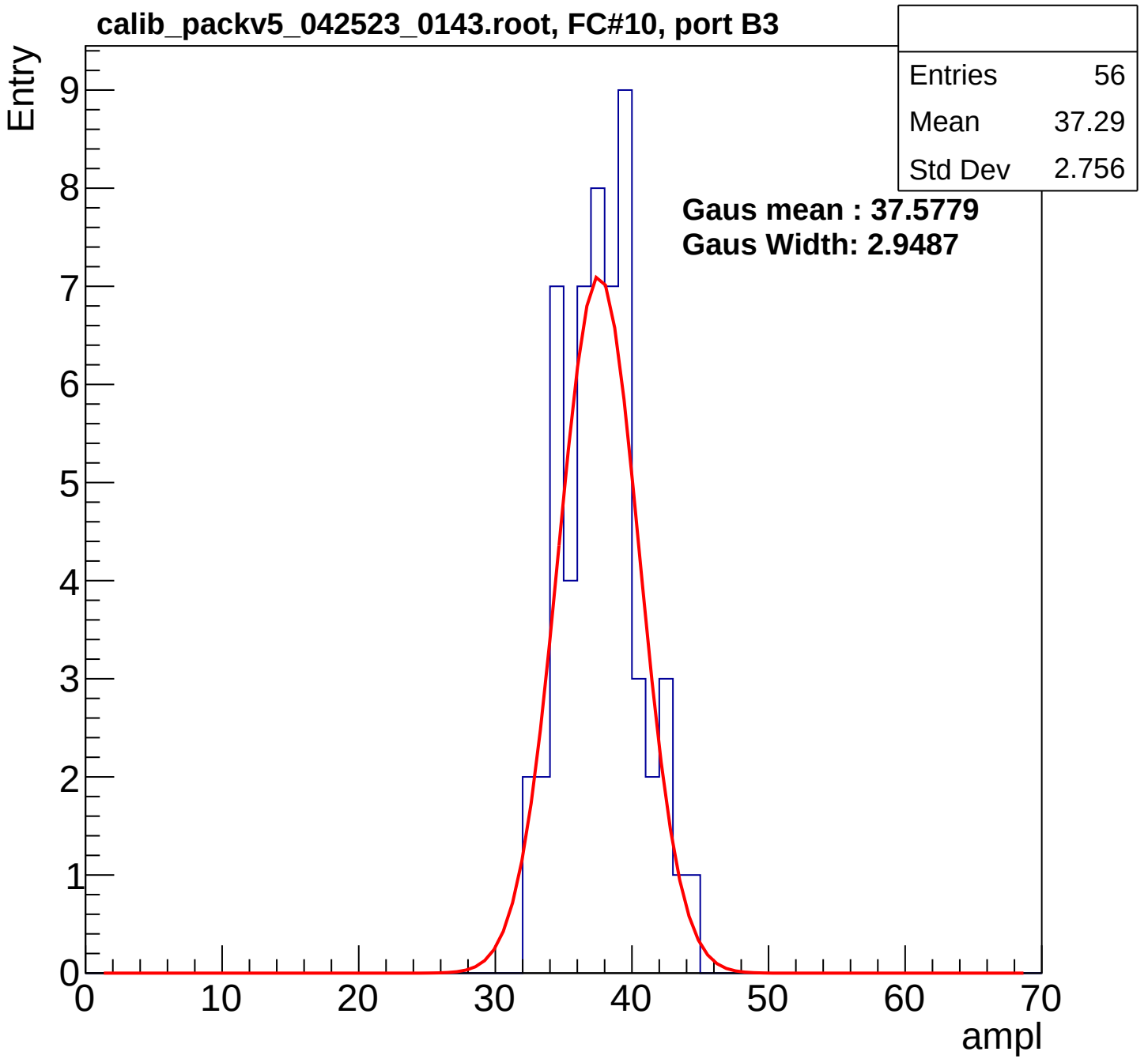

B1L002S, U11-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.74
Std Dev 12.64
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 21
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U11-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 29.56
Std Dev 4.702
Gaus mean : 30.3018
Gaus Width: 3.9749
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U11-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 37.29
Std Dev 2.756
Gaus mean : 37.5779
Gaus Width: 2.9487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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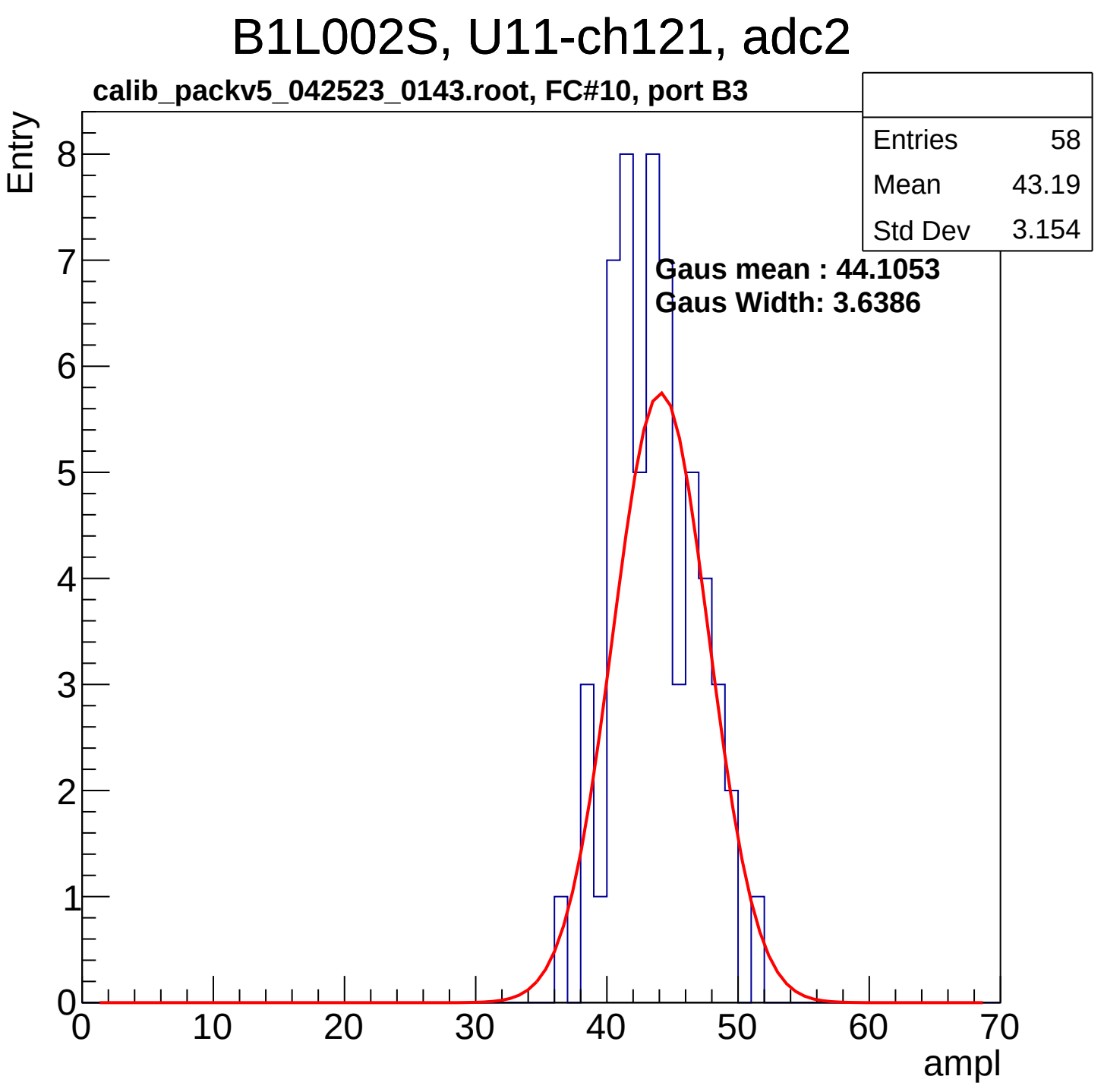
B1L002S, U11-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 43.19
Std Dev 3.154
Gaus mean : 44.1053
Gaus Width: 3.6386
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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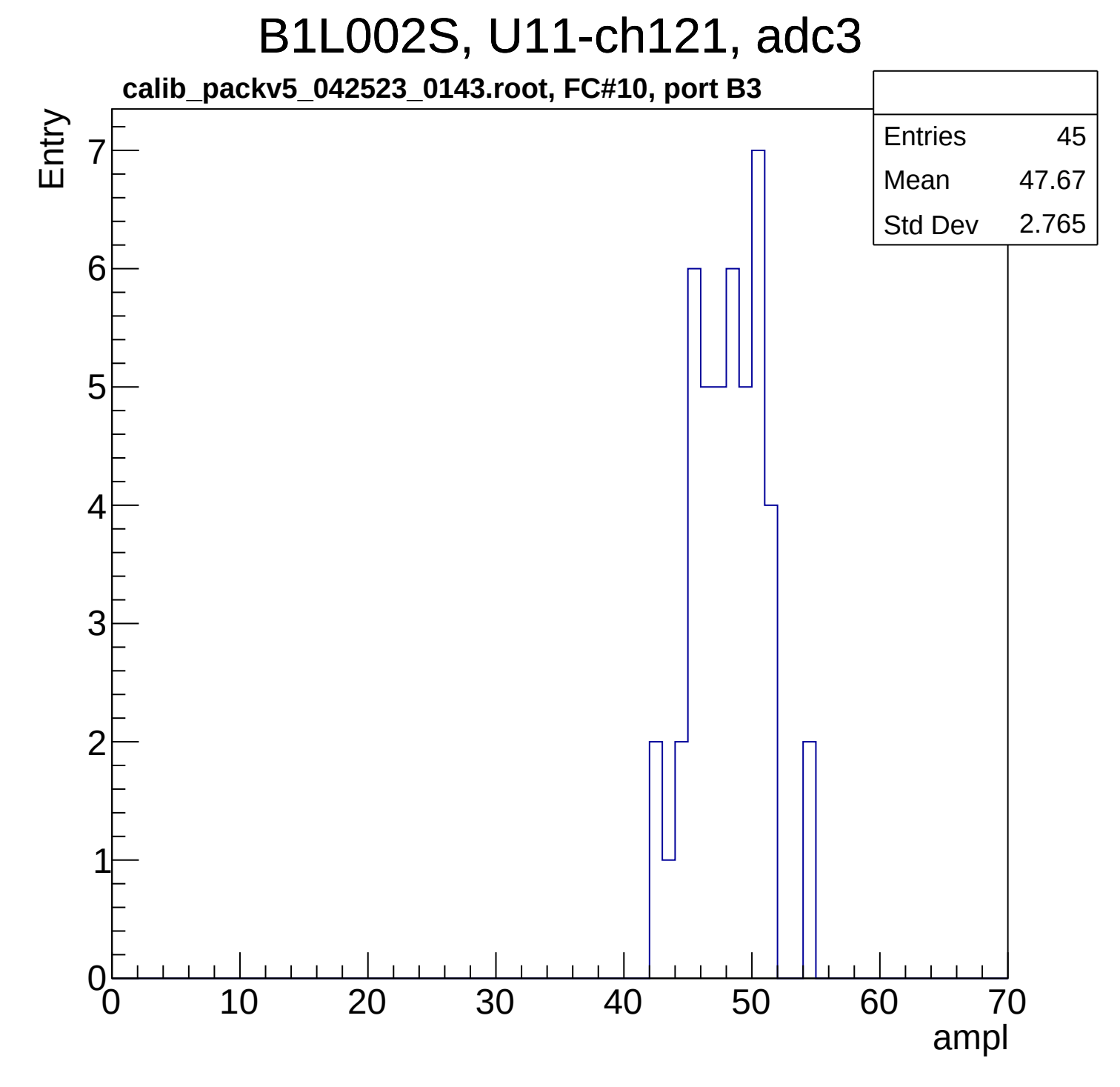
B1L002S, U11-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 47.67
Std Dev 2.765
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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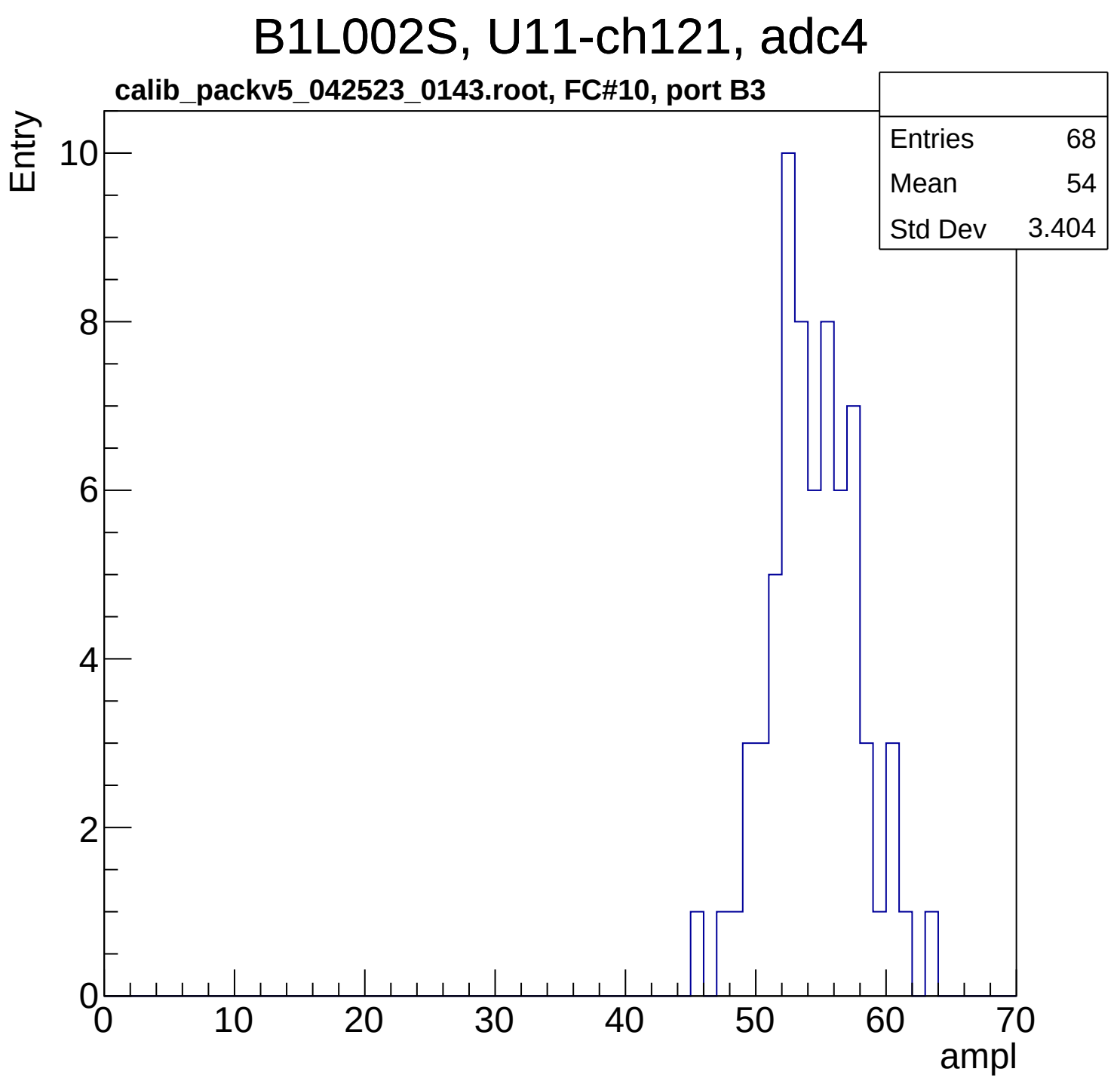
B1L002S, U11-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 54
Std Dev 3.404
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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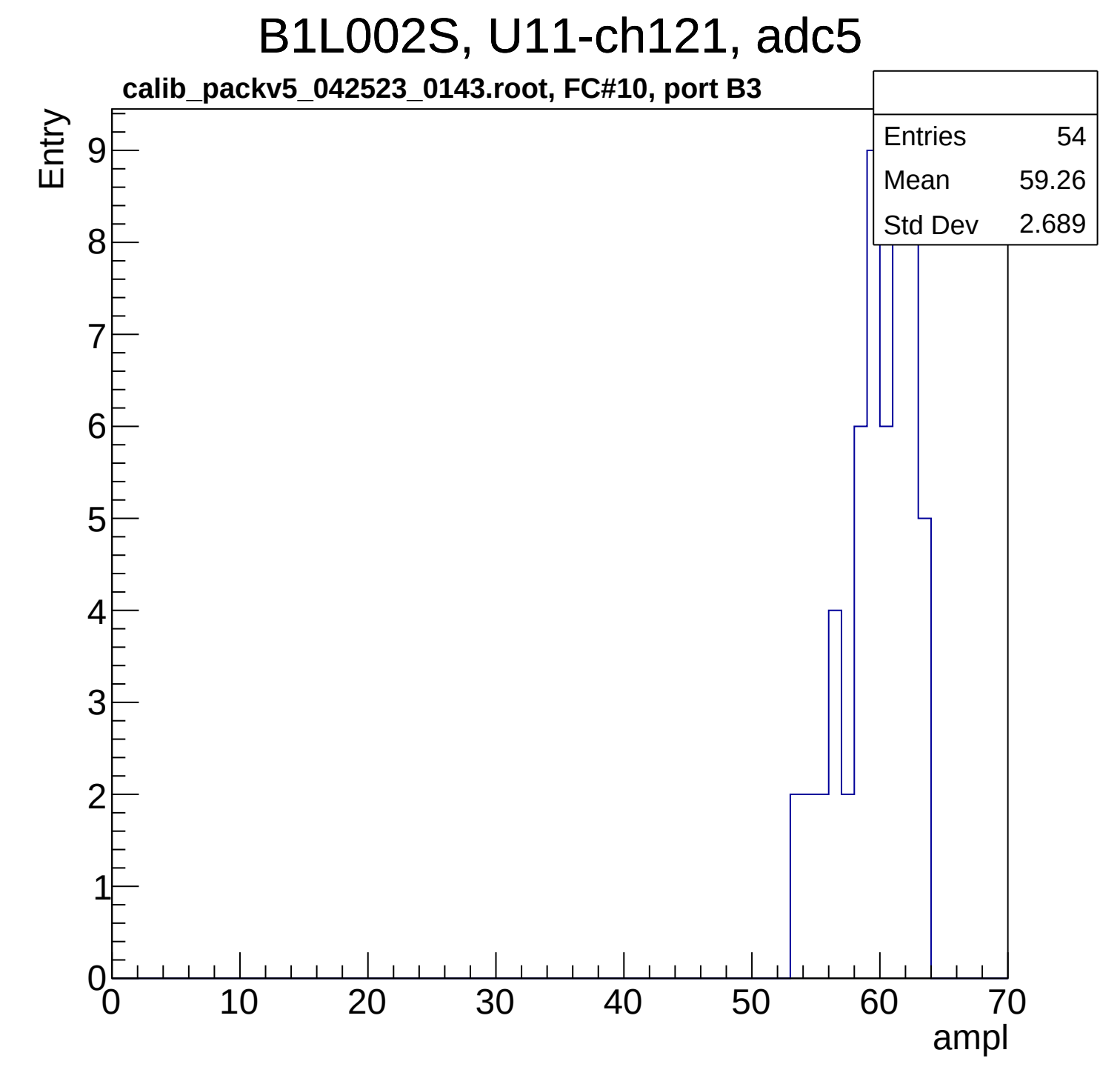
B1L002S, U11-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 59.26
Std Dev 2.689
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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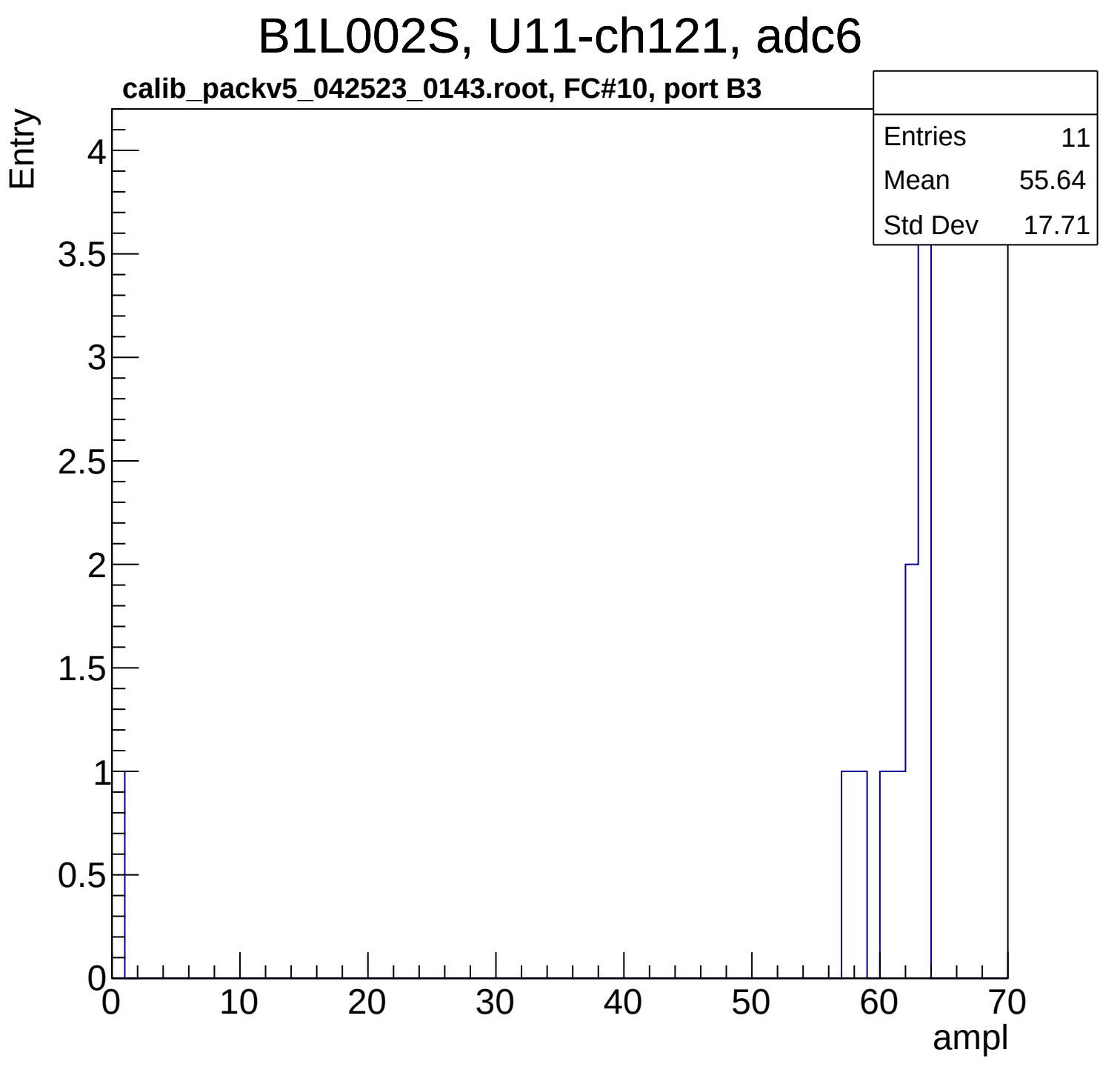
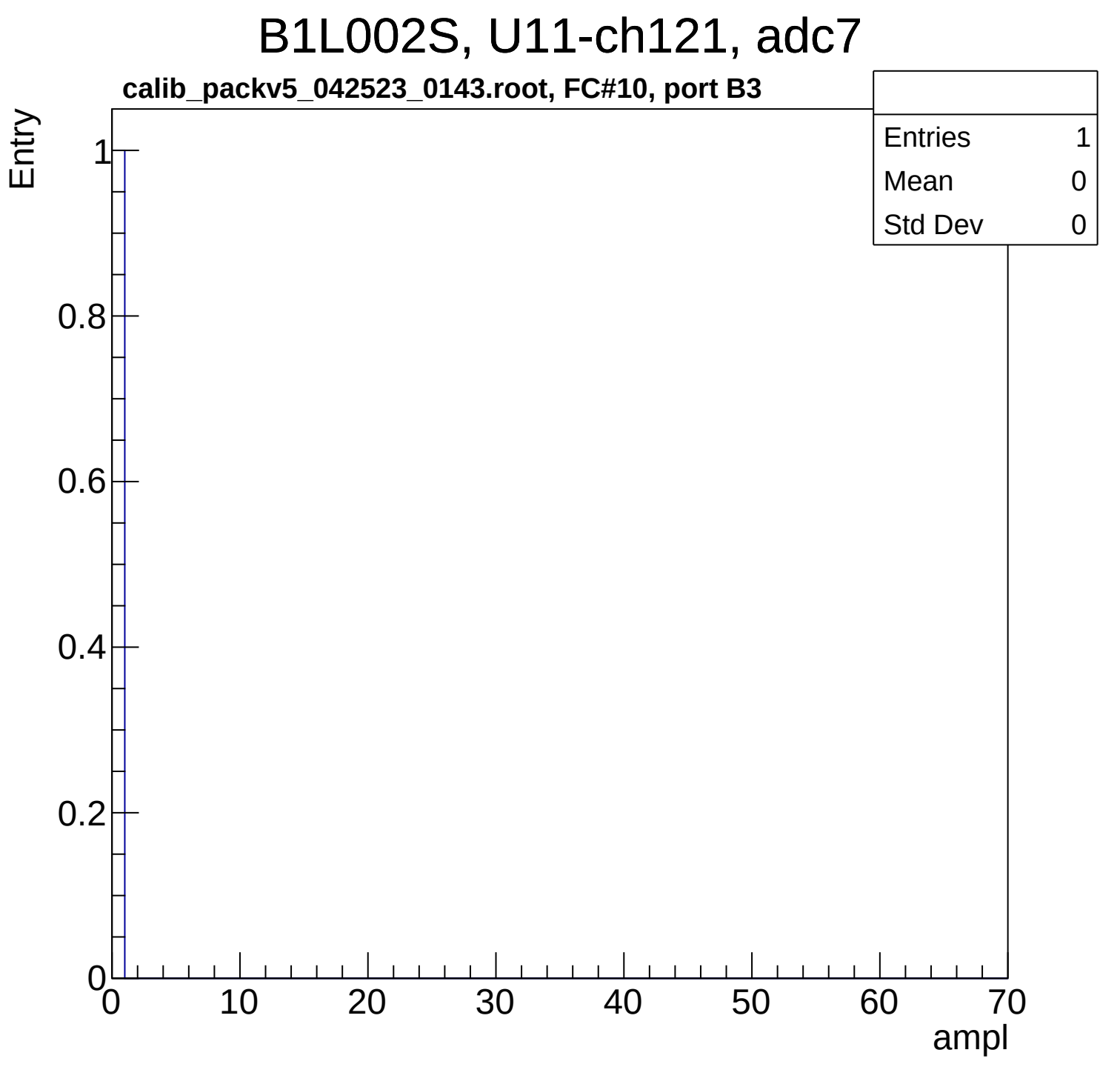
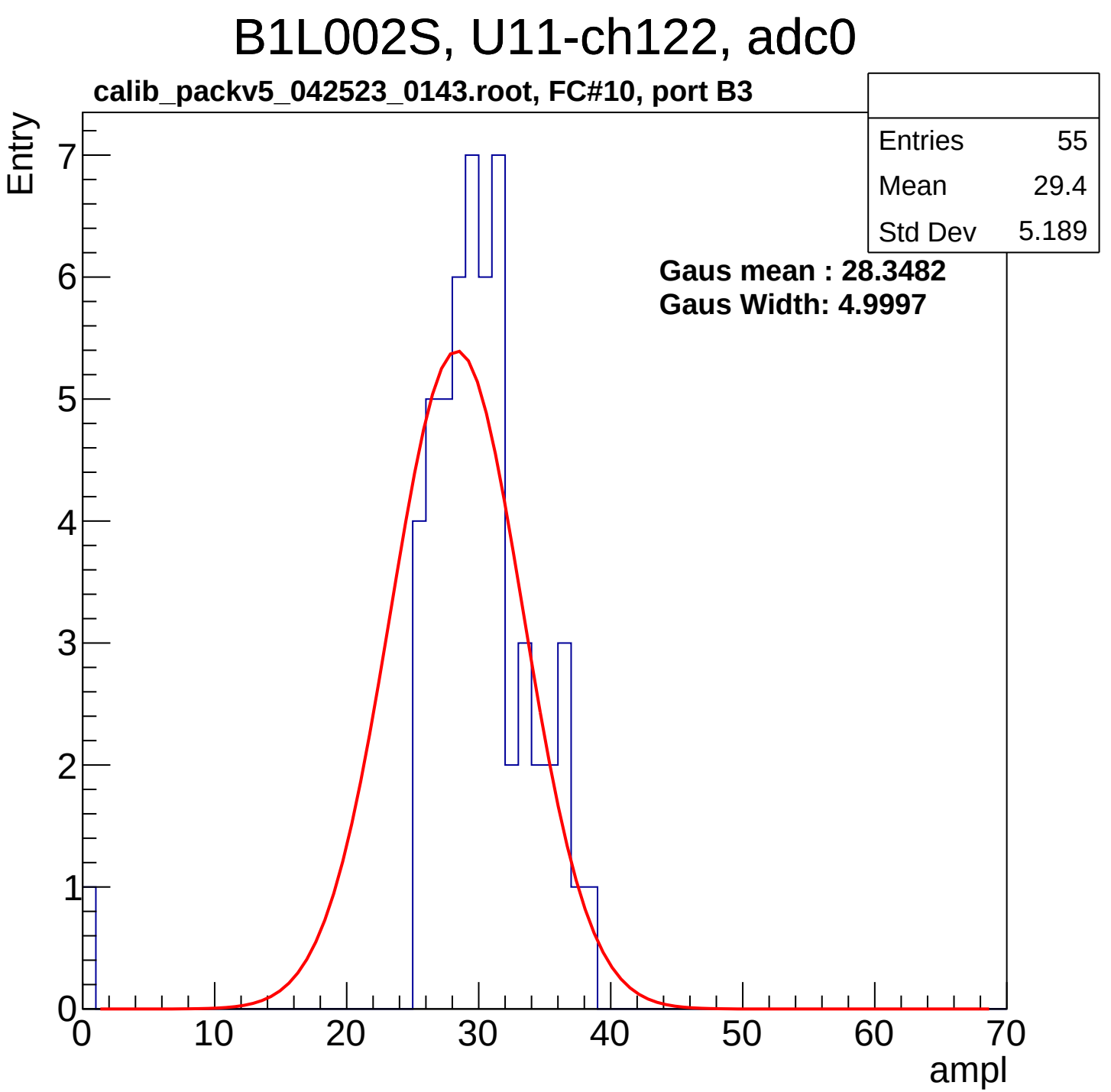
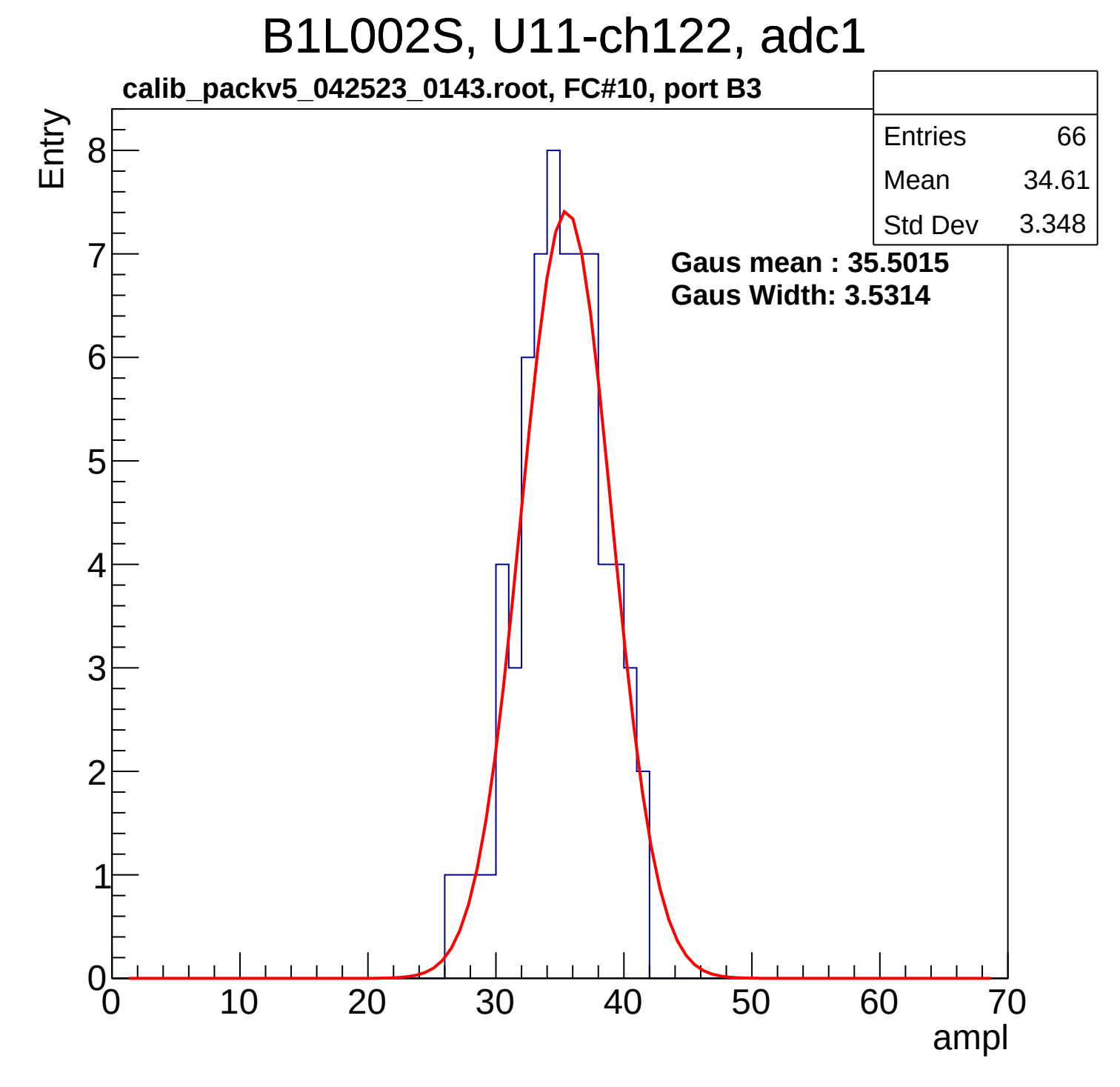
60
70


B1L002S, U11-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 55.64
Std Dev 17.71
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U11-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 29.4
Std Dev 5.189
Gaus mean : 28.3482
Gaus Width: 4.9997
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U11-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.61
Std Dev 3.348
Gaus mean : 35.5015
Gaus Width: 3.5314
Entry
ampl
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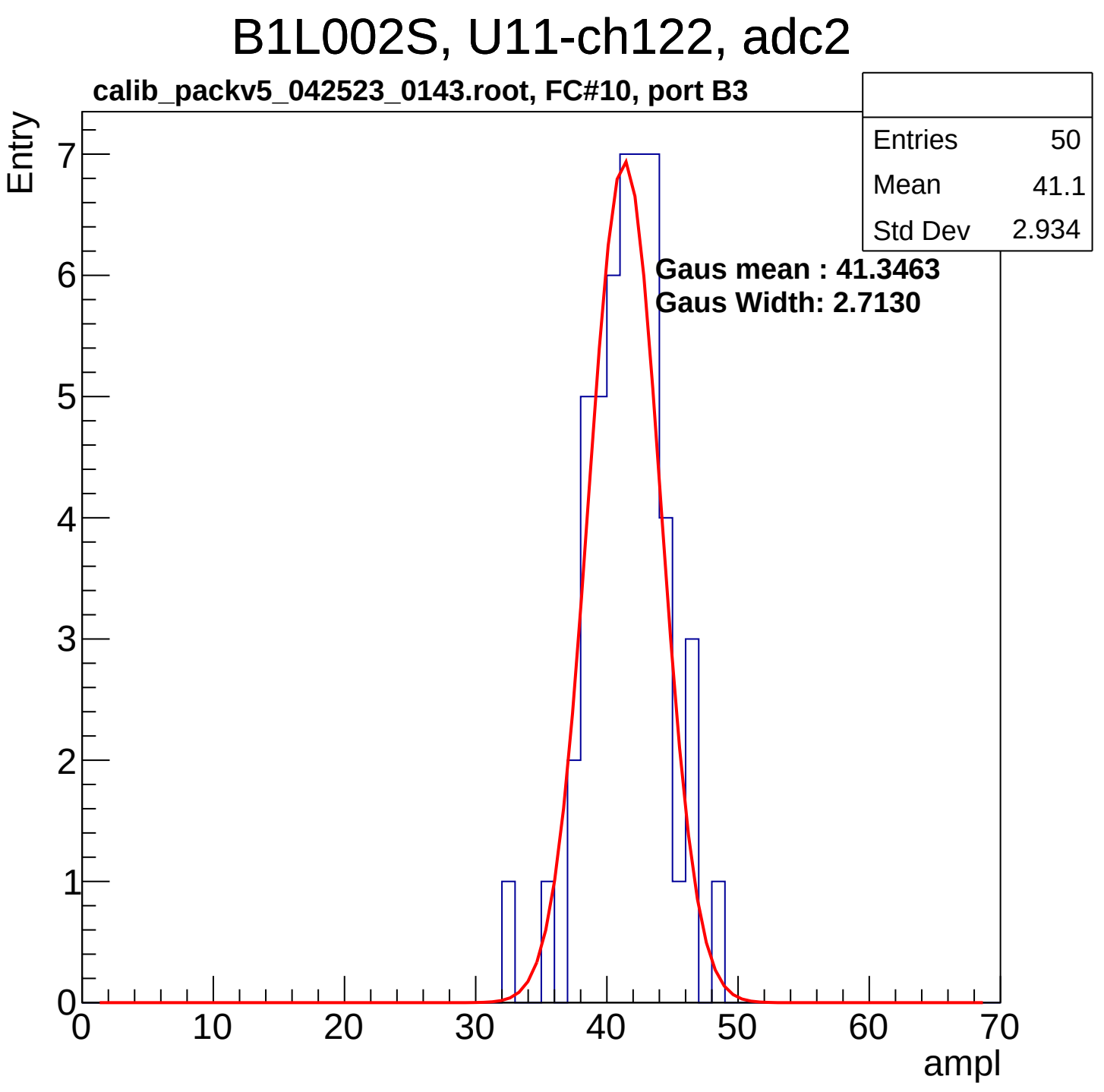
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.1
Std Dev 2.934
Gaus mean : 41.3463
Gaus Width: 2.7130
Entry
ampl
0
1
2
3
4
5
6
7
0
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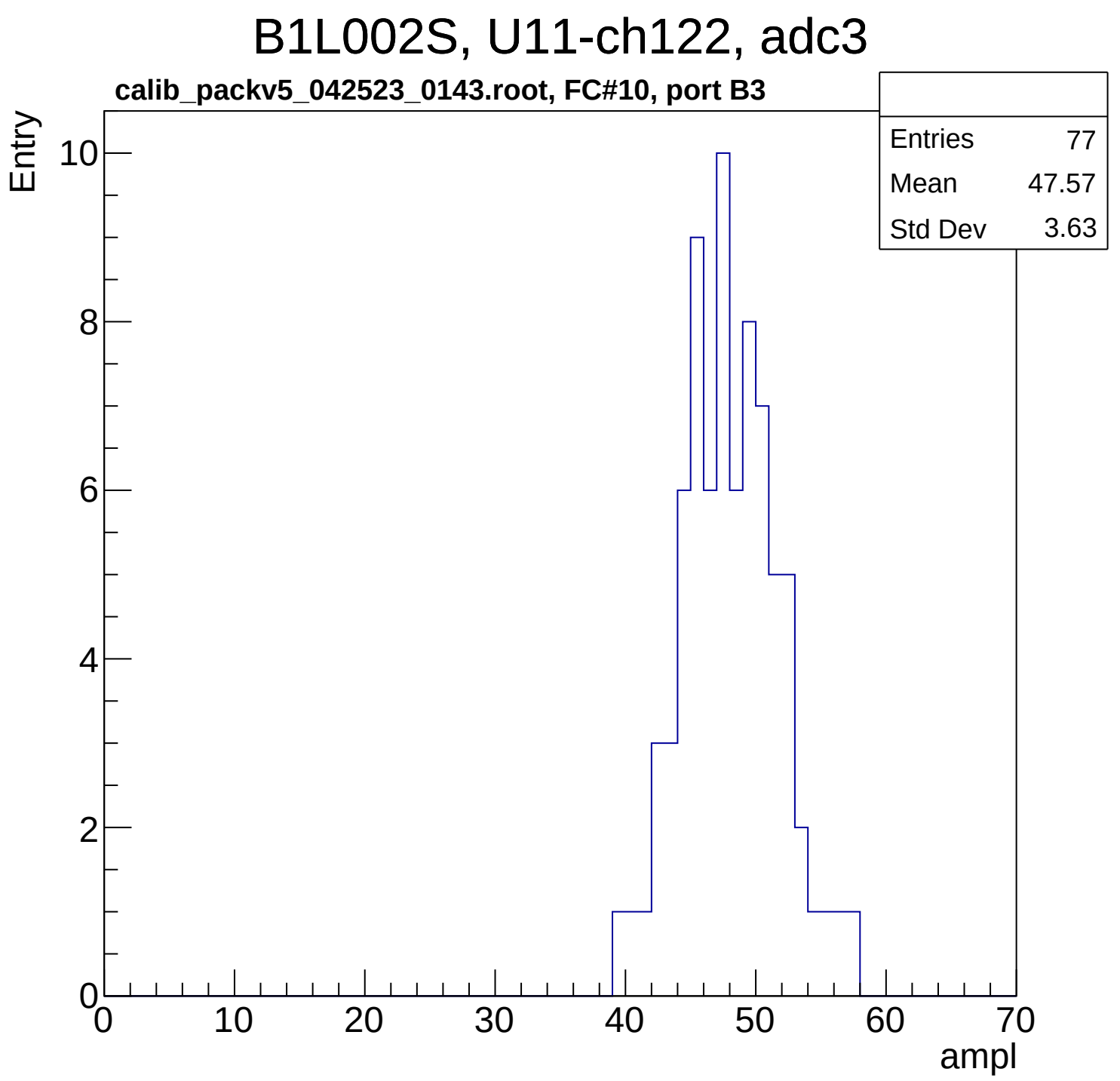
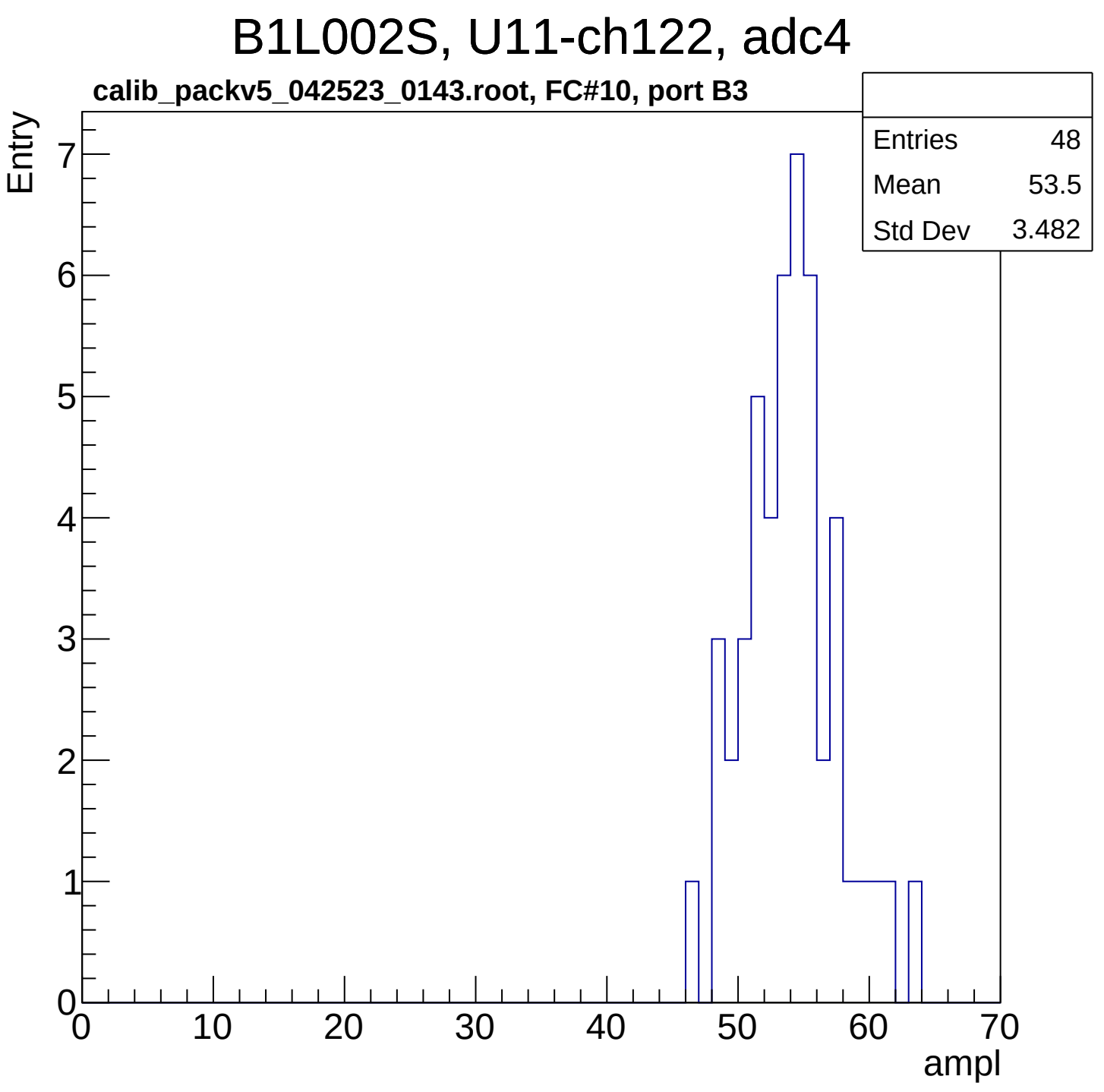
20
30
40
50
60
70


B1L002S, U11-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 47.57
Std Dev 3.63
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.5
Std Dev 3.482
Entry
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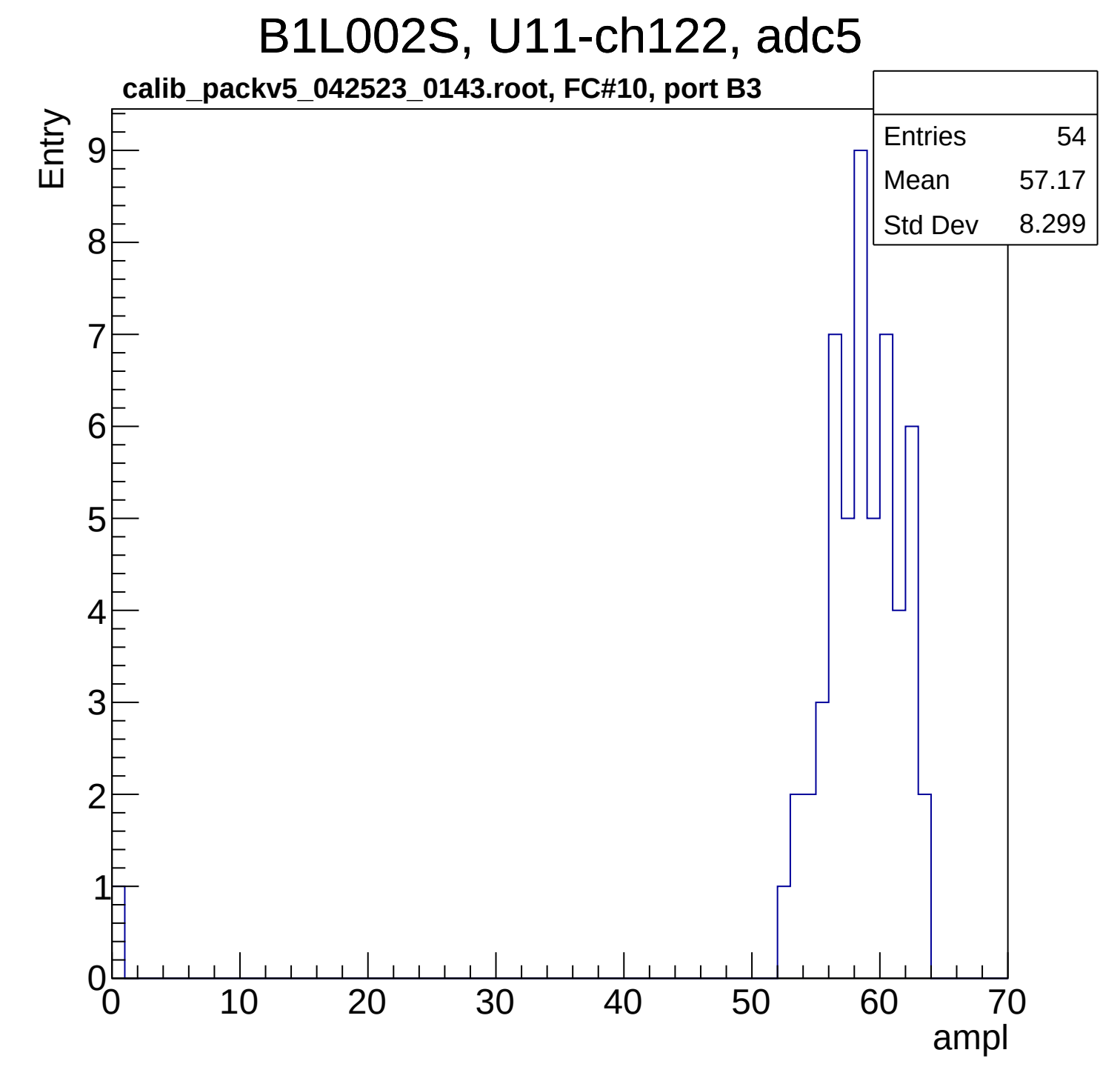
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.17
Std Dev 8.299
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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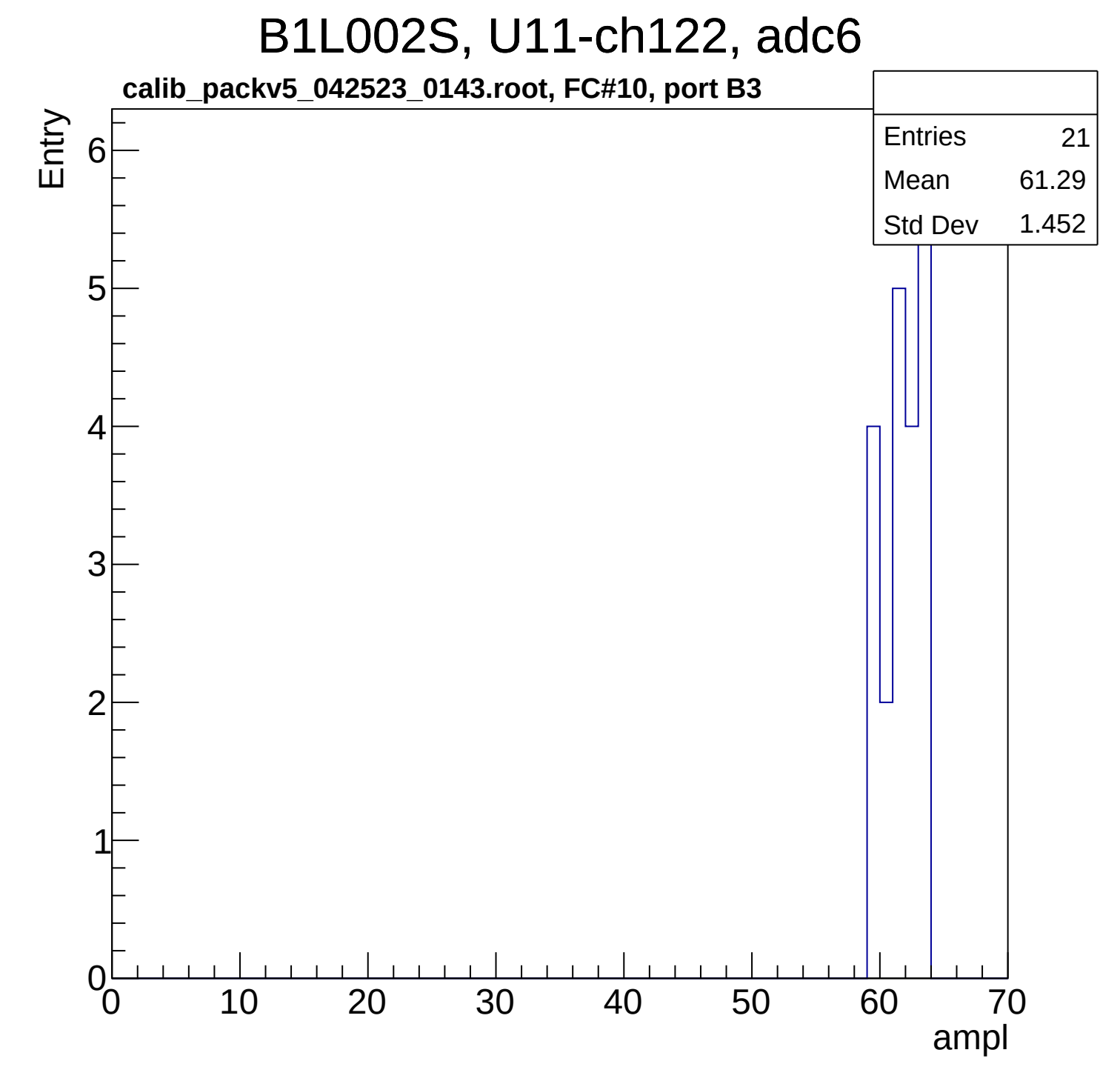
B1L002S, U11-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.29
Std Dev 1.452
Entry
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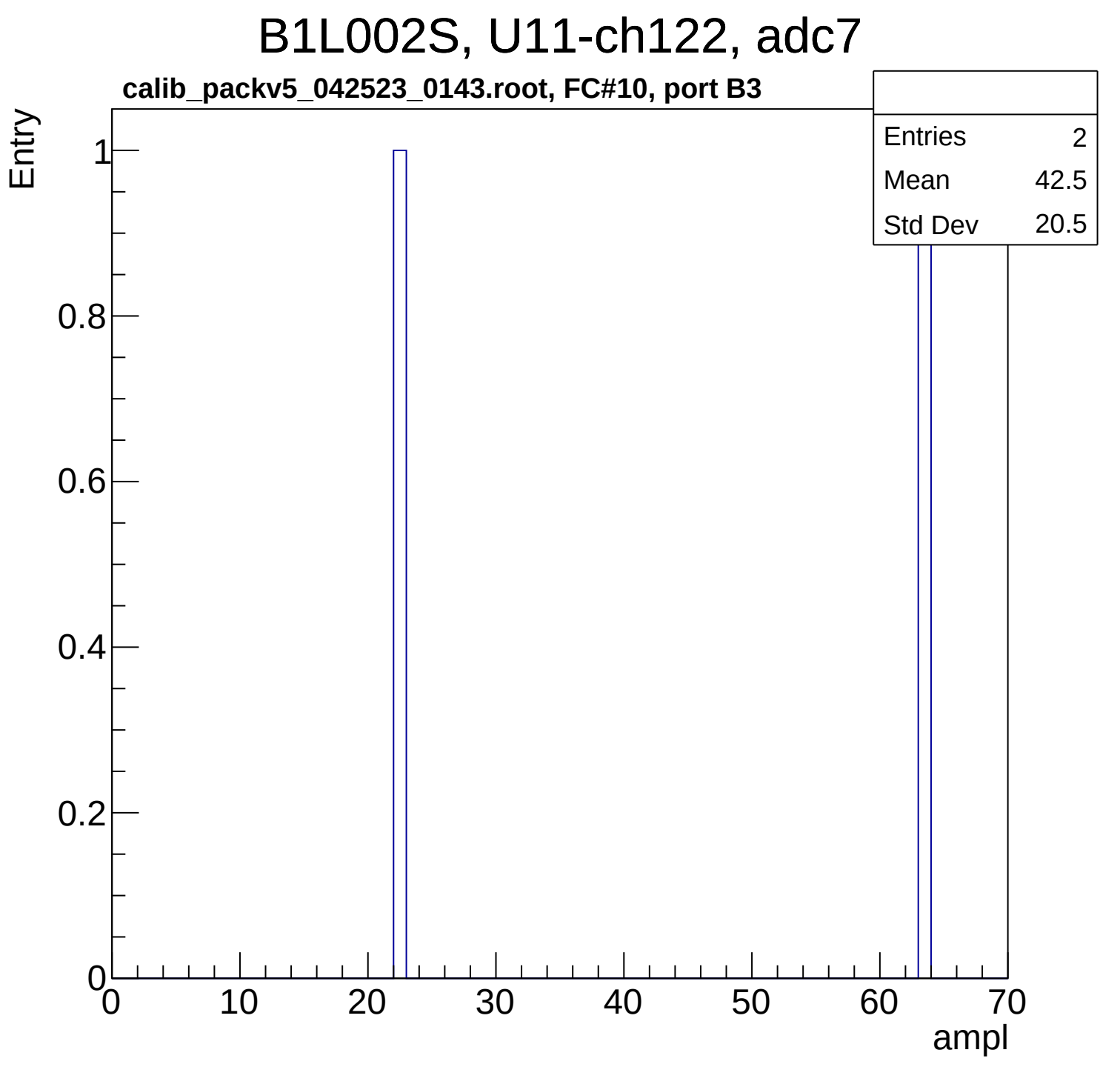
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
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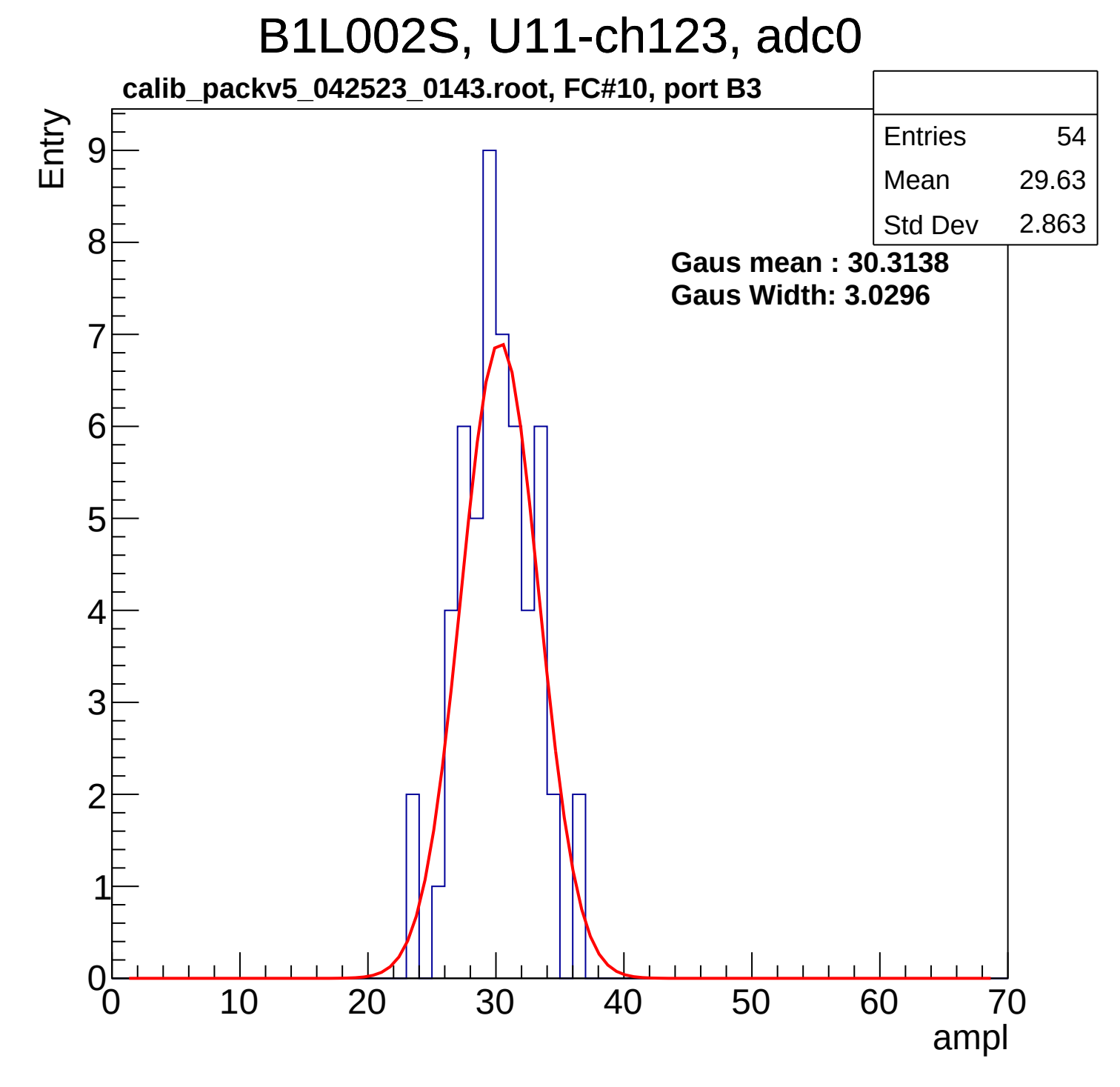
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 29.63
Std Dev 2.863
Gaus mean : 30.3138
Gaus Width: 3.0296
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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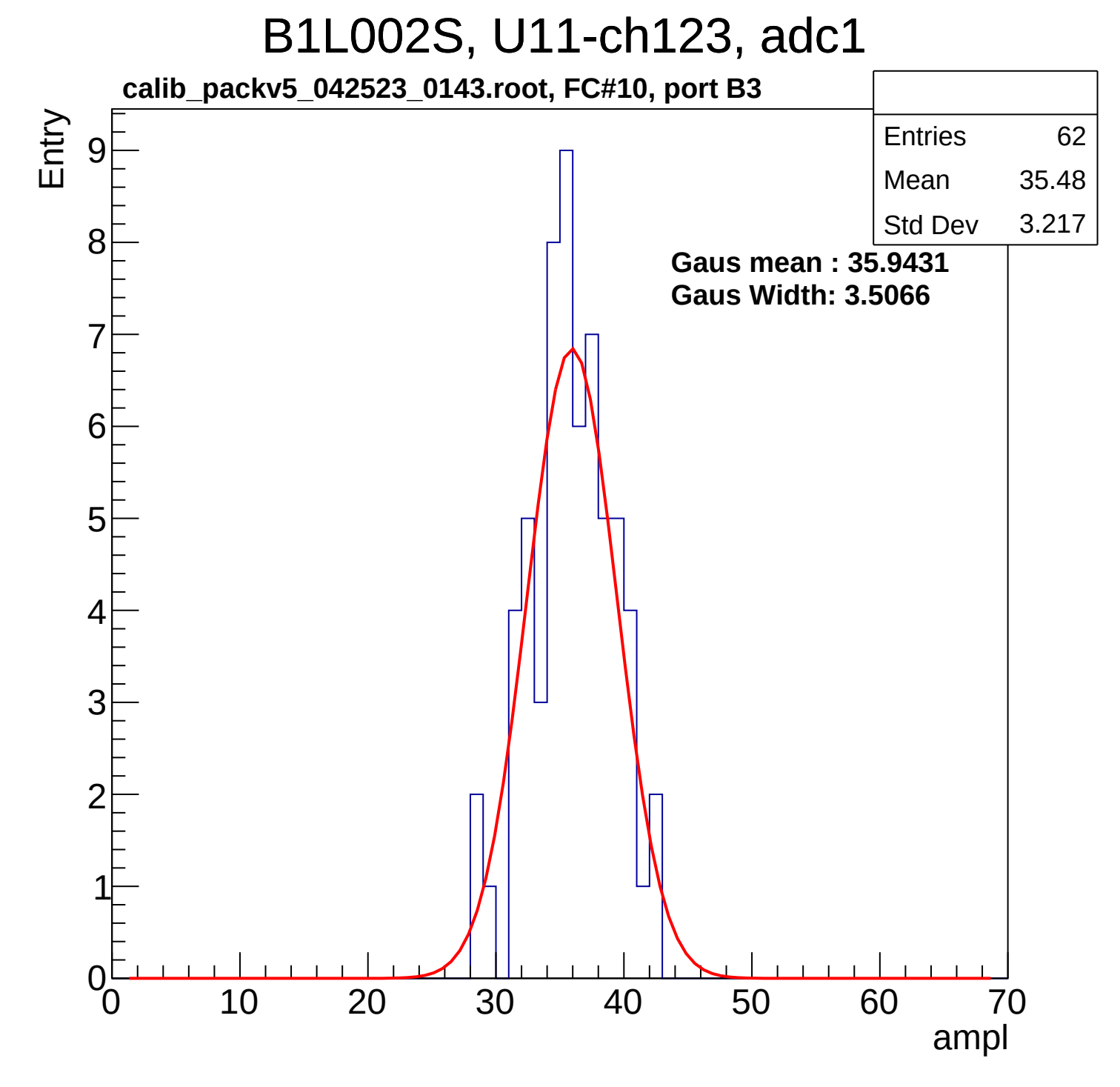
B1L002S, U11-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35.48
Std Dev 3.217
Gaus mean : 35.9431
Gaus Width: 3.5066
Entry
ampl
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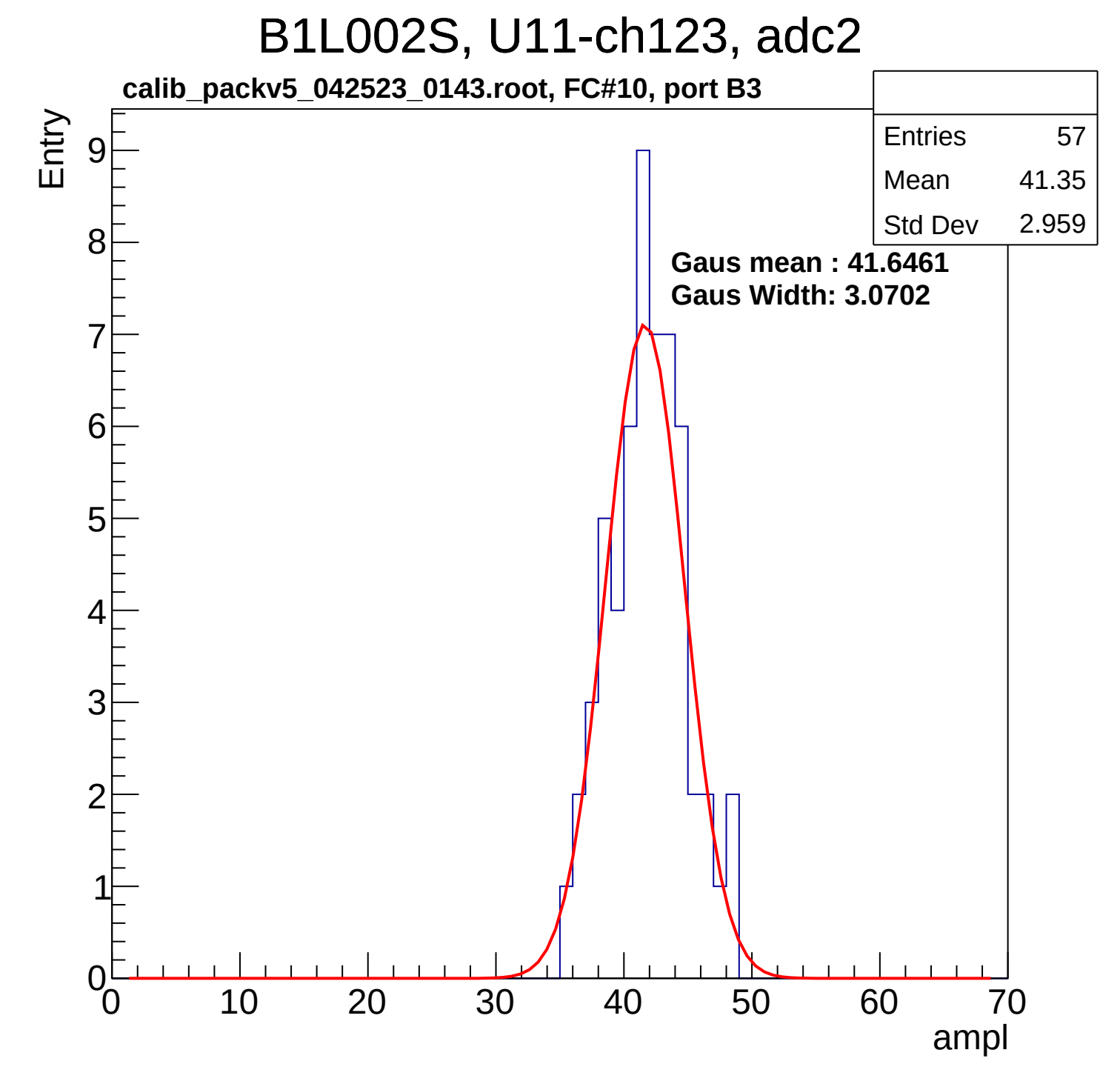
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U11-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.35
Std Dev 2.959
Gaus mean : 41.6461
Gaus Width: 3.0702
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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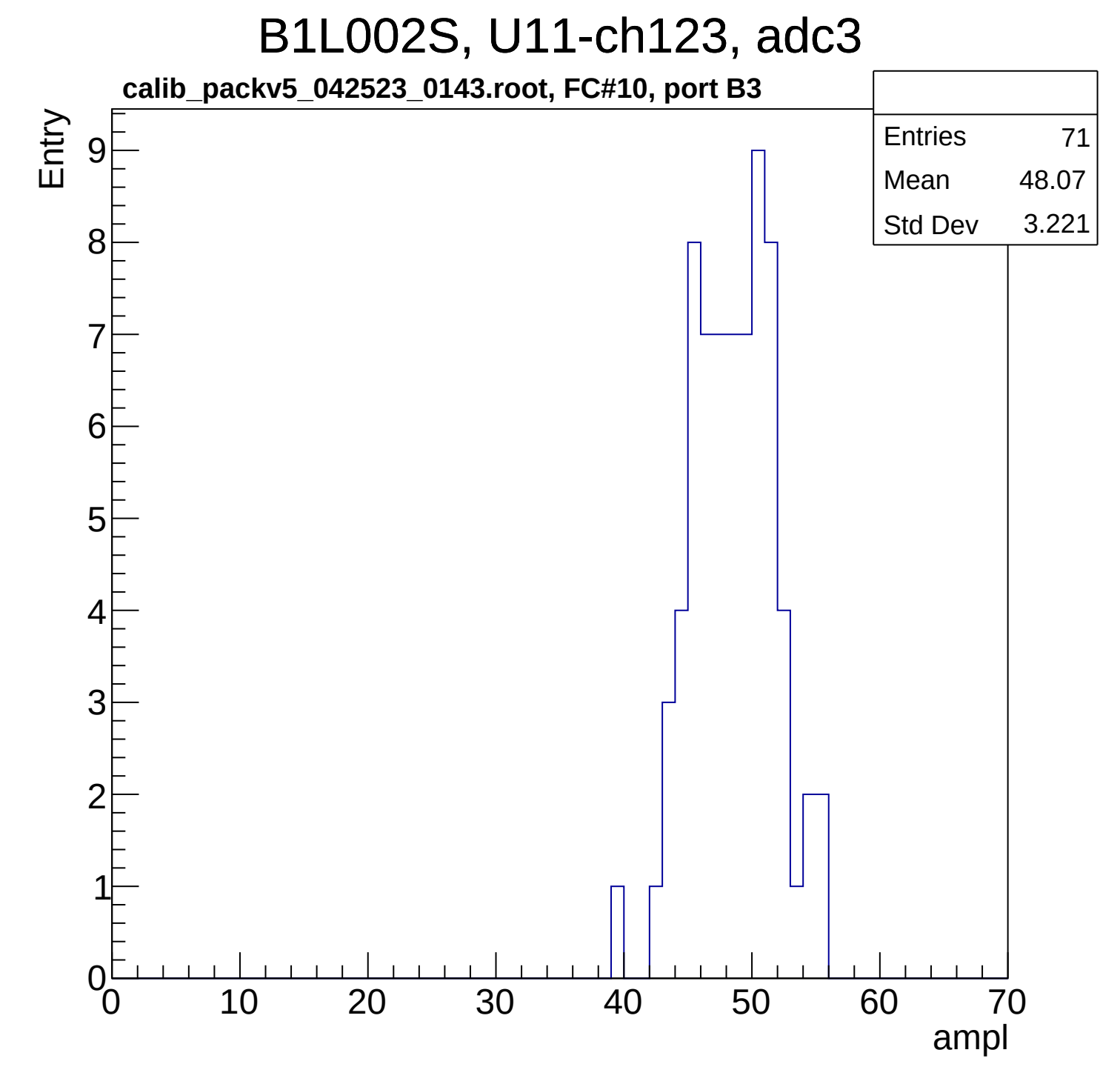
B1L002S, U11-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 48.07
Std Dev 3.221
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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30
40
50
60
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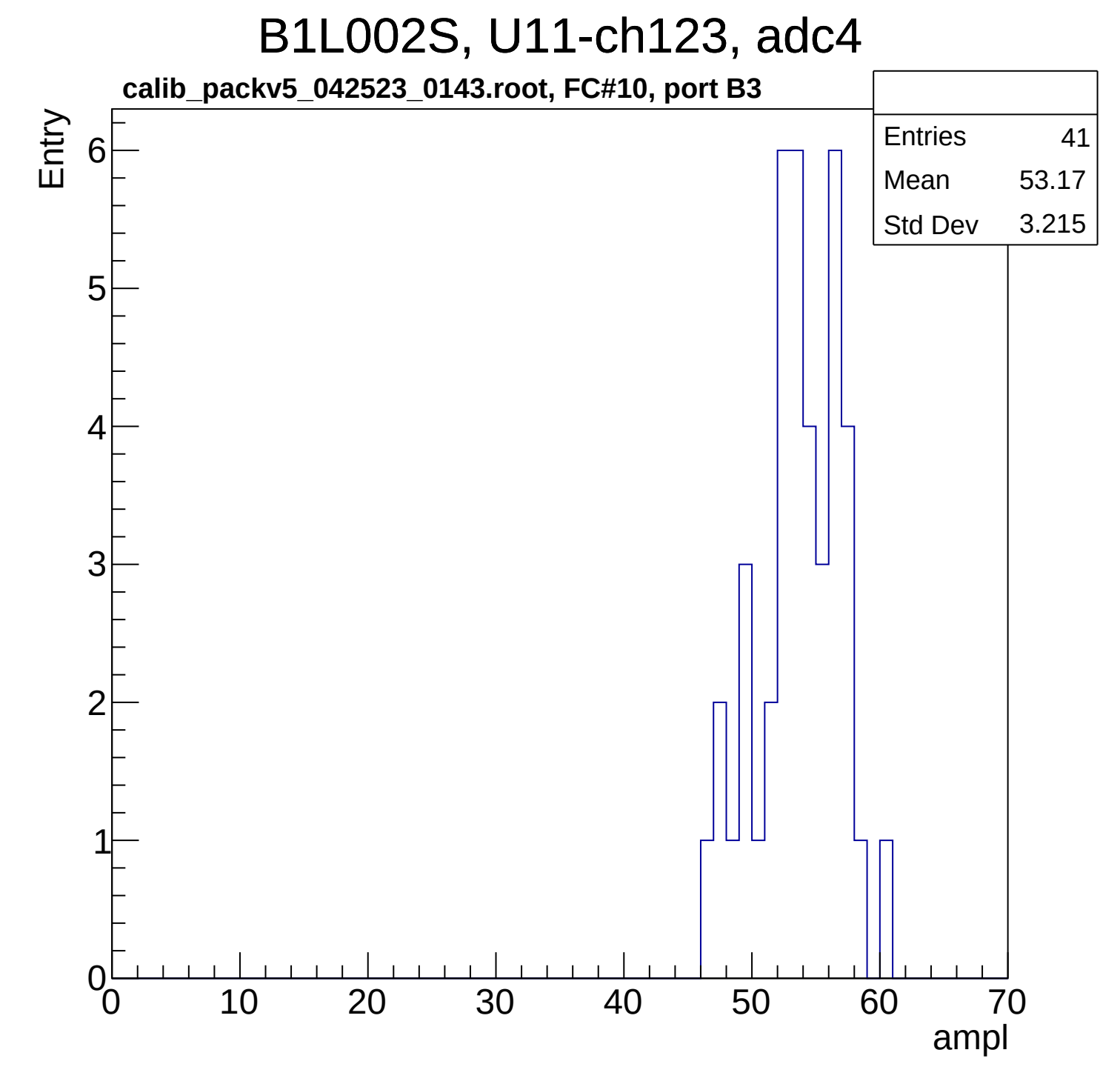
B1L002S, U11-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 53.17
Std Dev 3.215
Entry
ampl
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3
4
5
6
0
10
20
30
40
50
60
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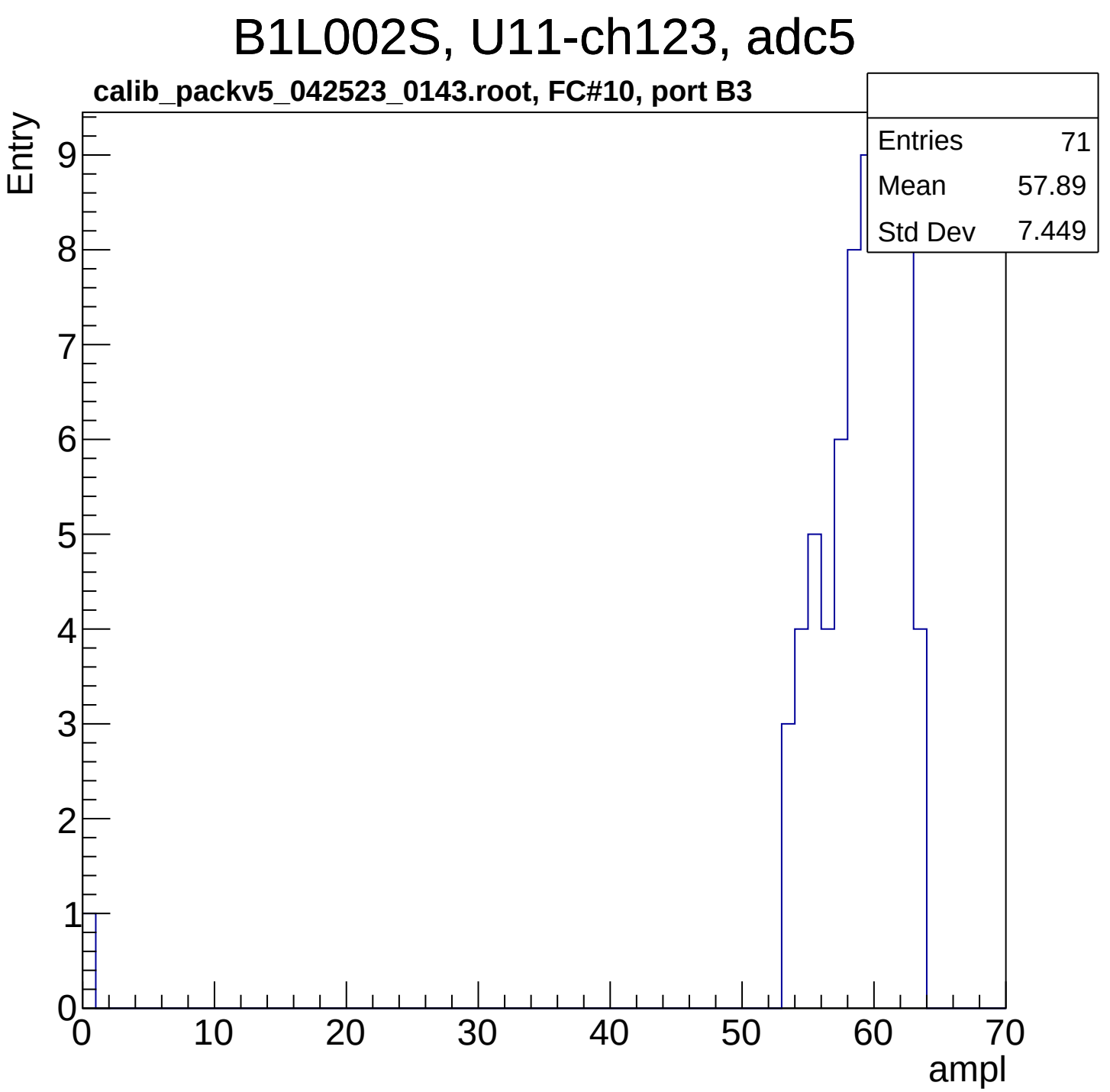
B1L002S, U11-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57.89
Std Dev 7.449
Entry
ampl
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2
3
4
5
6
7
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0
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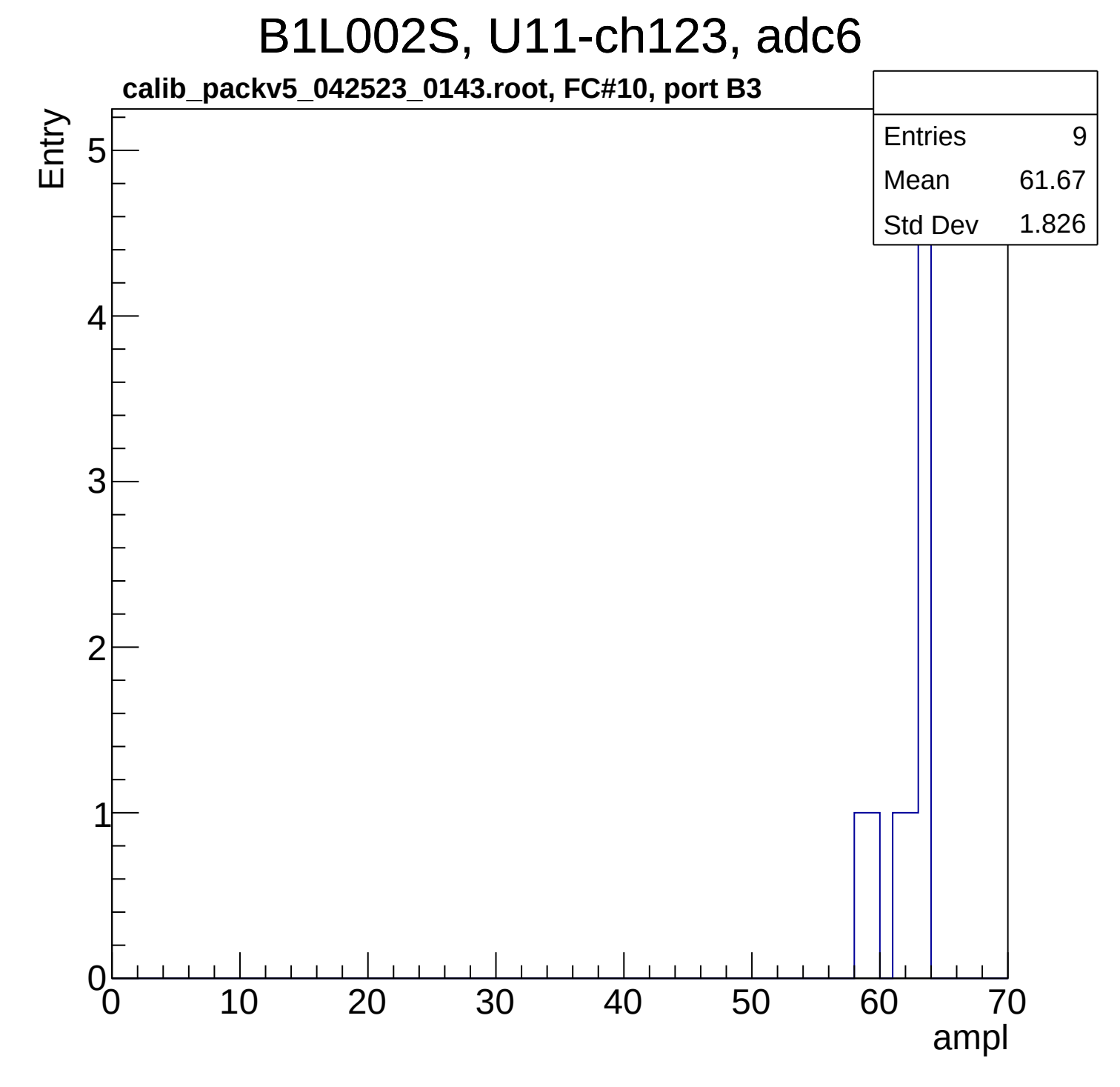
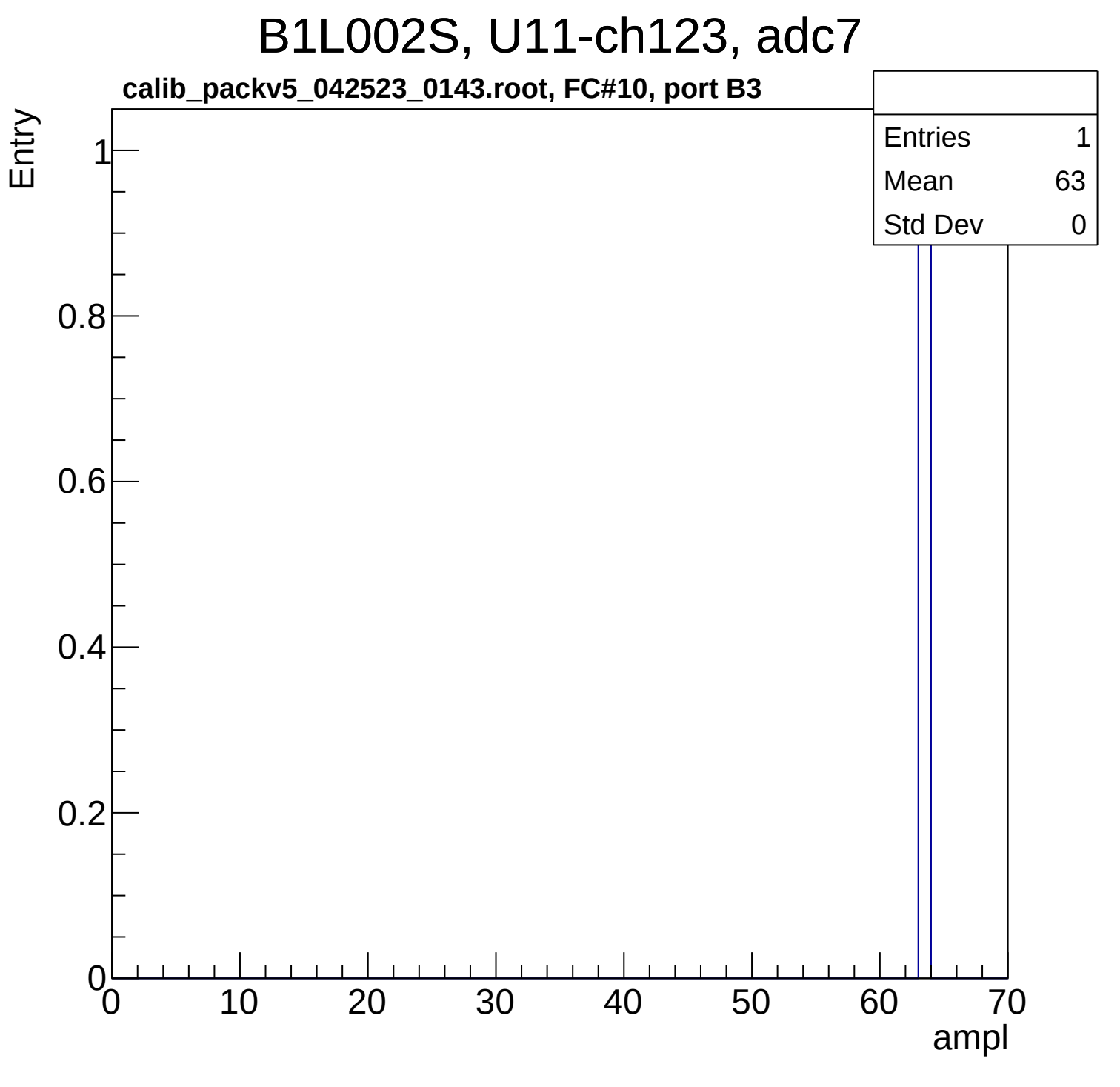

B1L002S, U11-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 61.67
Std Dev 1.826
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L002S, U11-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.8
0.6
0.4
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10
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50
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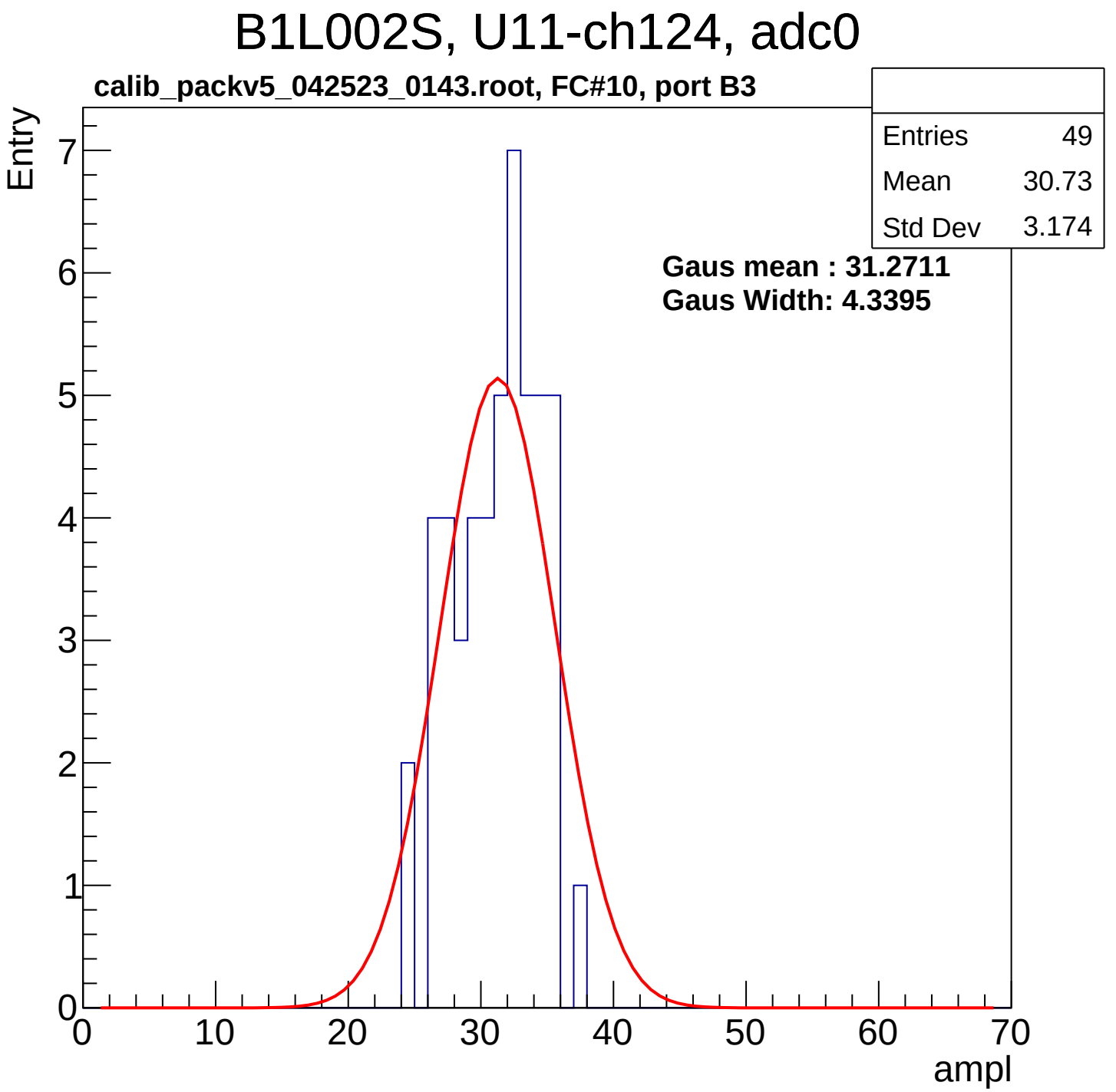
70
ampl


B1L002S, U11-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 30.73
Std Dev 3.174
Gaus mean : 31.2711
Gaus Width: 4.3395
Entry
ampl
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1
2
3
4
5
6
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0
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60
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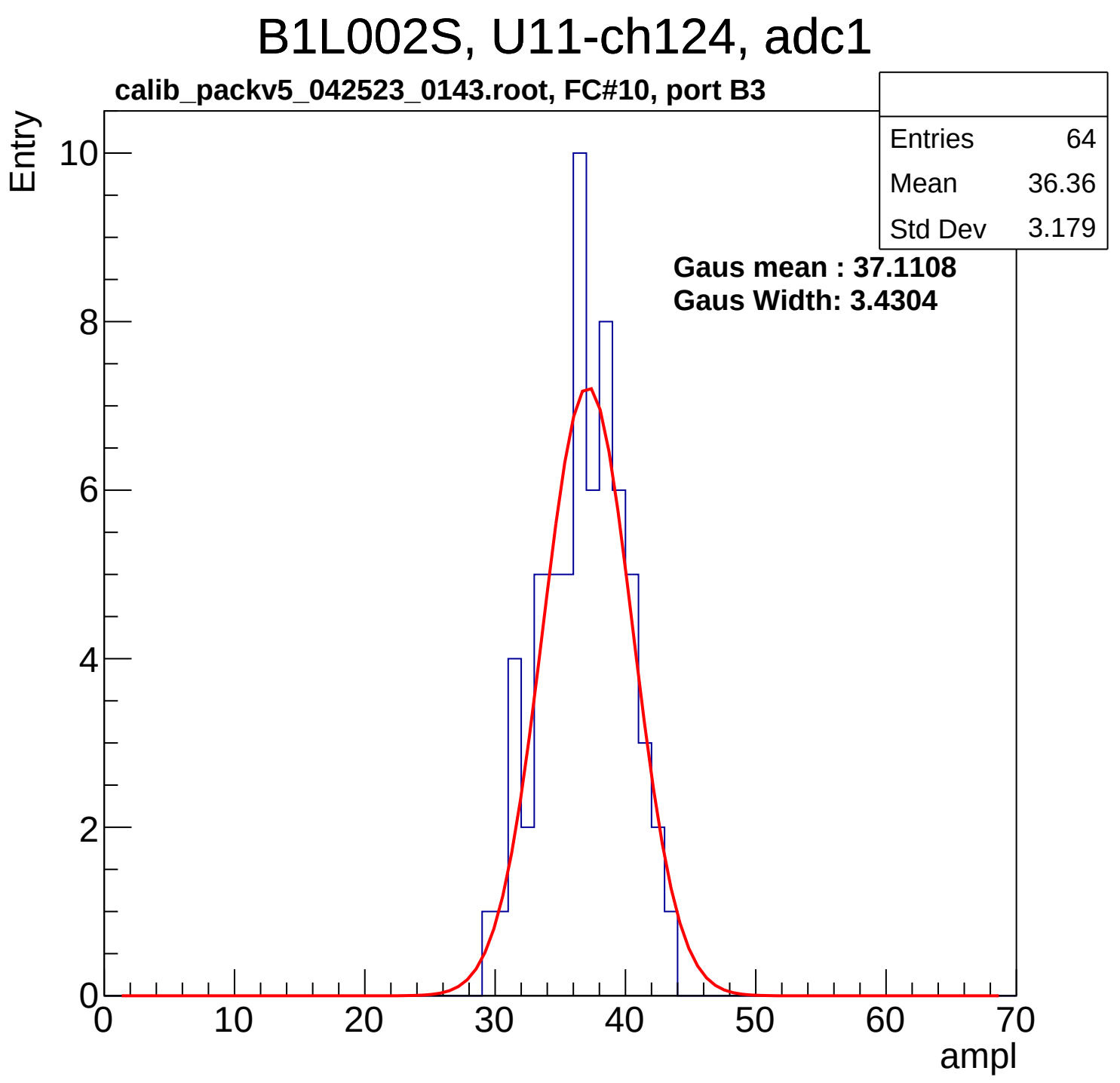
B1L002S, U11-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 36.36
Std Dev 3.179
Gaus mean : 37.1108
Gaus Width: 3.4304
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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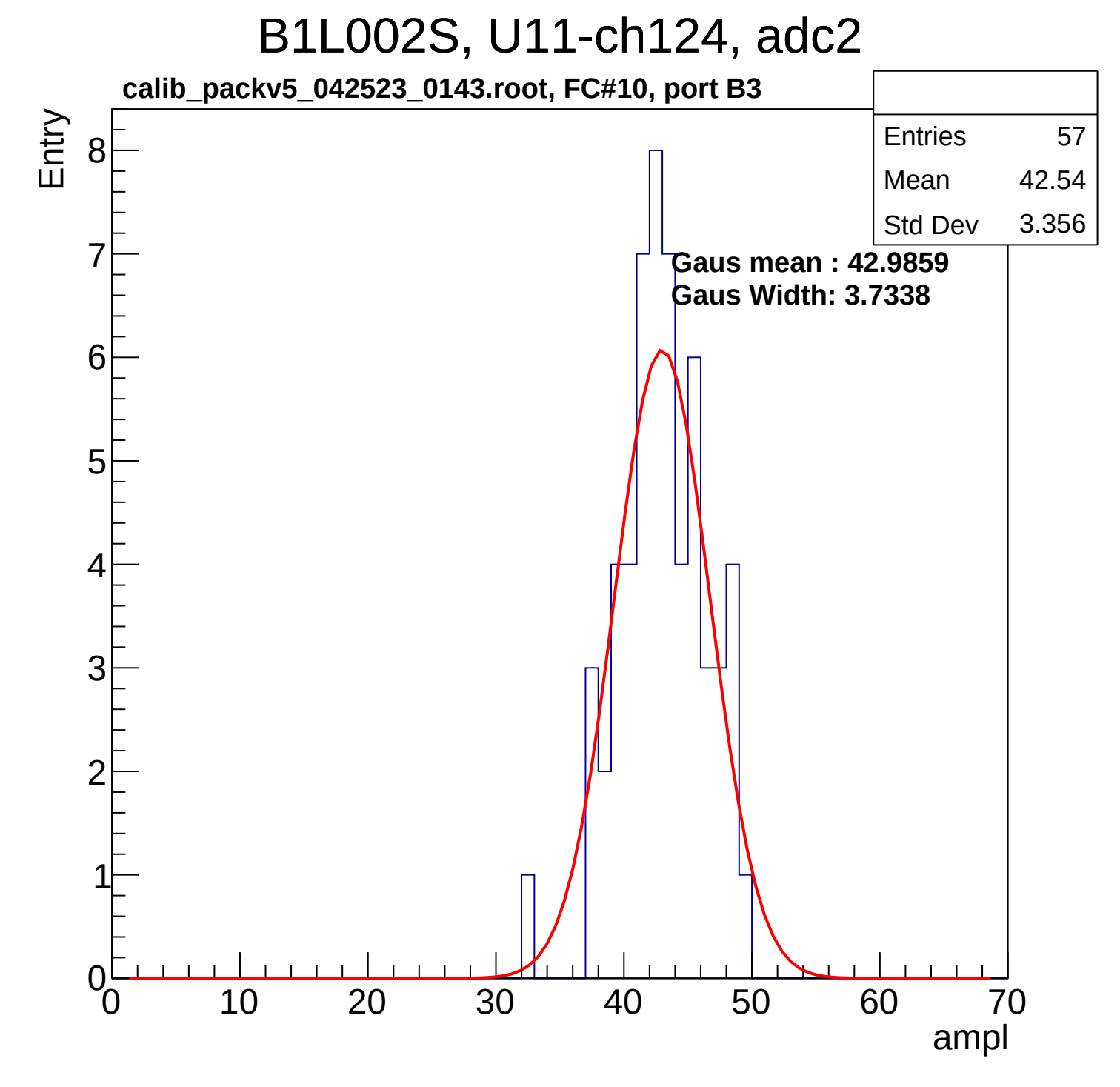
B1L002S, U11-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 42.54
Std Dev 3.356
Gaus mean : 42.9859
Gaus Width: 3.7338
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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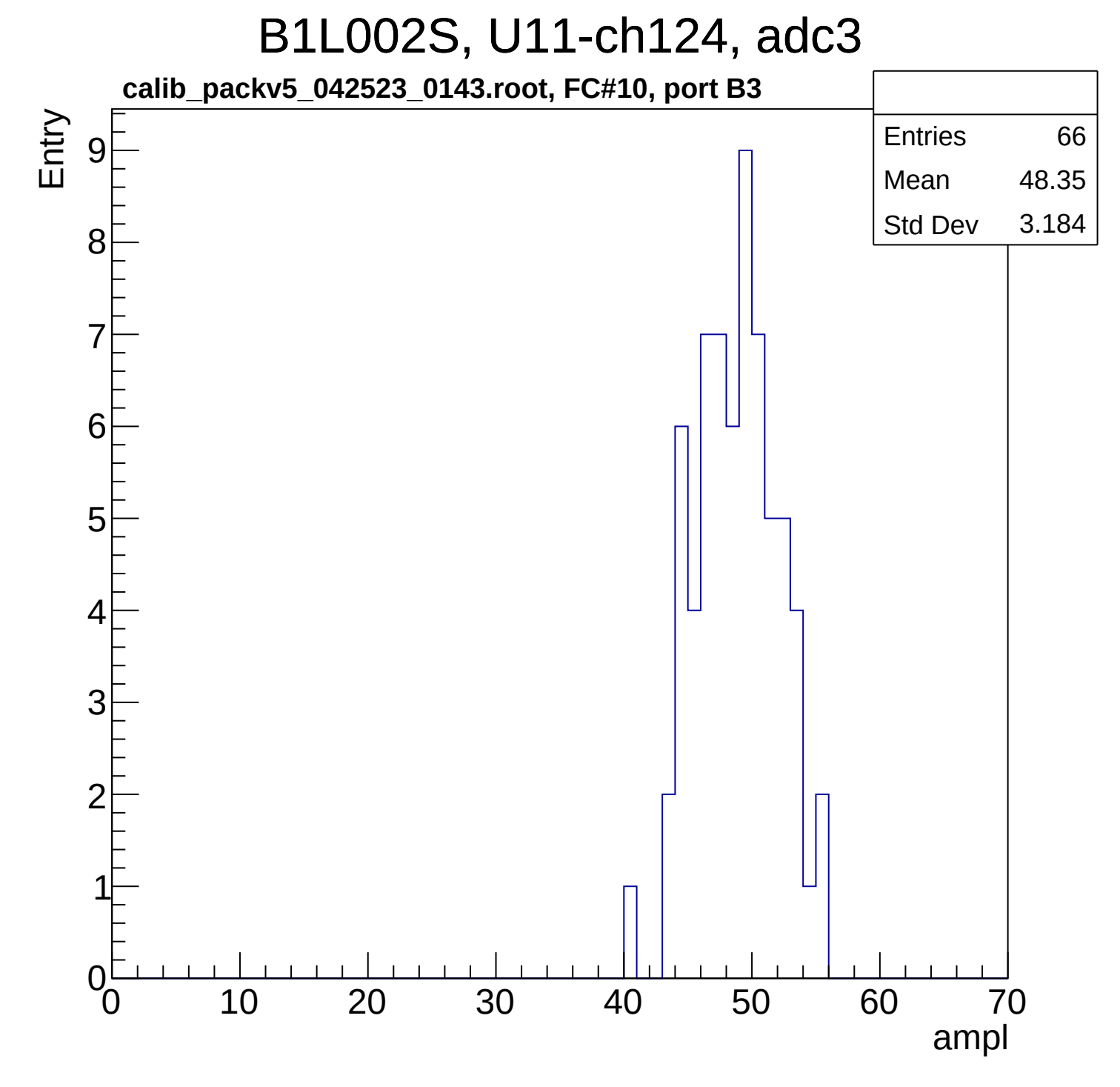
B1L002S, U11-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.35
Std Dev 3.184
Entry
ampl
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2
3
4
5
6
7
8
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0
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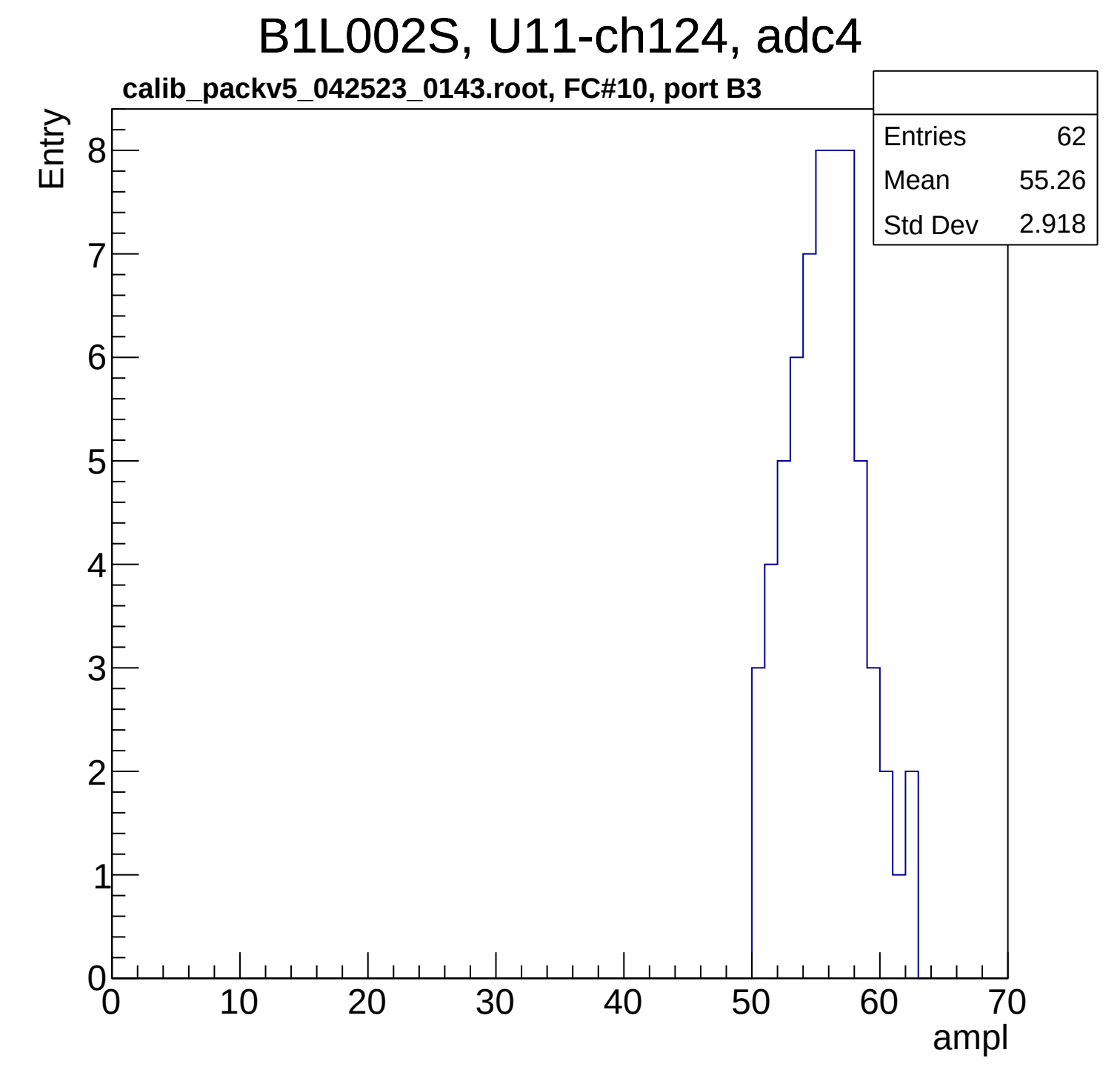
B1L002S, U11-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 55.26
Std Dev 2.918
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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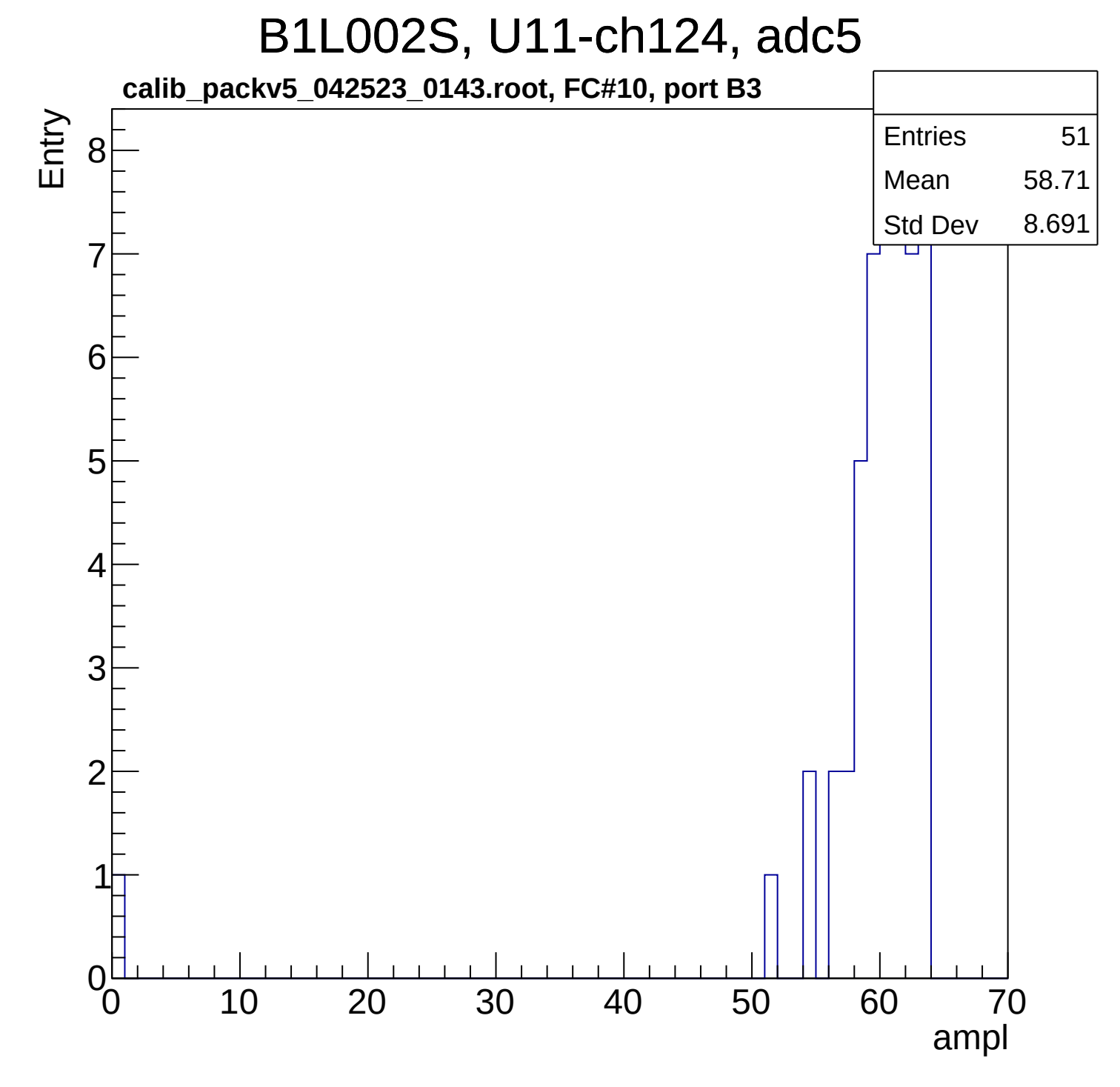
B1L002S, U11-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.71
Std Dev 8.691
Entry
ampl
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2
3
4
5
6
7
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0
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60
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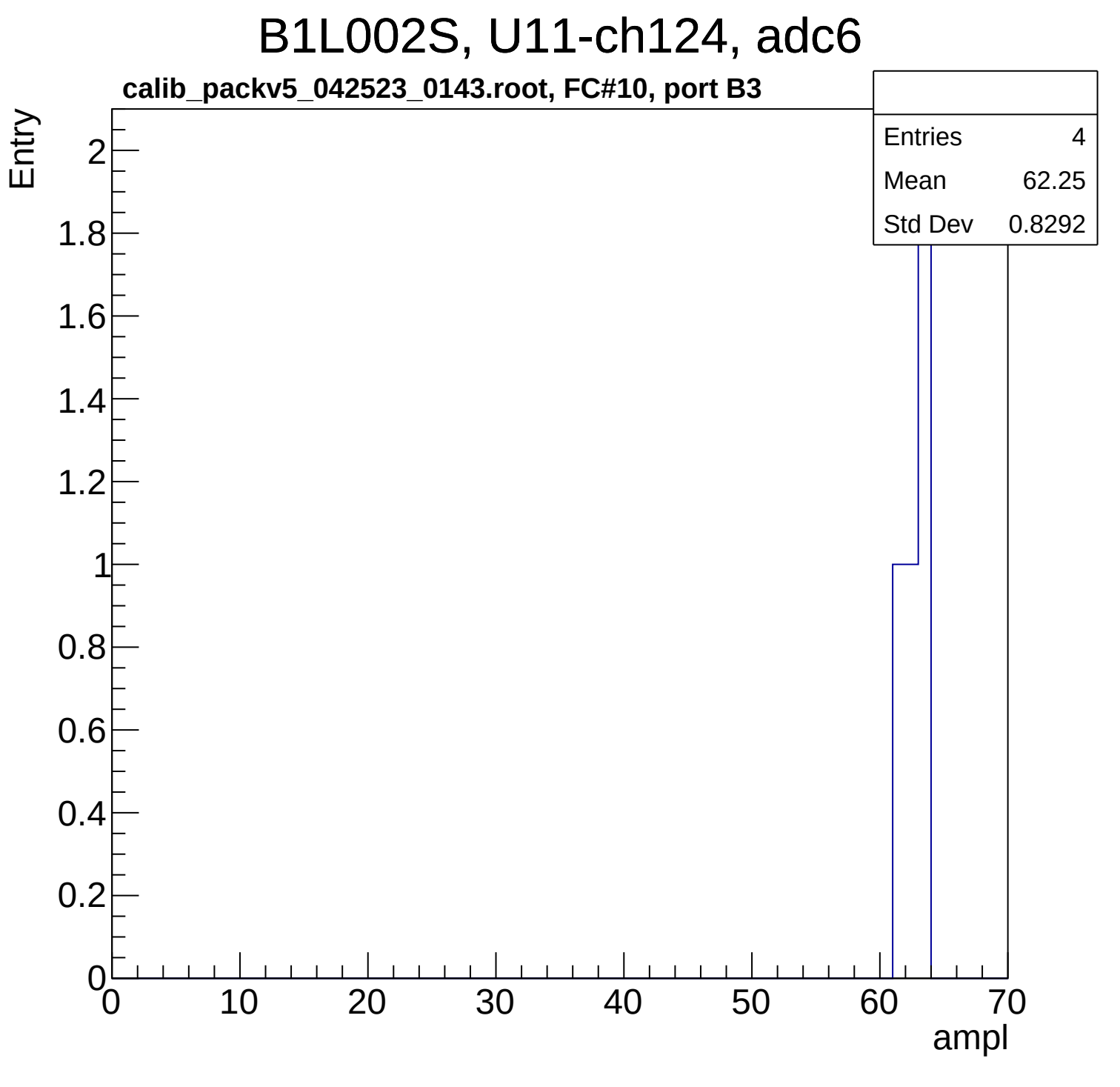
B1L002S, U11-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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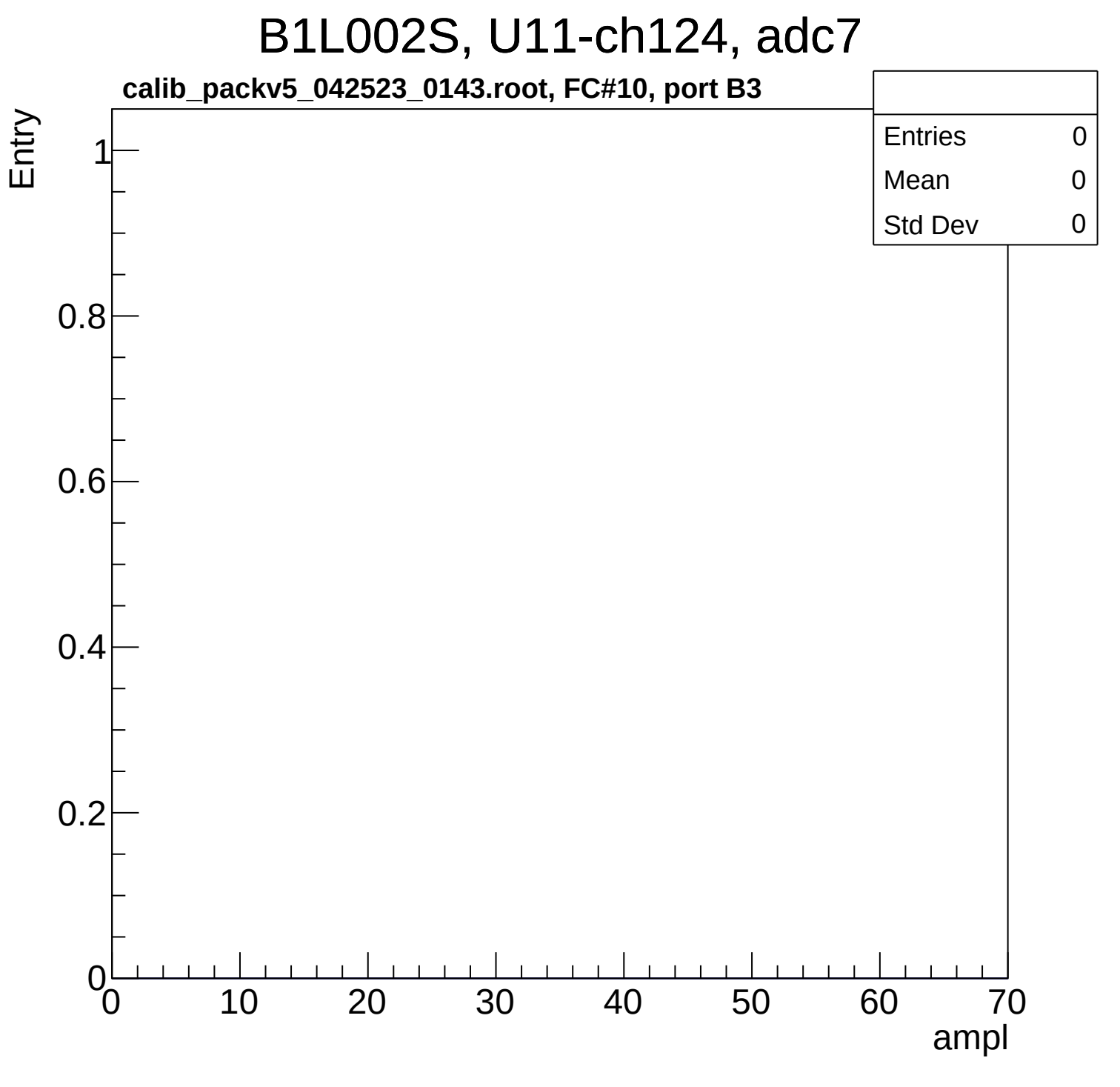
B1L002S, U11-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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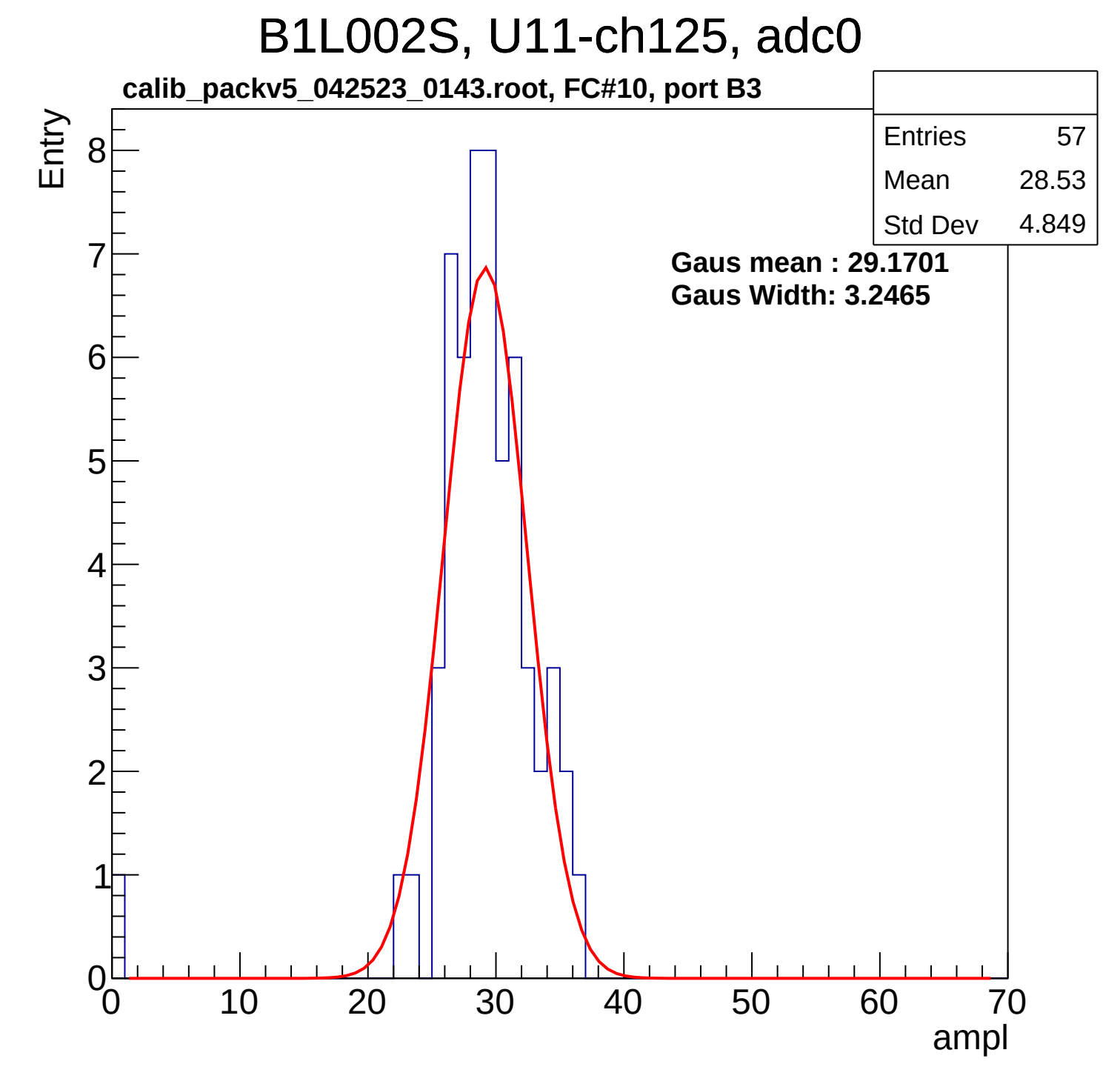
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.53
Std Dev 4.849
Gaus mean : 29.1701
Gaus Width: 3.2465
Entry
ampl
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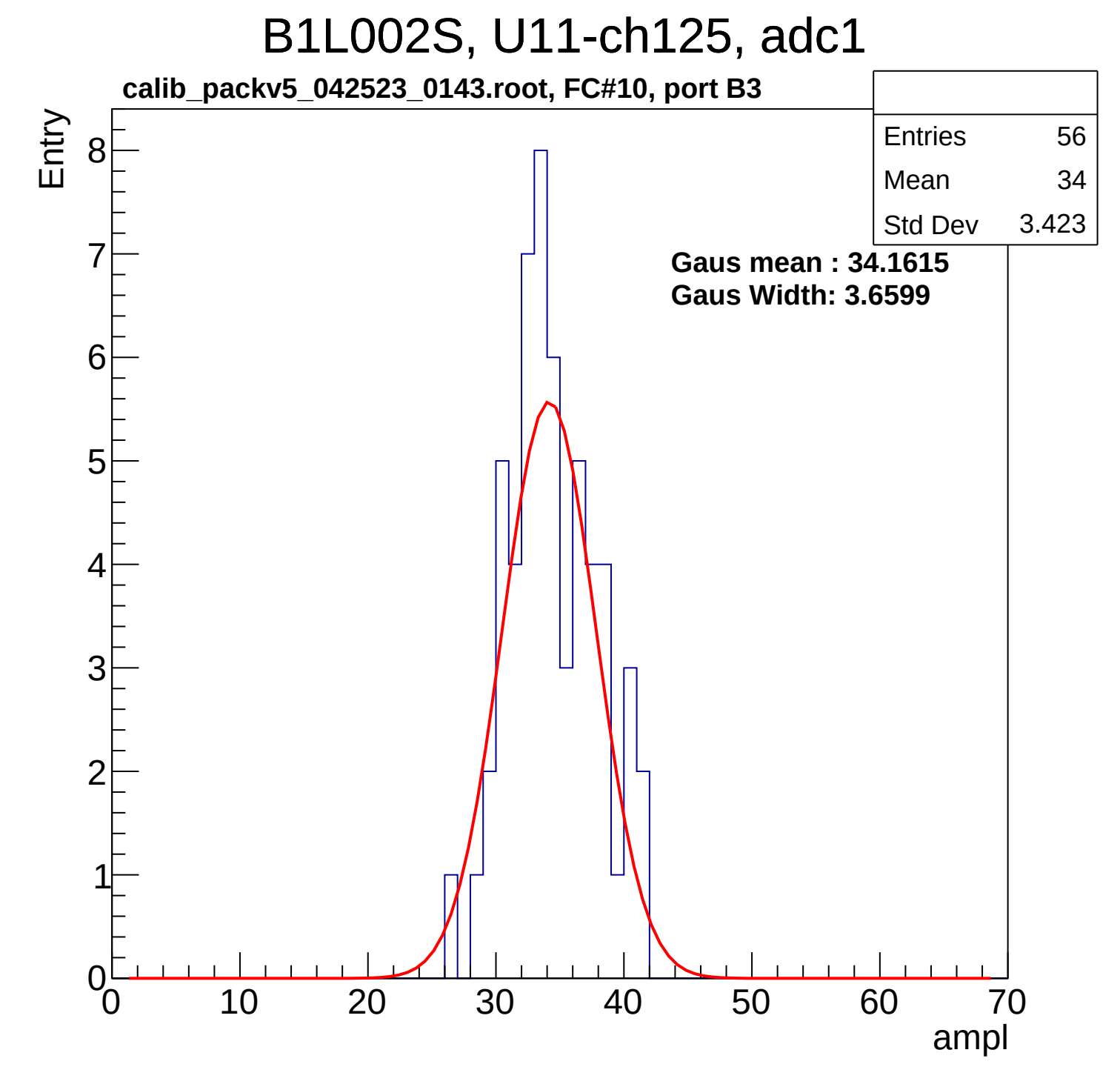
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U11-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34
Std Dev 3.423
Gaus mean : 34.1615
Gaus Width: 3.6599
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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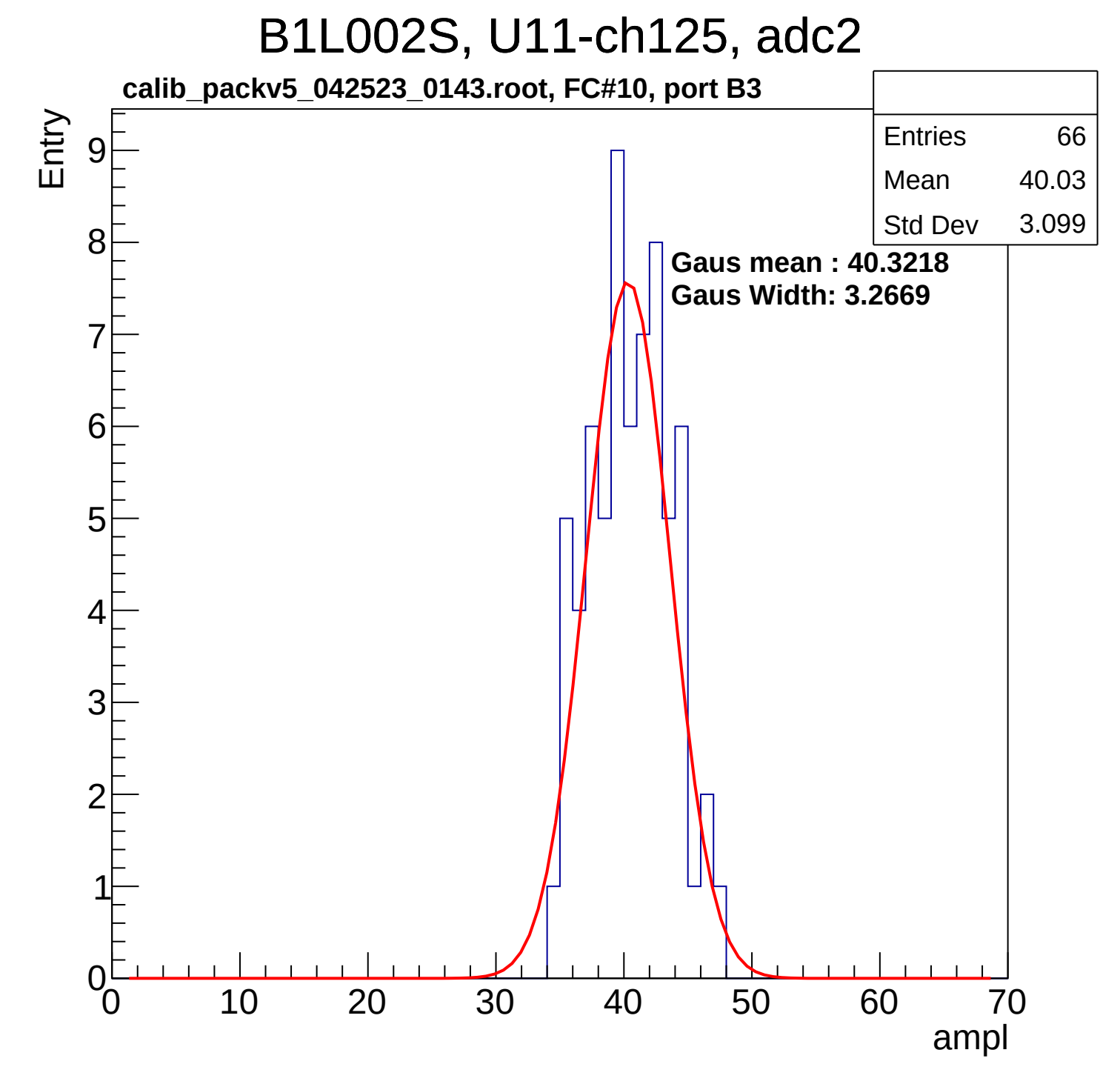
B1L002S, U11-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.03
Std Dev 3.099
Gaus mean : 40.3218
Gaus Width: 3.2669
Entry
ampl
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2
3
4
5
6
7
8
9
0
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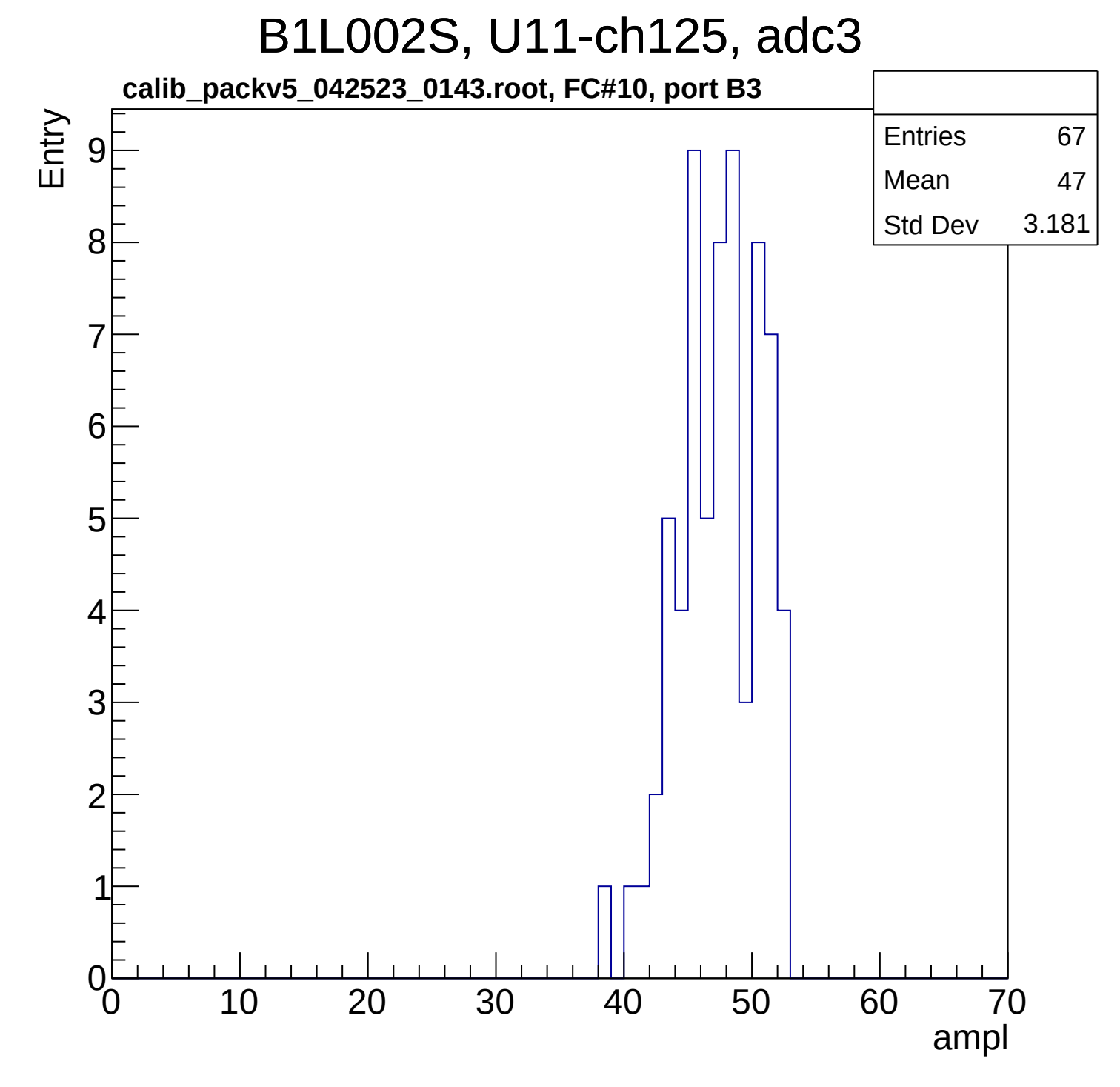
B1L002S, U11-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47
Std Dev 3.181
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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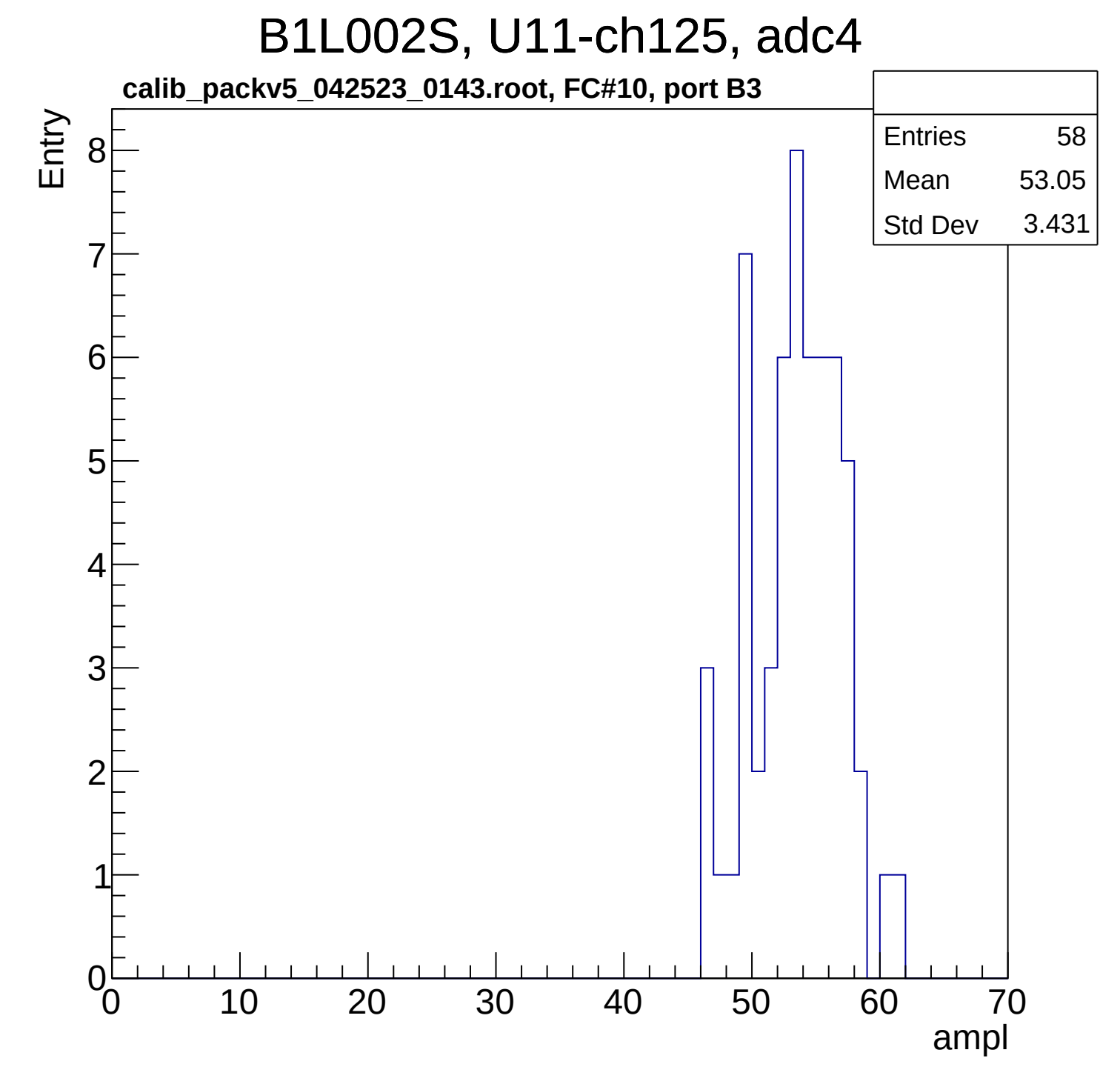
B1L002S, U11-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.05
Std Dev 3.431
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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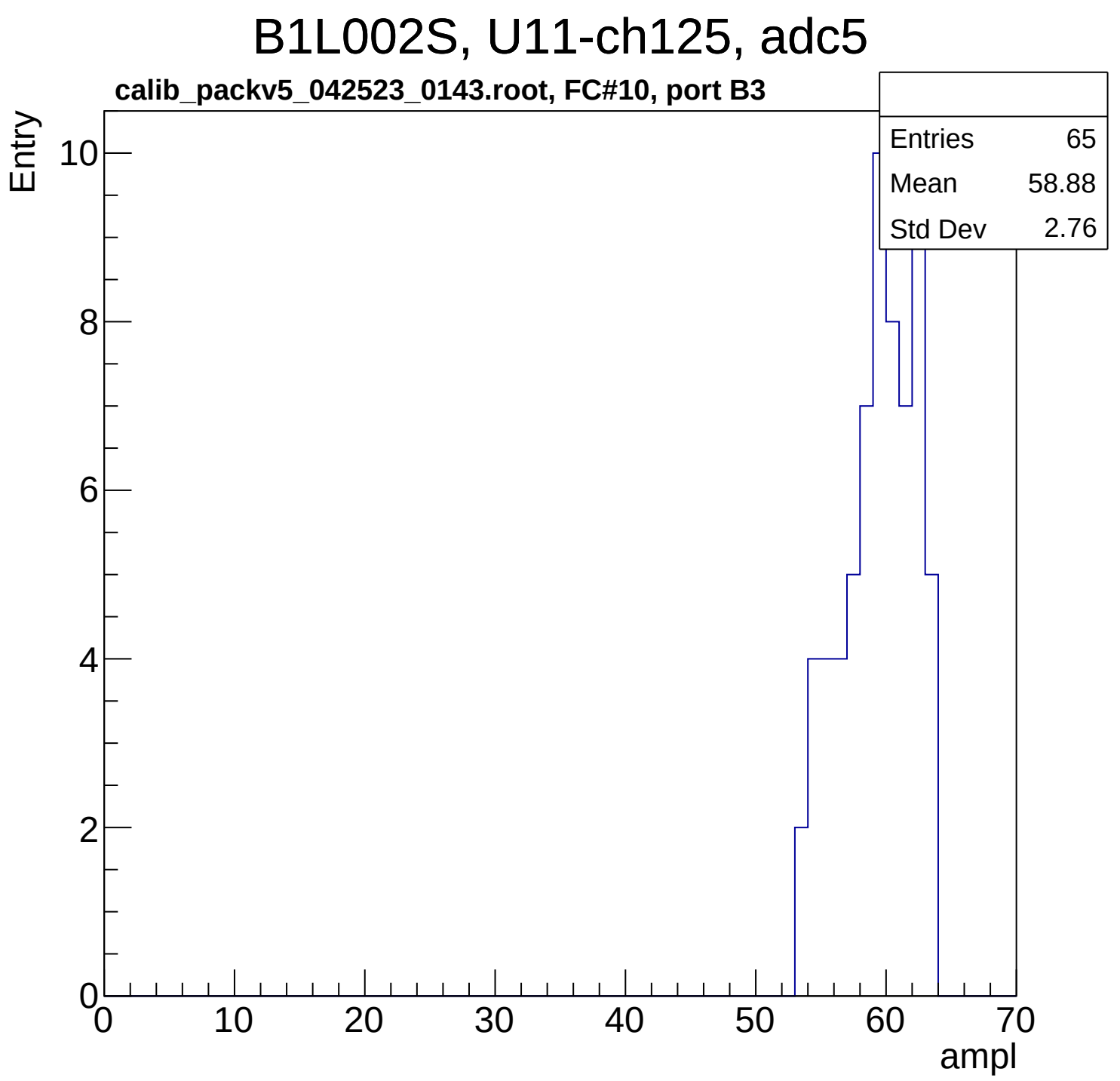
B1L002S, U11-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.88
Std Dev 2.76
Entry
0
2
4
6
8
10
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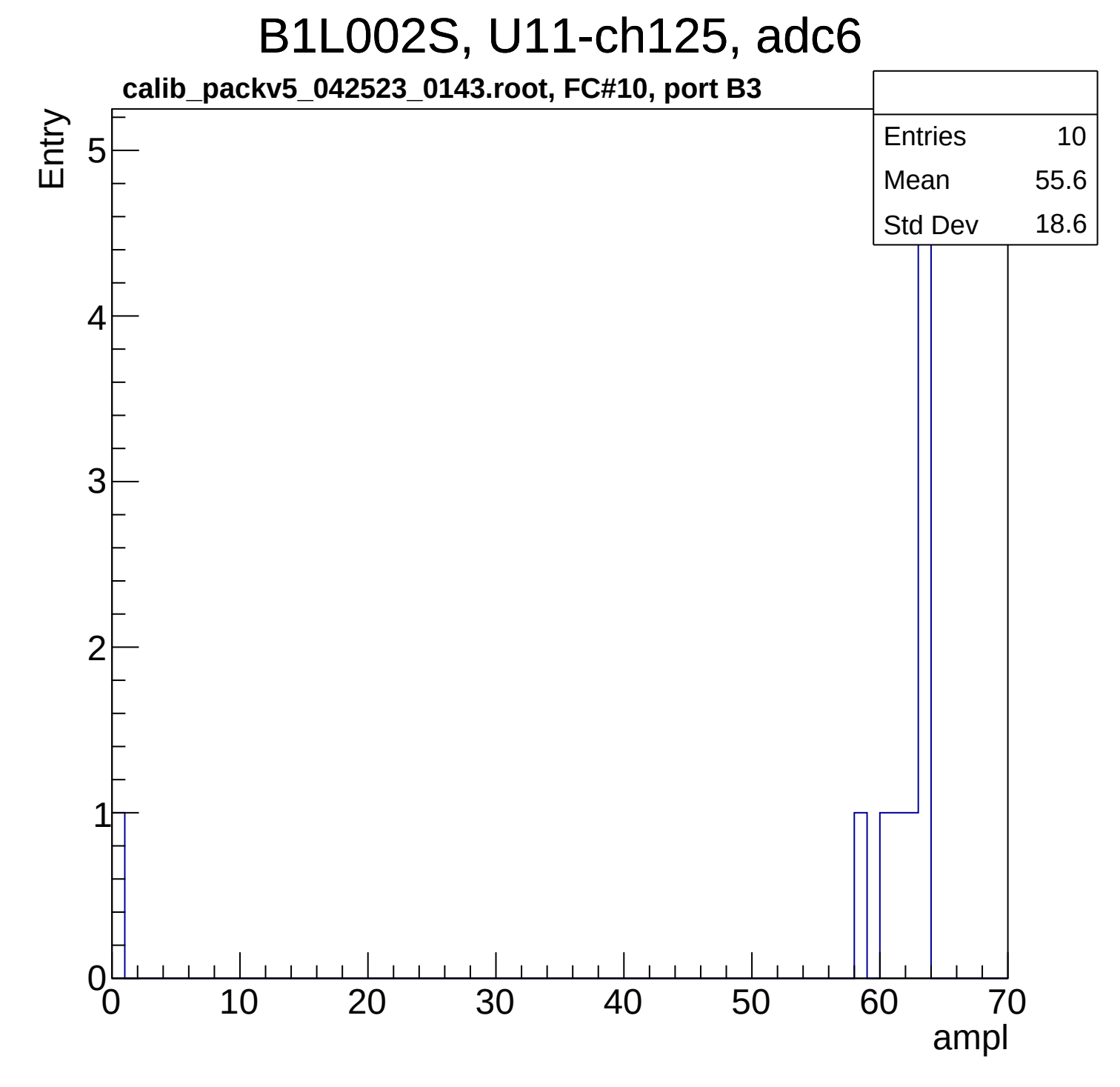
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.6
Std Dev 18.6
Entry
ampl
0
1
2
3
4
5
0
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20
30
40
50
60
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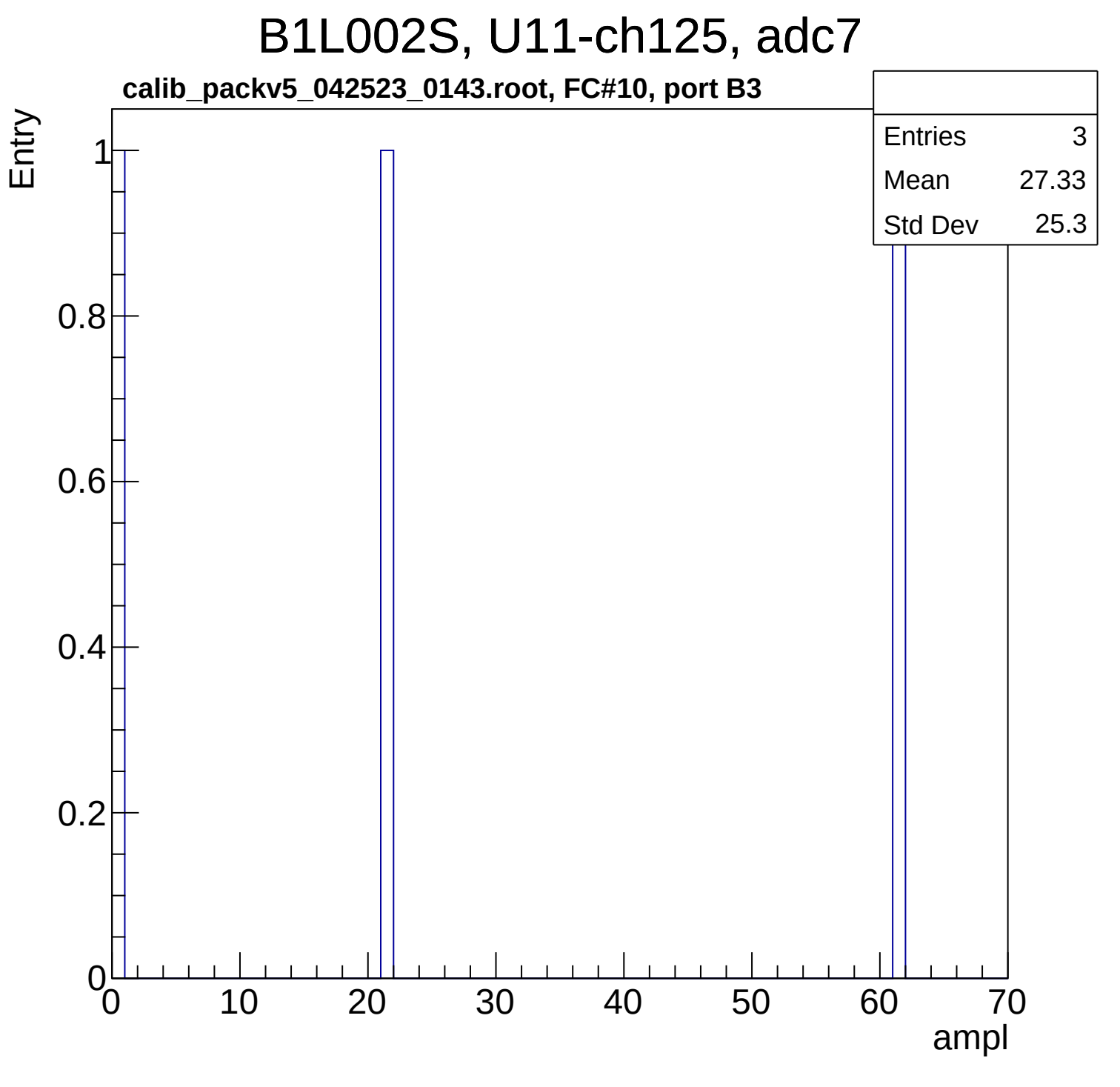
B1L002S, U11-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 25.3
Entry
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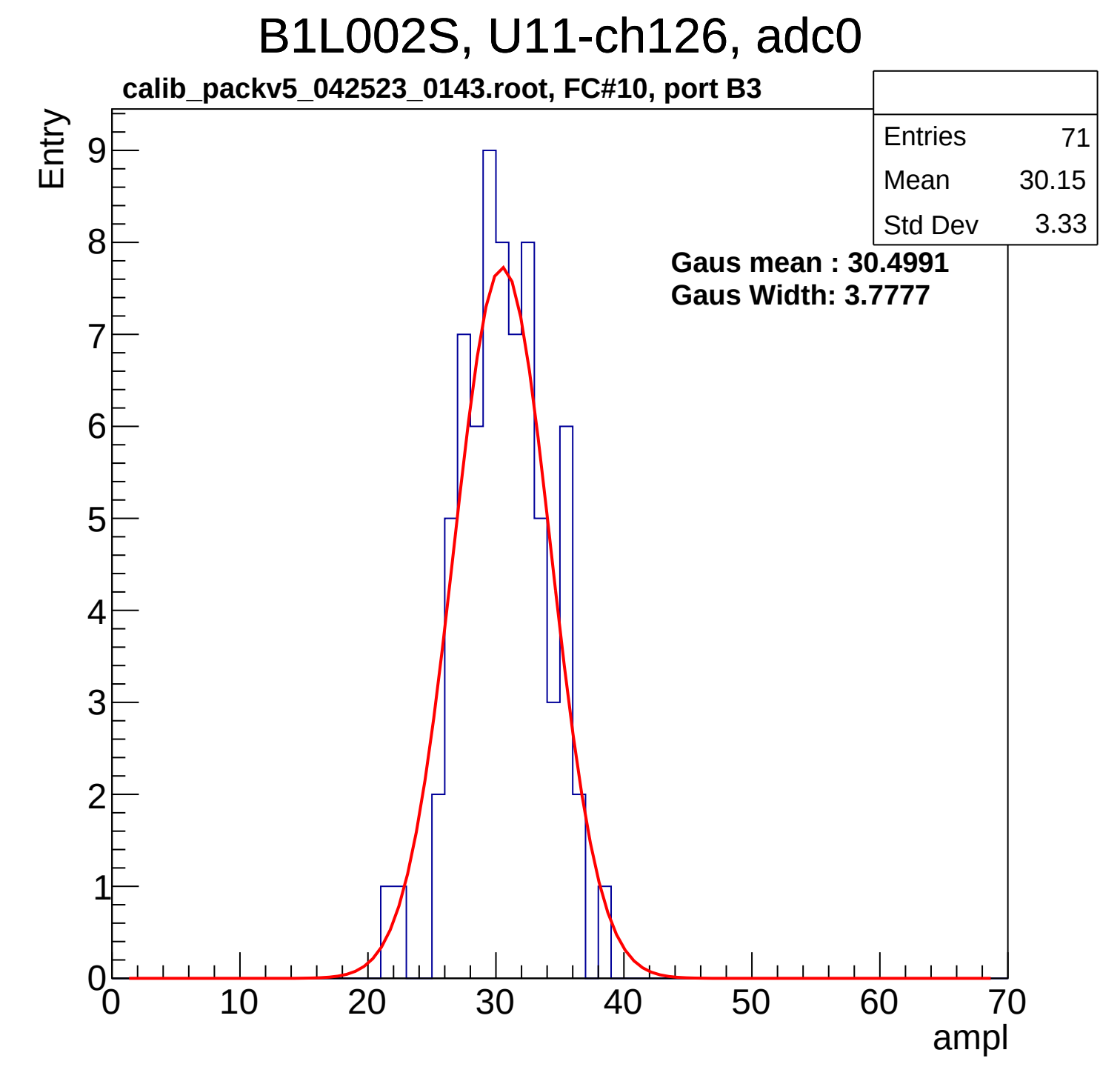
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U11-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 30.15
Std Dev 3.33
Gaus mean : 30.4991
Gaus Width: 3.7777
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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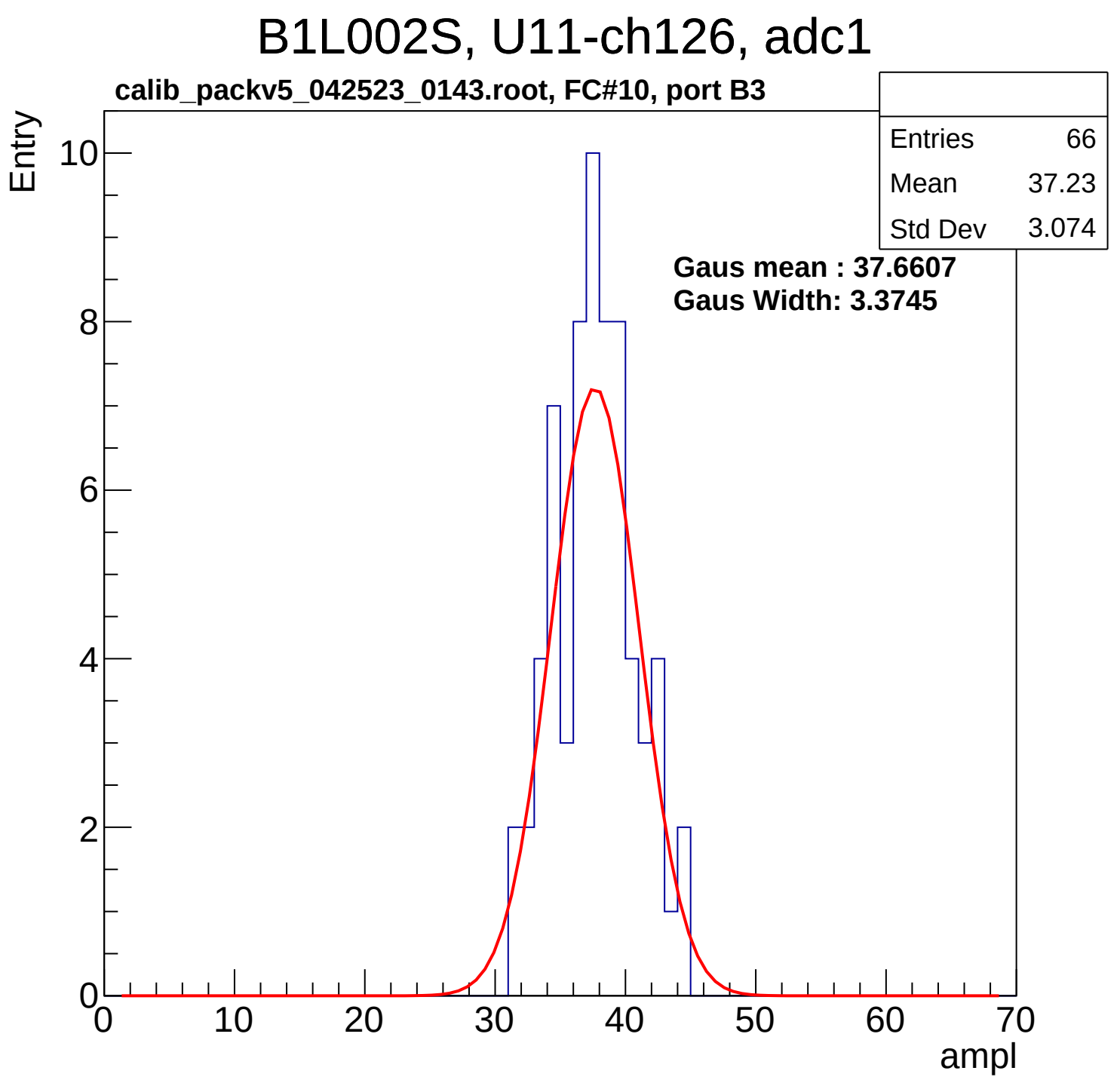
B1L002S, U11-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 37.23
Std Dev 3.074
Gaus mean : 37.6607
Gaus Width: 3.3745
Entry
ampl
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0
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60
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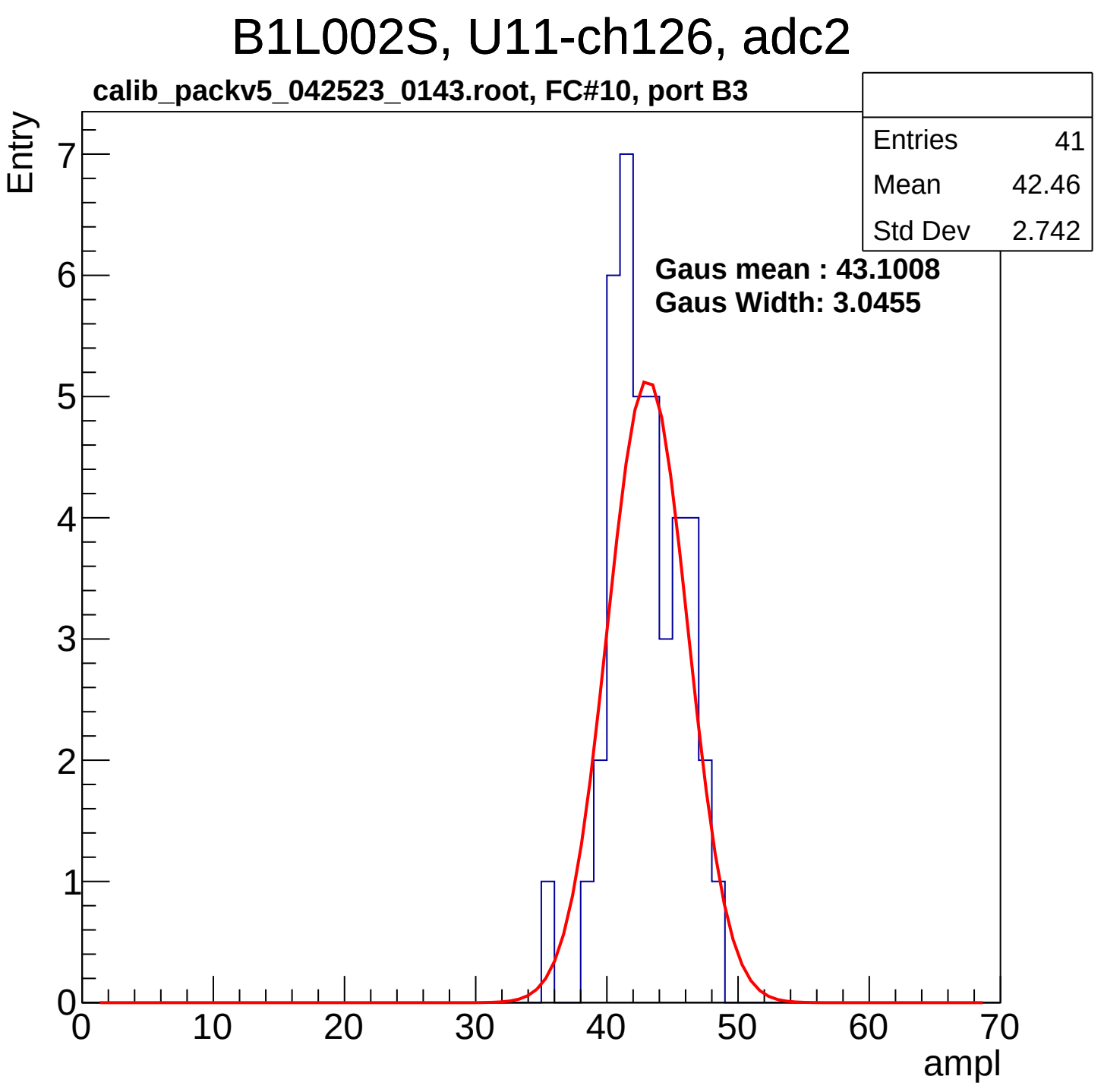
B1L002S, U11-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 42.46
Std Dev 2.742
Gaus mean : 43.1008
Gaus Width: 3.0455
Entry
ampl
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2
3
4
5
6
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0
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60
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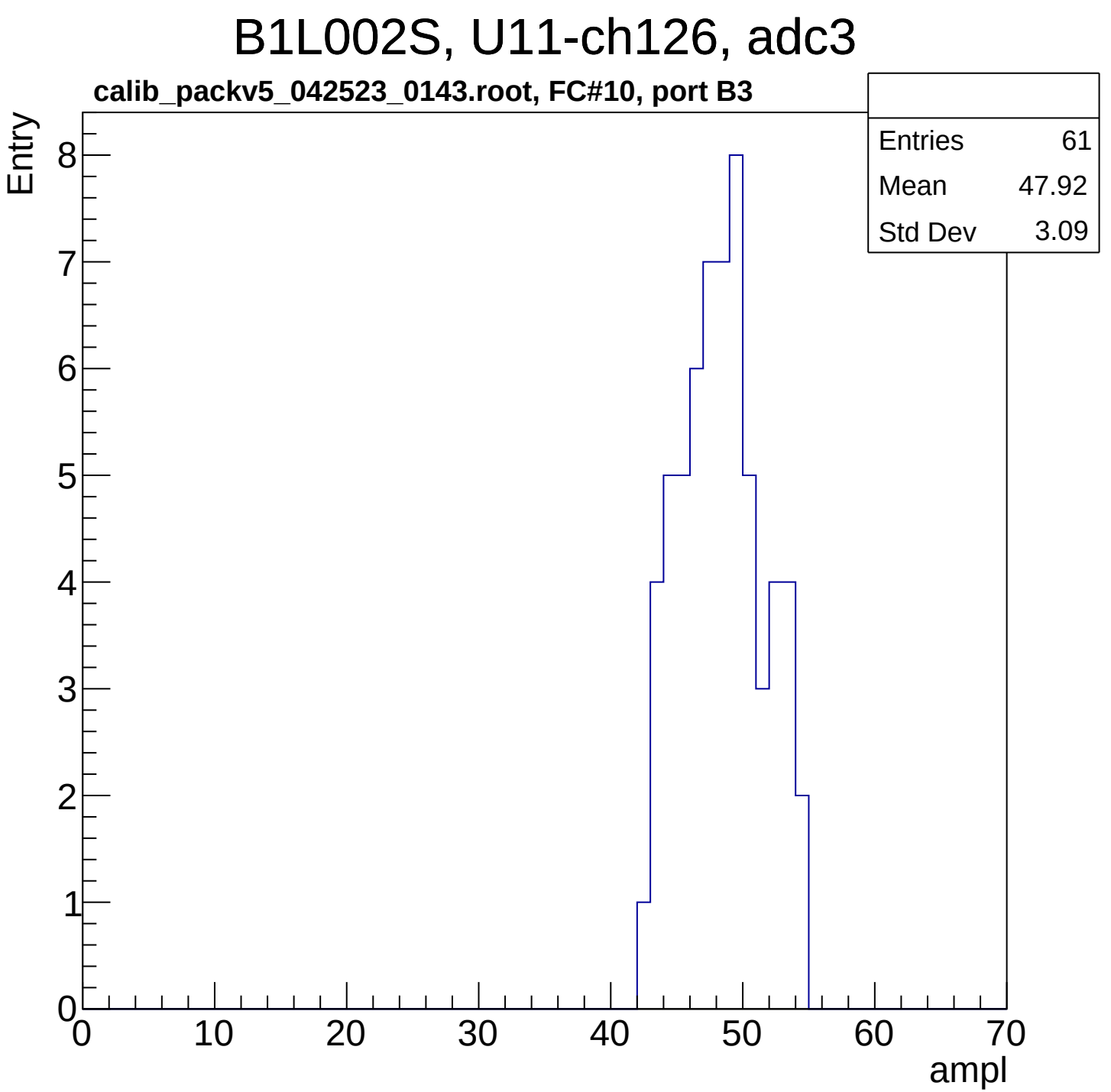
B1L002S, U11-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.92
Std Dev 3.09
Entry
ampl
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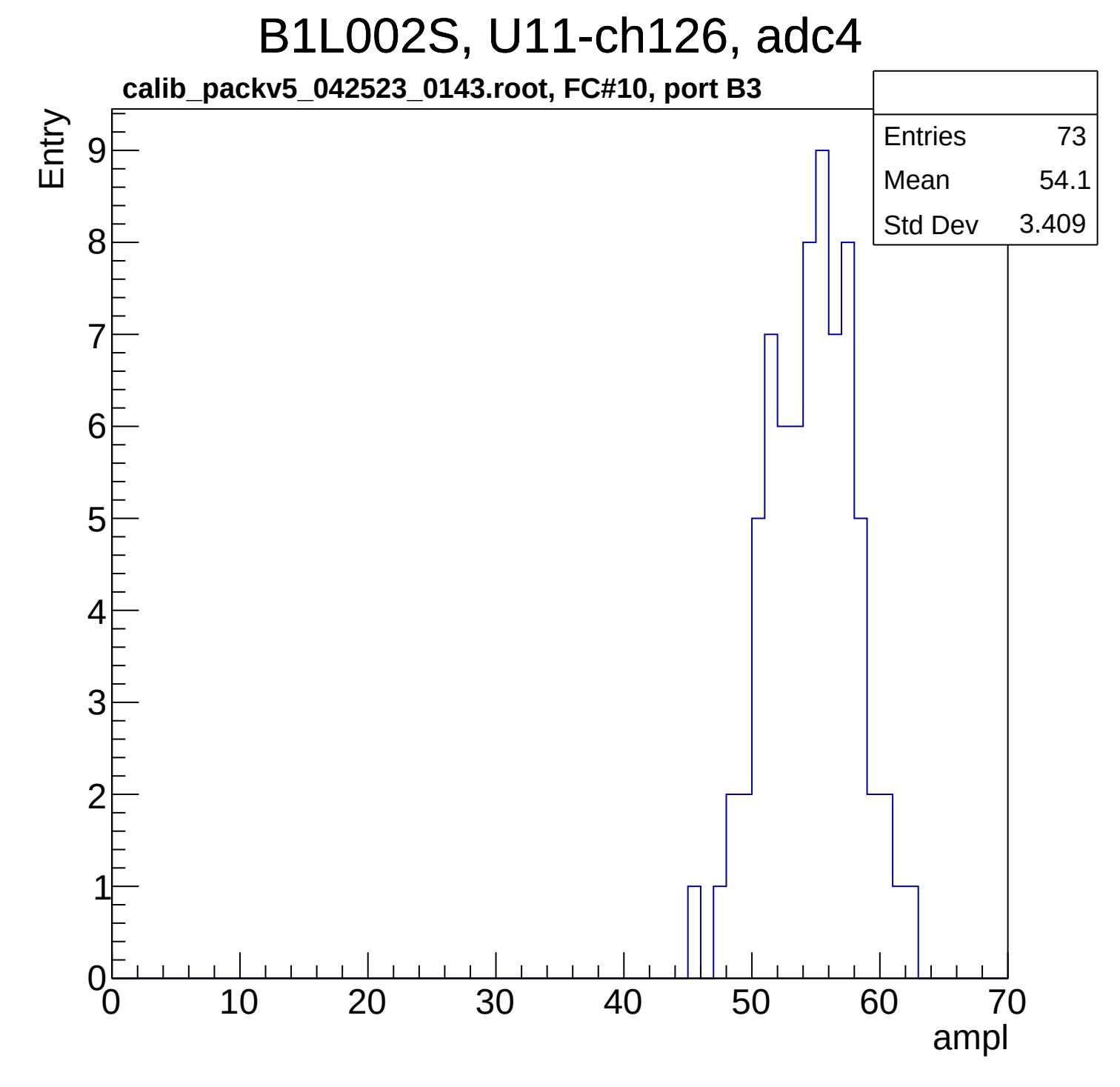
B1L002S, U11-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 54.1
Std Dev 3.409
Entry
ampl
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60
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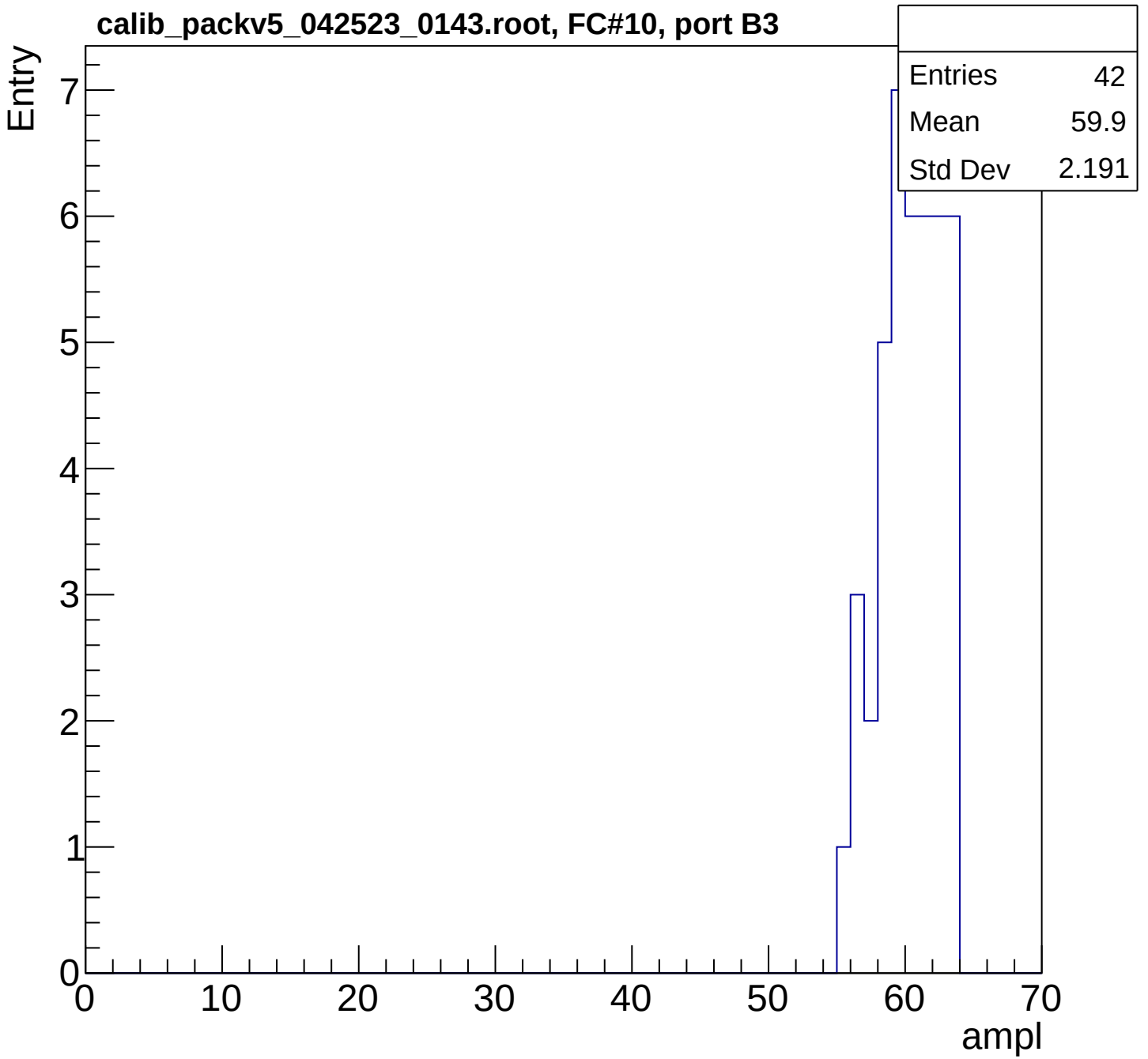
B1L002S, U11-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.9
Std Dev 2.191
Entry
ampl
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4
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0
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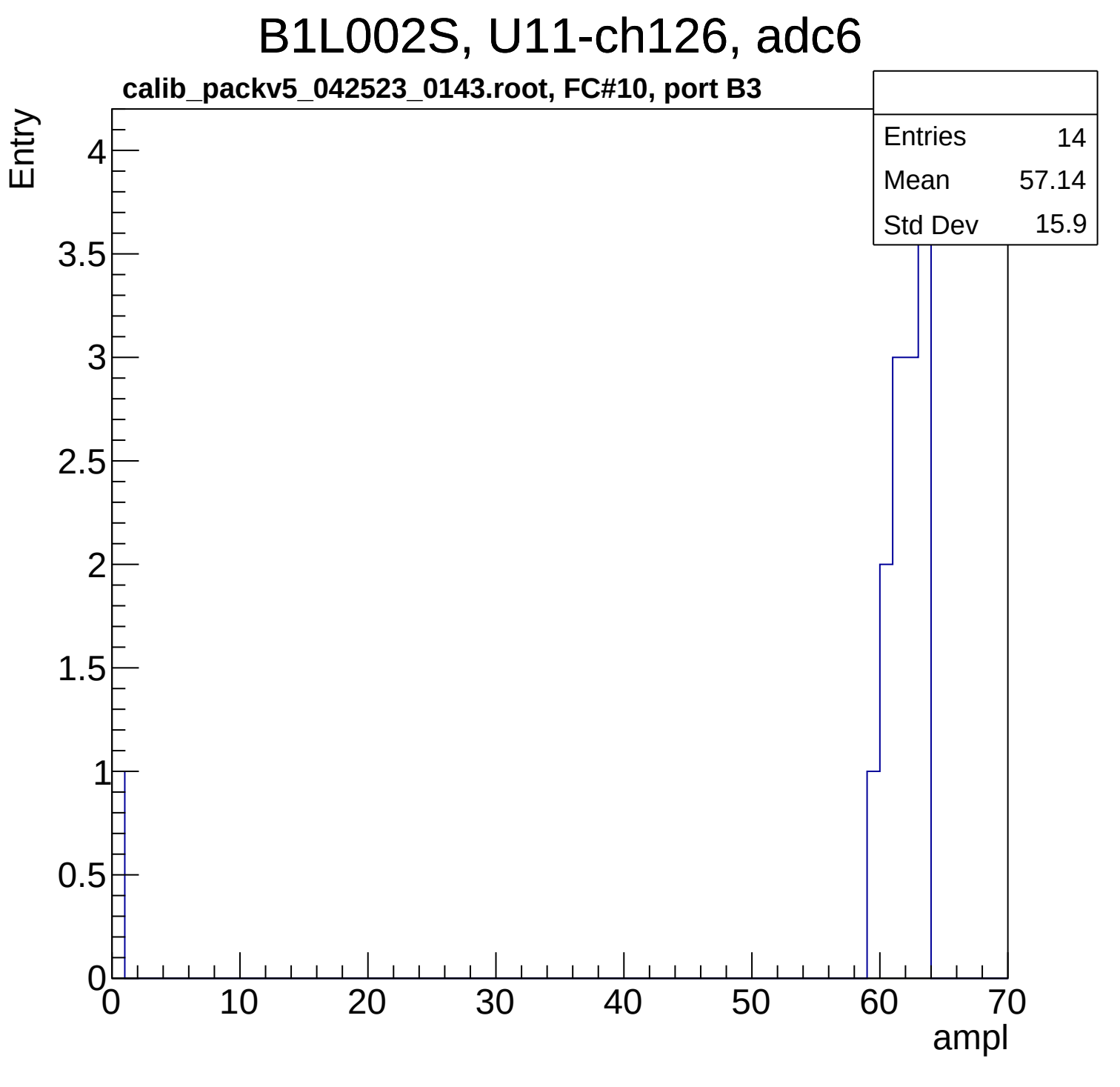
B1L002S, U11-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.14
Std Dev 15.9
Entry
ampl
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3
3.5
4
0
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60
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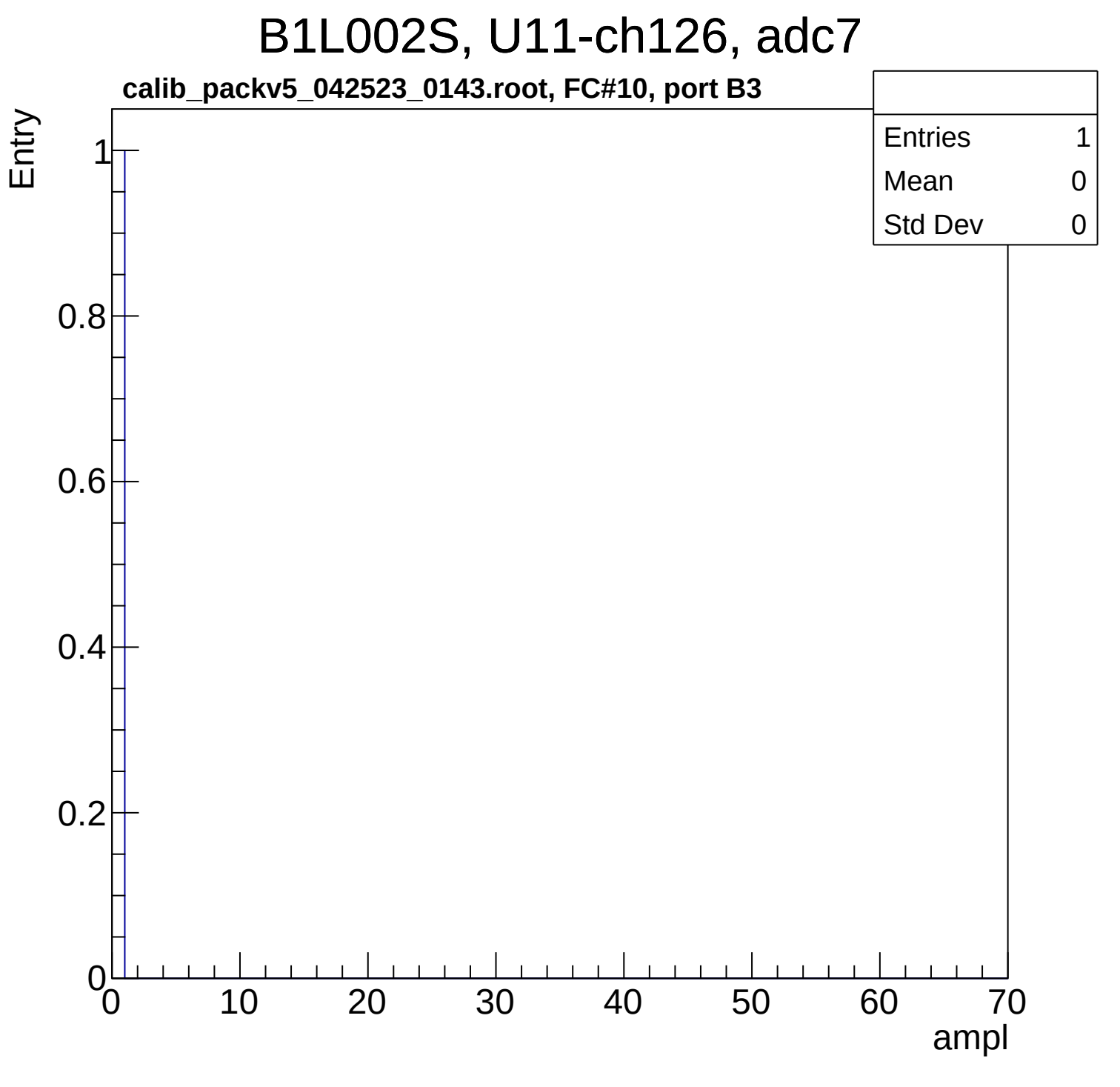
B1L002S, U11-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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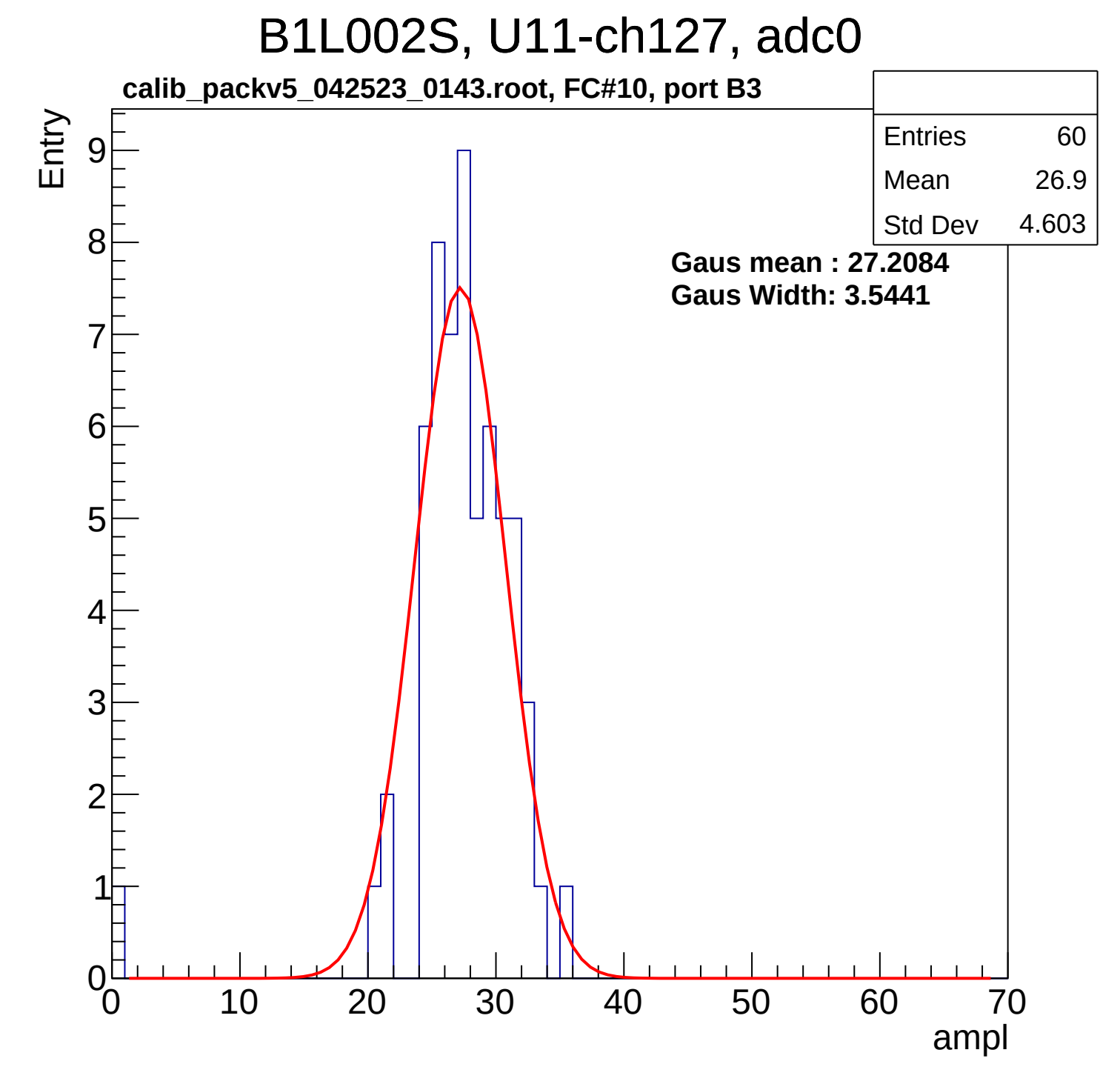
70
ampl


B1L002S, U11-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 26.9
Std Dev 4.603
Gaus mean : 27.2084
Gaus Width: 3.5441
Entry
ampl
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2
3
4
5
6
7
8
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0
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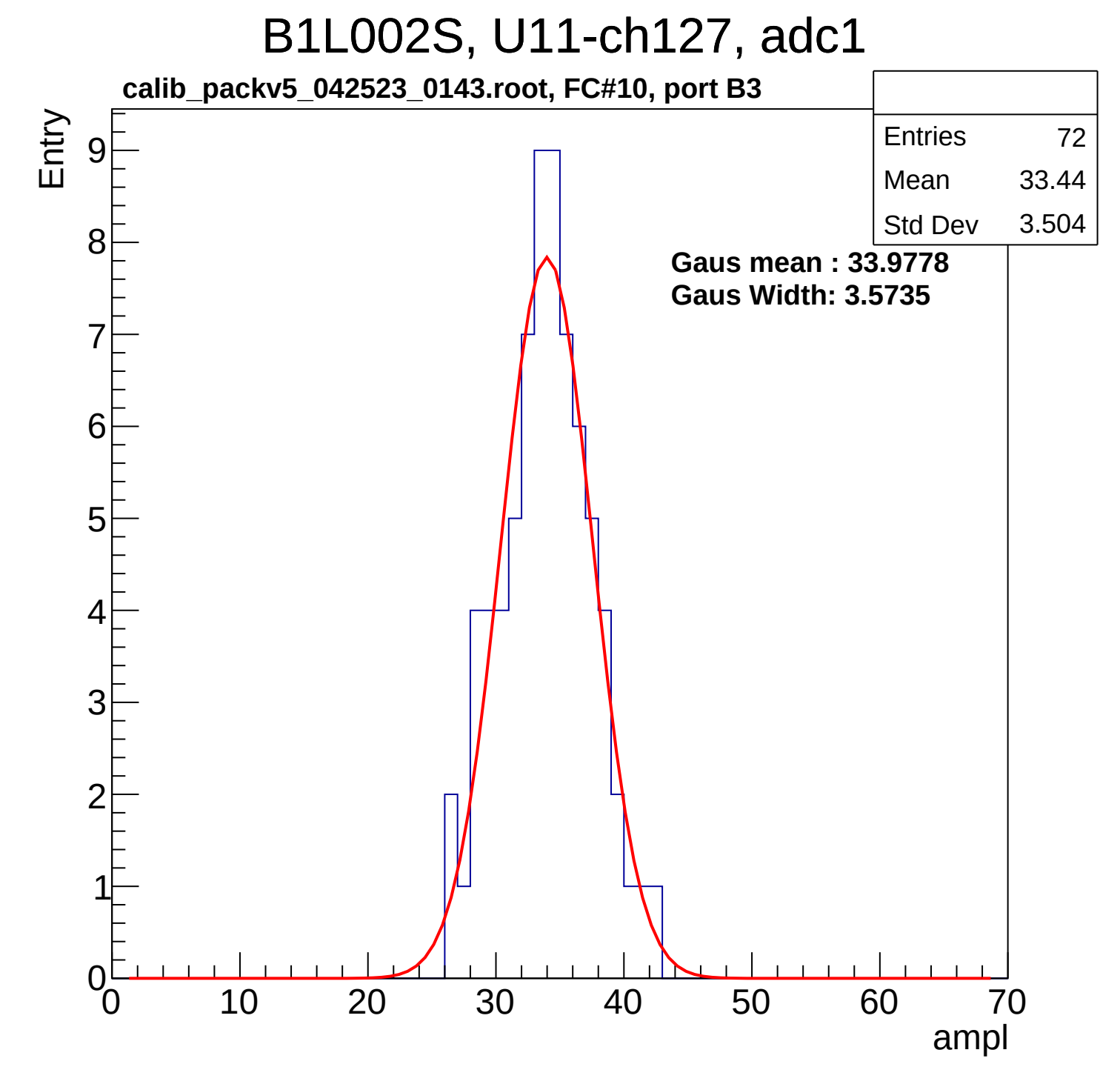
B1L002S, U11-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 33.44
Std Dev 3.504
Gaus mean : 33.9778
Gaus Width: 3.5735
Entry
ampl
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2
3
4
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6
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0
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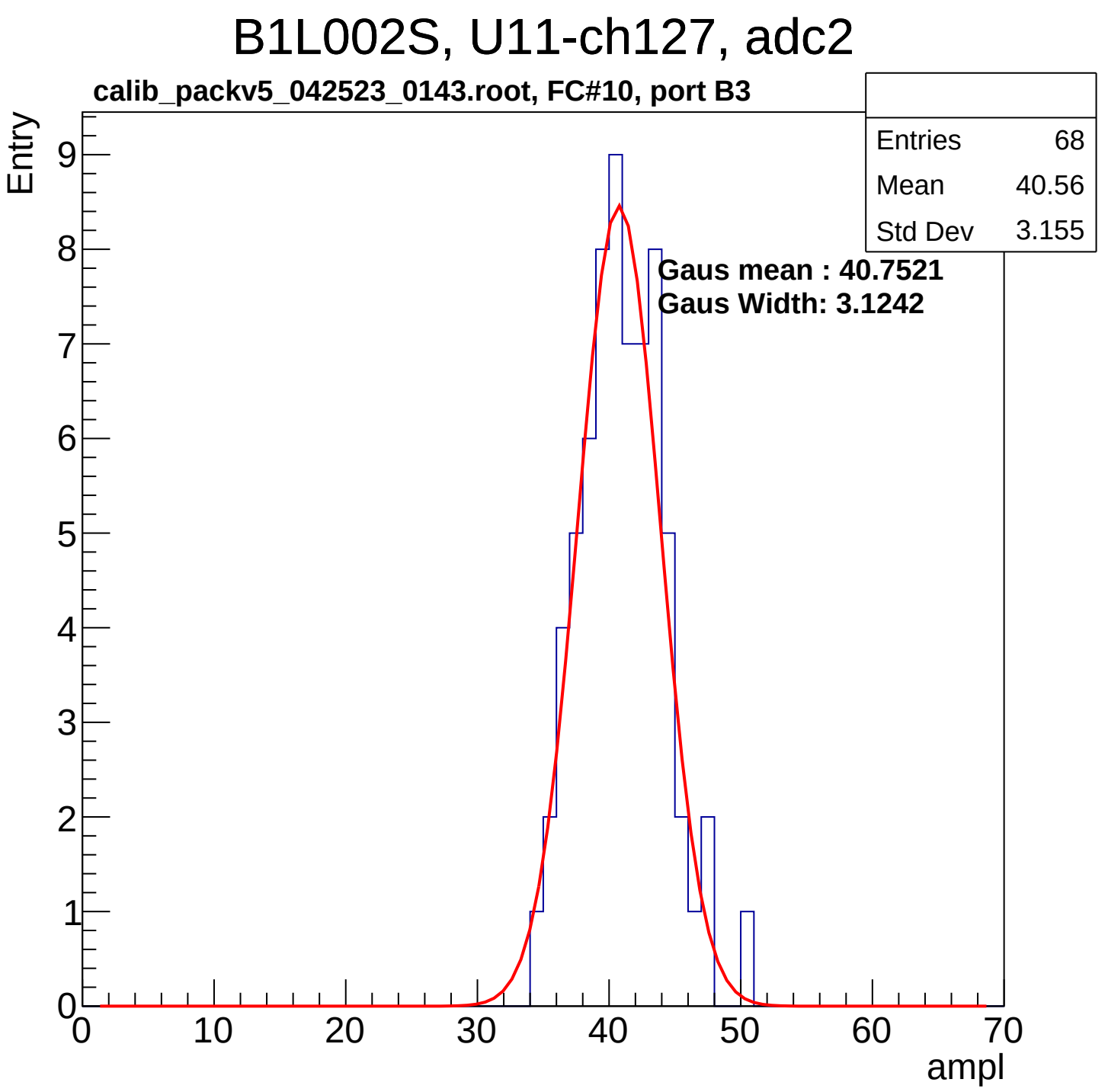
B1L002S, U11-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 40.56
Std Dev 3.155
Gaus mean : 40.7521
Gaus Width: 3.1242
Entry
ampl
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2
3
4
5
6
7
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0
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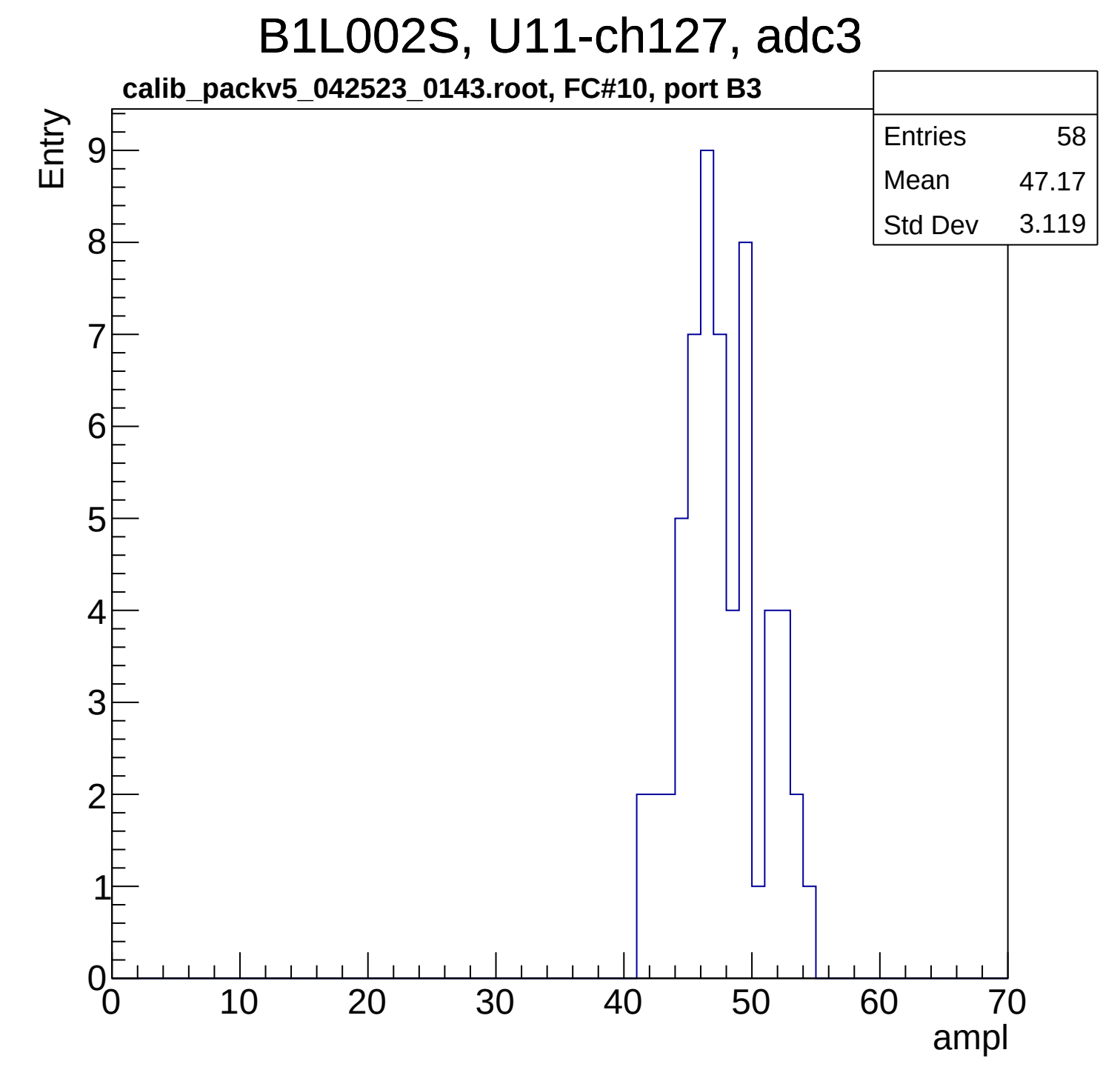
B1L002S, U11-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.17
Std Dev 3.119
Entry
ampl
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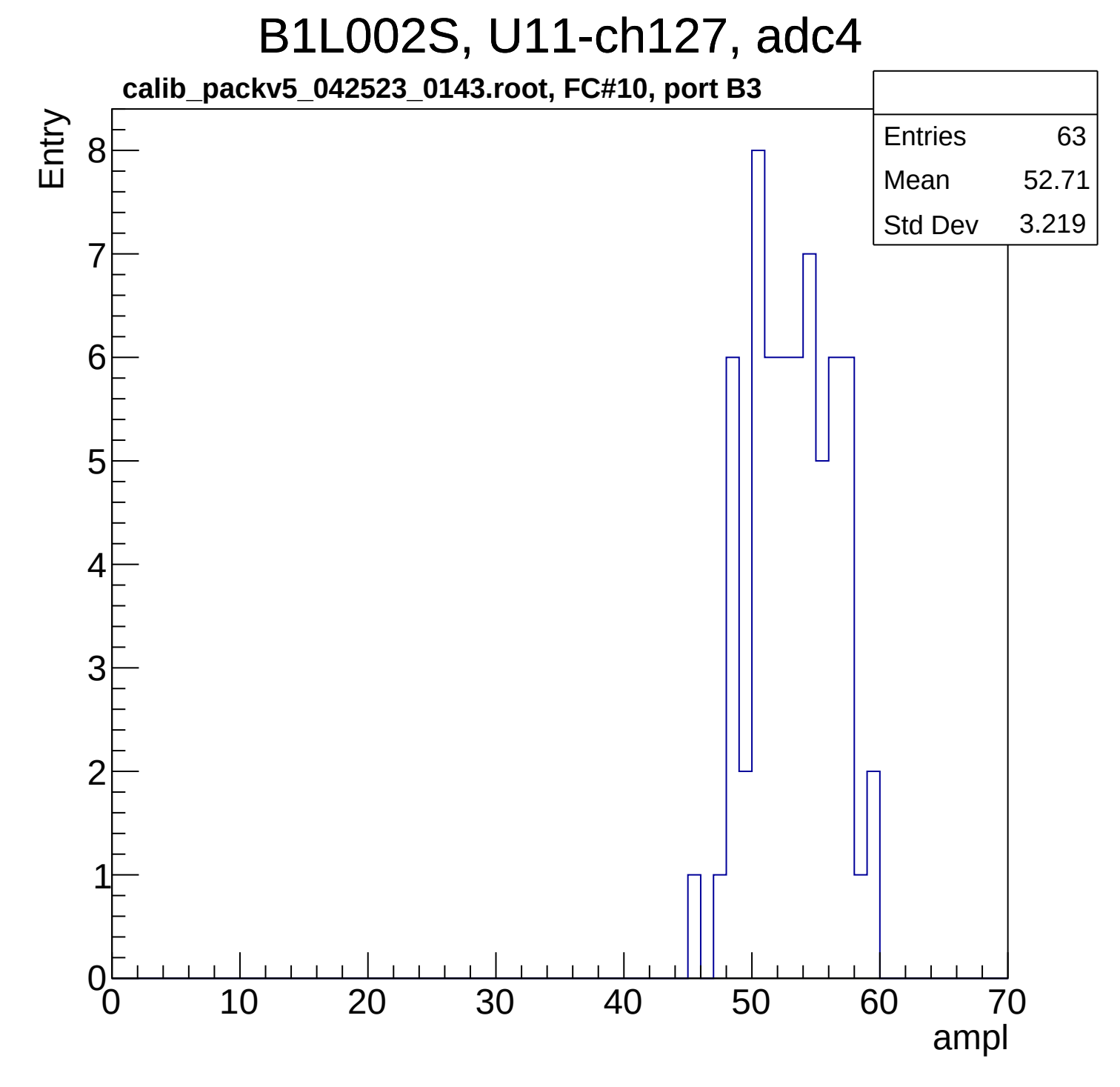
B1L002S, U11-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 52.71
Std Dev 3.219
Entry
ampl
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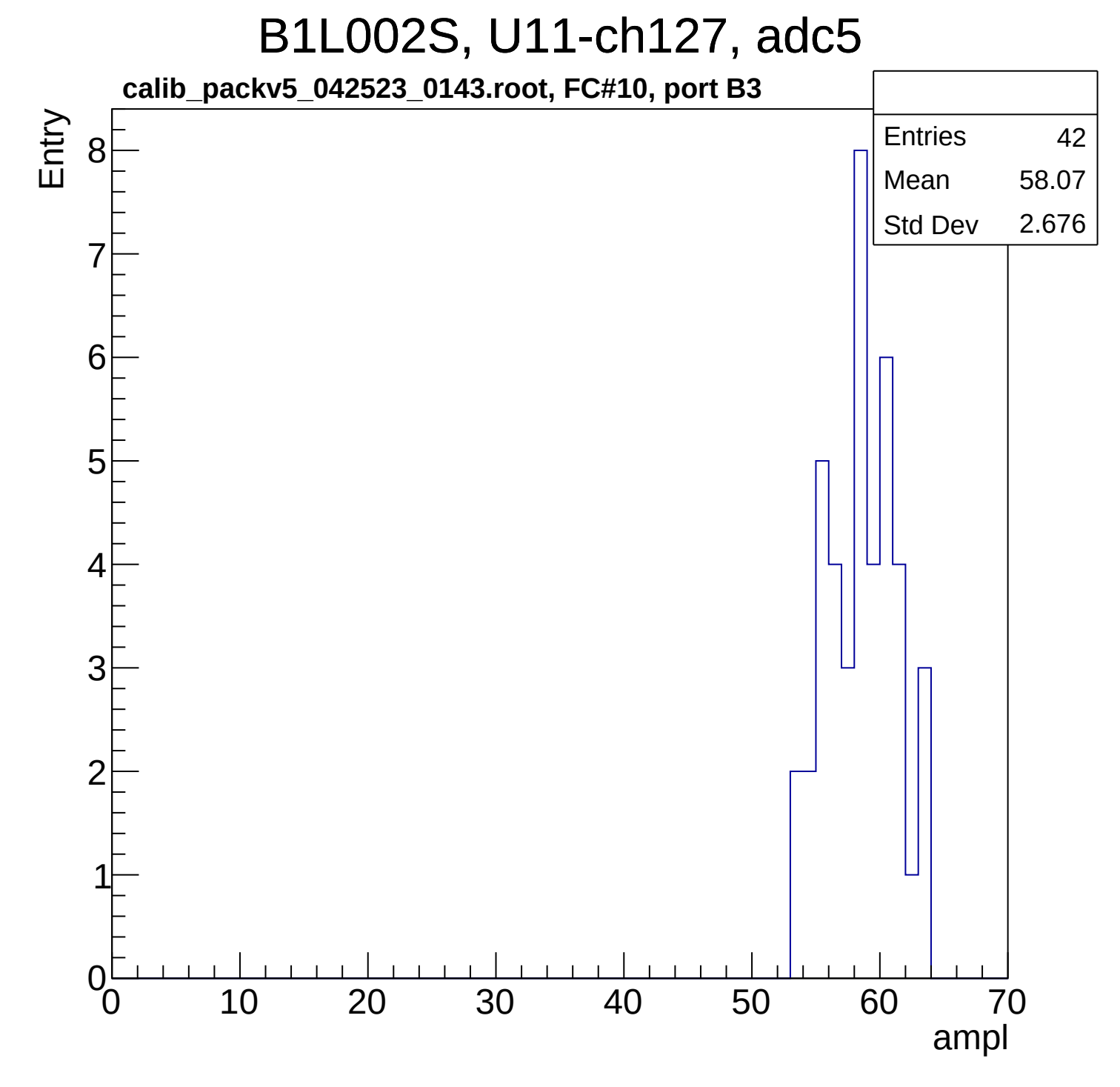
B1L002S, U11-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 58.07
Std Dev 2.676
Entry
ampl
0
1
2
3
4
5
6
7
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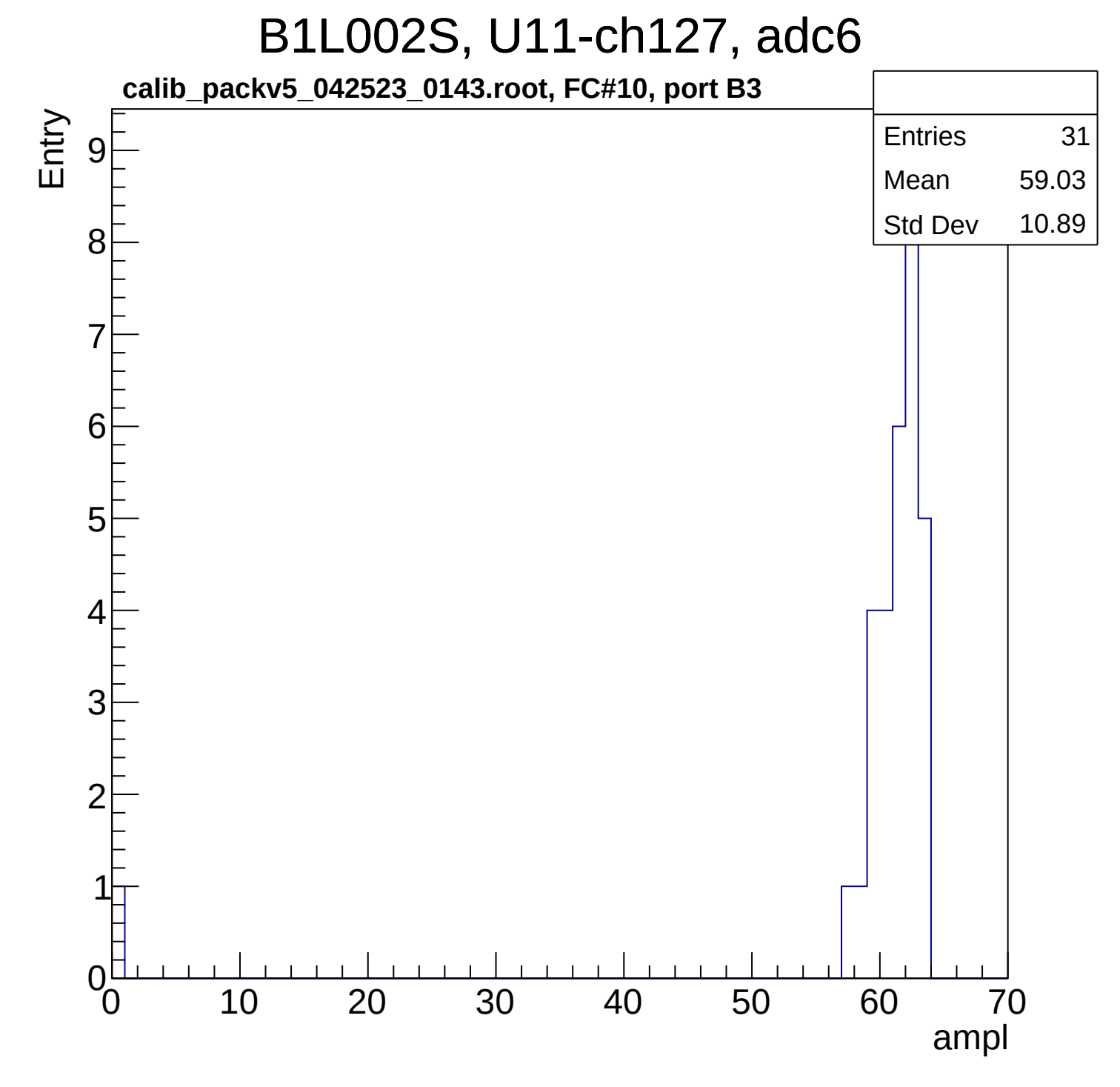
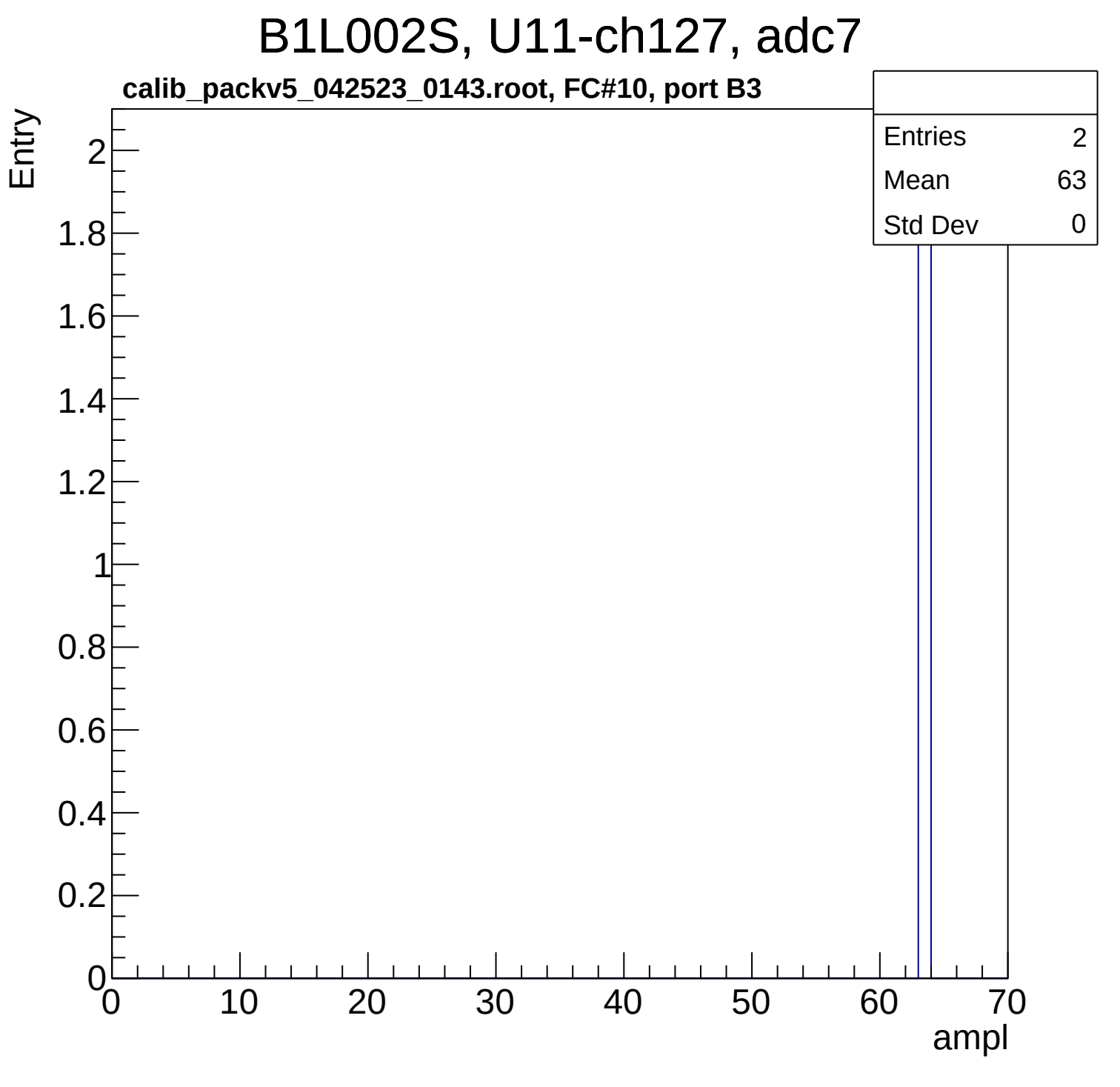
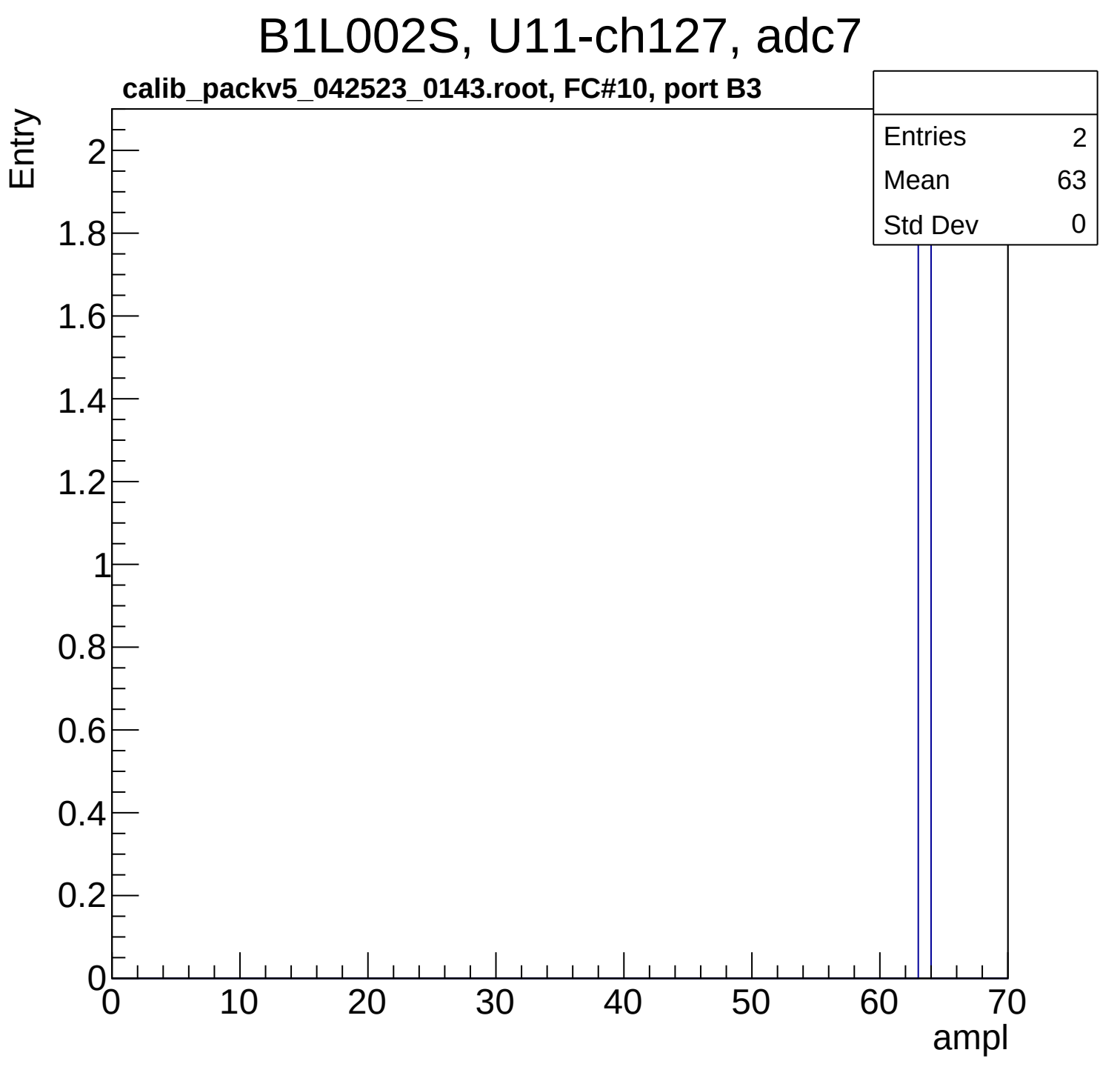
10
20
30
40
50
60
70


B1L002S, U11-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 59.03
Std Dev 10.89
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U11-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U11-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70