

B1L102S, U13-ch0, adc0

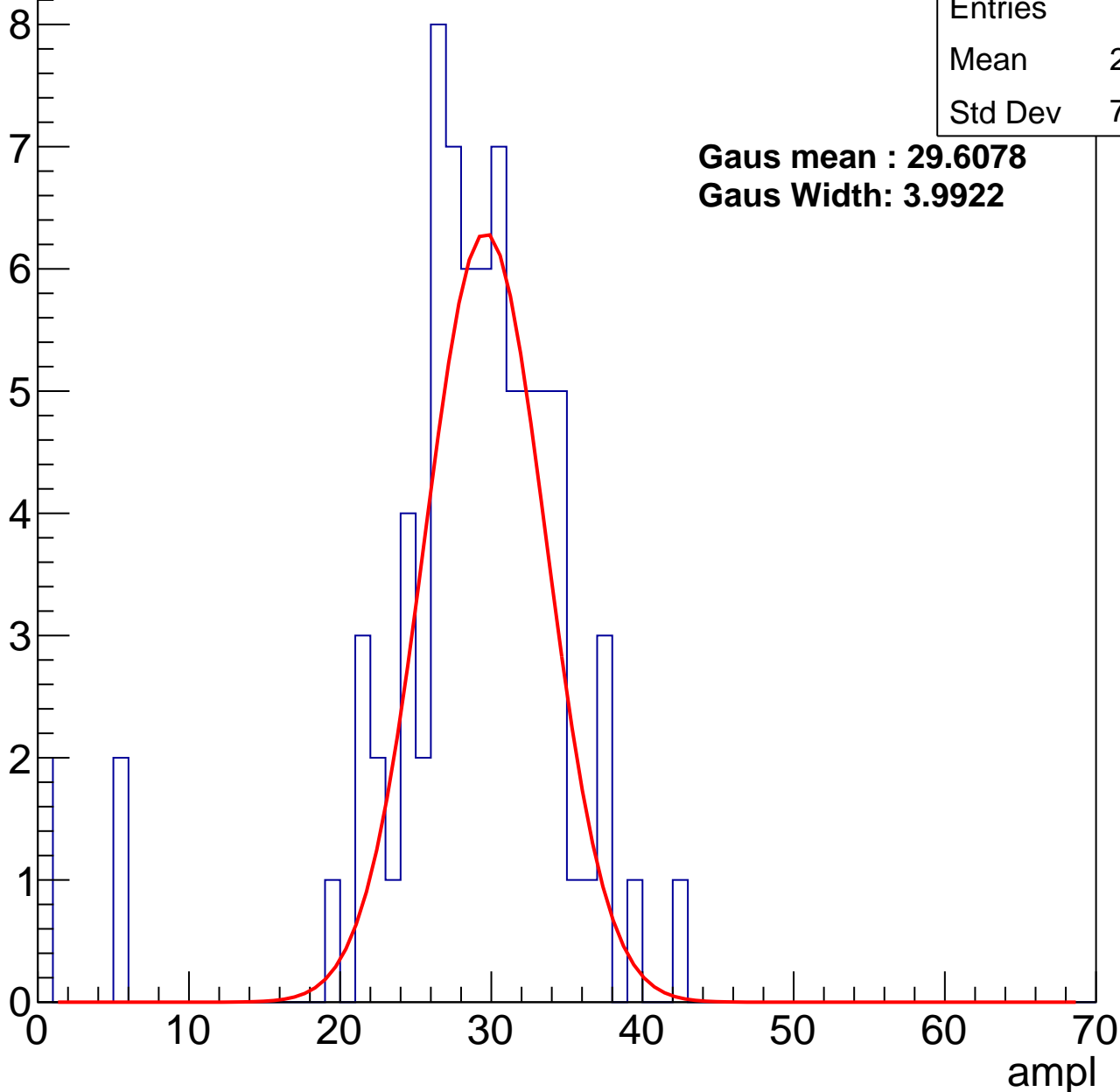
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	27.78
Std Dev	7.343

Gaus mean : 29.6078

Gaus Width: 3.9922



B1L102S, U13-ch0, adc1

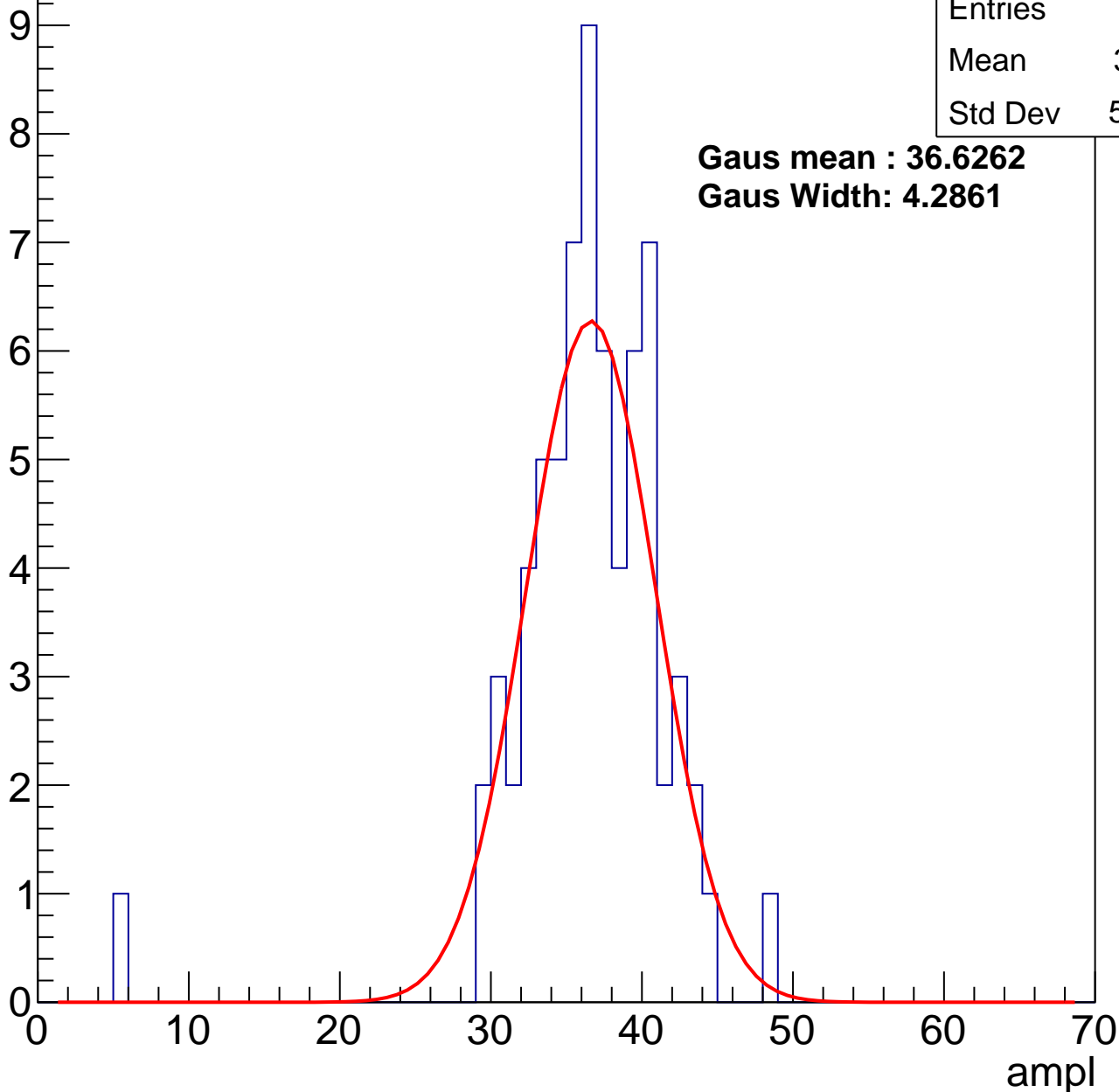
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.01
Std Dev	5.352

Gaus mean : 36.6262

Gaus Width: 4.2861



B1L102S, U13-ch0, adc2

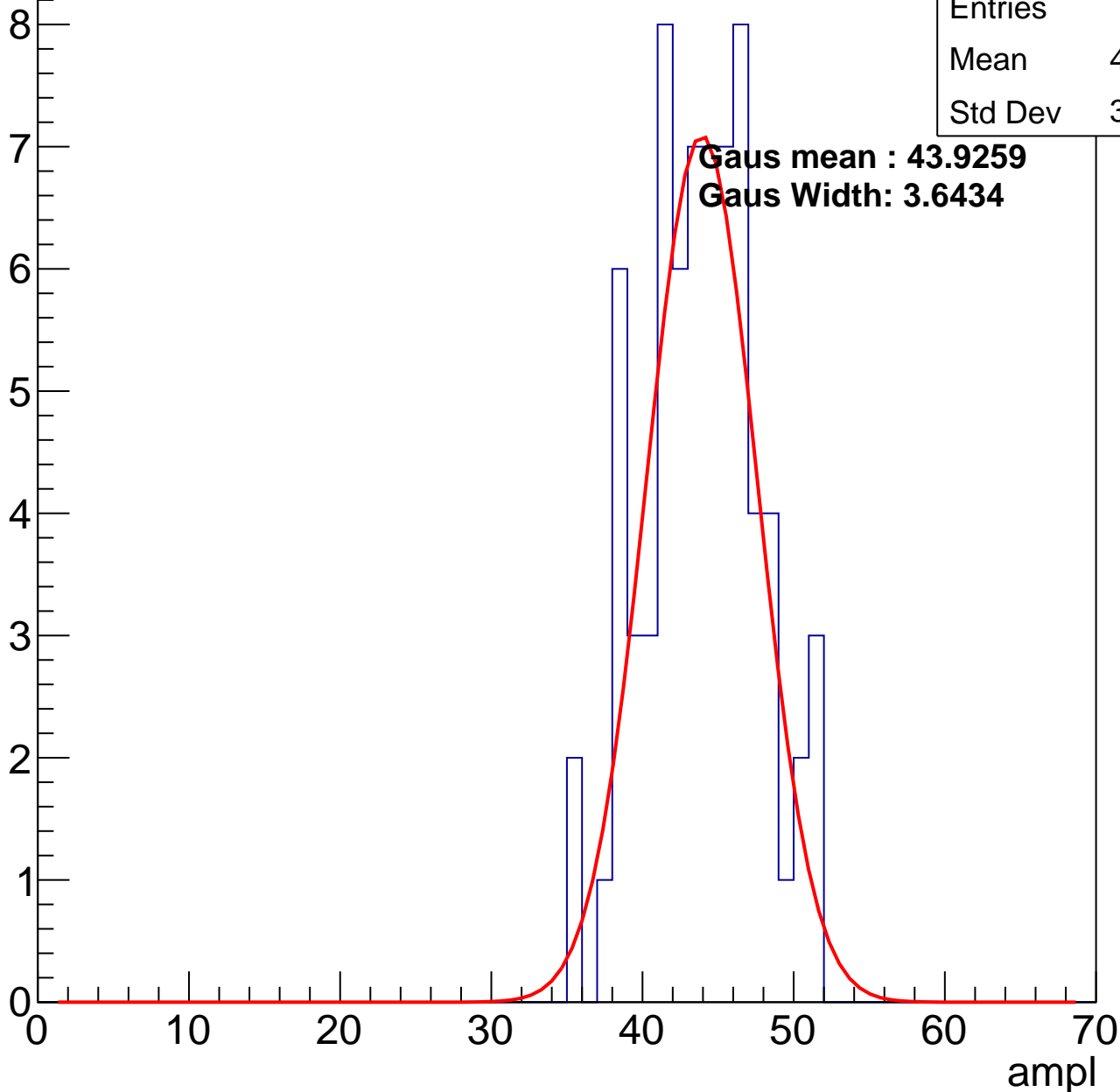
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.42
Std Dev	3.748

Gaus mean : 43.9259

Gaus Width: 3.6434

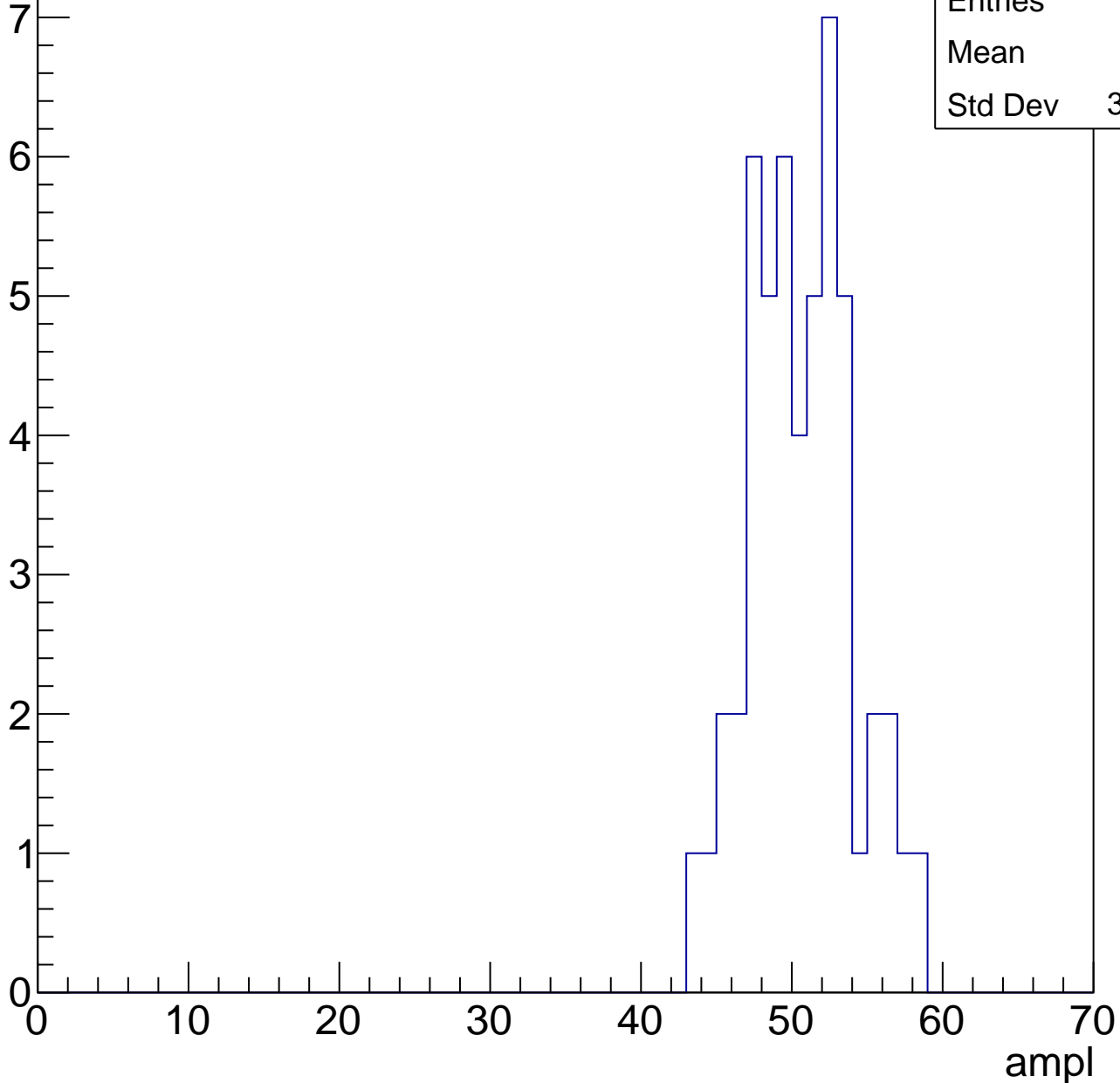


B1L102S, U13-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

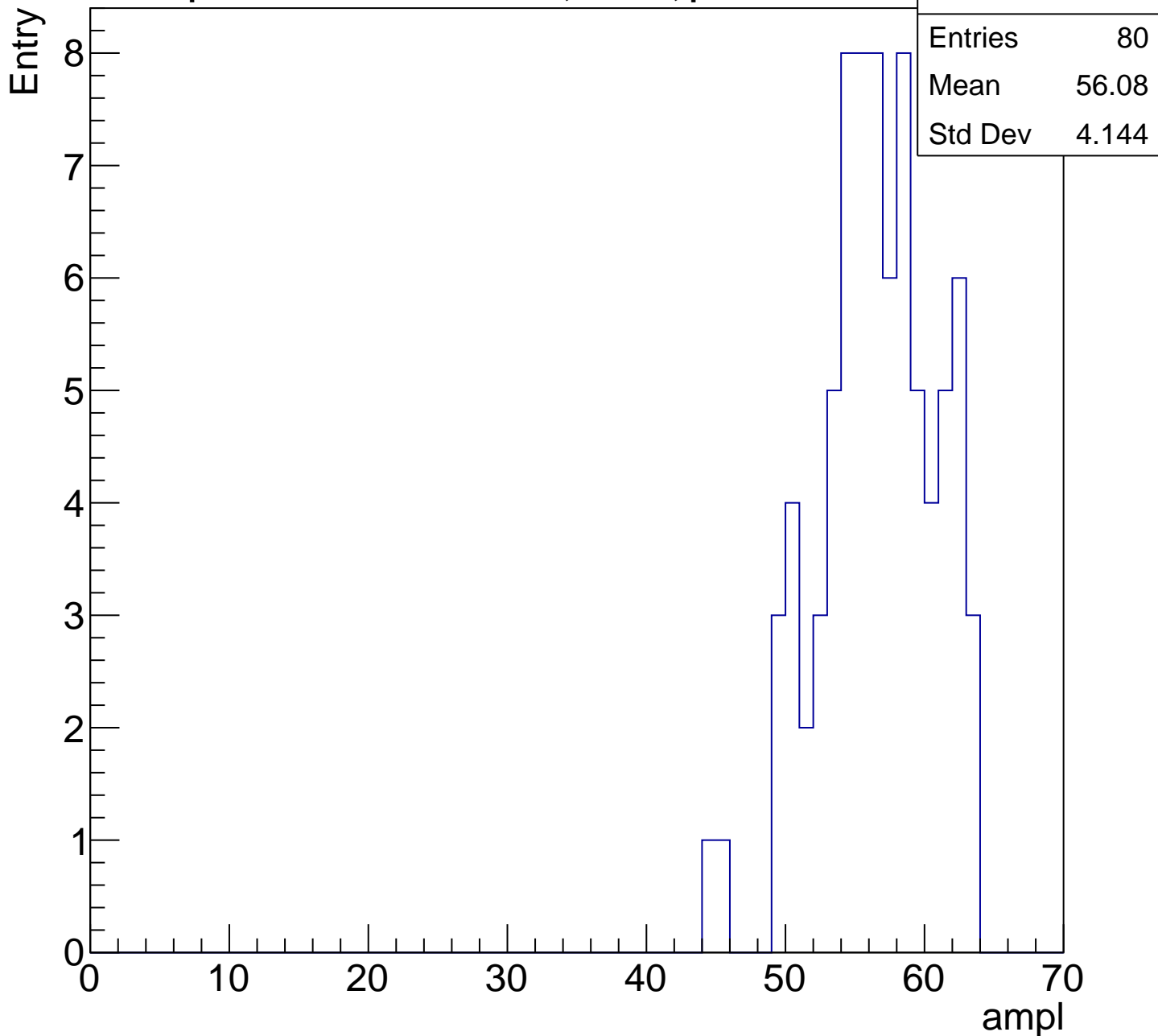
Entry

Entries	51
Mean	50.2
Std Dev	3.355



B1L102S, U13-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

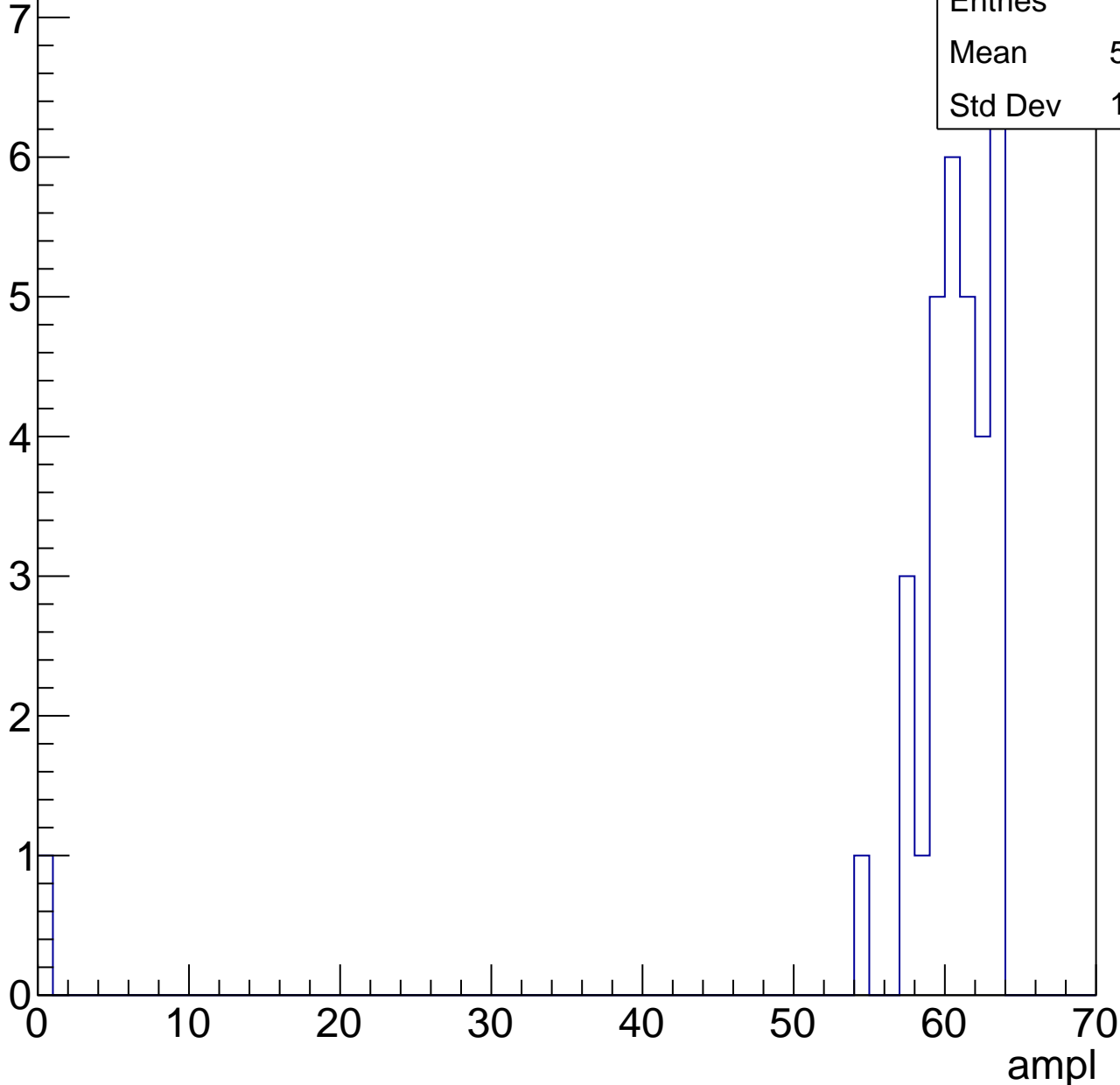


B1L102S, U13-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	58.55
Std Dev	10.57



B1L102S, U13-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

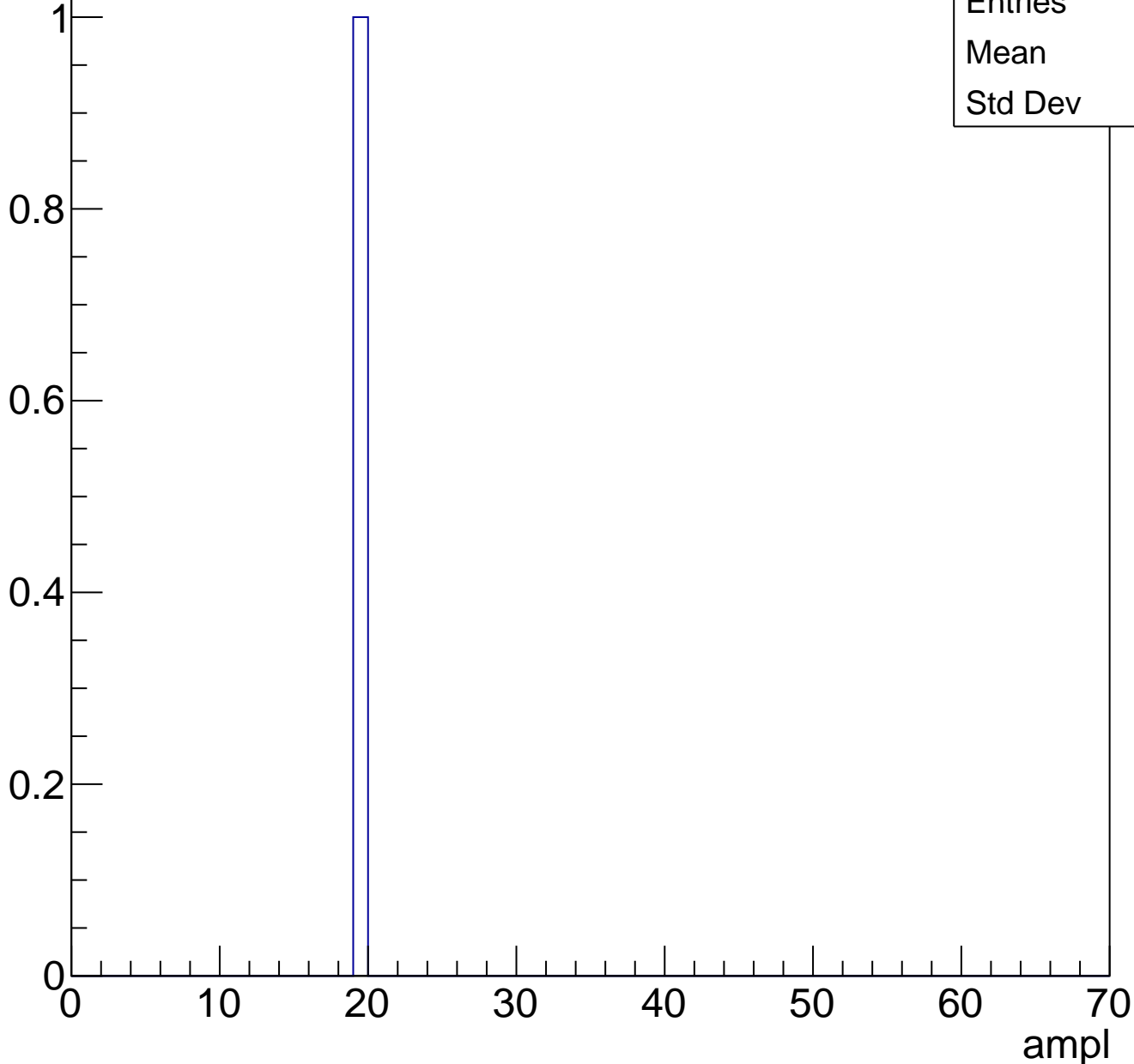


Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch1, adc0

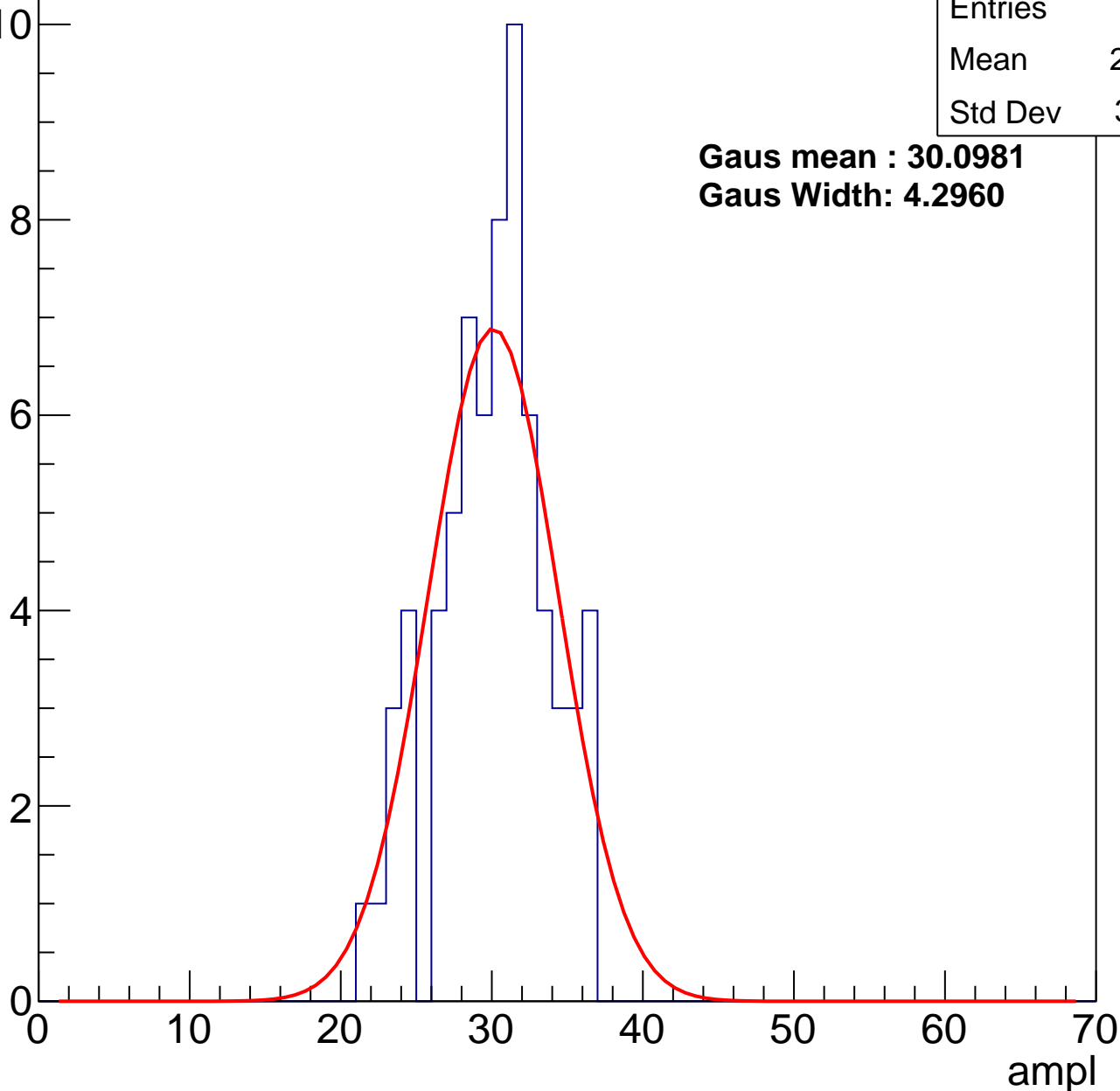
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.59
Std Dev	3.621

Gaus mean : 30.0981

Gaus Width: 4.2960



B1L102S, U13-ch1, adc1

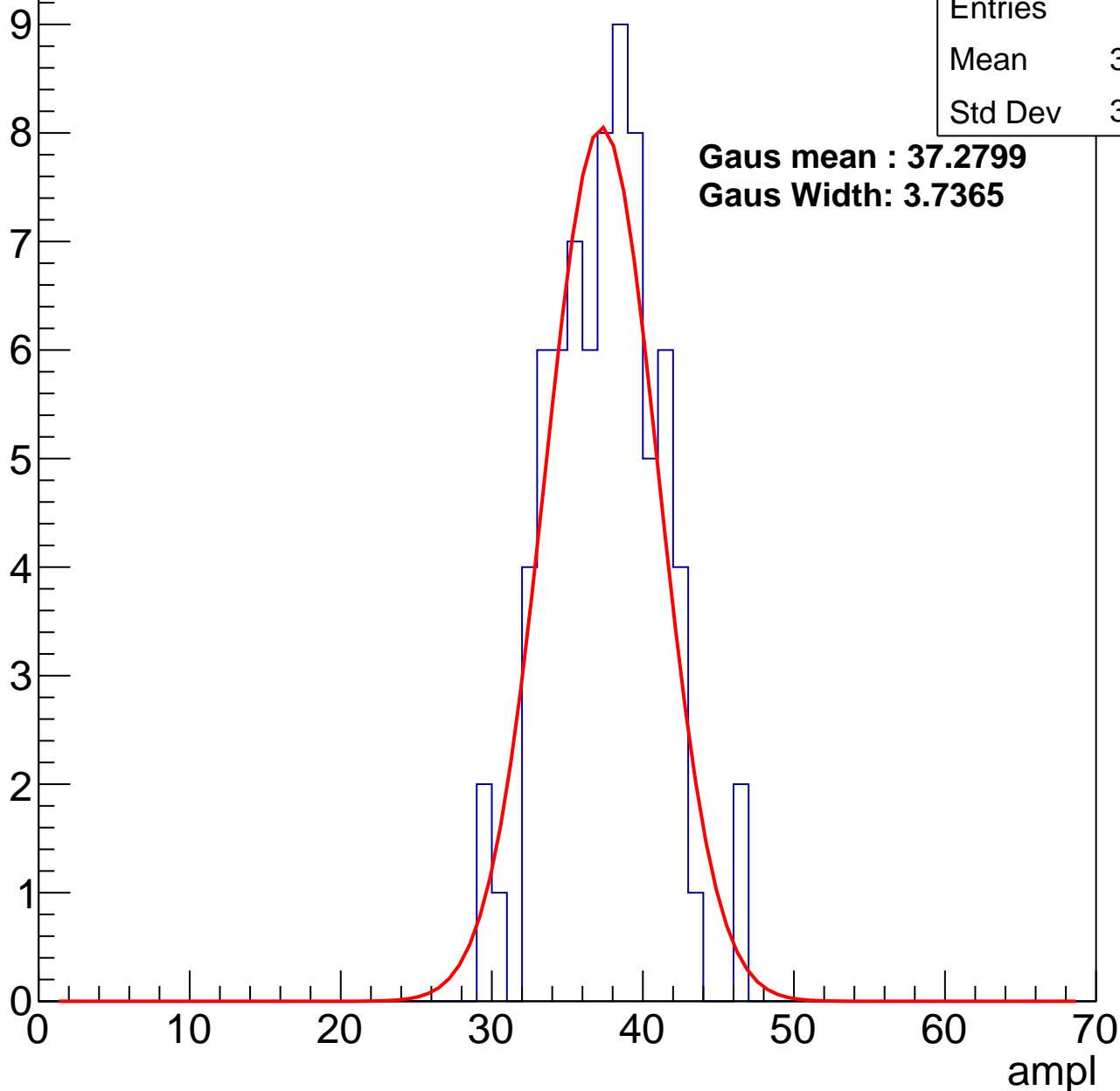
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	37.04
Std Dev	3.542

Gaus mean : 37.2799

Gaus Width: 3.7365



B1L102S, U13-ch1, adc2

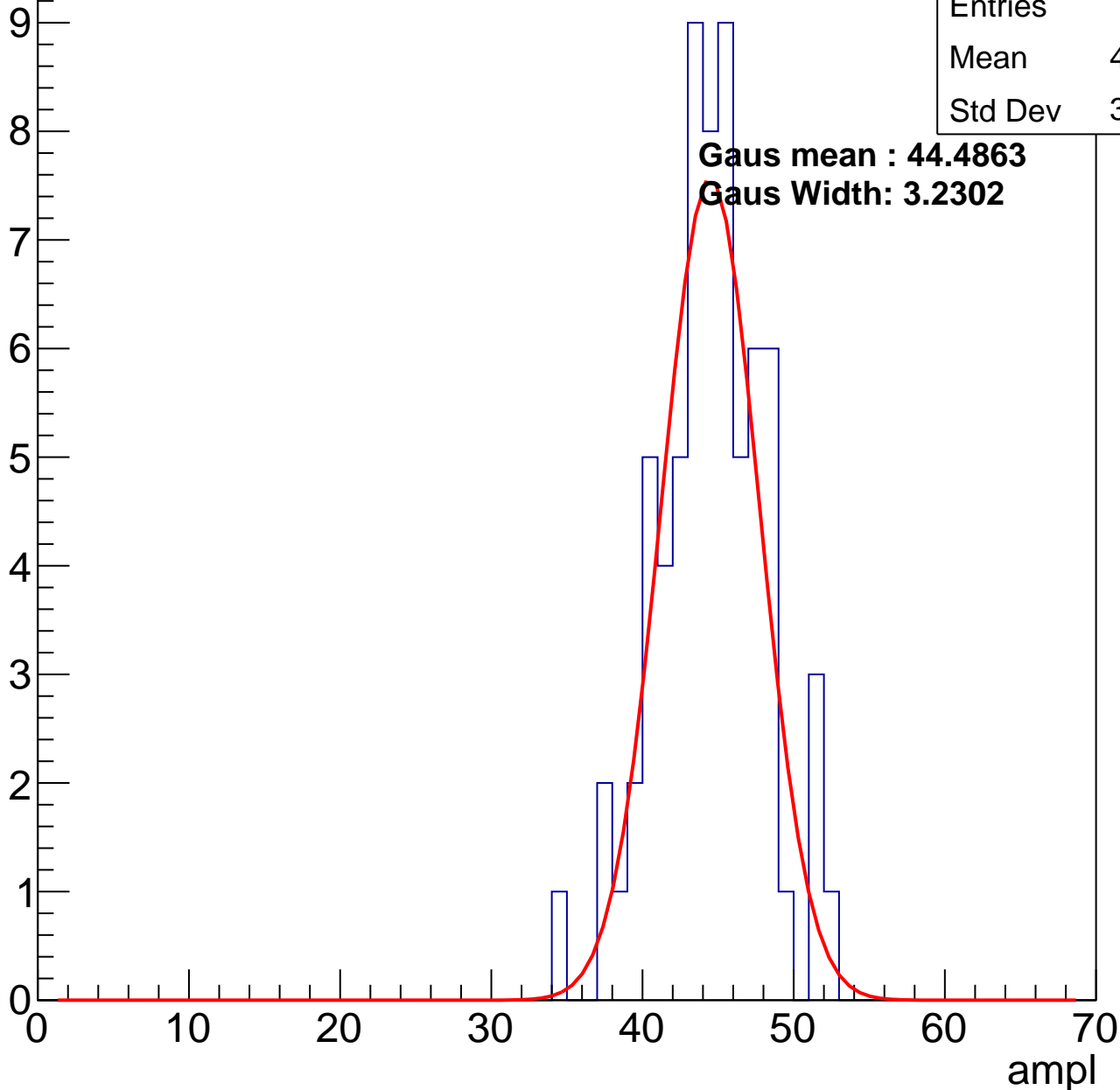
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	44.06
Std Dev	3.523

Gaus mean : 44.4863

Gaus Width: 3.2302

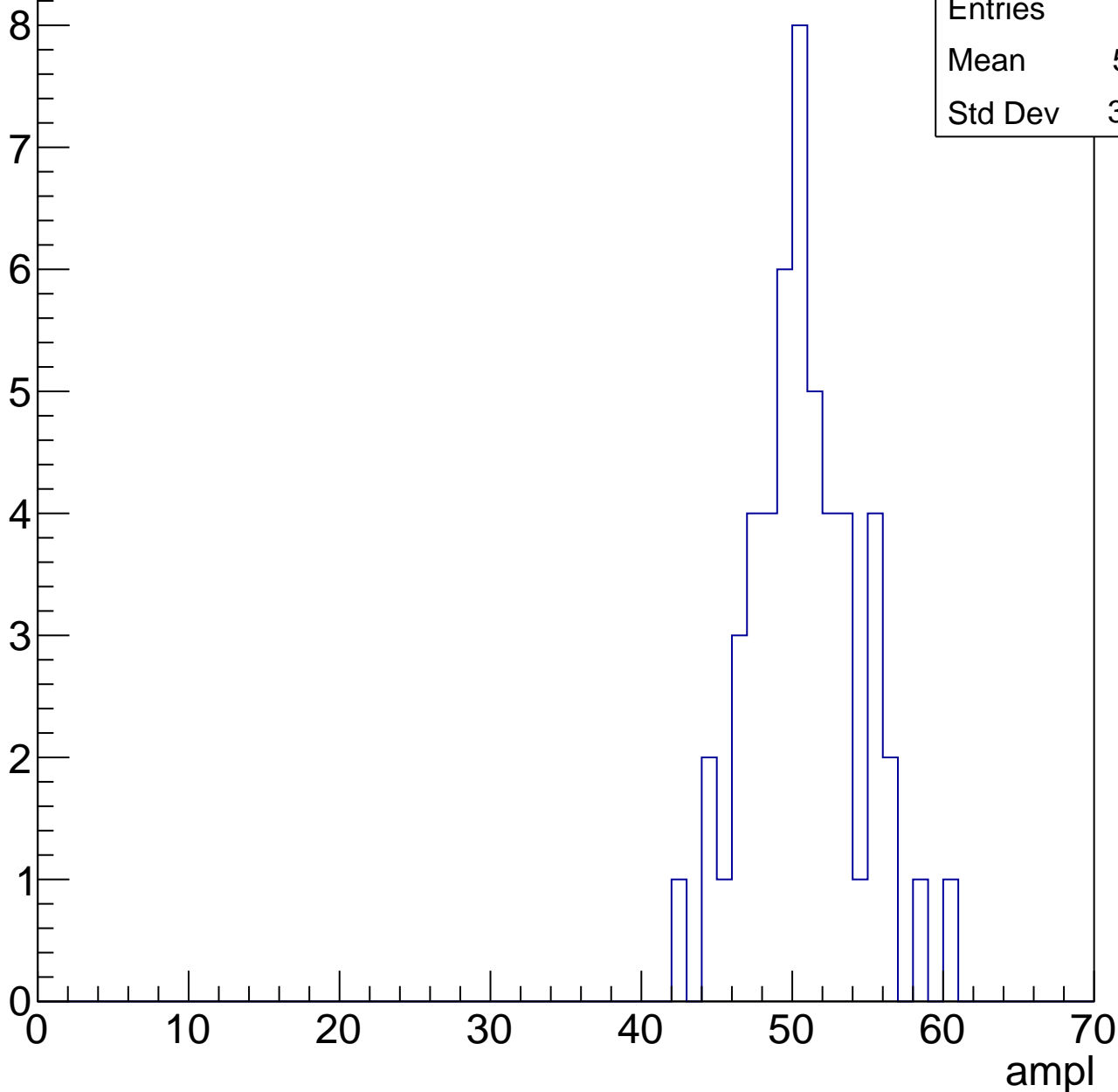


B1L102S, U13-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

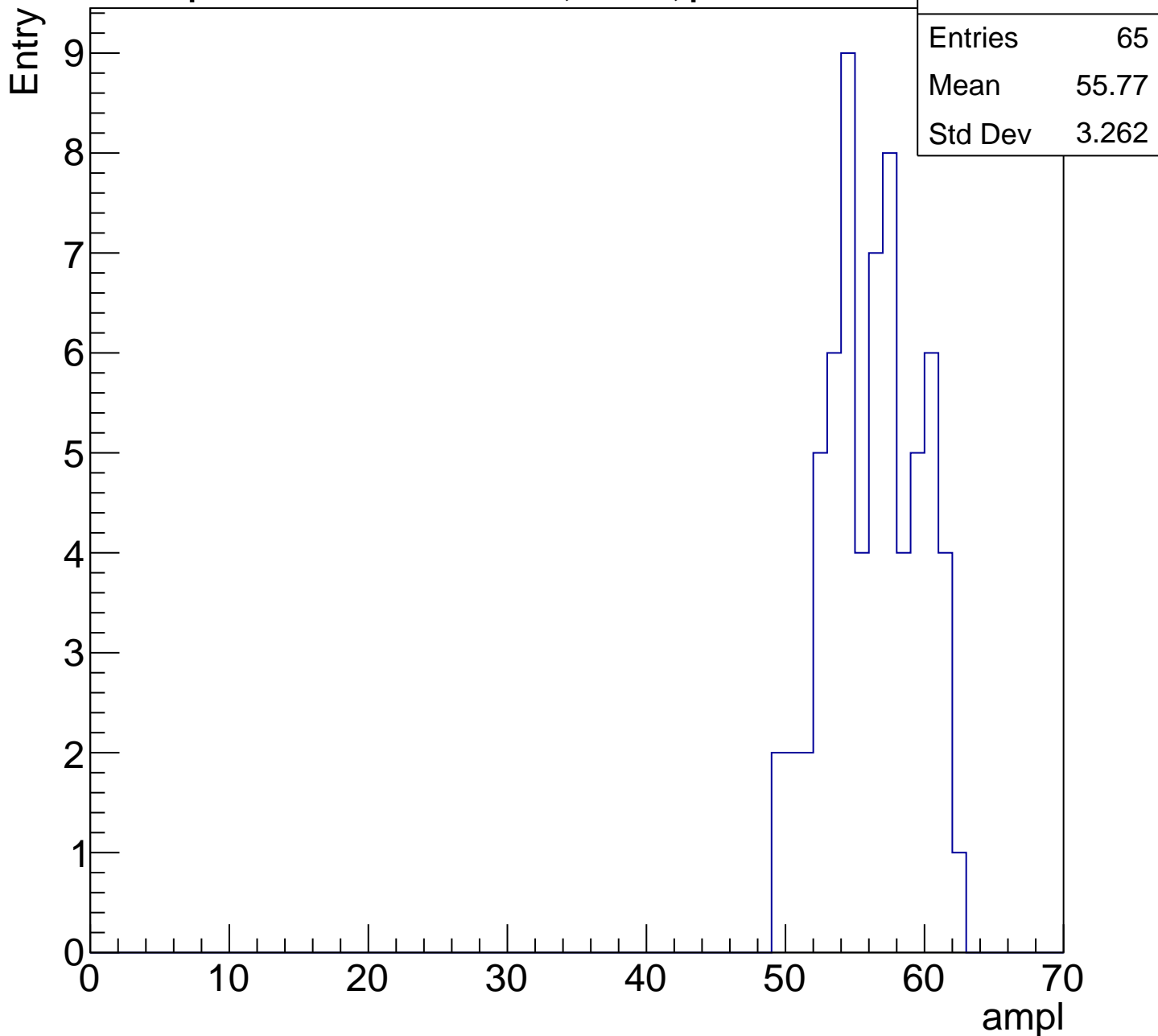
Entry

Entries	51
Mean	50.31
Std Dev	3.627



B1L102S, U13-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

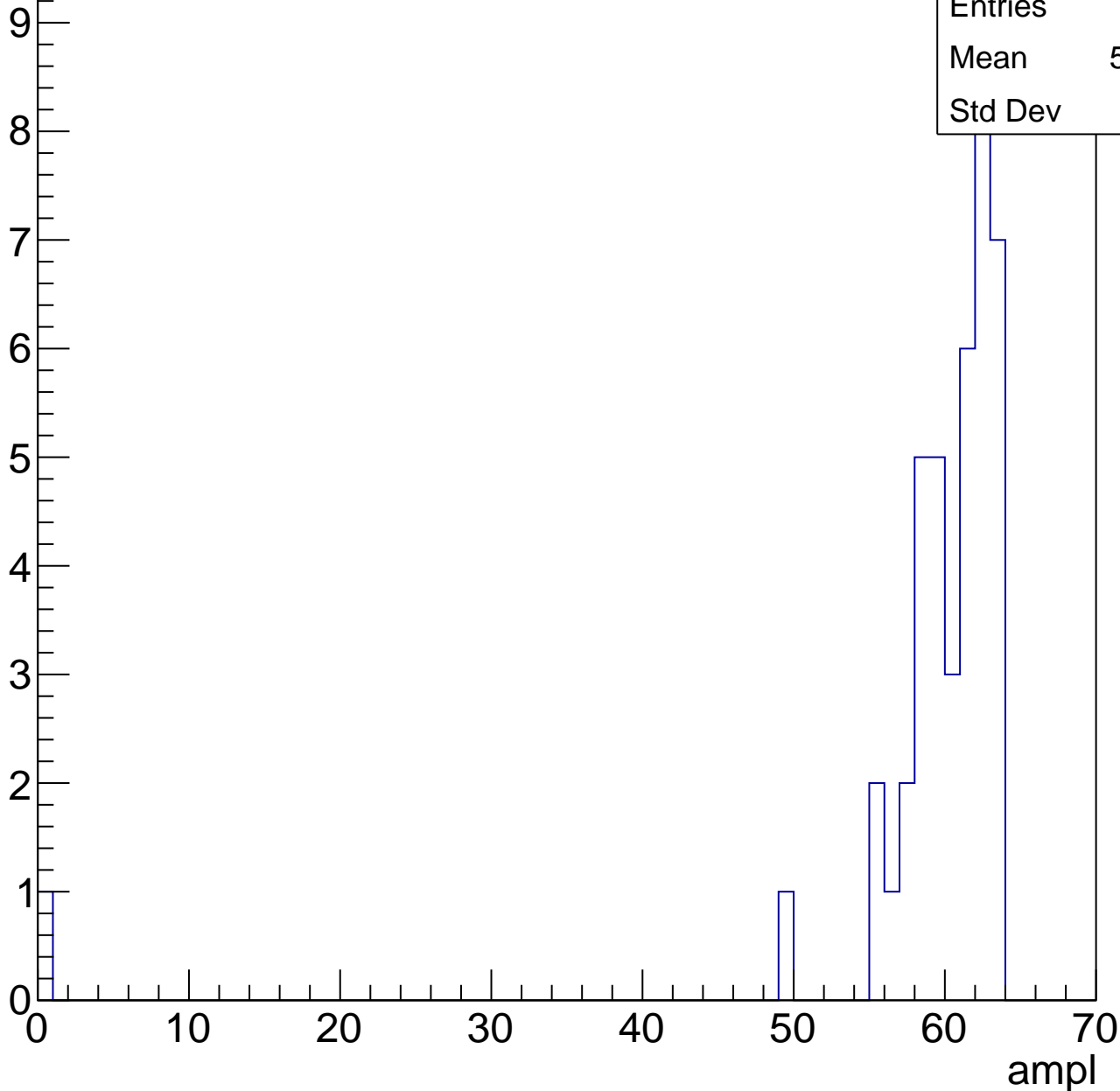


B1L102S, U13-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.55
Std Dev	9.57



B1L102S, U13-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch2, adc0

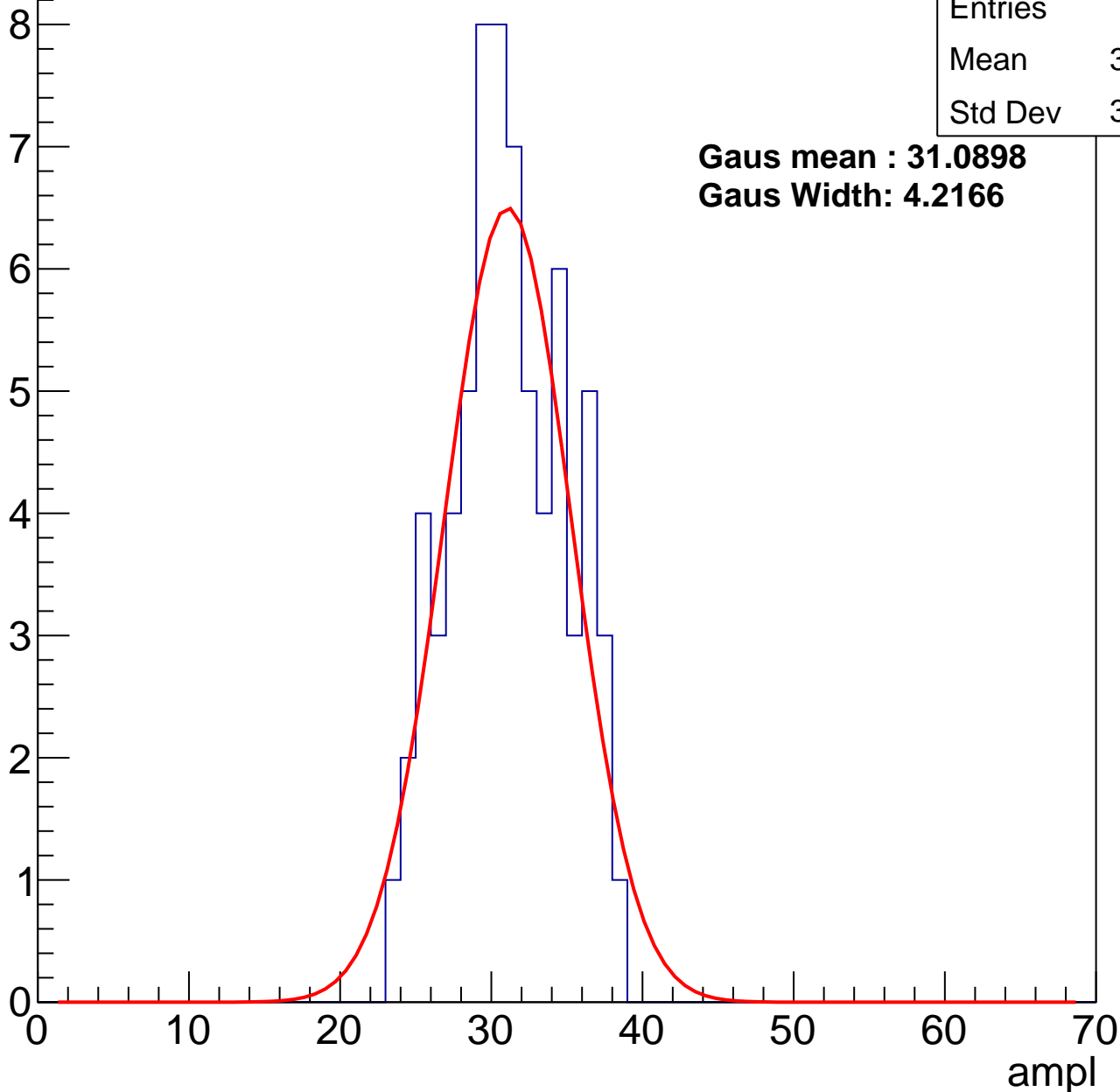
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	30.67
Std Dev	3.666

Gaus mean : 31.0898

Gaus Width: 4.2166



B1L102S, U13-ch2, adc1

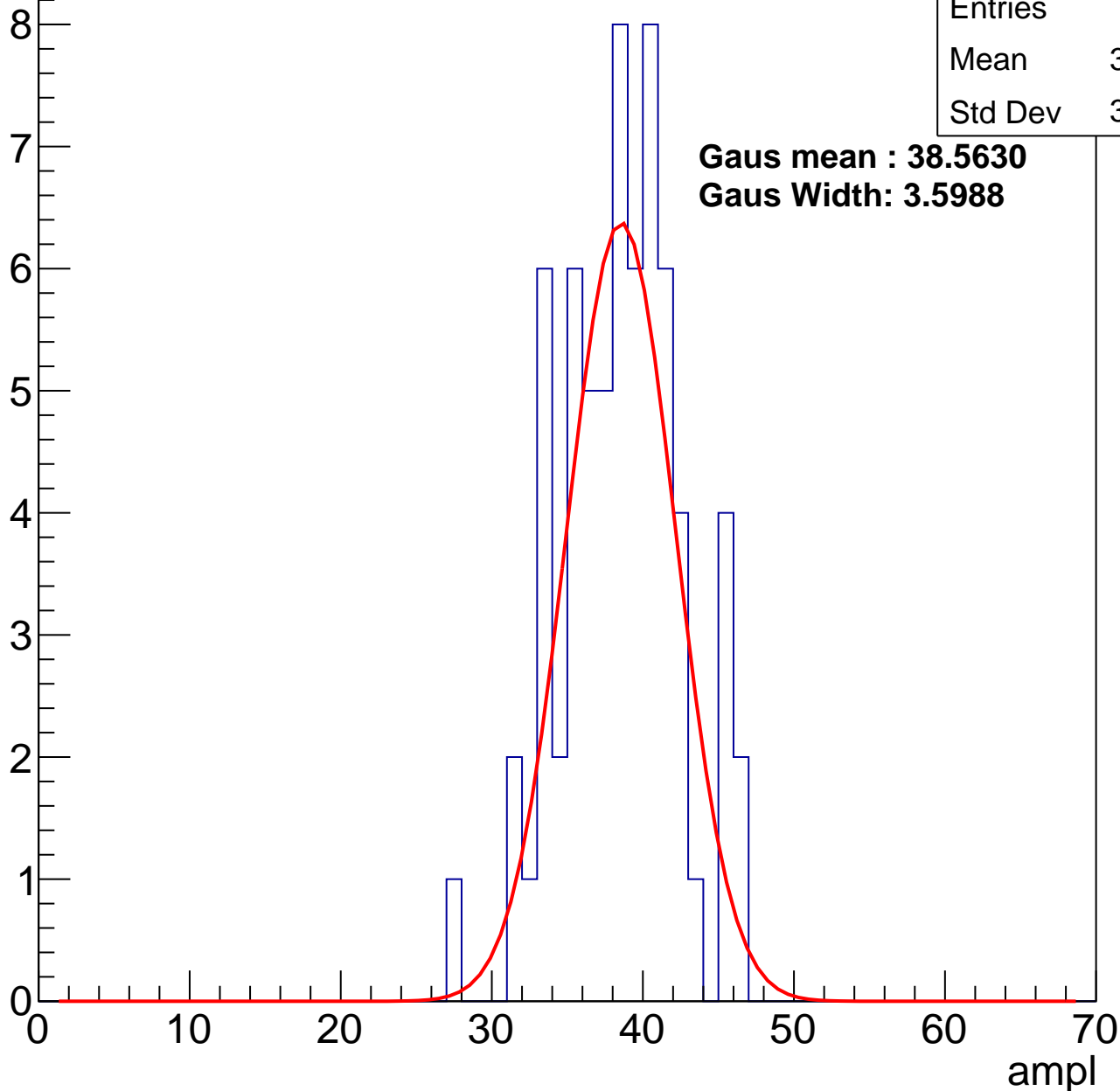
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	38.04
Std Dev	3.903

Gaus mean : 38.5630

Gaus Width: 3.5988



B1L102S, U13-ch2, adc2

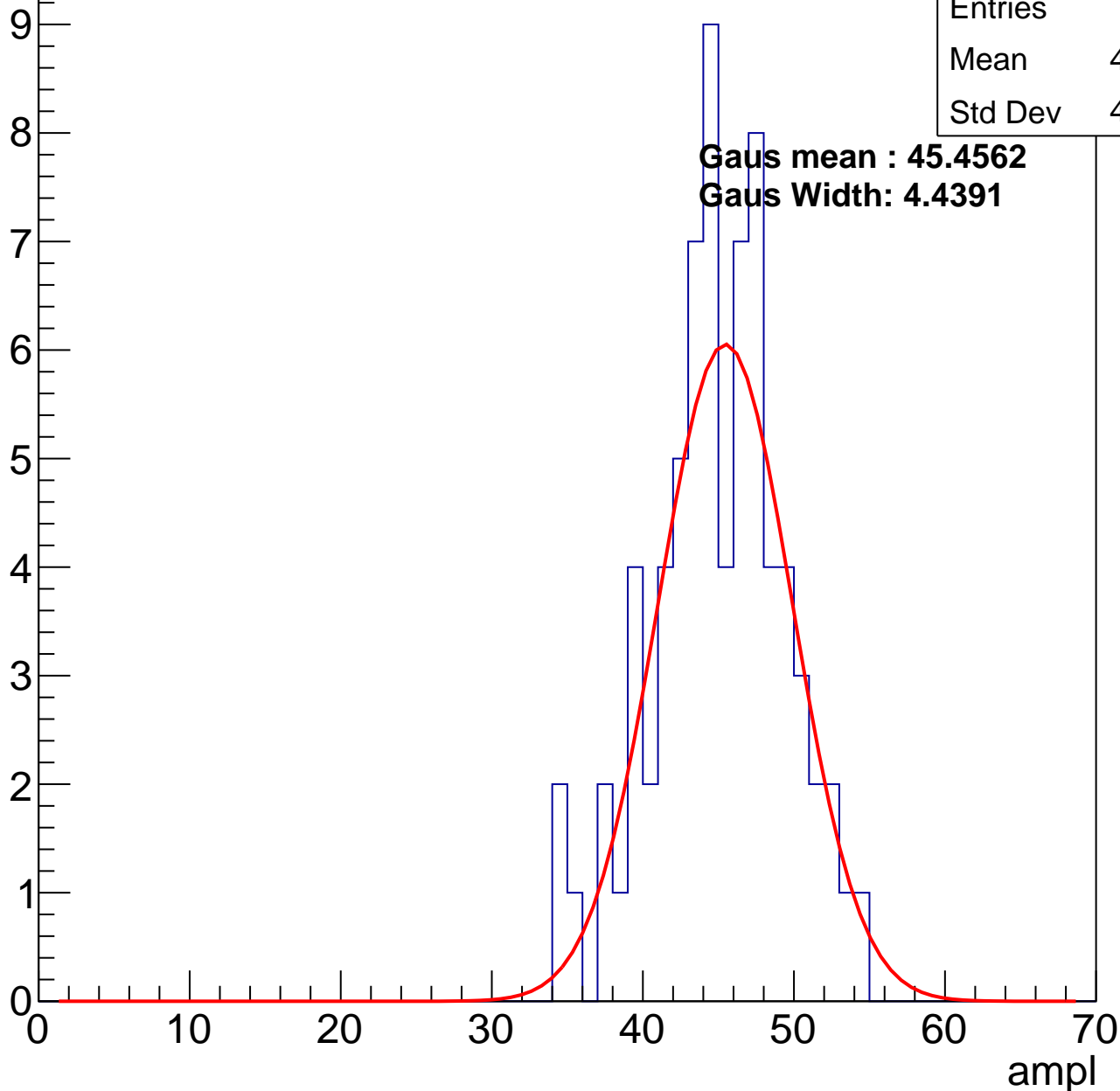
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	44.53
Std Dev	4.336

Gaus mean : 45.4562

Gaus Width: 4.4391

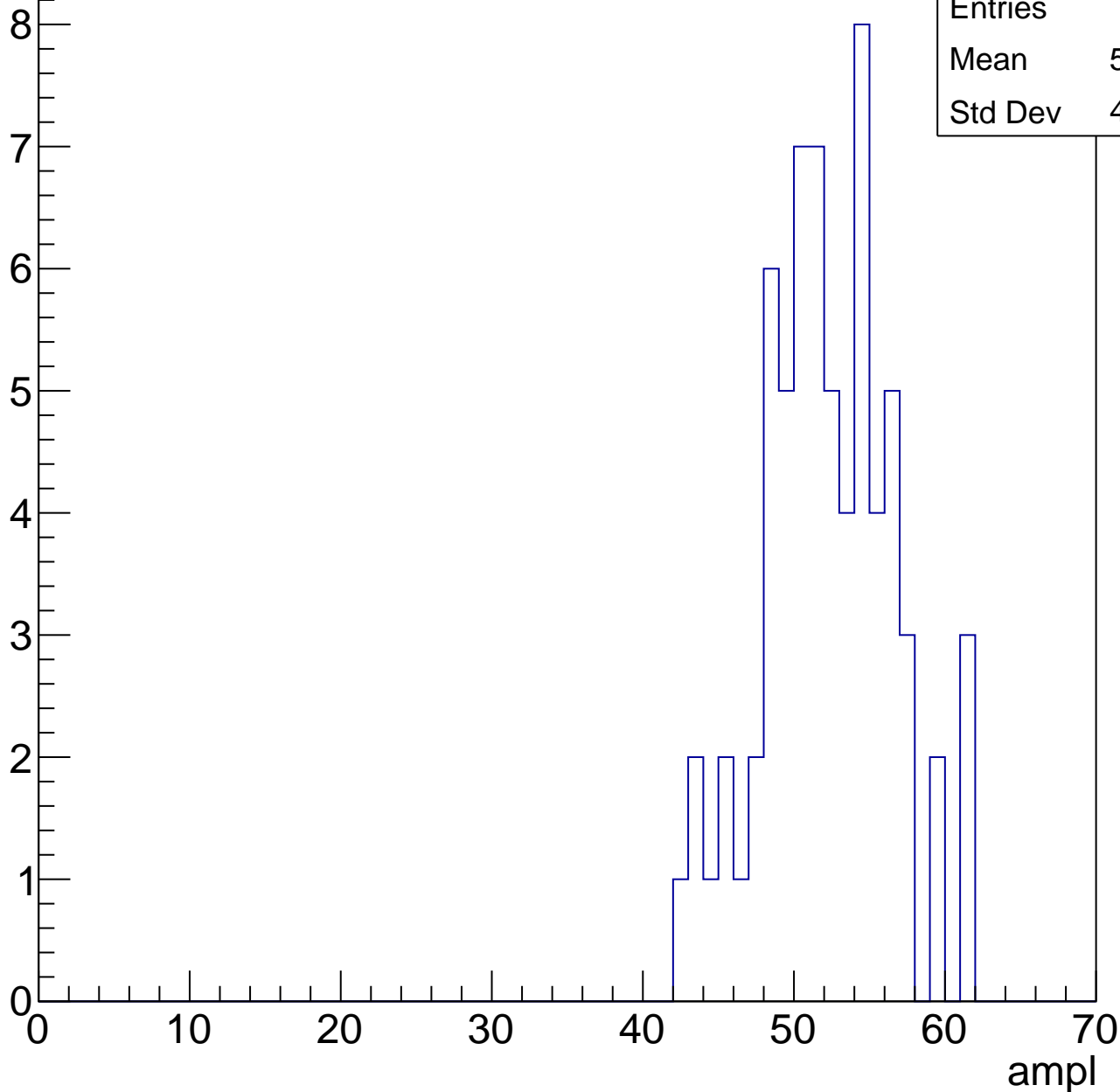


B1L102S, U13-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	51.74
Std Dev	4.286

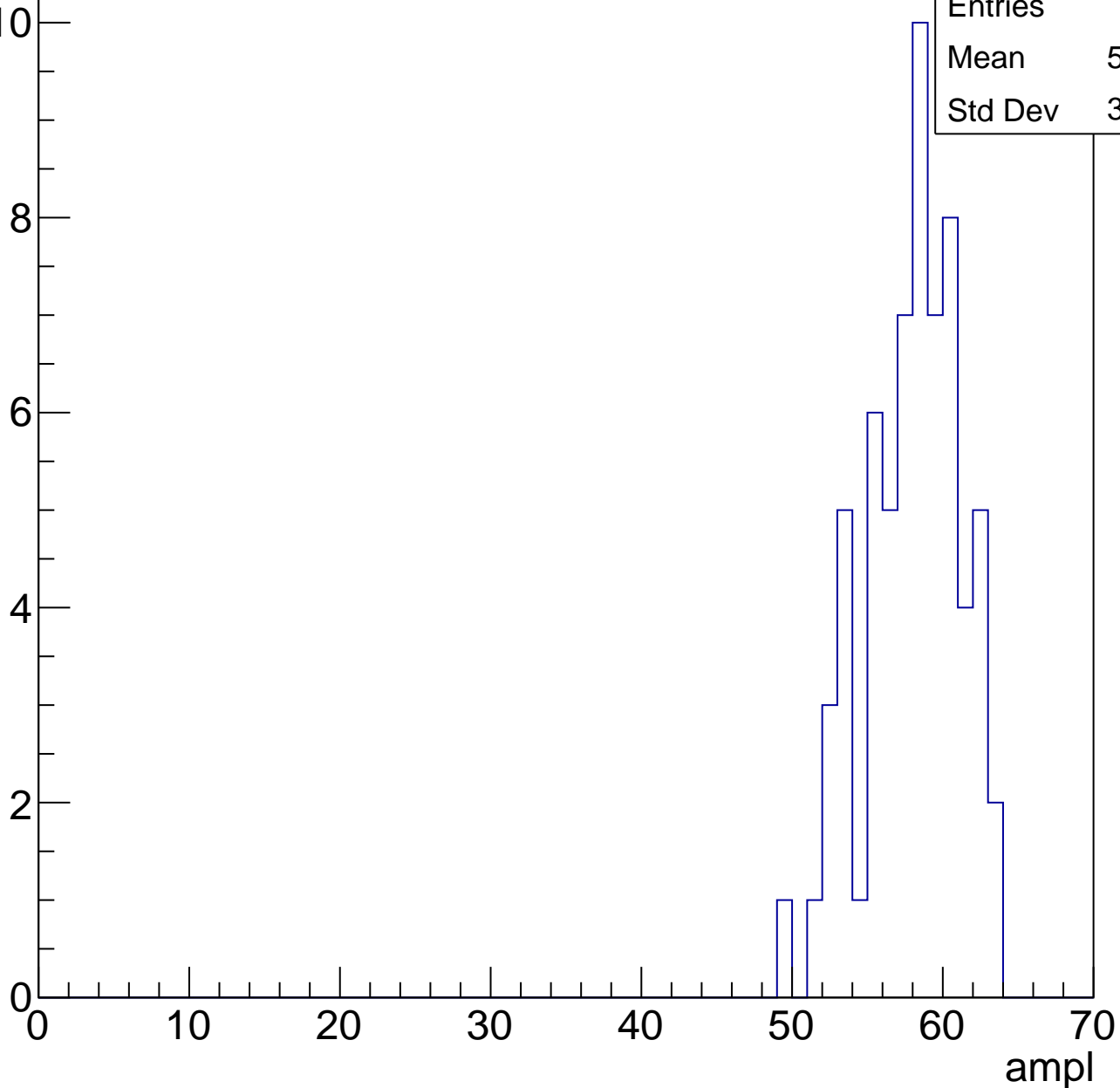


B1L102S, U13-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	57.49
Std Dev	3.158

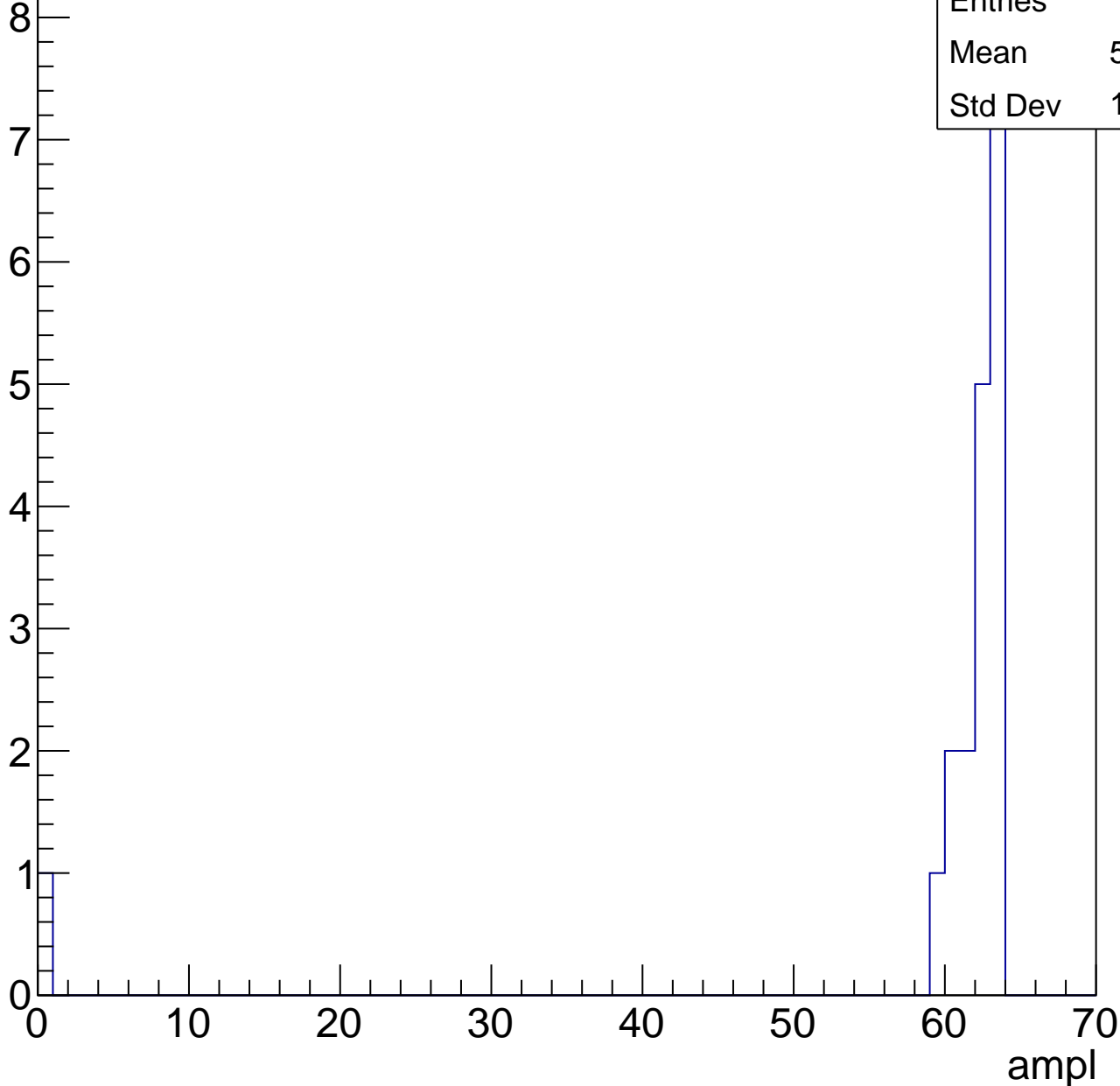


B1L102S, U13-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.68
Std Dev	13.88



B1L102S, U13-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch3, adc0

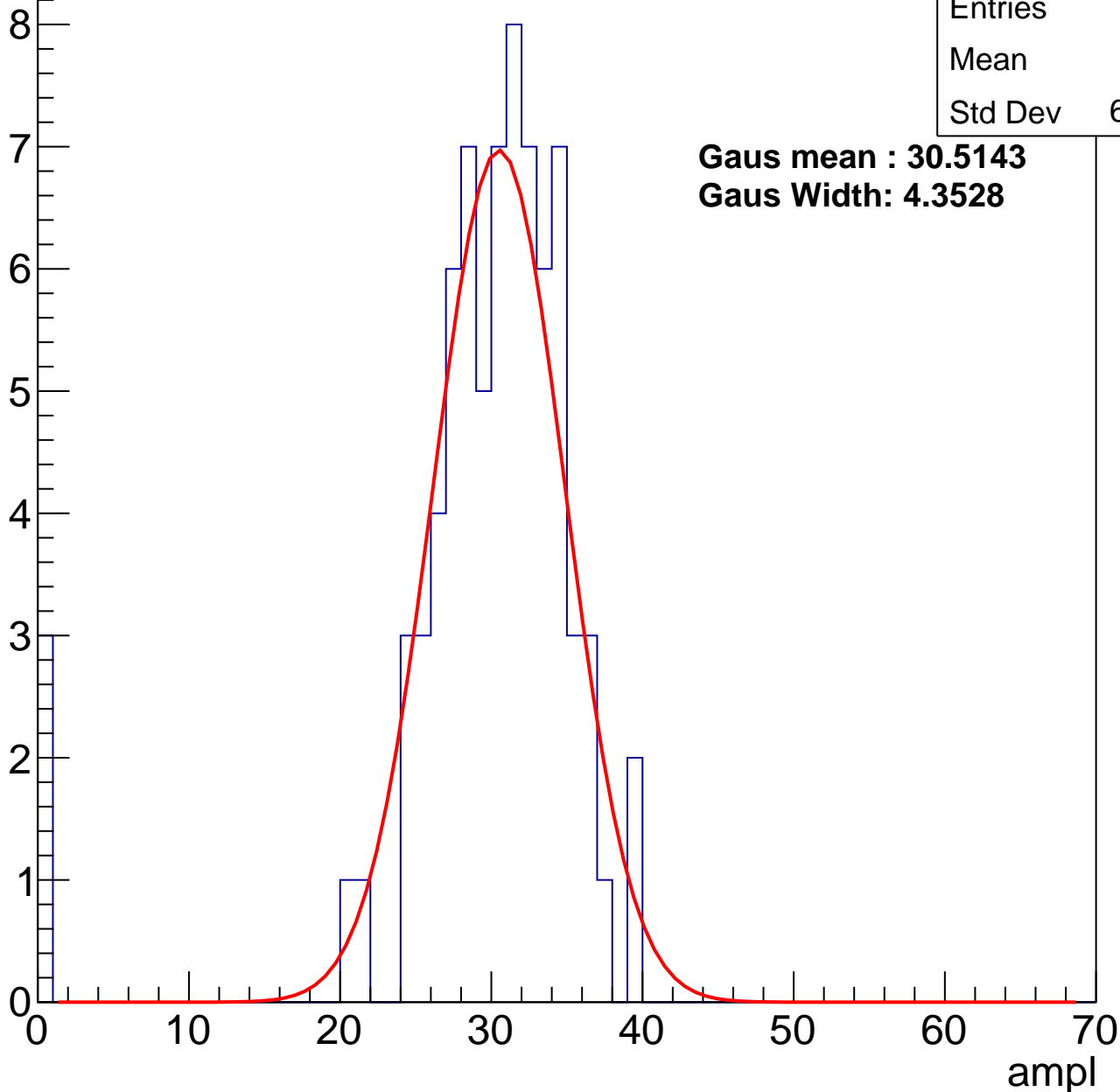
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.1
Std Dev	6.978

Gaus mean : 30.5143

Gaus Width: 4.3528



B1L102S, U13-ch3, adc1

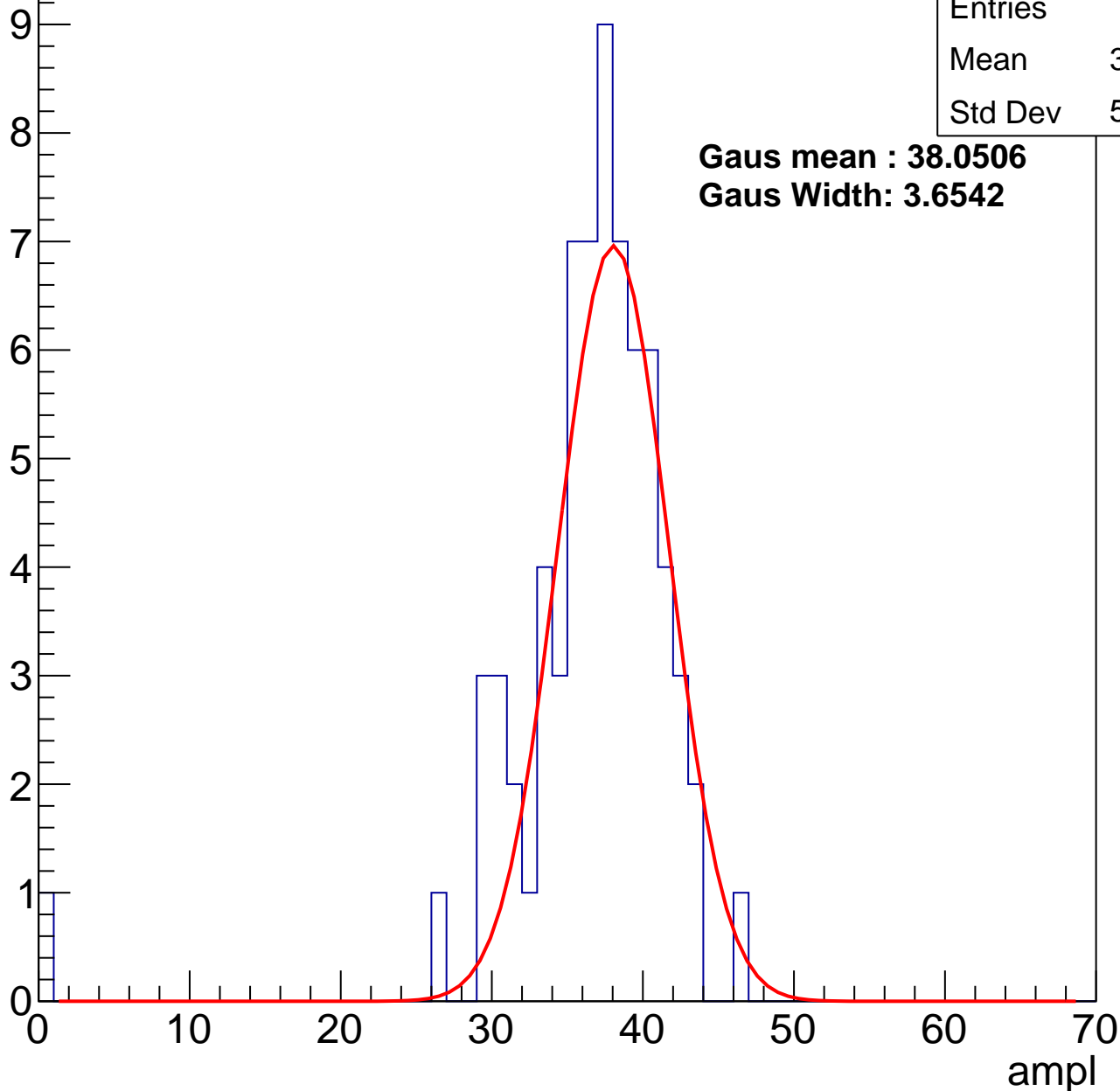
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.04
Std Dev	5.812

Gaus mean : 38.0506

Gaus Width: 3.6542



B1L102S, U13-ch3, adc2

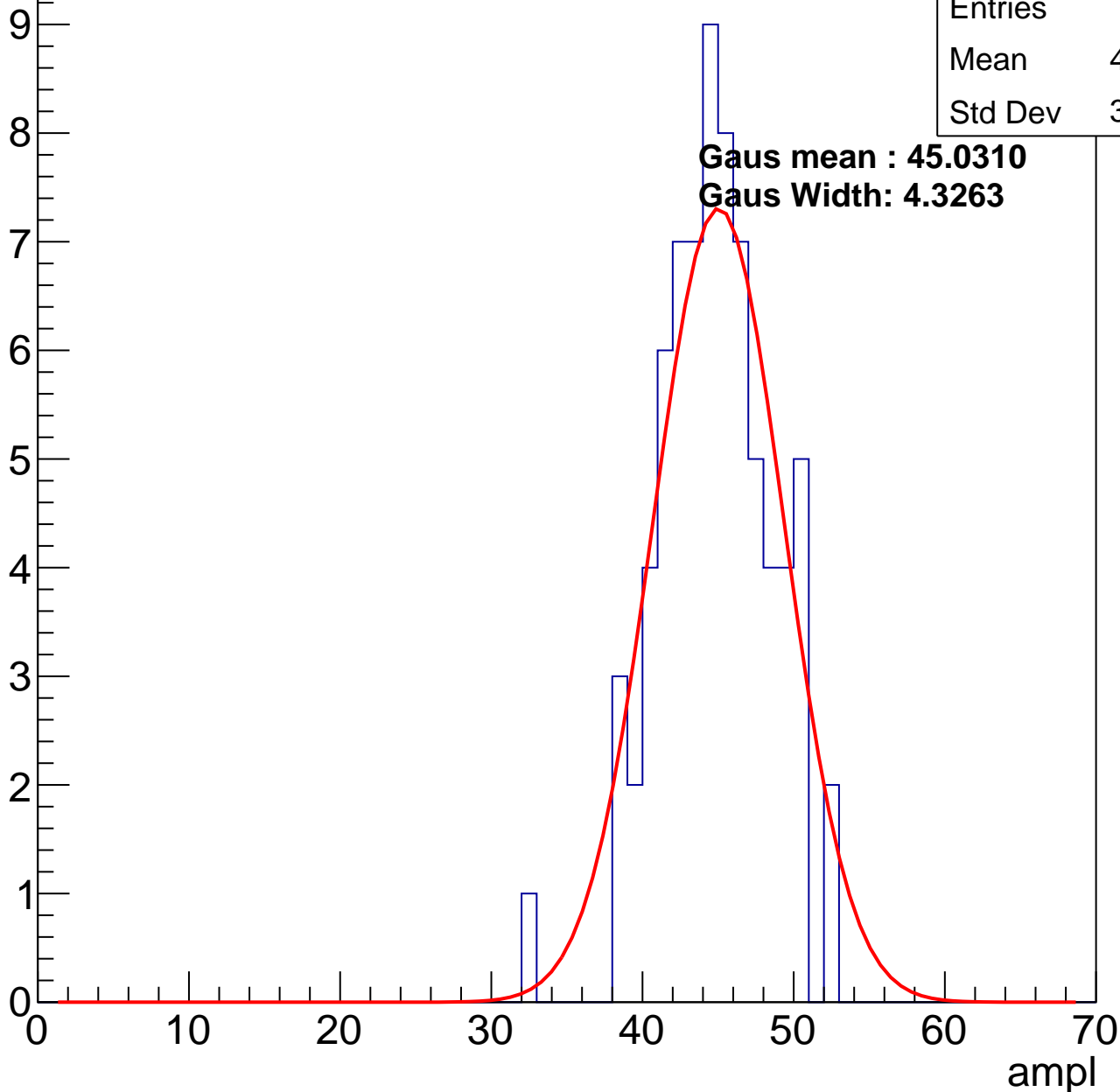
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	44.32
Std Dev	3.676

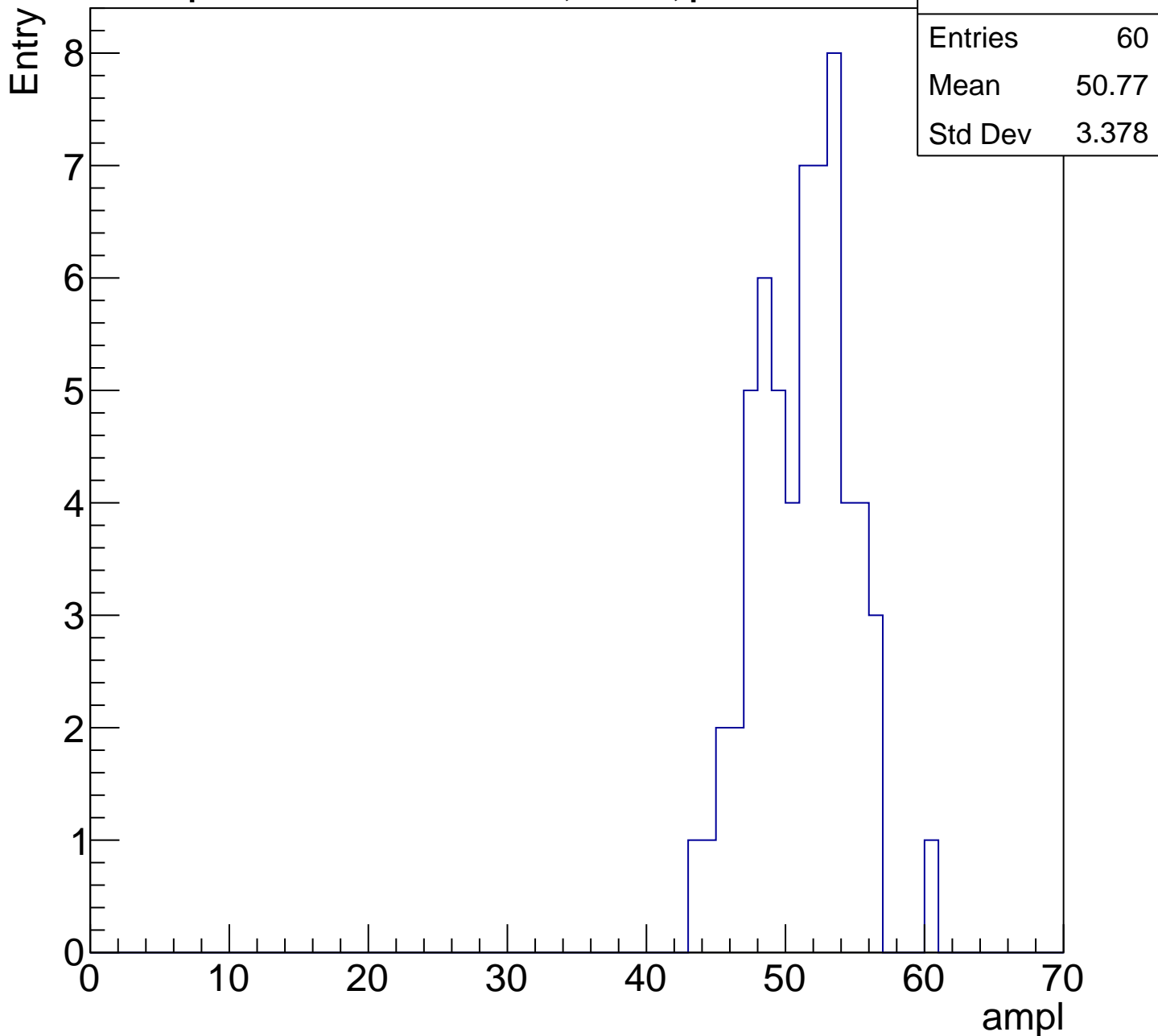
Gaus mean : 45.0310

Gaus Width: 4.3263



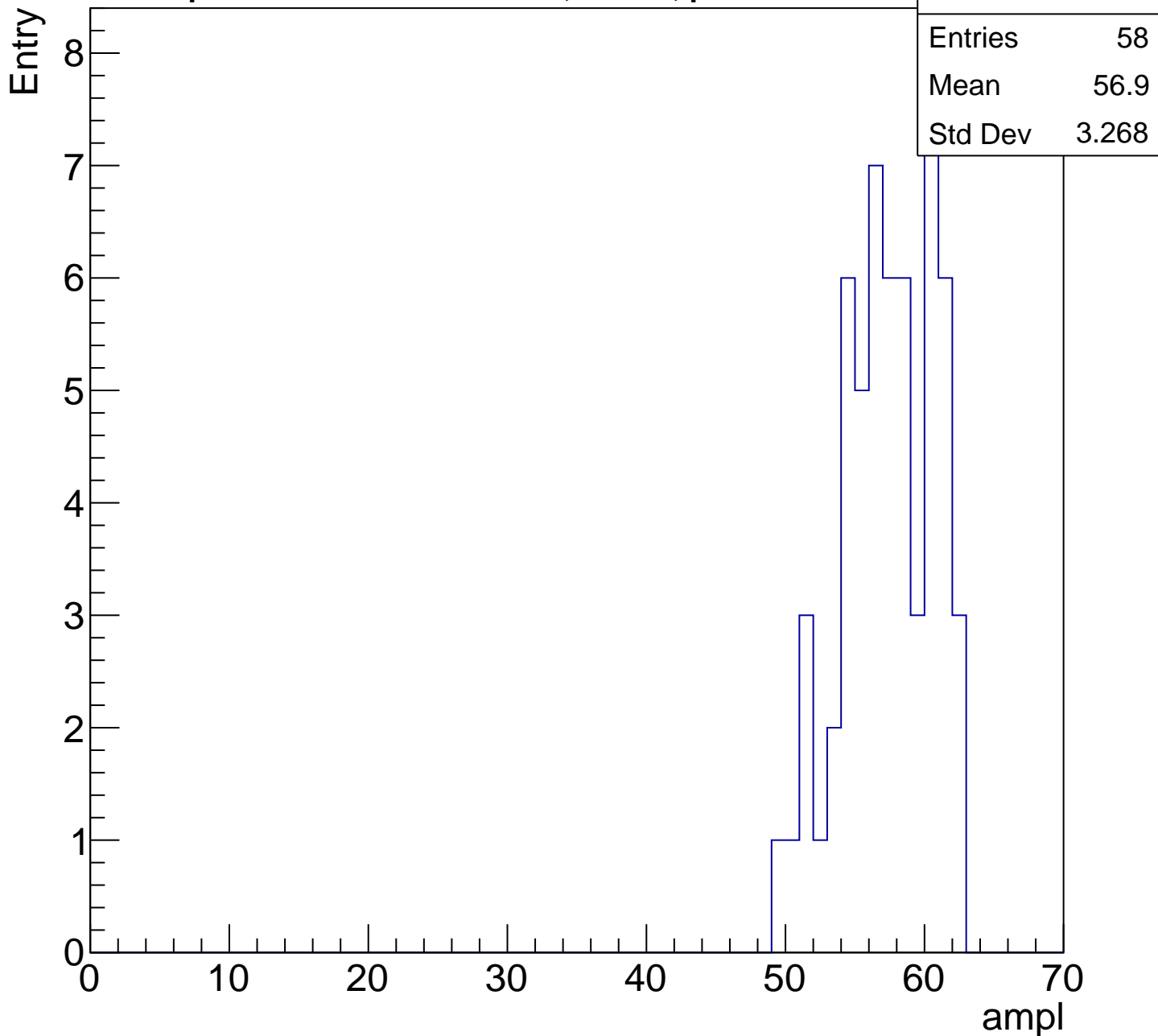
B1L102S, U13-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U13-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

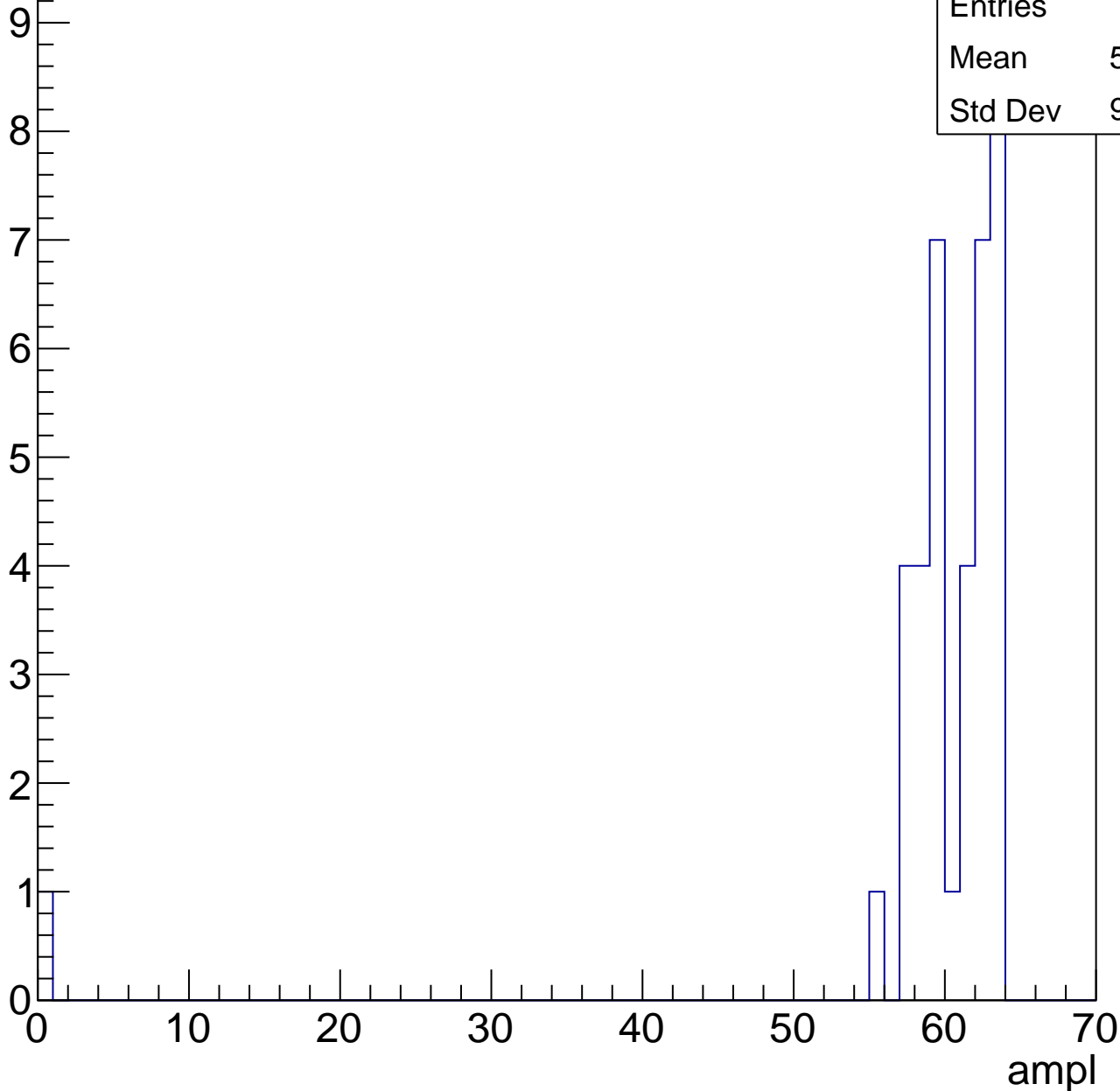


B1L102S, U13-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

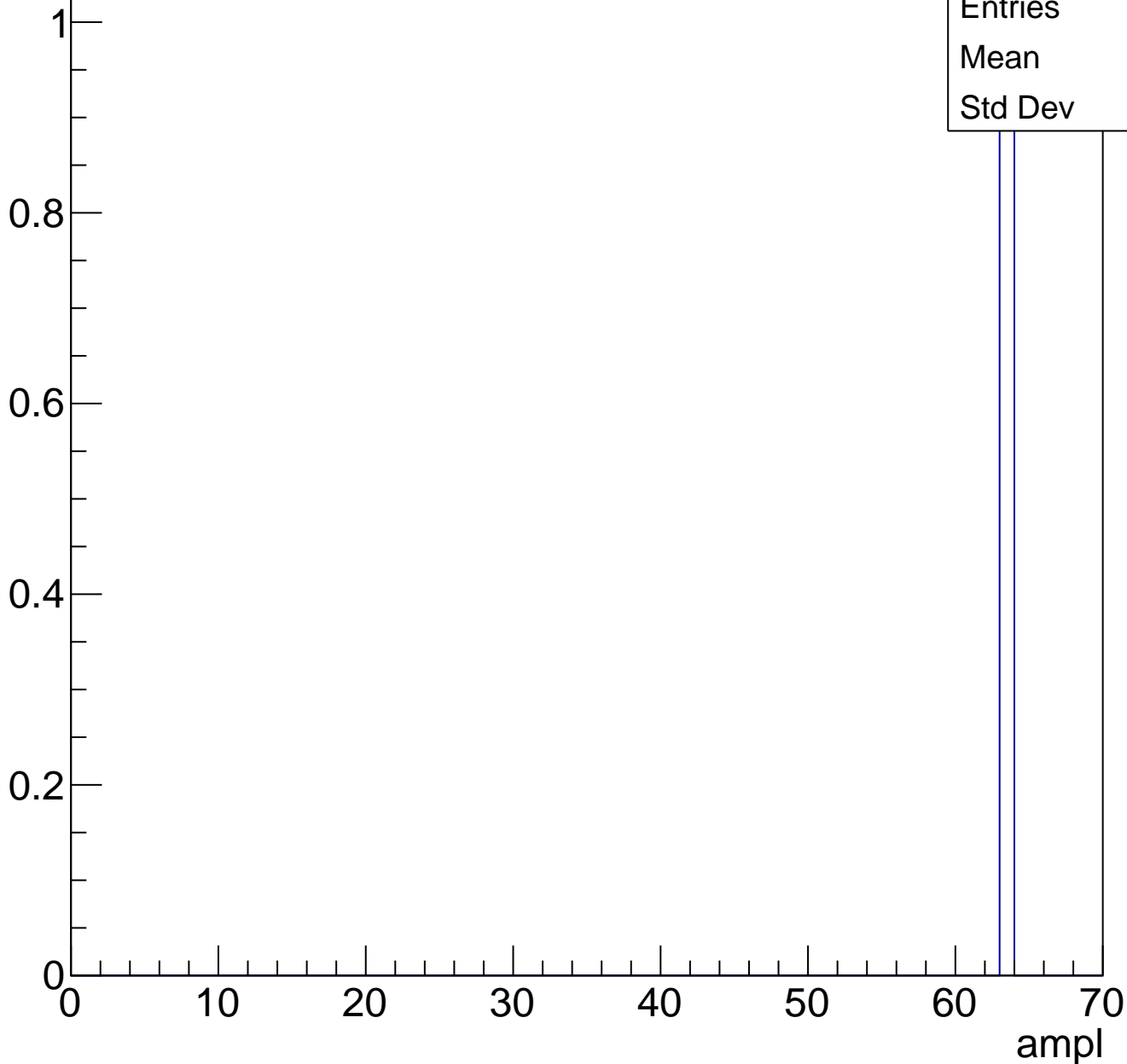
Entries	38
Mean	58.76
Std Dev	9.919



B1L102S, U13-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch4, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	30.86
Std Dev	3.62

Gaus mean : 31.3020

Gaus Width: 3.5469

10

8

6

4

2

0

0

10

20

30

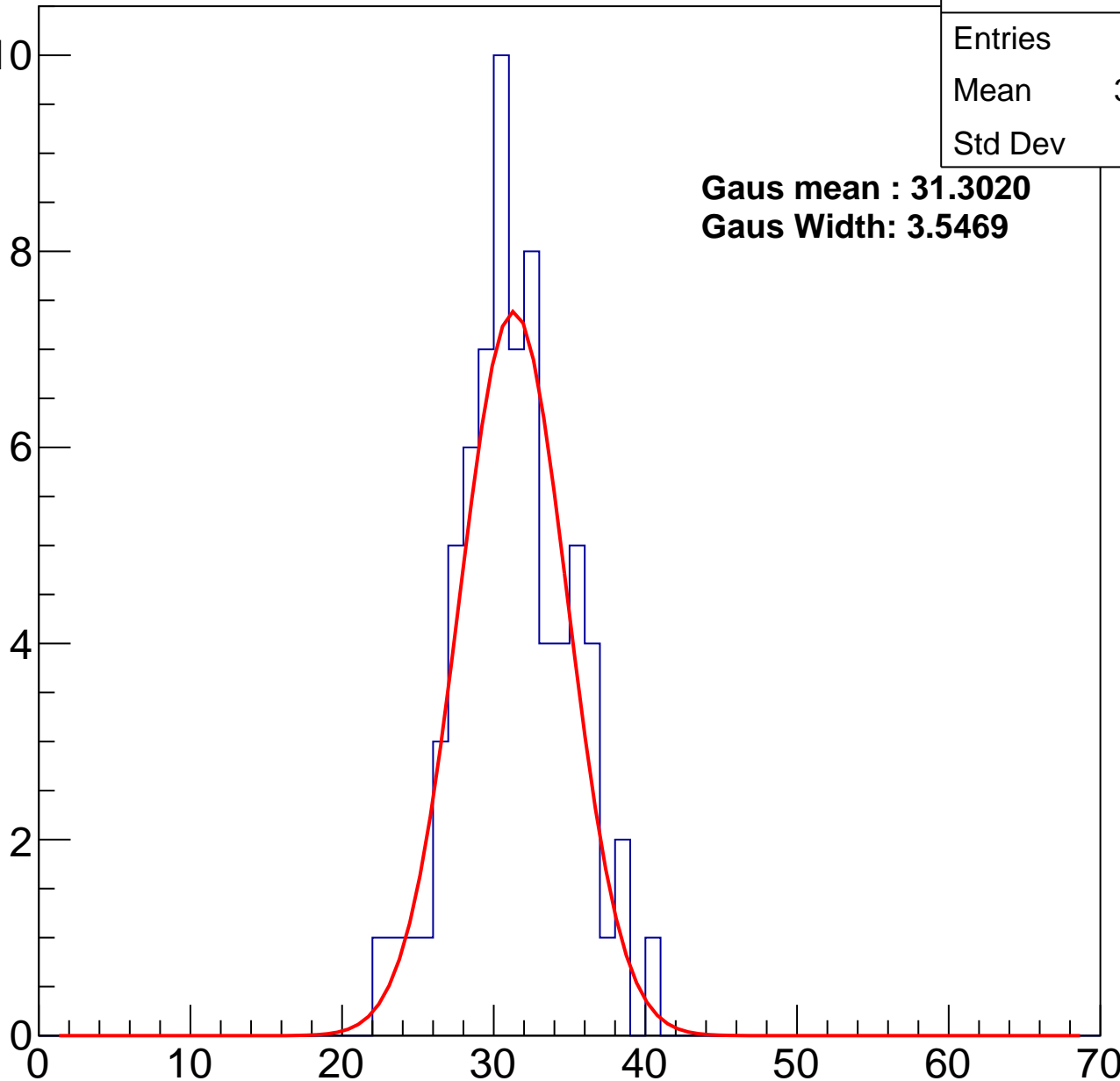
40

50

60

70

ampl



B1L102S, U13-ch4, adc1

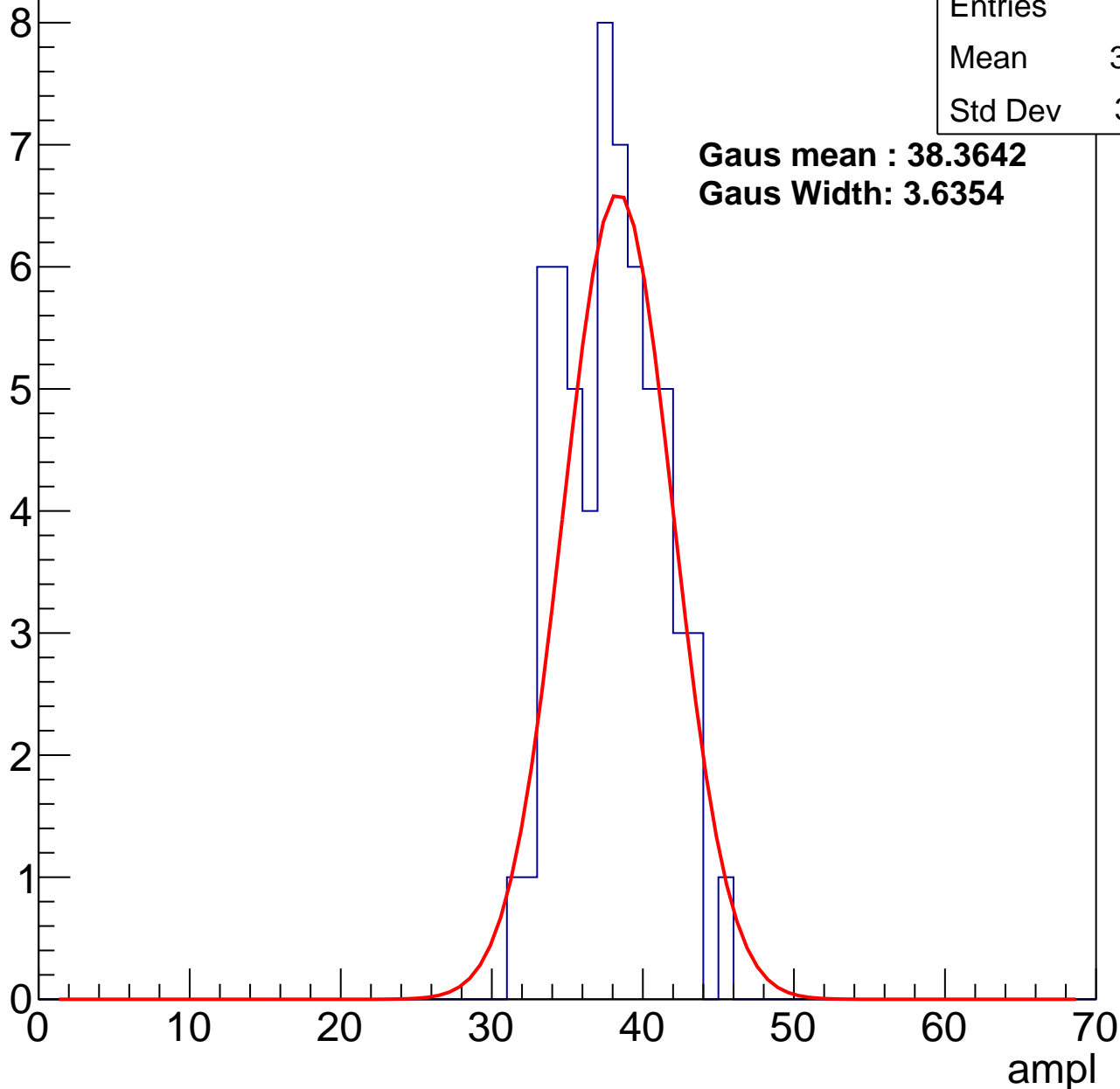
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	37.46
Std Dev	3.201

Gaus mean : 38.3642

Gaus Width: 3.6354

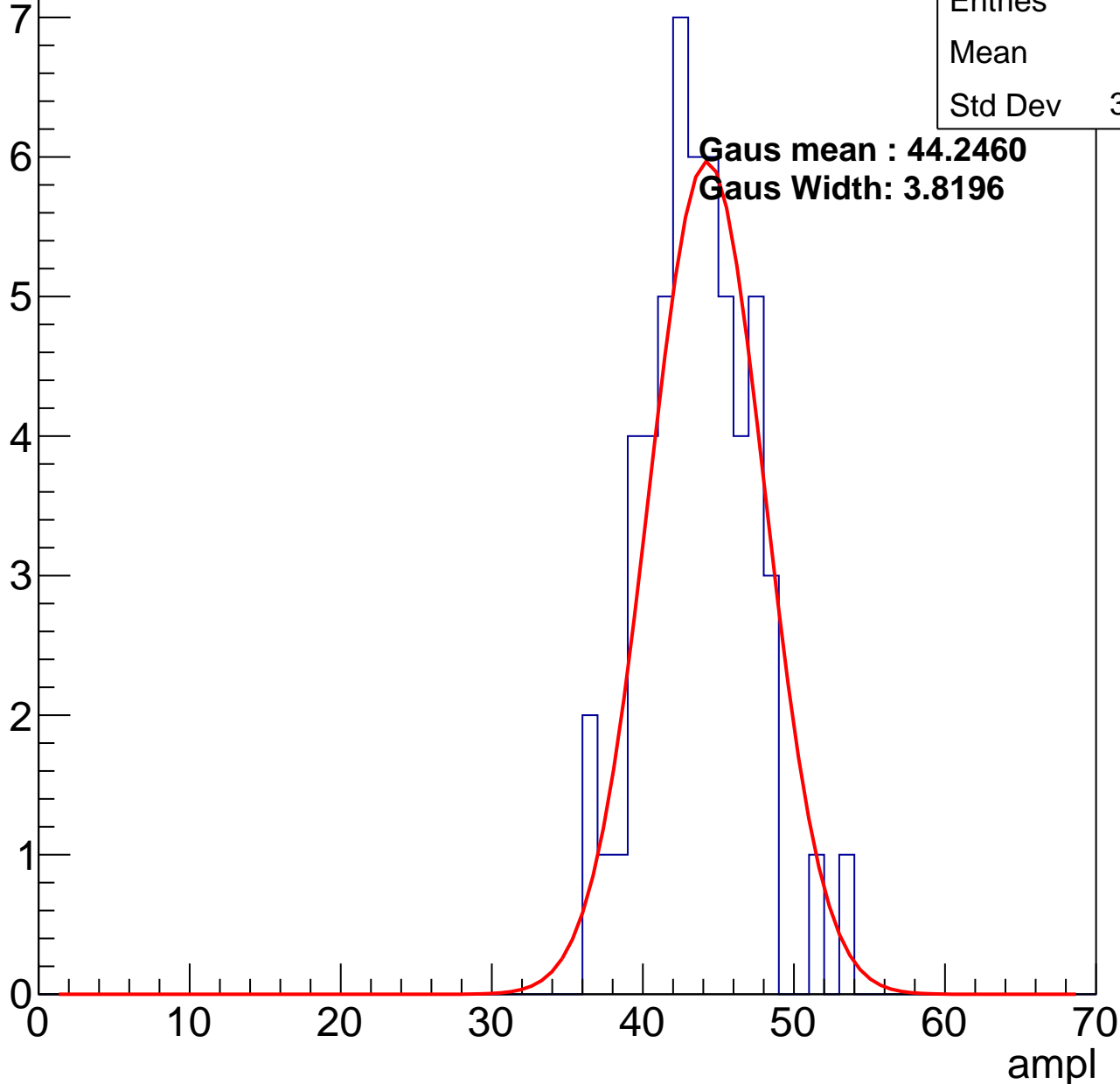


B1L102S, U13-ch4, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.2
Std Dev	3.477

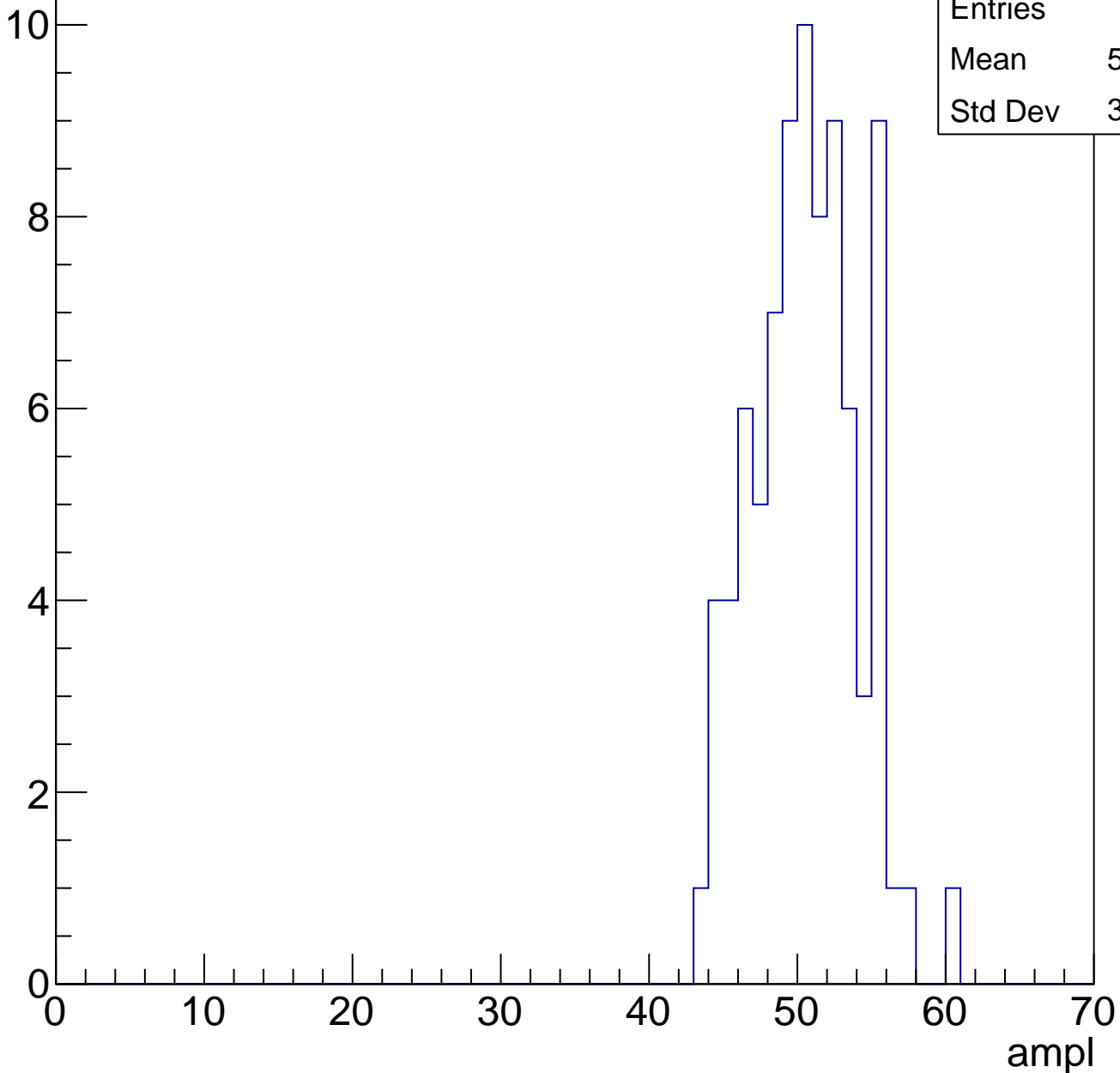


B1L102S, U13-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	50.13
Std Dev	3.494

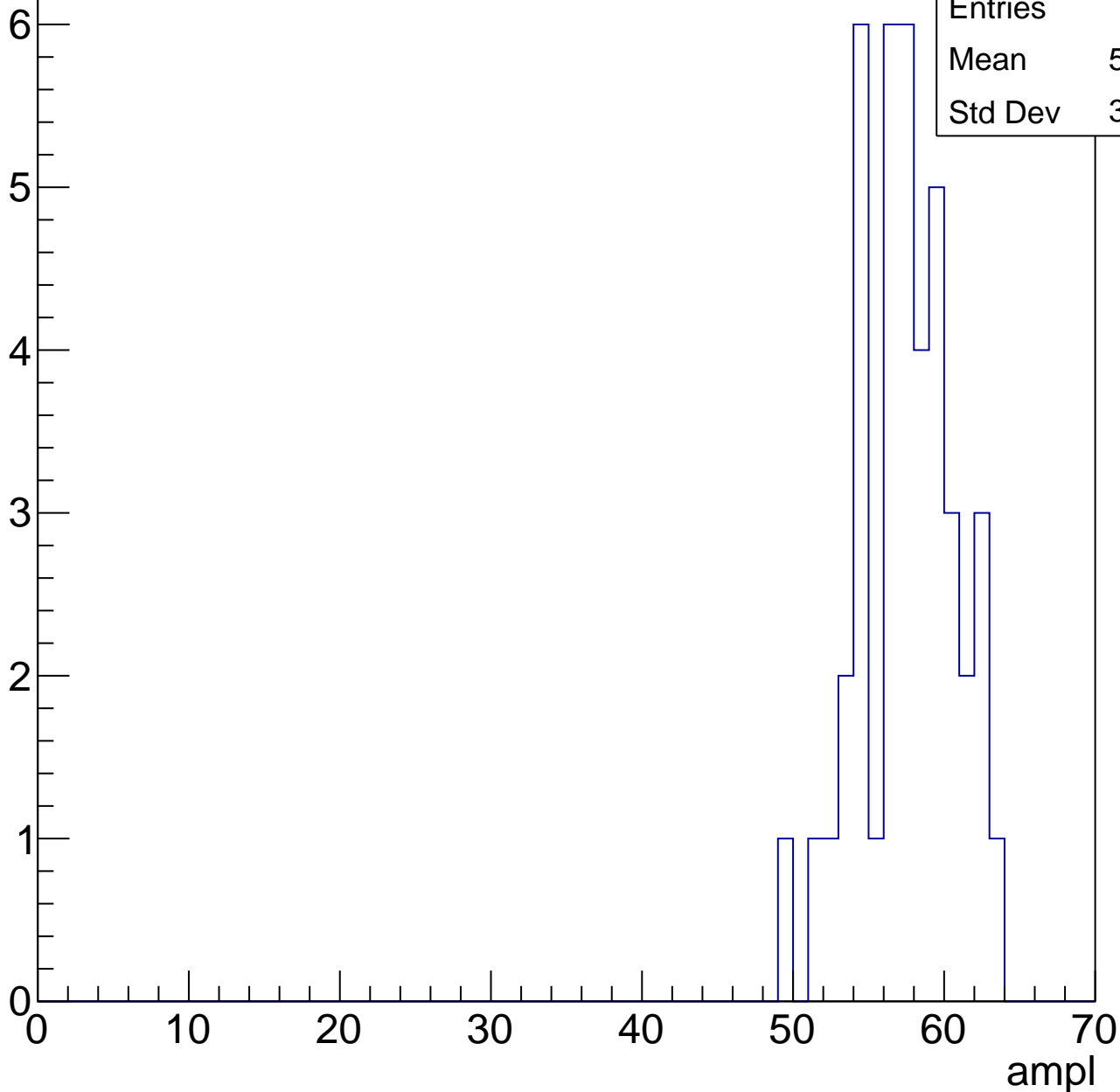


B1L102S, U13-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	56.98
Std Dev	3.143

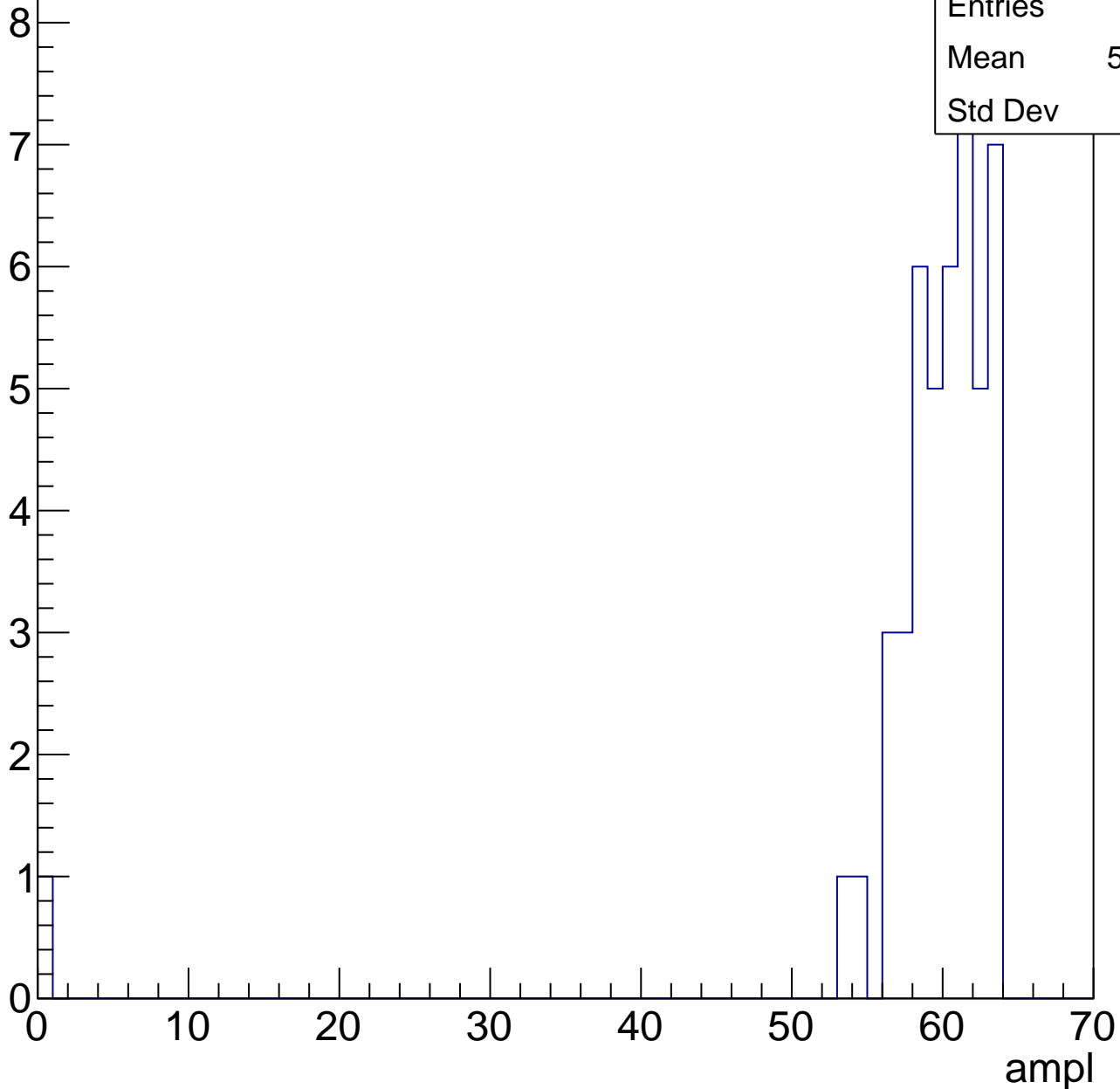


B1L102S, U13-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.43
Std Dev	9.05



B1L102S, U13-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

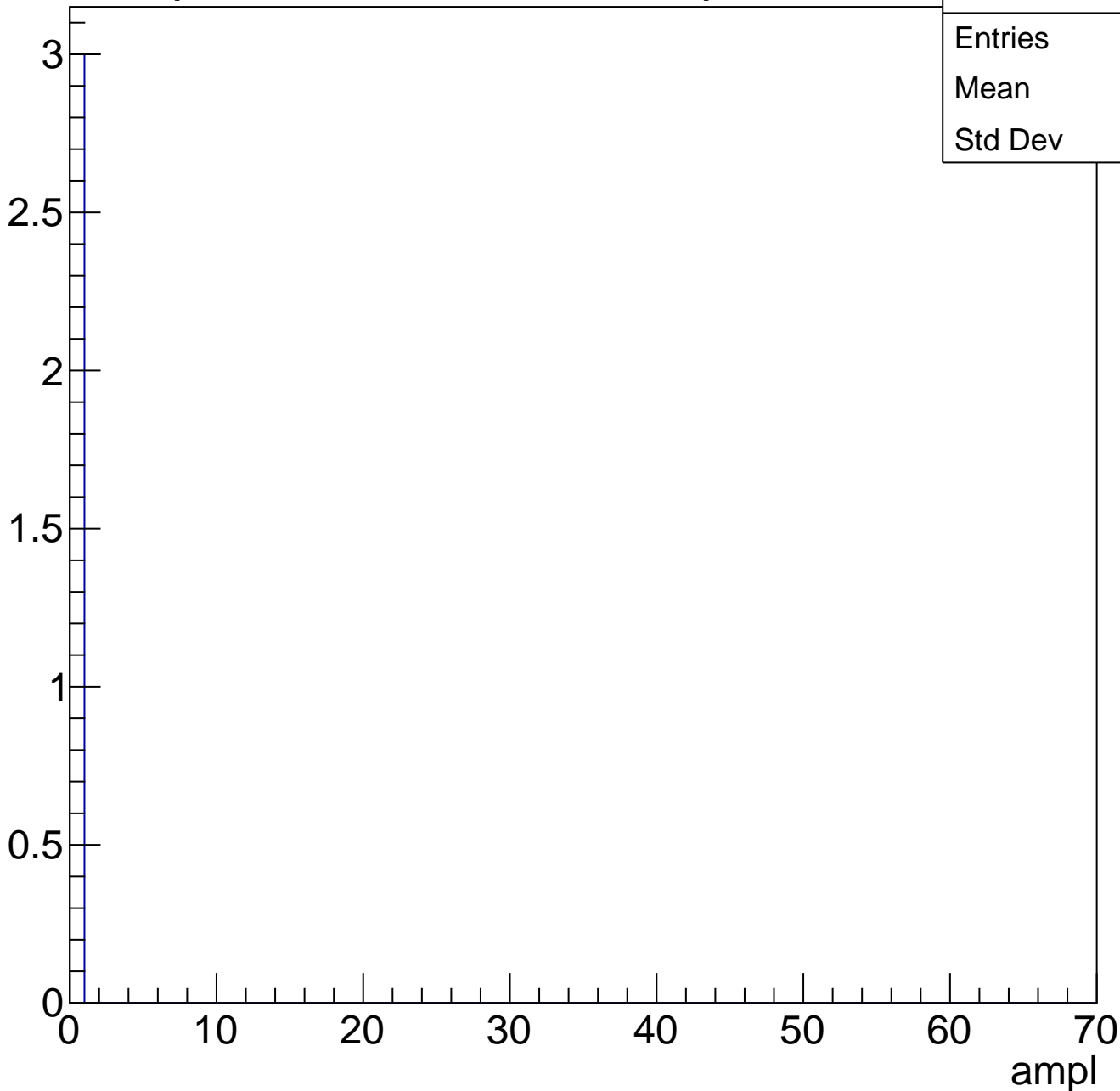
Entry



B1L102S, U13-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch5, adc0

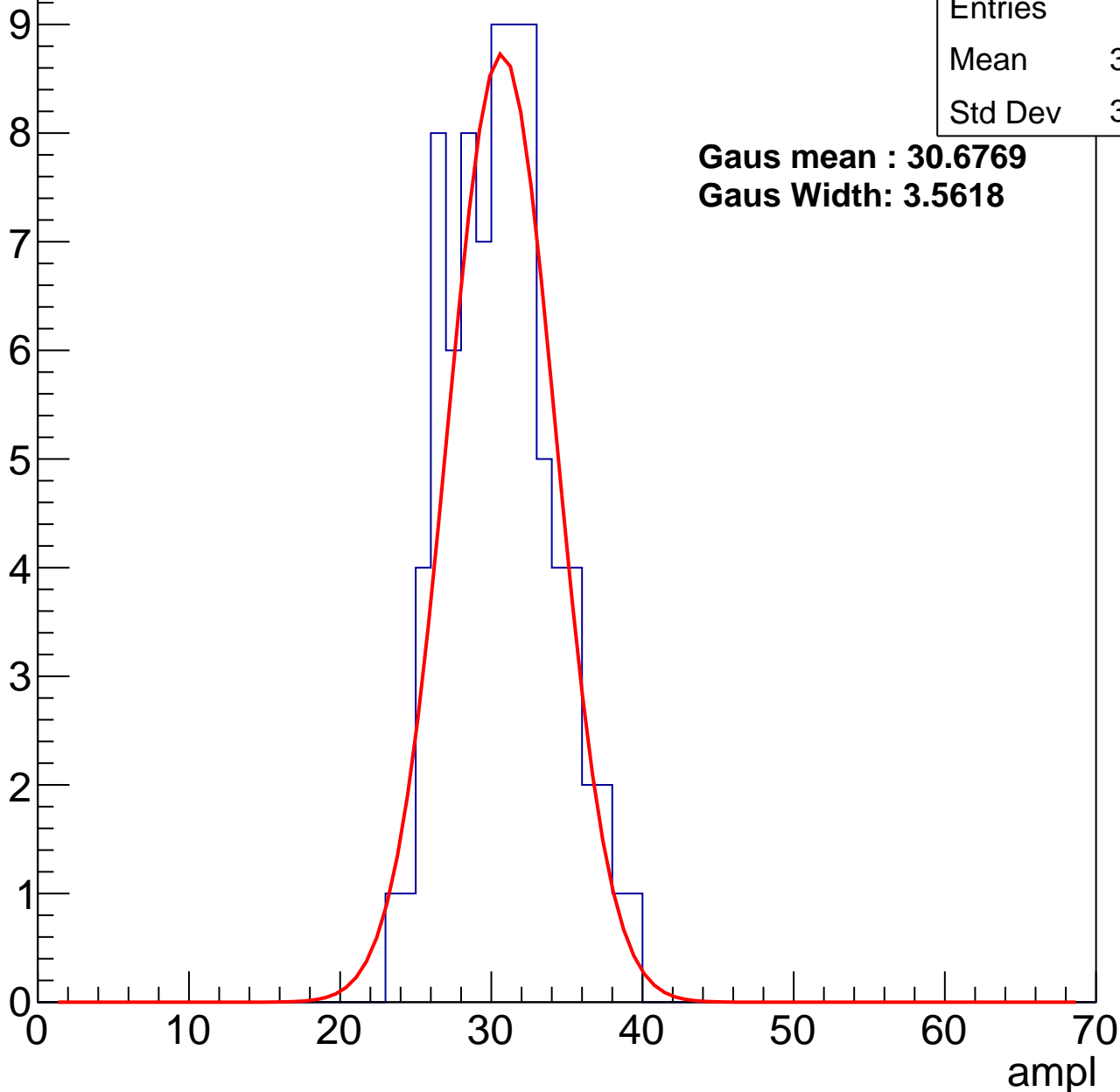
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	30.19
Std Dev	3.464

Gaus mean : 30.6769

Gaus Width: 3.5618



B1L102S, U13-ch5, adc1

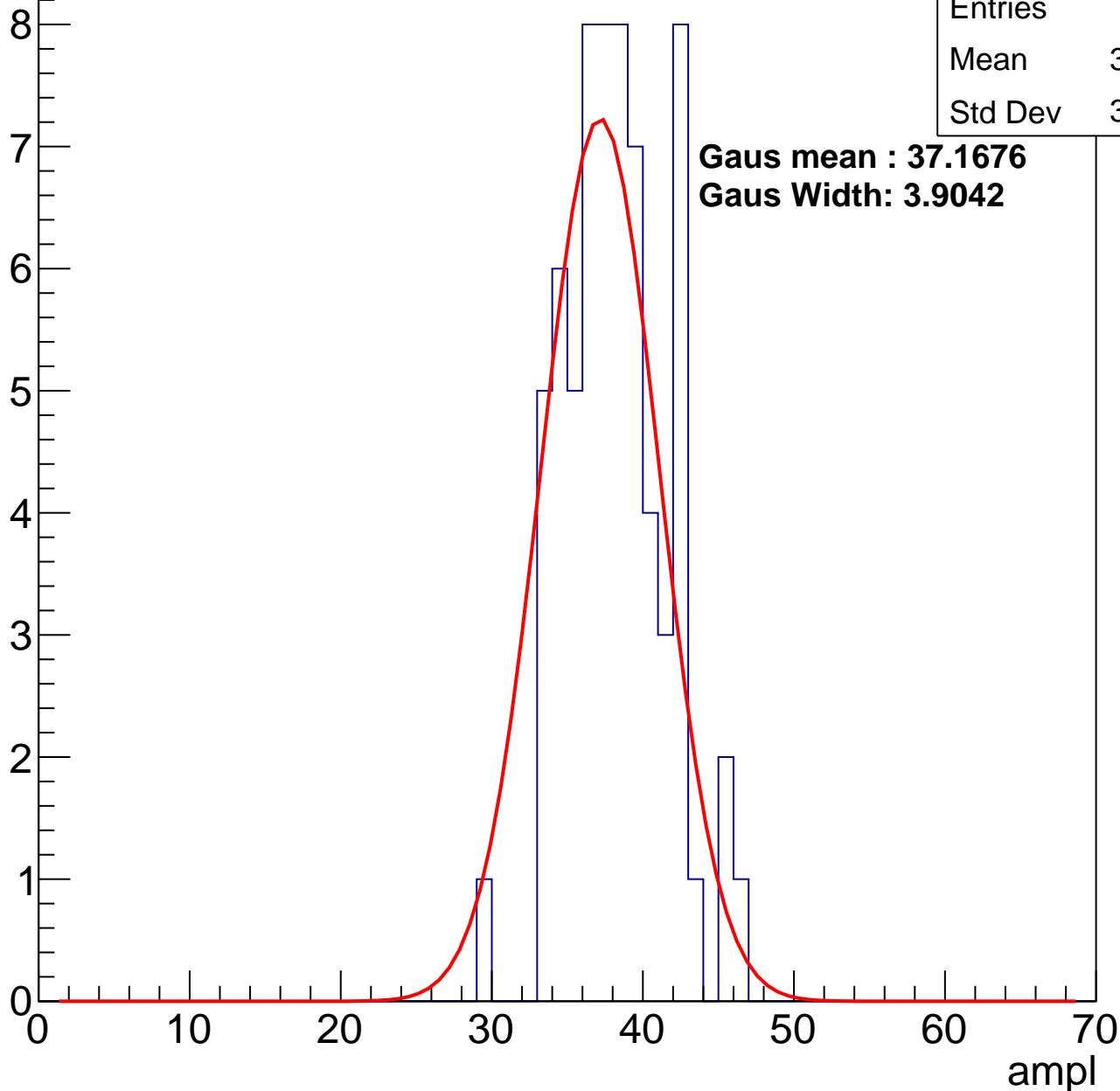
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	37.79
Std Dev	3.344

Gaus mean : 37.1676

Gaus Width: 3.9042



B1L102S, U13-ch5, adc2

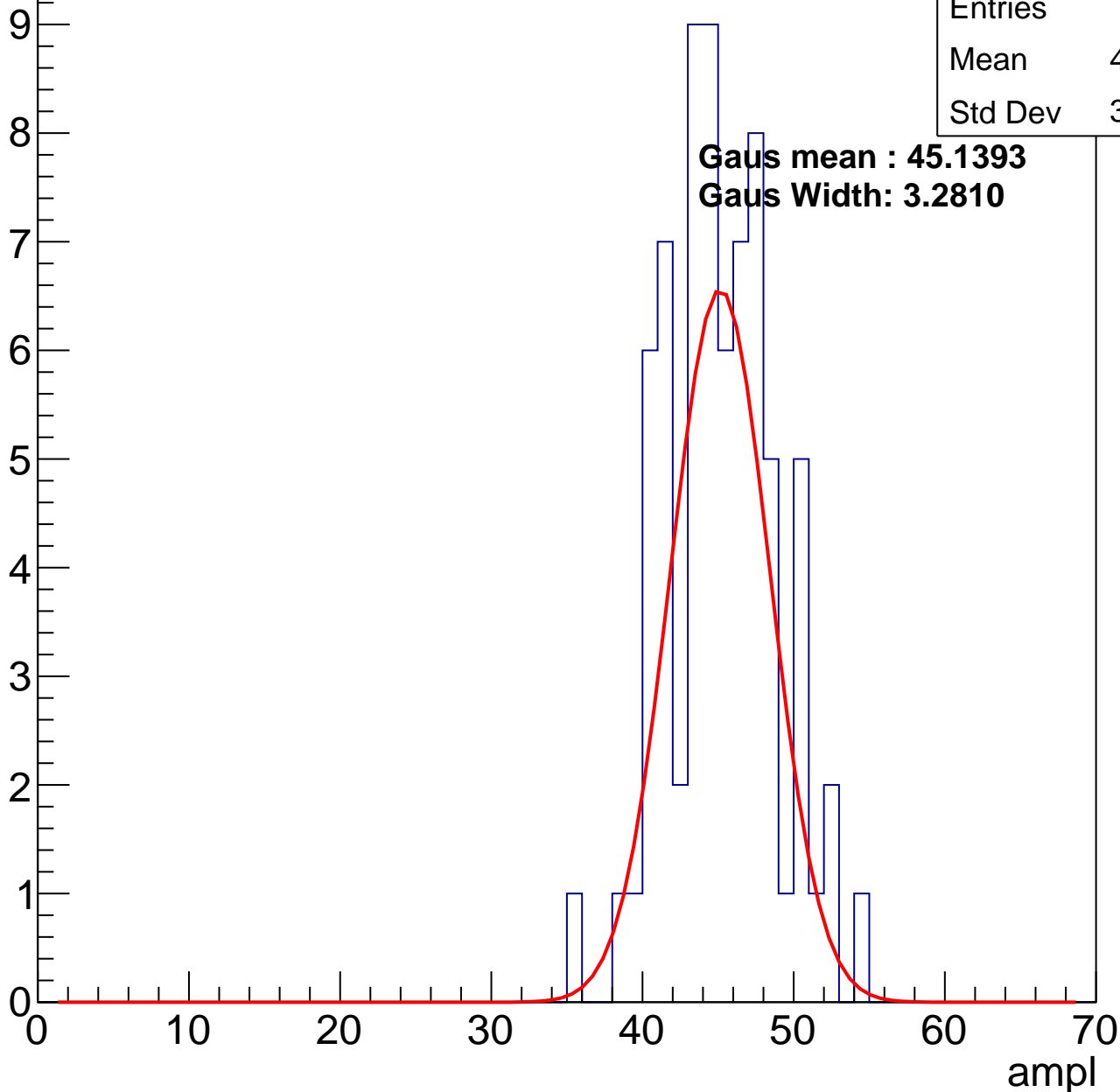
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	44.75
Std Dev	3.624

Gaus mean : 45.1393

Gaus Width: 3.2810

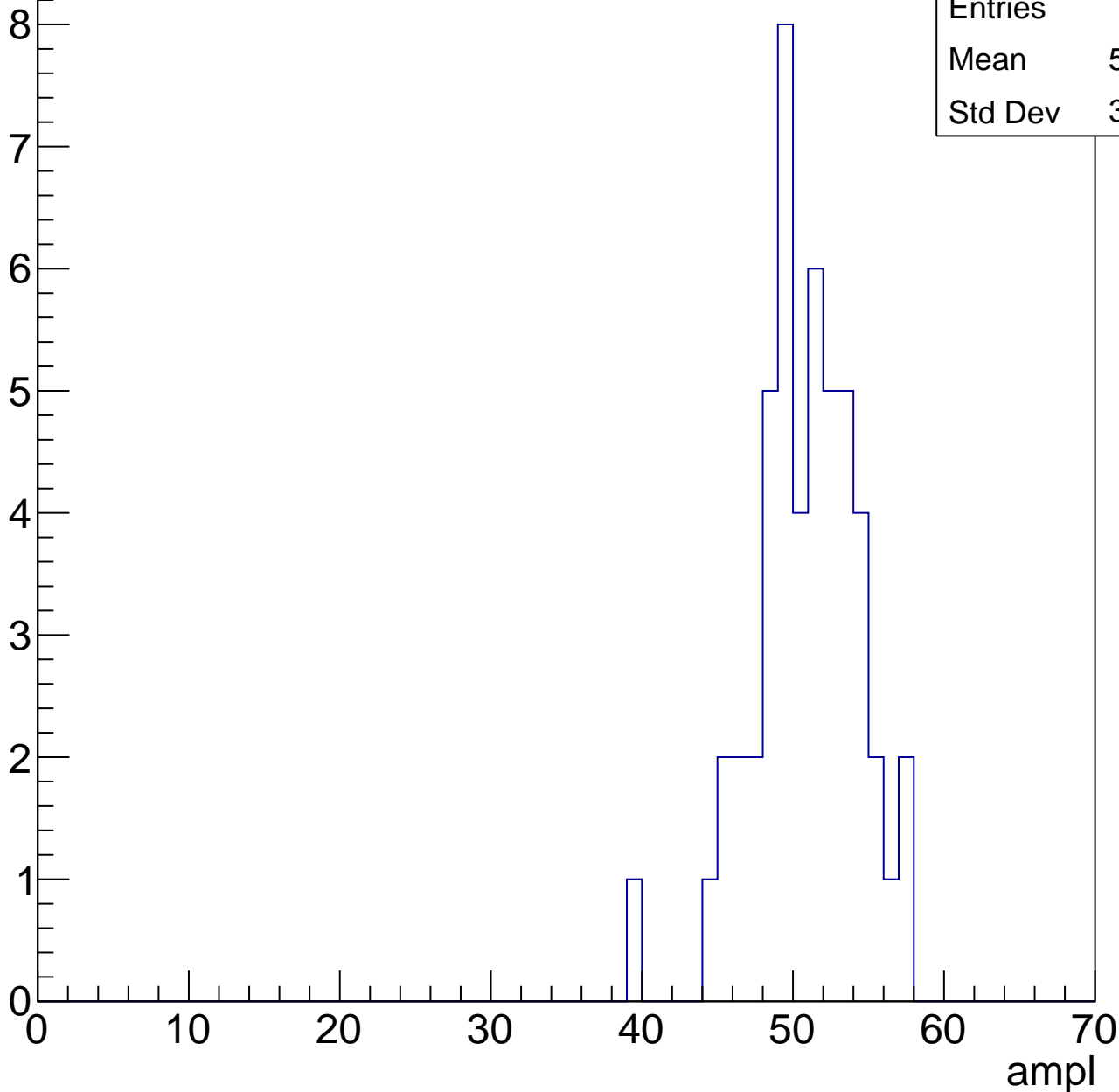


B1L102S, U13-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	50.36
Std Dev	3.457



B1L102S, U13-ch5, adc4

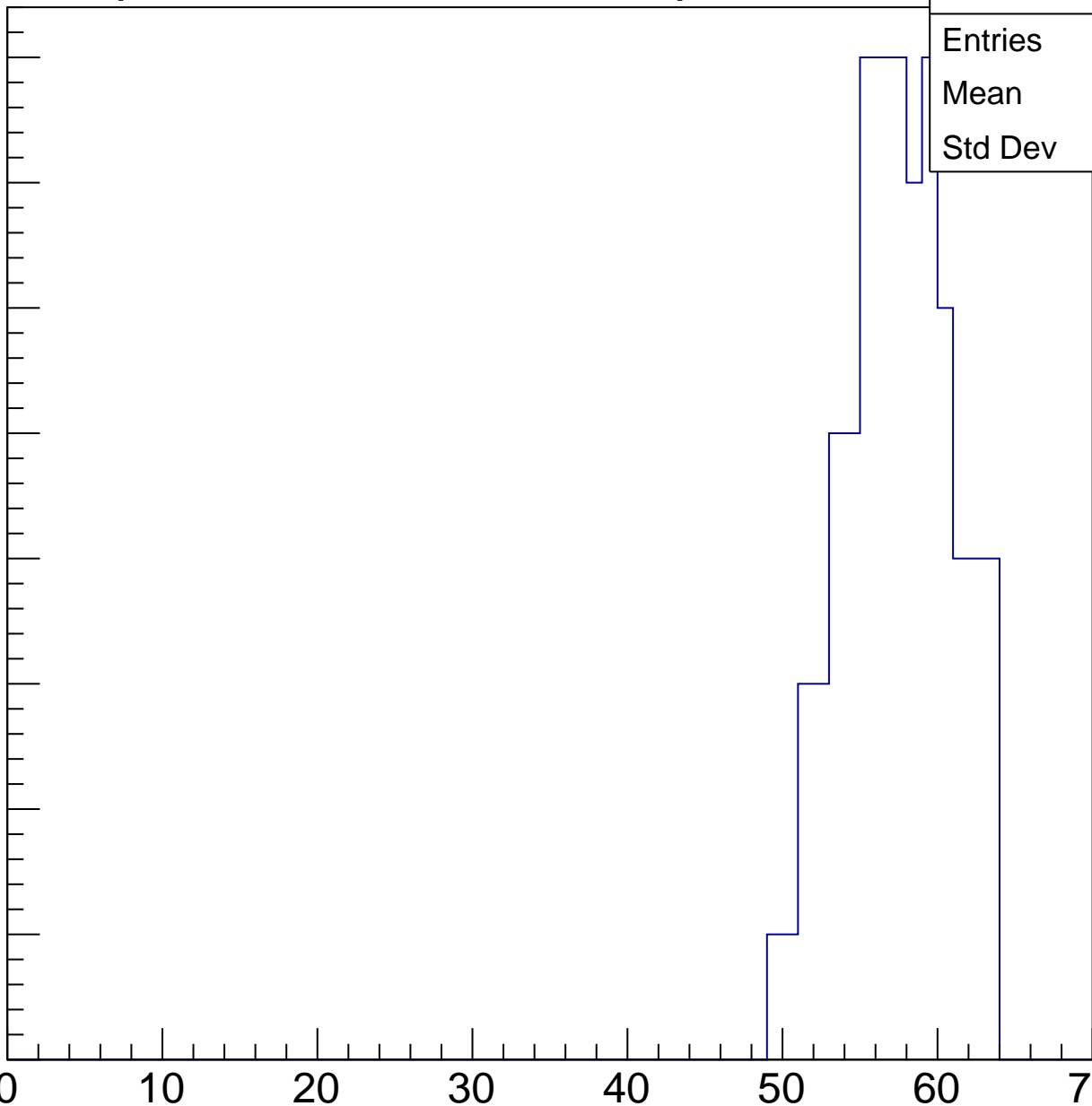
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	75
Mean	56.92
Std Dev	3.389

ampl

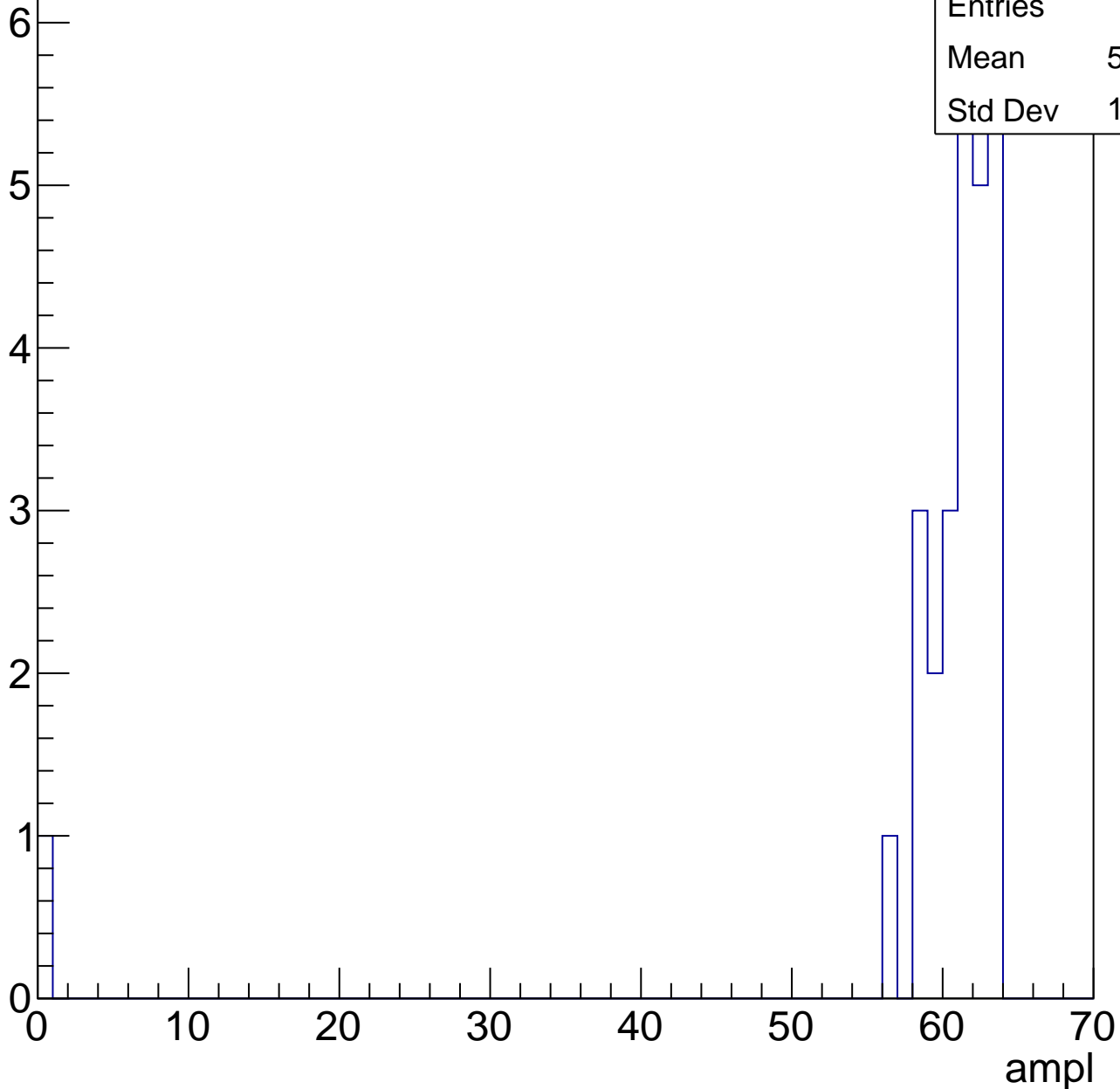


B1L102S, U13-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	27
Mean	58.59
Std Dev	11.64



B1L102S, U13-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch6, adc0

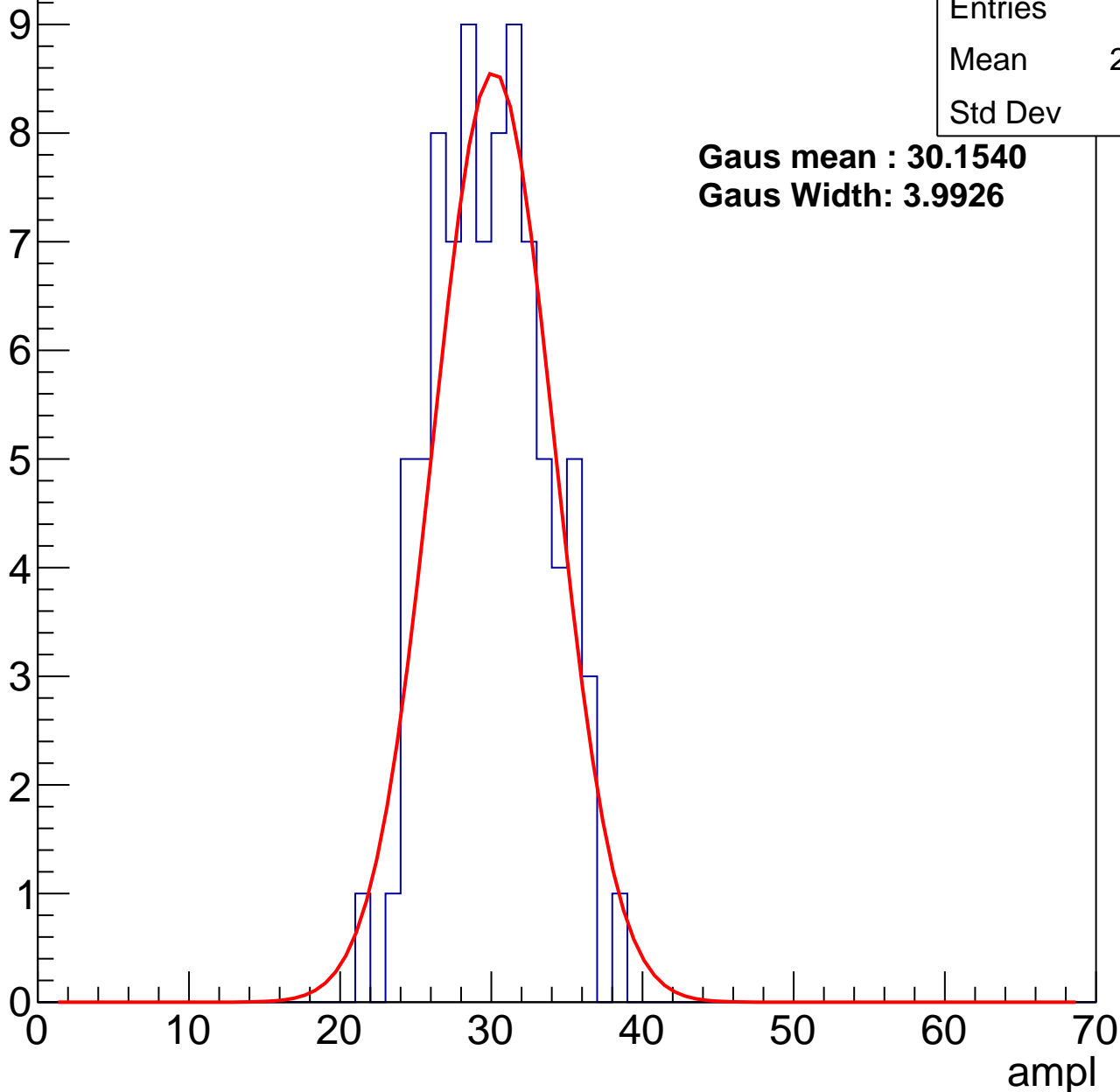
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	29.48
Std Dev	3.58

Gaus mean : 30.1540

Gaus Width: 3.9926



B1L102S, U13-ch6, adc1

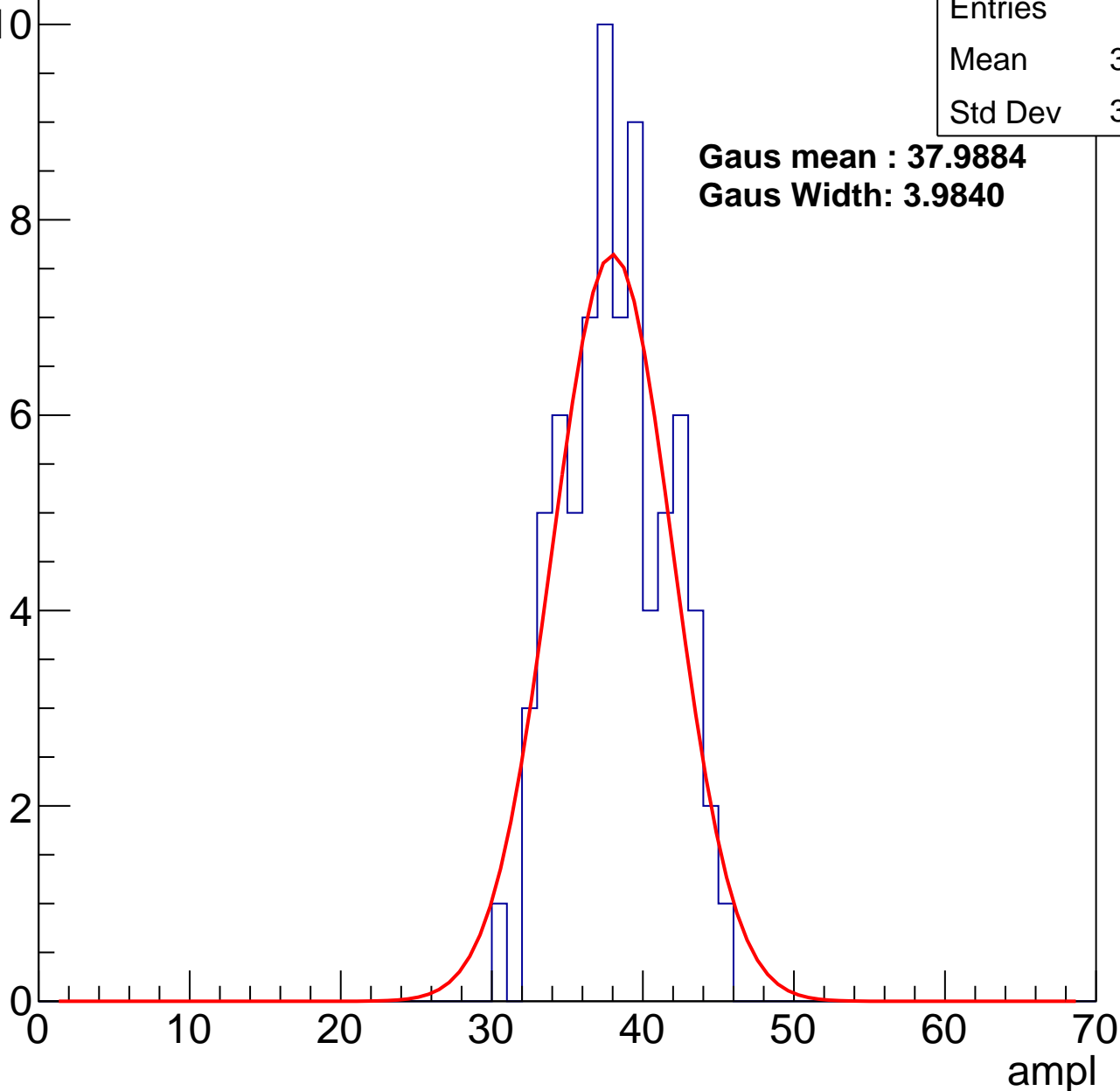
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	37.75
Std Dev	3.383

Gaus mean : 37.9884

Gaus Width: 3.9840



B1L102S, U13-ch6, adc2

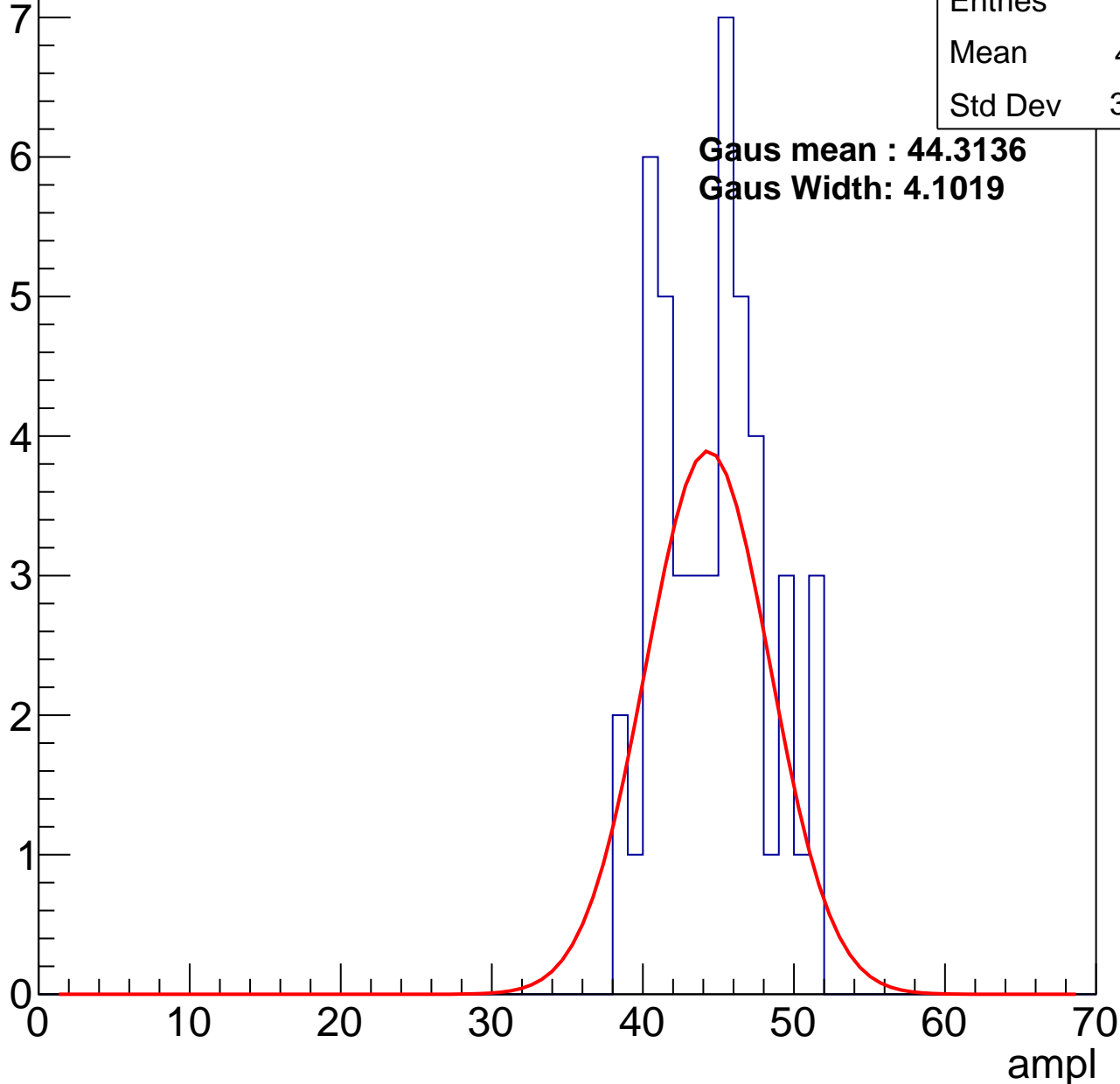
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	44.21
Std Dev	3.537

Gaus mean : 44.3136

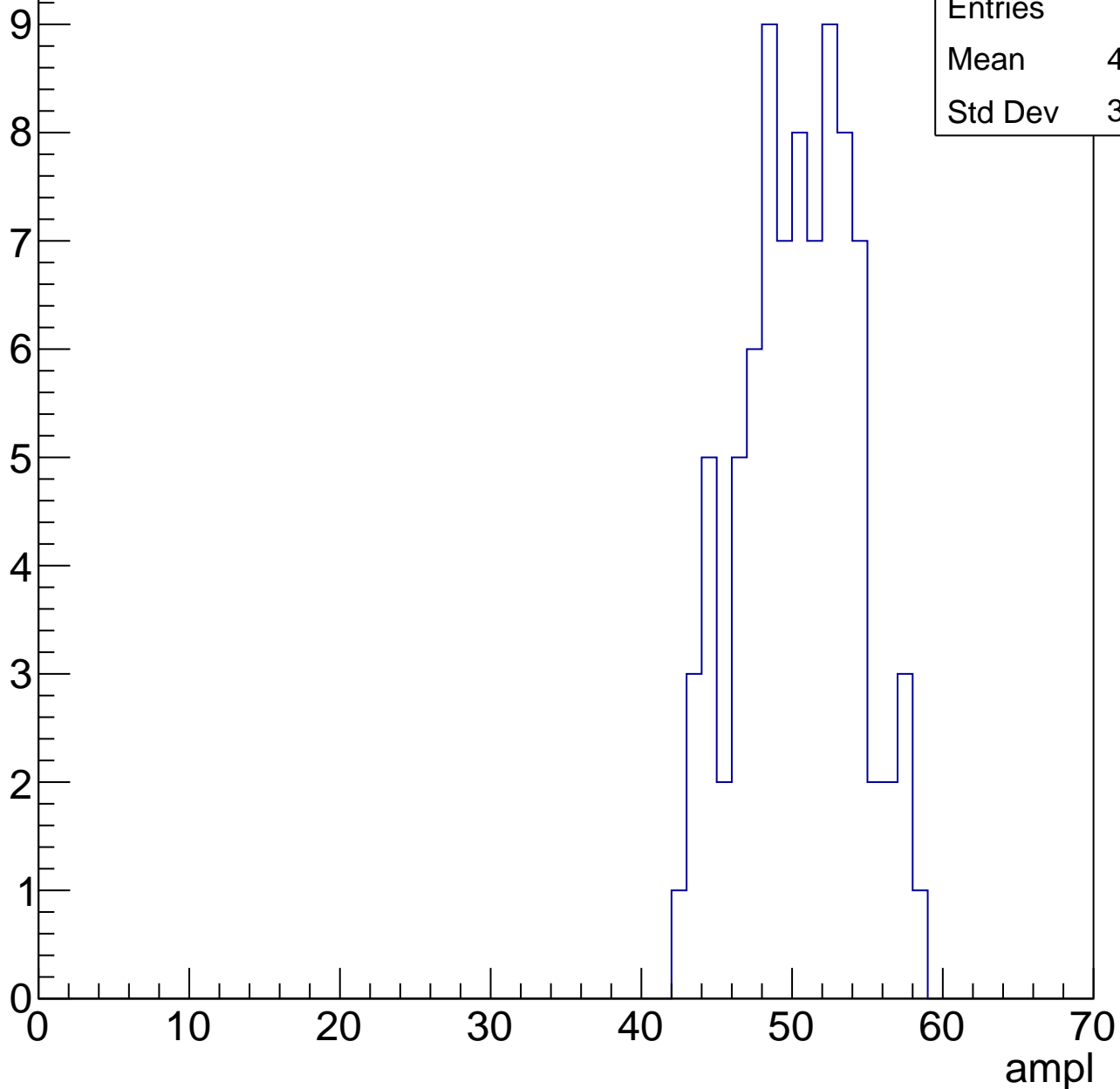
Gaus Width: 4.1019



B1L102S, U13-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

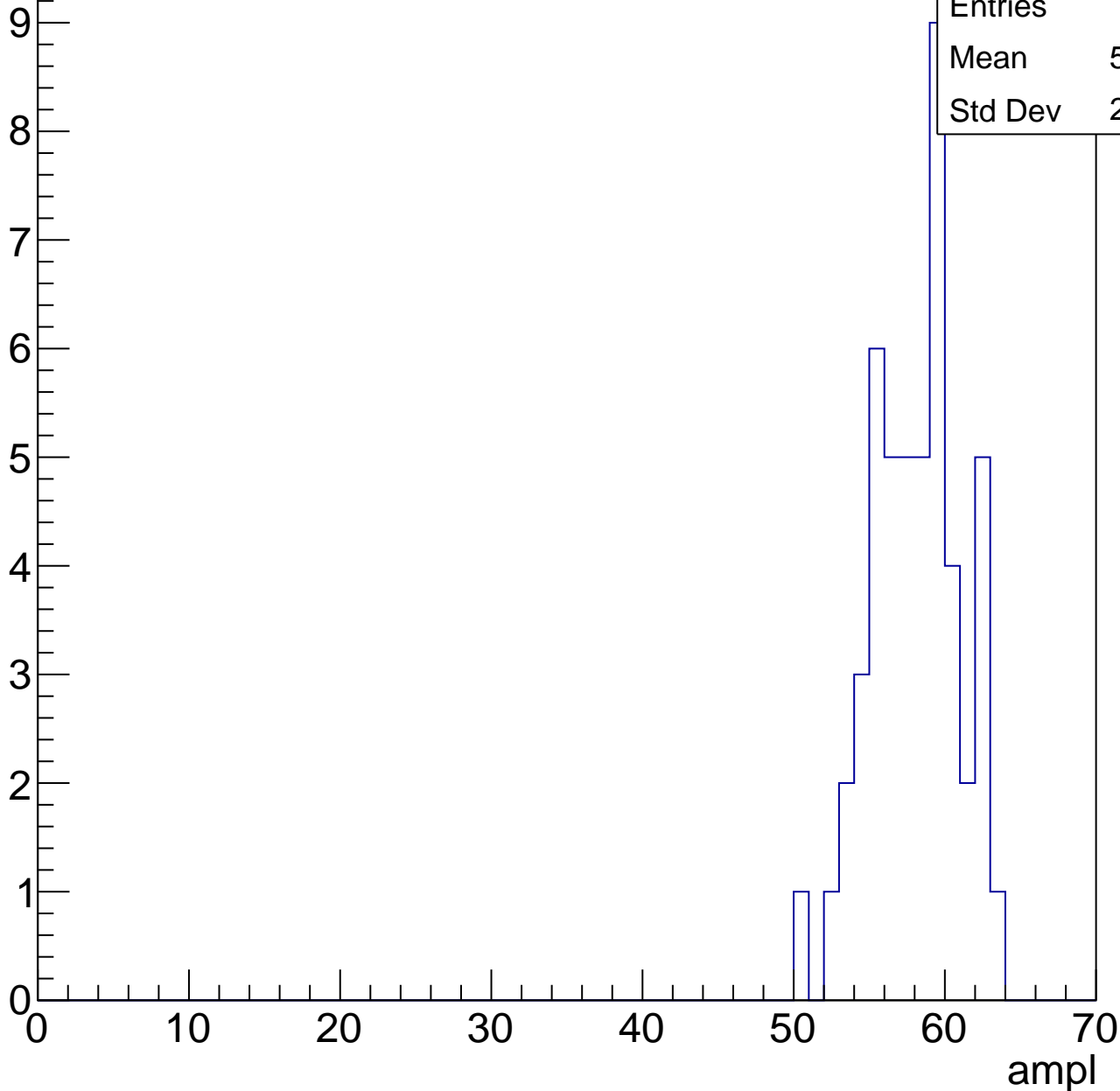


B1L102S, U13-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.57
Std Dev	2.914



B1L102S, U13-ch6, adc5

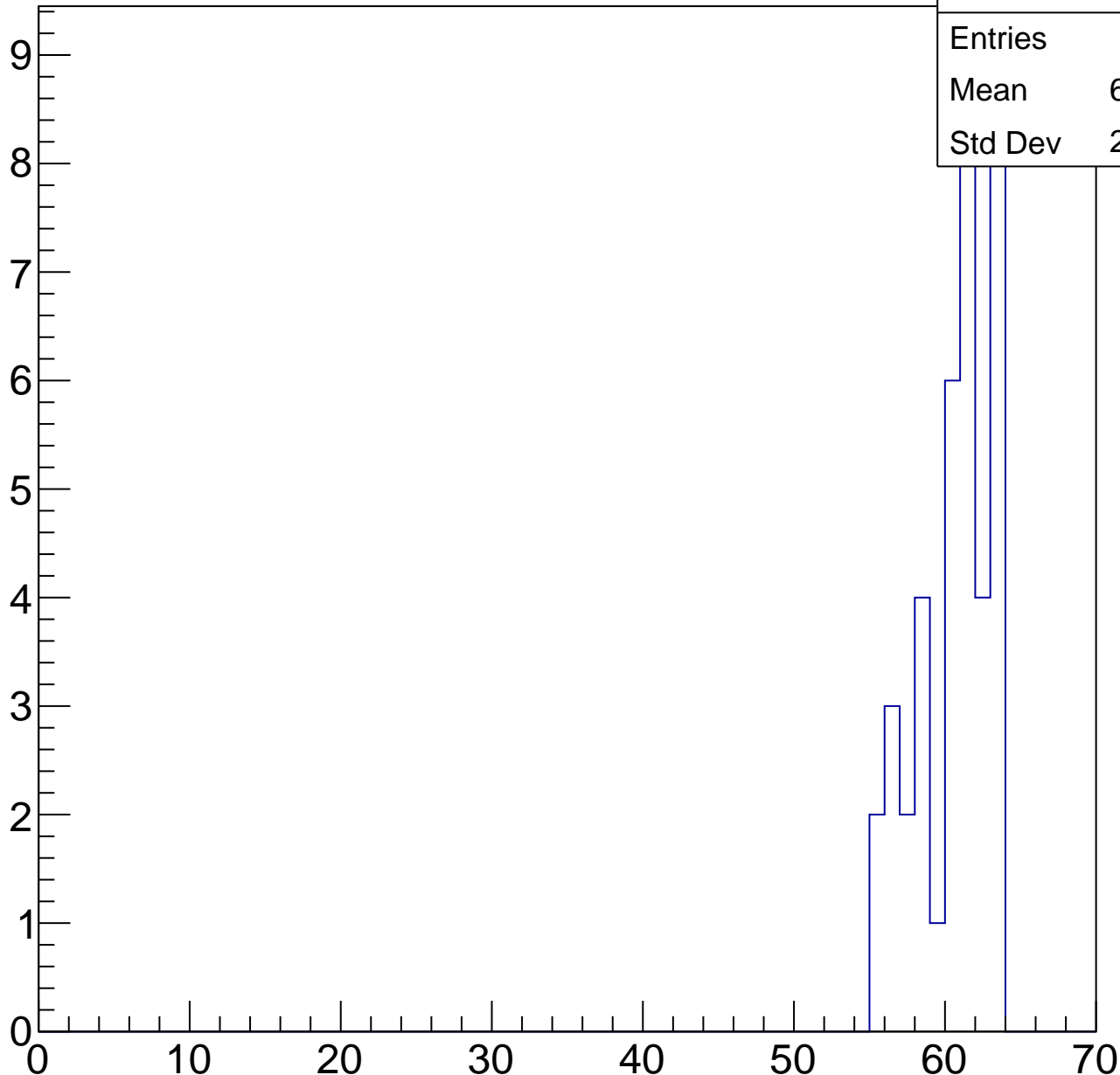
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.15
Std Dev	2.466

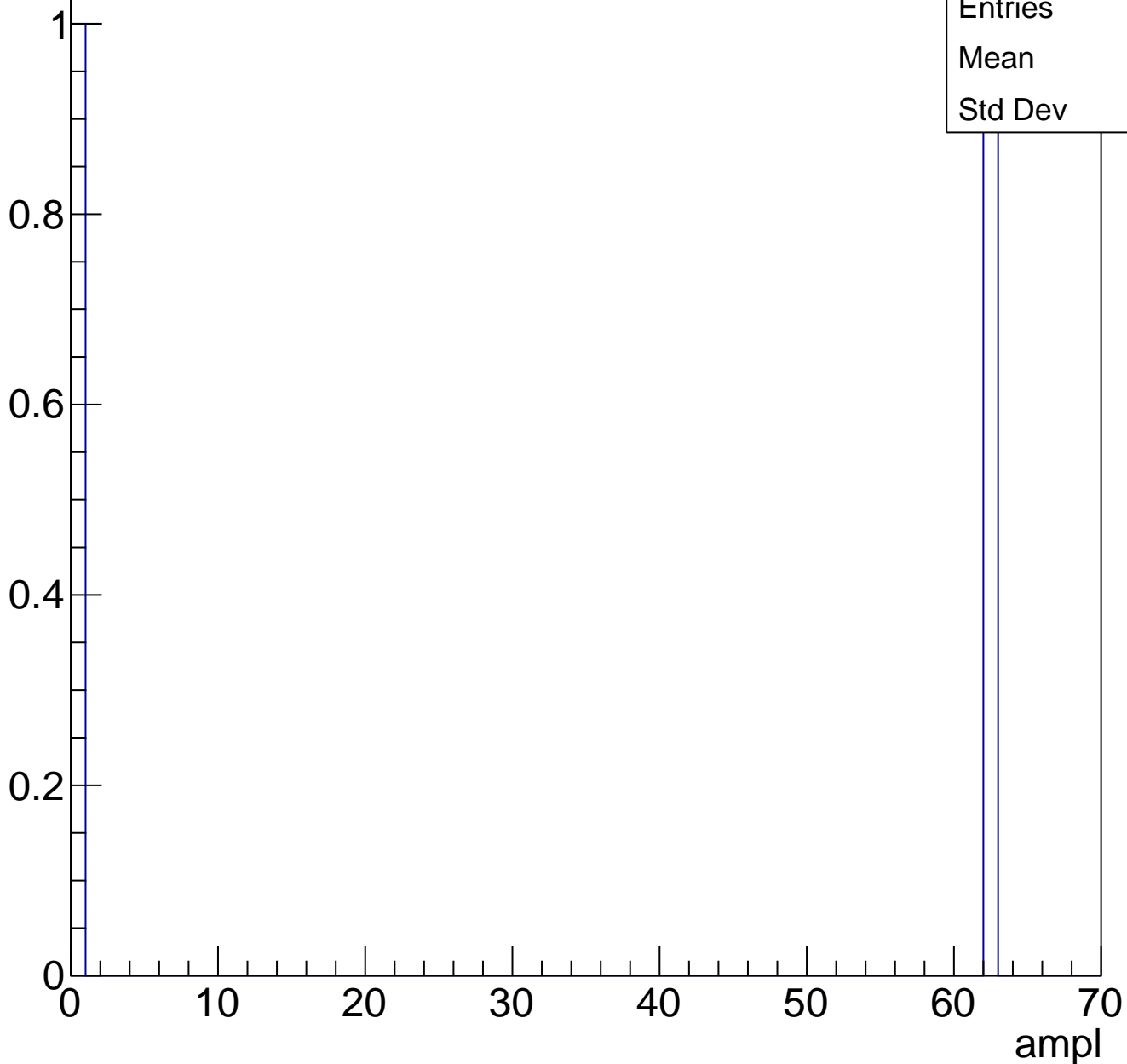
ampl



B1L102S, U13-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch7, adc0

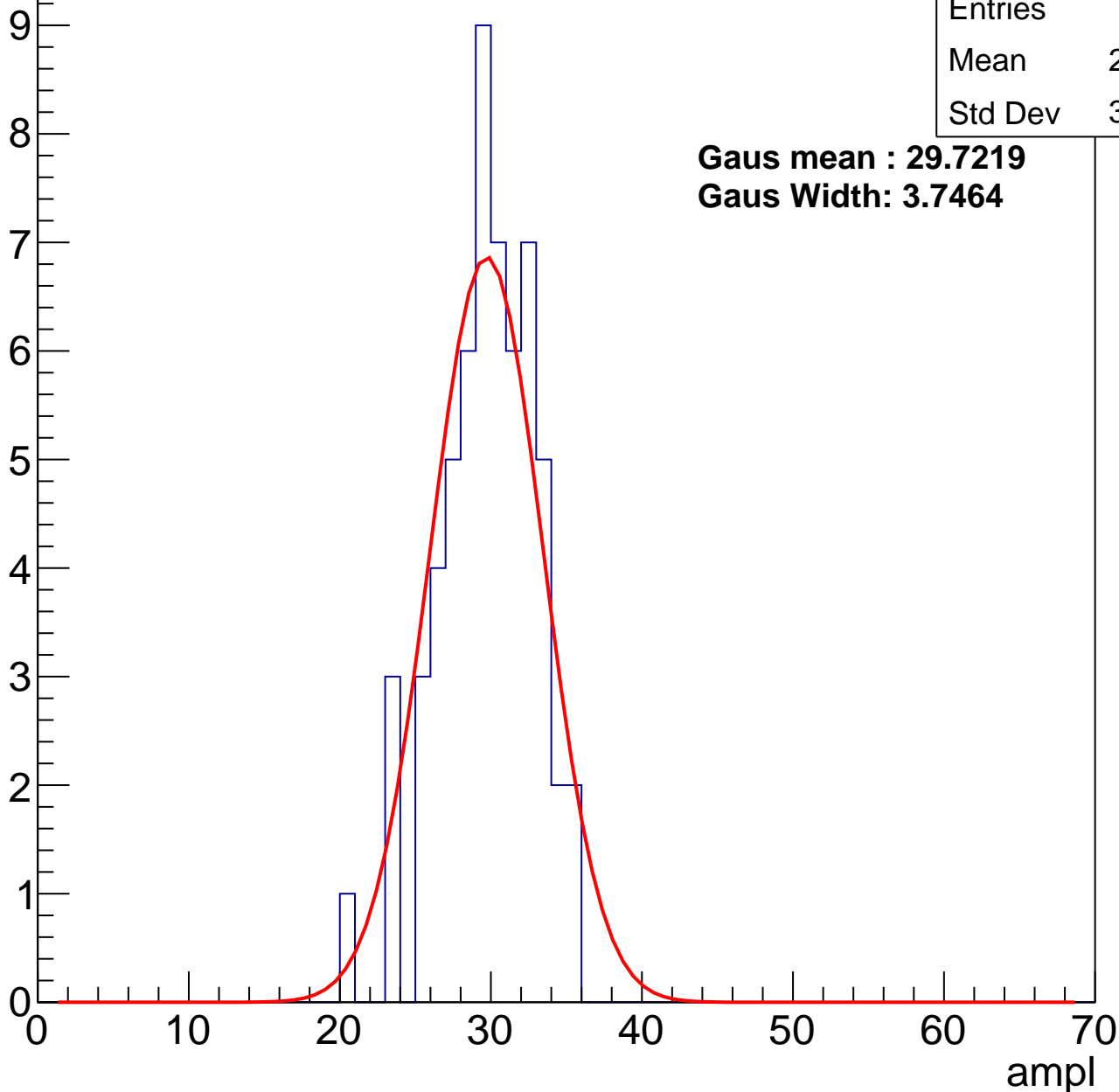
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.25
Std Dev	3.139

Gaus mean : 29.7219

Gaus Width: 3.7464



B1L102S, U13-ch7, adc1

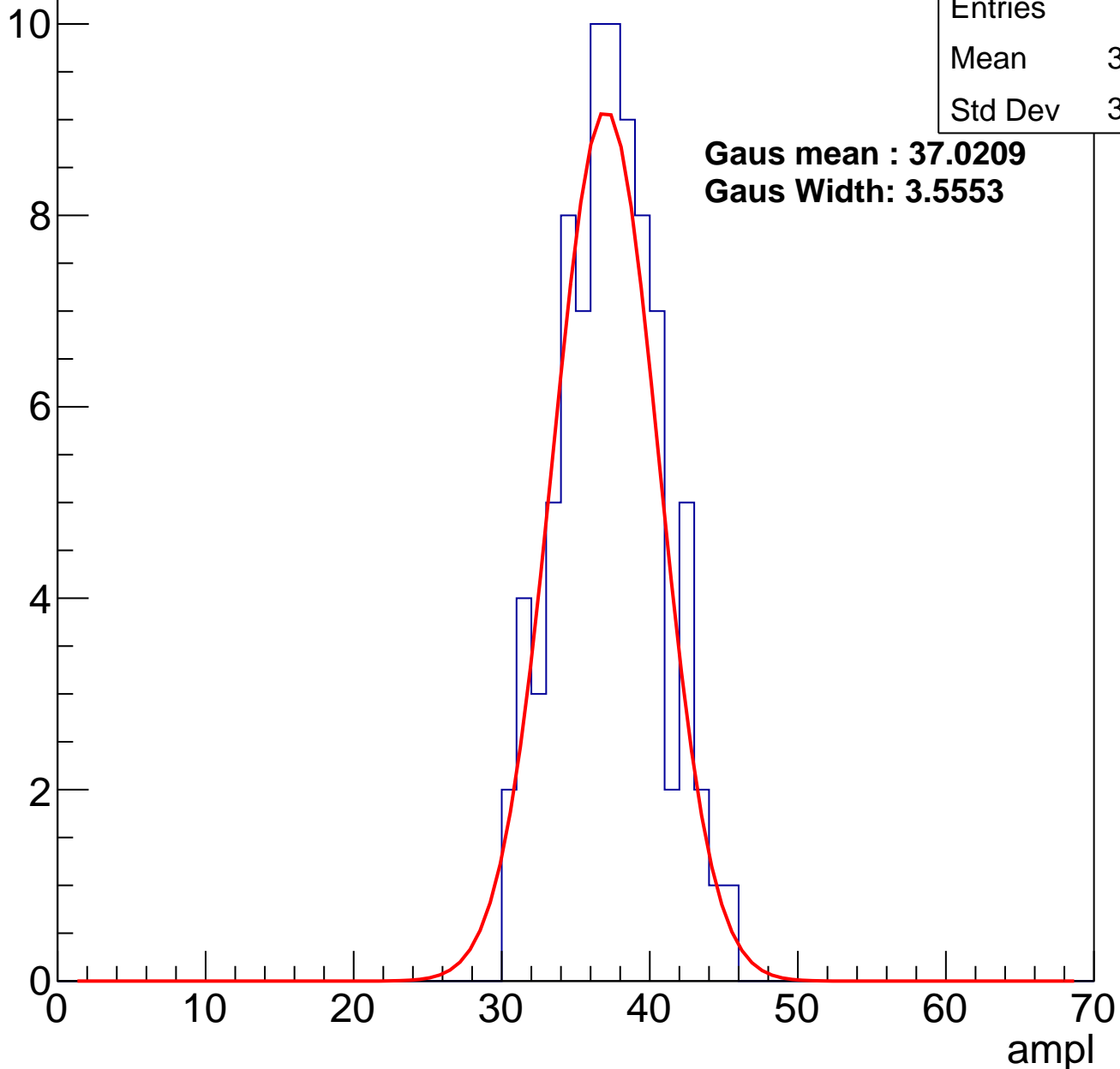
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	36.82
Std Dev	3.364

Gaus mean : 37.0209

Gaus Width: 3.5553

Entry



B1L102S, U13-ch7, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	61
Mean	44.3
Std Dev	3.069

Gaus mean : 44.7776

Gaus Width: 3.5137

Entry

10

8

6

4

2

0

0

10

20

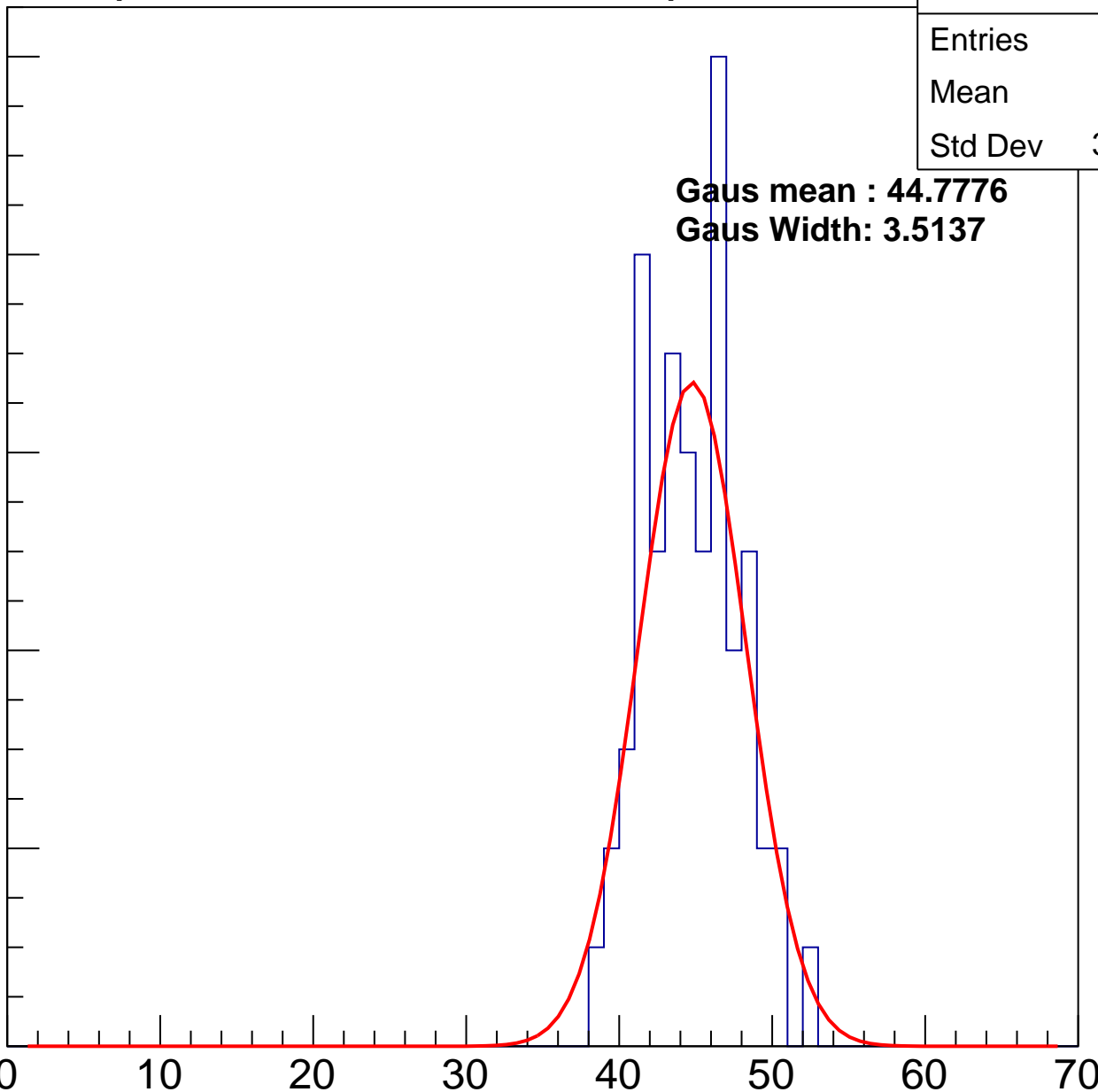
30

40

50

60

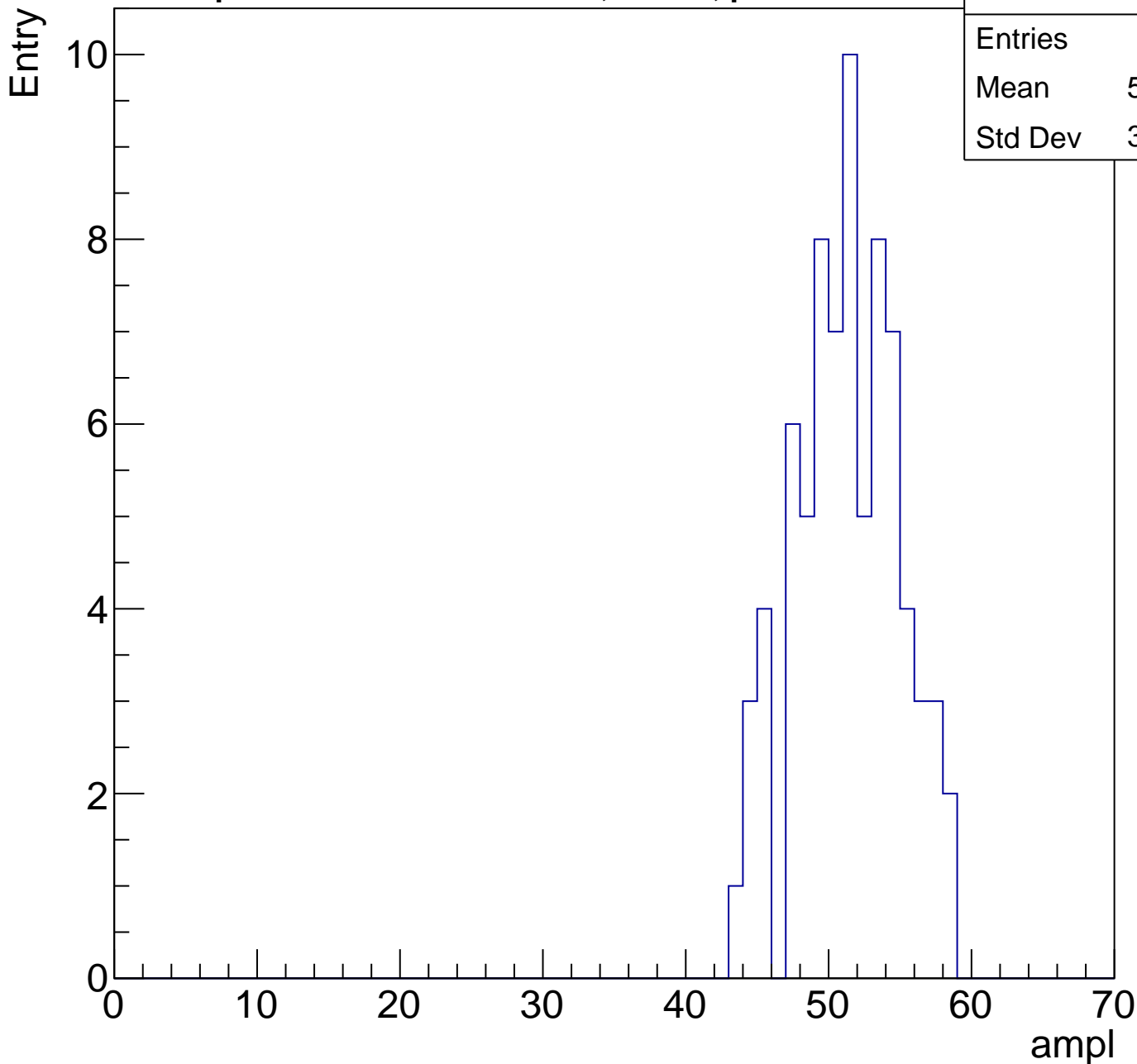
ampl



B1L102S, U13-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	50.87
Std Dev	3.589

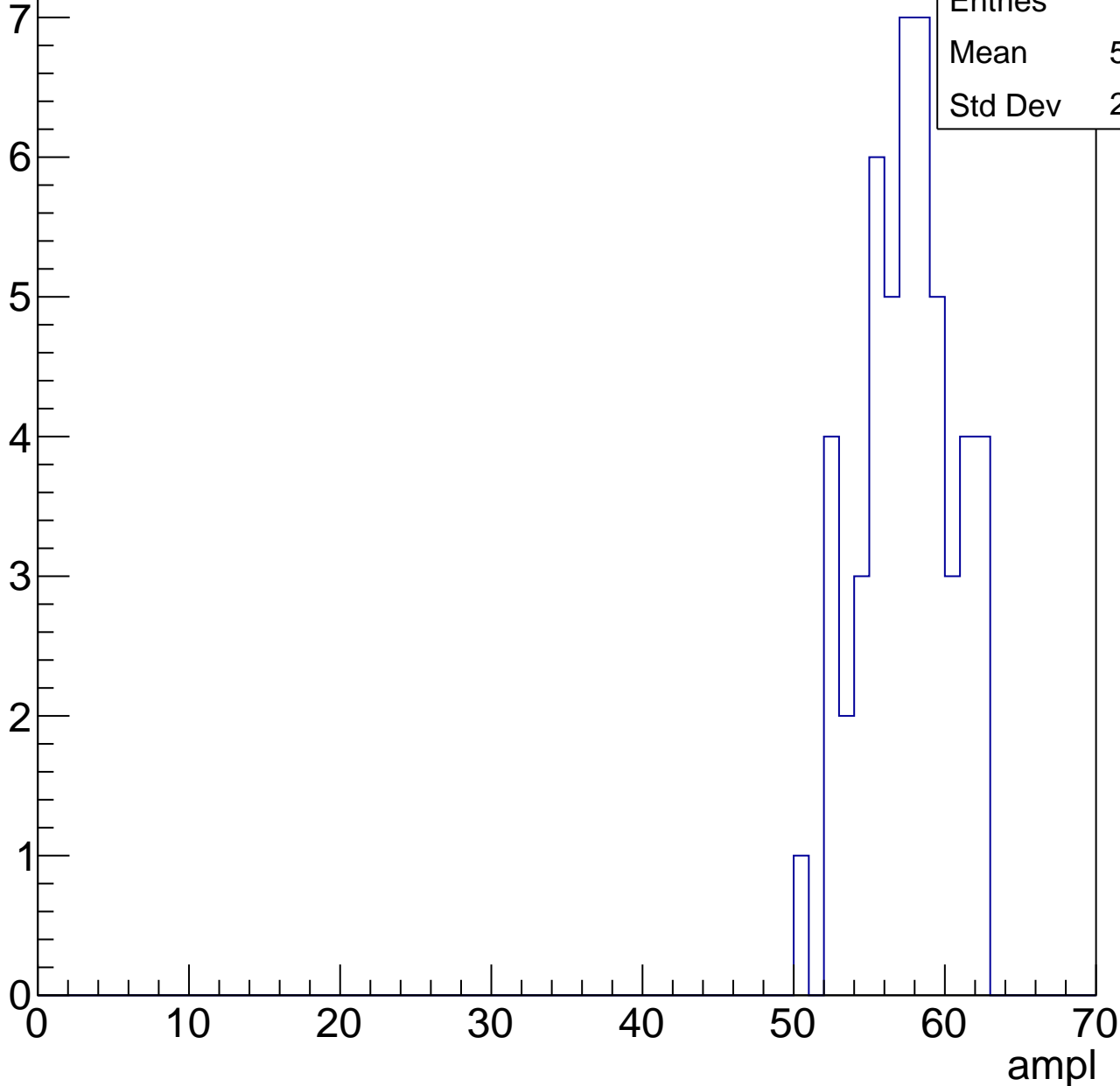


B1L102S, U13-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.02
Std Dev	2.987

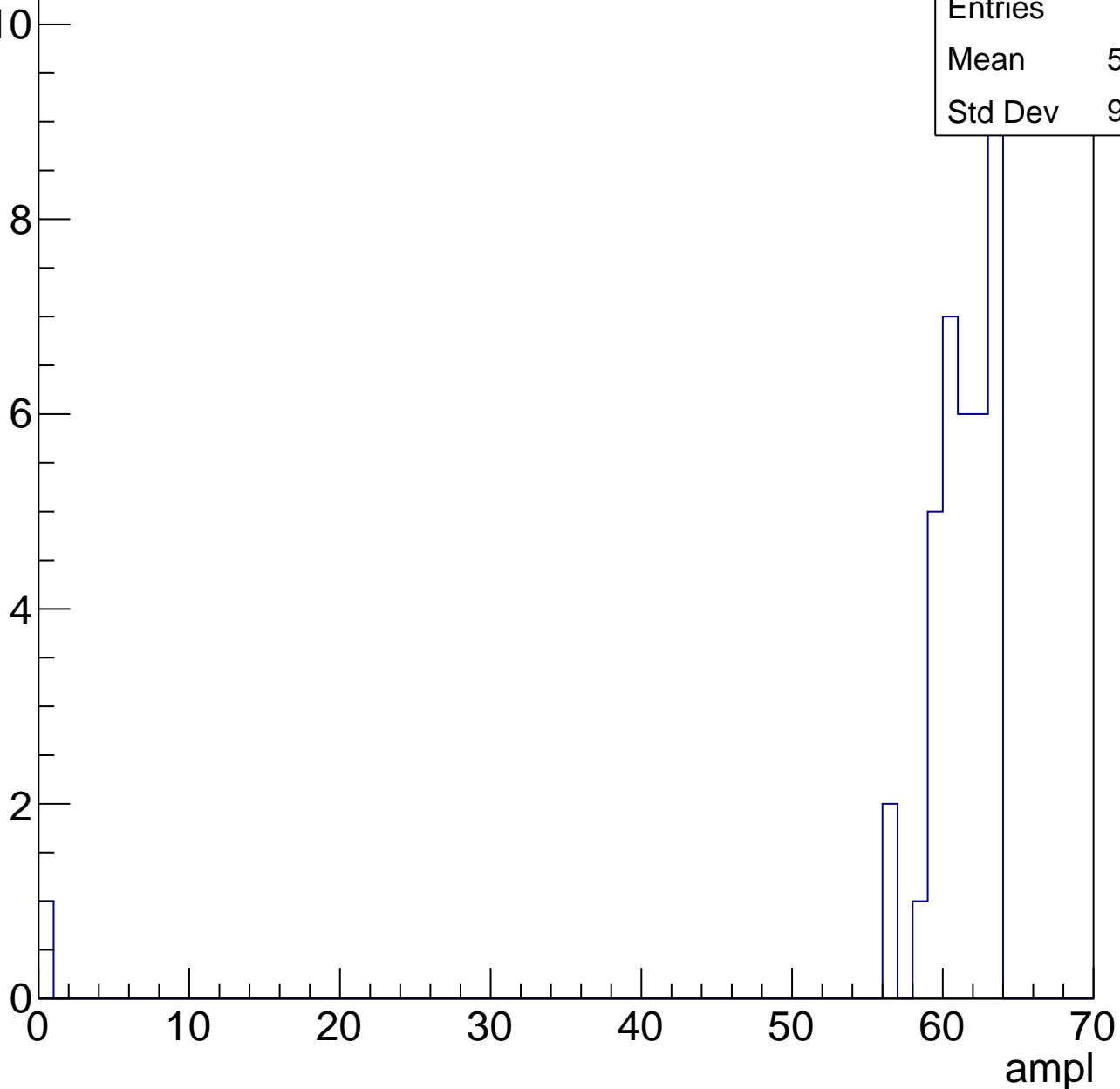


B1L102S, U13-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	59.29
Std Dev	9.923



B1L102S, U13-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch8, adc0

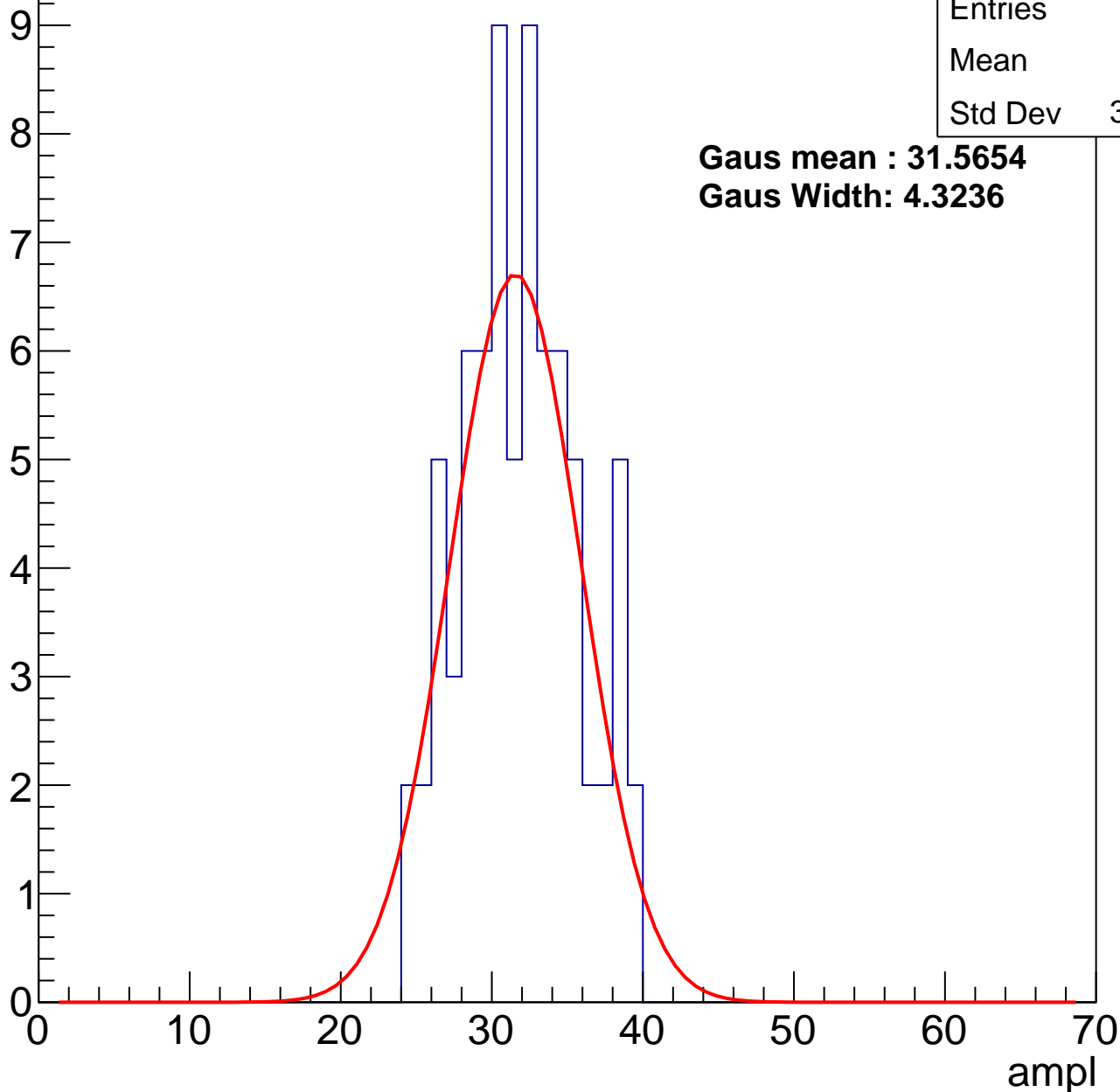
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	31.4
Std Dev	3.795

Gaus mean : 31.5654

Gaus Width: 4.3236



B1L102S, U13-ch8, adc1

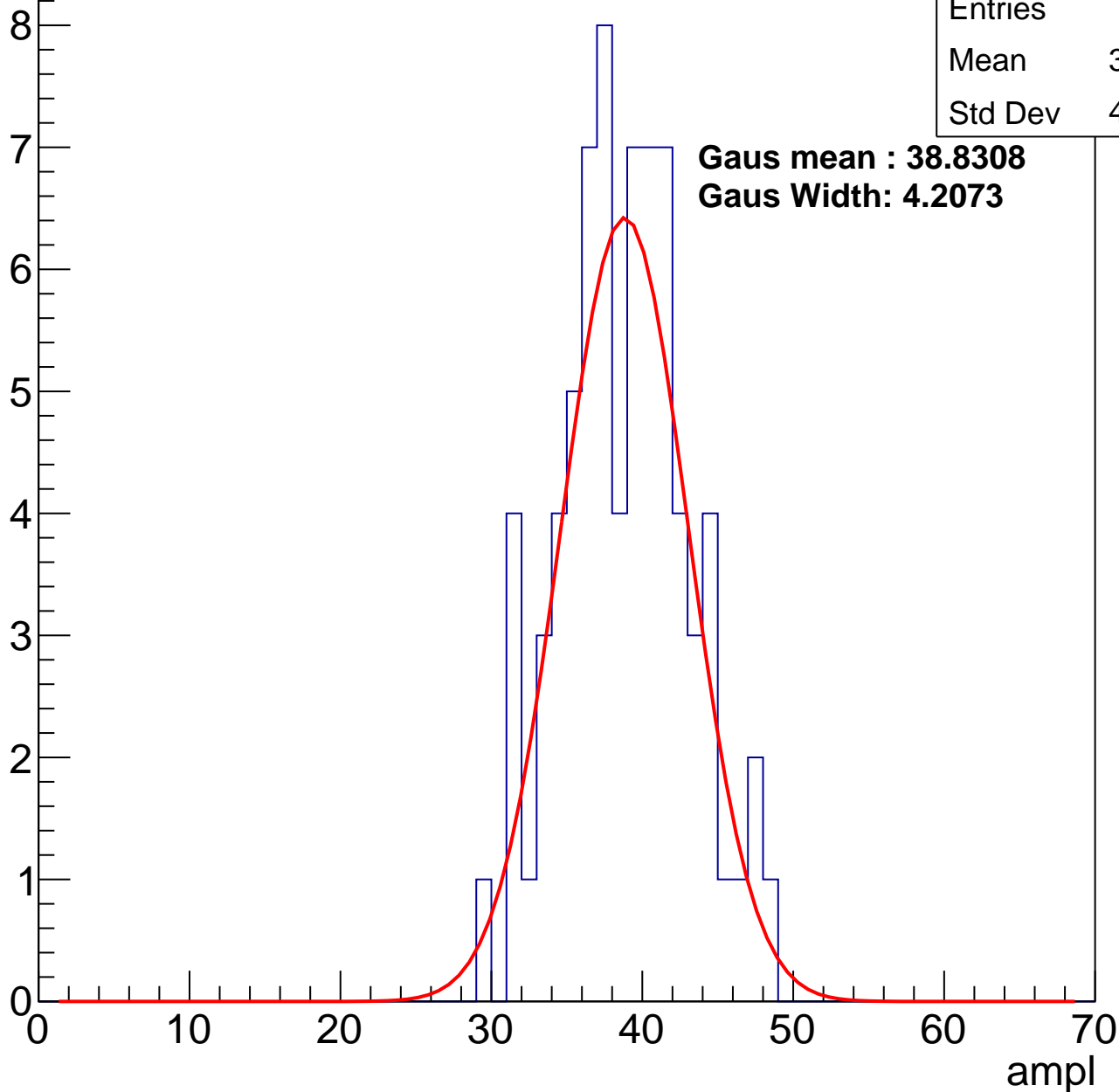
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	38.39
Std Dev	4.155

Gaus mean : 38.8308

Gaus Width: 4.2073

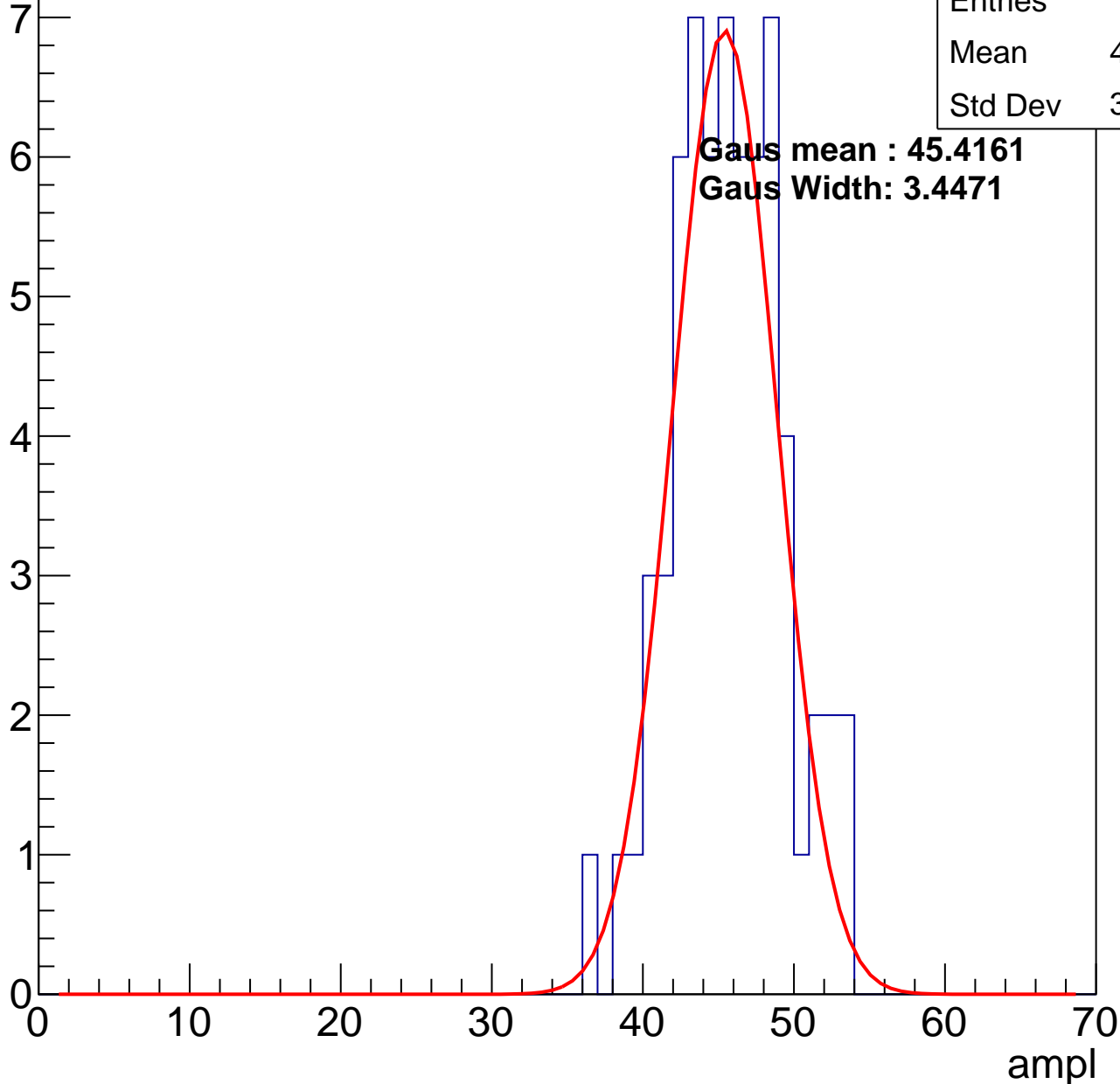


B1L102S, U13-ch8, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

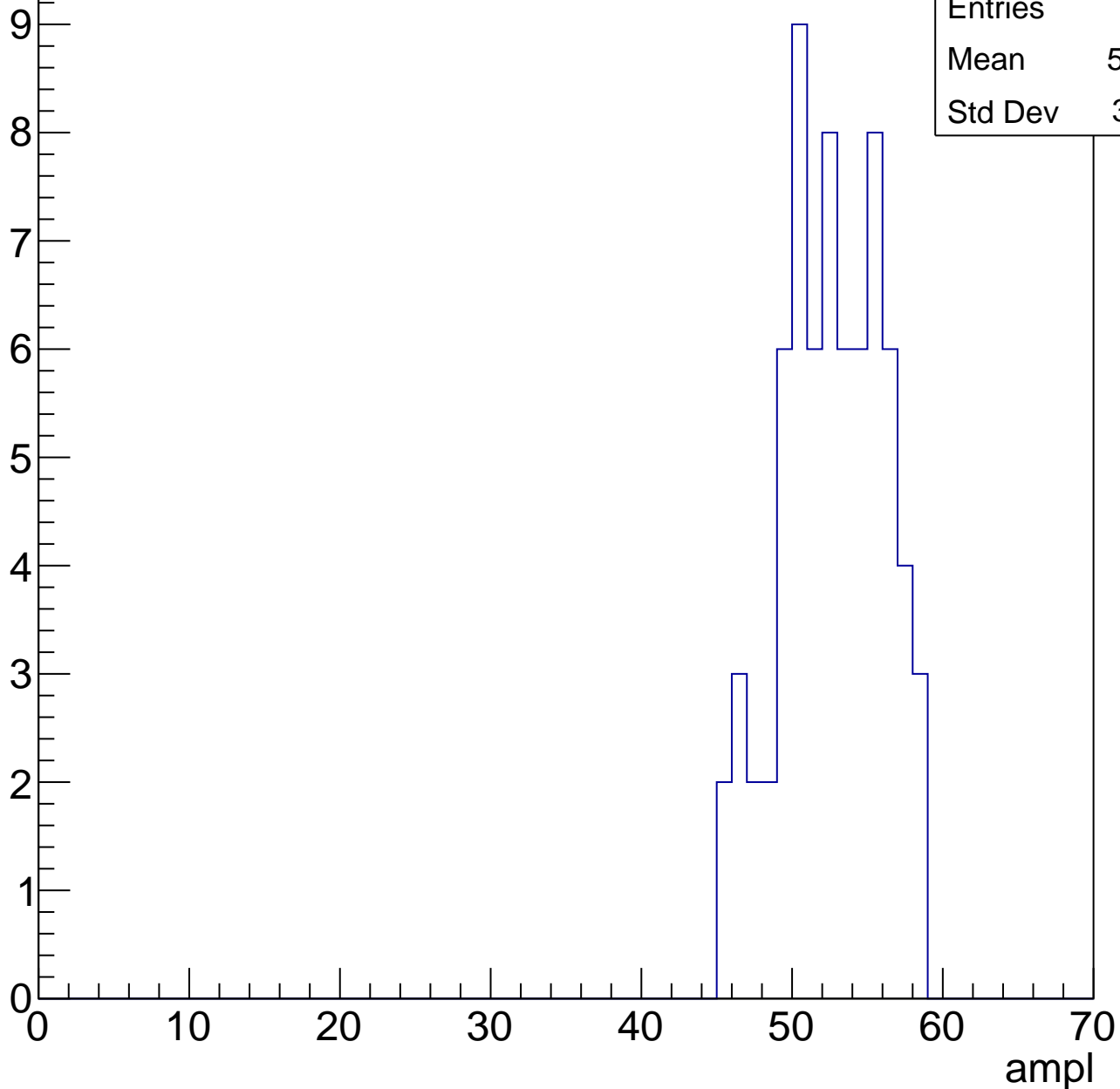
Entries	65
Mean	45.23
Std Dev	3.624



B1L102S, U13-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

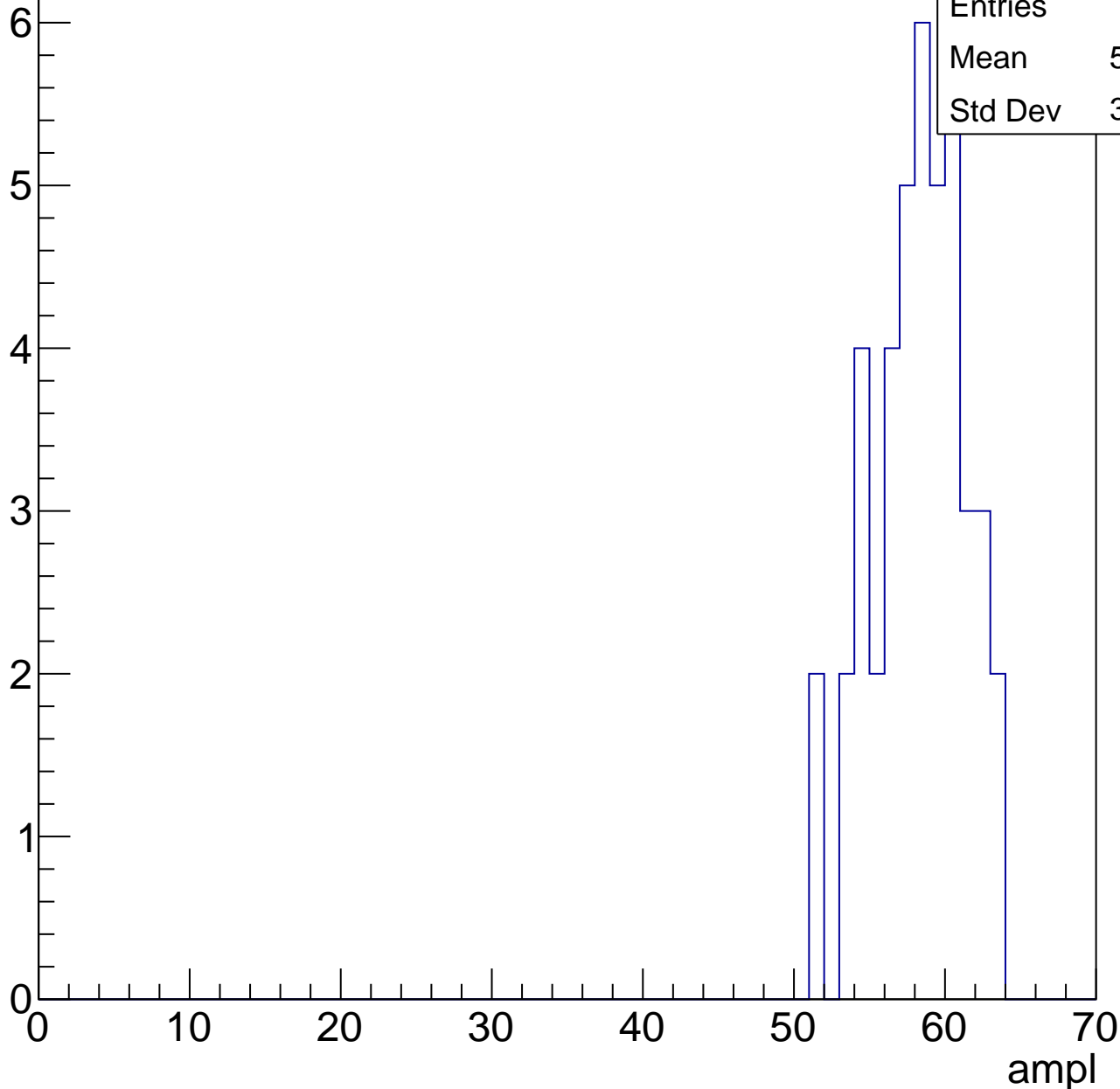


Entries	71
Mean	52.17
Std Dev	3.331

B1L102S, U13-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

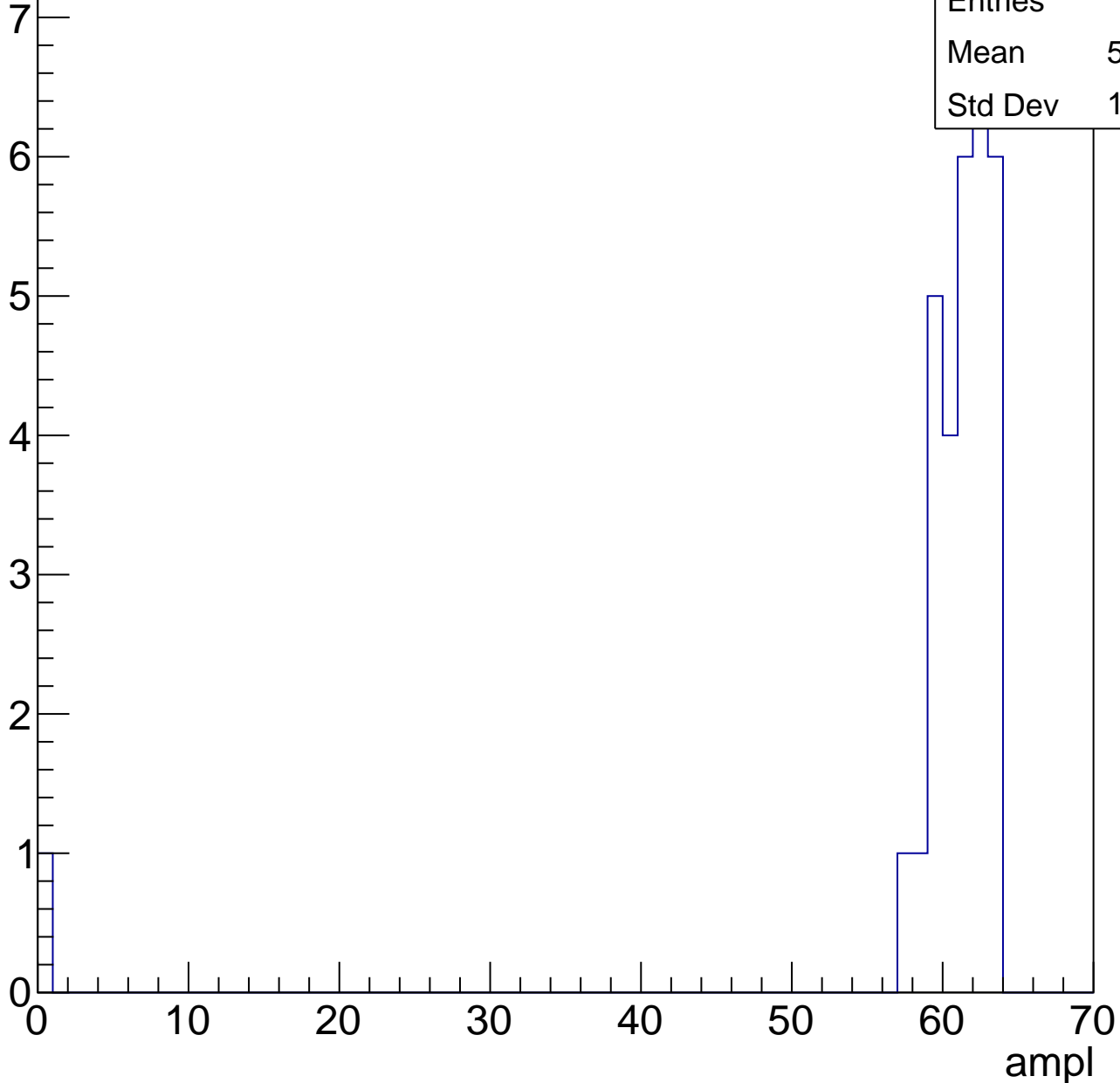


B1L102S, U13-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	58.97
Std Dev	10.88



B1L102S, U13-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L102S, U13-ch9, adc0

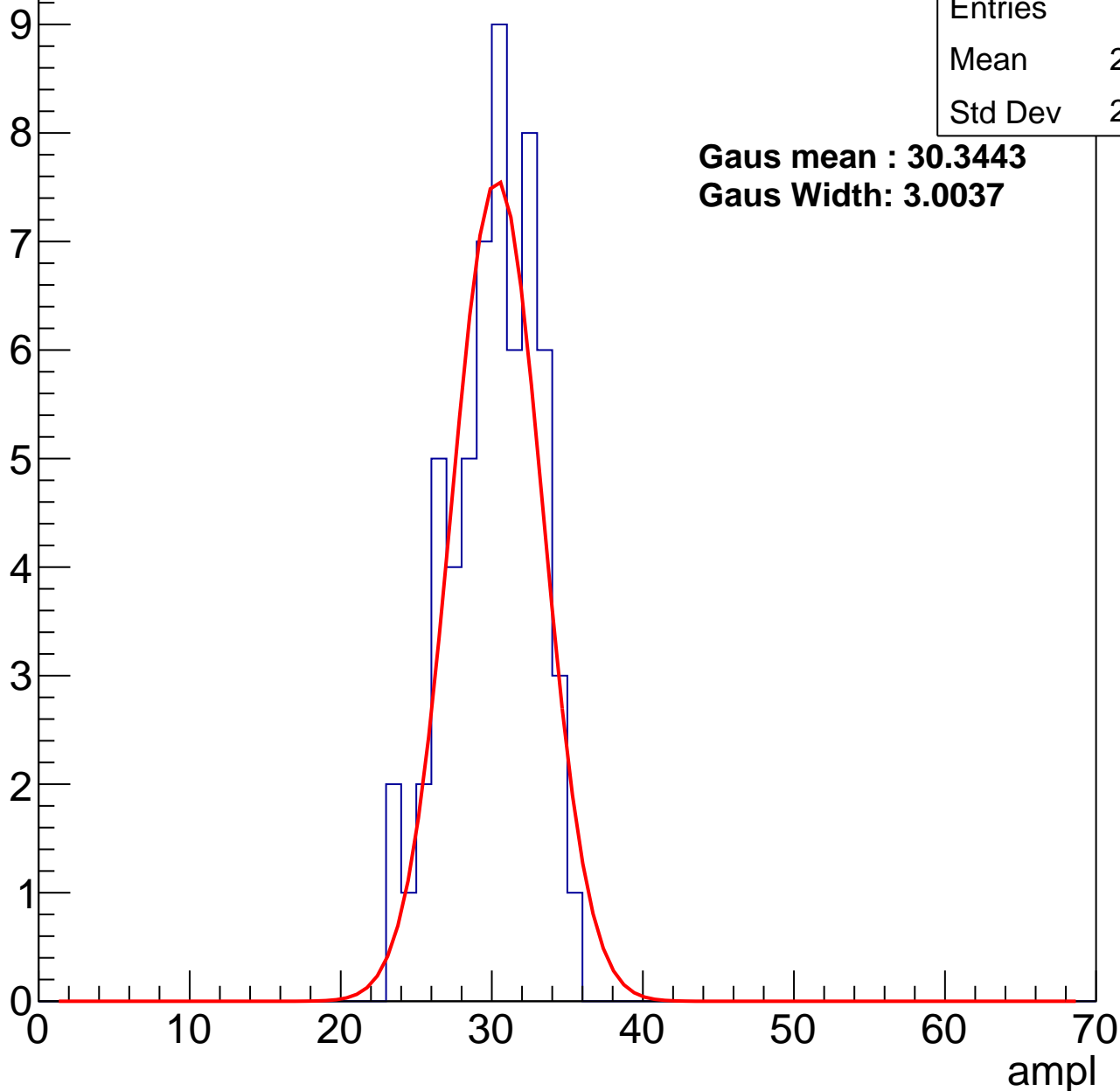
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.63
Std Dev	2.864

Gaus mean : 30.3443

Gaus Width: 3.0037



B1L102S, U13-ch9, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	37.23
Std Dev	3.49

Gaus mean : 37.8188

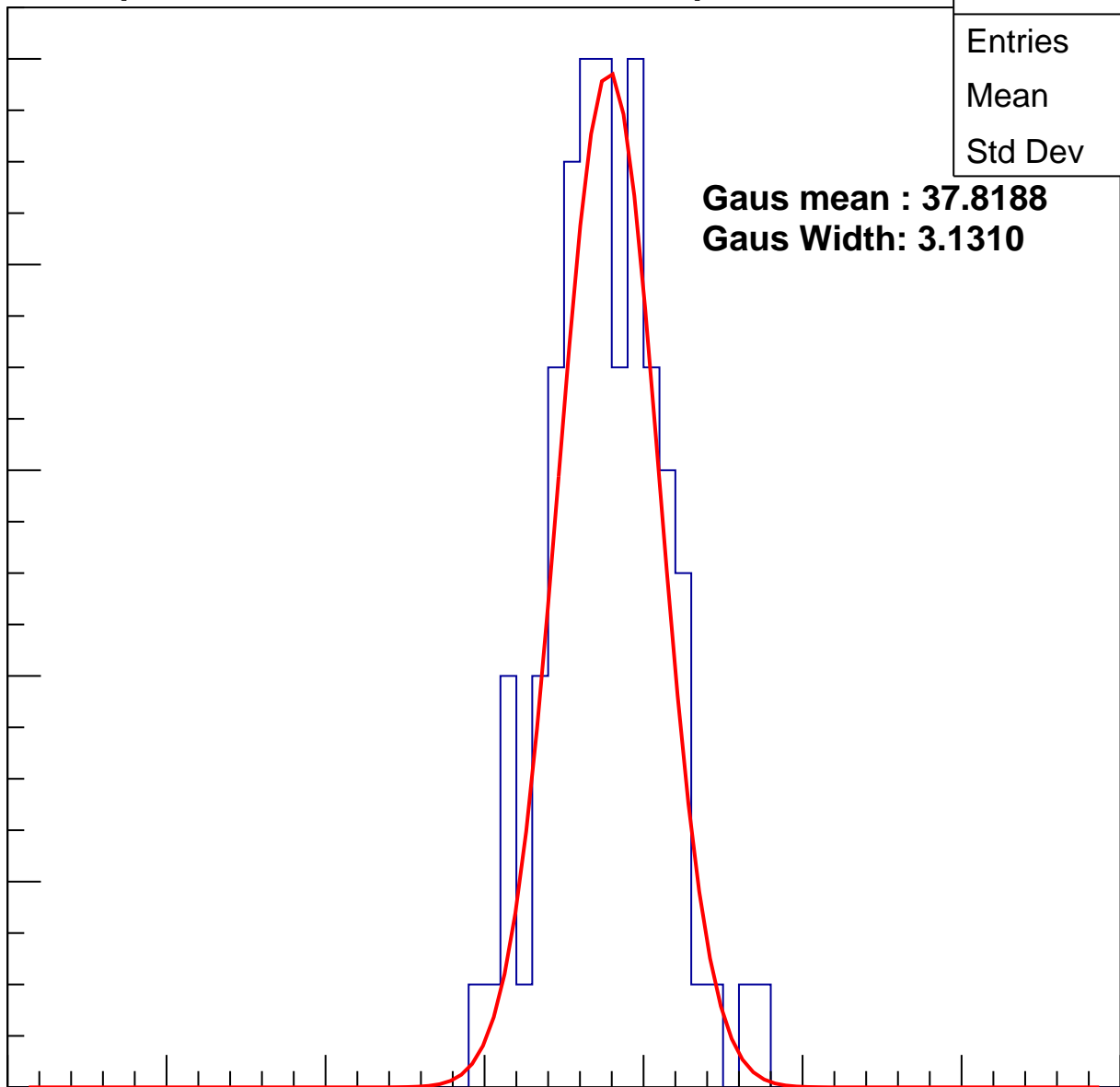
Gaus Width: 3.1310

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch9, adc2

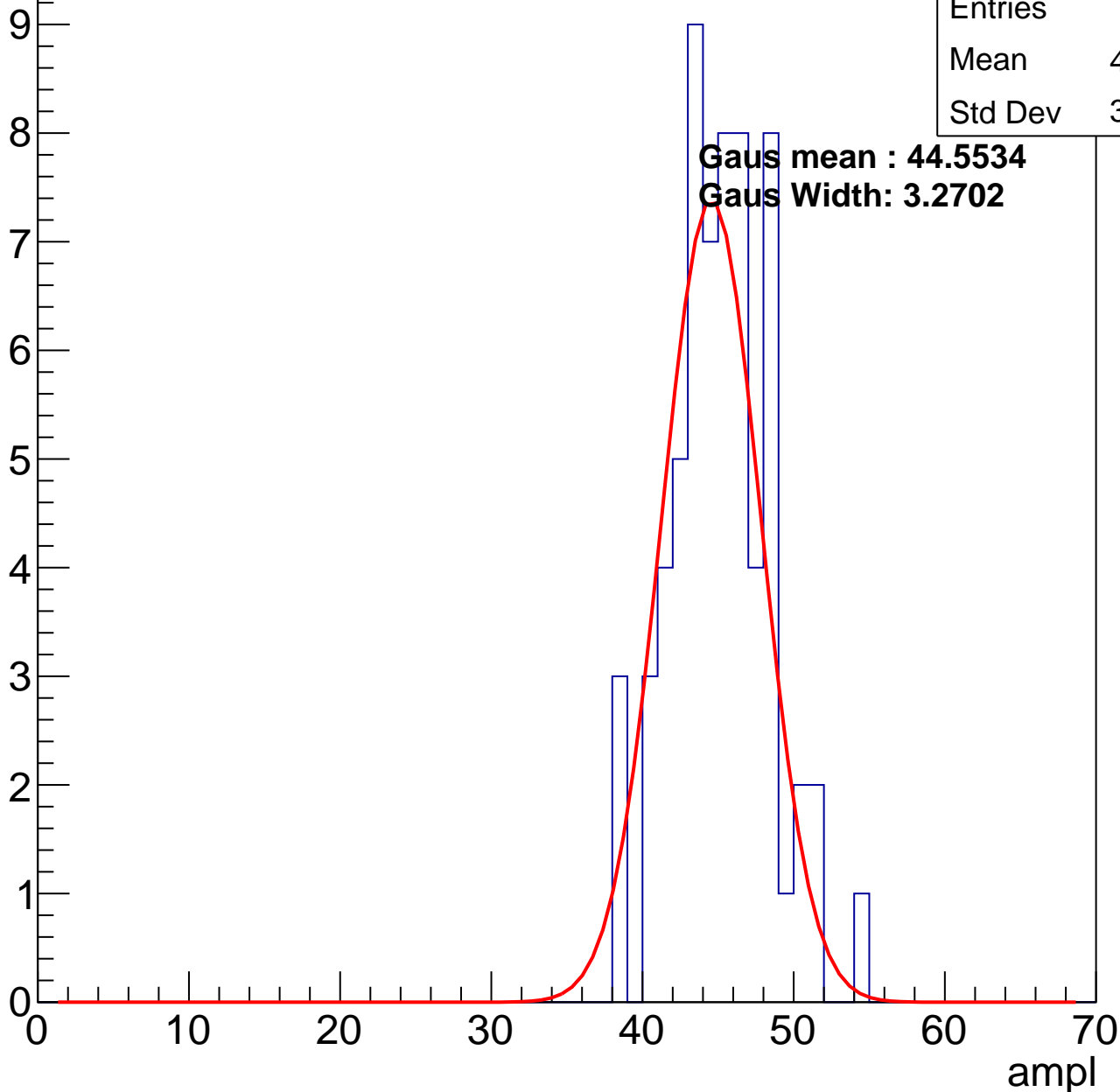
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.74
Std Dev	3.245

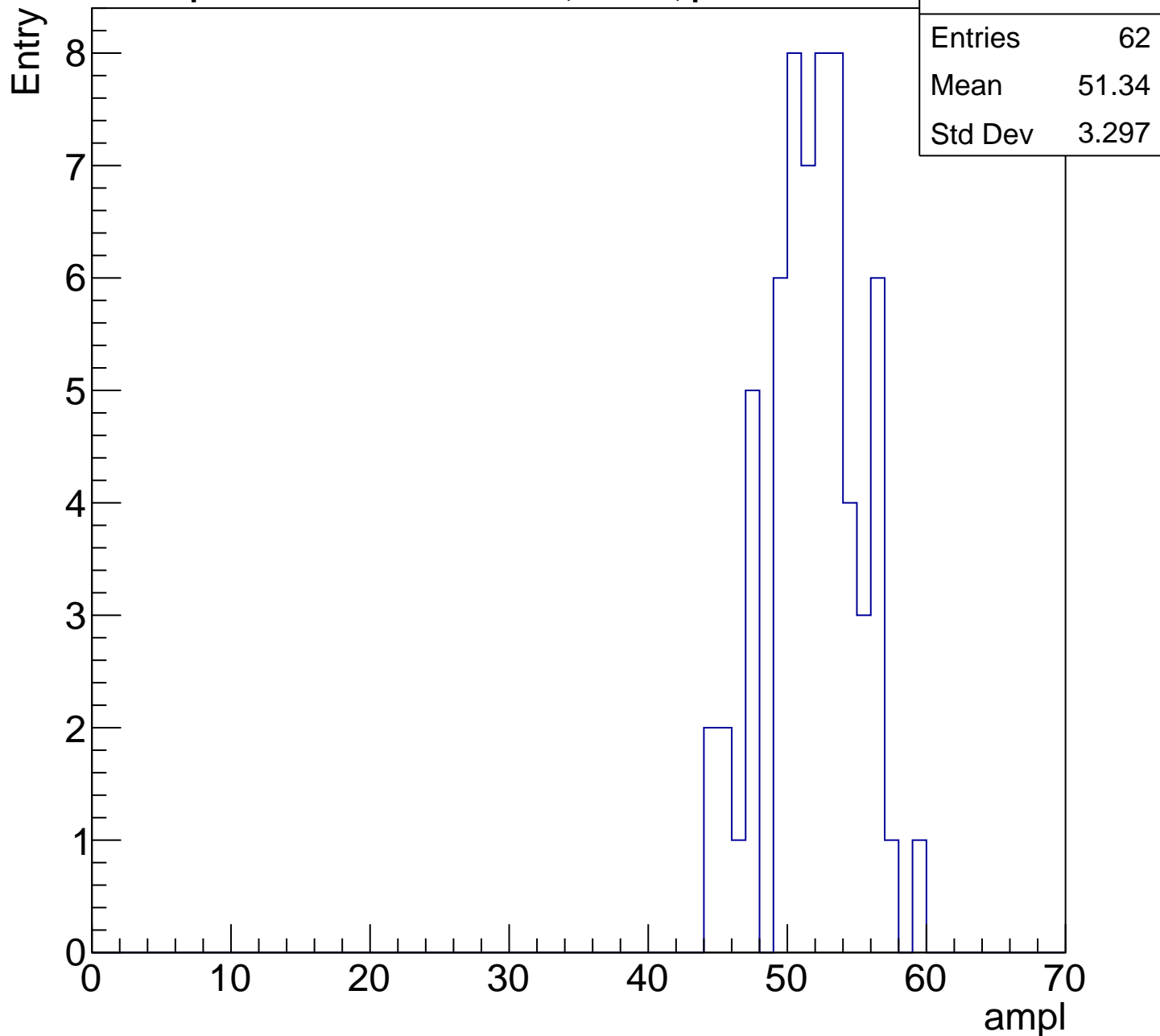
Gaus mean : 44.5534

Gaus Width: 3.2702



B1L102S, U13-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

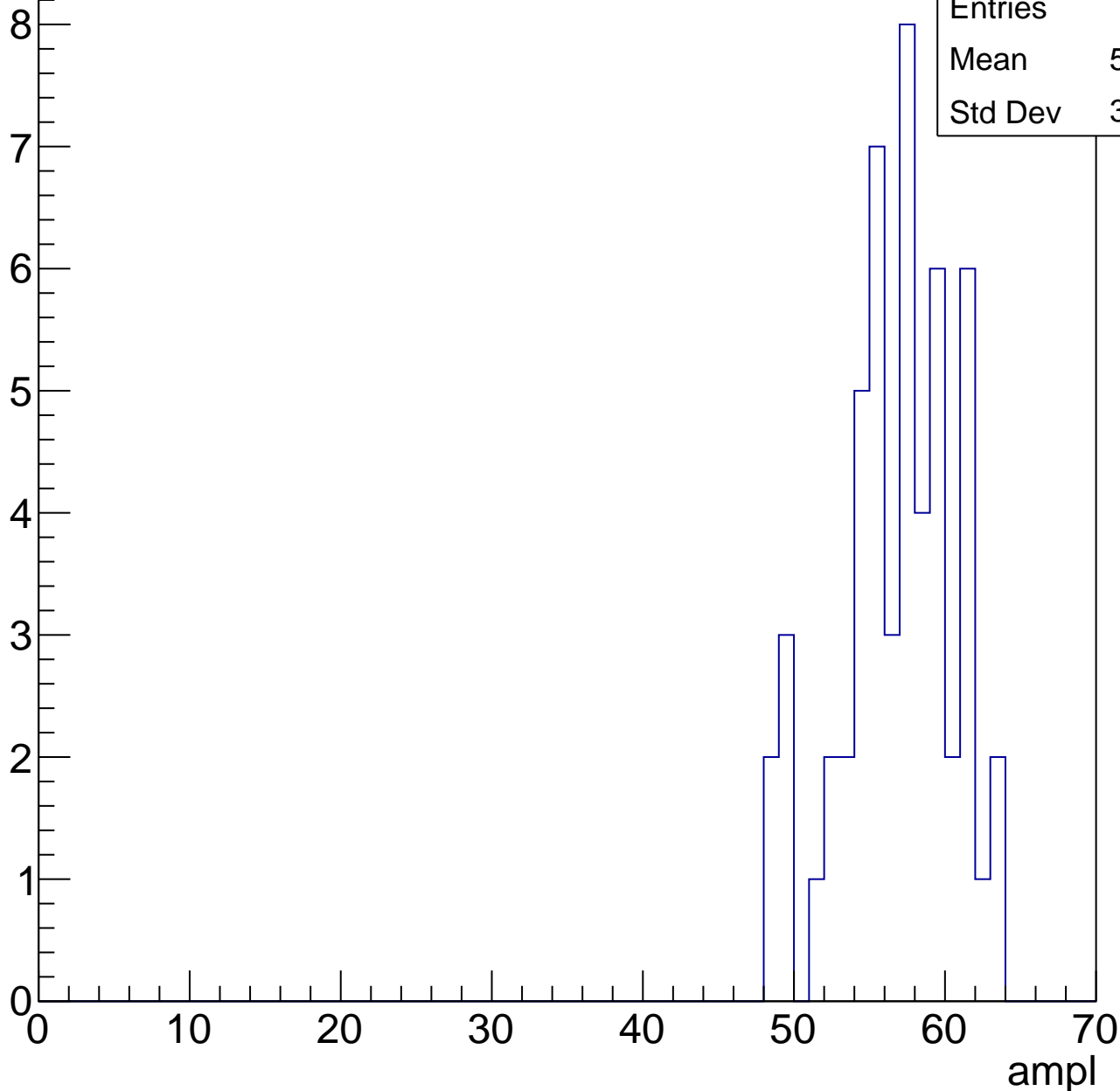


B1L102S, U13-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56.35
Std Dev	3.762

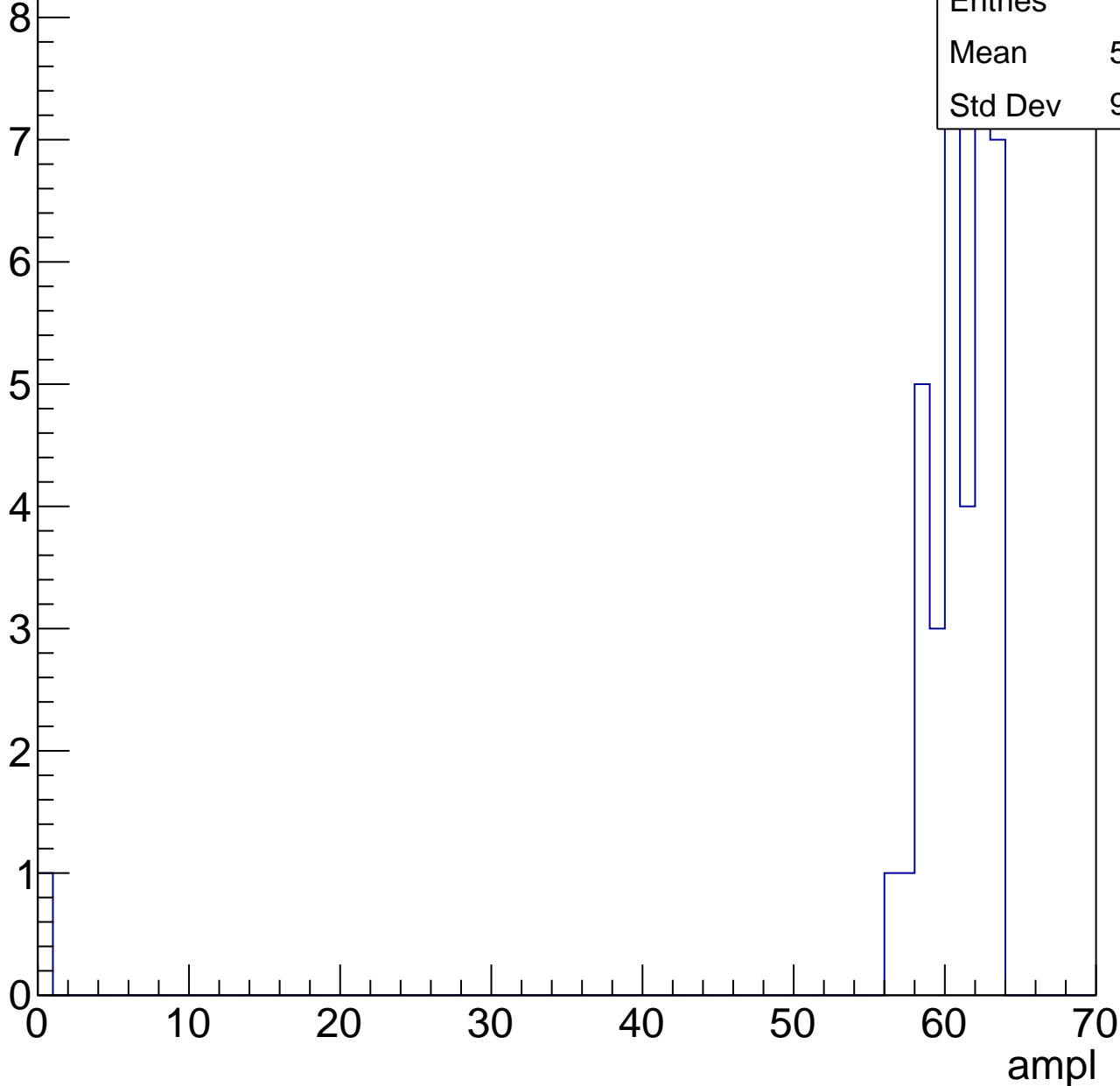


B1L102S, U13-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.97
Std Dev	9.877



B1L102S, U13-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

61

Std Dev

2.16

B1L102S, U13-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch10, adc0

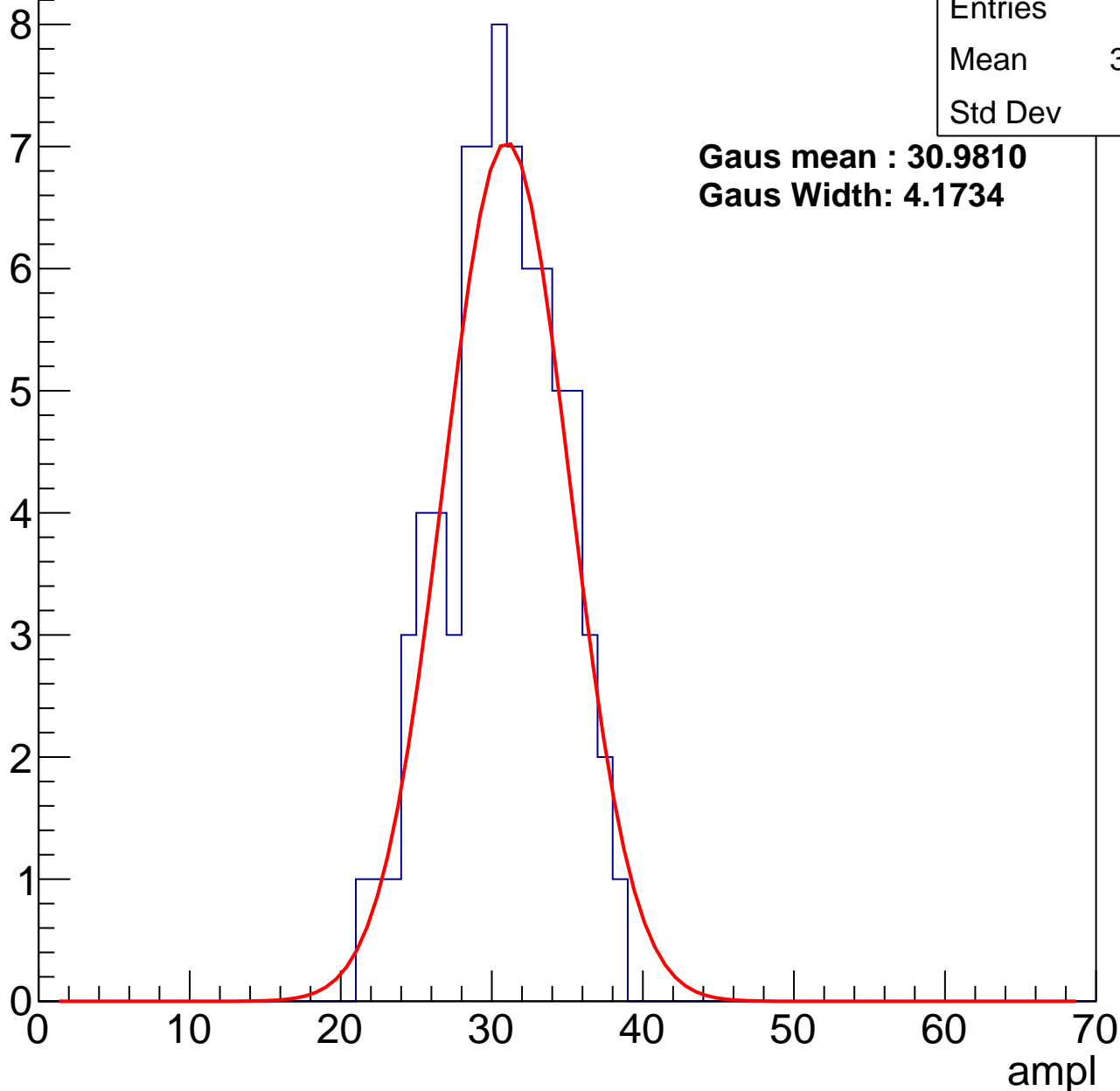
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	30.19
Std Dev	3.83

Gaus mean : 30.9810

Gaus Width: 4.1734



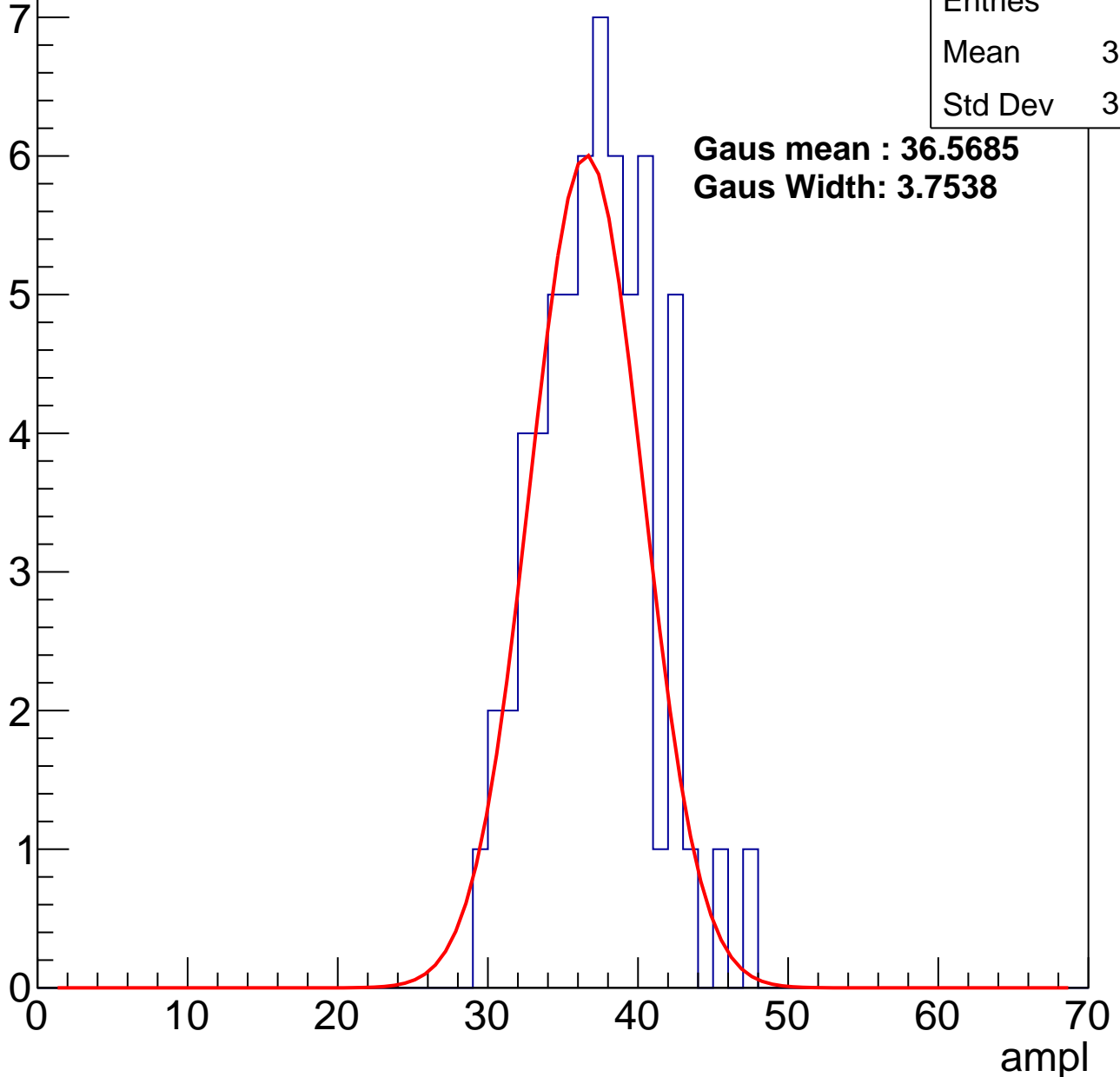
B1L102S, U13-ch10, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.77
Std Dev	3.782

Gaus mean : 36.5685
Gaus Width: 3.7538



B1L102S, U13-ch10, adc2

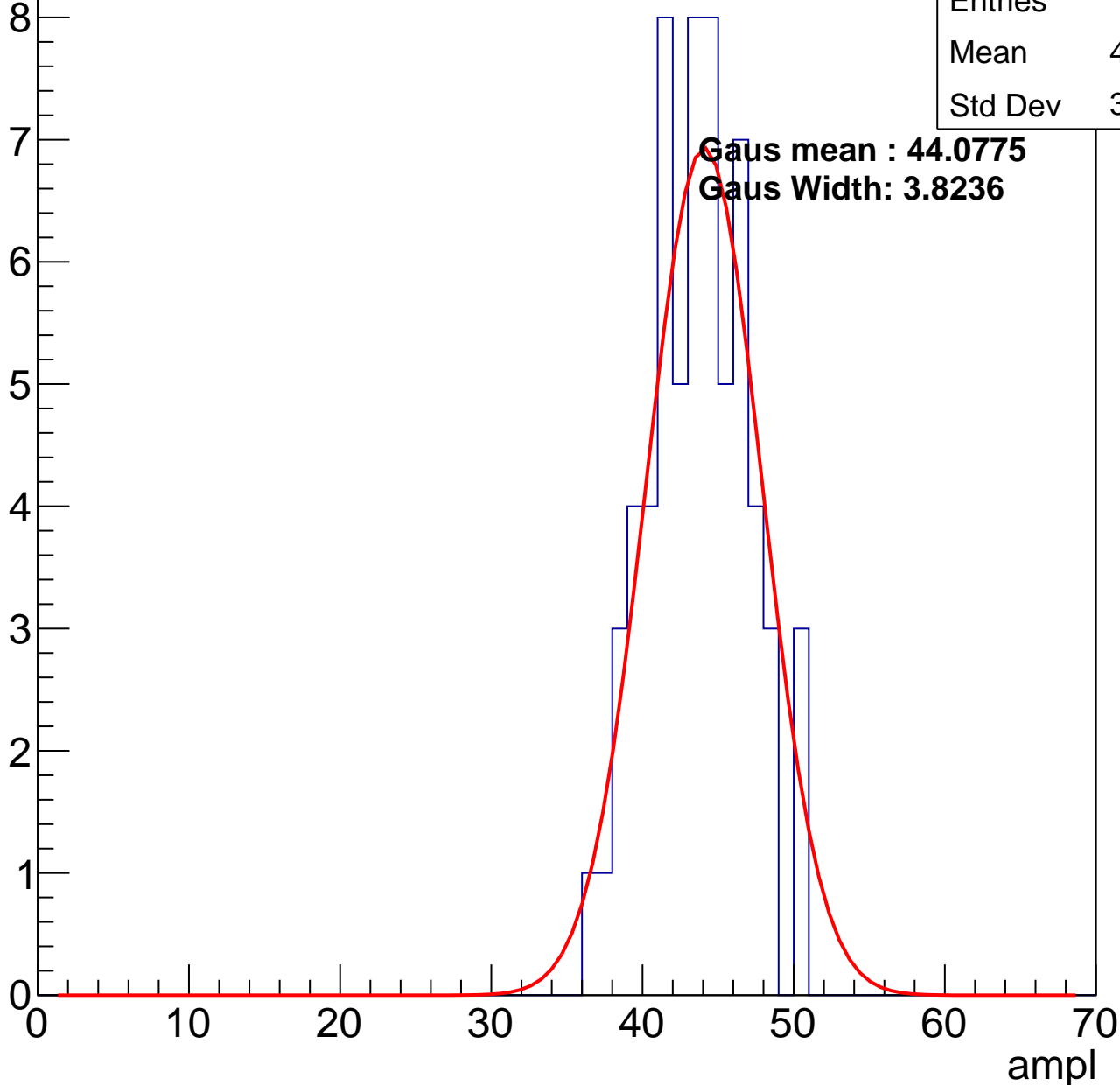
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.22
Std Dev	3.238

Gaus mean : 44.0775

Gaus Width: 3.8236

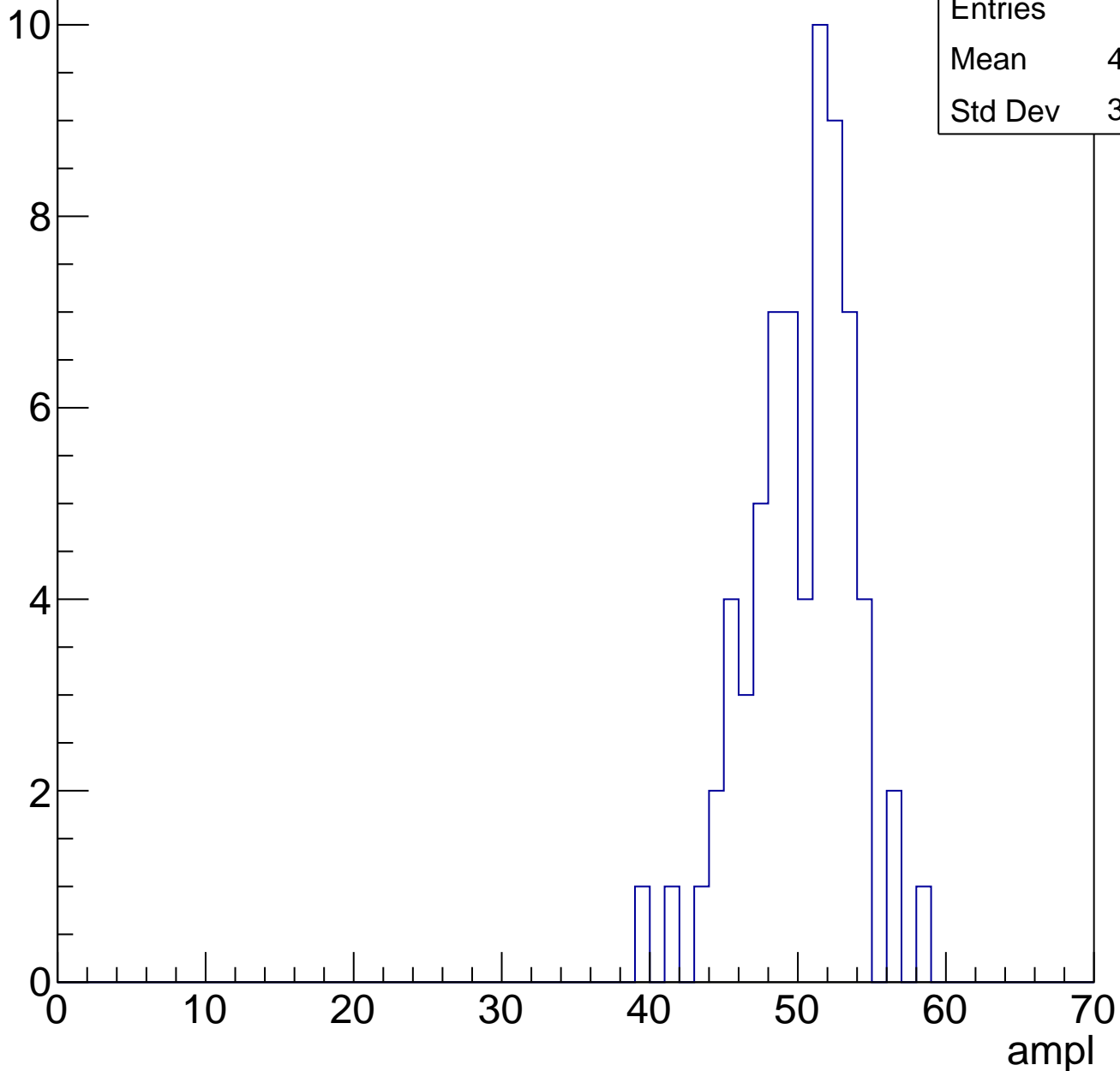


B1L102S, U13-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	49.68
Std Dev	3.542

Entry

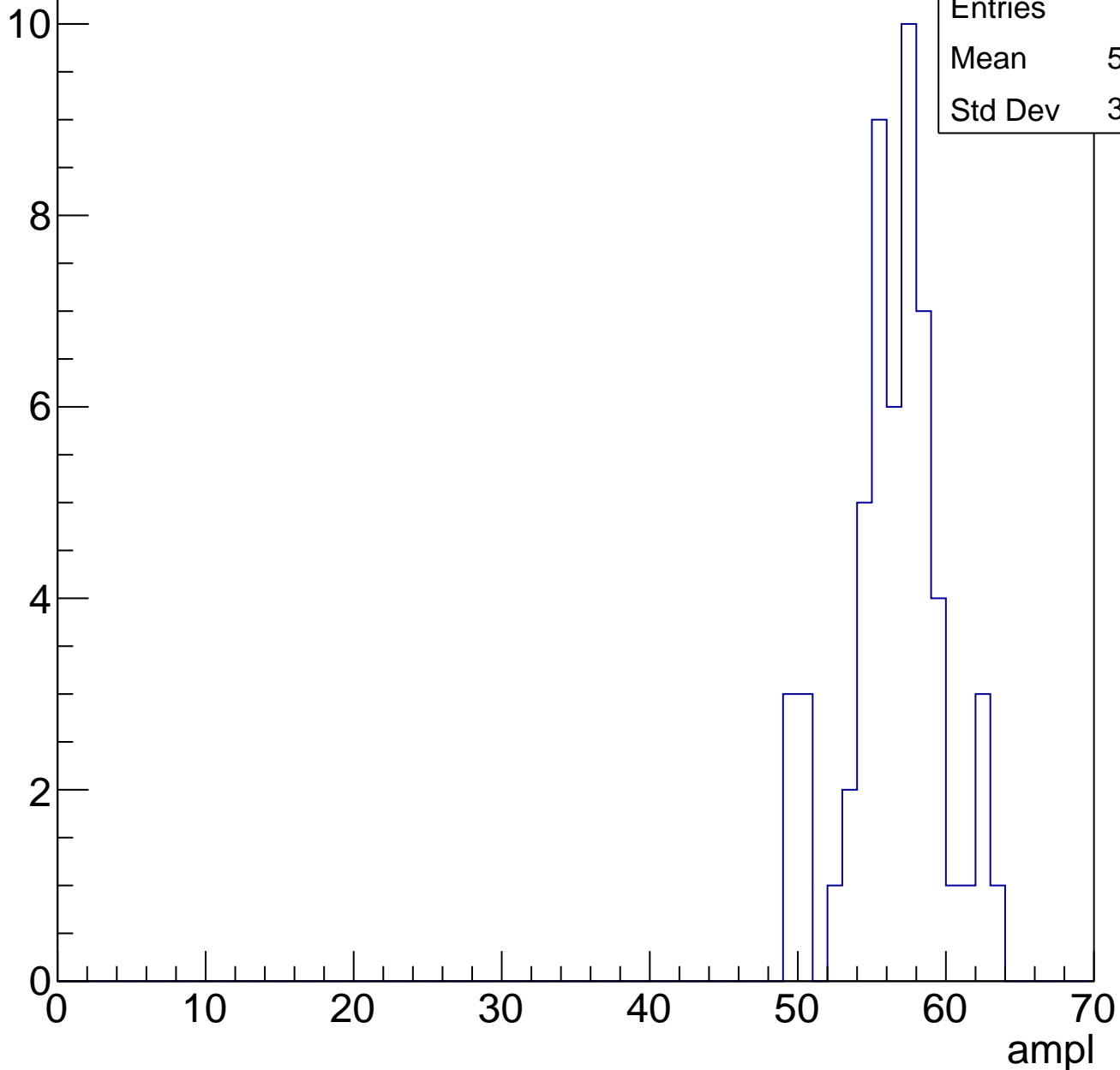


B1L102S, U13-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	56
Mean	56.04
Std Dev	3.246

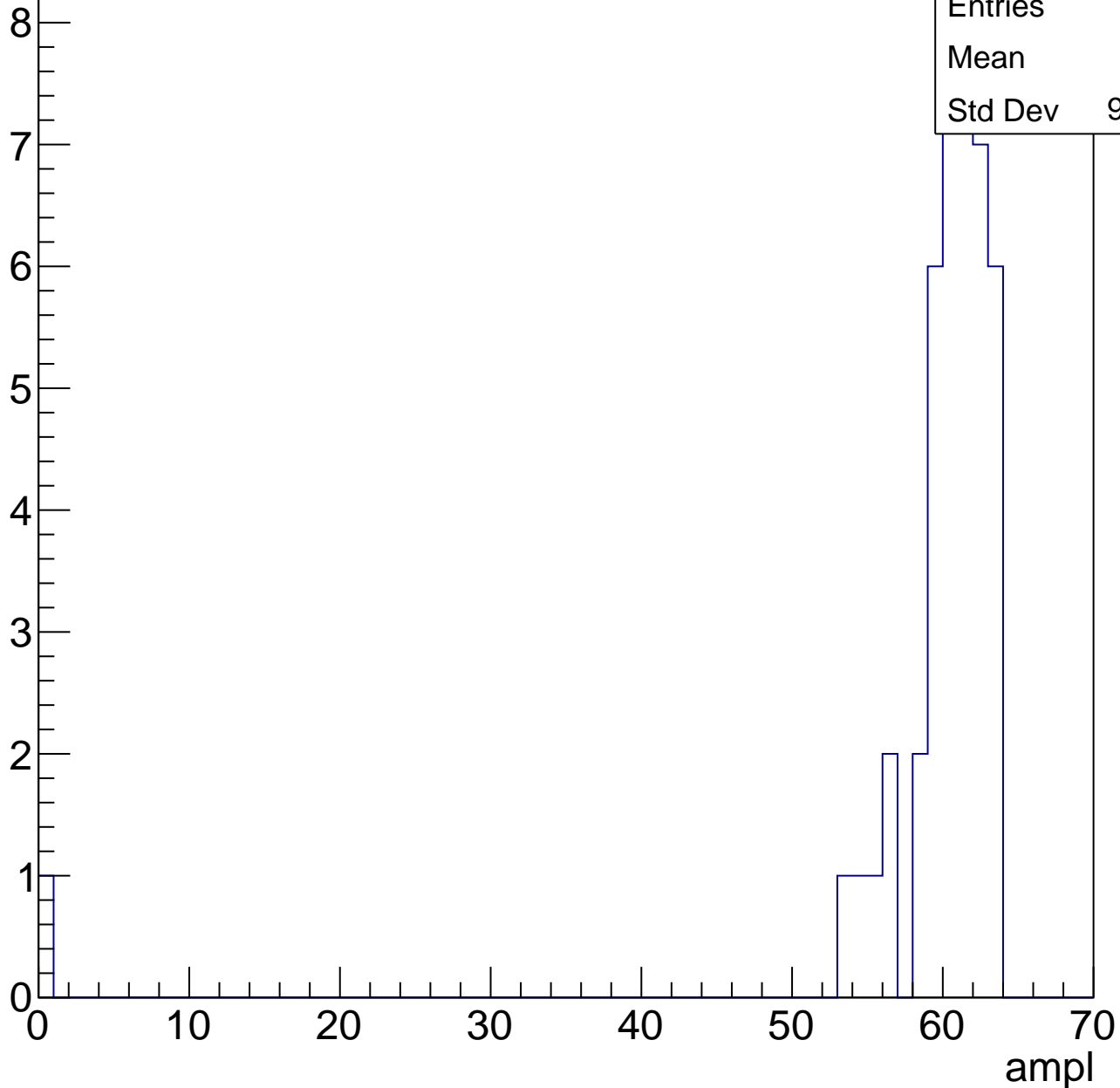
Entry



B1L102S, U13-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch11, adc0

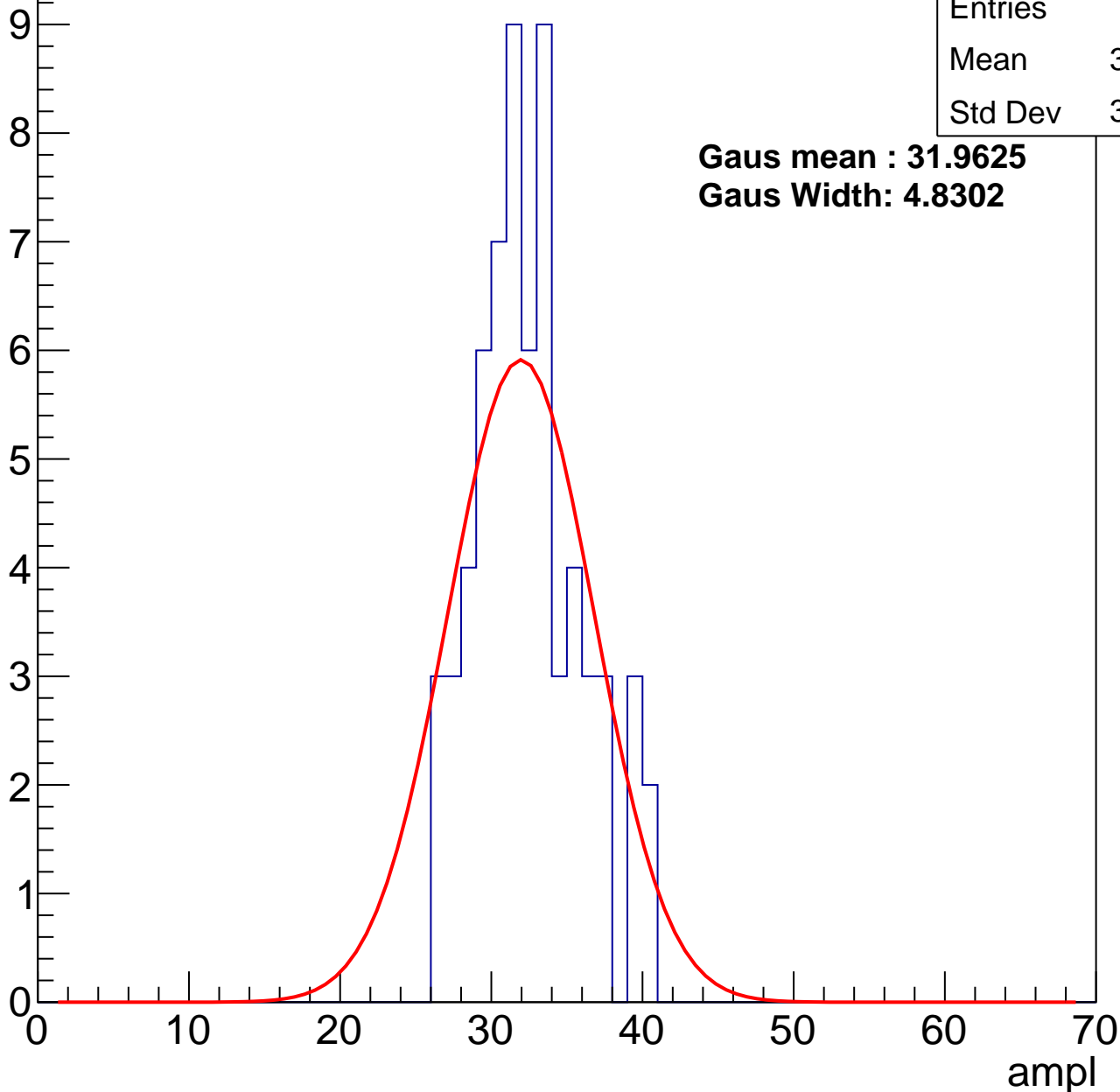
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	32.02
Std Dev	3.493

Gaus mean : 31.9625

Gaus Width: 4.8302



B1L102S, U13-ch11, adc1

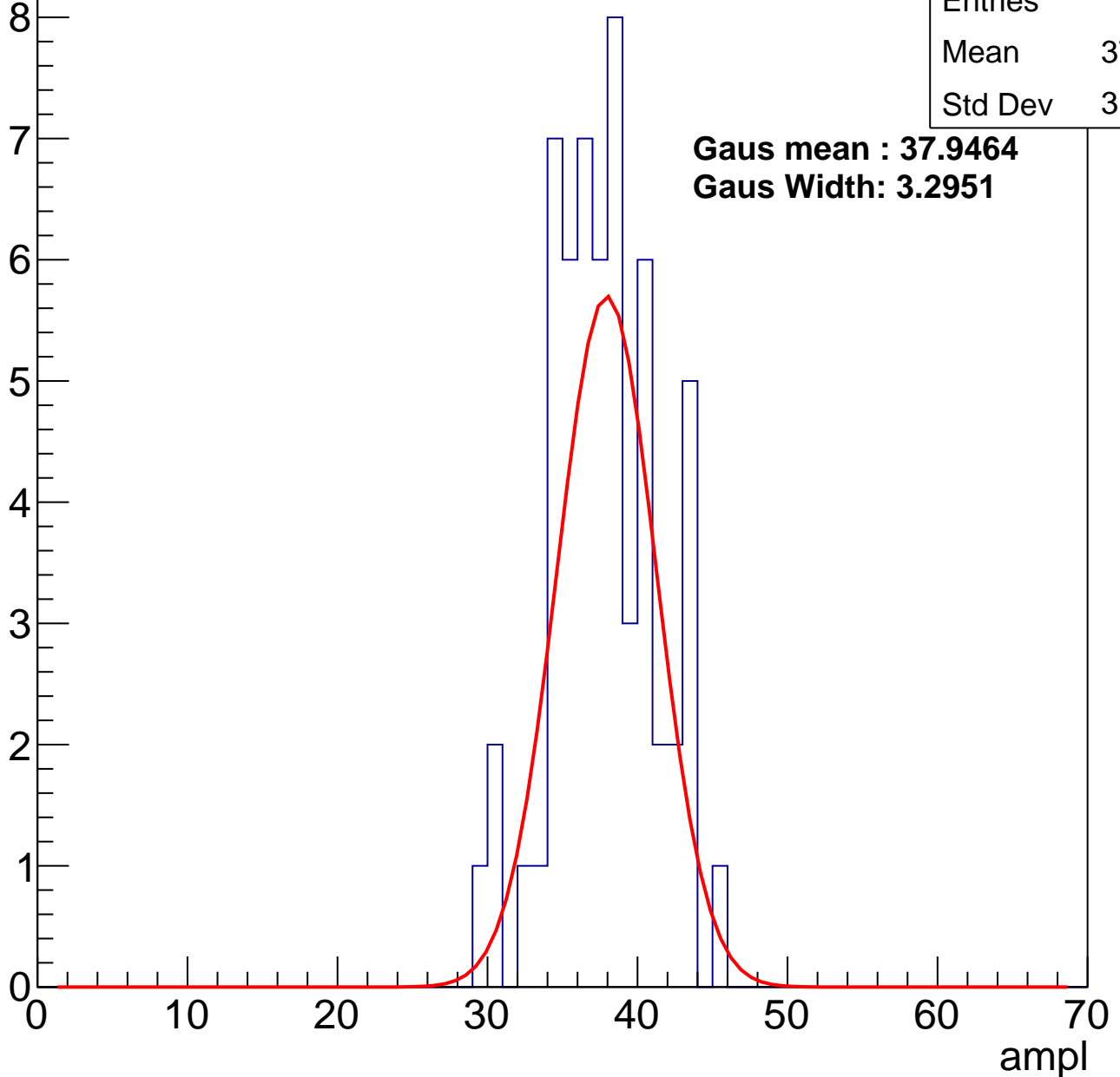
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	37.29
Std Dev	3.454

Gaus mean : 37.9464

Gaus Width: 3.2951



B1L102S, U13-ch11, adc2

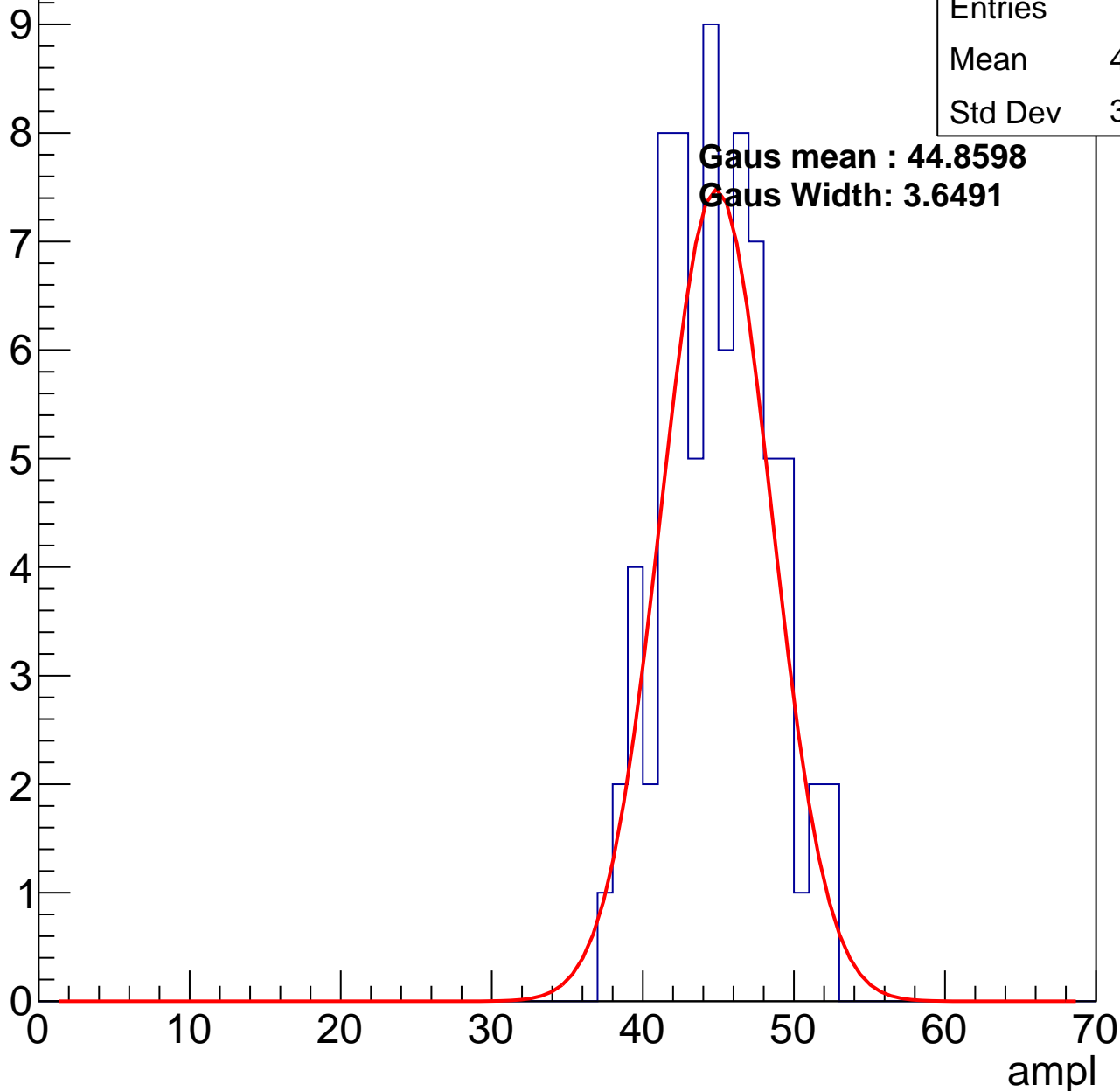
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	44.43
Std Dev	3.495

Gaus mean : 44.8598

Gaus Width: 3.6491

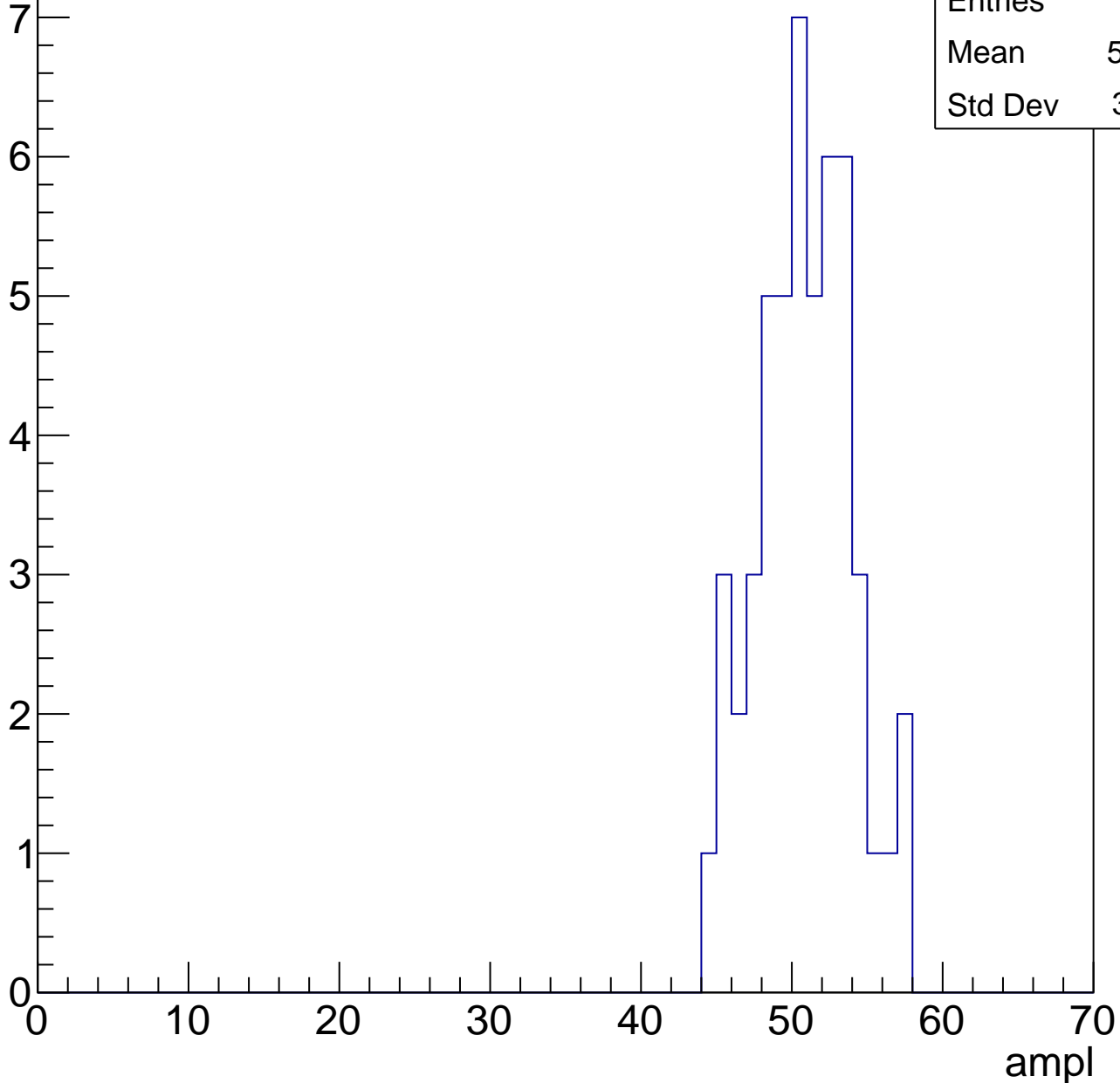


B1L102S, U13-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	50.38
Std Dev	3.091

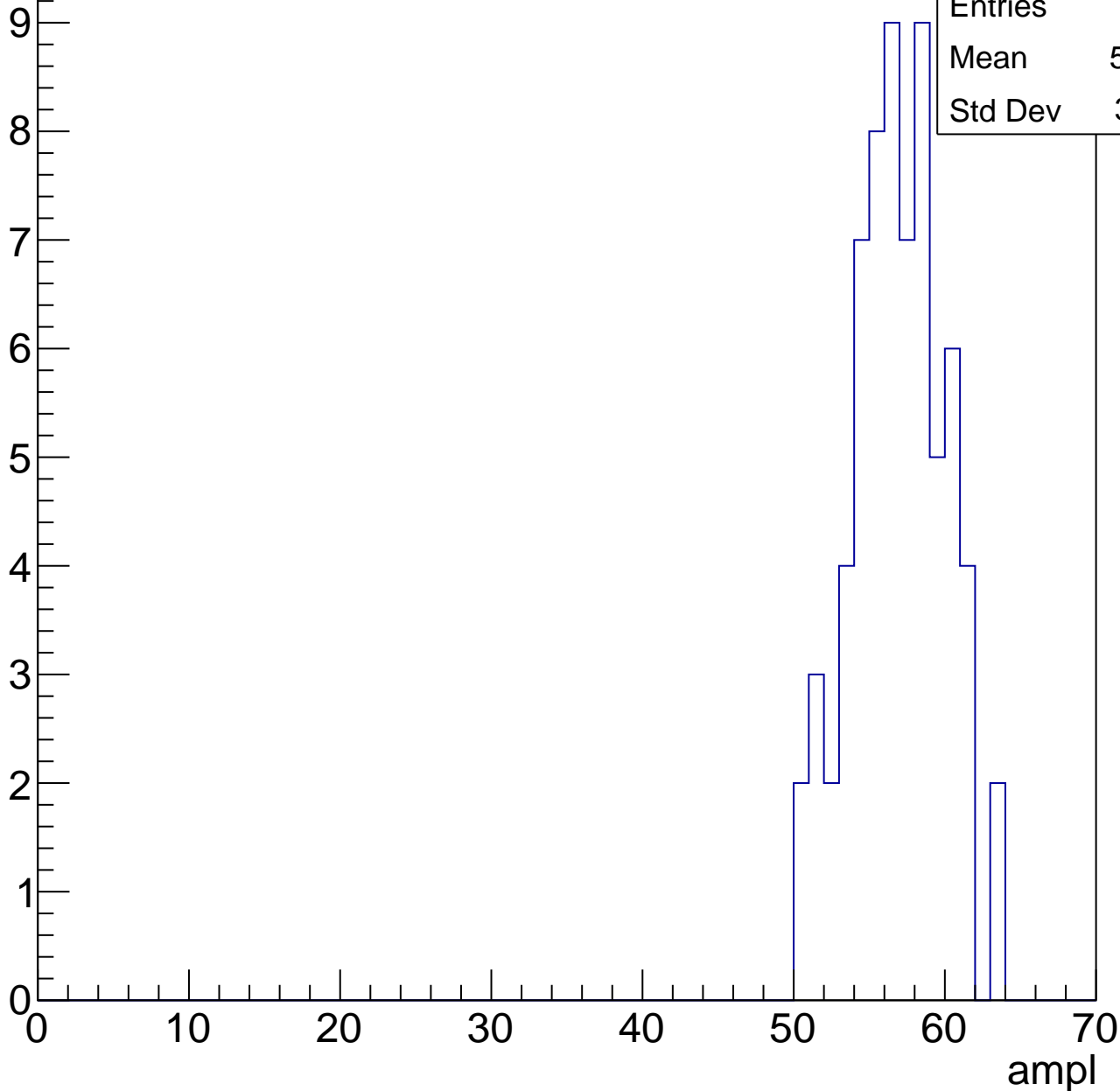


B1L102S, U13-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	56.43
Std Dev	3.021

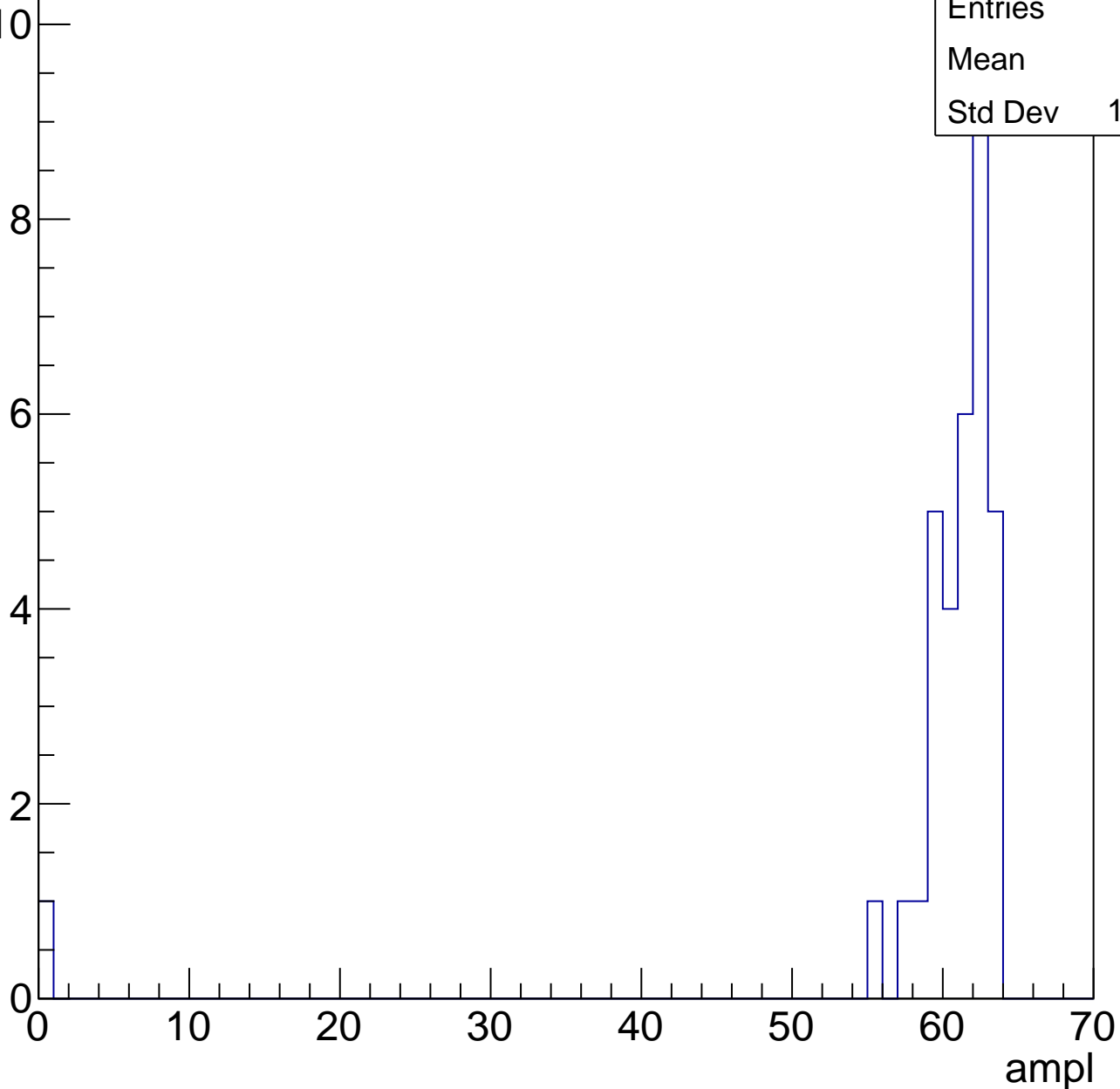


B1L102S, U13-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	59
Std Dev	10.43



B1L102S, U13-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

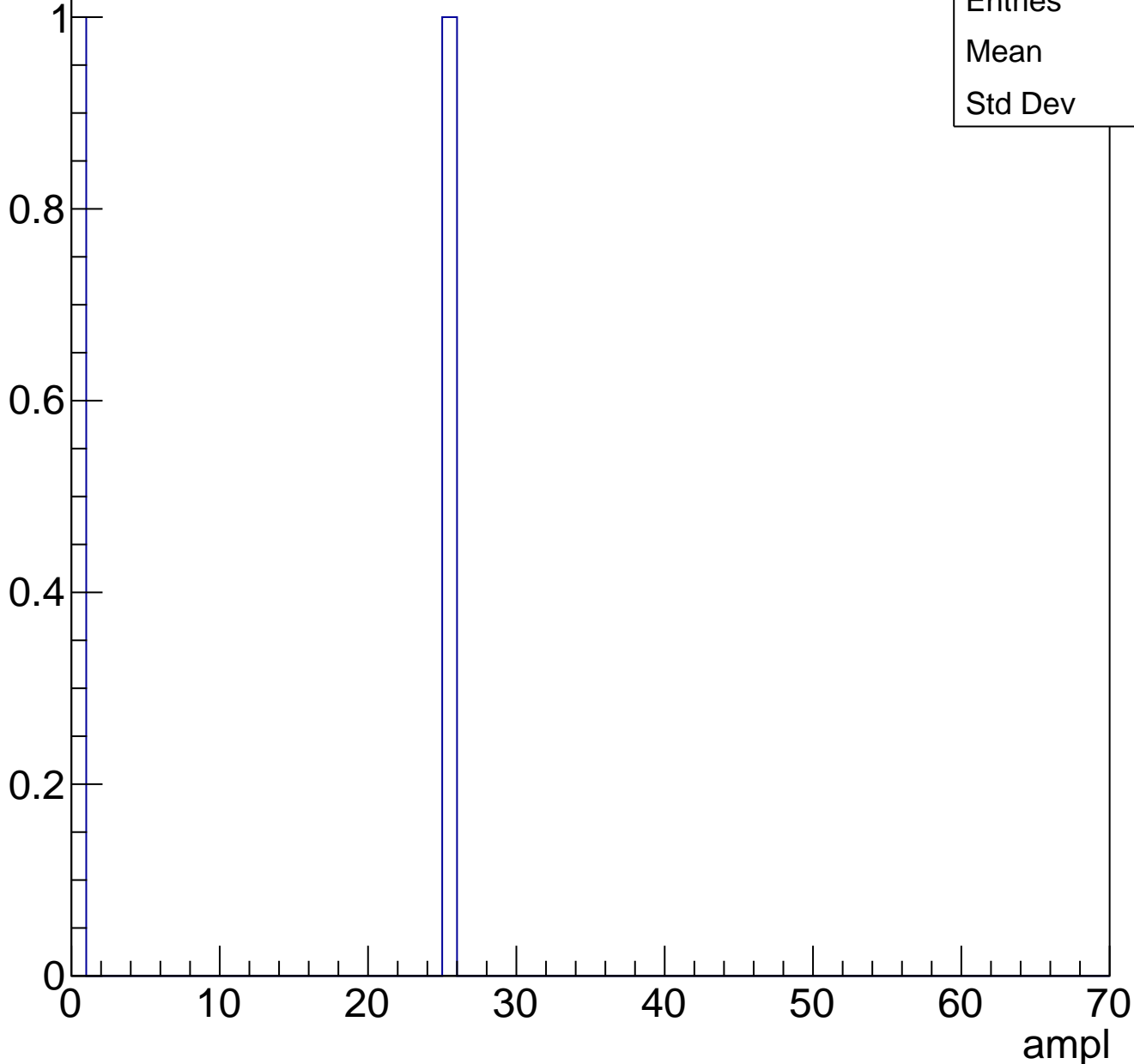
Entry



B1L102S, U13-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch12, adc0

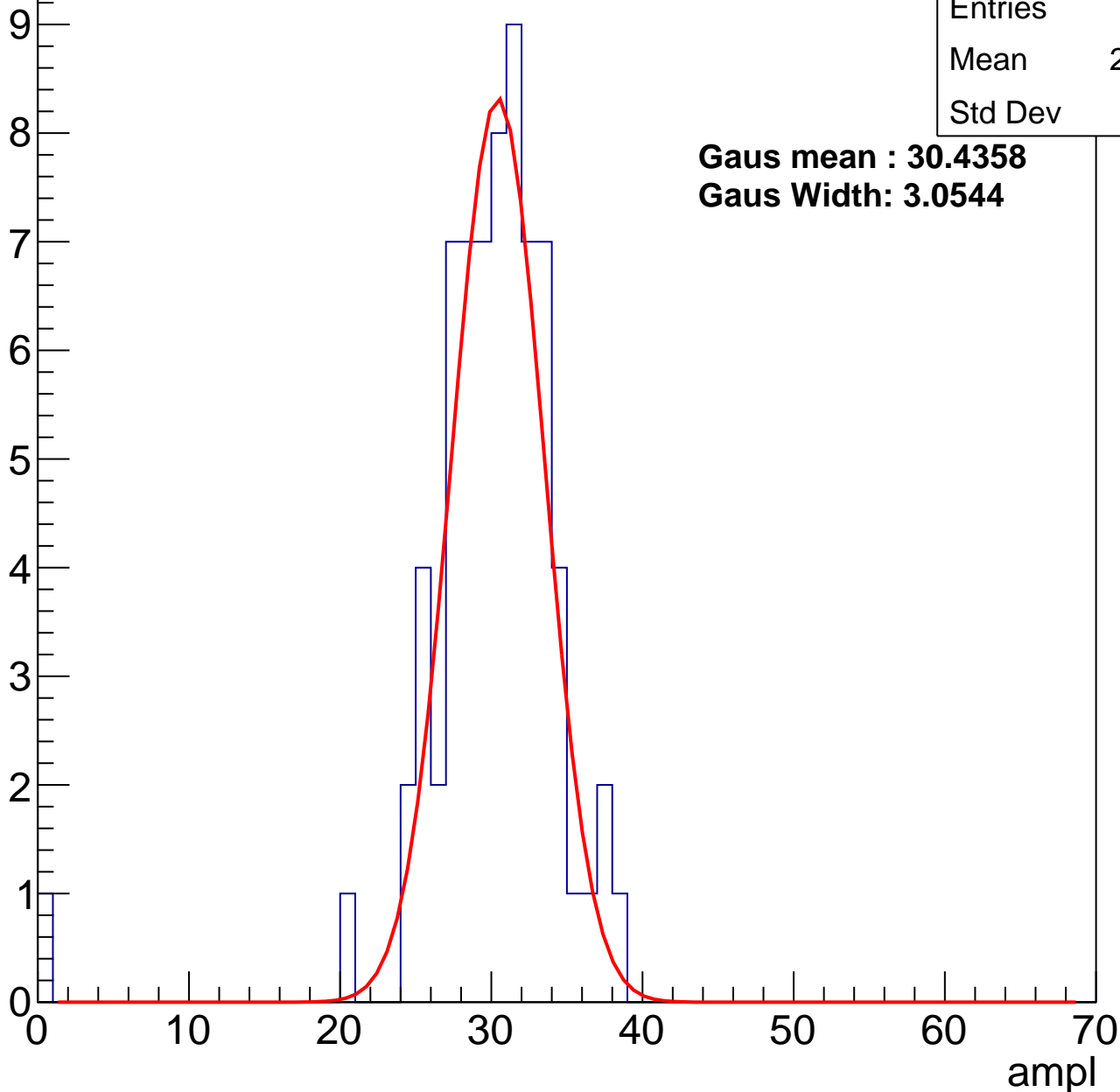
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.59
Std Dev	4.86

Gaus mean : 30.4358

Gaus Width: 3.0544



B1L102S, U13-ch12, adc1

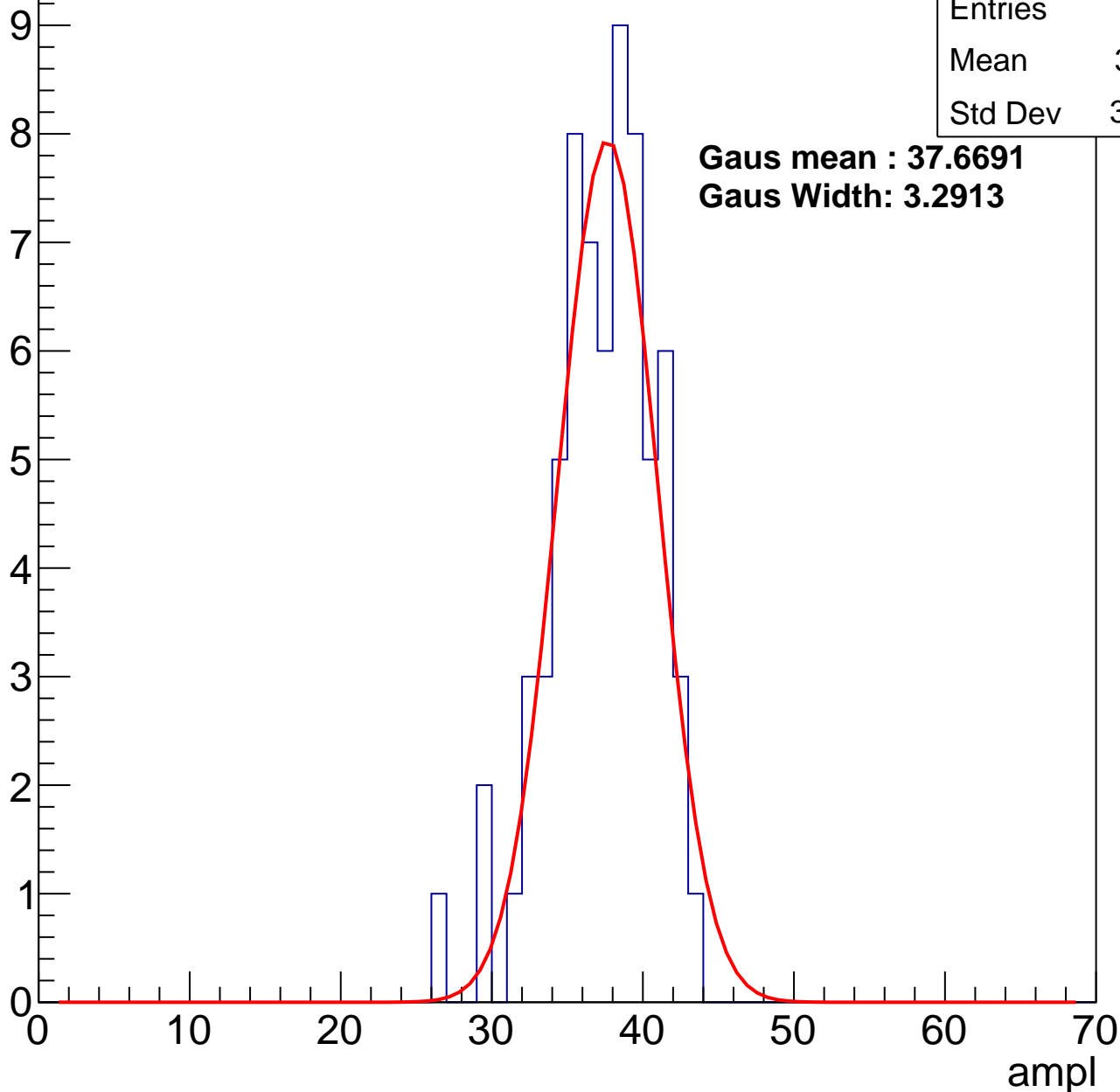
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.81
Std Dev	3.392

Gaus mean : 37.6691

Gaus Width: 3.2913



B1L102S, U13-ch12, adc2

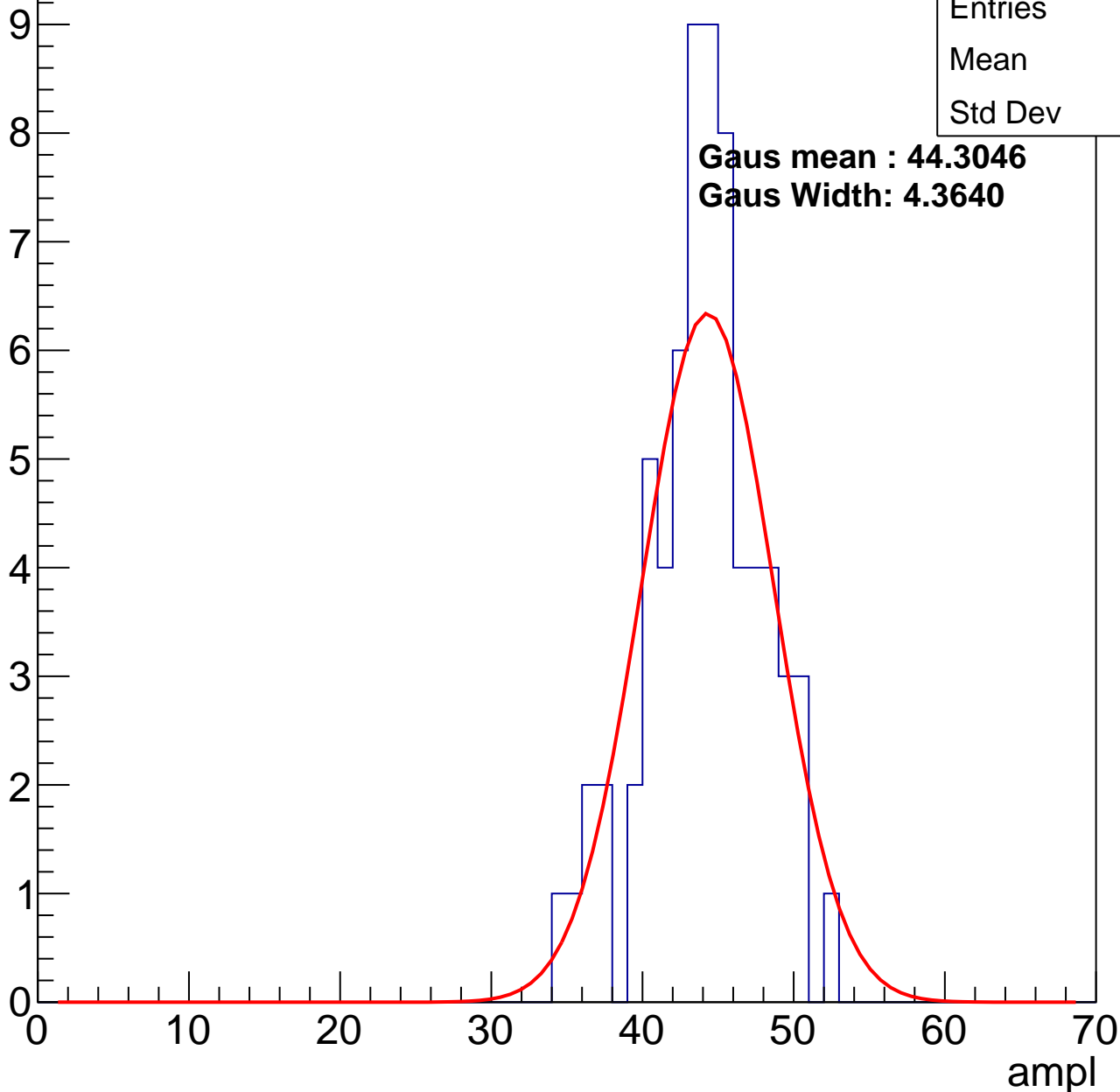
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.6
Std Dev	3.77

Gaus mean : 44.3046

Gaus Width: 4.3640

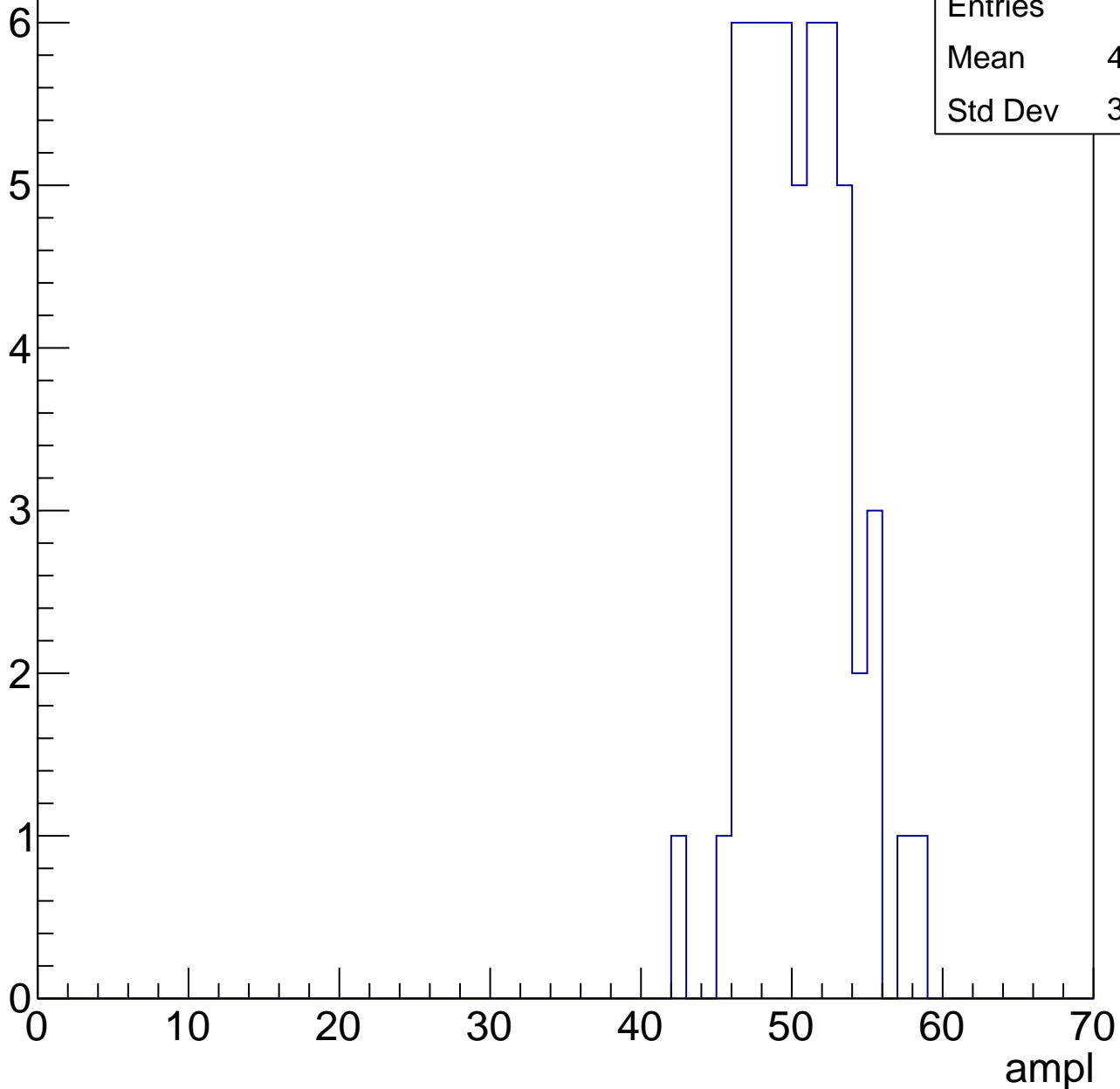


B1L102S, U13-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	49.96
Std Dev	3.202

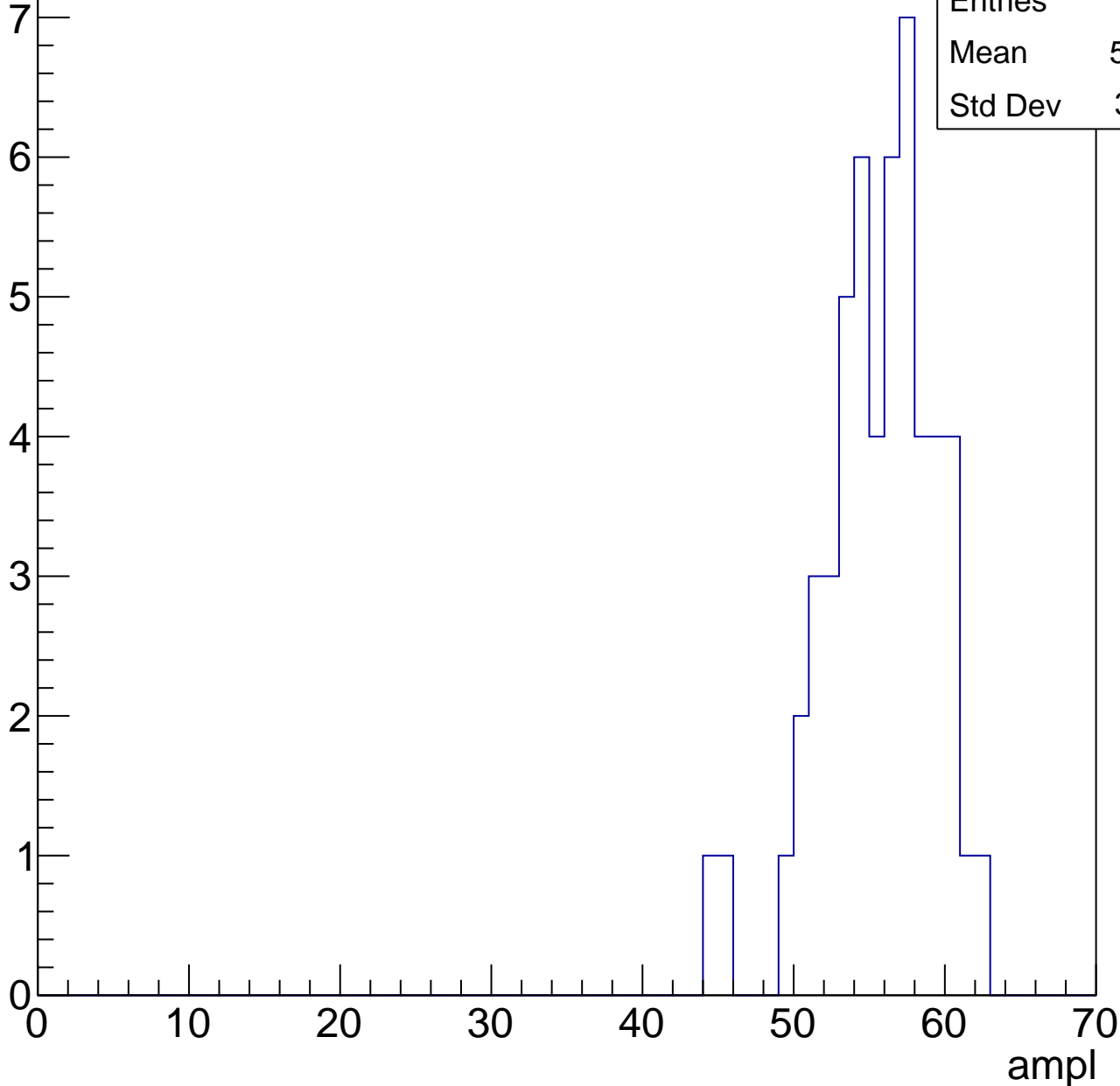


B1L102S, U13-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.13
Std Dev	3.701



B1L102S, U13-ch12, adc5

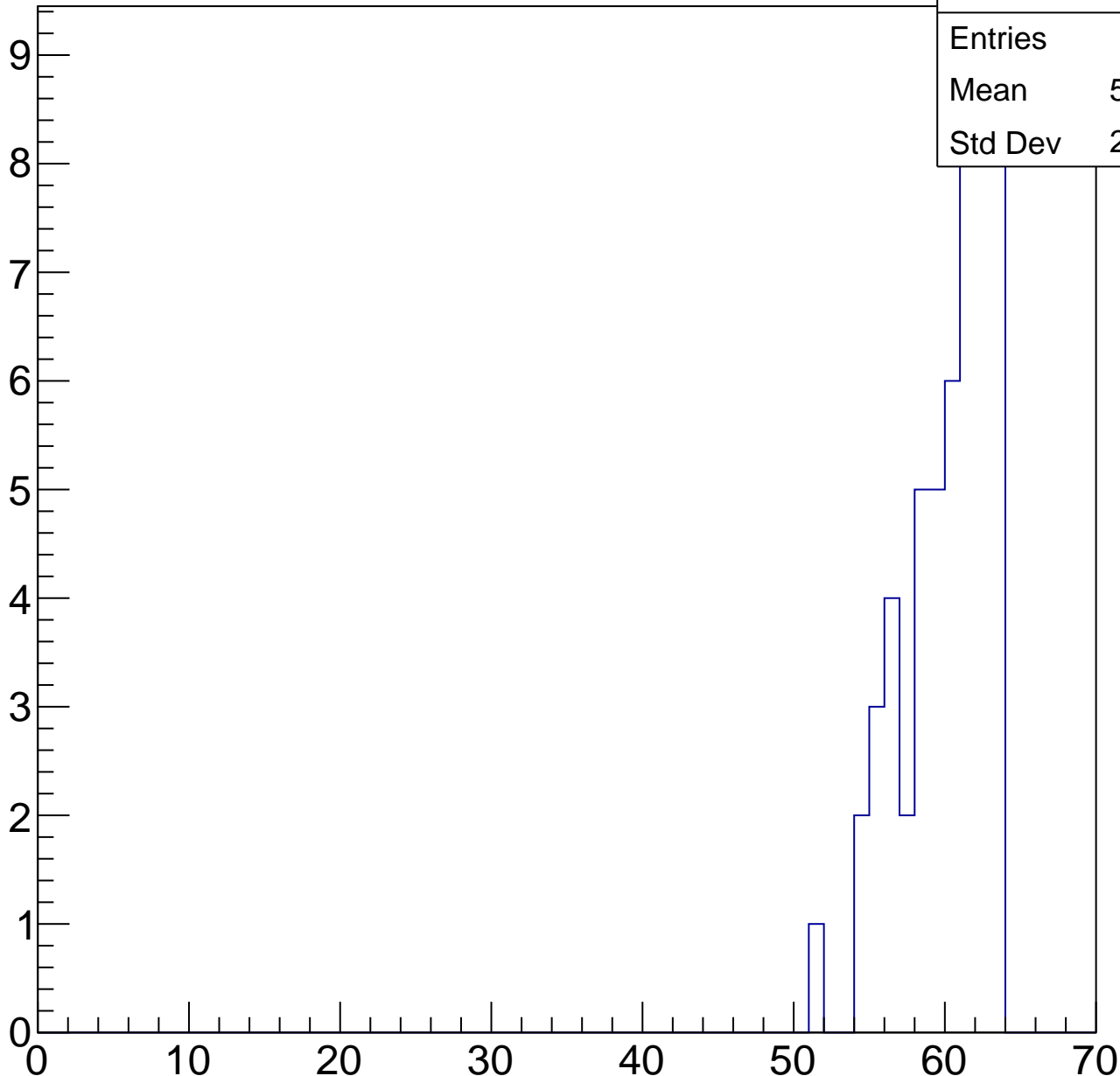
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.55
Std Dev	2.872

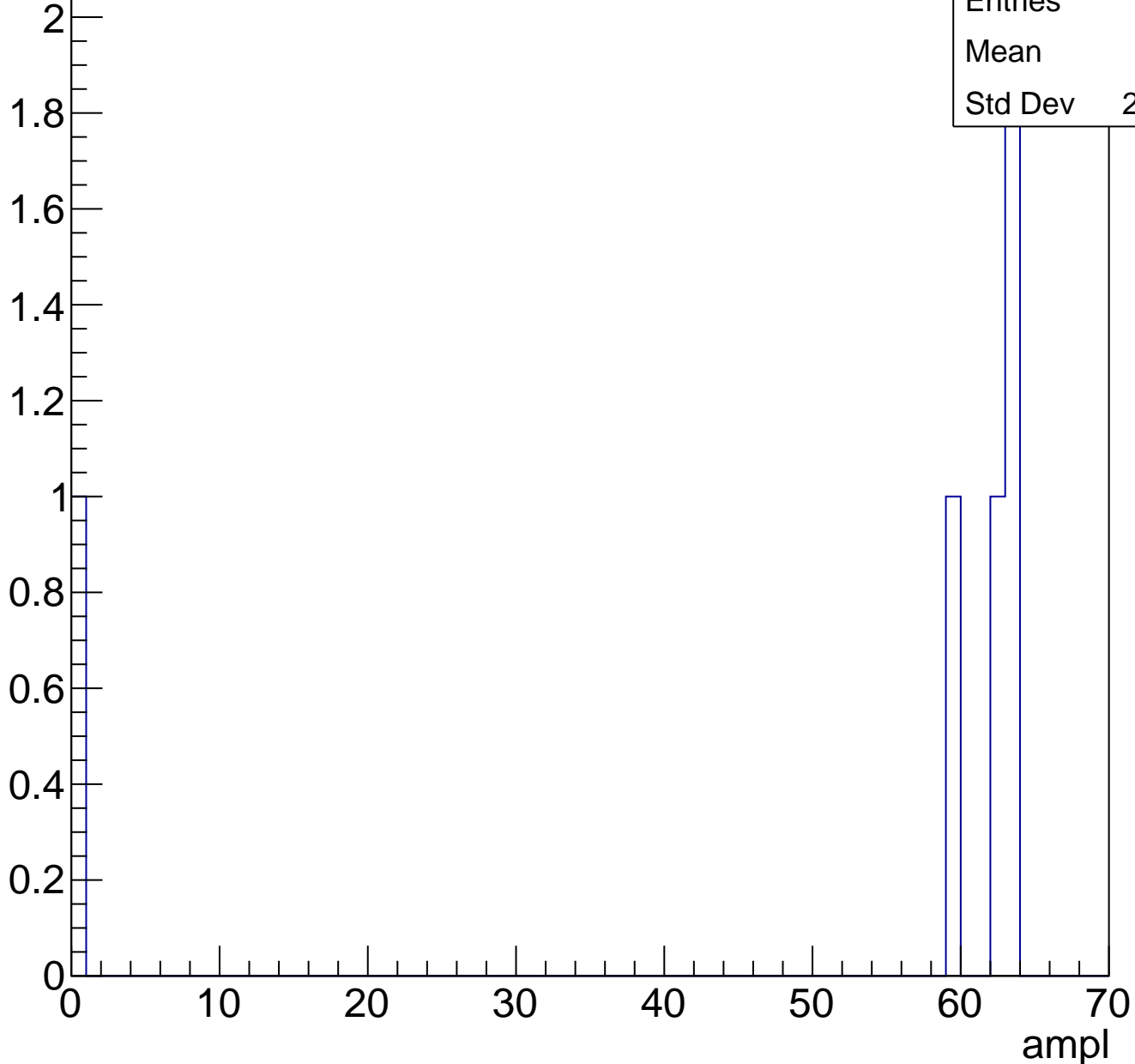
ampl



B1L102S, U13-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch13, adc0

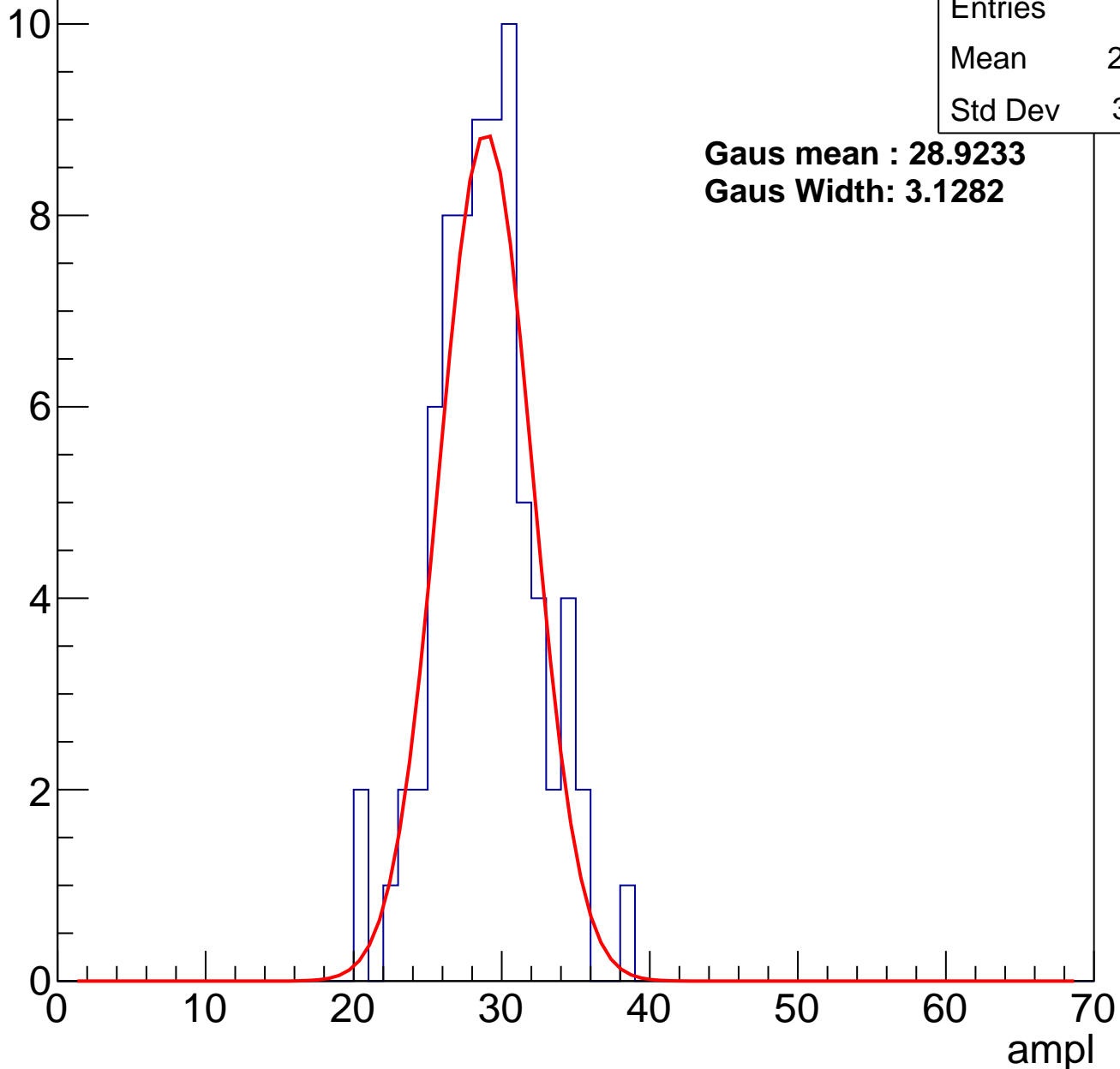
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	28.48
Std Dev	3.411

Gaus mean : 28.9233

Gaus Width: 3.1282

Entry



B1L102S, U13-ch13, adc1

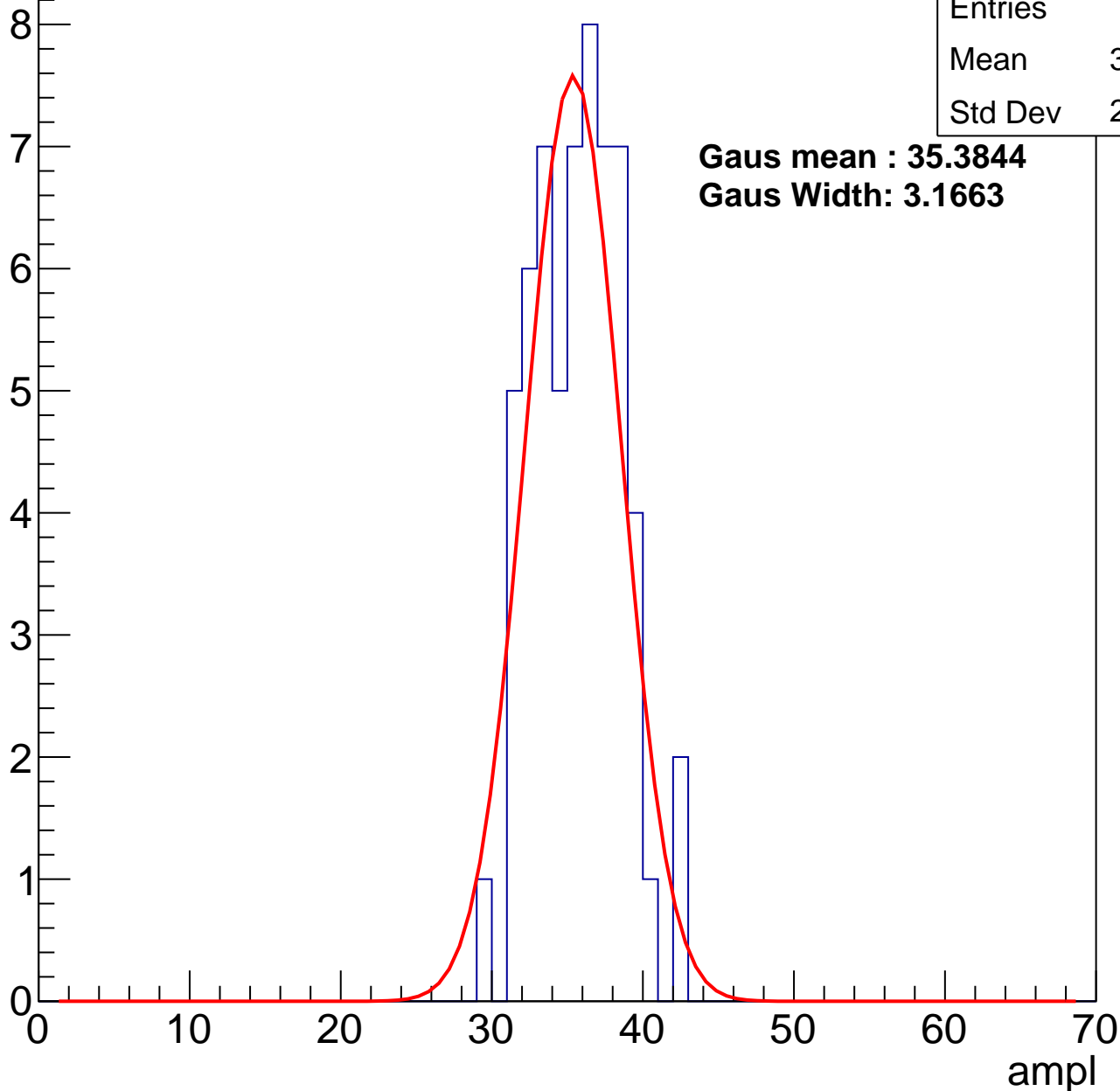
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.25
Std Dev	2.844

Gaus mean : 35.3844

Gaus Width: 3.1663



B1L102S, U13-ch13, adc2

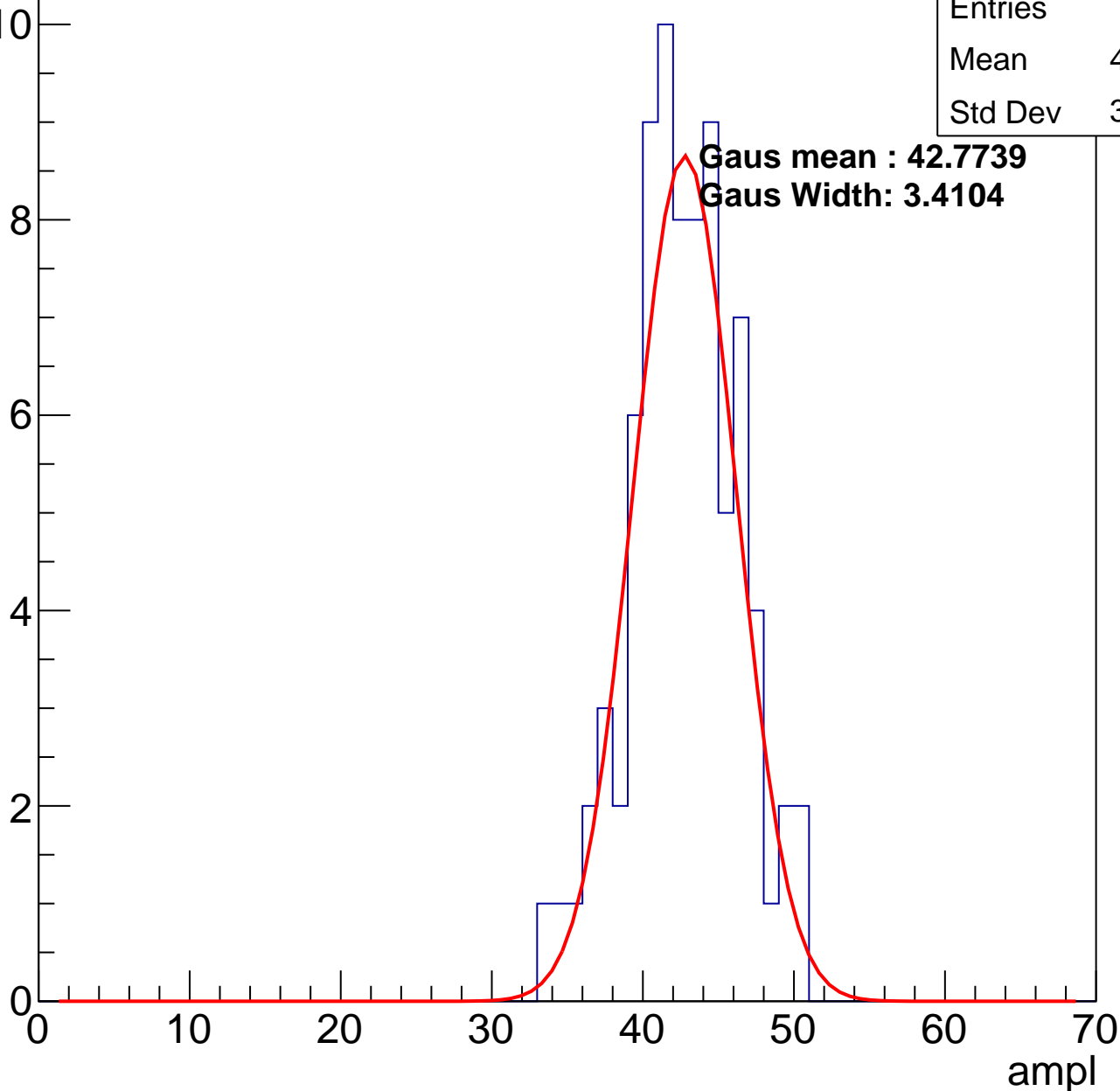
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	42.25
Std Dev	3.568

Gaus mean : 42.7739

Gaus Width: 3.4104

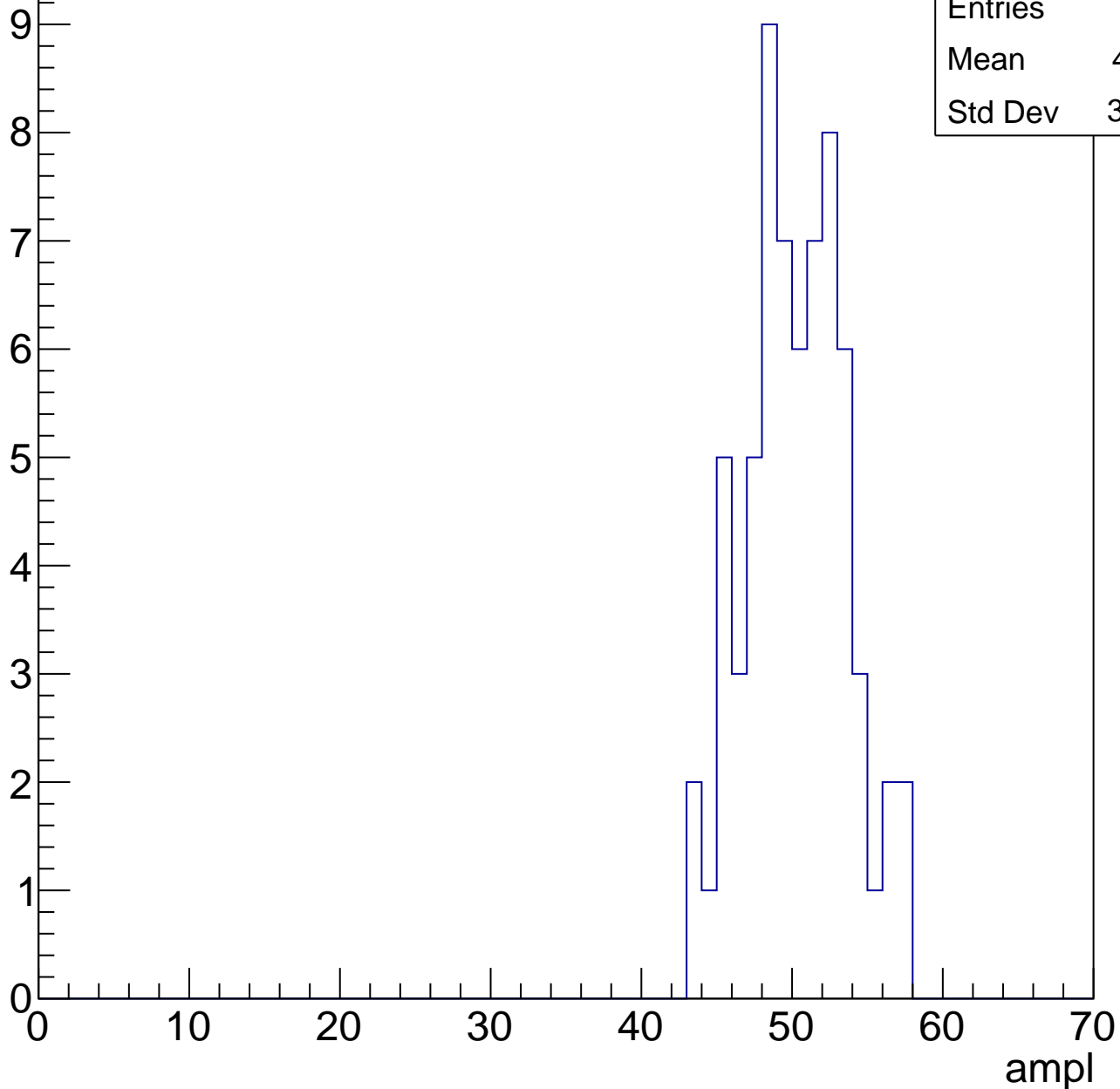


B1L102S, U13-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.81
Std Dev	3.297

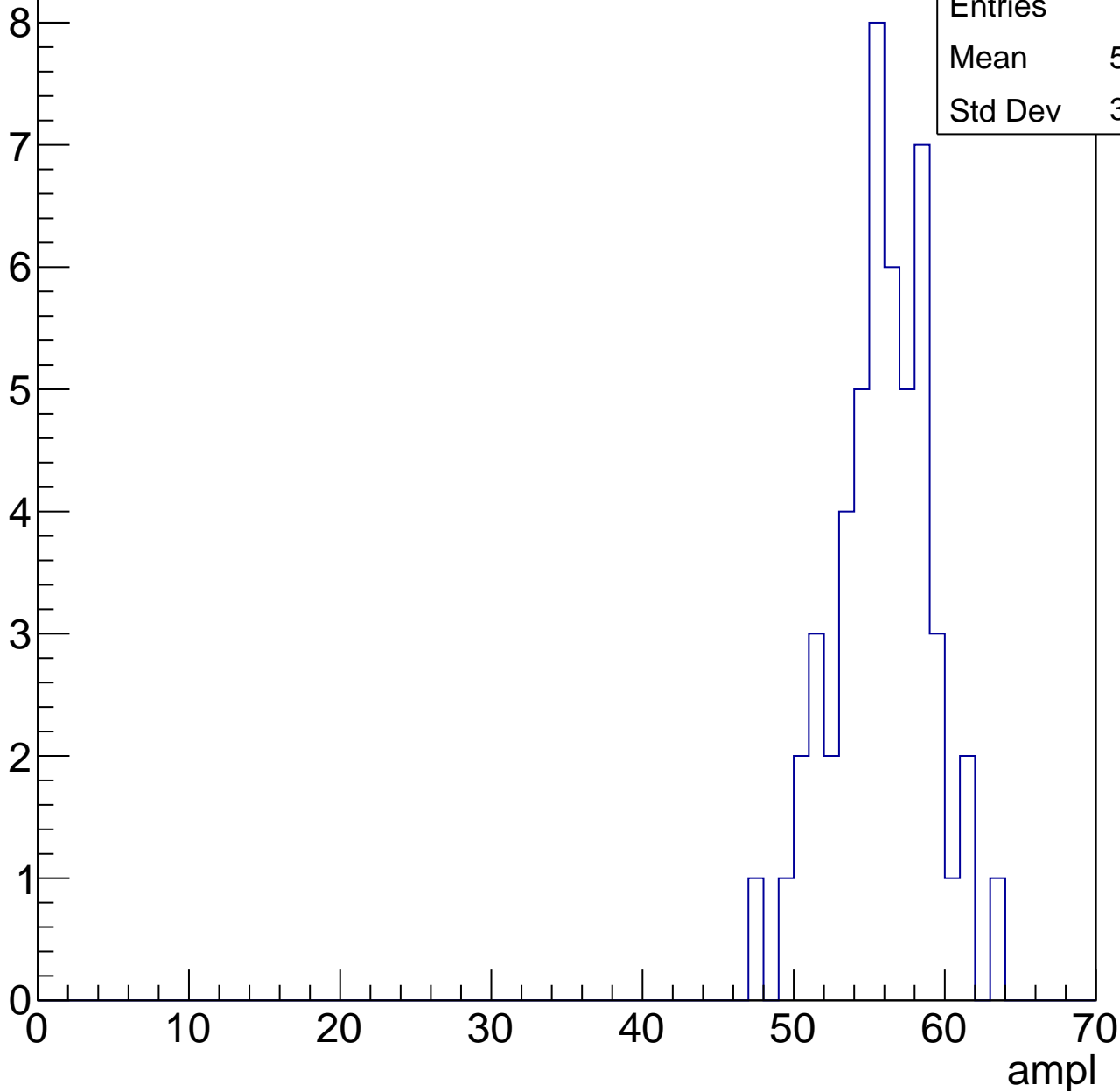


B1L102S, U13-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

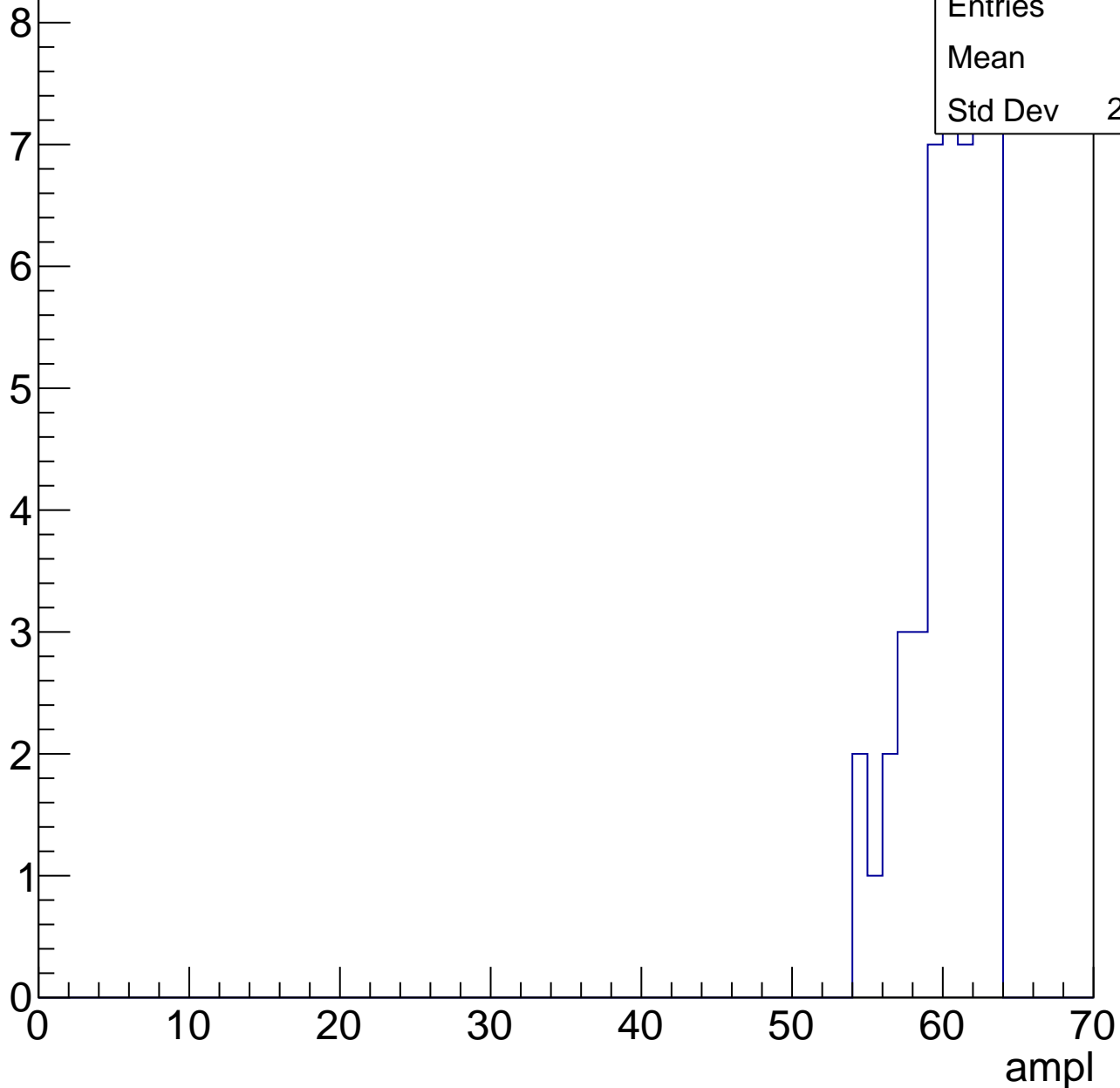
Entries	51
Mean	55.37
Std Dev	3.217



B1L102S, U13-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

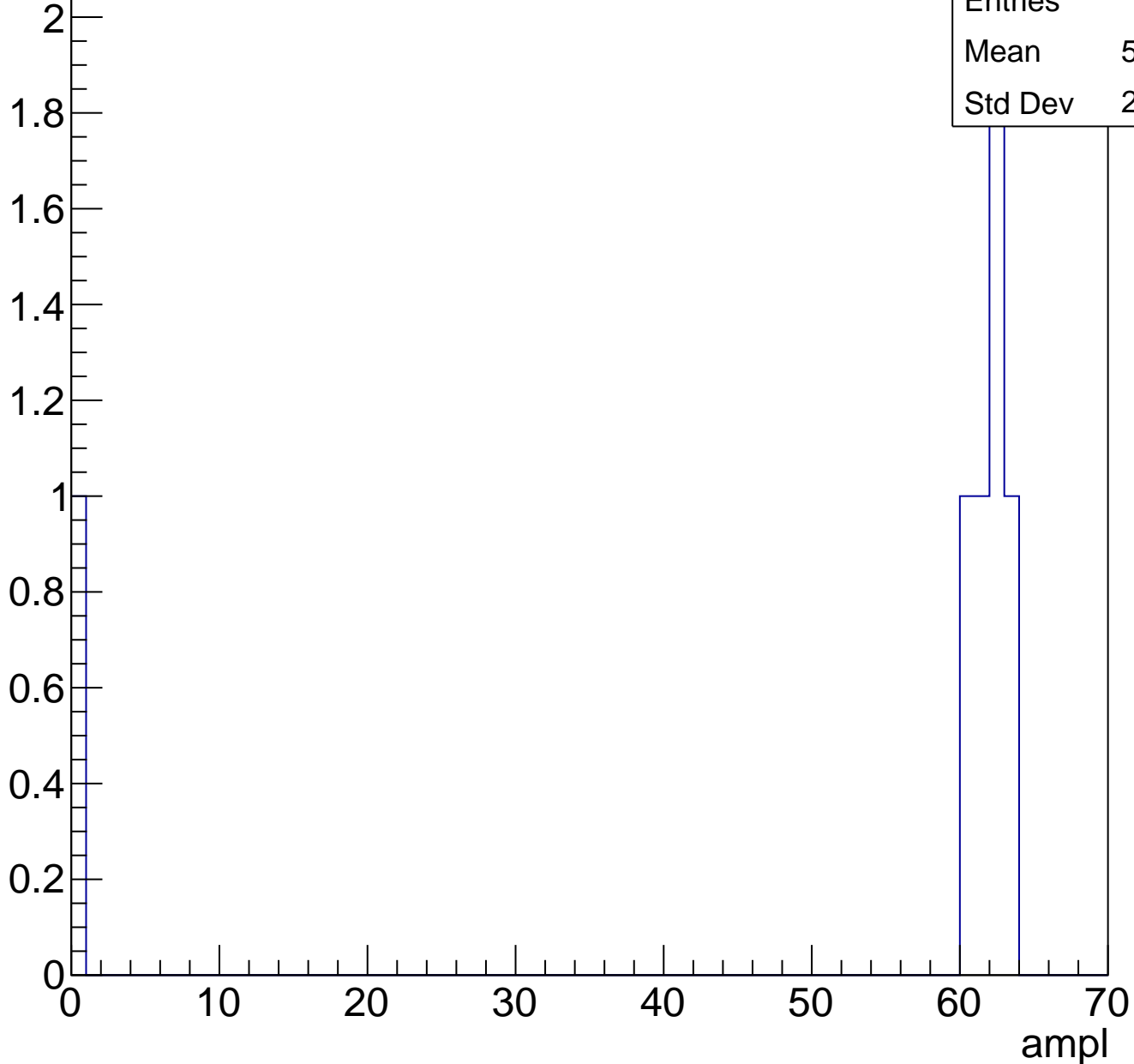
Entry



B1L102S, U13-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch14, adc0

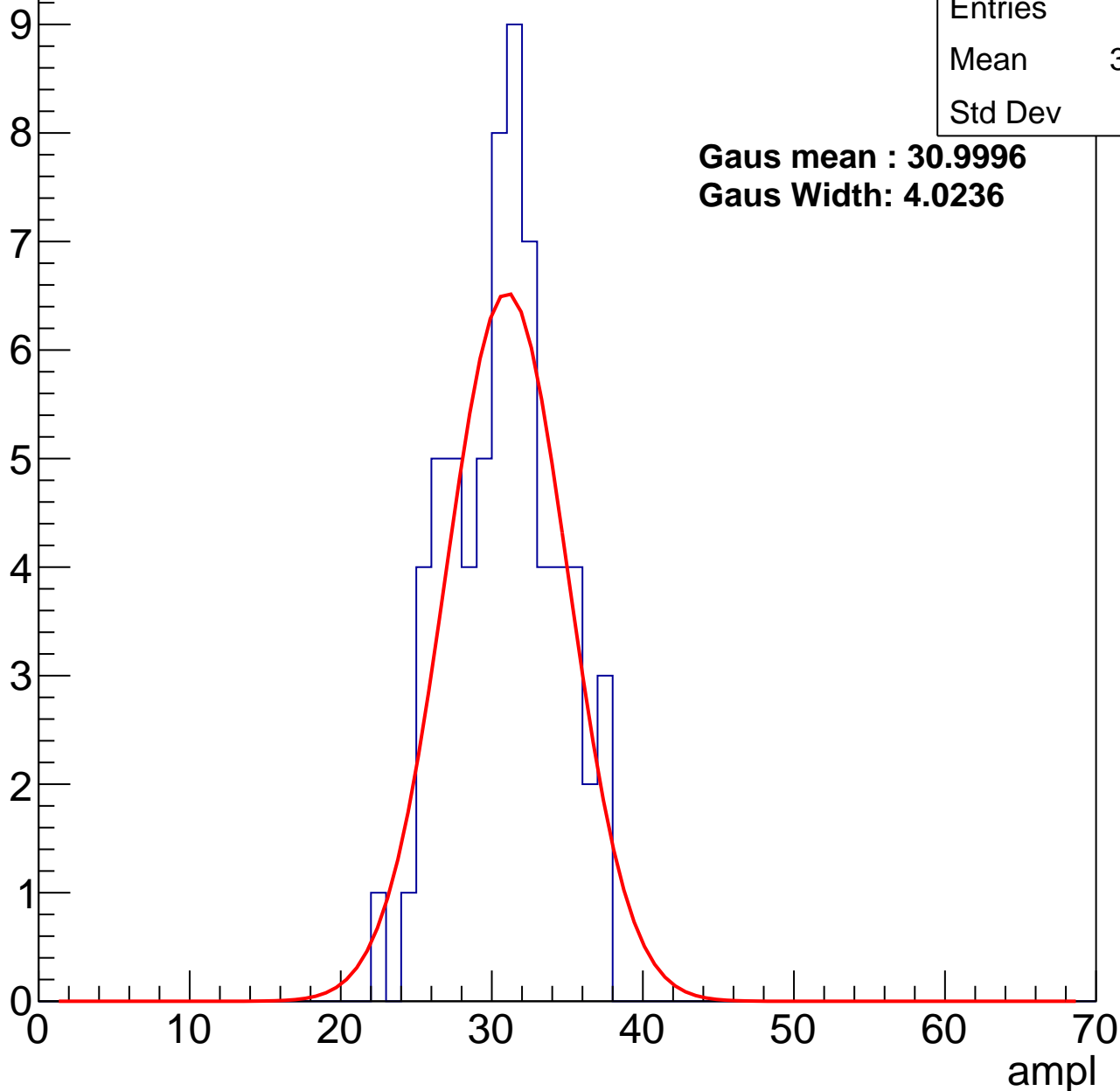
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	30.33
Std Dev	3.47

Gaus mean : 30.9996

Gaus Width: 4.0236



B1L102S, U13-ch14, adc1

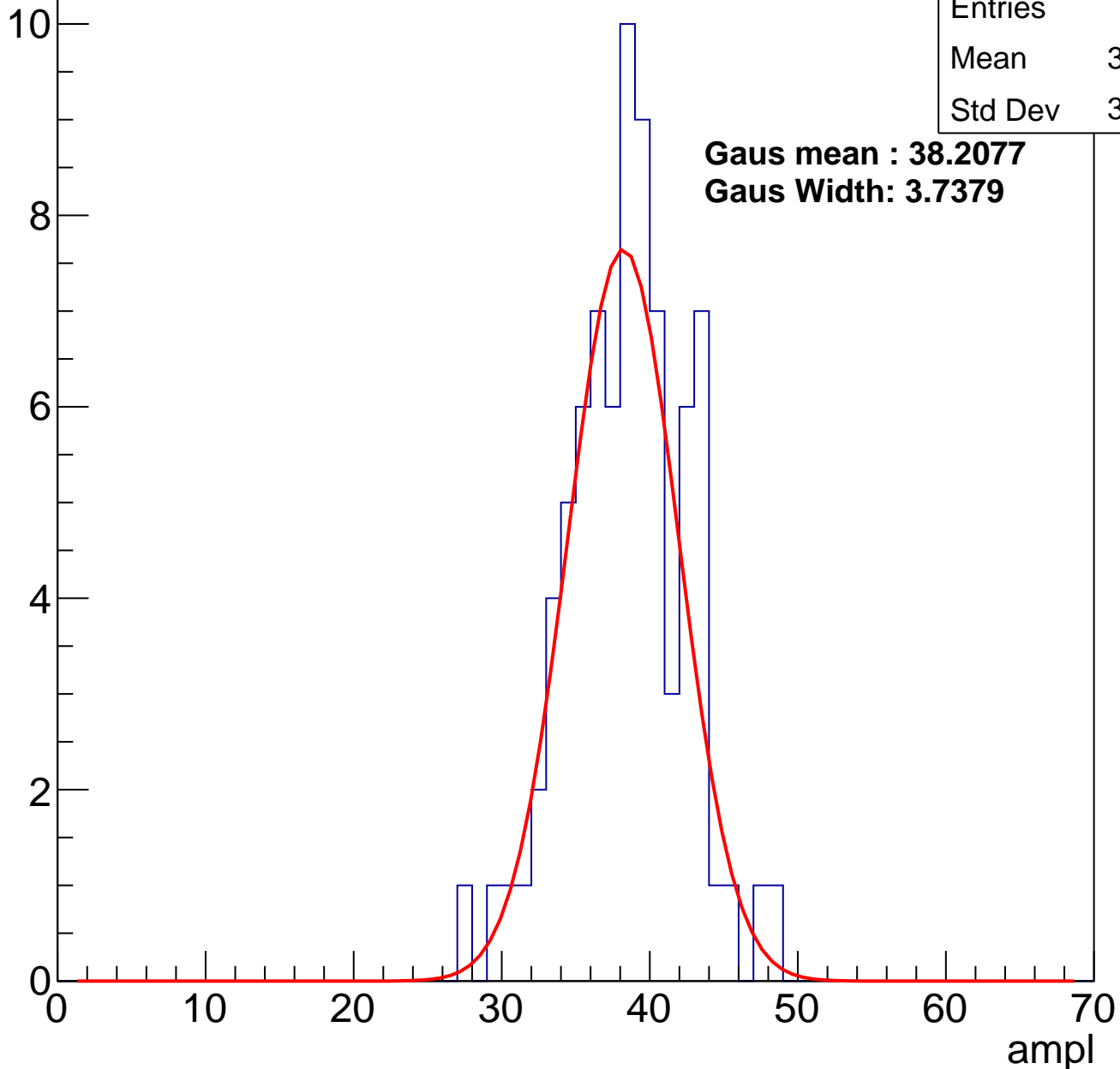
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	37.98
Std Dev	3.962

Gaus mean : 38.2077

Gaus Width: 3.7379

Entry



B1L102S, U13-ch14, adc2

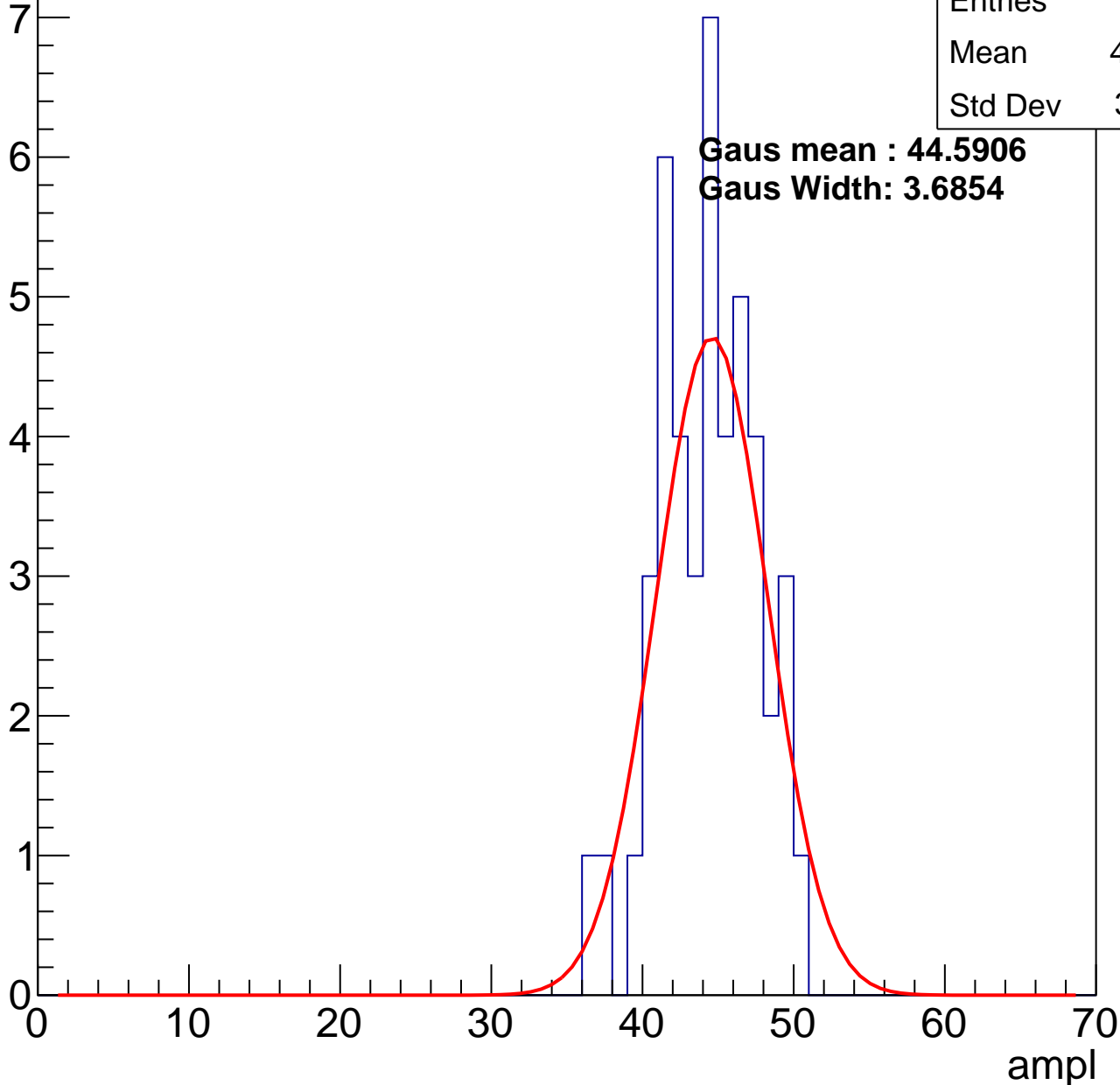
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	43.87
Std Dev	3.201

Gaus mean : 44.5906

Gaus Width: 3.6854

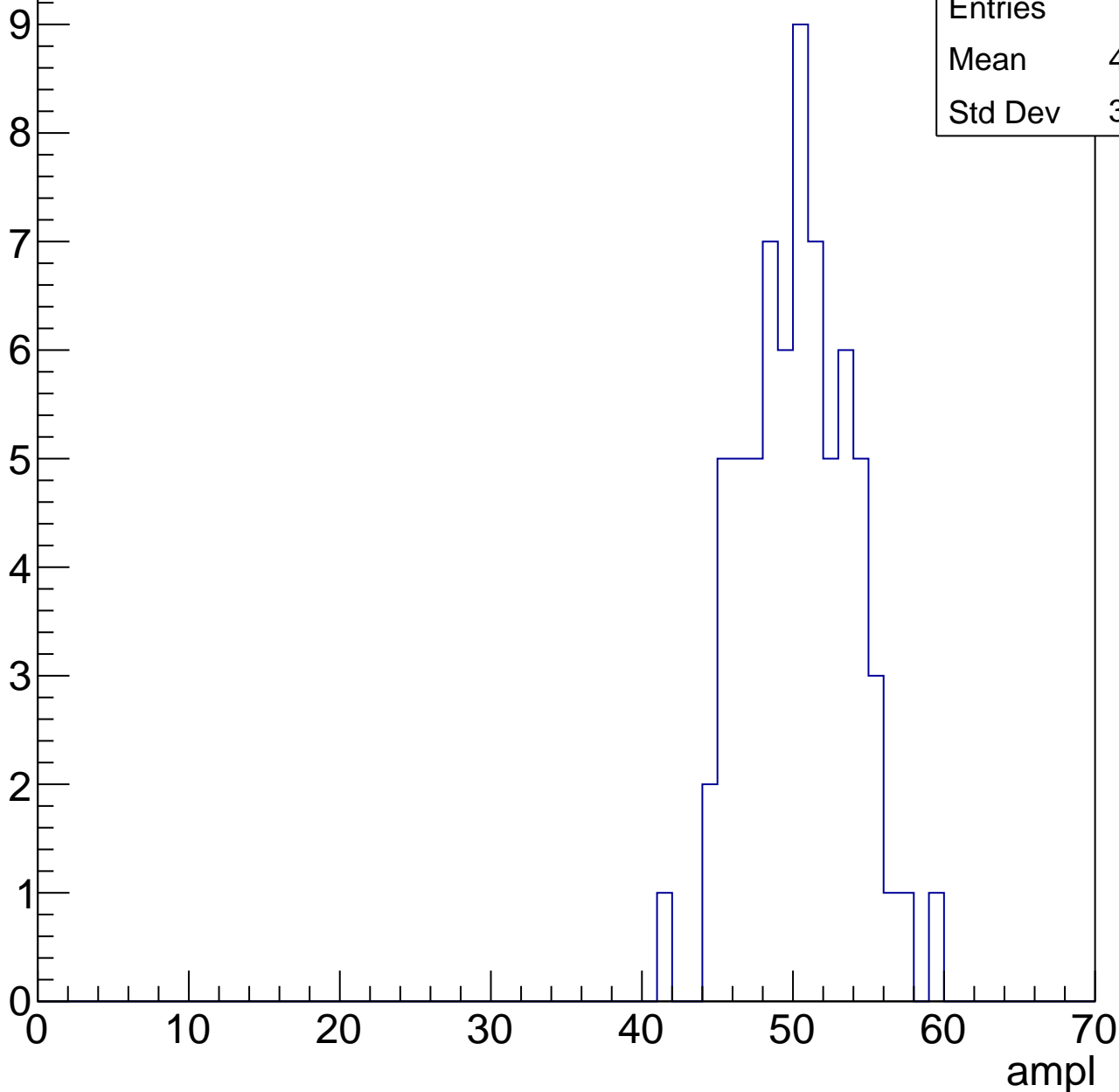


B1L102S, U13-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.87
Std Dev	3.485

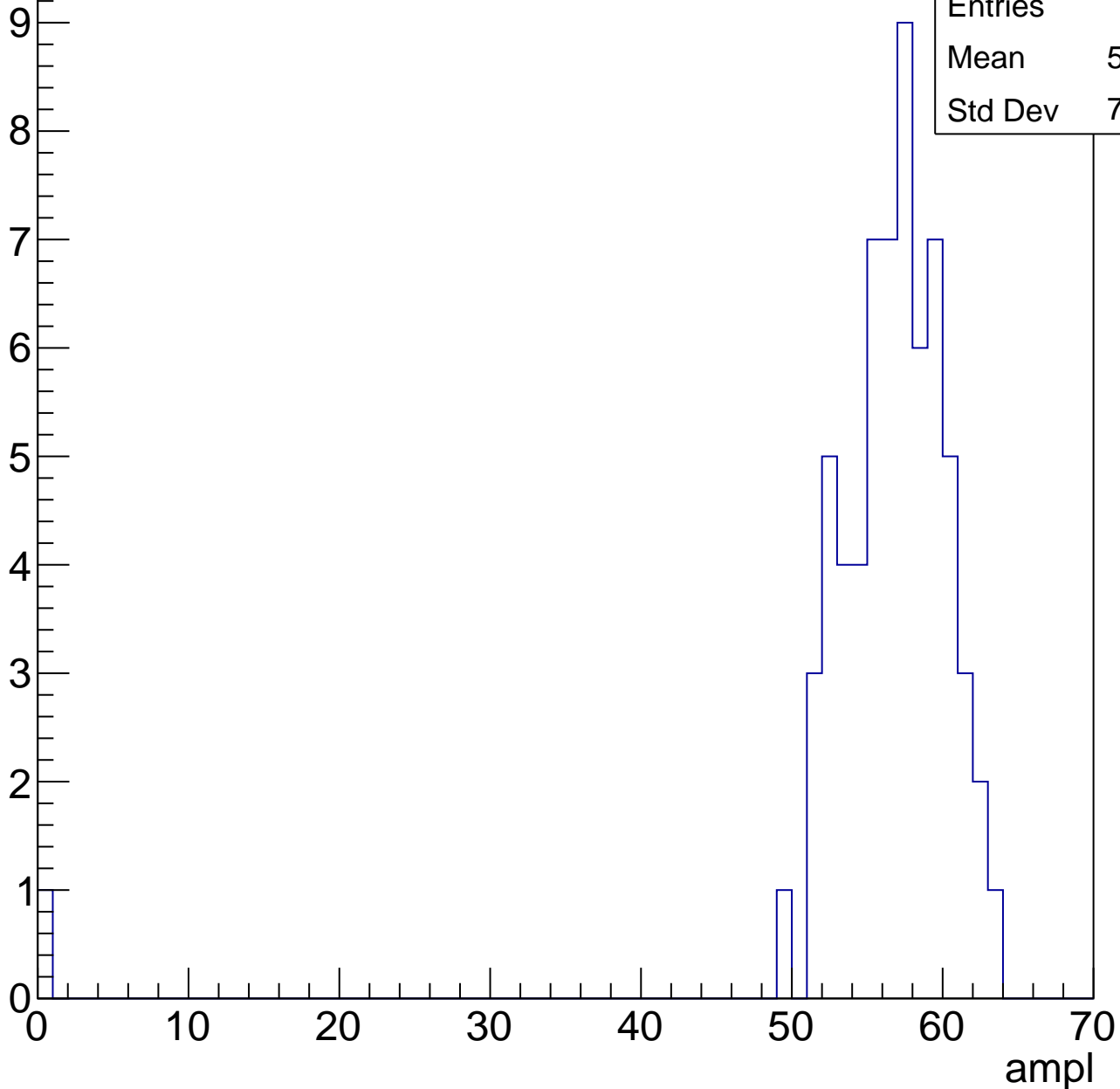


B1L102S, U13-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.55
Std Dev	7.604

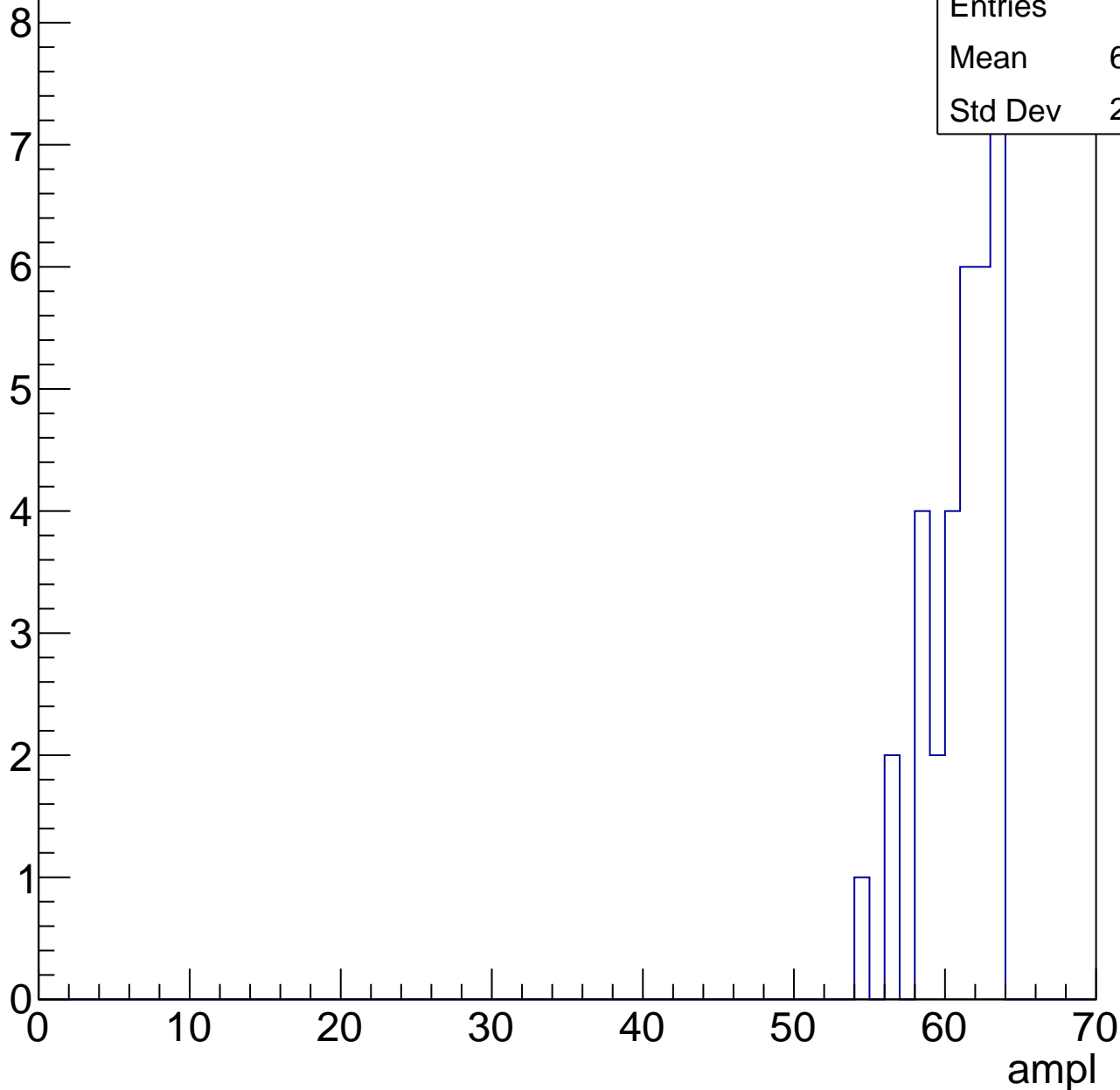


B1L102S, U13-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	60.55
Std Dev	2.324



B1L102S, U13-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch15, adc0

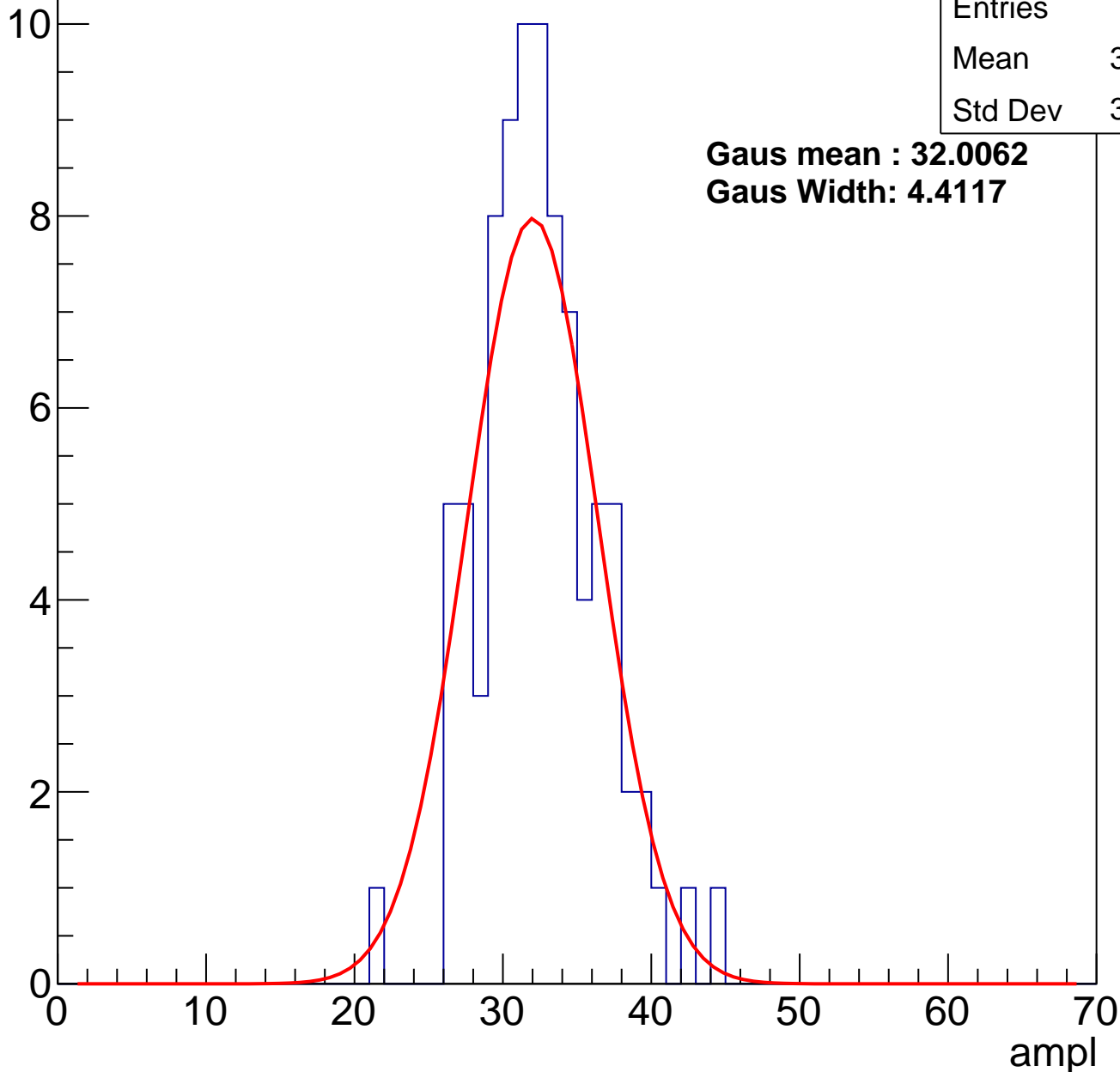
calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	32.06
Std Dev	3.934

Gaus mean : 32.0062

Gaus Width: 4.4117

Entry



B1L102S, U13-ch15, adc1

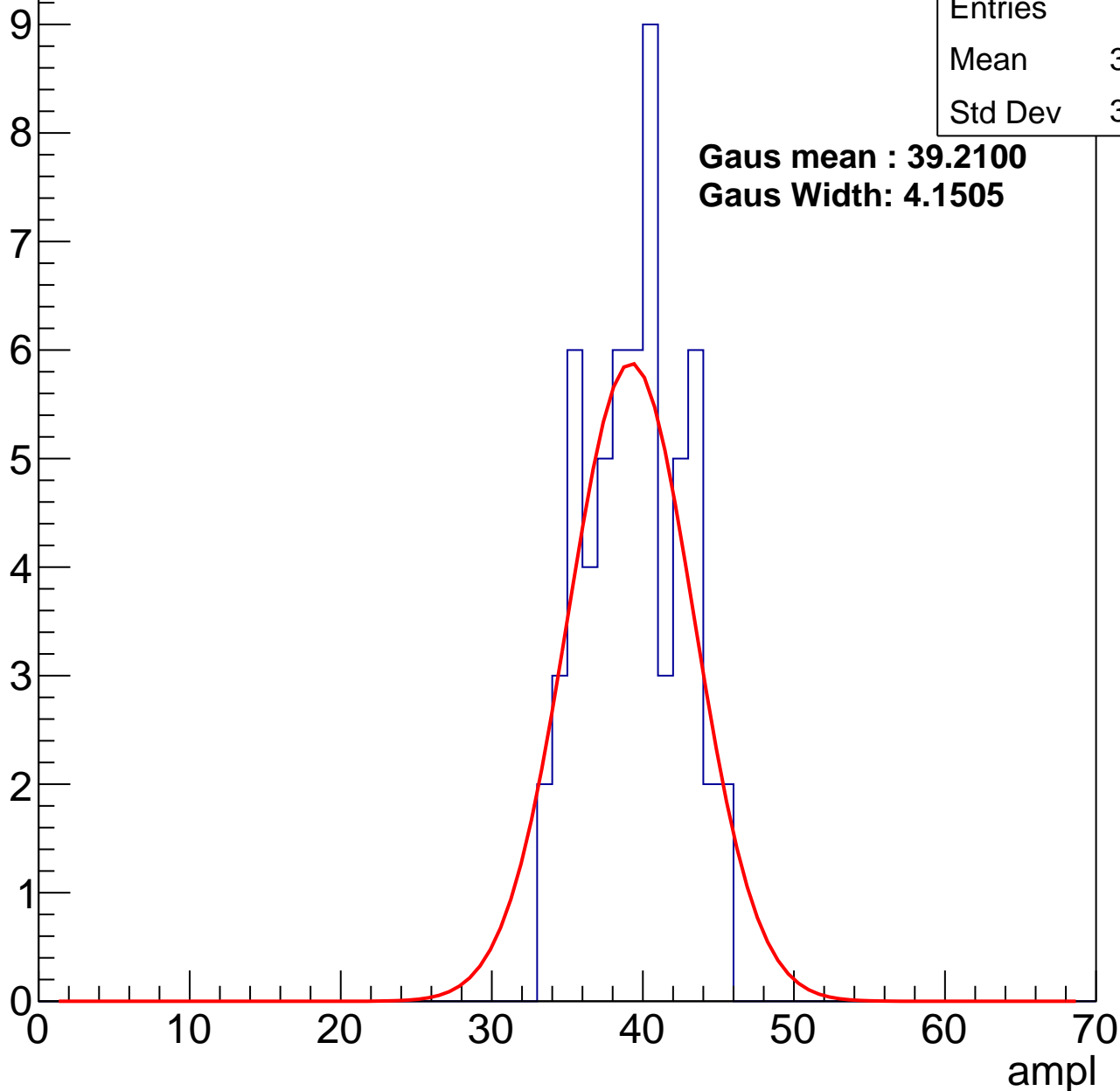
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	38.95
Std Dev	3.159

Gaus mean : 39.2100

Gaus Width: 4.1505



B1L102S, U13-ch15, adc2

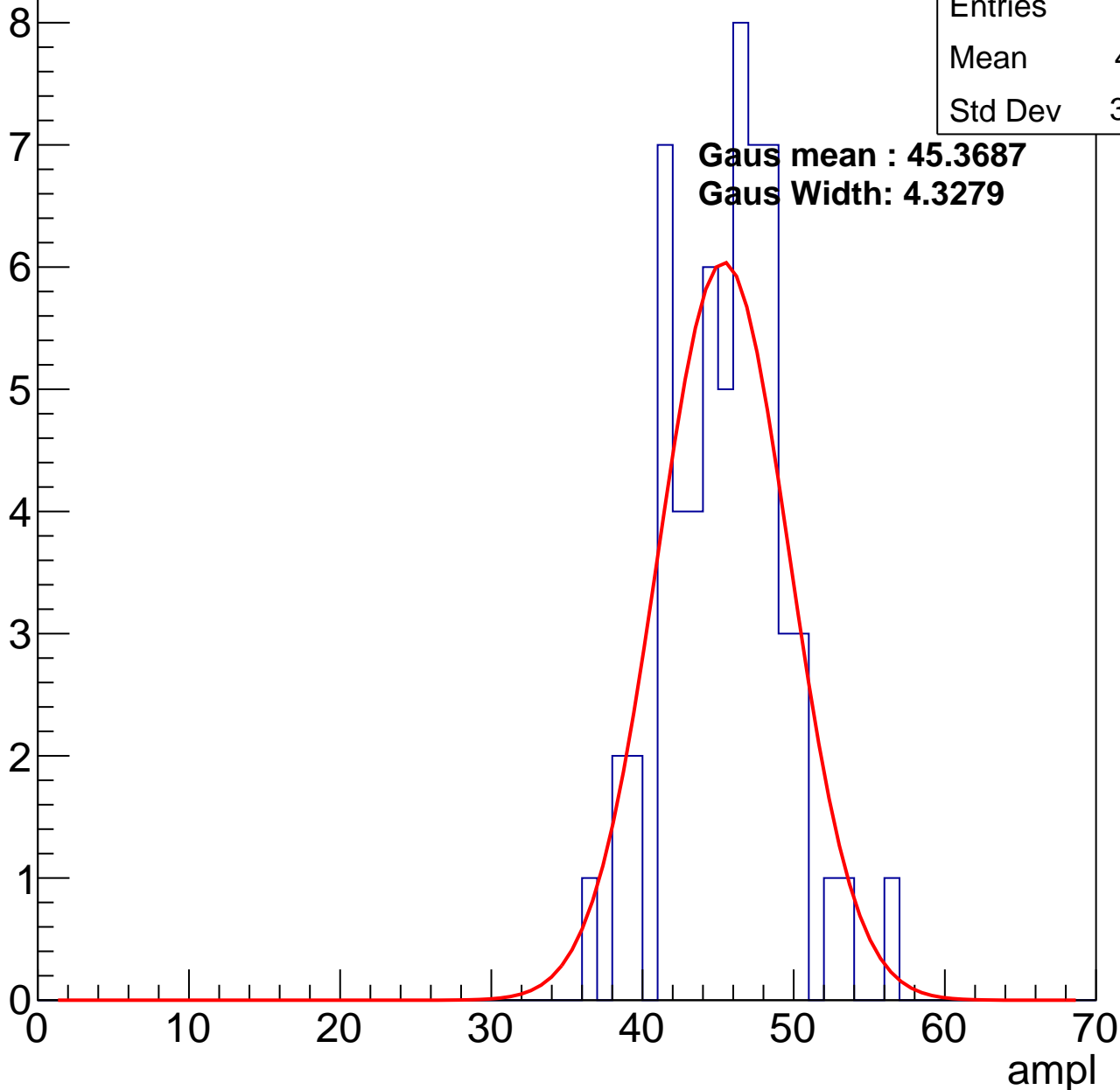
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	45.11
Std Dev	3.759

Gaus mean : 45.3687

Gaus Width: 4.3279

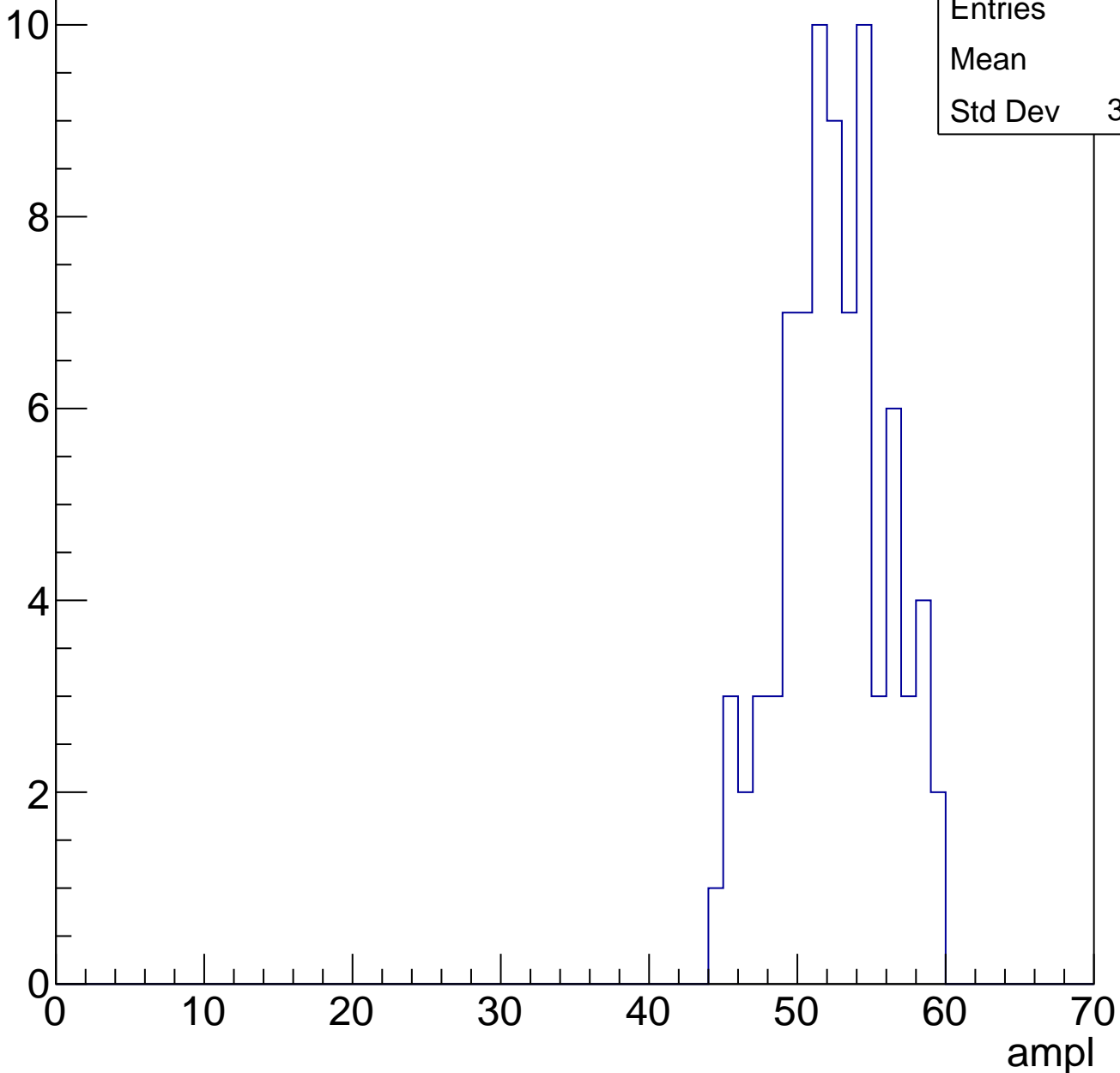


B1L102S, U13-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	52
Std Dev	3.525

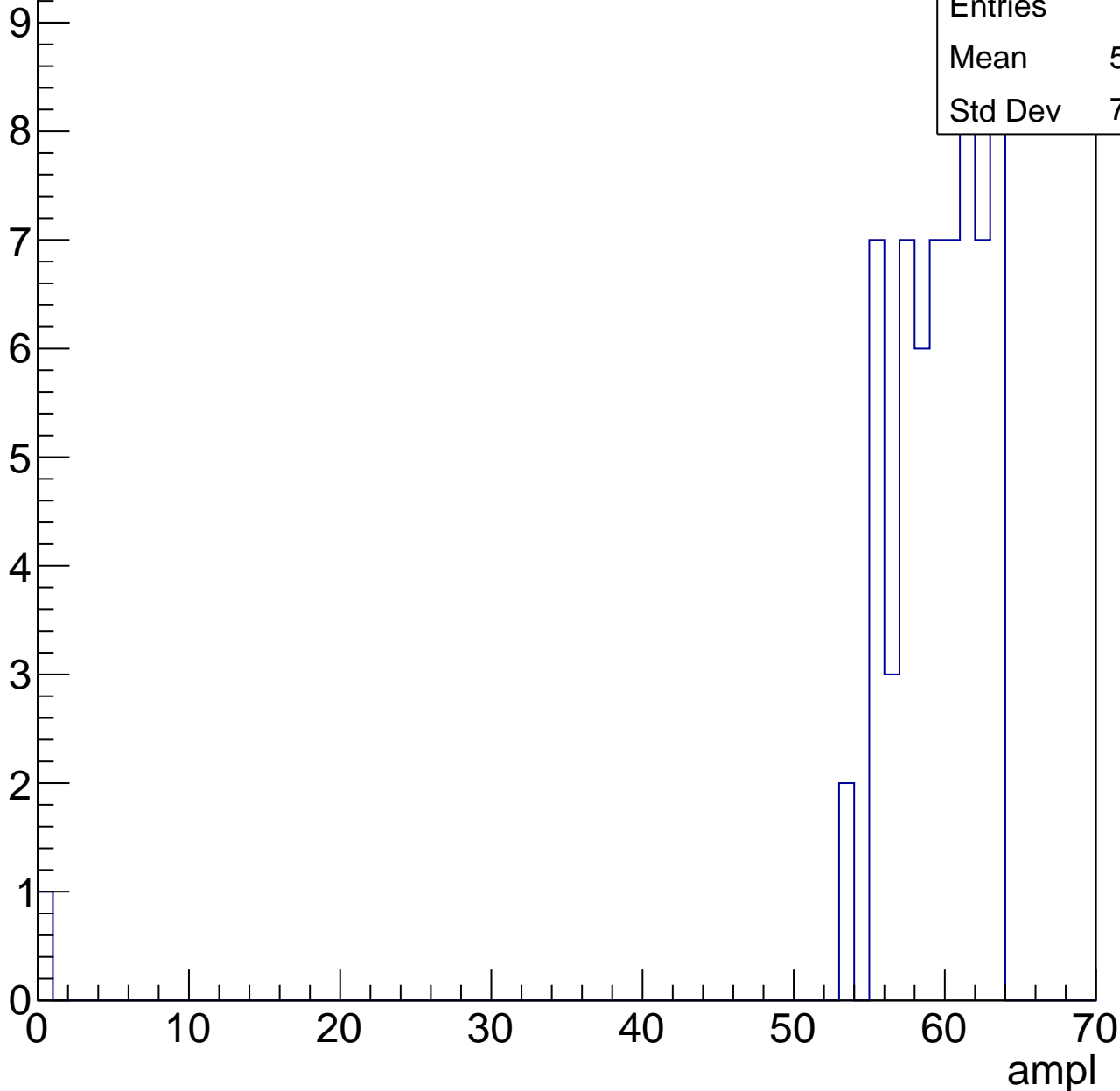


B1L102S, U13-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	58.22
Std Dev	7.827



B1L102S, U13-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	10
Mean	61.2
Std Dev	1.327

B1L102S, U13-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch16, adc0

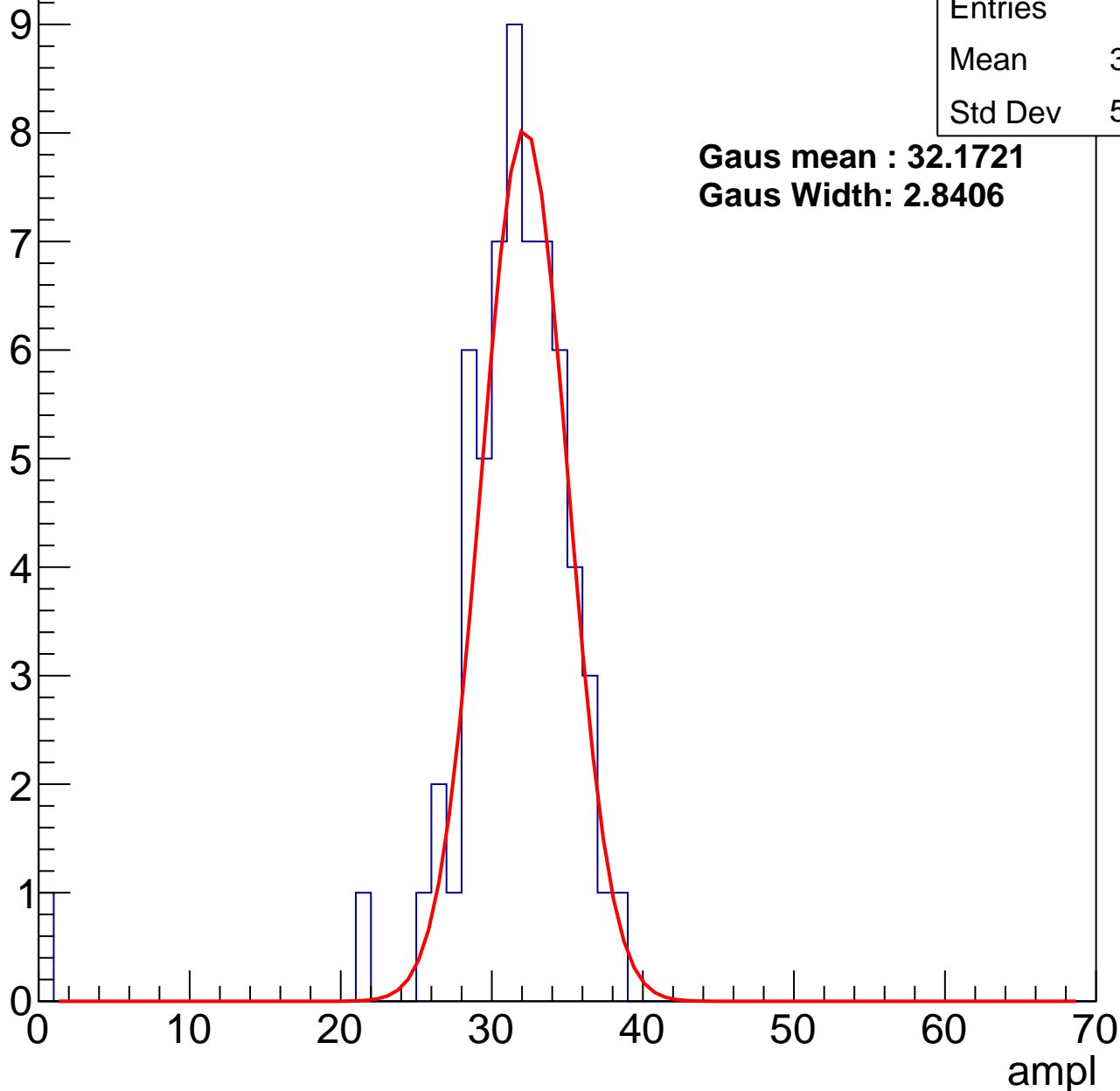
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	30.79
Std Dev	5.007

Gaus mean : 32.1721

Gaus Width: 2.8406



B1L102S, U13-ch16, adc1

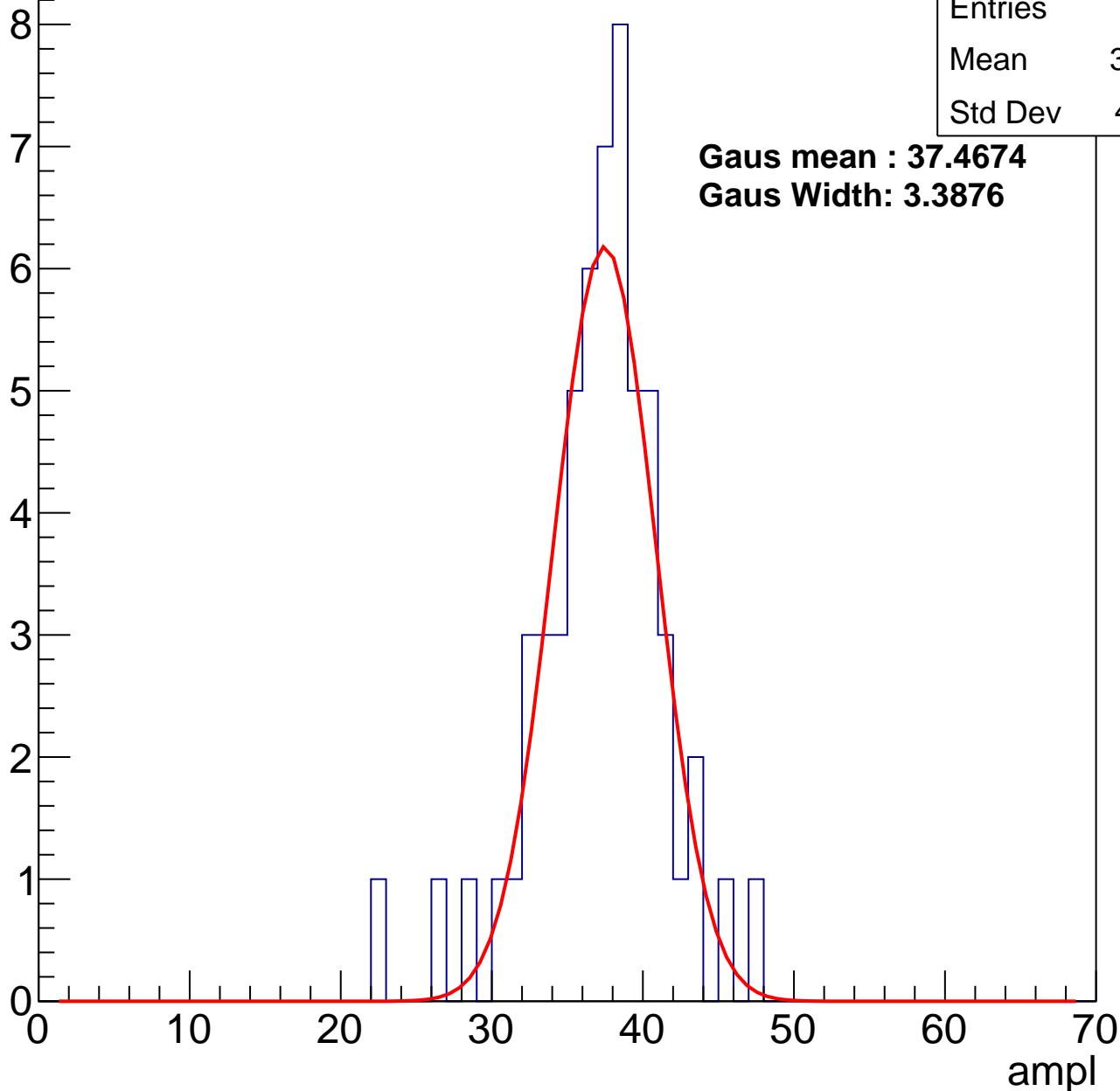
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.66
Std Dev	4.281

Gaus mean : 37.4674

Gaus Width: 3.3876



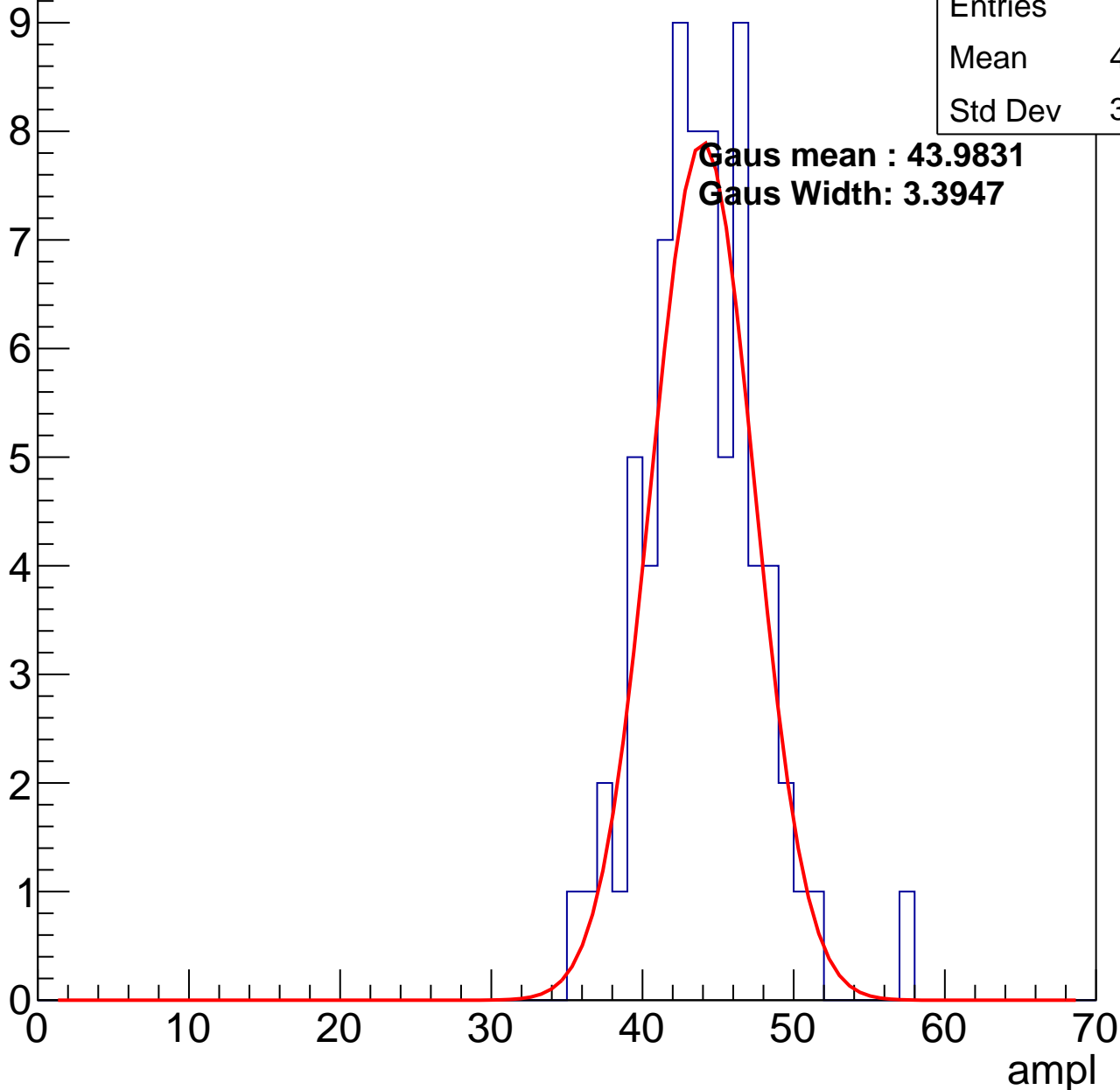
B1L102S, U13-ch16, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.48
Std Dev	3.702

Gaus mean : 43.9831
Gaus Width: 3.3947

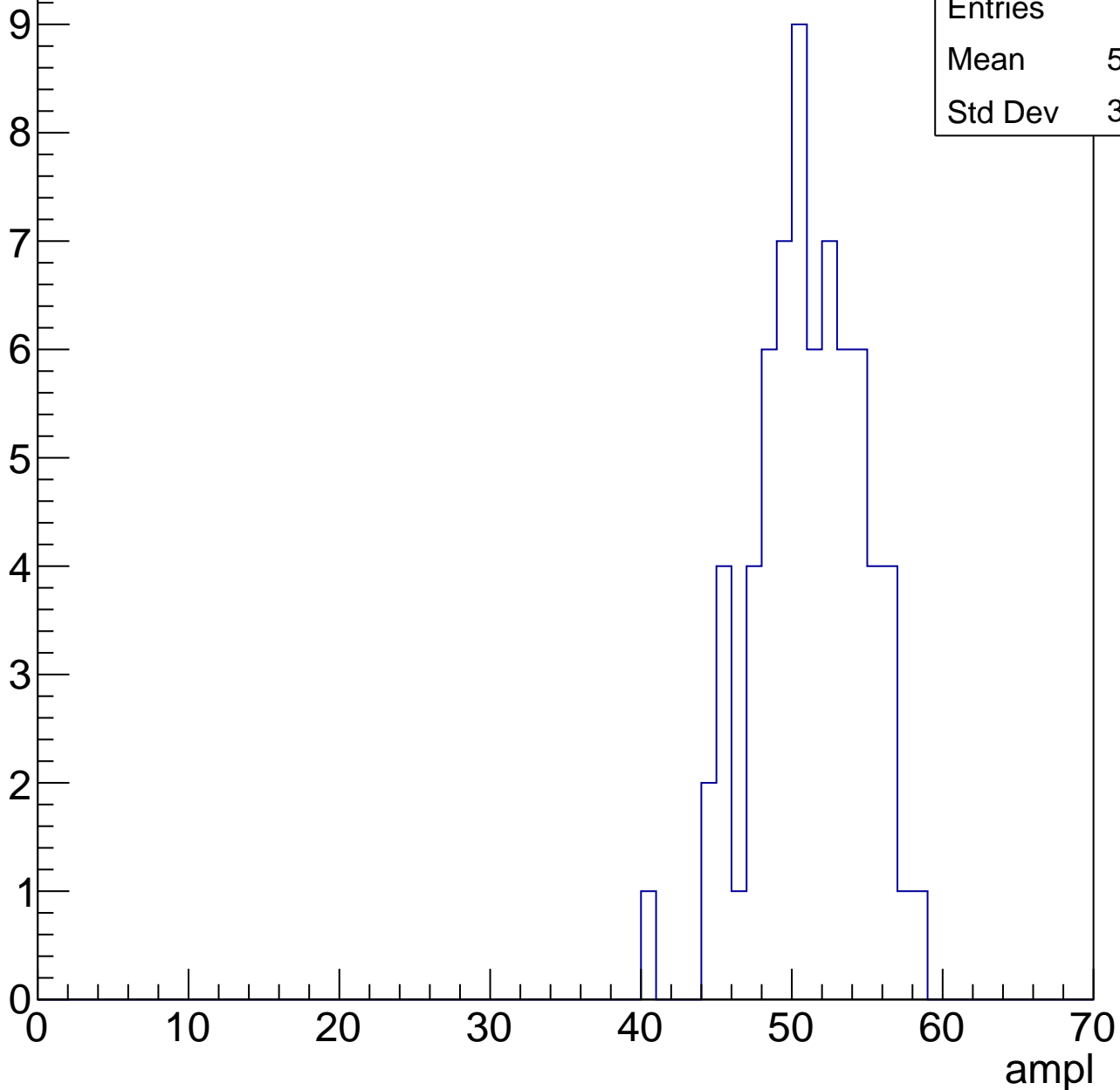


B1L102S, U13-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	50.64
Std Dev	3.547

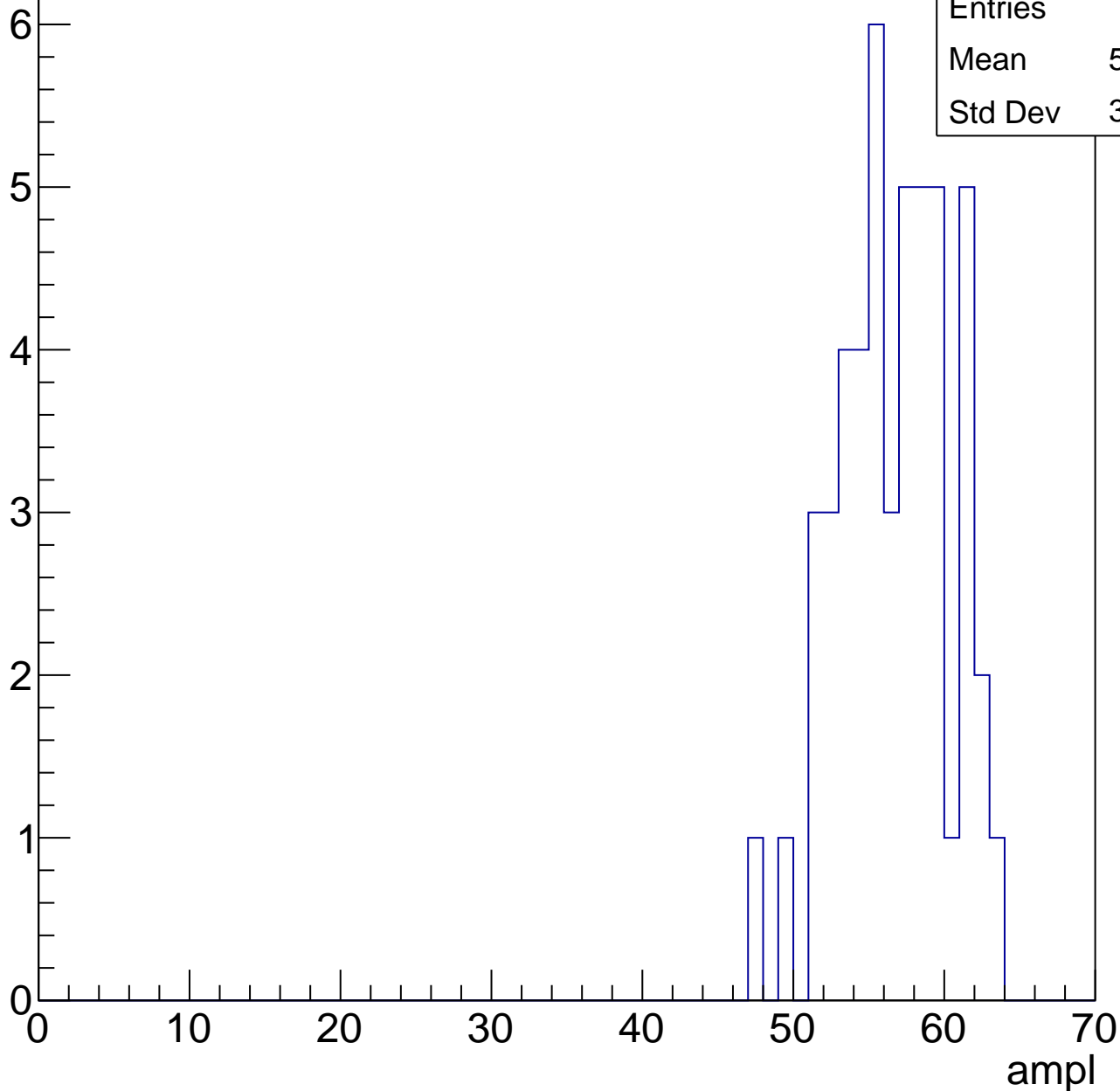


B1L102S, U13-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

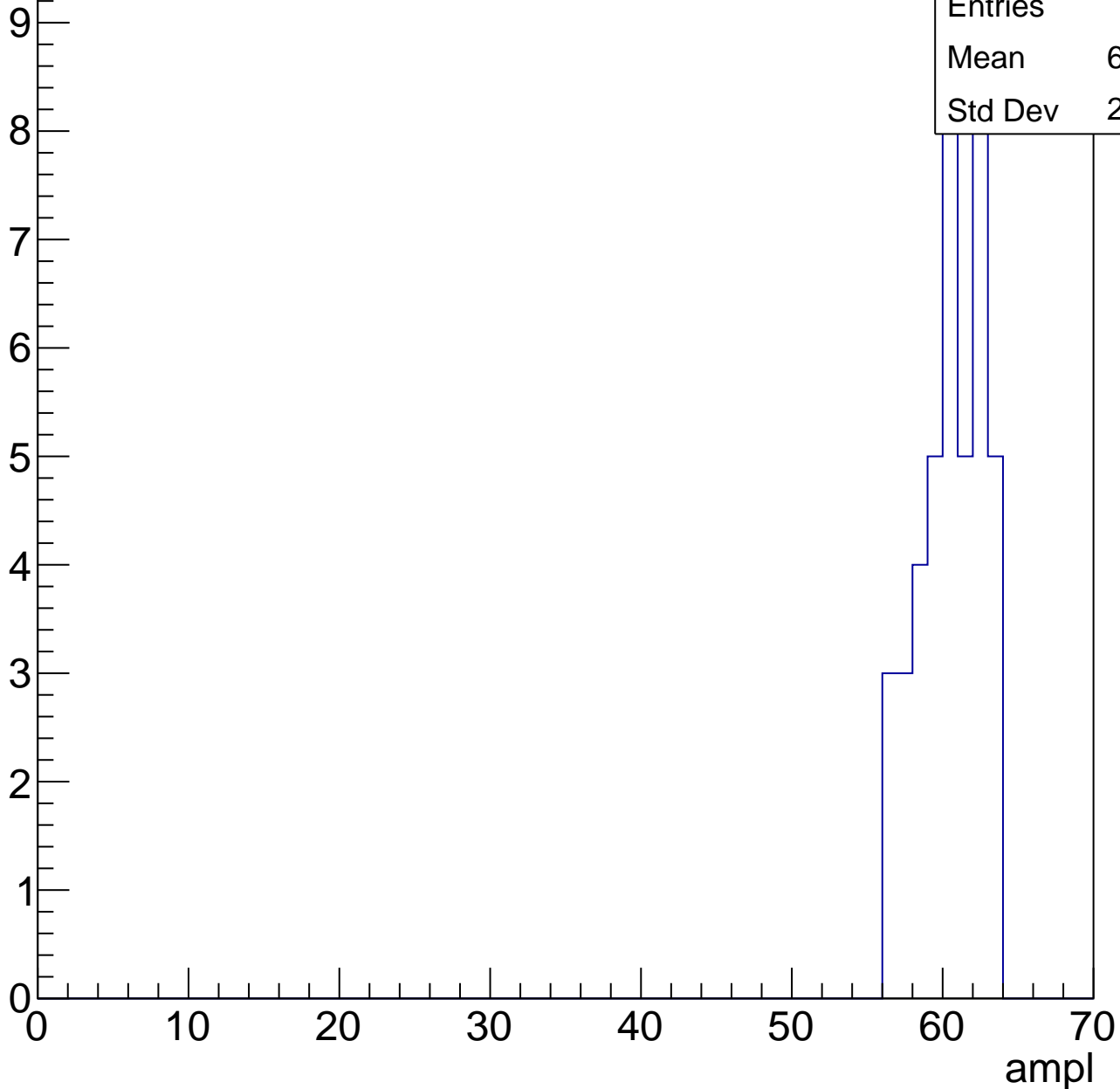
Entries	49
Mean	56.18
Std Dev	3.618



B1L102S, U13-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

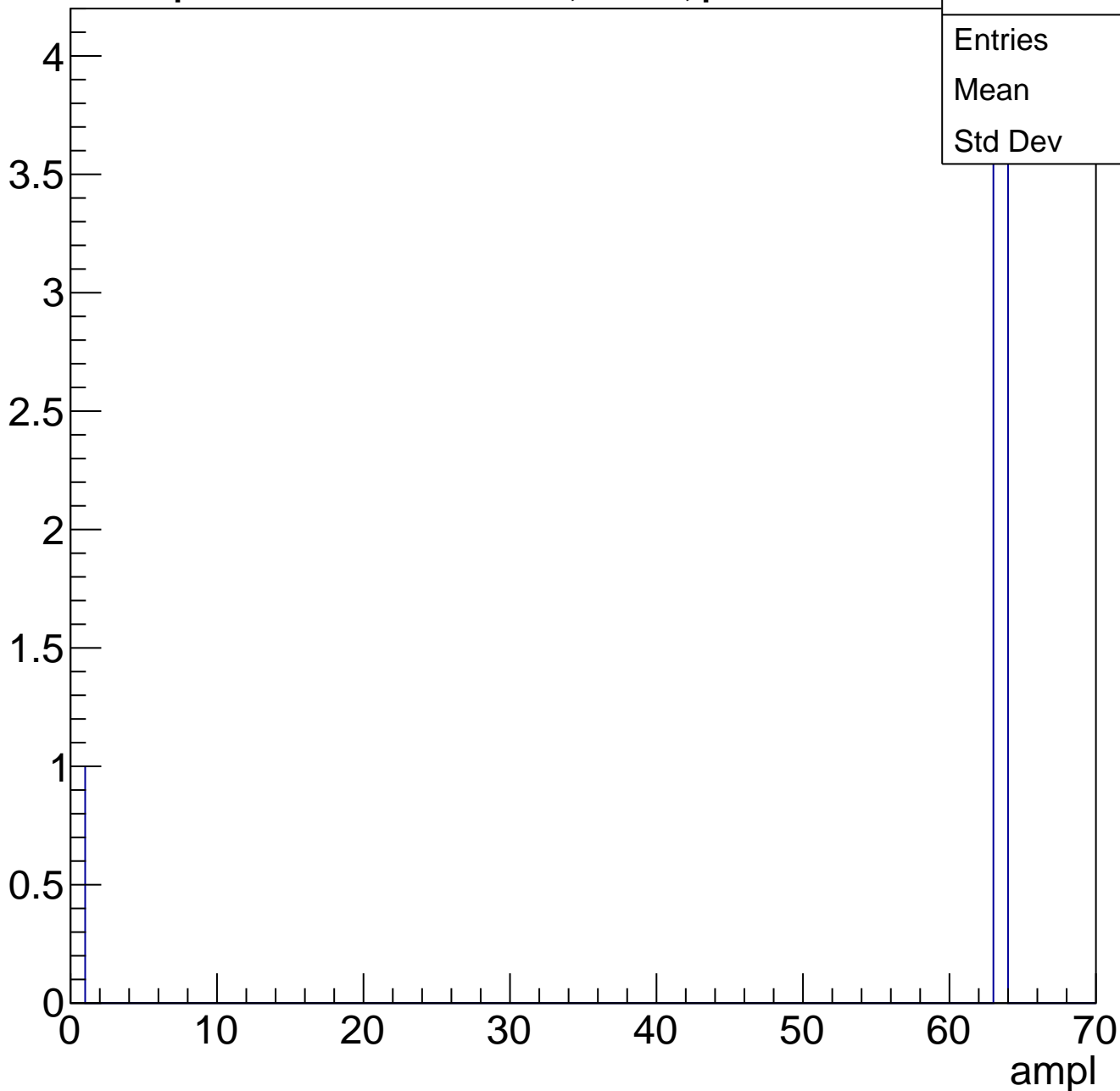
Entry



B1L102S, U13-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch17, adc0

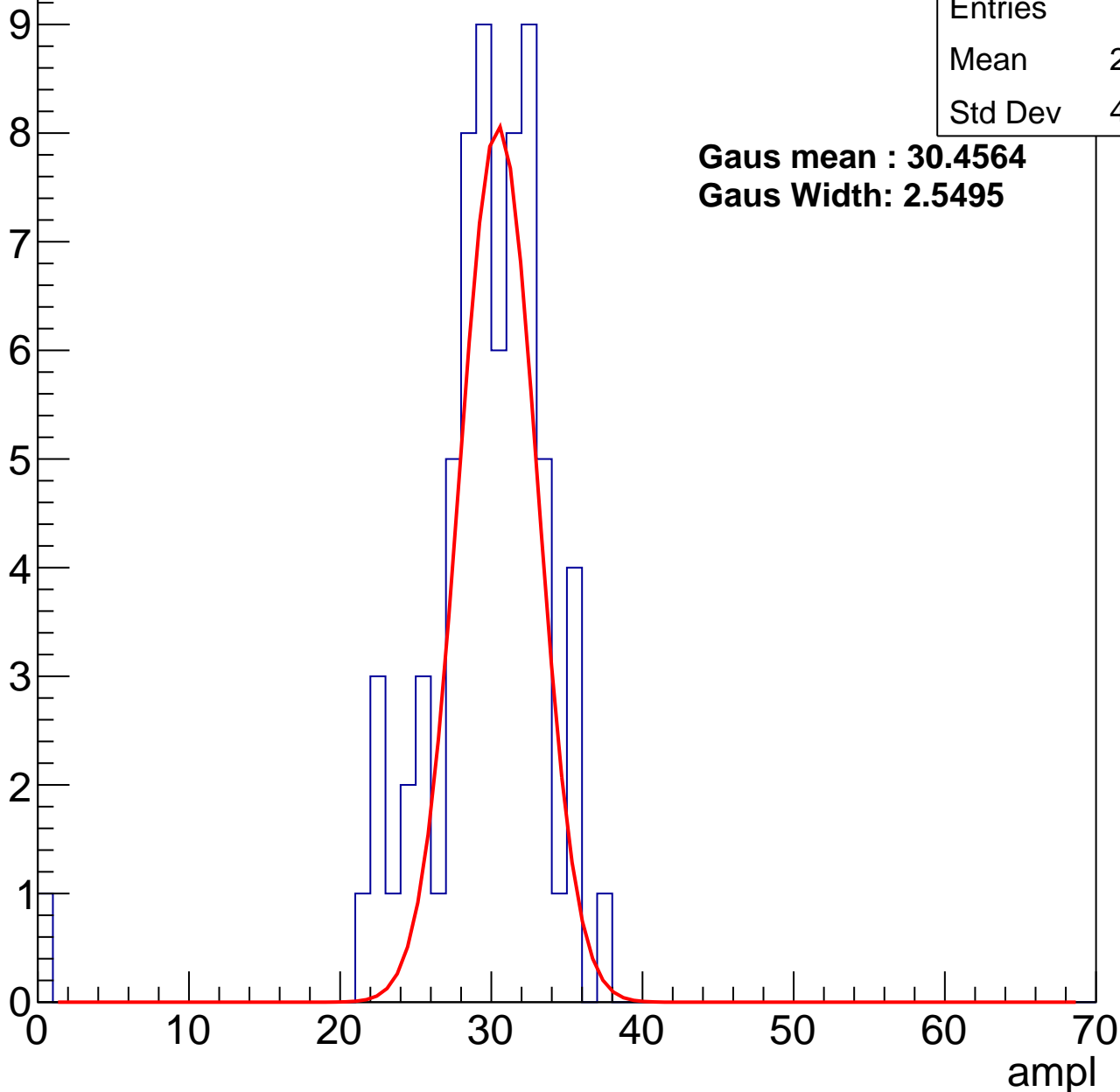
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.99
Std Dev	4.933

Gaus mean : 30.4564

Gaus Width: 2.5495

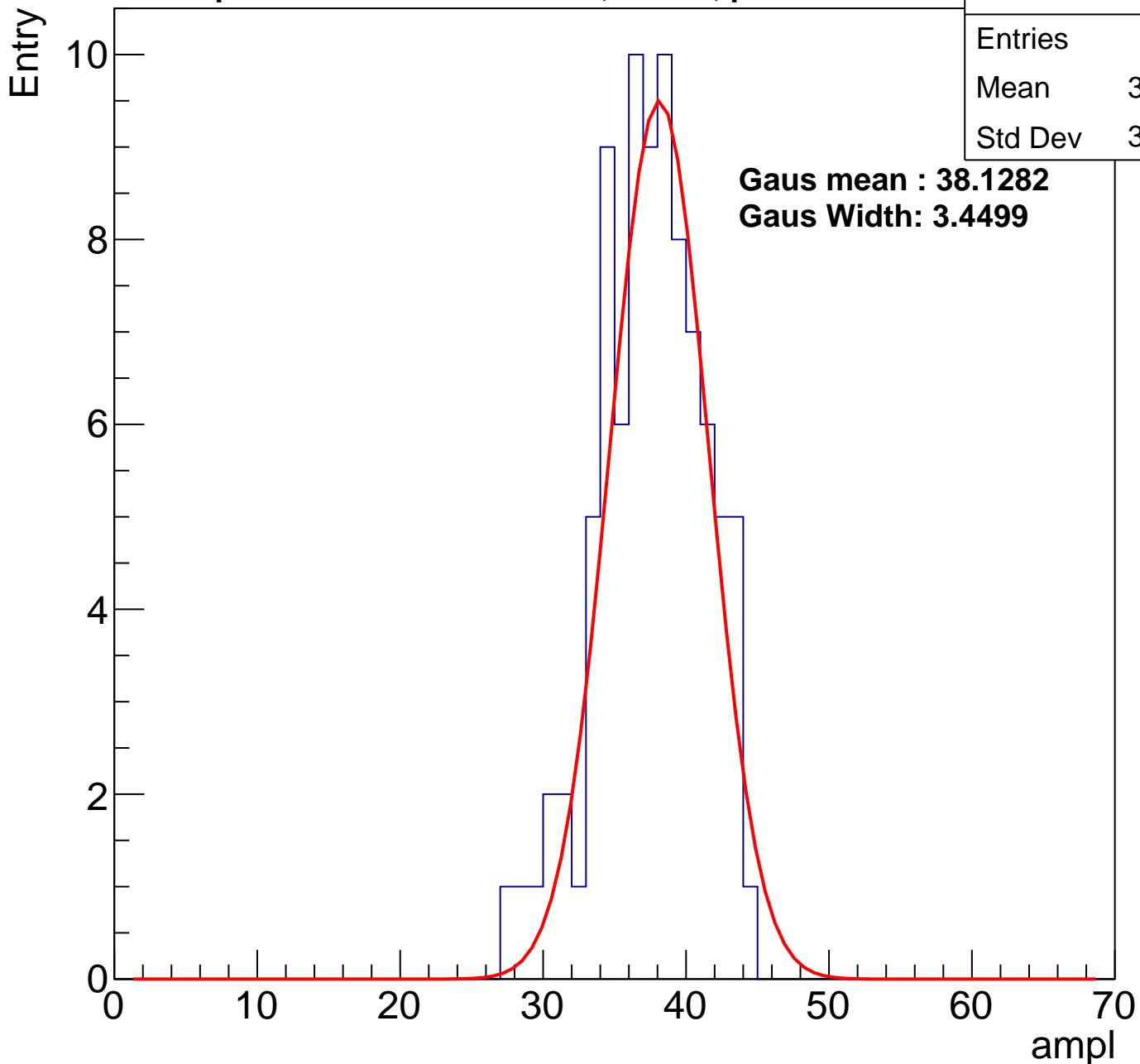


B1L102S, U13-ch17, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	37.07
Std Dev	3.656

Gaus mean : 38.1282
Gaus Width: 3.4499



B1L102S, U13-ch17, adc2

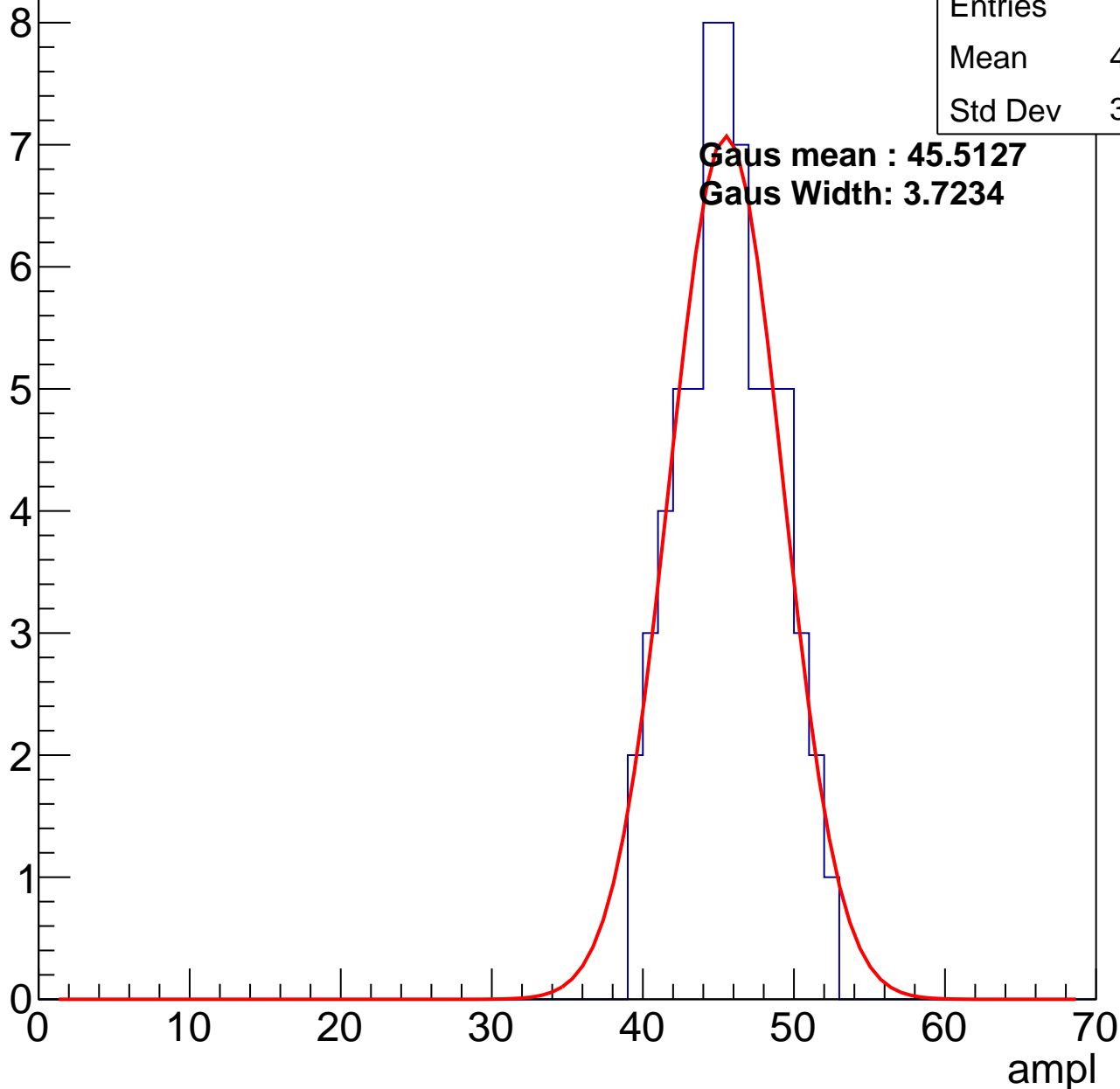
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	45.16
Std Dev	3.163

Gaus mean : 45.5127

Gaus Width: 3.7234

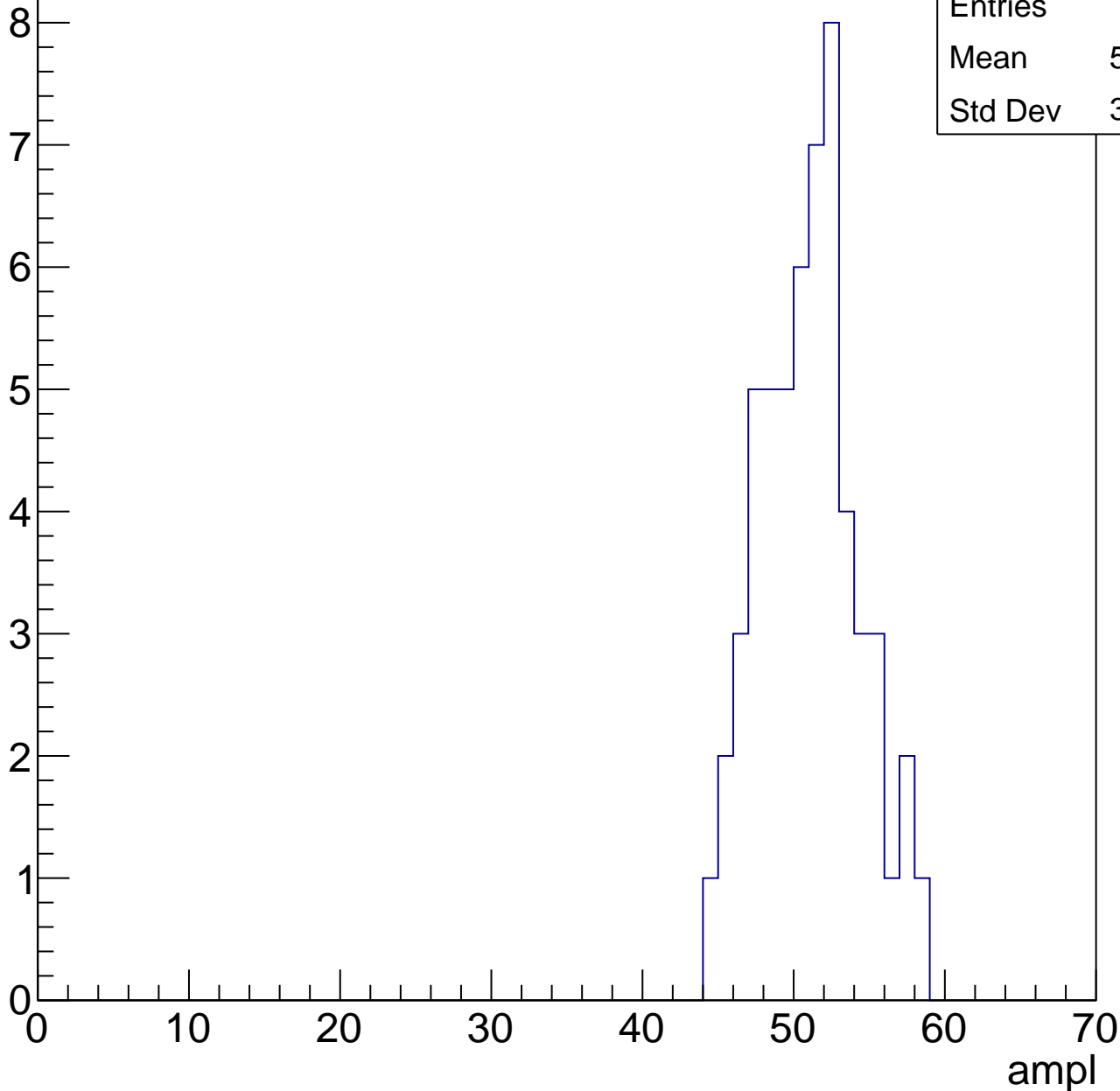


B1L102S, U13-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	50.57
Std Dev	3.223

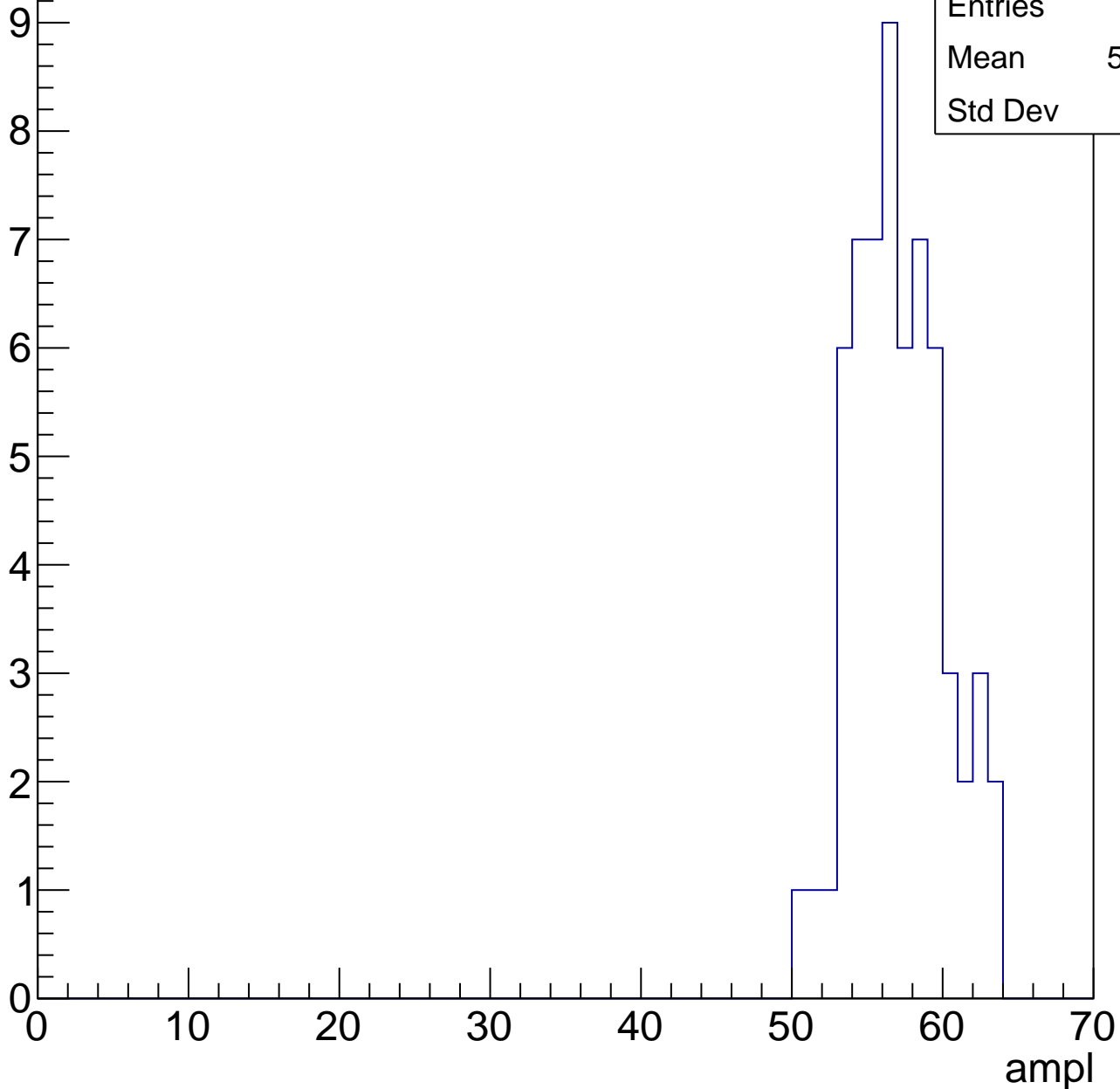


B1L102S, U13-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	56.62
Std Dev	2.96

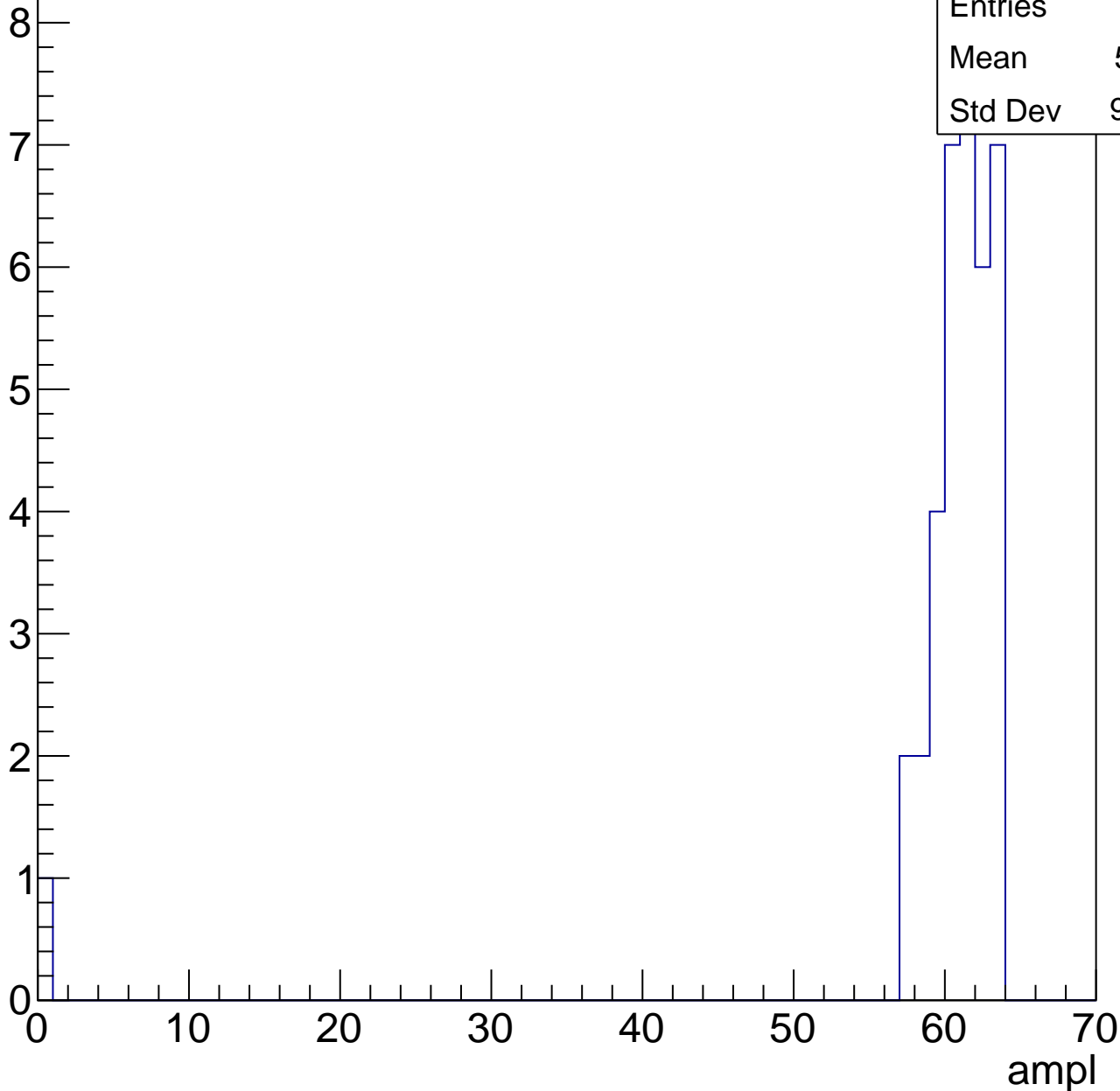


B1L102S, U13-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	59.11
Std Dev	9.994



B1L102S, U13-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch18, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	27.46
Std Dev	4.922

Gaus mean : 28.1030

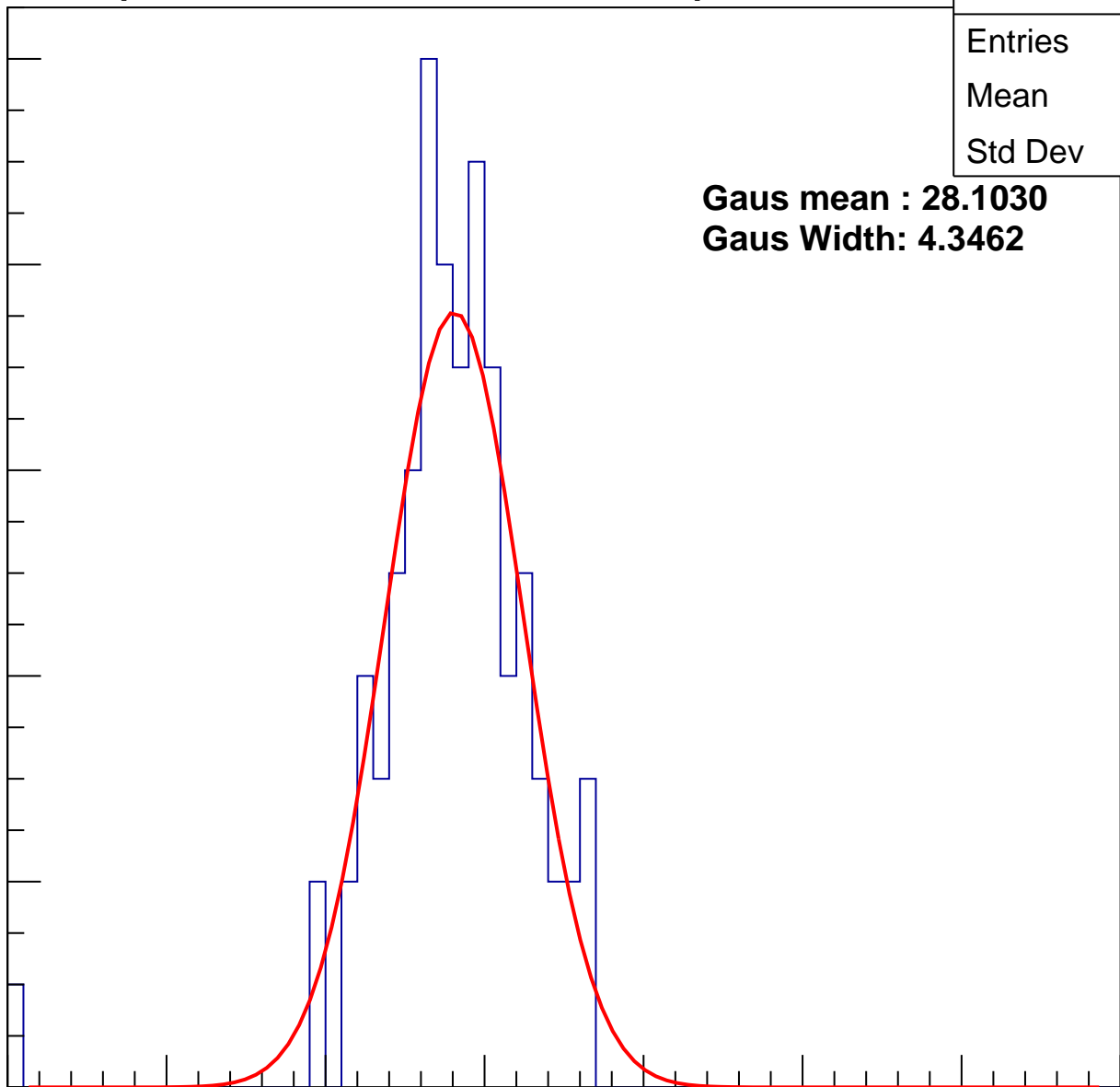
Gaus Width: 4.3462

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch18, adc1

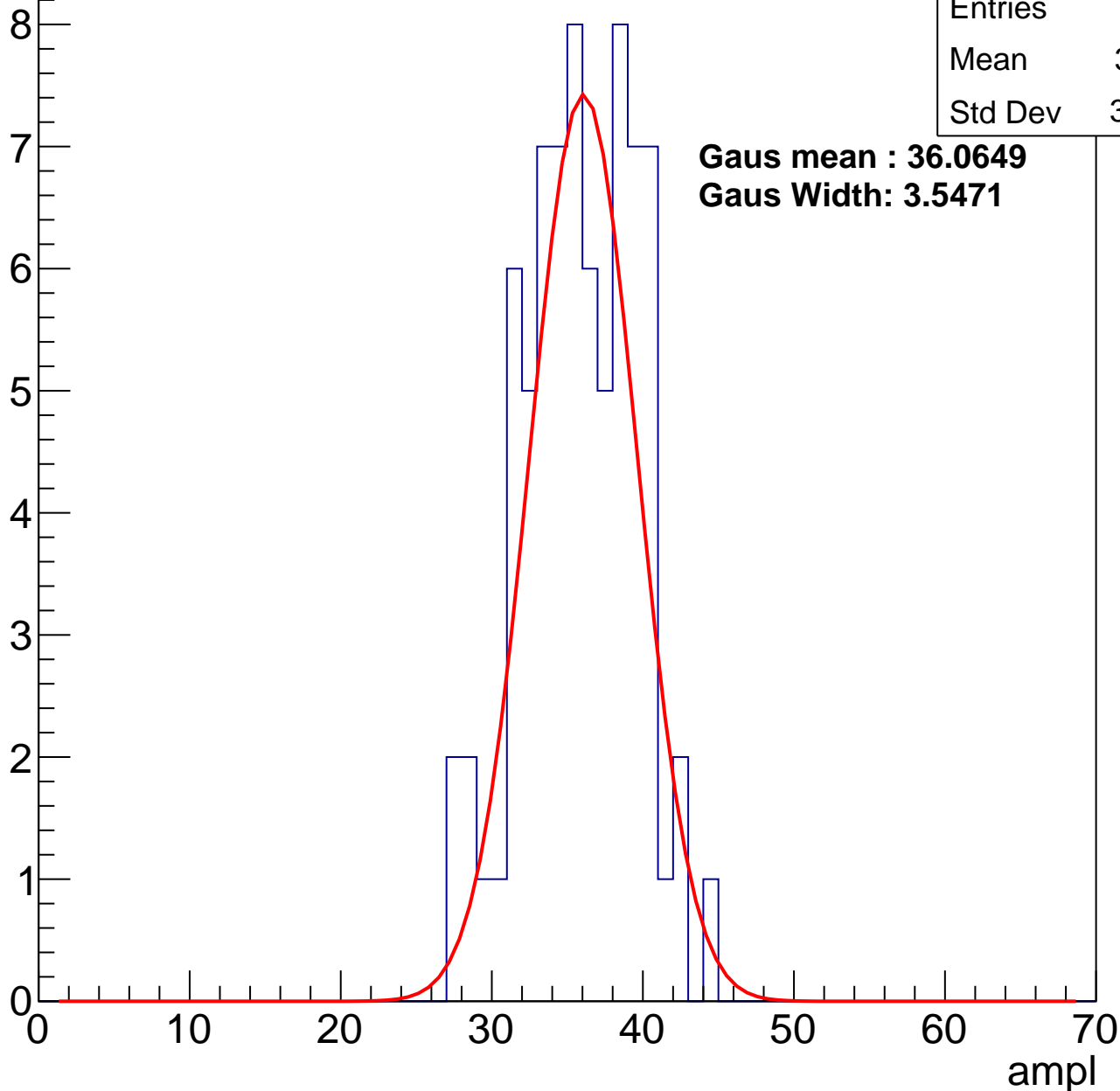
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.41
Std Dev	3.718

Gaus mean : 36.0649

Gaus Width: 3.5471



B1L102S, U13-ch18, adc2

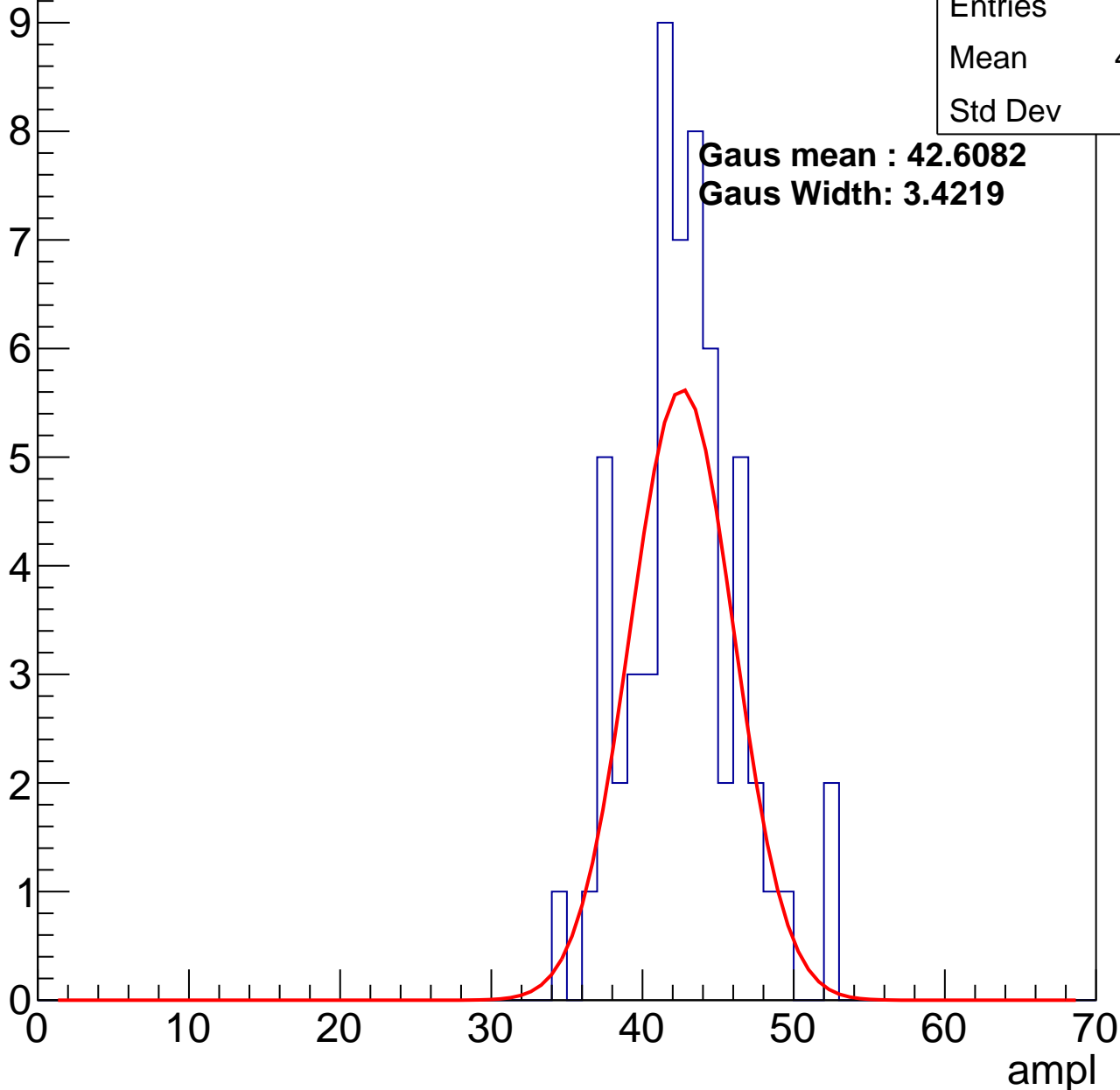
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.31
Std Dev	3.63

Gaus mean : 42.6082

Gaus Width: 3.4219

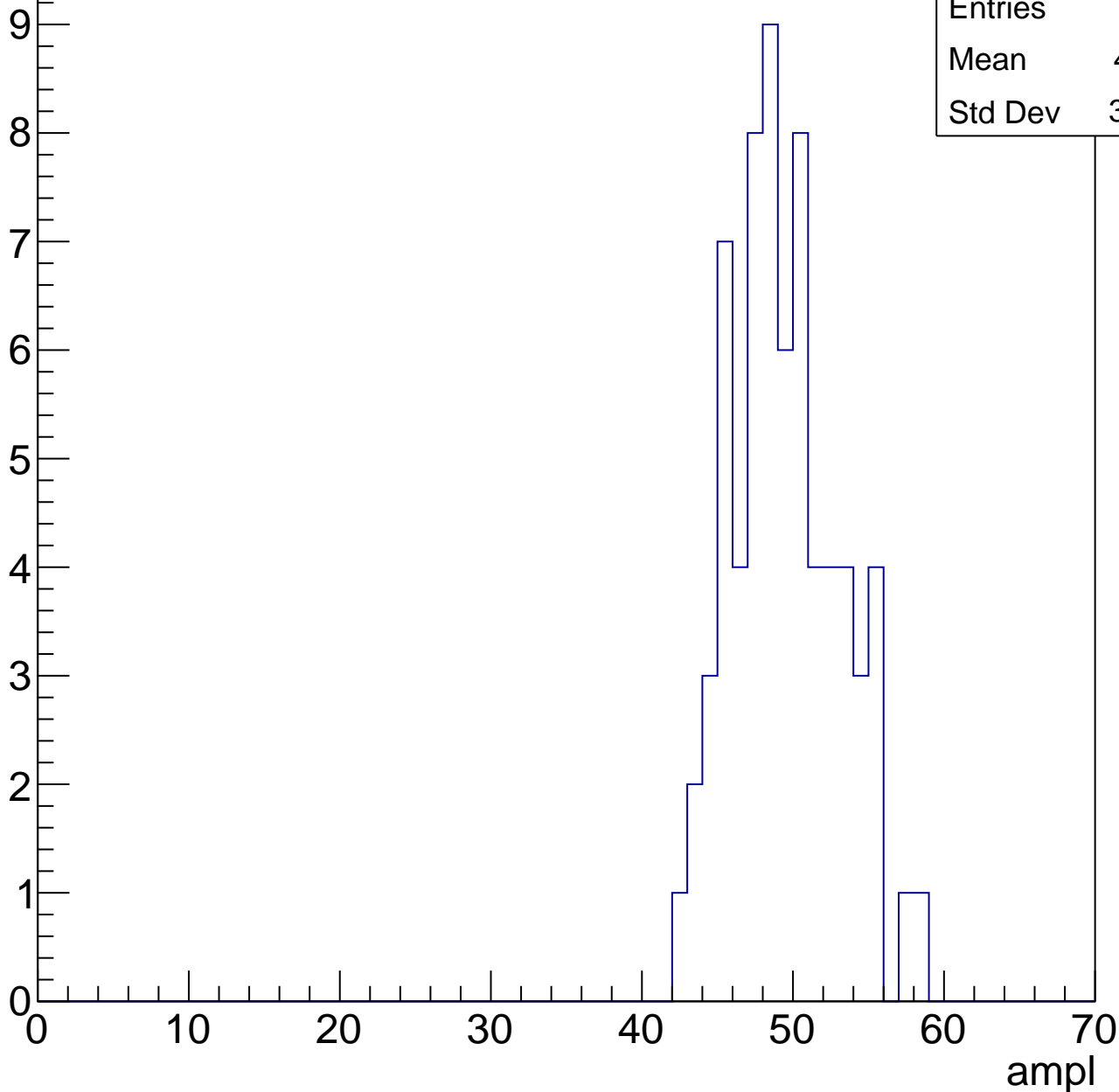


B1L102S, U13-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.01
Std Dev	3.557

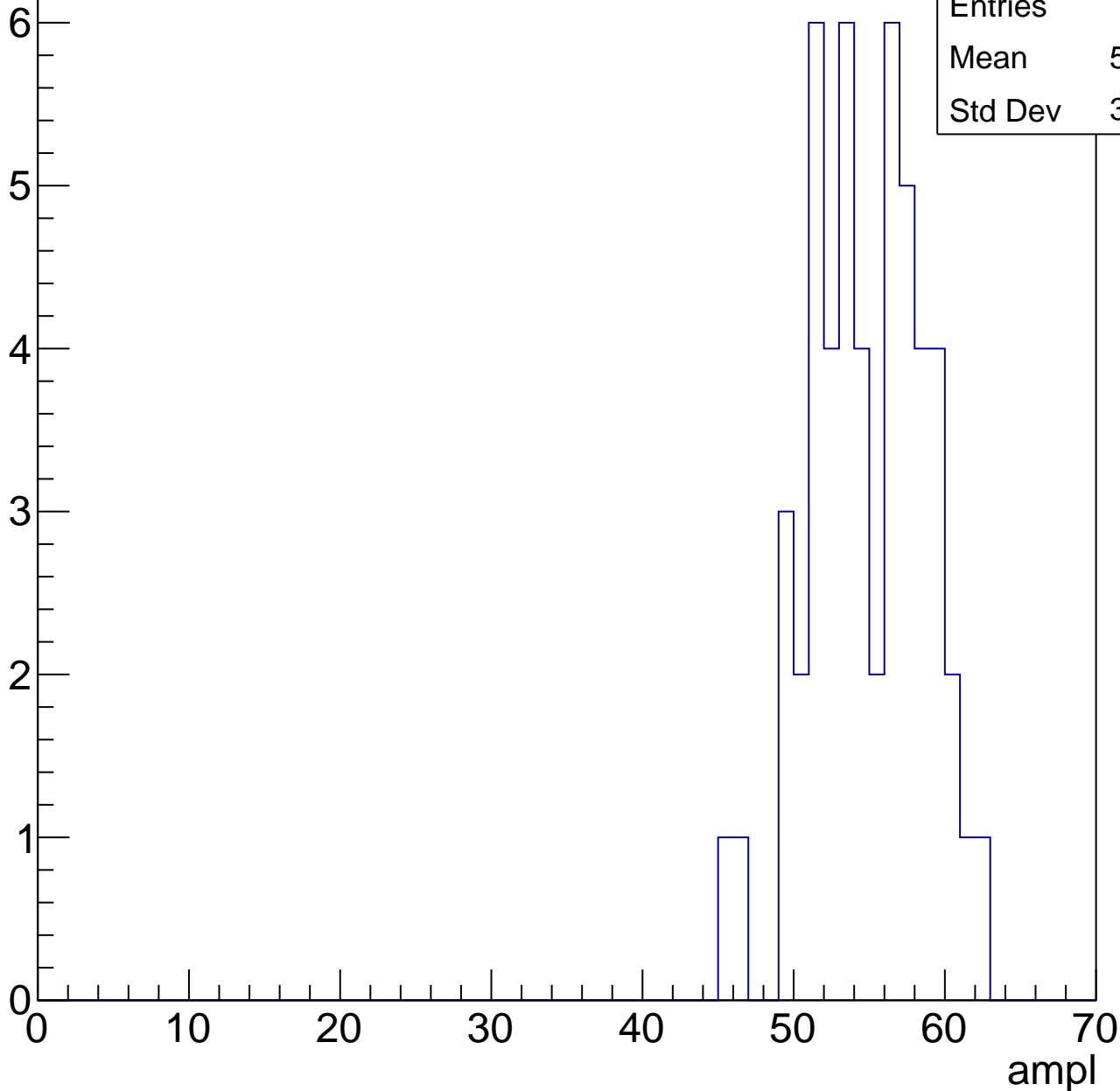


B1L102S, U13-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.38
Std Dev	3.773

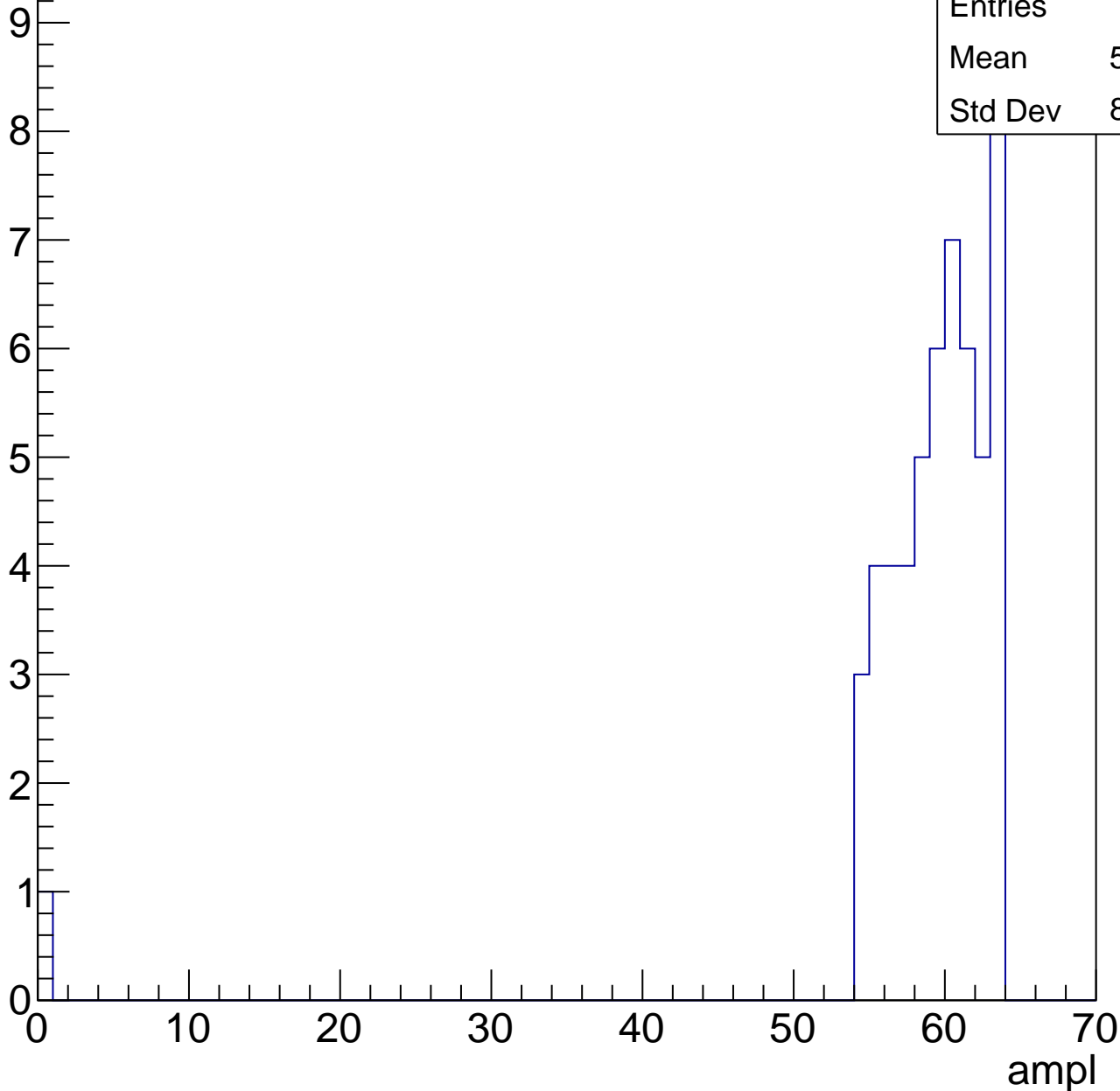


B1L102S, U13-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

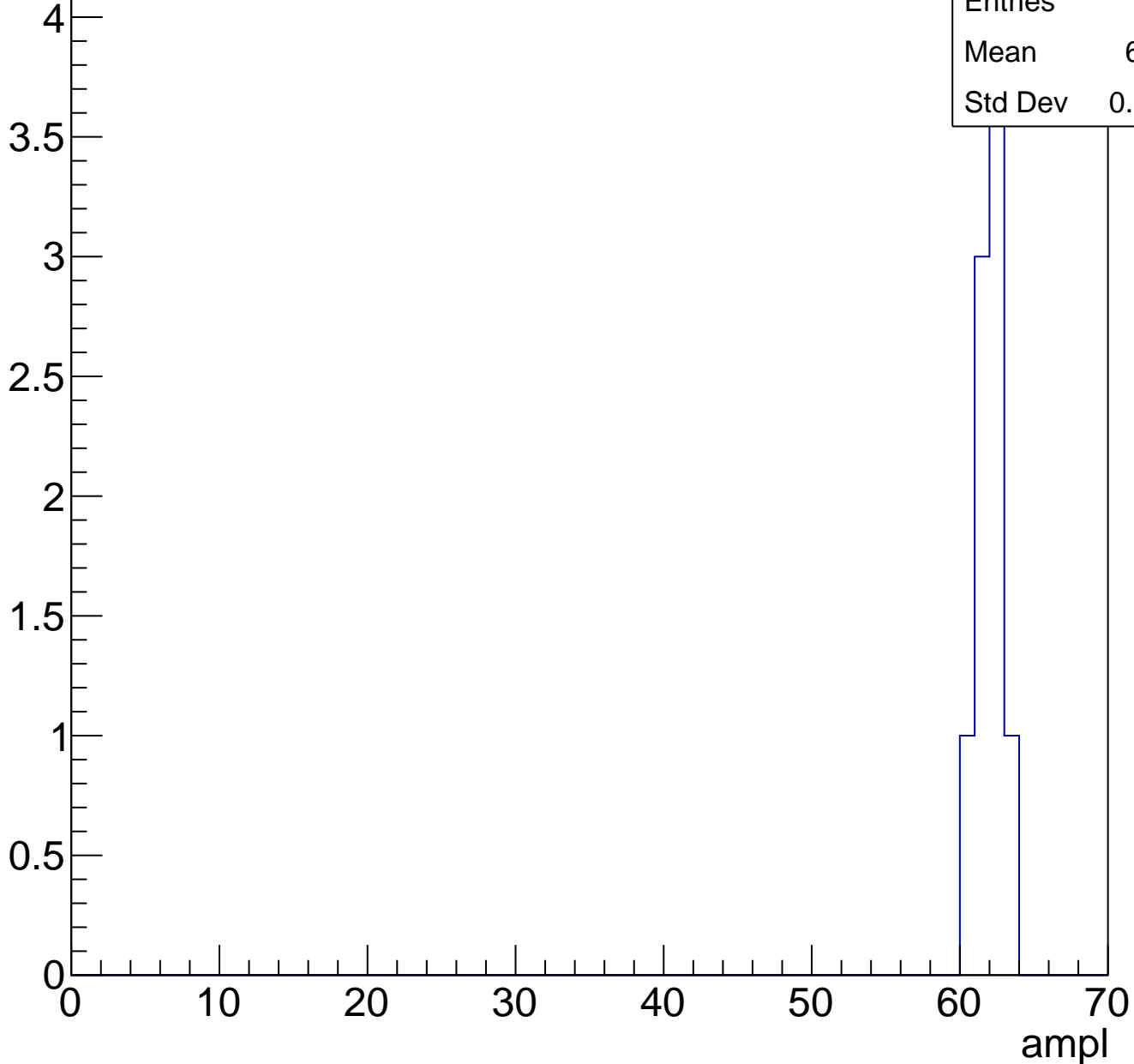
Entries	54
Mean	58.17
Std Dev	8.454



B1L102S, U13-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch19, adc0

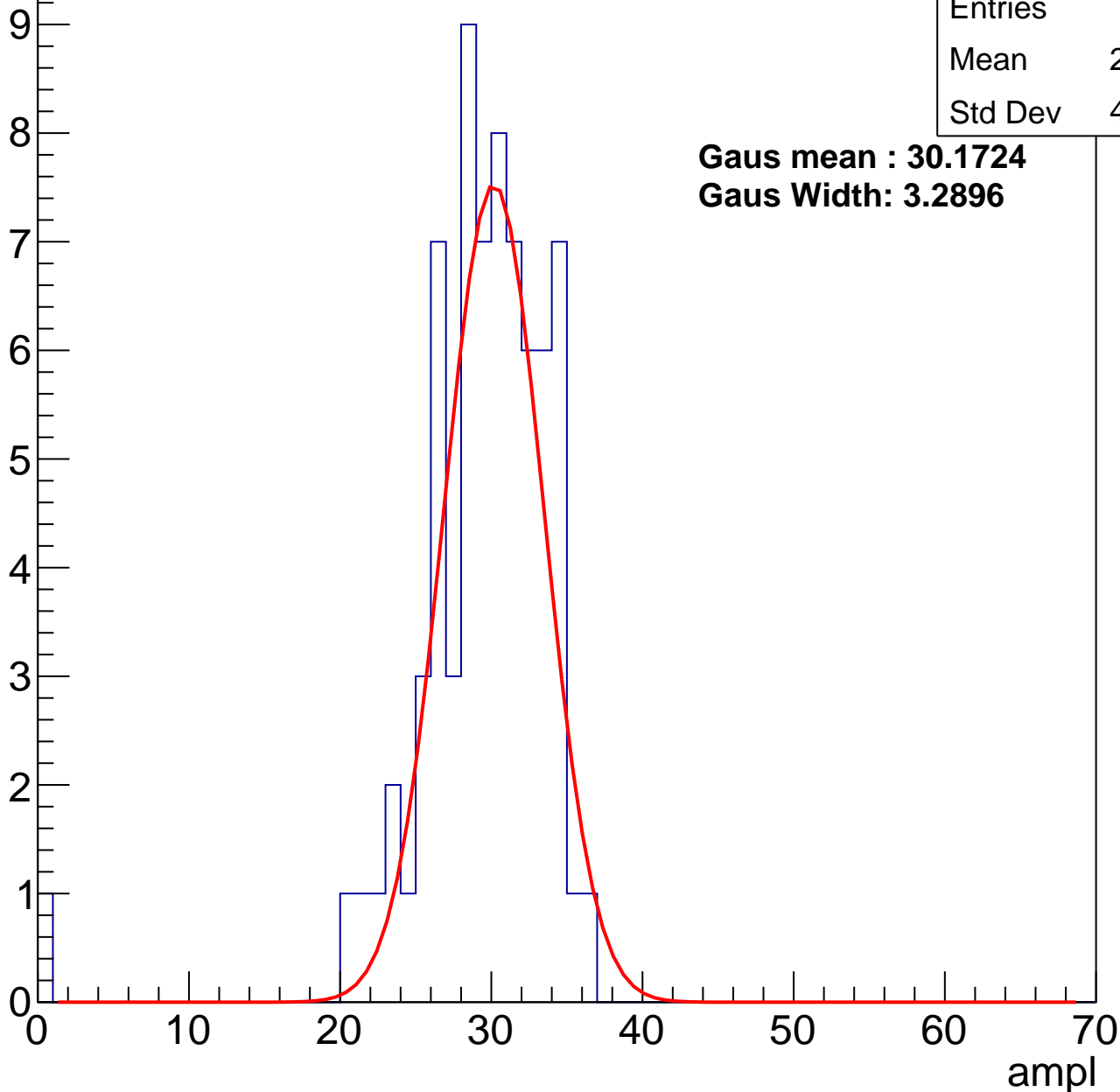
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.92
Std Dev	4.873

Gaus mean : 30.1724

Gaus Width: 3.2896



B1L102S, U13-ch19, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	37.44
Std Dev	3.905

Gaus mean : 38.4080

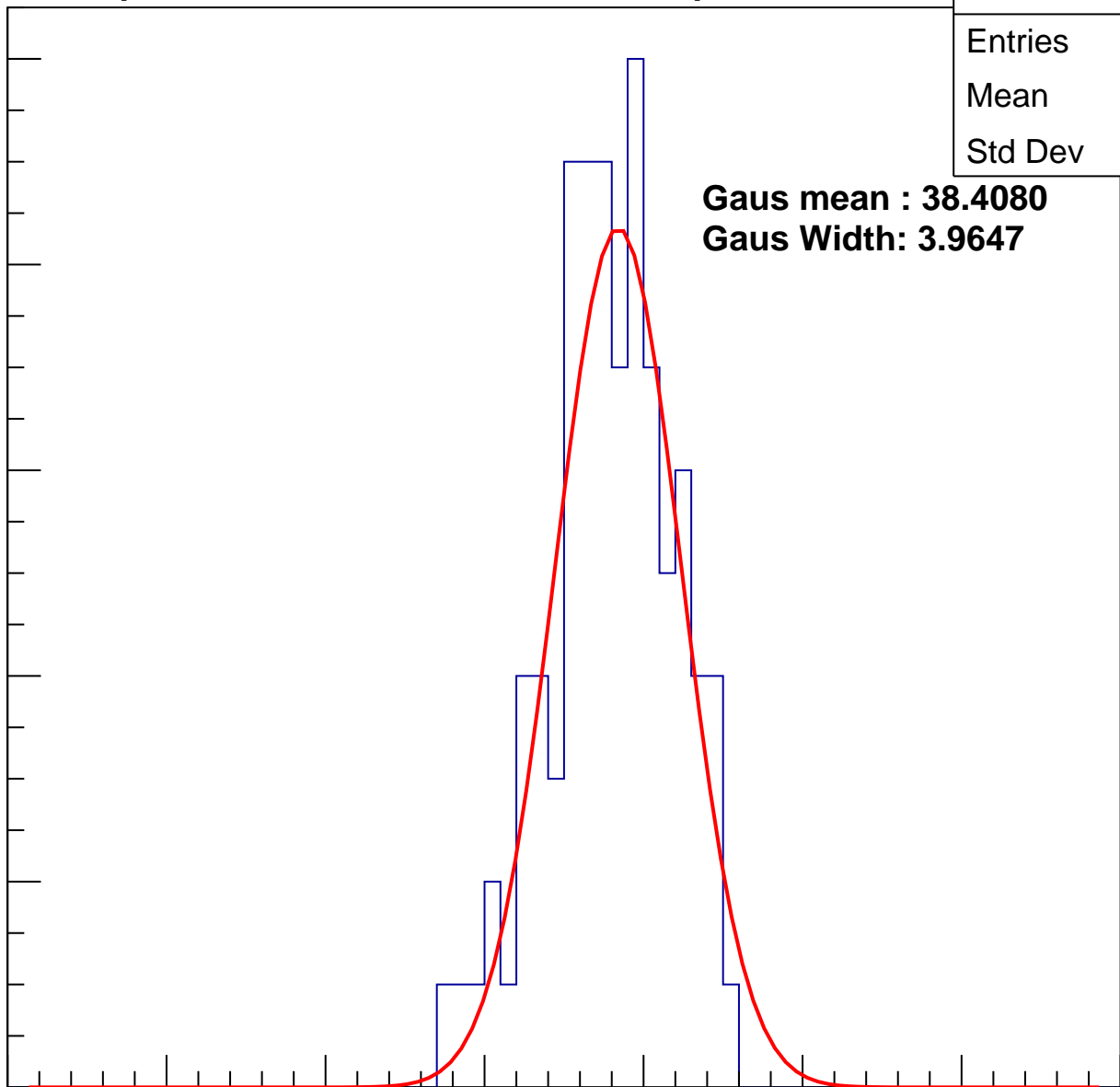
Gaus Width: 3.9647

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

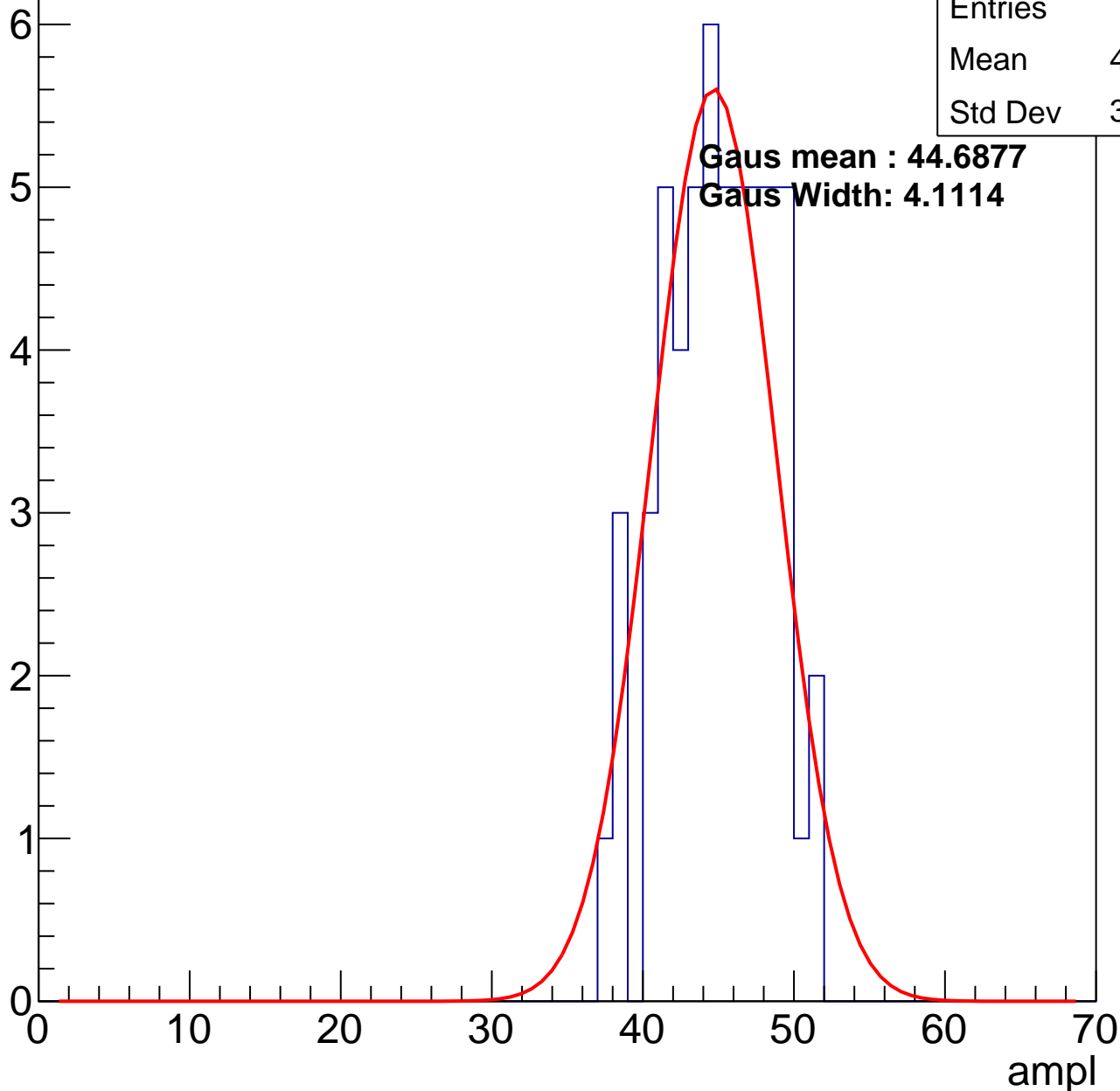


B1L102S, U13-ch19, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	44.55
Std Dev	3.474

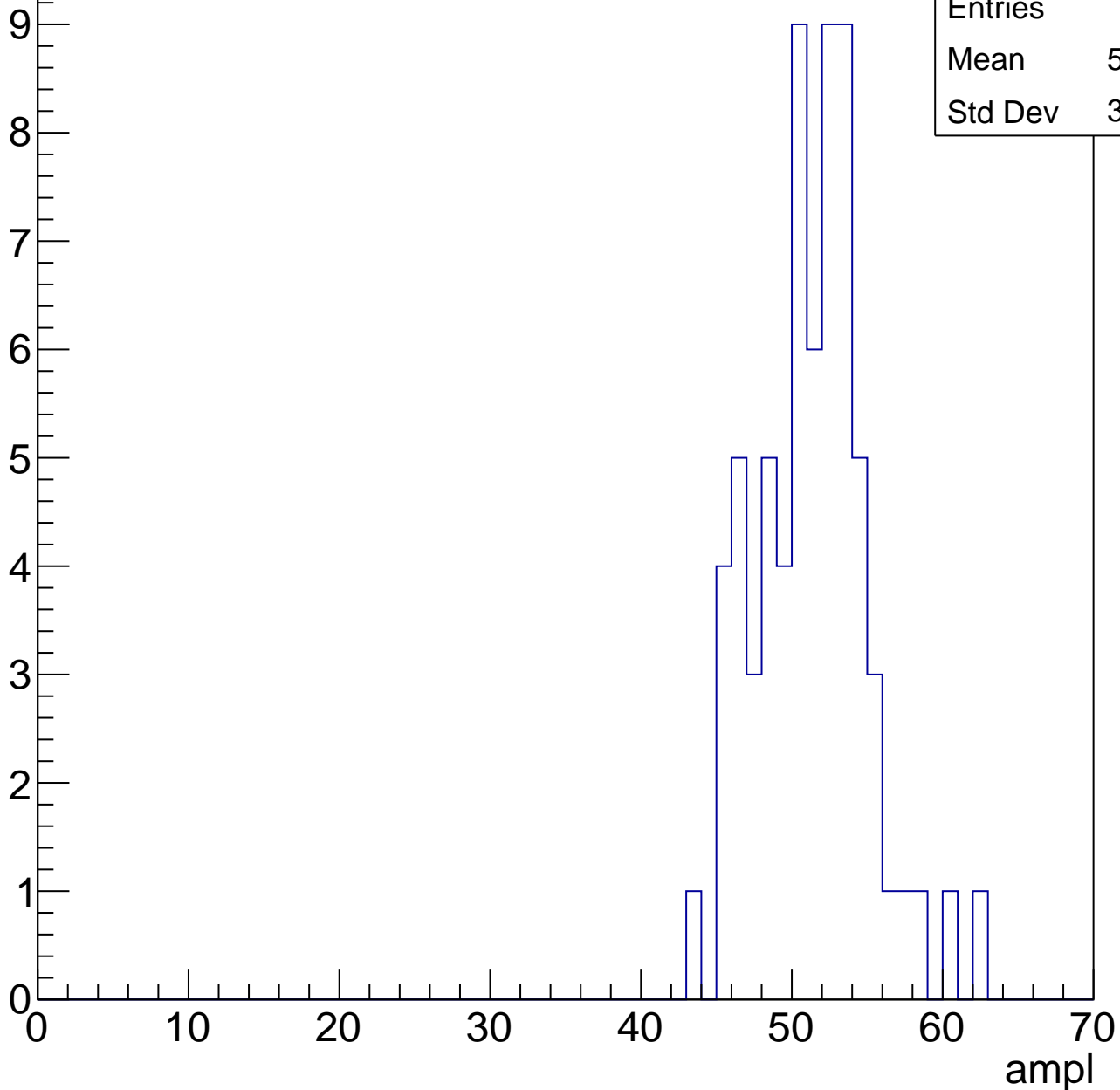


B1L102S, U13-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	50.87
Std Dev	3.638

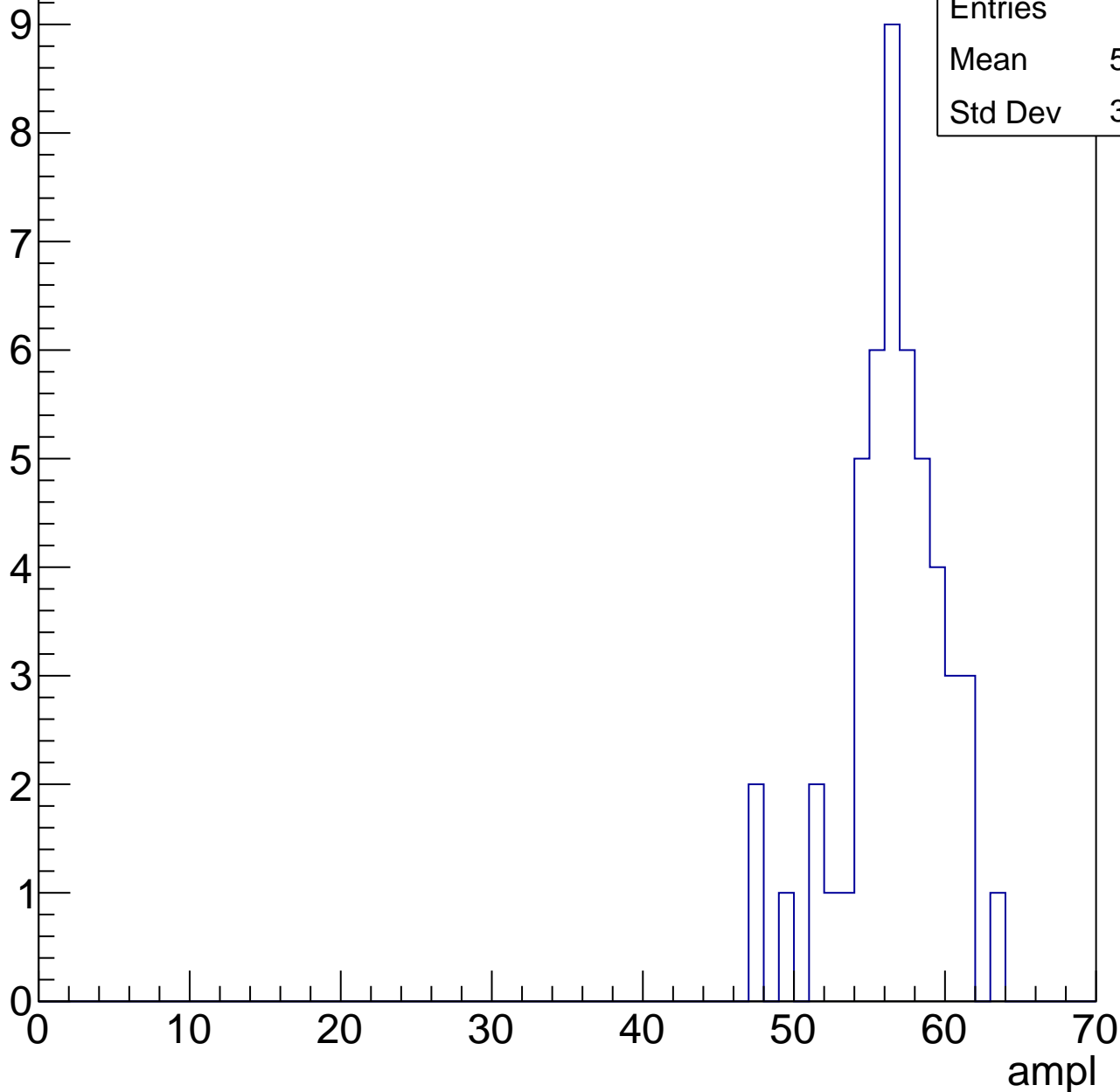


B1L102S, U13-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.08
Std Dev	3.337

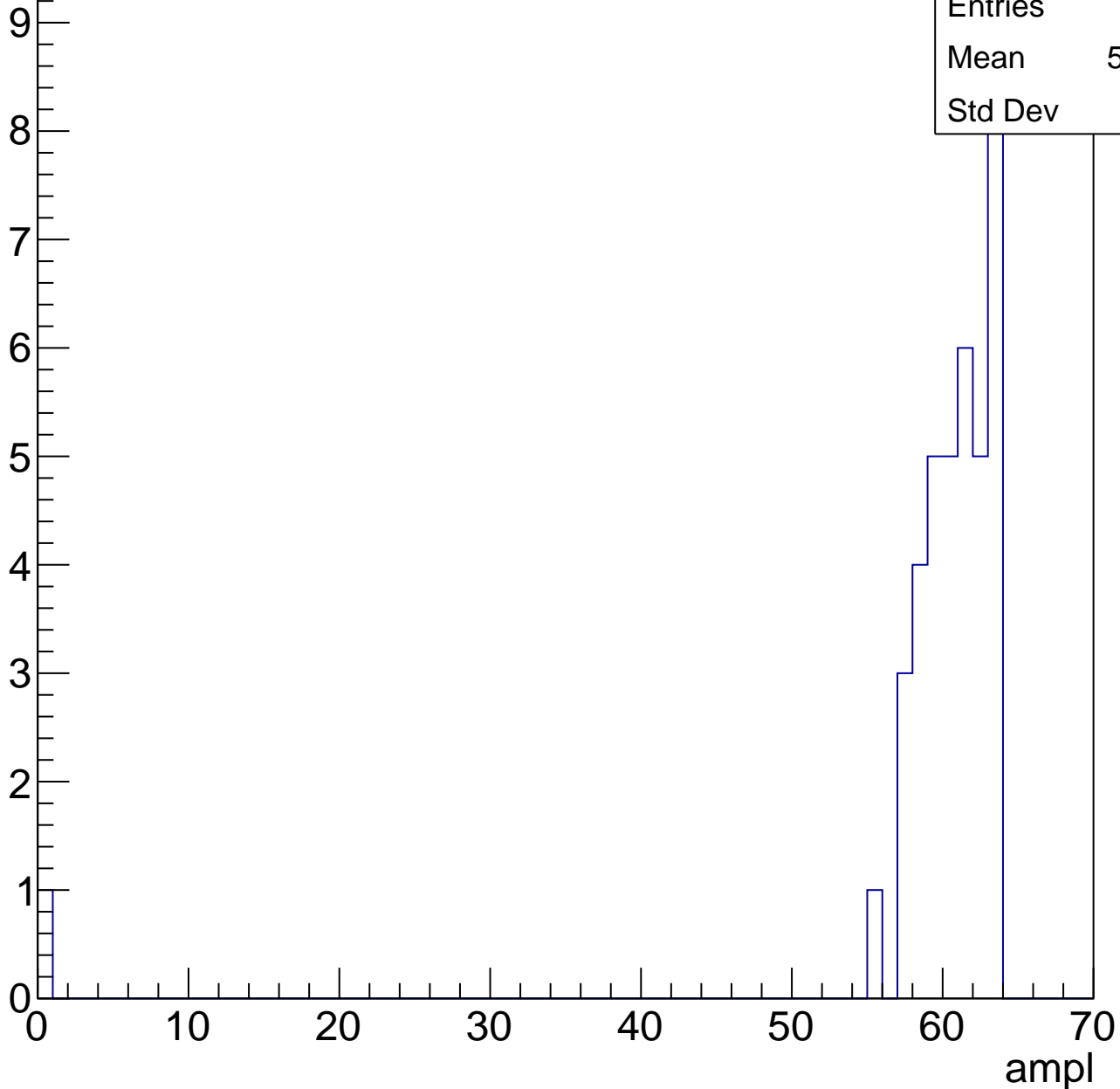


B1L102S, U13-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

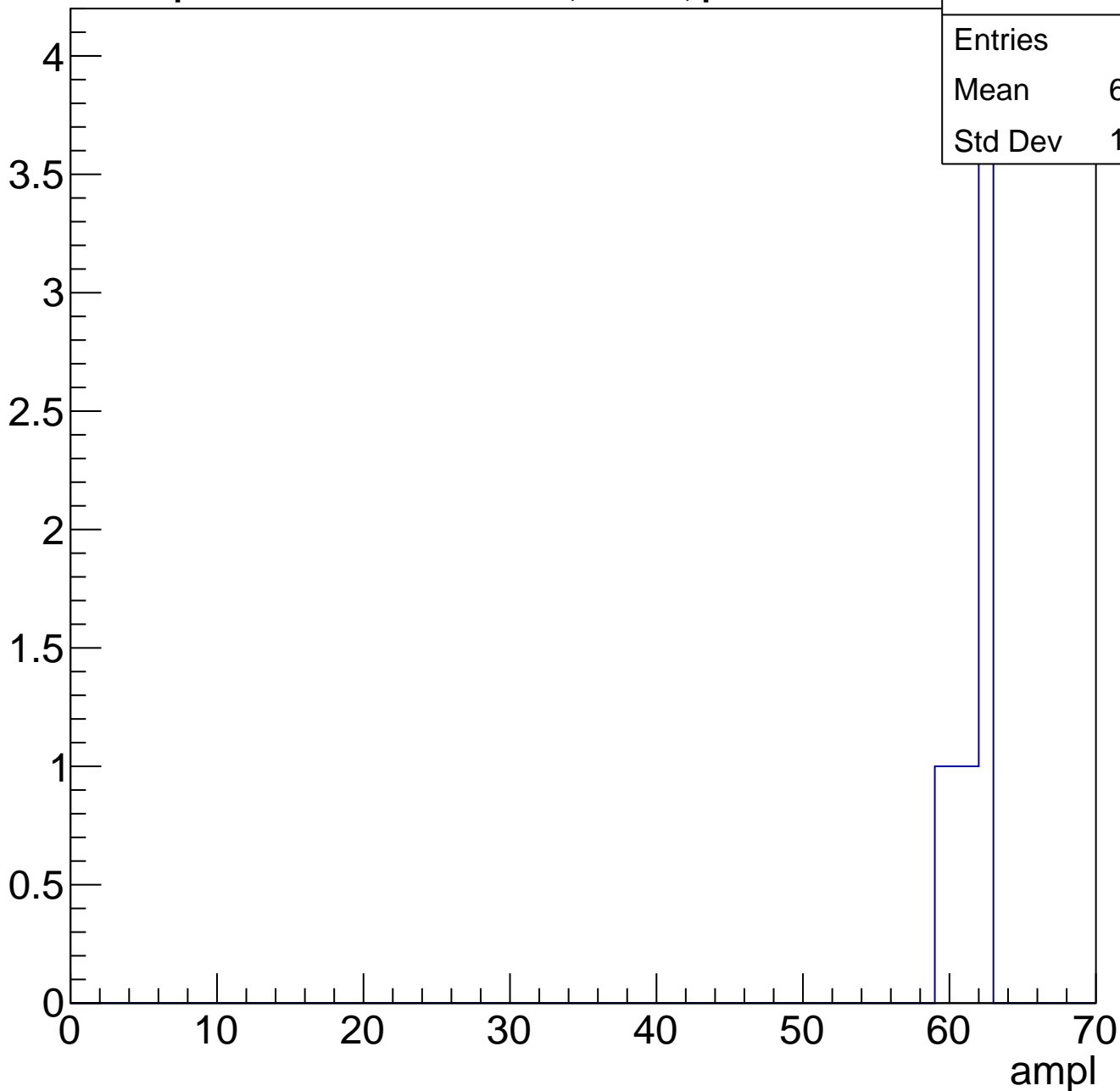
Entries	39
Mean	58.87
Std Dev	9.78



B1L102S, U13-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch20, adc0

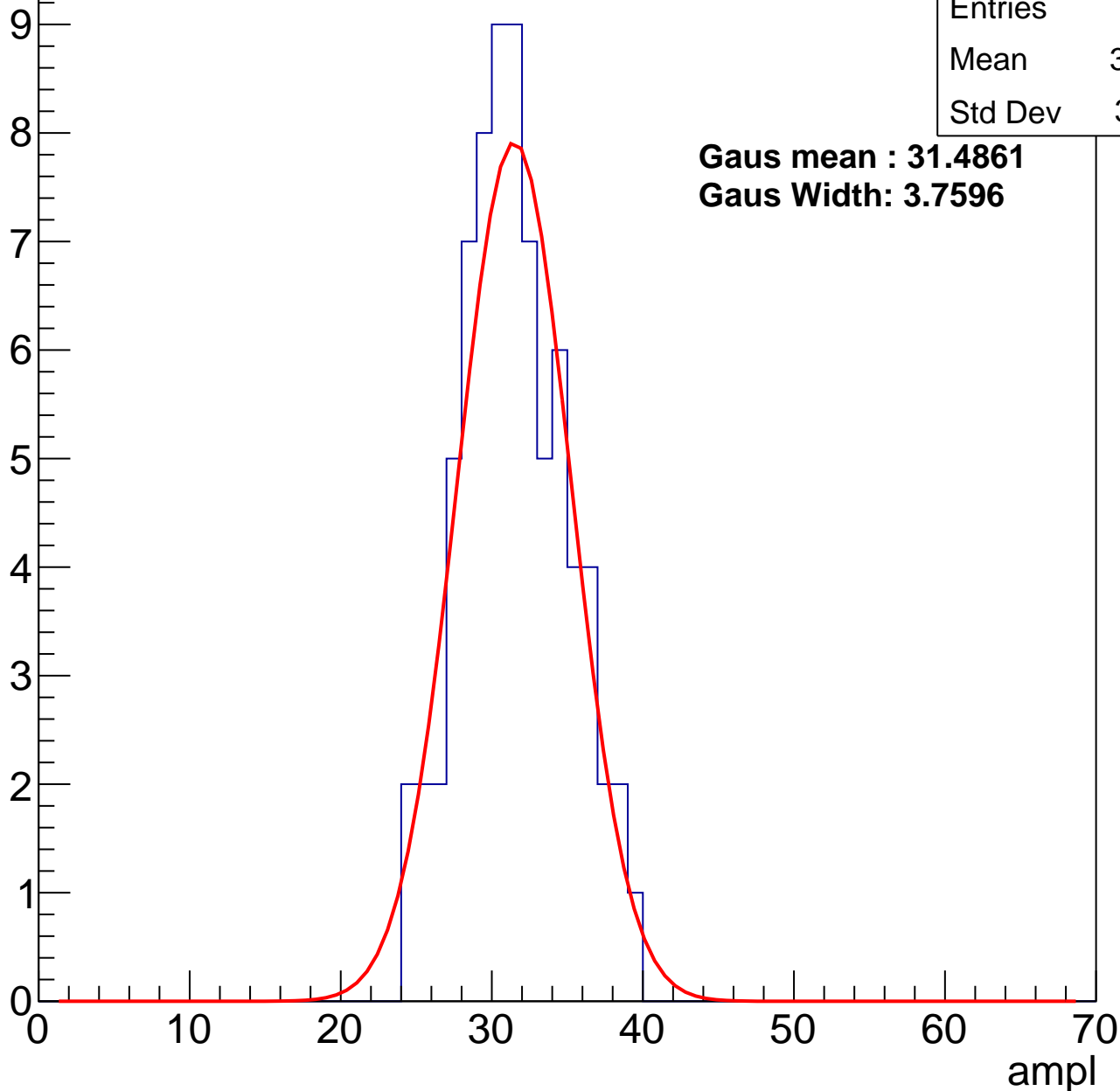
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	31.04
Std Dev	3.431

Gaus mean : 31.4861

Gaus Width: 3.7596



B1L102S, U13-ch20, adc1

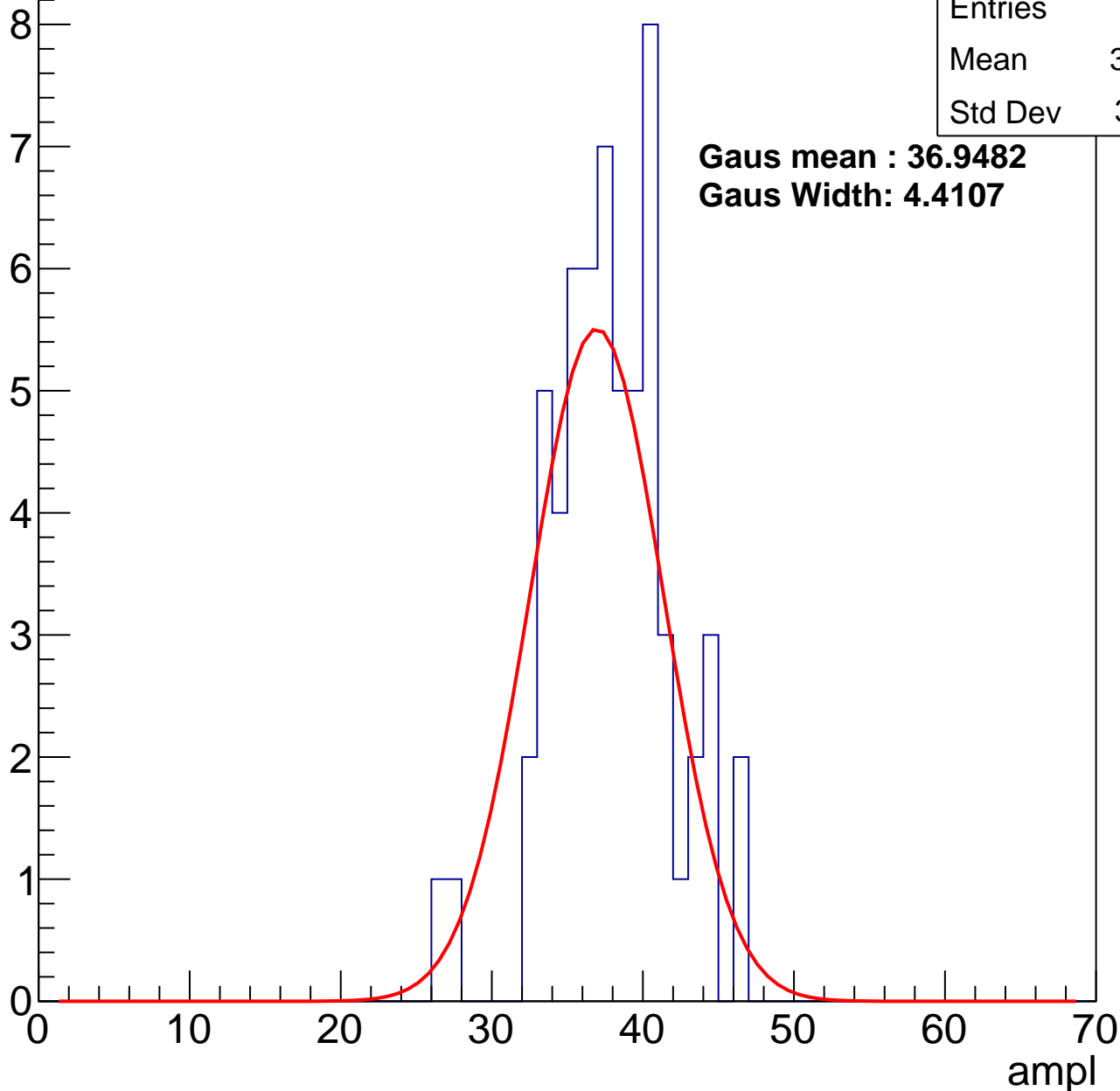
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	37.43
Std Dev	3.961

Gaus mean : 36.9482

Gaus Width: 4.4107



B1L102S, U13-ch20, adc2

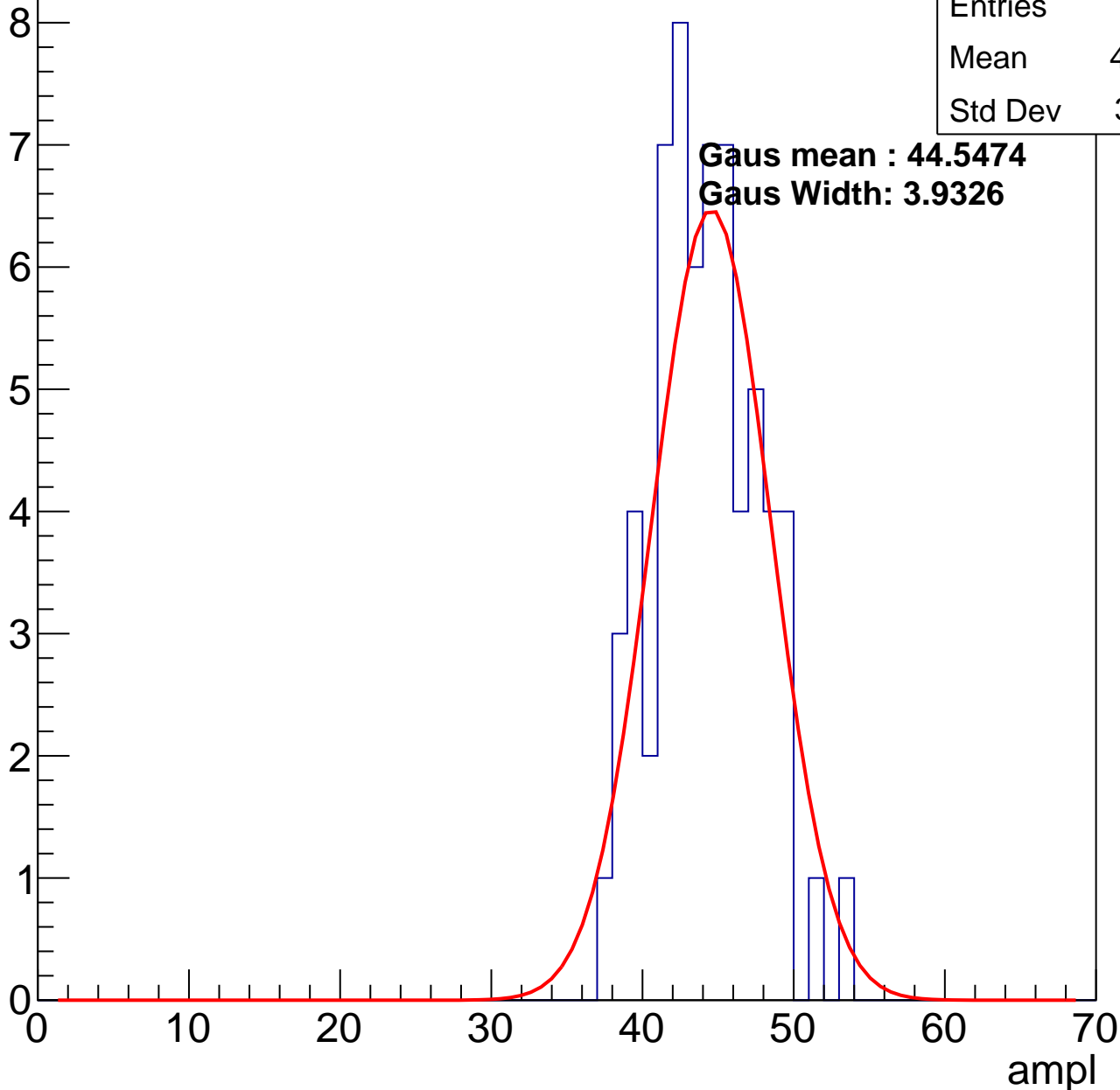
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.78
Std Dev	3.421

Gaus mean : 44.5474

Gaus Width: 3.9326



B1L102S, U13-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

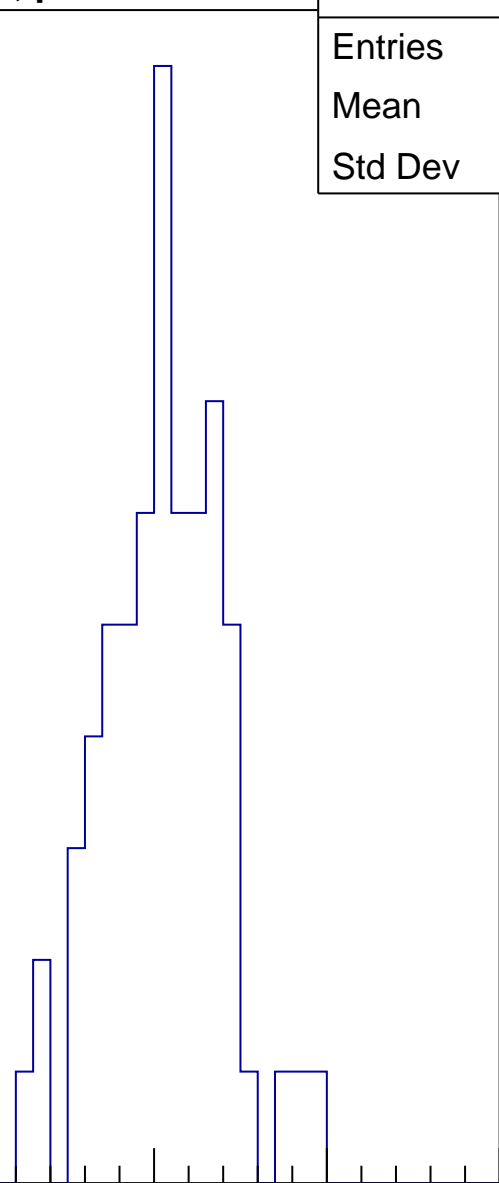
Entries	64
Mean	50.06
Std Dev	3.455

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

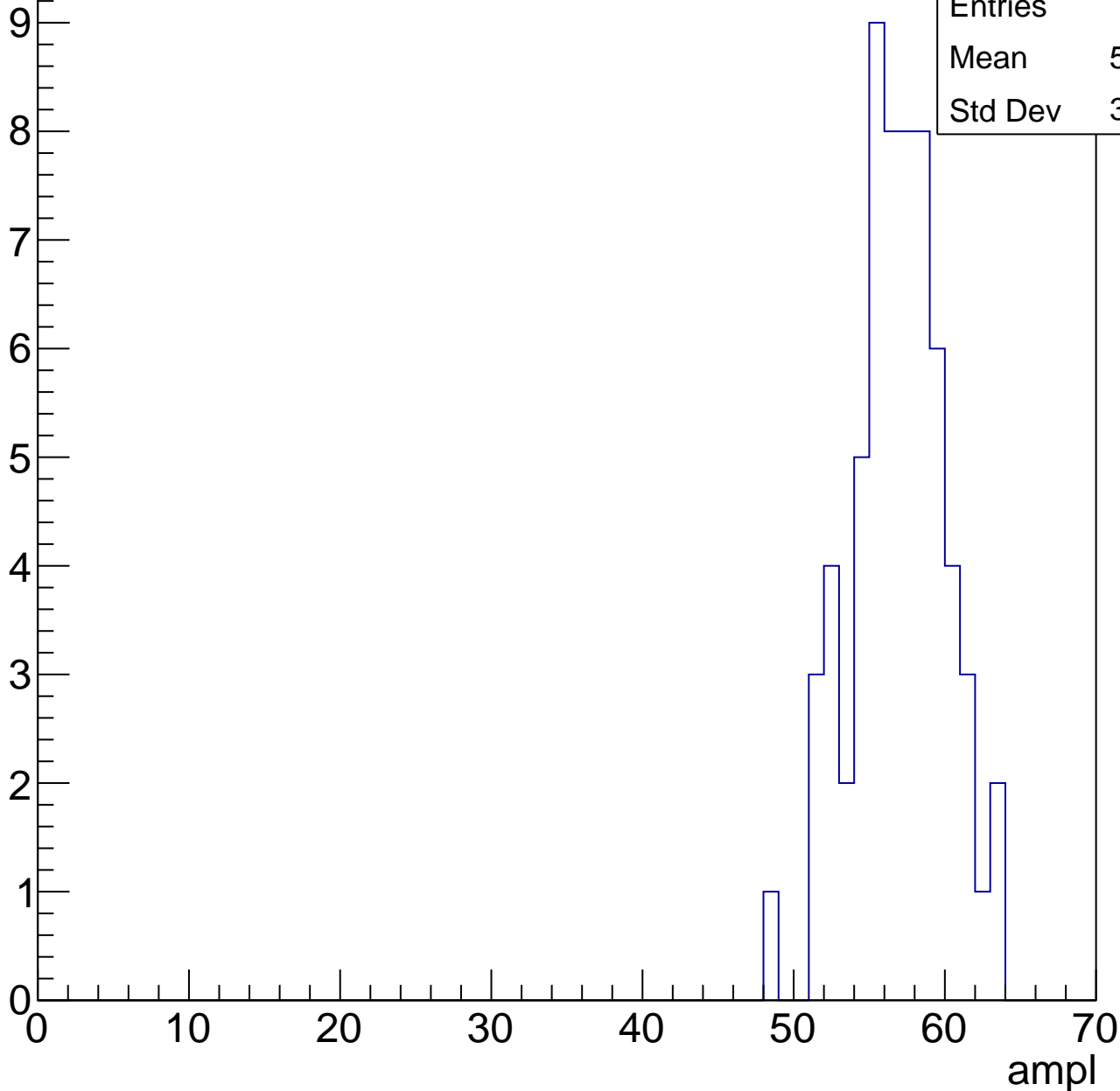


B1L102S, U13-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	56.45
Std Dev	3.072

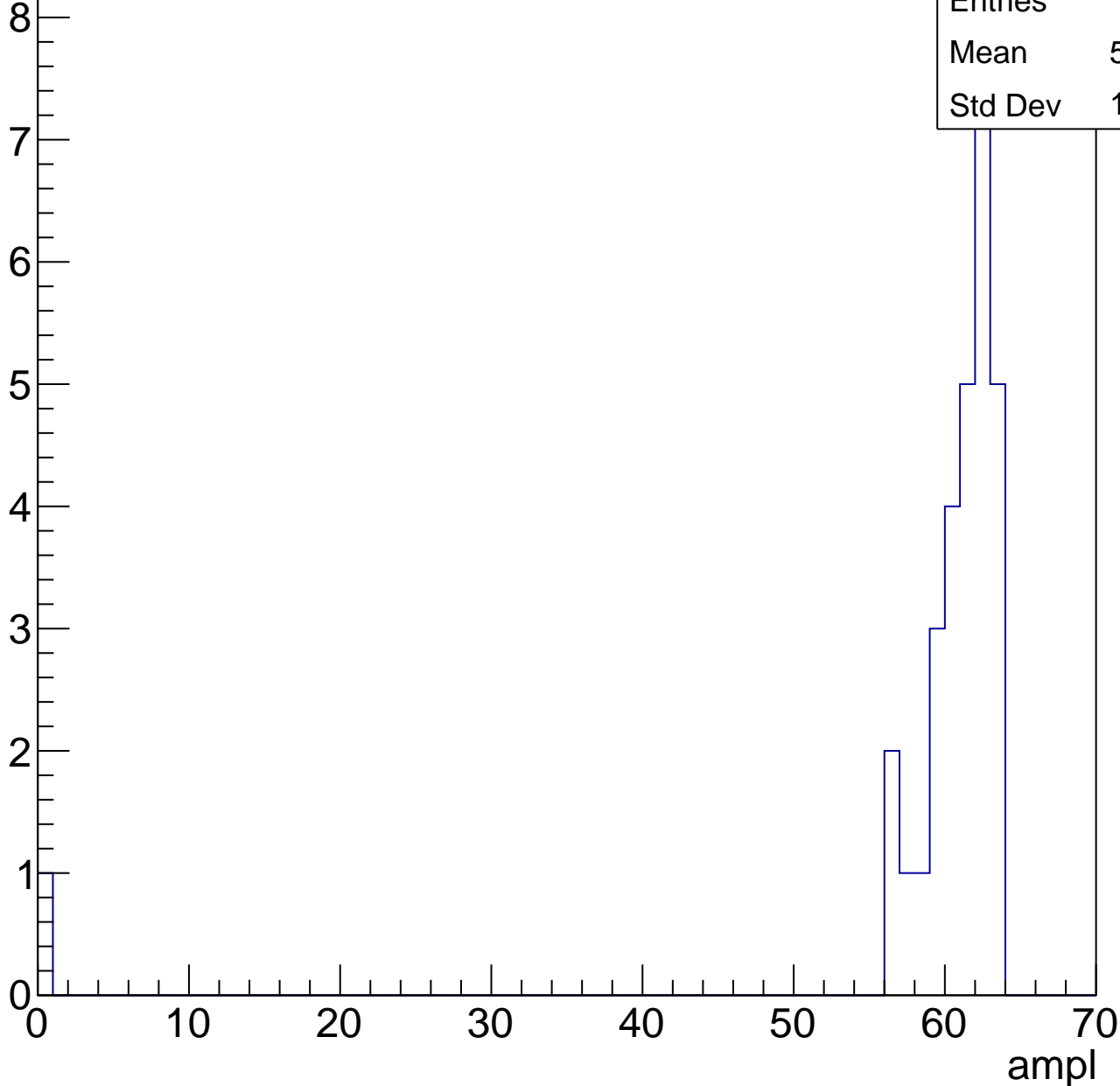


B1L102S, U13-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

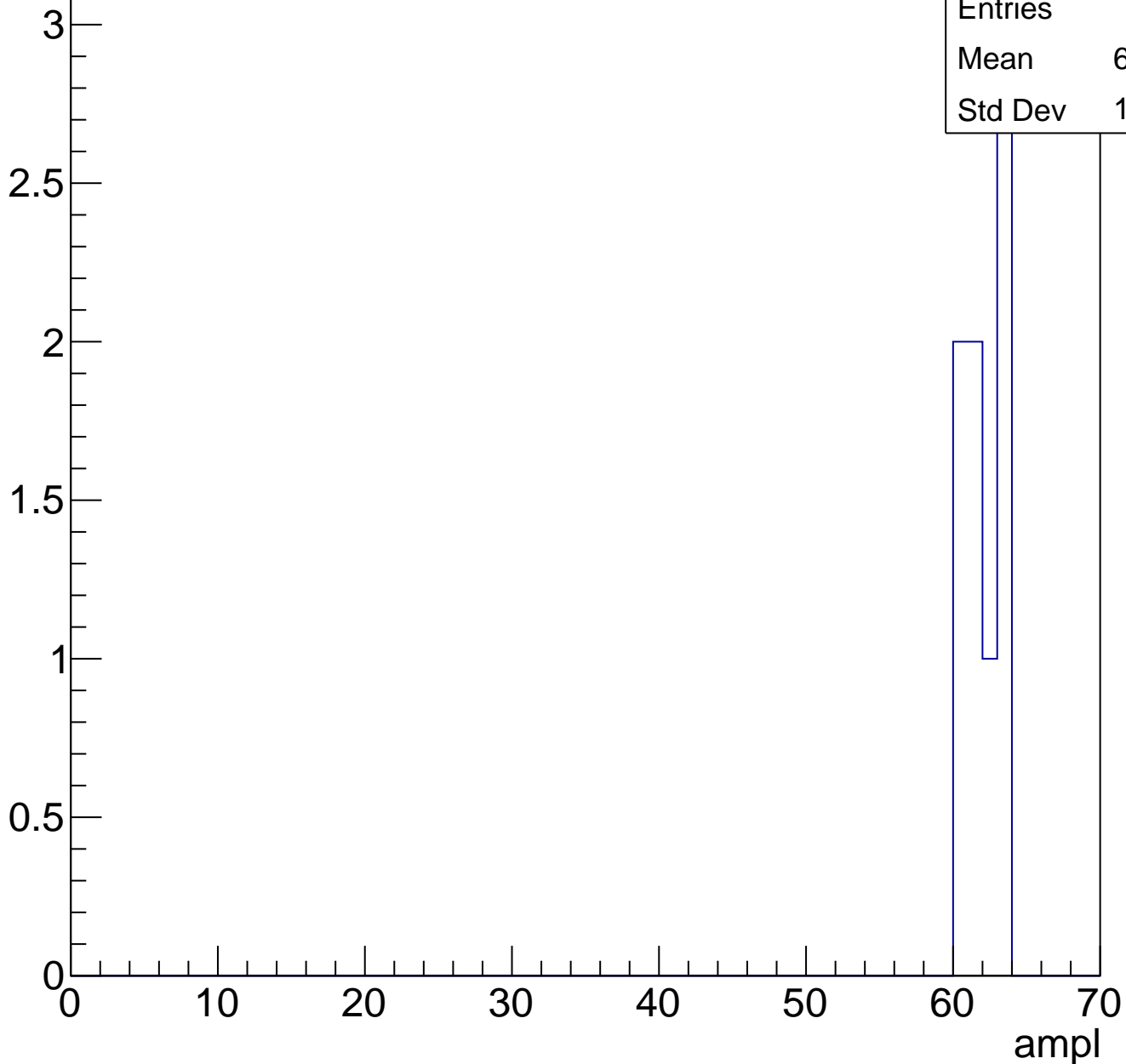
Entries	30
Mean	58.67
Std Dev	11.07



B1L102S, U13-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch21, adc0

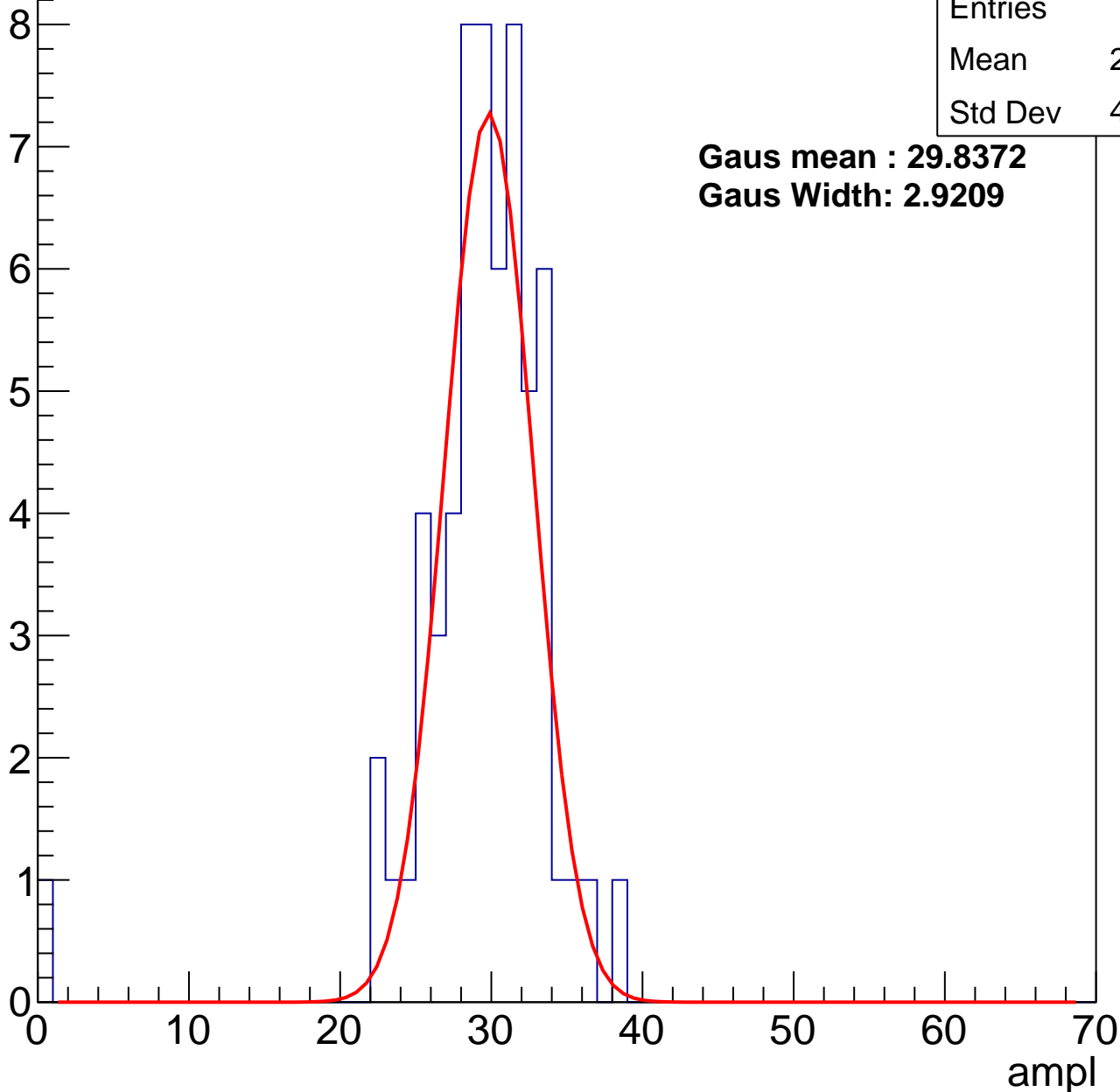
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.89
Std Dev	4.933

Gaus mean : 29.8372

Gaus Width: 2.9209



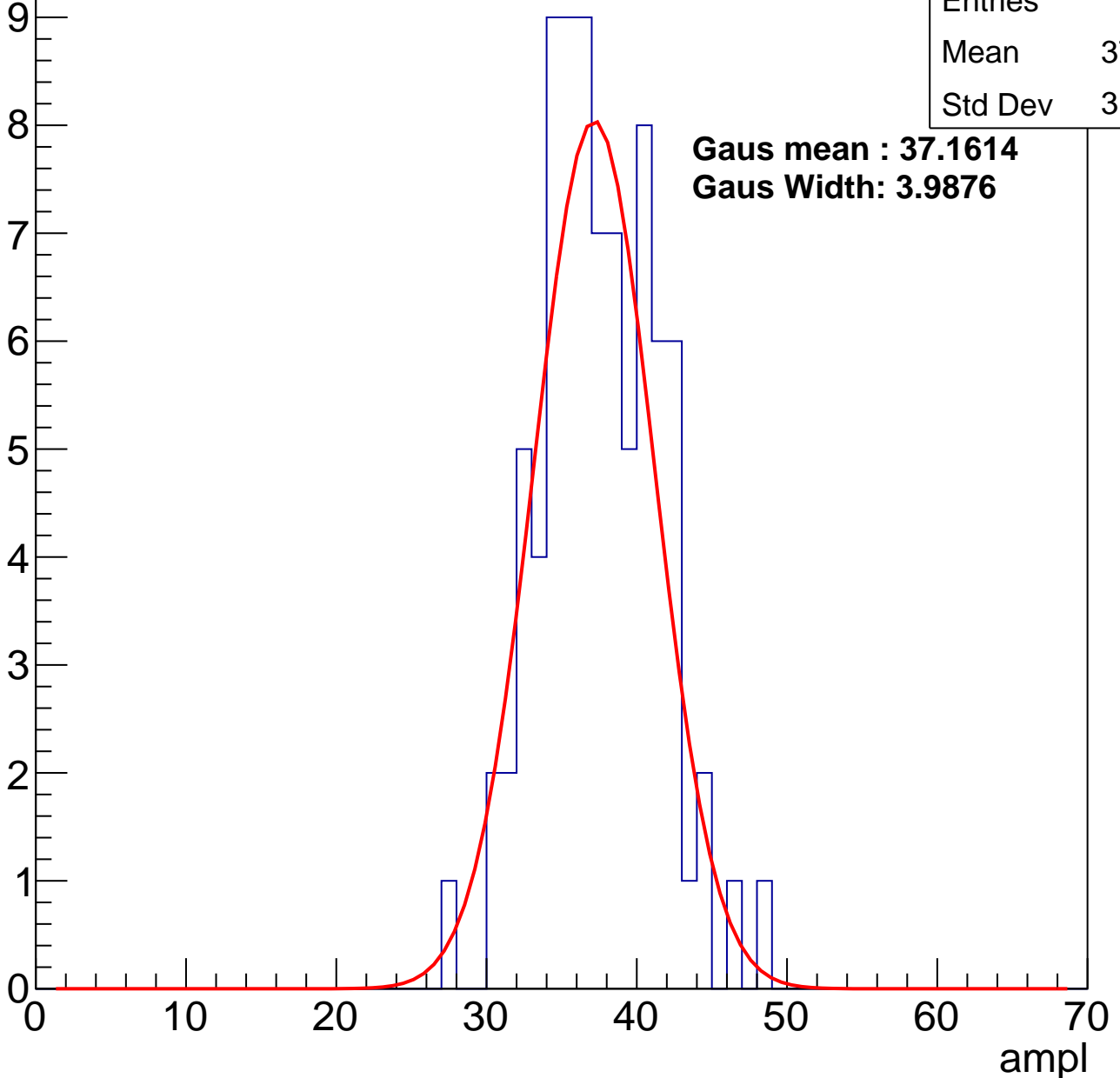
B1L102S, U13-ch21, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	37.05
Std Dev	3.859

Gaus mean : 37.1614
Gaus Width: 3.9876



B1L102S, U13-ch21, adc2

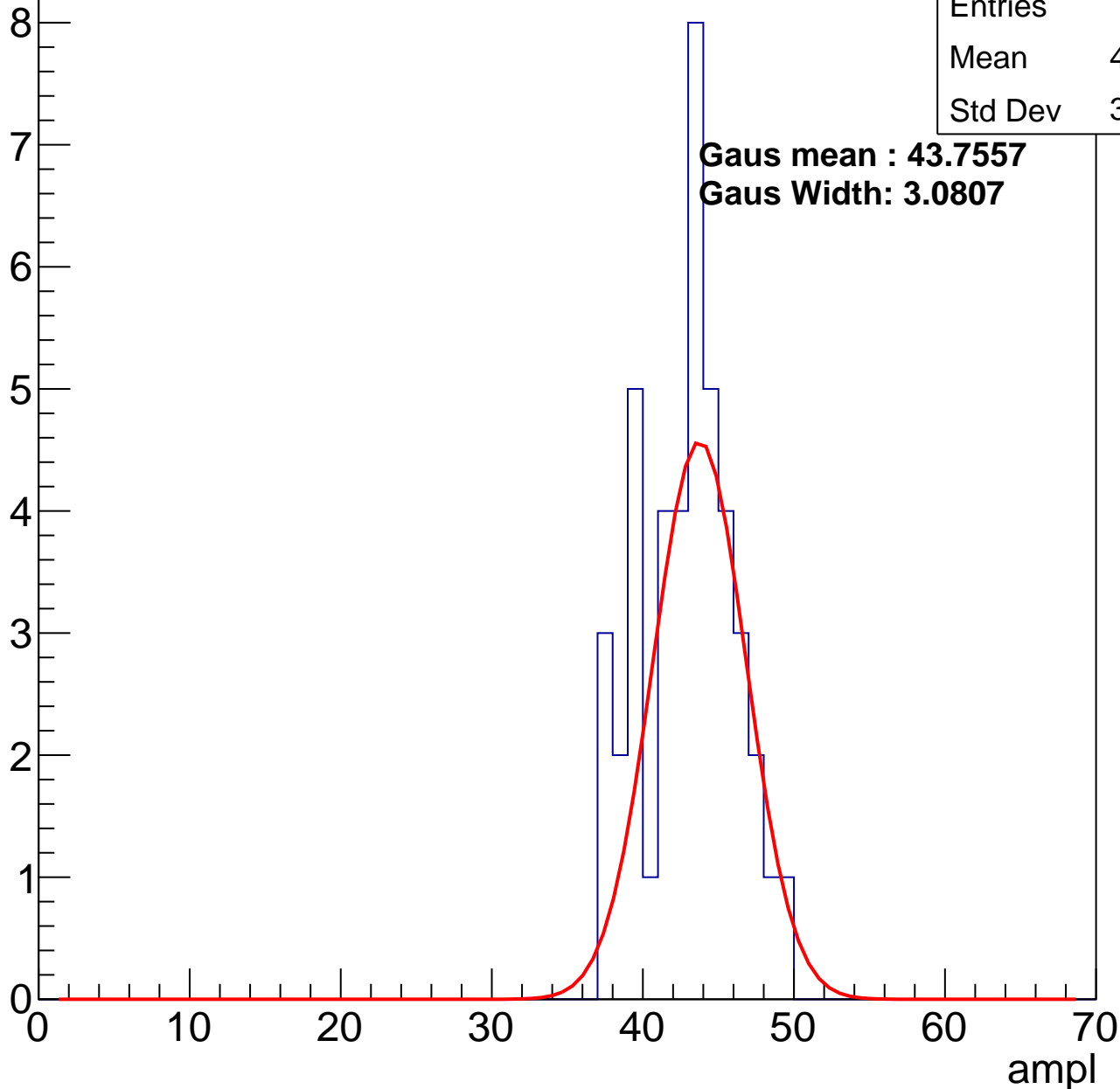
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	42.49
Std Dev	3.038

Gaus mean : 43.7557

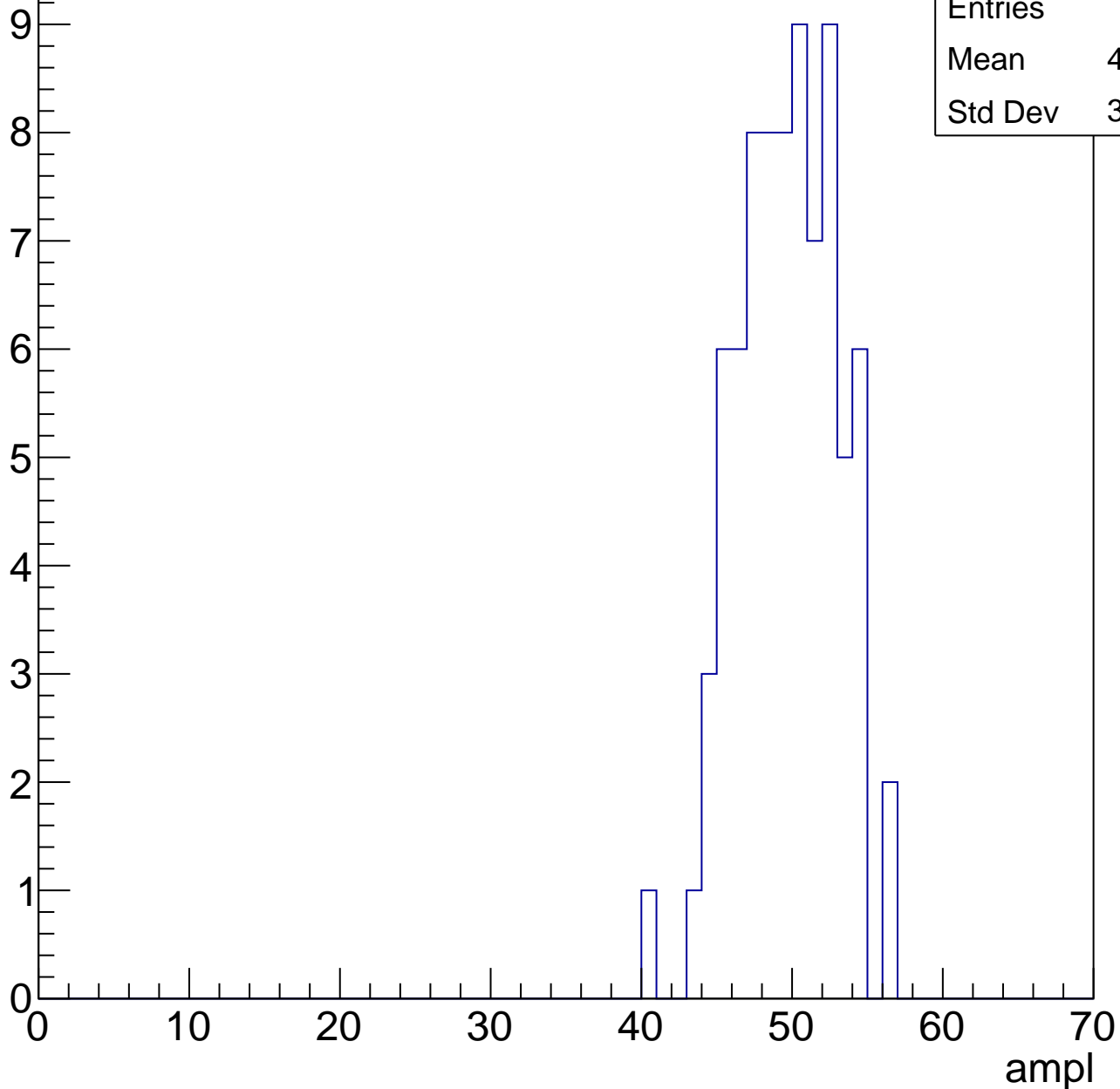
Gaus Width: 3.0807



B1L102S, U13-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

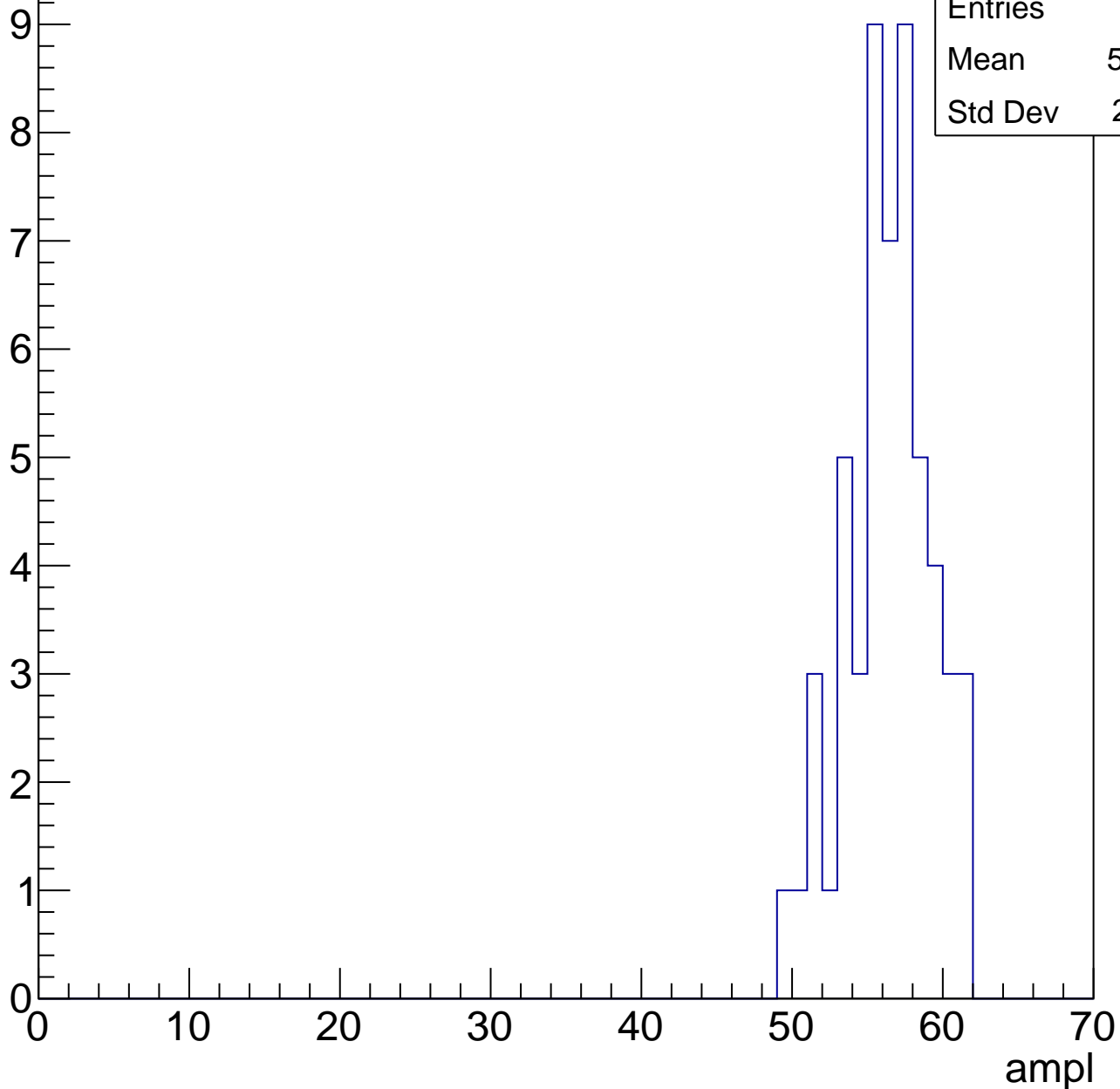


Entries	79
Mean	49.23
Std Dev	3.229

B1L102S, U13-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

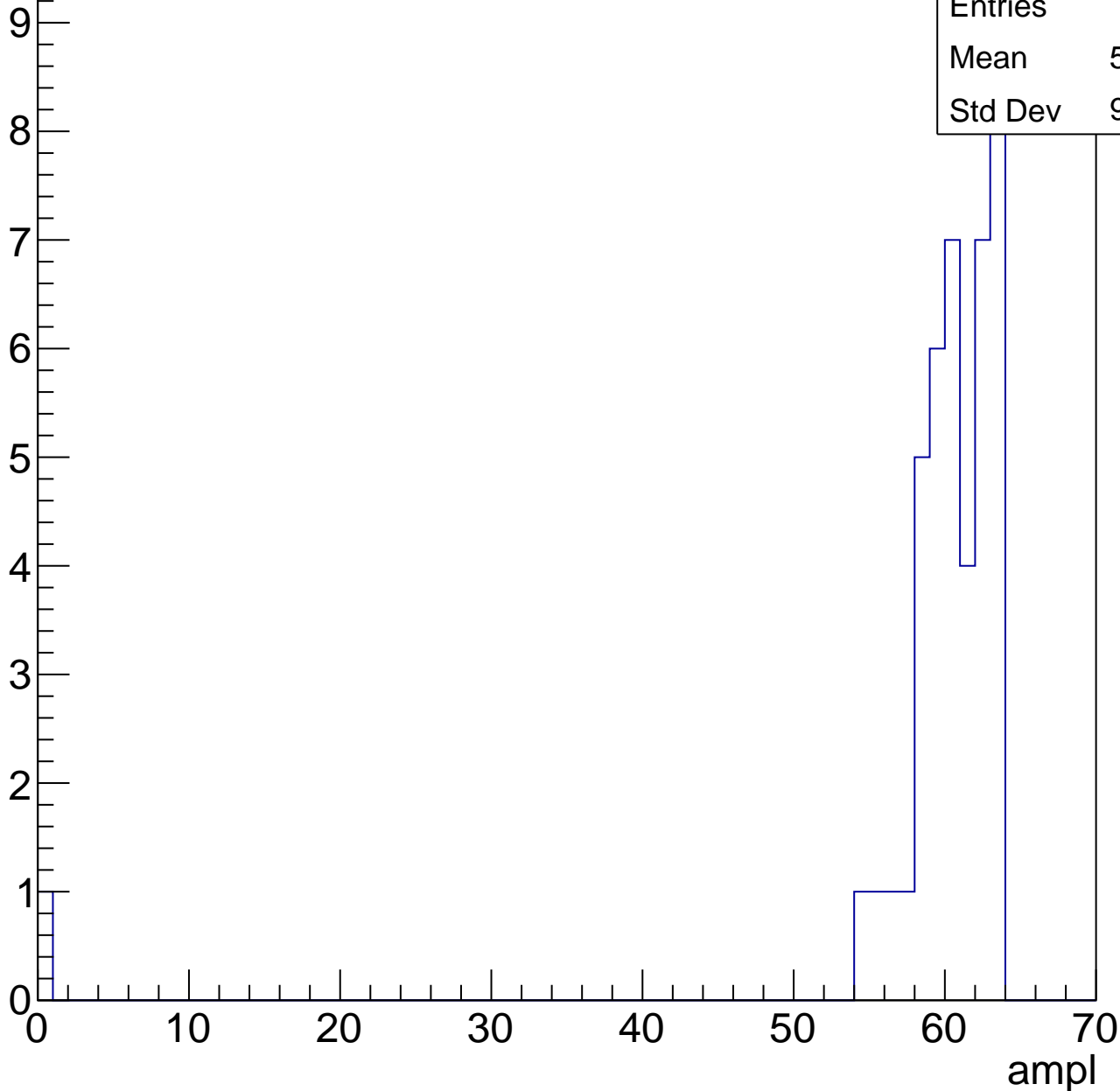


B1L102S, U13-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

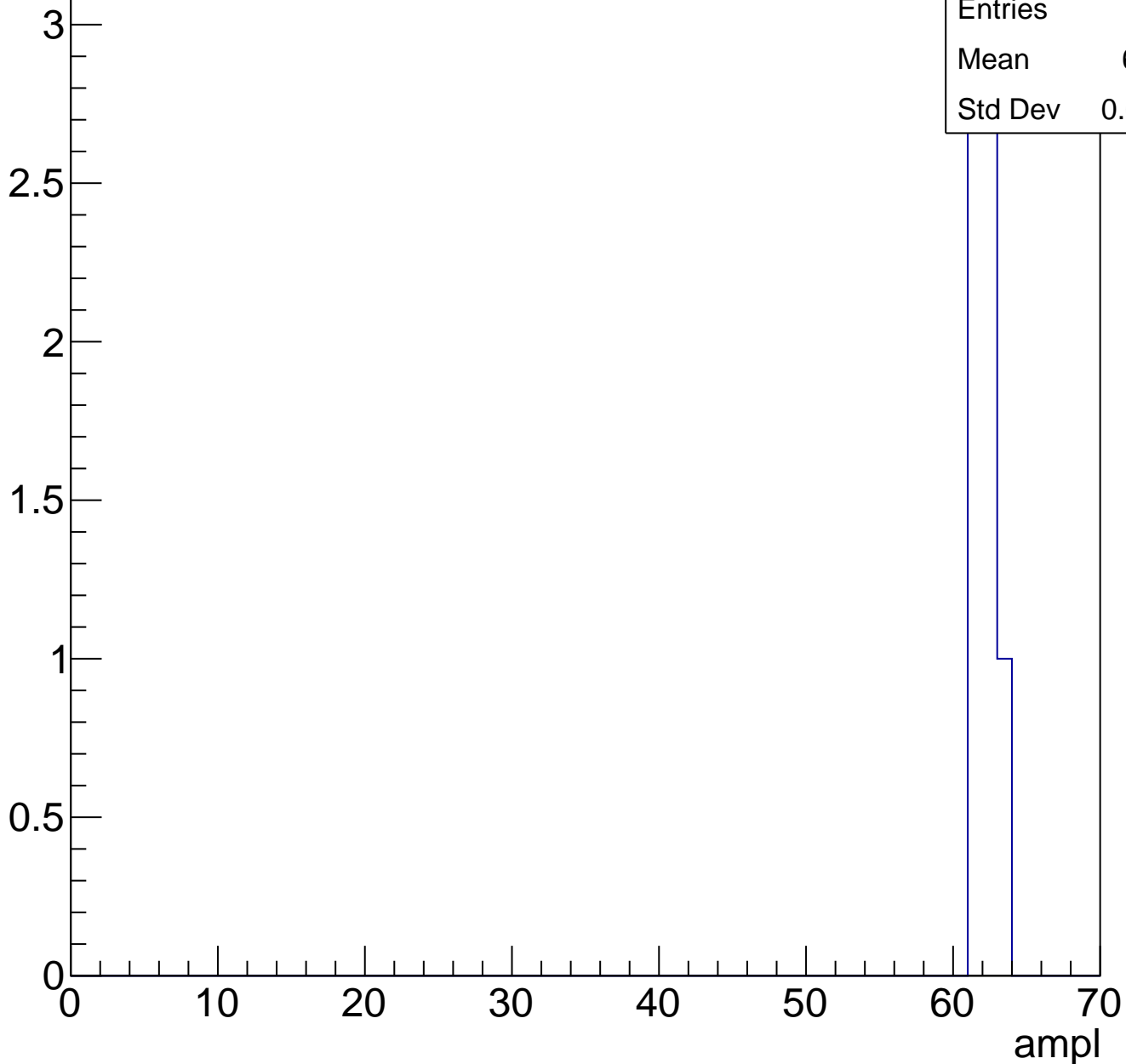
Entries	43
Mean	58.86
Std Dev	9.362



B1L102S, U13-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch22, adc0

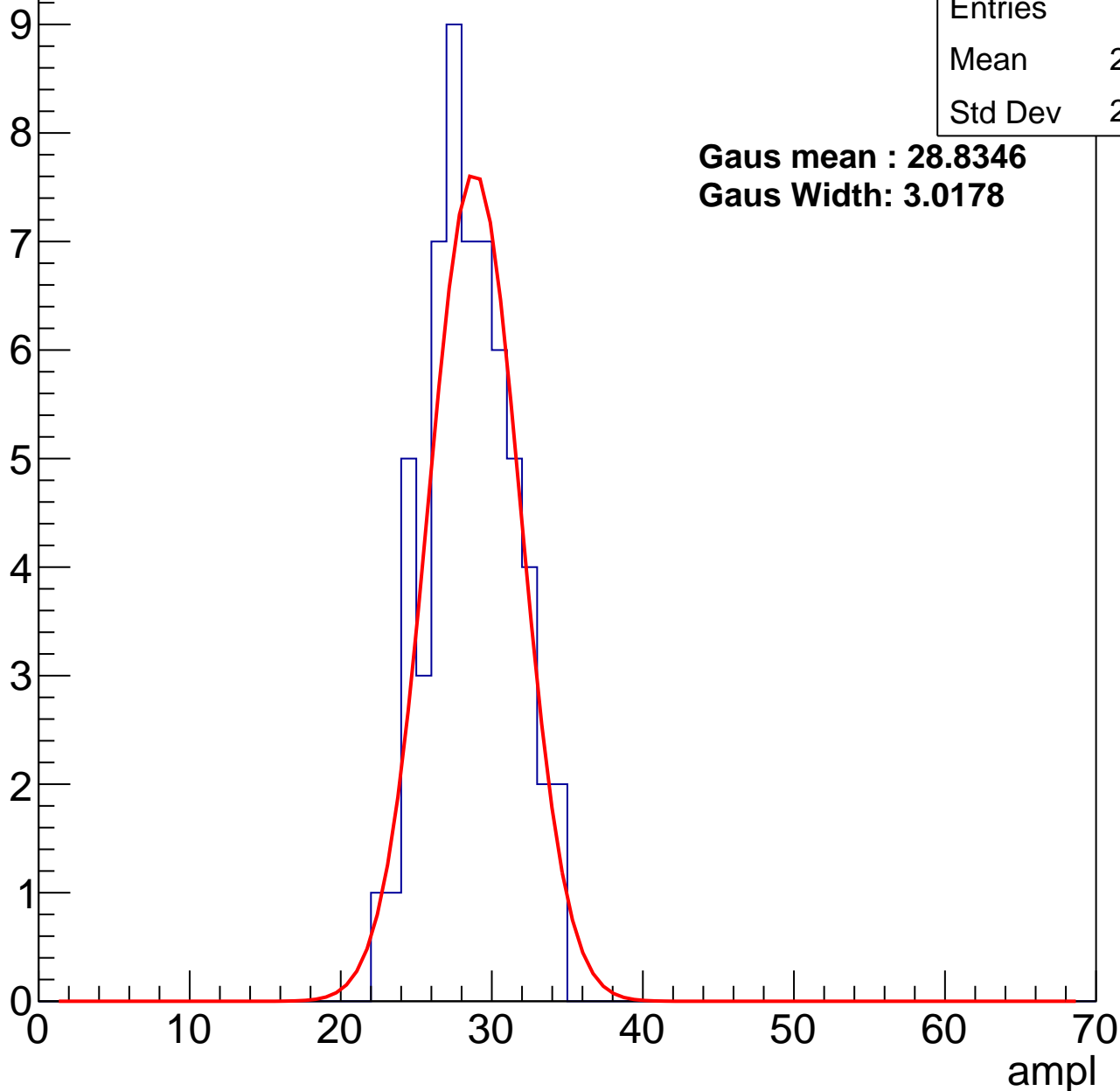
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.15
Std Dev	2.809

Gaus mean : 28.8346

Gaus Width: 3.0178



B1L102S, U13-ch22, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	34.85
Std Dev	3.498

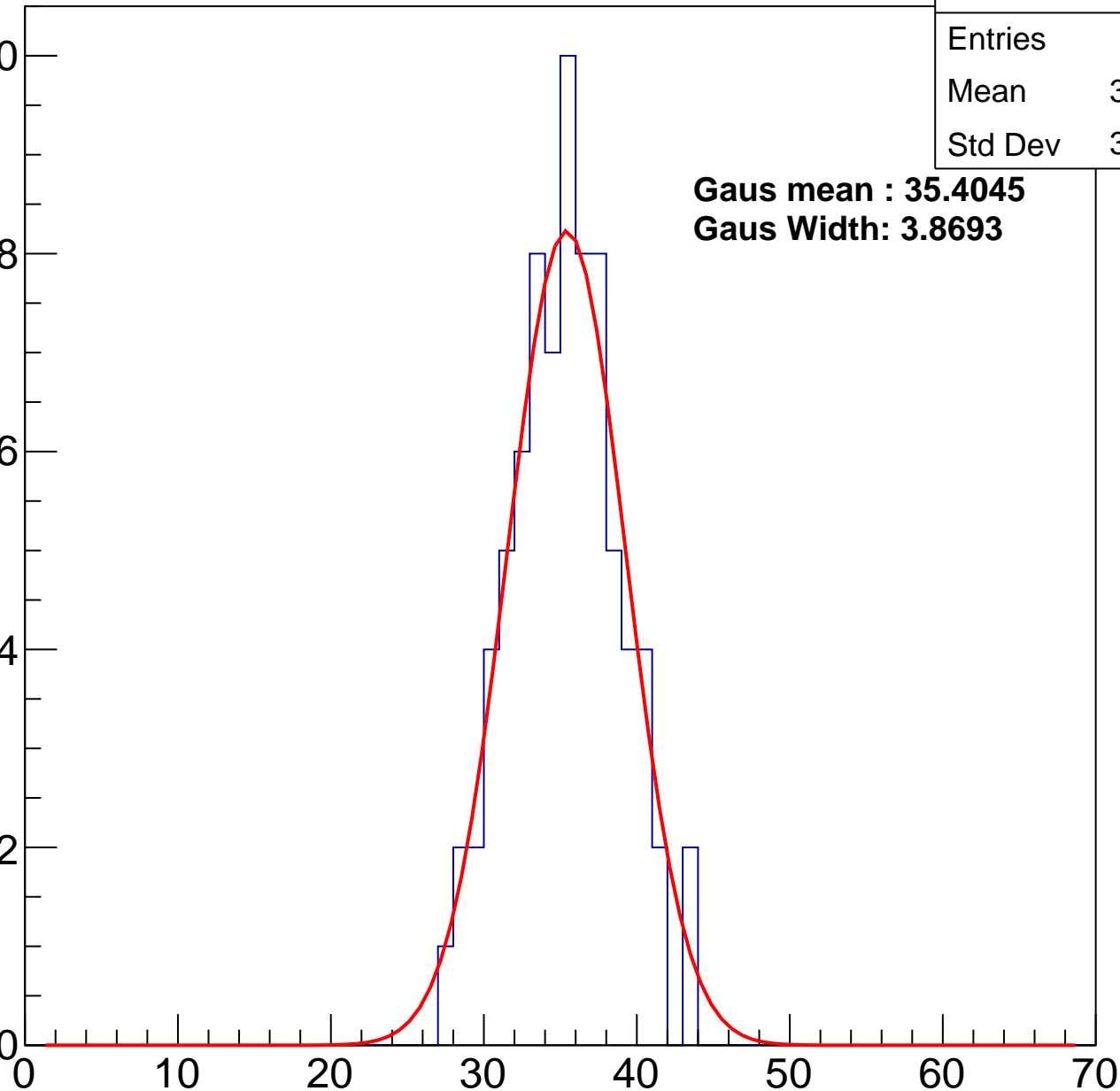
Gaus mean : 35.4045

Gaus Width: 3.8693

Entry

10
8
6
4
2
0

ampl



B1L102S, U13-ch22, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.1
Std Dev	3.406

Gaus mean : 42.4486

Gaus Width: 3.5055

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl



B1L102S, U13-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	49.03
Std Dev	3.2

Entry

10

8

6

4

2

0

0

10

20

30

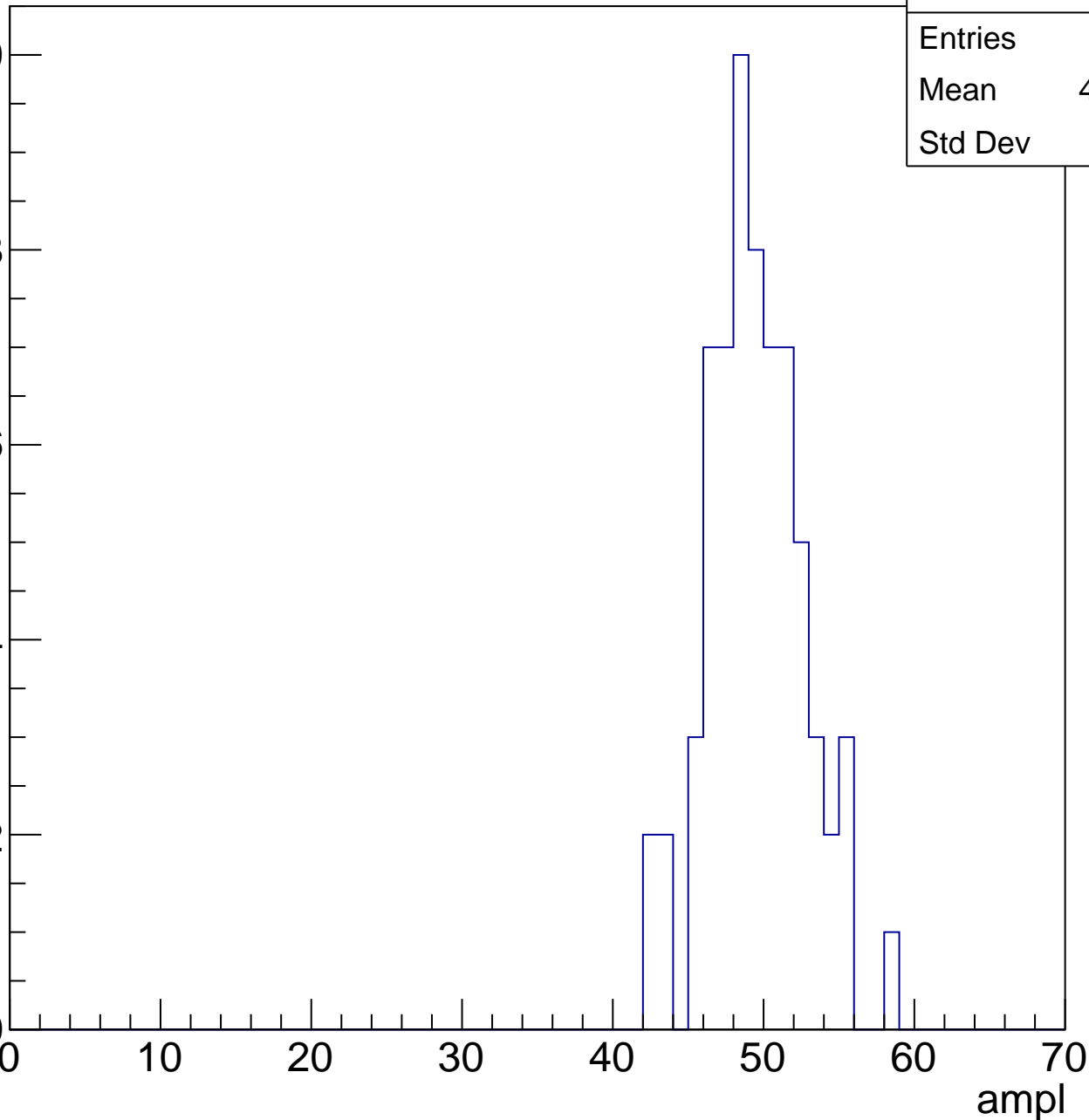
40

50

60

70

ampl

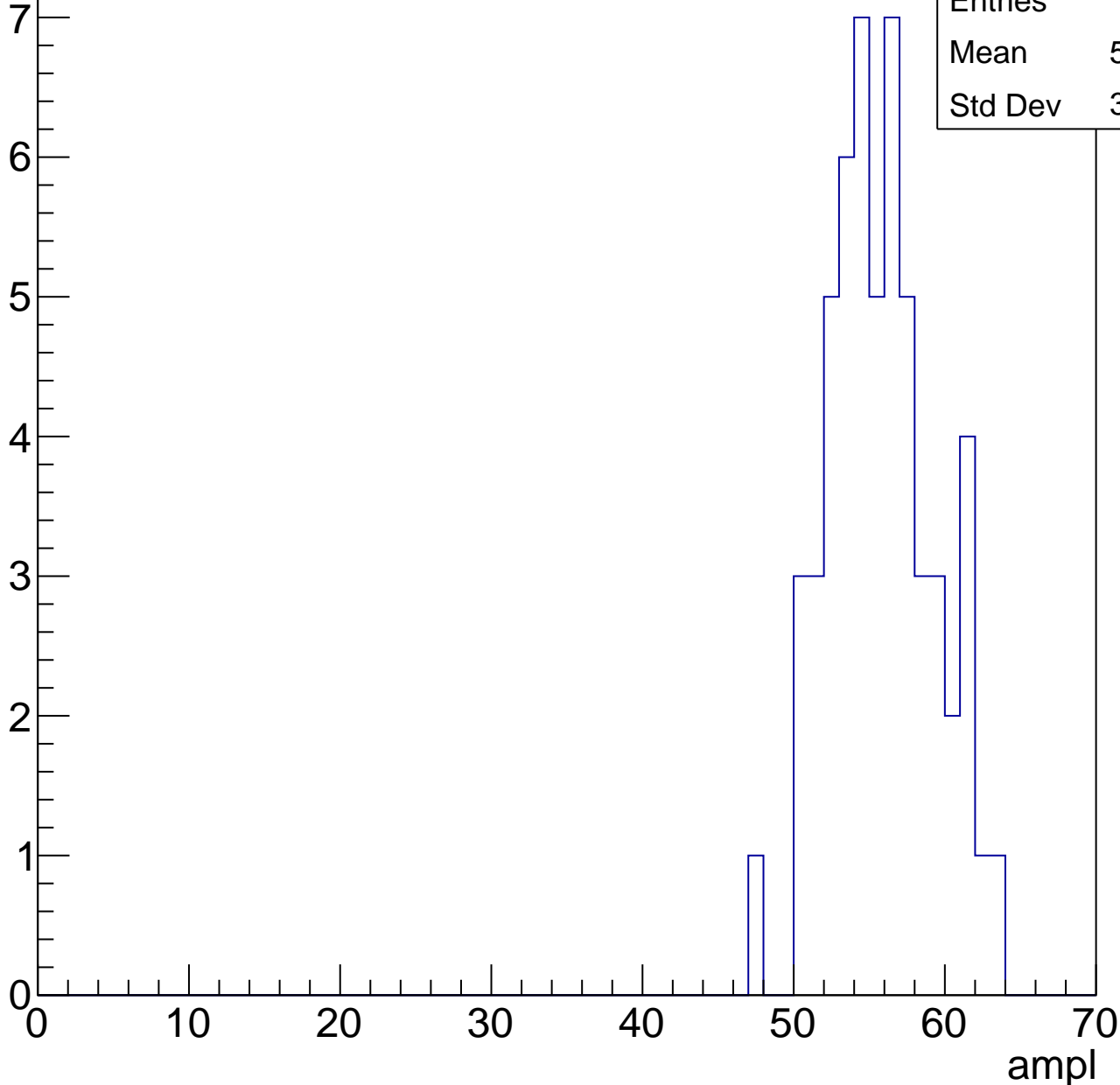


B1L102S, U13-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.32
Std Dev	3.449

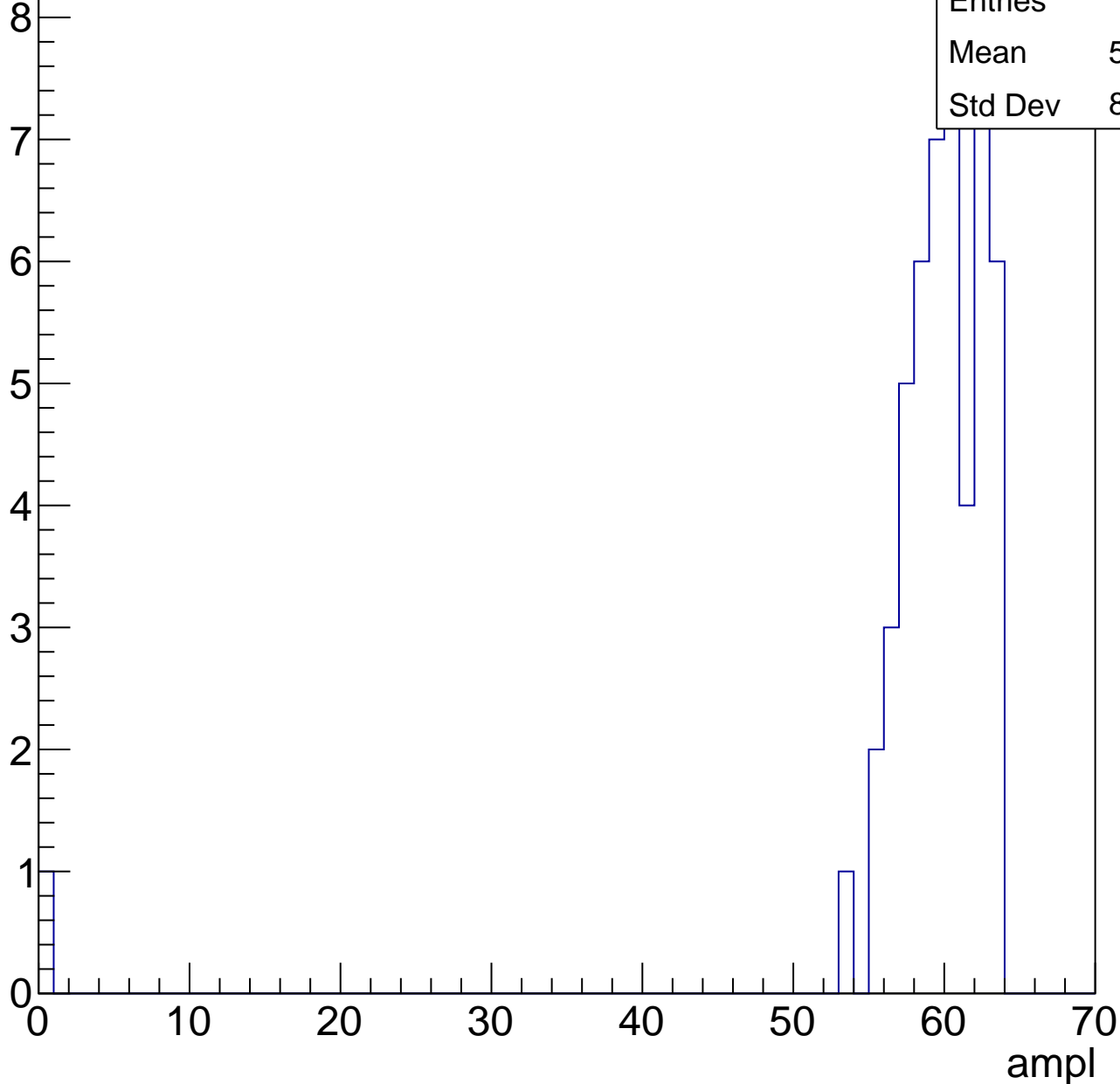


B1L102S, U13-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.33
Std Dev	8.599



B1L102S, U13-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

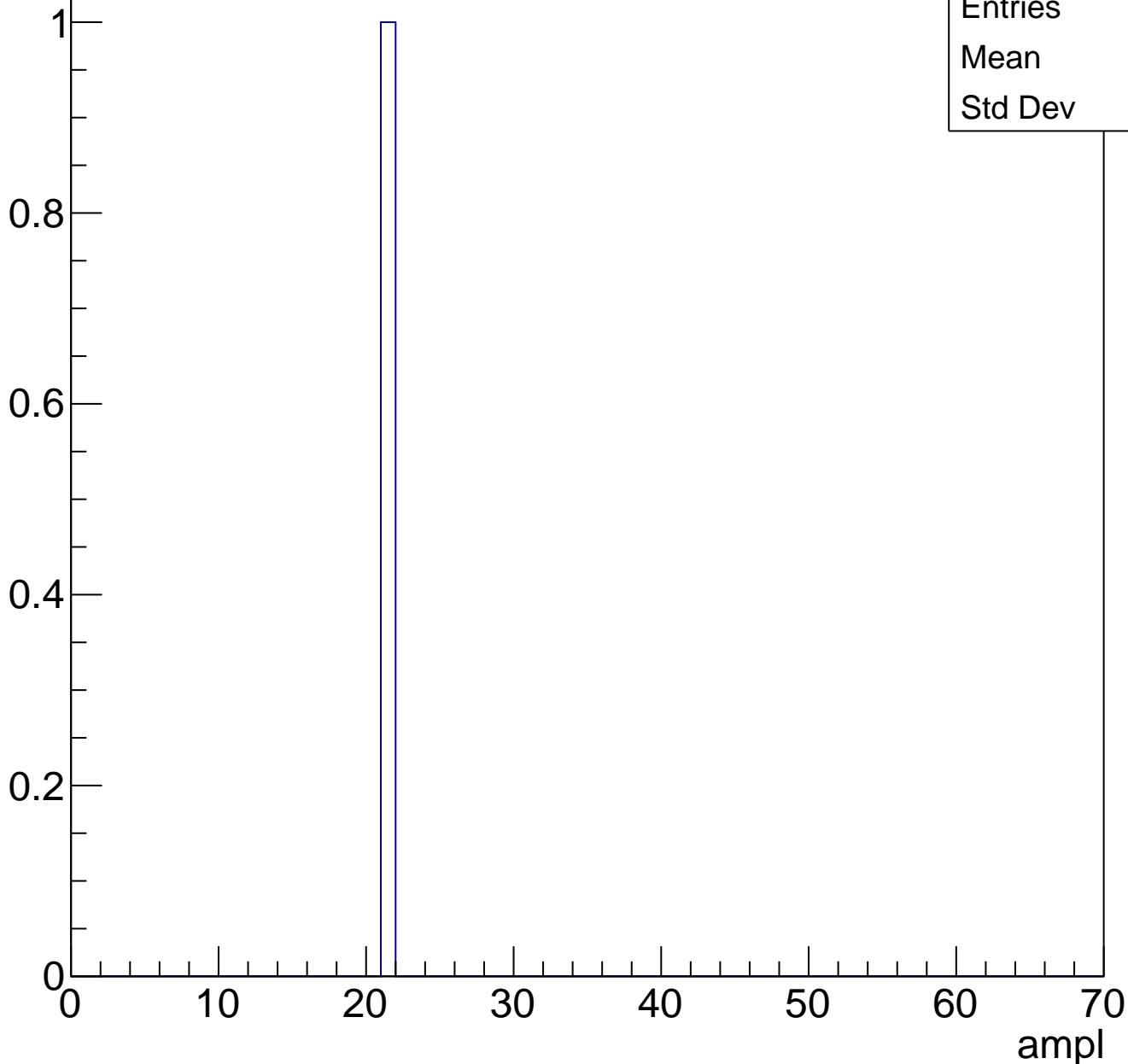
Entry



B1L102S, U13-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch23, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	28.3
Std Dev	2.966

Gaus mean : 29.2146

Gaus Width: 2.7873

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L102S, U13-ch23, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	35.76
Std Dev	3.506

Gaus mean : 35.9928

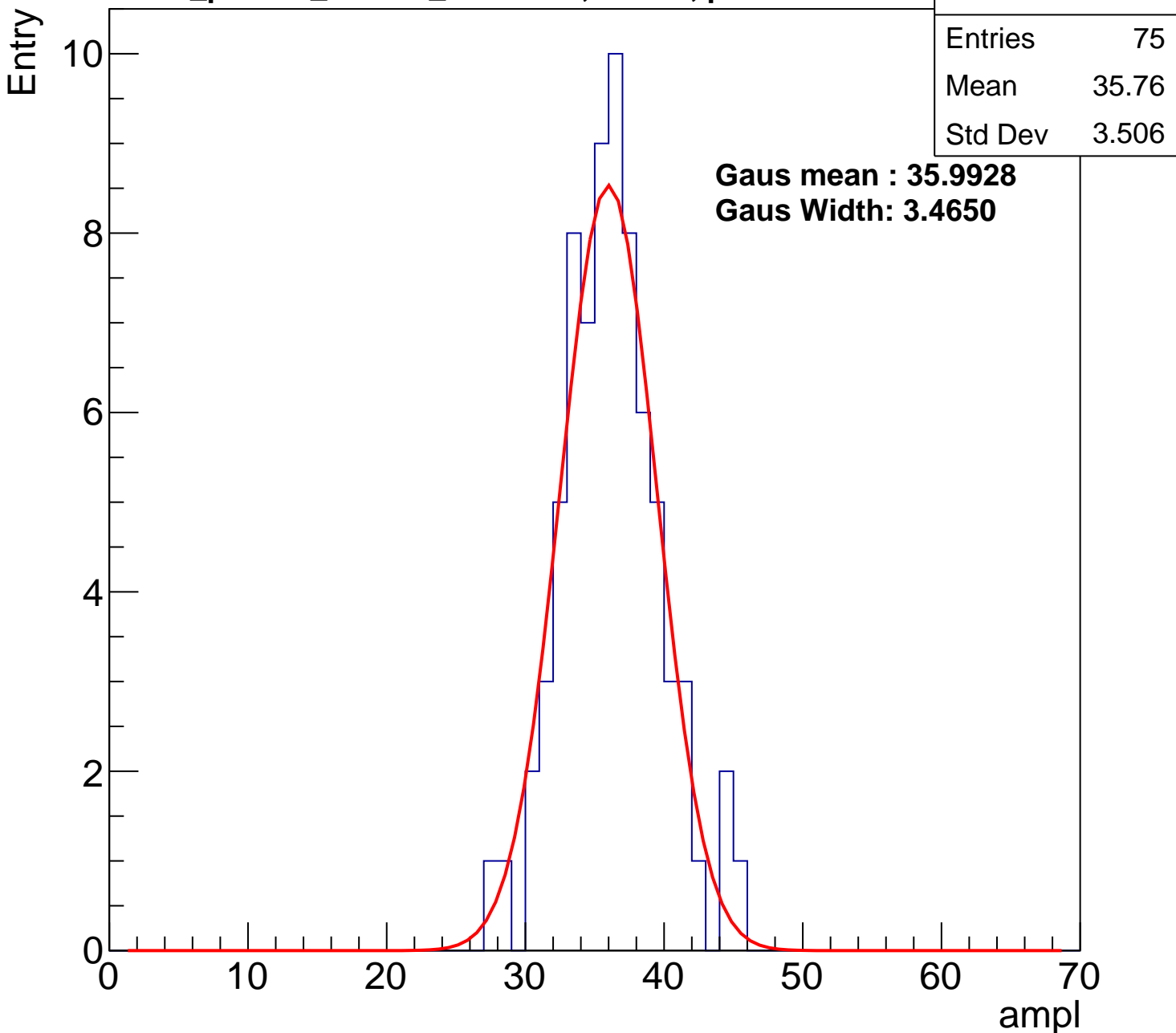
Gaus Width: 3.4650

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U13-ch23, adc2

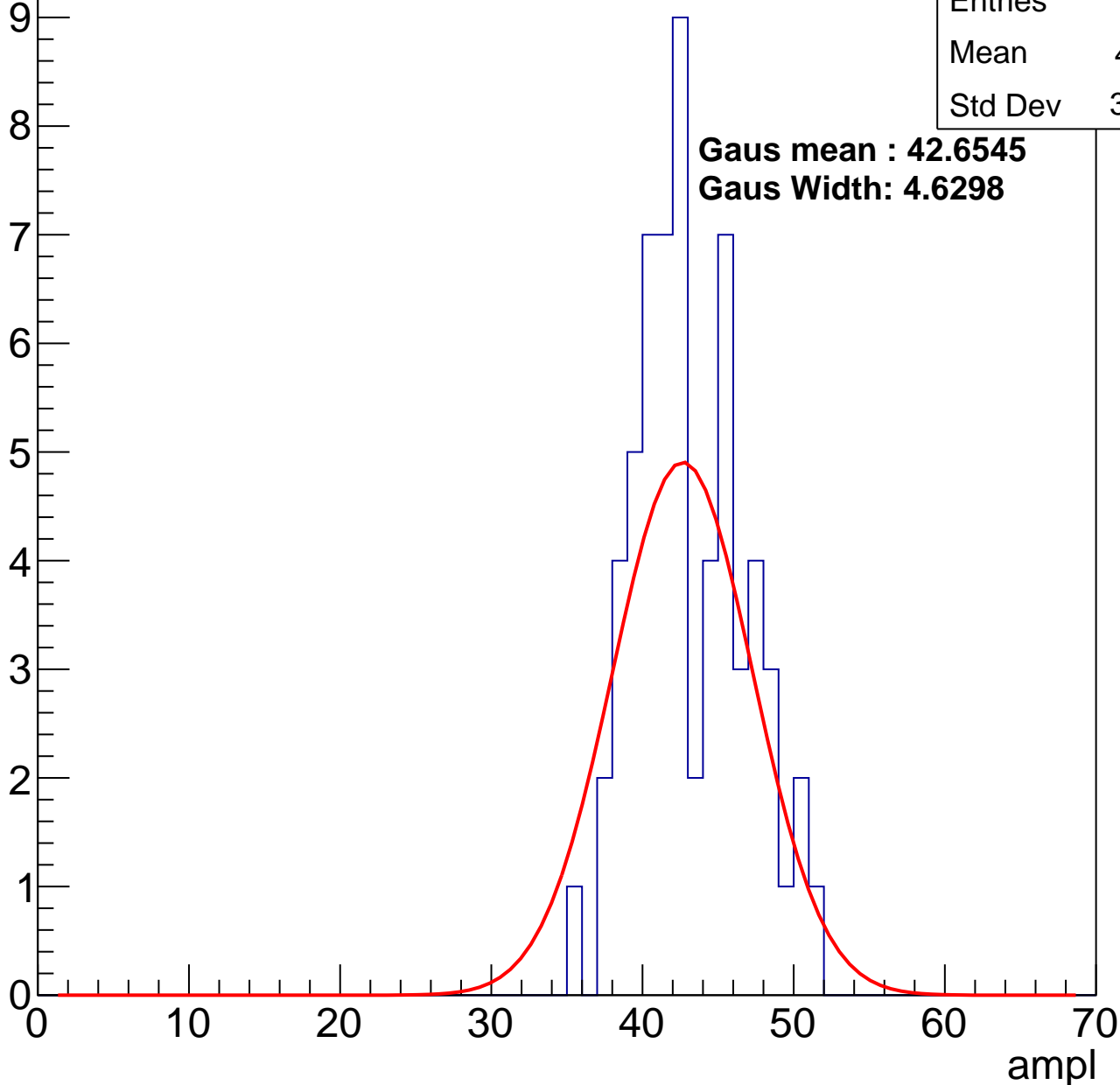
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.71
Std Dev	3.607

Gaus mean : 42.6545

Gaus Width: 4.6298

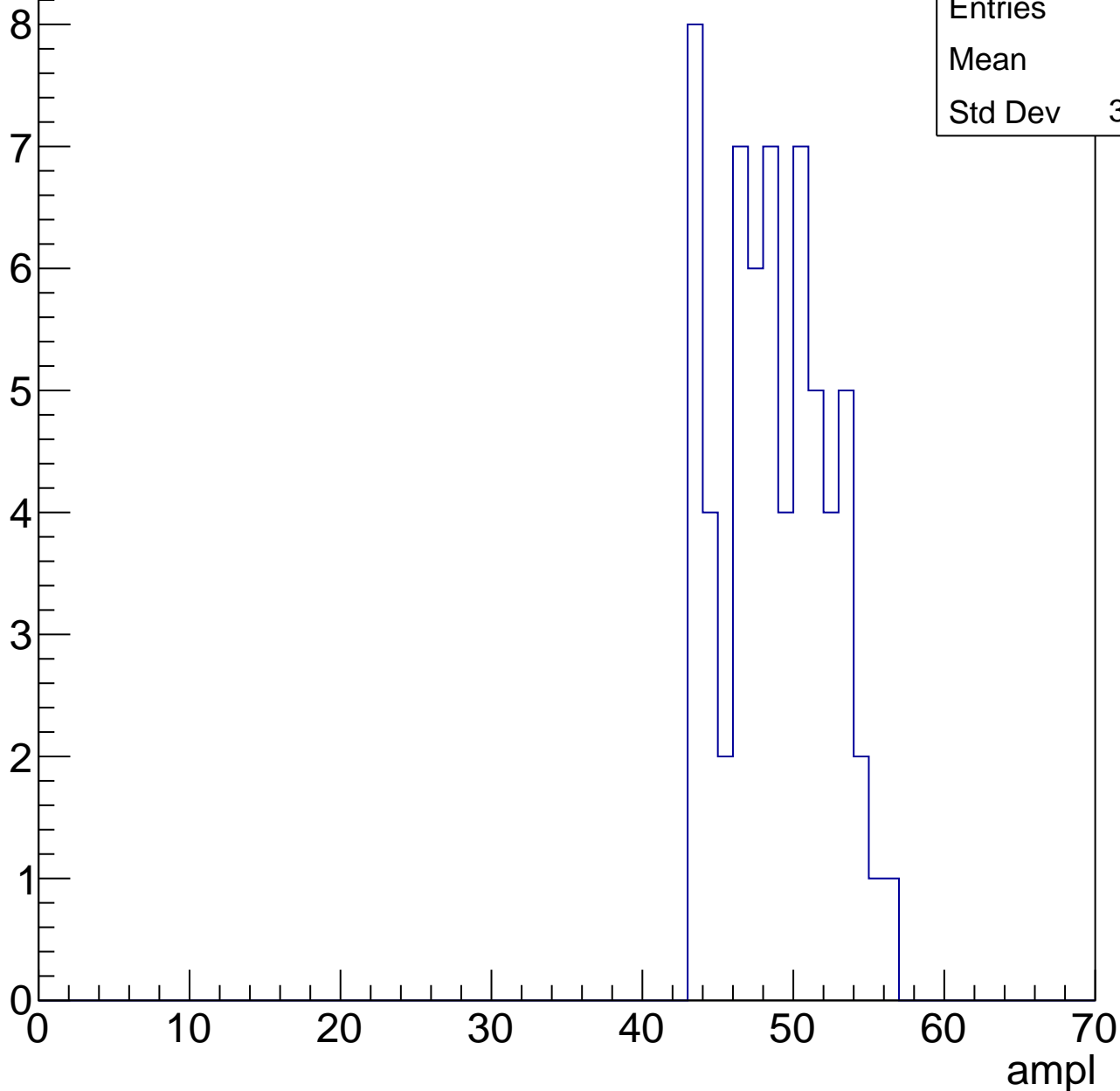


B1L102S, U13-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

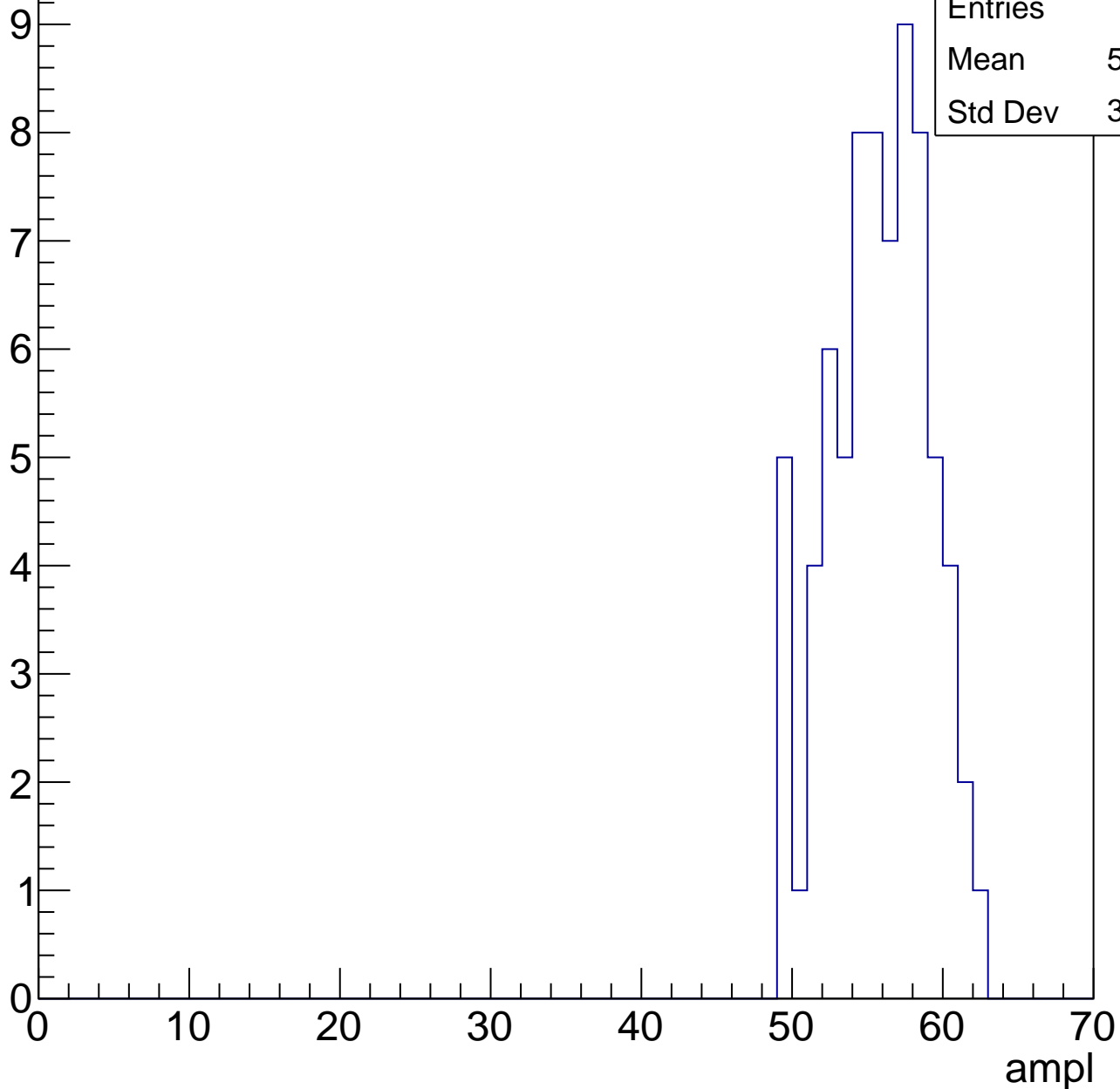
Entries	63
Mean	48.3
Std Dev	3.476



B1L102S, U13-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



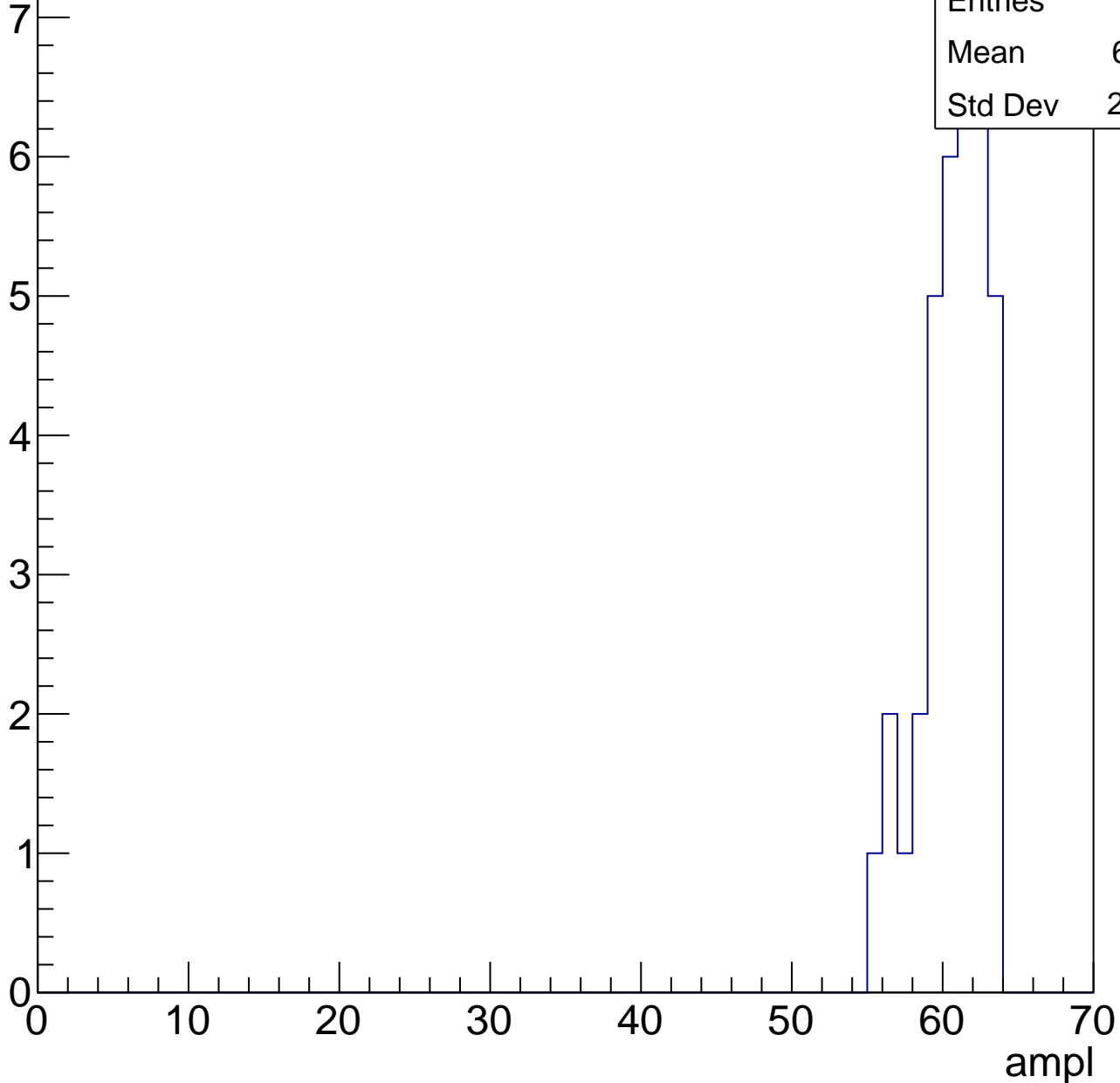
Entries	73
Mean	55.29
Std Dev	3.229

B1L102S, U13-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	60.31
Std Dev	2.079



B1L102S, U13-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch24, adc0

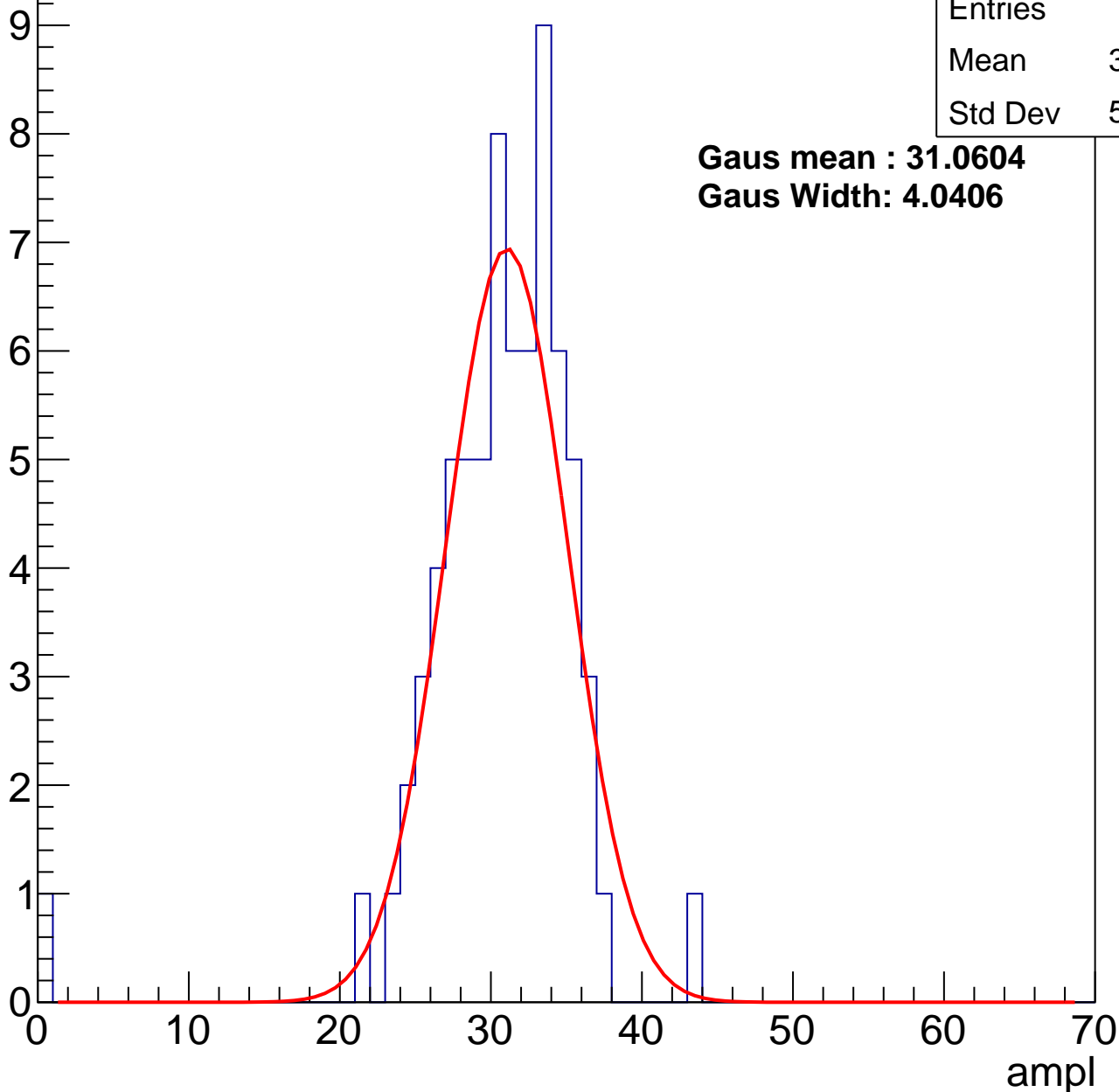
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	30.18
Std Dev	5.226

Gaus mean : 31.0604

Gaus Width: 4.0406



B1L102S, U13-ch24, adc1

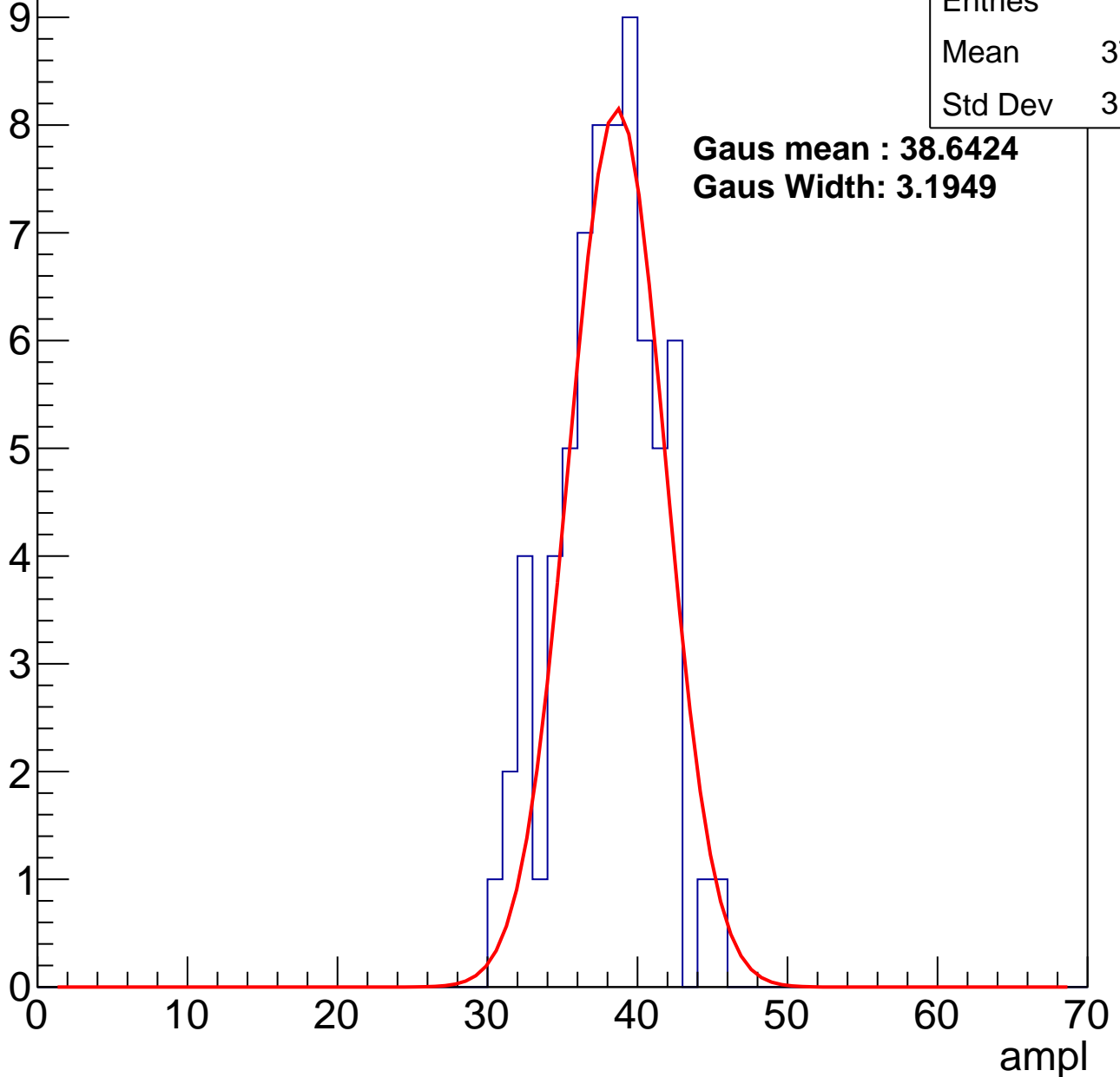
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.54
Std Dev	3.256

Gaus mean : 38.6424

Gaus Width: 3.1949



B1L102S, U13-ch24, adc2

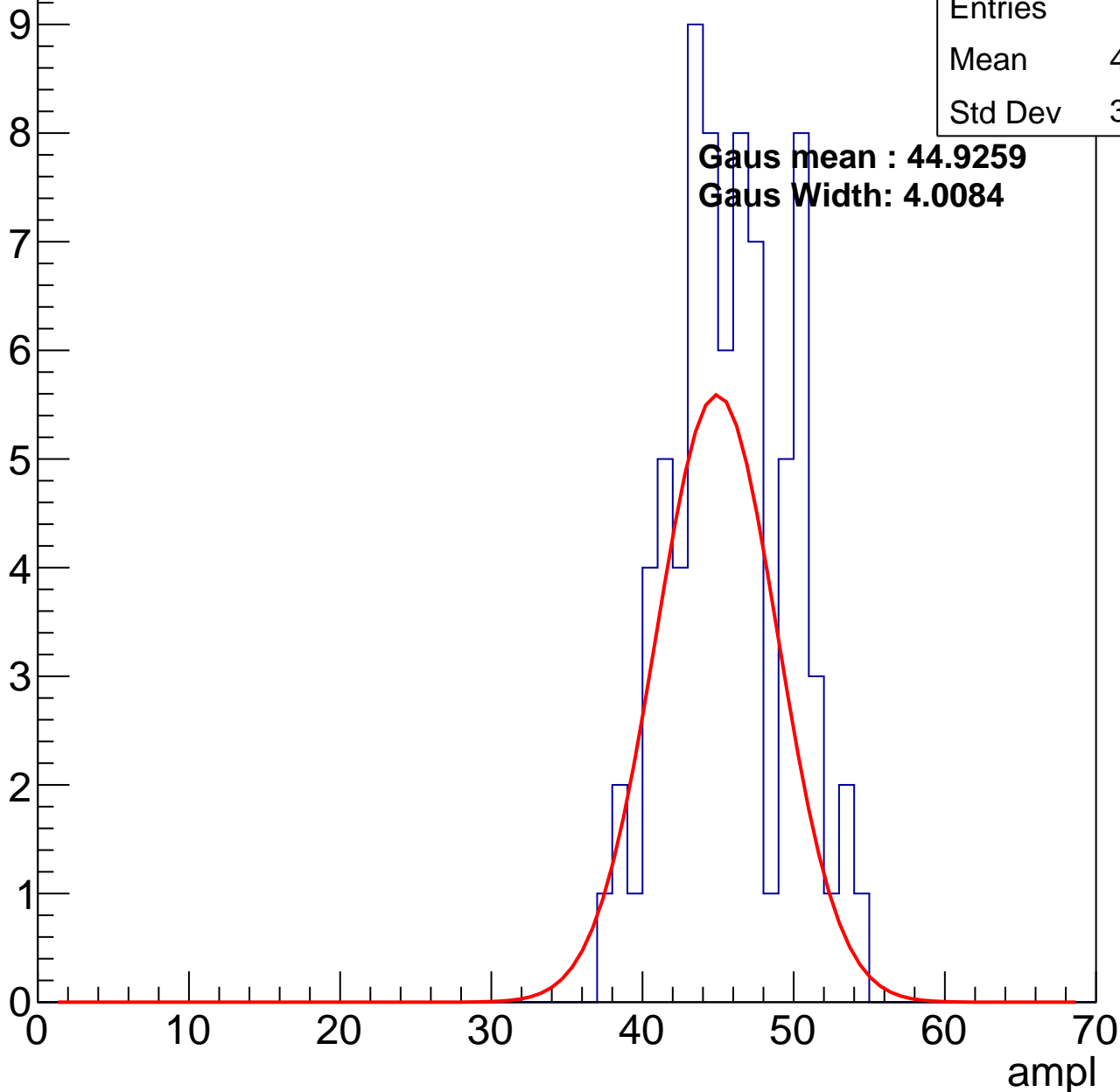
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	45.38
Std Dev	3.869

Gaus mean : 44.9259

Gaus Width: 4.0084

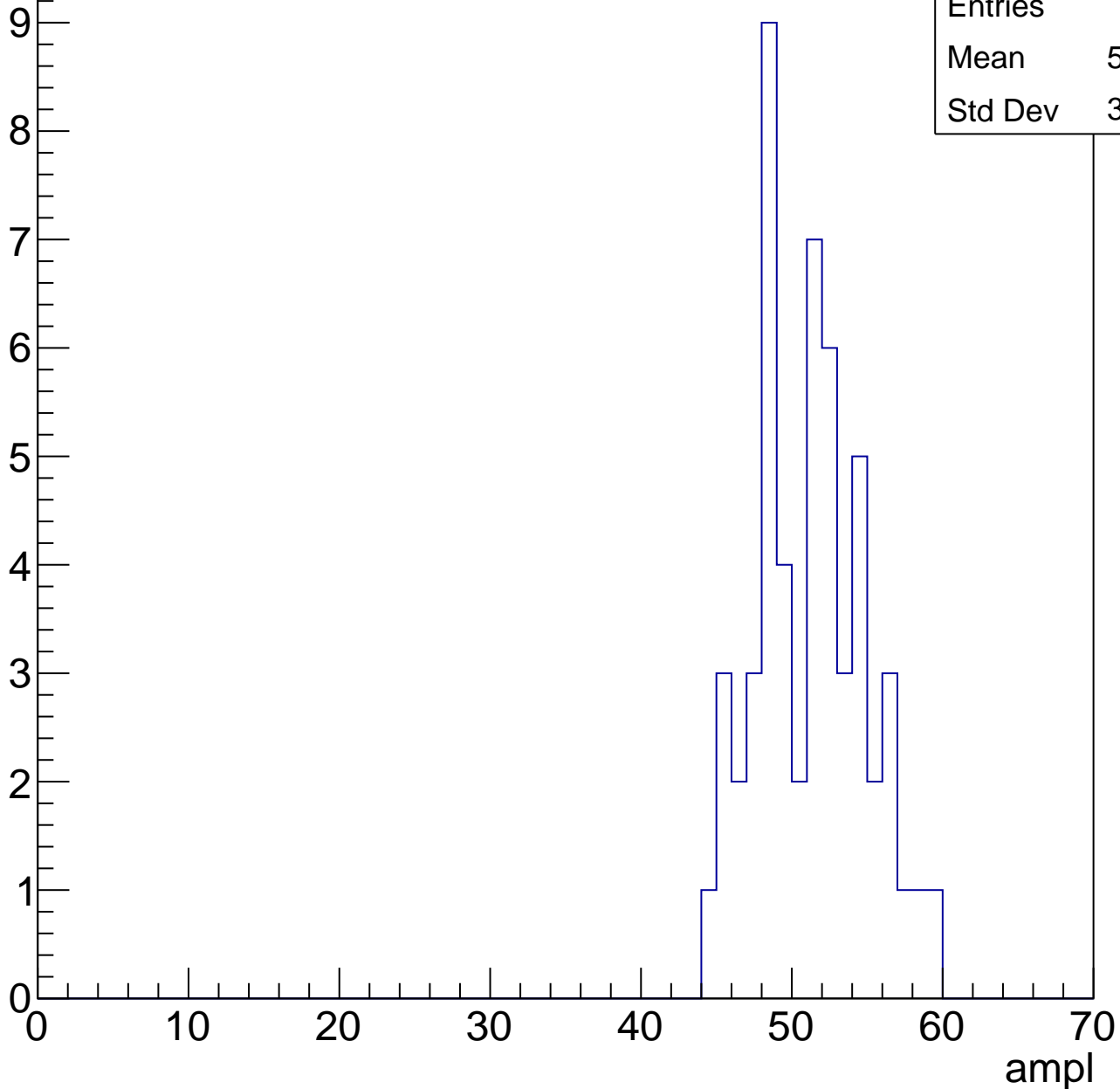


B1L102S, U13-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	50.75
Std Dev	3.555

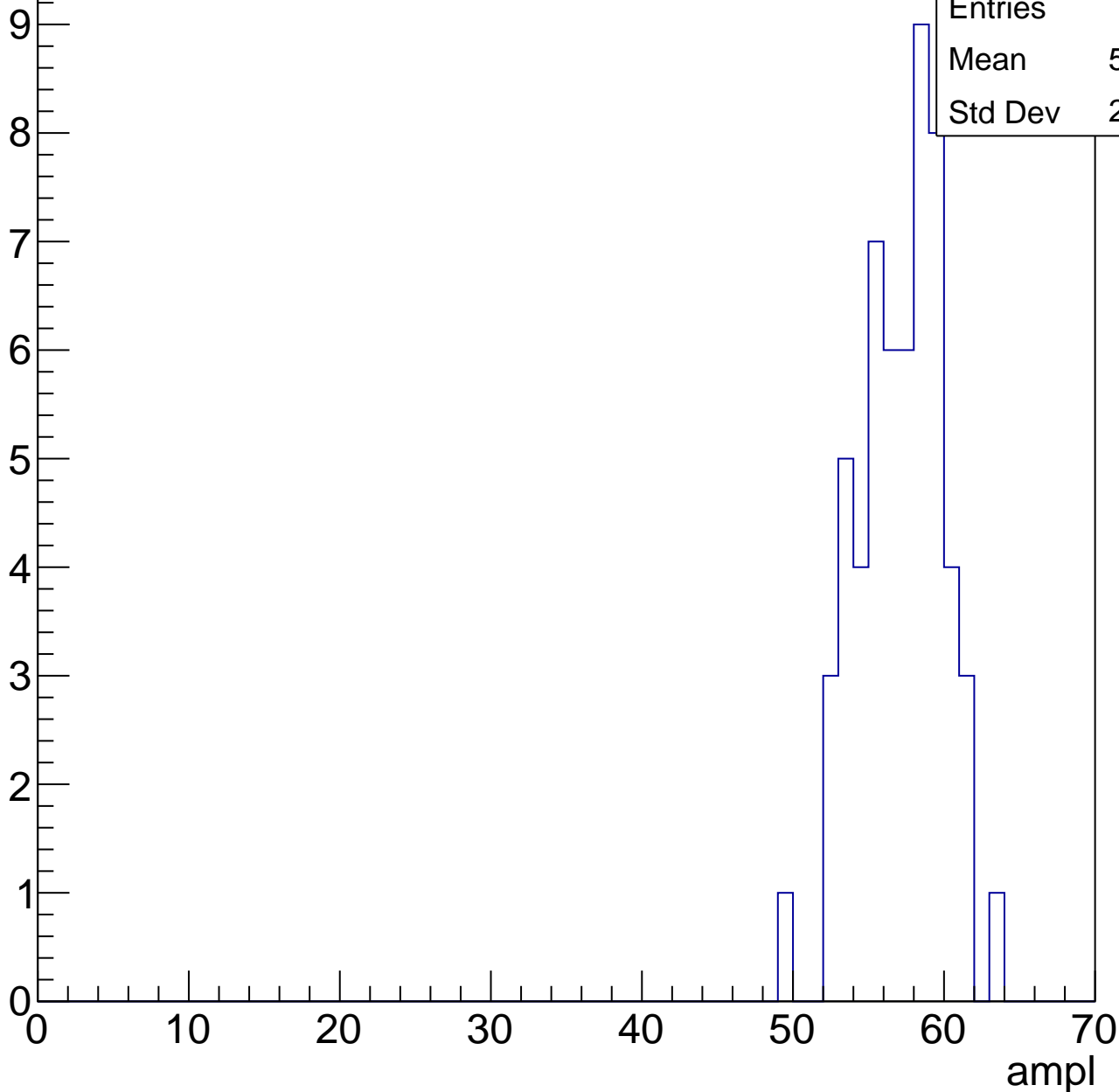


B1L102S, U13-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.65
Std Dev	2.788

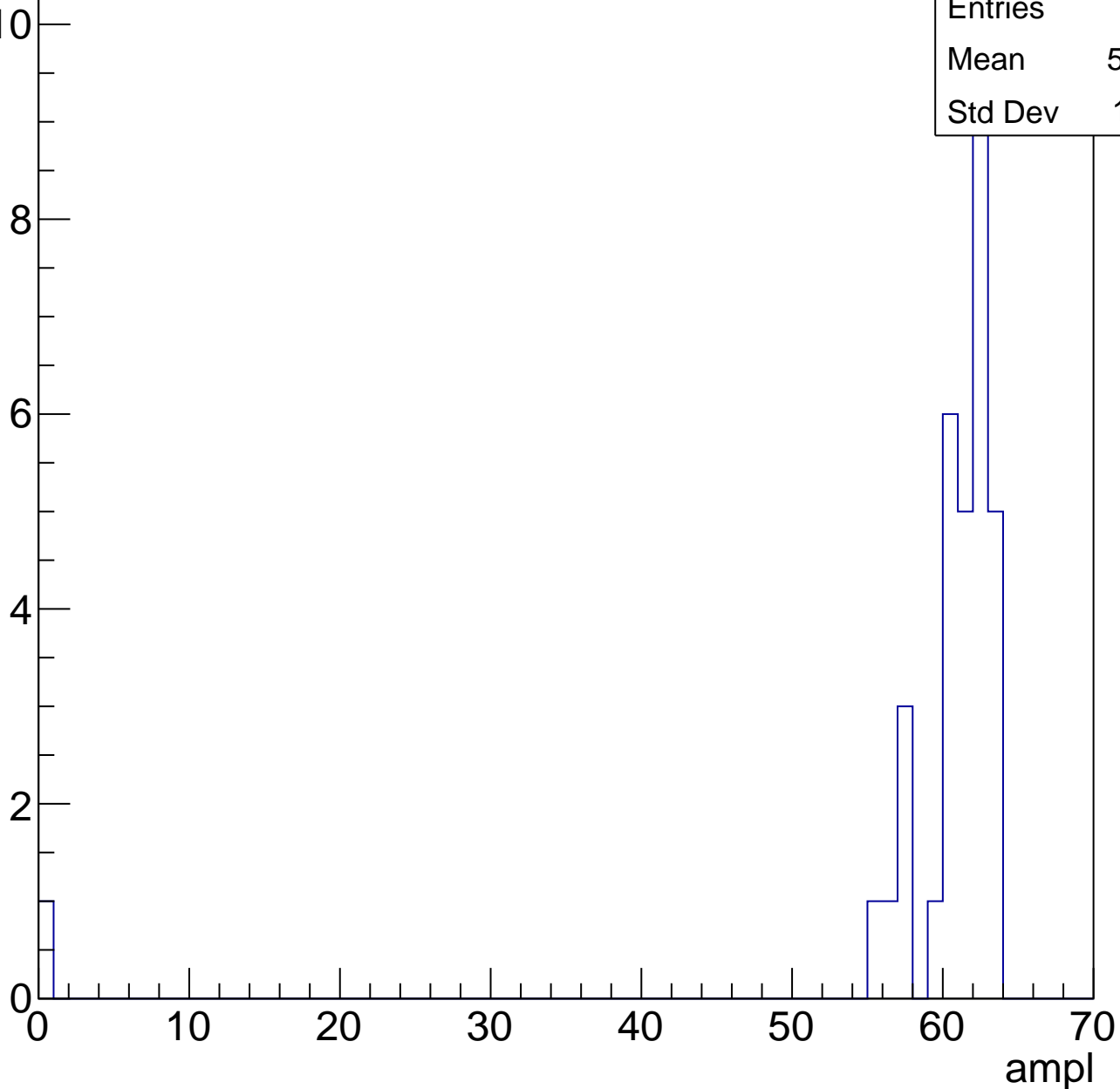


B1L102S, U13-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

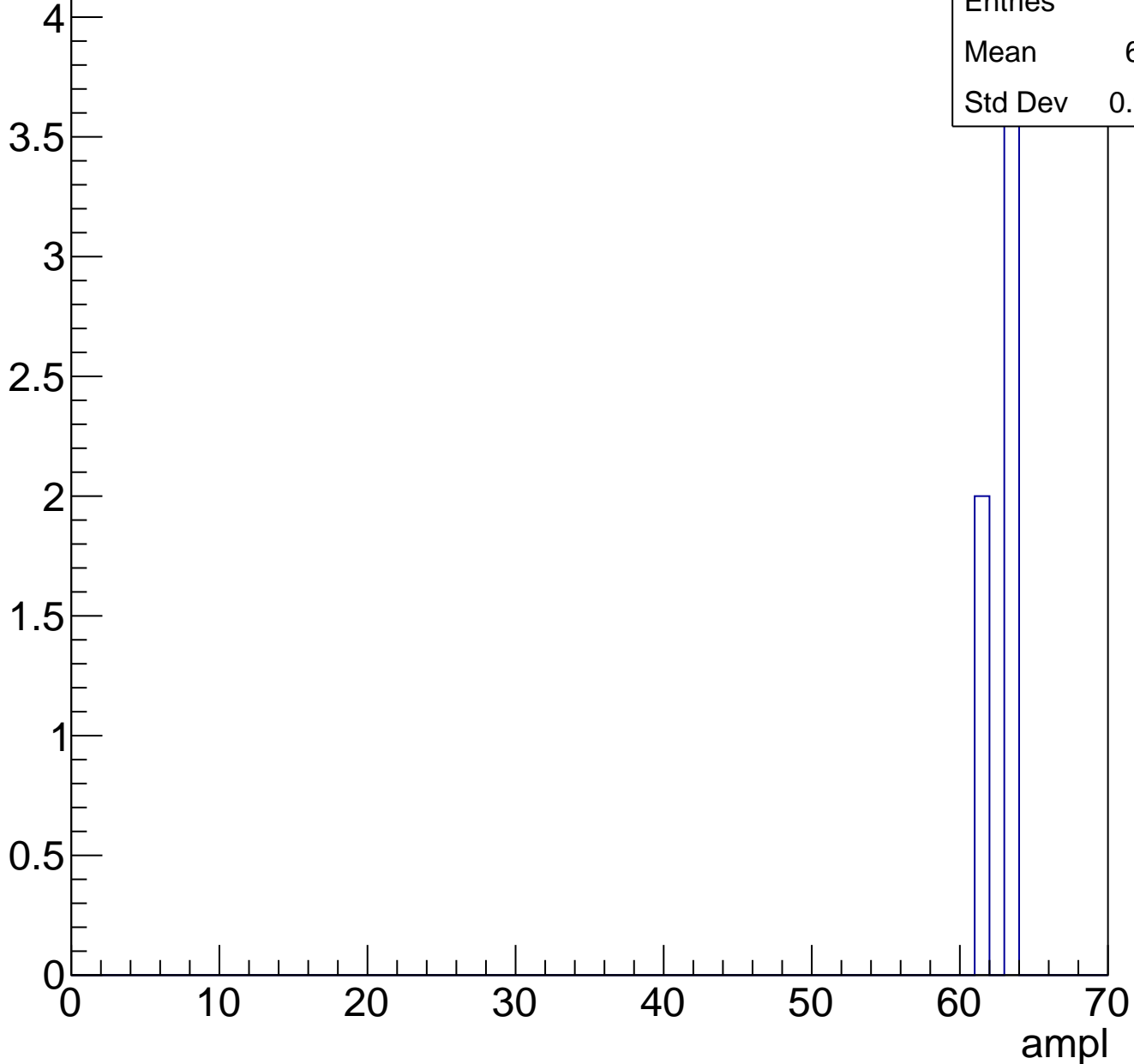
Entries	33
Mean	58.82
Std Dev	10.61



B1L102S, U13-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch25, adc0

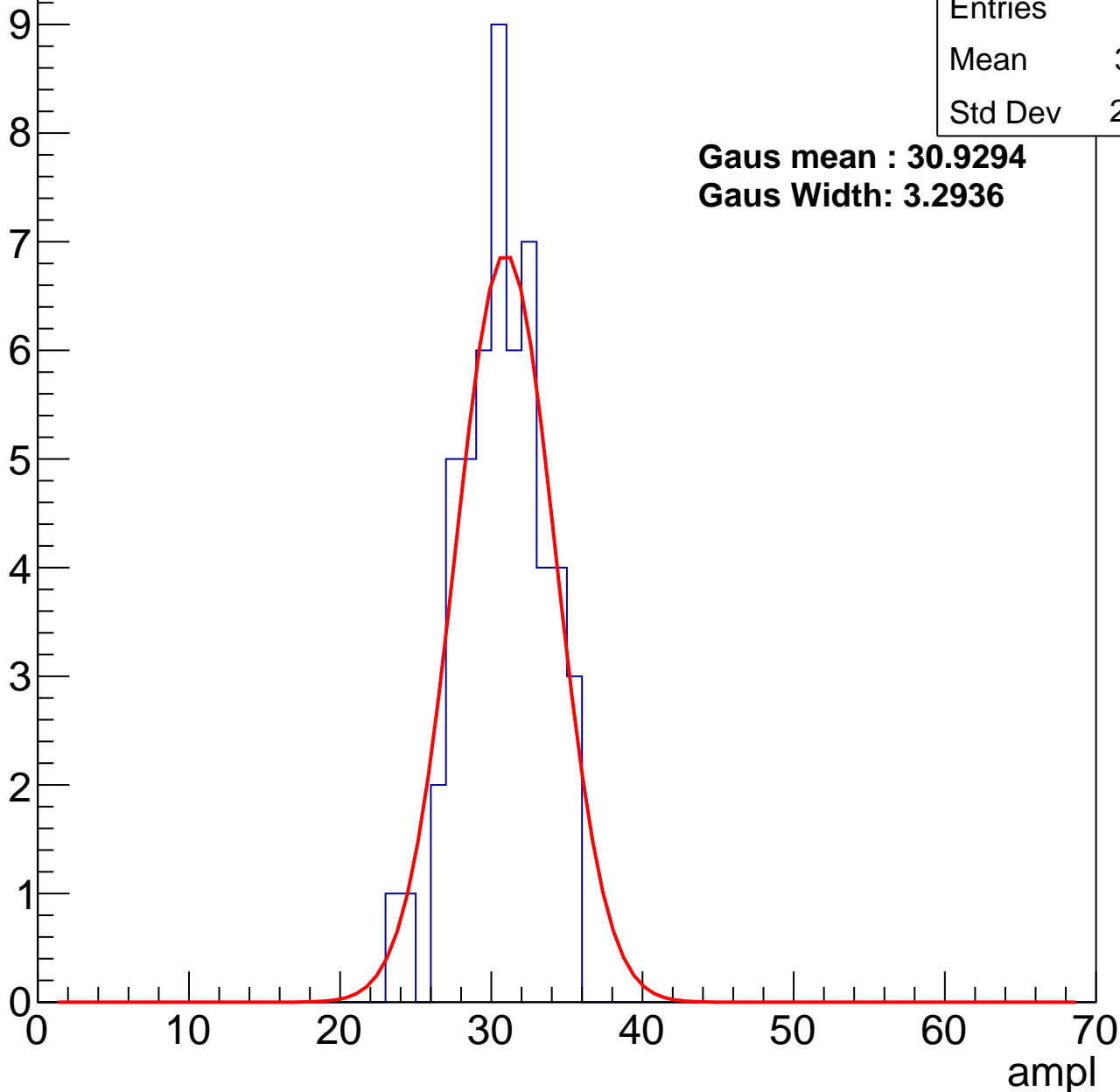
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	30.21
Std Dev	2.729

Gaus mean : 30.9294

Gaus Width: 3.2936



B1L102S, U13-ch25, adc1

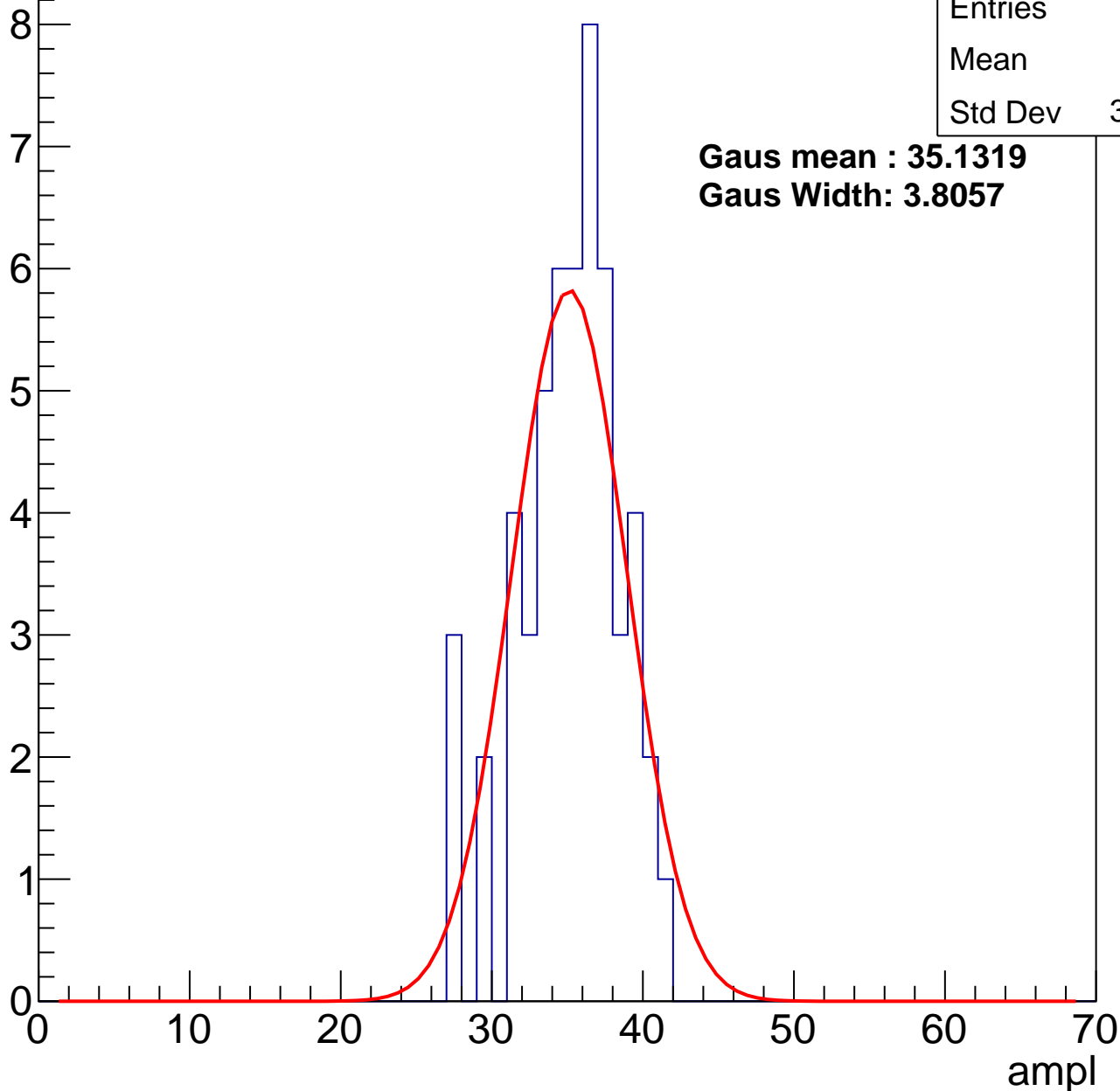
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.7
Std Dev	3.323

Gaus mean : 35.1319

Gaus Width: 3.8057



B1L102S, U13-ch25, adc2

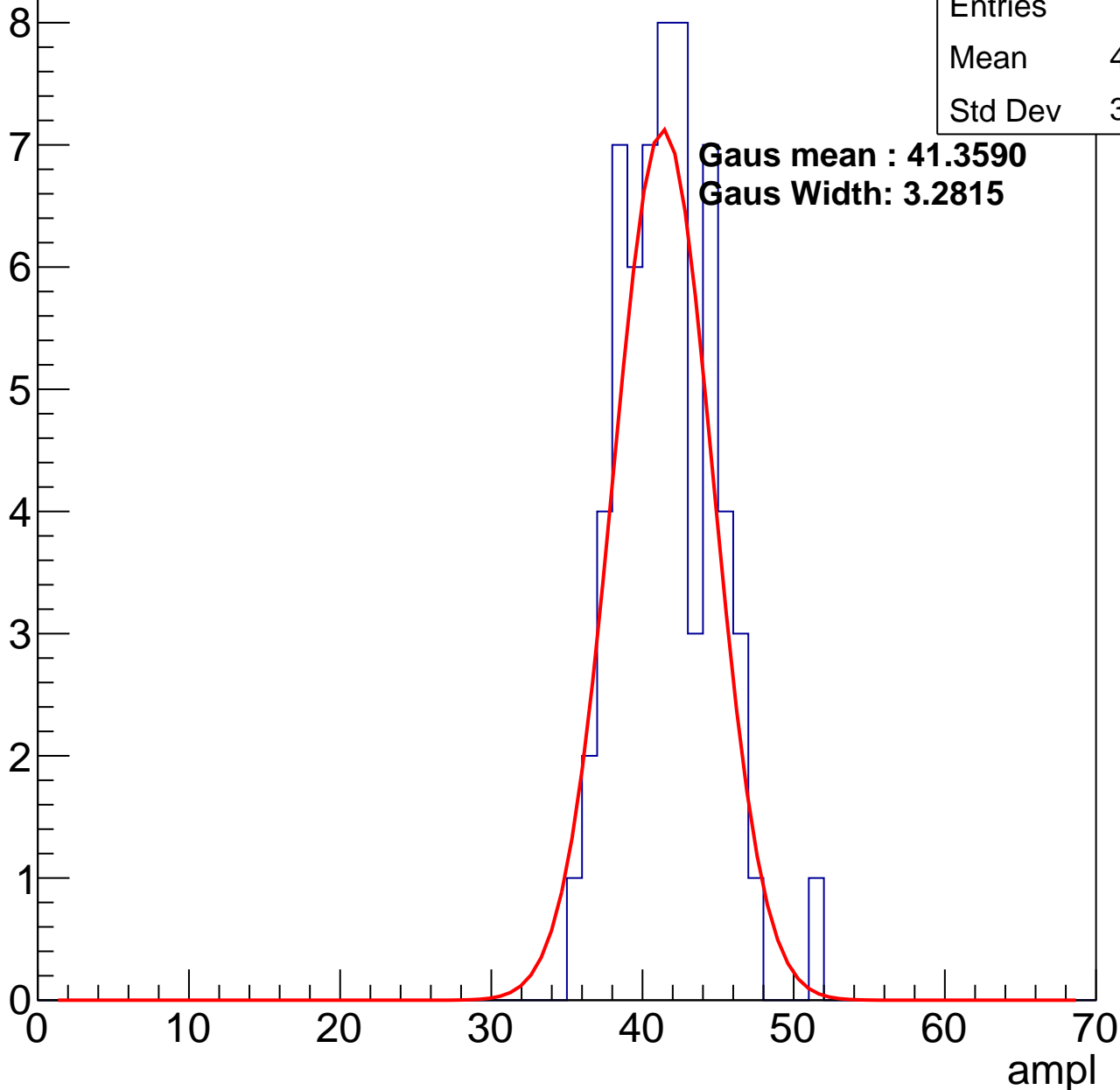
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.16
Std Dev	3.112

Gaus mean : 41.3590

Gaus Width: 3.2815



B1L102S, U13-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

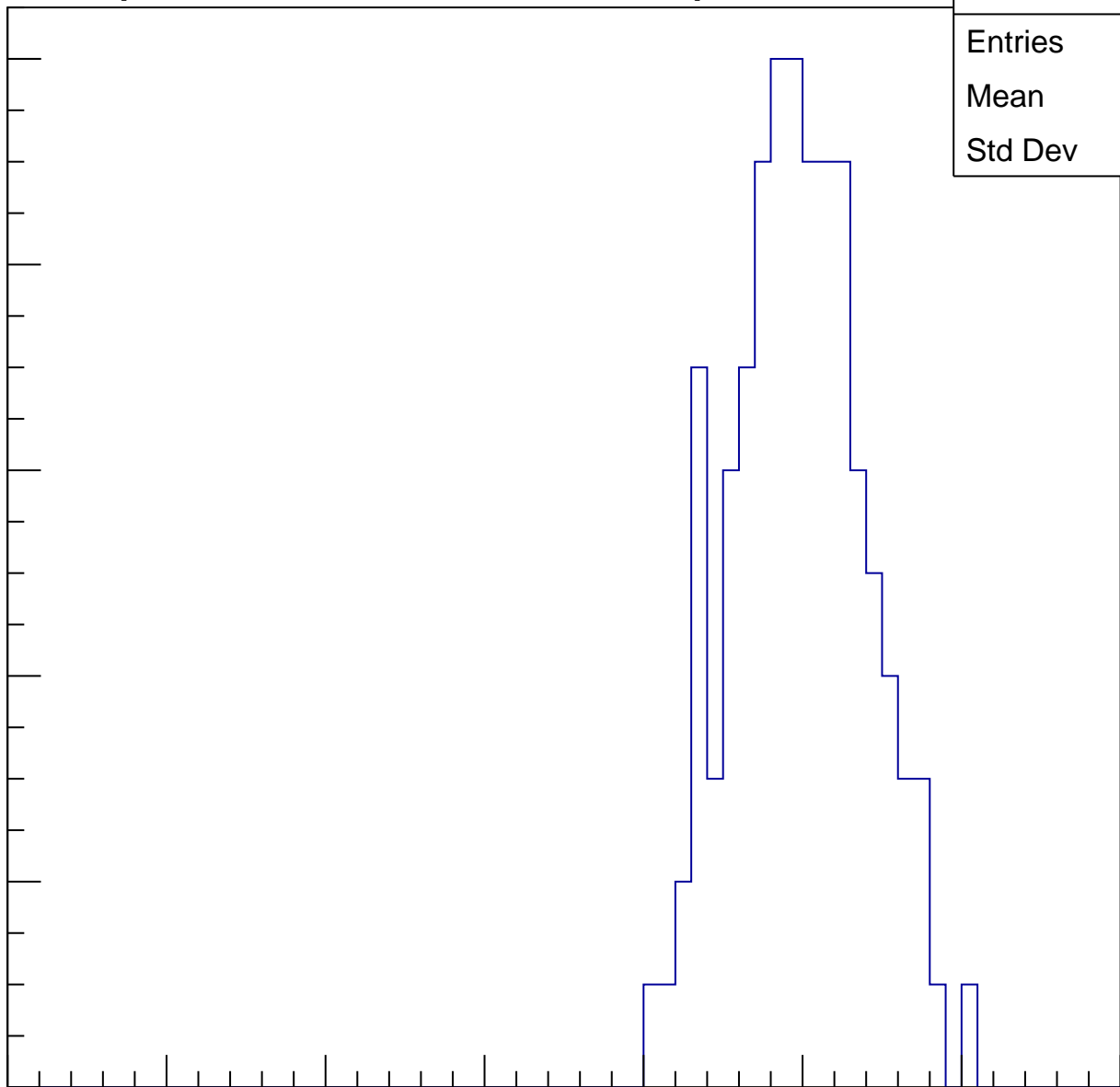
Entries	106
Mean	49.29
Std Dev	4.118

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

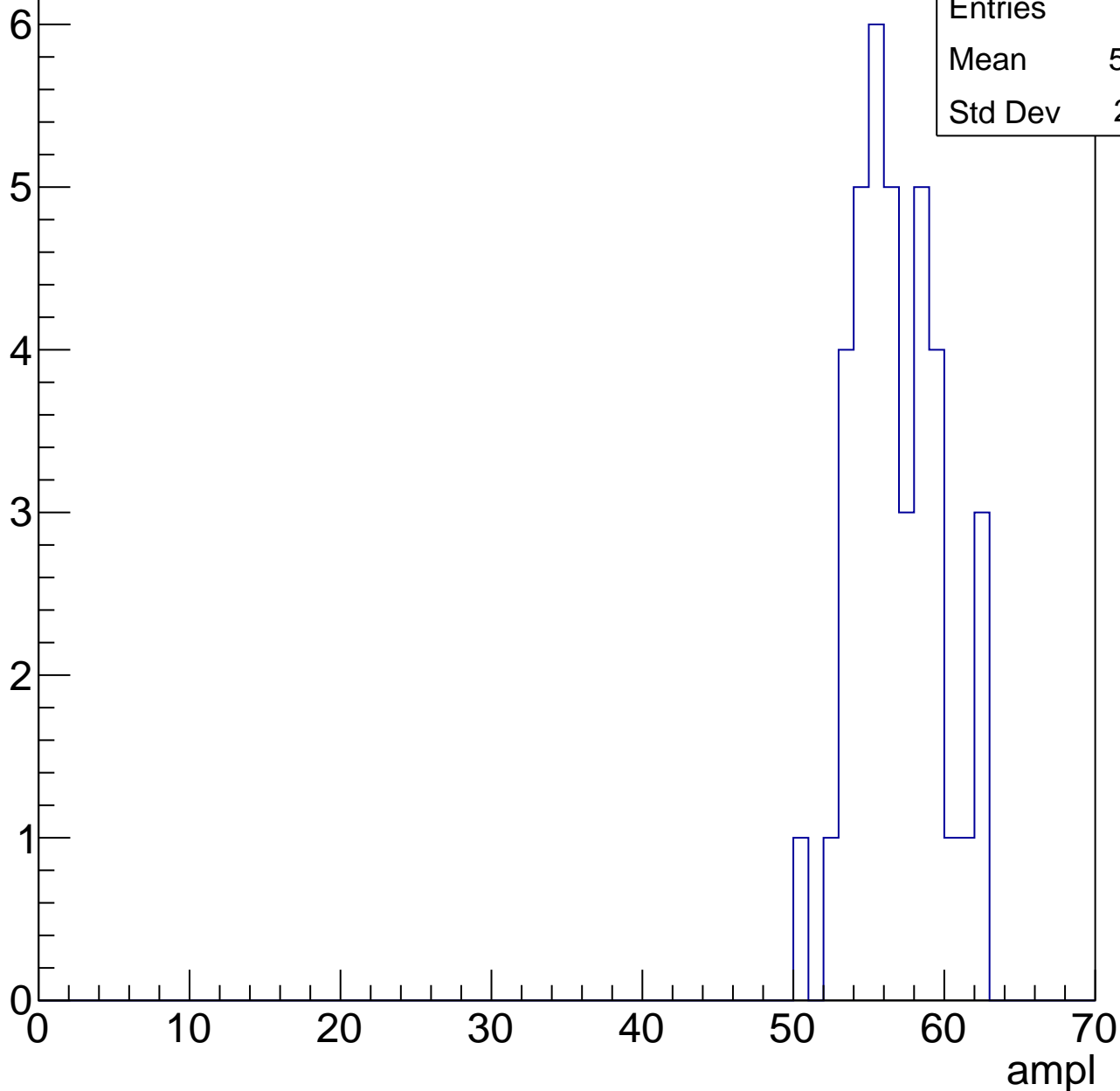


B1L102S, U13-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	56.36
Std Dev	2.851

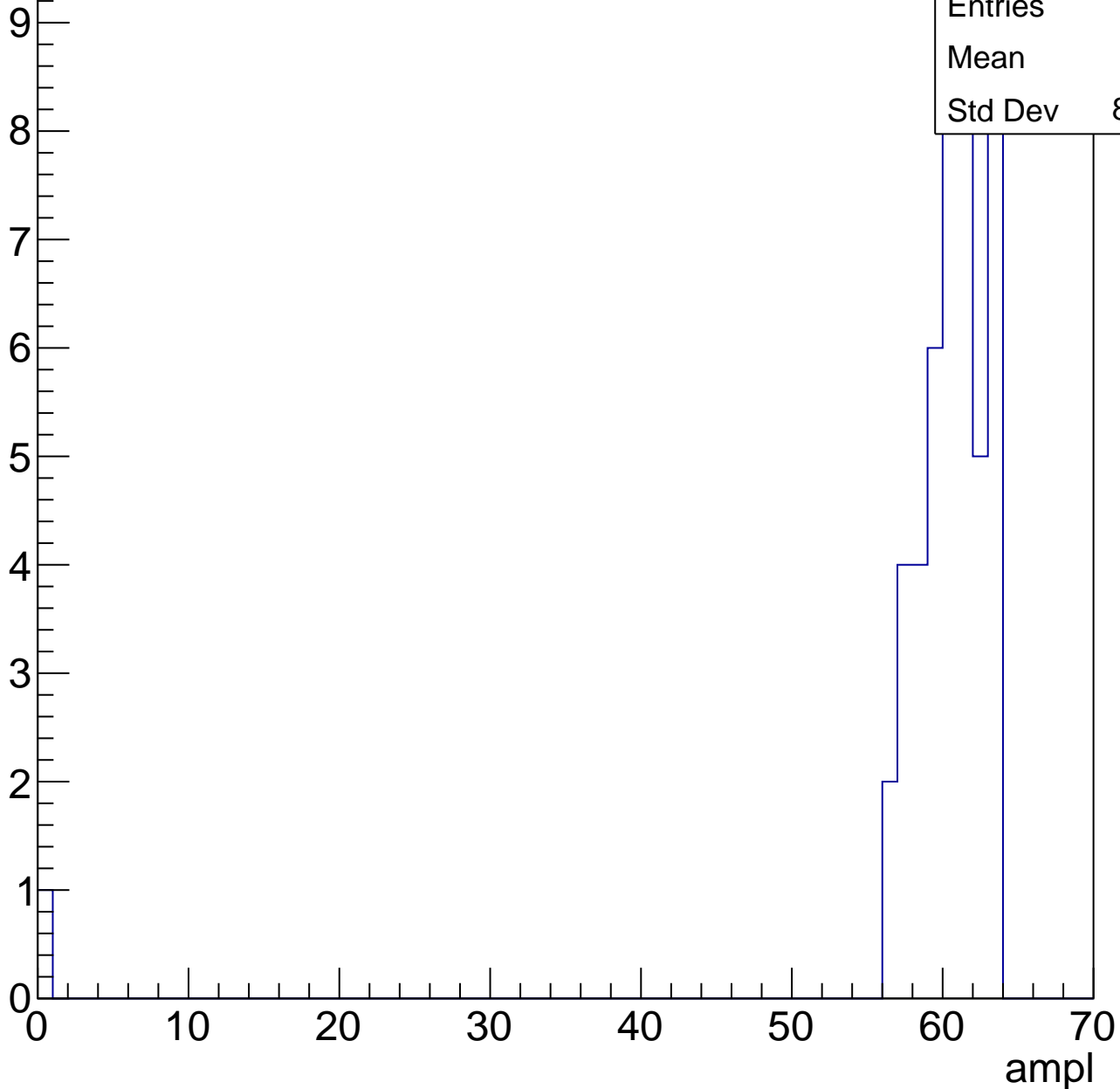


B1L102S, U13-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	59
Std Dev	8.841



B1L102S, U13-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch26, adc0

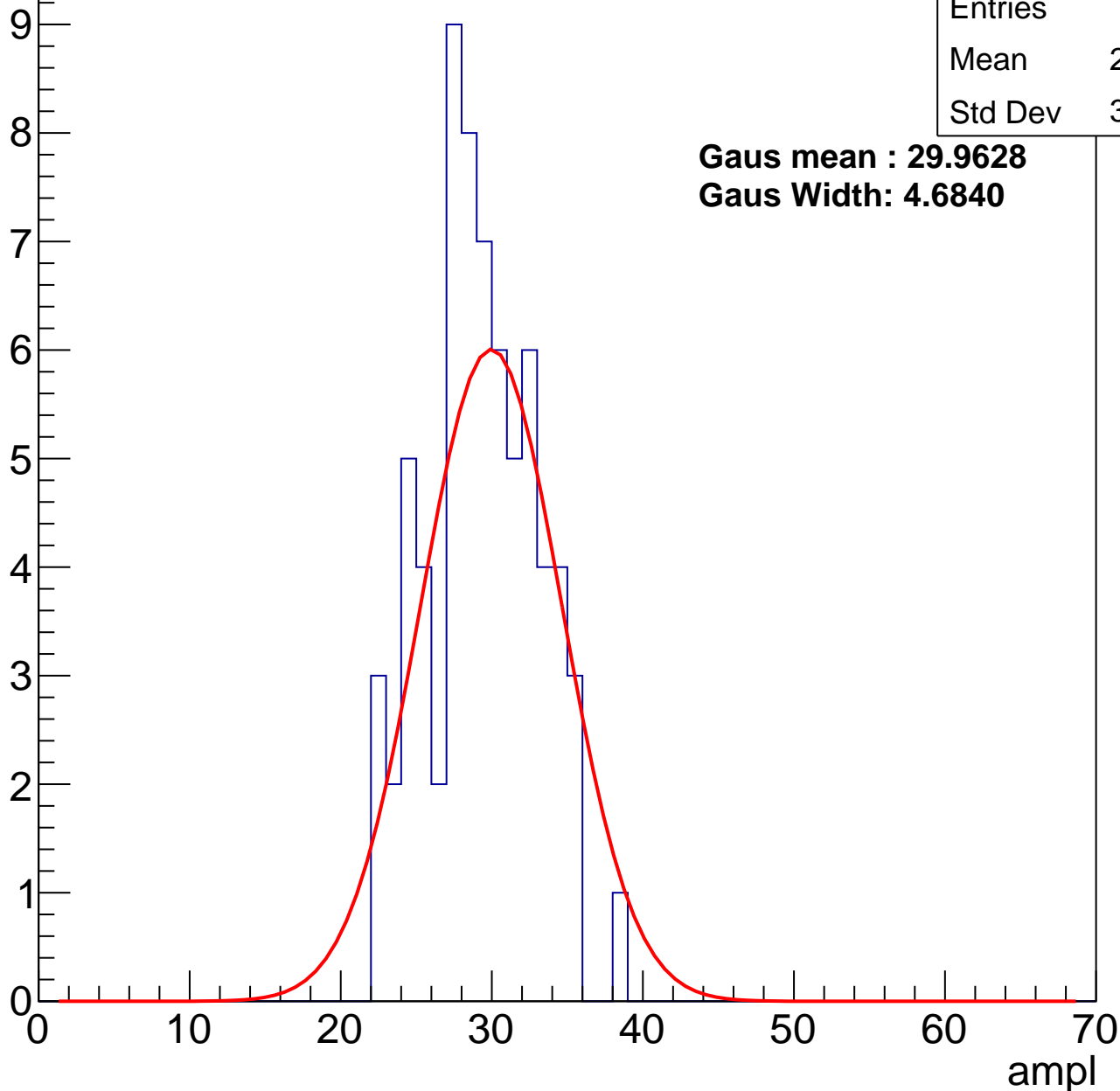
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.87
Std Dev	3.615

Gaus mean : 29.9628

Gaus Width: 4.6840



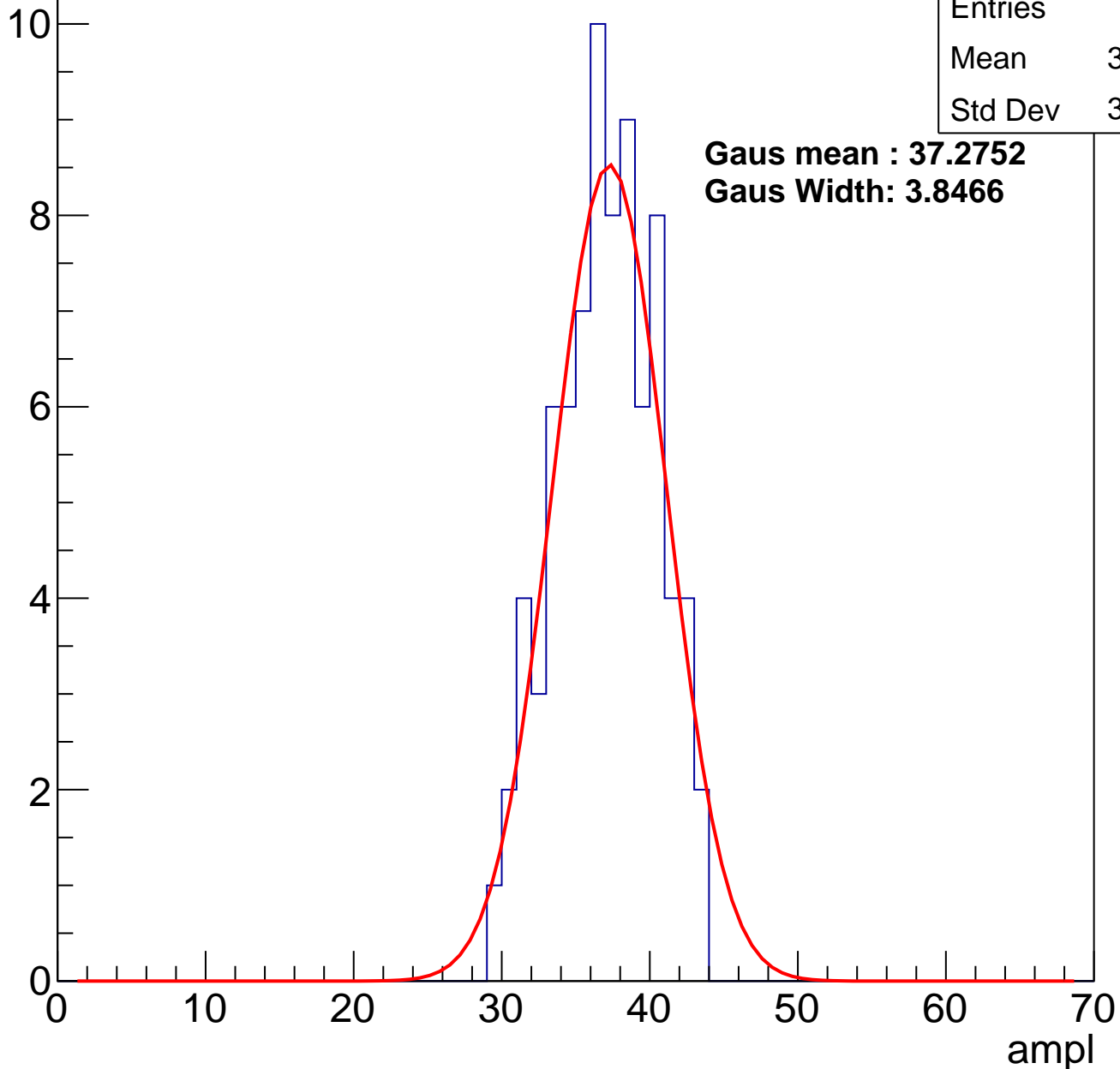
B1L102S, U13-ch26, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	36.58
Std Dev	3.346

Gaus mean : 37.2752
Gaus Width: 3.8466

Entry



B1L102S, U13-ch26, adc2

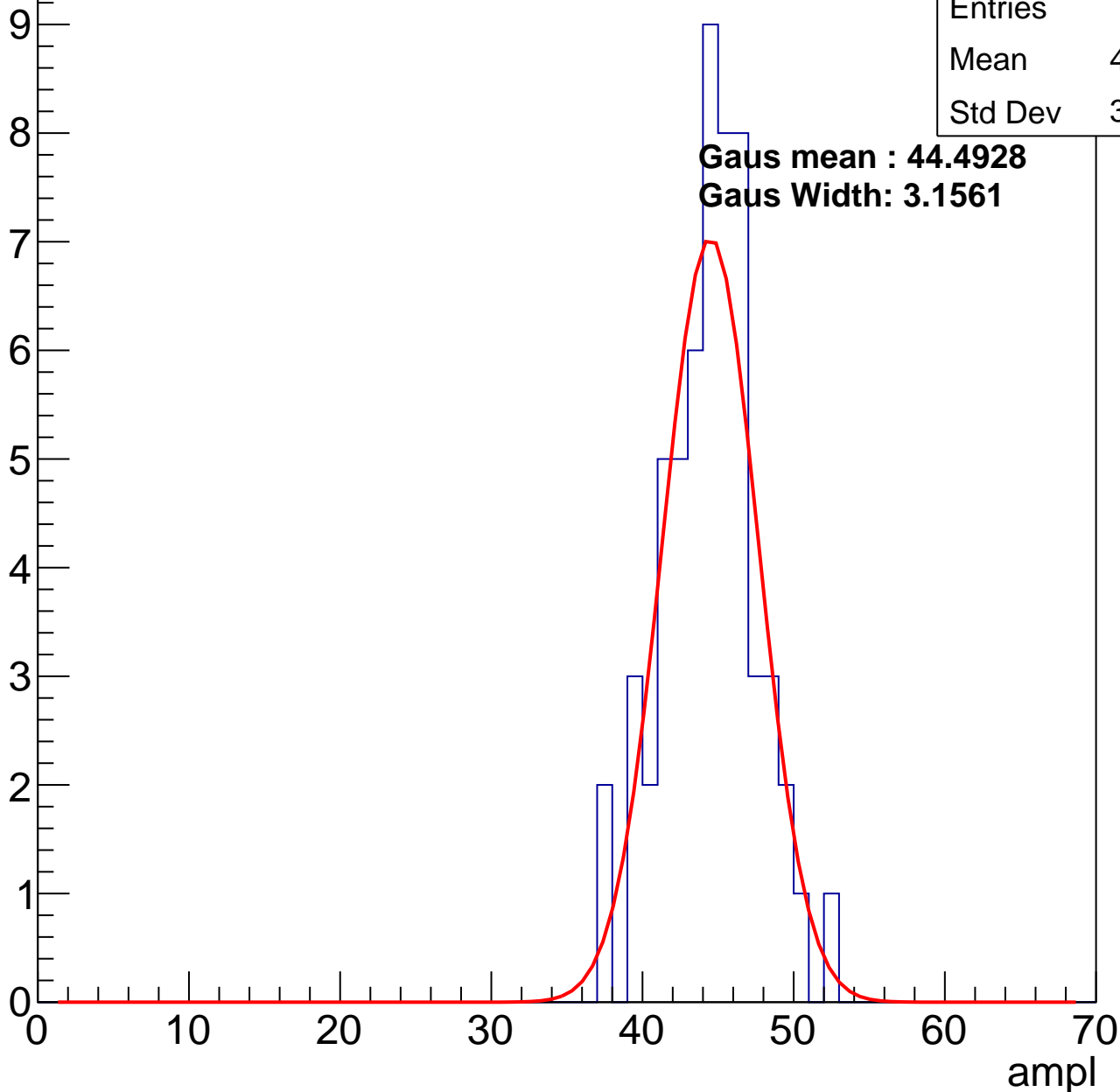
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	44.02
Std Dev	3.054

Gaus mean : 44.4928

Gaus Width: 3.1561



B1L102S, U13-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

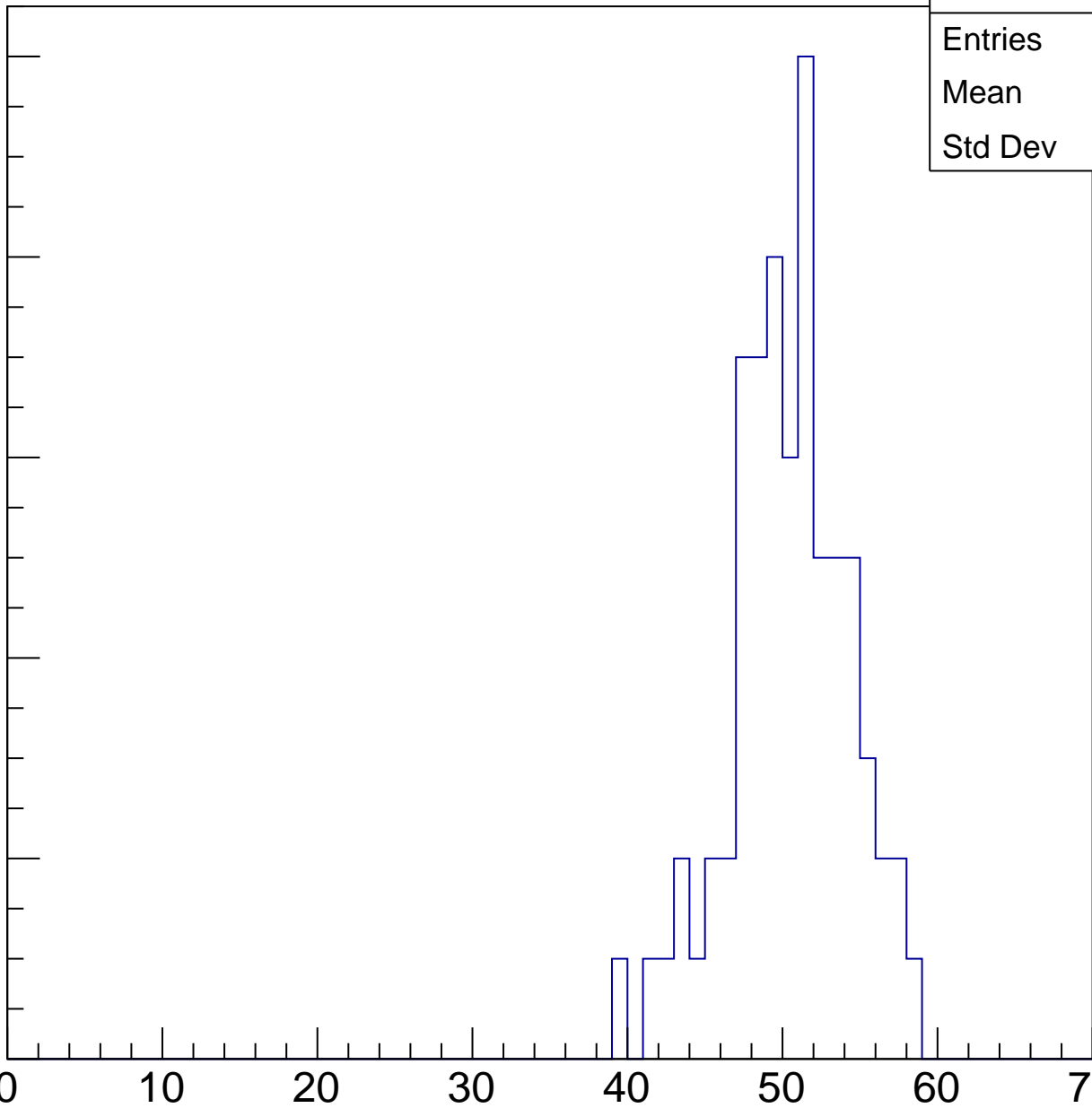
Entries	71
Mean	49.93
Std Dev	3.836

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

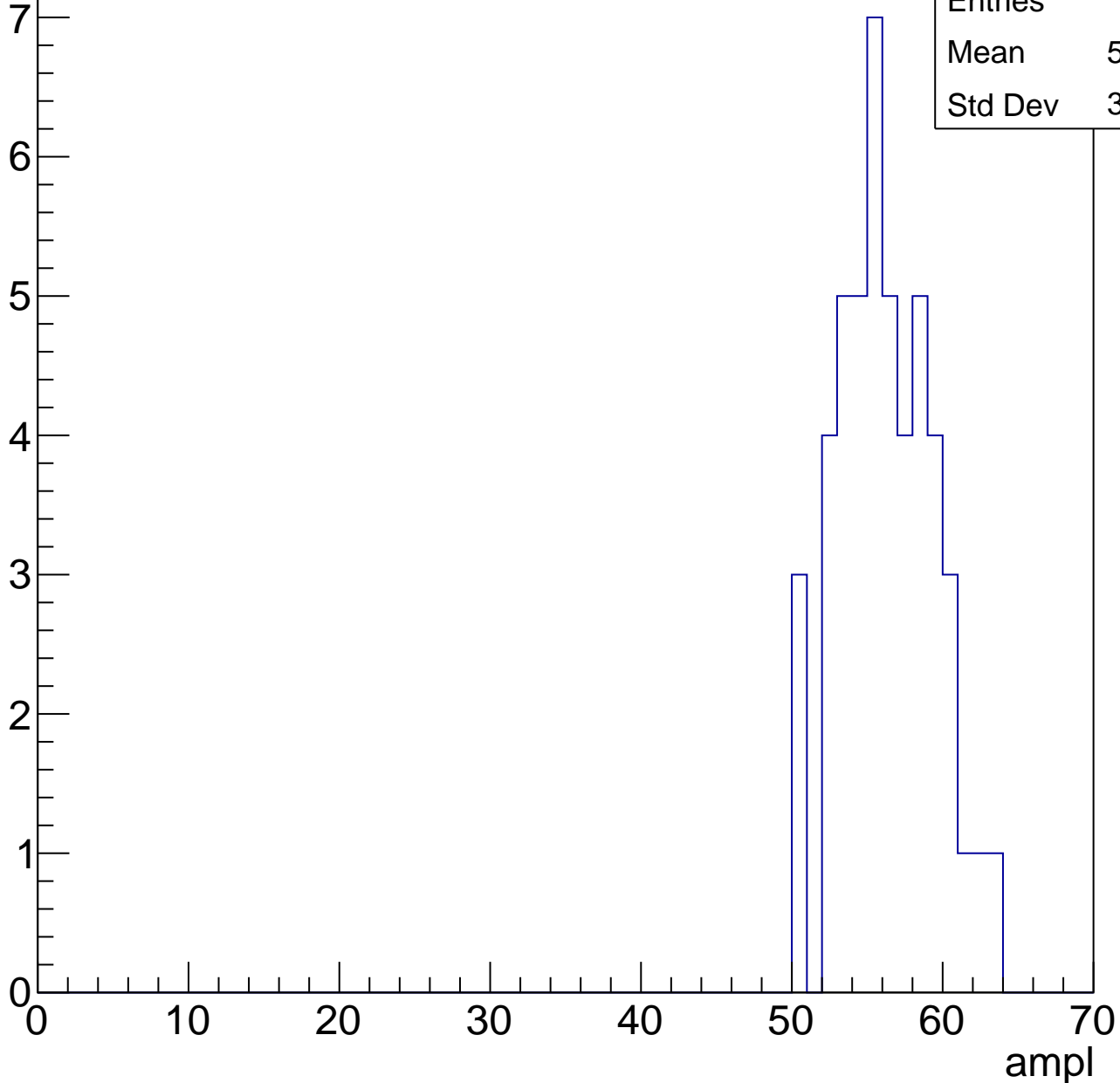


B1L102S, U13-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.79
Std Dev	3.095

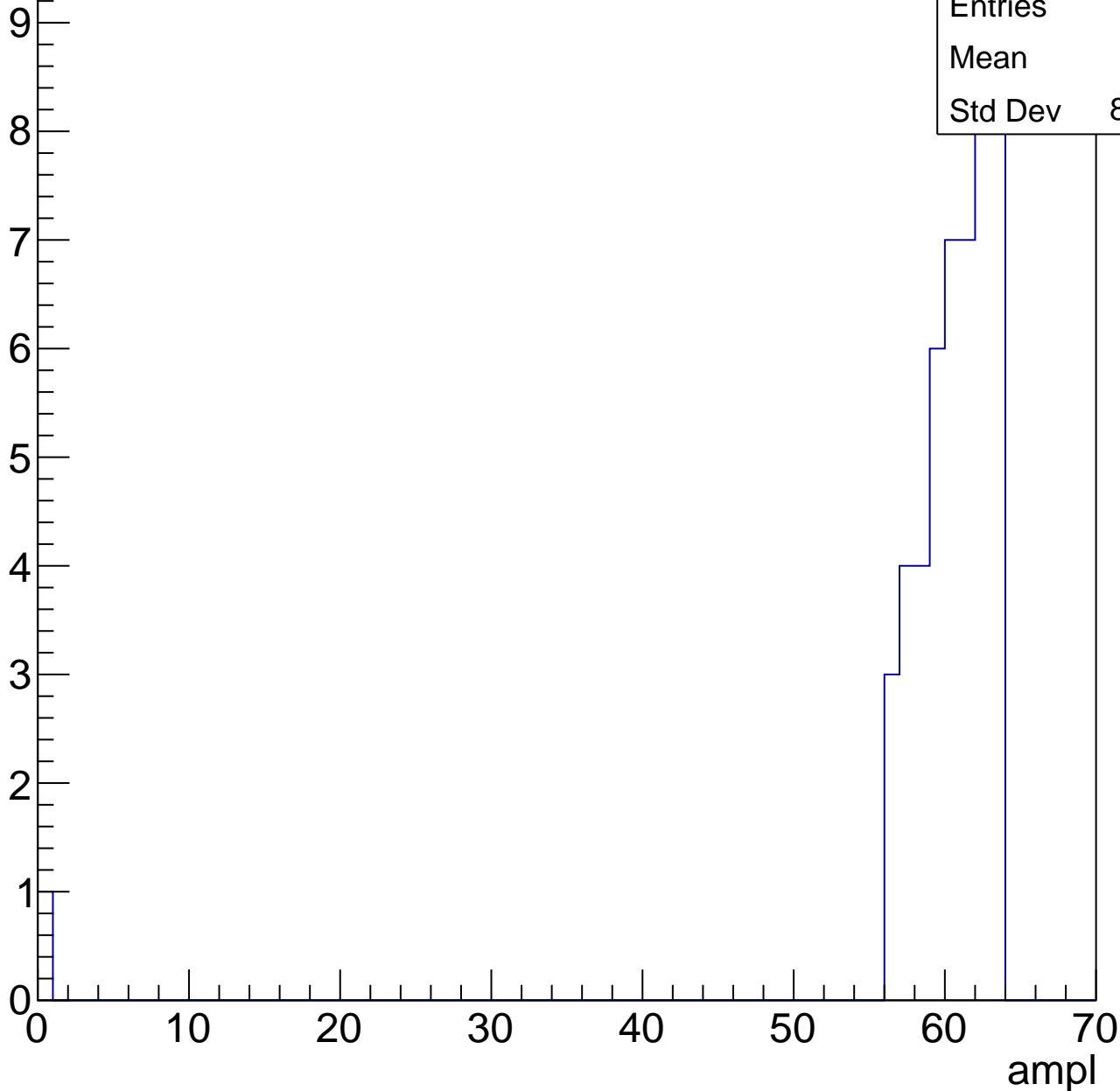


B1L102S, U13-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59
Std Dev	8.774



B1L102S, U13-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	61.67
Std Dev	0.9428

B1L102S, U13-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U13-ch27, adc0

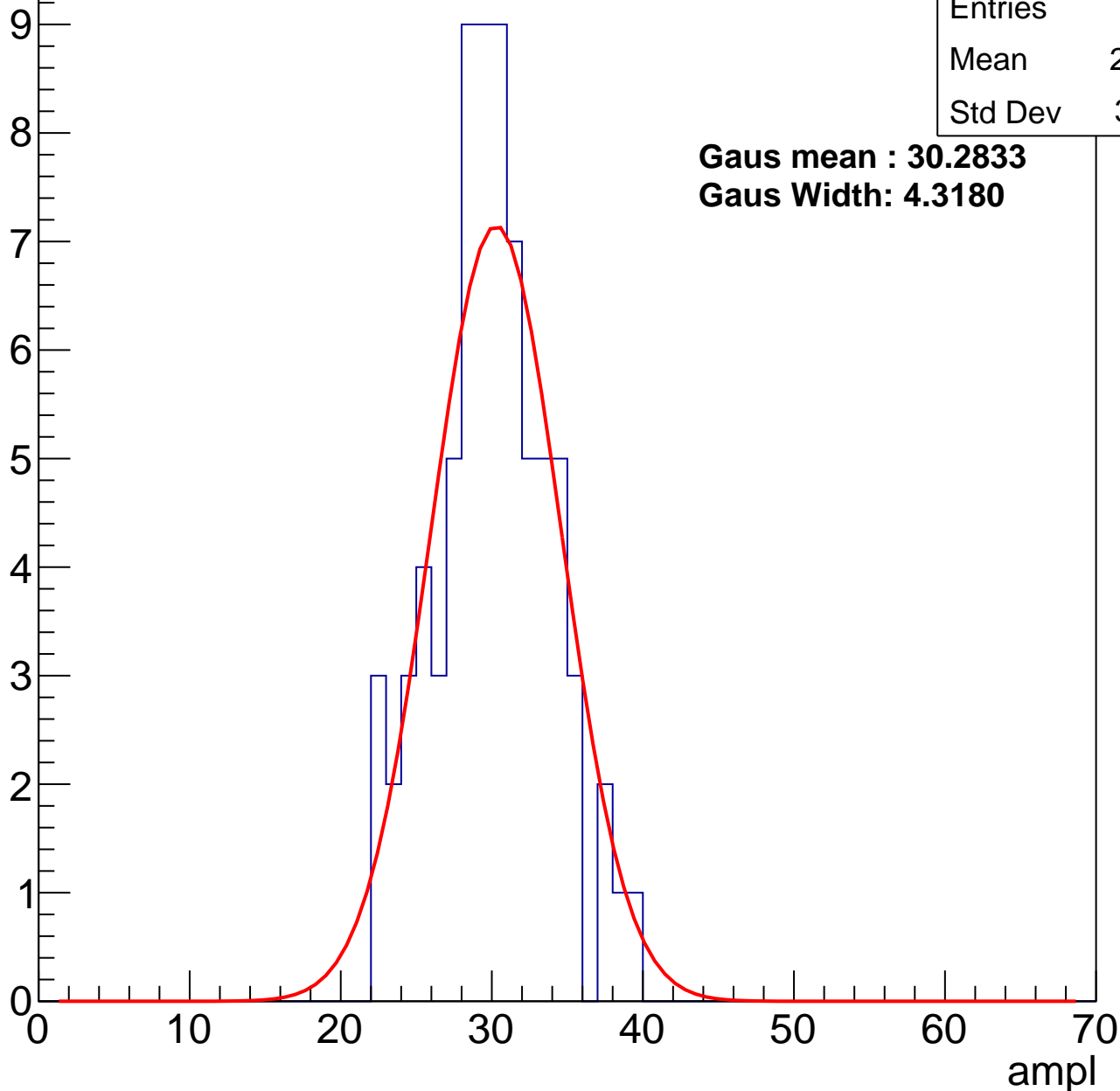
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.58
Std Dev	3.791

Gaus mean : 30.2833

Gaus Width: 4.3180



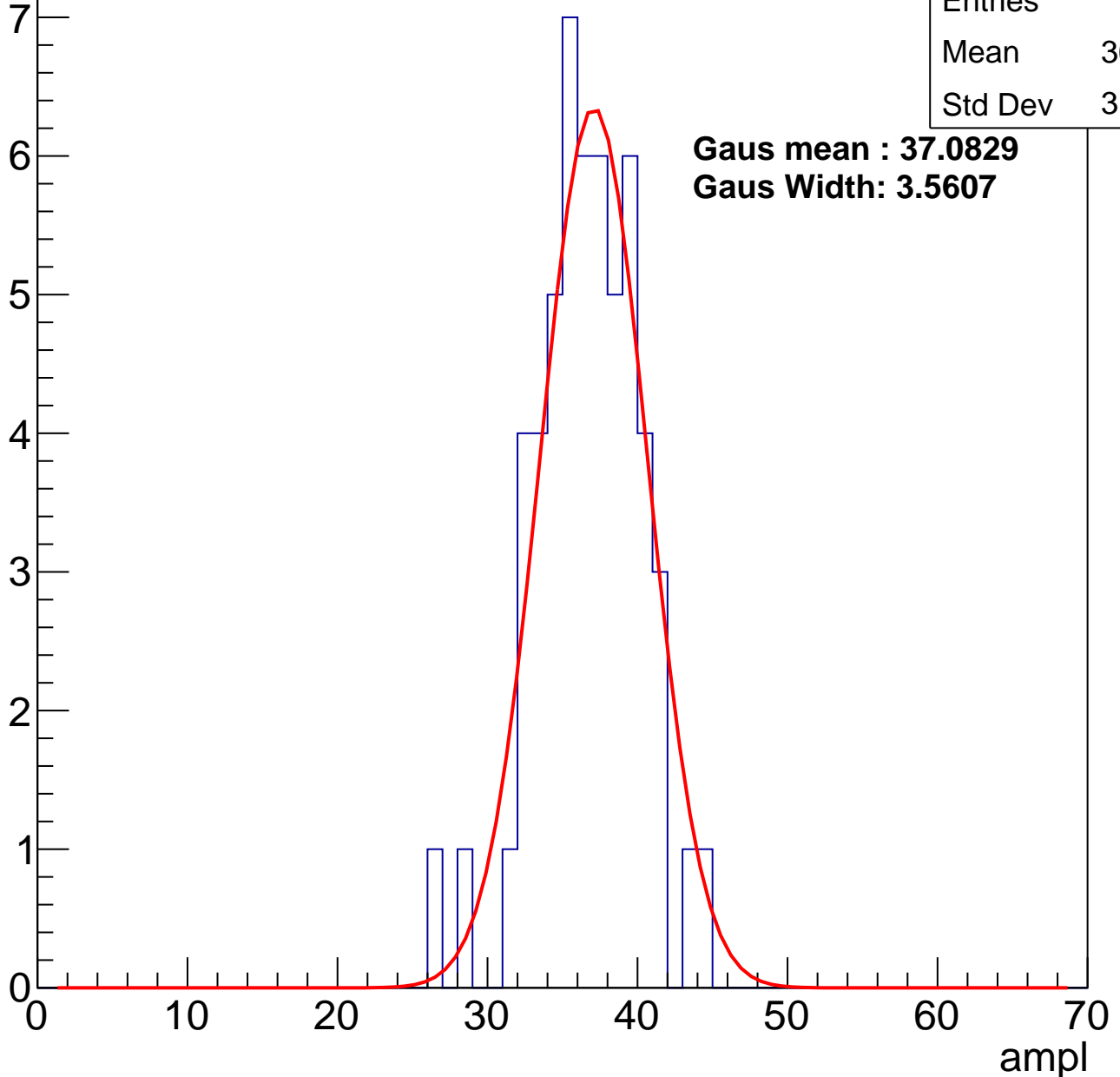
B1L102S, U13-ch27, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	36.22
Std Dev	3.426

Gaus mean : 37.0829
Gaus Width: 3.5607



B1L102S, U13-ch27, adc2

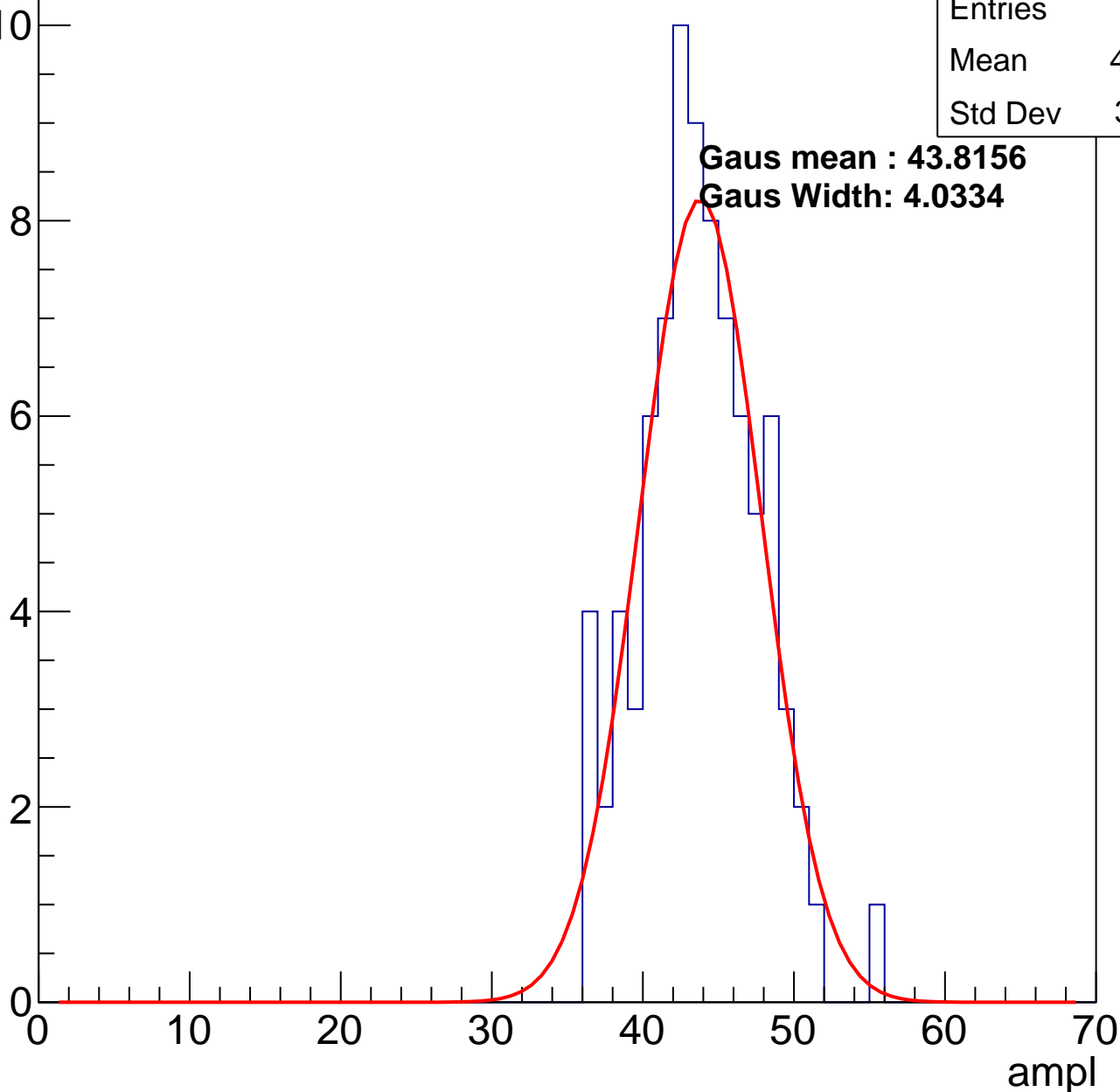
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	43.33
Std Dev	3.831

Gaus mean : 43.8156

Gaus Width: 4.0334



B1L102S, U13-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	51.77
Std Dev	3.657

Entry

10

8

6

4

2

0

0

10

20

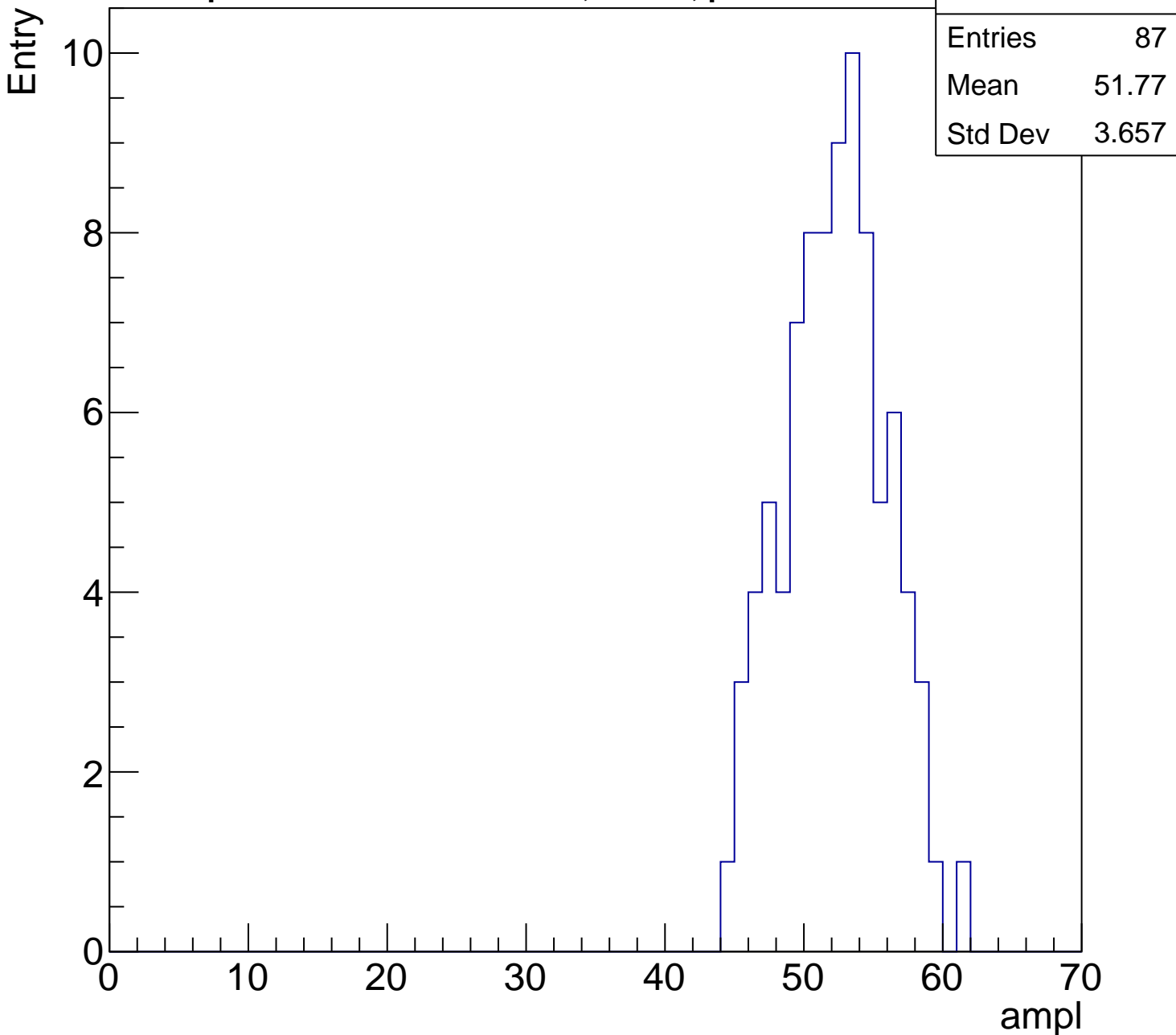
30

40

50

60

ampl

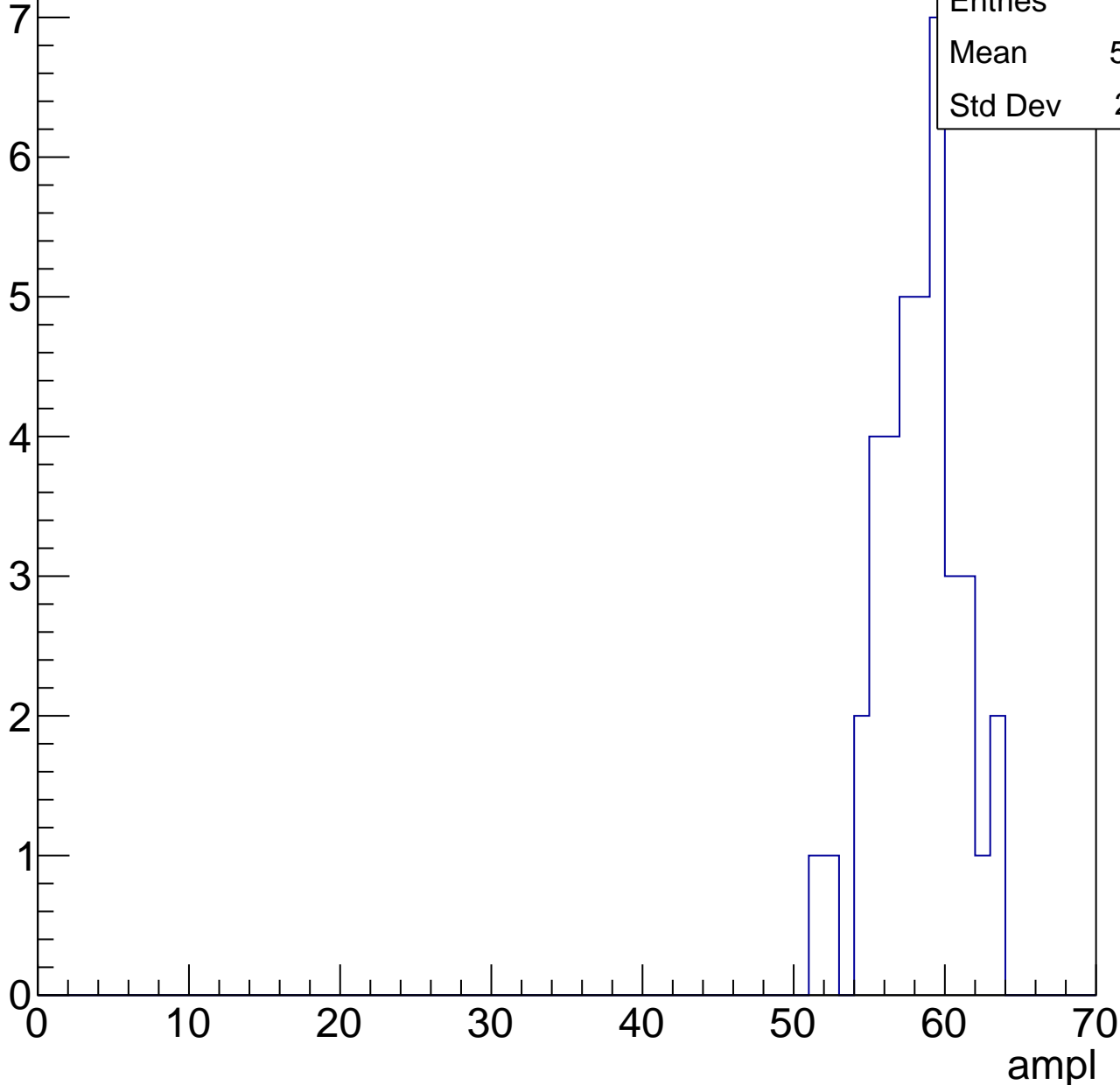


B1L102S, U13-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	57.74
Std Dev	2.731

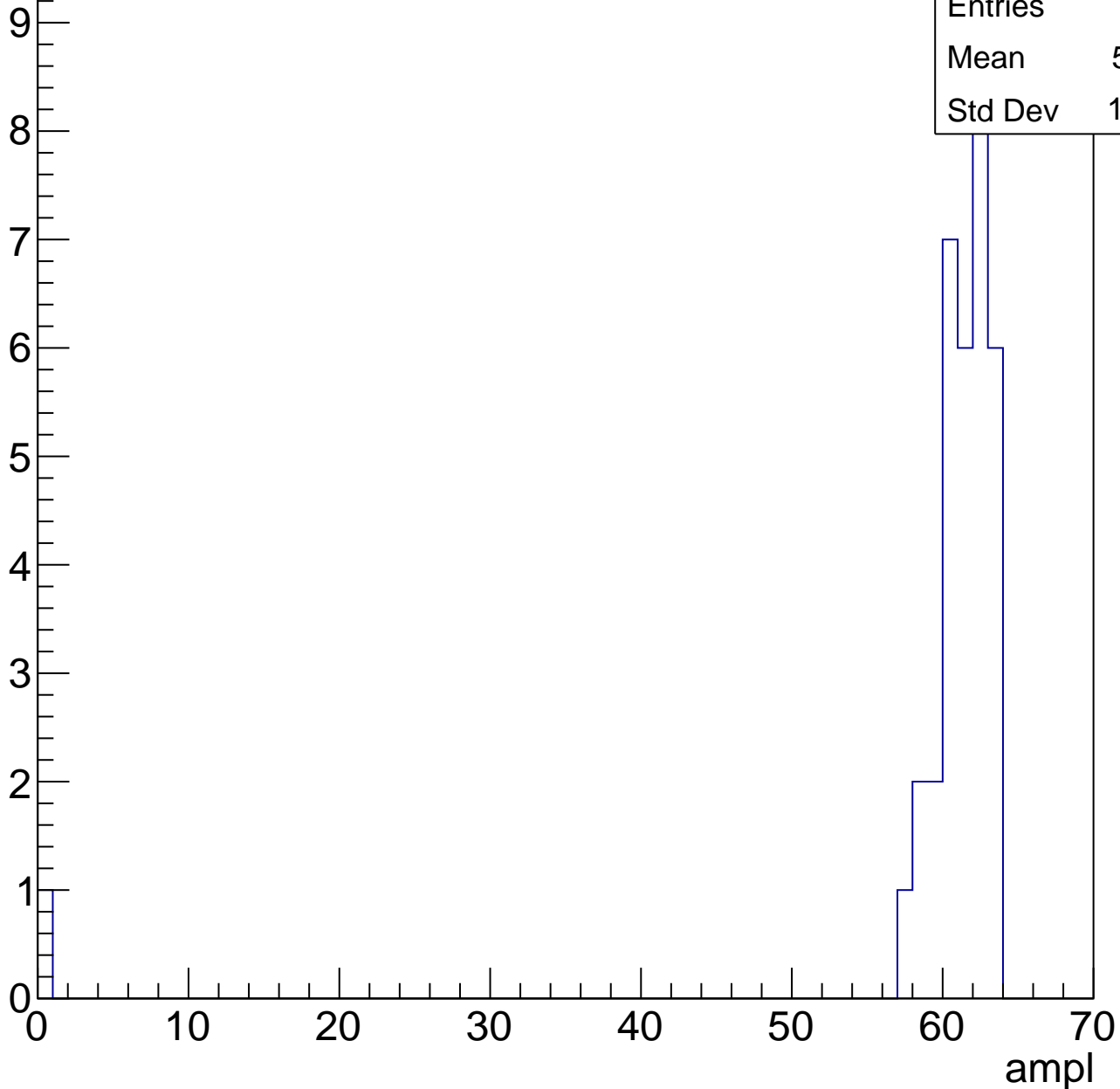


B1L102S, U13-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	59.21
Std Dev	10.42



B1L102S, U13-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch28, adc0

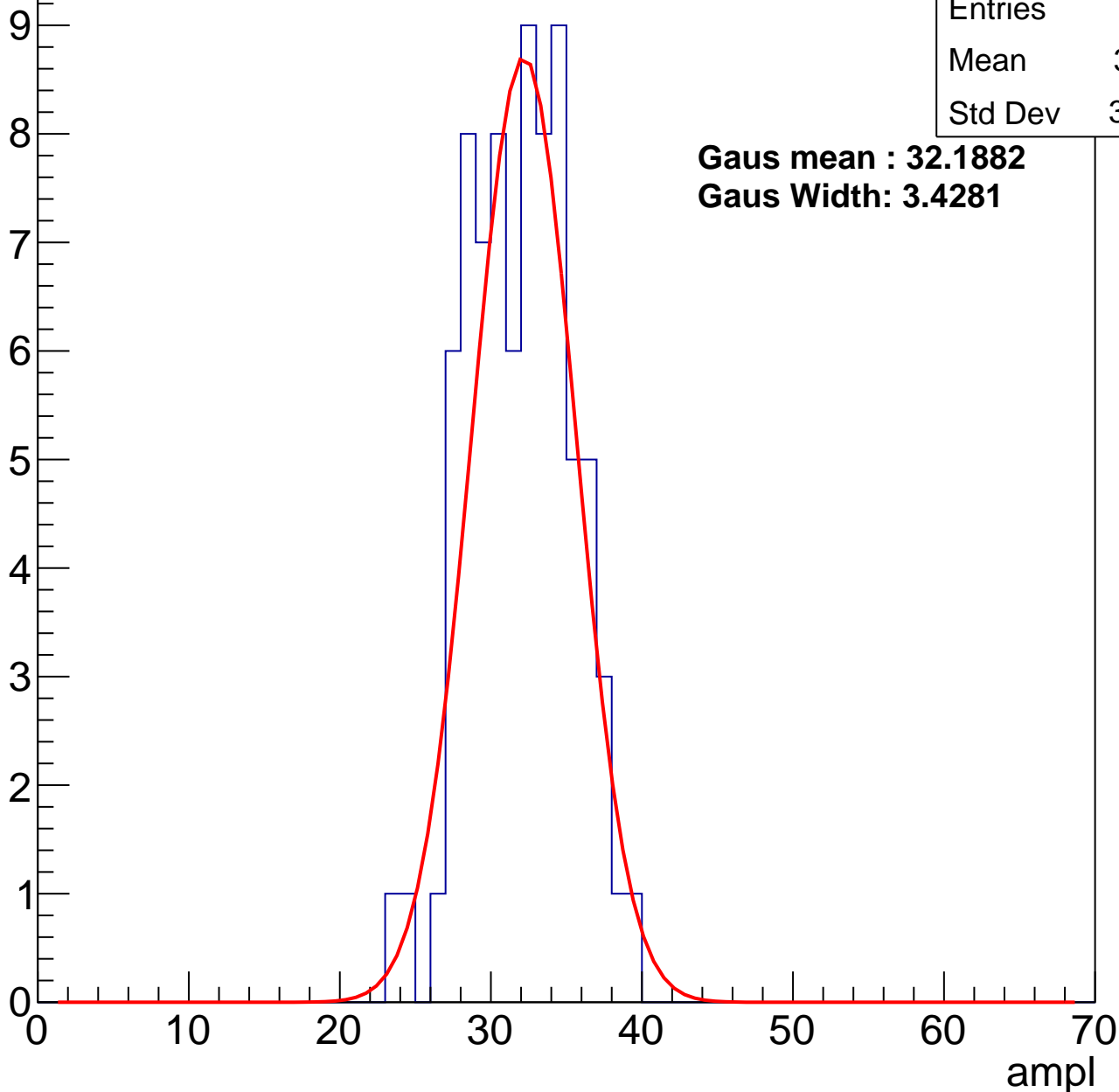
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	31.51
Std Dev	3.318

Gaus mean : 32.1882

Gaus Width: 3.4281



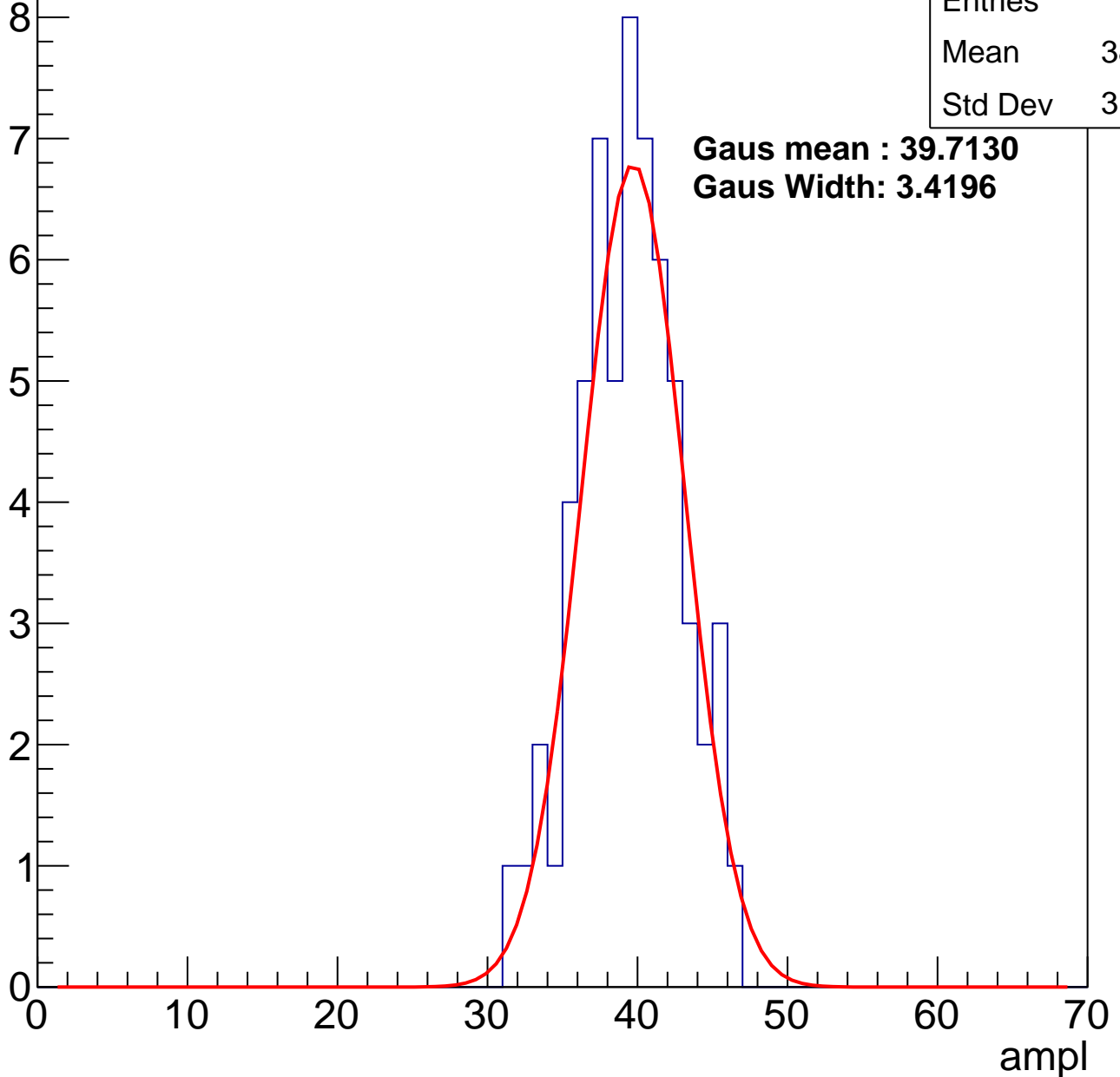
B1L102S, U13-ch28, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	38.98
Std Dev	3.346

Gaus mean : 39.7130
Gaus Width: 3.4196



B1L102S, U13-ch28, adc2

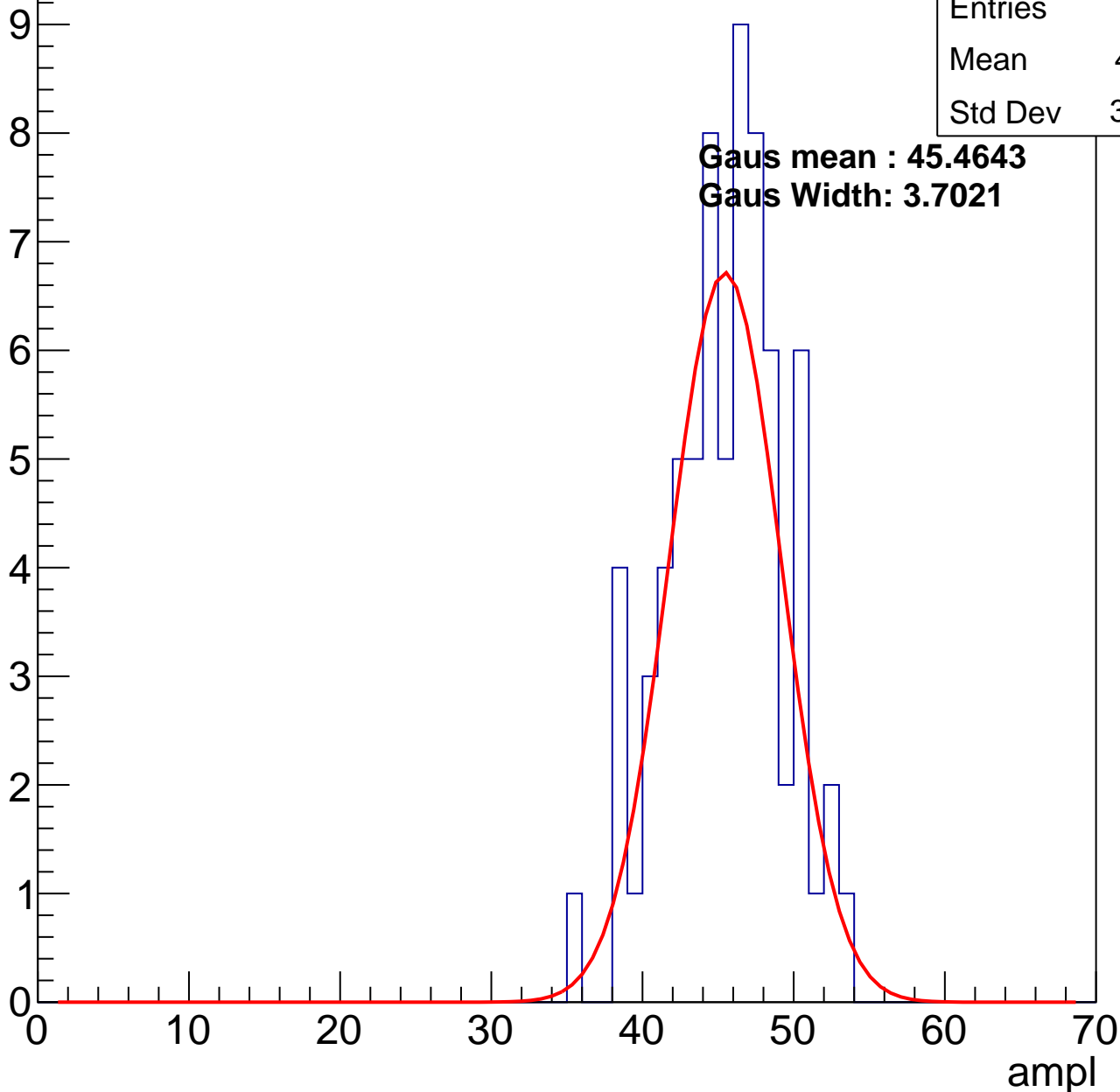
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	45.01
Std Dev	3.788

Gaus mean : 45.4643

Gaus Width: 3.7021

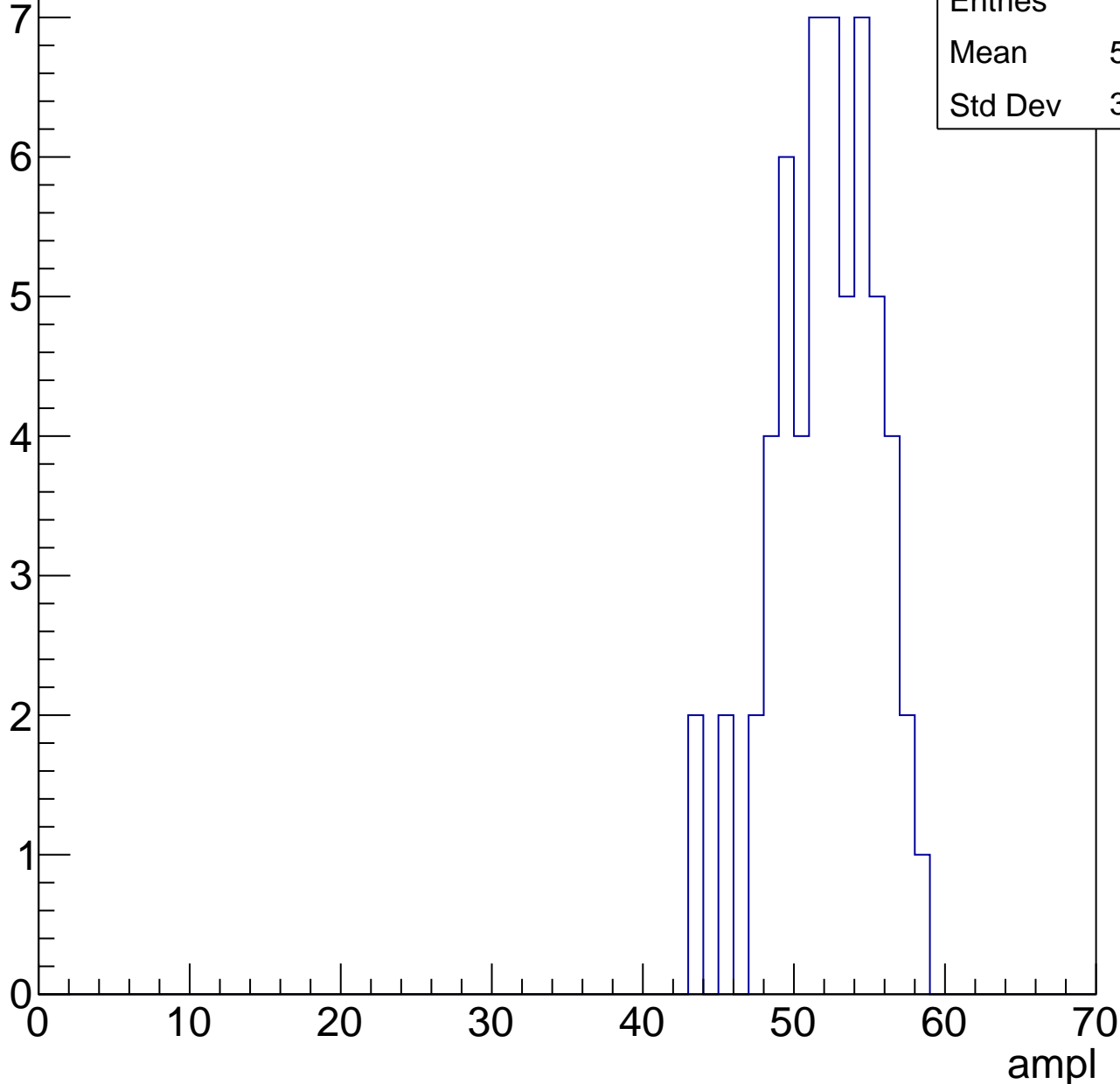


B1L102S, U13-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

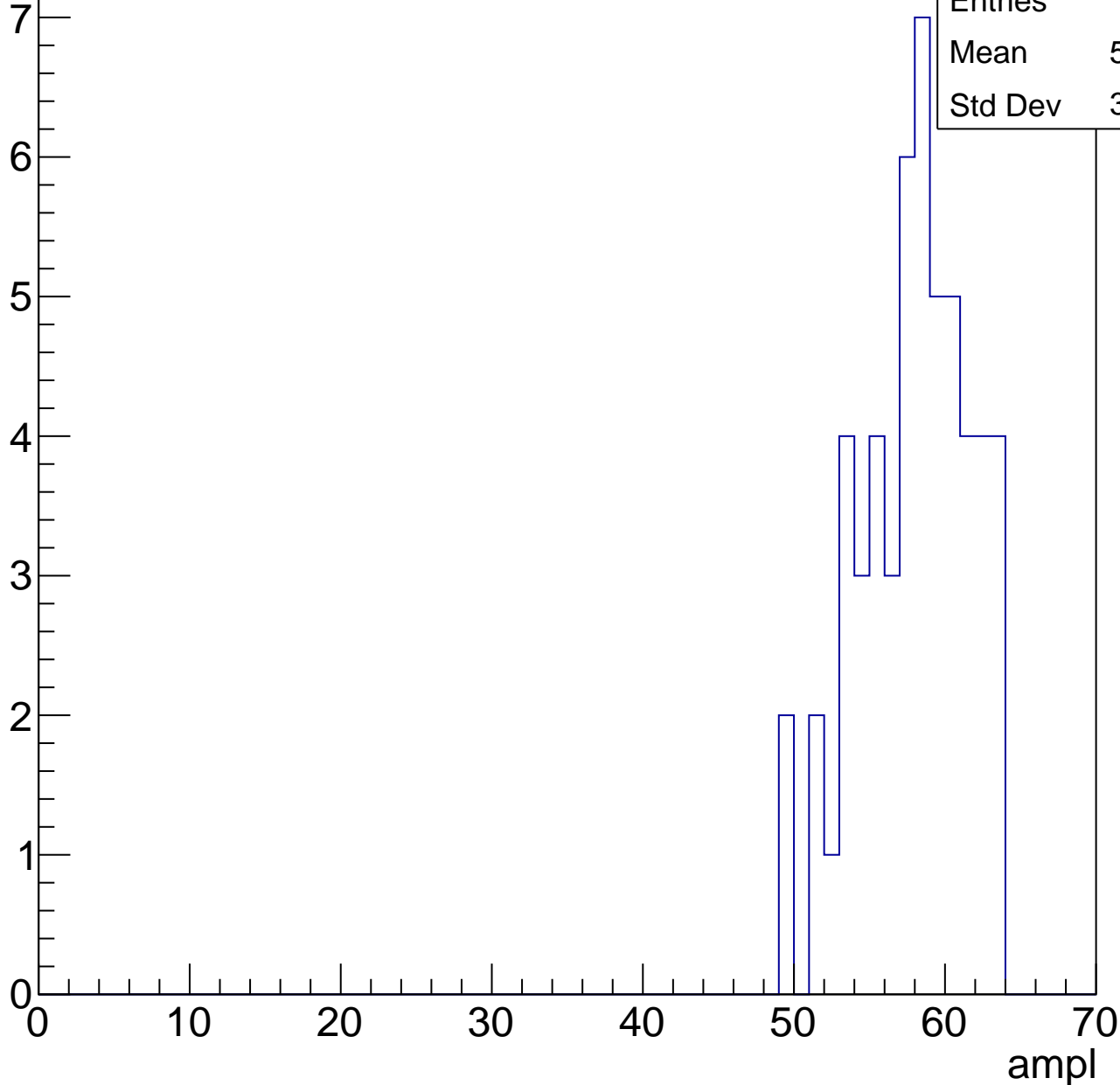
Entries	58
Mean	51.57
Std Dev	3.394



B1L102S, U13-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch28, adc5

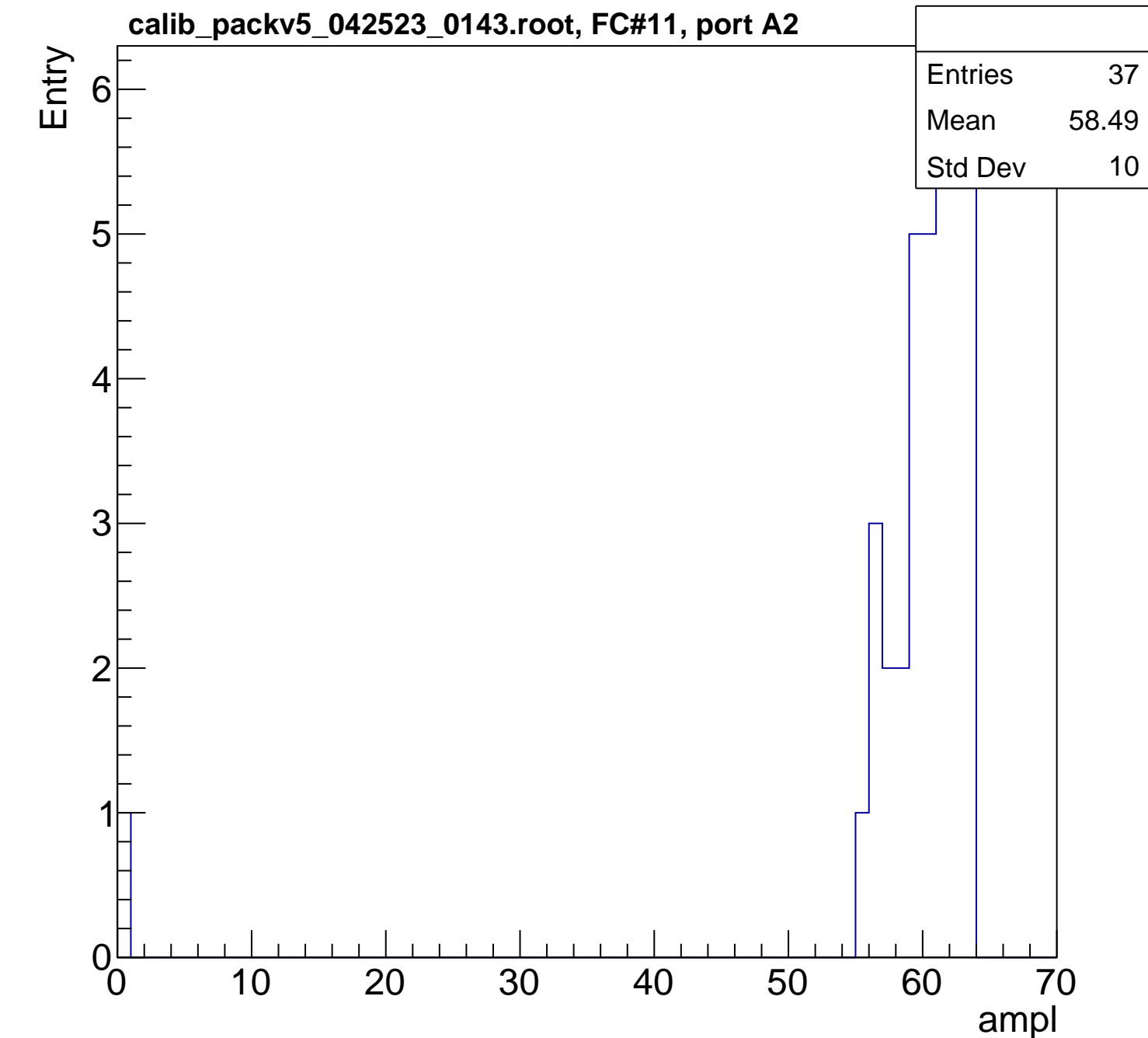
calib_packv5_042523_0143.root, FC#11, port A2

Entry

6
5
4
3
2
1
0

Entries	37
Mean	58.49
Std Dev	10

ampl



B1L102S, U13-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch29, adc0

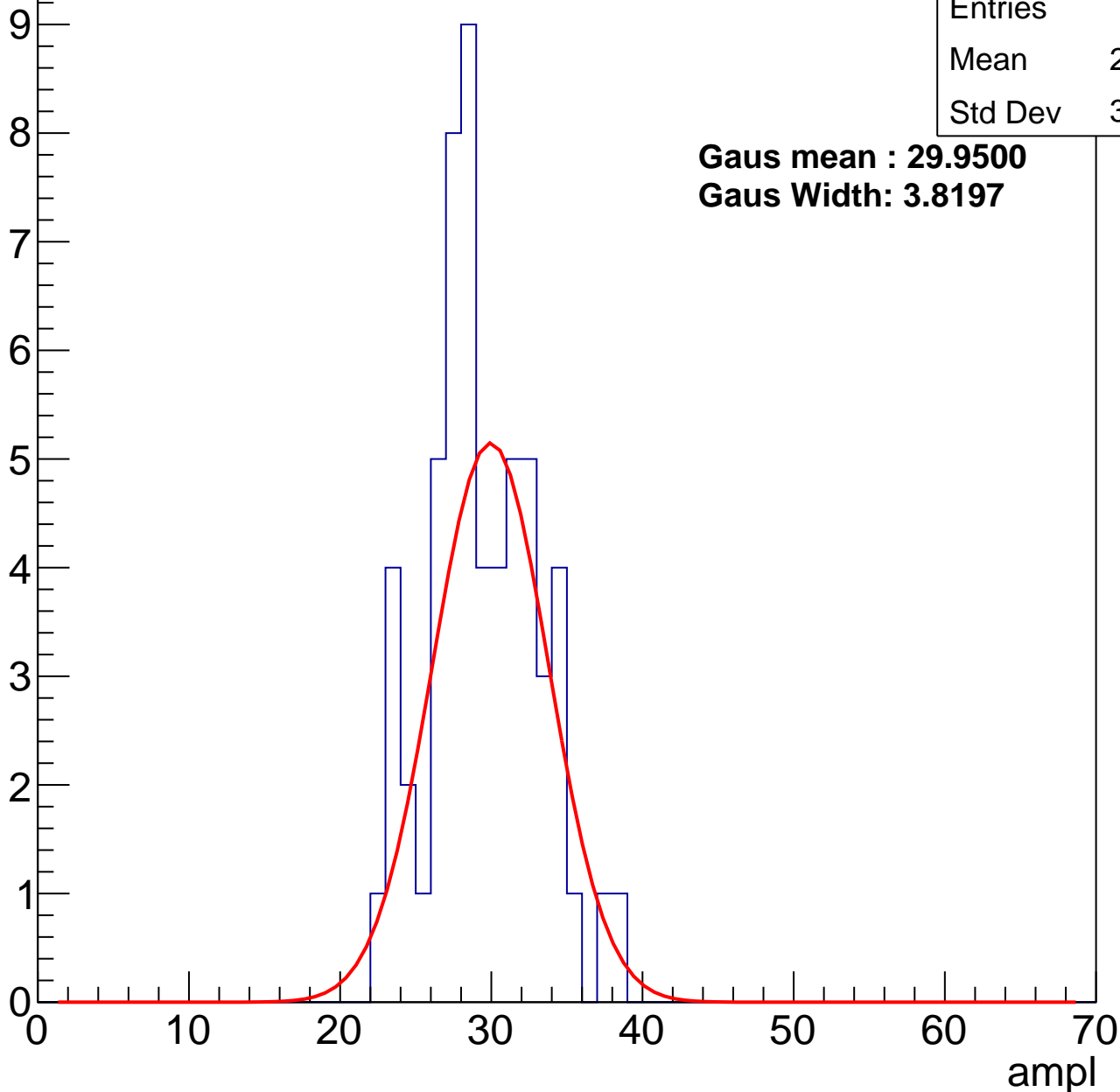
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.98
Std Dev	3.579

Gaus mean : 29.9500

Gaus Width: 3.8197



B1L102S, U13-ch29, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	34.39
Std Dev	3.128

Gaus mean : 34.6228

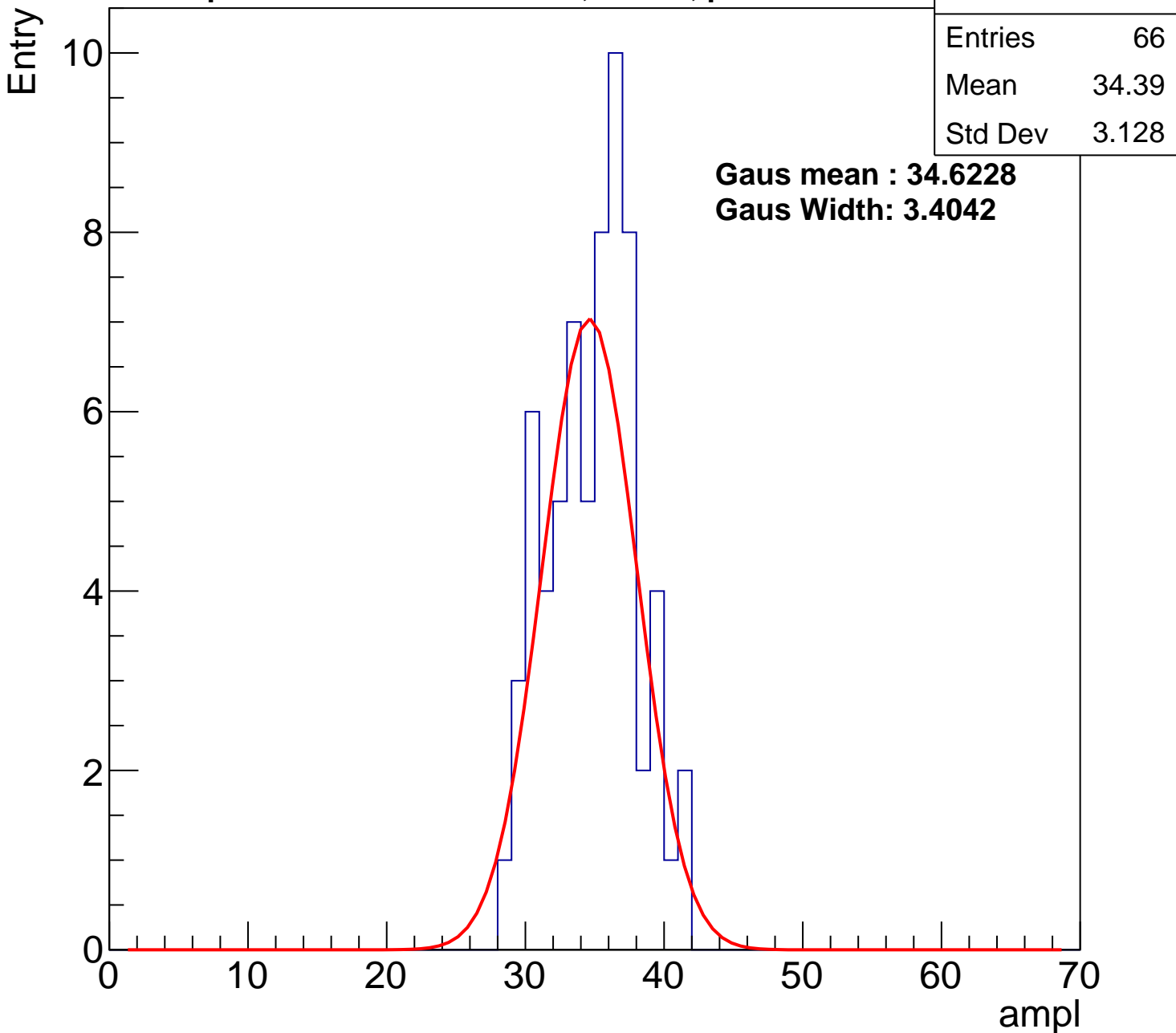
Gaus Width: 3.4042

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch29, adc2

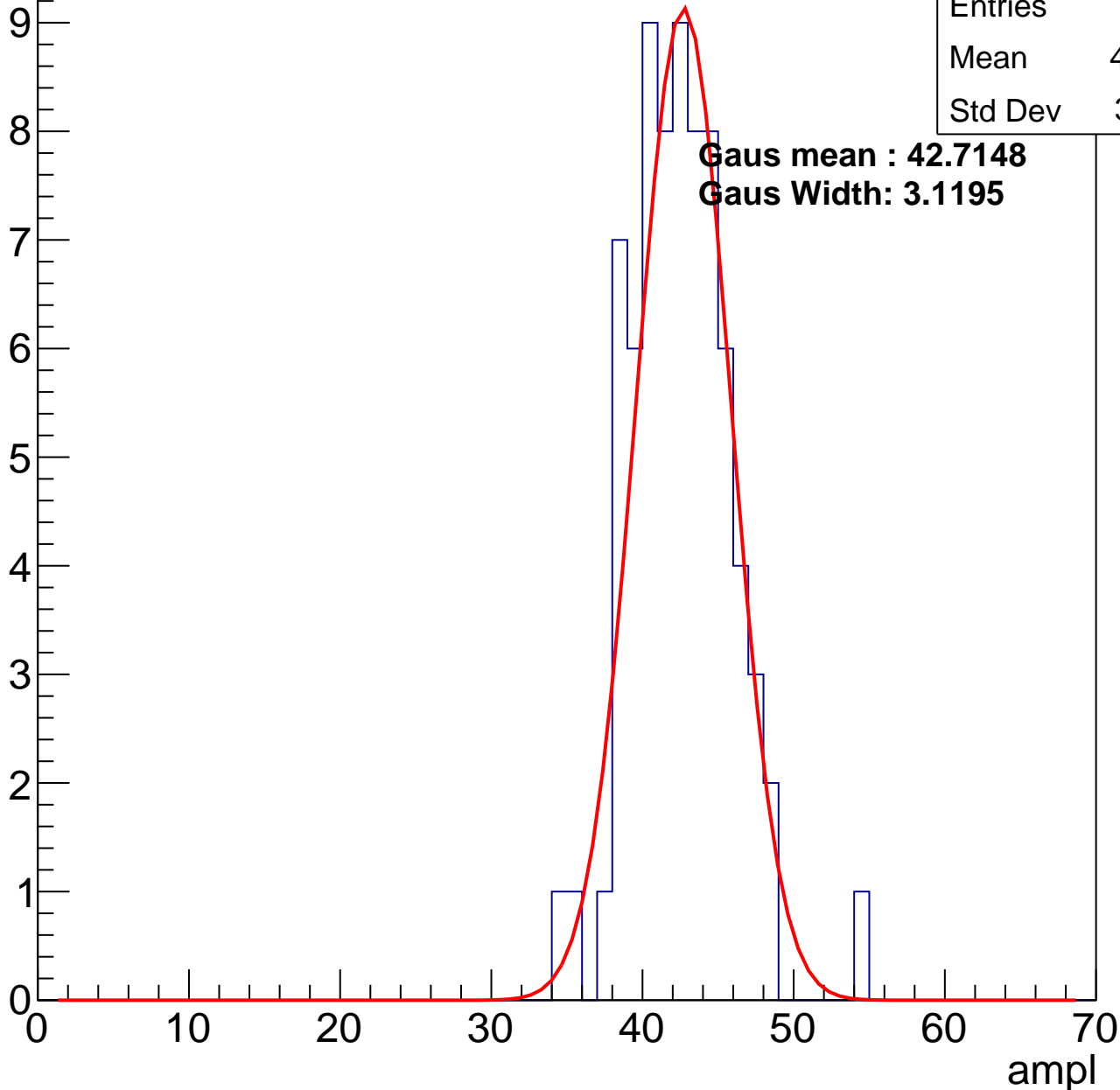
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.07
Std Dev	3.281

Gaus mean : 42.7148

Gaus Width: 3.1195

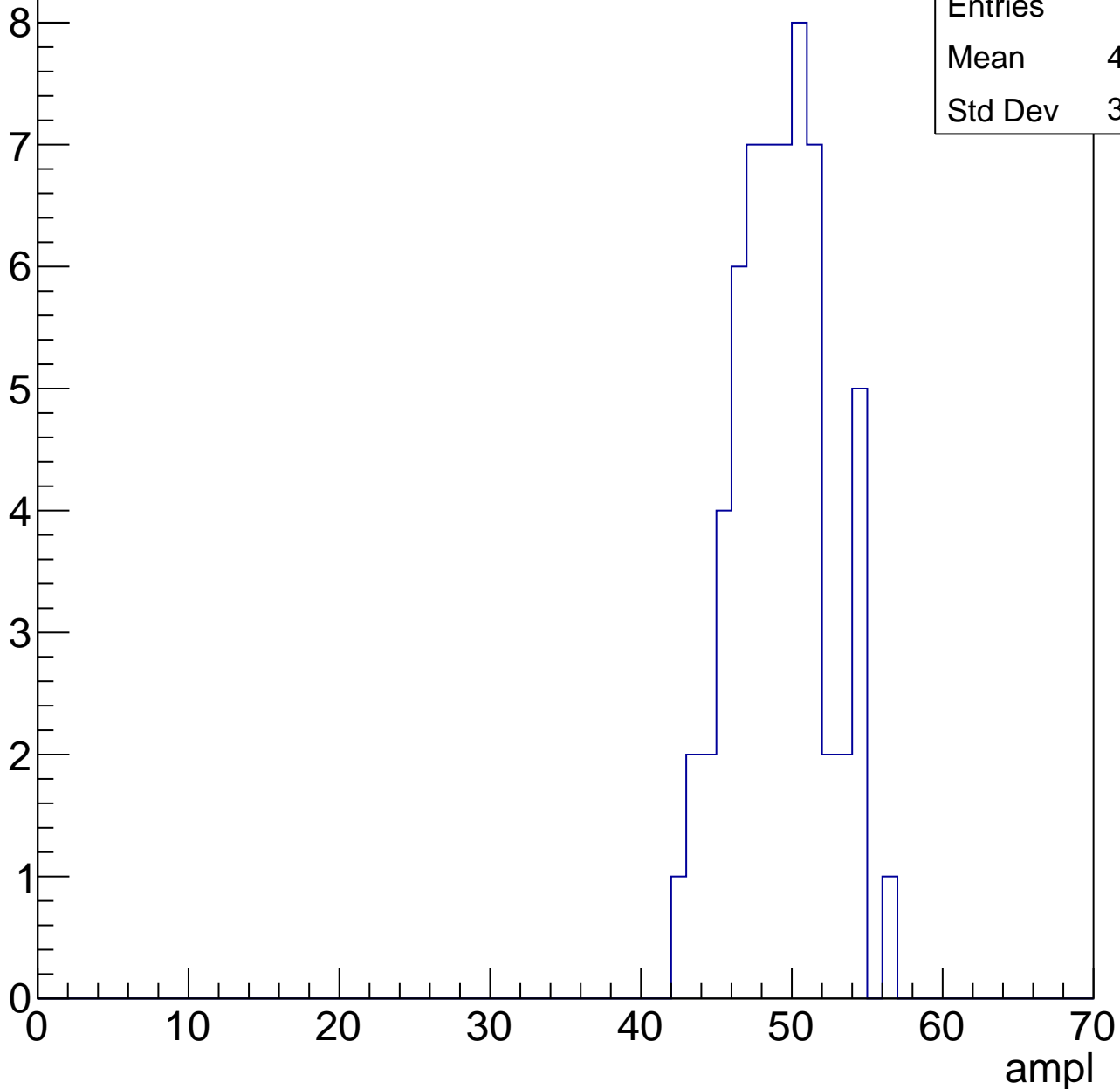


B1L102S, U13-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.74
Std Dev	3.083



B1L102S, U13-ch29, adc4

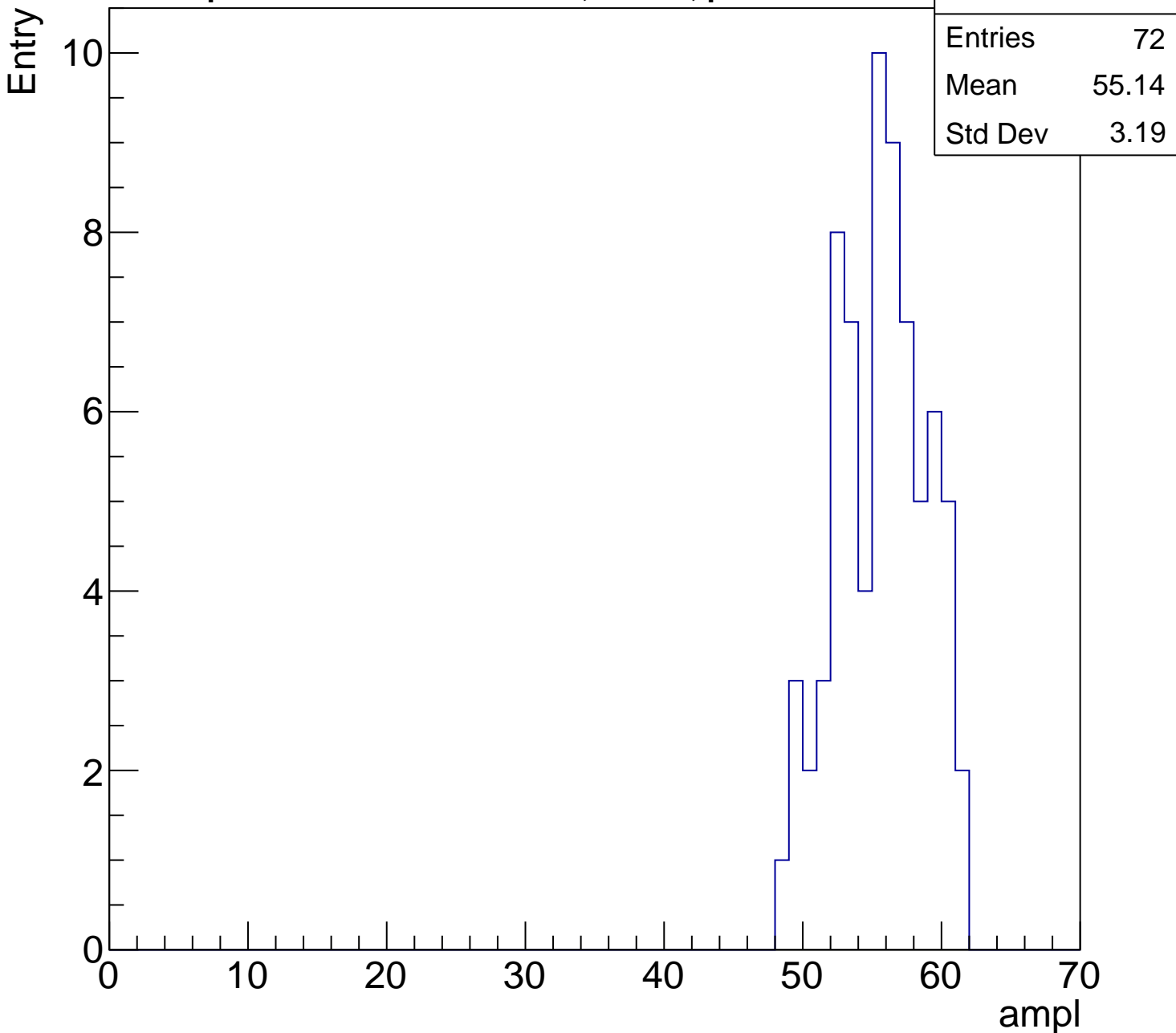
calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	55.14
Std Dev	3.19

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

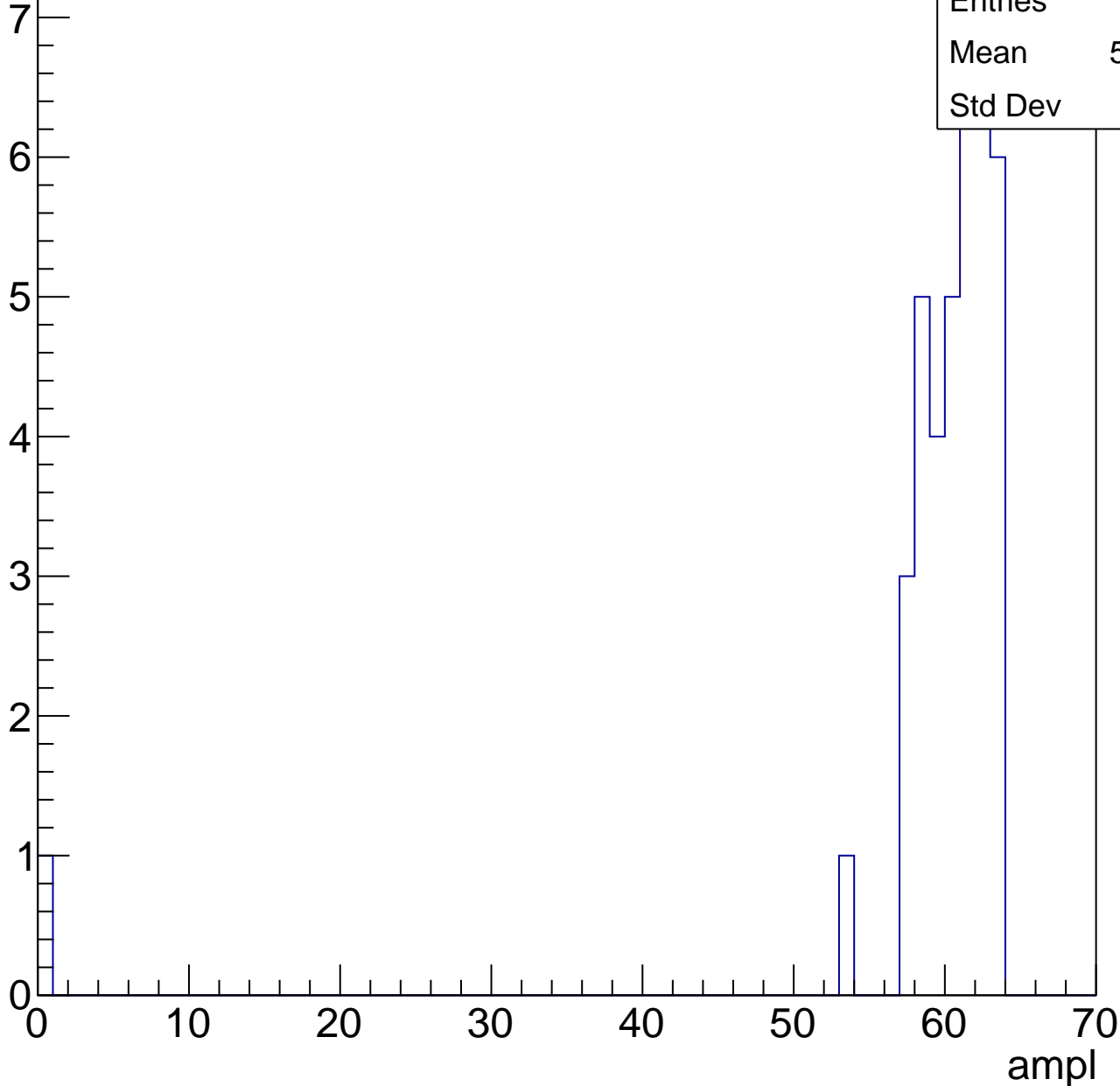


B1L102S, U13-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

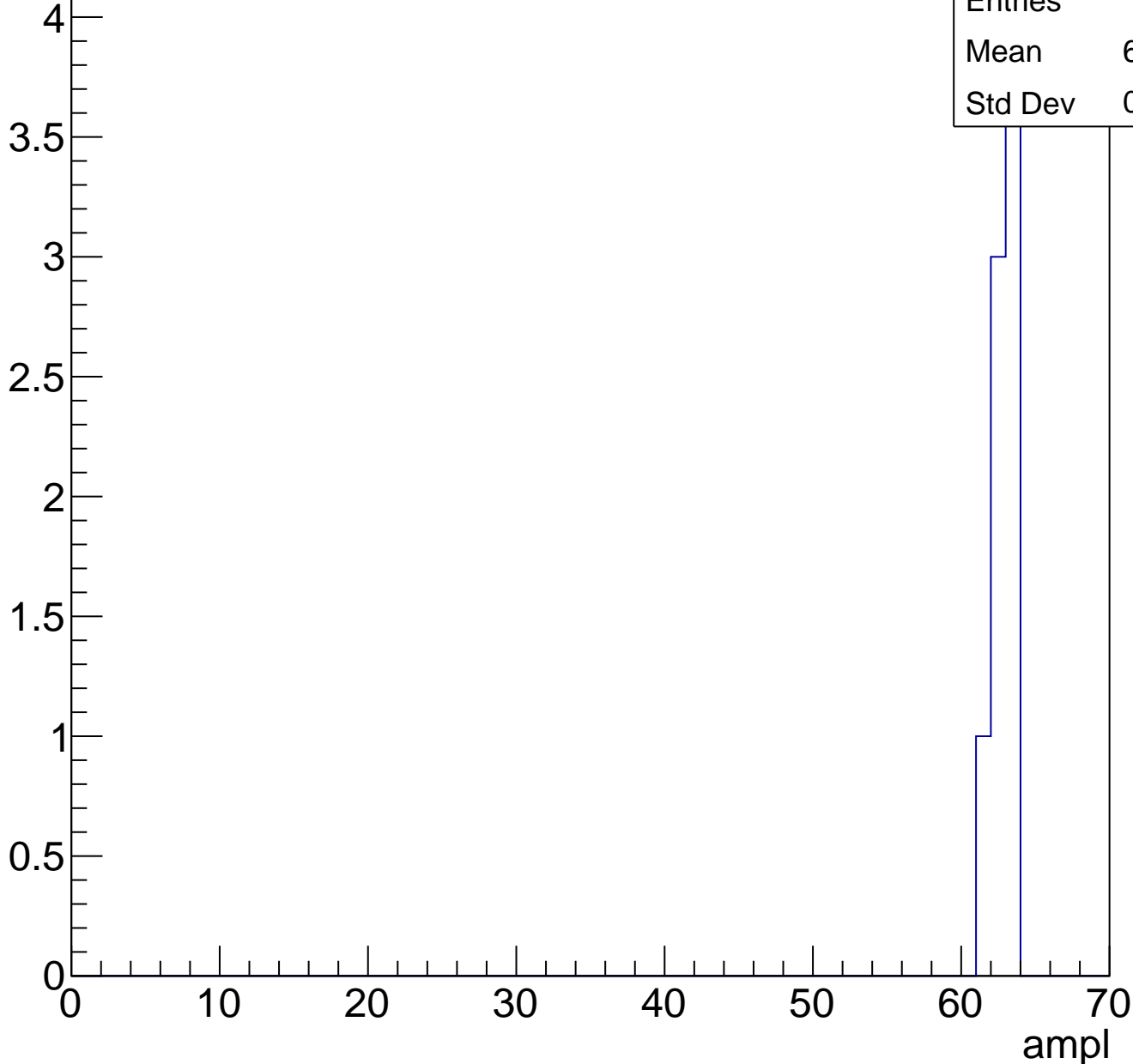
Entries	39
Mean	58.69
Std Dev	9.77



B1L102S, U13-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch30, adc0

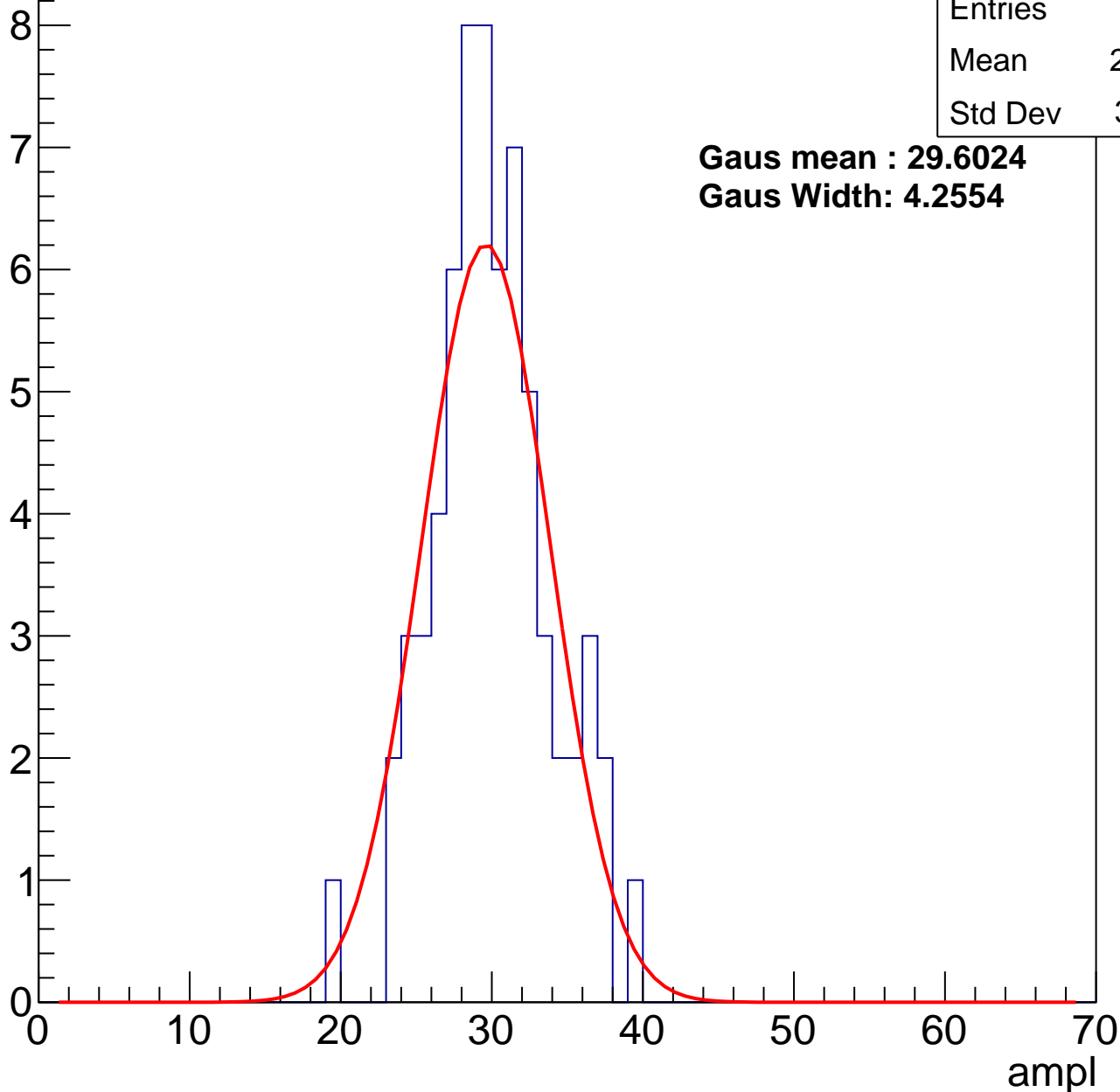
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.53
Std Dev	3.831

Gaus mean : 29.6024

Gaus Width: 4.2554



B1L102S, U13-ch30, adc1

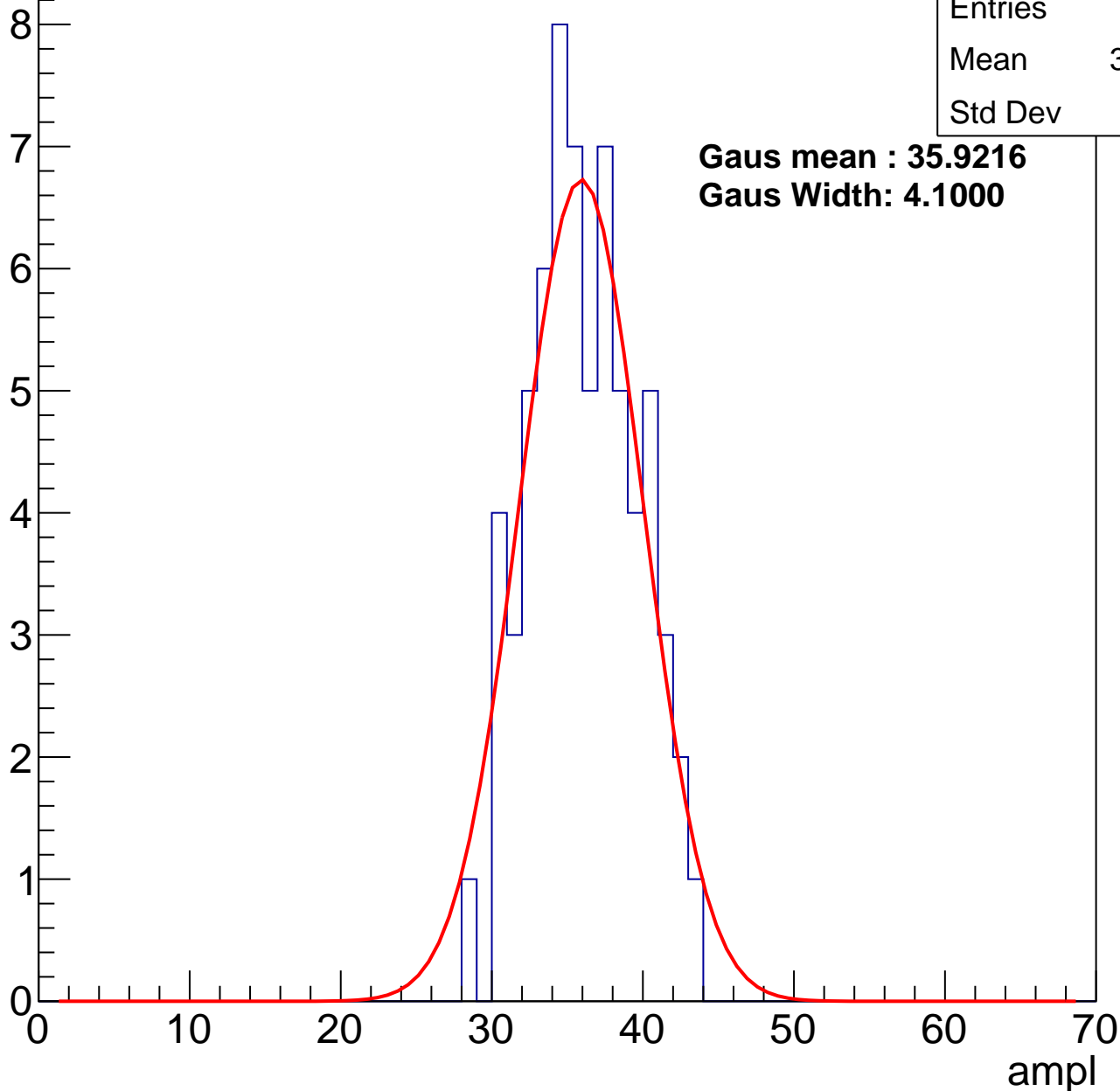
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.62
Std Dev	3.45

Gaus mean : 35.9216

Gaus Width: 4.1000



B1L102S, U13-ch30, adc2

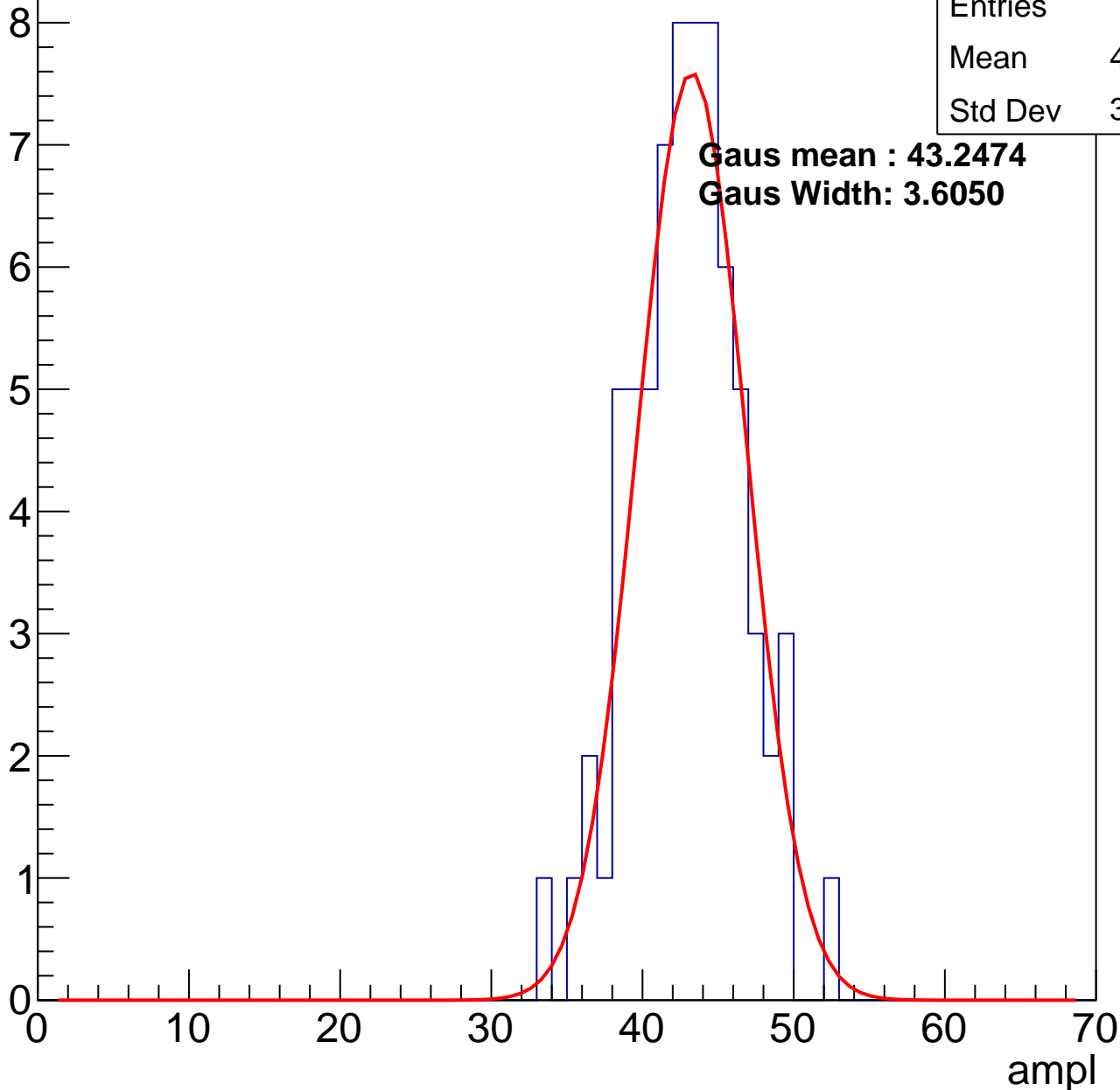
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.49
Std Dev	3.619

Gaus mean : 43.2474

Gaus Width: 3.6050

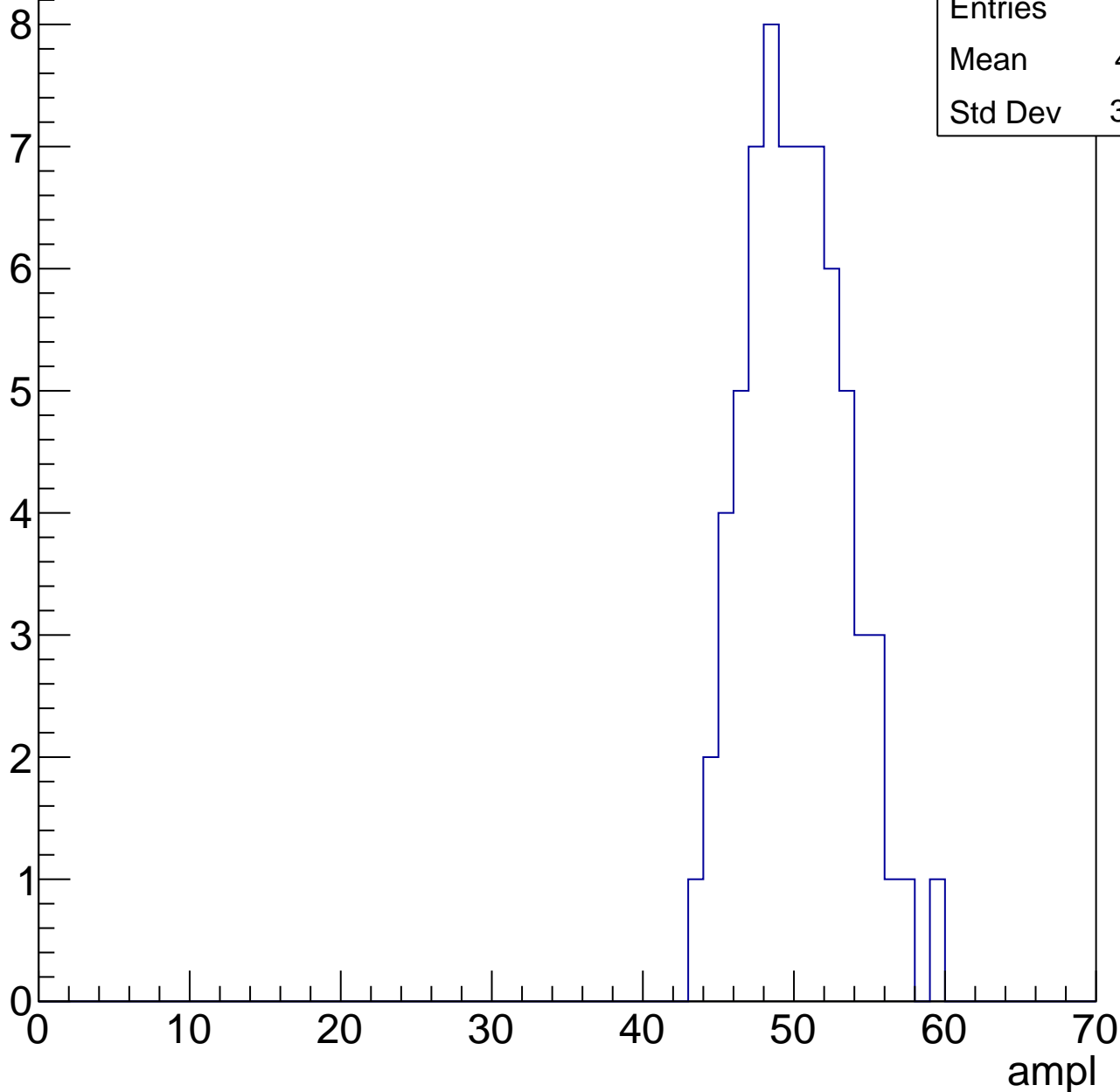


B1L102S, U13-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.71
Std Dev	3.348

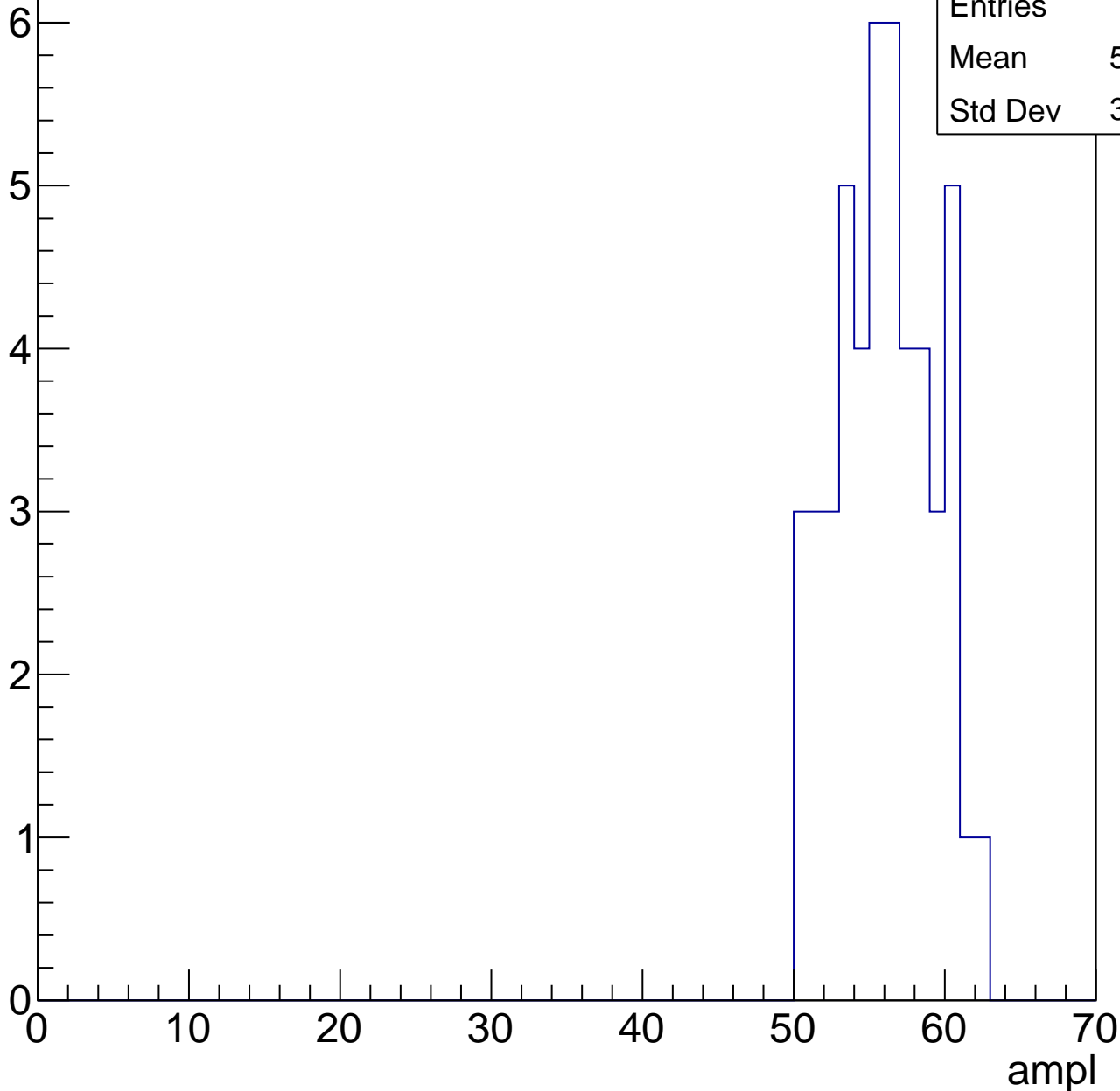


B1L102S, U13-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.54
Std Dev	3.149

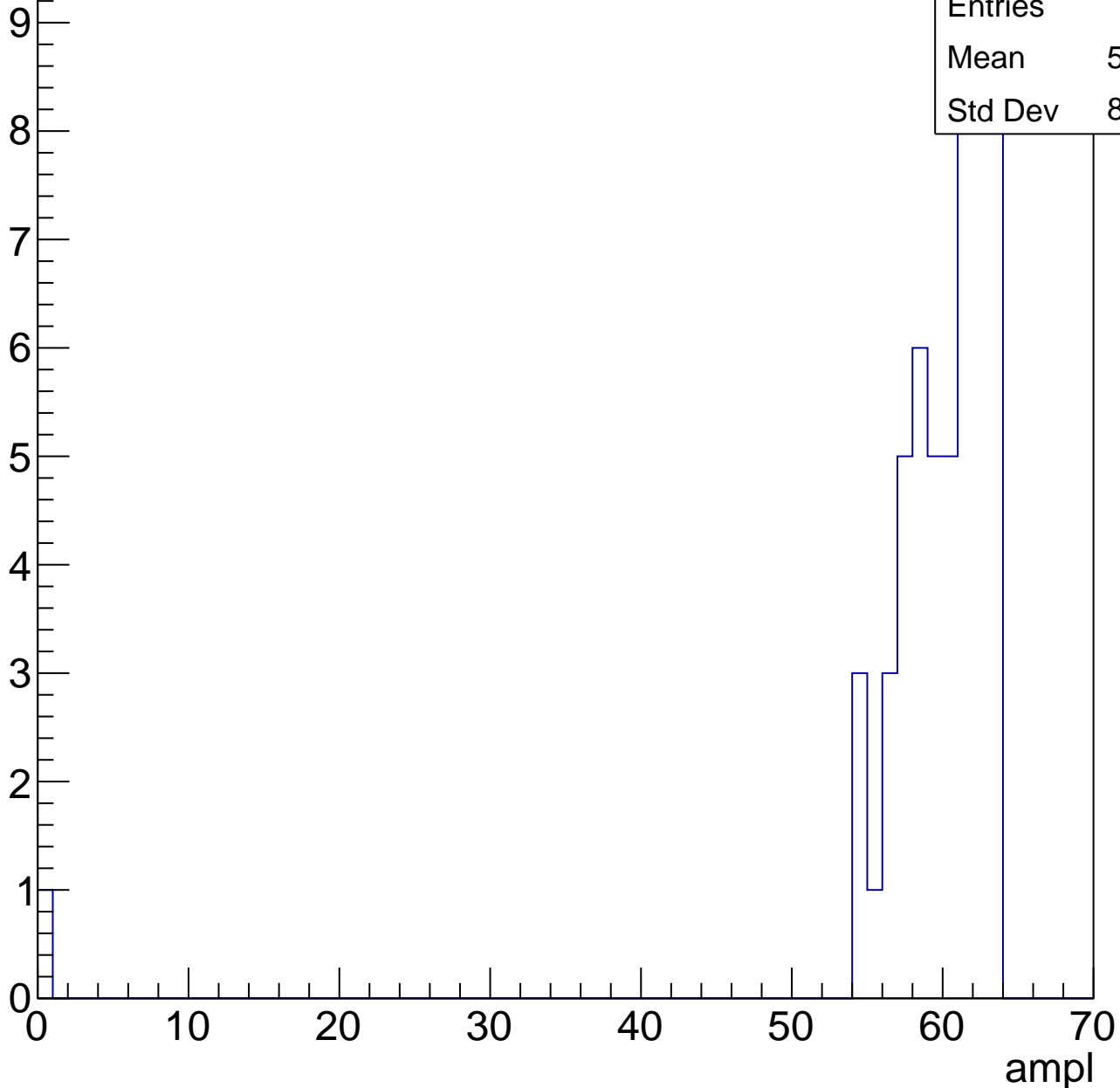


B1L102S, U13-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

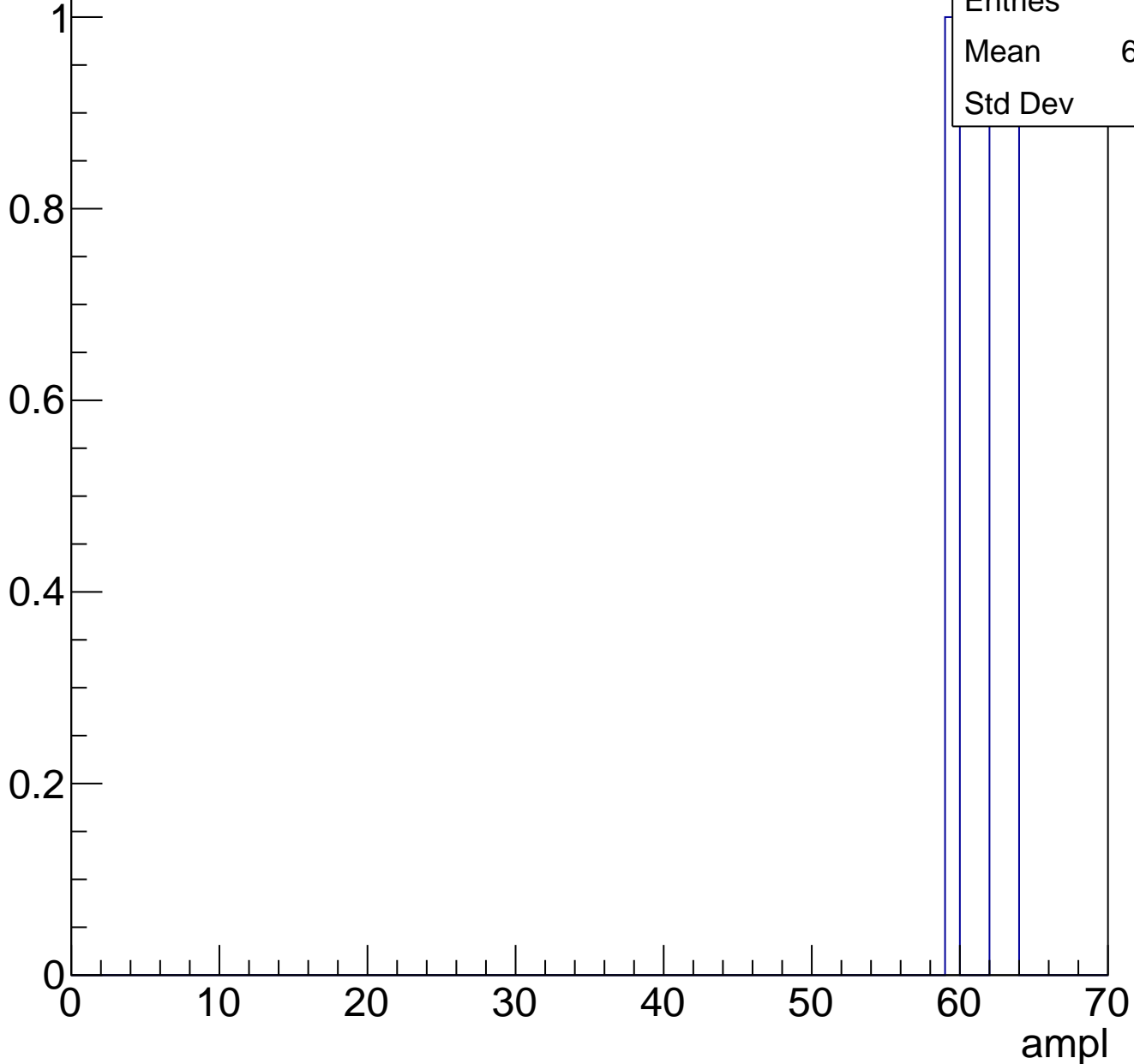
Entries	55
Mean	58.64
Std Dev	8.393



B1L102S, U13-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

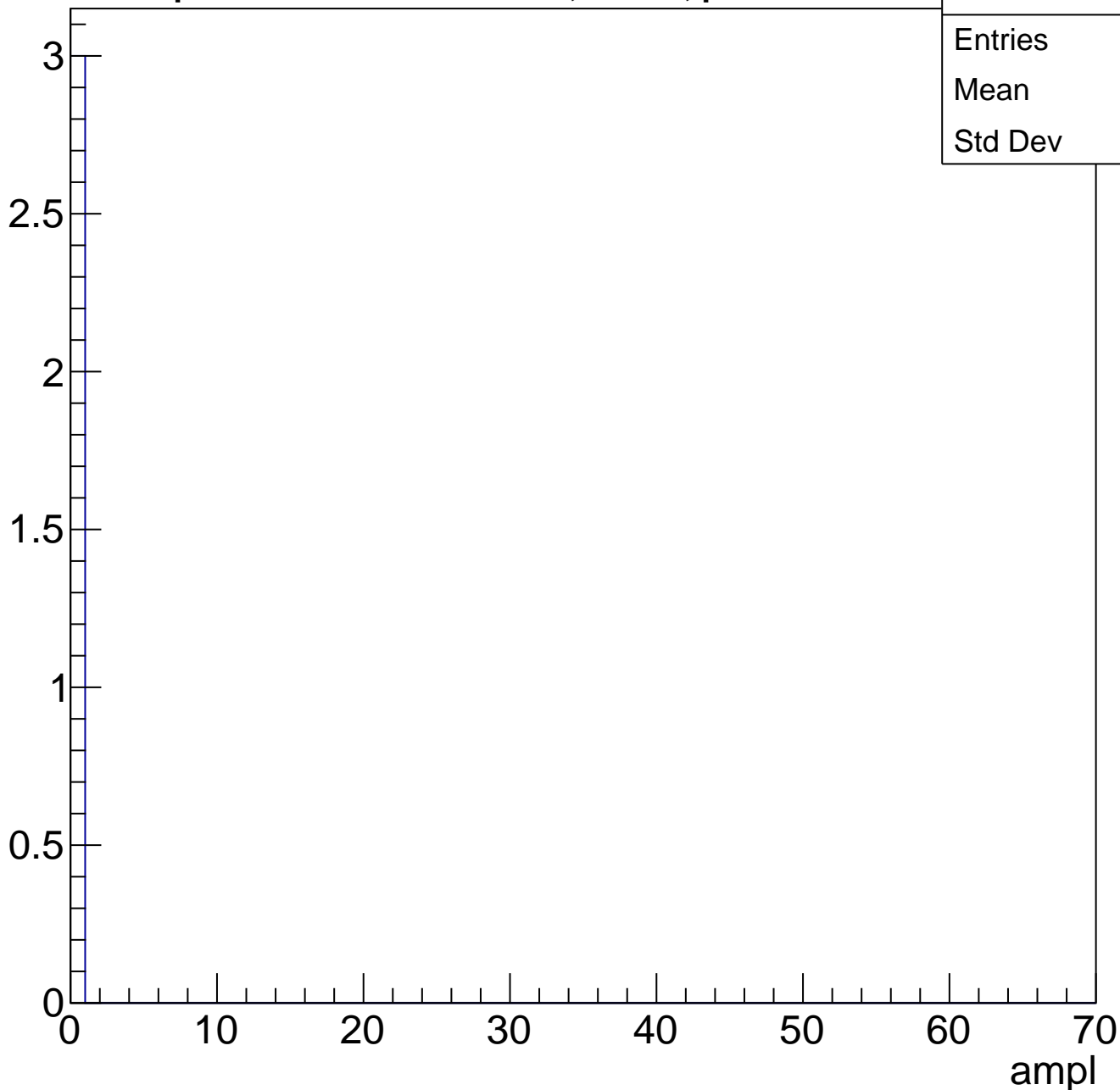
Entry



B1L102S, U13-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch31, adc0

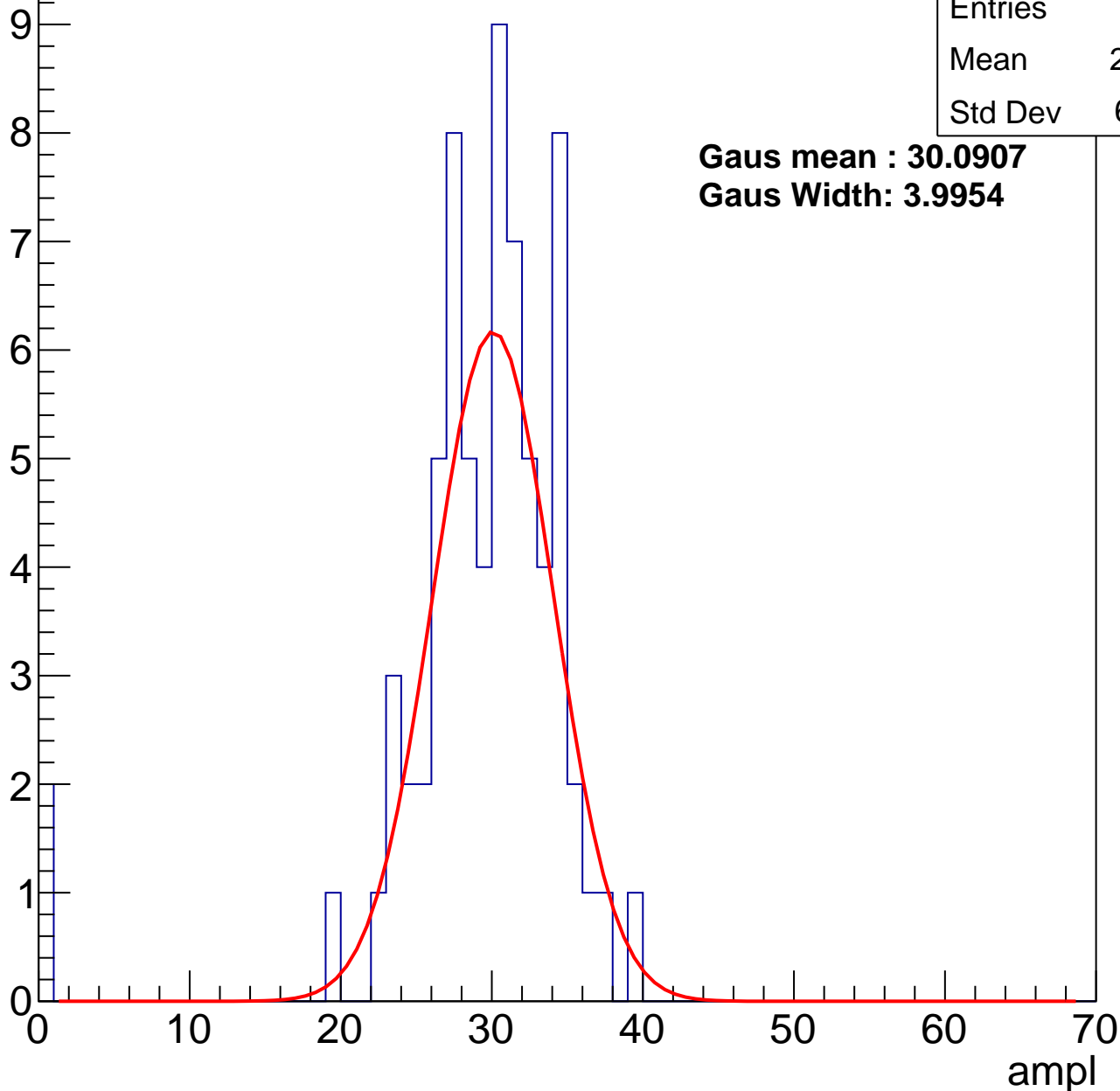
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.77
Std Dev	6.201

Gaus mean : 30.0907

Gaus Width: 3.9954



B1L102S, U13-ch31, adc1

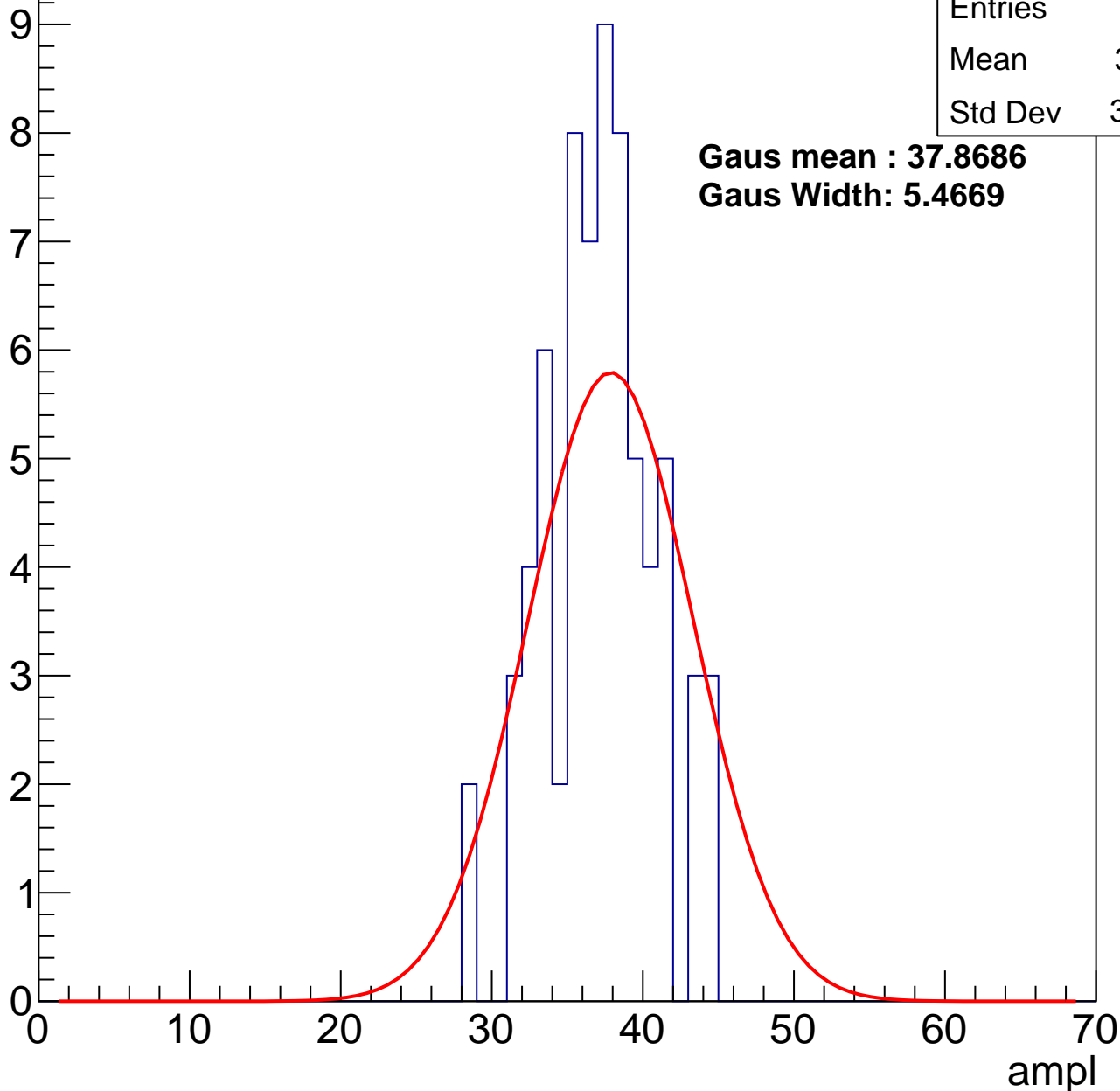
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.71
Std Dev	3.644

Gaus mean : 37.8686

Gaus Width: 5.4669



B1L102S, U13-ch31, adc2

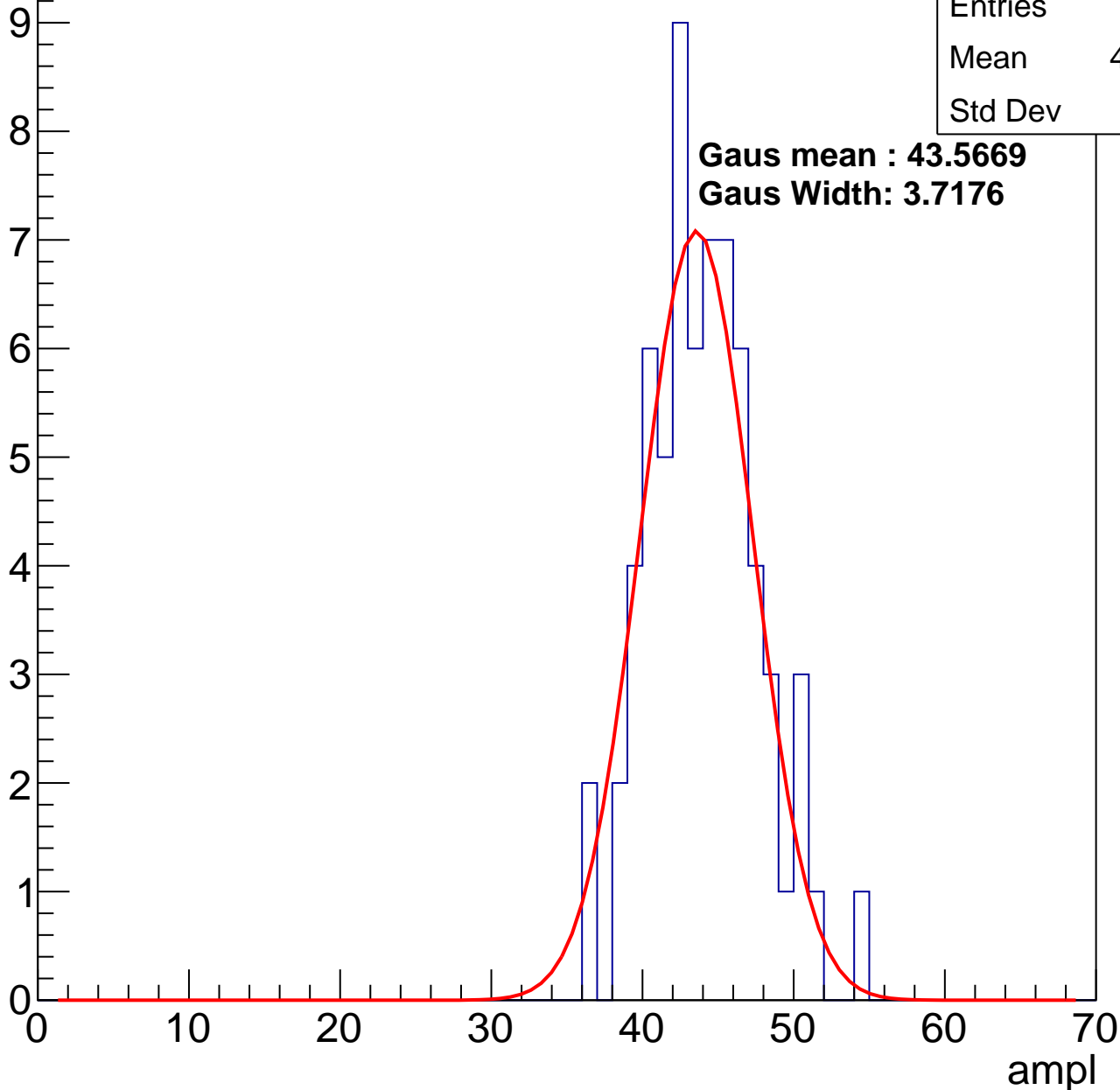
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.58
Std Dev	3.6

Gaus mean : 43.5669

Gaus Width: 3.7176

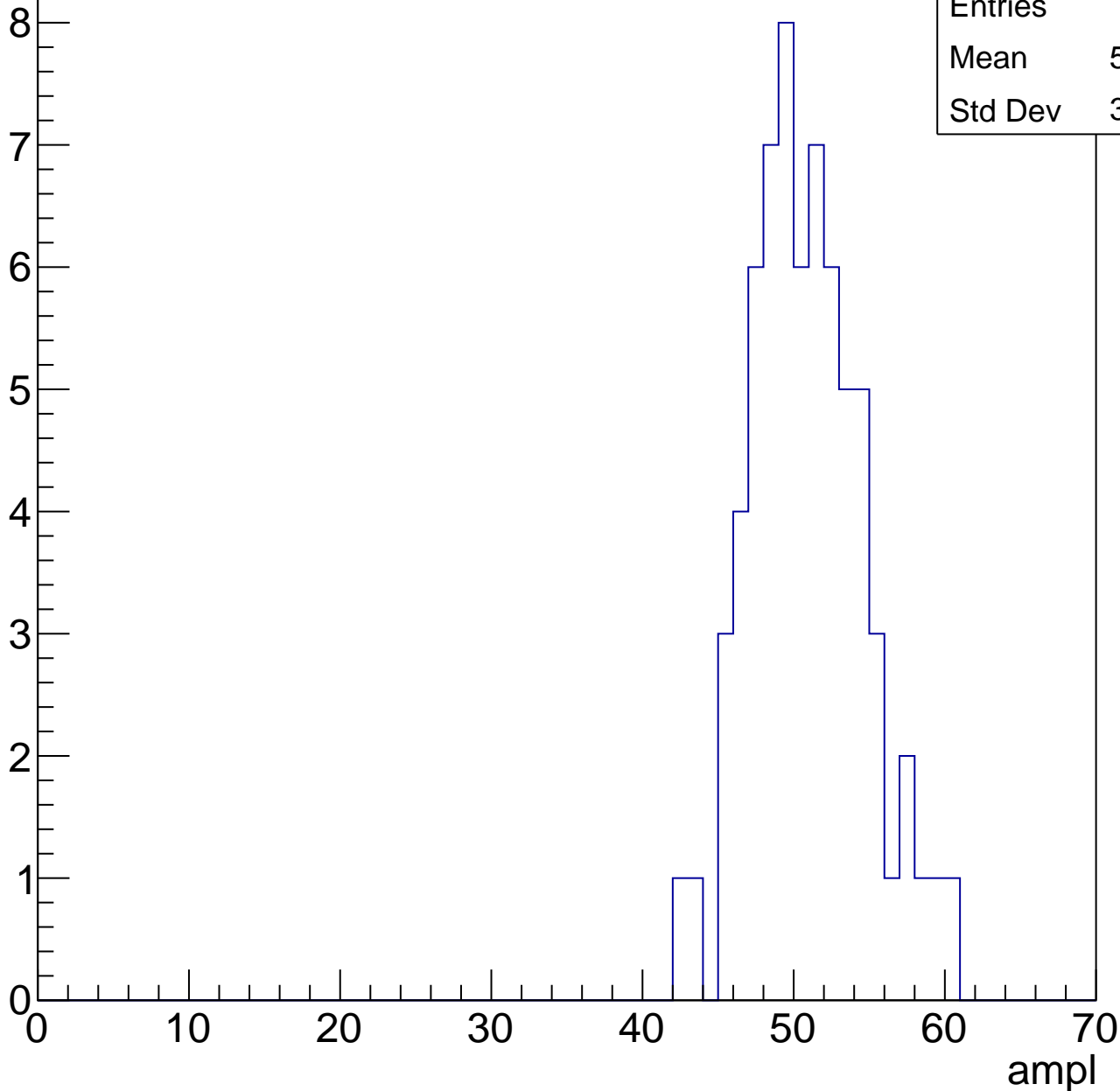


B1L102S, U13-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	50.44
Std Dev	3.716

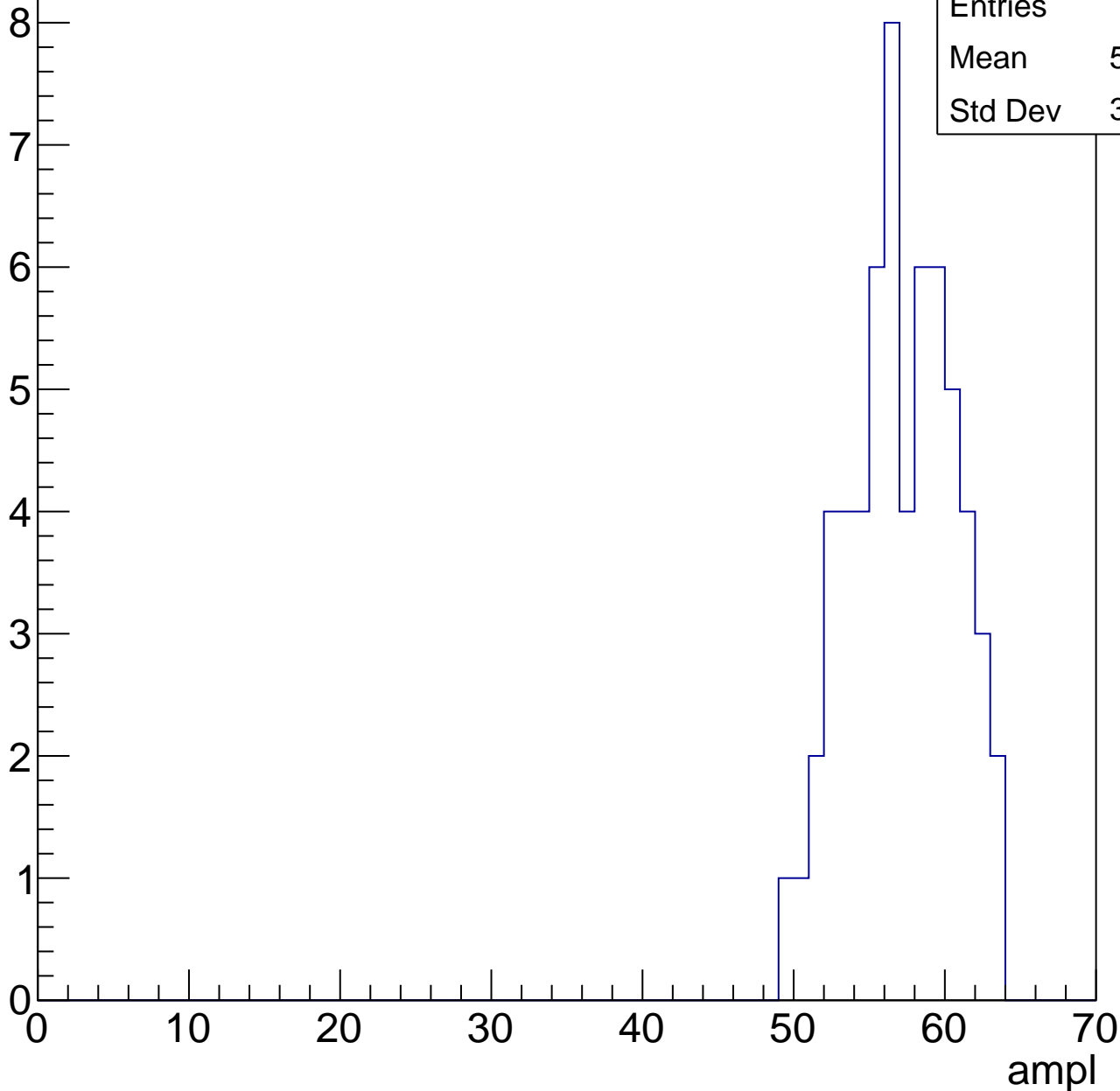


B1L102S, U13-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.68
Std Dev	3.408

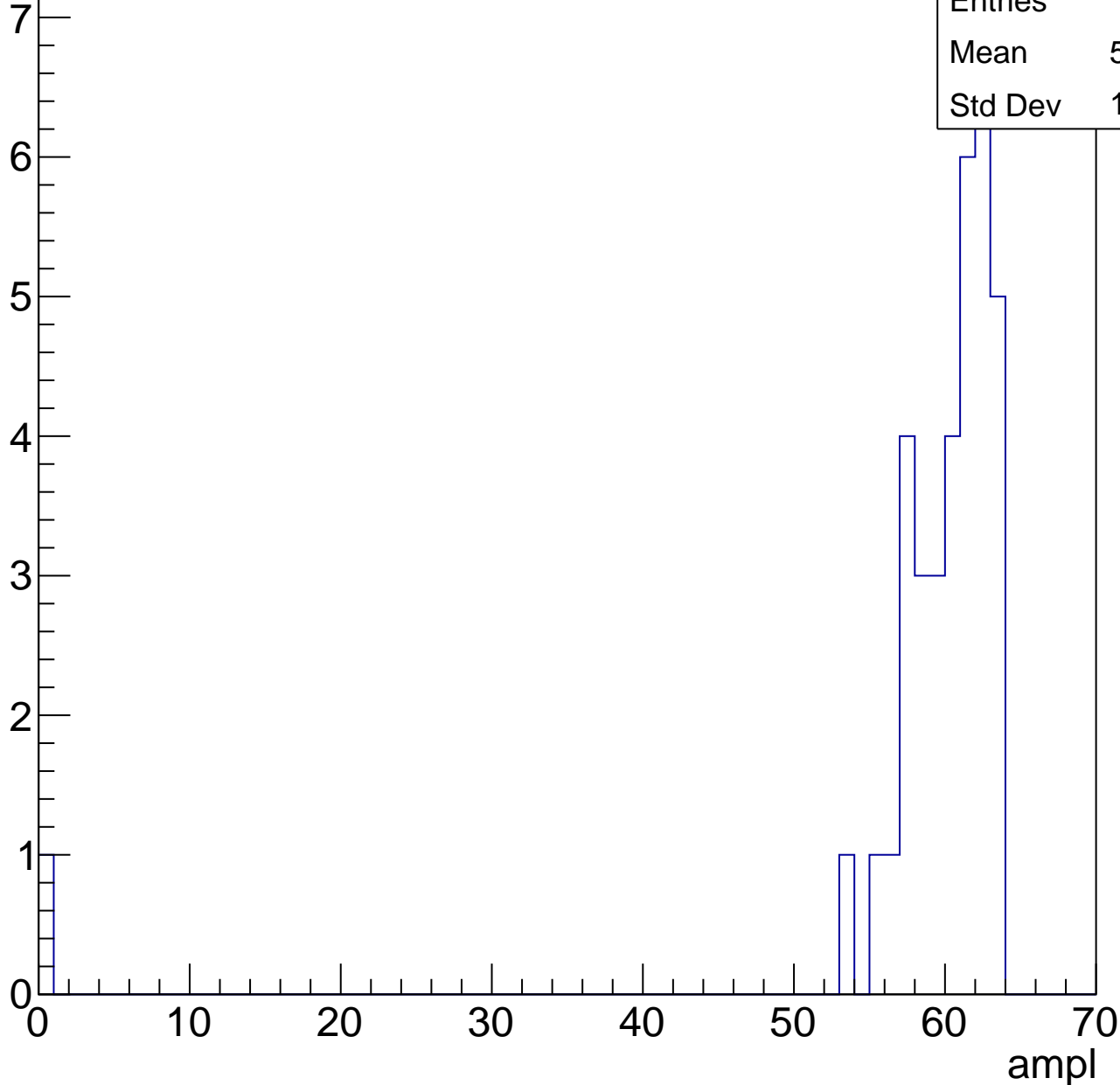


B1L102S, U13-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.28
Std Dev	10.16



B1L102S, U13-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch32, adc0

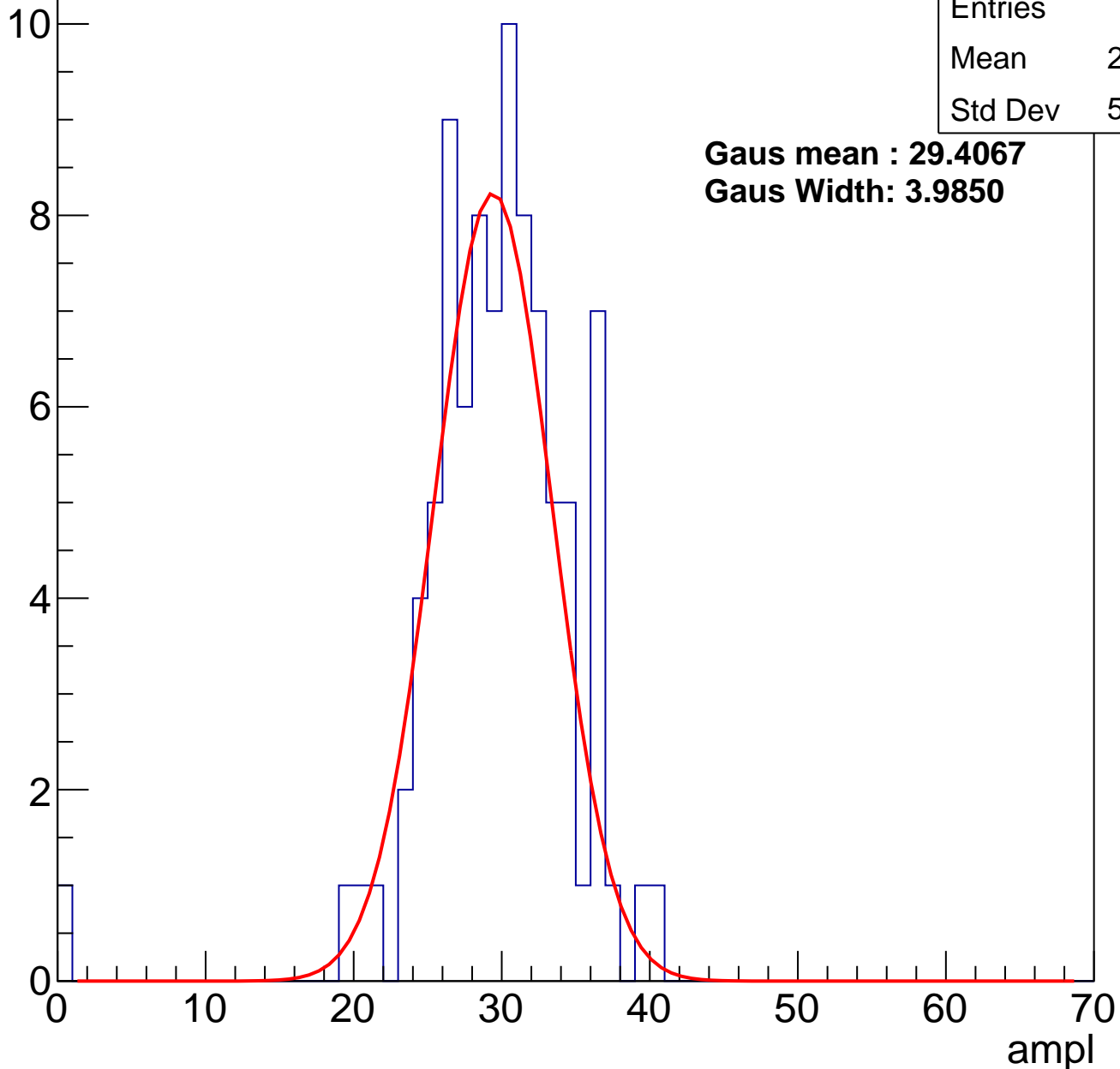
calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	29.23
Std Dev	5.157

Gaus mean : 29.4067

Gaus Width: 3.9850

Entry



B1L102S, U13-ch32, adc1

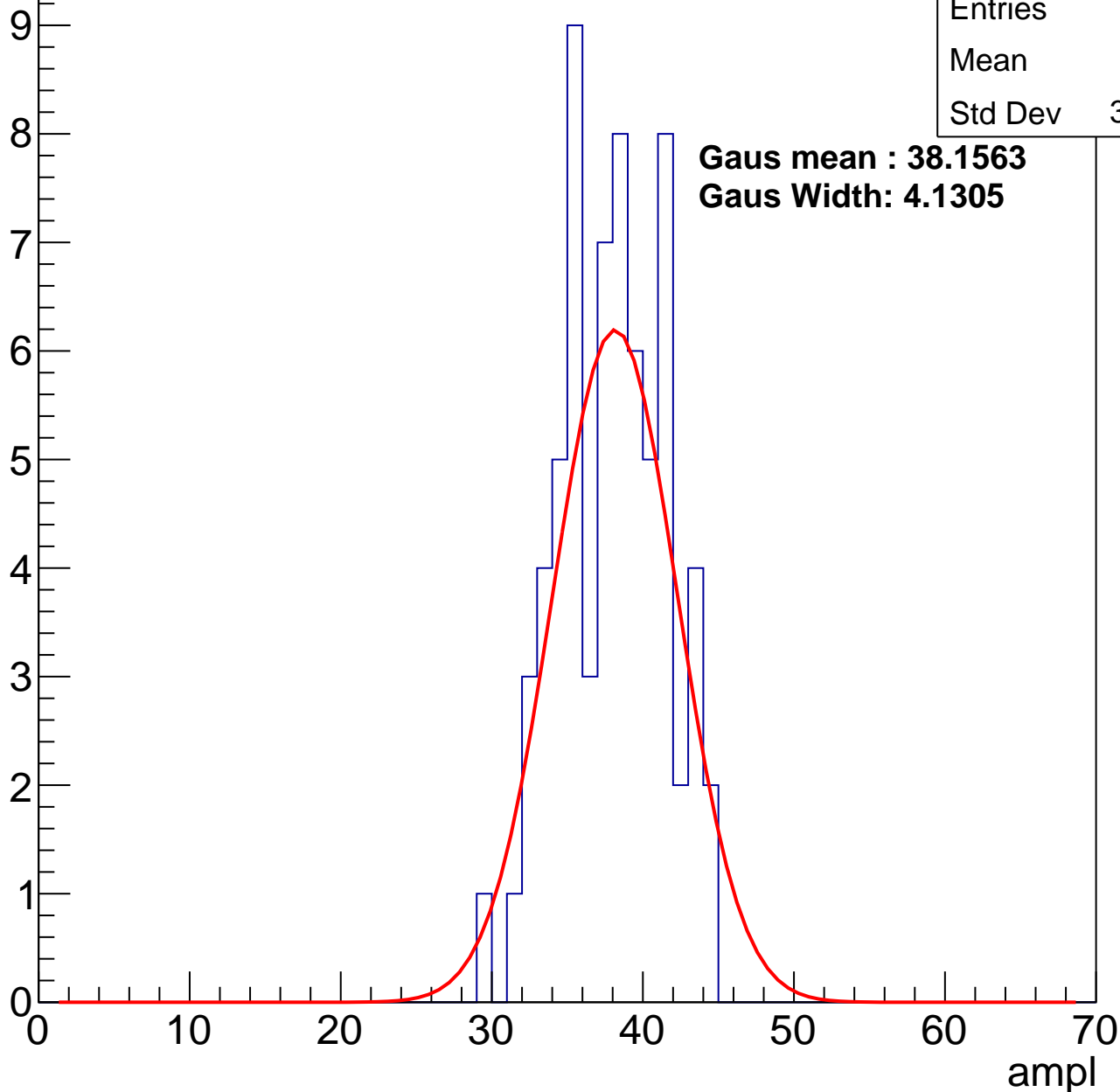
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.5
Std Dev	3.436

Gaus mean : 38.1563

Gaus Width: 4.1305



B1L102S, U13-ch32, adc2

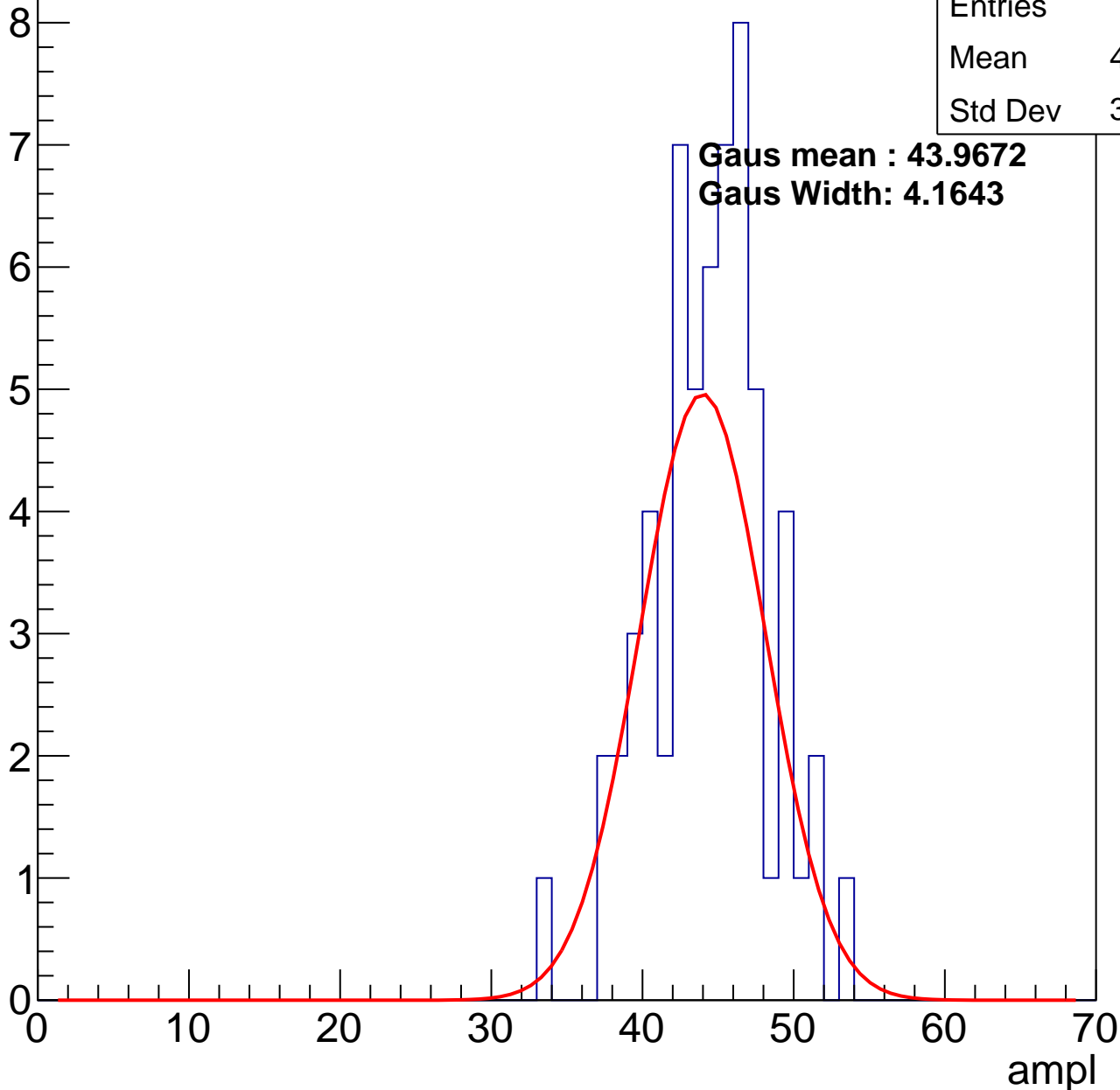
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.97
Std Dev	3.832

Gaus mean : 43.9672

Gaus Width: 4.1643

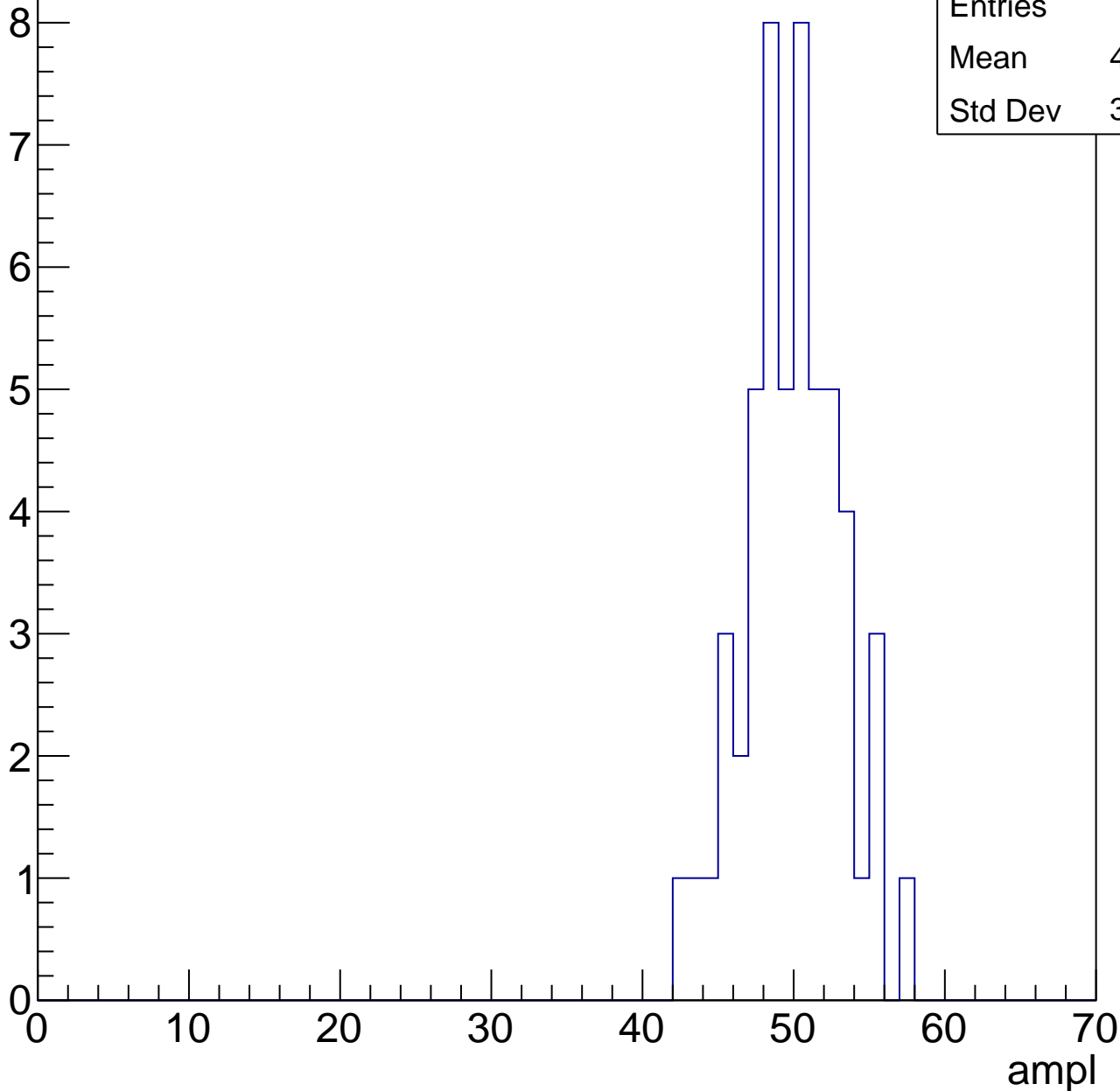


B1L102S, U13-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	49.49
Std Dev	3.148

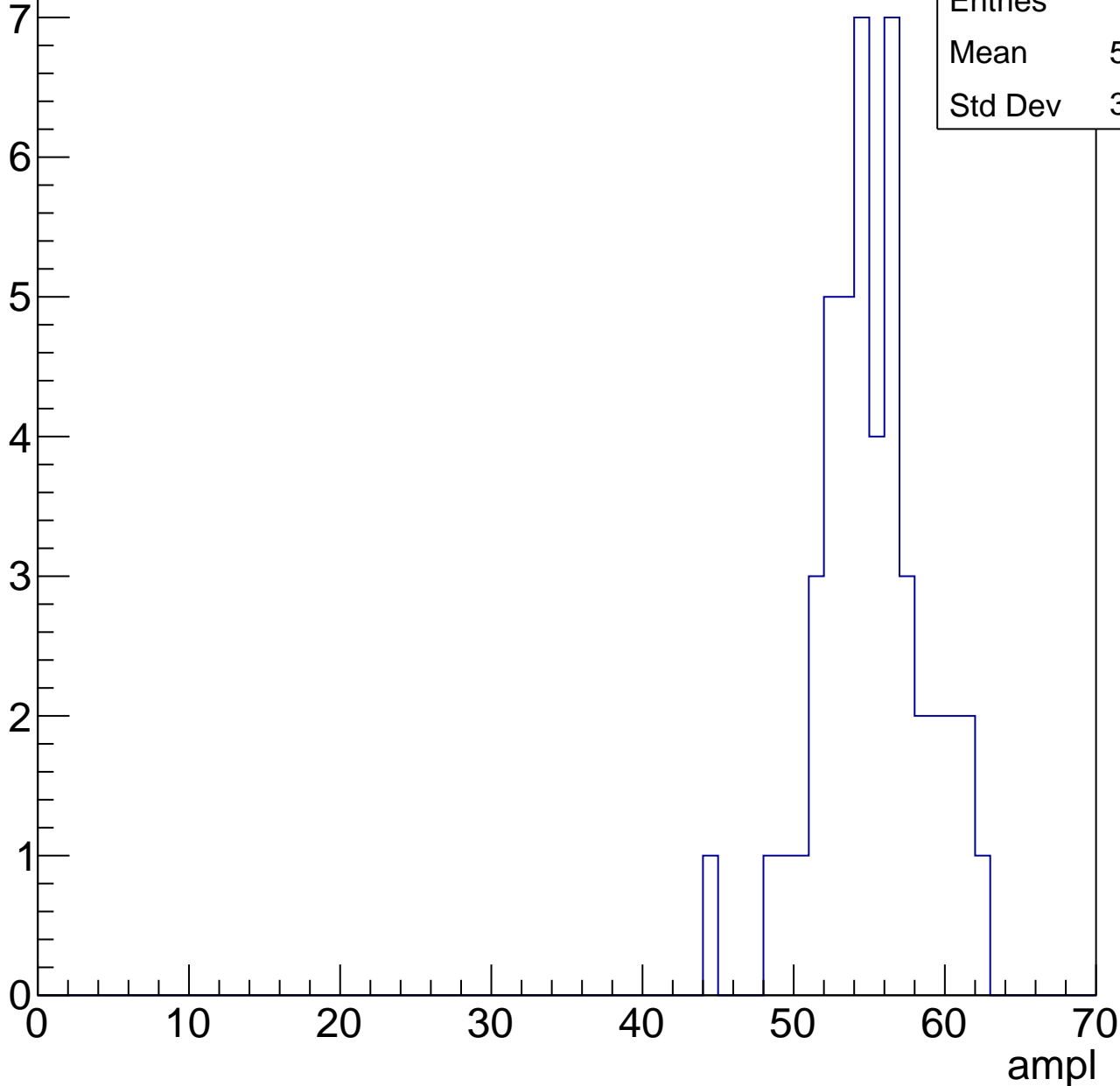


B1L102S, U13-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.64
Std Dev	3.528

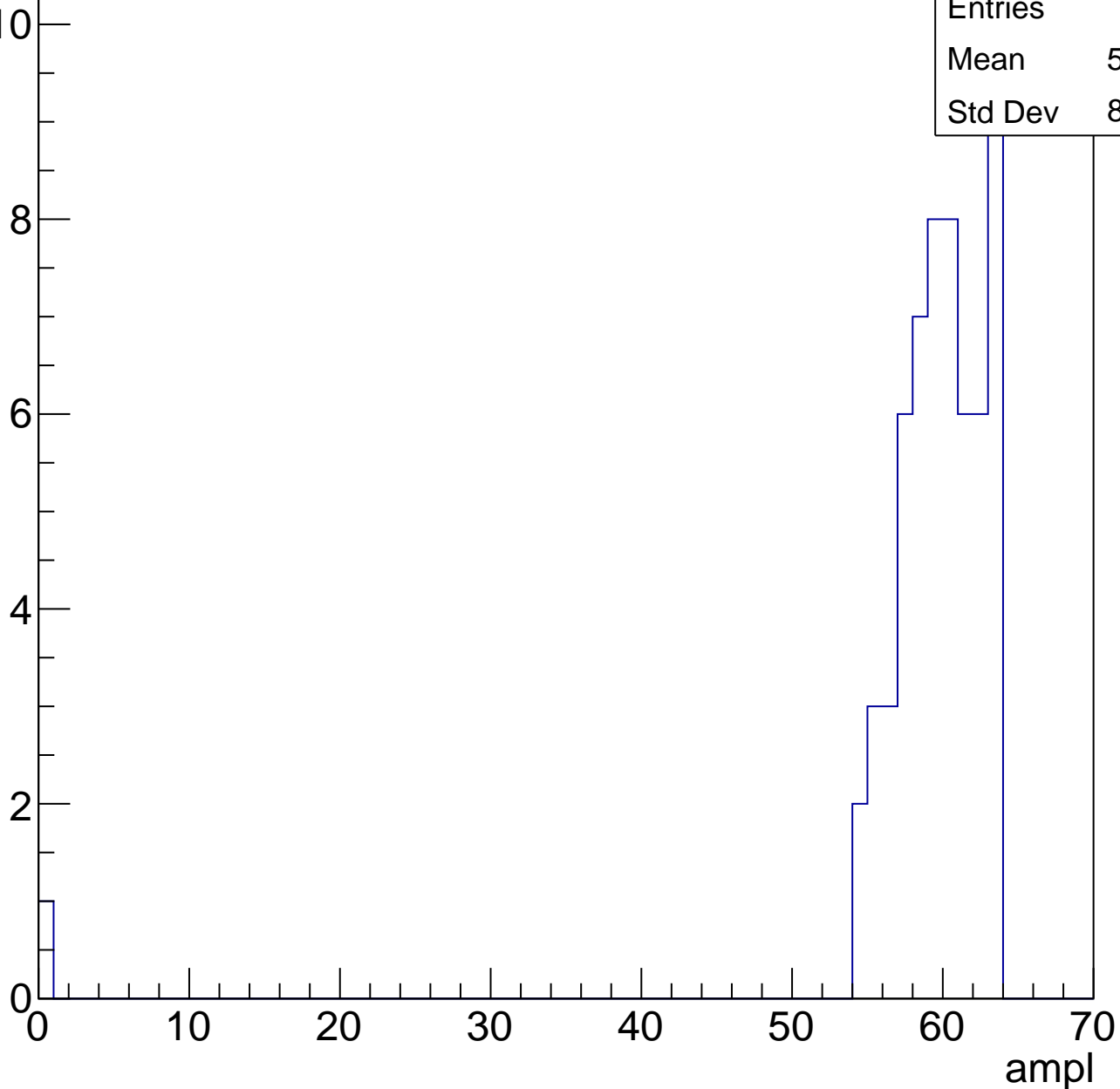


B1L102S, U13-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

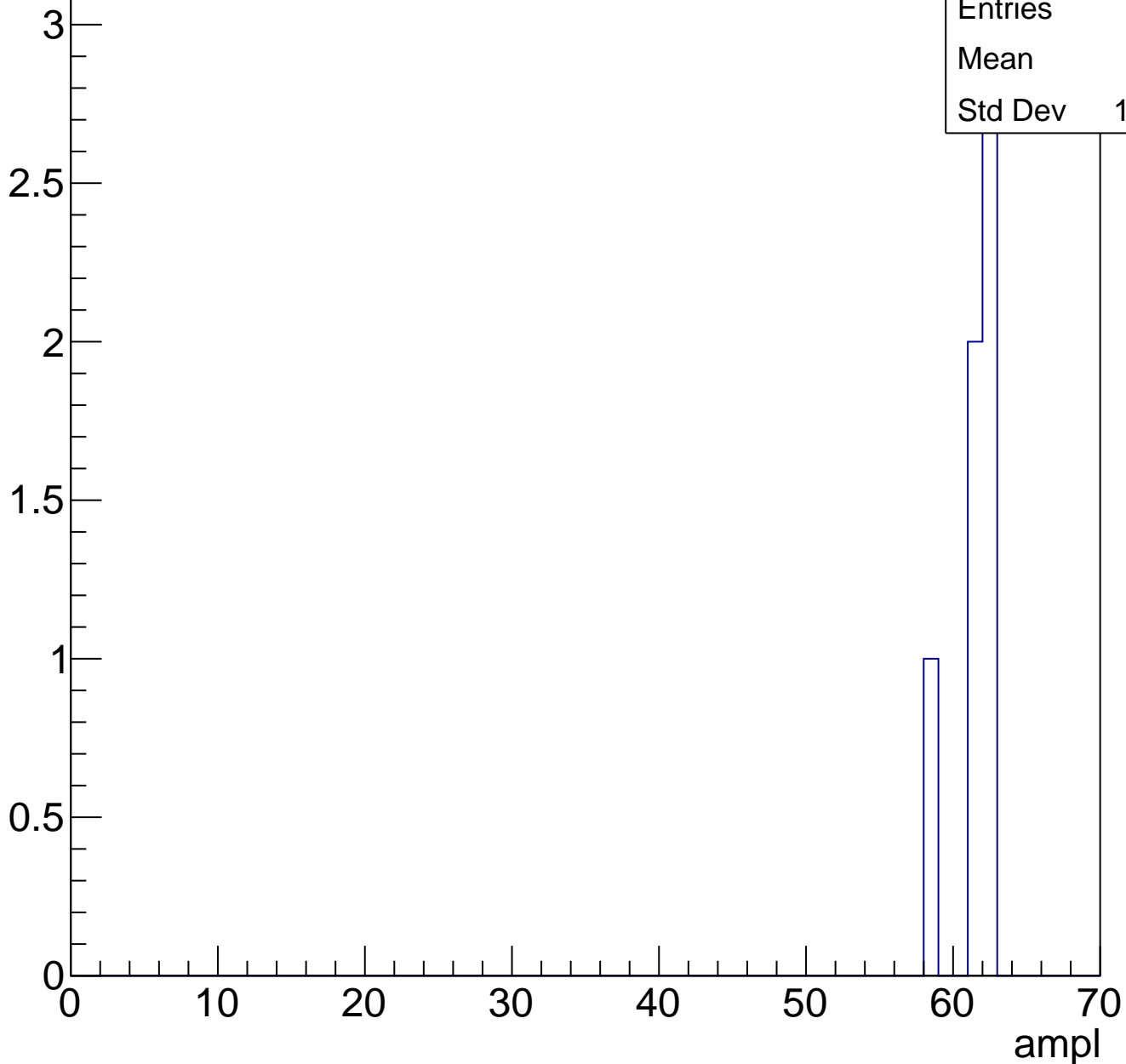
Entries	60
Mean	58.48
Std Dev	8.028



B1L102S, U13-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch33, adc0

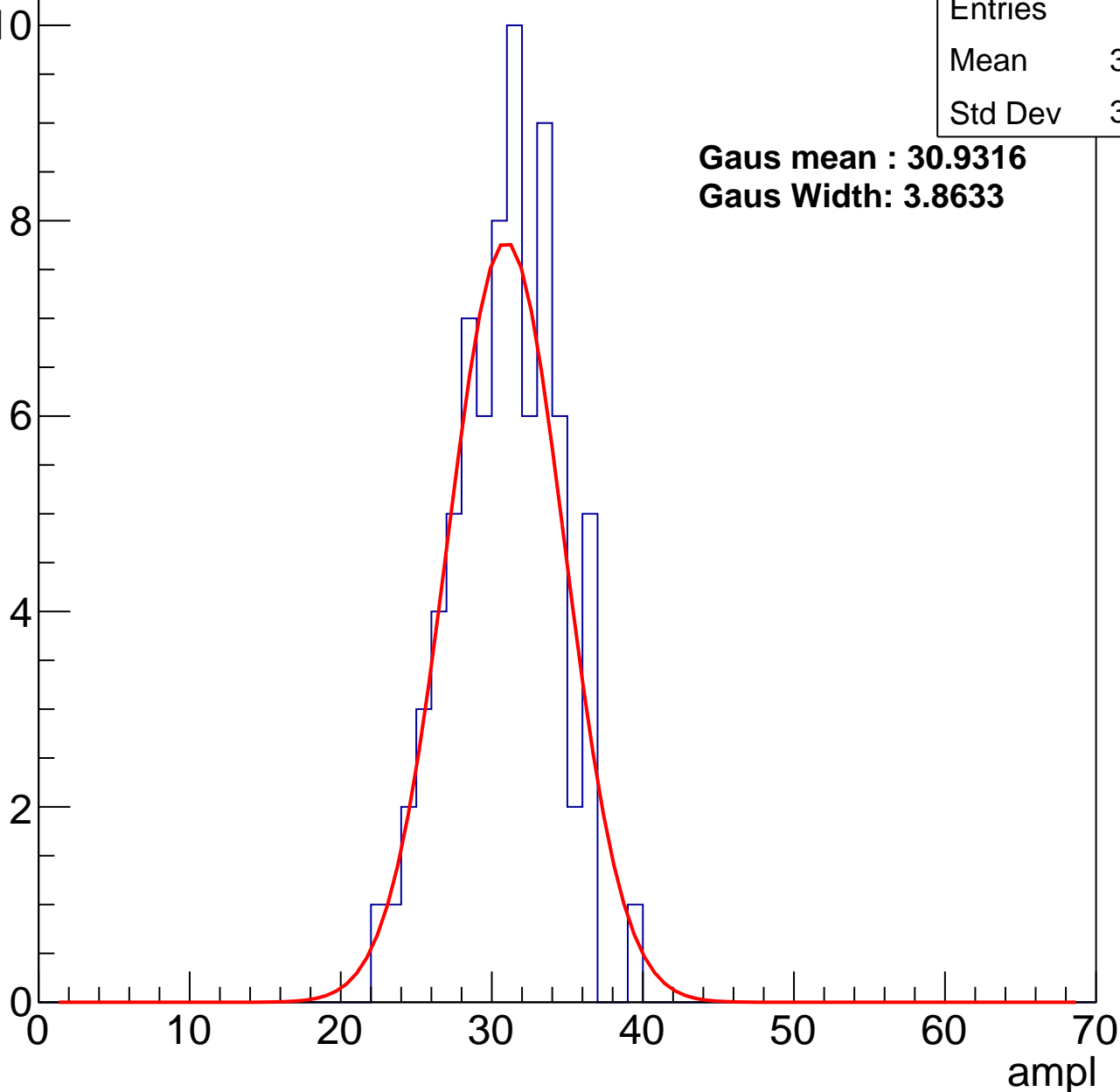
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	30.38
Std Dev	3.468

Gaus mean : 30.9316

Gaus Width: 3.8633



B1L102S, U13-ch33, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	38.44
Std Dev	3.453

Gaus mean : 38.8770

Gaus Width: 2.9843

10

8

6

4

2

0

0

10

20

30

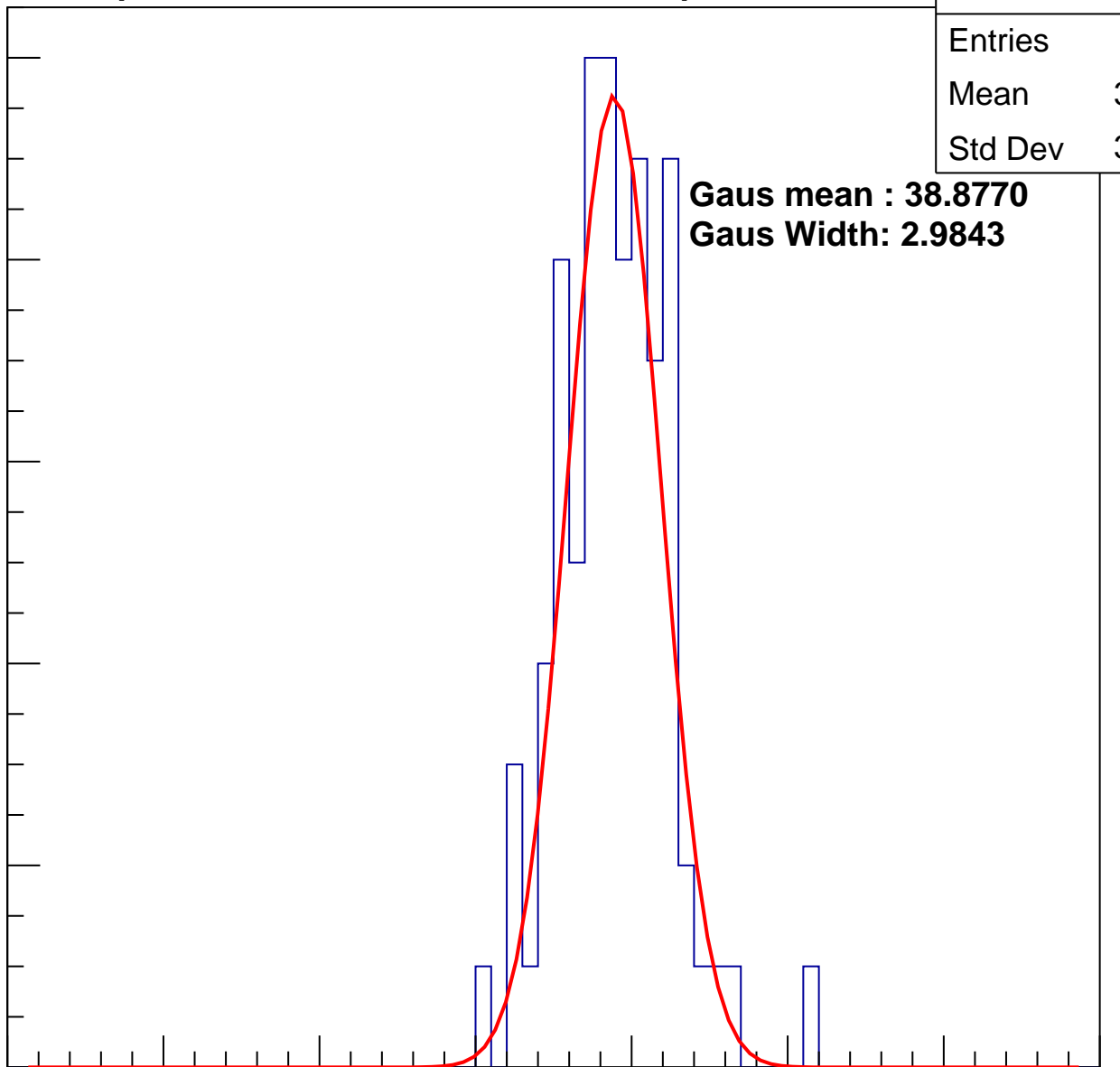
40

50

60

70

ampl



B1L102S, U13-ch33, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	46.03
Std Dev	2.957

Gaus mean : 46.8675

Gaus Width: 2.7907

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

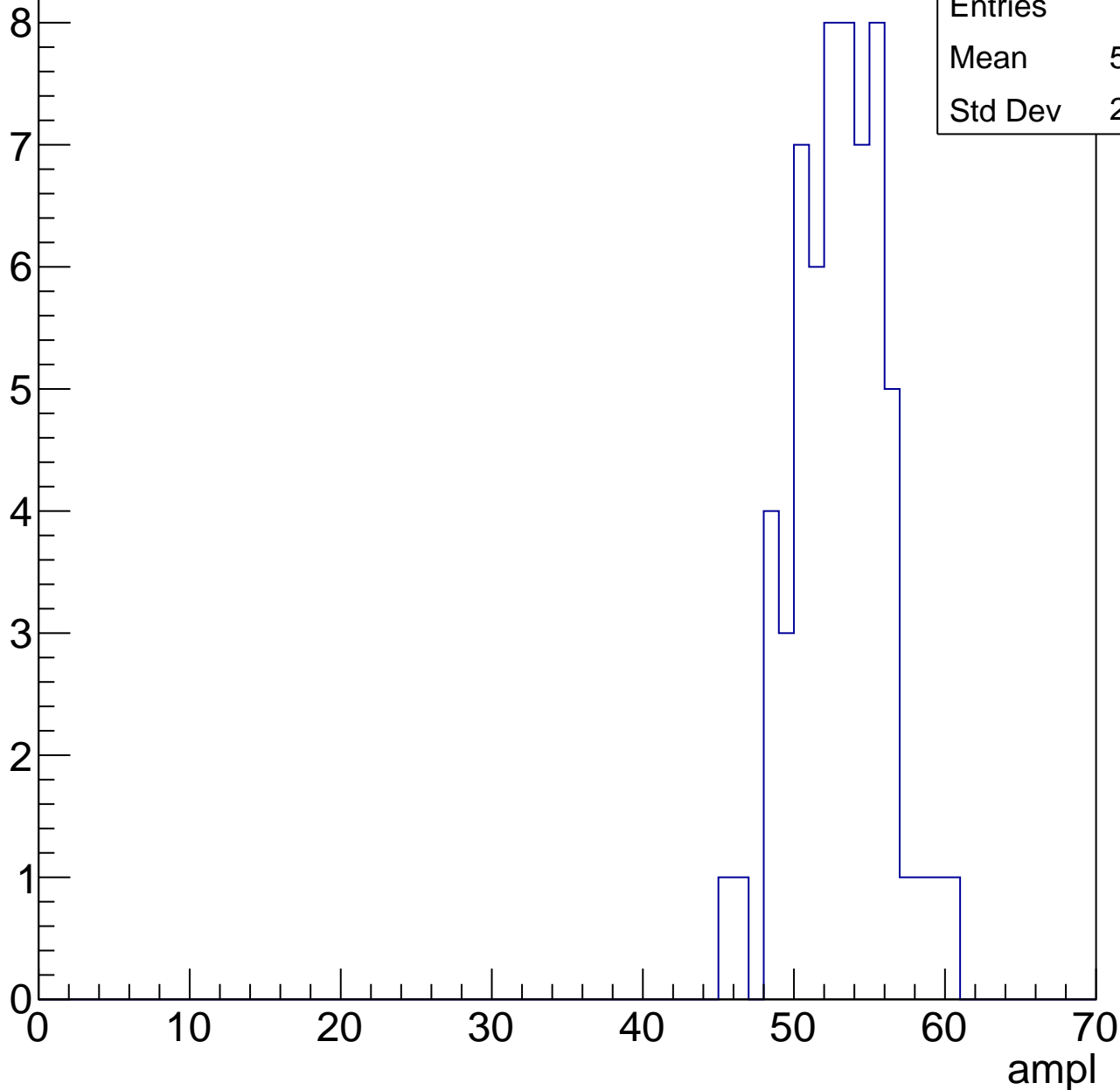
70

B1L102S, U13-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	52.55
Std Dev	2.987

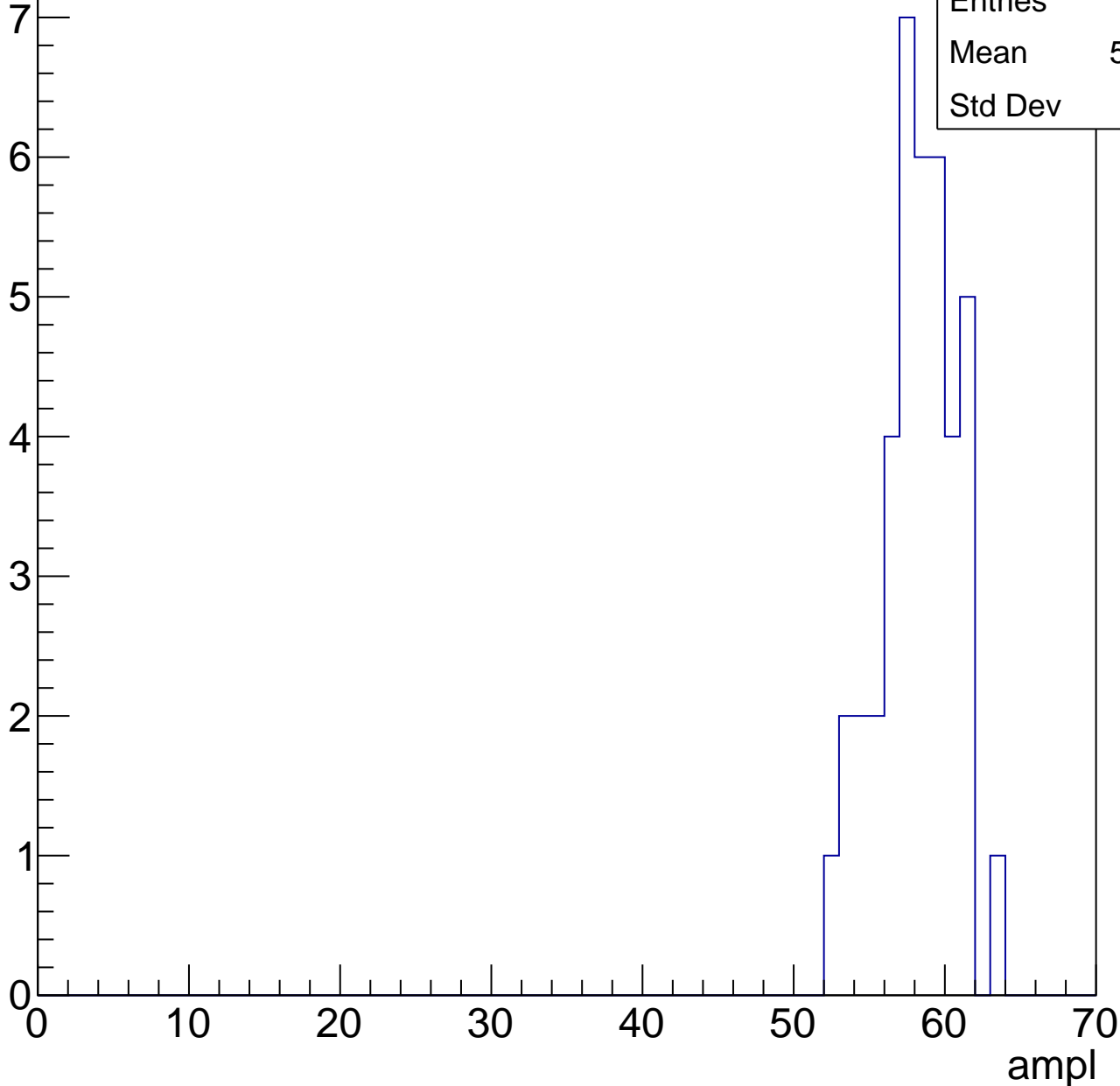


B1L102S, U13-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	57.73
Std Dev	2.49



B1L102S, U13-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	39
Mean	59.31
Std Dev	9.814

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

B1L102S, U13-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch34, adc0

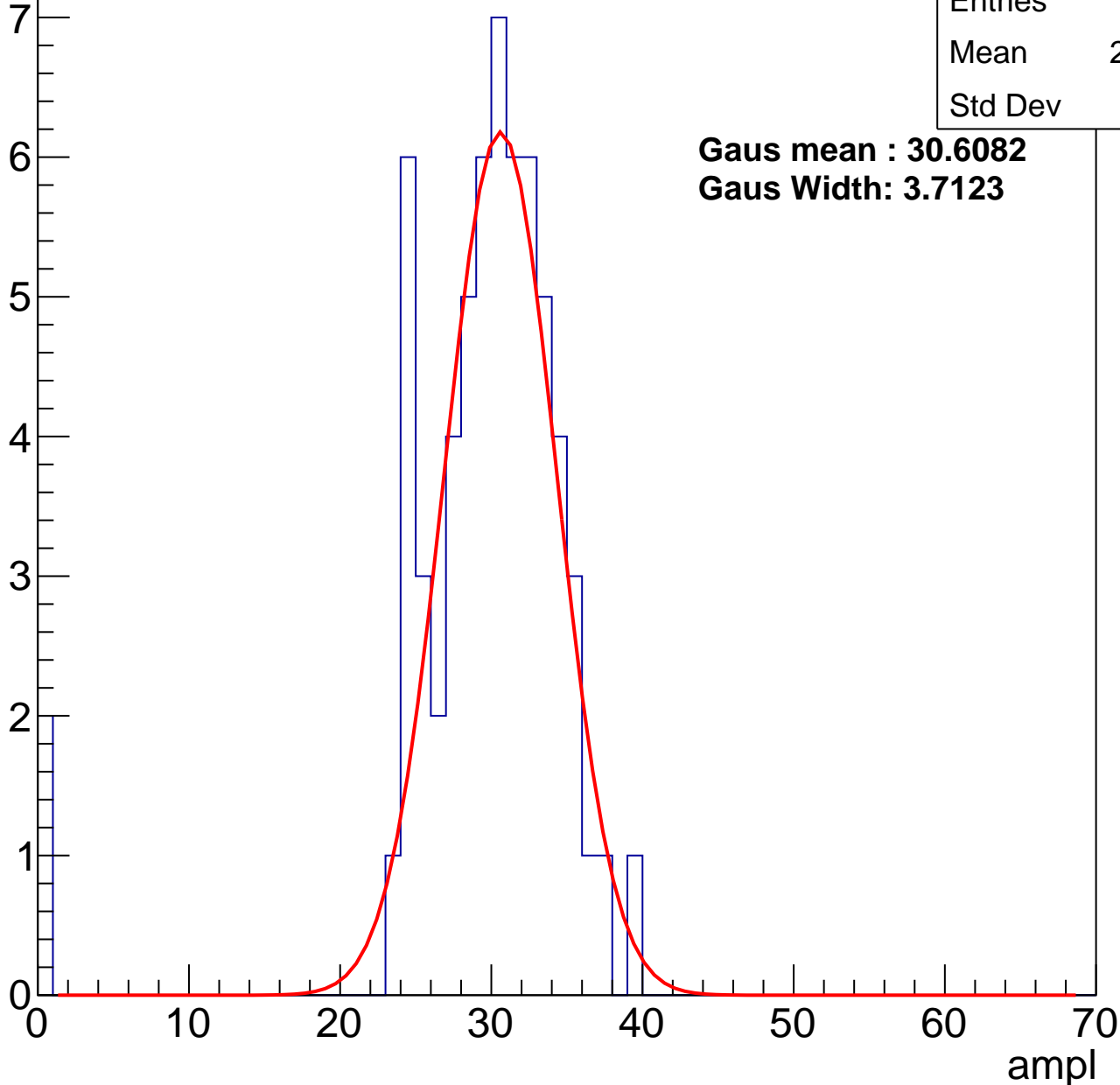
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.92
Std Dev	6.35

Gaus mean : 30.6082

Gaus Width: 3.7123



B1L102S, U13-ch34, adc1

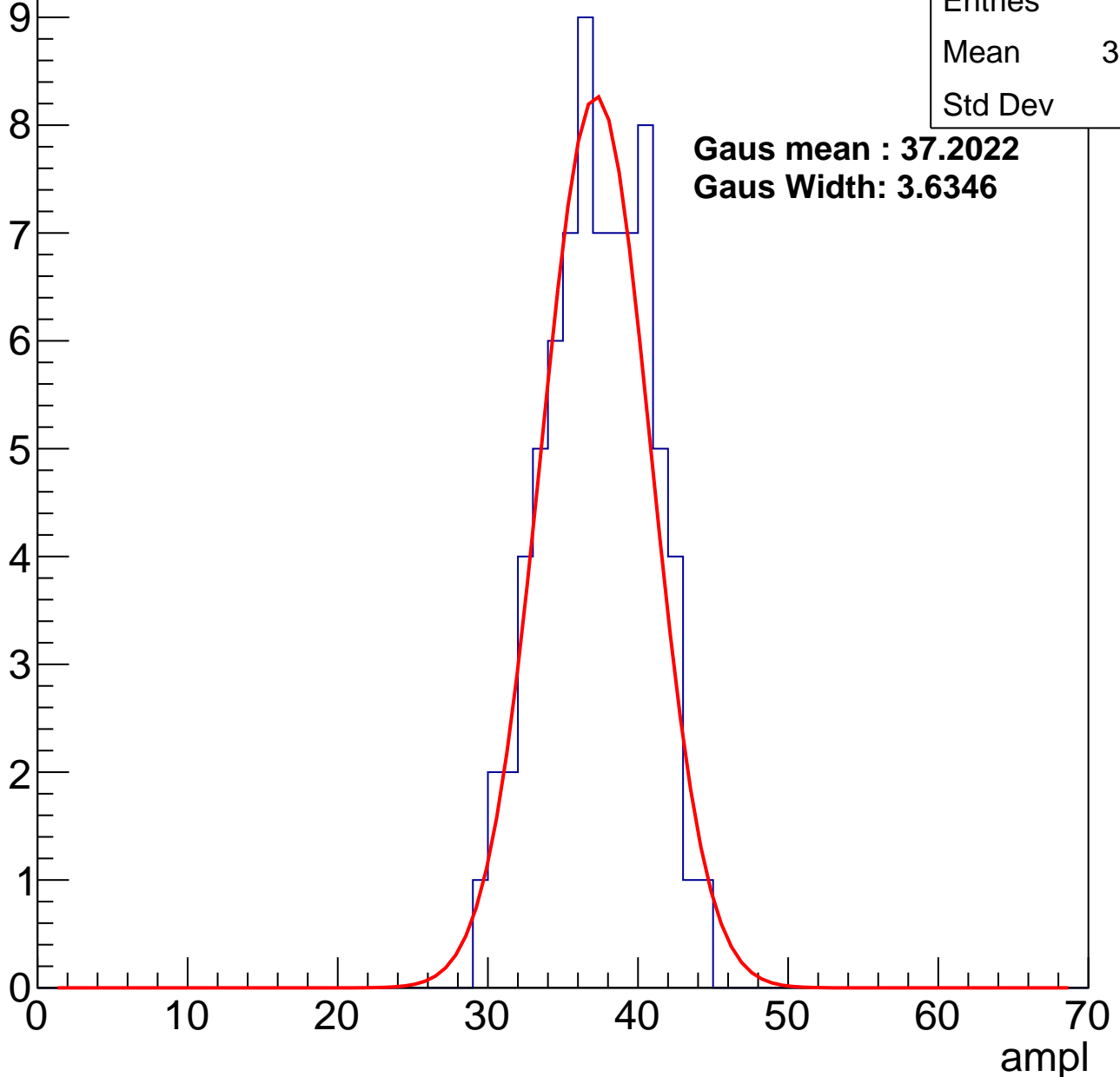
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.78
Std Dev	3.39

Gaus mean : 37.2022

Gaus Width: 3.6346



B1L102S, U13-ch34, adc2

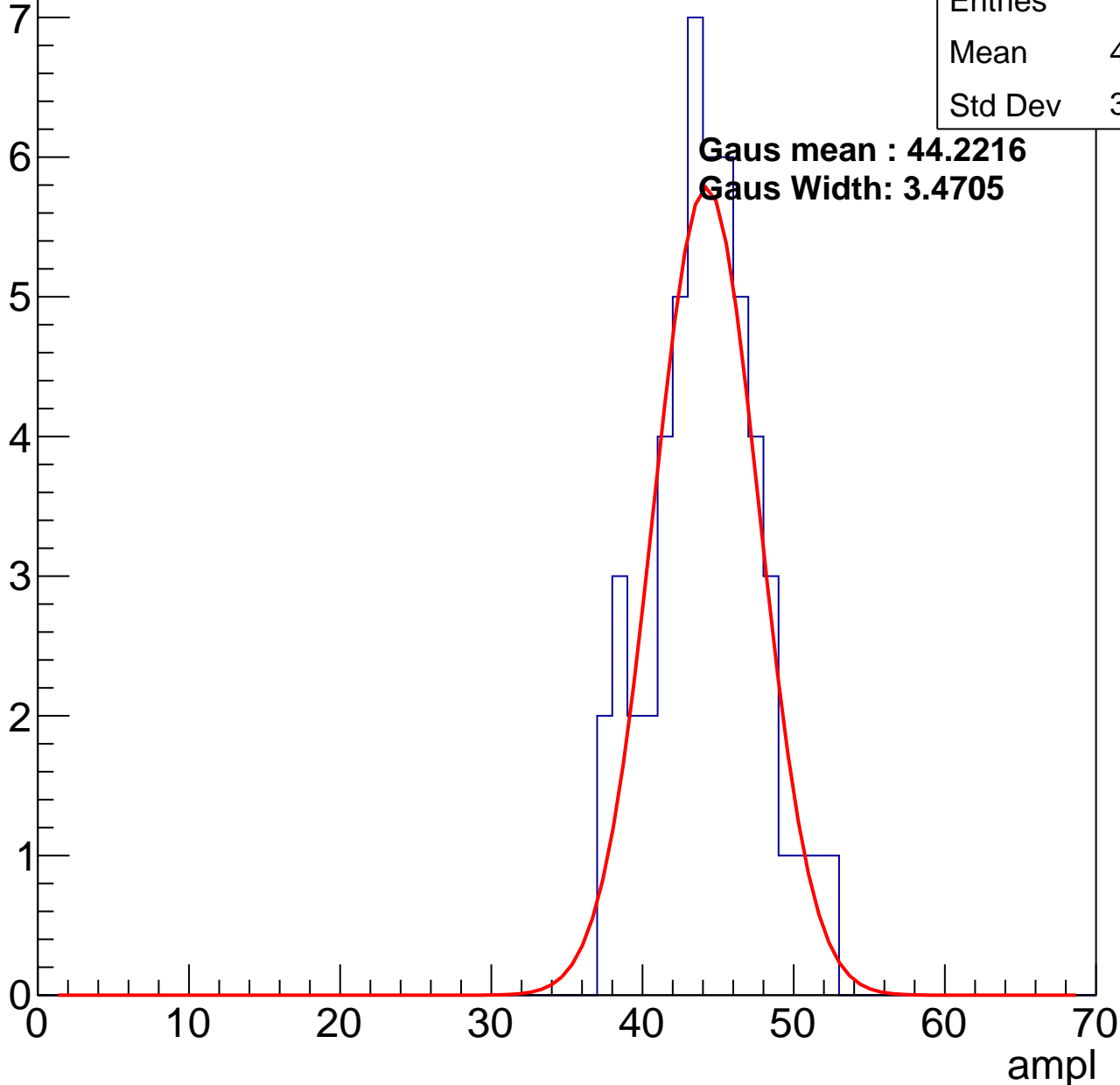
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	43.75
Std Dev	3.453

Gaus mean : 44.2216

Gaus Width: 3.4705

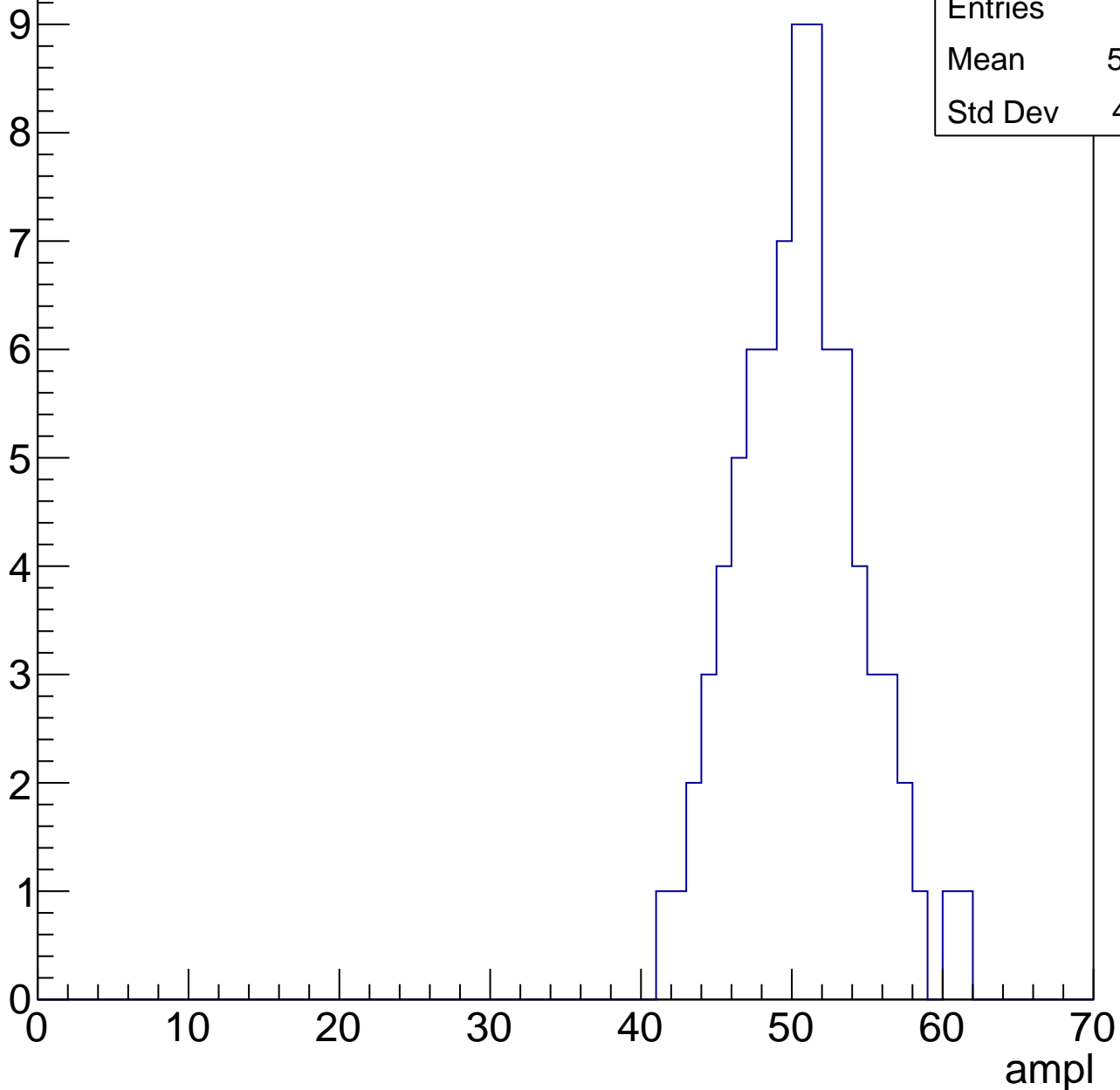


B1L102S, U13-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	50.06
Std Dev	4.081

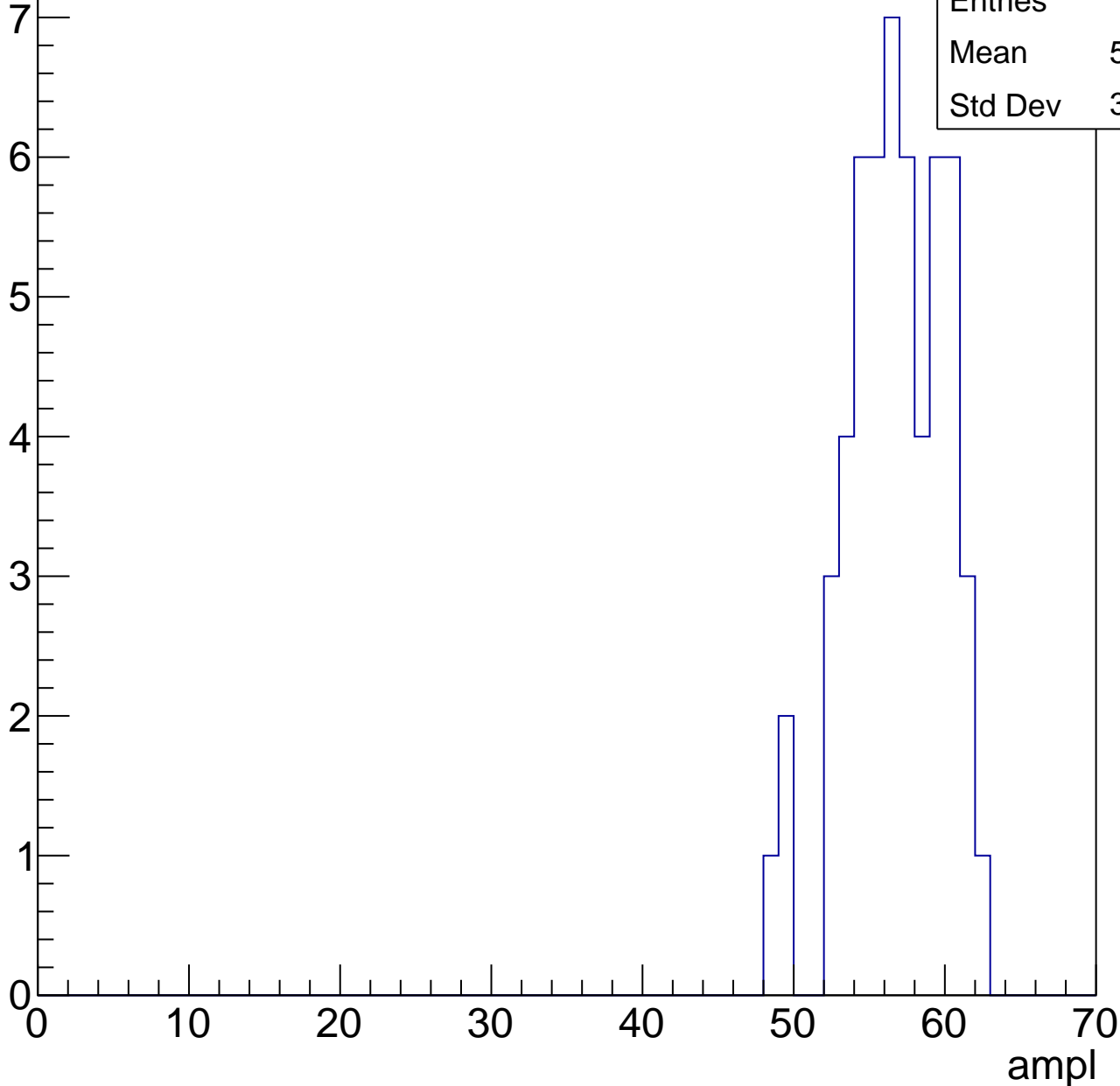


B1L102S, U13-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.24
Std Dev	3.179

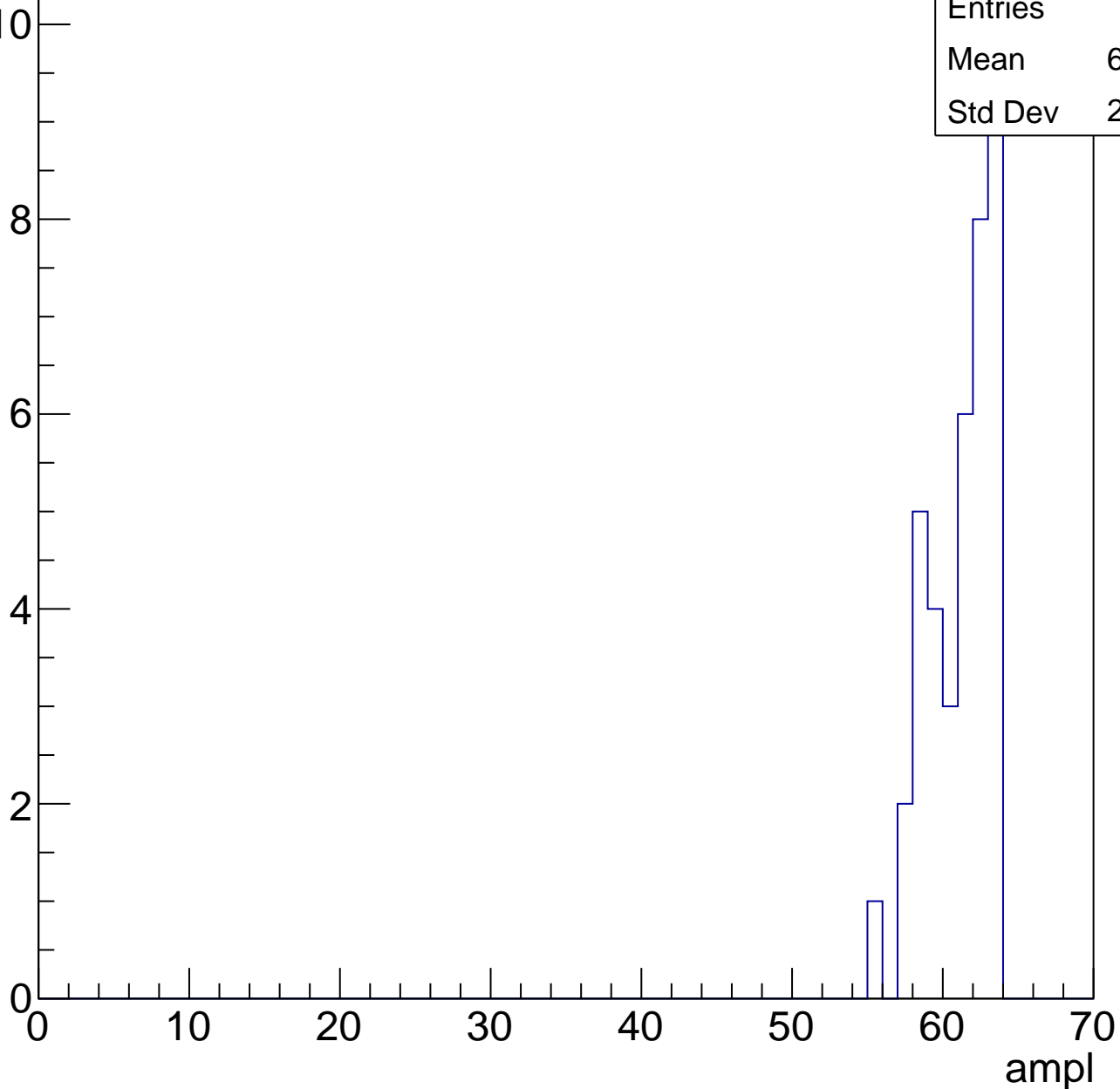


B1L102S, U13-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	60.69
Std Dev	2.126



B1L102S, U13-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch35, adc0

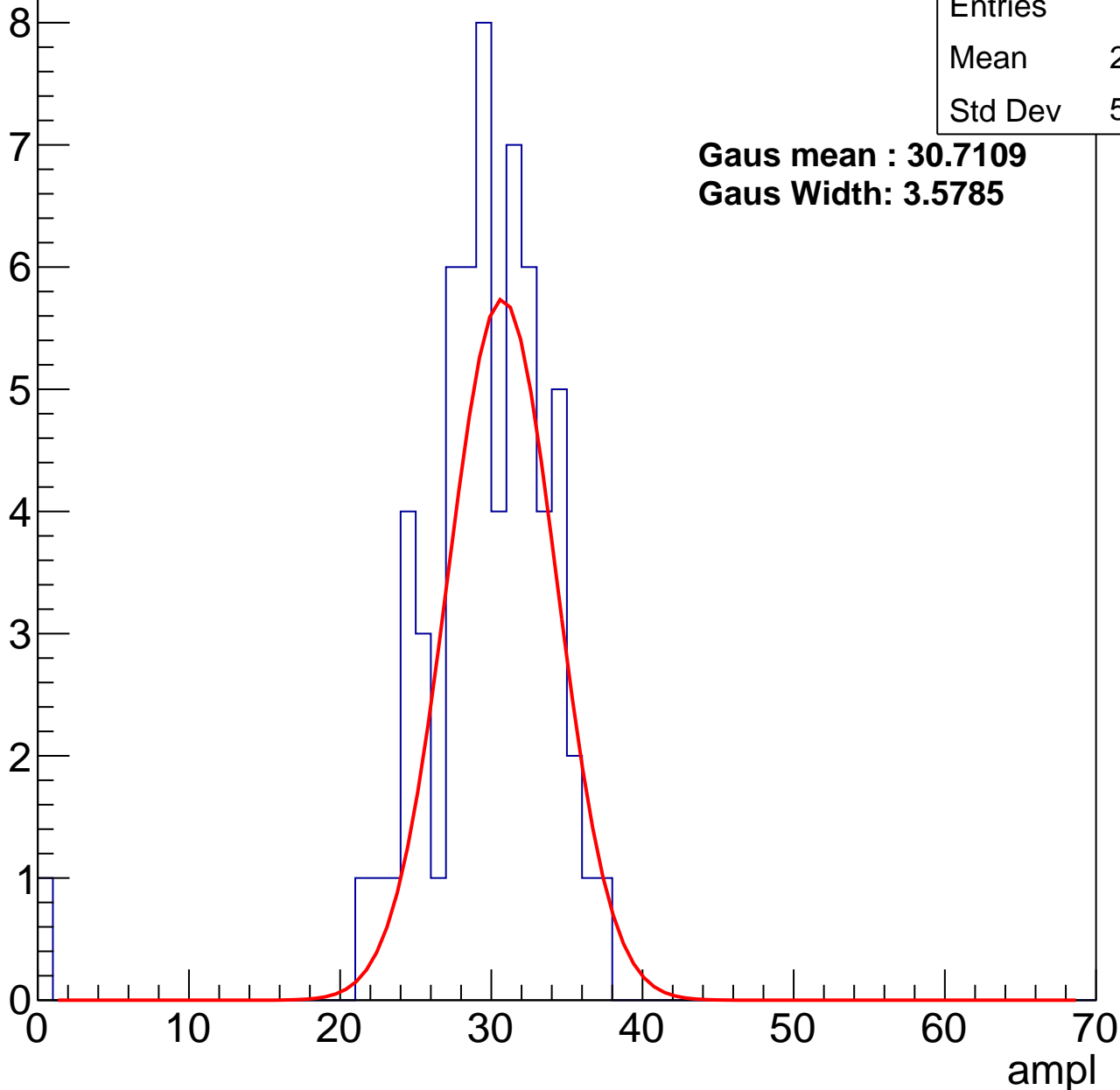
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.02
Std Dev	5.138

Gaus mean : 30.7109

Gaus Width: 3.5785



B1L102S, U13-ch35, adc1

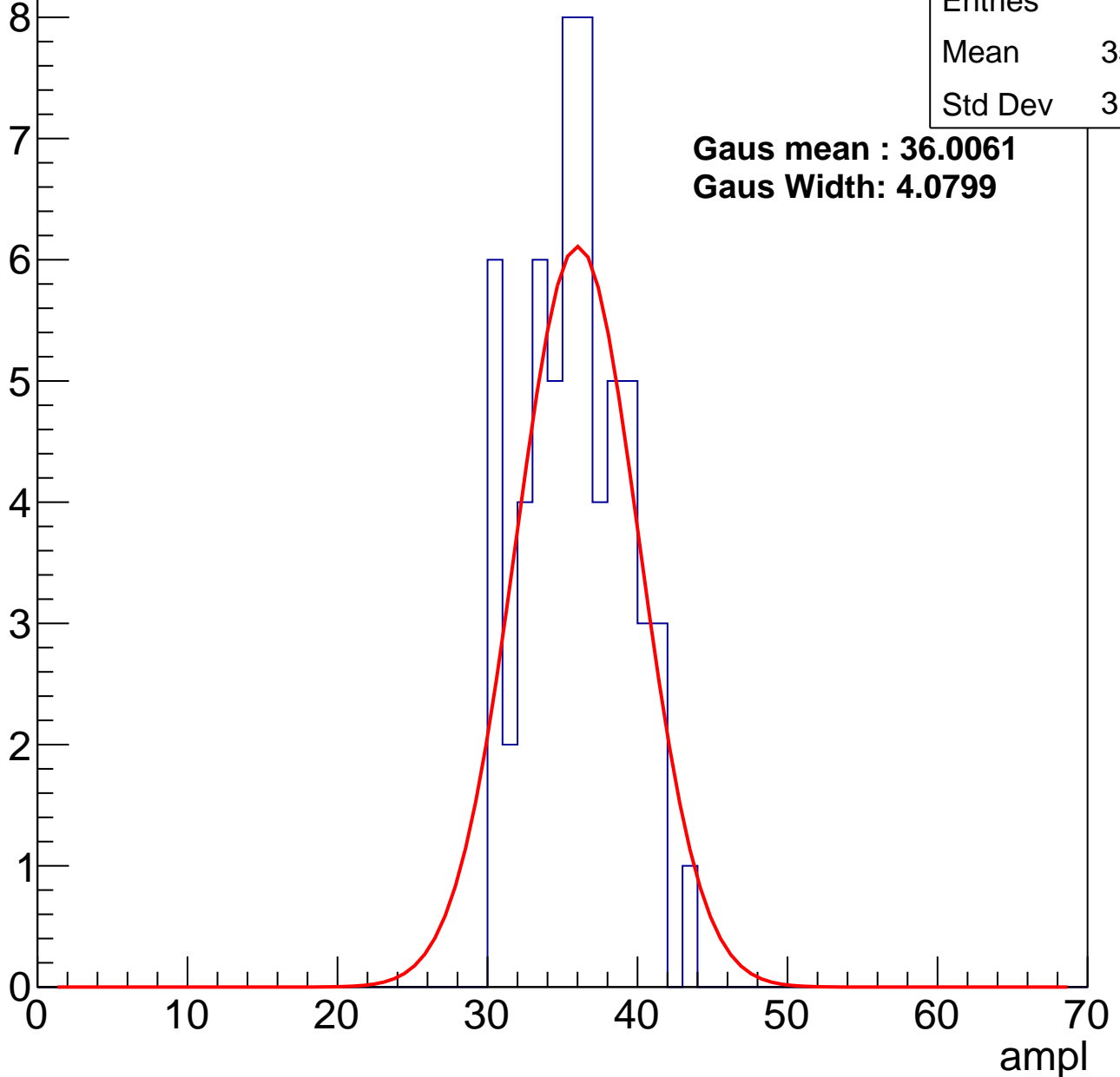
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.42
Std Dev	3.247

Gaus mean : 36.0061

Gaus Width: 4.0799



B1L102S, U13-ch35, adc2

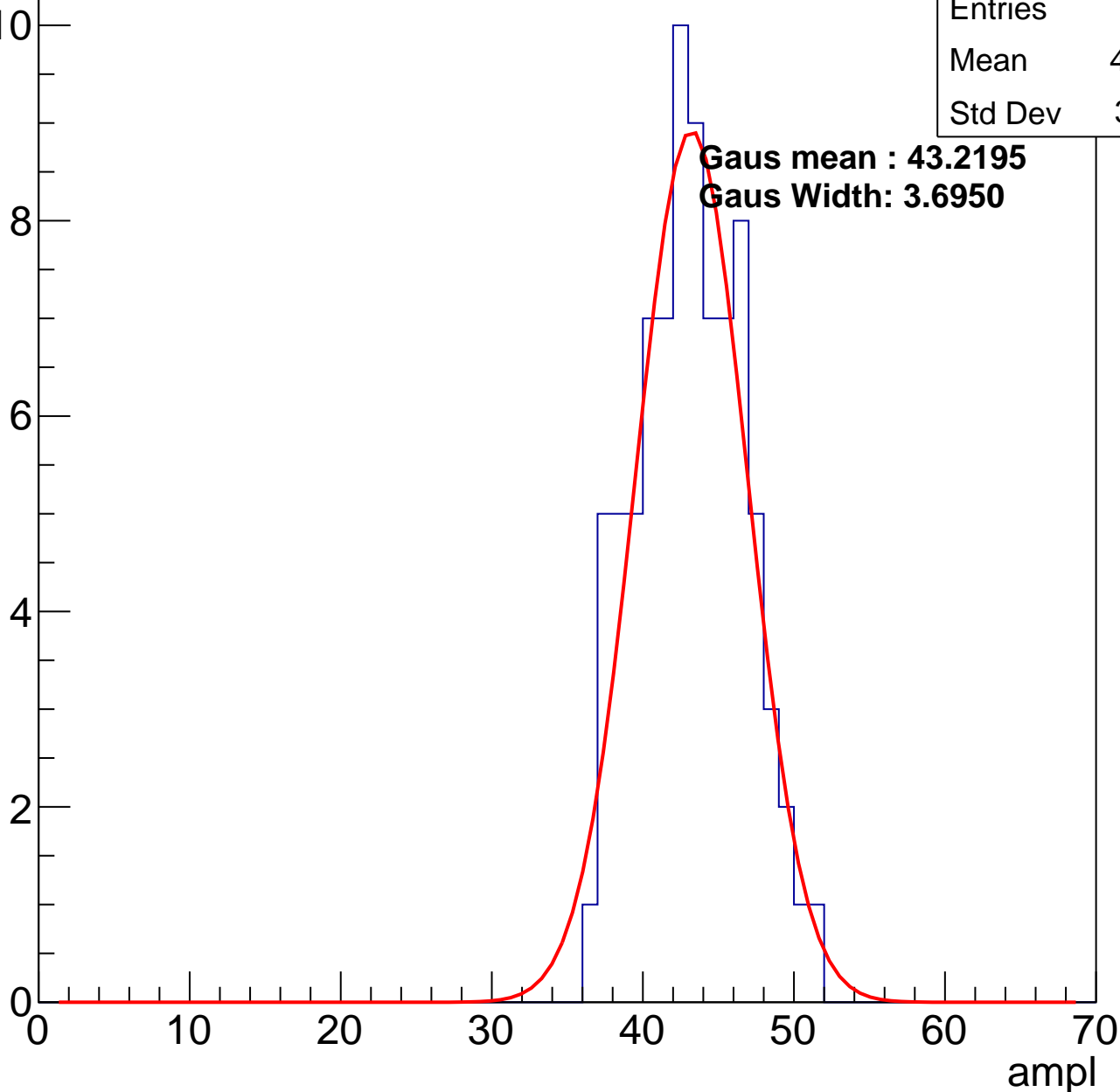
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	42.76
Std Dev	3.431

Gaus mean : 43.2195

Gaus Width: 3.6950

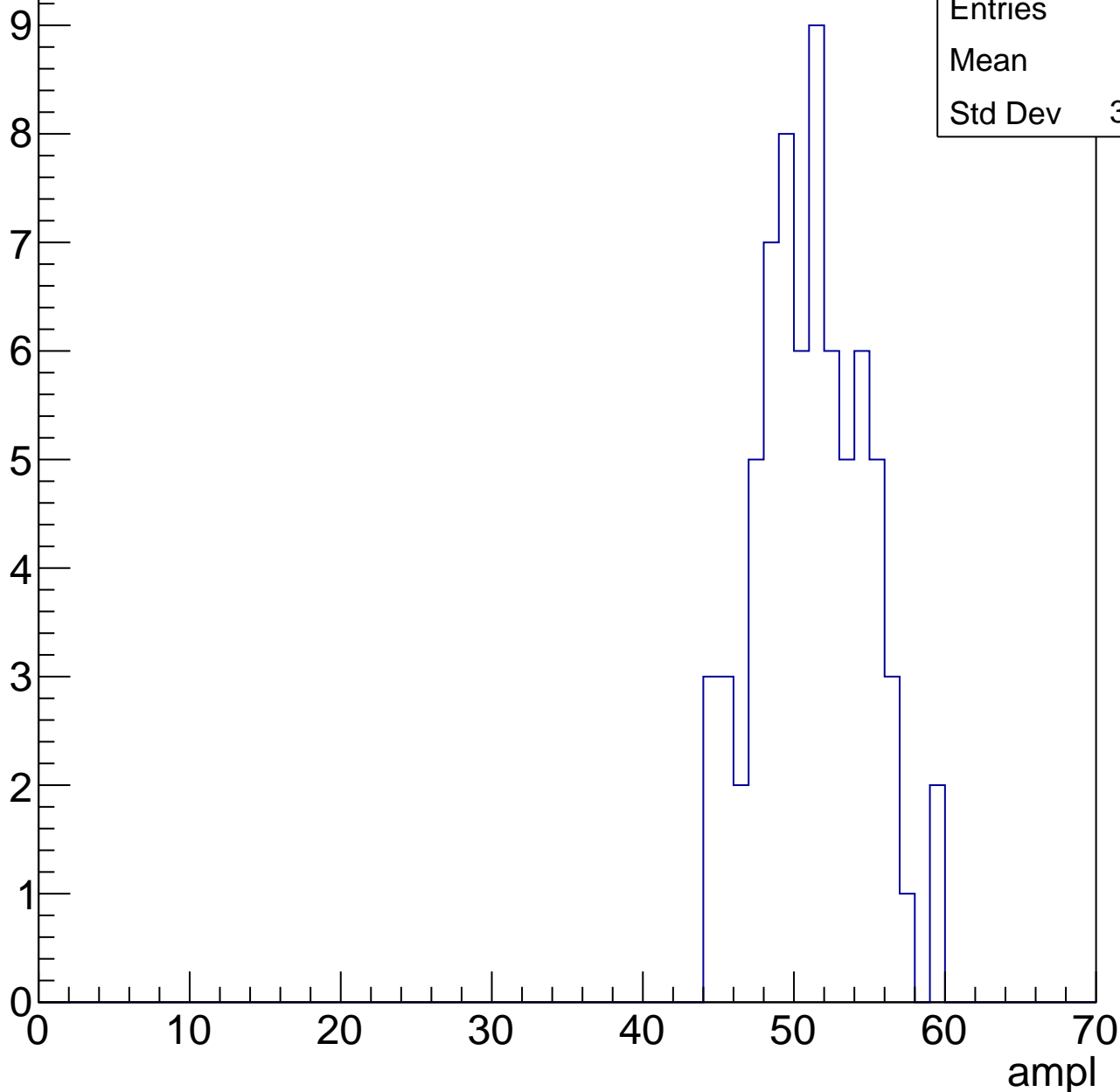


B1L102S, U13-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	50.7
Std Dev	3.518

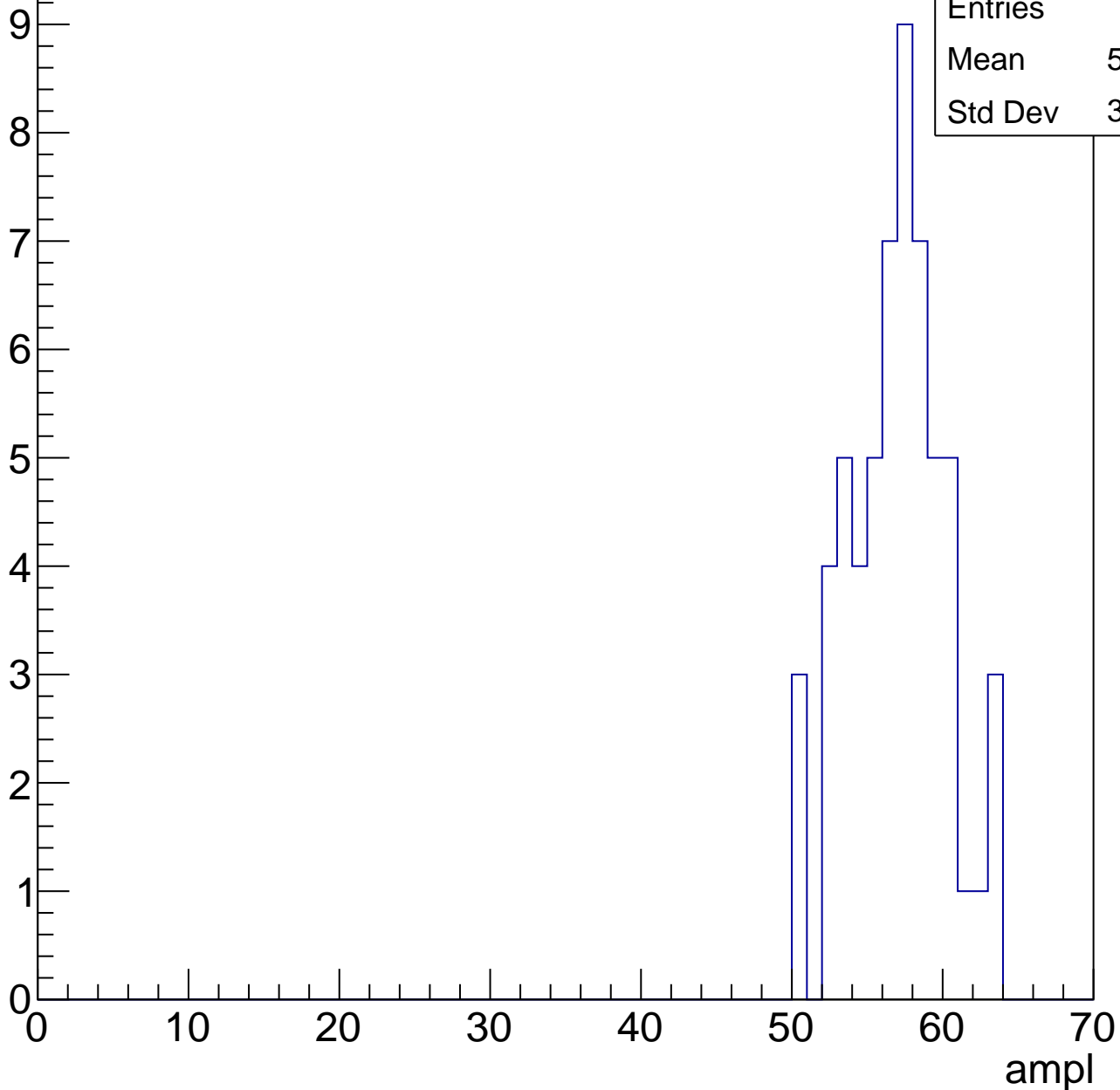


B1L102S, U13-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.47
Std Dev	3.175

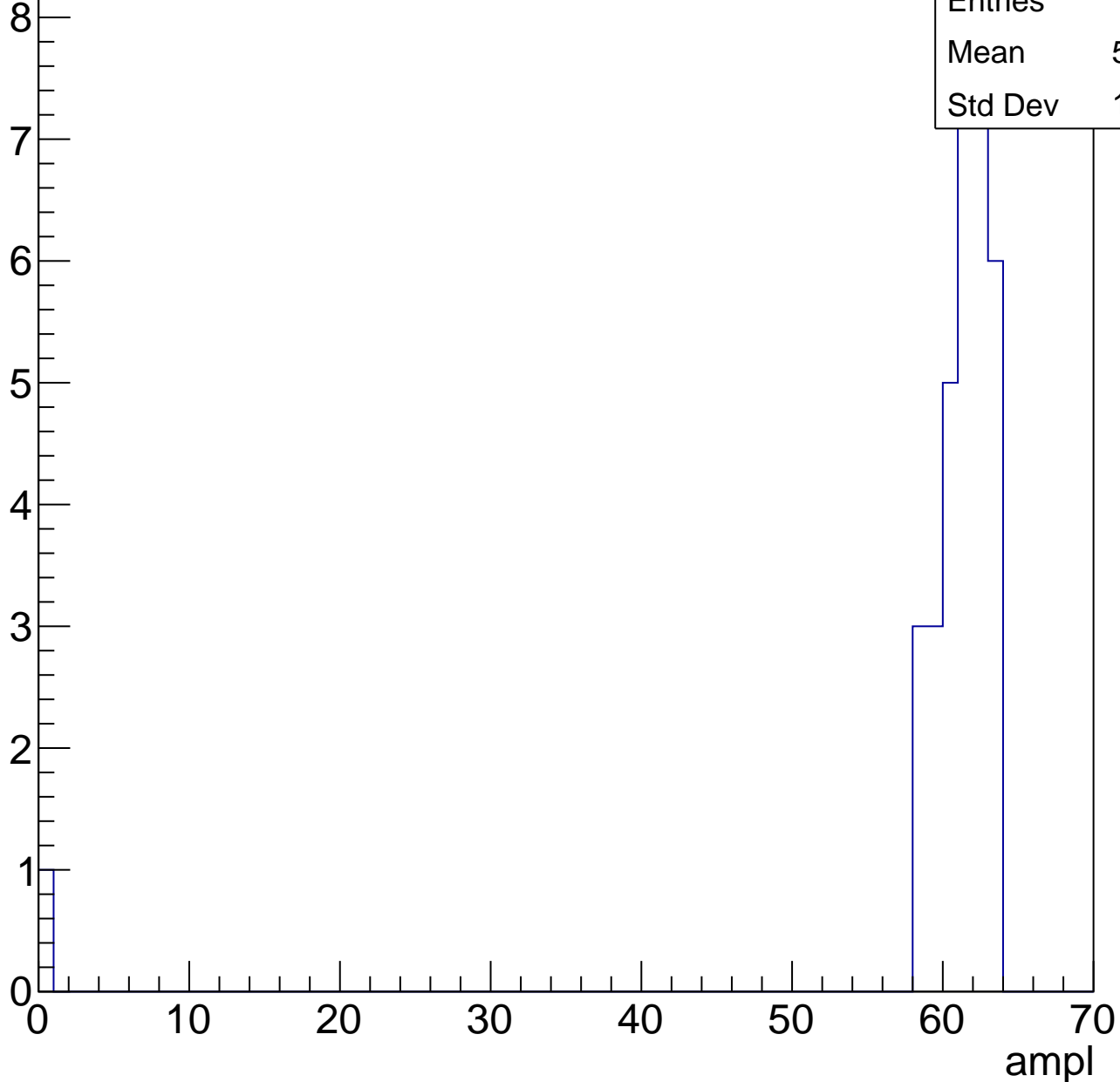


B1L102S, U13-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	59.21
Std Dev	10.41



B1L102S, U13-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch36, adc0

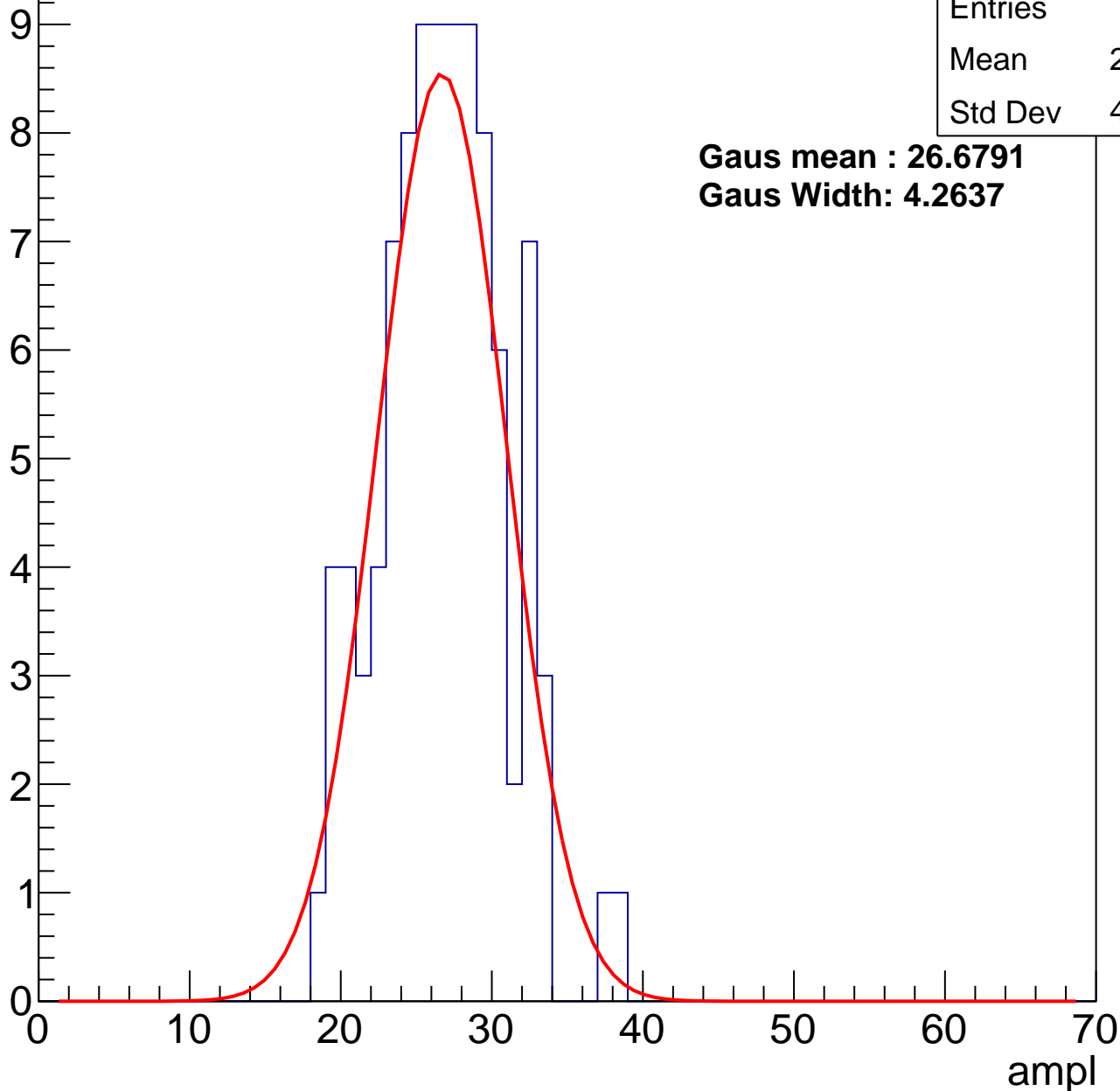
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	95
Mean	26.36
Std Dev	4.055

Gaus mean : 26.6791

Gaus Width: 4.2637



B1L102S, U13-ch36, adc1

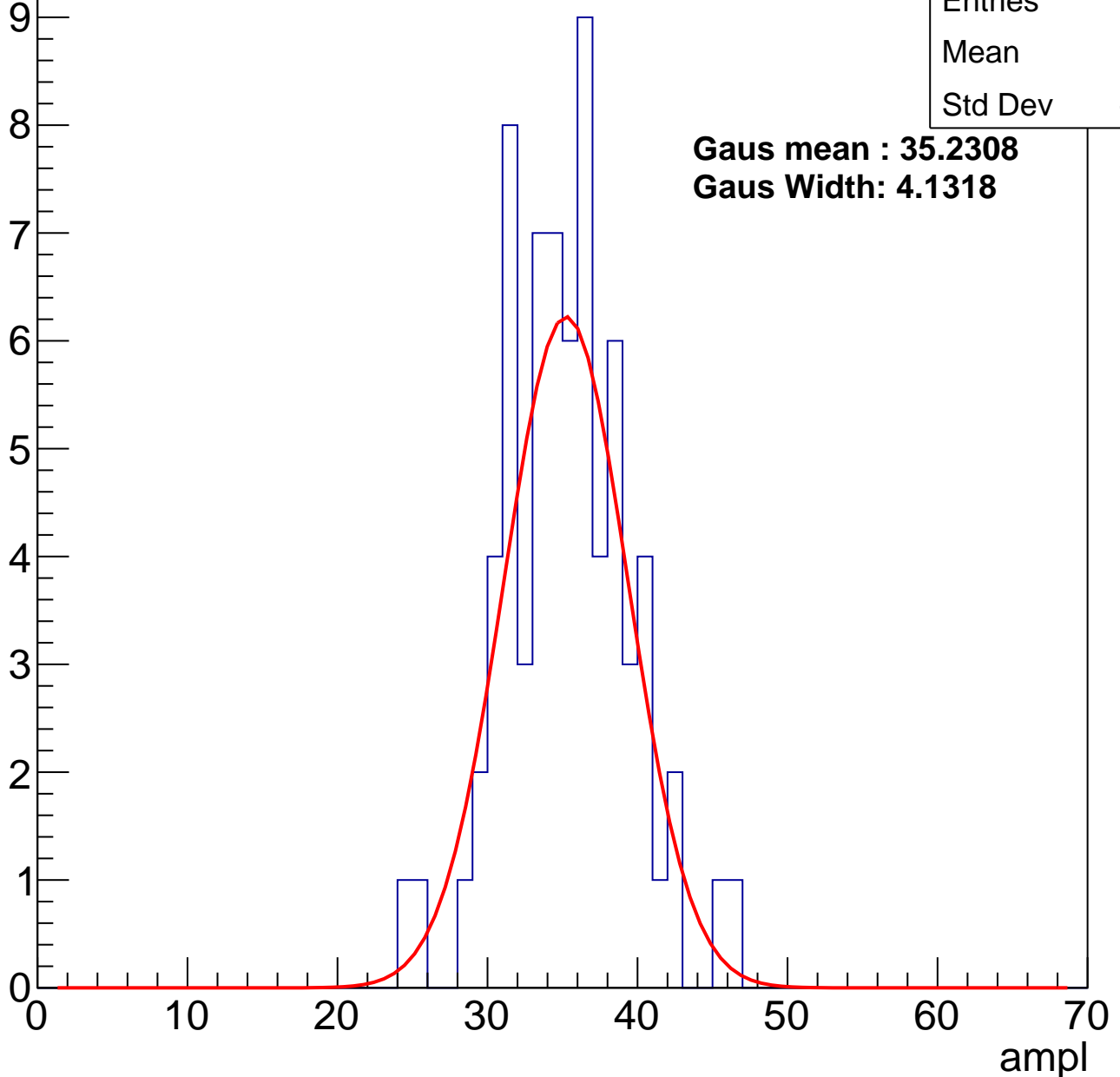
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.8
Std Dev	4.13

Gaus mean : 35.2308

Gaus Width: 4.1318



B1L102S, U13-ch36, adc2

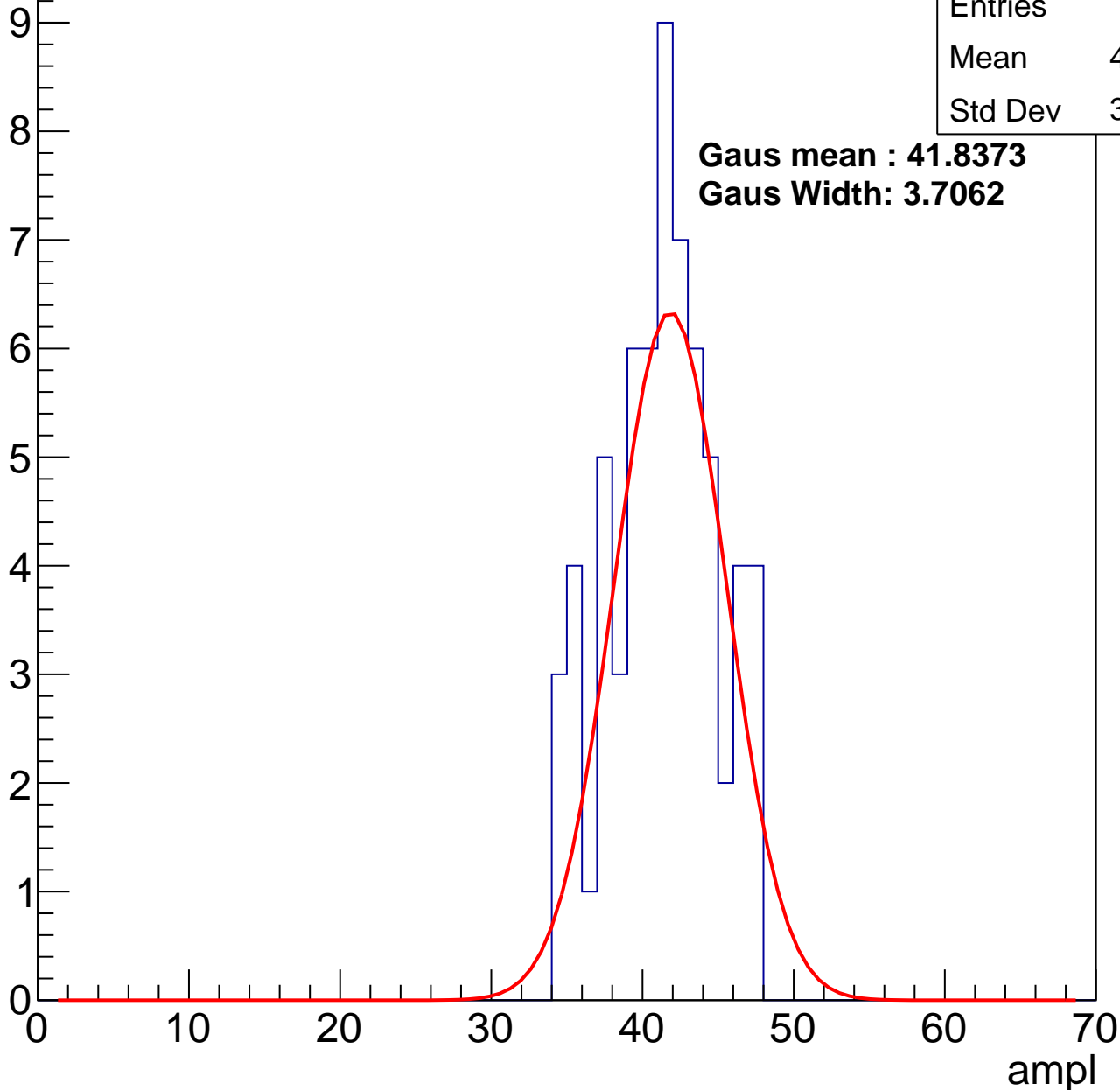
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	40.83
Std Dev	3.515

Gaus mean : 41.8373

Gaus Width: 3.7062

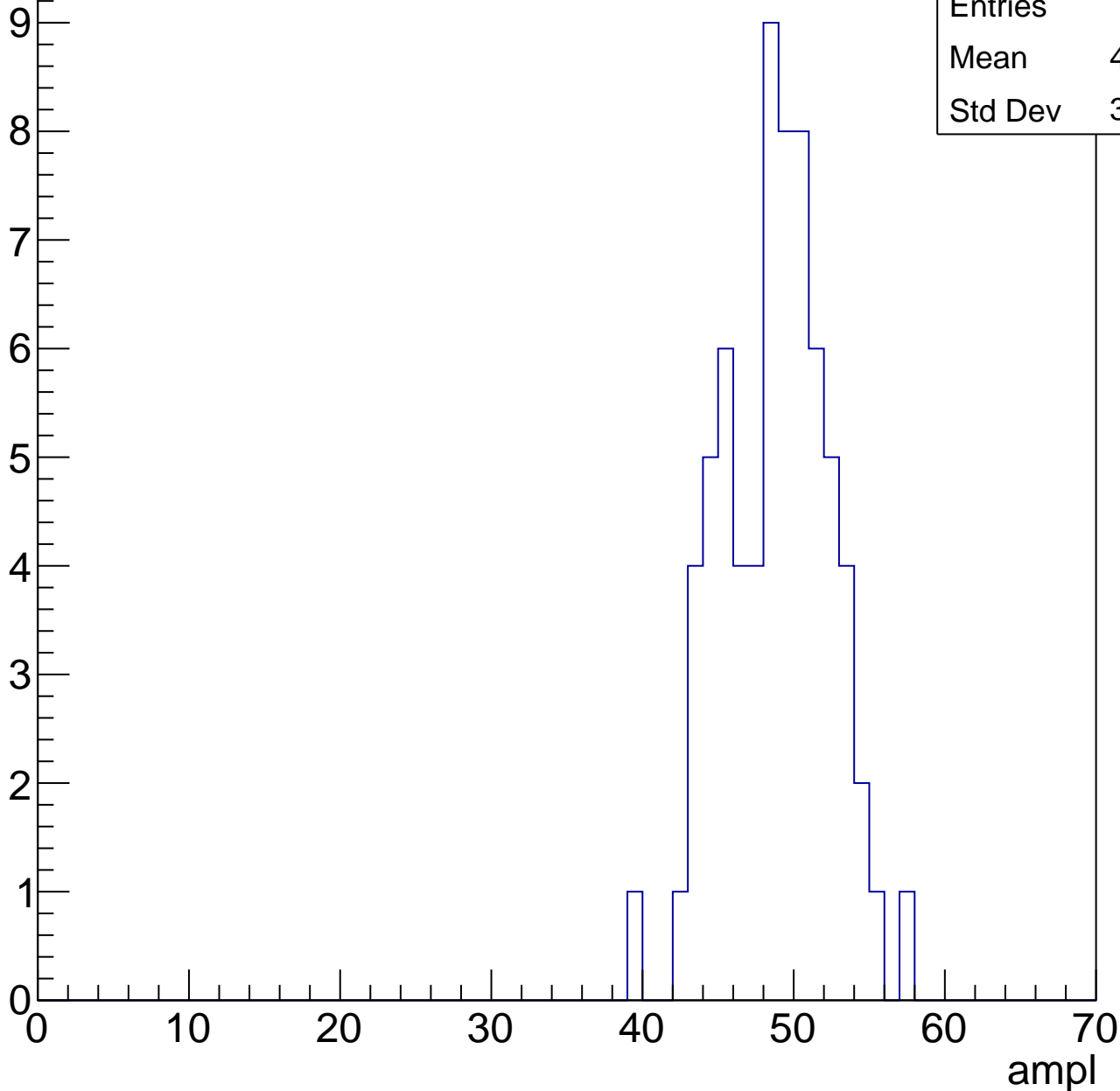


B1L102S, U13-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.36
Std Dev	3.485

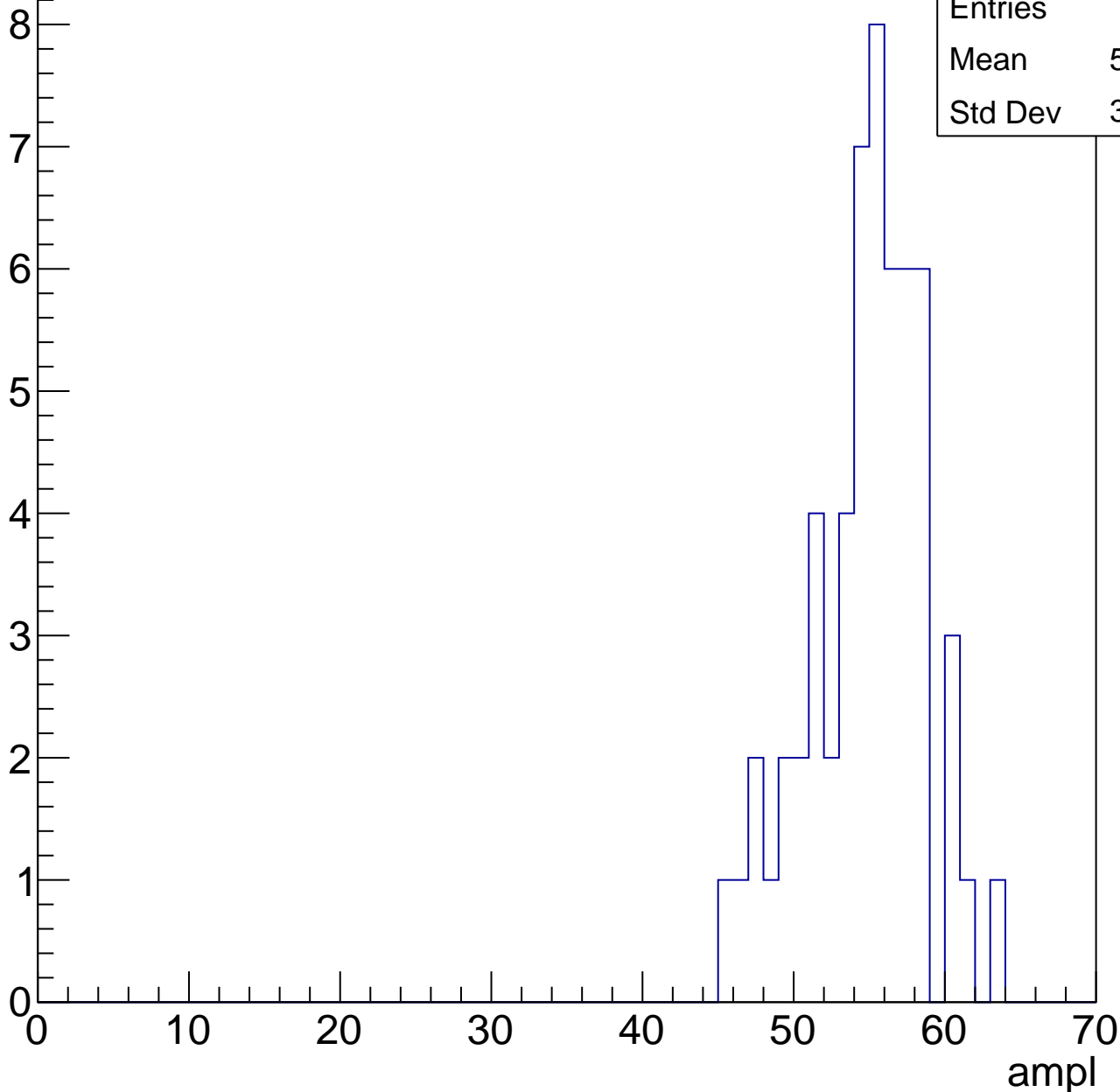


B1L102S, U13-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.37
Std Dev	3.796

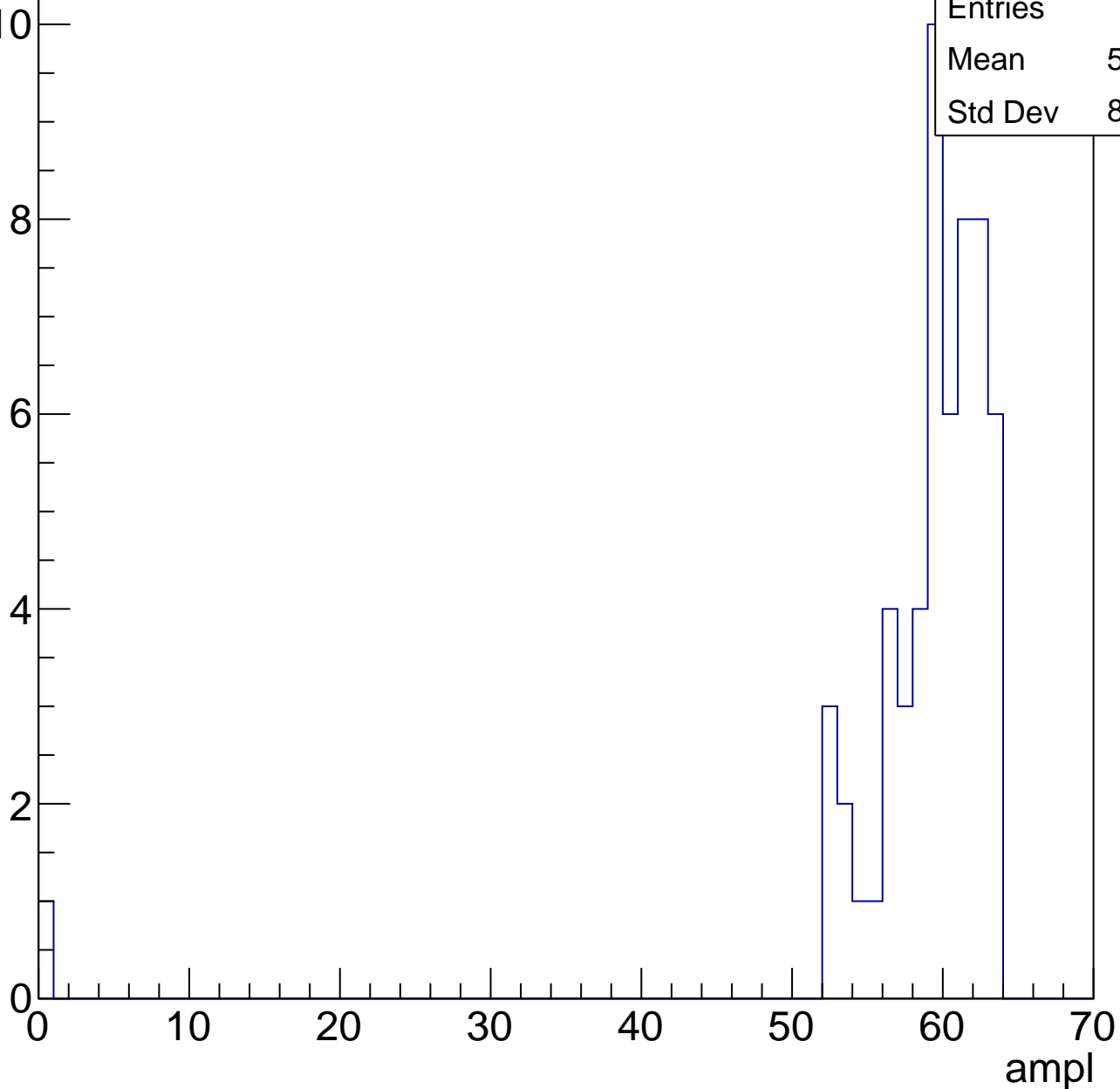


B1L102S, U13-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

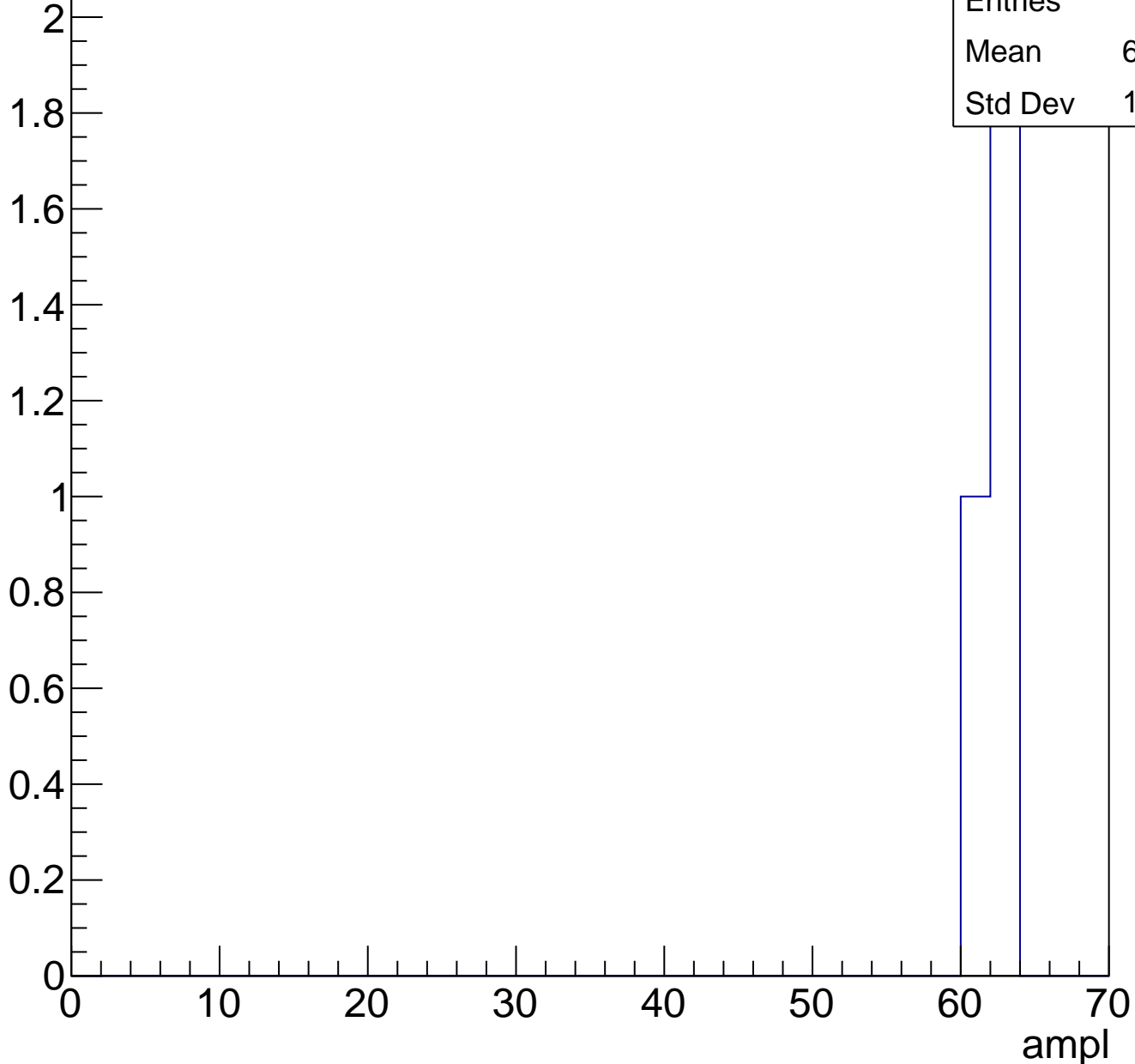
Entries	57
Mean	58.07
Std Dev	8.324



B1L102S, U13-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch37, adc0

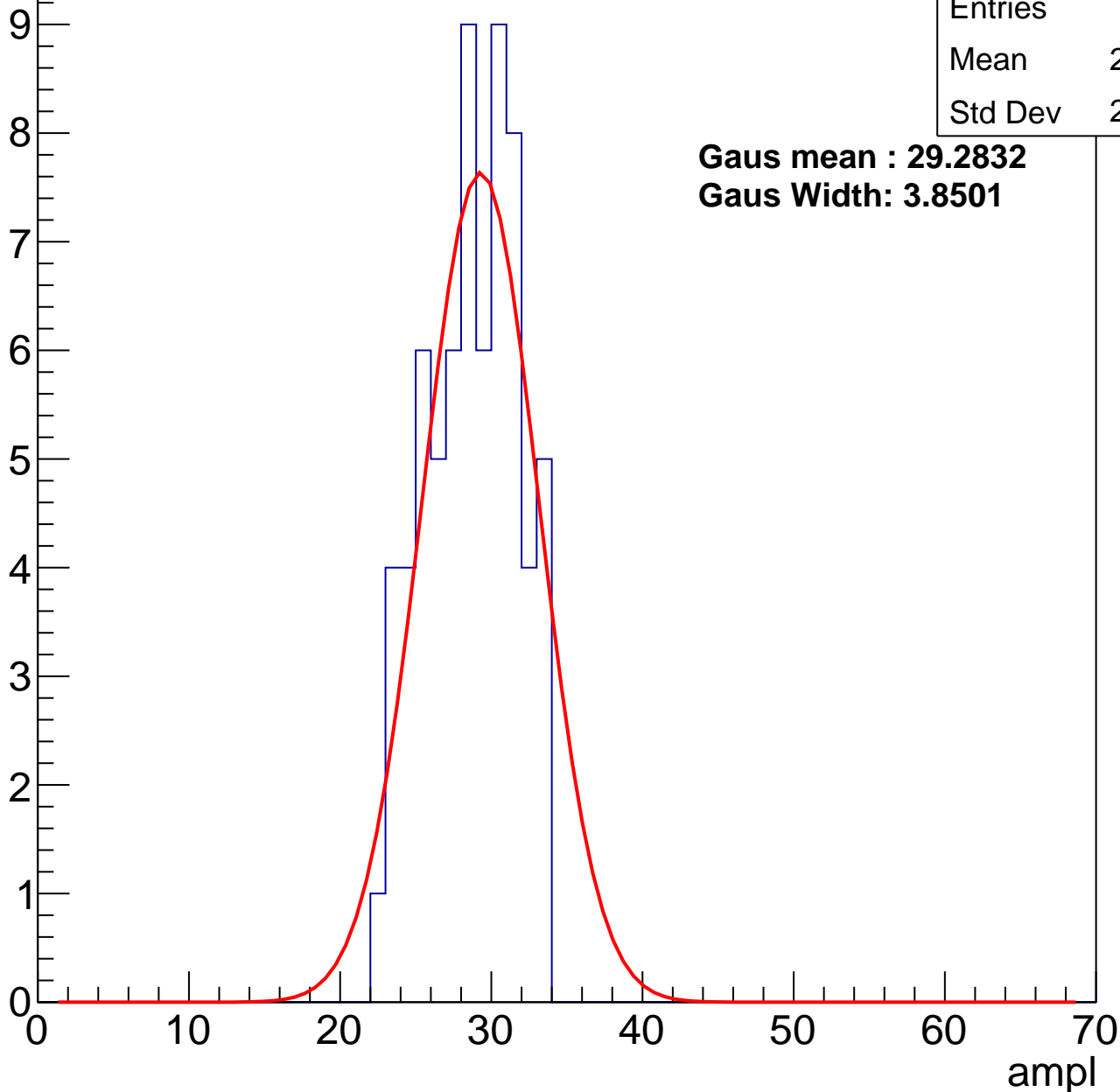
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.19
Std Dev	2.943

Gaus mean : 29.2832

Gaus Width: 3.8501



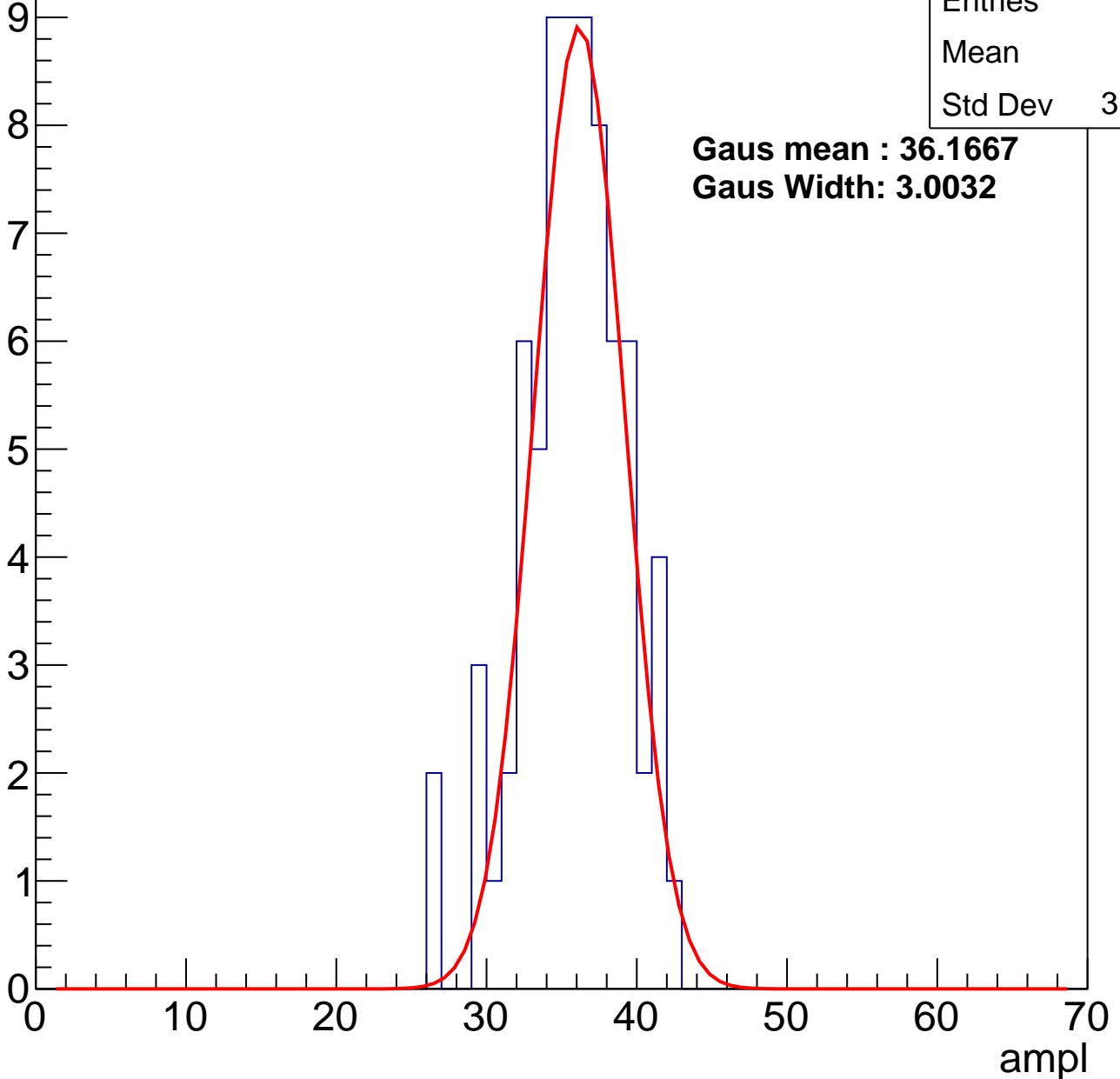
B1L102S, U13-ch37, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.3
Std Dev	3.387

Gaus mean : 36.1667
Gaus Width: 3.0032



B1L102S, U13-ch37, adc2

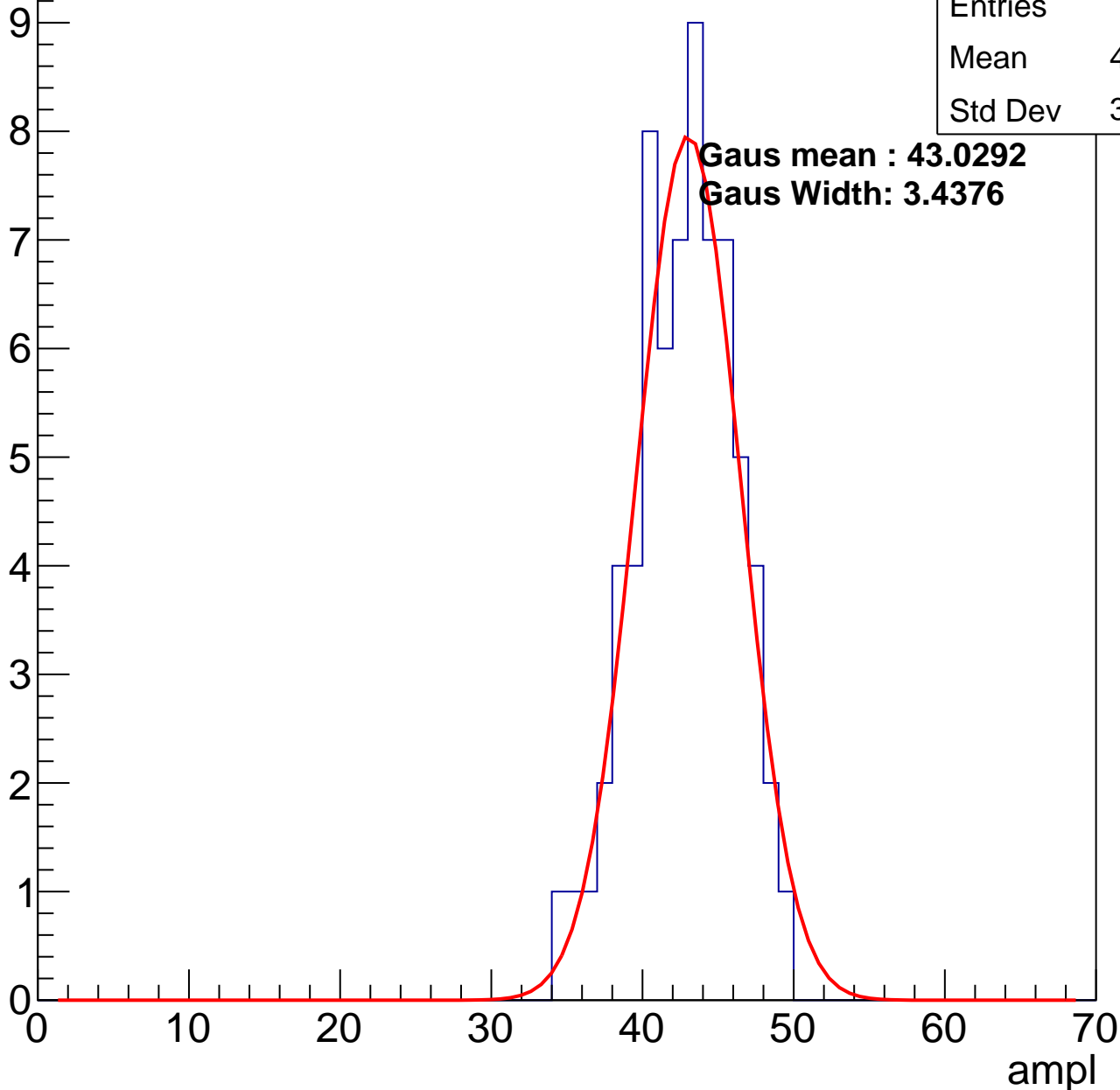
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.32
Std Dev	3.255

Gaus mean : 43.0292

Gaus Width: 3.4376

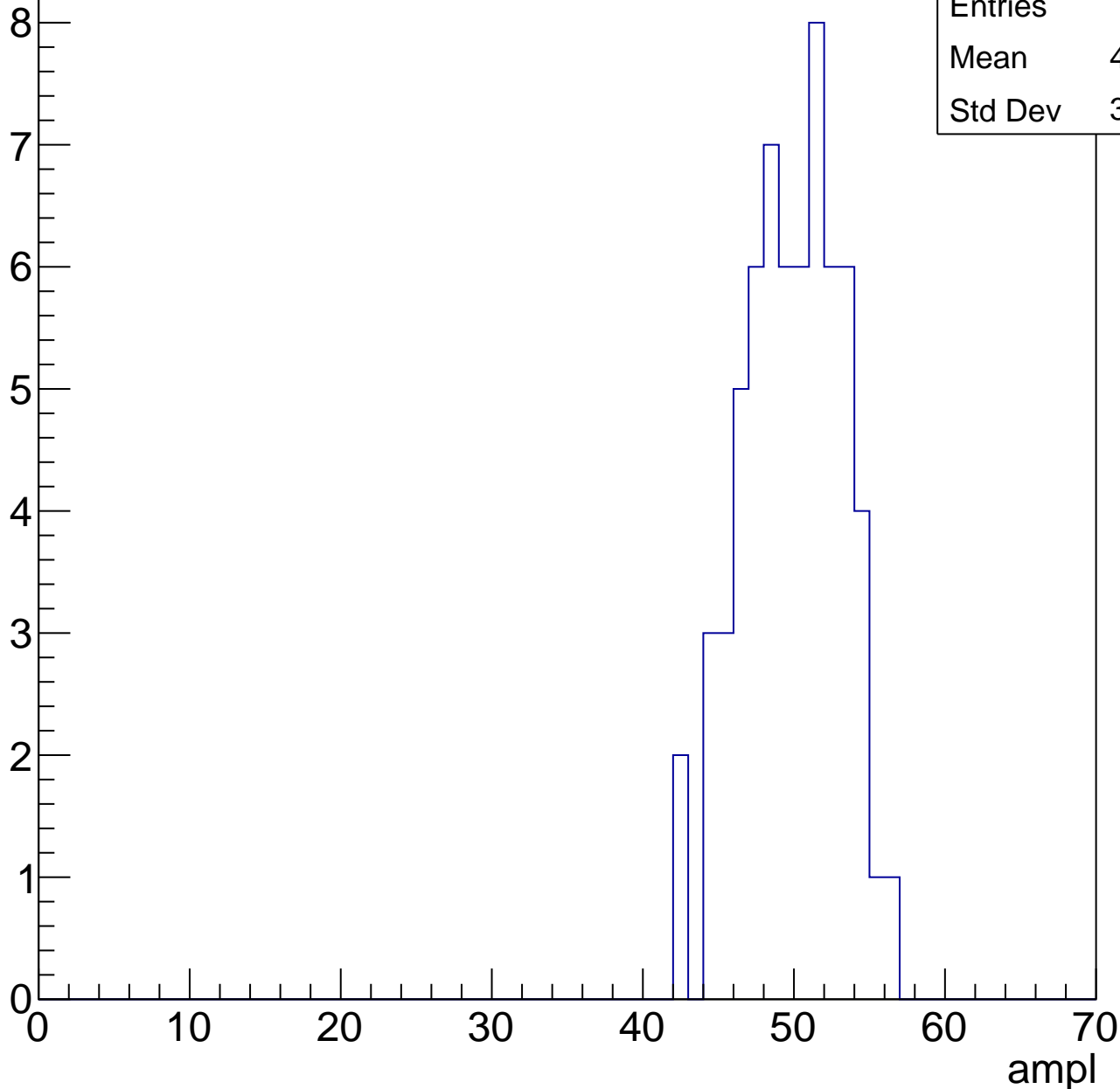


B1L102S, U13-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.34
Std Dev	3.217

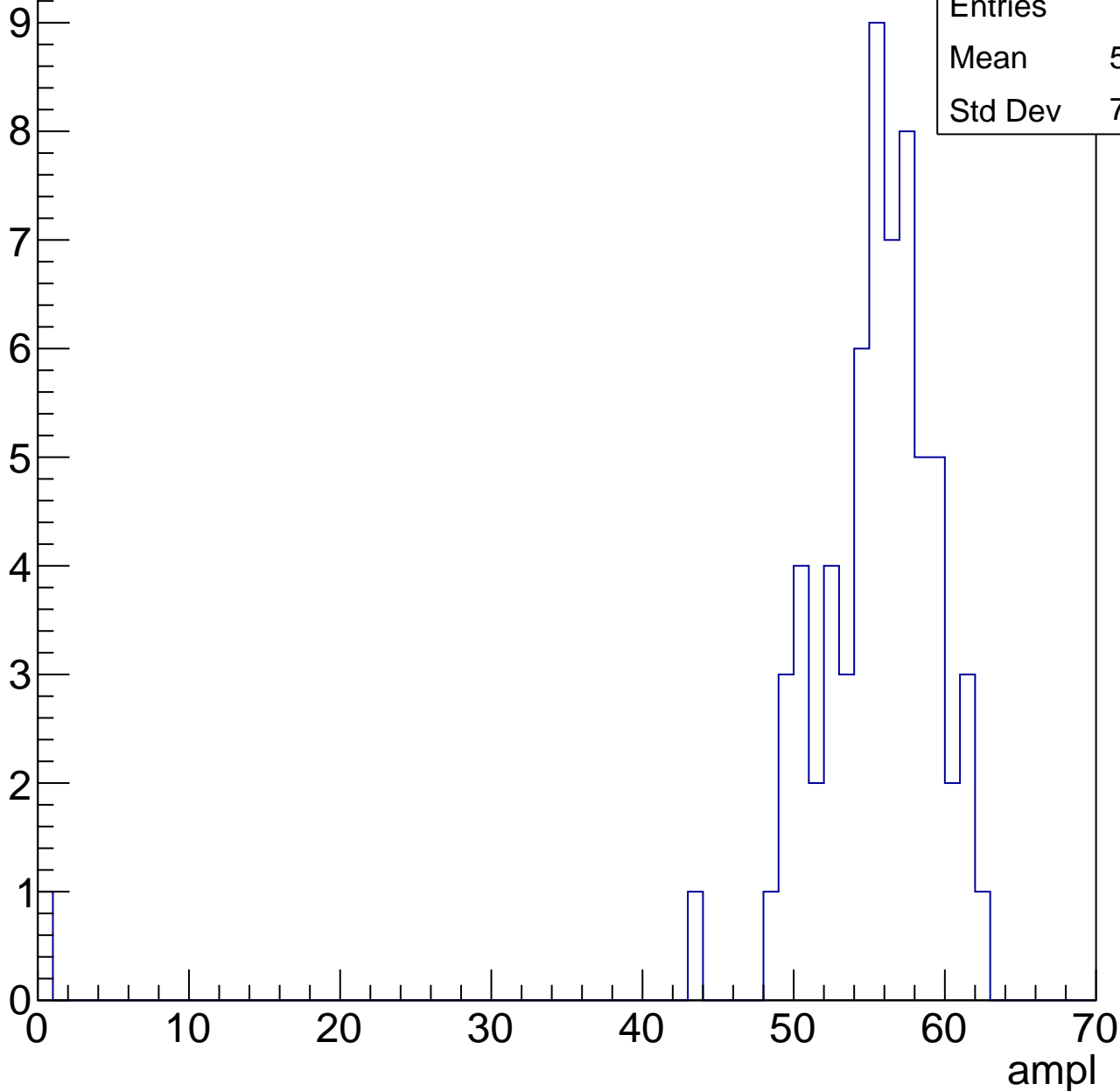


B1L102S, U13-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.22
Std Dev	7.689



B1L102S, U13-ch37, adc5

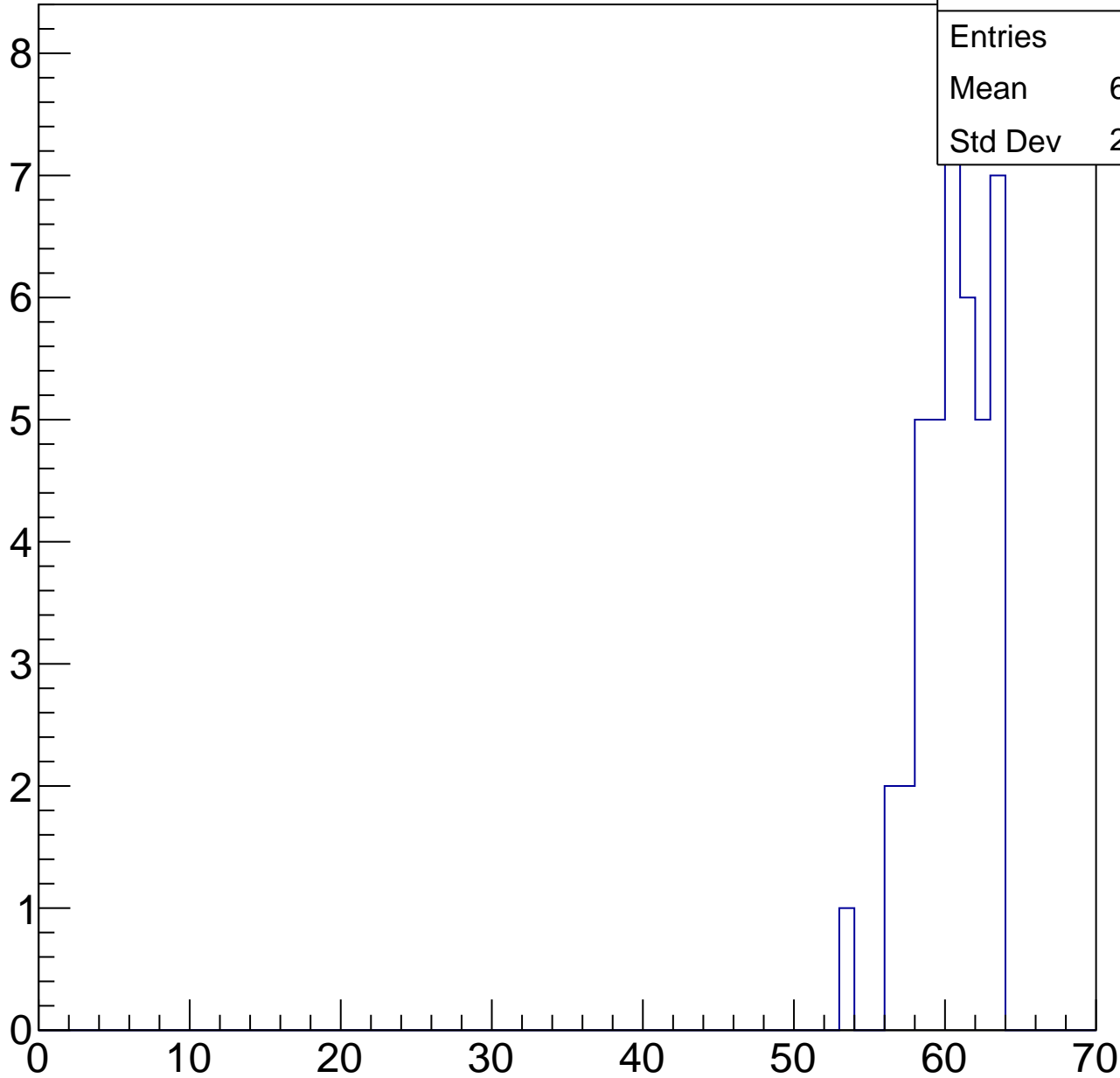
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.02
Std Dev	2.279

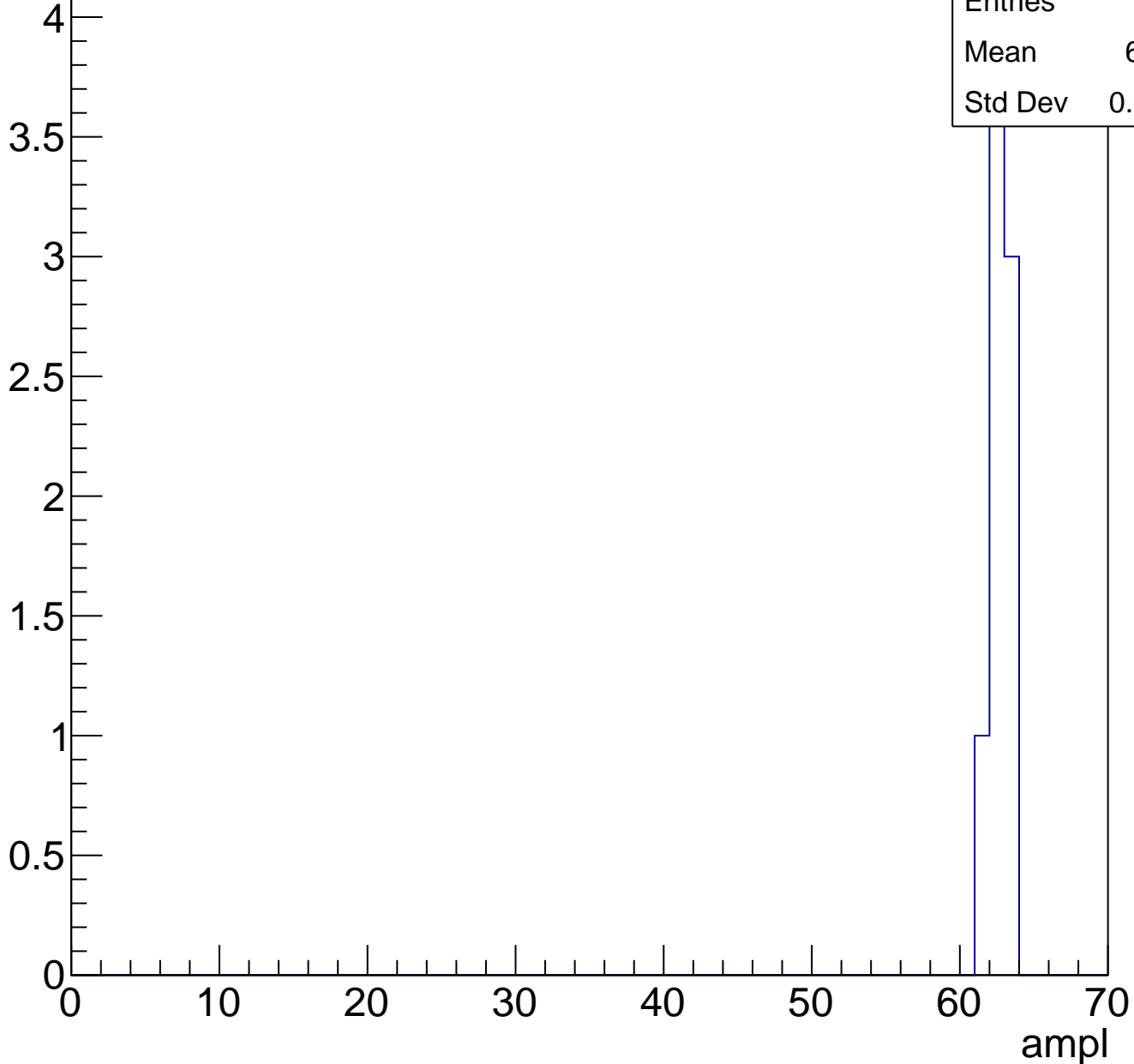
ampl



B1L102S, U13-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch38, adc0

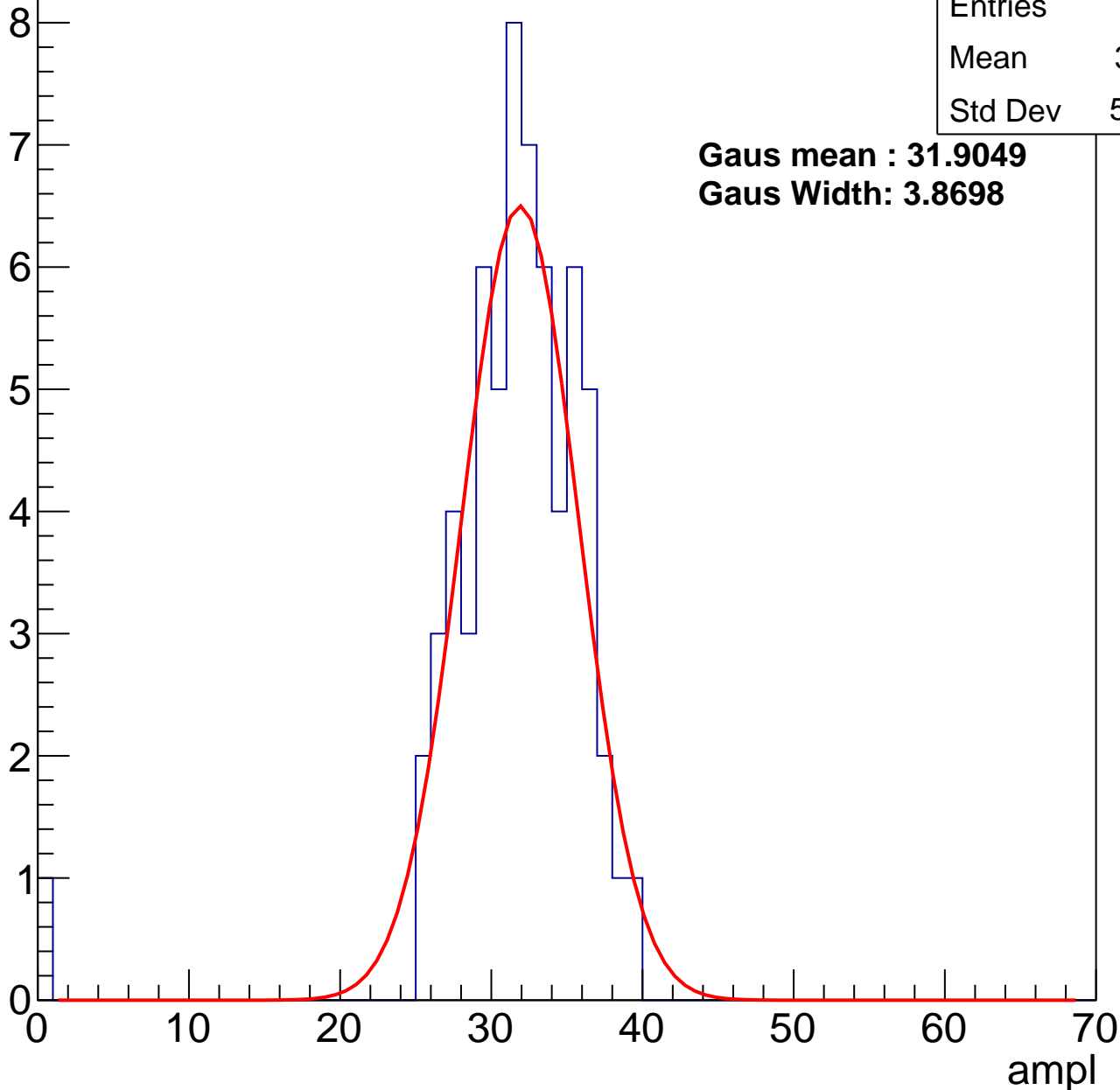
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	31.11
Std Dev	5.154

Gaus mean : 31.9049

Gaus Width: 3.8698



B1L102S, U13-ch38, adc1

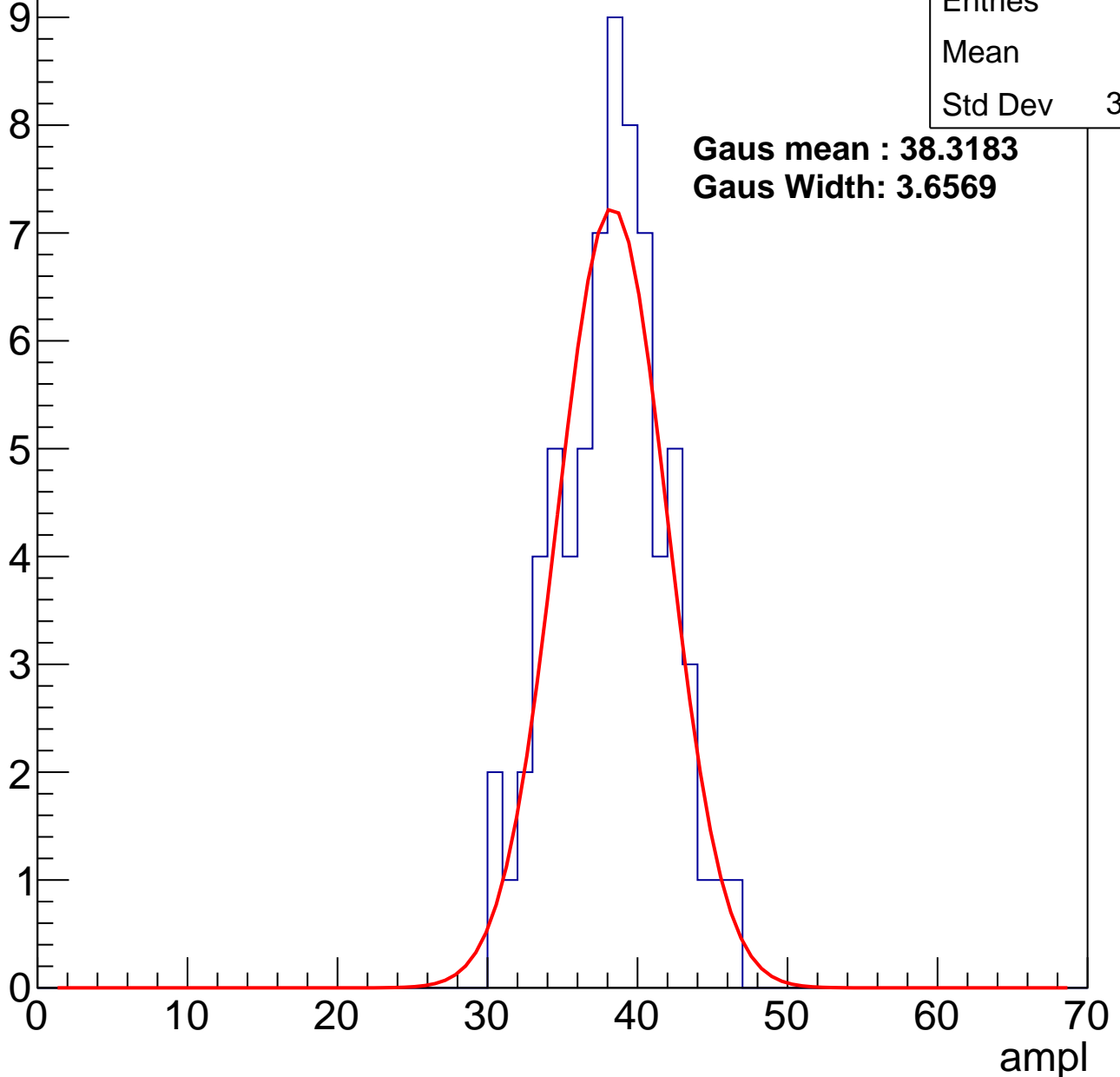
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.8
Std Dev	3.541

Gaus mean : 38.3183

Gaus Width: 3.6569



B1L102S, U13-ch38, adc2

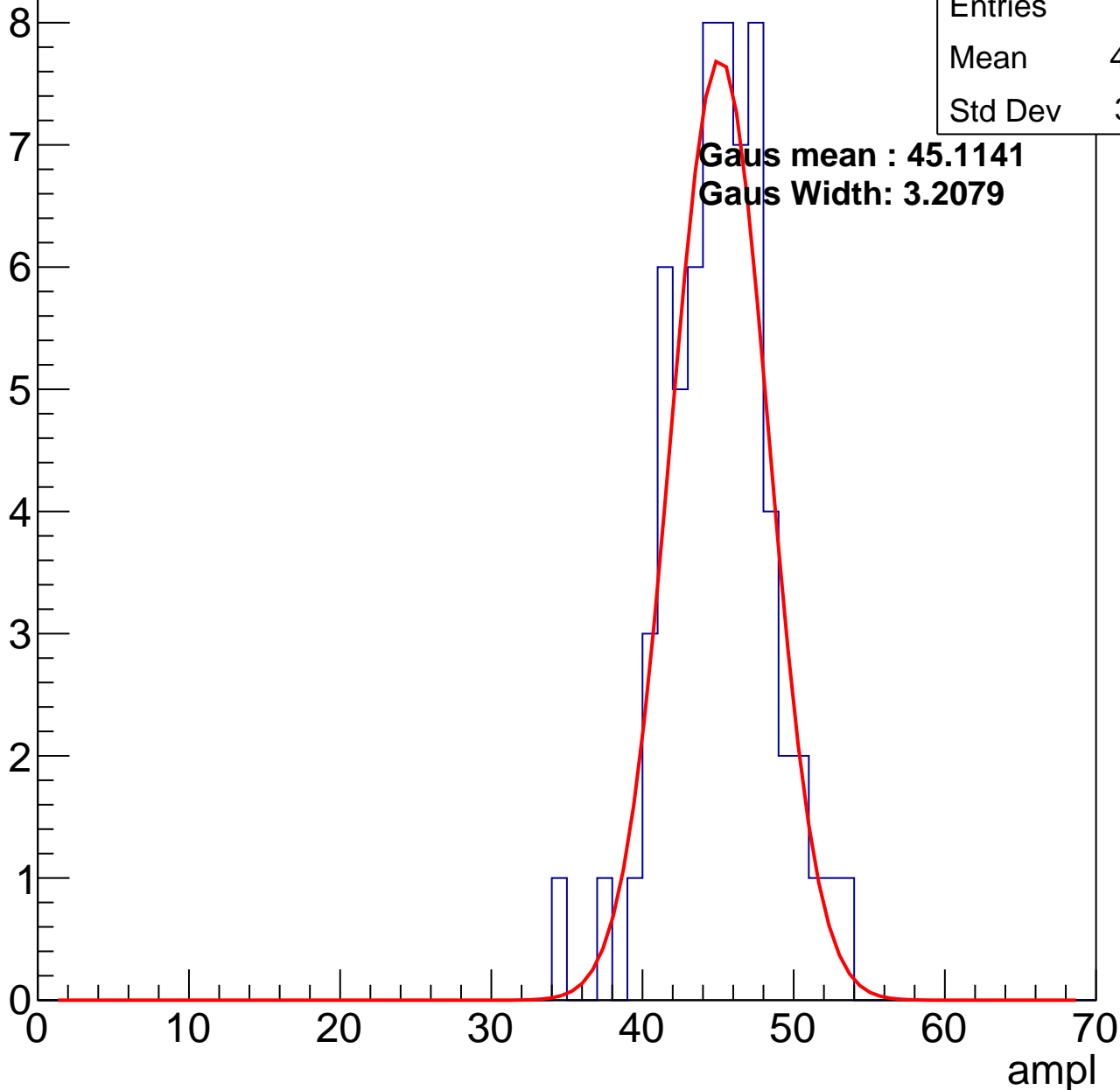
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.62
Std Dev	3.431

Gaus mean : 45.1141

Gaus Width: 3.2079

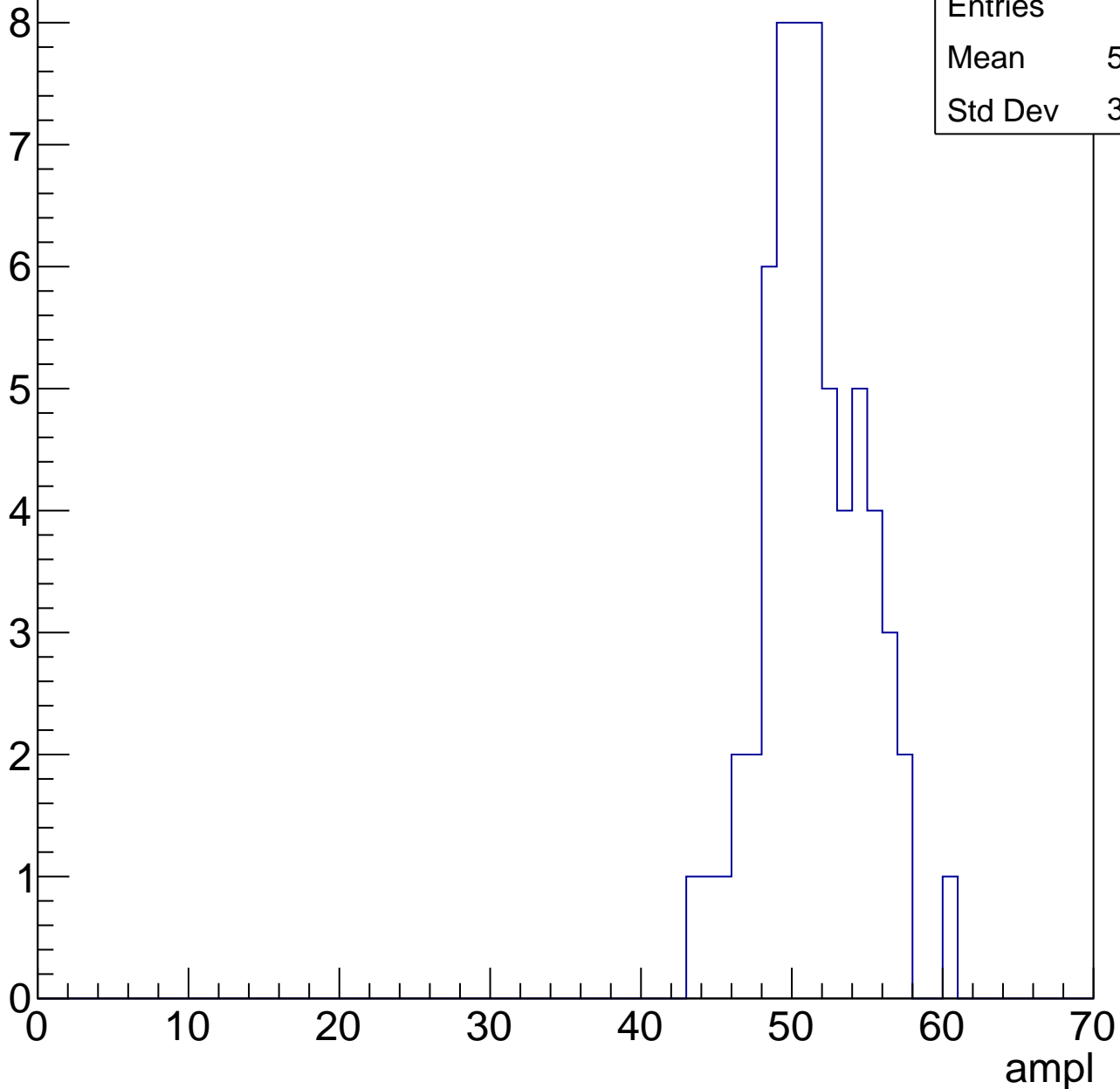


B1L102S, U13-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.98
Std Dev	3.346

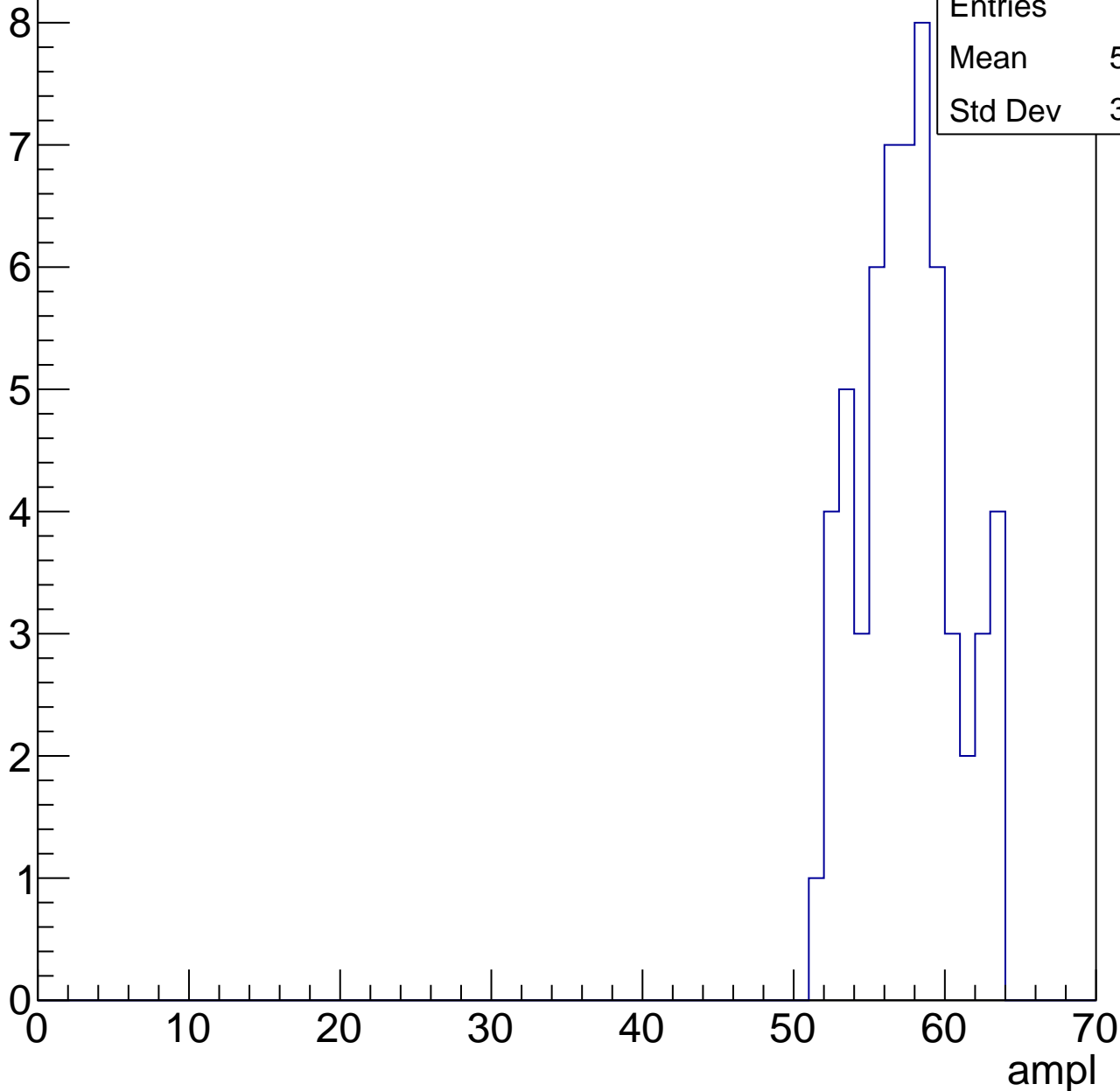


B1L102S, U13-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.03
Std Dev	3.146

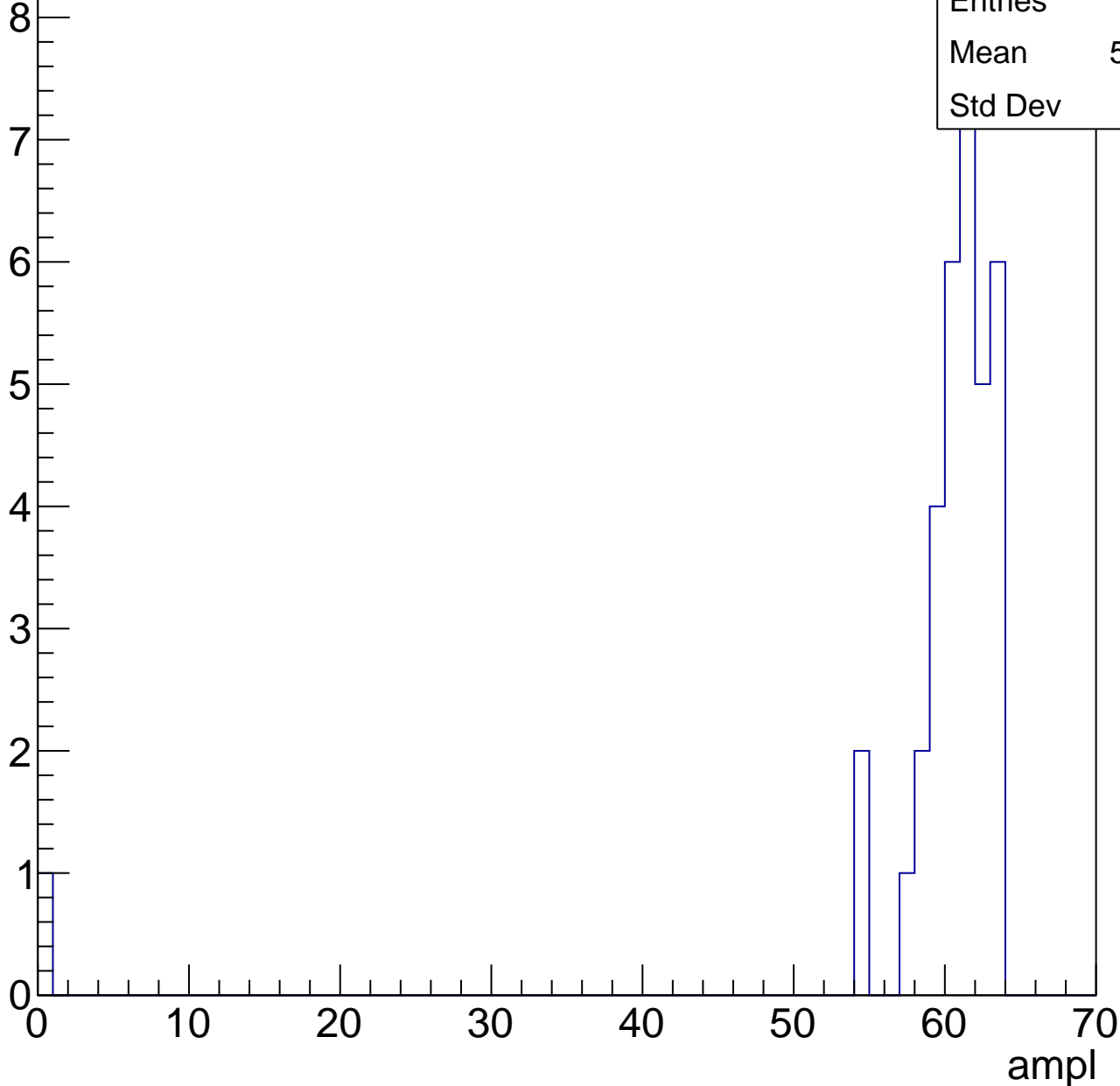


B1L102S, U13-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	58.66
Std Dev	10.3



B1L102S, U13-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch39, adc0

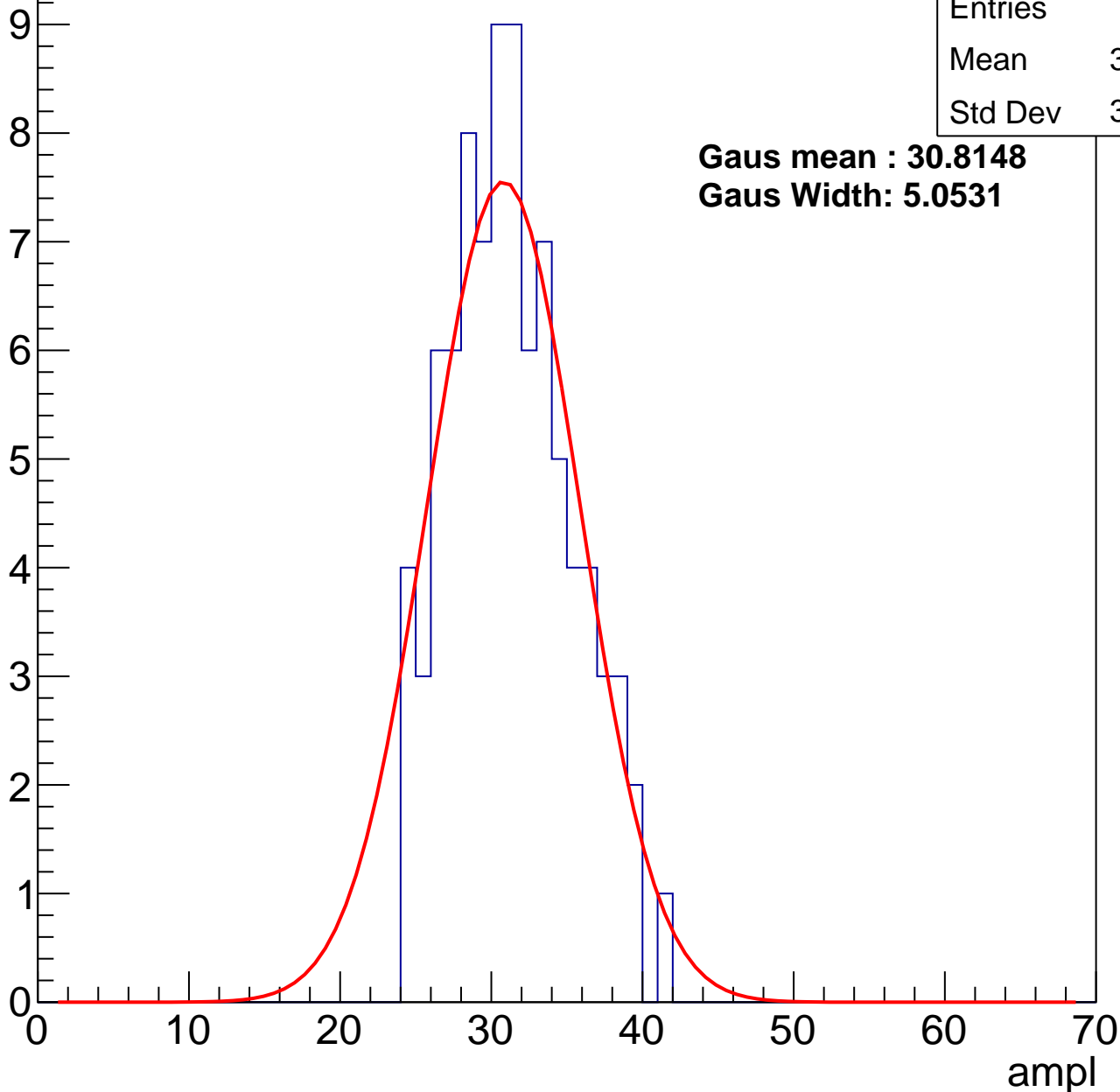
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	30.87
Std Dev	3.988

Gaus mean : 30.8148

Gaus Width: 5.0531



B1L102S, U13-ch39, adc1

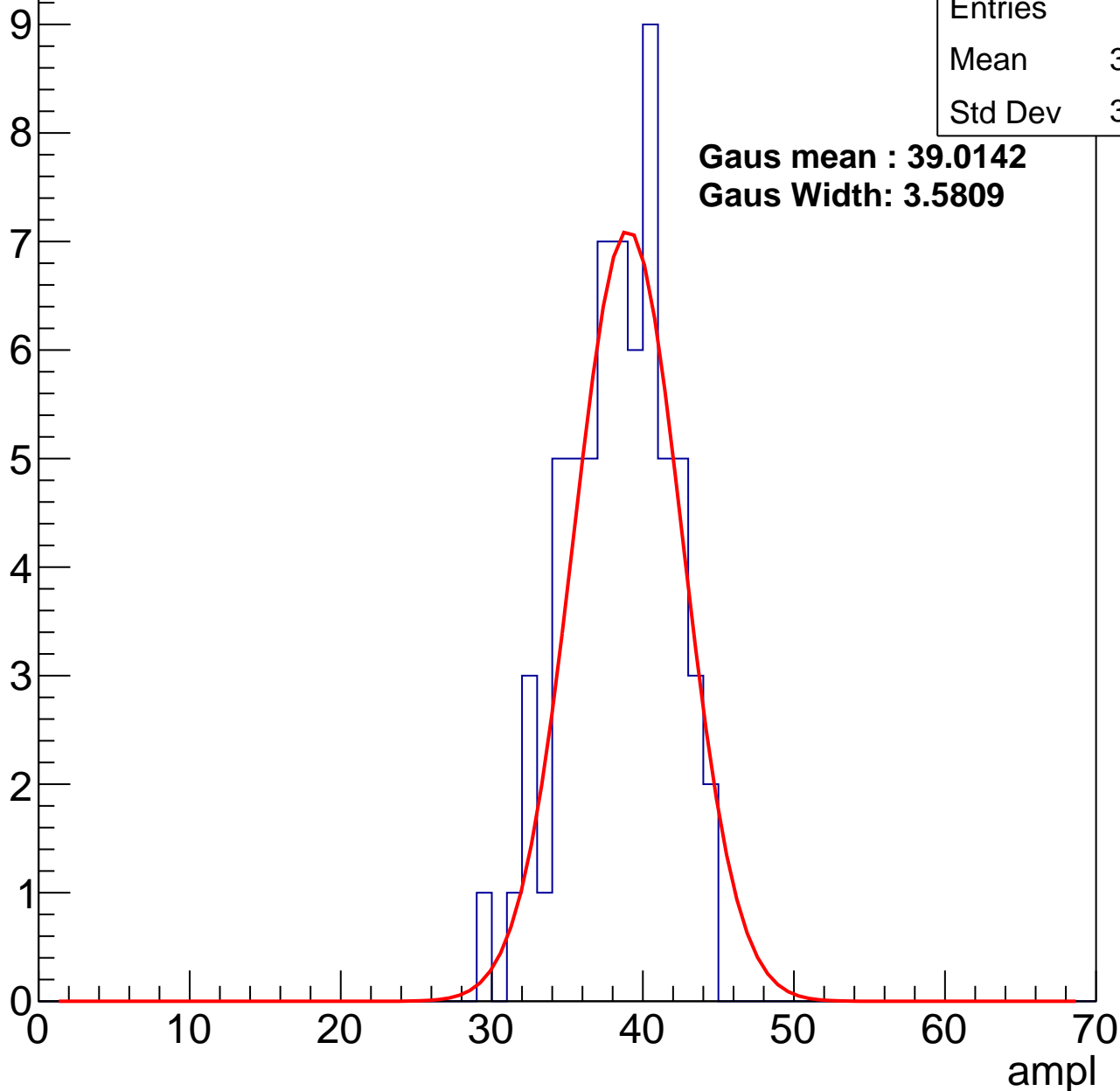
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	37.92
Std Dev	3.348

Gaus mean : 39.0142

Gaus Width: 3.5809



B1L102S, U13-ch39, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	45.08
Std Dev	3.817

Gaus mean : 45.9405

Gaus Width: 3.2083

10

8

6

4

2

0

0

10

20

30

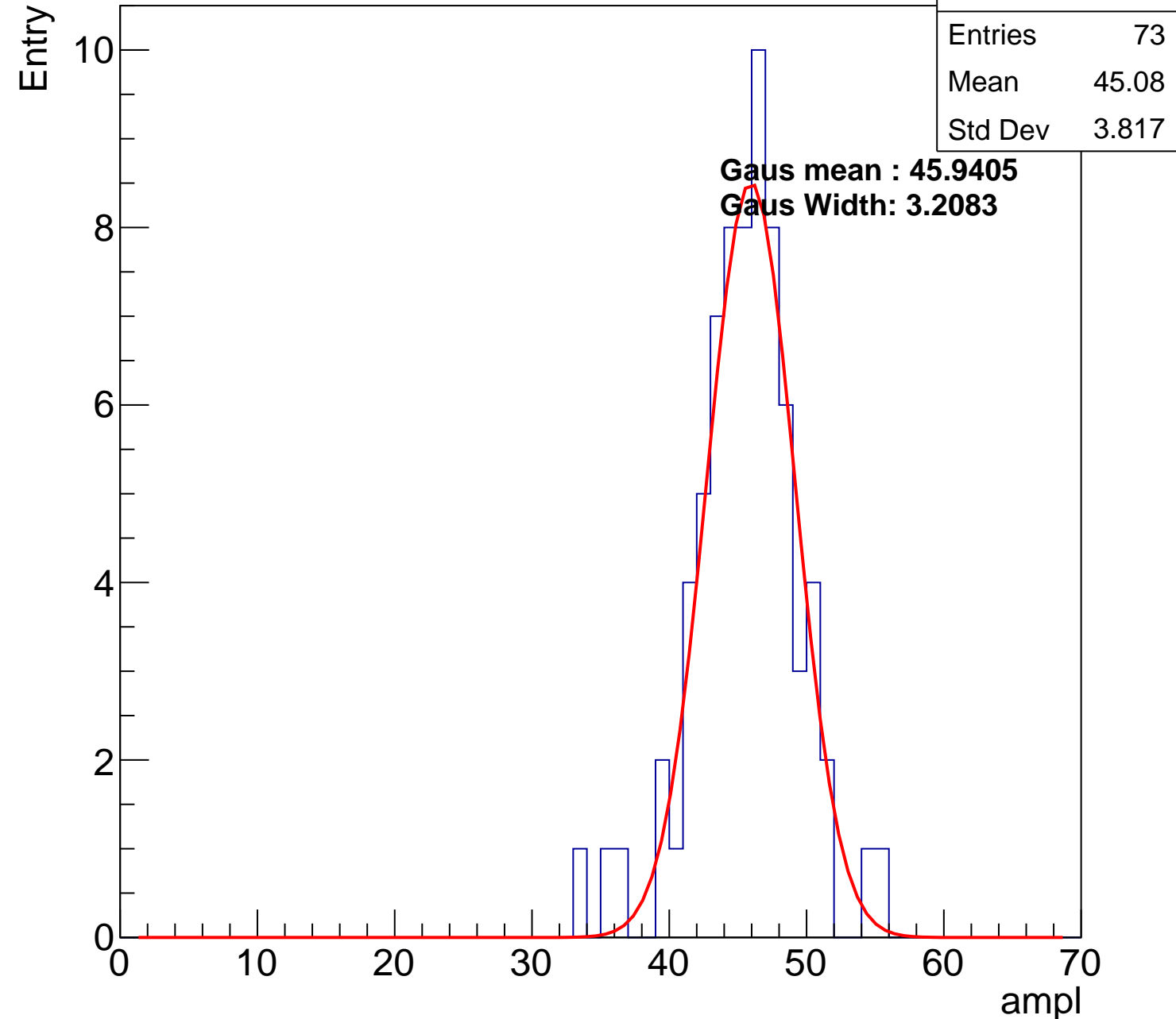
40

50

60

70

ampl

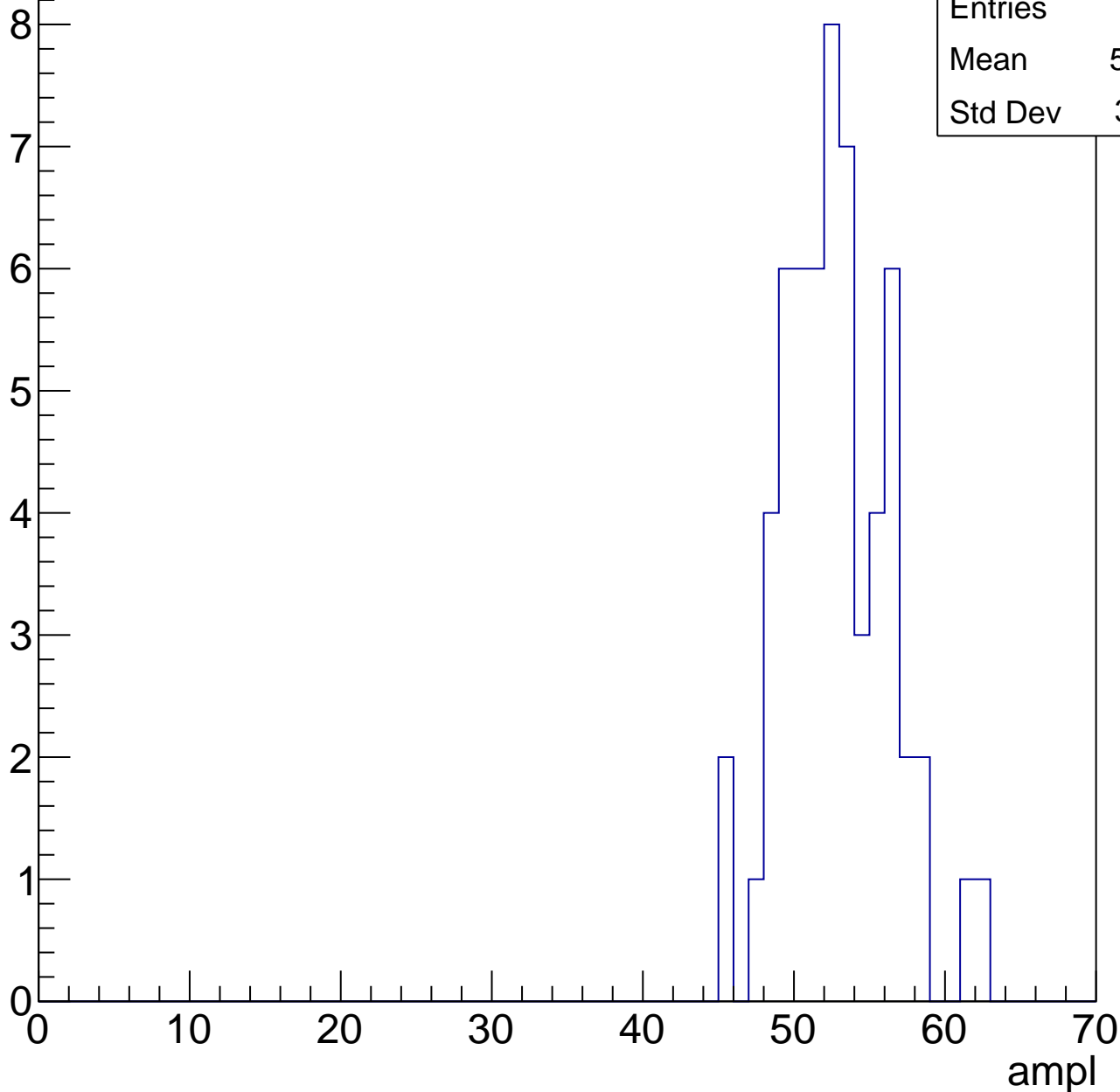


B1L102S, U13-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	52.32
Std Dev	3.491

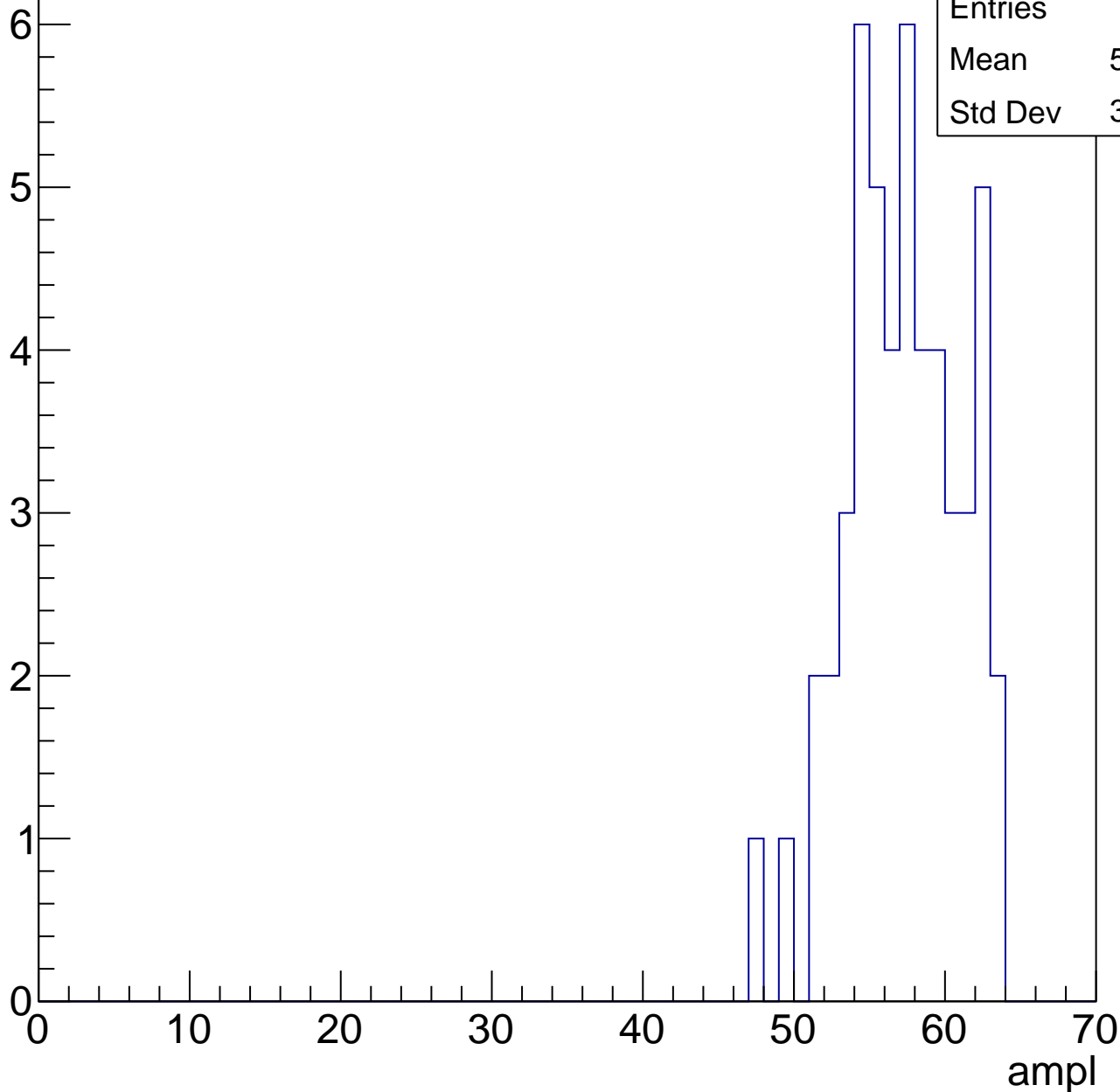


B1L102S, U13-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.73
Std Dev	3.705



B1L102S, U13-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

58.78

Std Dev

9.96

ampl

0

10

20

30

40

50

60

70

B1L102S, U13-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

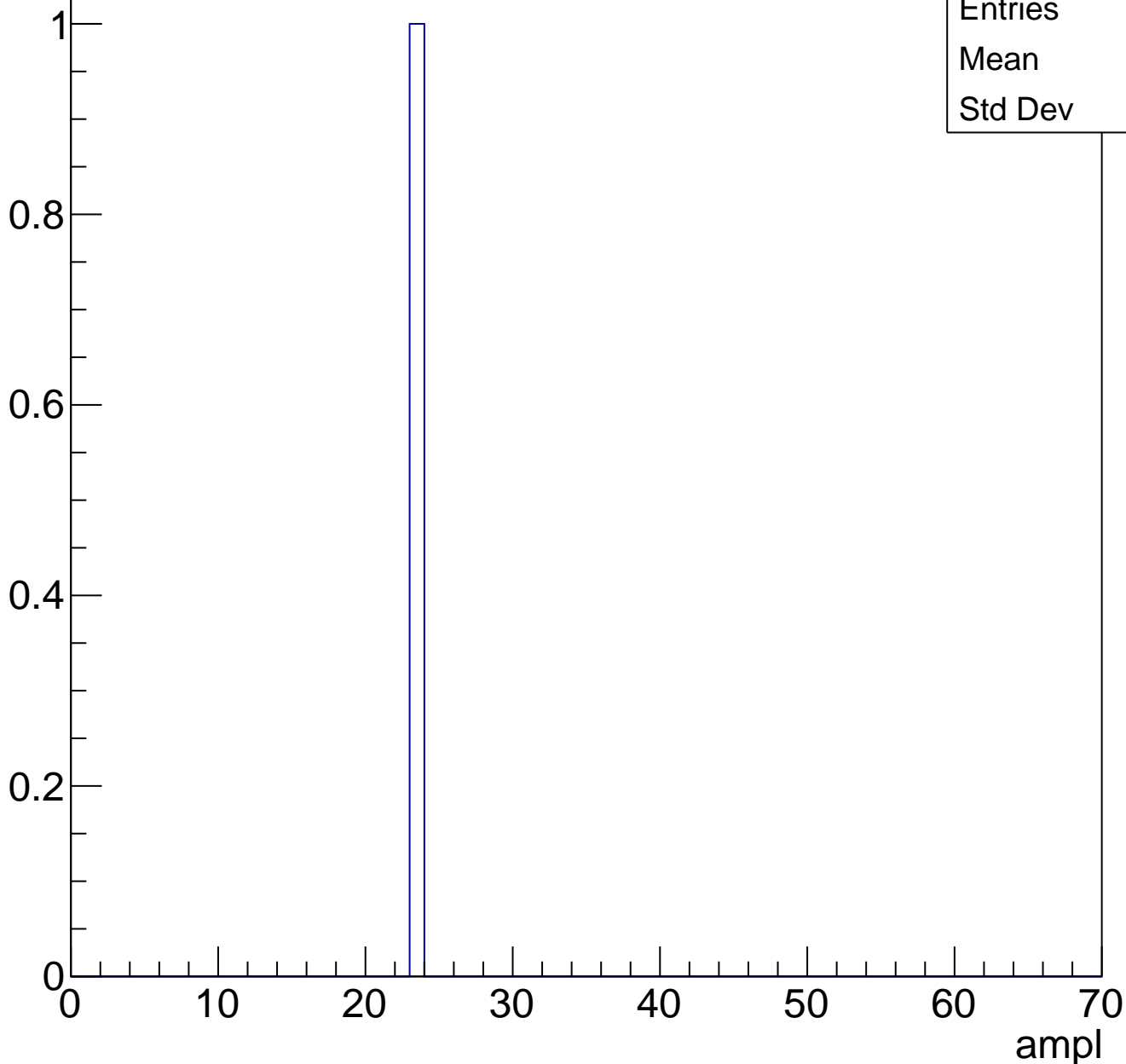
Entry



B1L102S, U13-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch40, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	30.25
Std Dev	2.906

Gaus mean : 30.1984

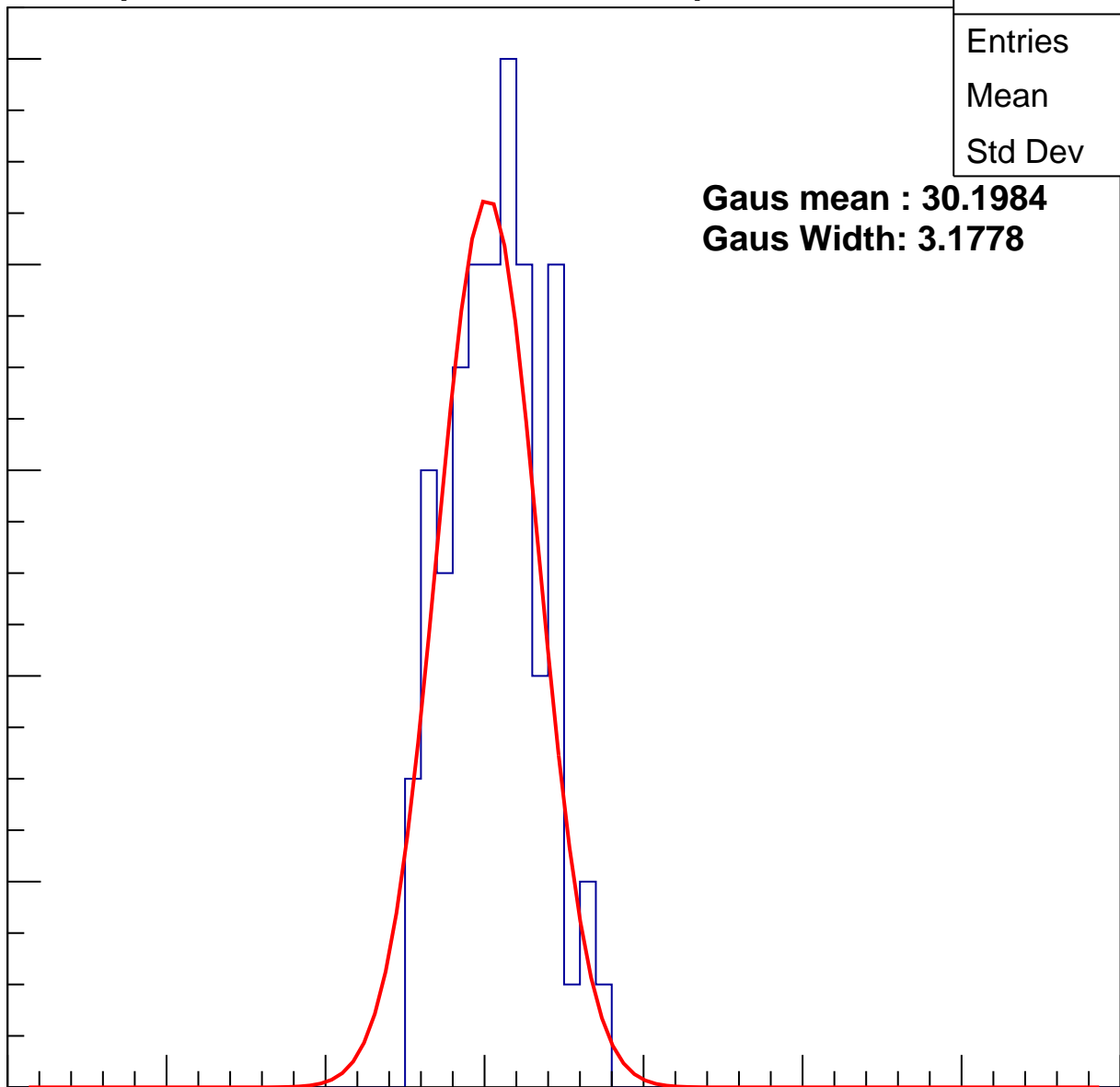
Gaus Width: 3.1778

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch40, adc1

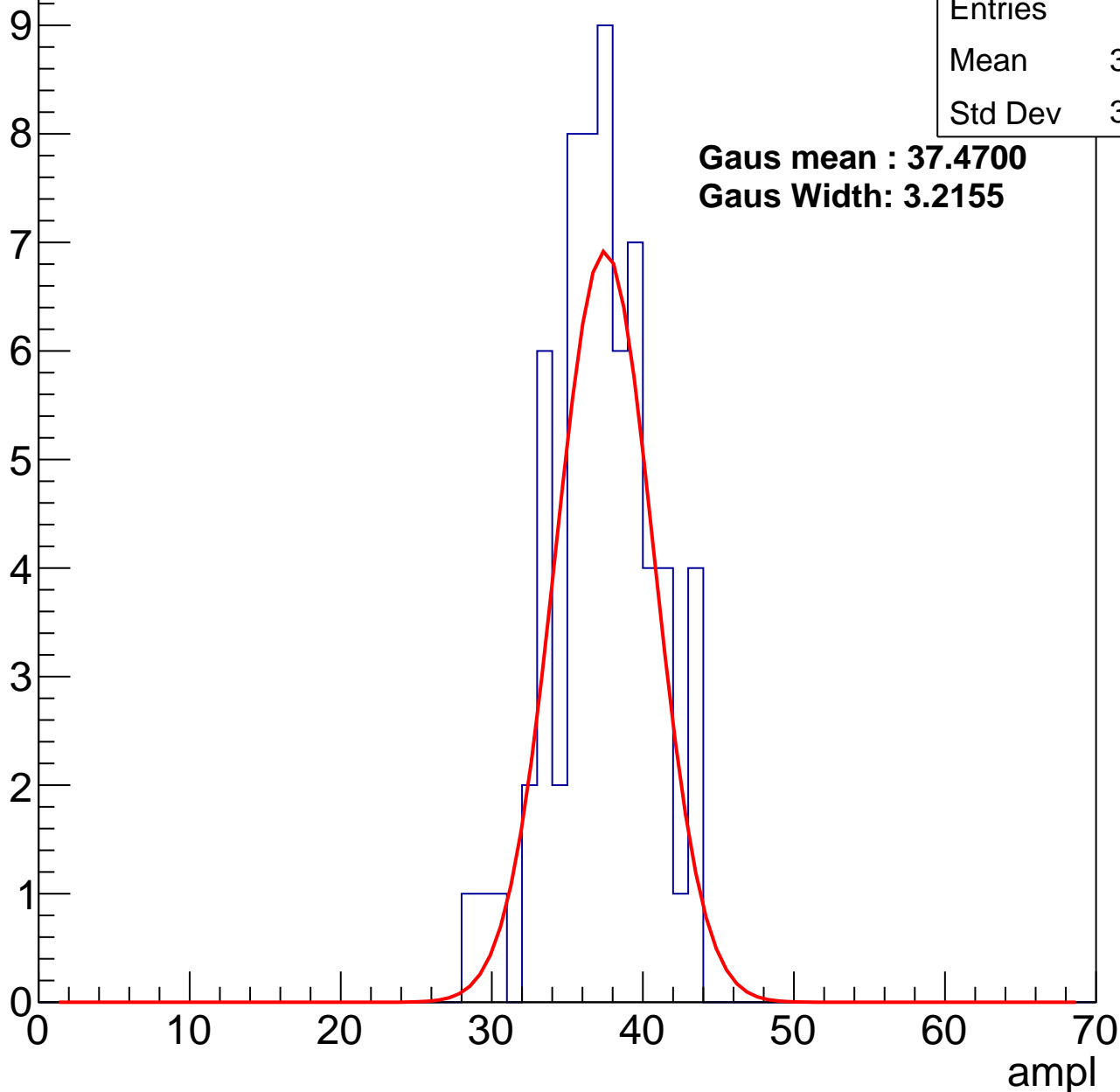
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.83
Std Dev	3.305

Gaus mean : 37.4700

Gaus Width: 3.2155



B1L102S, U13-ch40, adc2

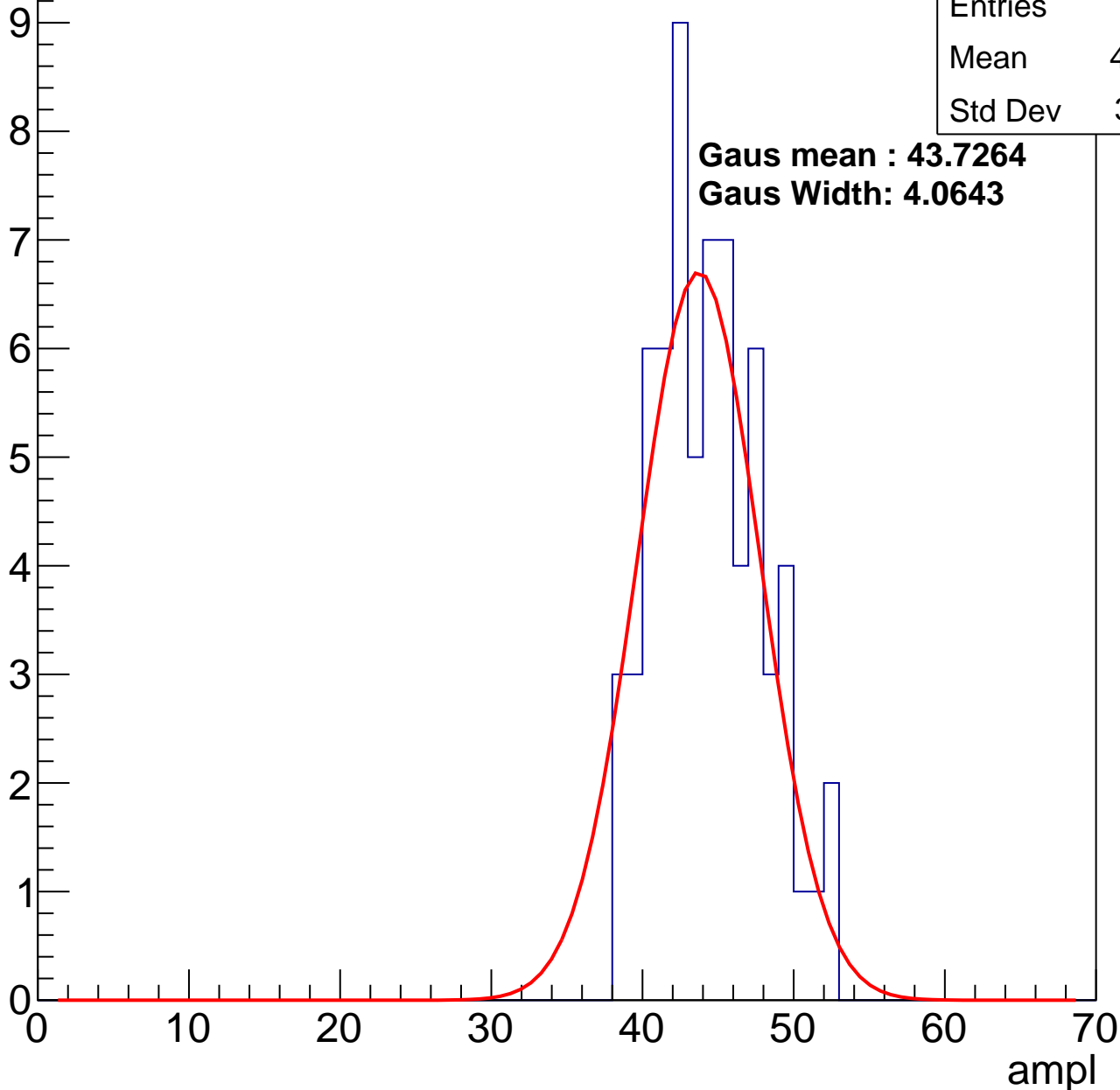
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.94
Std Dev	3.481

Gaus mean : 43.7264

Gaus Width: 4.0643

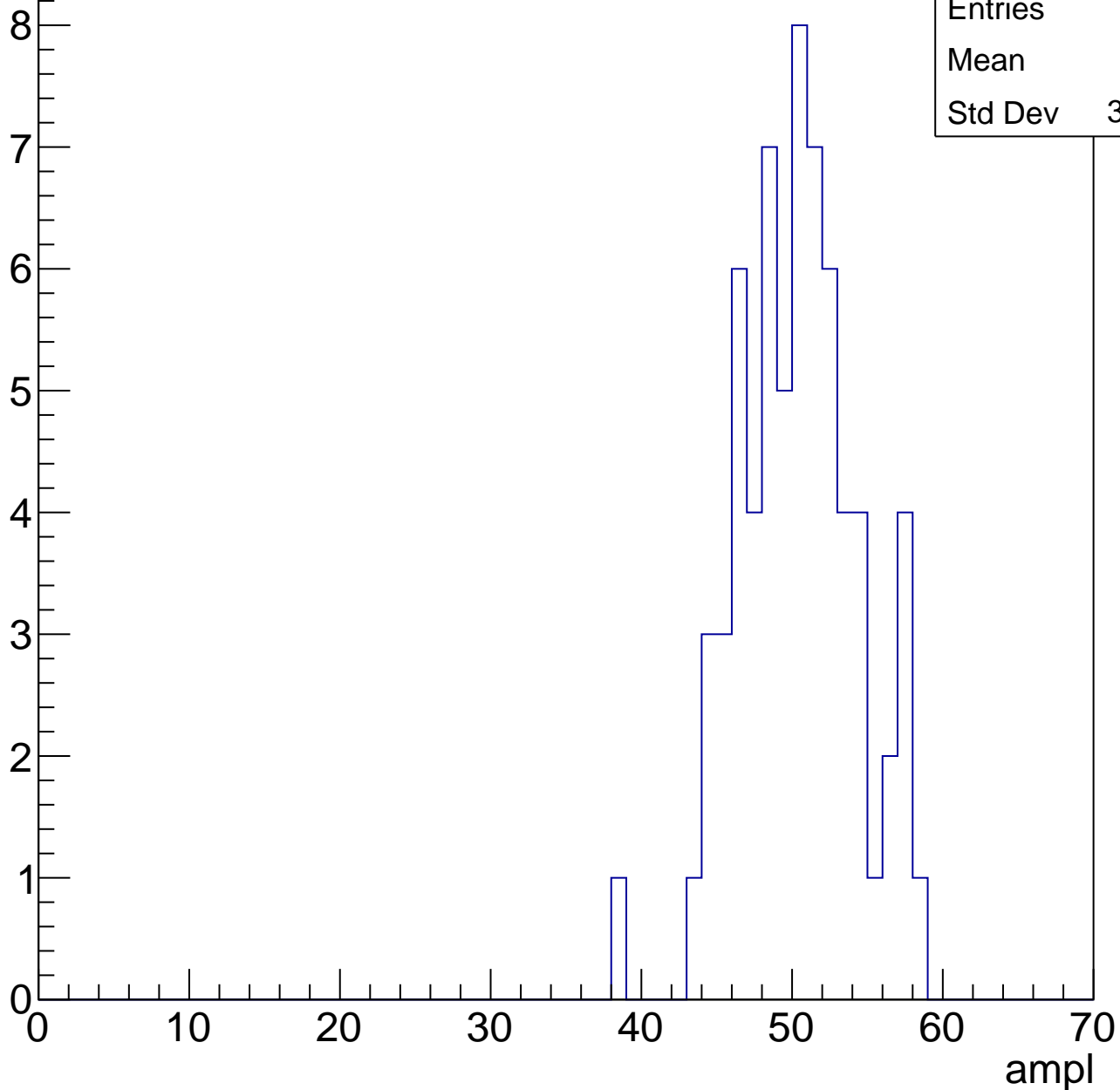


B1L102S, U13-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.9
Std Dev	3.918

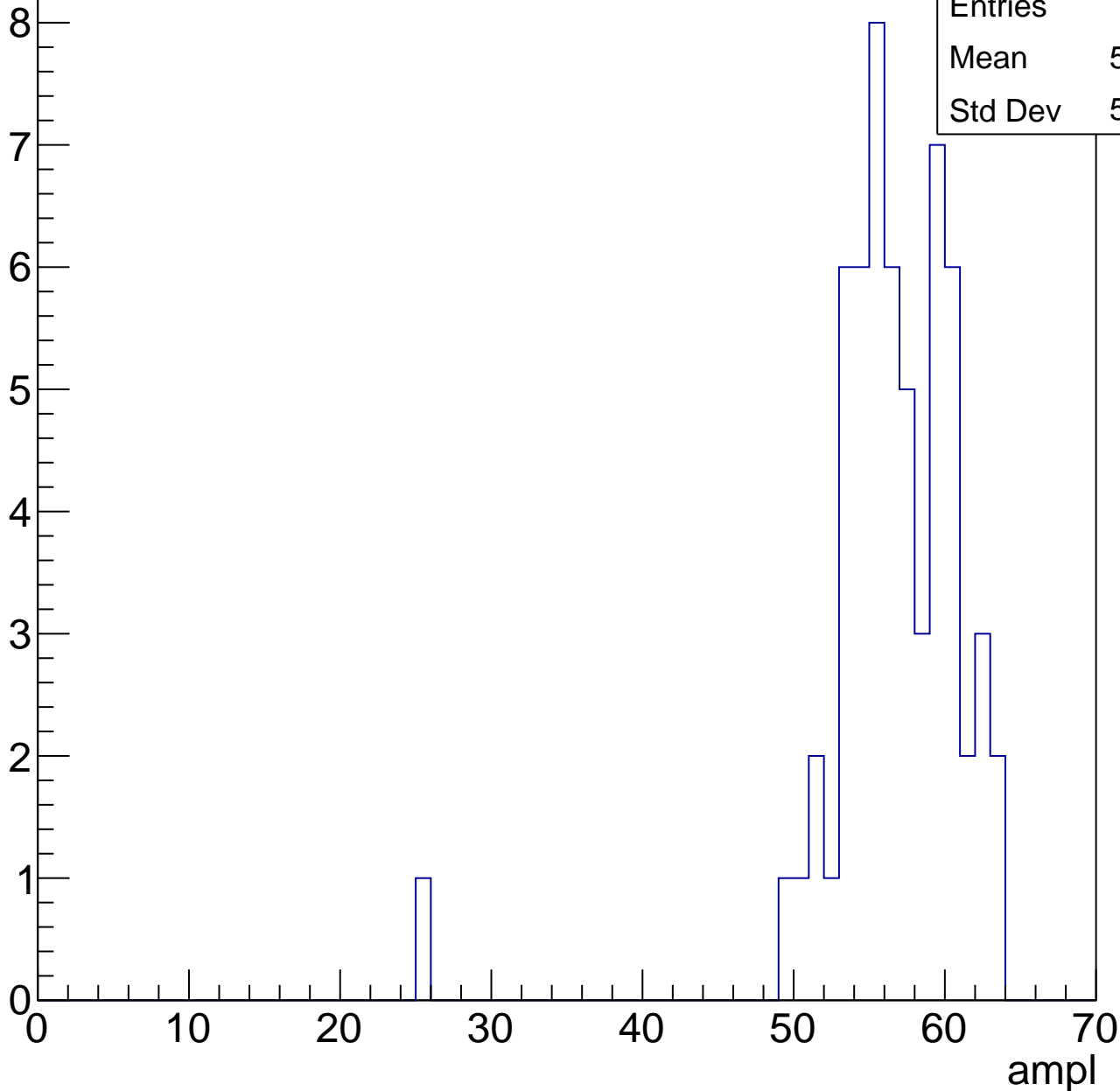


B1L102S, U13-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.03
Std Dev	5.212

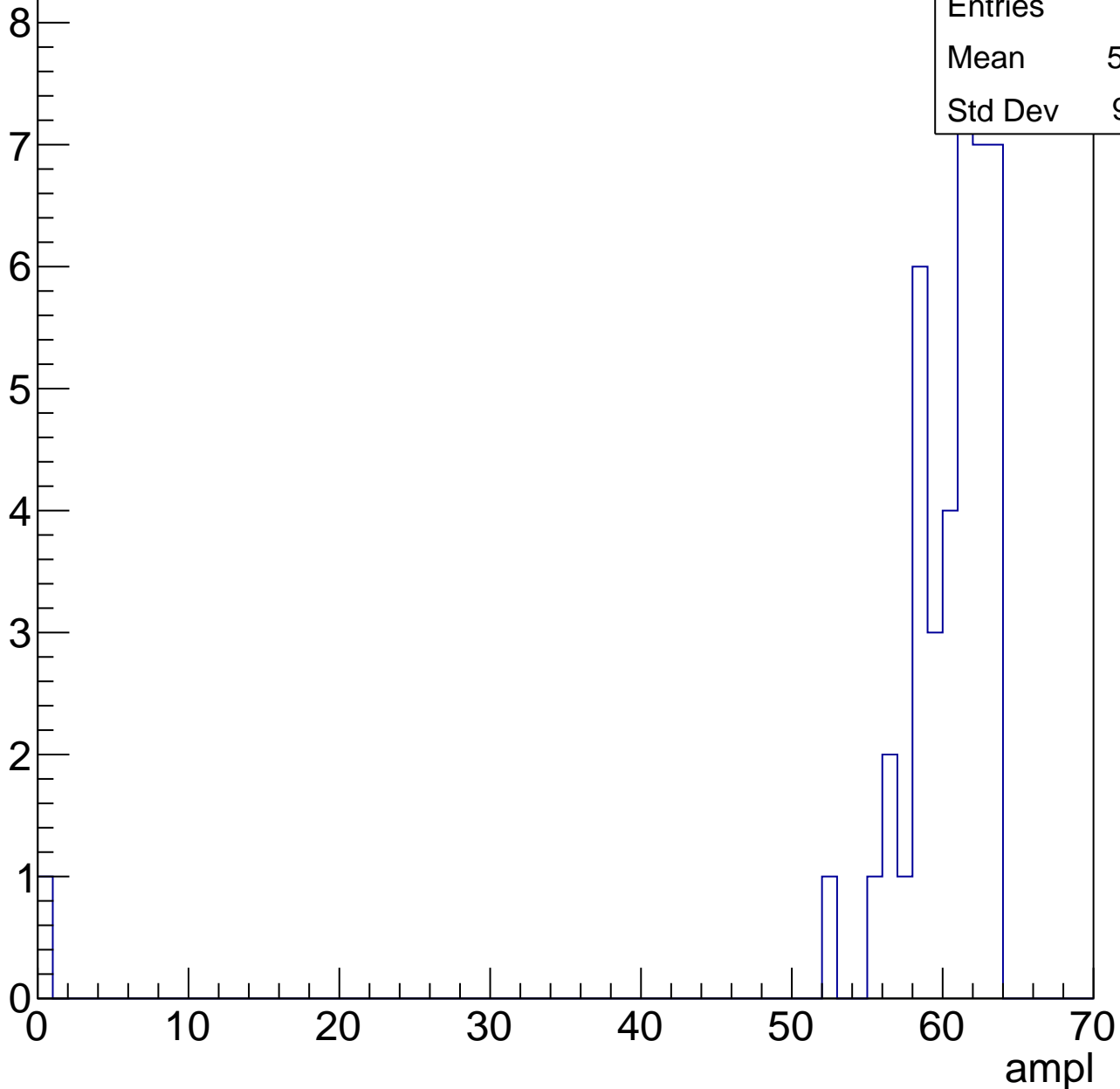


B1L102S, U13-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.63
Std Dev	9.601



B1L102S, U13-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch41, adc0

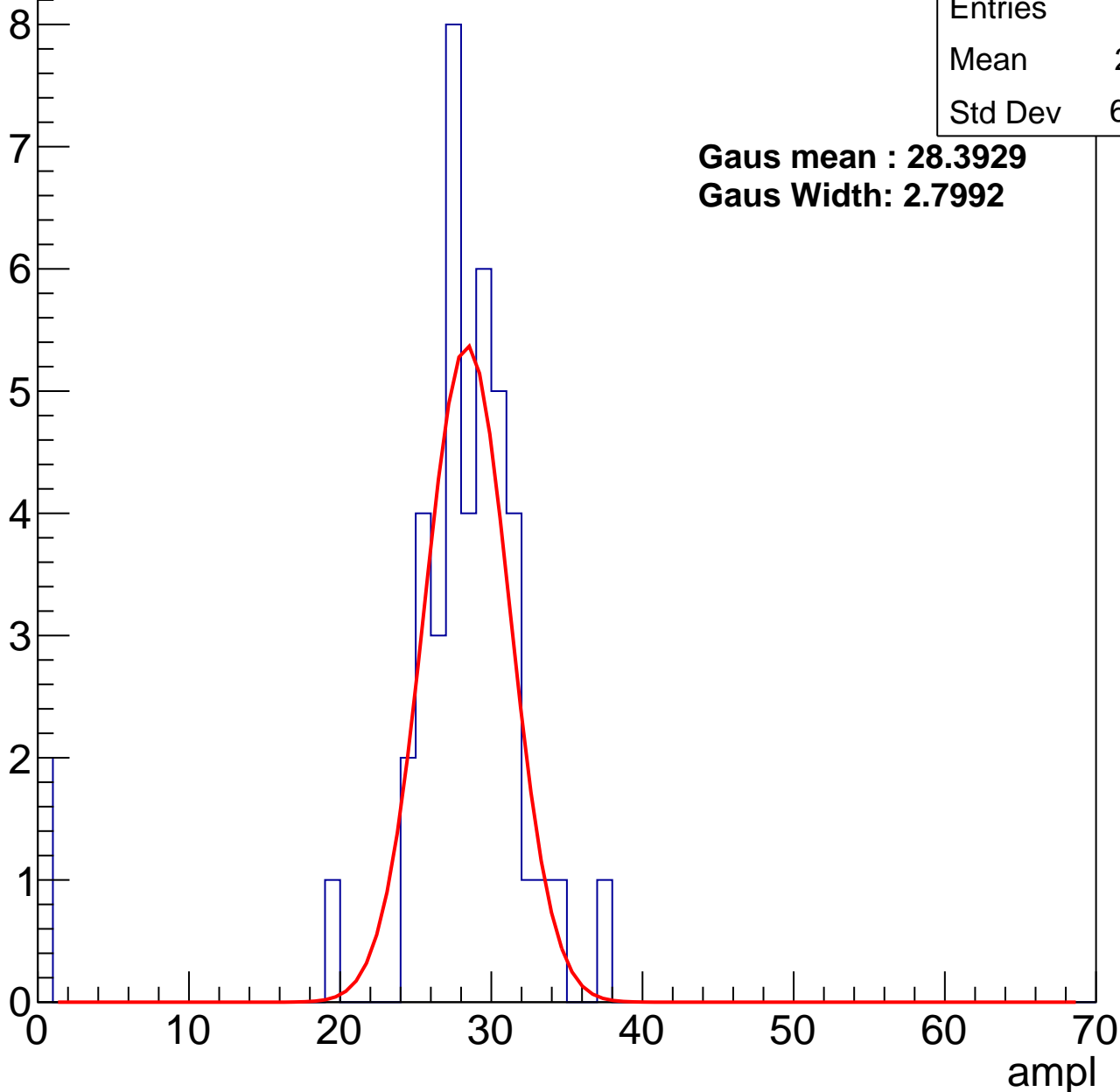
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	26.91
Std Dev	6.657

Gaus mean : 28.3929

Gaus Width: 2.7992

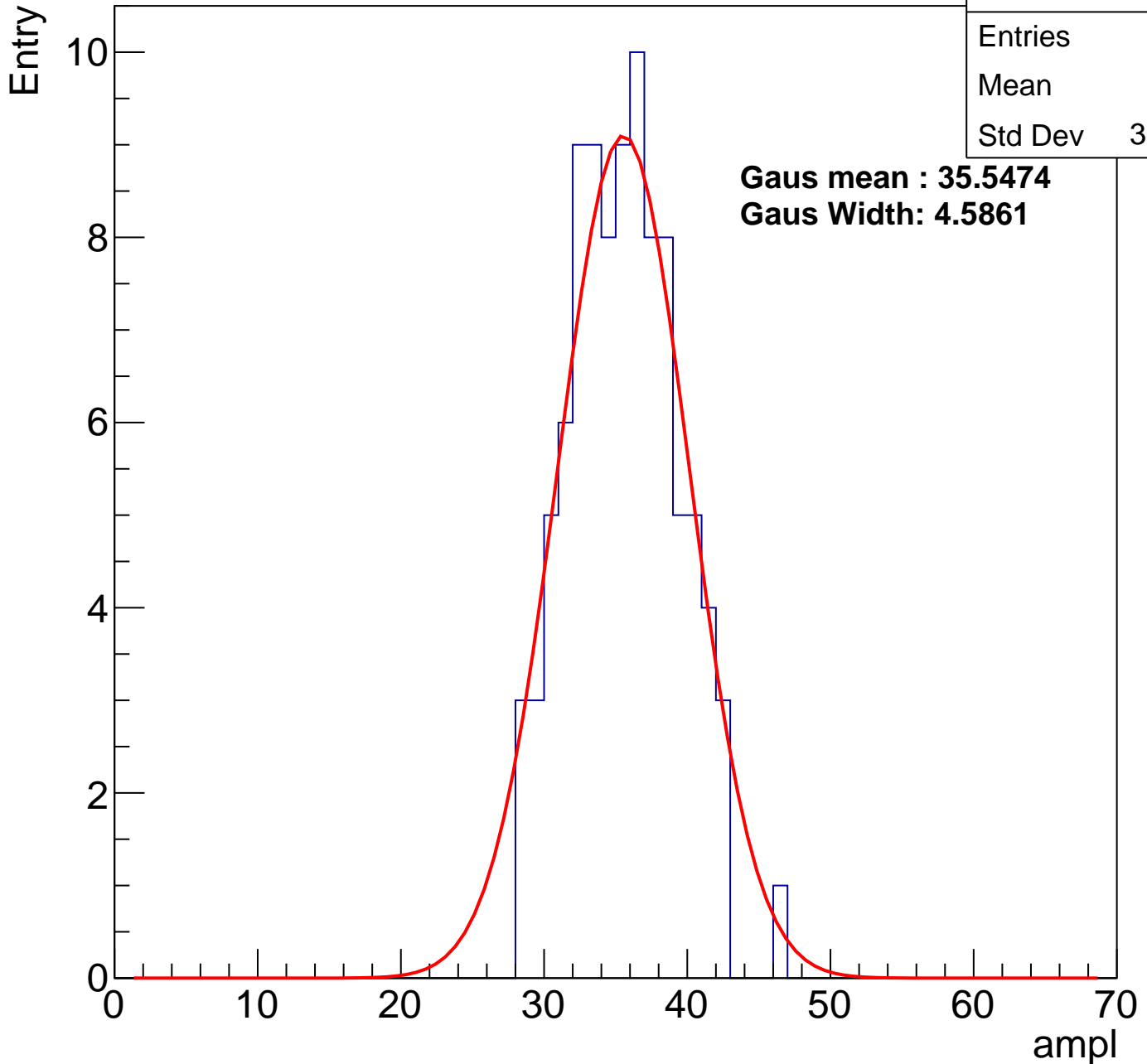


B1L102S, U13-ch41, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	96
Mean	35.1
Std Dev	3.723

Gaus mean : 35.5474
Gaus Width: 4.5861



B1L102S, U13-ch41, adc2

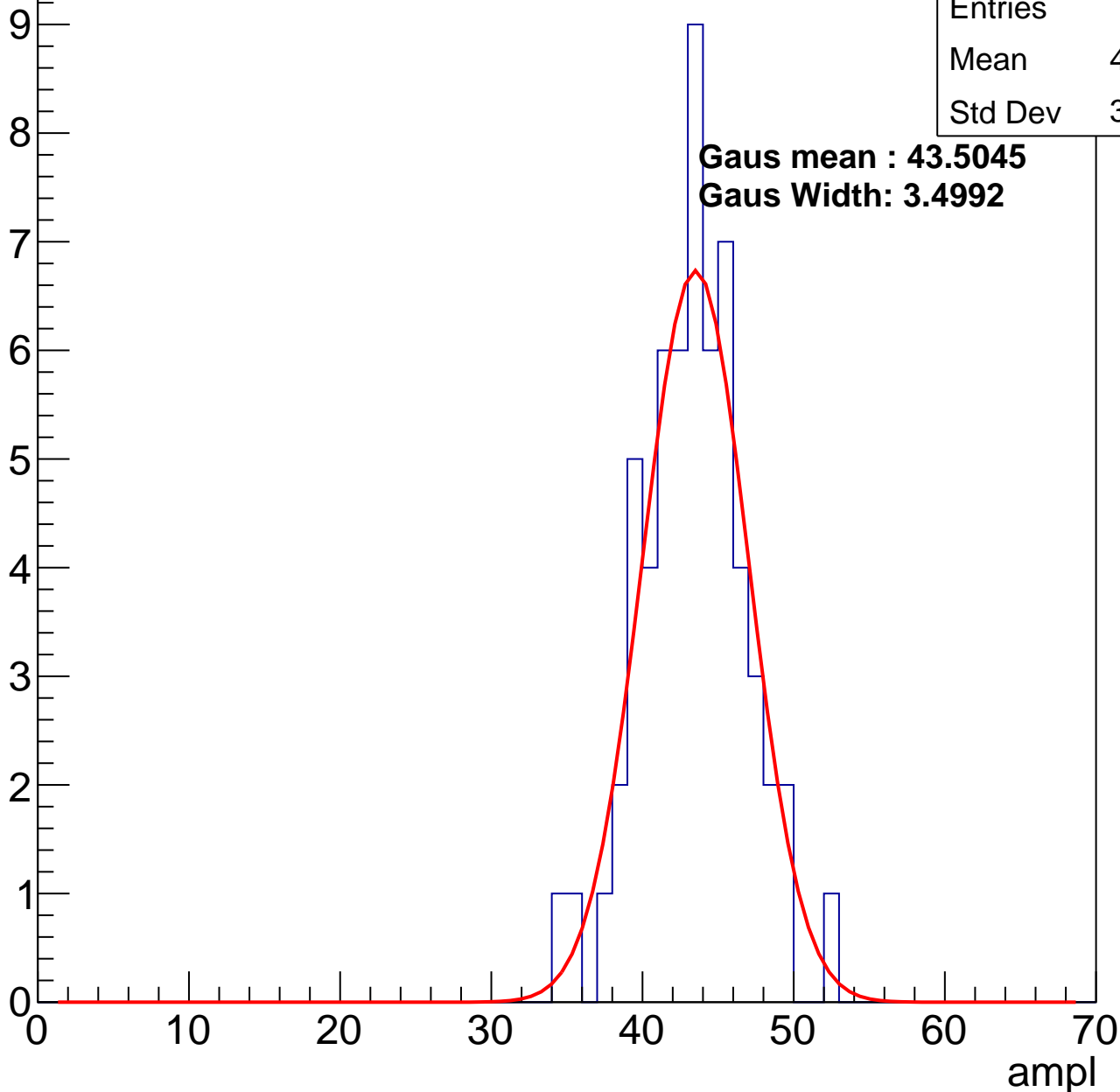
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.87
Std Dev	3.408

Gaus mean : 43.5045

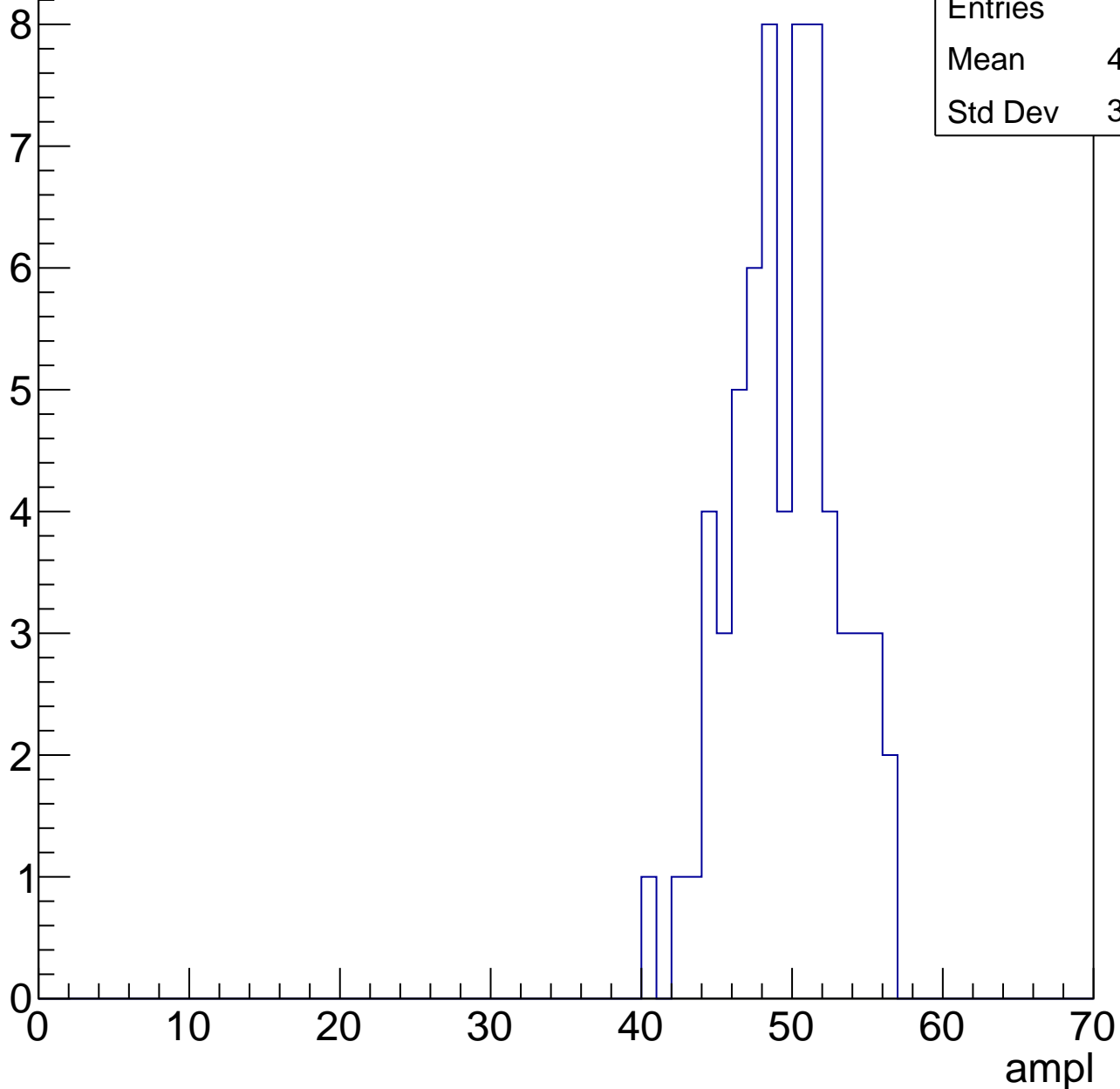
Gaus Width: 3.4992



B1L102S, U13-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



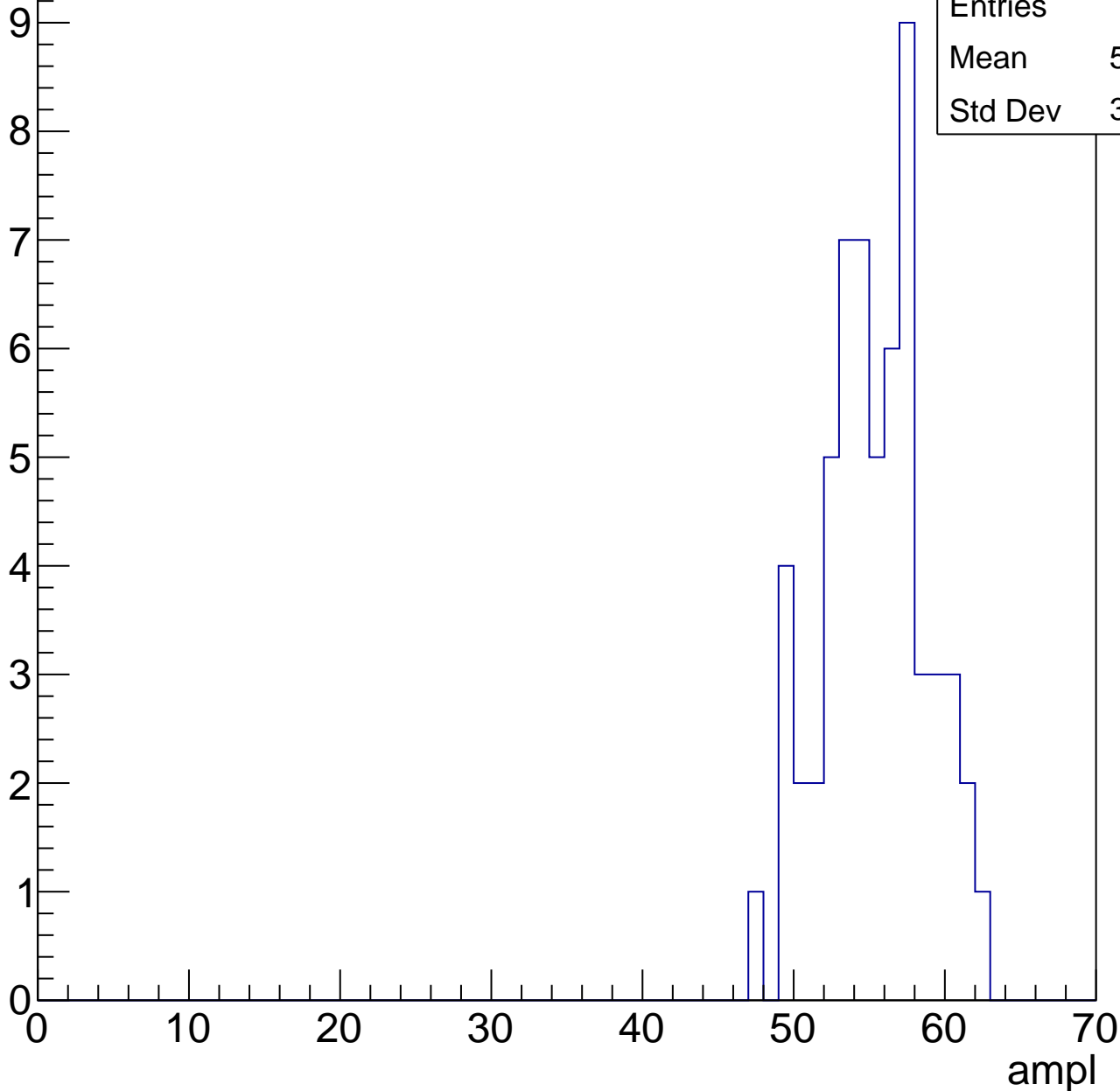
Entries	64
Mean	49.09
Std Dev	3.525

B1L102S, U13-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.88
Std Dev	3.372

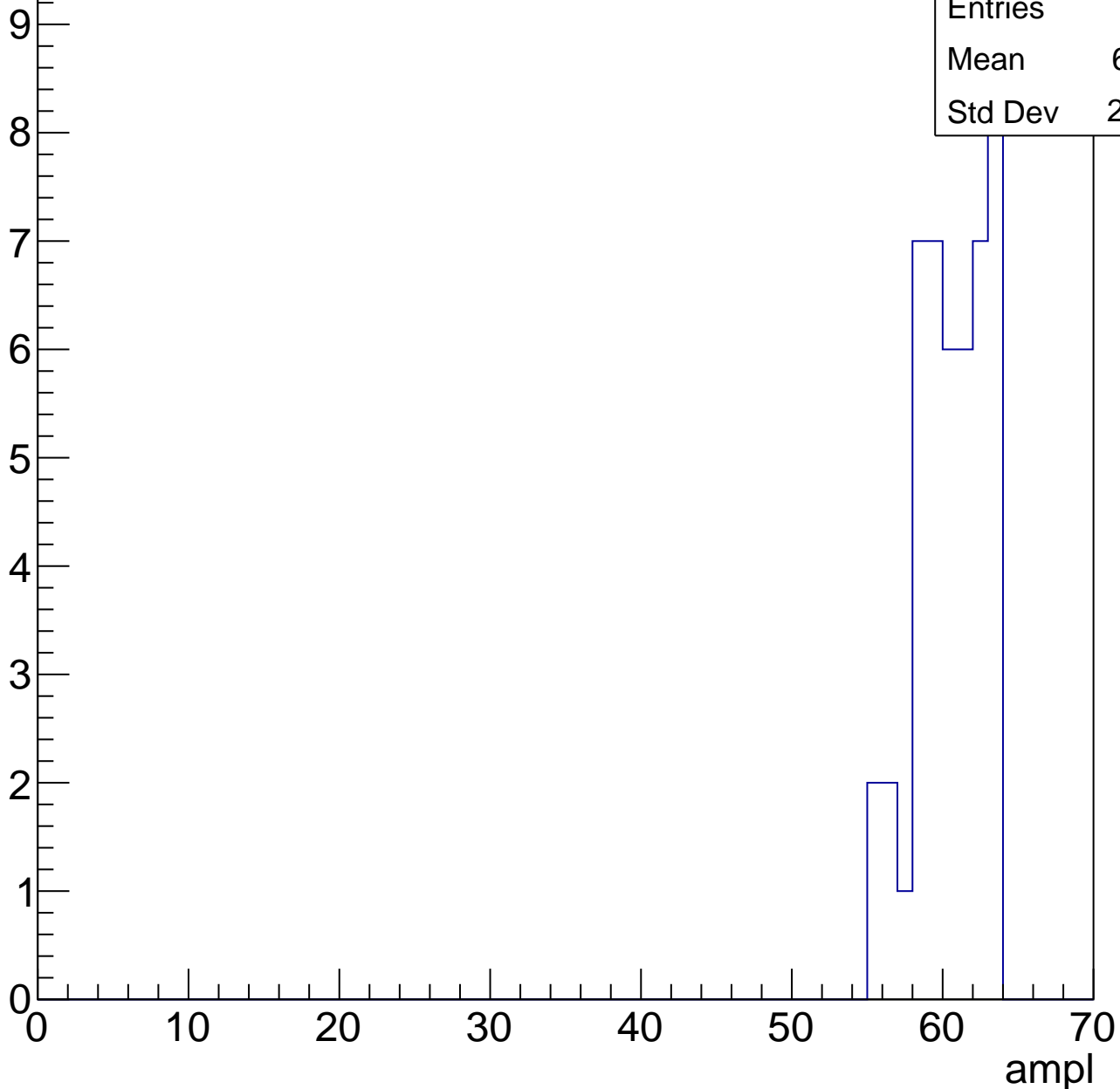


B1L102S, U13-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	60.11
Std Dev	2.262



B1L102S, U13-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

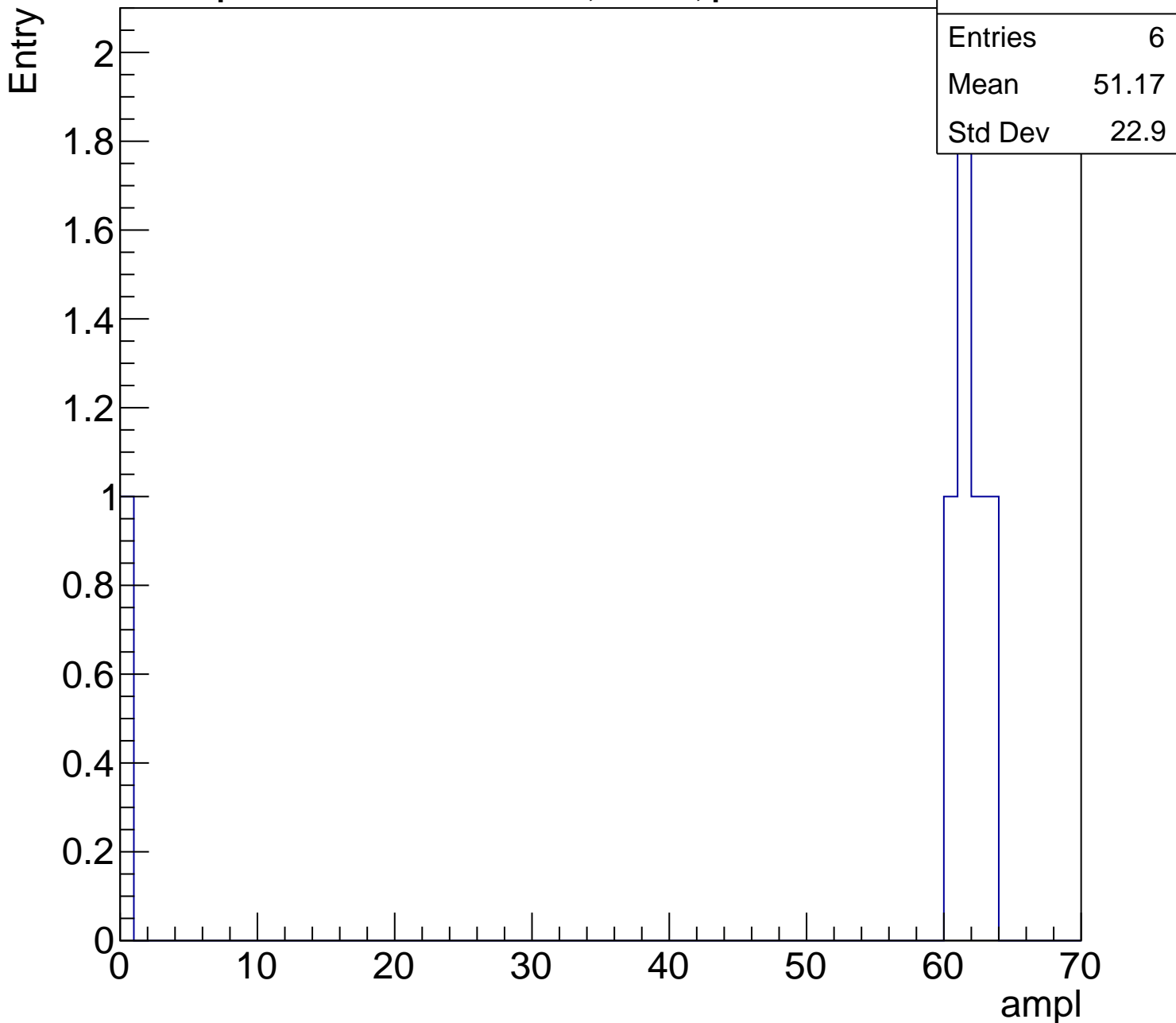
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.9

0 10 20 30 40 50 60 70

ampl



B1L102S, U13-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch42, adc0

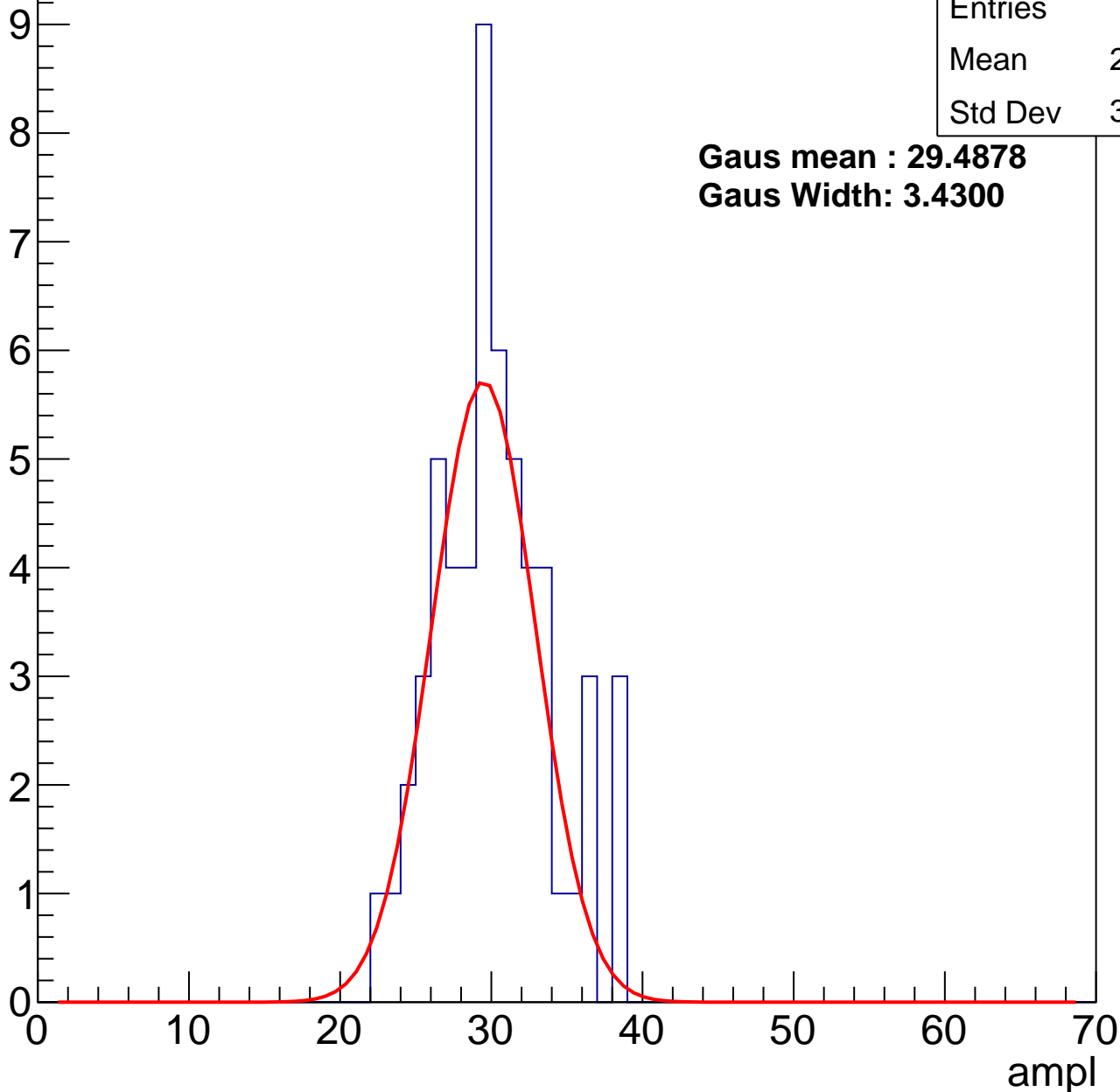
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	29.73
Std Dev	3.768

Gaus mean : 29.4878

Gaus Width: 3.4300



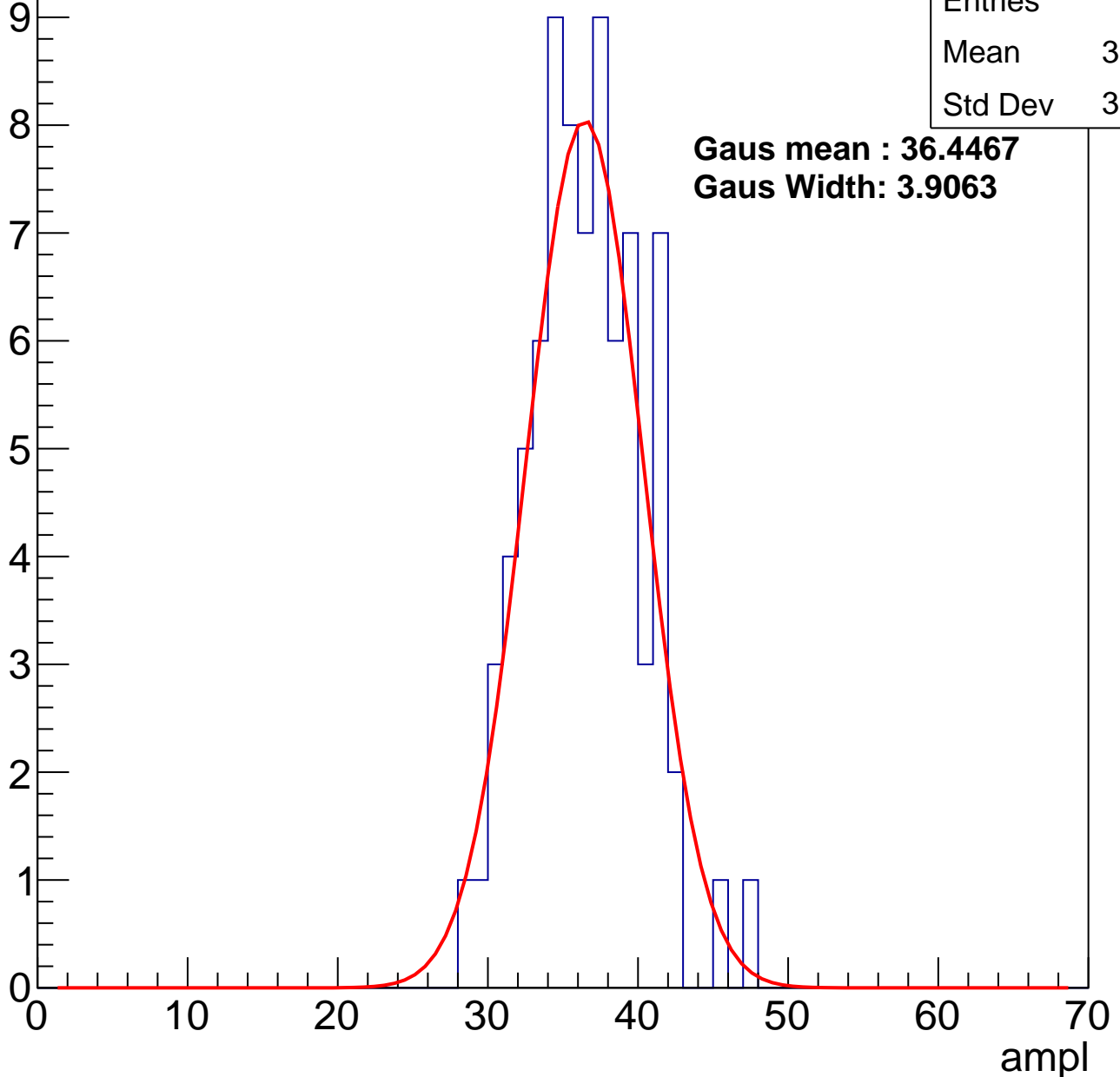
B1L102S, U13-ch42, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.05
Std Dev	3.694

Gaus mean : 36.4467
Gaus Width: 3.9063



B1L102S, U13-ch42, adc2

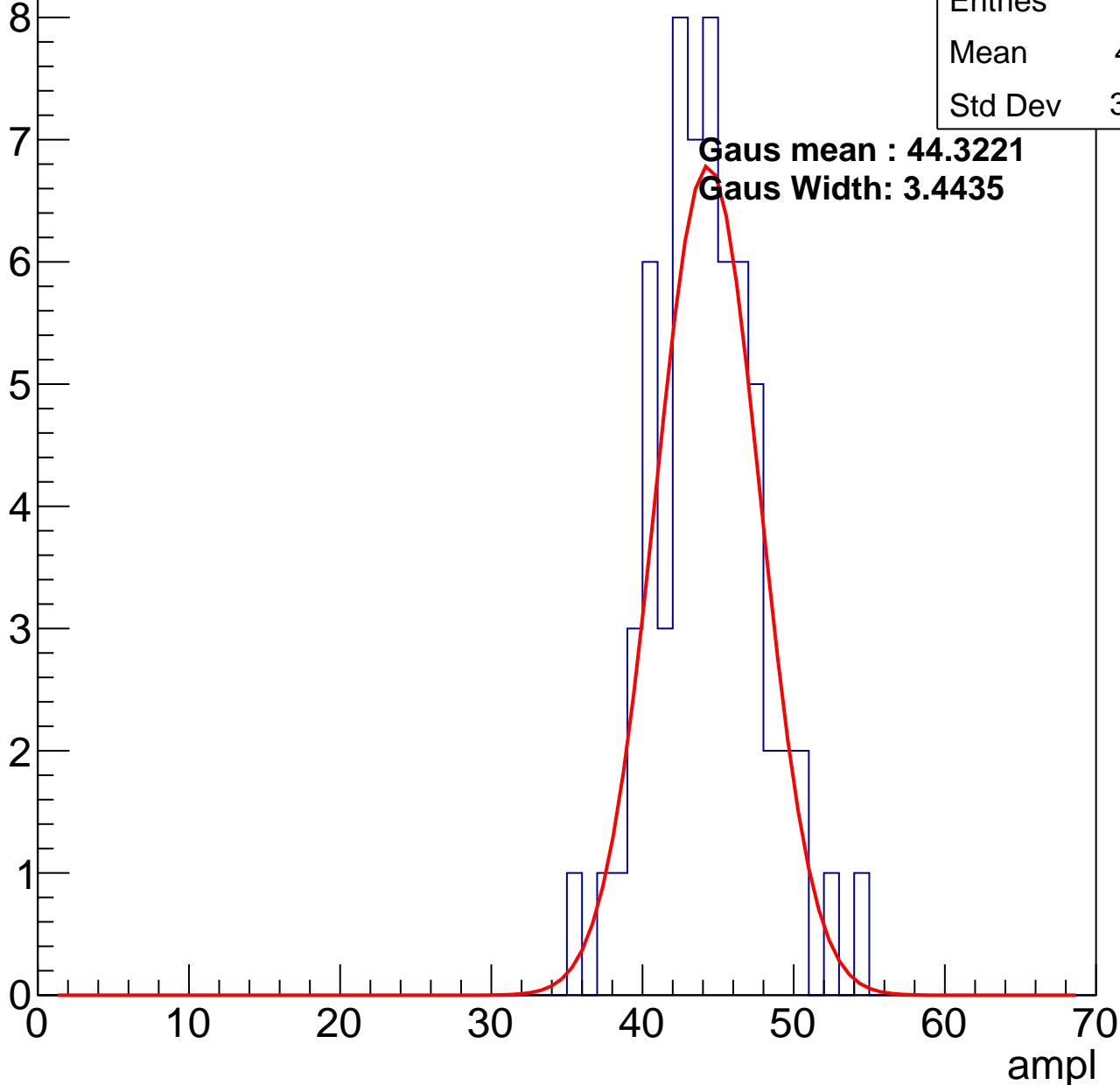
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.81
Std Dev	3.549

Gaus mean : 44.3221

Gaus Width: 3.4435

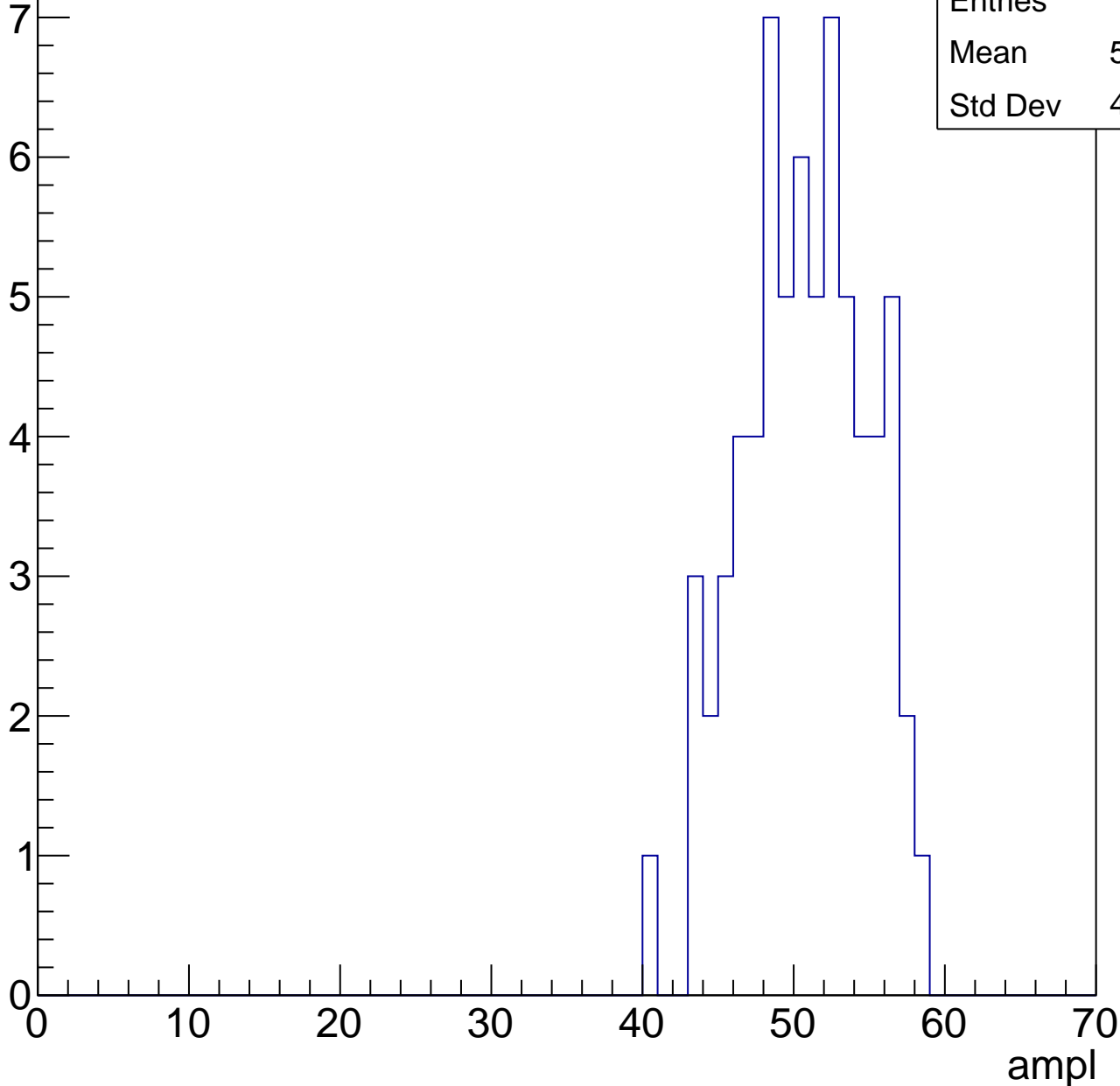


B1L102S, U13-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	50.25
Std Dev	4.034

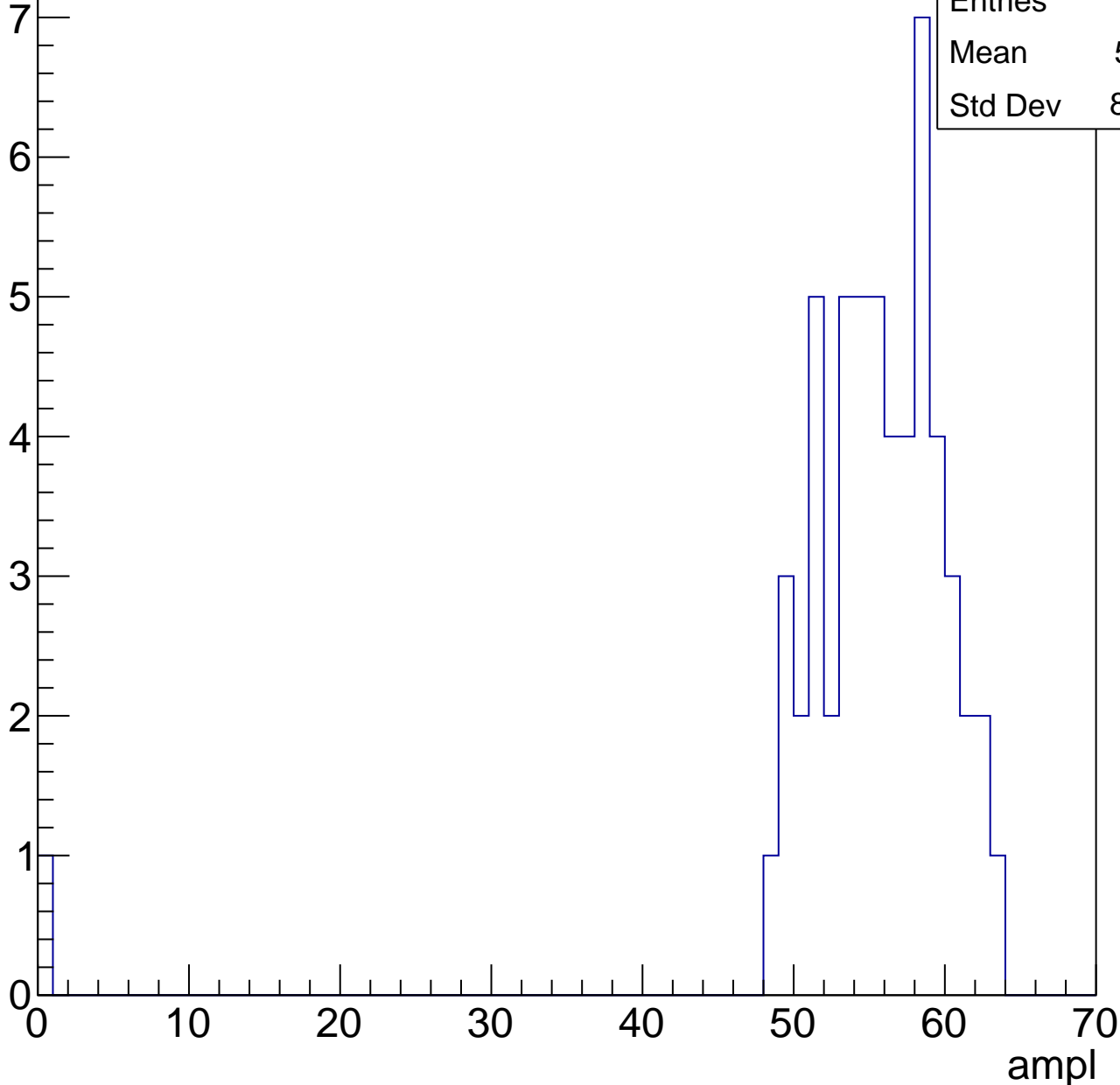


B1L102S, U13-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.41
Std Dev	8.228



B1L102S, U13-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

60.24

Std Dev

2.149

0

10

20

30

40

50

60

70

ampl

0

10

20

30

40

50

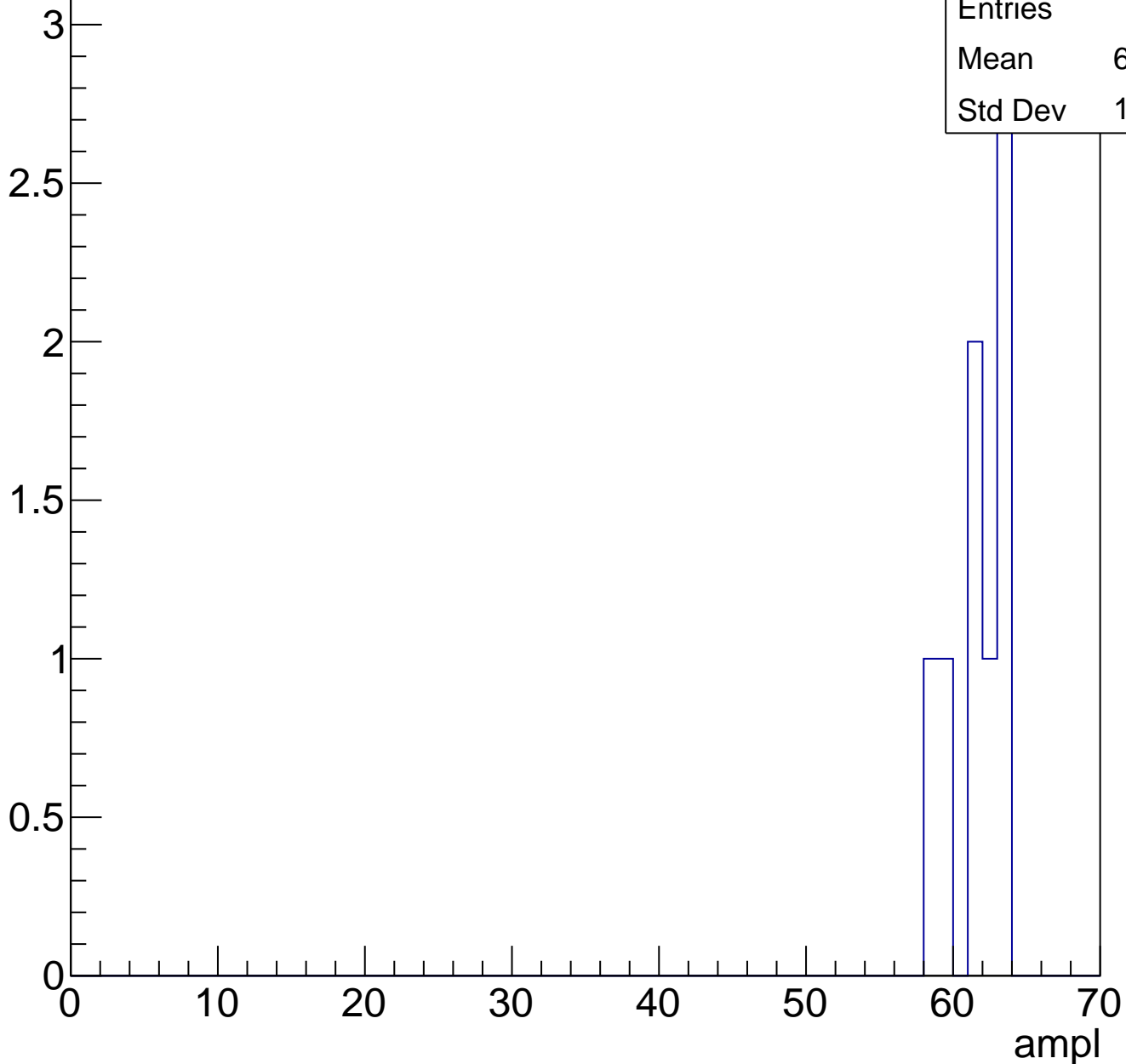
60

70

B1L102S, U13-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

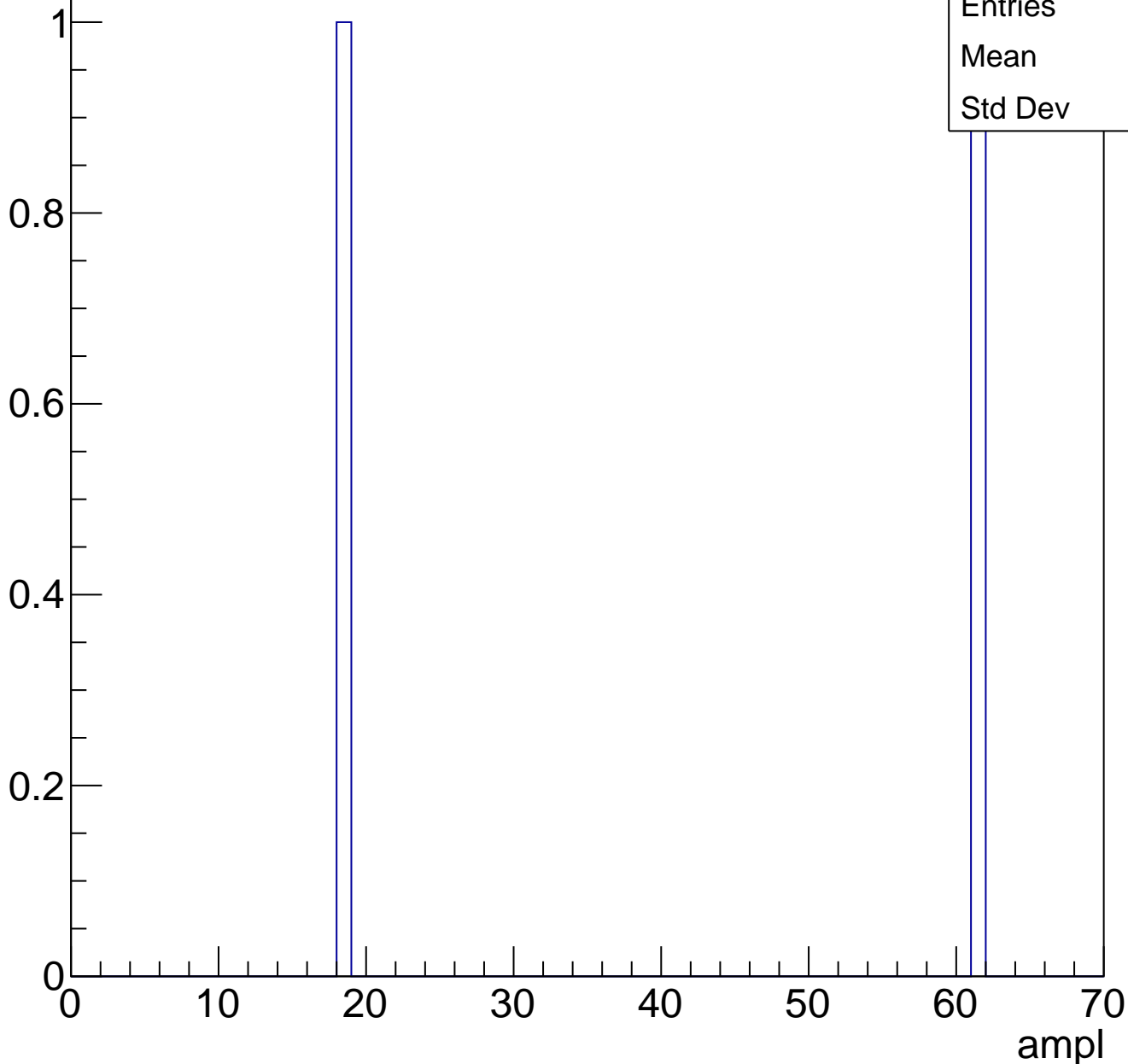
Entry



B1L102S, U13-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch43, adc0

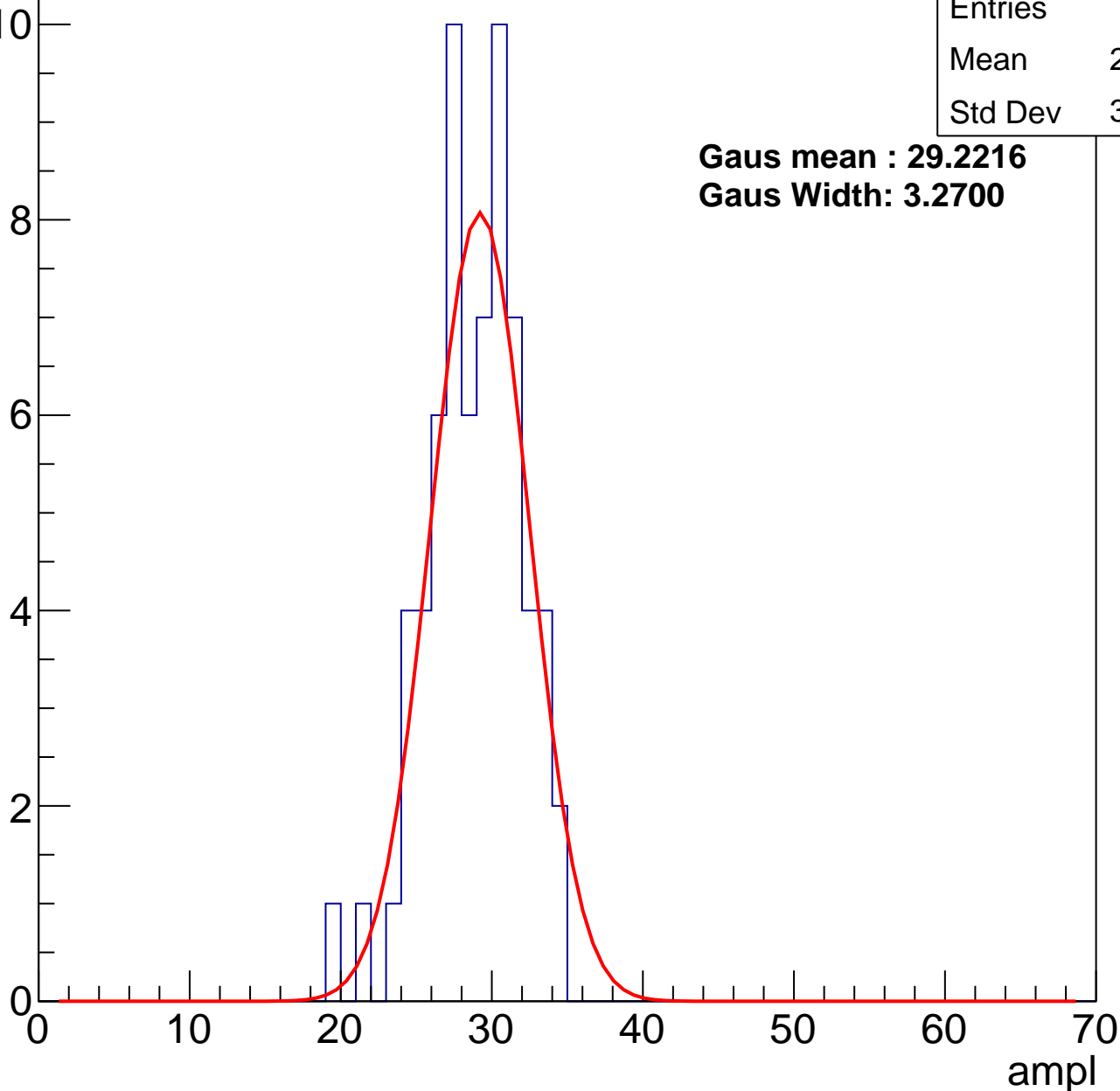
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.37
Std Dev	3.056

Gaus mean : 29.2216

Gaus Width: 3.2700



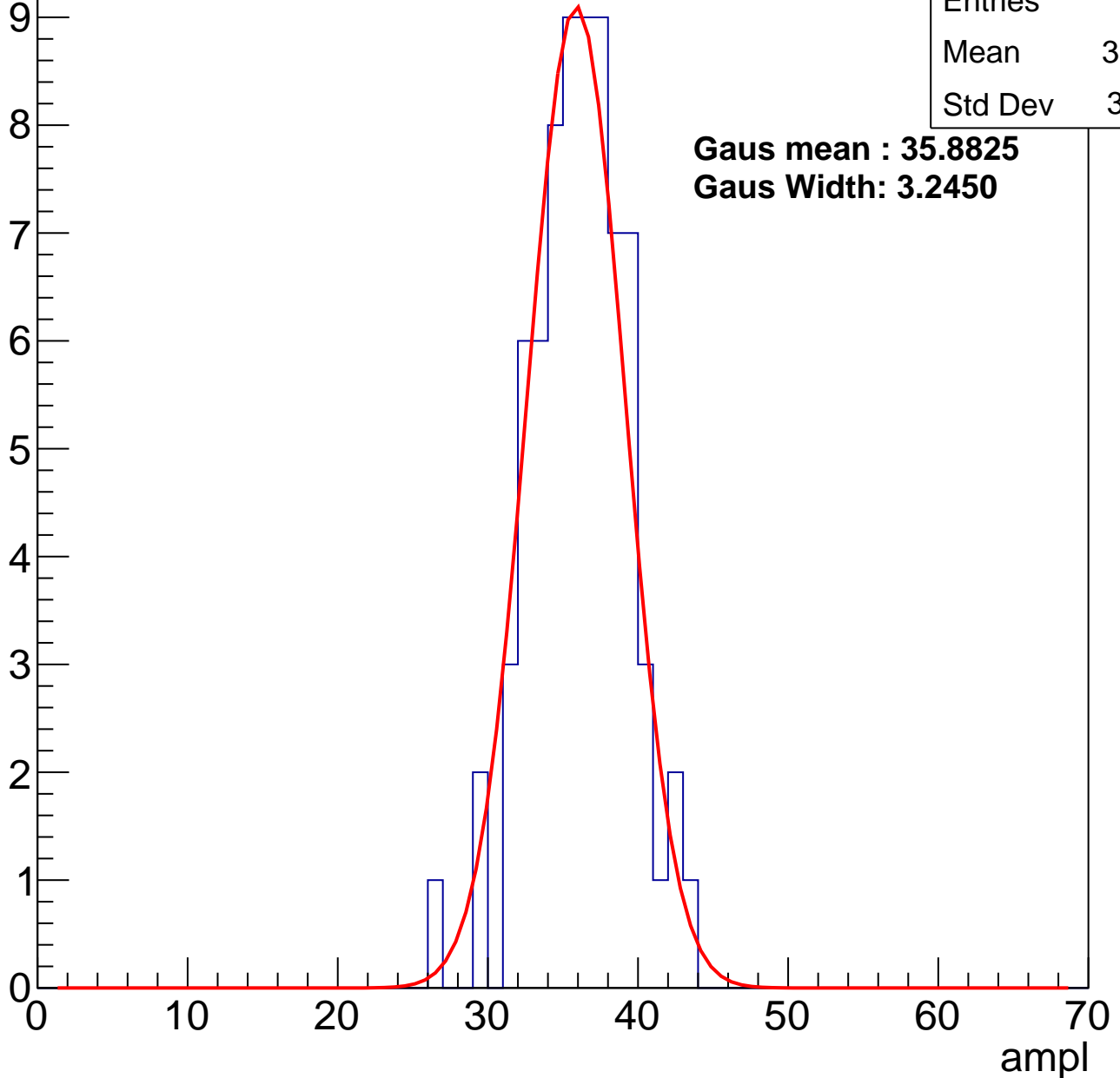
B1L102S, U13-ch43, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.65
Std Dev	3.181

Gaus mean : 35.8825
Gaus Width: 3.2450



B1L102S, U13-ch43, adc2

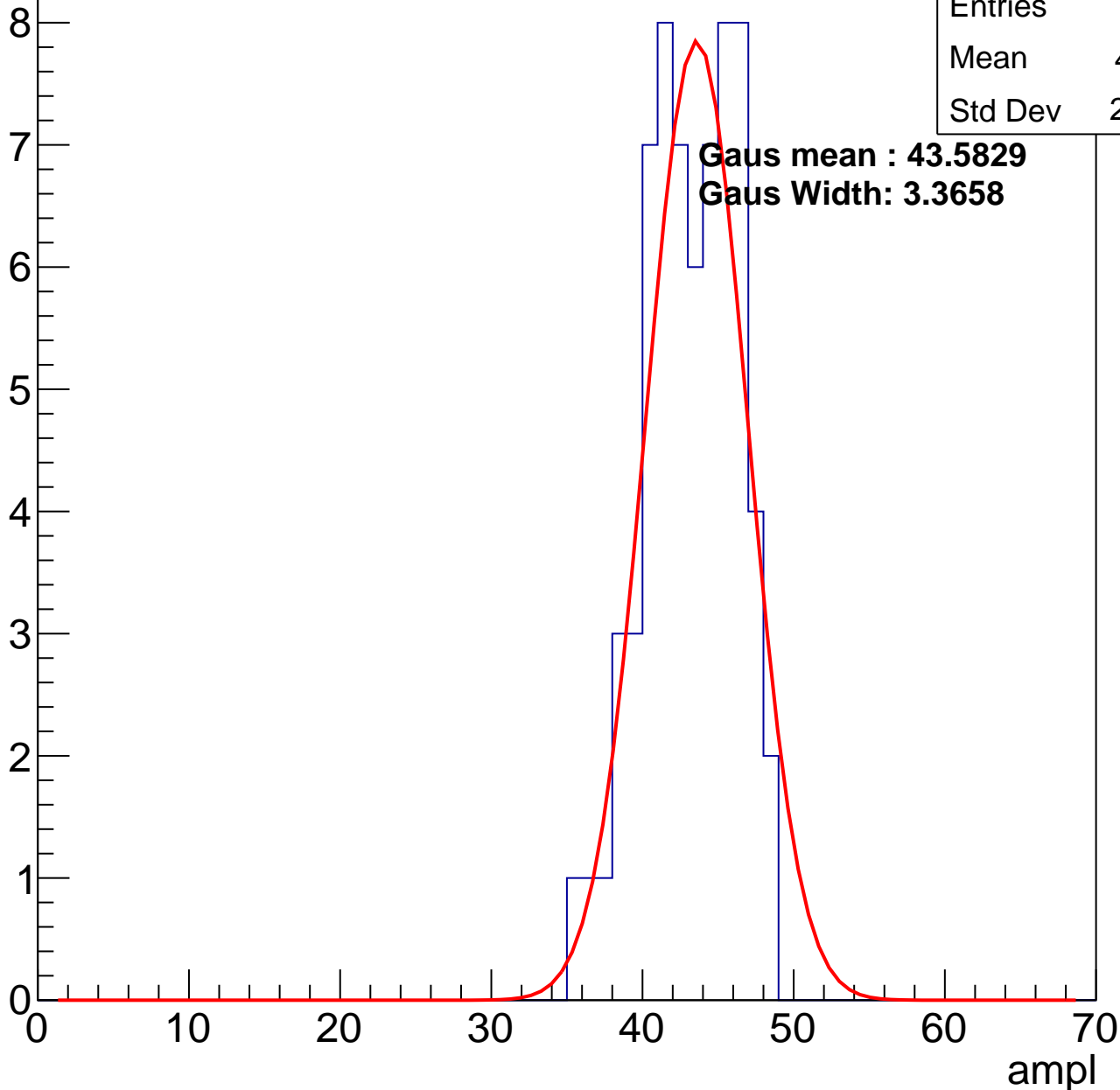
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.71
Std Dev	2.999

Gaus mean : 43.5829

Gaus Width: 3.3658

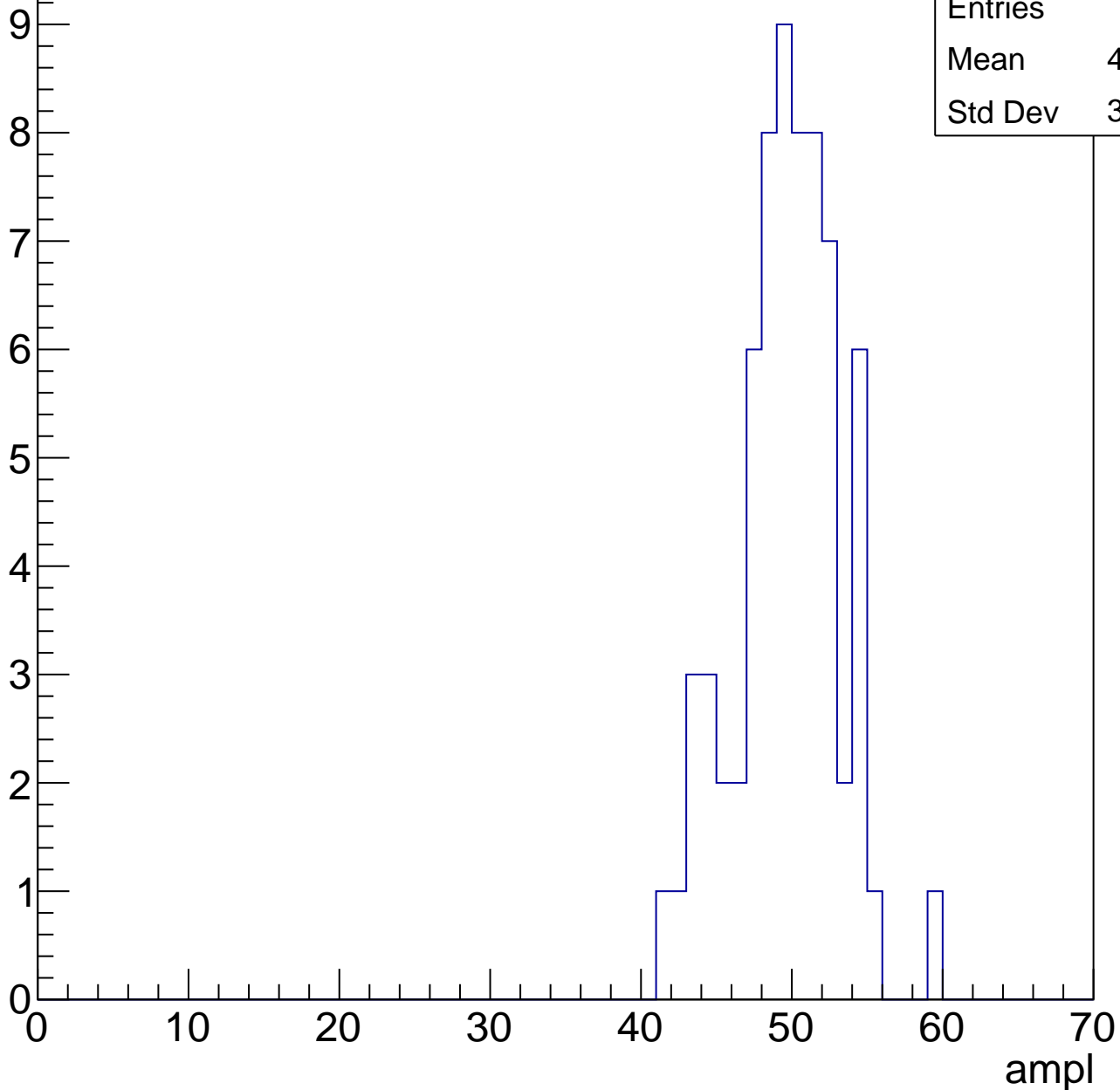


B1L102S, U13-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.25
Std Dev	3.419

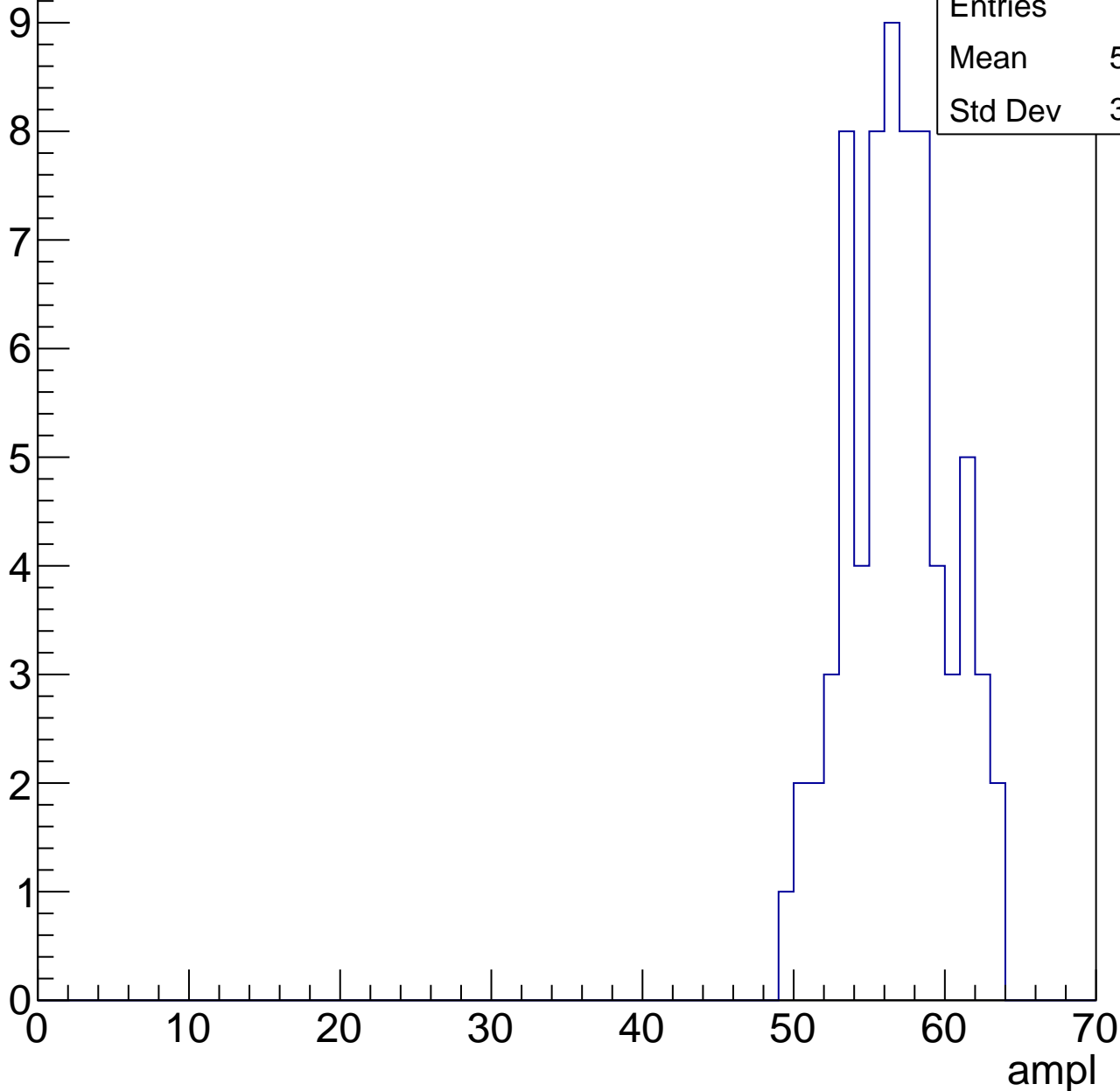


B1L102S, U13-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	56.34
Std Dev	3.299



B1L102S, U13-ch43, adc5

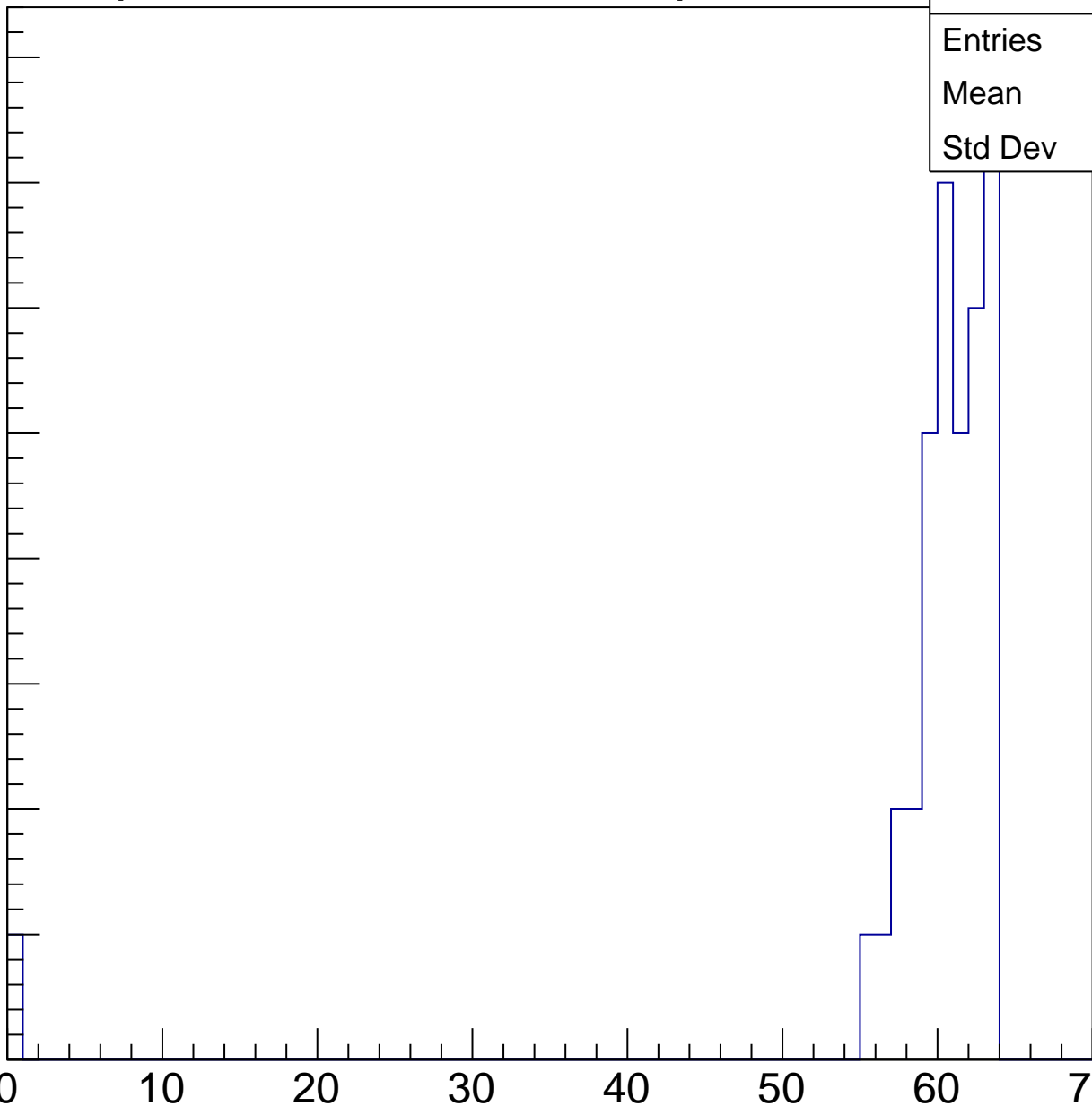
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	58.87
Std Dev	9.9

ampl



B1L102S, U13-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch44, adc0

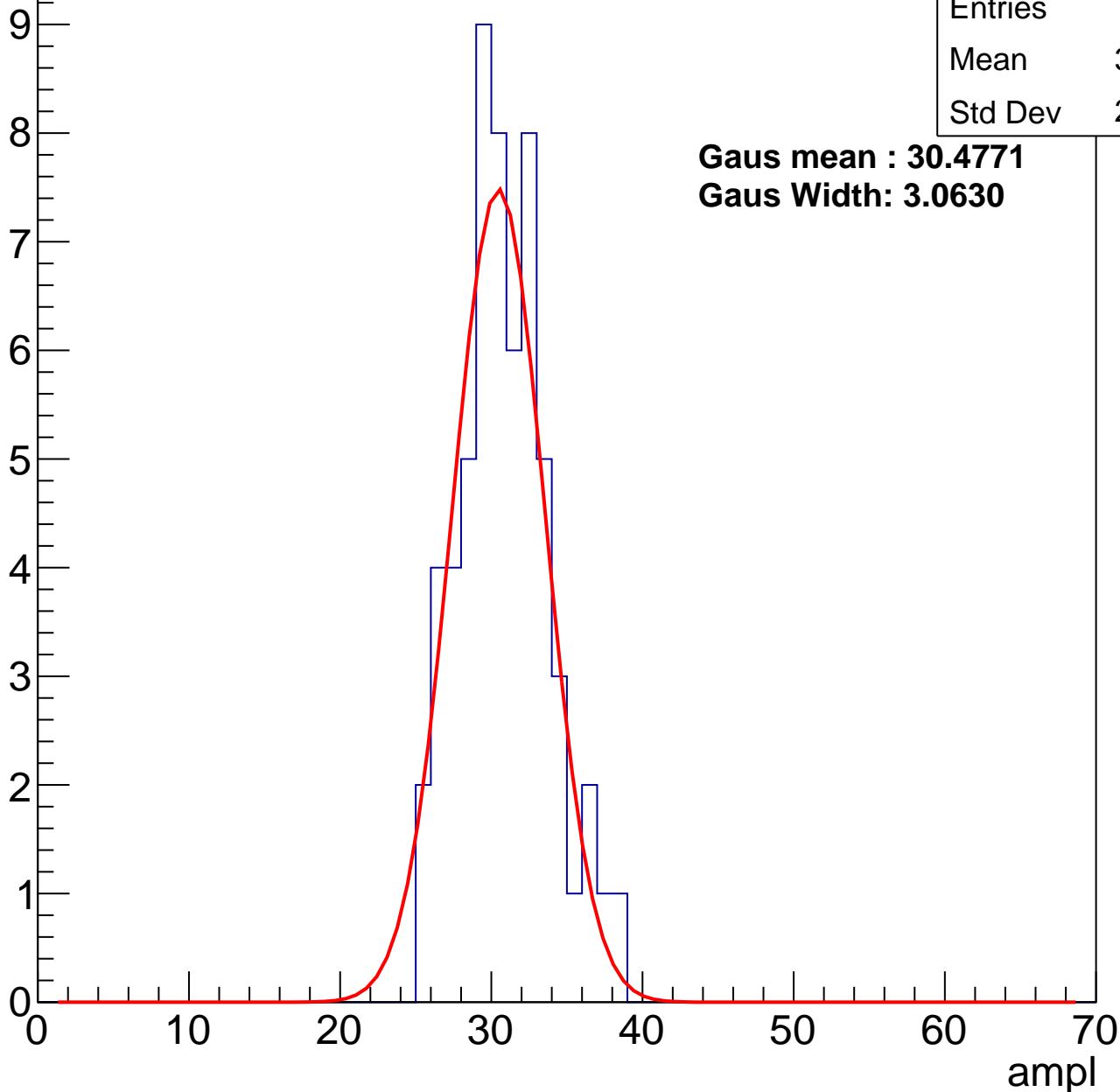
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	30.41
Std Dev	2.941

Gaus mean : 30.4771

Gaus Width: 3.0630



B1L102S, U13-ch44, adc1

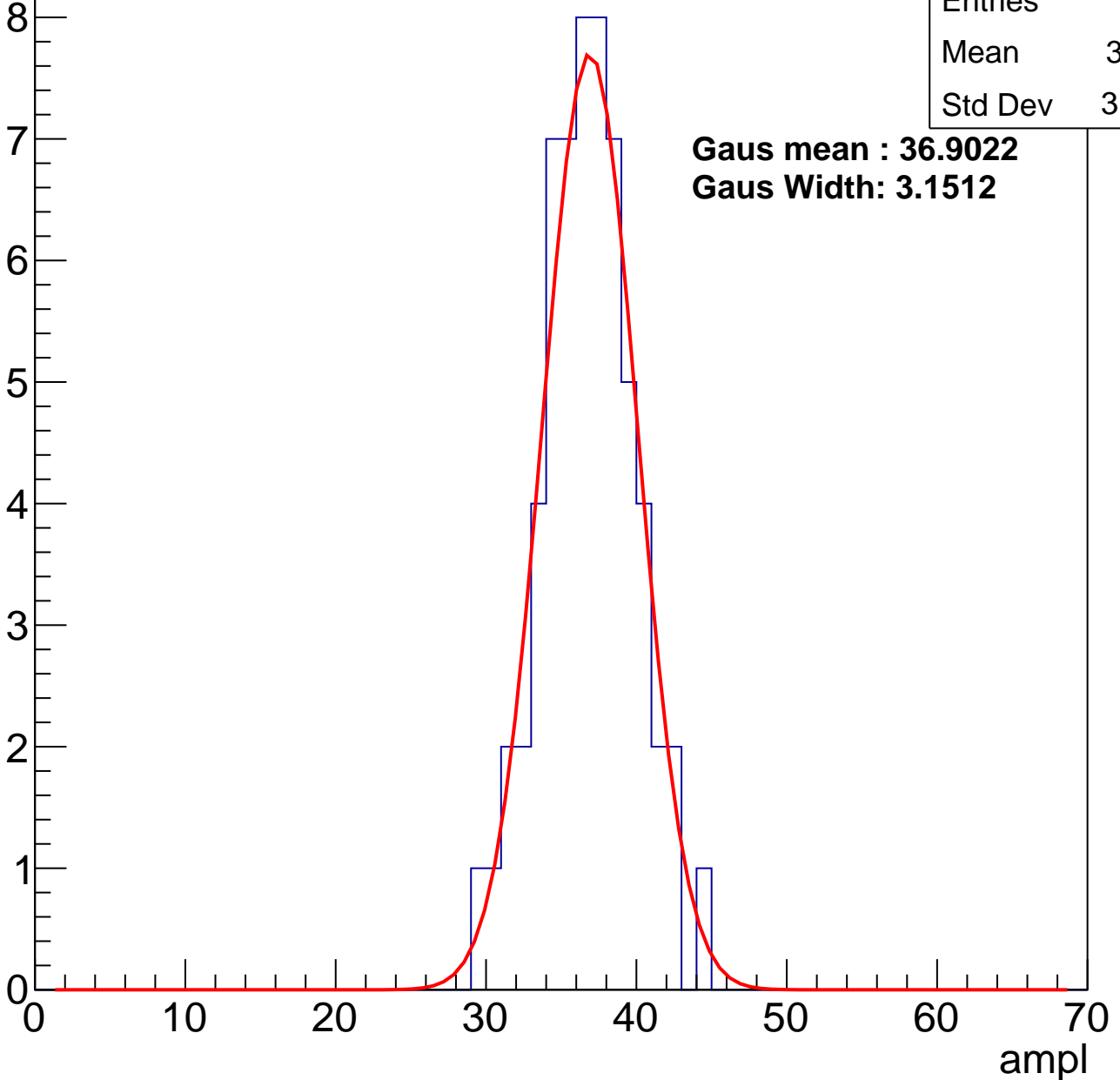
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.31
Std Dev	3.038

Gaus mean : 36.9022

Gaus Width: 3.1512



B1L102S, U13-ch44, adc2

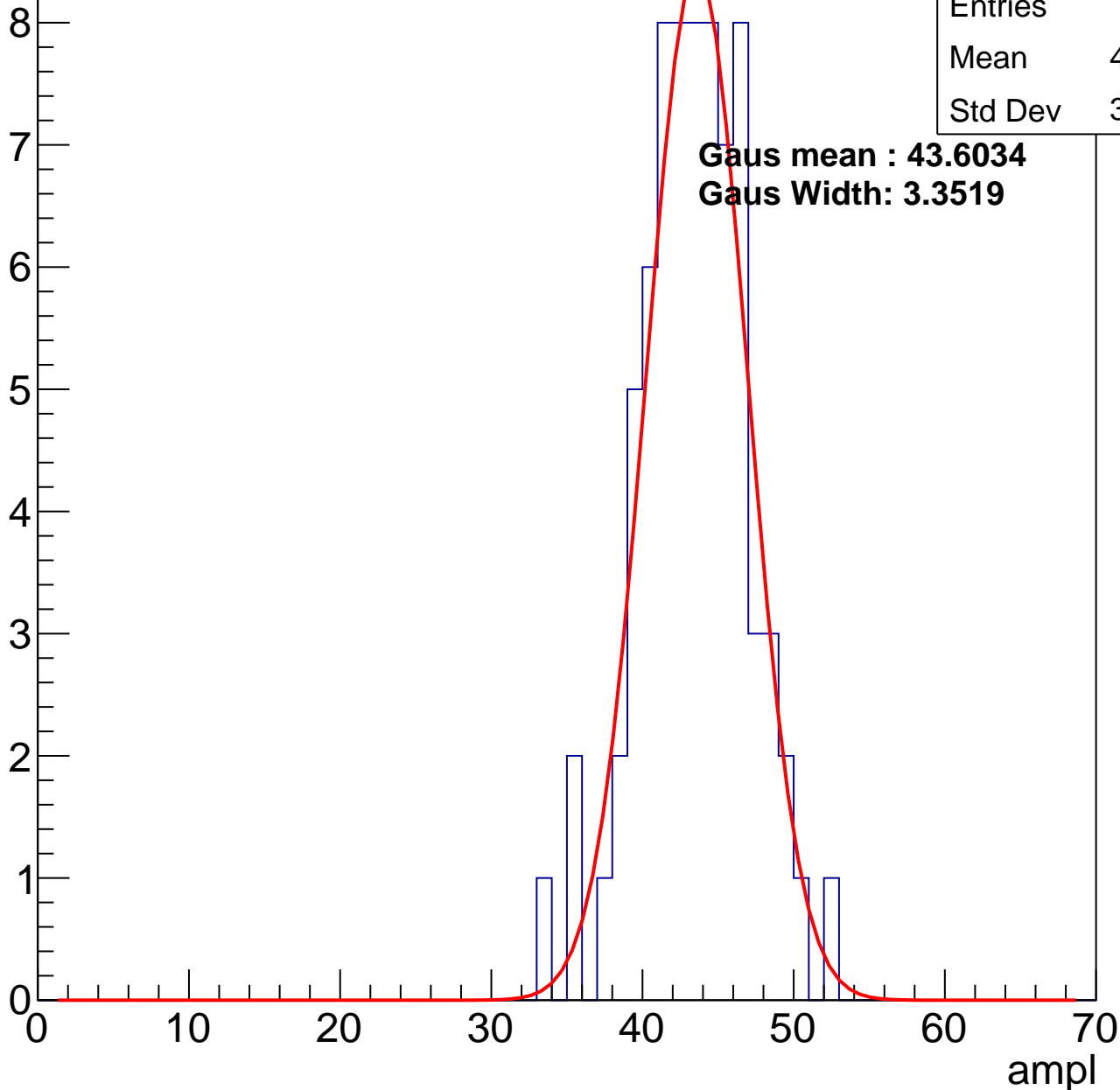
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.96
Std Dev	3.528

Gaus mean : 43.6034

Gaus Width: 3.3519

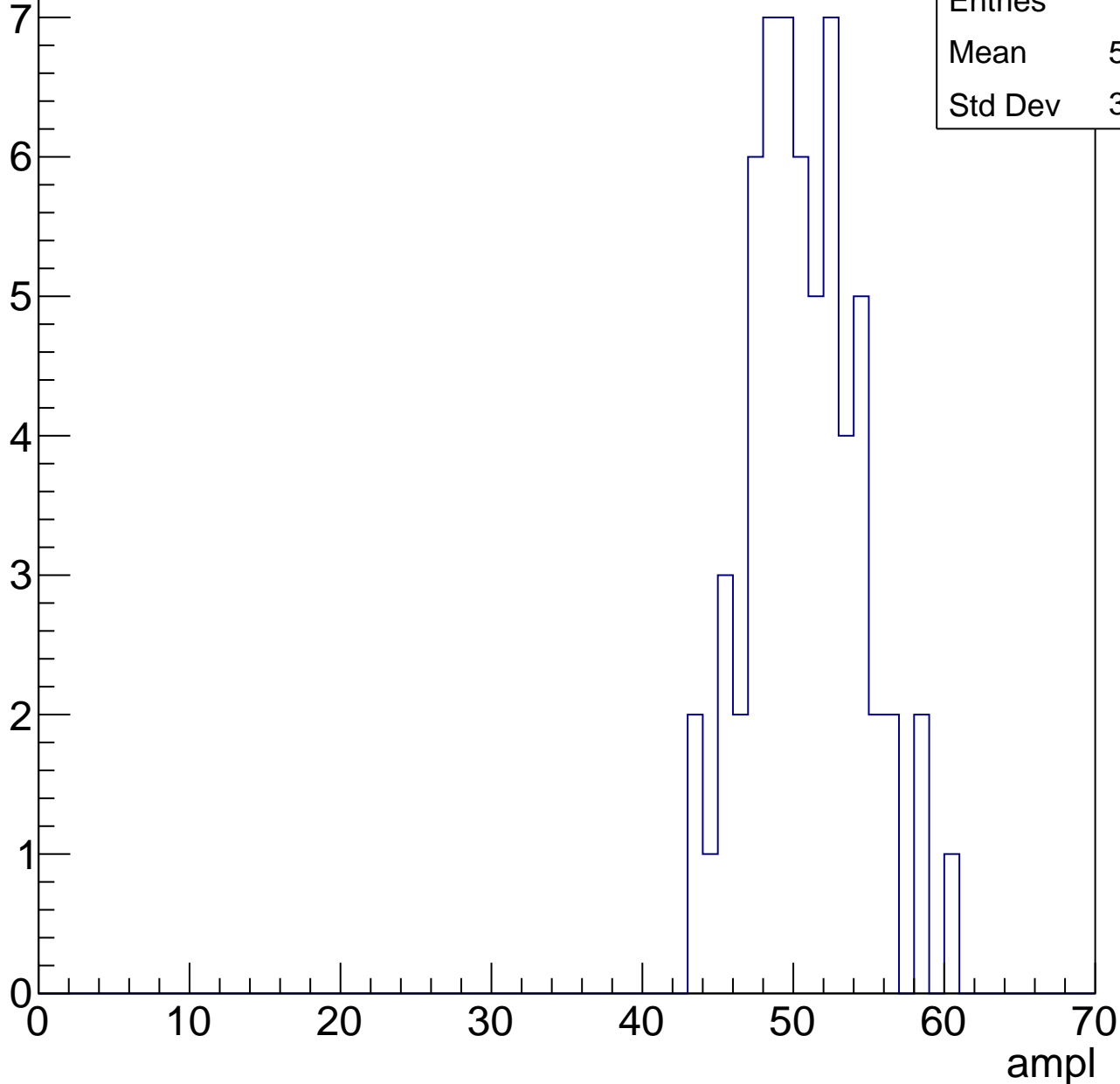


B1L102S, U13-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	50.27
Std Dev	3.646

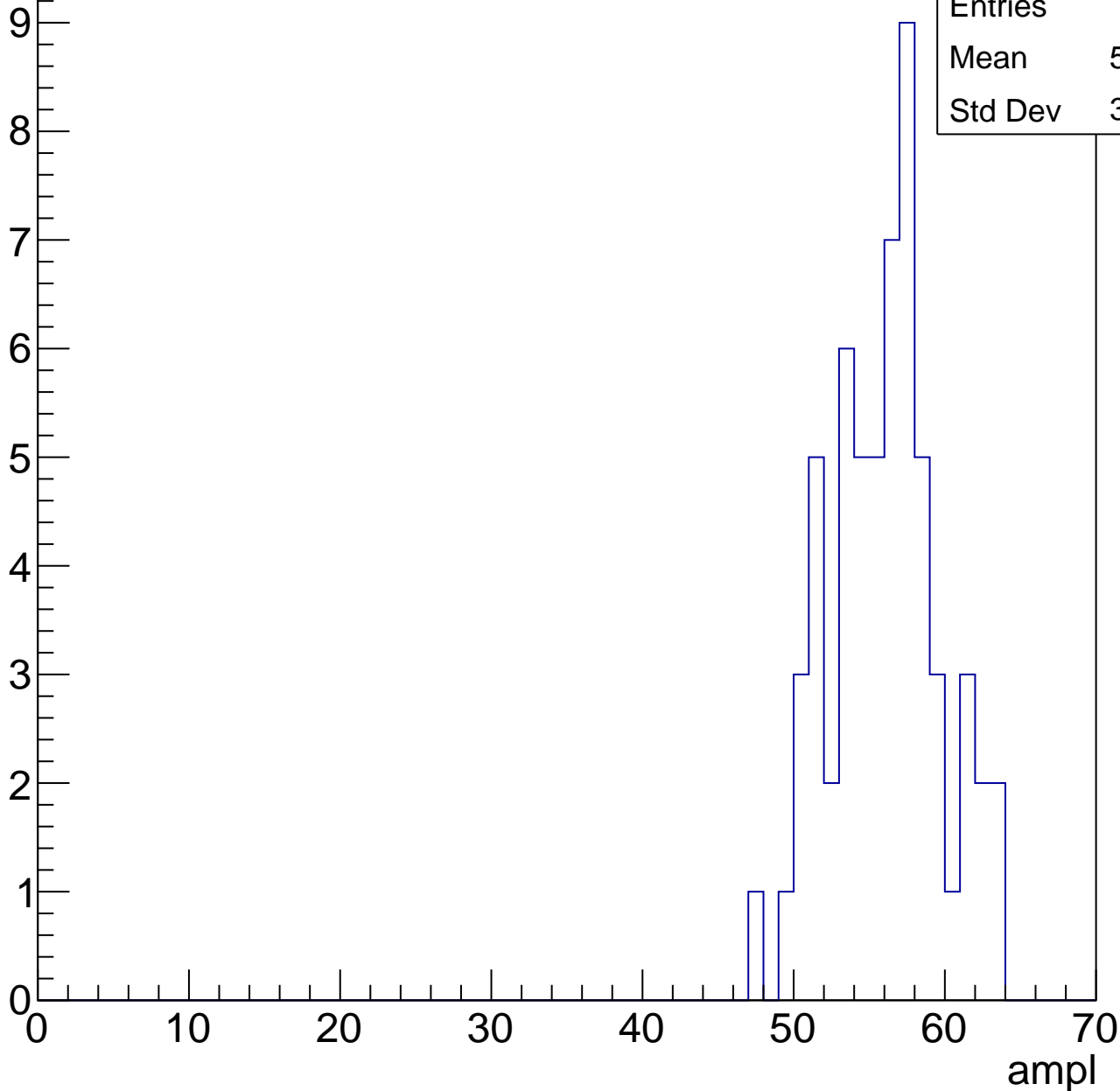


B1L102S, U13-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.55
Std Dev	3.603

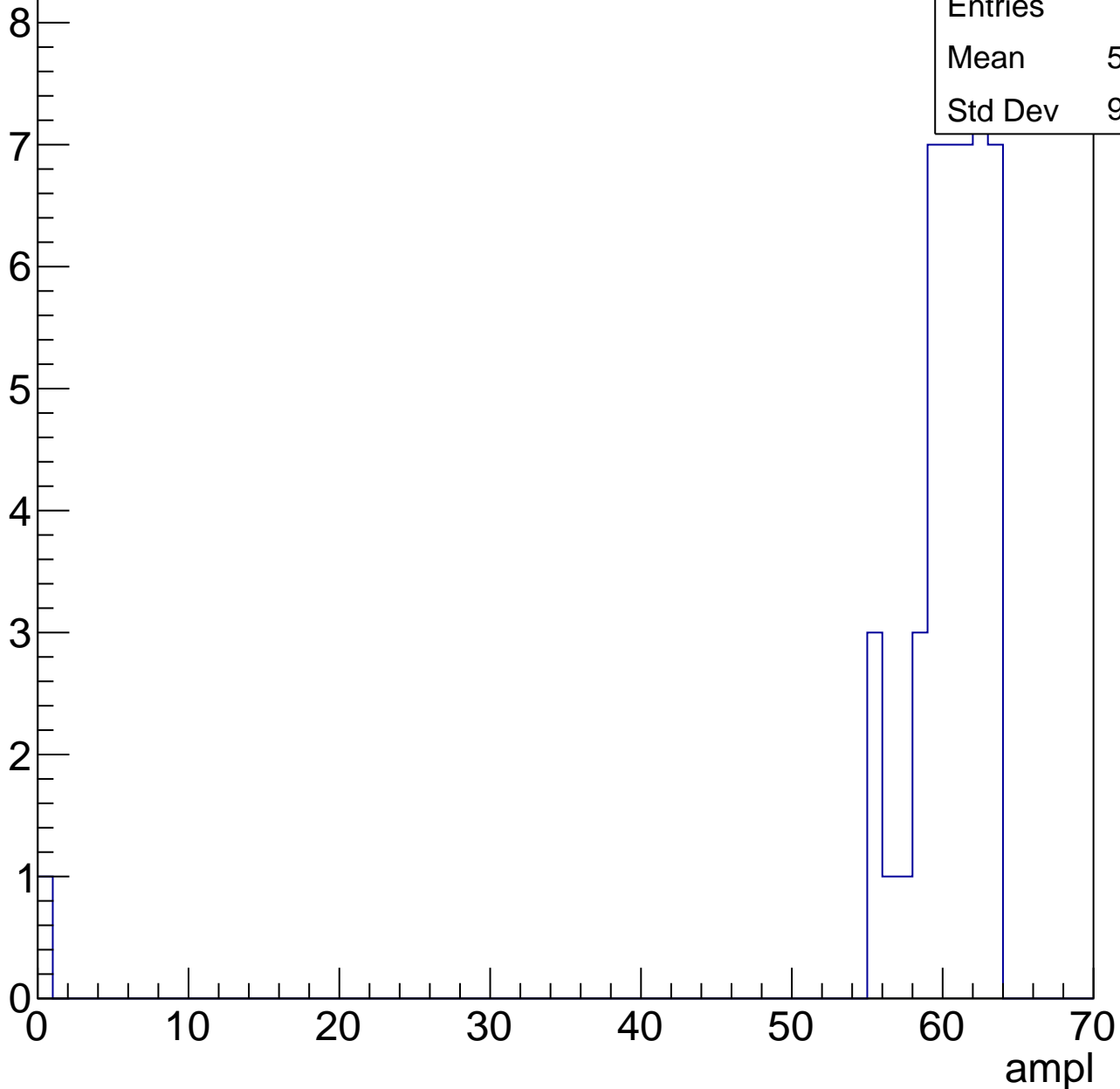


B1L102S, U13-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.87
Std Dev	9.145



B1L102S, U13-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch45, adc0

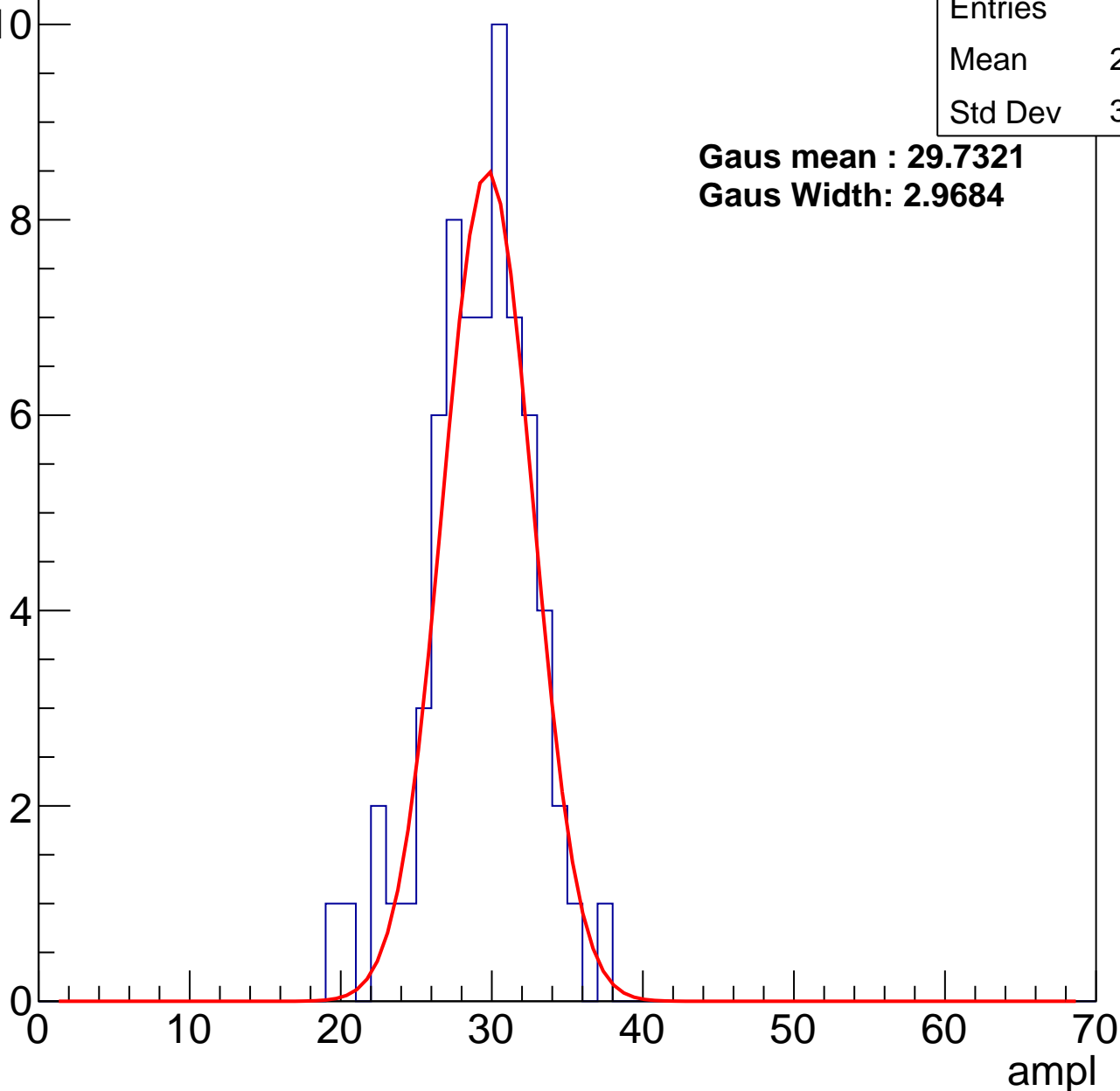
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.78
Std Dev	3.395

Gaus mean : 29.7321

Gaus Width: 2.9684



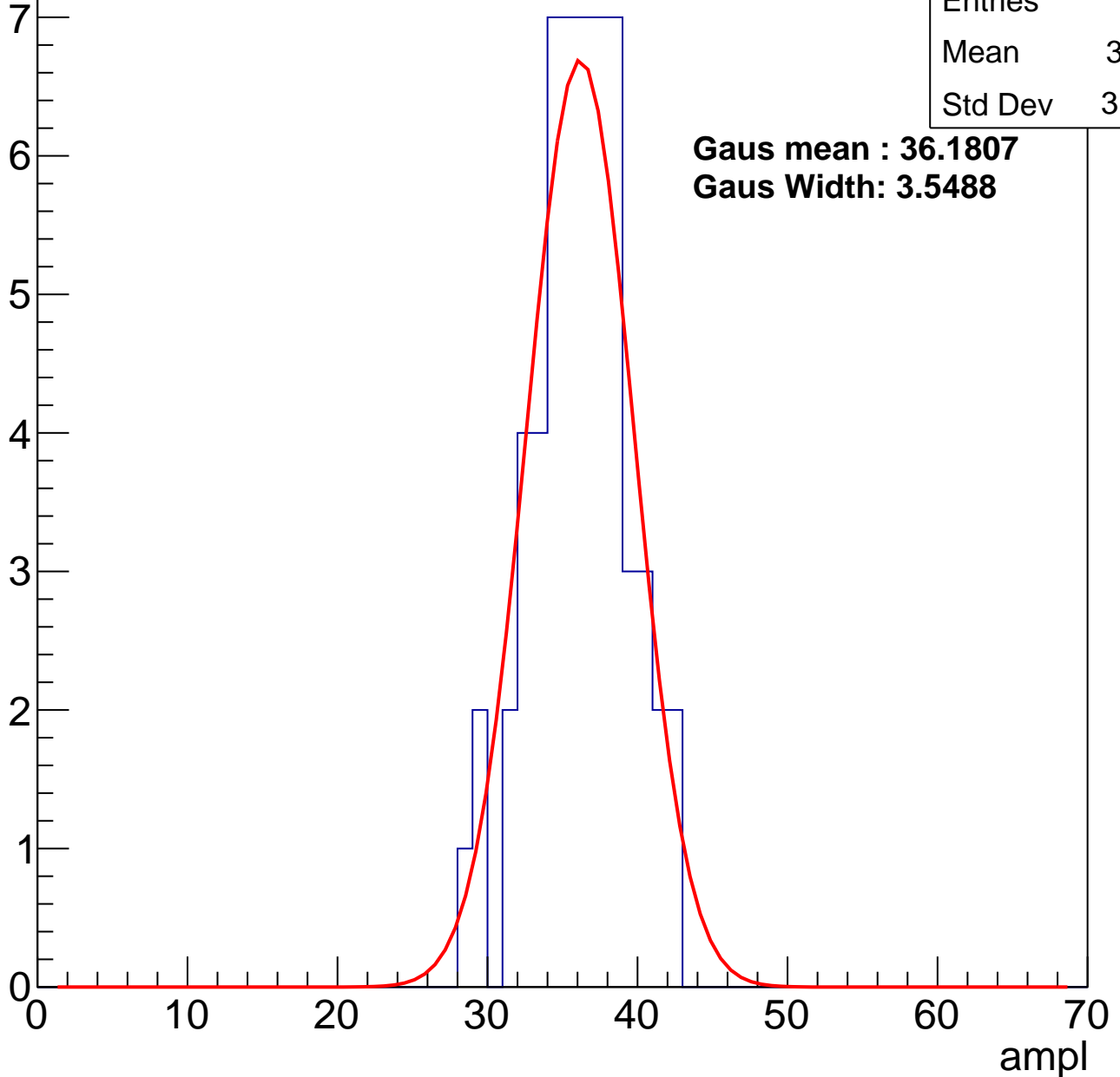
B1L102S, U13-ch45, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.71
Std Dev	3.146

Gaus mean : 36.1807
Gaus Width: 3.5488



B1L102S, U13-ch45, adc2

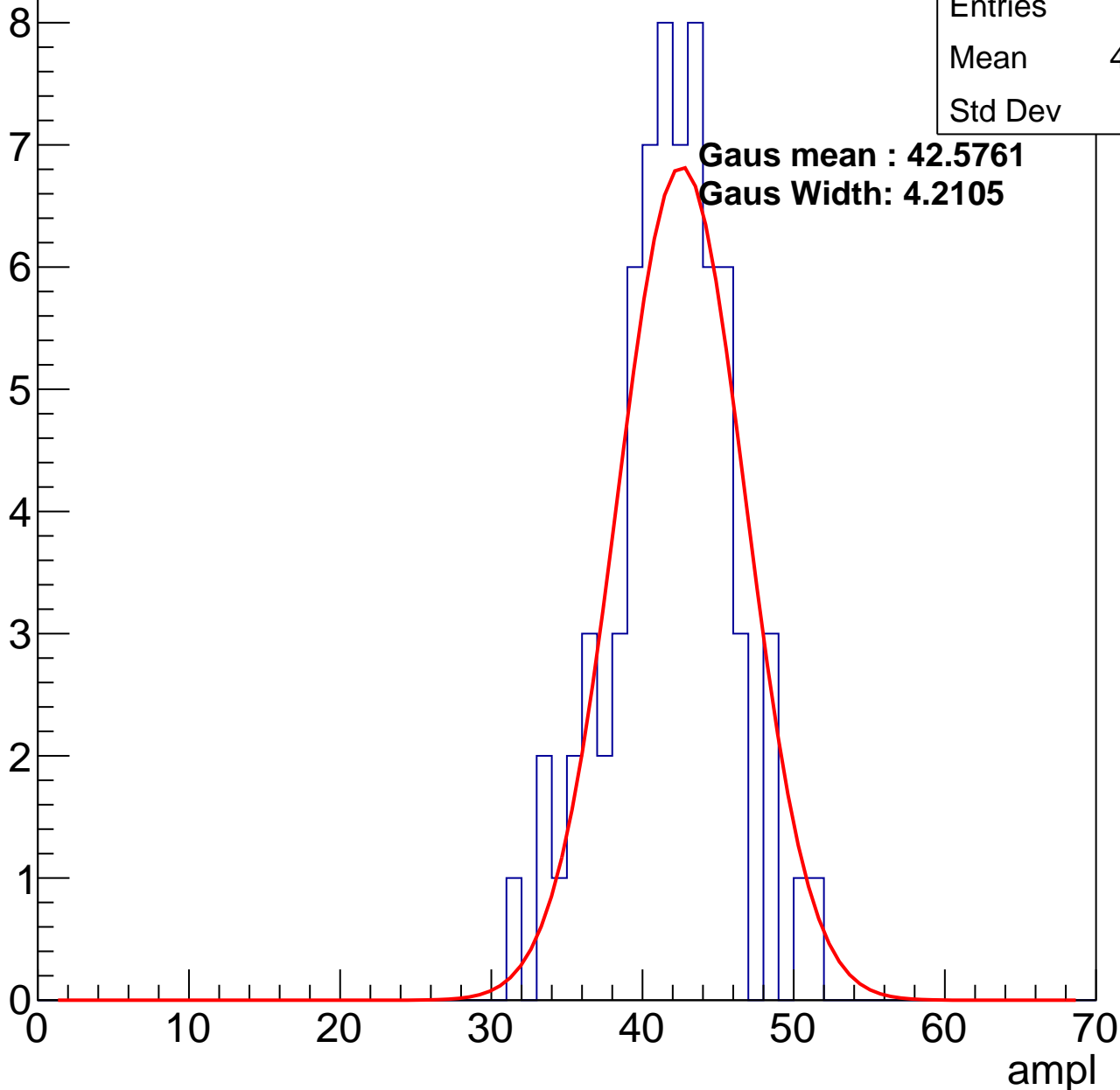
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.34
Std Dev	3.96

Gaus mean : 42.5761

Gaus Width: 4.2105



B1L102S, U13-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

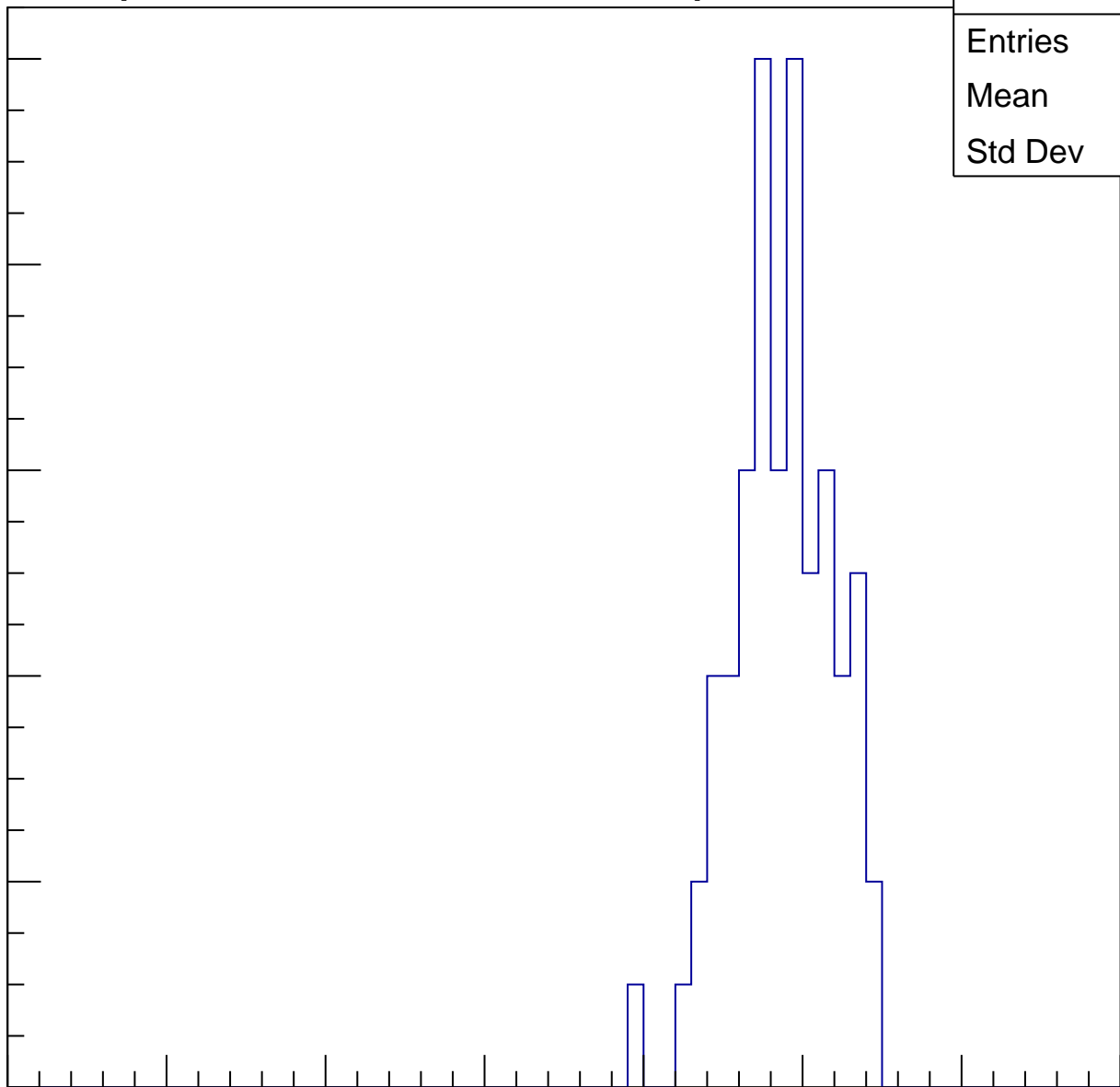
Entries	66
Mean	48.24
Std Dev	3.119

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

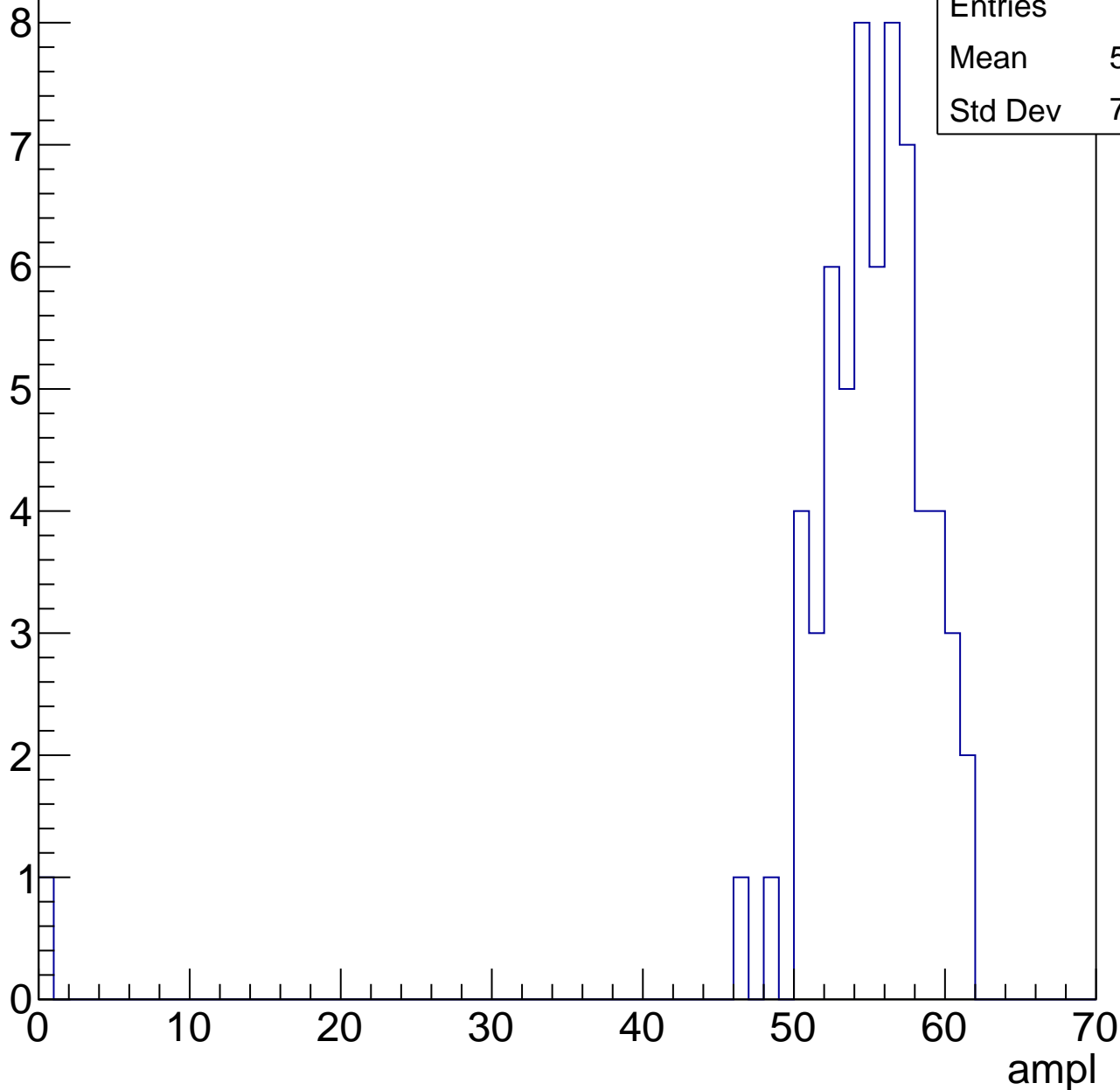


B1L102S, U13-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.02
Std Dev	7.567

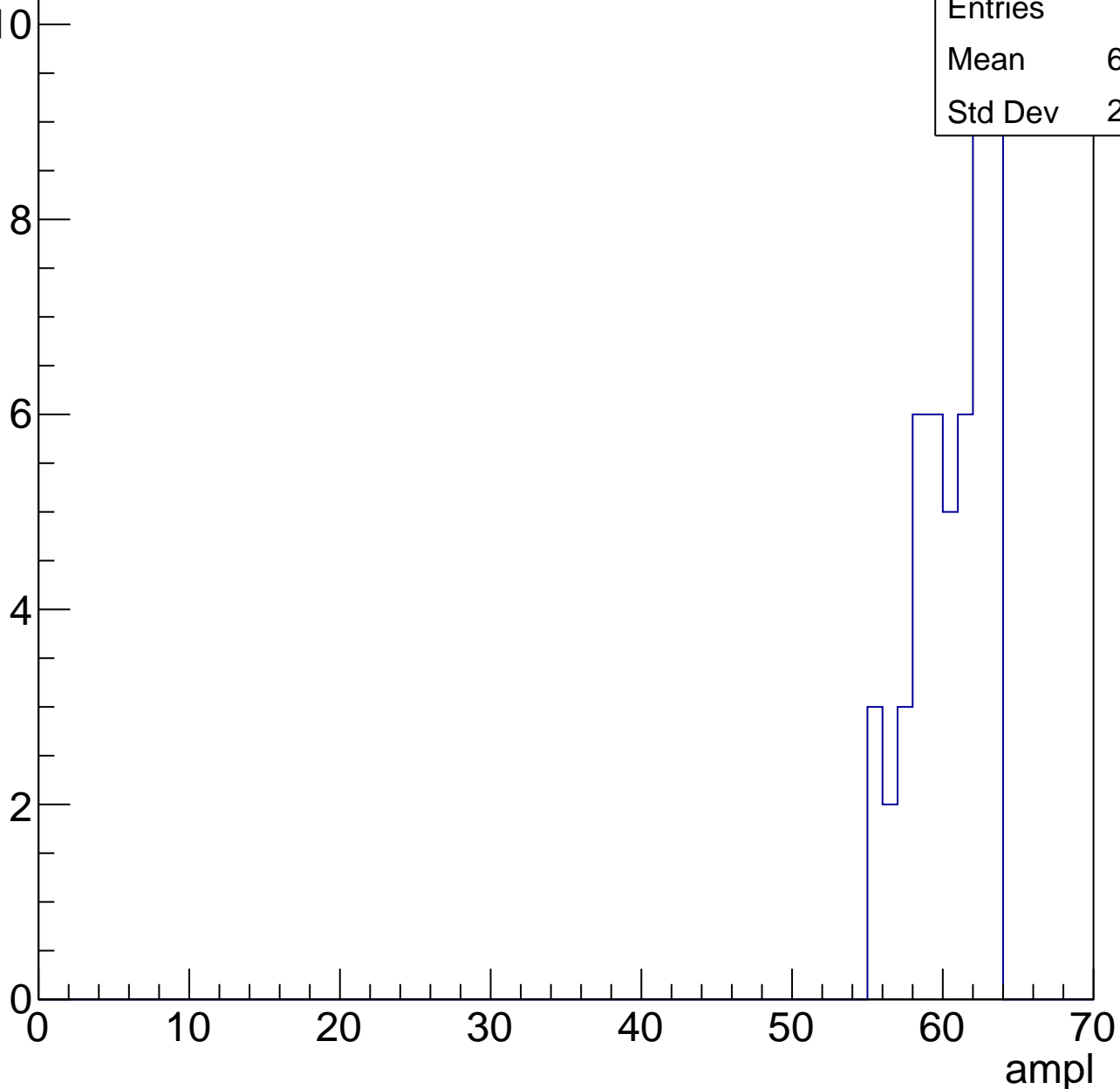


B1L102S, U13-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	60.08
Std Dev	2.432



B1L102S, U13-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

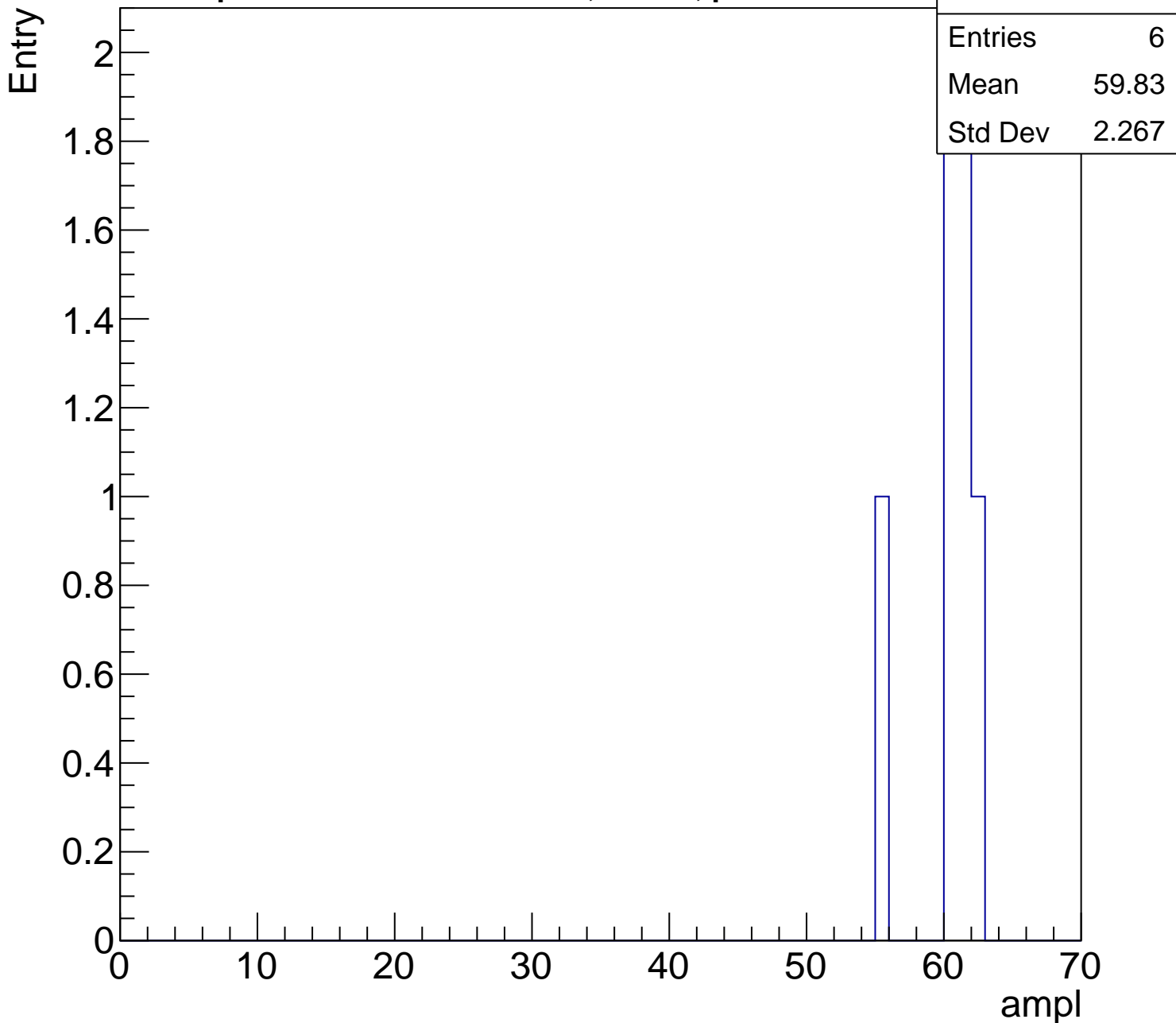
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	59.83
Std Dev	2.267

0 10 20 30 40 50 60 70

ampl



B1L102S, U13-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch46, adc0

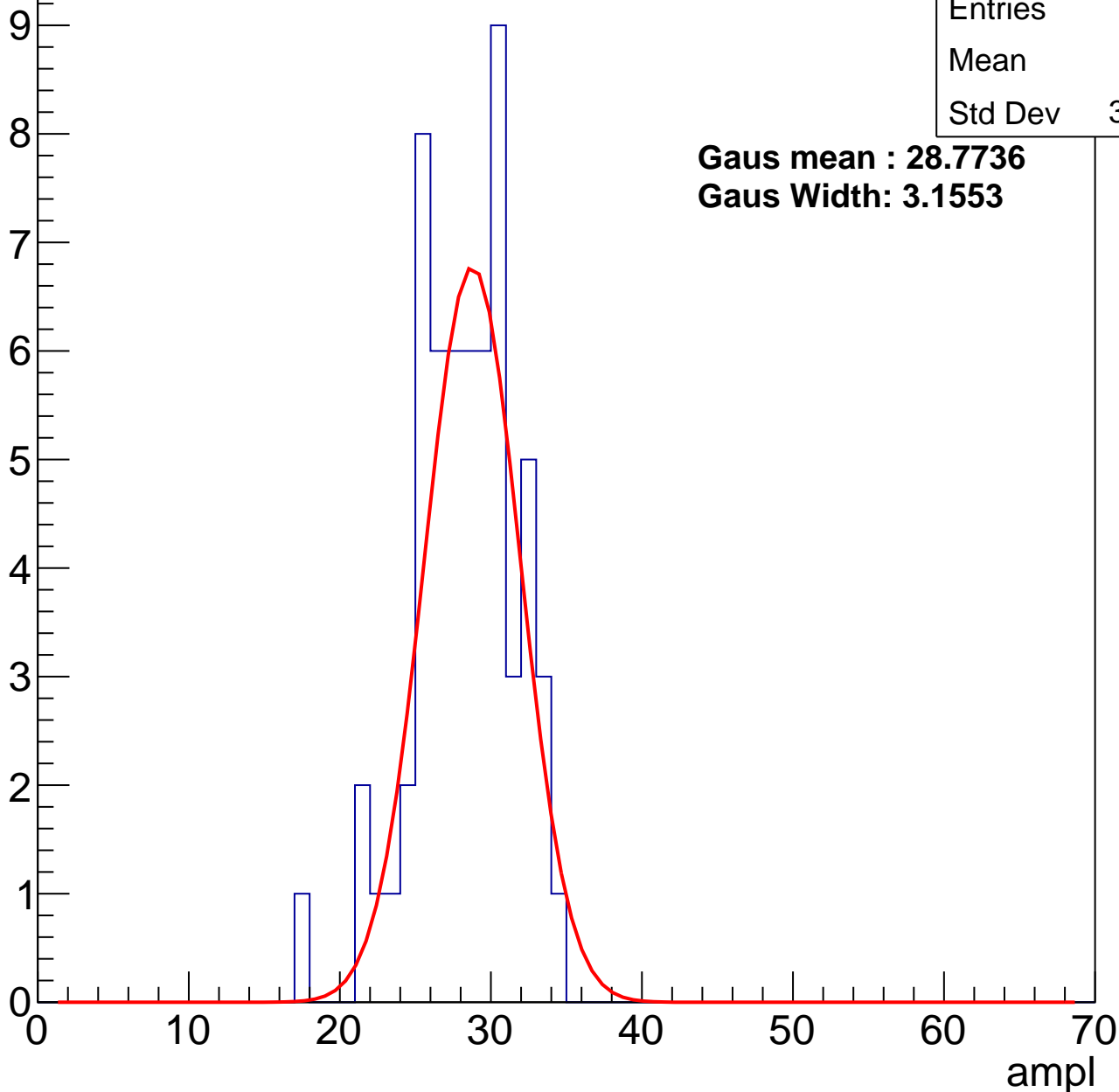
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	27.8
Std Dev	3.346

Gaus mean : 28.7736

Gaus Width: 3.1553



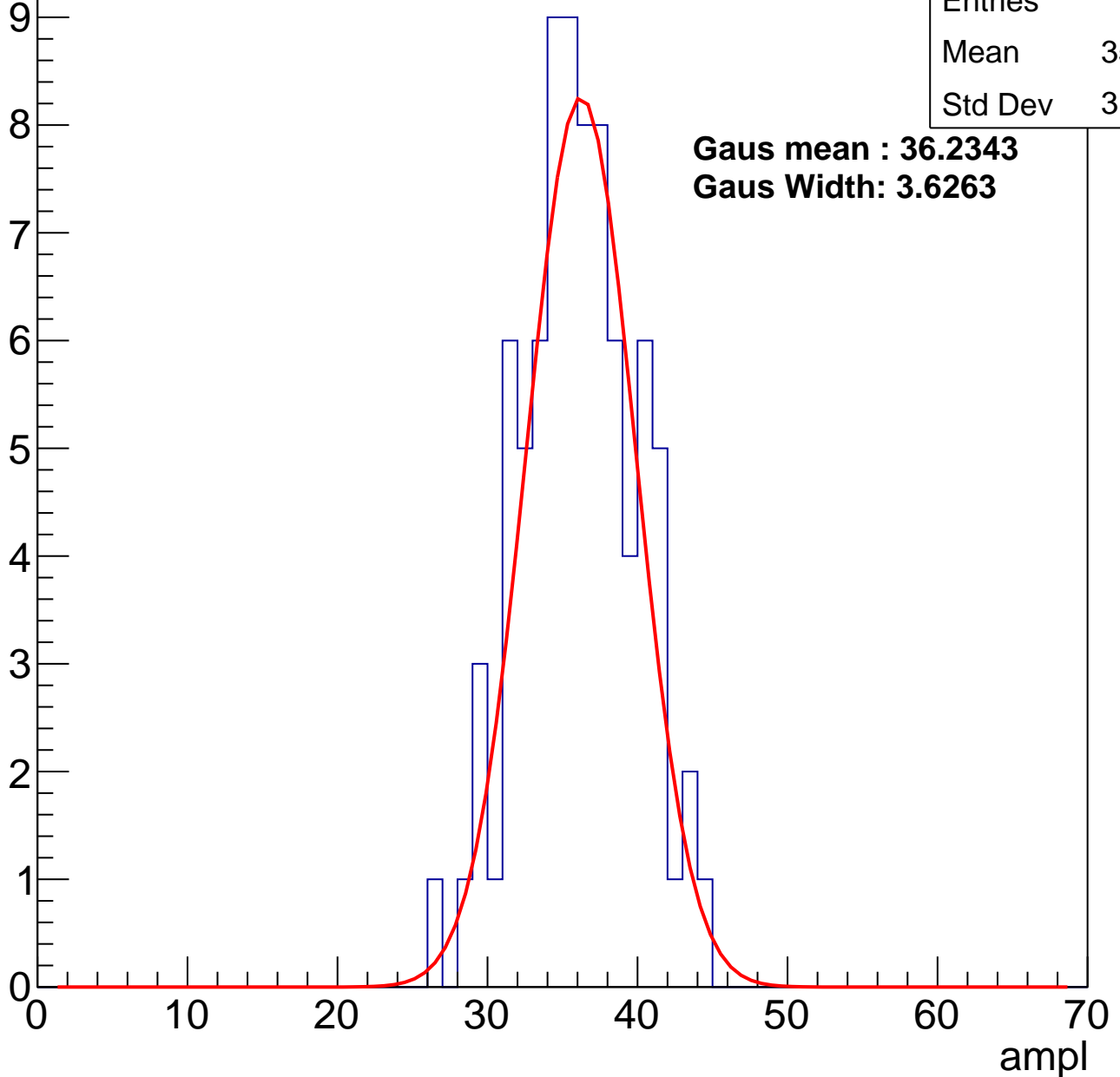
B1L102S, U13-ch46, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.62
Std Dev	3.747

Gaus mean : 36.2343
Gaus Width: 3.6263



B1L102S, U13-ch46, adc2

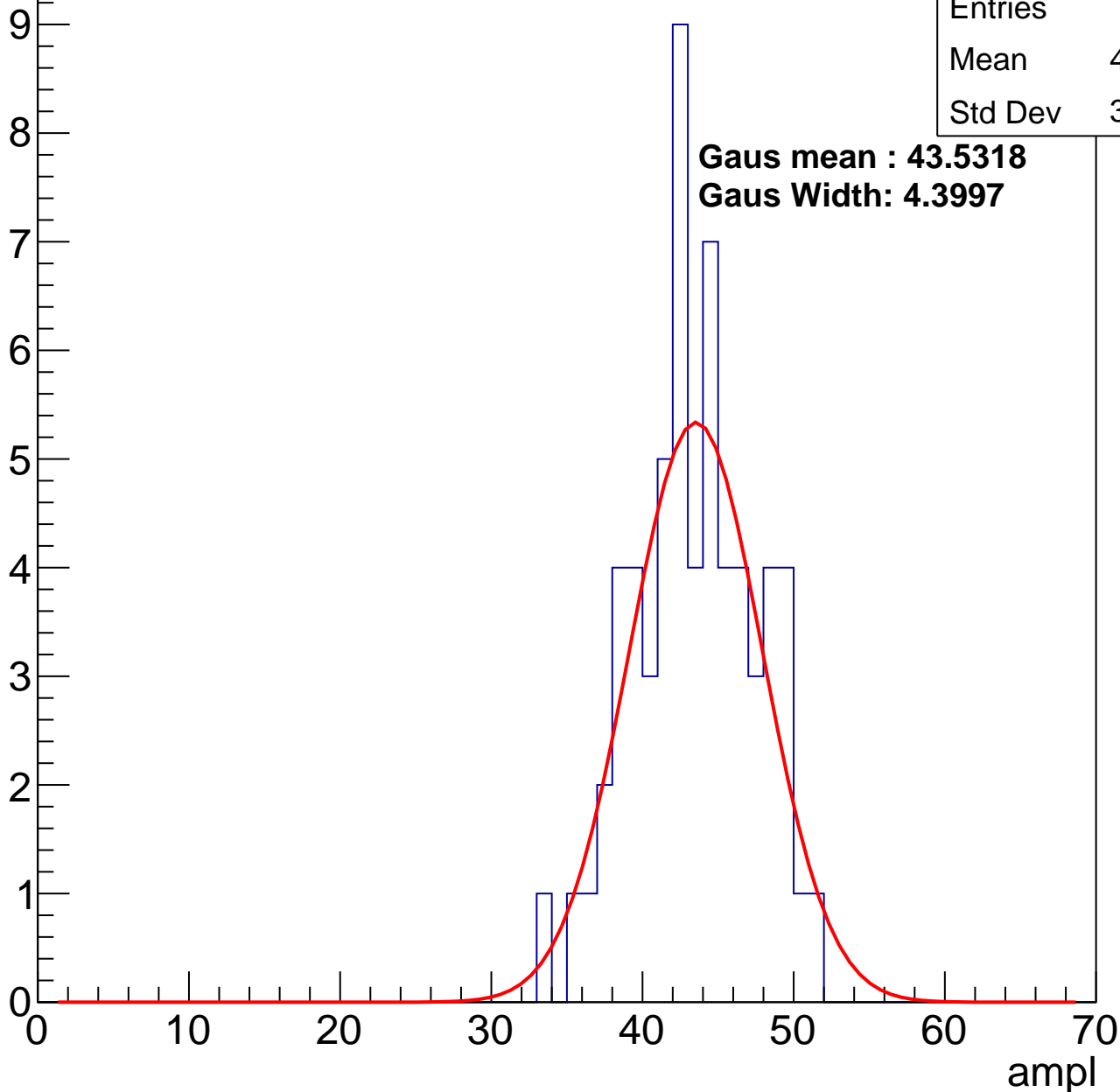
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.95
Std Dev	3.953

Gaus mean : 43.5318

Gaus Width: 4.3997

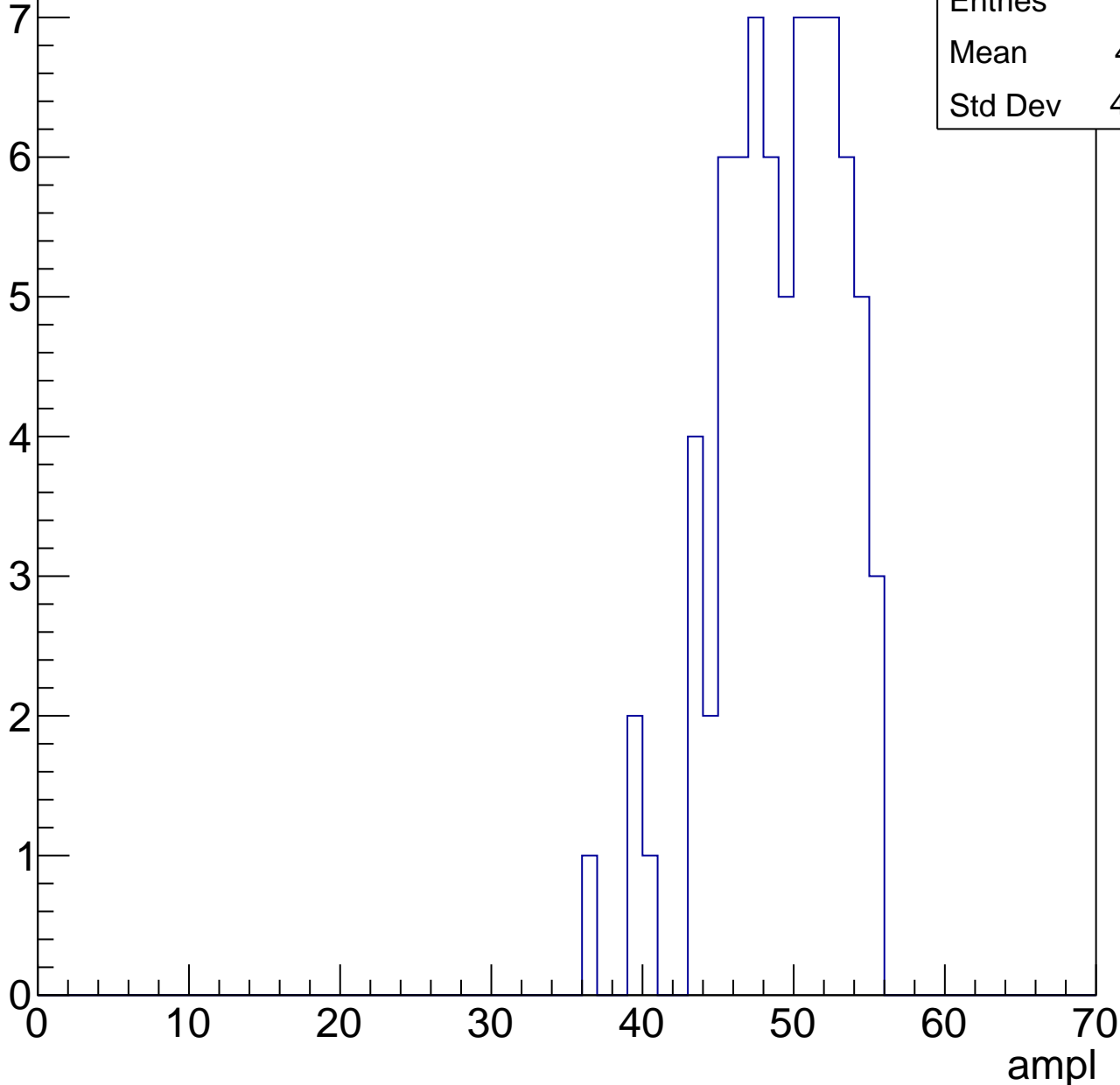


B1L102S, U13-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

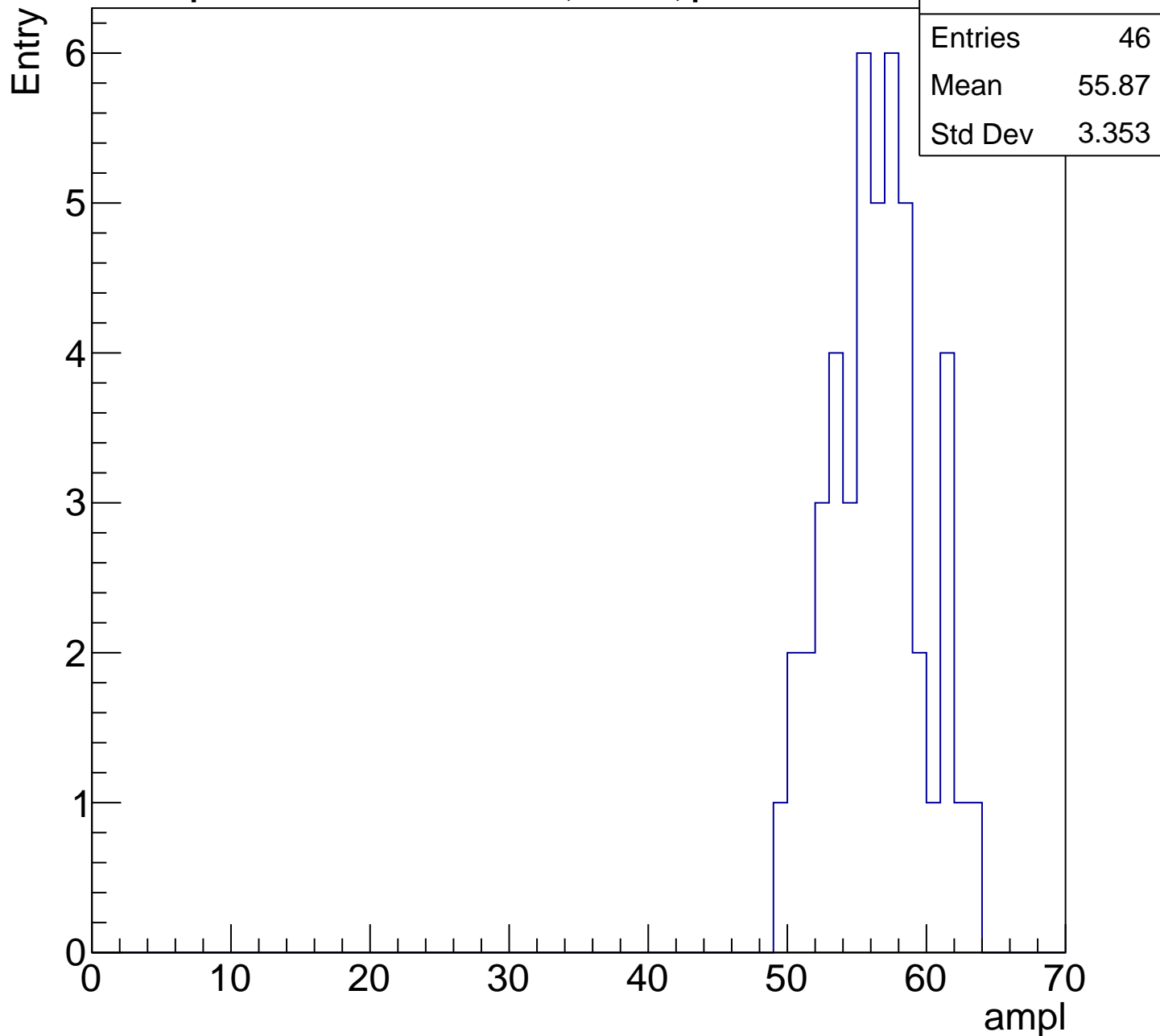
Entry

Entries	75
Mean	48.61
Std Dev	4.072



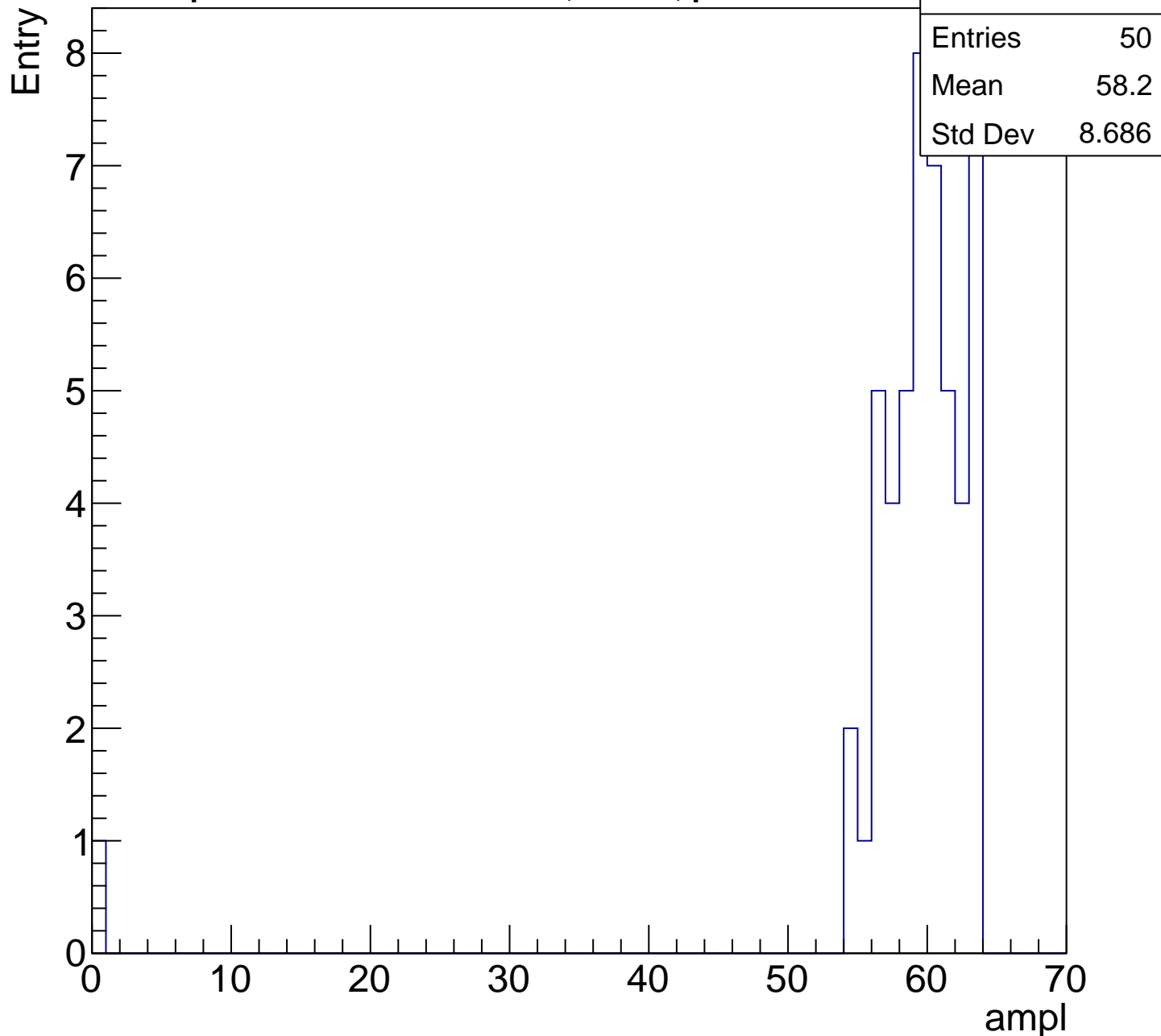
B1L102S, U13-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U13-ch46, adc5

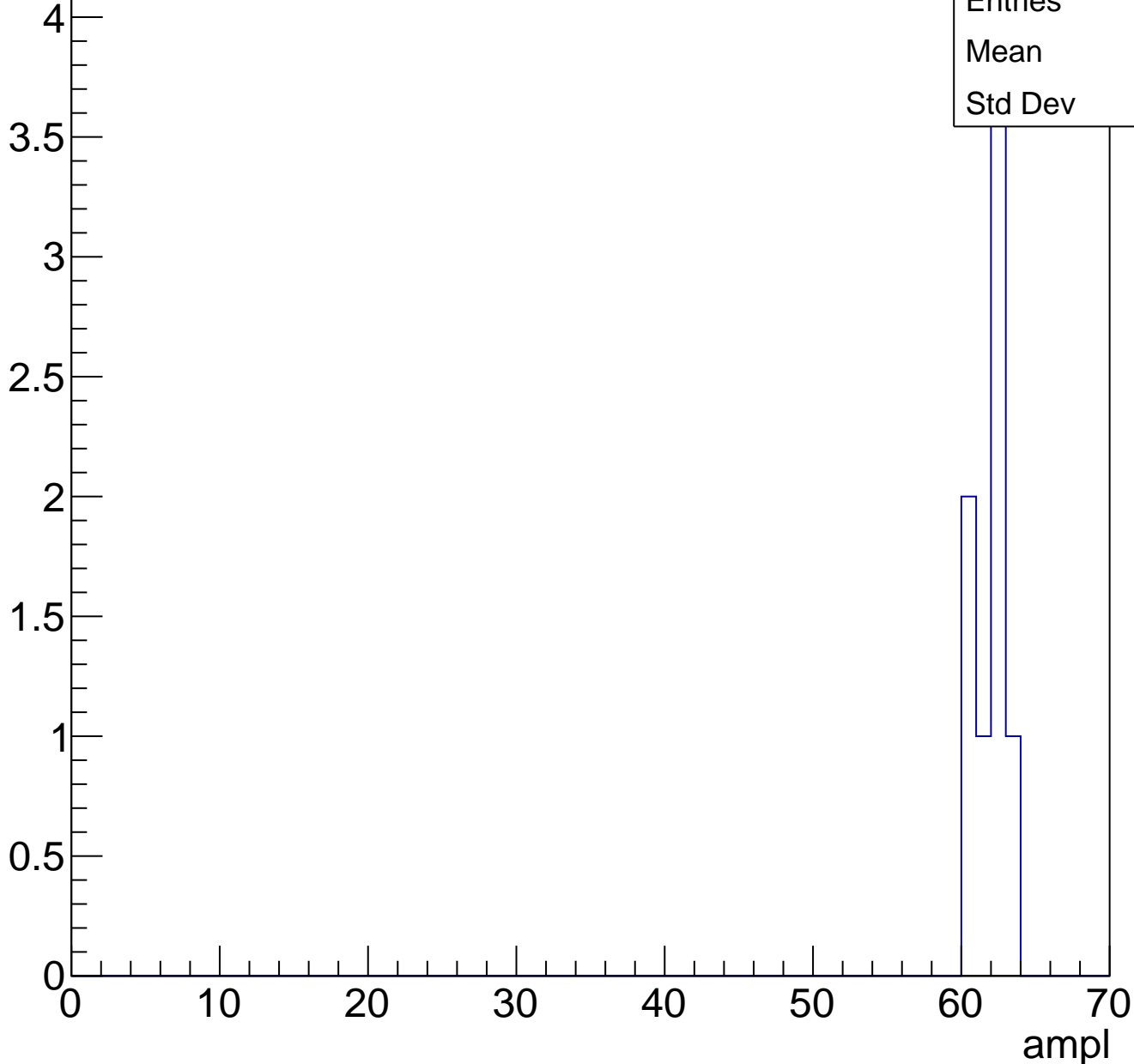
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U13-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch47, adc0

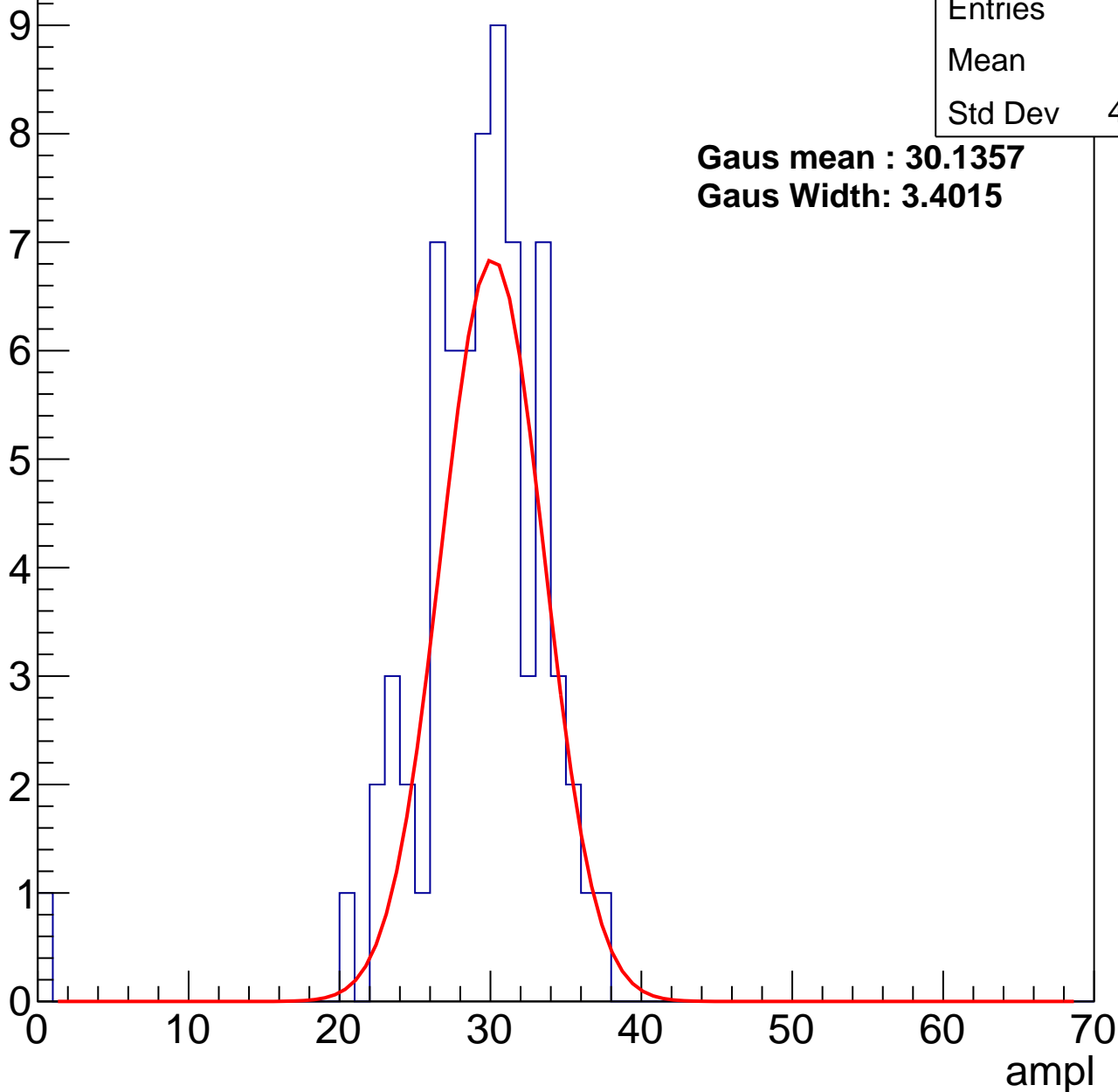
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.7
Std Dev	4.952

Gaus mean : 30.1357

Gaus Width: 3.4015



B1L102S, U13-ch47, adc1

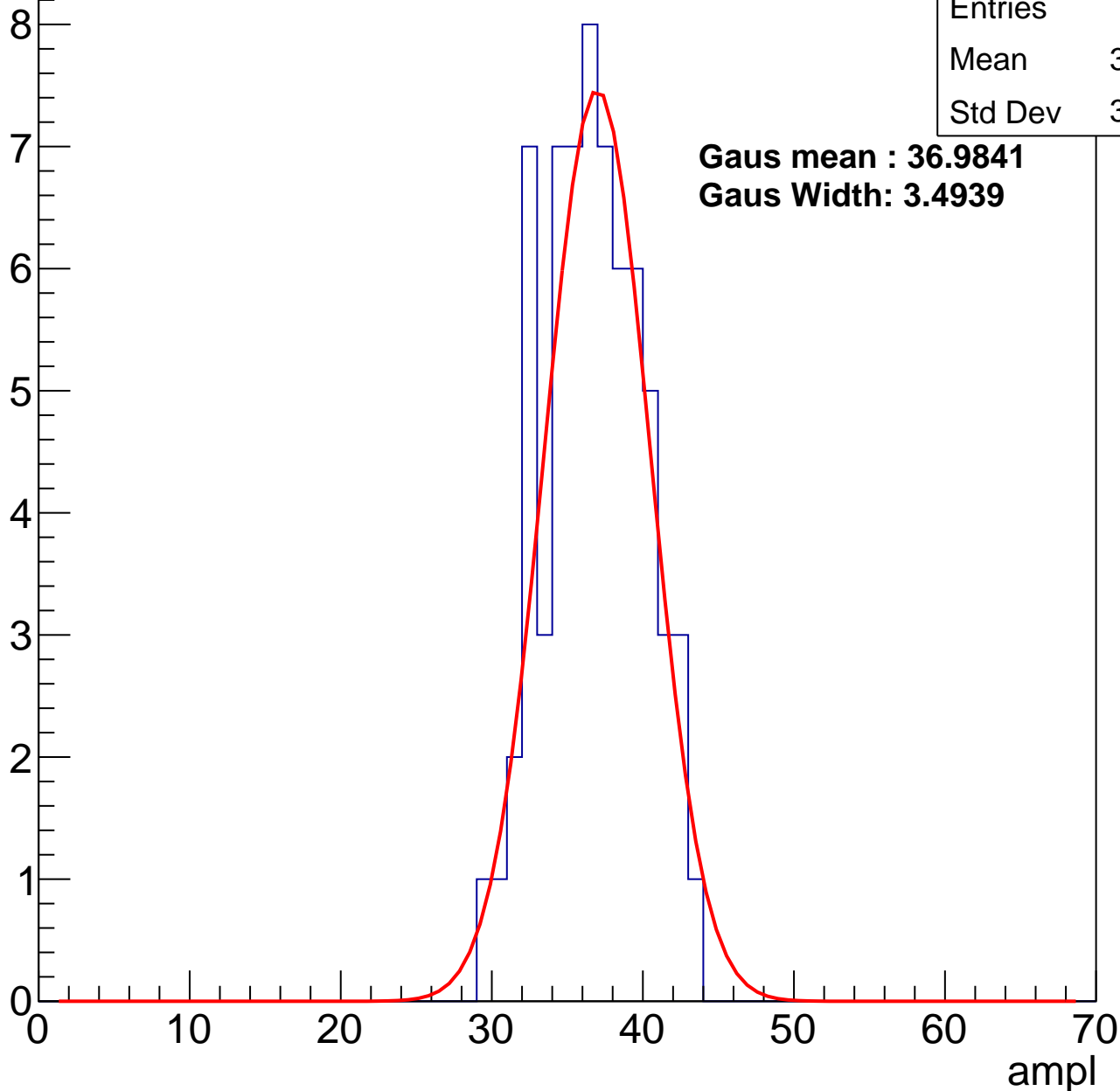
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.24
Std Dev	3.237

Gaus mean : 36.9841

Gaus Width: 3.4939



B1L102S, U13-ch47, adc2

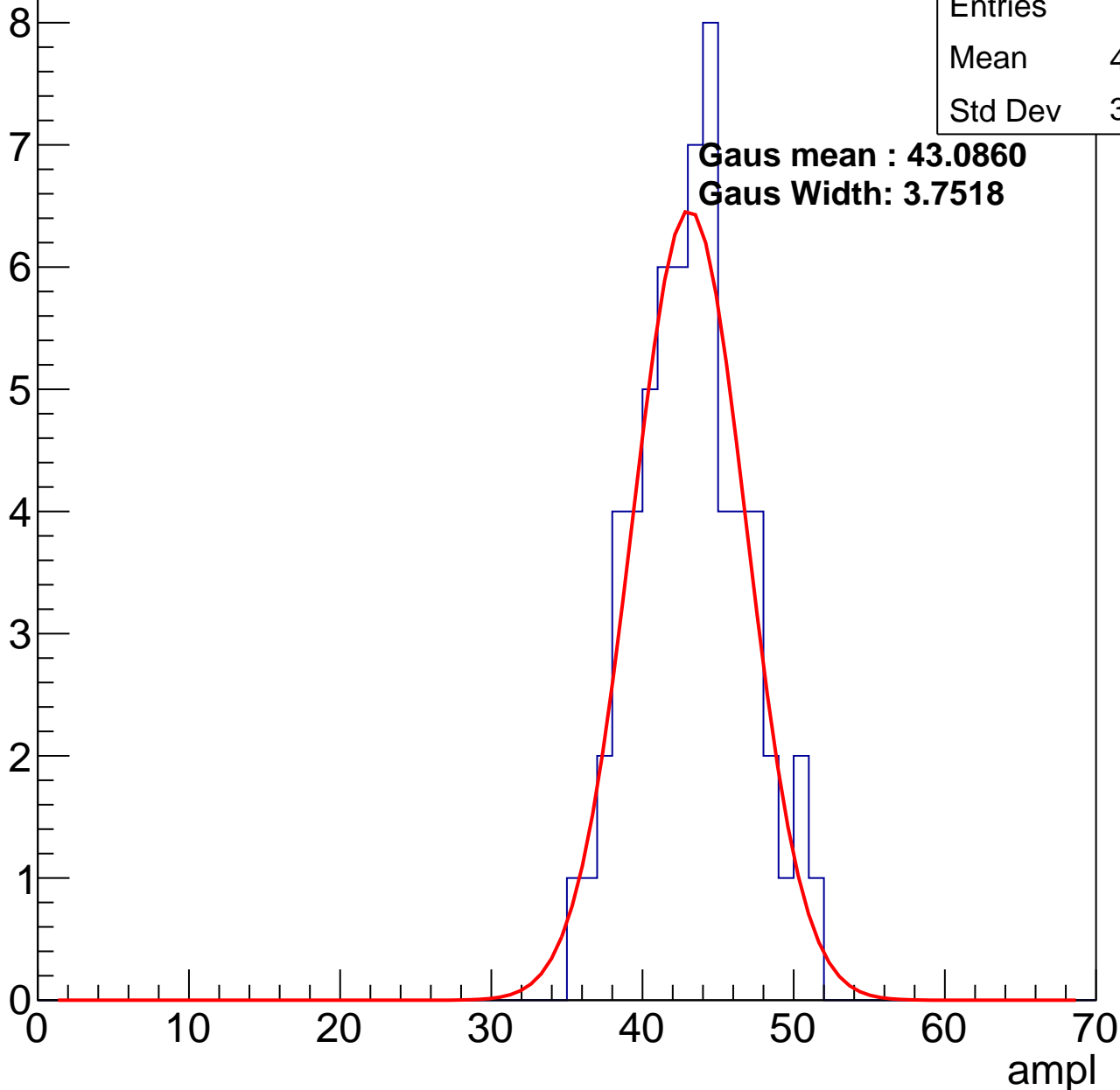
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.77
Std Dev	3.576

Gaus mean : 43.0860

Gaus Width: 3.7518

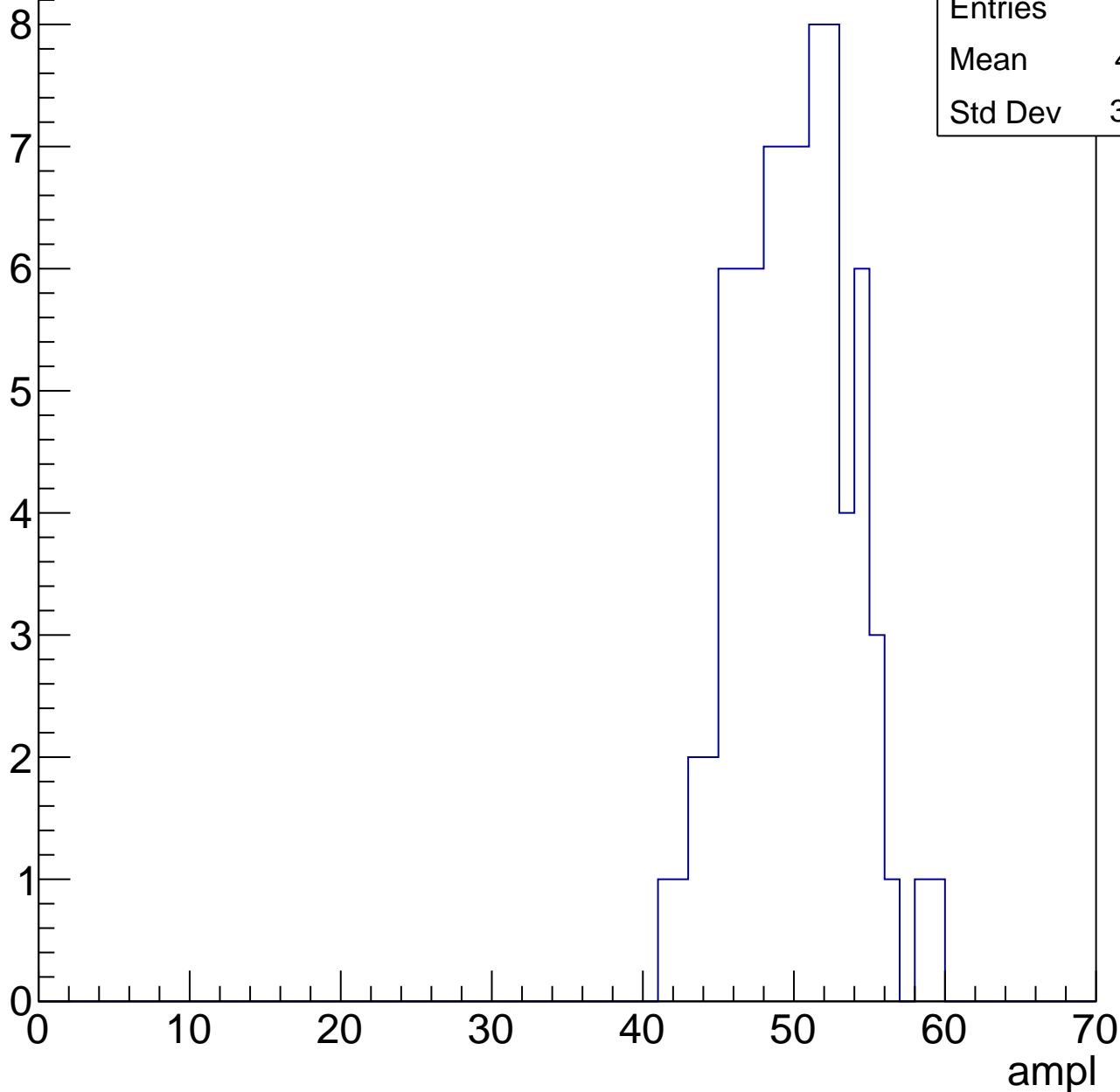


B1L102S, U13-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.51
Std Dev	3.709

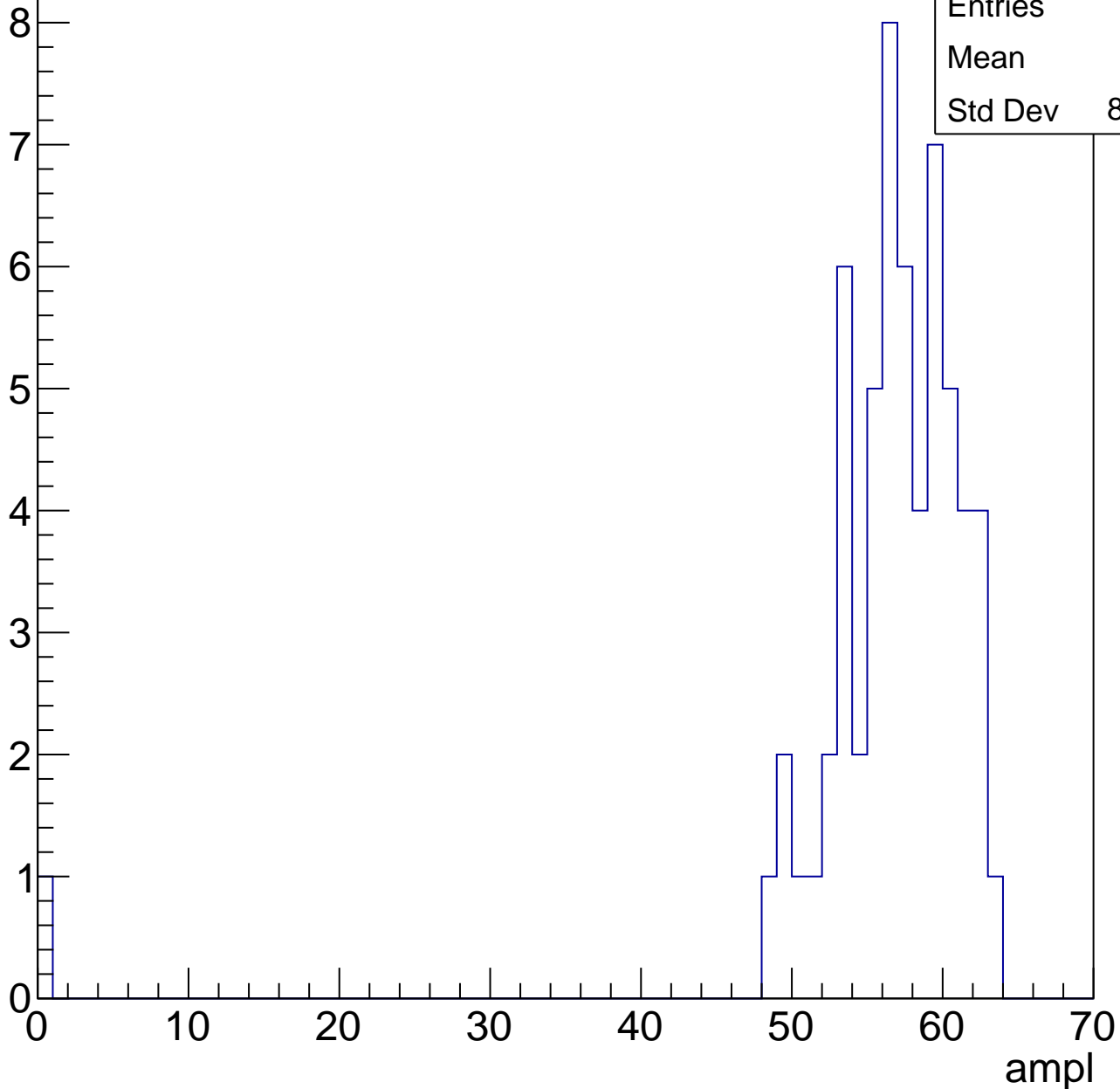


B1L102S, U13-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.7
Std Dev	8.073

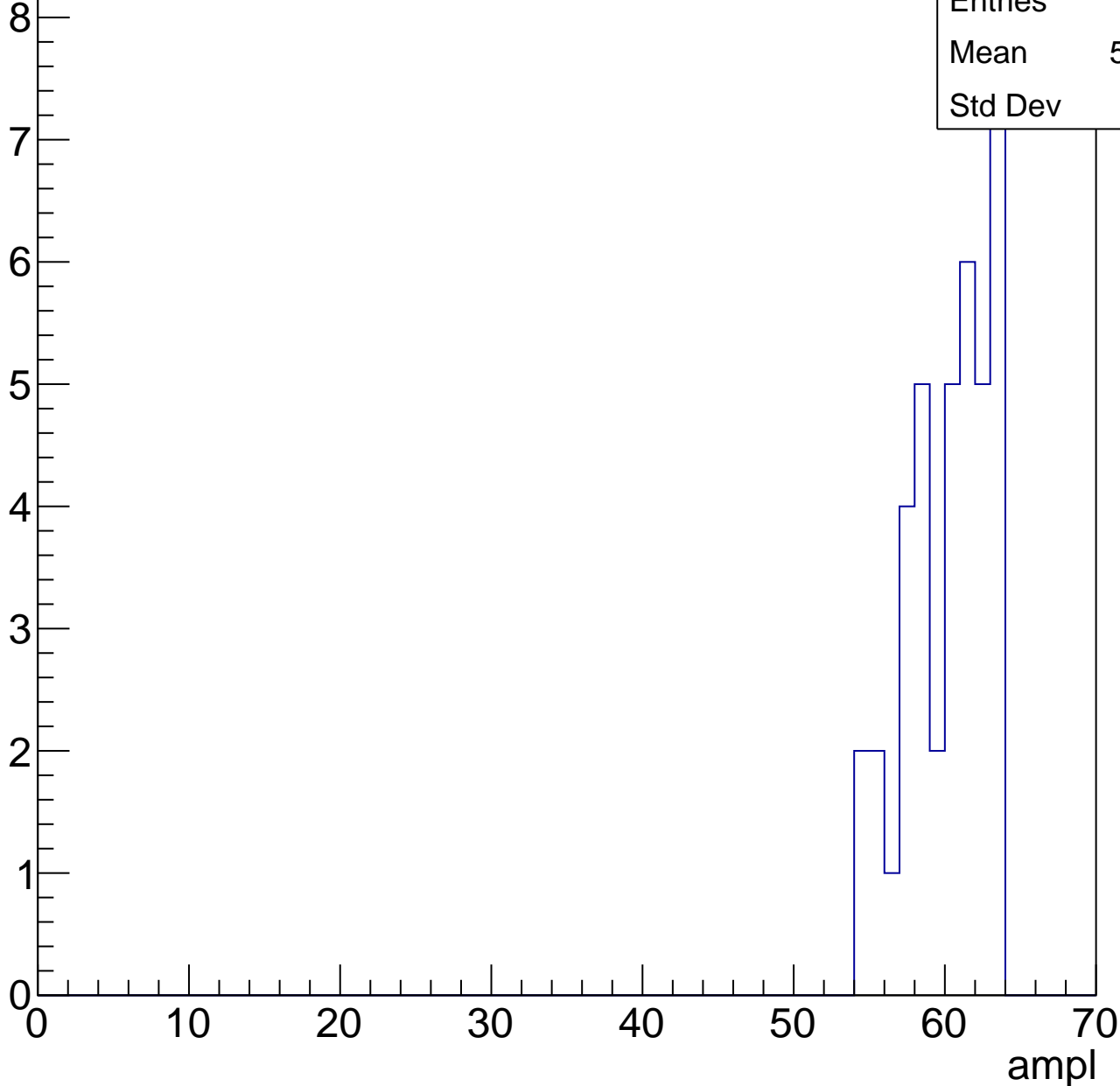


B1L102S, U13-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	59.75
Std Dev	2.7



B1L102S, U13-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch48, adc0

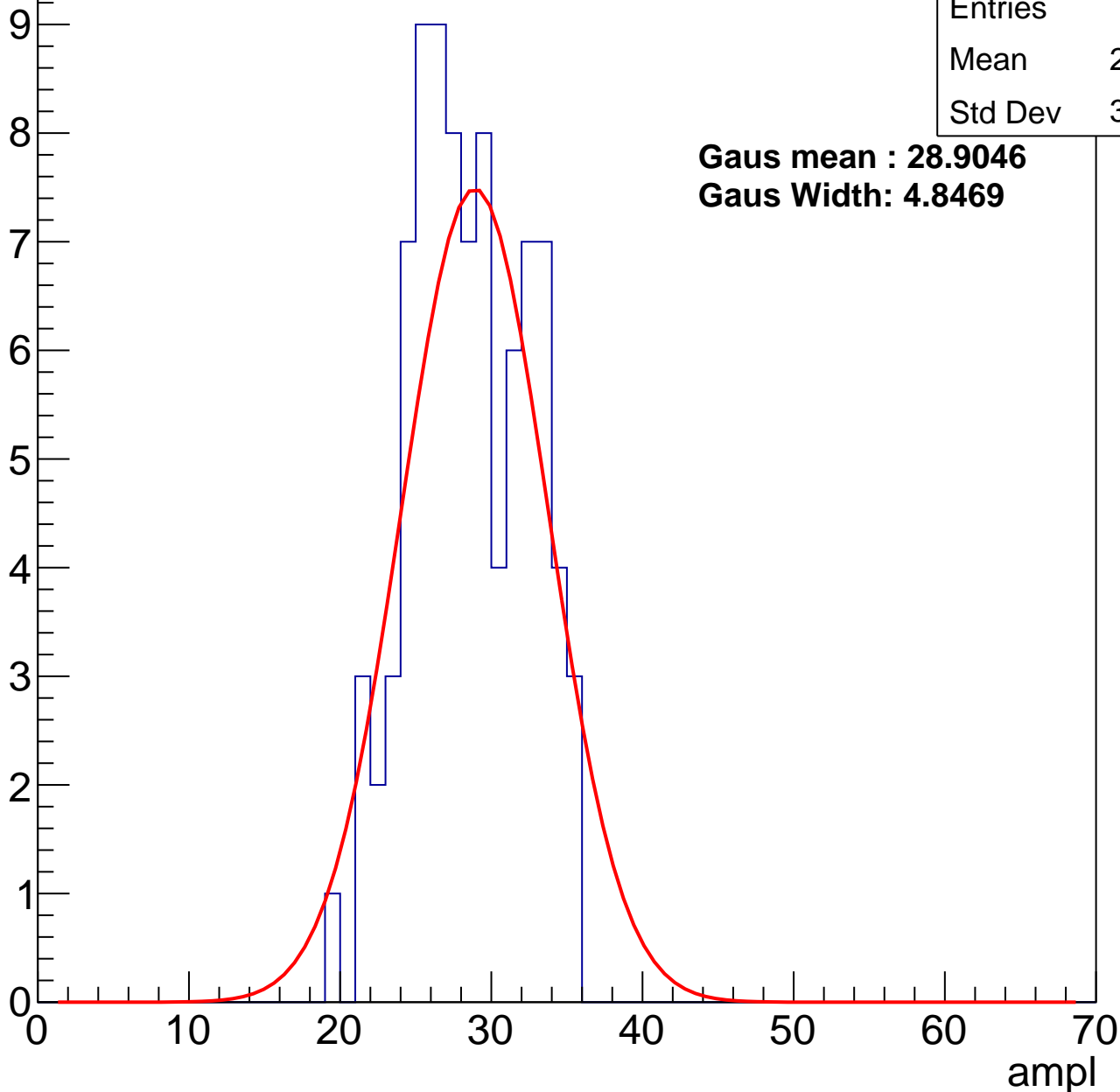
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	28.05
Std Dev	3.796

Gaus mean : 28.9046

Gaus Width: 4.8469



B1L102S, U13-ch48, adc1

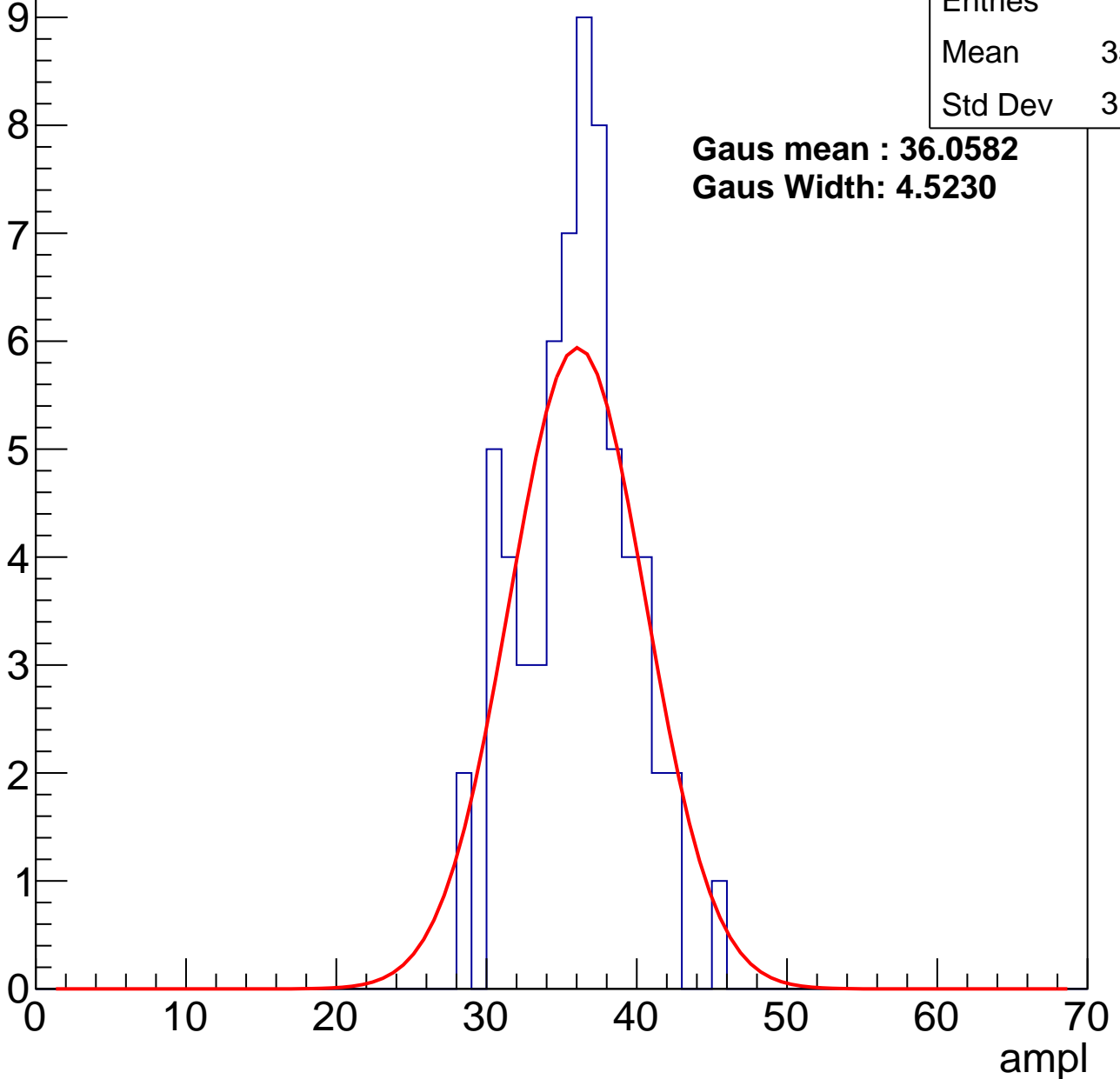
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.55
Std Dev	3.578

Gaus mean : 36.0582

Gaus Width: 4.5230



B1L102S, U13-ch48, adc2

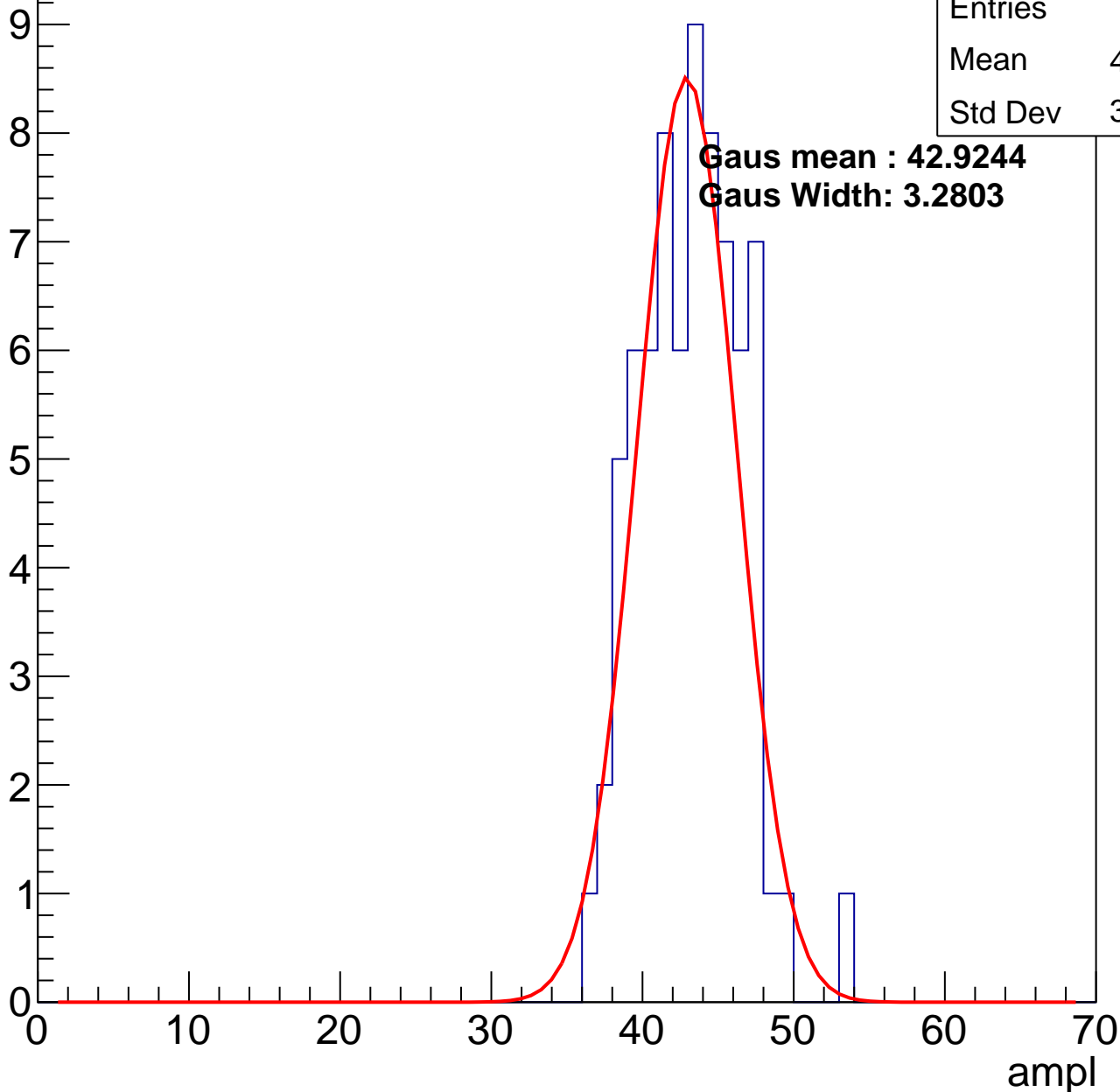
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.74
Std Dev	3.272

Gaus mean : 42.9244

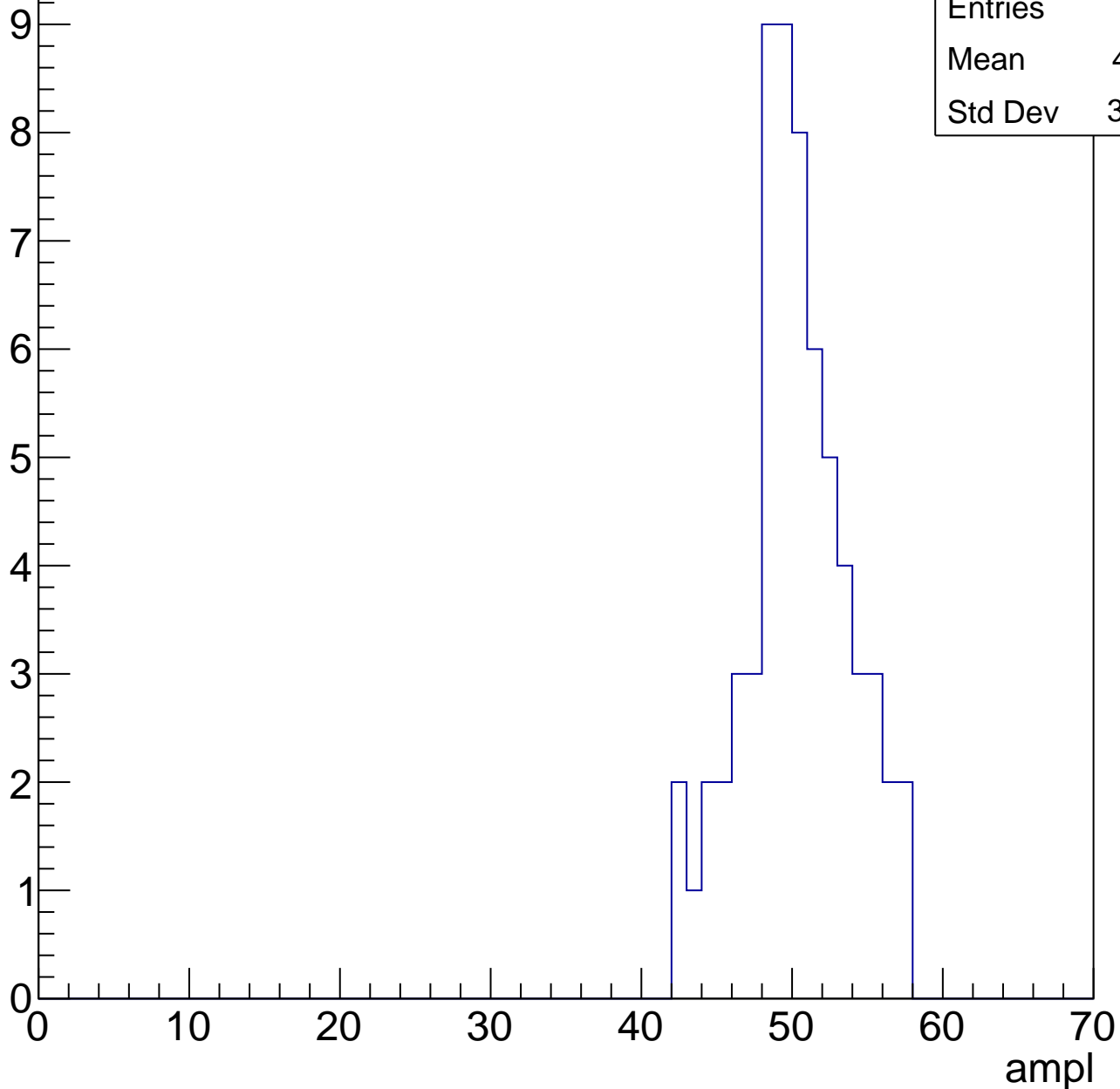
Gaus Width: 3.2803



B1L102S, U13-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

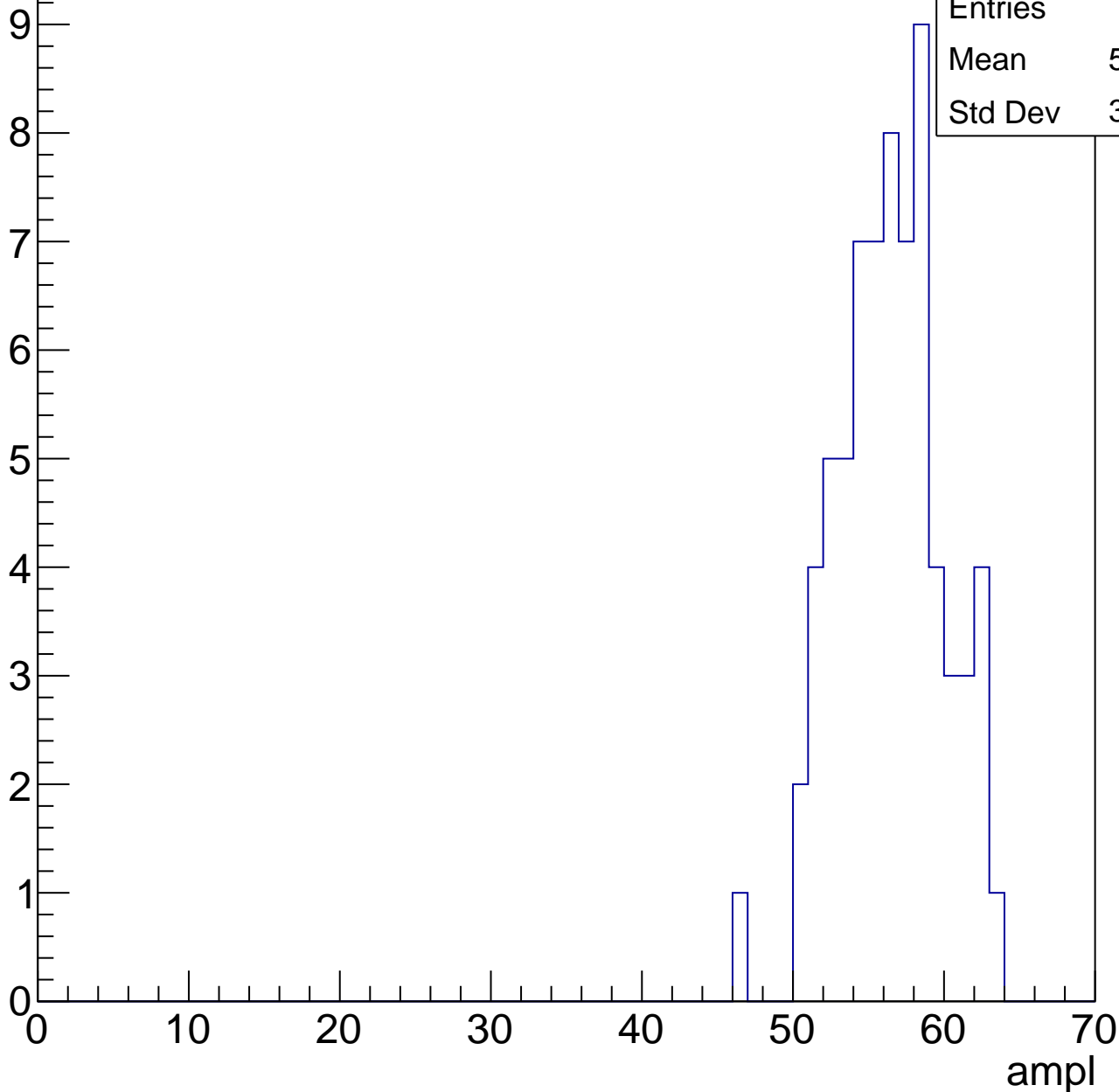


B1L102S, U13-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

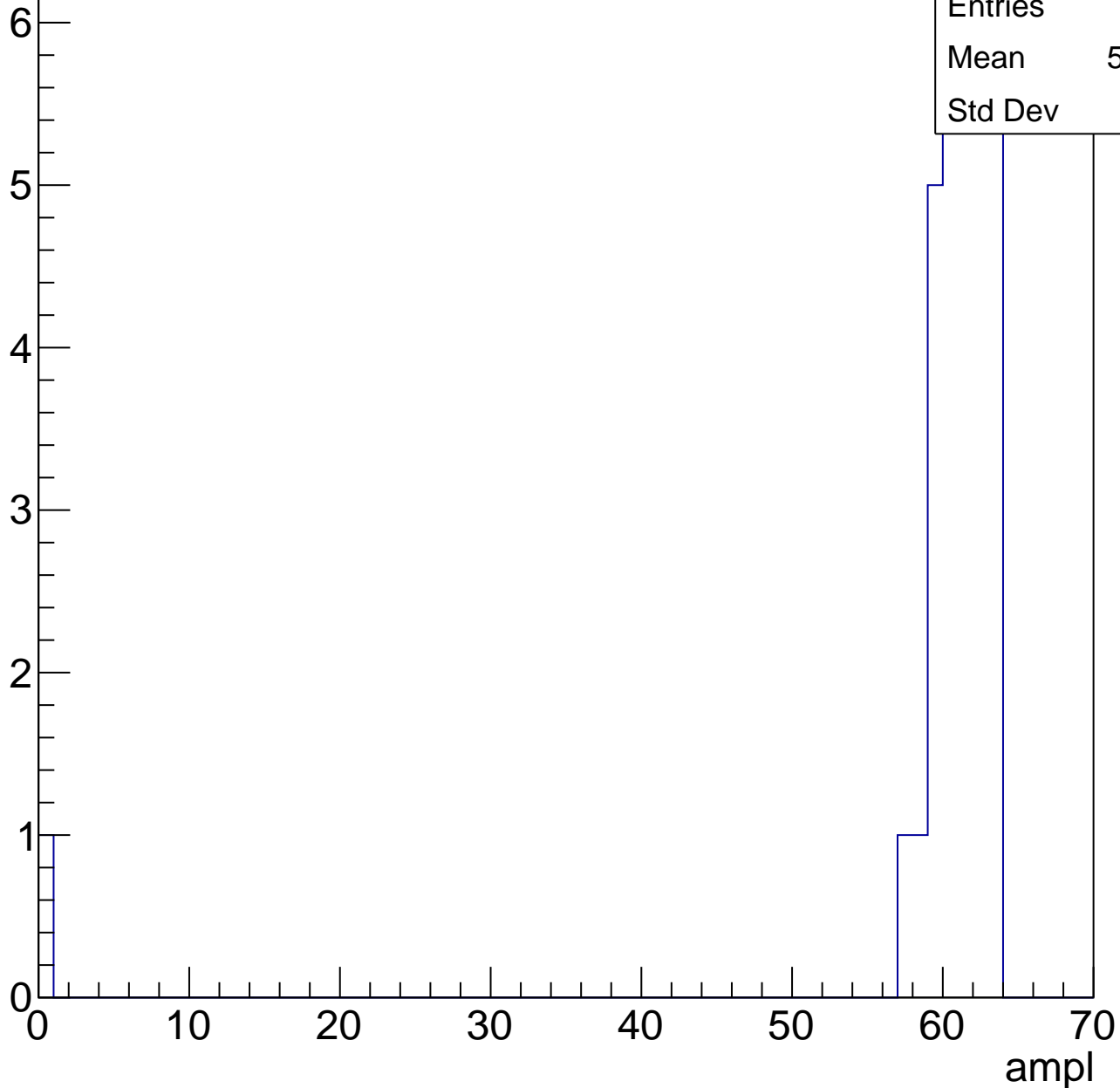
Entries	70
Mean	55.96
Std Dev	3.437



B1L102S, U13-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

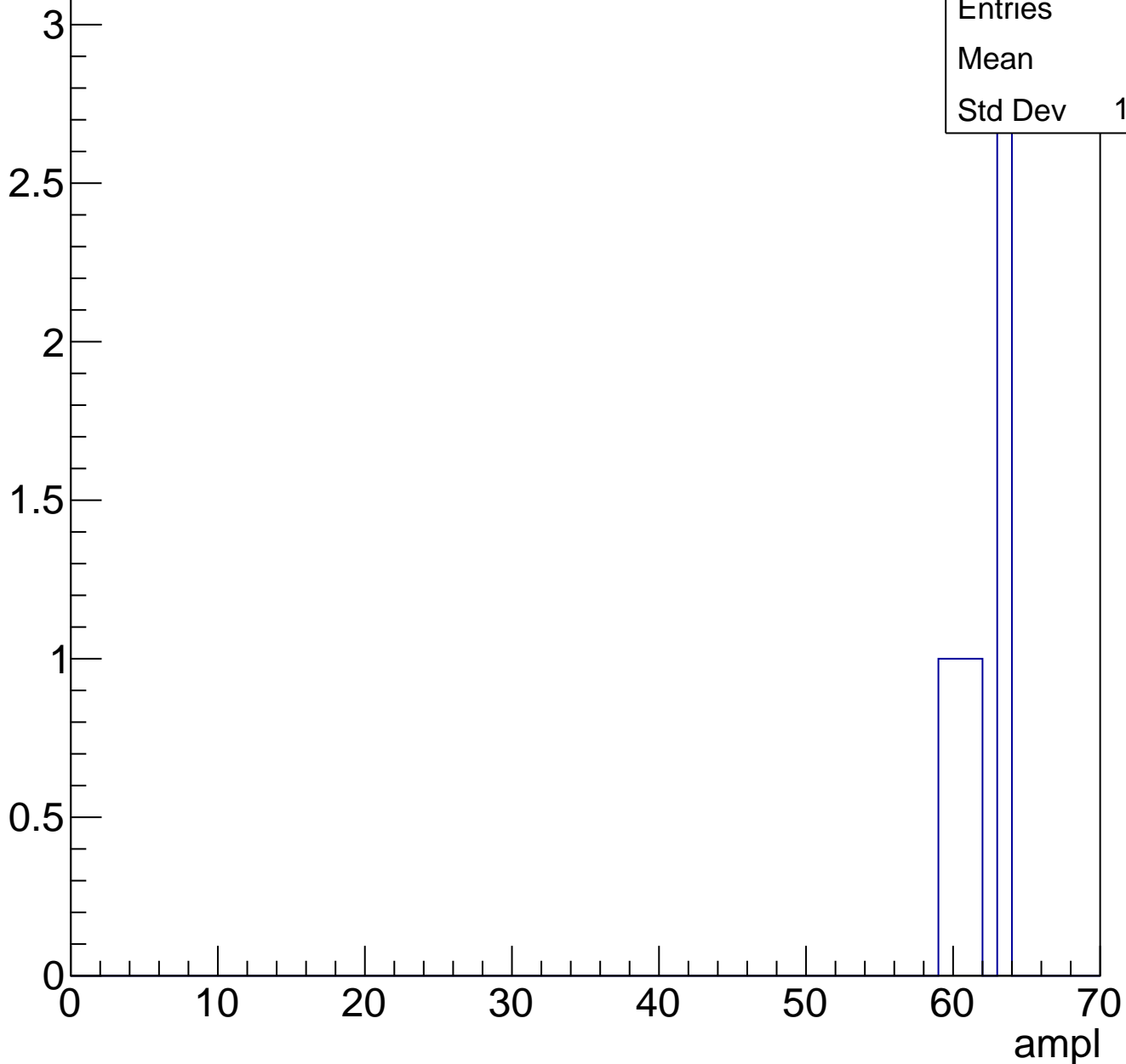
Entry



B1L102S, U13-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch49, adc0

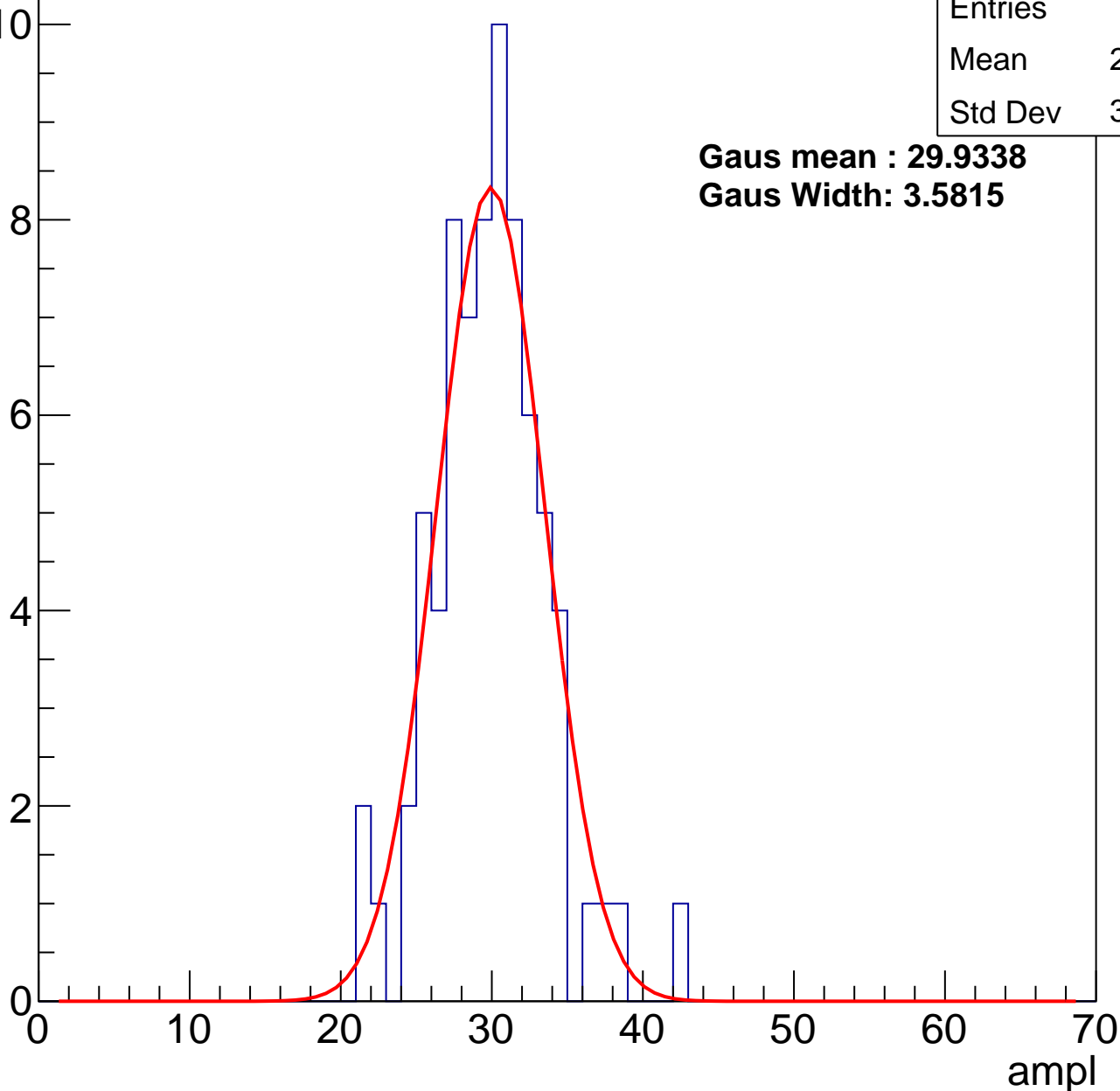
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29.45
Std Dev	3.688

Gaus mean : 29.9338

Gaus Width: 3.5815



B1L102S, U13-ch49, adc1

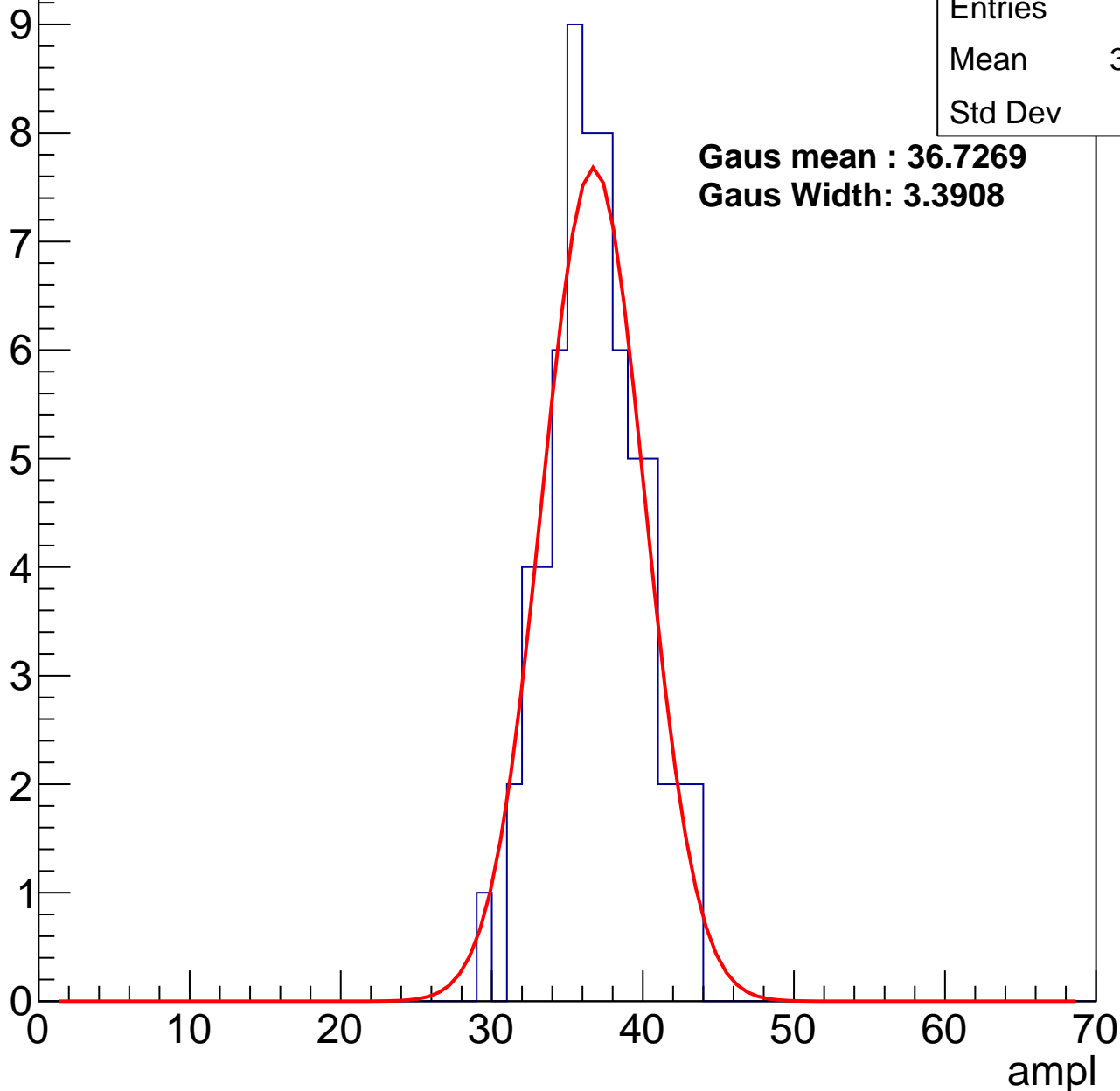
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.39
Std Dev	3.06

Gaus mean : 36.7269

Gaus Width: 3.3908



B1L102S, U13-ch49, adc2

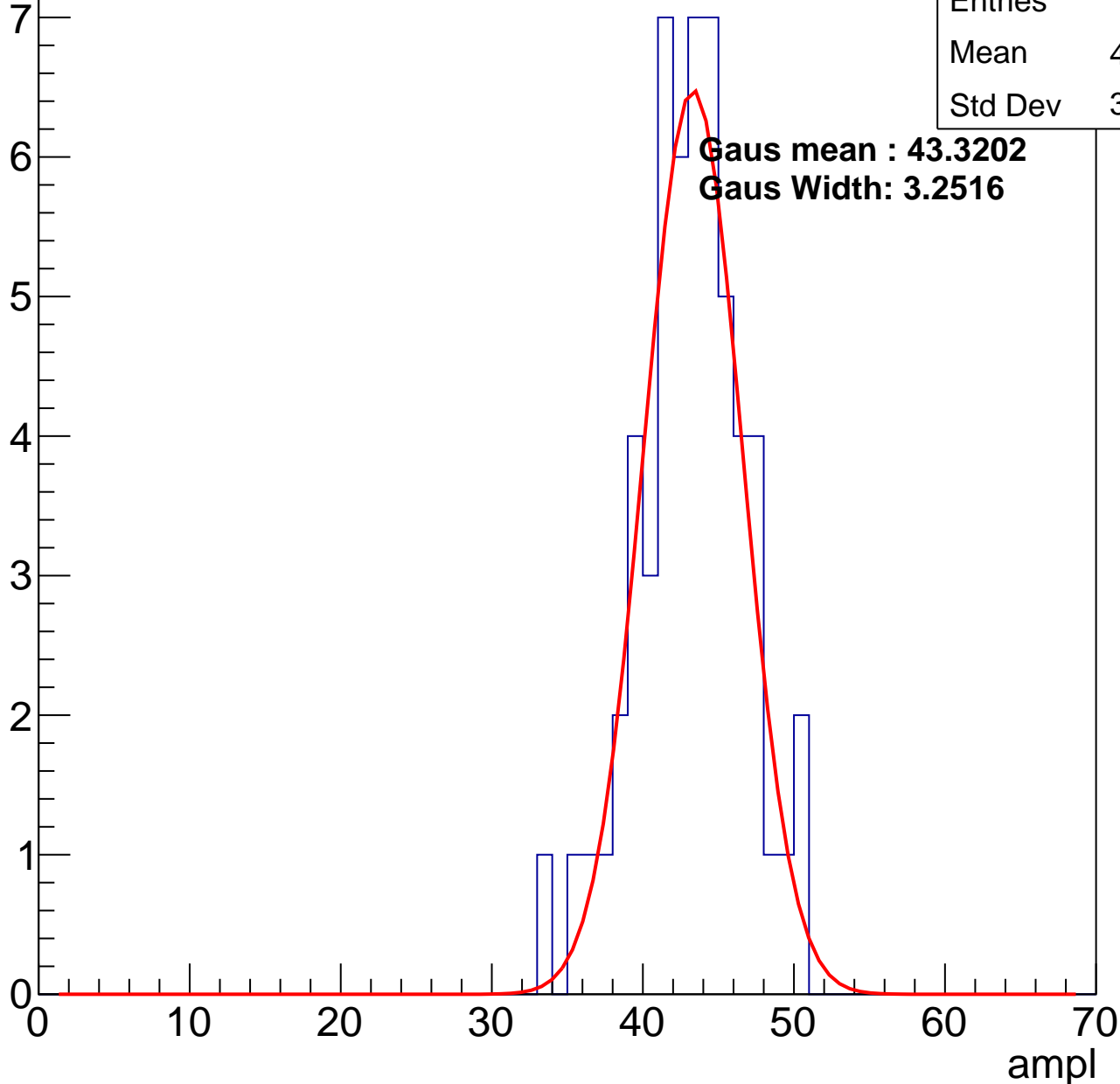
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.72
Std Dev	3.518

Gaus mean : 43.3202

Gaus Width: 3.2516

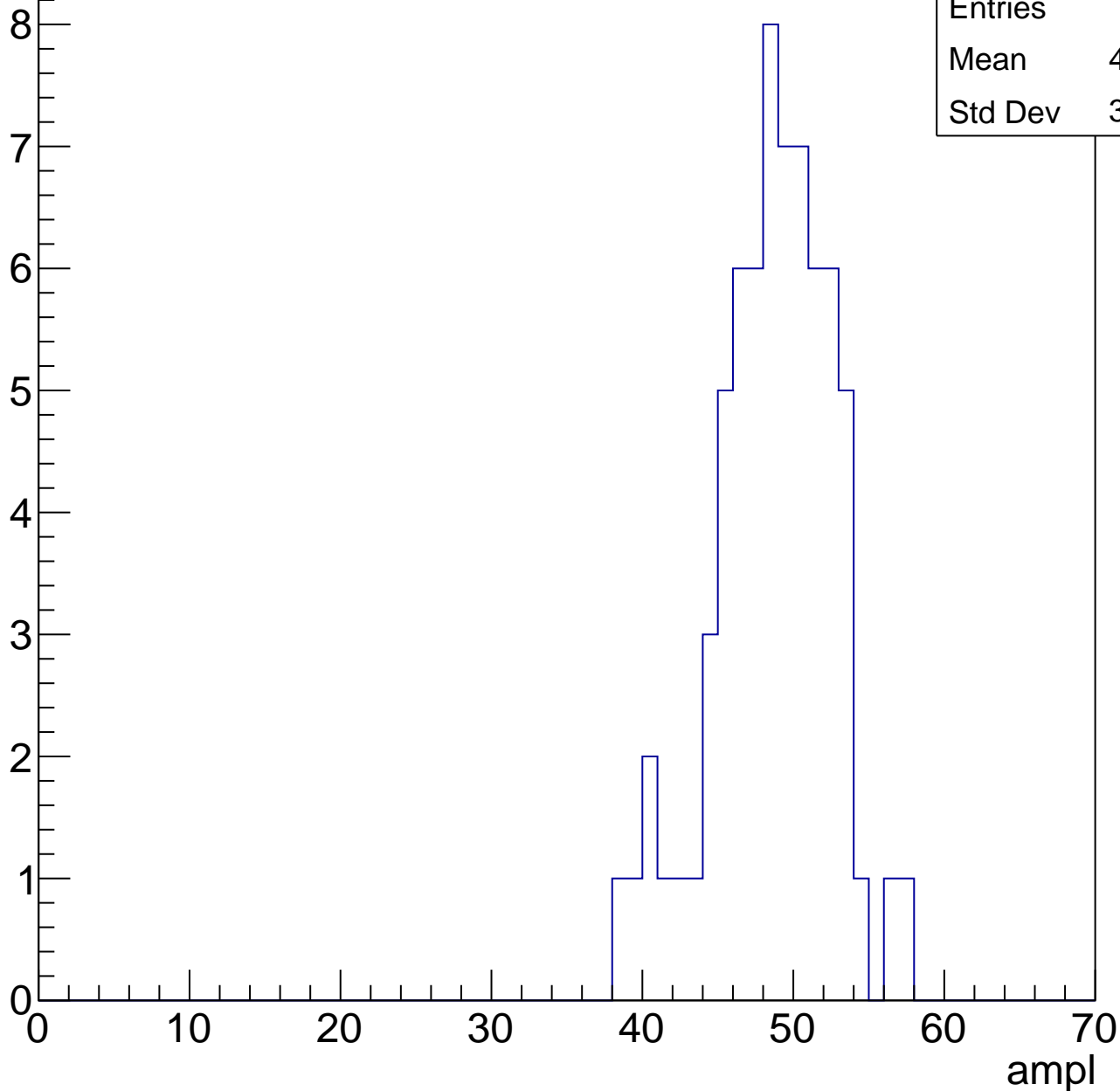


B1L102S, U13-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

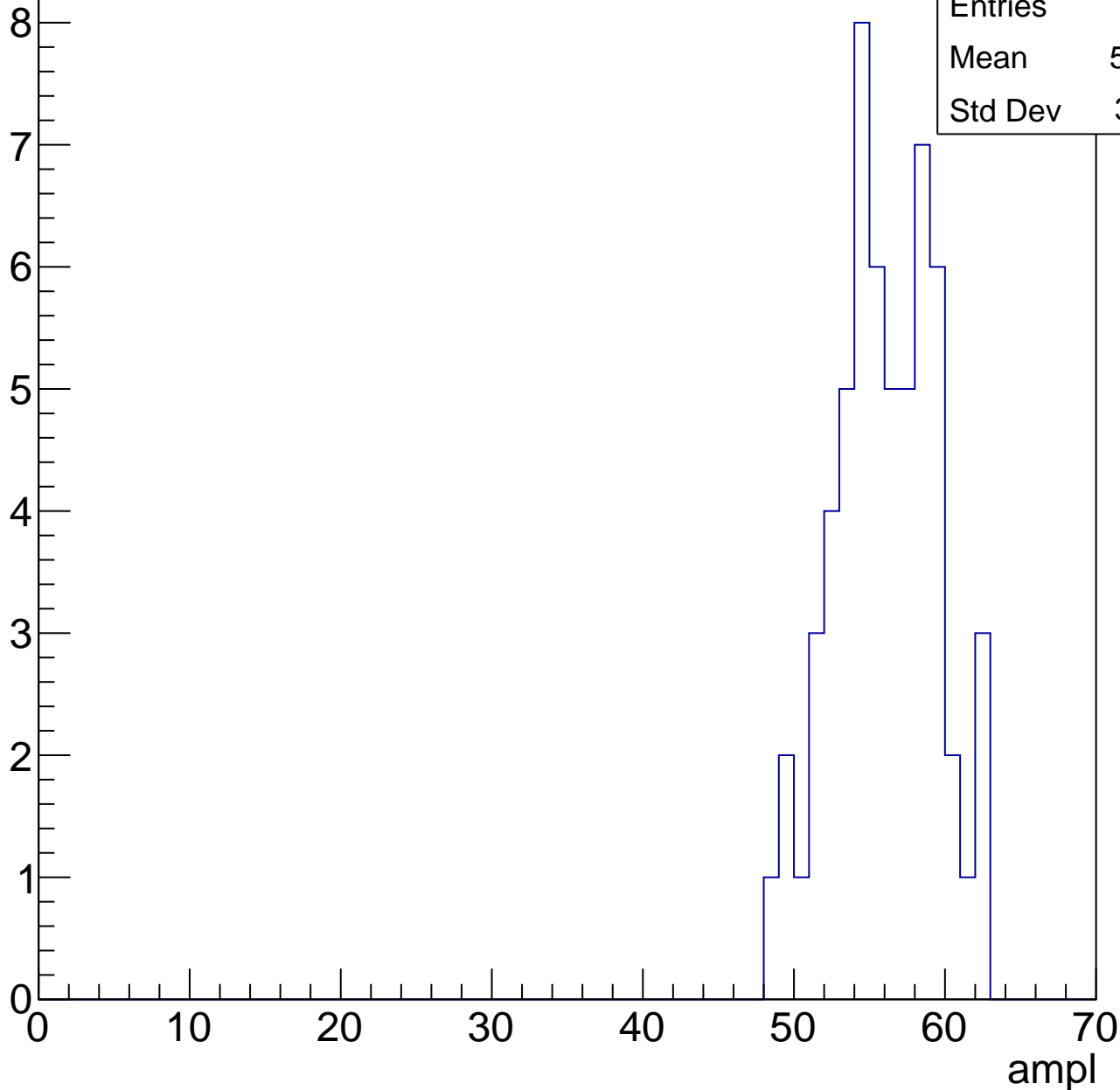
Entries	69
Mean	48.19
Std Dev	3.865



B1L102S, U13-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



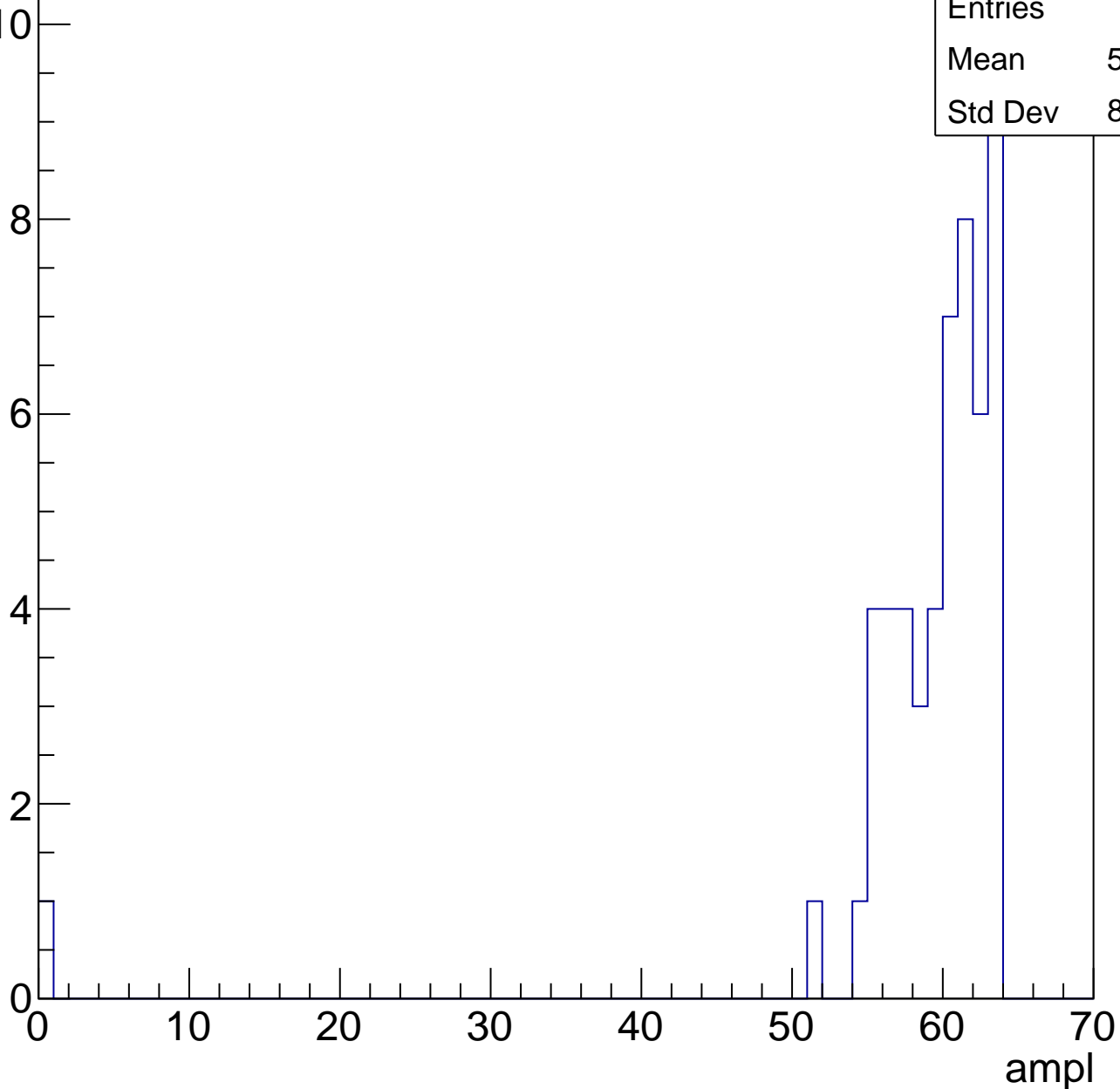
Entries	59
Mean	55.53
Std Dev	3.341

B1L102S, U13-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.43
Std Dev	8.606



B1L102S, U13-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch50, adc0

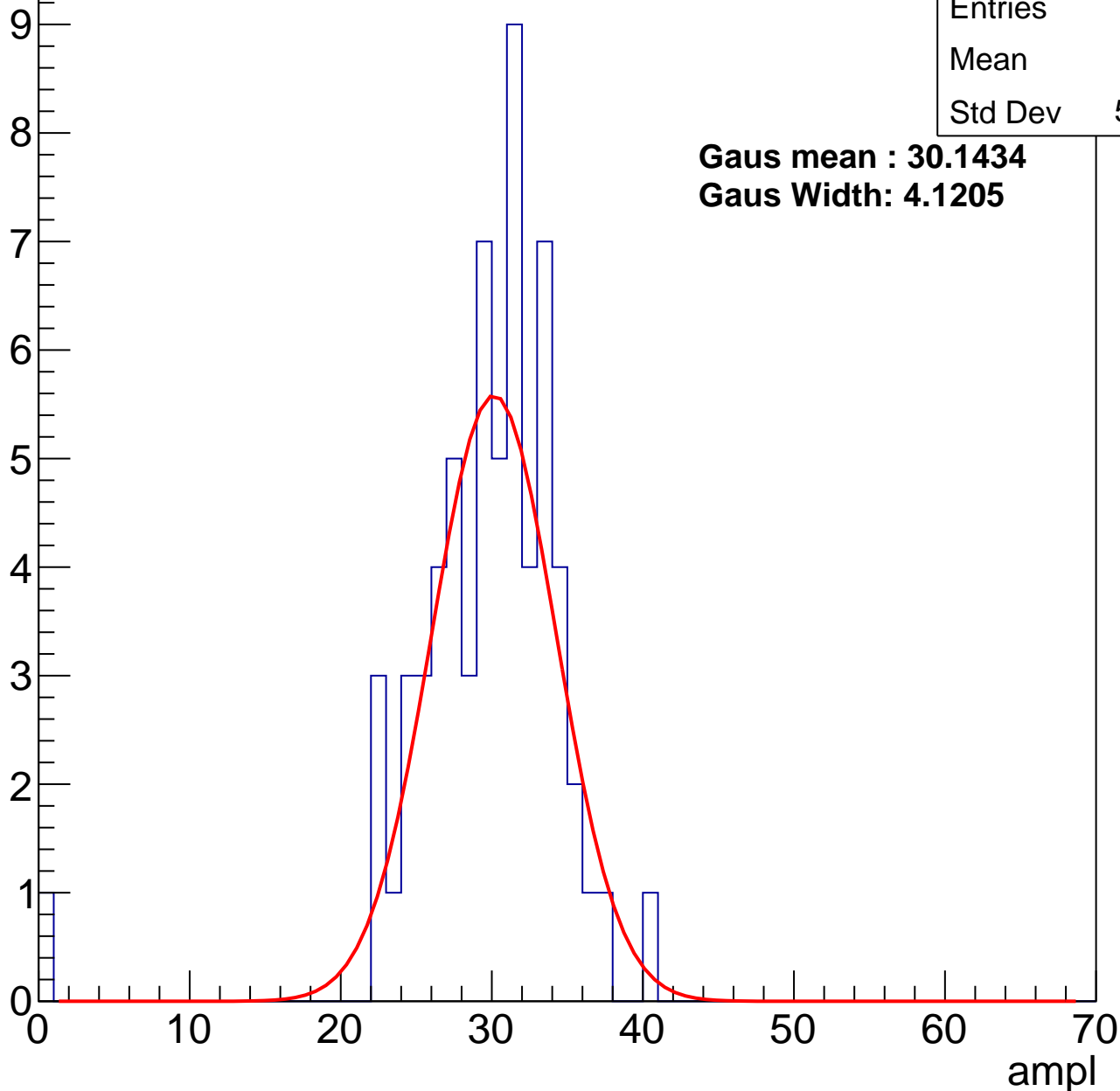
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.2
Std Dev	5.301

Gaus mean : 30.1434

Gaus Width: 4.1205



B1L102S, U13-ch50, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	97
Mean	37.8
Std Dev	4.007

Gaus mean : 38.3730

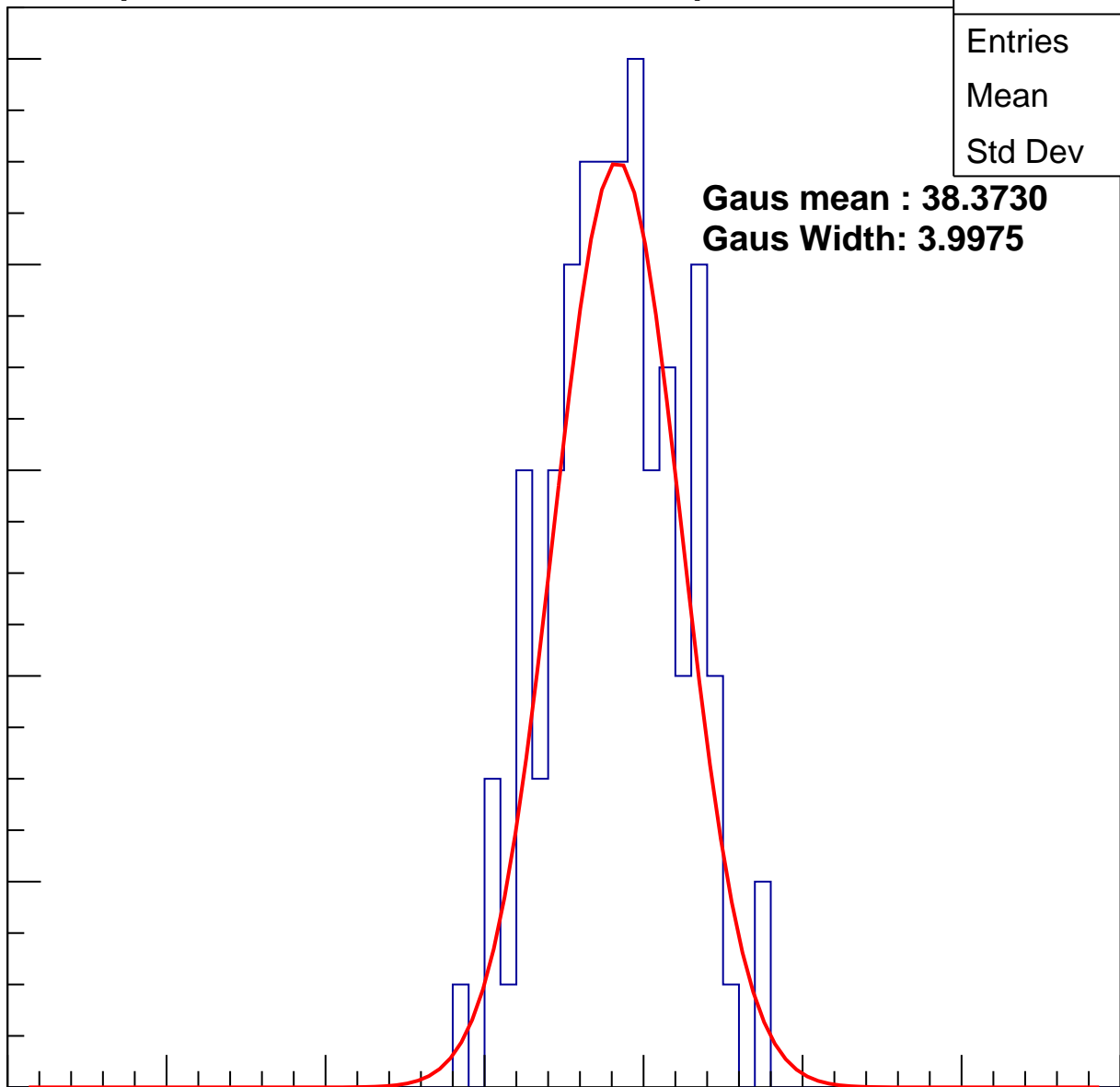
Gaus Width: 3.9975

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch50, adc2

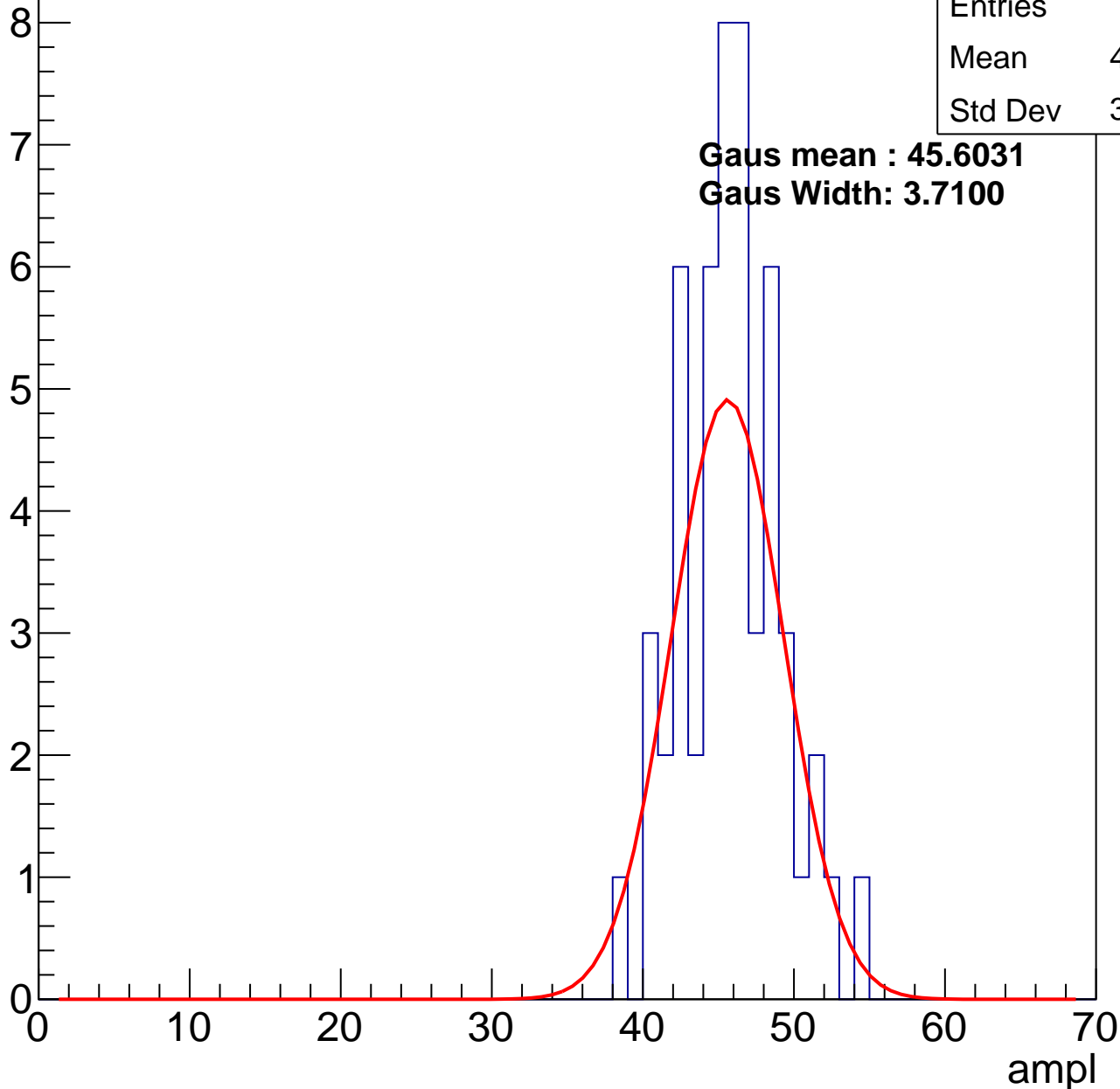
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	45.36
Std Dev	3.268

Gaus mean : 45.6031

Gaus Width: 3.7100

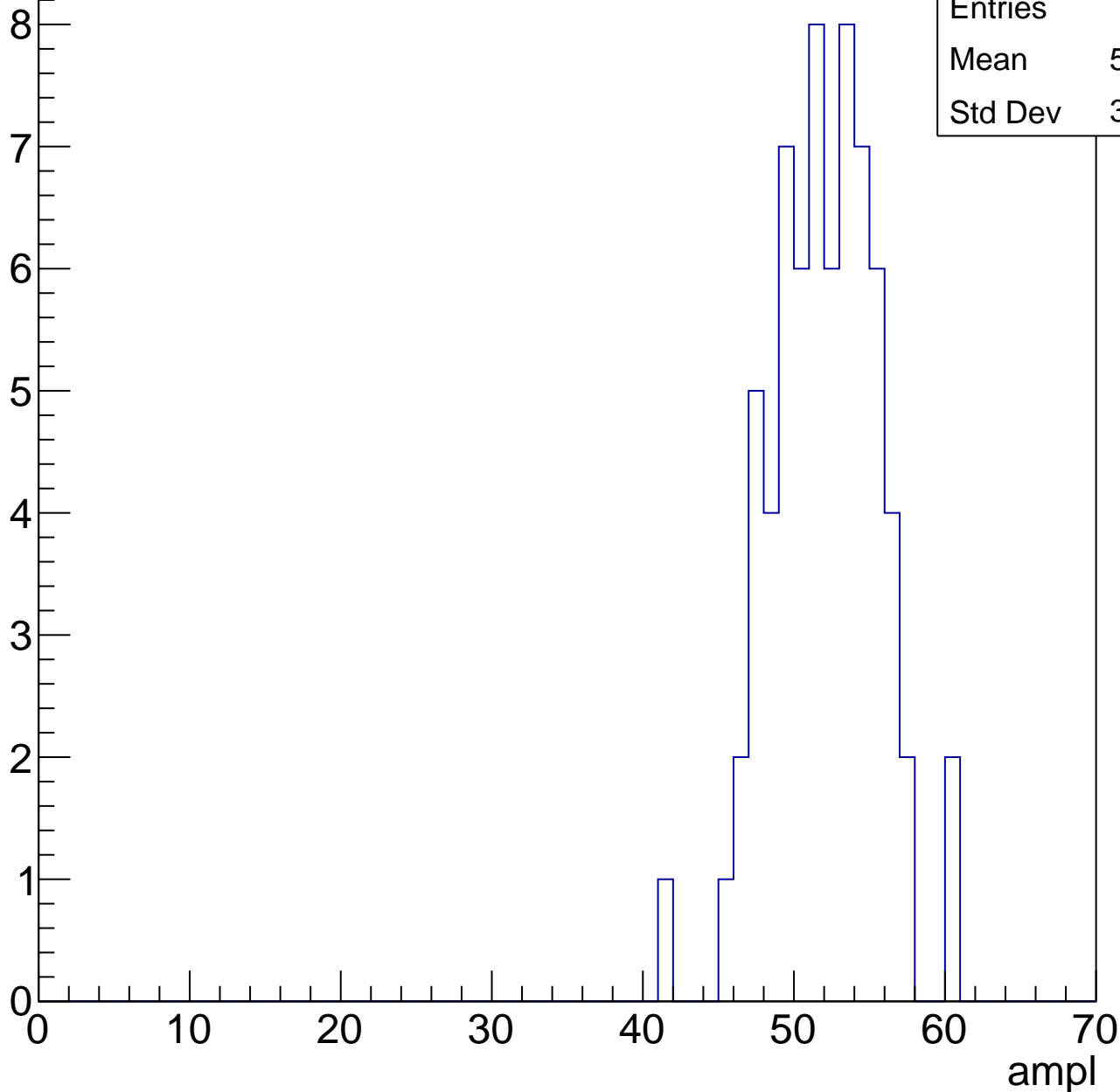


B1L102S, U13-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	51.57
Std Dev	3.499

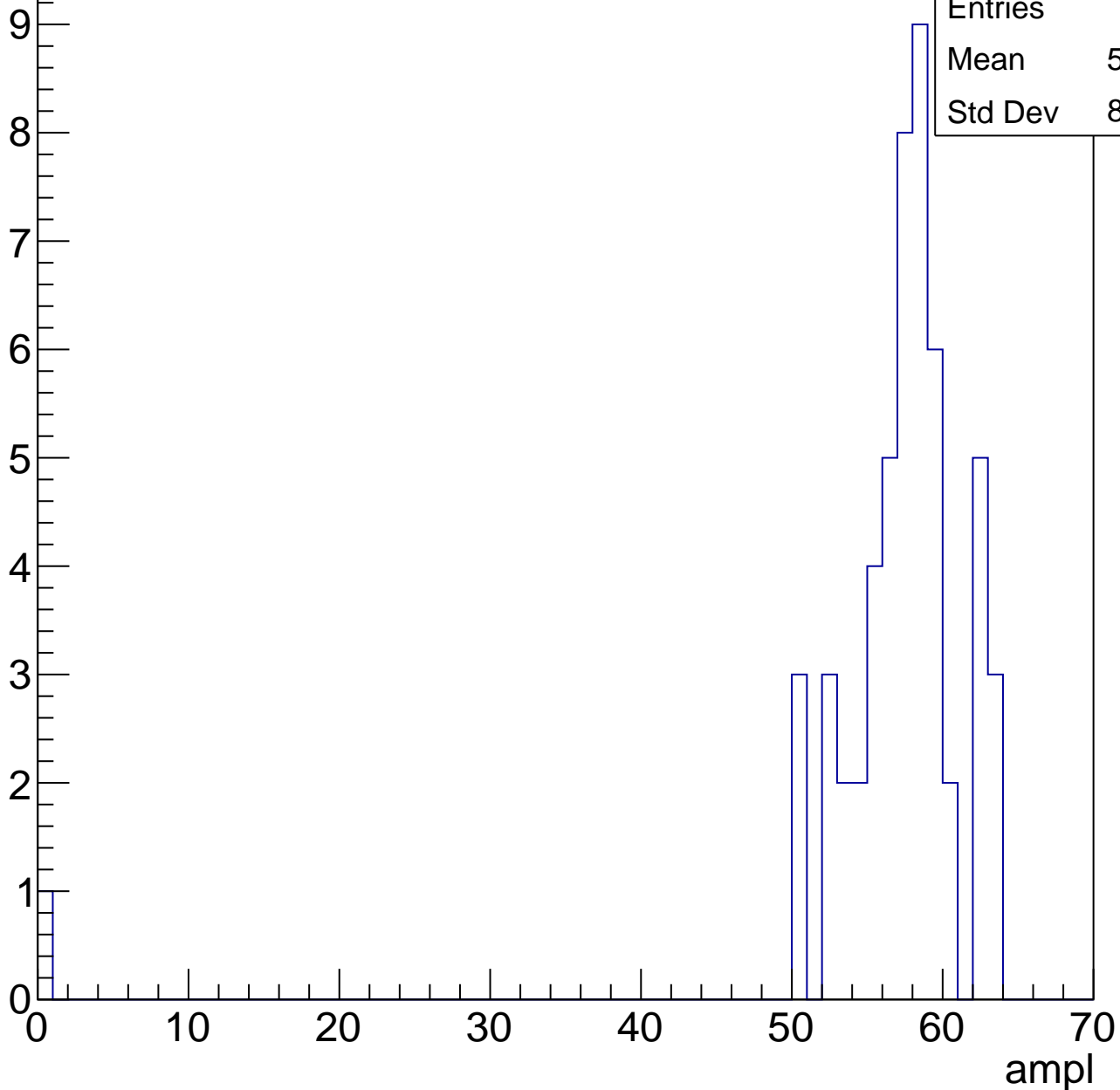


B1L102S, U13-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.06
Std Dev	8.444

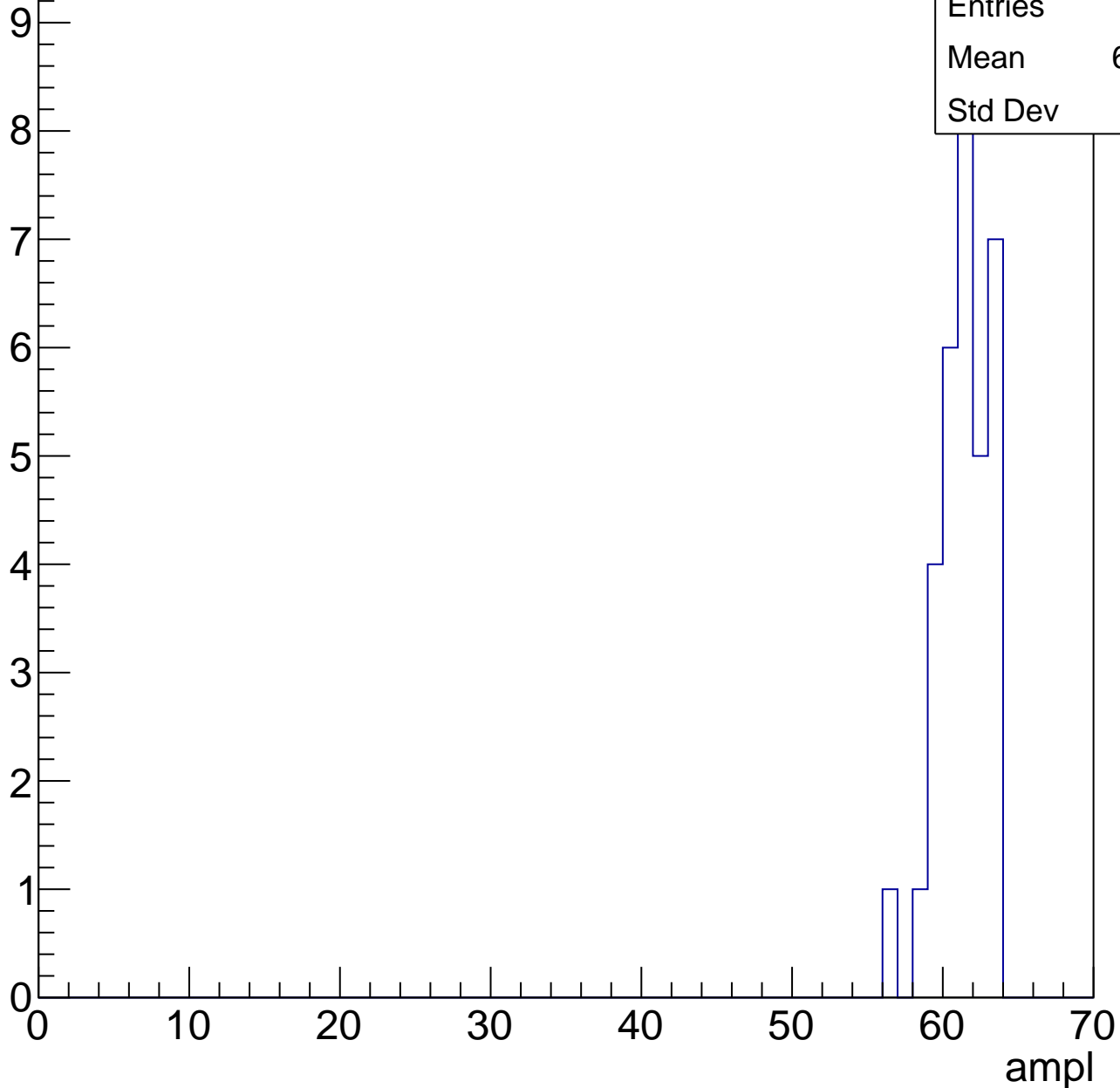


B1L102S, U13-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	60.91
Std Dev	1.64



B1L102S, U13-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch51, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	30.73
Std Dev	5.257

Gaus mean : 30.7668

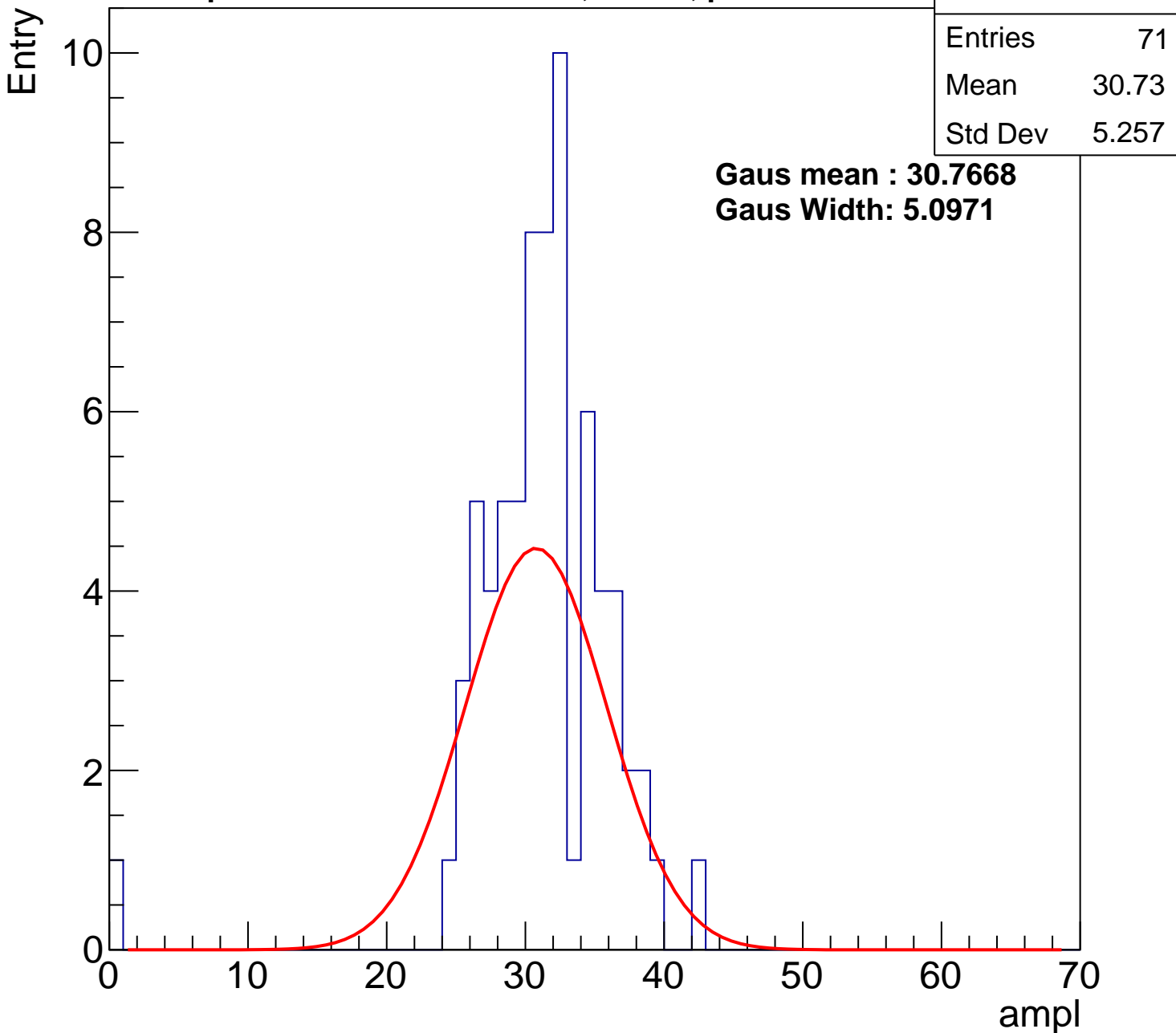
Gaus Width: 5.0971

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch51, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	38.15
Std Dev	3.756

Gaus mean : 38.6472

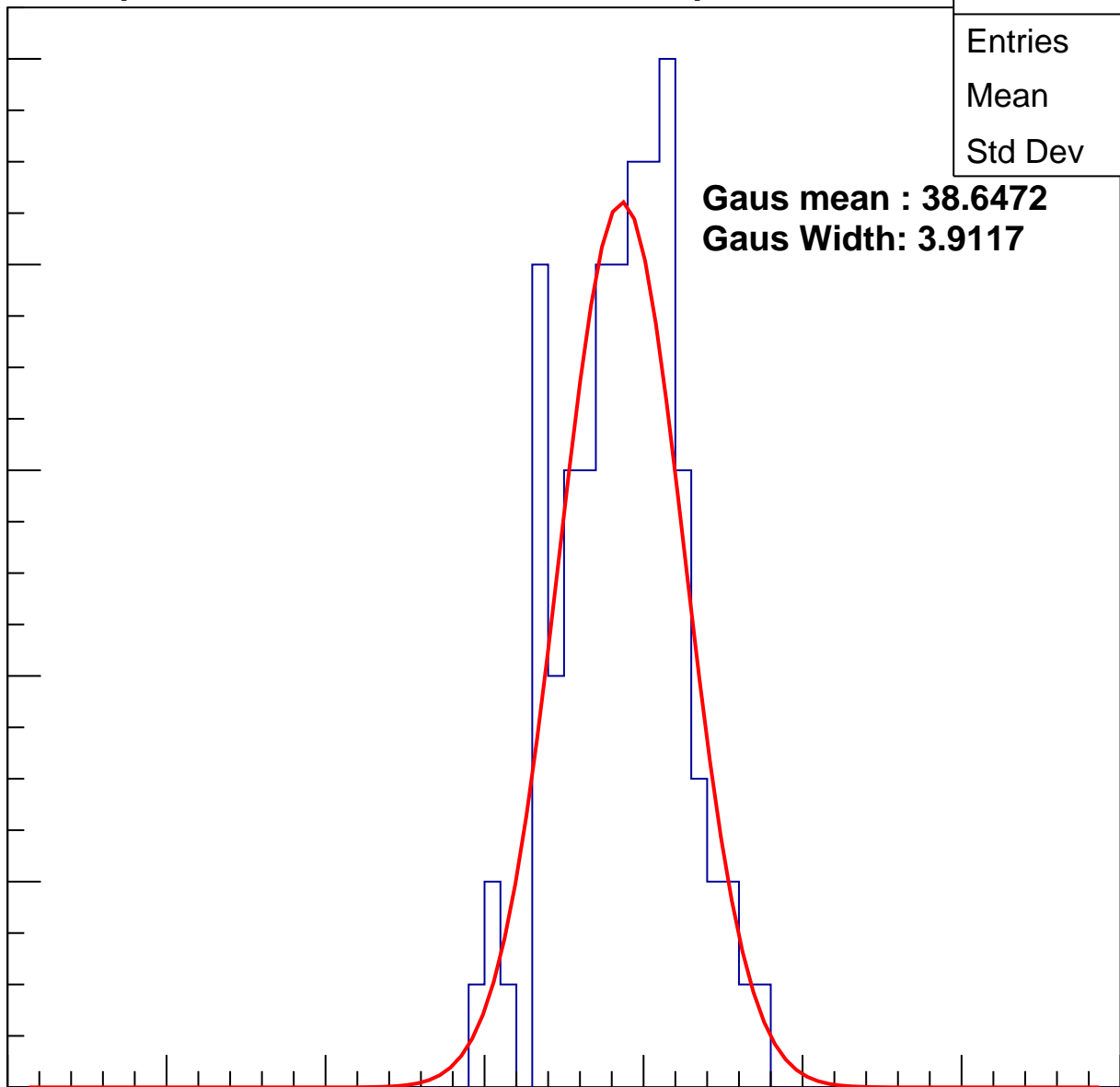
Gaus Width: 3.9117

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch51, adc2

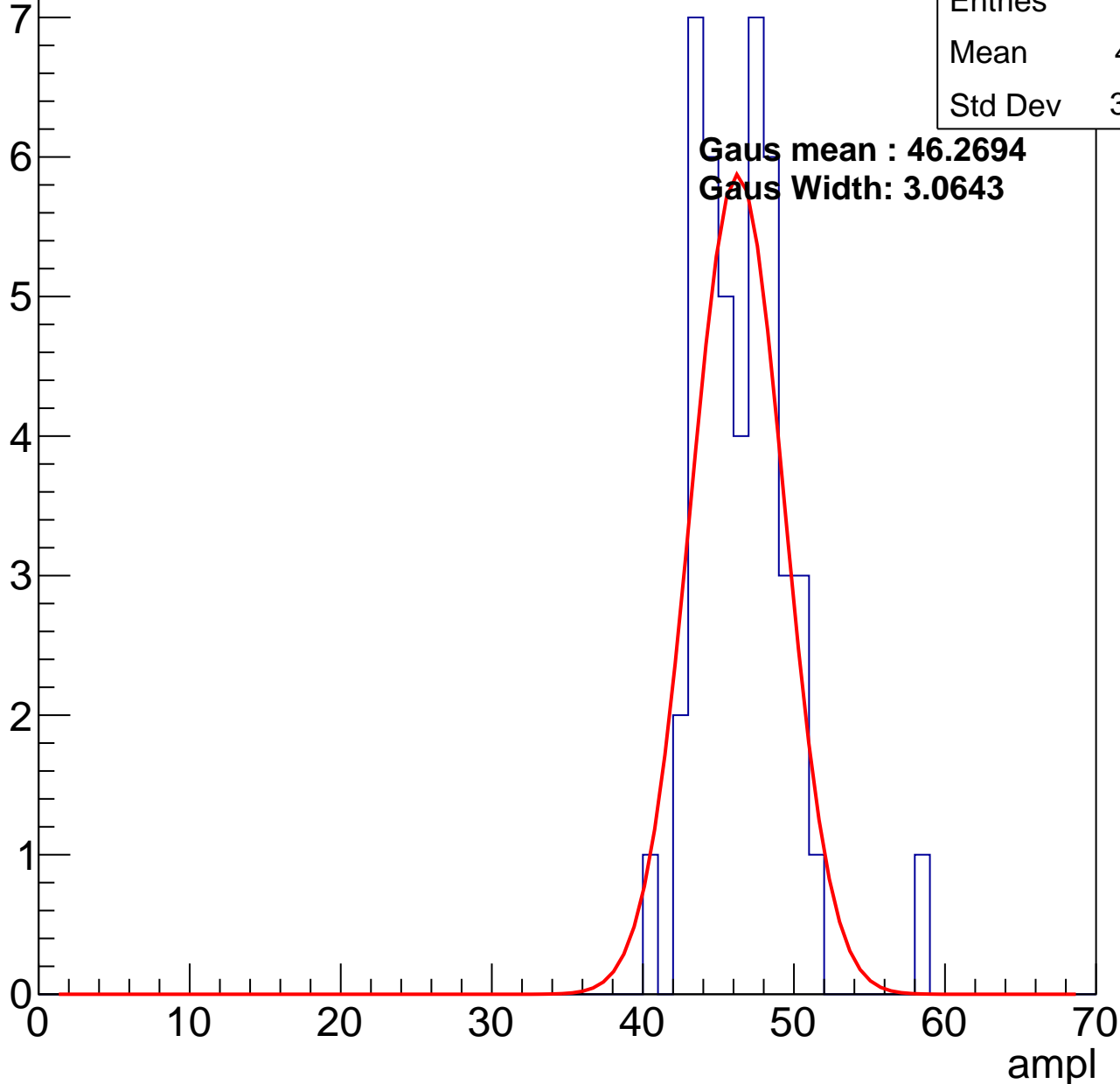
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	46.11
Std Dev	3.073

Gaus mean : 46.2694

Gaus Width: 3.0643

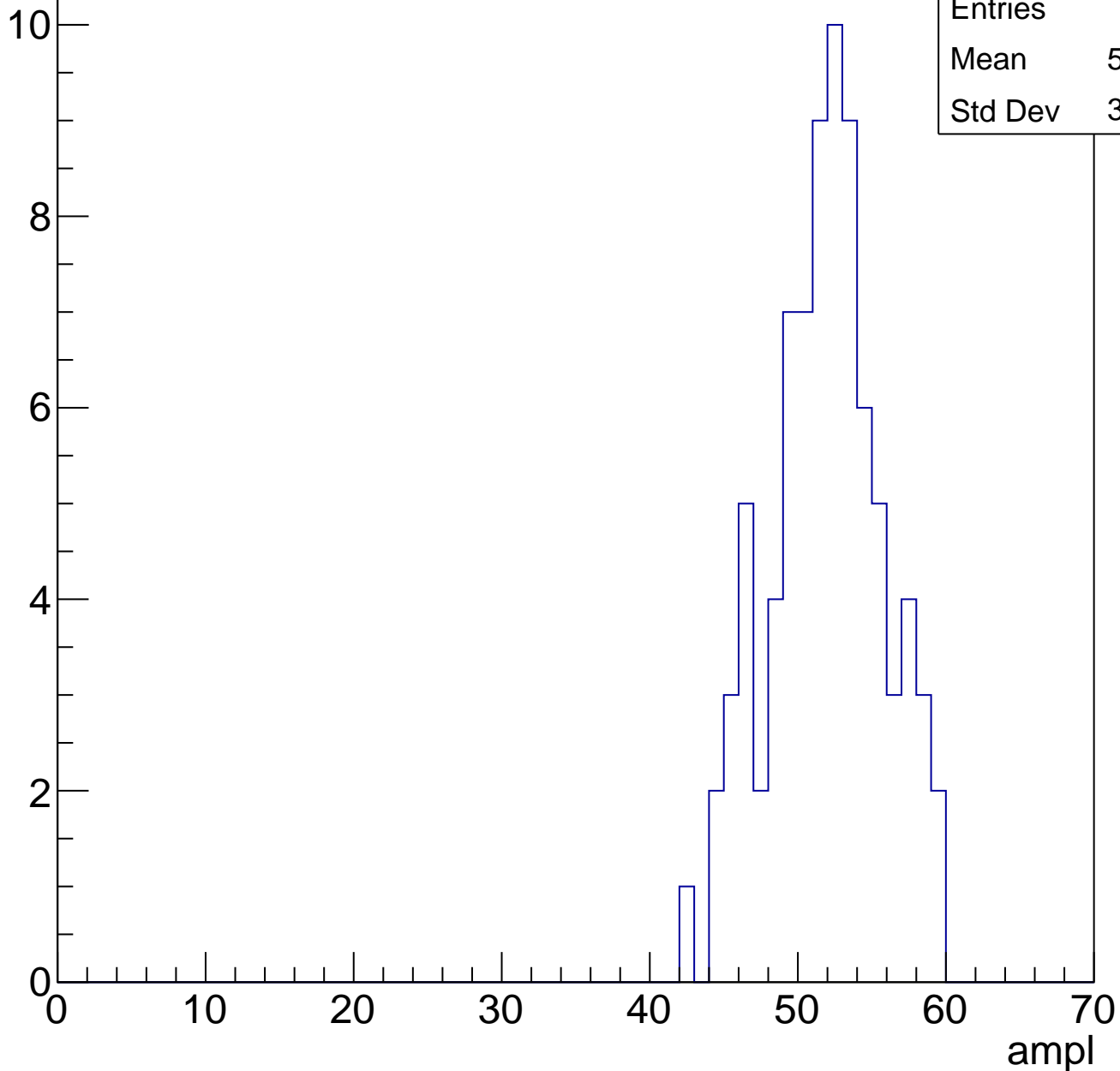


B1L102S, U13-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	51.43
Std Dev	3.784

Entry

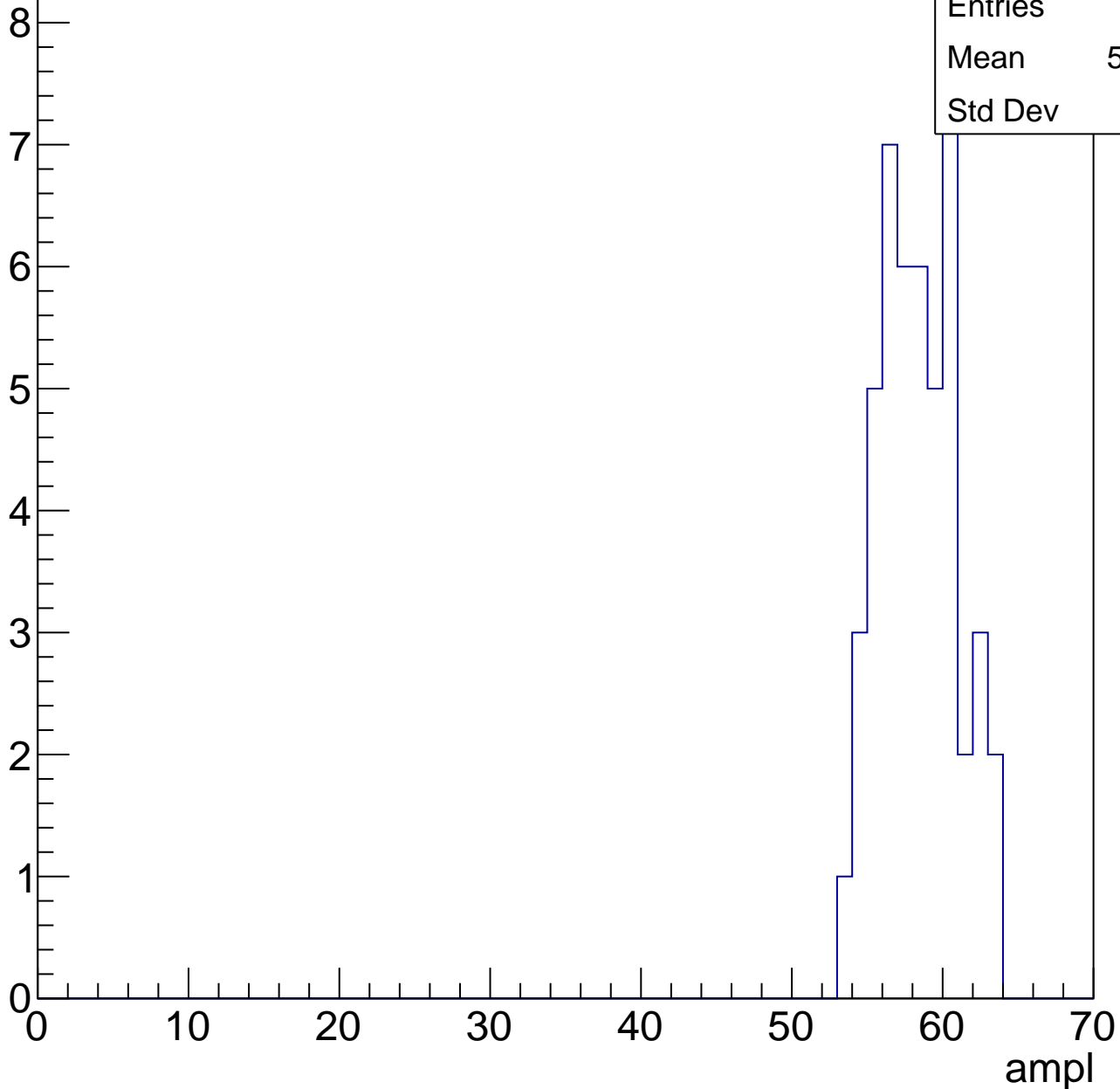


B1L102S, U13-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	57.94
Std Dev	2.52

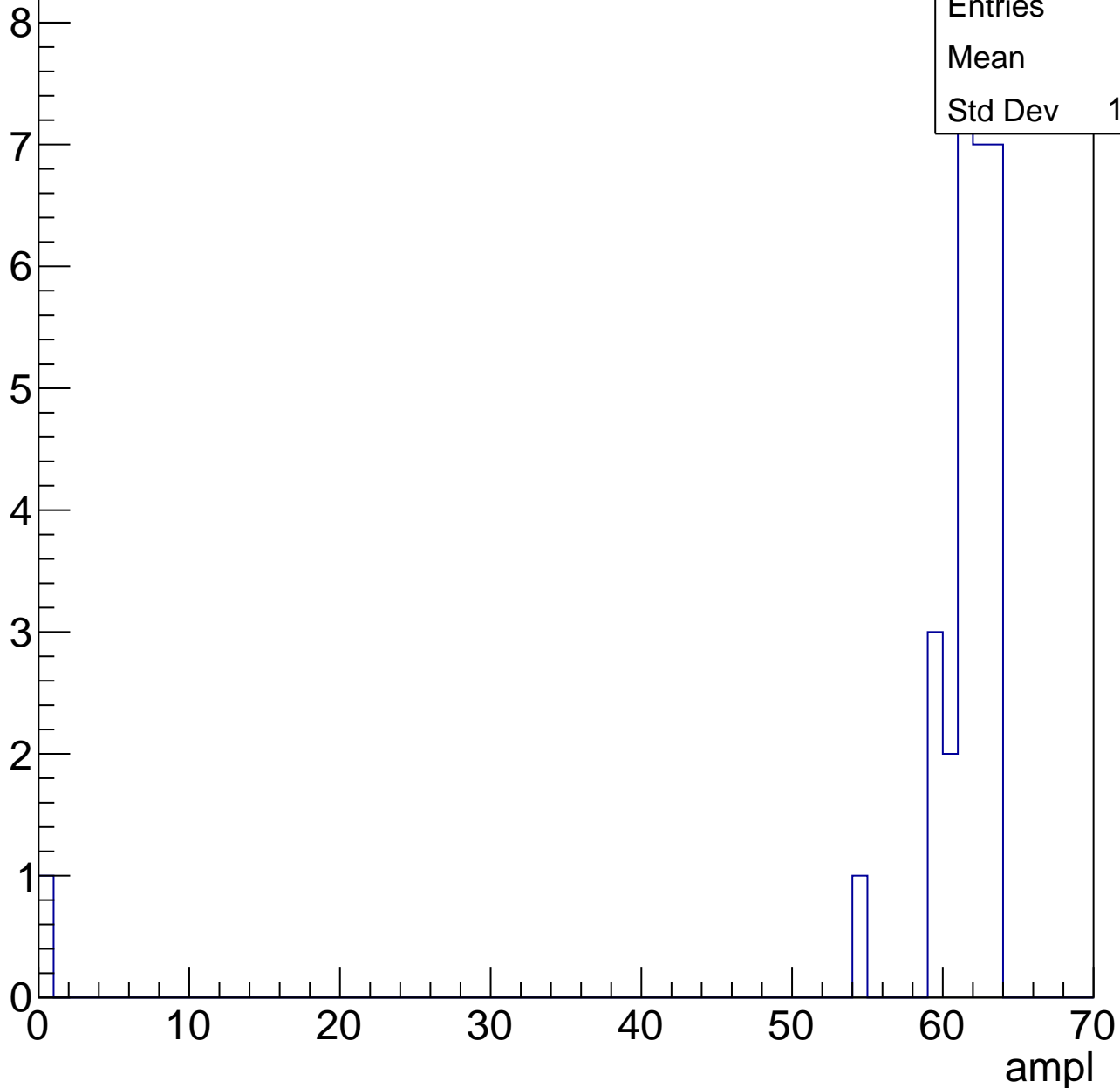


B1L102S, U13-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	29
Mean	59.1
Std Dev	11.32



B1L102S, U13-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch52, adc0

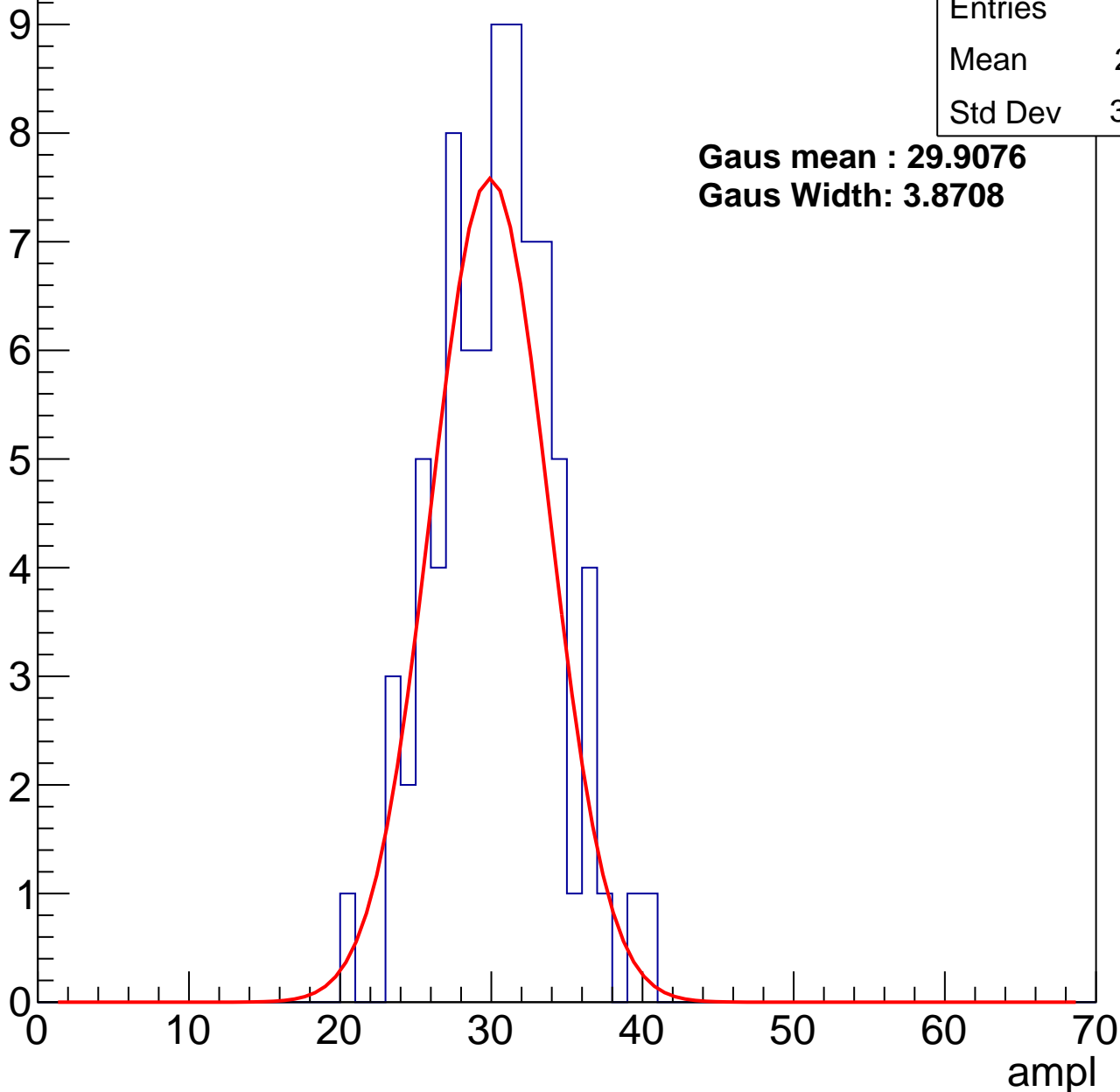
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.91
Std Dev	3.854

Gaus mean : 29.9076

Gaus Width: 3.8708



B1L102S, U13-ch52, adc1

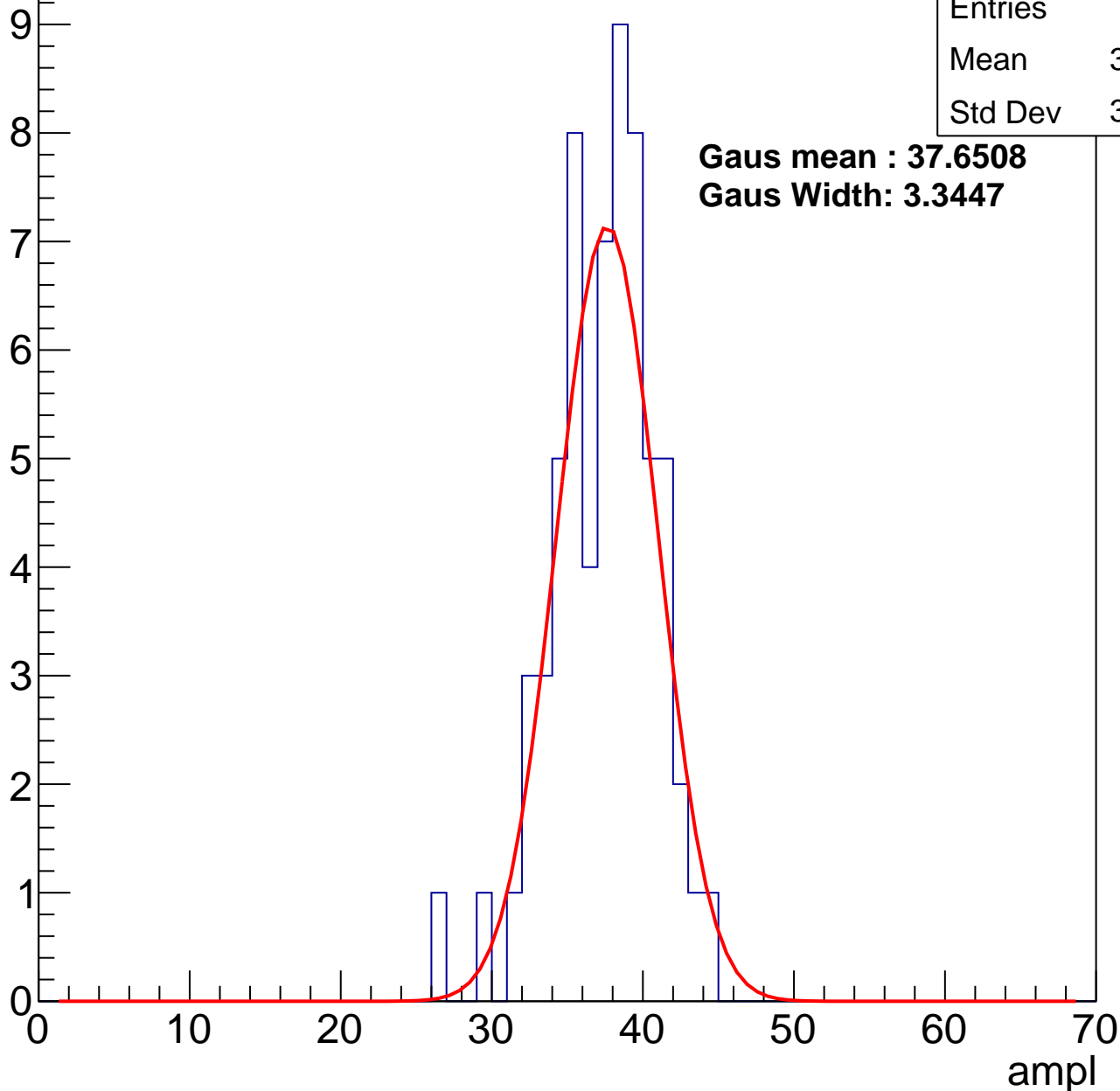
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.94
Std Dev	3.367

Gaus mean : 37.6508

Gaus Width: 3.3447



B1L102S, U13-ch52, adc2

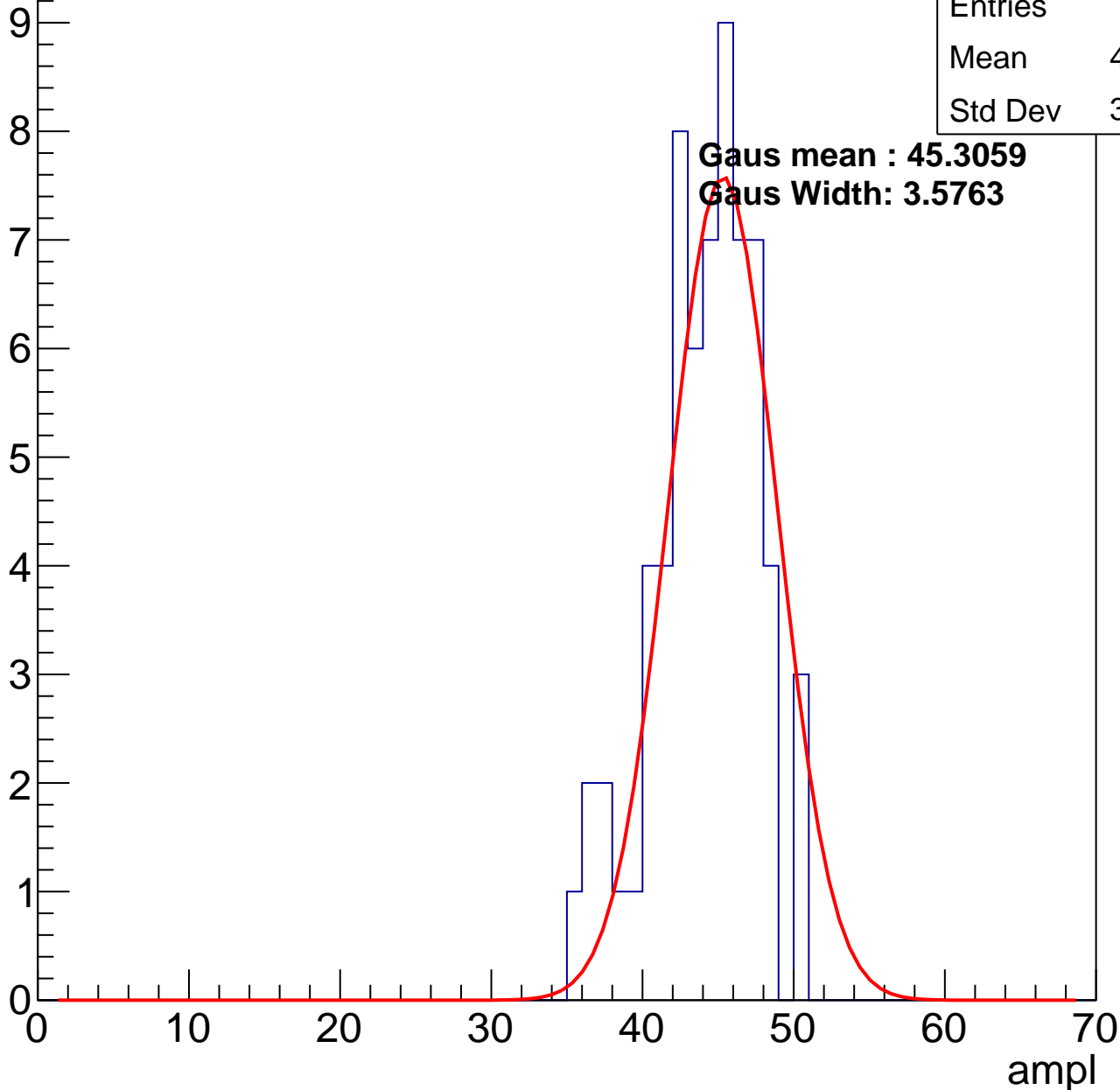
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.67
Std Dev	3.417

Gaus mean : 45.3059

Gaus Width: 3.5763

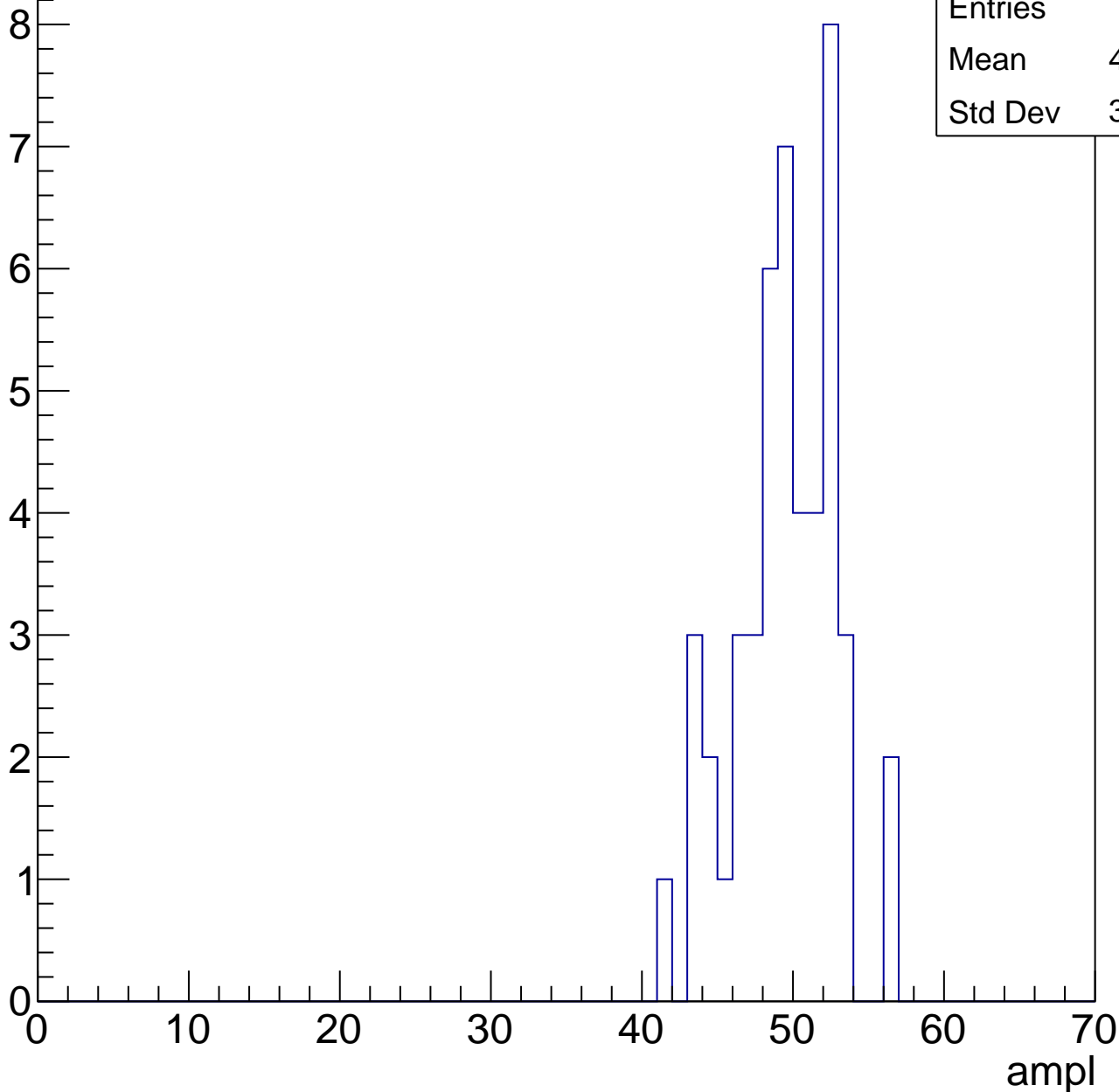


B1L102S, U13-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	49.02
Std Dev	3.329

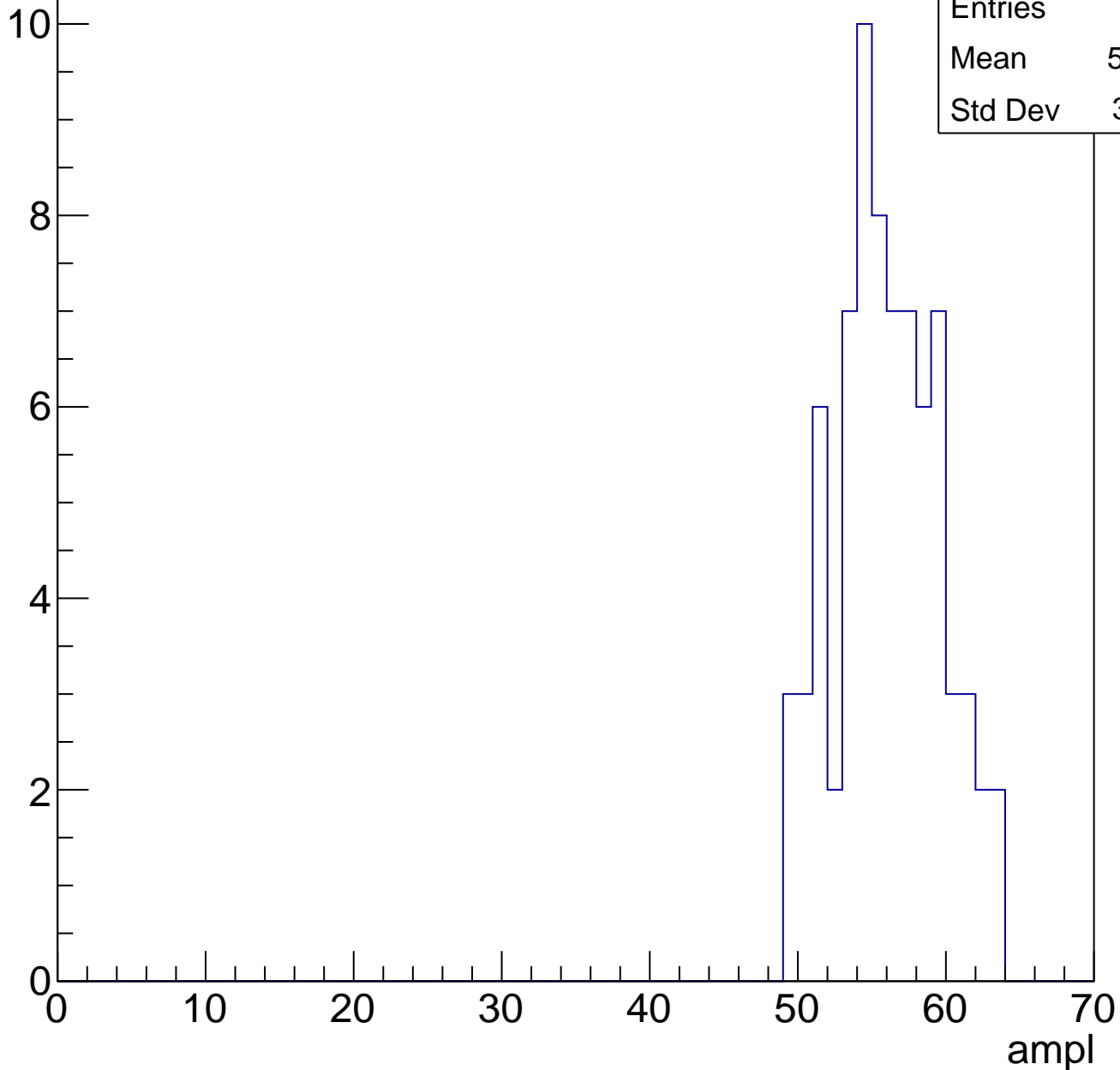


B1L102S, U13-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	55.57
Std Dev	3.481

Entry

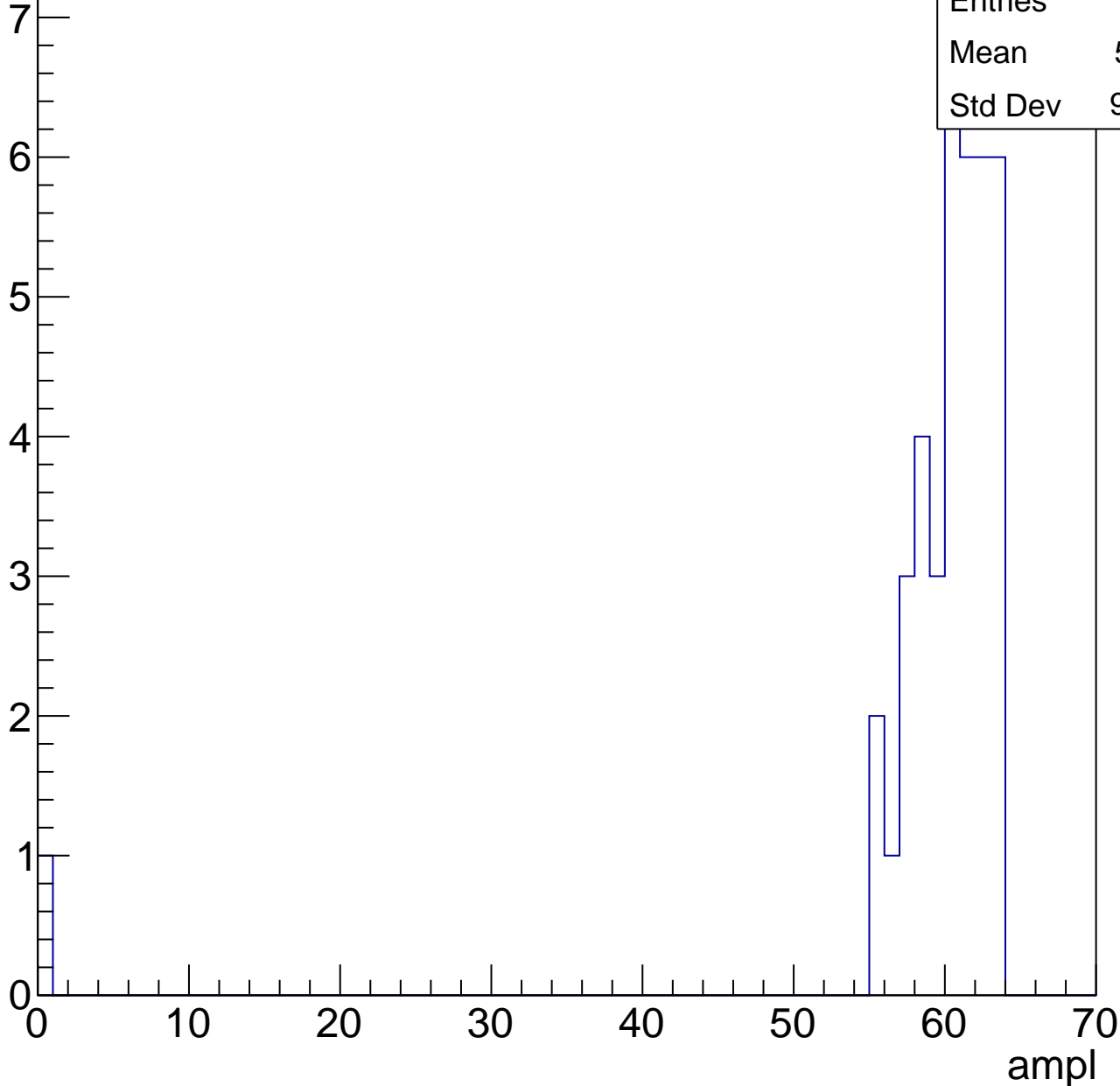


B1L102S, U13-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.51
Std Dev	9.753



B1L102S, U13-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U13-ch53, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	30.29
Std Dev	3.421

Gaus mean : 30.3373

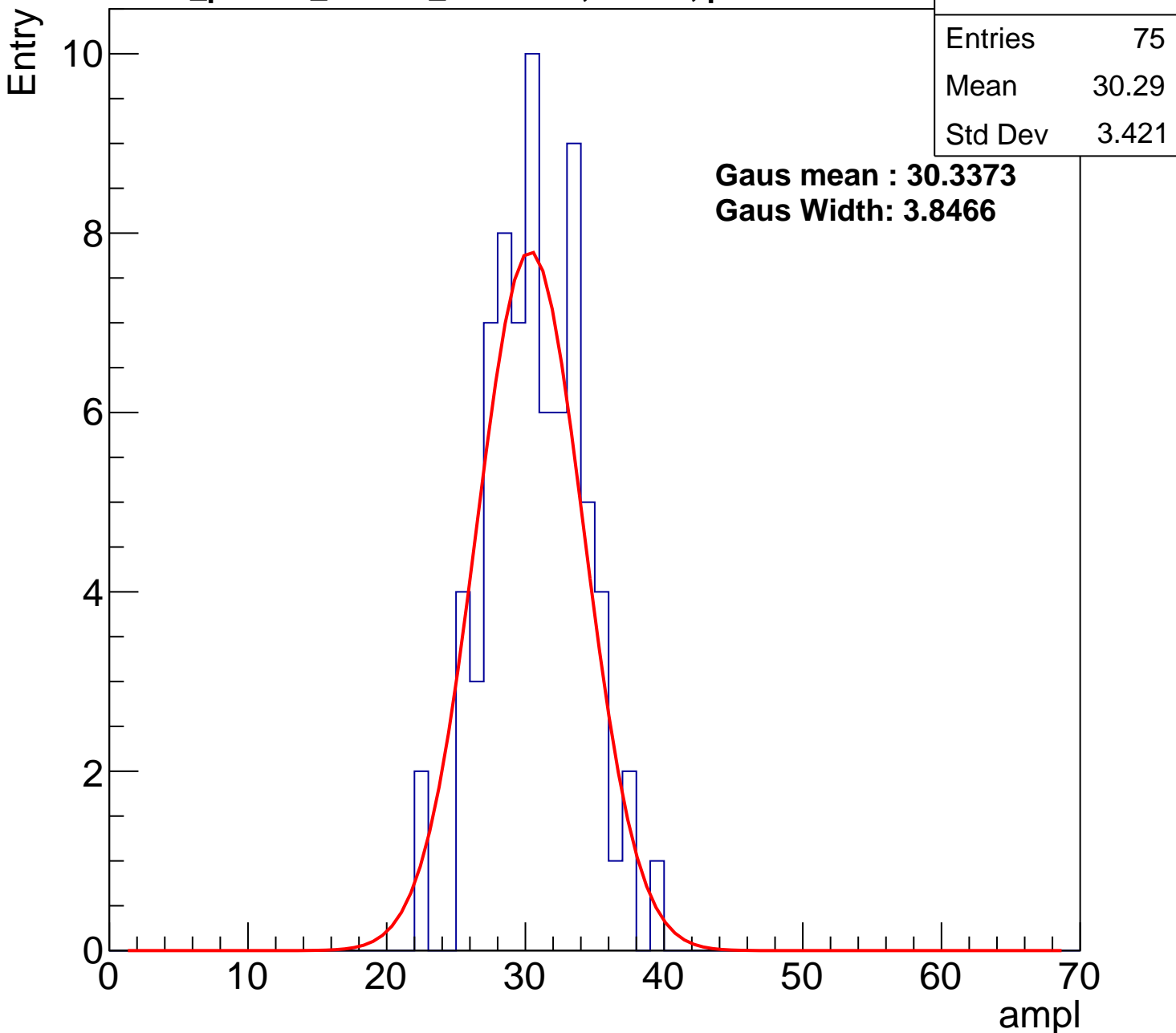
Gaus Width: 3.8466

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch53, adc1

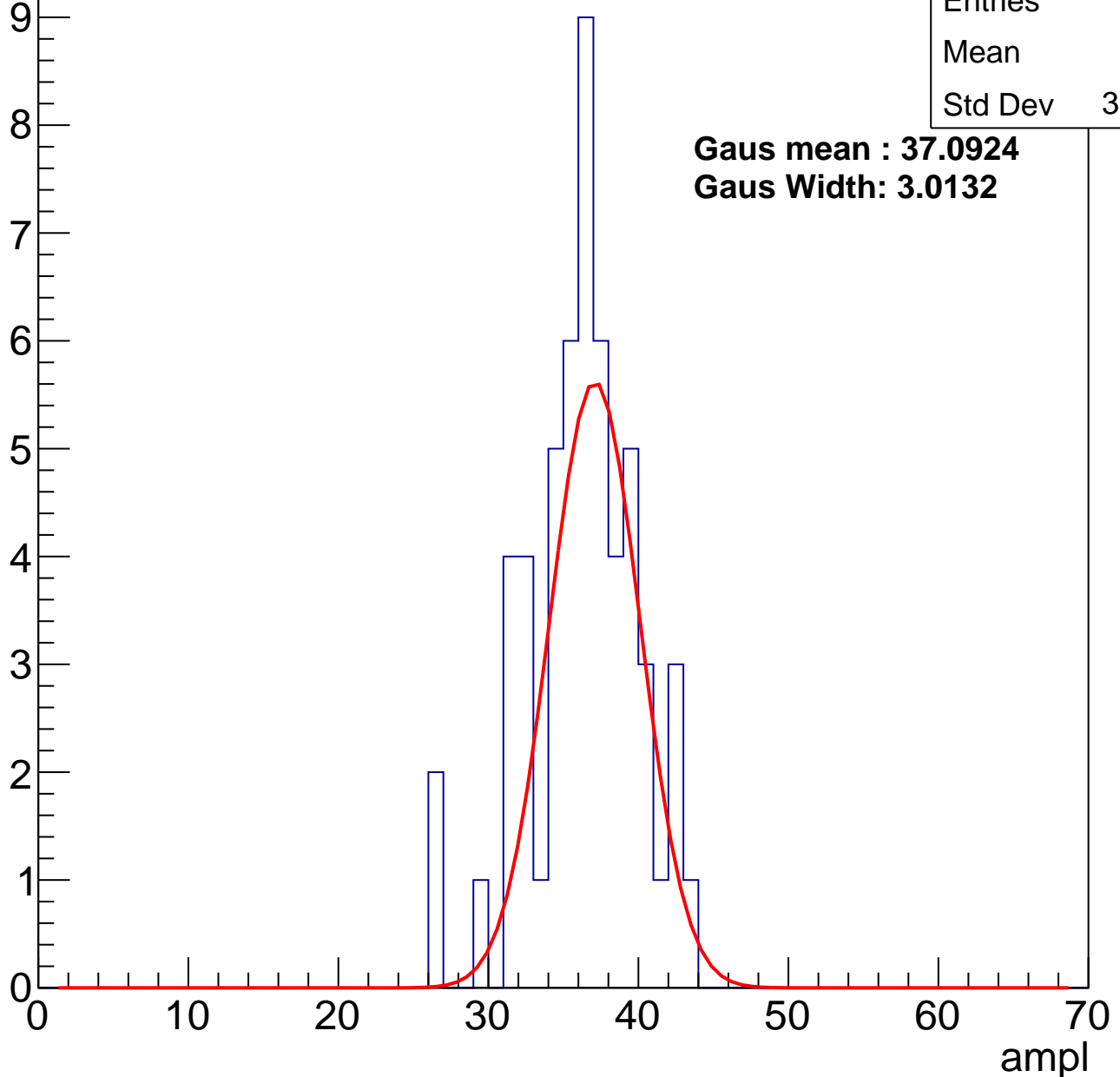
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	35.8
Std Dev	3.675

Gaus mean : 37.0924

Gaus Width: 3.0132



B1L102S, U13-ch53, adc2

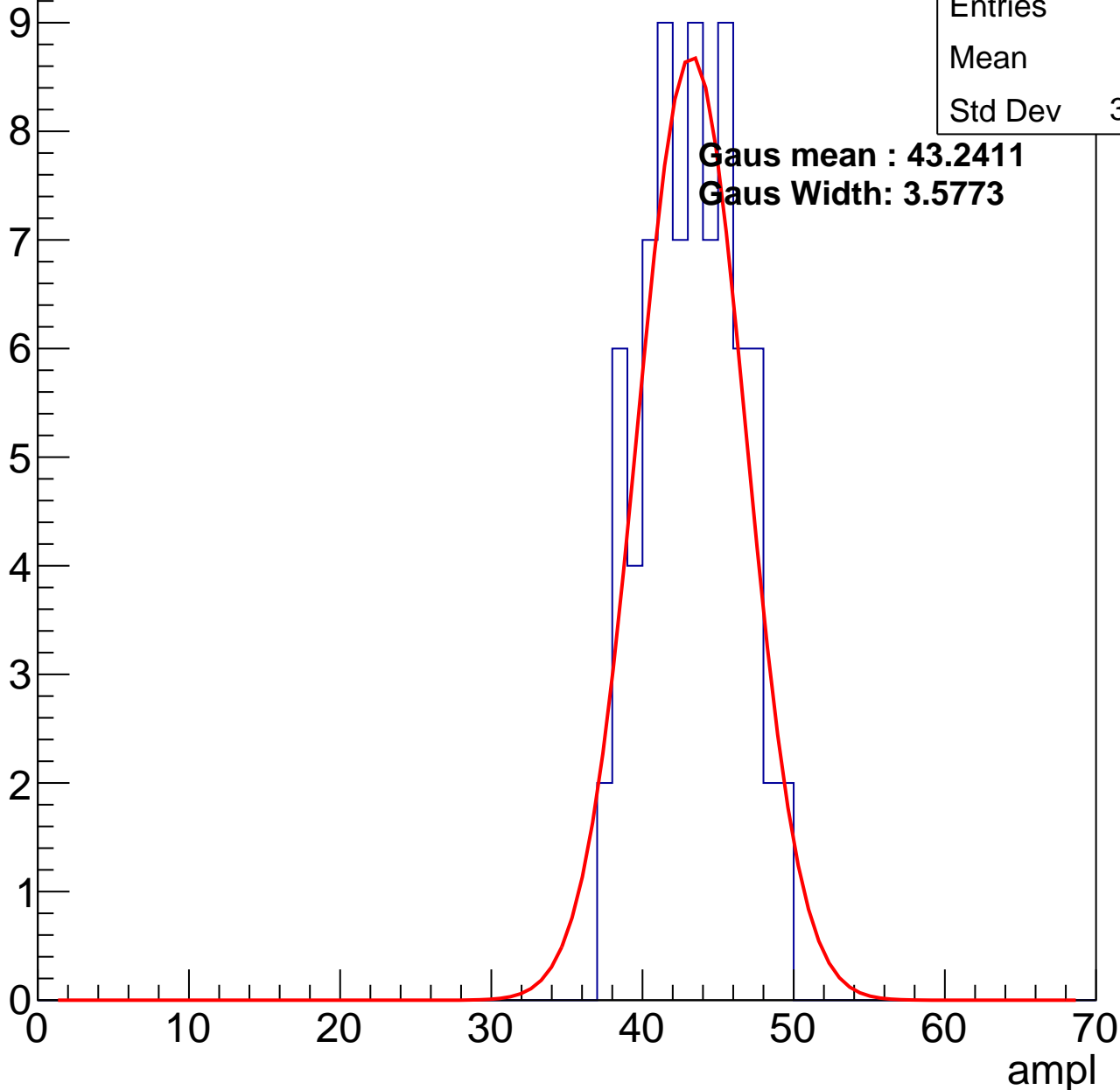
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.8
Std Dev	3.044

Gaus mean : 43.2411

Gaus Width: 3.5773

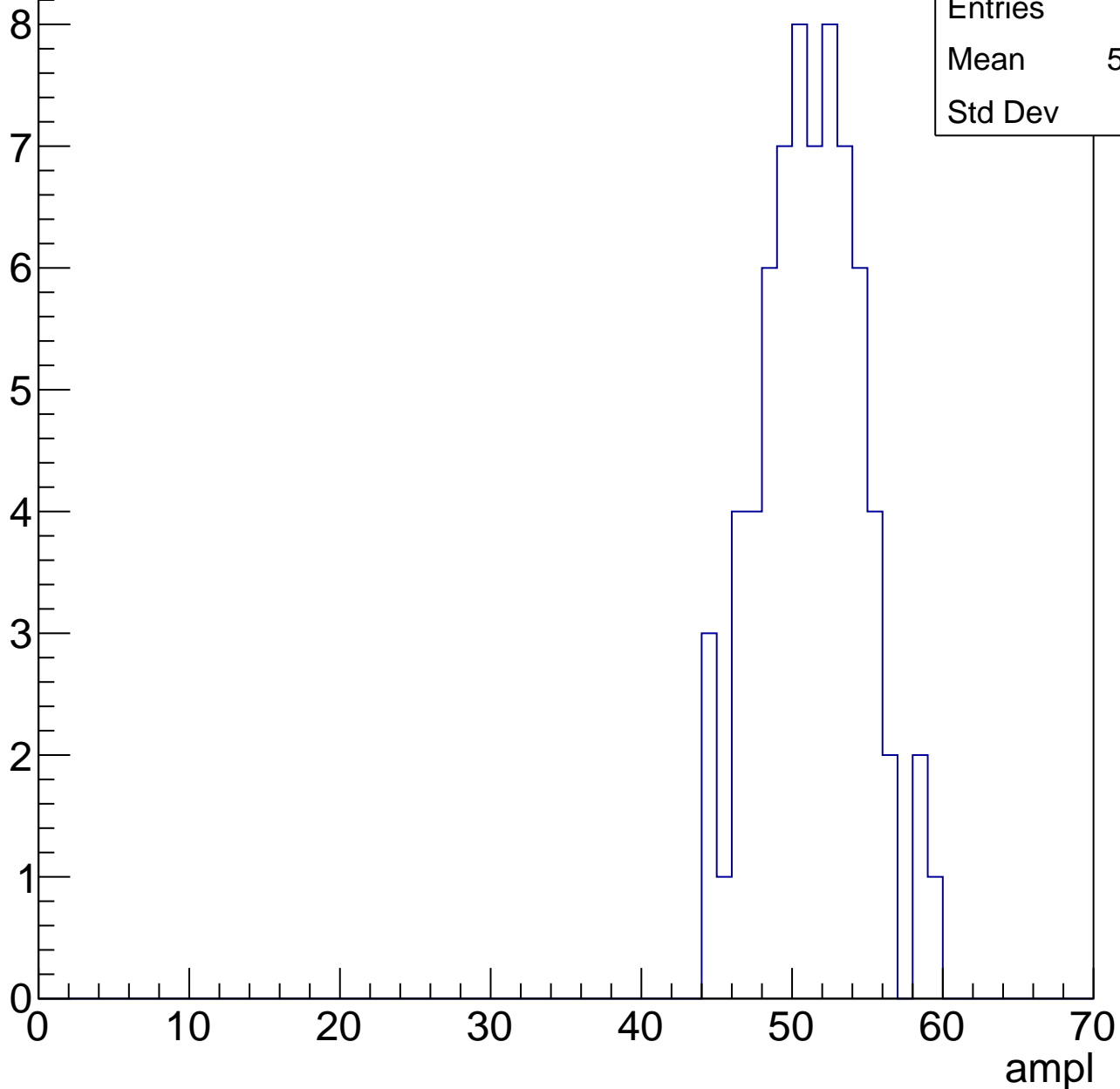


B1L102S, U13-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	50.79
Std Dev	3.38

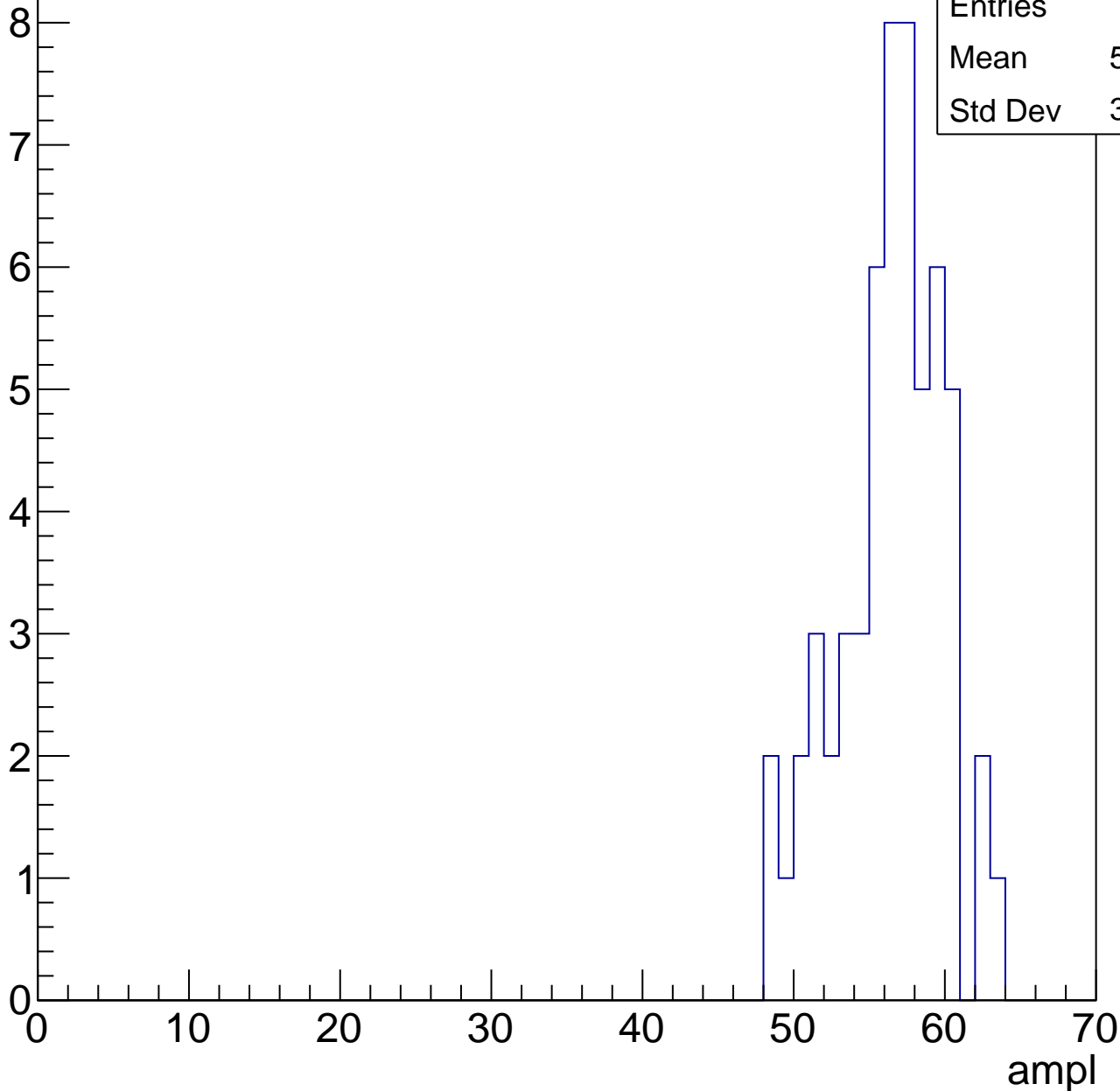


B1L102S, U13-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.93
Std Dev	3.463

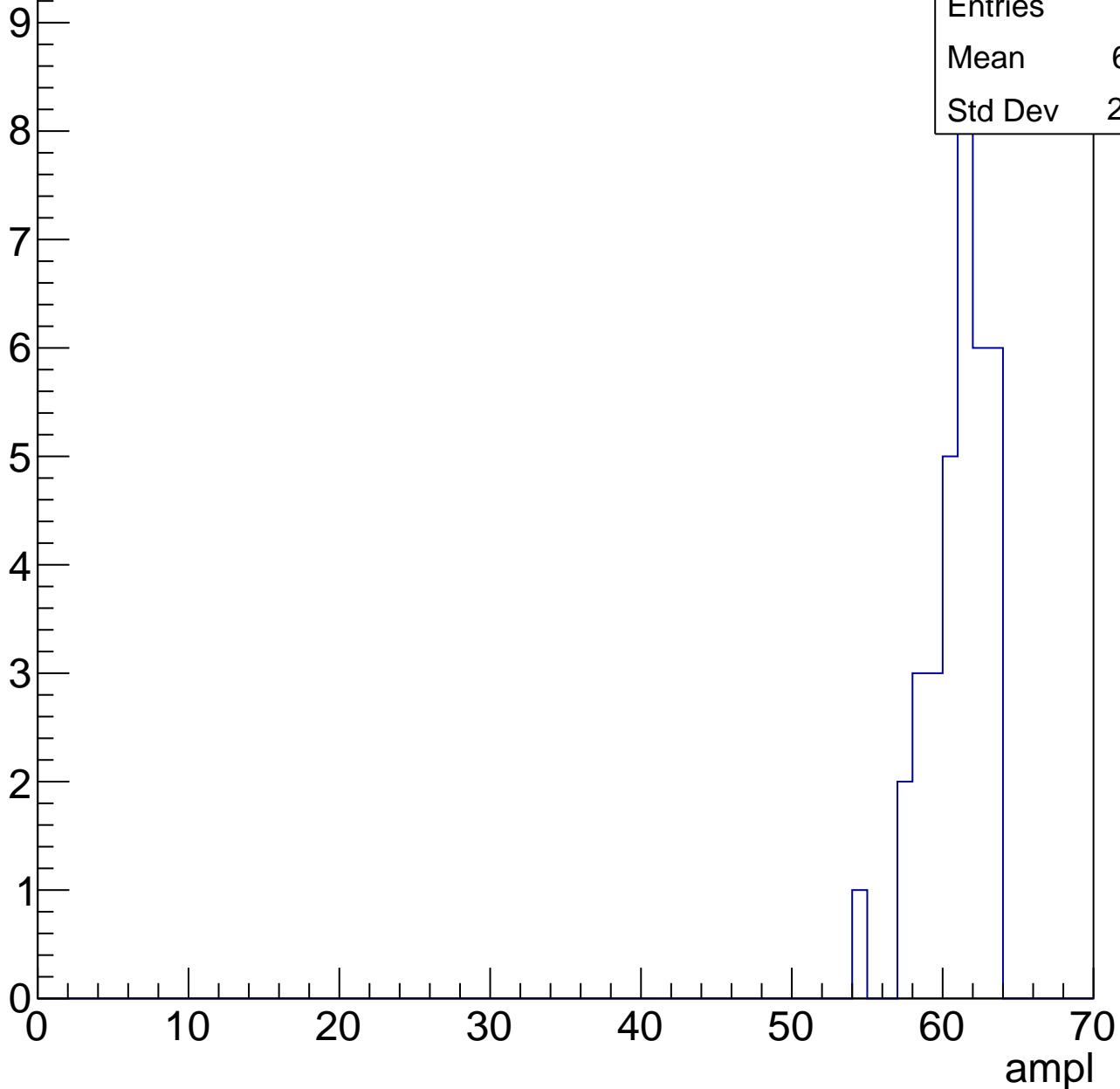


B1L102S, U13-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	60.51
Std Dev	2.048



B1L102S, U13-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	7
Mean	53.43
Std Dev	21.82

B1L102S, U13-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U13-ch54, adc0

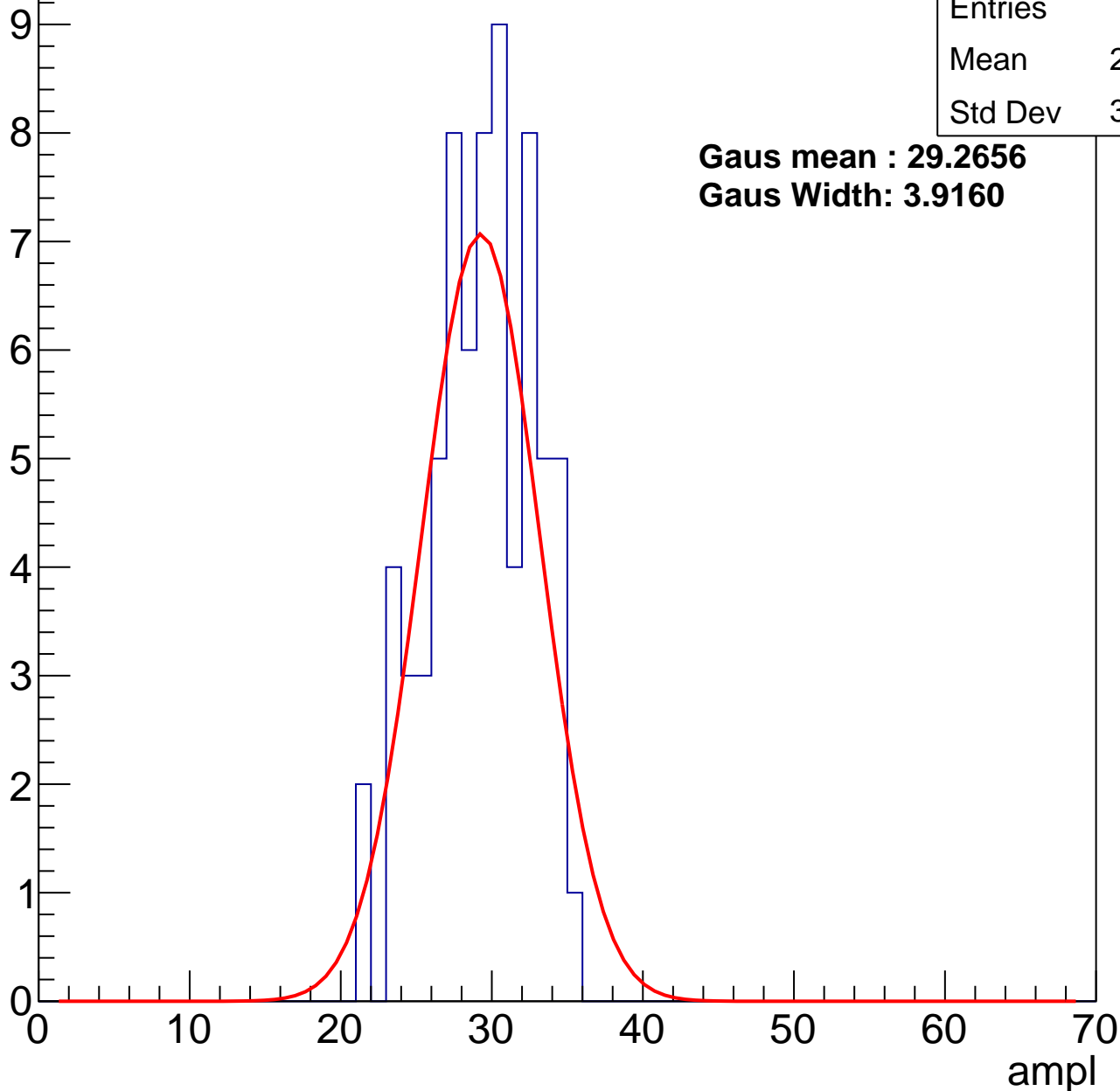
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.83
Std Dev	3.386

Gaus mean : 29.2656

Gaus Width: 3.9160



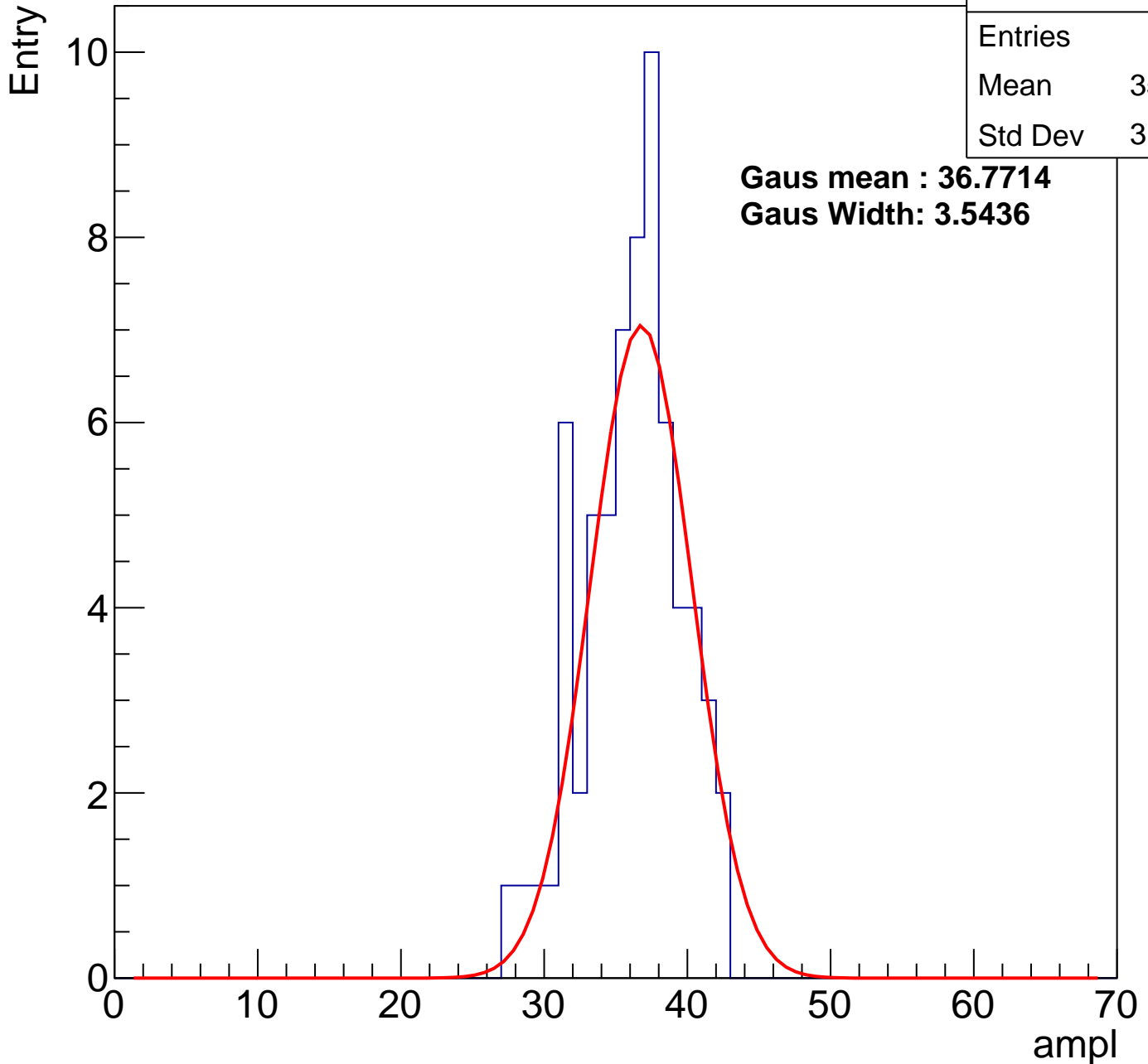
B1L102S, U13-ch54, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	35.65
Std Dev	3.387

Gaus mean : 36.7714

Gaus Width: 3.5436



B1L102S, U13-ch54, adc2

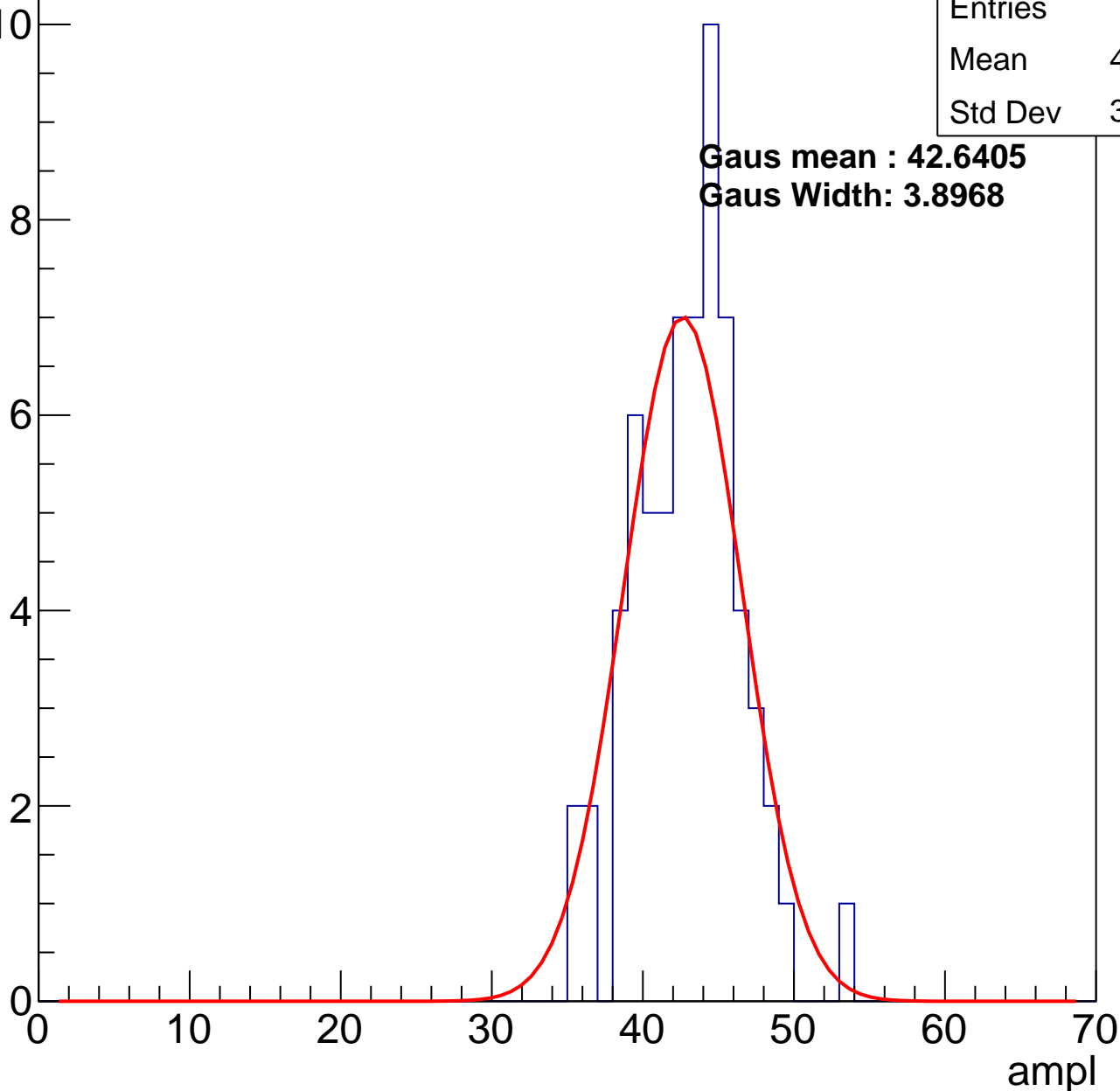
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.52
Std Dev	3.452

Gaus mean : 42.6405

Gaus Width: 3.8968

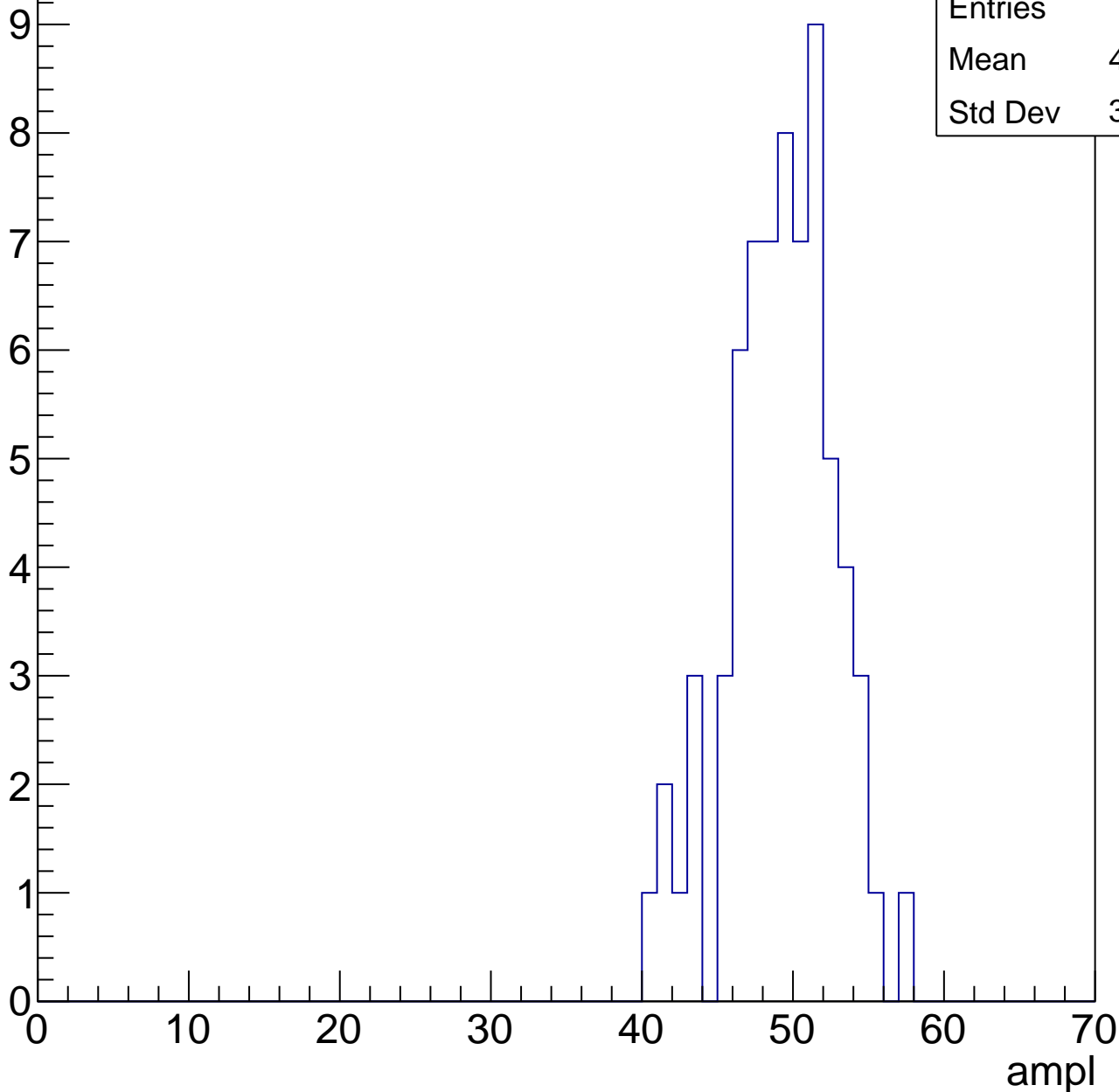


B1L102S, U13-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.76
Std Dev	3.486



B1L102S, U13-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	55.37
Std Dev	2.94

Entry

10

8

6

4

2

0

0

10

20

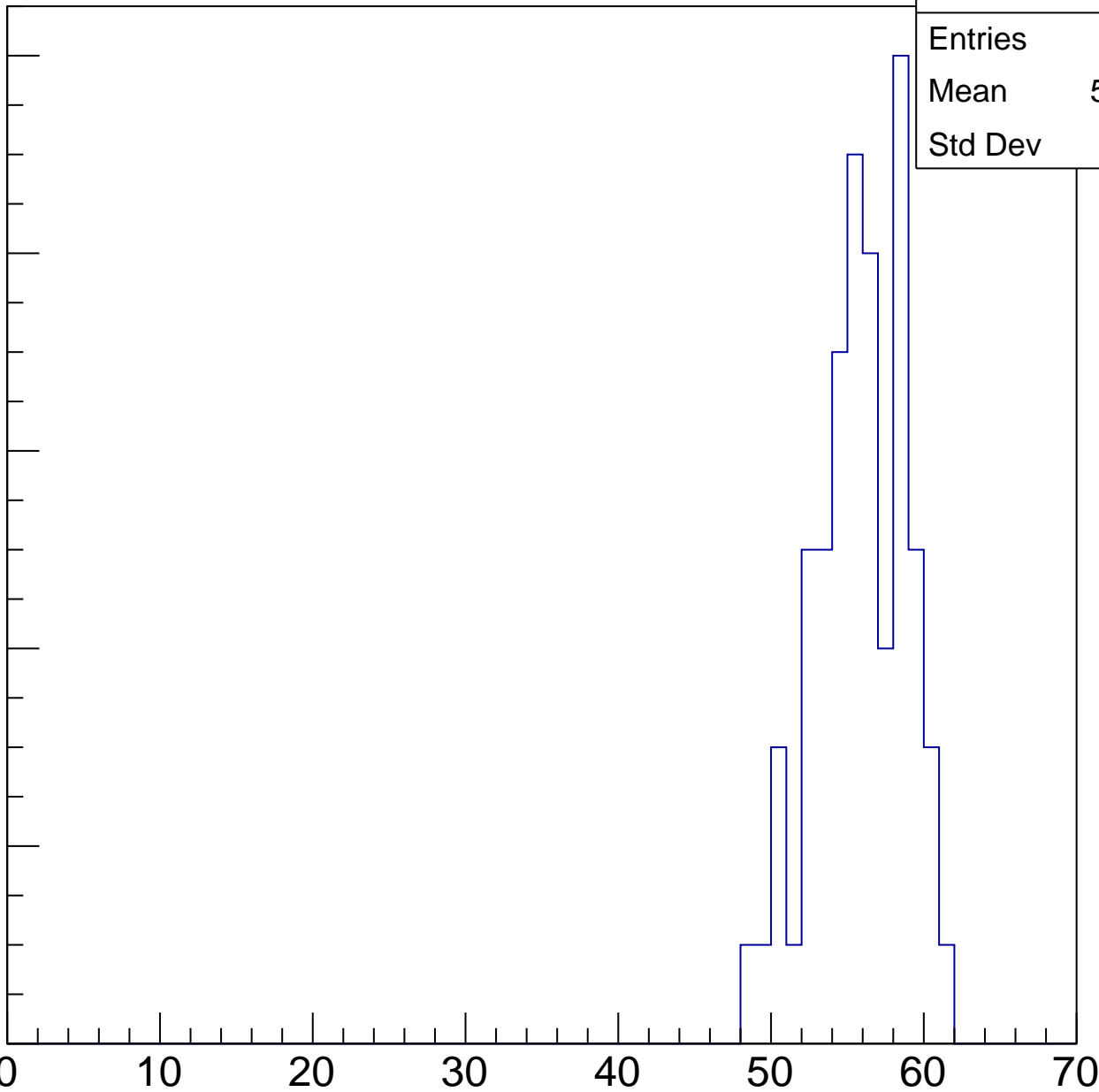
30

40

50

60

ampl

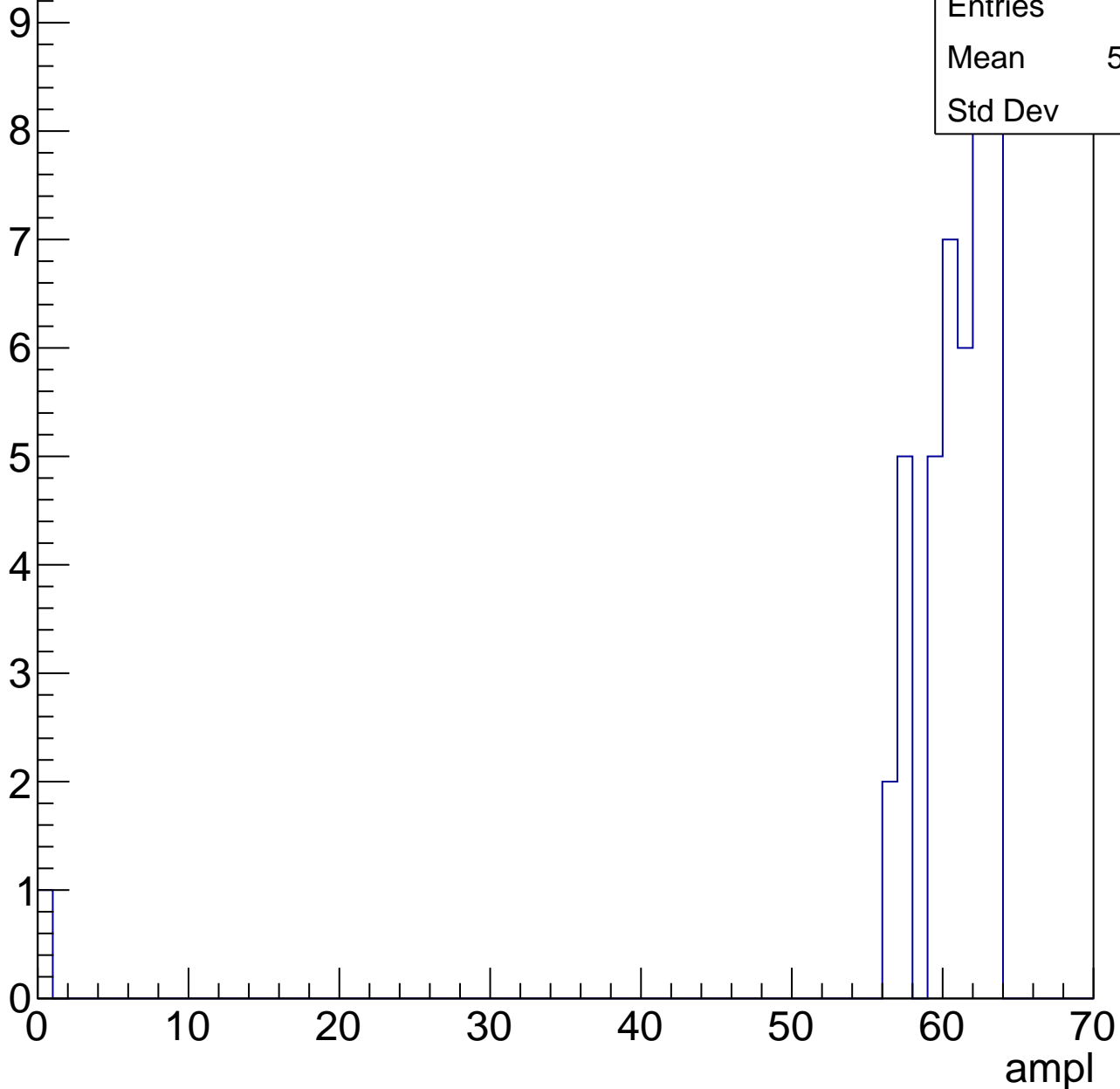


B1L102S, U13-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

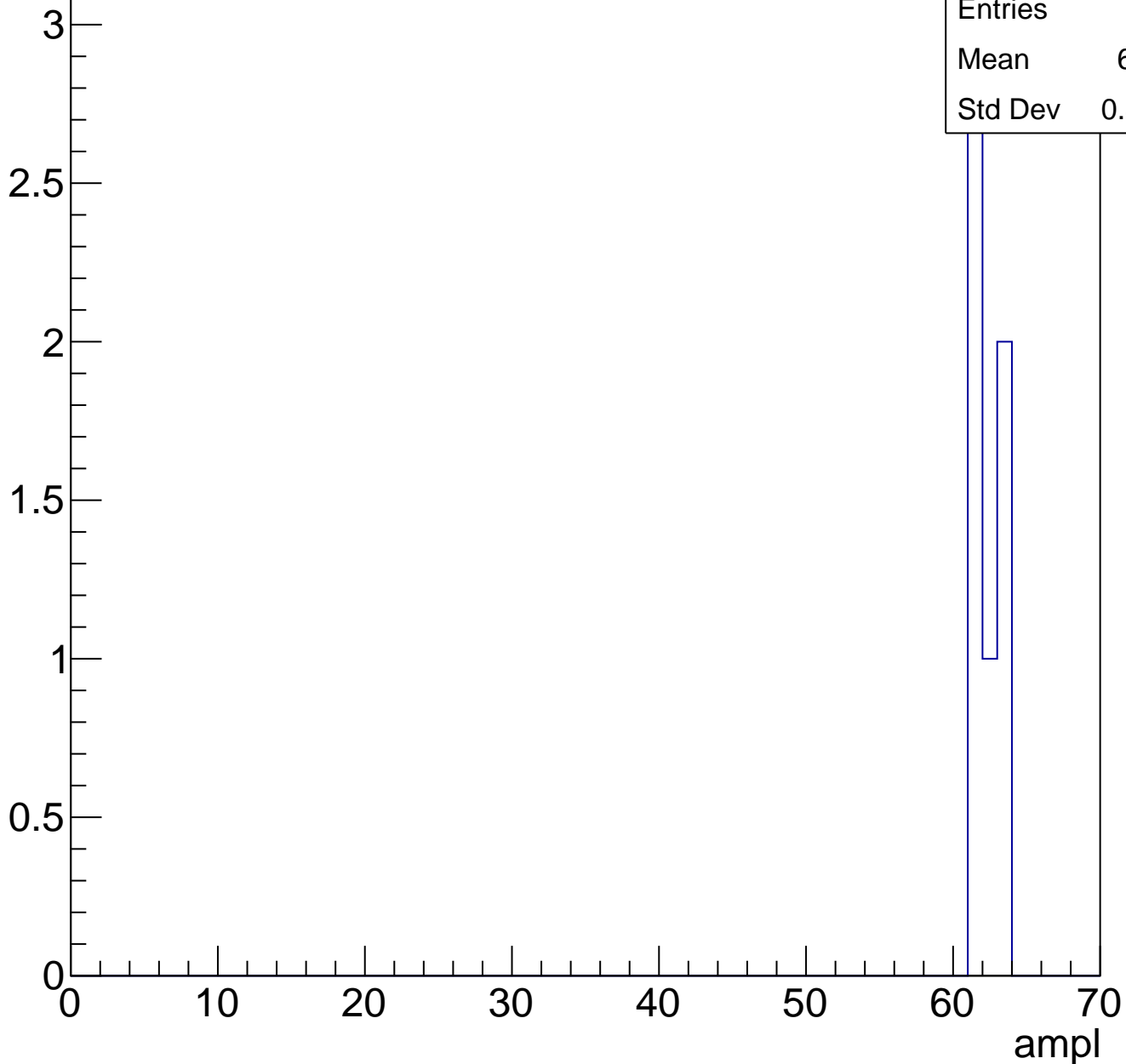
Entries	43
Mean	59.07
Std Dev	9.35



B1L102S, U13-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch55, adc0

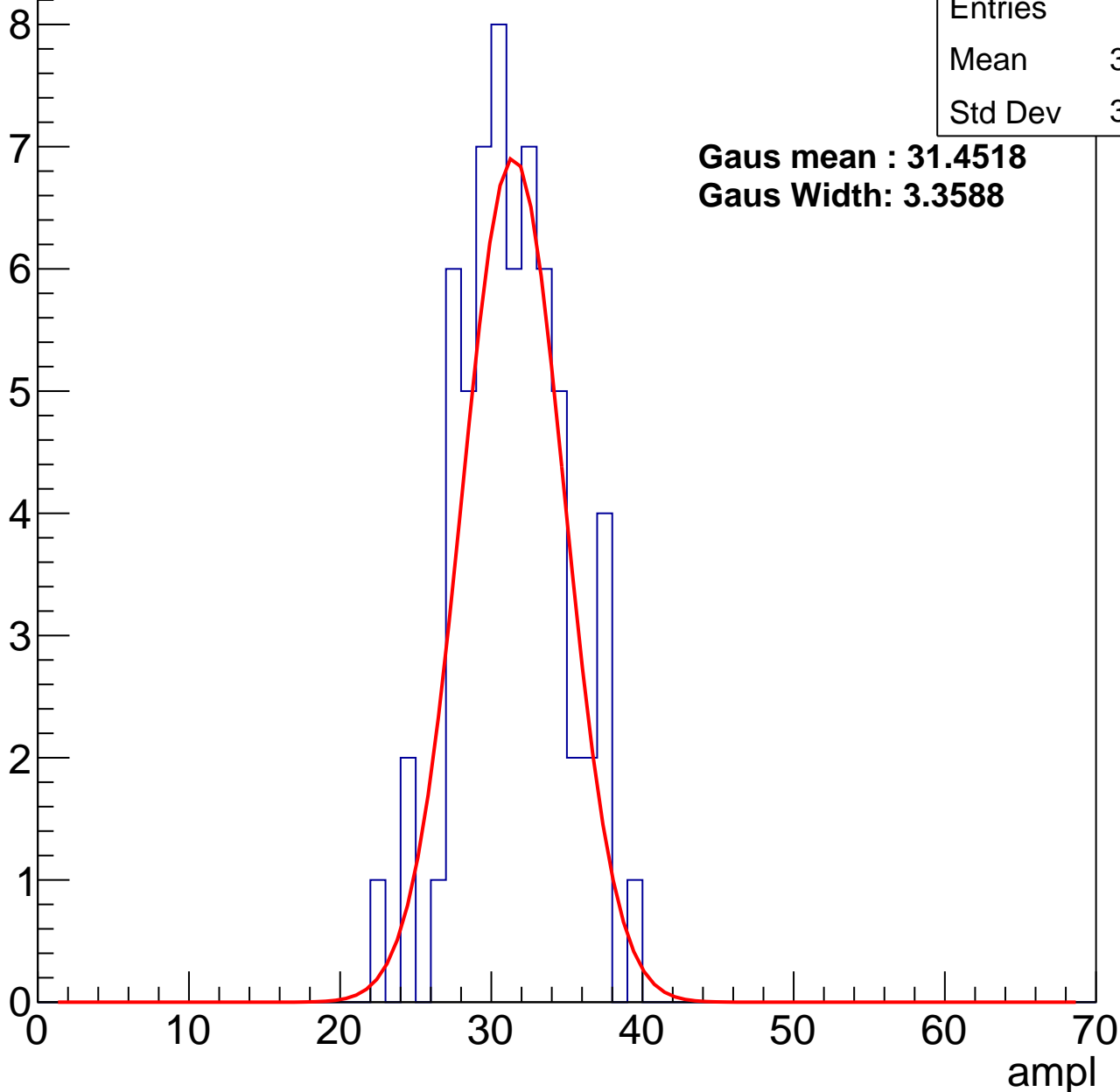
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	30.92
Std Dev	3.443

Gaus mean : 31.4518

Gaus Width: 3.3588



B1L102S, U13-ch55, adc1

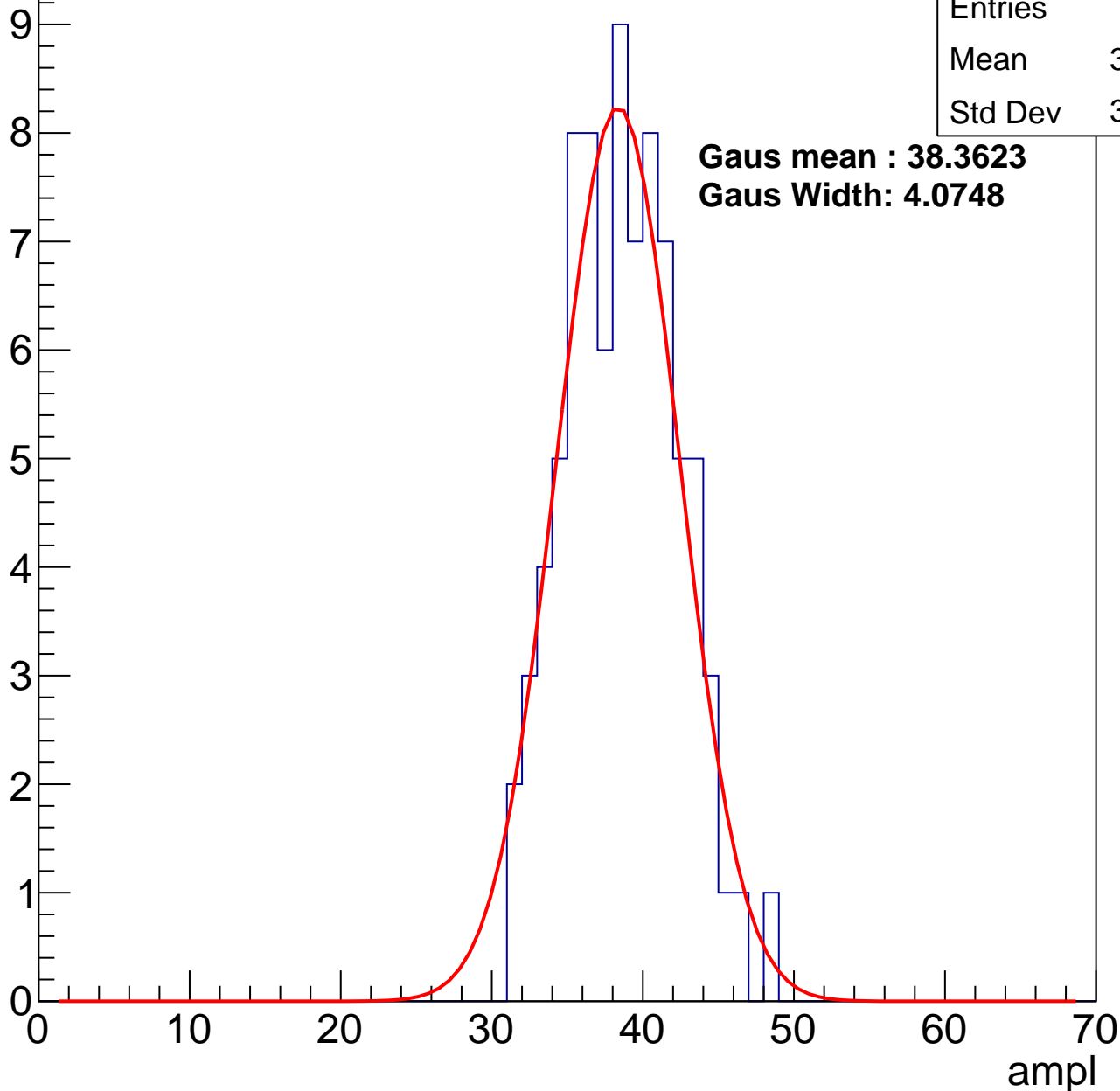
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	38.17
Std Dev	3.676

Gaus mean : 38.3623

Gaus Width: 4.0748



B1L102S, U13-ch55, adc2

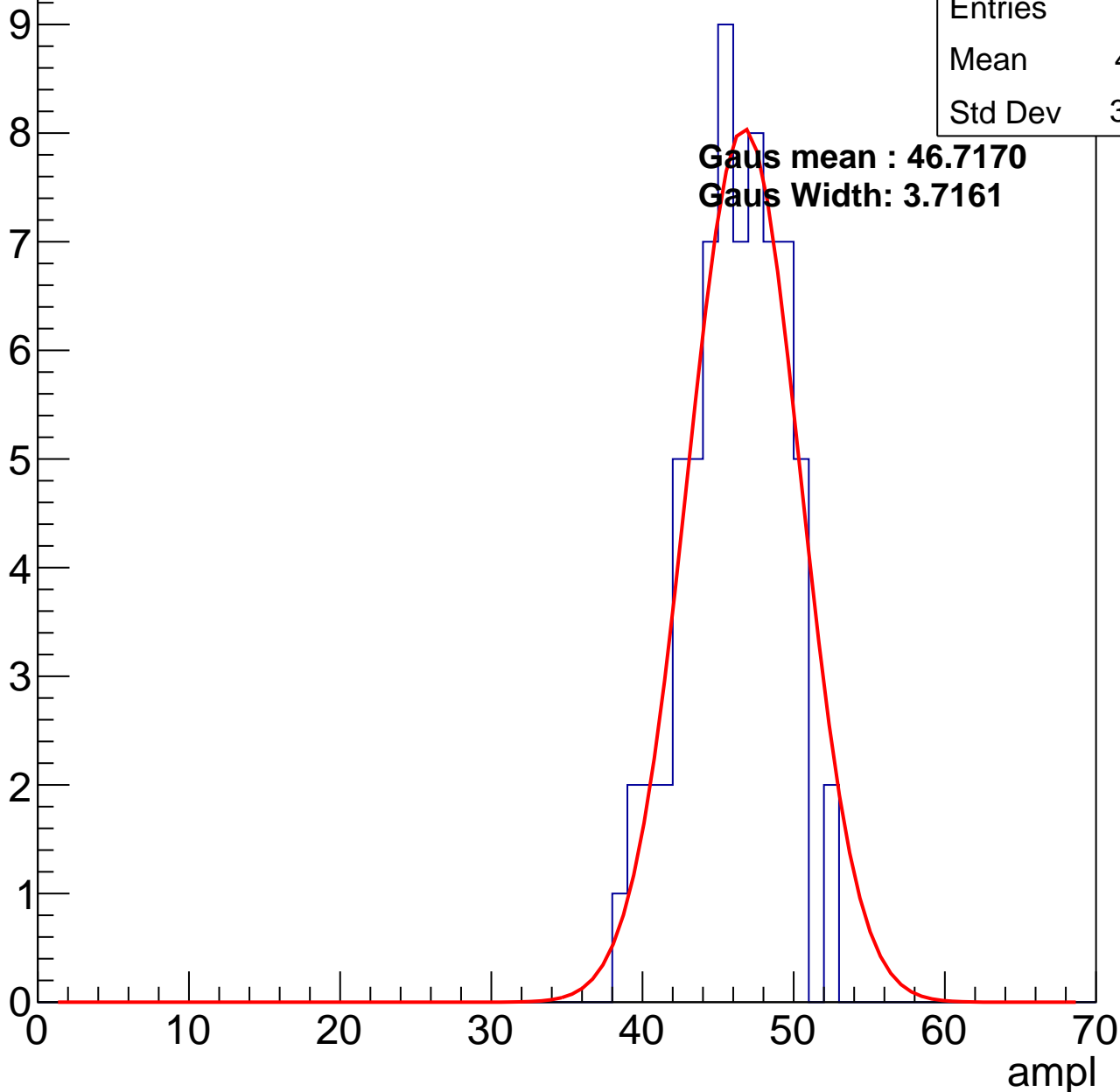
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	45.61
Std Dev	3.154

Gaus mean : 46.7170

Gaus Width: 3.7161

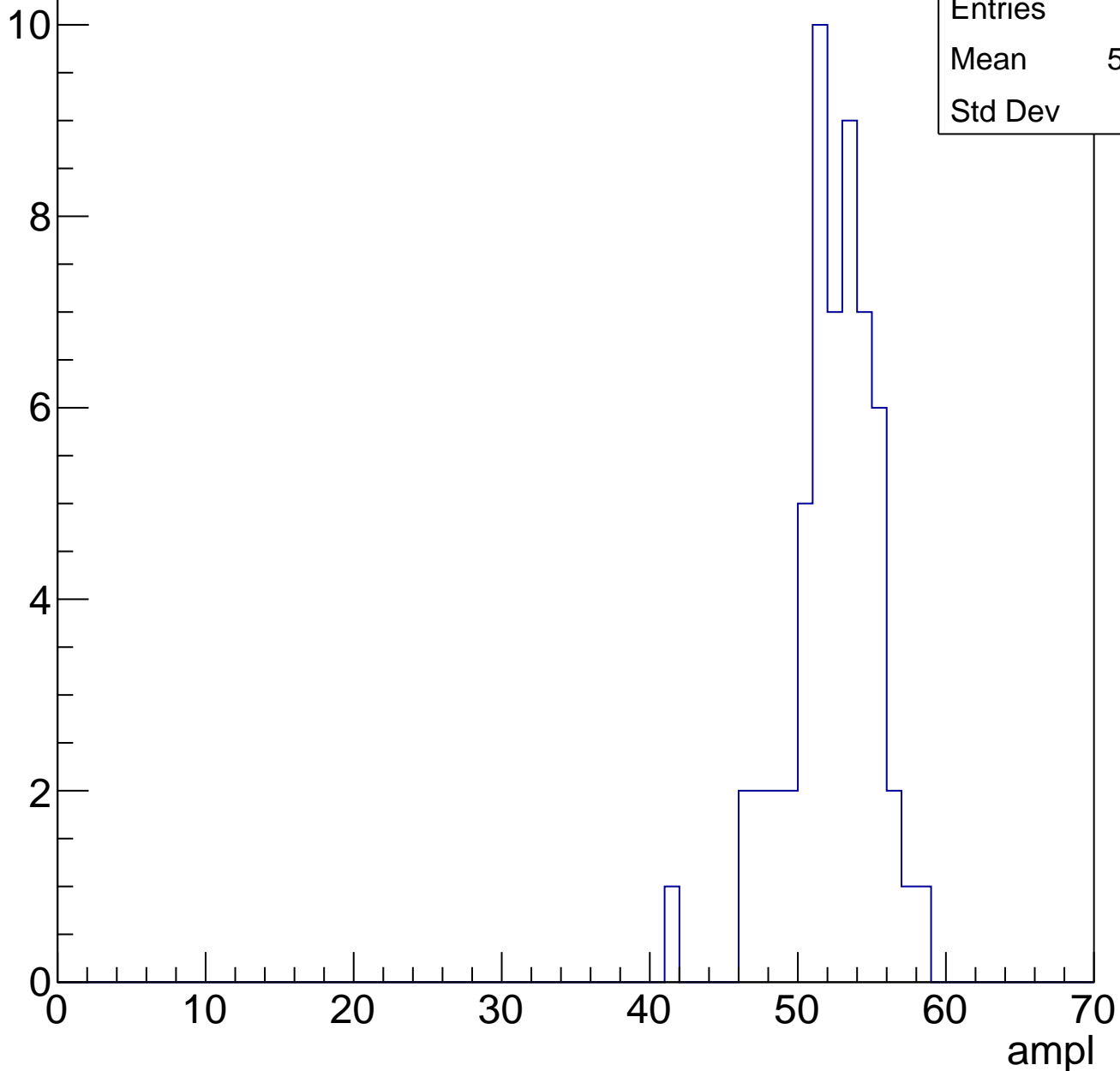


B1L102S, U13-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	57
Mean	51.88
Std Dev	2.98

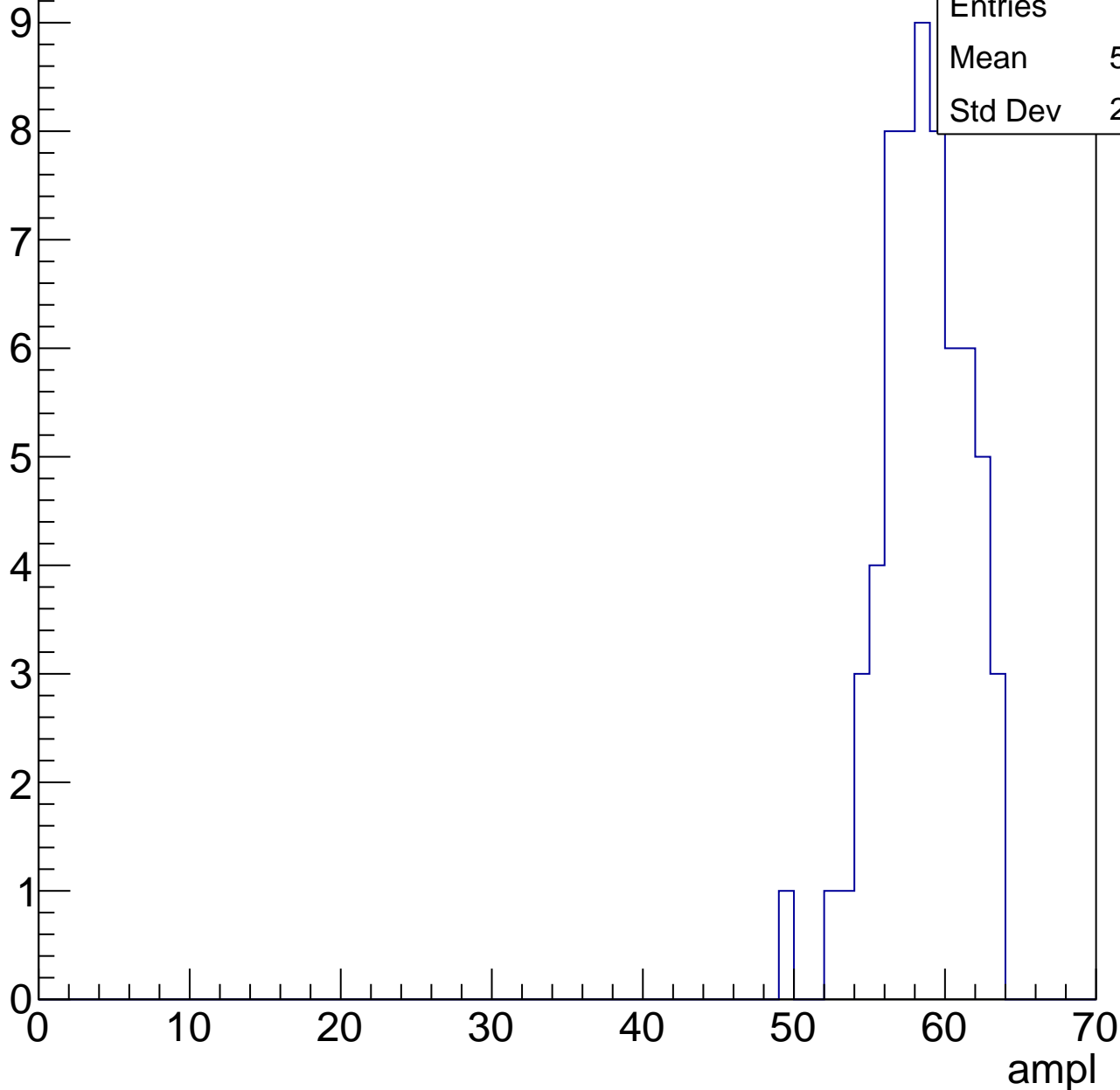
Entry



B1L102S, U13-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

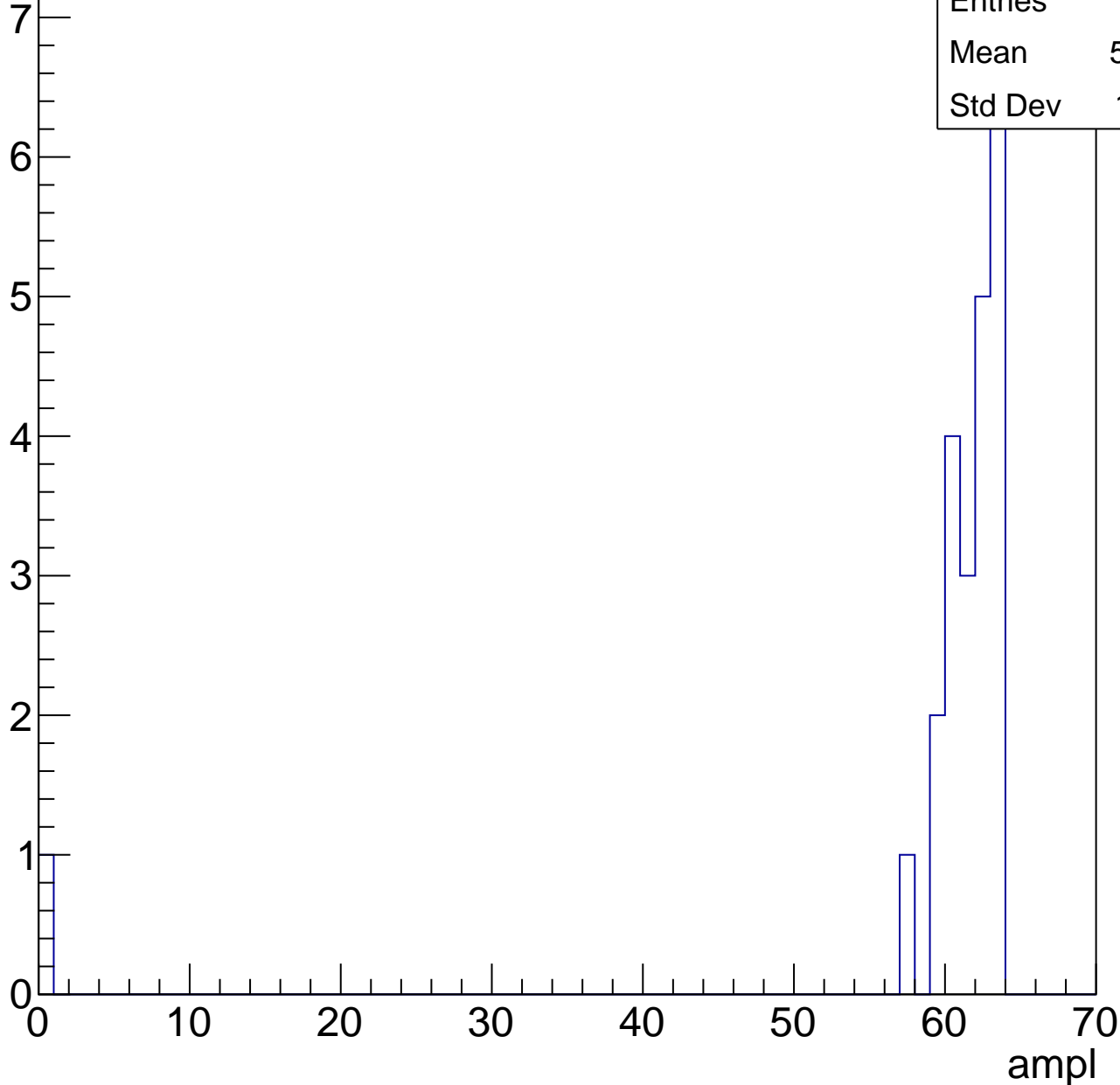


B1L102S, U13-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	58.65
Std Dev	12.61



B1L102S, U13-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch56, adc0

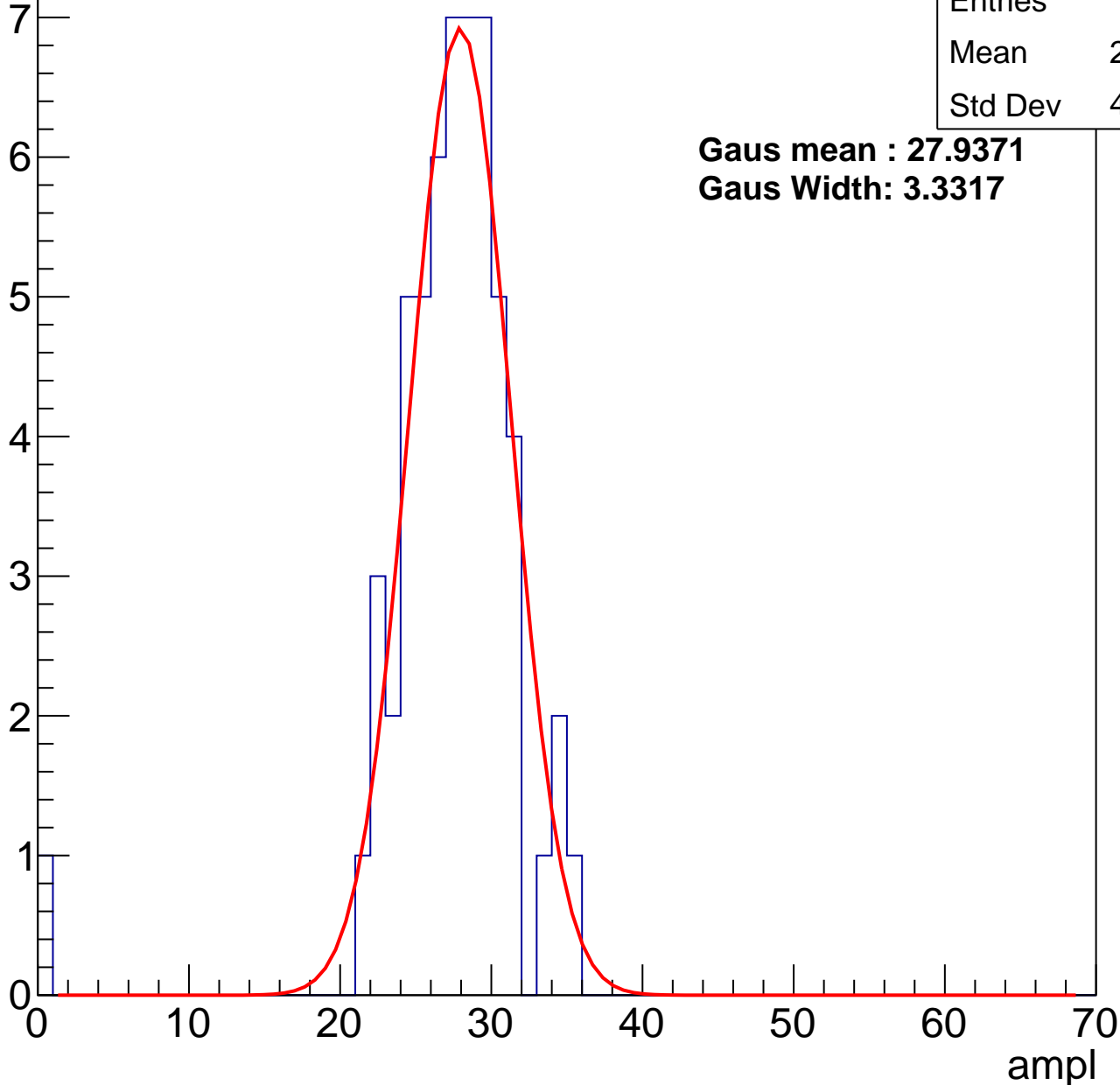
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	26.88
Std Dev	4.746

Gaus mean : 27.9371

Gaus Width: 3.3317



B1L102S, U13-ch56, adc1

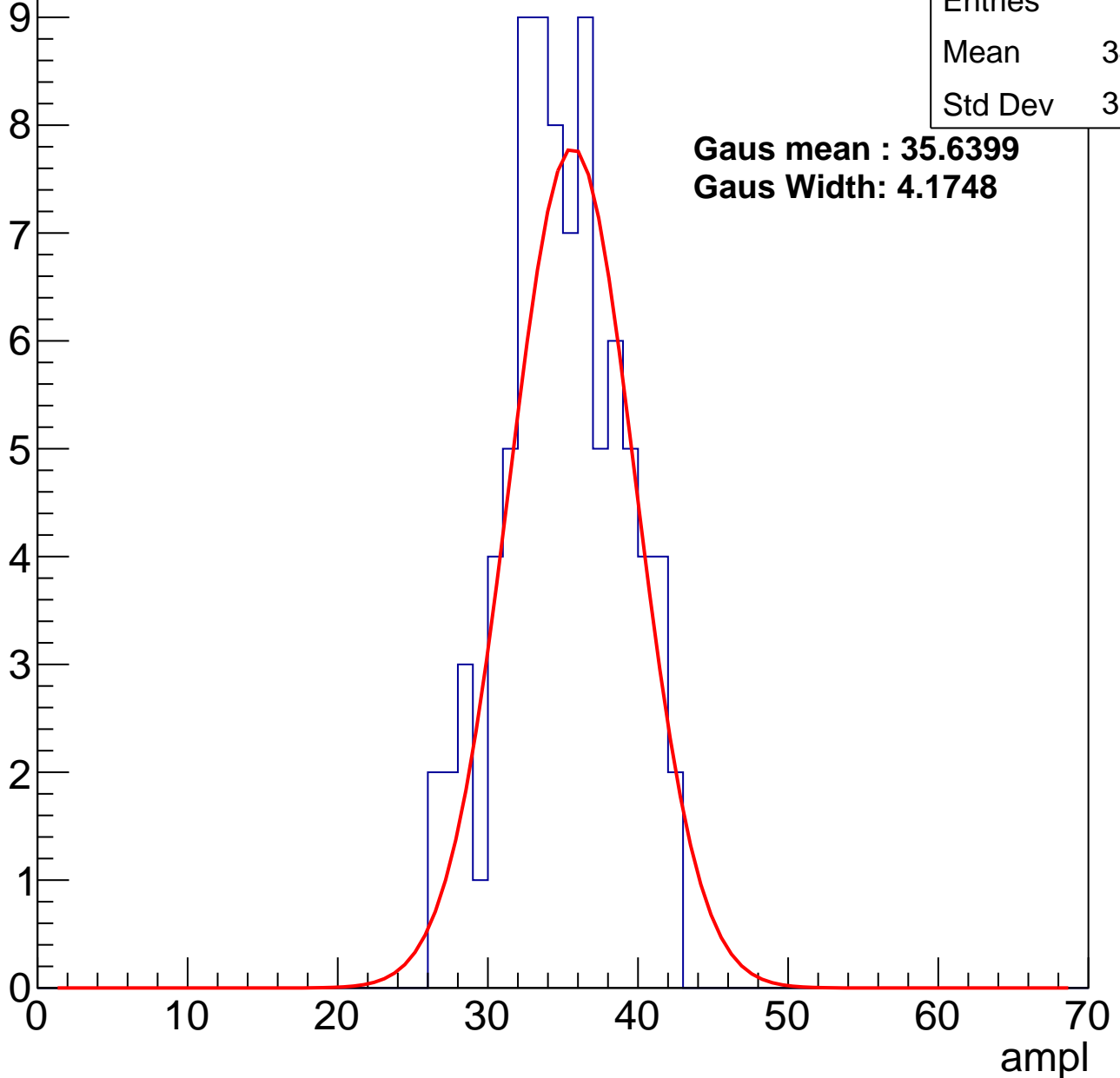
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	34.54
Std Dev	3.858

Gaus mean : 35.6399

Gaus Width: 4.1748



B1L102S, U13-ch56, adc2

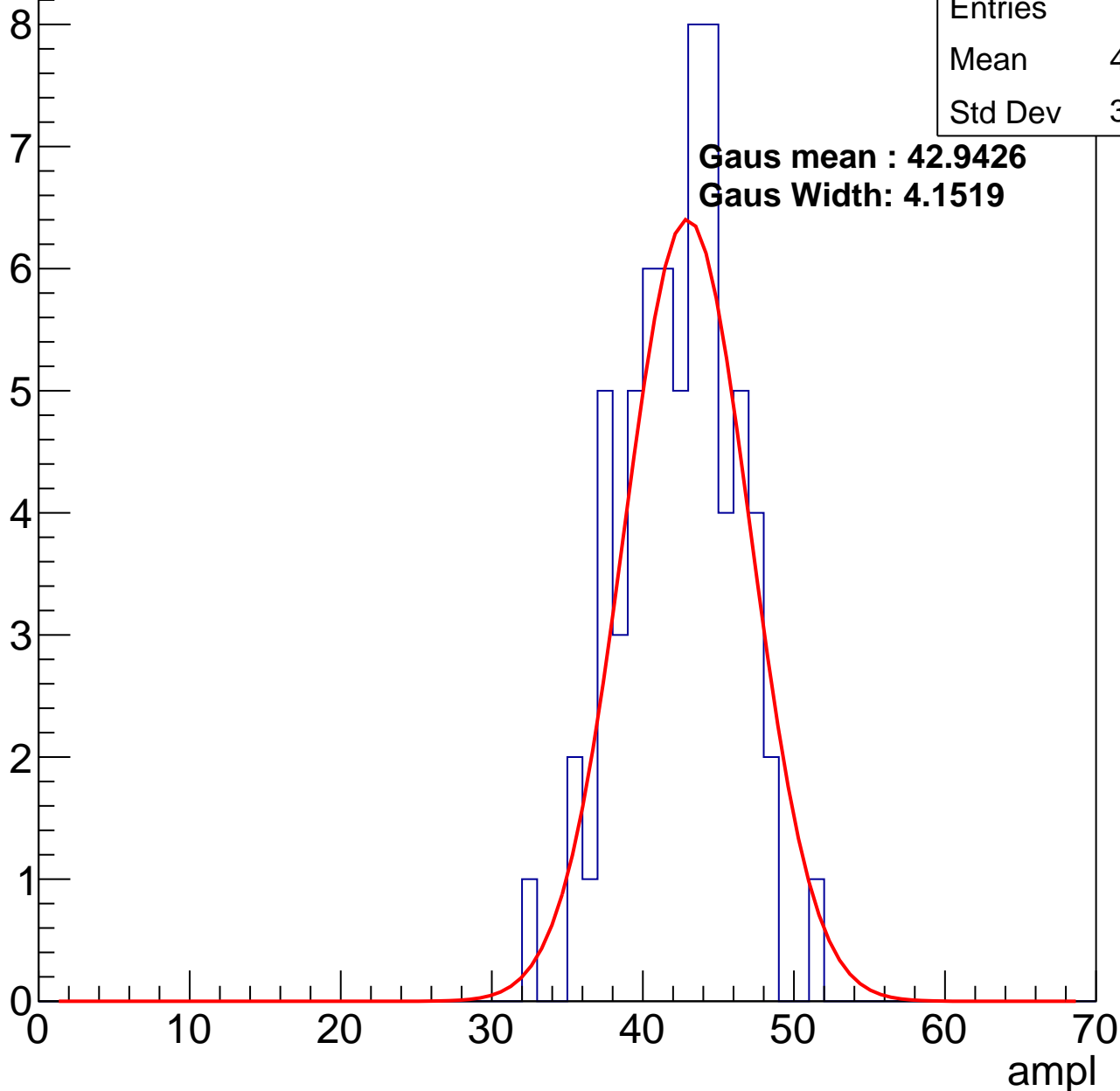
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.95
Std Dev	3.674

Gaus mean : 42.9426

Gaus Width: 4.1519

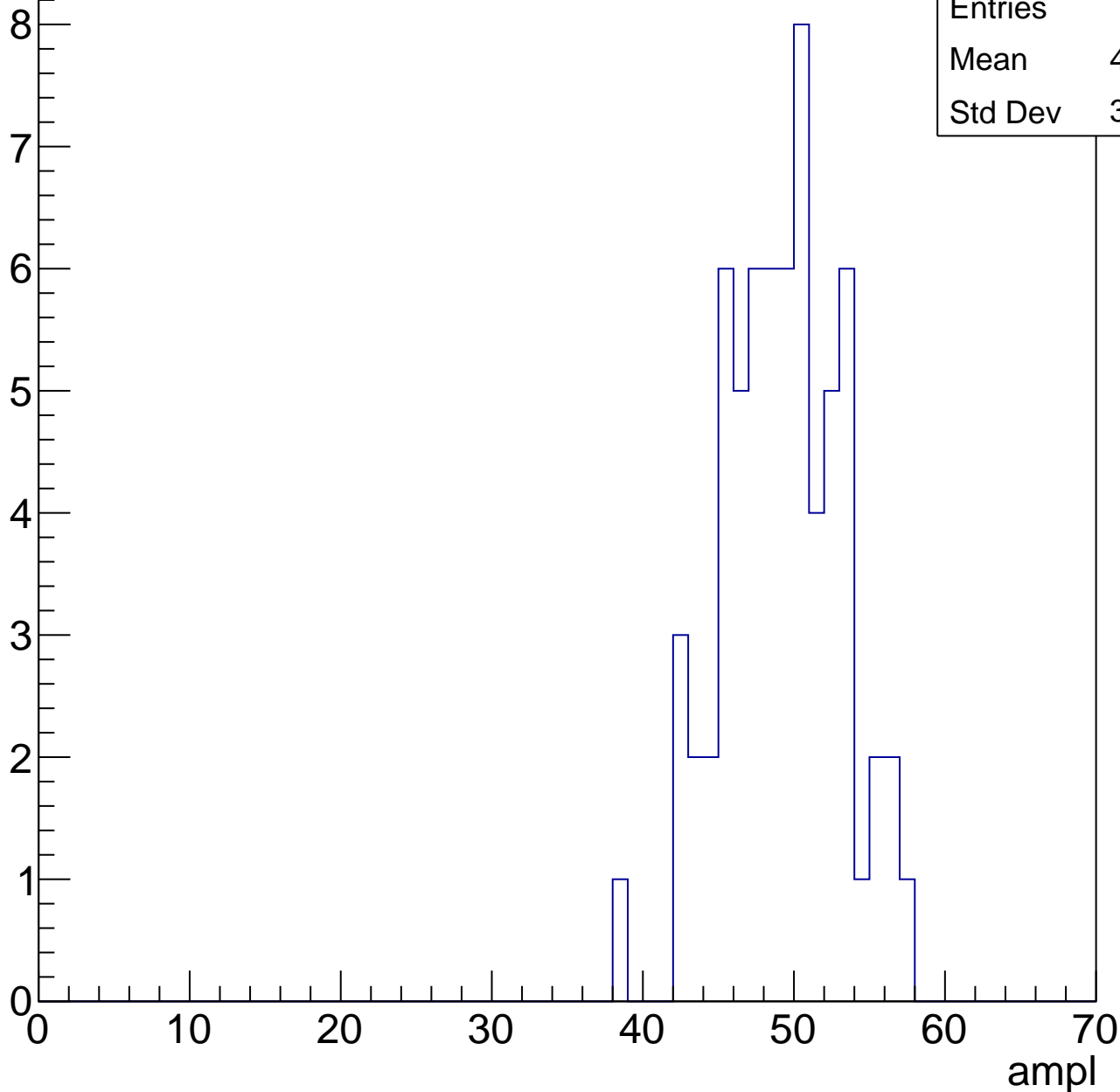


B1L102S, U13-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.74
Std Dev	3.855

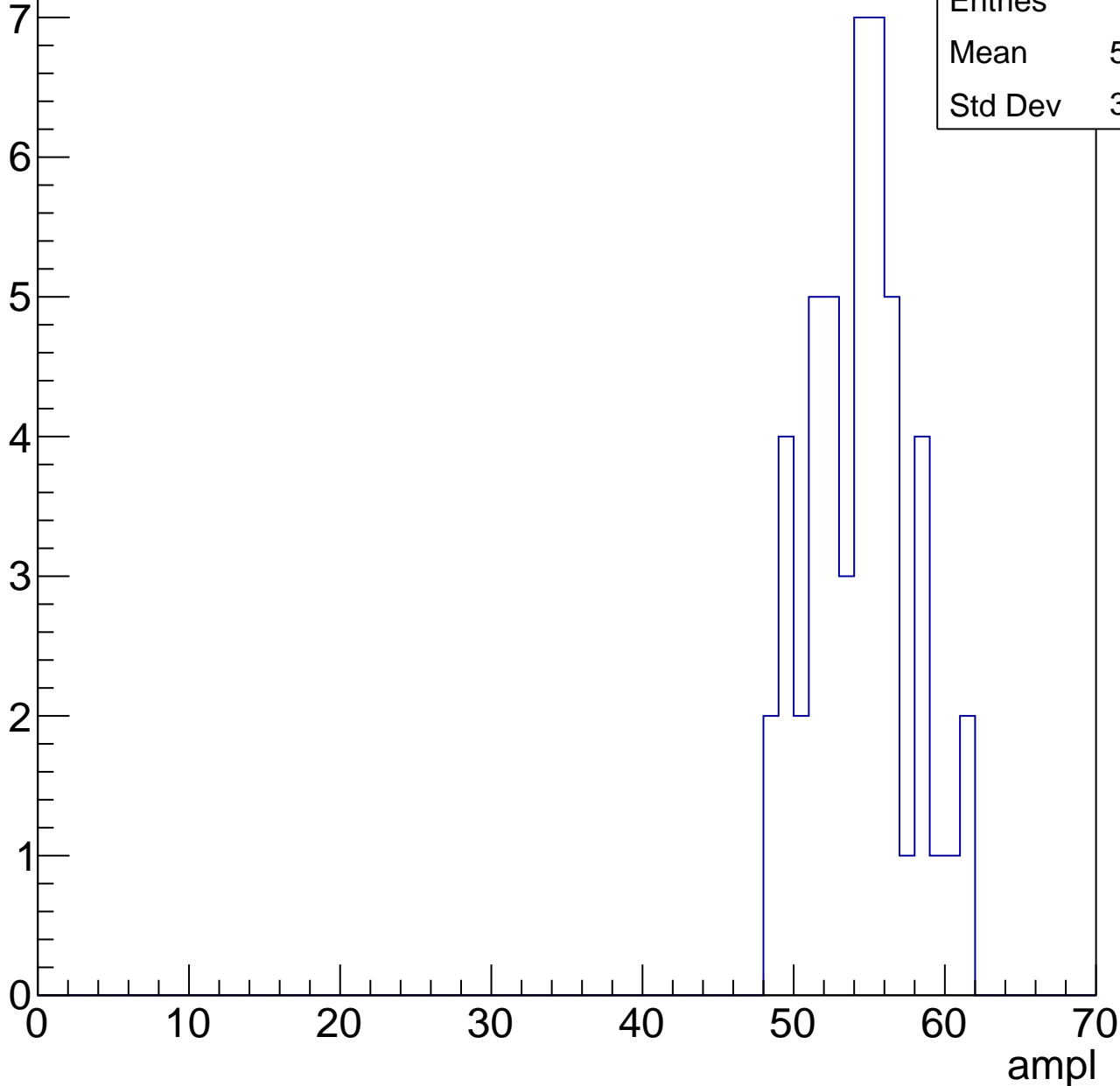


B1L102S, U13-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.86
Std Dev	3.289



B1L102S, U13-ch56, adc5

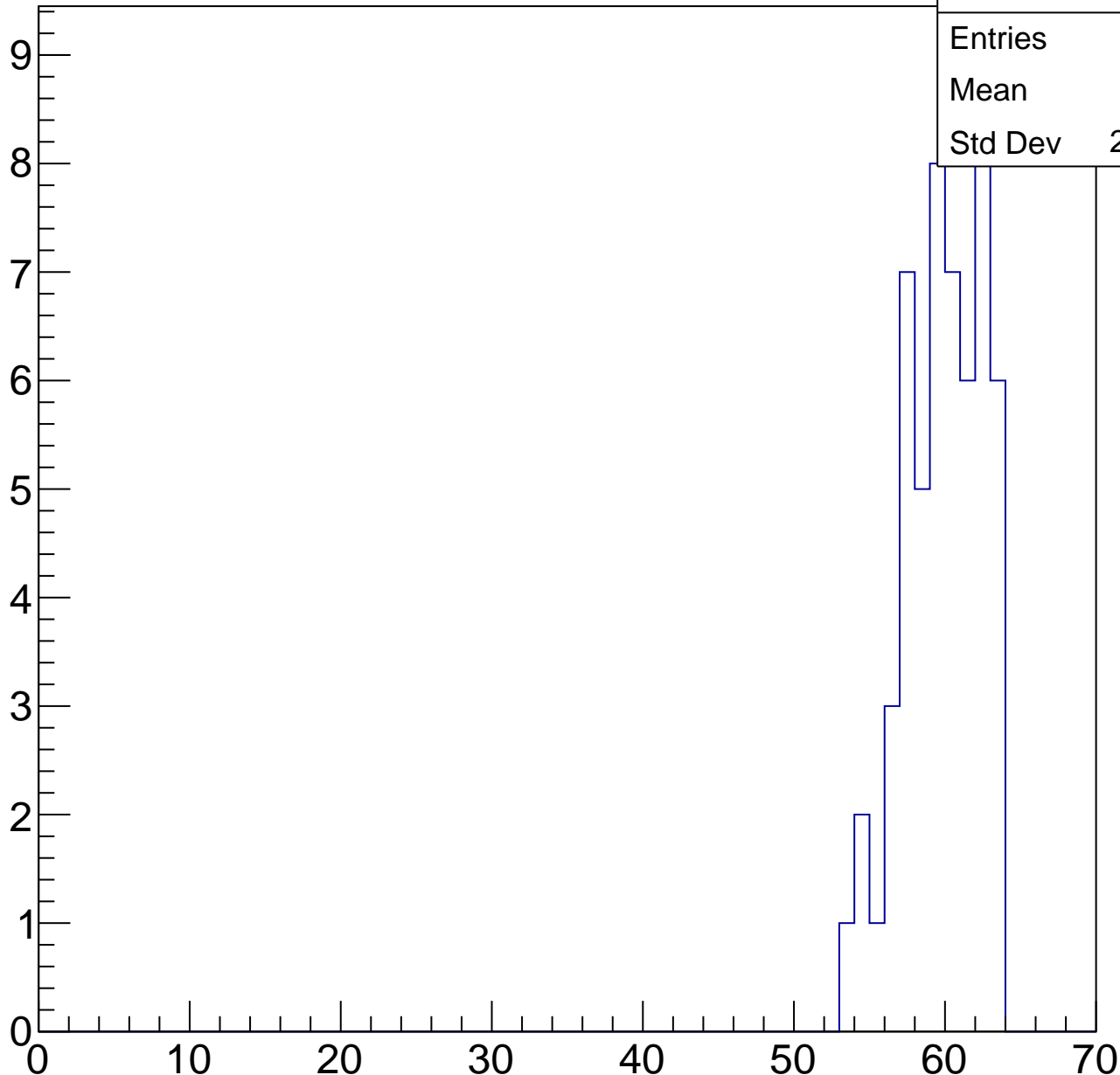
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	59.4
Std Dev	2.563

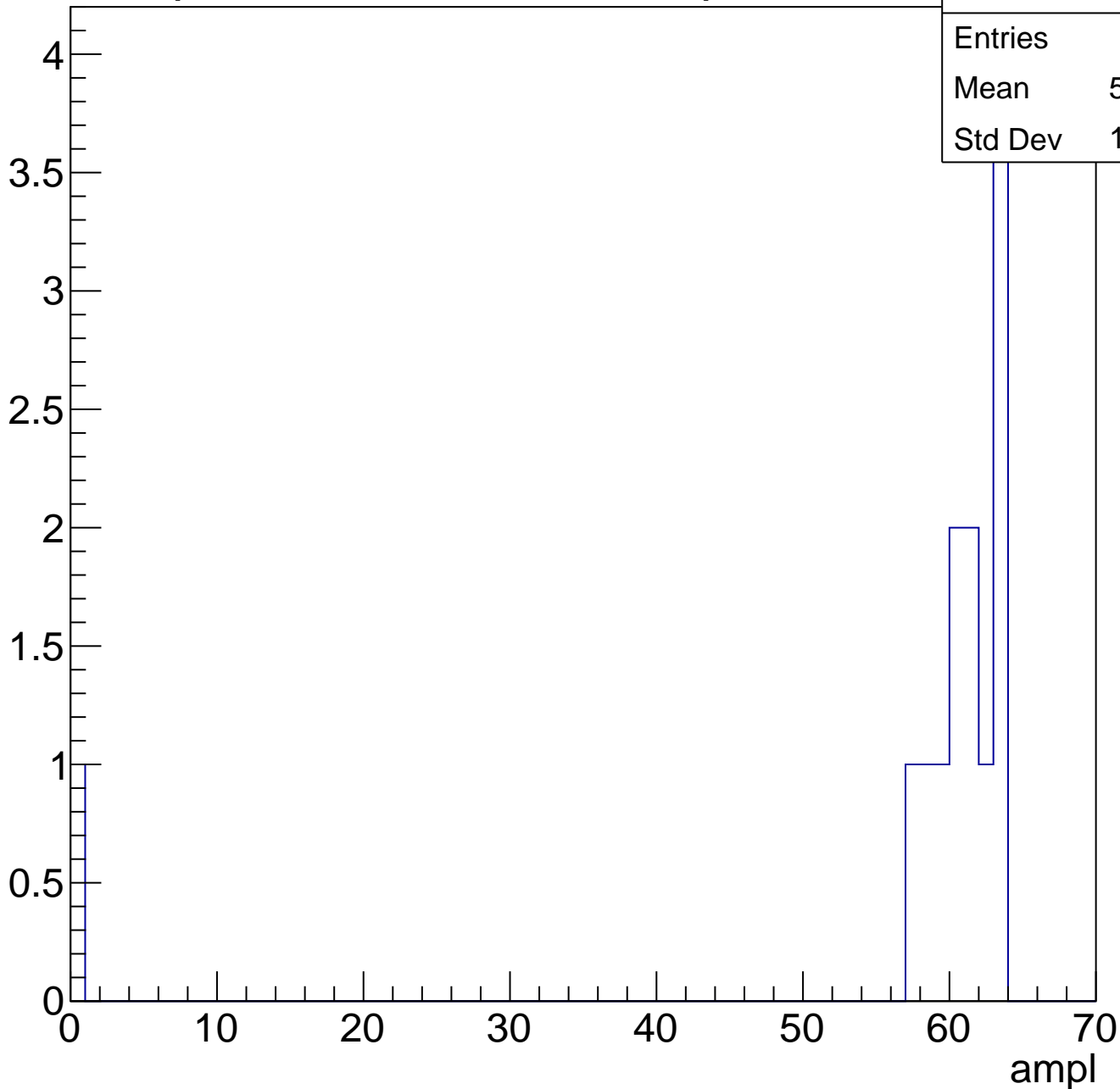
ampl



B1L102S, U13-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch57, adc0

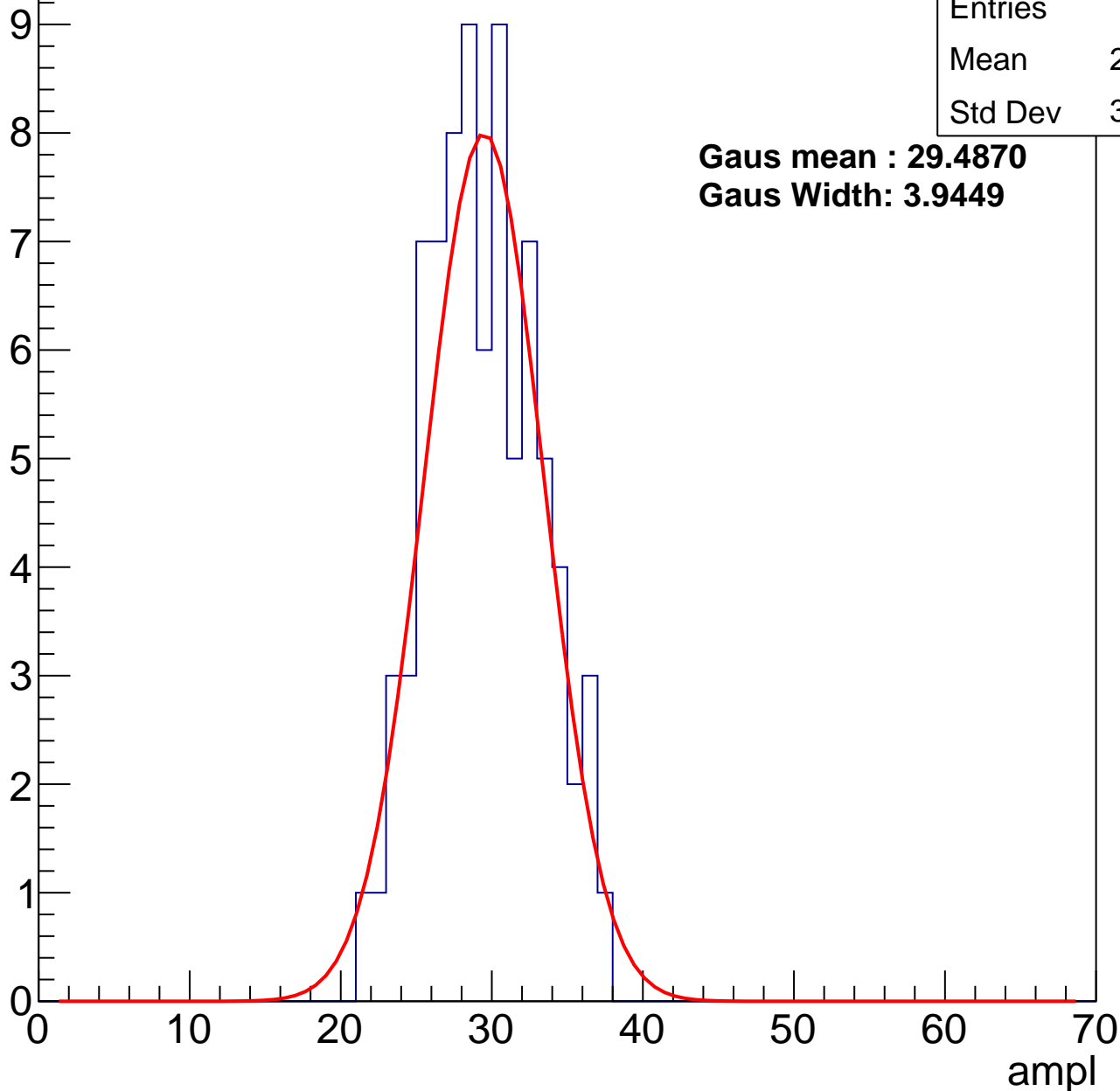
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.99
Std Dev	3.629

Gaus mean : 29.4870

Gaus Width: 3.9449



B1L102S, U13-ch57, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	36.45
Std Dev	3.105

Gaus mean : 36.6826

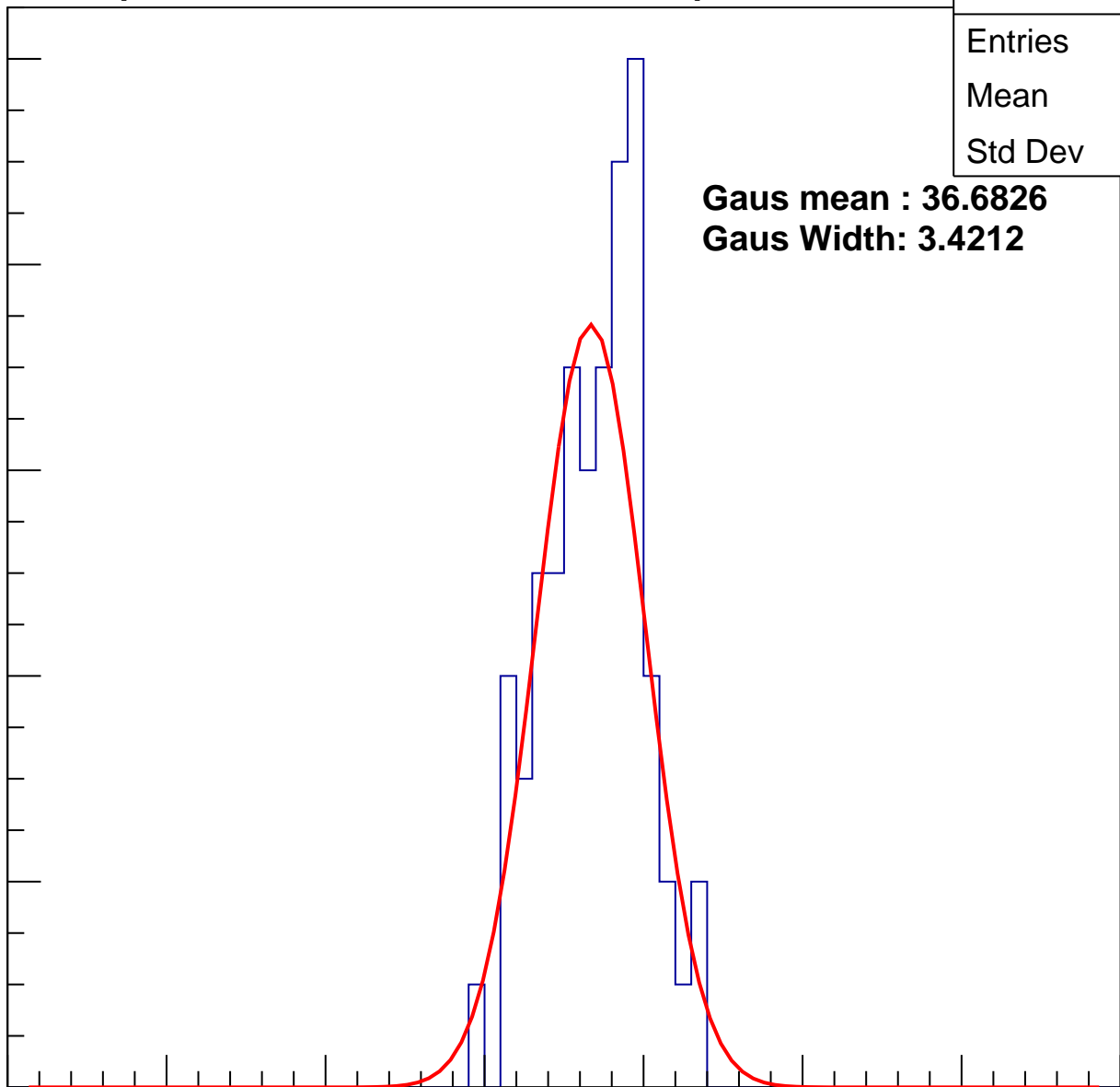
Gaus Width: 3.4212

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch57, adc2

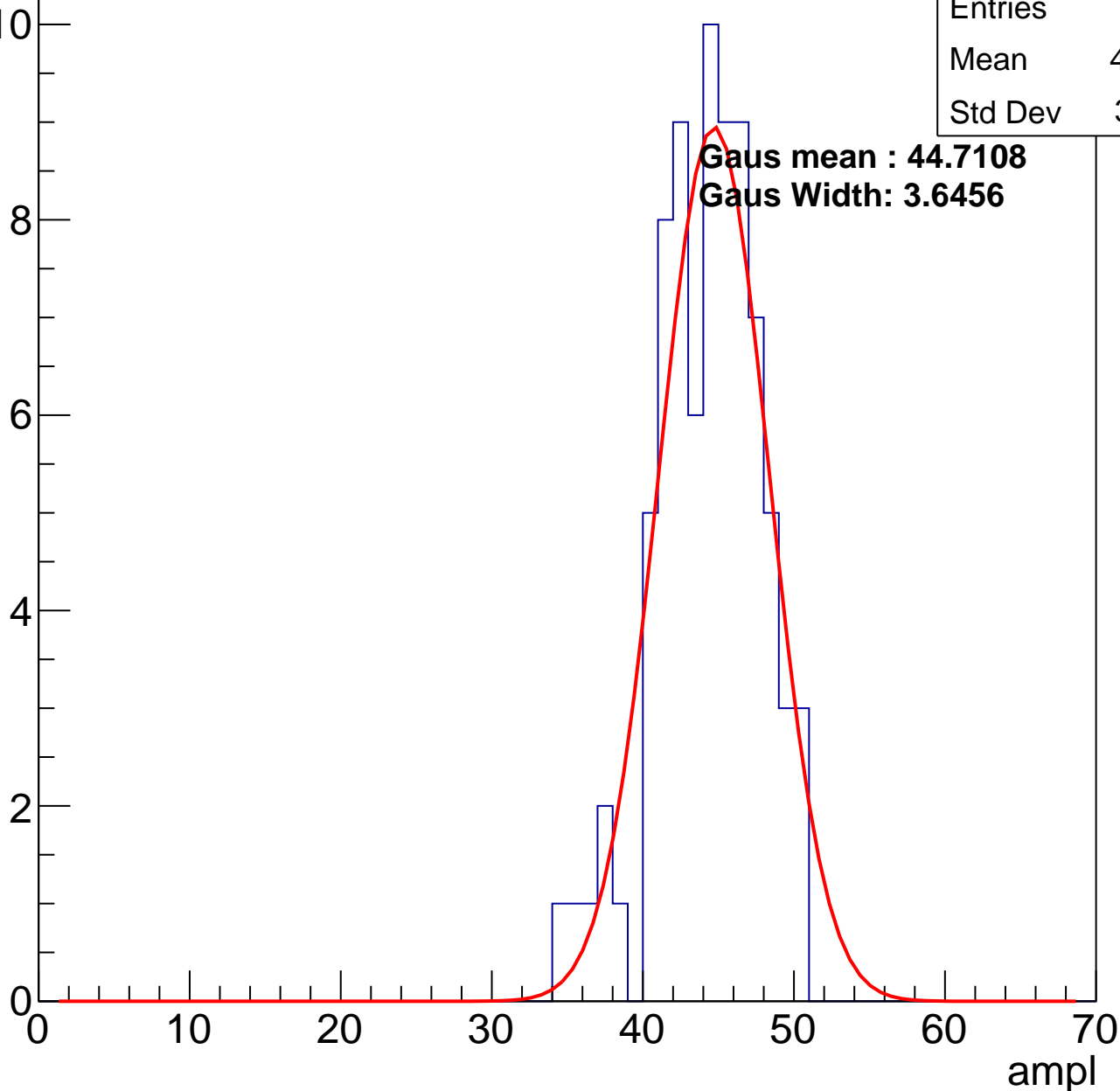
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	43.83
Std Dev	3.431

Gaus mean : 44.7108

Gaus Width: 3.6456

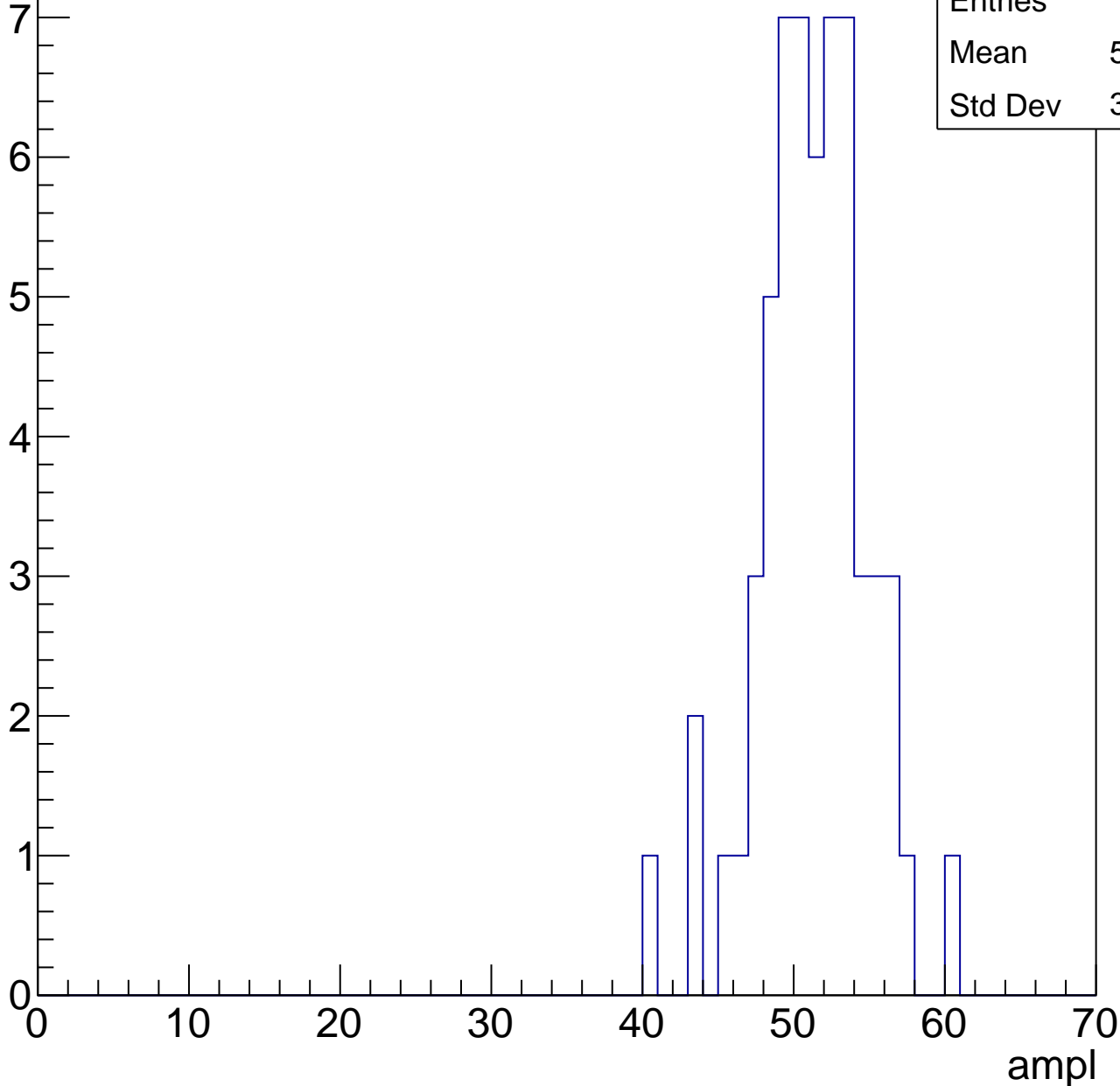


B1L102S, U13-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.76
Std Dev	3.564

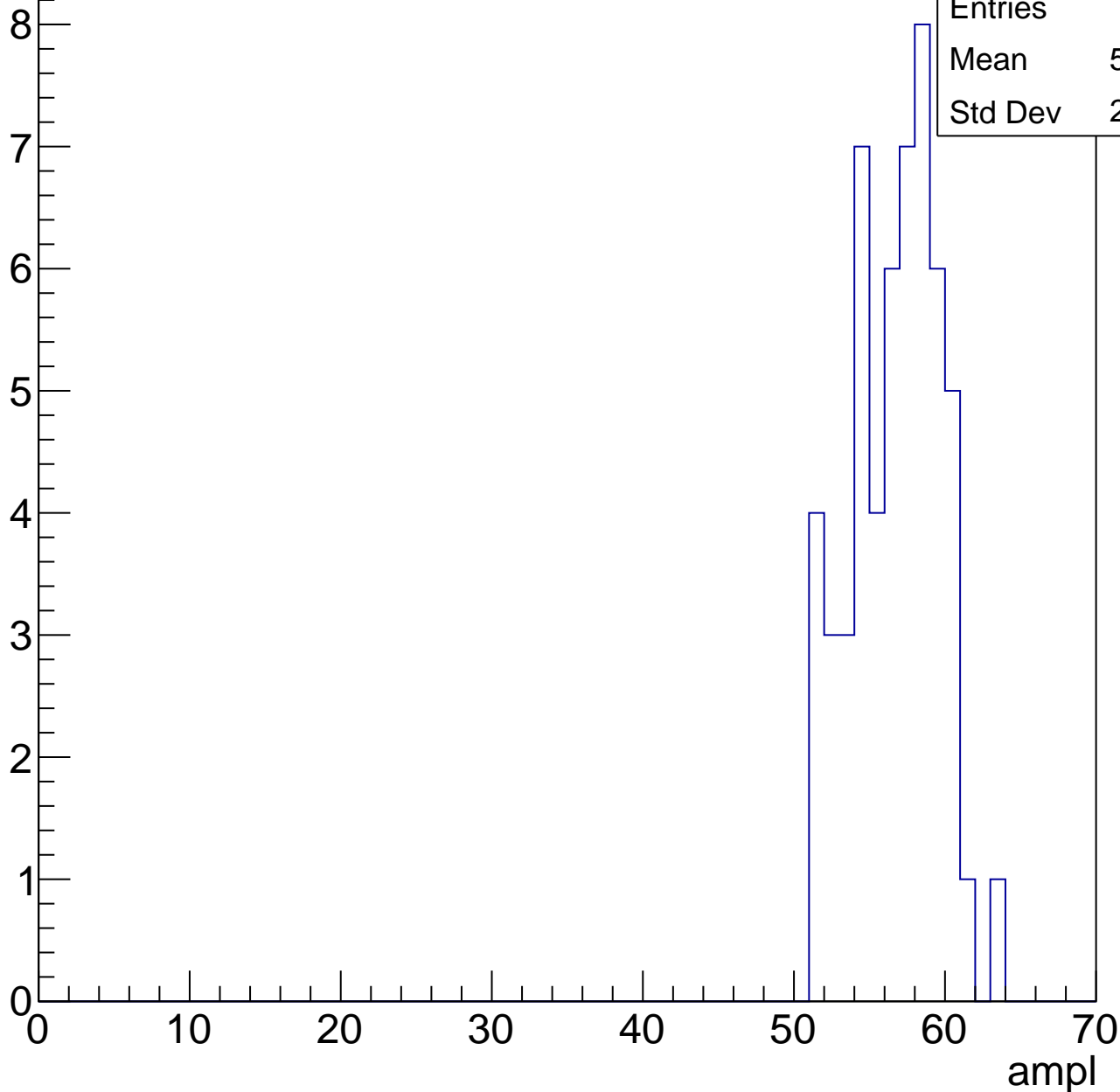


B1L102S, U13-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.25
Std Dev	2.862



B1L102S, U13-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

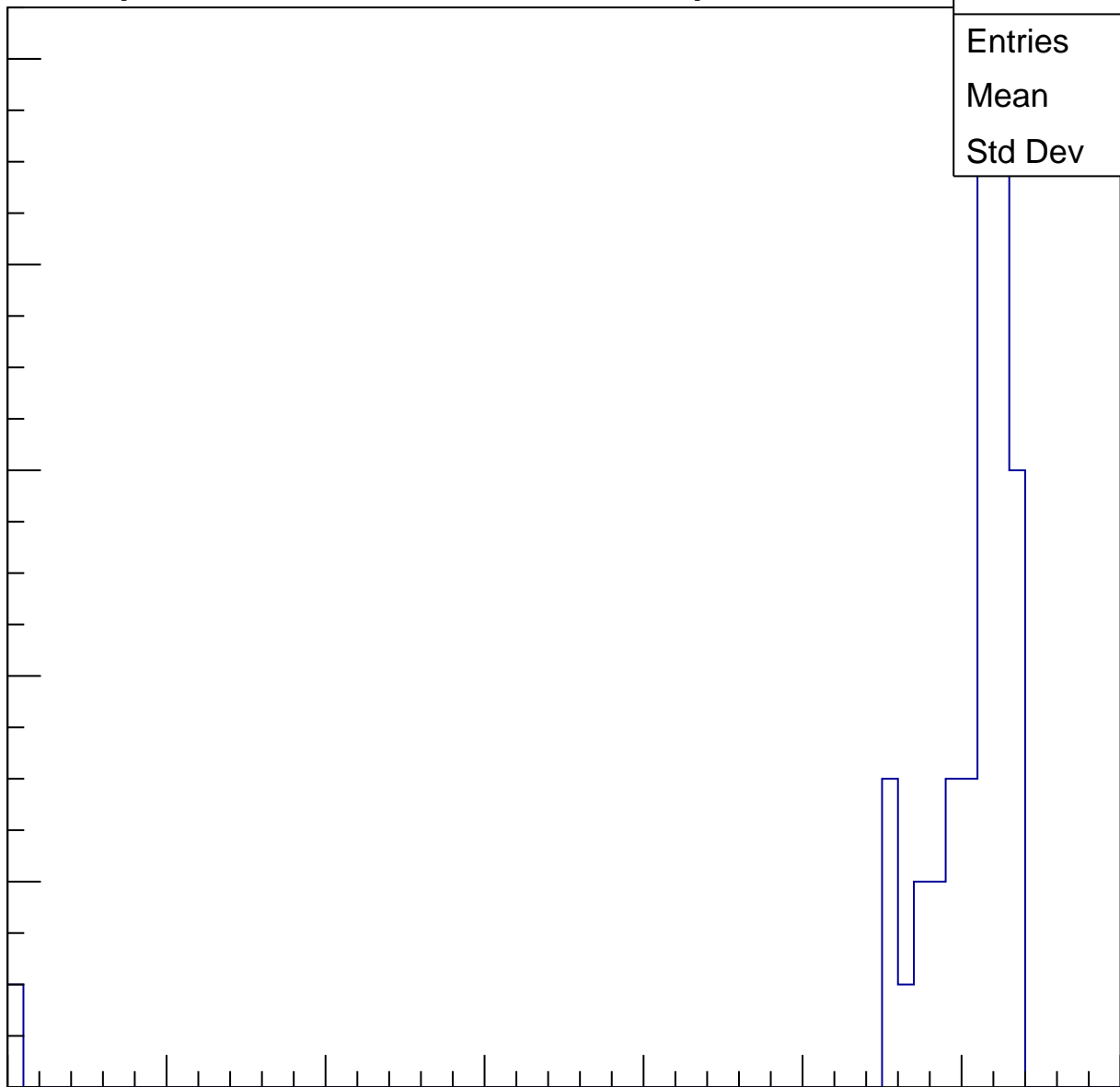
Entries	40
Mean	58.88
Std Dev	9.711

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

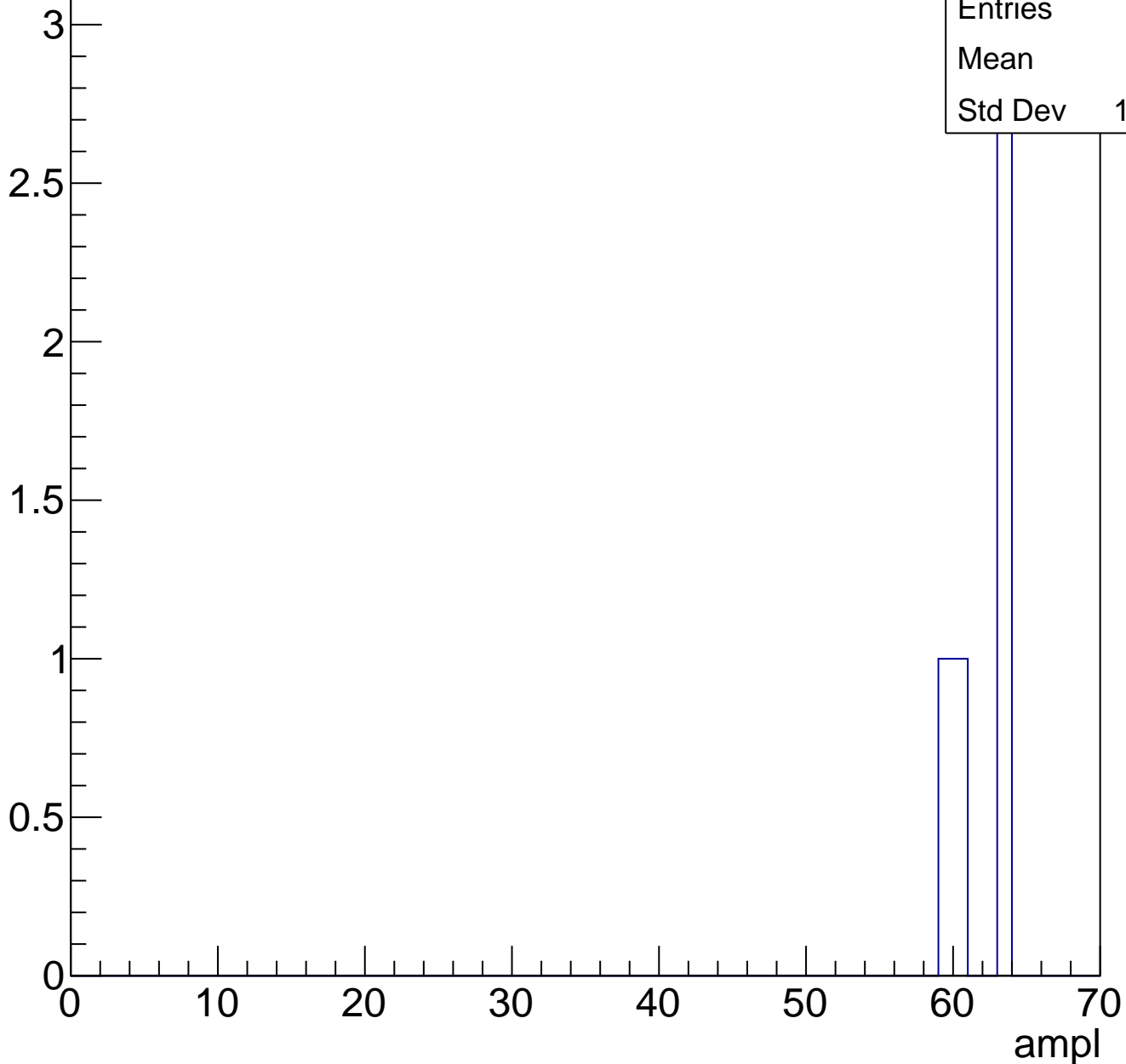
ampl



B1L102S, U13-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch58, adc0

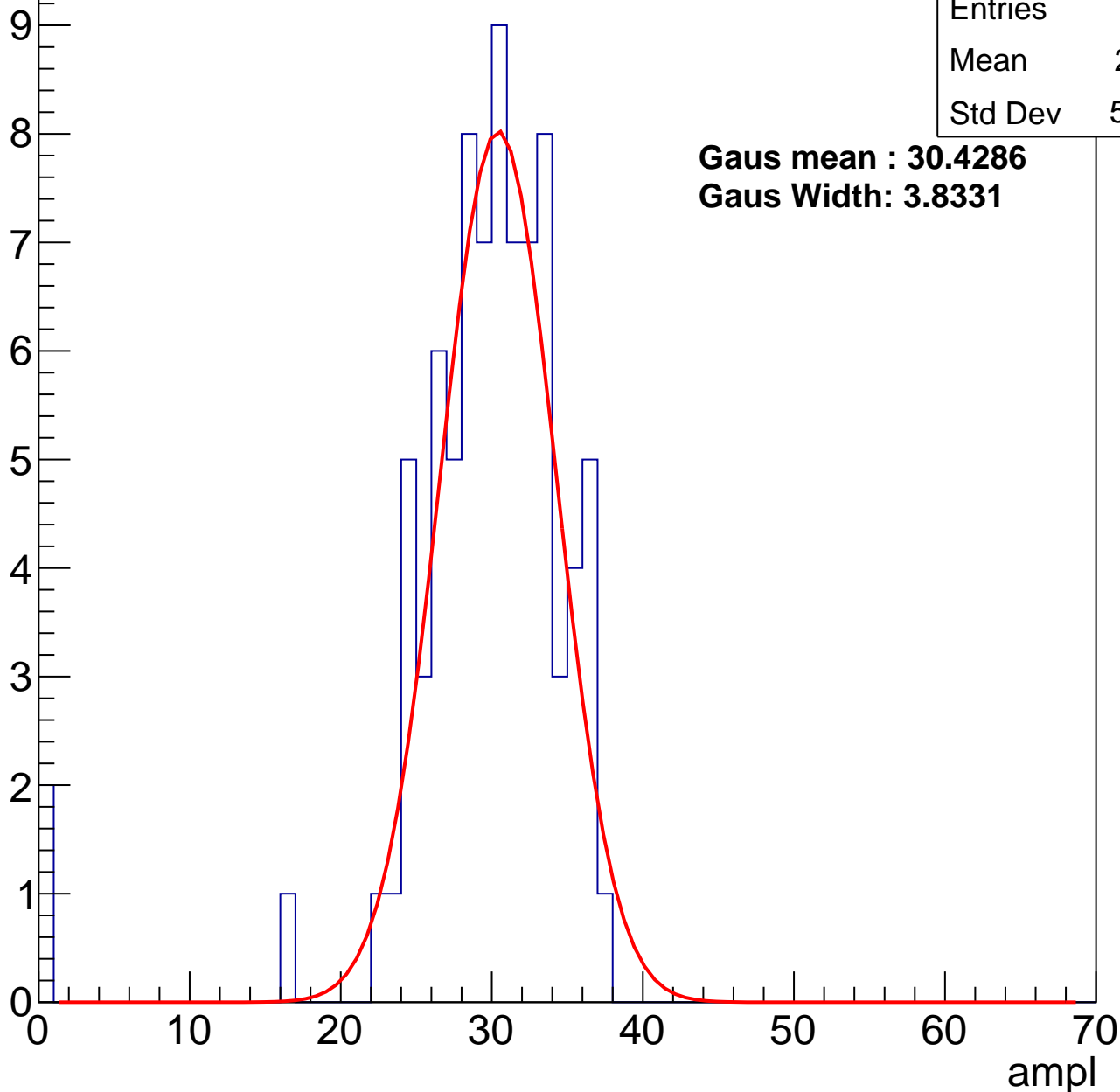
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	29.01
Std Dev	5.959

Gaus mean : 30.4286

Gaus Width: 3.8331



B1L102S, U13-ch58, adc1

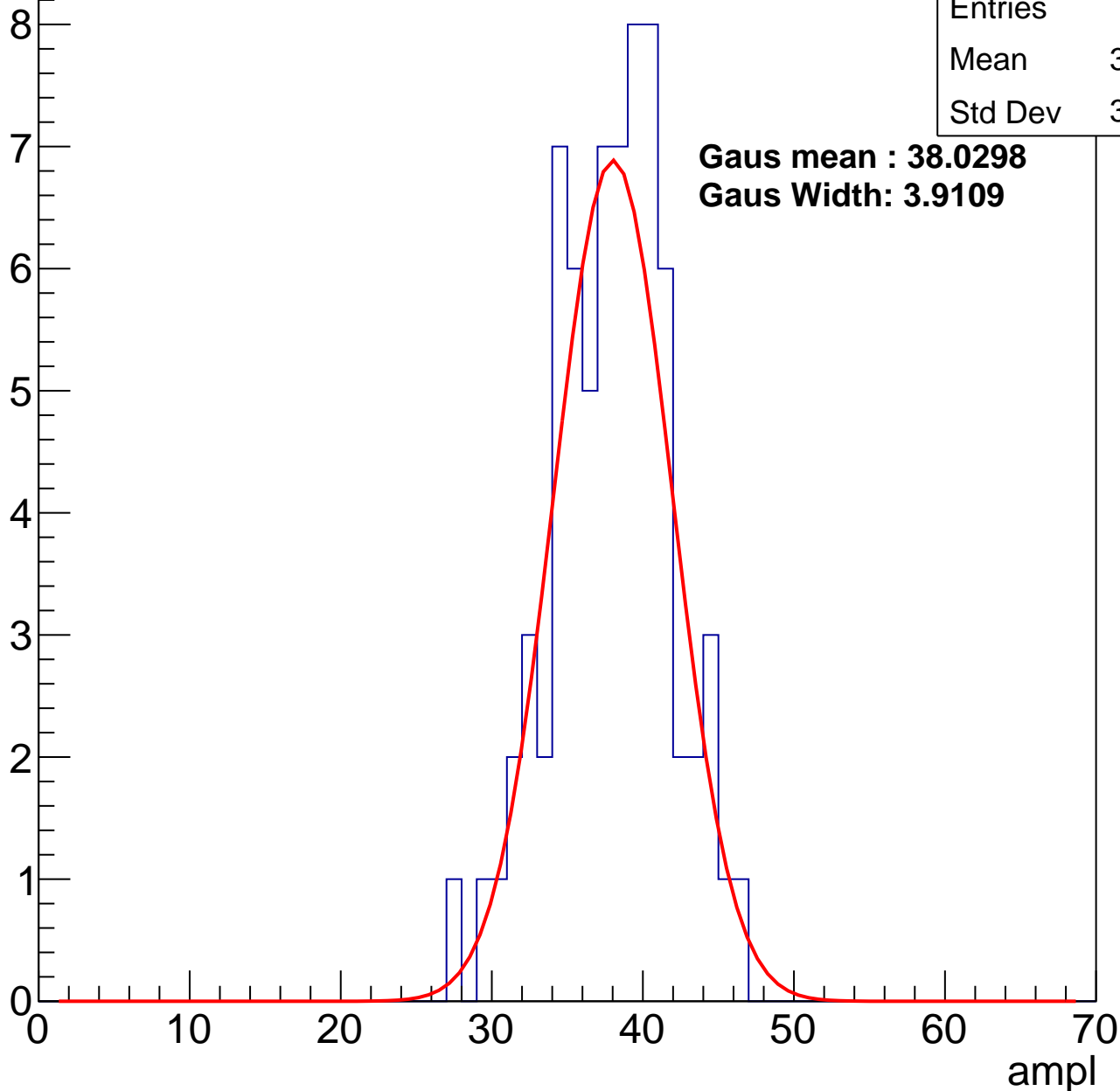
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.45
Std Dev	3.864

Gaus mean : 38.0298

Gaus Width: 3.9109



B1L102S, U13-ch58, adc2

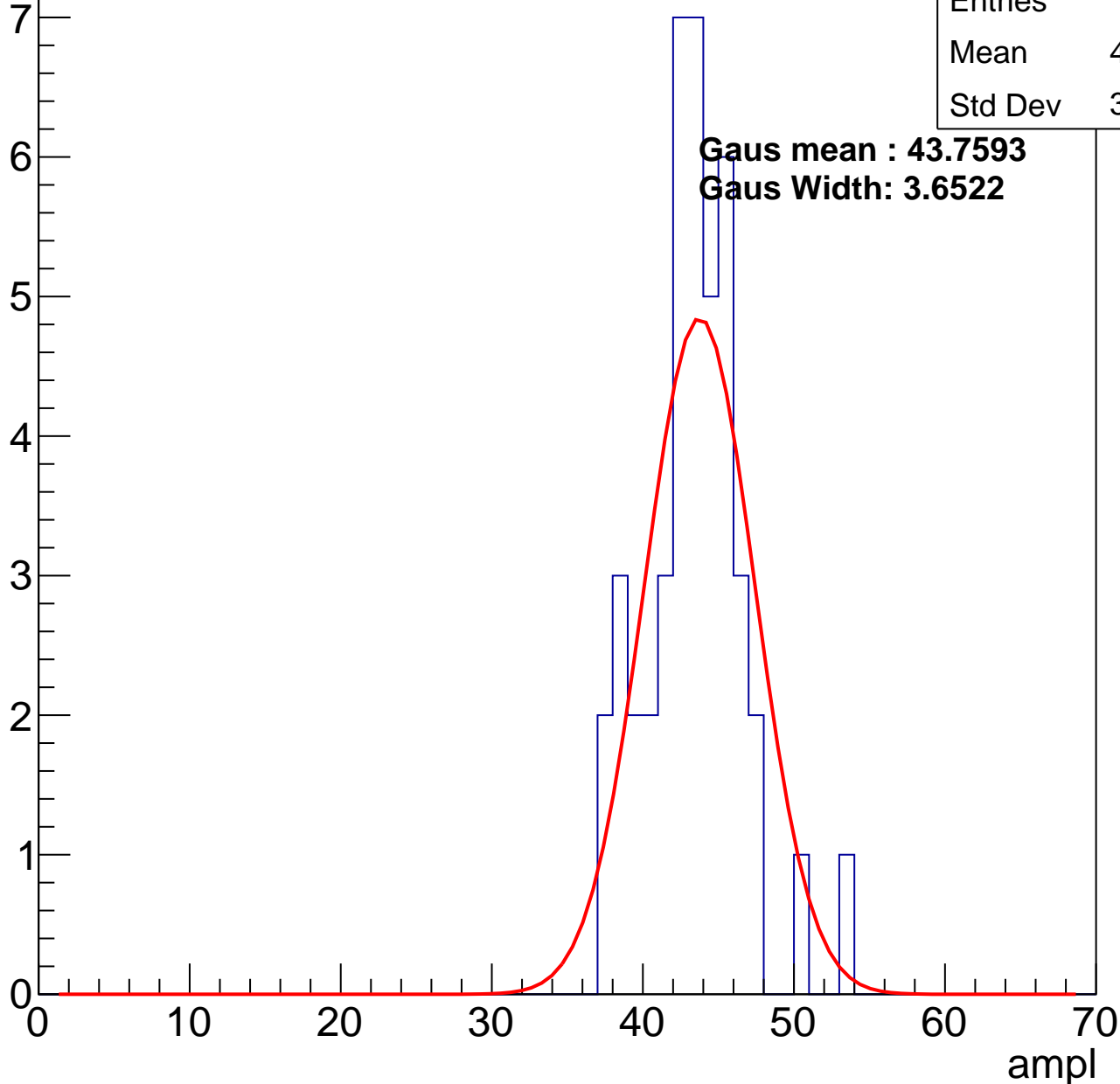
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	42.93
Std Dev	3.208

Gaus mean : 43.7593

Gaus Width: 3.6522

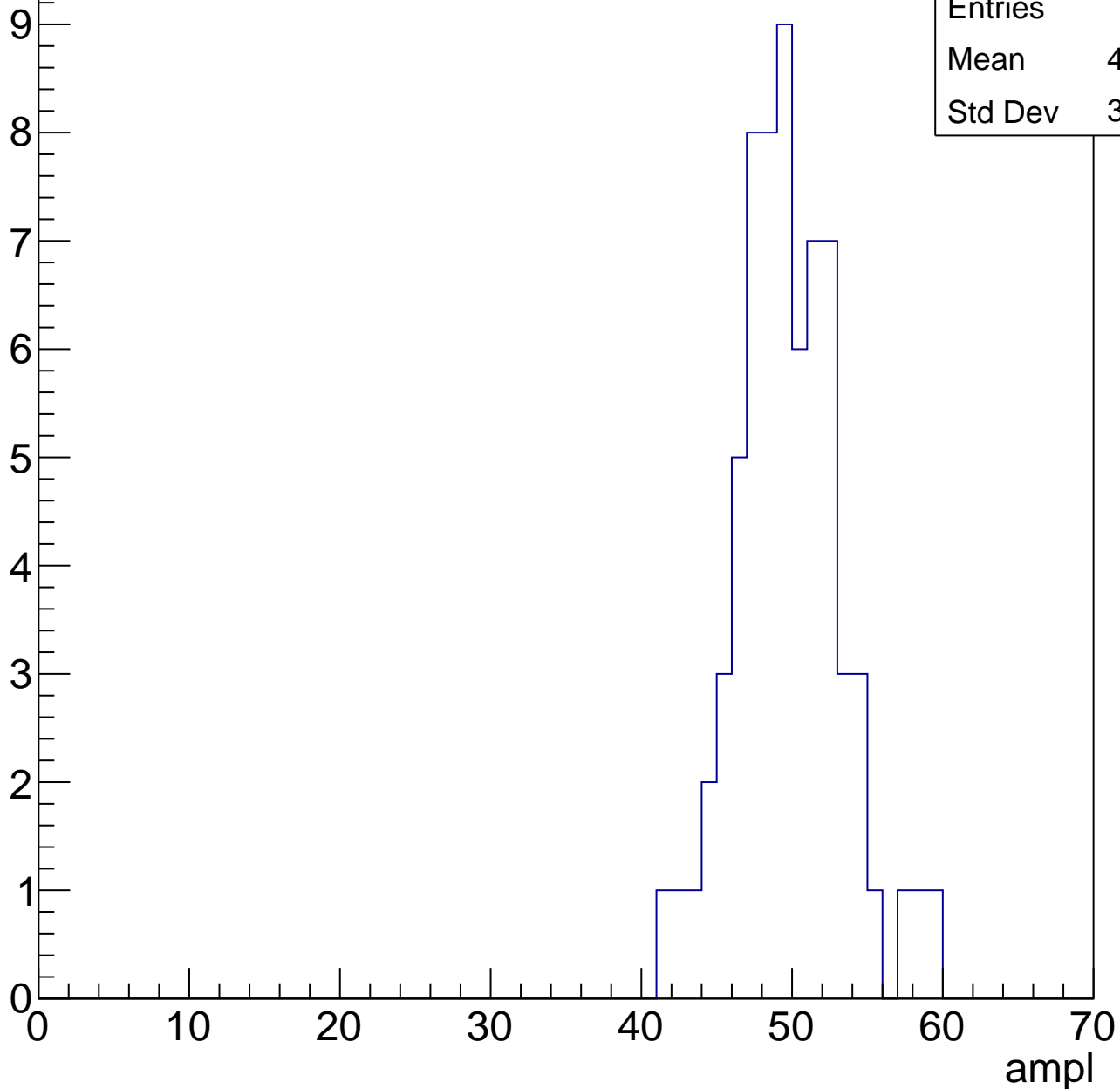


B1L102S, U13-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.28
Std Dev	3.489

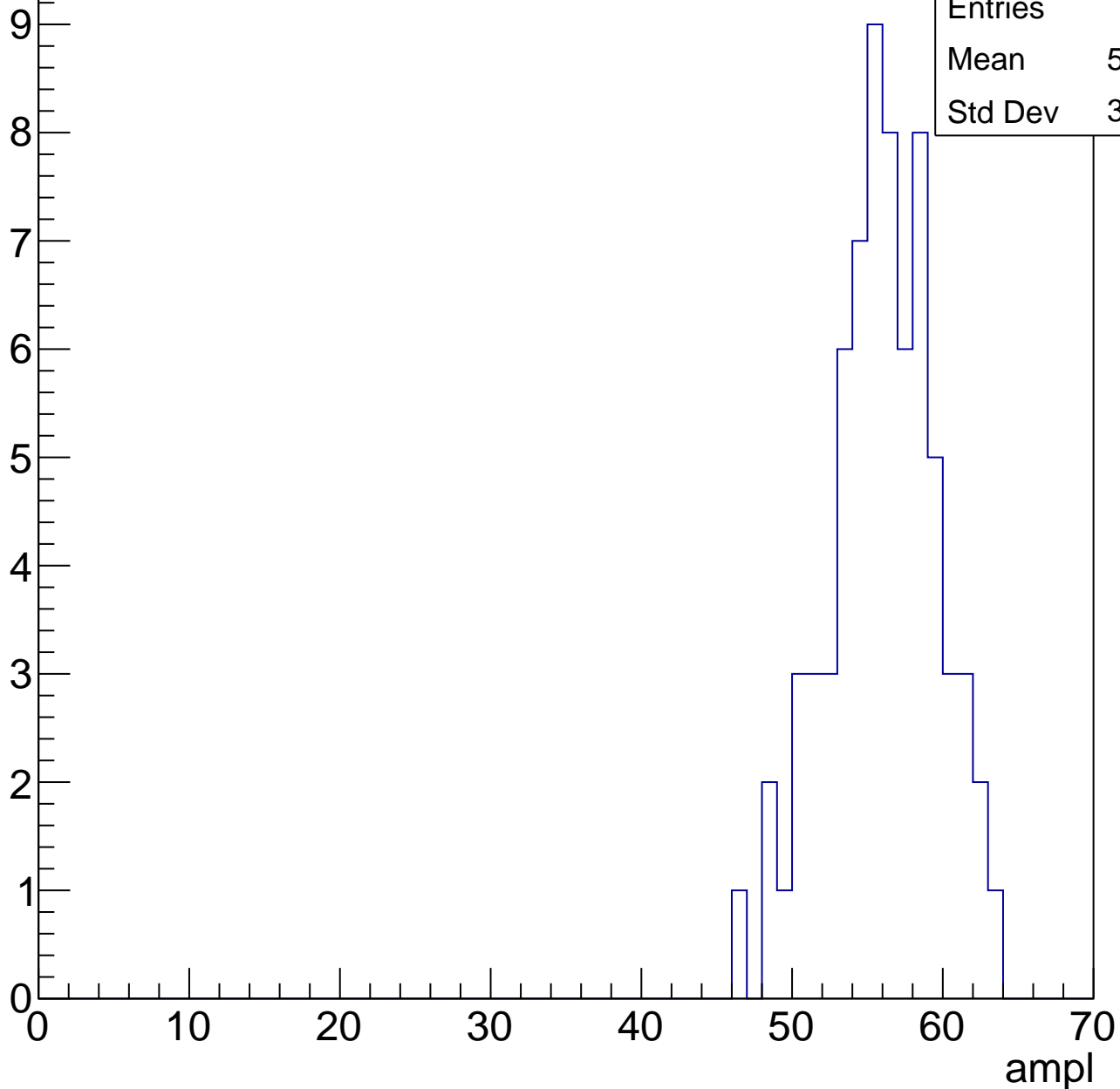


B1L102S, U13-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	55.49
Std Dev	3.576

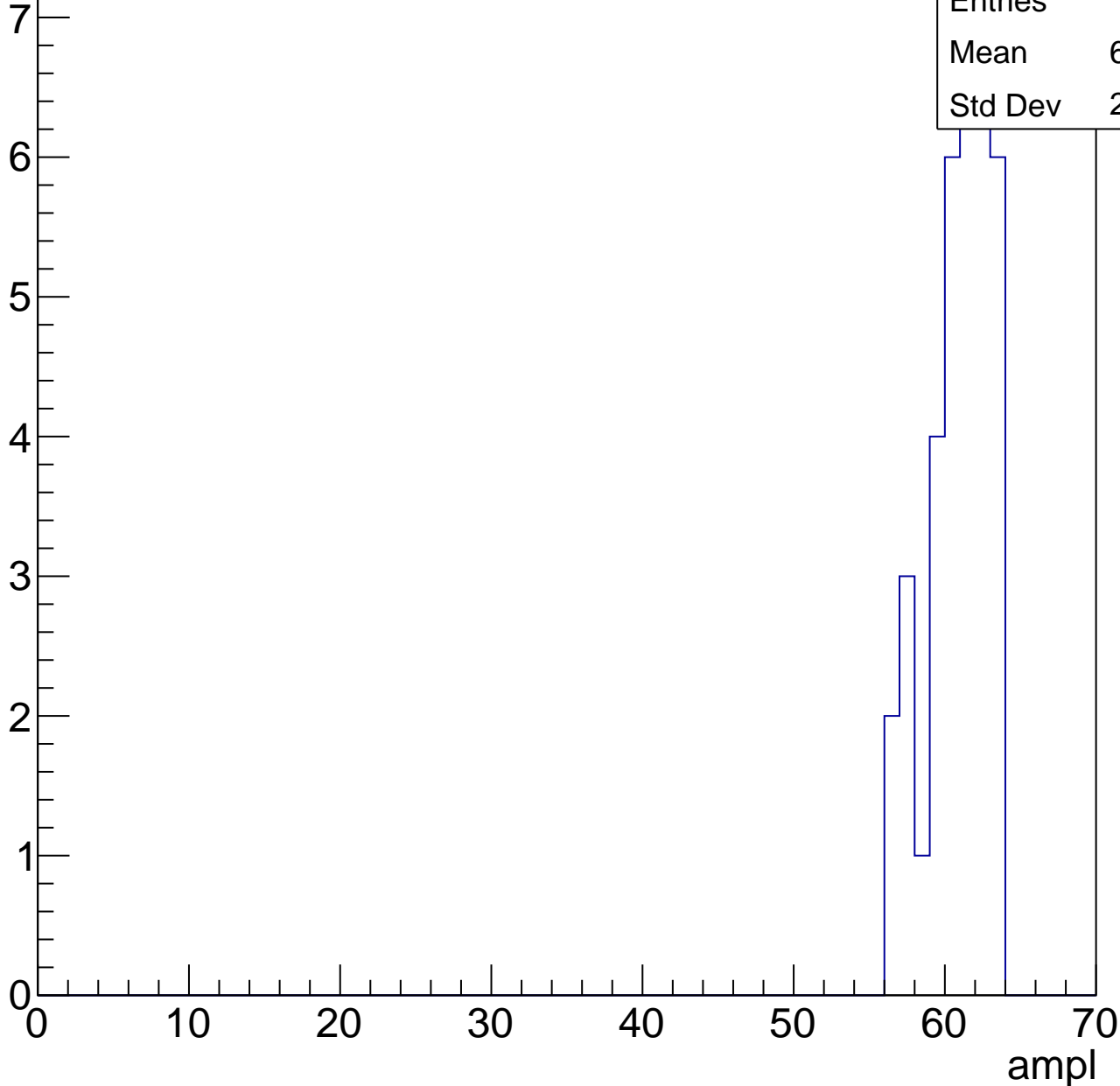


B1L102S, U13-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

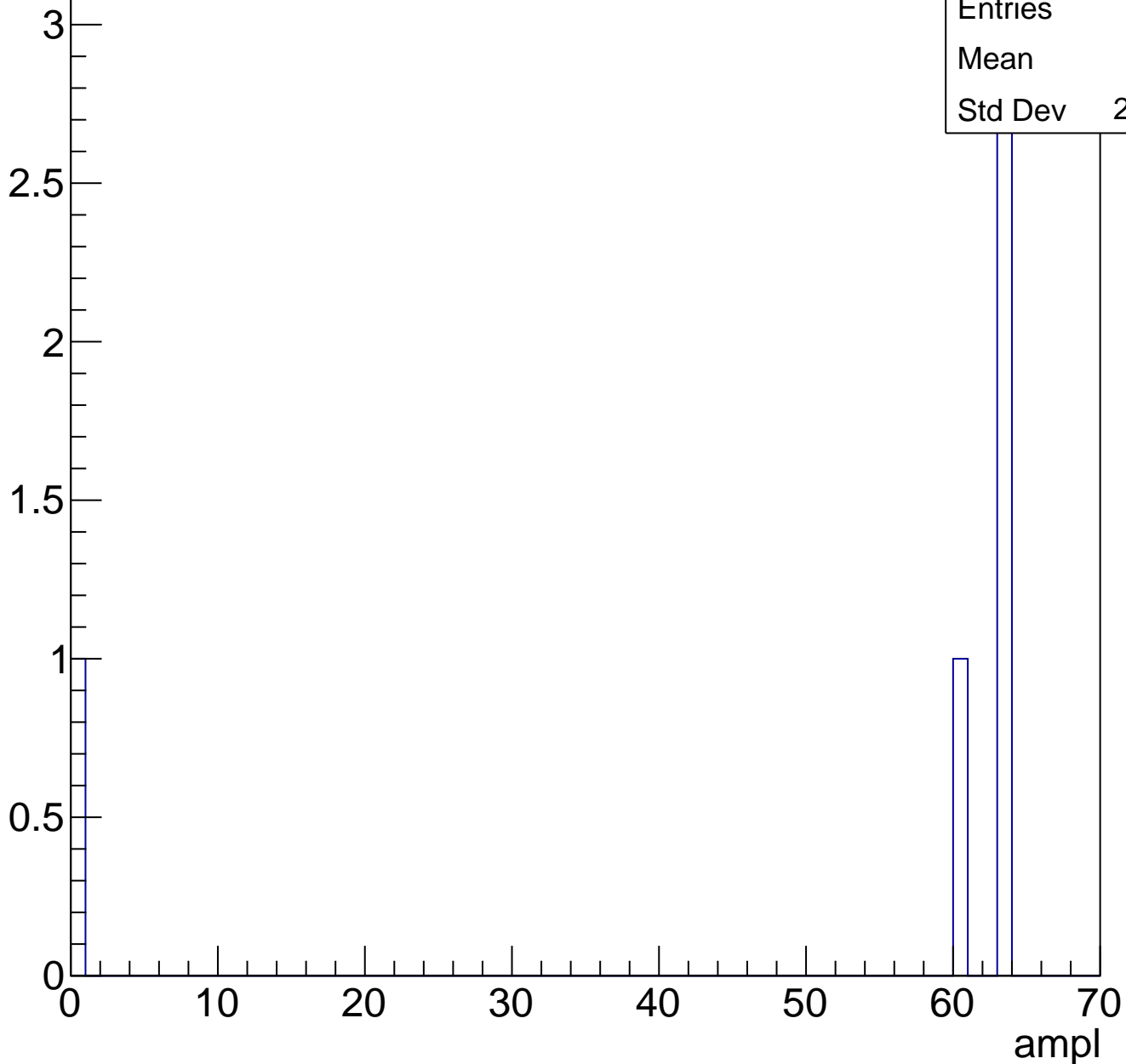
Entries	36
Mean	60.44
Std Dev	2.034



B1L102S, U13-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch59, adc0

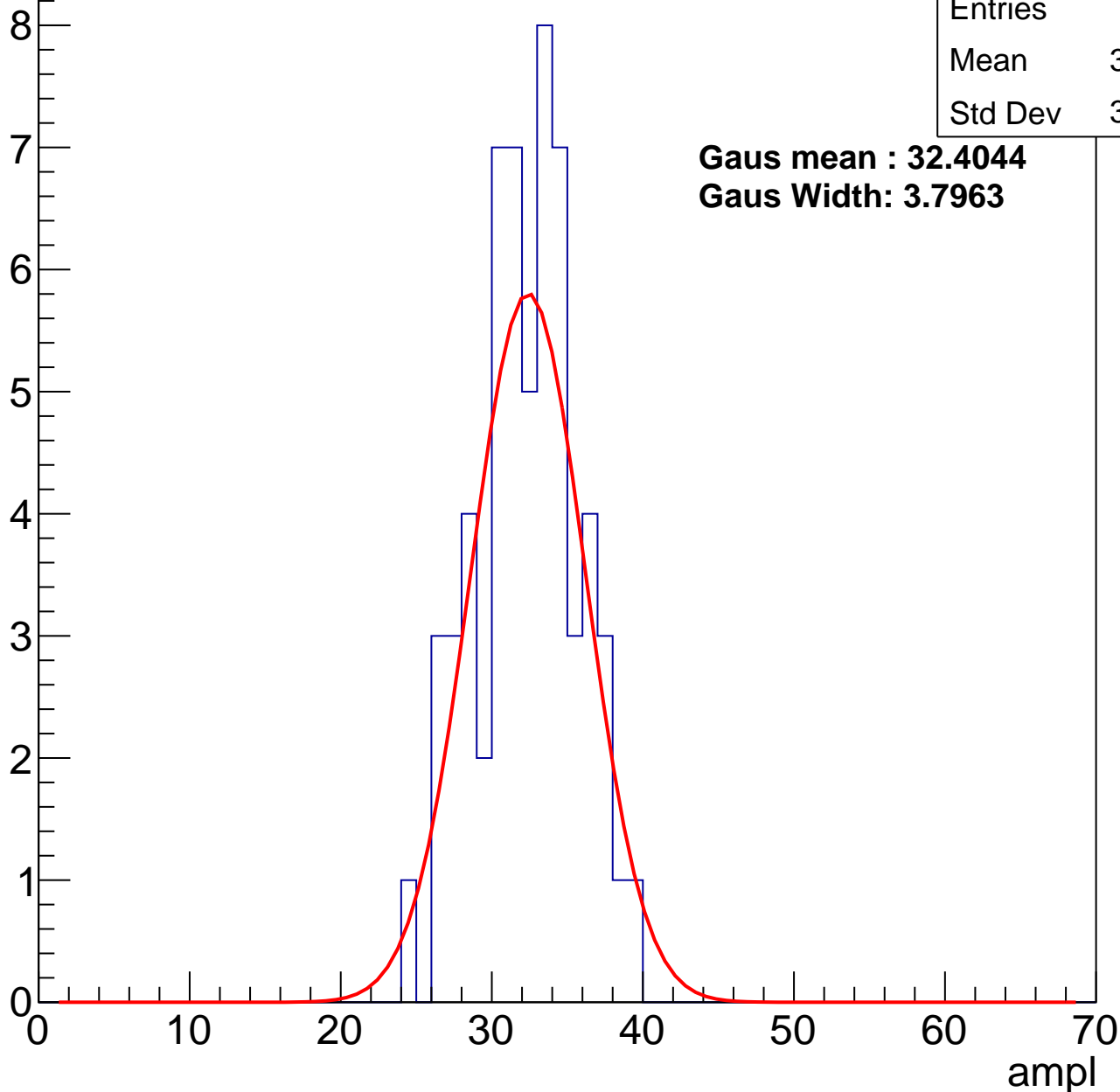
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	31.85
Std Dev	3.318

Gaus mean : 32.4044

Gaus Width: 3.7963



B1L102S, U13-ch59, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	37.81
Std Dev	3.426

Gaus mean : 38.3387

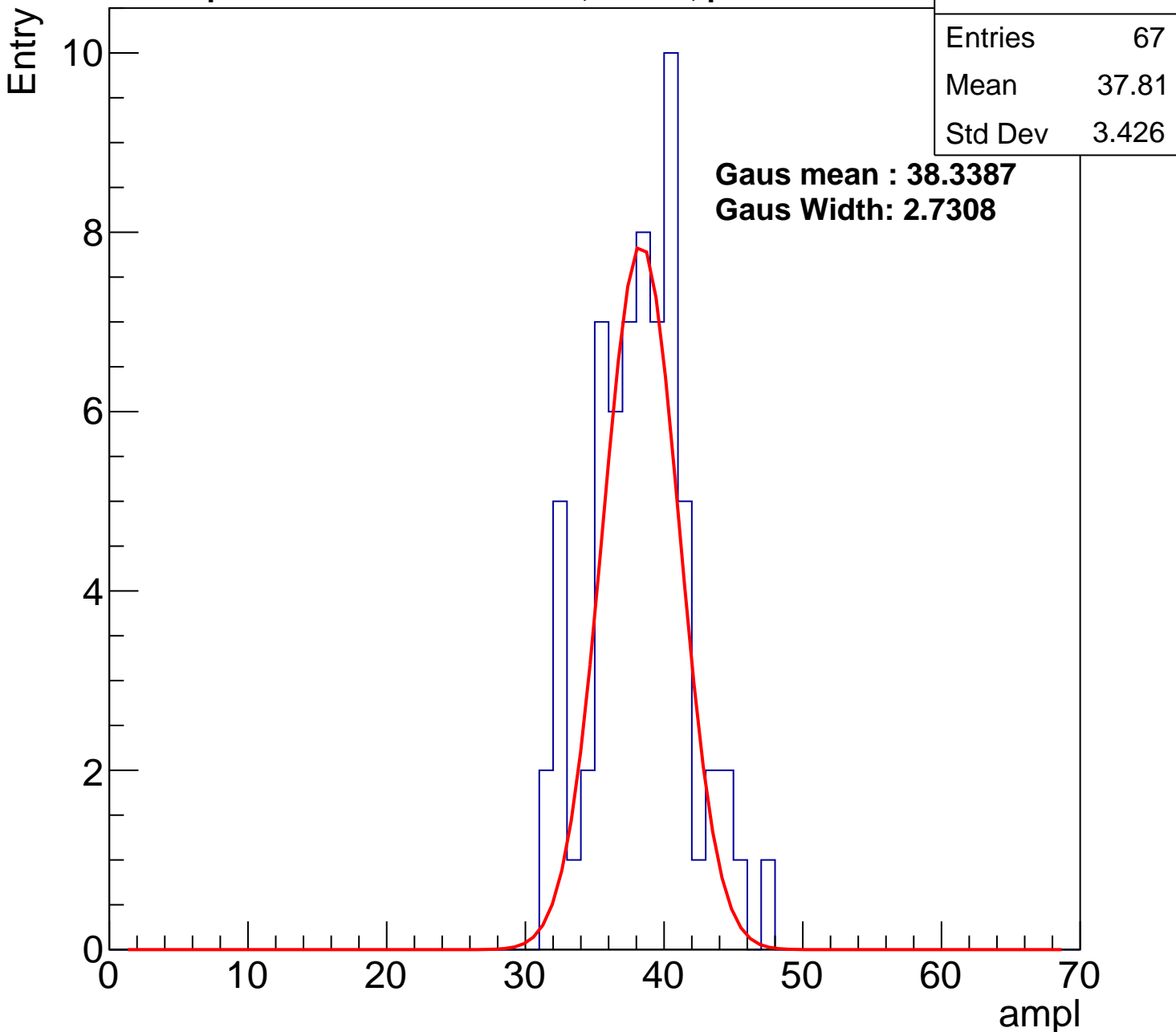
Gaus Width: 2.7308

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch59, adc2

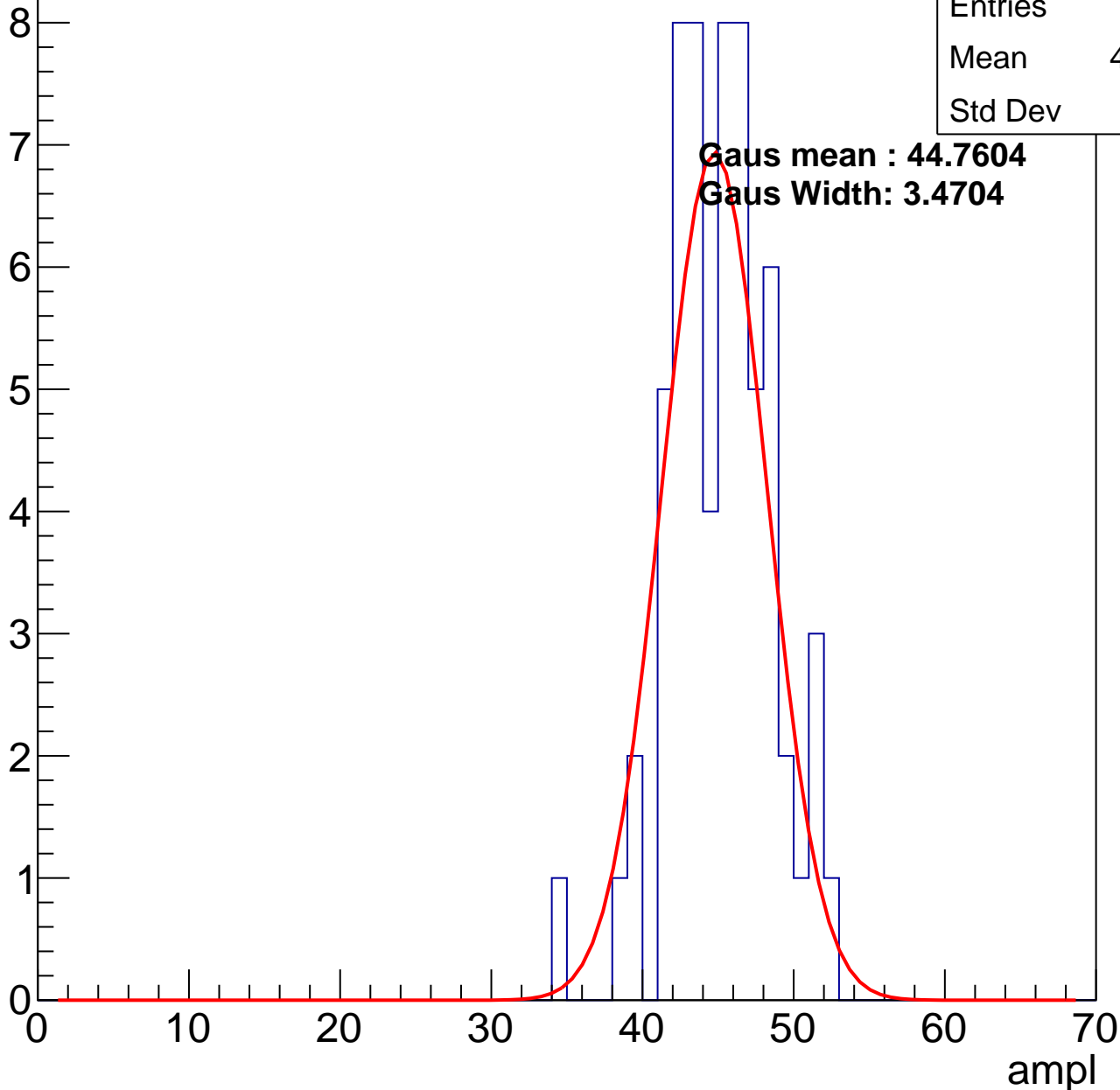
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	44.68
Std Dev	3.38

Gaus mean : 44.7604

Gaus Width: 3.4704

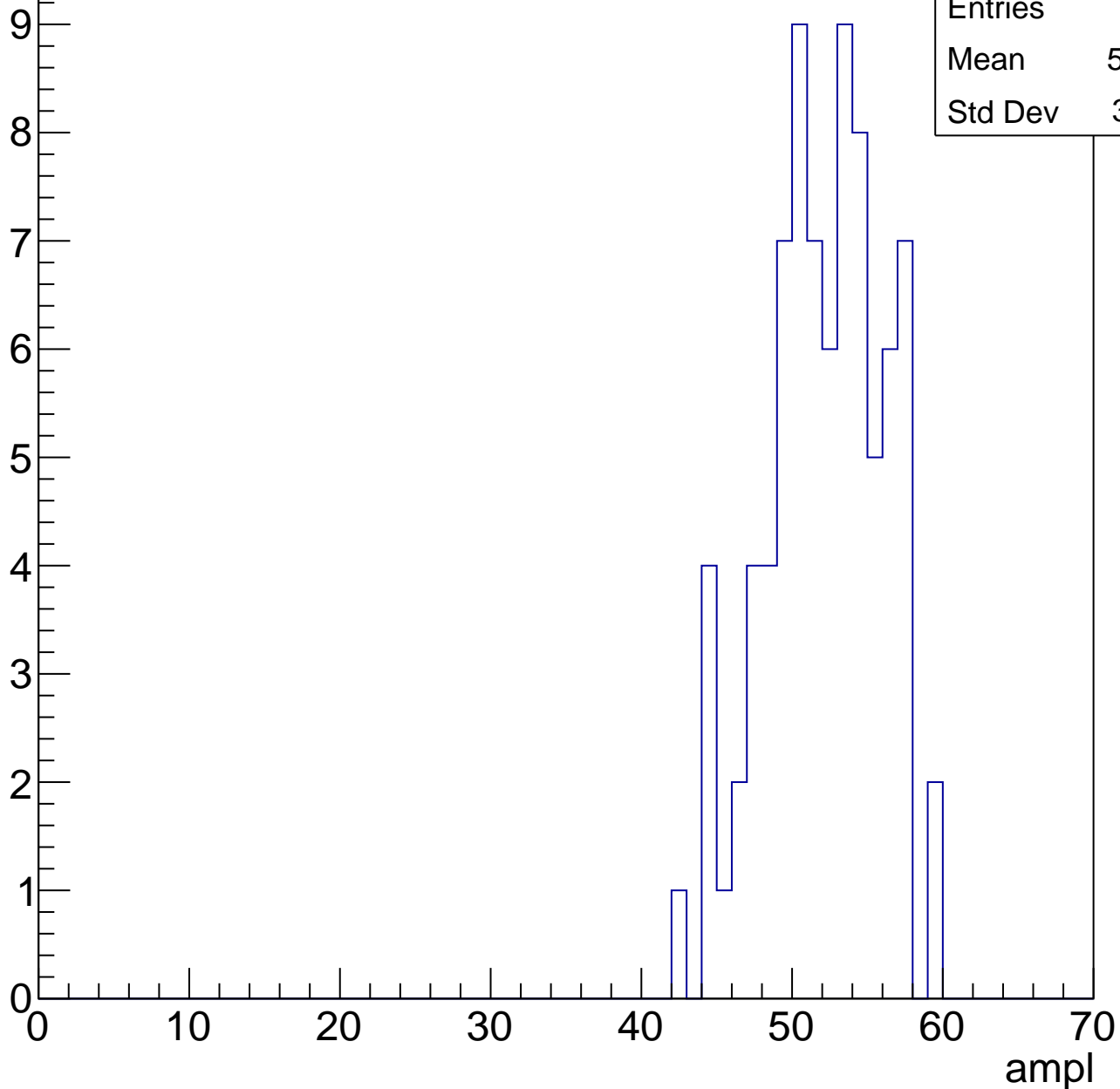


B1L102S, U13-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	51.63
Std Dev	3.811

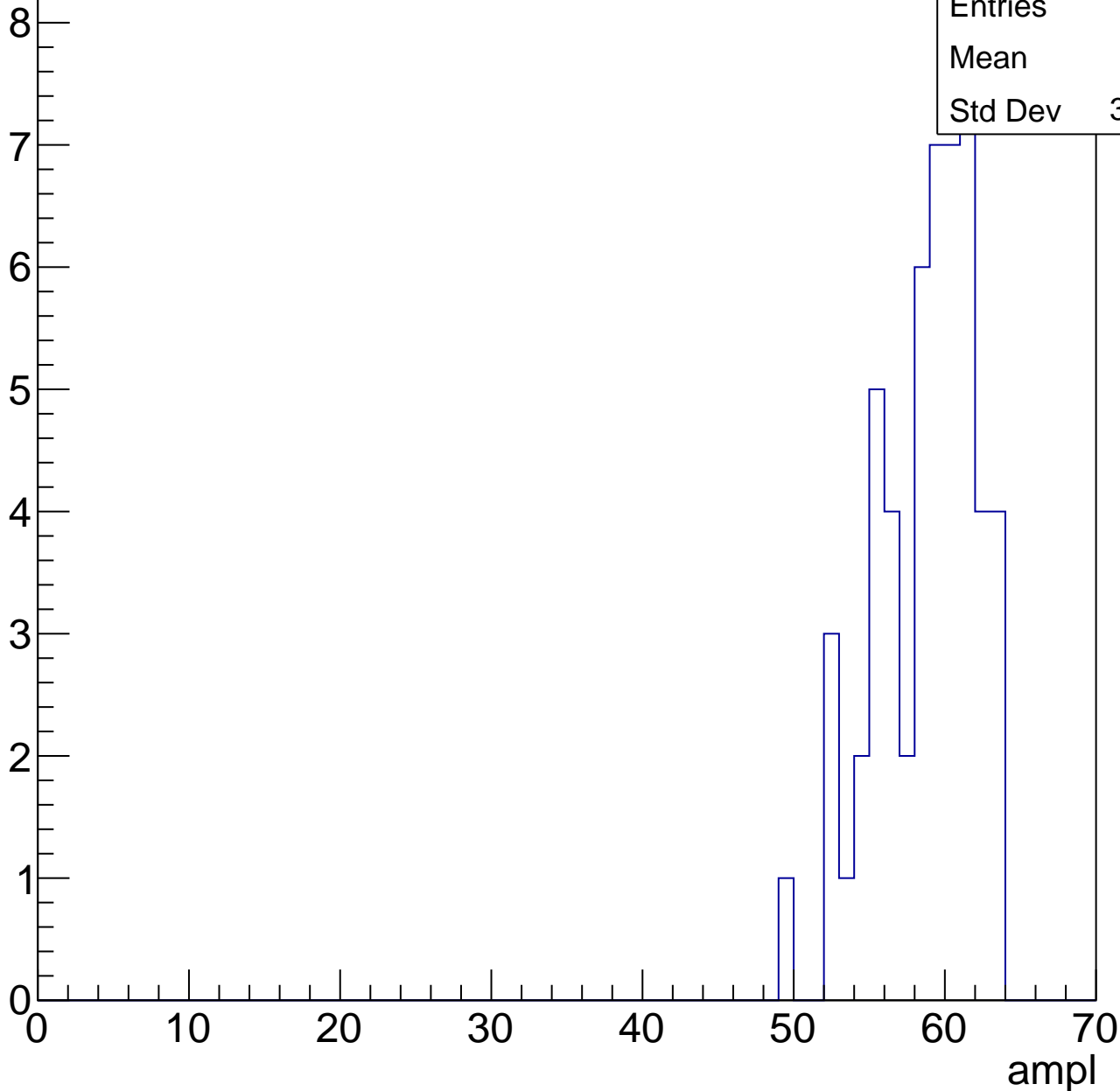


B1L102S, U13-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.3
Std Dev	3.264

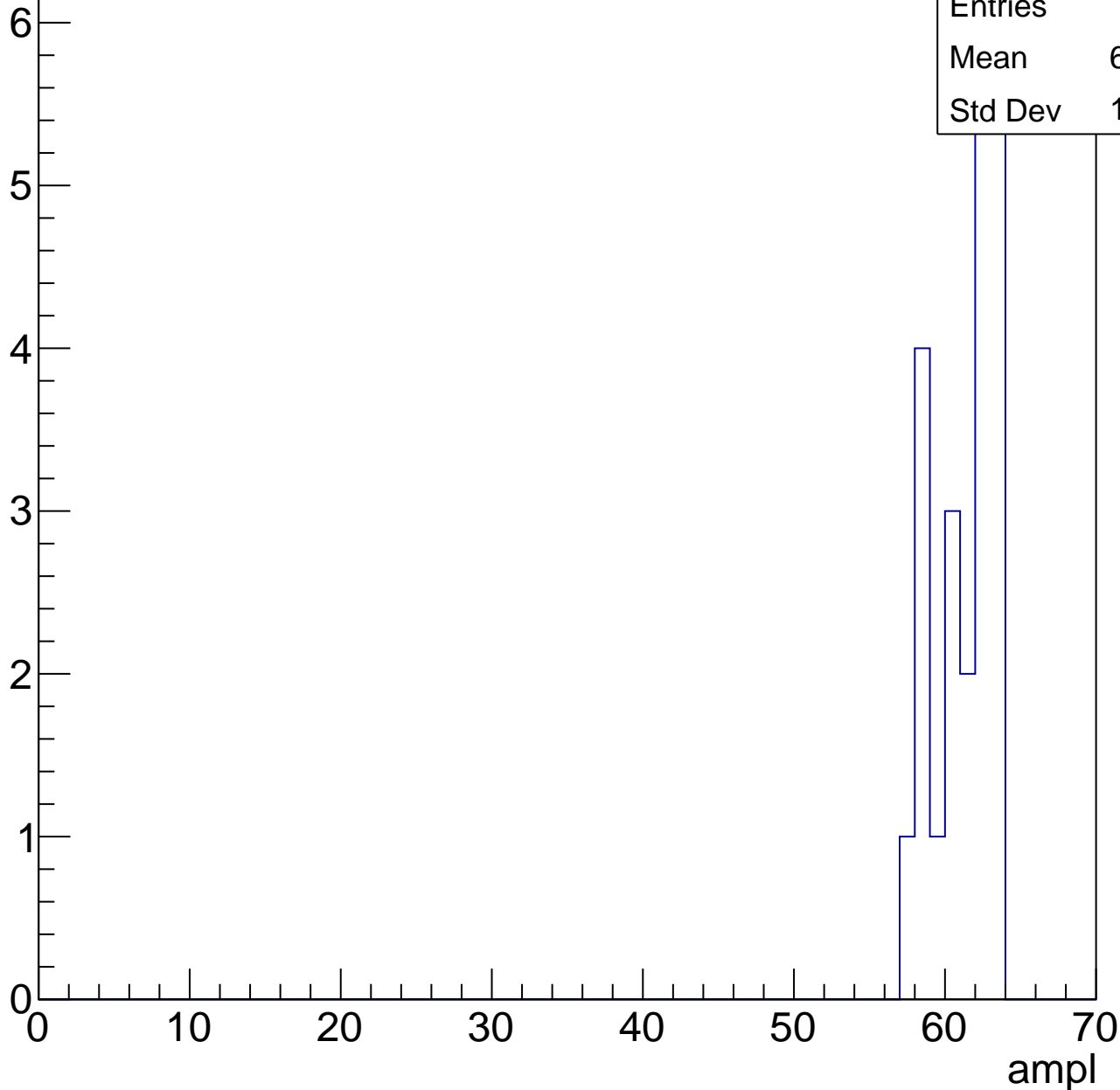


B1L102S, U13-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	60.87
Std Dev	1.963



B1L102S, U13-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch60, adc0

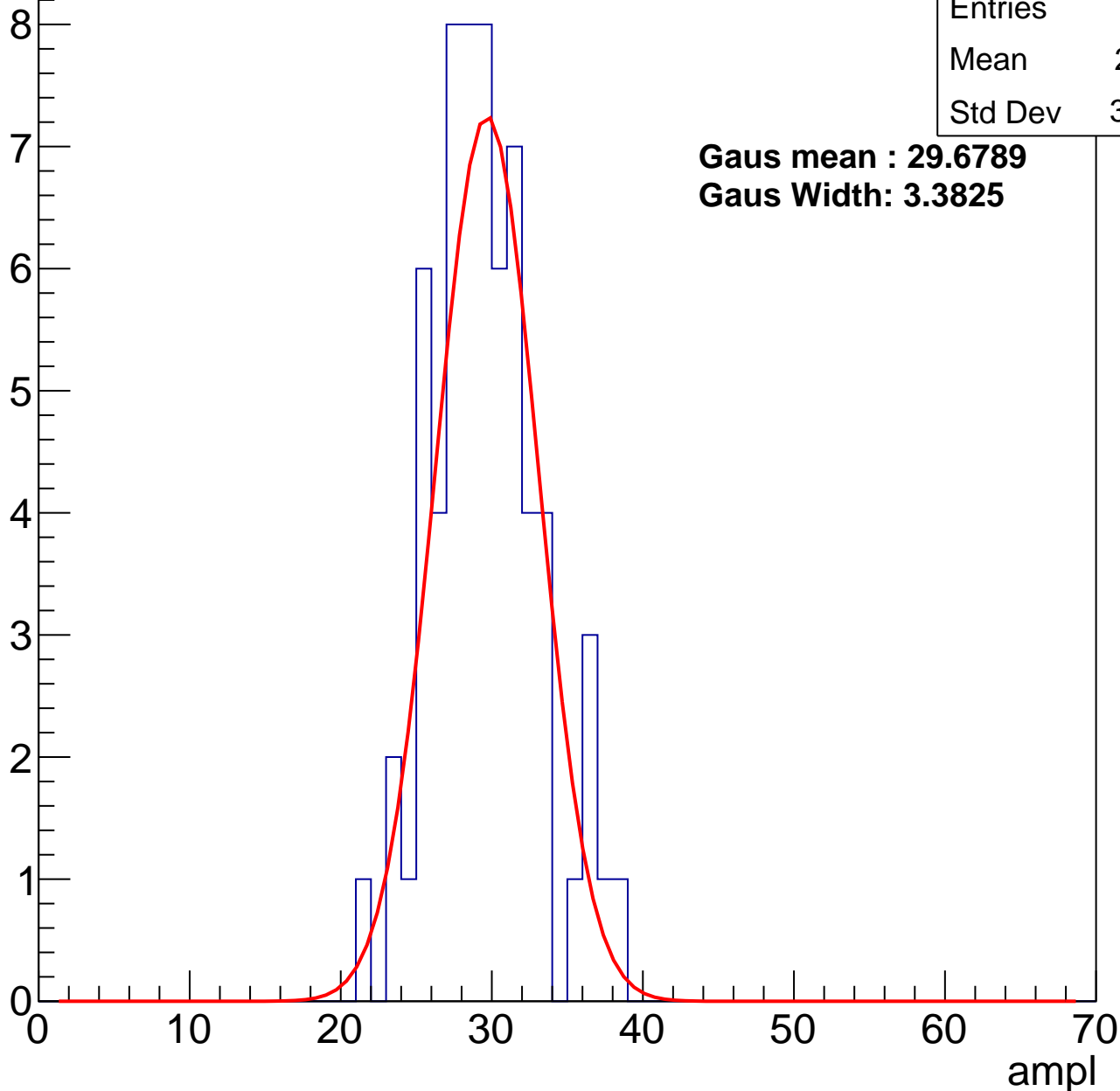
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.11
Std Dev	3.496

Gaus mean : 29.6789

Gaus Width: 3.3825



B1L102S, U13-ch60, adc1

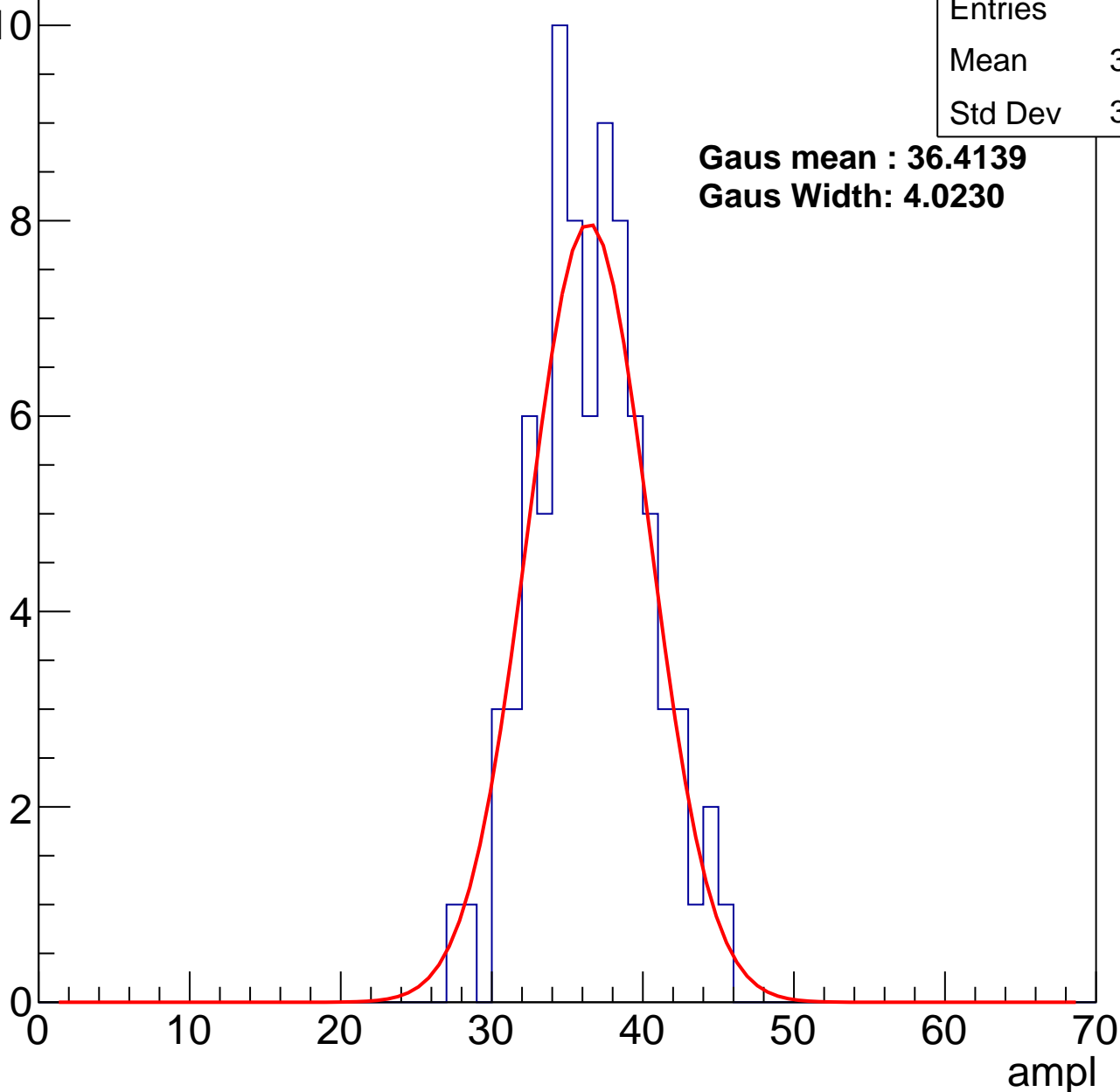
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.14
Std Dev	3.738

Gaus mean : 36.4139

Gaus Width: 4.0230



B1L102S, U13-ch60, adc2

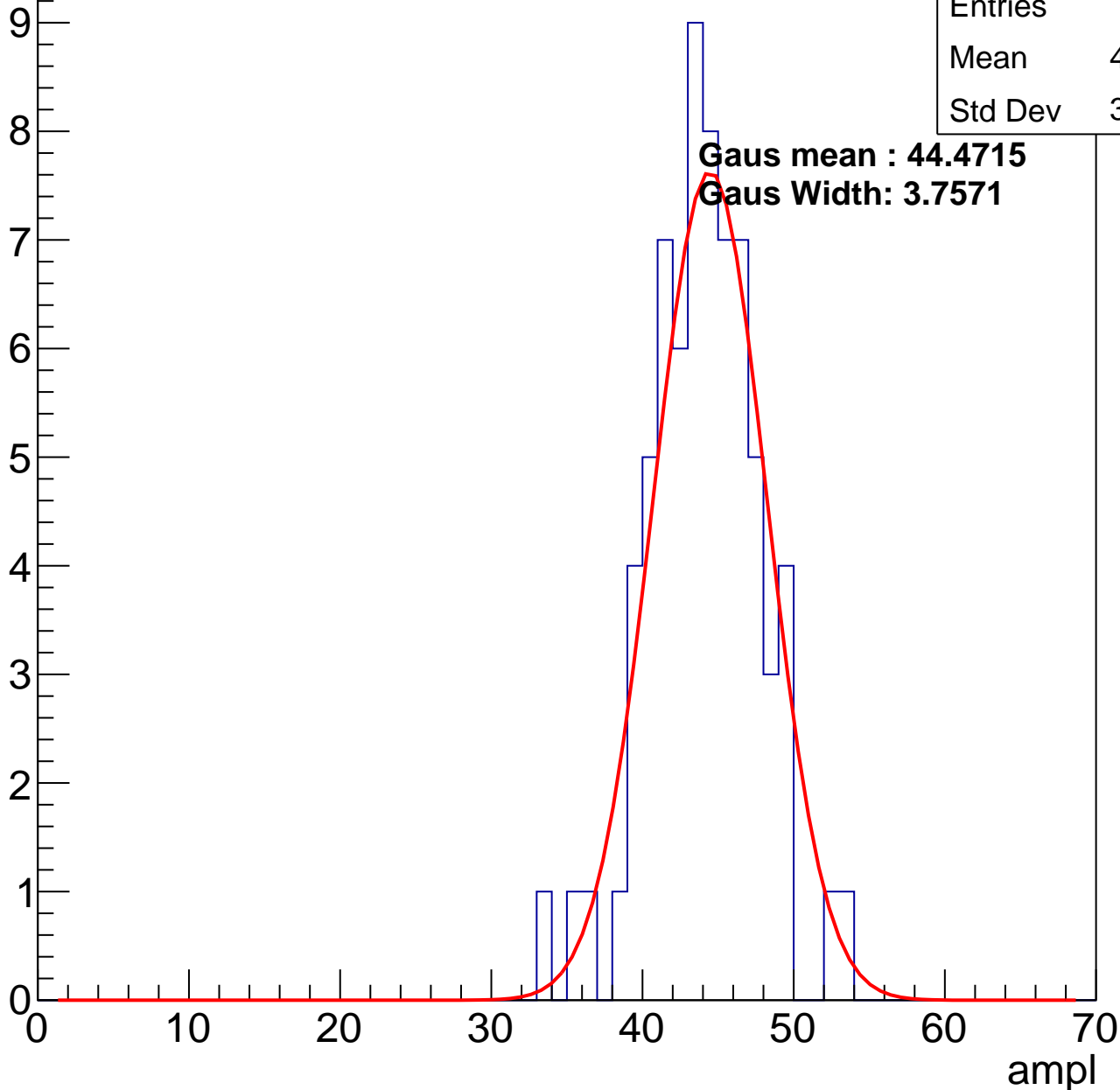
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.56
Std Dev	3.634

Gaus mean : 44.4715

Gaus Width: 3.7571

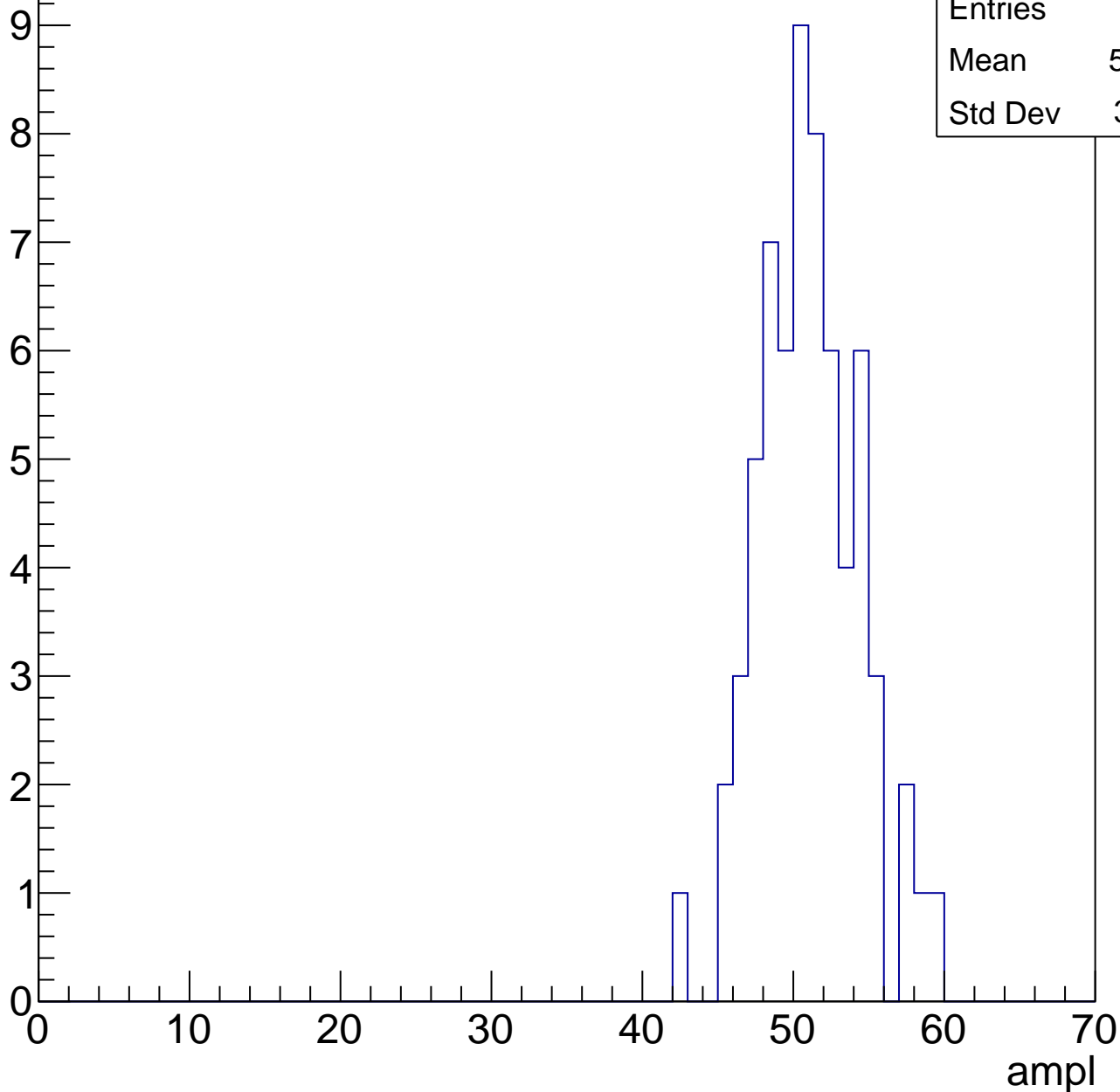


B1L102S, U13-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	50.58
Std Dev	3.311

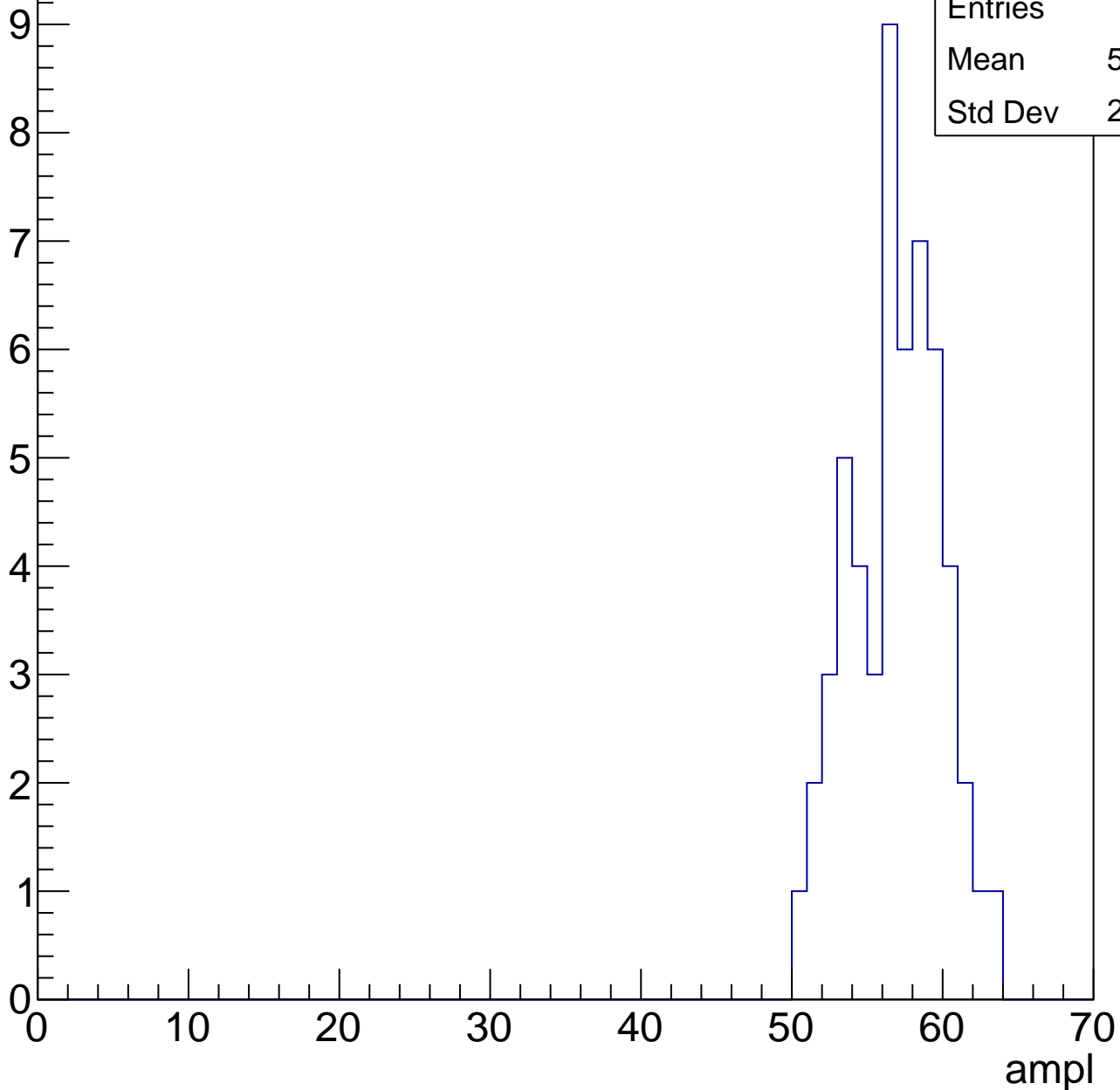


B1L102S, U13-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56.43
Std Dev	2.966

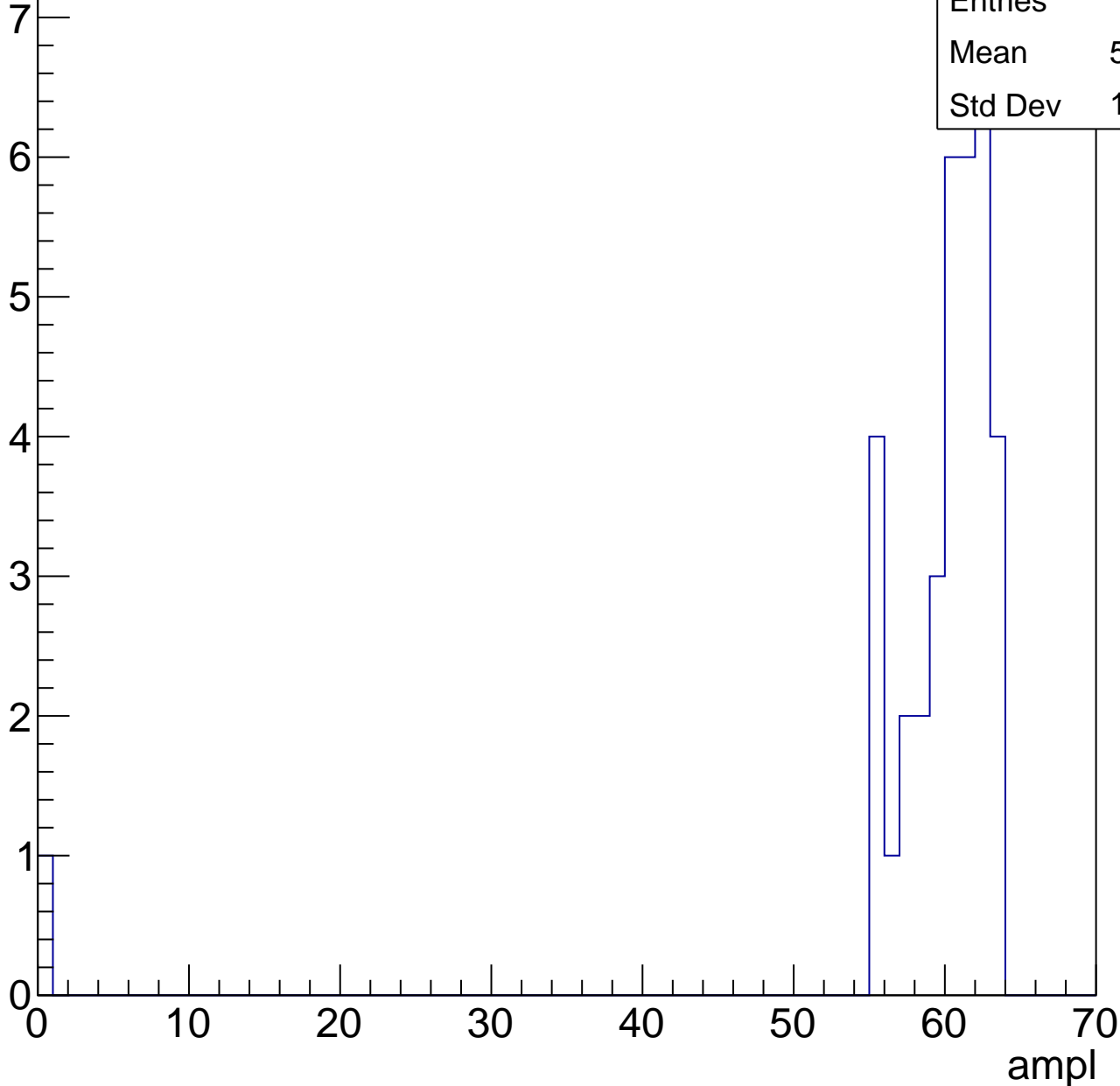


B1L102S, U13-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.19
Std Dev	10.13

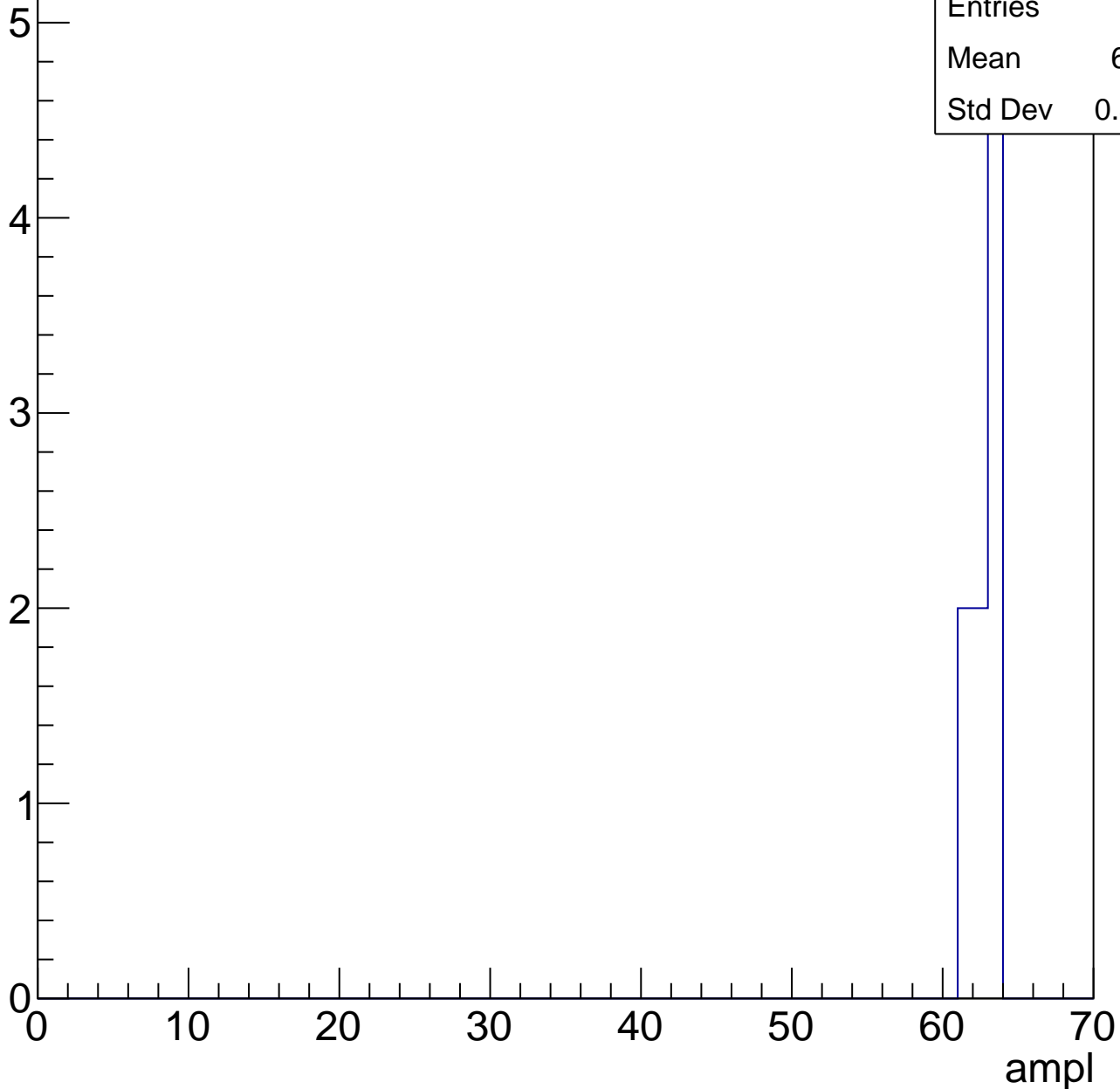


B1L102S, U13-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

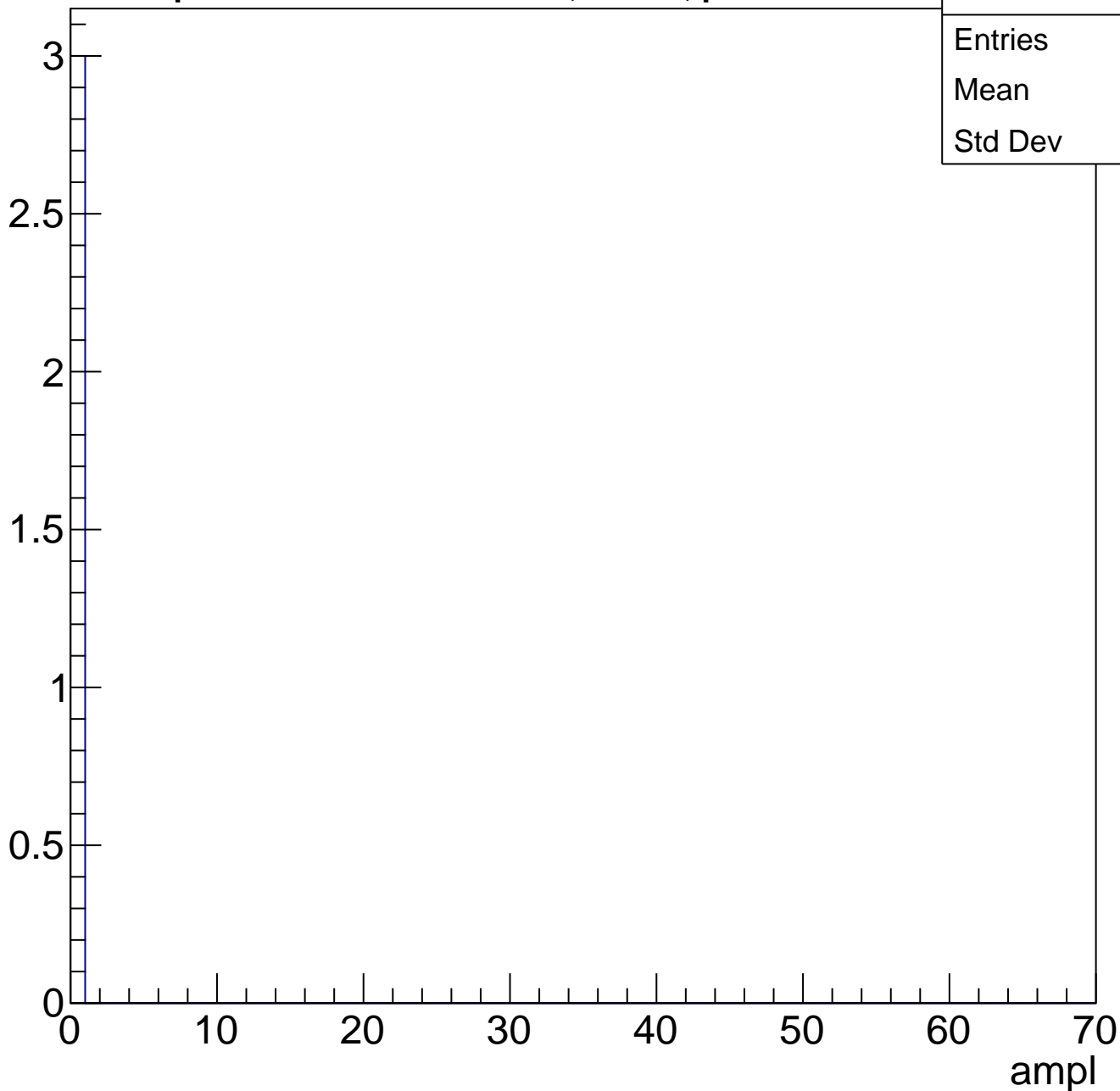
Entries	9
Mean	62.33
Std Dev	0.8165



B1L102S, U13-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch61, adc0

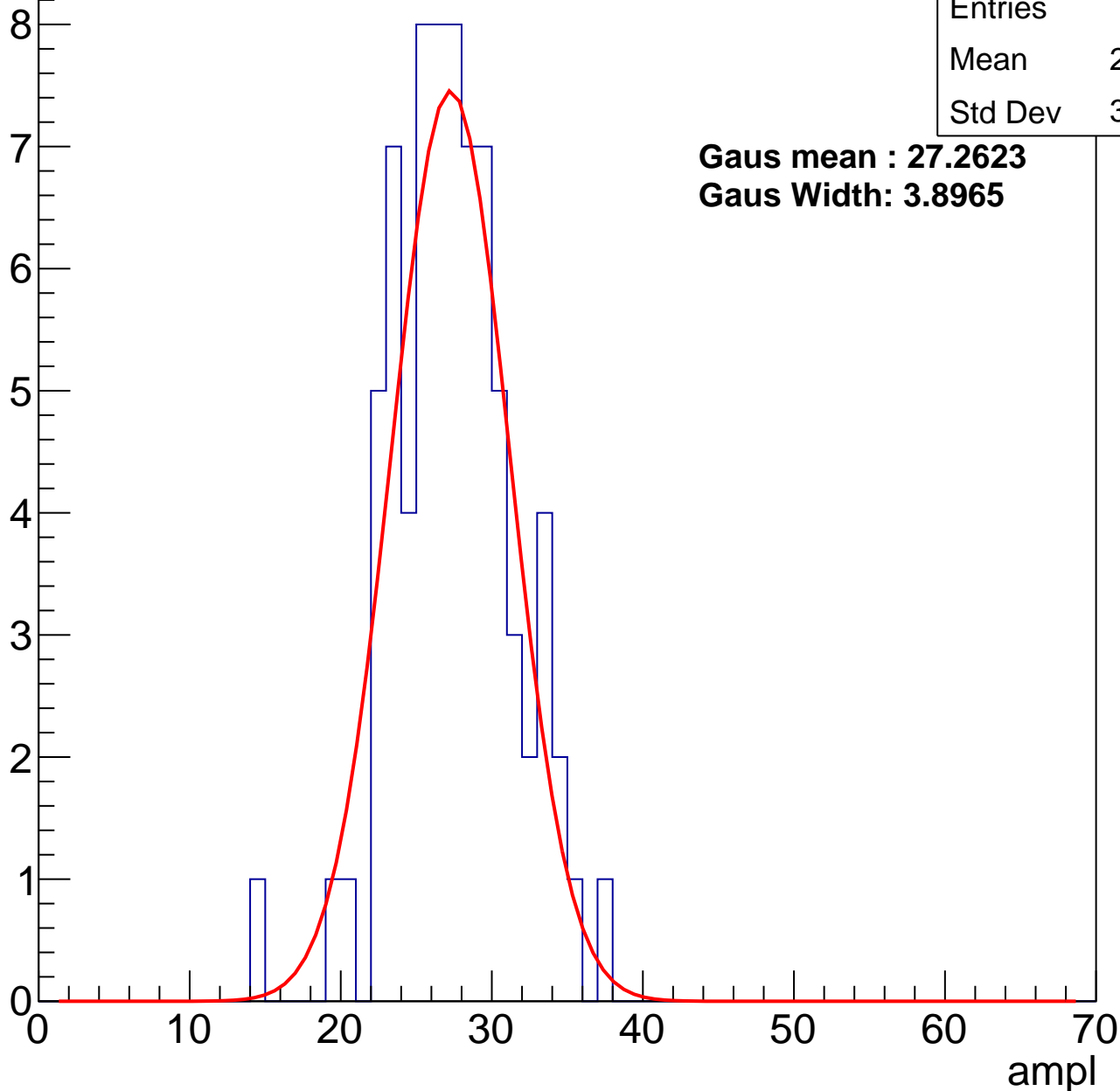
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	26.96
Std Dev	3.965

Gaus mean : 27.2623

Gaus Width: 3.8965



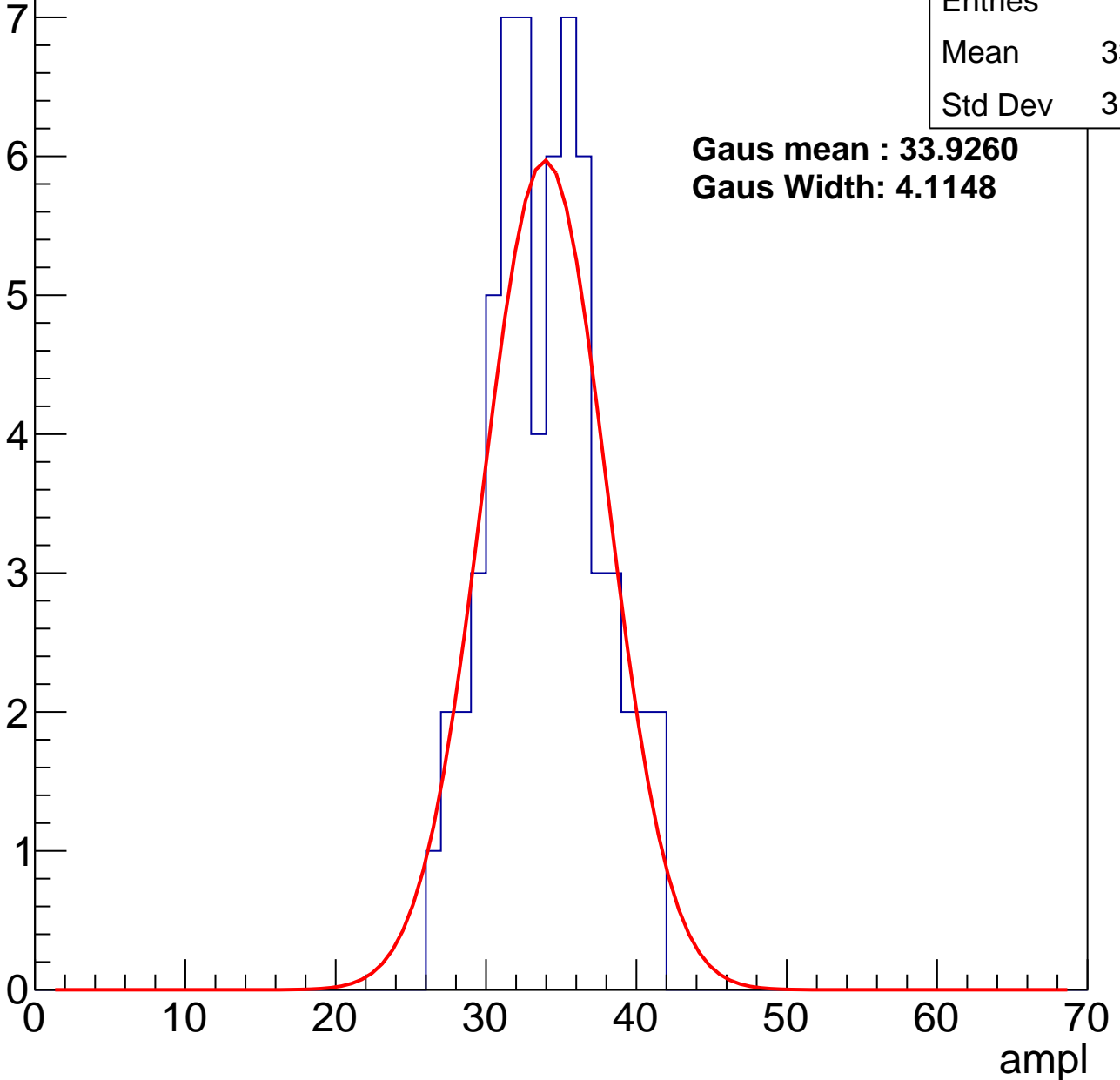
B1L102S, U13-ch61, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	33.48
Std Dev	3.577

Gaus mean : 33.9260
Gaus Width: 4.1148



B1L102S, U13-ch61, adc2

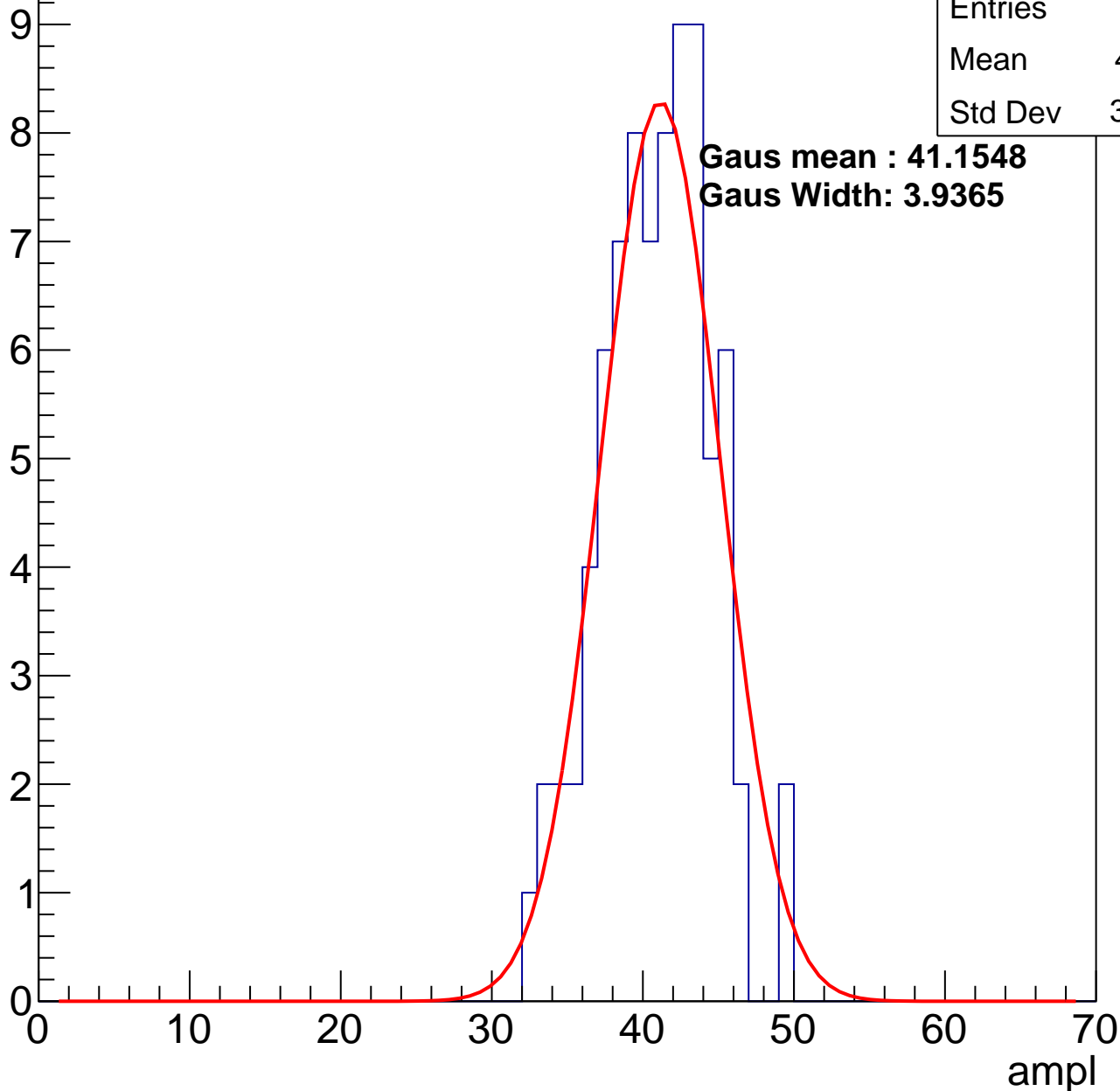
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	40.41
Std Dev	3.559

Gaus mean : 41.1548

Gaus Width: 3.9365

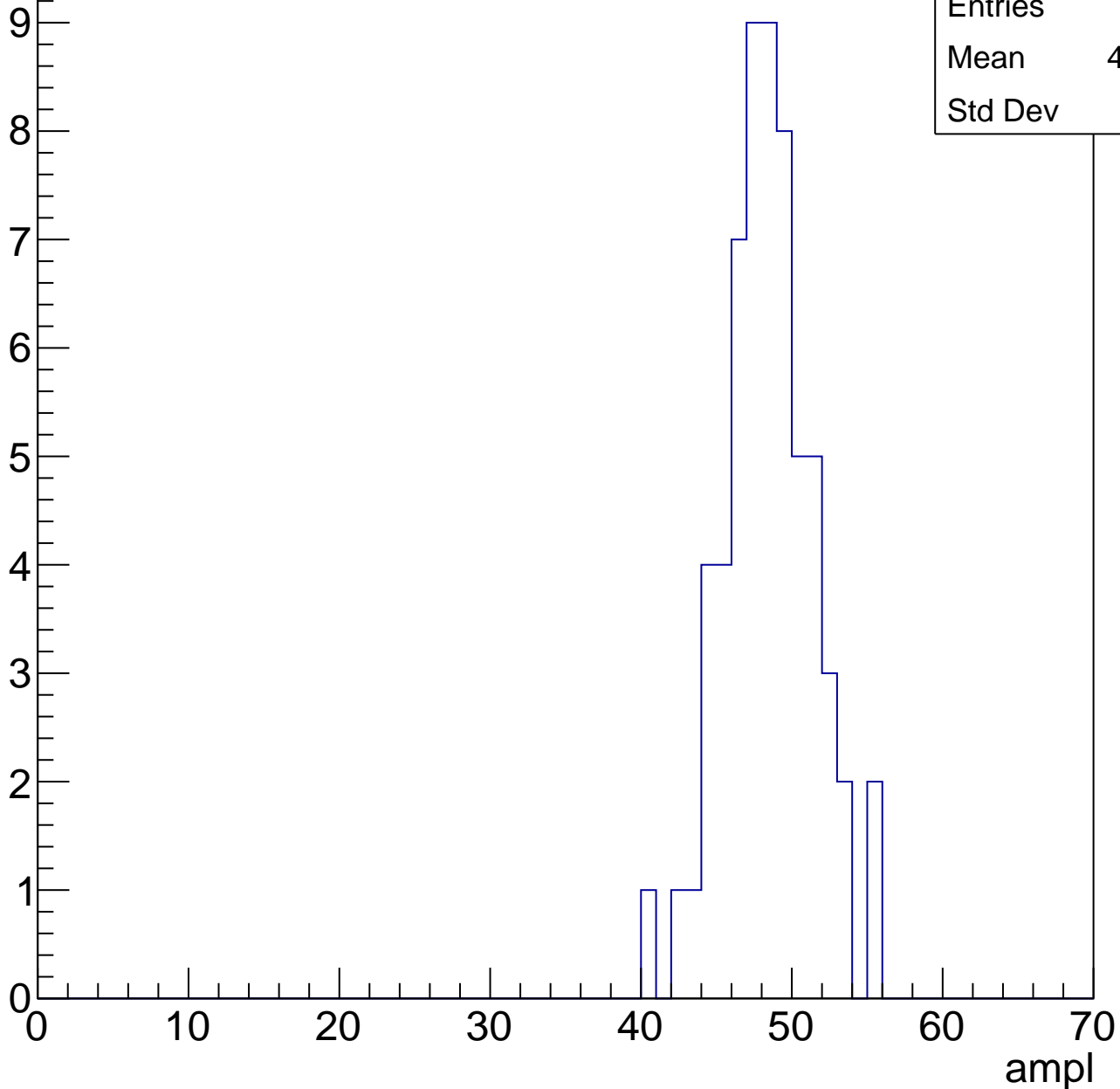


B1L102S, U13-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.98
Std Dev	2.95

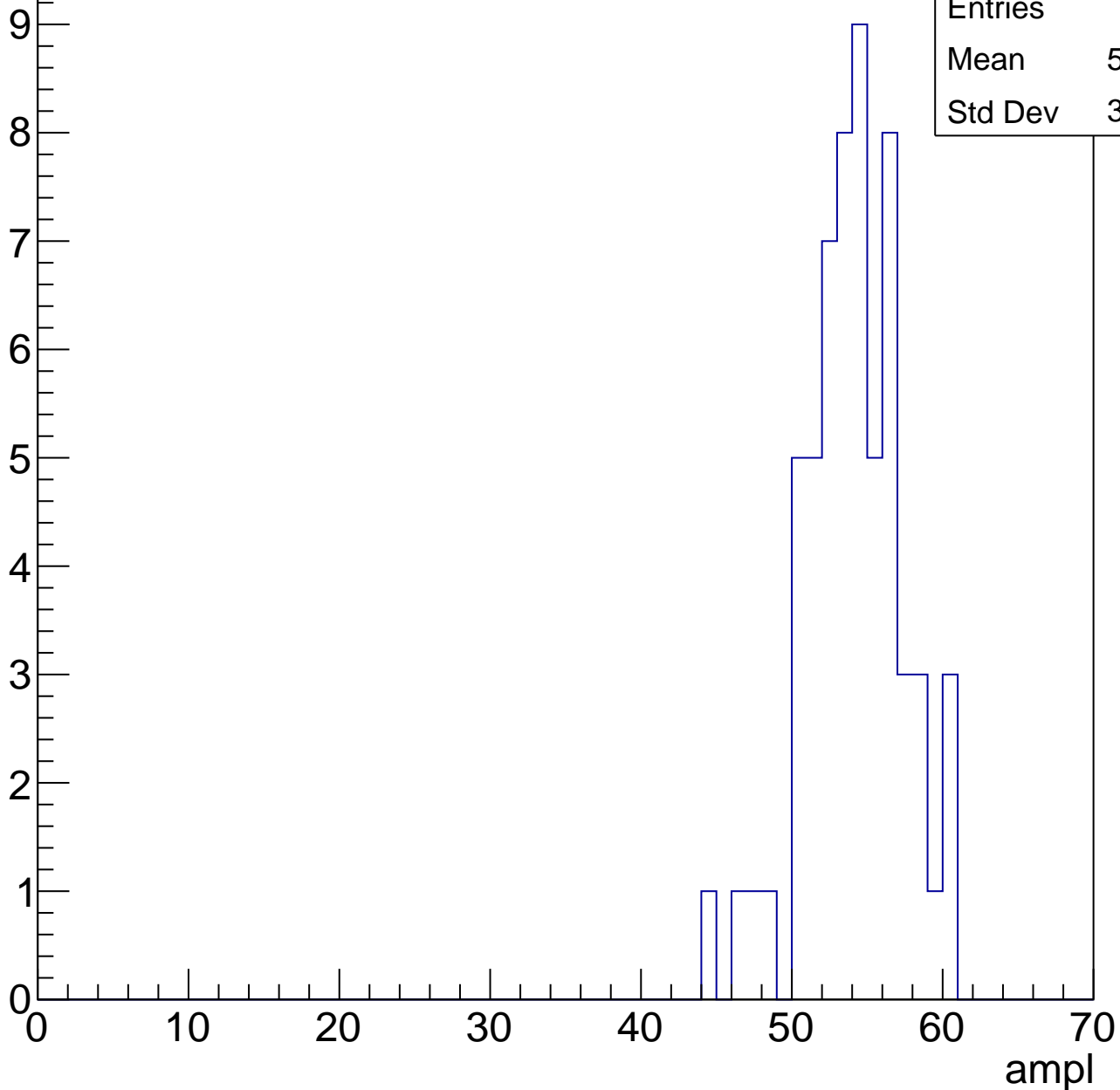


B1L102S, U13-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.62
Std Dev	3.255

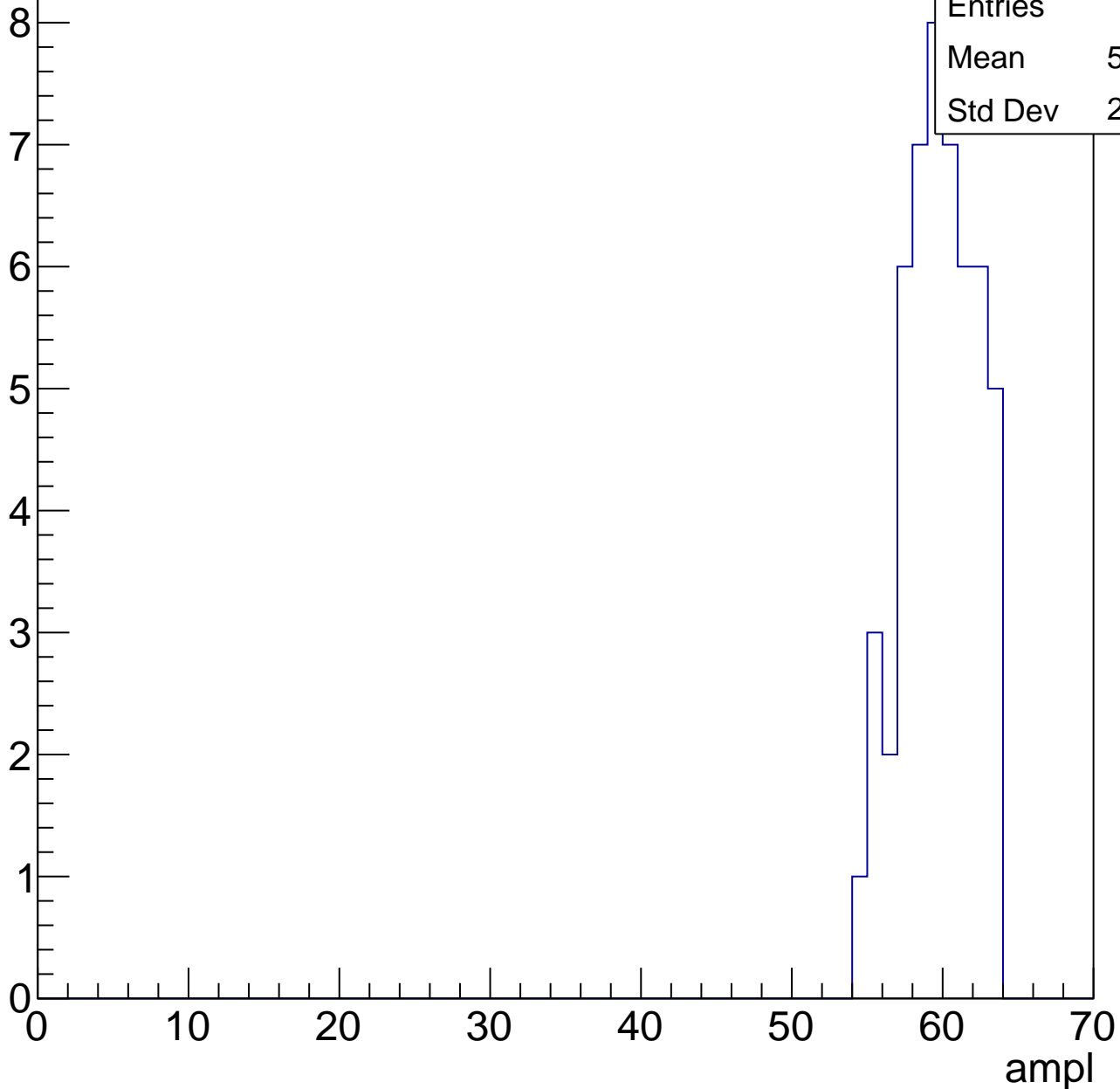


B1L102S, U13-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.29
Std Dev	2.354

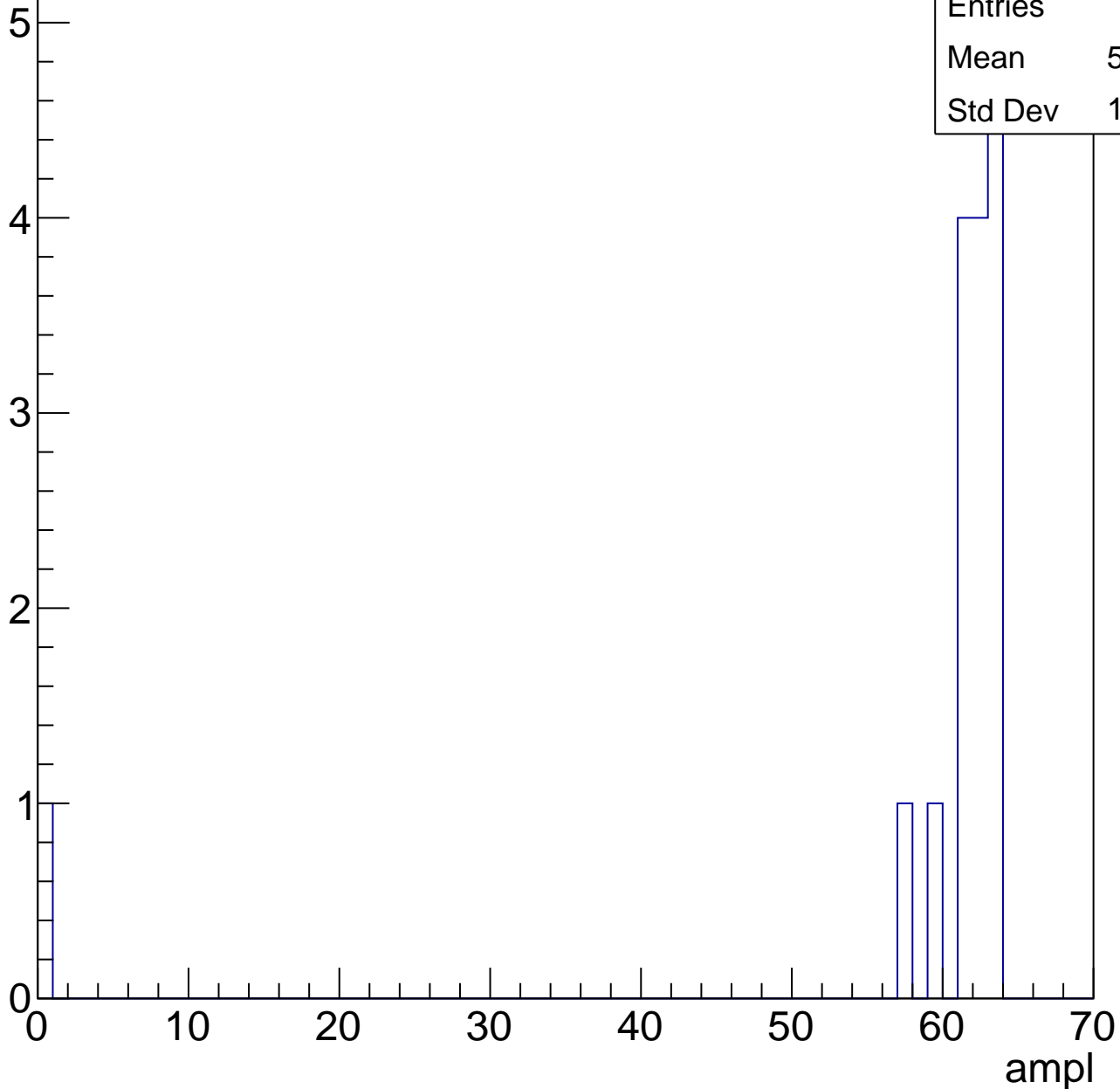


B1L102S, U13-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

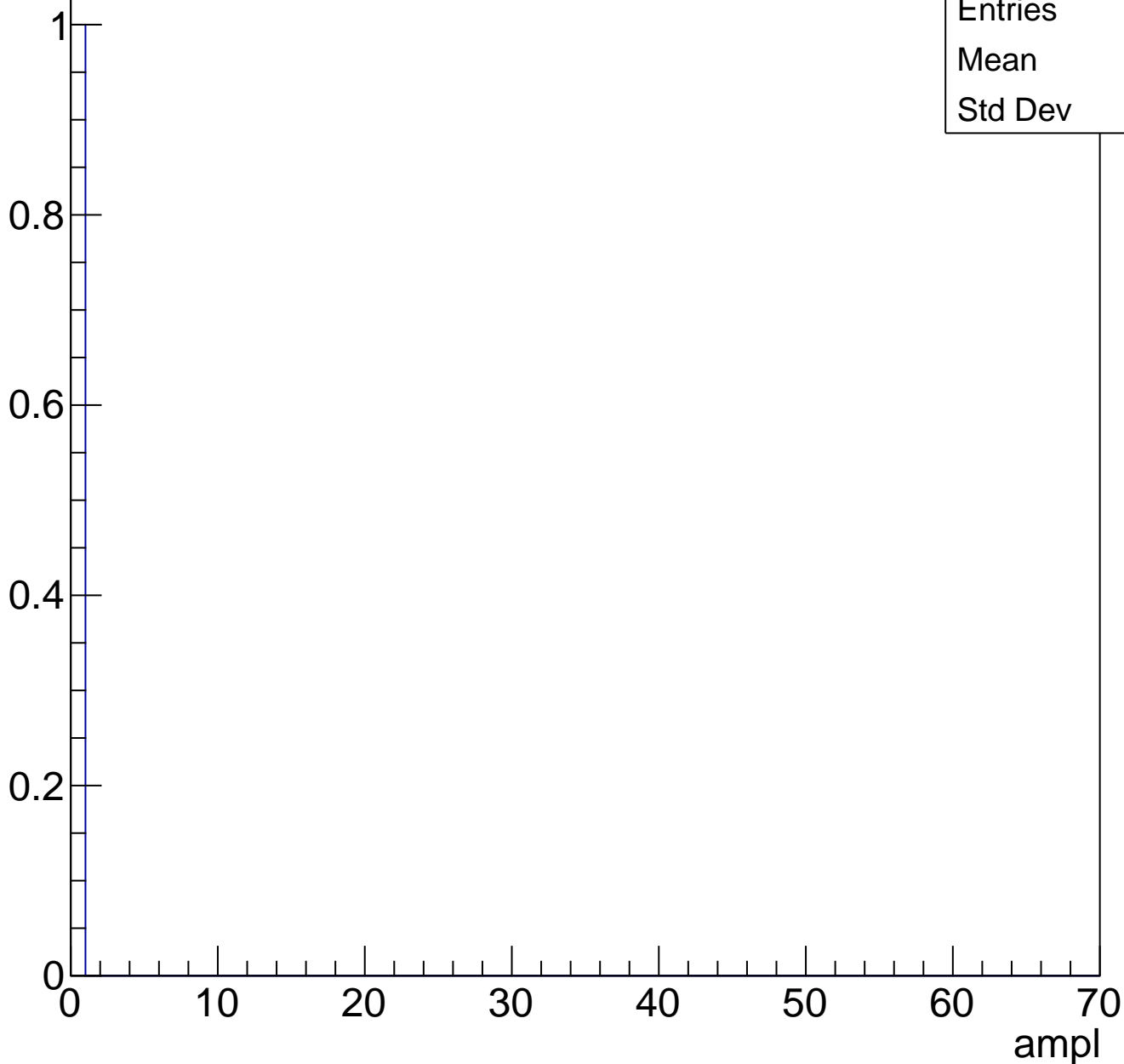
Entries	16
Mean	57.69
Std Dev	14.98



B1L102S, U13-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch62, adc0

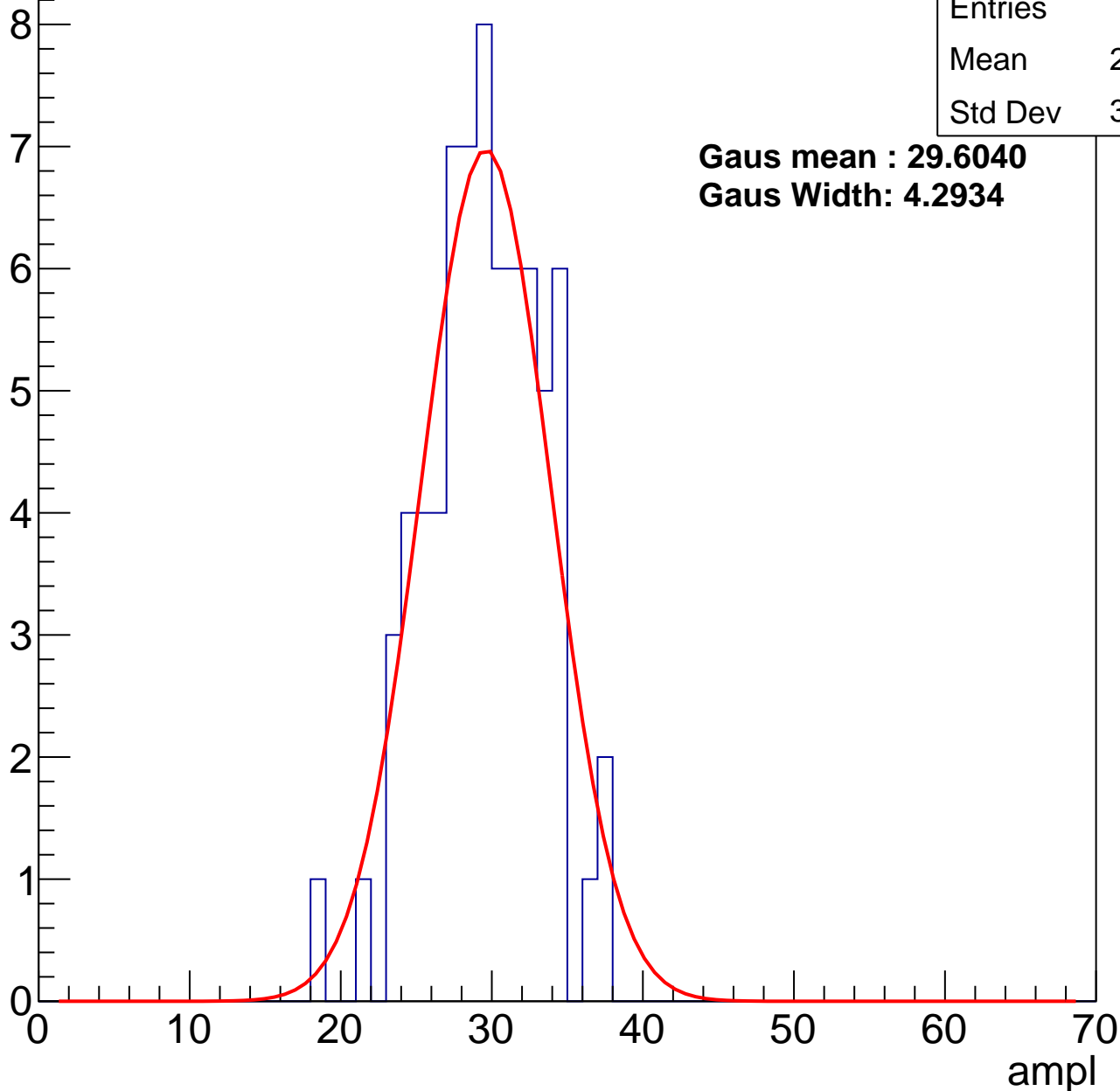
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.04
Std Dev	3.788

Gaus mean : 29.6040

Gaus Width: 4.2934



B1L102S, U13-ch62, adc1

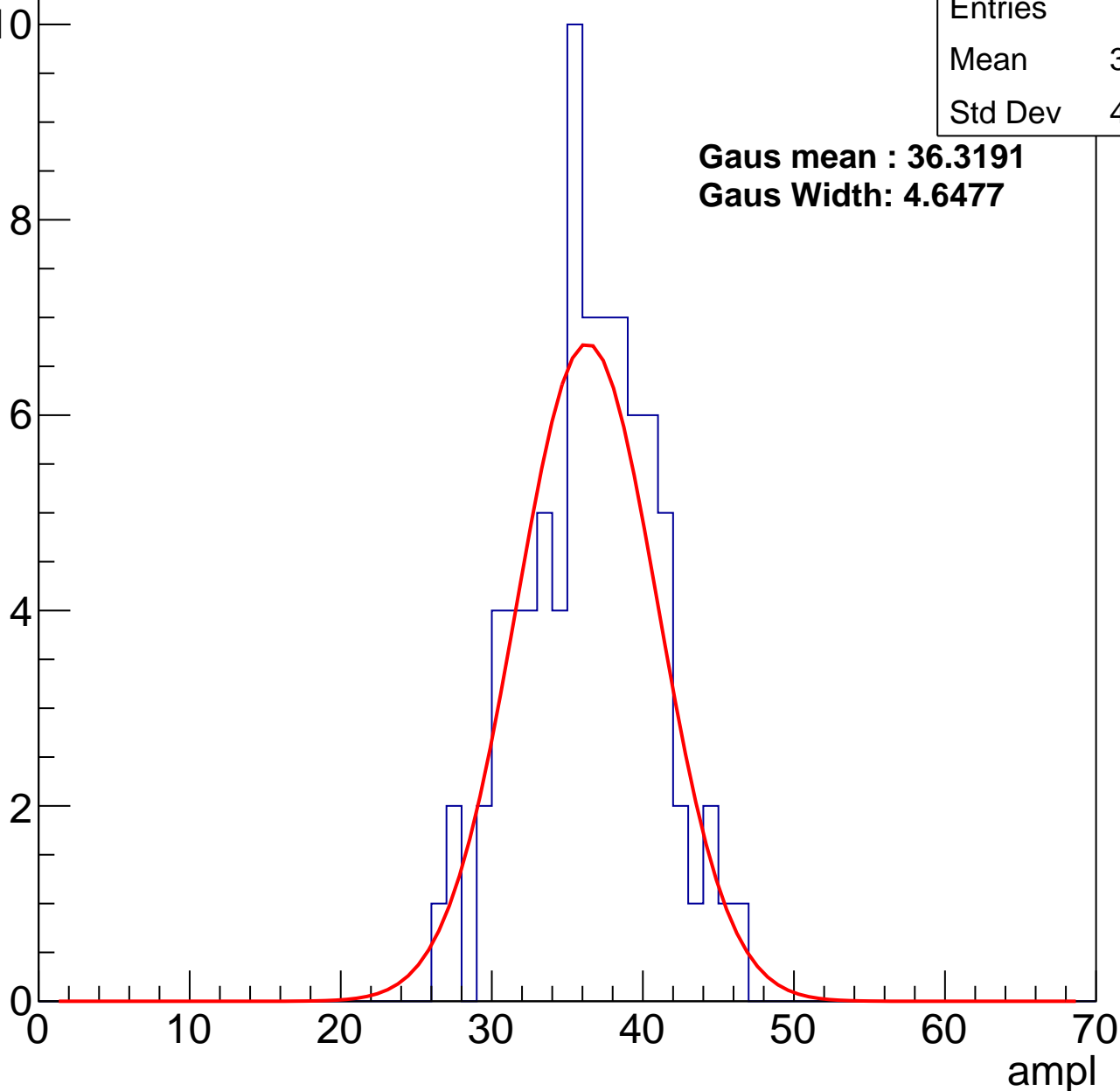
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.09
Std Dev	4.272

Gaus mean : 36.3191

Gaus Width: 4.6477



B1L102S, U13-ch62, adc2

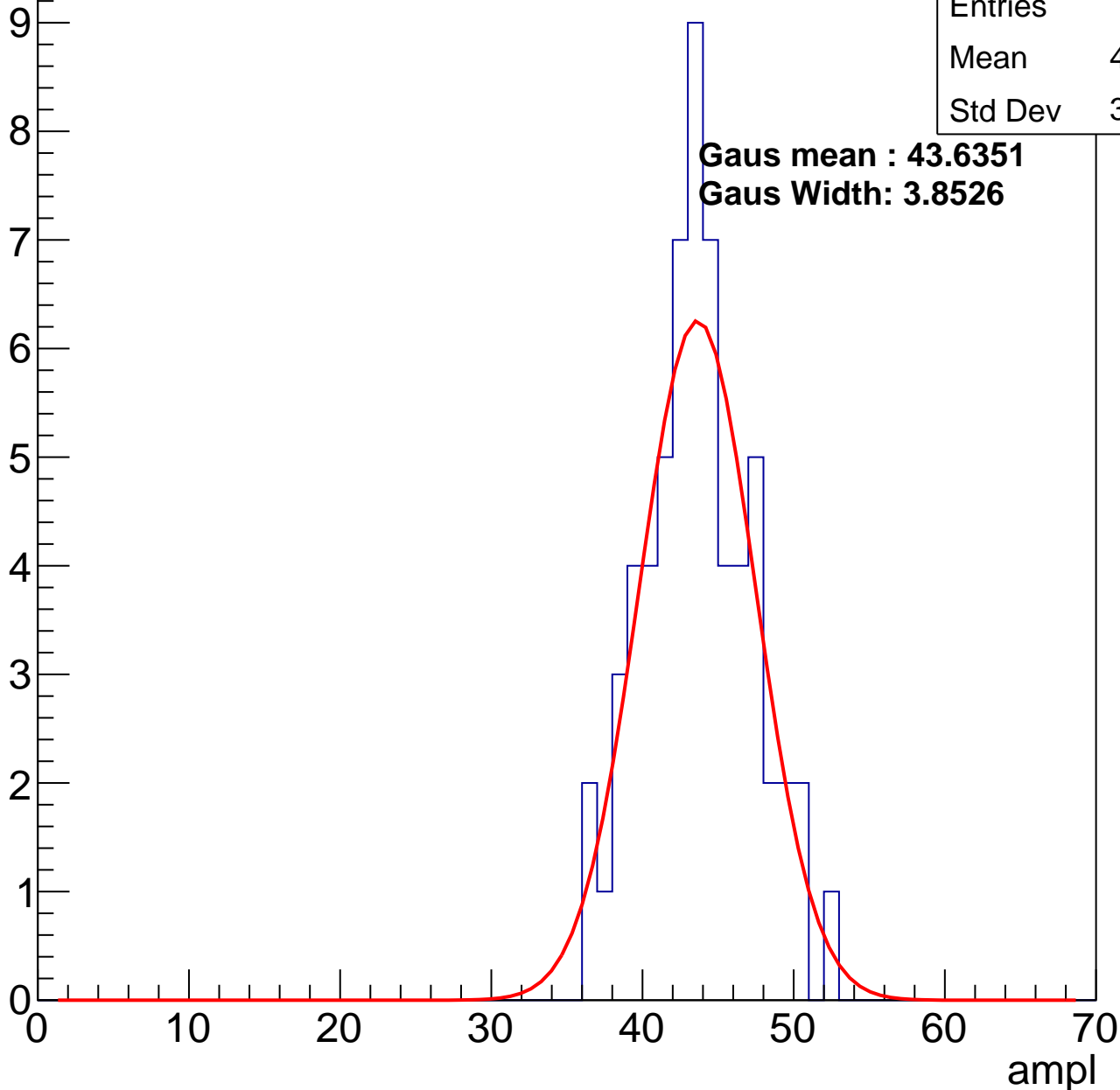
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.19
Std Dev	3.533

Gaus mean : 43.6351

Gaus Width: 3.8526

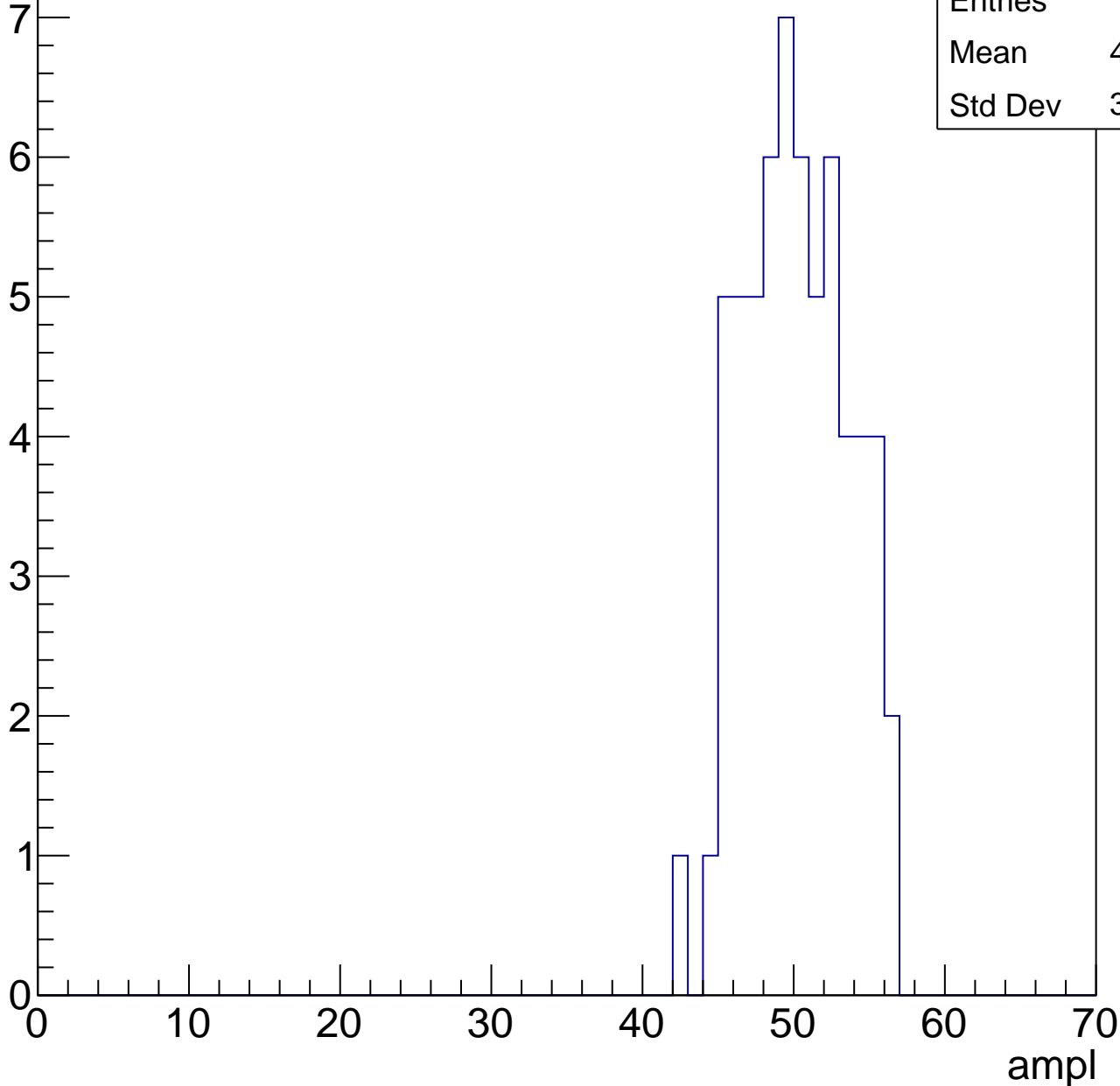


B1L102S, U13-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

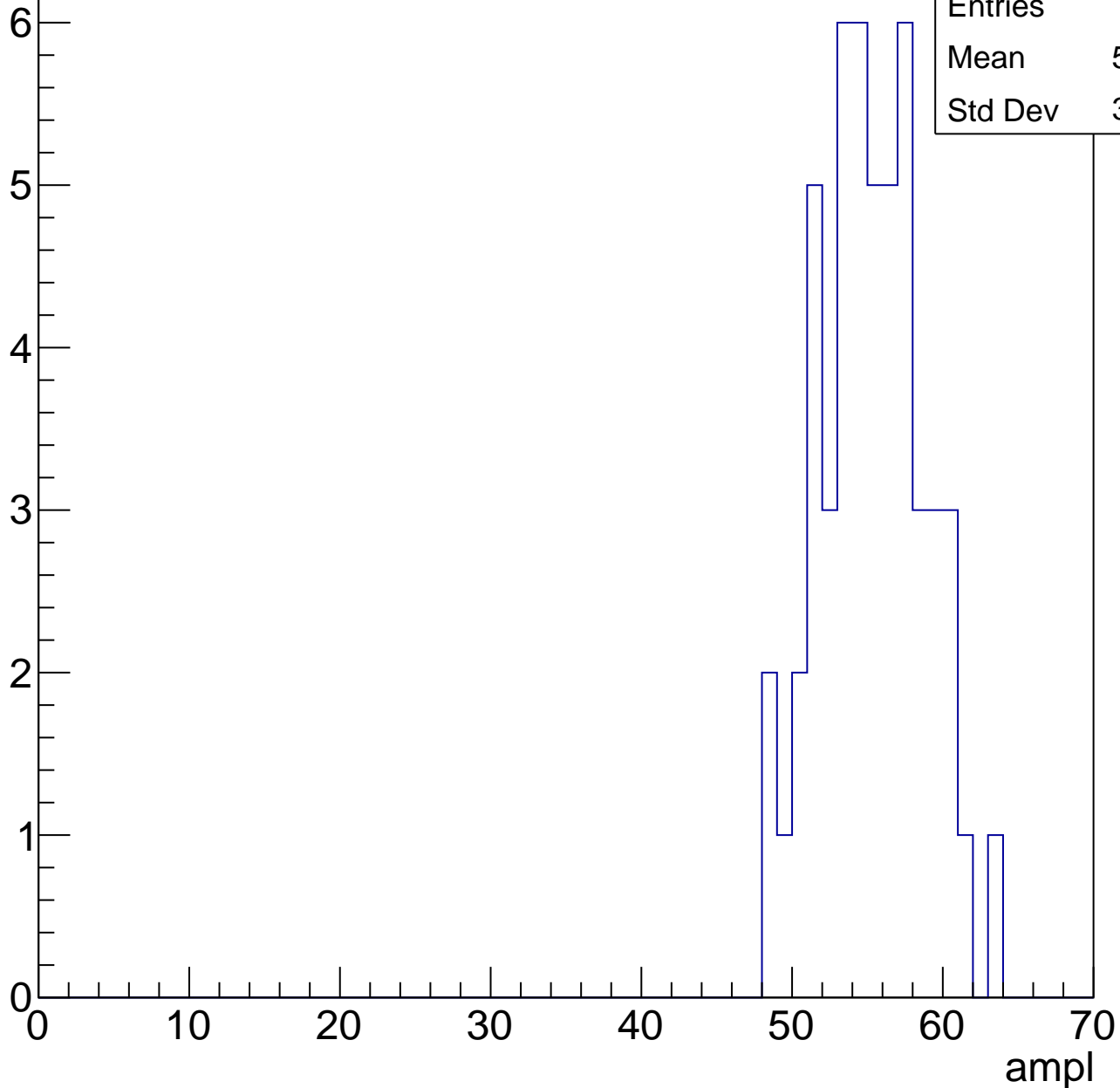
Entries	61
Mean	49.74
Std Dev	3.333



B1L102S, U13-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

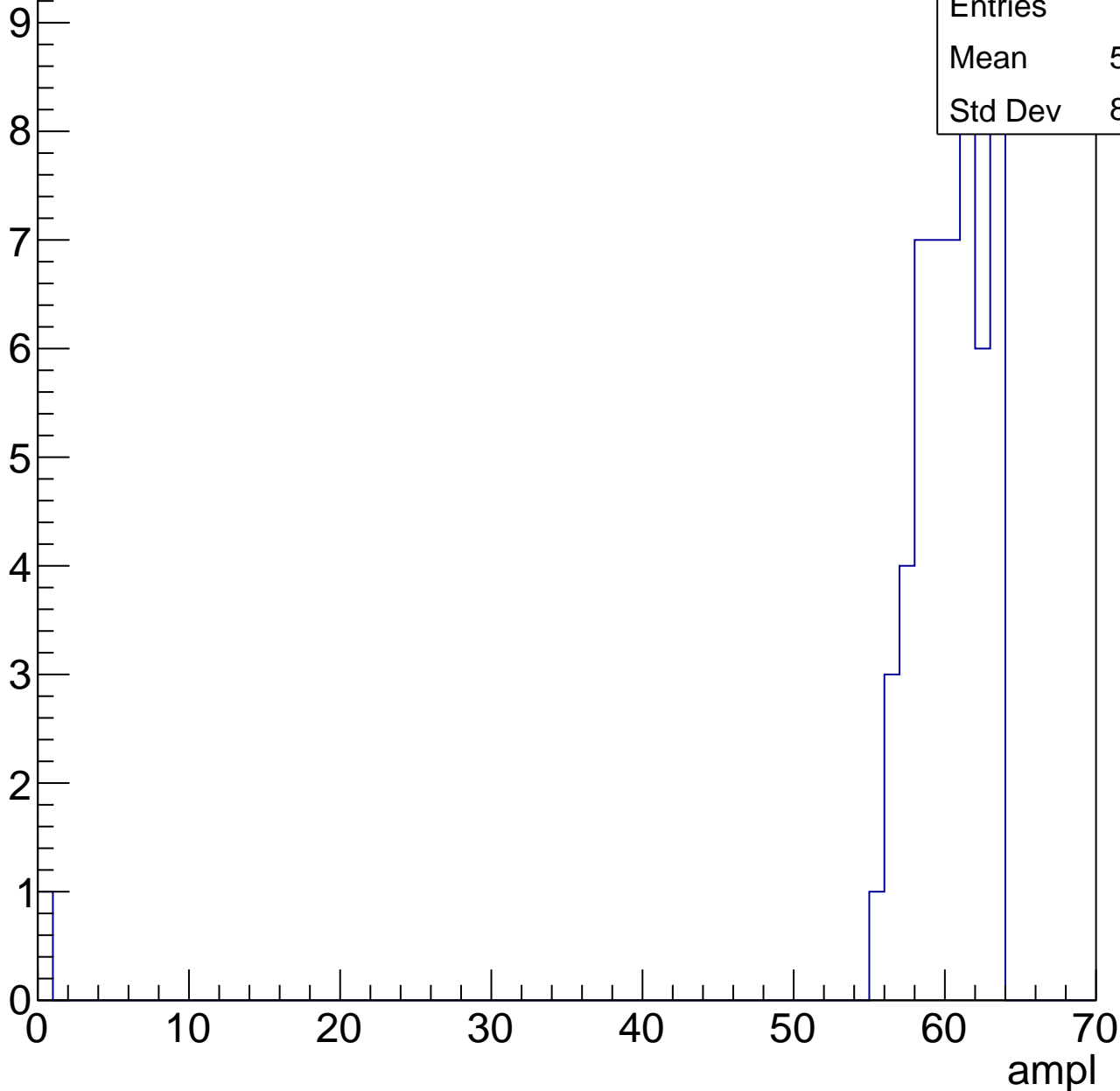


B1L102S, U13-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

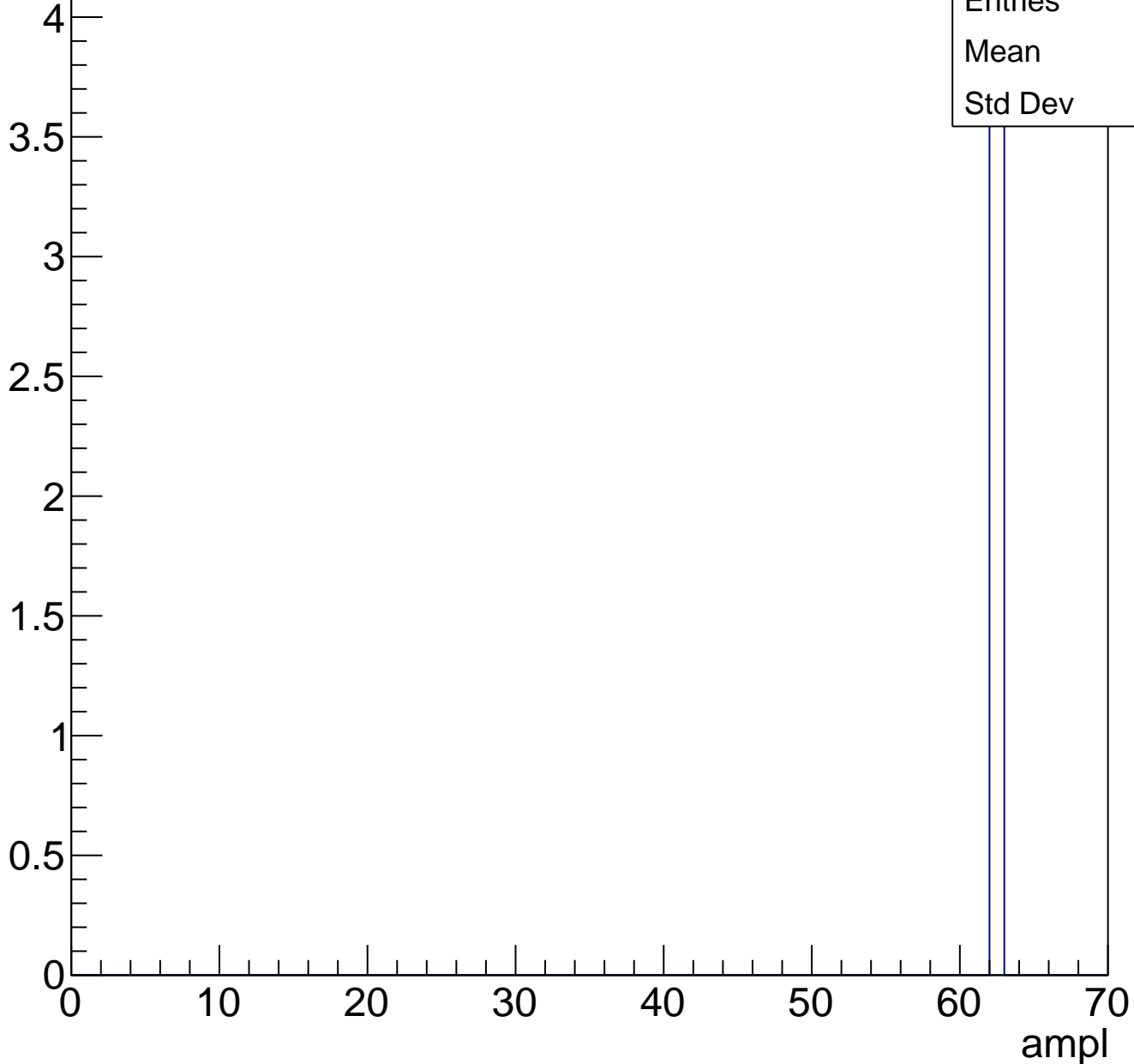
Entries	54
Mean	58.85
Std Dev	8.374



B1L102S, U13-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch63, adc0

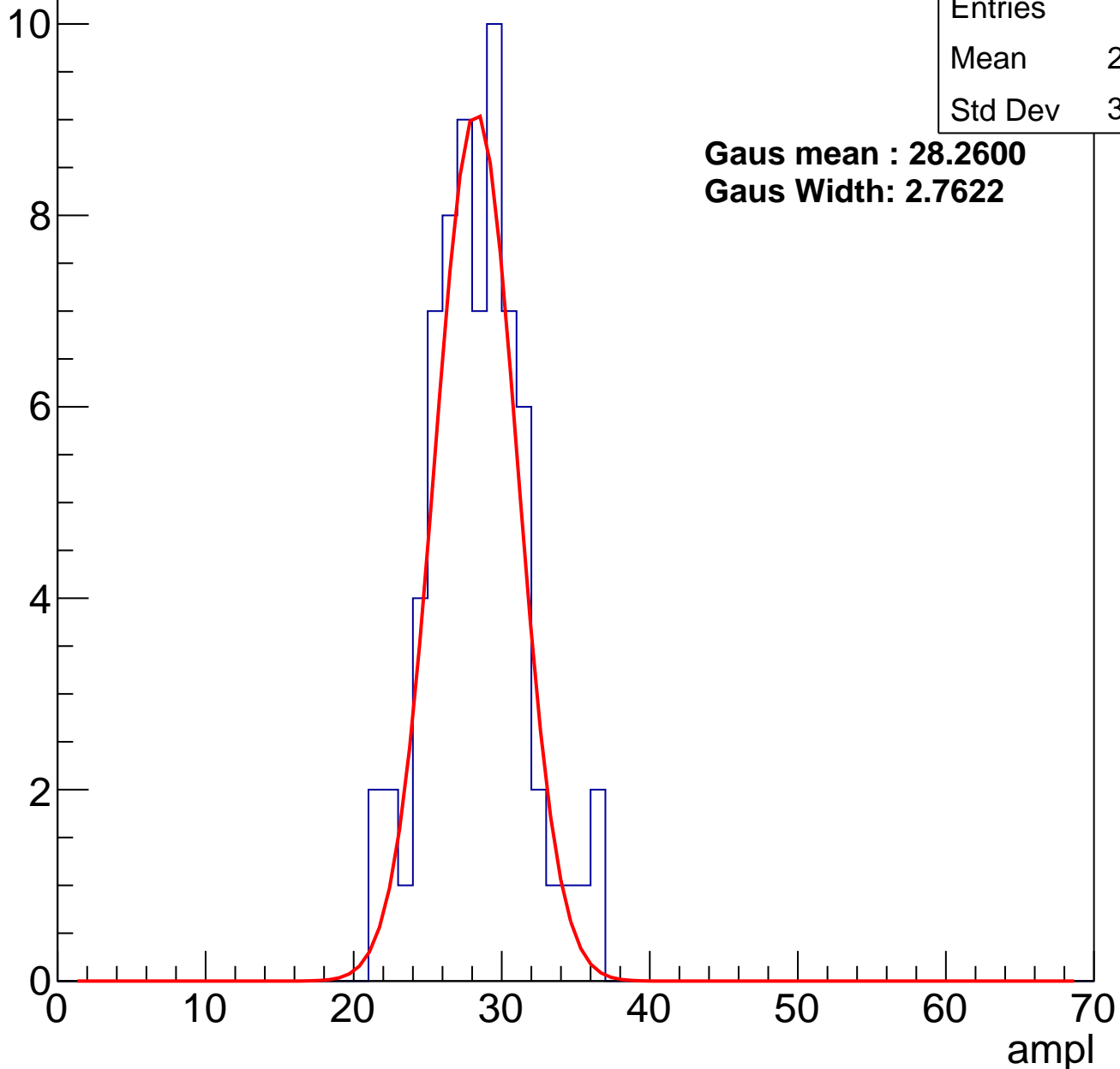
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	27.87
Std Dev	3.216

Gaus mean : 28.2600

Gaus Width: 2.7622

Entry



B1L102S, U13-ch63, adc1

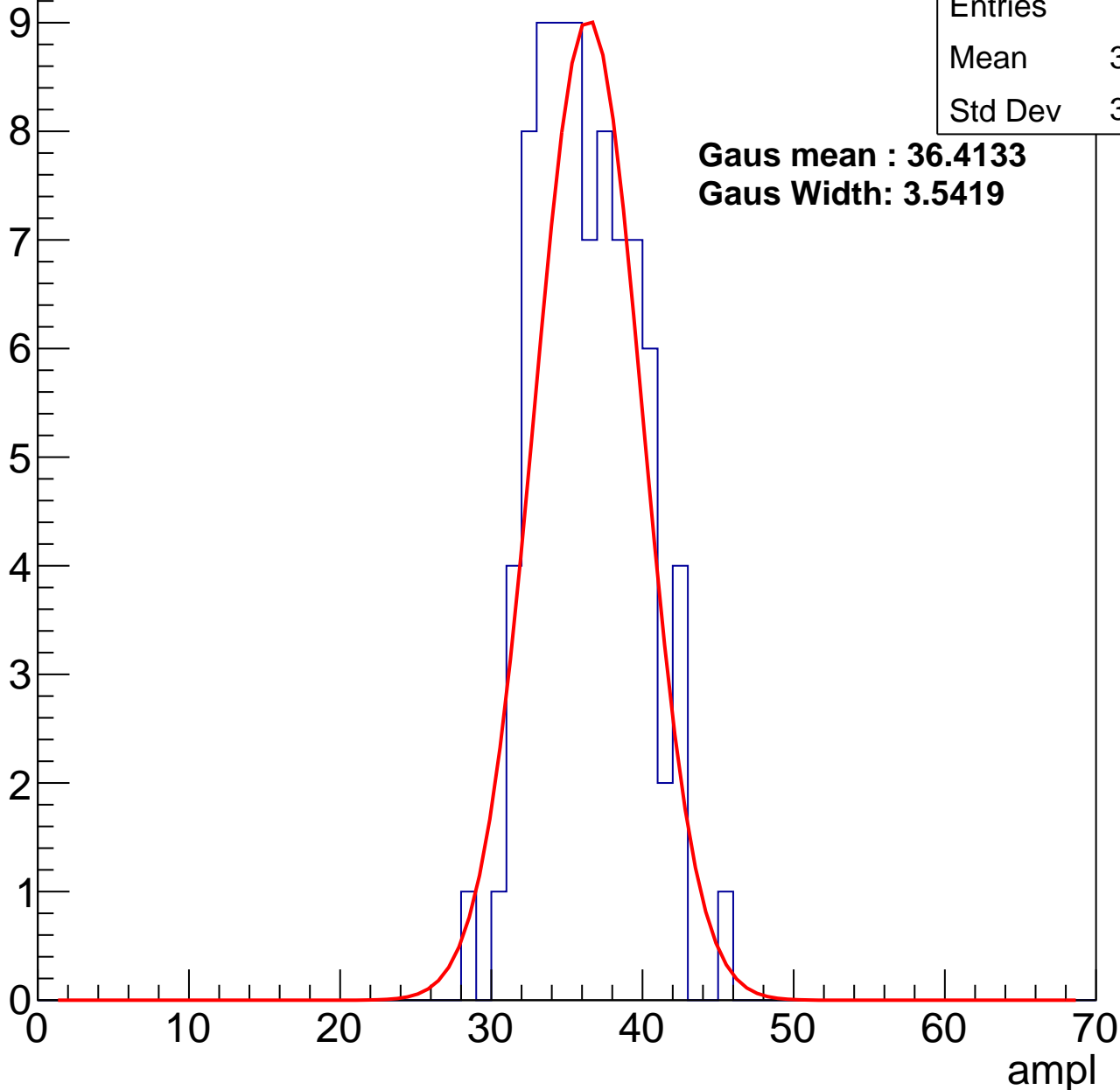
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	35.88
Std Dev	3.334

Gaus mean : 36.4133

Gaus Width: 3.5419



B1L102S, U13-ch63, adc2

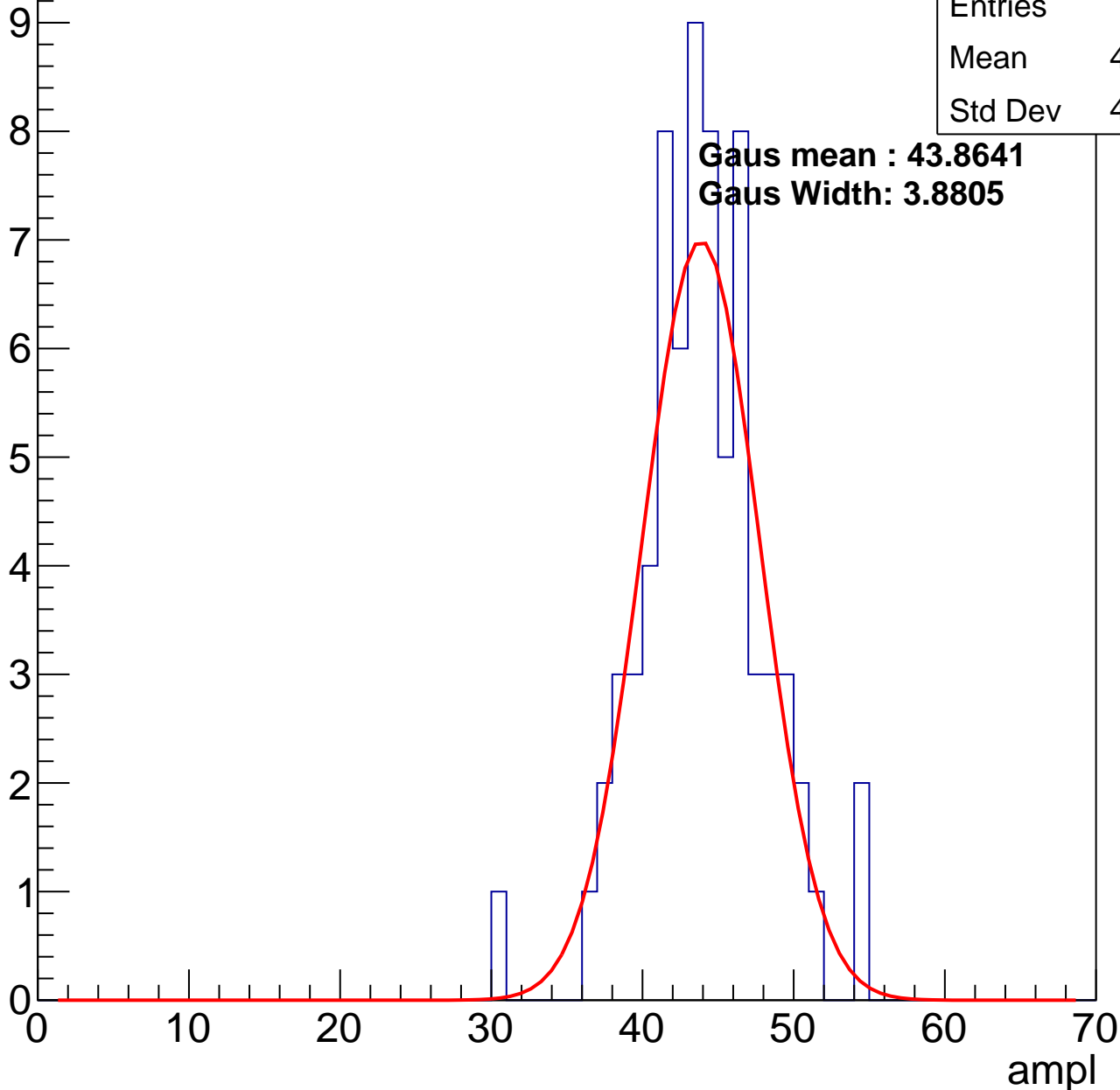
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.53
Std Dev	4.086

Gaus mean : 43.8641

Gaus Width: 3.8805

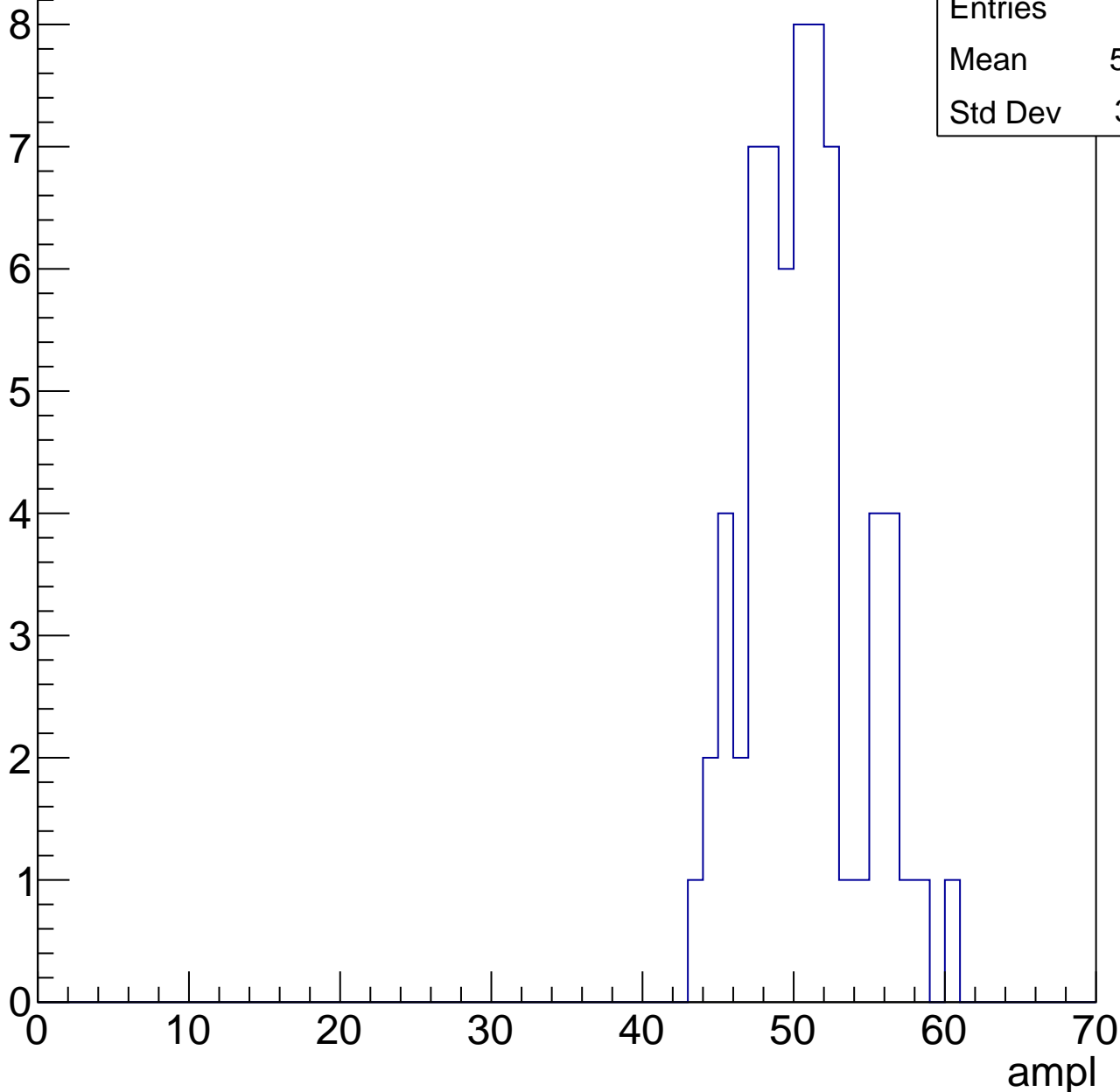


B1L102S, U13-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	50.15
Std Dev	3.651

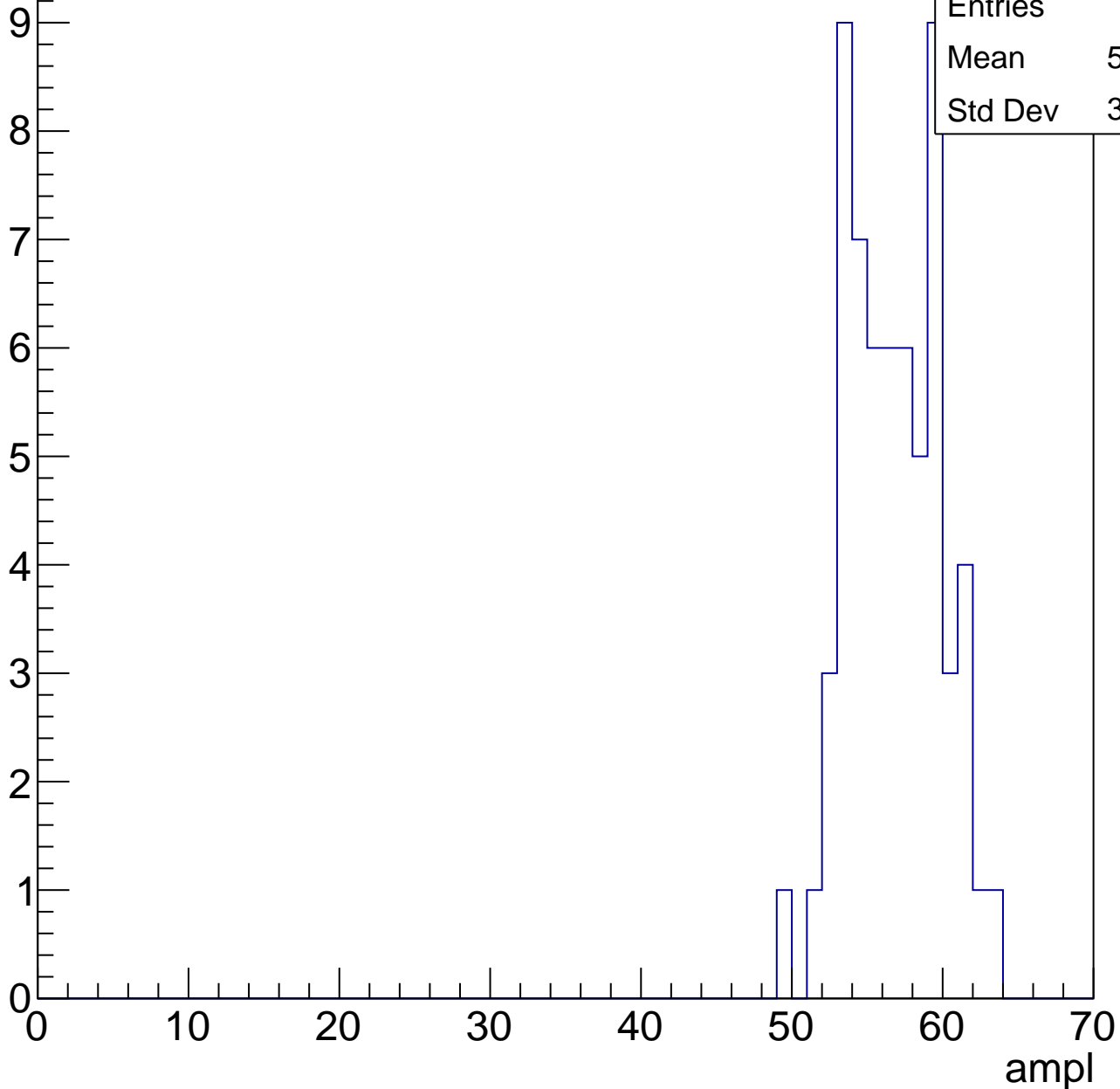


B1L102S, U13-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.27
Std Dev	3.038

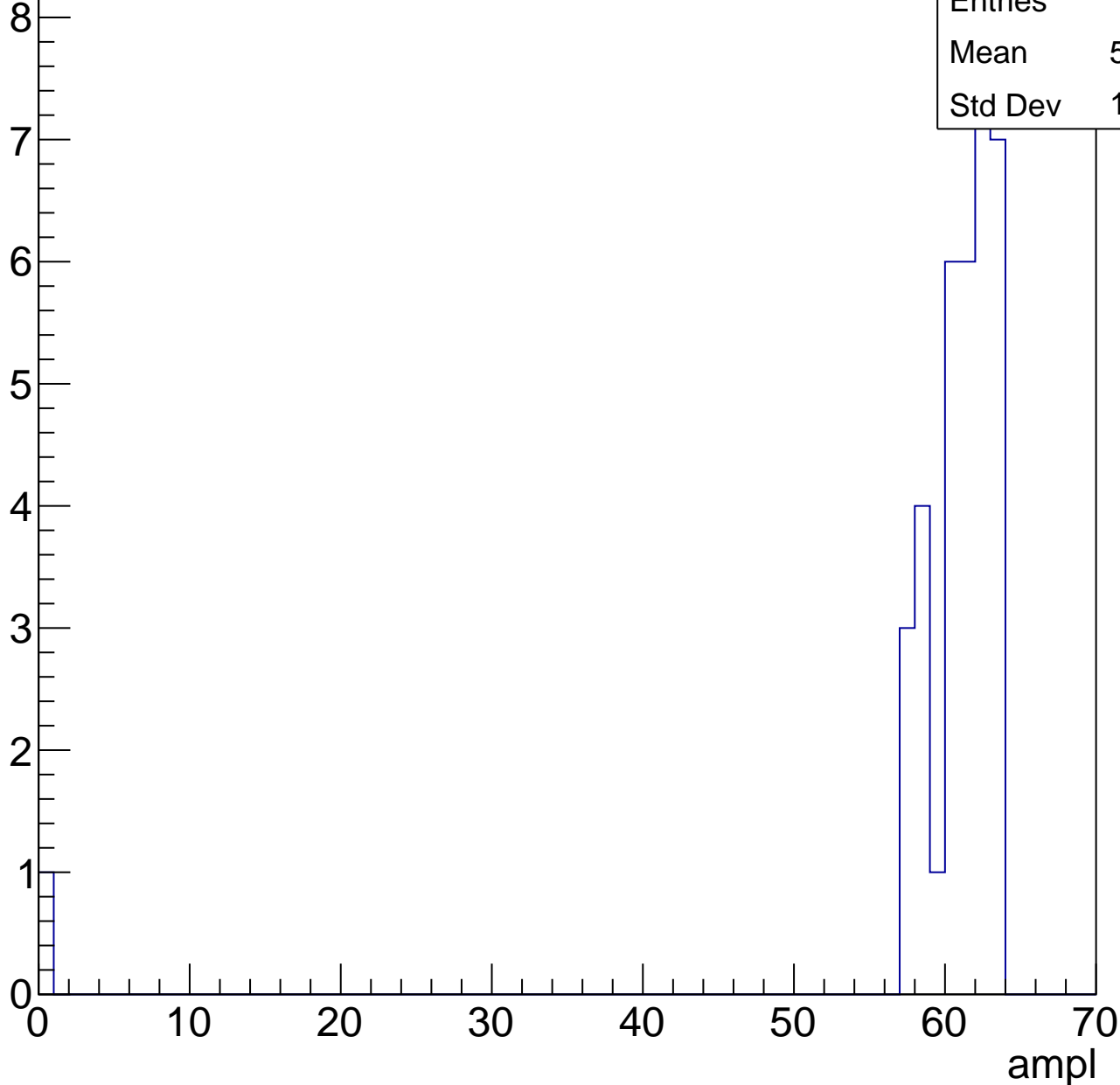


B1L102S, U13-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	59.03
Std Dev	10.15



B1L102S, U13-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch64, adc0

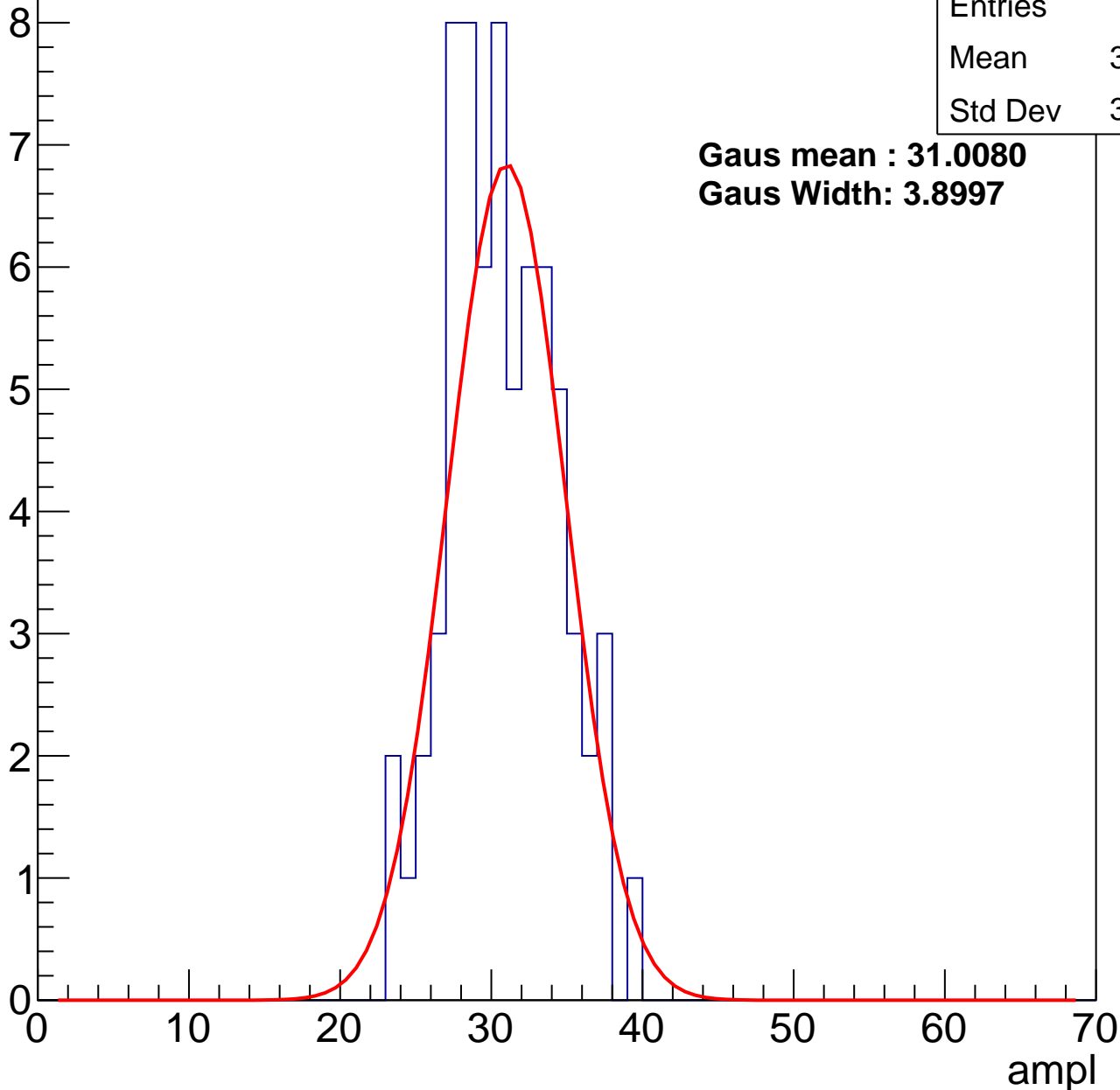
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	30.35
Std Dev	3.554

Gaus mean : 31.0080

Gaus Width: 3.8997



B1L102S, U13-ch64, adc1

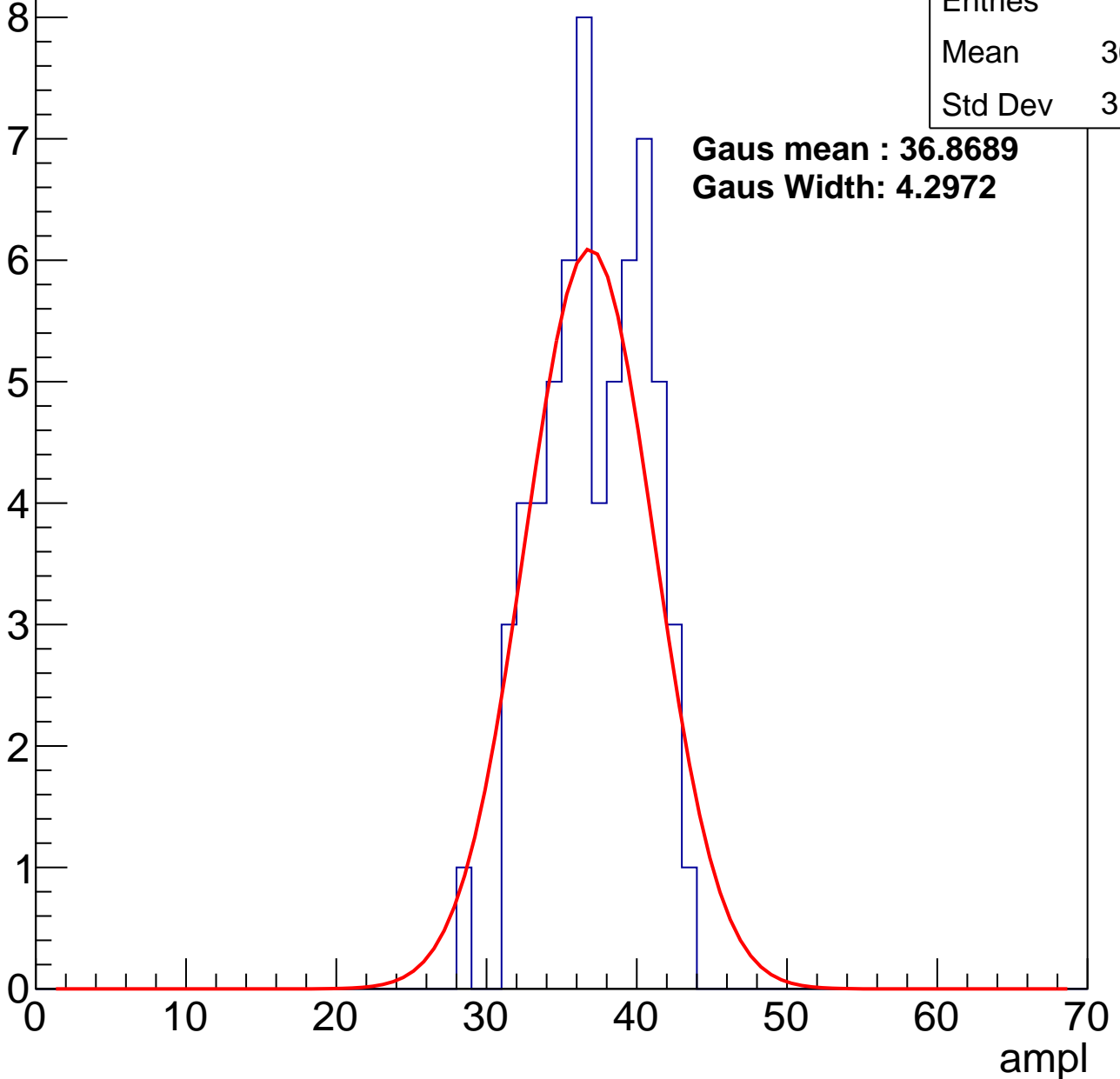
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.69
Std Dev	3.377

Gaus mean : 36.8689

Gaus Width: 4.2972



B1L102S, U13-ch64, adc2

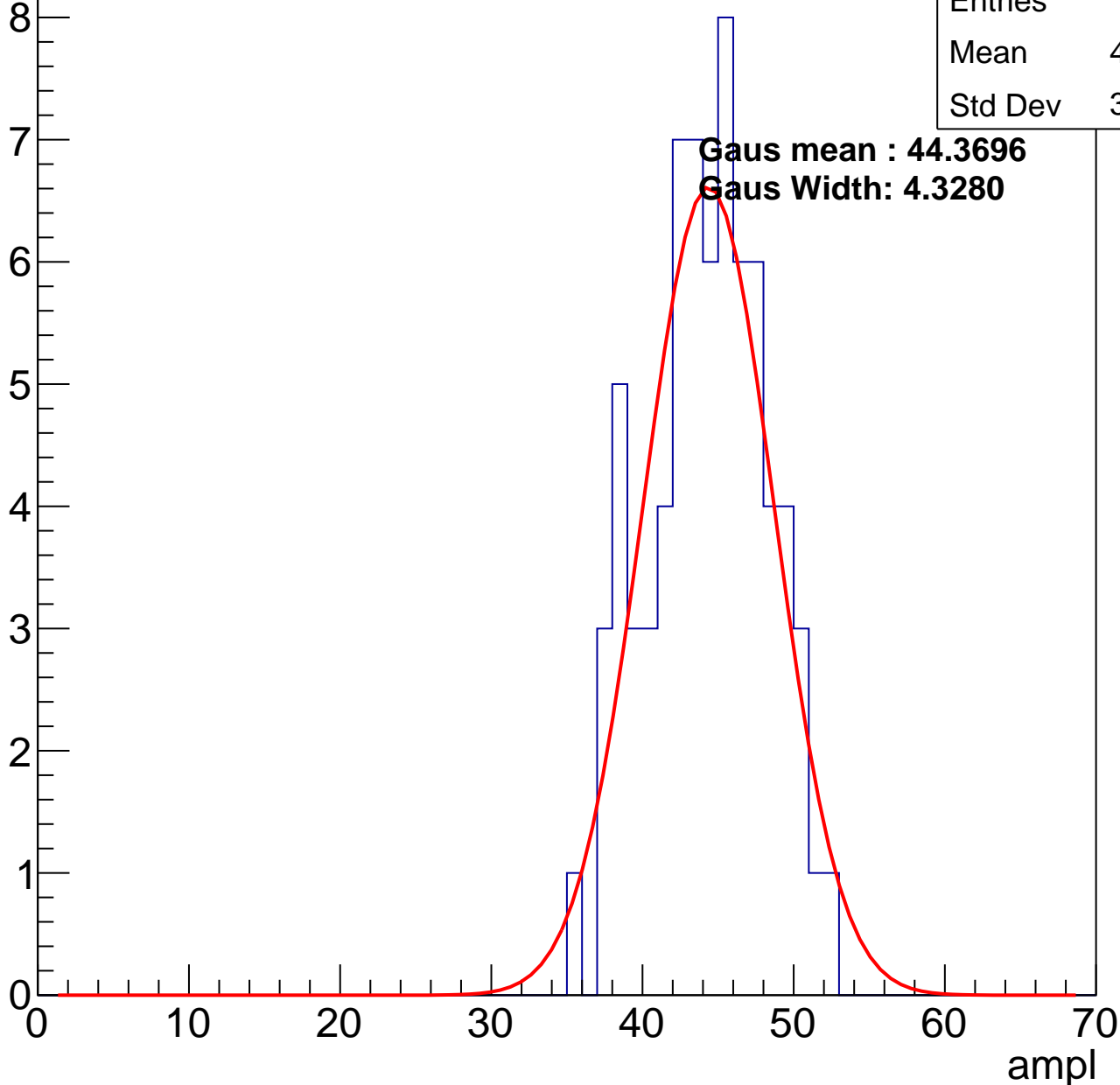
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.82
Std Dev	3.856

Gaus mean : 44.3696

Gaus Width: 4.3280

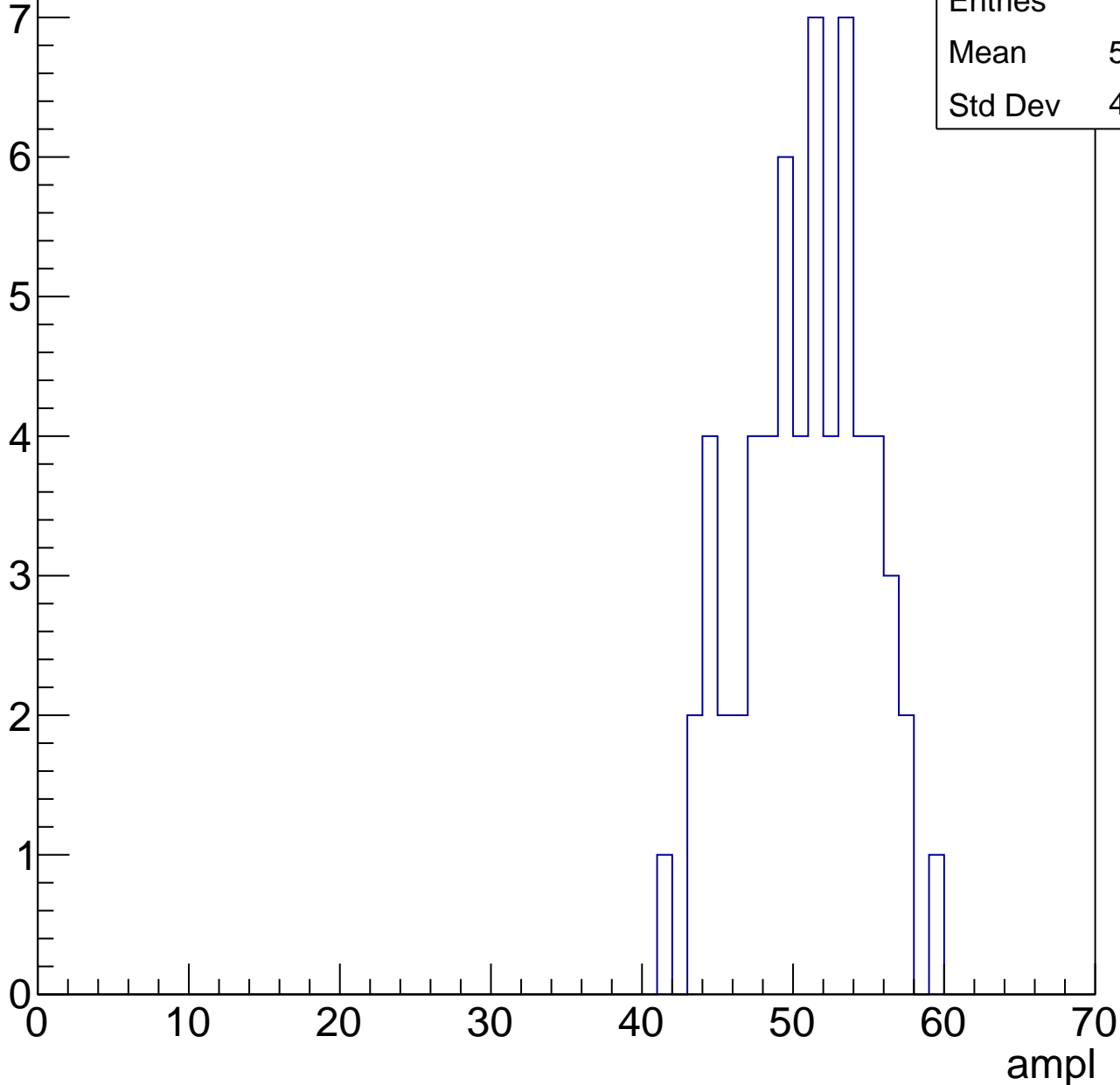


B1L102S, U13-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.36
Std Dev	4.033

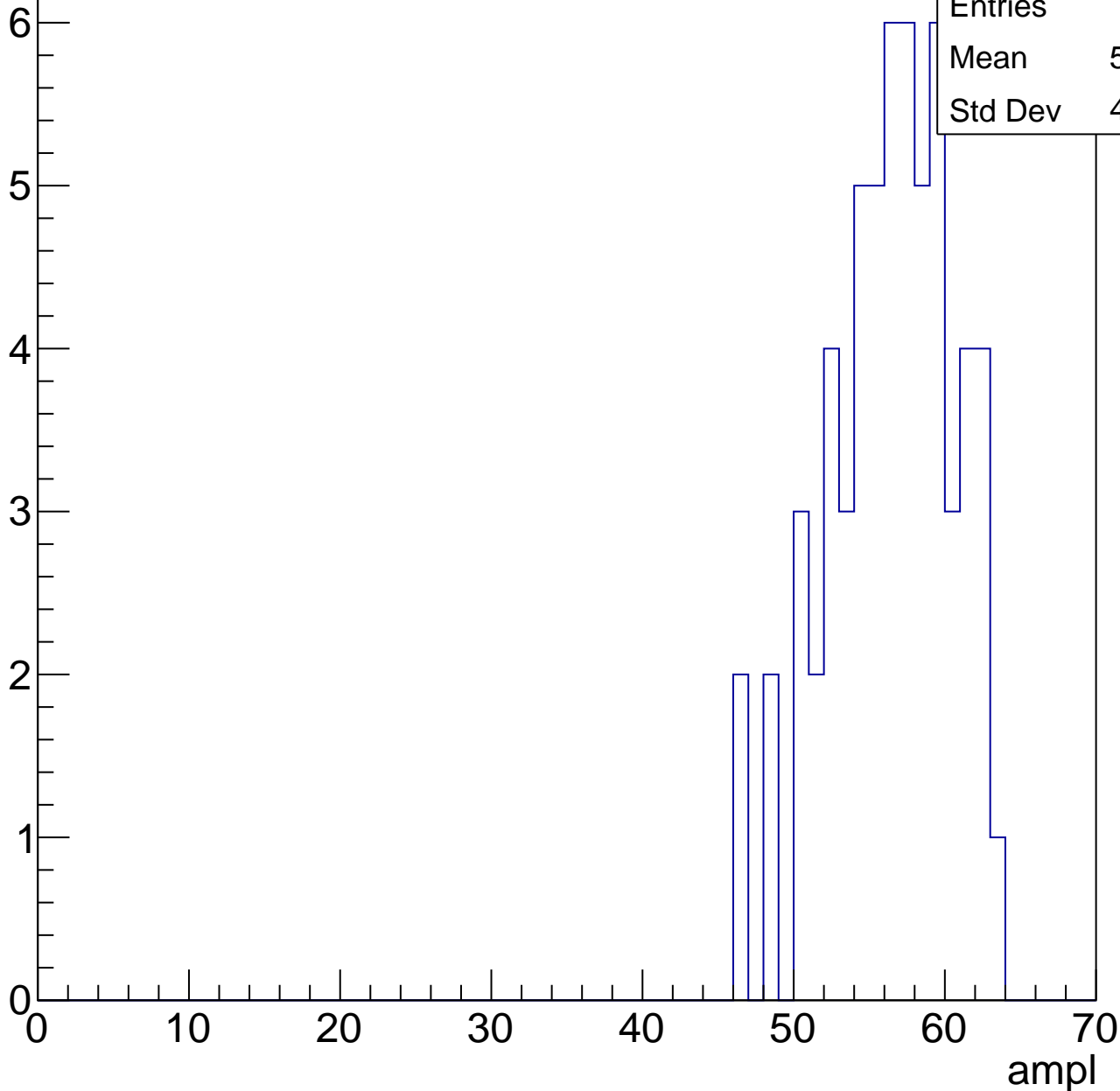


B1L102S, U13-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.89
Std Dev	4.094

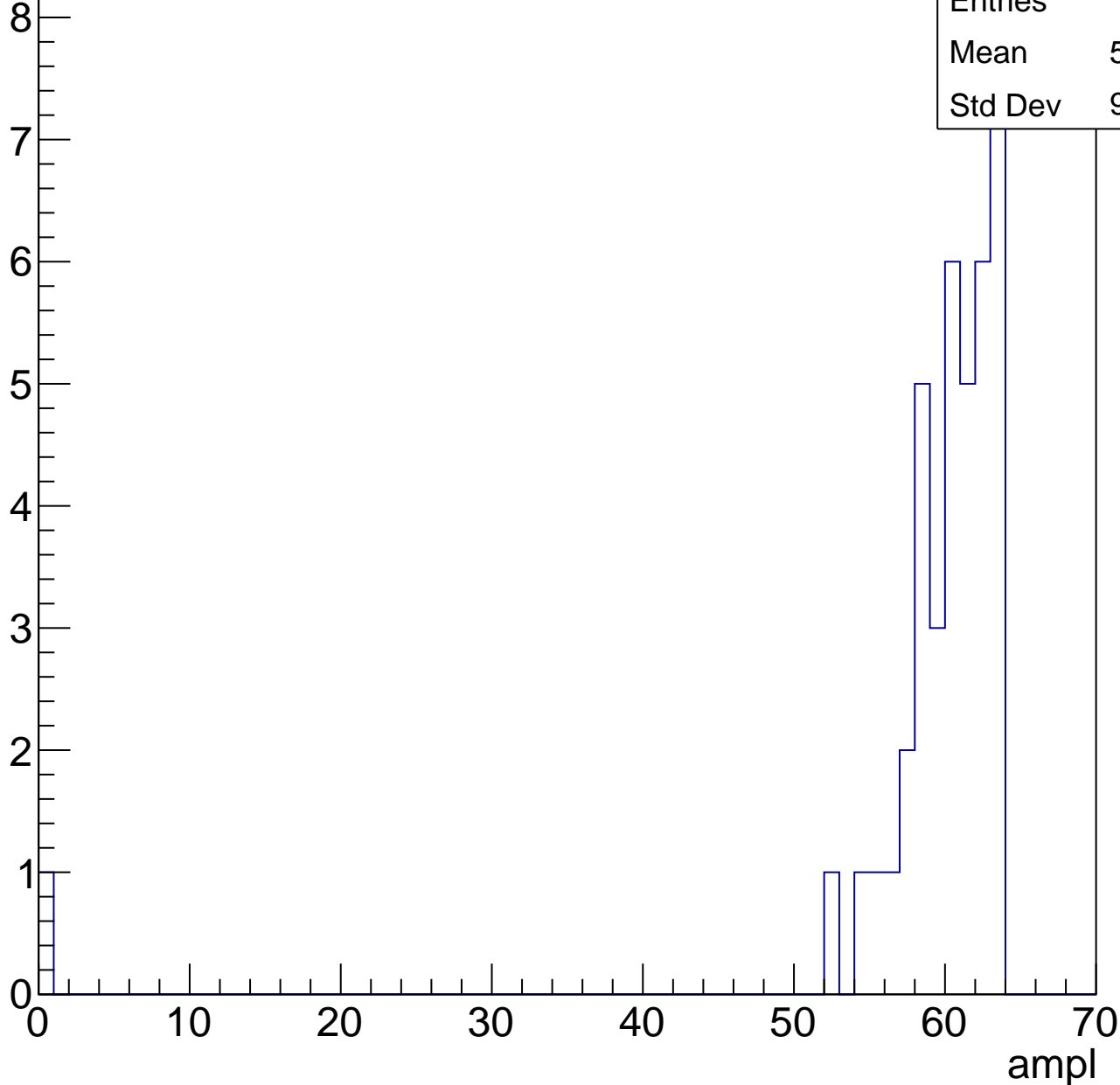


B1L102S, U13-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.48
Std Dev	9.734



B1L102S, U13-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch65, adc0

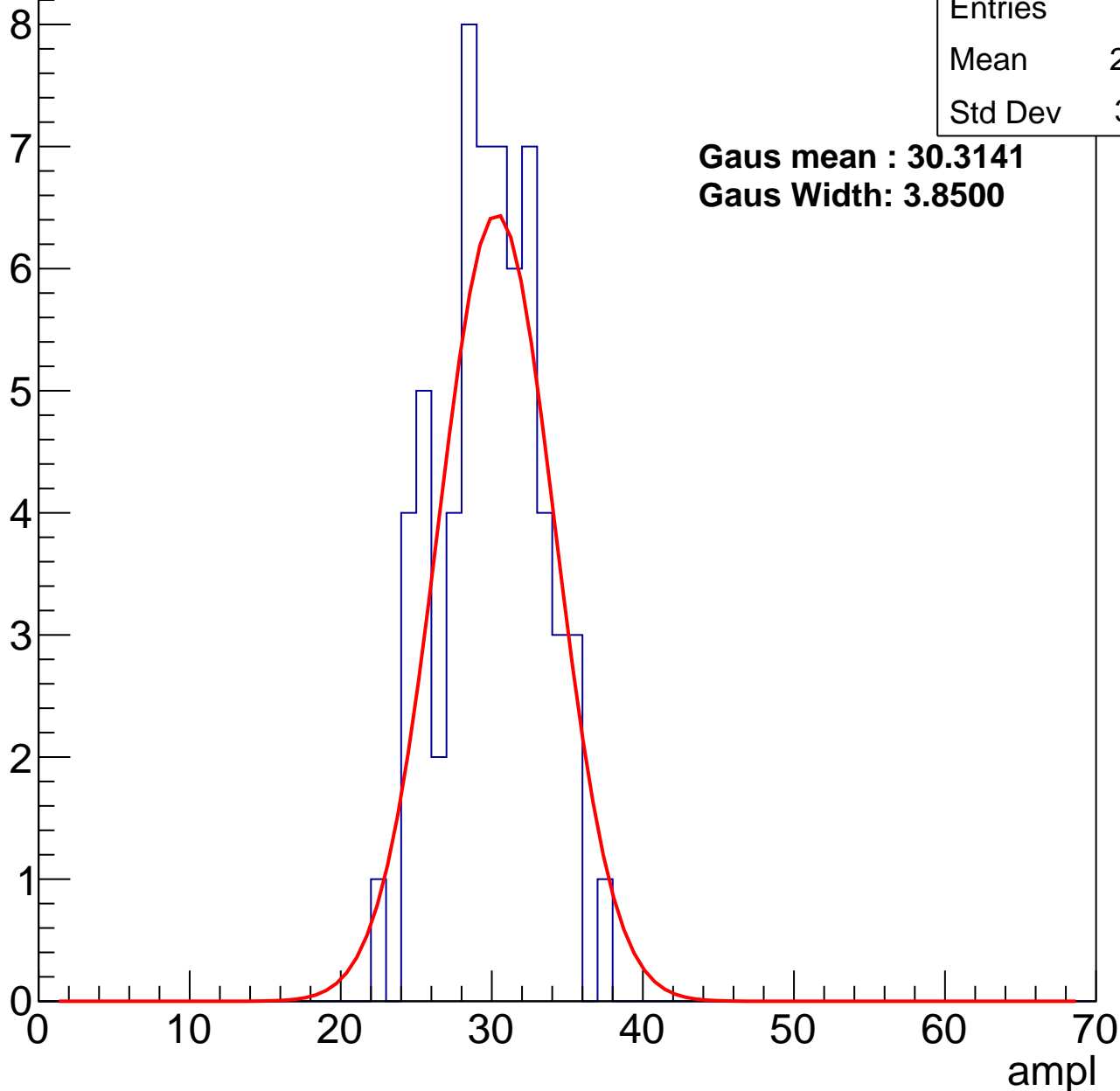
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.45
Std Dev	3.271

Gaus mean : 30.3141

Gaus Width: 3.8500



B1L102S, U13-ch65, adc1

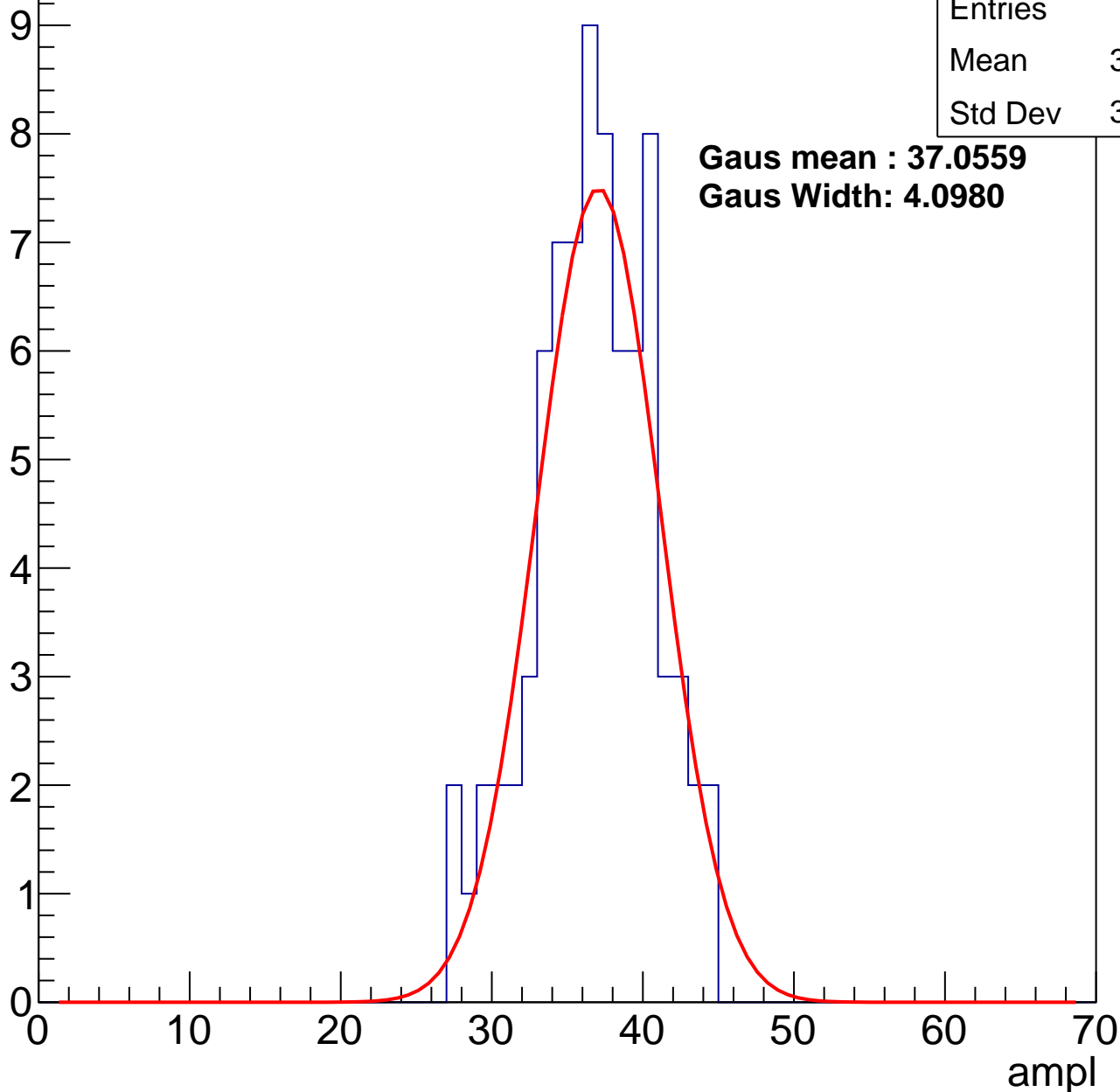
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.25
Std Dev	3.899

Gaus mean : 37.0559

Gaus Width: 4.0980



B1L102S, U13-ch65, adc2

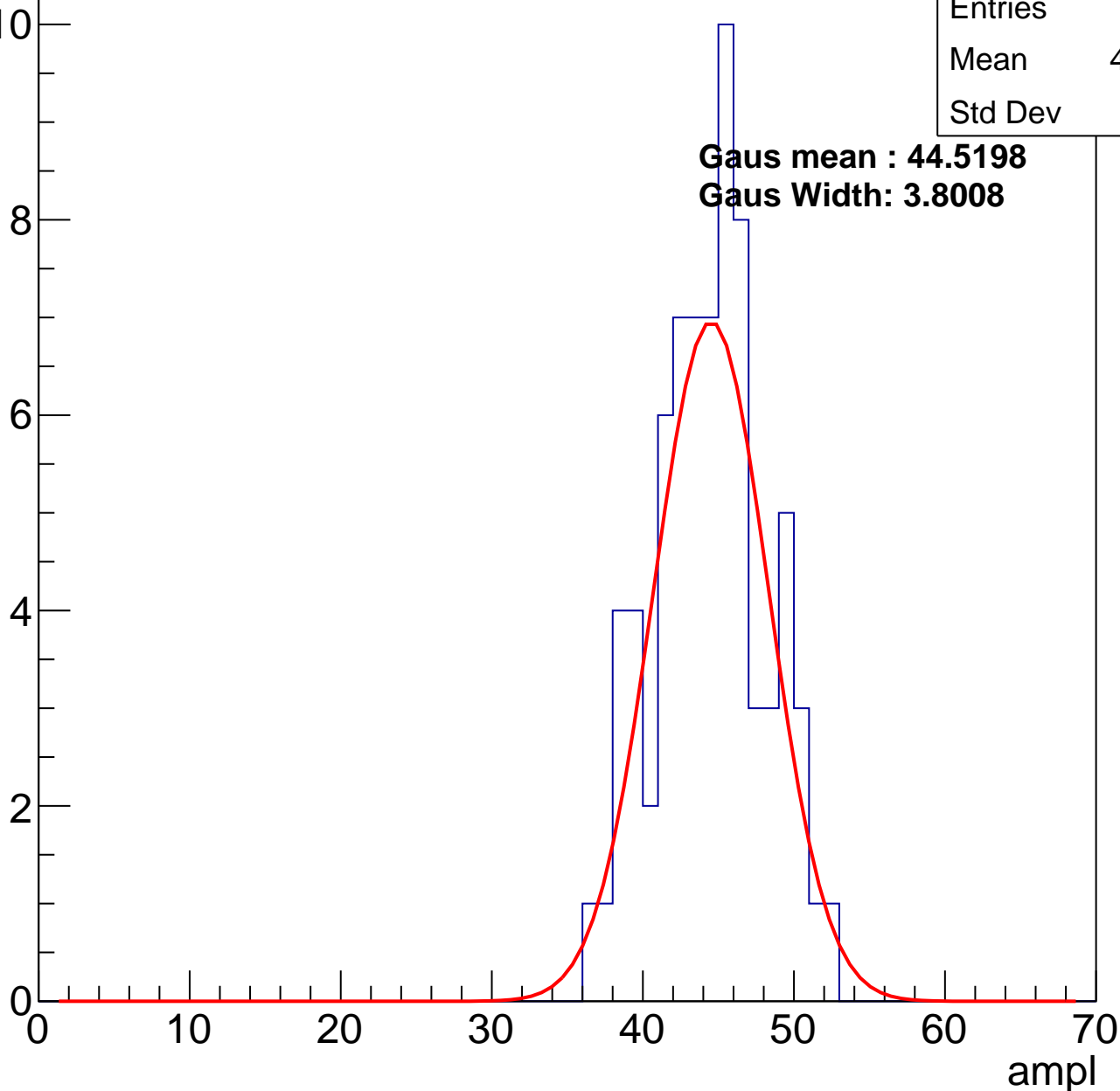
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.99
Std Dev	3.59

Gaus mean : 44.5198

Gaus Width: 3.8008

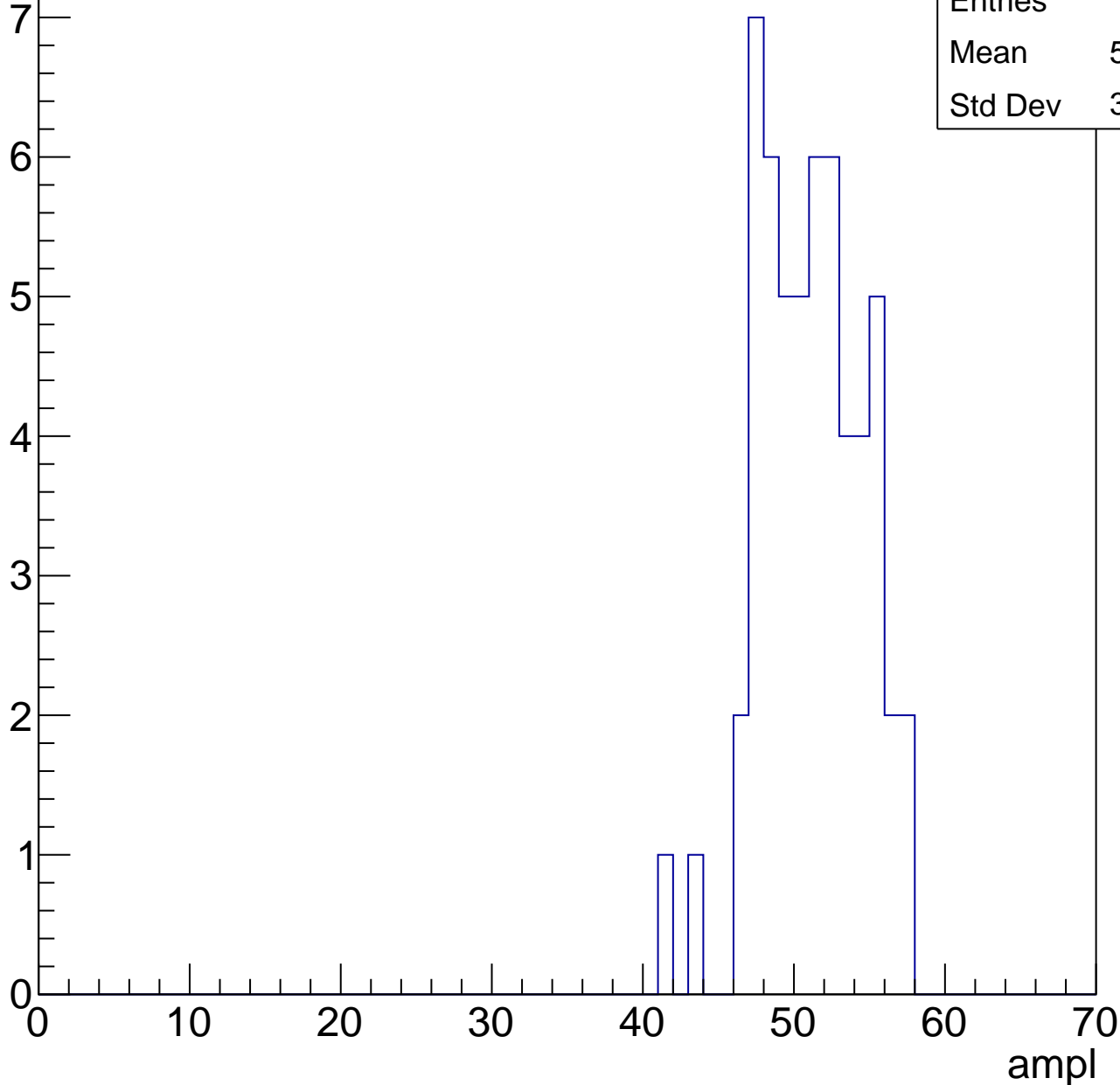


B1L102S, U13-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	50.62
Std Dev	3.426

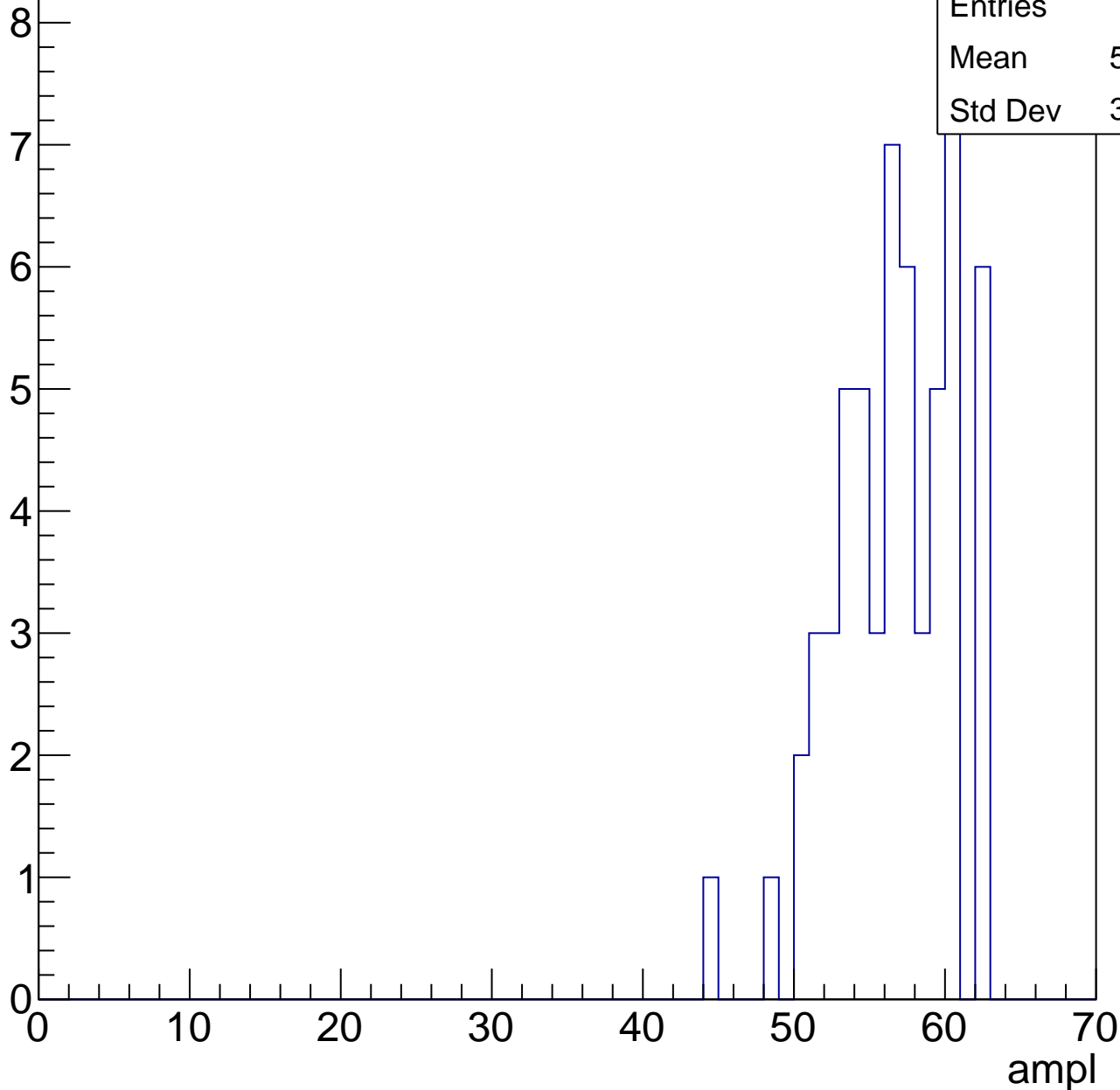


B1L102S, U13-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.14
Std Dev	3.888

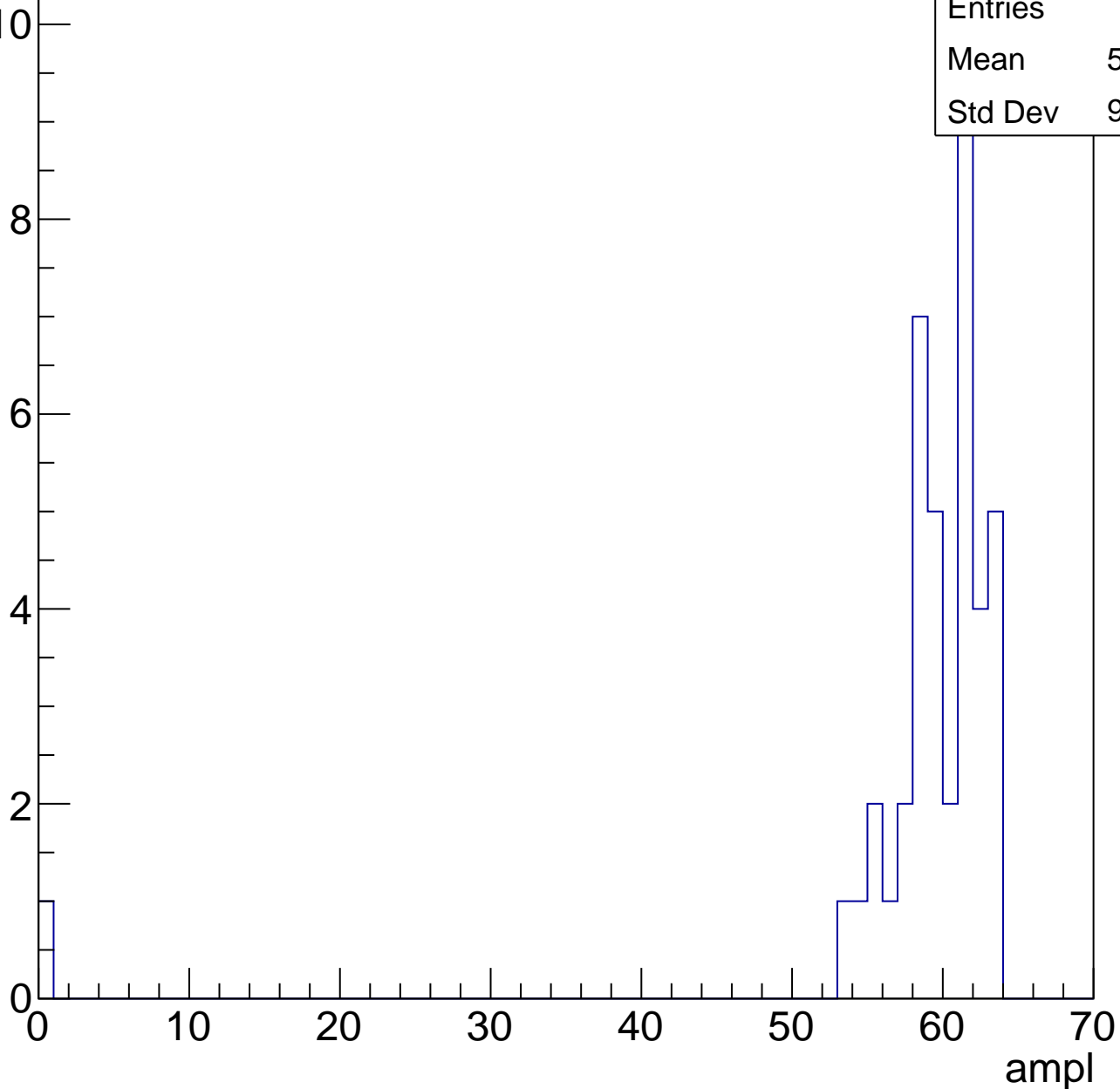


B1L102S, U13-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.07
Std Dev	9.526



B1L102S, U13-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

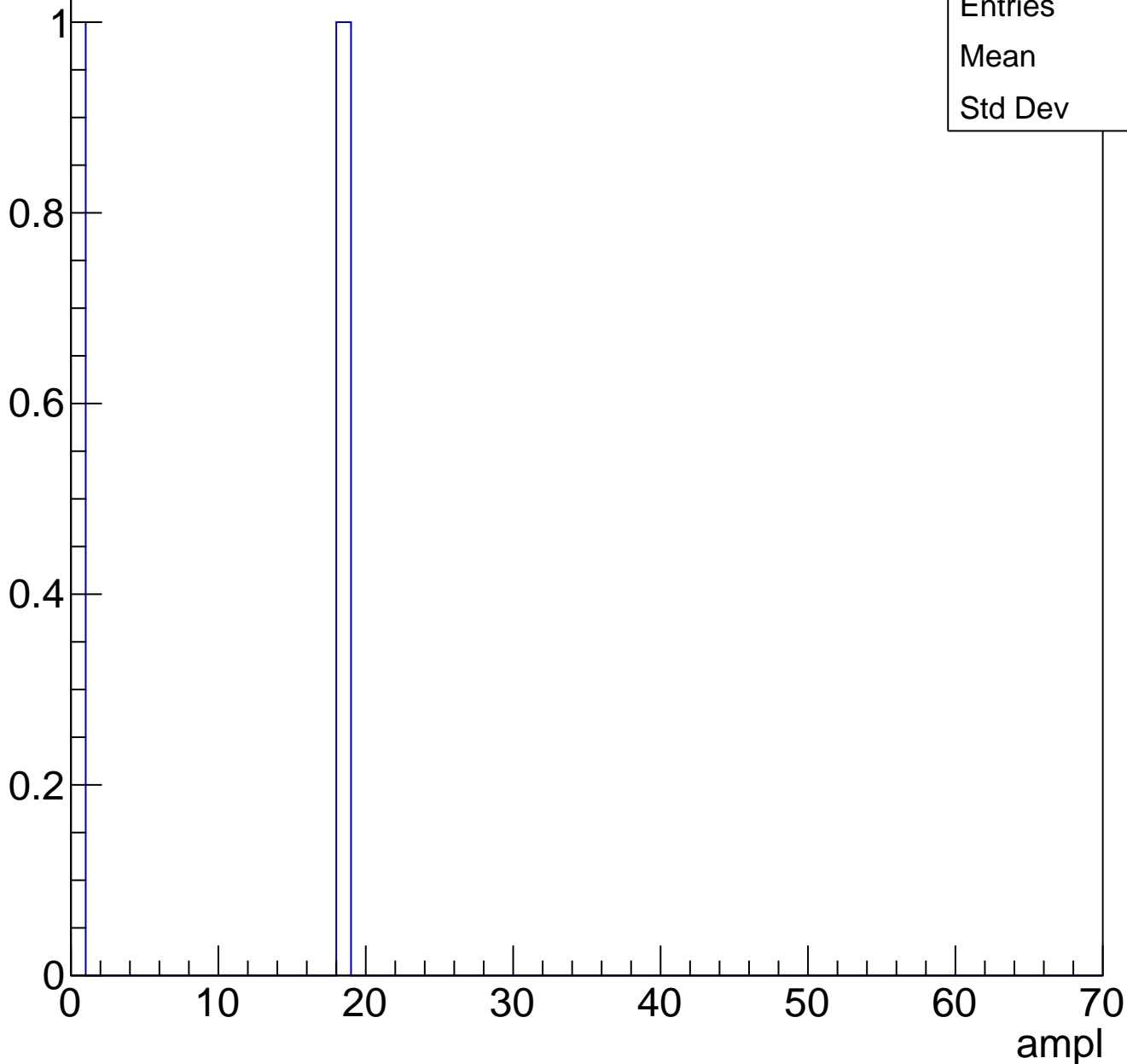
Entry



B1L102S, U13-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch66, adc0

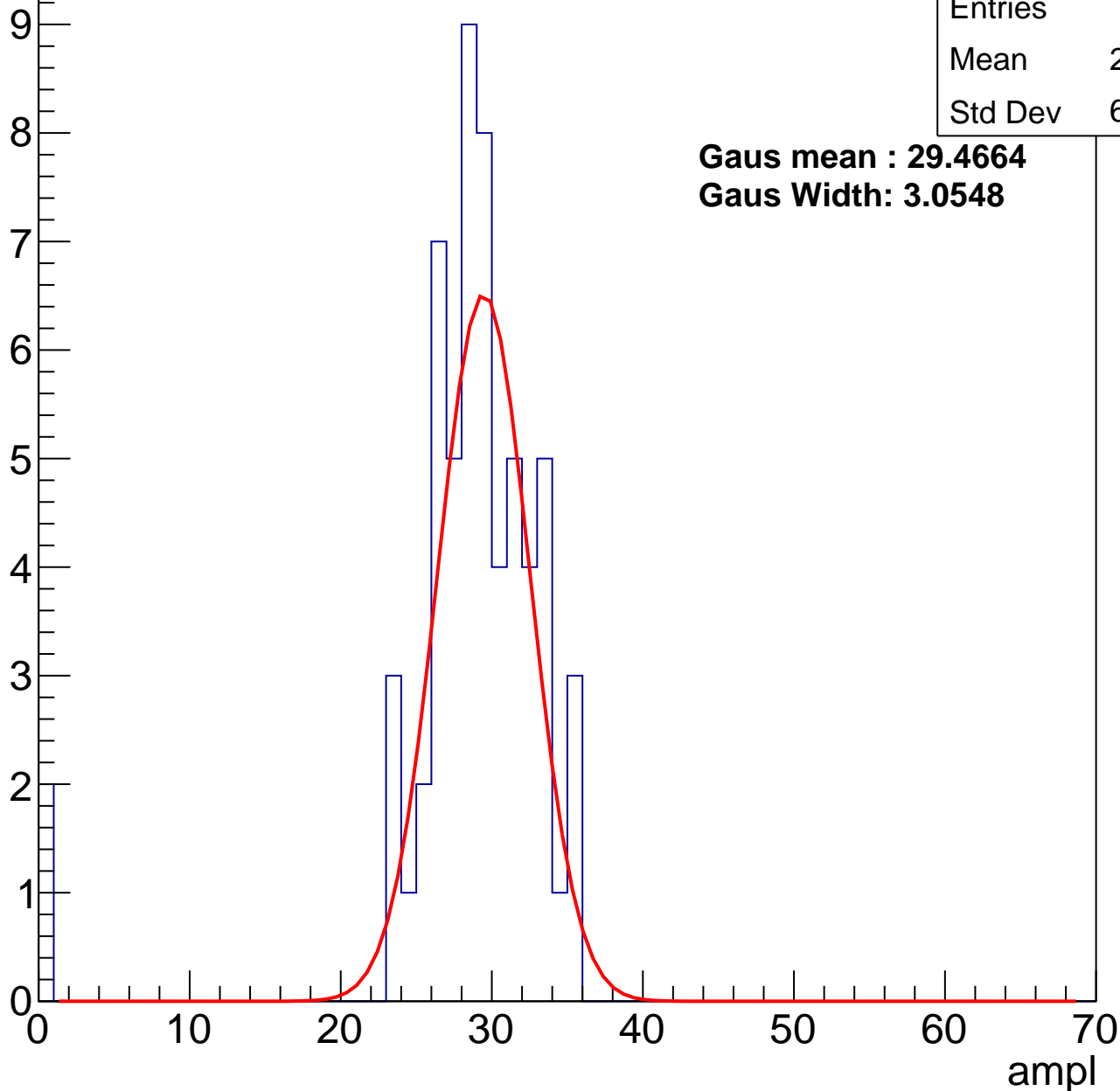
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.98
Std Dev	6.038

Gaus mean : 29.4664

Gaus Width: 3.0548



B1L102S, U13-ch66, adc1

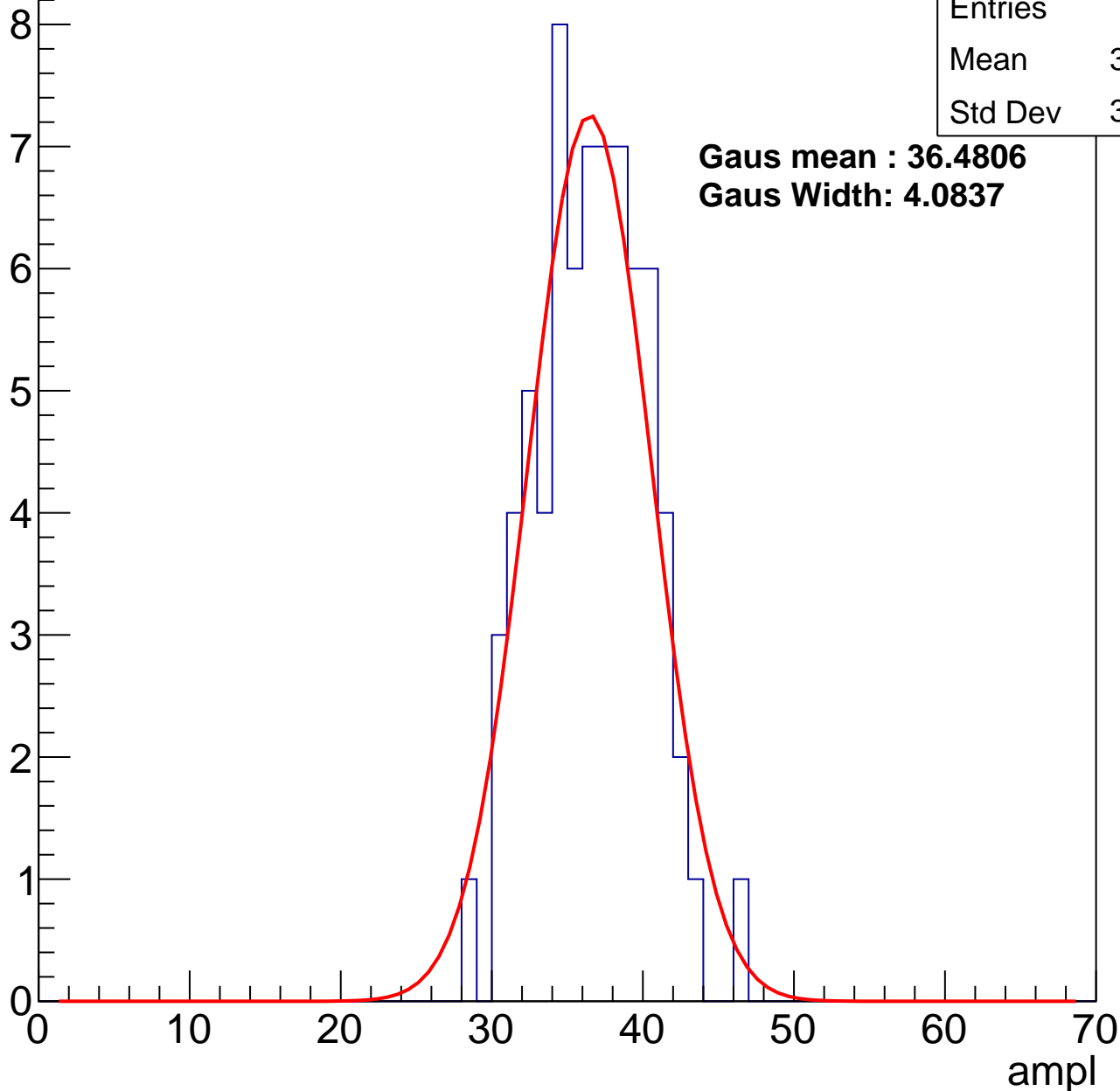
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.17
Std Dev	3.594

Gaus mean : 36.4806

Gaus Width: 4.0837



B1L102S, U13-ch66, adc2

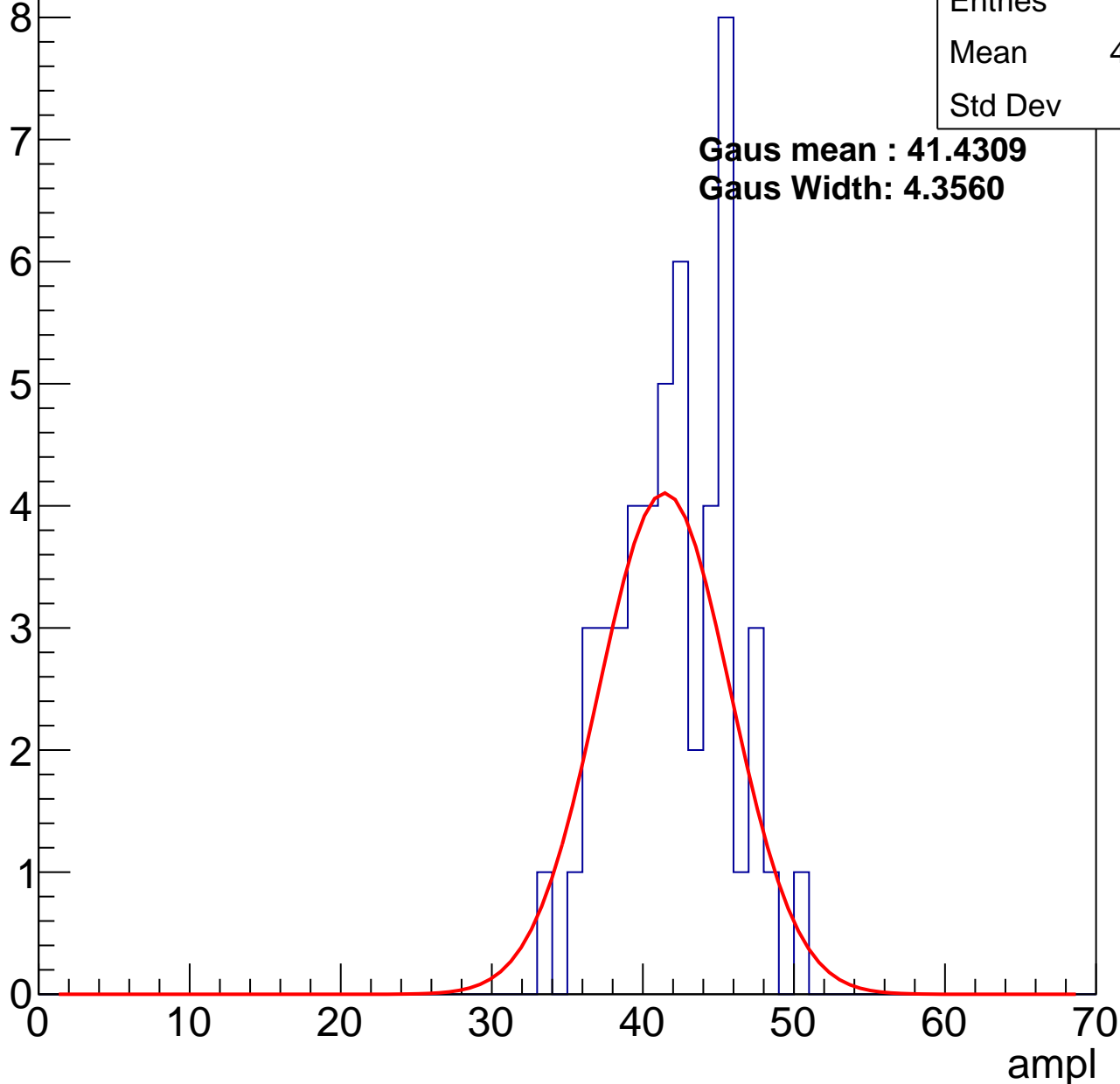
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	41.62
Std Dev	3.72

Gaus mean : 41.4309

Gaus Width: 4.3560

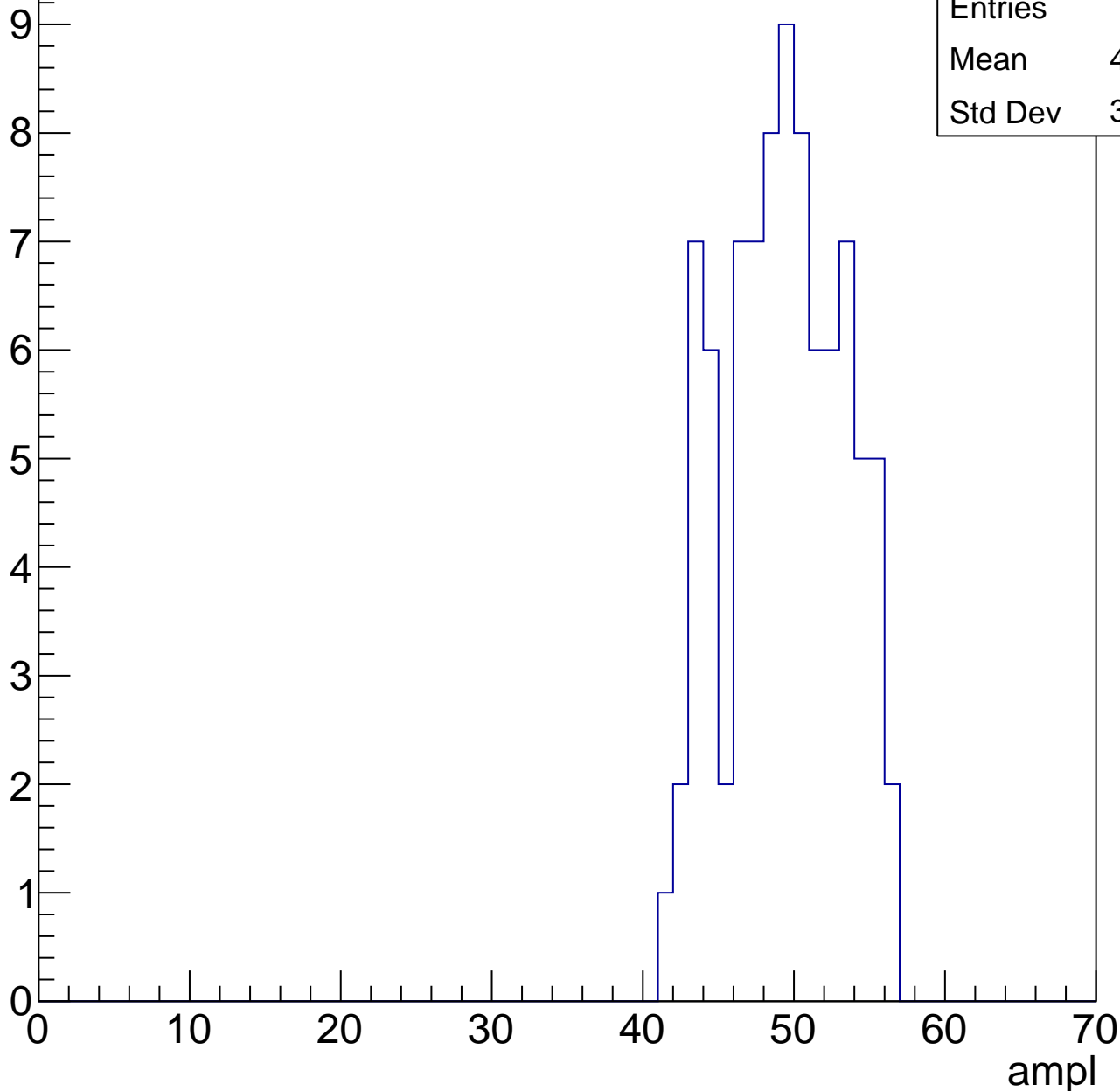


B1L102S, U13-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	48.89
Std Dev	3.836

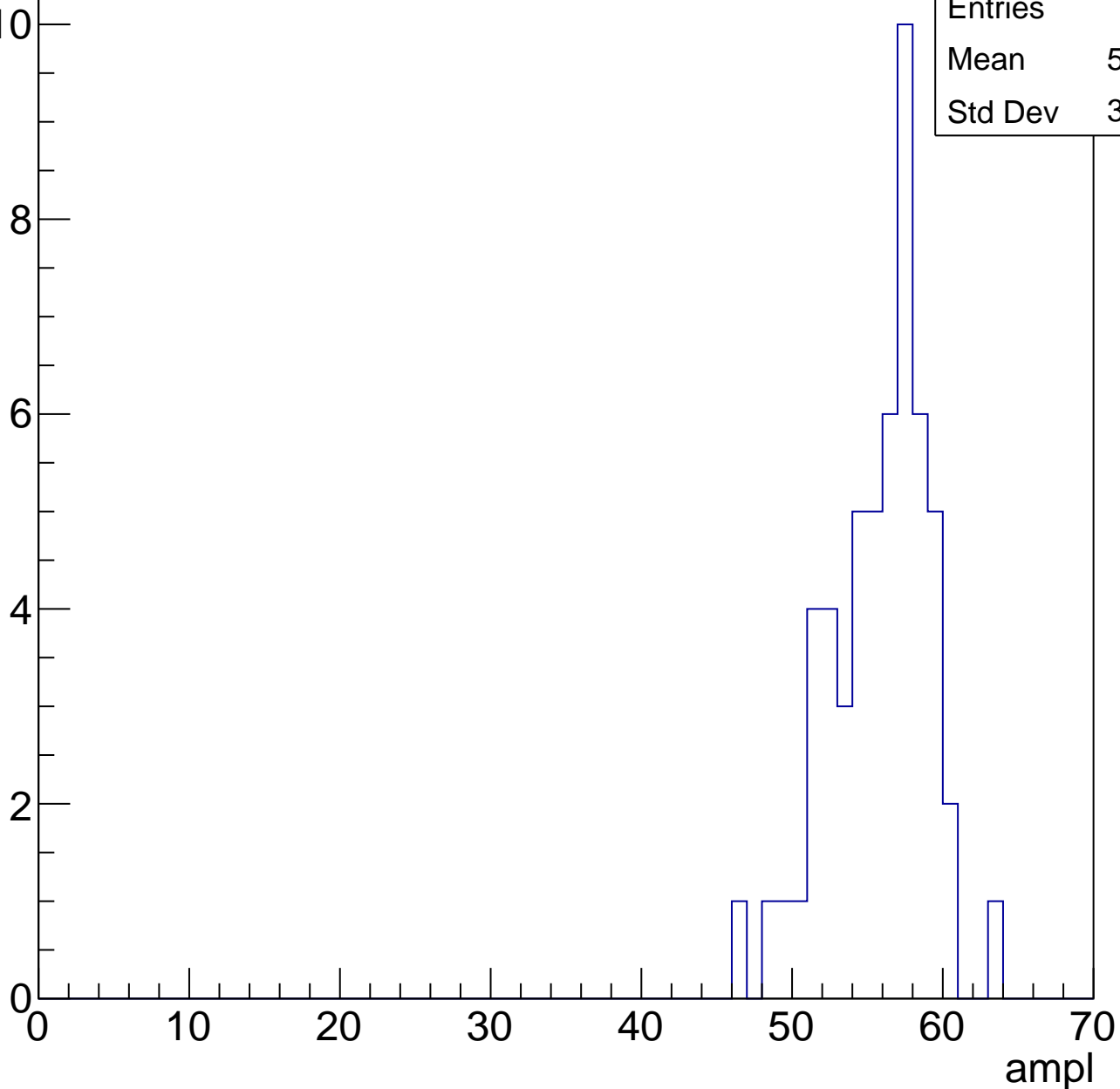


B1L102S, U13-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.29
Std Dev	3.296



B1L102S, U13-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	44
Mean	59.16
Std Dev	9.19

Entry

10

8

6

4

2

0

0

10

20

30

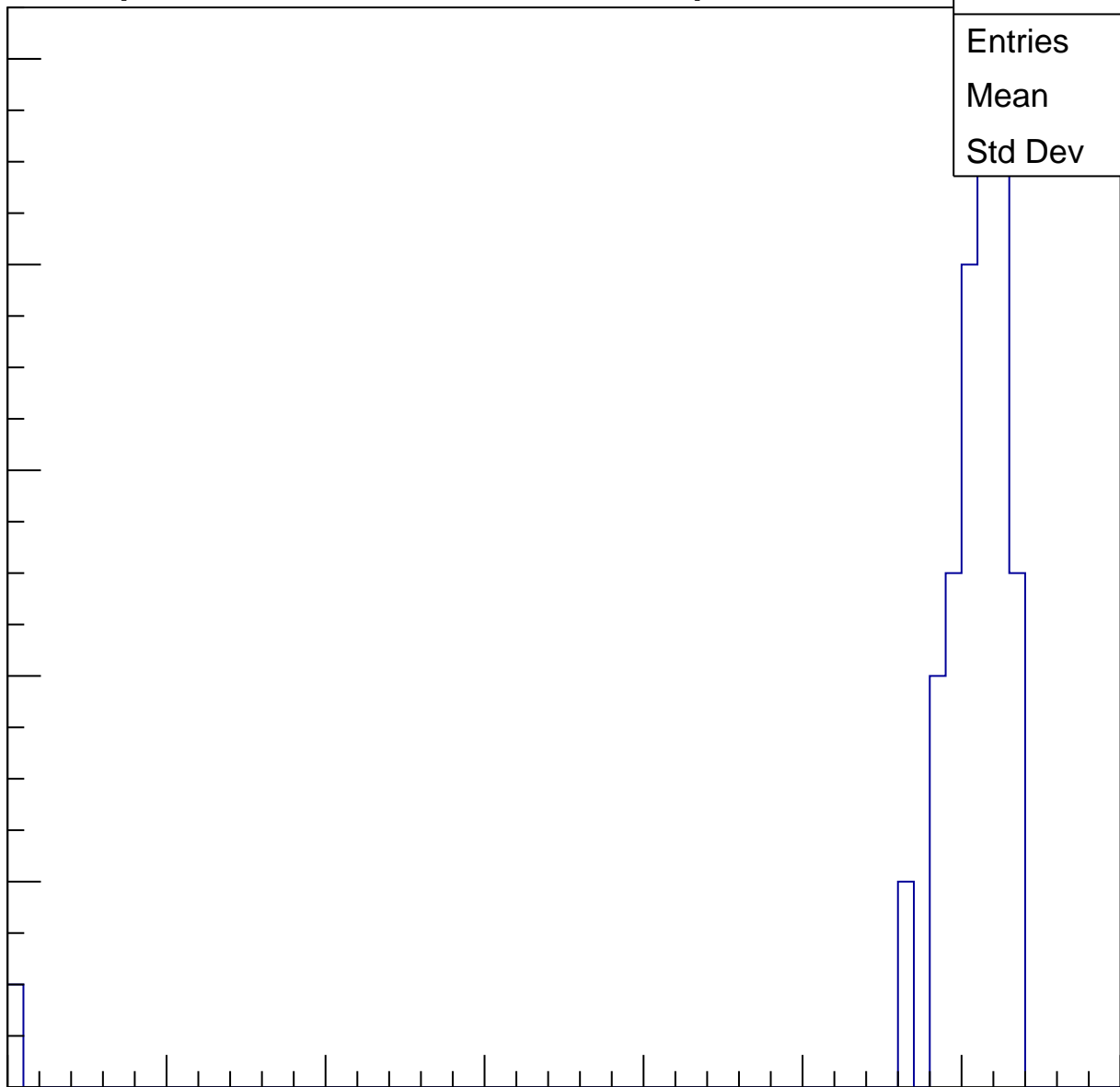
40

50

60

70

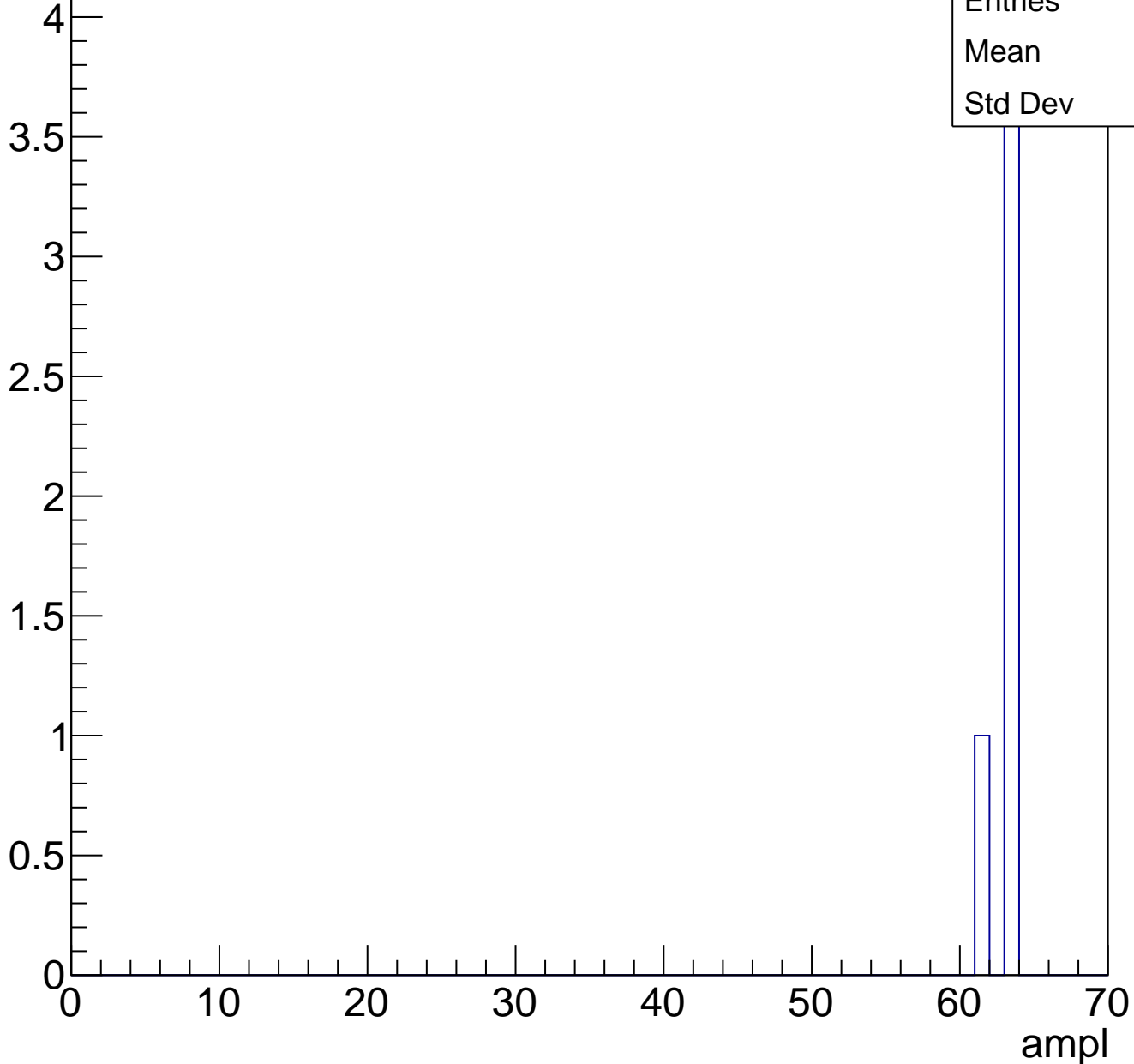
ampl



B1L102S, U13-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch67, adc0

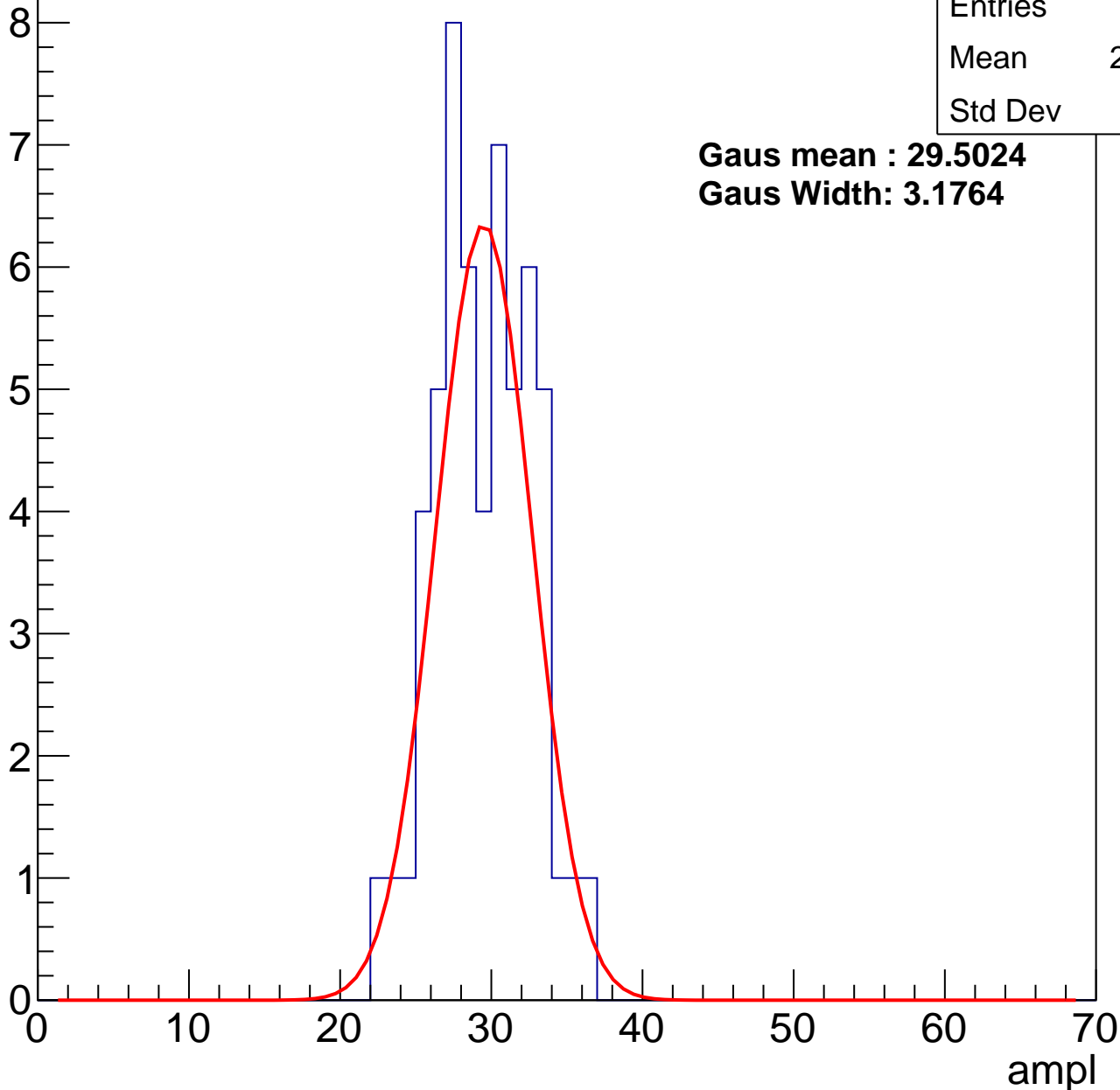
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	29.04
Std Dev	3.07

Gaus mean : 29.5024

Gaus Width: 3.1764



B1L102S, U13-ch67, adc1

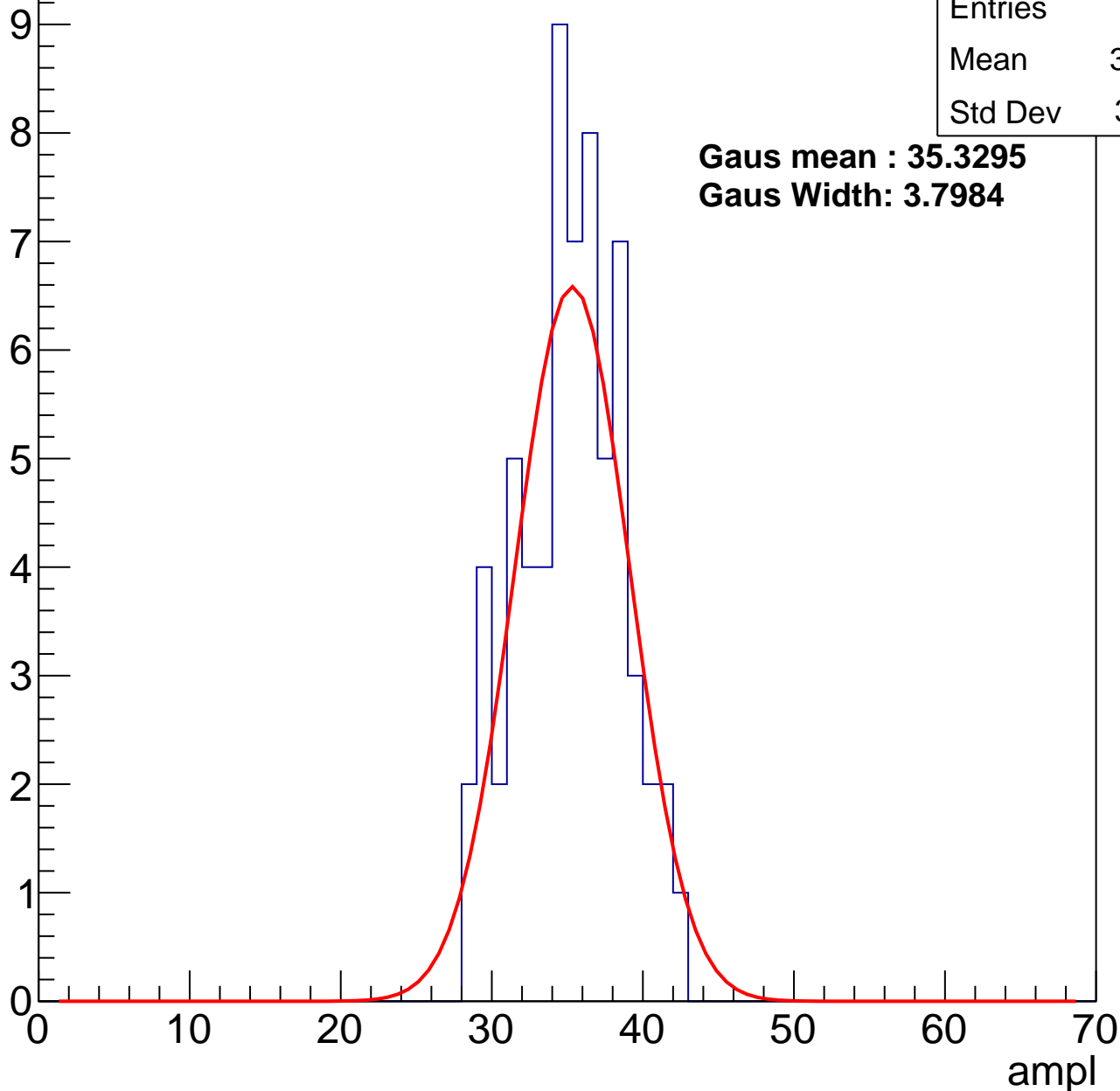
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.74
Std Dev	3.371

Gaus mean : 35.3295

Gaus Width: 3.7984



B1L102S, U13-ch67, adc2

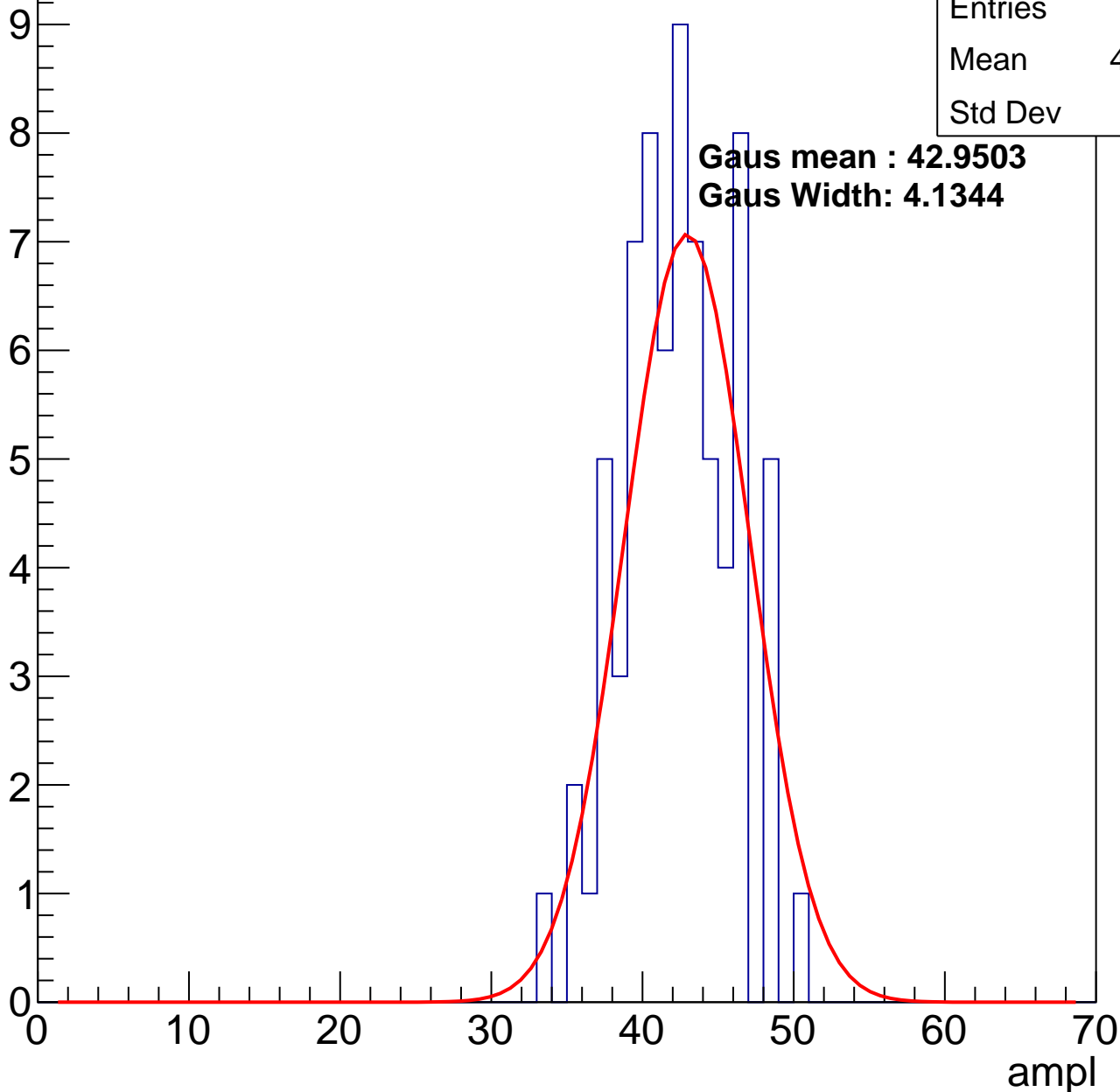
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.86
Std Dev	3.58

Gaus mean : 42.9503

Gaus Width: 4.1344

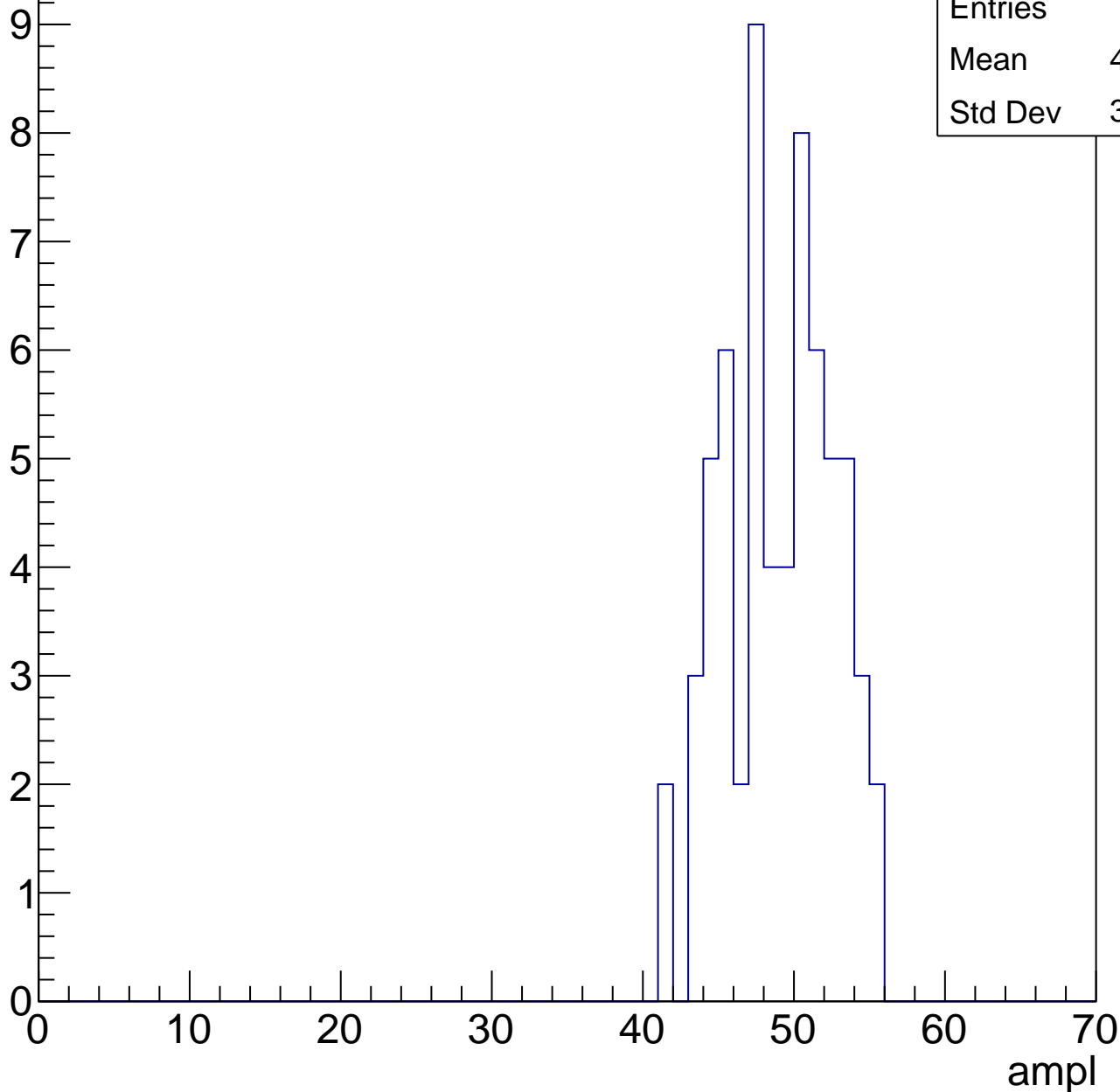


B1L102S, U13-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.55
Std Dev	3.548

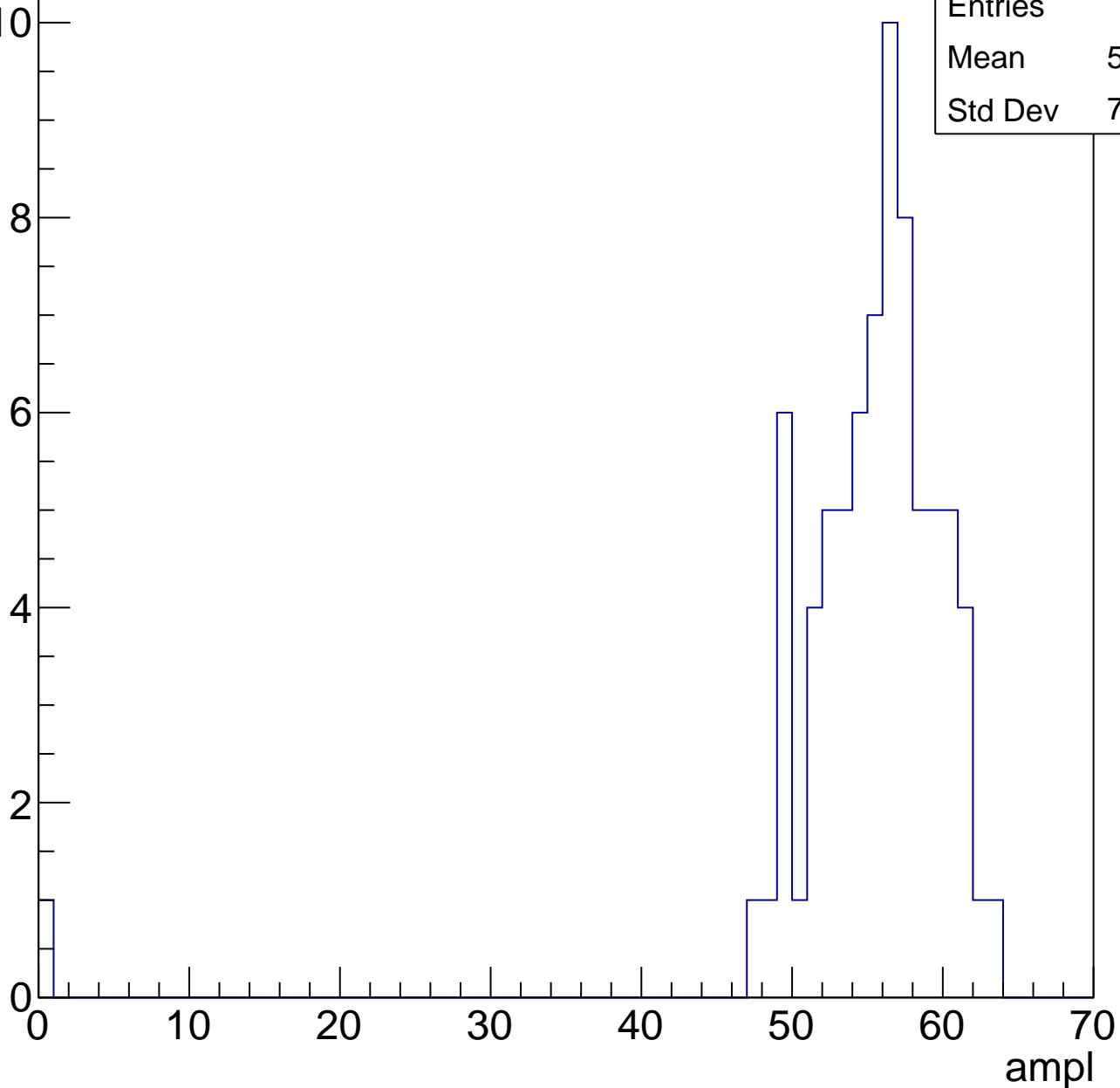


B1L102S, U13-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	54.57
Std Dev	7.299

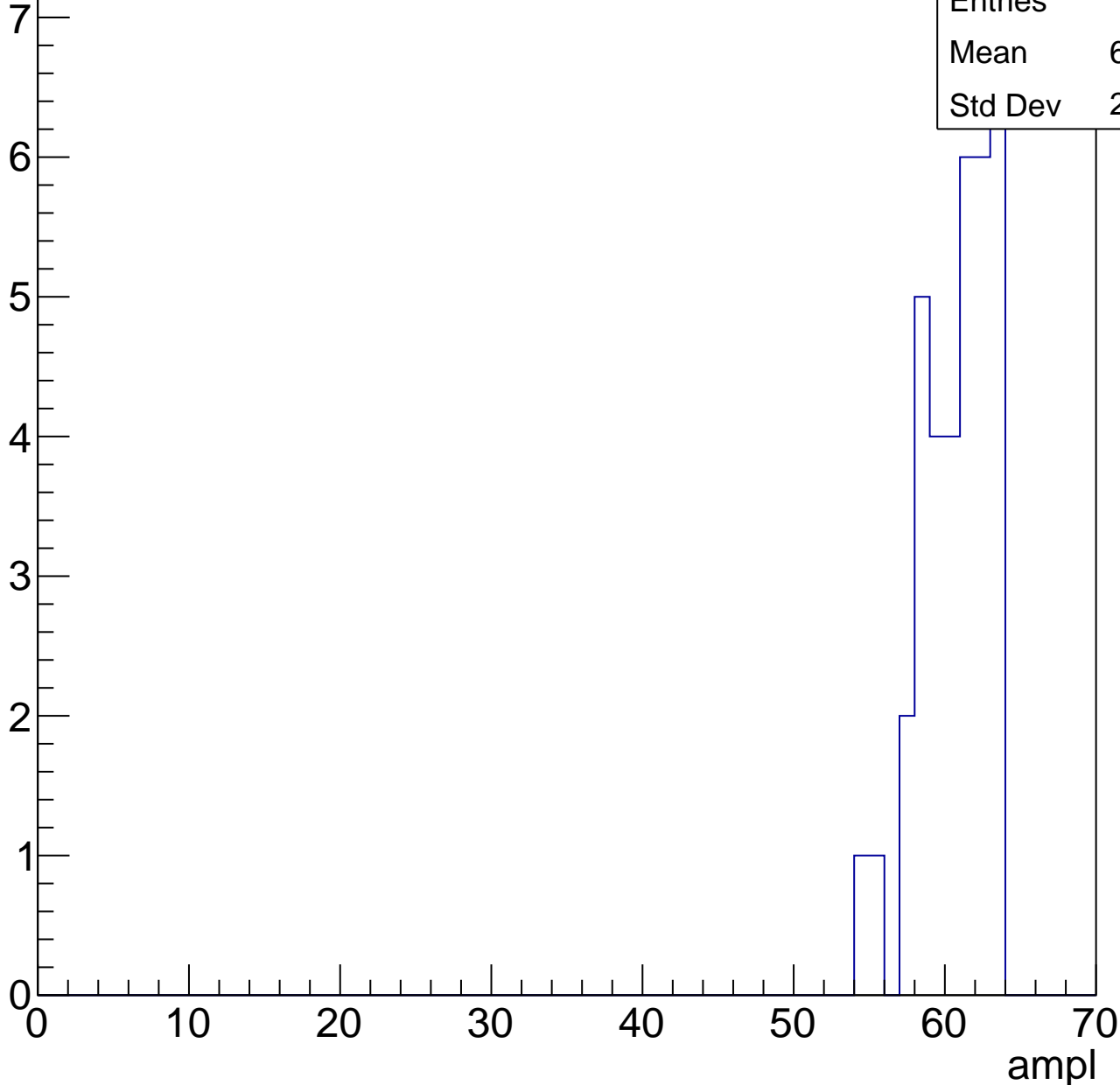


B1L102S, U13-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

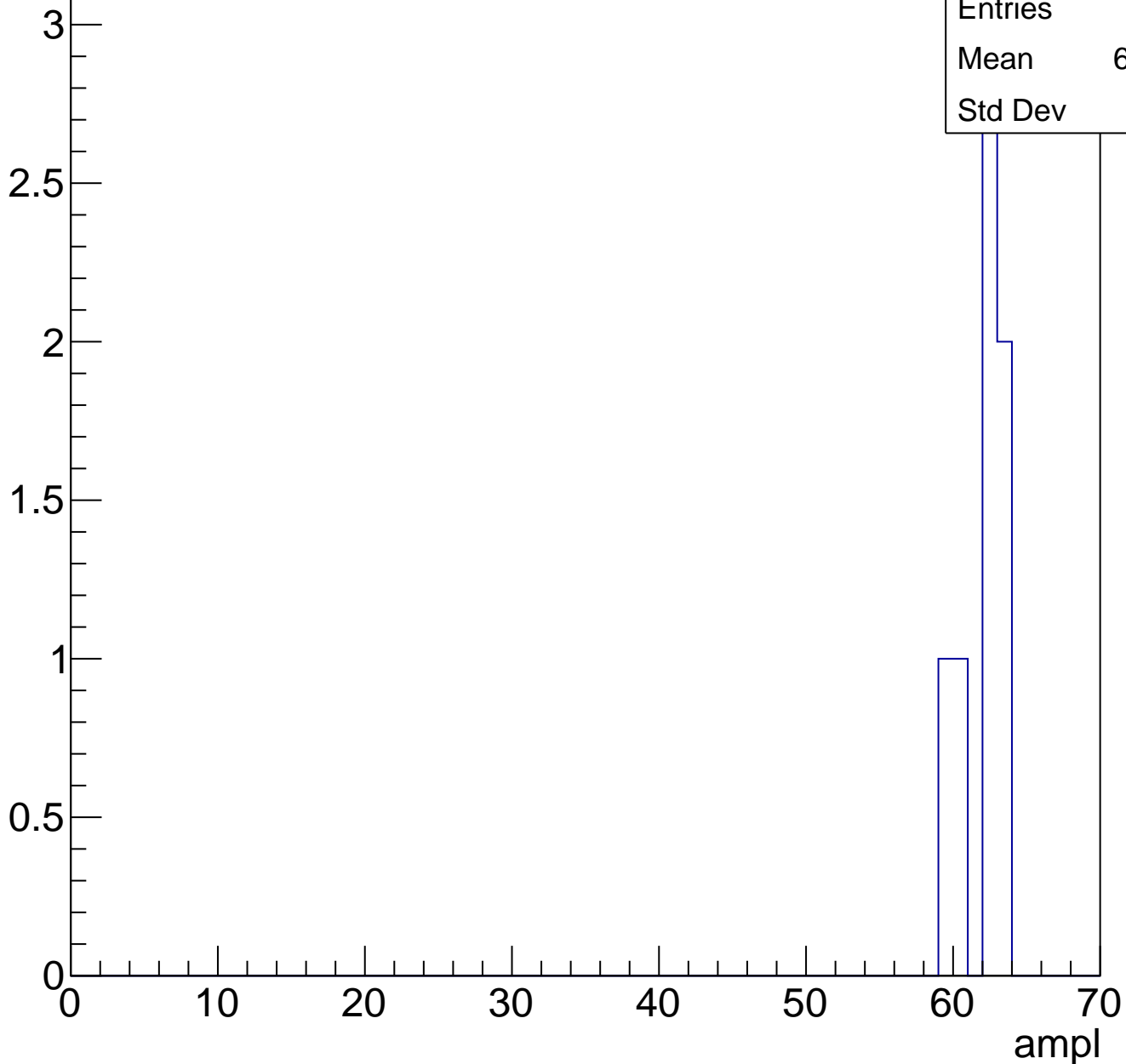
Entries	36
Mean	60.22
Std Dev	2.323



B1L102S, U13-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch68, adc0

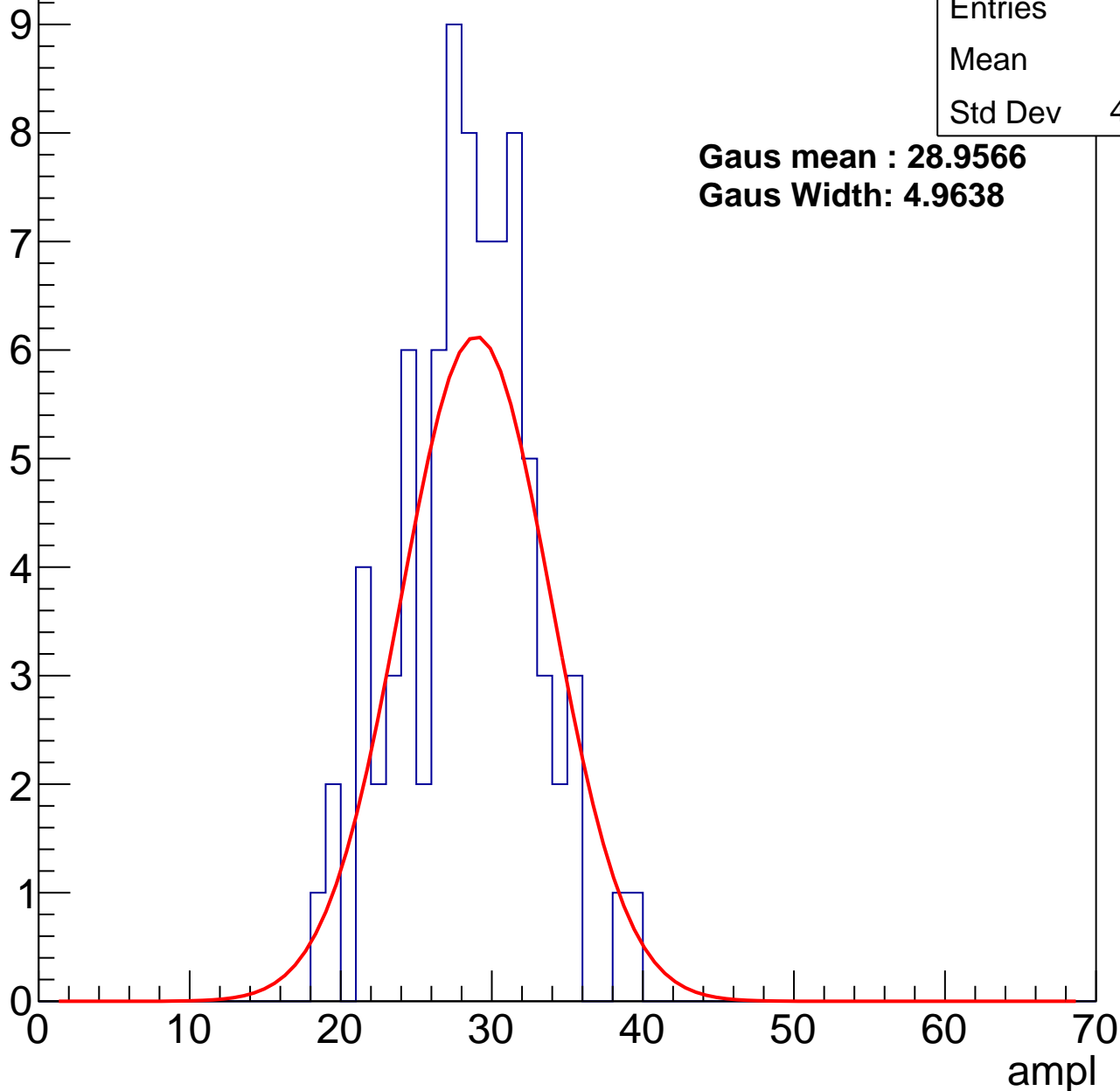
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28
Std Dev	4.263

Gaus mean : 28.9566

Gaus Width: 4.9638



B1L102S, U13-ch68, adc1

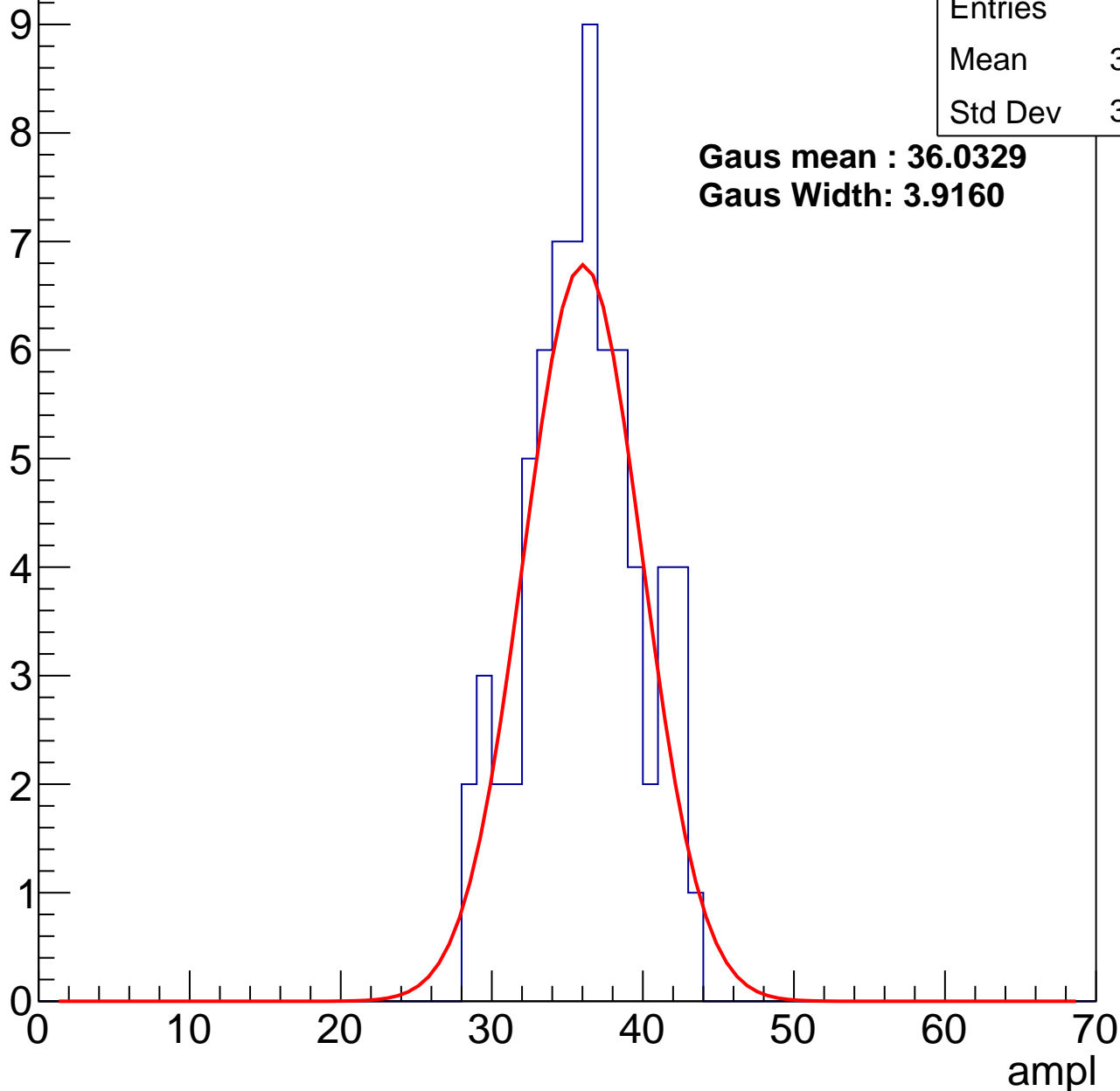
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.59
Std Dev	3.674

Gaus mean : 36.0329

Gaus Width: 3.9160



B1L102S, U13-ch68, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42.56
Std Dev	3.533

Gaus mean : 43.6795

Gaus Width: 4.0670

10

8

6

4

2

0

0

10

20

30

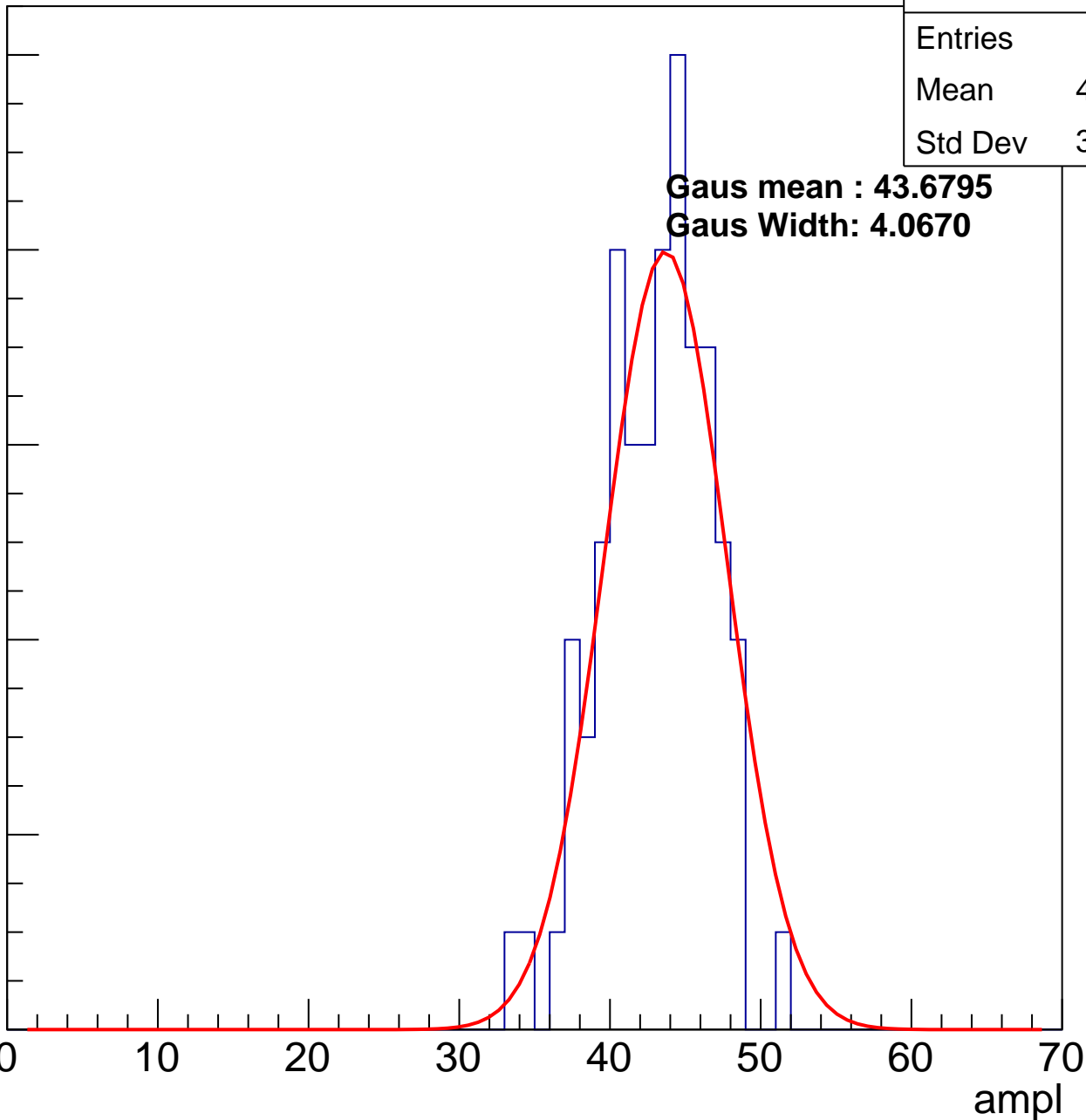
40

50

60

70

ampl

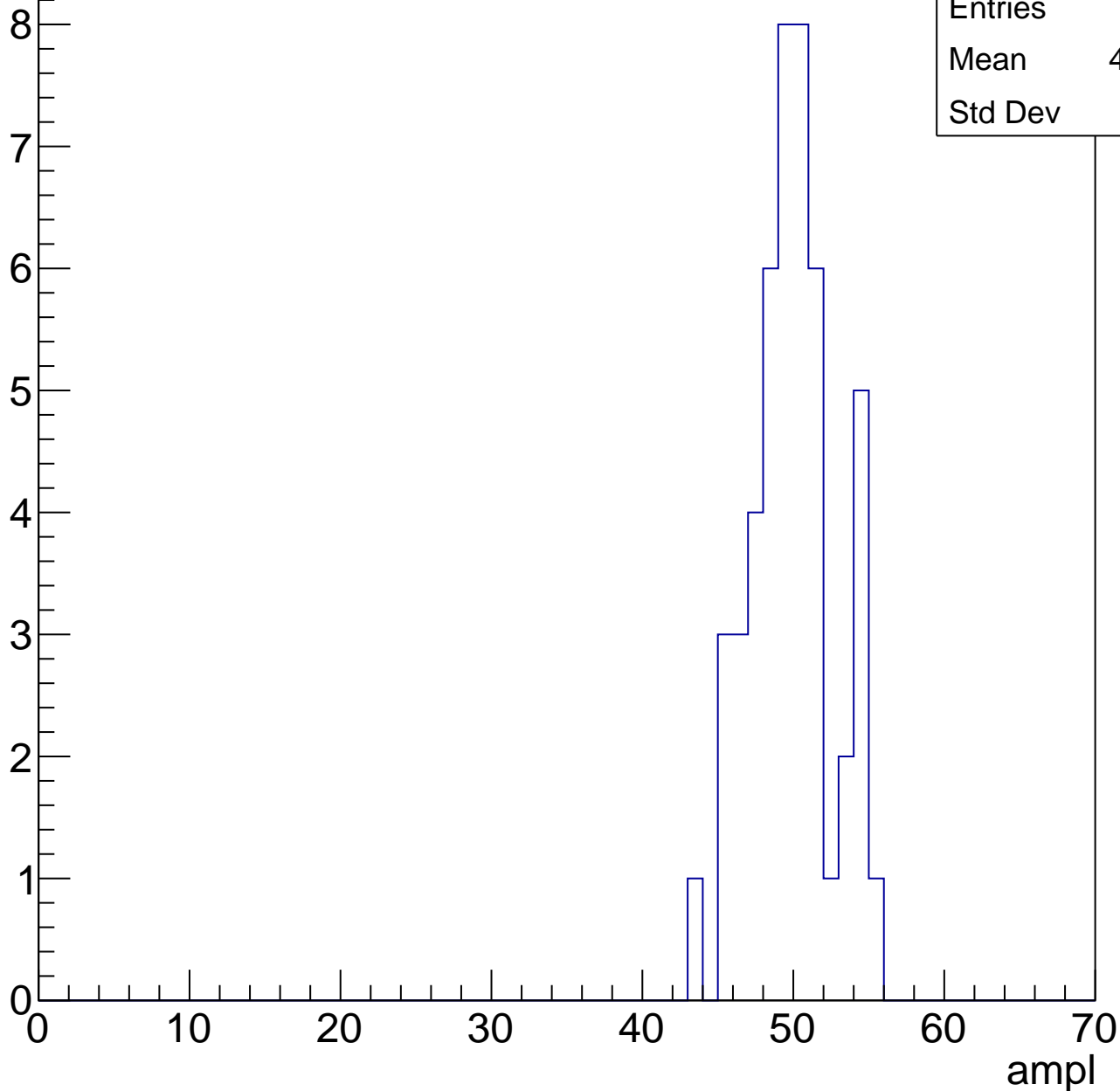


B1L102S, U13-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	49.44
Std Dev	2.73

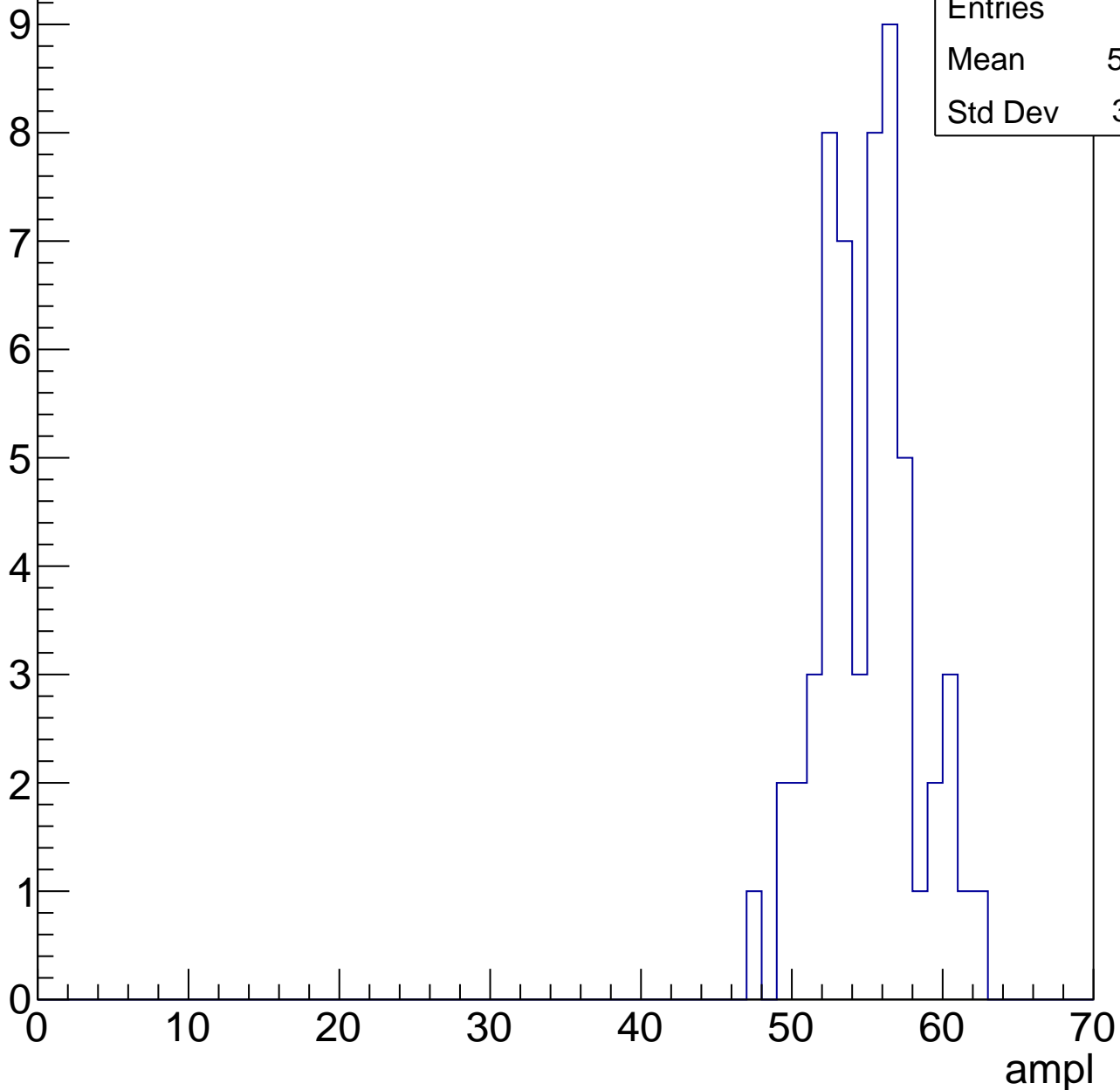


B1L102S, U13-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.55
Std Dev	3.151



B1L102S, U13-ch68, adc5

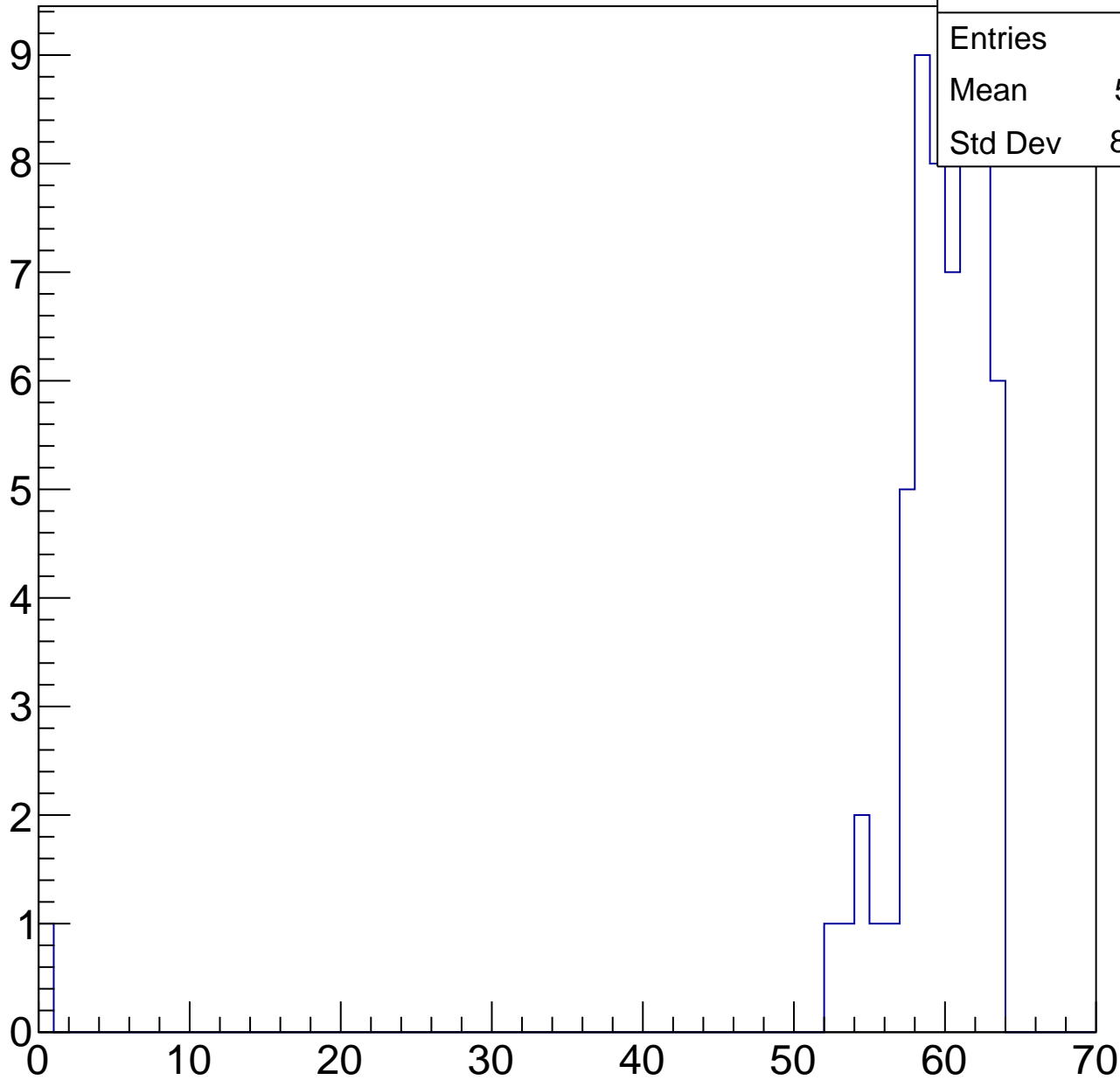
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	58.41
Std Dev	8.089

ampl



B1L102S, U13-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch69, adc0

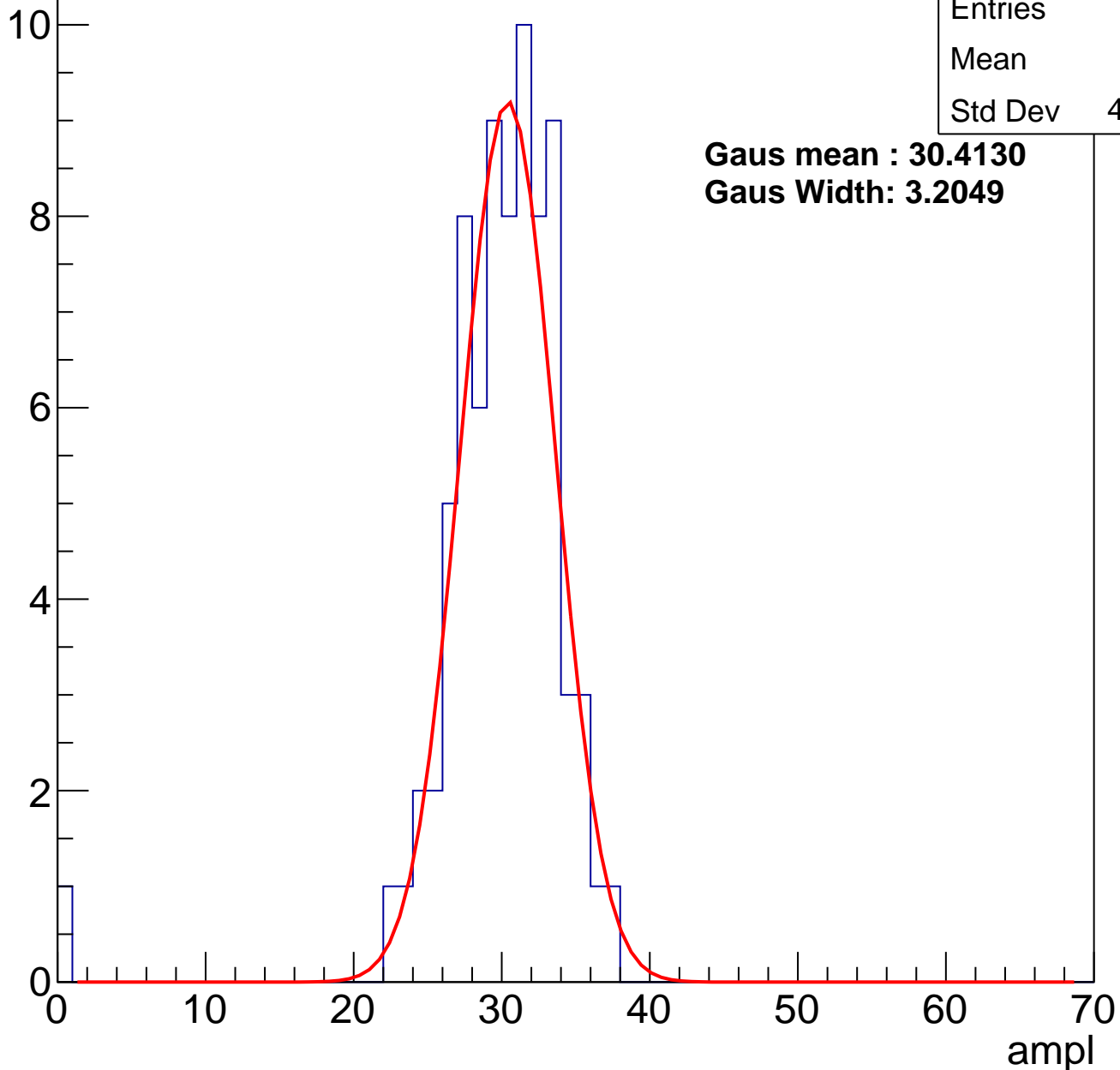
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	29.5
Std Dev	4.573

Gaus mean : 30.4130

Gaus Width: 3.2049

Entry



B1L102S, U13-ch69, adc1

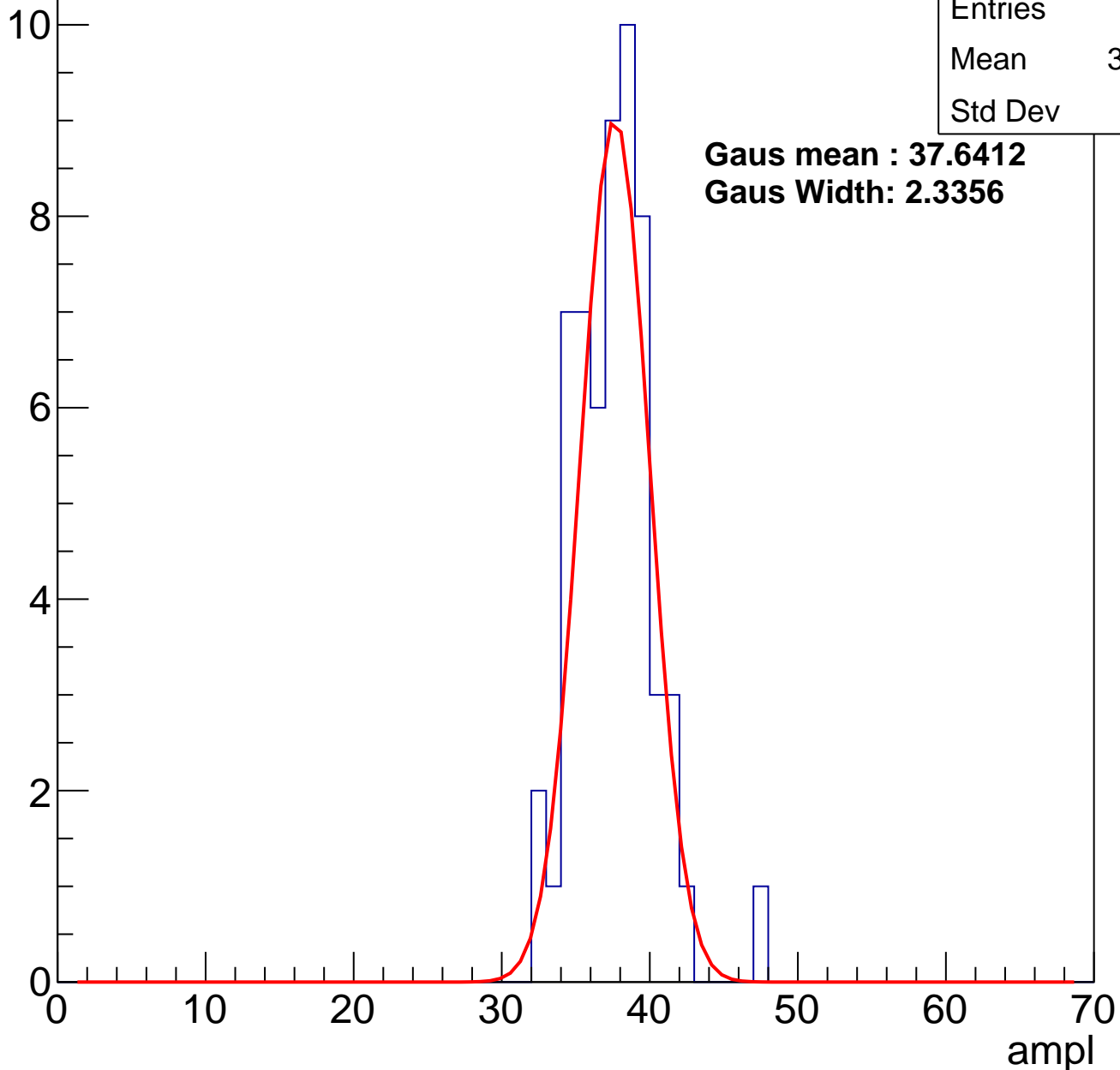
calib_packv5_042523_0143.root, FC#11, port A2

Entries	58
Mean	37.12
Std Dev	2.64

Gaus mean : 37.6412

Gaus Width: 2.3356

Entry



B1L102S, U13-ch69, adc2

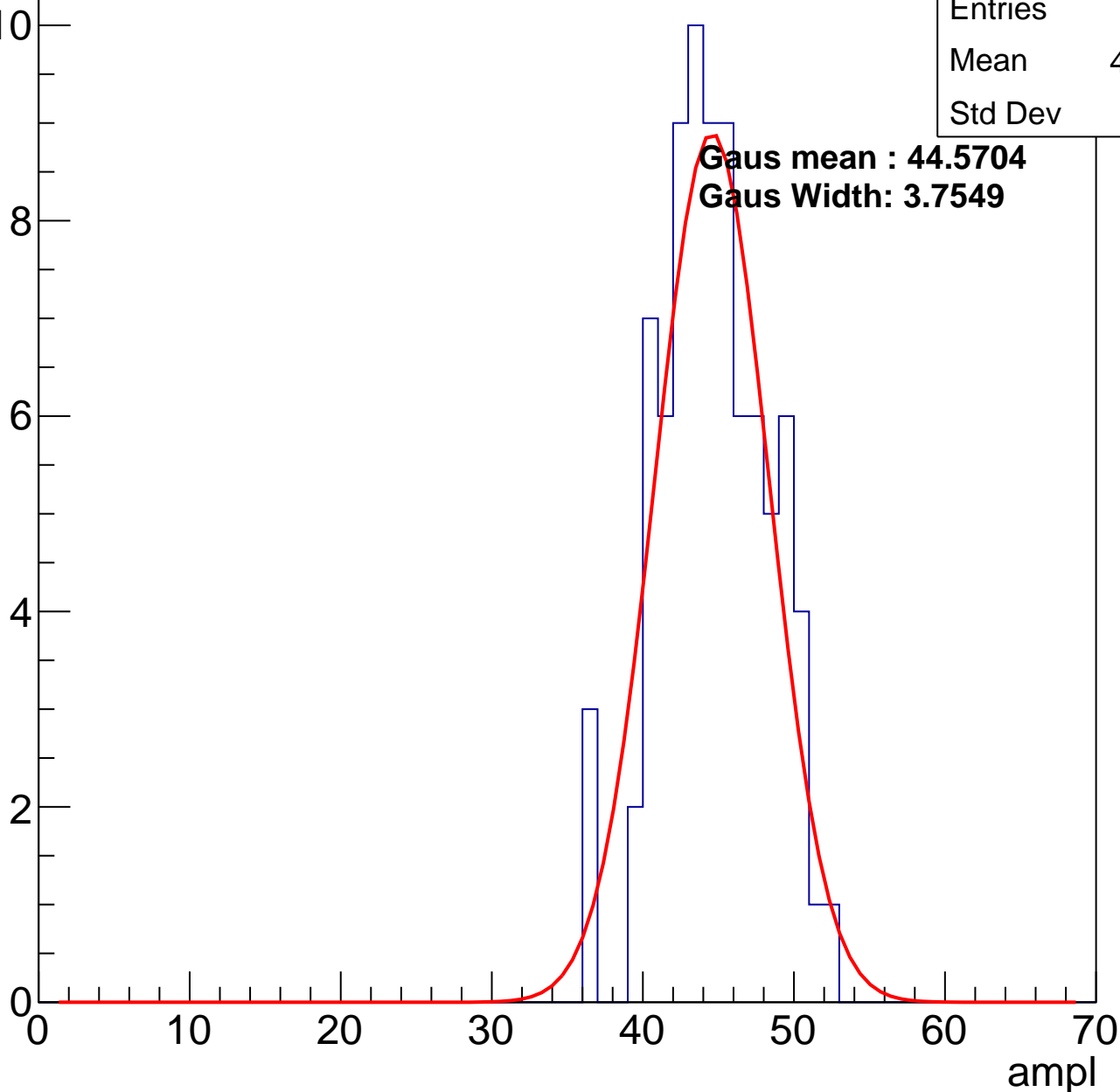
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	44.24
Std Dev	3.49

Gaus mean : 44.5704

Gaus Width: 3.7549

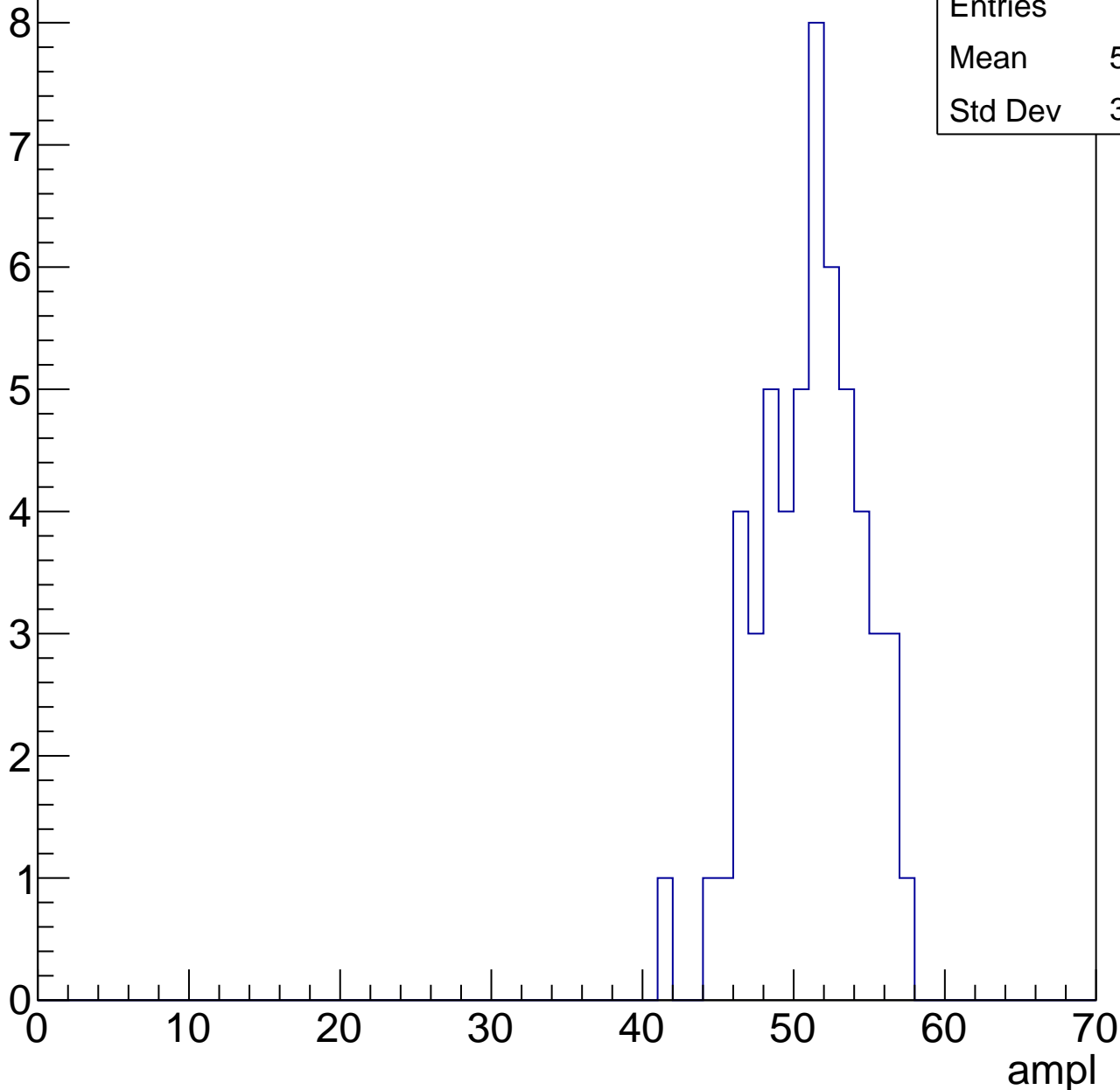


B1L102S, U13-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	50.59
Std Dev	3.364



B1L102S, U13-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

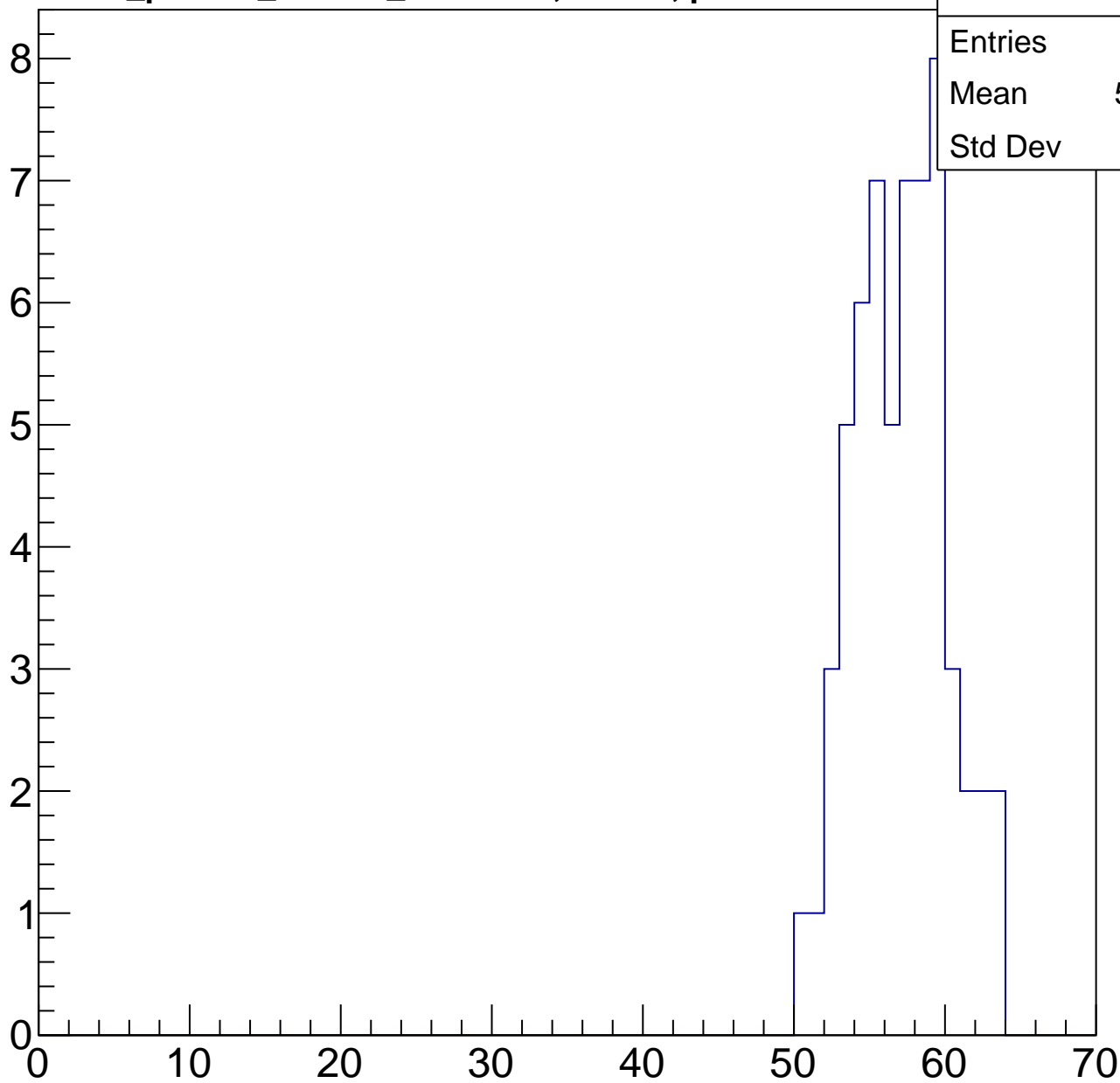
Entry

8
7
6
5
4
3
2
1
0

Entries	59
Mean	56.61
Std Dev	3.02

ampl

0 10 20 30 40 50 60 70

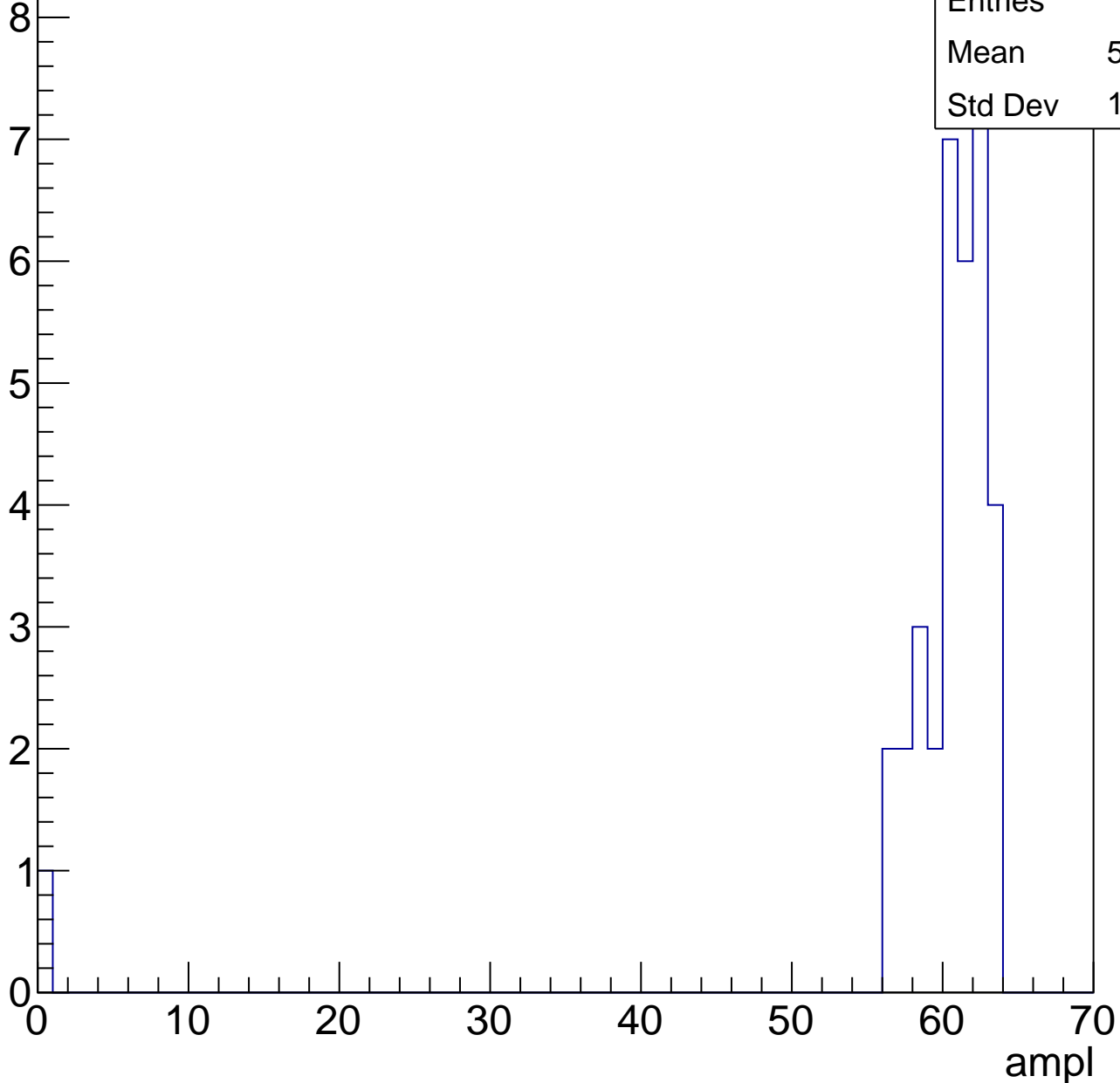


B1L102S, U13-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

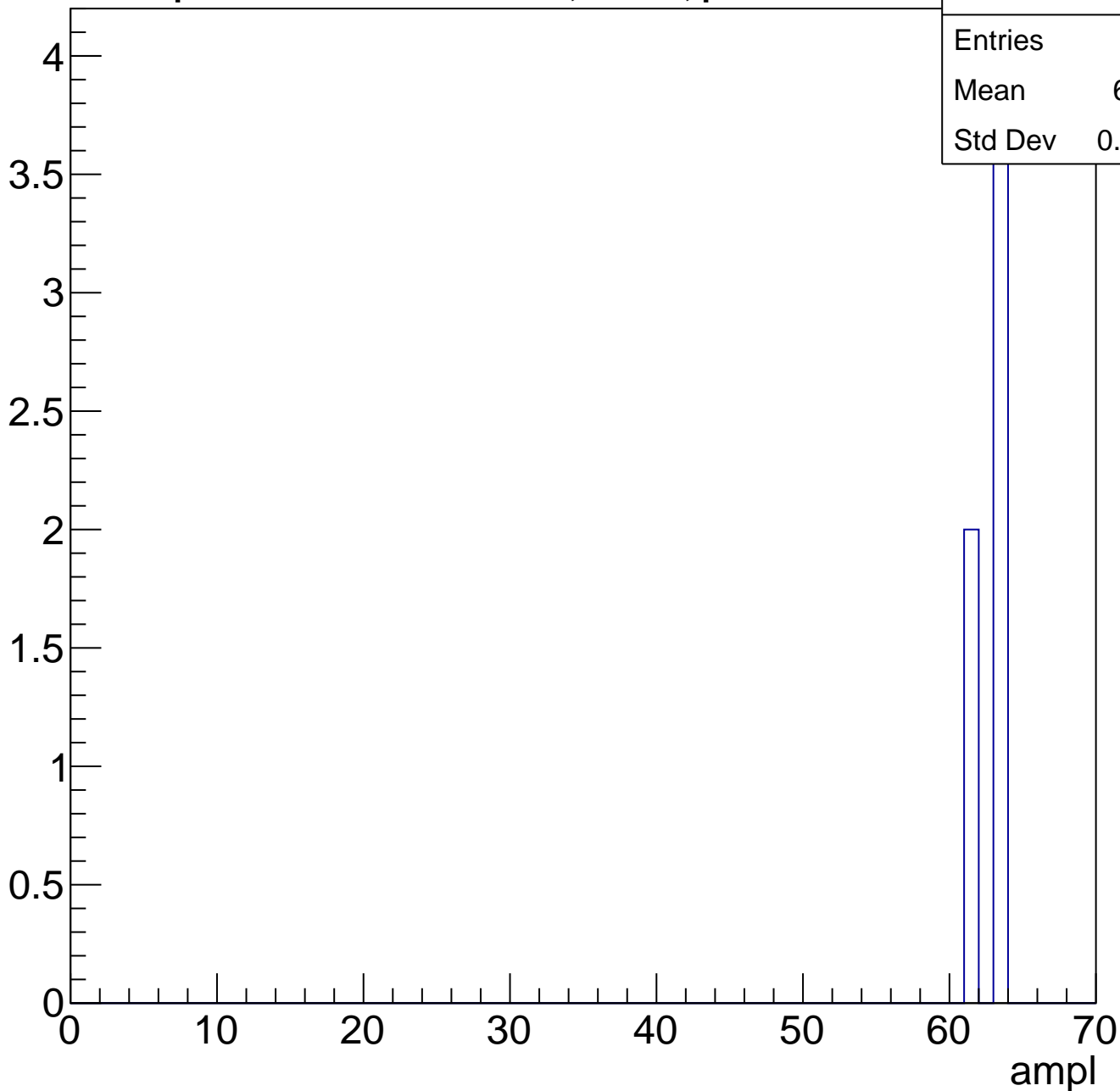
Entries	35
Mean	58.63
Std Dev	10.24



B1L102S, U13-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch70, adc0

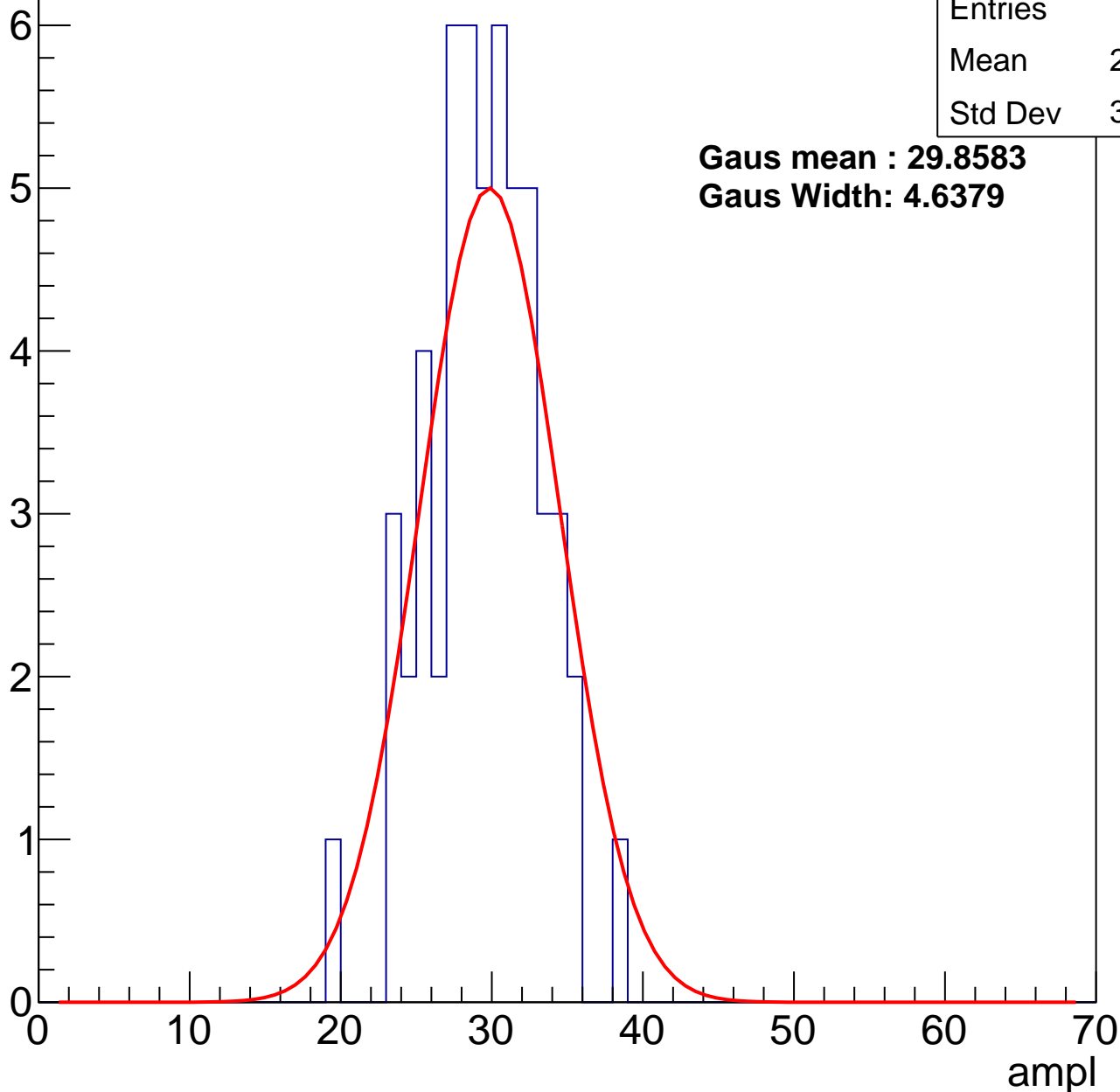
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	29.02
Std Dev	3.644

Gaus mean : 29.8583

Gaus Width: 4.6379



B1L102S, U13-ch70, adc1

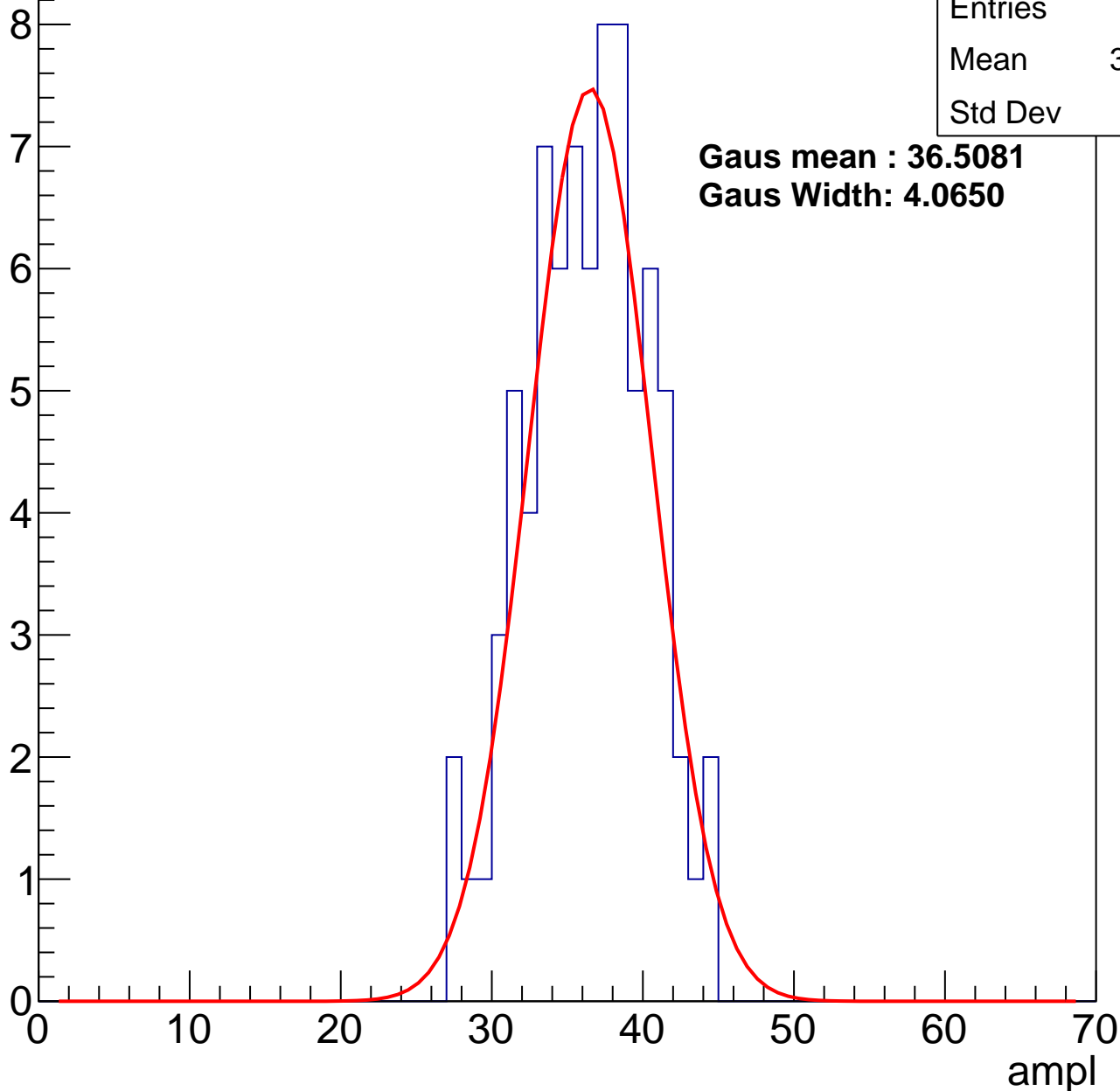
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.89
Std Dev	3.93

Gaus mean : 36.5081

Gaus Width: 4.0650



B1L102S, U13-ch70, adc2

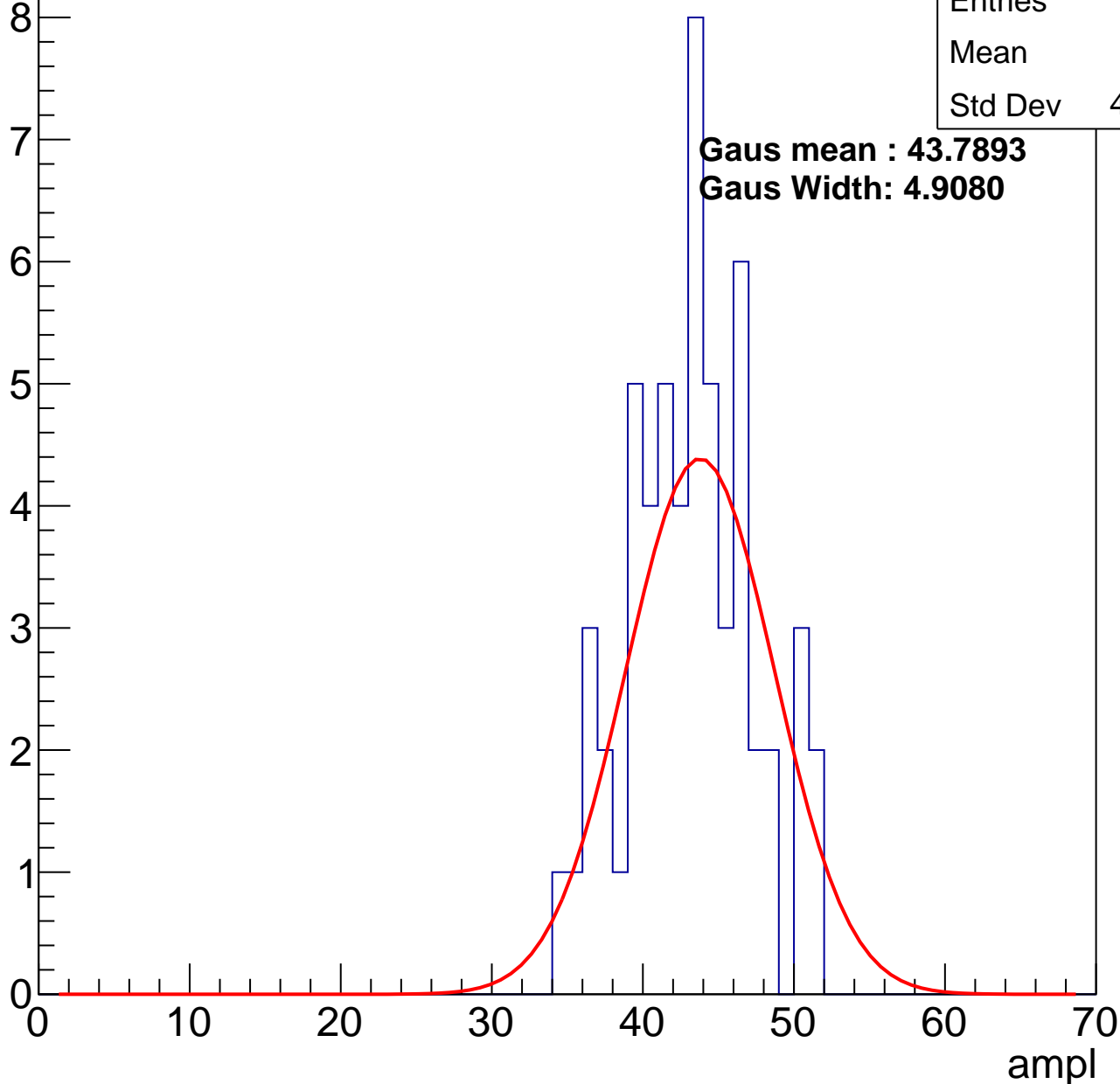
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.7
Std Dev	4.087

Gaus mean : 43.7893

Gaus Width: 4.9080



B1L102S, U13-ch70, adc3

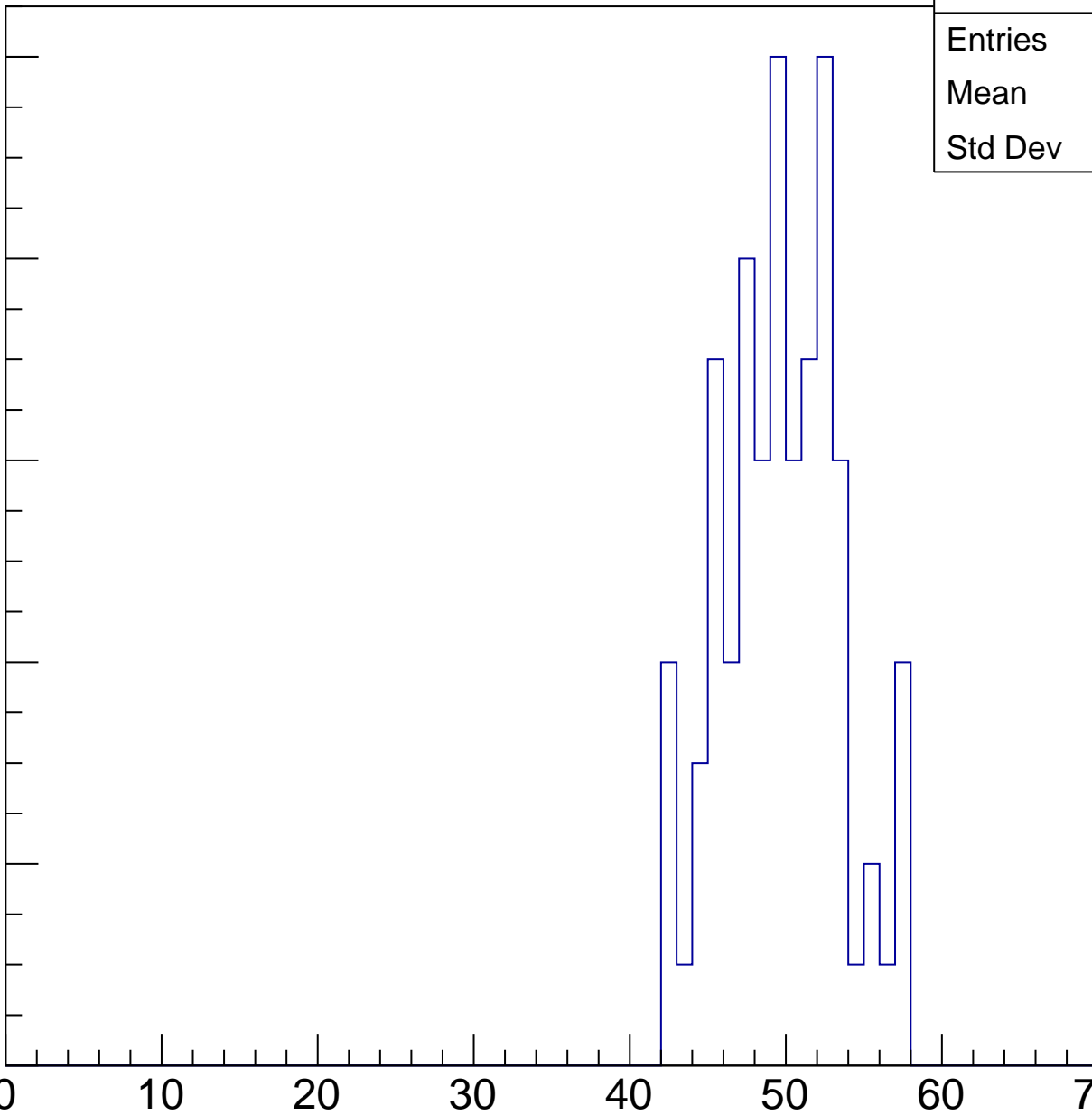
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	49.24
Std Dev	3.726

Entry

10
8
6
4
2
0

ampl

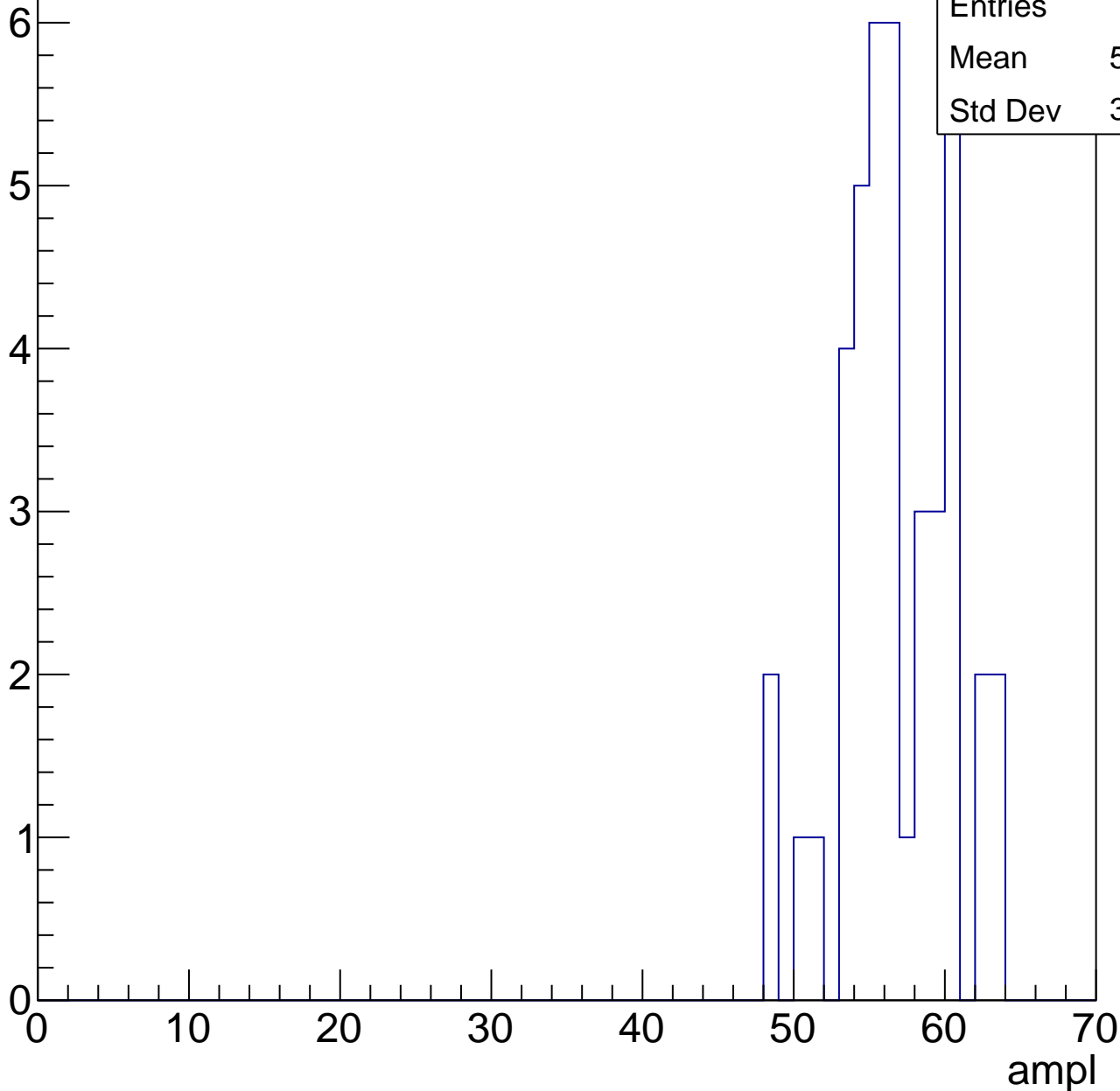


B1L102S, U13-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	56.26
Std Dev	3.632

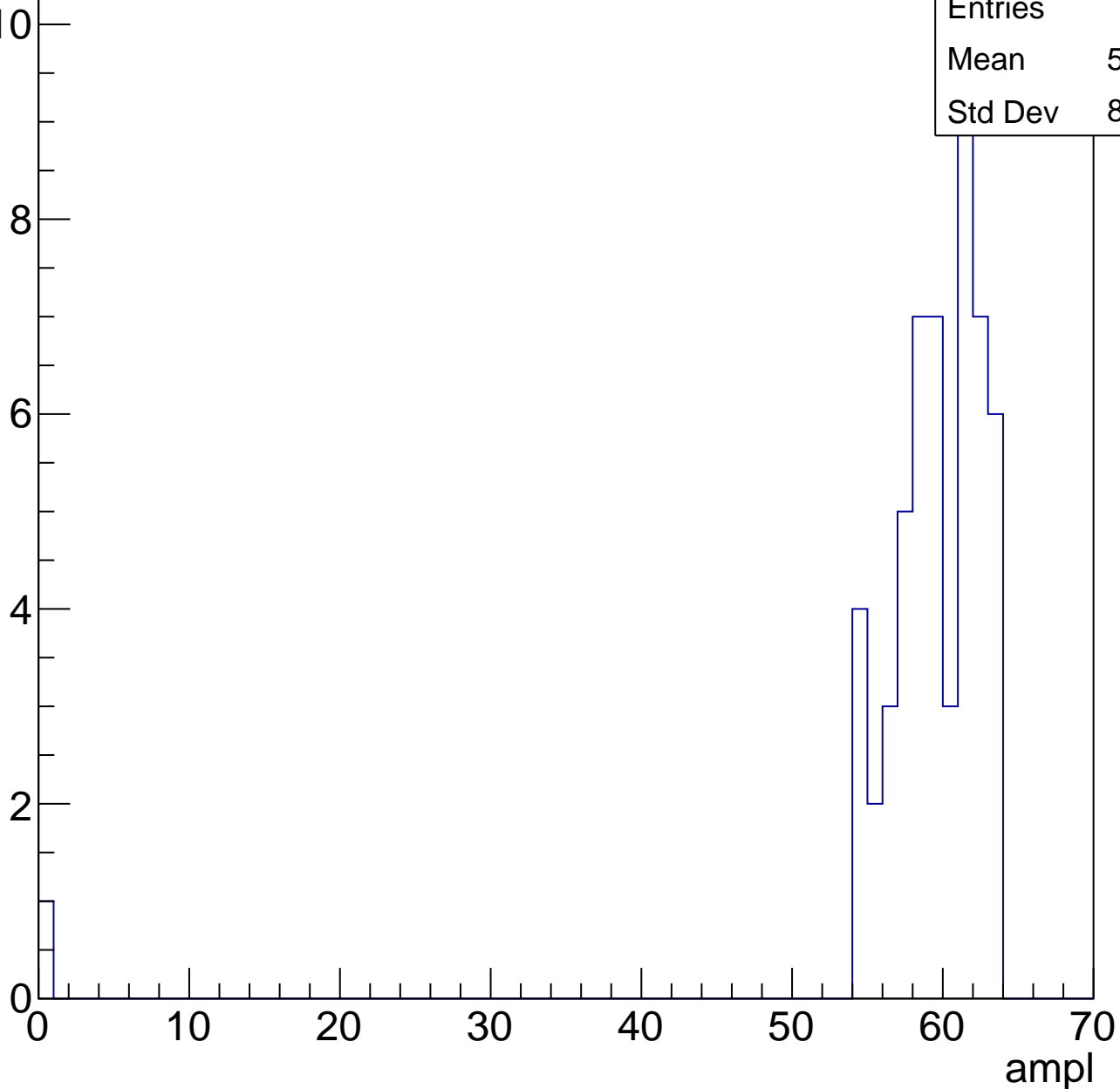


B1L102S, U13-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.18
Std Dev	8.347



B1L102S, U13-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch71, adc0

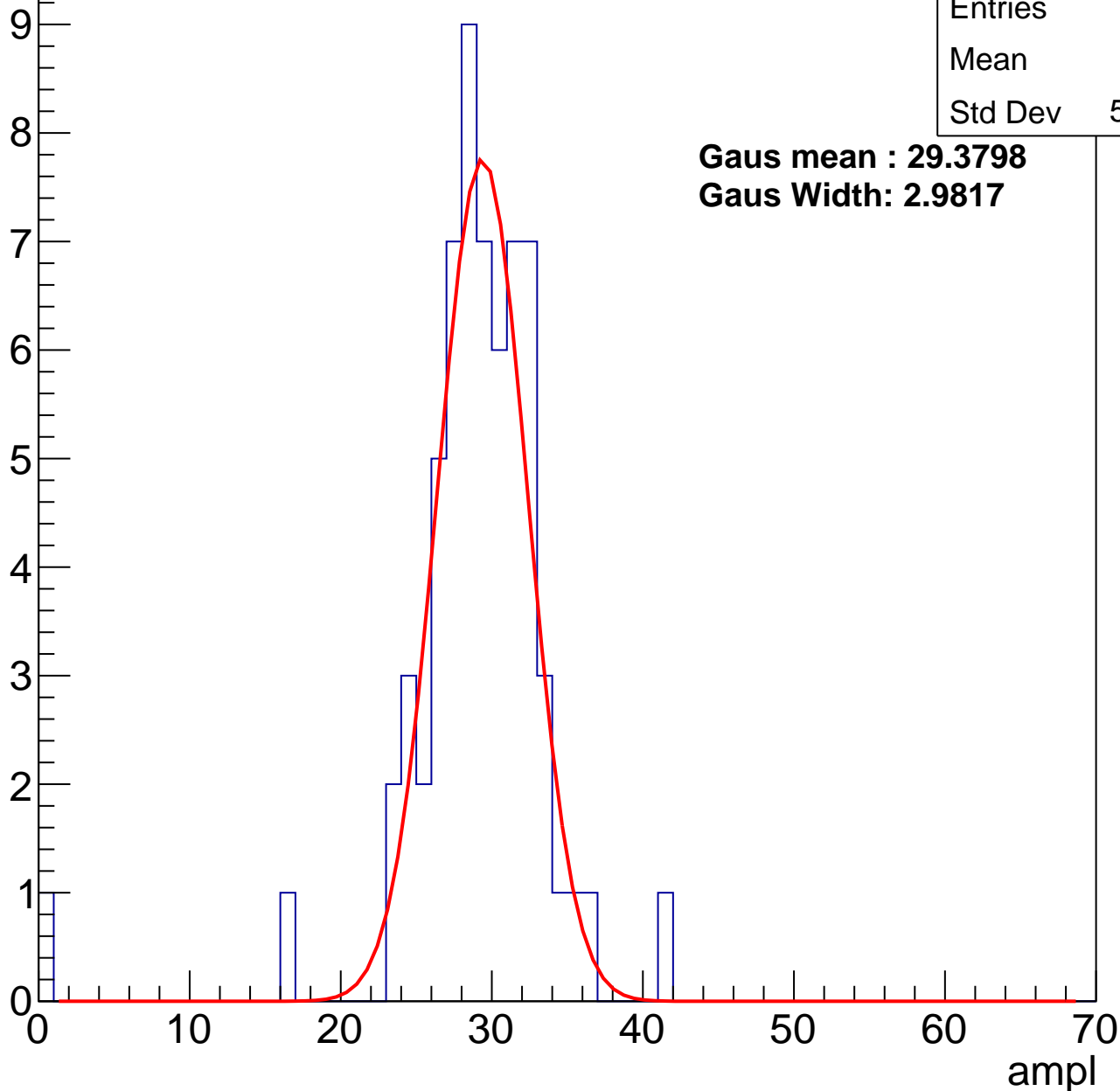
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.5
Std Dev	5.084

Gaus mean : 29.3798

Gaus Width: 2.9817



B1L102S, U13-ch71, adc1

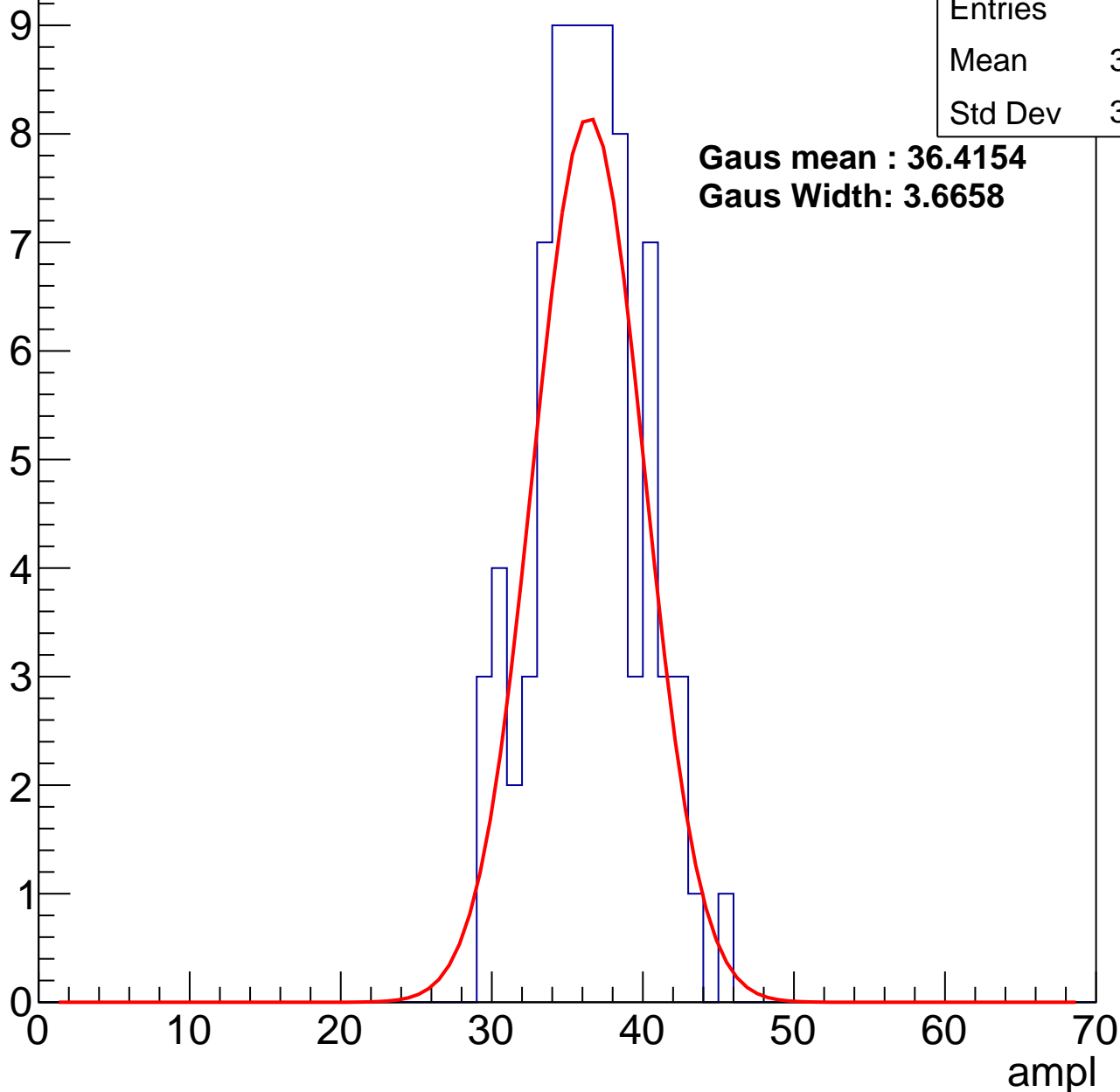
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.95
Std Dev	3.496

Gaus mean : 36.4154

Gaus Width: 3.6658



B1L102S, U13-ch71, adc2

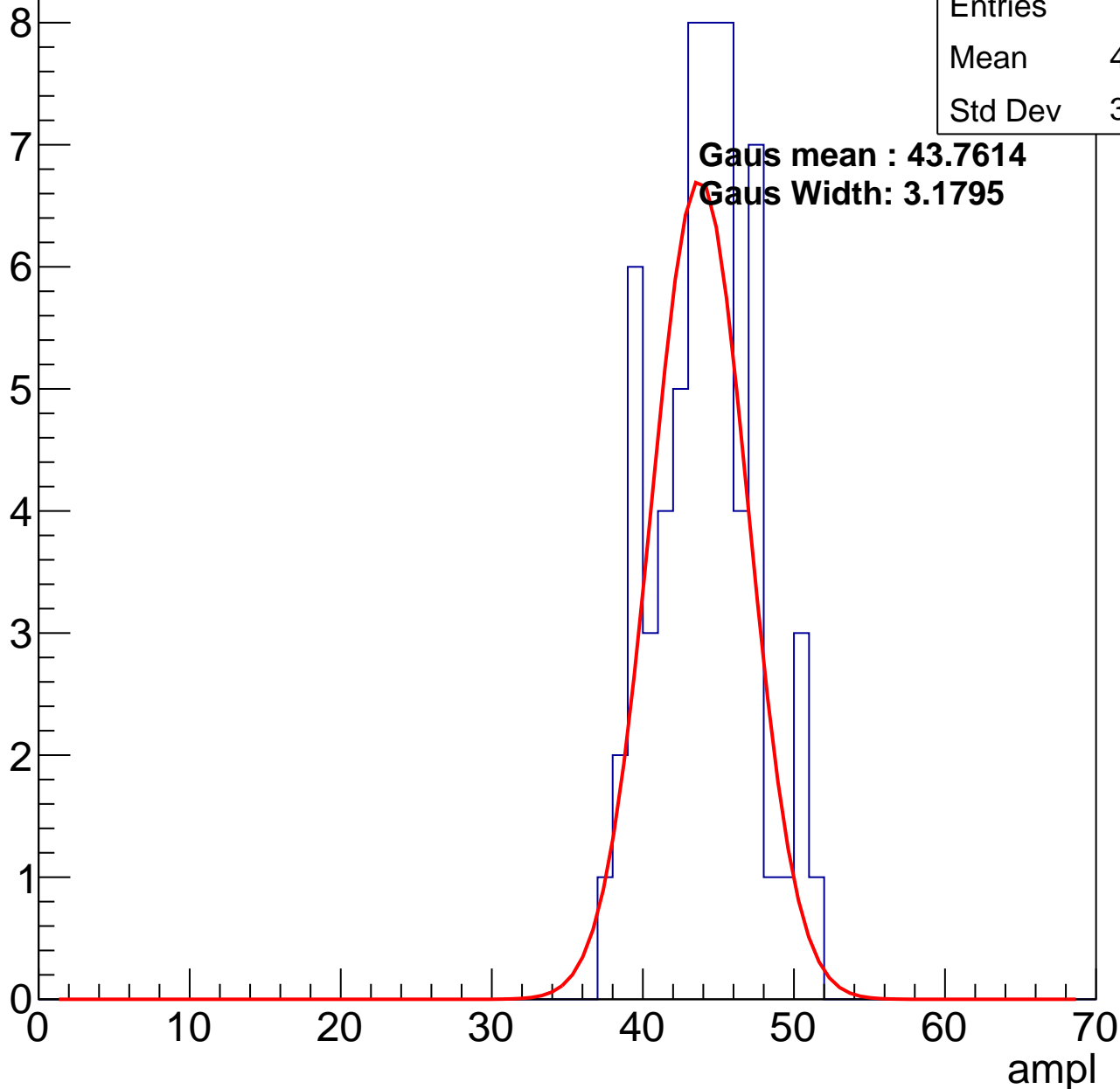
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.68
Std Dev	3.267

Gaus mean : 43.7614

Gaus Width: 3.1795

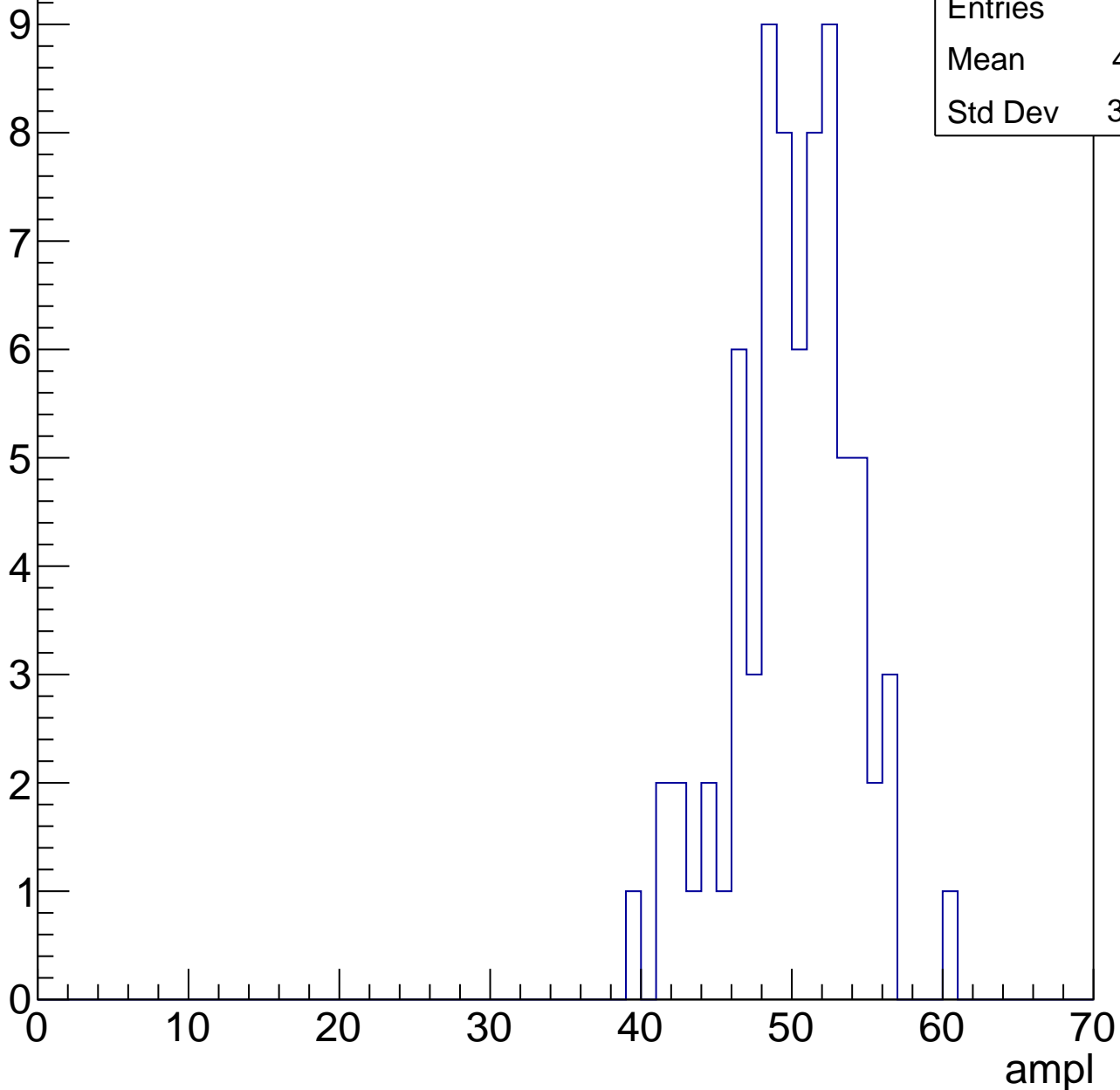


B1L102S, U13-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	49.61
Std Dev	3.924

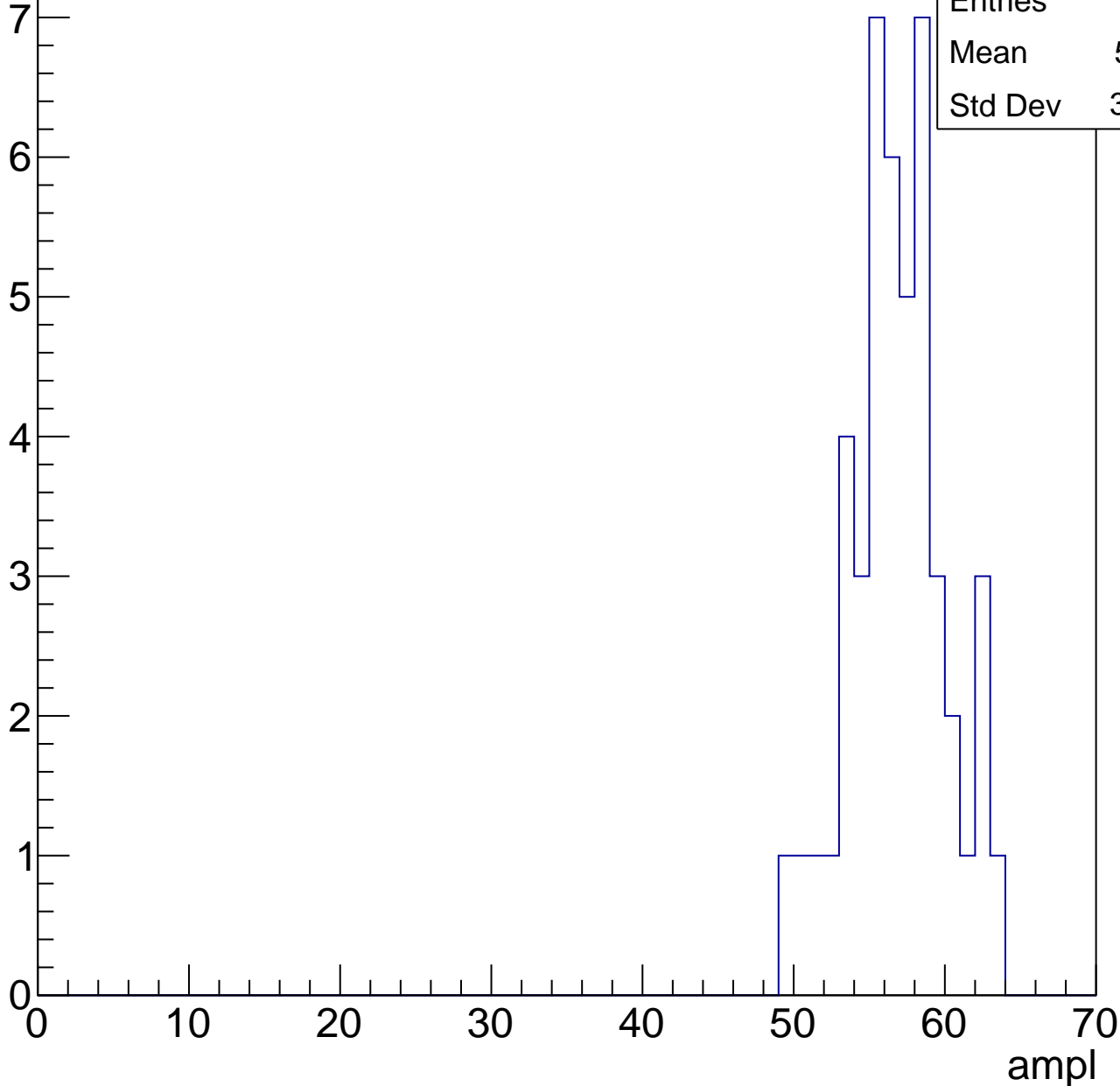


B1L102S, U13-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

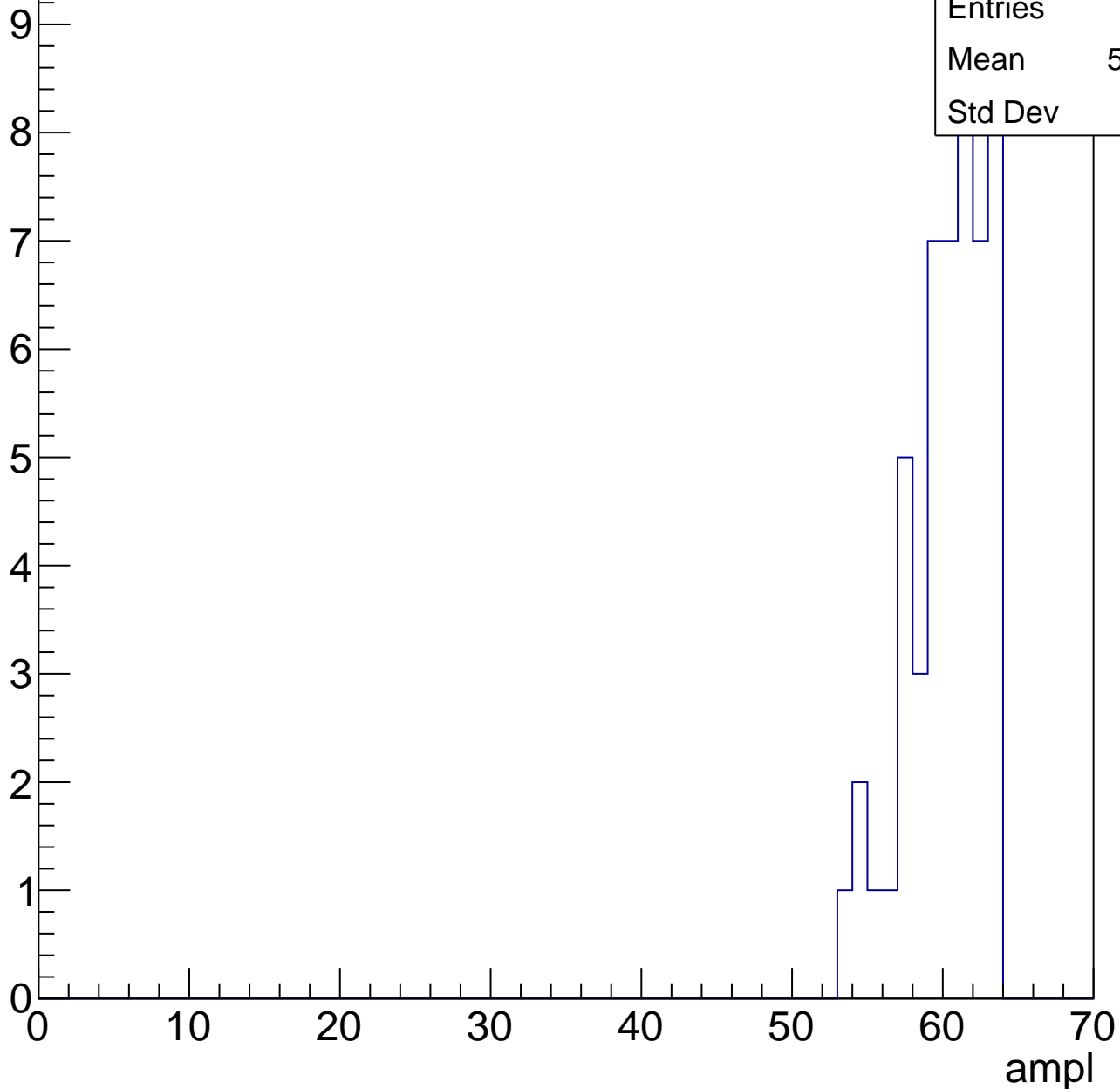
Entries	46
Mean	56.41
Std Dev	3.118



B1L102S, U13-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

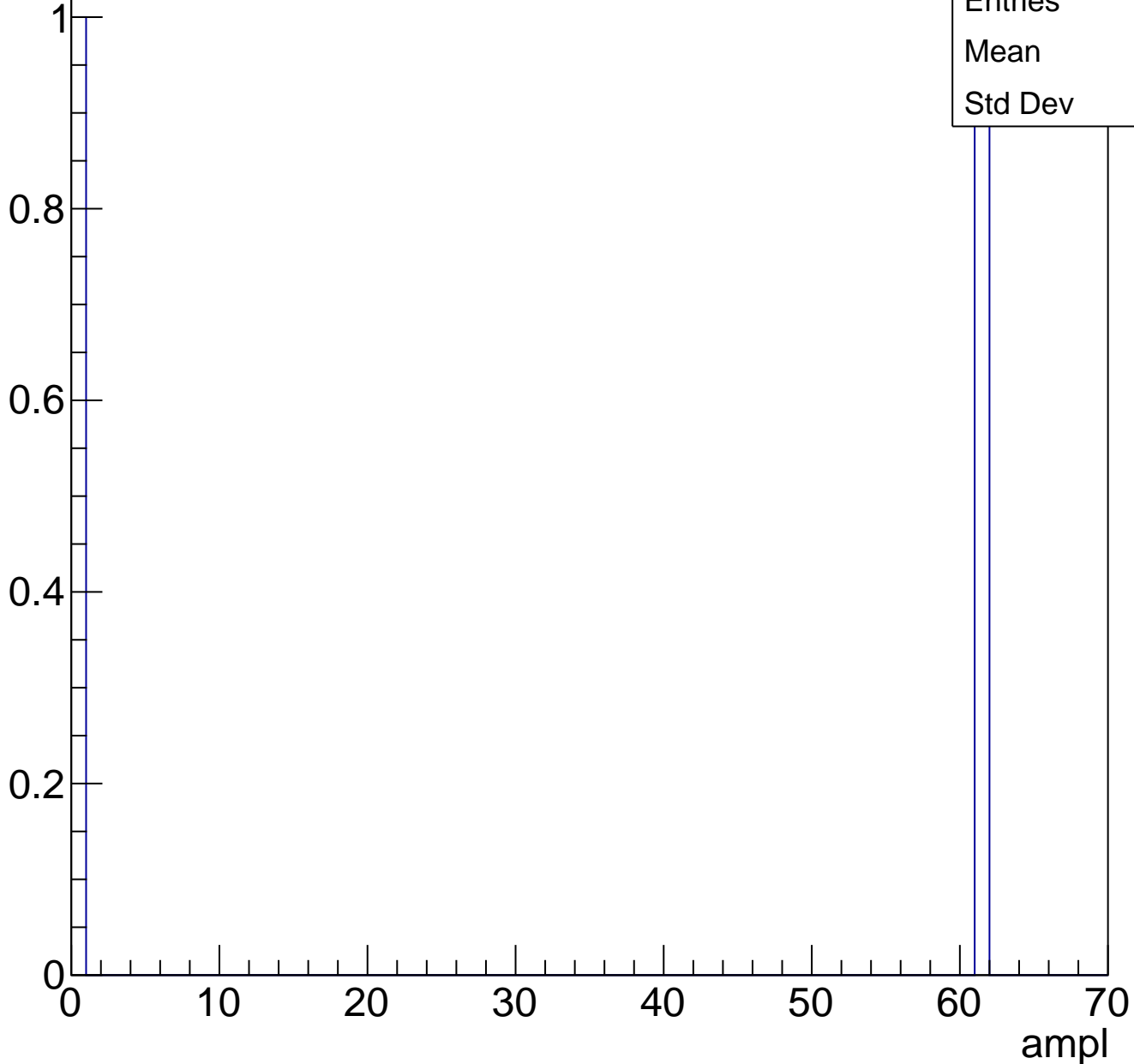


Entries	51
Mean	59.86
Std Dev	2.59

B1L102S, U13-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch72, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	28.59
Std Dev	6.228

Gaus mean : 29.6394

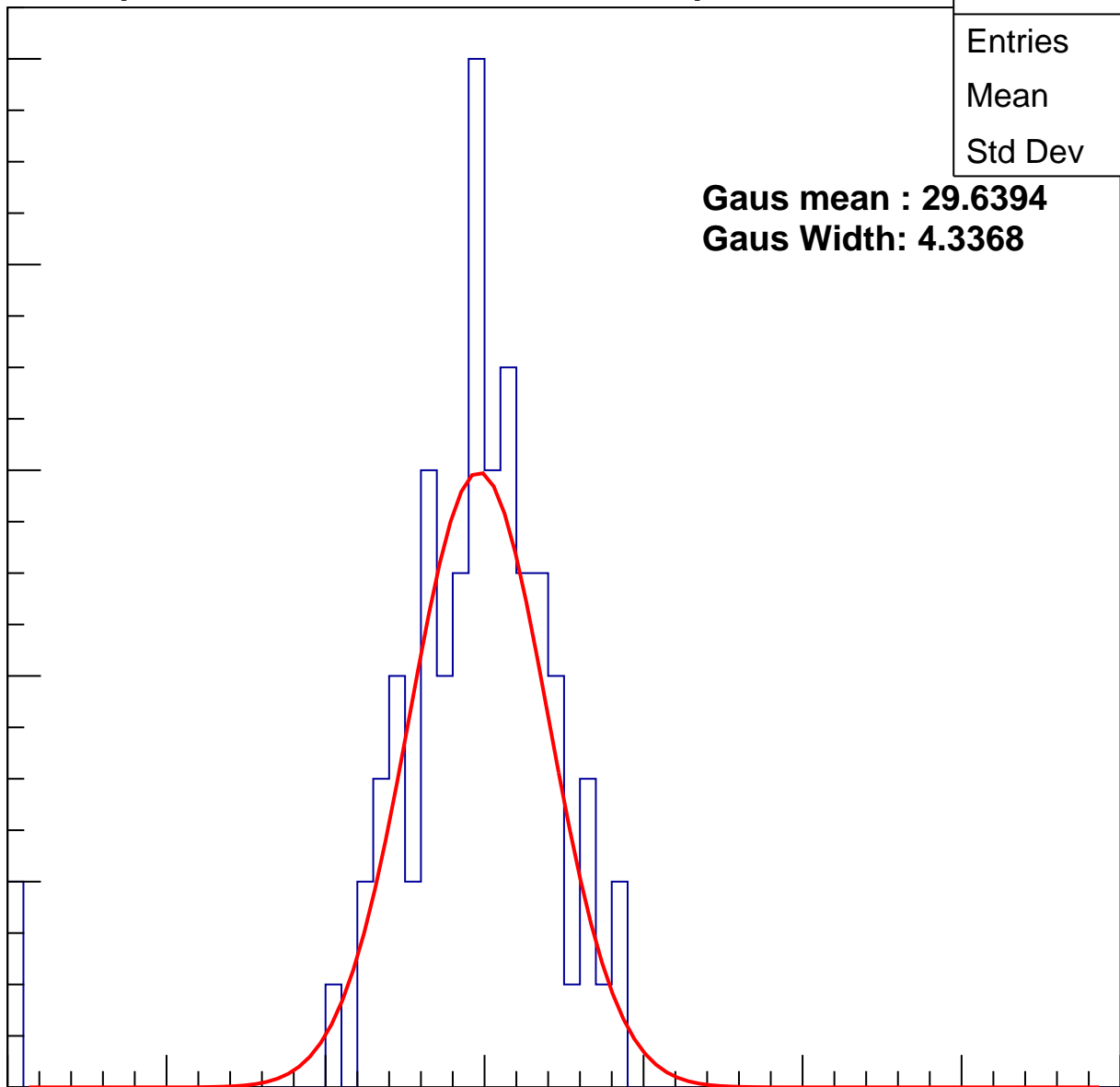
Gaus Width: 4.3368

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch72, adc1

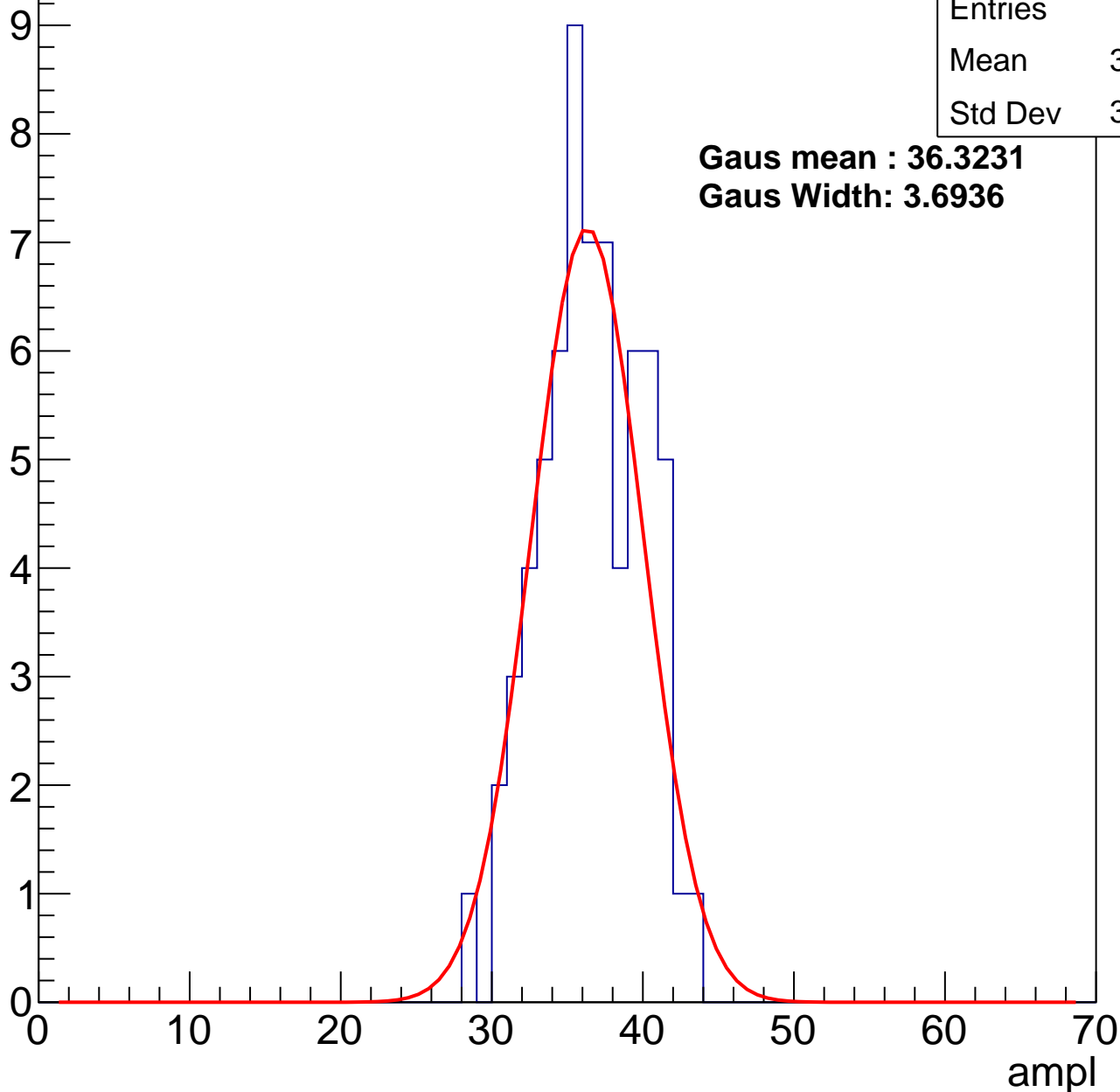
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.12
Std Dev	3.312

Gaus mean : 36.3231

Gaus Width: 3.6936



B1L102S, U13-ch72, adc2

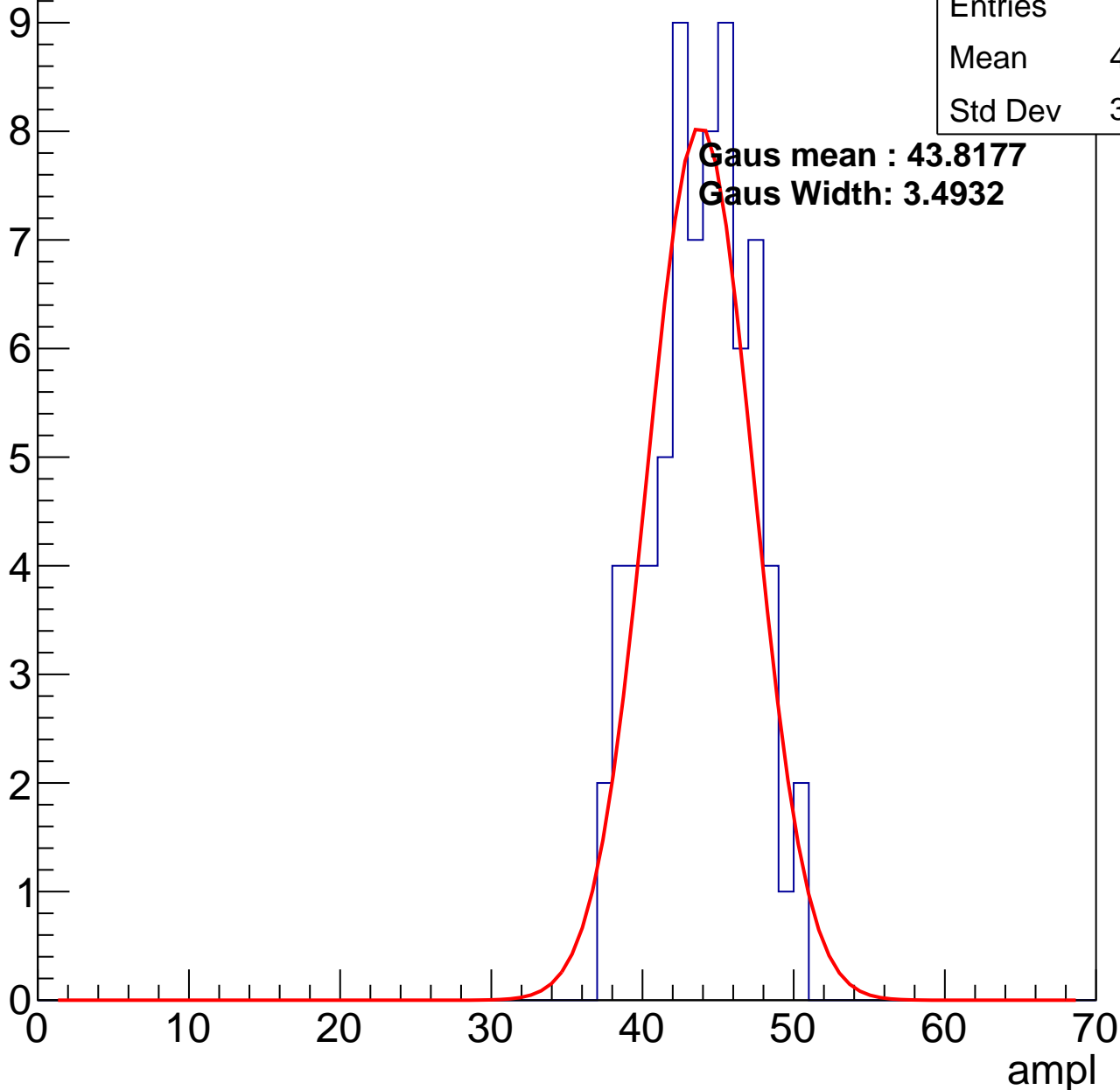
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.46
Std Dev	3.184

Gaus mean : 43.8177

Gaus Width: 3.4932

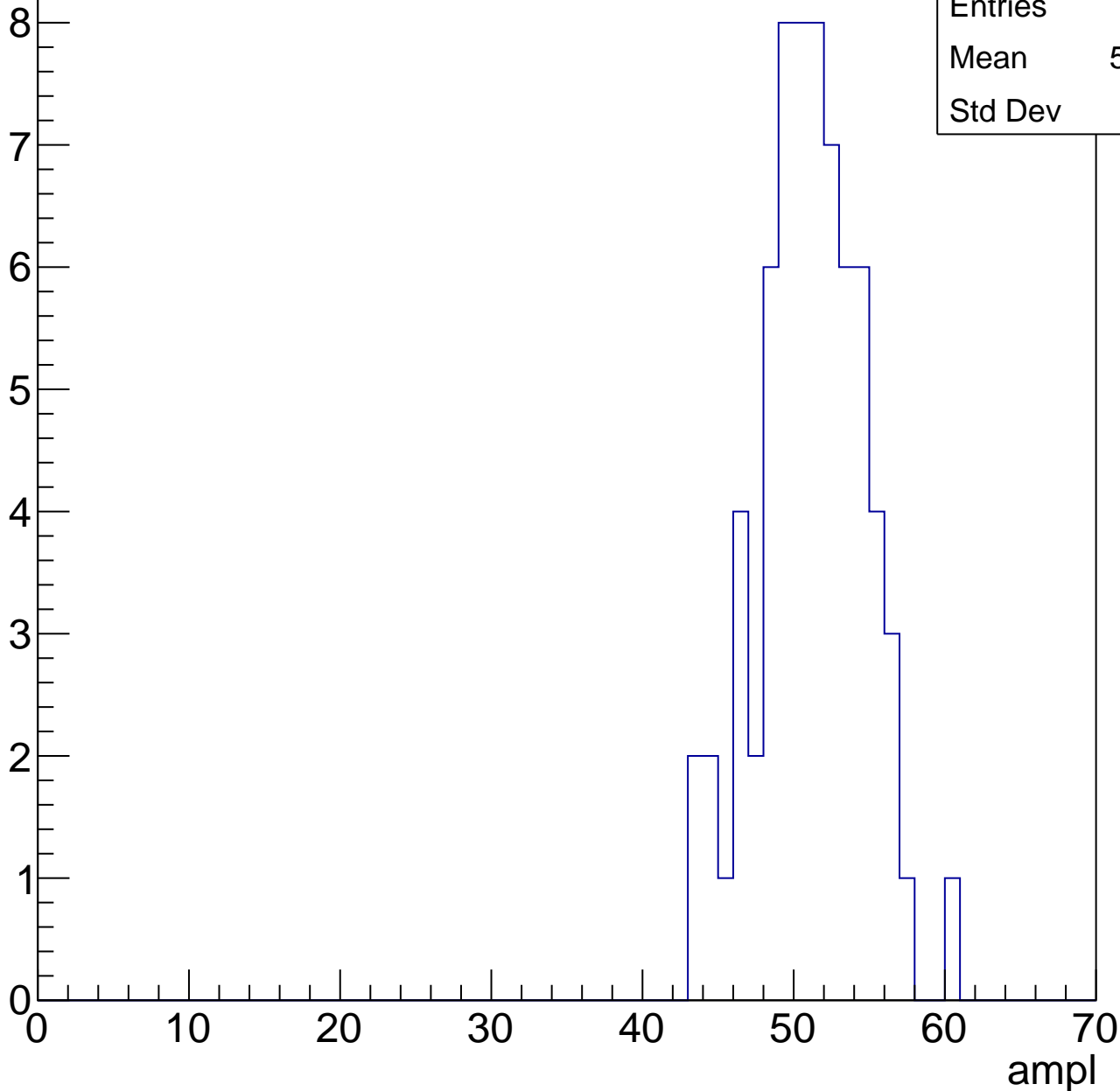


B1L102S, U13-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	50.67
Std Dev	3.45

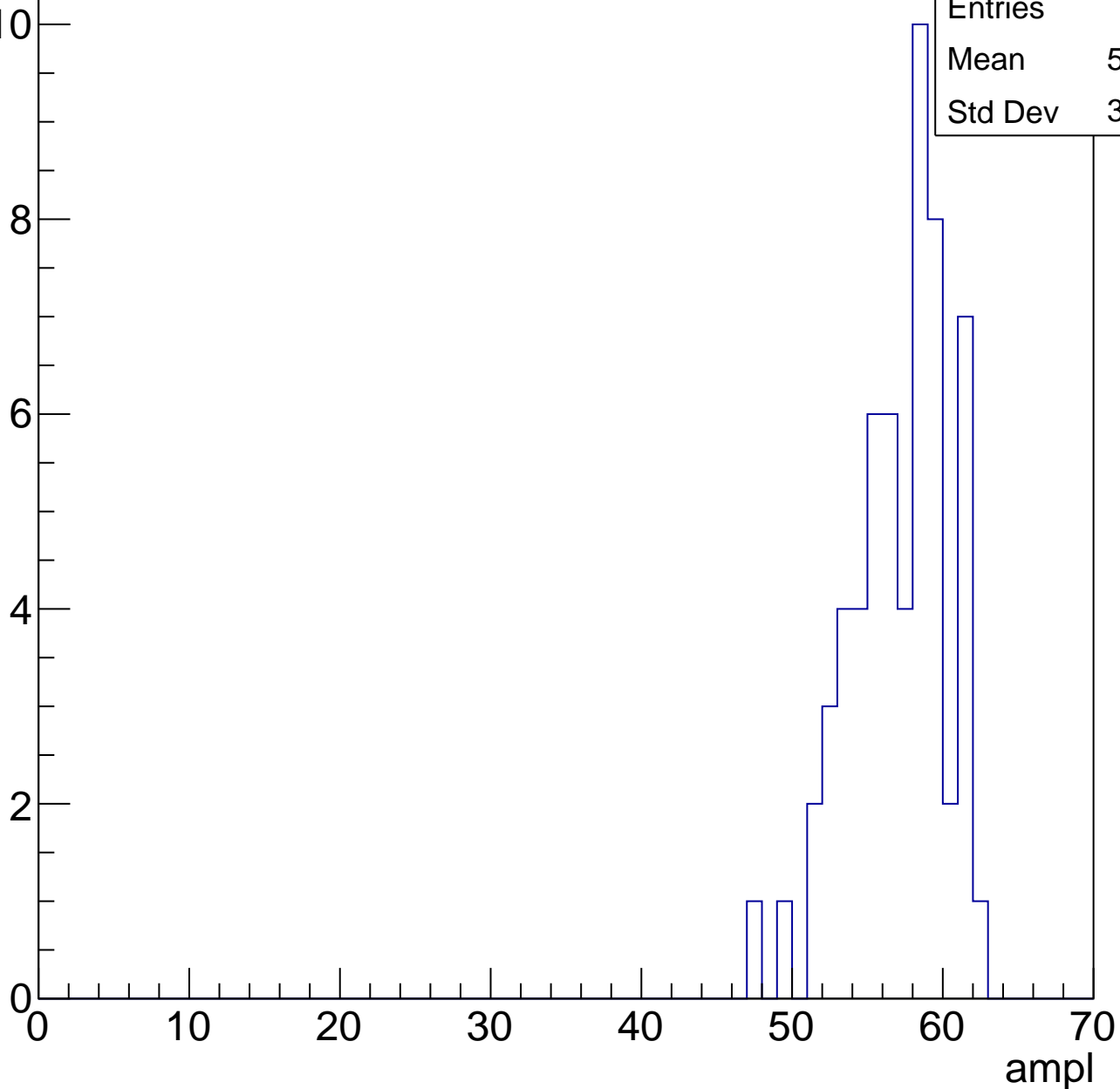


B1L102S, U13-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.56
Std Dev	3.259

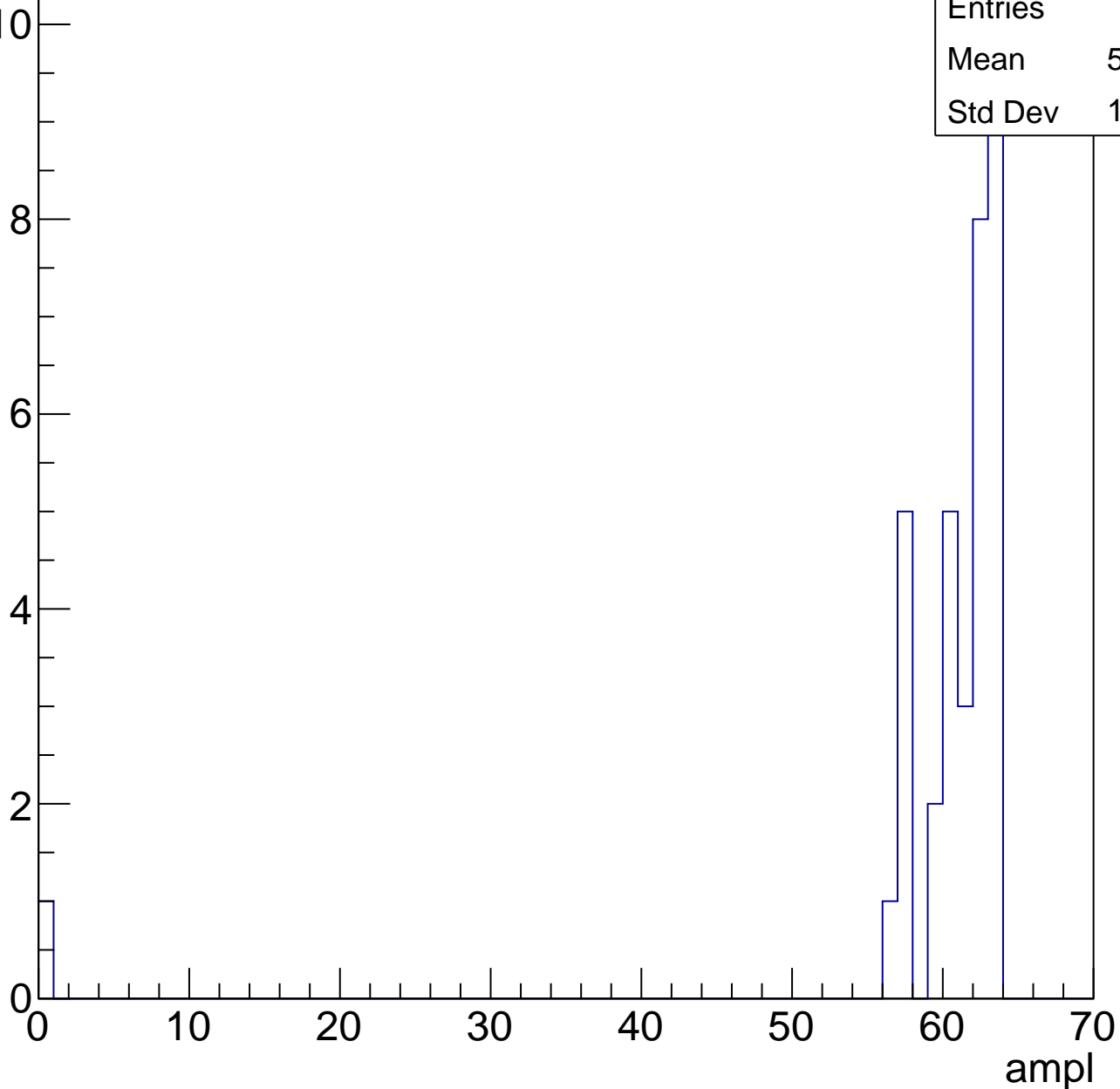


B1L102S, U13-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

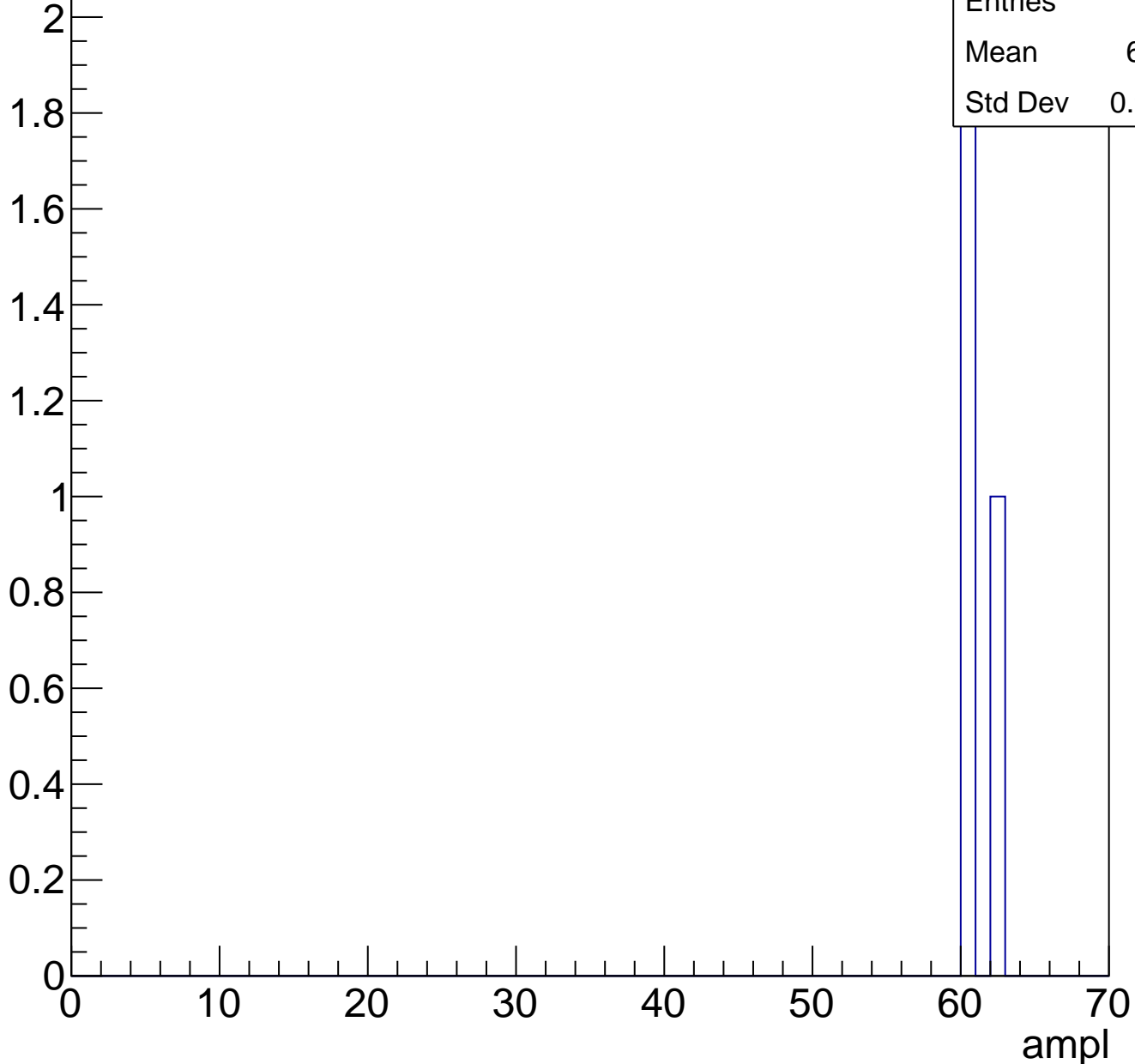
Entries	35
Mean	59.09
Std Dev	10.36



B1L102S, U13-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

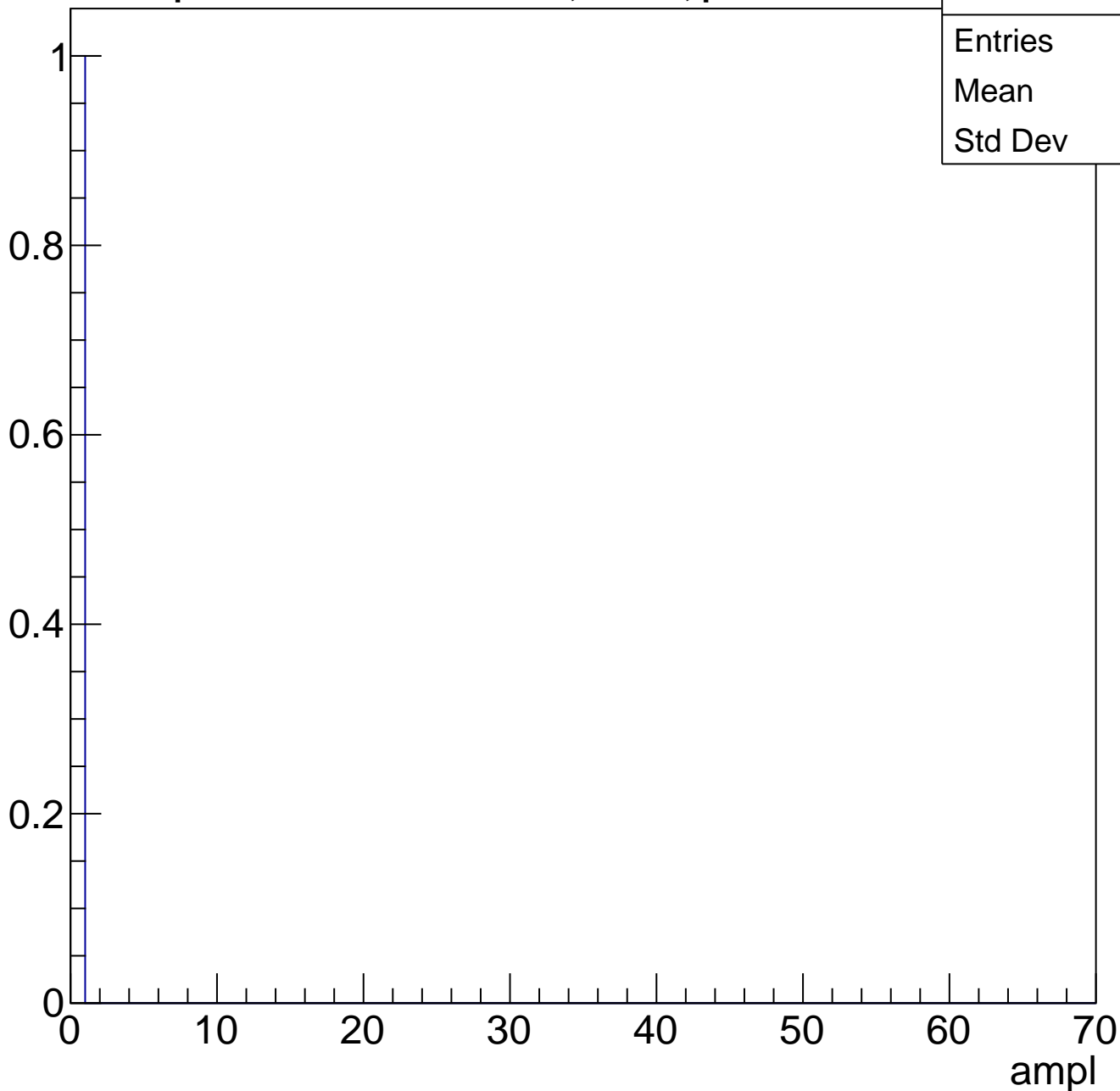


Entries	3
Mean	60.67
Std Dev	0.9428

B1L102S, U13-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch73, adc0

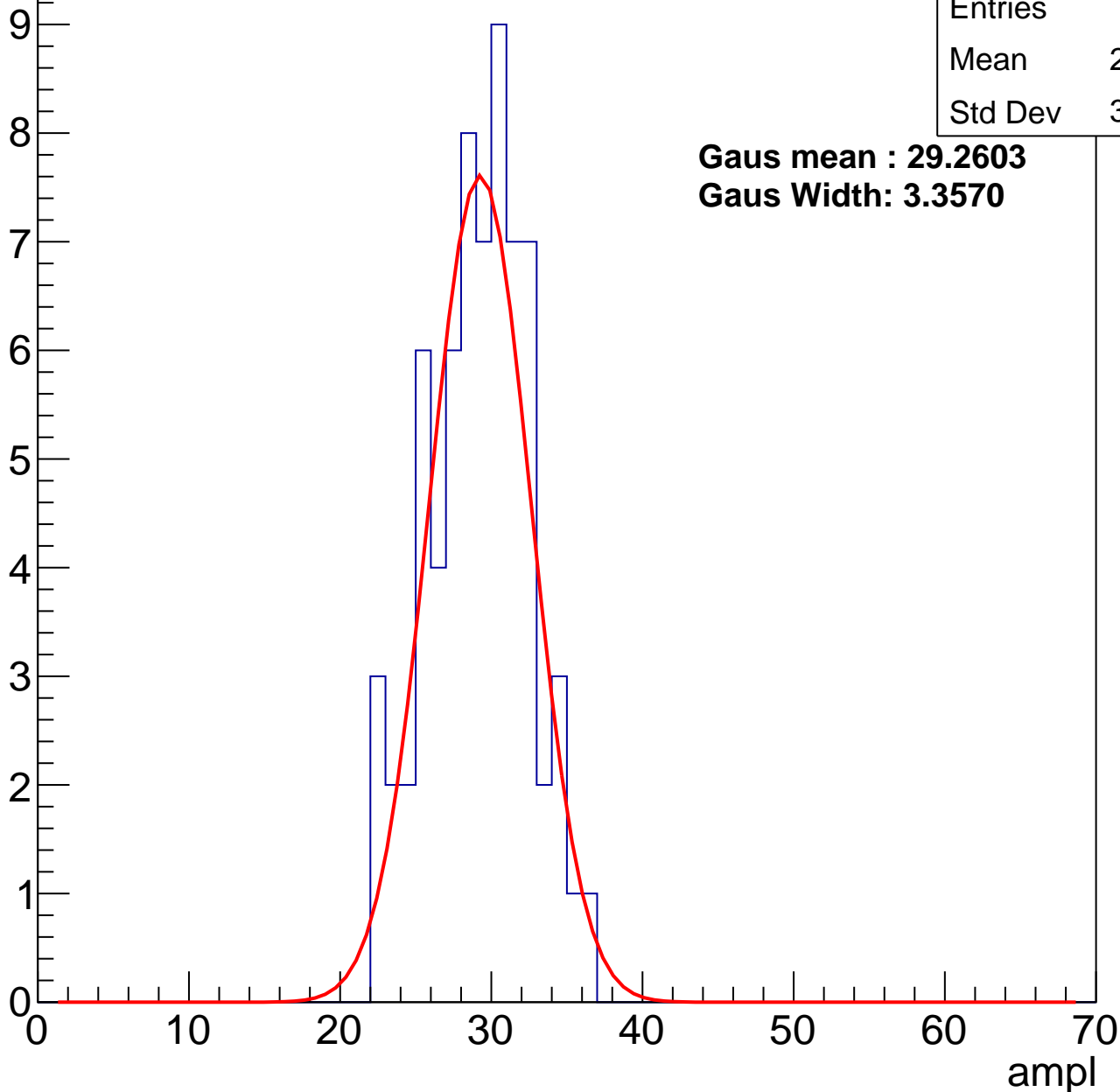
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.72
Std Dev	3.253

Gaus mean : 29.2603

Gaus Width: 3.3570



B1L102S, U13-ch73, adc1

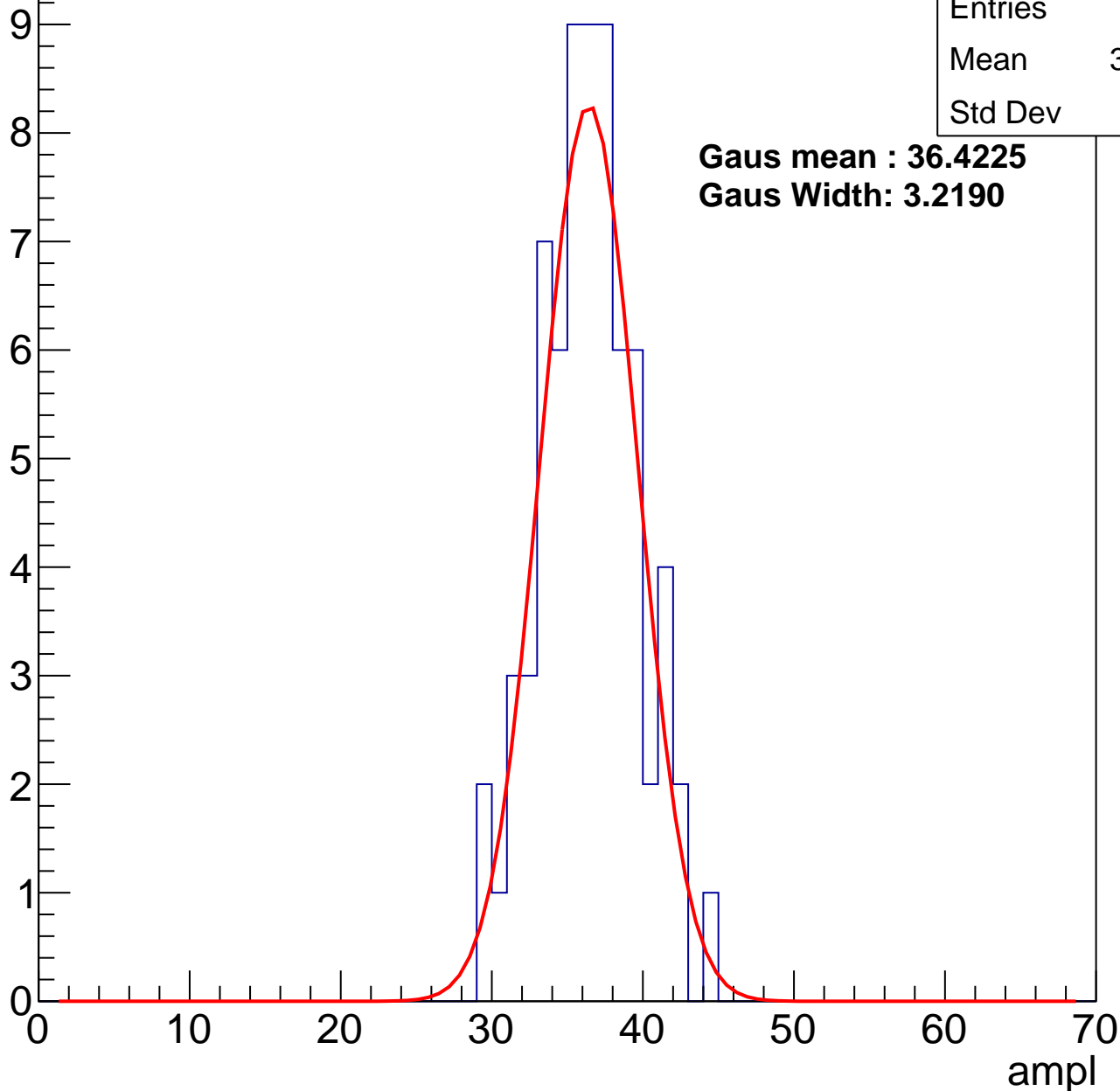
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.97
Std Dev	3.18

Gaus mean : 36.4225

Gaus Width: 3.2190



B1L102S, U13-ch73, adc2

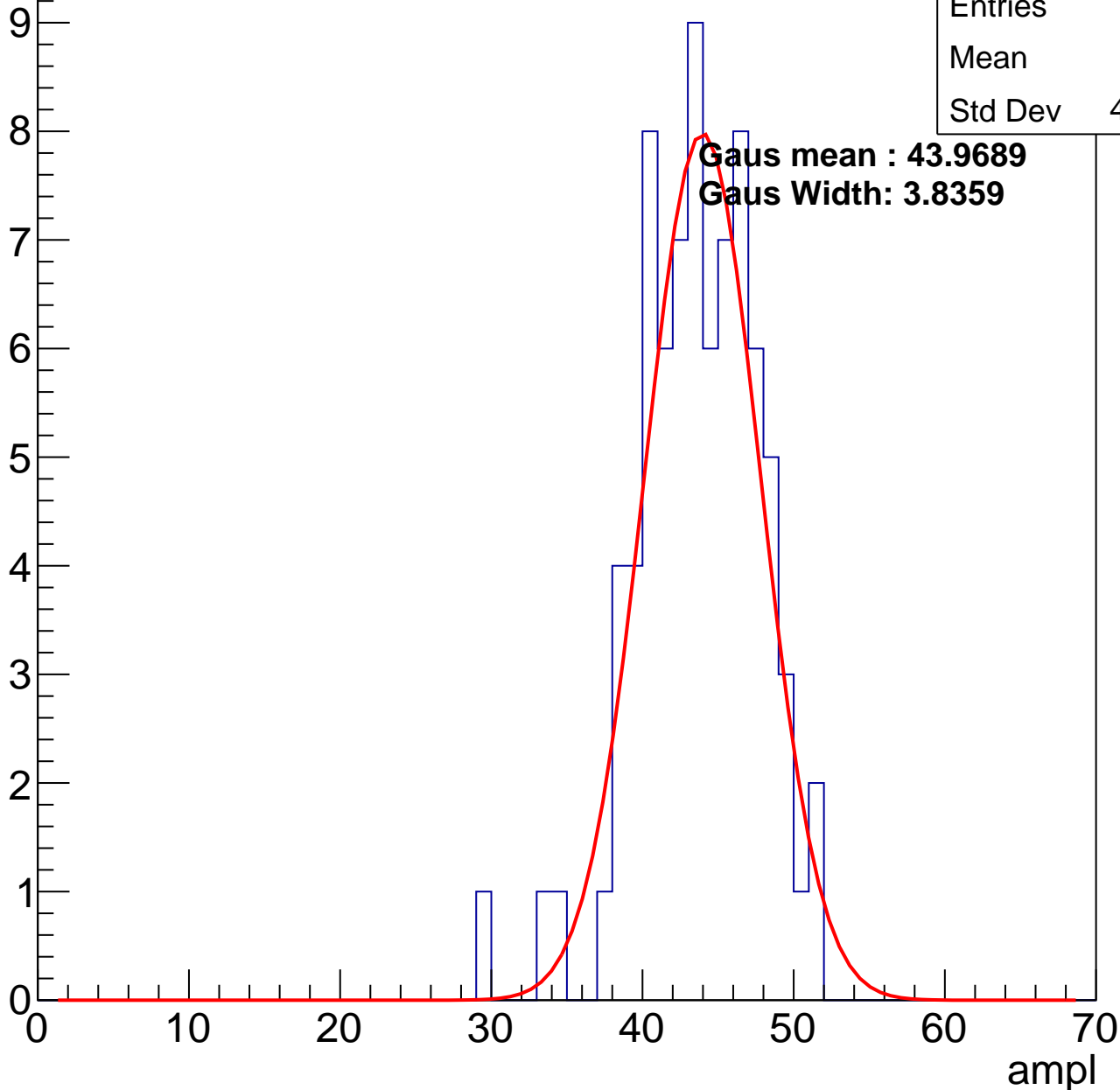
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	43.2
Std Dev	4.014

Gaus mean : 43.9689

Gaus Width: 3.8359

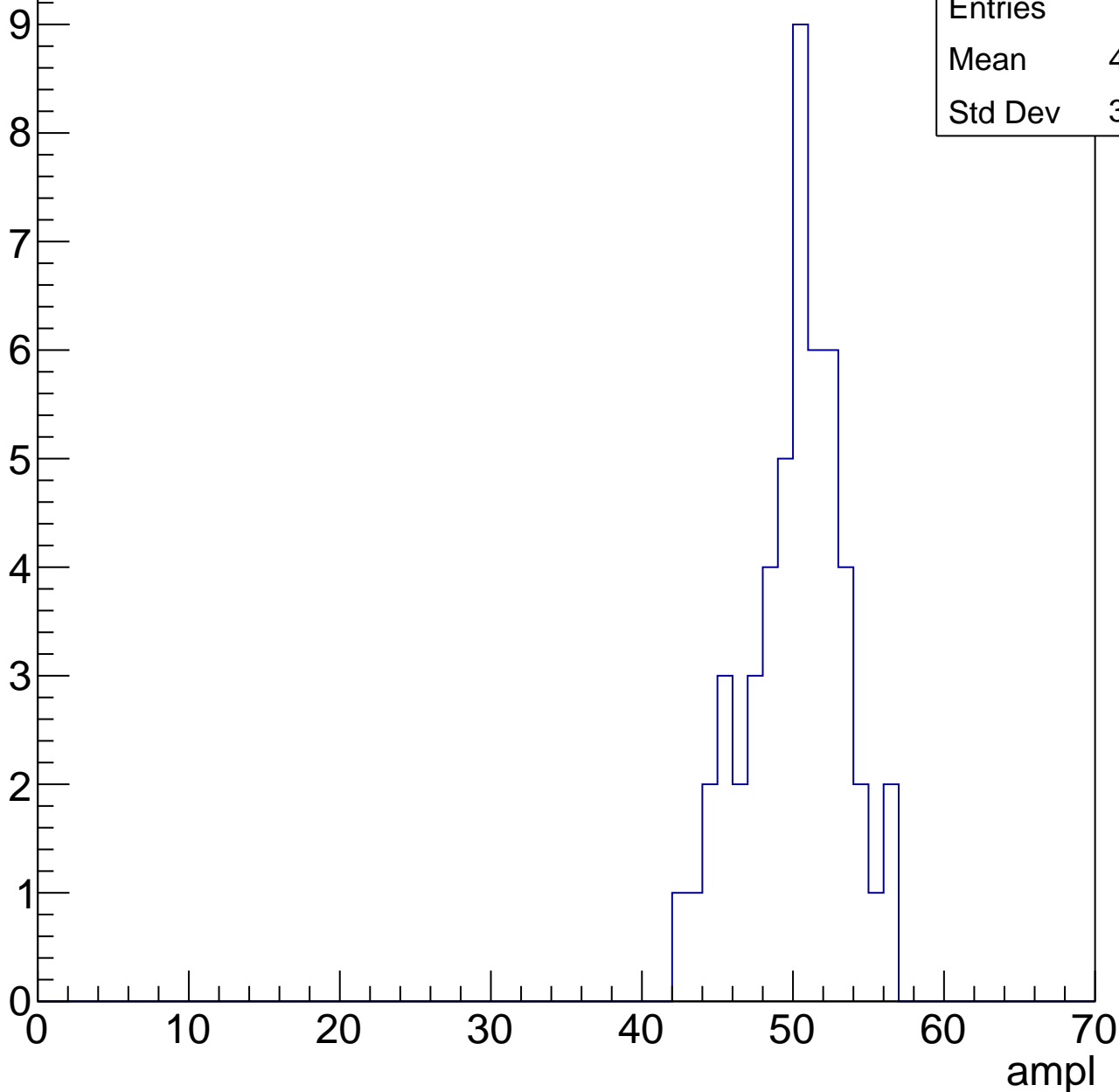


B1L102S, U13-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.67
Std Dev	3.222



B1L102S, U13-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

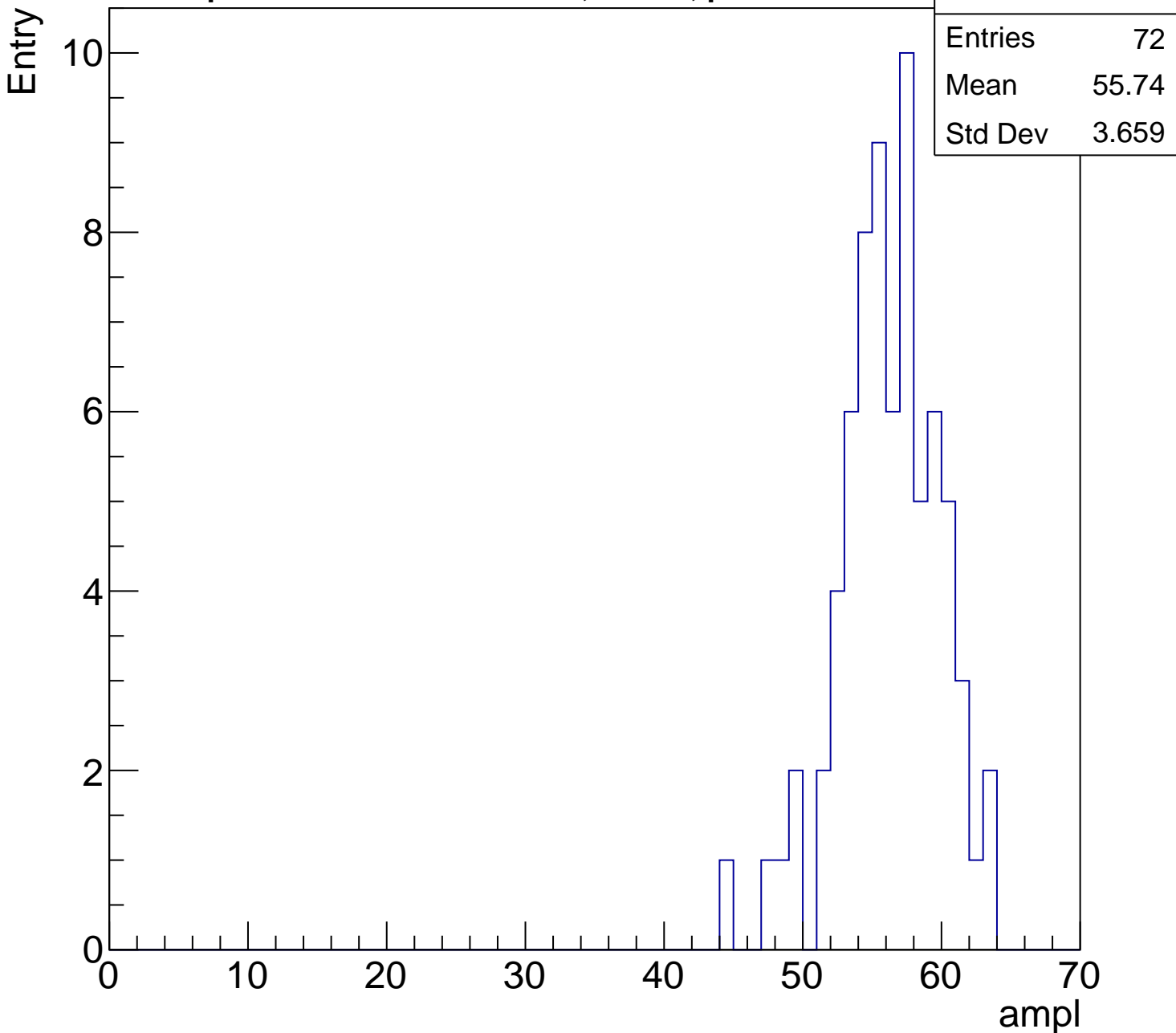
Entries	72
Mean	55.74
Std Dev	3.659

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

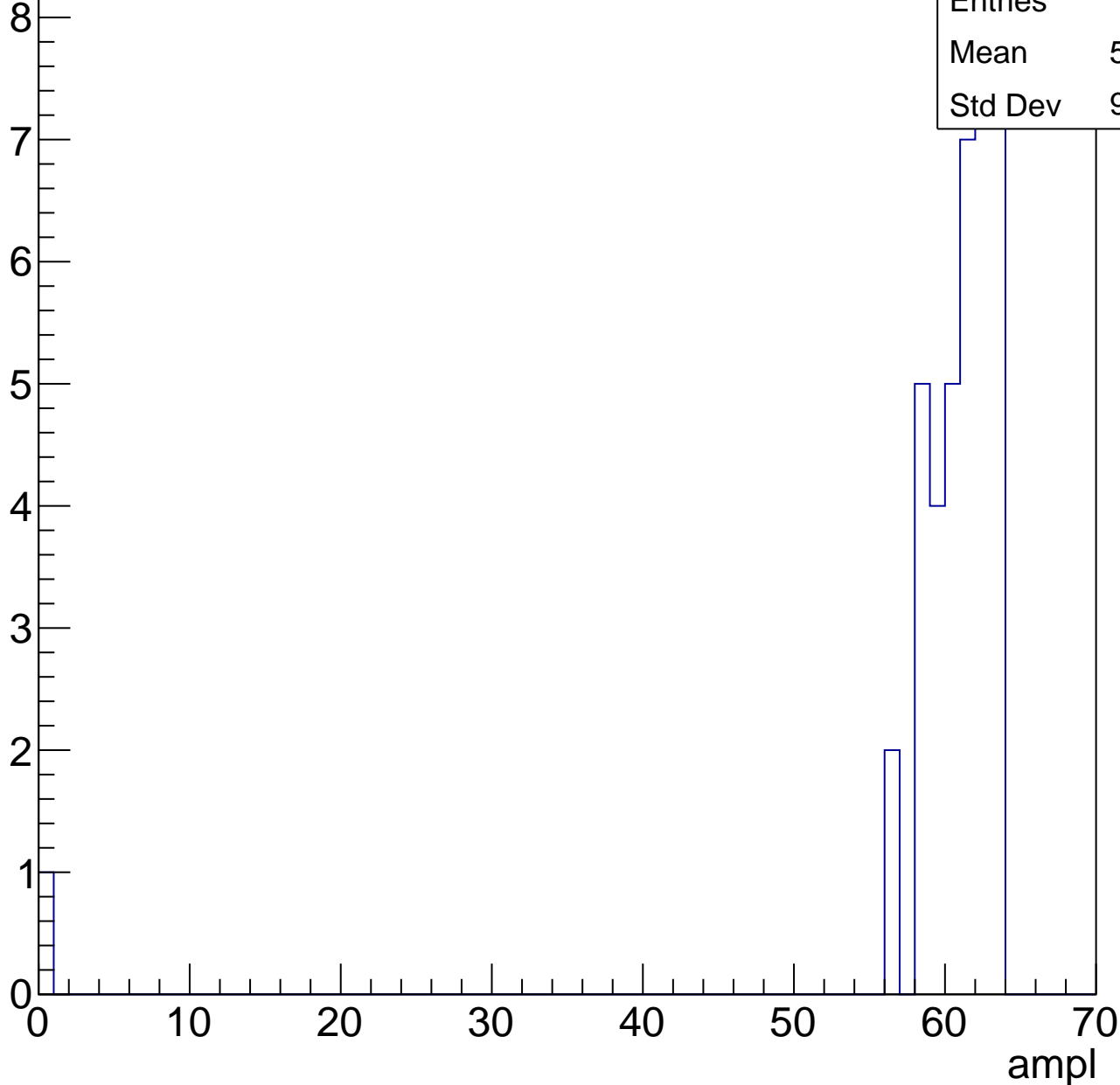


B1L102S, U13-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	59.12
Std Dev	9.665



B1L102S, U13-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch74, adc0

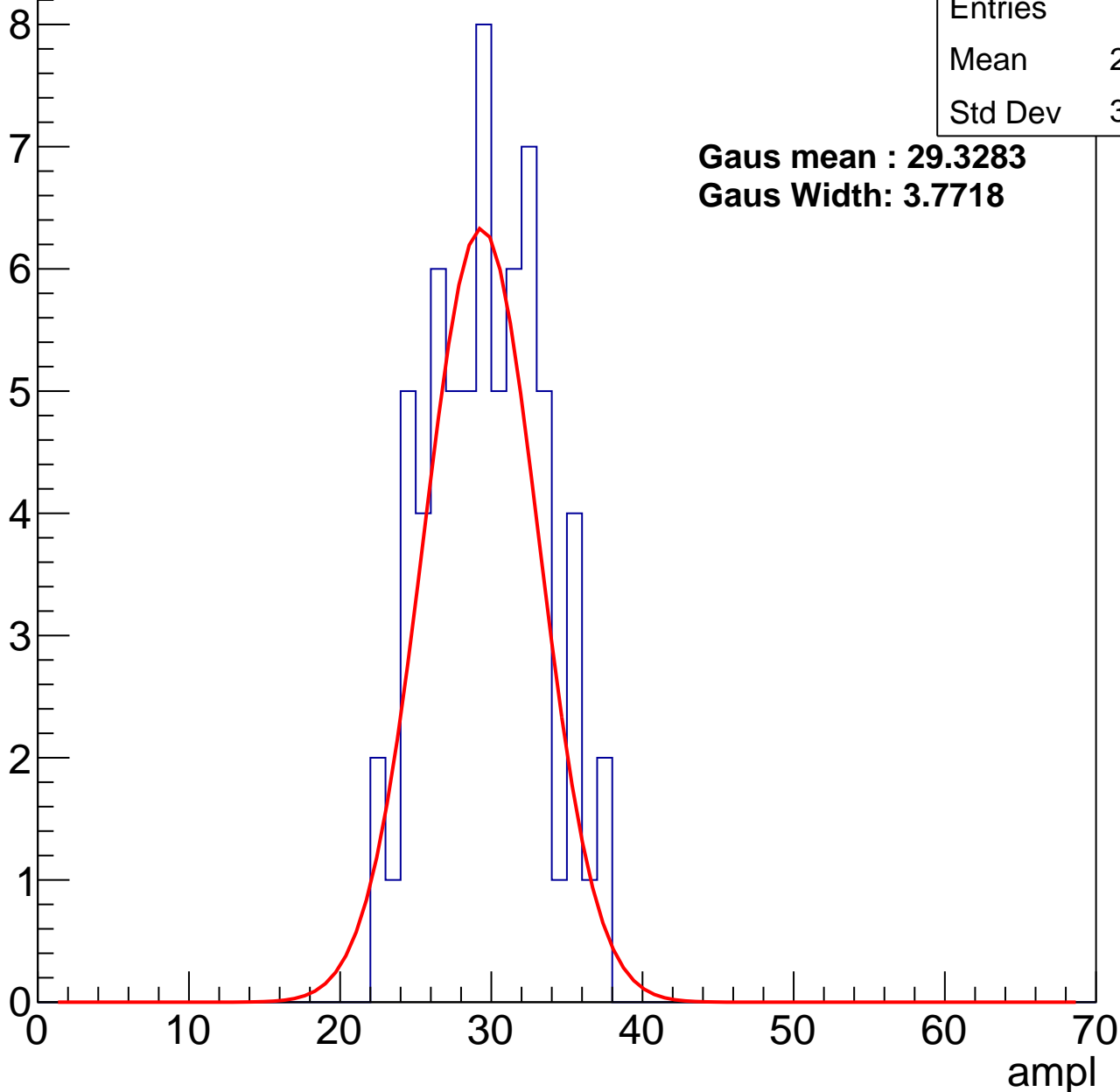
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.24
Std Dev	3.698

Gaus mean : 29.3283

Gaus Width: 3.7718



B1L102S, U13-ch74, adc1

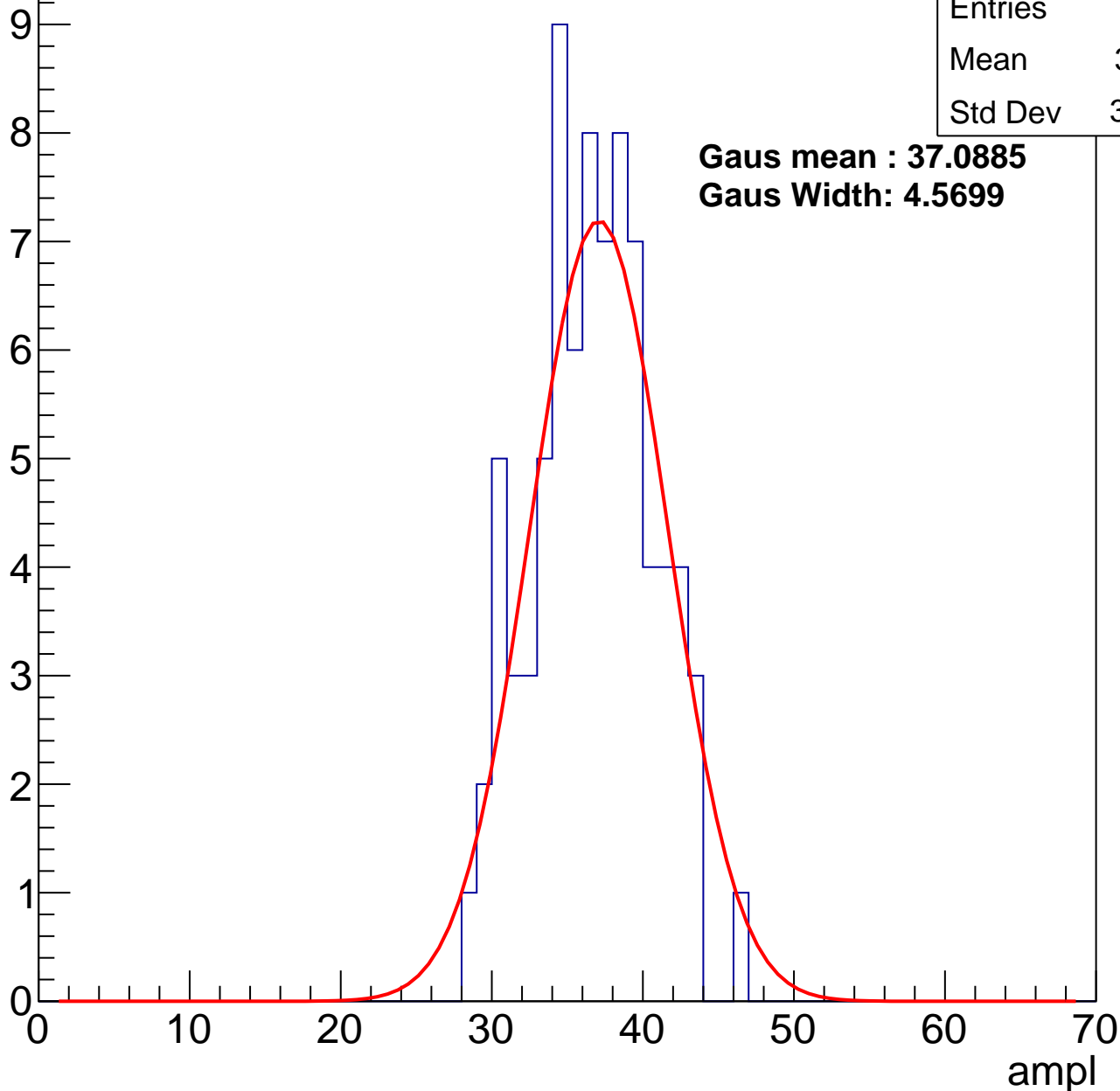
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.21
Std Dev	3.882

Gaus mean : 37.0885

Gaus Width: 4.5699



B1L102S, U13-ch74, adc2

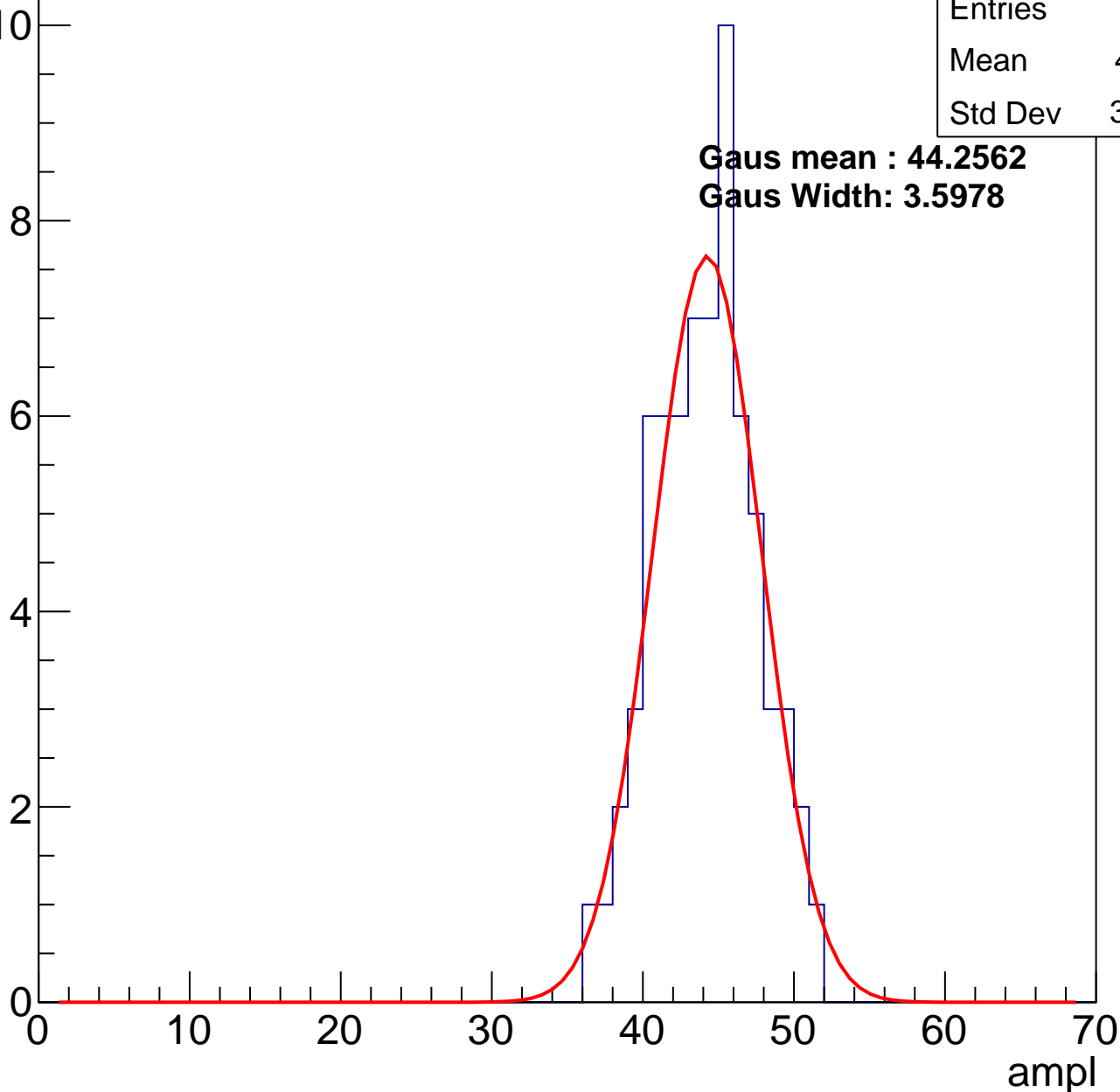
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.71
Std Dev	3.315

Gaus mean : 44.2562

Gaus Width: 3.5978

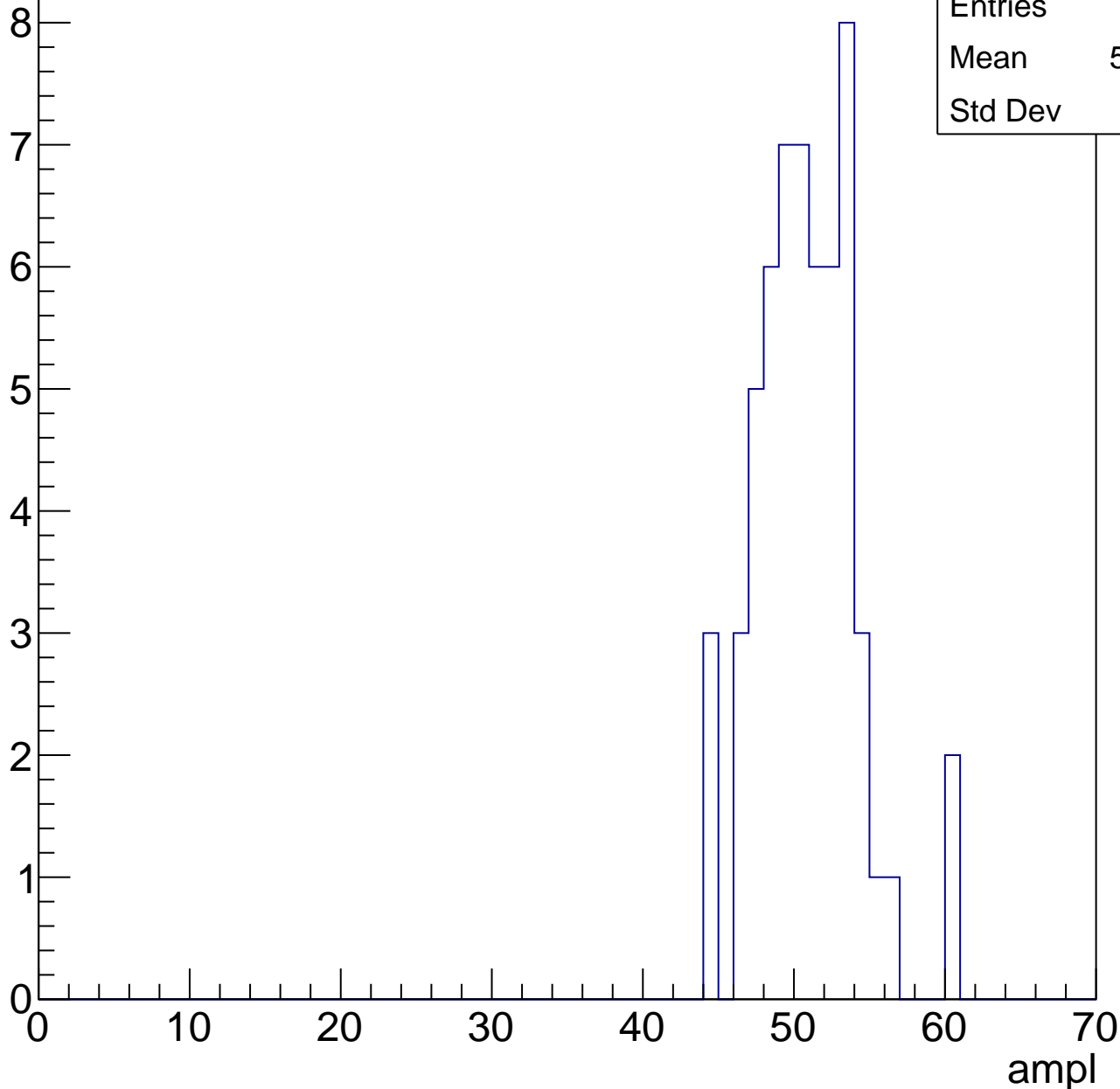


B1L102S, U13-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.36
Std Dev	3.31

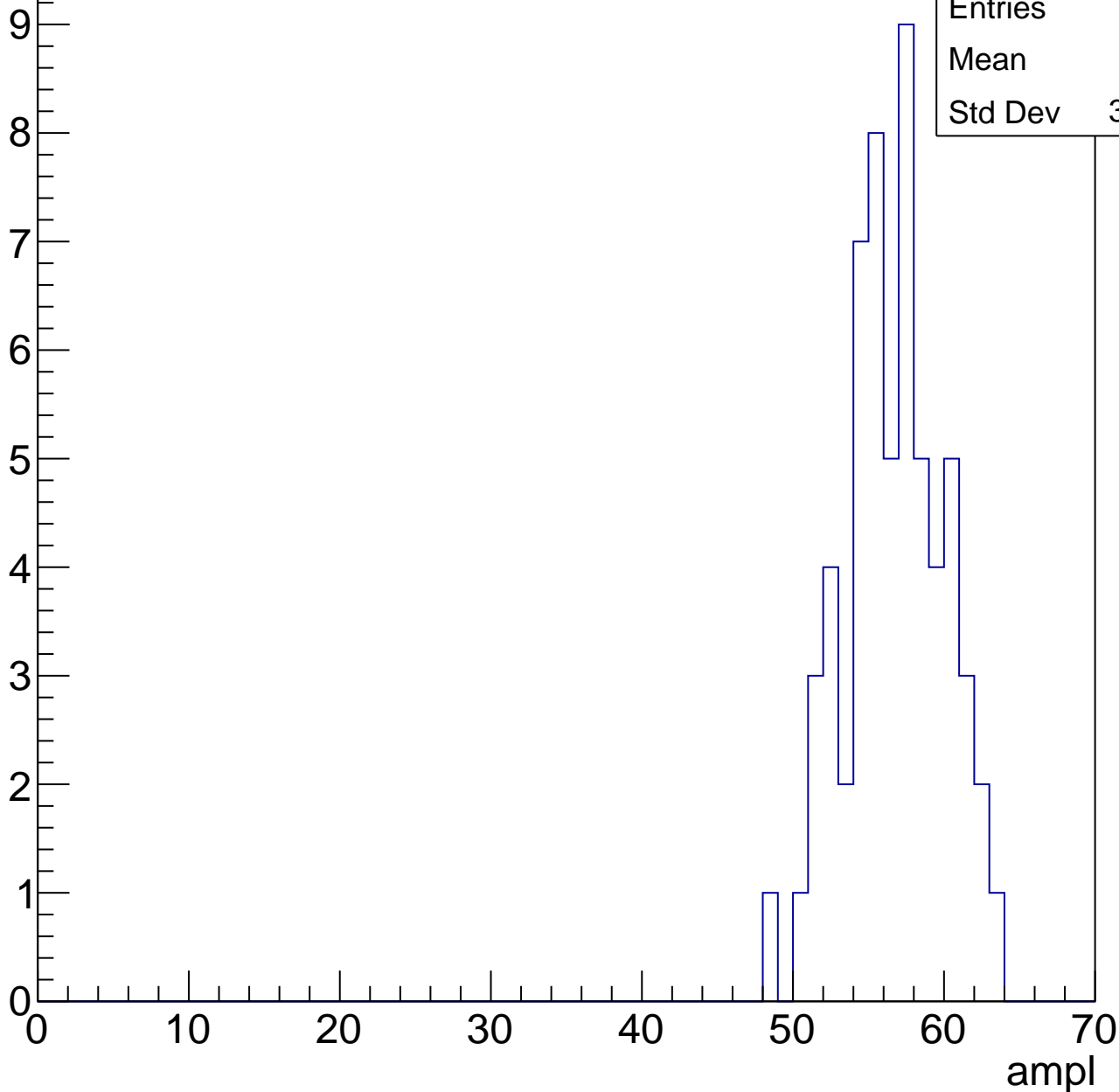


B1L102S, U13-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.2
Std Dev	3.244

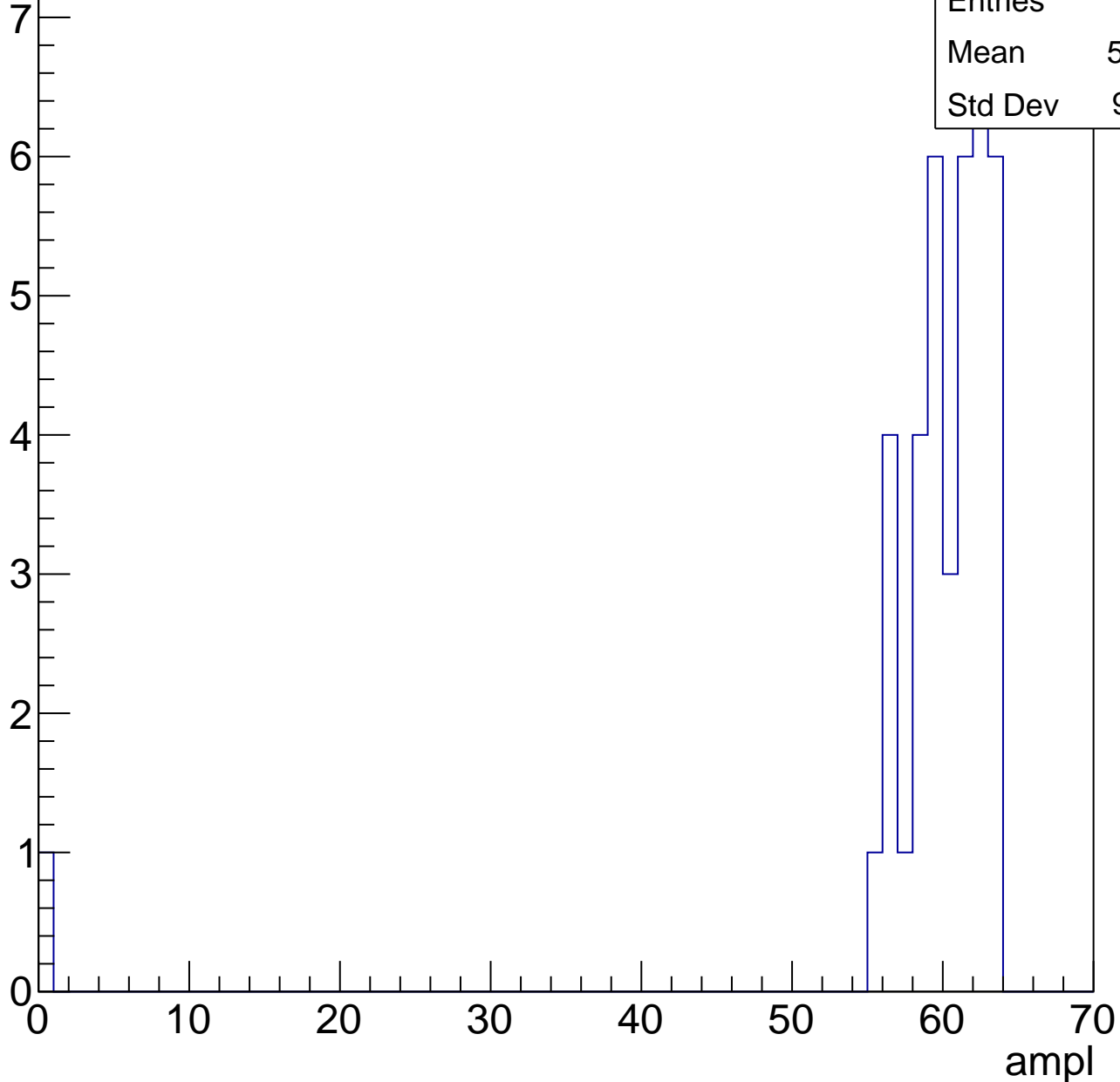


B1L102S, U13-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

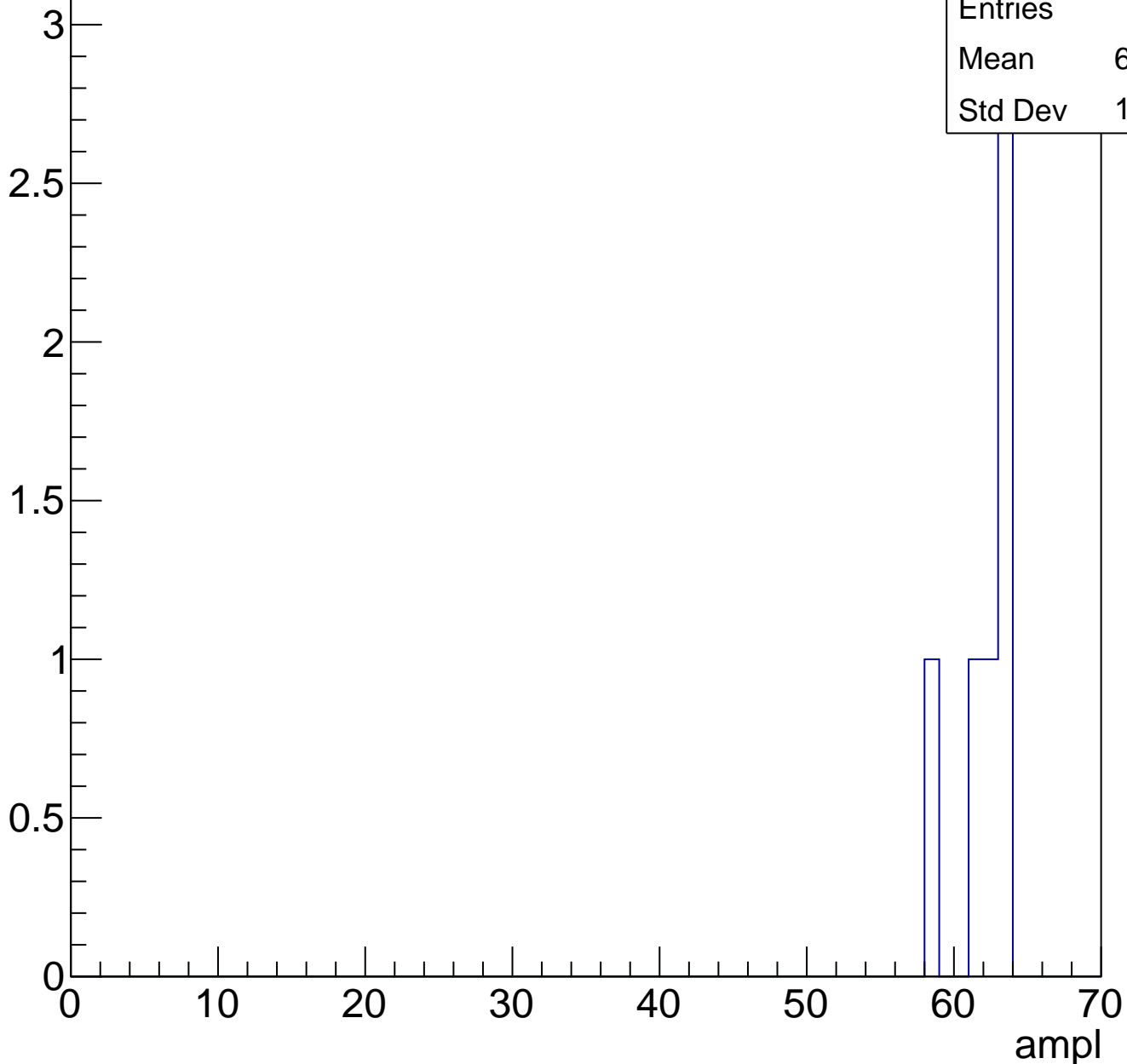
Entries	39
Mean	58.46
Std Dev	9.761



B1L102S, U13-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch75, adc0

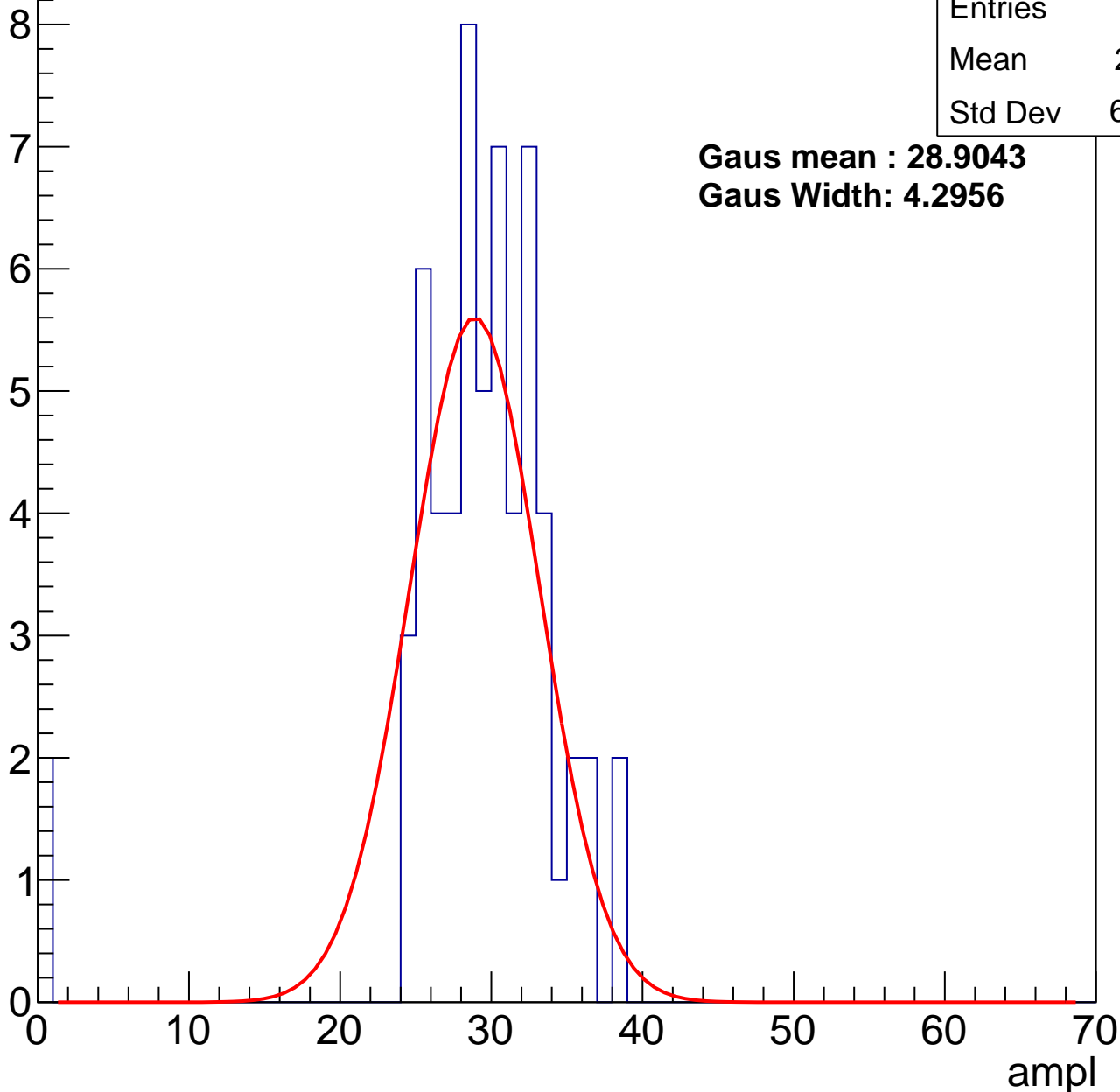
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.61
Std Dev	6.286

Gaus mean : 28.9043

Gaus Width: 4.2956



B1L102S, U13-ch75, adc1

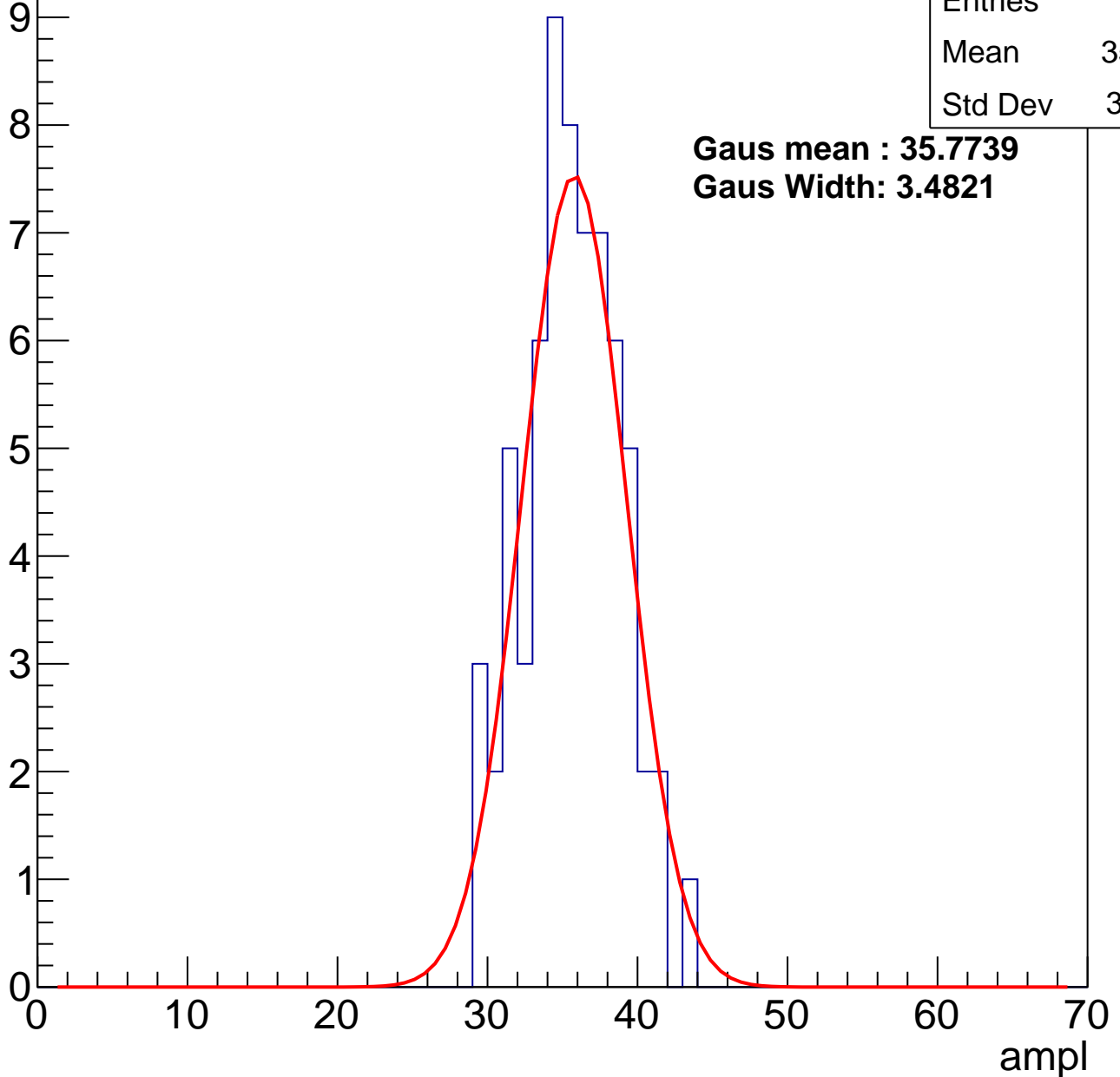
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.17
Std Dev	3.141

Gaus mean : 35.7739

Gaus Width: 3.4821



B1L102S, U13-ch75, adc2

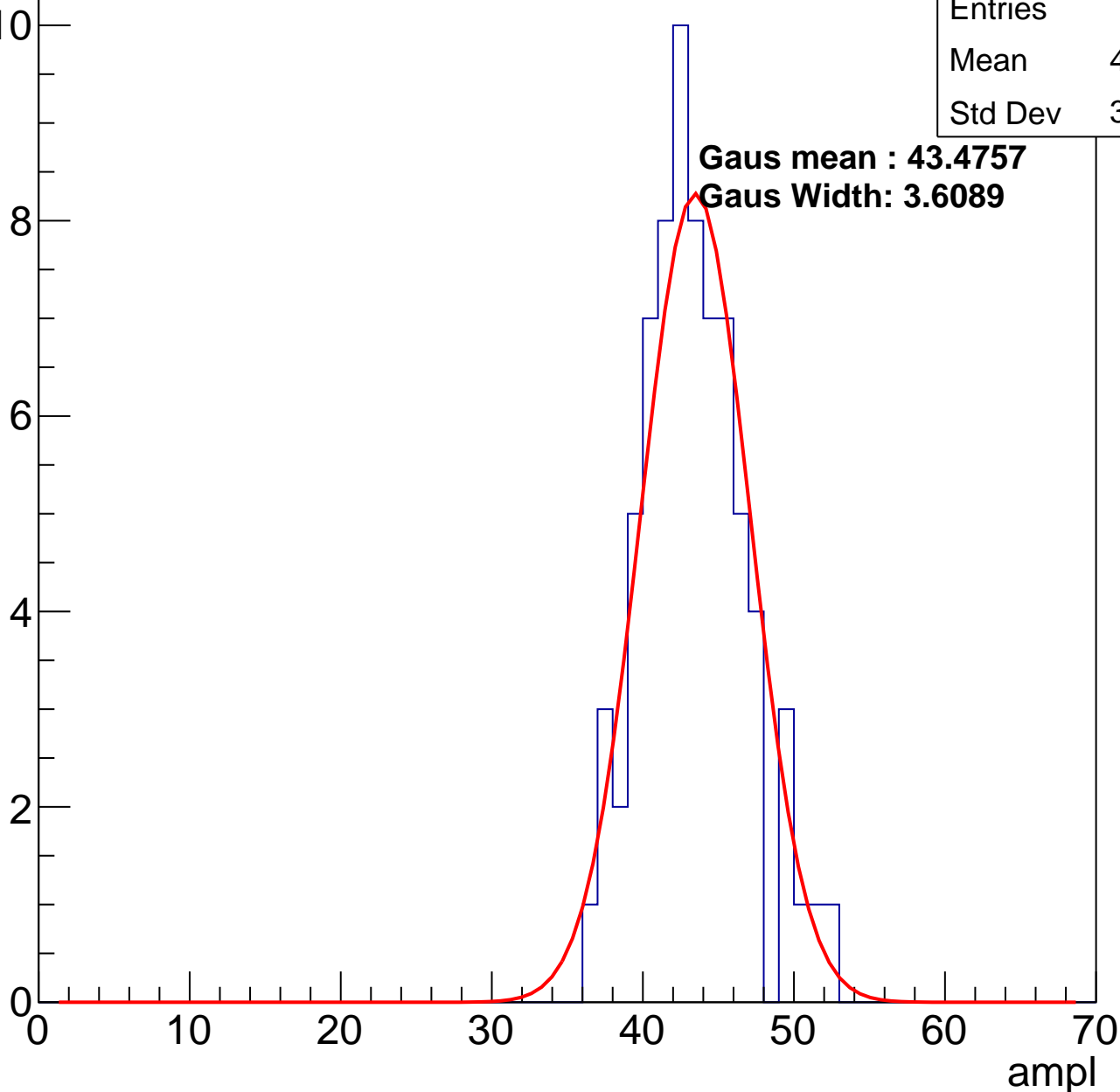
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.89
Std Dev	3.386

Gaus mean : 43.4757

Gaus Width: 3.6089

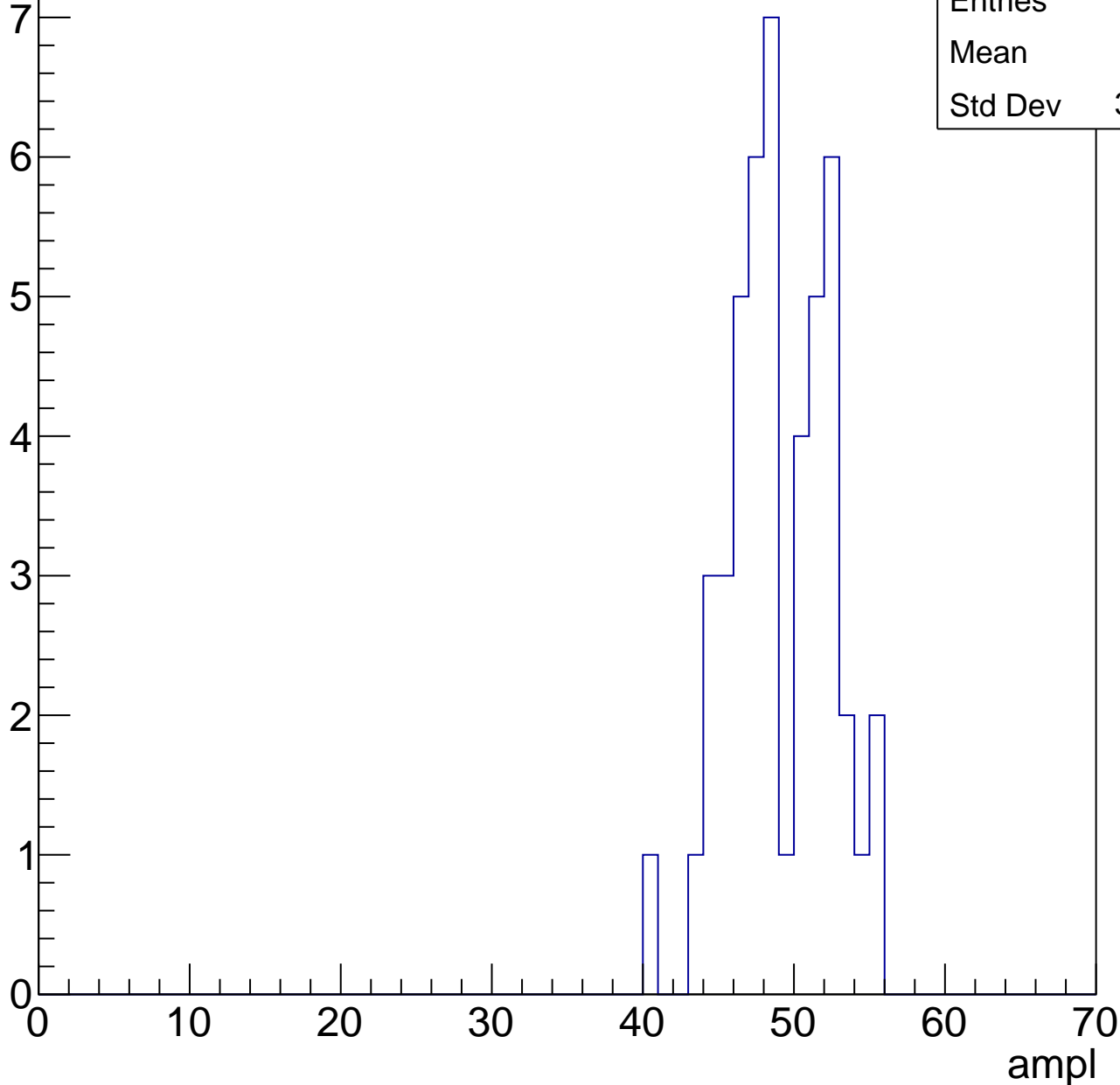


B1L102S, U13-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	48.6
Std Dev	3.311

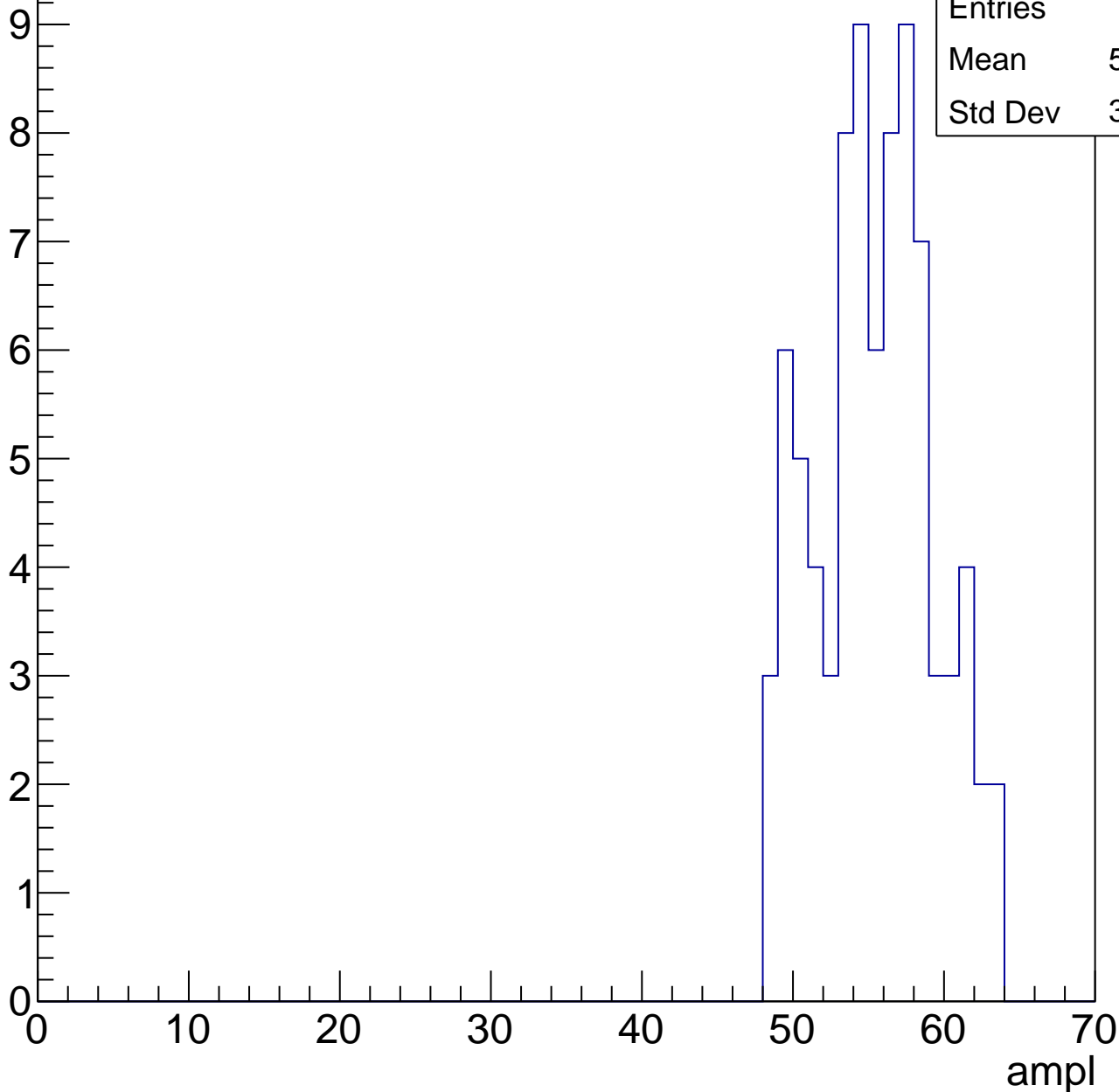


B1L102S, U13-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	54.95
Std Dev	3.857

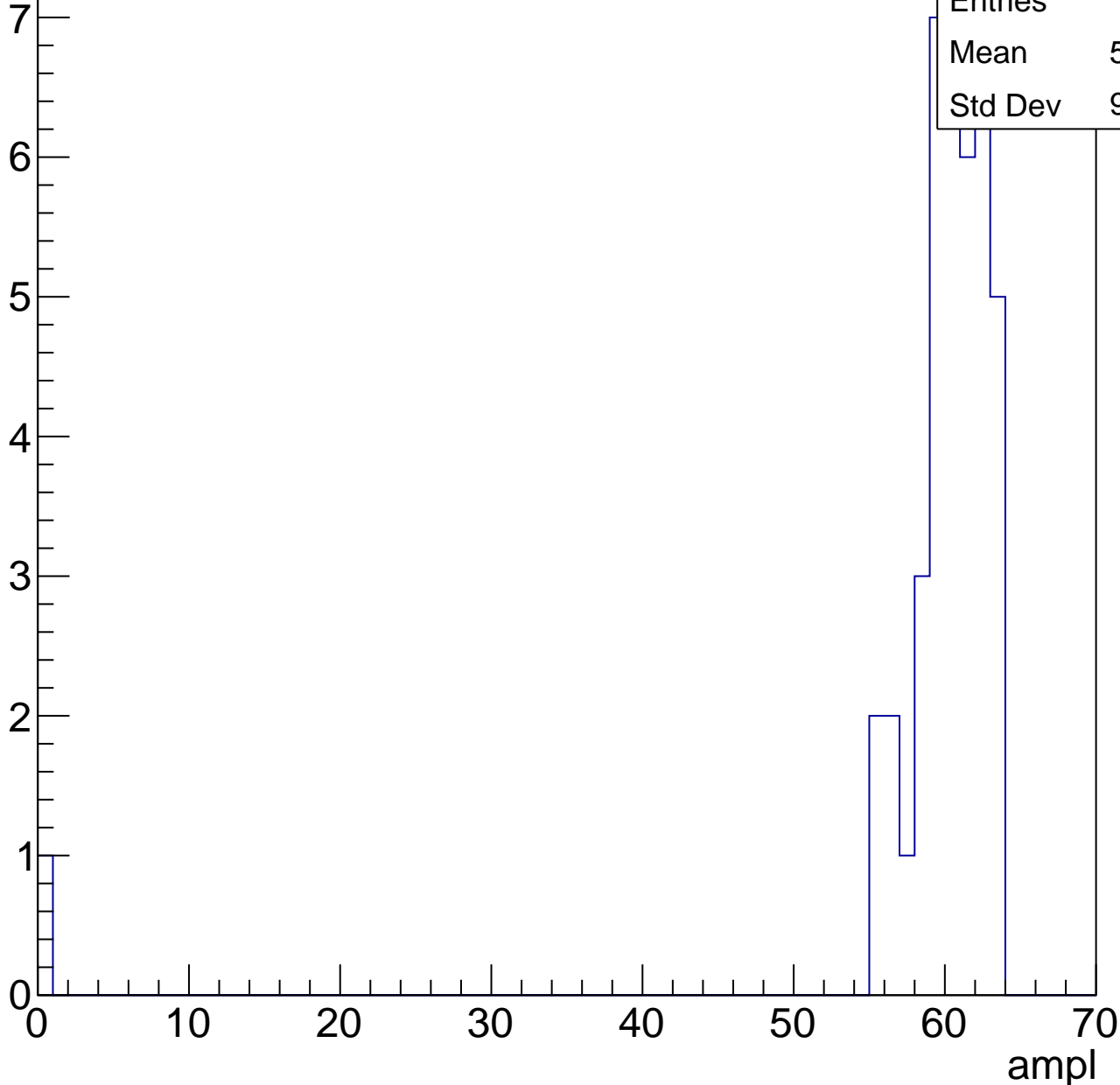


B1L102S, U13-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.56
Std Dev	9.505



B1L102S, U13-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch76, adc0

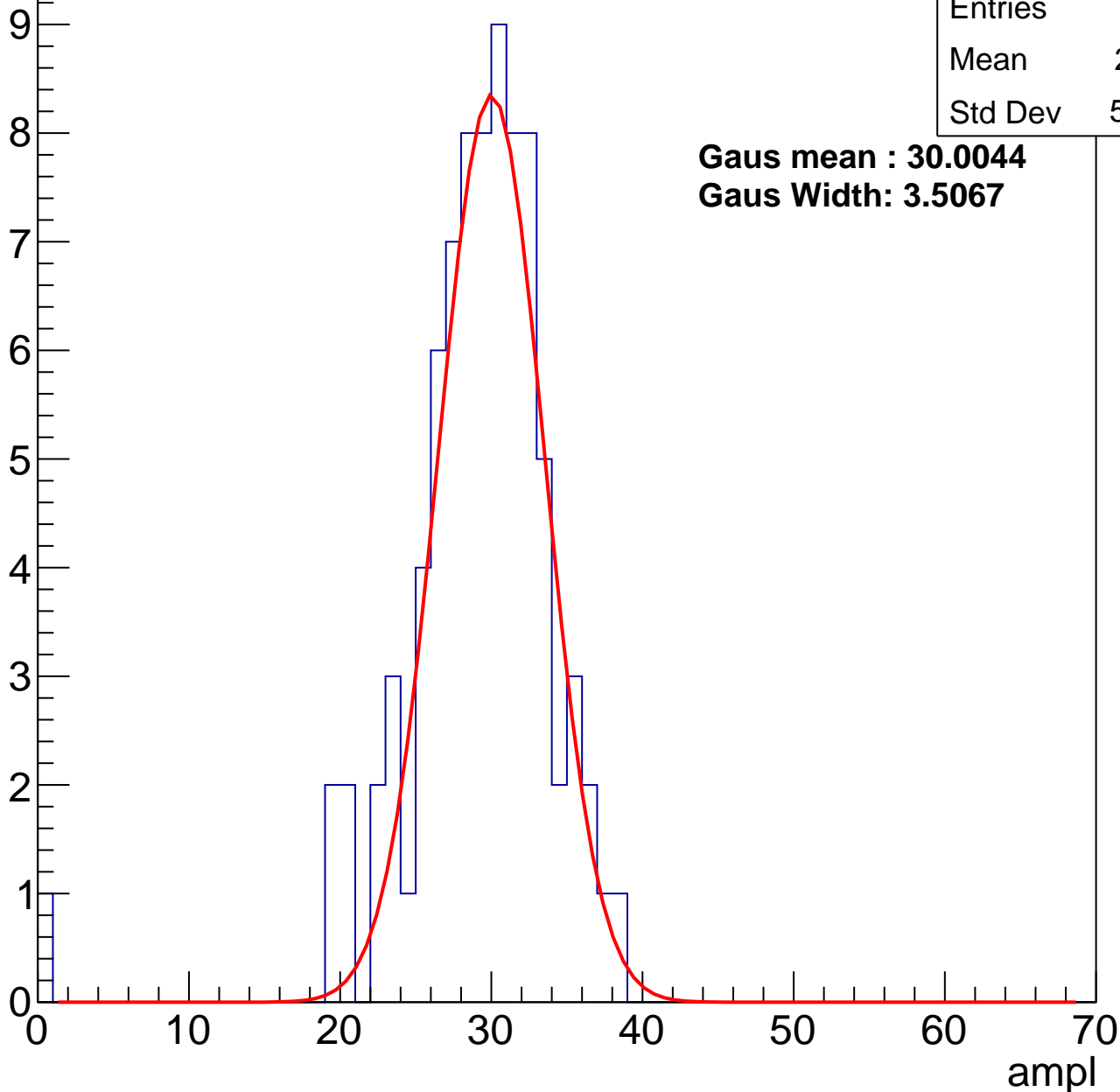
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.61
Std Dev	5.113

Gaus mean : 30.0044

Gaus Width: 3.5067



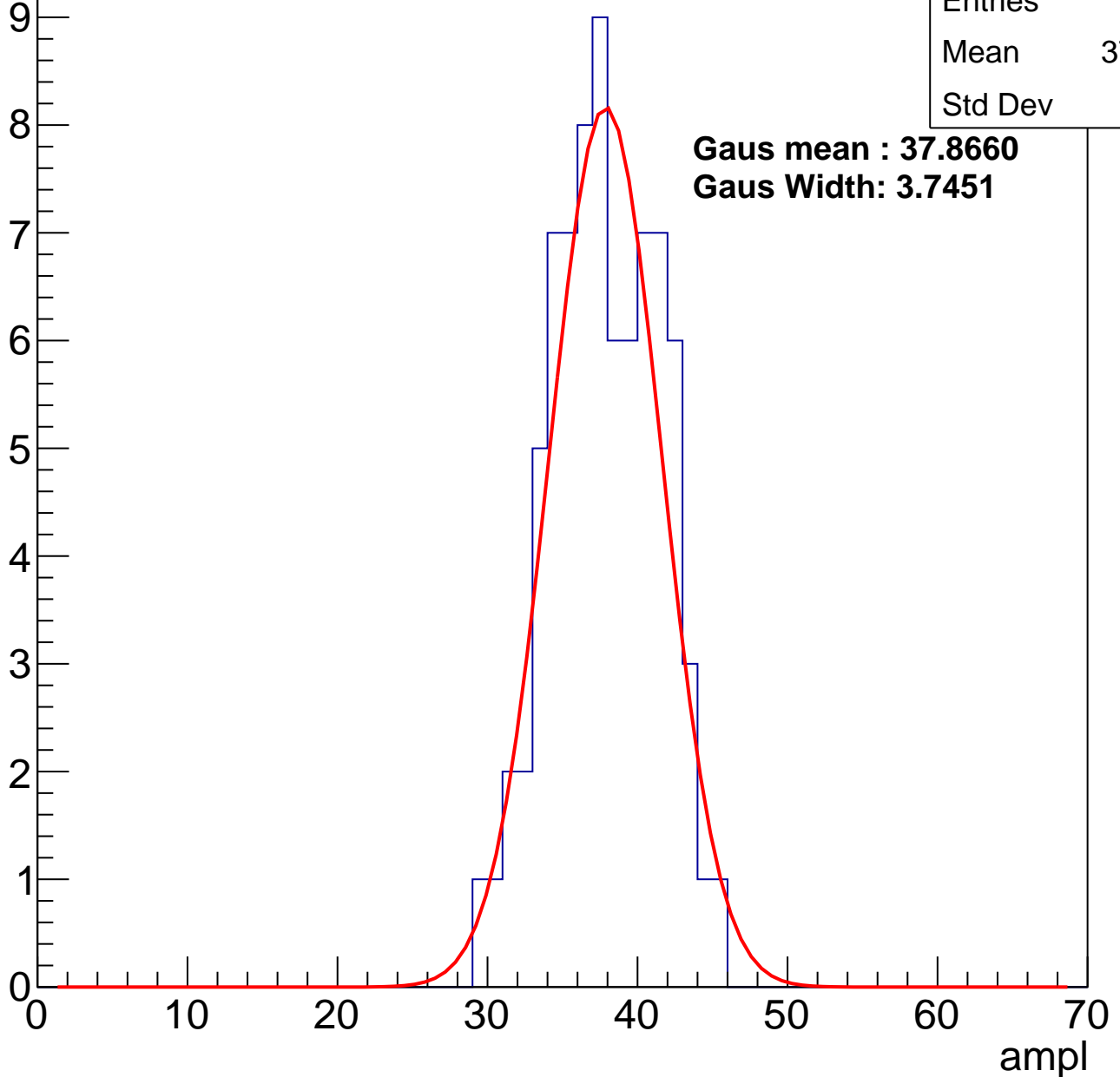
B1L102S, U13-ch76, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	37.38
Std Dev	3.52

Gaus mean : 37.8660
Gaus Width: 3.7451



B1L102S, U13-ch76, adc2

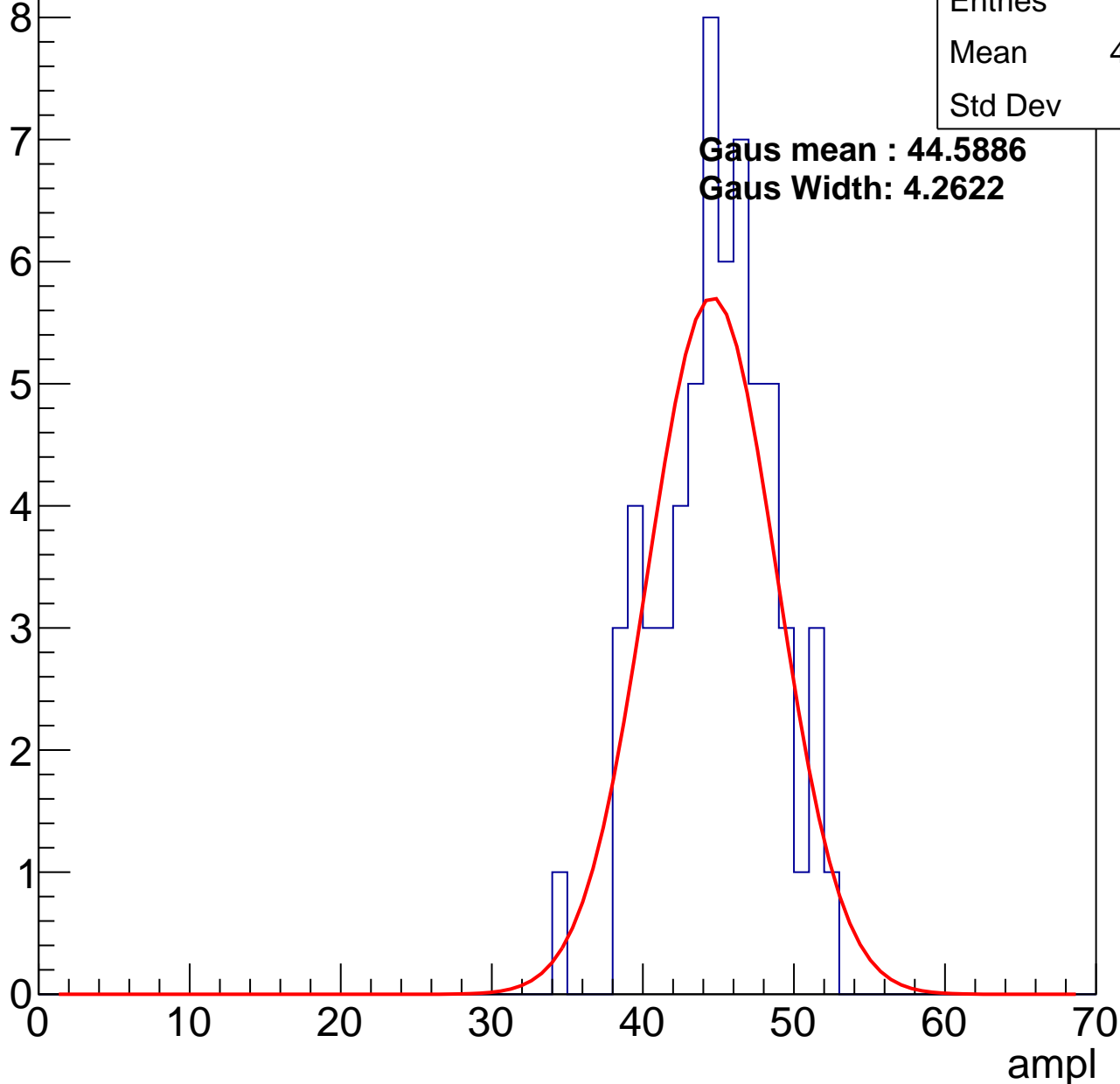
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.37
Std Dev	3.76

Gaus mean : 44.5886

Gaus Width: 4.2622

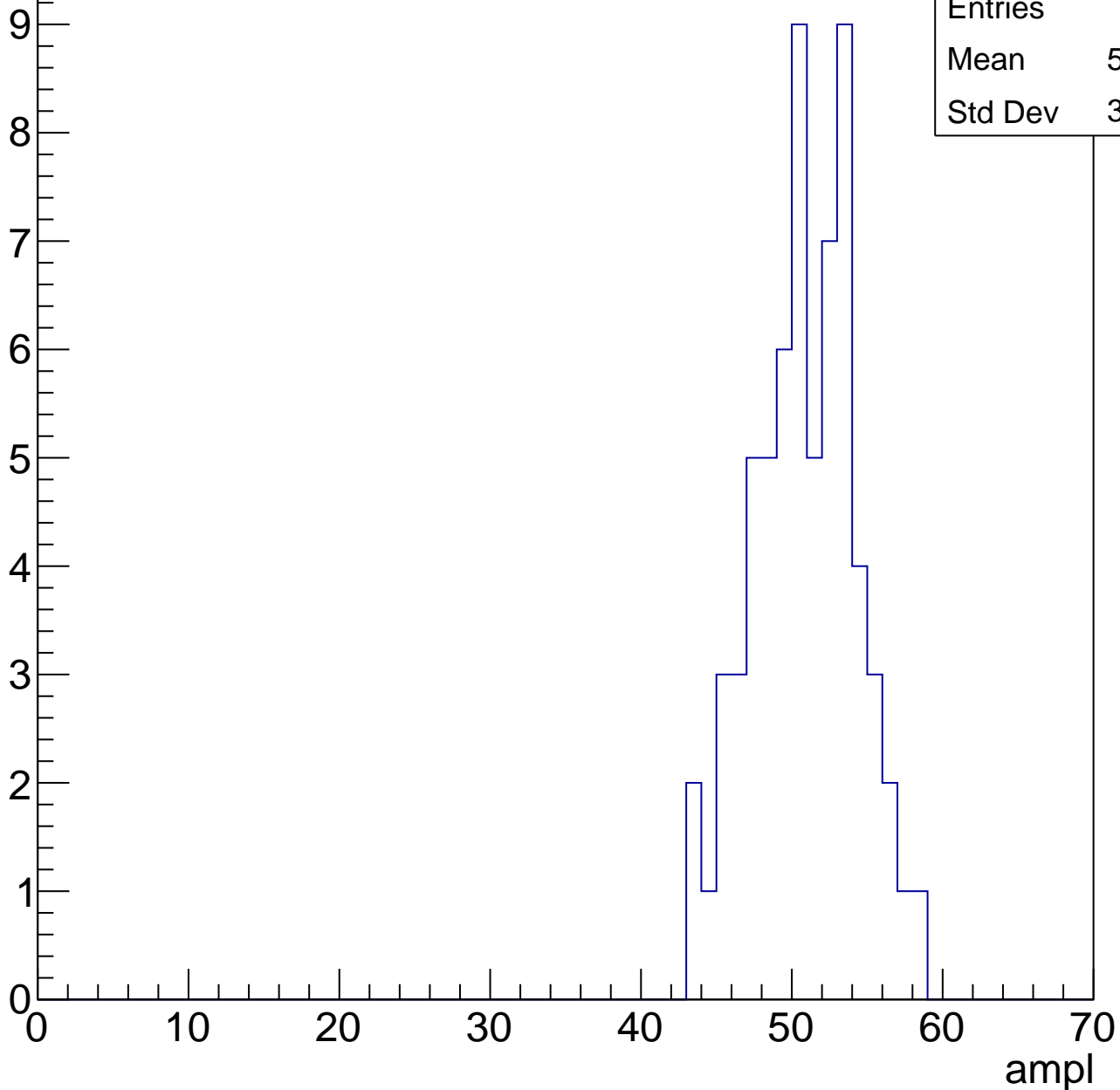


B1L102S, U13-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.39
Std Dev	3.384

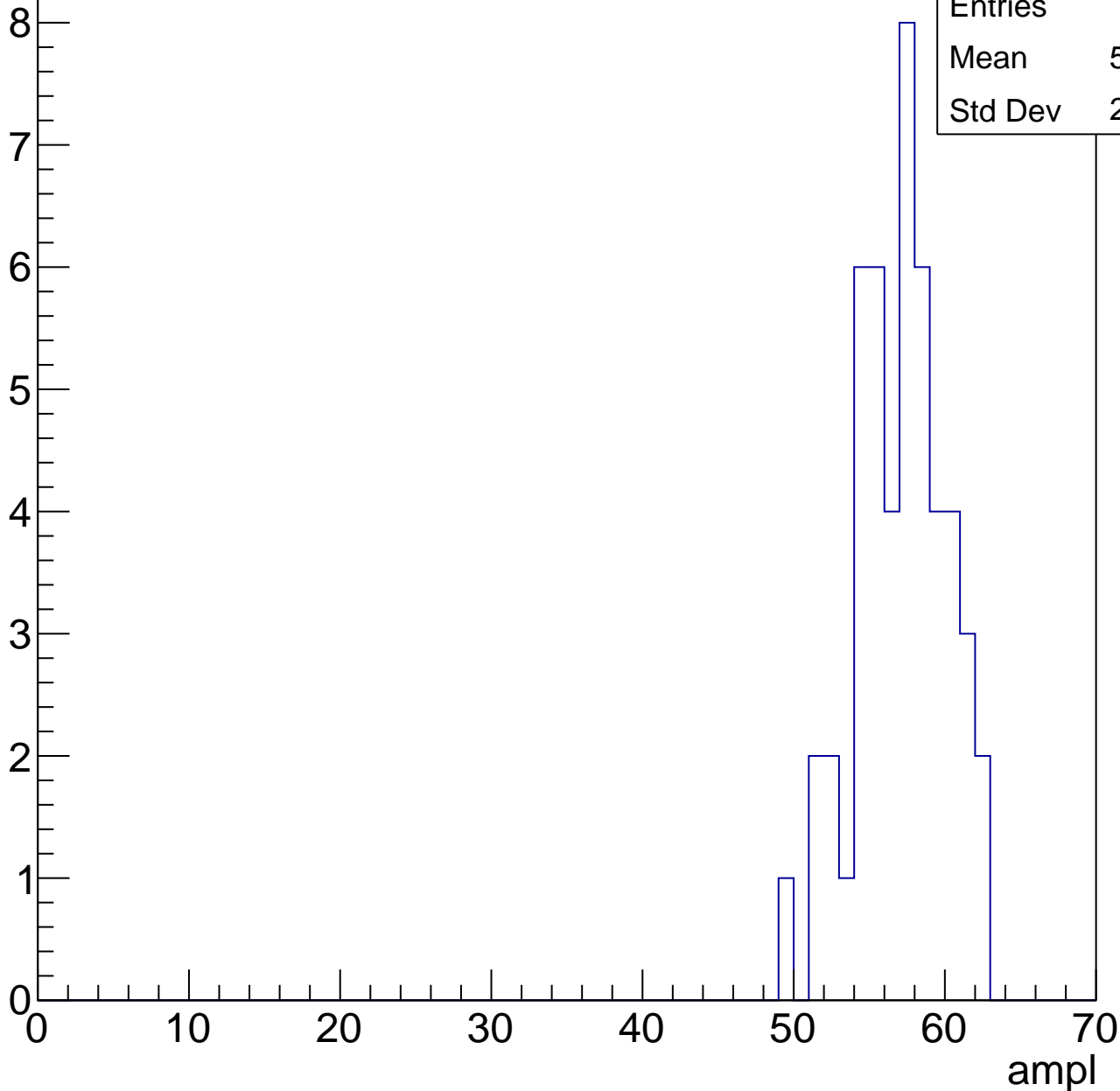


B1L102S, U13-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.59
Std Dev	2.969

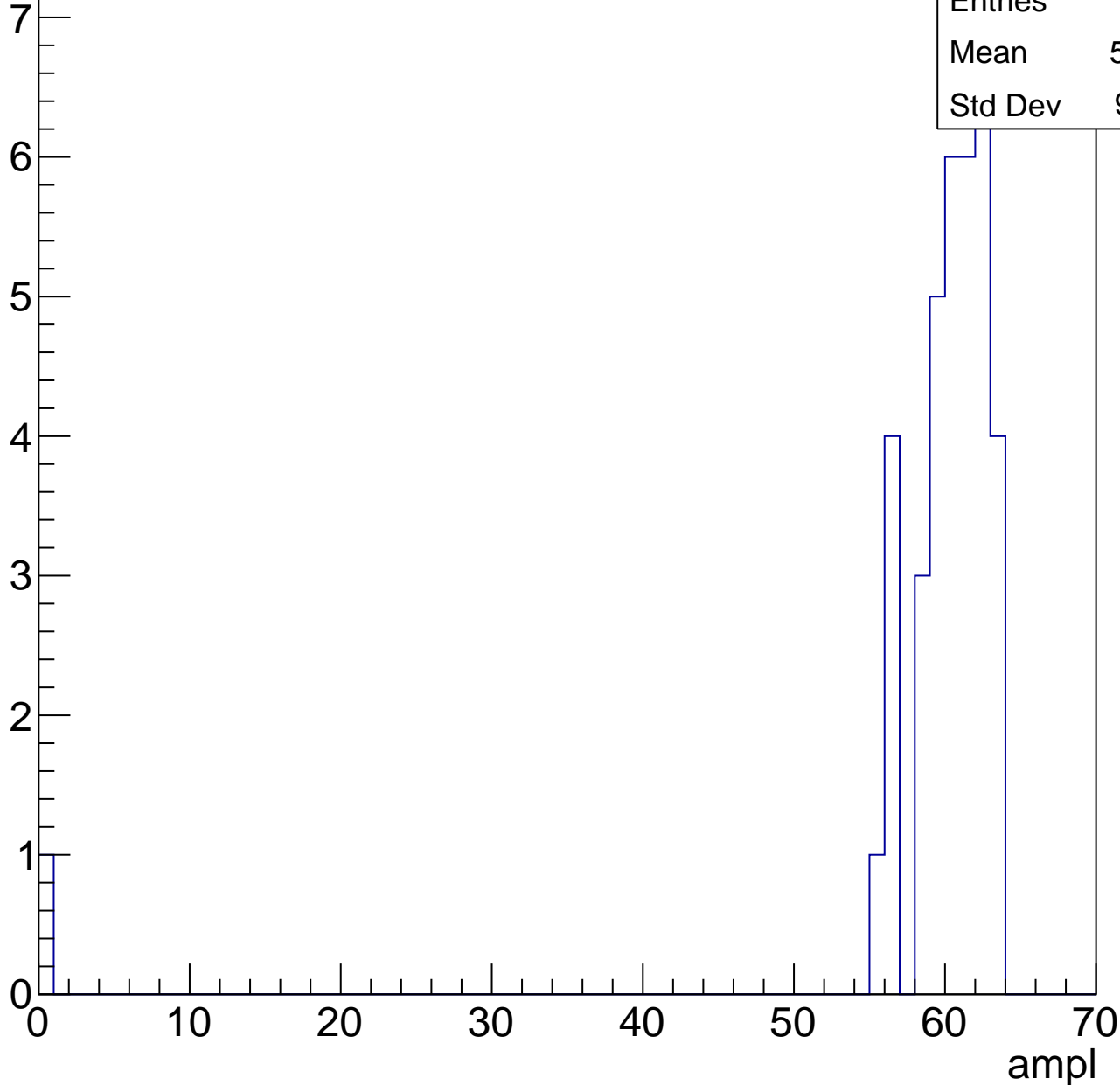


B1L102S, U13-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.38
Std Dev	9.971

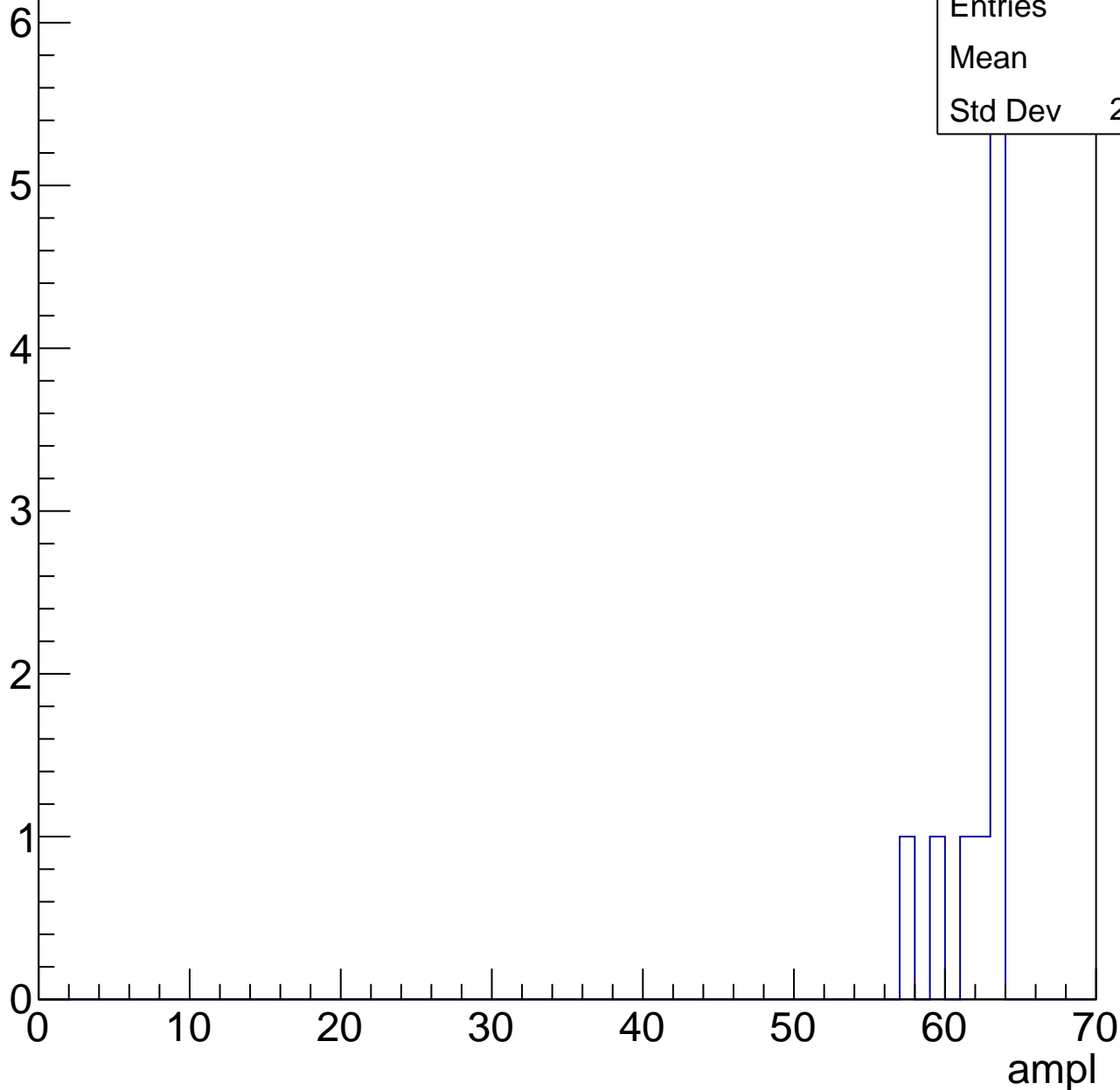


B1L102S, U13-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

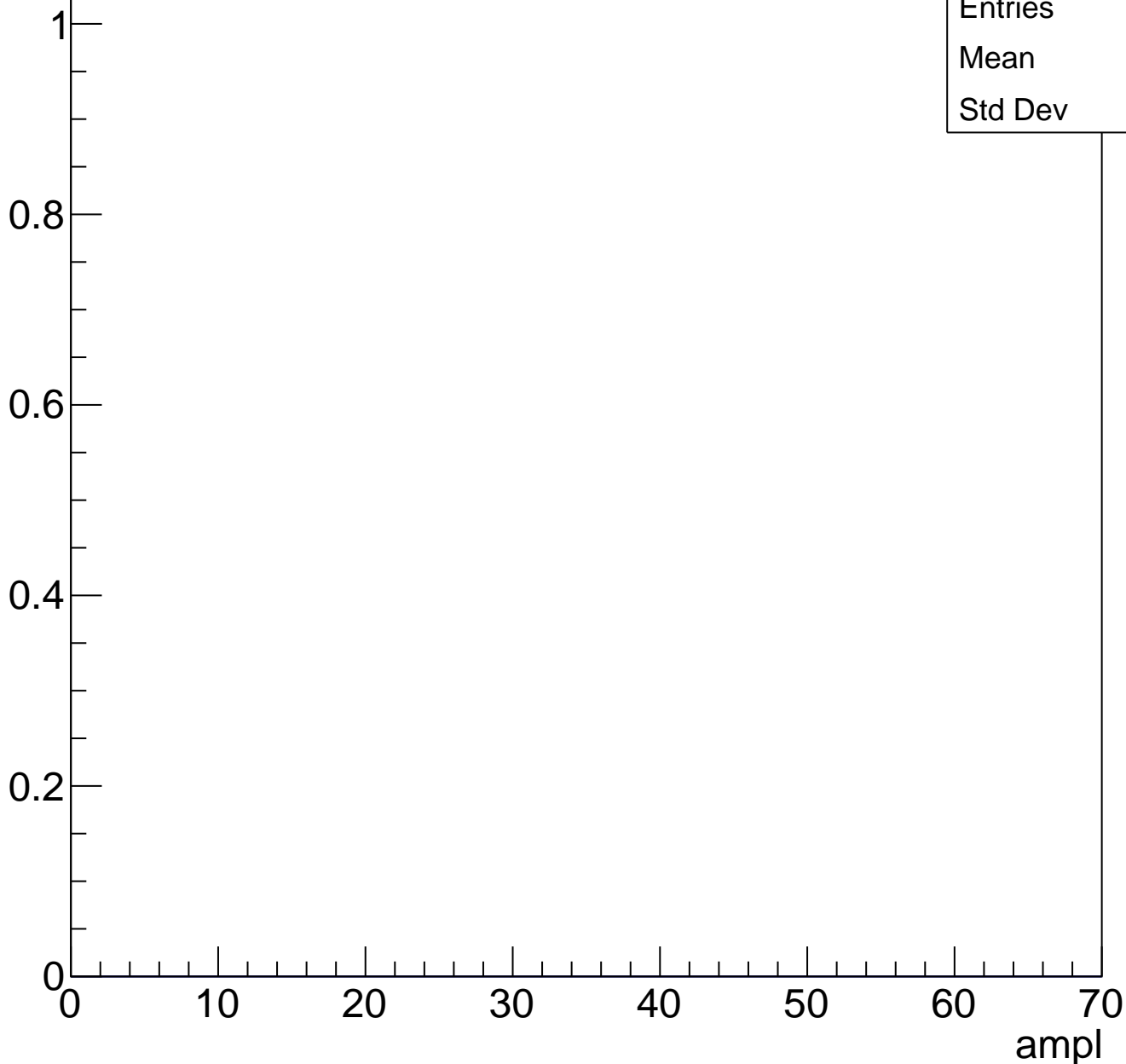
Entries	10
Mean	61.7
Std Dev	2.002



B1L102S, U13-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch77, adc0

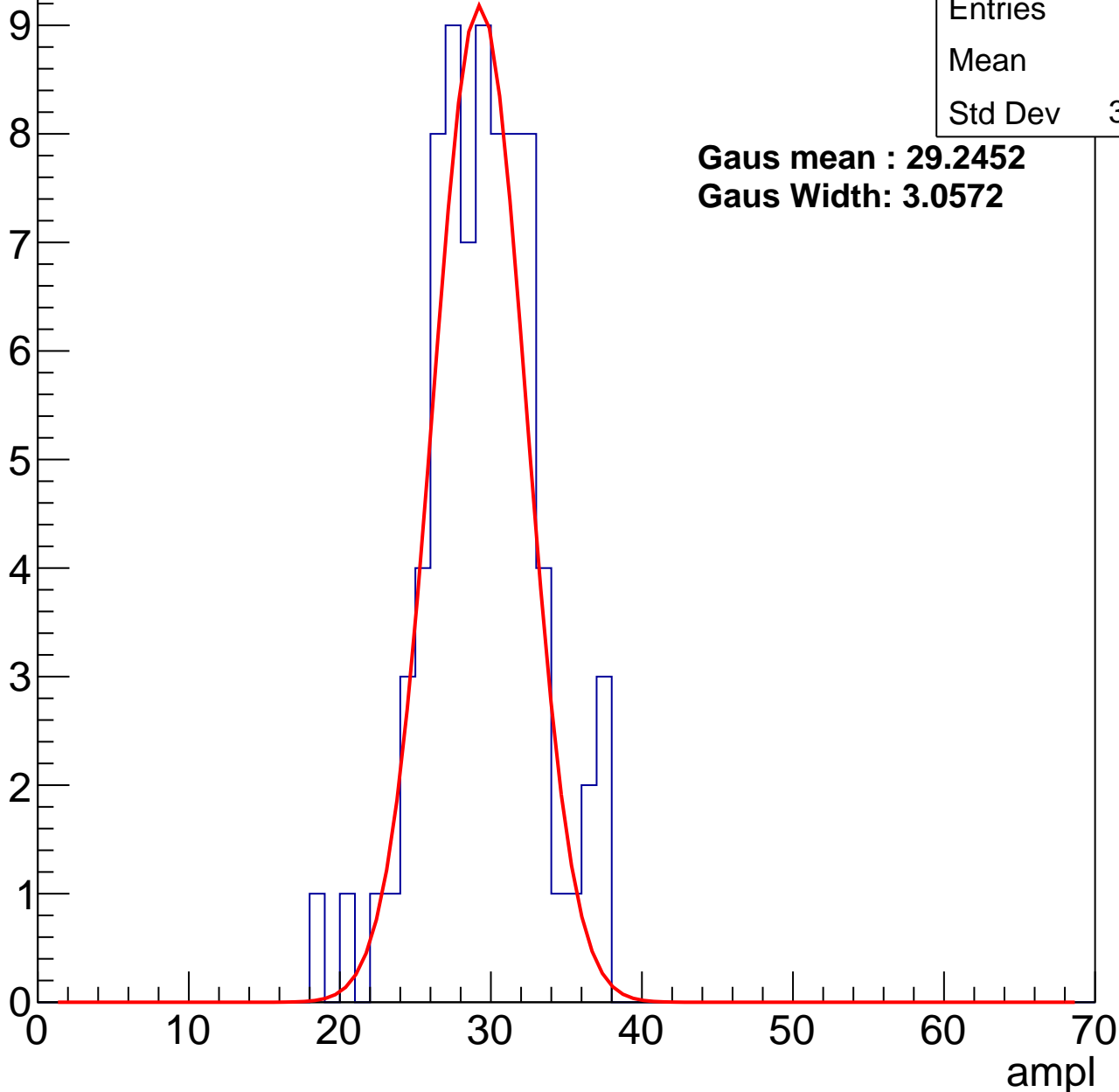
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29
Std Dev	3.677

Gaus mean : 29.2452

Gaus Width: 3.0572



B1L102S, U13-ch77, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	36.86
Std Dev	3.66

Gaus mean : 37.6498

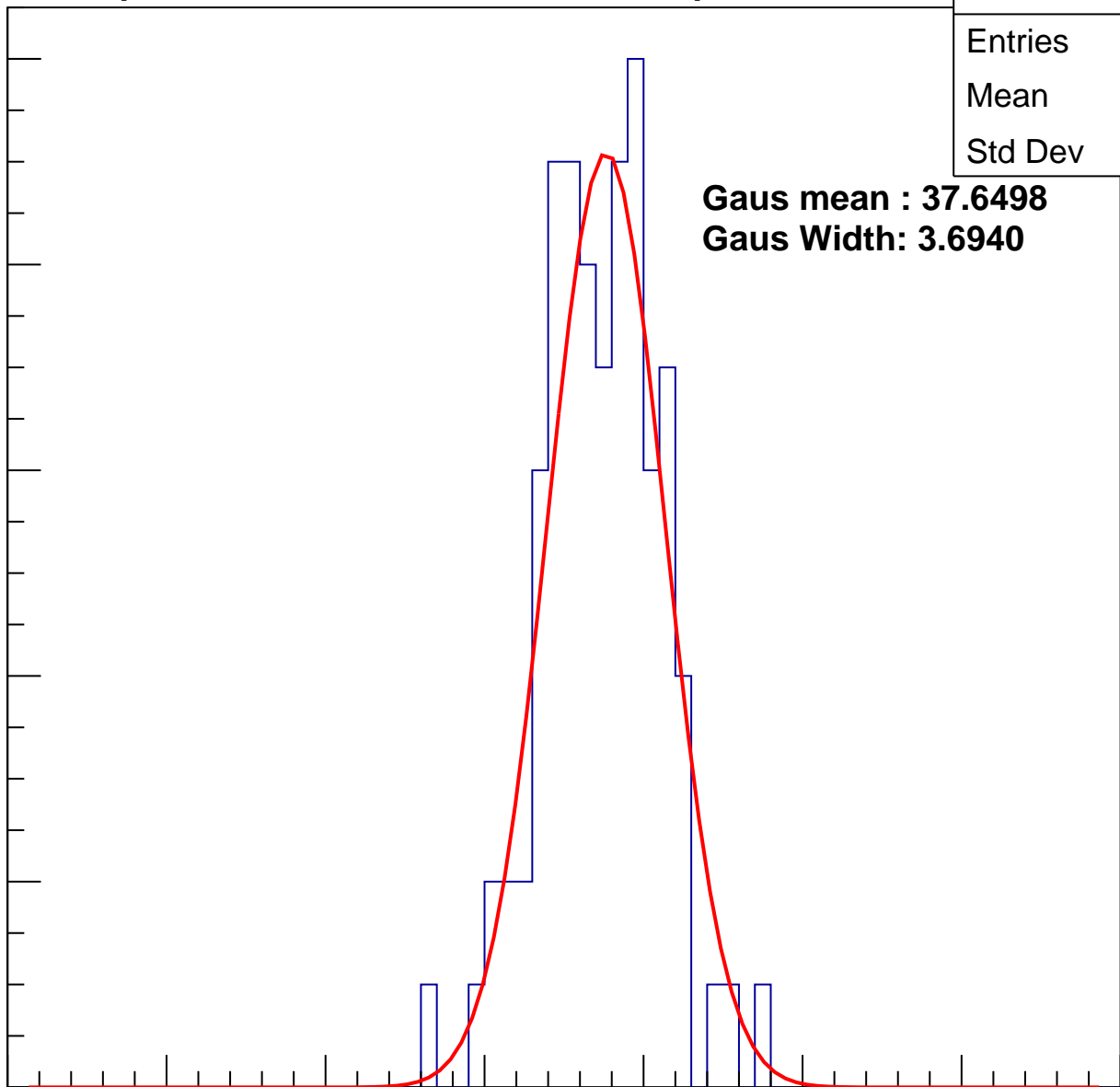
Gaus Width: 3.6940

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

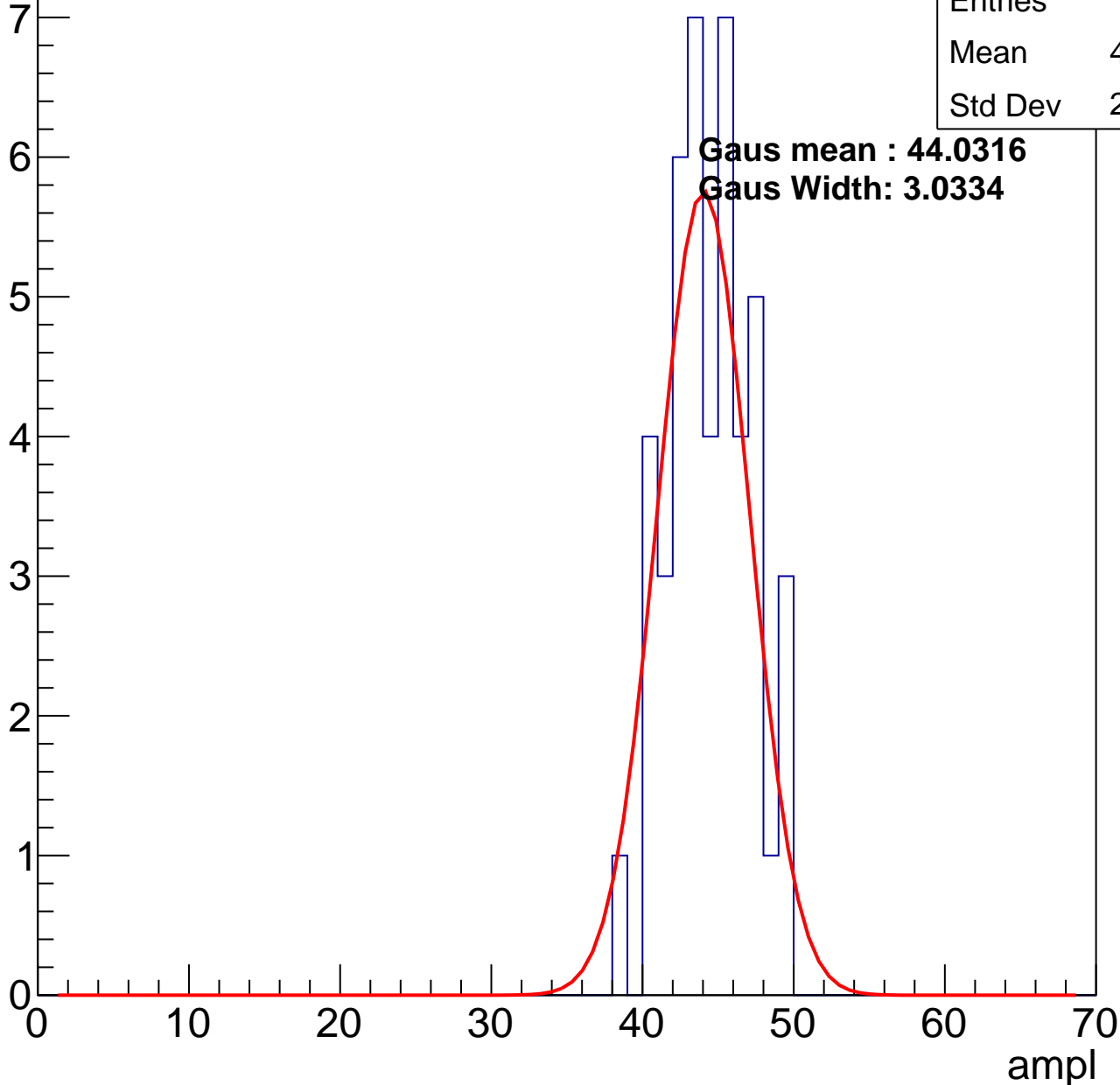


B1L102S, U13-ch77, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	43.98
Std Dev	2.654

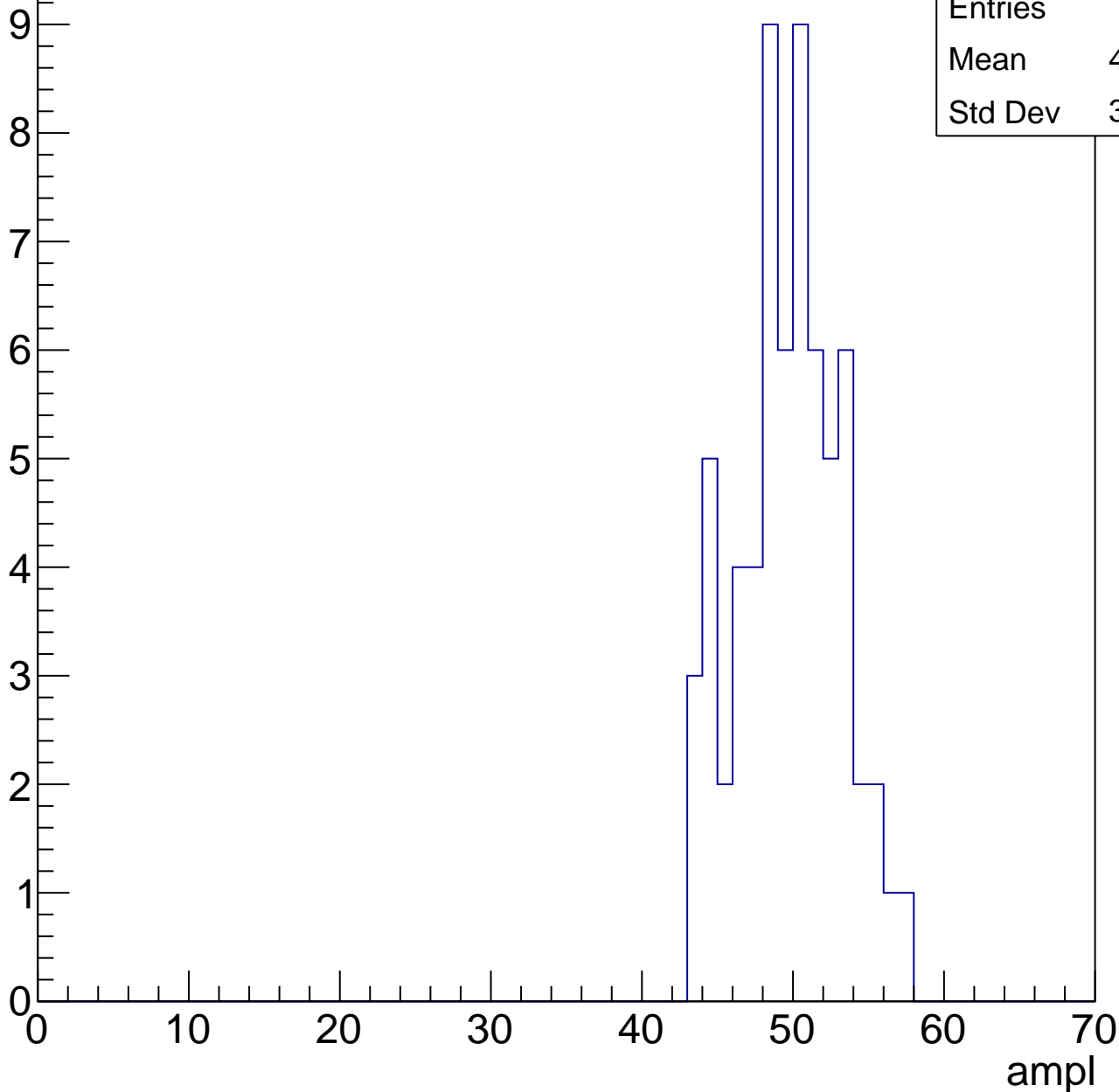


B1L102S, U13-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

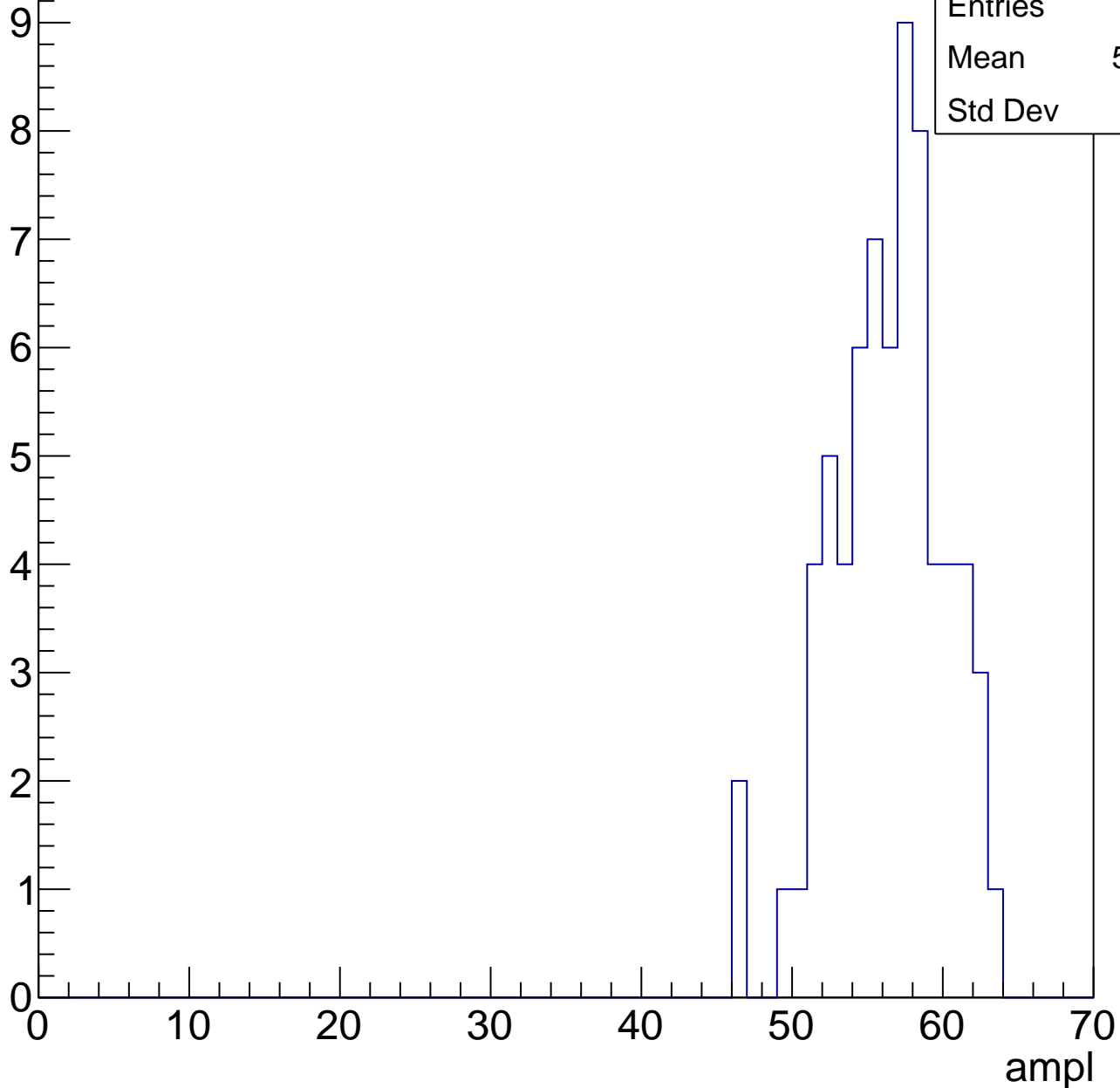
Entries	65
Mean	49.26
Std Dev	3.353



B1L102S, U13-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

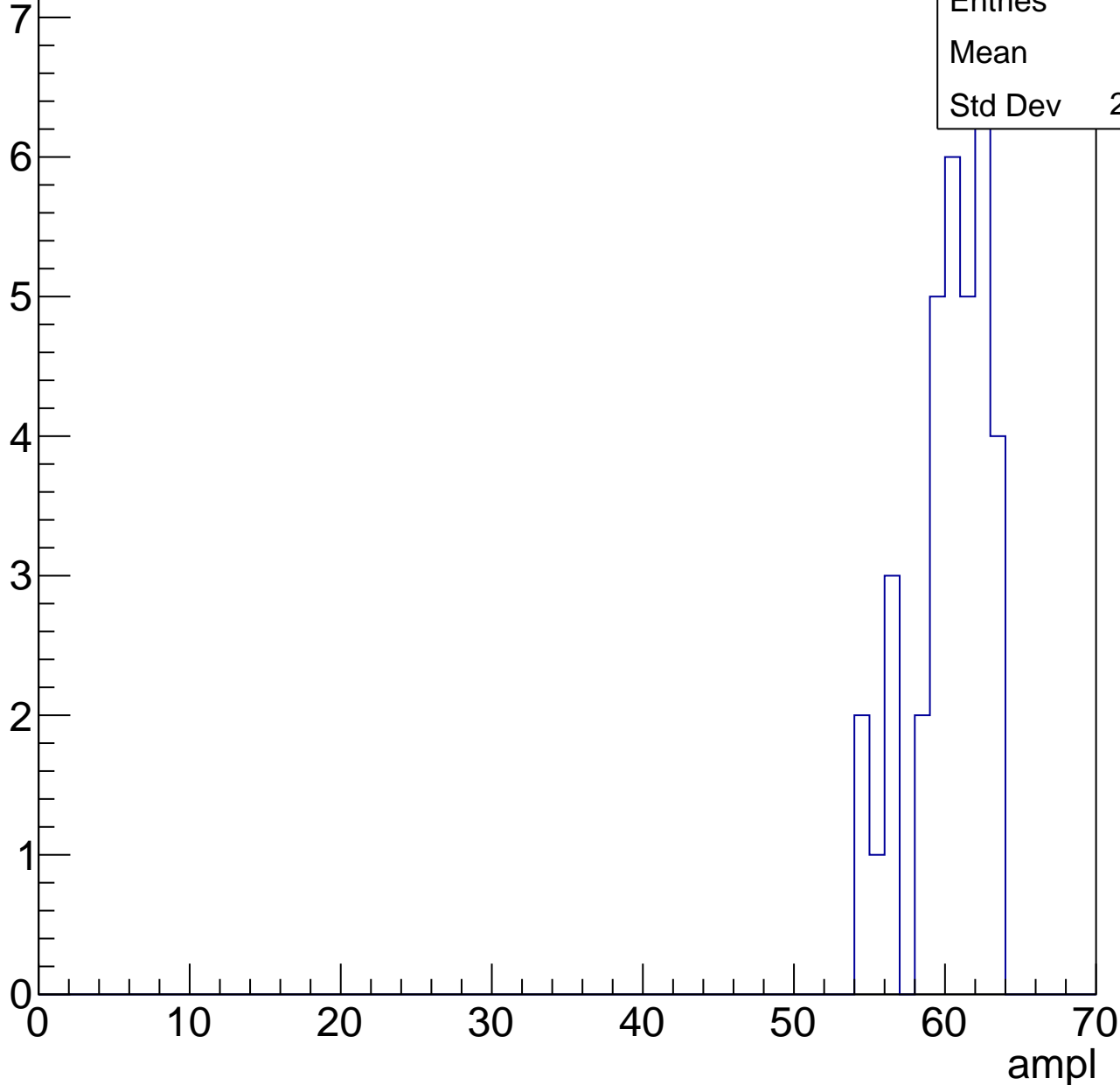


B1L102S, U13-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	59.8
Std Dev	2.539



B1L102S, U13-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch78, adc0

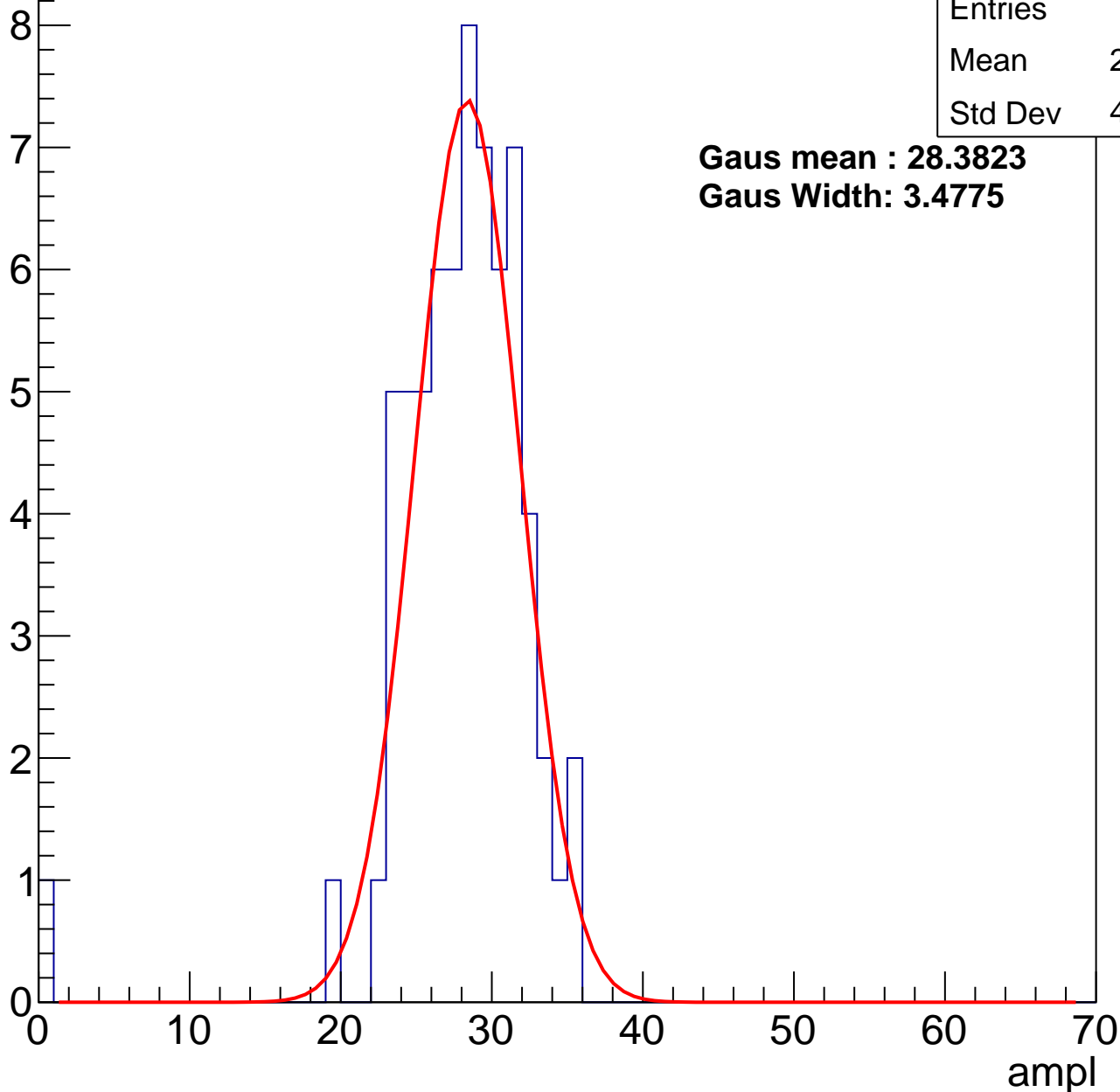
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.48
Std Dev	4.739

Gaus mean : 28.3823

Gaus Width: 3.4775



B1L102S, U13-ch78, adc1

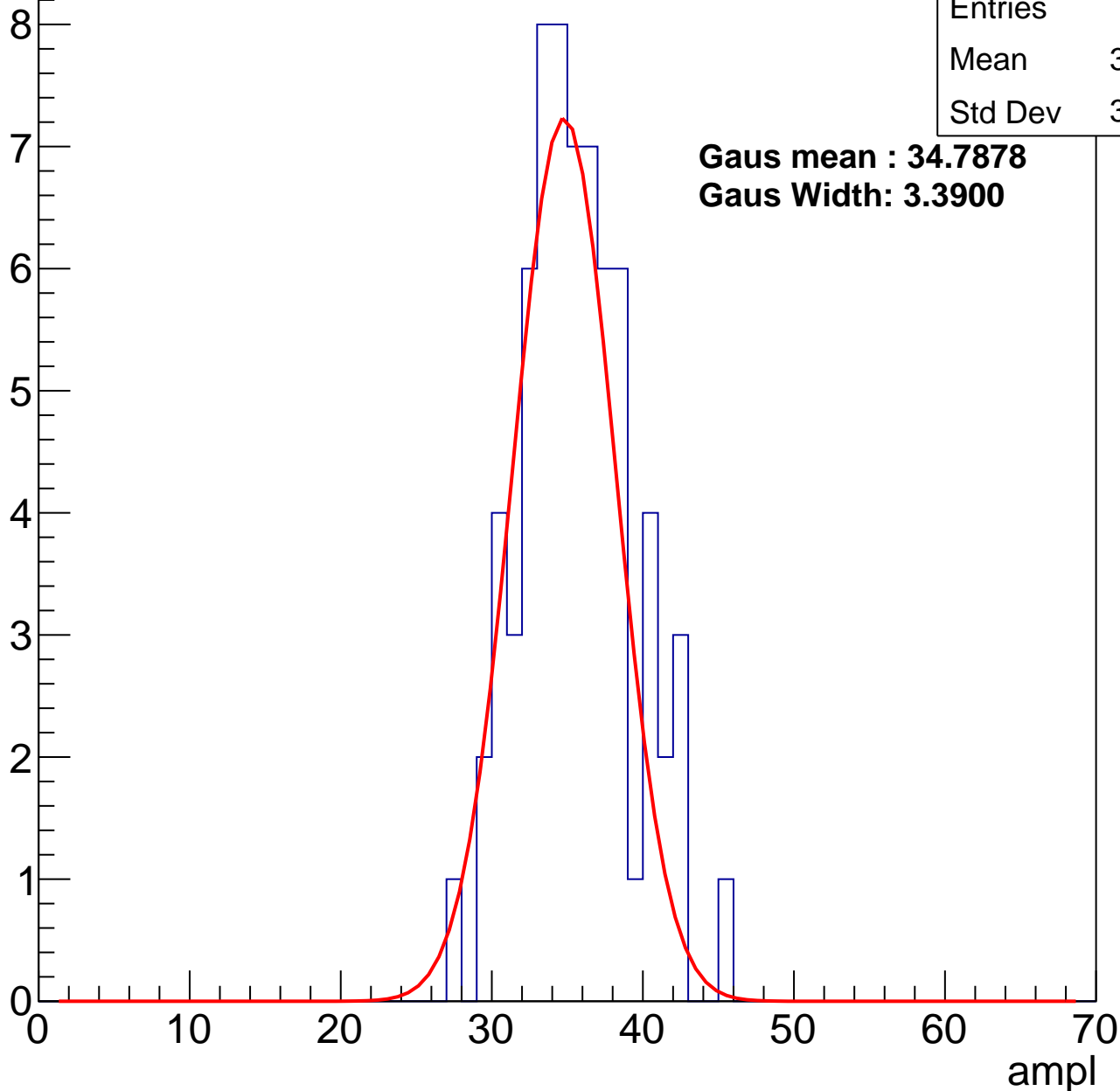
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.14
Std Dev	3.605

Gaus mean : 34.7878

Gaus Width: 3.3900



B1L102S, U13-ch78, adc2

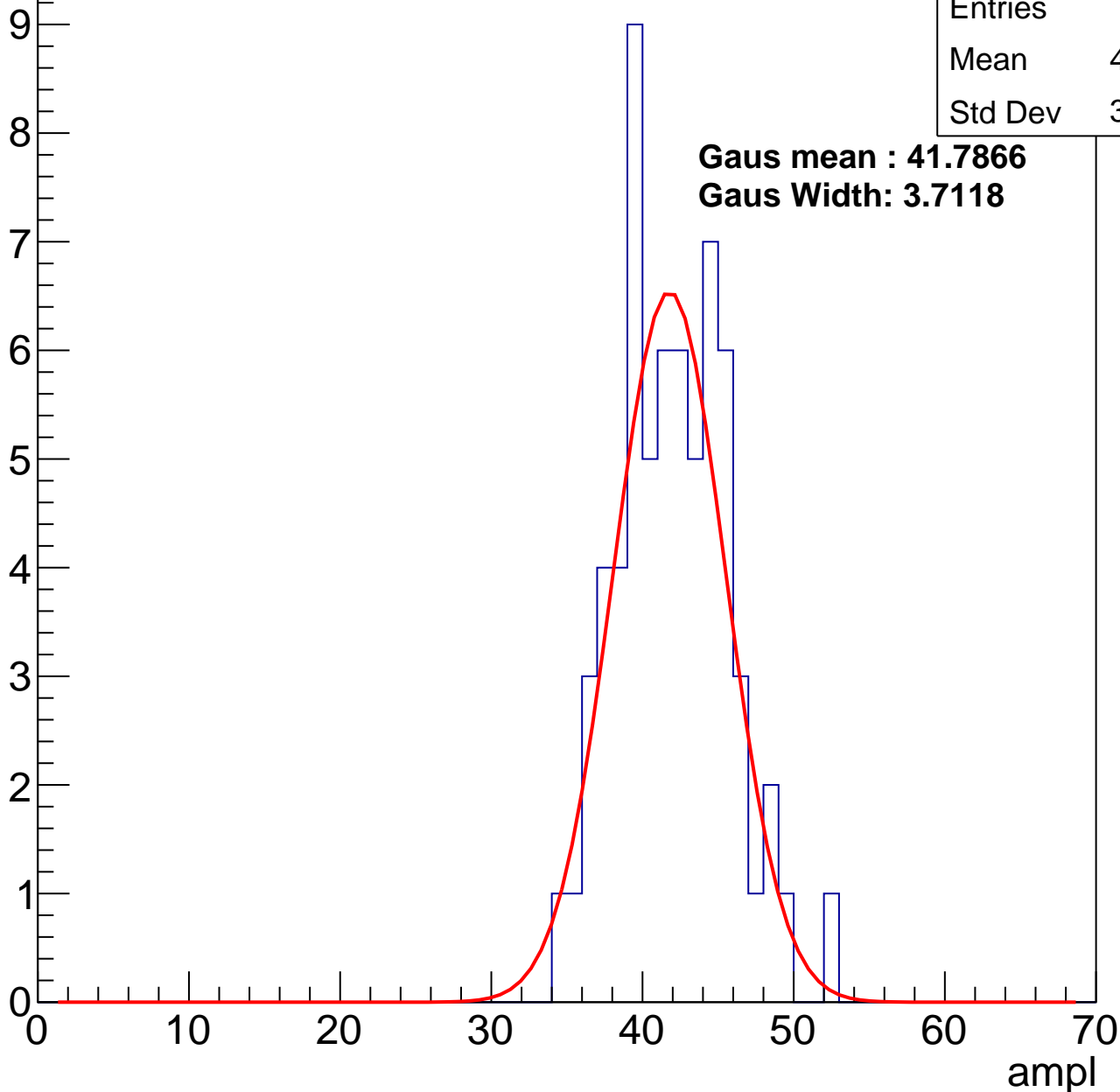
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.55
Std Dev	3.638

Gaus mean : 41.7866

Gaus Width: 3.7118

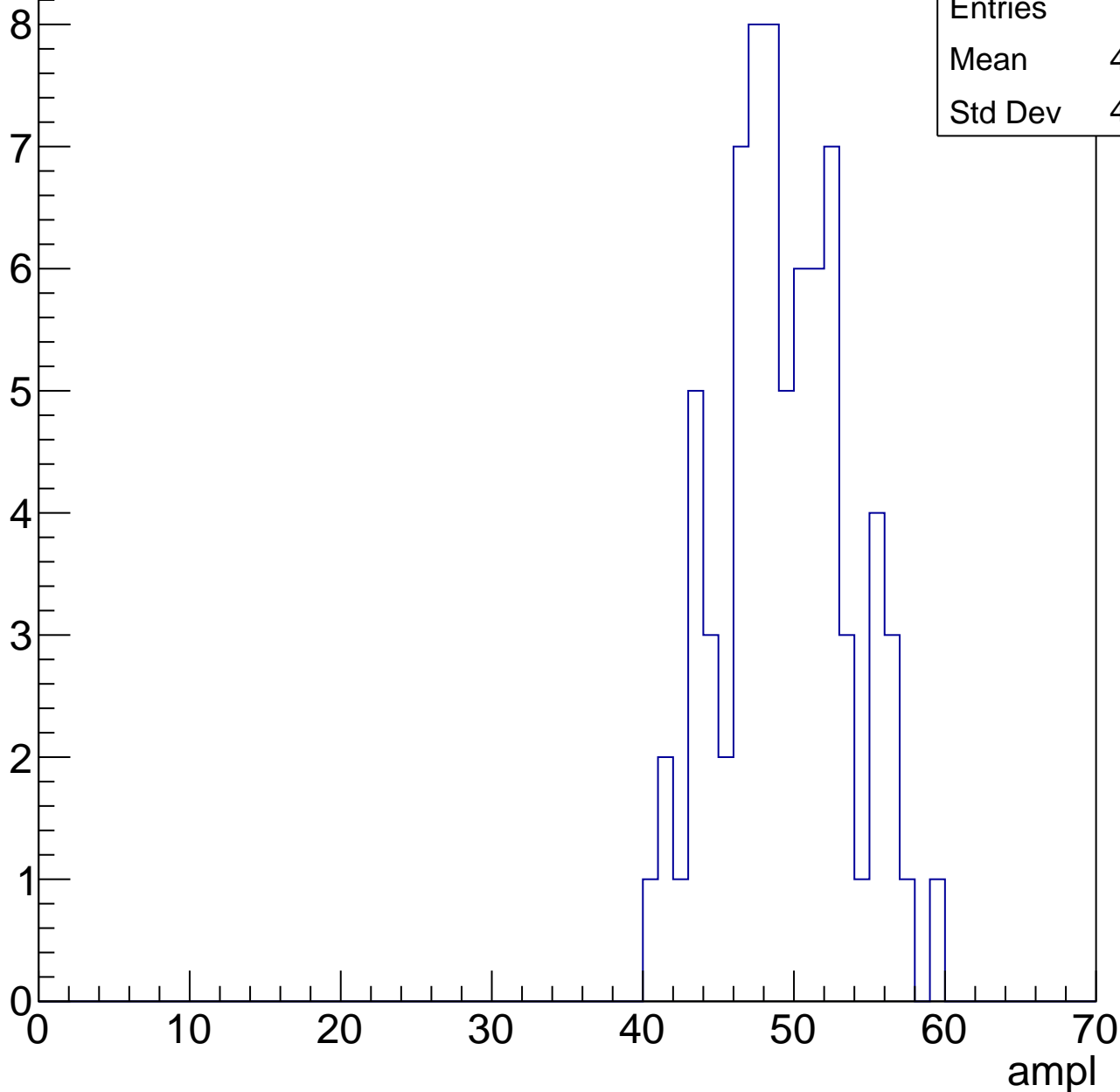


B1L102S, U13-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.85
Std Dev	4.138

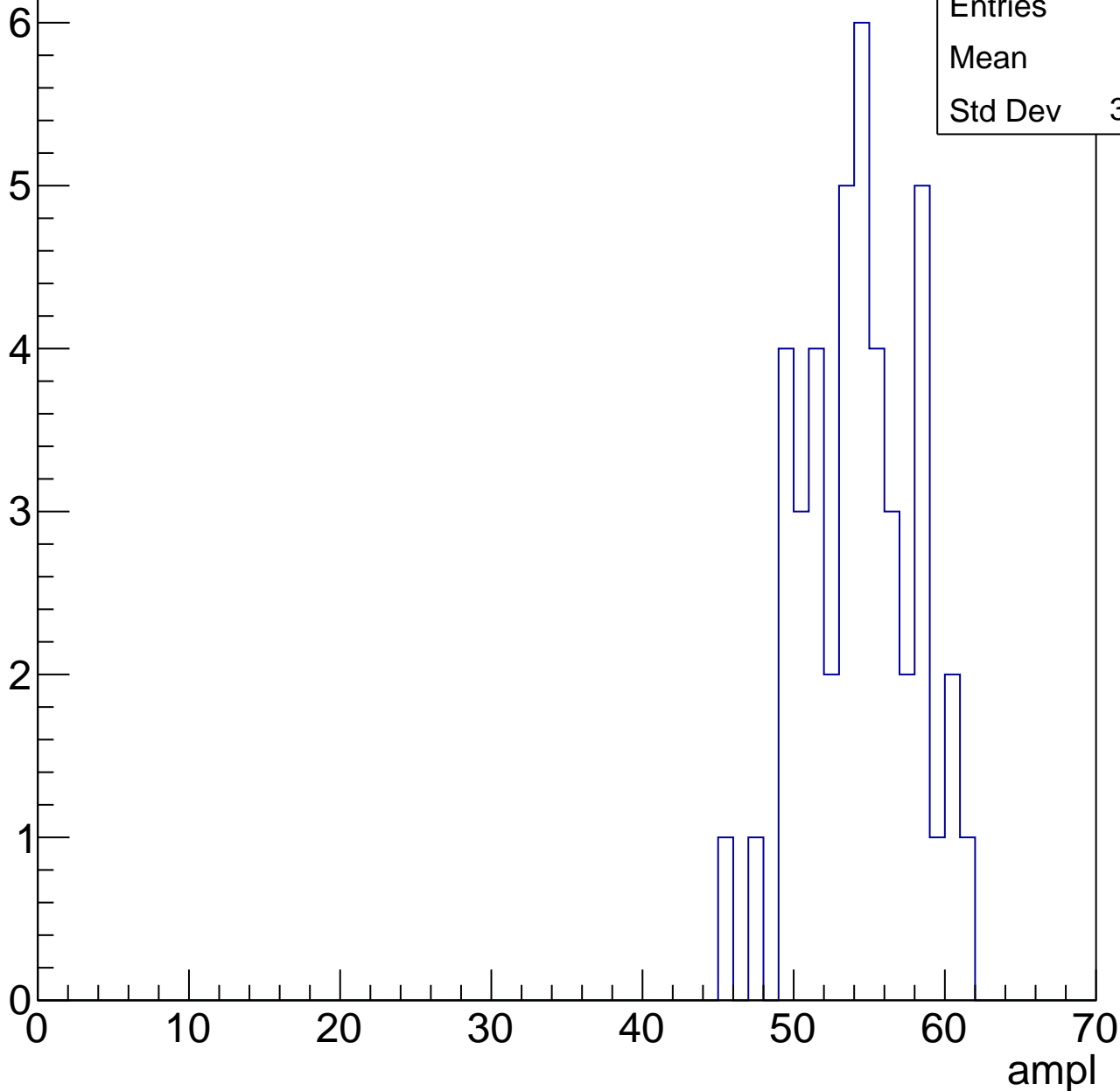


B1L102S, U13-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	53.8
Std Dev	3.647

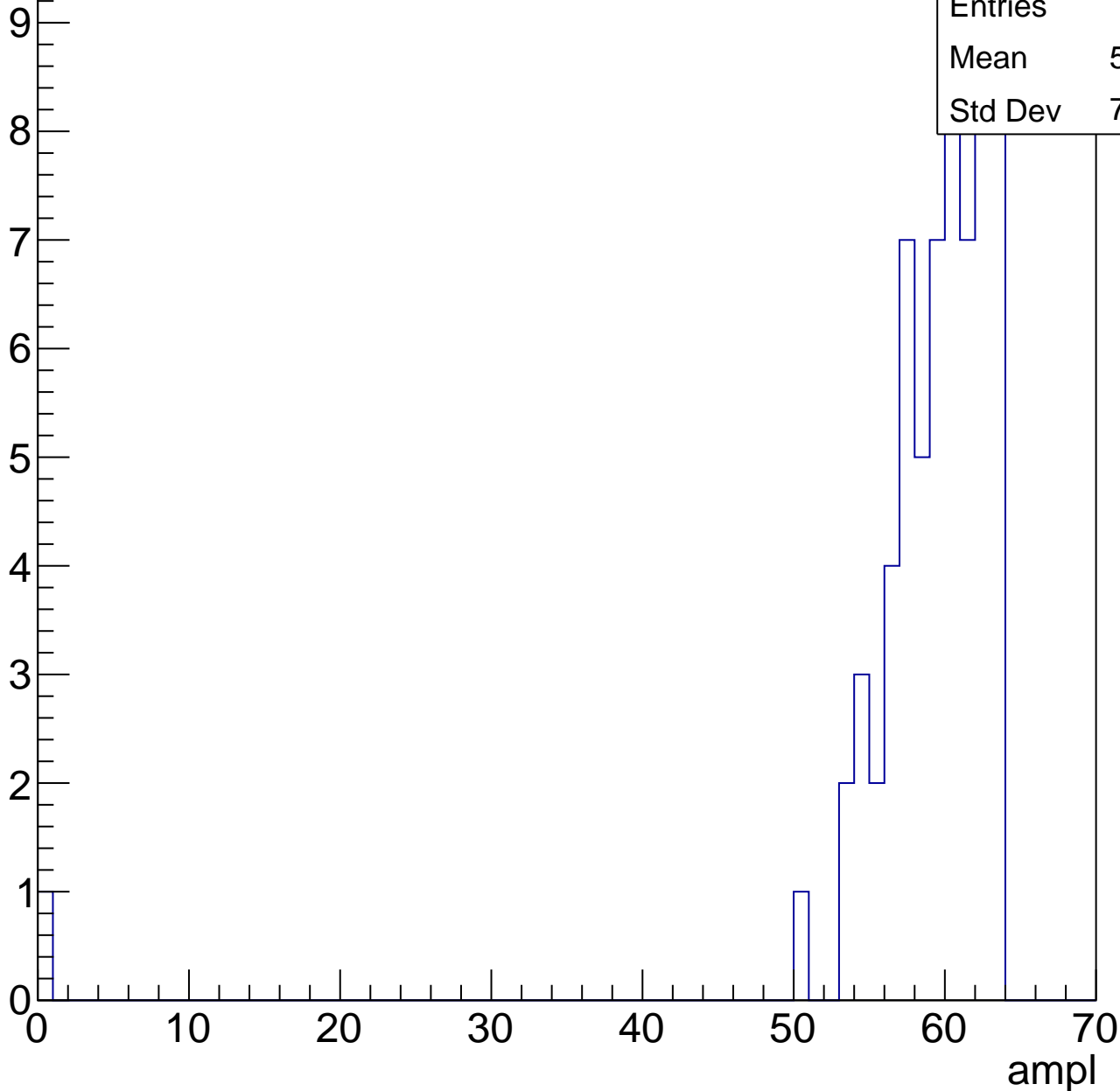


B1L102S, U13-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

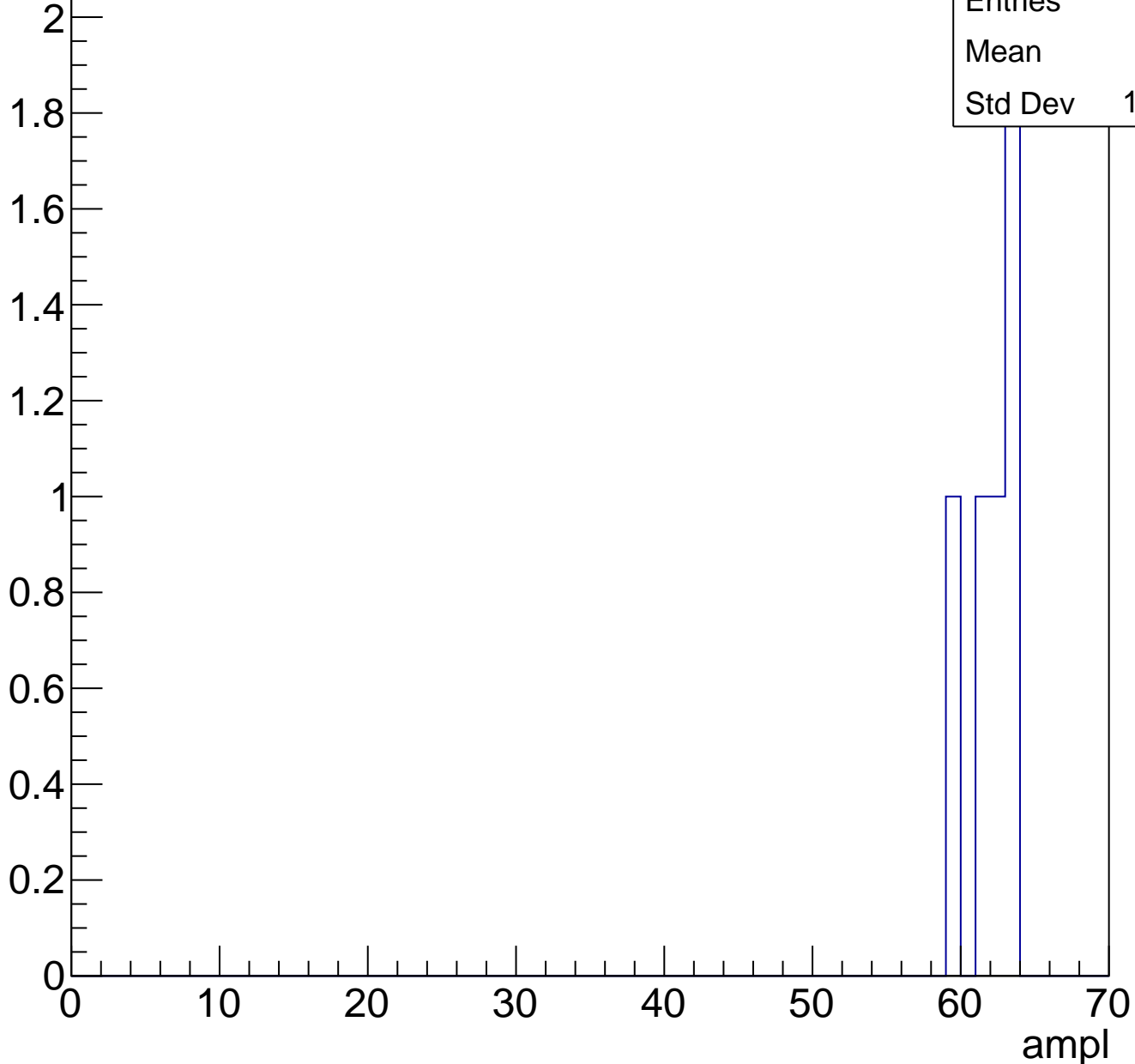
Entries	64
Mean	58.17
Std Dev	7.917



B1L102S, U13-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch79, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	29.92
Std Dev	3.691

Gaus mean : 29.9828

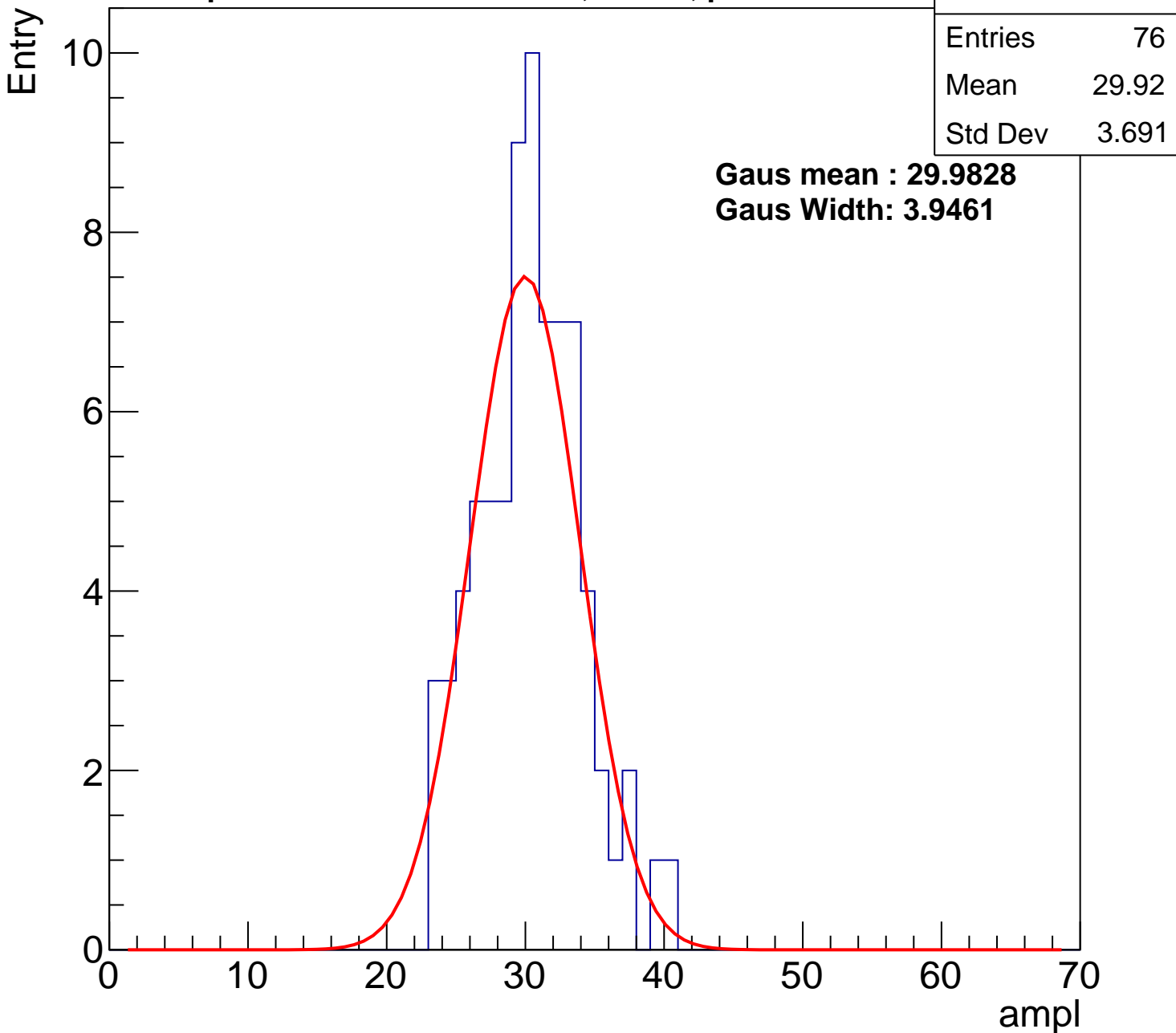
Gaus Width: 3.9461

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch79, adc1

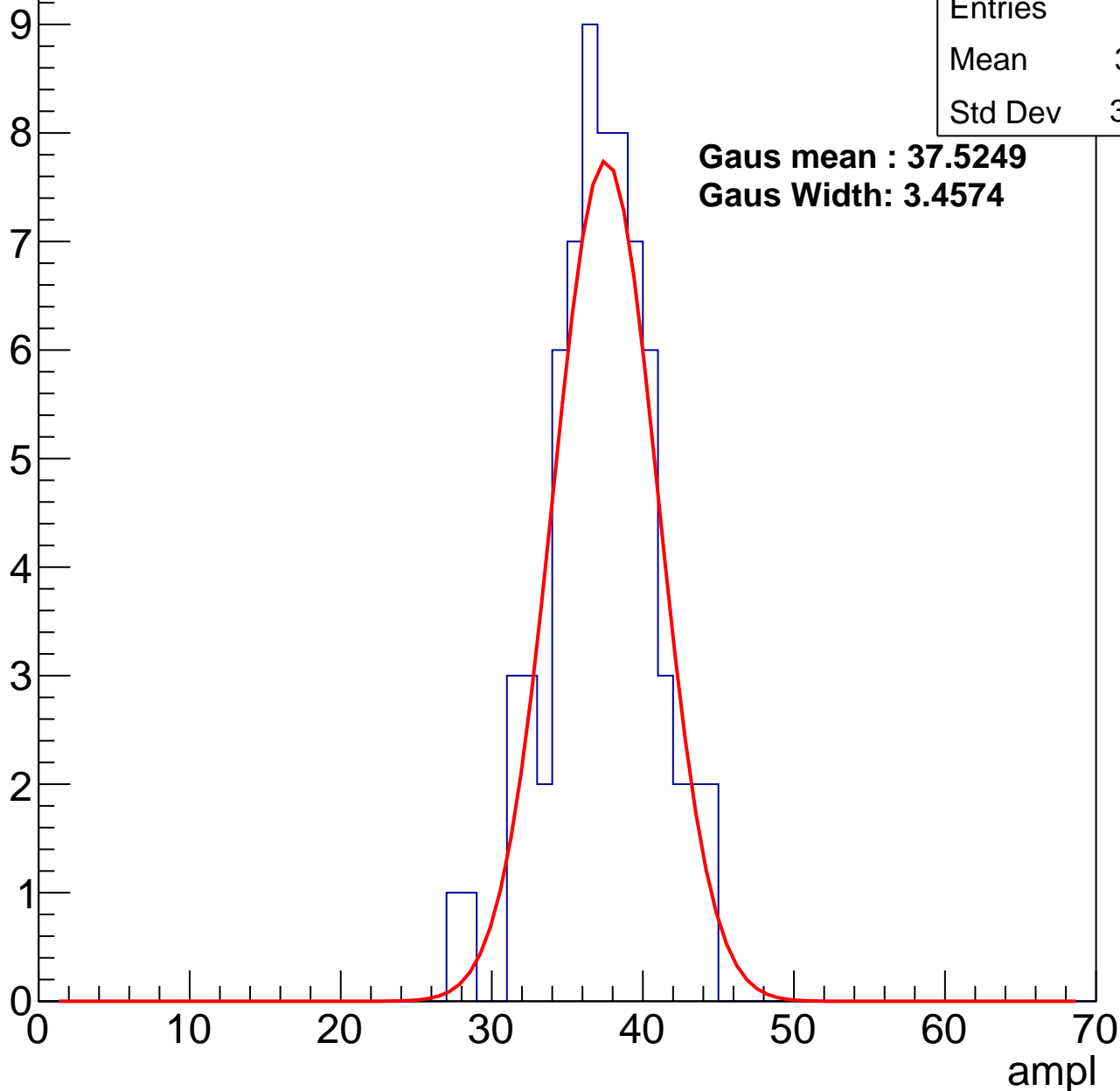
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.81
Std Dev	3.469

Gaus mean : 37.5249

Gaus Width: 3.4574



B1L102S, U13-ch79, adc2

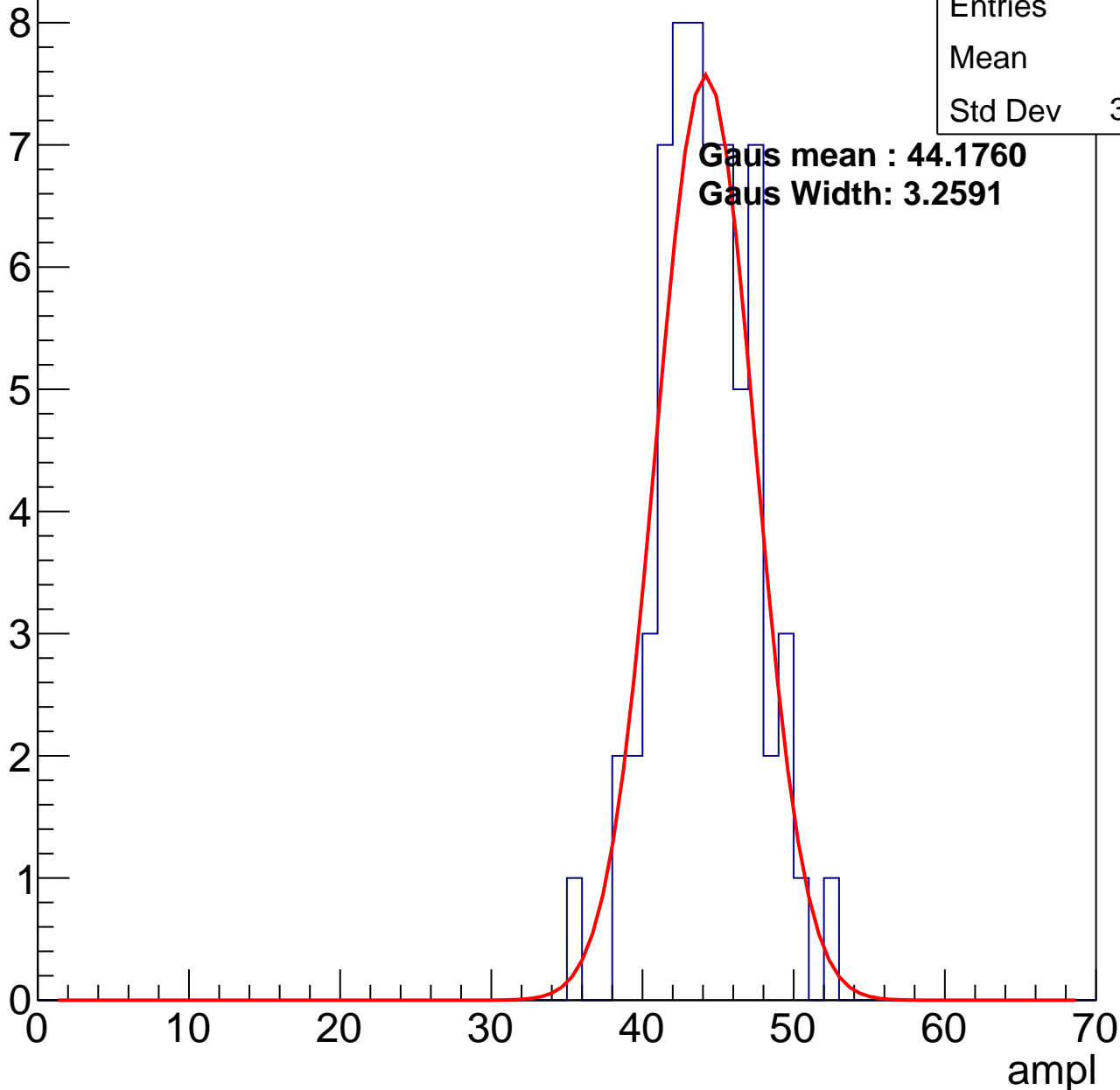
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.8
Std Dev	3.183

Gaus mean : 44.1760

Gaus Width: 3.2591

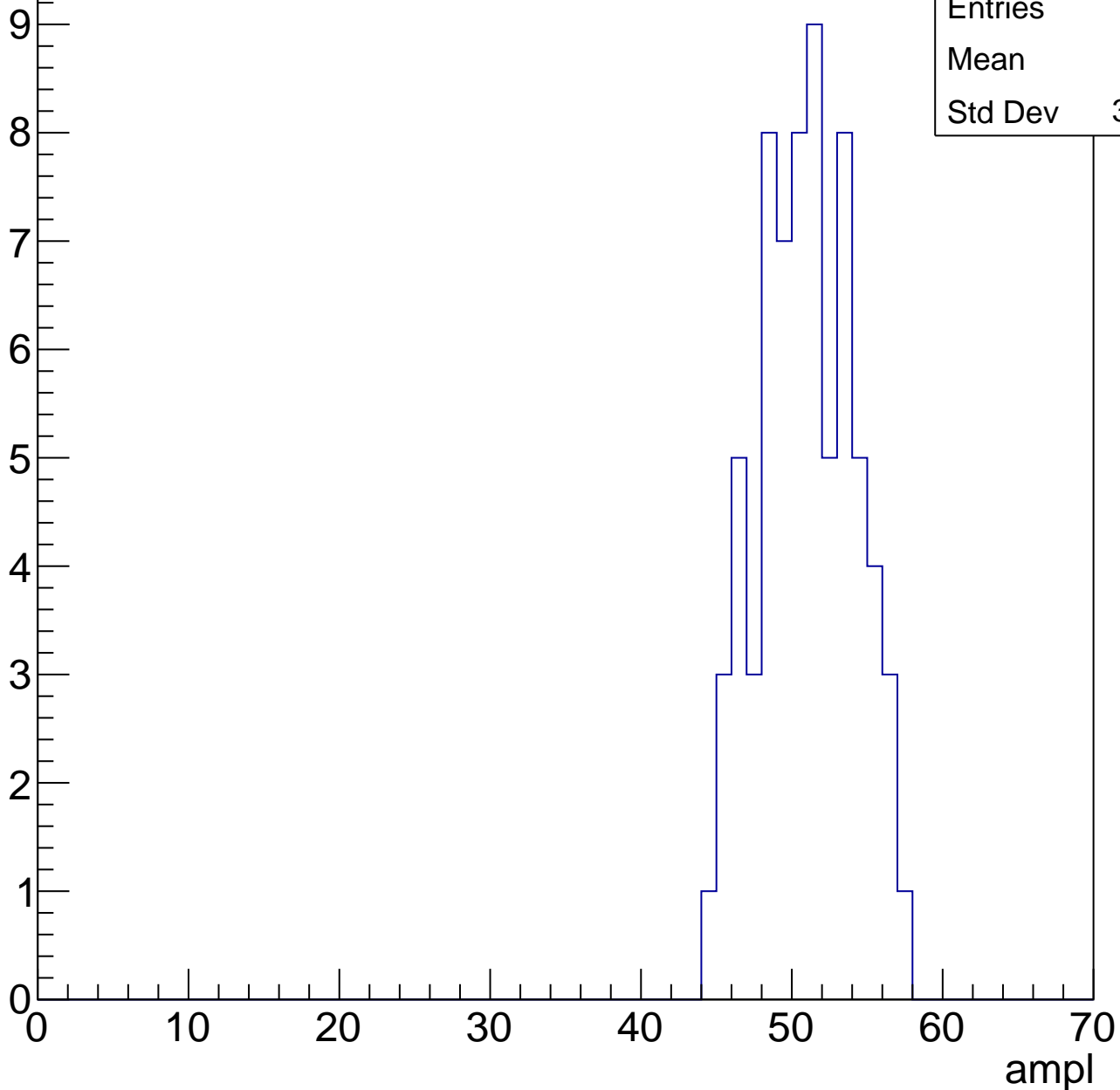


B1L102S, U13-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	50.5
Std Dev	3.111

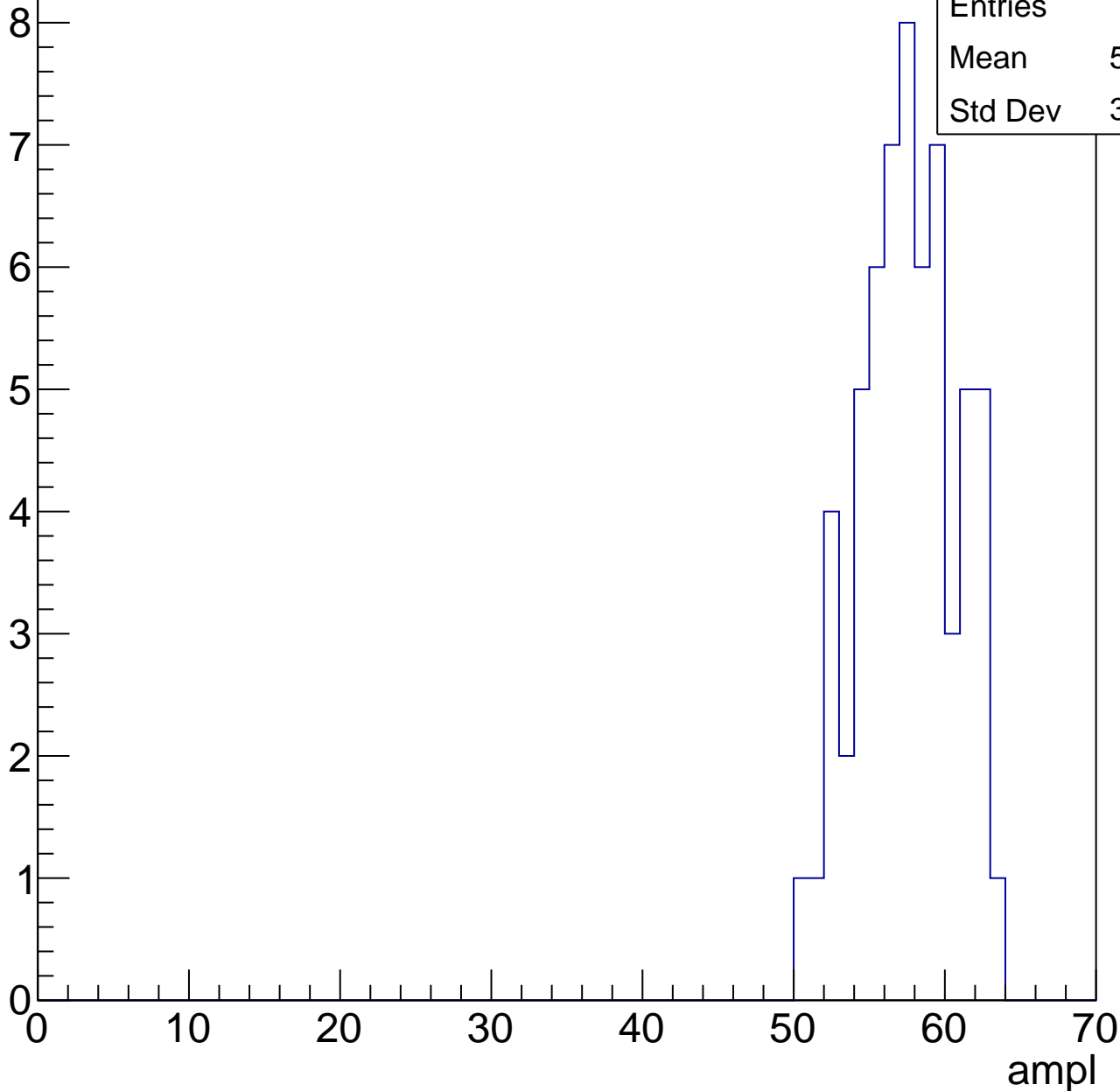


B1L102S, U13-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	57.08
Std Dev	3.122

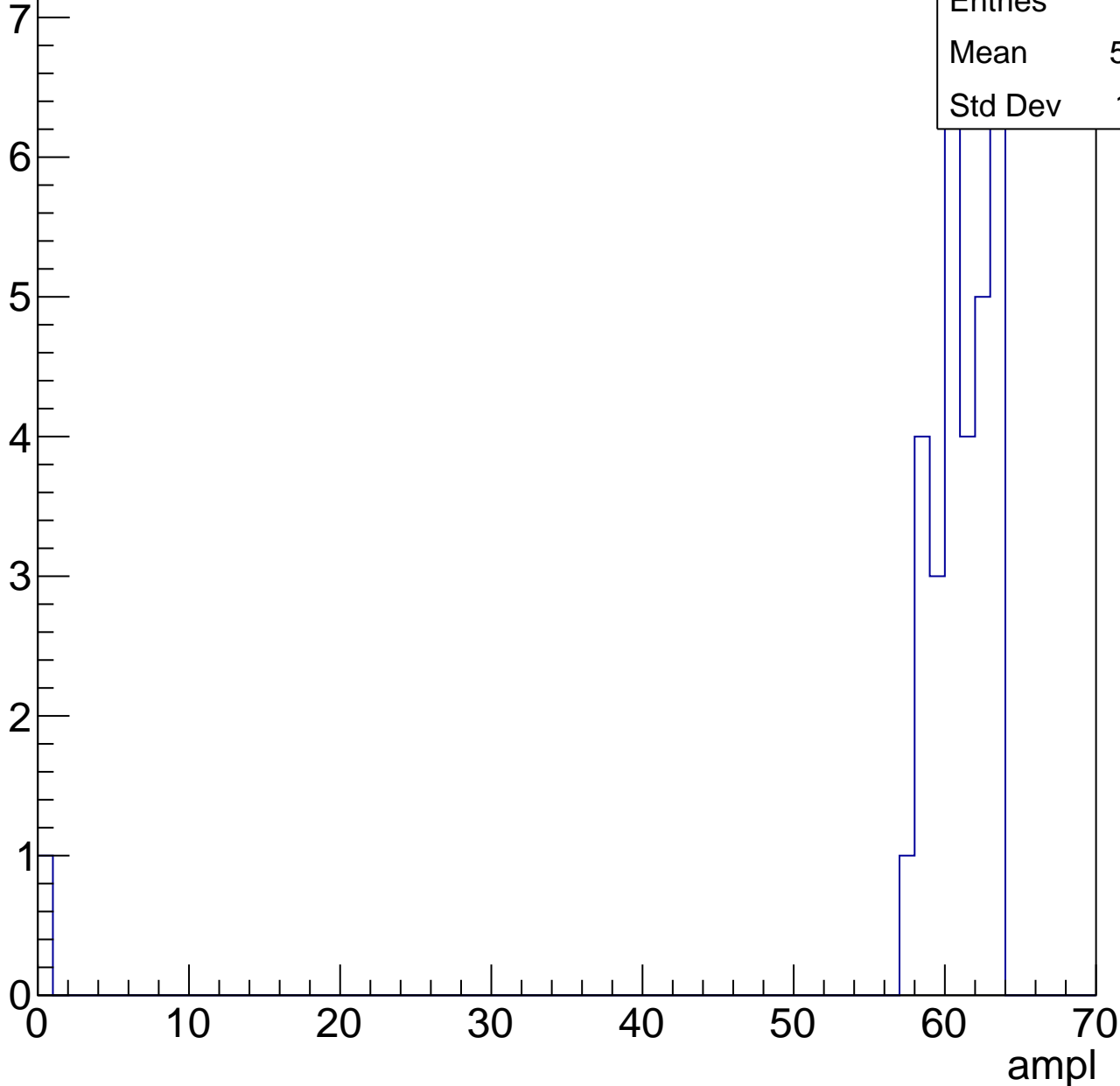


B1L102S, U13-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	58.78
Std Dev	10.71



B1L102S, U13-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L102S, U13-ch80, adc0

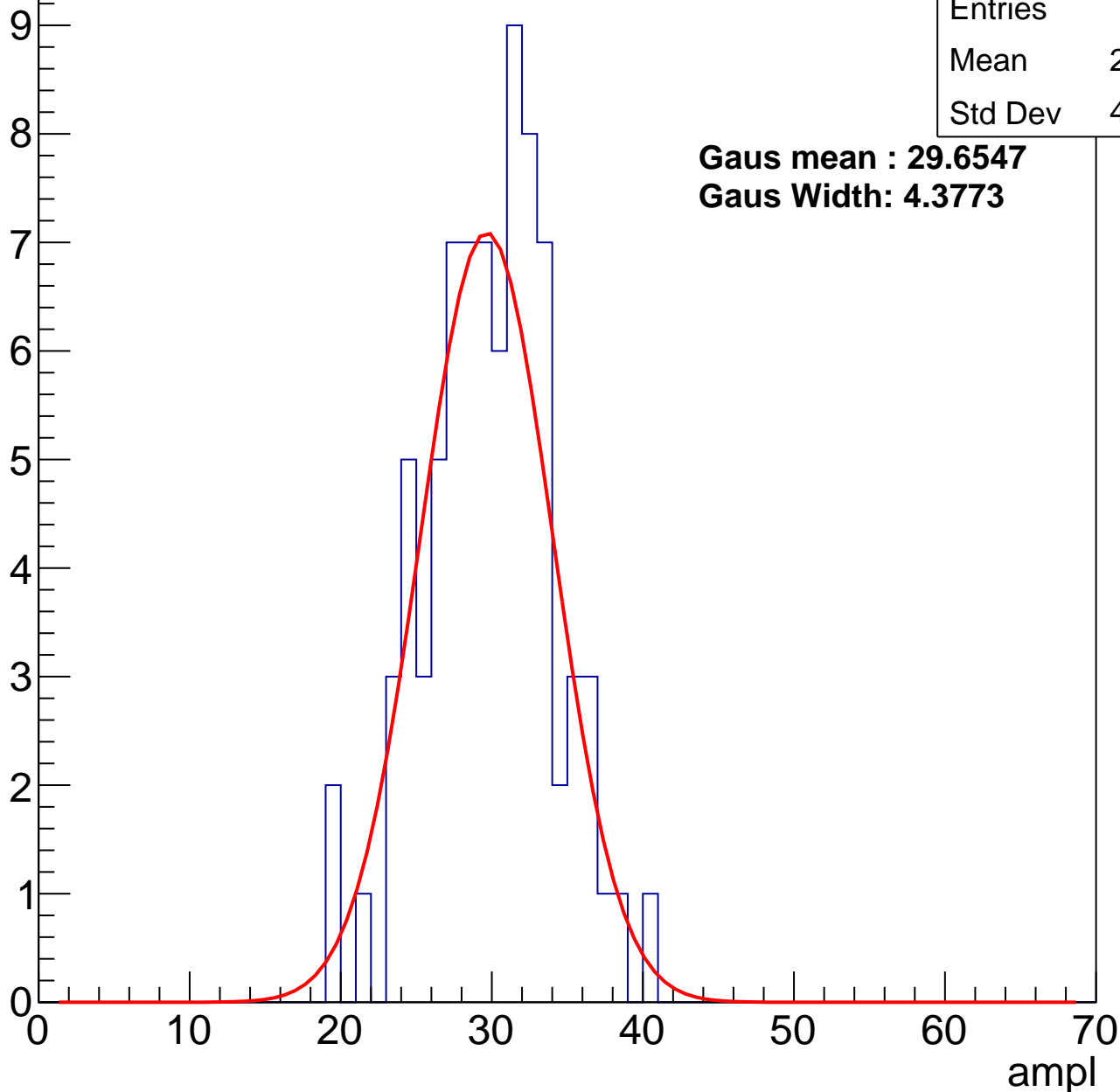
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.42
Std Dev	4.169

Gaus mean : 29.6547

Gaus Width: 4.3773



B1L102S, U13-ch80, adc1

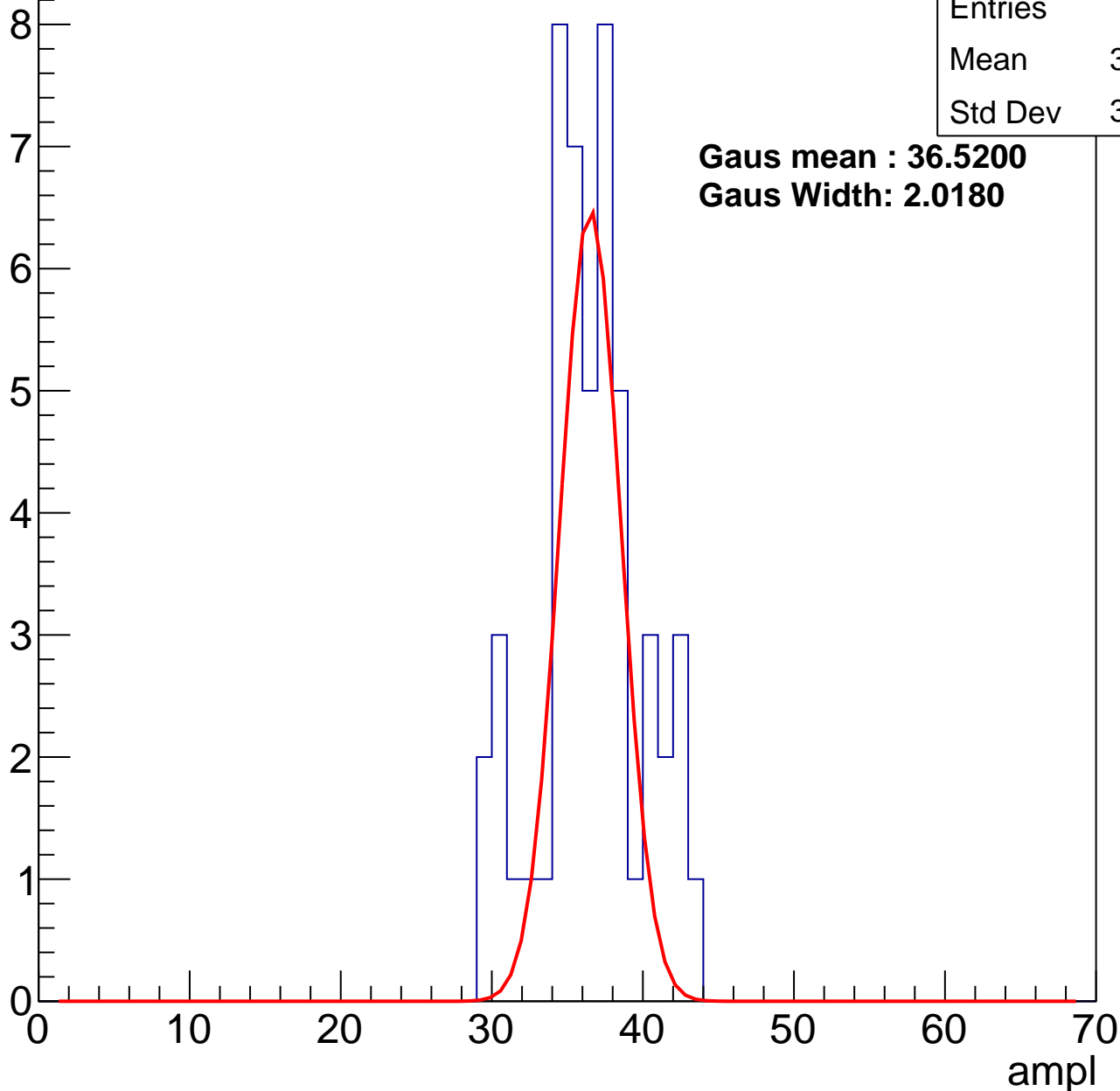
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	36.02
Std Dev	3.393

Gaus mean : 36.5200

Gaus Width: 2.0180



B1L102S, U13-ch80, adc2

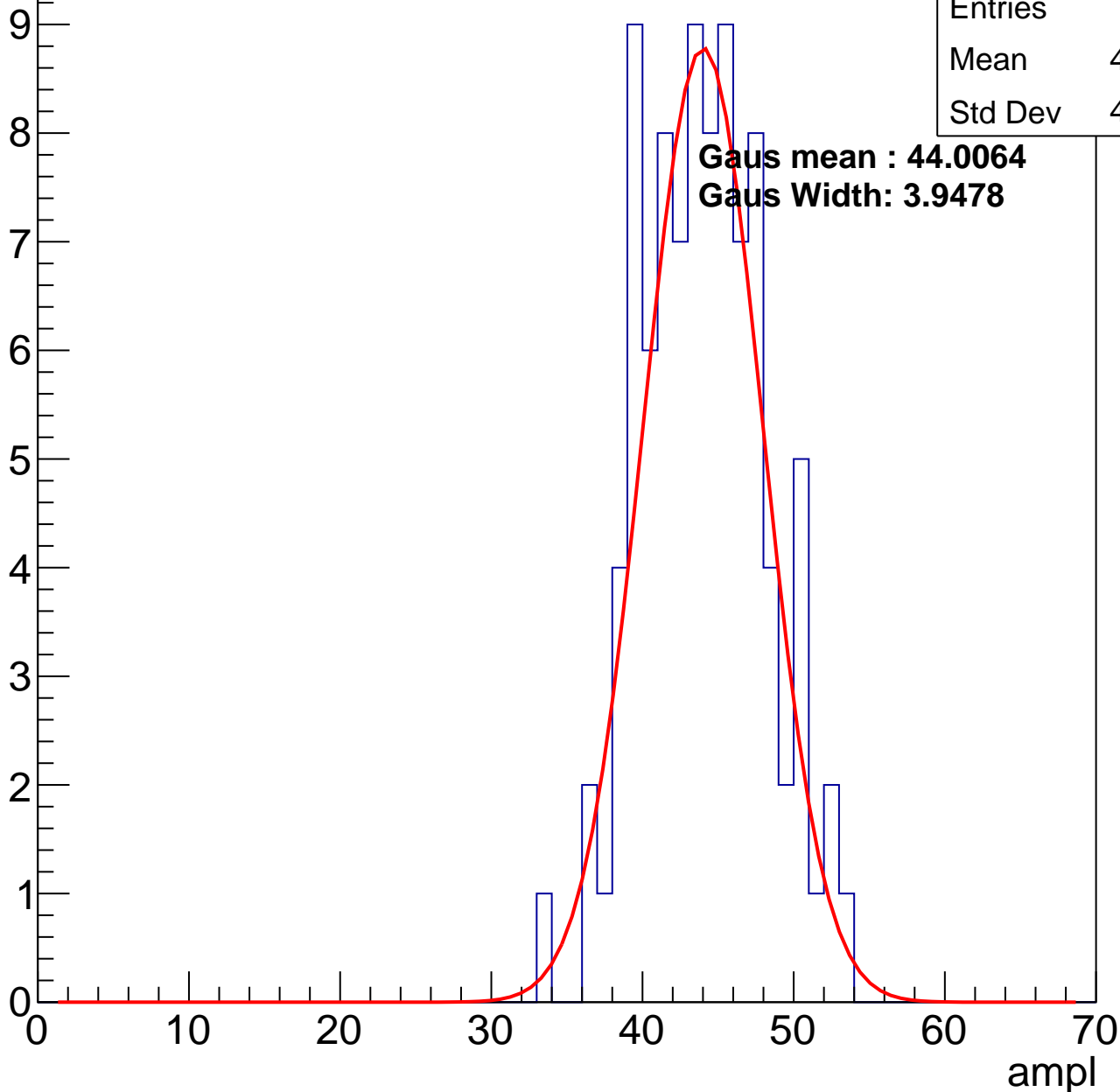
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	94
Mean	43.59
Std Dev	4.017

Gaus mean : 44.0064

Gaus Width: 3.9478

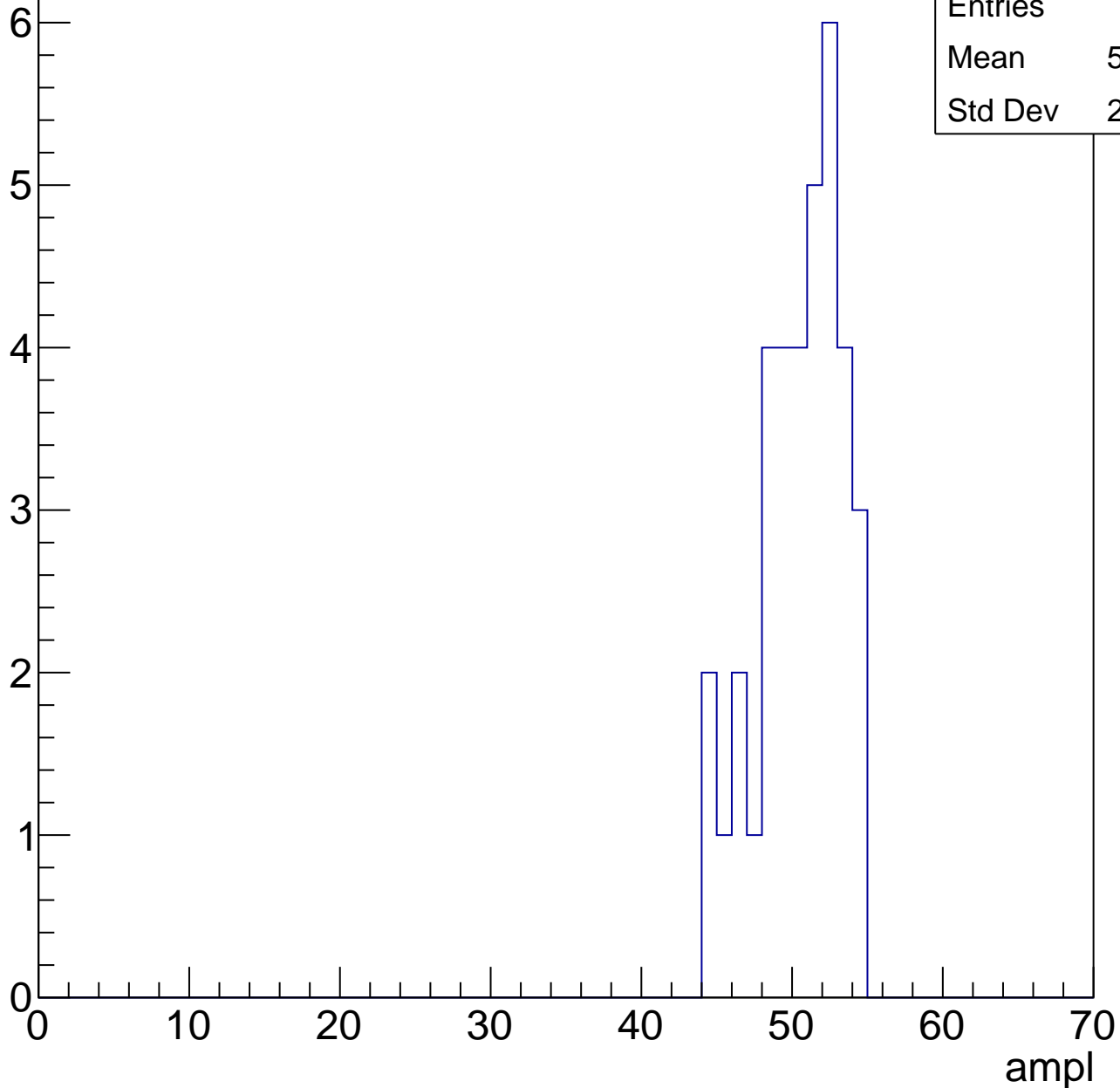


B1L102S, U13-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	50.03
Std Dev	2.744

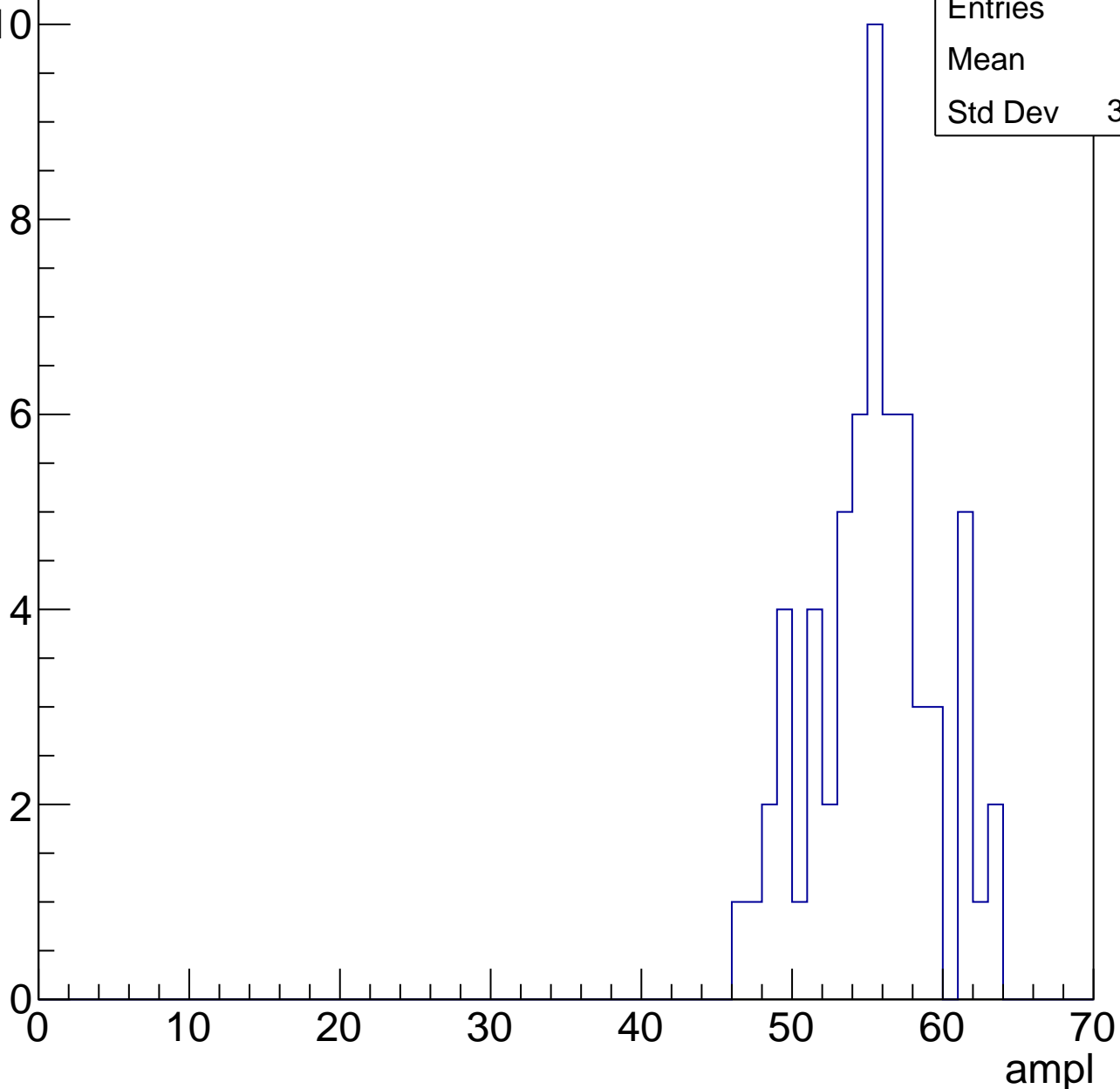


B1L102S, U13-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.9
Std Dev	3.979

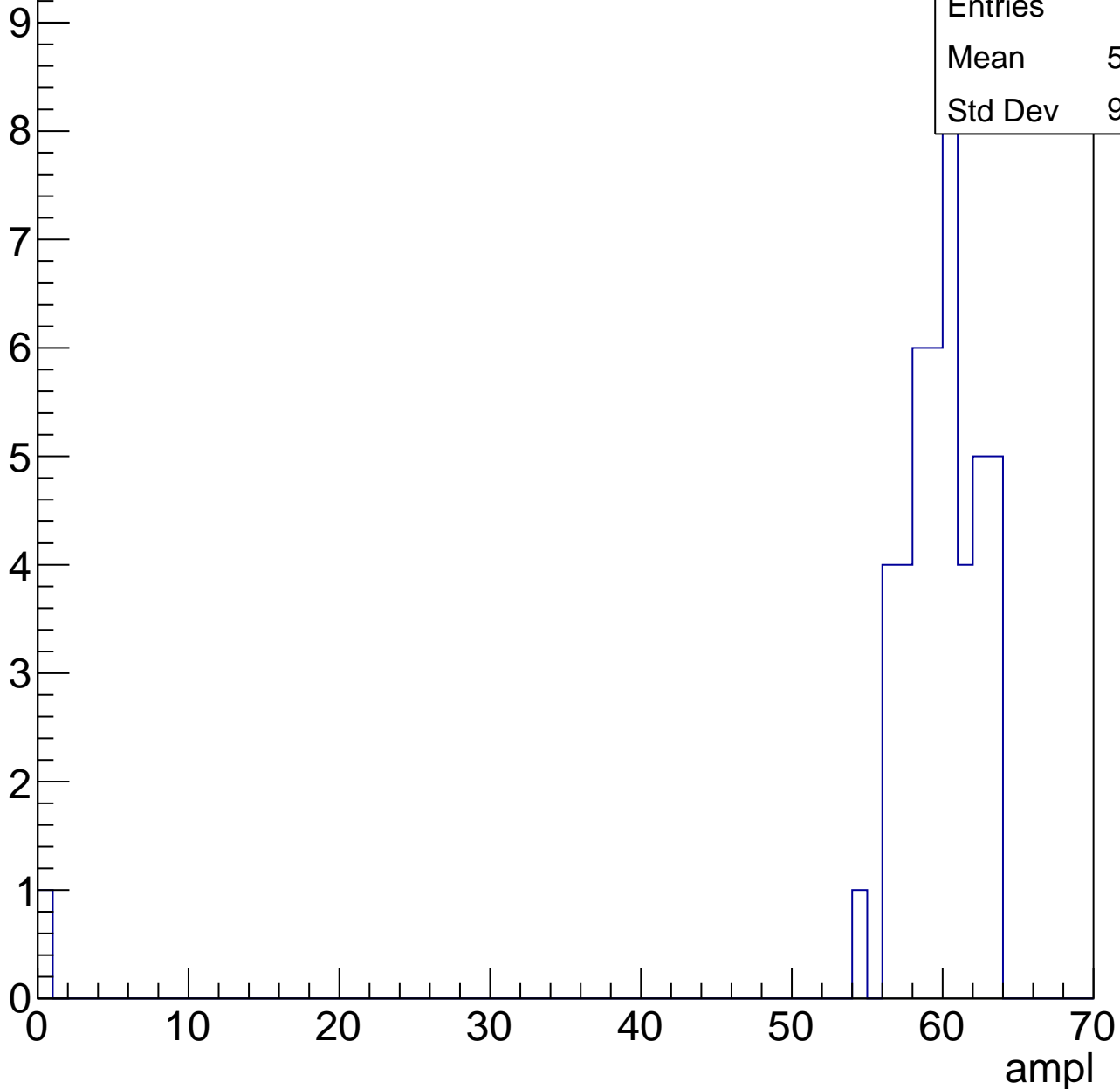


B1L102S, U13-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

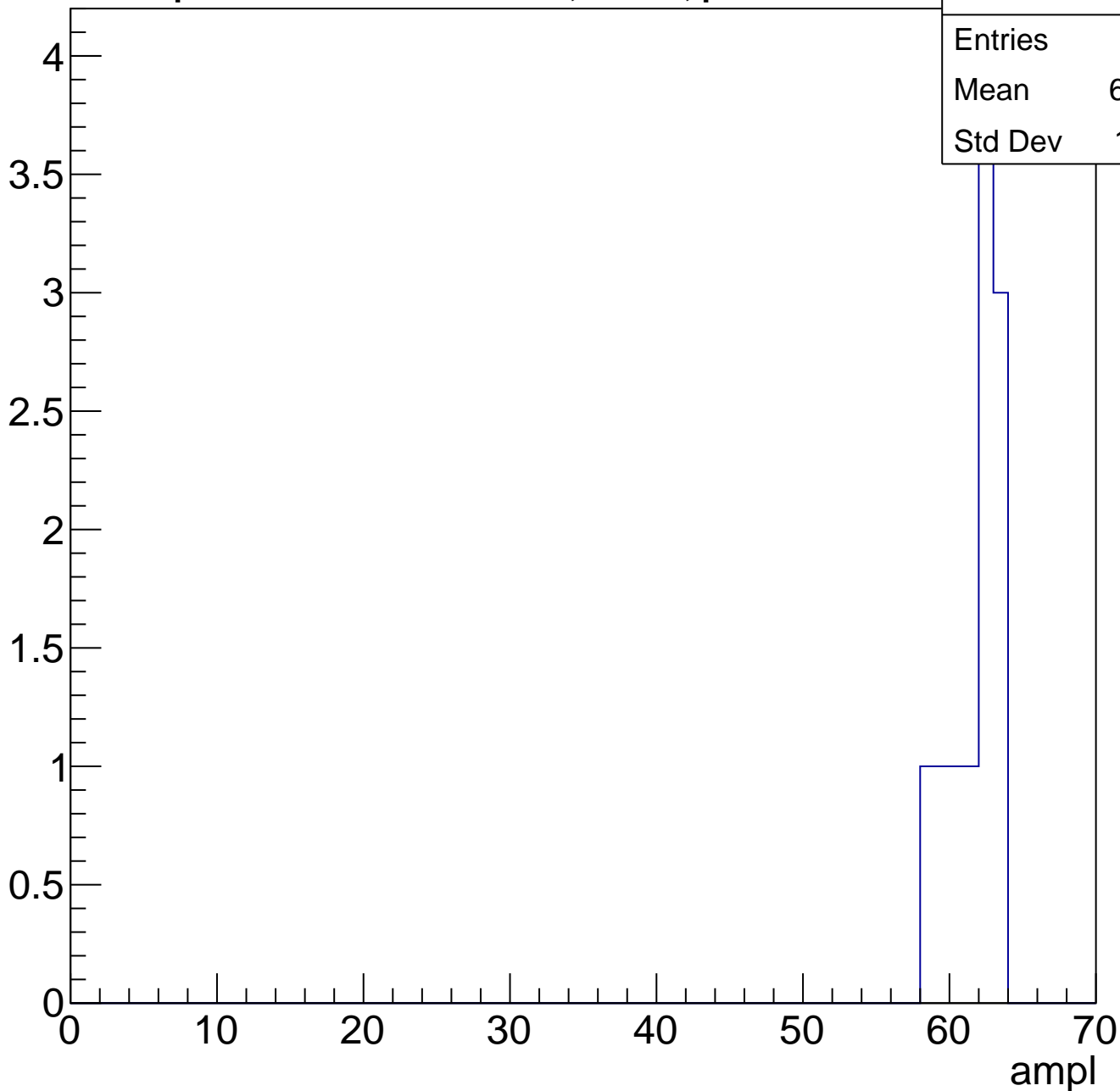
Entries	45
Mean	58.16
Std Dev	9.045



B1L102S, U13-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch81, adc0

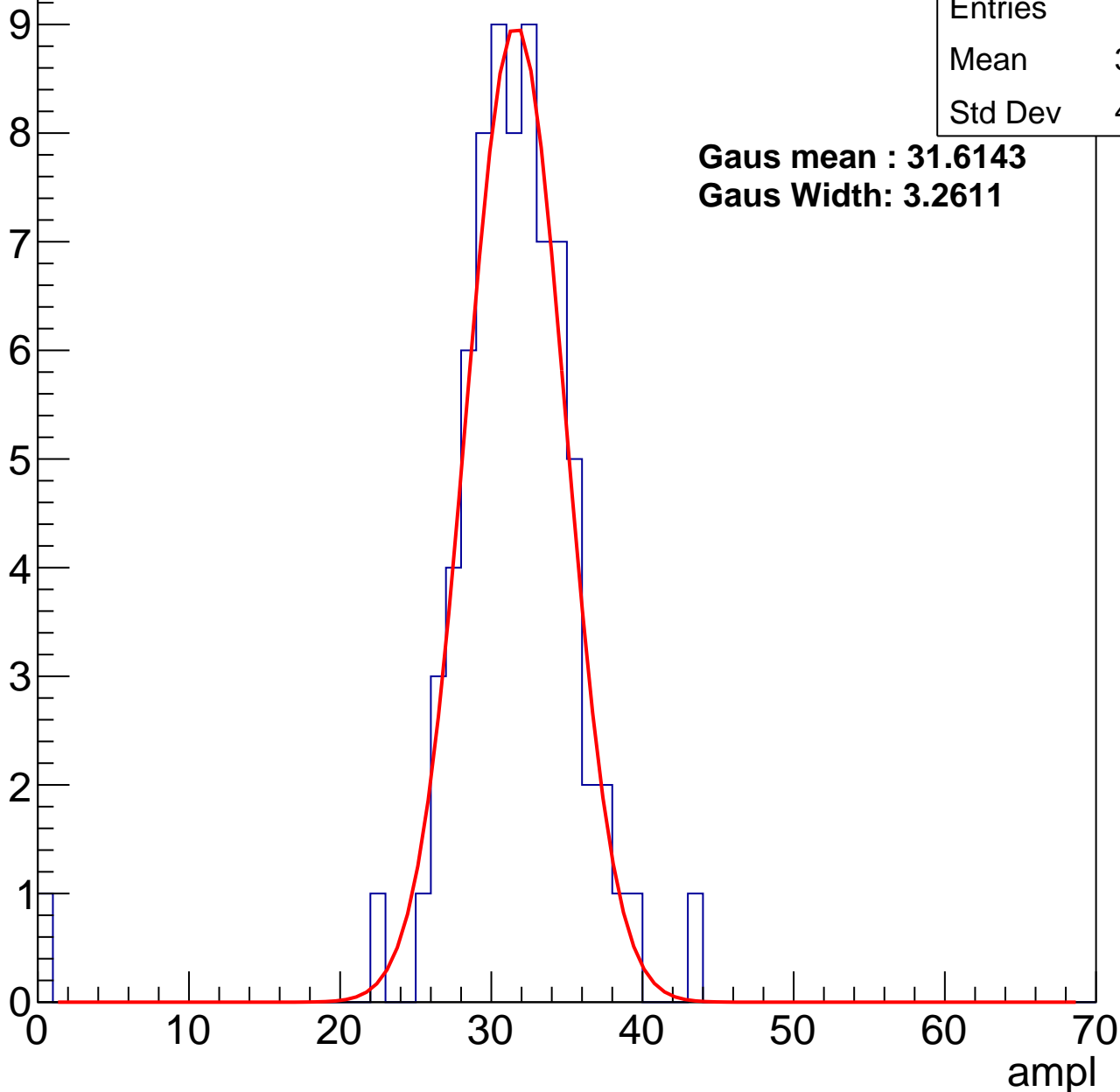
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	30.91
Std Dev	4.961

Gaus mean : 31.6143

Gaus Width: 3.2611



B1L102S, U13-ch81, adc1

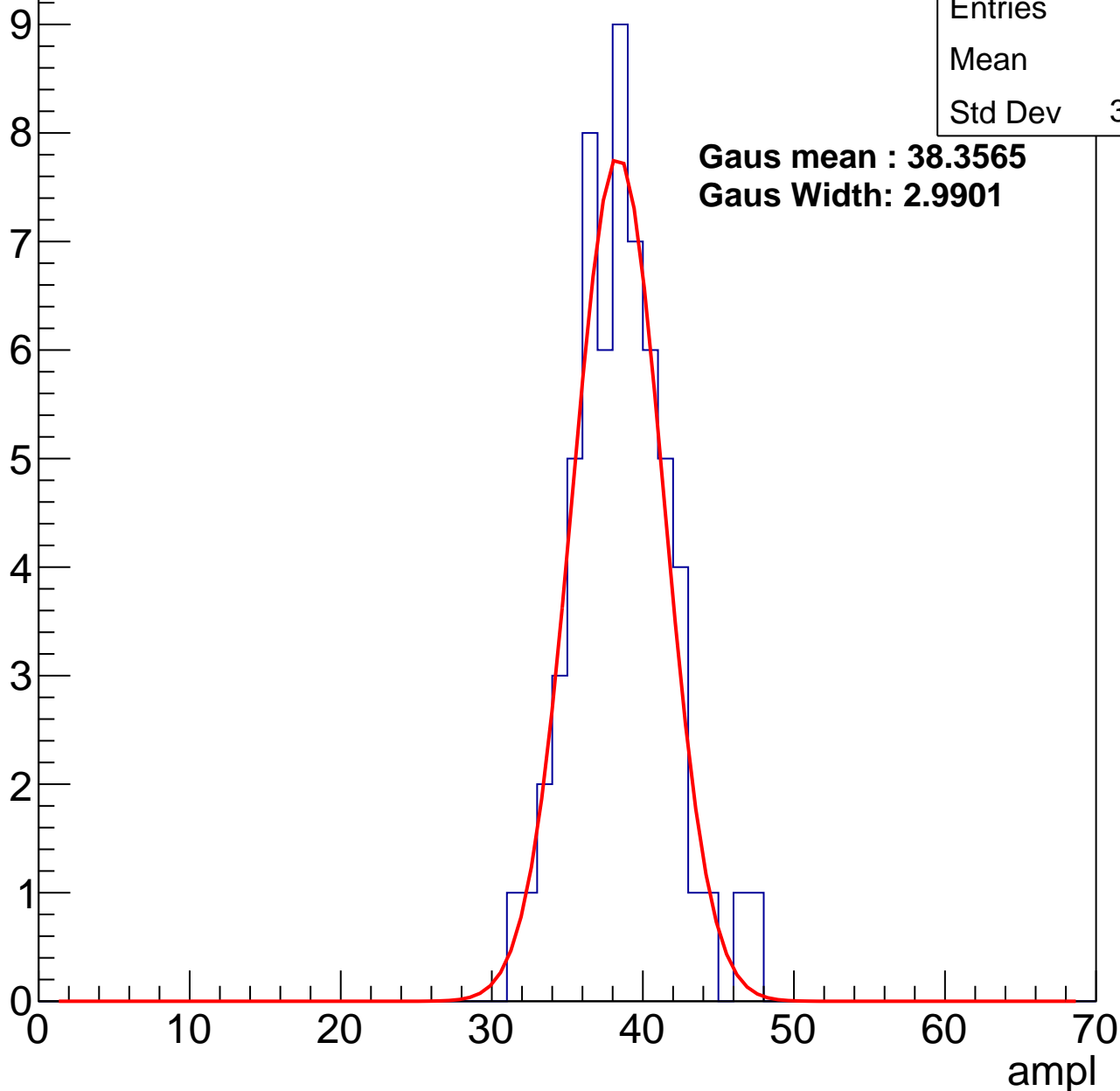
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	38.1
Std Dev	3.166

Gaus mean : 38.3565

Gaus Width: 2.9901



B1L102S, U13-ch81, adc2

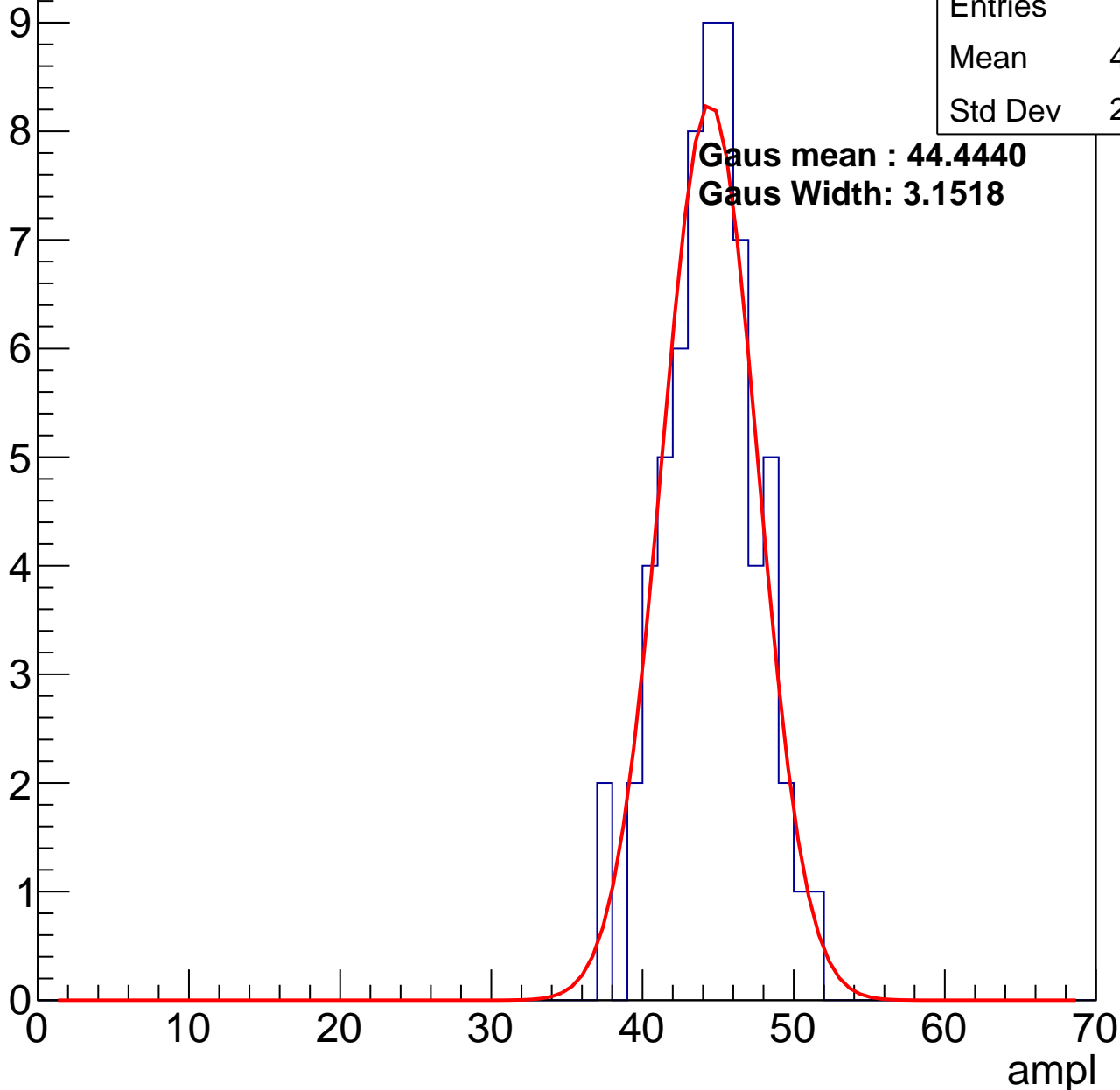
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.05
Std Dev	2.979

Gaus mean : 44.4440

Gaus Width: 3.1518



B1L102S, U13-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

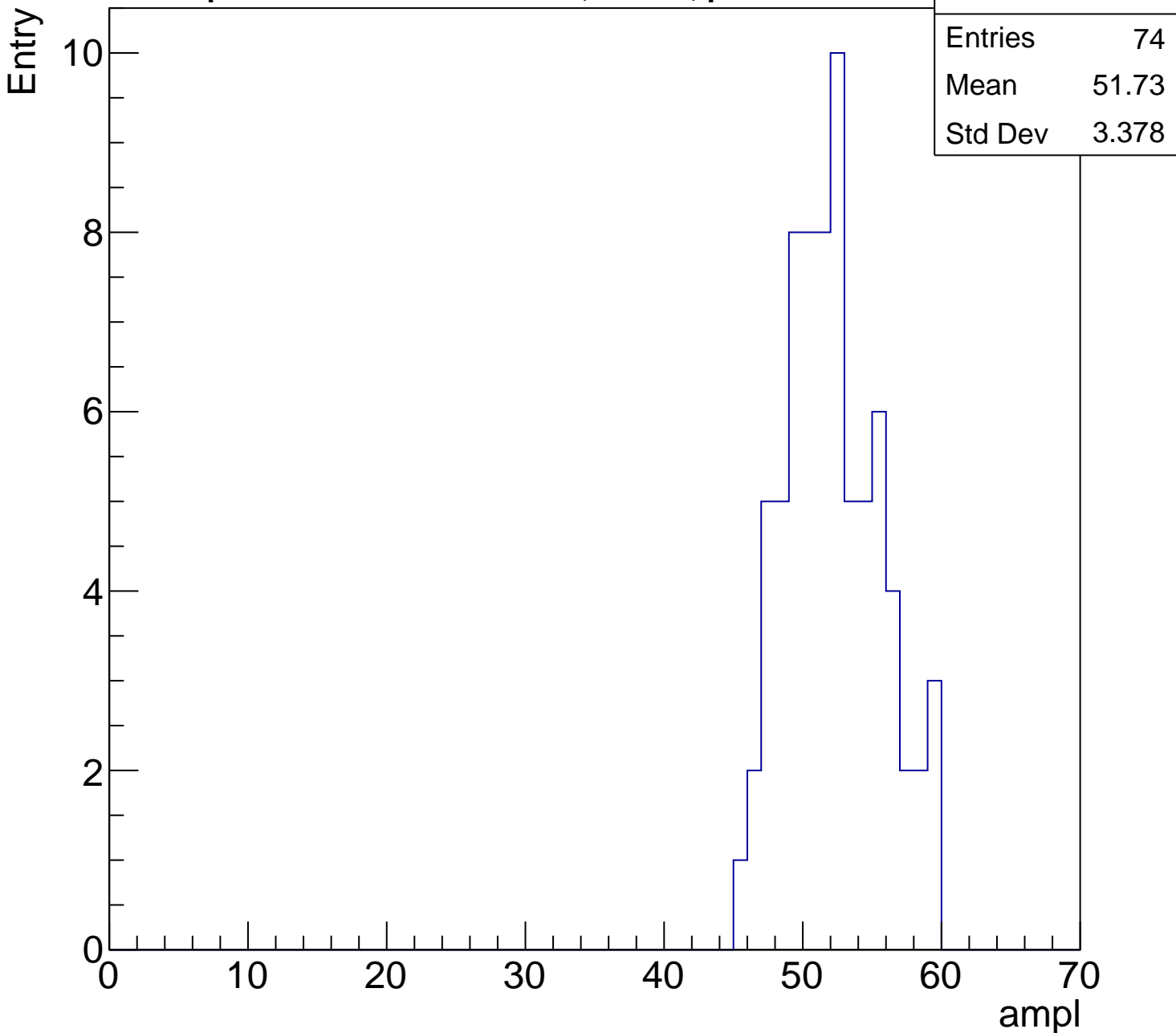
Entries	74
Mean	51.73
Std Dev	3.378

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

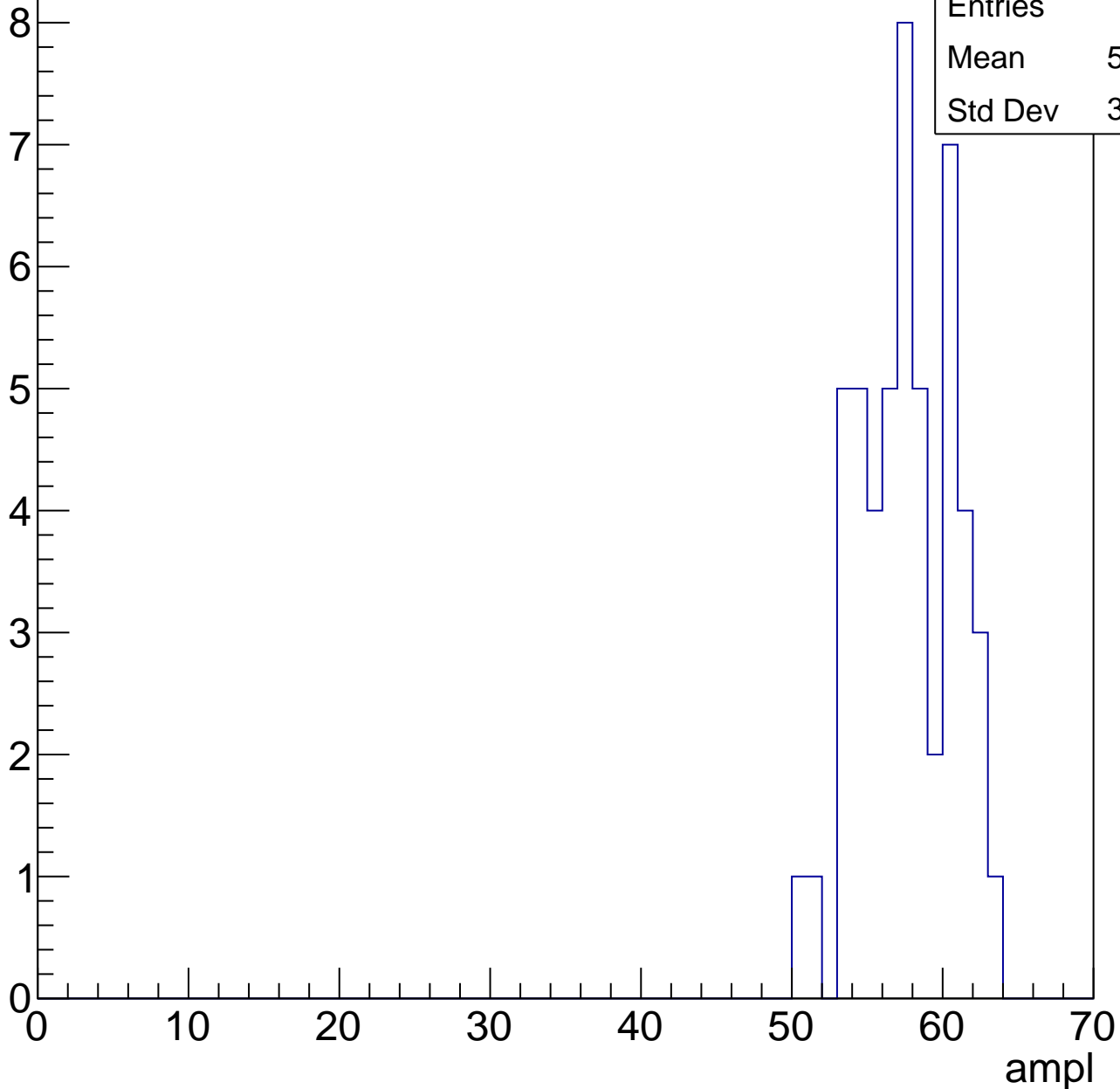


B1L102S, U13-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.12
Std Dev	3.072

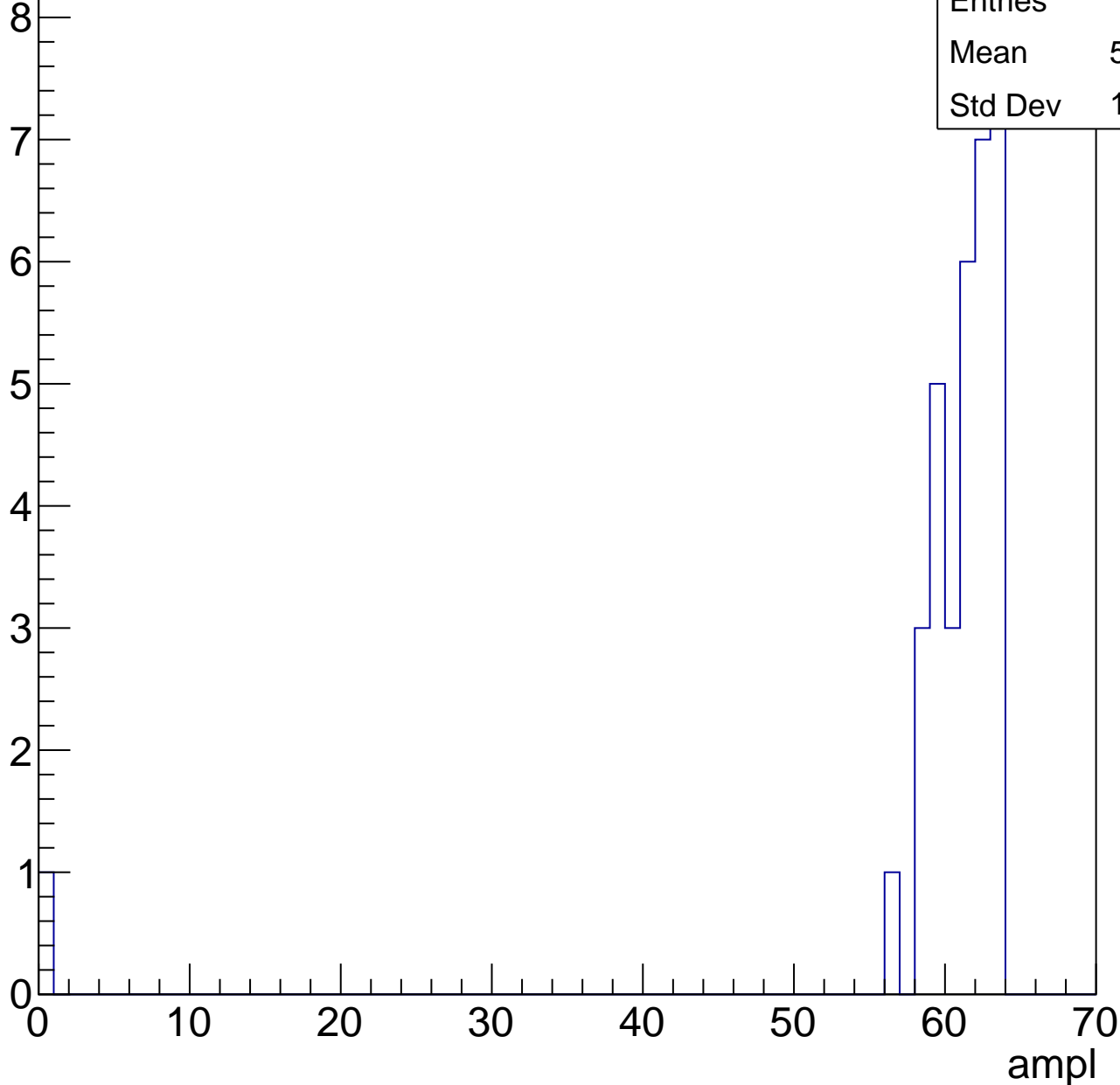


B1L102S, U13-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	59.09
Std Dev	10.45



B1L102S, U13-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch82, adc0

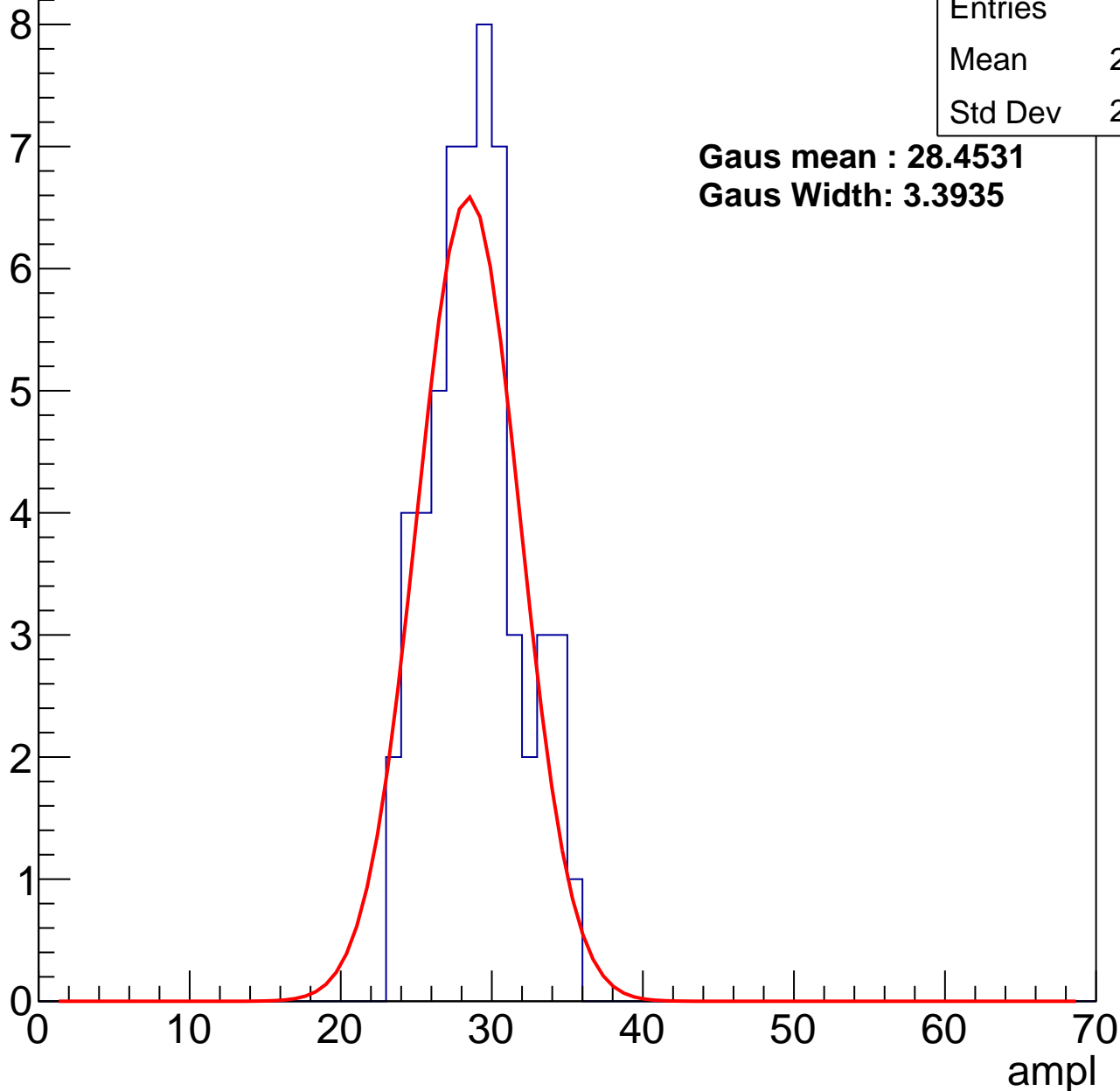
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.43
Std Dev	2.969

Gaus mean : 28.4531

Gaus Width: 3.3935



B1L102S, U13-ch82, adc1

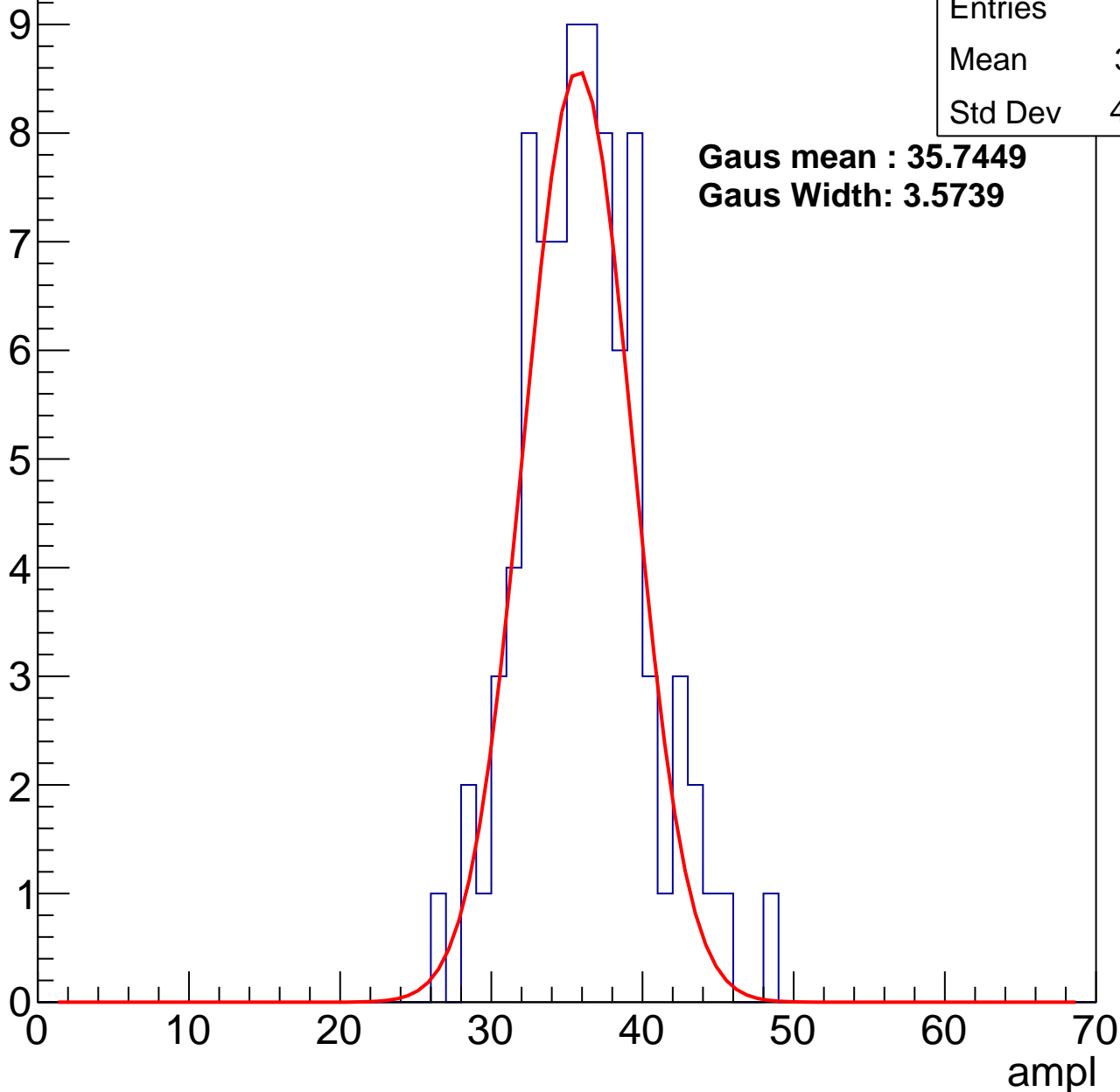
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	35.71
Std Dev	4.026

Gaus mean : 35.7449

Gaus Width: 3.5739



B1L102S, U13-ch82, adc2

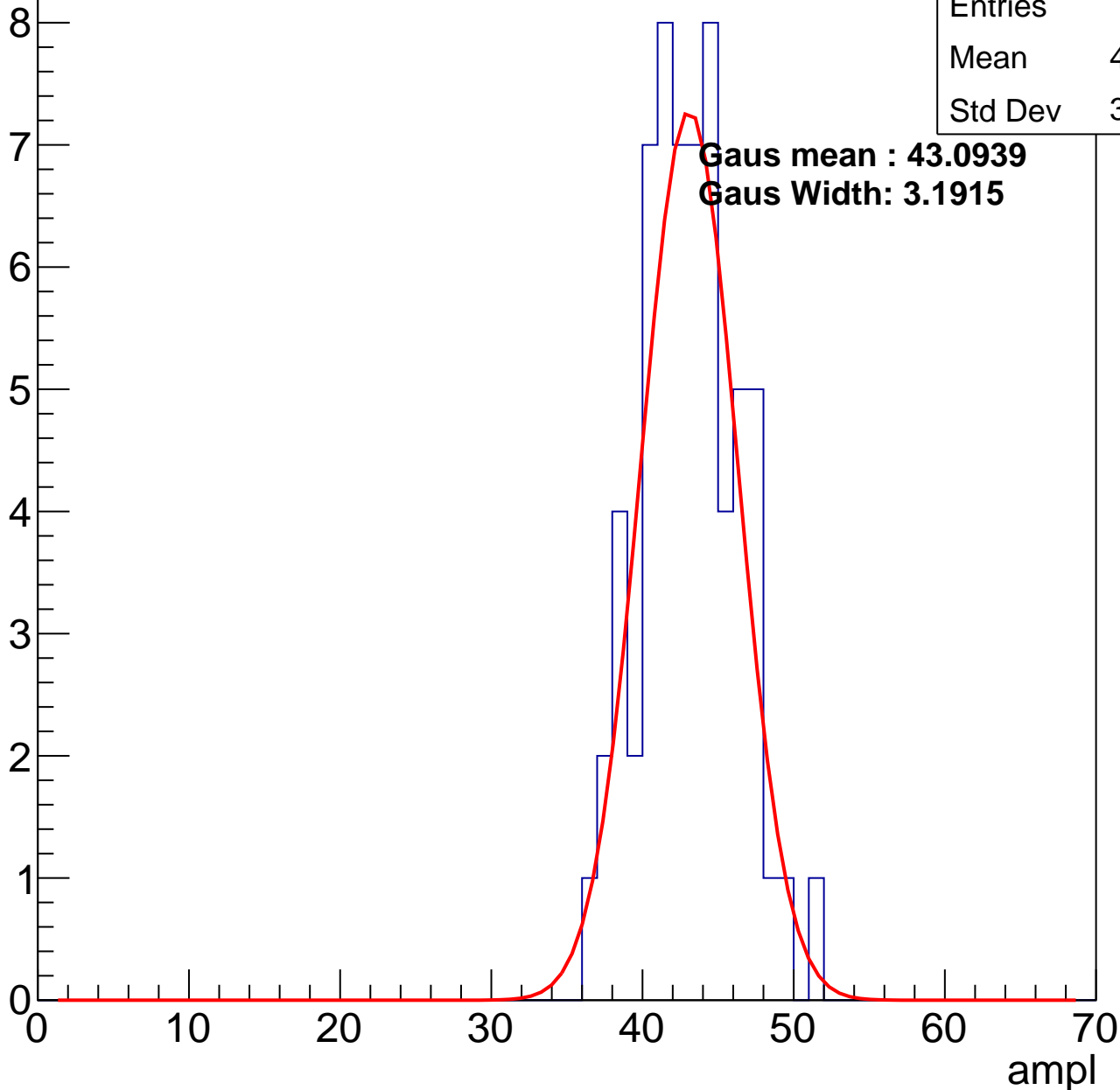
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.67
Std Dev	3.142

Gaus mean : 43.0939

Gaus Width: 3.1915

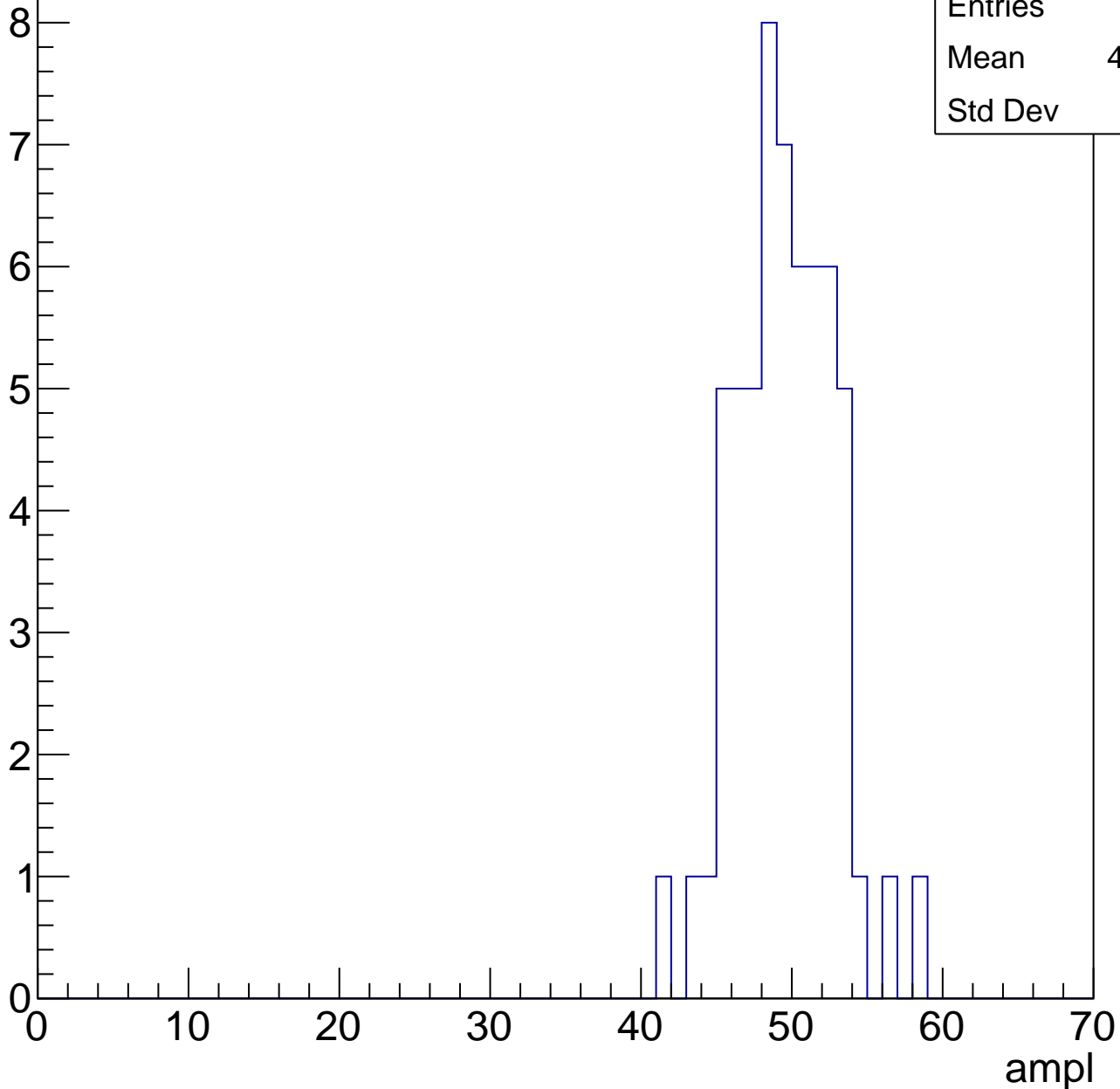


B1L102S, U13-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.08
Std Dev	3.18



B1L102S, U13-ch82, adc4

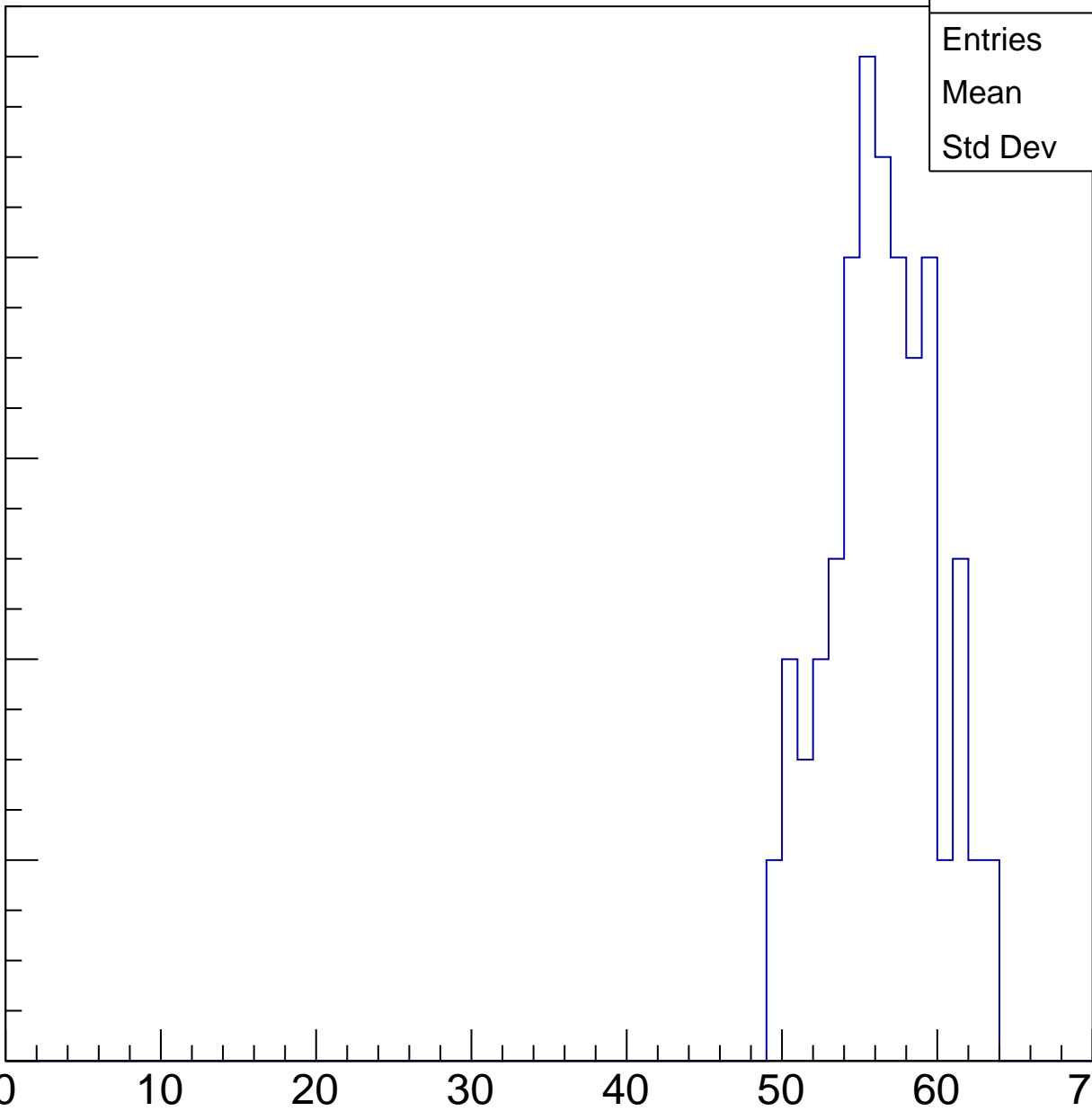
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	55.94
Std Dev	3.38

Entry

10
8
6
4
2
0

ampl

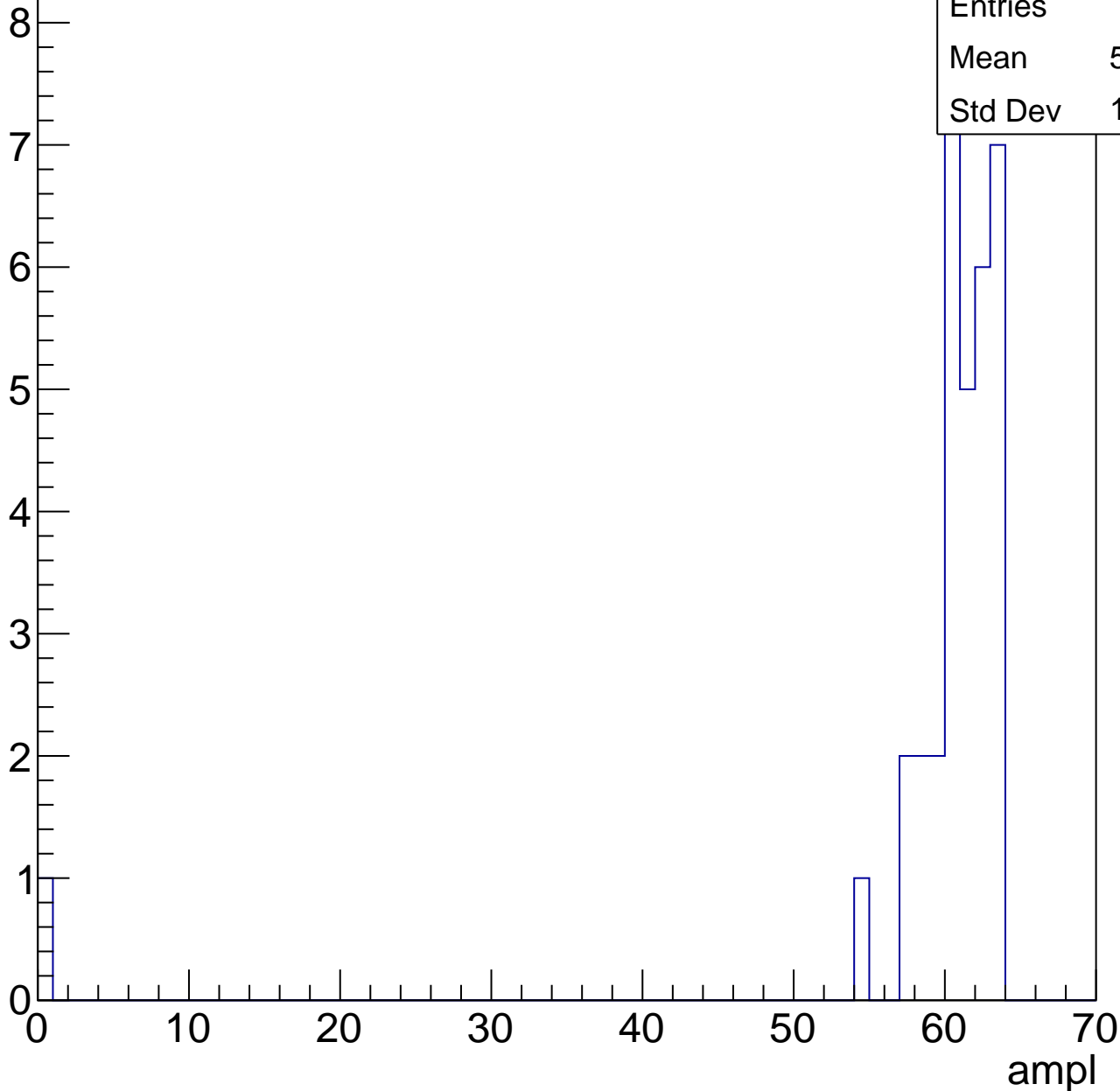


B1L102S, U13-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	58.82
Std Dev	10.44



B1L102S, U13-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

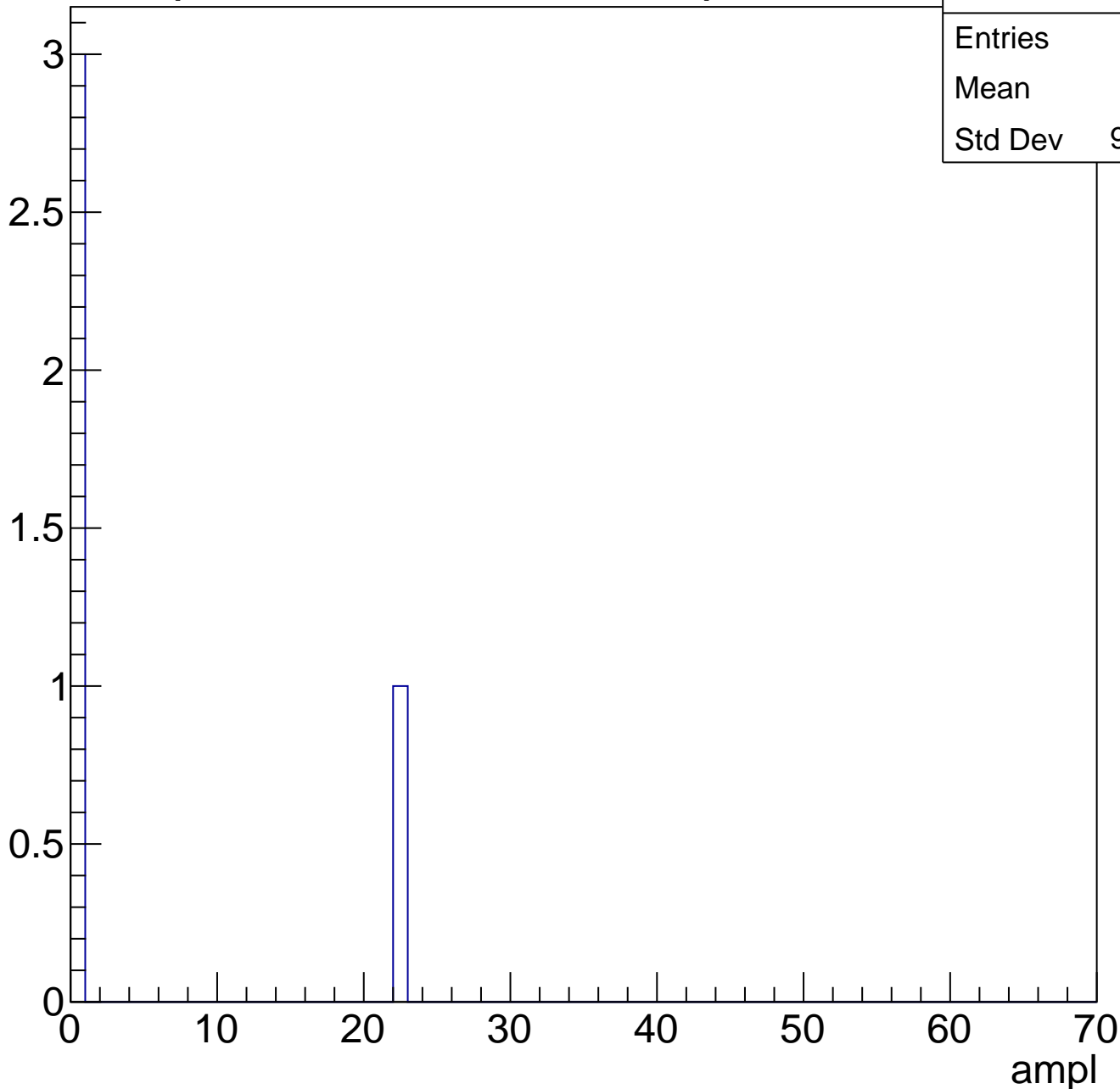
Entry



B1L102S, U13-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	5.5
Std Dev	9.526

B1L102S, U13-ch83, adc0

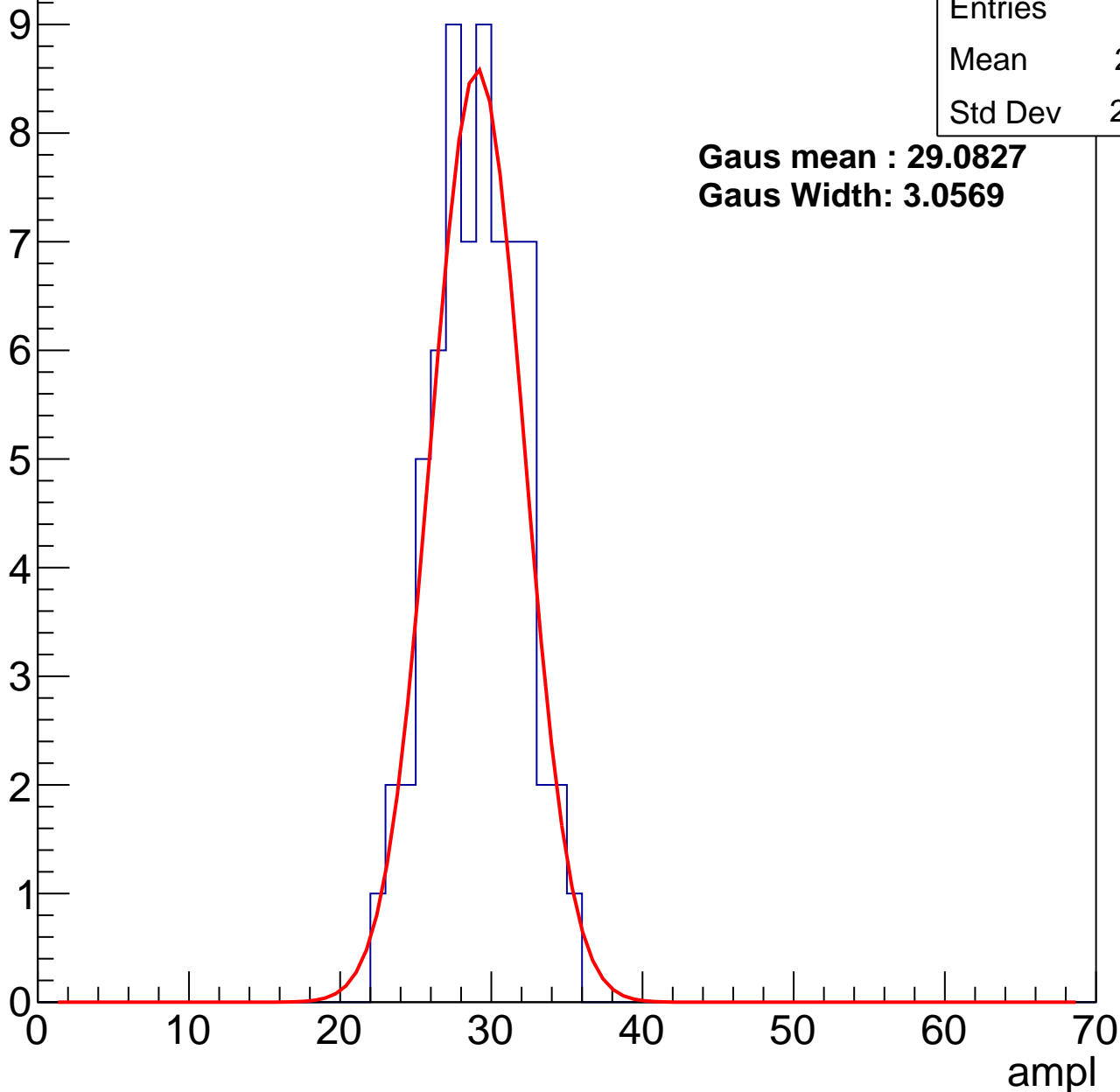
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.61
Std Dev	2.875

Gaus mean : 29.0827

Gaus Width: 3.0569



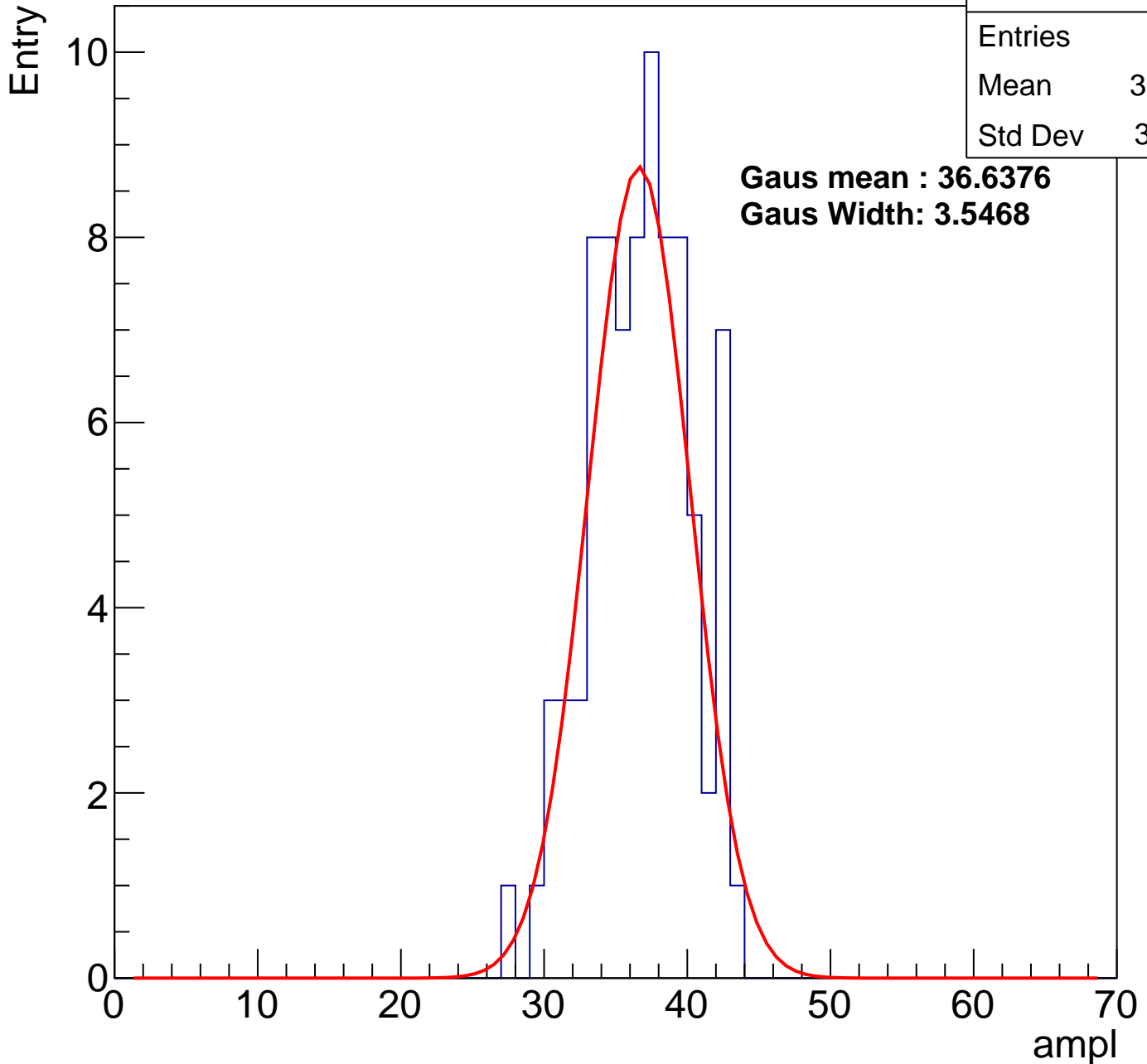
B1L102S, U13-ch83, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	36.25
Std Dev	3.491

Gaus mean : 36.6376

Gaus Width: 3.5468



B1L102S, U13-ch83, adc2

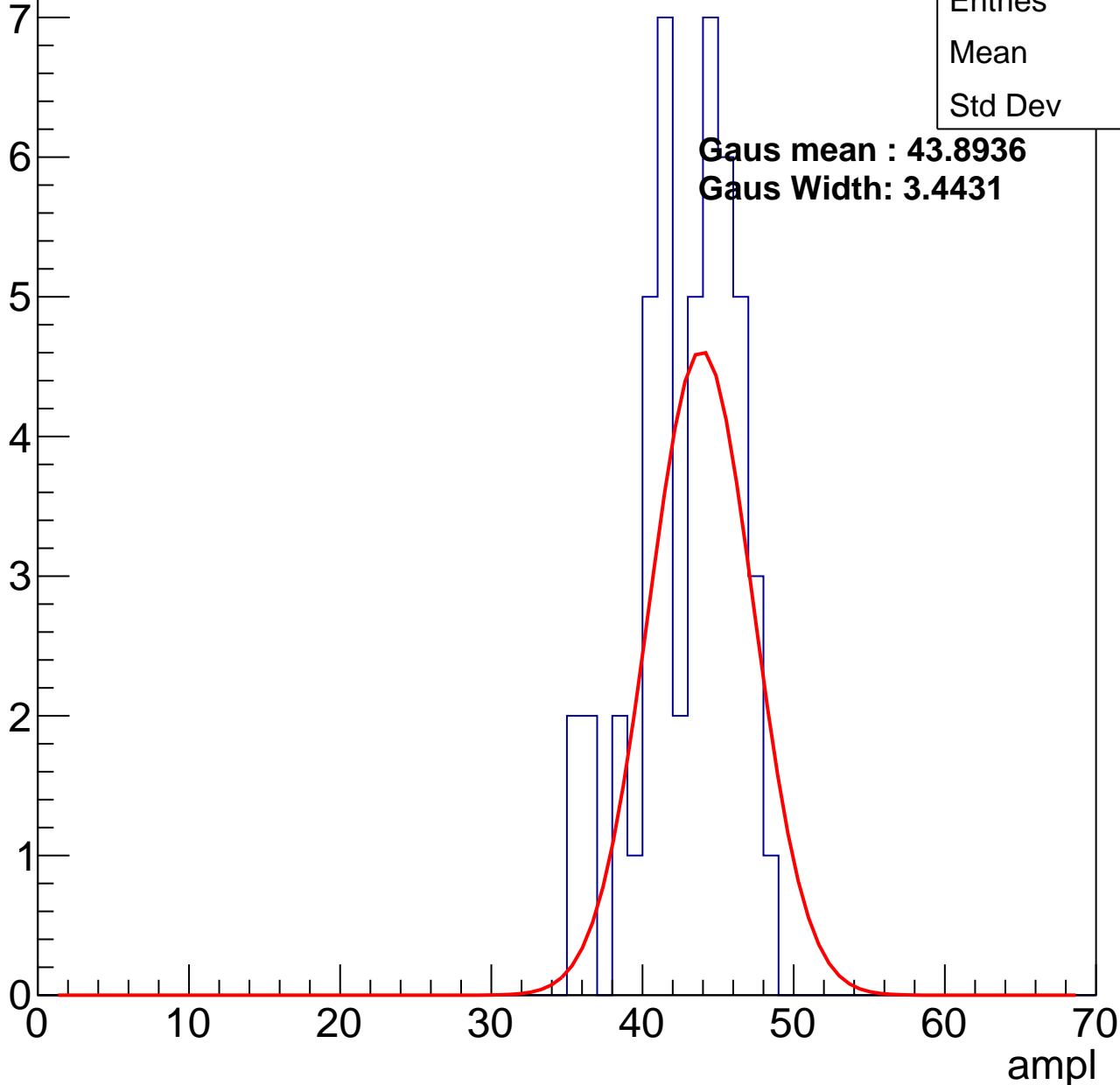
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	42.5
Std Dev	3.24

Gaus mean : 43.8936

Gaus Width: 3.4431



B1L102S, U13-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	48.64
Std Dev	3.516

Entry

10

8

6

4

2

0

0

10

20

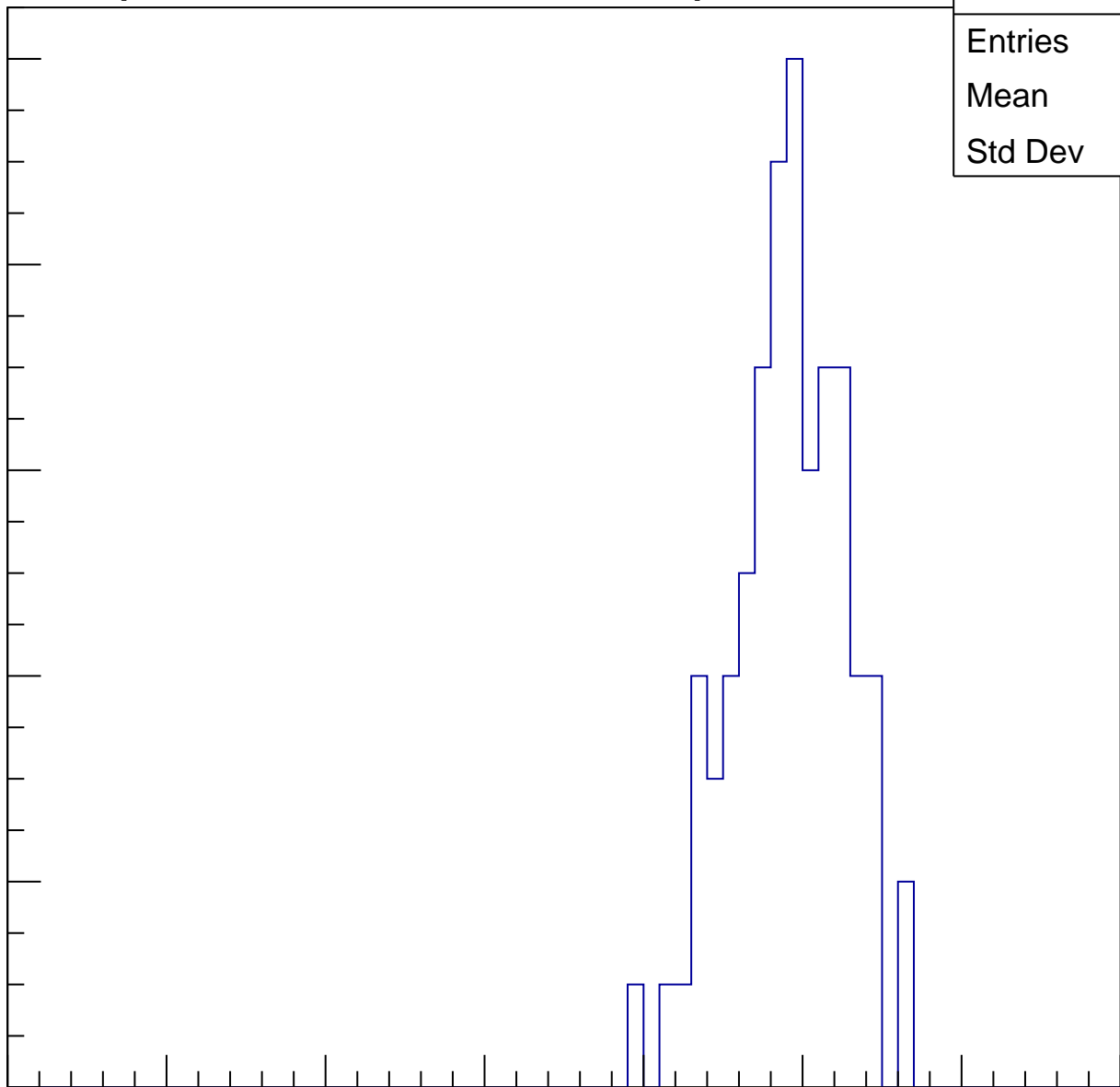
30

40

50

60

ampl

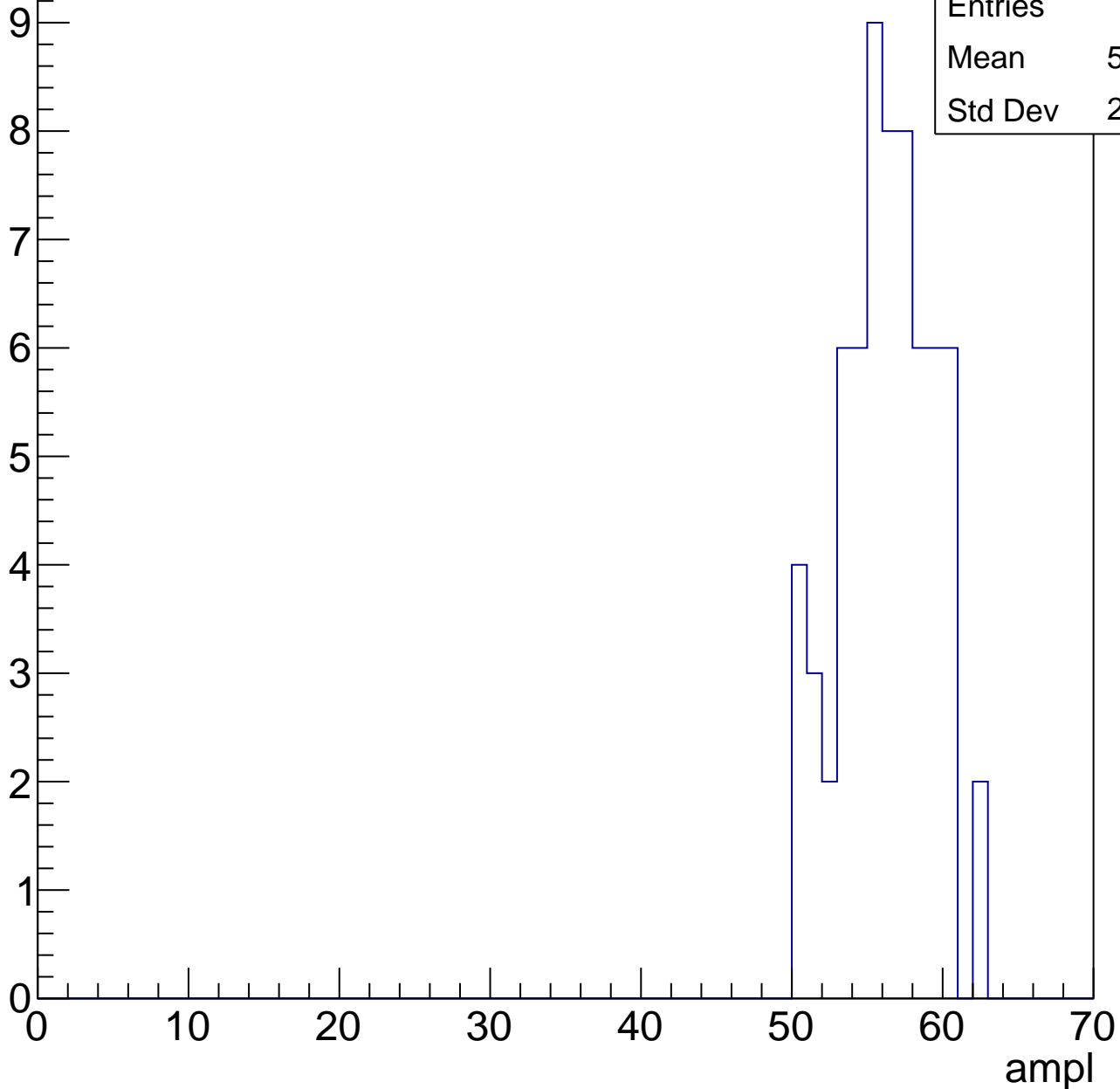


B1L102S, U13-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.82
Std Dev	2.989

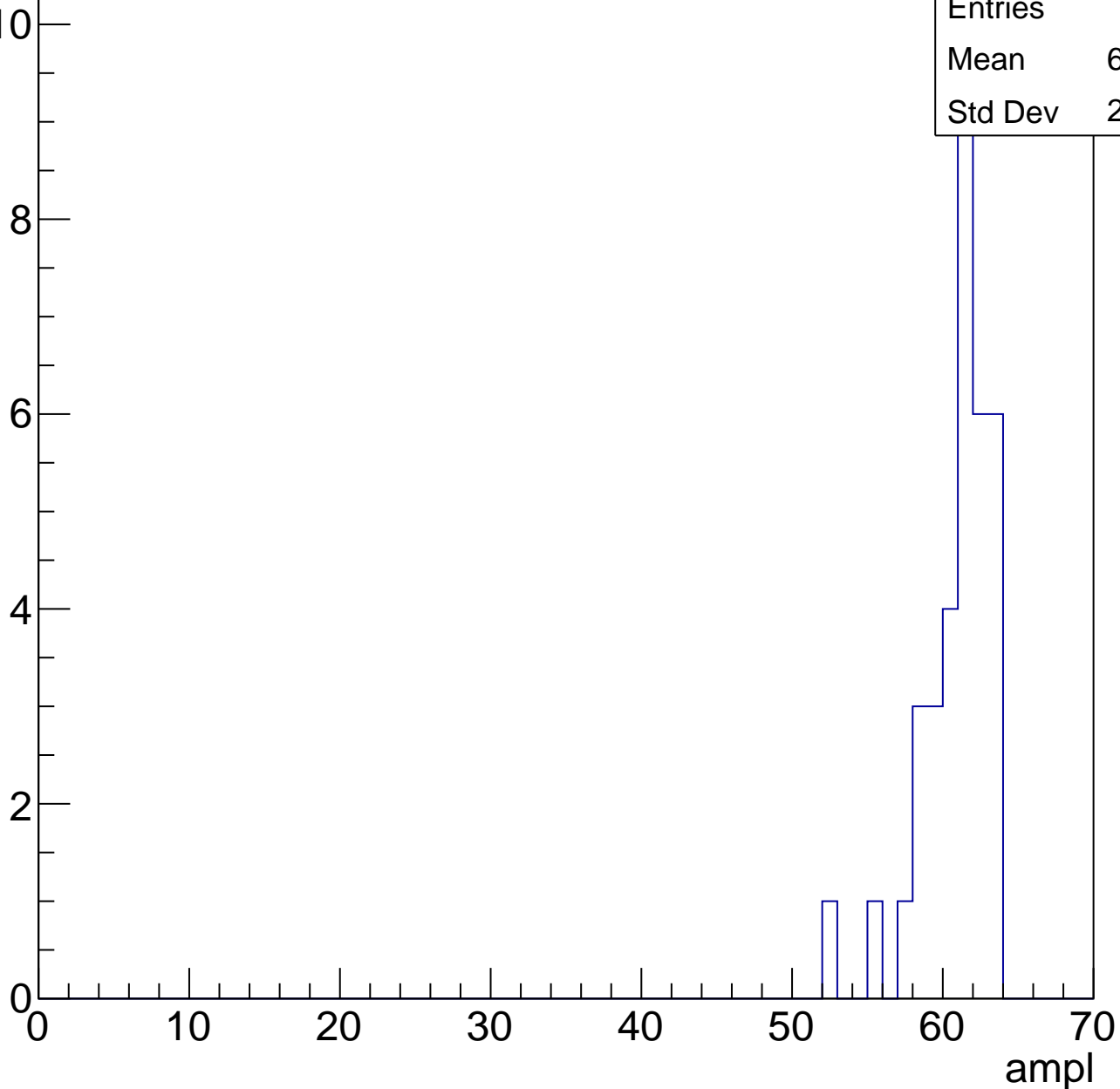


B1L102S, U13-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

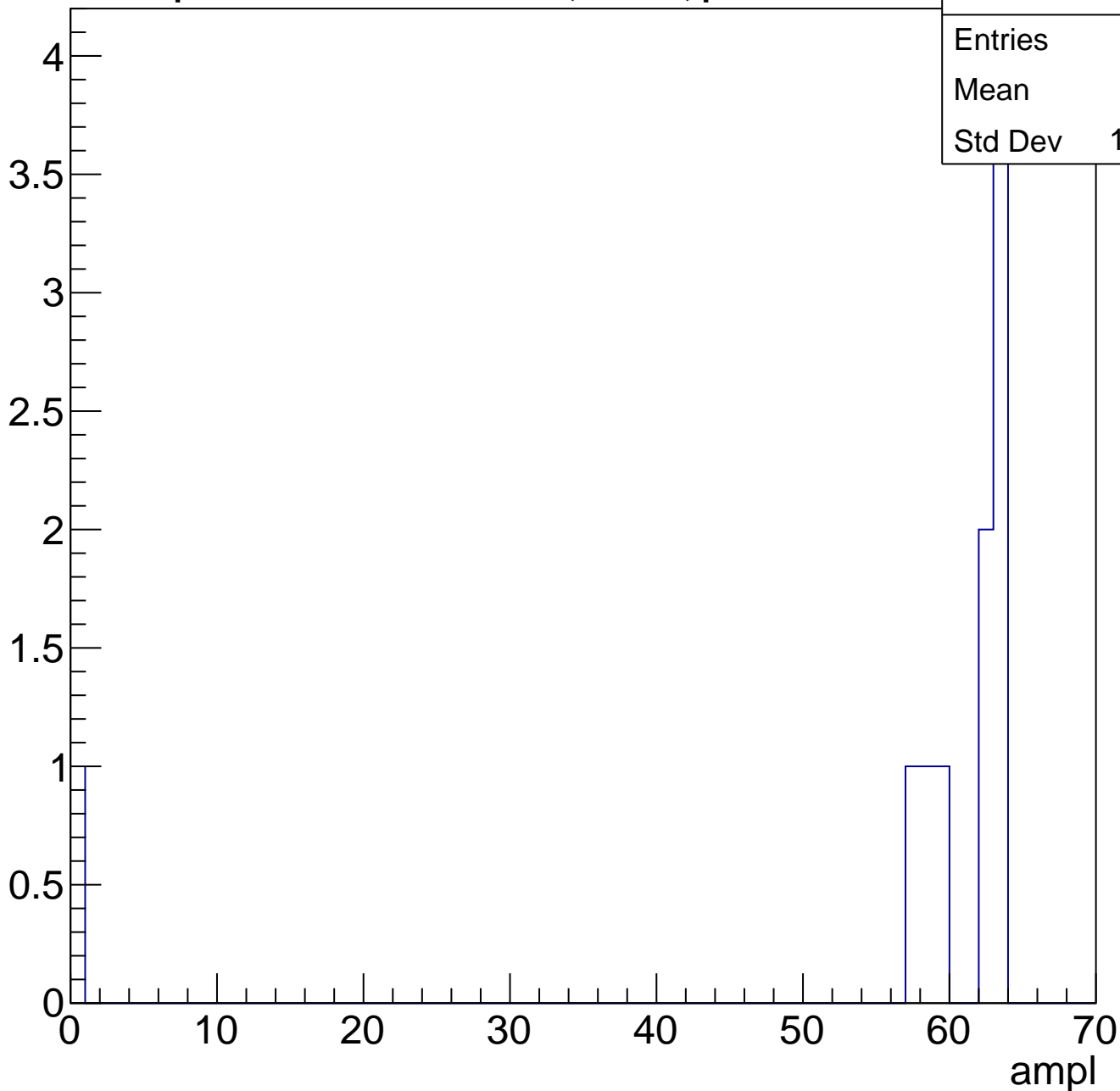
Entries	35
Mean	60.43
Std Dev	2.358



B1L102S, U13-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch84, adc0

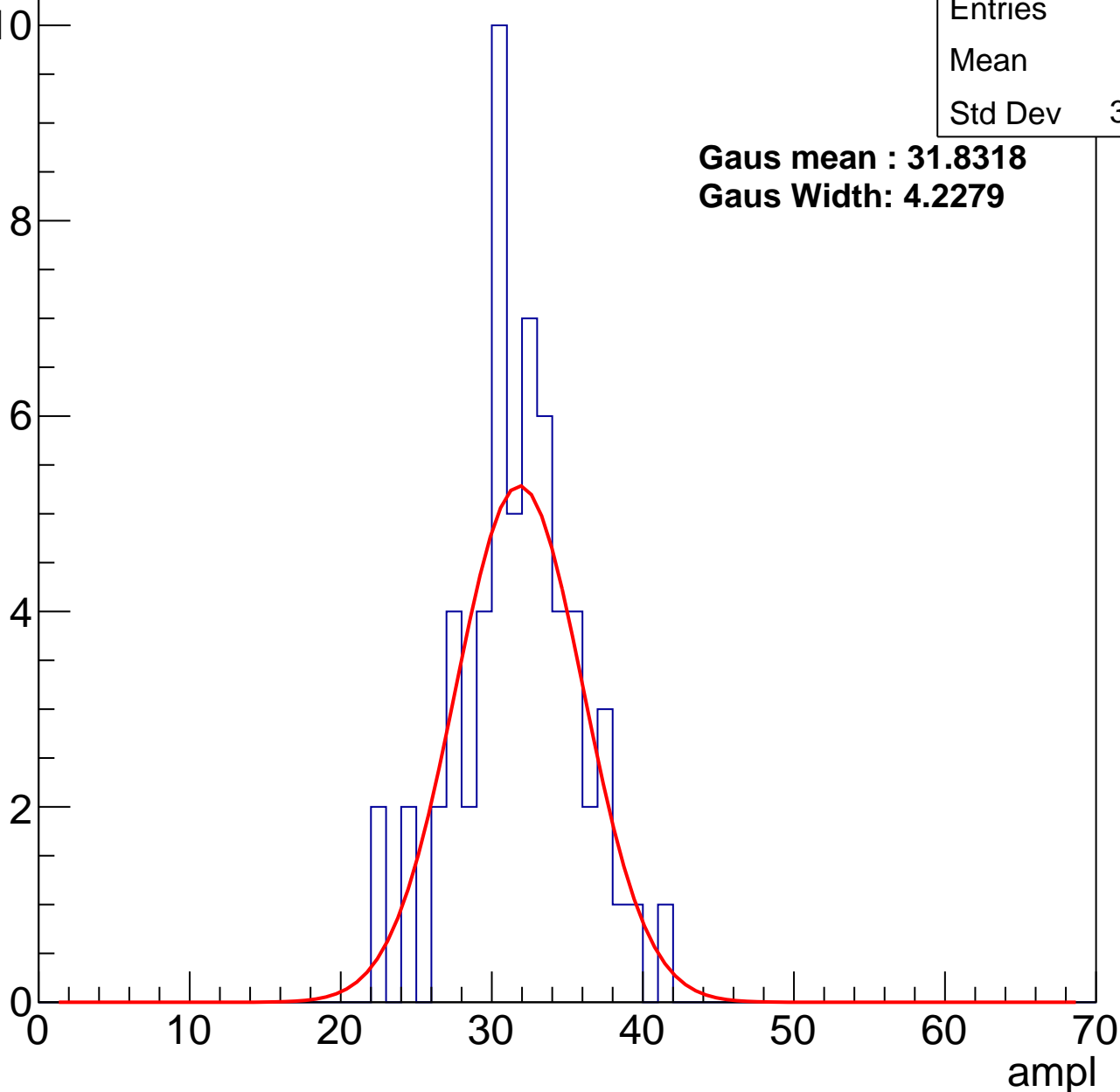
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	31.3
Std Dev	3.904

Gaus mean : 31.8318

Gaus Width: 4.2279



B1L102S, U13-ch84, adc1

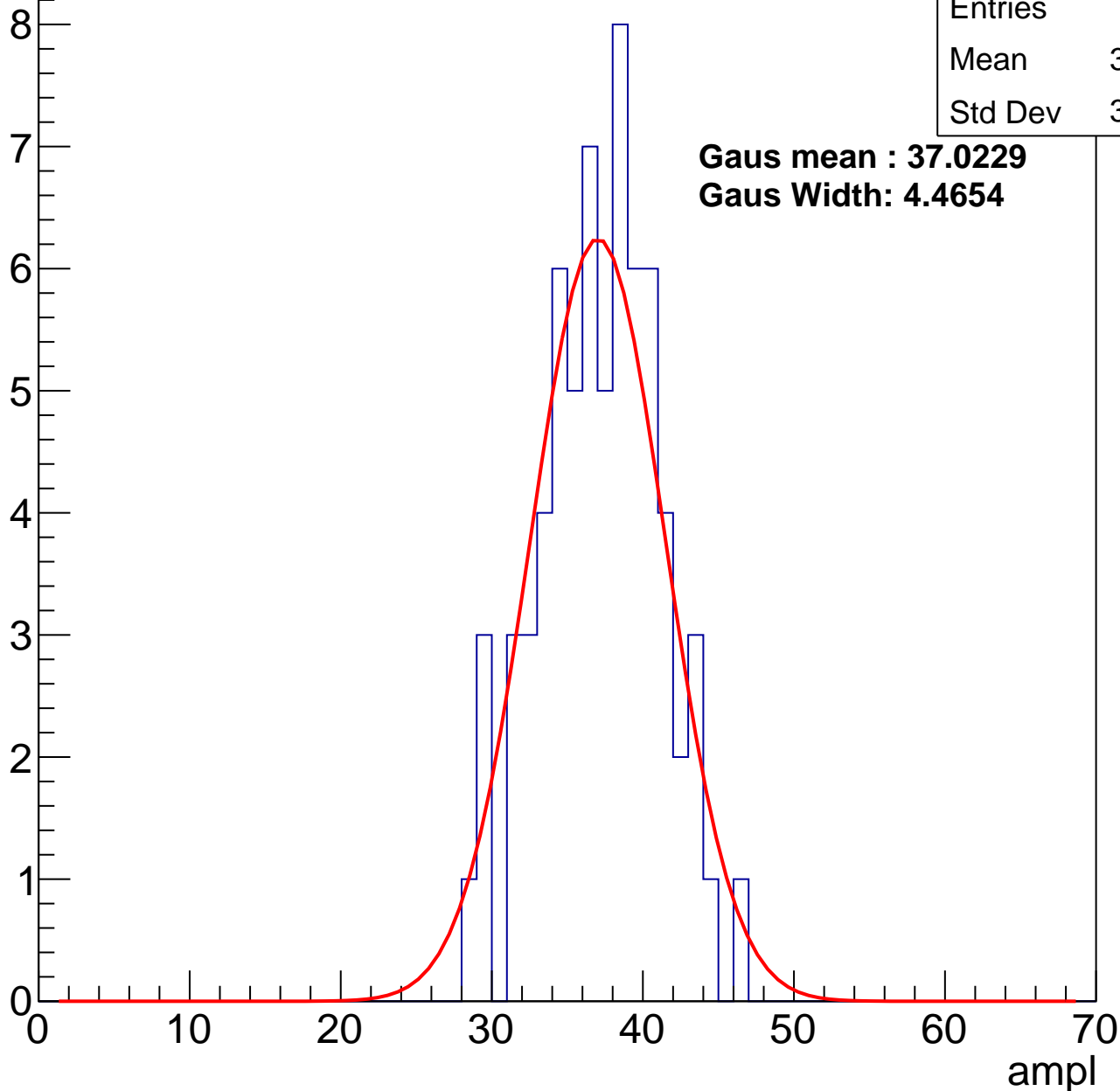
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.72
Std Dev	3.899

Gaus mean : 37.0229

Gaus Width: 4.4654



B1L102S, U13-ch84, adc2

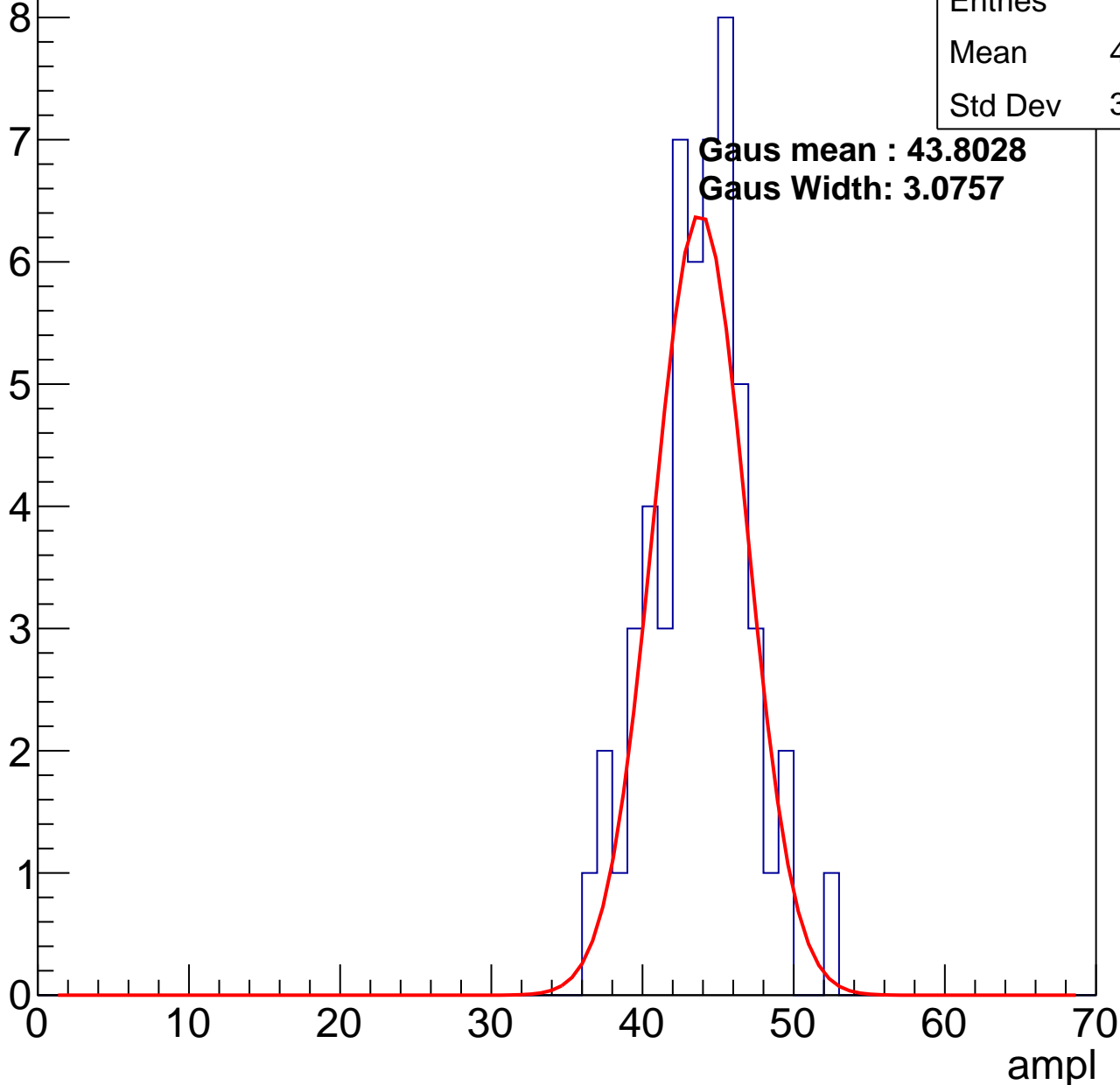
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	43.28
Std Dev	3.205

Gaus mean : 43.8028

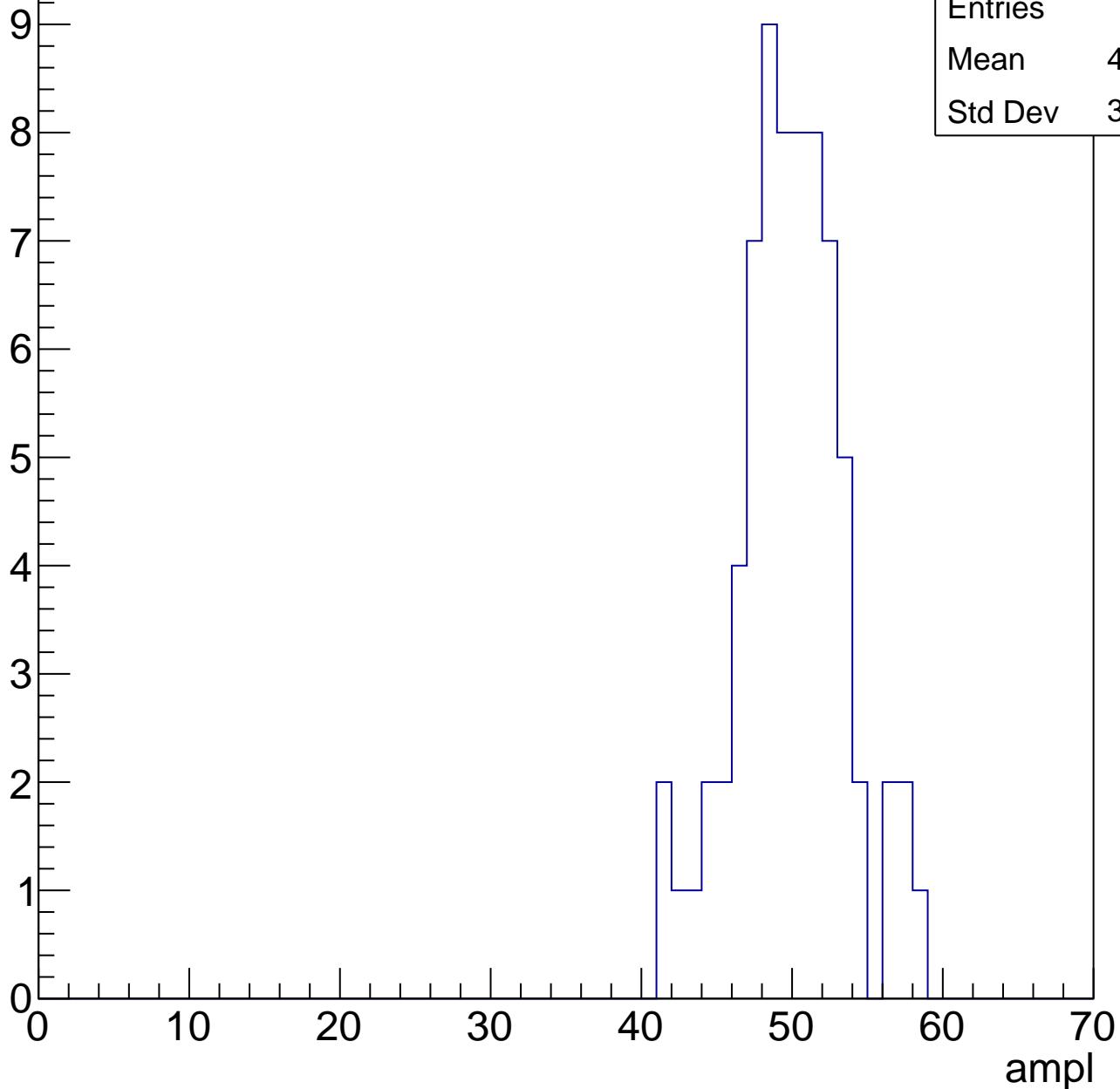
Gaus Width: 3.0757



B1L102S, U13-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	71
Mean	49.45
Std Dev	3.548

B1L102S, U13-ch84, adc4

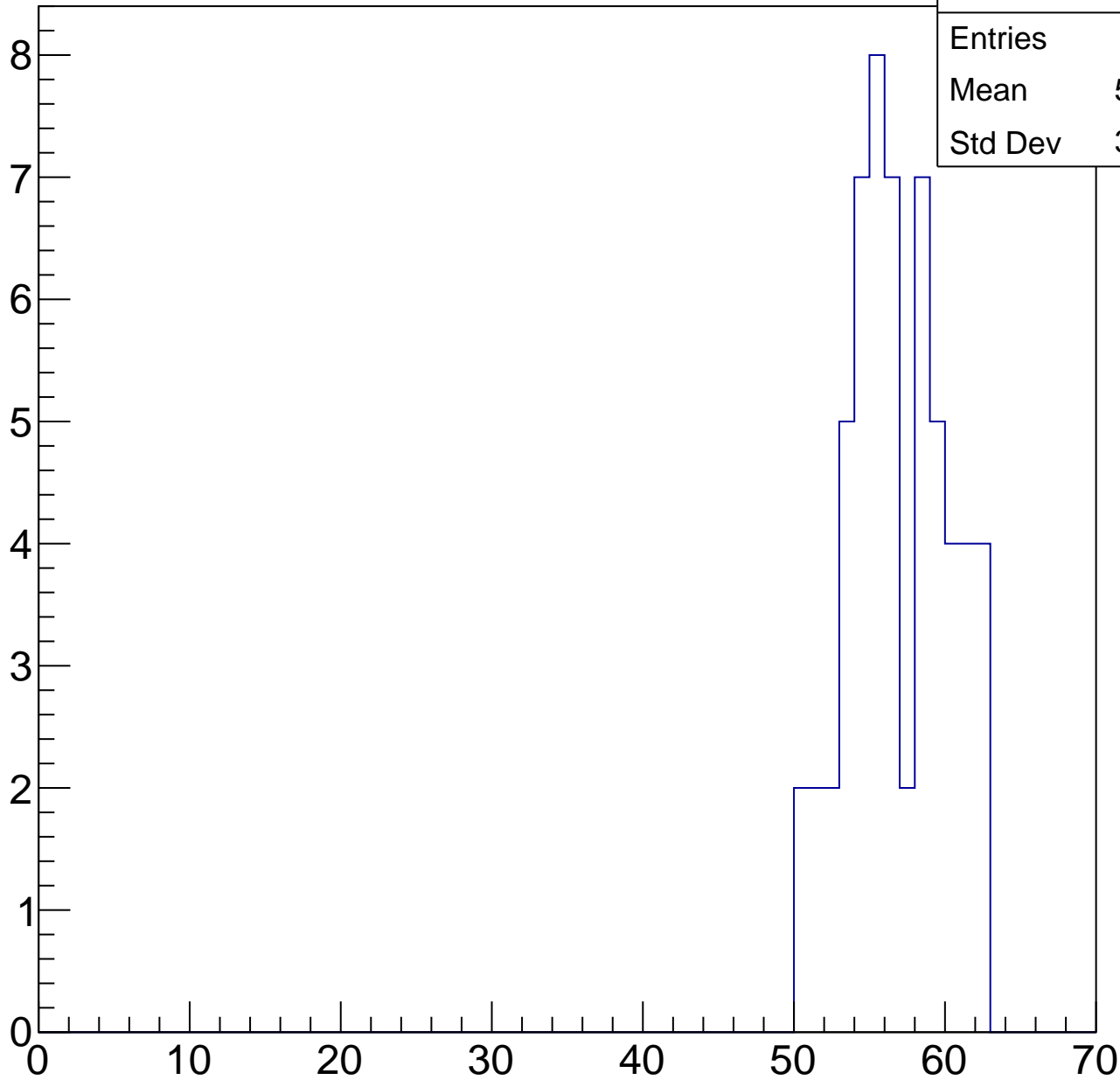
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	59
Mean	56.41
Std Dev	3.211

ampl



B1L102S, U13-ch84, adc5

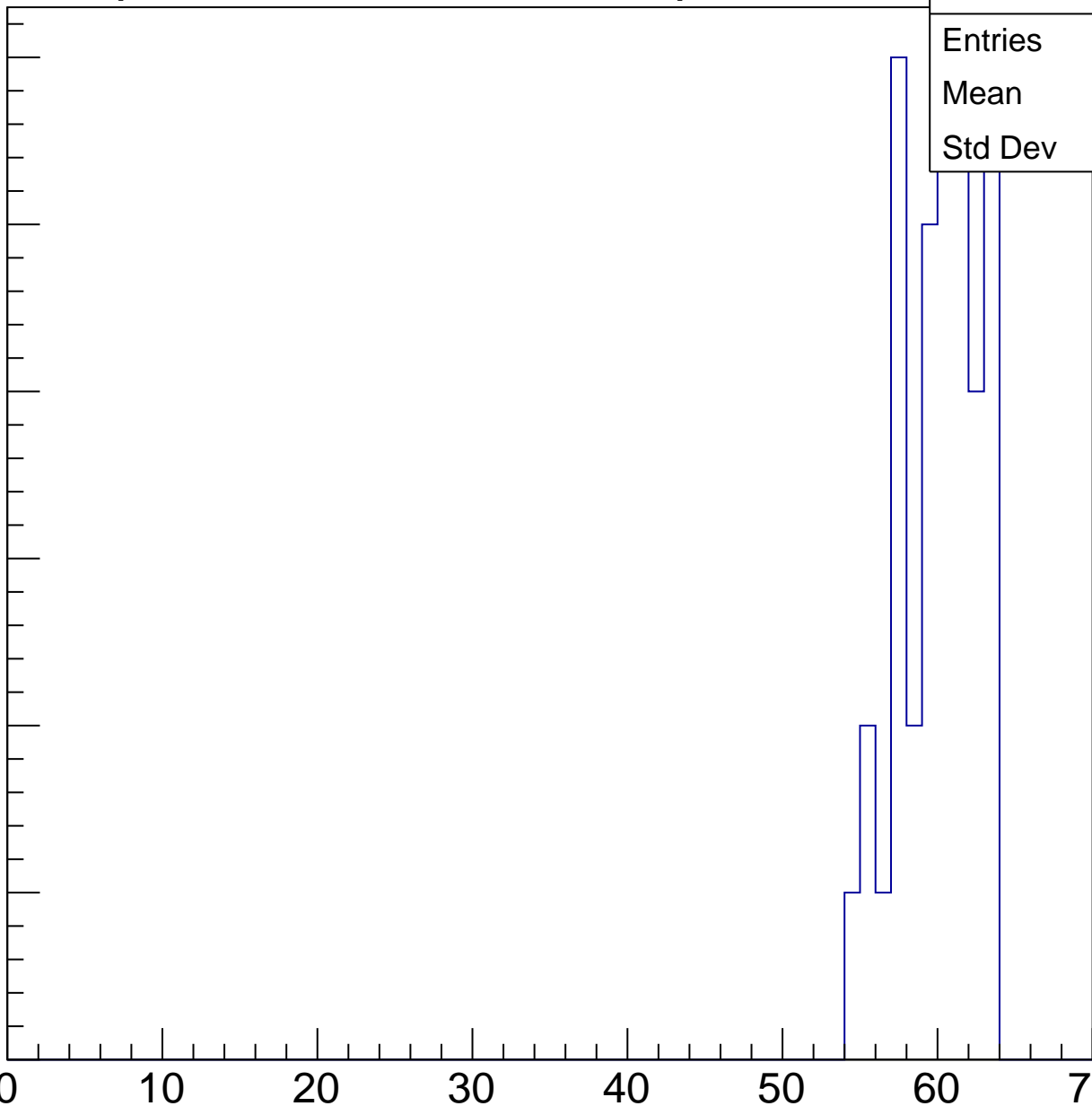
calib_packv5_042523_0143.root, FC#11, port A2

Entry

6
5
4
3
2
1
0

Entries	39
Mean	59.62
Std Dev	2.477

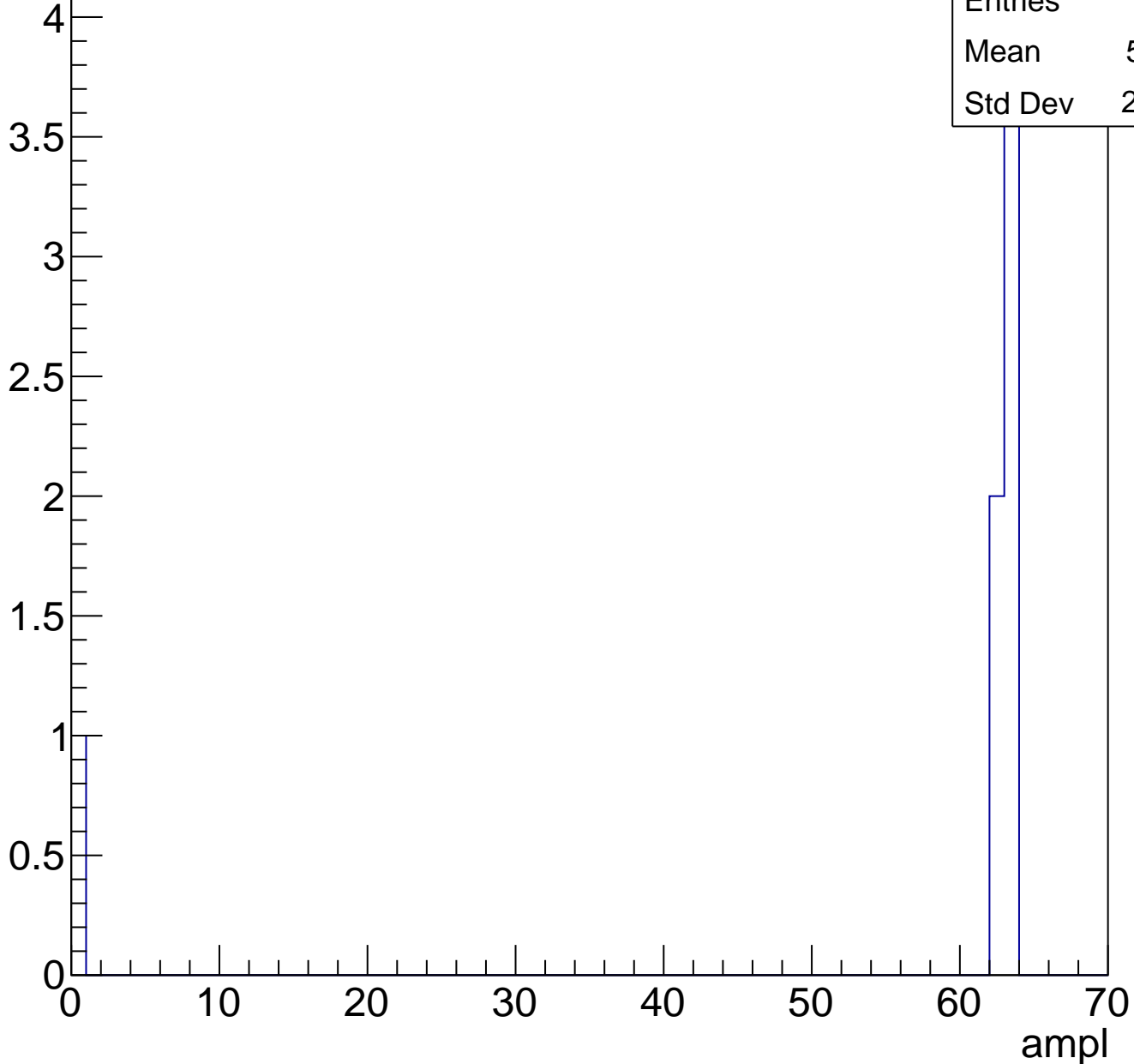
ampl



B1L102S, U13-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch85, adc0

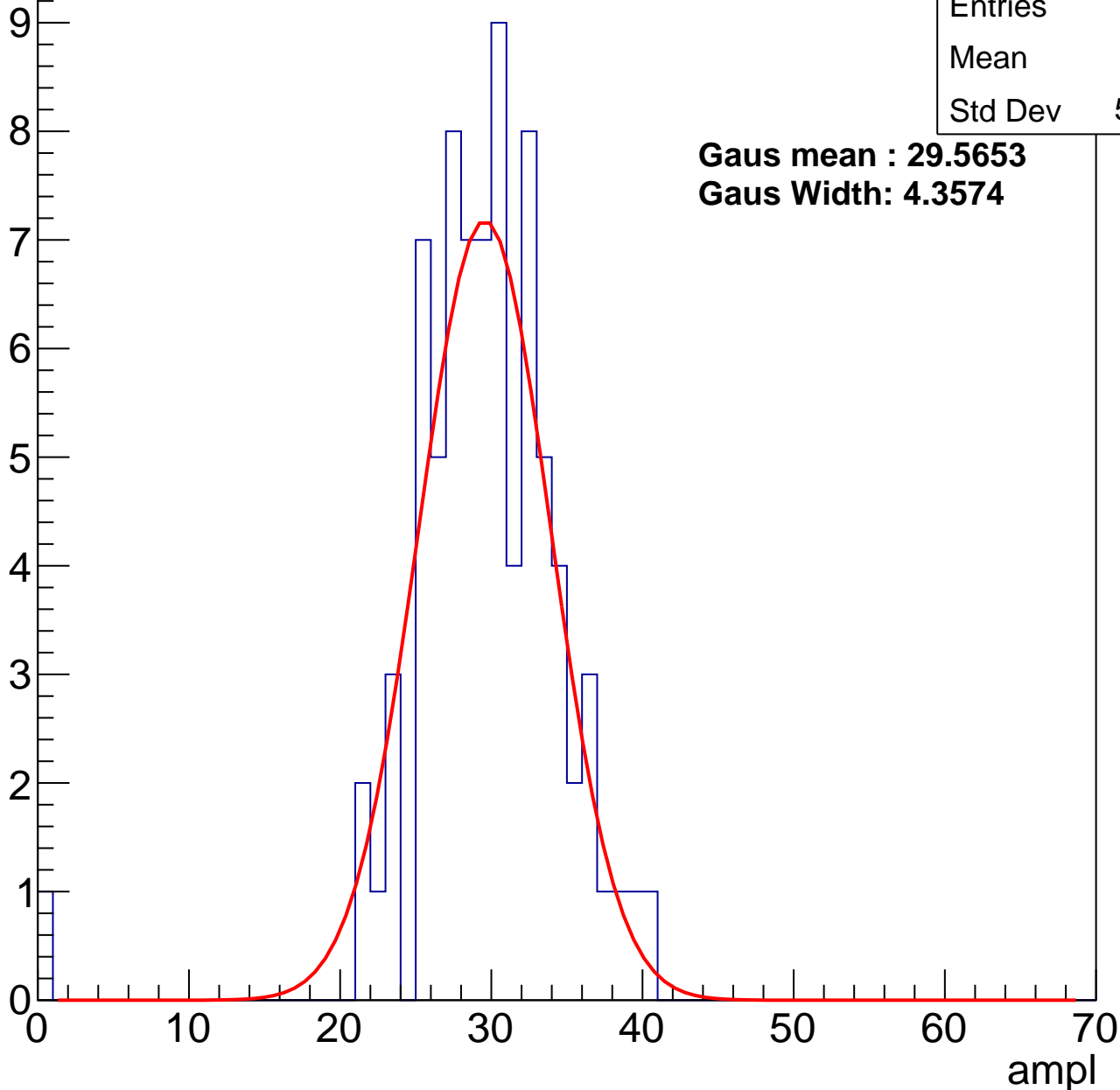
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.2
Std Dev	5.221

Gaus mean : 29.5653

Gaus Width: 4.3574



B1L102S, U13-ch85, adc1

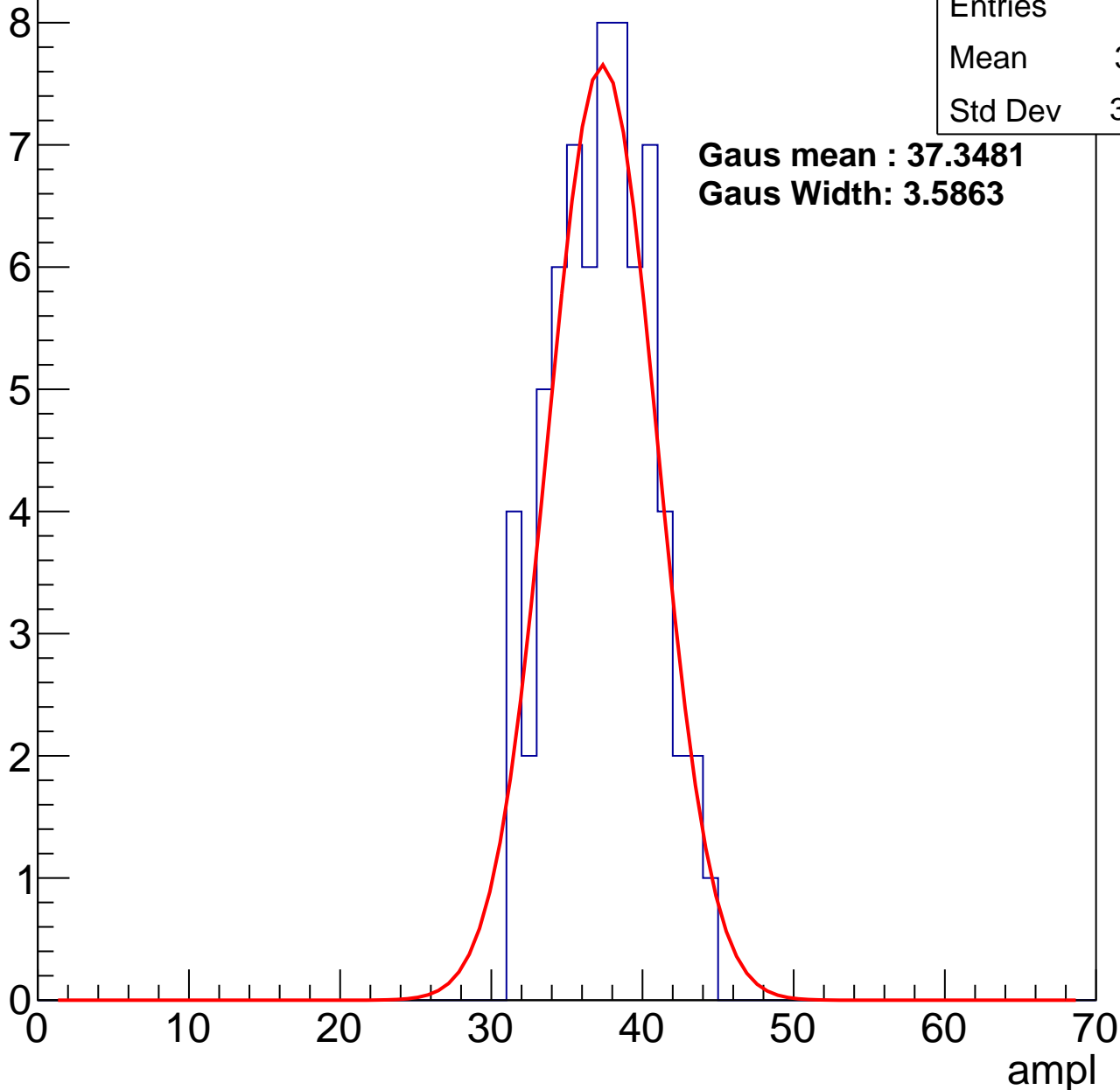
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.91
Std Dev	3.189

Gaus mean : 37.3481

Gaus Width: 3.5863



B1L102S, U13-ch85, adc2

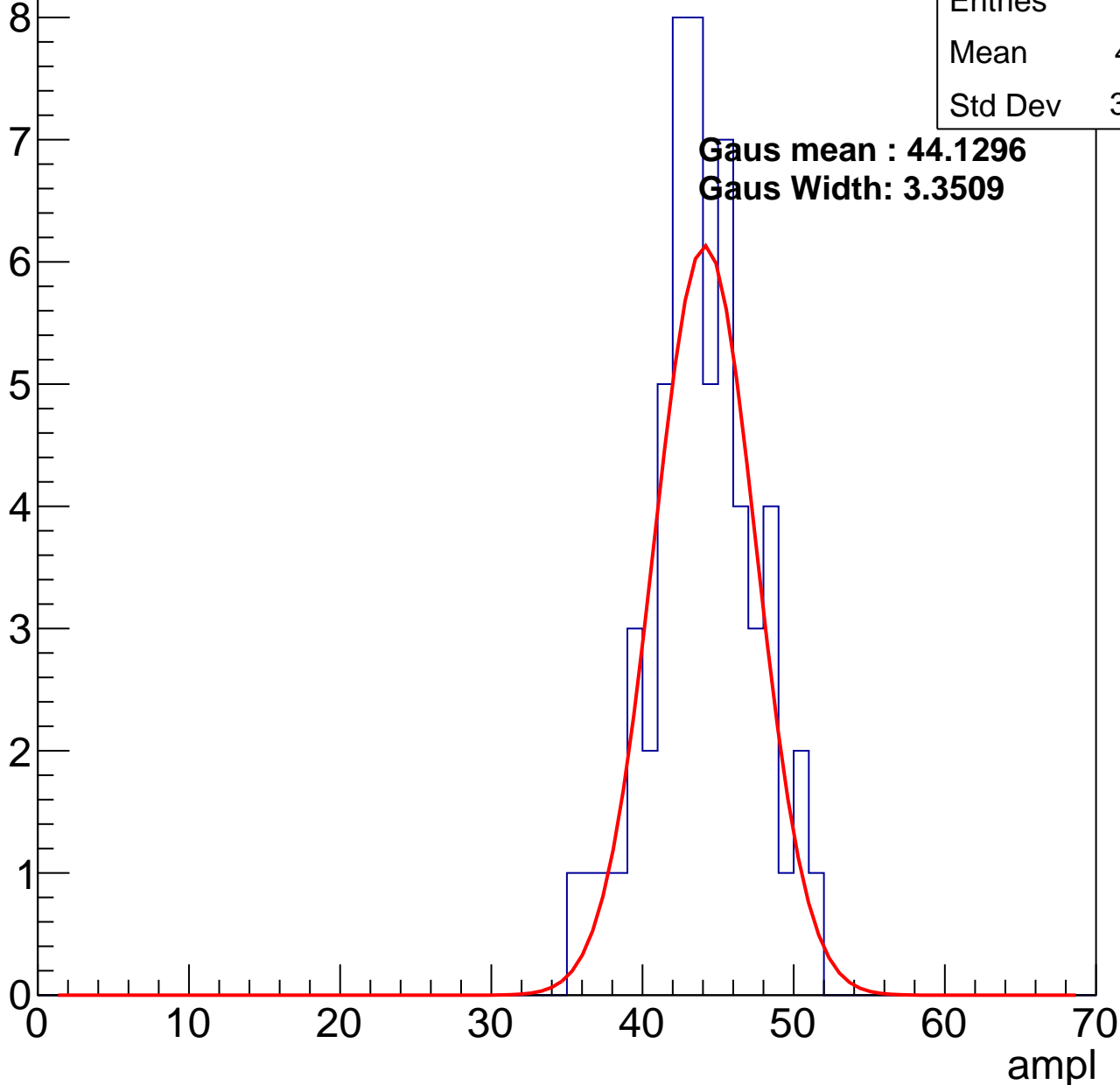
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.51
Std Dev	3.424

Gaus mean : 44.1296

Gaus Width: 3.3509

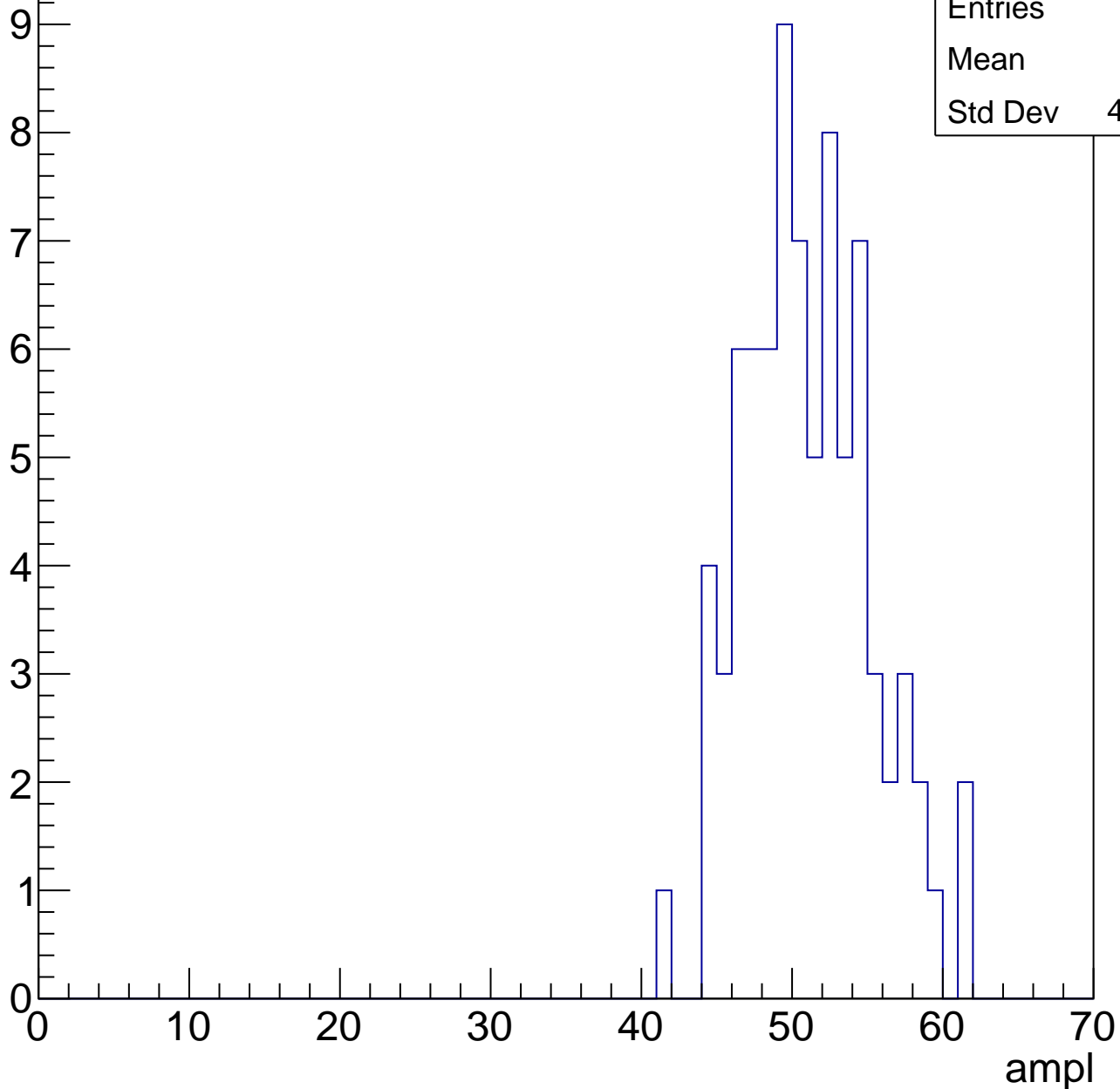


B1L102S, U13-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	50.6
Std Dev	4.173

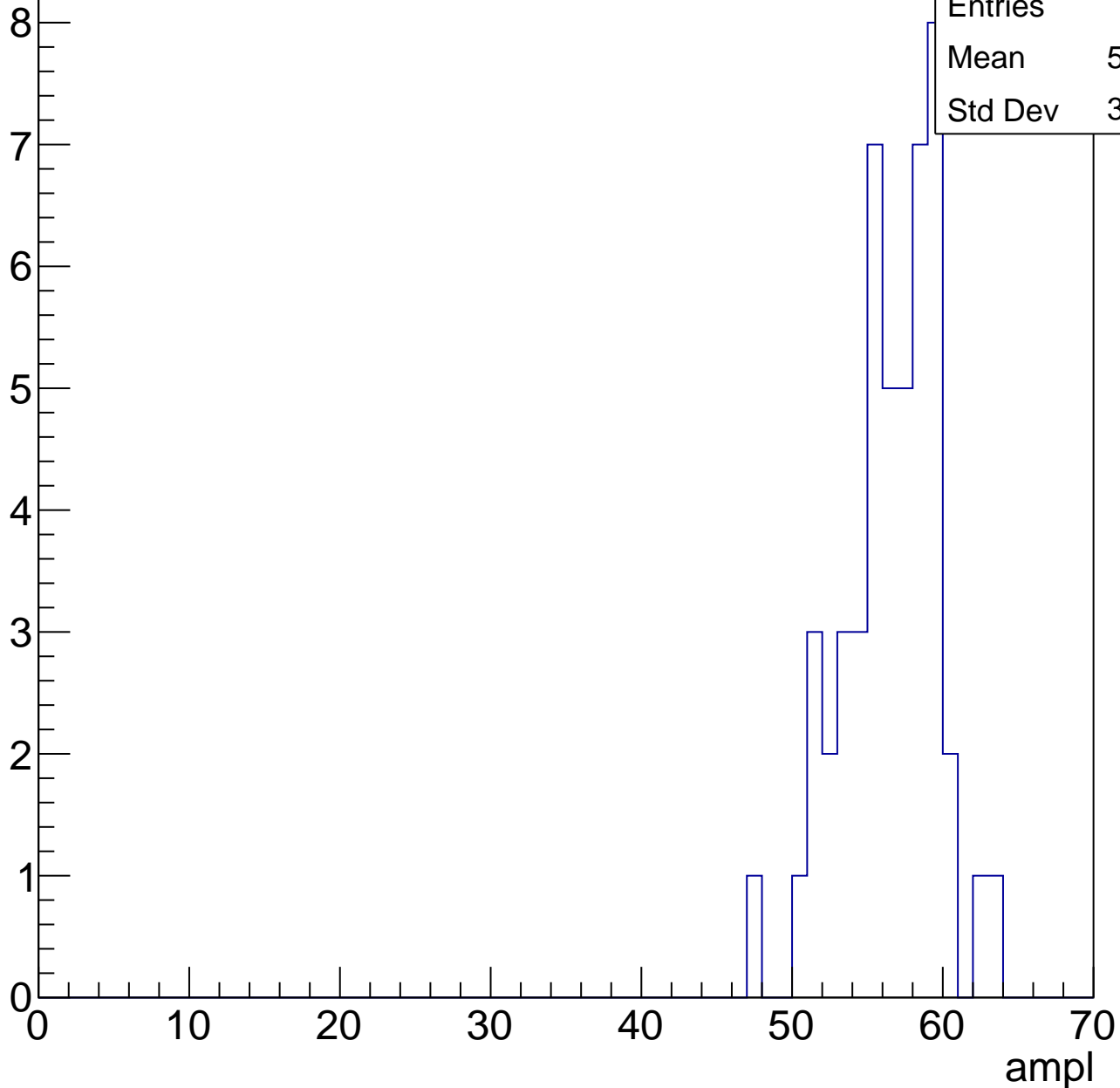


B1L102S, U13-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.08
Std Dev	3.168

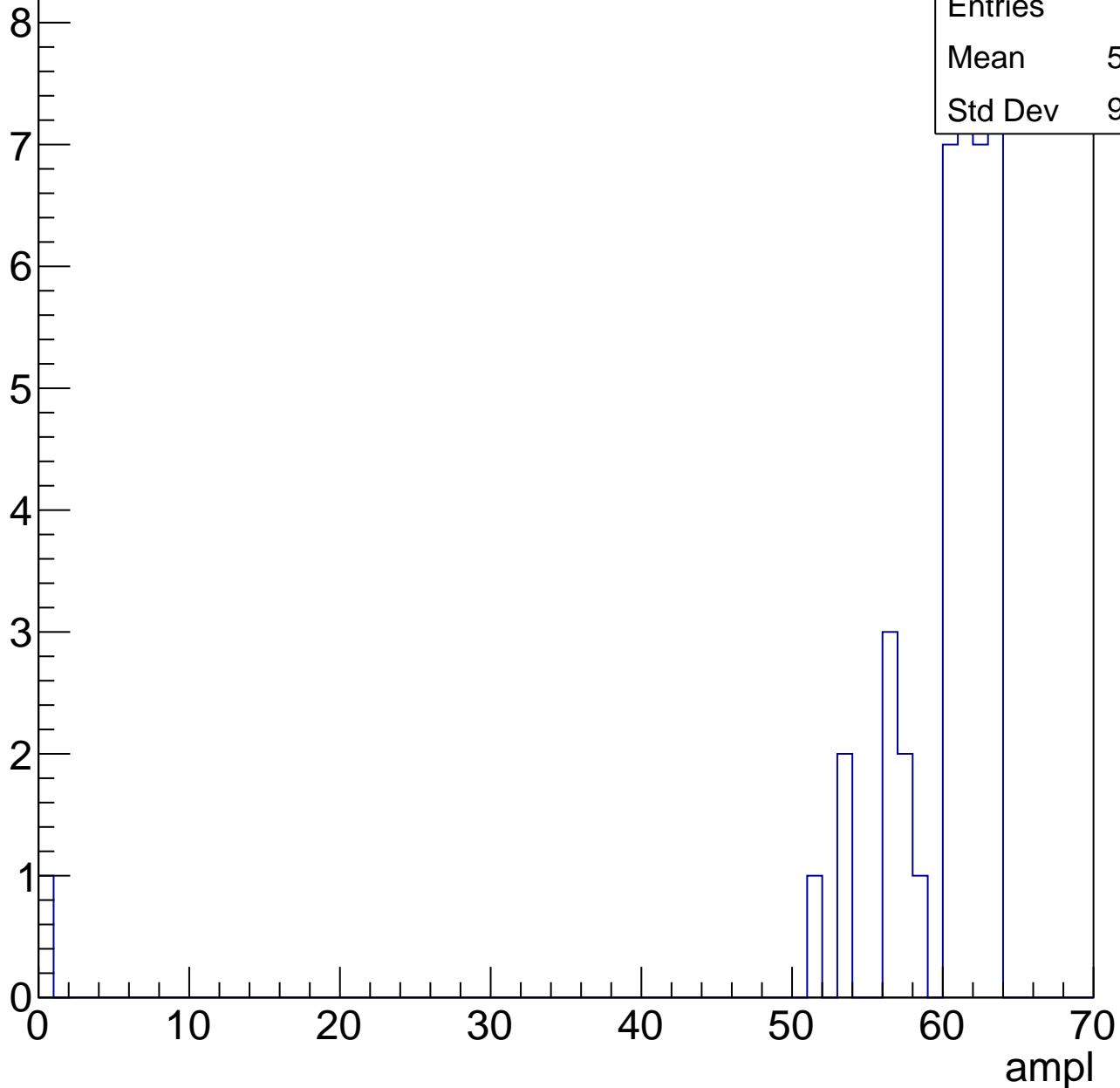


B1L102S, U13-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

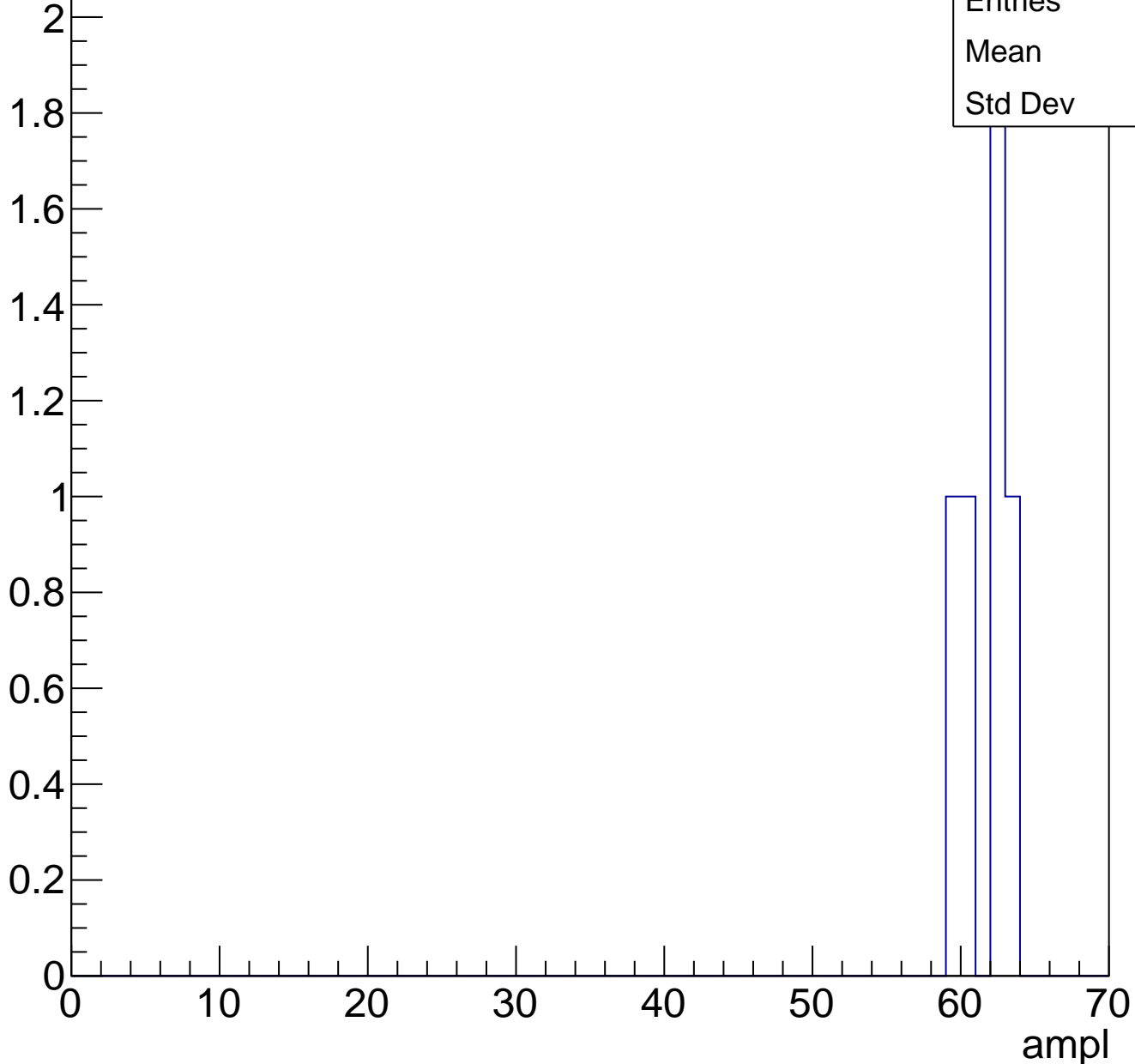
Entries	40
Mean	58.58
Std Dev	9.843



B1L102S, U13-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	61.2
Std Dev	1.47

B1L102S, U13-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U13-ch86, adc0

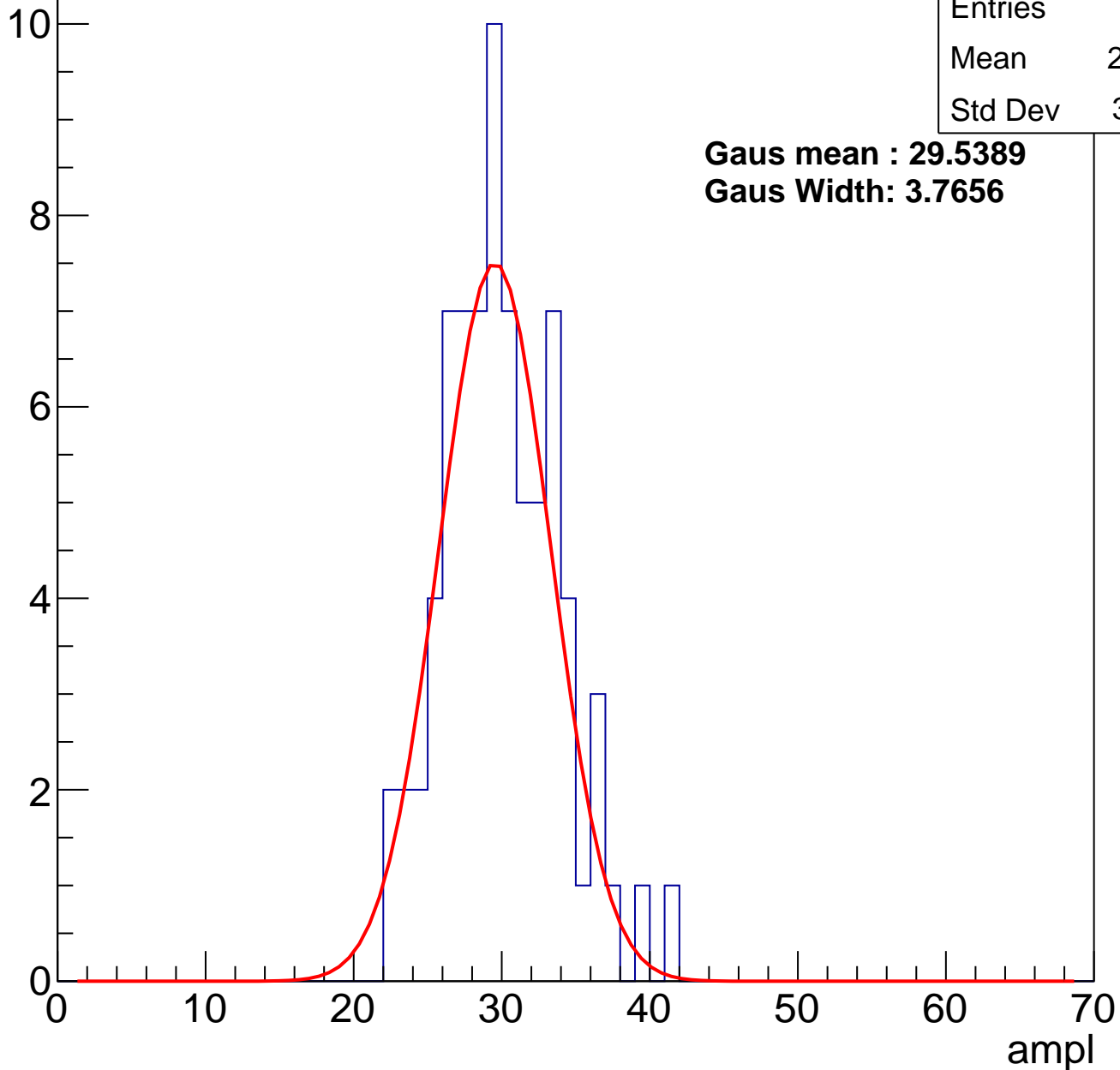
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	29.57
Std Dev	3.861

Gaus mean : 29.5389

Gaus Width: 3.7656

Entry



B1L102S, U13-ch86, adc1

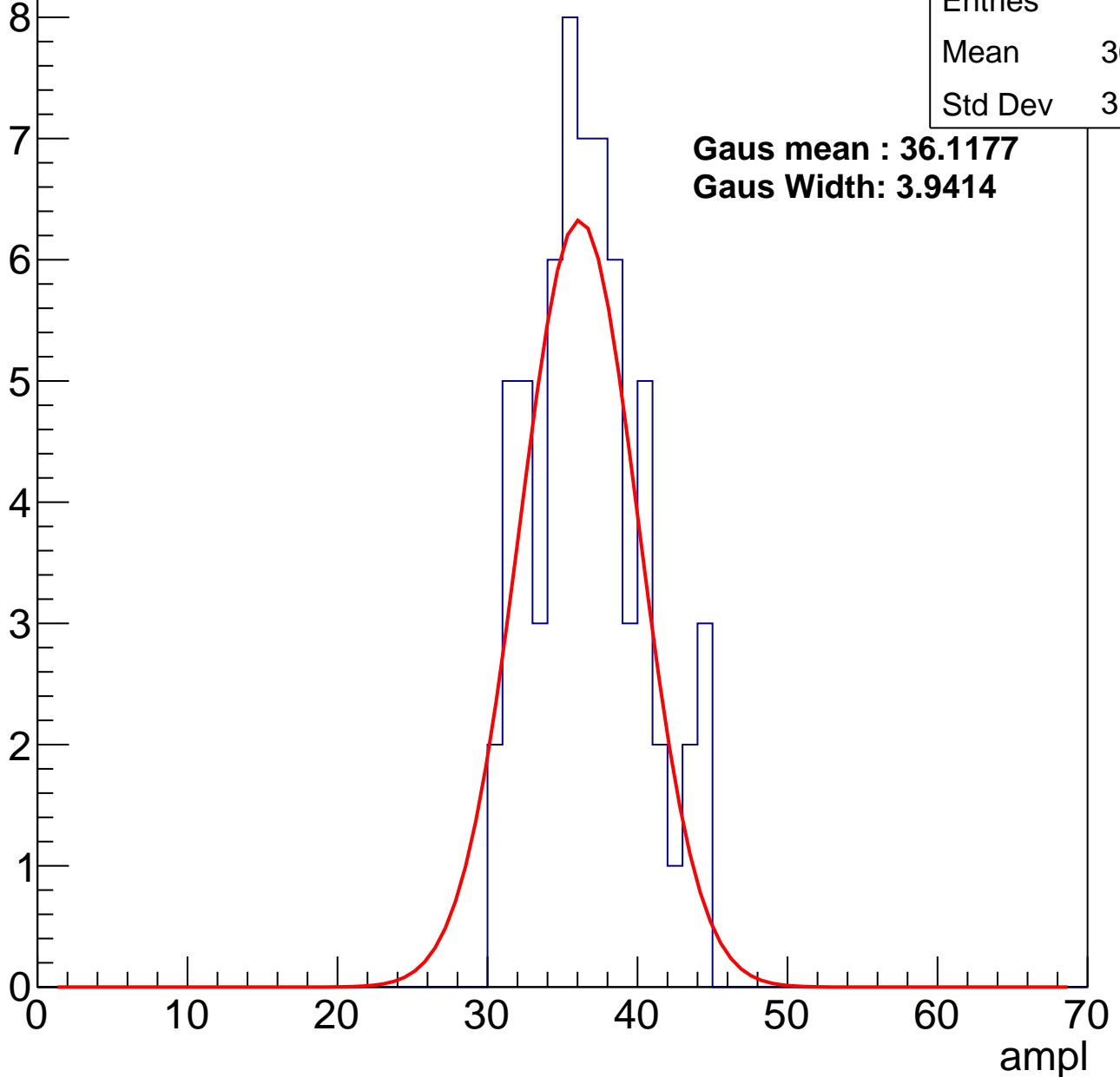
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.25
Std Dev	3.608

Gaus mean : 36.1177

Gaus Width: 3.9414



B1L102S, U13-ch86, adc2

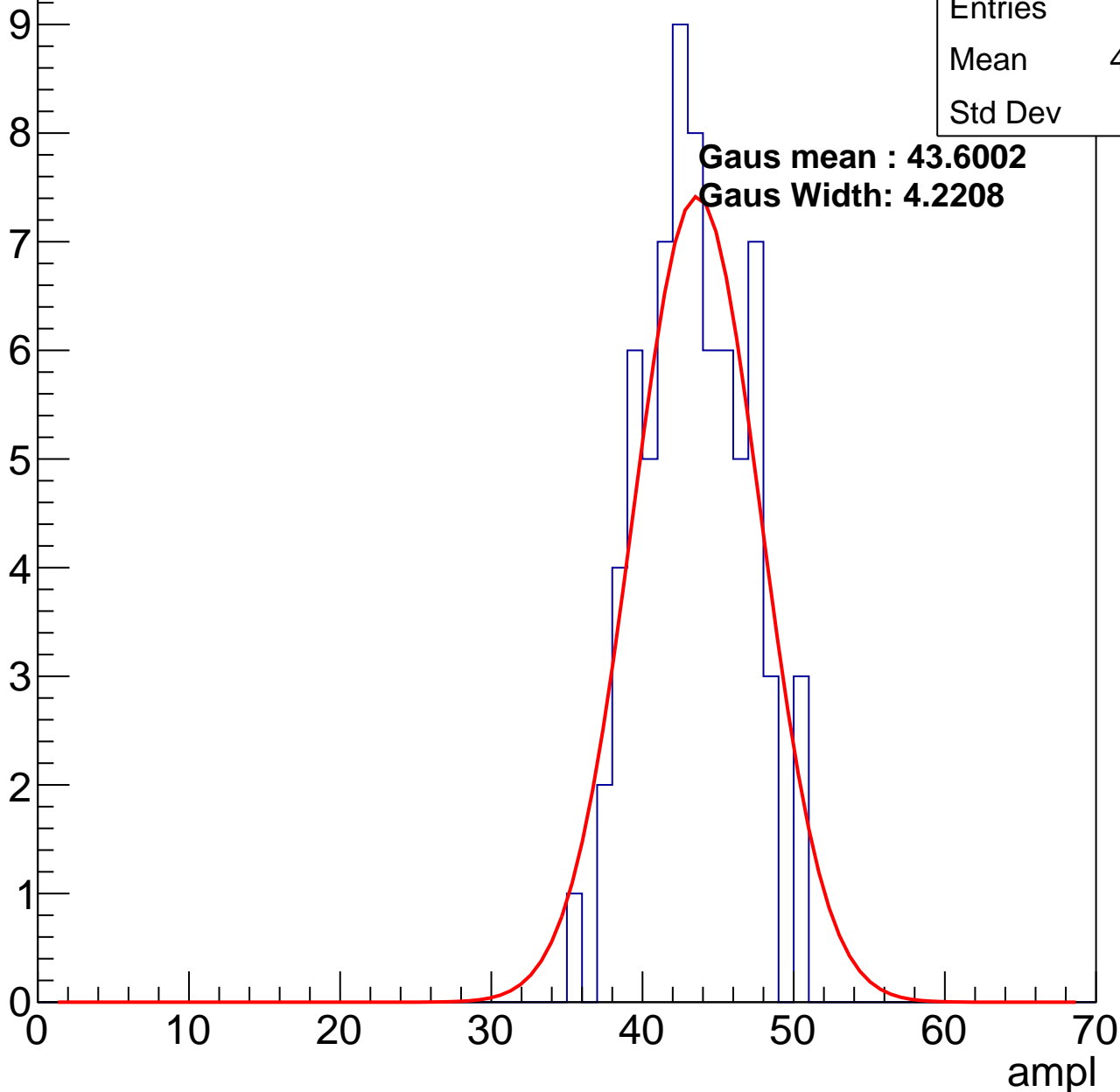
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.93
Std Dev	3.38

Gaus mean : 43.6002

Gaus Width: 4.2208

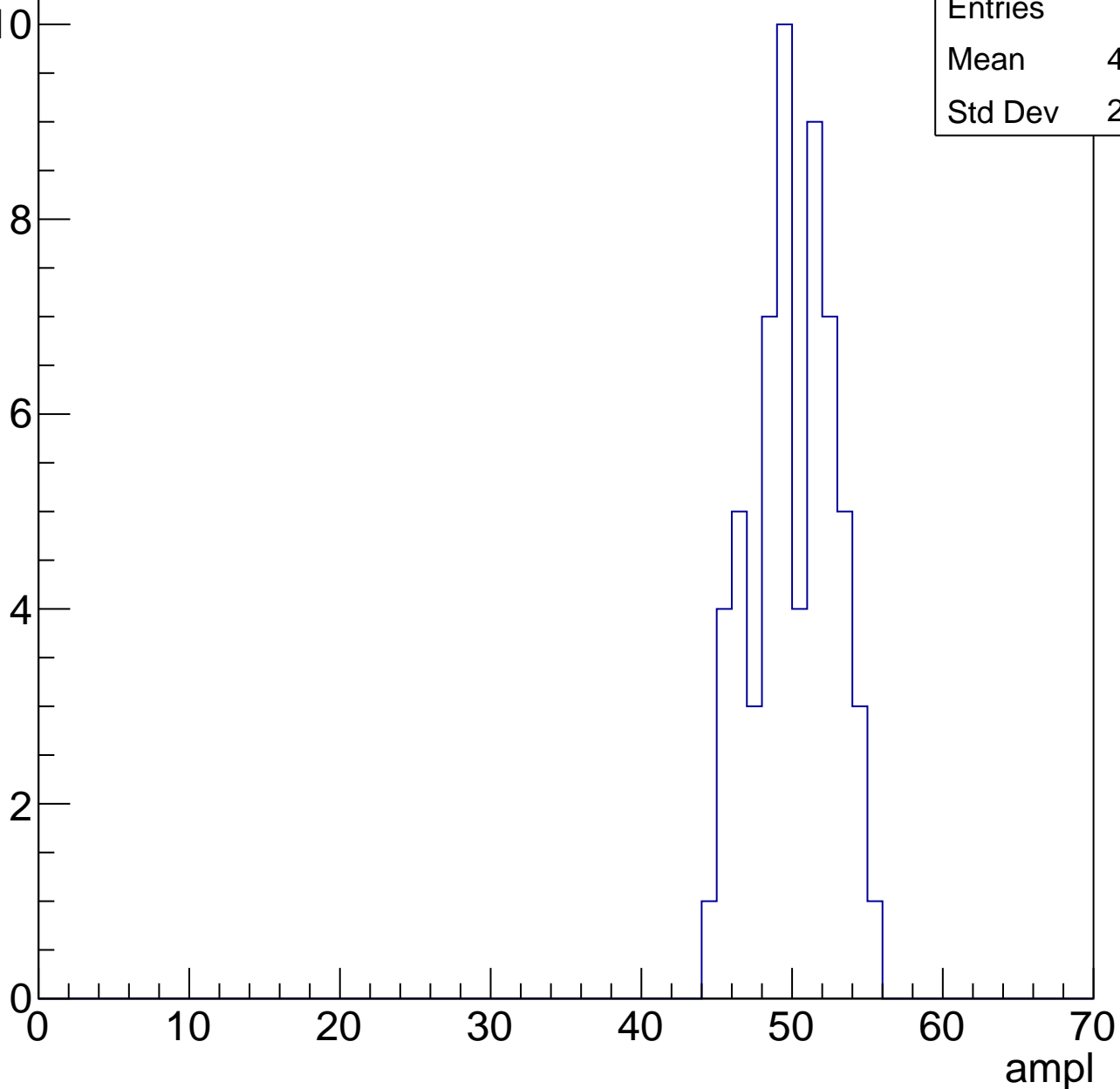


B1L102S, U13-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.59
Std Dev	2.688

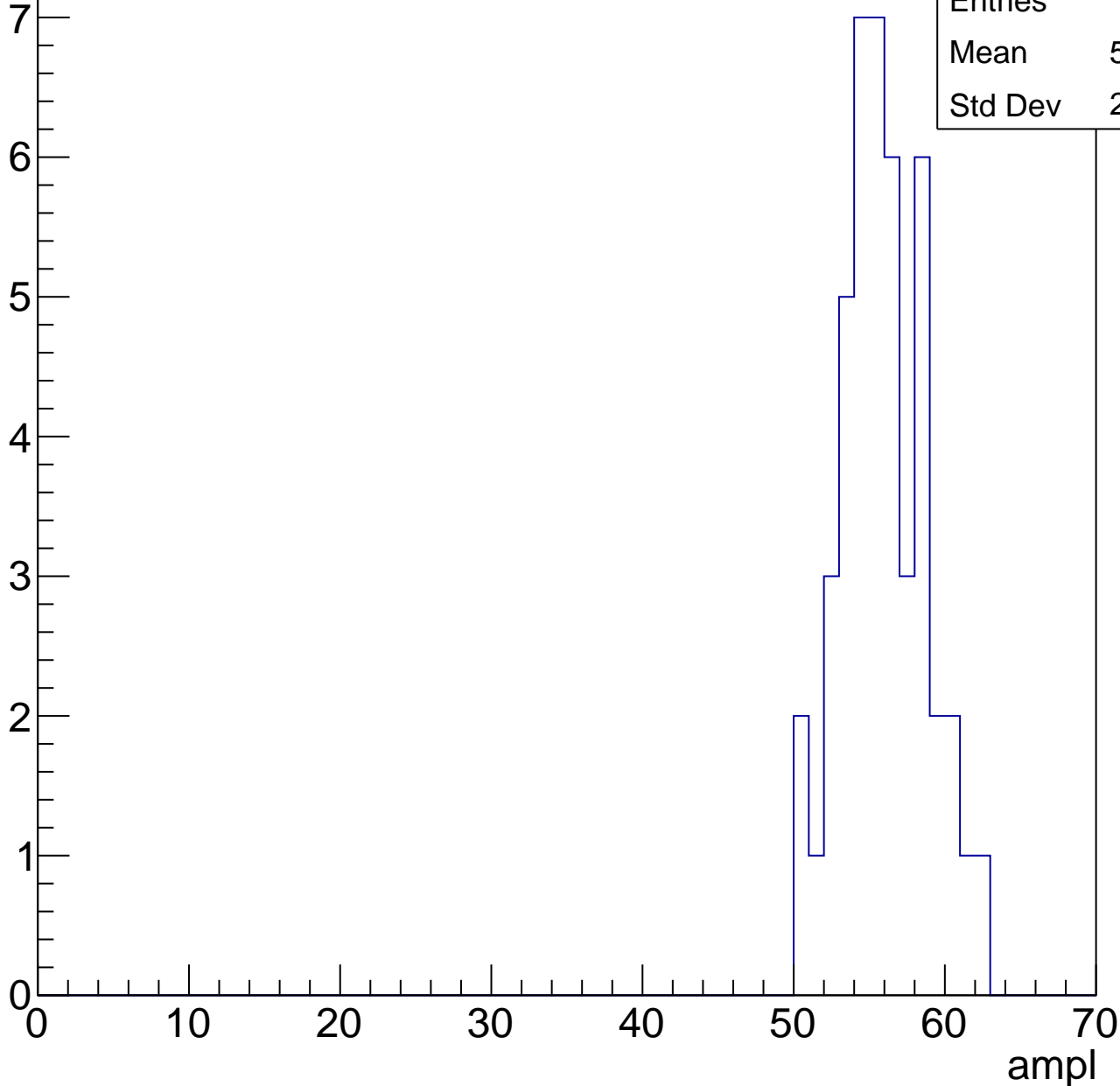


B1L102S, U13-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

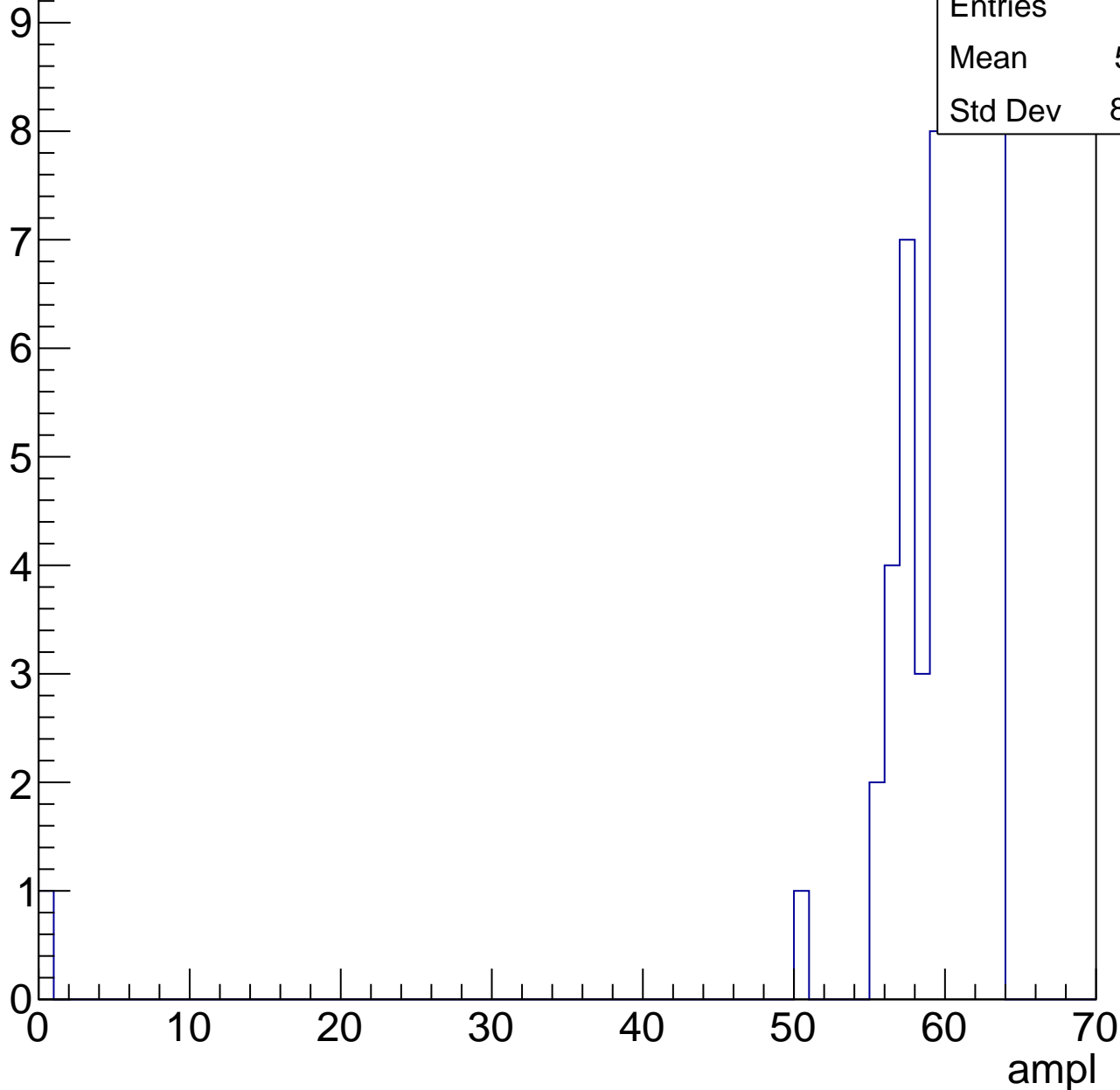
Entries	46
Mean	55.46
Std Dev	2.756



B1L102S, U13-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	61.5
Std Dev	2.062

B1L102S, U13-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch87, adc0

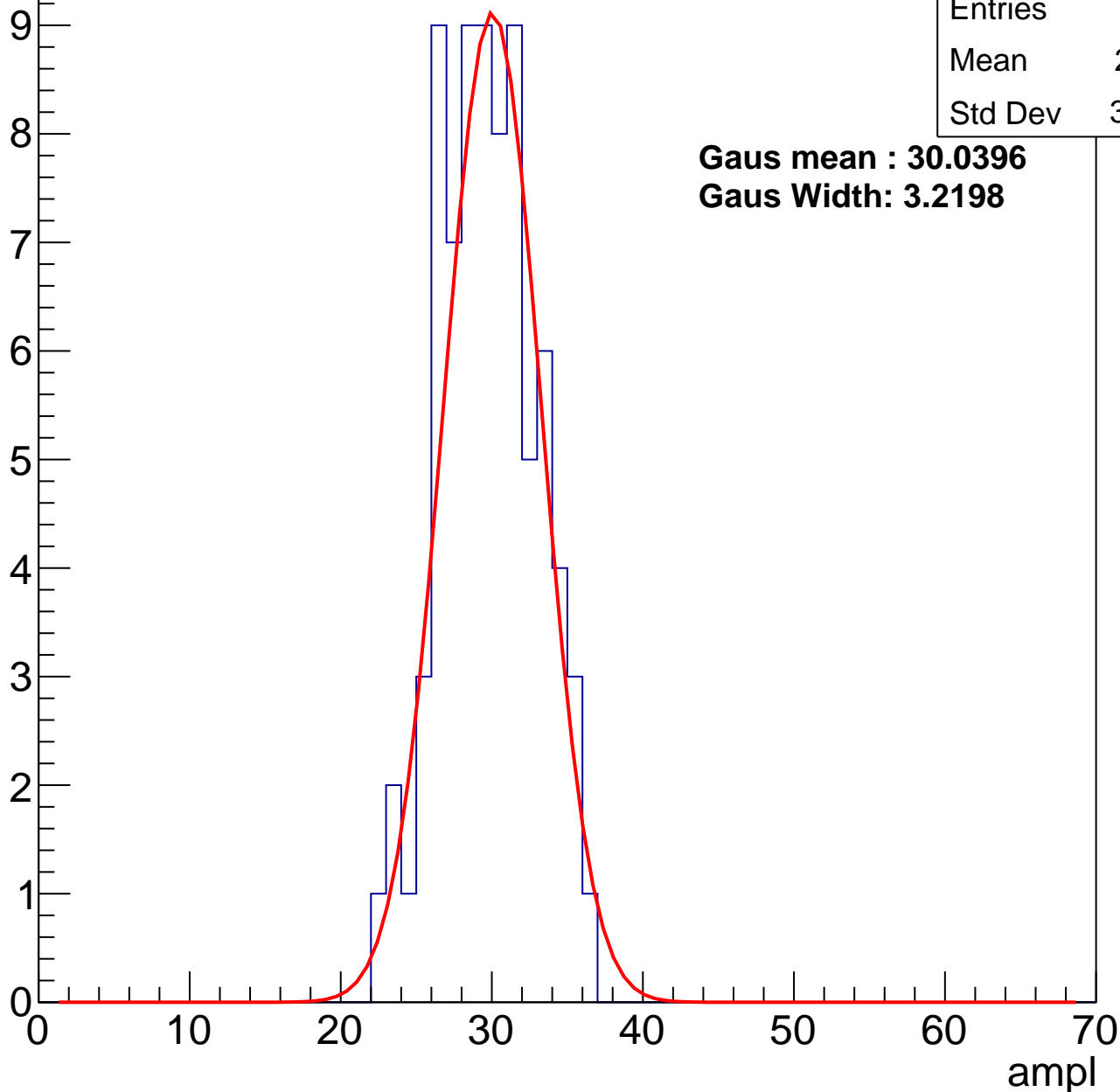
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.31
Std Dev	3.114

Gaus mean : 30.0396

Gaus Width: 3.2198



B1L102S, U13-ch87, adc1

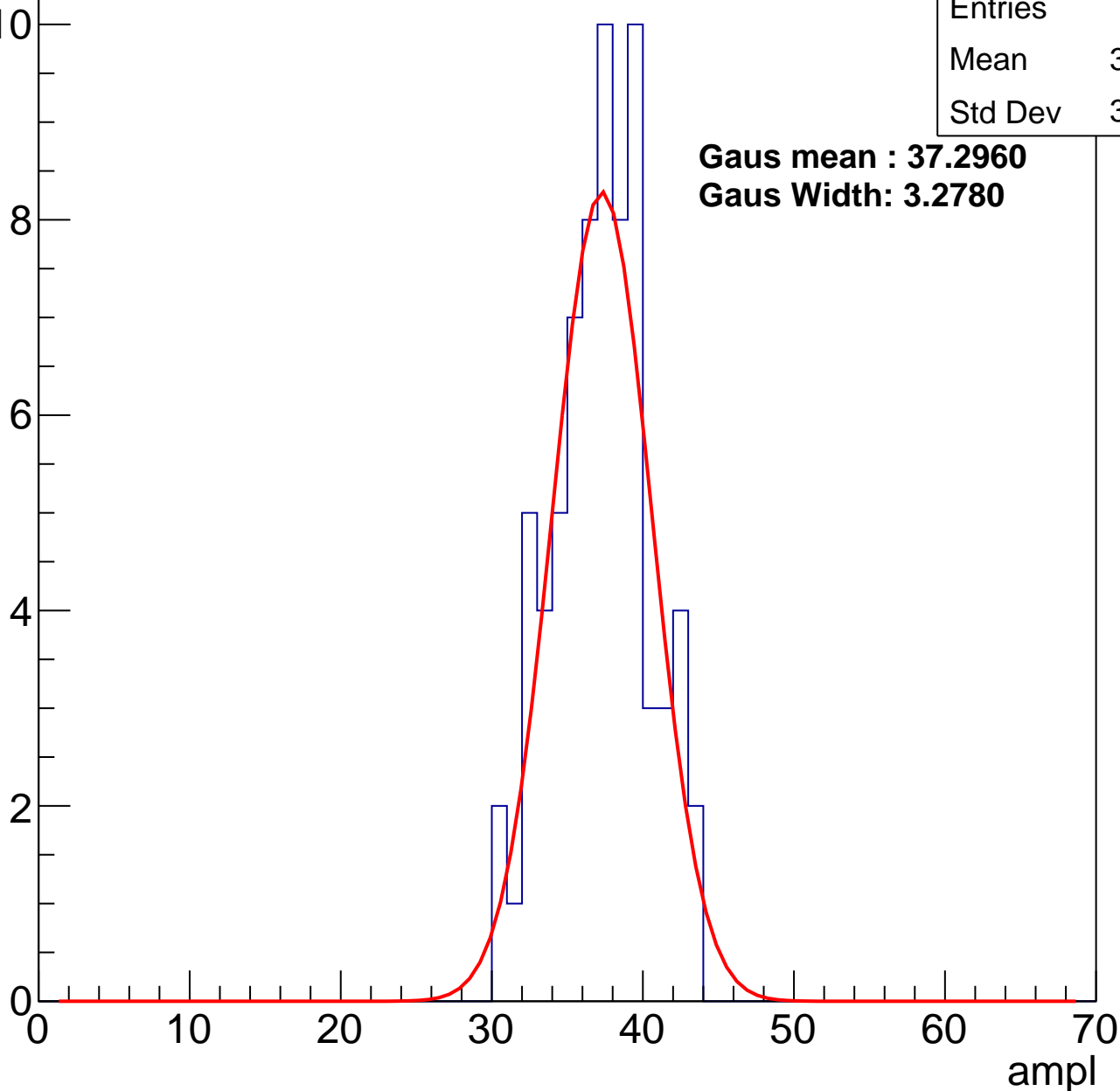
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.76
Std Dev	3.107

Gaus mean : 37.2960

Gaus Width: 3.2780



B1L102S, U13-ch87, adc2

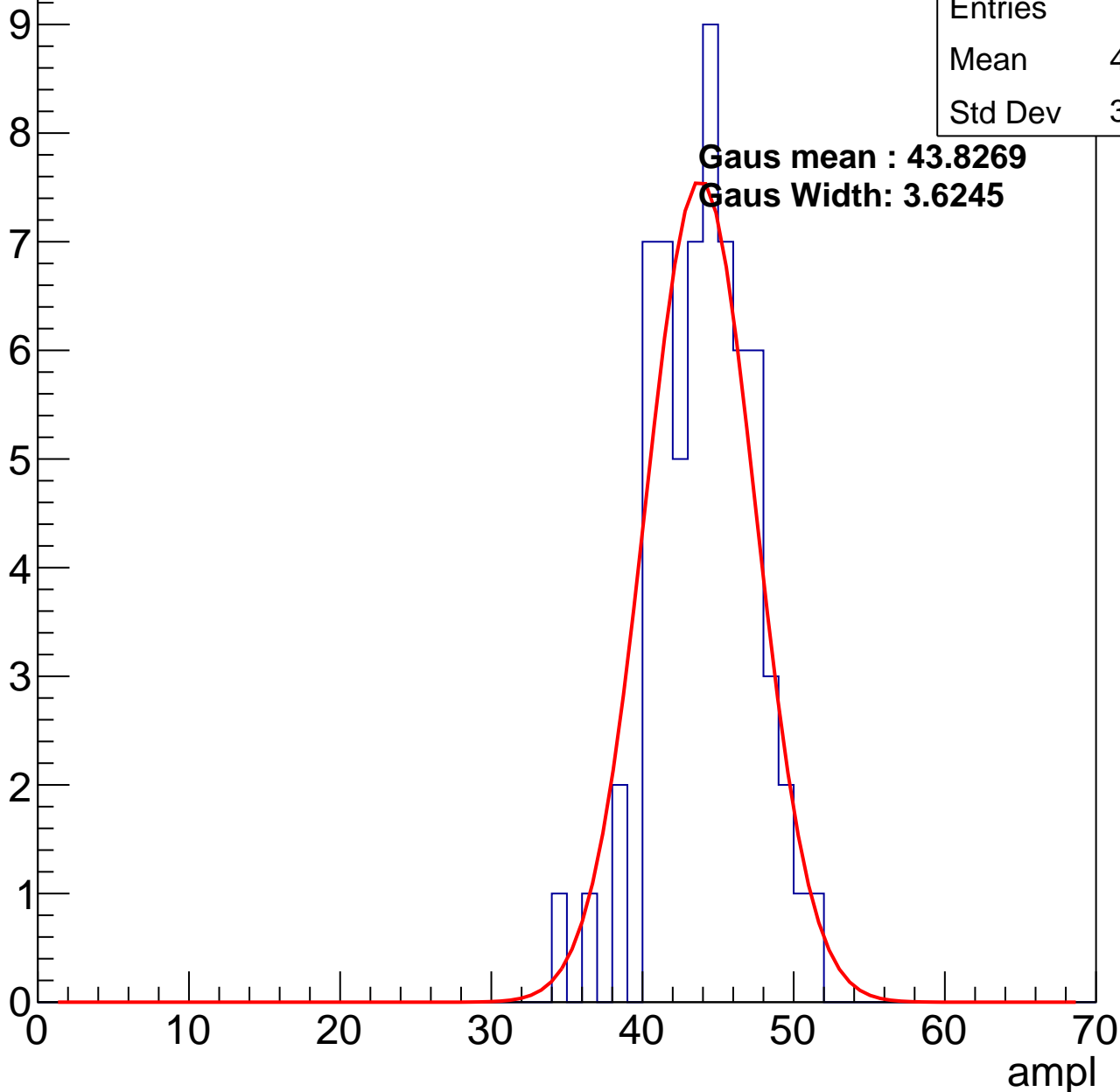
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.63
Std Dev	3.275

Gaus mean : 43.8269

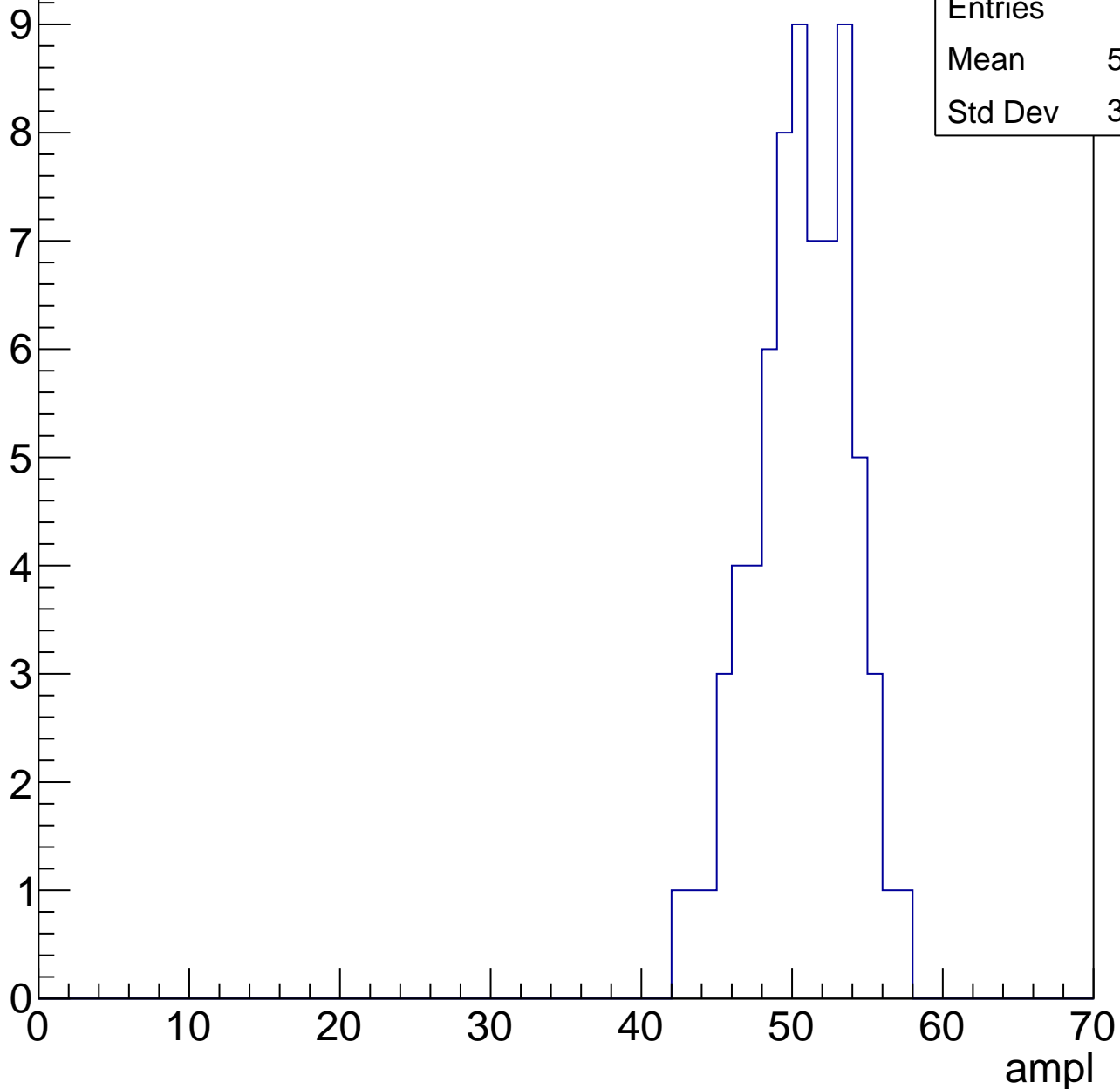
Gaus Width: 3.6245



B1L102S, U13-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



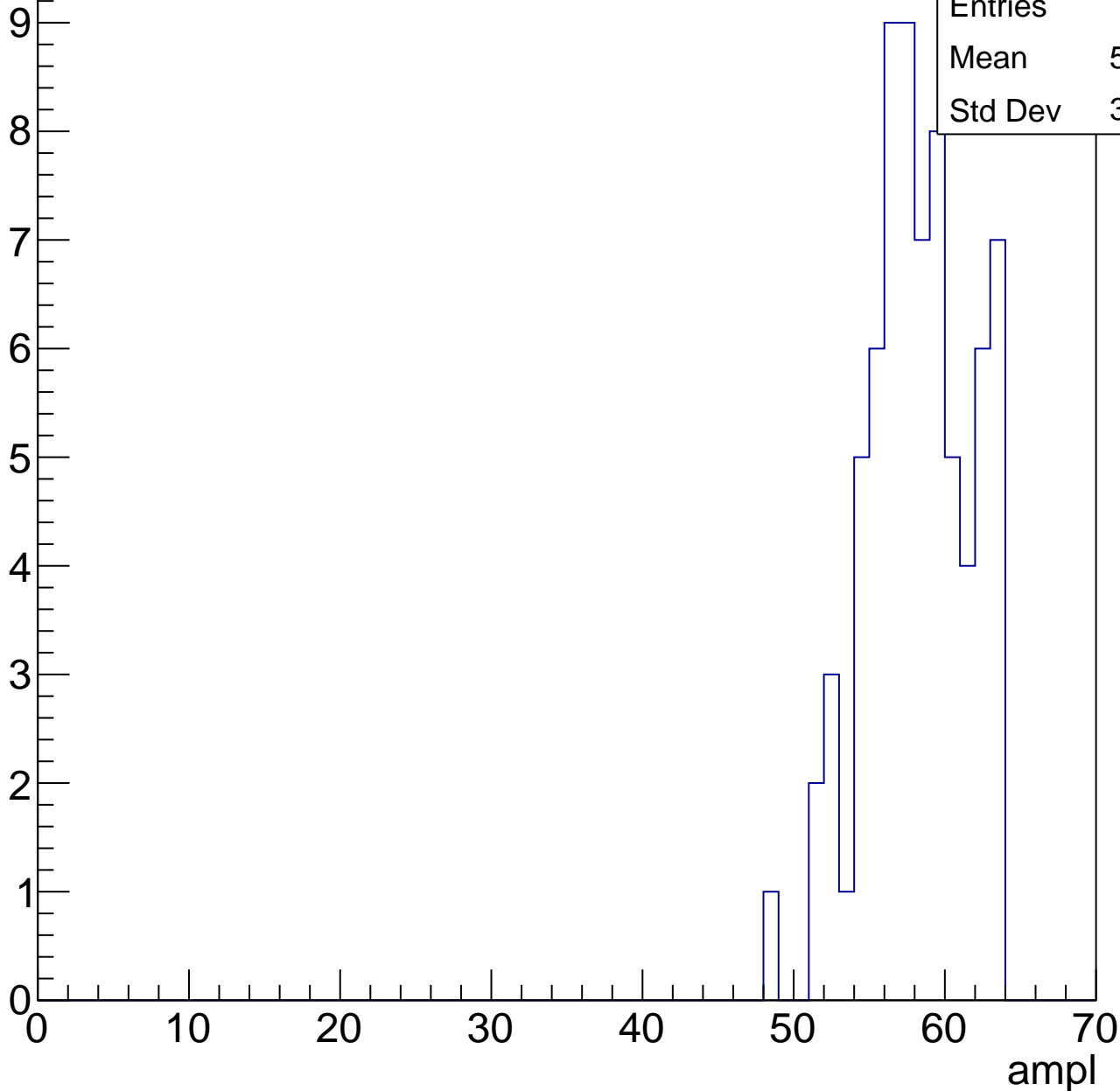
Entries	70
Mean	50.17
Std Dev	3.185

B1L102S, U13-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	57.68
Std Dev	3.388

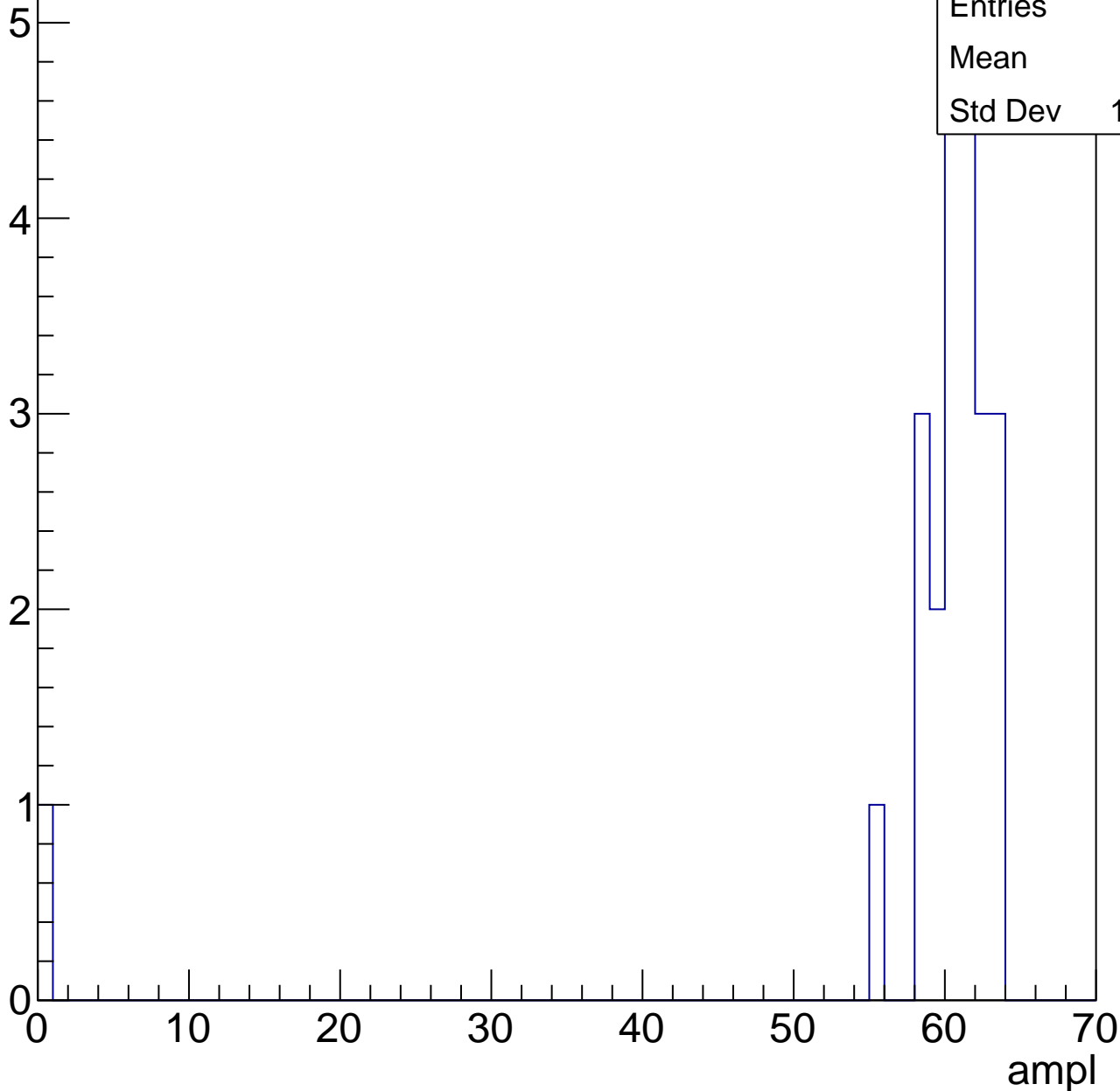


B1L102S, U13-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	57.7
Std Dev	12.44



B1L102S, U13-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch88, adc0

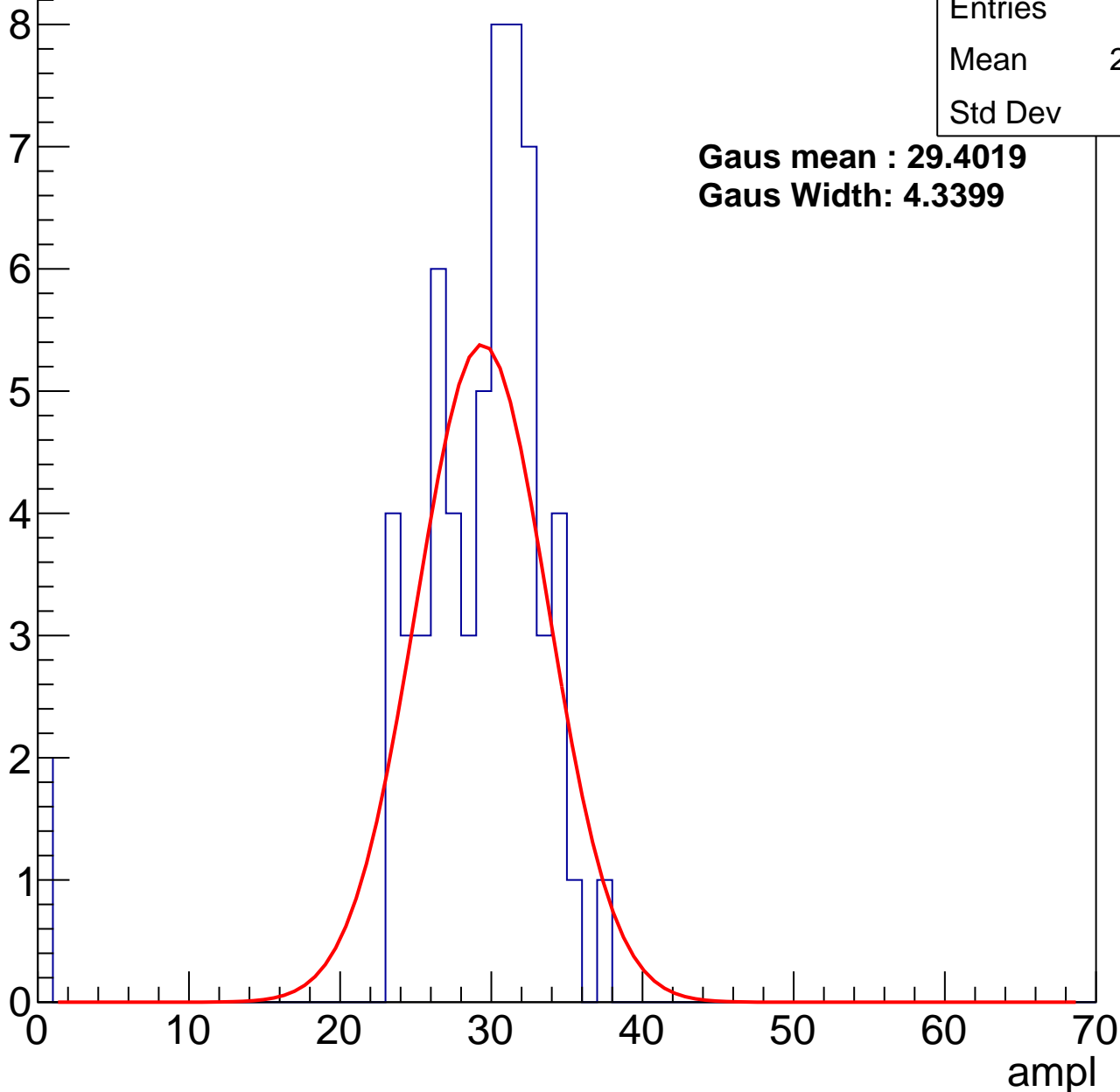
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.24
Std Dev	6.14

Gaus mean : 29.4019

Gaus Width: 4.3399



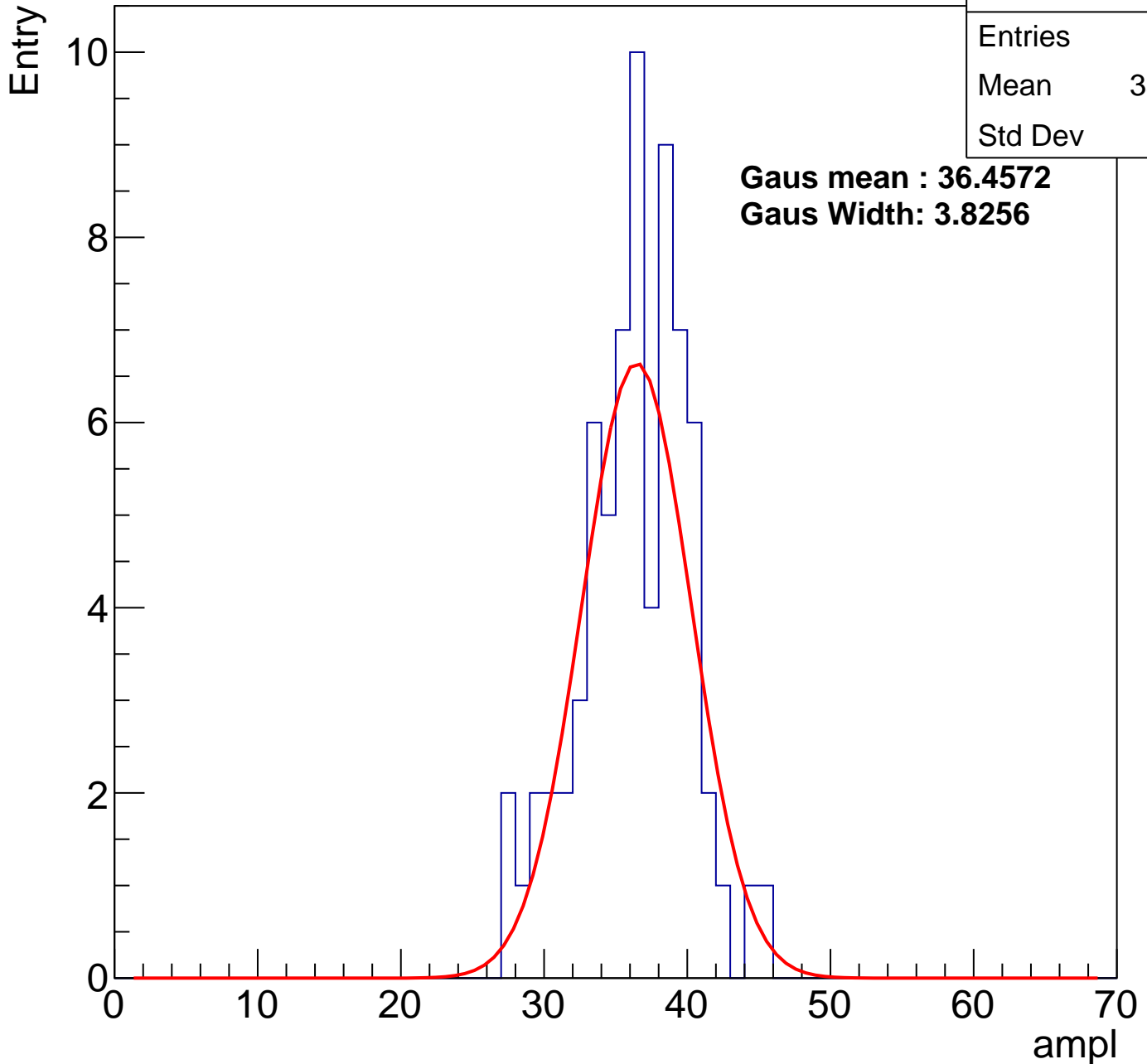
B1L102S, U13-ch88, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	35.87
Std Dev	3.76

Gaus mean : 36.4572

Gaus Width: 3.8256



B1L102S, U13-ch88, adc2

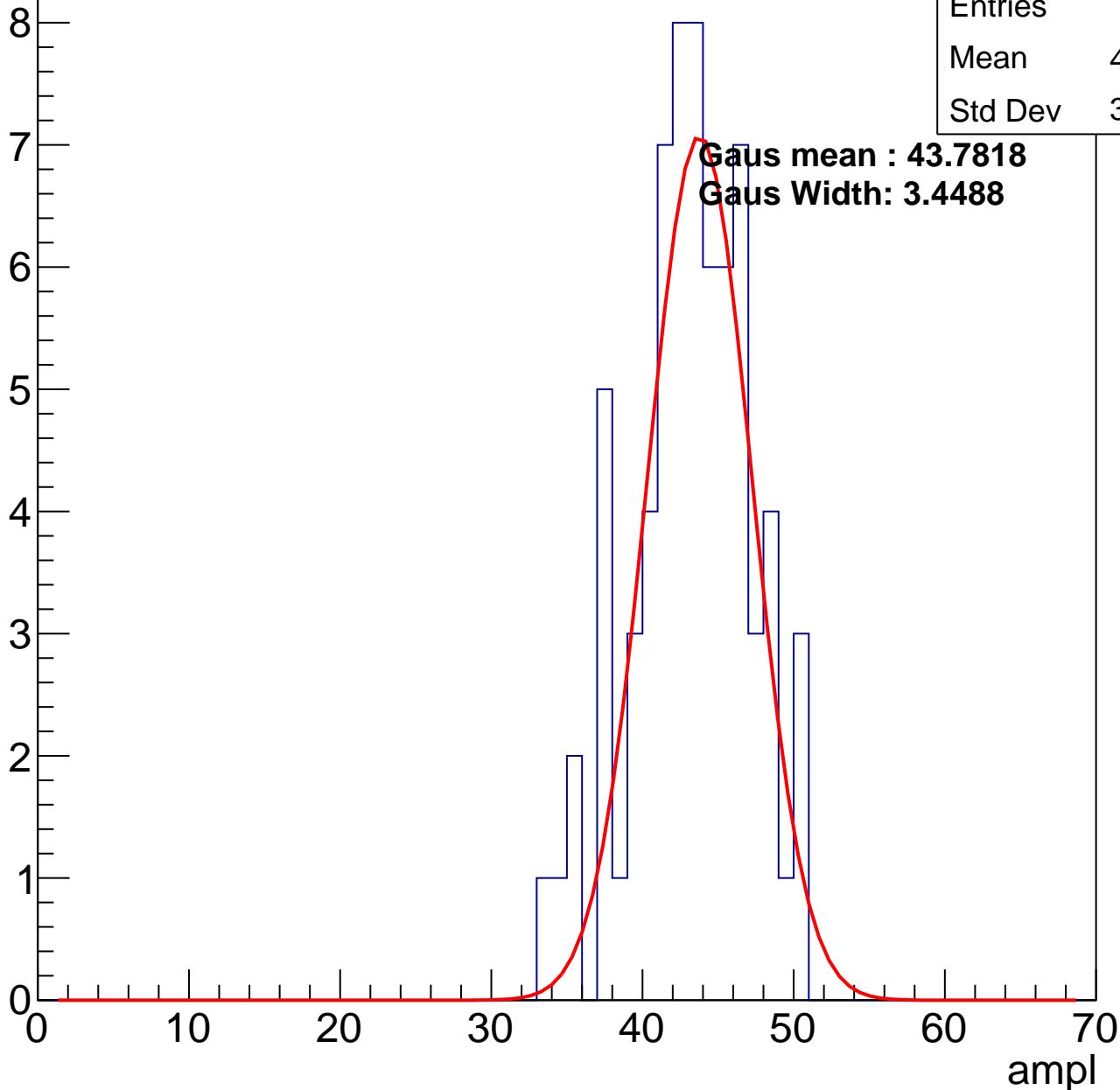
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.74
Std Dev	3.879

Gaus mean : 43.7818

Gaus Width: 3.4488

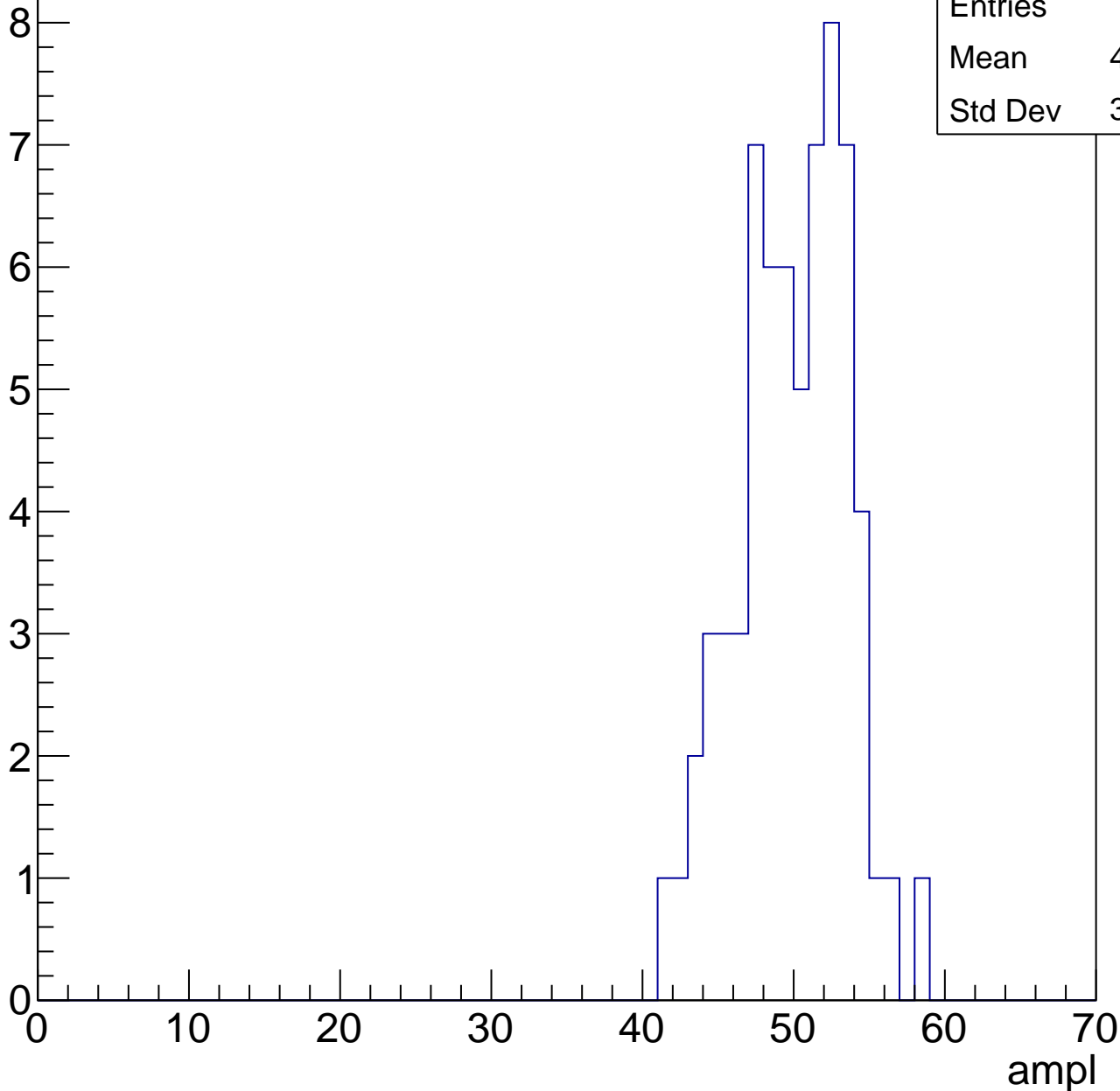


B1L102S, U13-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.45
Std Dev	3.564

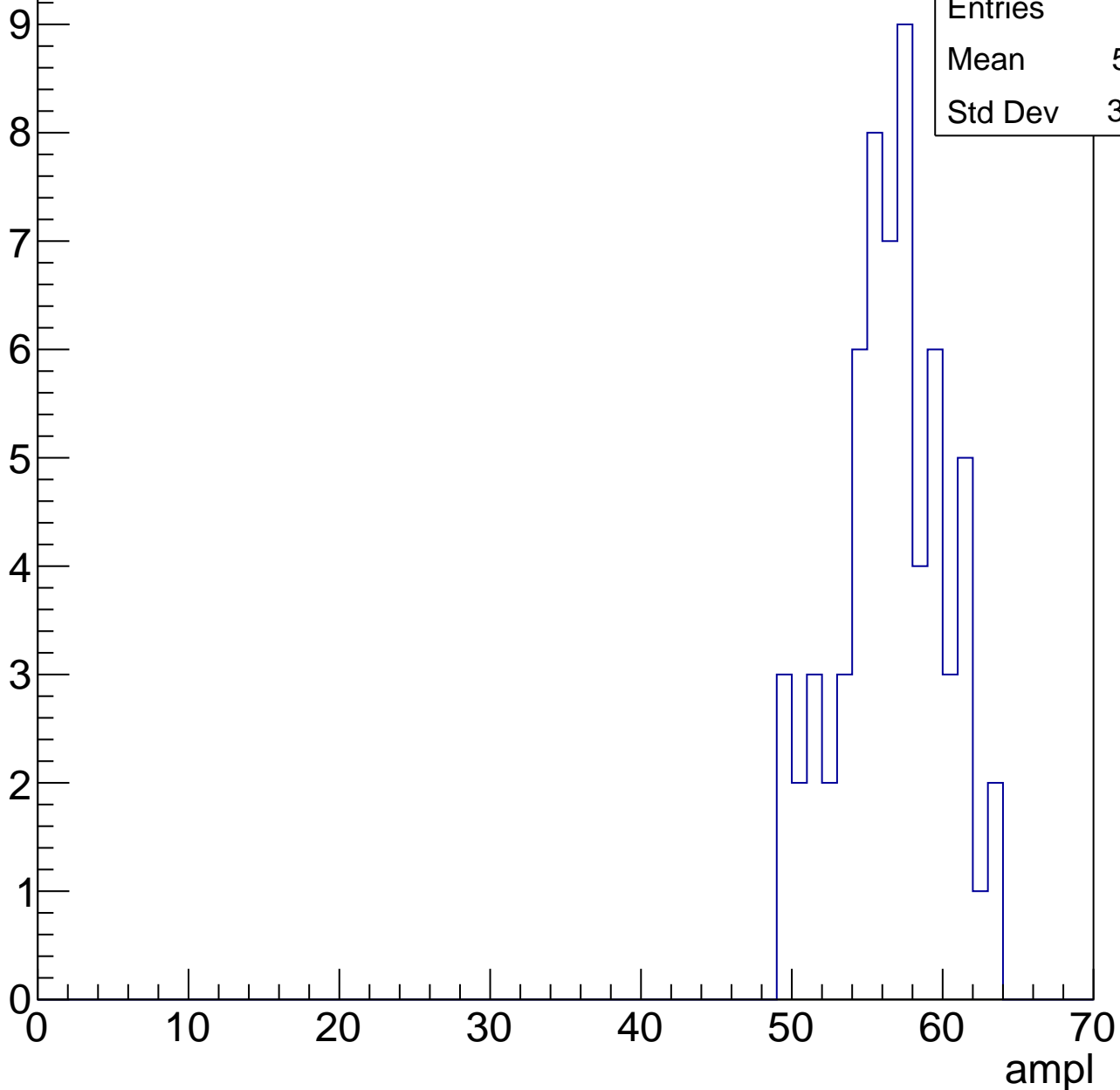


B1L102S, U13-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	56.11
Std Dev	3.469

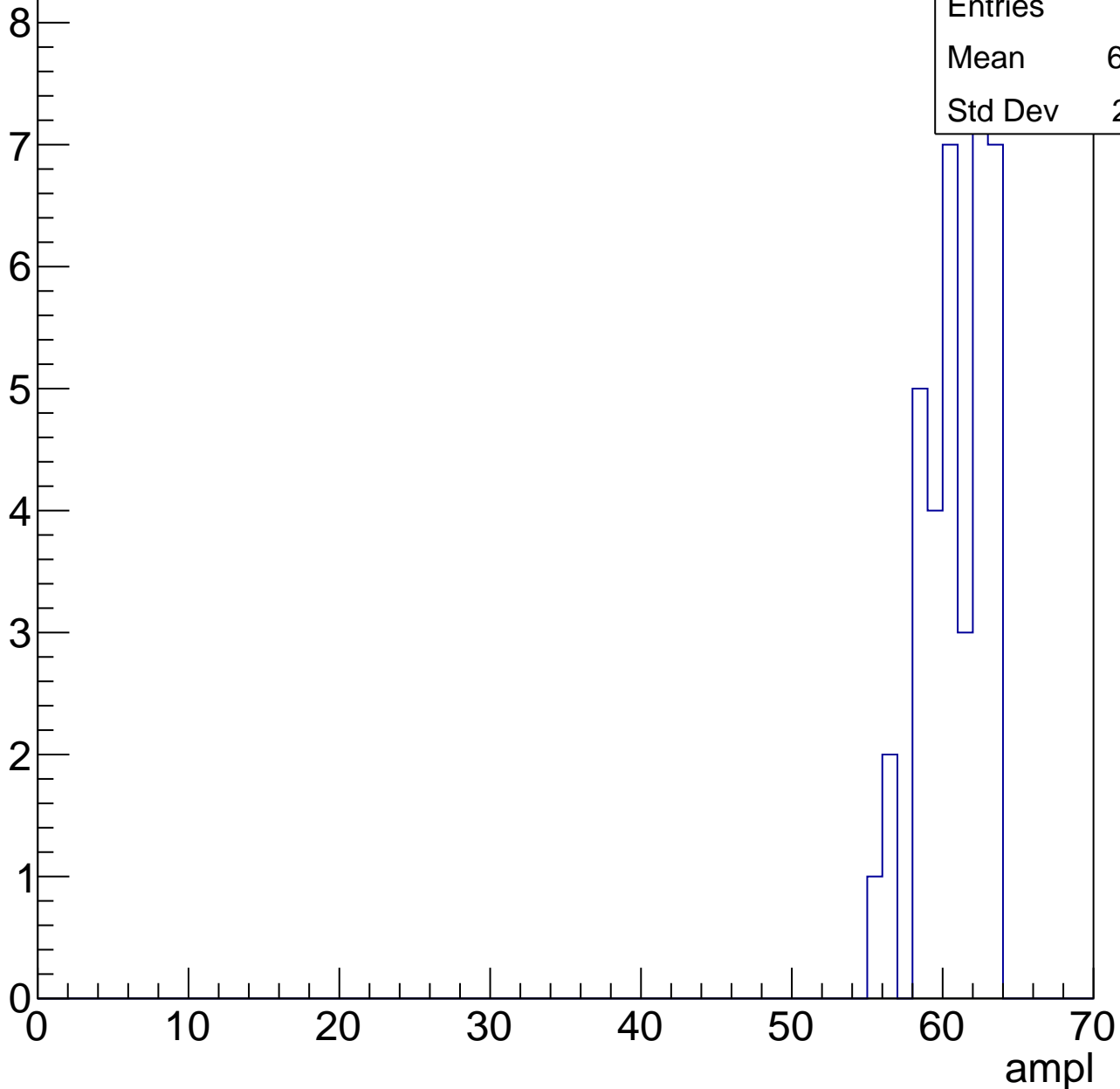


B1L102S, U13-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

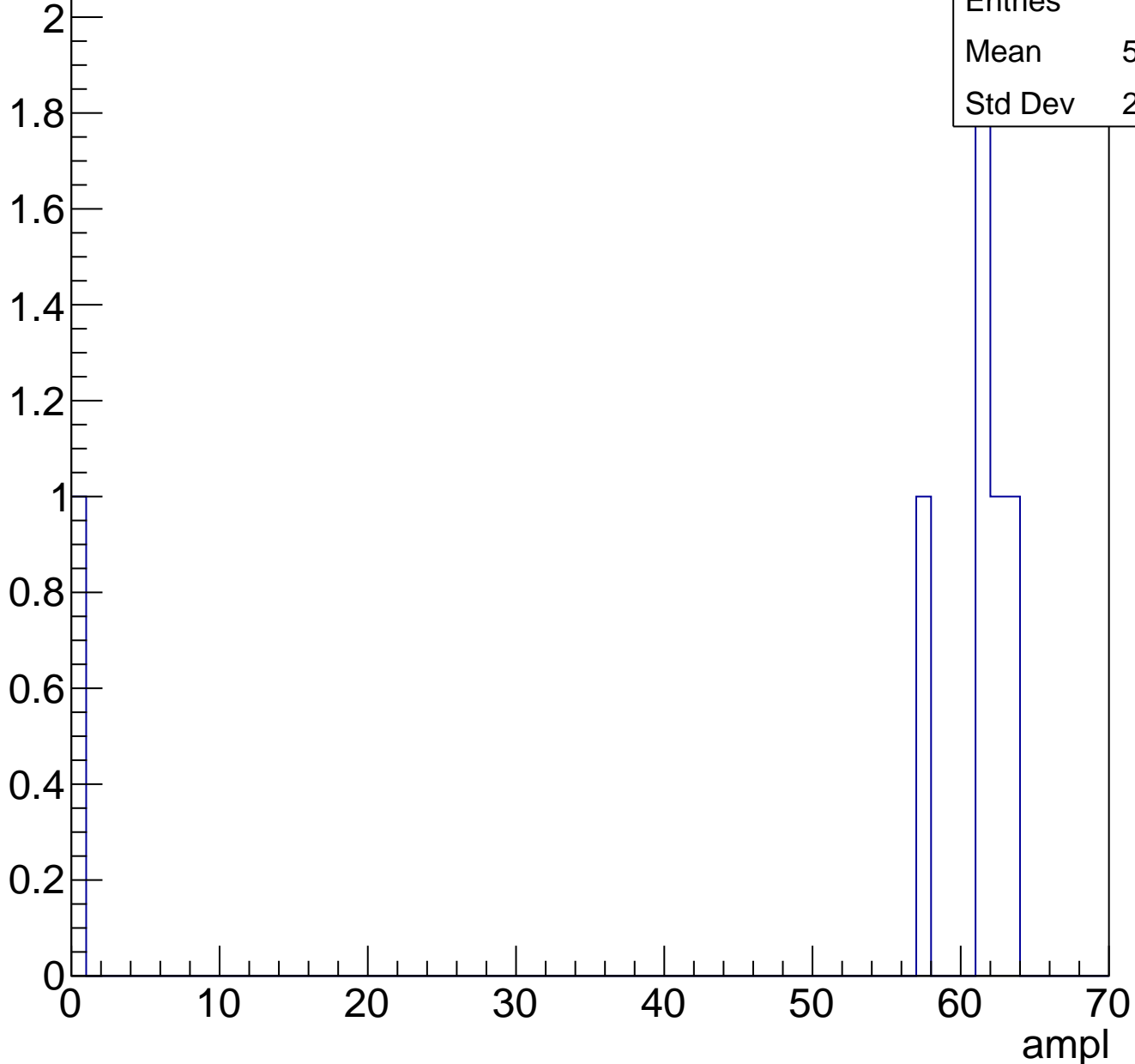
Entries	37
Mean	60.35
Std Dev	2.171



B1L102S, U13-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch89, adc0

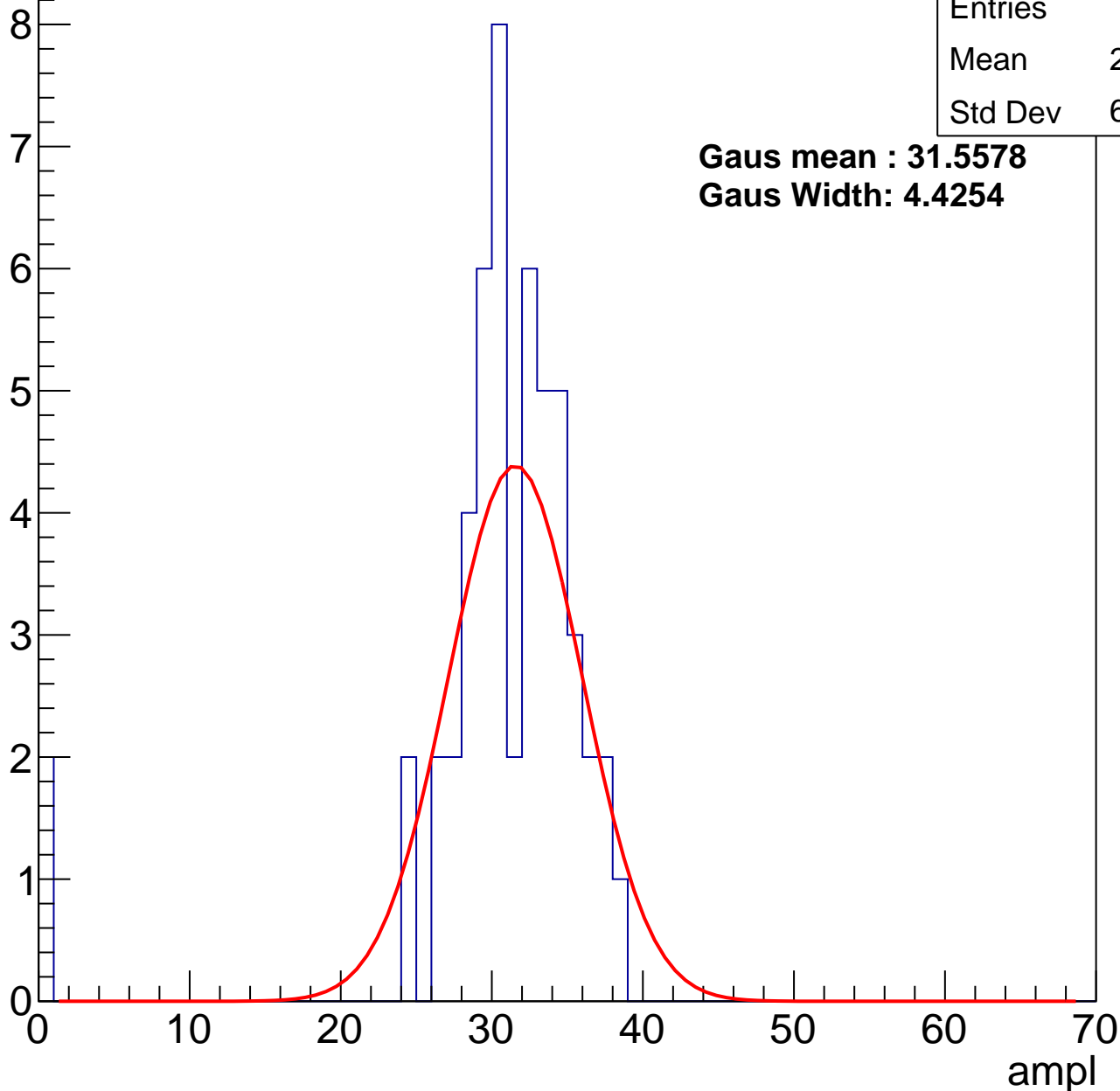
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	29.96
Std Dev	6.799

Gaus mean : 31.5578

Gaus Width: 4.4254



B1L102S, U13-ch89, adc1

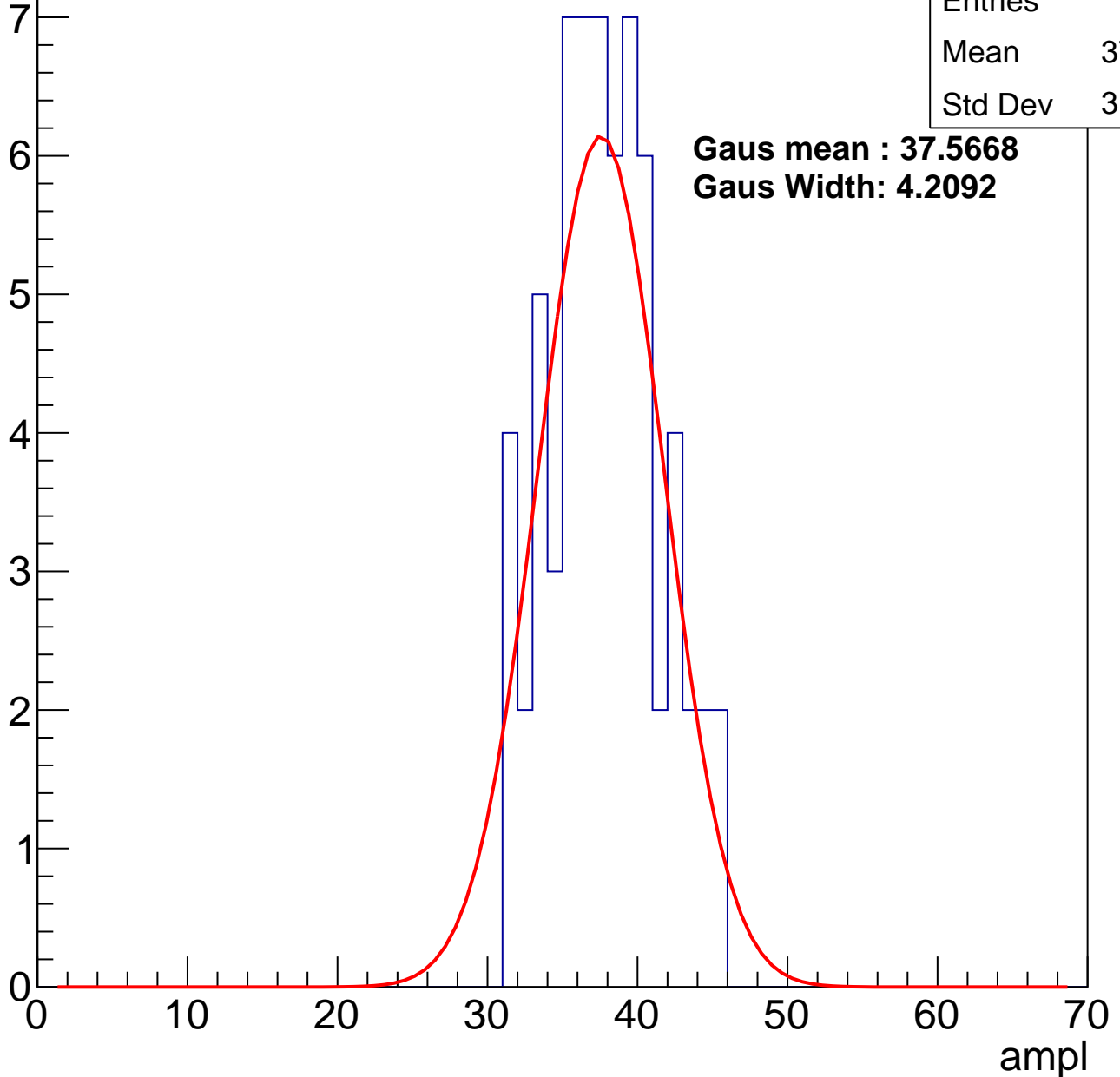
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	37.36
Std Dev	3.579

Gaus mean : 37.5668

Gaus Width: 4.2092



B1L102S, U13-ch89, adc2

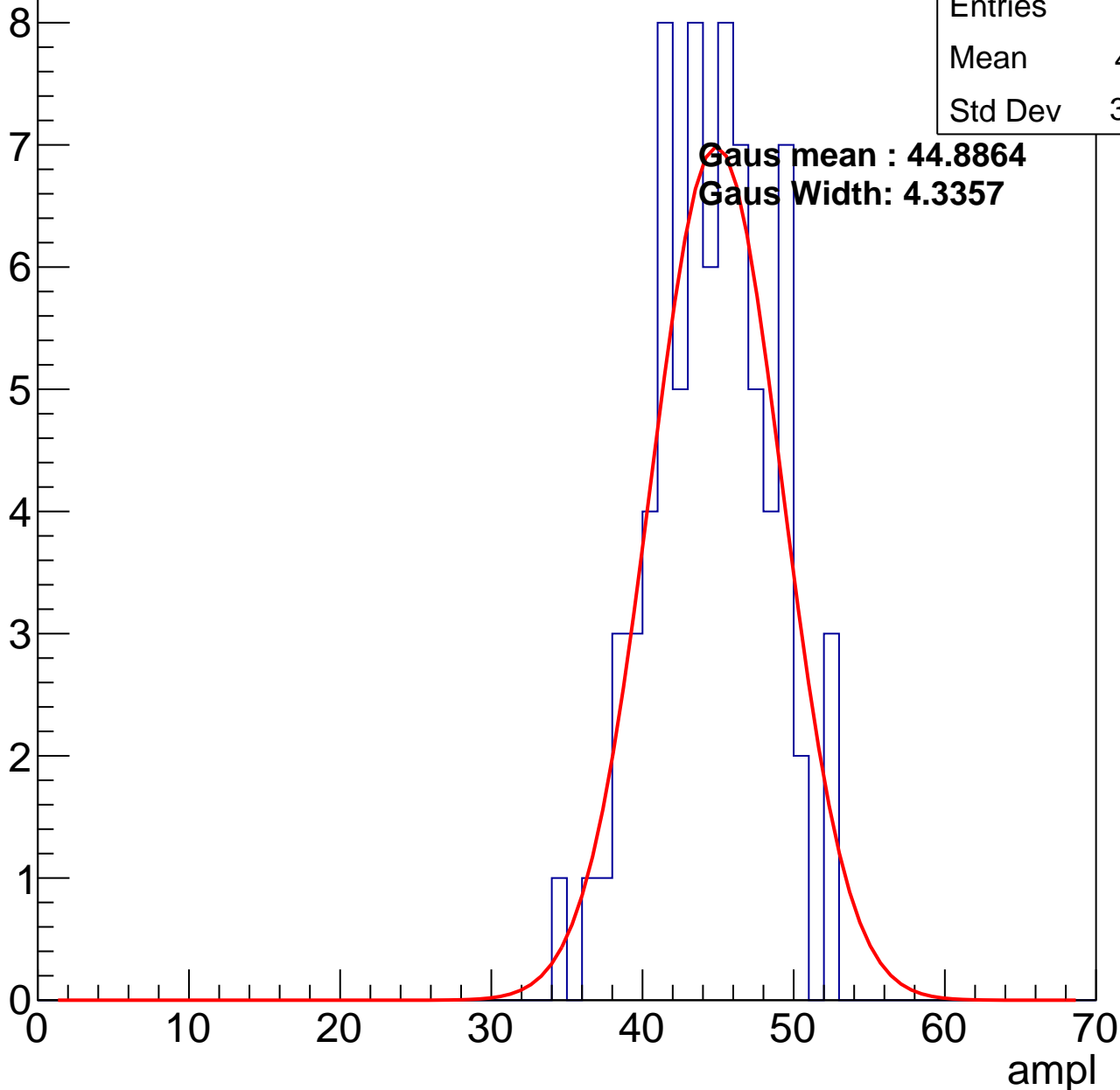
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	44.11
Std Dev	3.878

Gaus mean : 44.8864

Gaus Width: 4.3357

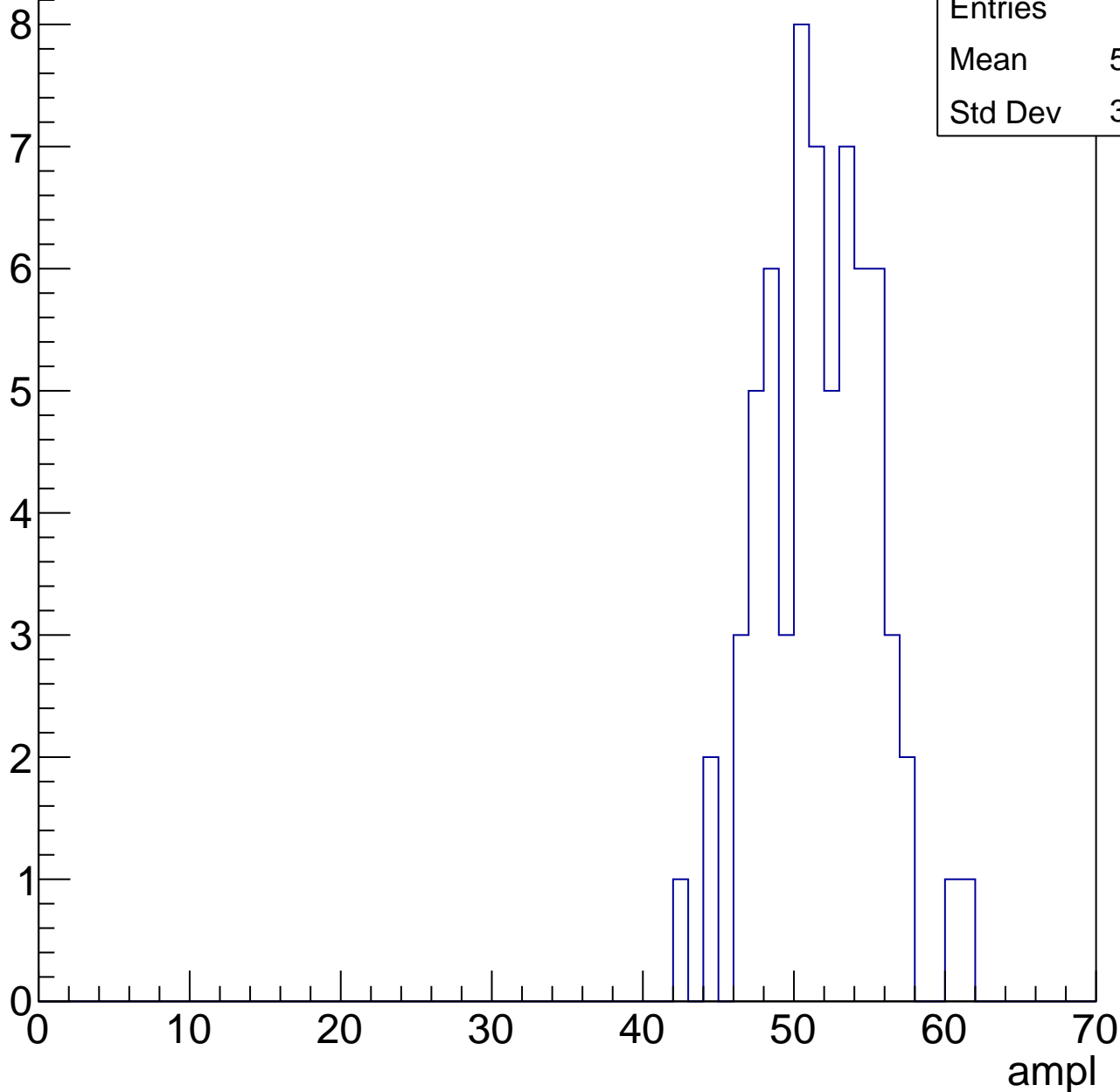


B1L102S, U13-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	51.26
Std Dev	3.723

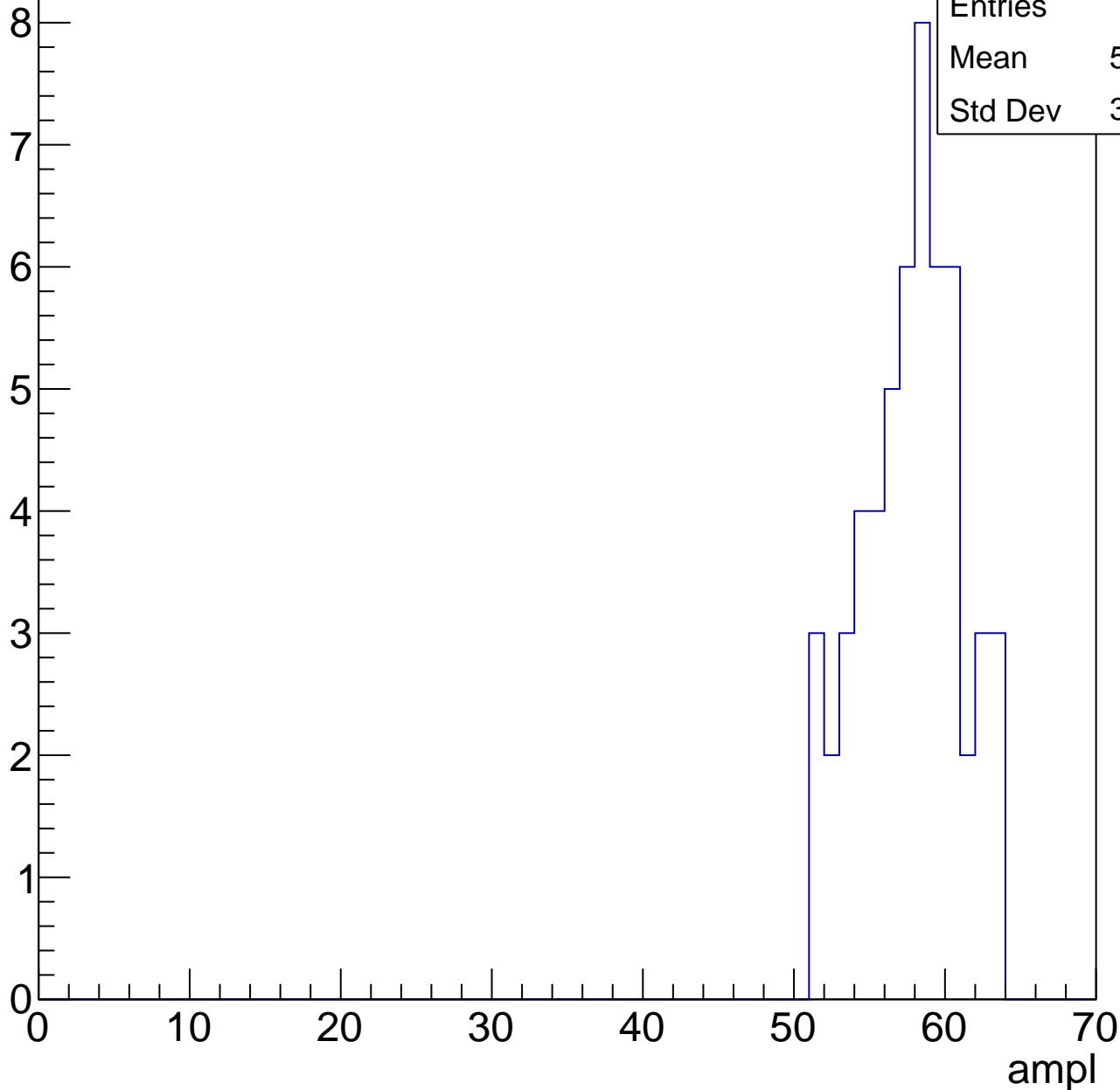


B1L102S, U13-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

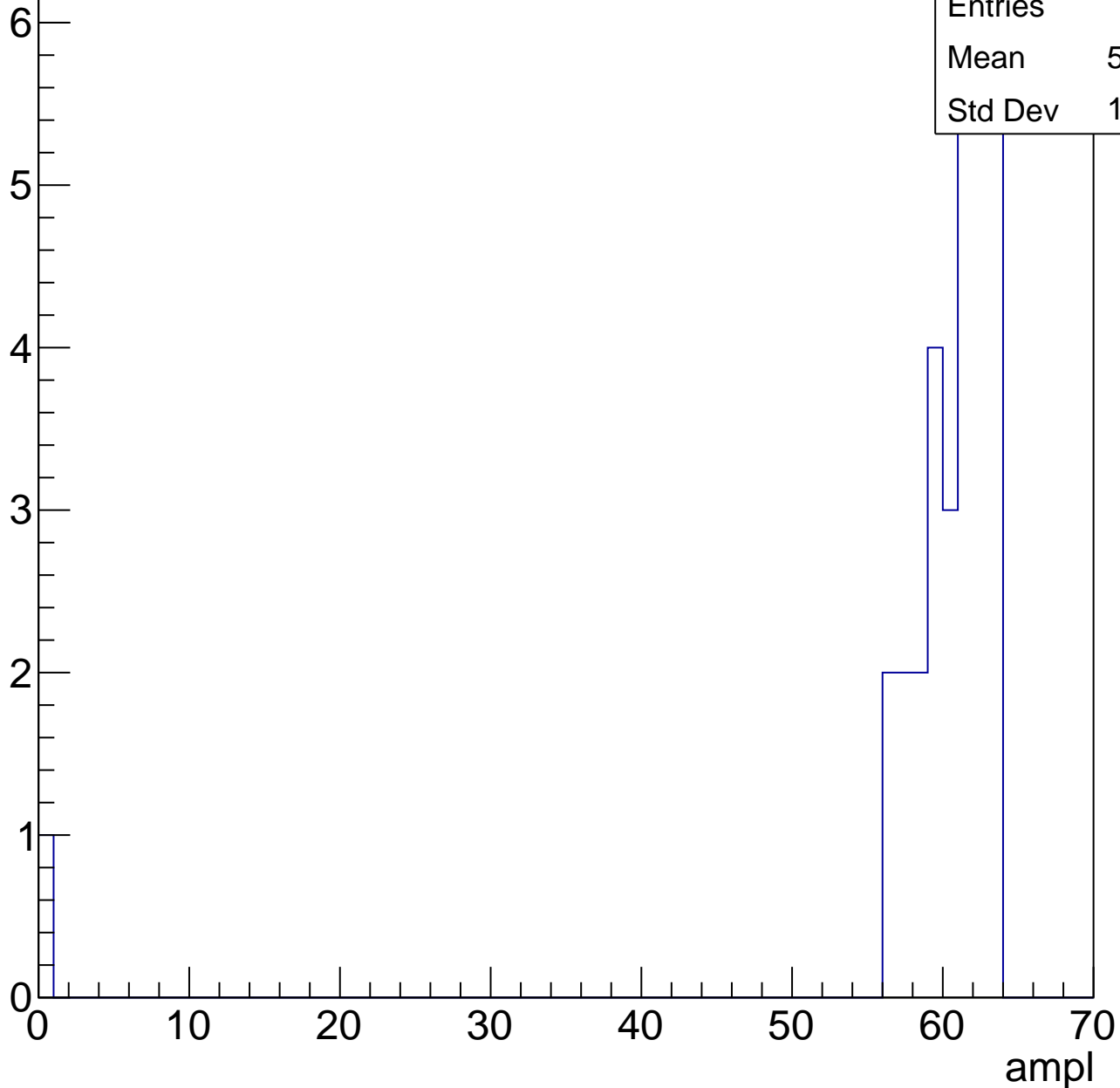
Entries	55
Mean	57.25
Std Dev	3.192



B1L102S, U13-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch90, adc0

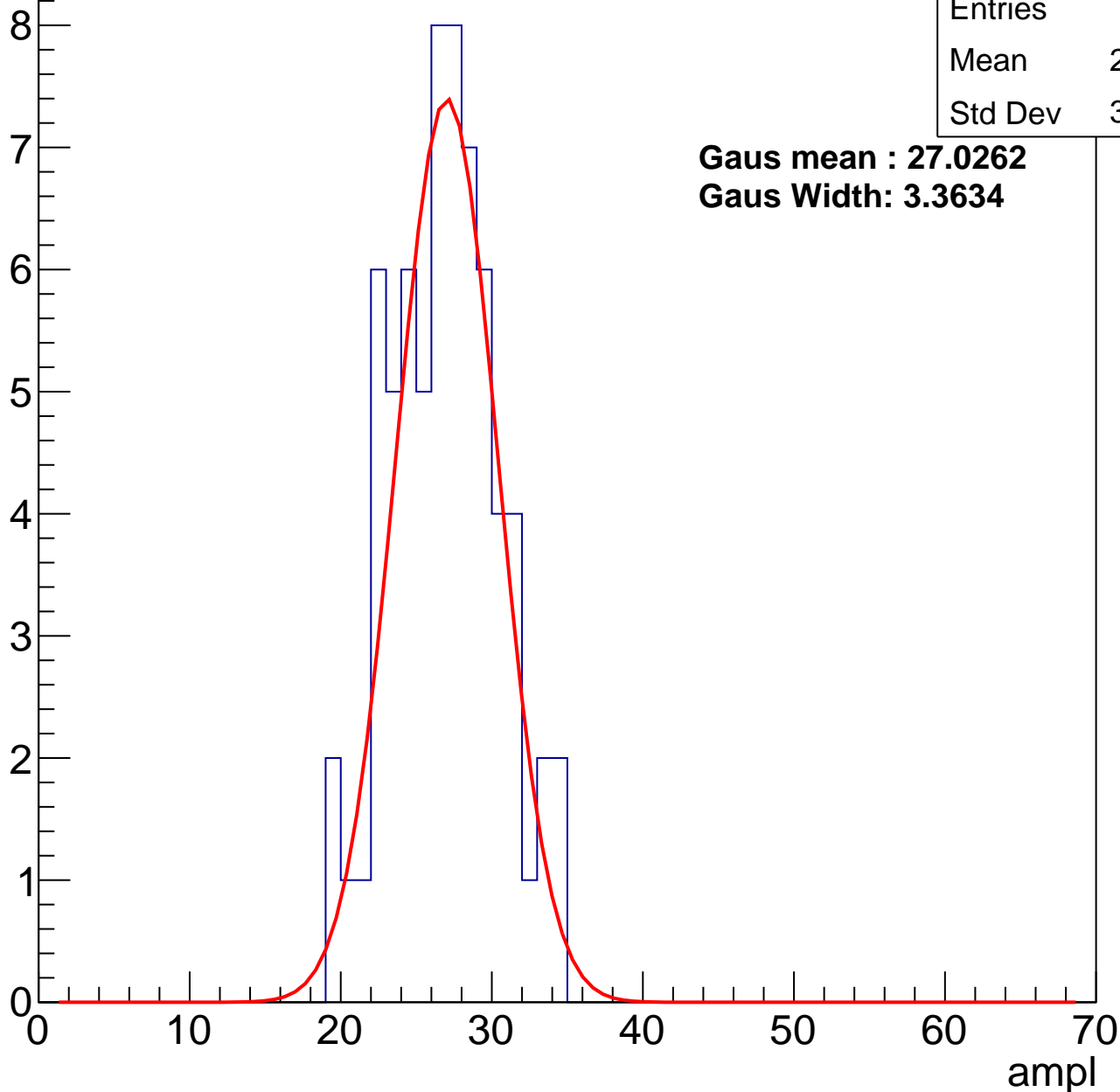
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	26.46
Std Dev	3.496

Gaus mean : 27.0262

Gaus Width: 3.3634



B1L102S, U13-ch90, adc1

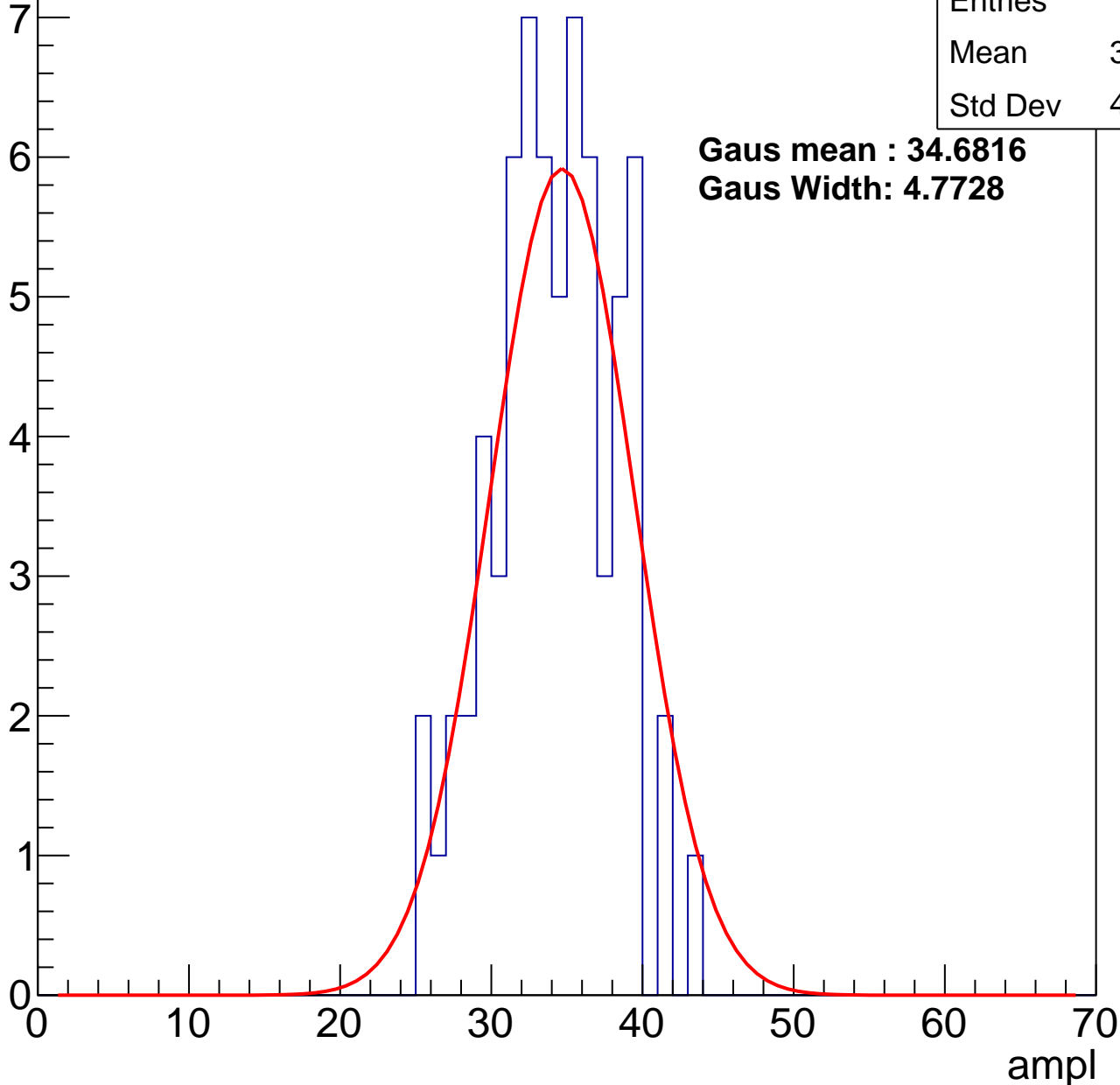
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	33.69
Std Dev	4.008

Gaus mean : 34.6816

Gaus Width: 4.7728



B1L102S, U13-ch90, adc2

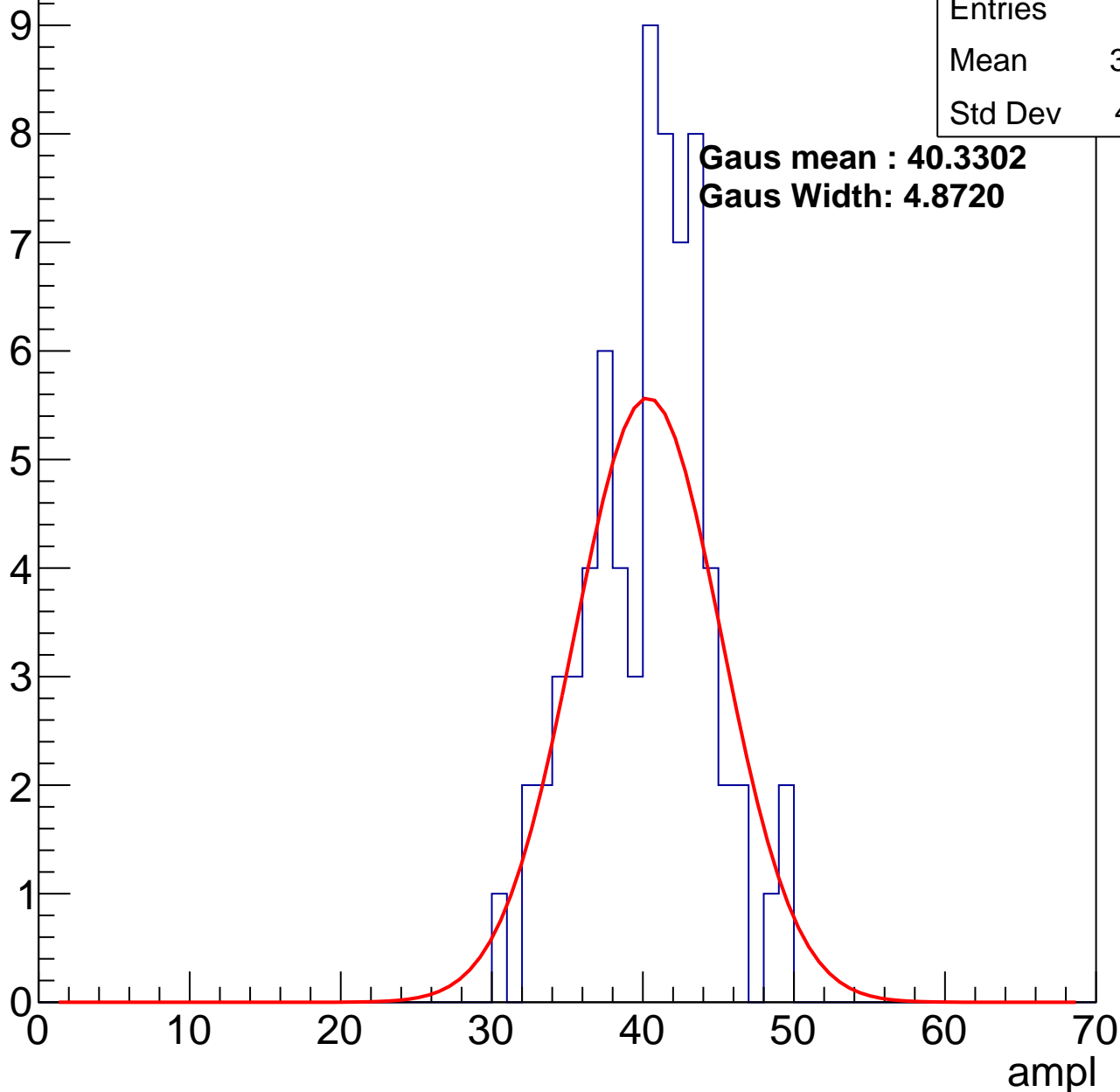
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	39.89
Std Dev	4.051

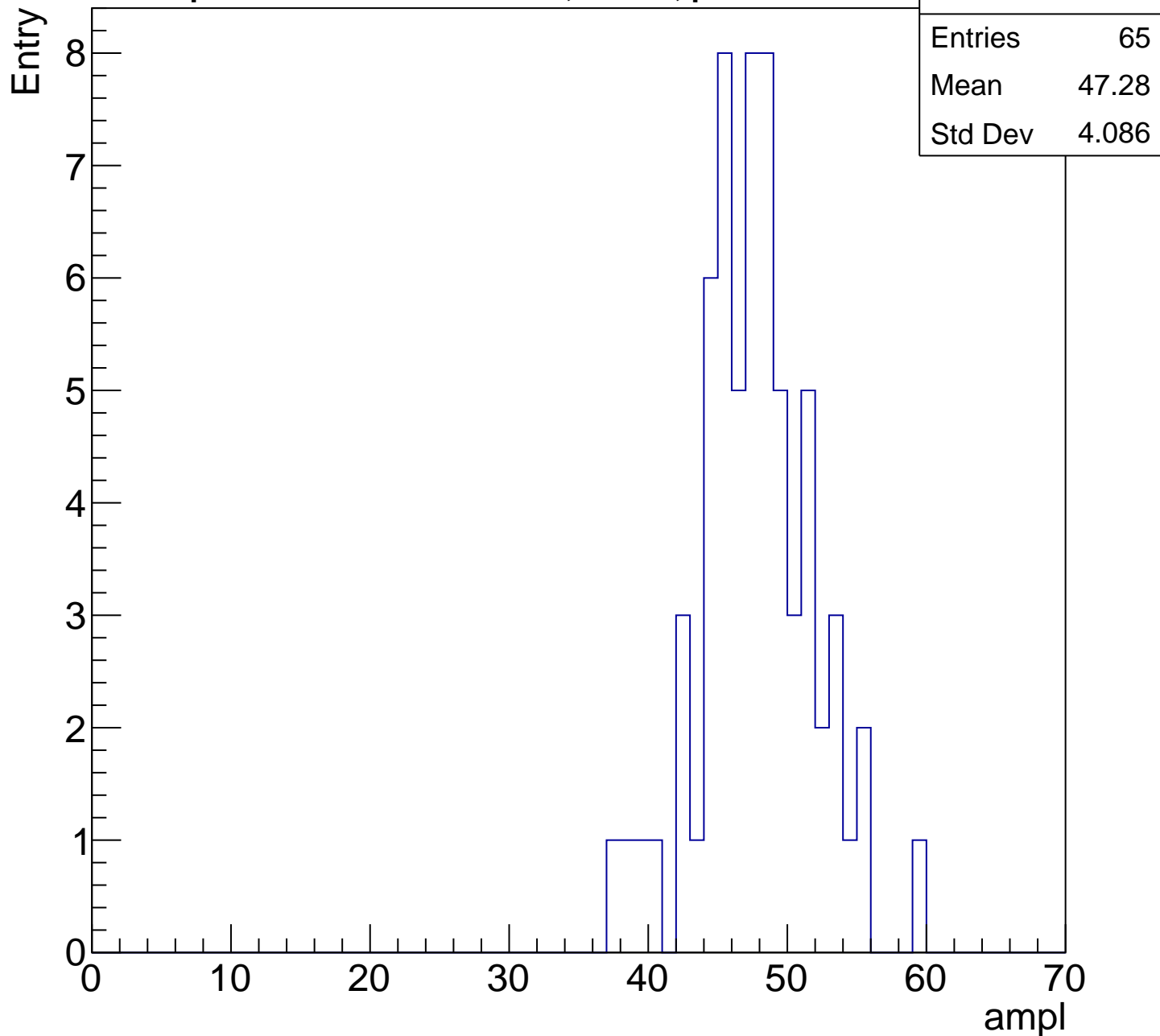
Gaus mean : 40.3302

Gaus Width: 4.8720



B1L102S, U13-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

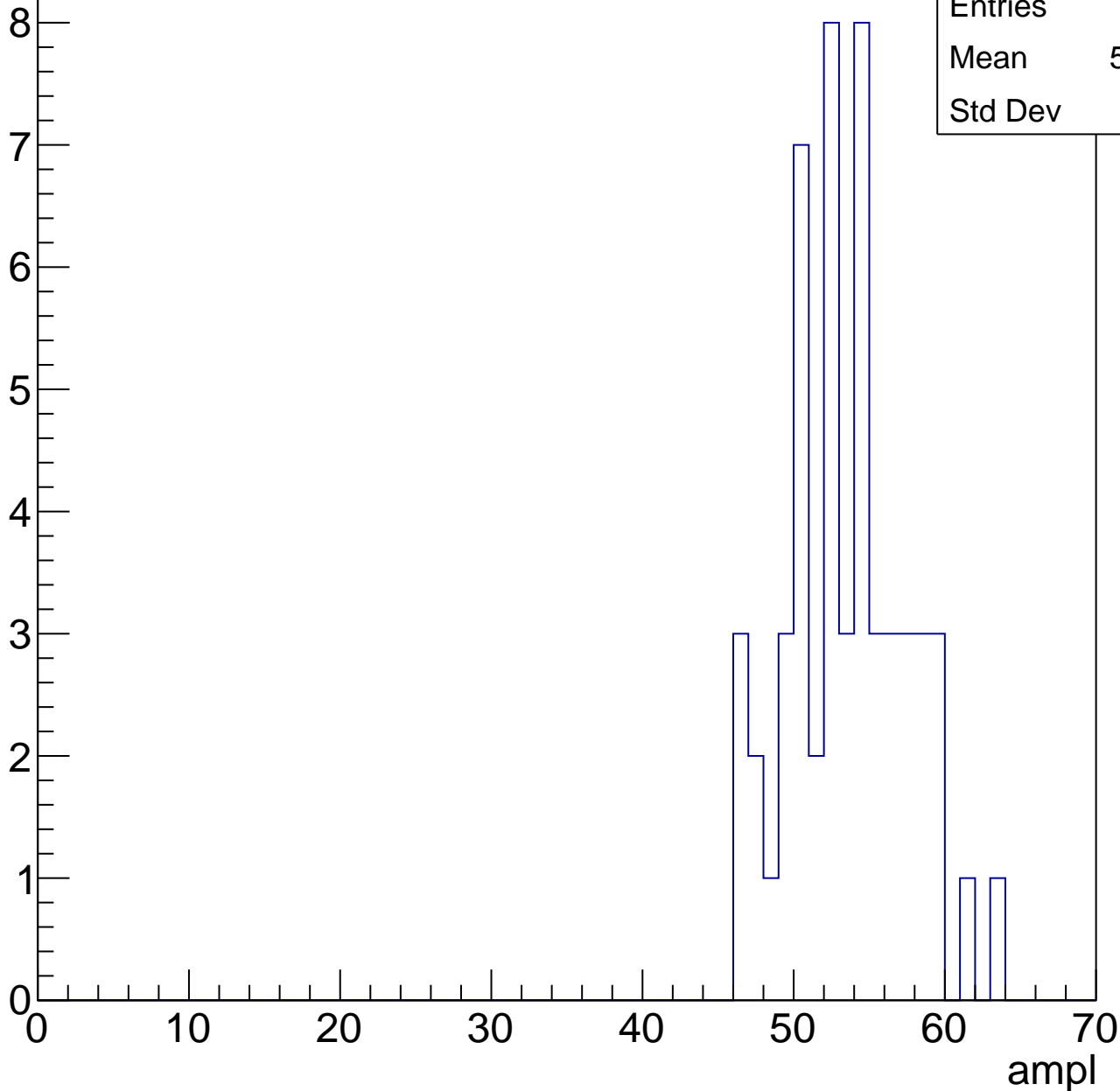


B1L102S, U13-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.06
Std Dev	3.88

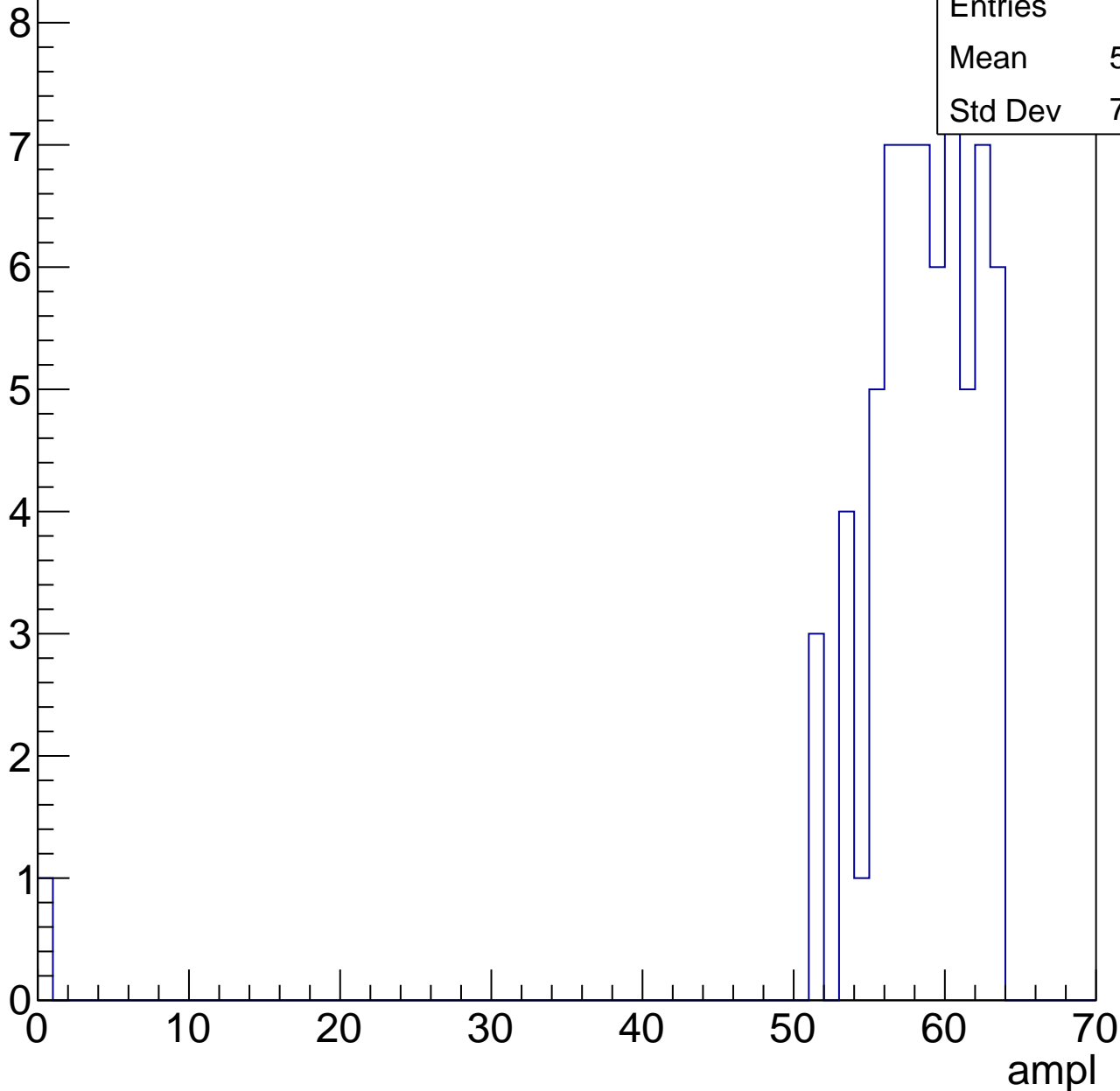


B1L102S, U13-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

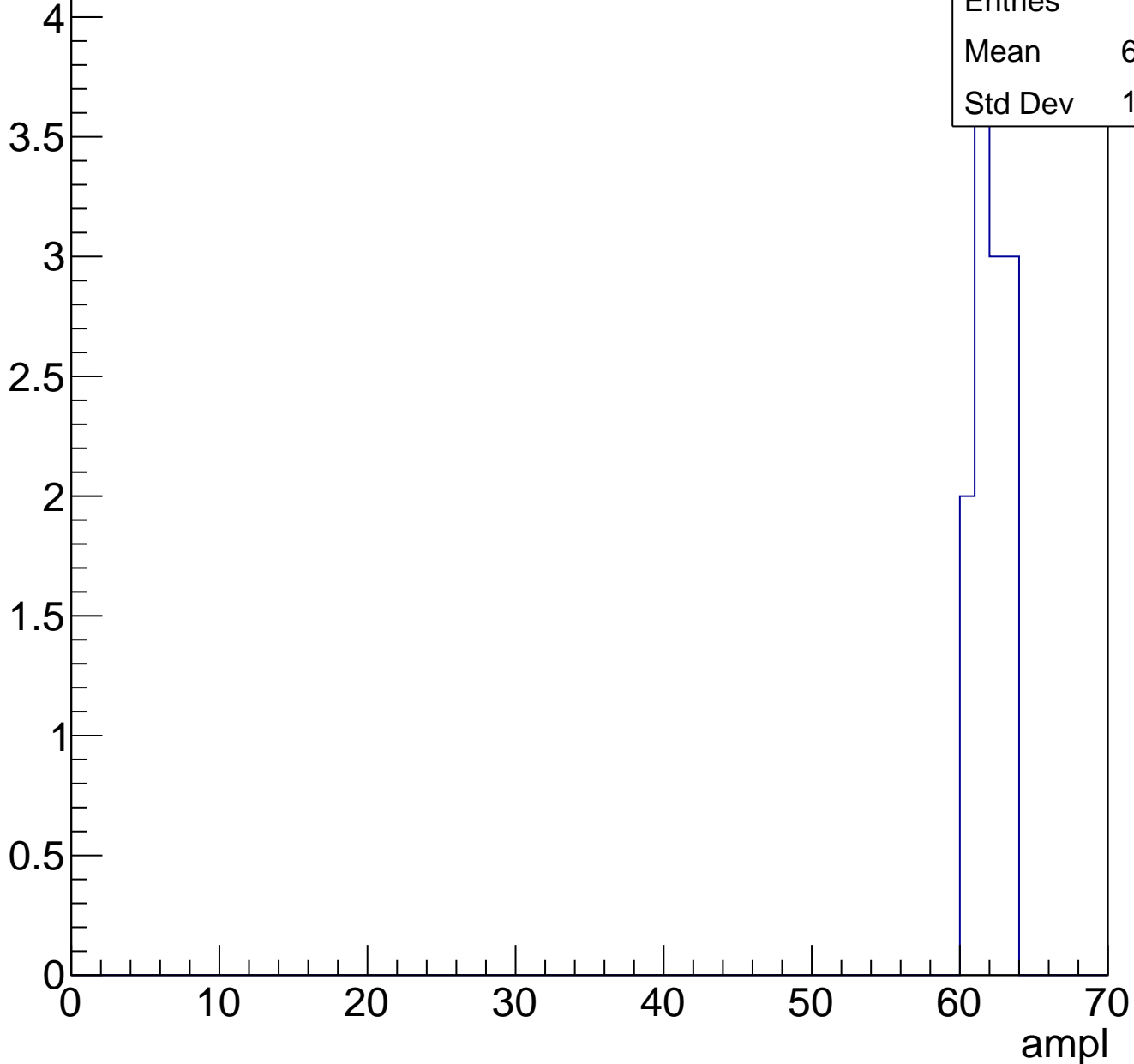
Entries	67
Mean	57.34
Std Dev	7.749



B1L102S, U13-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

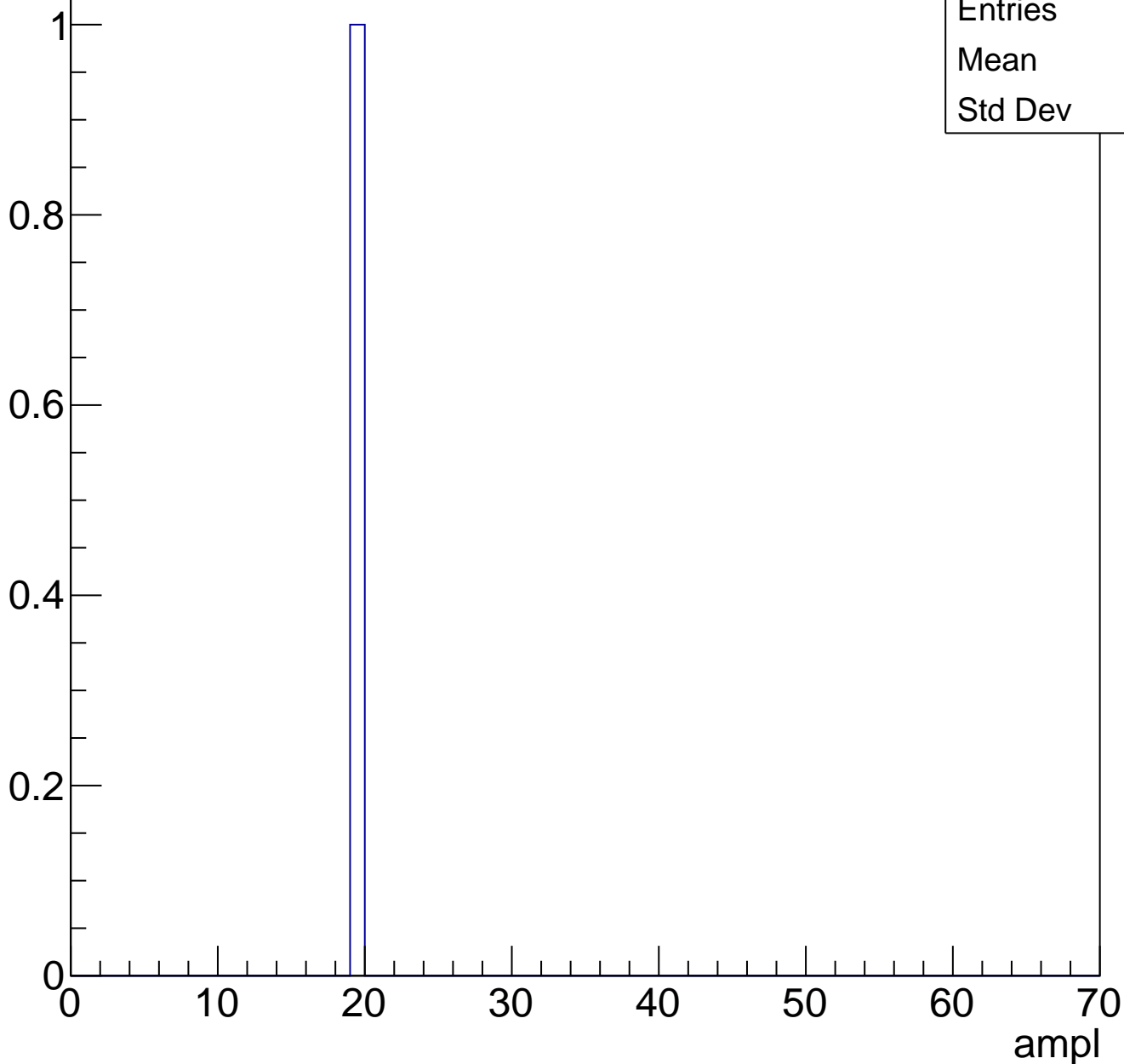
Entry



B1L102S, U13-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch91, adc0

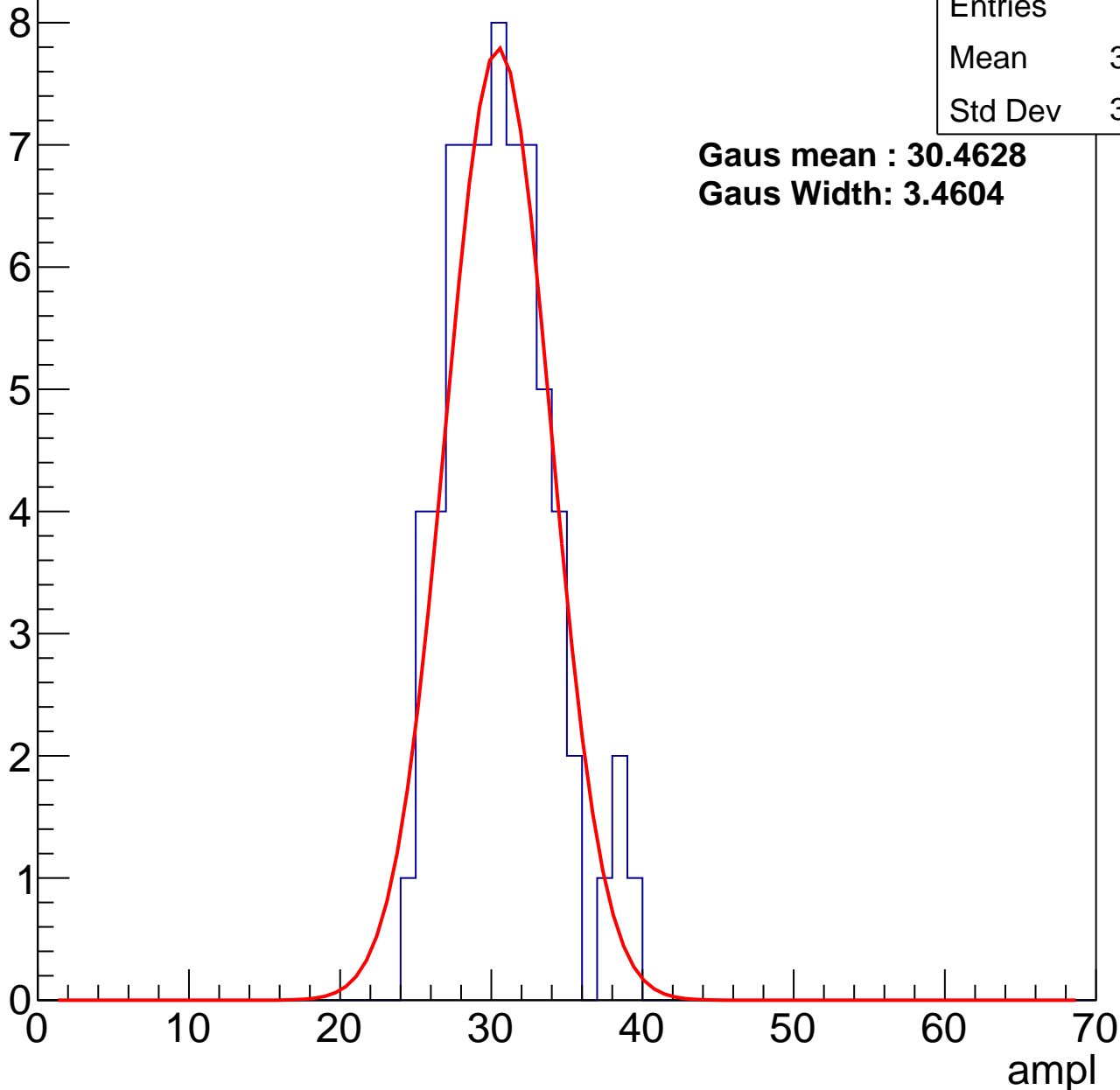
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30.15
Std Dev	3.342

Gaus mean : 30.4628

Gaus Width: 3.4604



B1L102S, U13-ch91, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	37.13
Std Dev	3.736

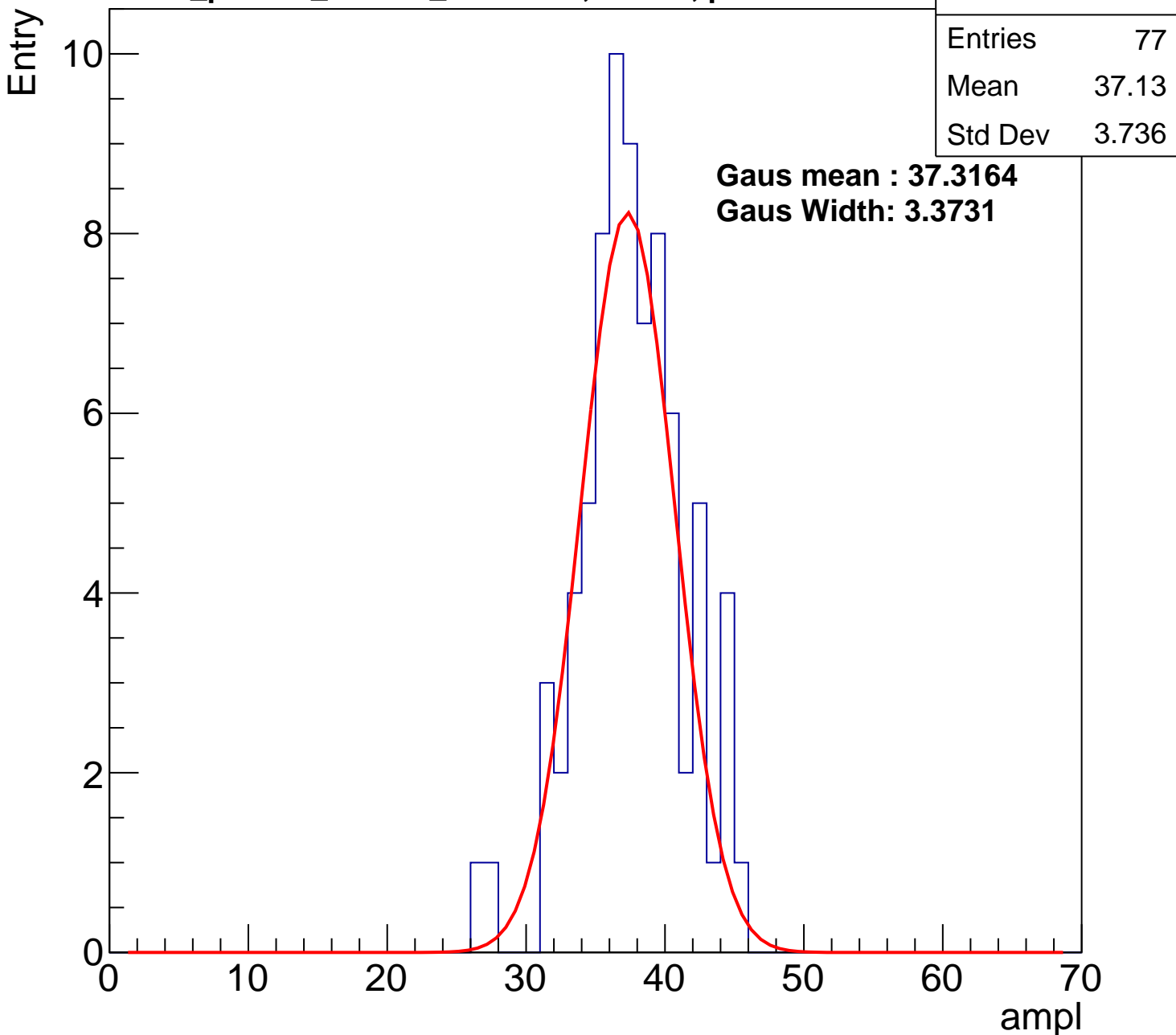
Gaus mean : 37.3164
Gaus Width: 3.3731

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch91, adc2

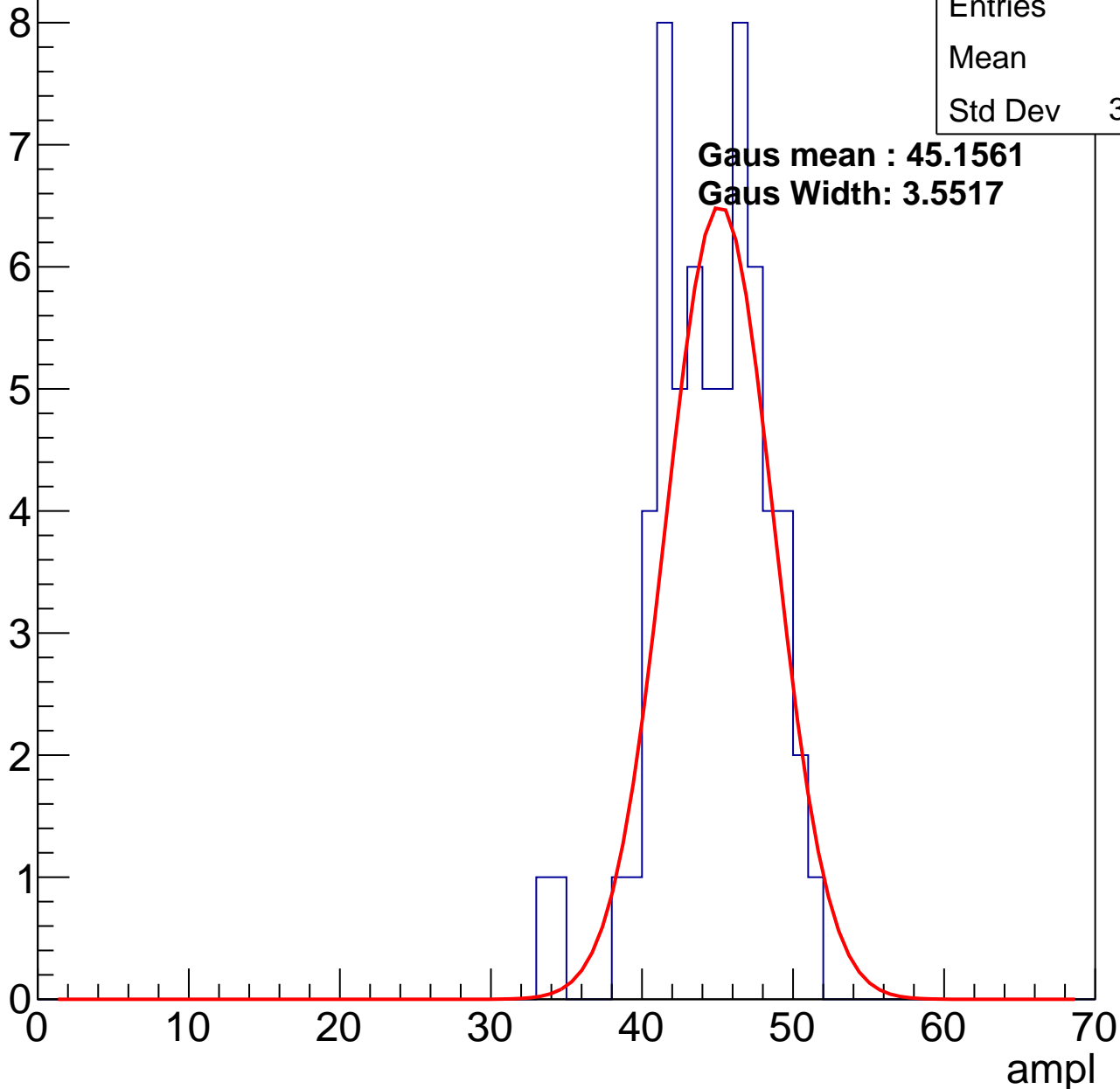
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.1
Std Dev	3.635

Gaus mean : 45.1561

Gaus Width: 3.5517



B1L102S, U13-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	50.75
Std Dev	3.584

Entry

10

8

6

4

2

0

0

10

20

30

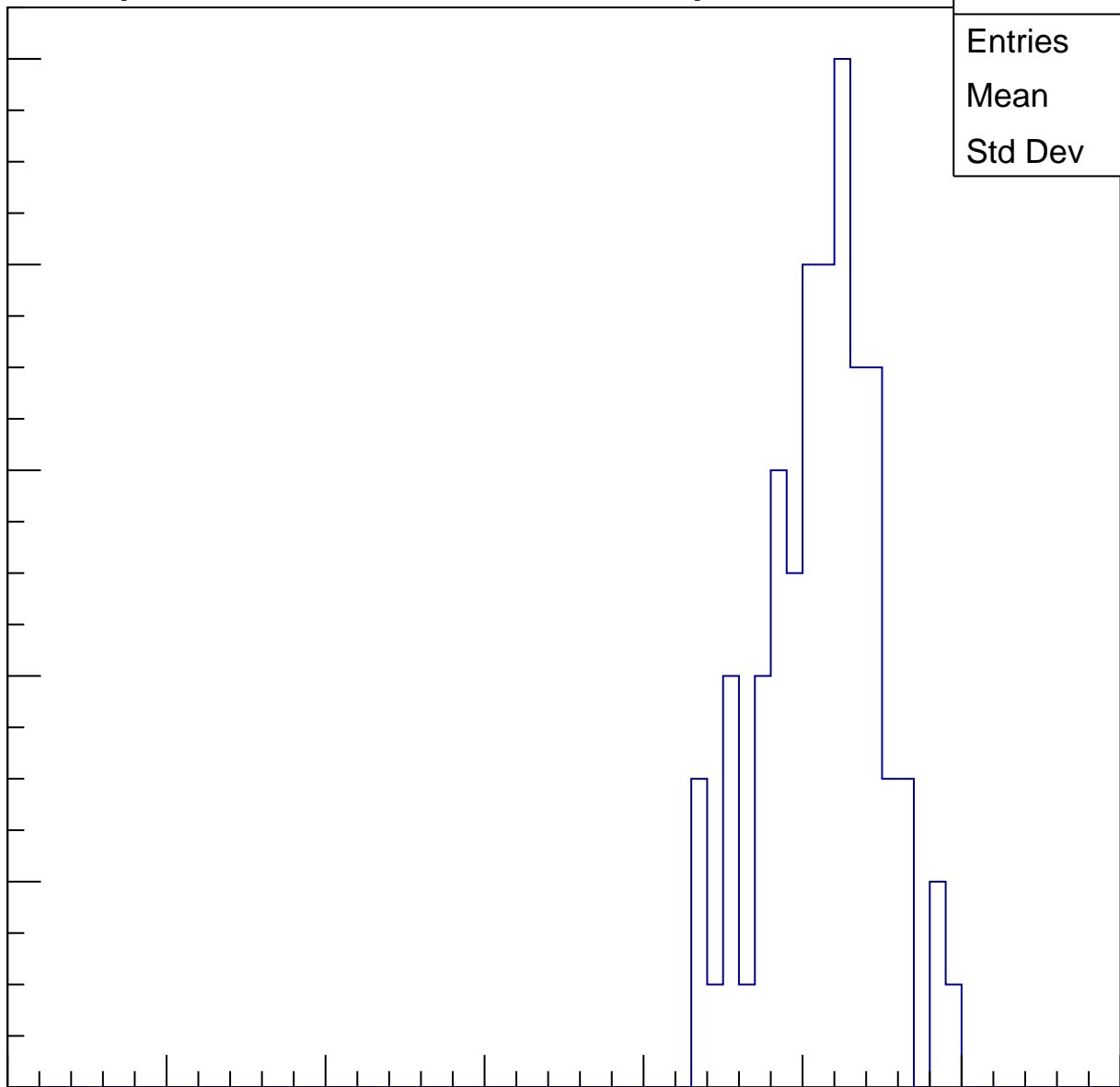
40

50

60

70

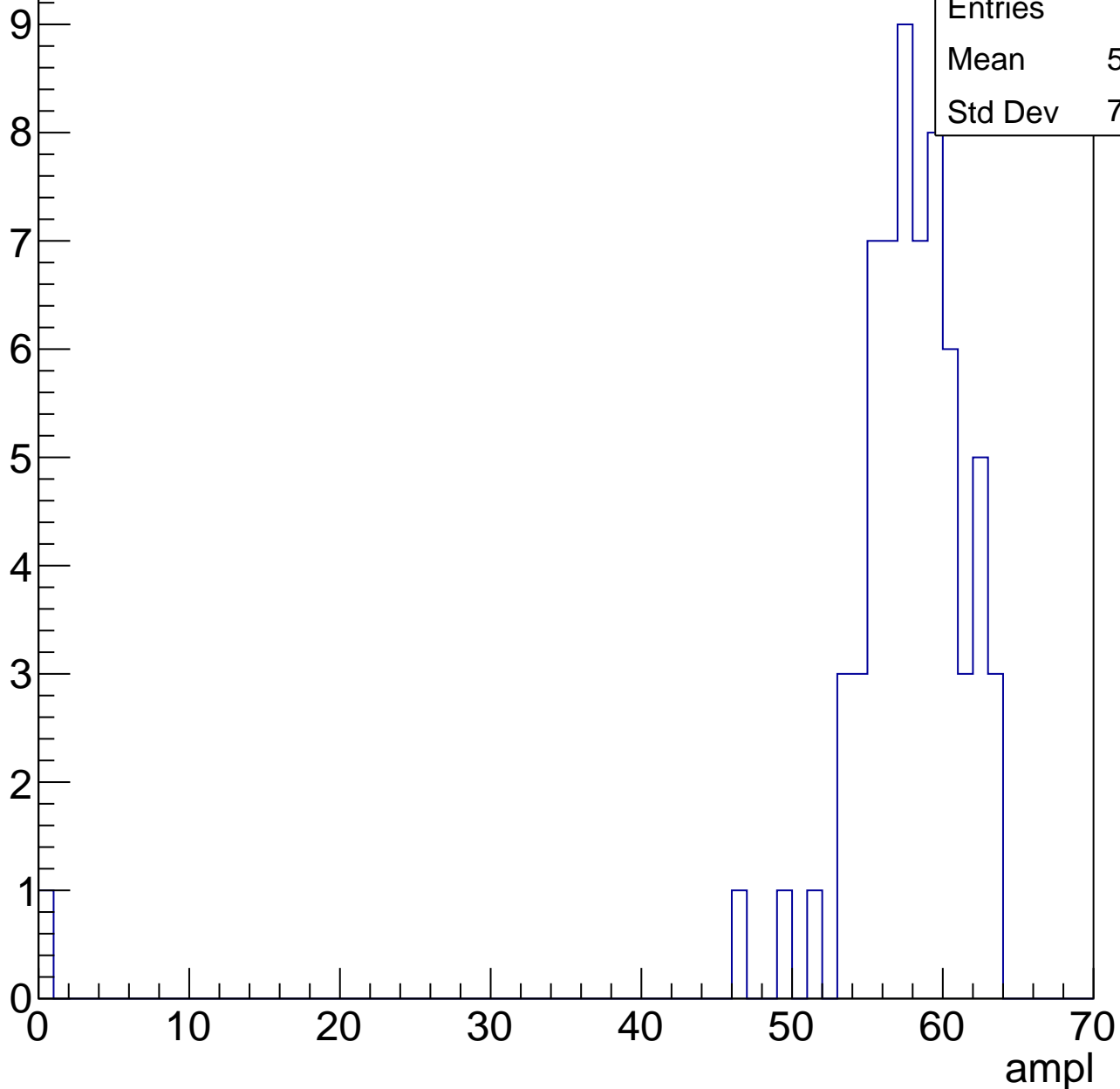
ampl



B1L102S, U13-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

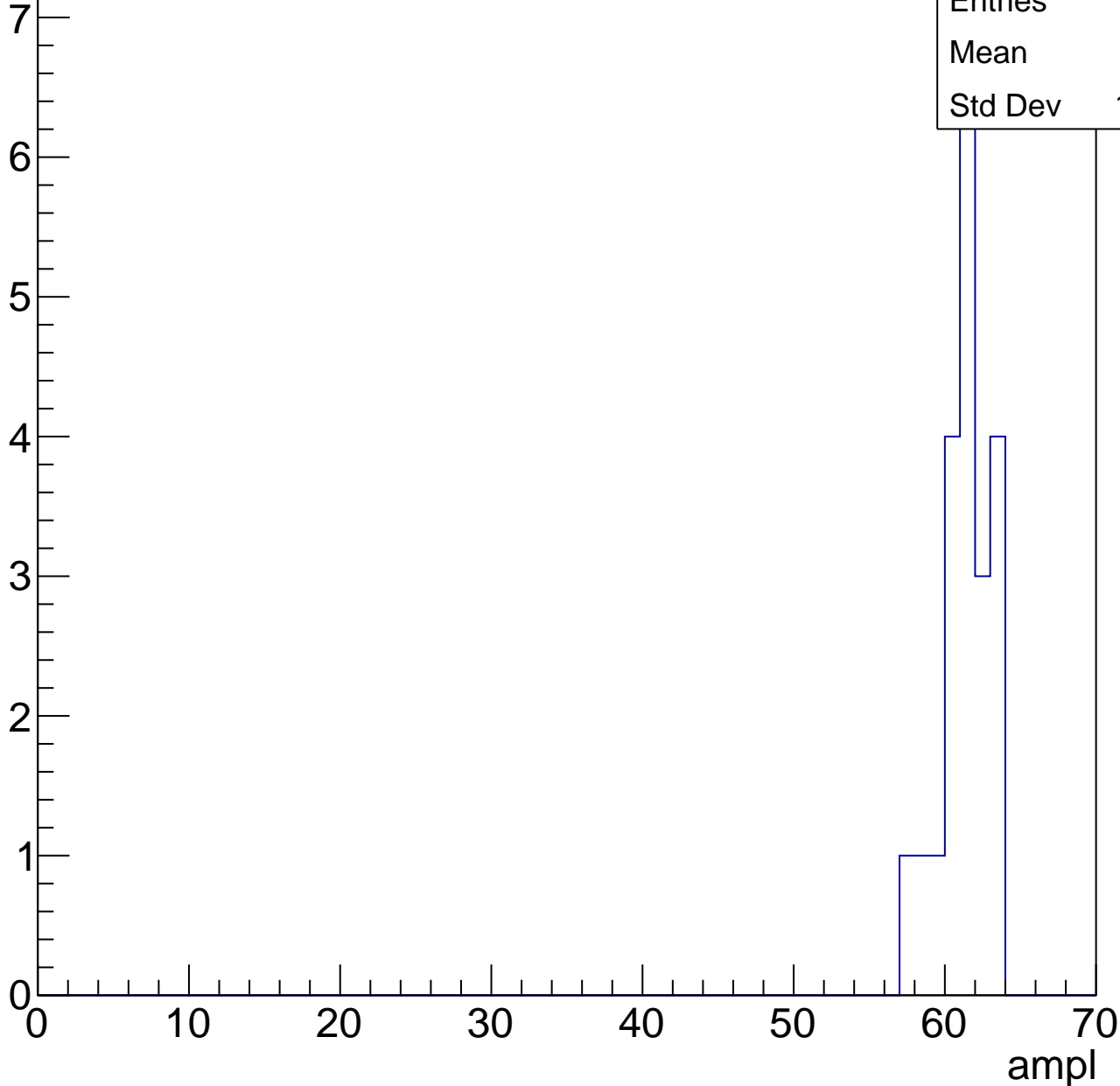


B1L102S, U13-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	60.9
Std Dev	1.571



B1L102S, U13-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch92, adc0

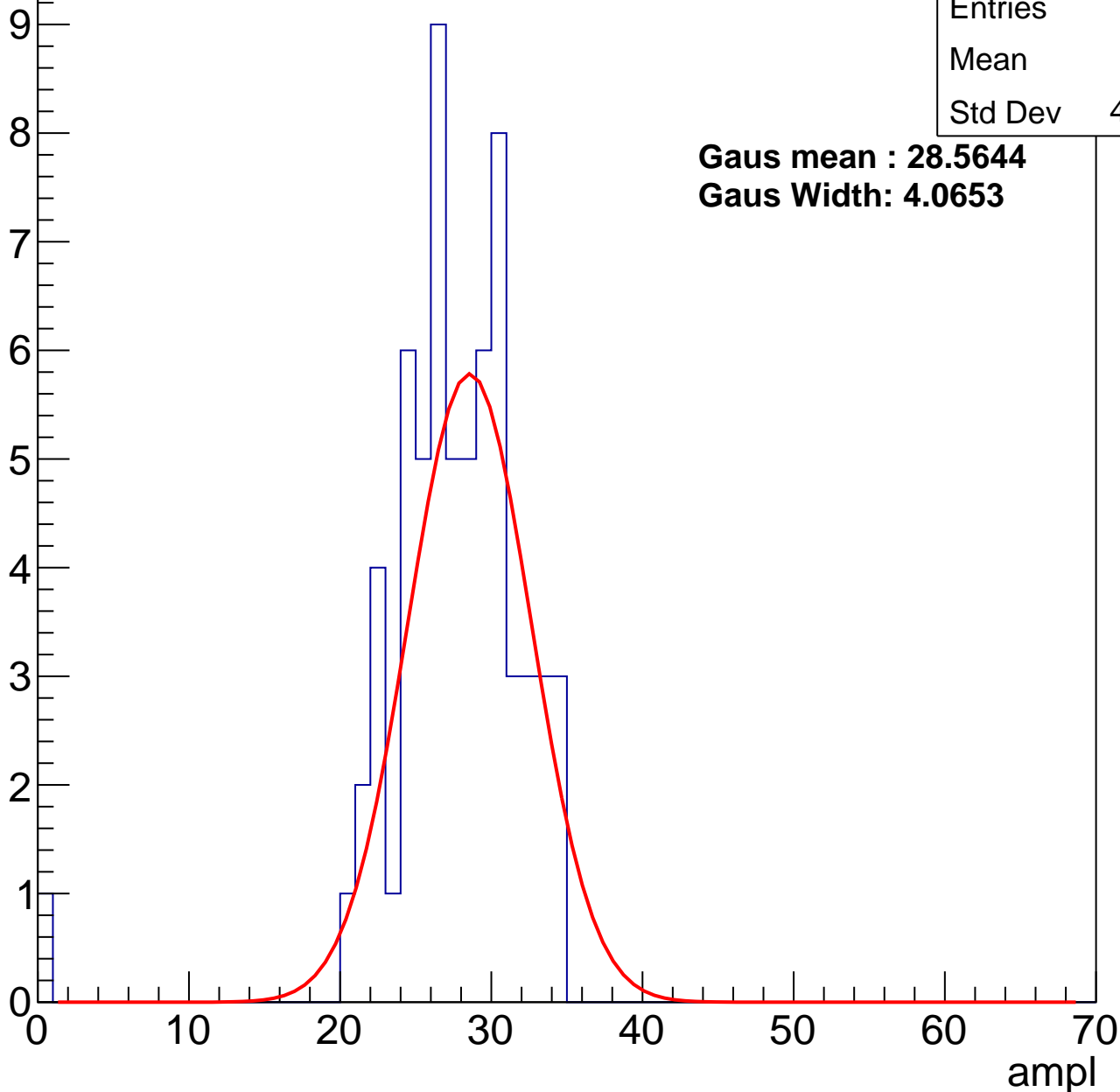
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27
Std Dev	4.848

Gaus mean : 28.5644

Gaus Width: 4.0653



B1L102S, U13-ch92, adc1

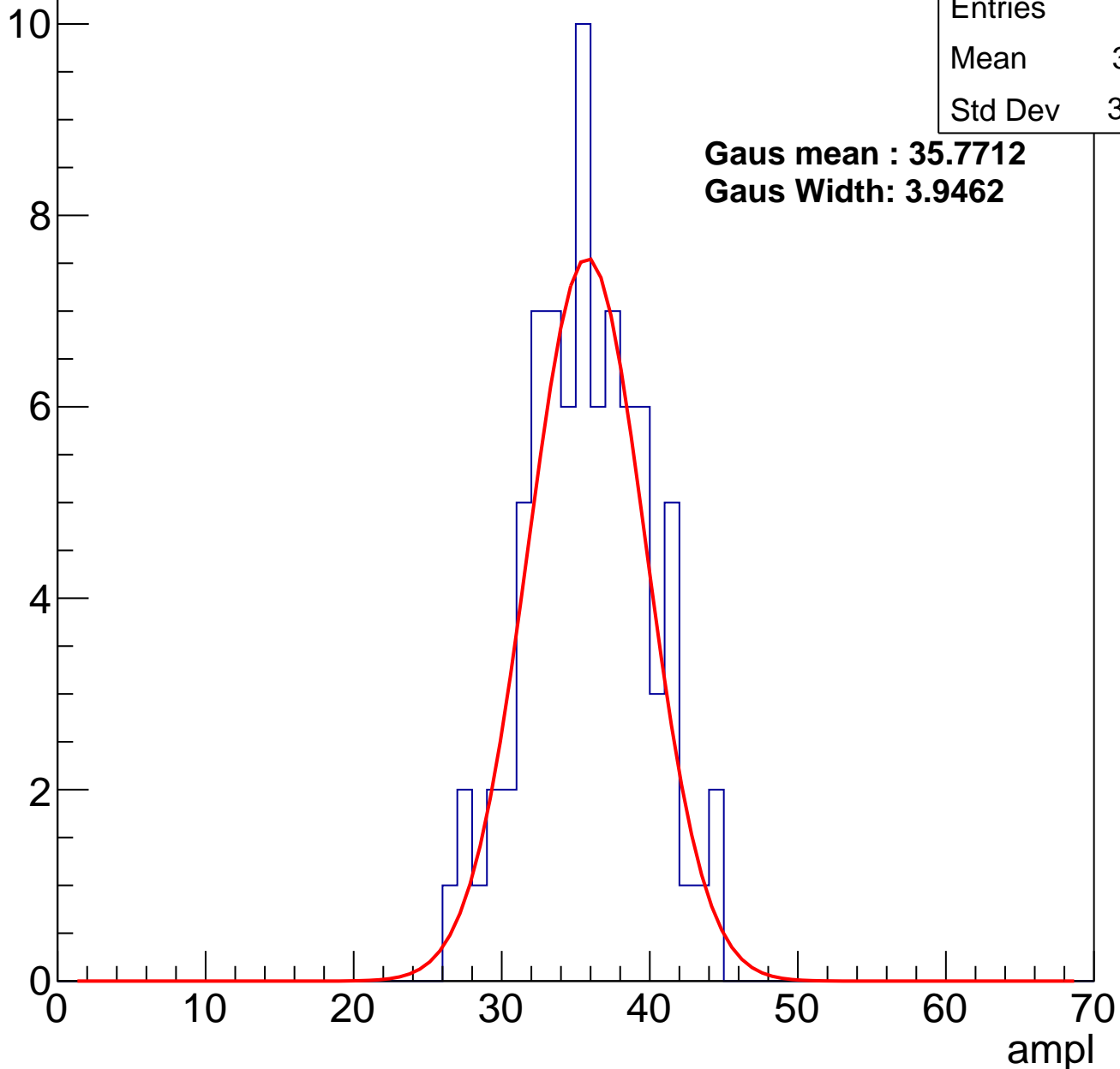
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	35.31
Std Dev	3.974

Gaus mean : 35.7712

Gaus Width: 3.9462

Entry



B1L102S, U13-ch92, adc2

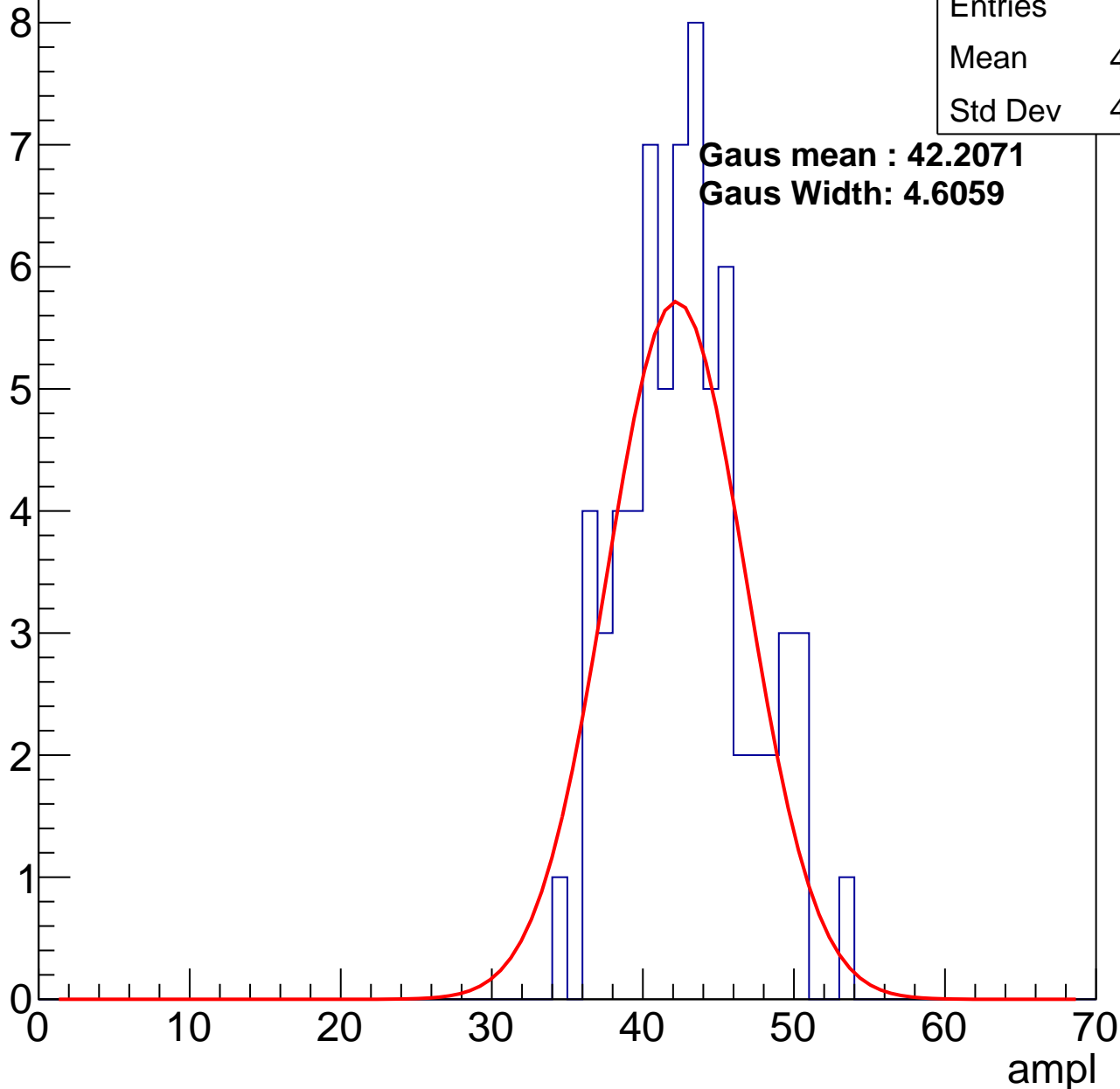
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.42
Std Dev	4.052

Gaus mean : 42.2071

Gaus Width: 4.6059

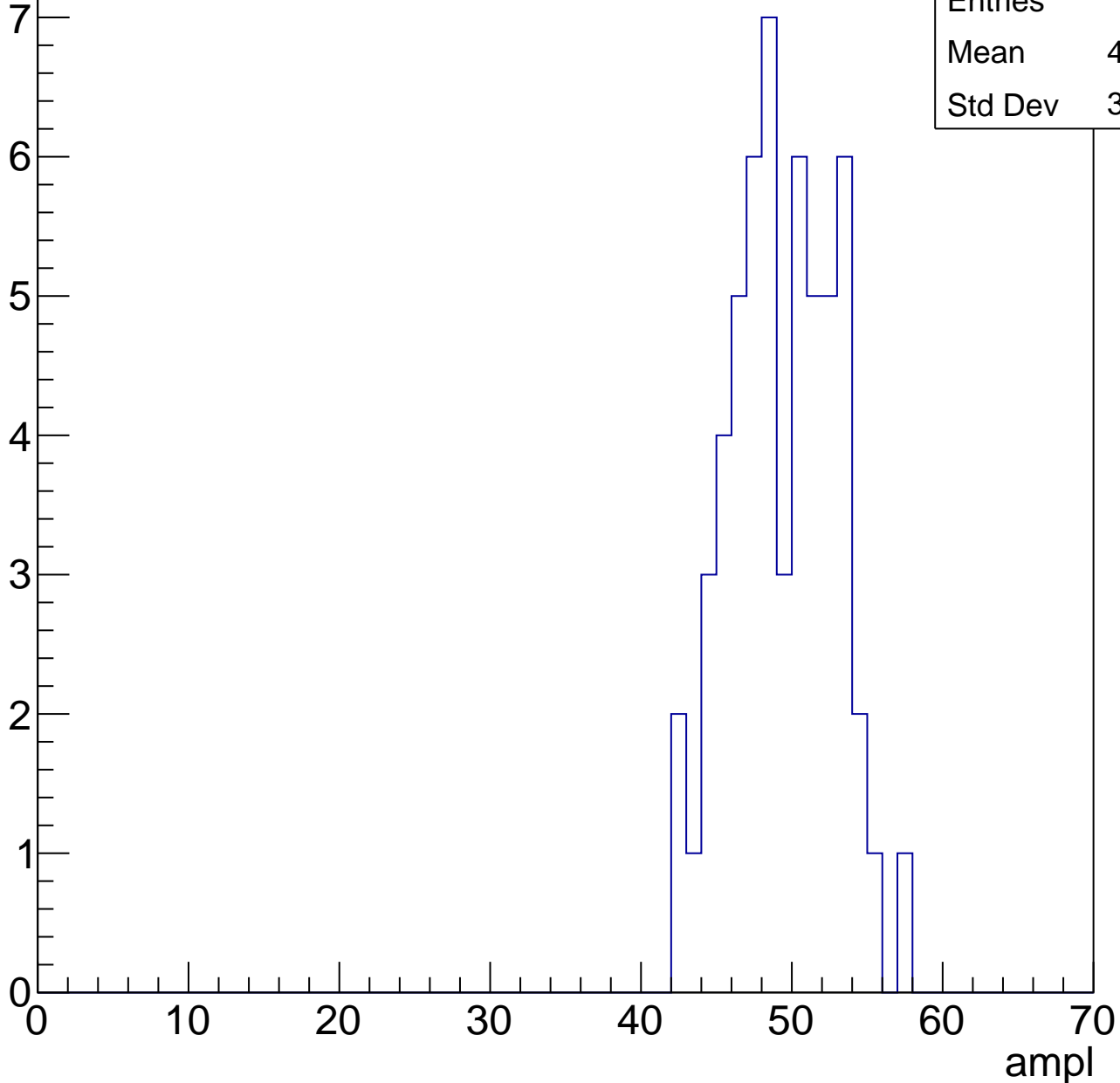


B1L102S, U13-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

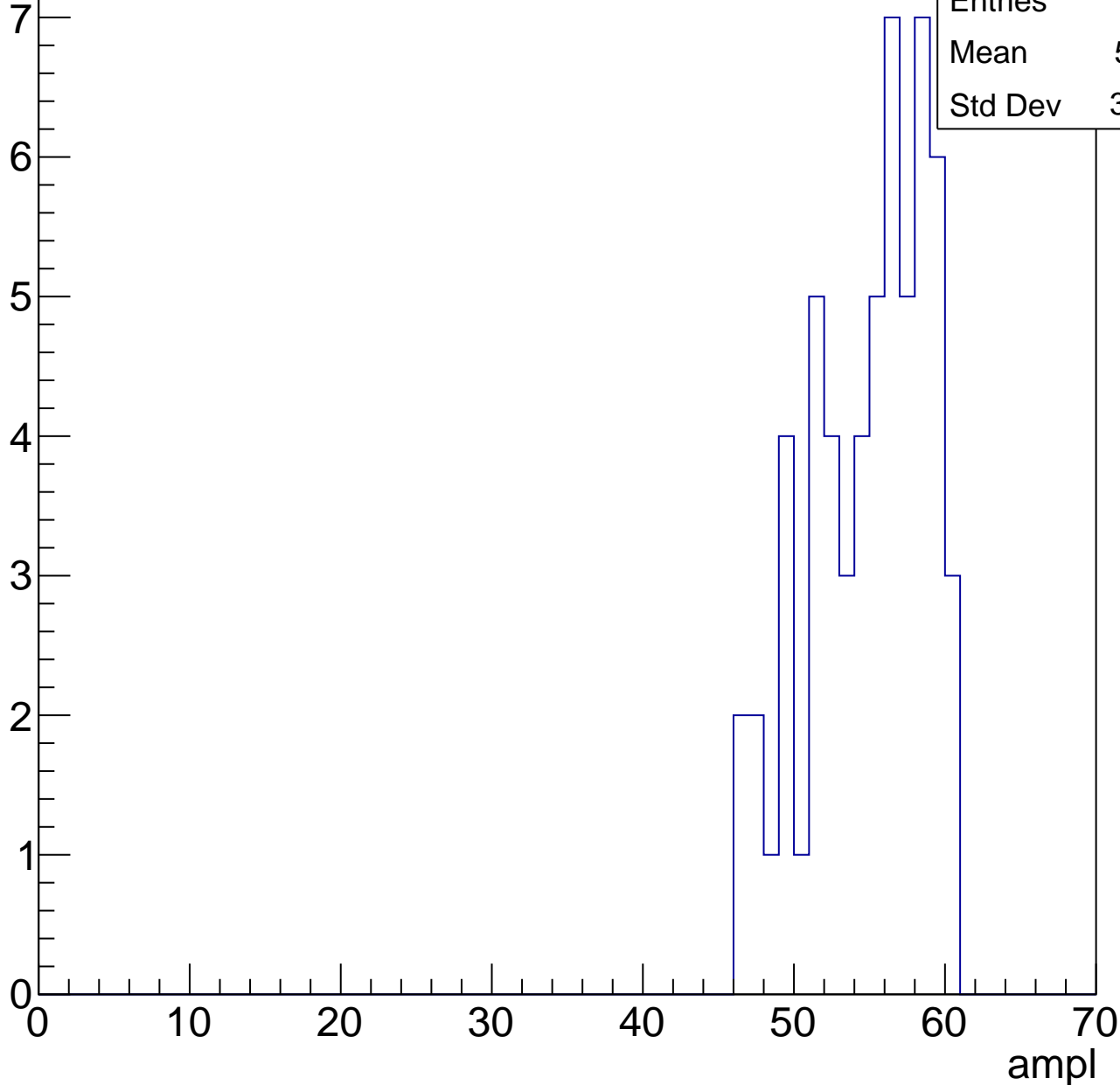
Entries	57
Mean	48.89
Std Dev	3.417



B1L102S, U13-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

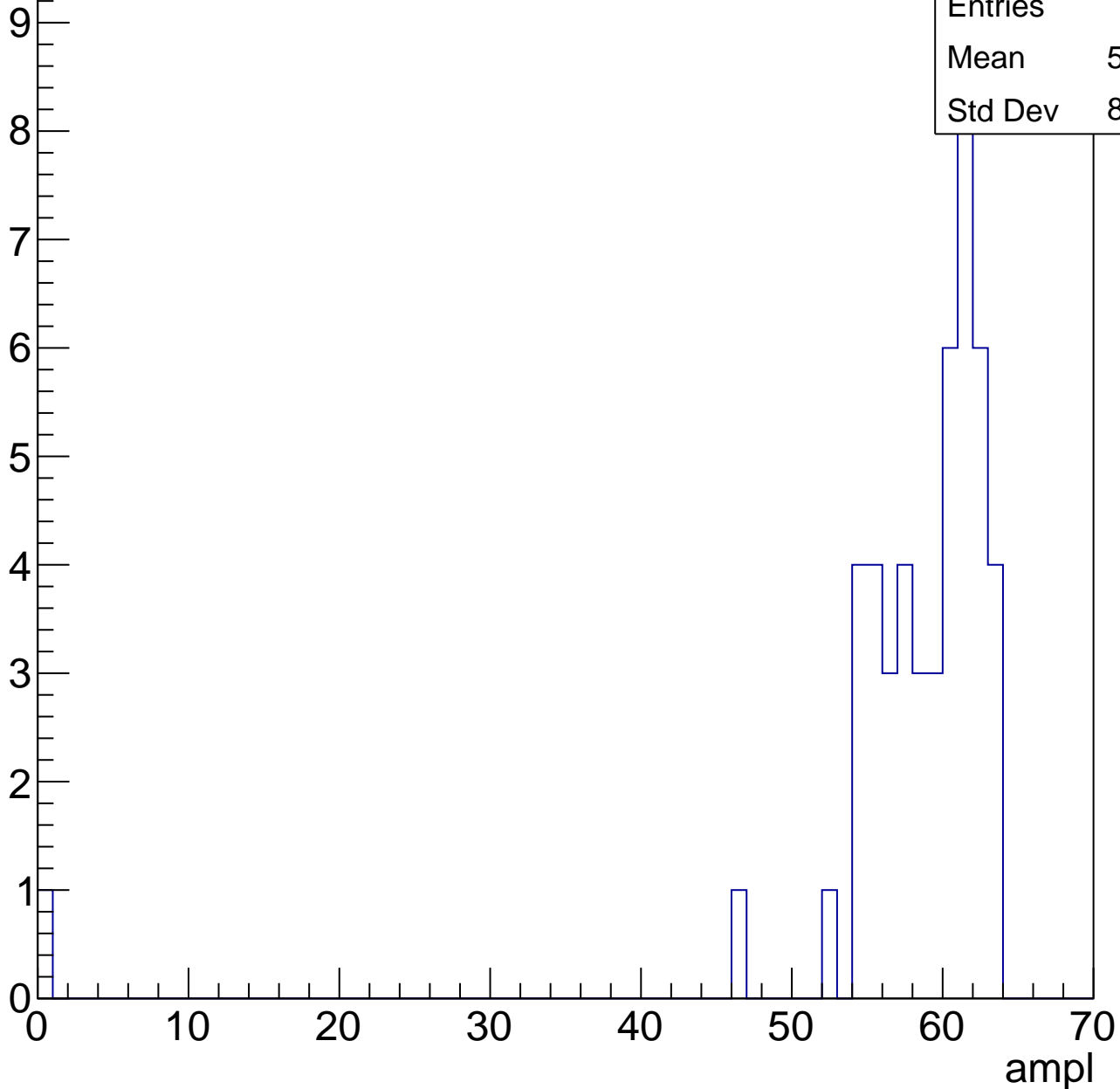


B1L102S, U13-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.43
Std Dev	8.976

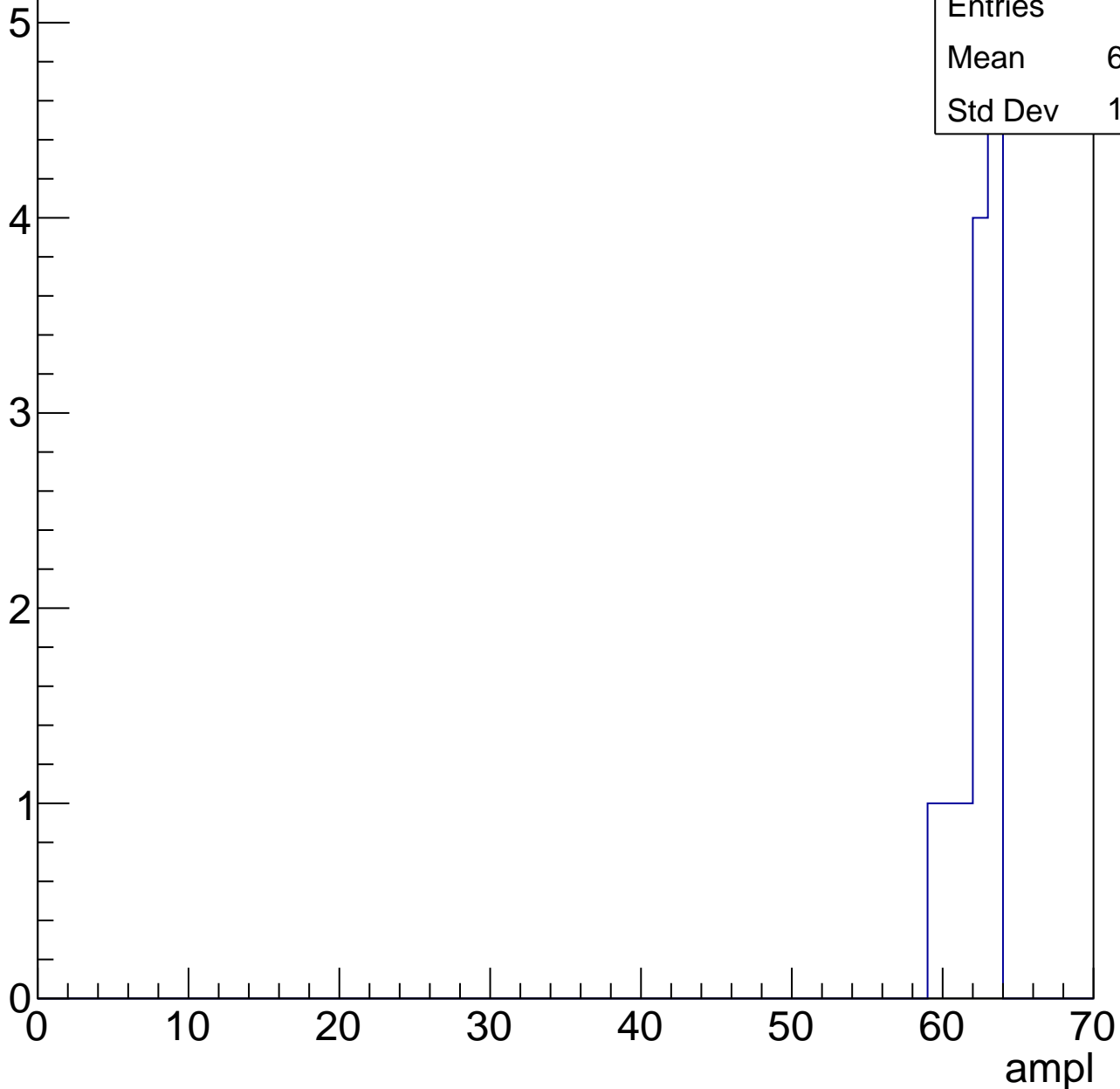


B1L102S, U13-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.92
Std Dev	1.256



B1L102S, U13-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U13-ch93, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	29.85
Std Dev	4.836

Gaus mean : 30.8332

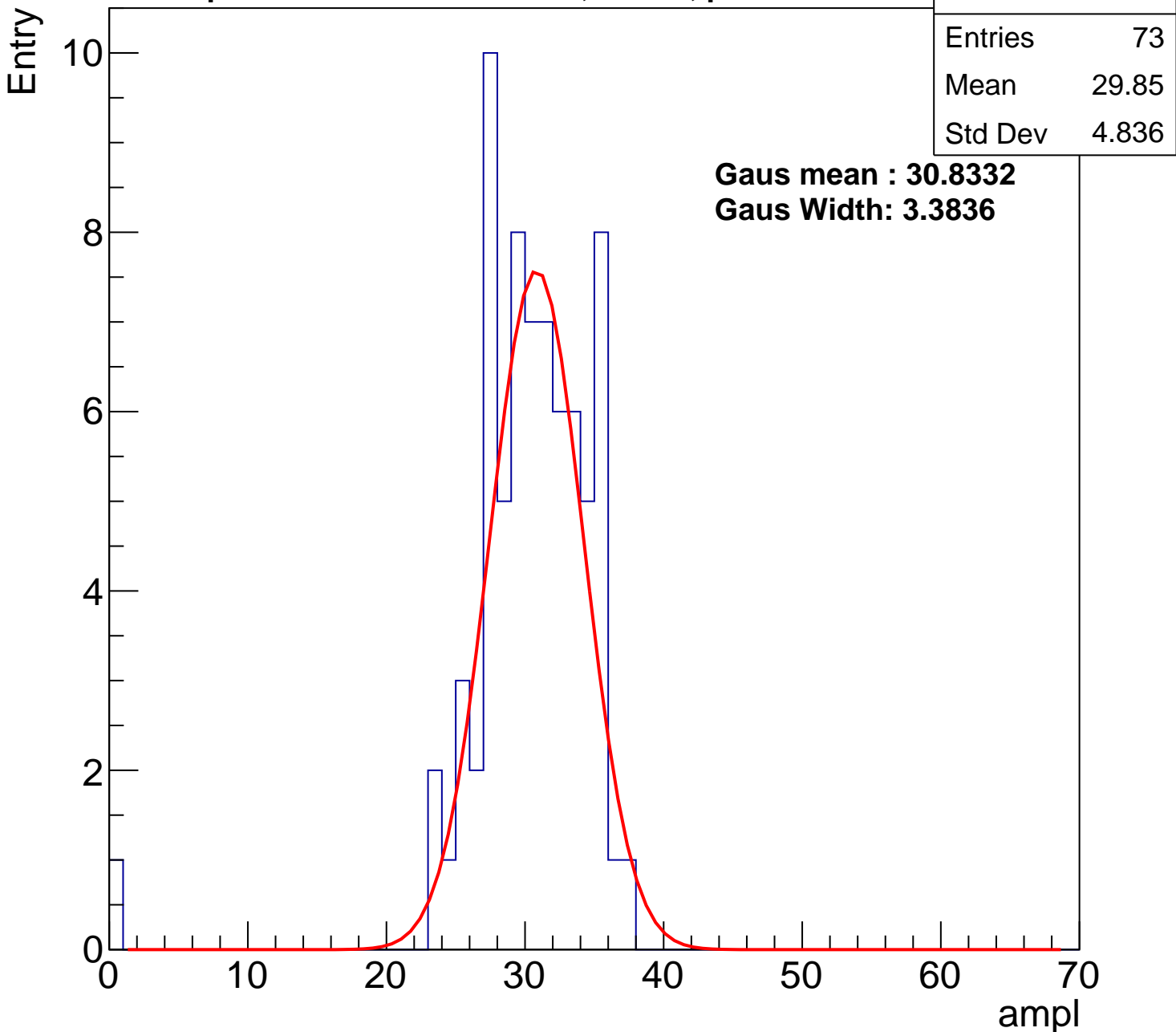
Gaus Width: 3.3836

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch93, adc1

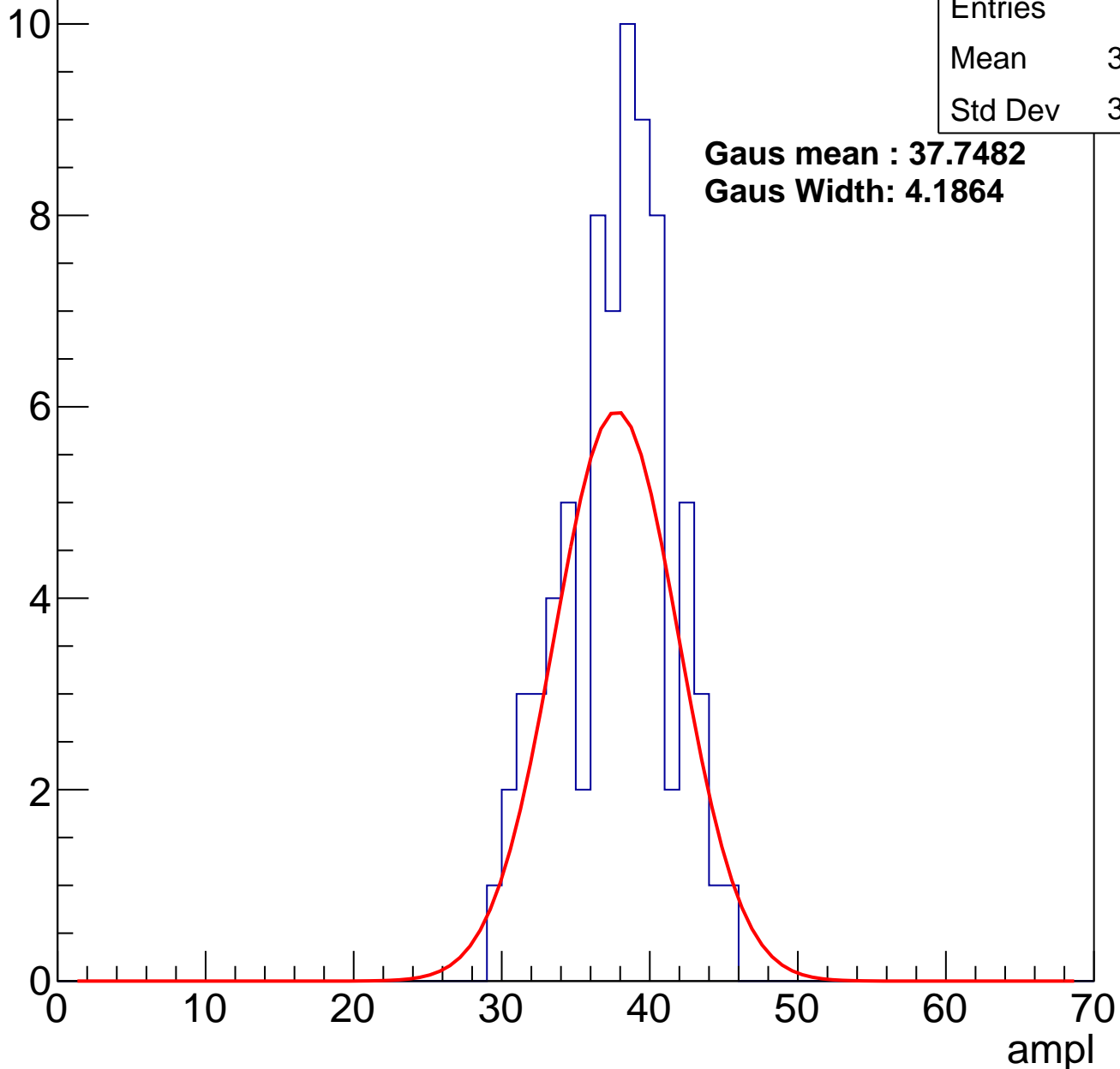
calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	37.27
Std Dev	3.603

Gaus mean : 37.7482

Gaus Width: 4.1864

Entry



B1L102S, U13-ch93, adc2

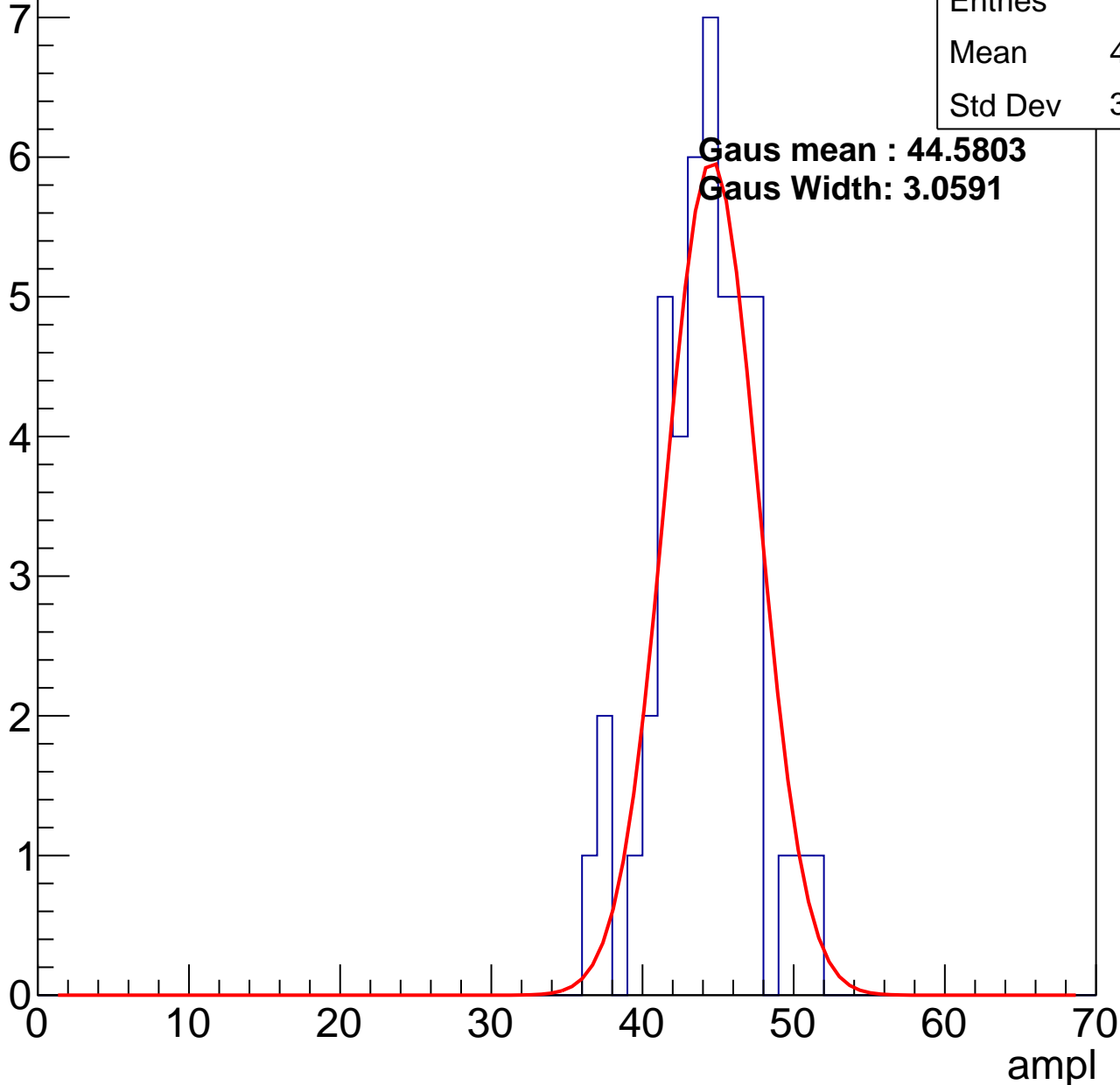
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	43.65
Std Dev	3.164

Gaus mean : 44.5803

Gaus Width: 3.0591

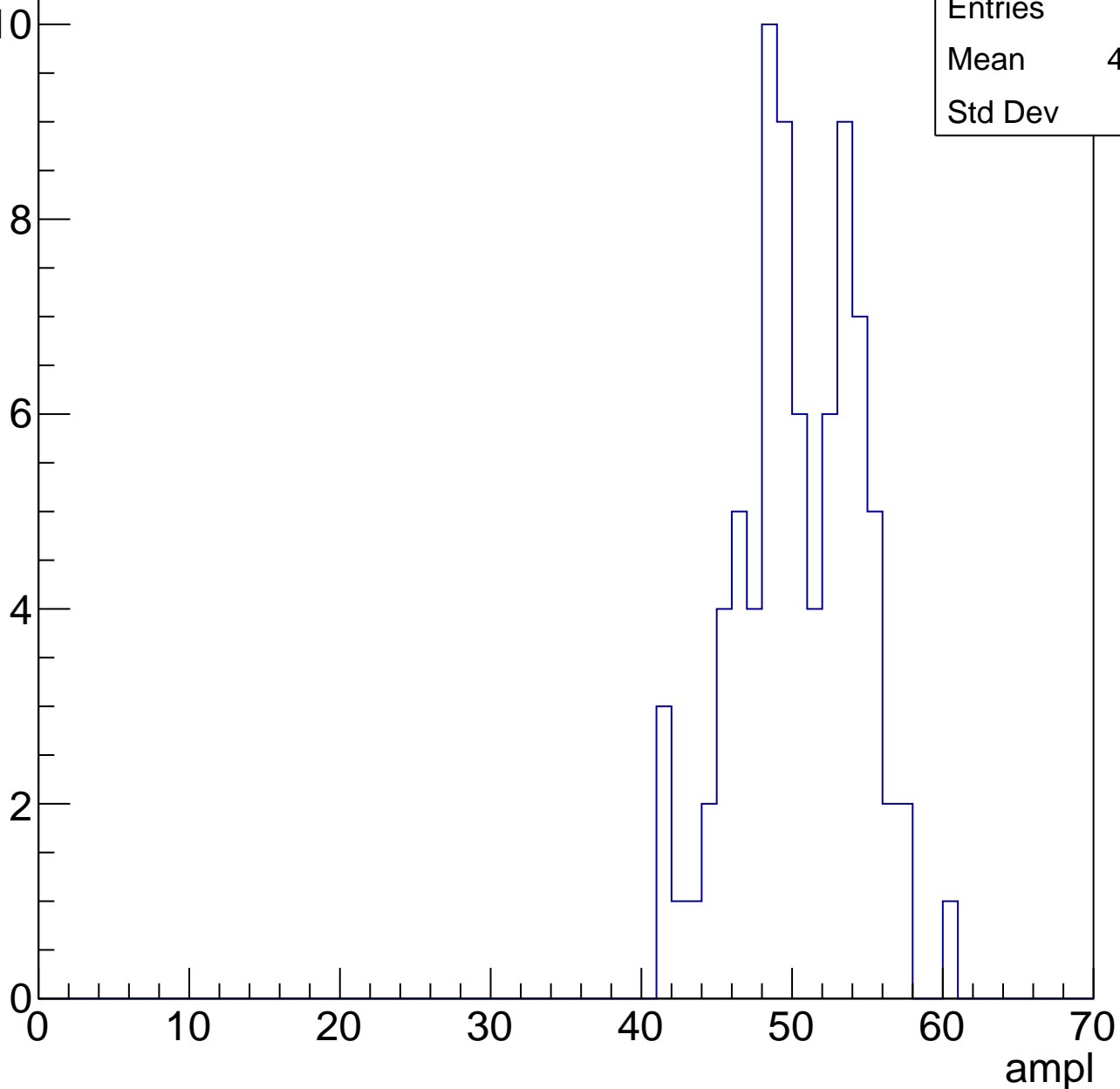


B1L102S, U13-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	49.96
Std Dev	4.02

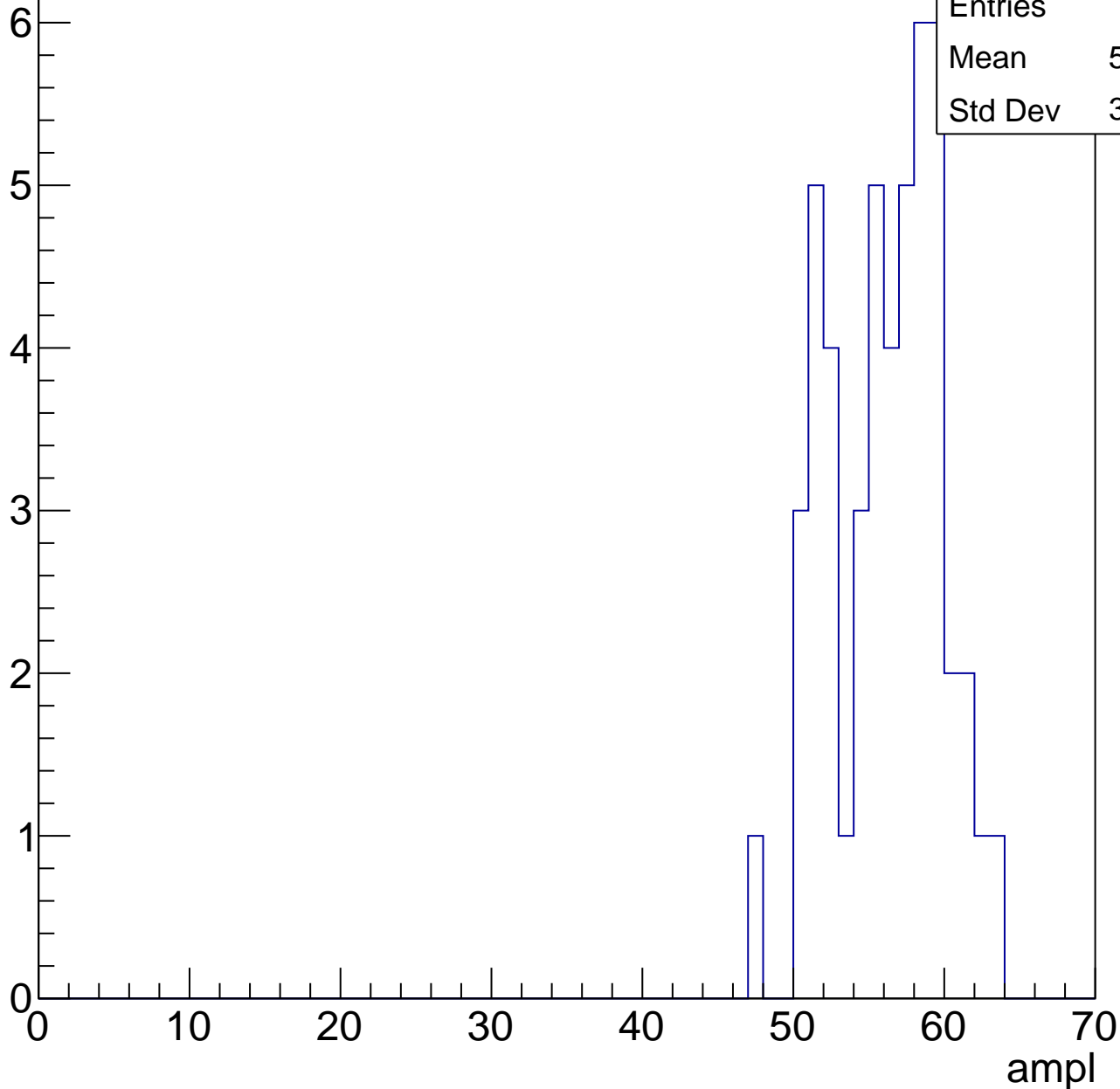


B1L102S, U13-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.67
Std Dev	3.639

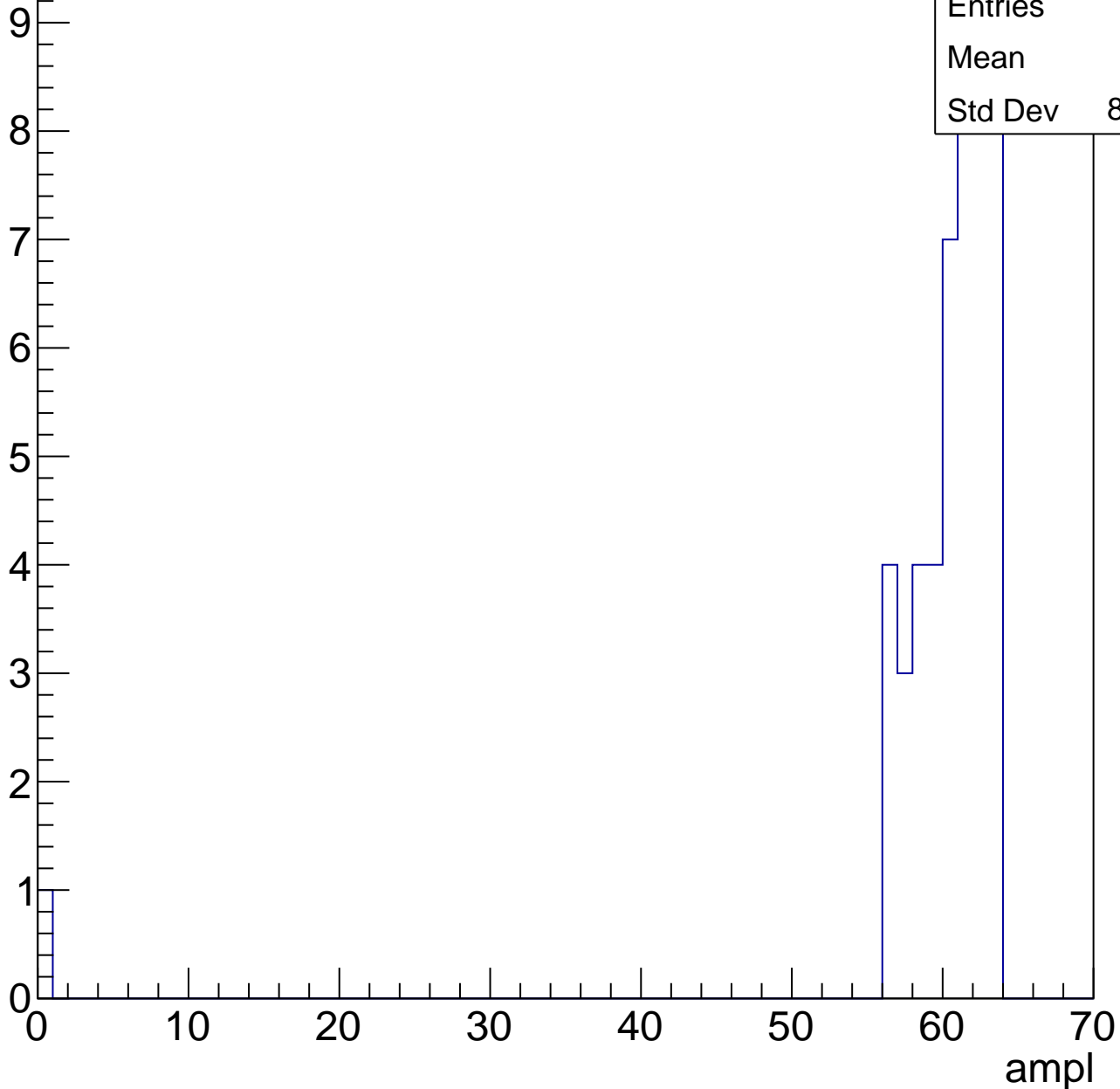


B1L102S, U13-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59.1
Std Dev	8.802



B1L102S, U13-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch94, adc0

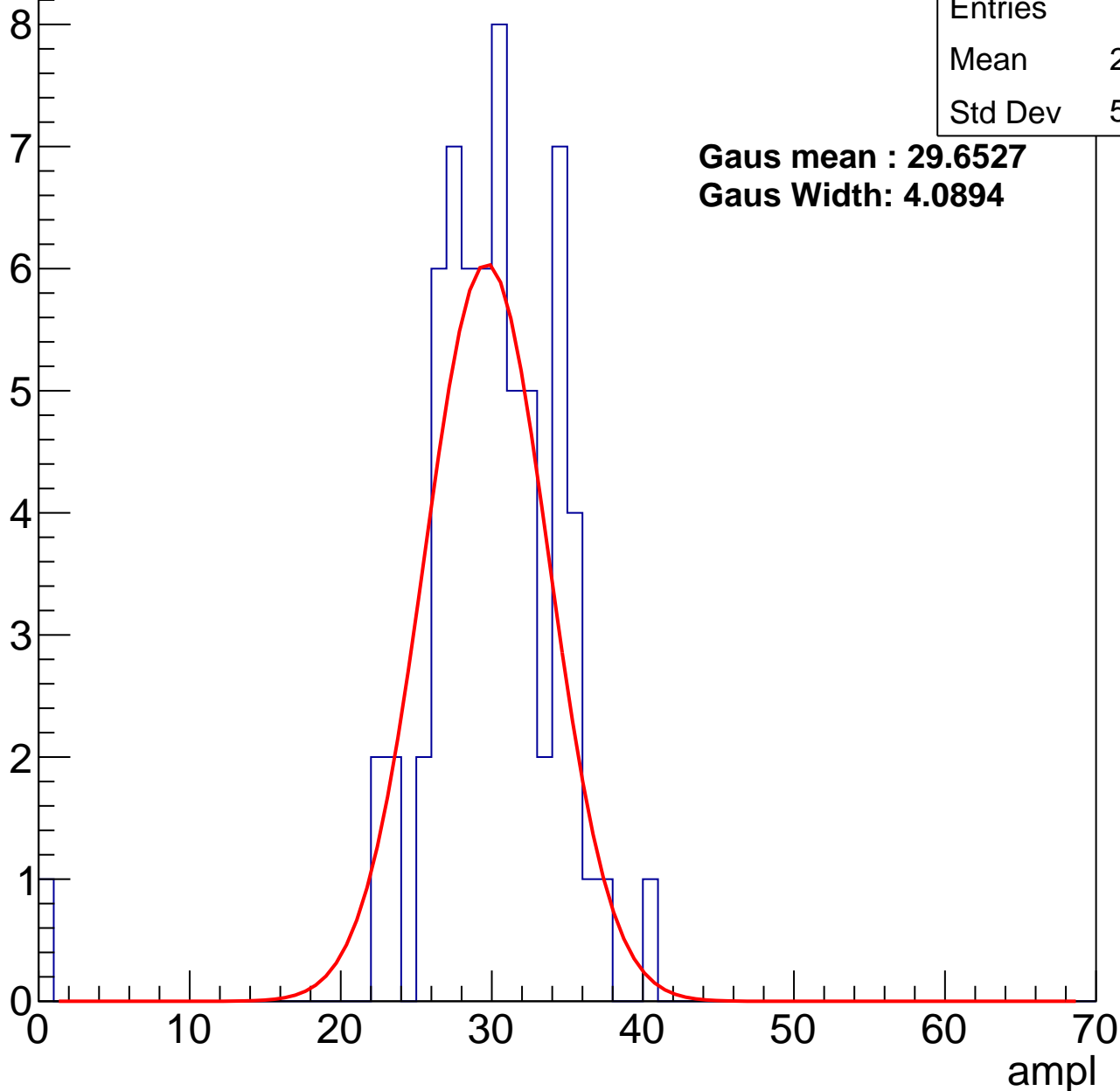
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.38
Std Dev	5.193

Gaus mean : 29.6527

Gaus Width: 4.0894



B1L102S, U13-ch94, adc1

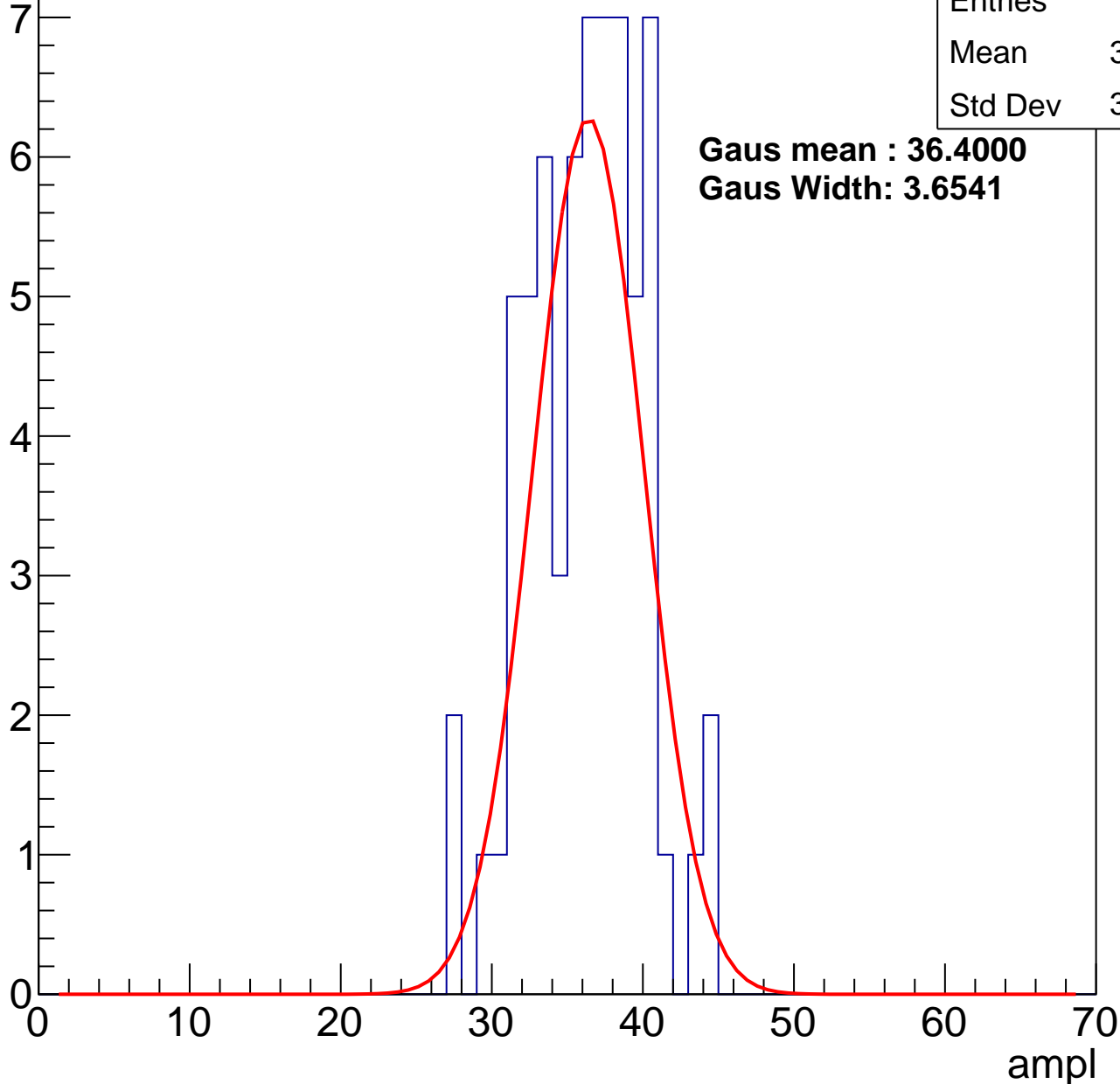
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.79
Std Dev	3.723

Gaus mean : 36.4000

Gaus Width: 3.6541



B1L102S, U13-ch94, adc2

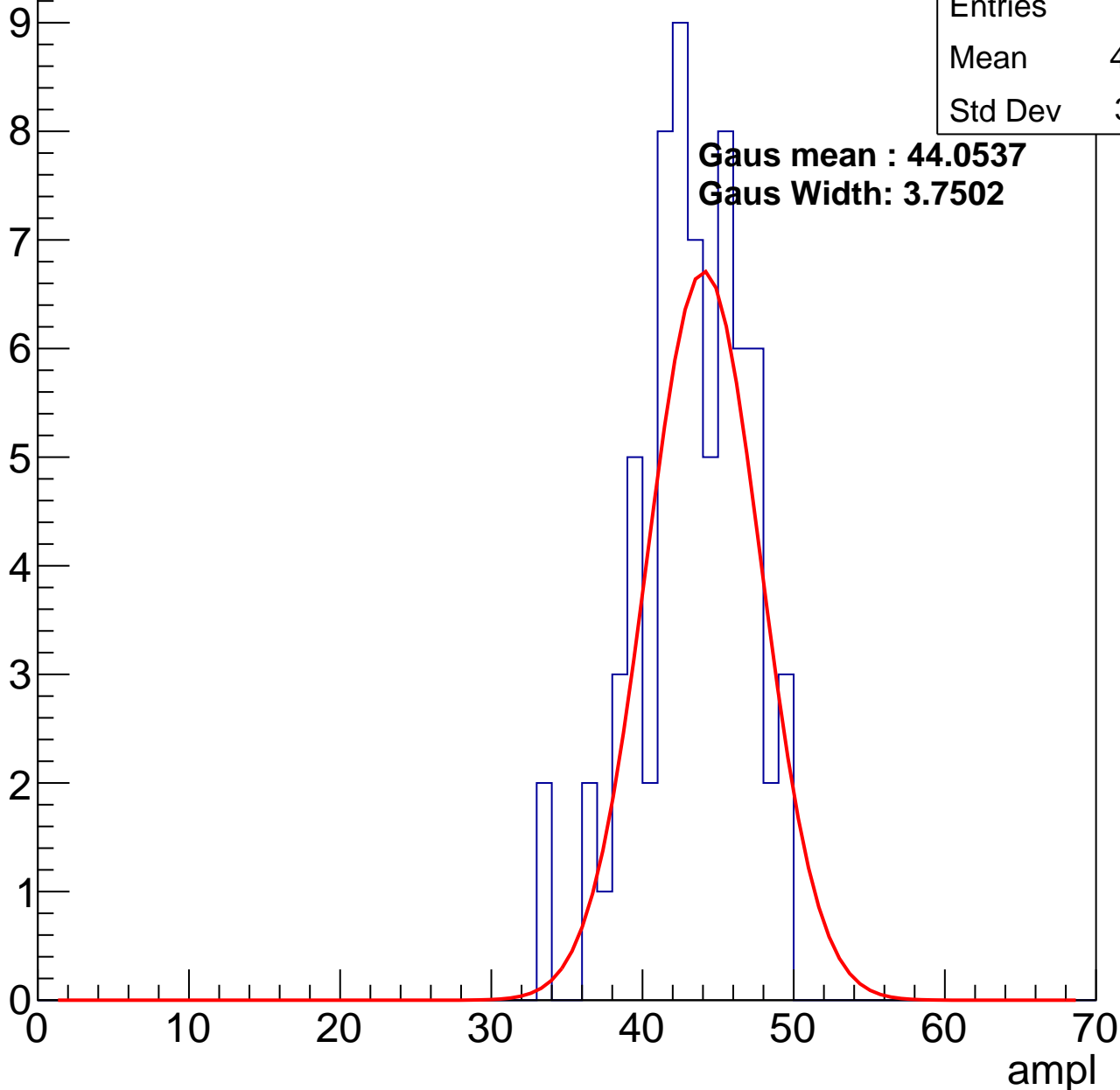
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.78
Std Dev	3.591

Gaus mean : 44.0537

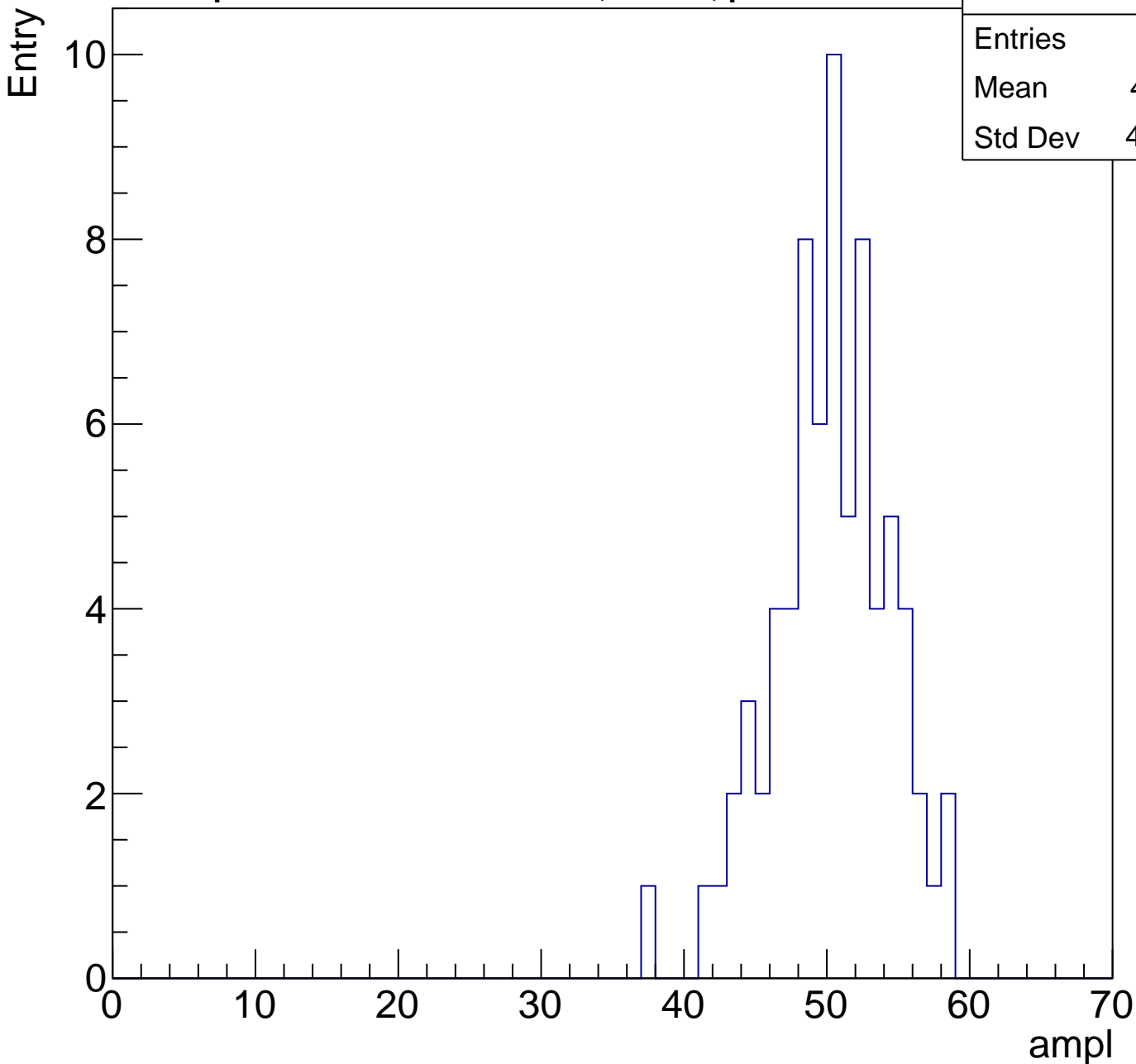
Gaus Width: 3.7502



B1L102S, U13-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

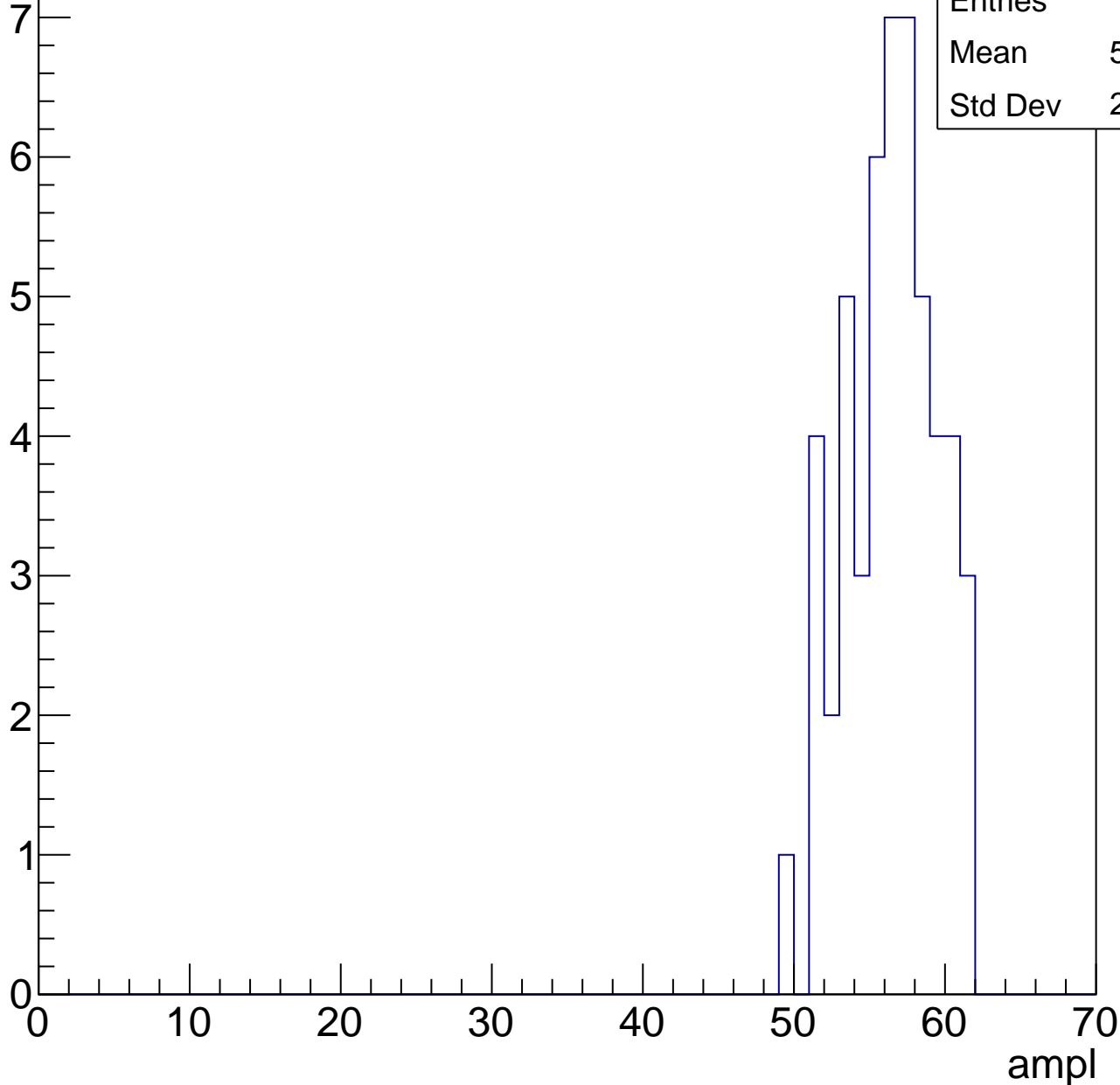
Entries	73
Mean	49.81
Std Dev	4.067



B1L102S, U13-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

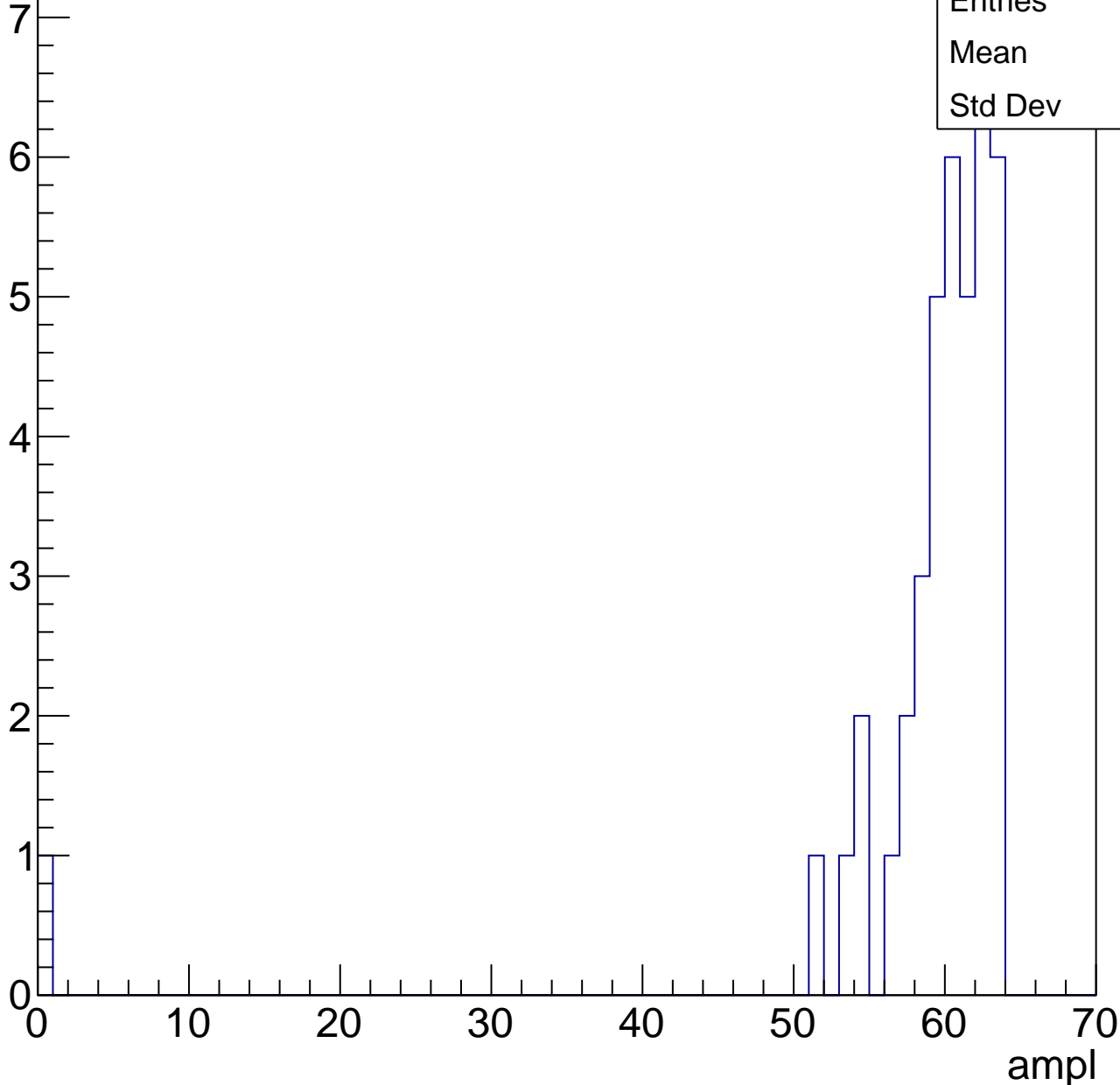


B1L102S, U13-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

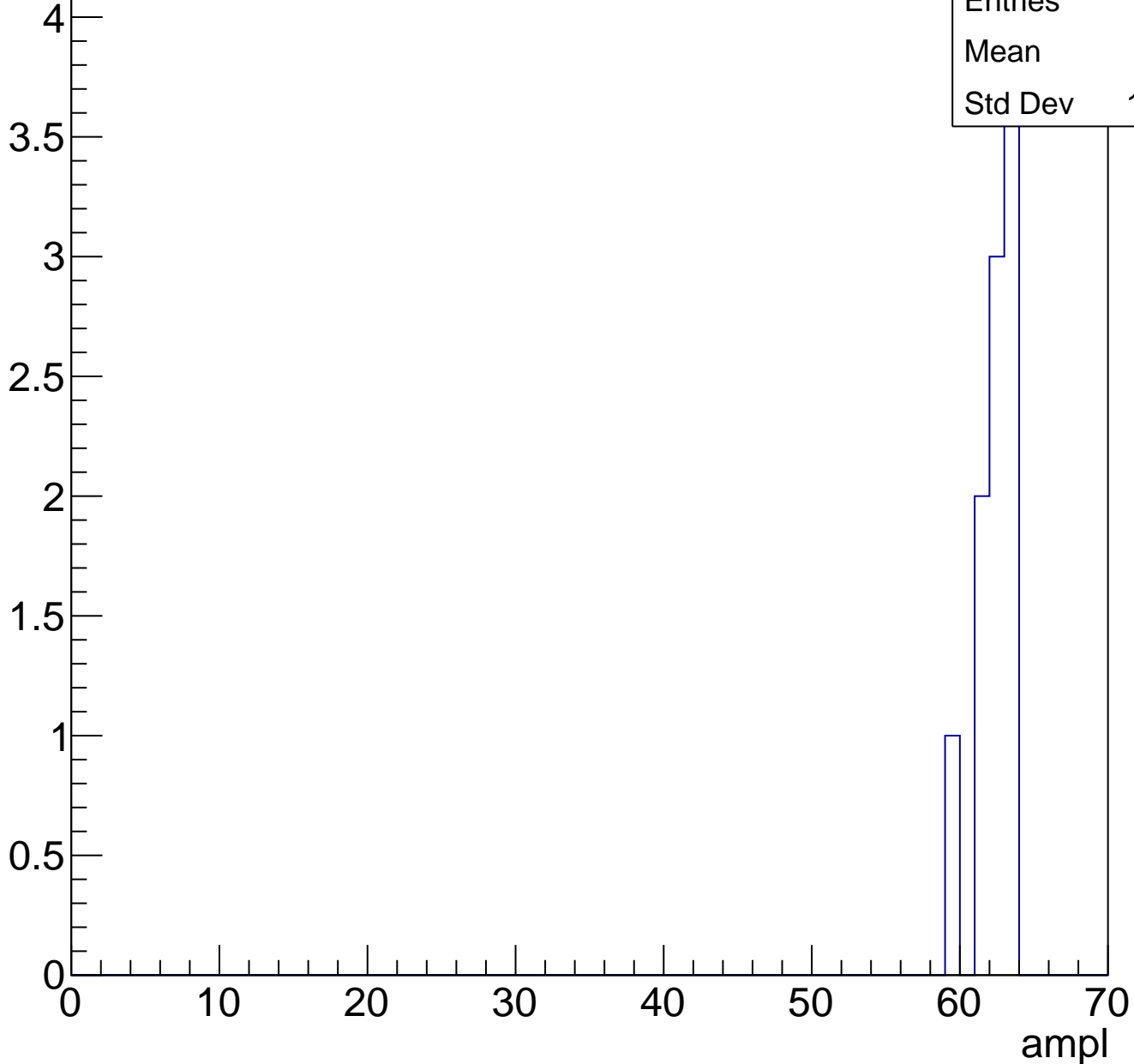
Entries	40
Mean	58.2
Std Dev	9.76



B1L102S, U13-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	61.9
Std Dev	1.221

B1L102S, U13-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch95, adc0

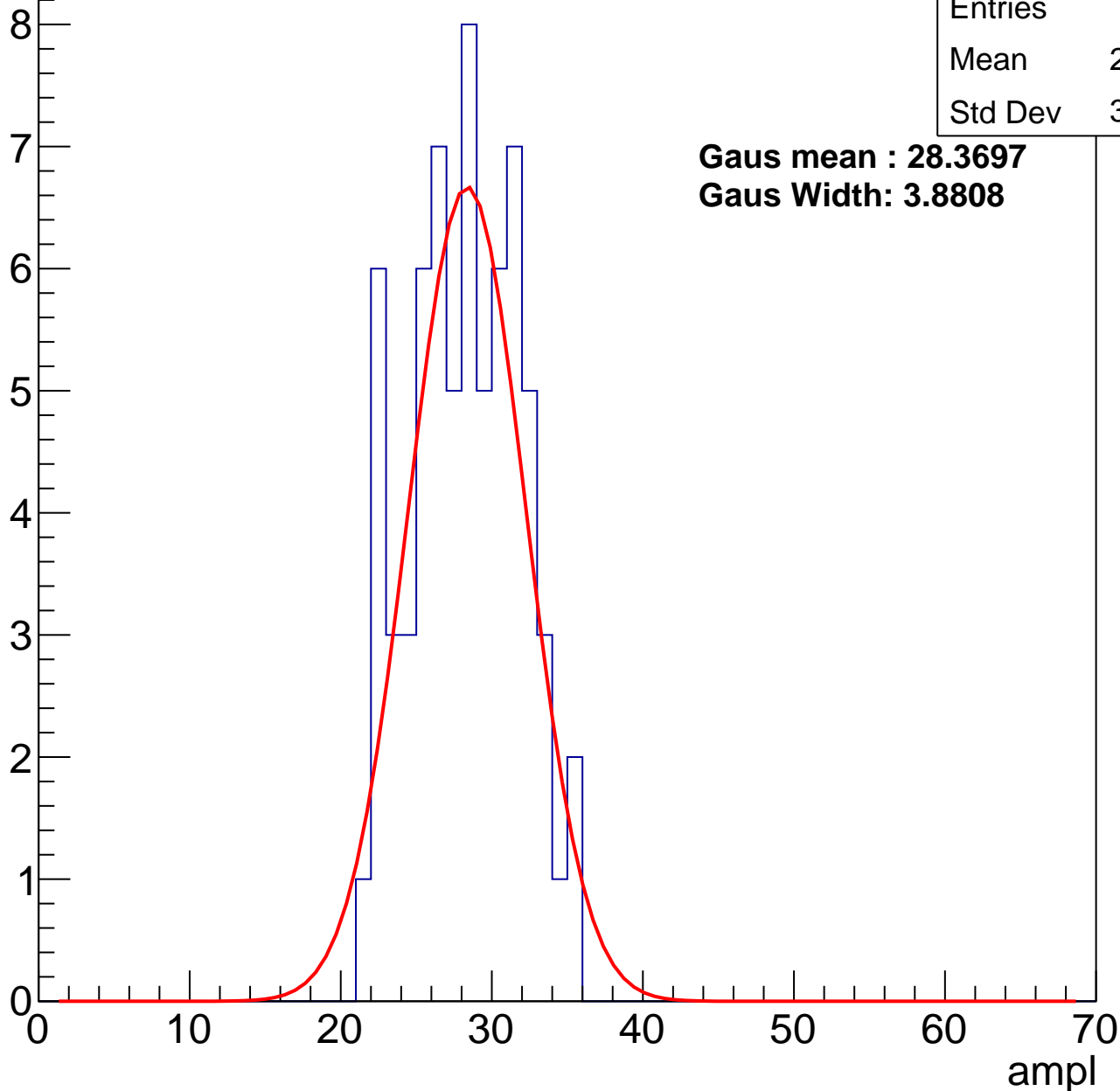
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.79
Std Dev	3.542

Gaus mean : 28.3697

Gaus Width: 3.8808



B1L102S, U13-ch95, adc1

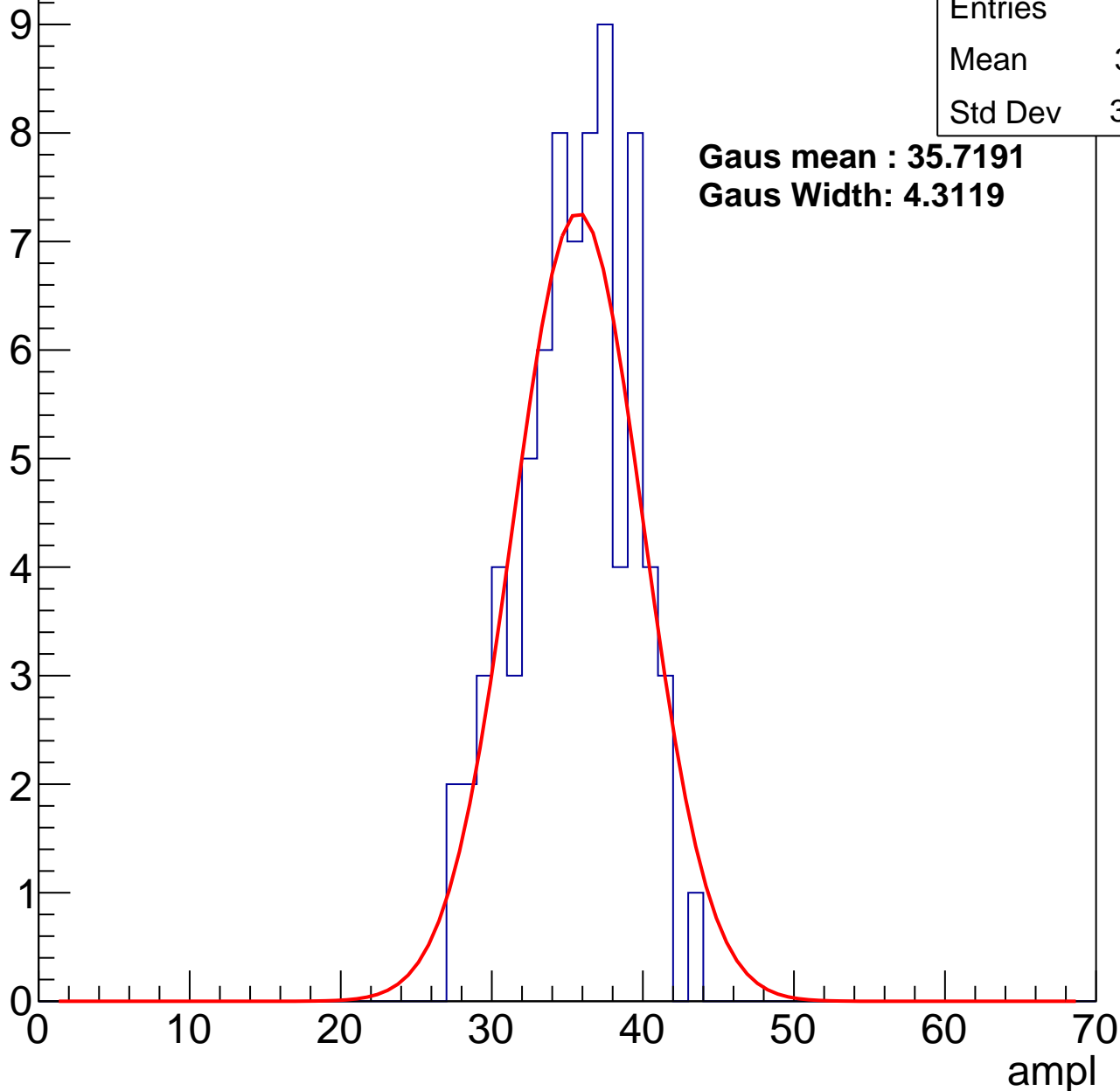
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.01
Std Dev	3.673

Gaus mean : 35.7191

Gaus Width: 4.3119



B1L102S, U13-ch95, adc2

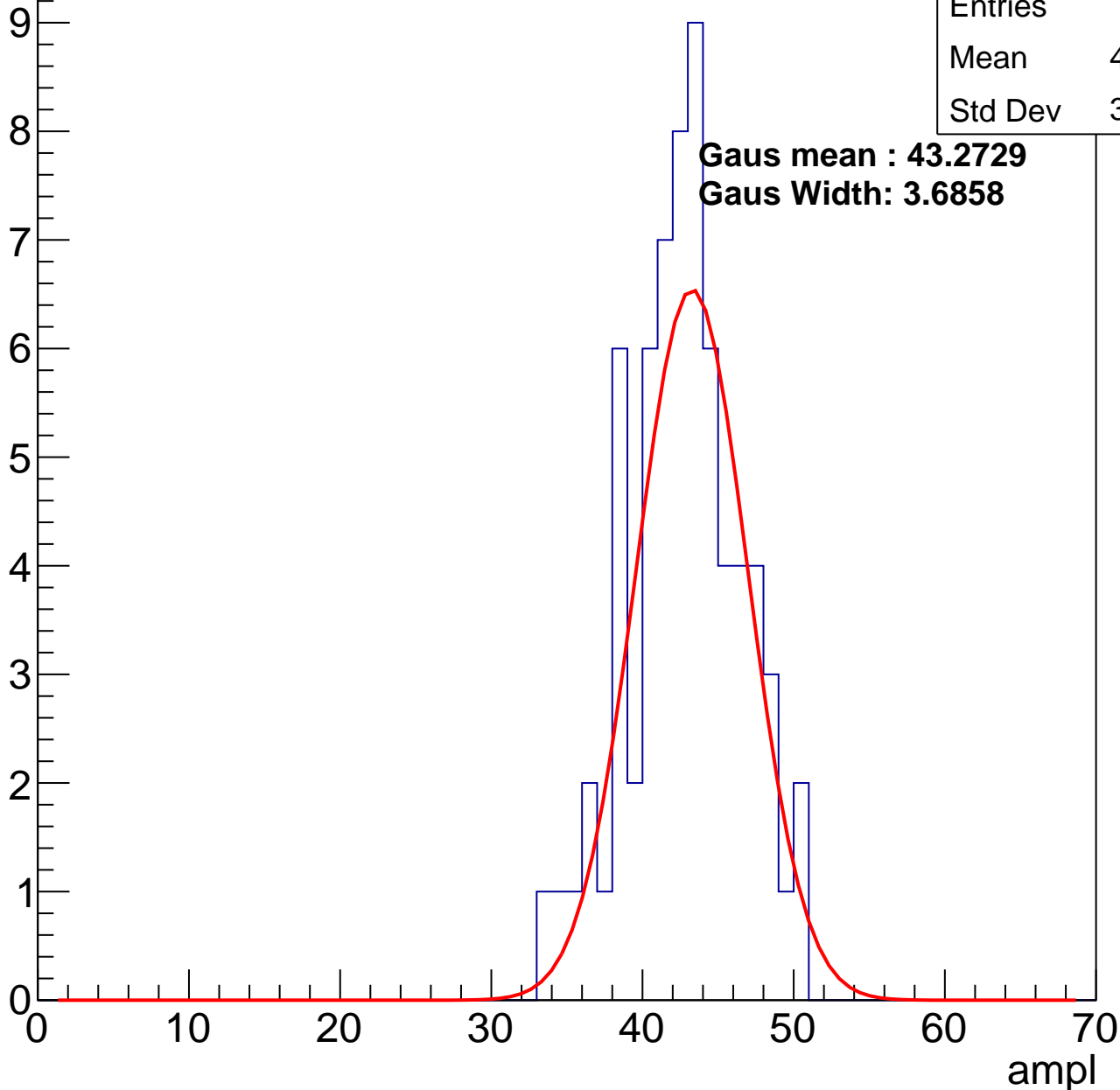
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.29
Std Dev	3.734

Gaus mean : 43.2729

Gaus Width: 3.6858

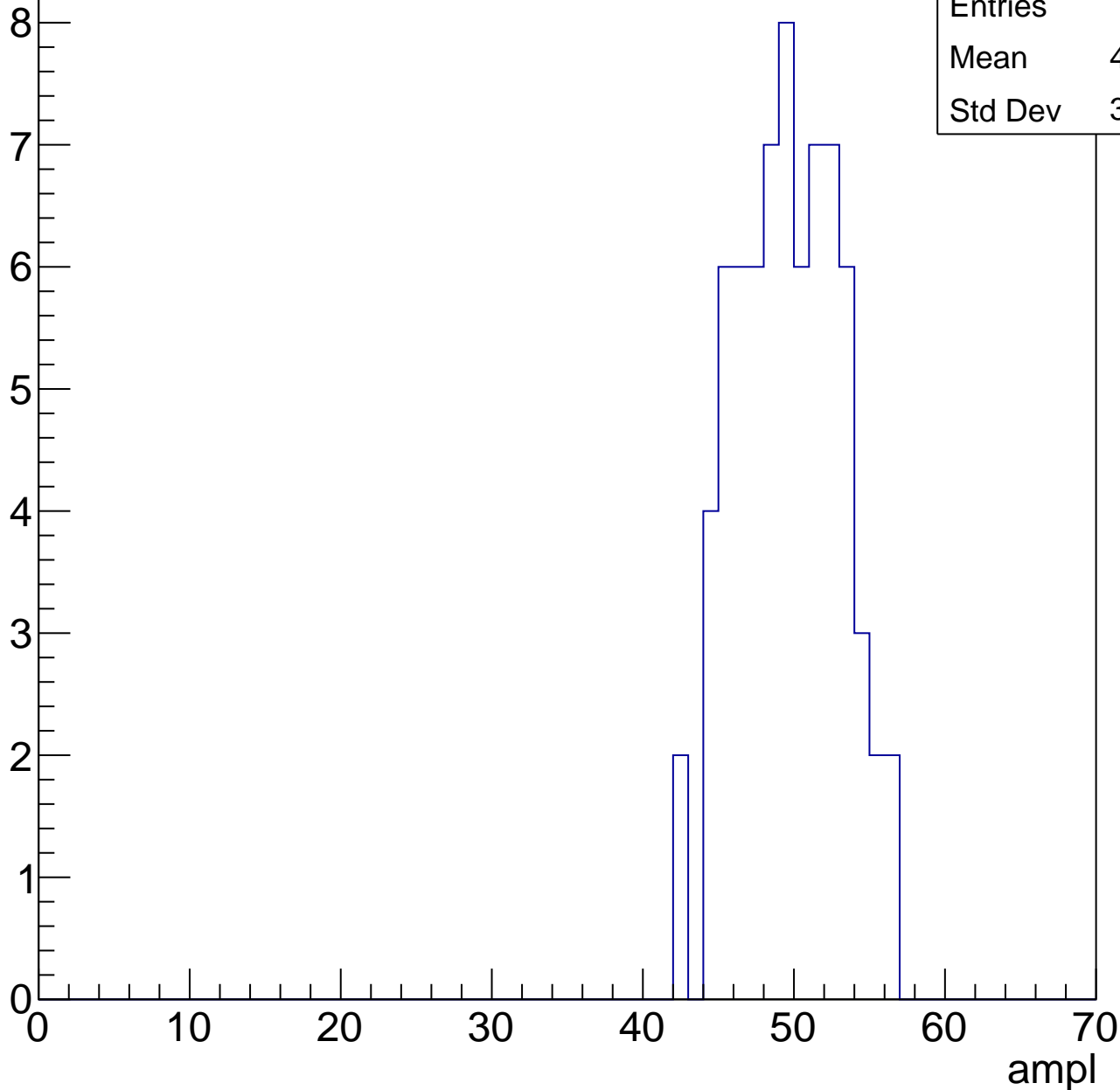


B1L102S, U13-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	49.15
Std Dev	3.365

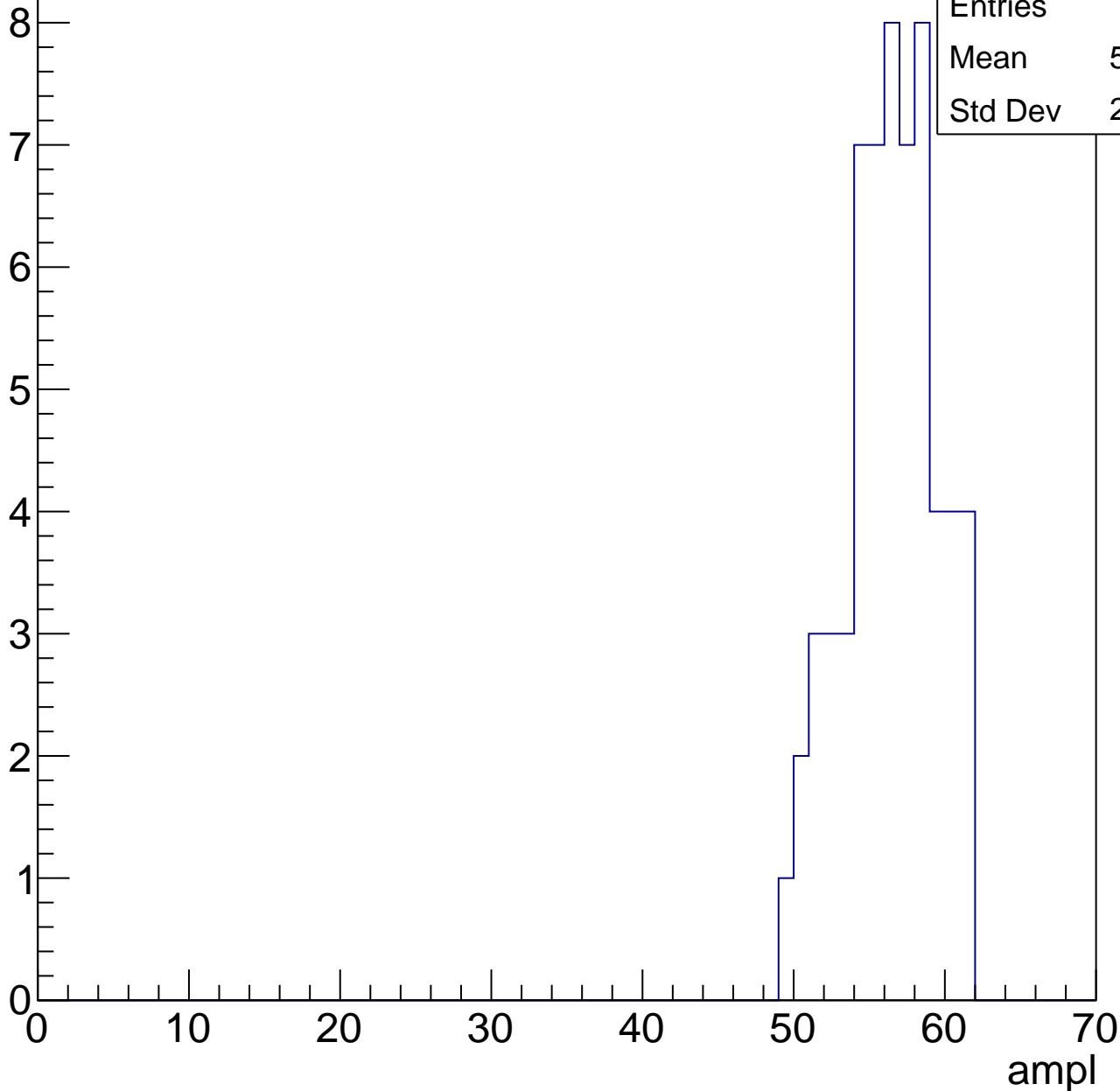


B1L102S, U13-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.92
Std Dev	2.988

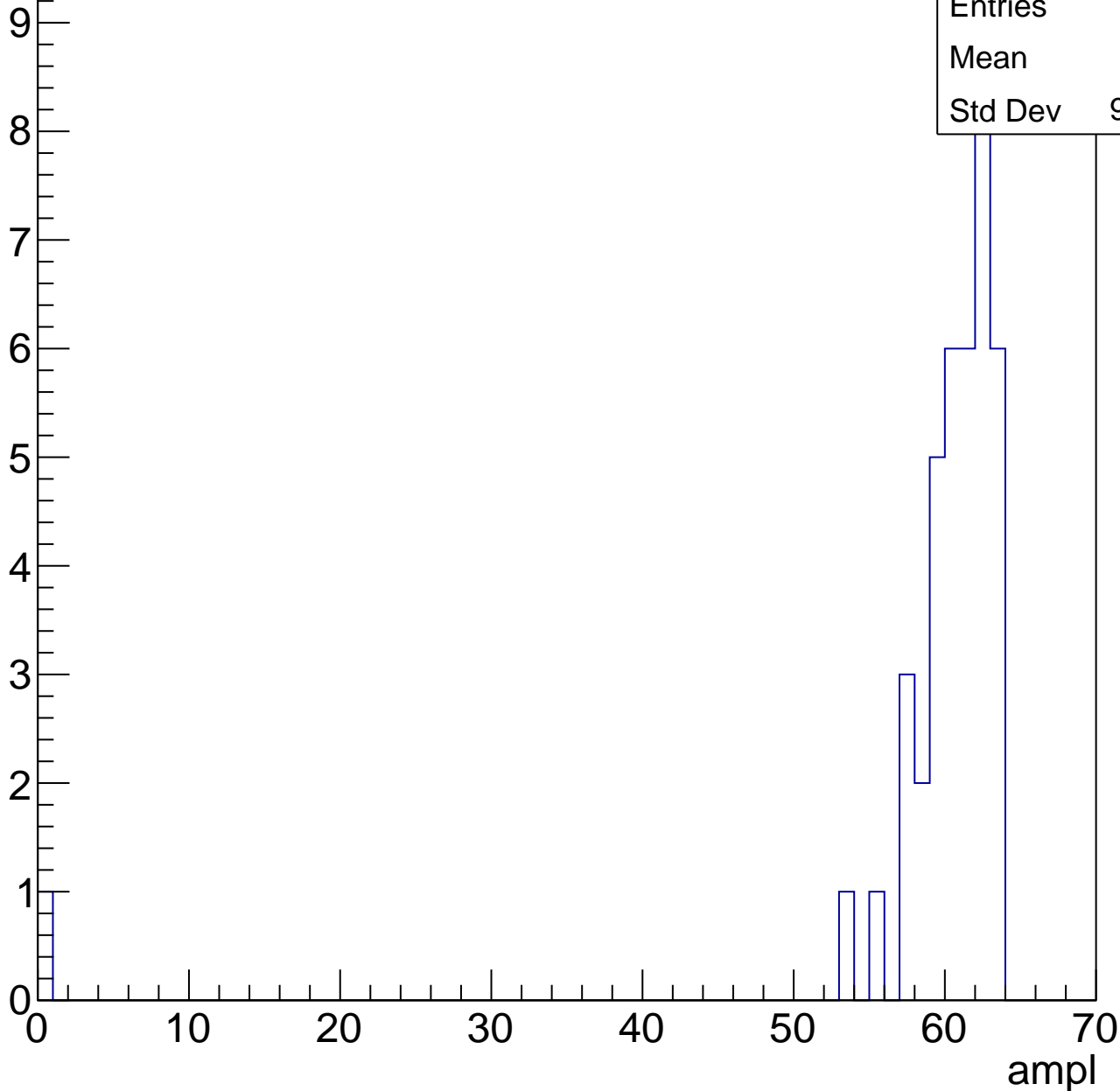


B1L102S, U13-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

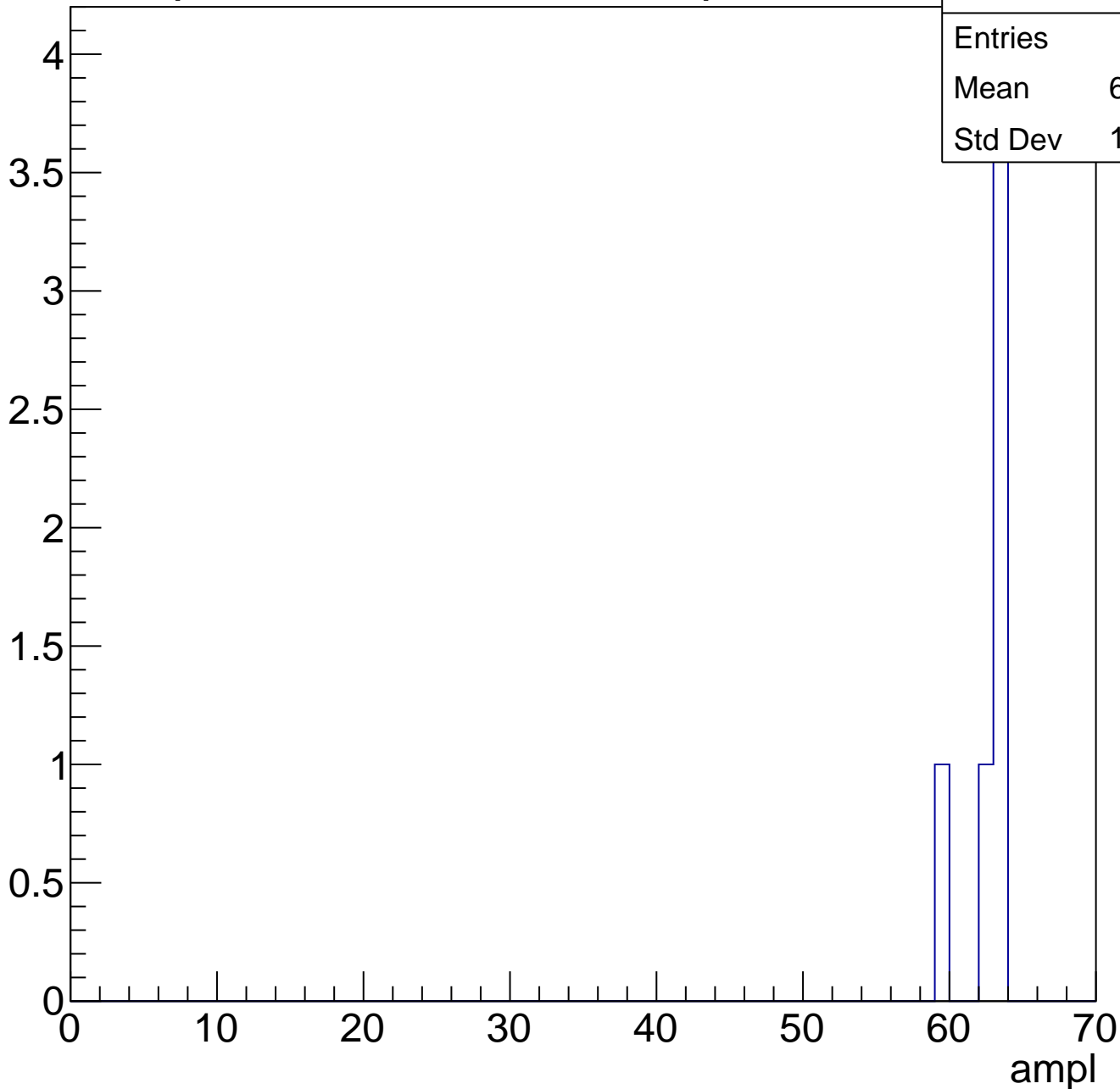
Entries	40
Mean	58.8
Std Dev	9.686



B1L102S, U13-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch96, adc0

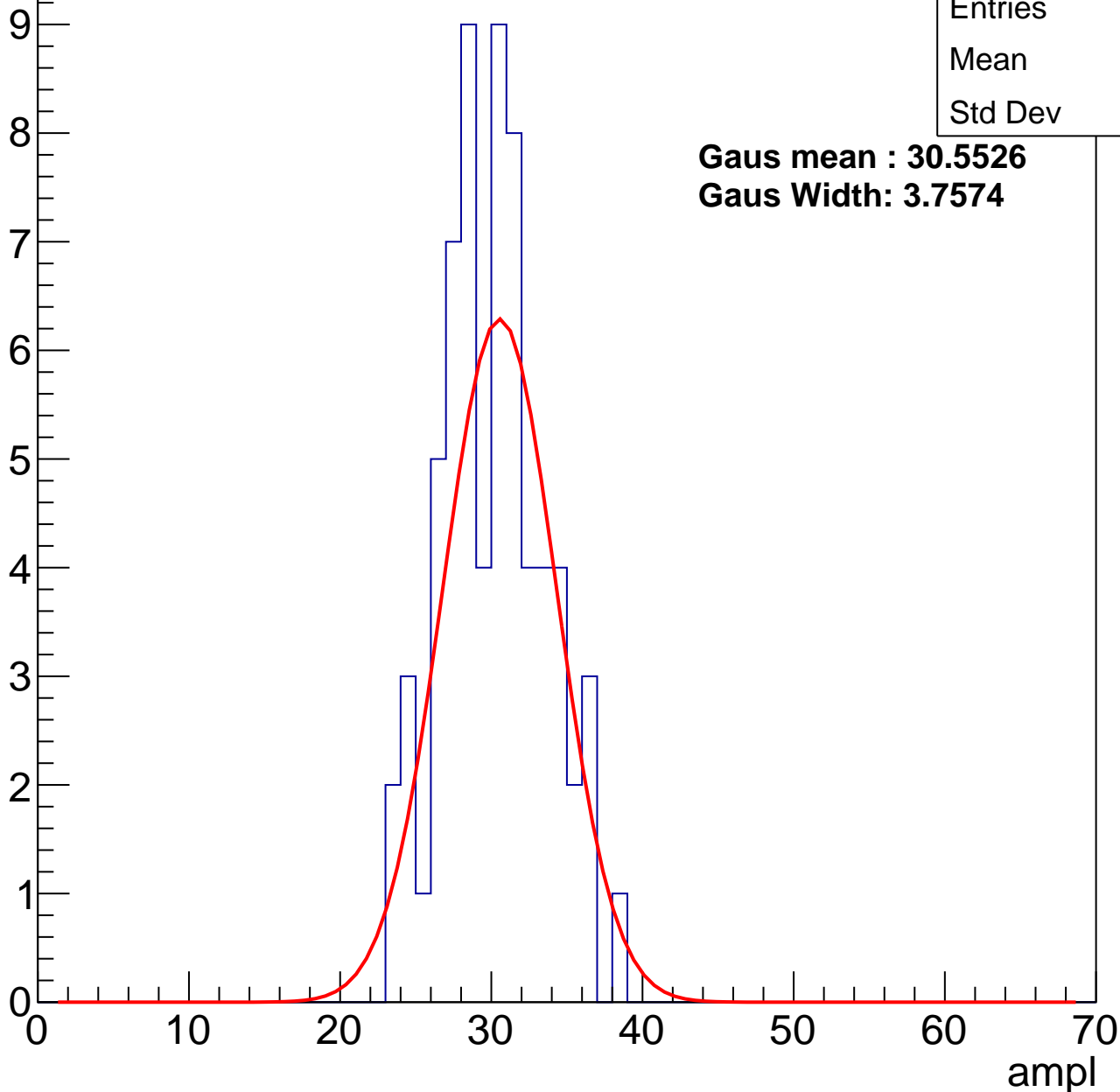
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.7
Std Dev	3.38

Gaus mean : 30.5526

Gaus Width: 3.7574



B1L102S, U13-ch96, adc1

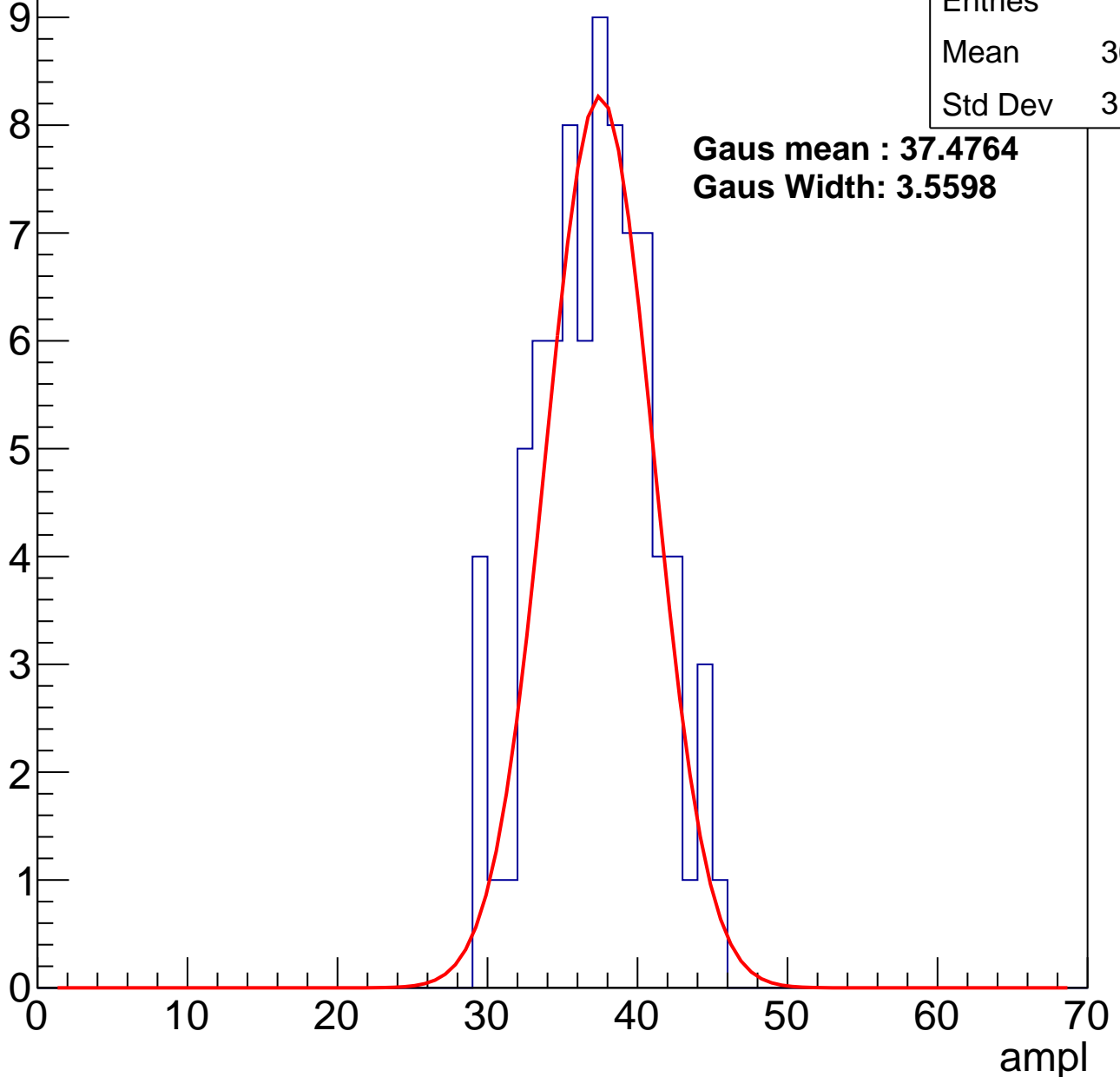
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.75
Std Dev	3.783

Gaus mean : 37.4764

Gaus Width: 3.5598



B1L102S, U13-ch96, adc2

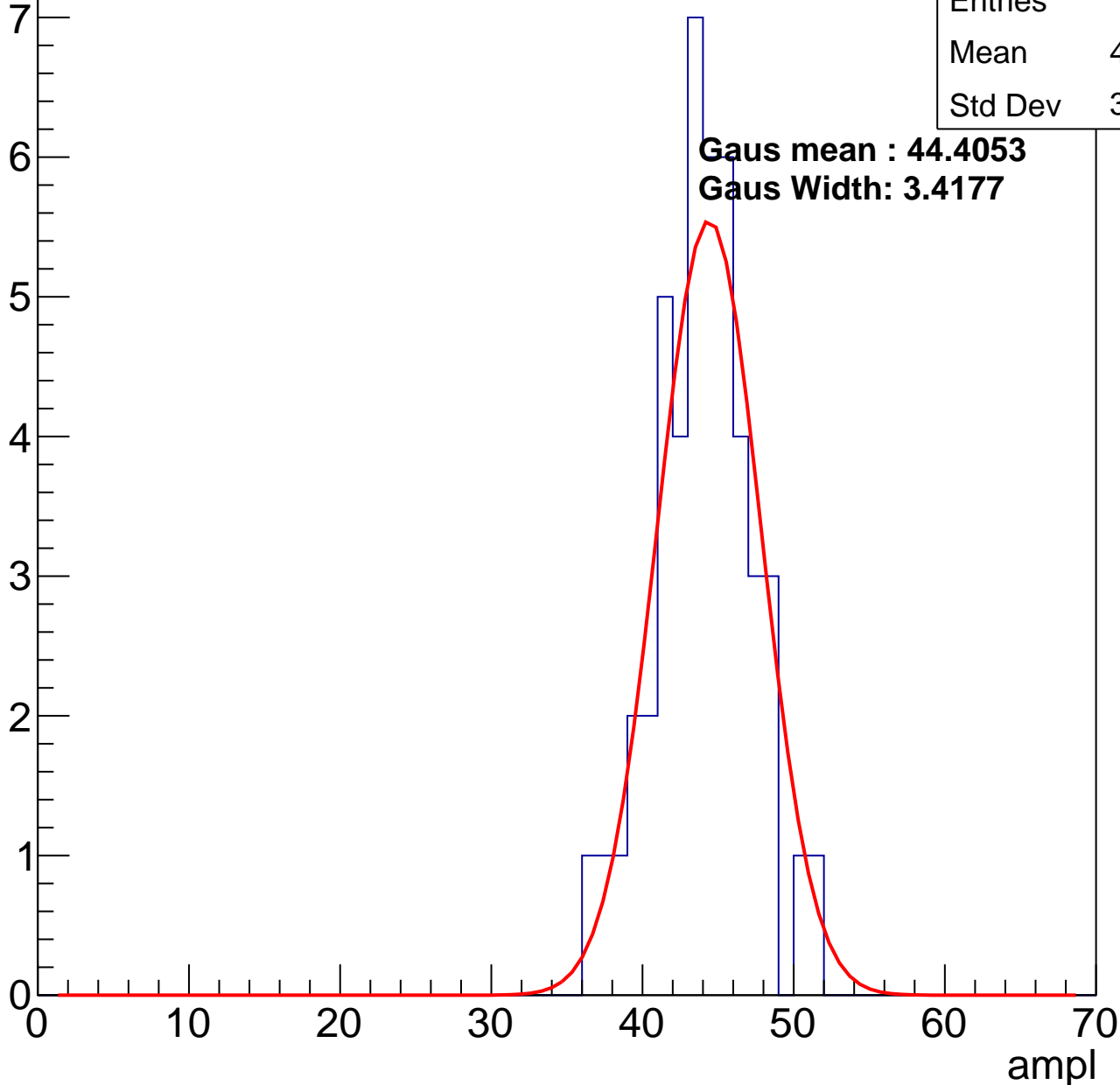
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	43.55
Std Dev	3.168

Gaus mean : 44.4053

Gaus Width: 3.4177

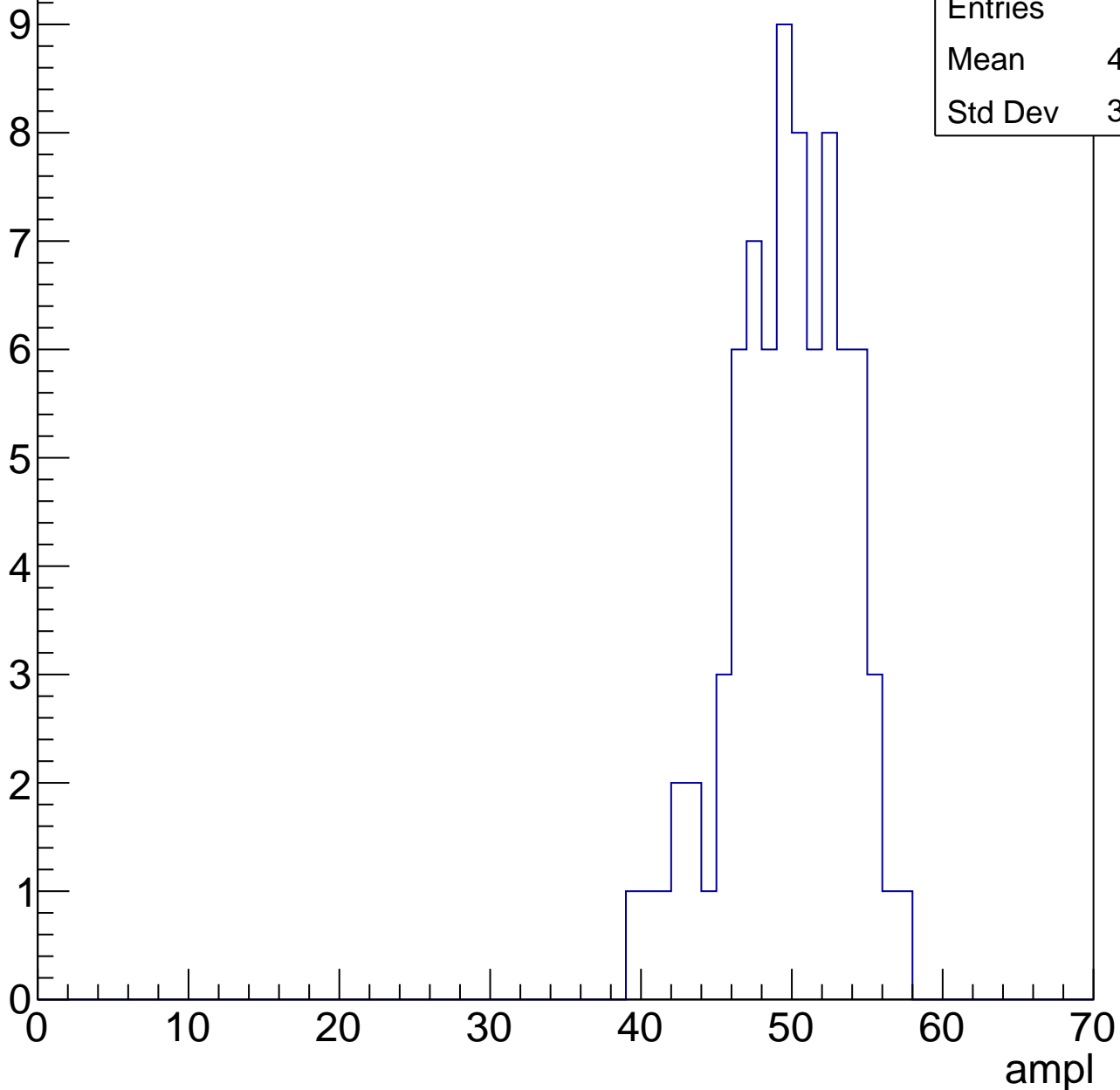


B1L102S, U13-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	49.29
Std Dev	3.823

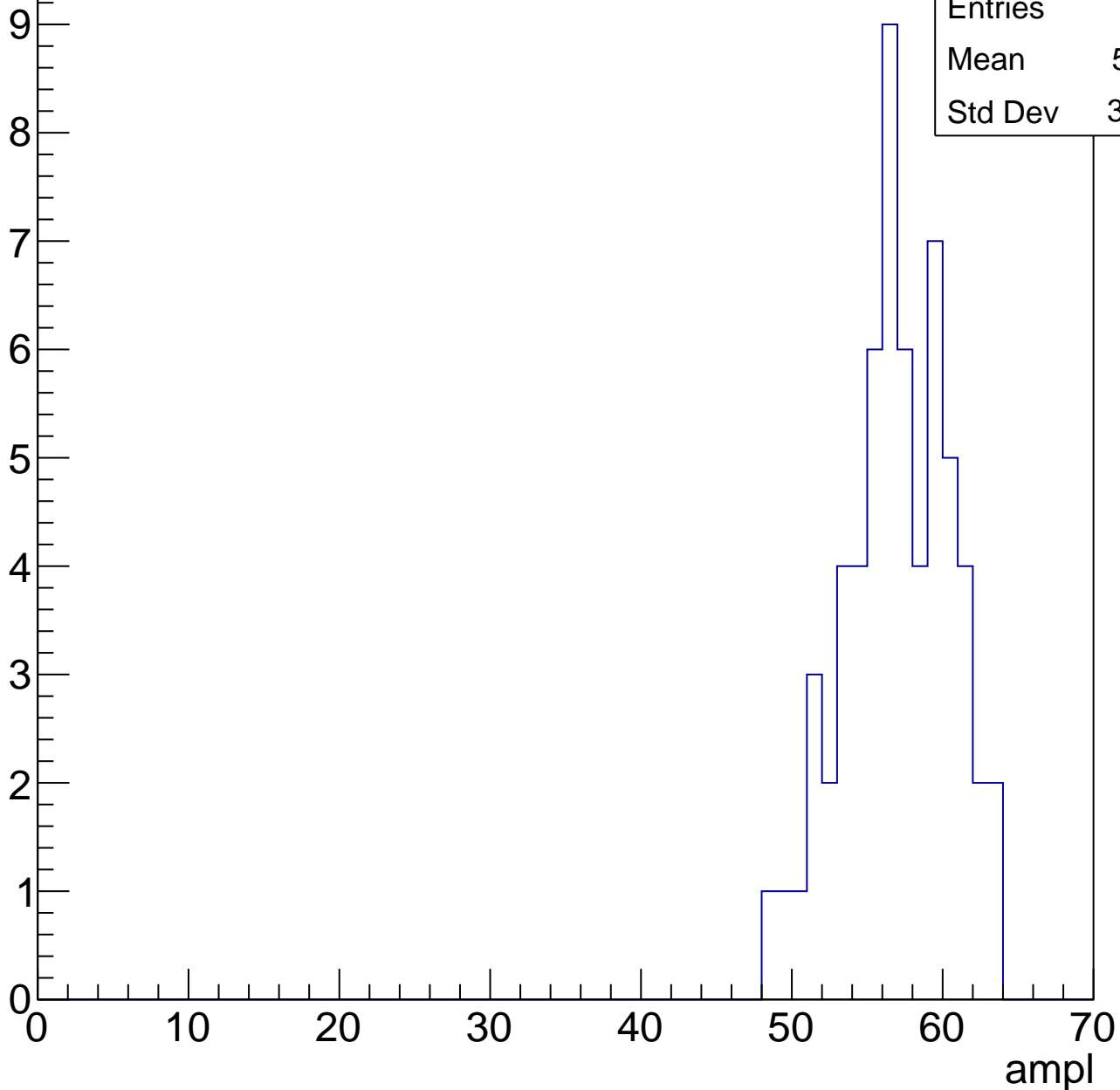


B1L102S, U13-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	56.51
Std Dev	3.467

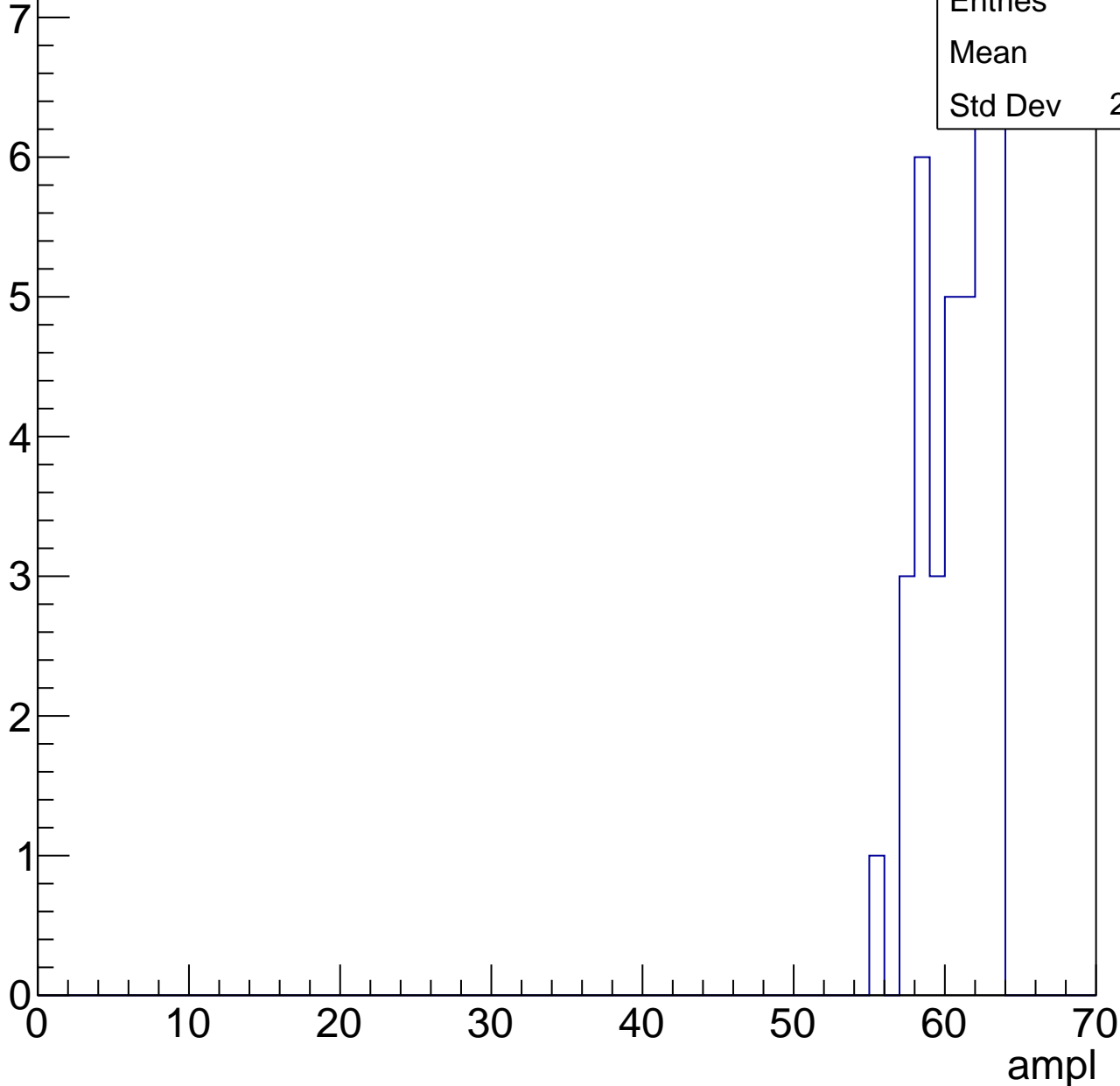


B1L102S, U13-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	60.3
Std Dev	2.154



B1L102S, U13-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch97, adc0

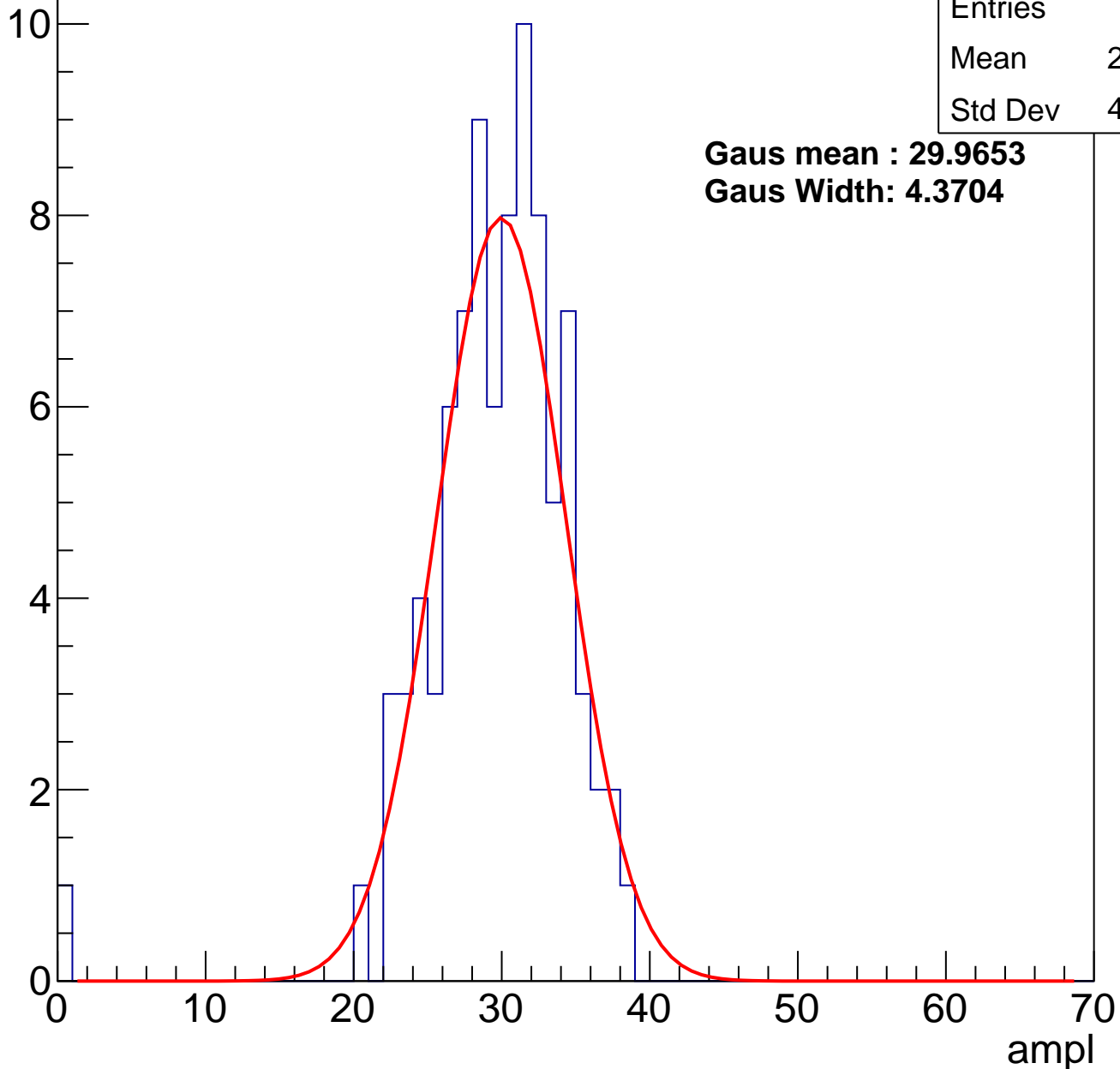
calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	29.16
Std Dev	4.976

Gaus mean : 29.9653

Gaus Width: 4.3704

Entry



B1L102S, U13-ch97, adc1

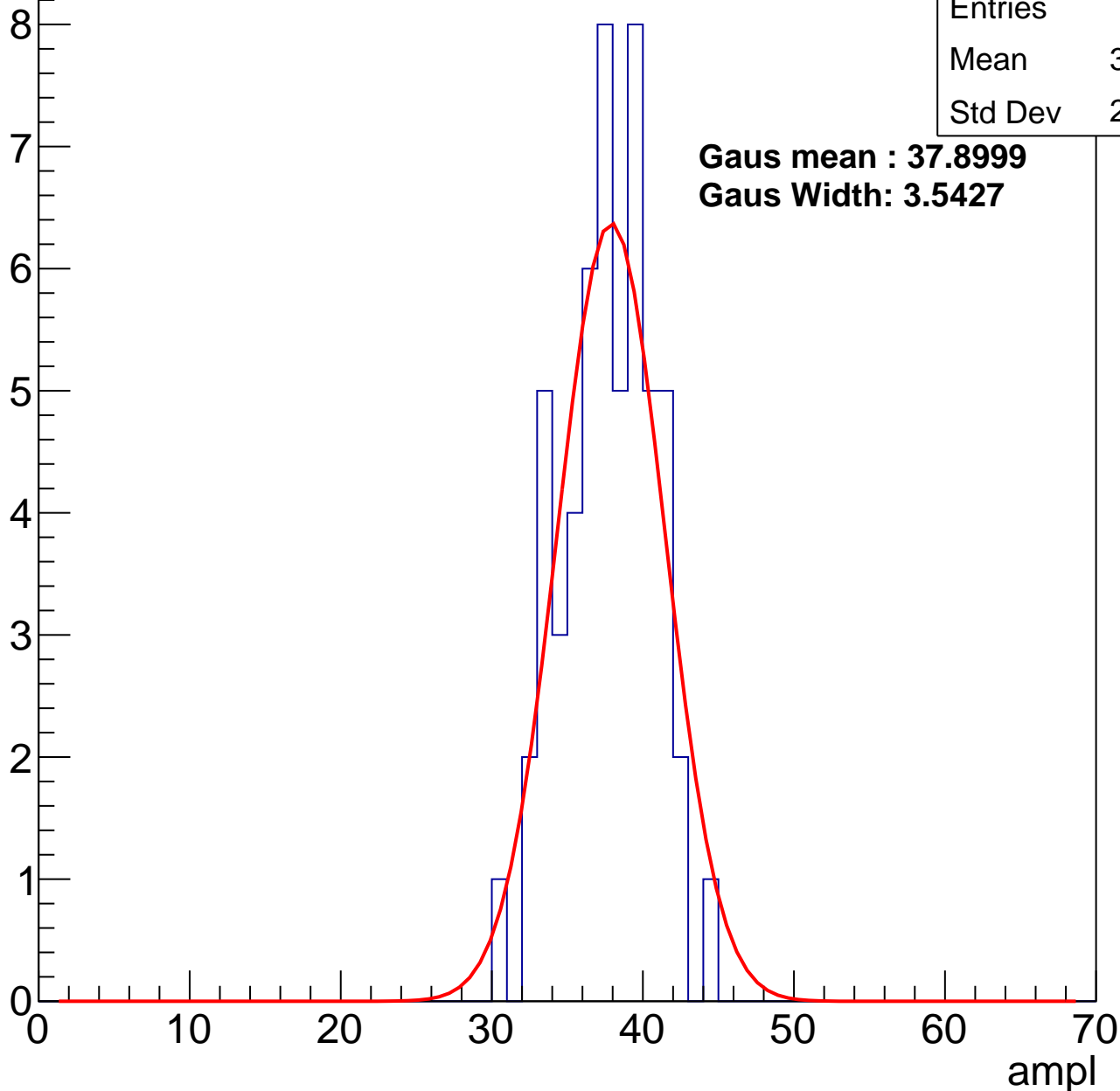
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	37.24
Std Dev	2.972

Gaus mean : 37.8999

Gaus Width: 3.5427



B1L102S, U13-ch97, adc2

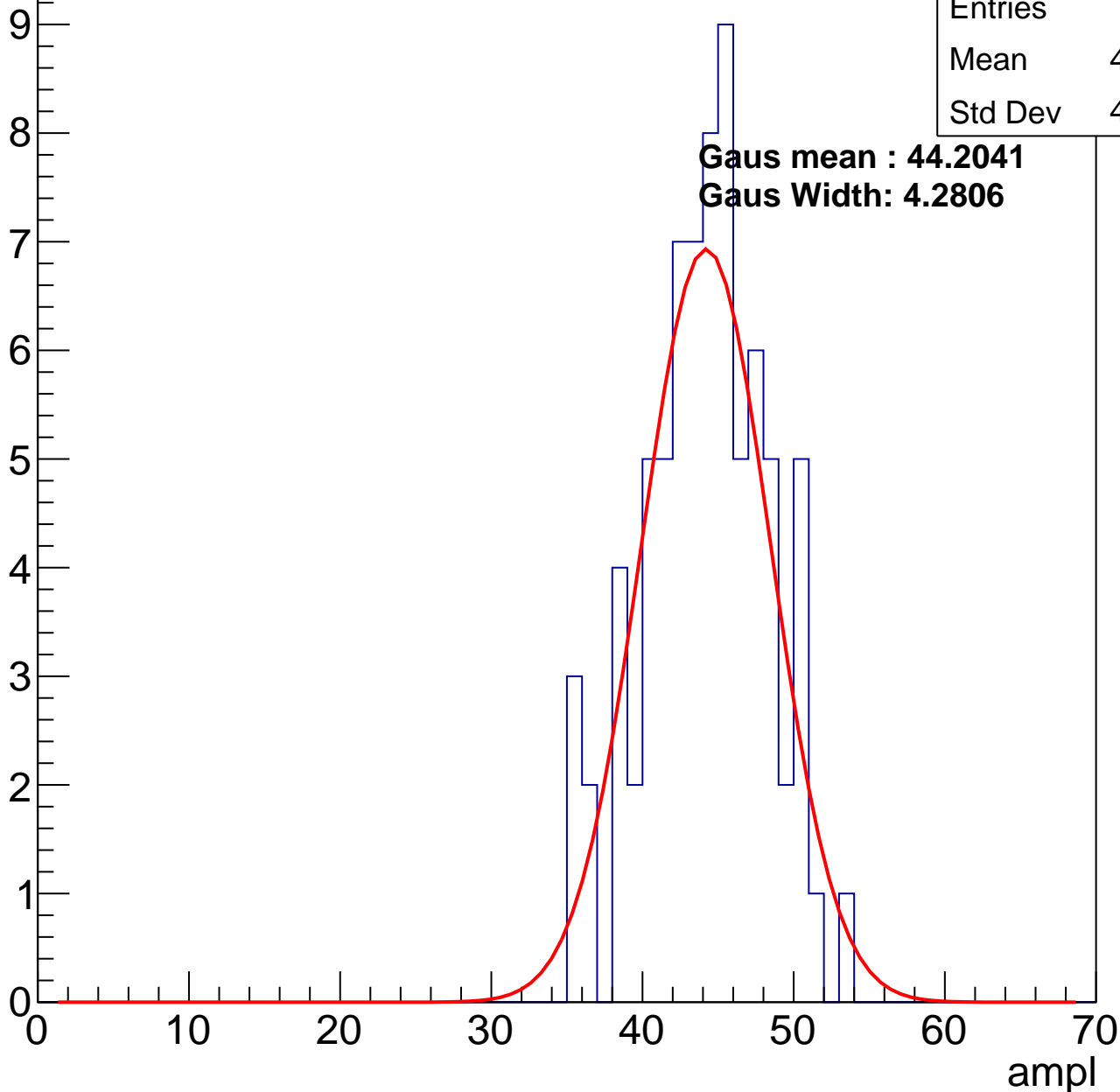
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	43.74
Std Dev	4.018

Gaus mean : 44.2041

Gaus Width: 4.2806

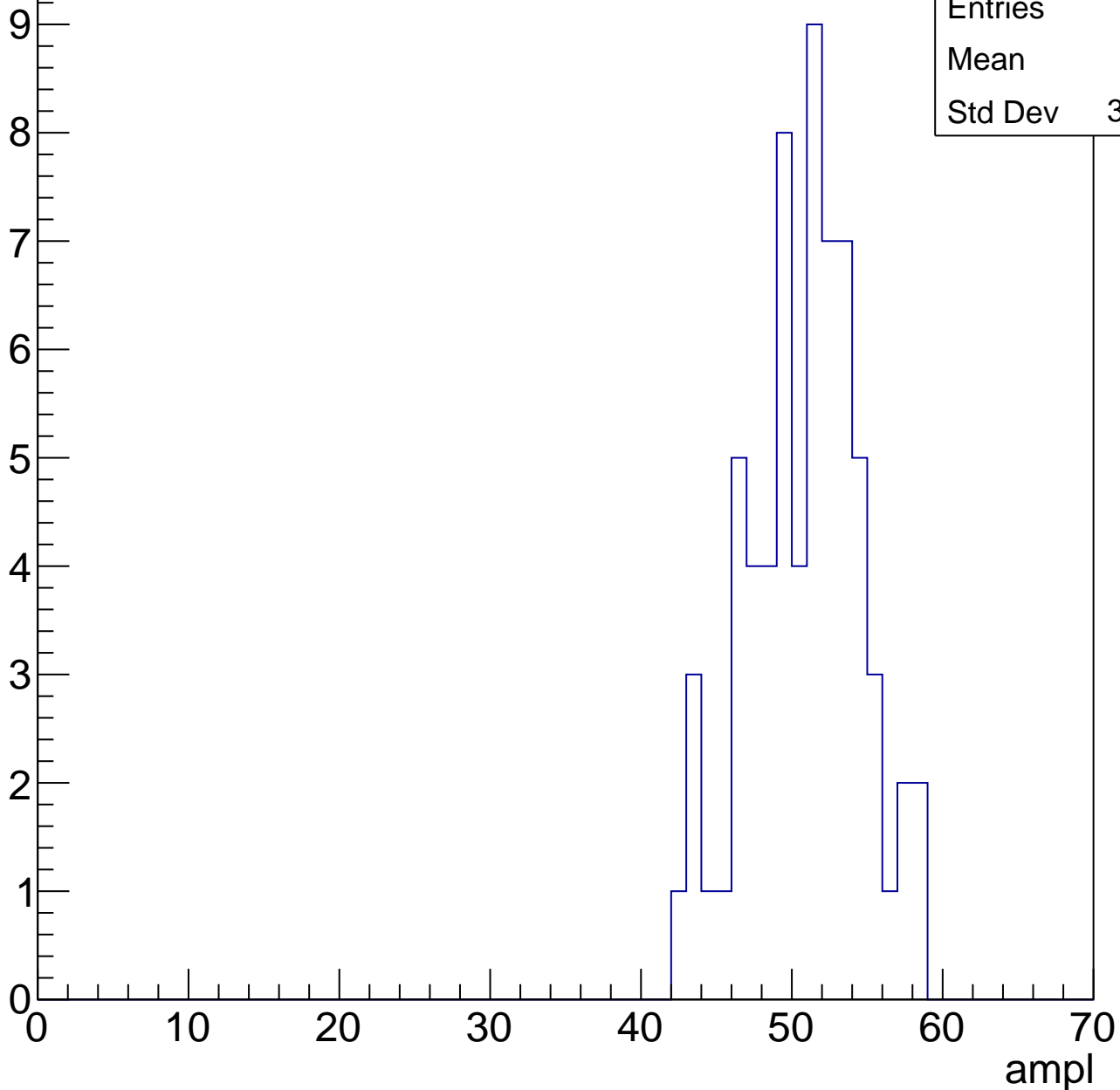


B1L102S, U13-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50.4
Std Dev	3.702

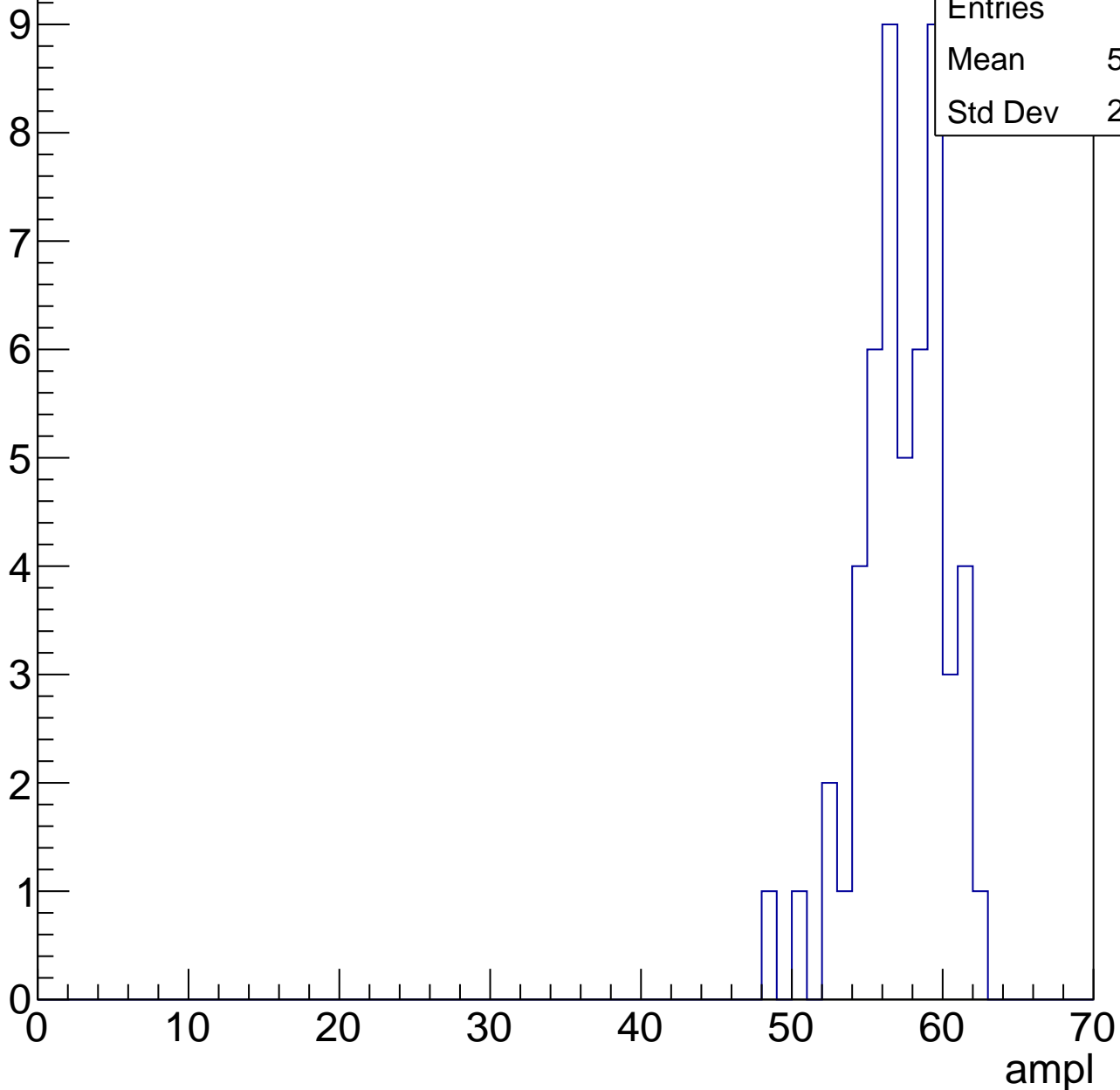


B1L102S, U13-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.83
Std Dev	2.847

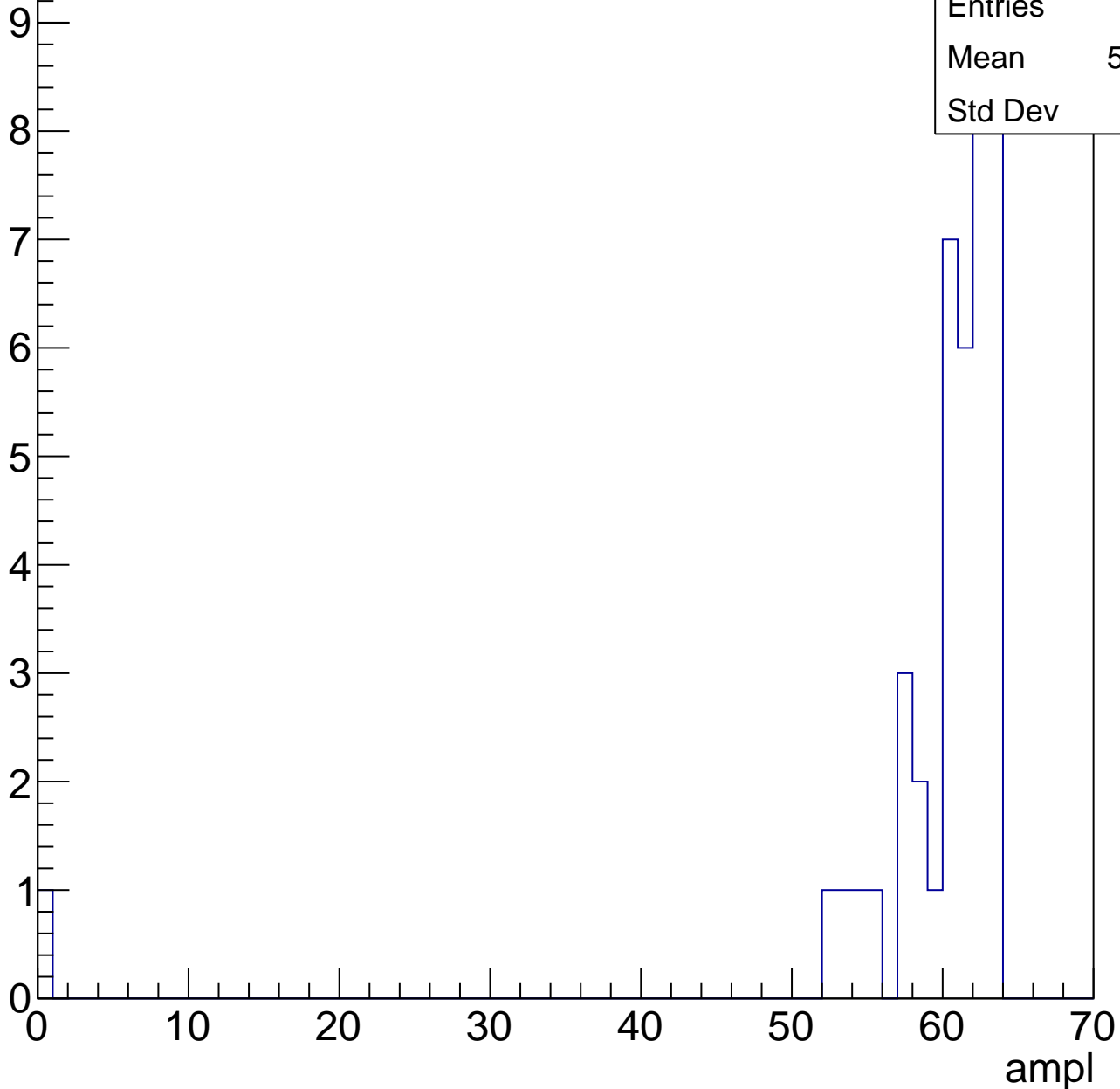


B1L102S, U13-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.73
Std Dev	9.7



B1L102S, U13-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

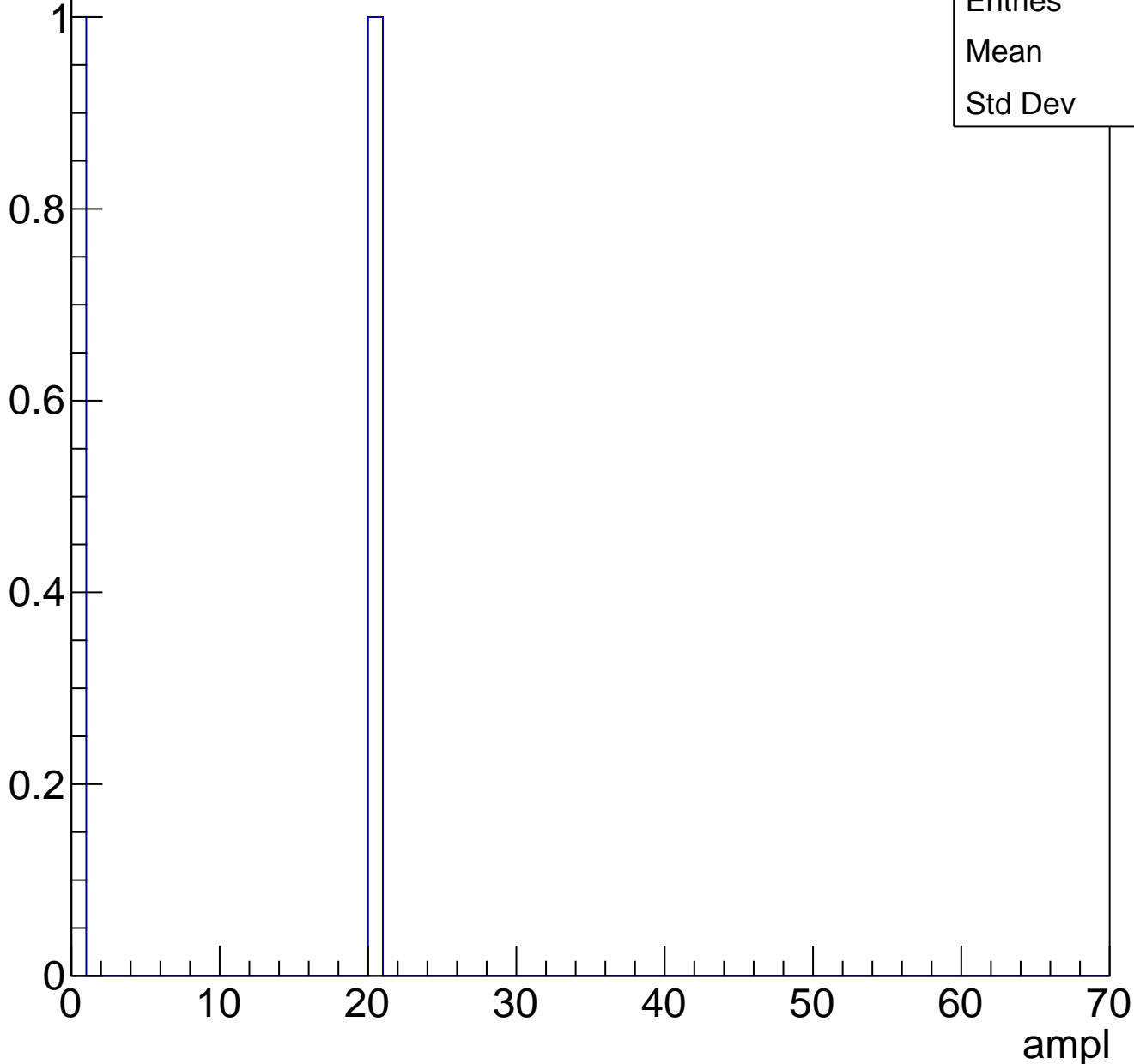
Entry



B1L102S, U13-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

B1L102S, U13-ch98, adc0

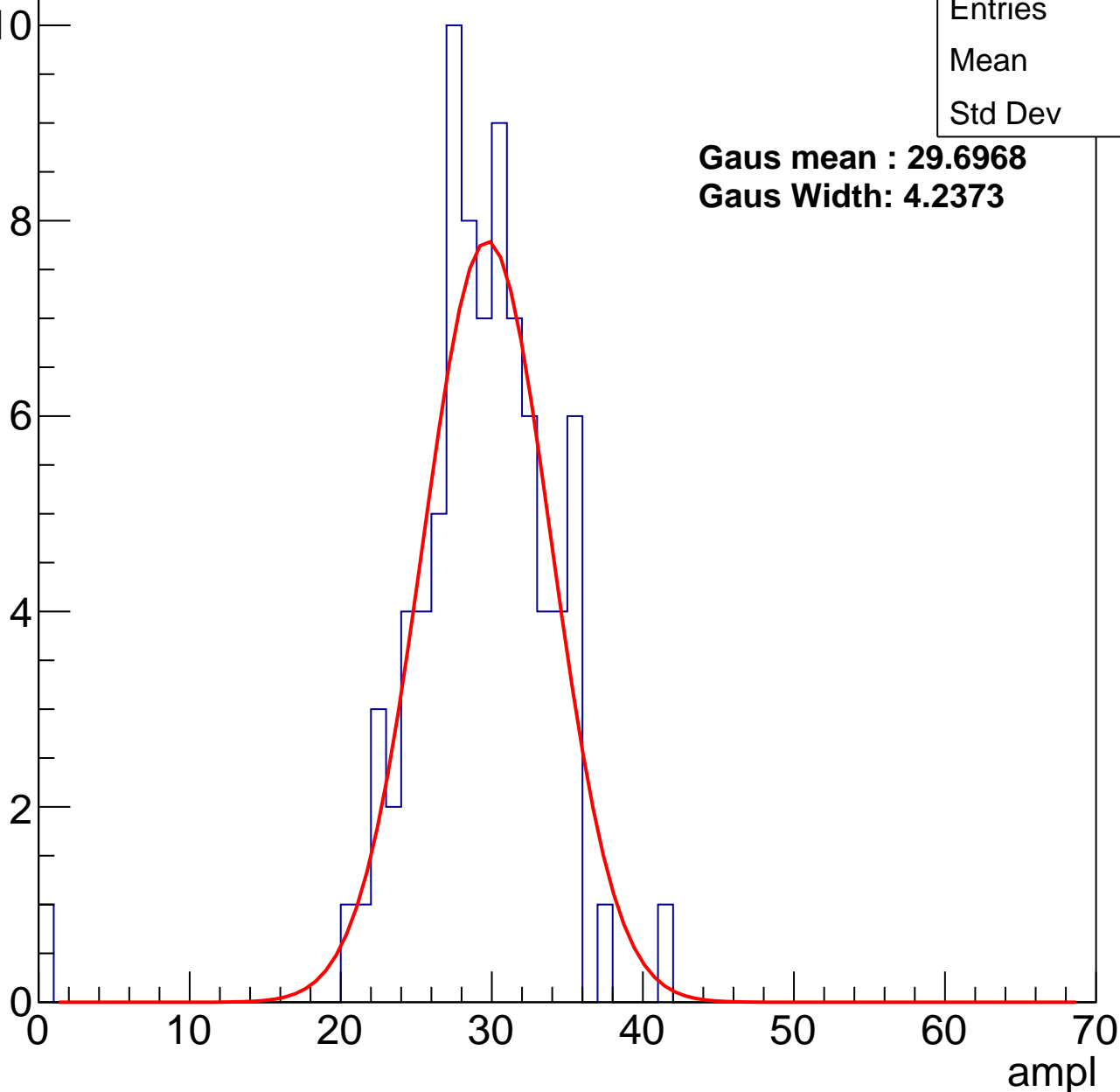
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.7
Std Dev	5.04

Gaus mean : 29.6968

Gaus Width: 4.2373



B1L102S, U13-ch98, adc1

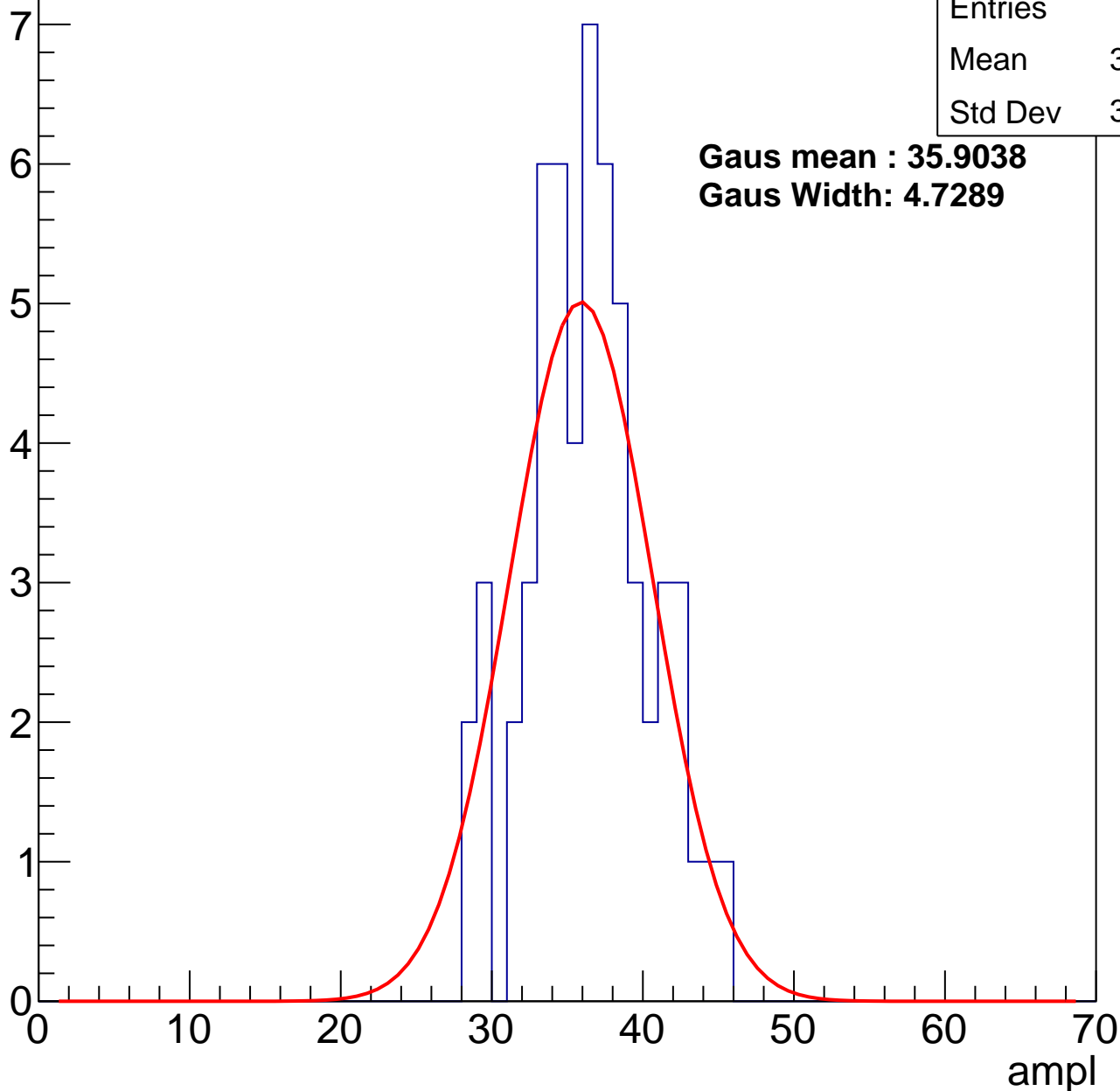
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.95
Std Dev	3.976

Gaus mean : 35.9038

Gaus Width: 4.7289



B1L102S, U13-ch98, adc2

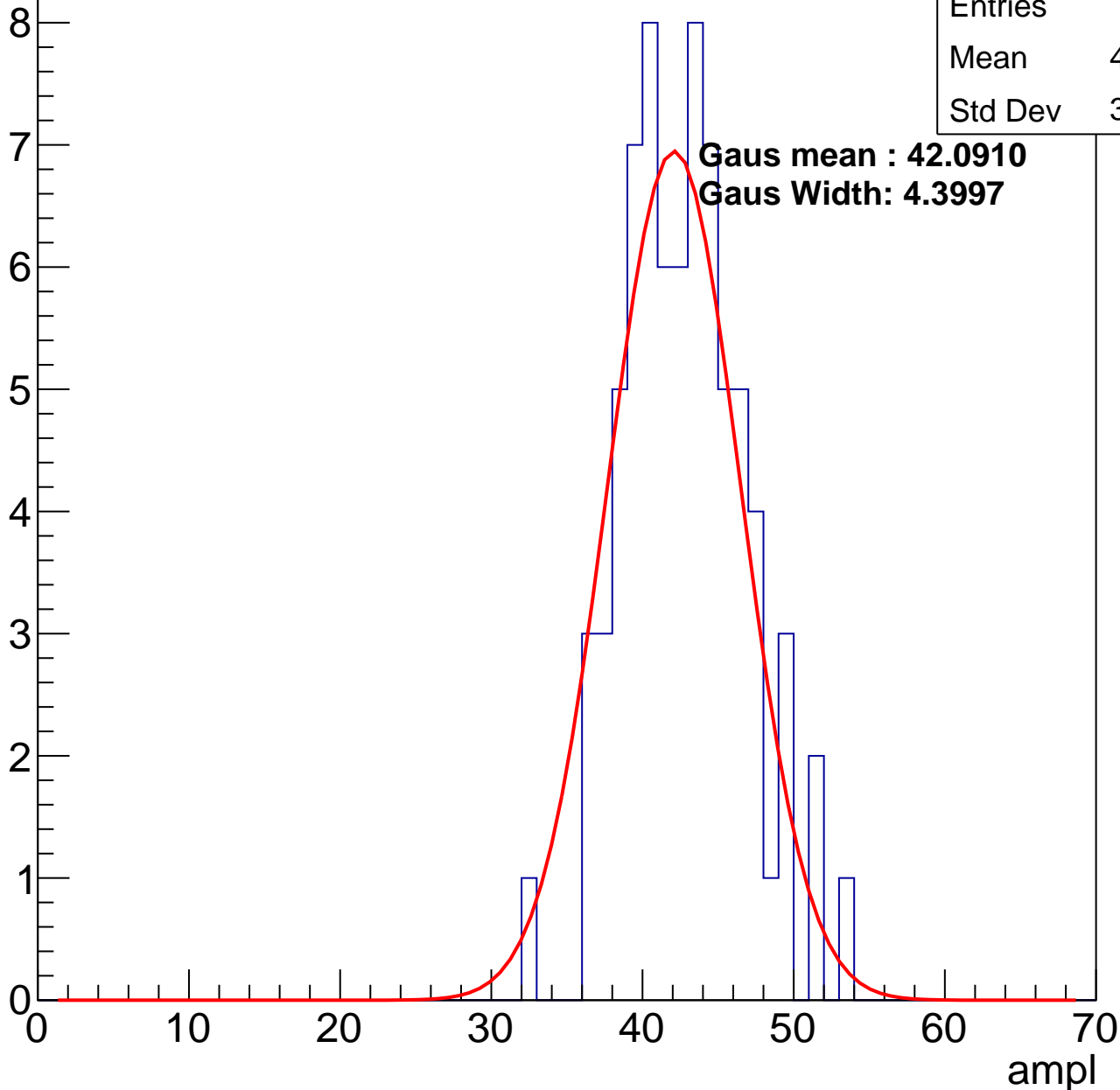
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	42.36
Std Dev	3.969

Gaus mean : 42.0910

Gaus Width: 4.3997

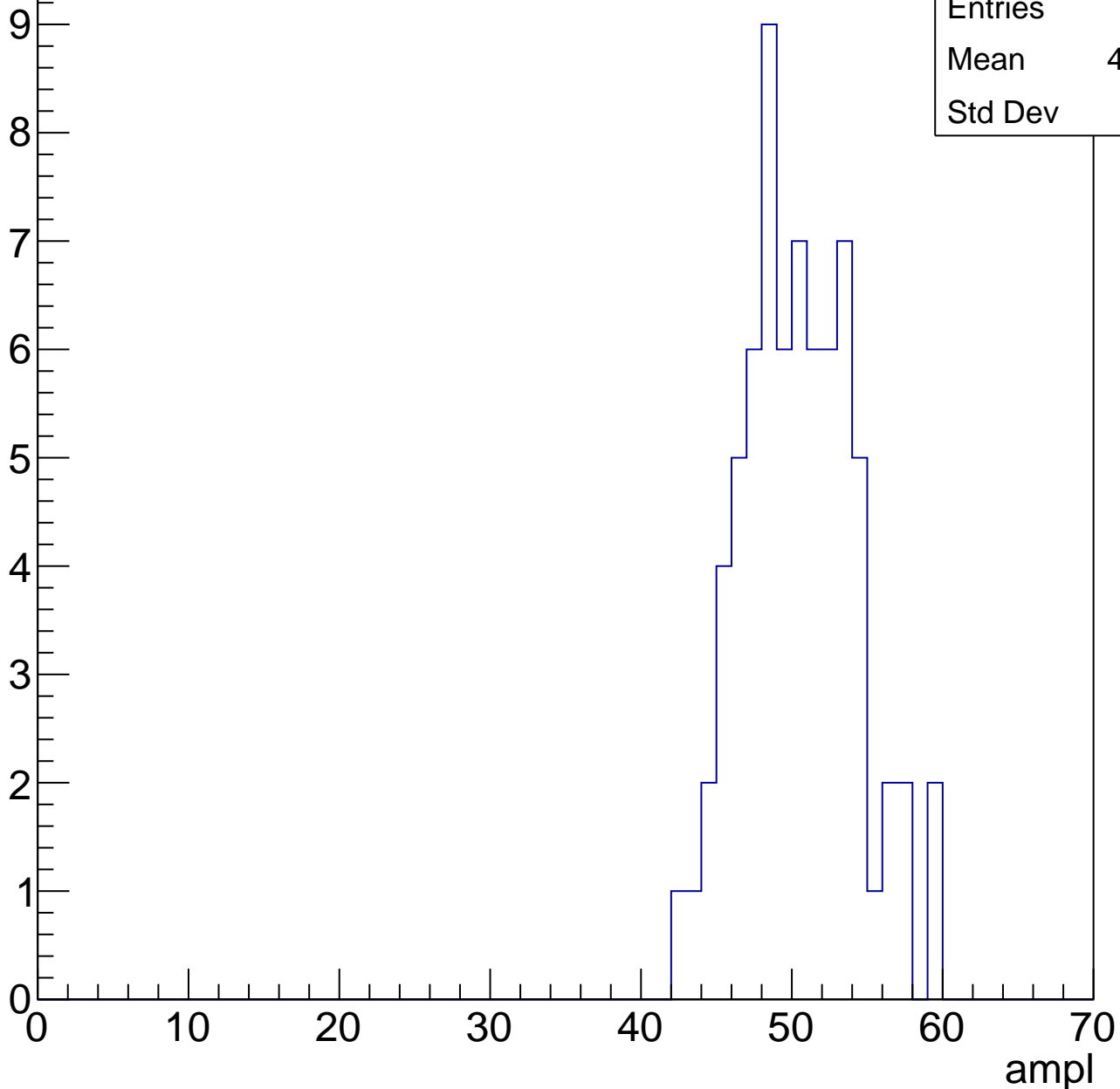


B1L102S, U13-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	49.99
Std Dev	3.71

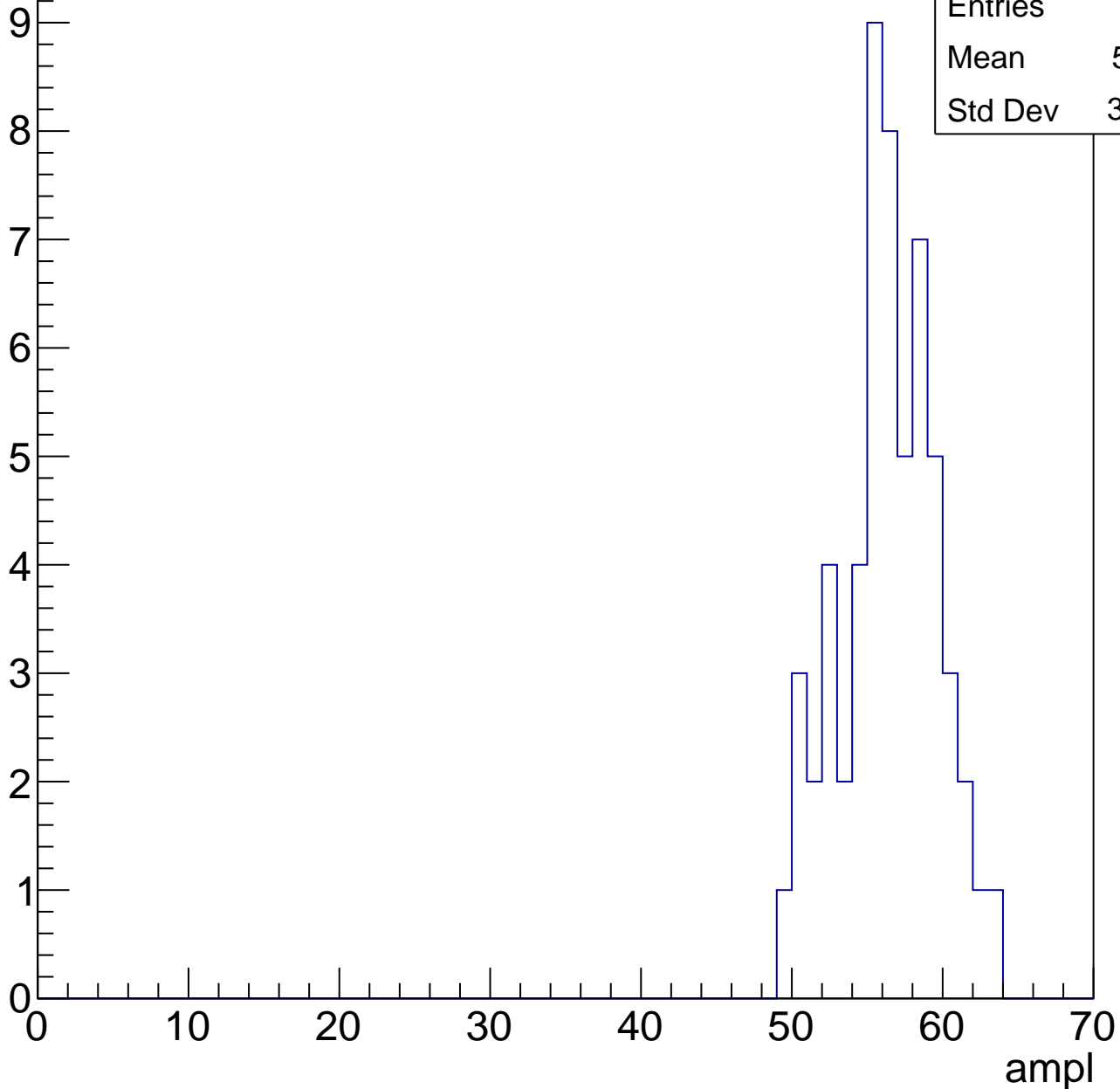


B1L102S, U13-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.91
Std Dev	3.175

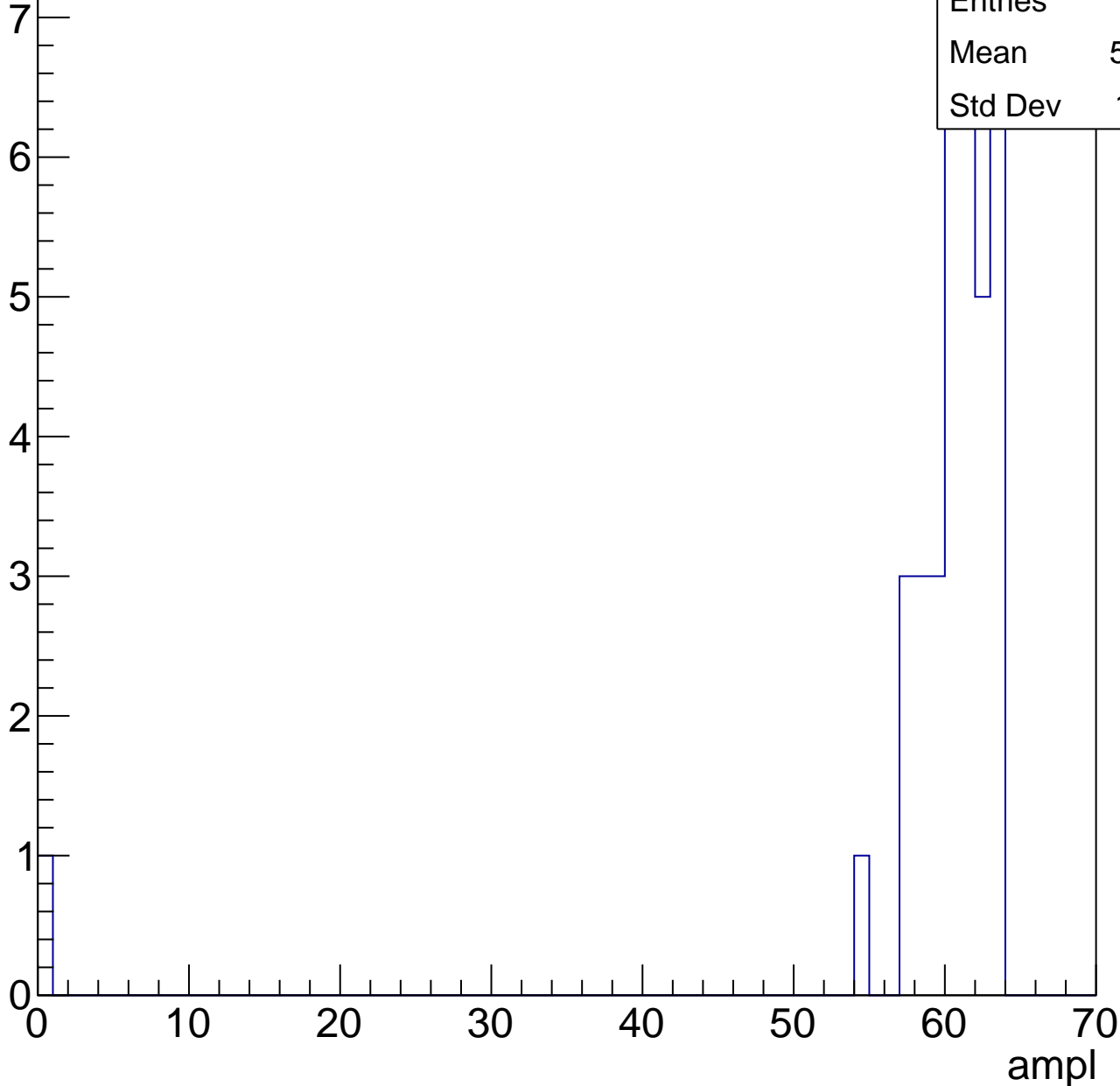


B1L102S, U13-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.76
Std Dev	10.01



B1L102S, U13-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch99, adc0

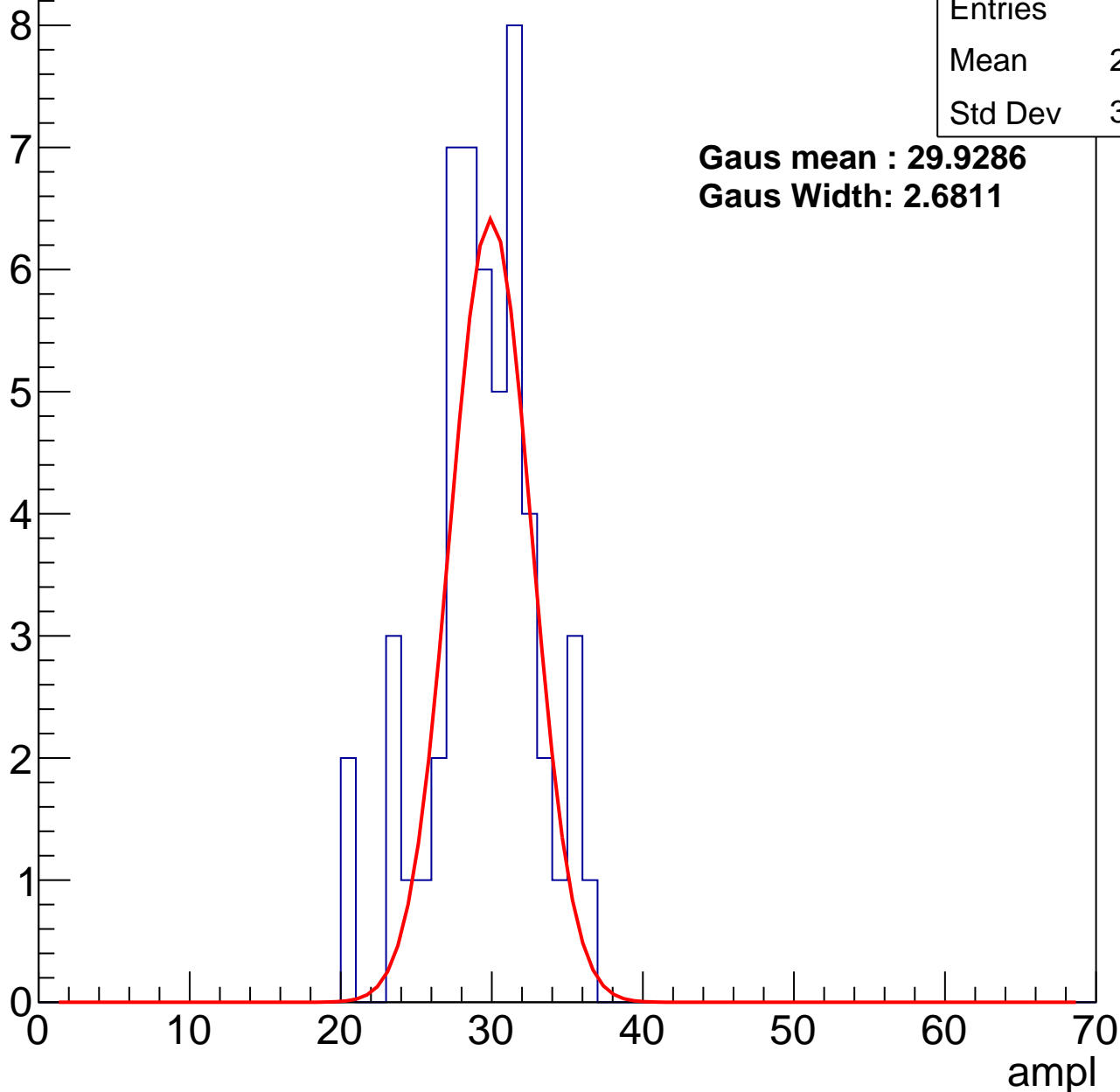
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	28.98
Std Dev	3.505

Gaus mean : 29.9286

Gaus Width: 2.6811



B1L102S, U13-ch99, adc1

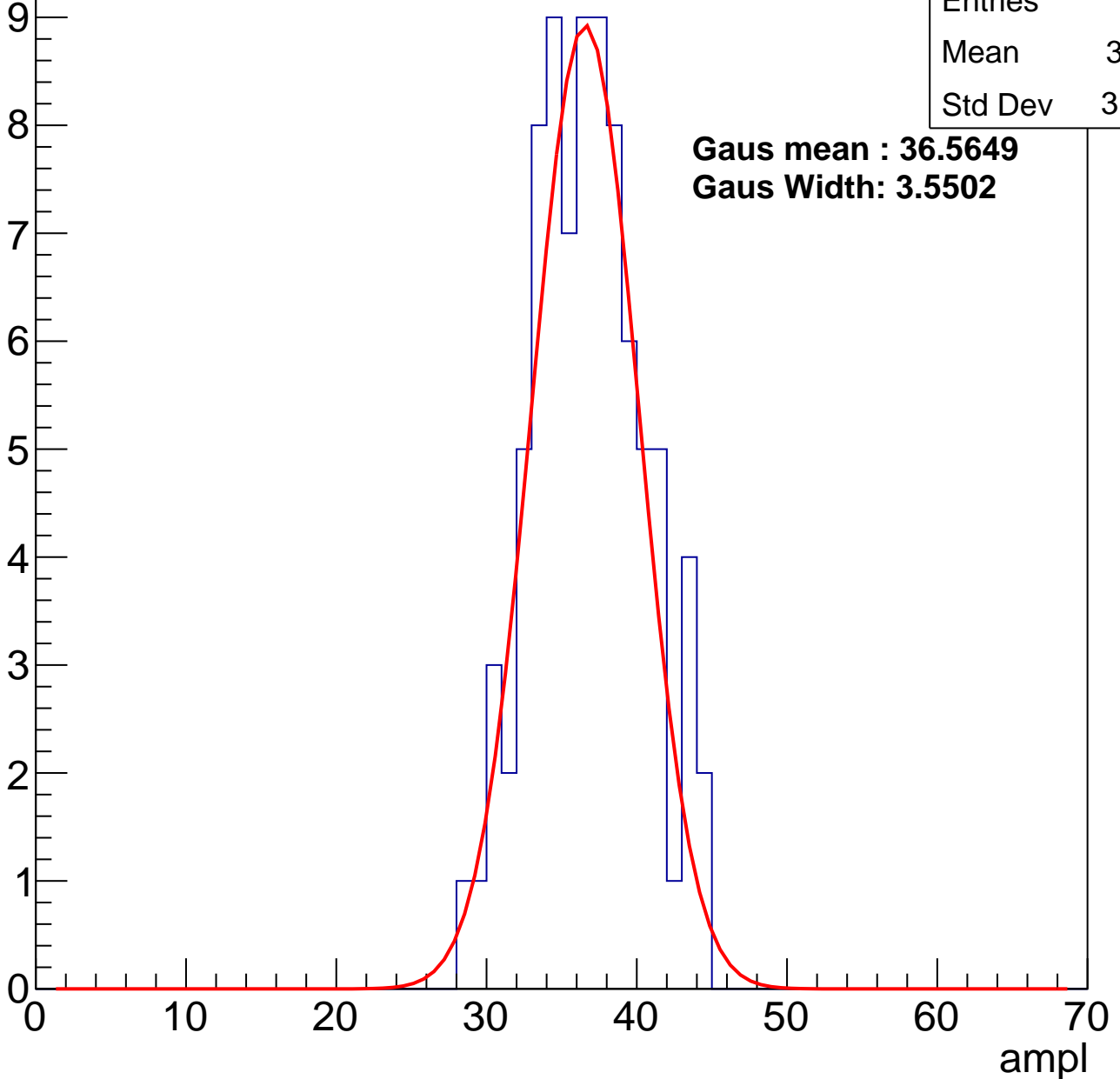
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	36.31
Std Dev	3.627

Gaus mean : 36.5649

Gaus Width: 3.5502



B1L102S, U13-ch99, adc2

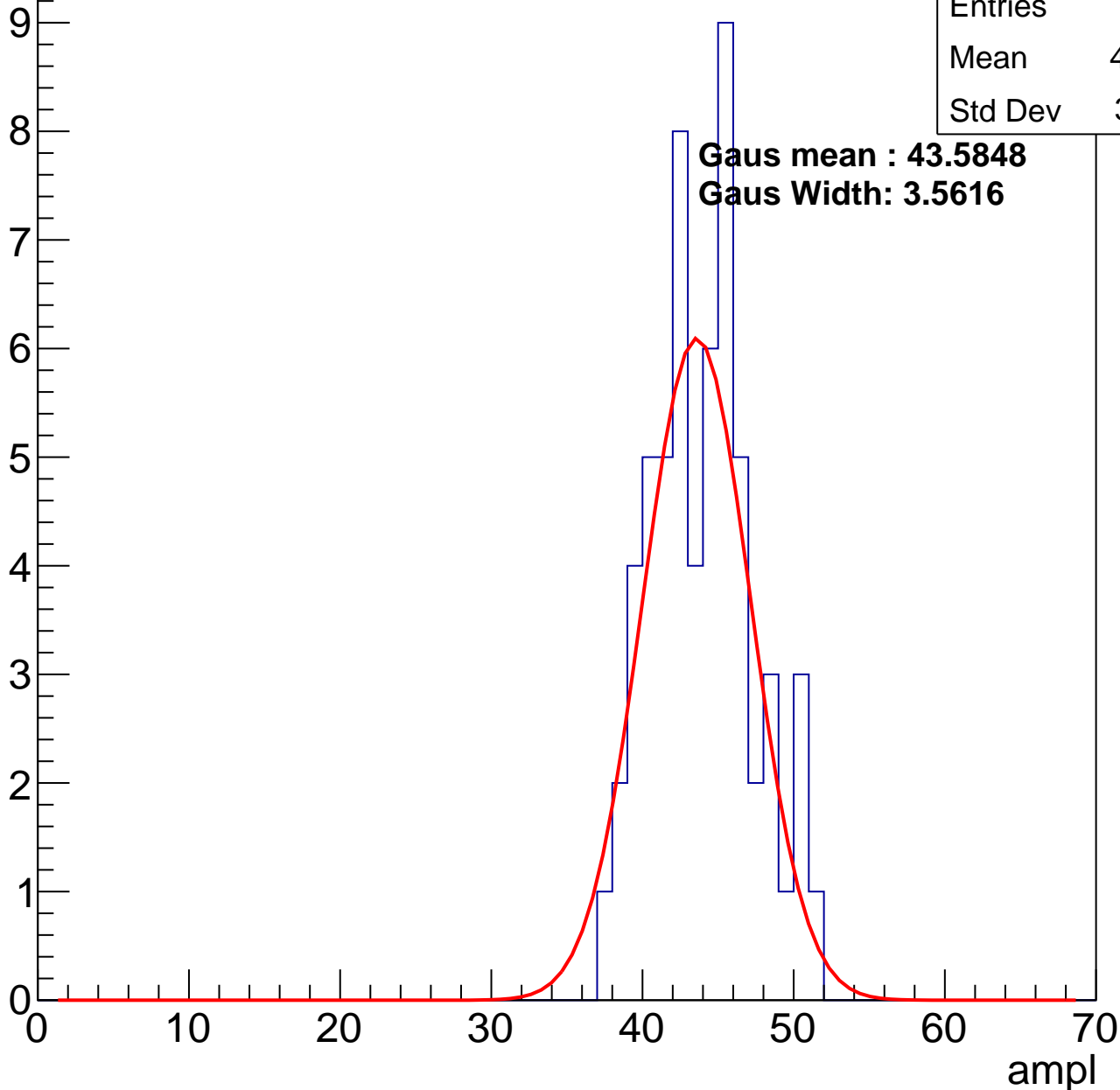
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	43.54
Std Dev	3.311

Gaus mean : 43.5848

Gaus Width: 3.5616

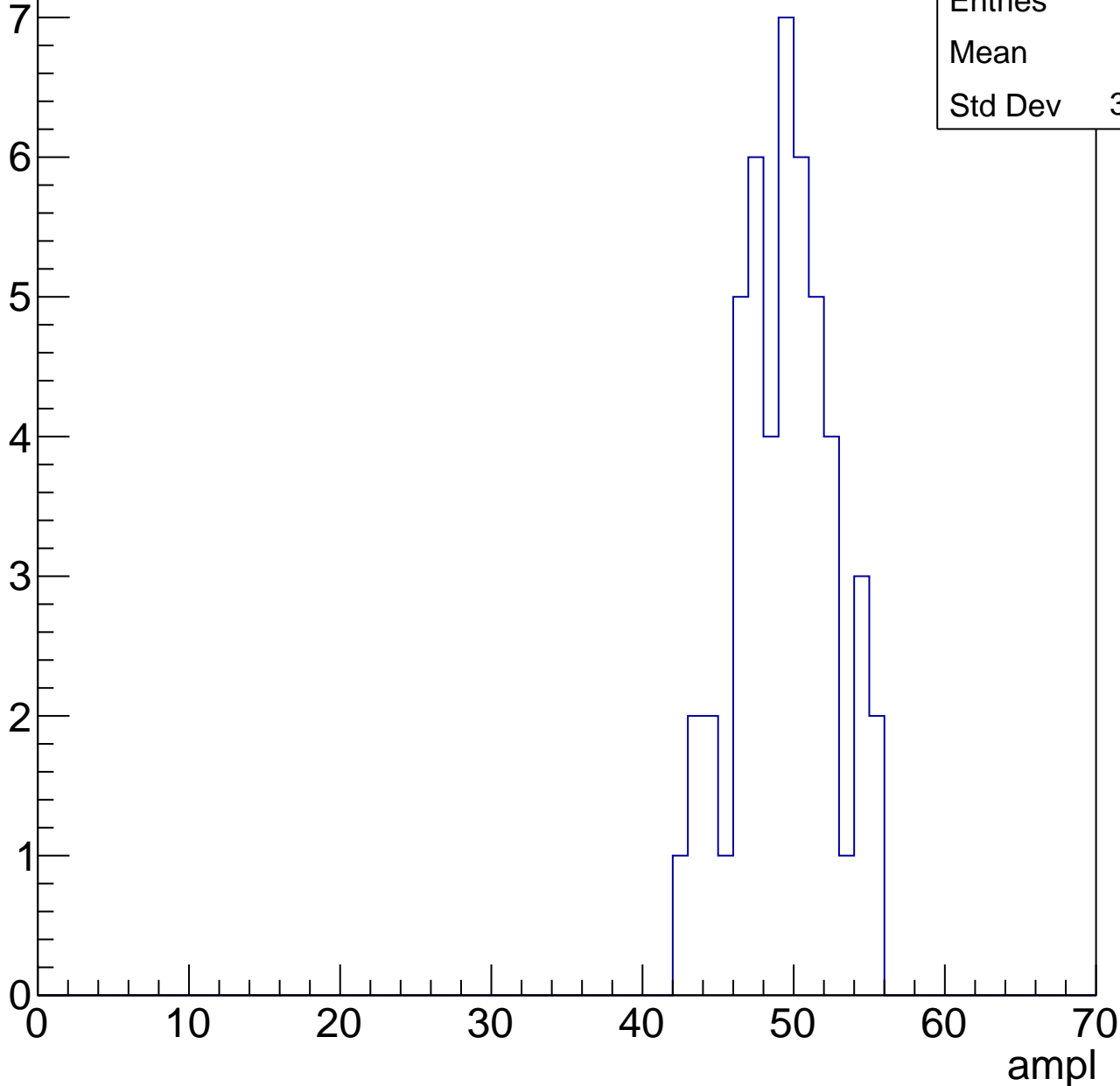


B1L102S, U13-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	48.9
Std Dev	3.144

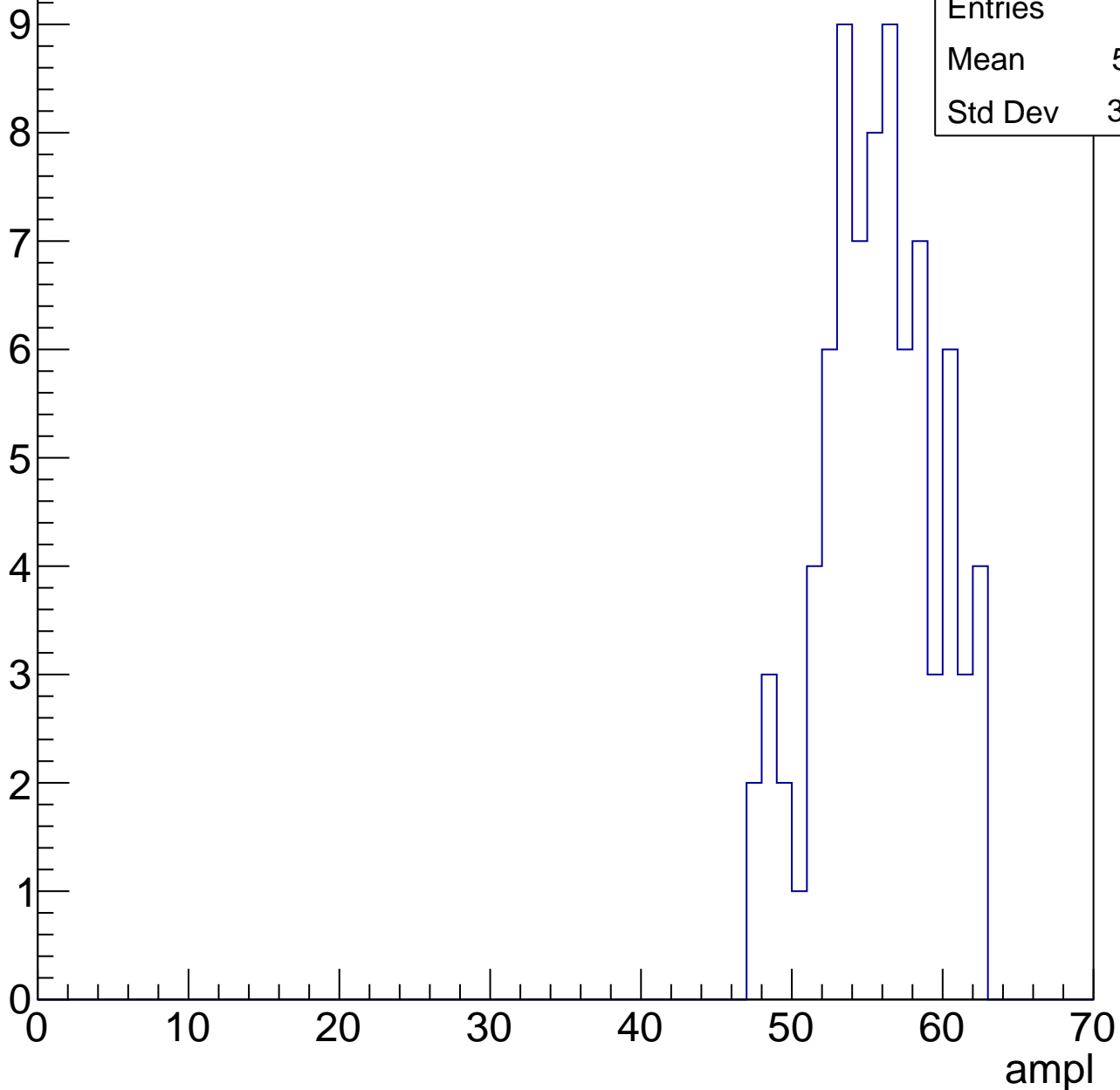


B1L102S, U13-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	55.21
Std Dev	3.754

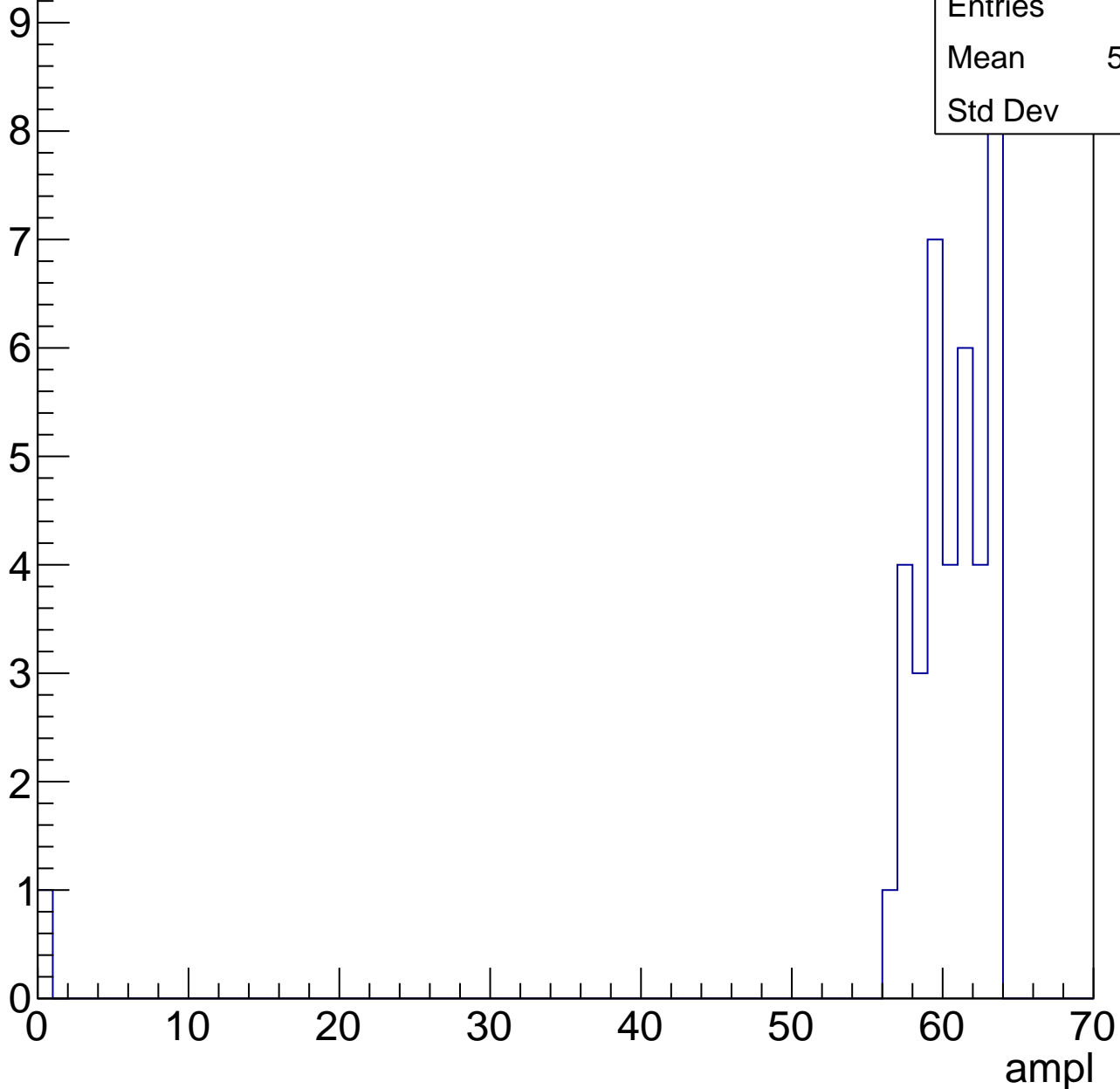


B1L102S, U13-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.77
Std Dev	9.76



B1L102S, U13-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U13-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch100, adc0

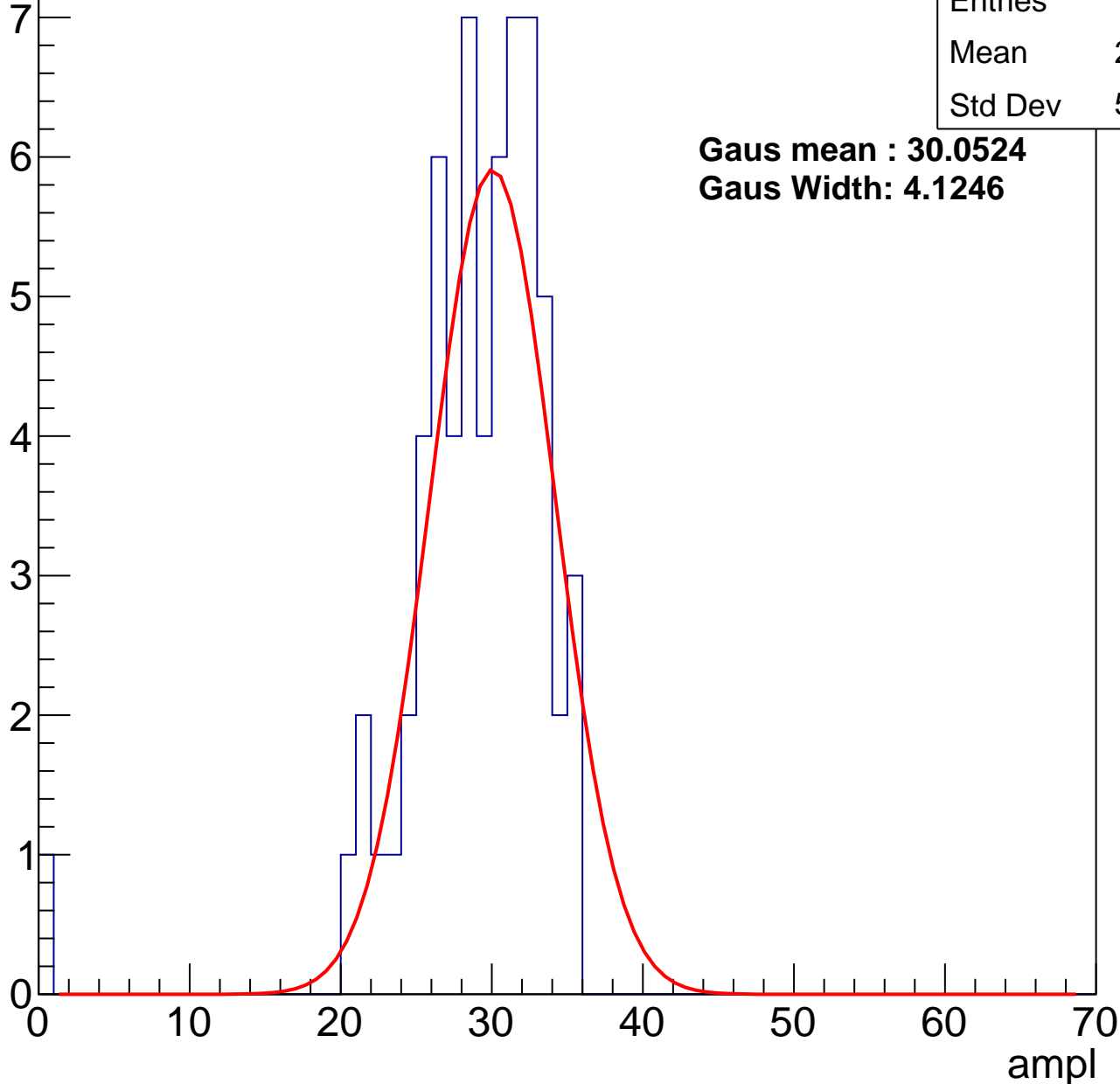
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.41
Std Dev	5.101

Gaus mean : 30.0524

Gaus Width: 4.1246



B1L102S, U13-ch100, adc1

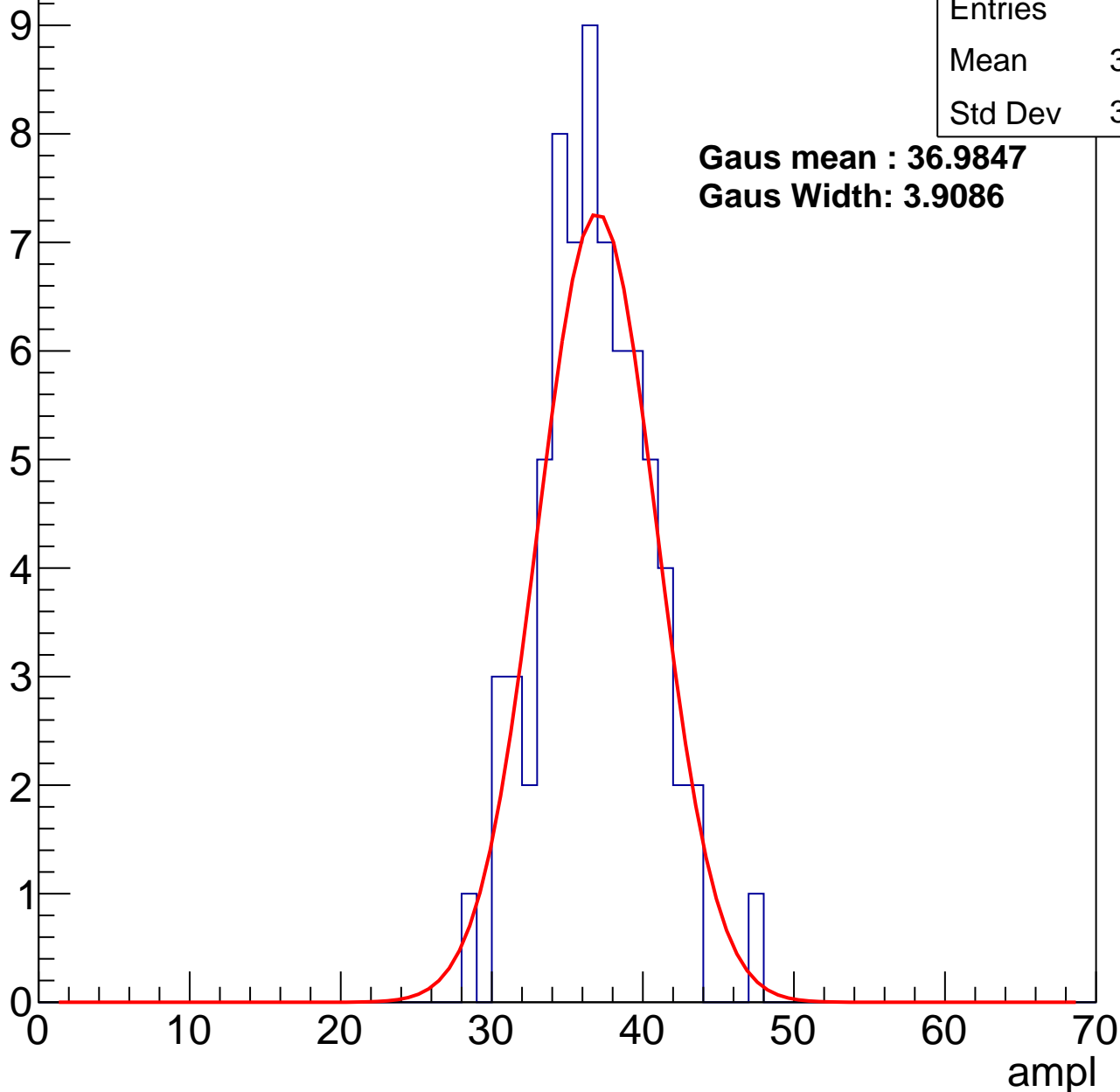
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.38
Std Dev	3.578

Gaus mean : 36.9847

Gaus Width: 3.9086



B1L102S, U13-ch100, adc2

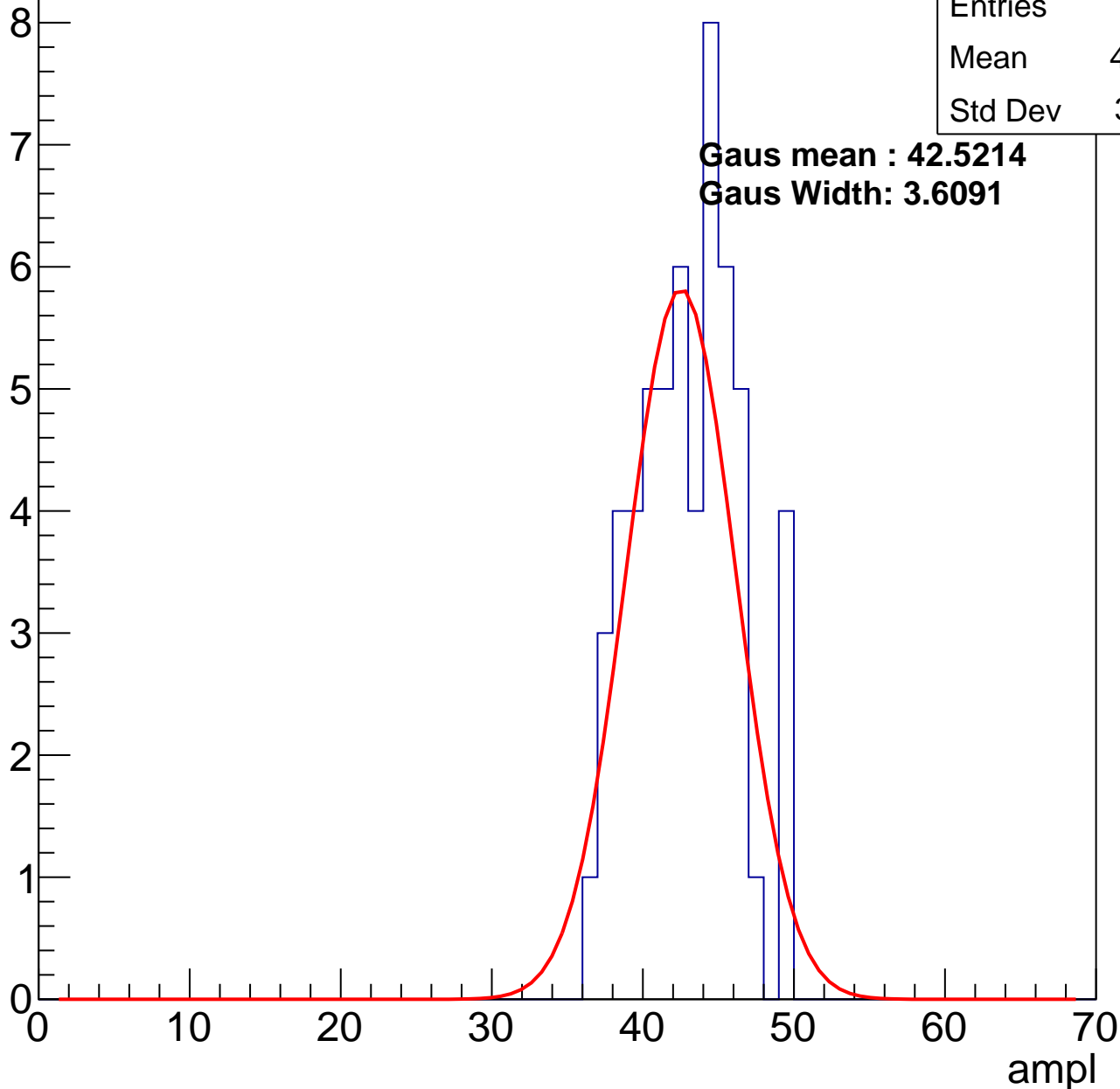
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.48
Std Dev	3.311

Gaus mean : 42.5214

Gaus Width: 3.6091



B1L102S, U13-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

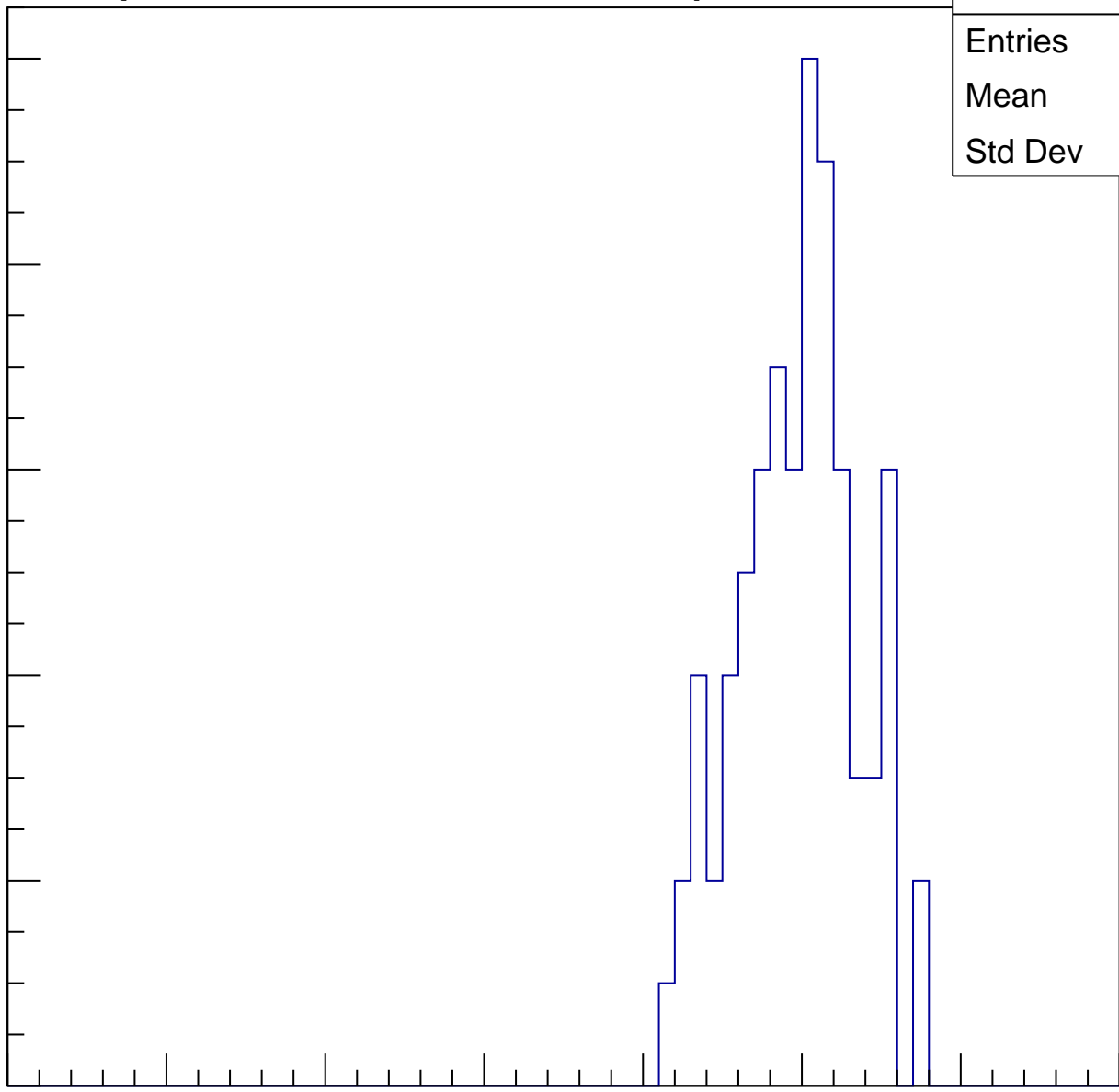
Entries	76
Mean	49.25
Std Dev	3.732

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

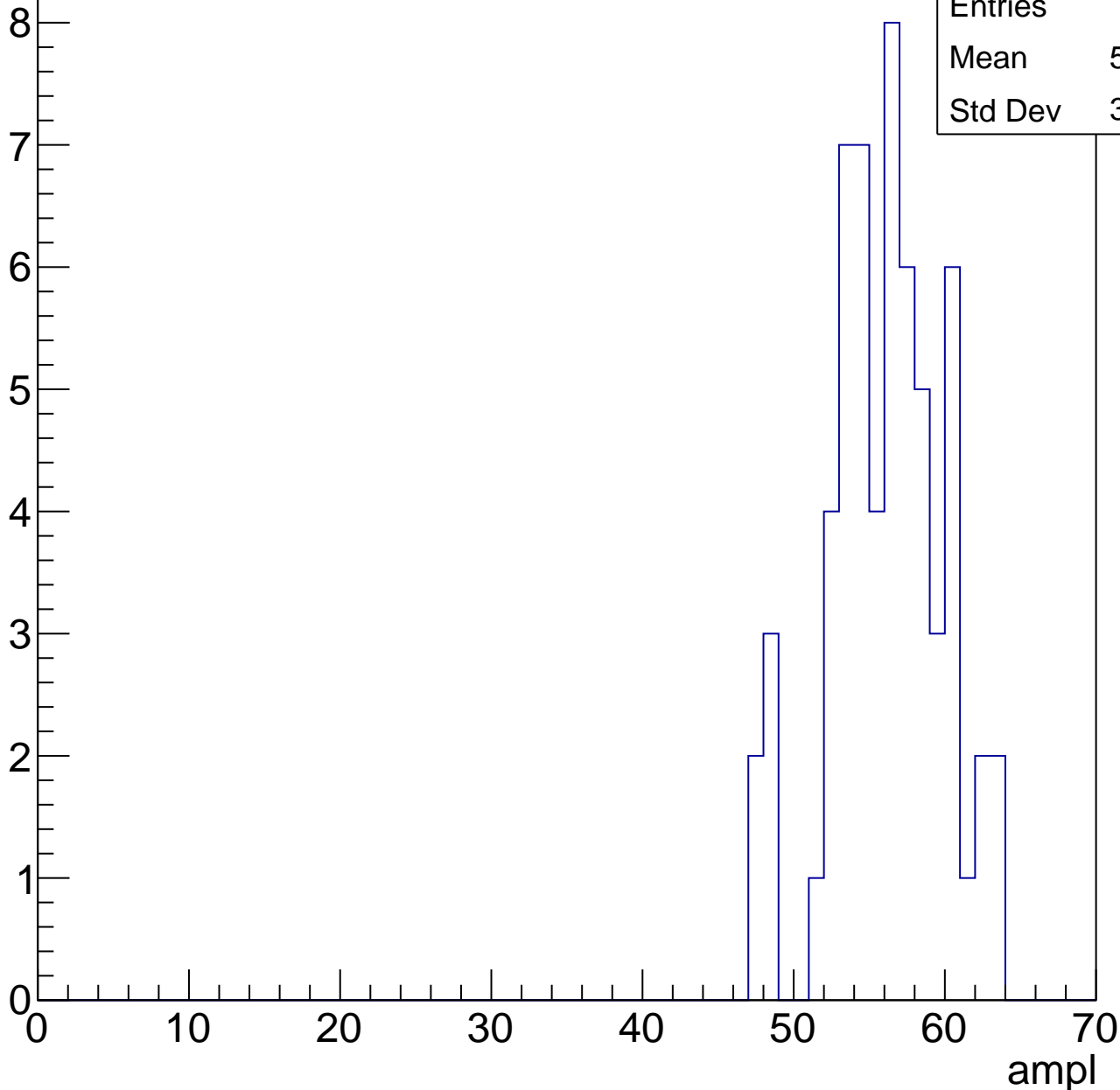


B1L102S, U13-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.64
Std Dev	3.785

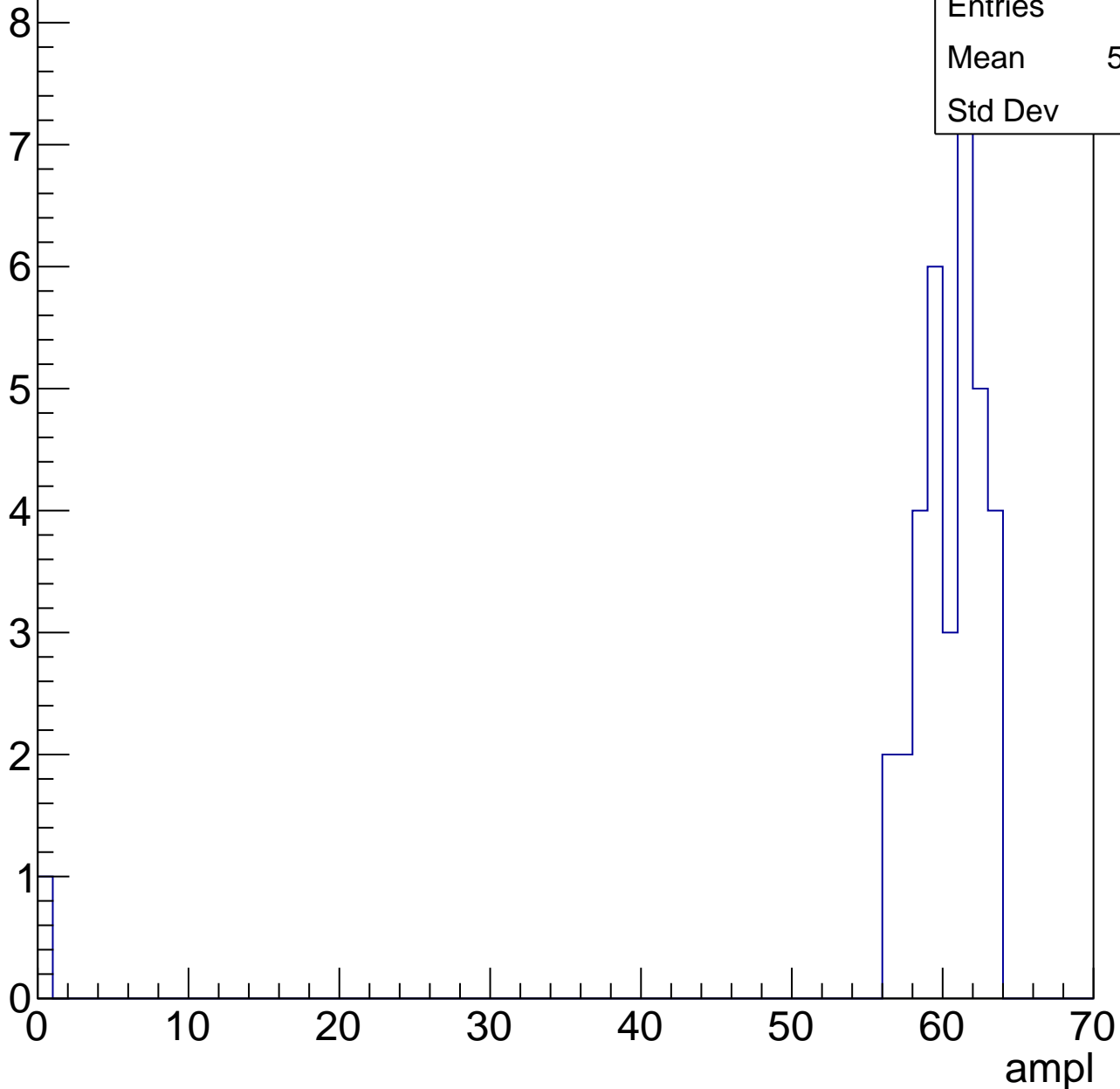


B1L102S, U13-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

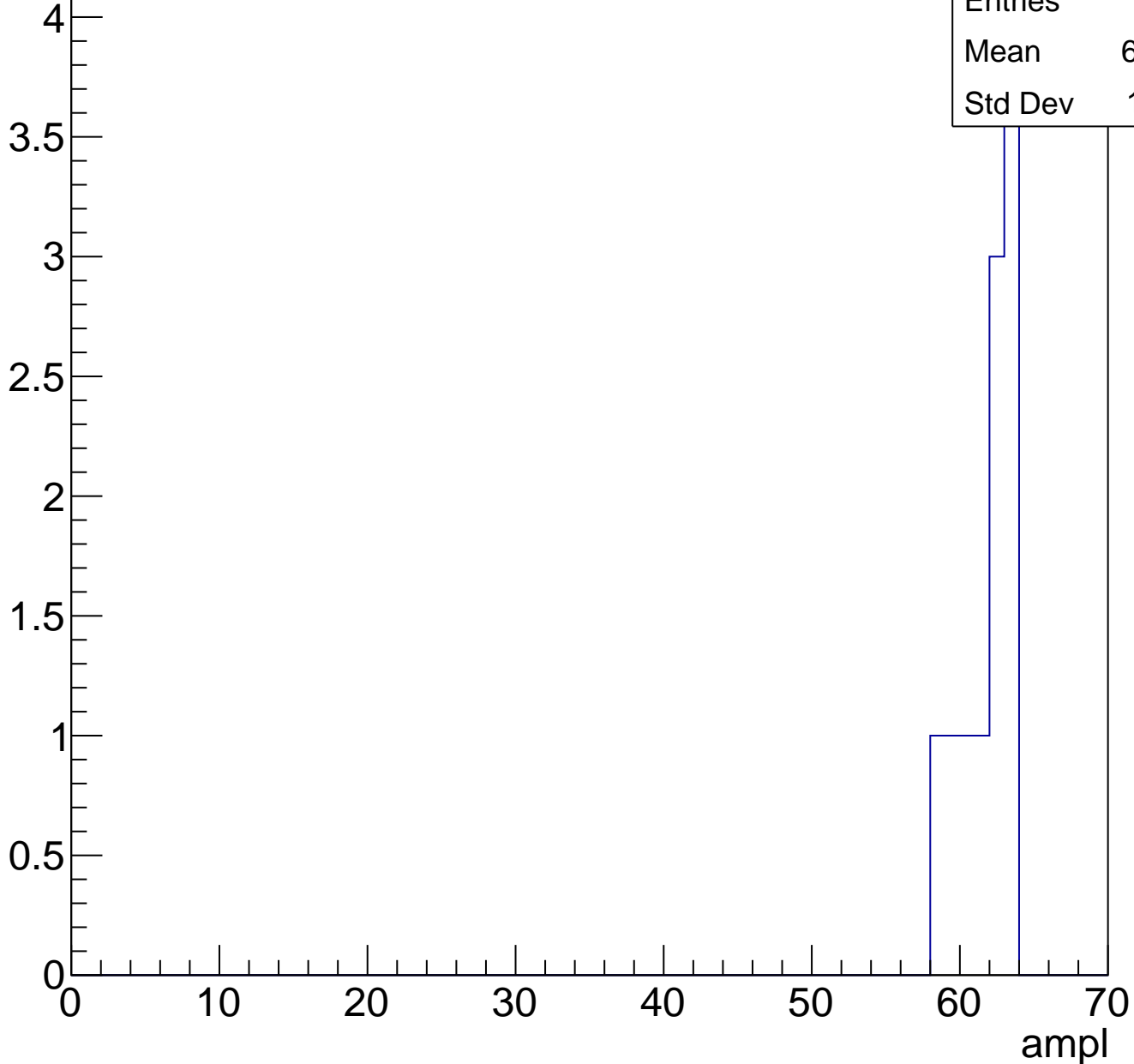
Entries	35
Mean	58.34
Std Dev	10.2



B1L102S, U13-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch101, adc0

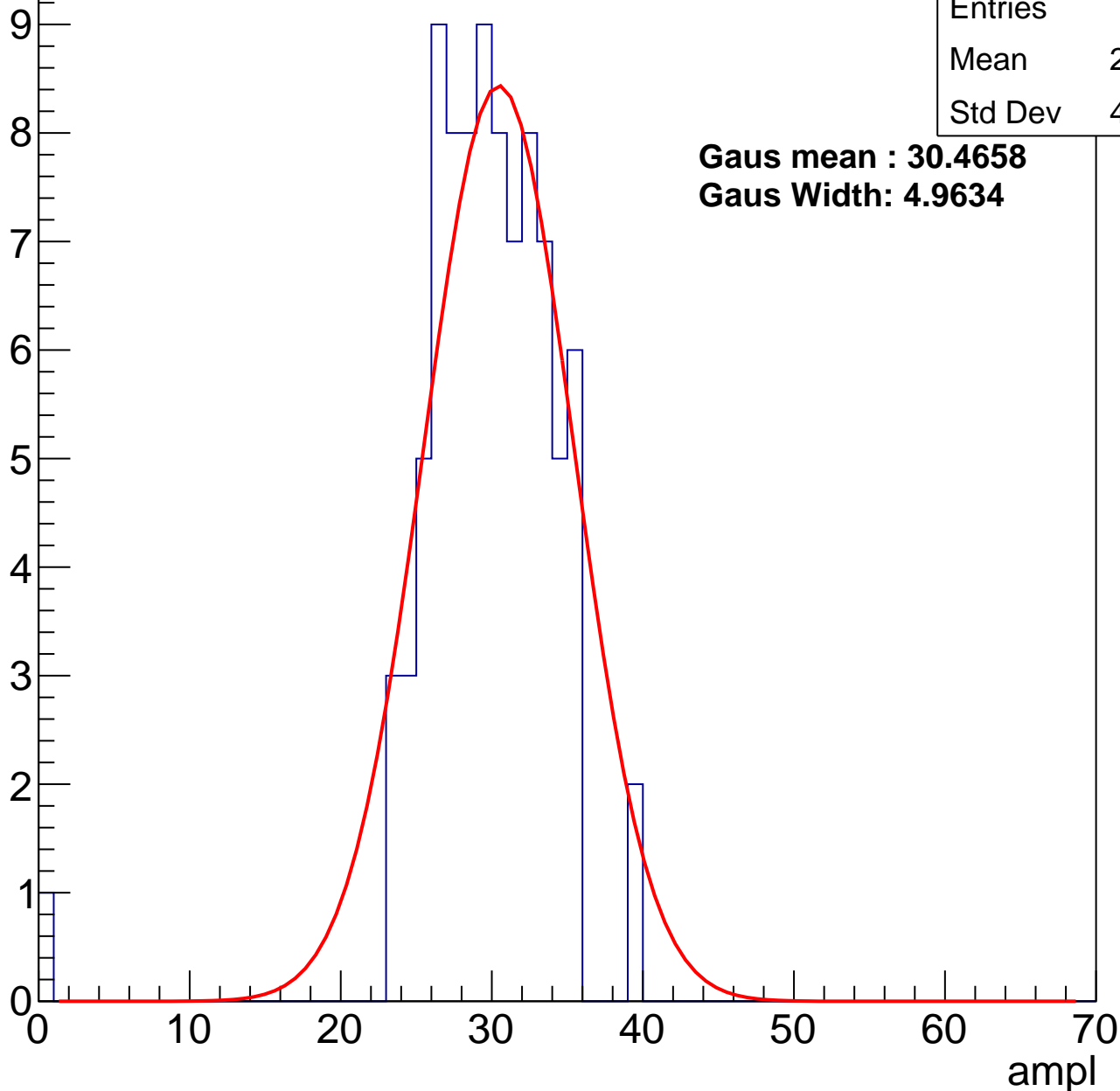
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	29.25
Std Dev	4.717

Gaus mean : 30.4658

Gaus Width: 4.9634



B1L102S, U13-ch101, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	37.6
Std Dev	3.719

Gaus mean : 38.3444

Gaus Width: 4.1837

Entry

10
8
6
4
2
0

0

10

20

30

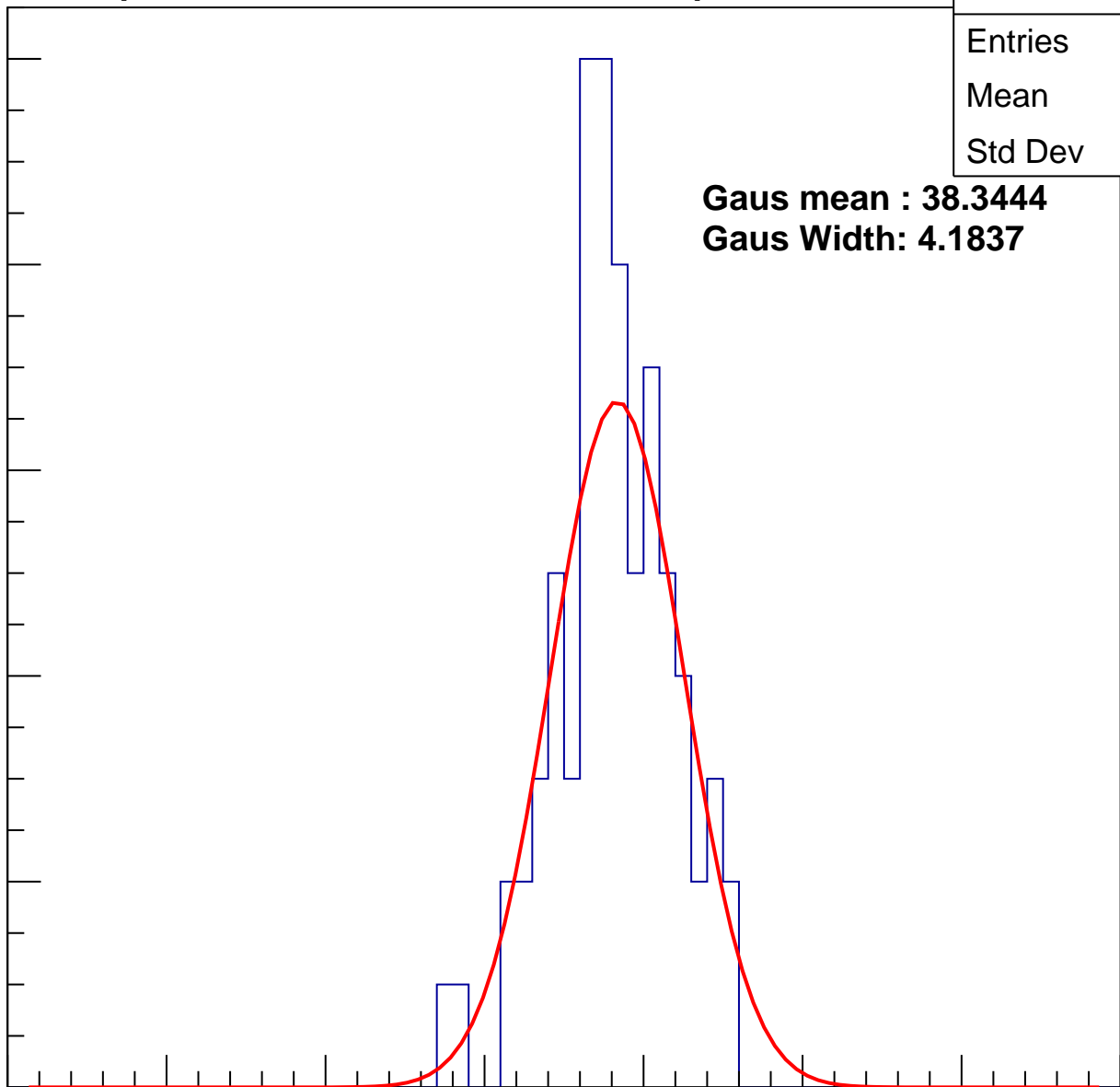
40

50

60

70

ampl



B1L102S, U13-ch101, adc2

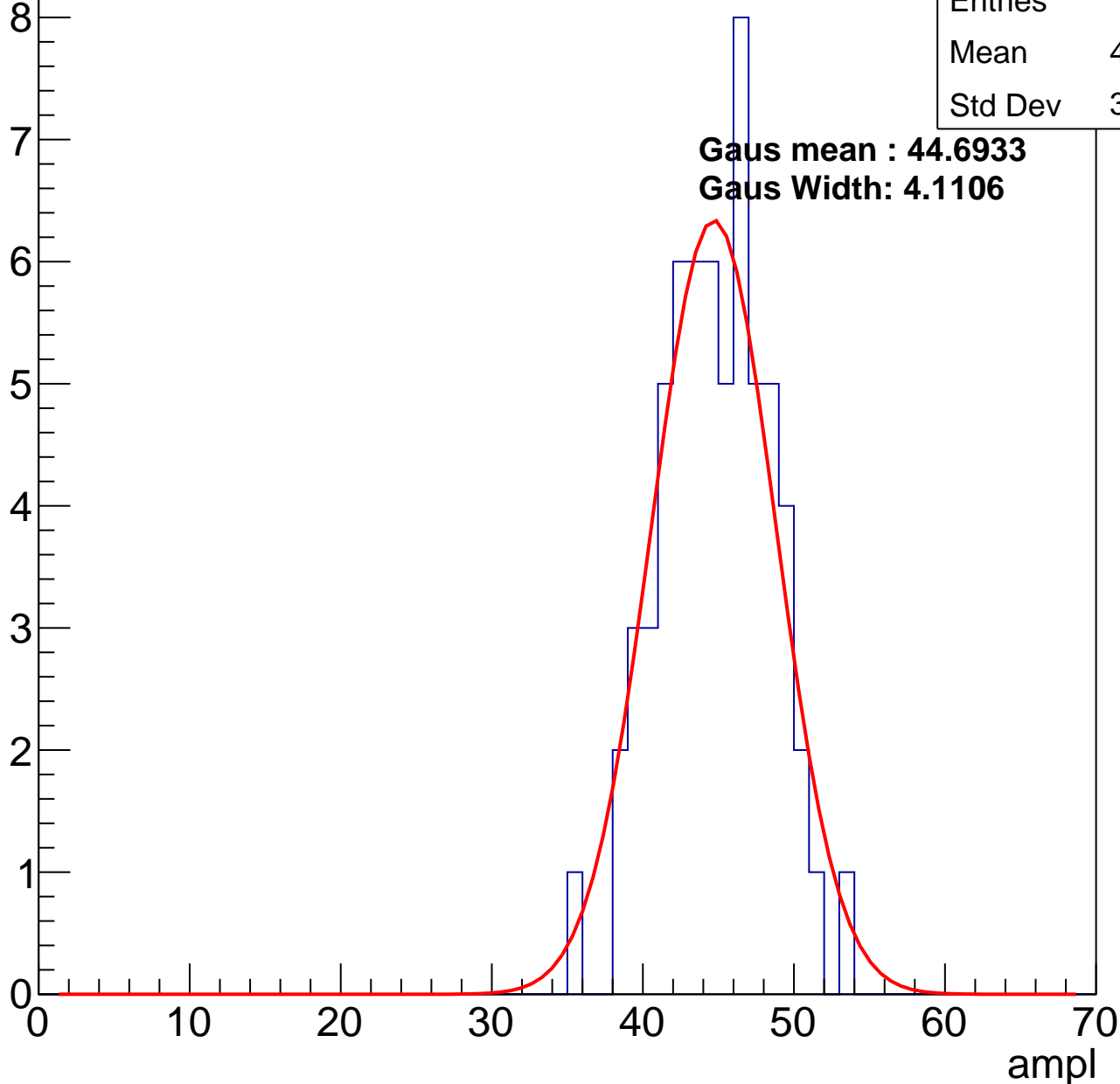
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	44.37
Std Dev	3.578

Gaus mean : 44.6933

Gaus Width: 4.1106

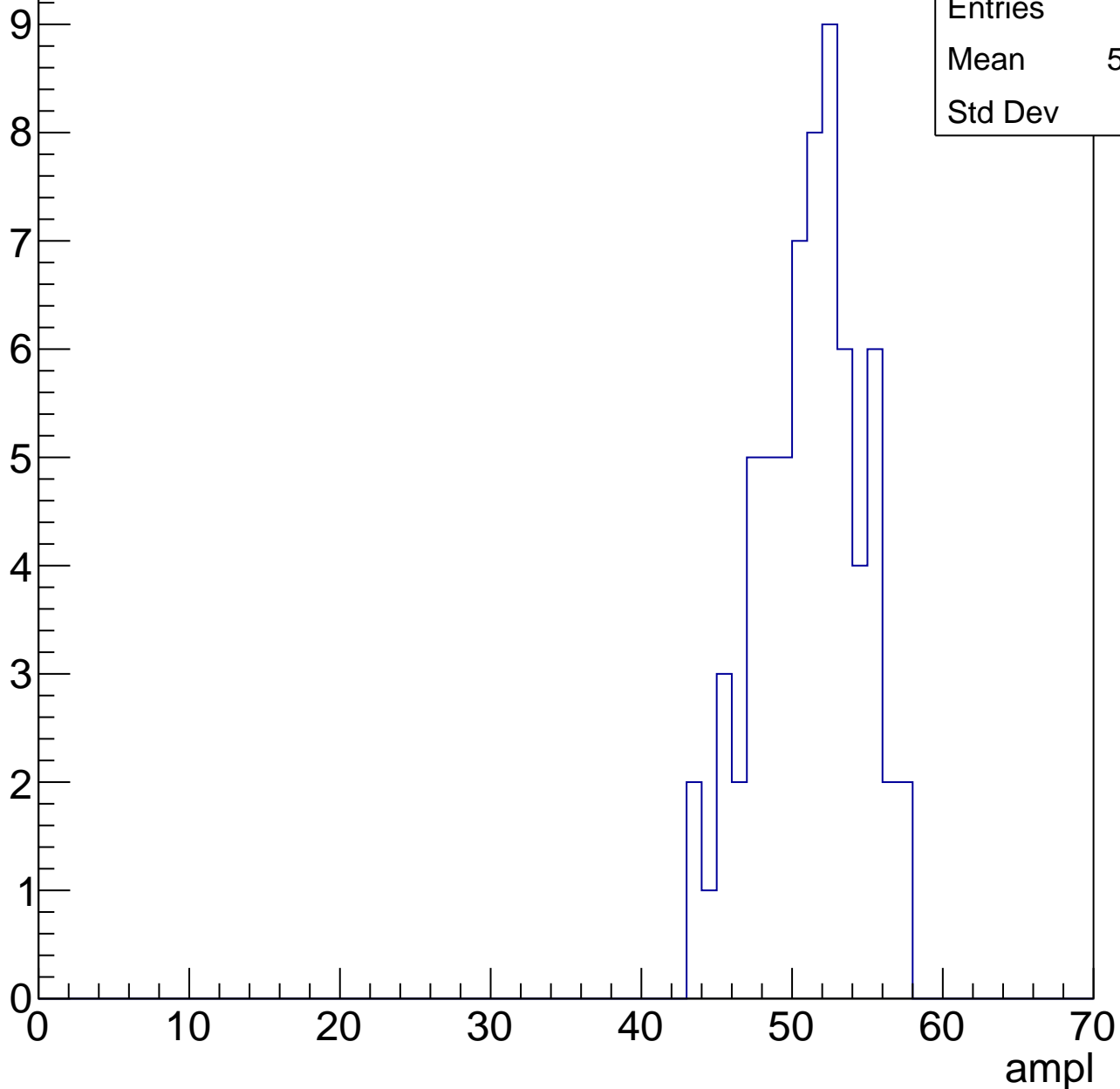


B1L102S, U13-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50.64
Std Dev	3.38

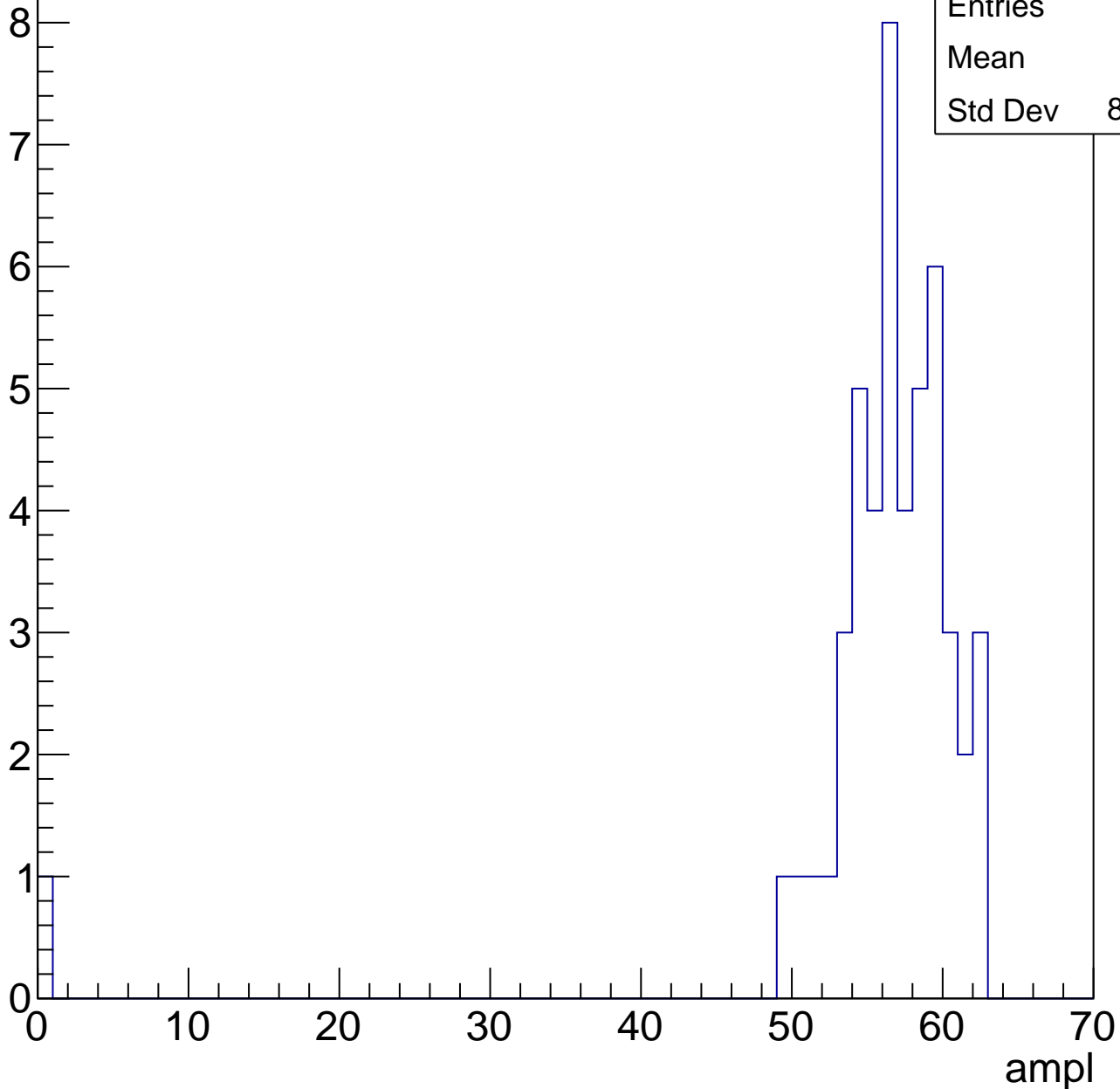


B1L102S, U13-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.4
Std Dev	8.636

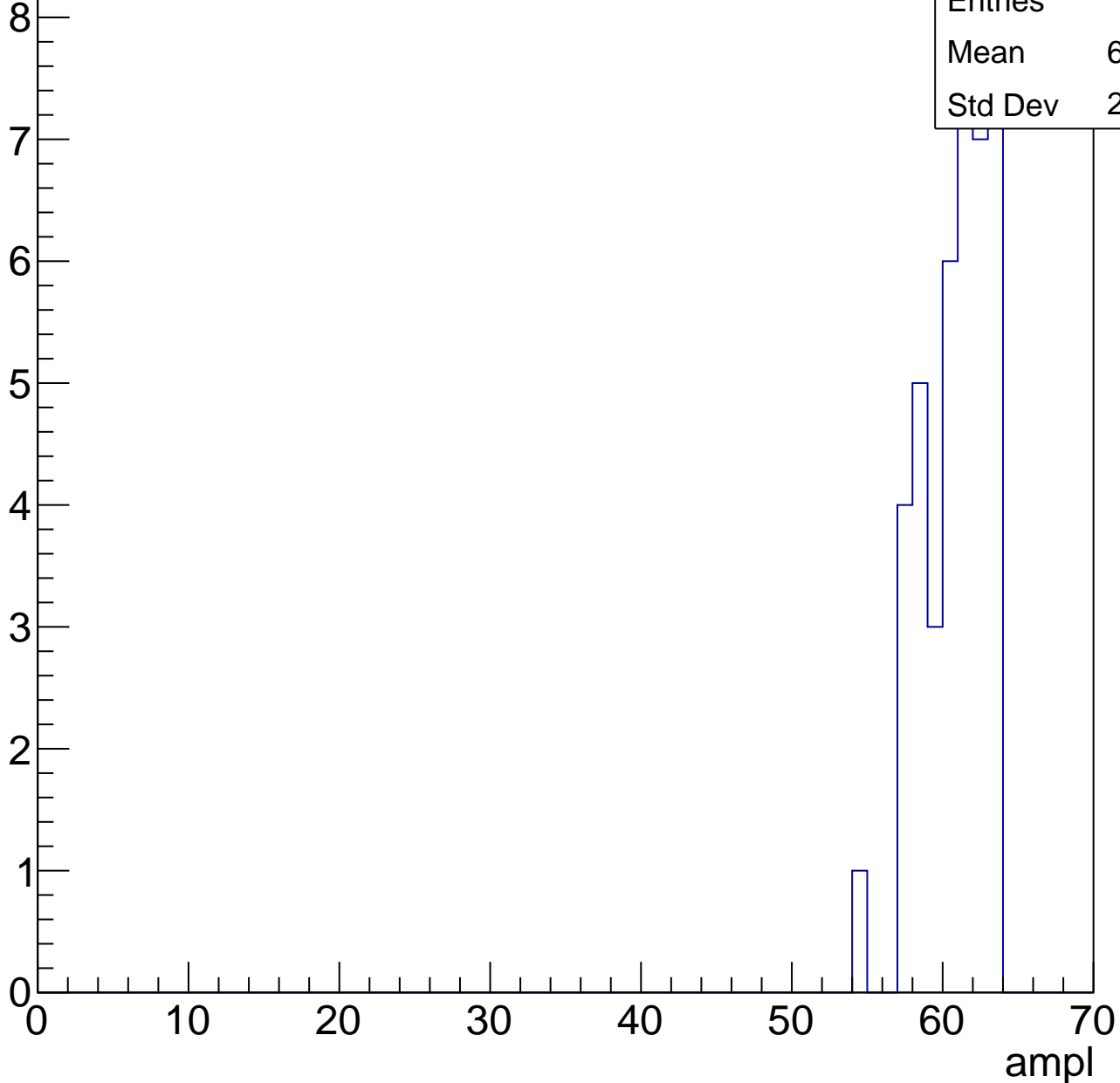


B1L102S, U13-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

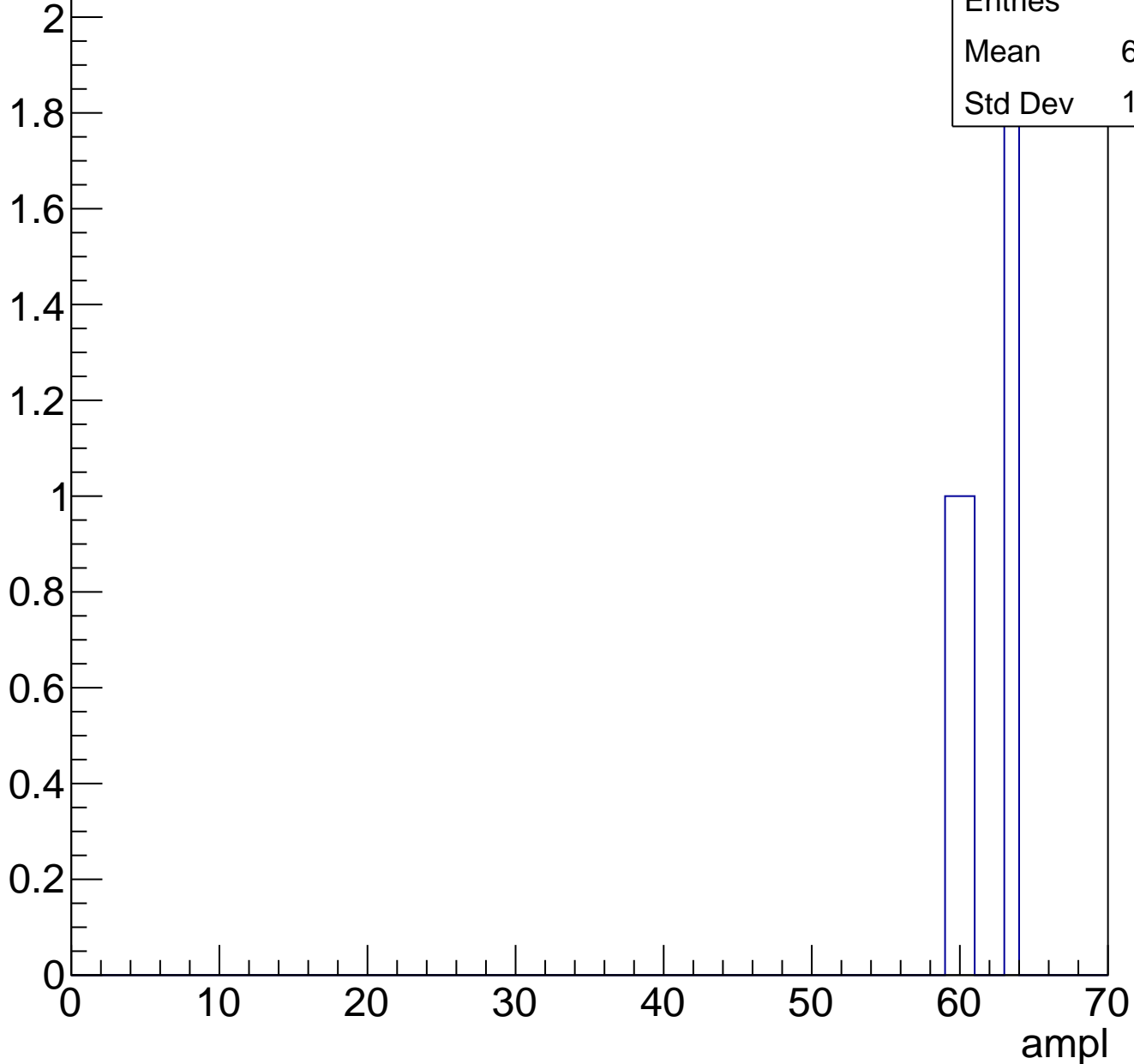
Entries	42
Mean	60.36
Std Dev	2.169



B1L102S, U13-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch102, adc0

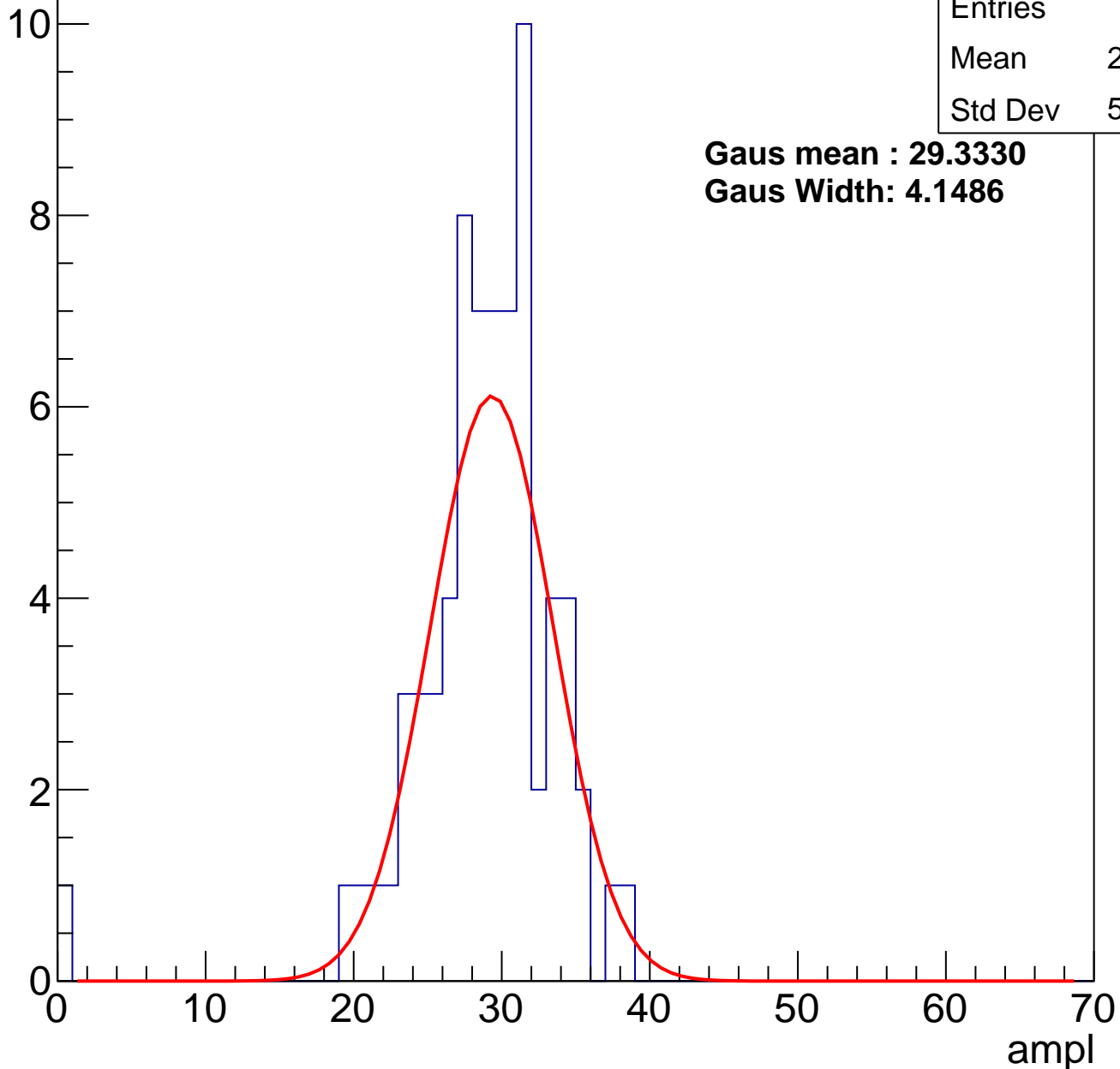
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	28.37
Std Dev	5.116

Gaus mean : 29.3330

Gaus Width: 4.1486

Entry



B1L102S, U13-ch102, adc1

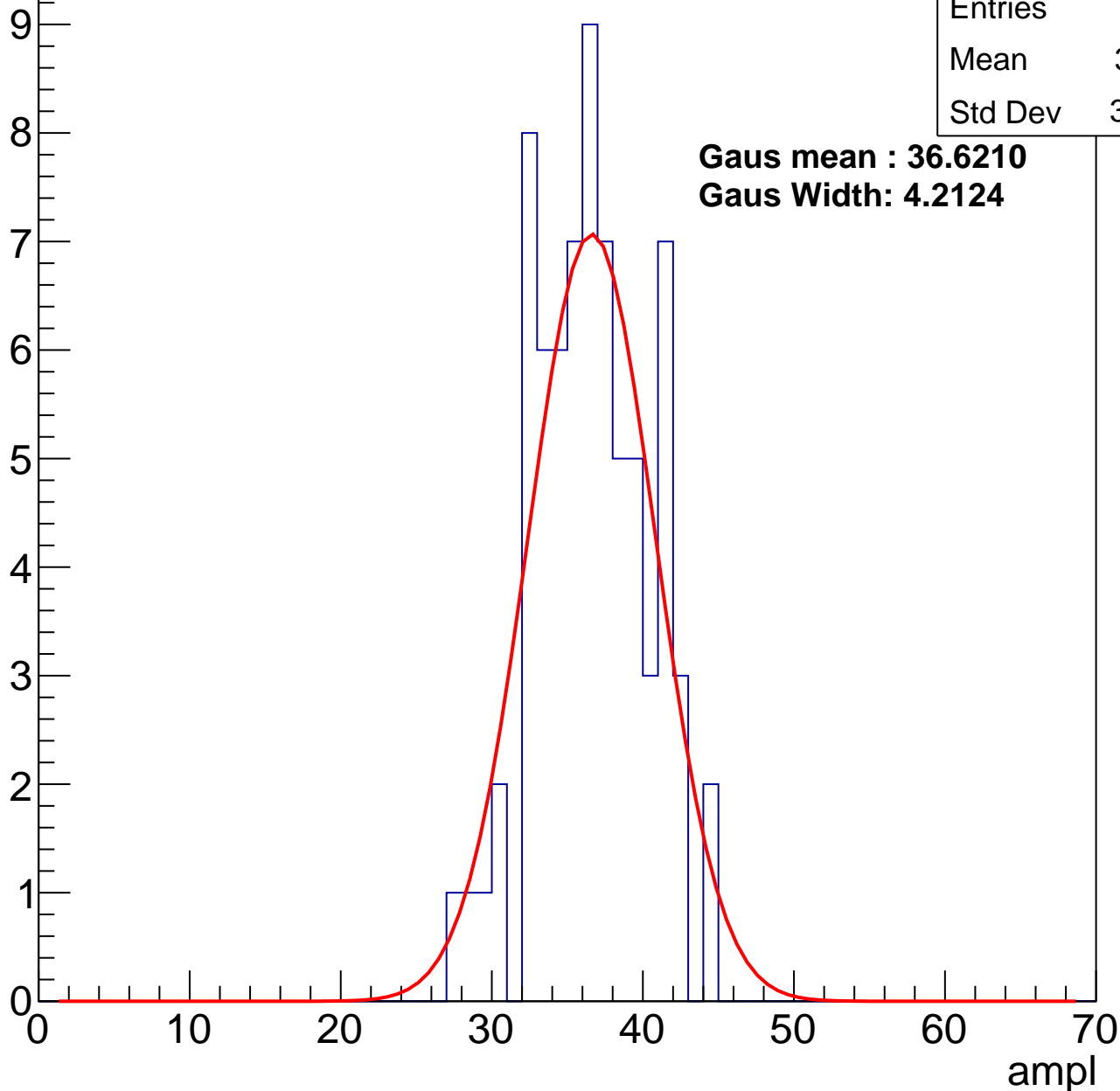
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.11
Std Dev	3.718

Gaus mean : 36.6210

Gaus Width: 4.2124



B1L102S, U13-ch102, adc2

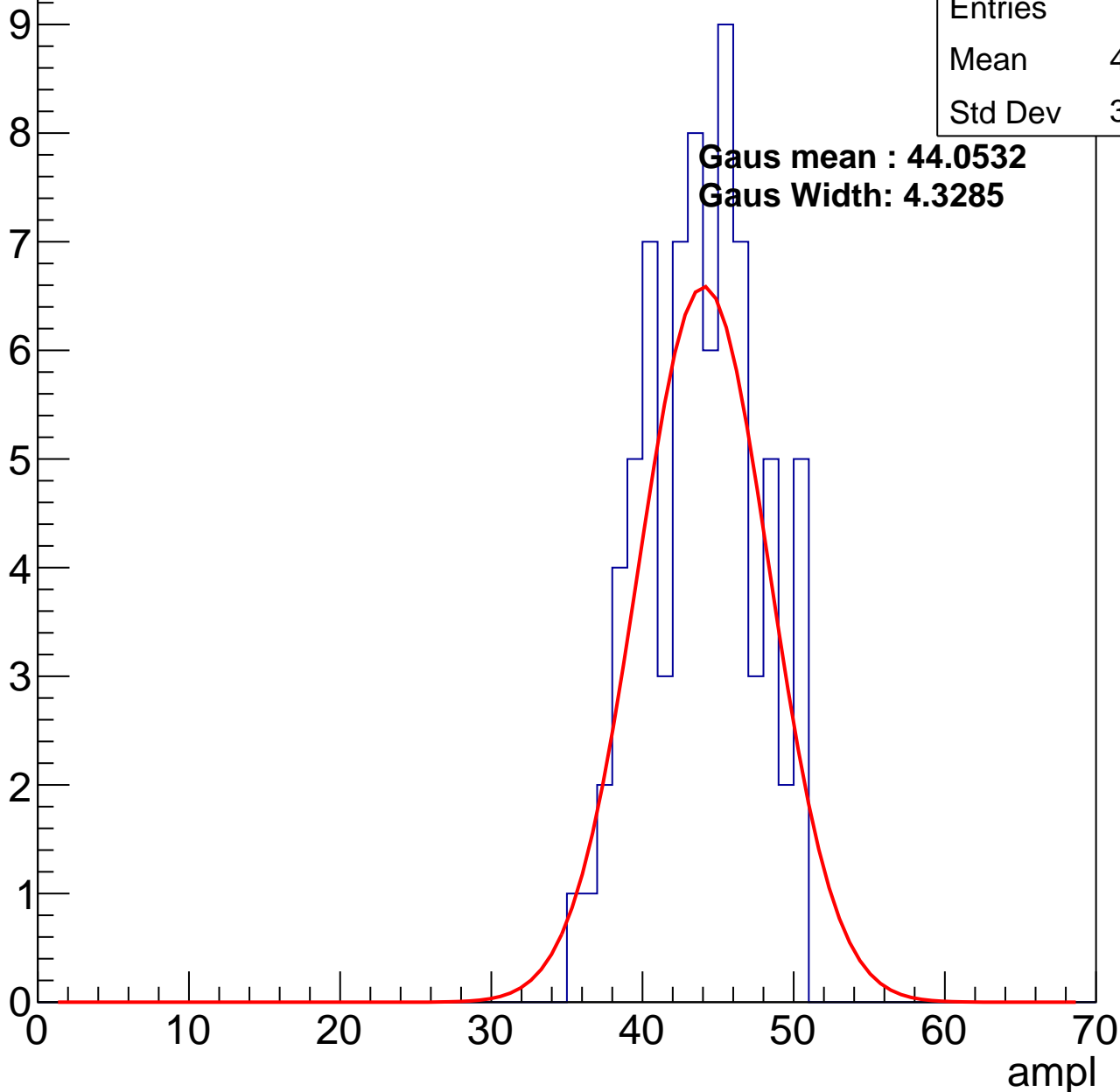
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.37
Std Dev	3.709

Gaus mean : 44.0532

Gaus Width: 4.3285

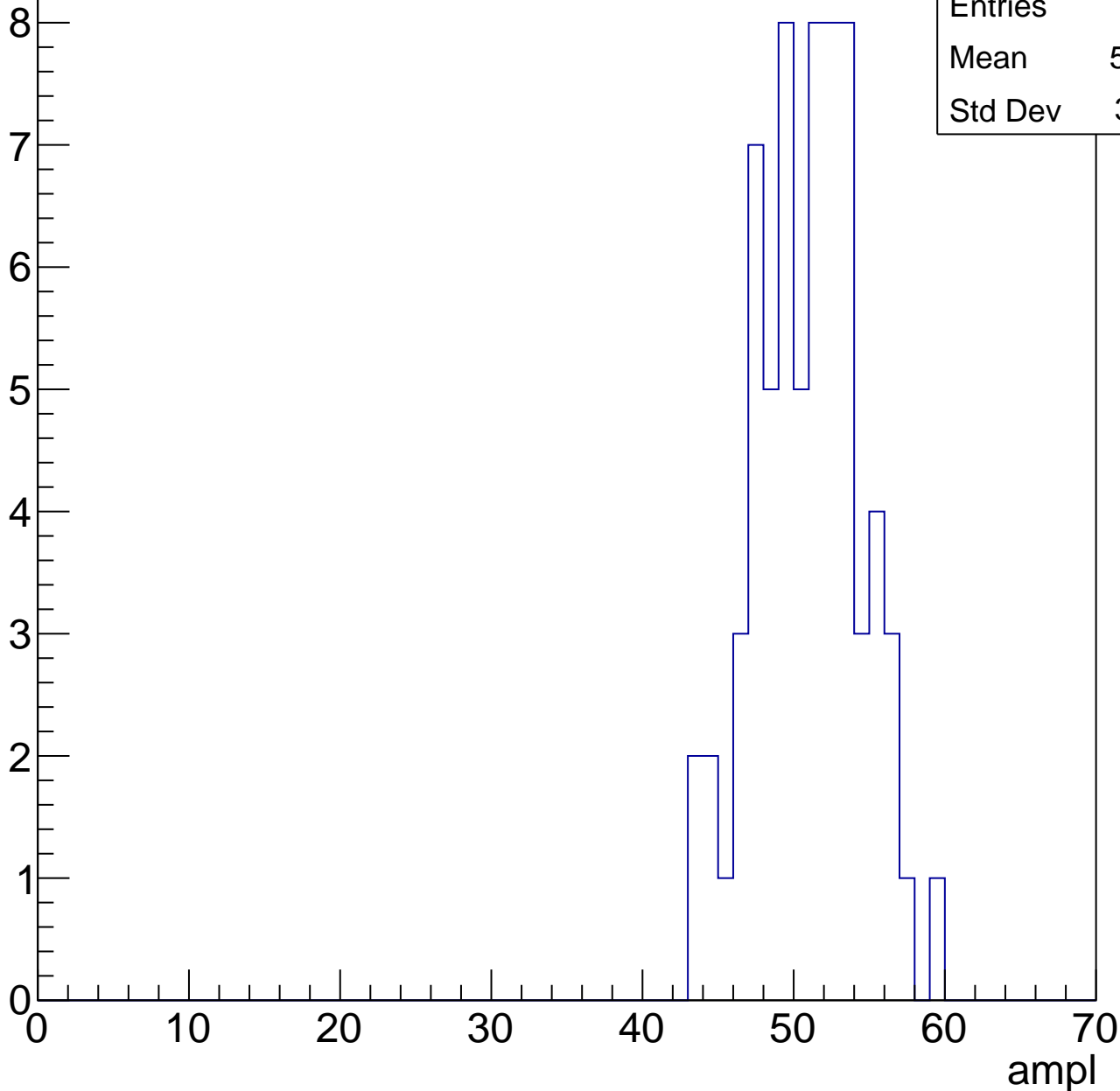


B1L102S, U13-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	50.46
Std Dev	3.441

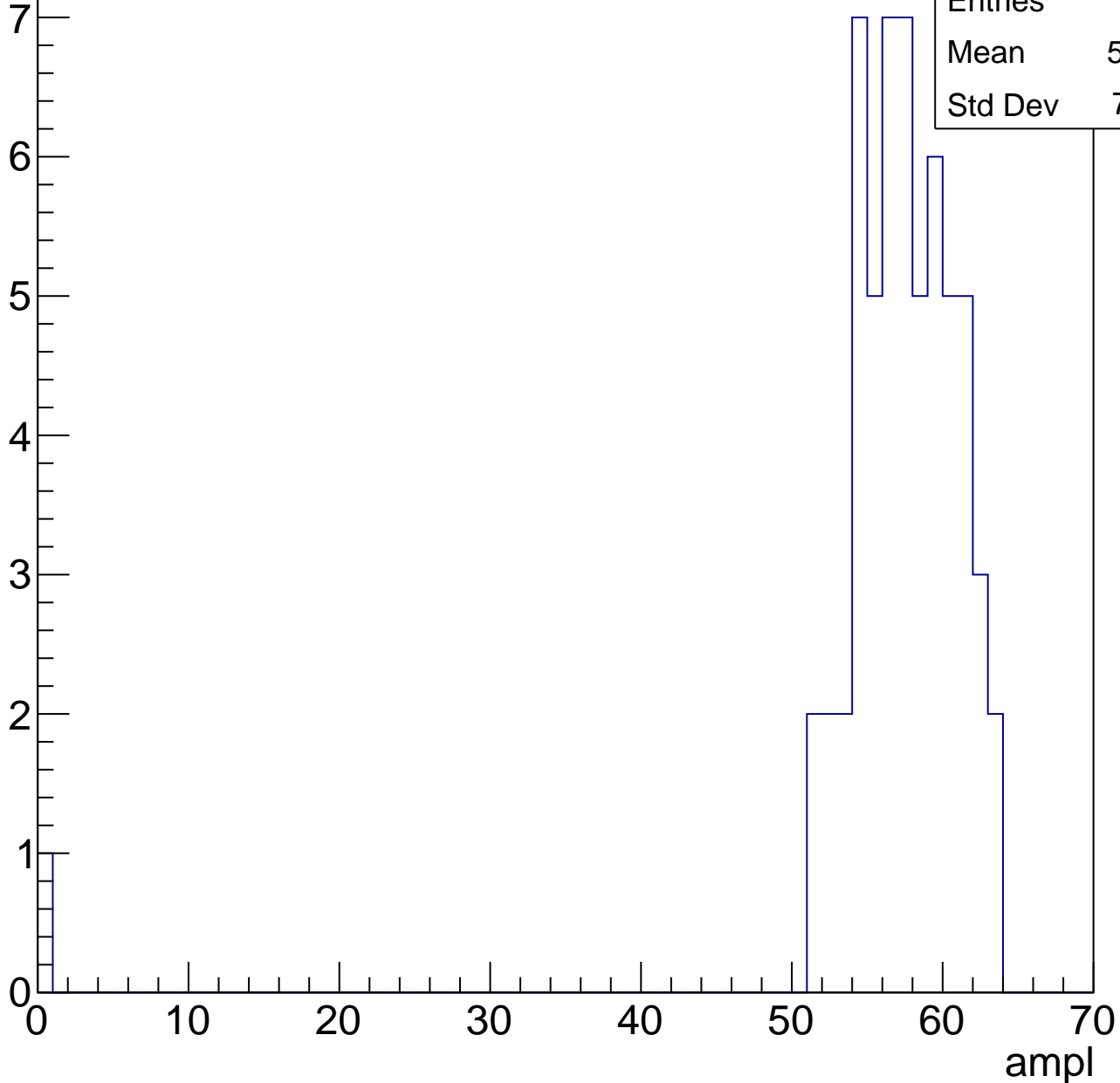


B1L102S, U13-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.22
Std Dev	7.981

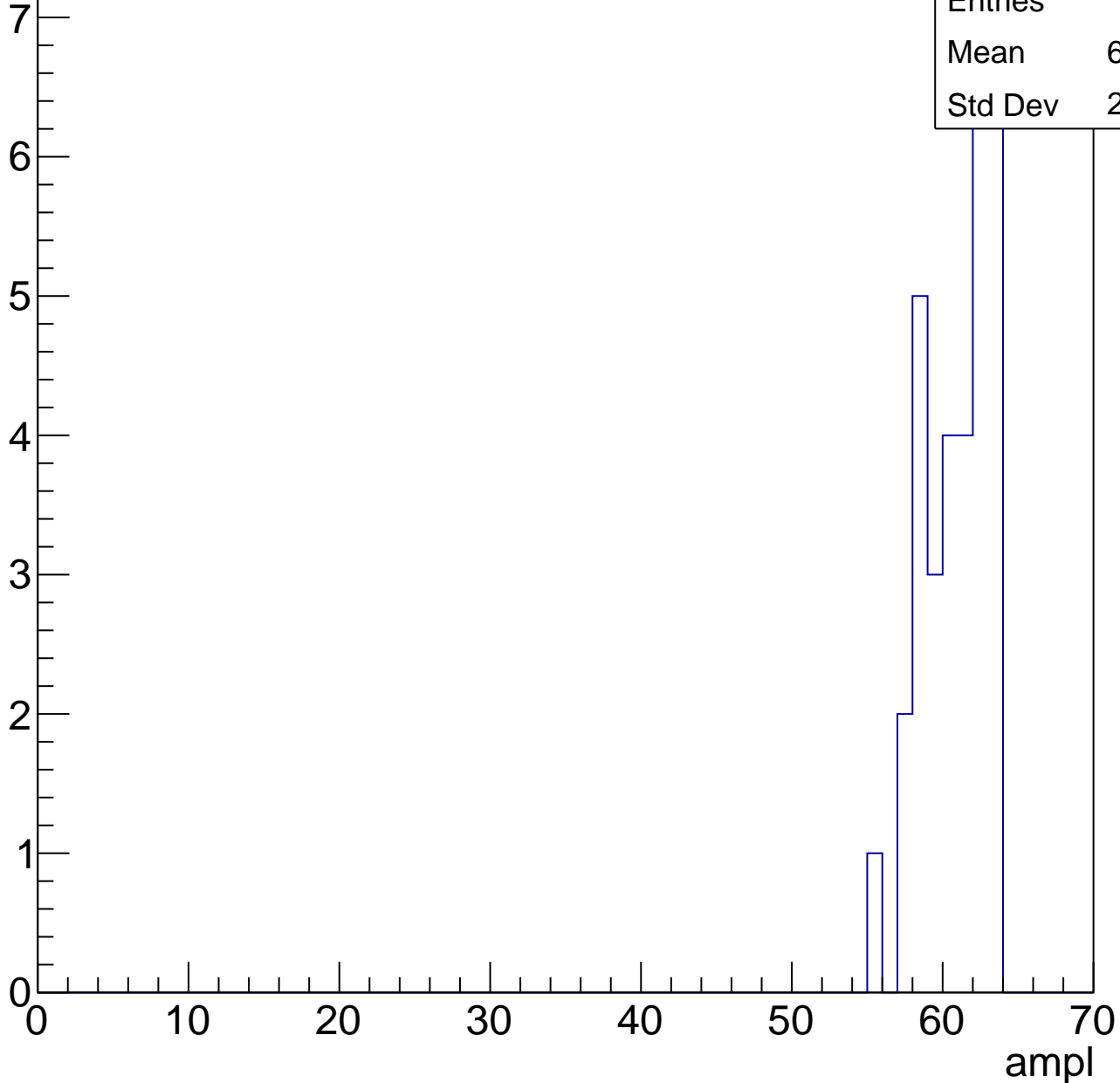


B1L102S, U13-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	60.45
Std Dev	2.162



B1L102S, U13-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

61.33

Std Dev

1.247

B1L102S, U13-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch103, adc0

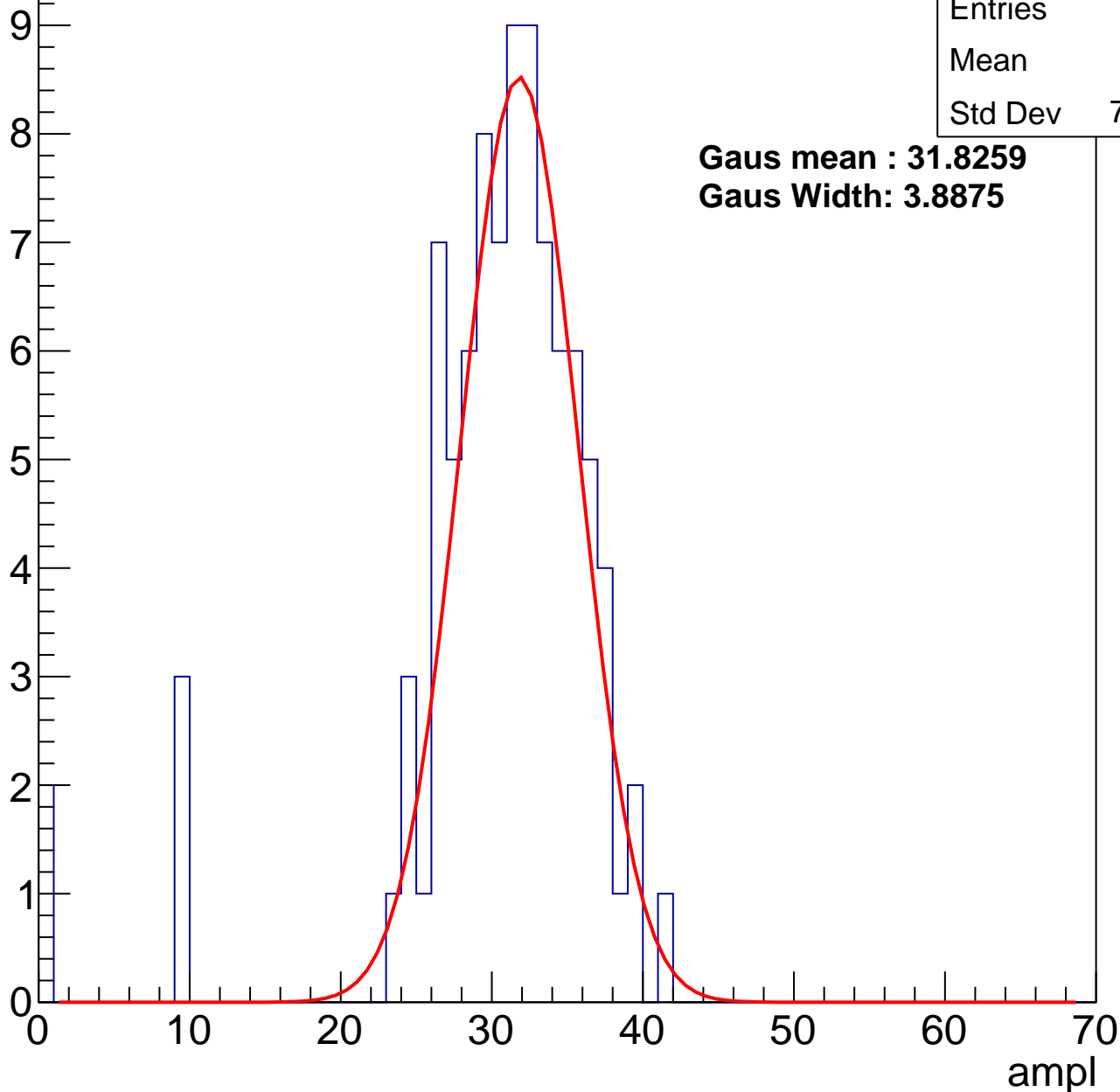
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	29.8
Std Dev	7.008

Gaus mean : 31.8259

Gaus Width: 3.8875



B1L102S, U13-ch103, adc1

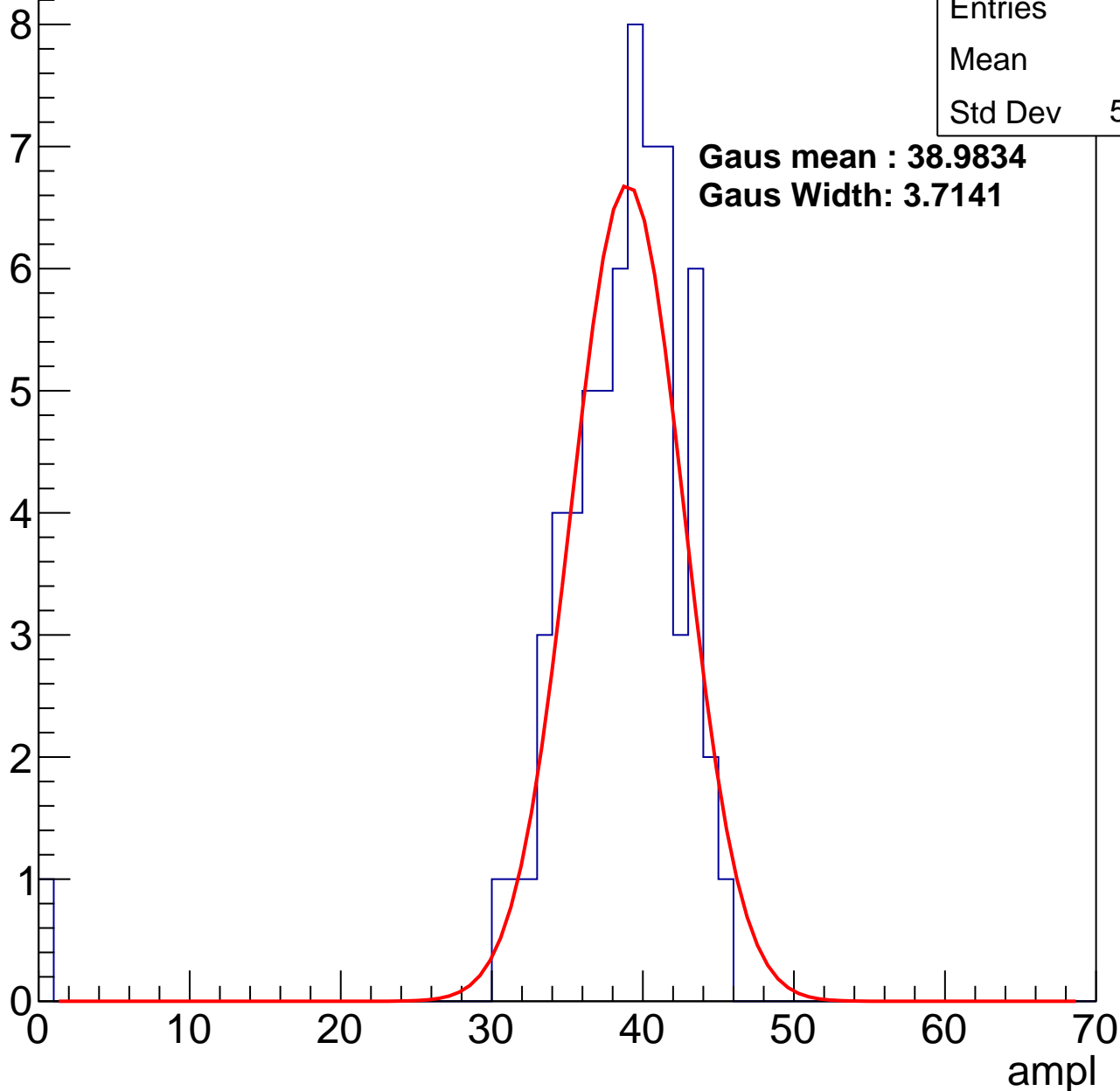
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	37.8
Std Dev	5.829

Gaus mean : 38.9834

Gaus Width: 3.7141



B1L102S, U13-ch103, adc2

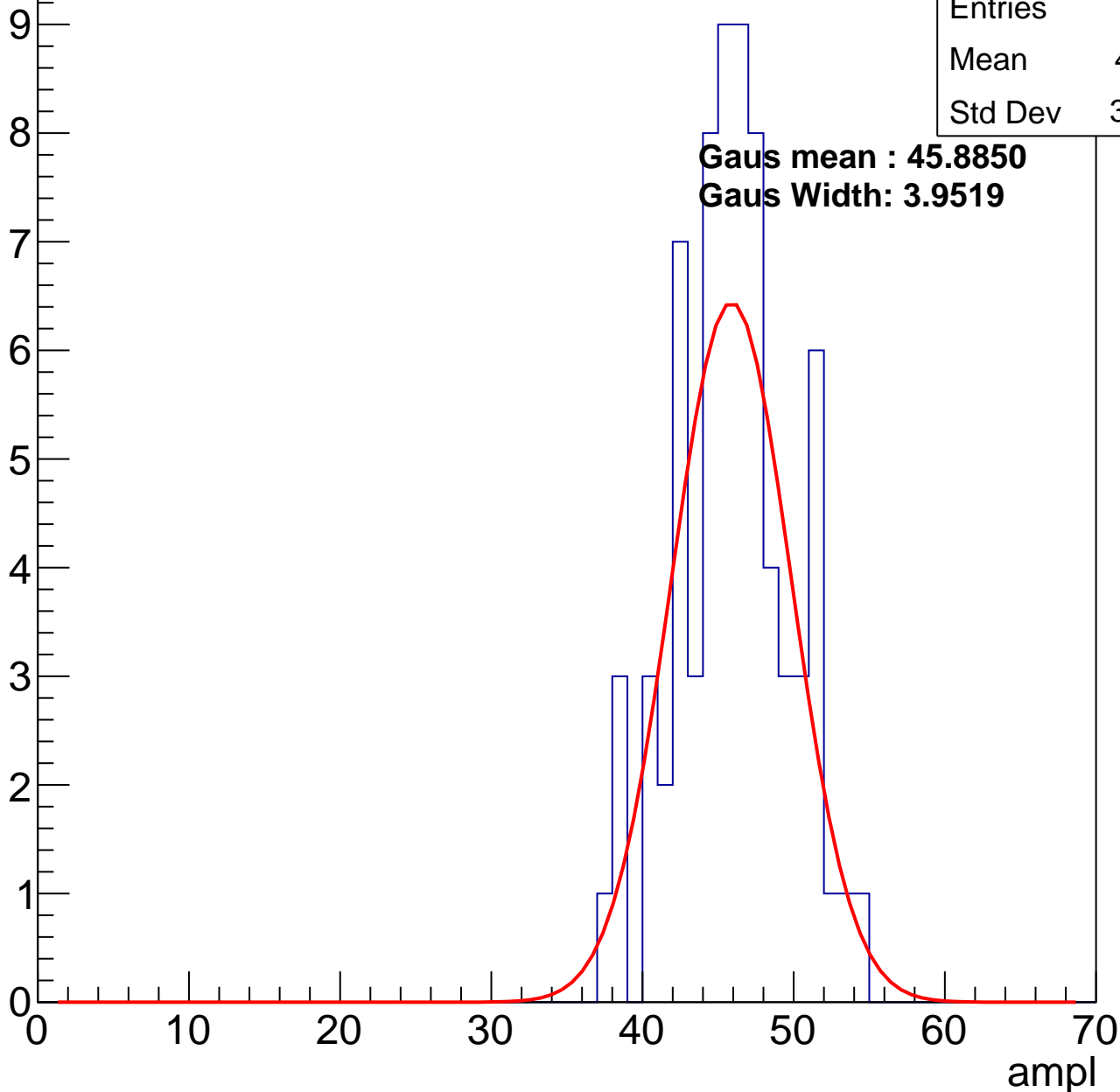
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	45.51
Std Dev	3.712

Gaus mean : 45.8850

Gaus Width: 3.9519

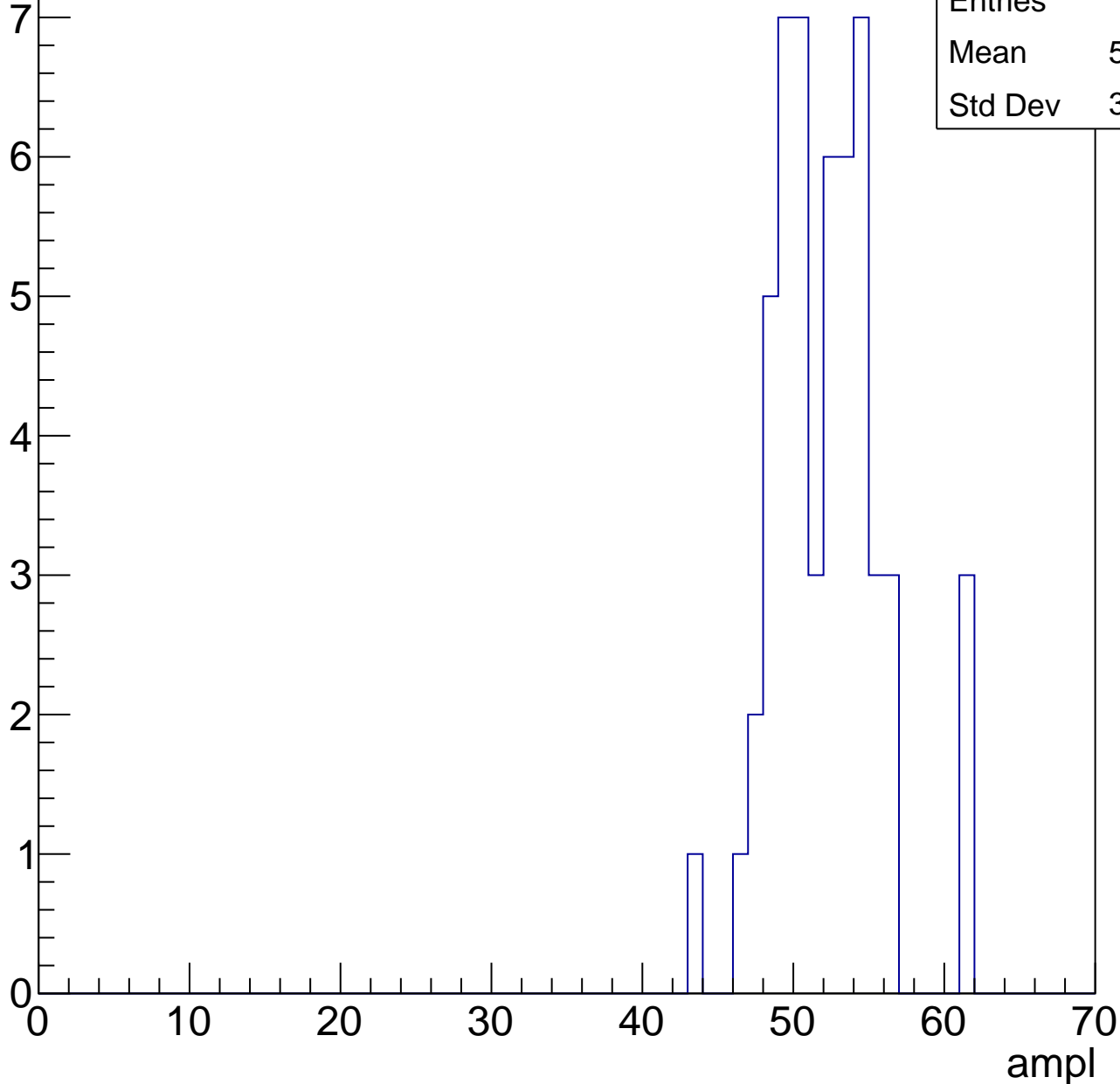


B1L102S, U13-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	51.72
Std Dev	3.572



B1L102S, U13-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

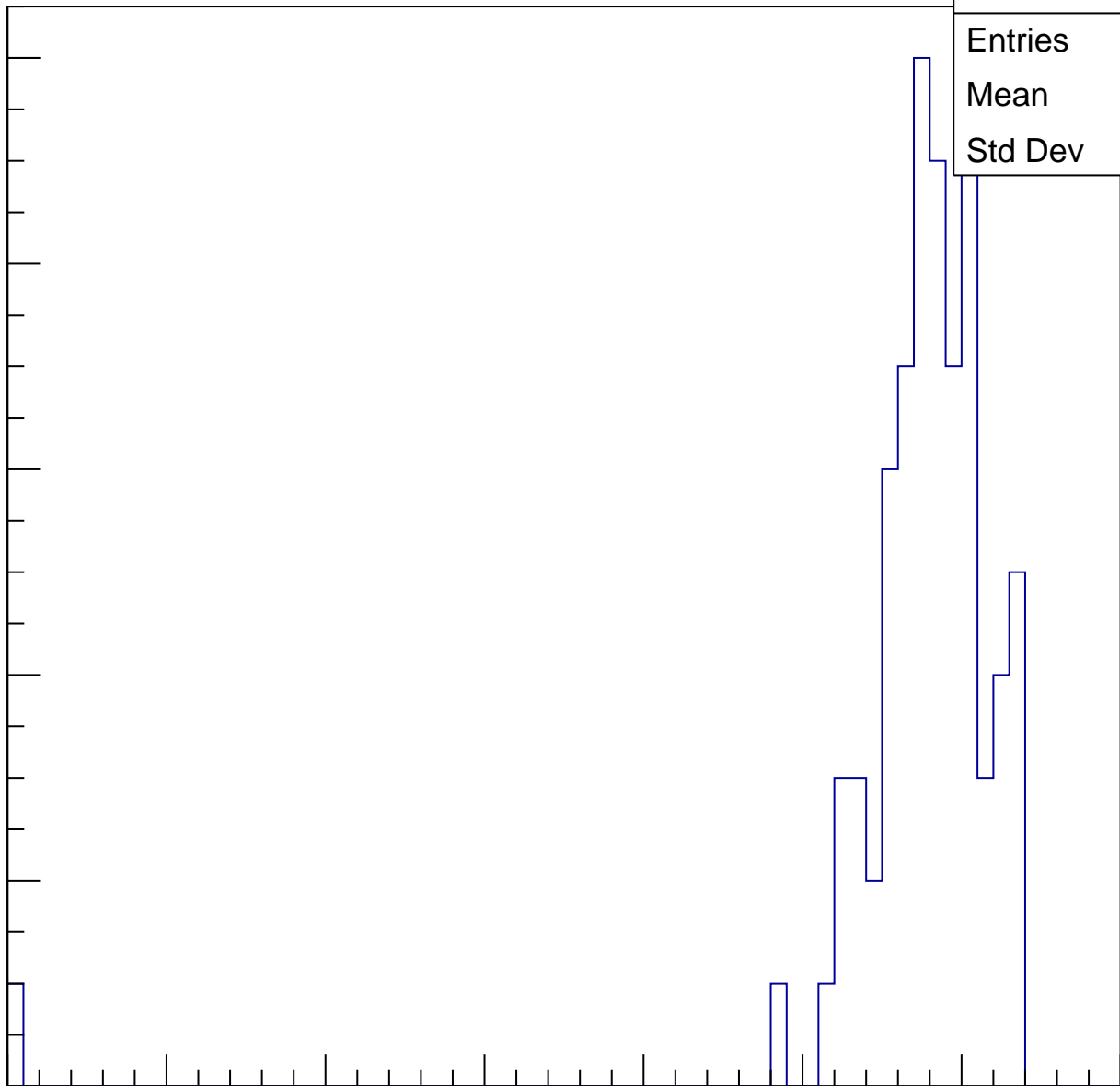
Entries	71
Mean	56.83
Std Dev	7.489

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

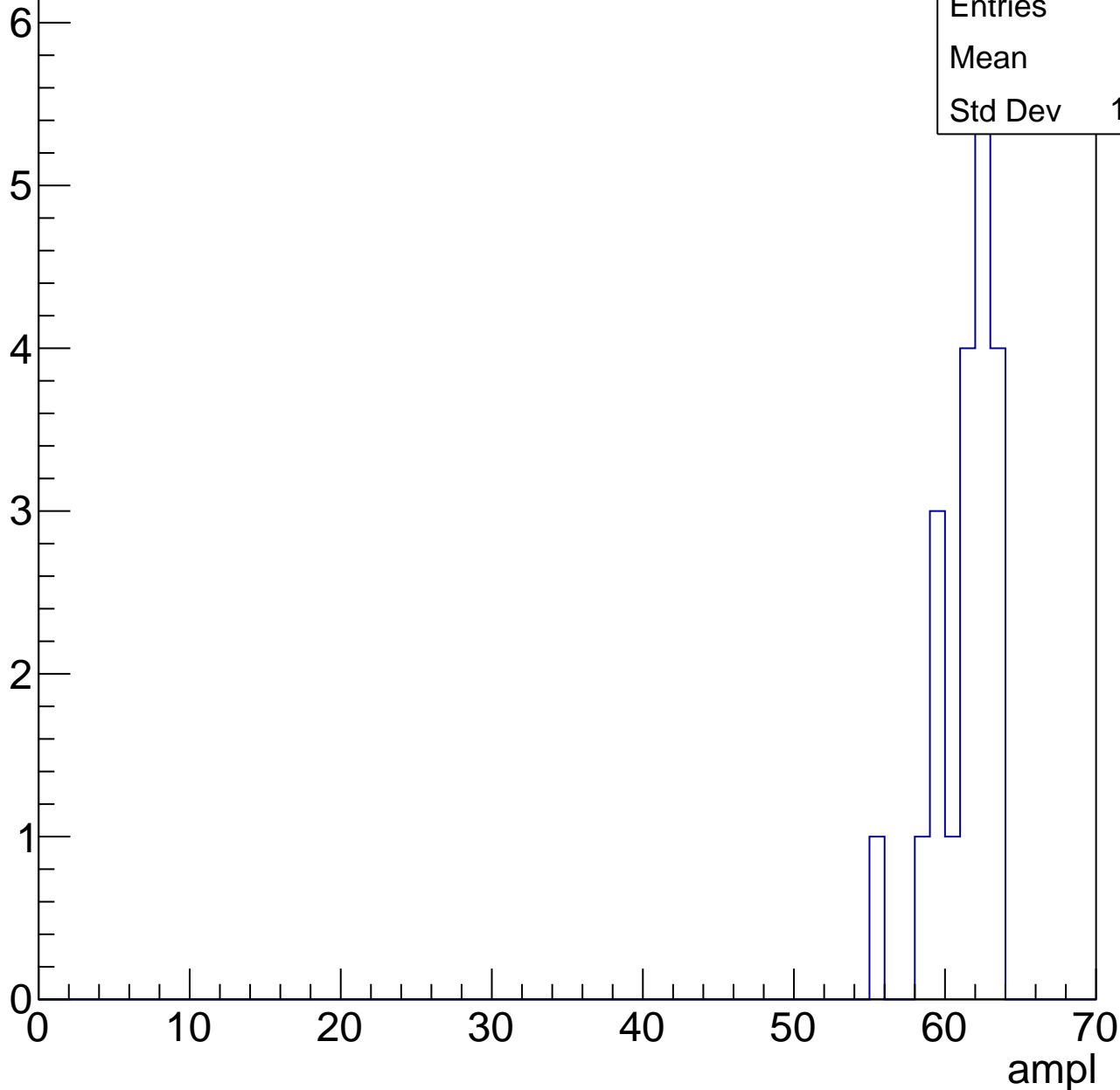


B1L102S, U13-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	60.9
Std Dev	1.997



B1L102S, U13-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L102S, U13-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch104, adc0

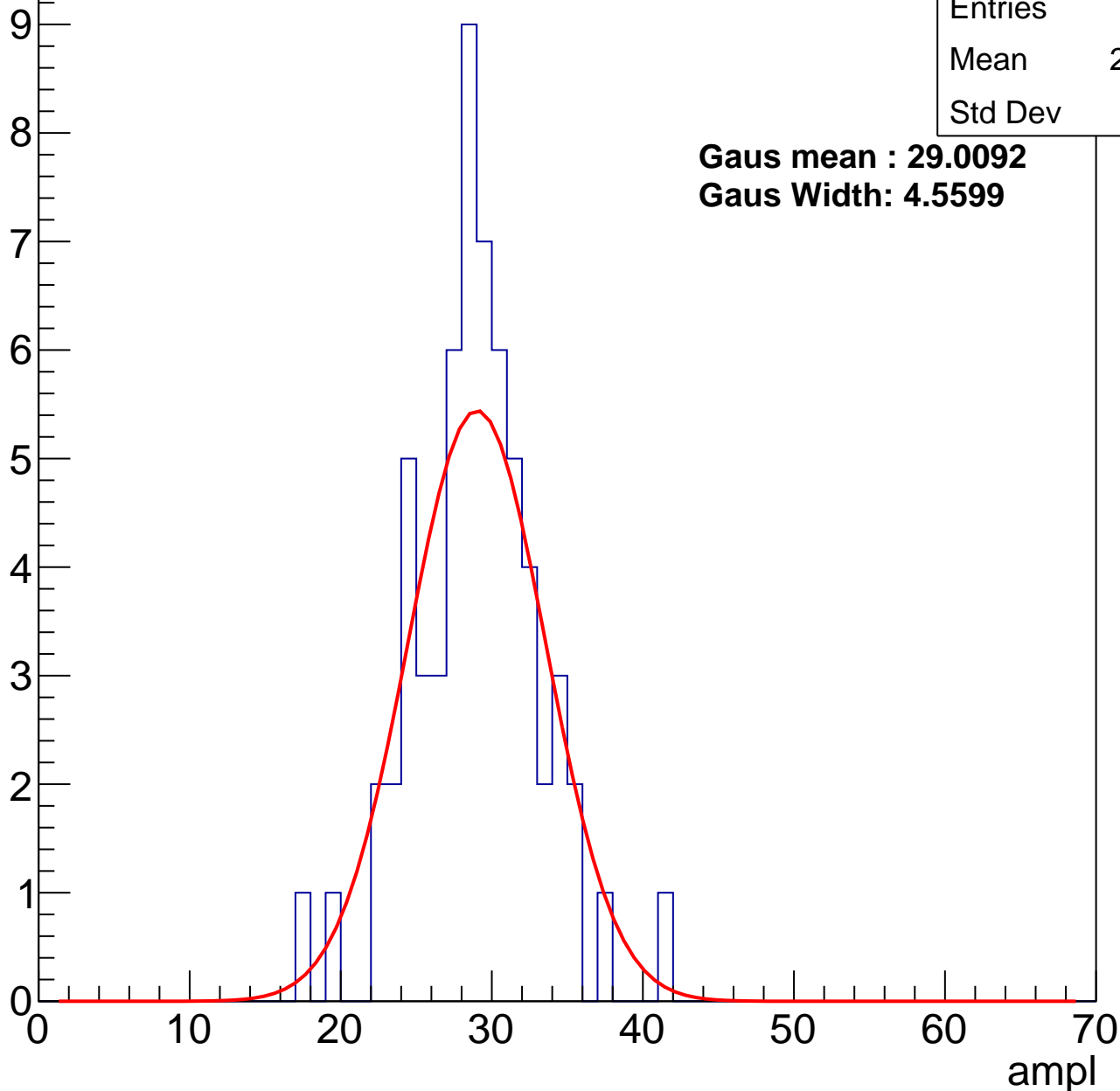
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.49
Std Dev	4.14

Gaus mean : 29.0092

Gaus Width: 4.5599



B1L102S, U13-ch104, adc1

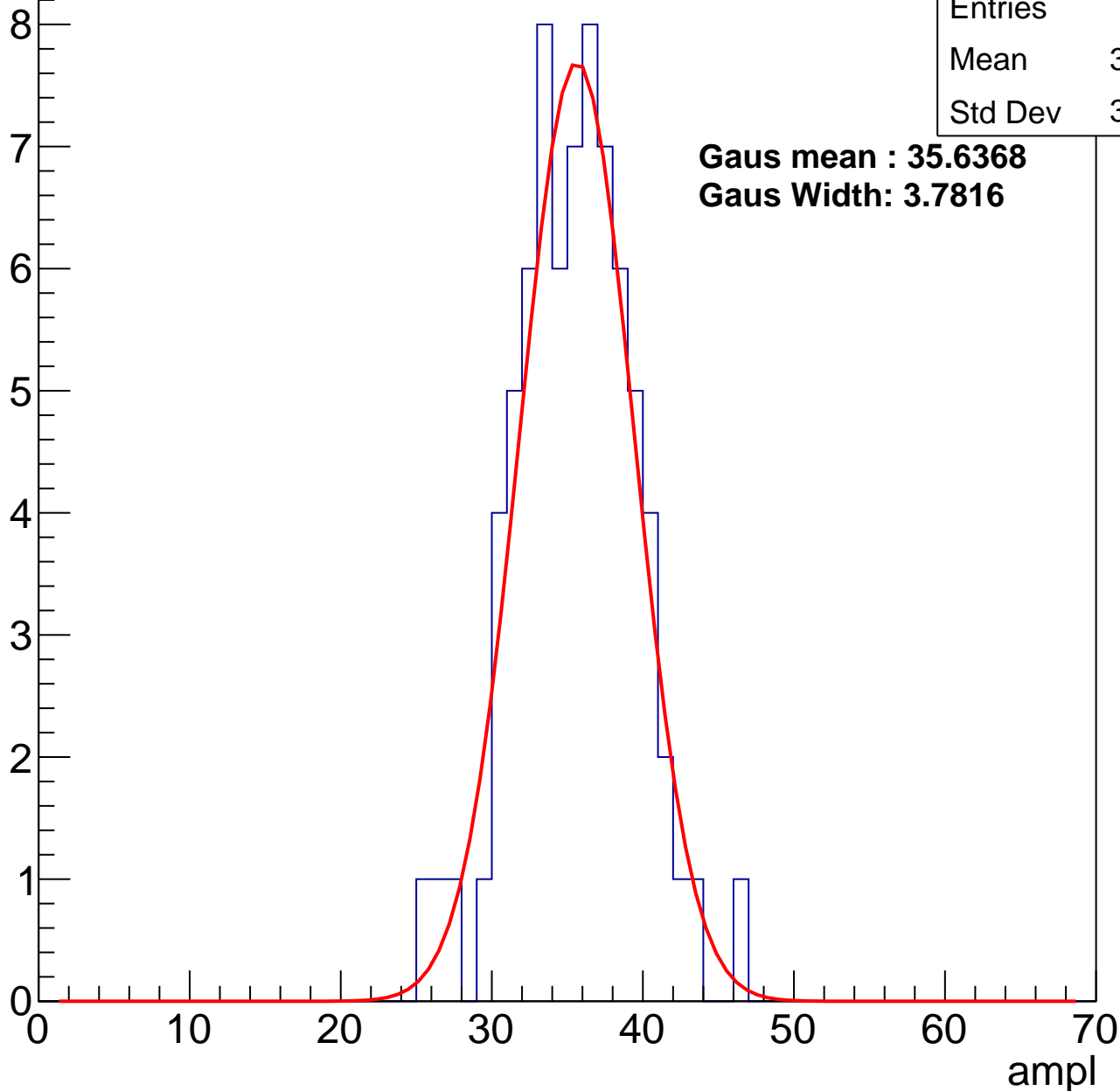
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.07
Std Dev	3.879

Gaus mean : 35.6368

Gaus Width: 3.7816



B1L102S, U13-ch104, adc2

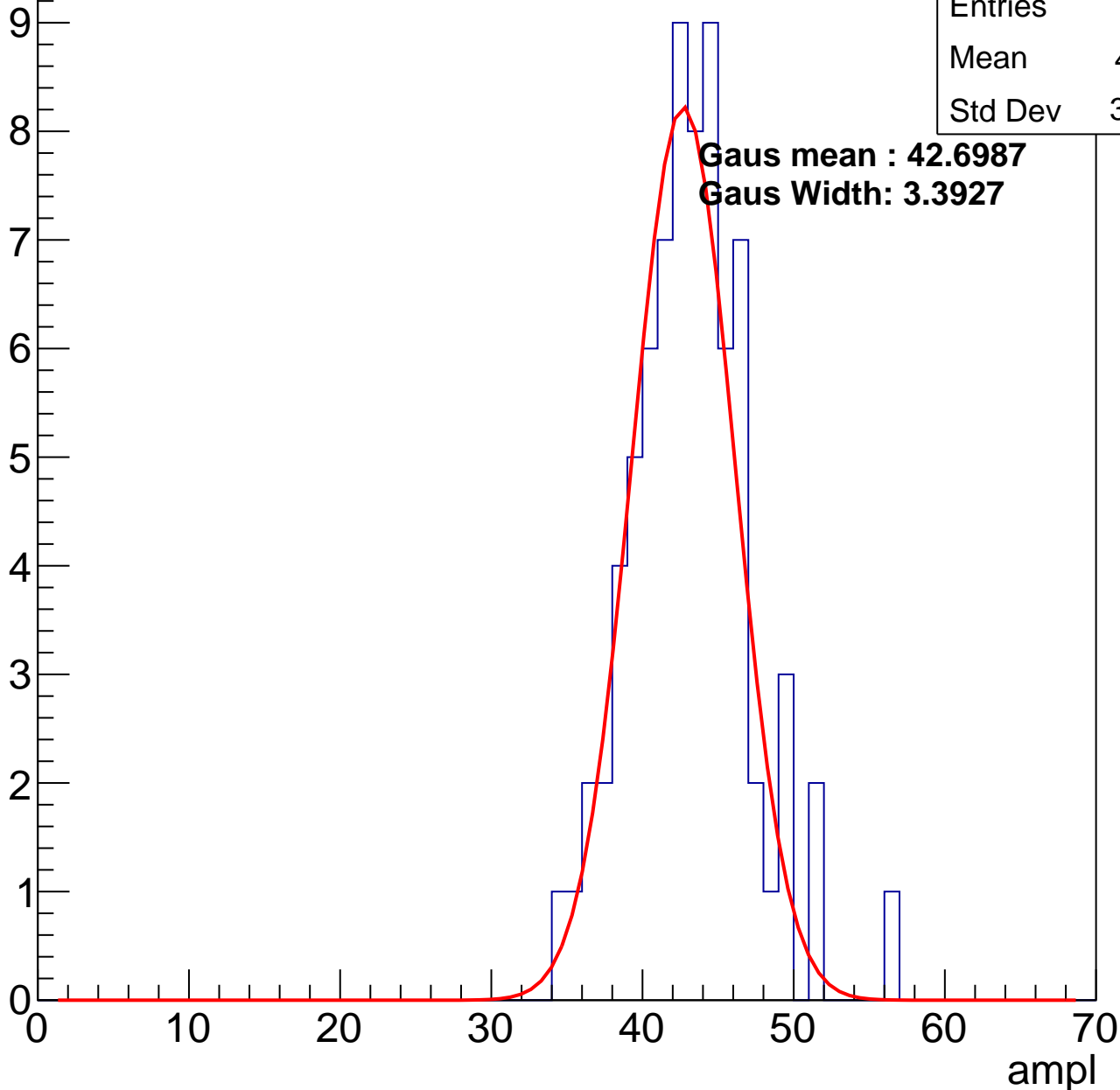
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.71
Std Dev	3.869

Gaus mean : 42.6987

Gaus Width: 3.3927

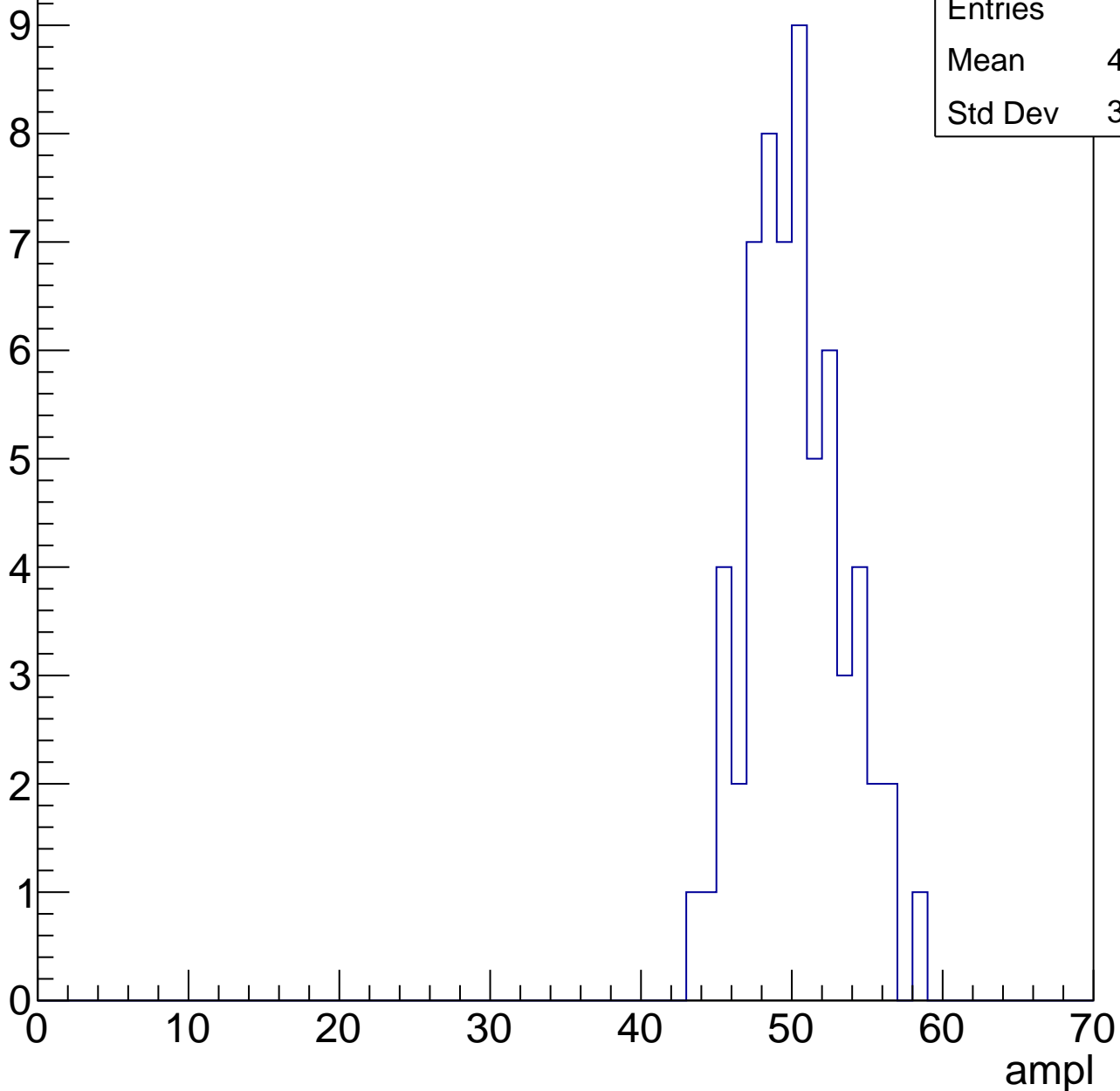


B1L102S, U13-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.79
Std Dev	3.168

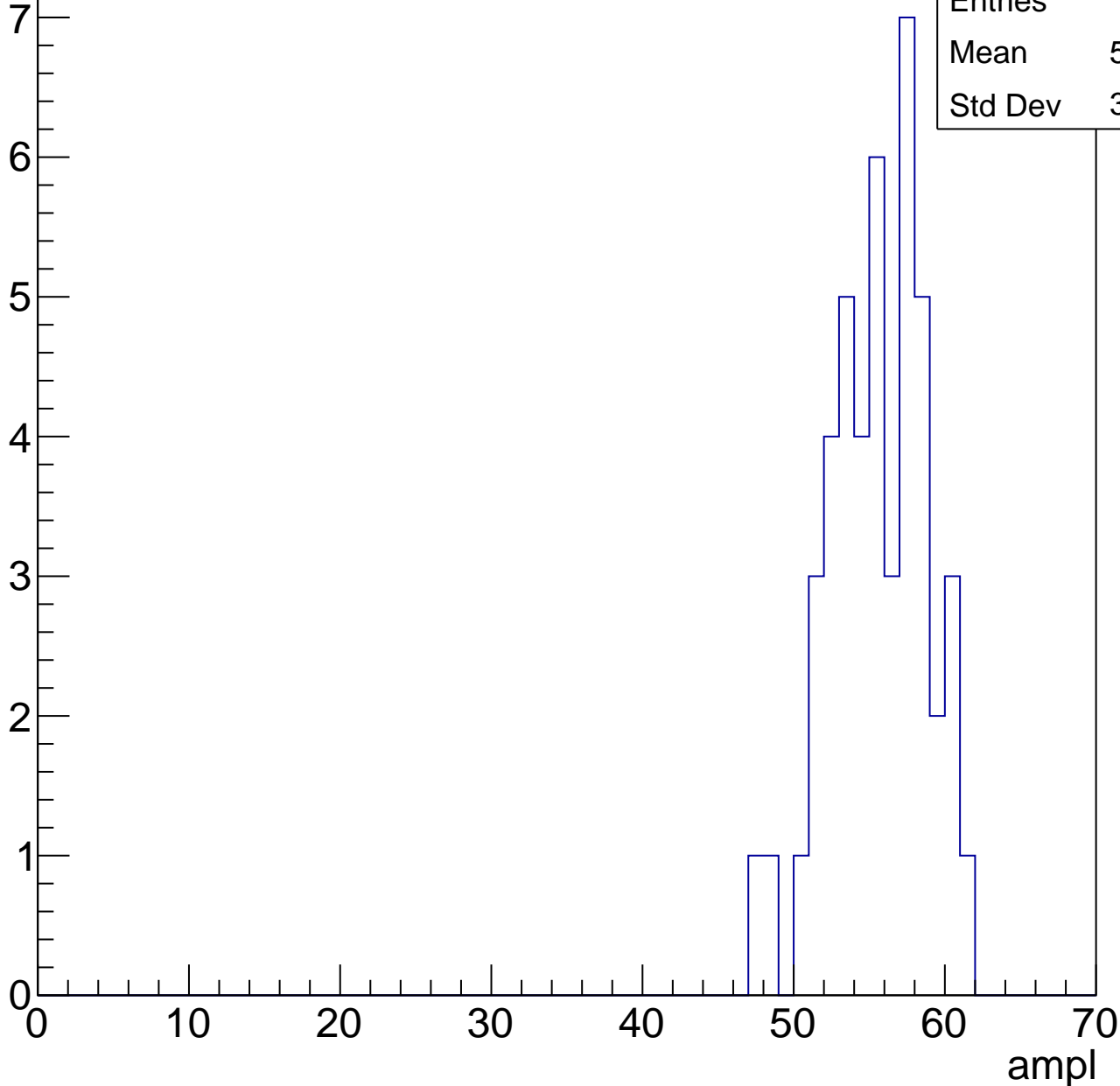


B1L102S, U13-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

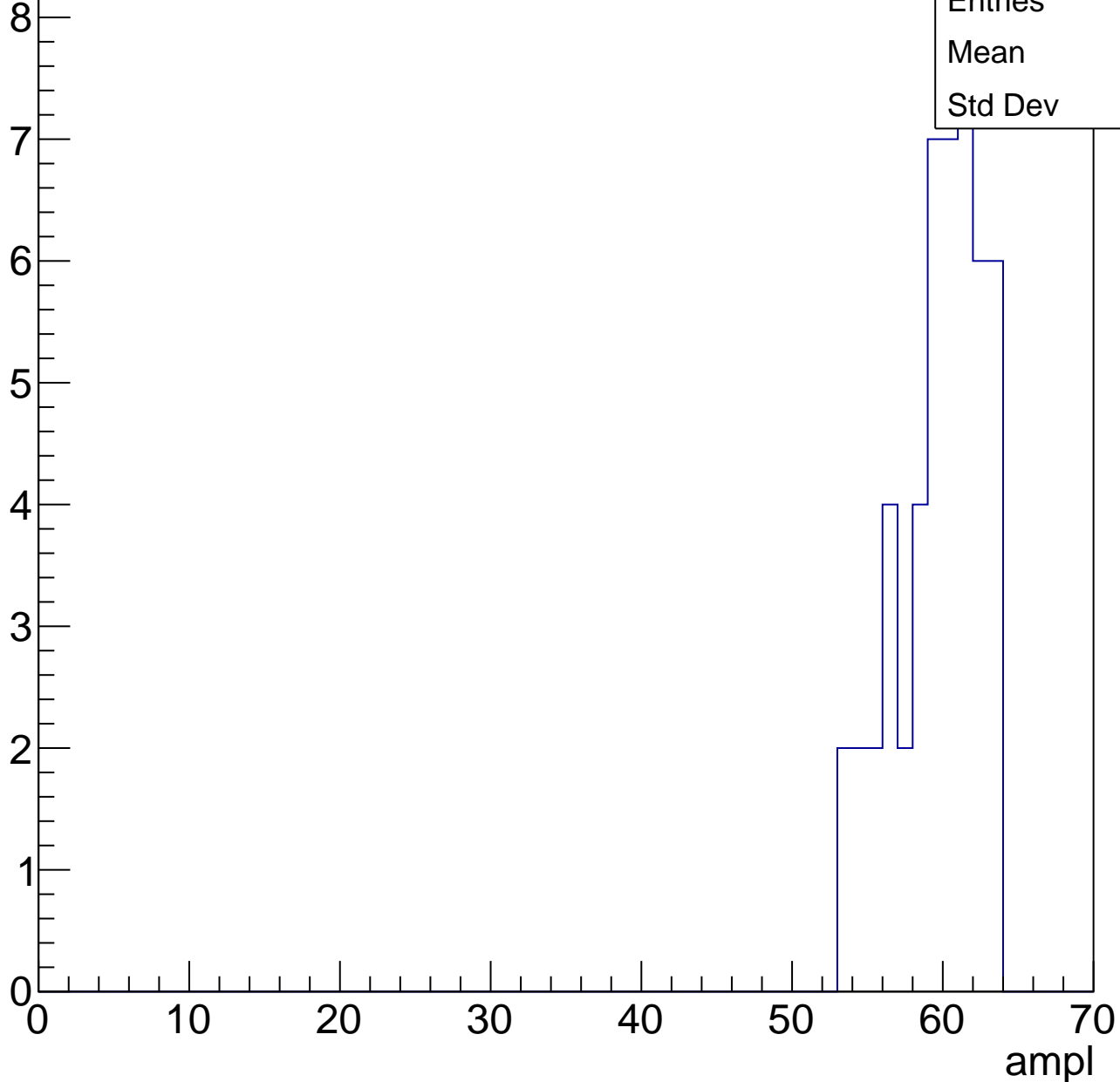
Entries	46
Mean	55.07
Std Dev	3.179



B1L102S, U13-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

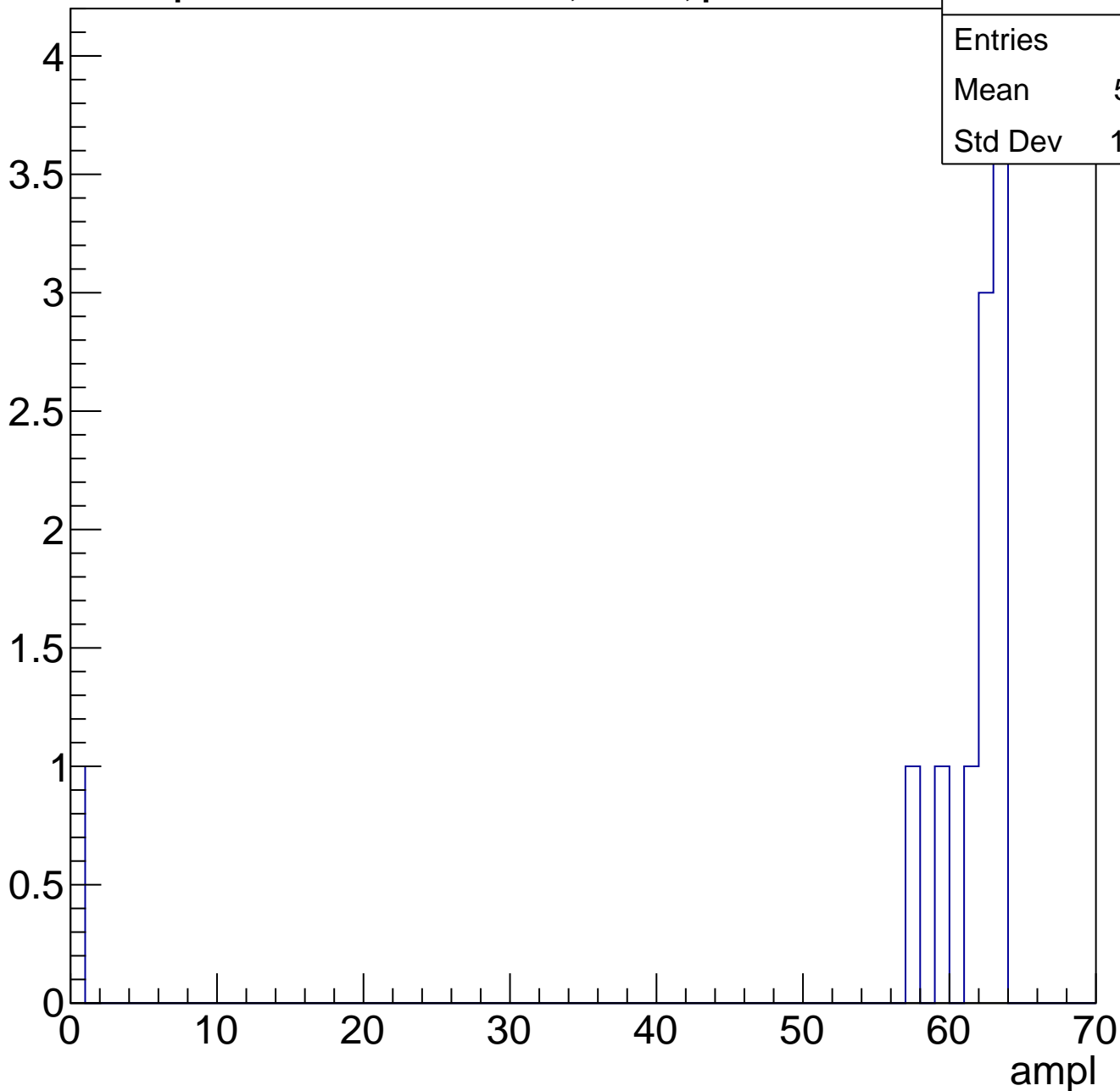
Entry



B1L102S, U13-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch105, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	29.37
Std Dev	3.706

Gaus mean : 30.3162

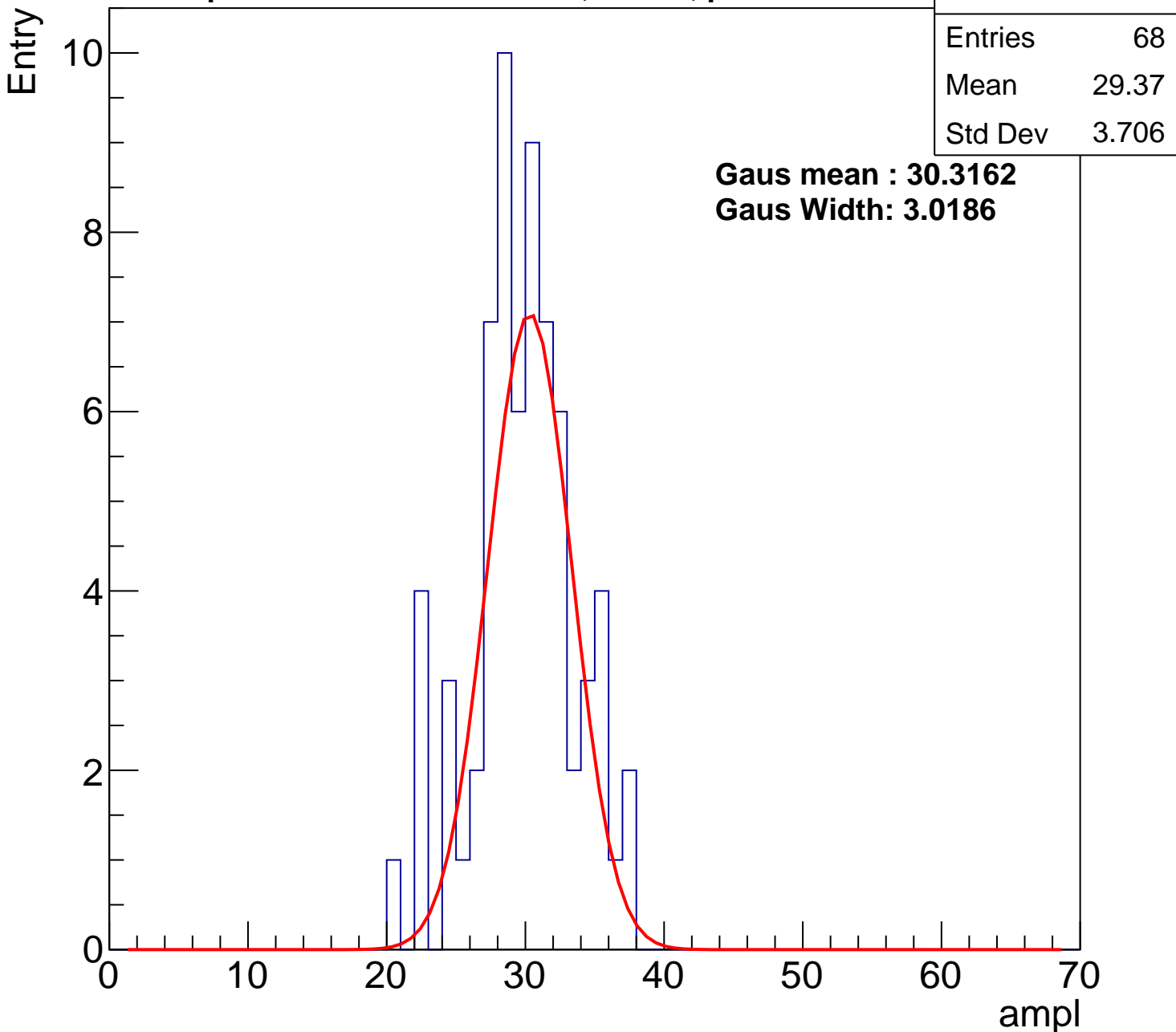
Gaus Width: 3.0186

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch105, adc1

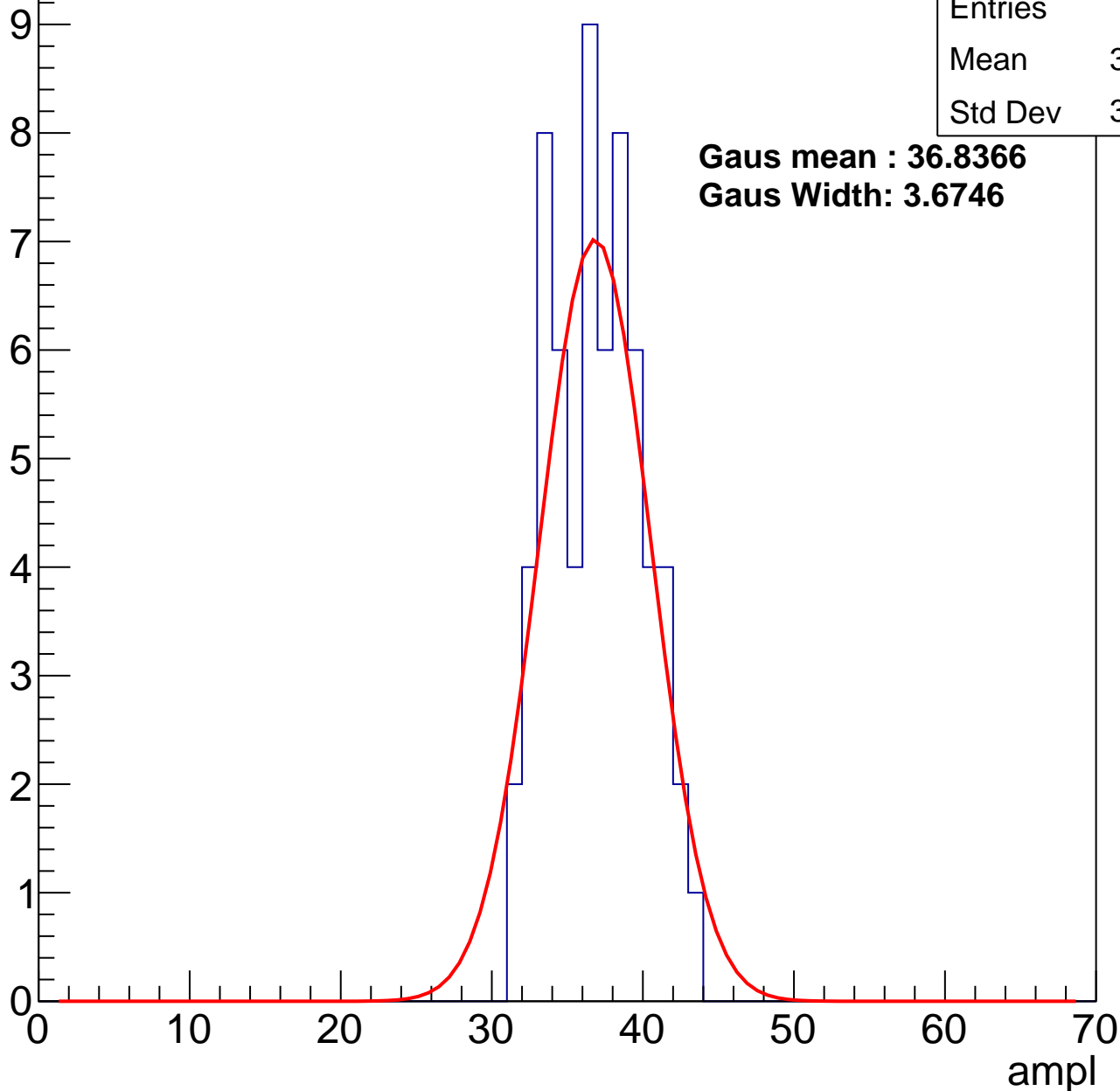
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.45
Std Dev	3.005

Gaus mean : 36.8366

Gaus Width: 3.6746



B1L102S, U13-ch105, adc2

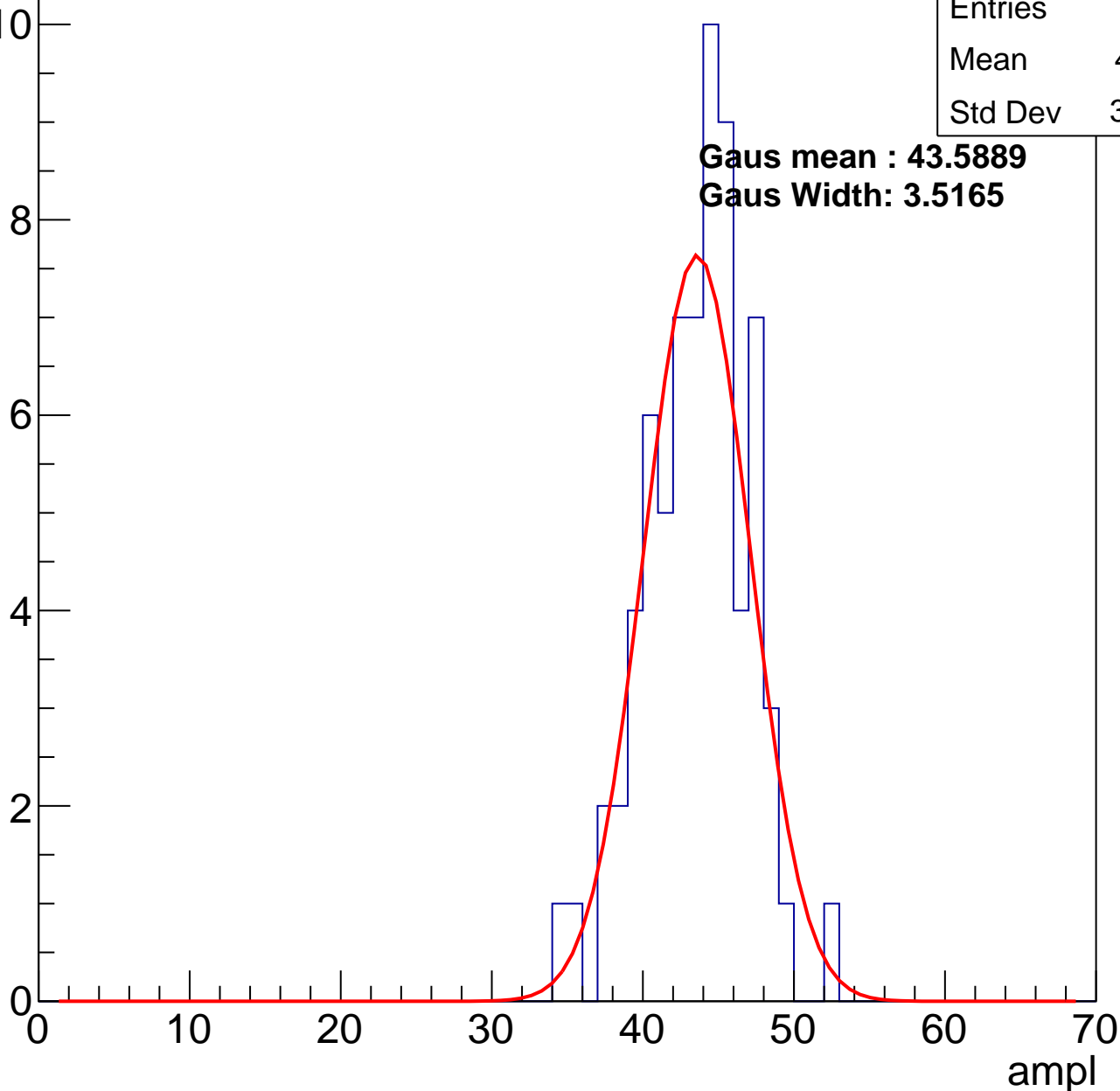
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.11
Std Dev	3.374

Gaus mean : 43.5889

Gaus Width: 3.5165

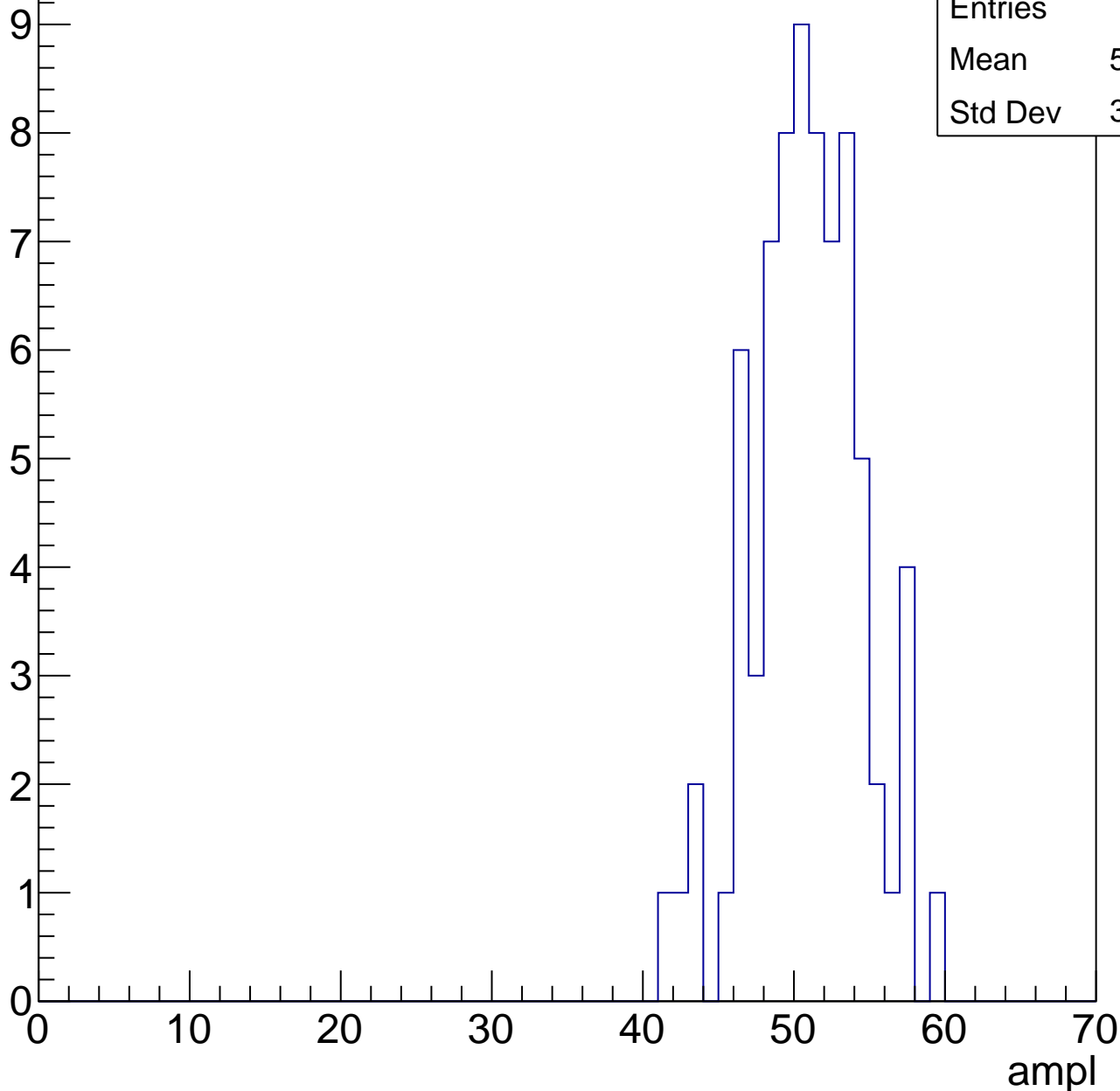


B1L102S, U13-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

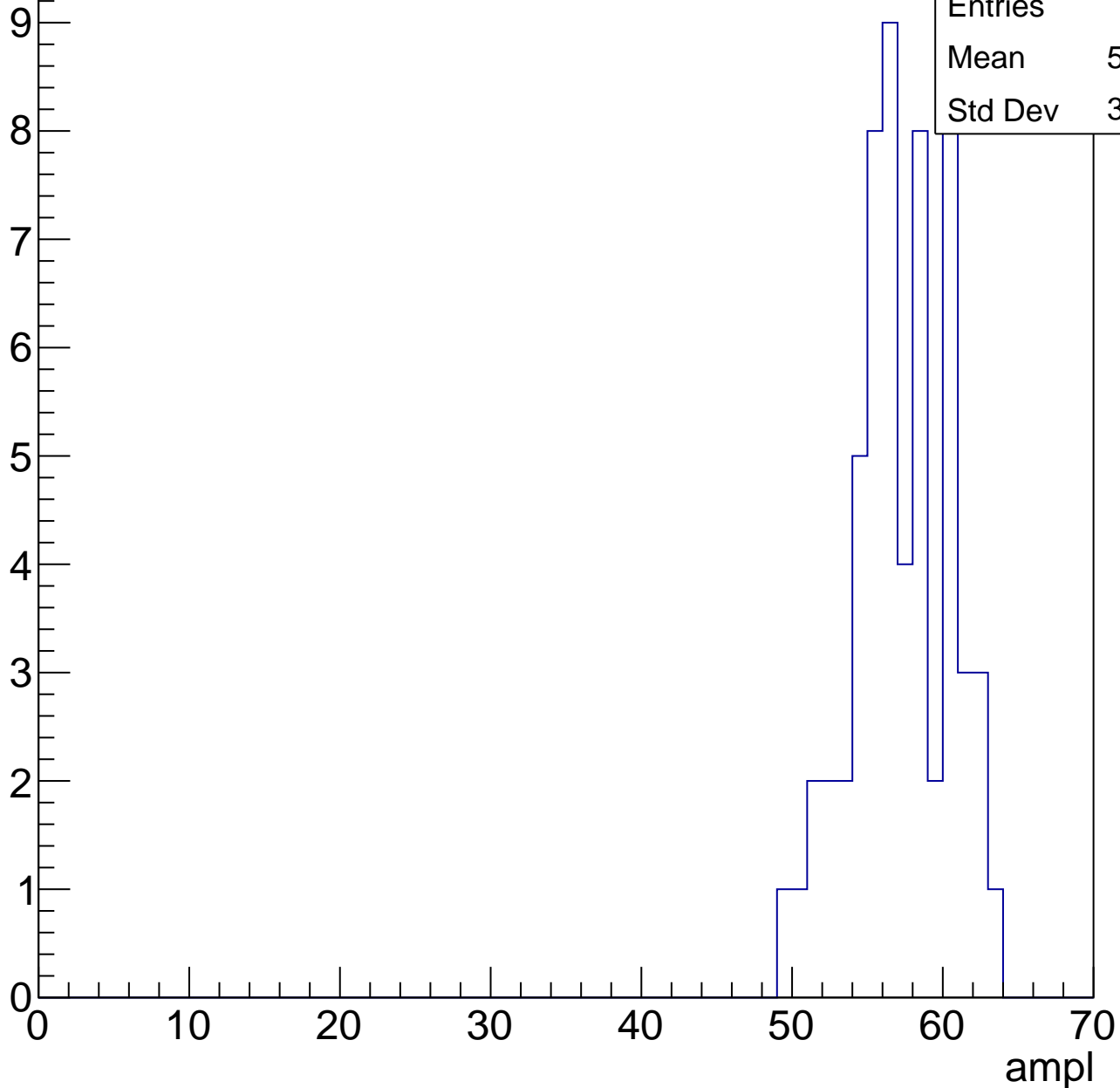
Entries	74
Mean	50.38
Std Dev	3.616



B1L102S, U13-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

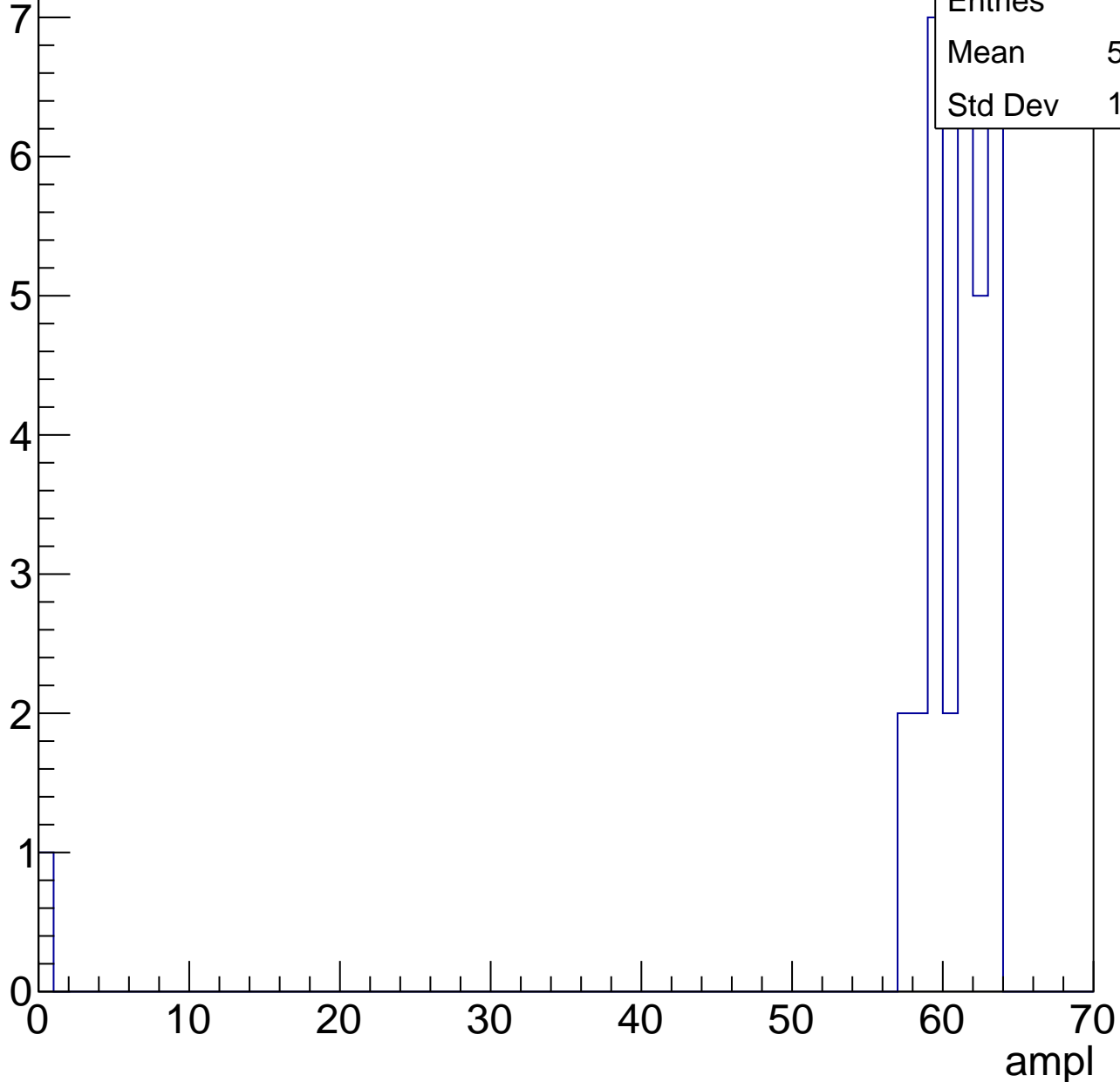


Entries	59
Mean	56.73
Std Dev	3.177

B1L102S, U13-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

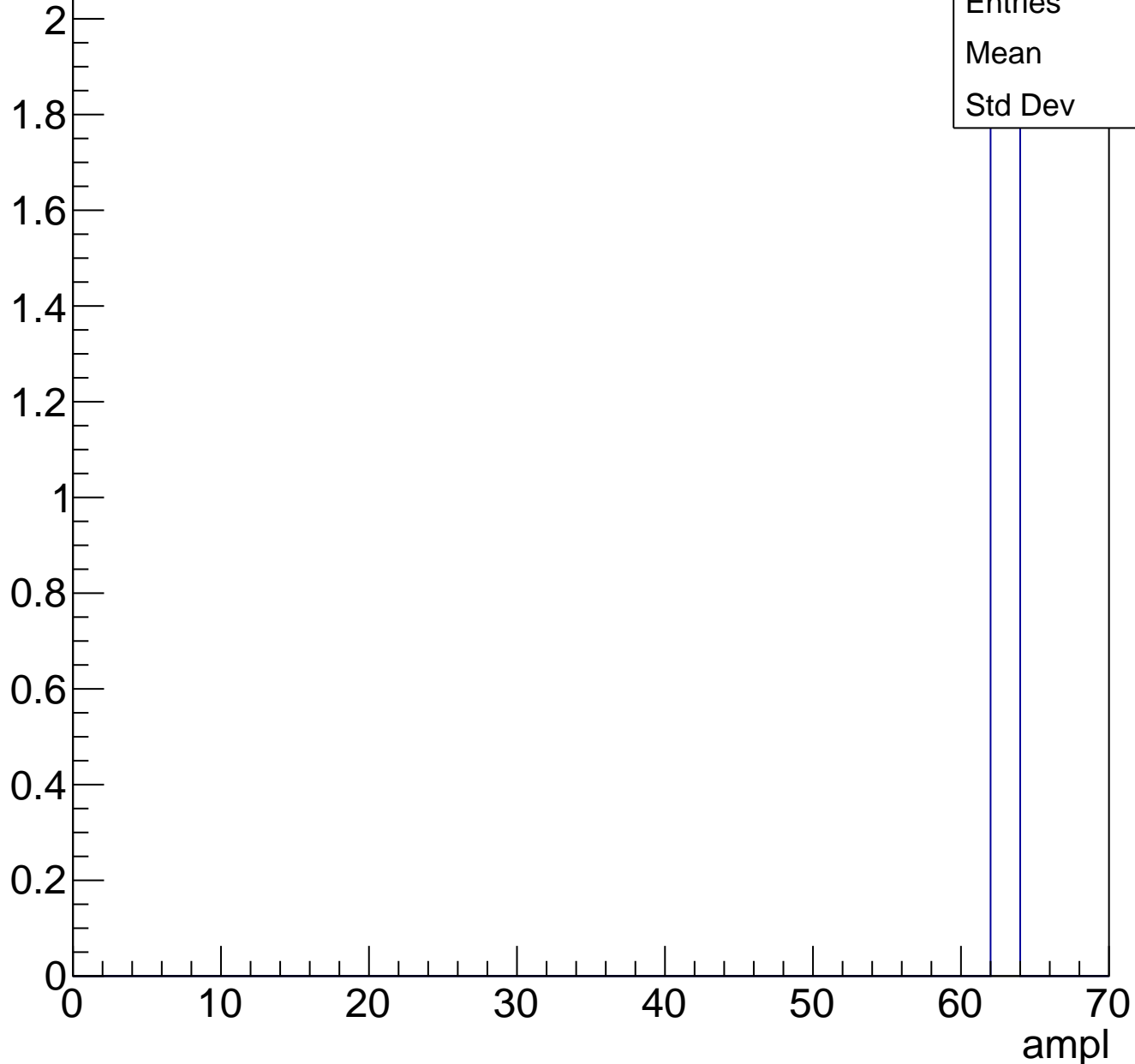
Entry



B1L102S, U13-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch106, adc0

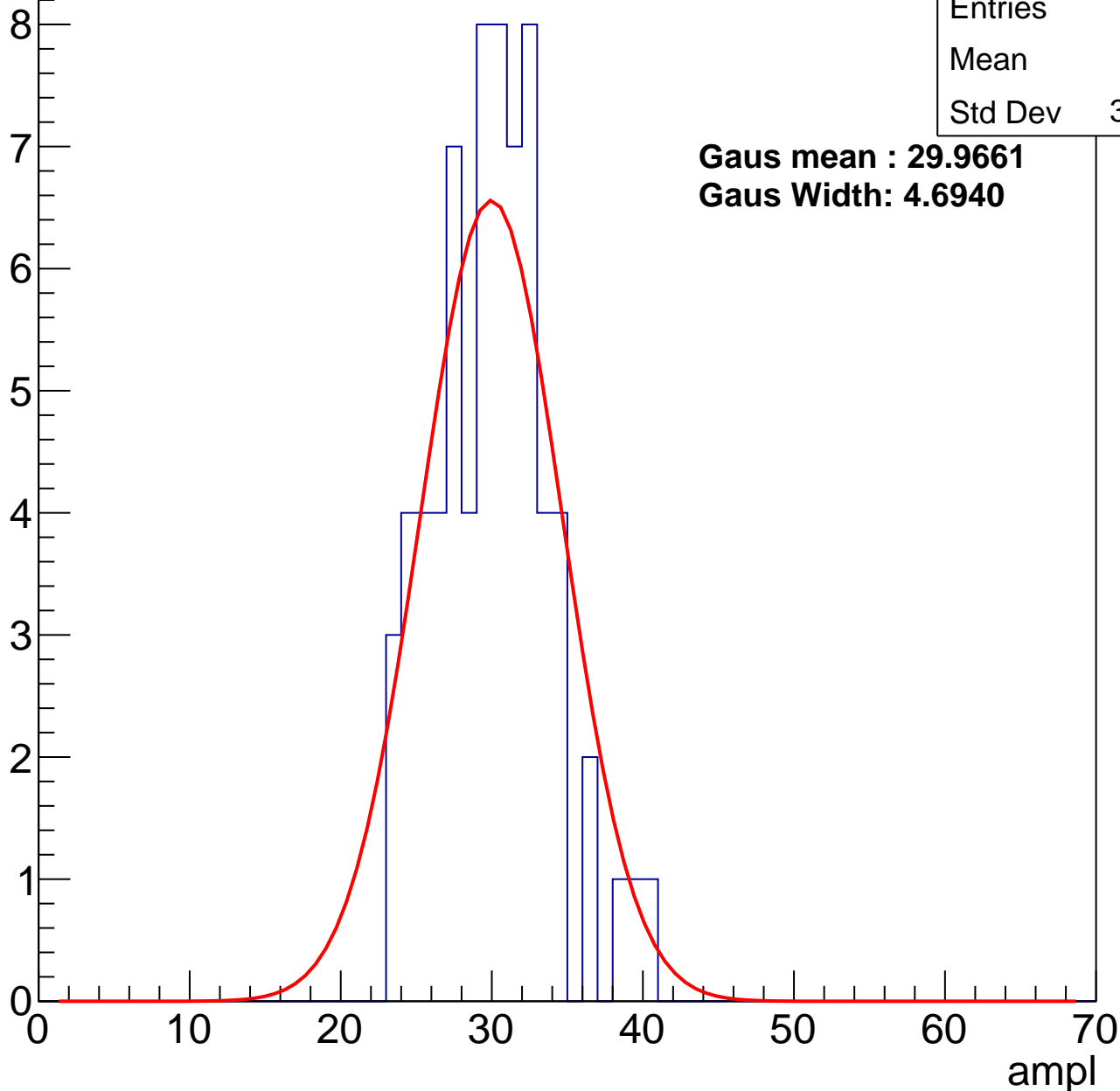
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.6
Std Dev	3.755

Gaus mean : 29.9661

Gaus Width: 4.6940



B1L102S, U13-ch106, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	35.82
Std Dev	4.51

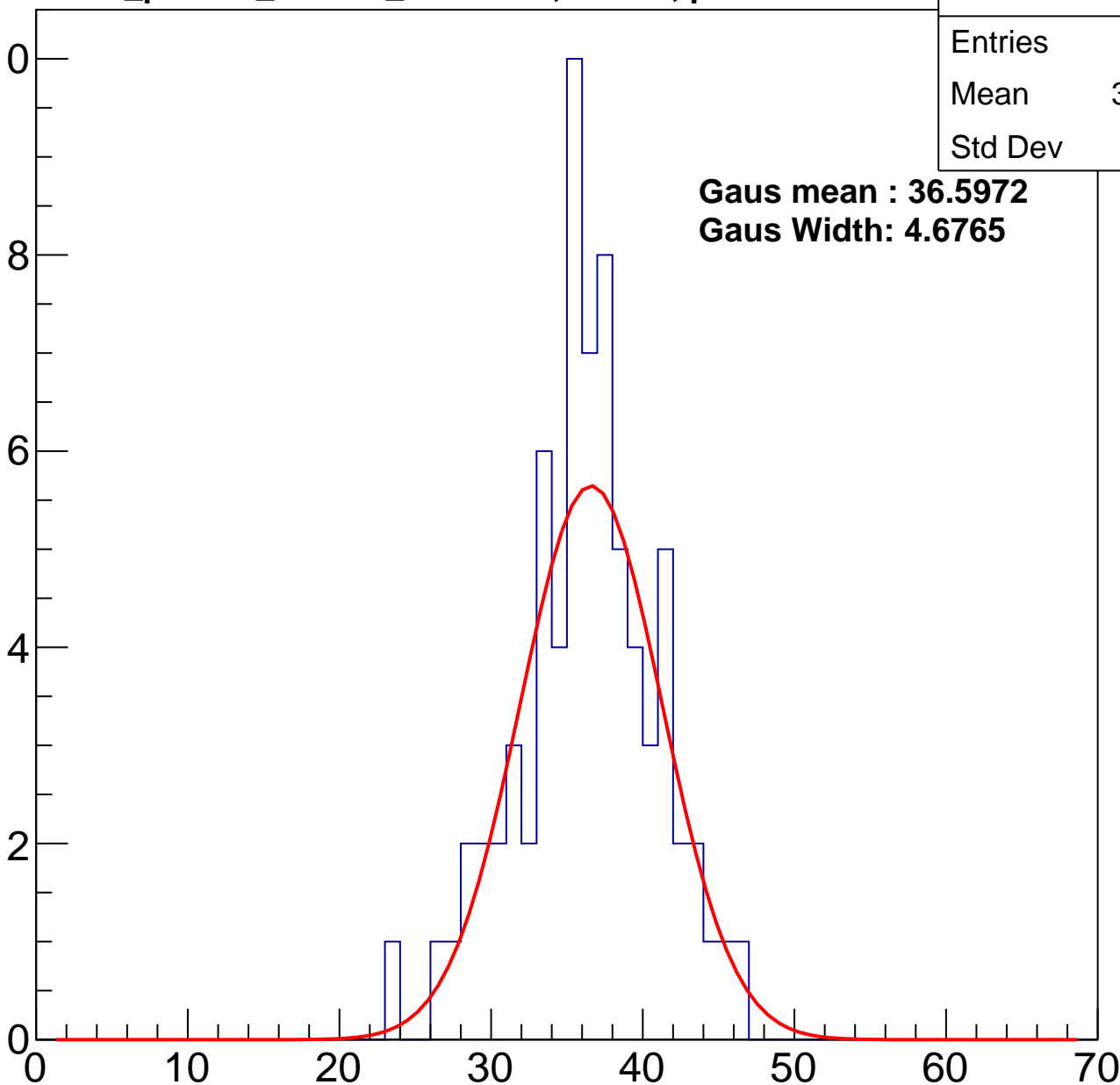
Gaus mean : 36.5972

Gaus Width: 4.6765

Entry

10
8
6
4
2
0

ampl



B1L102S, U13-ch106, adc2

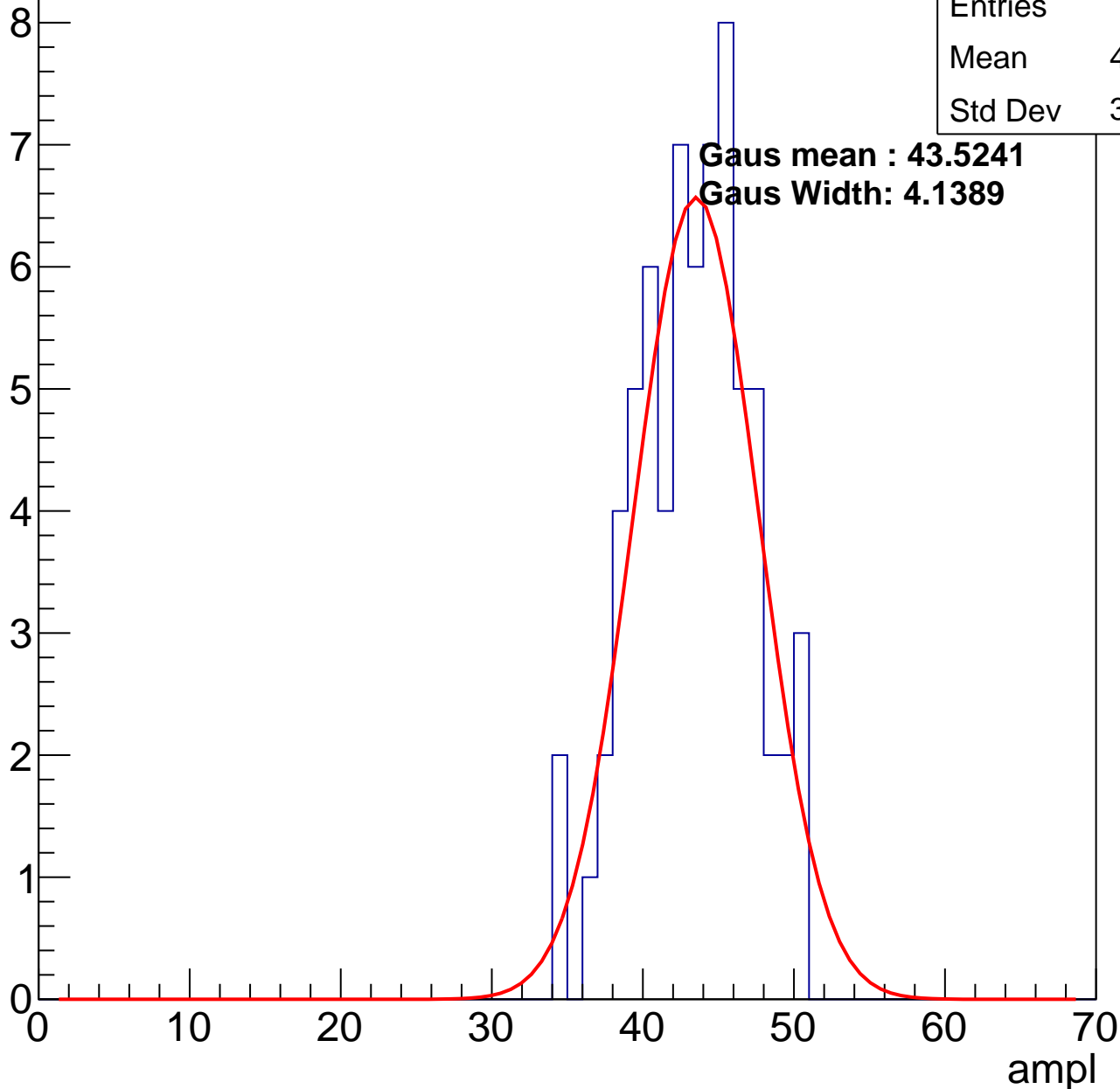
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.87
Std Dev	3.753

Gaus mean : 43.5241

Gaus Width: 4.1389

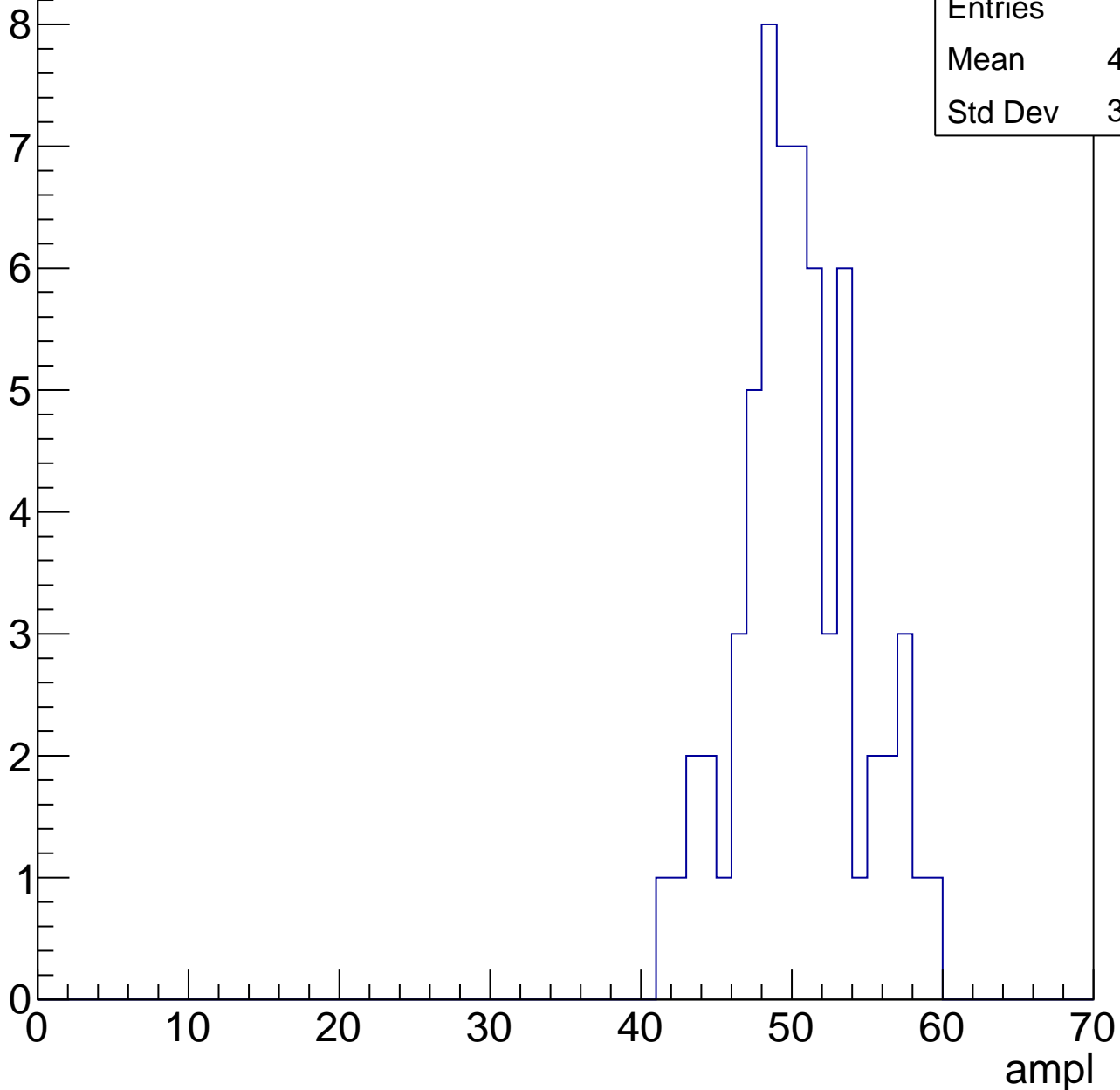


B1L102S, U13-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.94
Std Dev	3.963

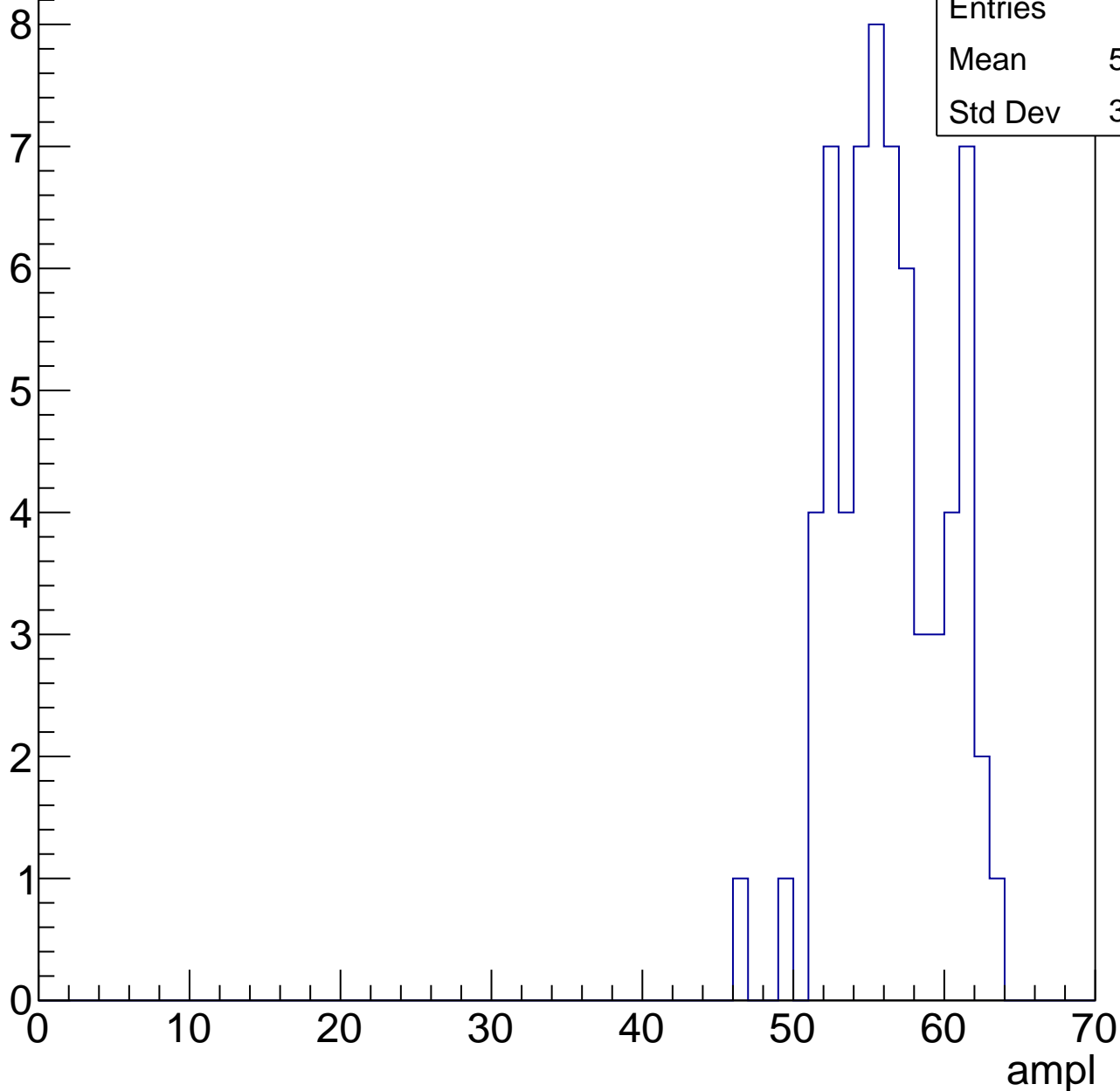


B1L102S, U13-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

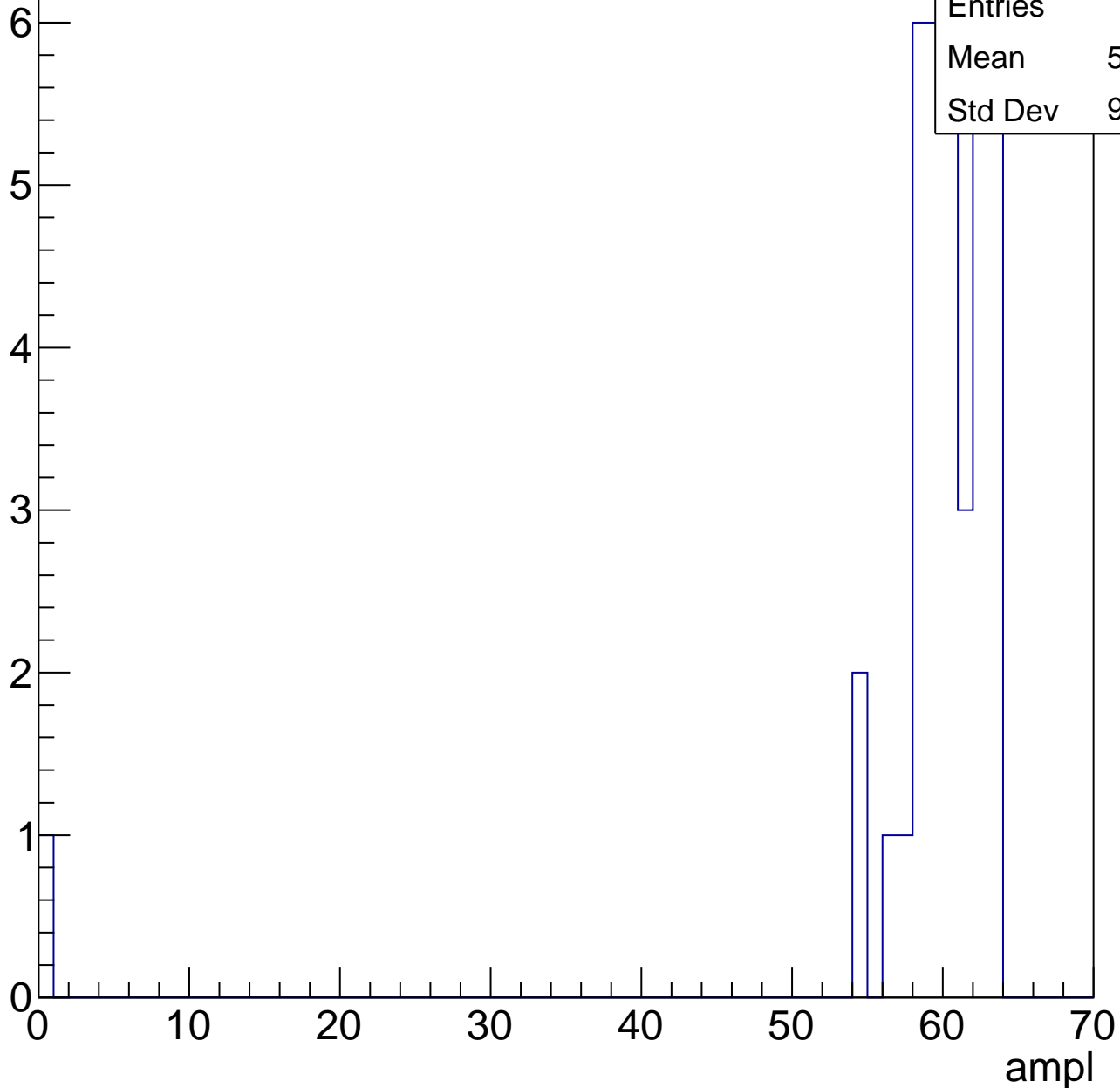
Entries	65
Mean	55.88
Std Dev	3.588



B1L102S, U13-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch107, adc0

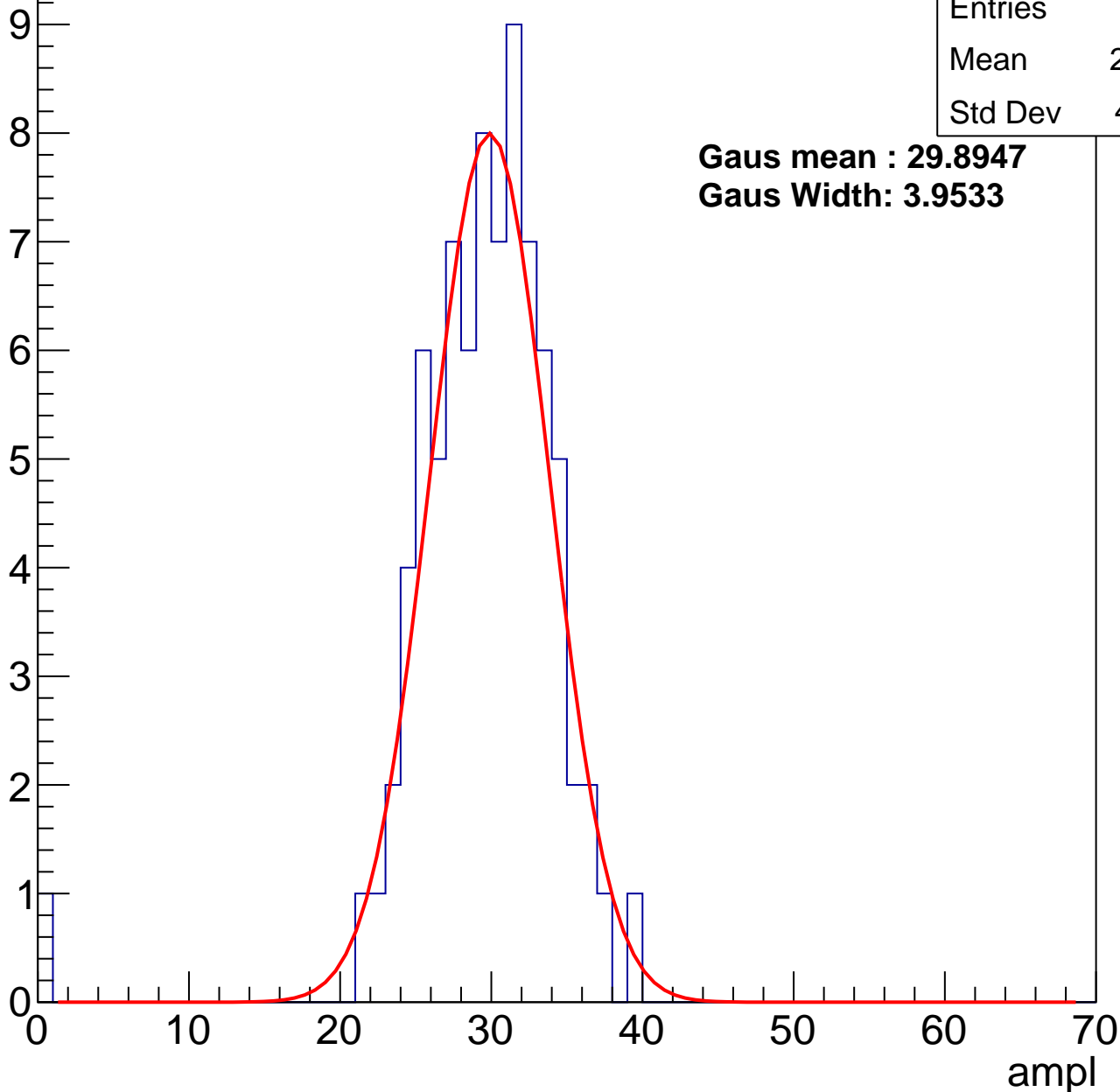
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.05
Std Dev	4.921

Gaus mean : 29.8947

Gaus Width: 3.9533



B1L102S, U13-ch107, adc1

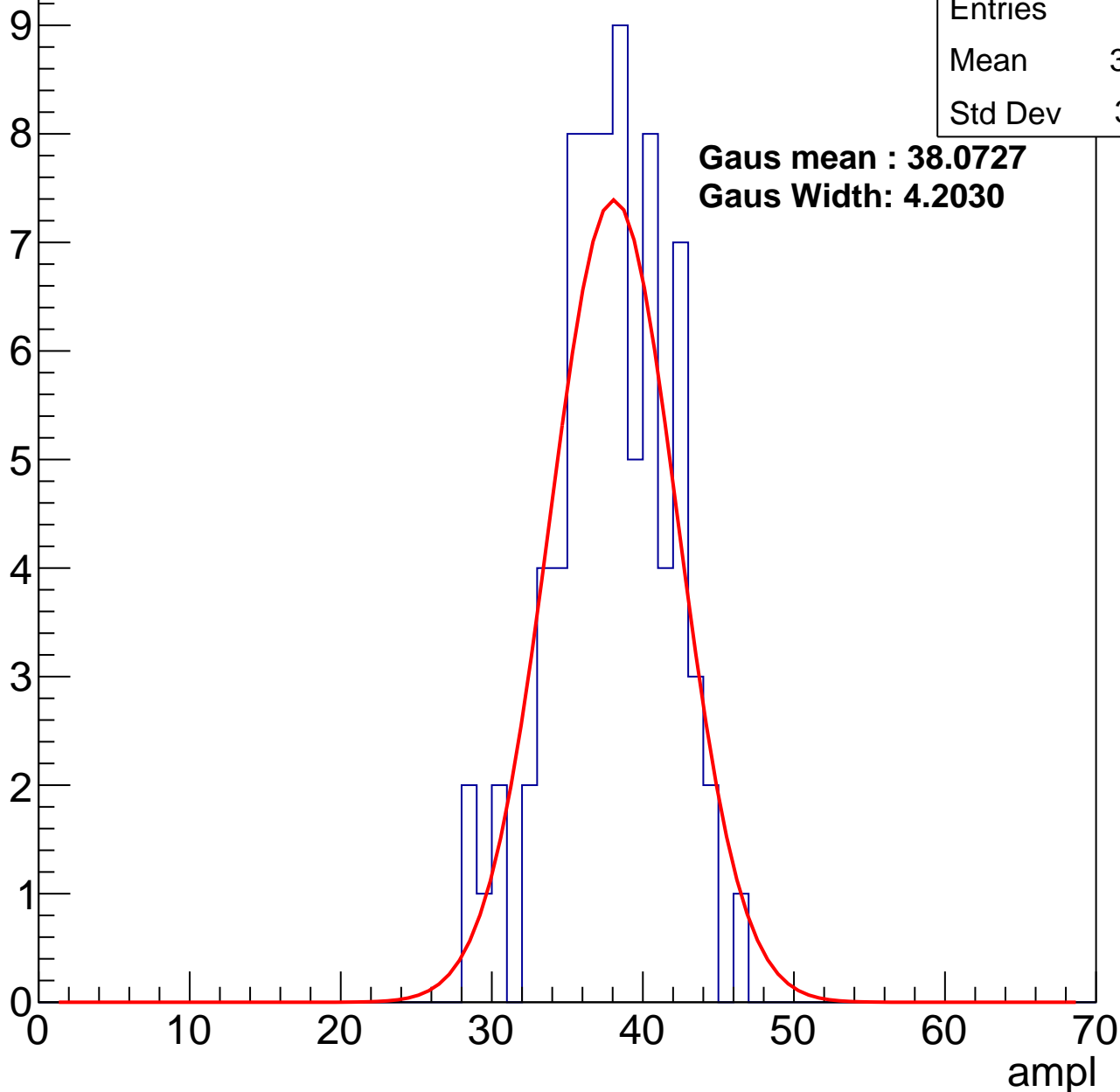
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	37.42
Std Dev	3.801

Gaus mean : 38.0727

Gaus Width: 4.2030



B1L102S, U13-ch107, adc2

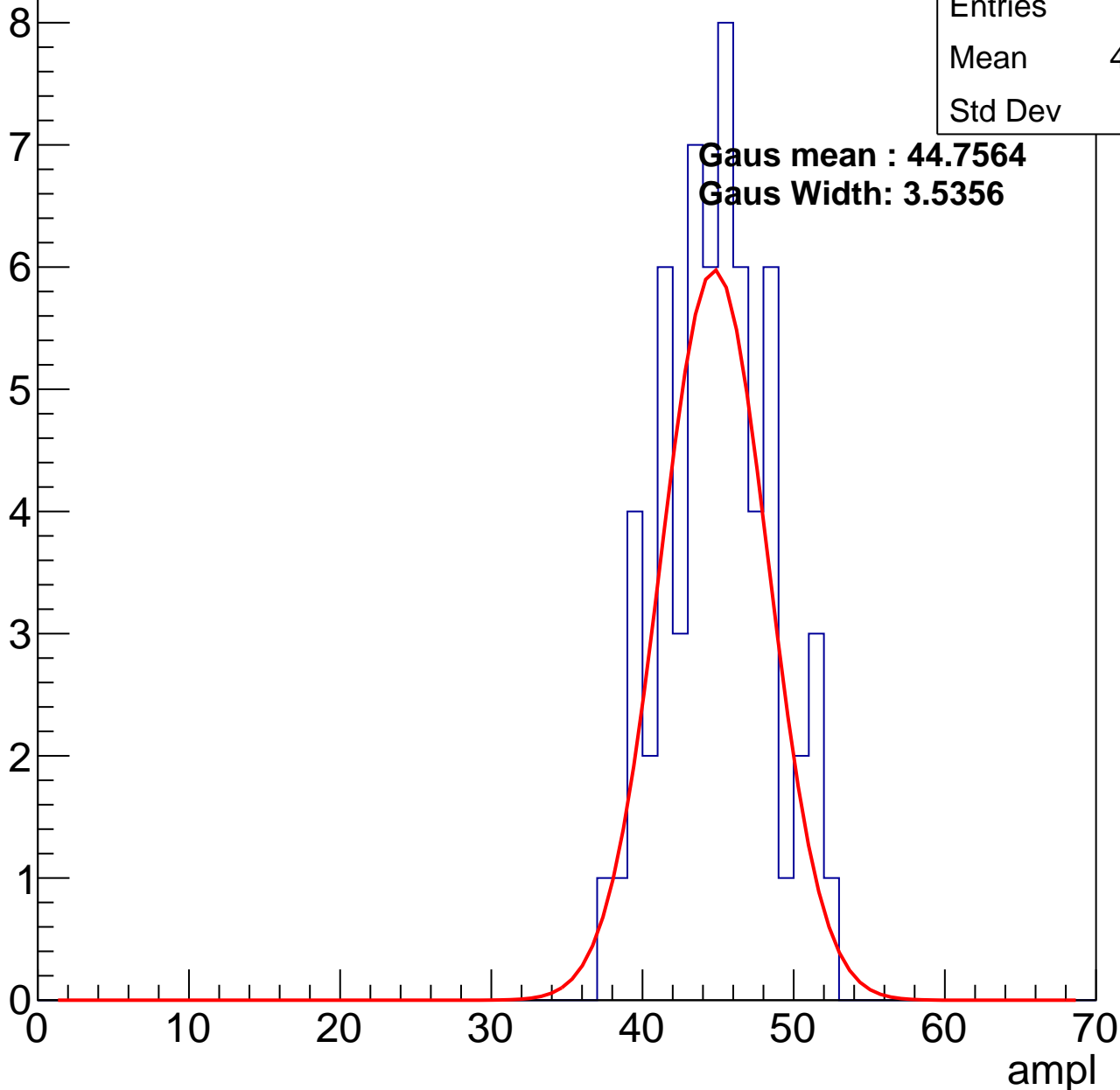
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	44.49
Std Dev	3.5

Gaus mean : 44.7564

Gaus Width: 3.5356

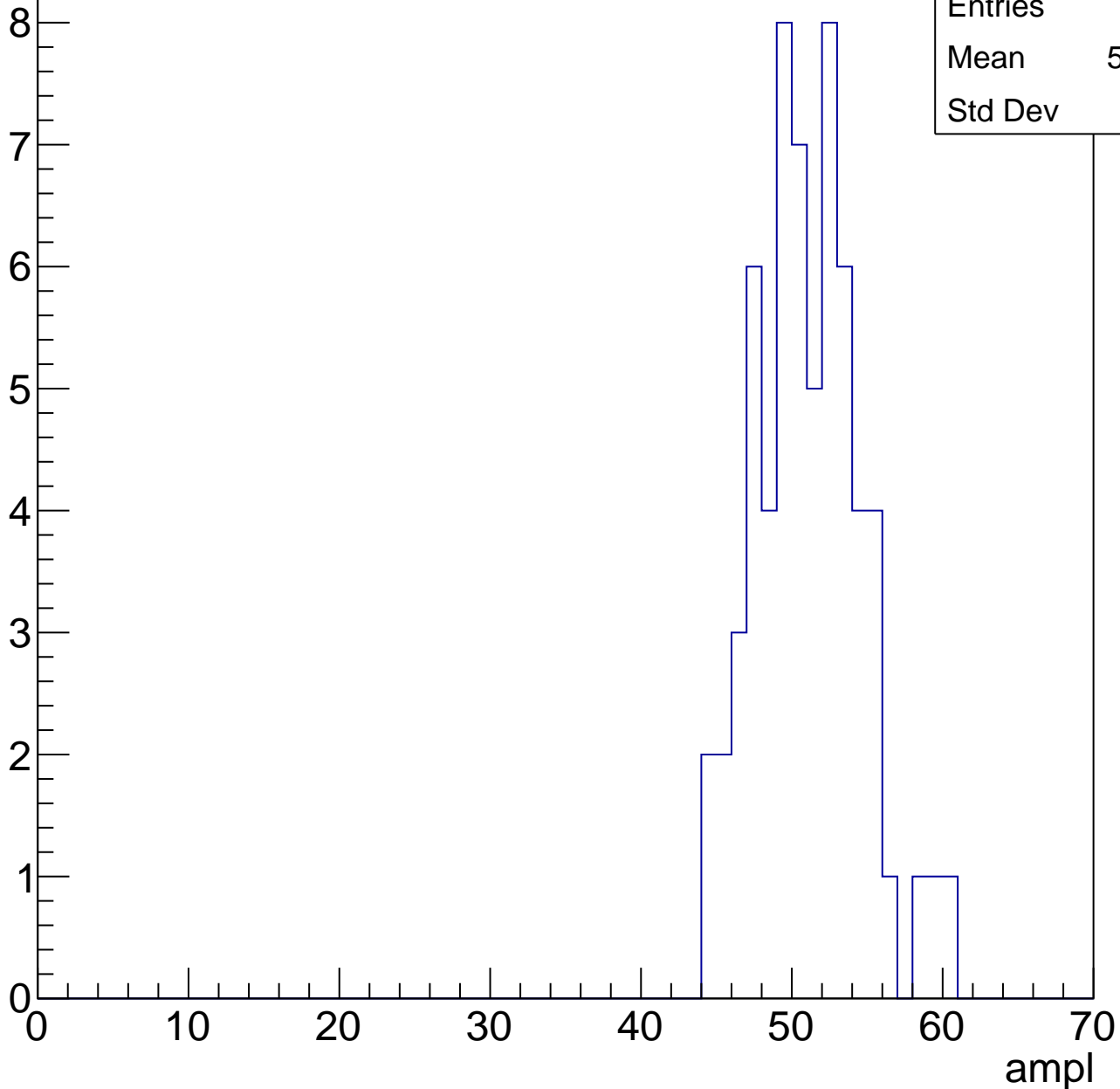


B1L102S, U13-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.63
Std Dev	3.47

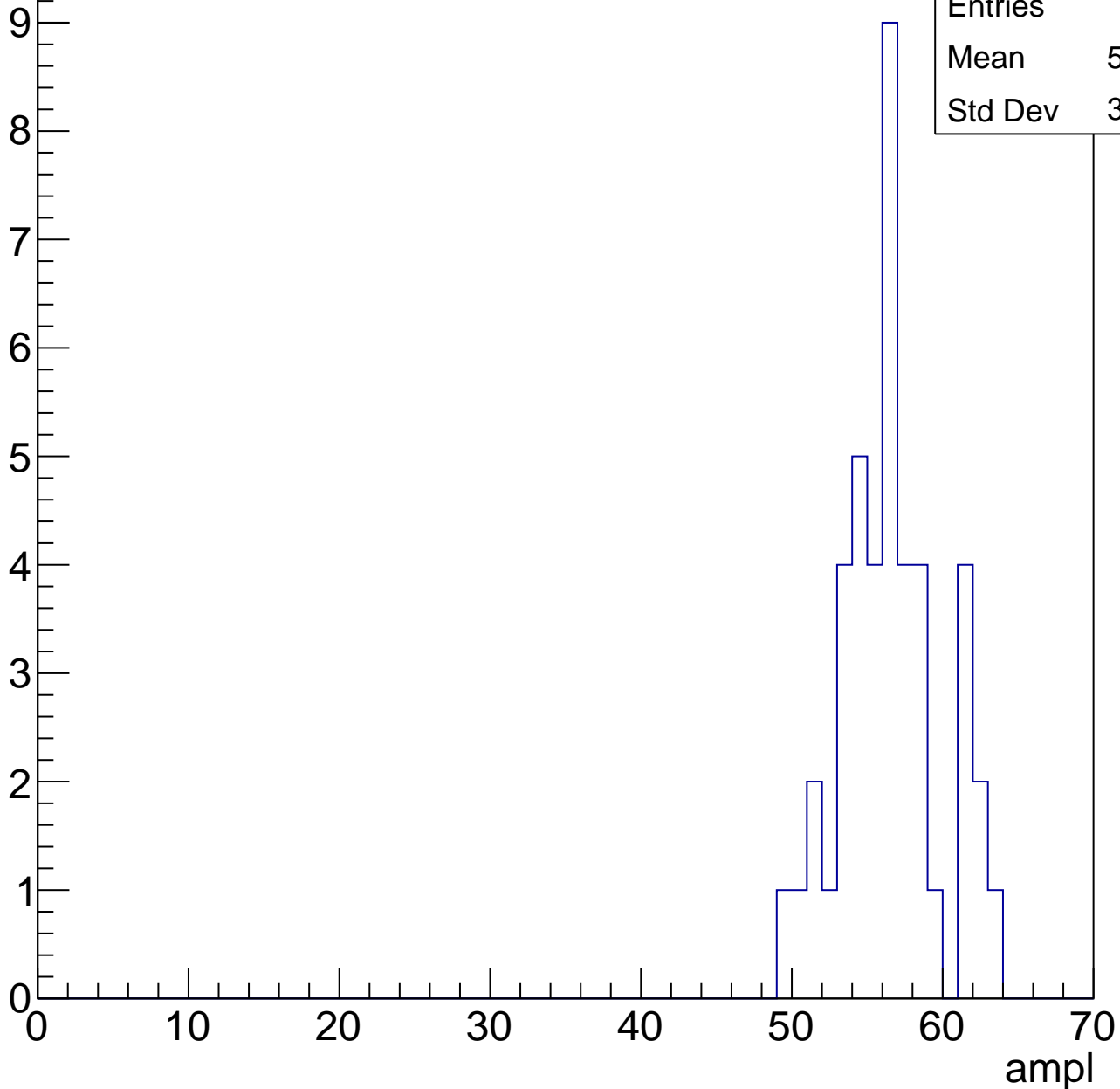


B1L102S, U13-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	56.02
Std Dev	3.274

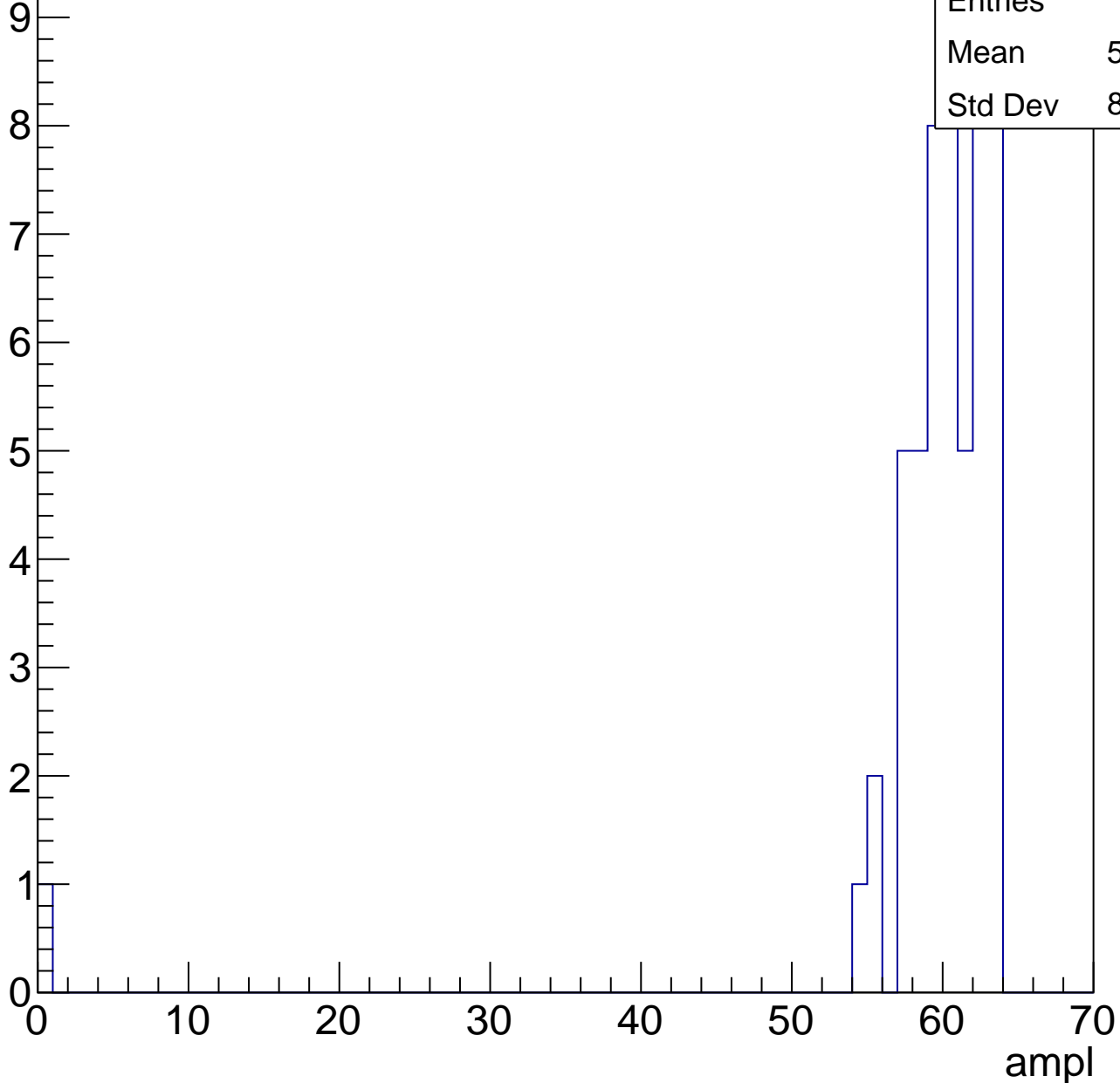


B1L102S, U13-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

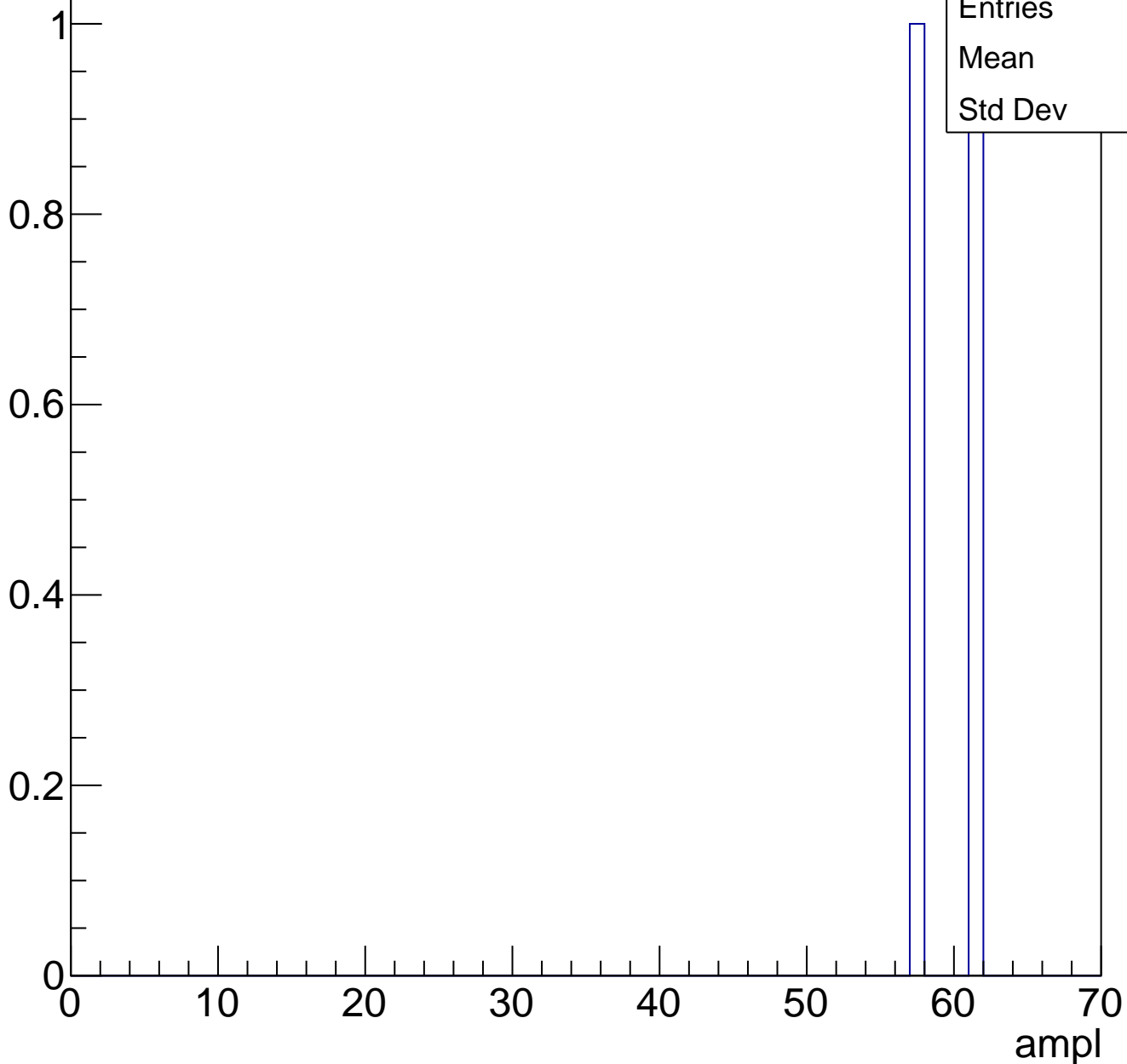
Entries	53
Mean	58.85
Std Dev	8.475



B1L102S, U13-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch108, adc0

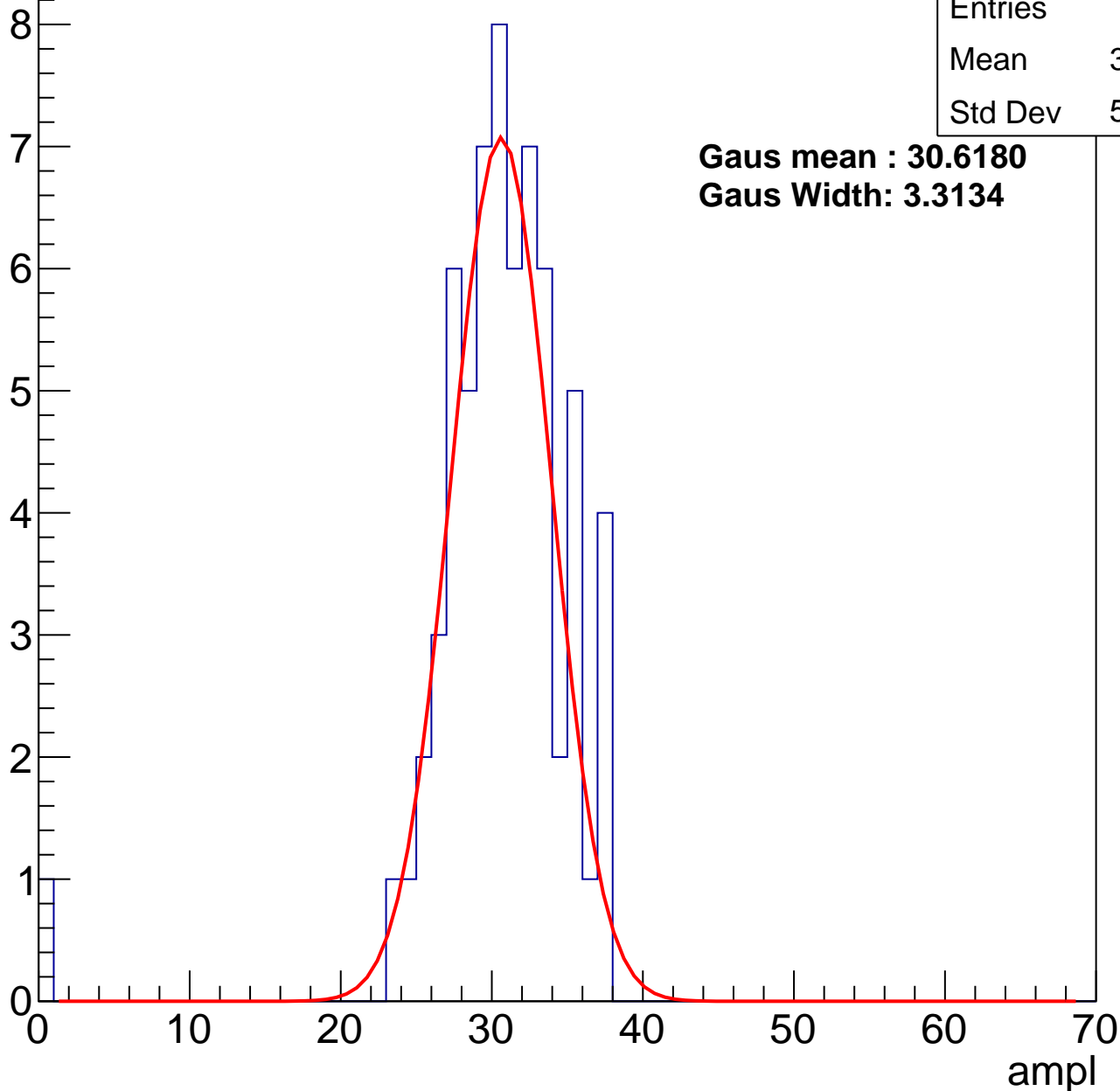
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.08
Std Dev	5.024

Gaus mean : 30.6180

Gaus Width: 3.3134



B1L102S, U13-ch108, adc1

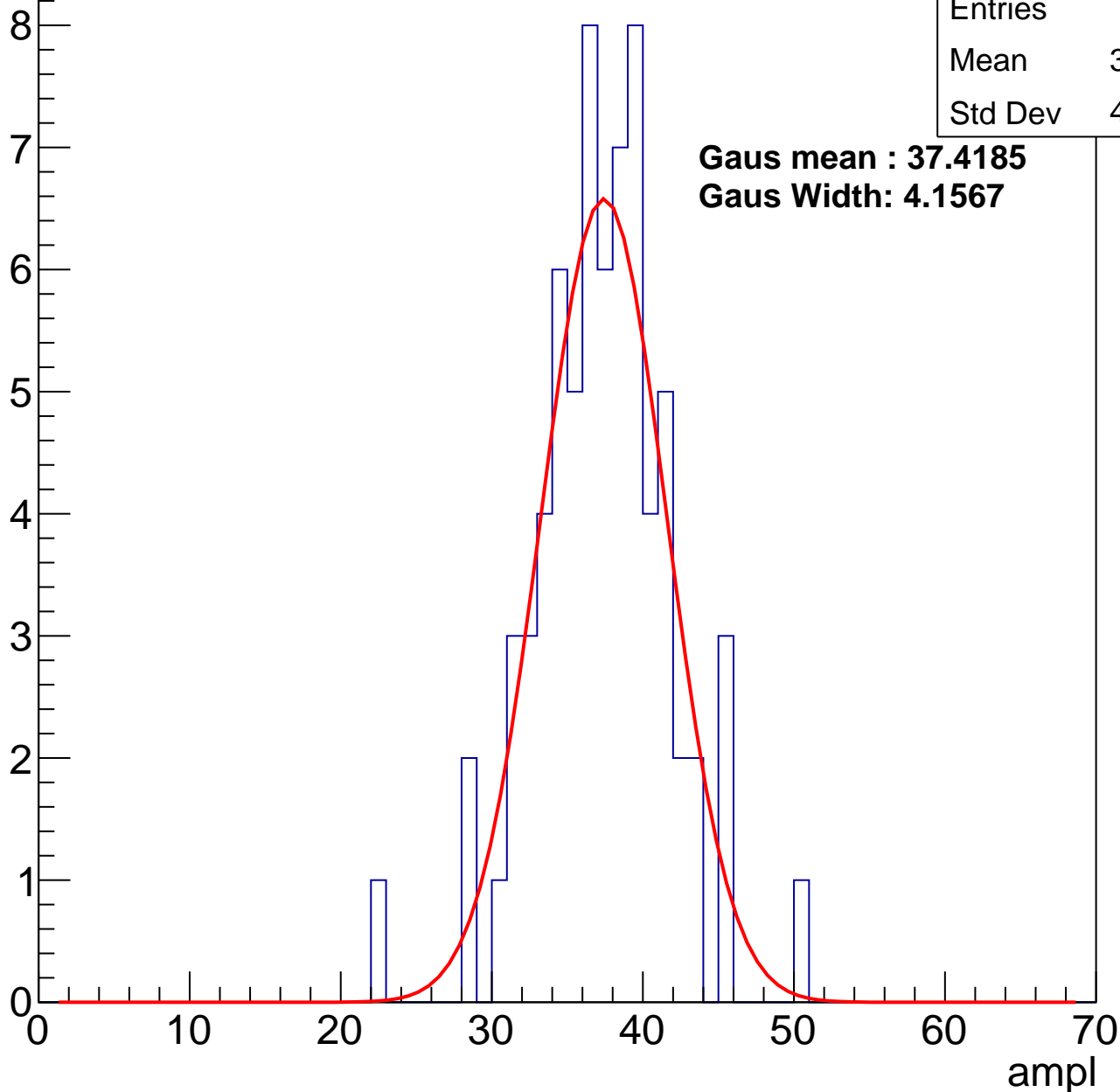
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.85
Std Dev	4.443

Gaus mean : 37.4185

Gaus Width: 4.1567

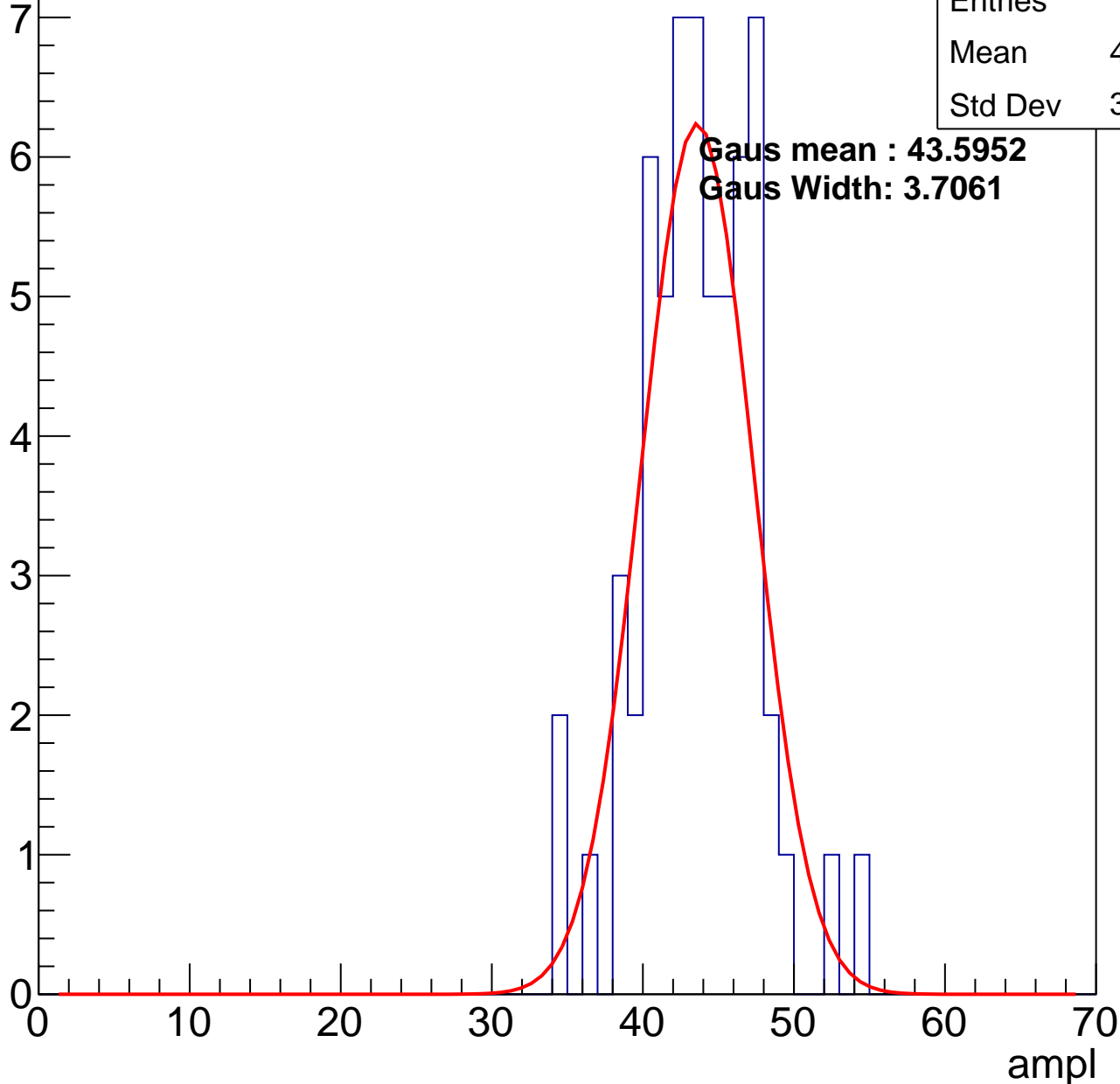


B1L102S, U13-ch108, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.23
Std Dev	3.787

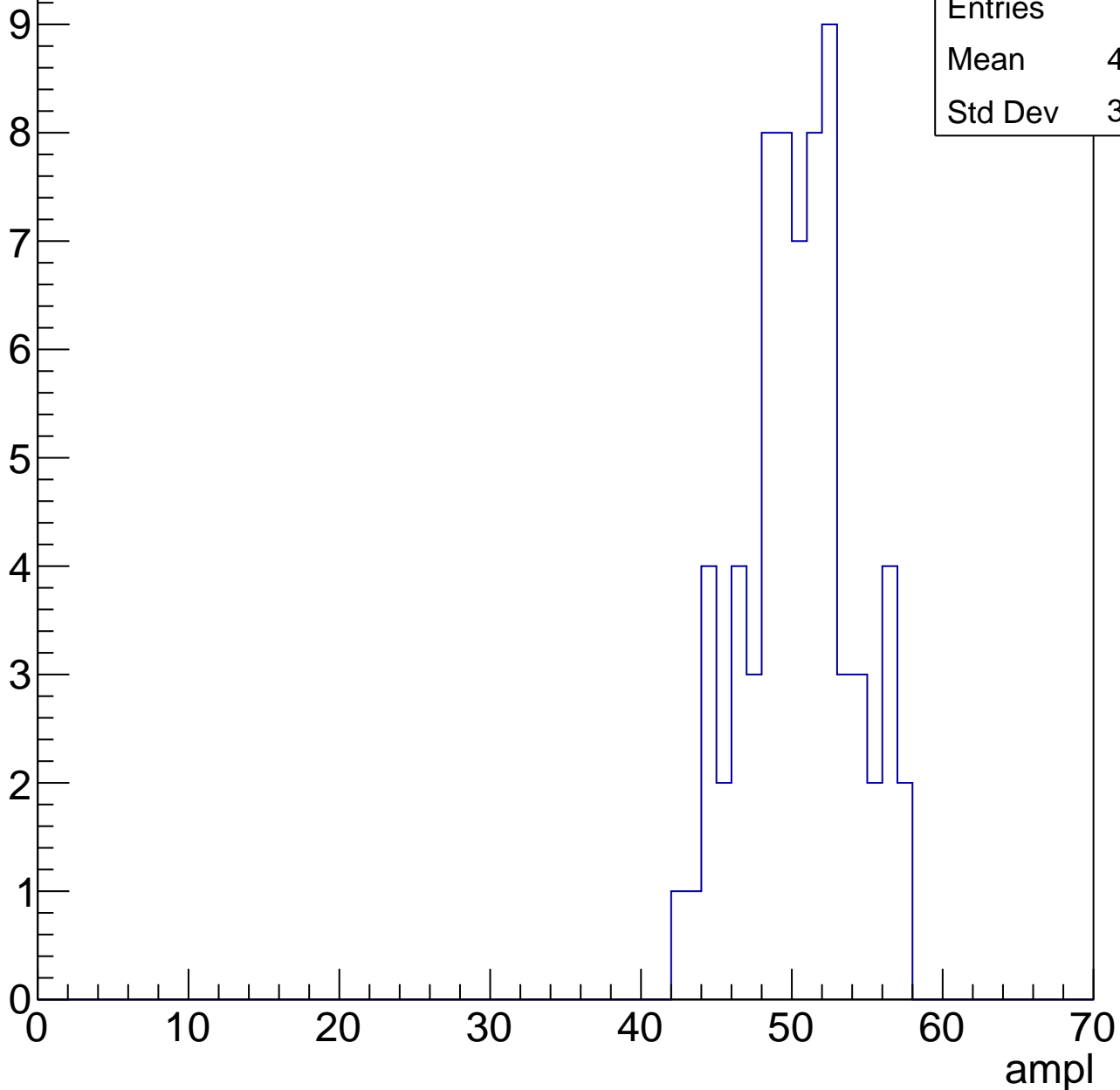


B1L102S, U13-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.96
Std Dev	3.507

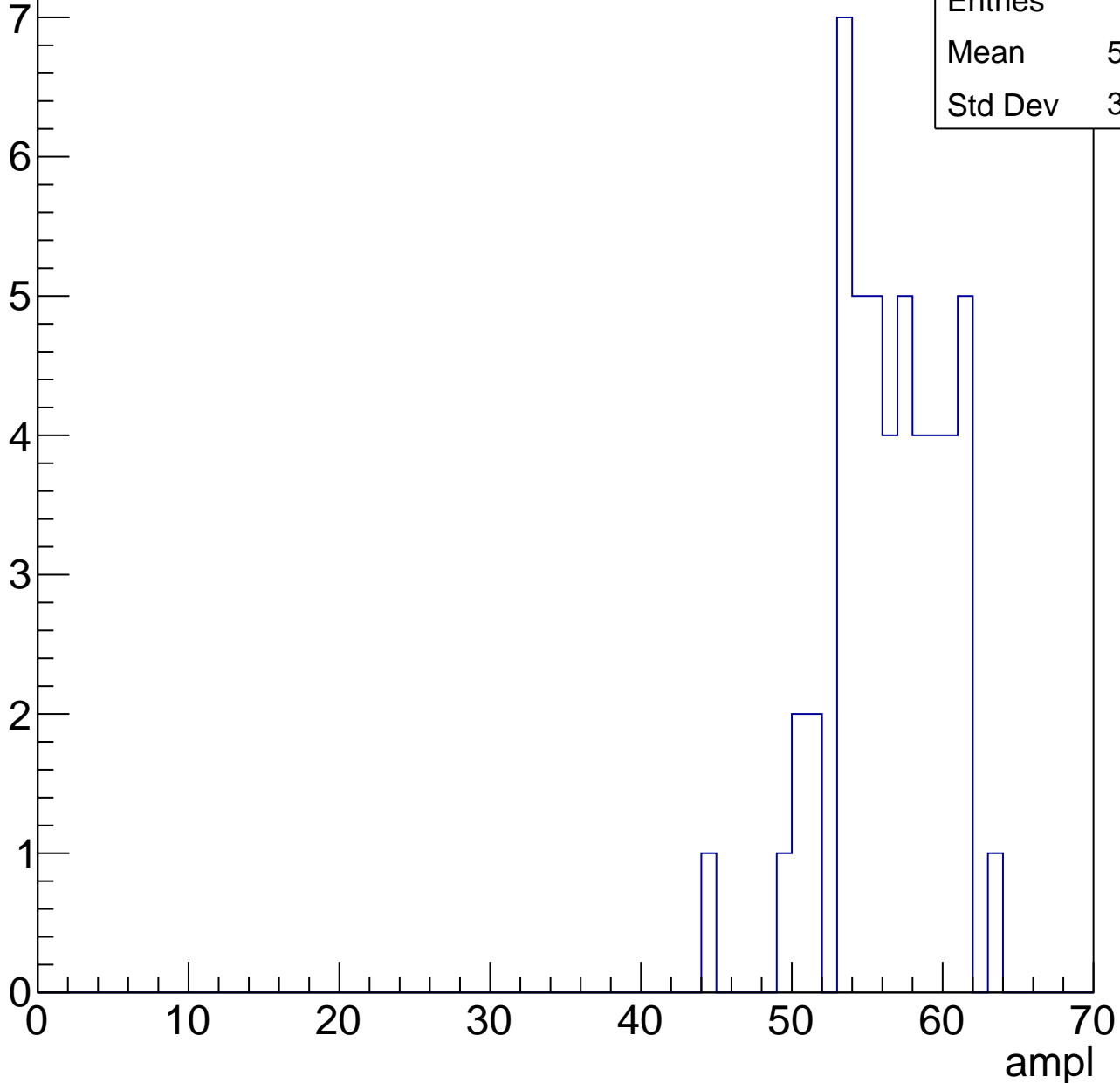


B1L102S, U13-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.92
Std Dev	3.735

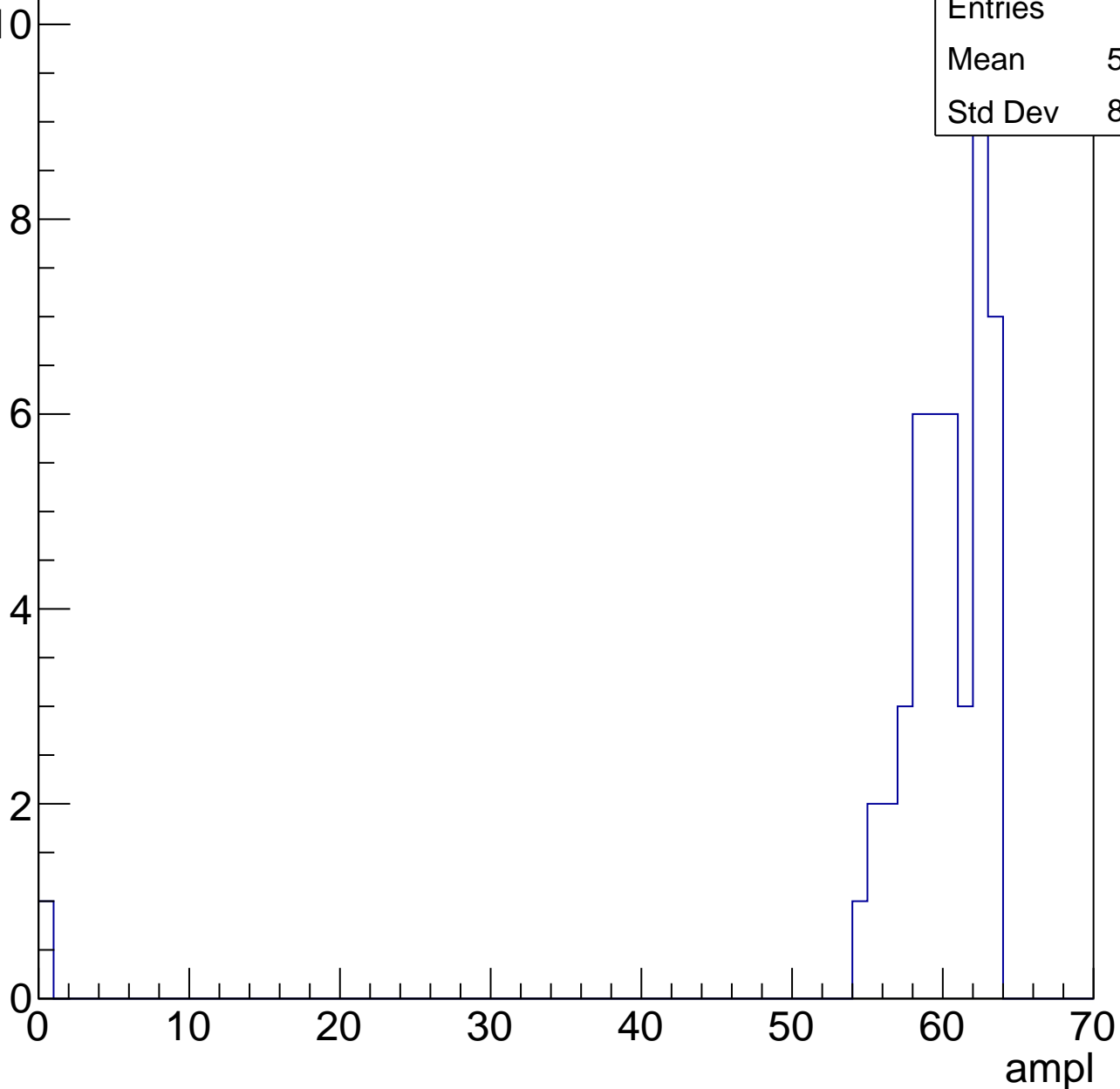


B1L102S, U13-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.57
Std Dev	8.975



B1L102S, U13-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

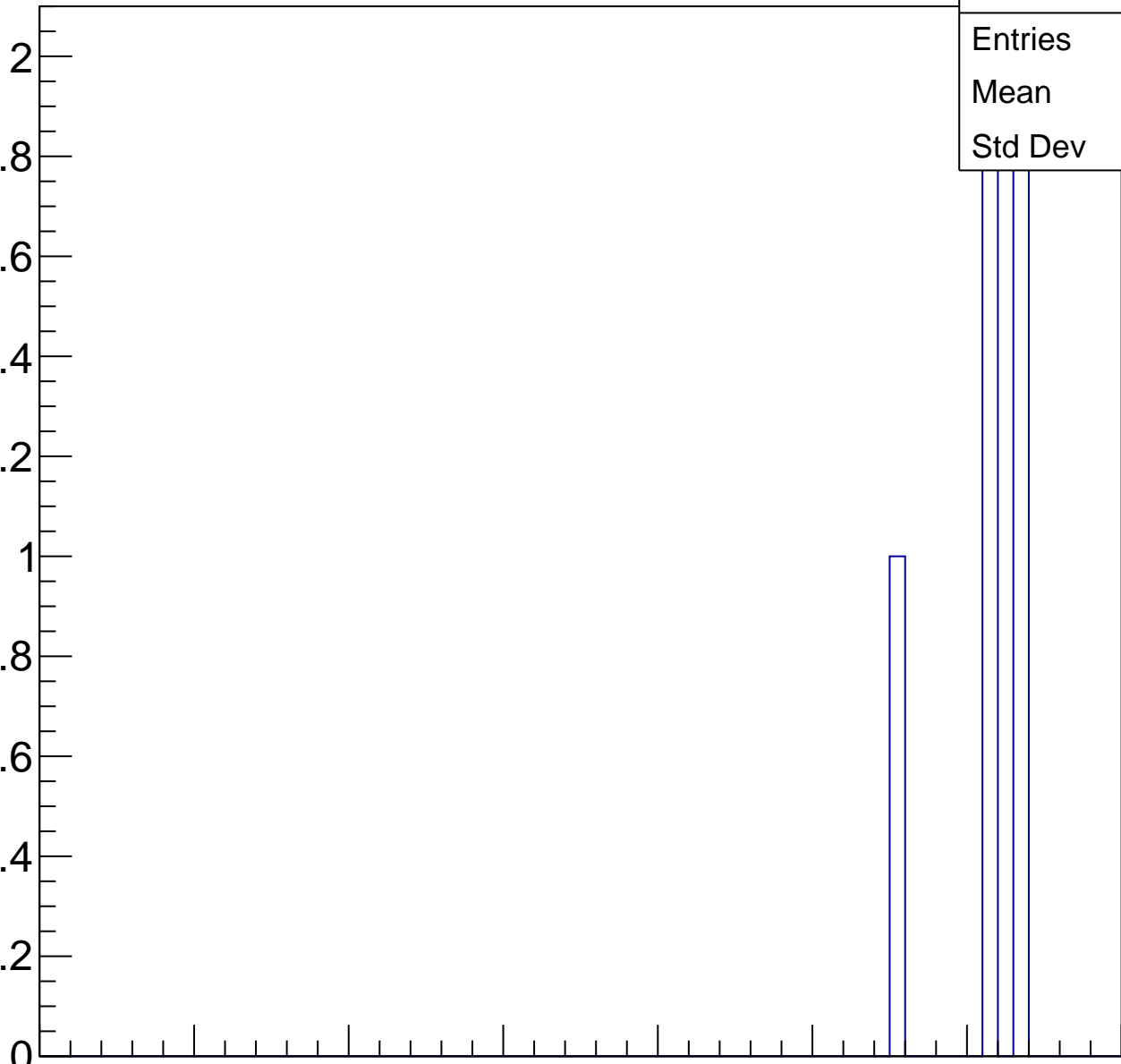
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.6
Std Dev	2.939

0 10 20 30 40 50 60 70

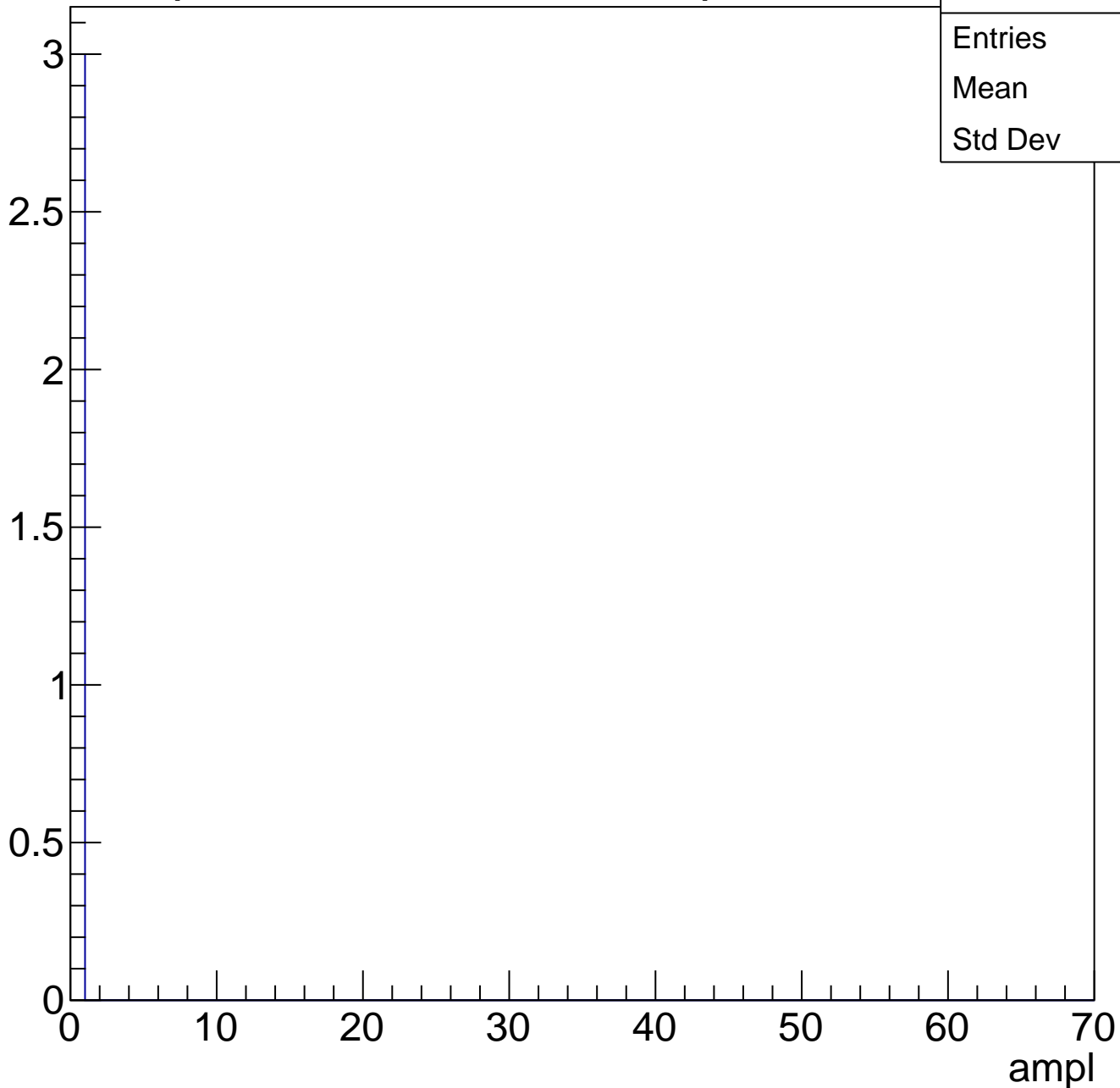
ampl



B1L102S, U13-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch109, adc0

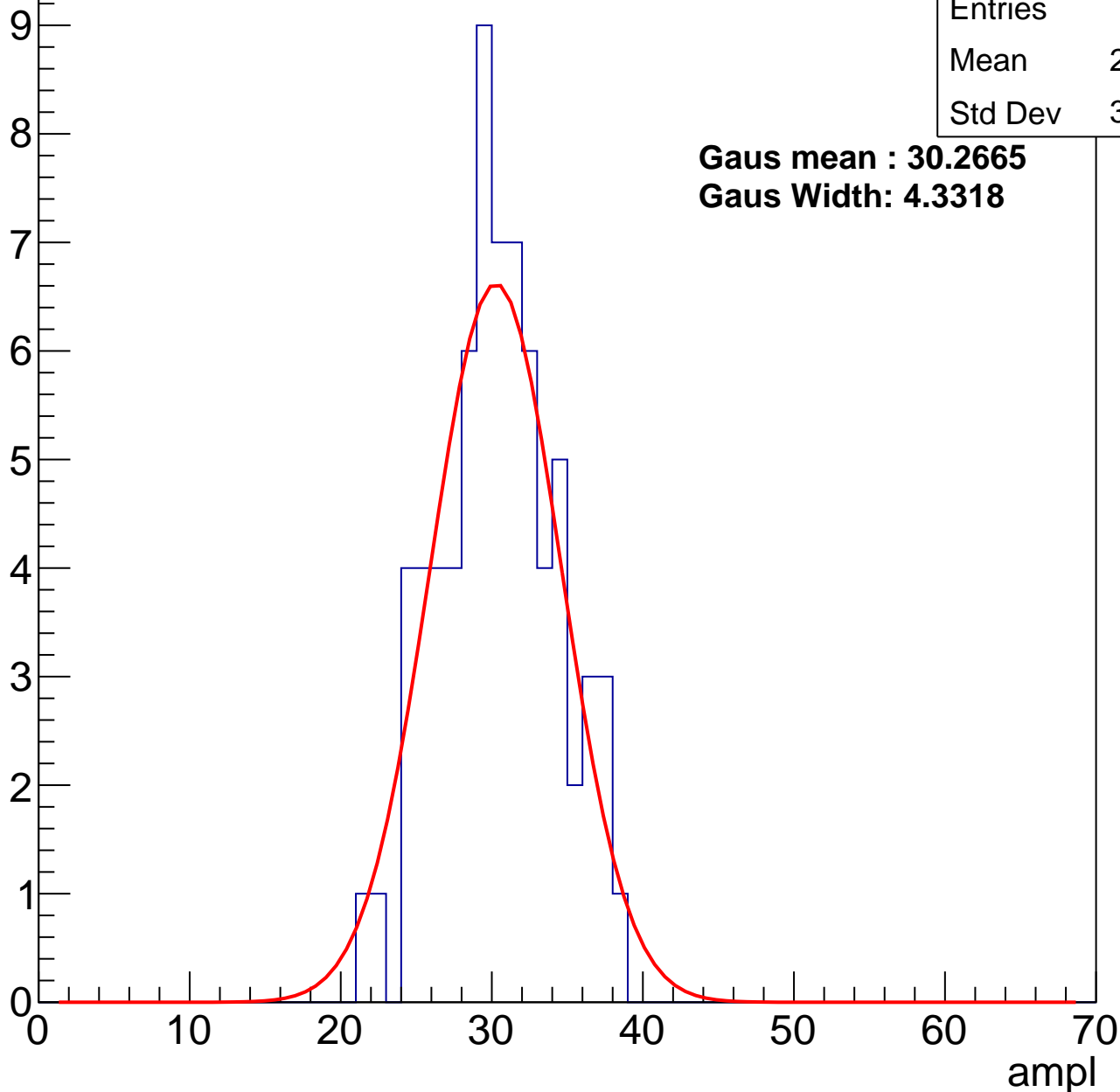
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.97
Std Dev	3.823

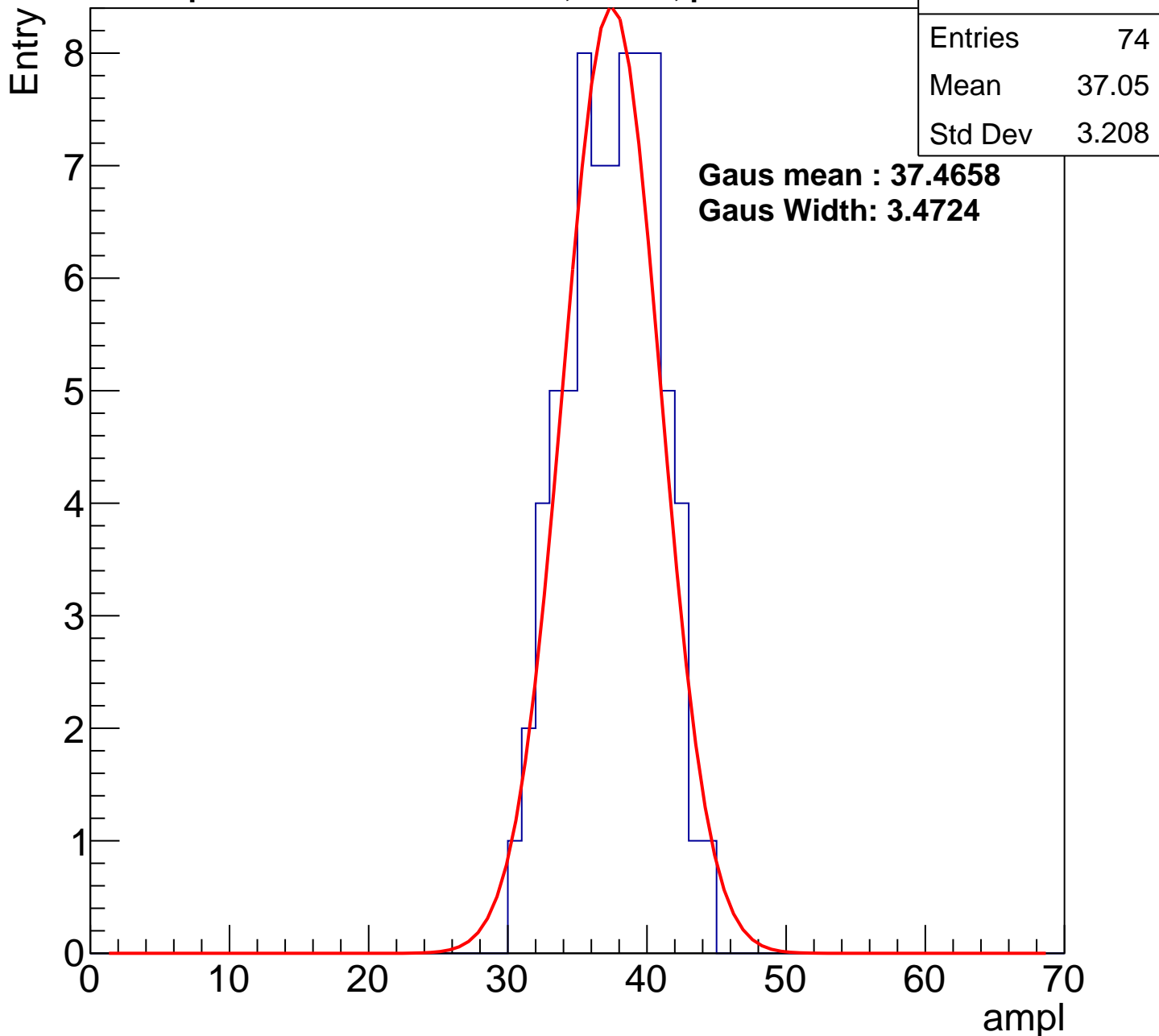
Gaus mean : 30.2665

Gaus Width: 4.3318



B1L102S, U13-ch109, adc1

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U13-ch109, adc2

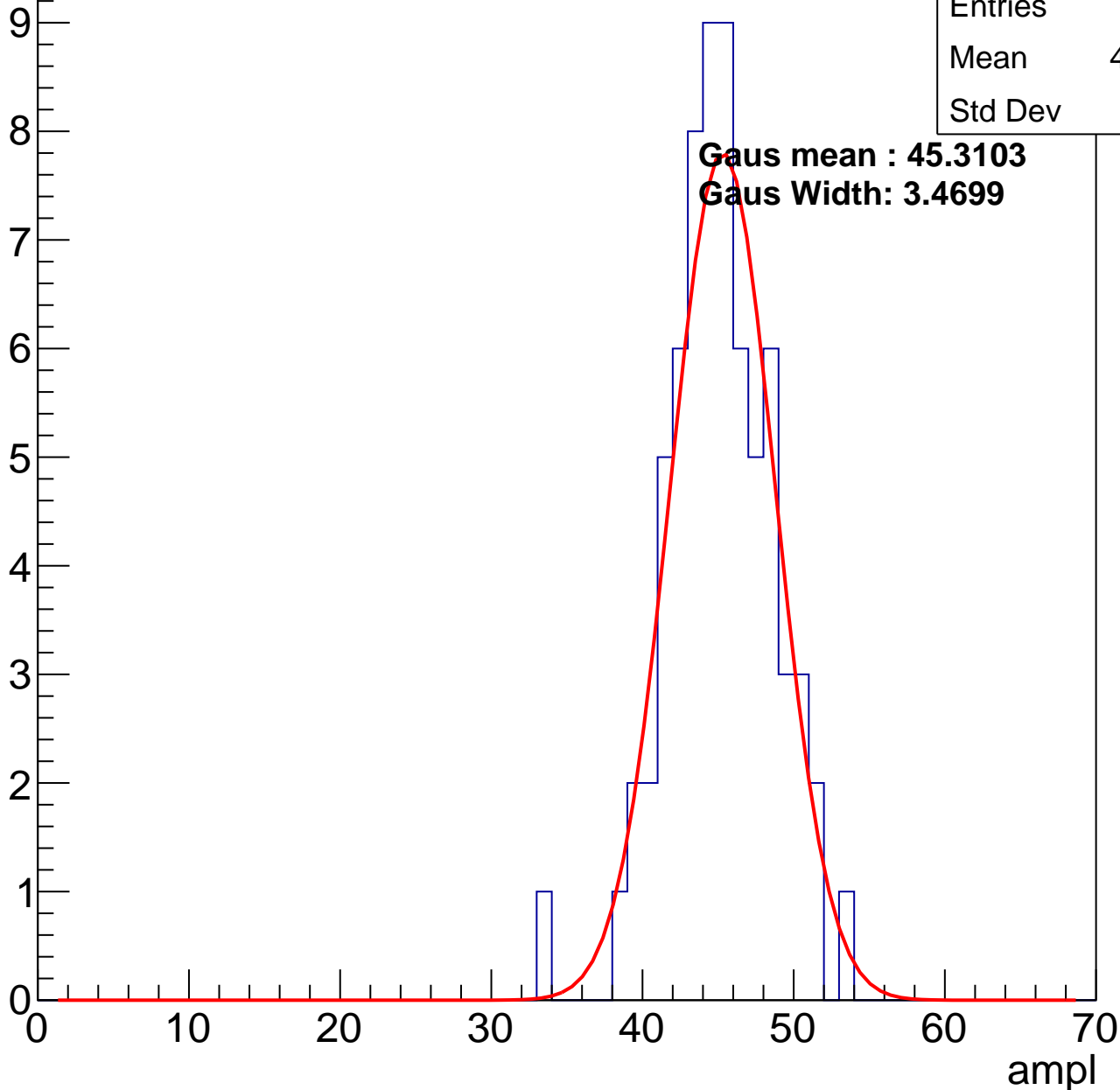
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	44.67
Std Dev	3.45

Gaus mean : 45.3103

Gaus Width: 3.4699

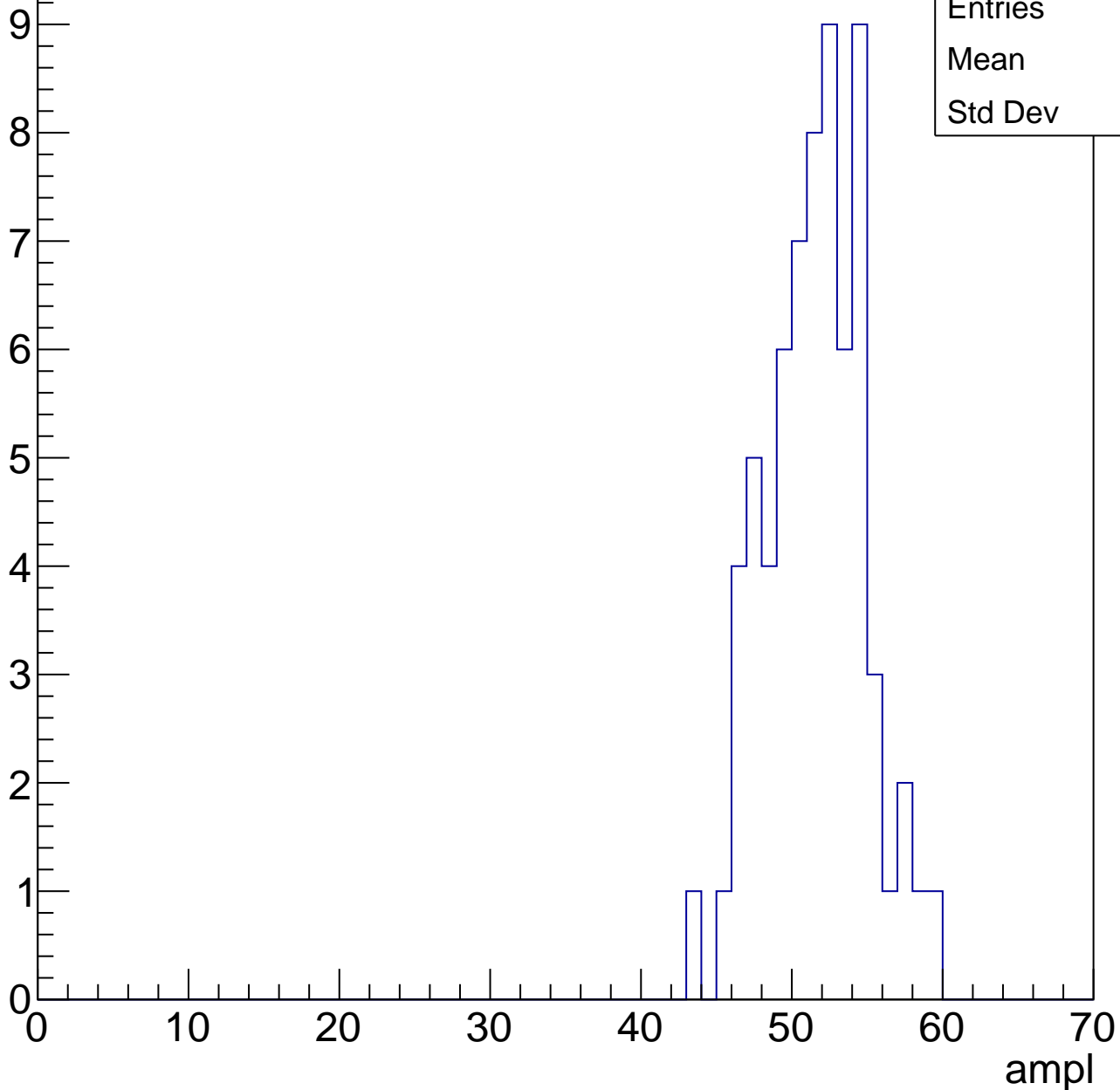


B1L102S, U13-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	51.1
Std Dev	3.25

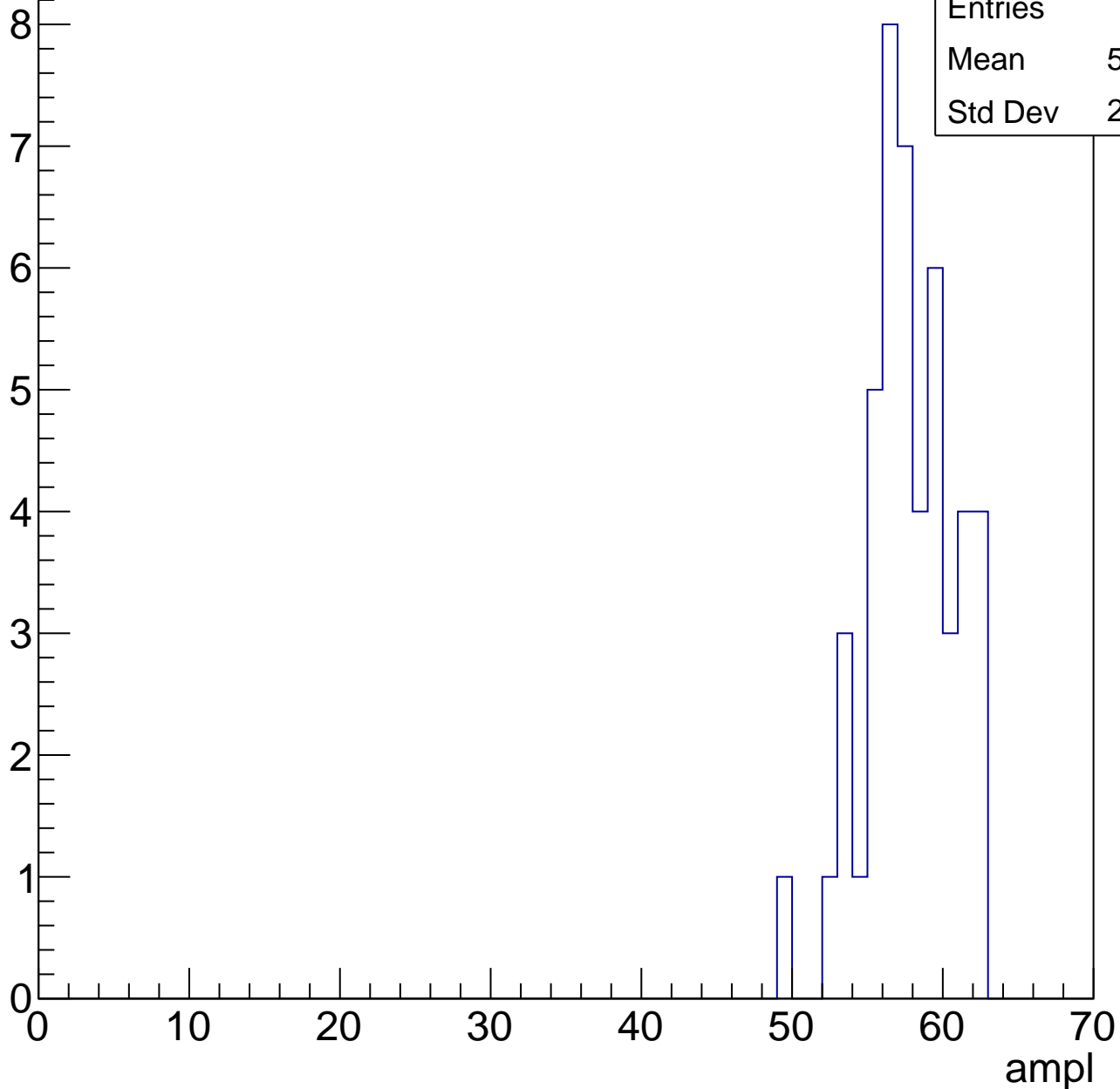


B1L102S, U13-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.32
Std Dev	2.874

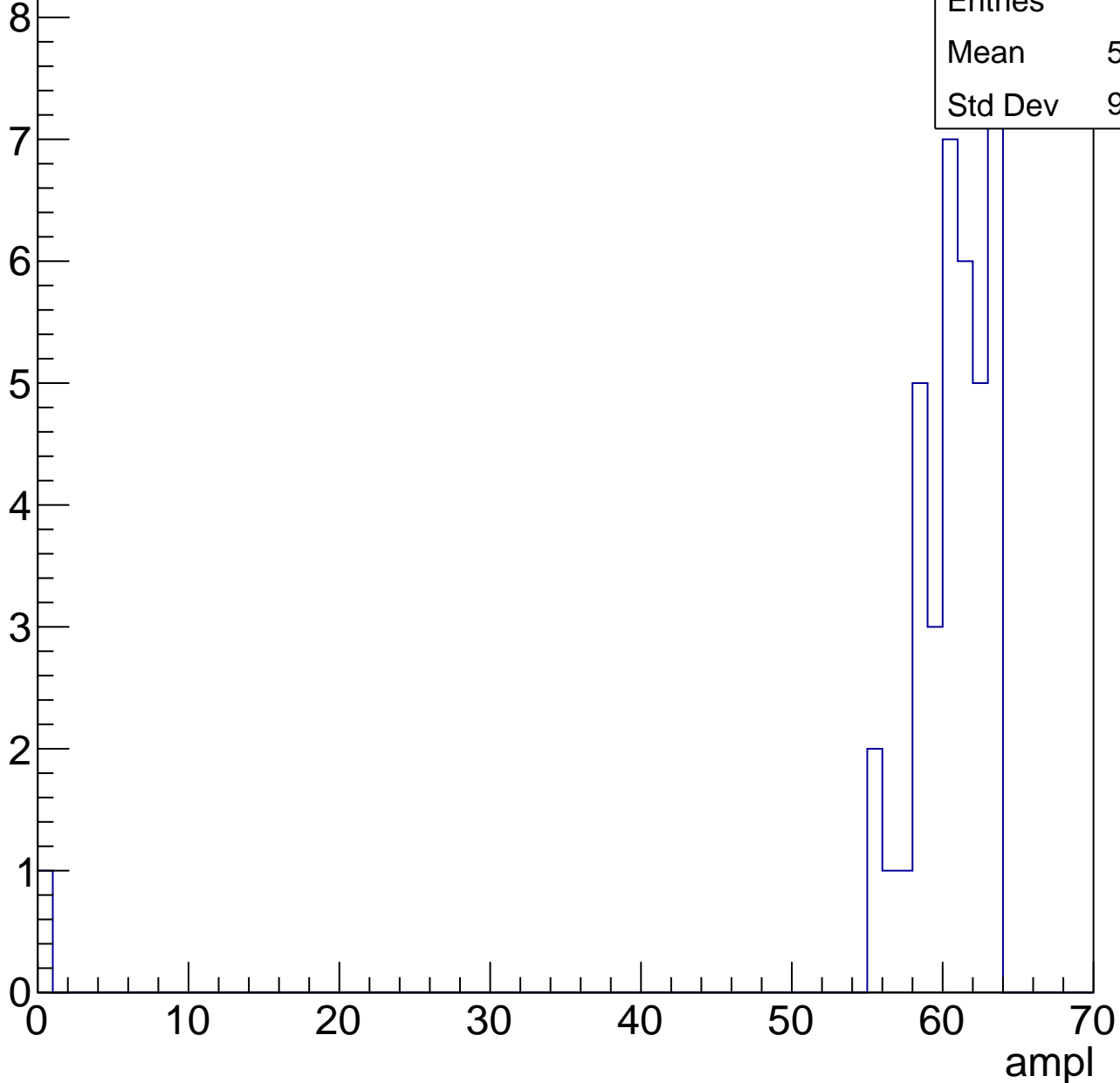


B1L102S, U13-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.72
Std Dev	9.782



B1L102S, U13-ch109, adc6

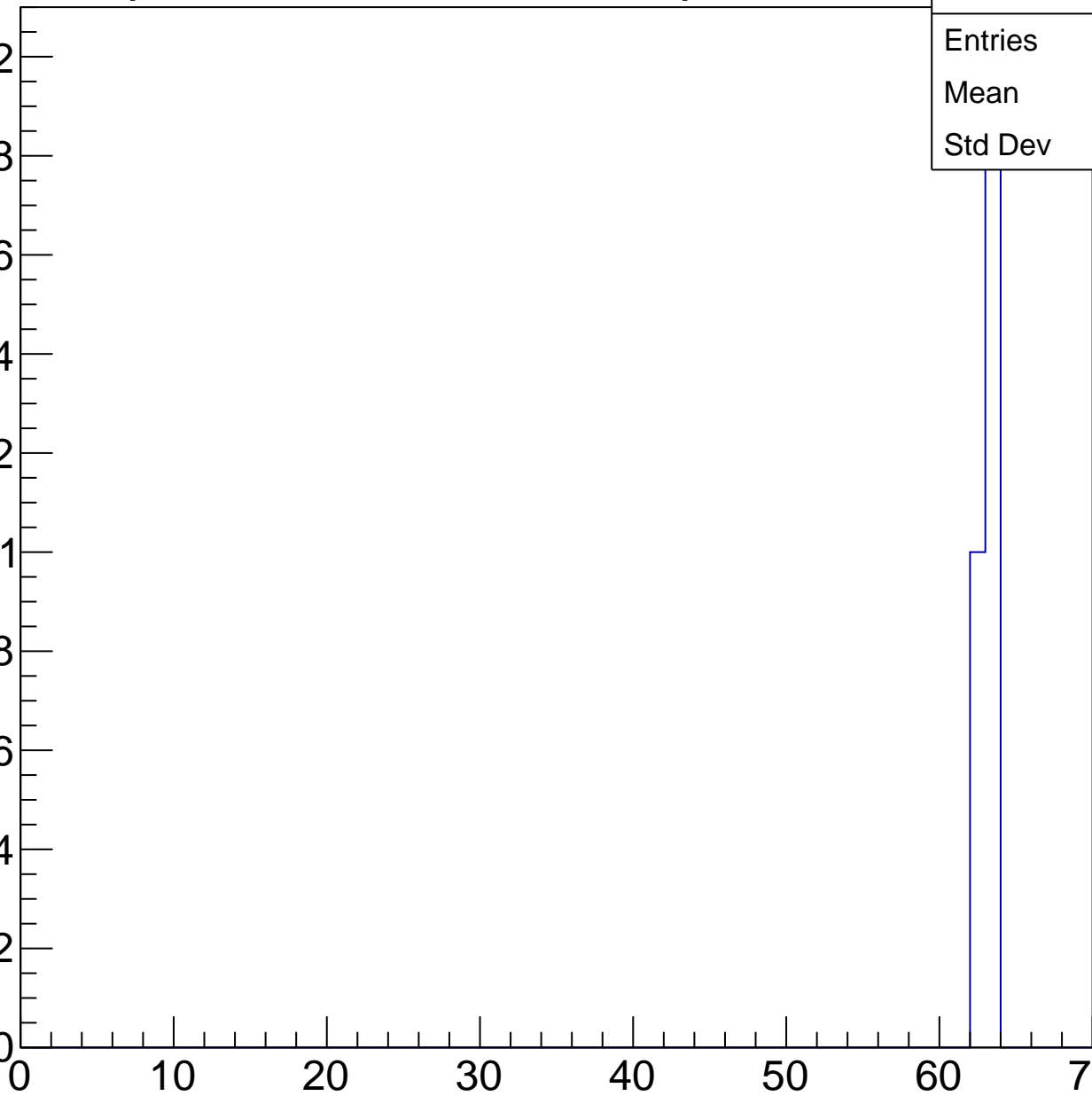
calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L102S, U13-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch110, adc0

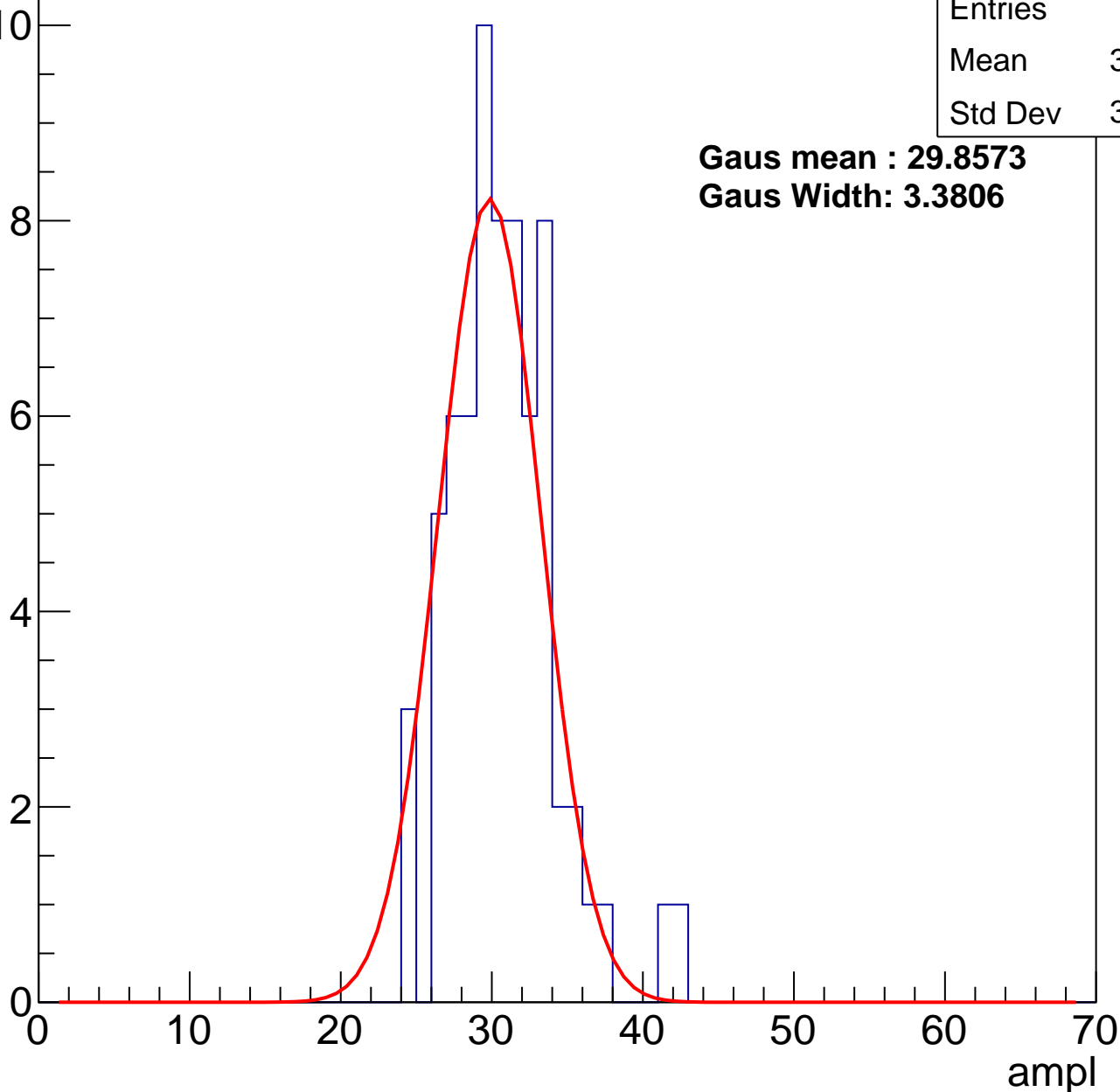
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	30.29
Std Dev	3.443

Gaus mean : 29.8573

Gaus Width: 3.3806



B1L102S, U13-ch110, adc1

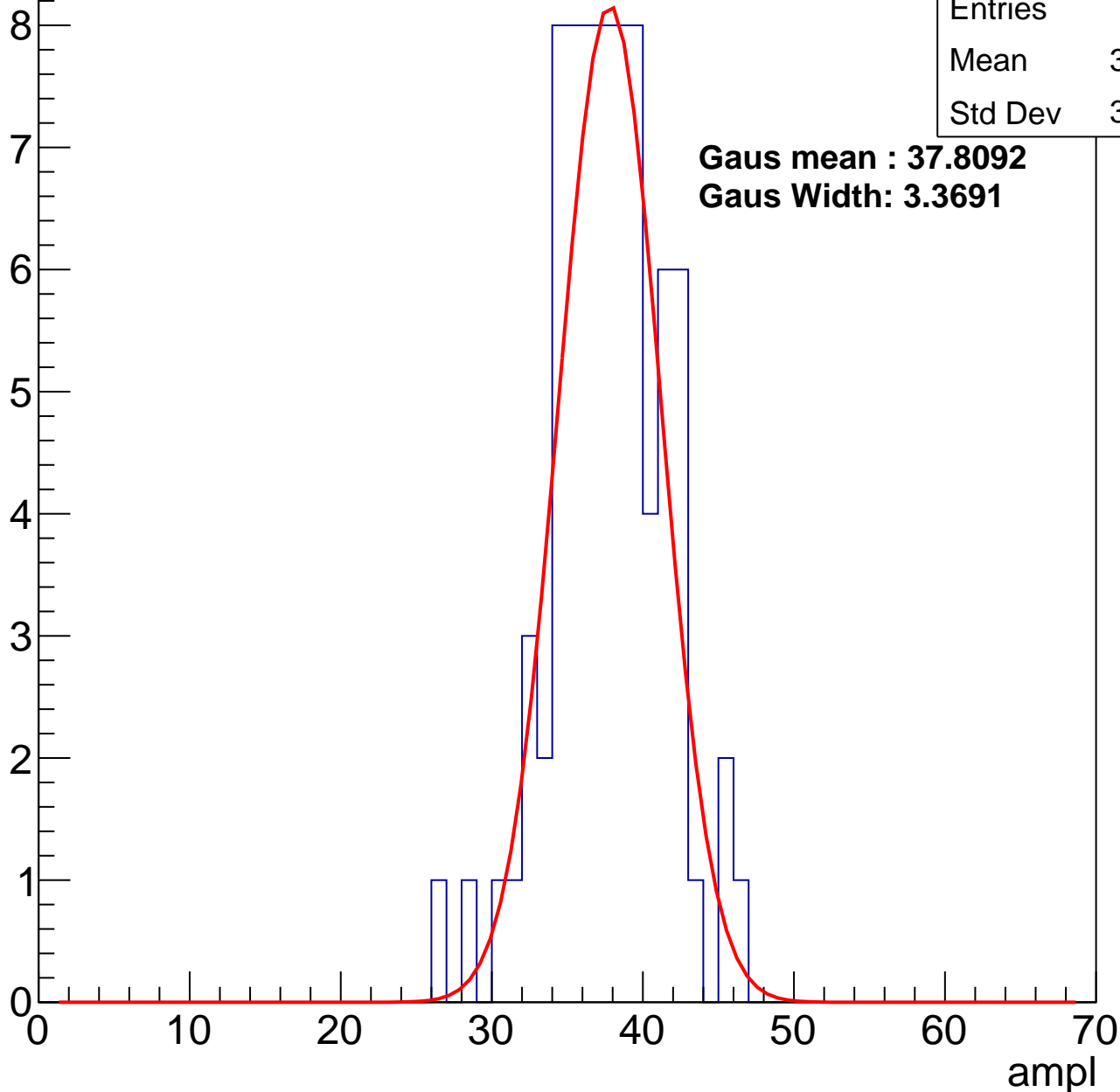
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	37.22
Std Dev	3.726

Gaus mean : 37.8092

Gaus Width: 3.3691



B1L102S, U13-ch110, adc2

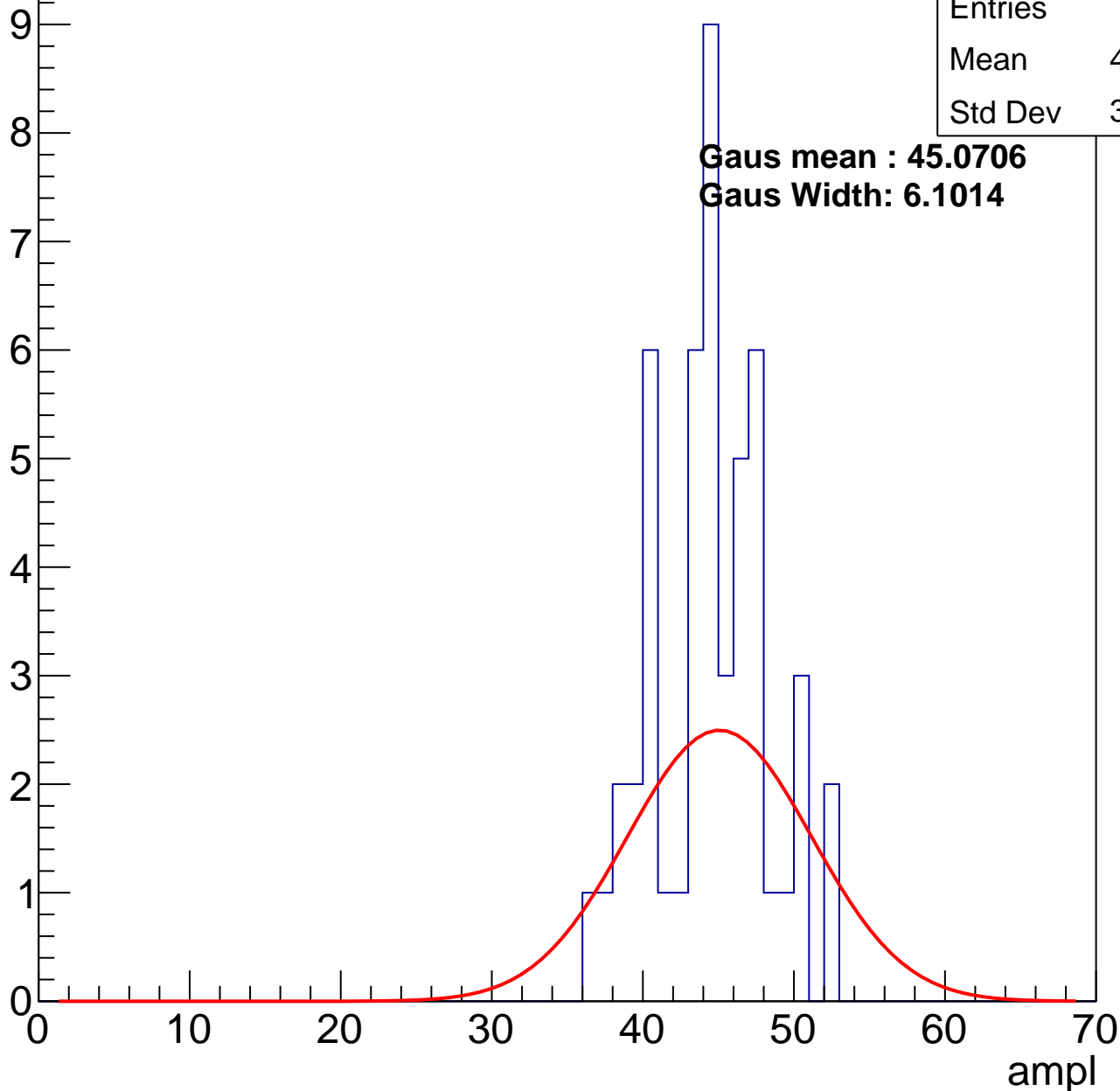
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	44.04
Std Dev	3.752

Gaus mean : 45.0706

Gaus Width: 6.1014

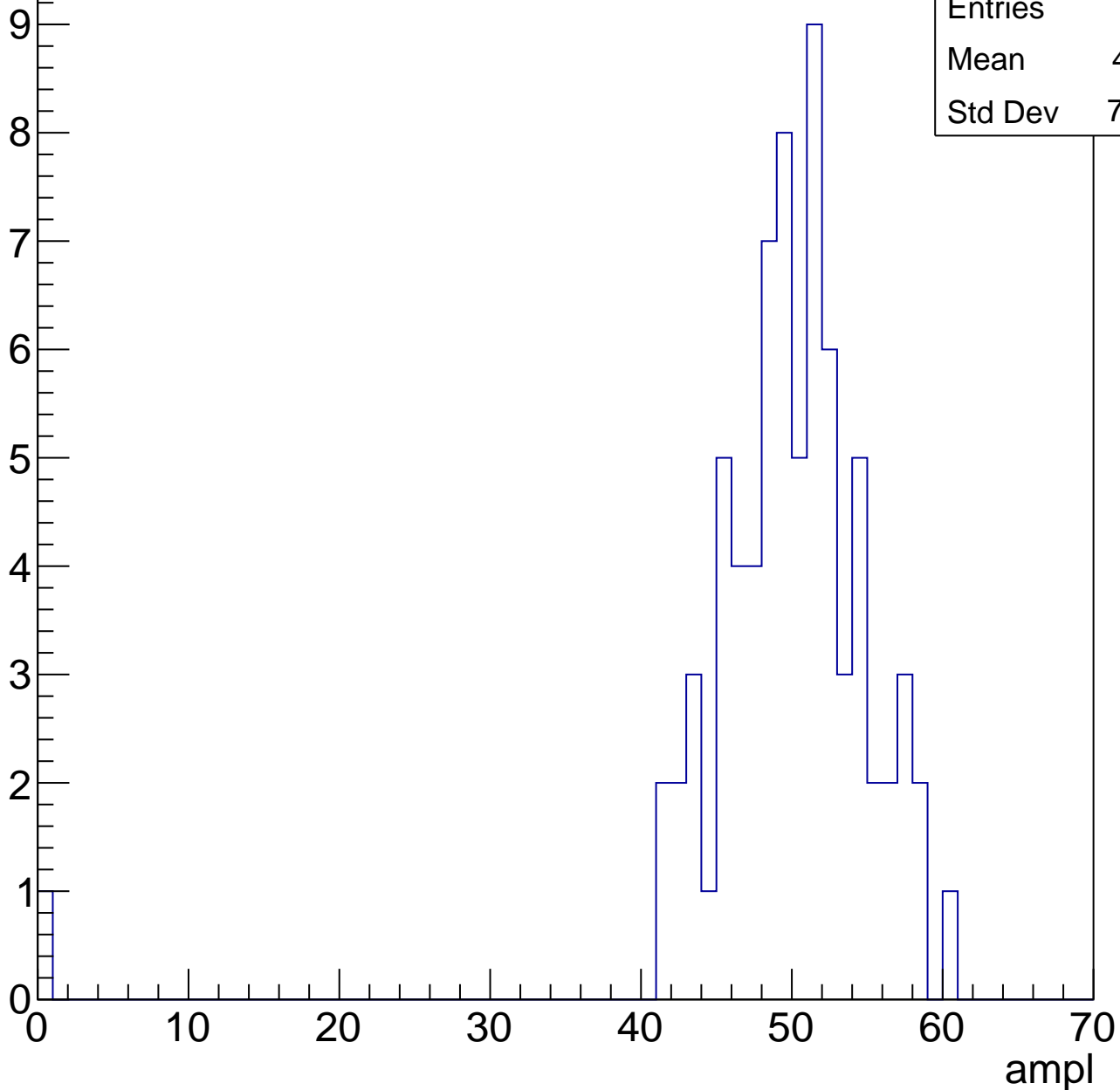


B1L102S, U13-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	49.11
Std Dev	7.128

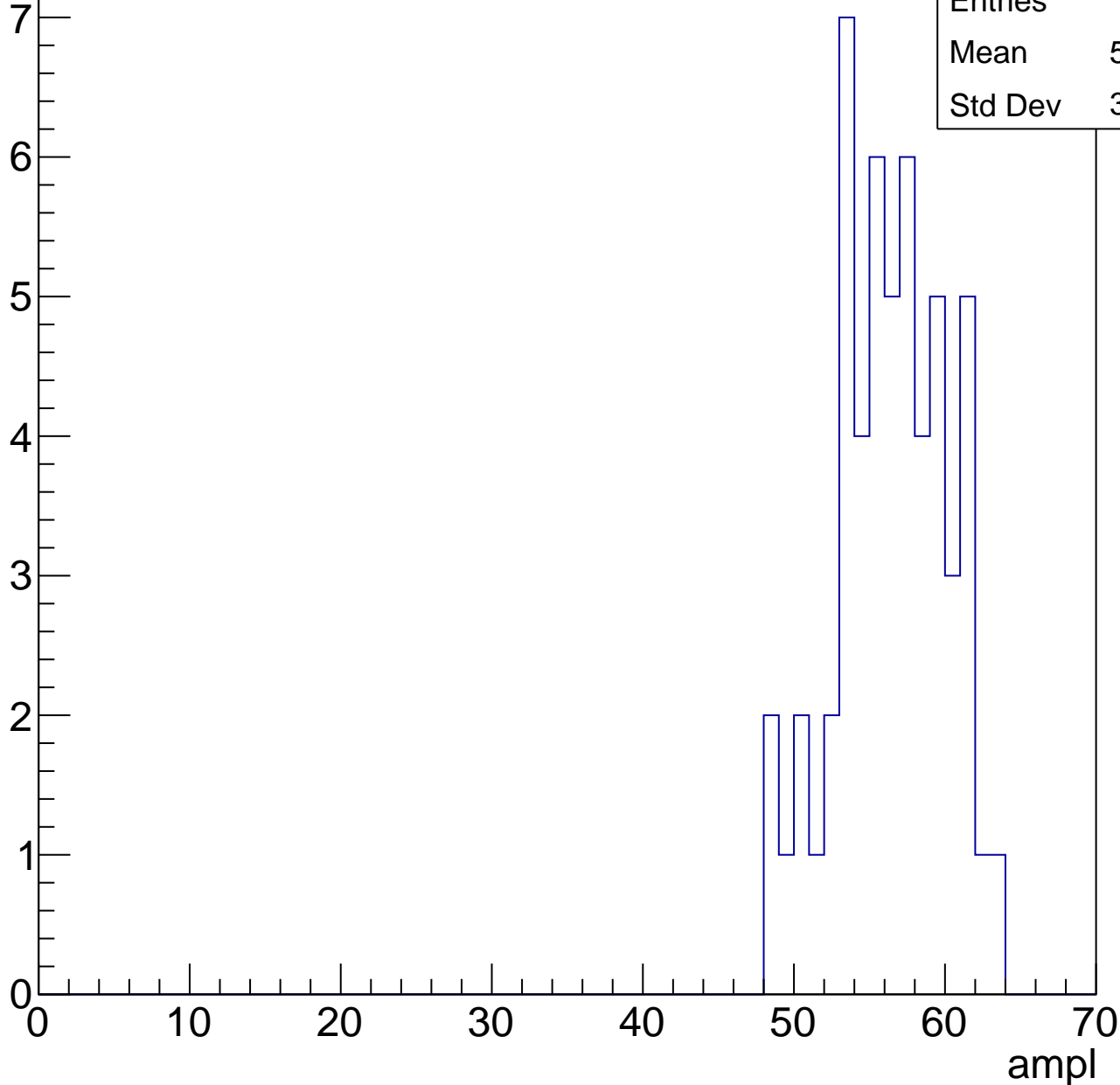


B1L102S, U13-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.93
Std Dev	3.607

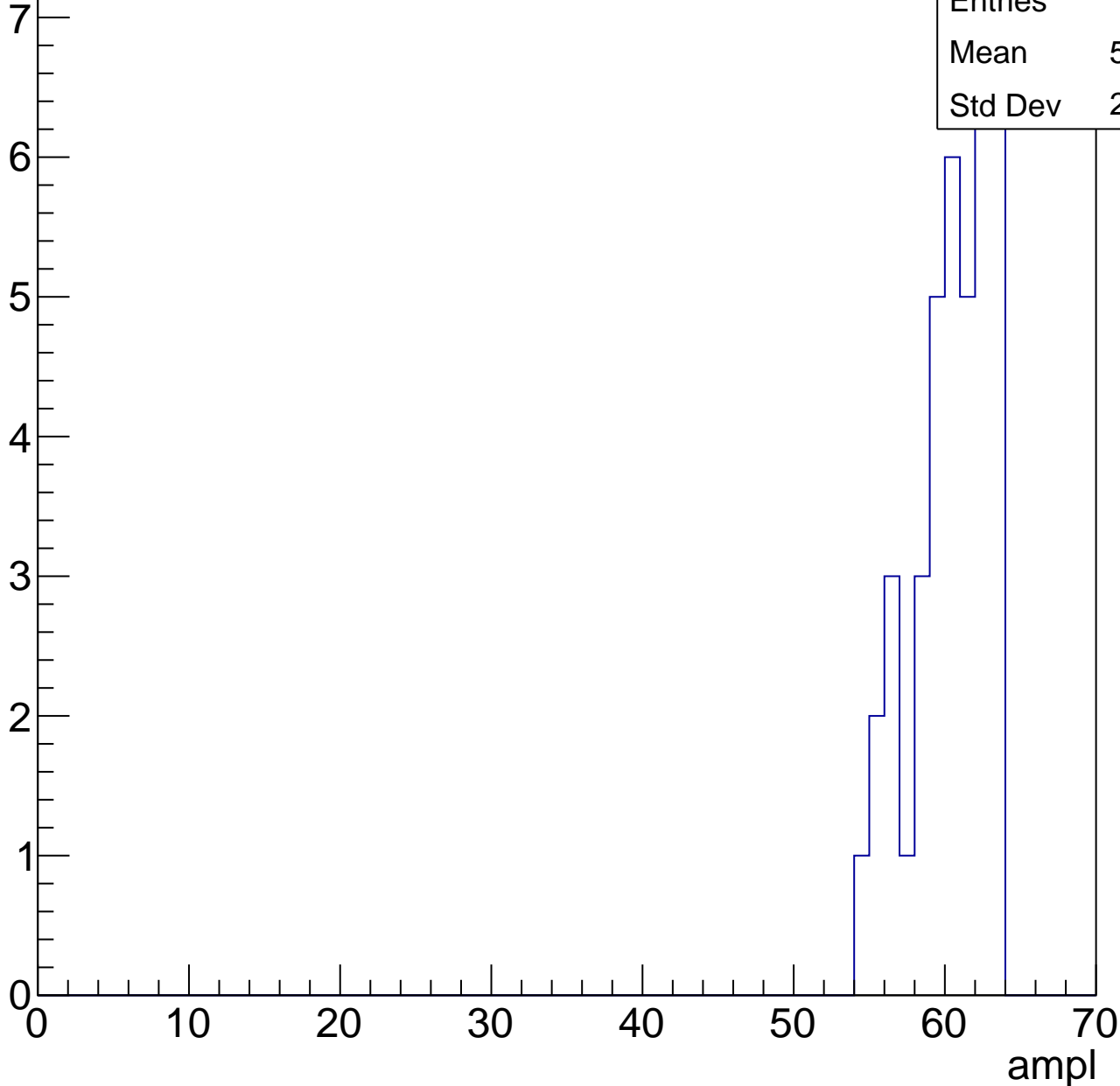


B1L102S, U13-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

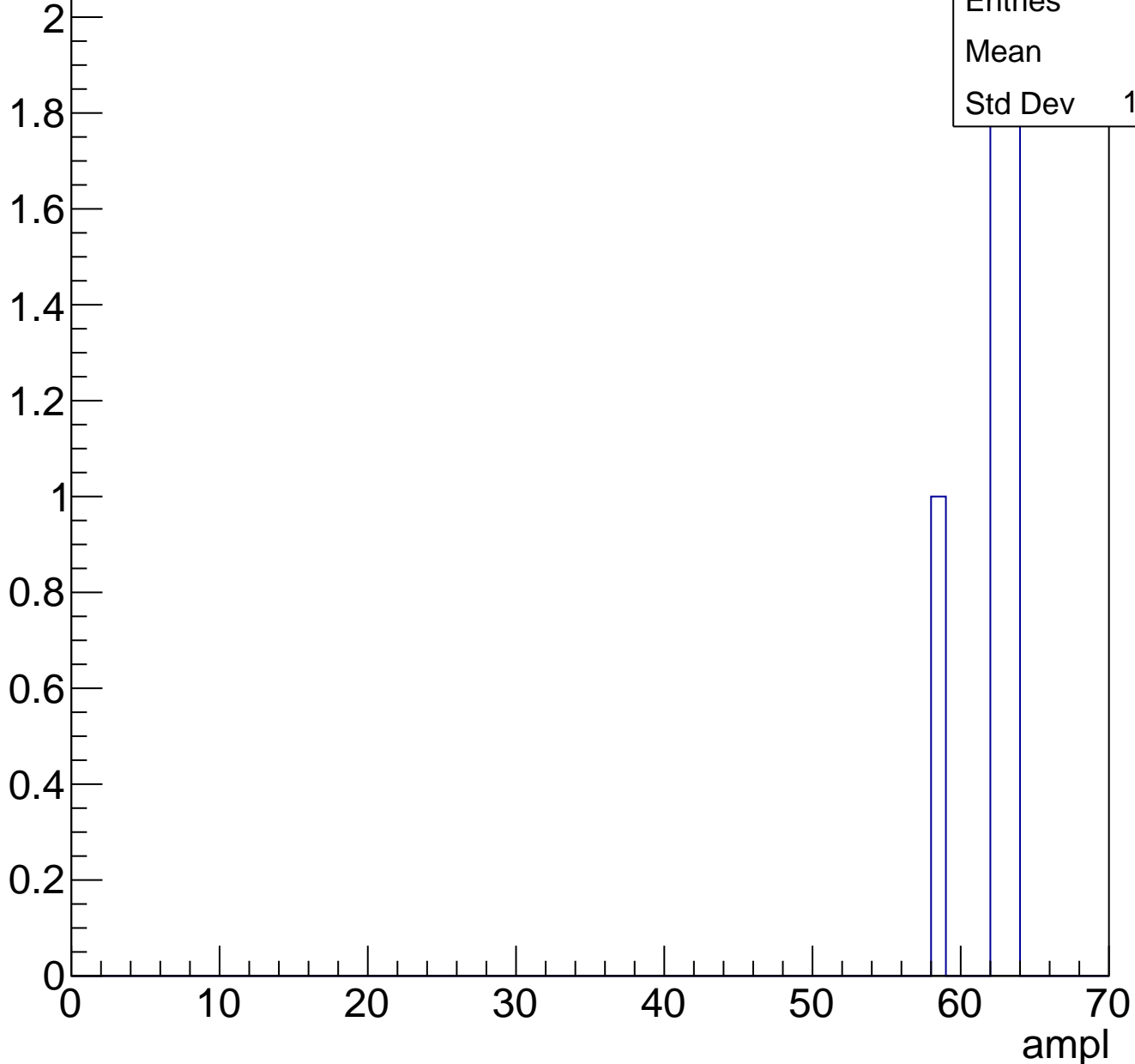
Entries	40
Mean	59.95
Std Dev	2.529



B1L102S, U13-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch111, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	28.36
Std Dev	5.948

Gaus mean : 29.8826

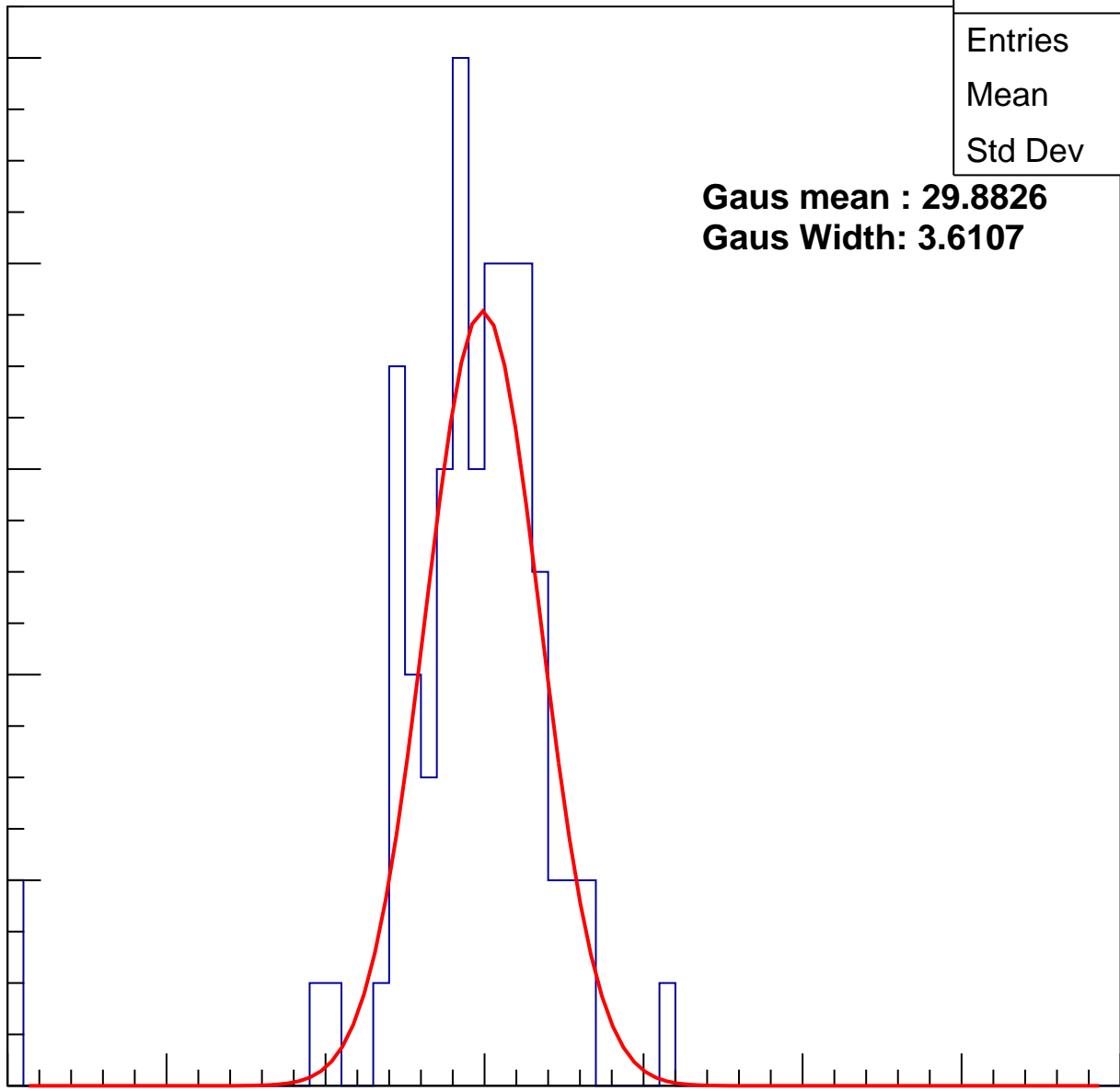
Gaus Width: 3.6107

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch111, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	35.9
Std Dev	5.604

Gaus mean : 37.1951

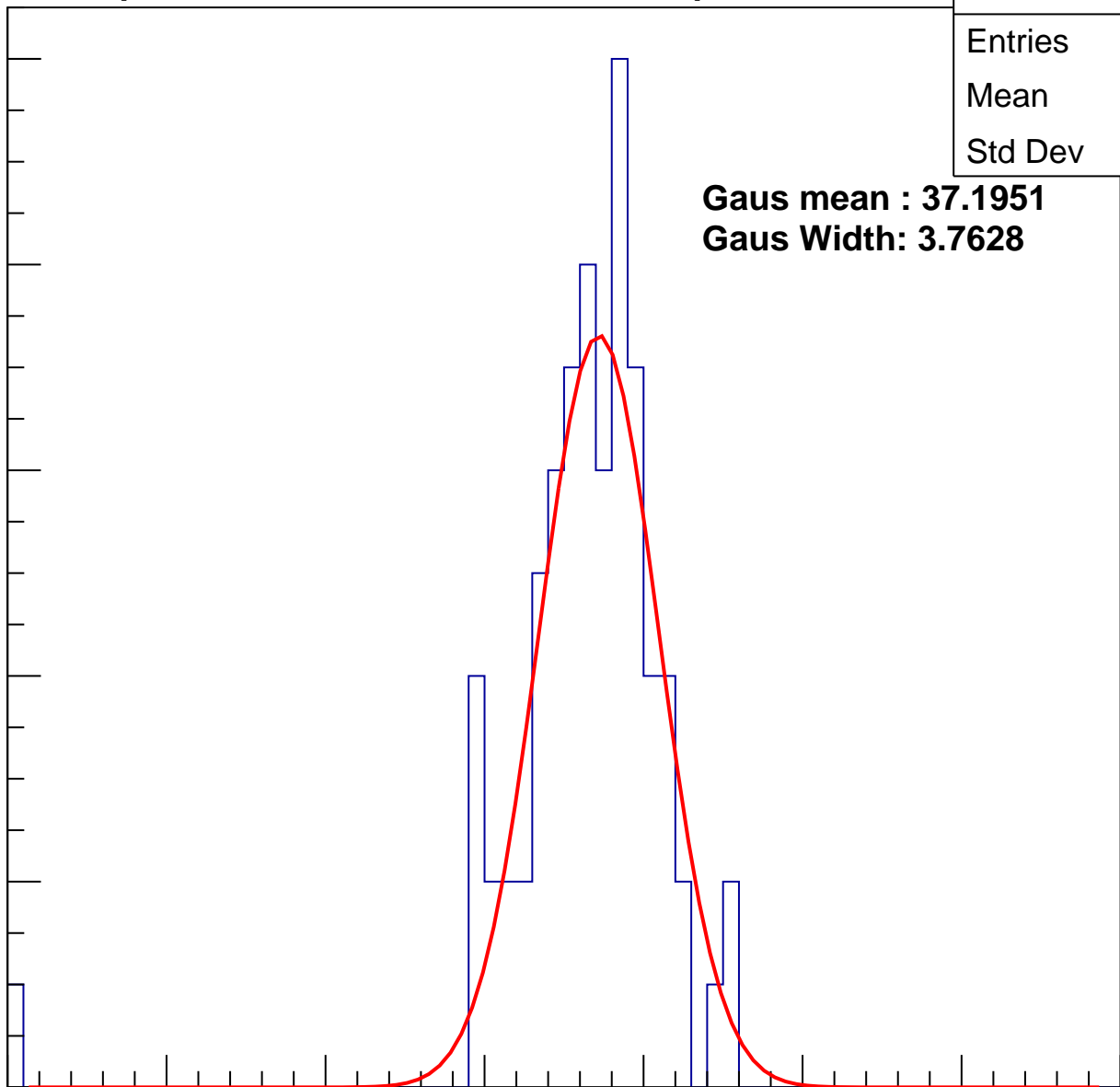
Gaus Width: 3.7628

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch111, adc2

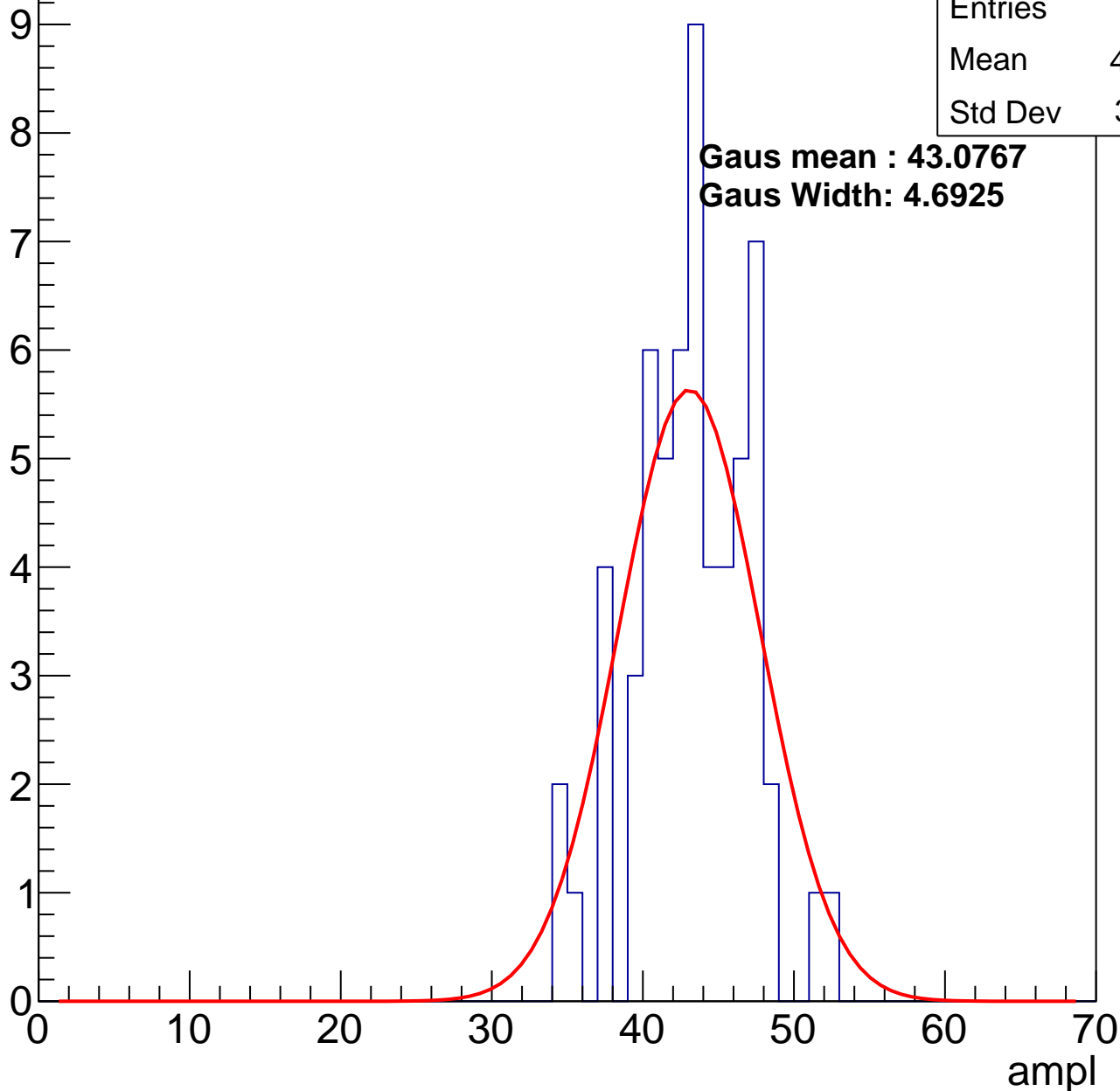
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.77
Std Dev	3.801

Gaus mean : 43.0767

Gaus Width: 4.6925

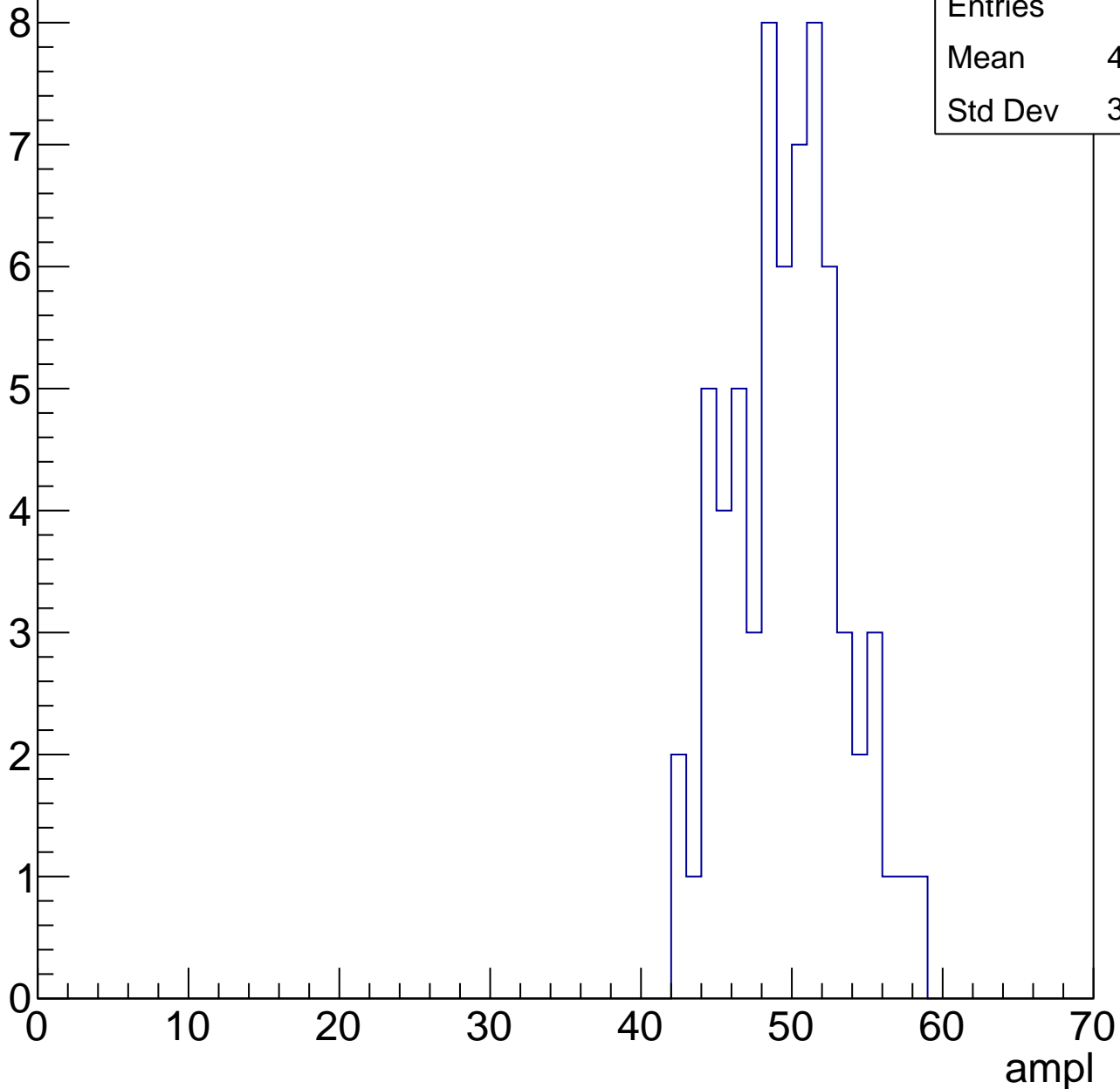


B1L102S, U13-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.23
Std Dev	3.646

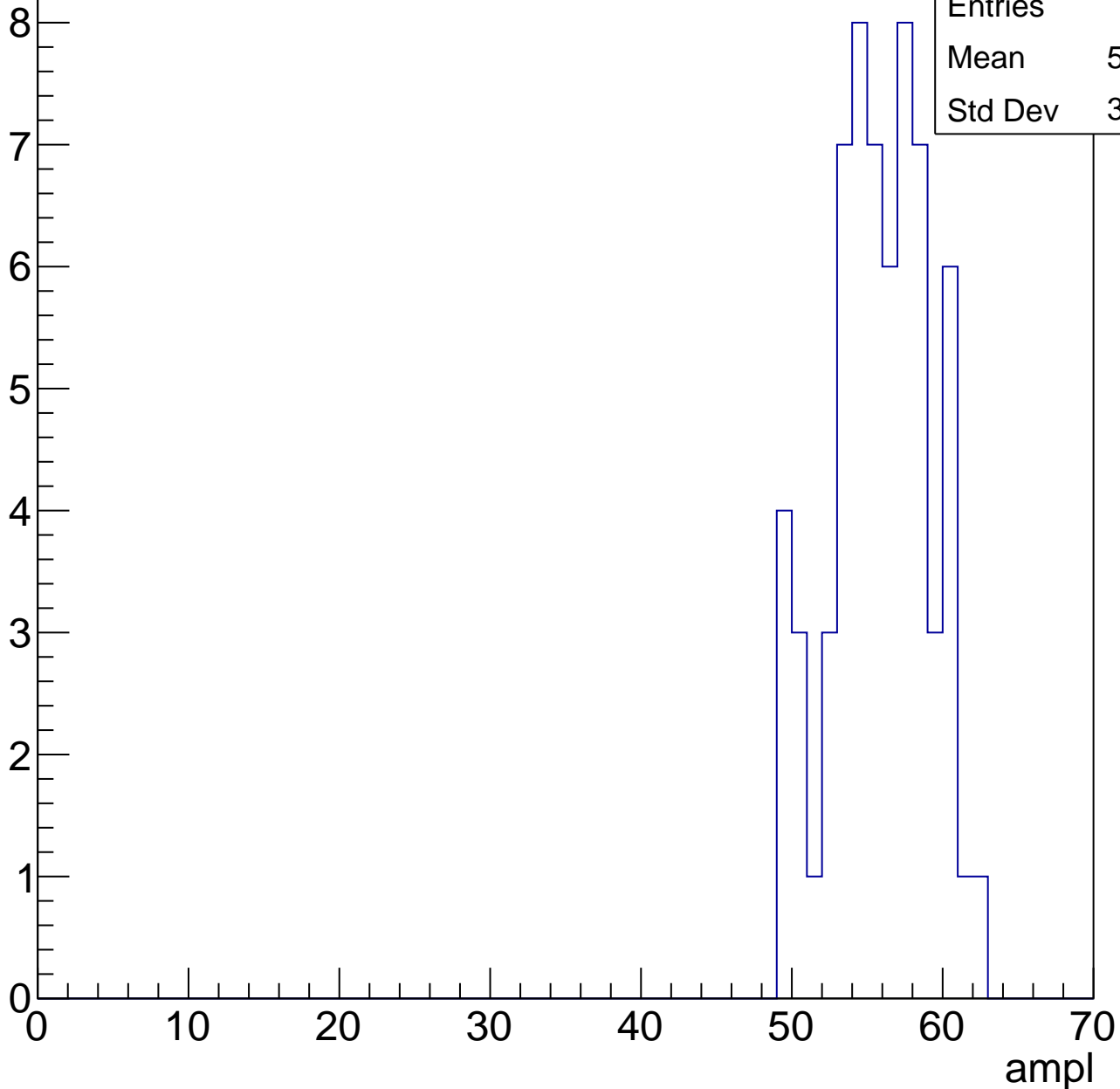


B1L102S, U13-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.37
Std Dev	3.218

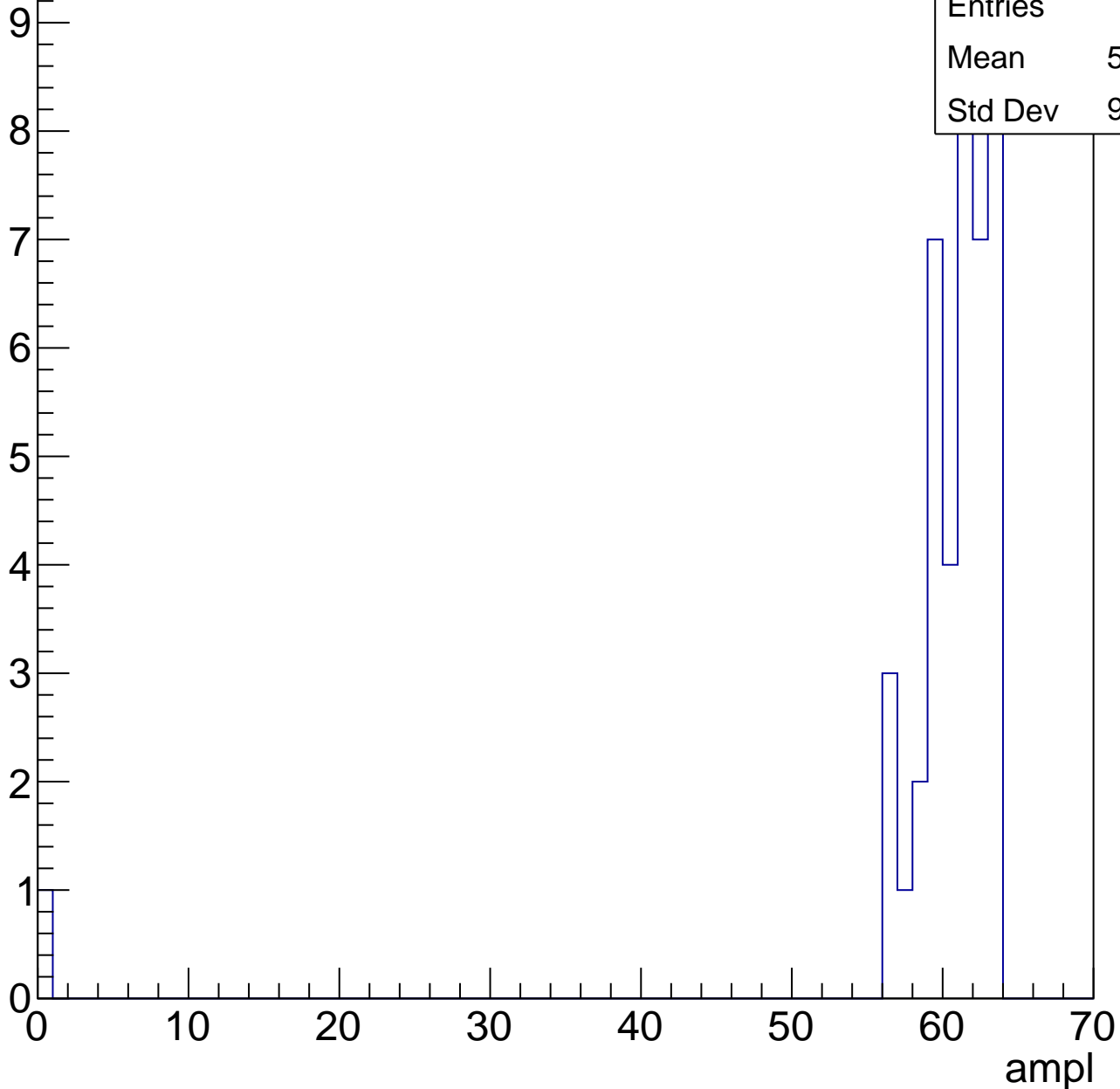


B1L102S, U13-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	59.12
Std Dev	9.457



B1L102S, U13-ch111, adc6

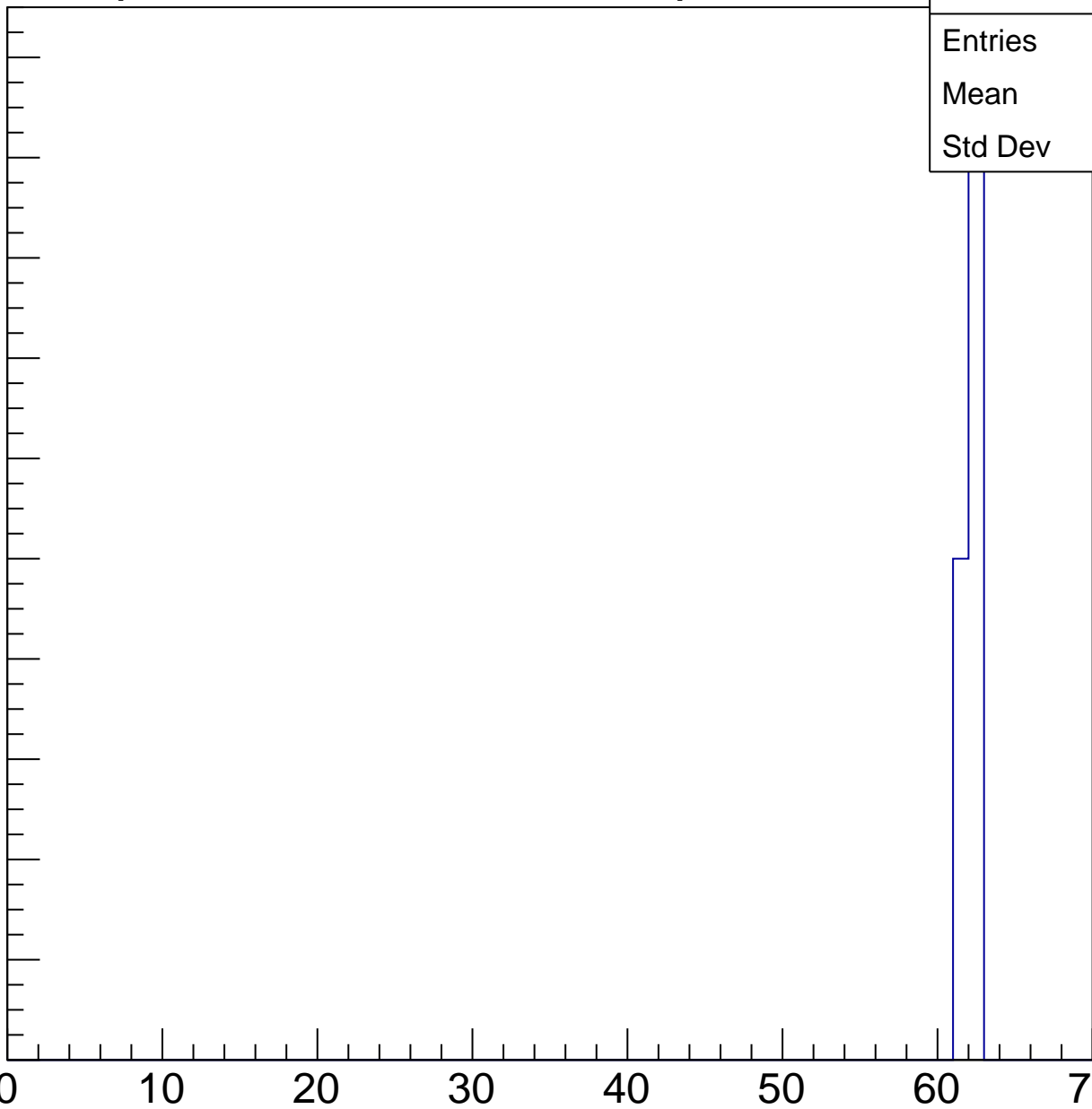
calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl



B1L102S, U13-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch112, adc0

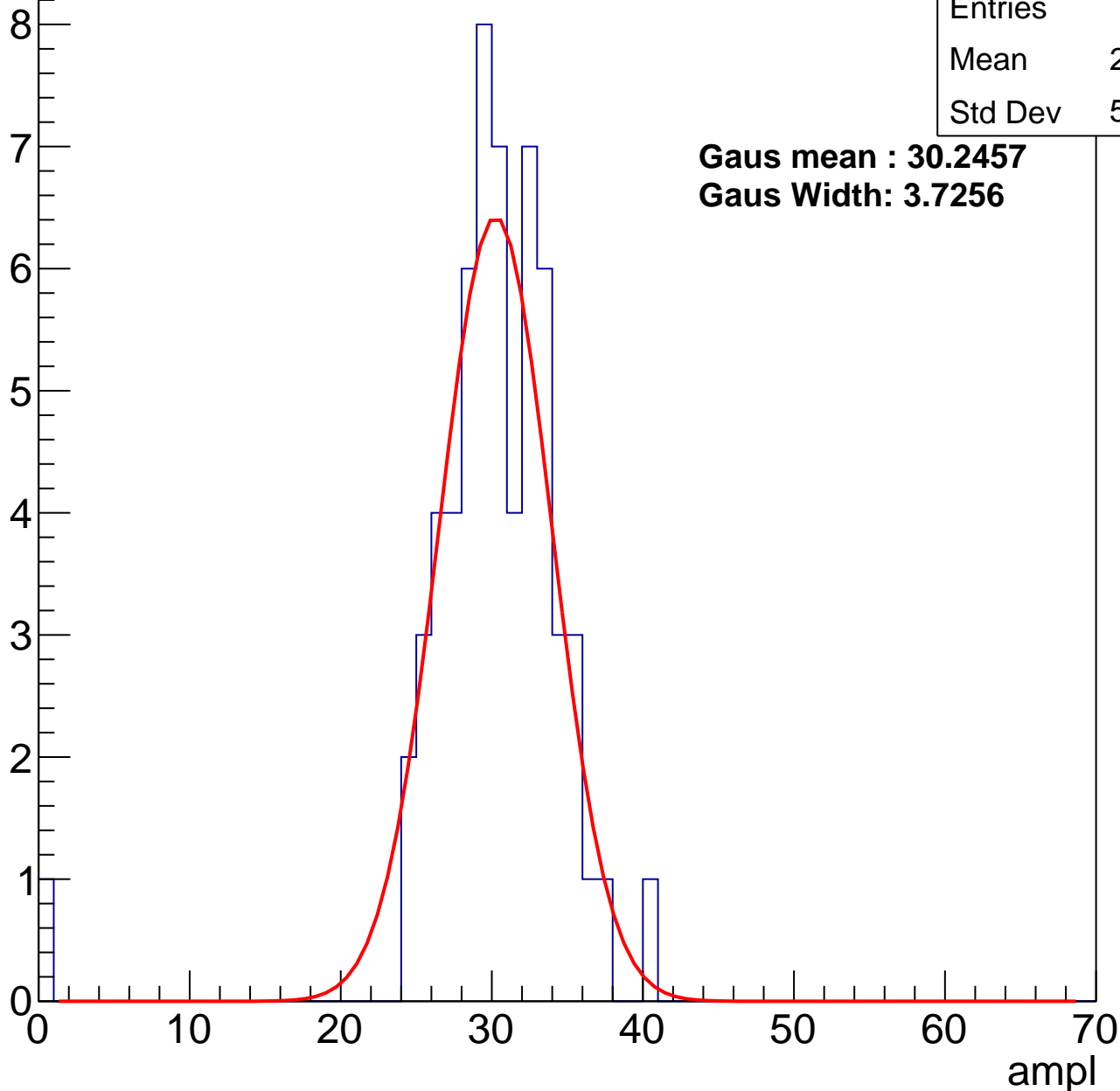
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	29.69
Std Dev	5.069

Gaus mean : 30.2457

Gaus Width: 3.7256



B1L102S, U13-ch112, adc1

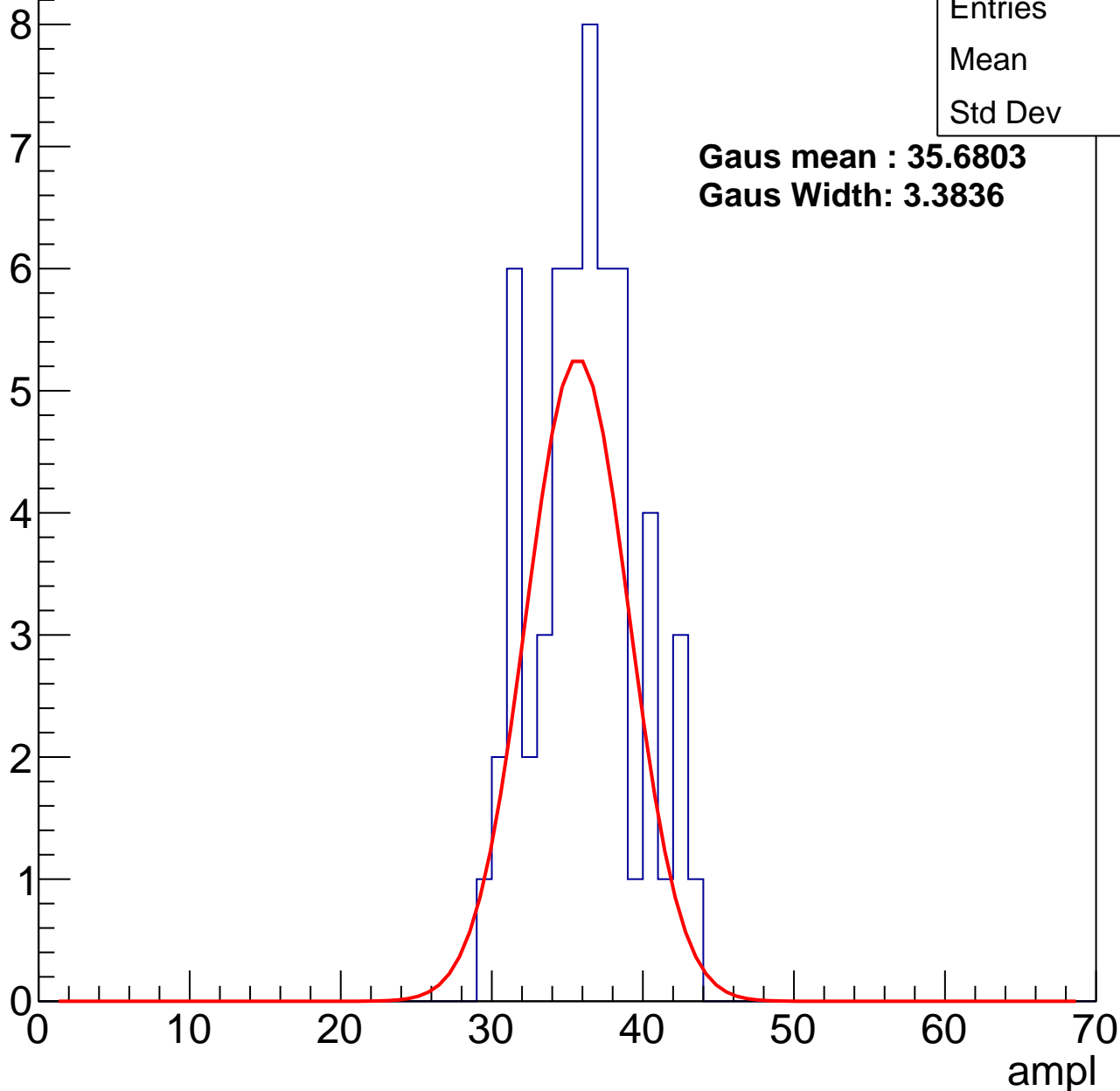
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.7
Std Dev	3.38

Gaus mean : 35.6803

Gaus Width: 3.3836



B1L102S, U13-ch112, adc2

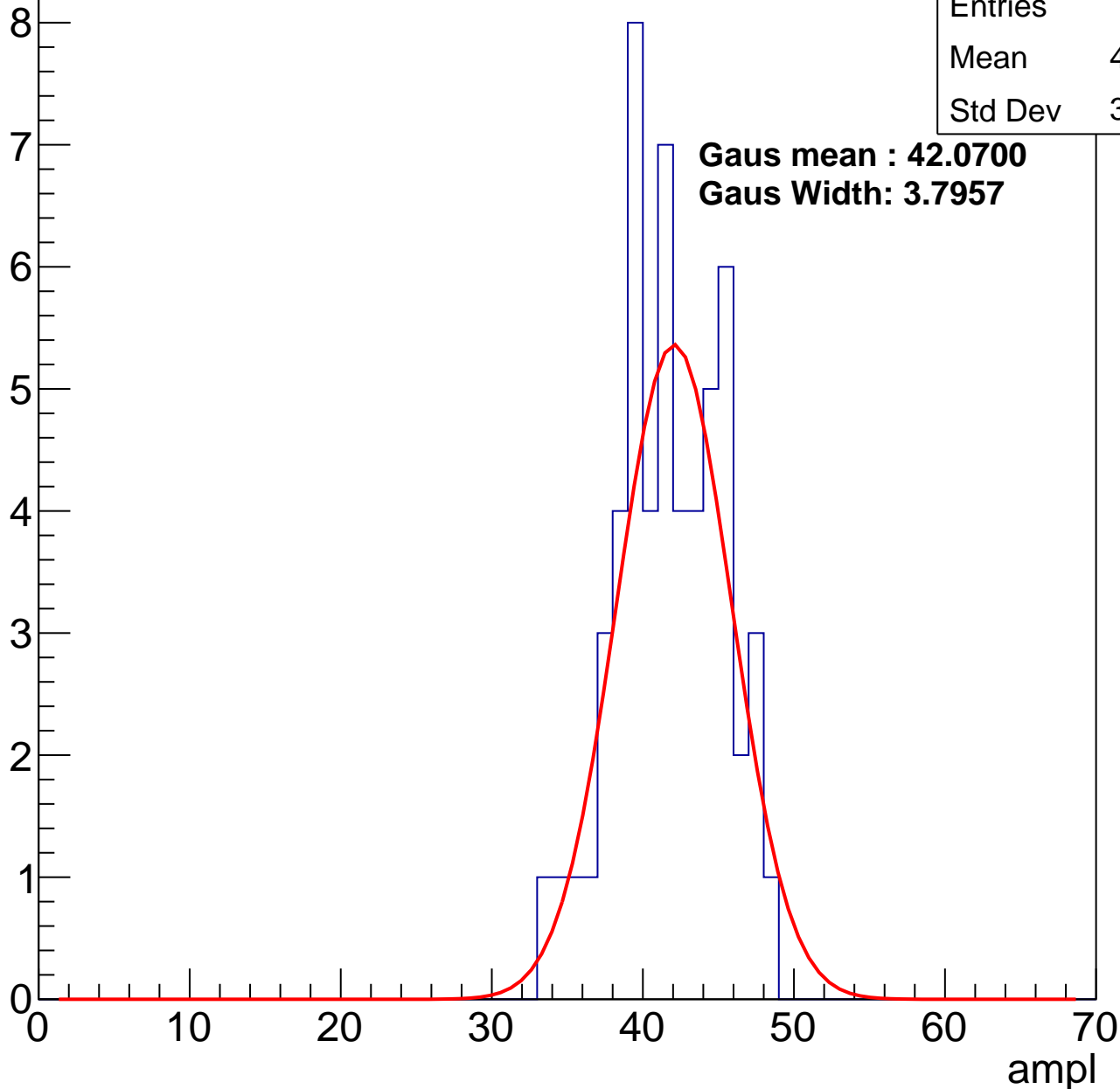
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.29
Std Dev	3.457

Gaus mean : 42.0700

Gaus Width: 3.7957

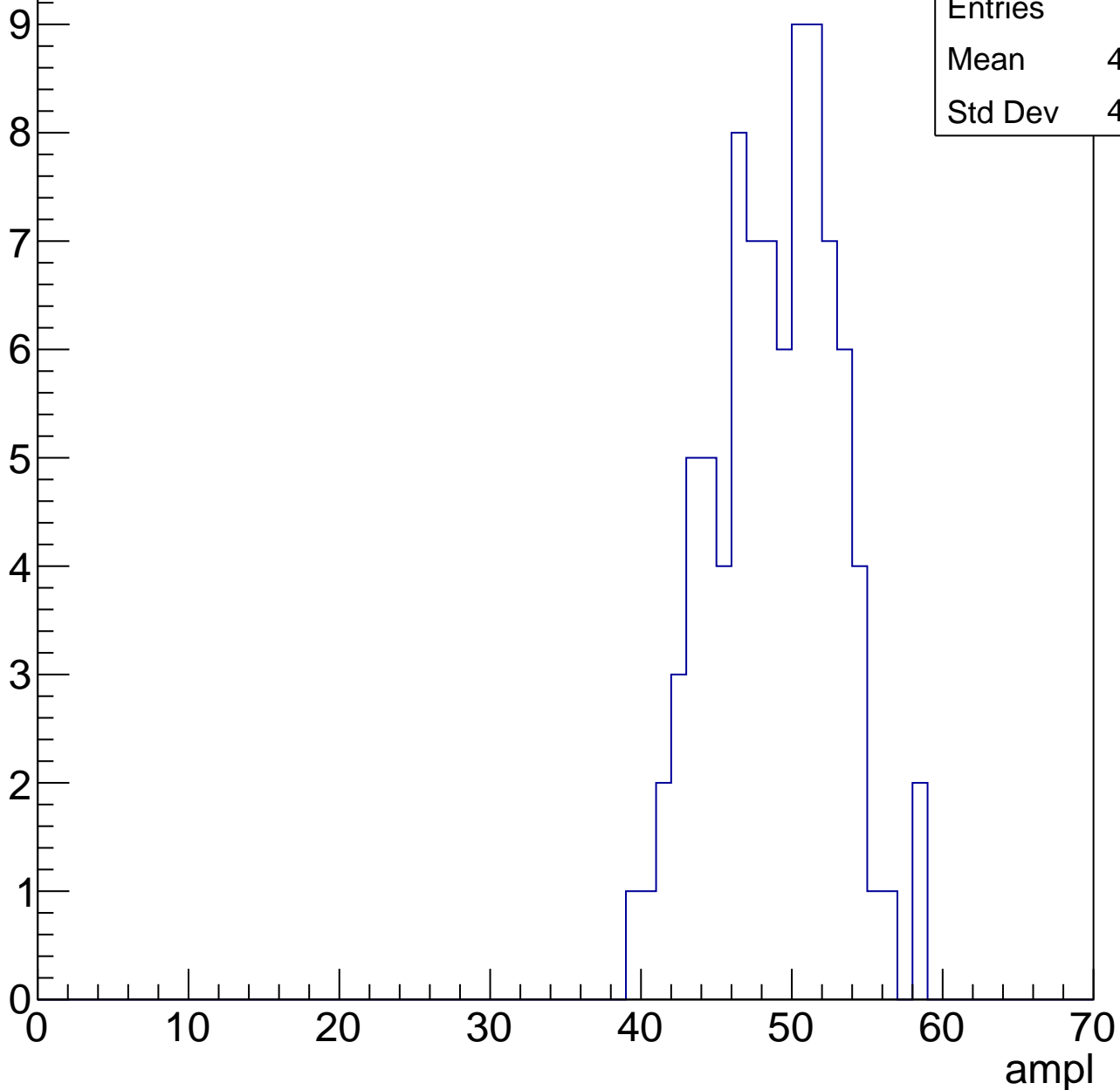


B1L102S, U13-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	48.44
Std Dev	4.067

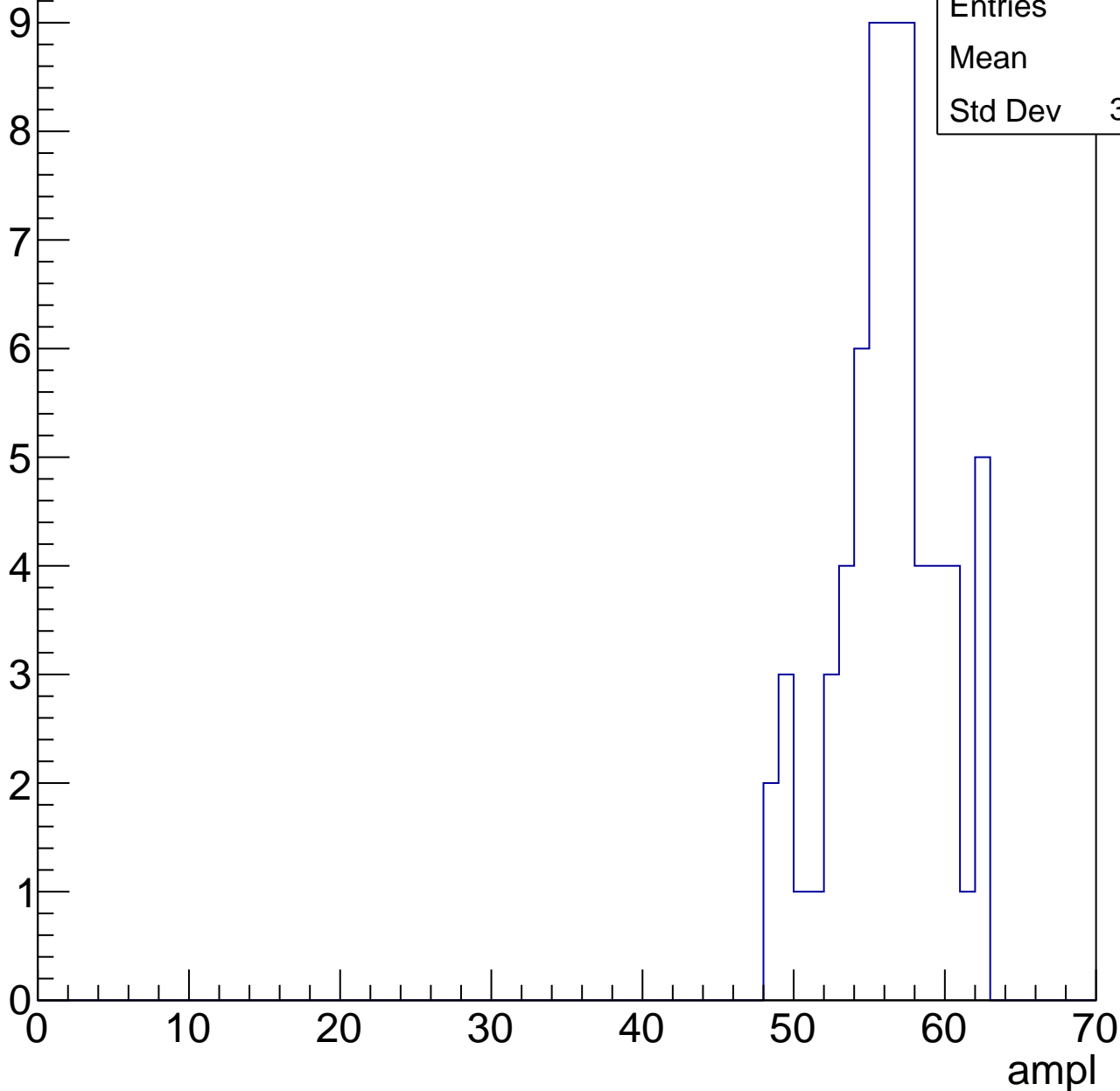


B1L102S, U13-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.8
Std Dev	3.465

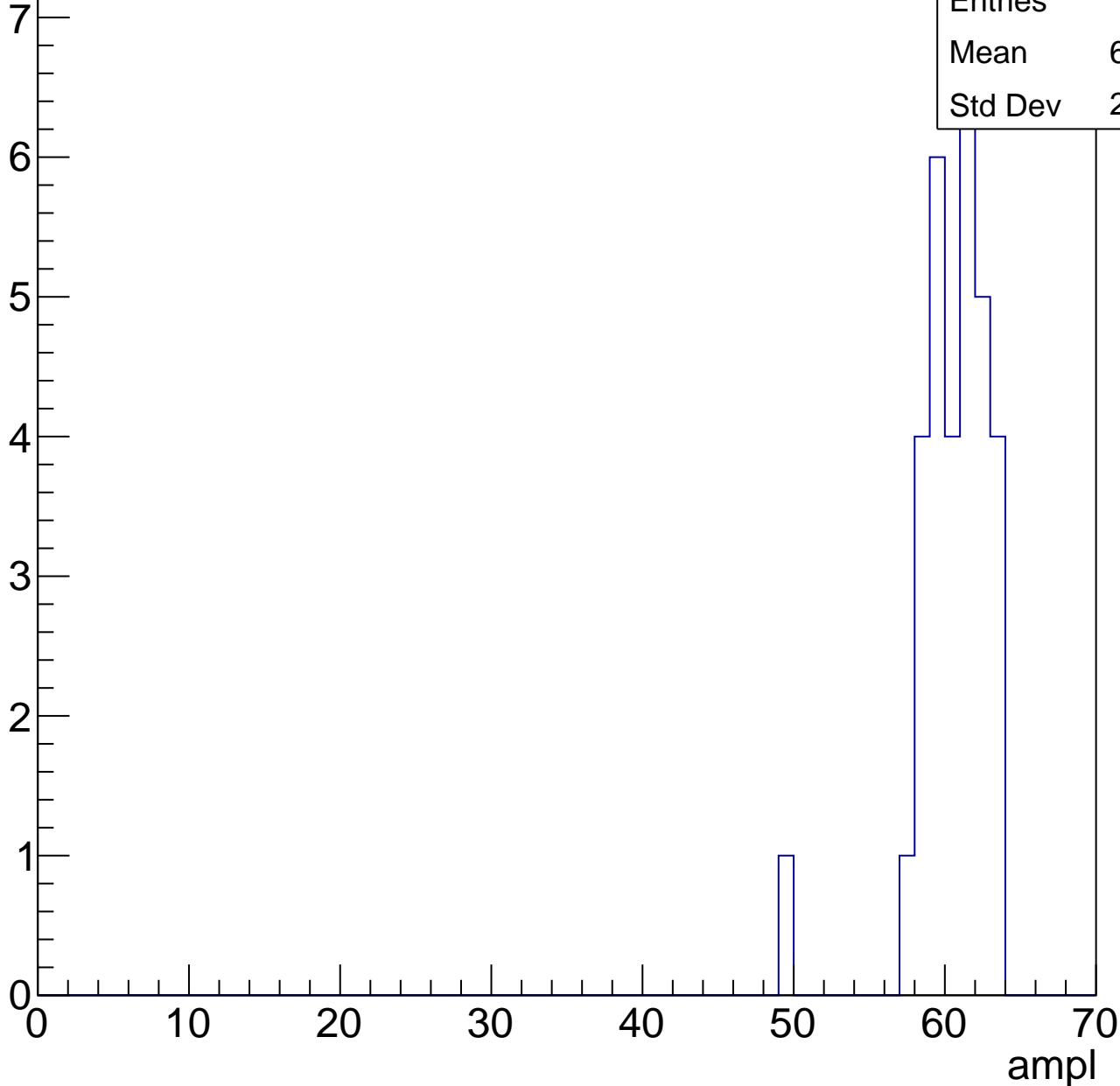


B1L102S, U13-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	60.03
Std Dev	2.592

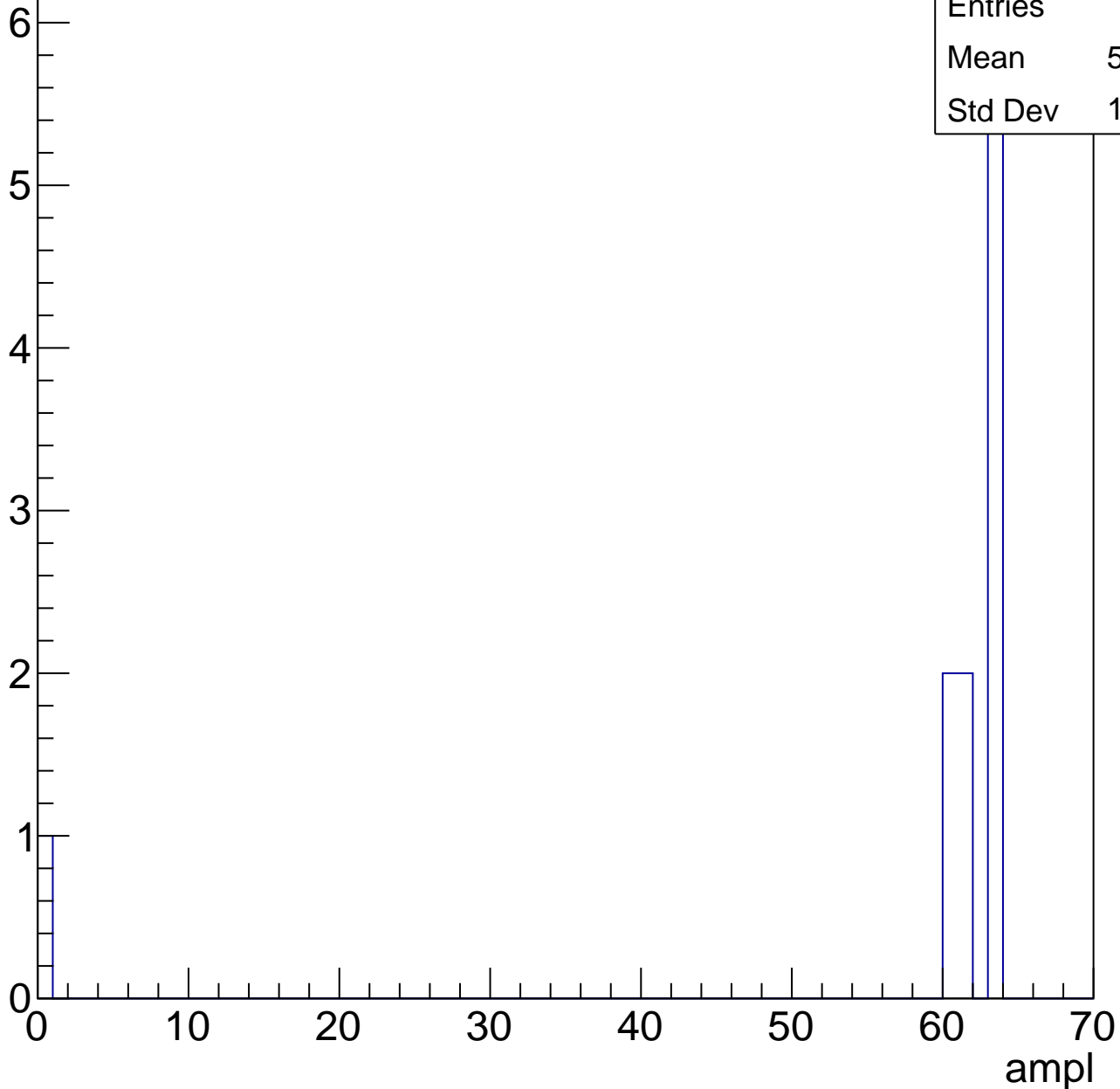


B1L102S, U13-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	56.36
Std Dev	17.86



B1L102S, U13-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch113, adc0

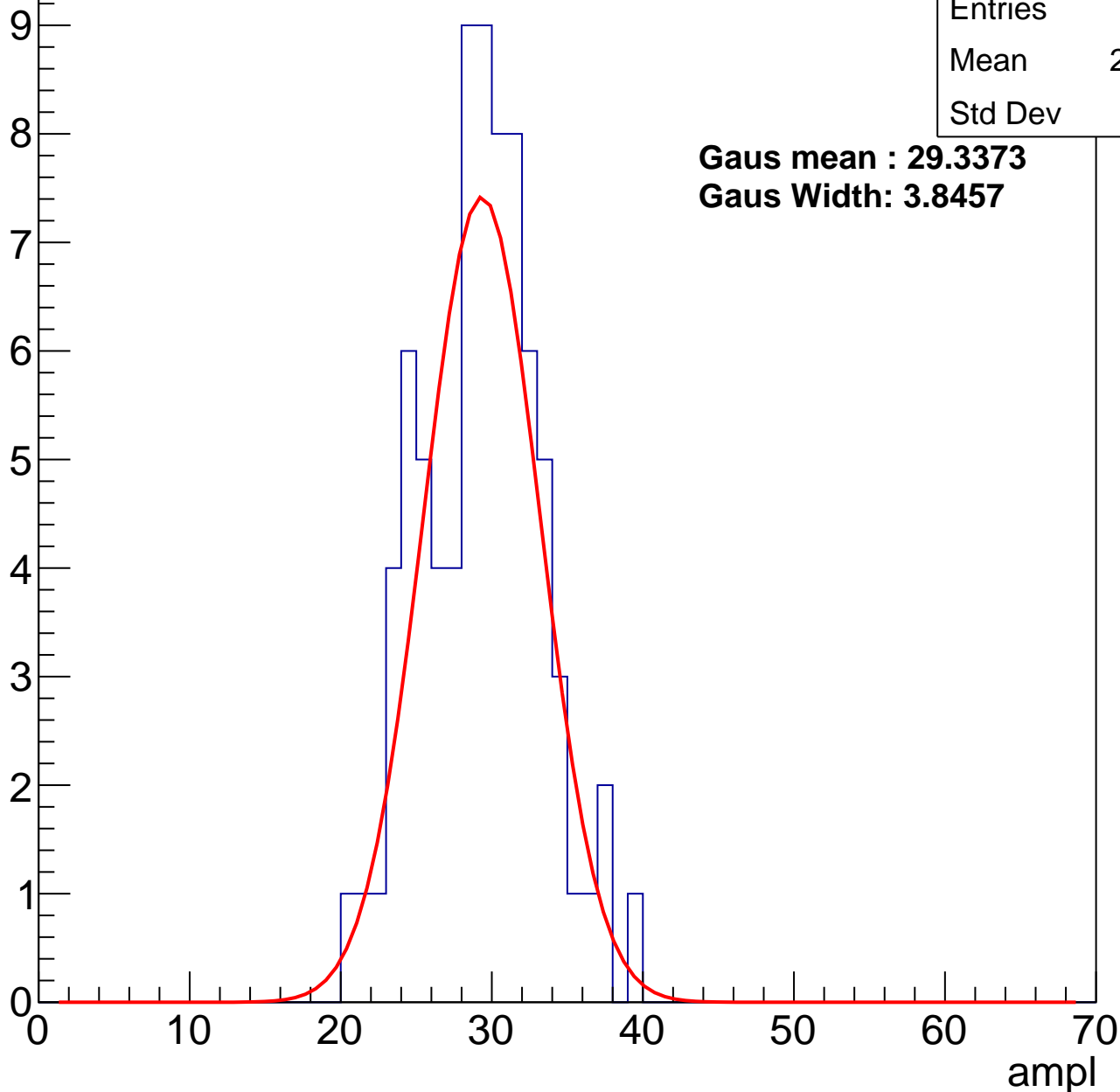
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.86
Std Dev	3.88

Gaus mean : 29.3373

Gaus Width: 3.8457



B1L102S, U13-ch113, adc1

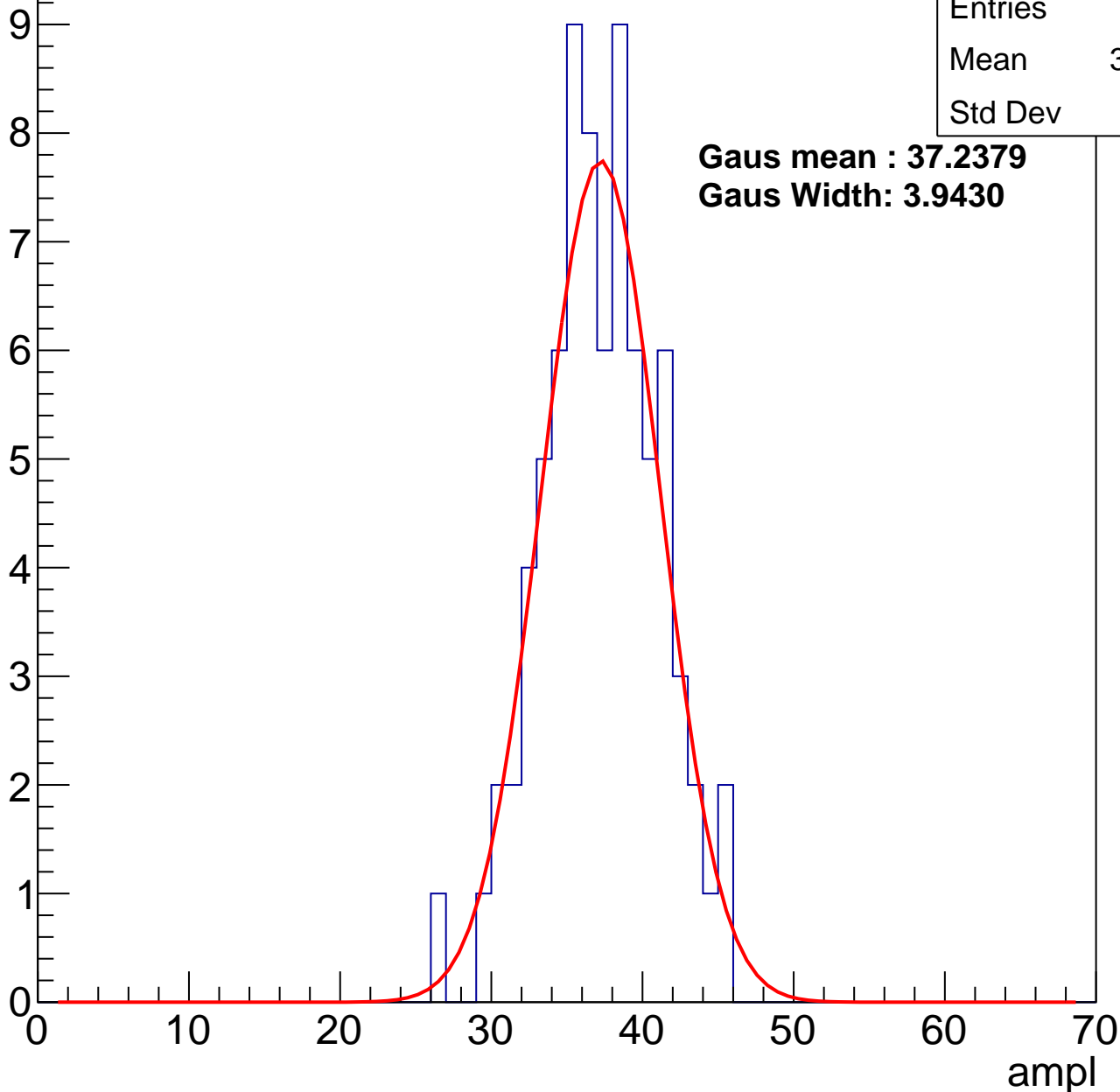
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.76
Std Dev	3.8

Gaus mean : 37.2379

Gaus Width: 3.9430



B1L102S, U13-ch113, adc2

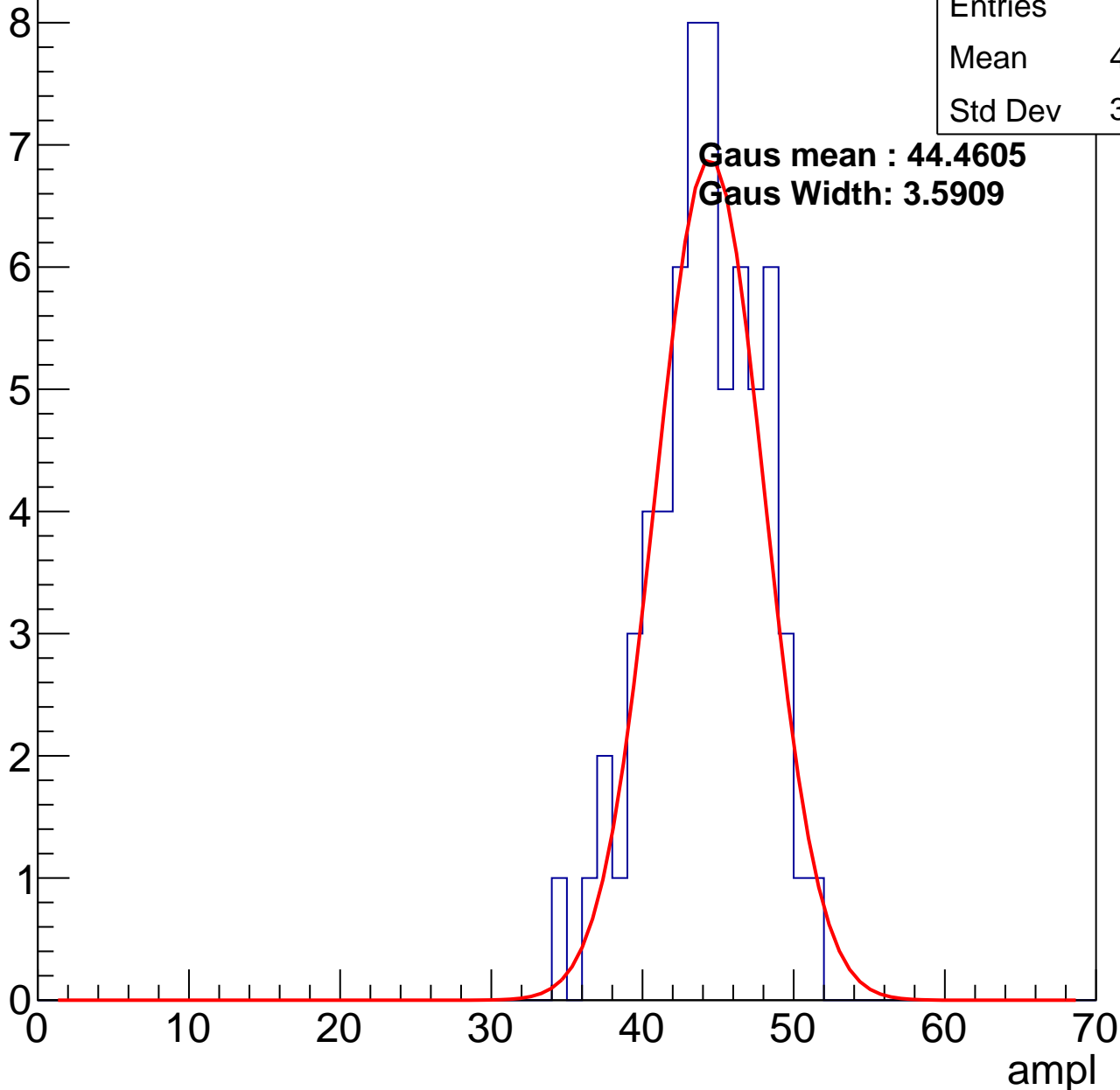
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.74
Std Dev	3.575

Gaus mean : 44.4605

Gaus Width: 3.5909

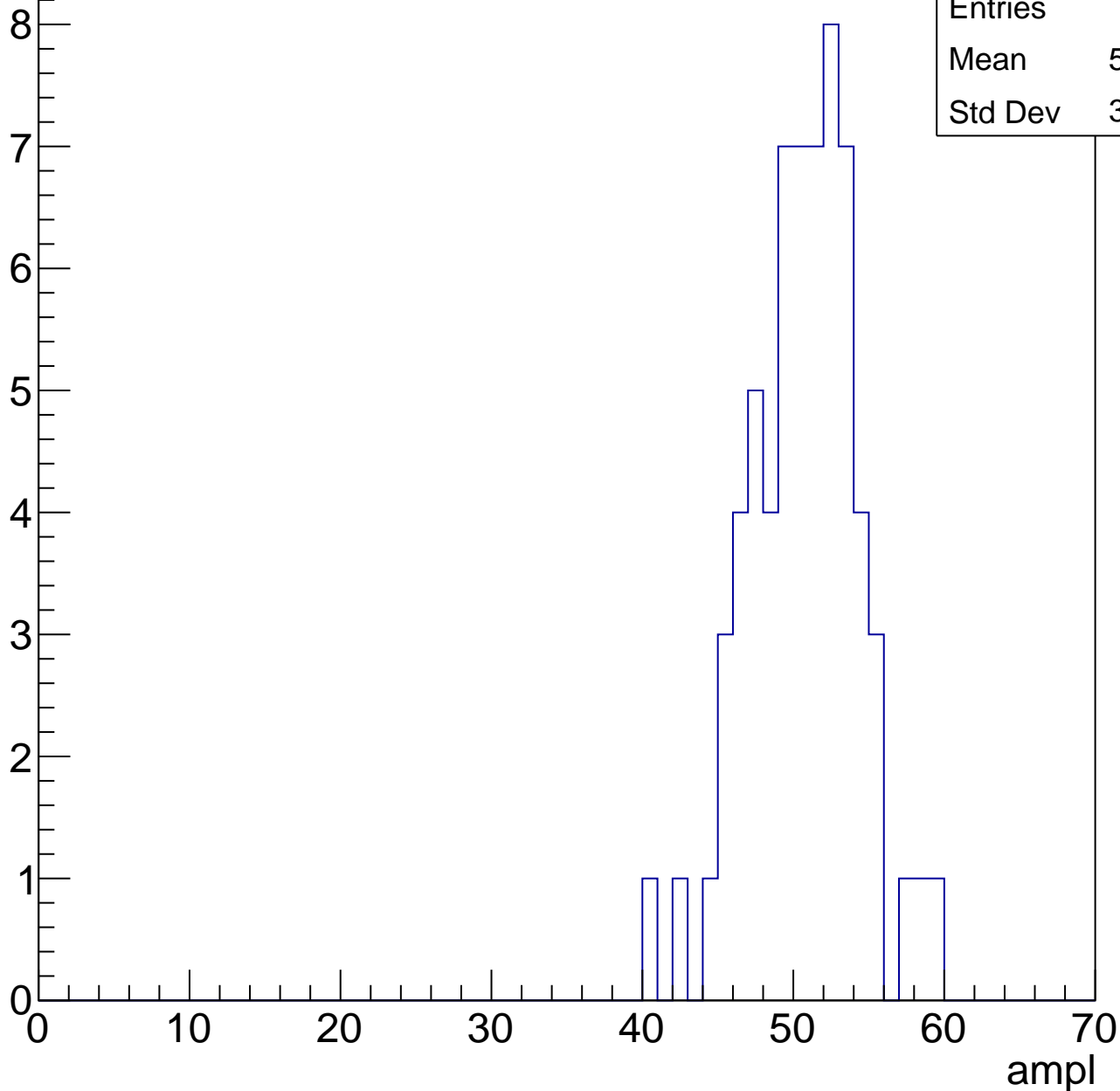


B1L102S, U13-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	50.22
Std Dev	3.593

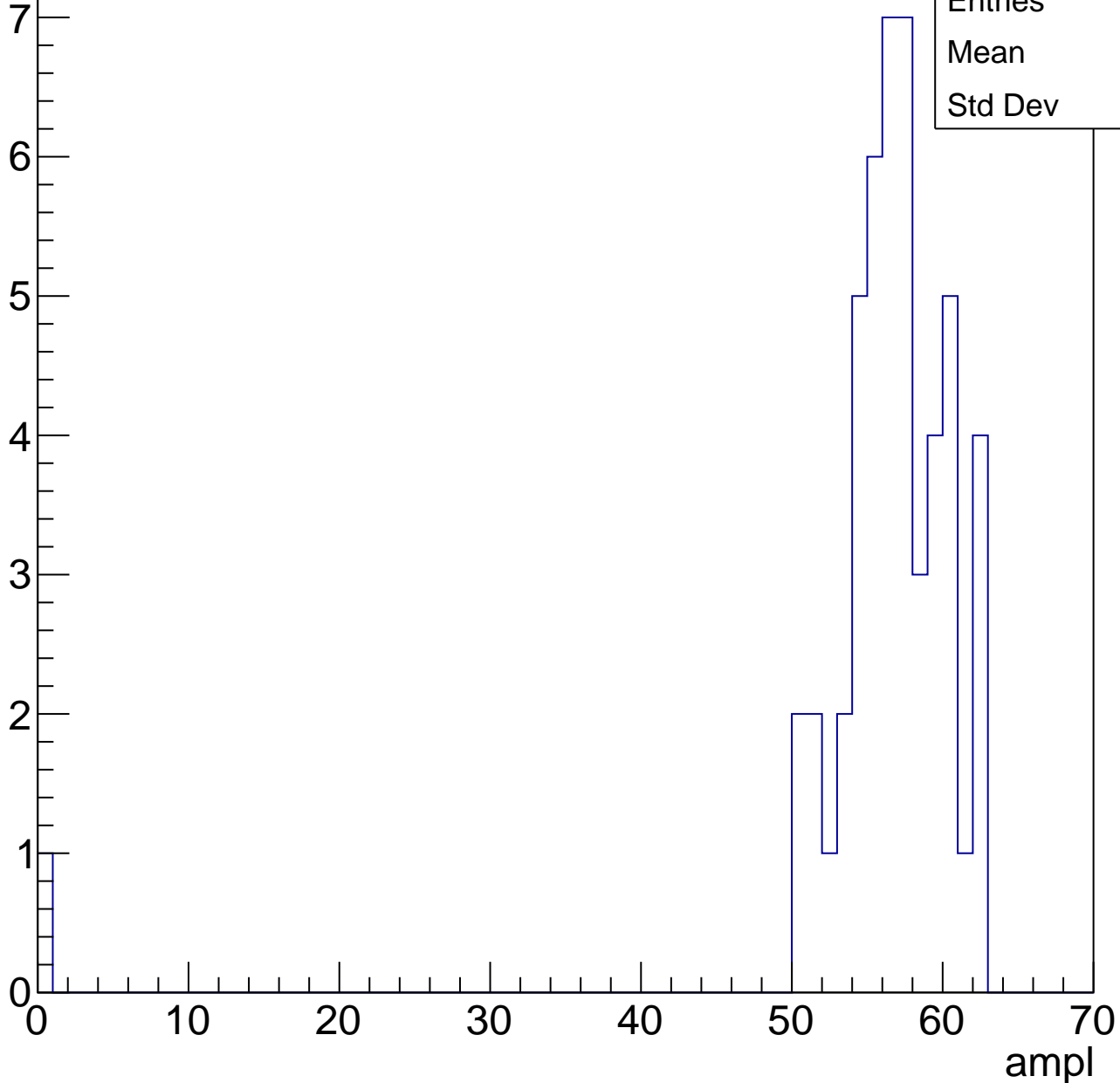


B1L102S, U13-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.4
Std Dev	8.49

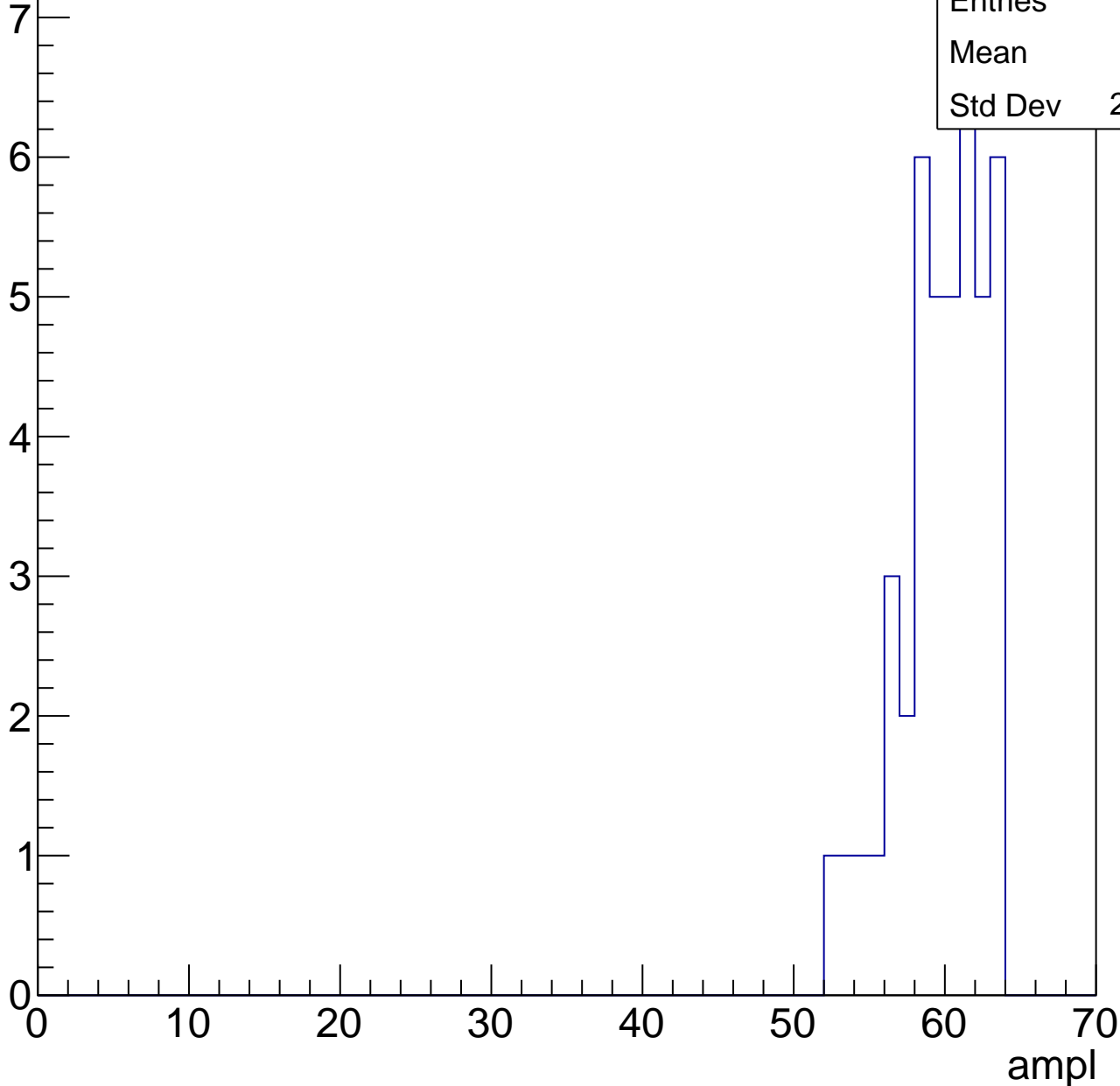


B1L102S, U13-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

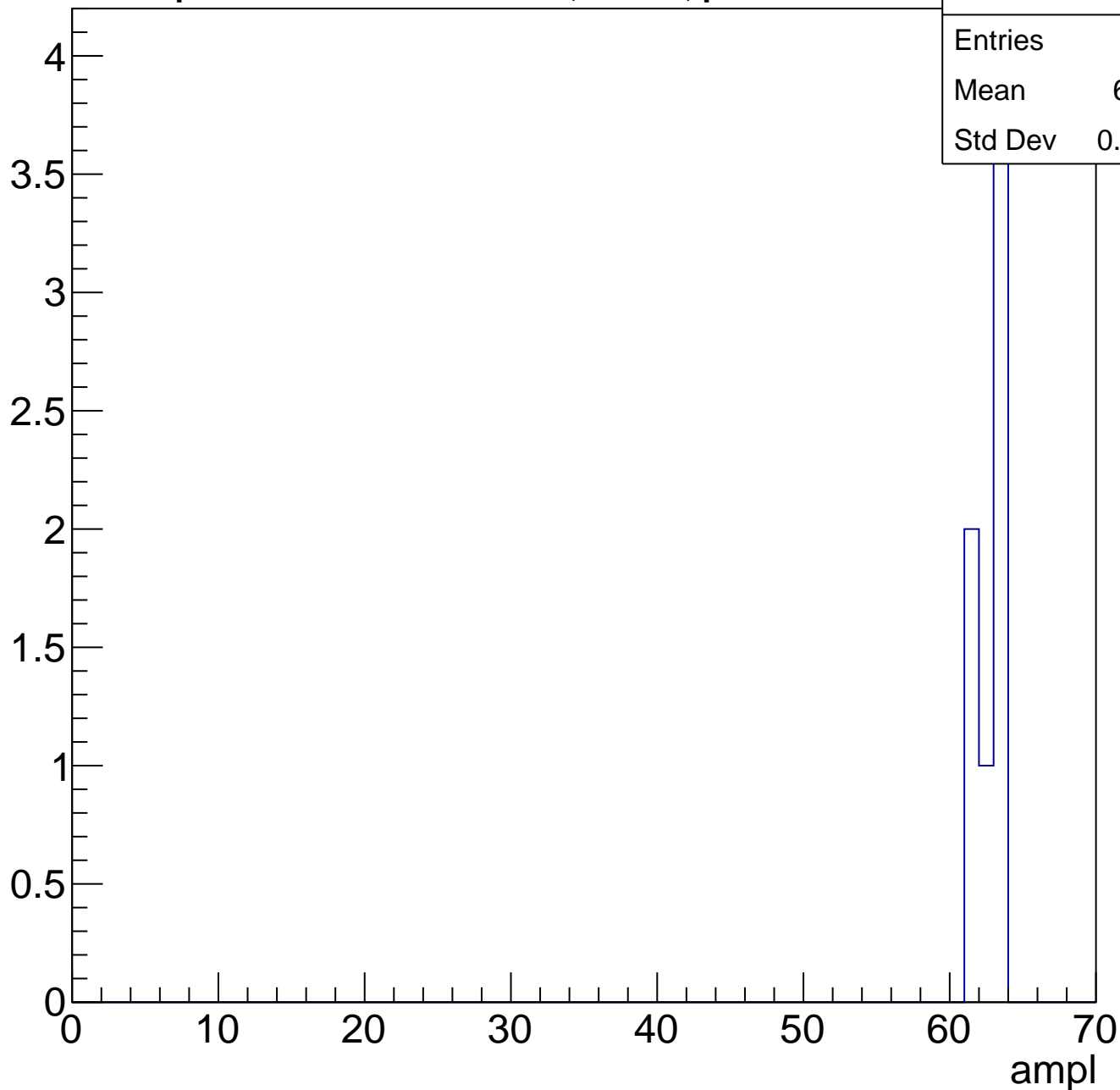
Entries	43
Mean	59.4
Std Dev	2.788



B1L102S, U13-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch114, adc0

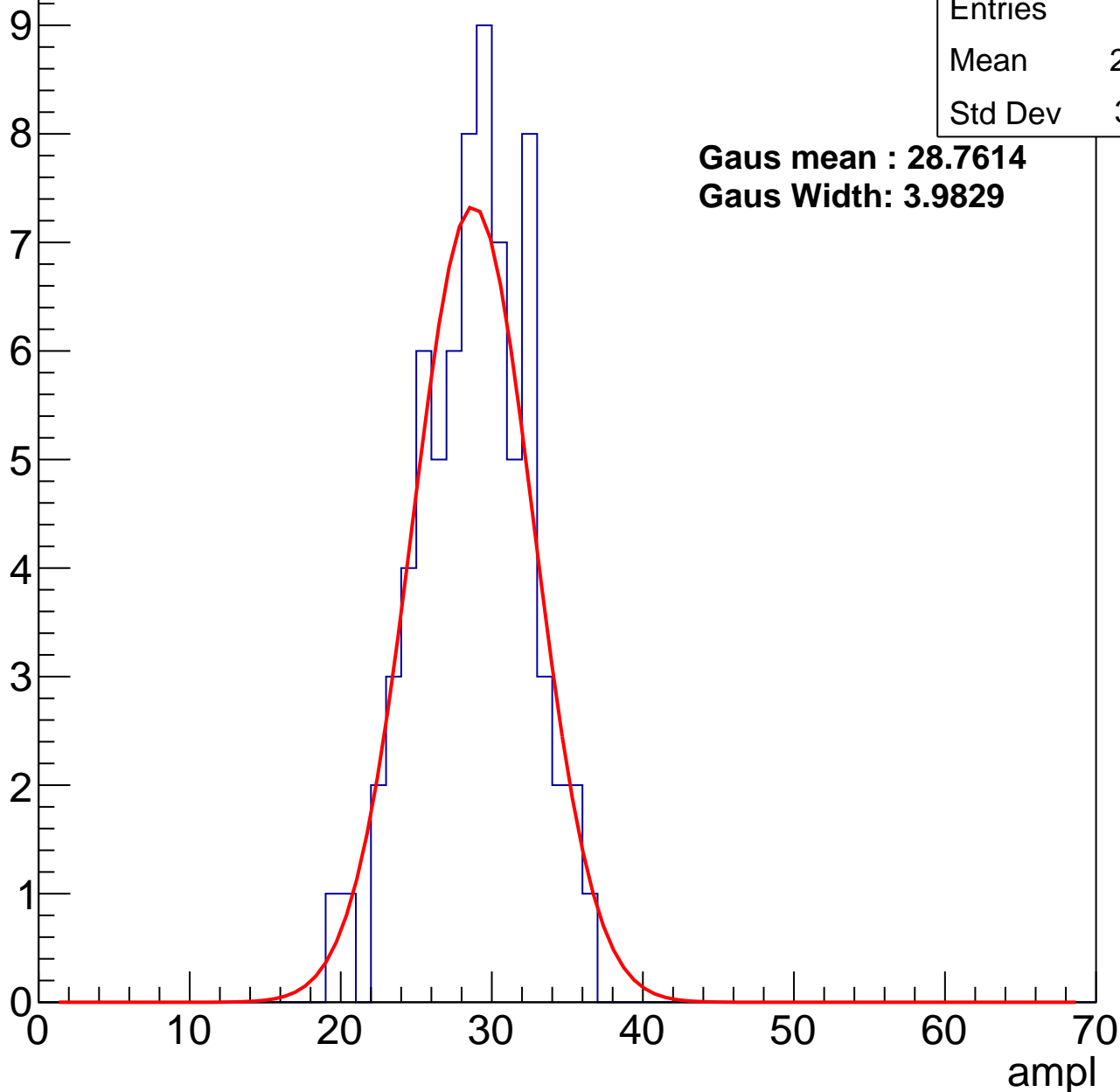
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.34
Std Dev	3.601

Gaus mean : 28.7614

Gaus Width: 3.9829



B1L102S, U13-ch114, adc1

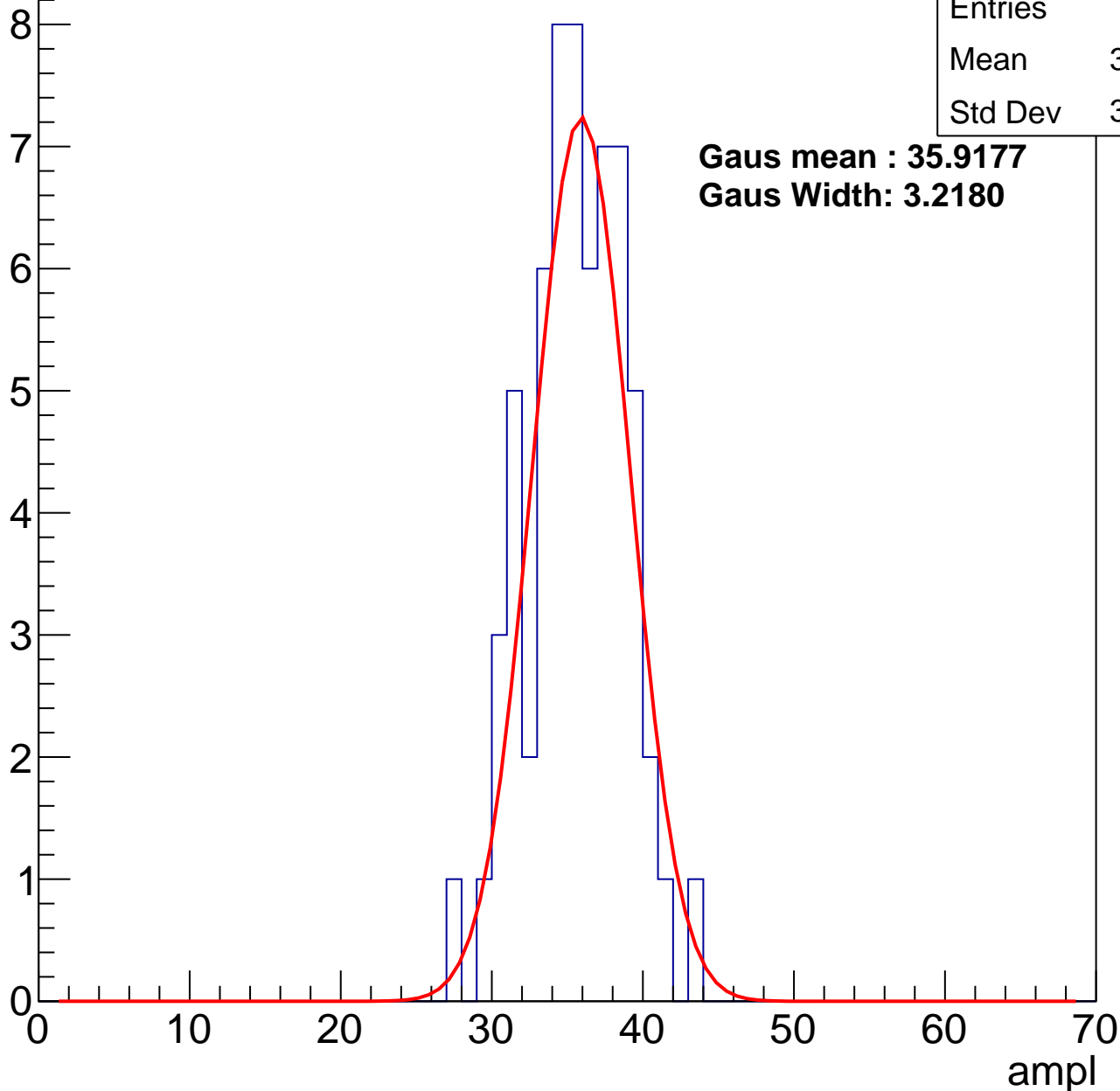
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.16
Std Dev	3.163

Gaus mean : 35.9177

Gaus Width: 3.2180



B1L102S, U13-ch114, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.36
Std Dev	3.286

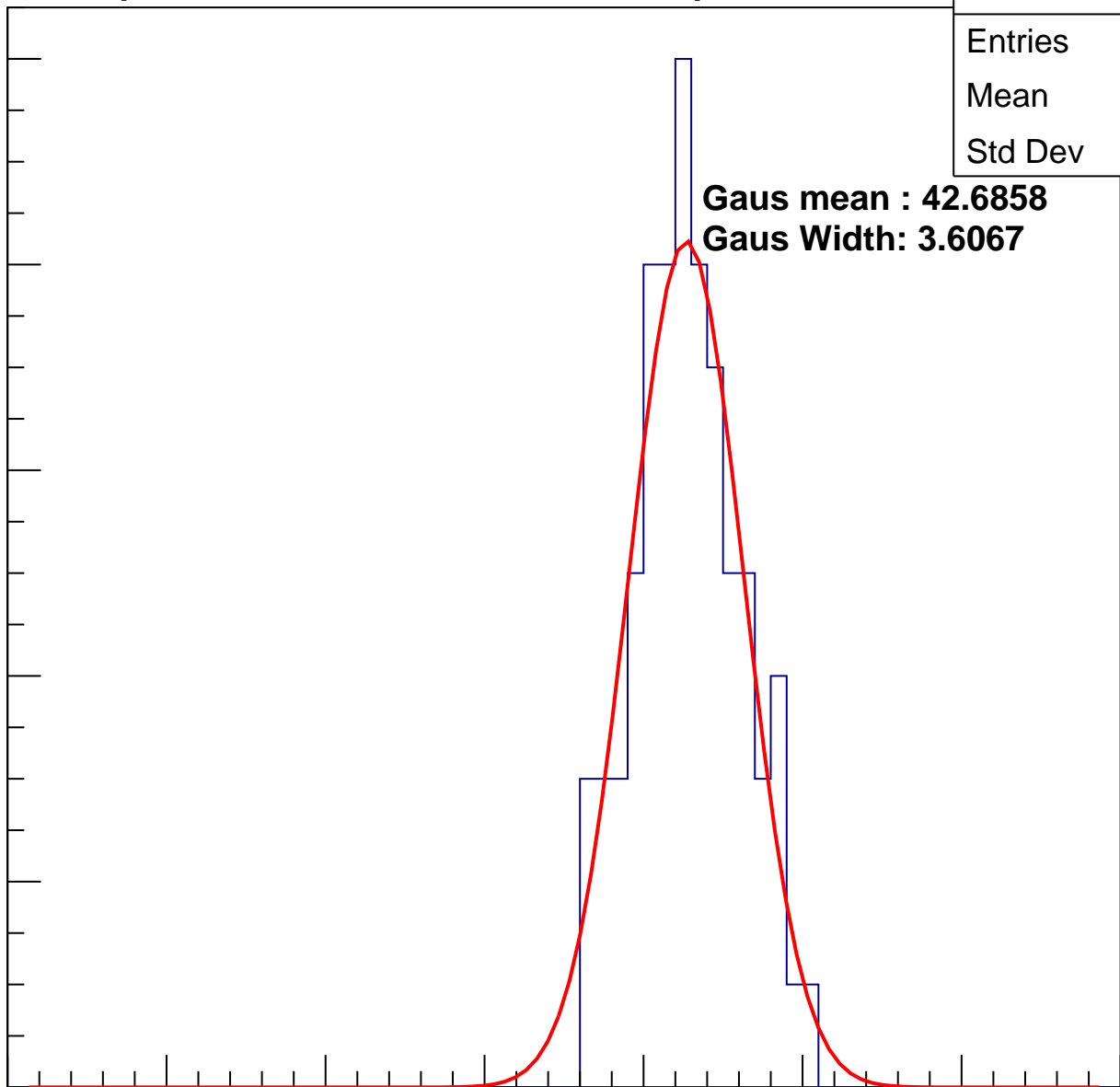
Gaus mean : 42.6858

Gaus Width: 3.6067

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

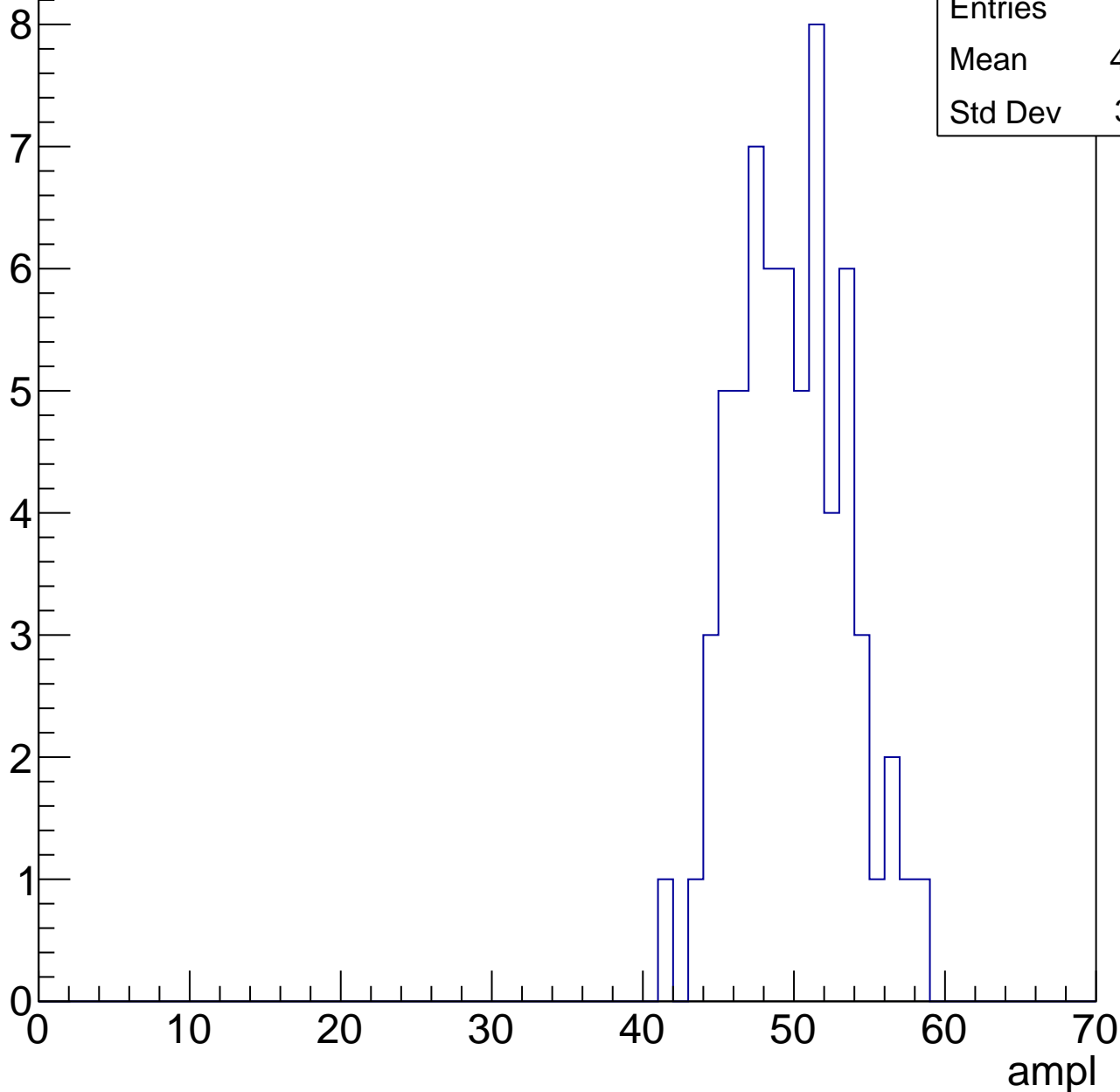


B1L102S, U13-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.38
Std Dev	3.611

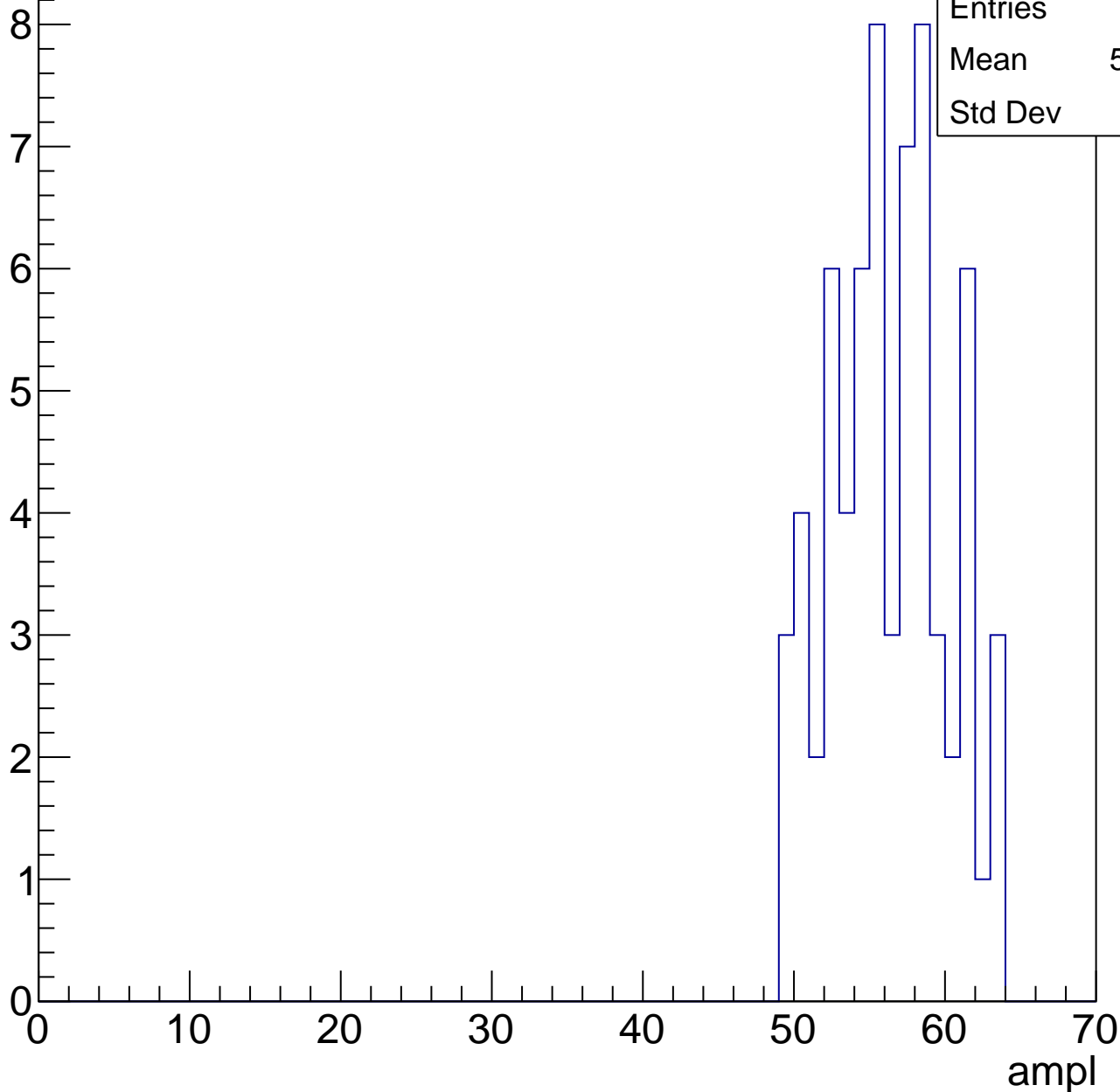


B1L102S, U13-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.79
Std Dev	3.76

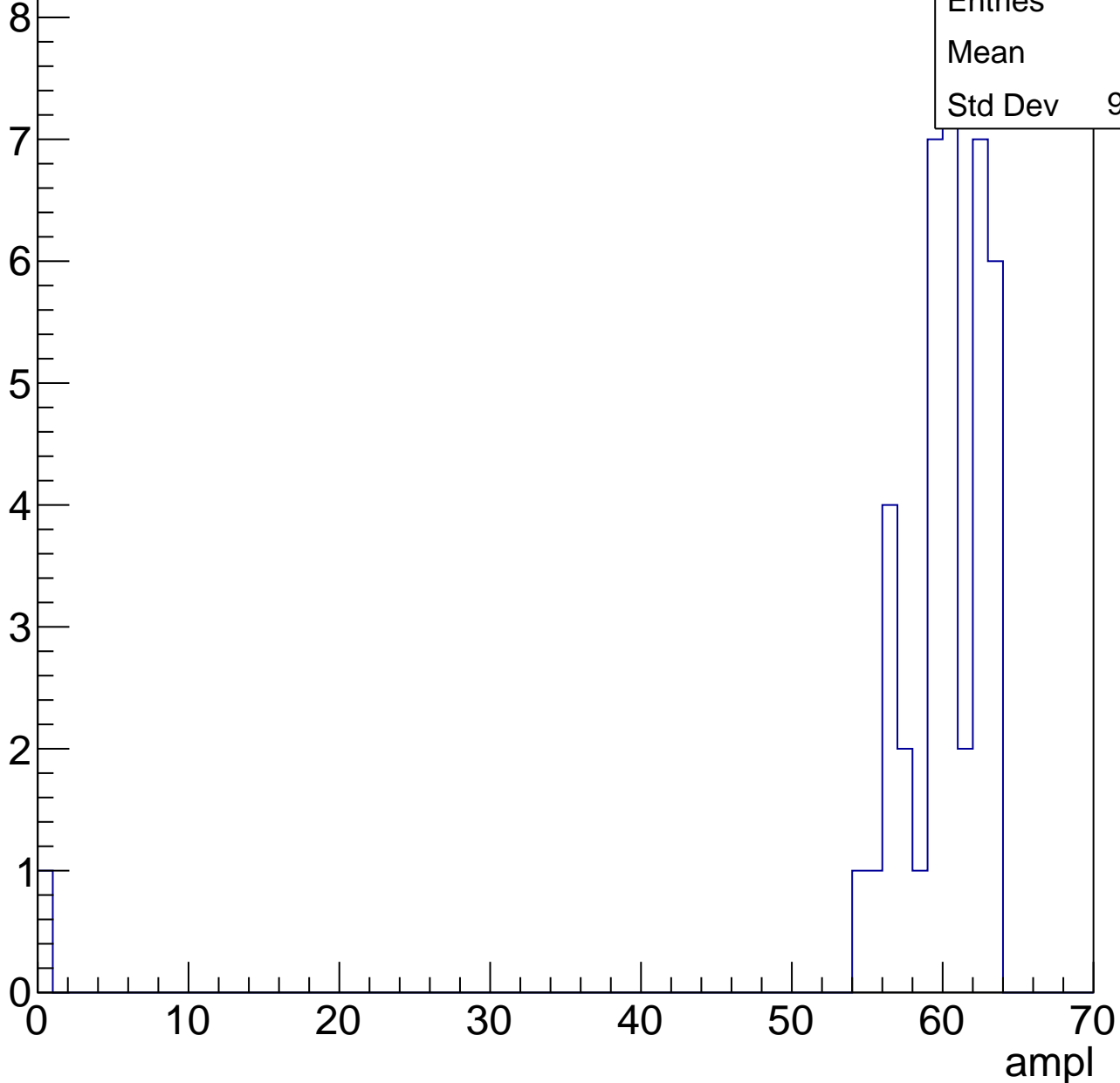


B1L102S, U13-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.3
Std Dev	9.647



B1L102S, U13-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

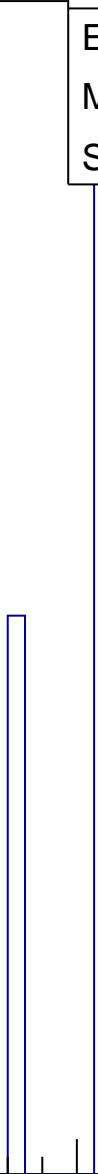
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.83
Std Dev	2.267

0 10 20 30 40 50 60 70

ampl



B1L102S, U13-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch115, adc0

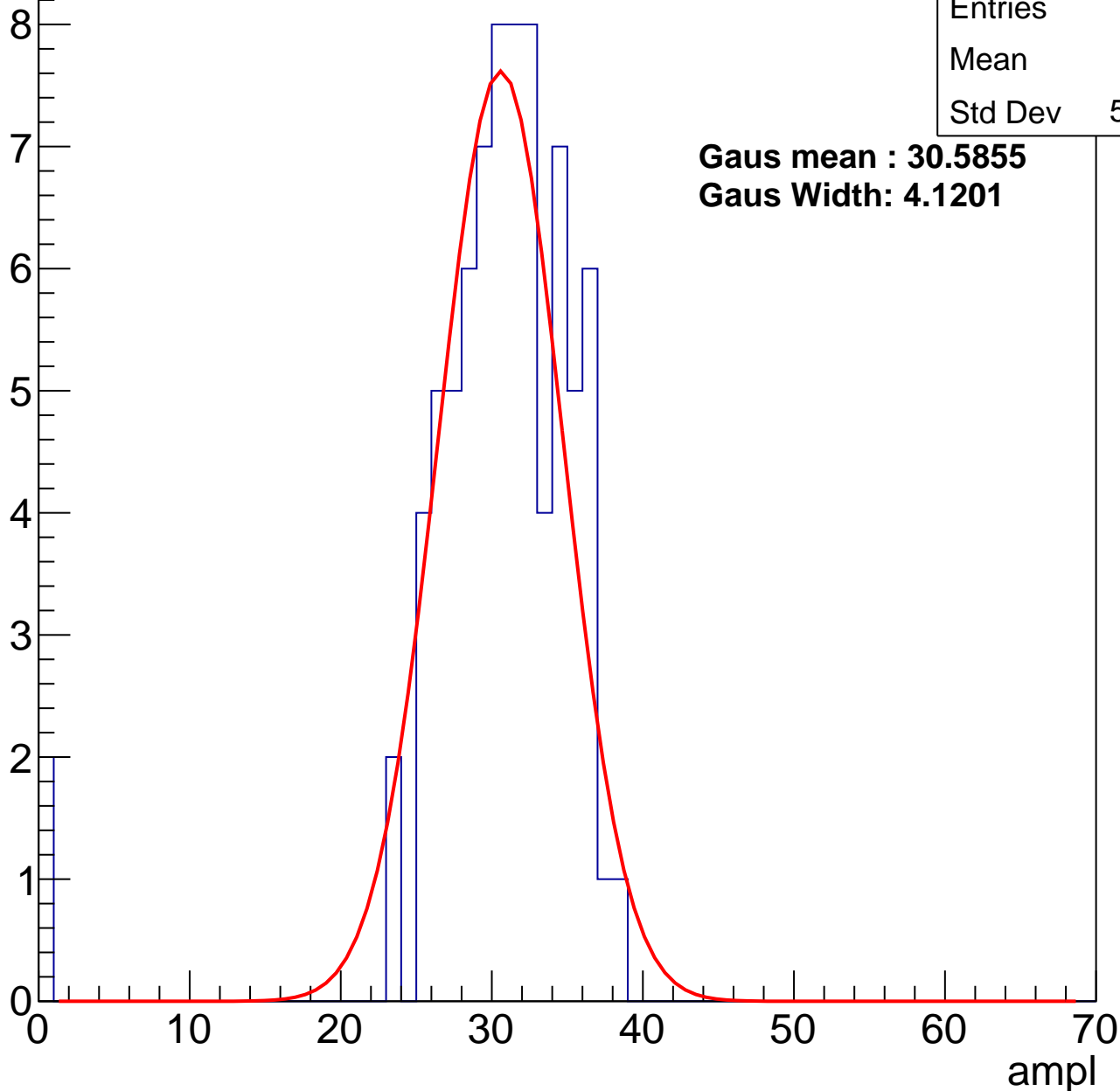
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29.9
Std Dev	5.948

Gaus mean : 30.5855

Gaus Width: 4.1201



B1L102S, U13-ch115, adc1

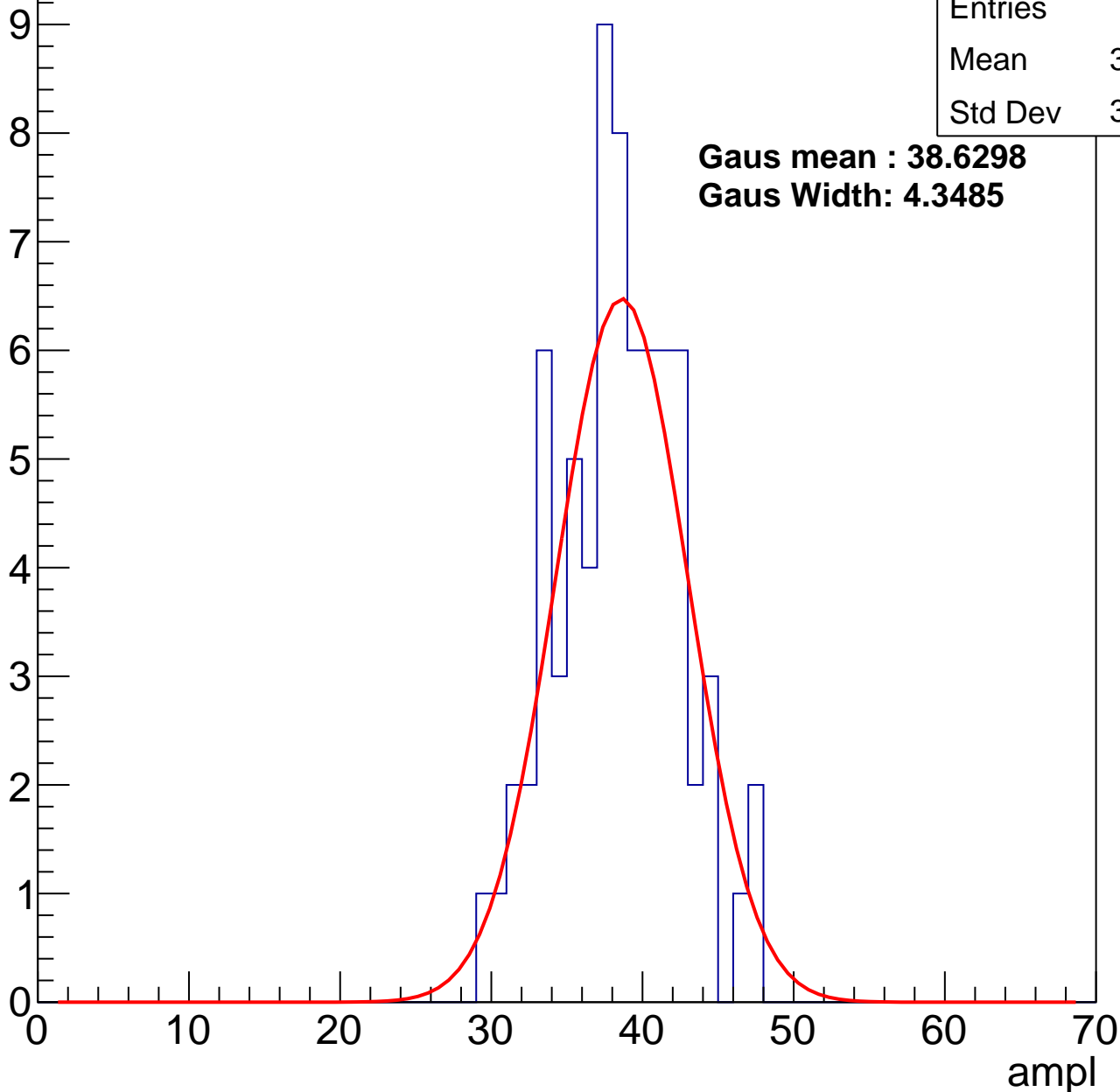
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.96
Std Dev	3.964

Gaus mean : 38.6298

Gaus Width: 4.3485



B1L102S, U13-ch115, adc2

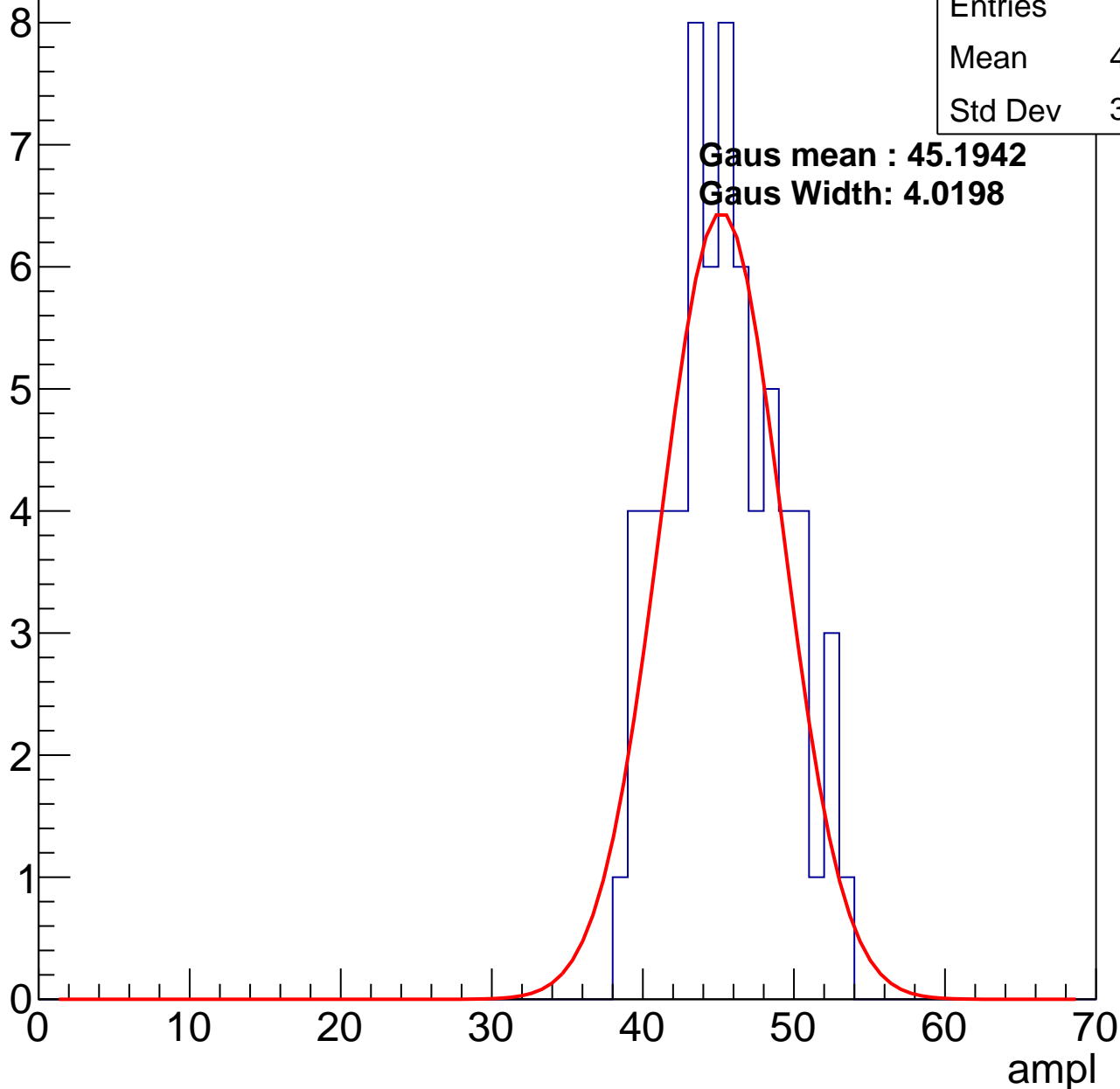
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.99
Std Dev	3.683

Gaus mean : 45.1942

Gaus Width: 4.0198

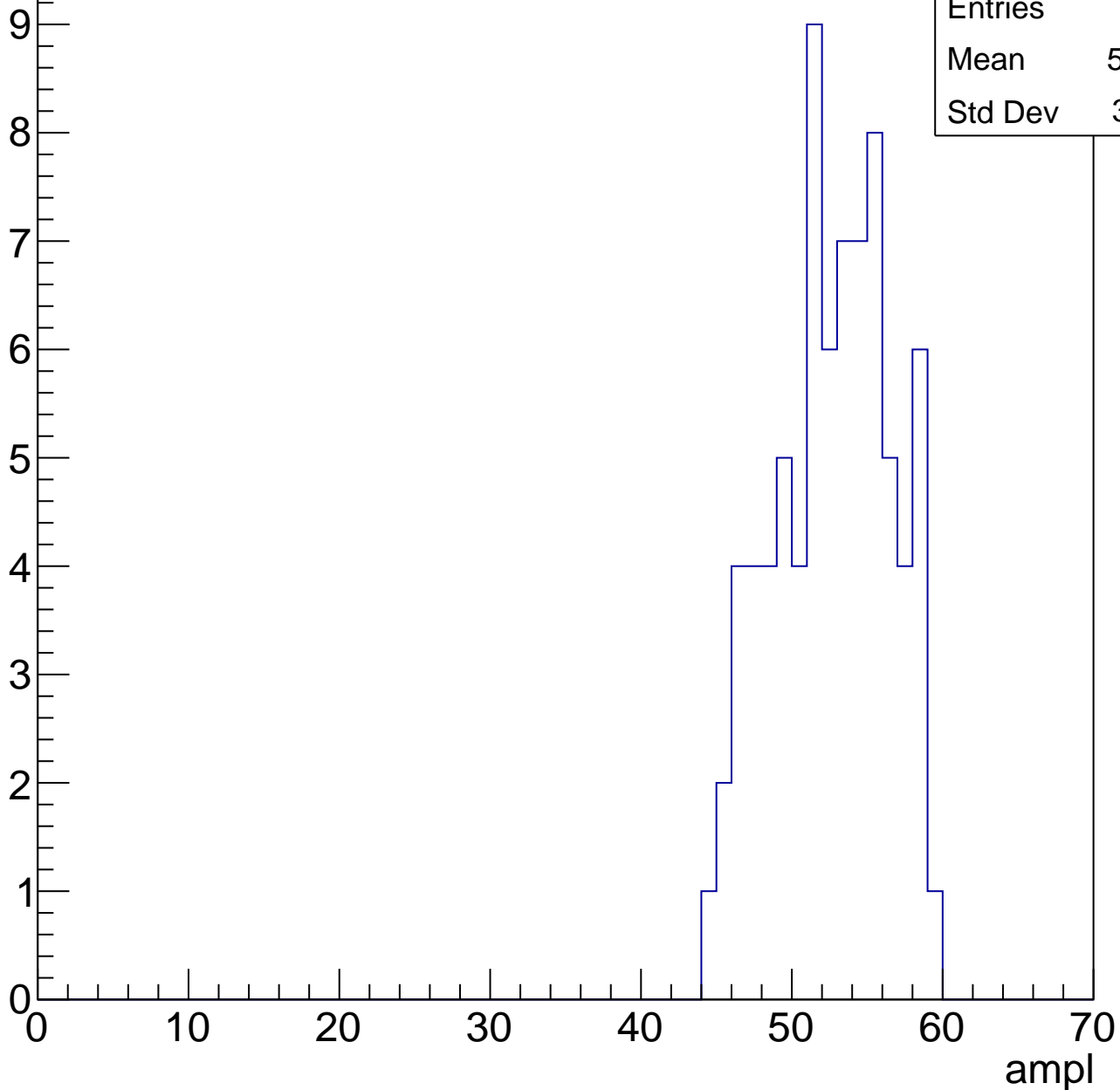


B1L102S, U13-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	52.18
Std Dev	3.761

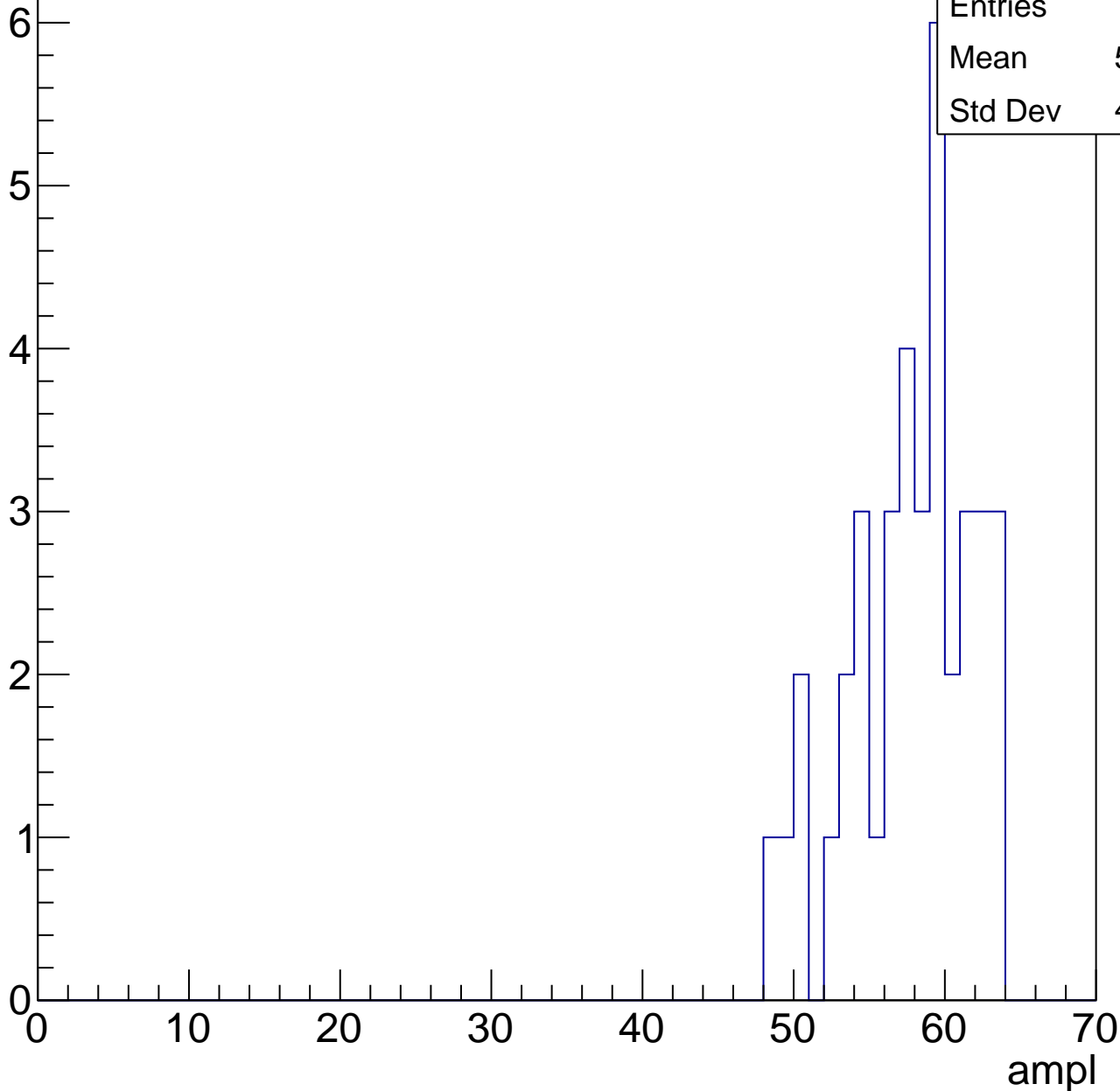


B1L102S, U13-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	57.21
Std Dev	4.001



B1L102S, U13-ch115, adc5

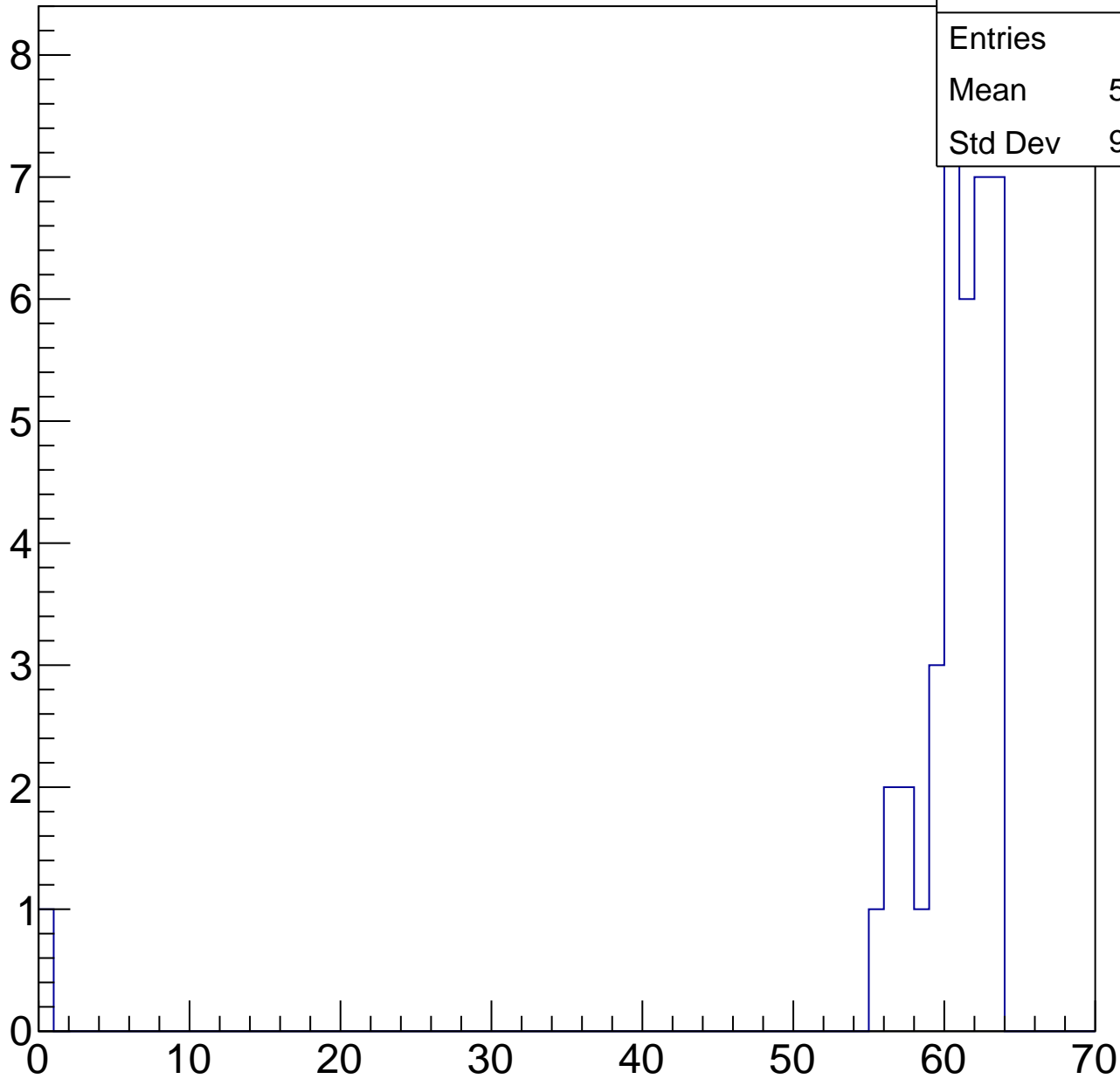
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	58.87
Std Dev	9.908

ampl



B1L102S, U13-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	61
Std Dev	0

B1L102S, U13-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch116, adc0

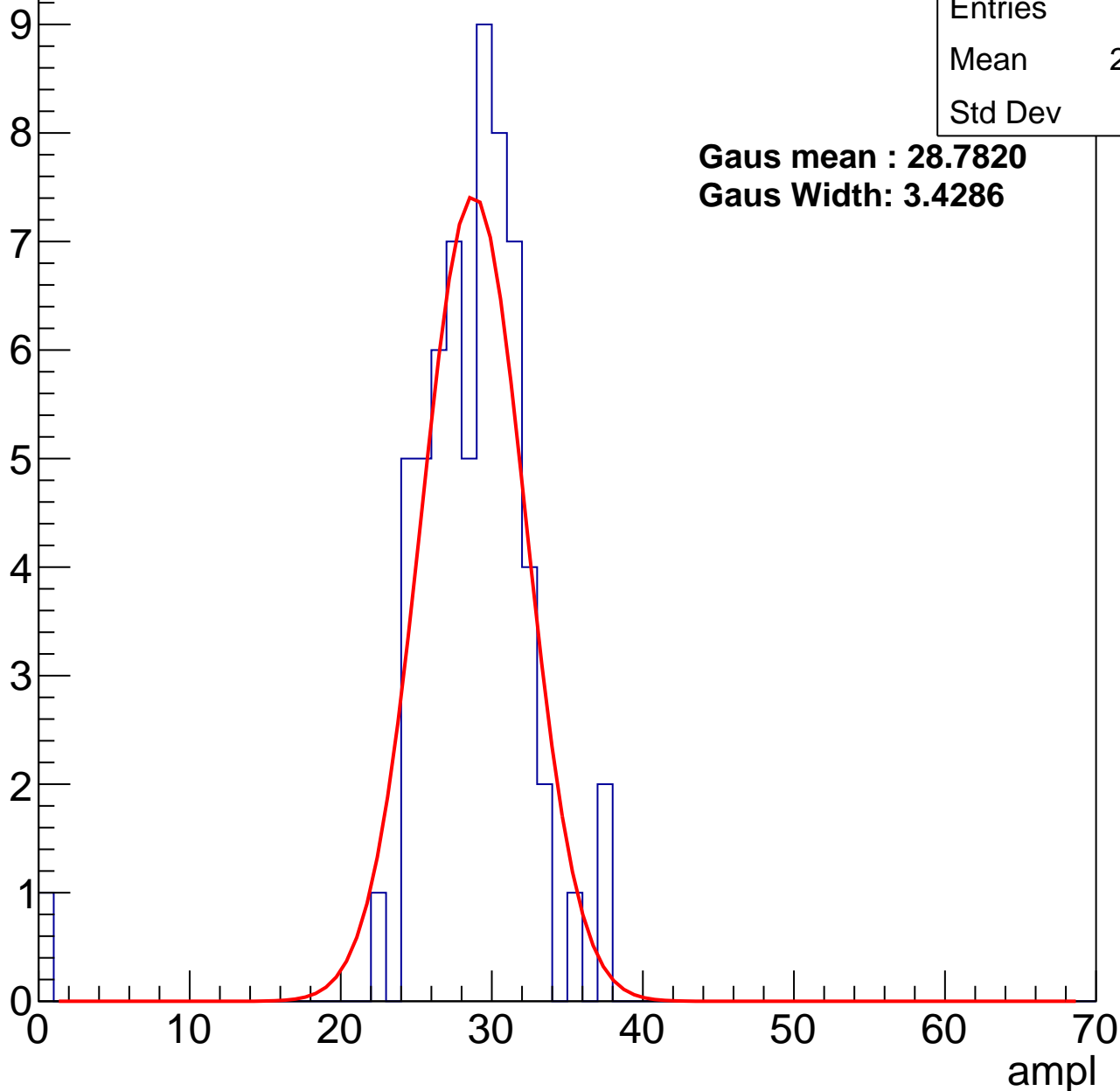
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.14
Std Dev	4.72

Gaus mean : 28.7820

Gaus Width: 3.4286



B1L102S, U13-ch116, adc1

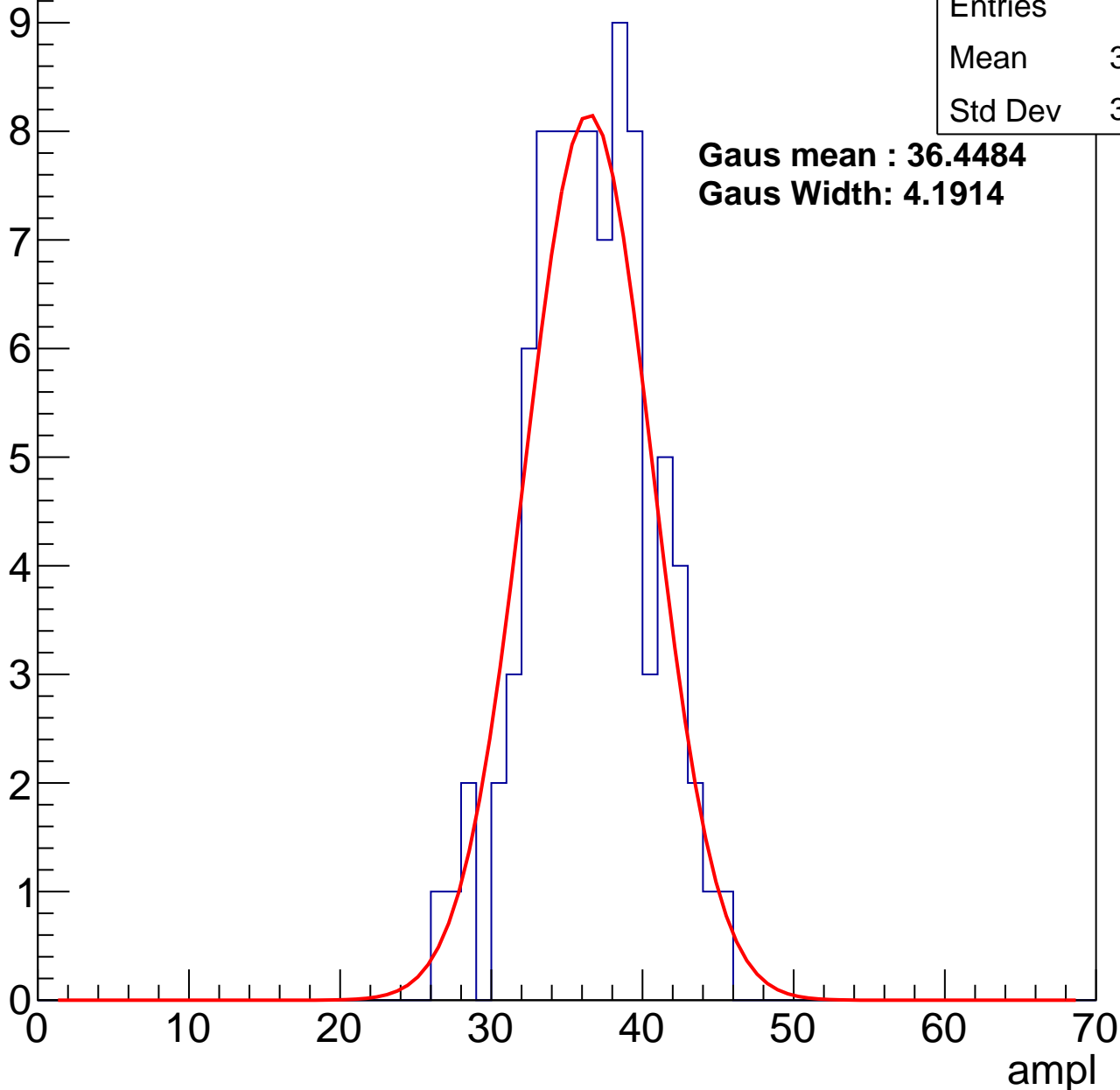
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	36.08
Std Dev	3.922

Gaus mean : 36.4484

Gaus Width: 4.1914



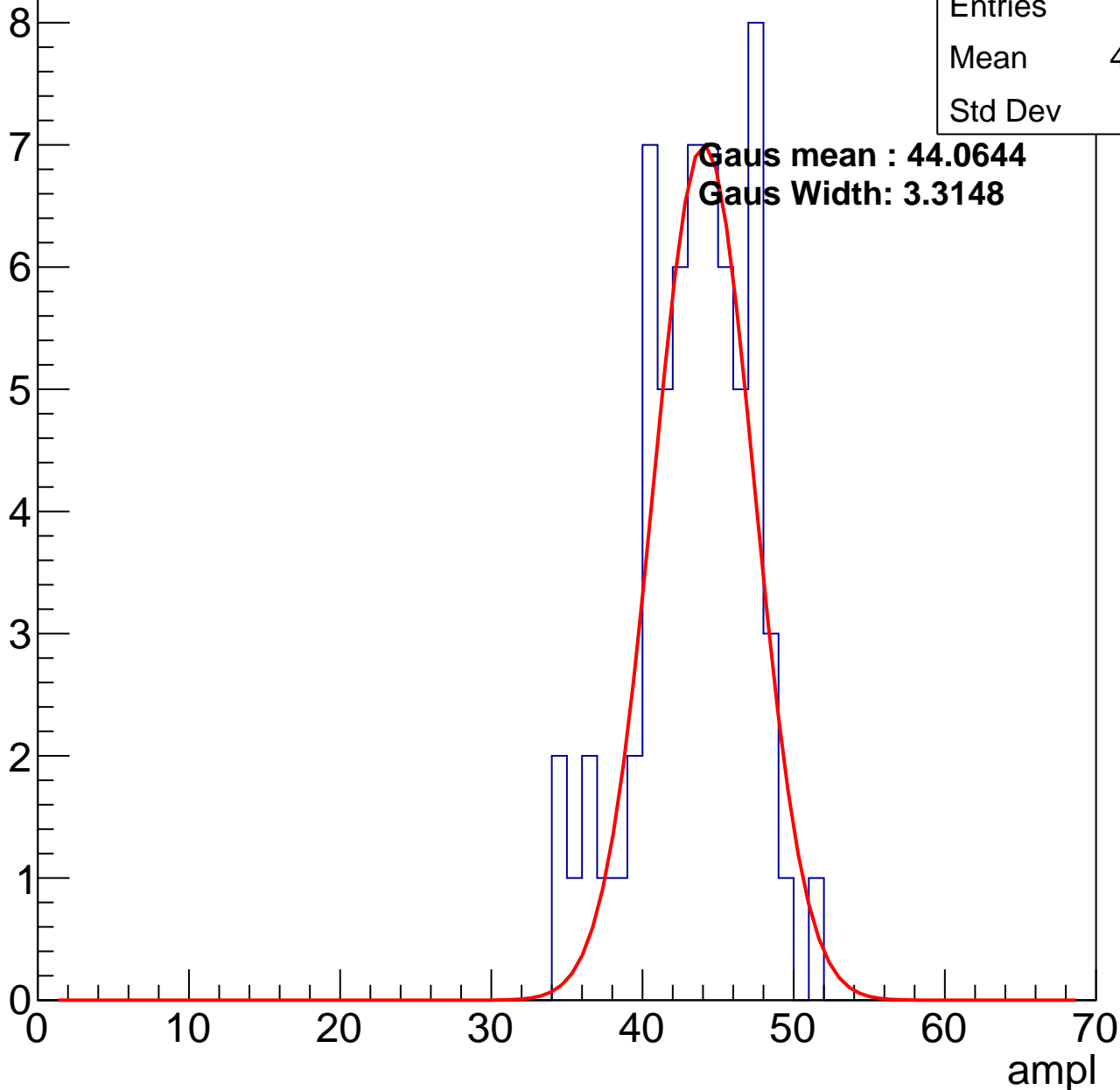
B1L102S, U13-ch116, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.98
Std Dev	3.69

Gaus mean : 44.0644
Gaus Width: 3.3148

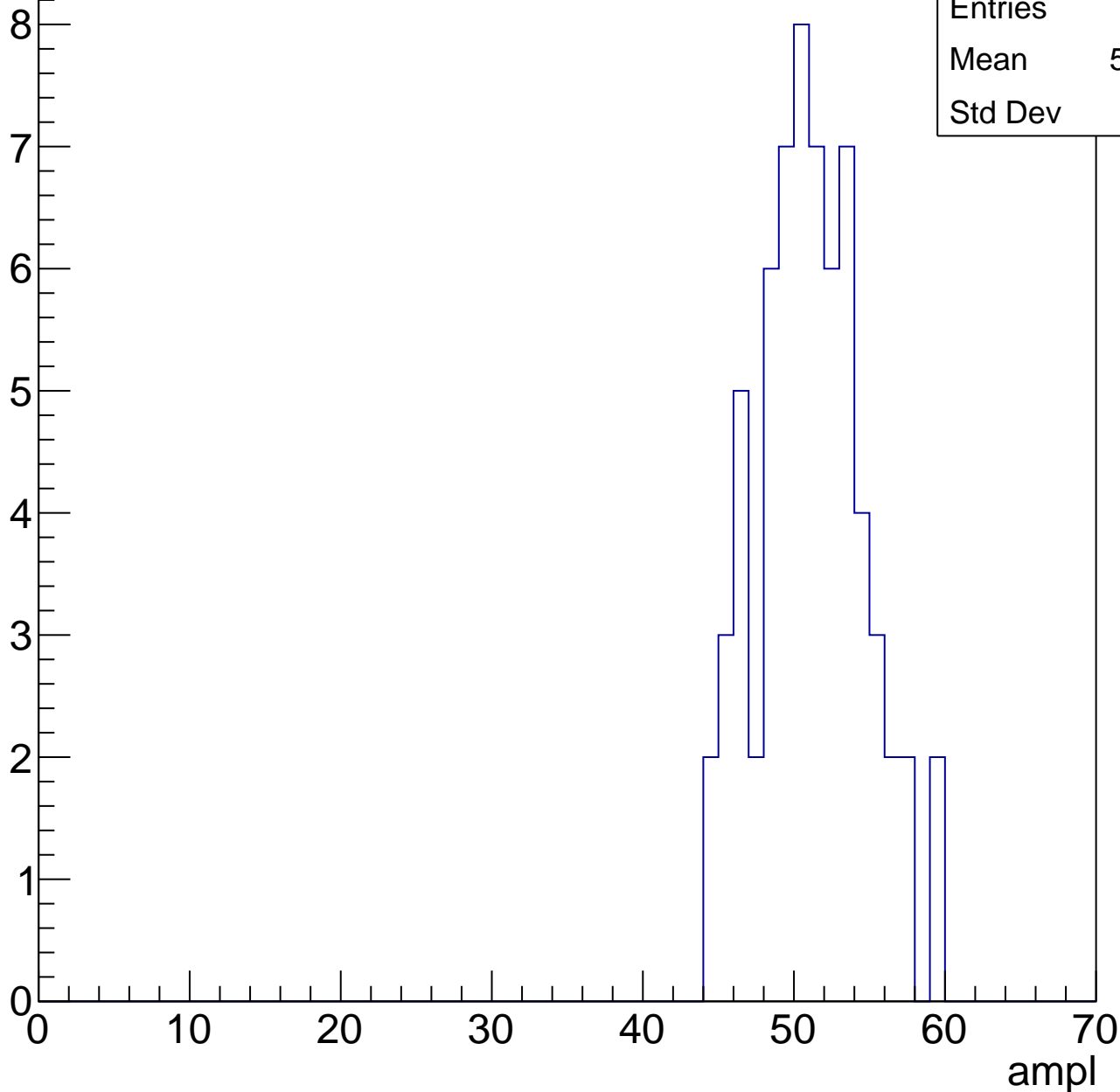


B1L102S, U13-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.65
Std Dev	3.51

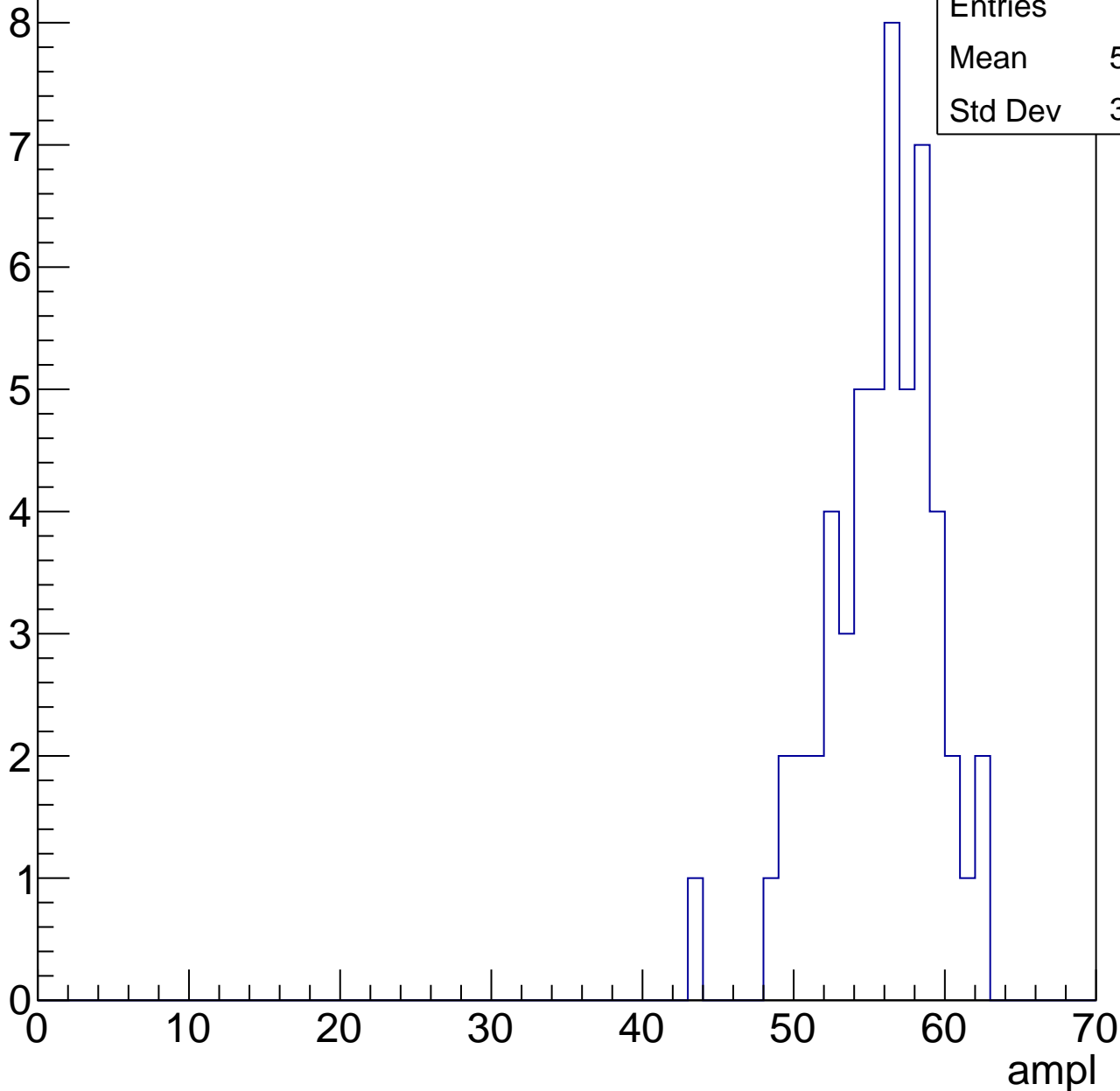


B1L102S, U13-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.24
Std Dev	3.692

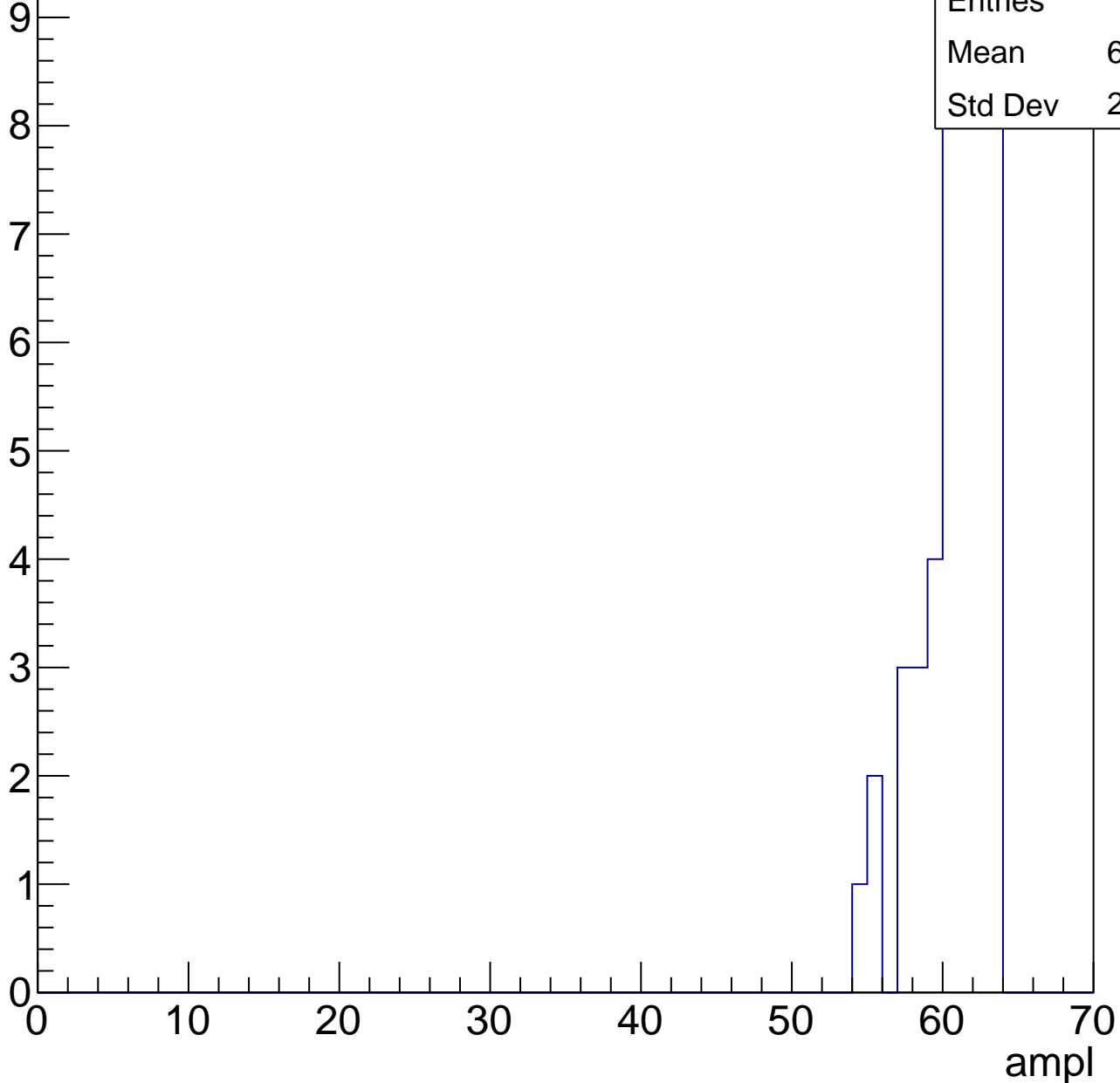


B1L102S, U13-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

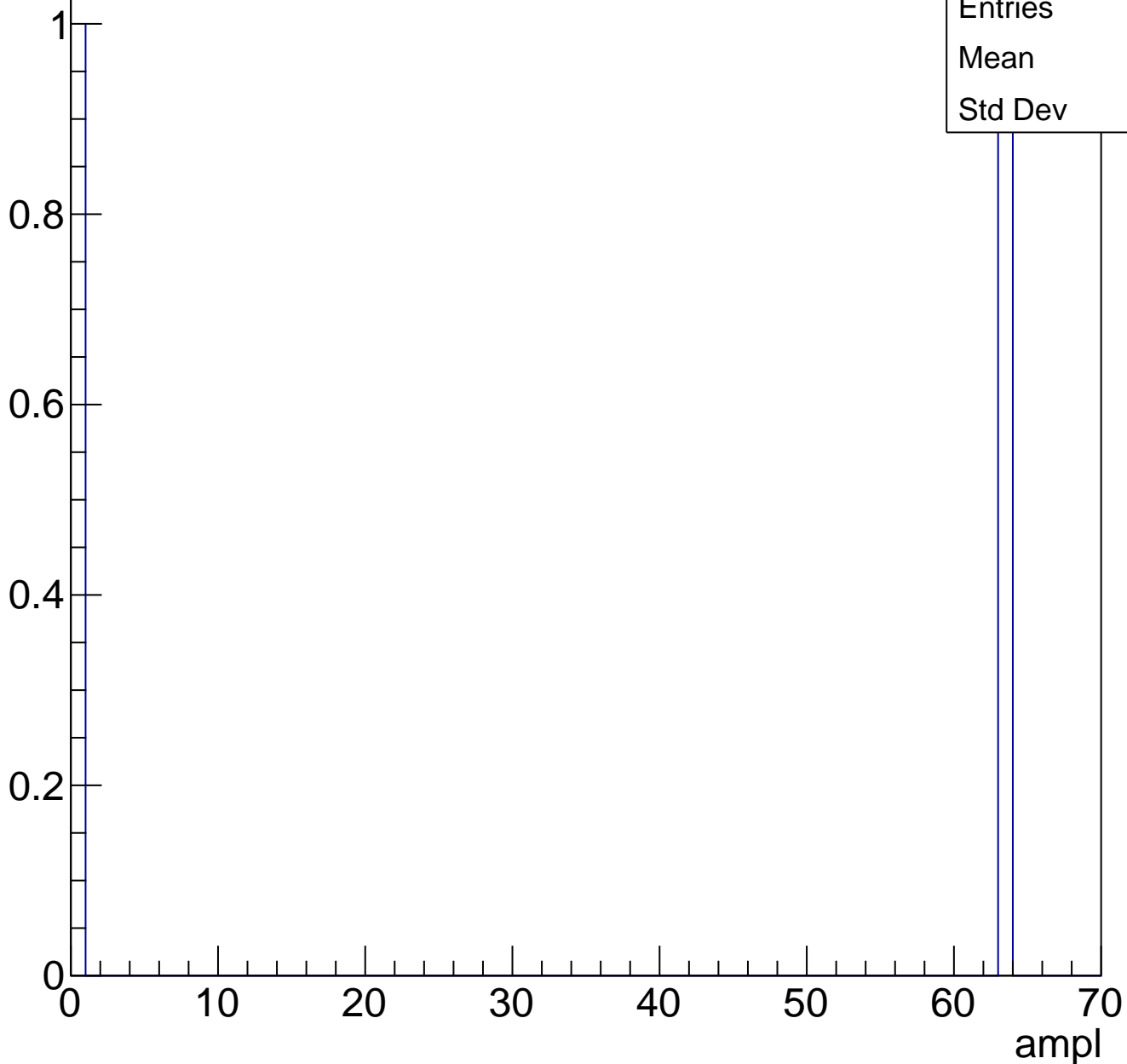
Entries	47
Mean	60.36
Std Dev	2.282



B1L102S, U13-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

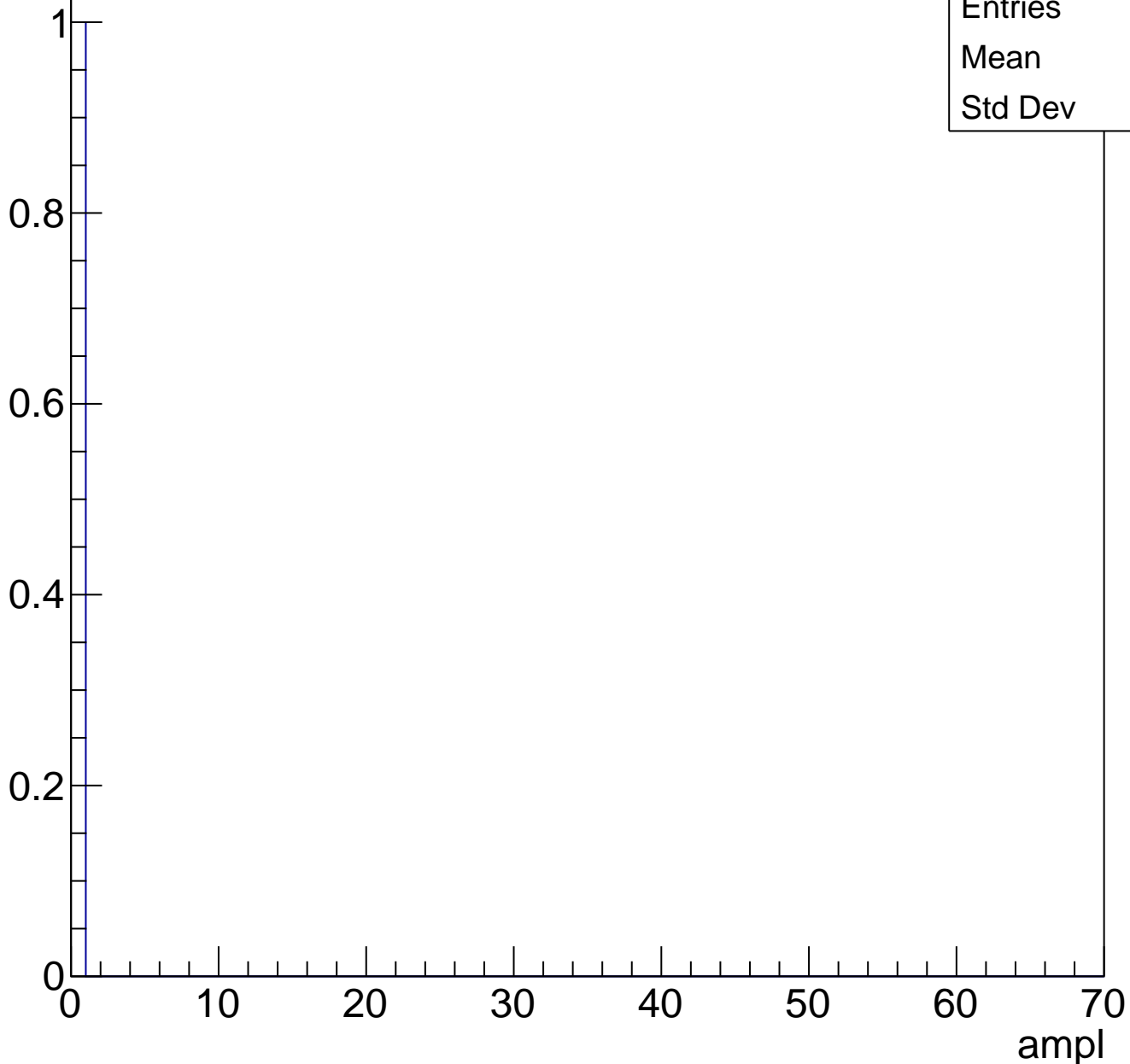
Entry



B1L102S, U13-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch117, adc0

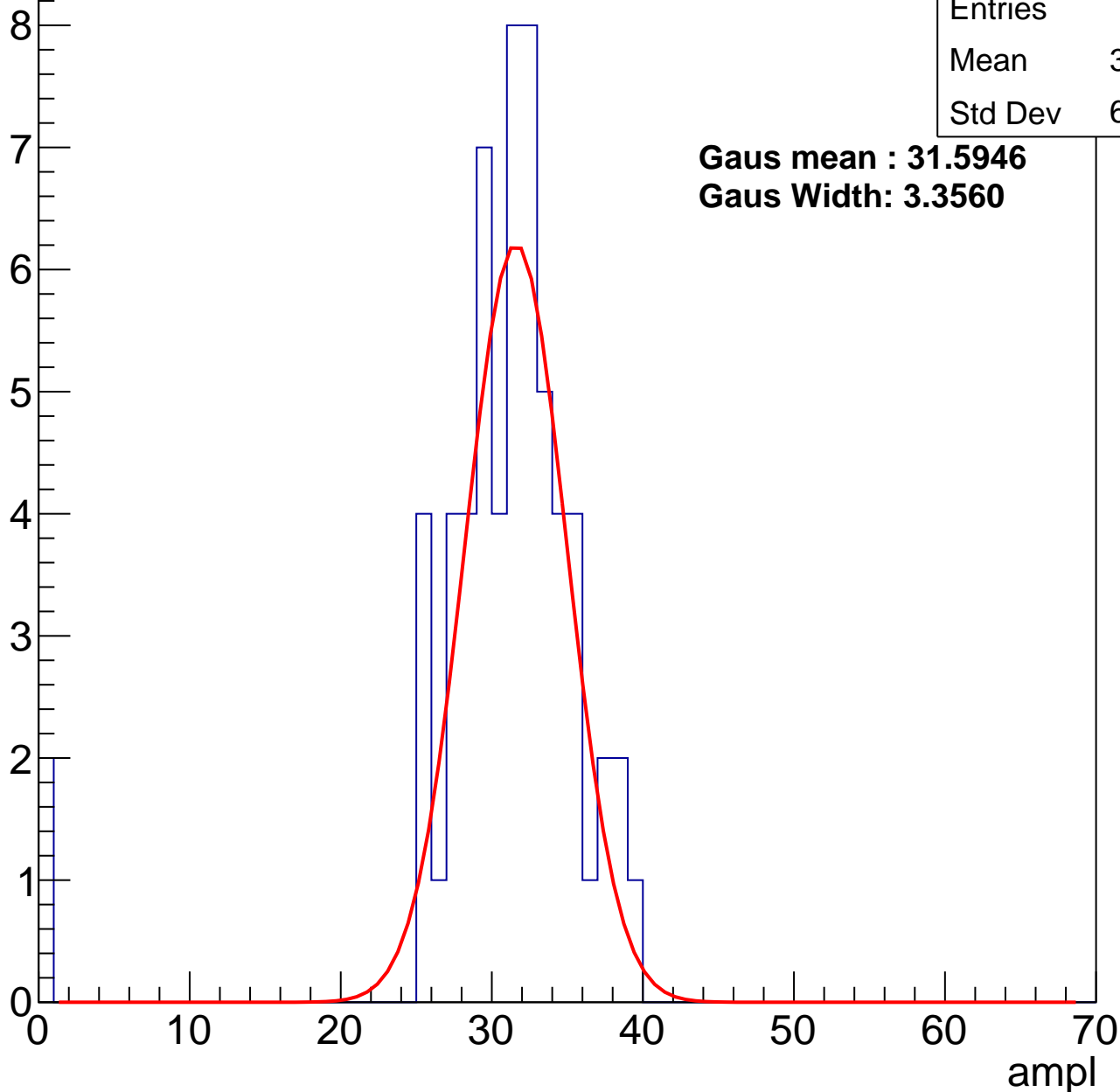
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	30.15
Std Dev	6.485

Gaus mean : 31.5946

Gaus Width: 3.3560



B1L102S, U13-ch117, adc1

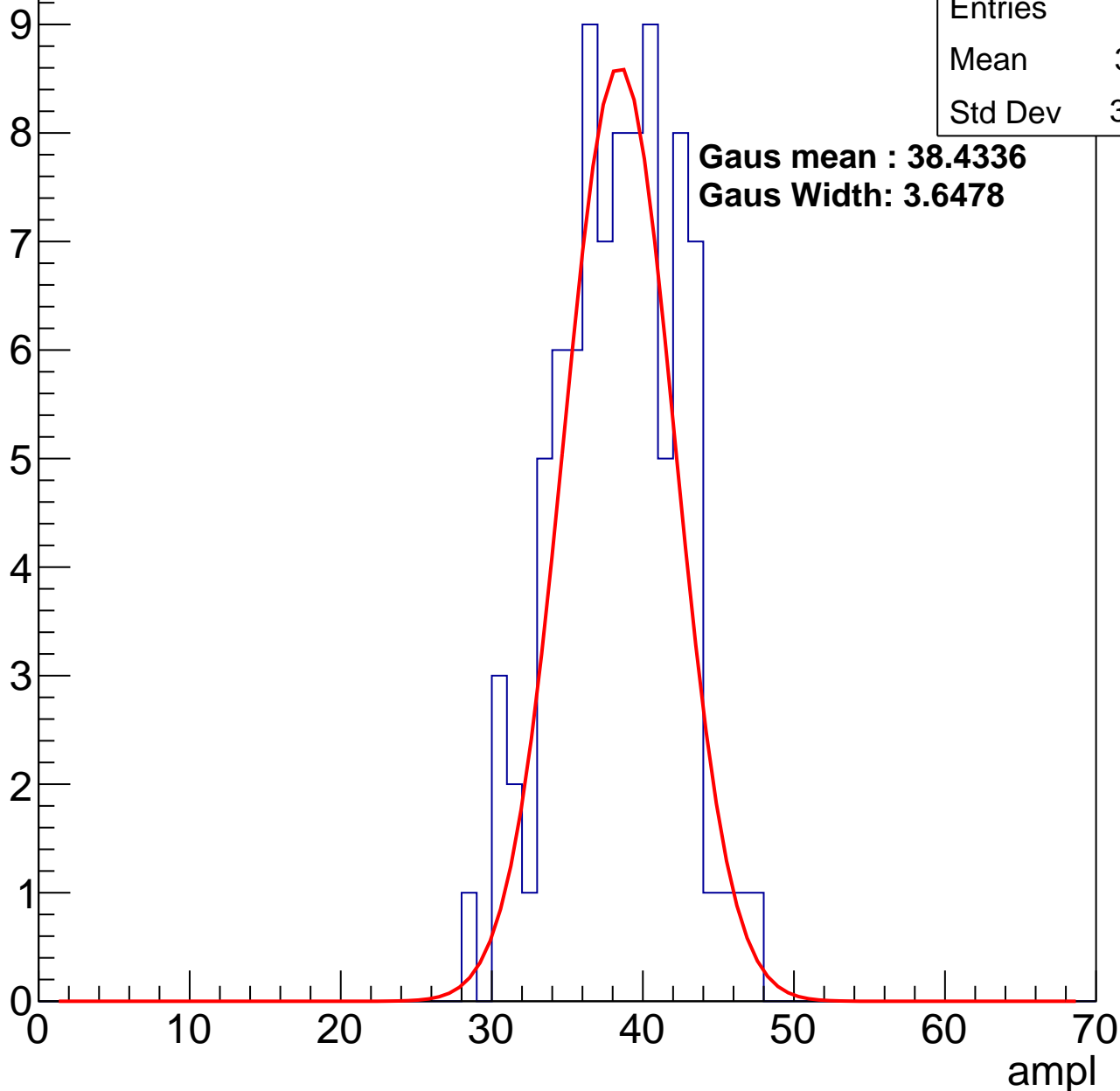
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	37.91
Std Dev	3.917

Gaus mean : 38.4336

Gaus Width: 3.6478



B1L102S, U13-ch117, adc2

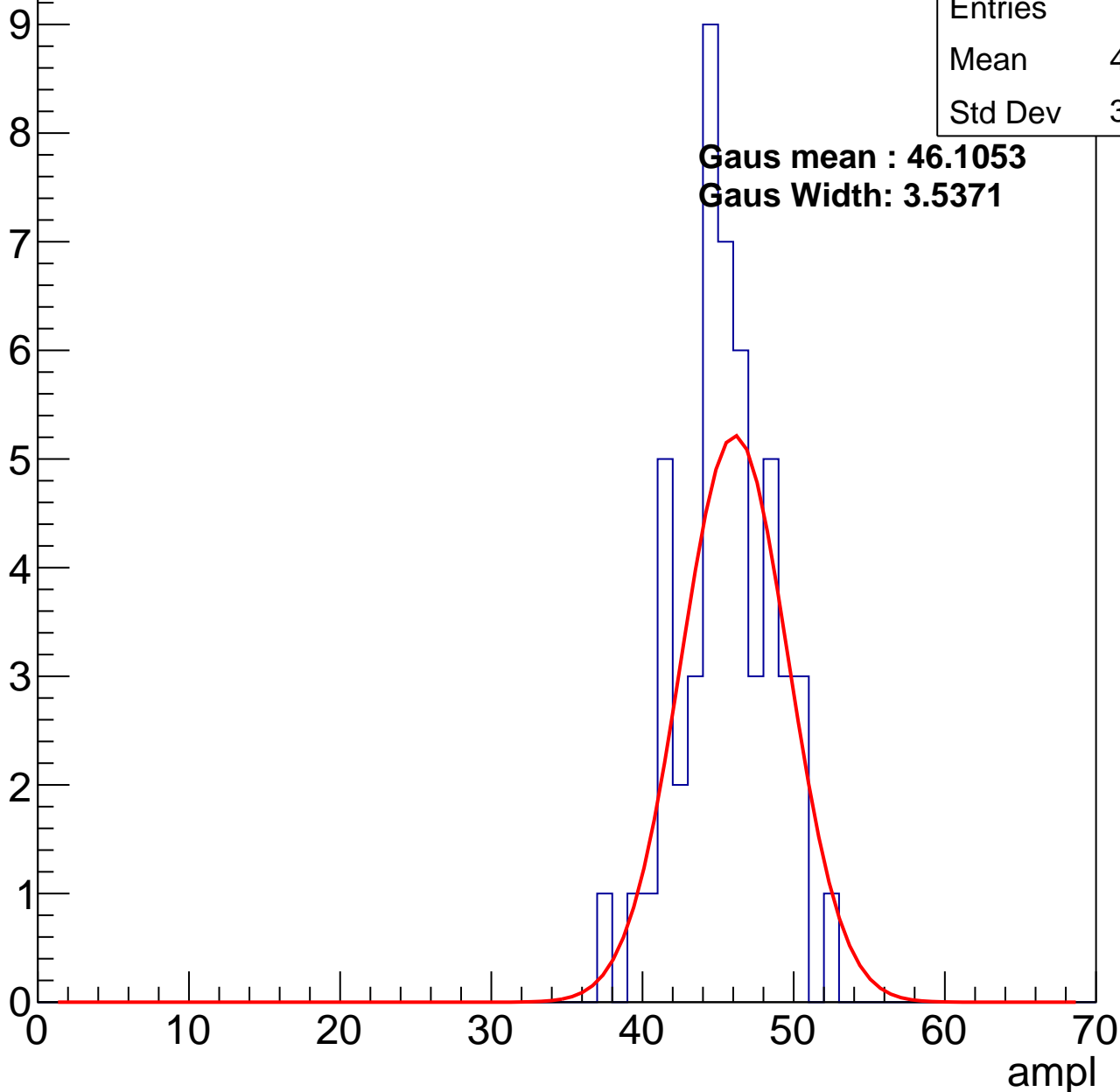
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	45.02
Std Dev	3.095

Gaus mean : 46.1053

Gaus Width: 3.5371

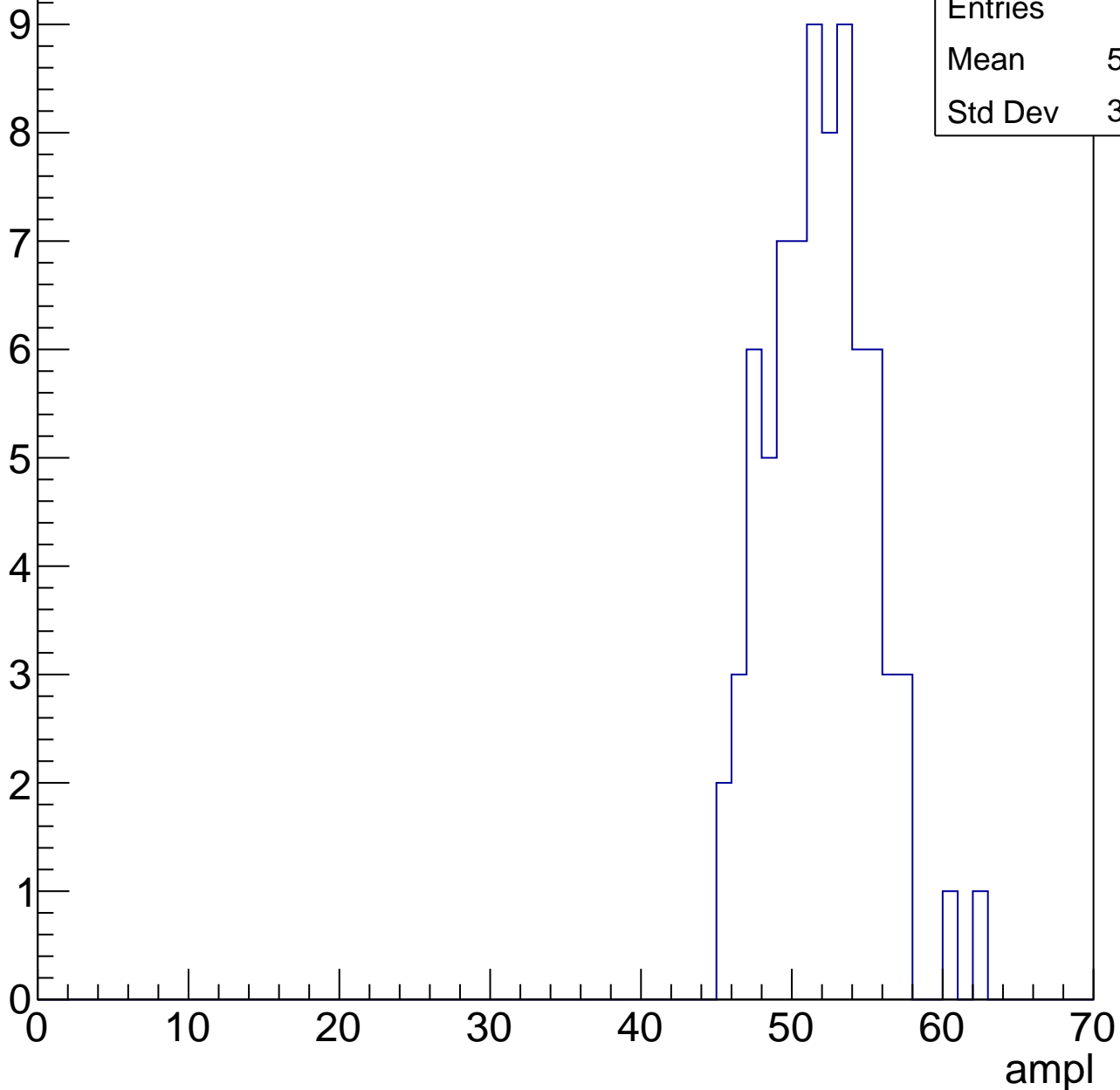


B1L102S, U13-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	51.45
Std Dev	3.416

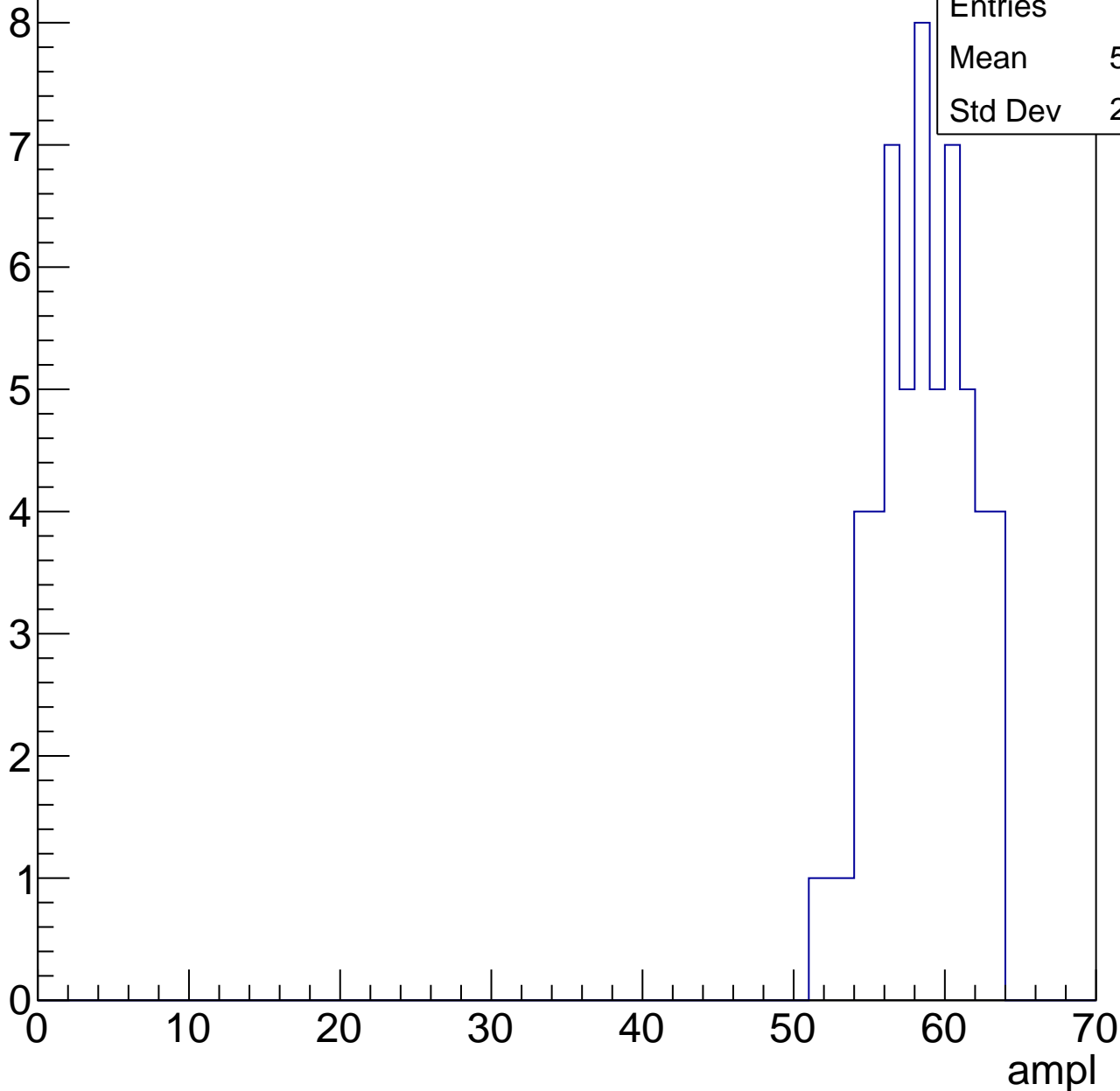


B1L102S, U13-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.09
Std Dev	2.942

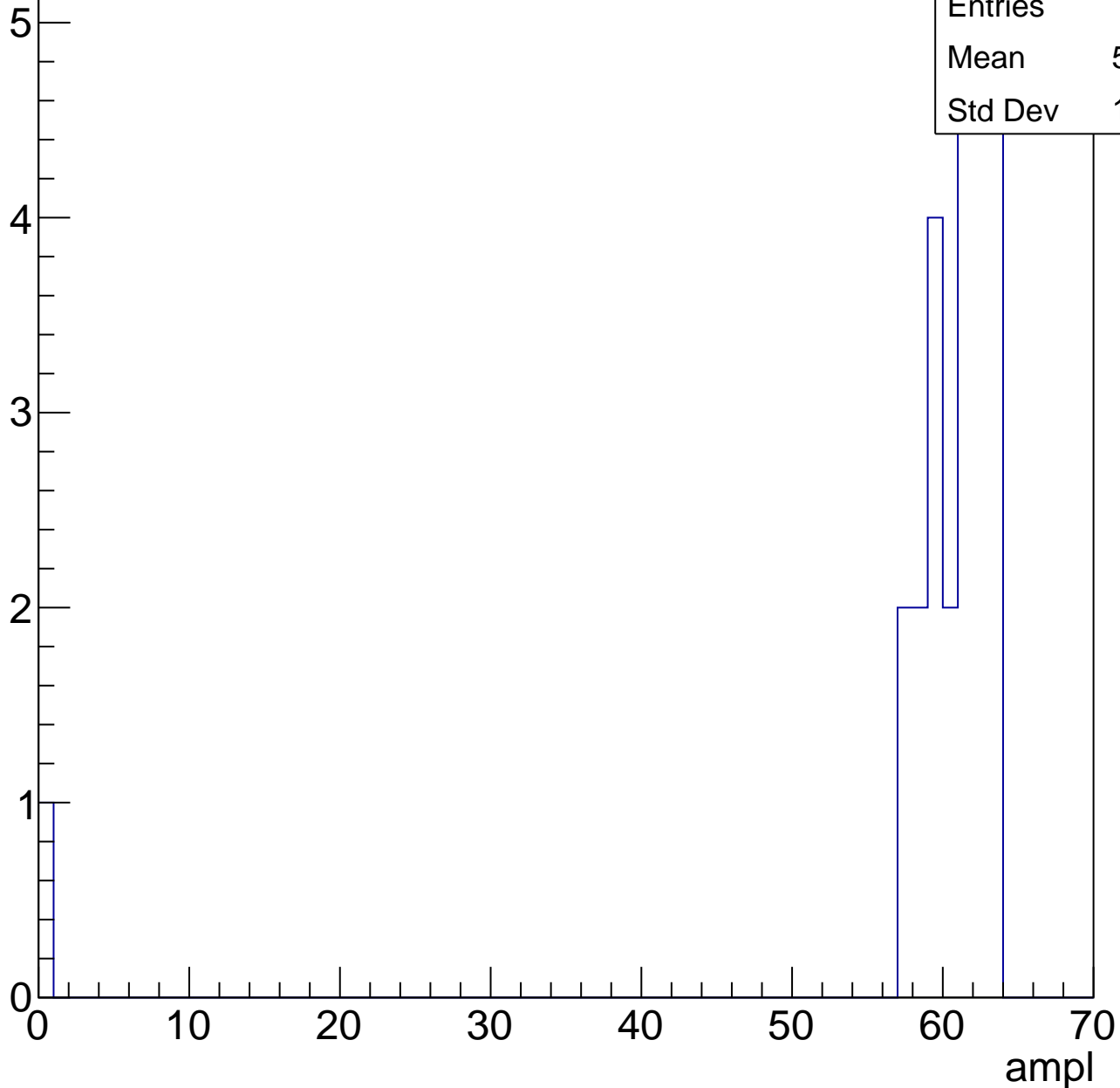


B1L102S, U13-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

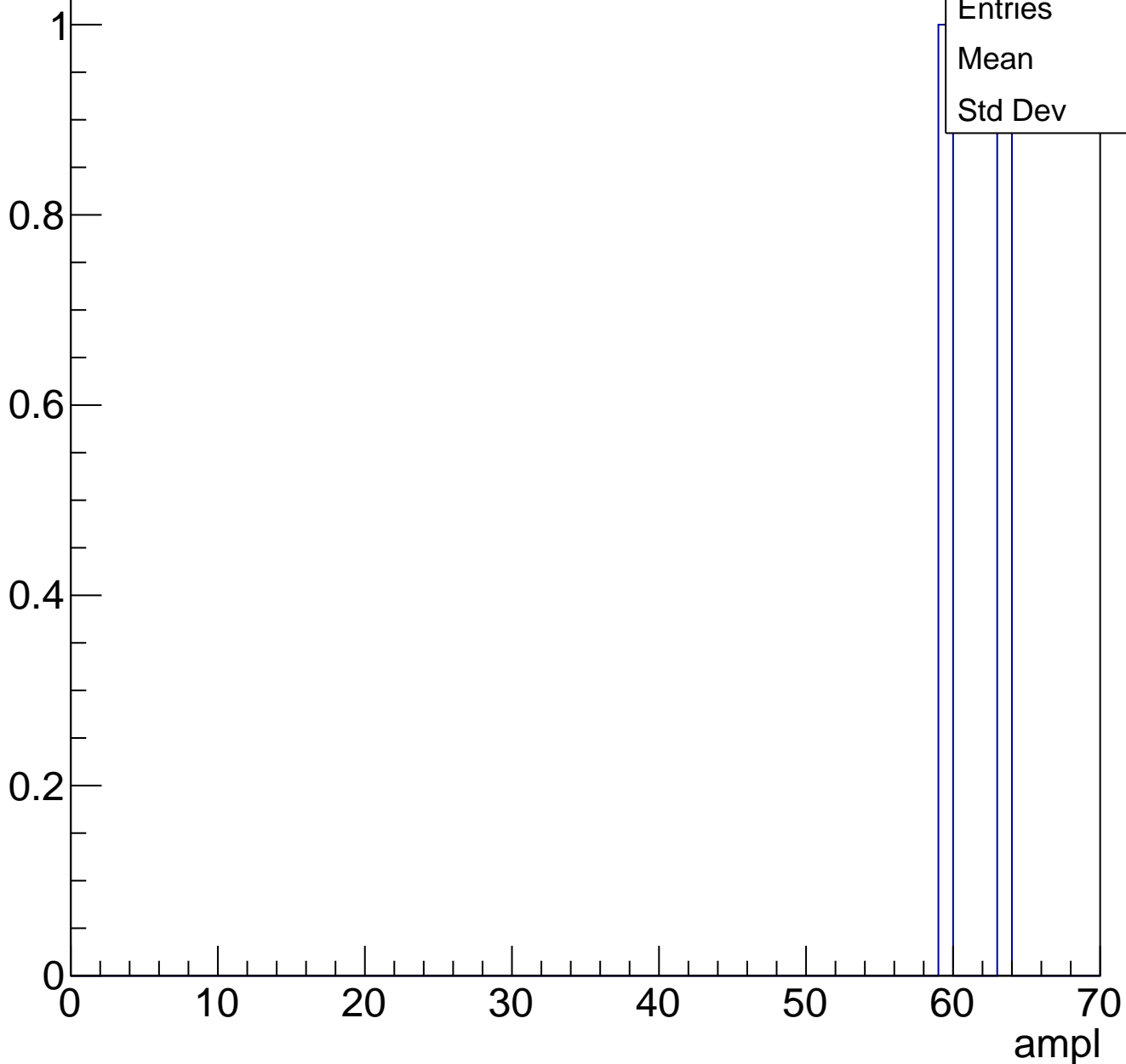
Entries	26
Mean	58.31
Std Dev	11.81



B1L102S, U13-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch118, adc0

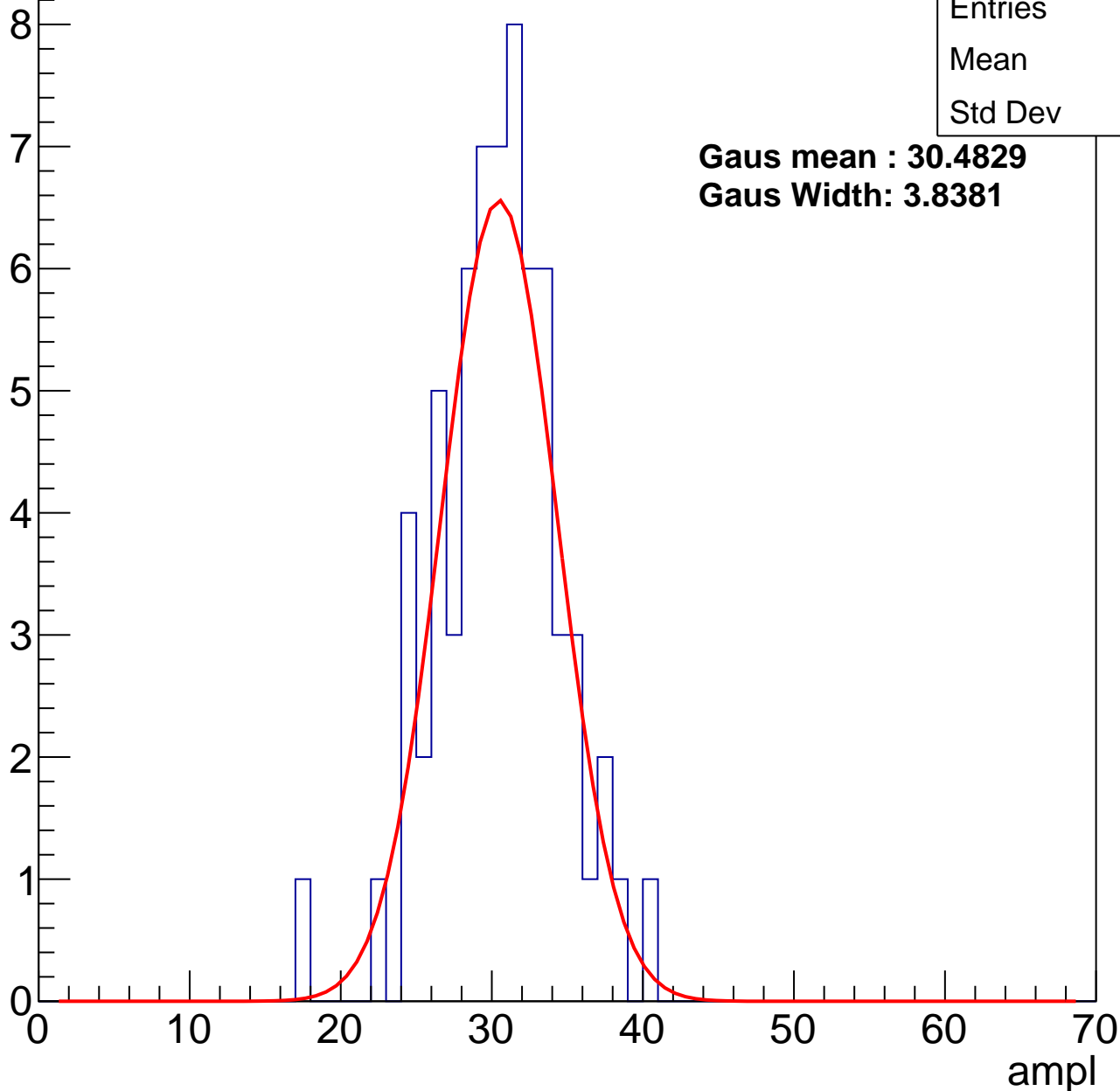
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30
Std Dev	4

Gaus mean : 30.4829

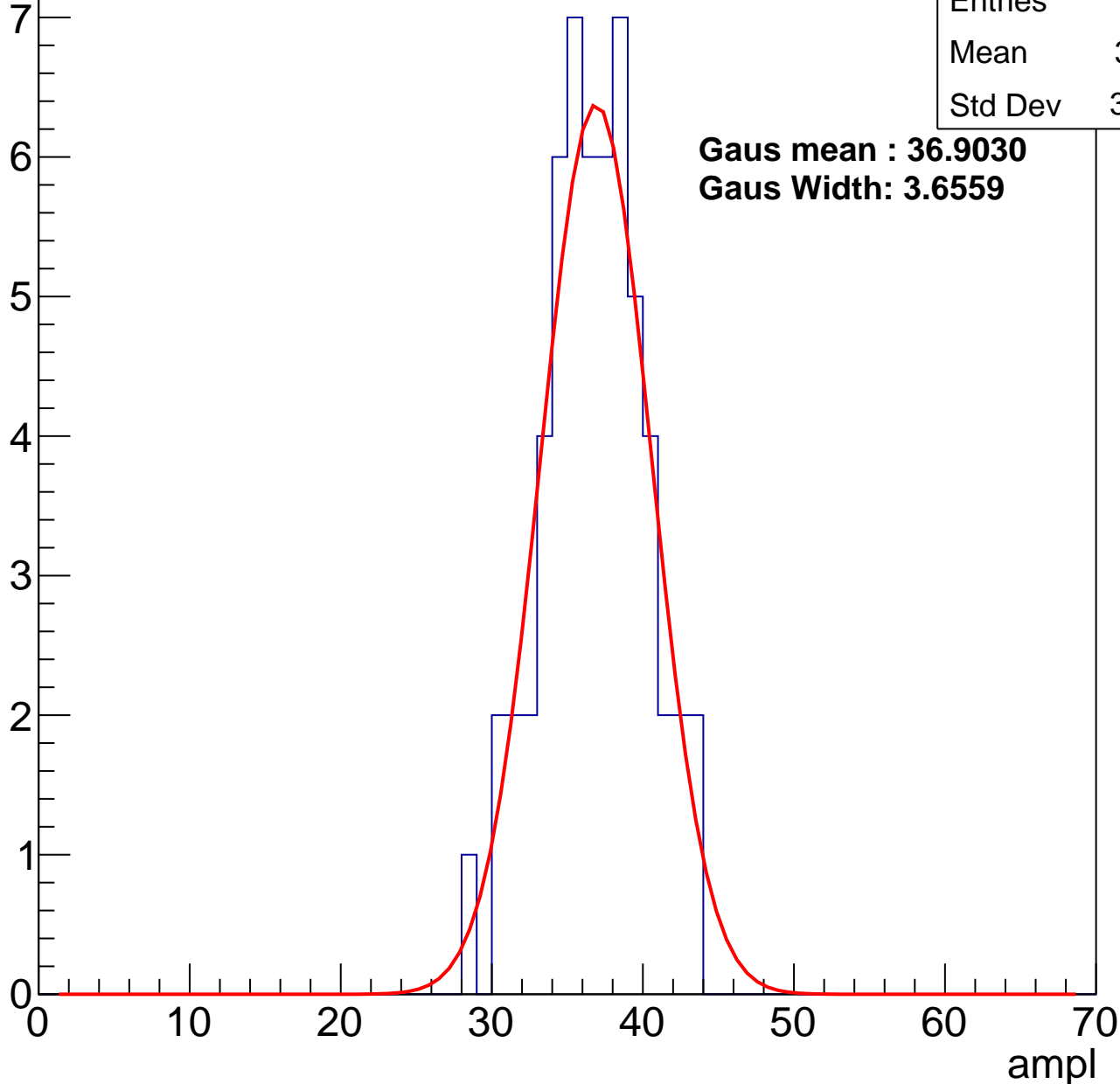
Gaus Width: 3.8381



B1L102S, U13-ch118, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch118, adc2

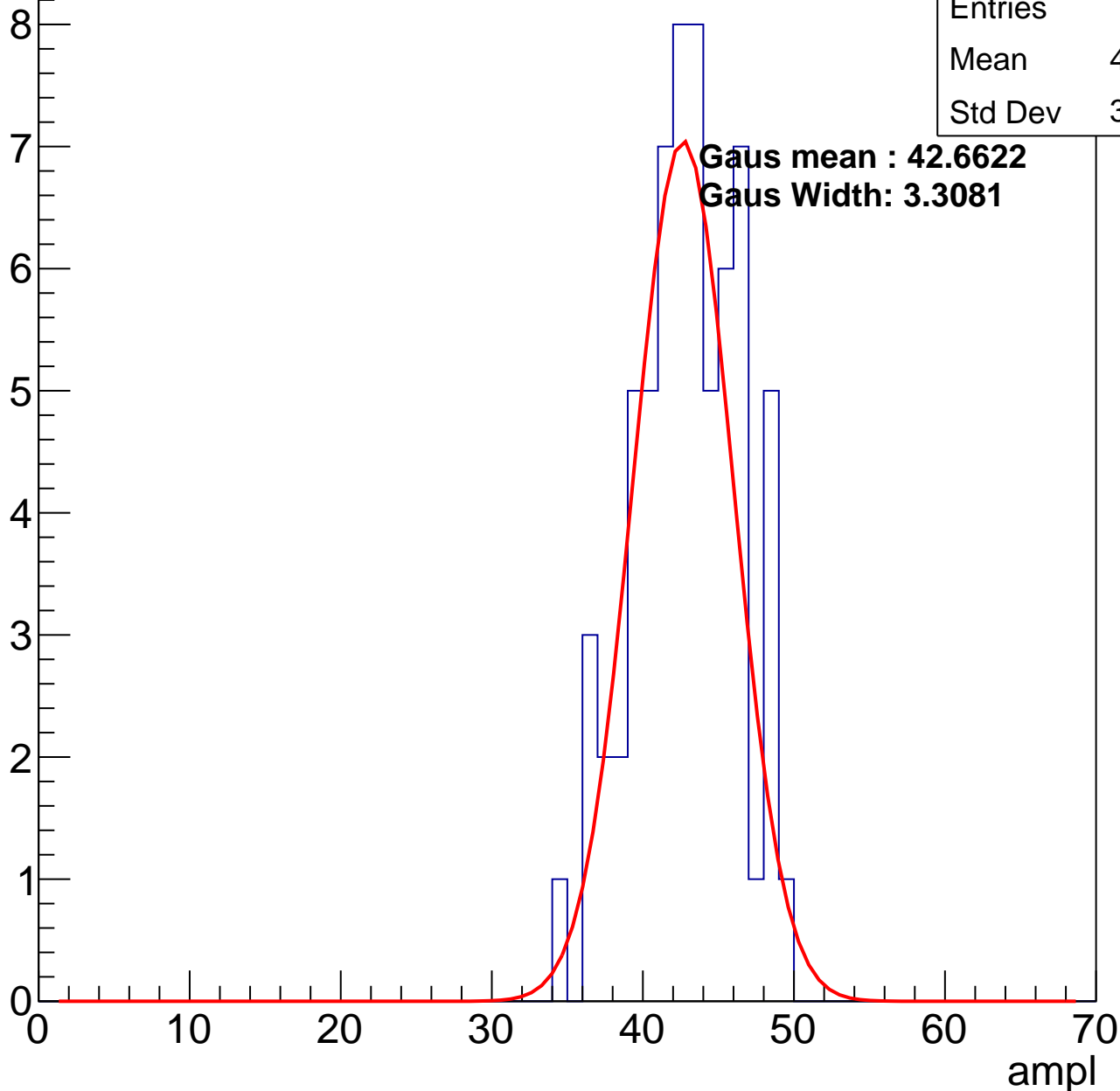
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.45
Std Dev	3.394

Gaus mean : 42.6622

Gaus Width: 3.3081

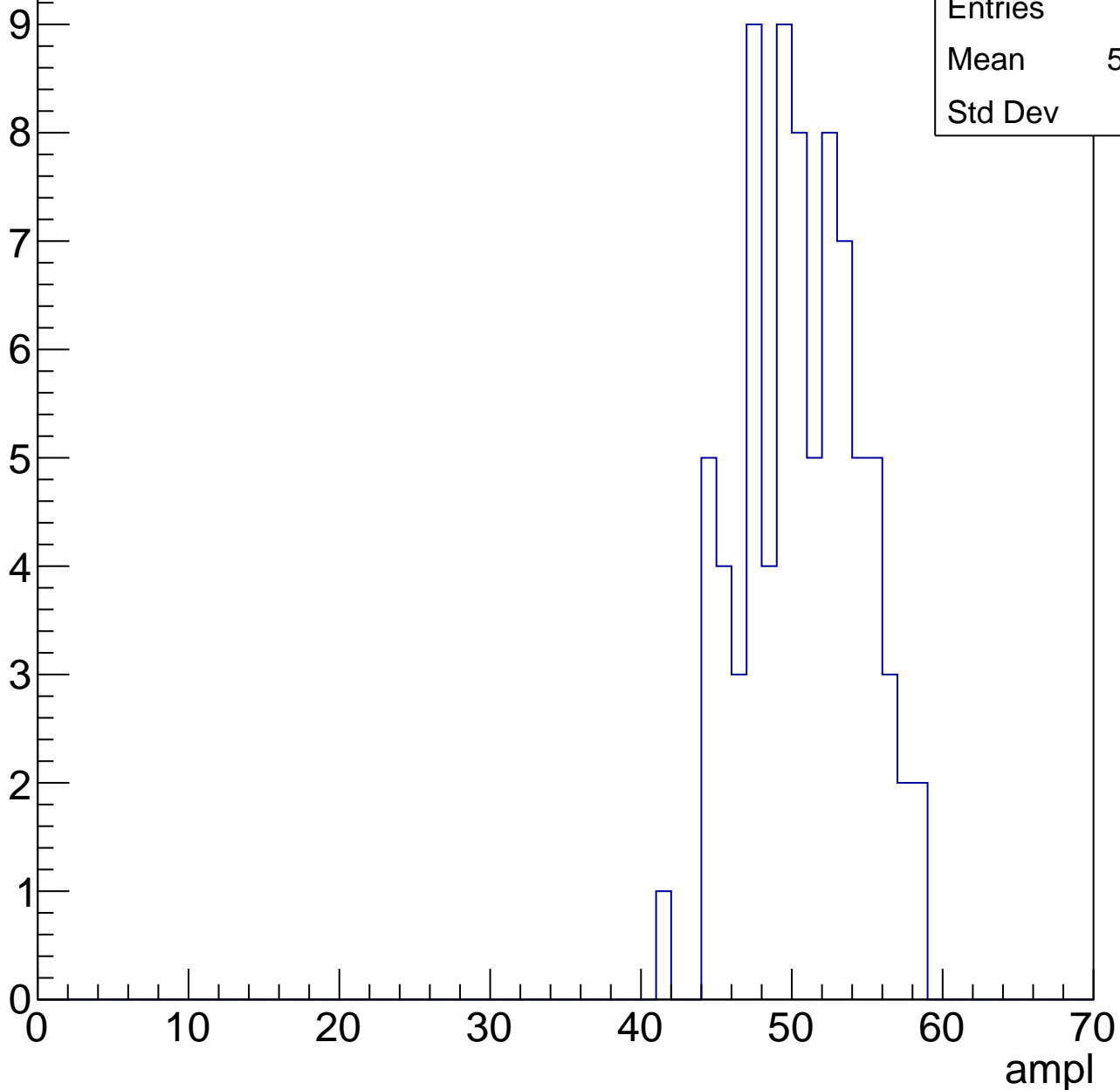


B1L102S, U13-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	50.25
Std Dev	3.79

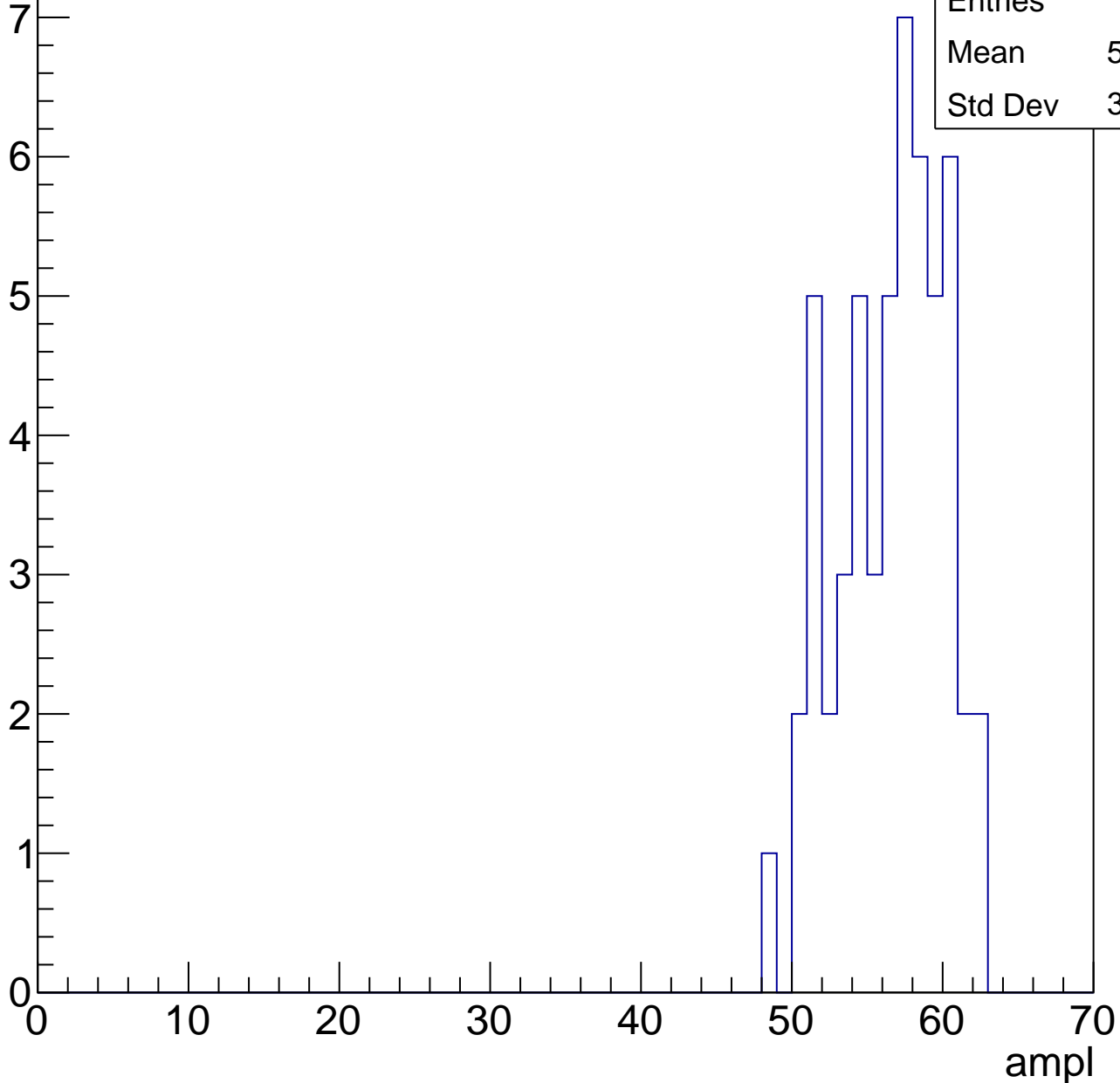


B1L102S, U13-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56.09
Std Dev	3.433

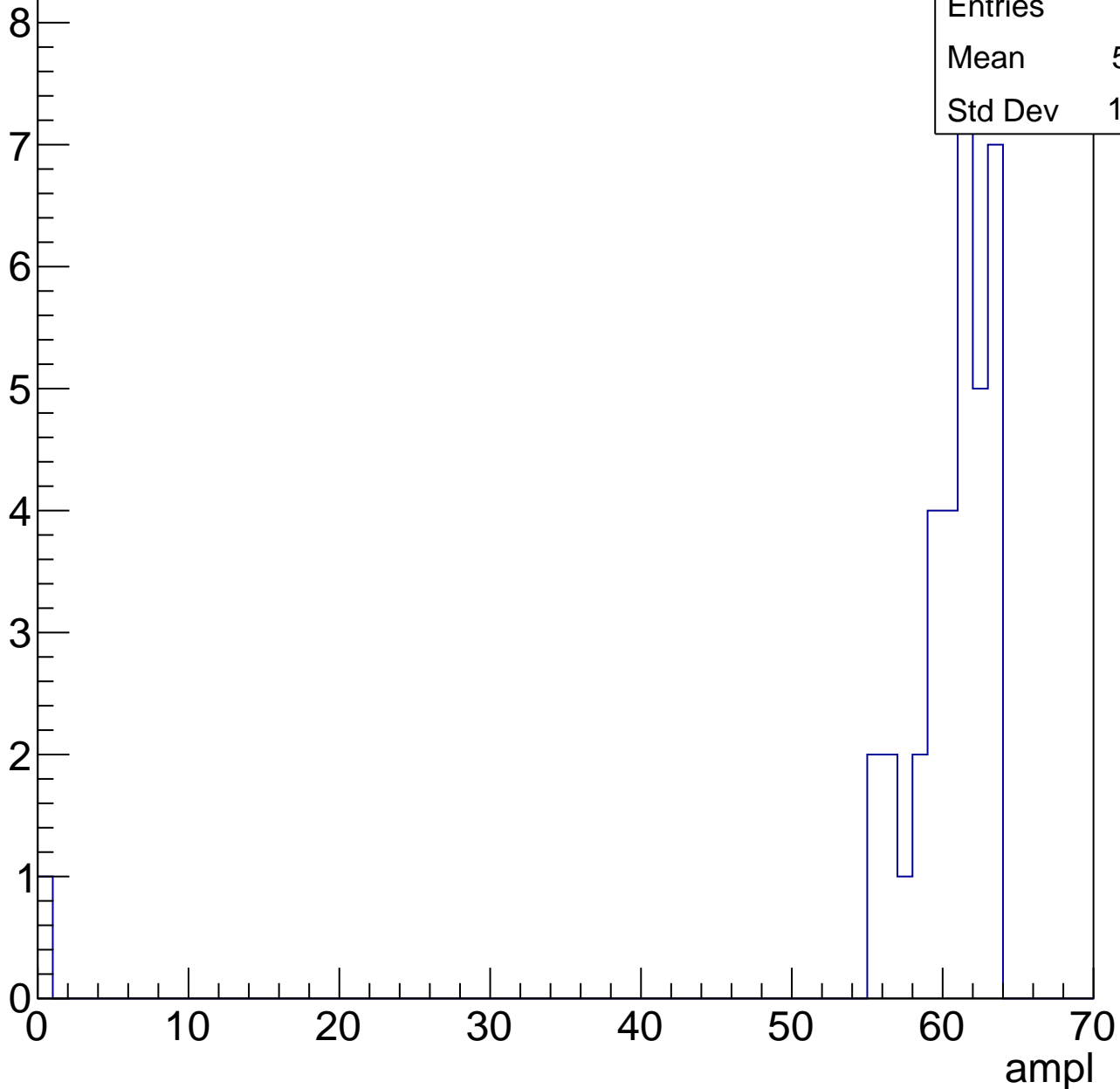


B1L102S, U13-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

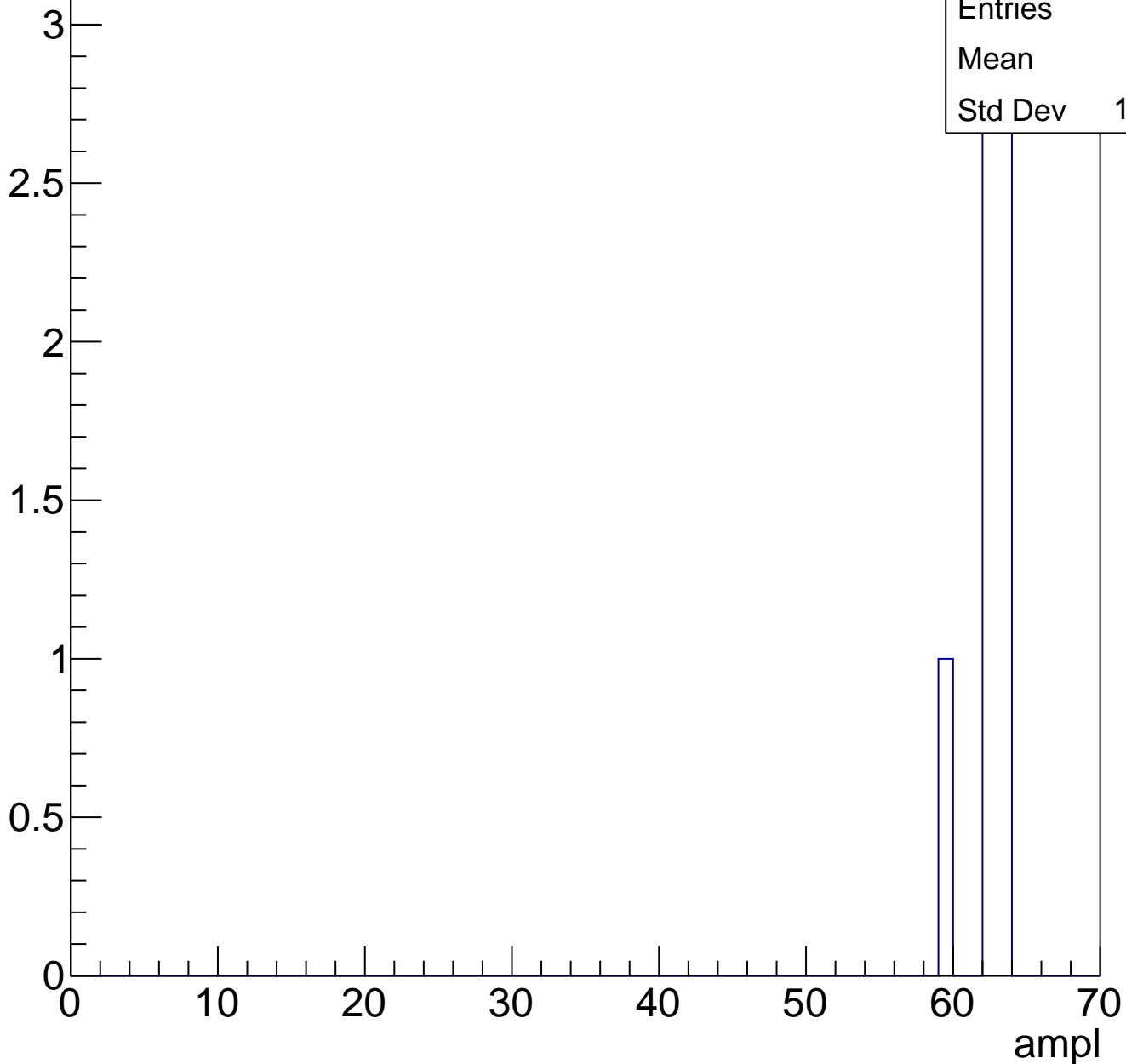
Entries	36
Mean	58.61
Std Dev	10.17



B1L102S, U13-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U13-ch119, adc0

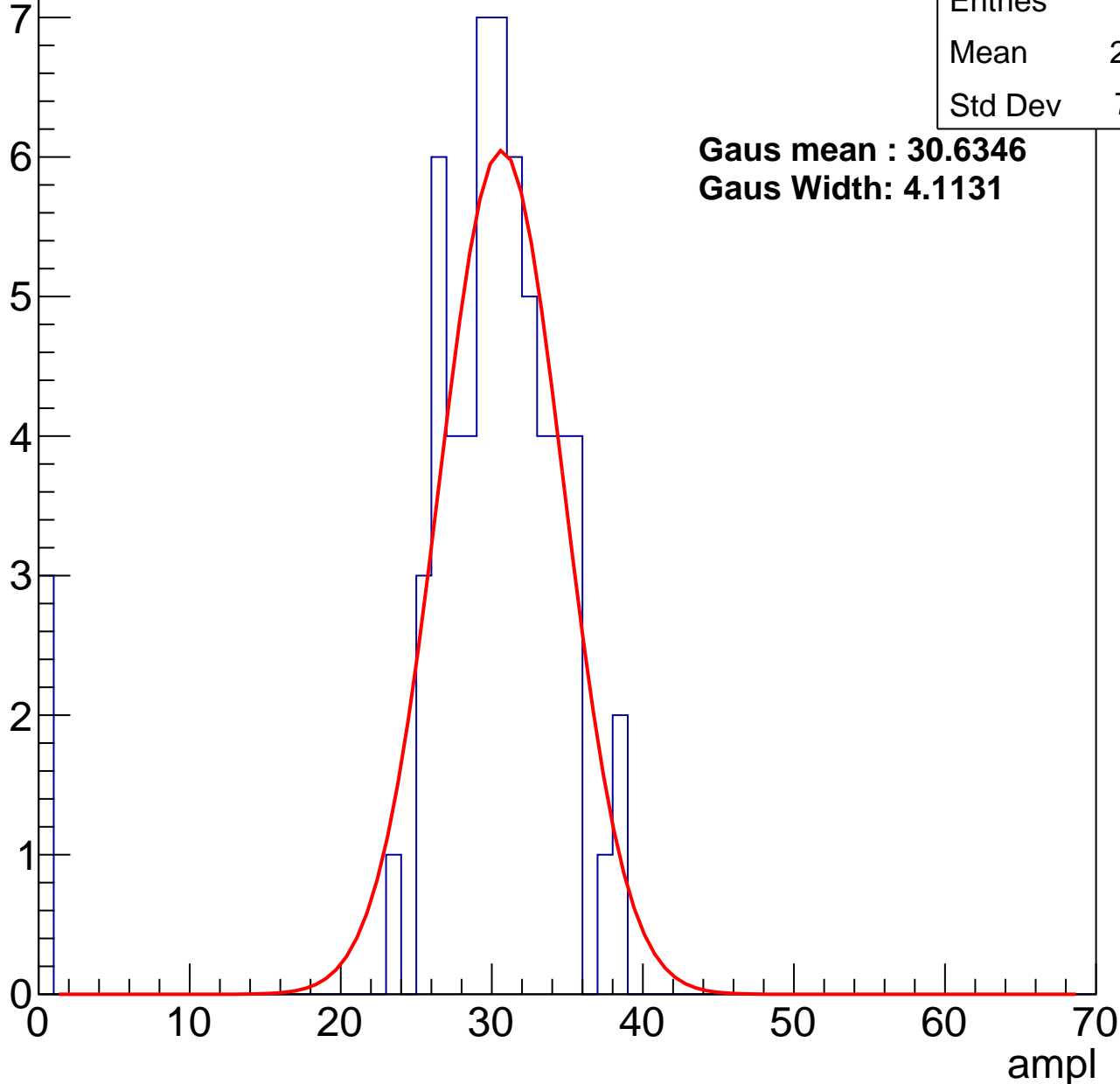
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.75
Std Dev	7.341

Gaus mean : 30.6346

Gaus Width: 4.1131



B1L102S, U13-ch119, adc1

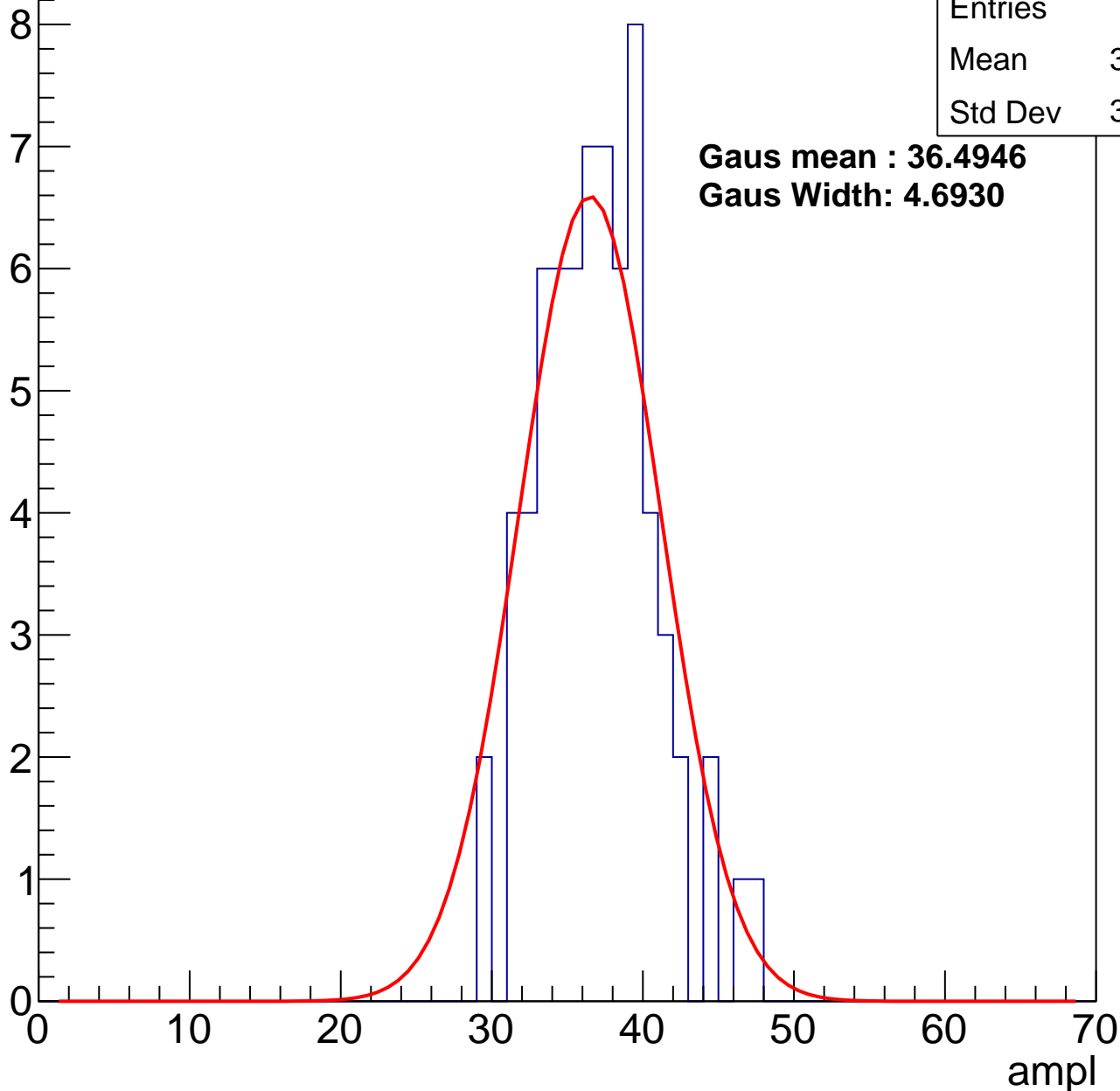
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.54
Std Dev	3.786

Gaus mean : 36.4946

Gaus Width: 4.6930



B1L102S, U13-ch119, adc2

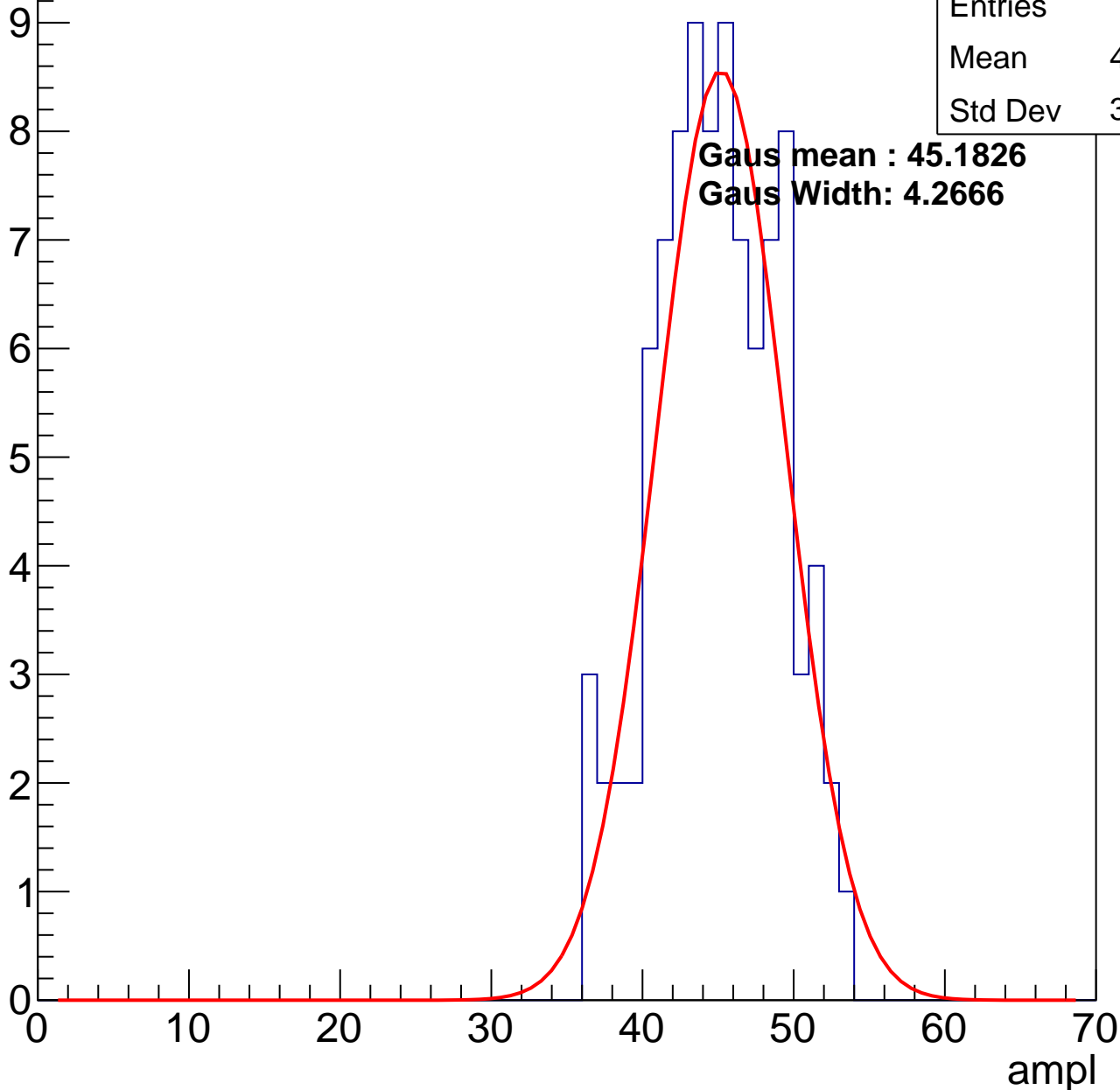
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	94
Mean	44.53
Std Dev	3.997

Gaus mean : 45.1826

Gaus Width: 4.2666

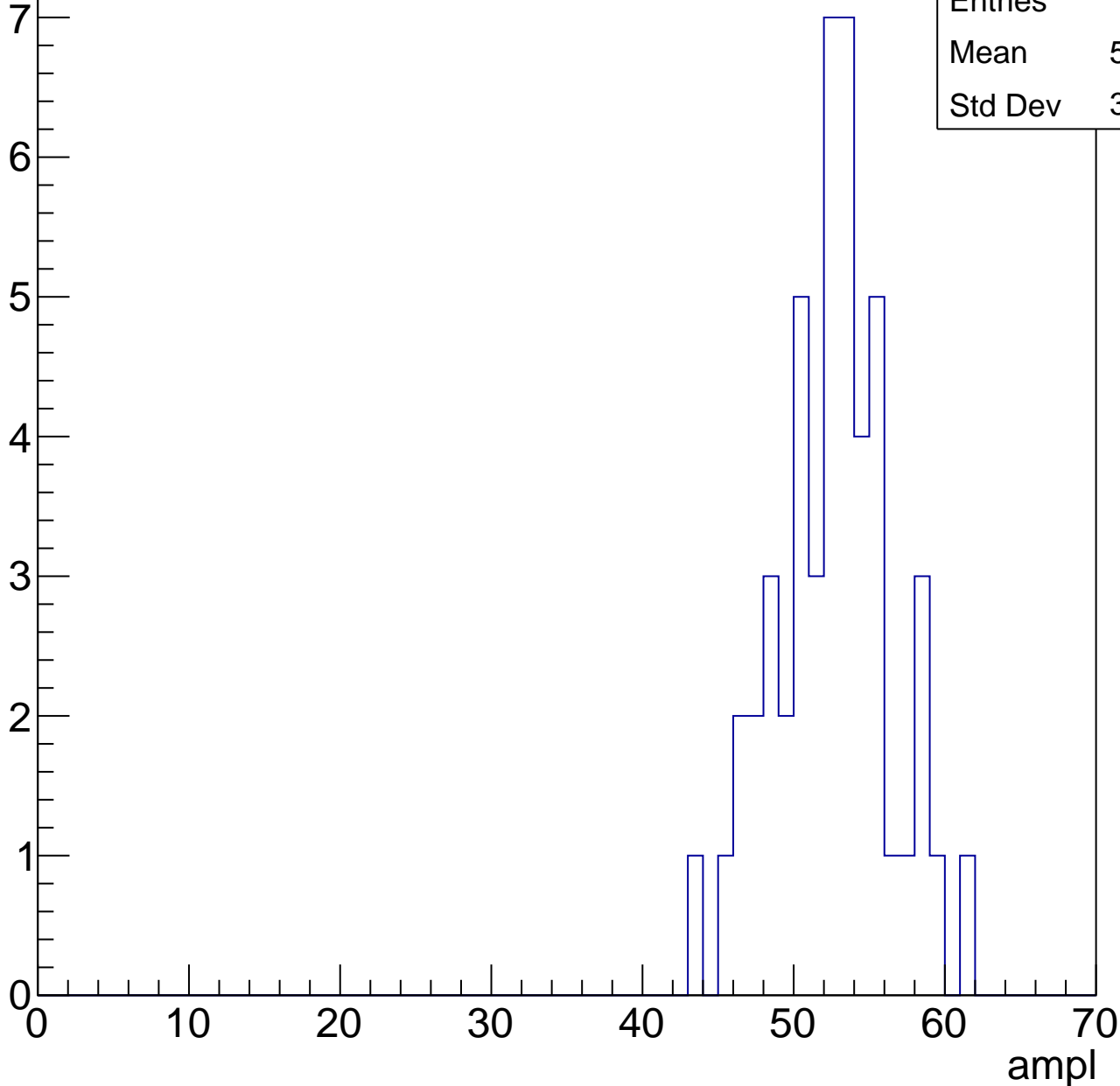


B1L102S, U13-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

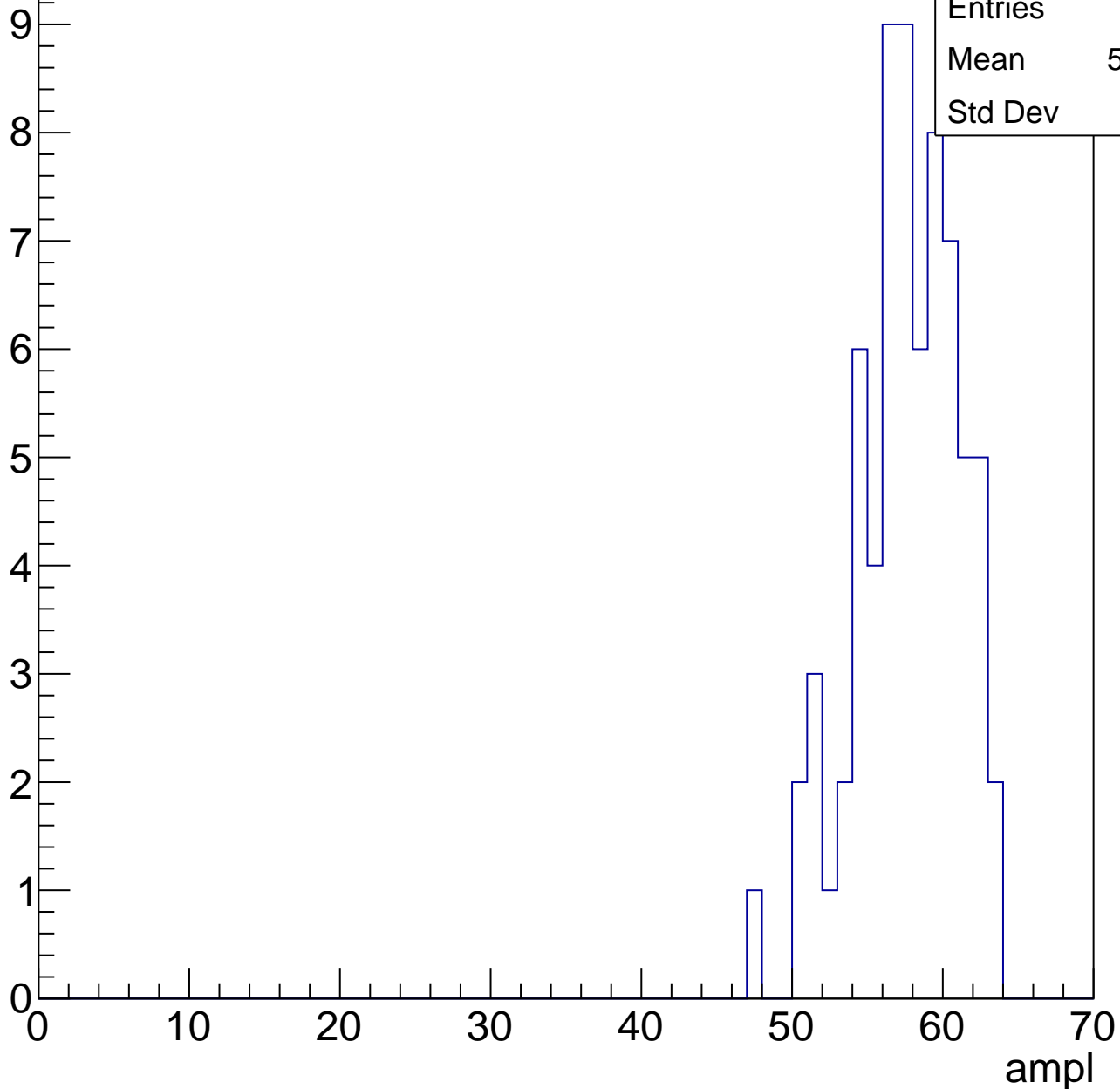
Entries	49
Mean	52.08
Std Dev	3.746



B1L102S, U13-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

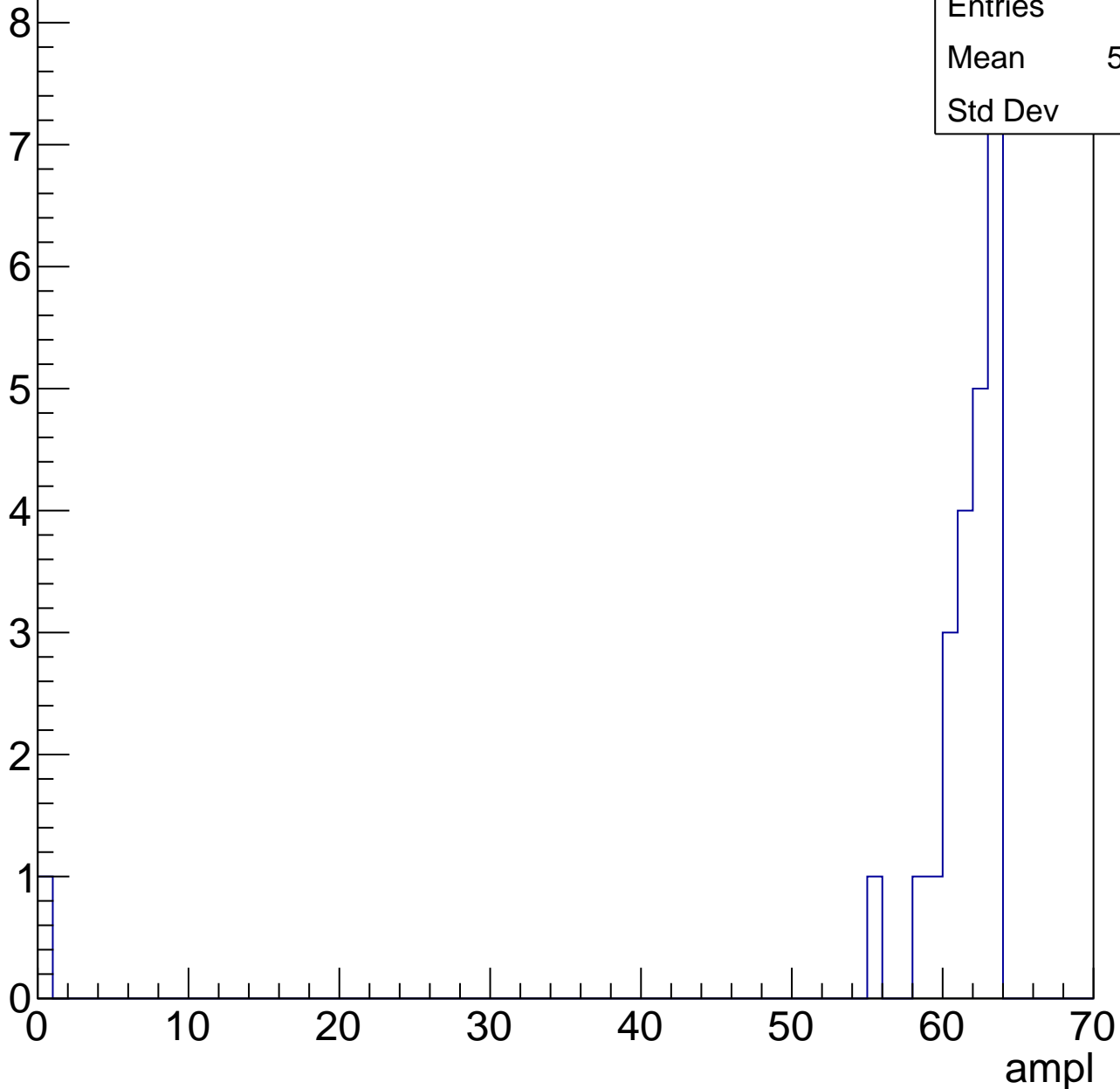


B1L102S, U13-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	24
Mean	58.75
Std Dev	12.4



B1L102S, U13-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch120, adc0

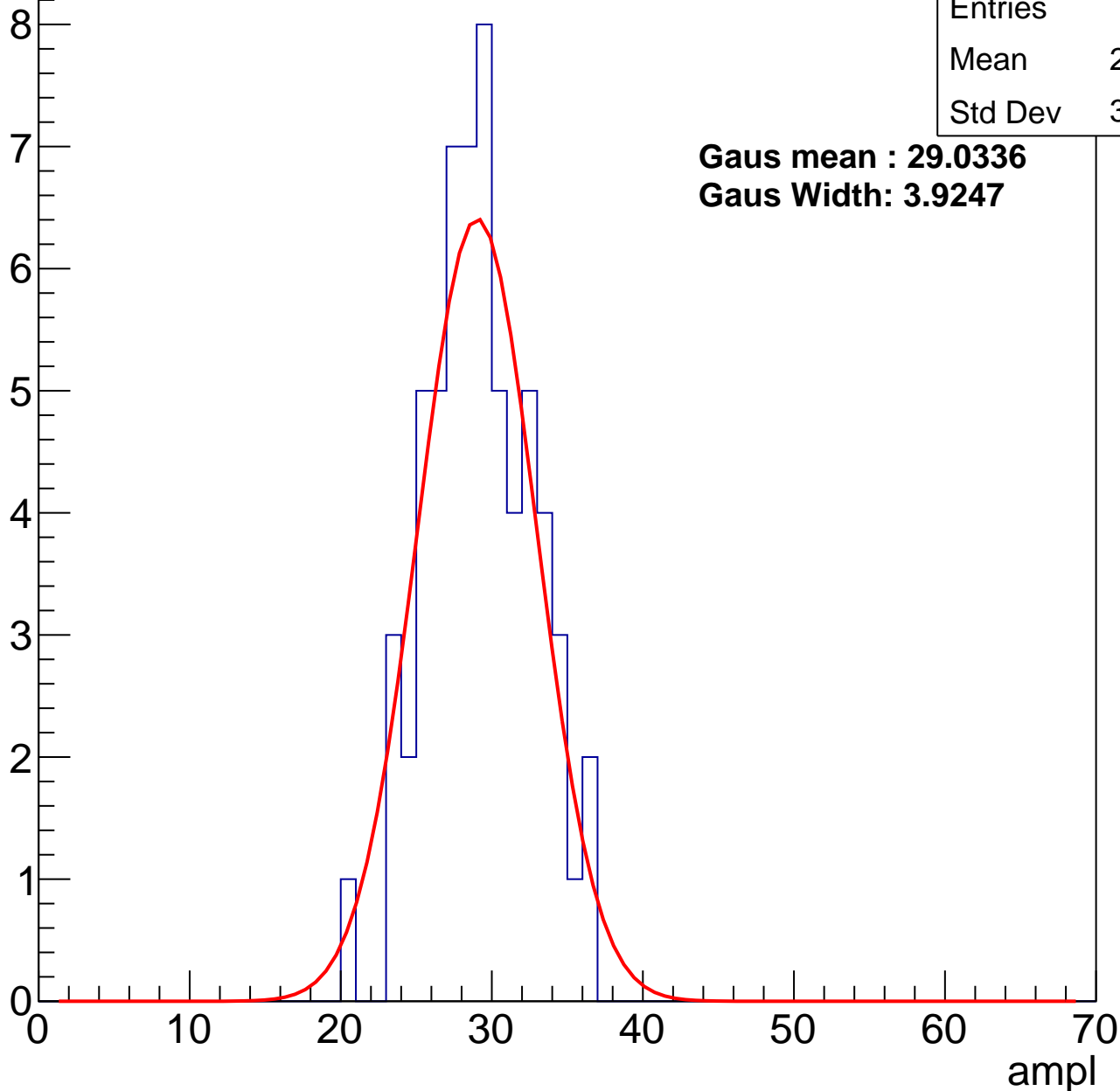
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.77
Std Dev	3.443

Gaus mean : 29.0336

Gaus Width: 3.9247



B1L102S, U13-ch120, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	93
Mean	36.58
Std Dev	4.246

Gaus mean : 37.1873

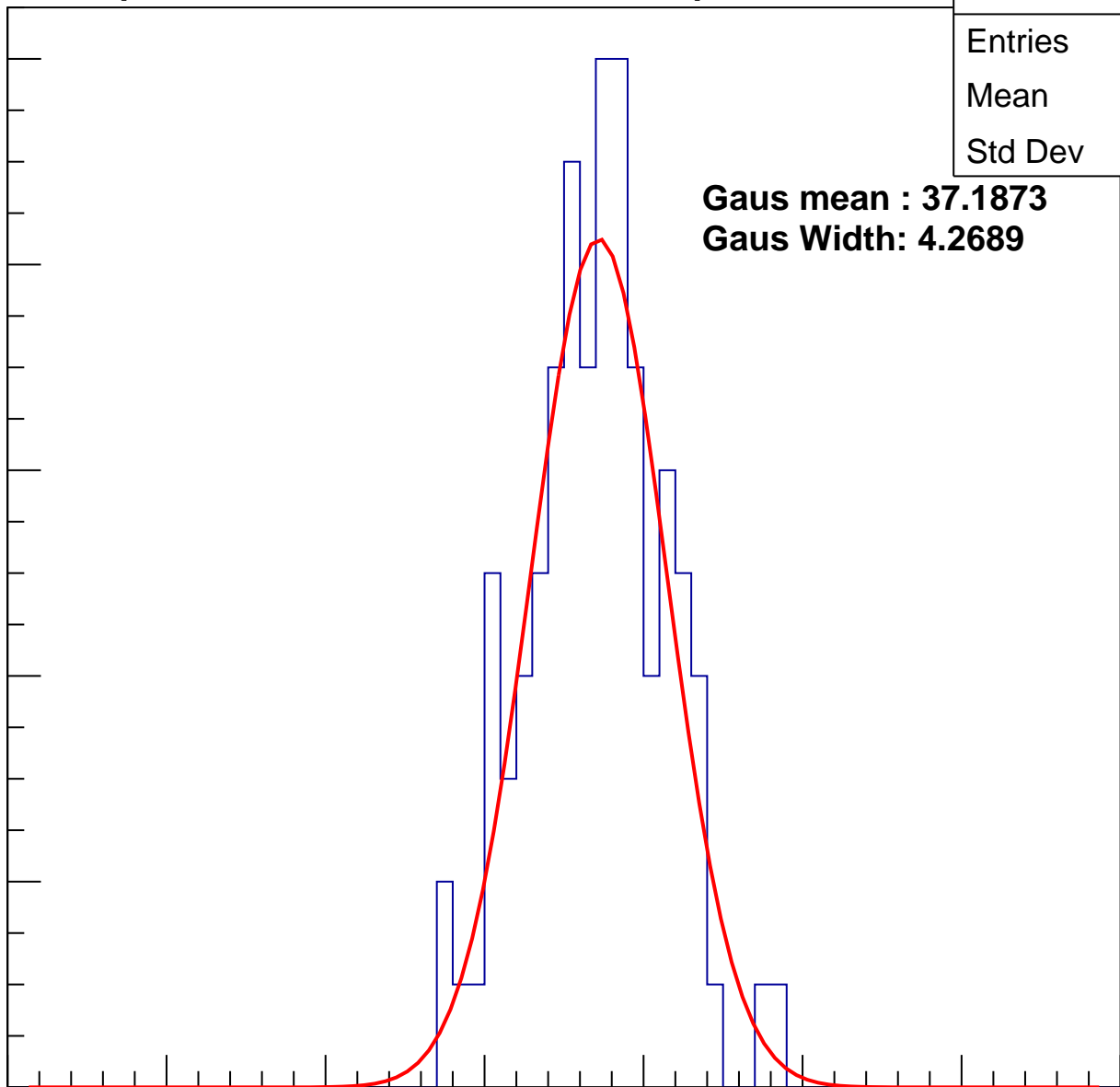
Gaus Width: 4.2689

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U13-ch120, adc2

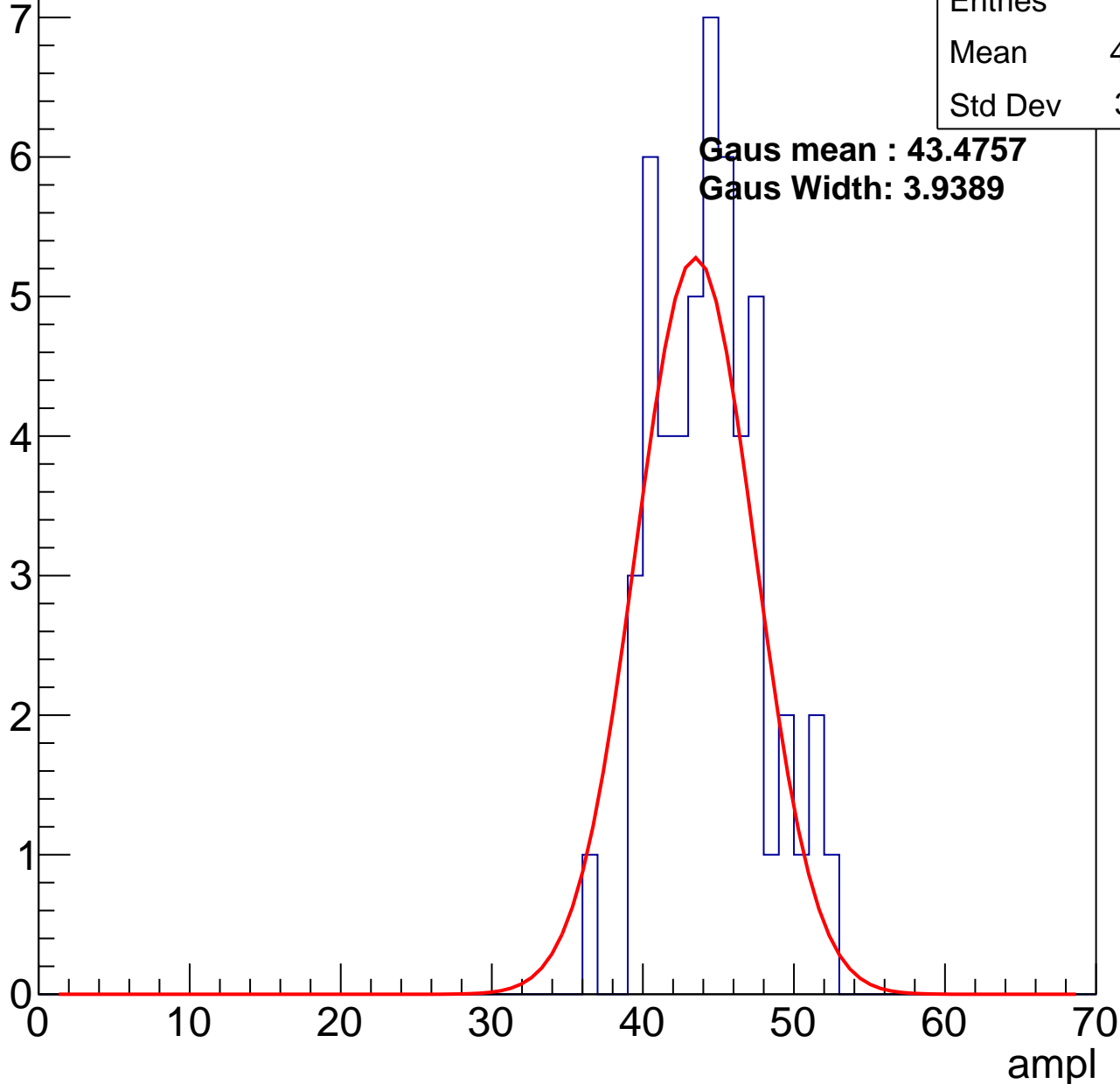
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	43.98
Std Dev	3.461

Gaus mean : 43.4757

Gaus Width: 3.9389

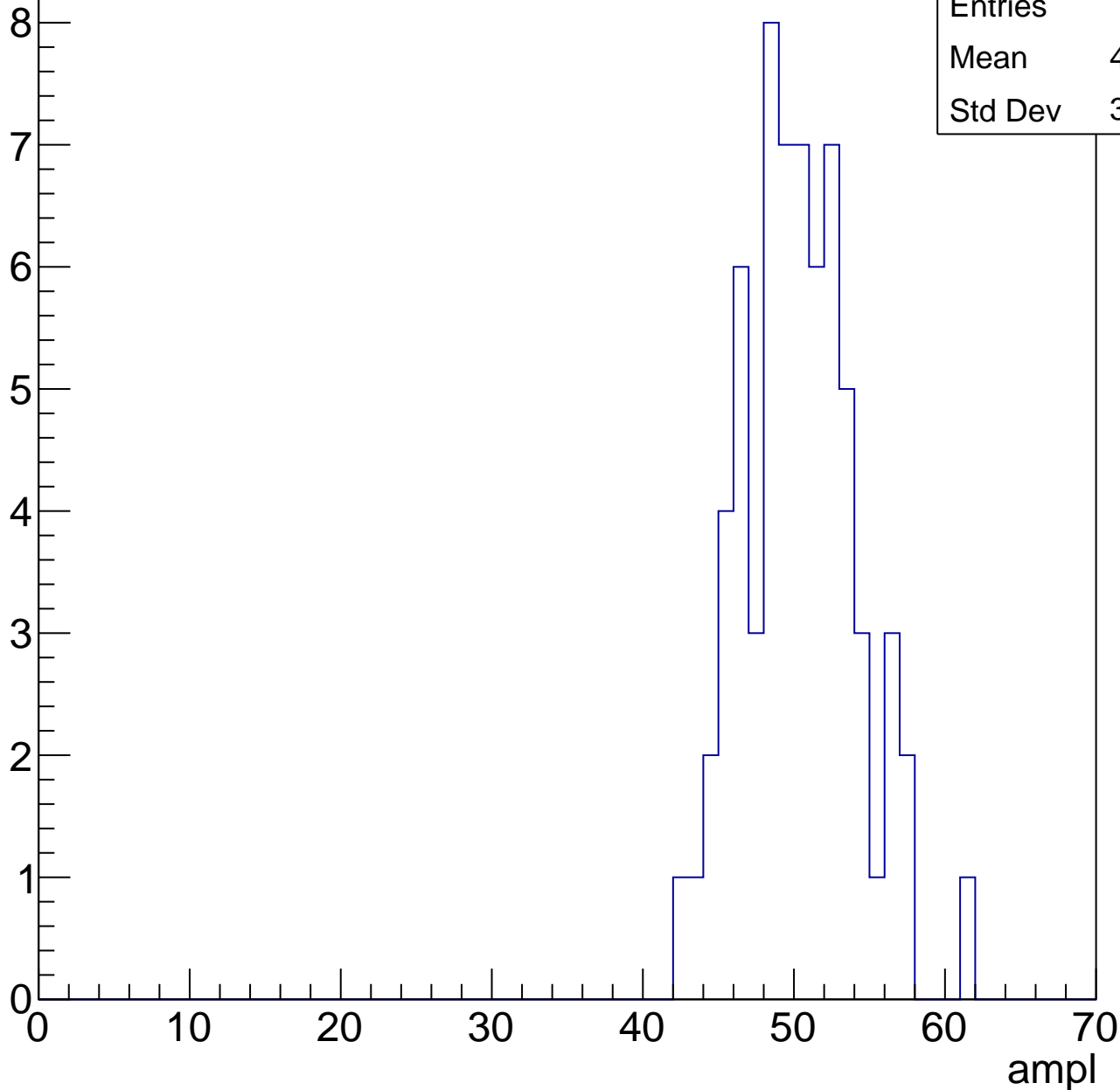


B1L102S, U13-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.88
Std Dev	3.716

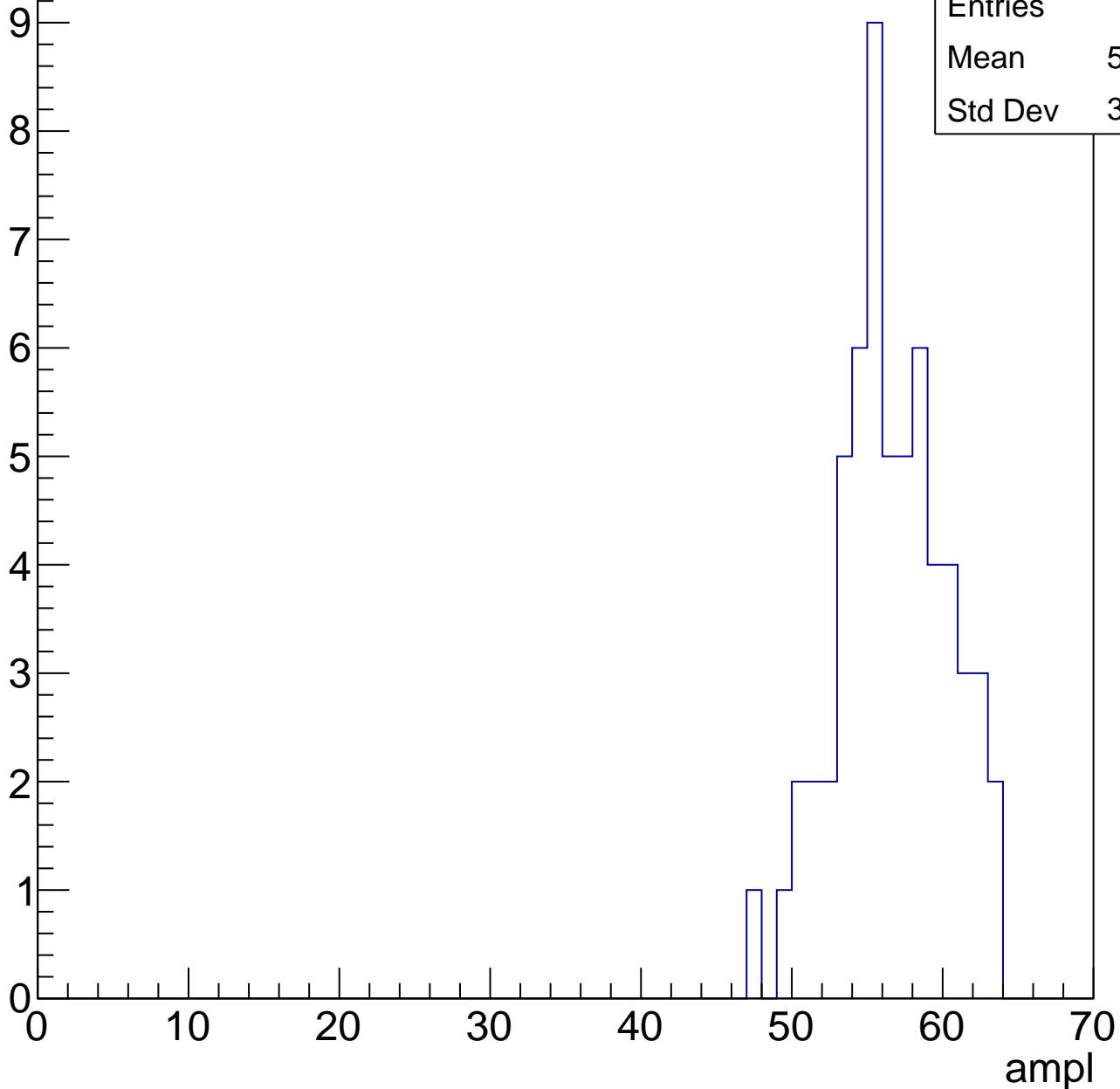


B1L102S, U13-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

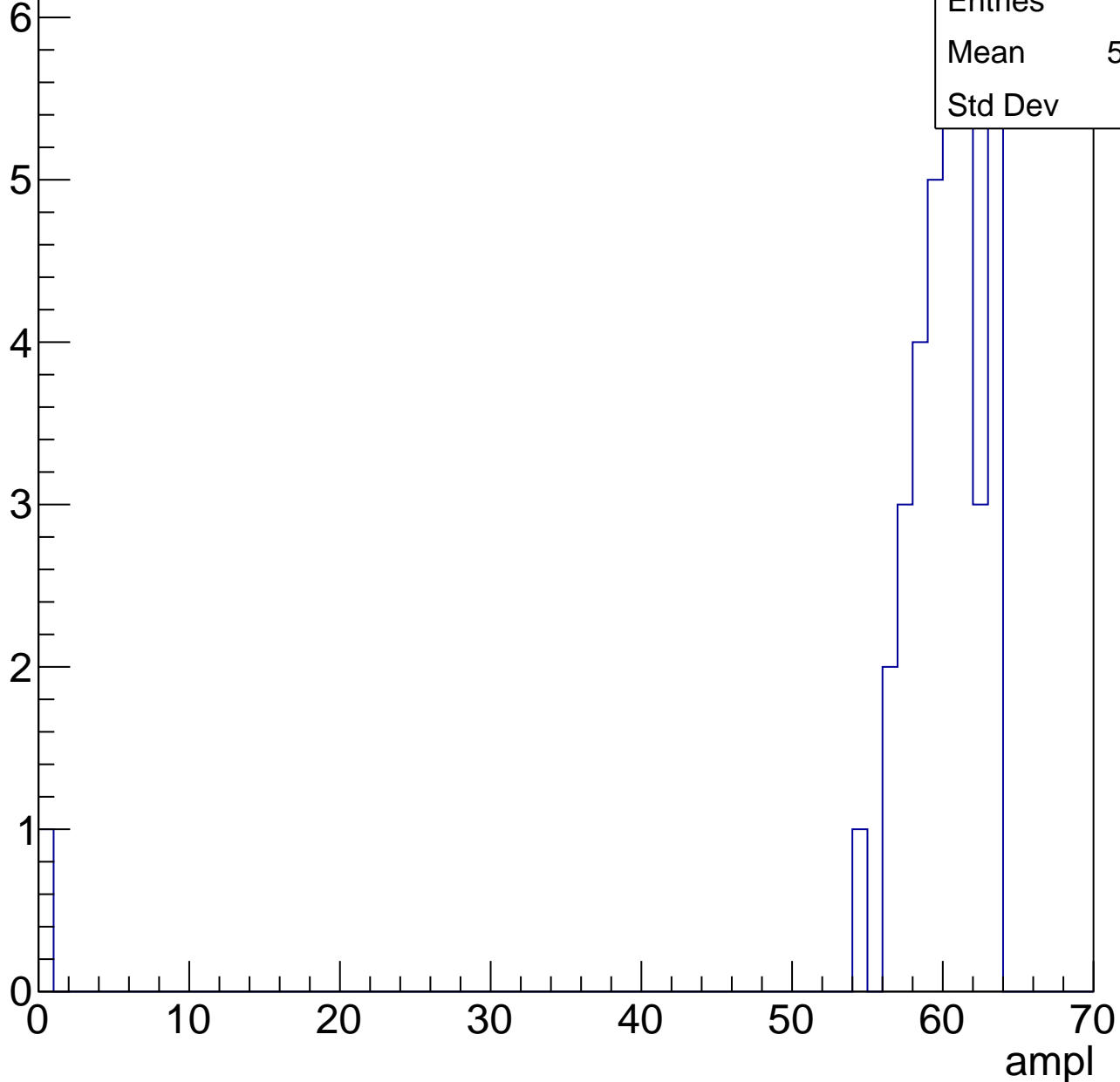
Entries	60
Mean	56.17
Std Dev	3.583



B1L102S, U13-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

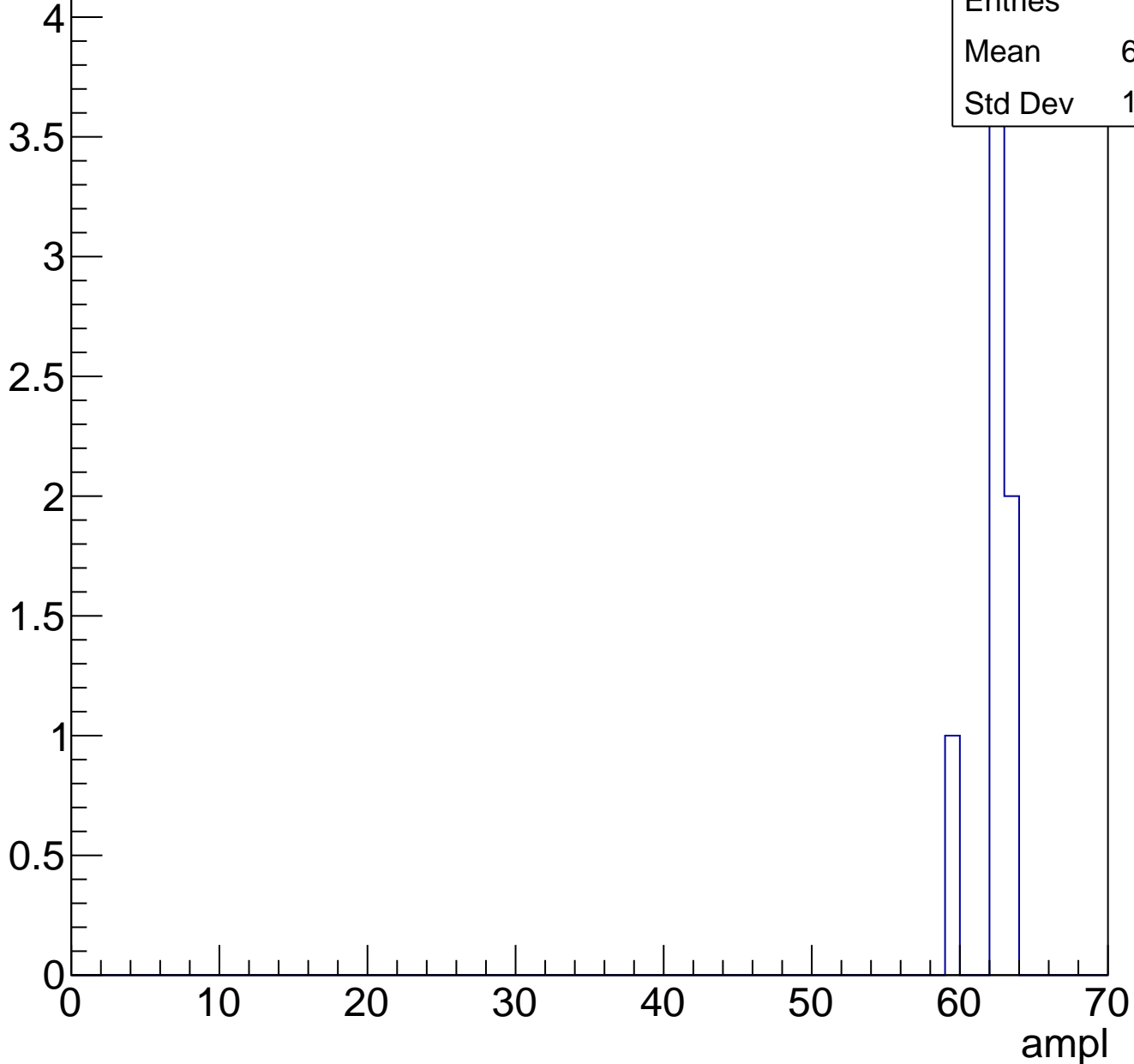
Entry



B1L102S, U13-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch121, adc0

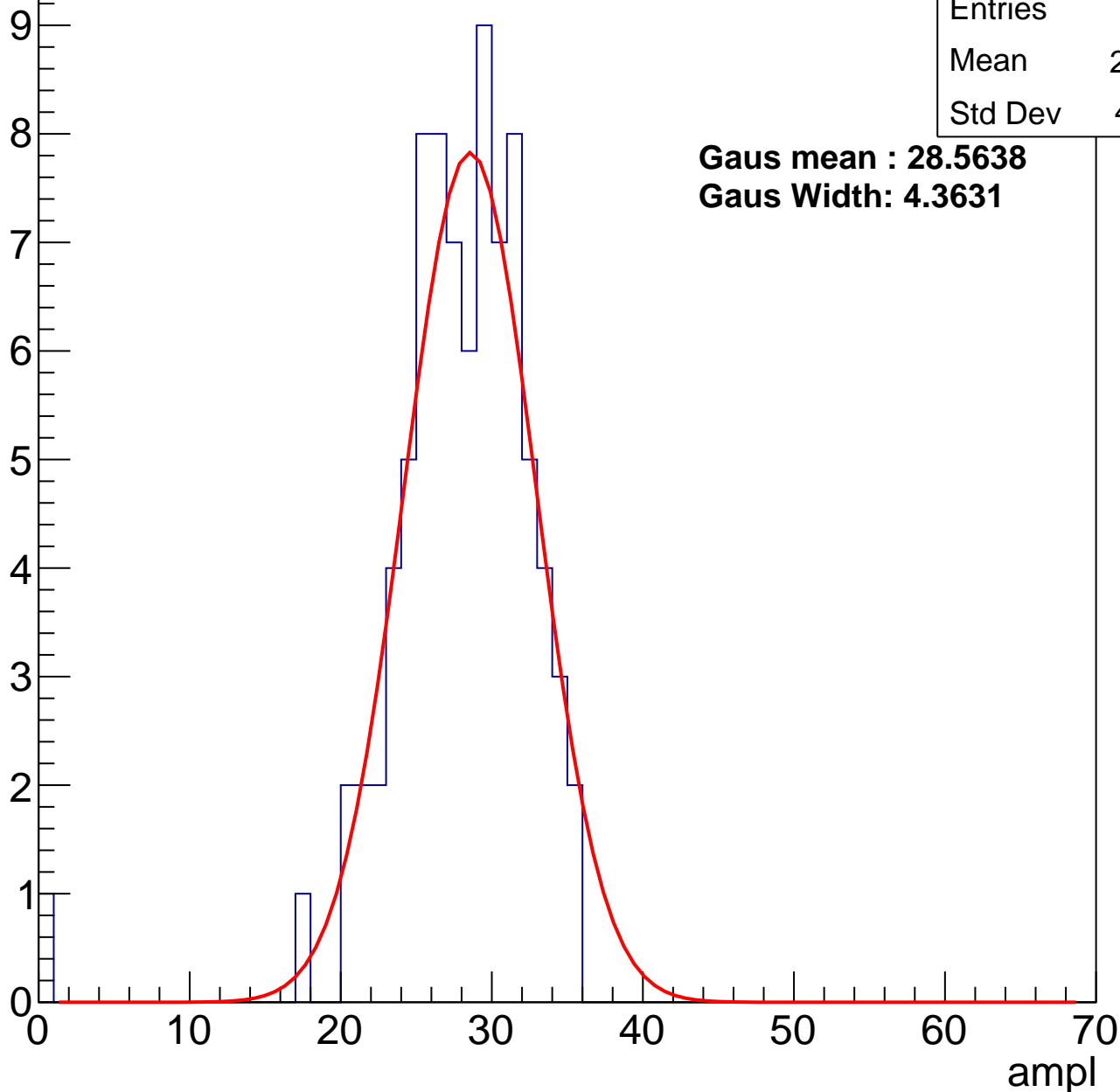
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	27.42
Std Dev	4.831

Gaus mean : 28.5638

Gaus Width: 4.3631



B1L102S, U13-ch121, adc1

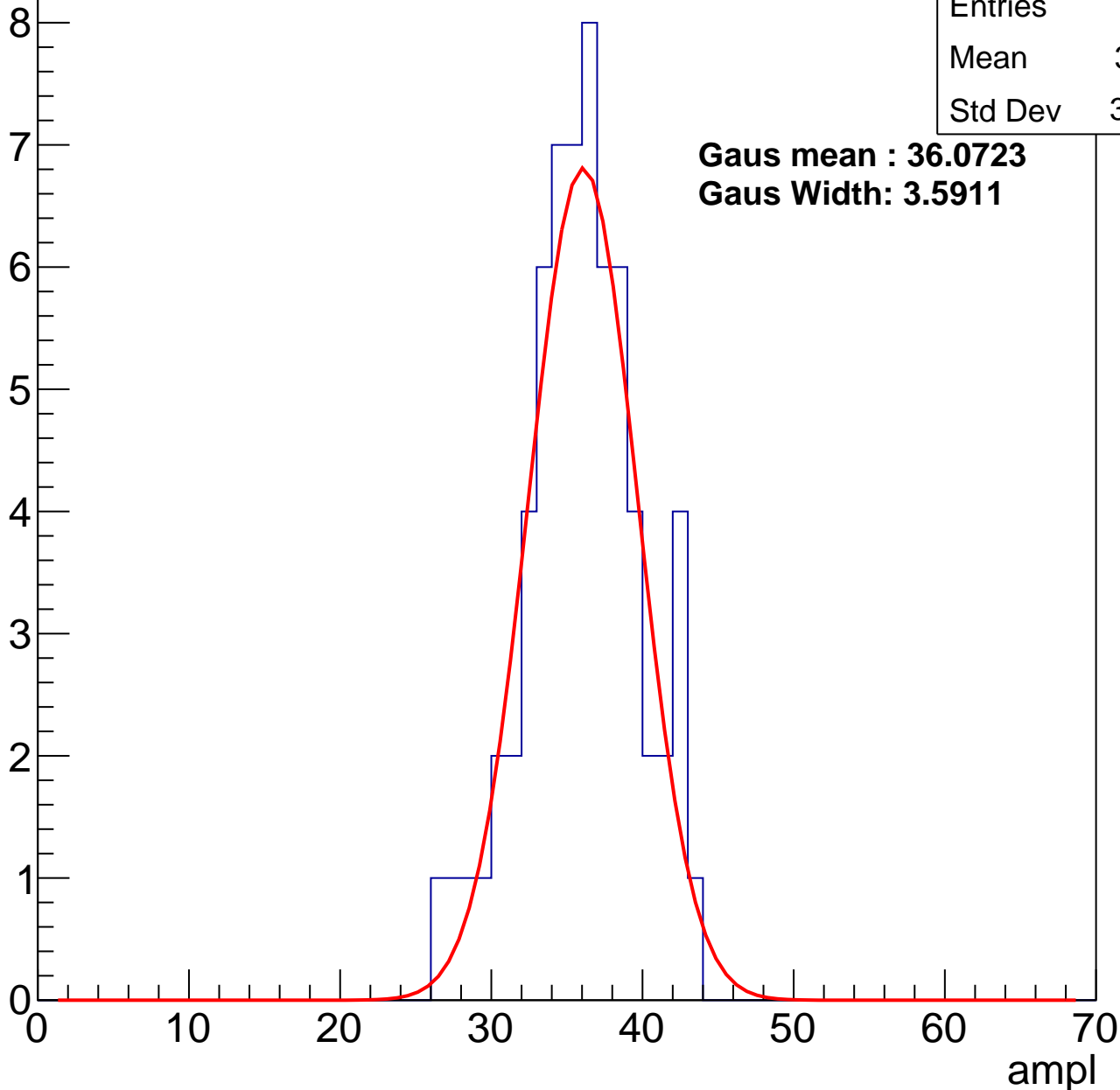
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.51
Std Dev	3.709

Gaus mean : 36.0723

Gaus Width: 3.5911



B1L102S, U13-ch121, adc2

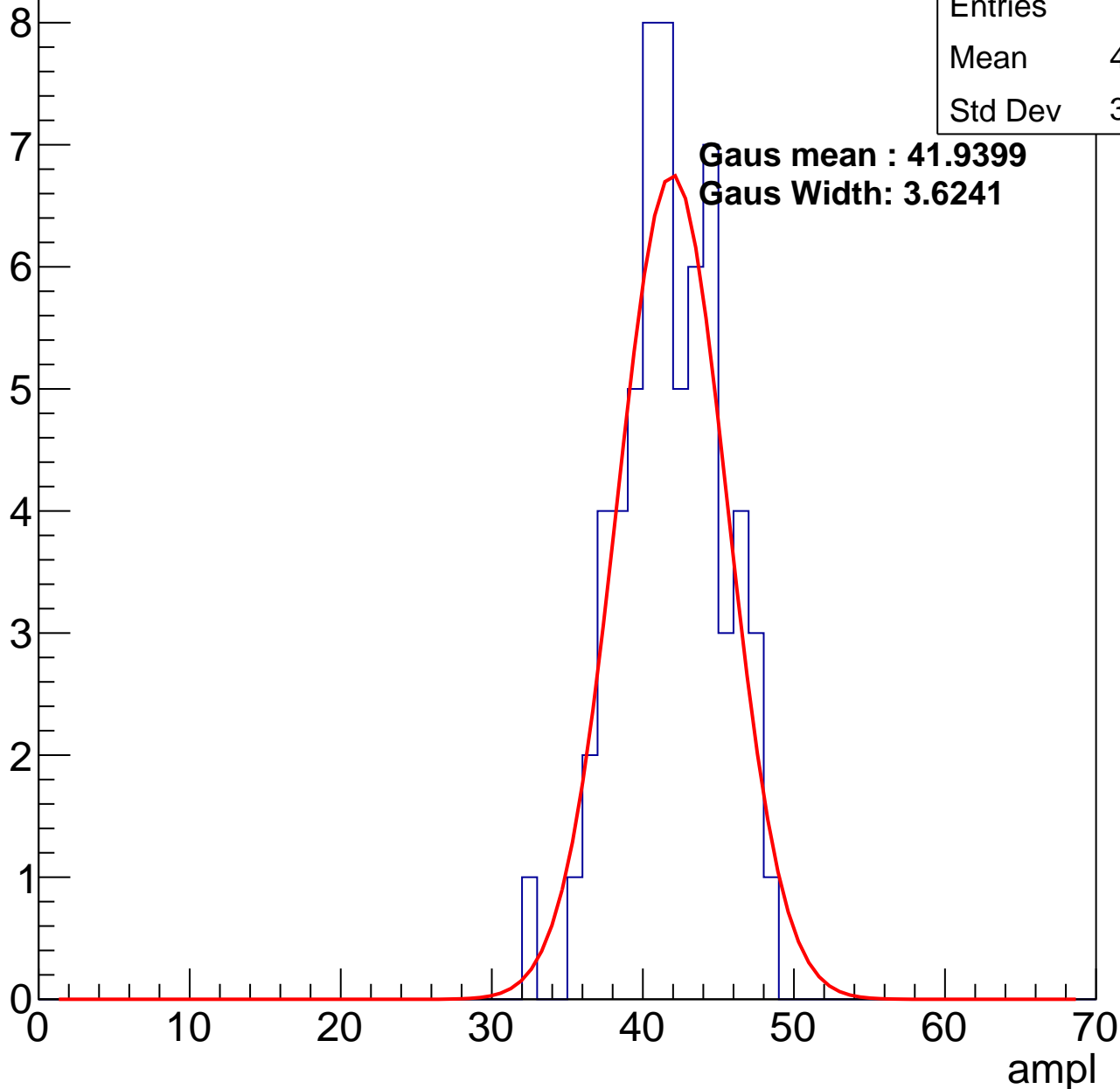
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.39
Std Dev	3.318

Gaus mean : 41.9399

Gaus Width: 3.6241

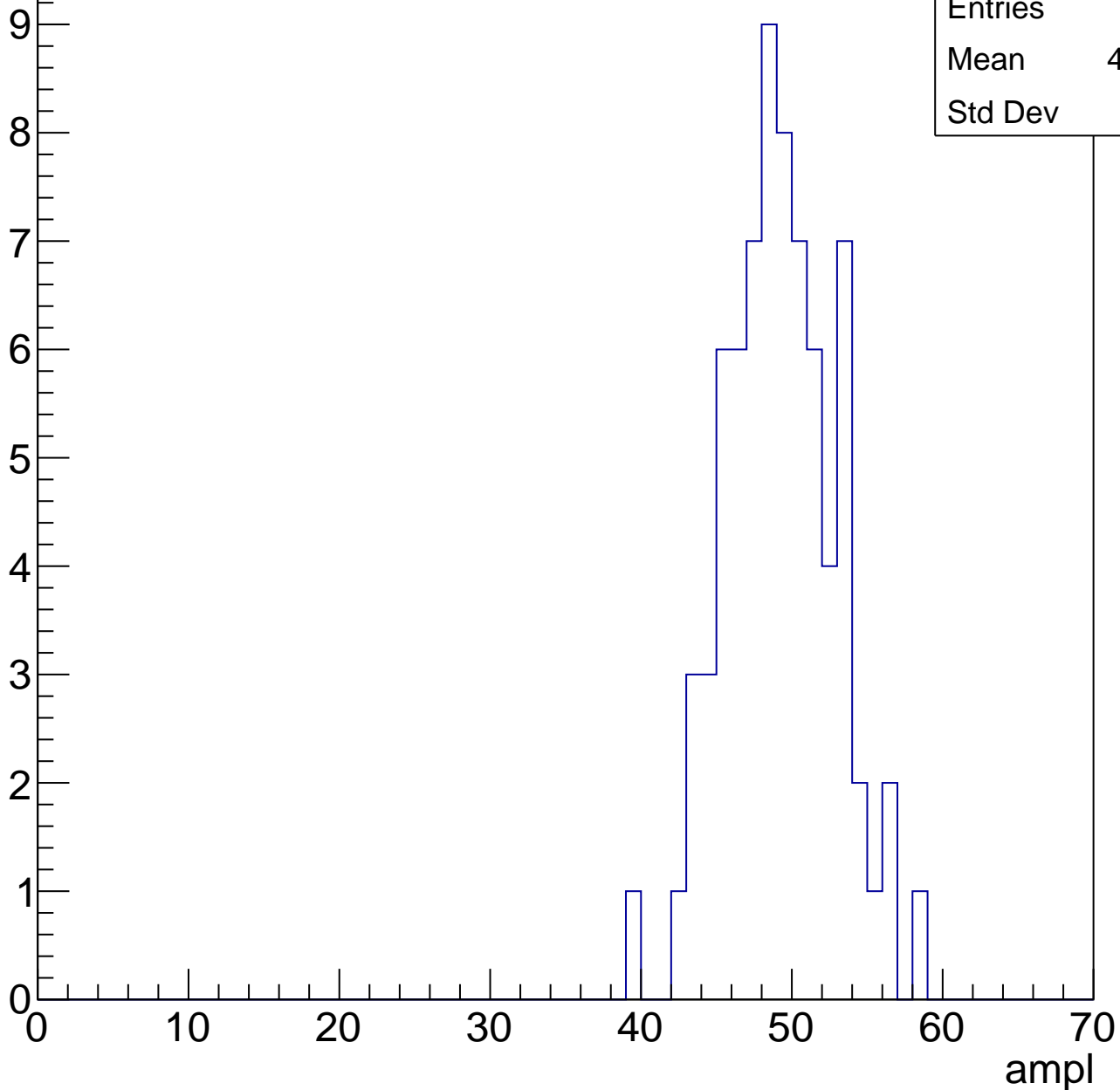


B1L102S, U13-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.77
Std Dev	3.6

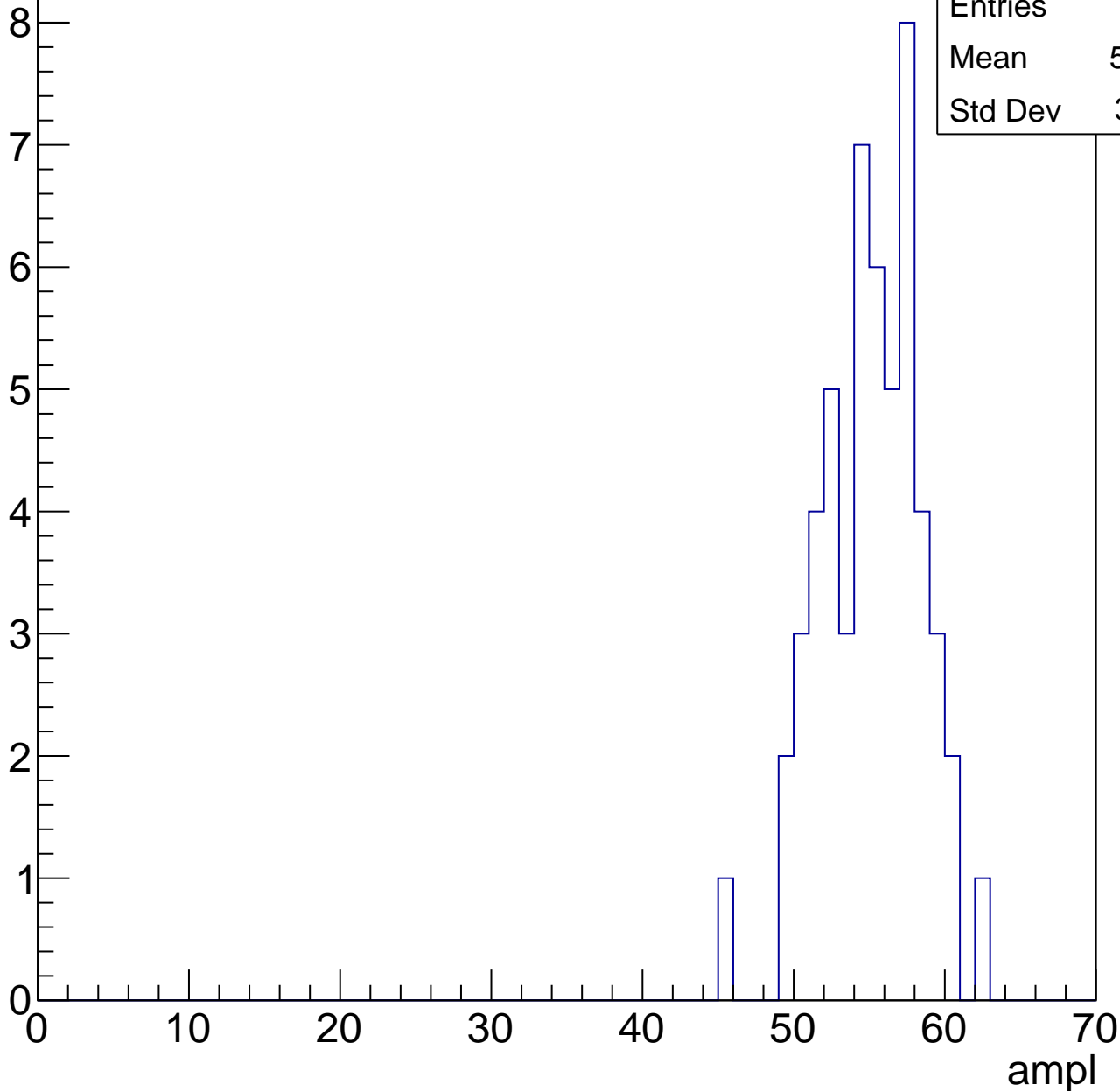


B1L102S, U13-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.65
Std Dev	3.301

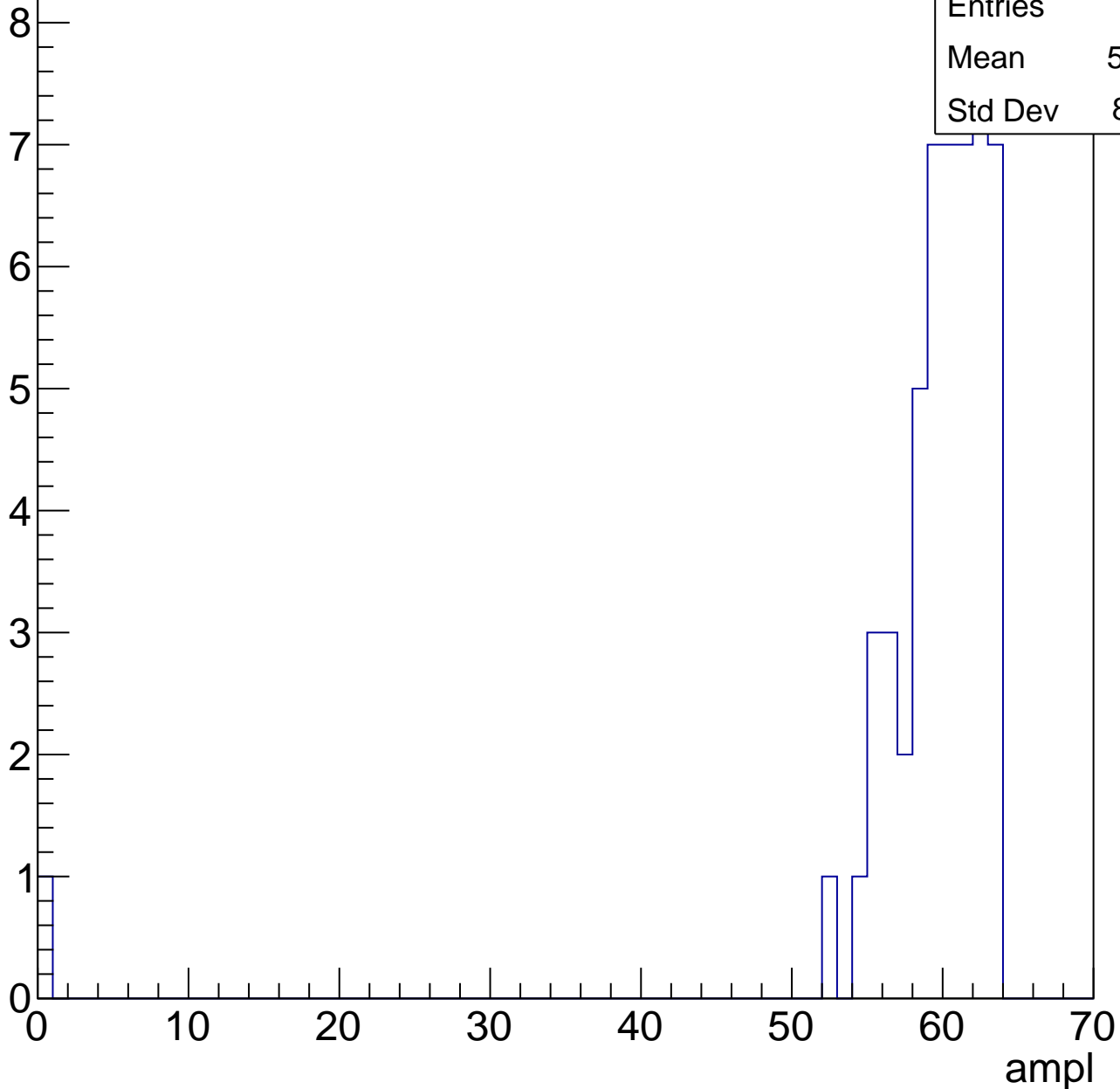


B1L102S, U13-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

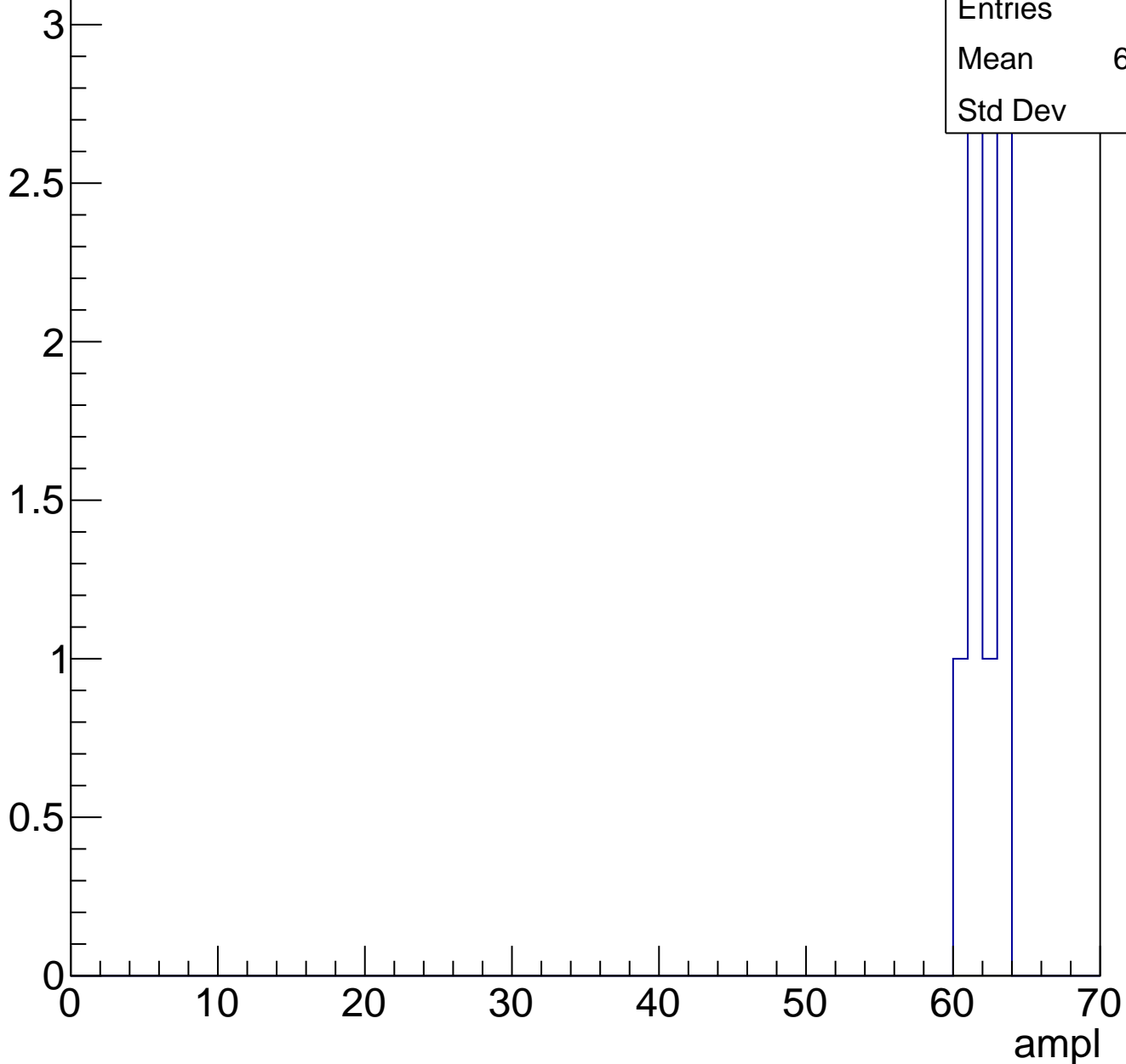
Entries	52
Mean	58.46
Std Dev	8.601



B1L102S, U13-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch122, adc0

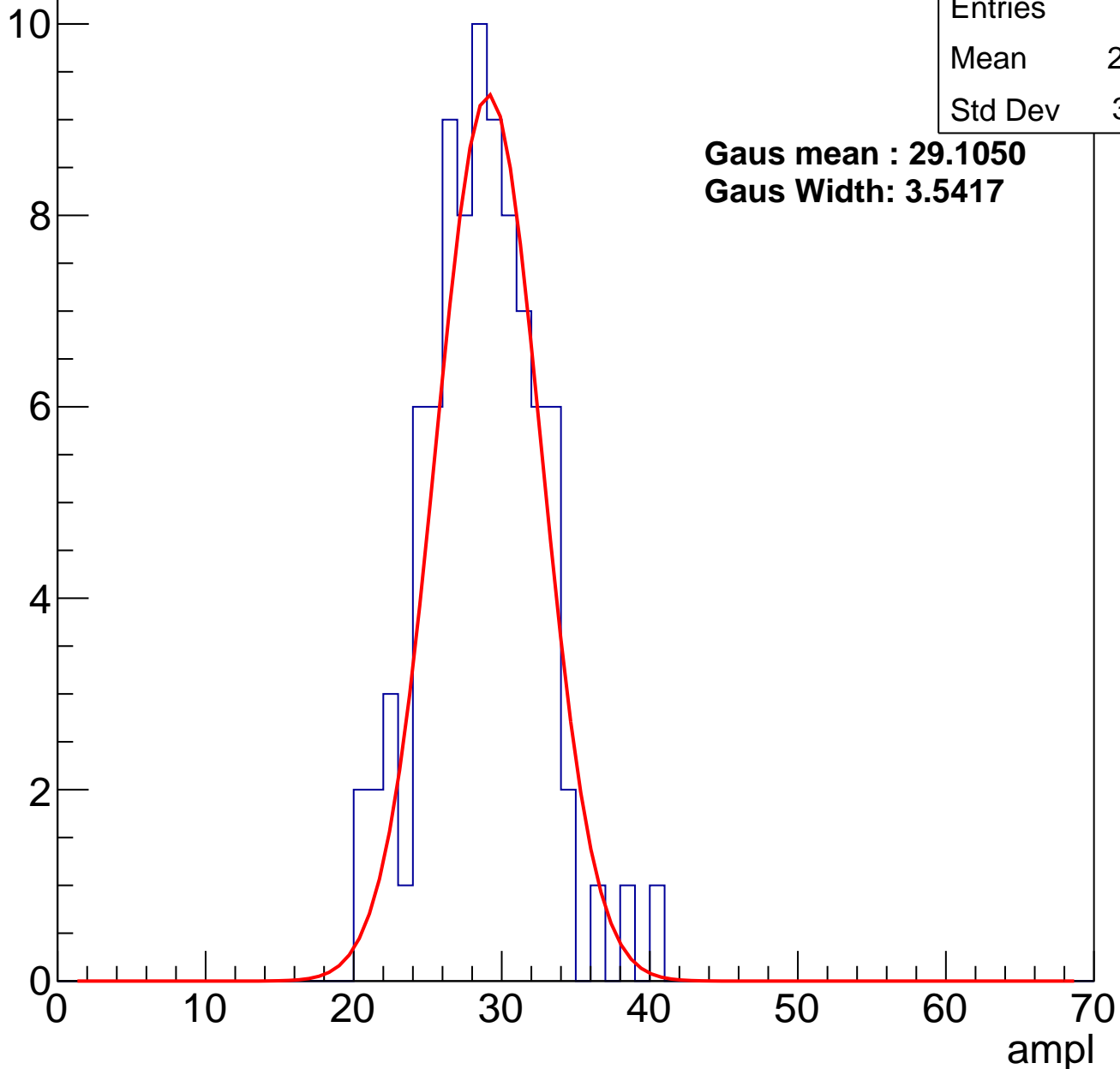
calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	28.24
Std Dev	3.811

Gaus mean : 29.1050

Gaus Width: 3.5417

Entry



B1L102S, U13-ch122, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.69
Std Dev	3.102

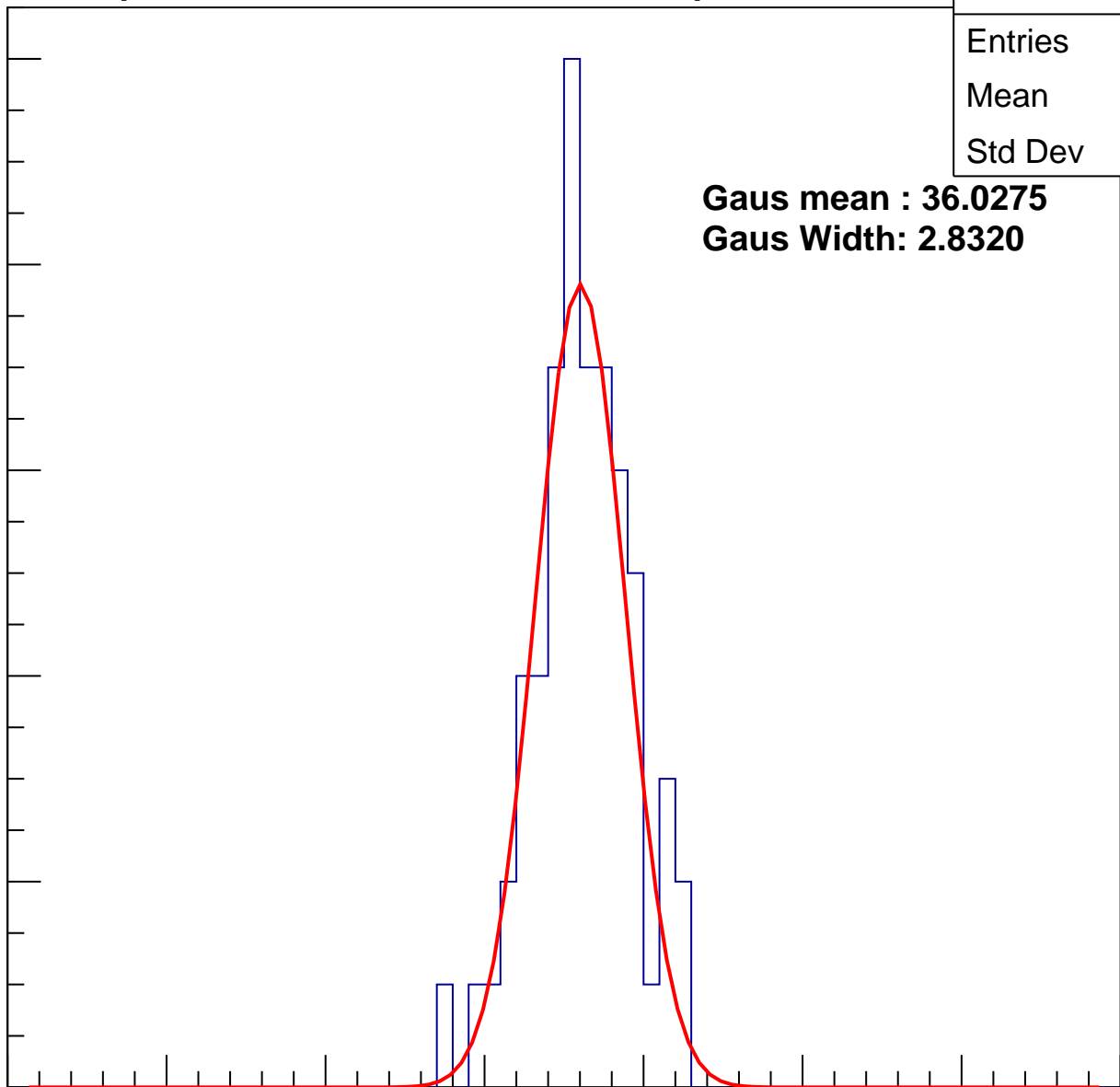
Gaus mean : 36.0275

Gaus Width: 2.8320

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch122, adc2

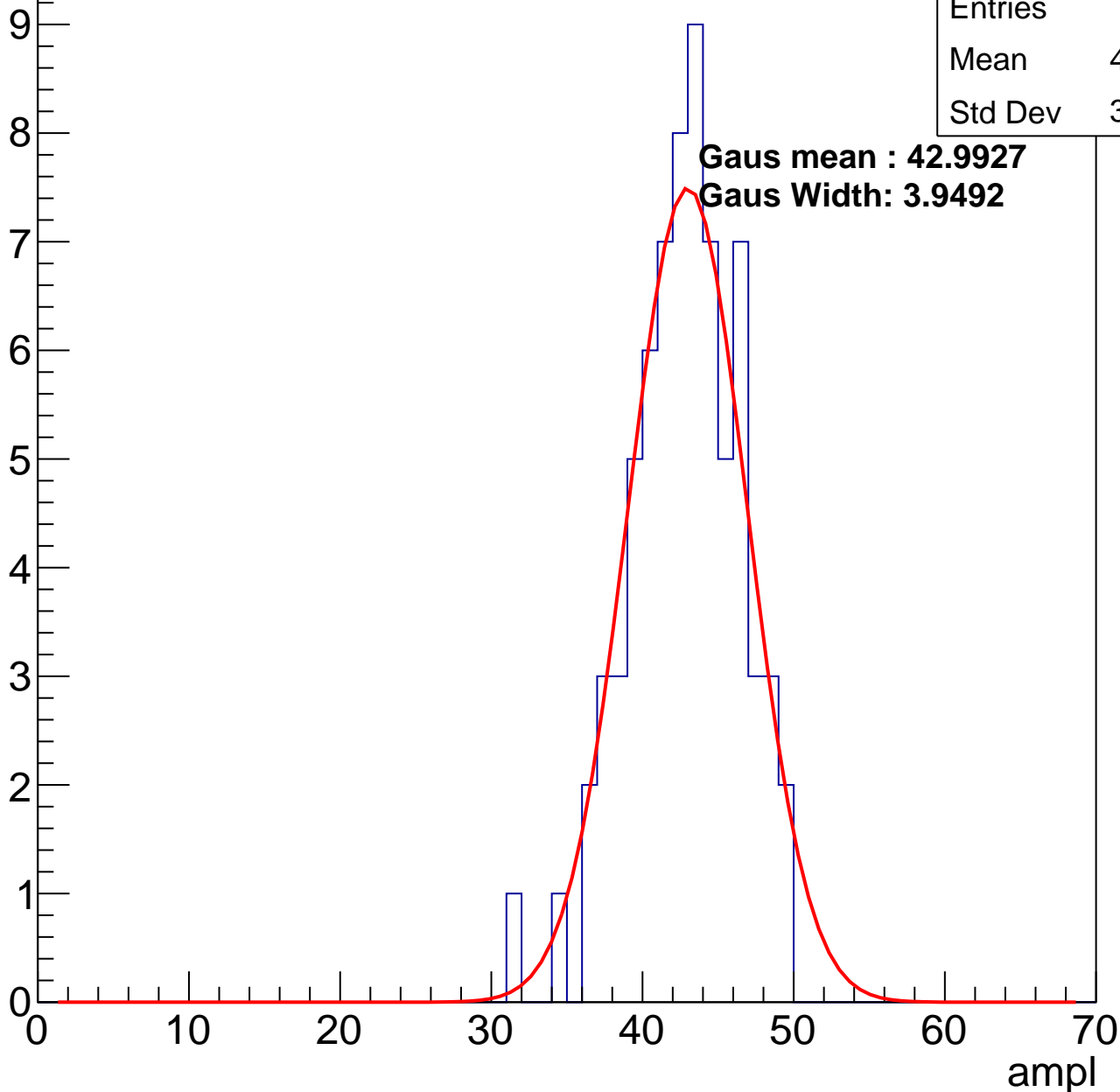
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.29
Std Dev	3.584

Gaus mean : 42.9927

Gaus Width: 3.9492

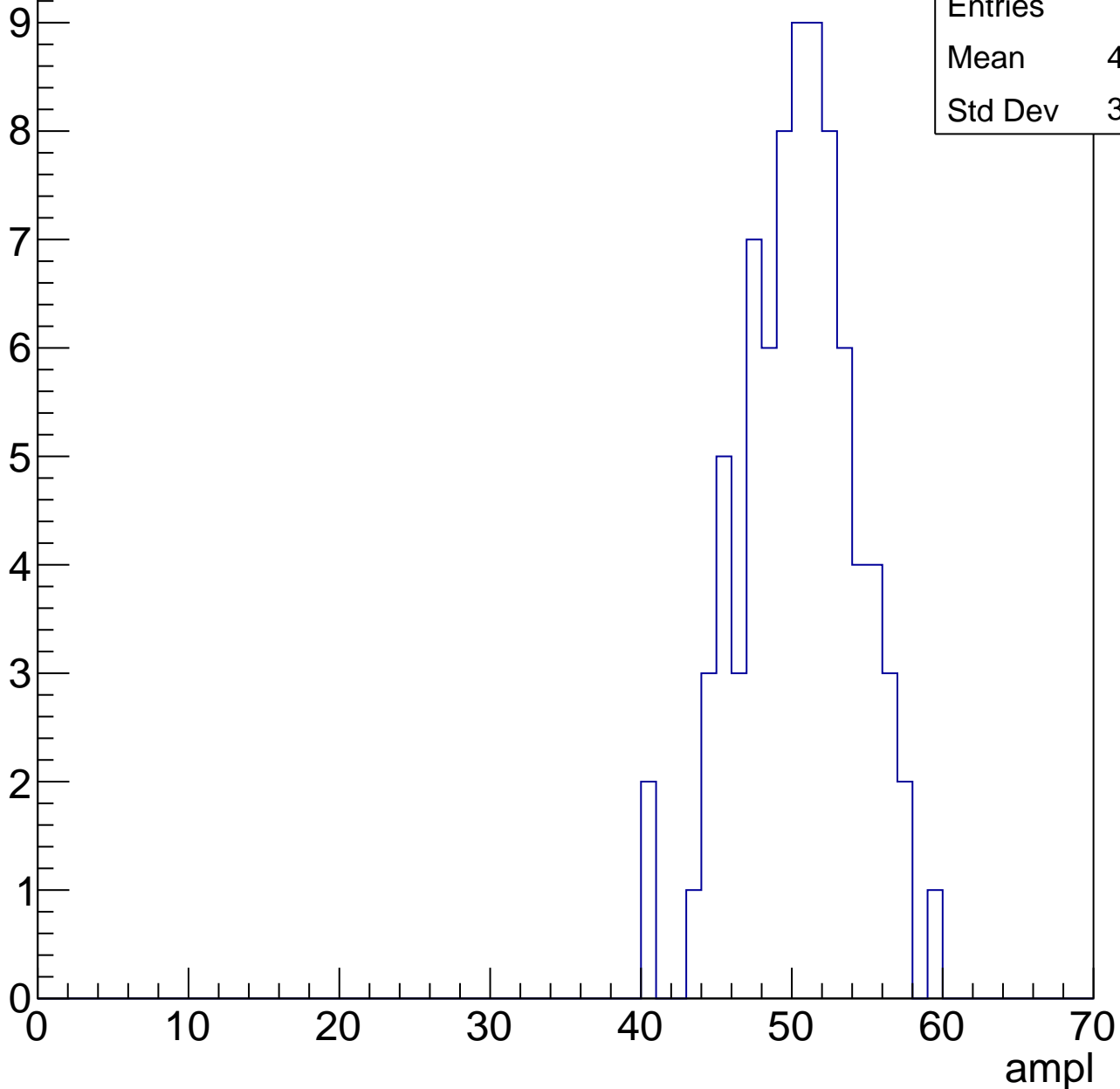


B1L102S, U13-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	49.96
Std Dev	3.802

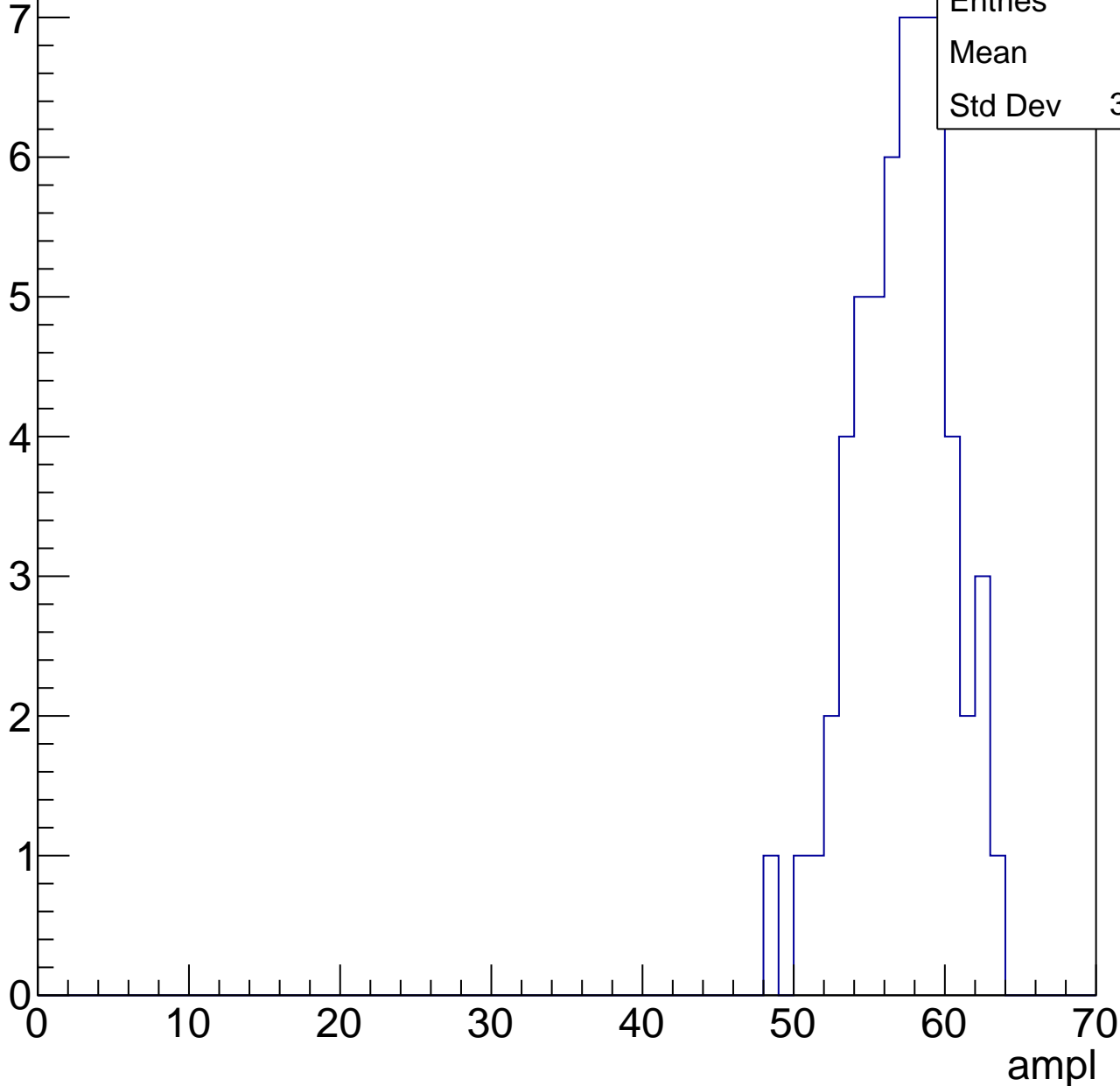


B1L102S, U13-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	56.7
Std Dev	3.162

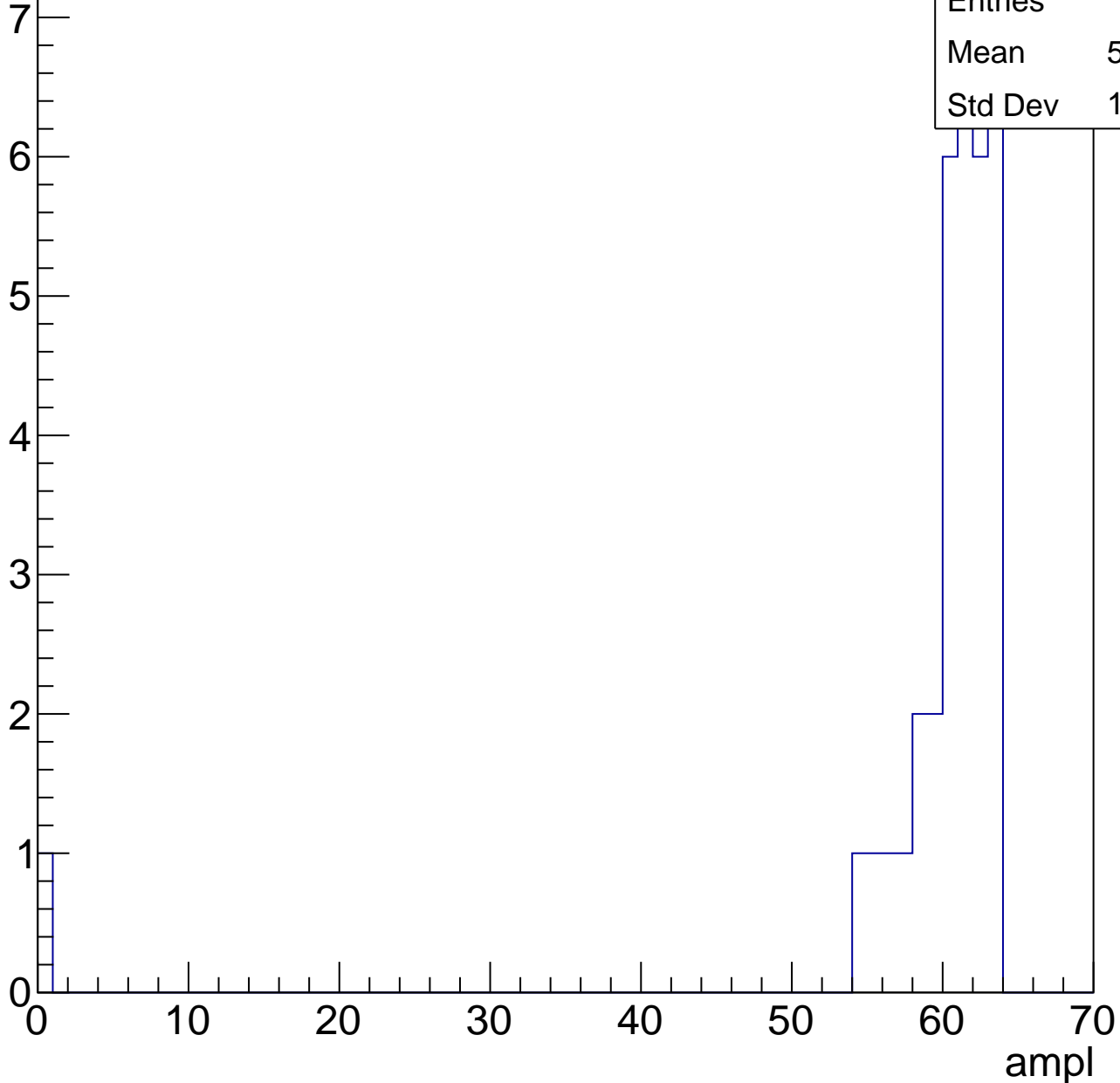


B1L102S, U13-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	58.74
Std Dev	10.33



B1L102S, U13-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.855

ampl

0 10 20 30 40 50 60 70

0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2

B1L102S, U13-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U13-ch123, adc0

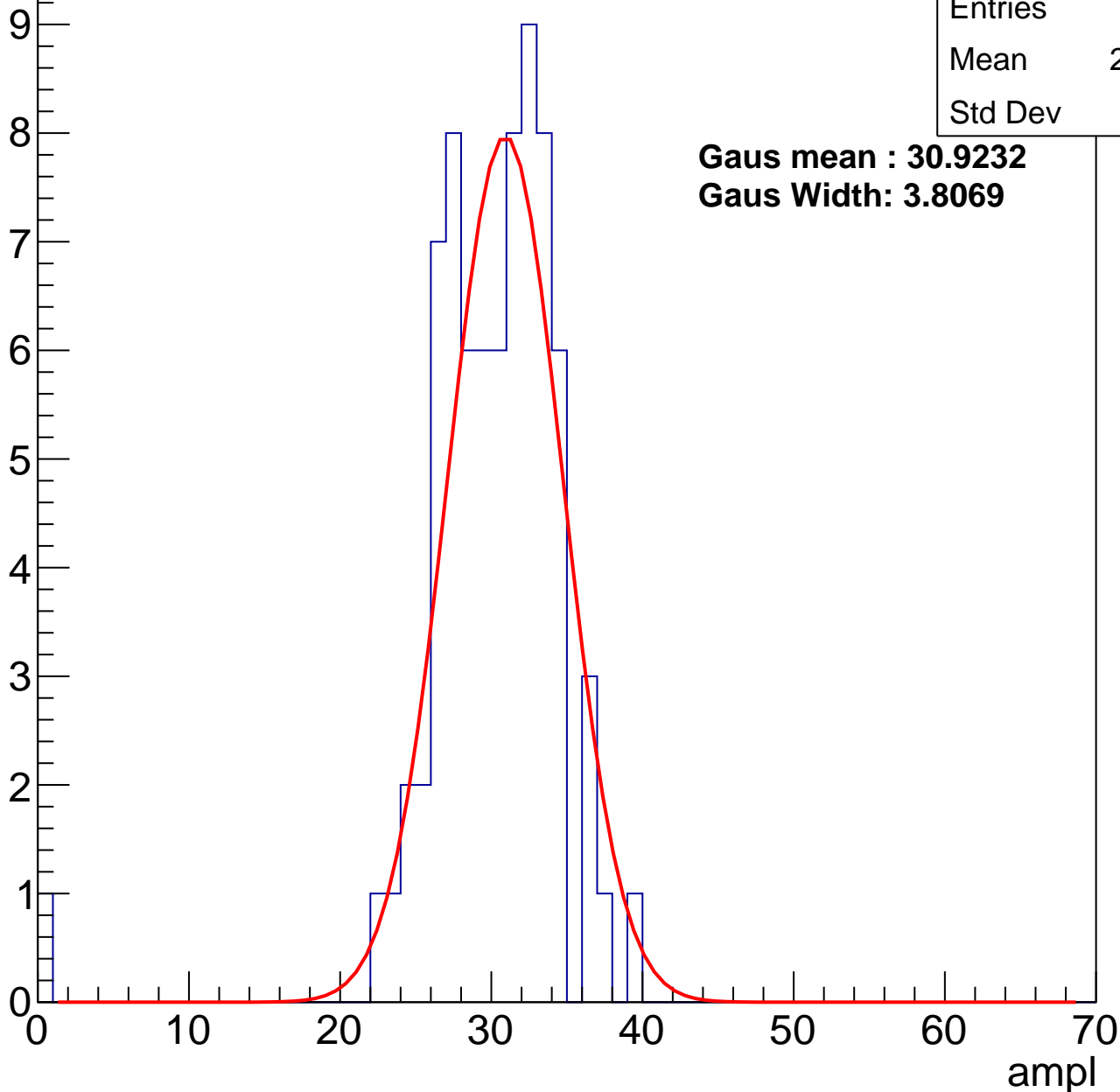
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.62
Std Dev	4.85

Gaus mean : 30.9232

Gaus Width: 3.8069



B1L102S, U13-ch123, adc1

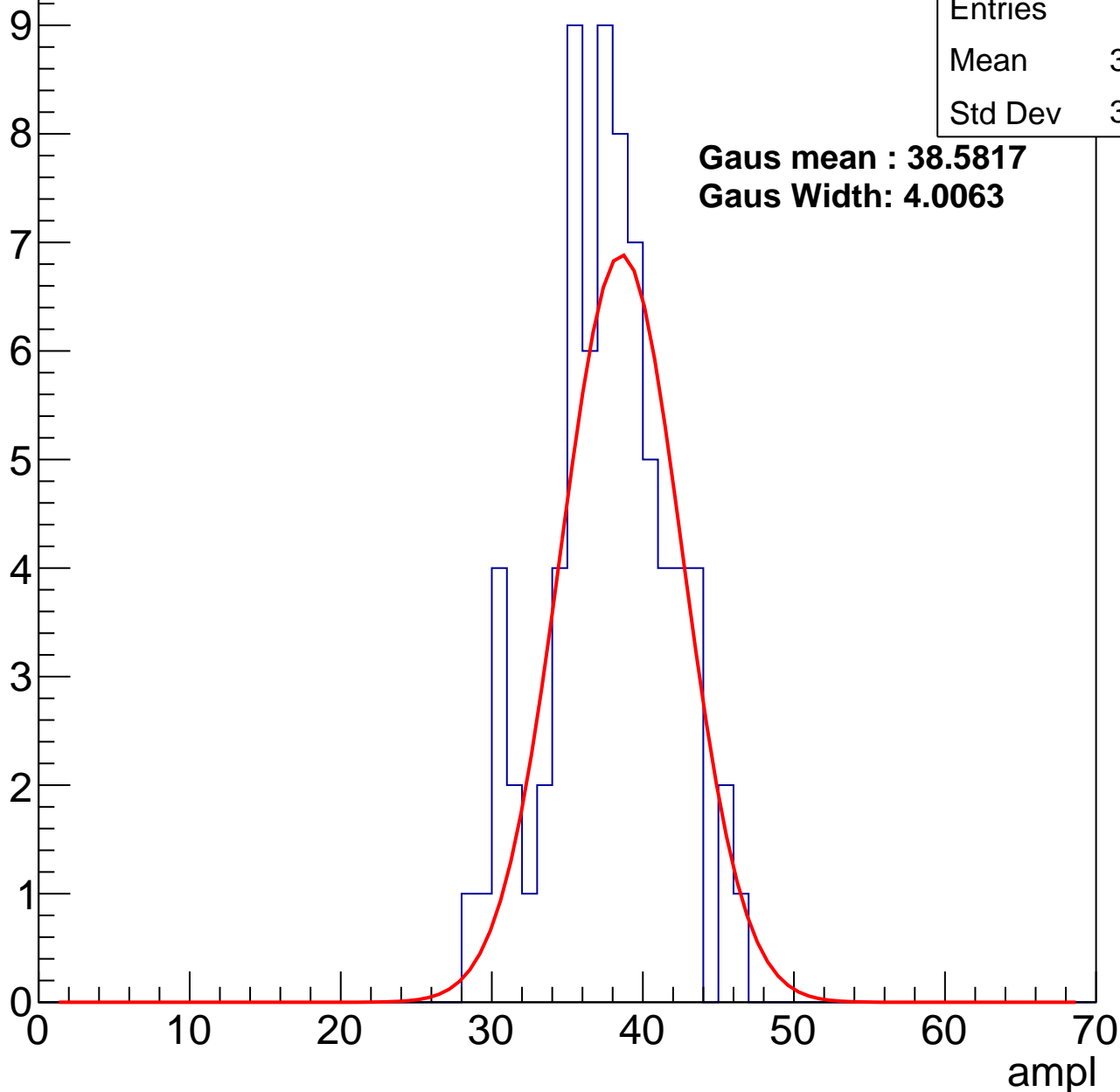
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	37.22
Std Dev	3.929

Gaus mean : 38.5817

Gaus Width: 4.0063



B1L102S, U13-ch123, adc2

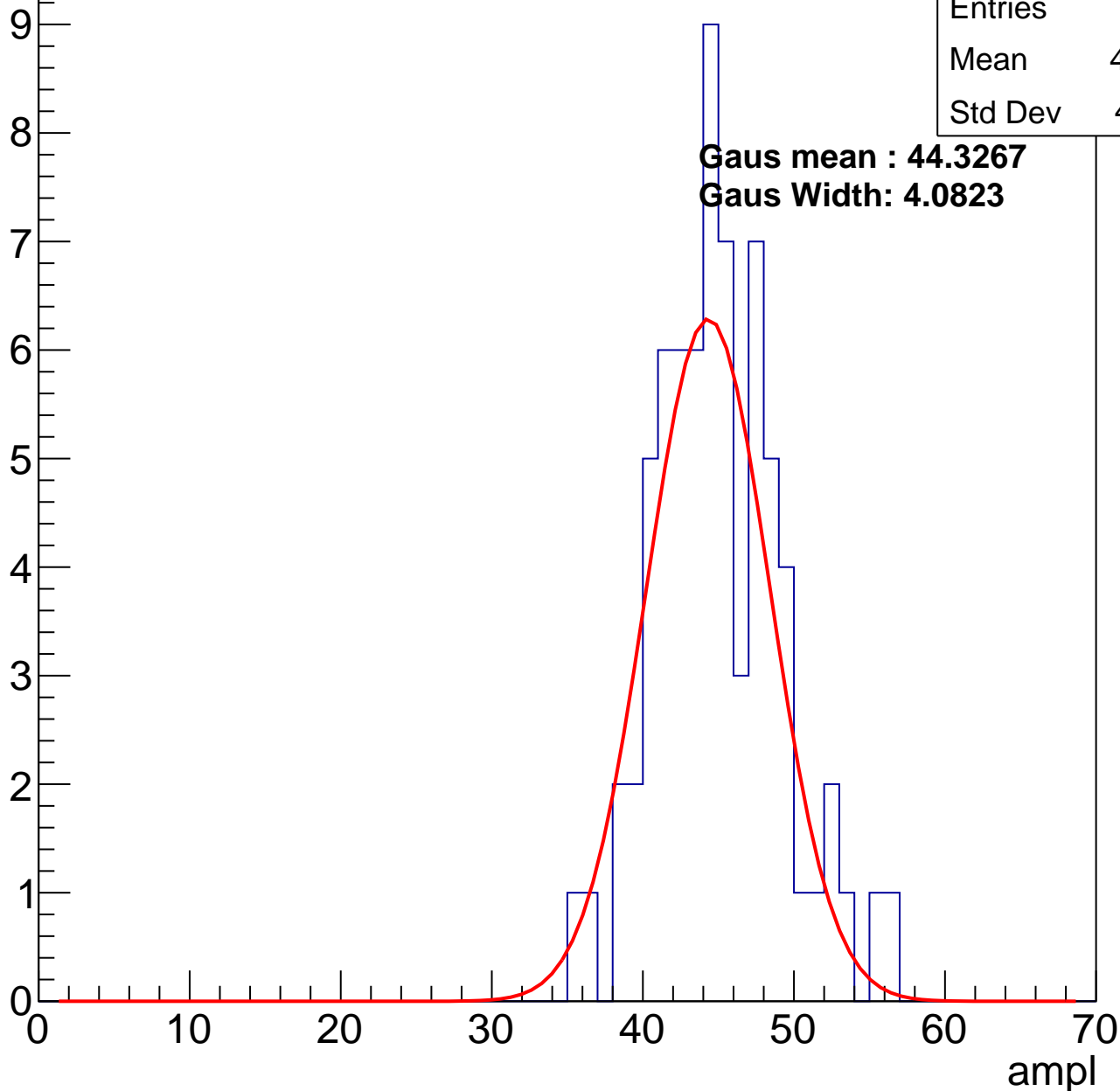
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	44.56
Std Dev	4.161

Gaus mean : 44.3267

Gaus Width: 4.0823

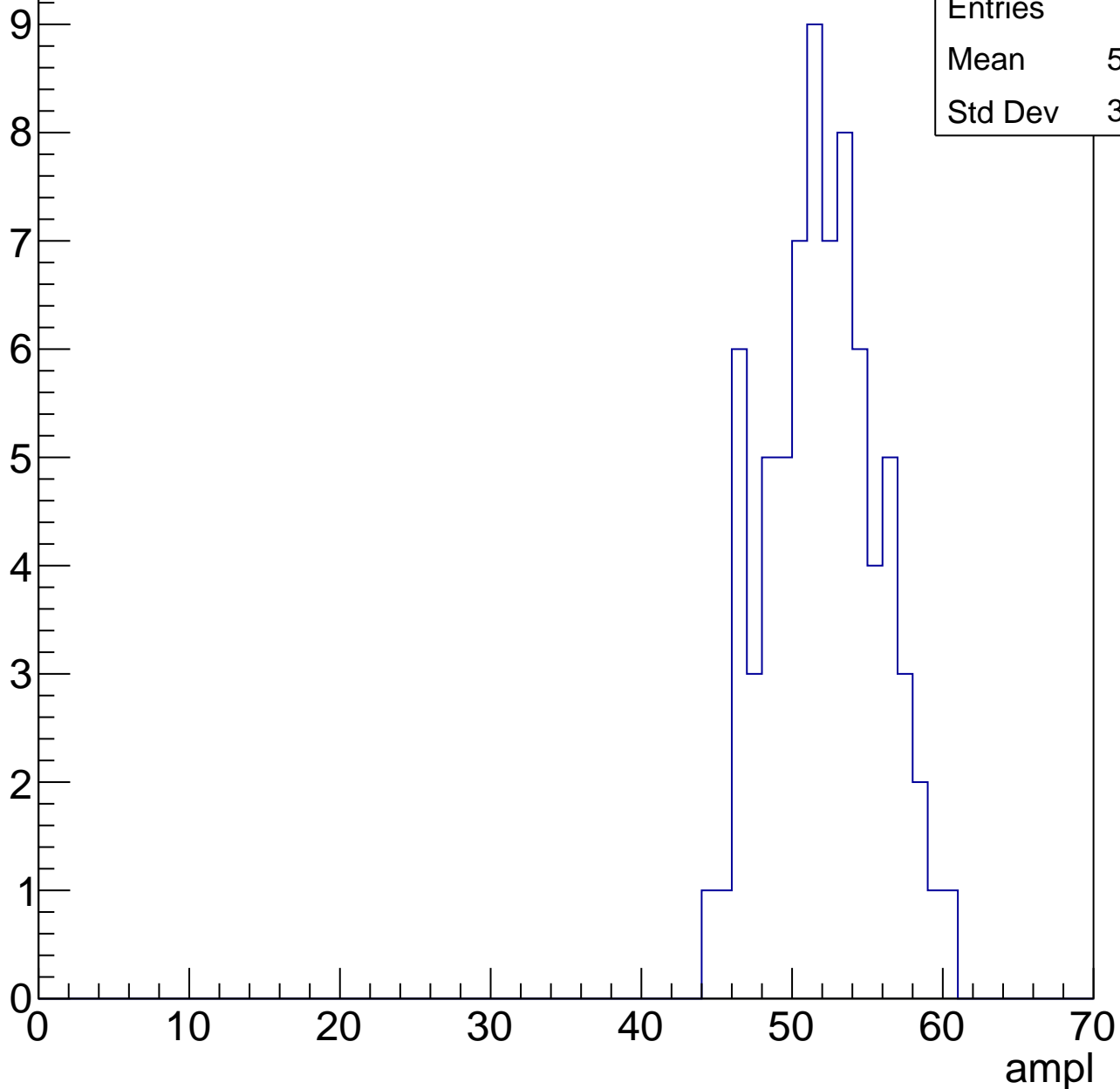


B1L102S, U13-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	51.59
Std Dev	3.605

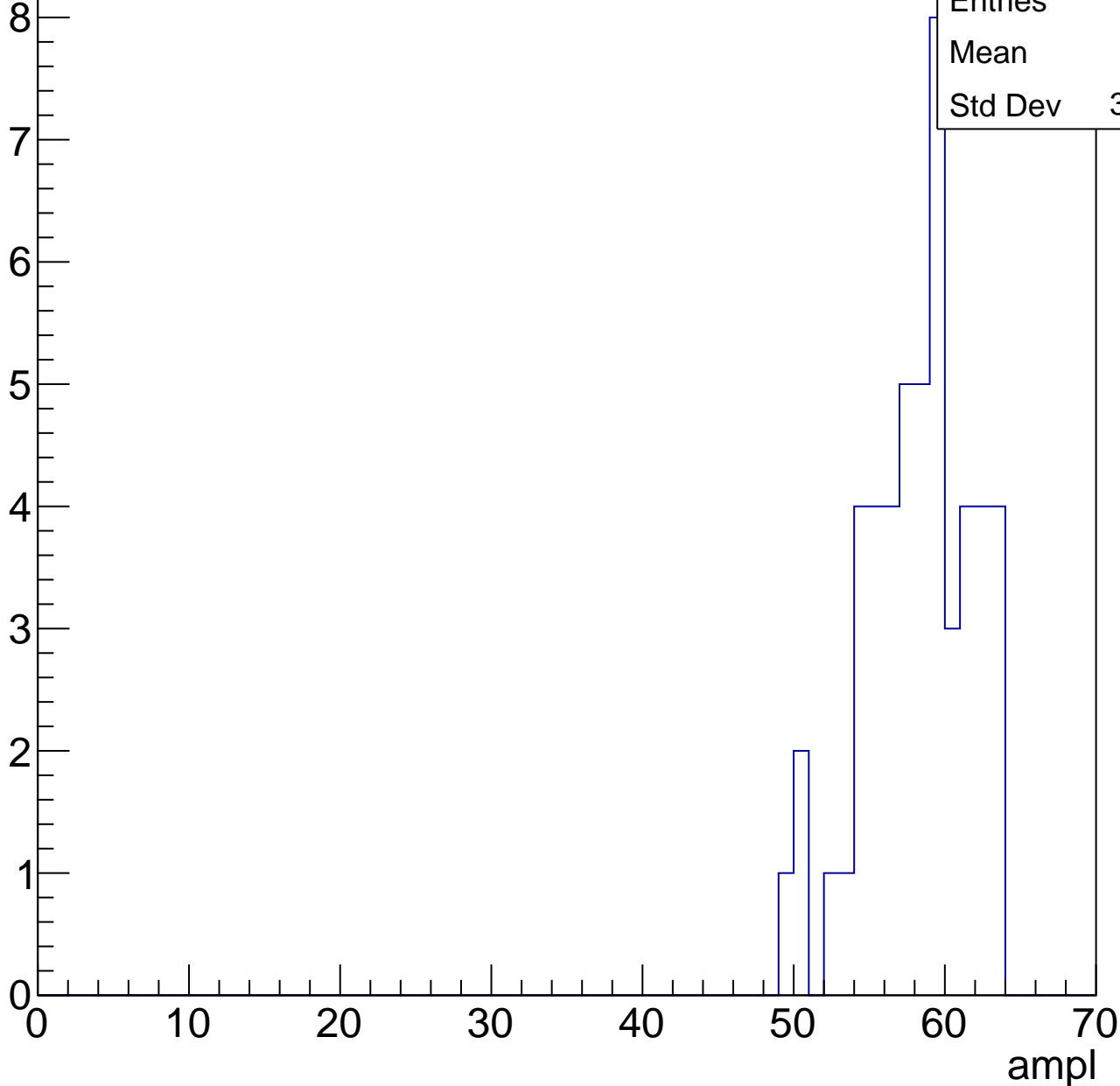


B1L102S, U13-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.7
Std Dev	3.483

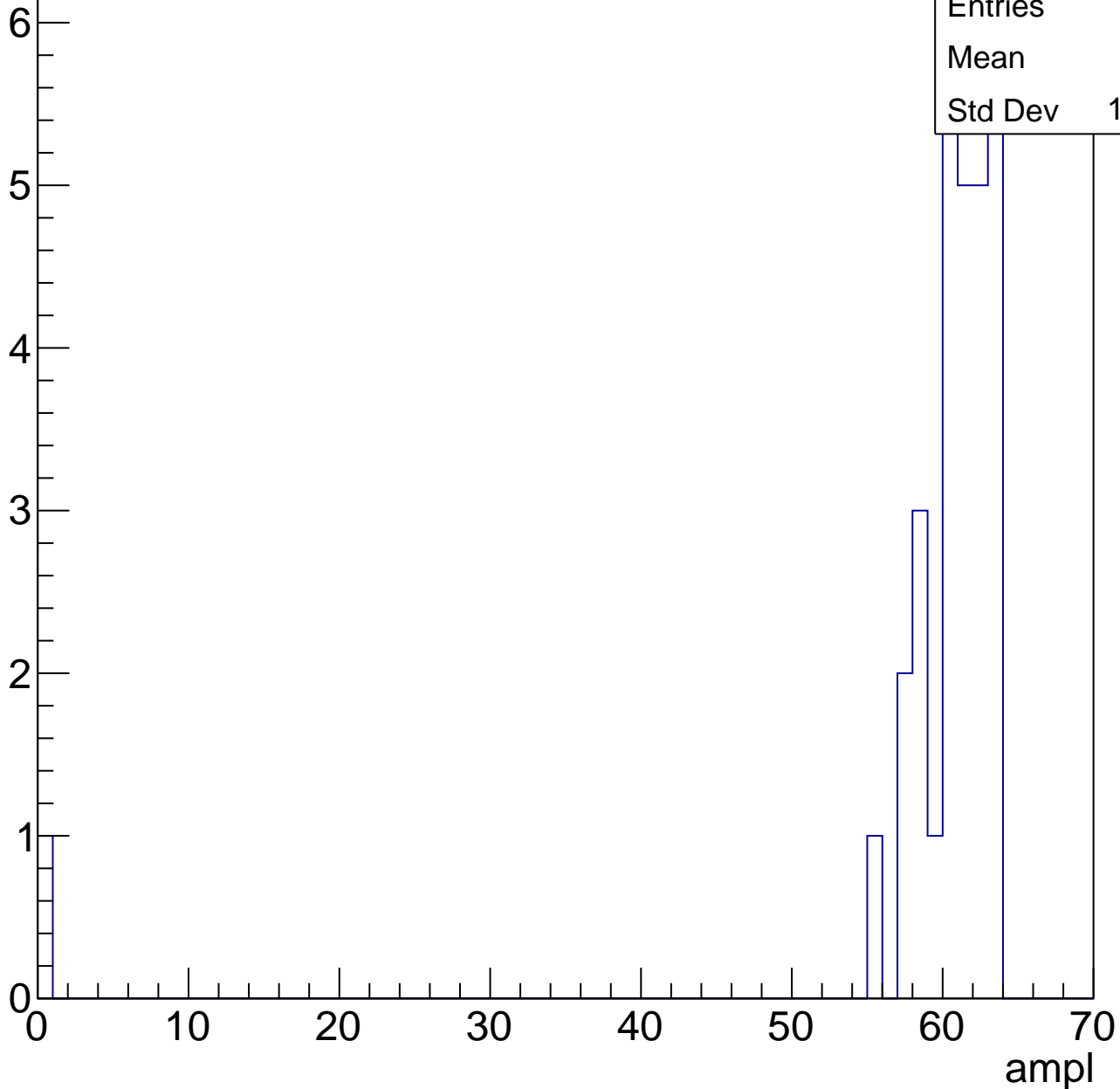


B1L102S, U13-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	58.5
Std Dev	11.06



B1L102S, U13-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U13-ch124, adc0

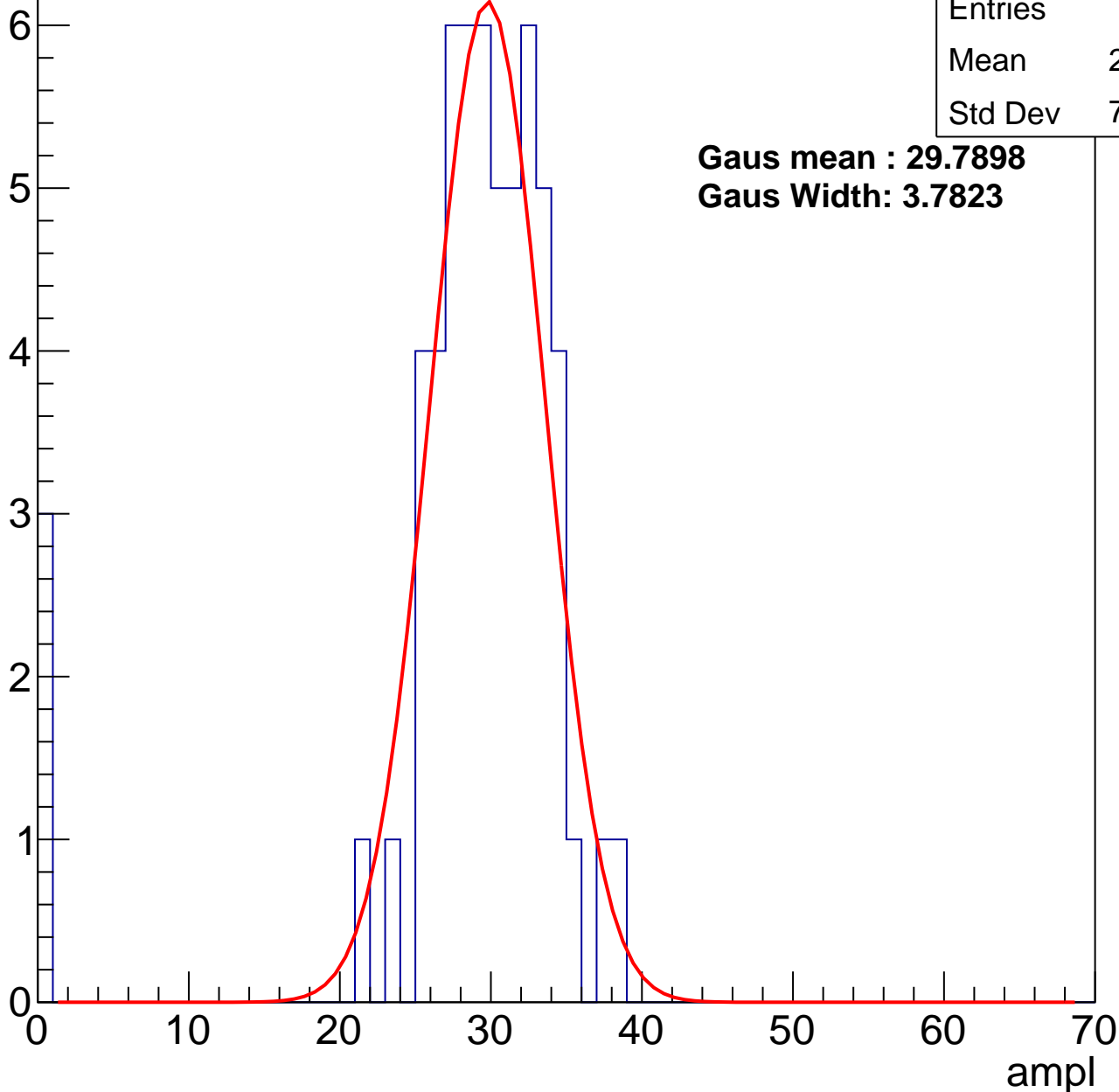
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.14
Std Dev	7.306

Gaus mean : 29.7898

Gaus Width: 3.7823



B1L102S, U13-ch124, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	36.33
Std Dev	5.467

Gaus mean : 36.9306

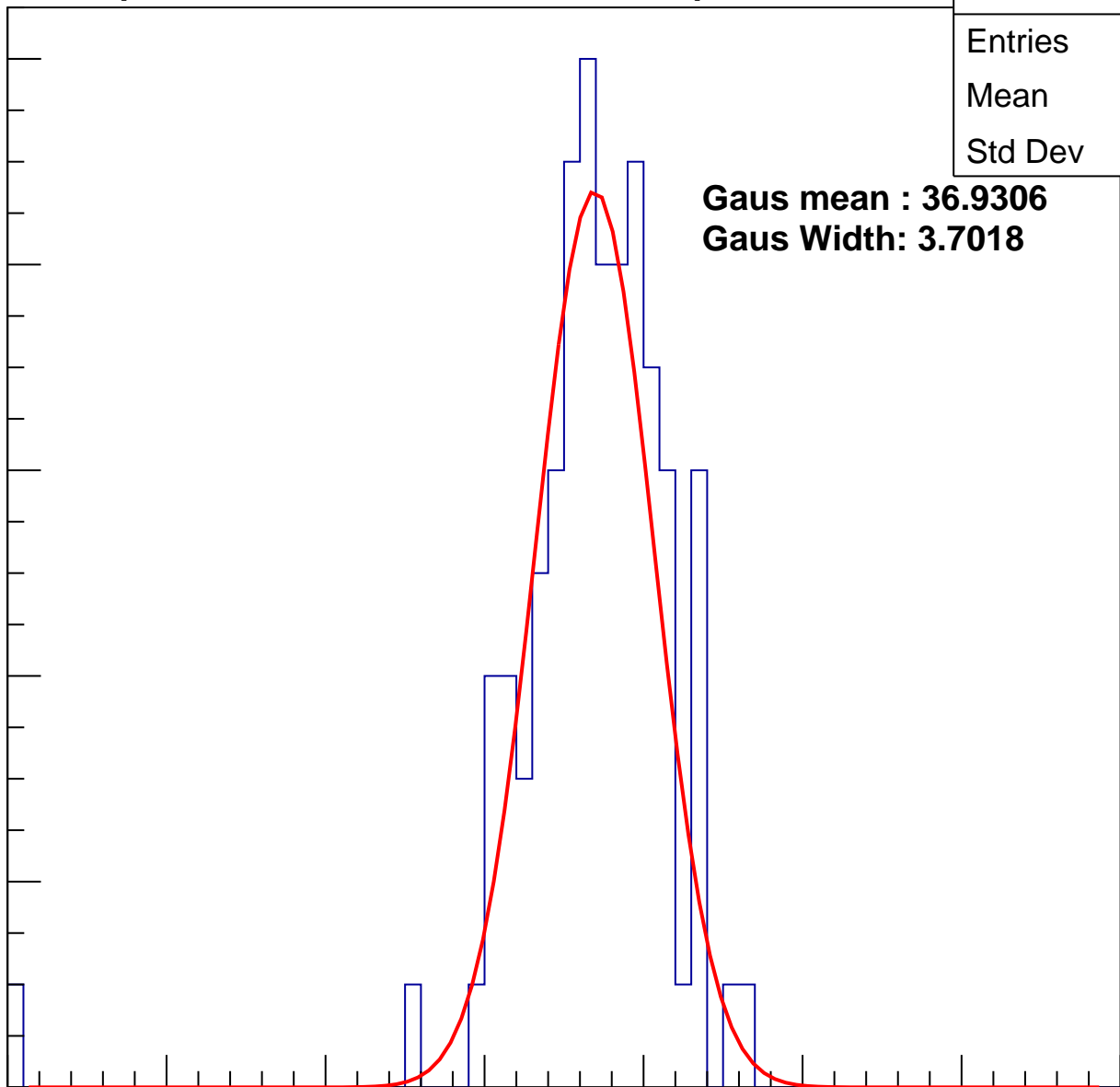
Gaus Width: 3.7018

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch124, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	44.63
Std Dev	3.199

Gaus mean : 45.4522

Gaus Width: 3.3489

Entry

10

8

6

4

2

0

0

10

20

30

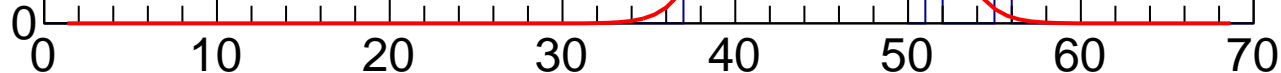
40

50

60

70

ampl

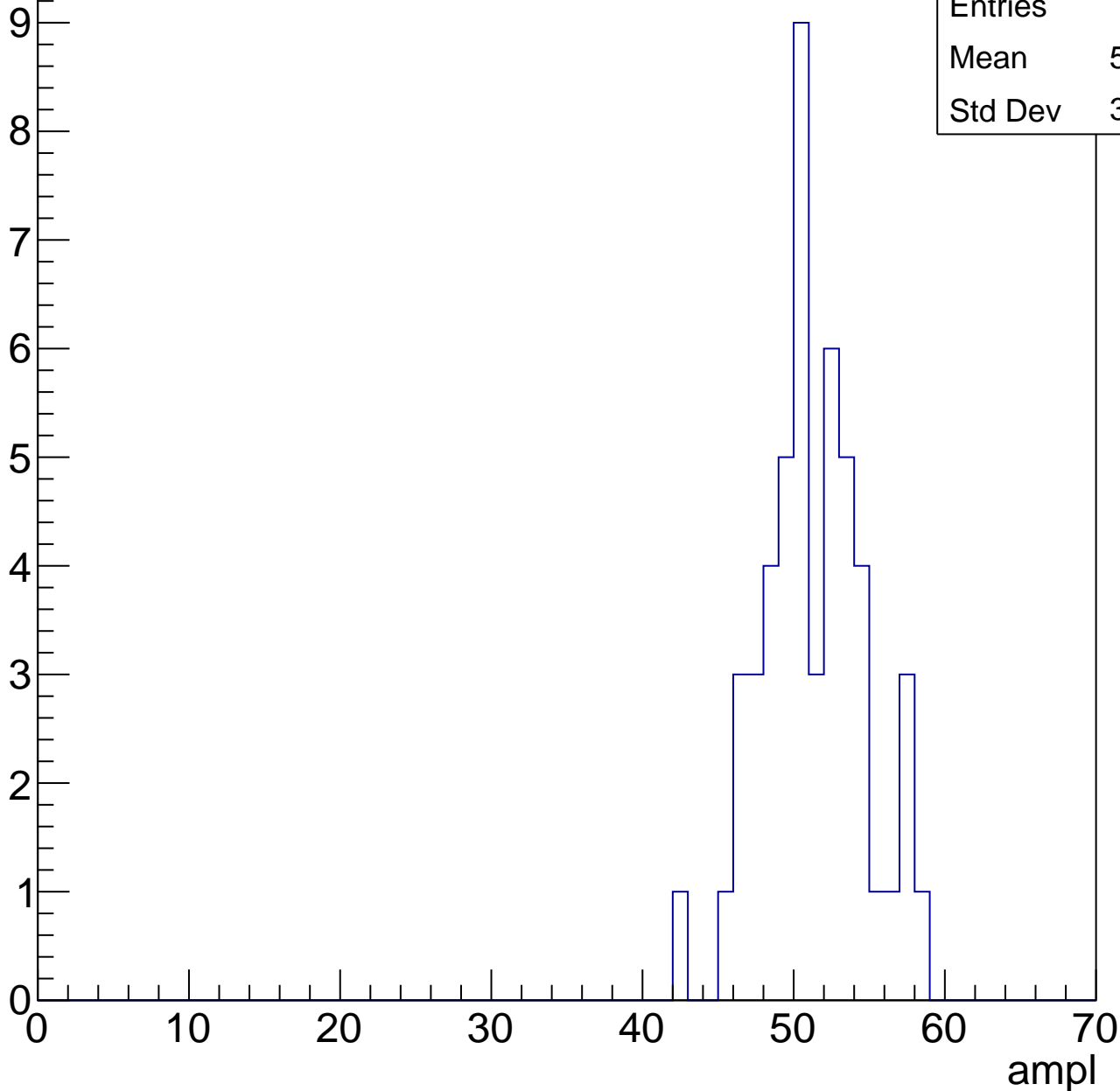


B1L102S, U13-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	50.78
Std Dev	3.354

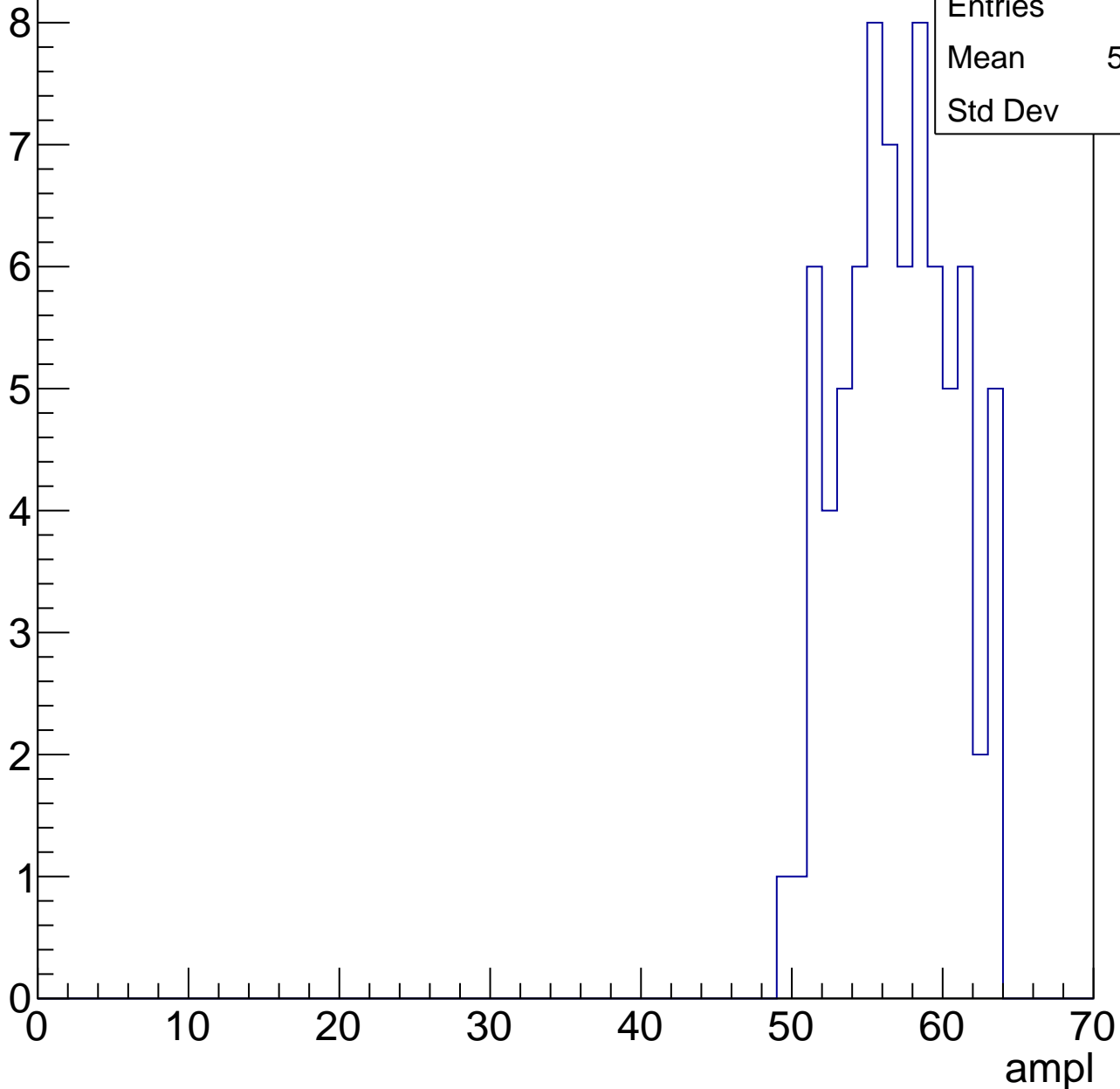


B1L102S, U13-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	56.57
Std Dev	3.61

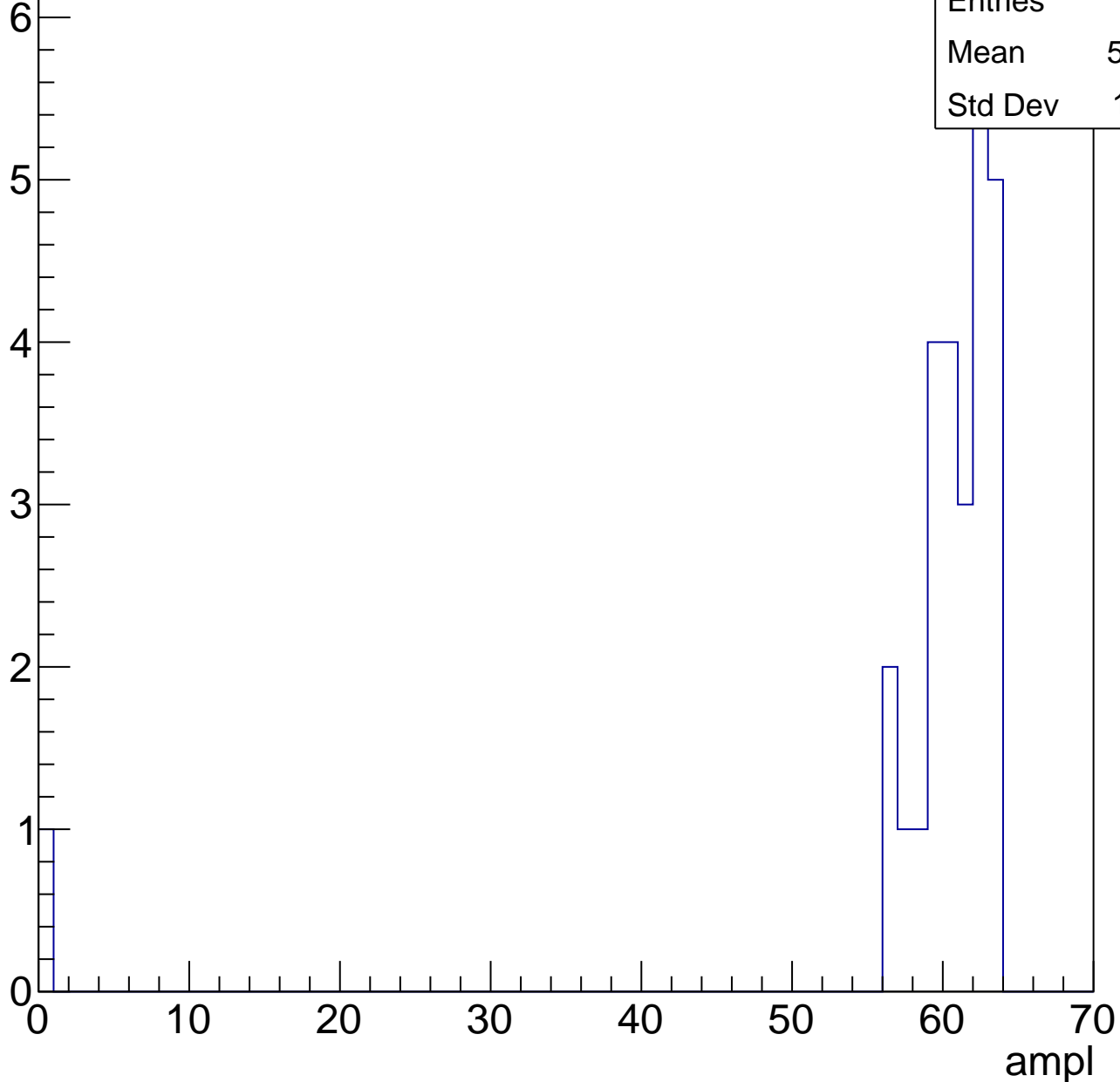


B1L102S, U13-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

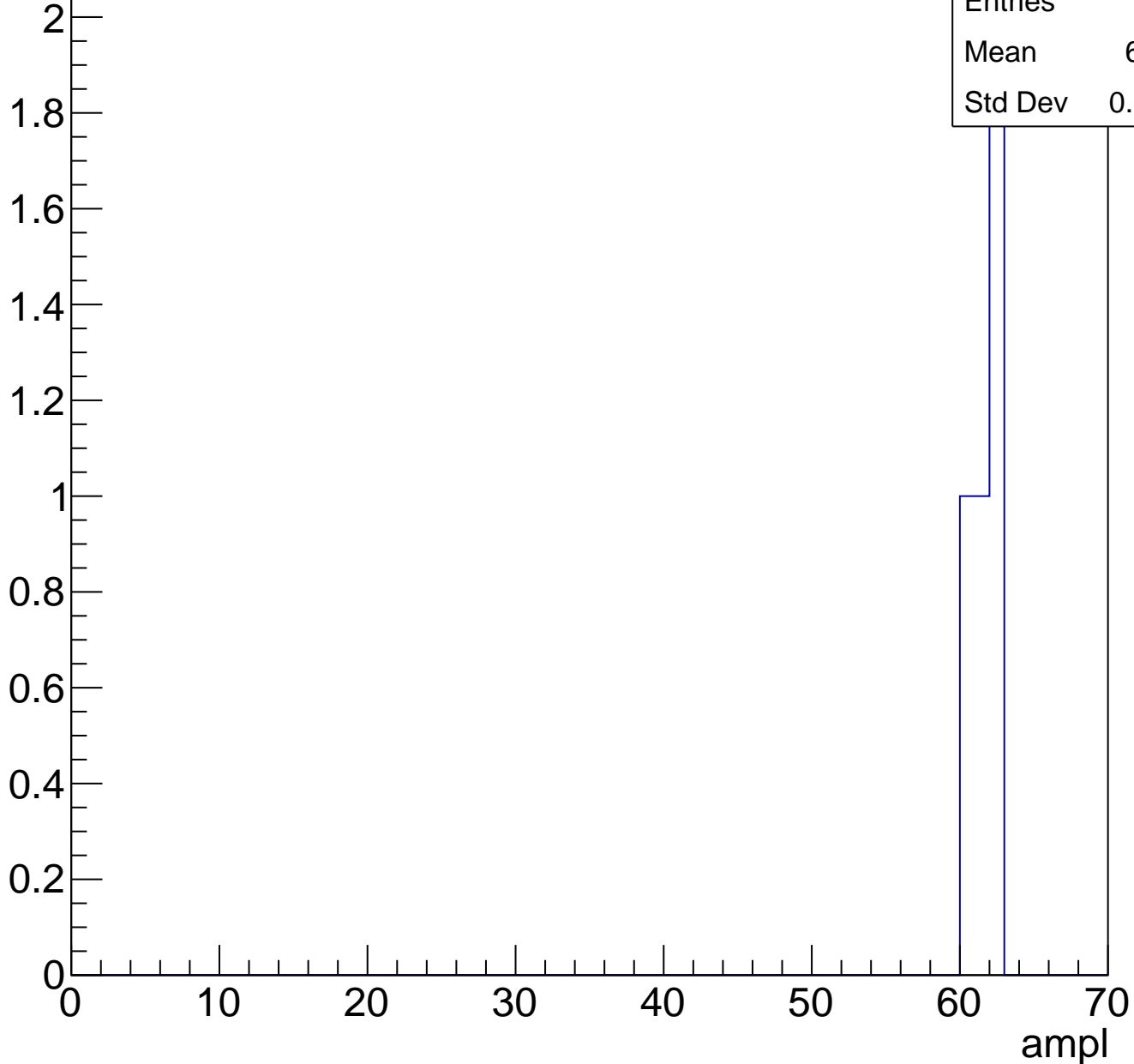
Entries	27
Mean	58.26
Std Dev	11.61



B1L102S, U13-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch125, adc0

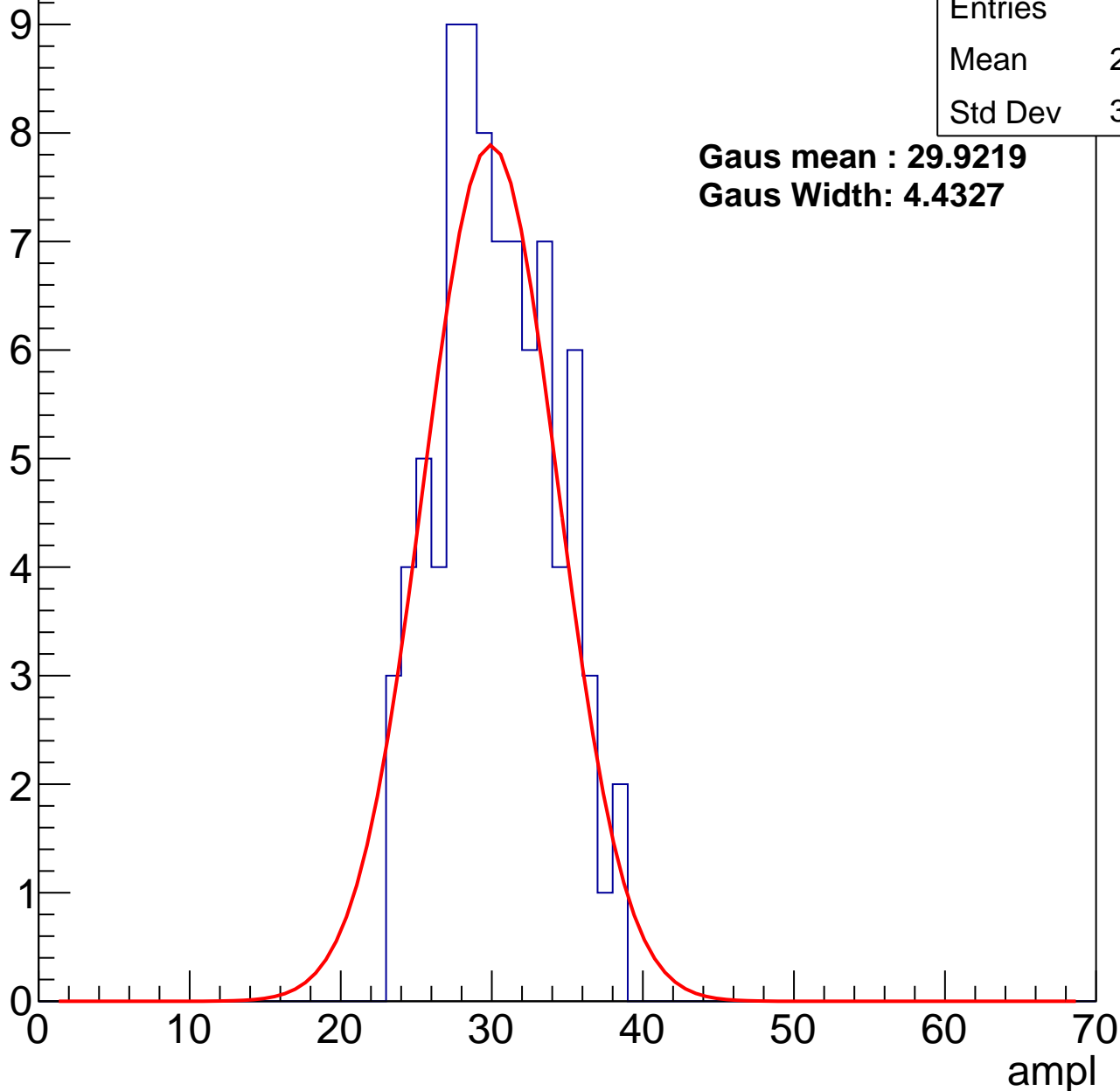
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	29.86
Std Dev	3.752

Gaus mean : 29.9219

Gaus Width: 4.4327



B1L102S, U13-ch125, adc1

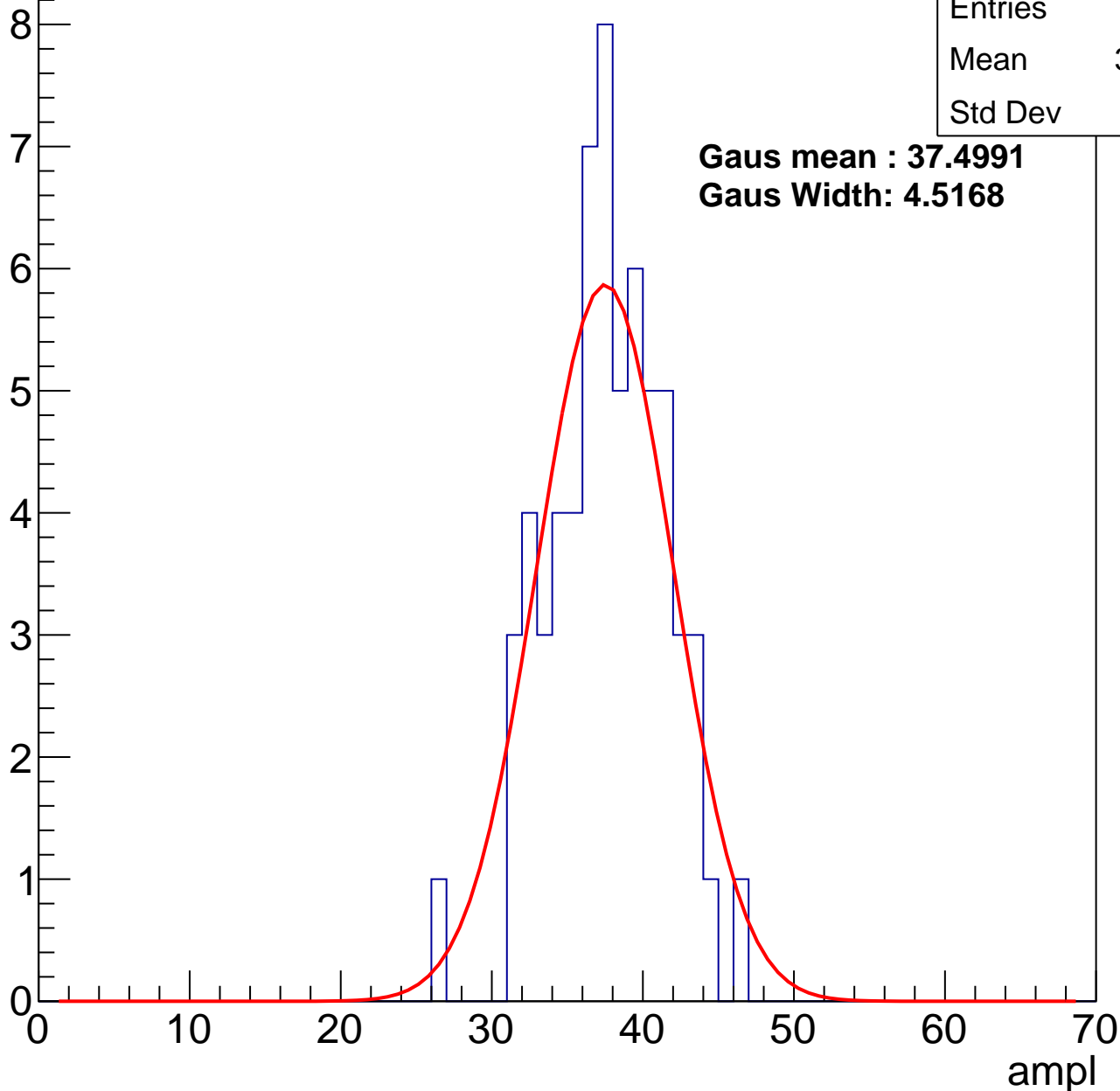
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.21
Std Dev	3.78

Gaus mean : 37.4991

Gaus Width: 4.5168



B1L102S, U13-ch125, adc2

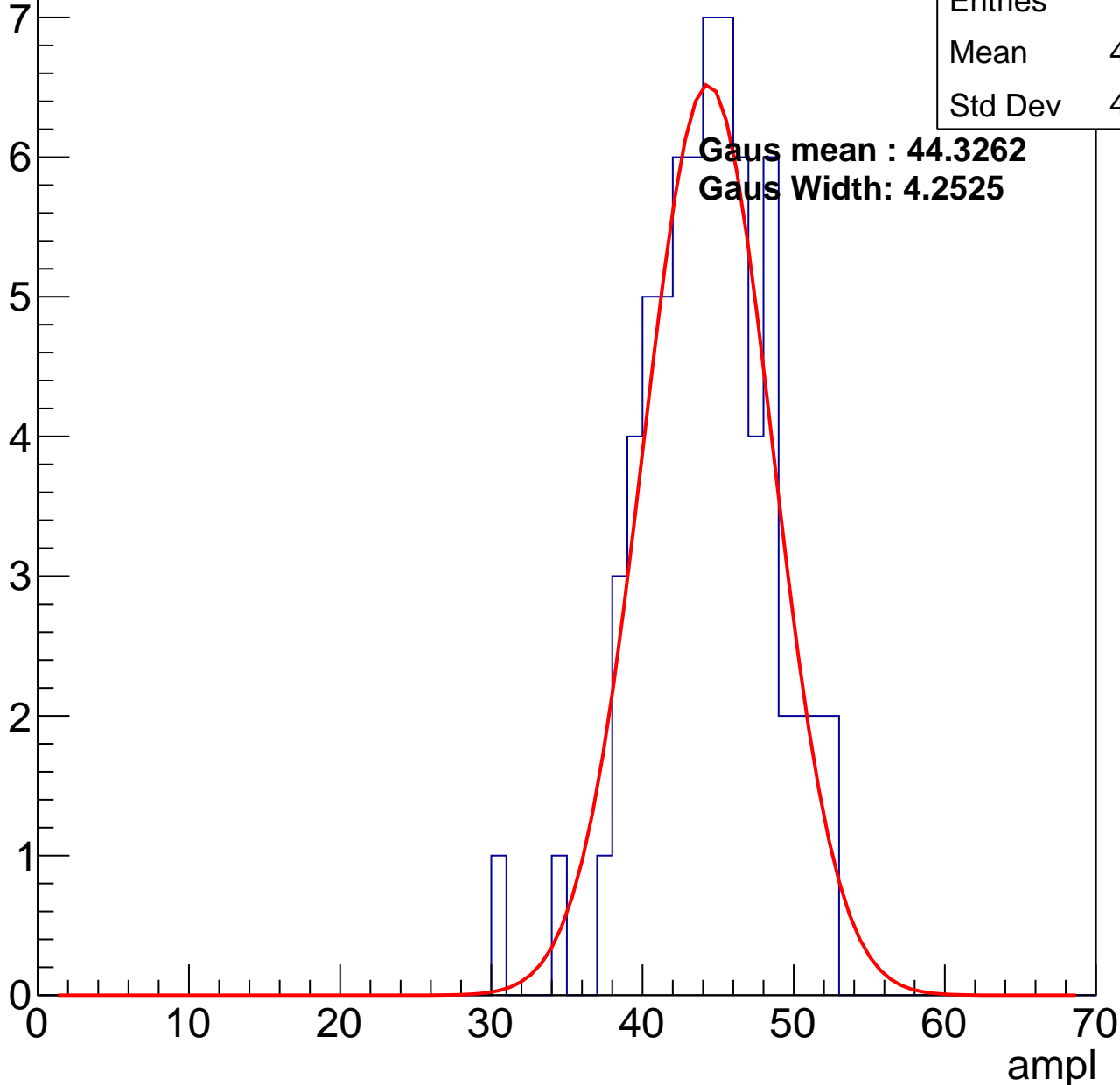
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.79
Std Dev	4.168

Gaus mean : 44.3262

Gaus Width: 4.2525

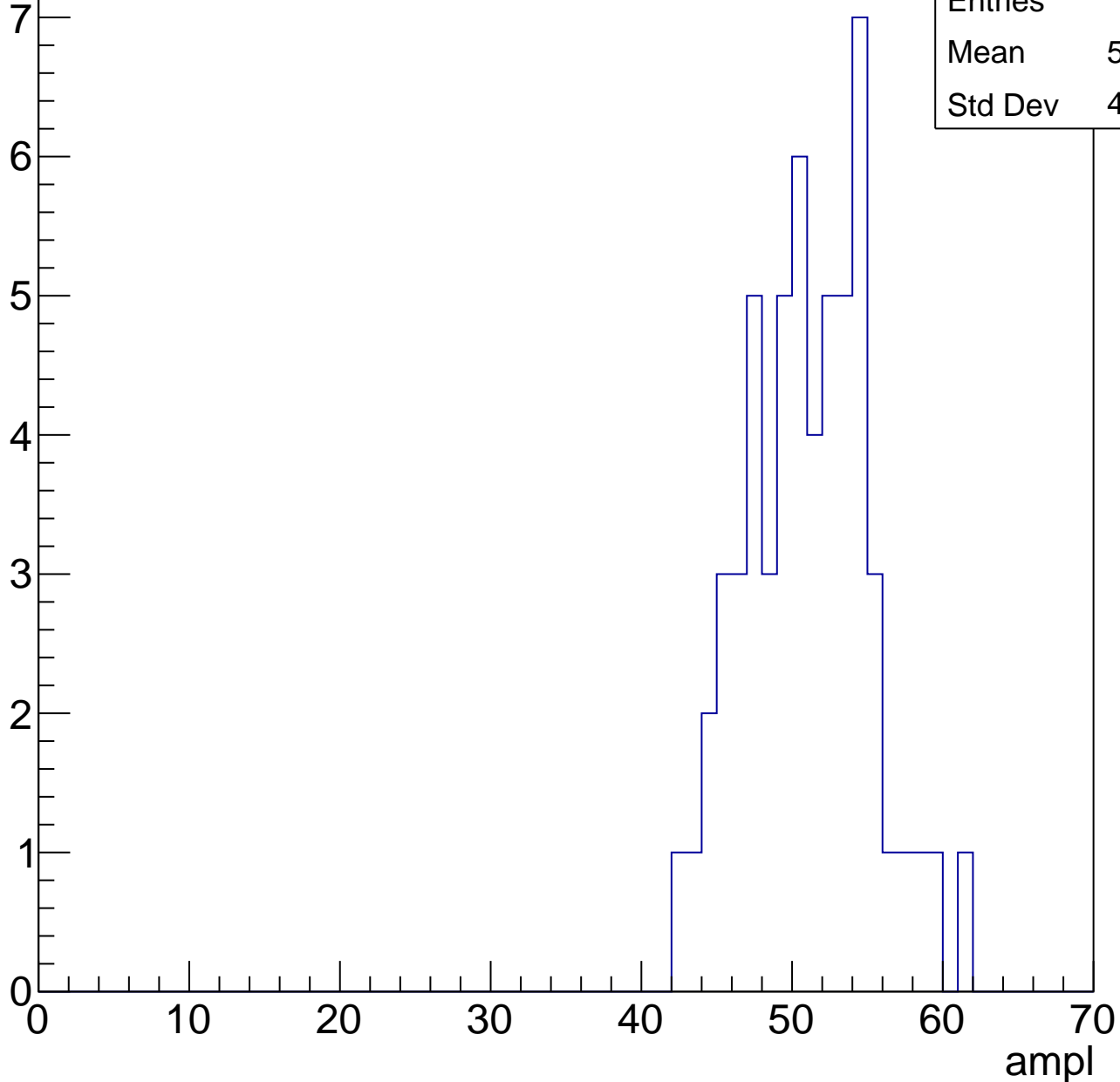


B1L102S, U13-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.57
Std Dev	4.065

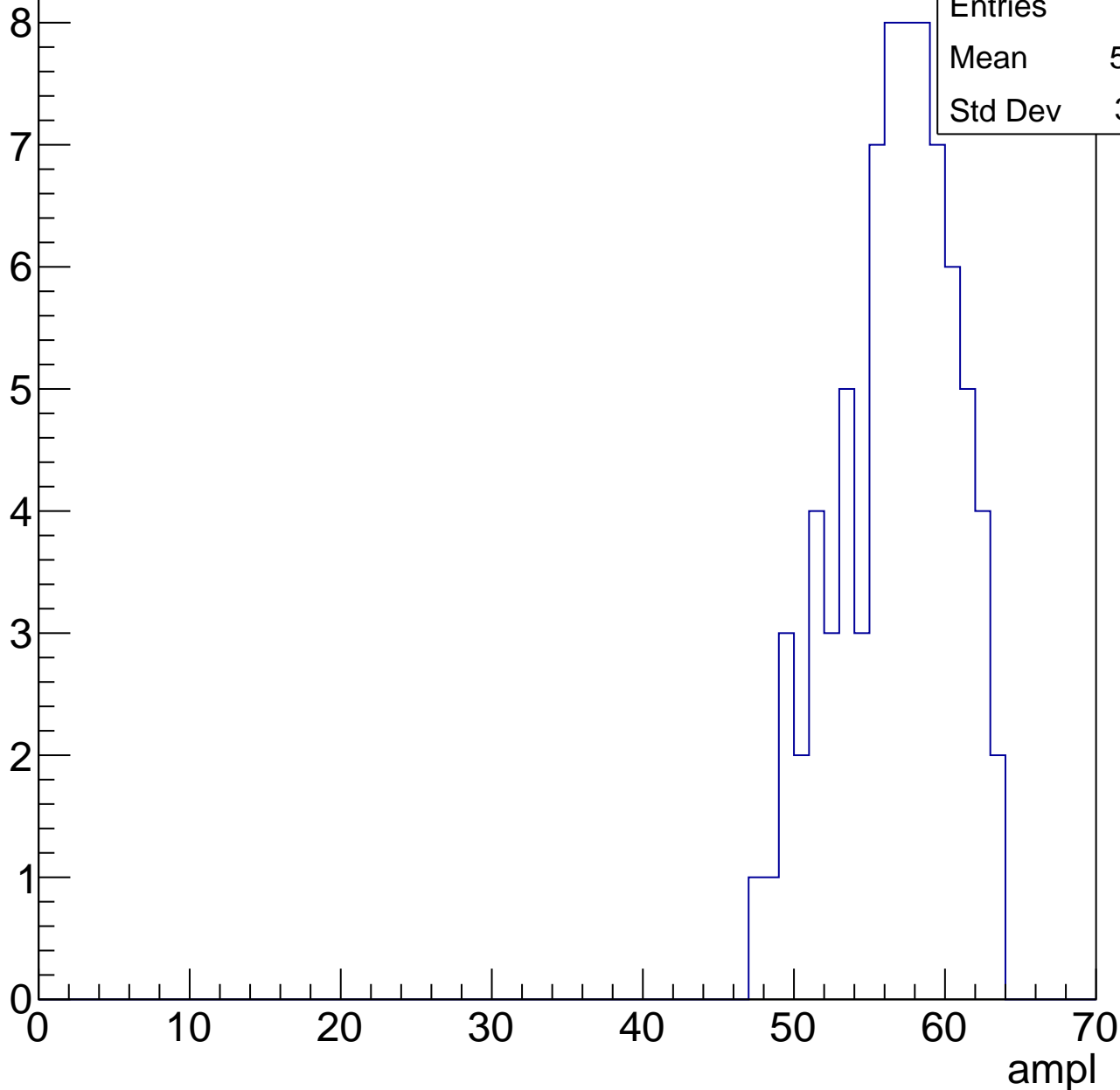


B1L102S, U13-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	56.29
Std Dev	3.841

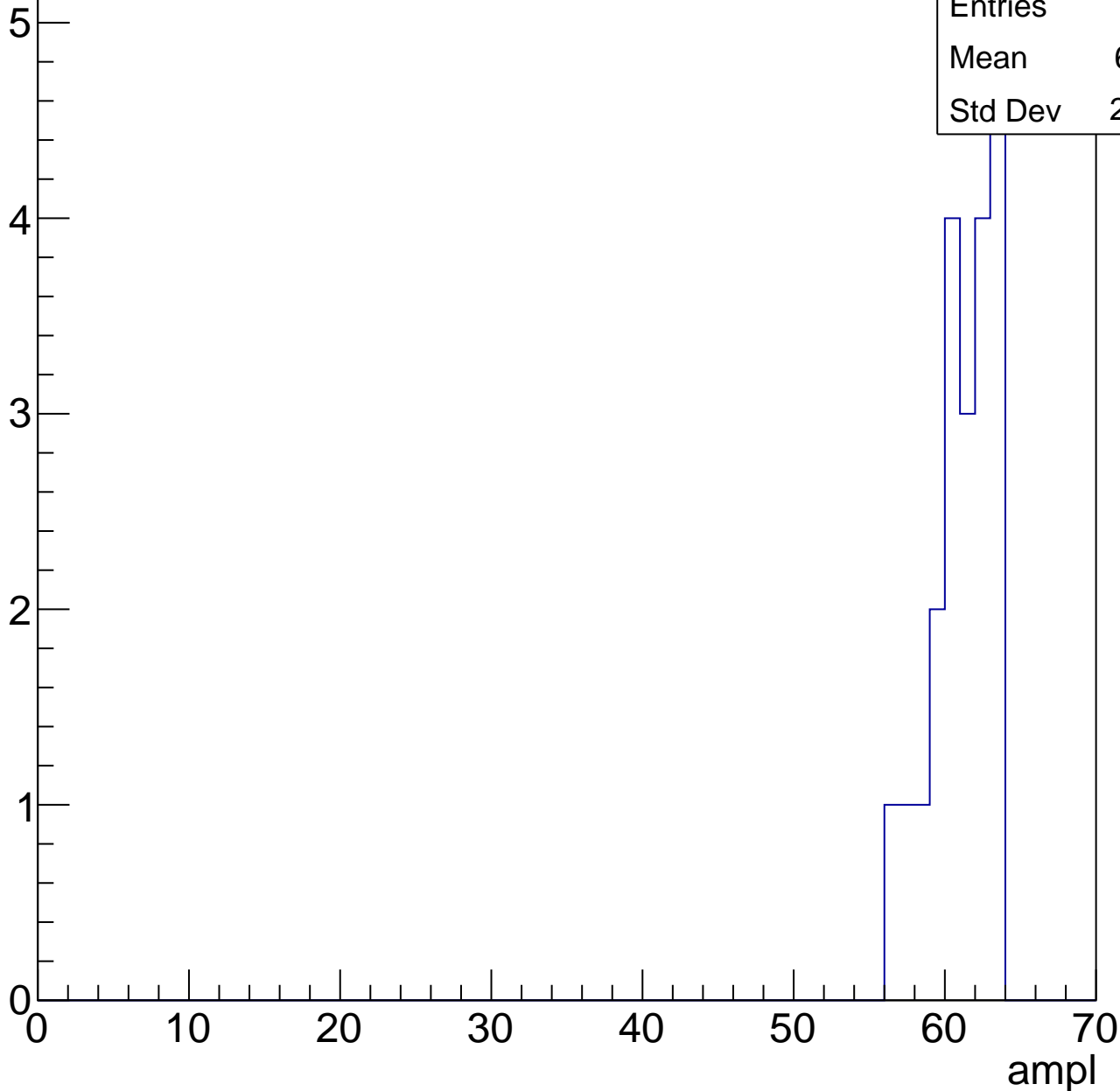


B1L102S, U13-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	60.71
Std Dev	2.003



B1L102S, U13-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	7
Mean	53.43
Std Dev	21.82

B1L102S, U13-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch126, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	92
Mean	29.3
Std Dev	4.336

Gaus mean : 29.6097

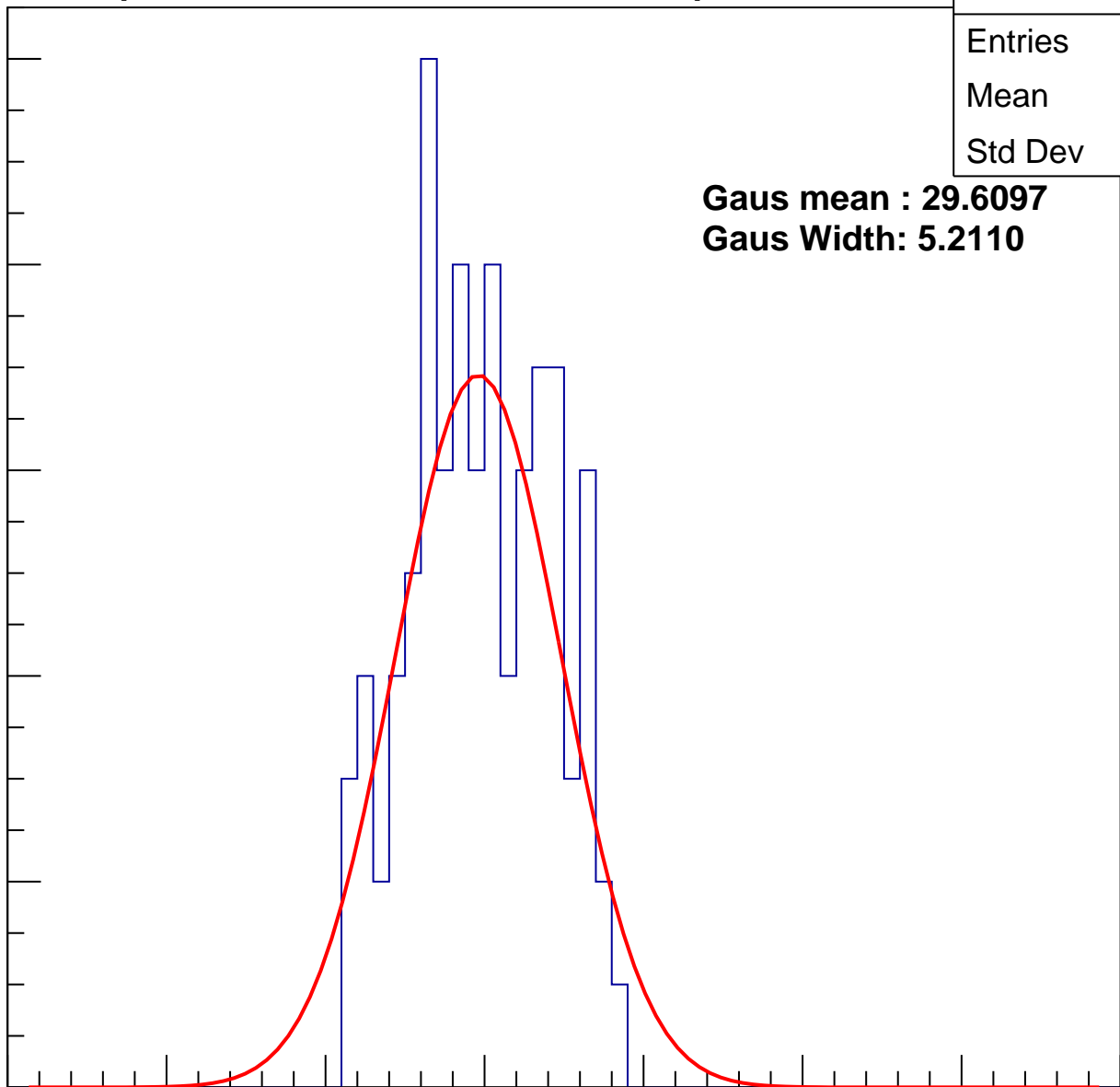
Gaus Width: 5.2110

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch126, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	37.83
Std Dev	3.655

Gaus mean : 38.4352

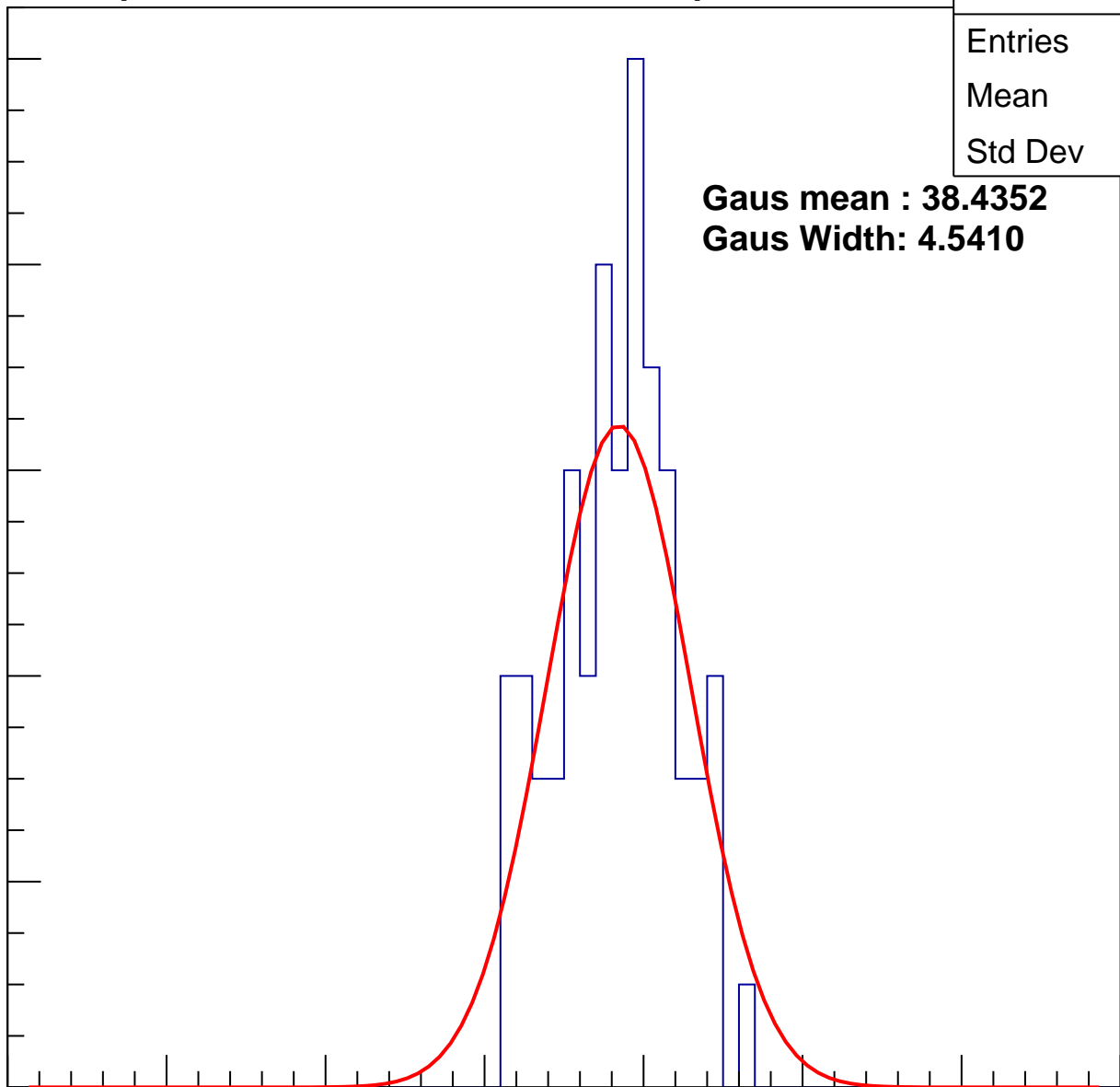
Gaus Width: 4.5410

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U13-ch126, adc2

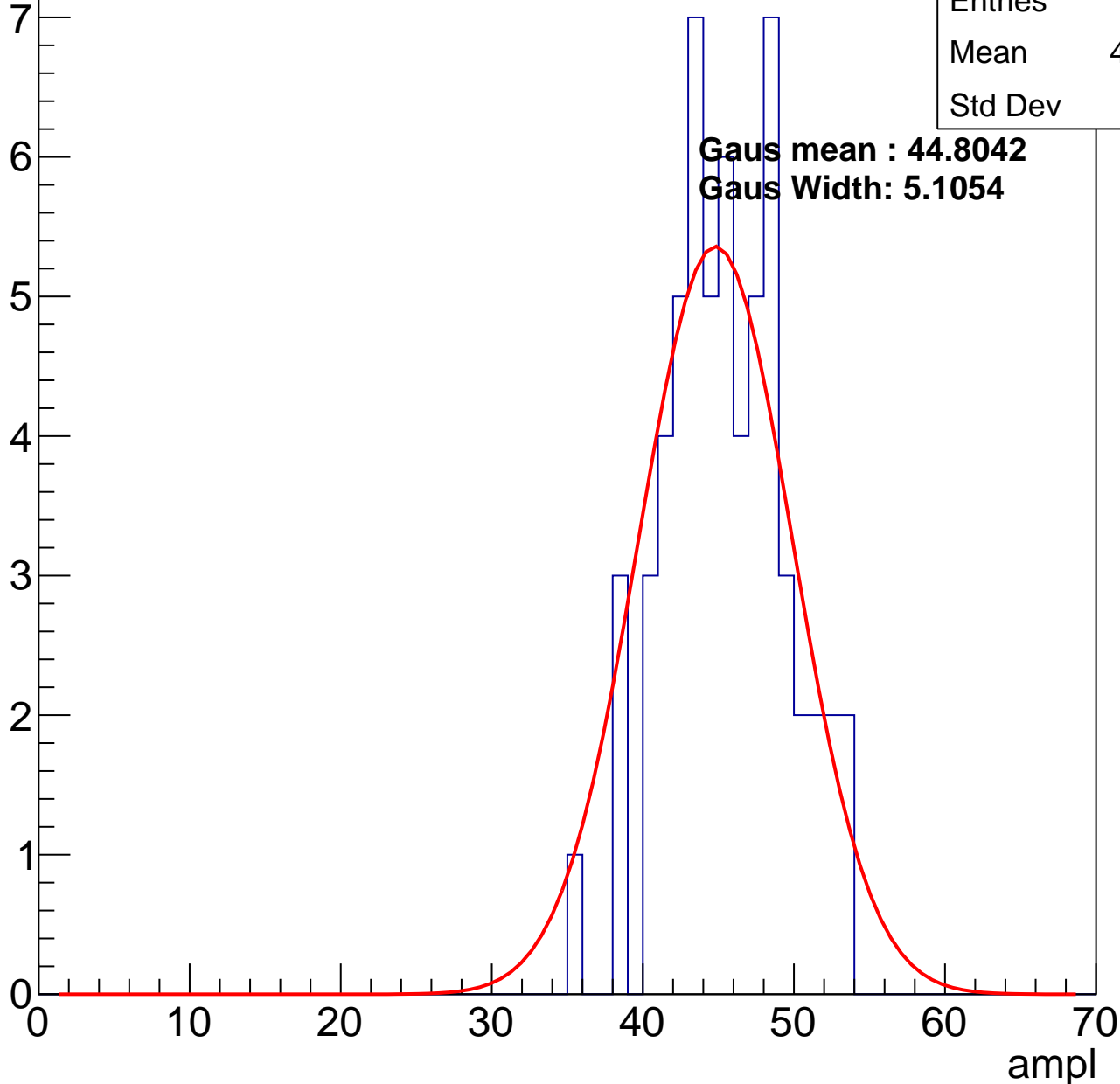
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	45.05
Std Dev	3.94

Gaus mean : 44.8042

Gaus Width: 5.1054

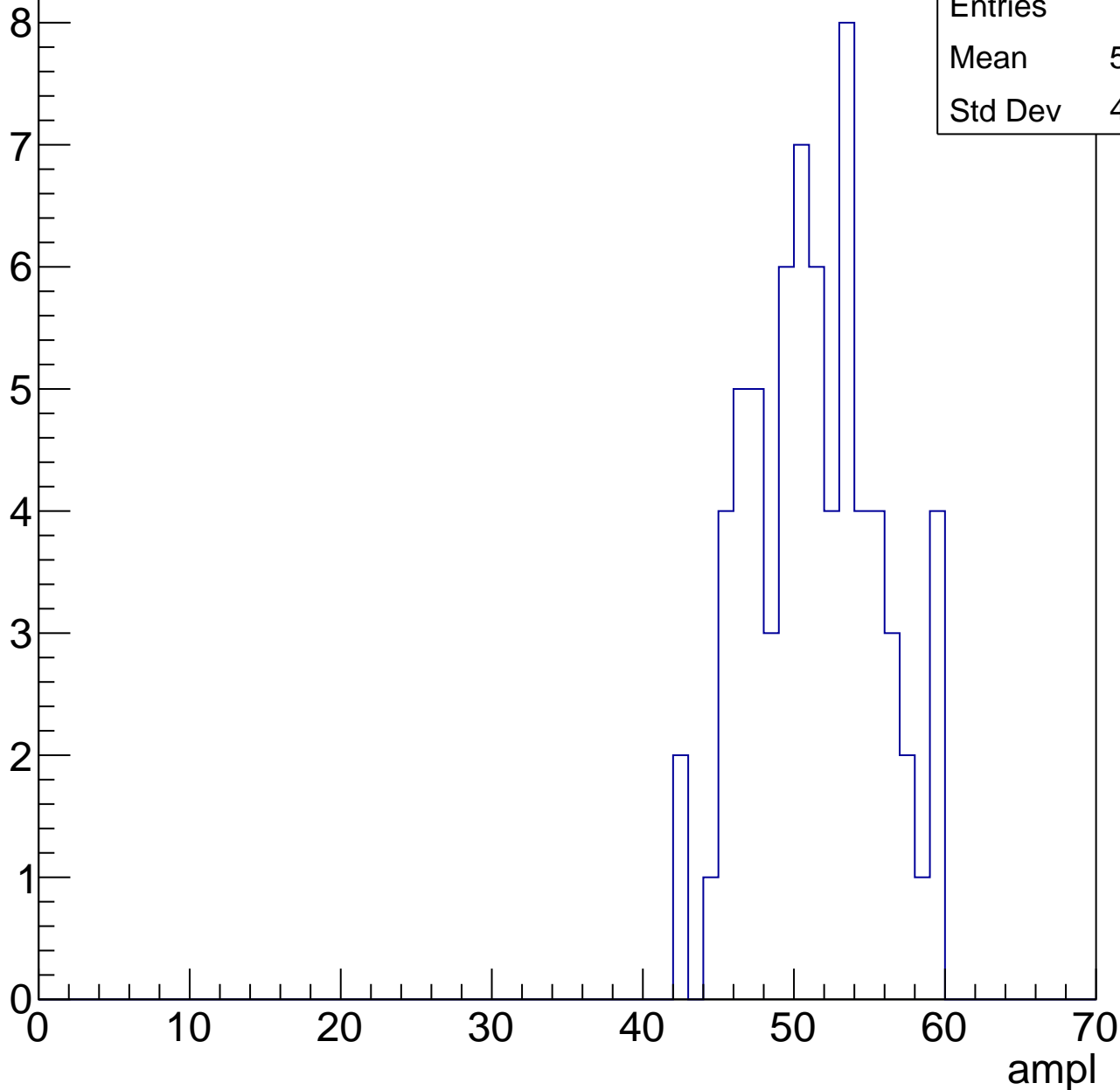


B1L102S, U13-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	50.88
Std Dev	4.179

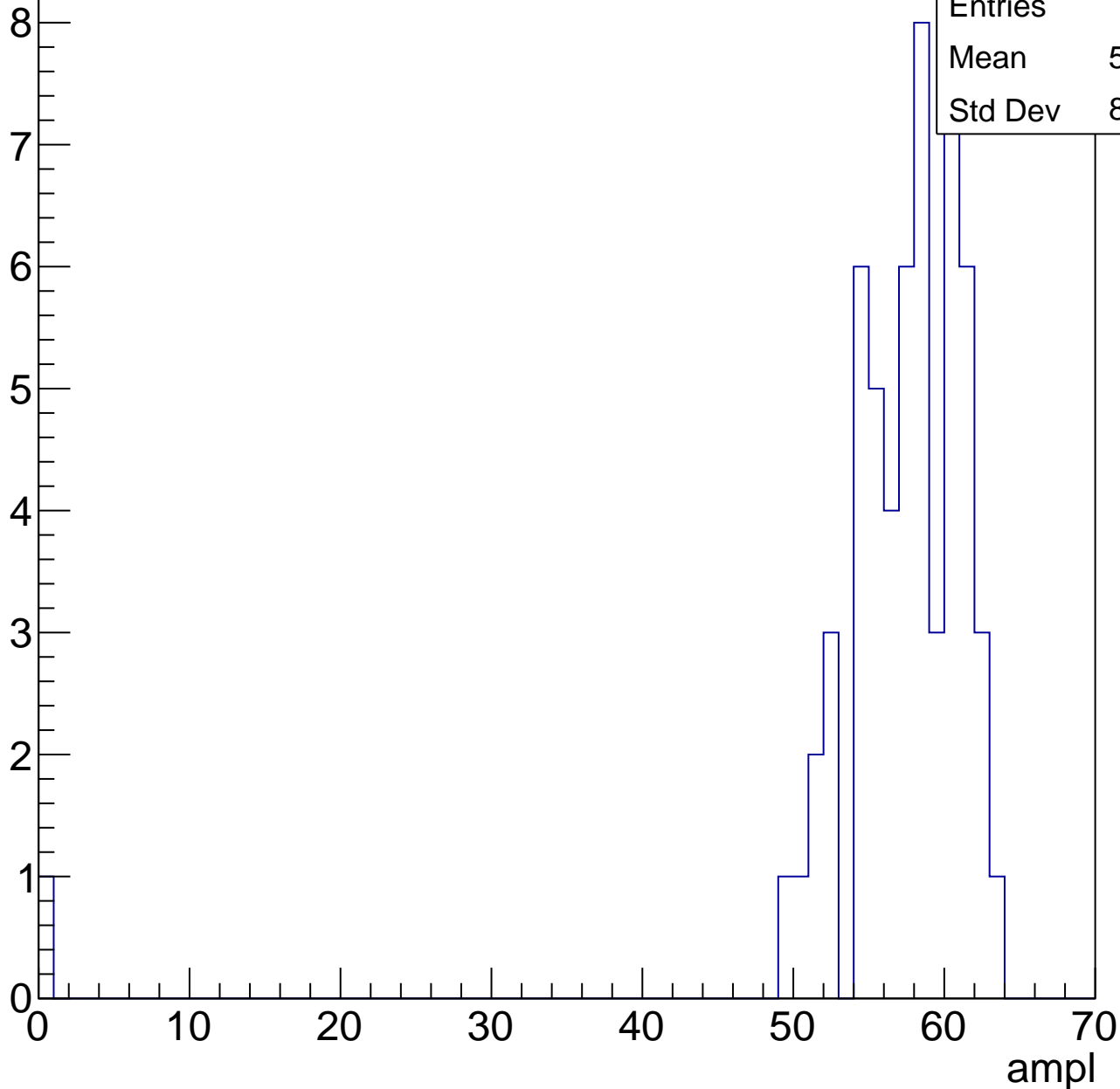


B1L102S, U13-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.17
Std Dev	8.143

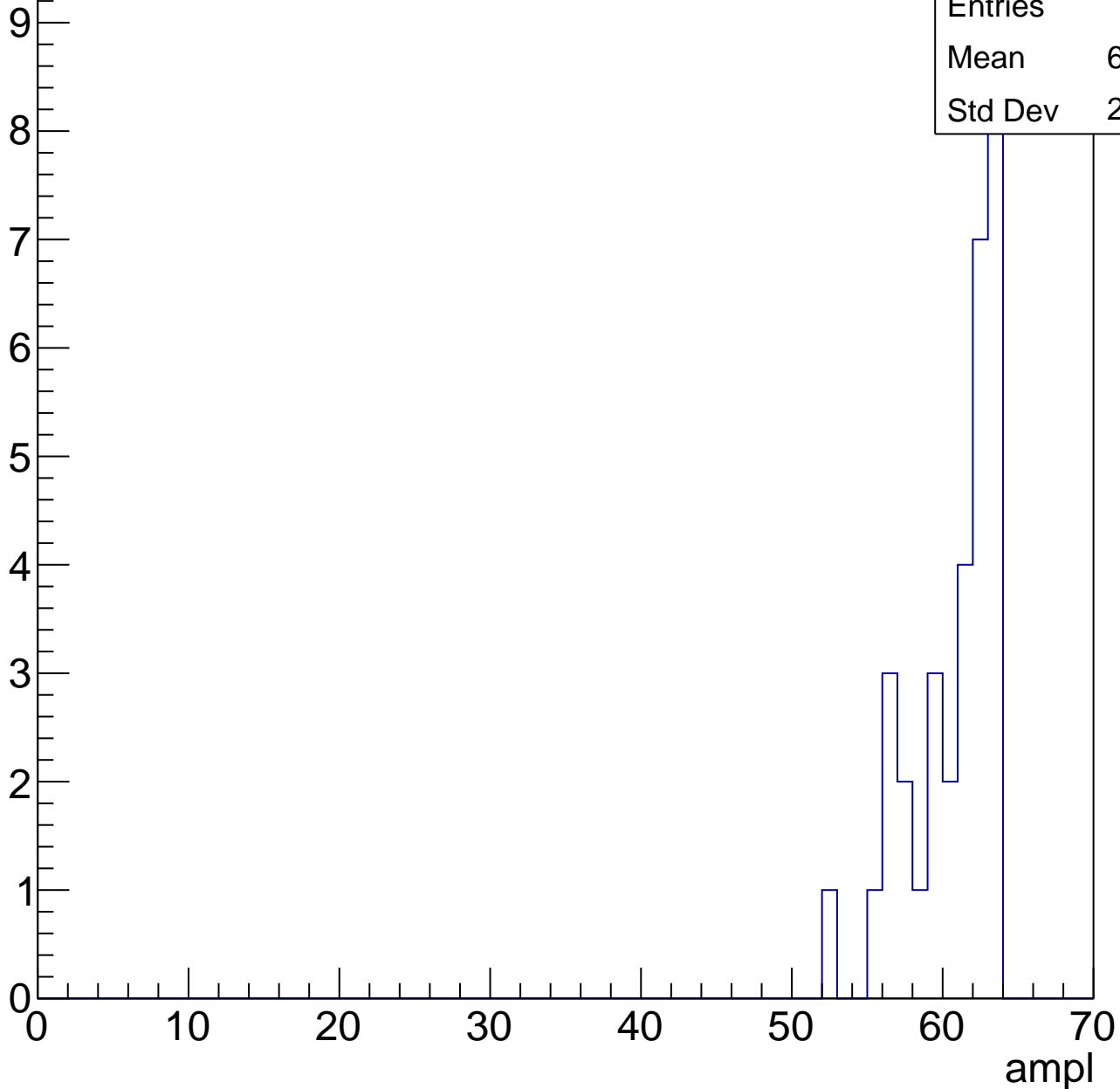


B1L102S, U13-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	60.27
Std Dev	2.874



B1L102S, U13-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U13-ch127, adc0

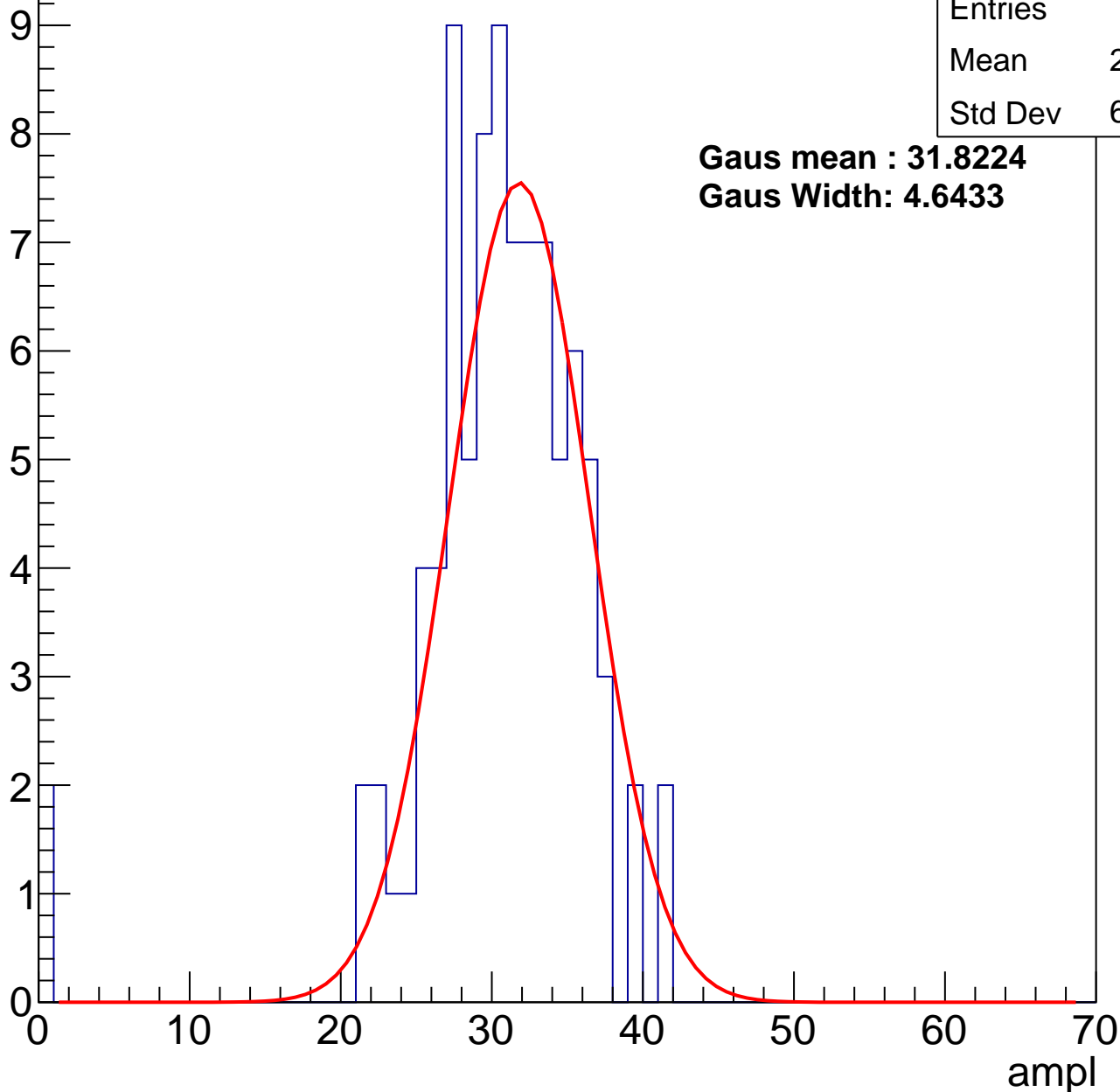
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	91
Mean	29.95
Std Dev	6.208

Gaus mean : 31.8224

Gaus Width: 4.6433



B1L102S, U13-ch127, adc1

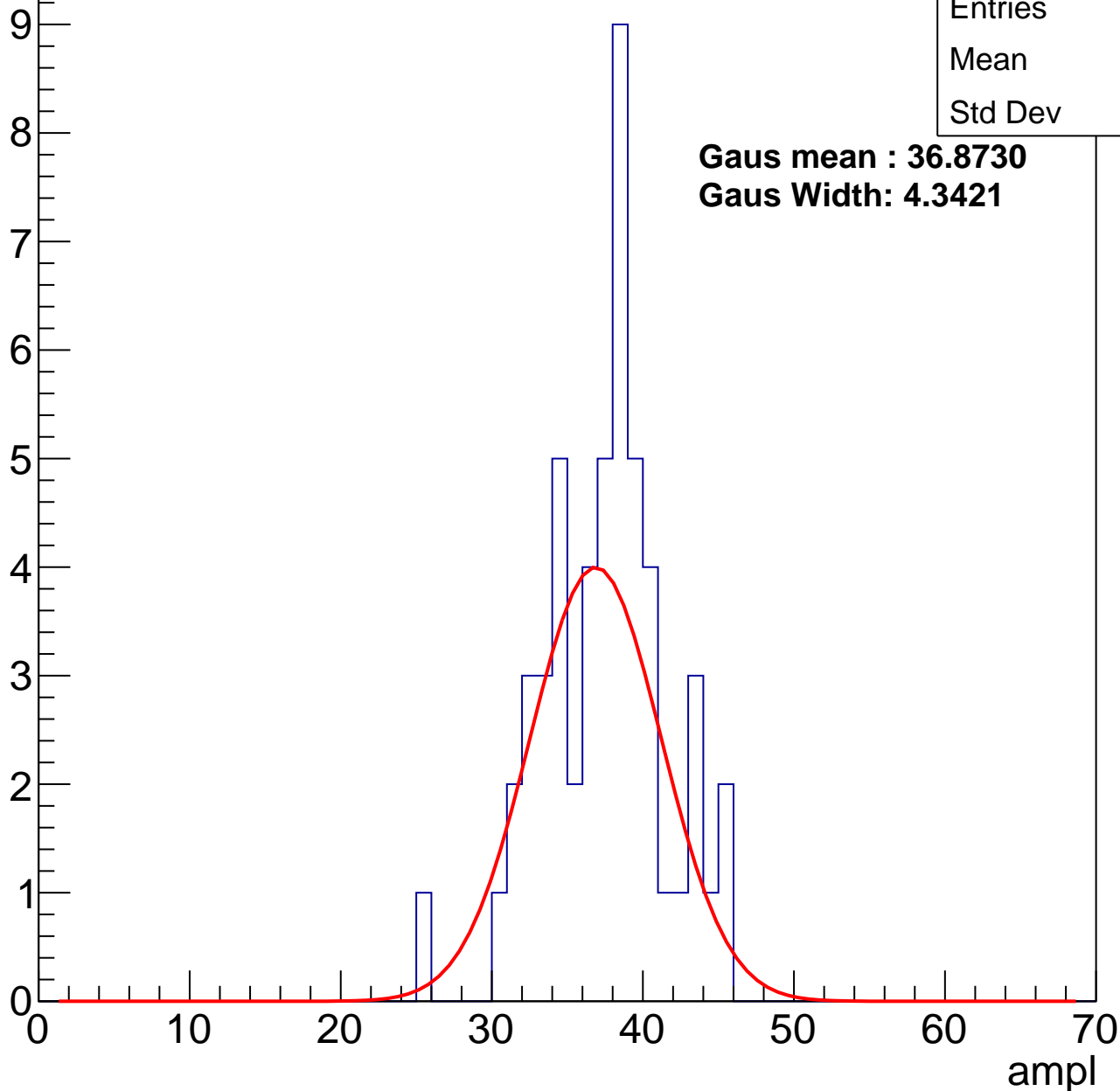
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	37
Std Dev	4.01

Gaus mean : 36.8730

Gaus Width: 4.3421



B1L102S, U13-ch127, adc2

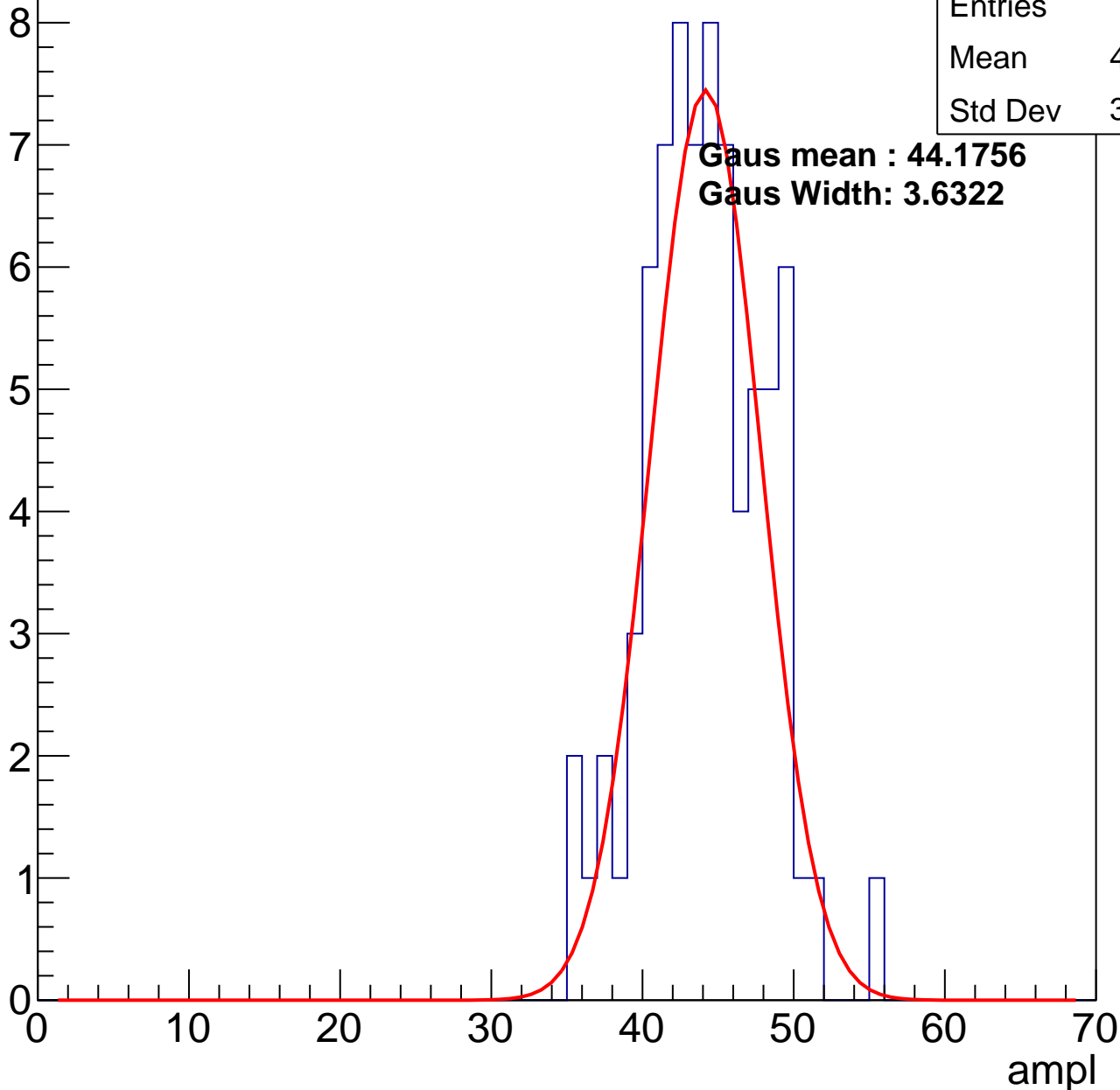
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.67
Std Dev	3.879

Gaus mean : 44.1756

Gaus Width: 3.6322

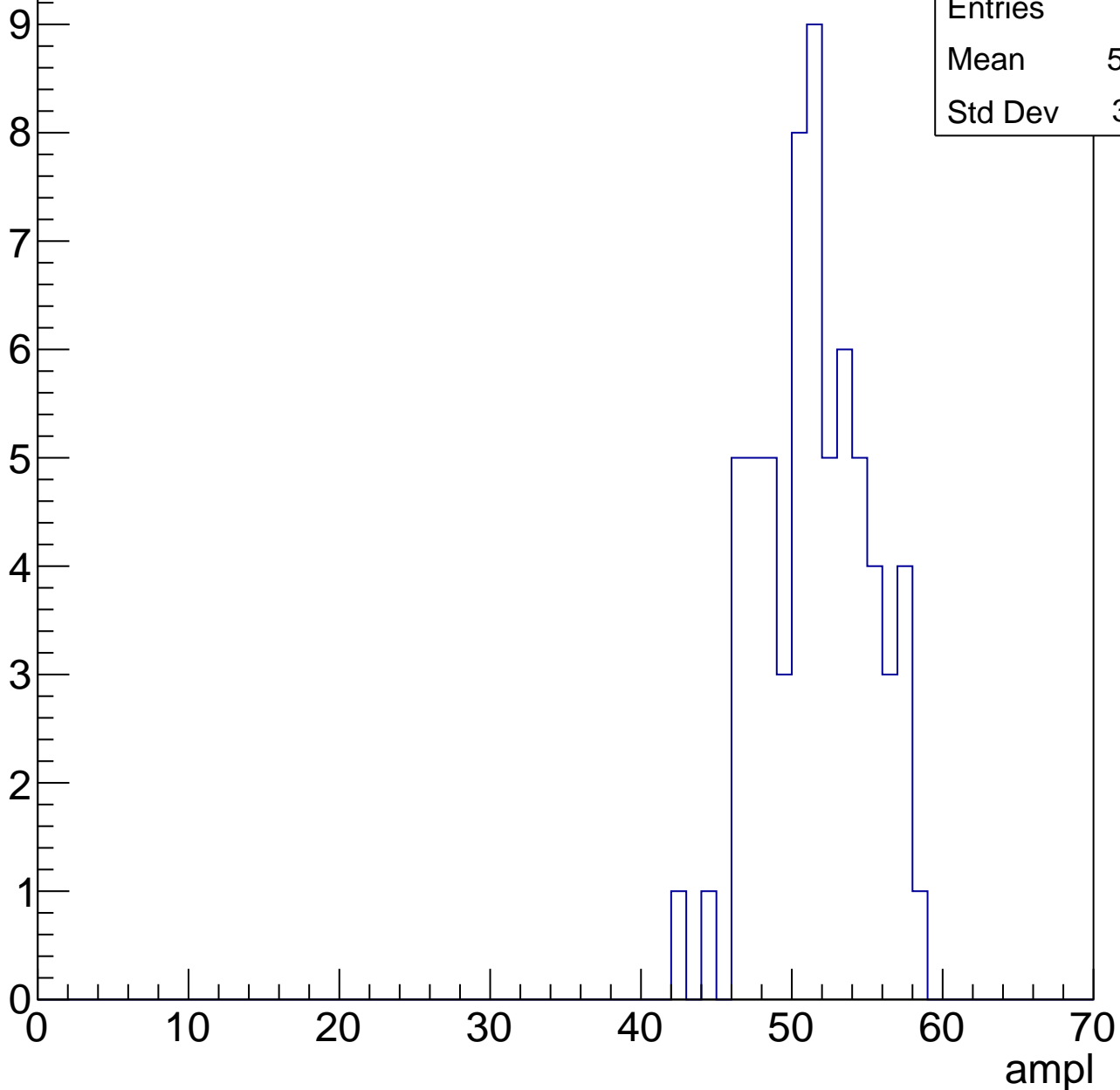


B1L102S, U13-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	51.06
Std Dev	3.521

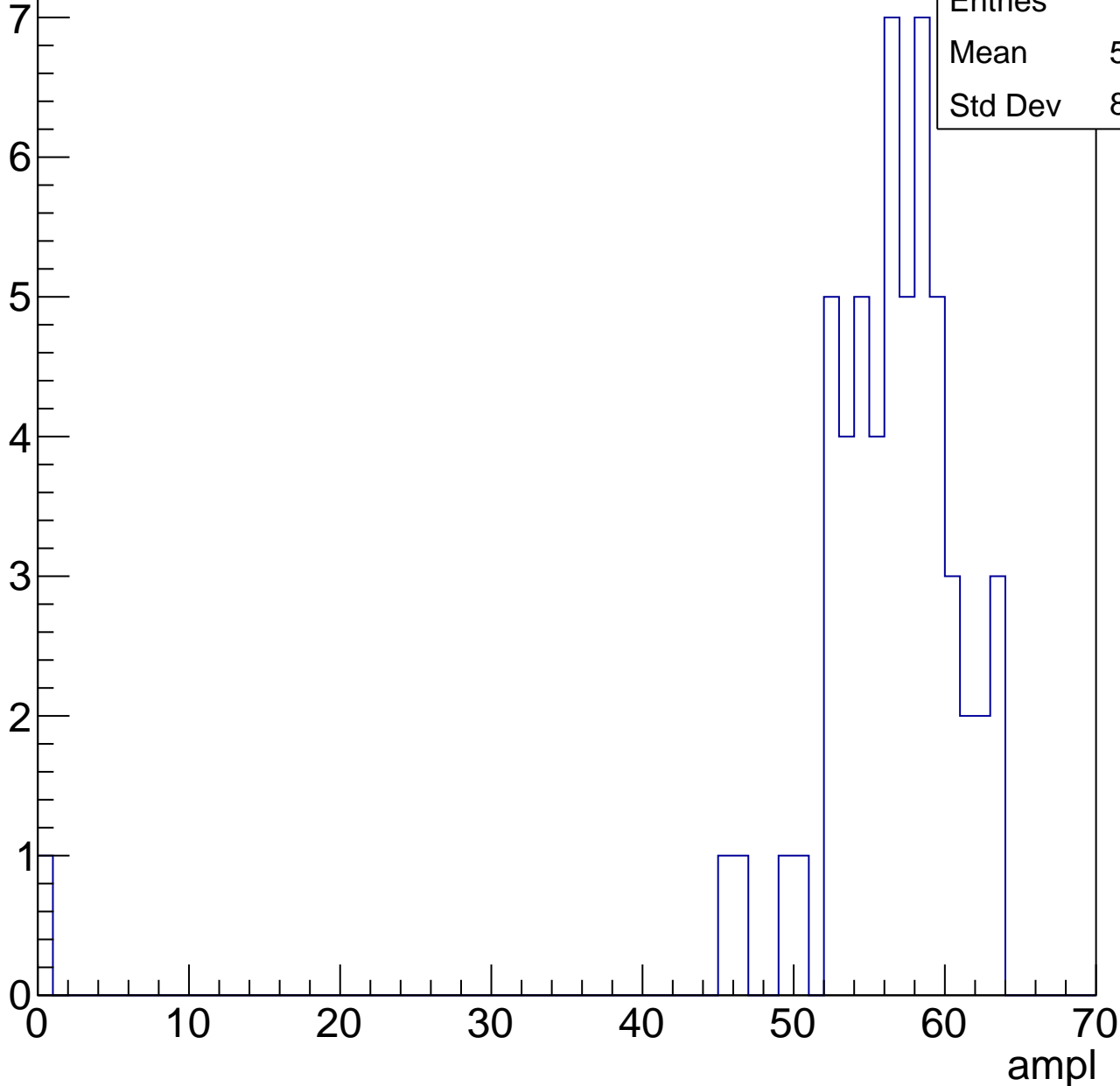


B1L102S, U13-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.18
Std Dev	8.312

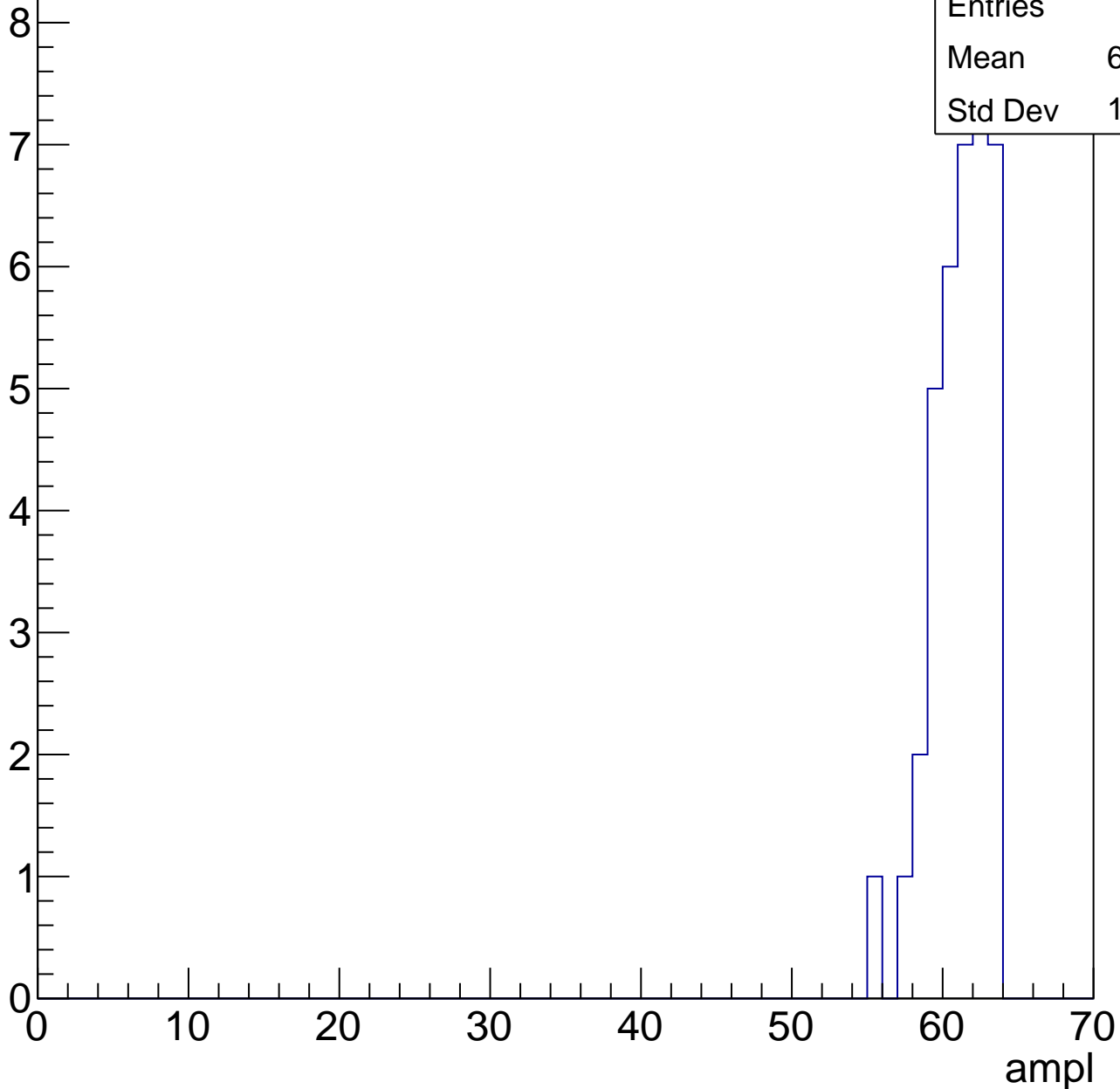


B1L102S, U13-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	60.73
Std Dev	1.869



B1L102S, U13-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

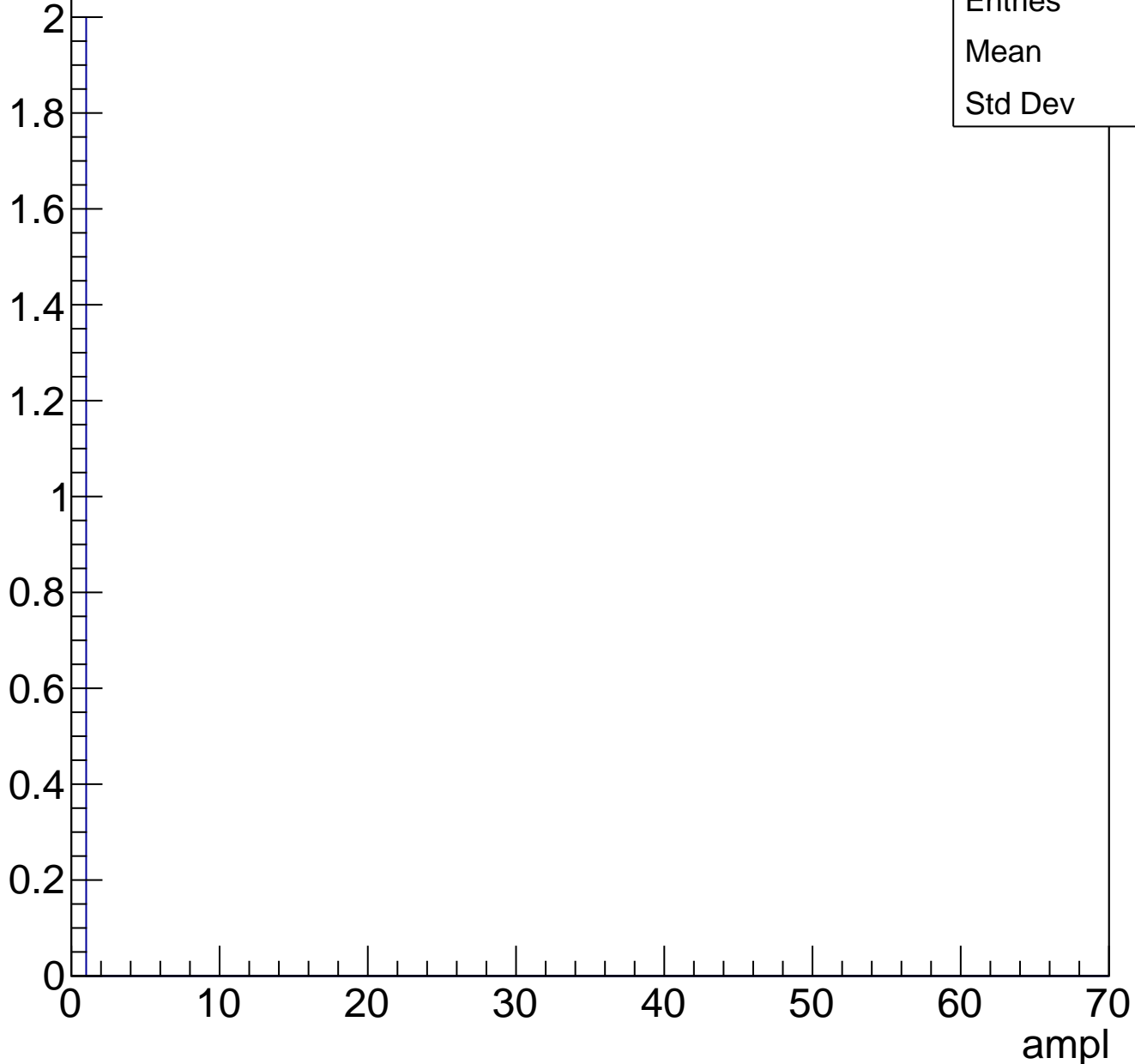


Entries	2
Mean	0
Std Dev	0

B1L102S, U13-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0