

B0L002S, U26-ch0, adc0

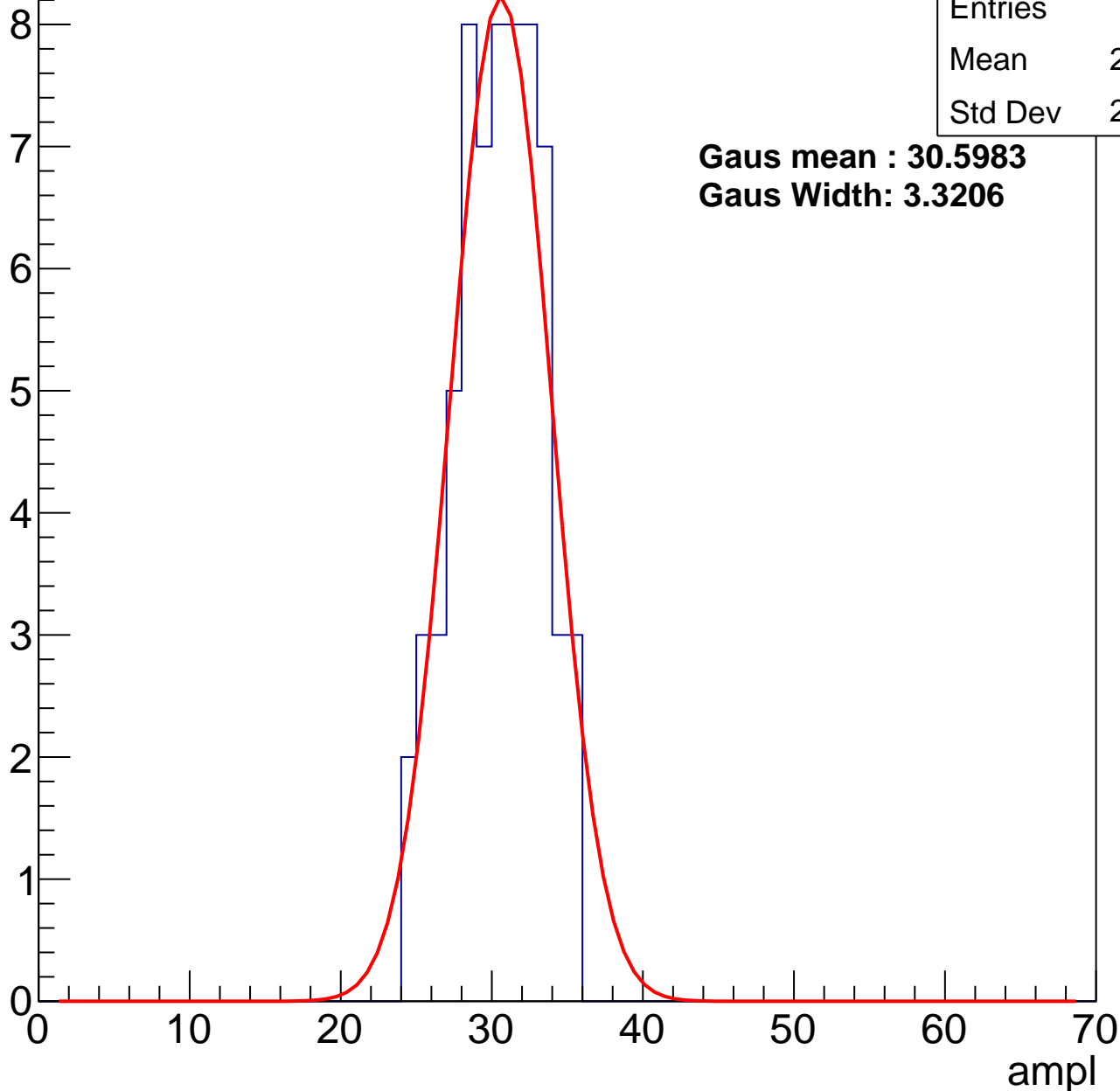
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.92
Std Dev	2.786

Gaus mean : 30.5983

Gaus Width: 3.3206



B0L002S, U26-ch0, adc1

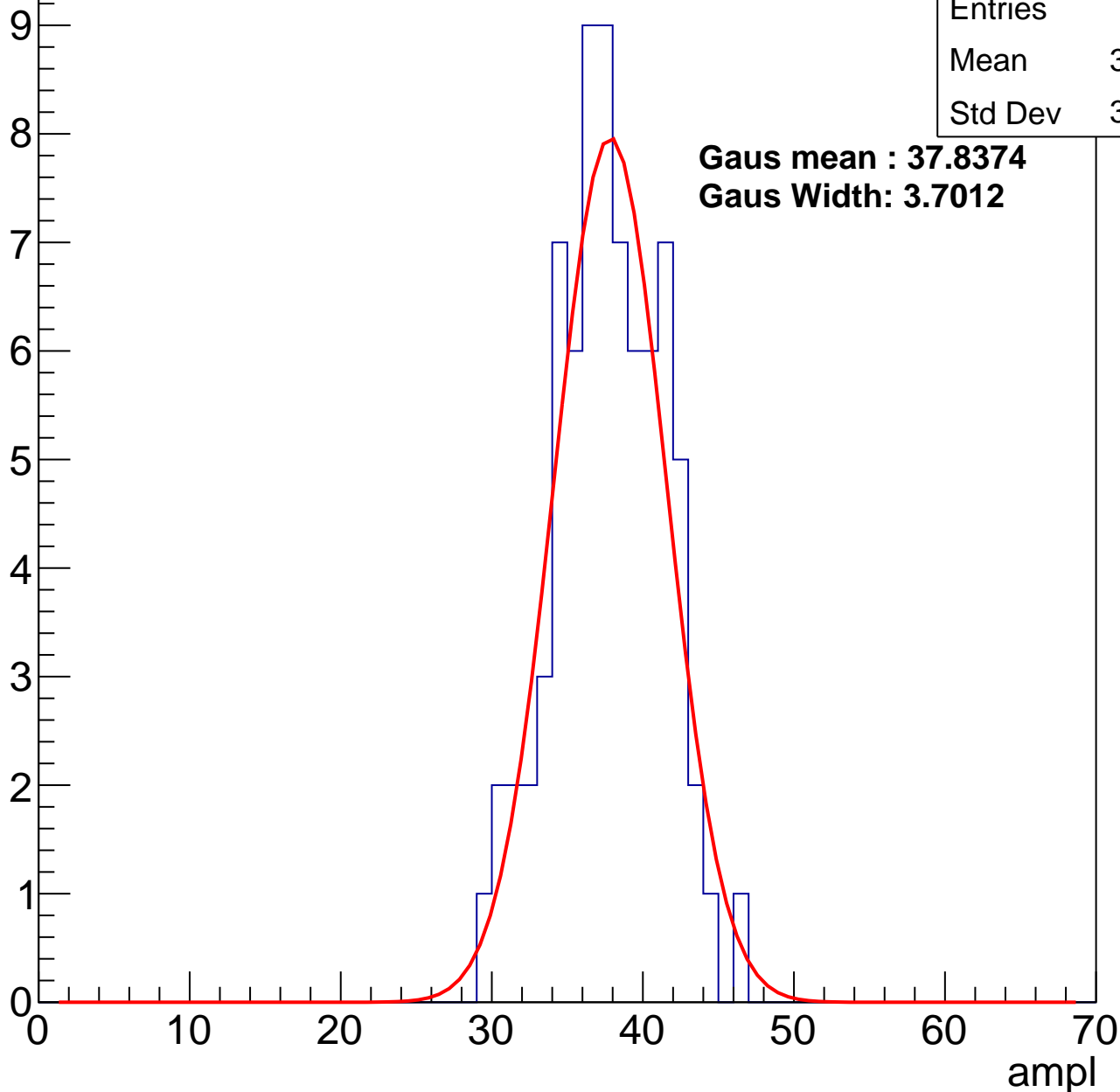
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.26
Std Dev	3.529

Gaus mean : 37.8374

Gaus Width: 3.7012



B0L002S, U26-ch0, adc2

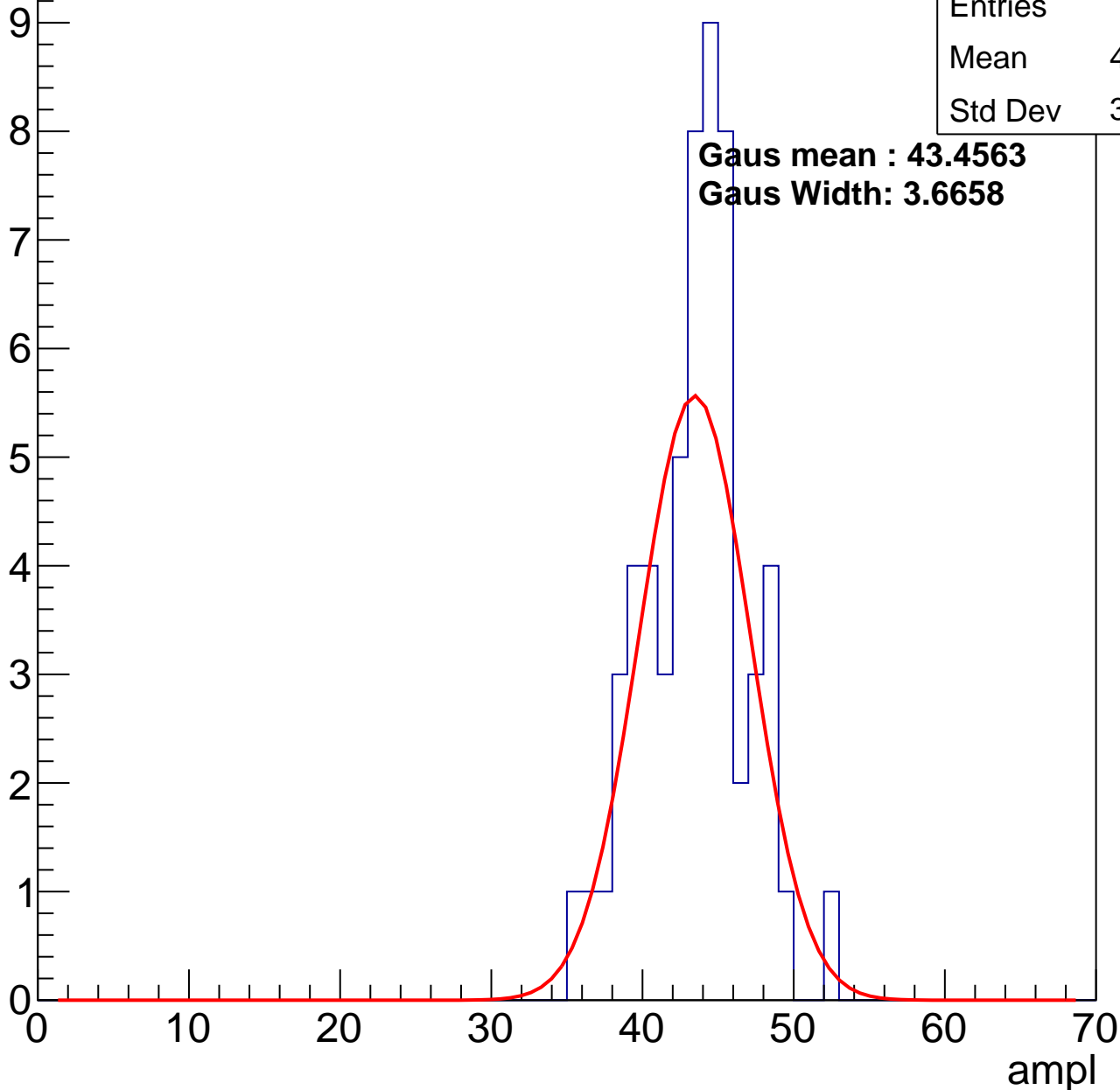
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.05
Std Dev	3.386

Gaus mean : 43.4563

Gaus Width: 3.6658

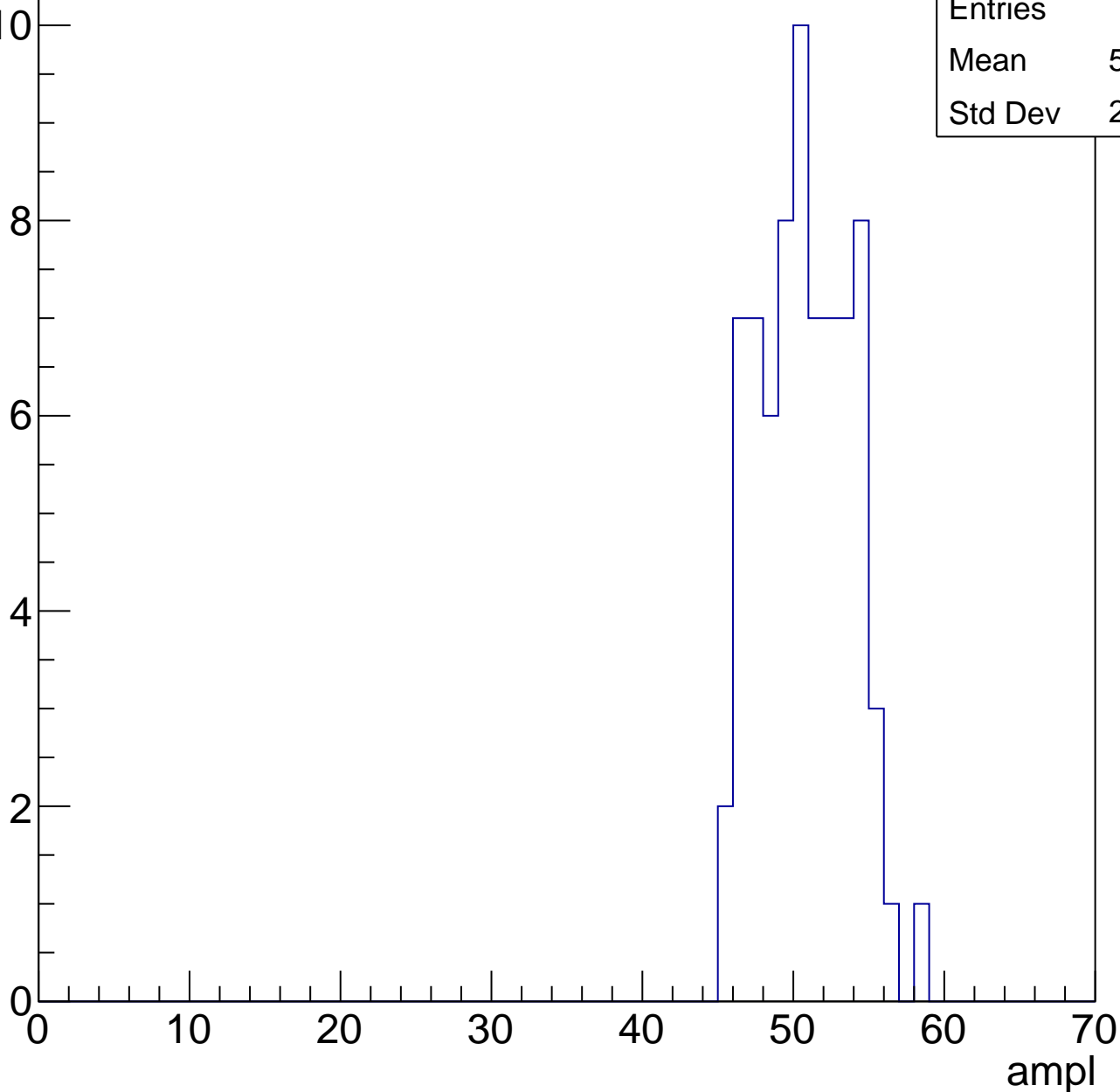


B0L002S, U26-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	50.32
Std Dev	2.964

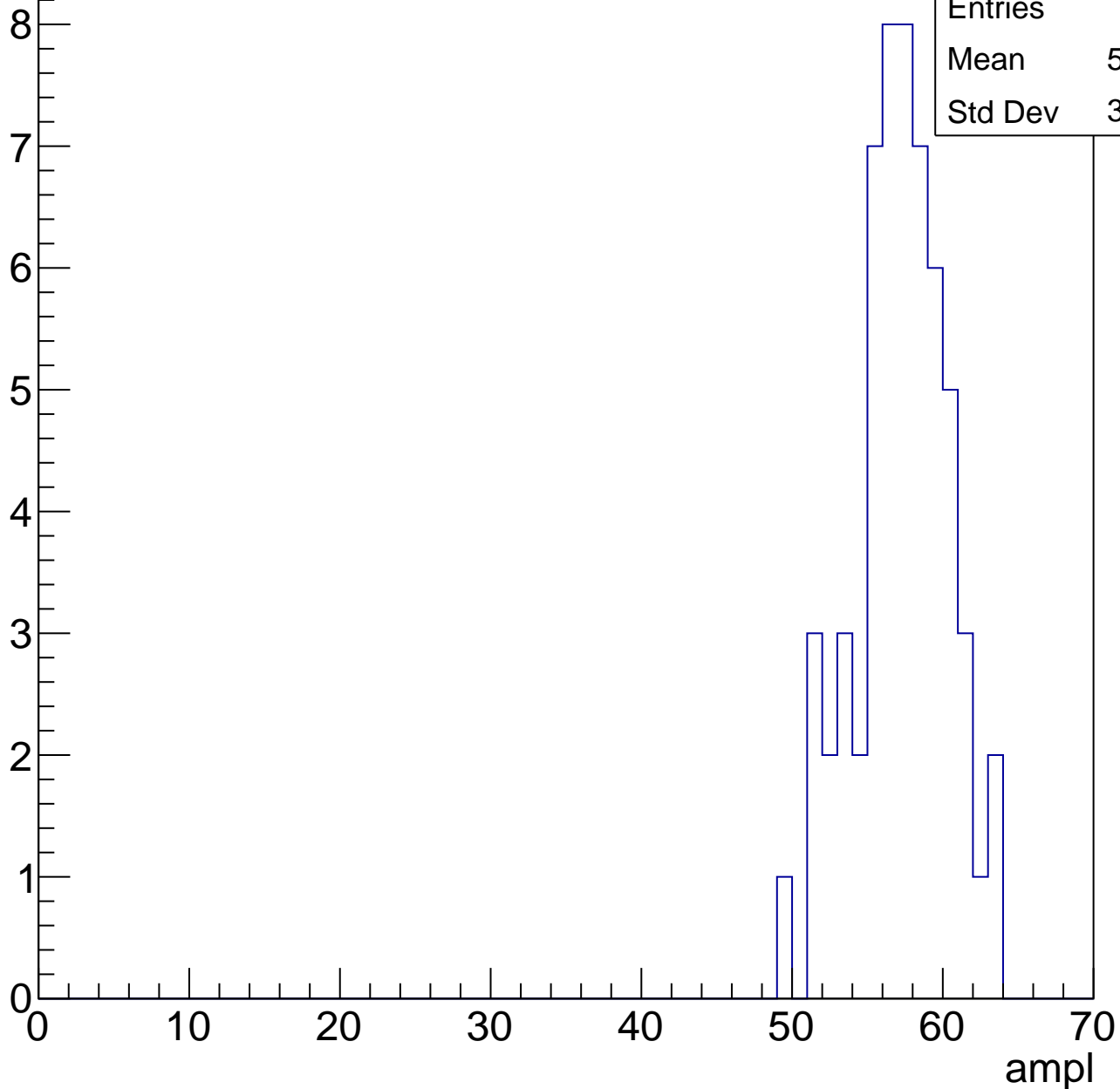


B0L002S, U26-ch0, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.78
Std Dev	3.057

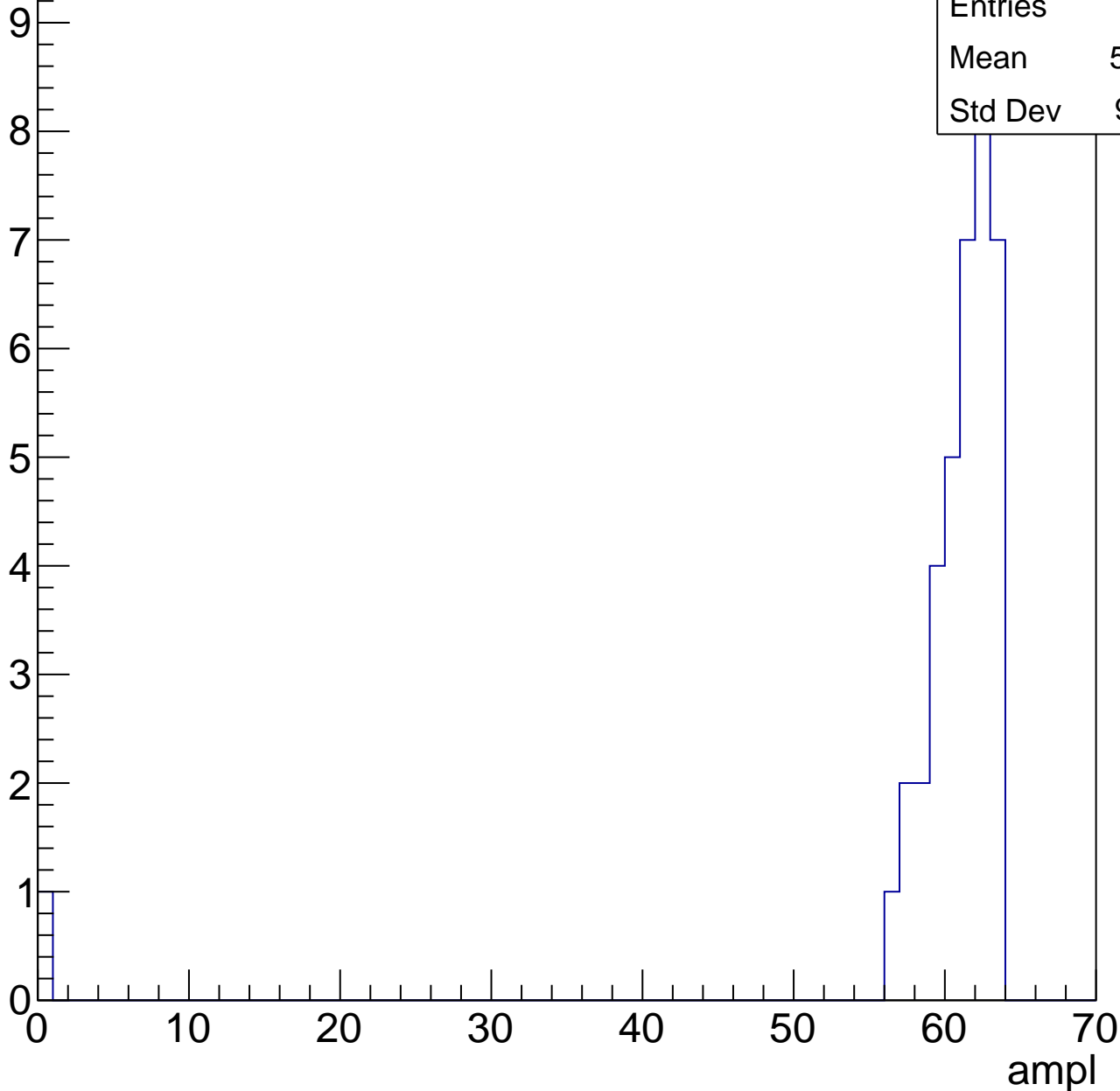


B0L002S, U26-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.16
Std Dev	9.901



B0L002S, U26-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch1, adc0

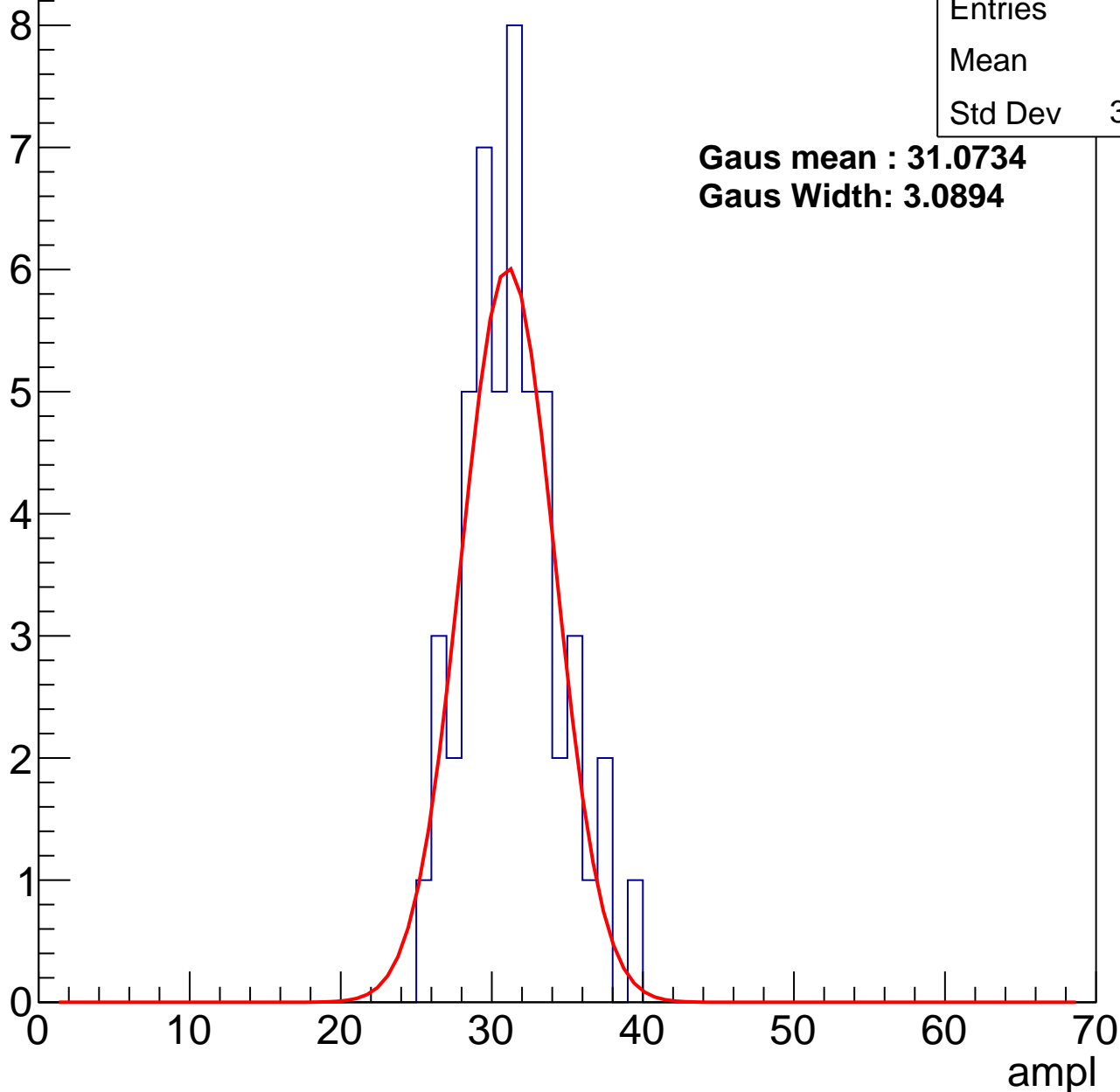
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	30.9
Std Dev	3.074

Gaus mean : 31.0734

Gaus Width: 3.0894



B0L002S, U26-ch1, adc1

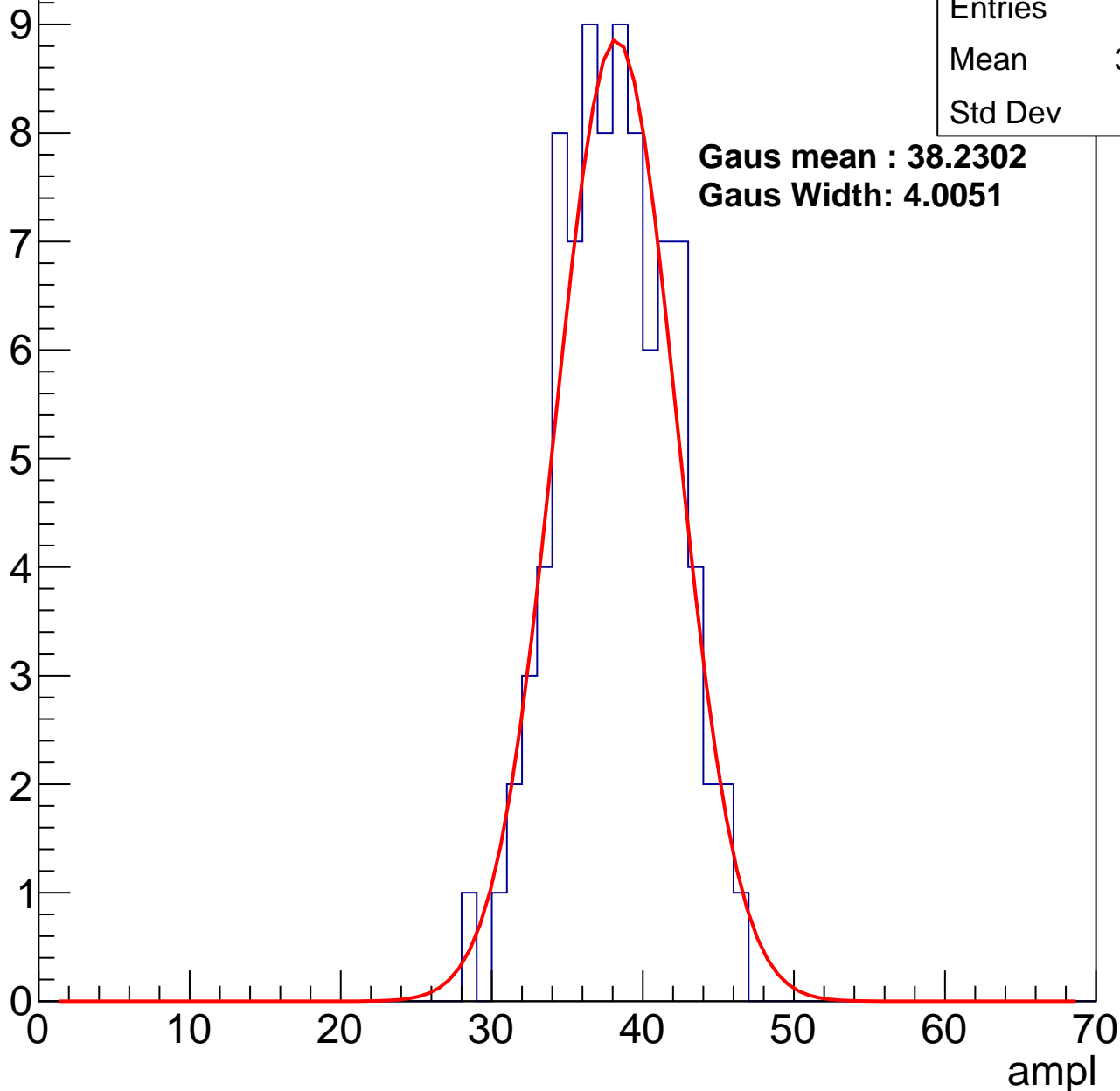
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	89
Mean	37.71
Std Dev	3.73

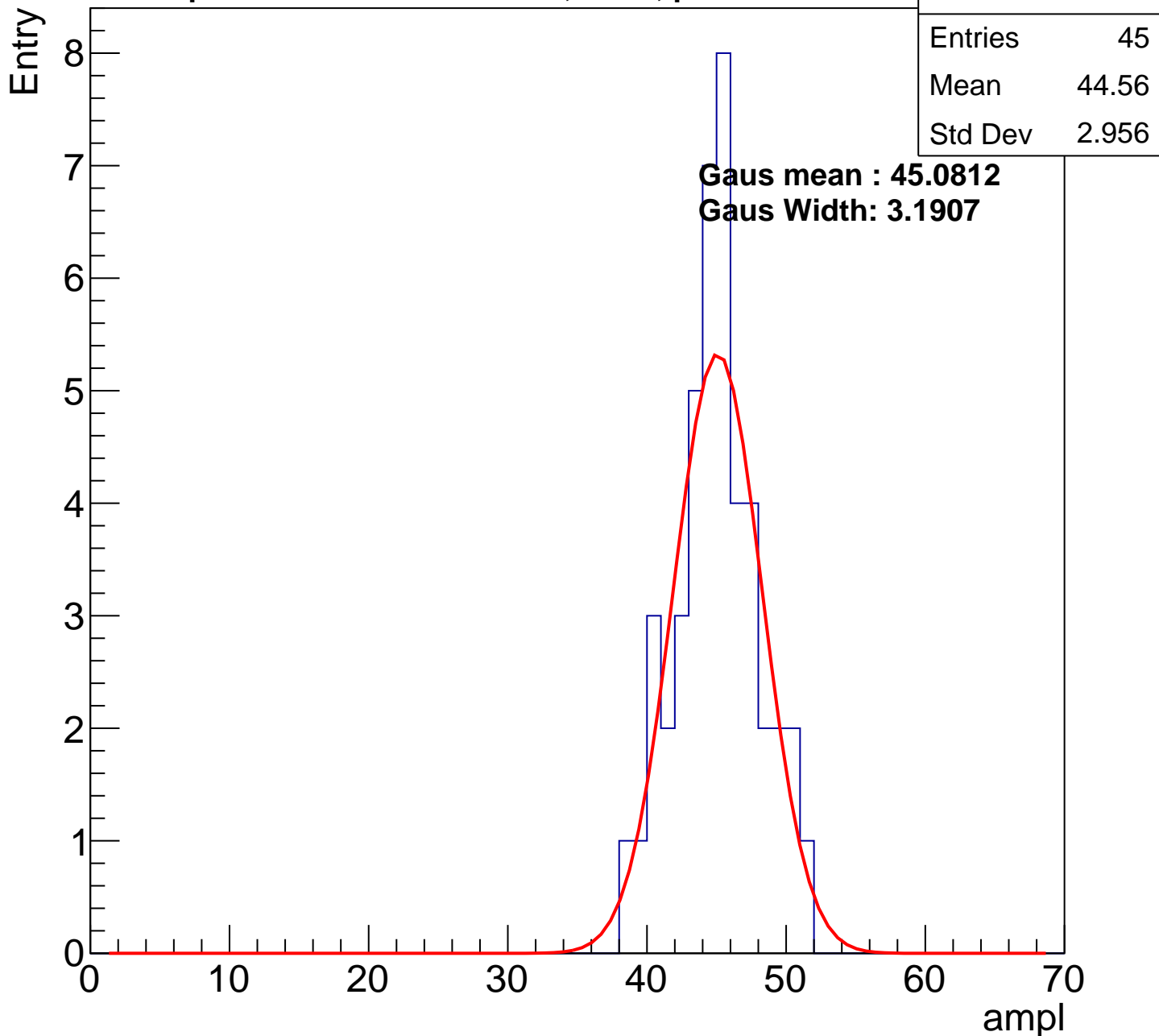
Gaus mean : 38.2302

Gaus Width: 4.0051



B0L002S, U26-ch1, adc2

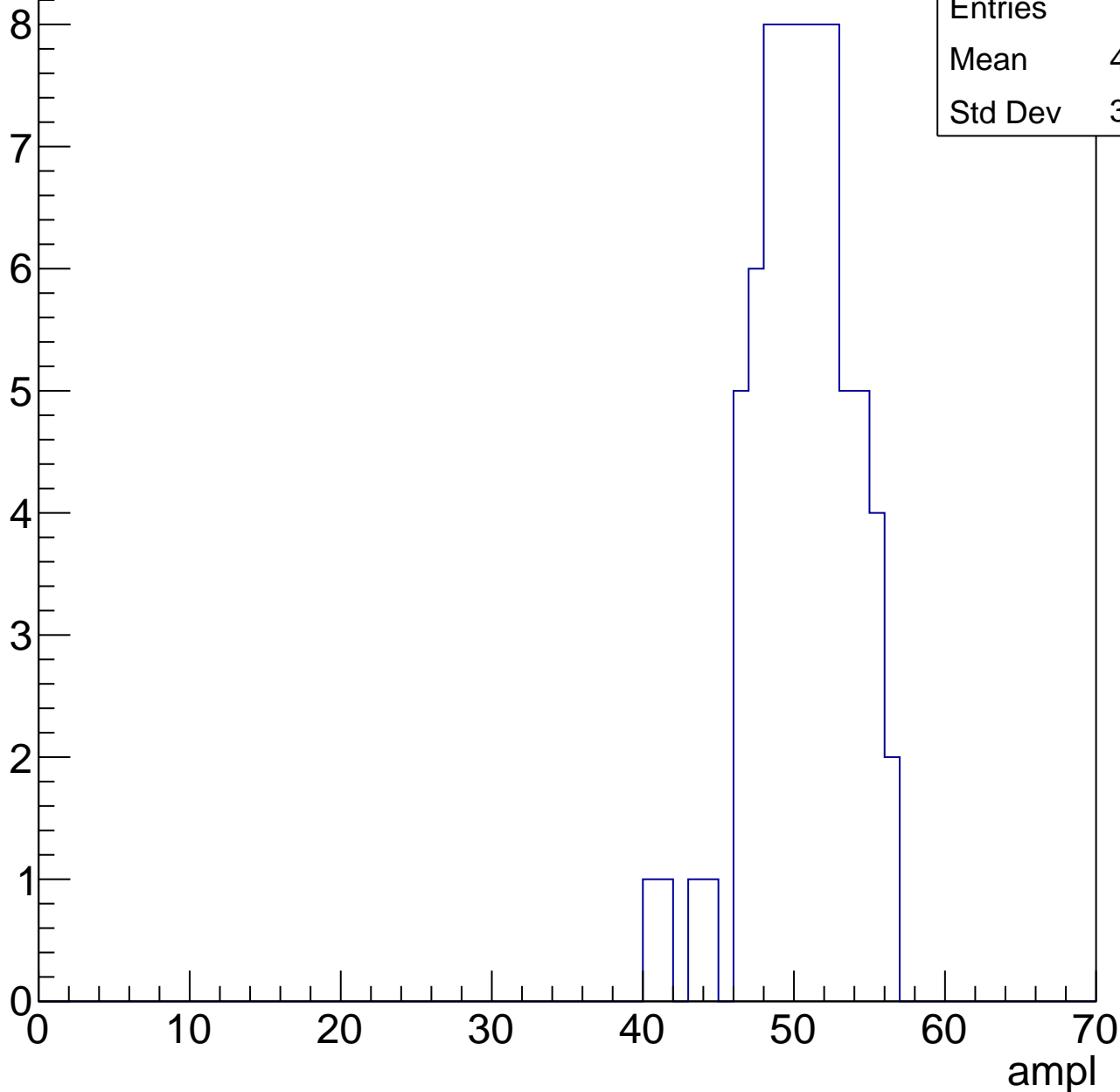
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U26-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

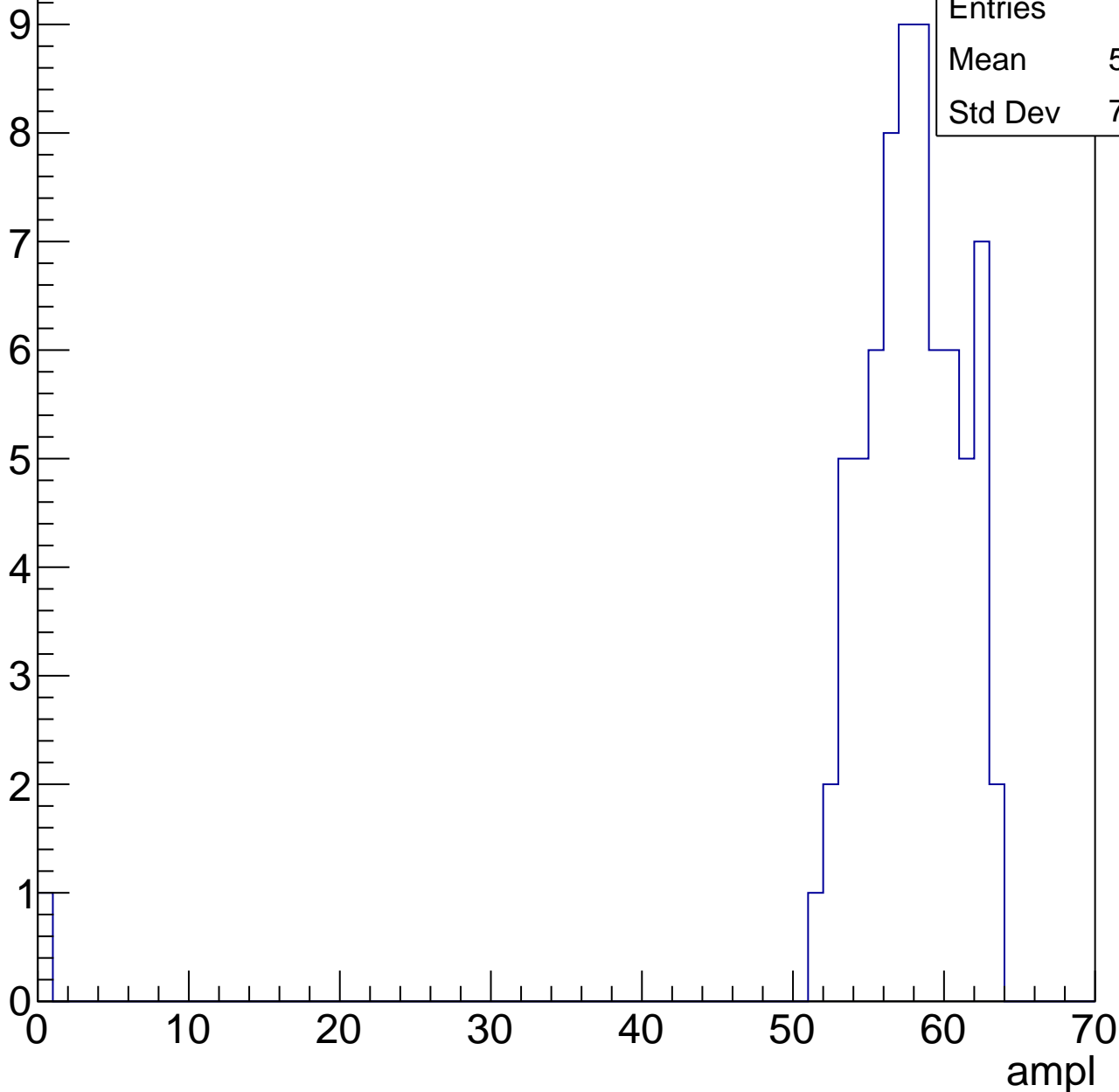


B0L002S, U26-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	56.69
Std Dev	7.359

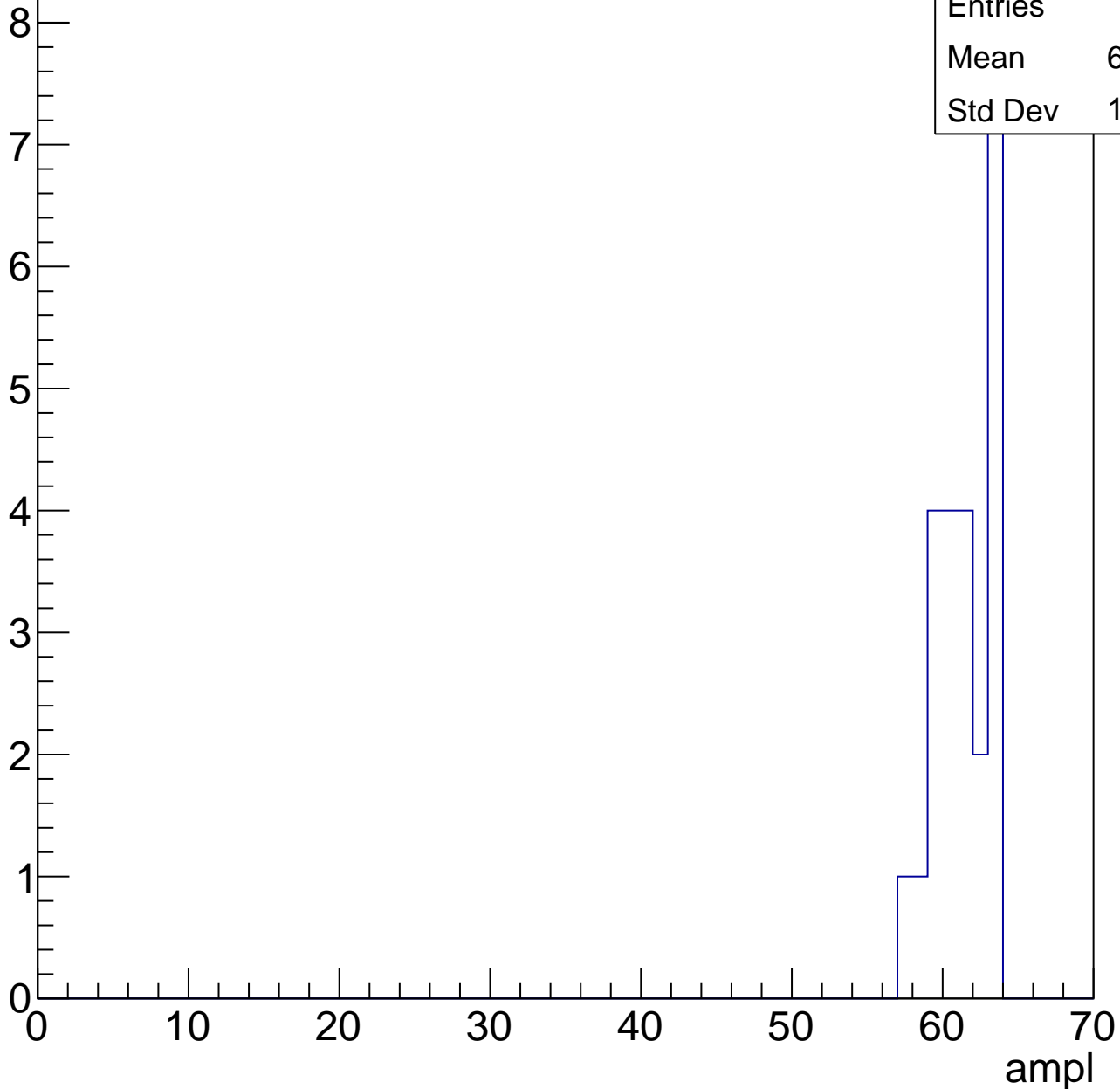


B0L002S, U26-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	60.96
Std Dev	1.814



B0L002S, U26-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch2, adc0

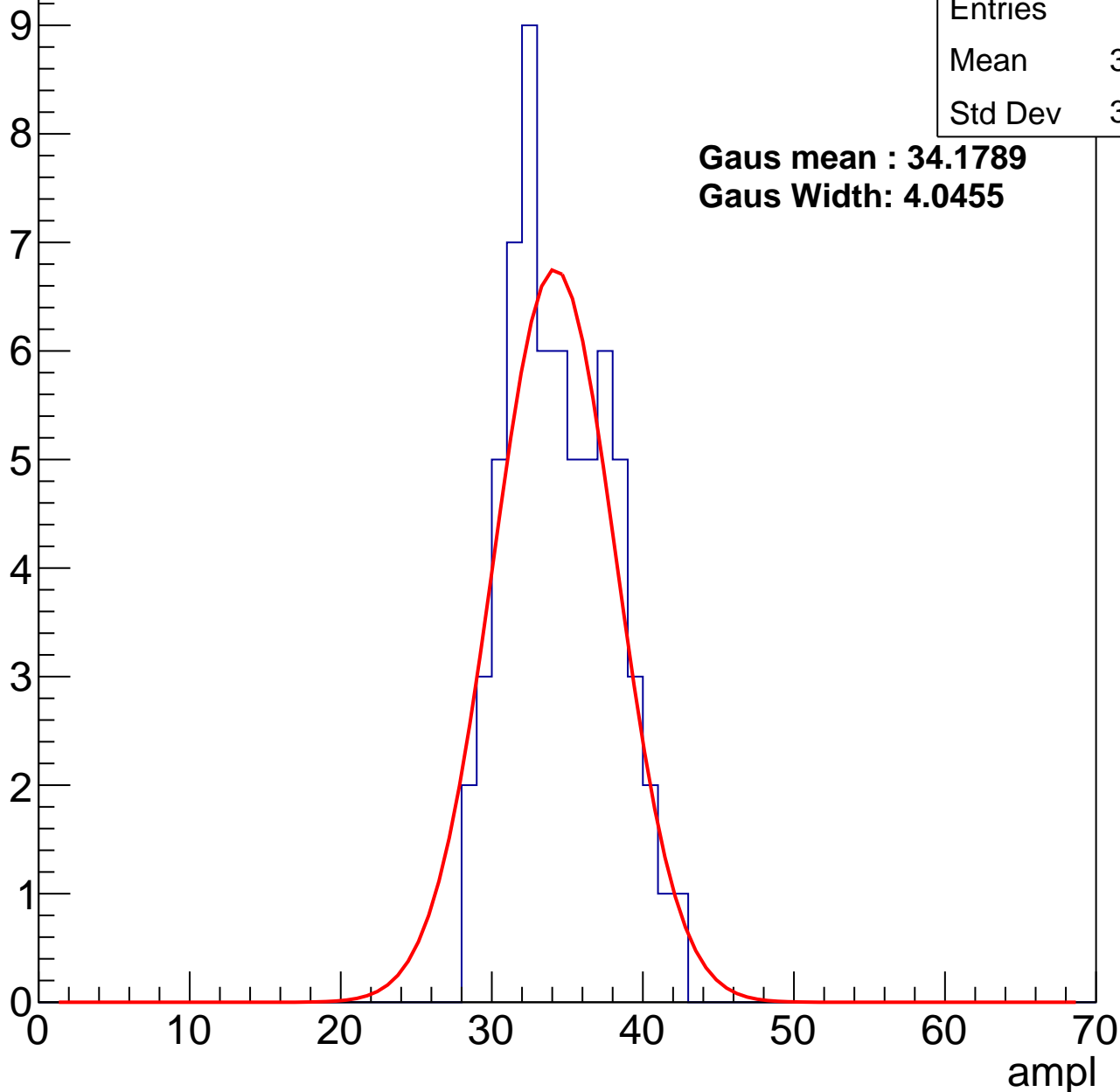
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	34.05
Std Dev	3.373

Gaus mean : 34.1789

Gaus Width: 4.0455



B0L002S, U26-ch2, adc1

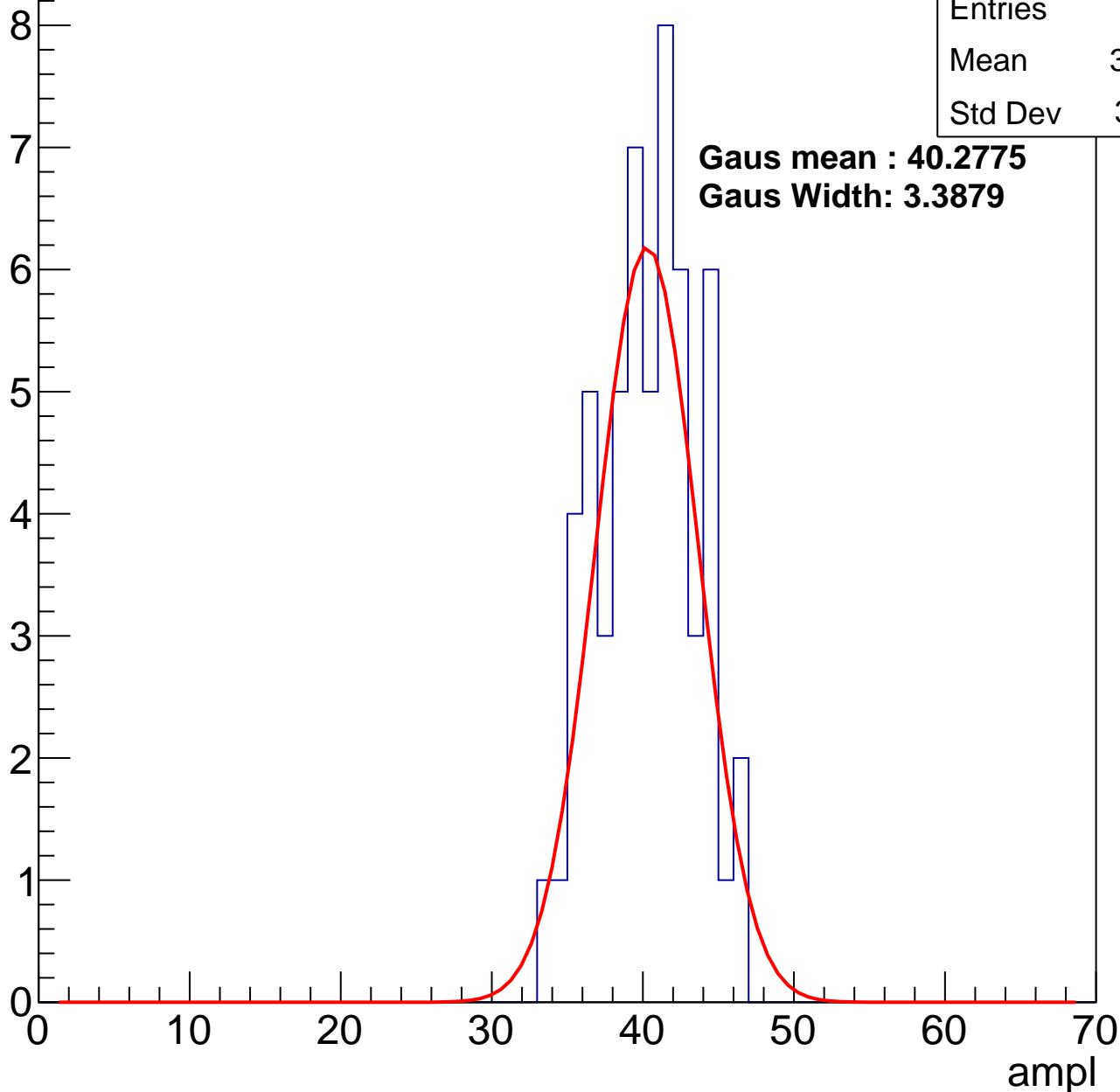
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	39.84
Std Dev	3.161

Gaus mean : 40.2775

Gaus Width: 3.3879



B0L002S, U26-ch2, adc2

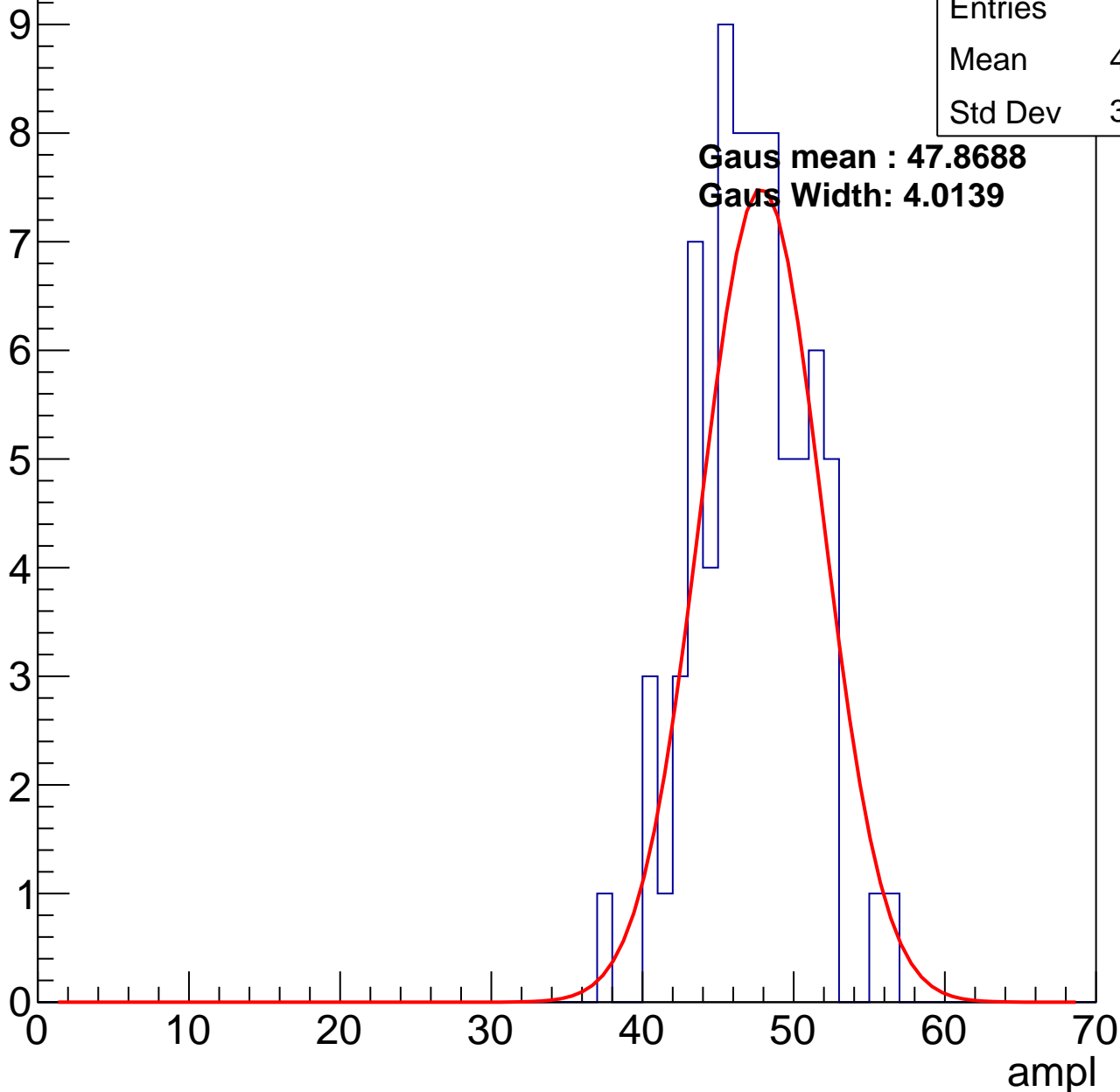
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	46.75
Std Dev	3.622

Gaus mean : 47.8688

Gaus Width: 4.0139



B0L002S, U26-ch2, adc3

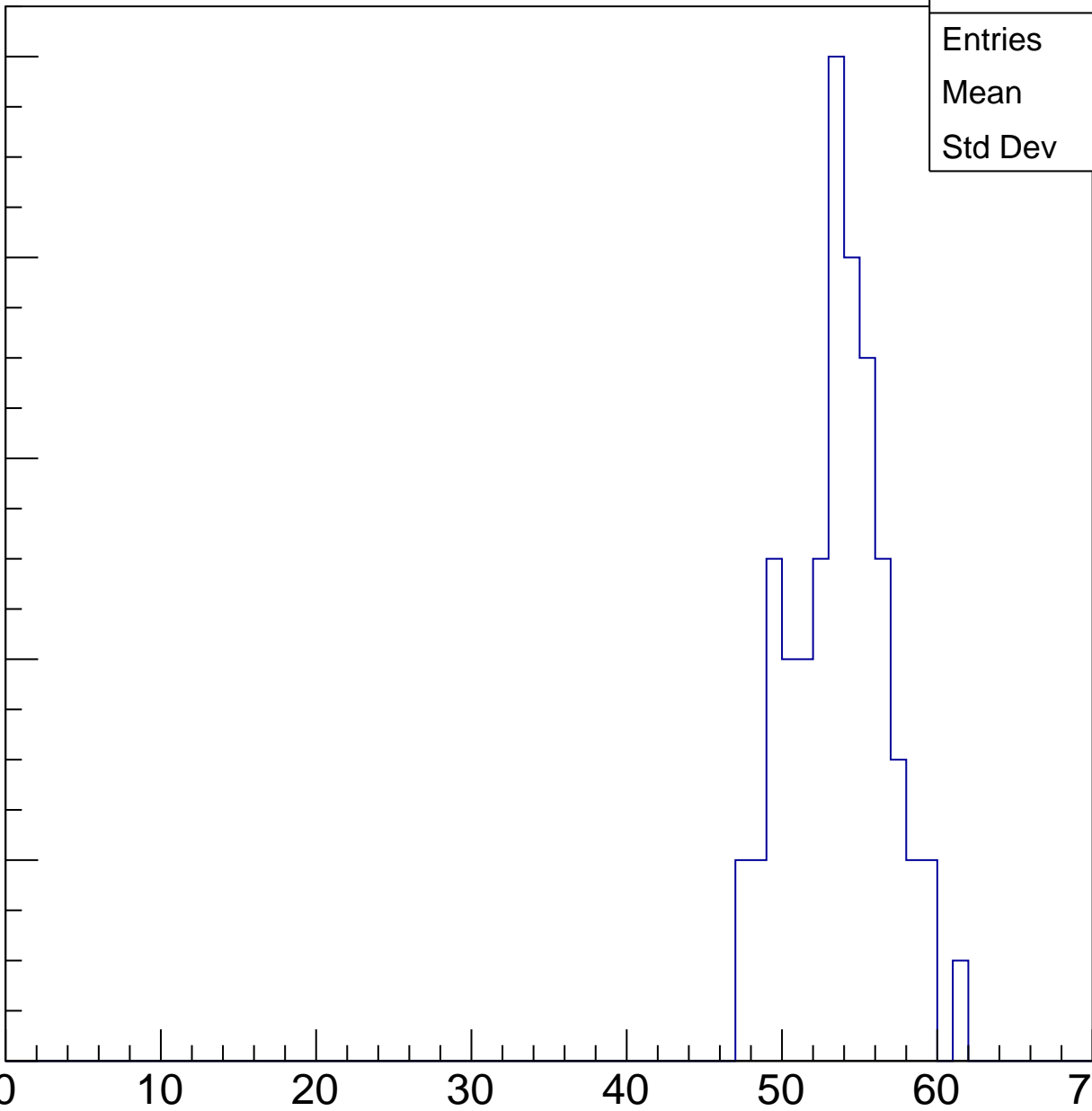
calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	53.2
Std Dev	3.087

Entry

10
8
6
4
2
0

ampl

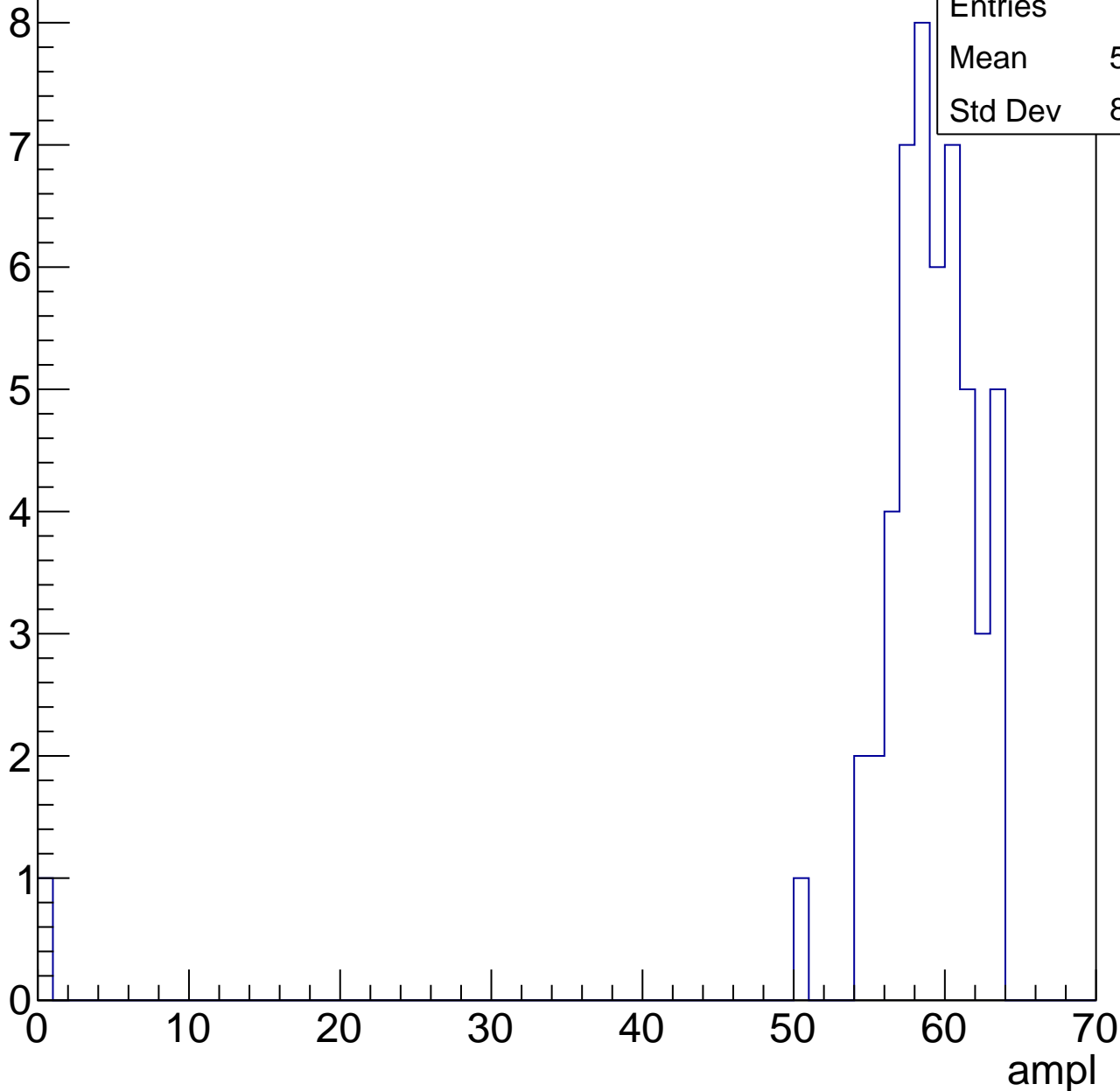


B0L002S, U26-ch2, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57.55
Std Dev	8.566

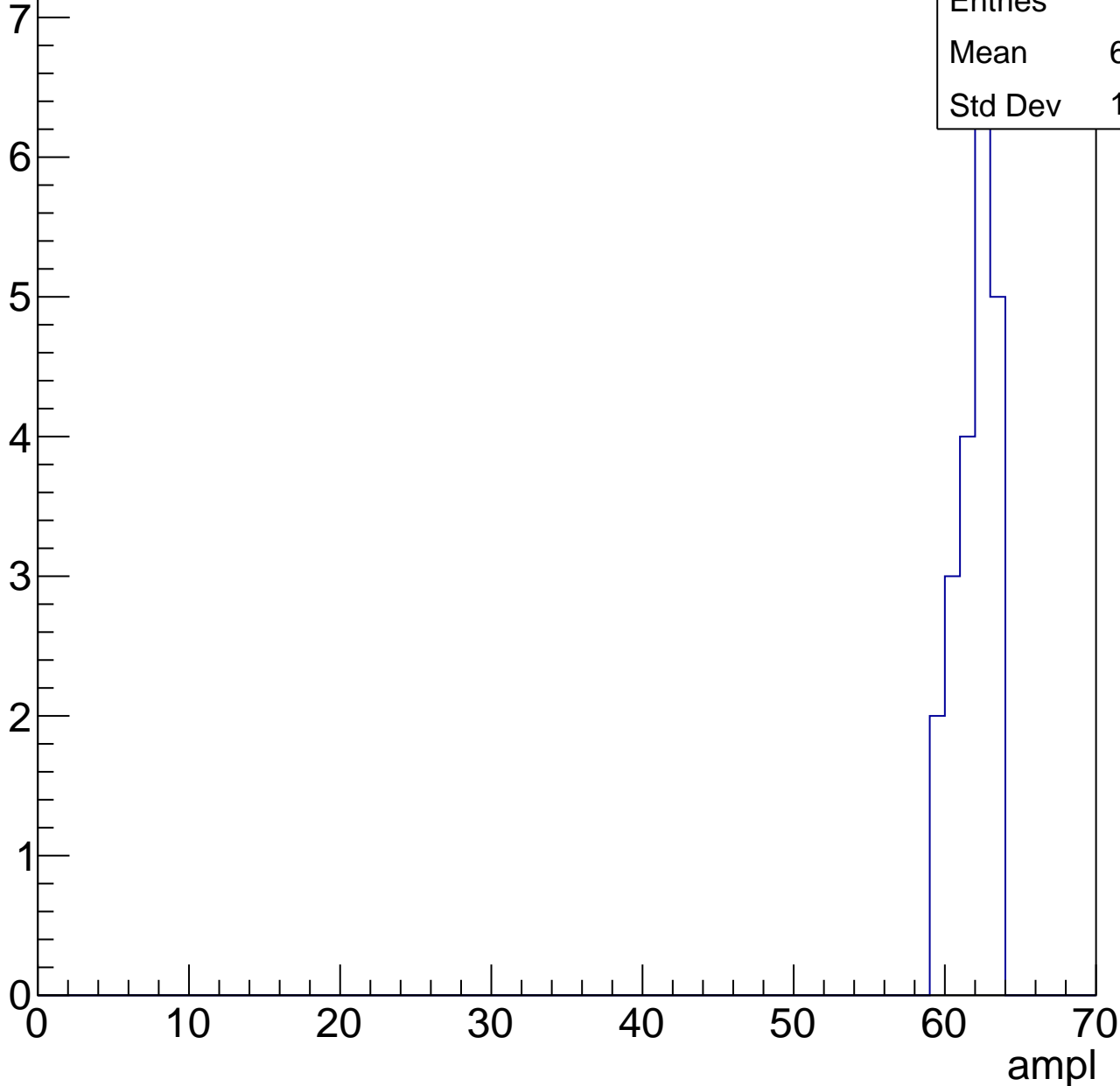


B0L002S, U26-ch2, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	61.48
Std Dev	1.258



B0L002S, U26-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch3, adc0

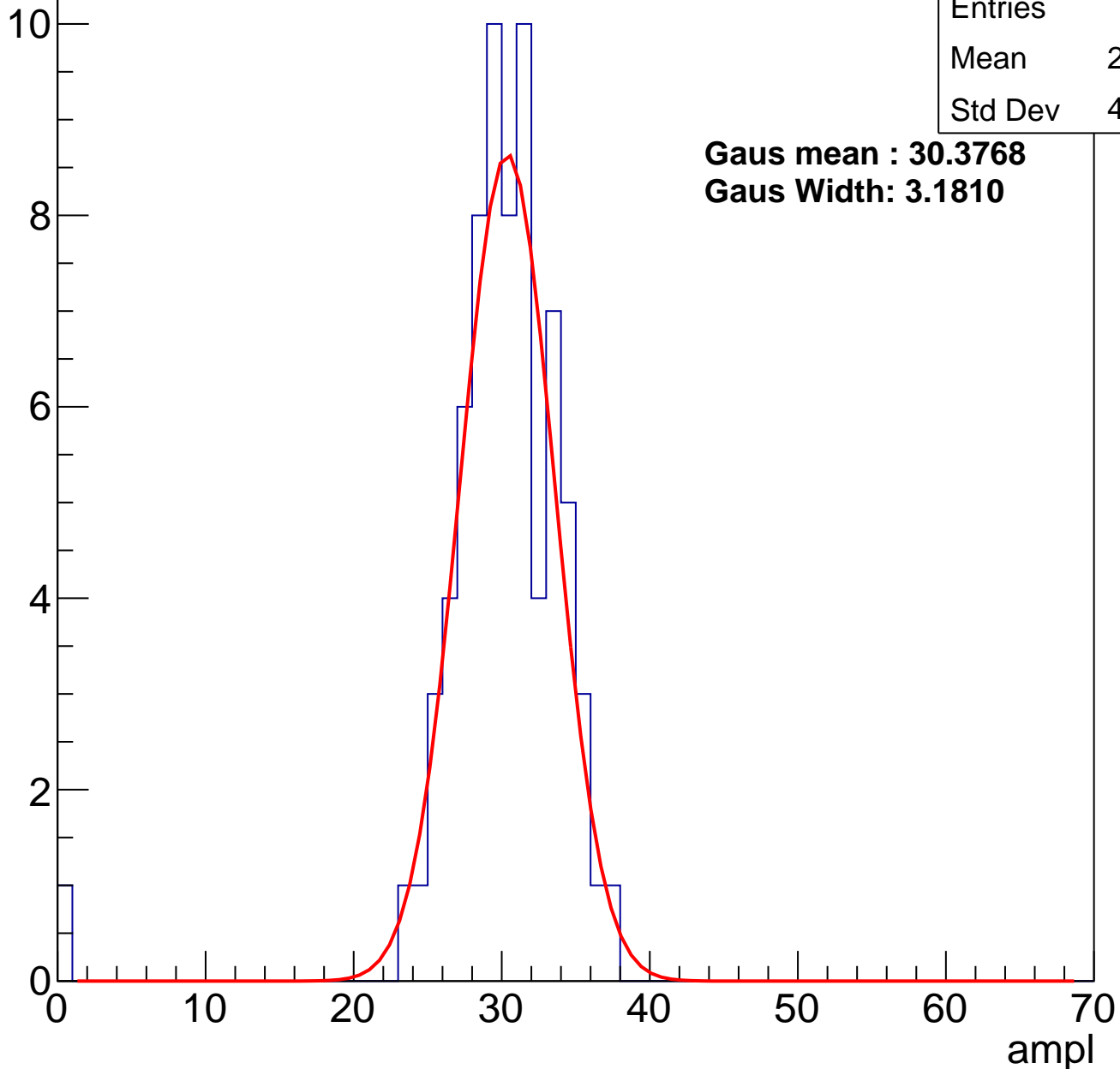
calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	29.58
Std Dev	4.587

Gaus mean : 30.3768

Gaus Width: 3.1810

Entry



B0L002S, U26-ch3, adc1

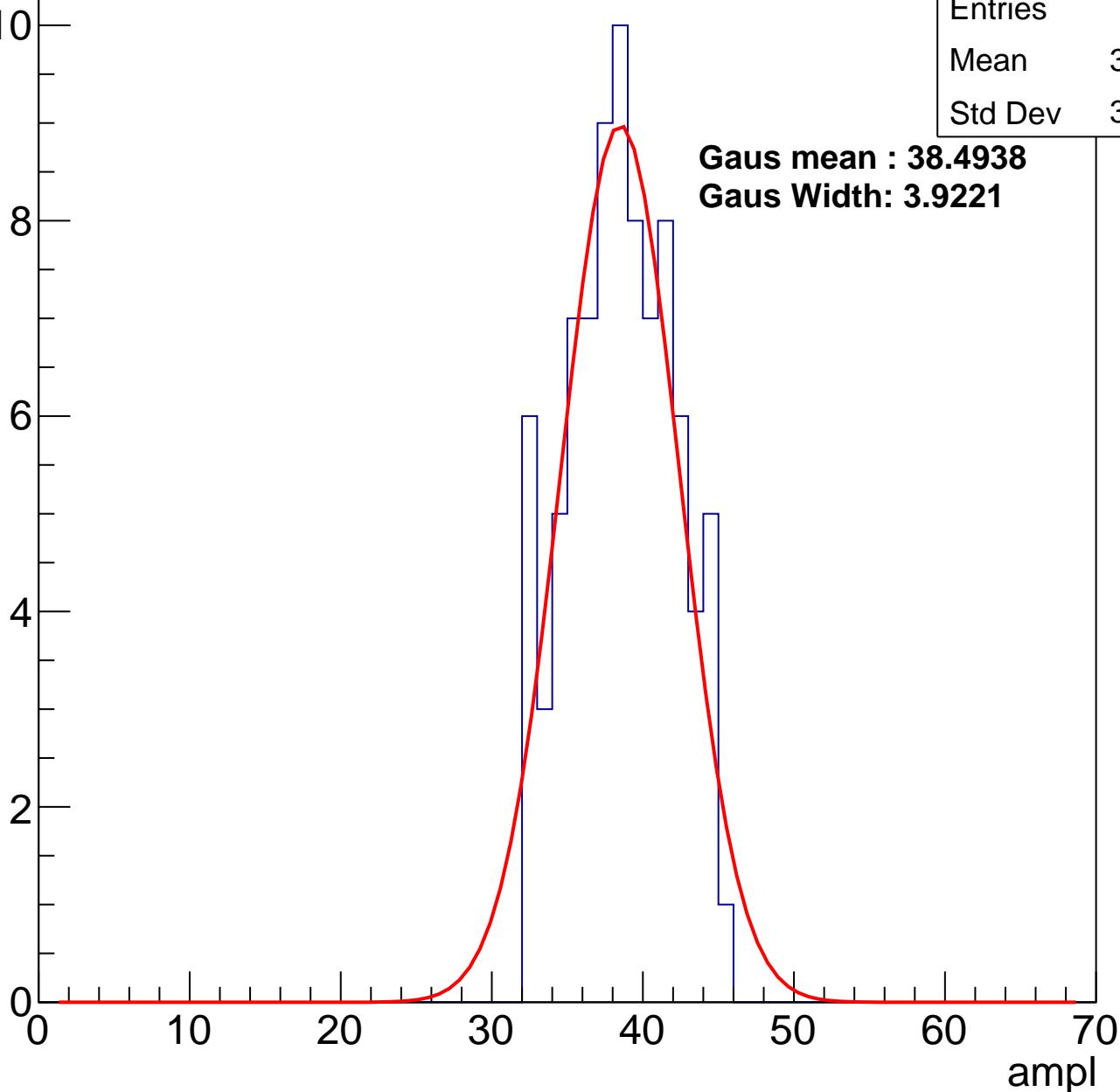
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	38.14
Std Dev	3.414

Gaus mean : 38.4938

Gaus Width: 3.9221



B0L002S, U26-ch3, adc2

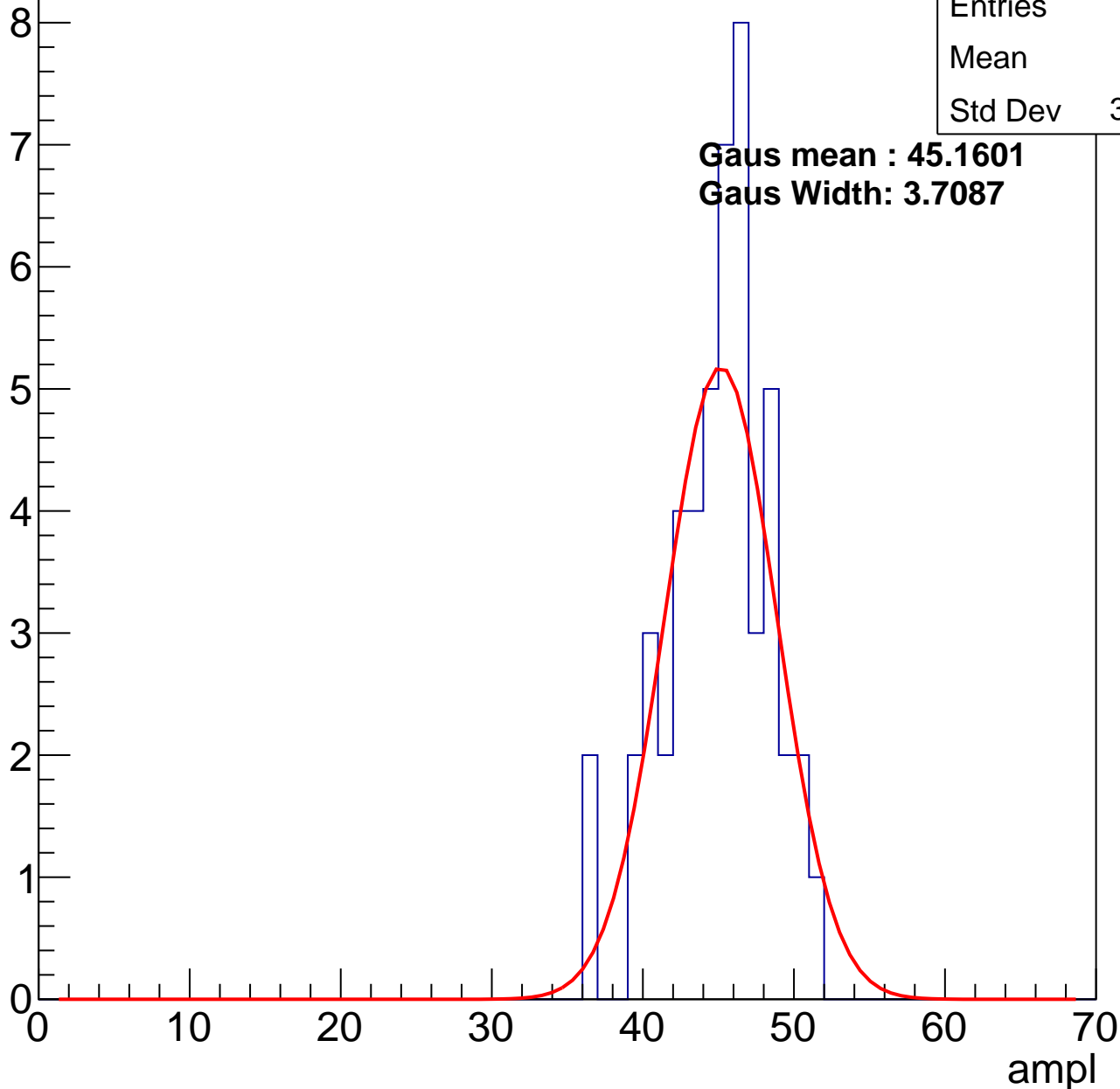
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	44.5
Std Dev	3.372

Gaus mean : 45.1601

Gaus Width: 3.7087



B0L002S, U26-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	51.05
Std Dev	3.266

Entry

10

8

6

4

2

0

0

10

20

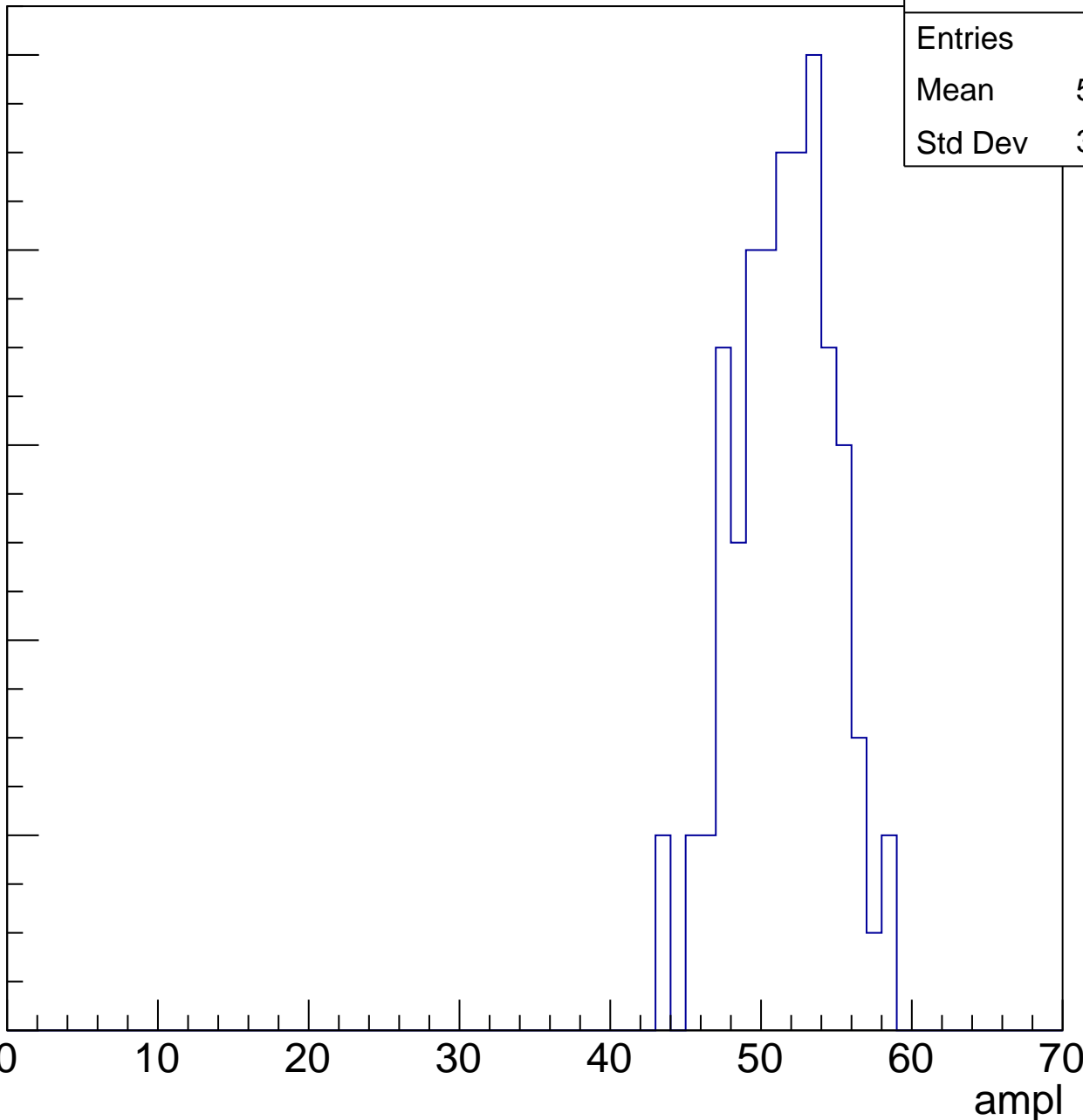
30

40

50

60

ampl

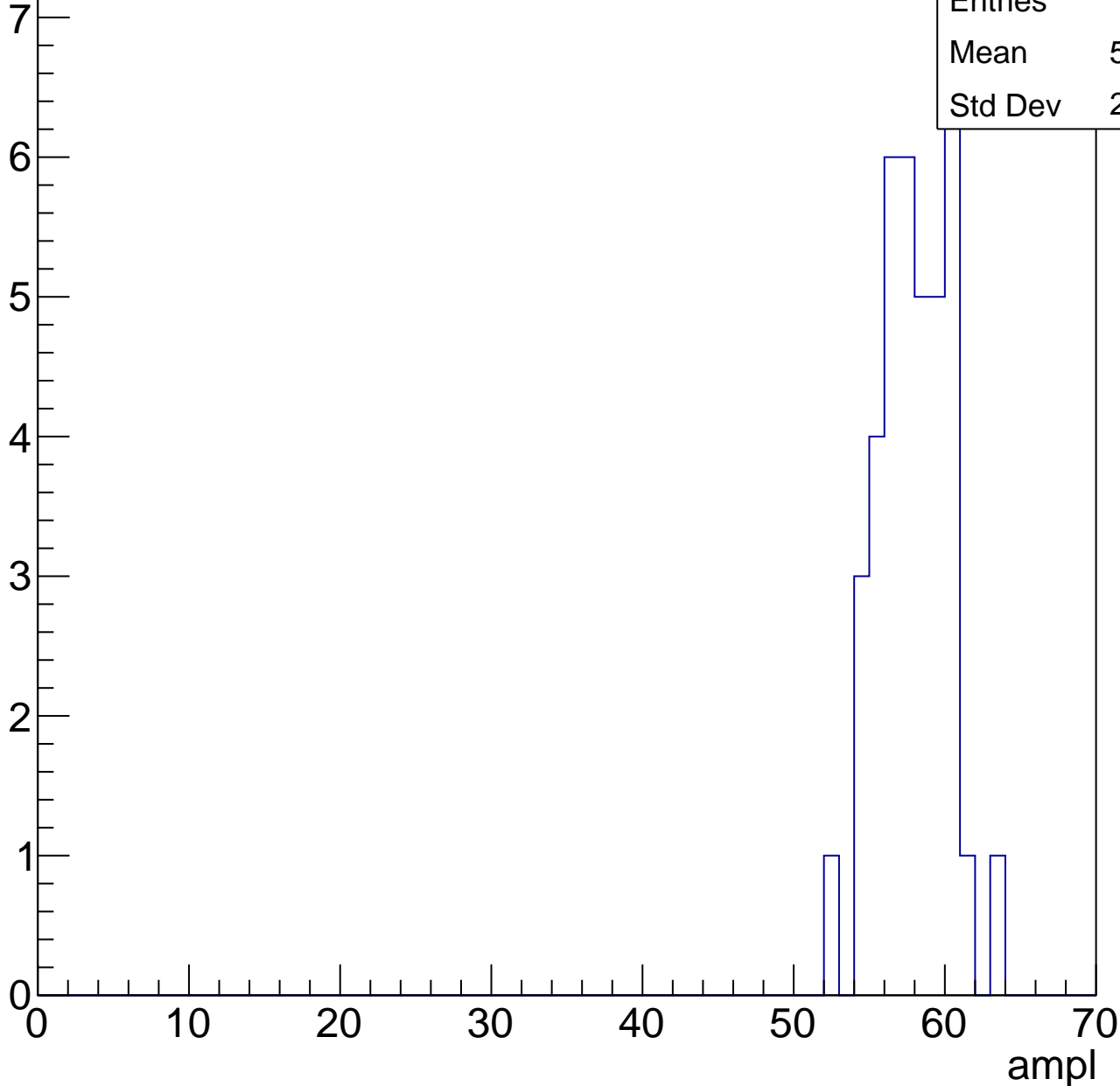


B0L002S, U26-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	57.46
Std Dev	2.297



B0L002S, U26-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries 43

Mean 59.33

Std Dev 9.363

8

6

4

2

0

0

10

20

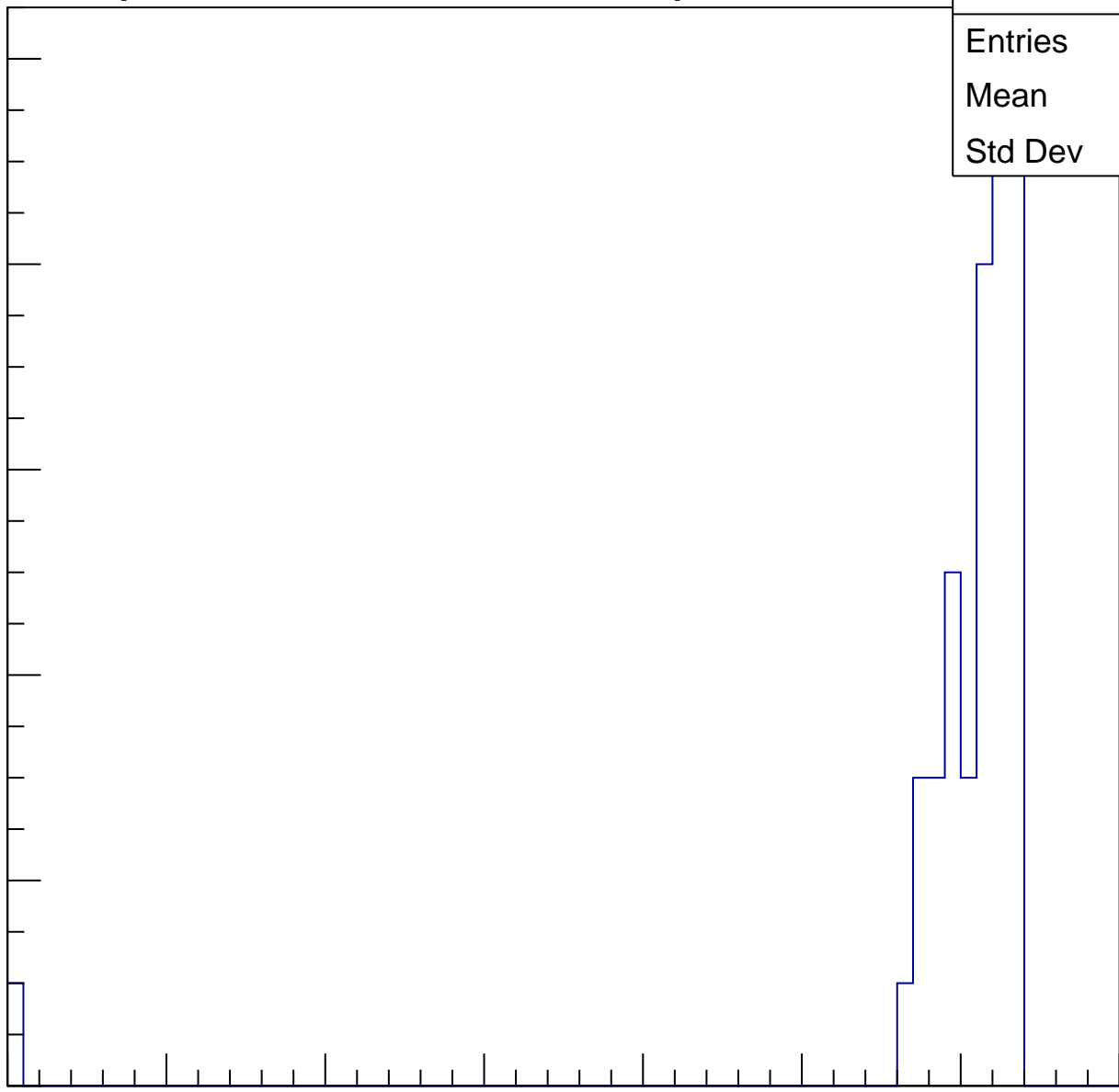
30

40

50

60

ampl



B0L002S, U26-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch4, adc0

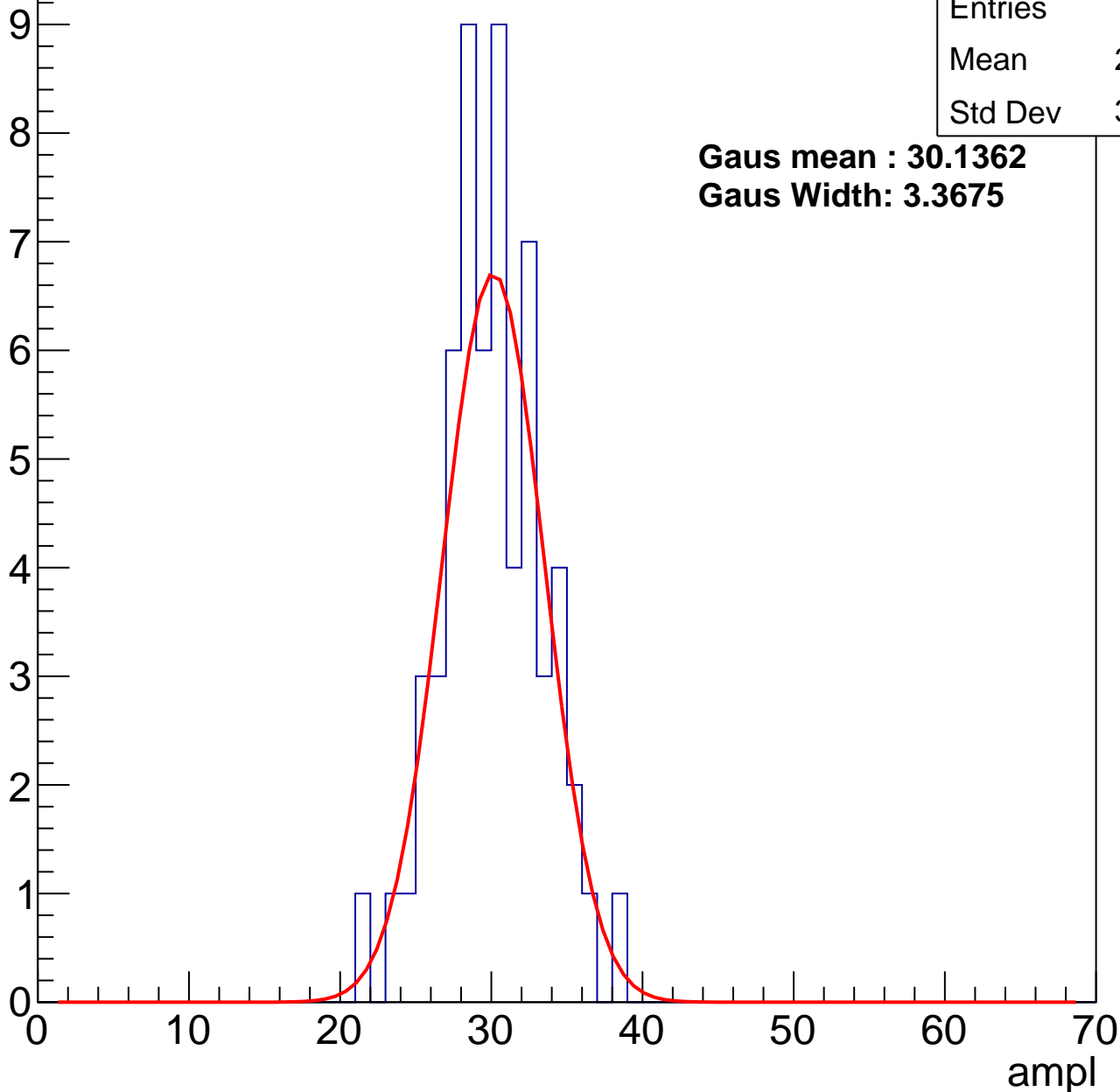
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.61
Std Dev	3.261

Gaus mean : 30.1362

Gaus Width: 3.3675



B0L002S, U26-ch4, adc1

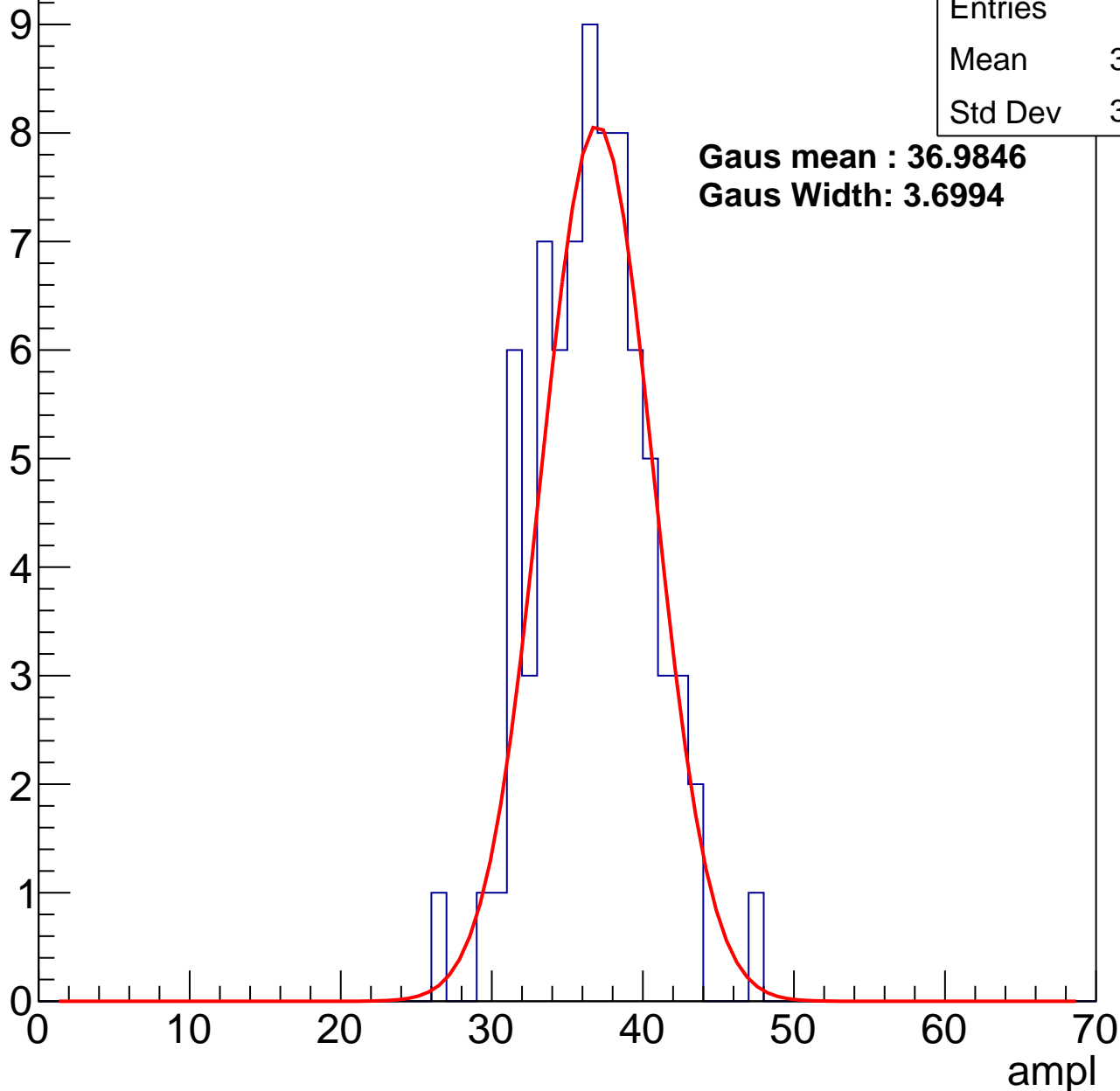
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.19
Std Dev	3.693

Gaus mean : 36.9846

Gaus Width: 3.6994



B0L002S, U26-ch4, adc2

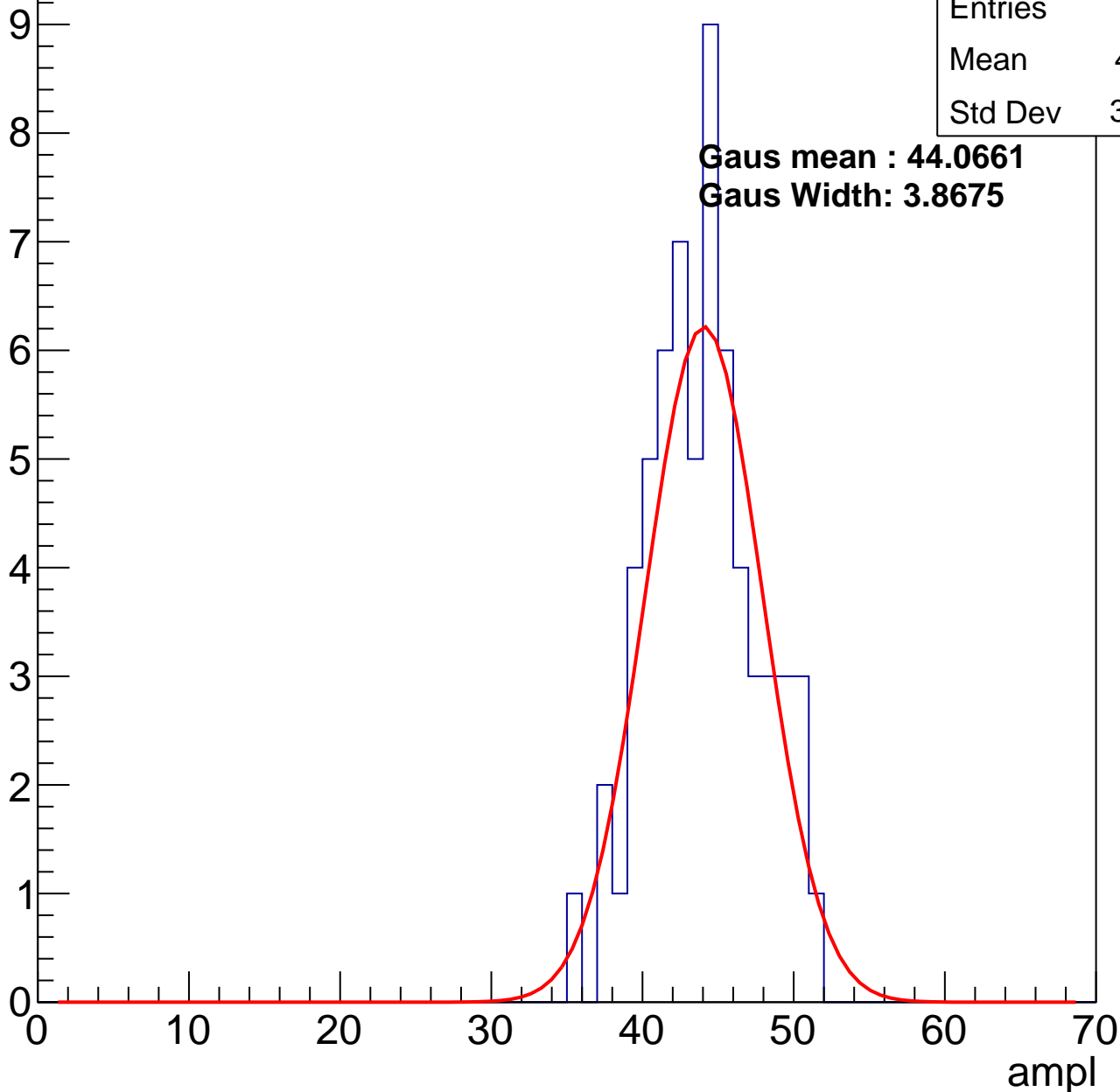
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.51
Std Dev	3.554

Gaus mean : 44.0661

Gaus Width: 3.8675

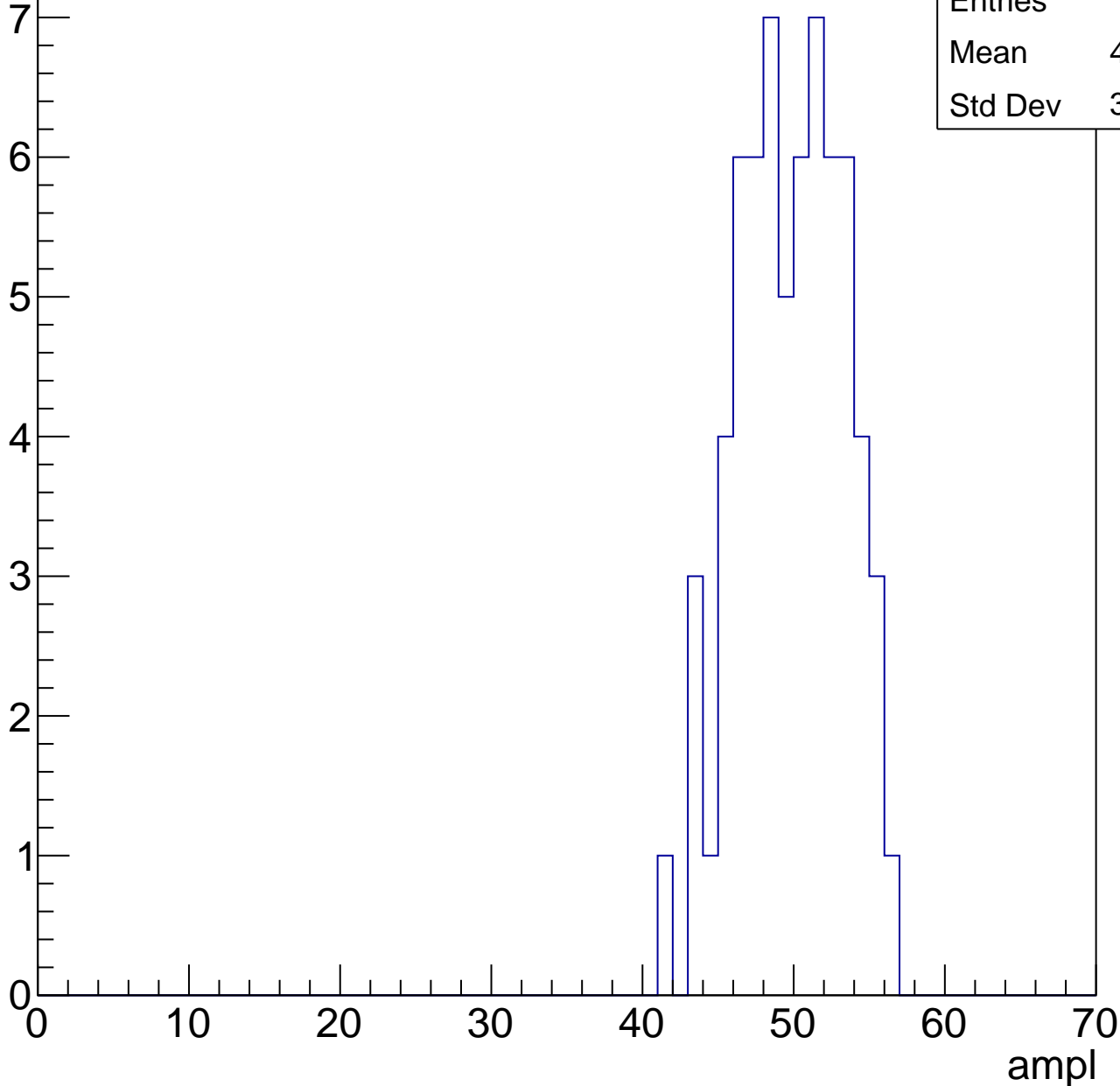


B0L002S, U26-ch4, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	49.35
Std Dev	3.436



B0L002S, U26-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

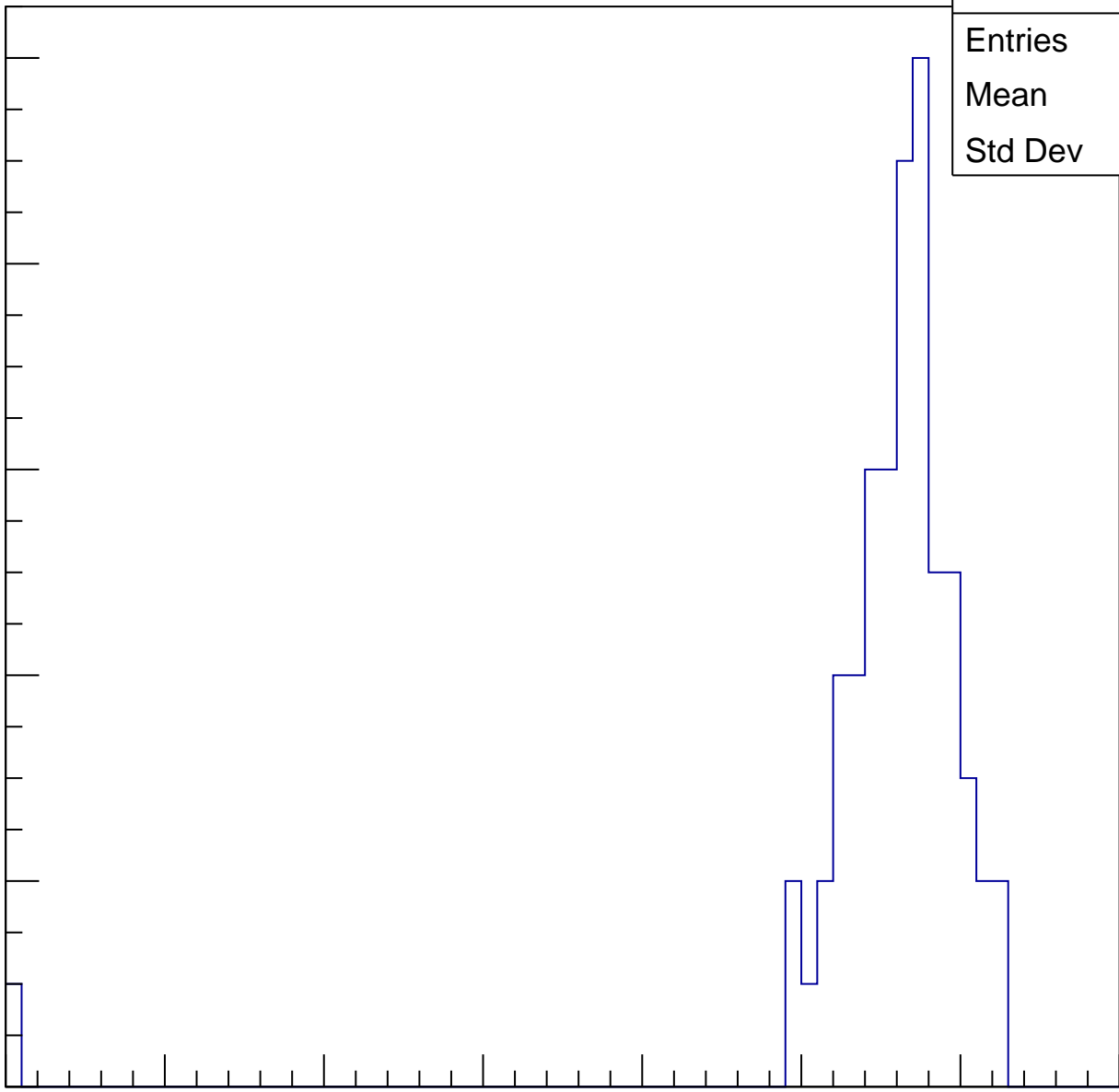
Entries	62
Mean	54.98
Std Dev	7.653

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

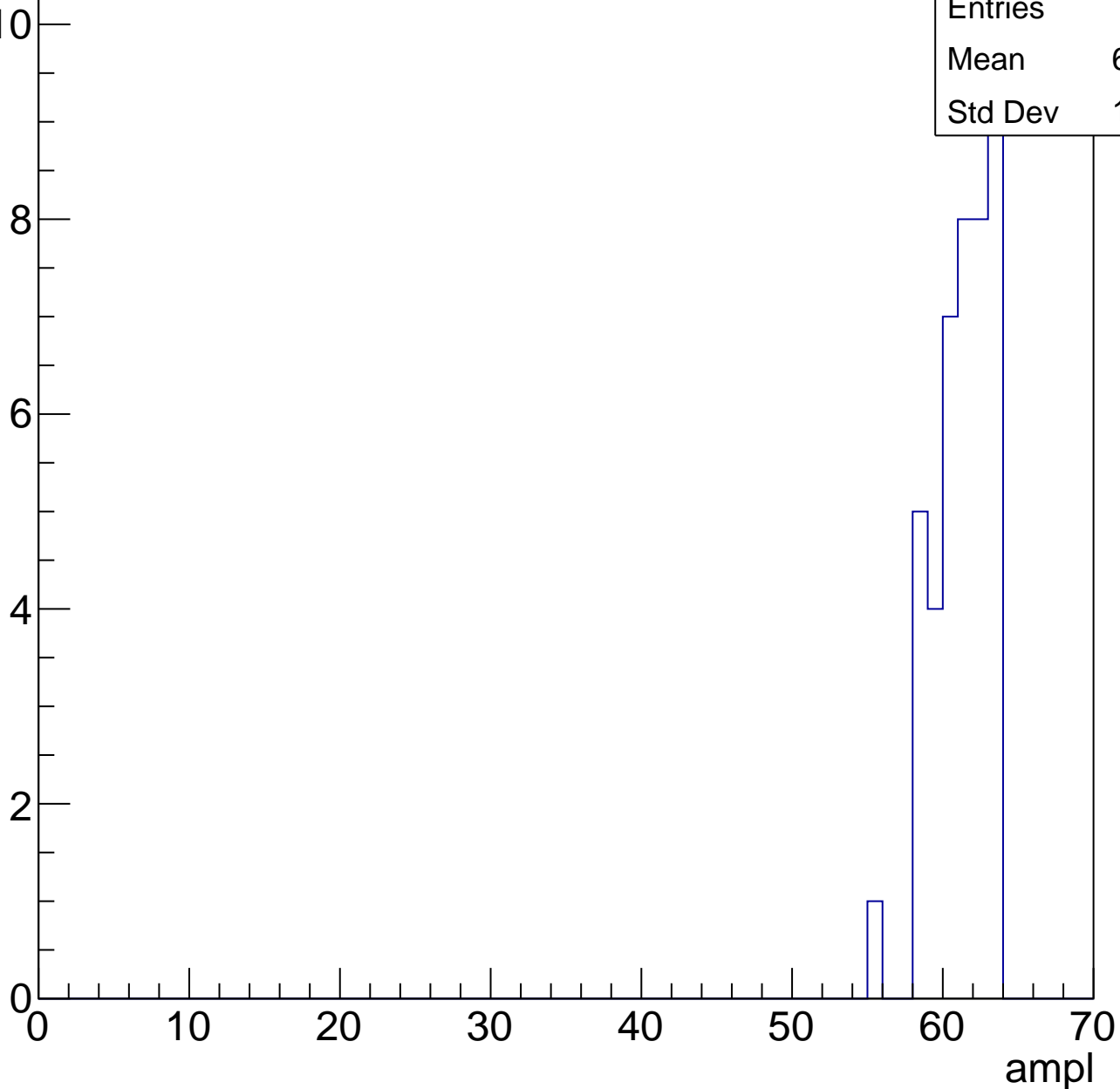


B0L002S, U26-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	60.81
Std Dev	1.871



B0L002S, U26-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch5, adc0

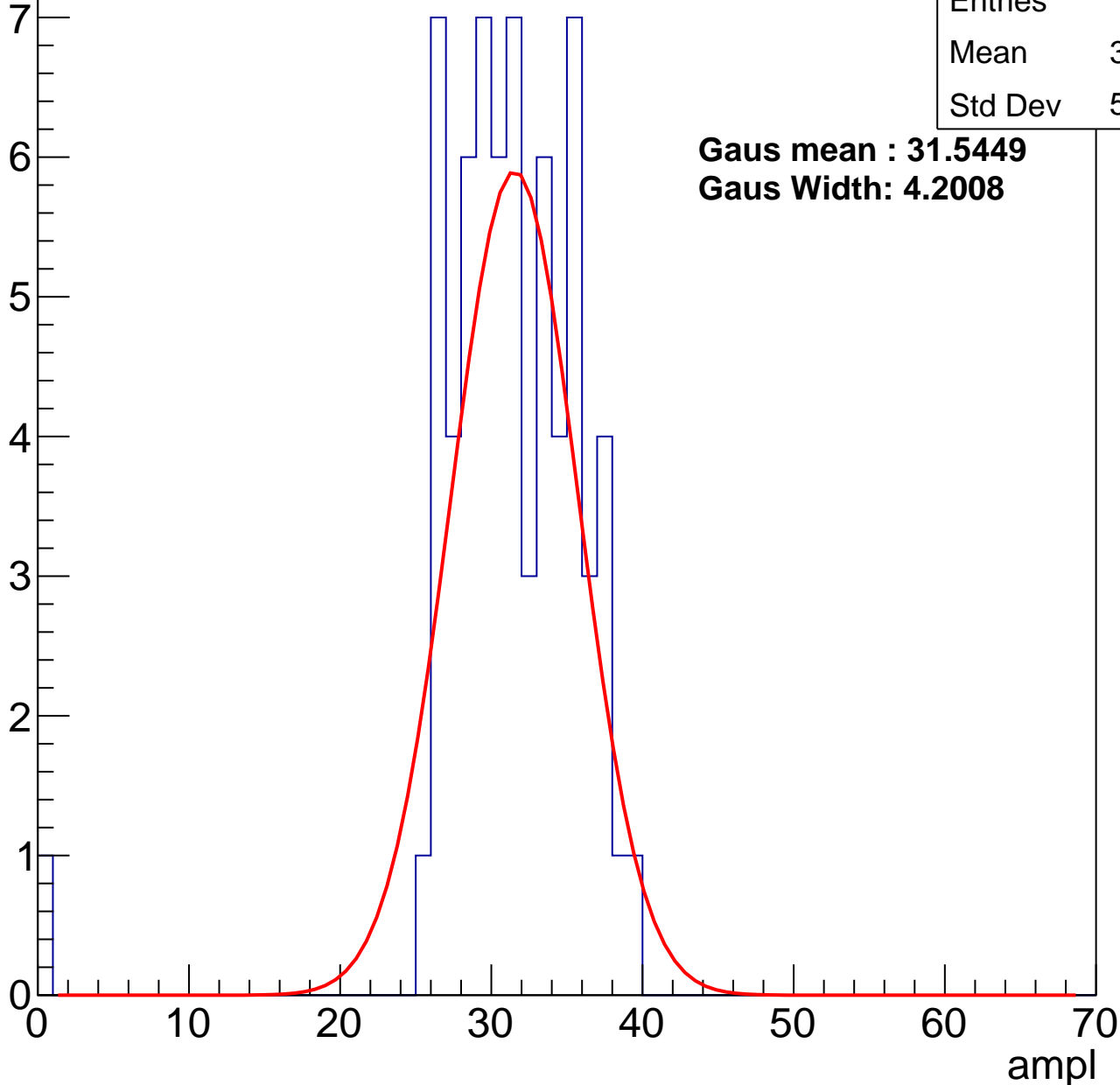
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.75
Std Dev	5.183

Gaus mean : 31.5449

Gaus Width: 4.2008



B0L002S, U26-ch5, adc1

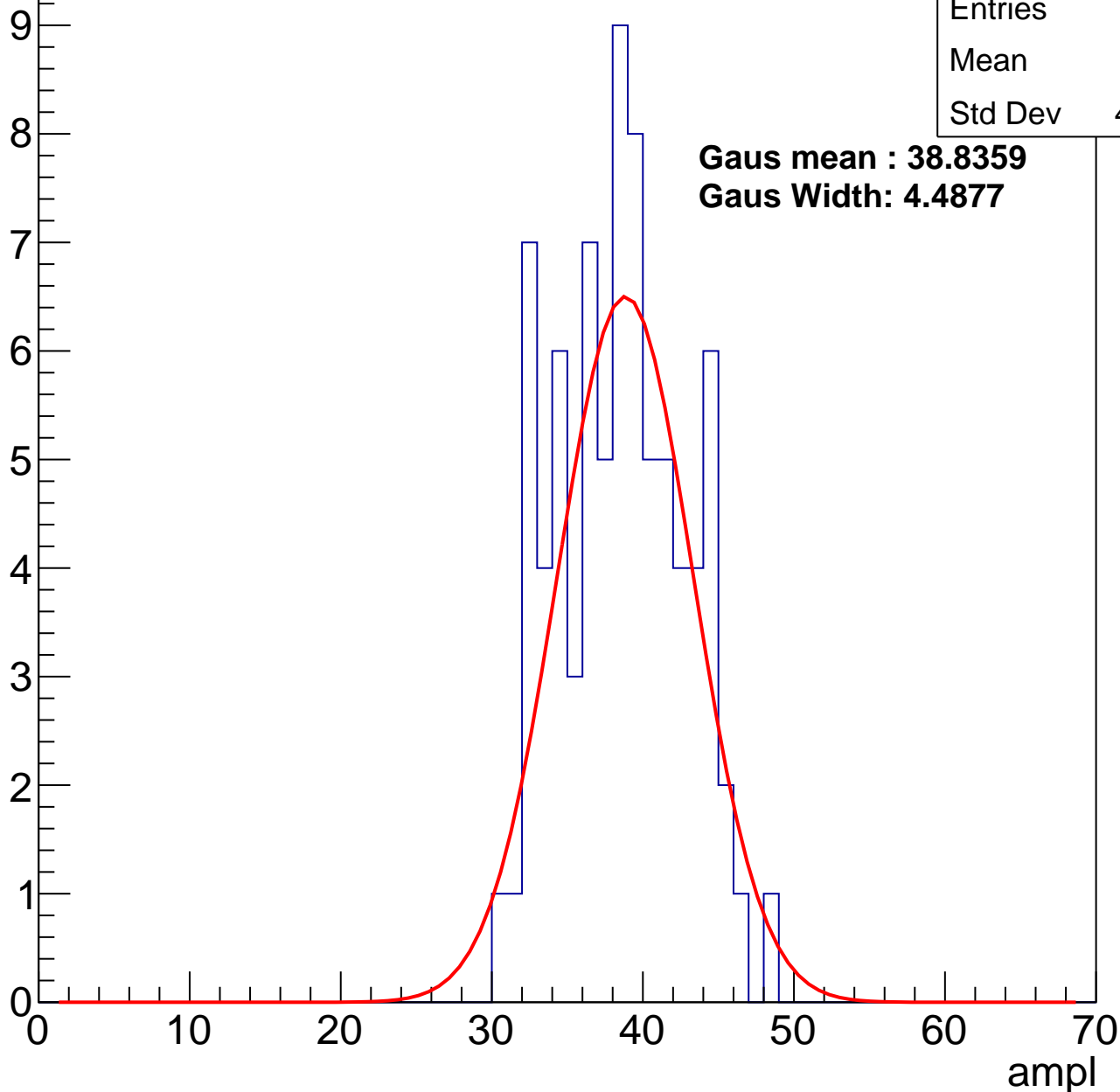
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	38.1
Std Dev	4.111

Gaus mean : 38.8359

Gaus Width: 4.4877



B0L002S, U26-ch5, adc2

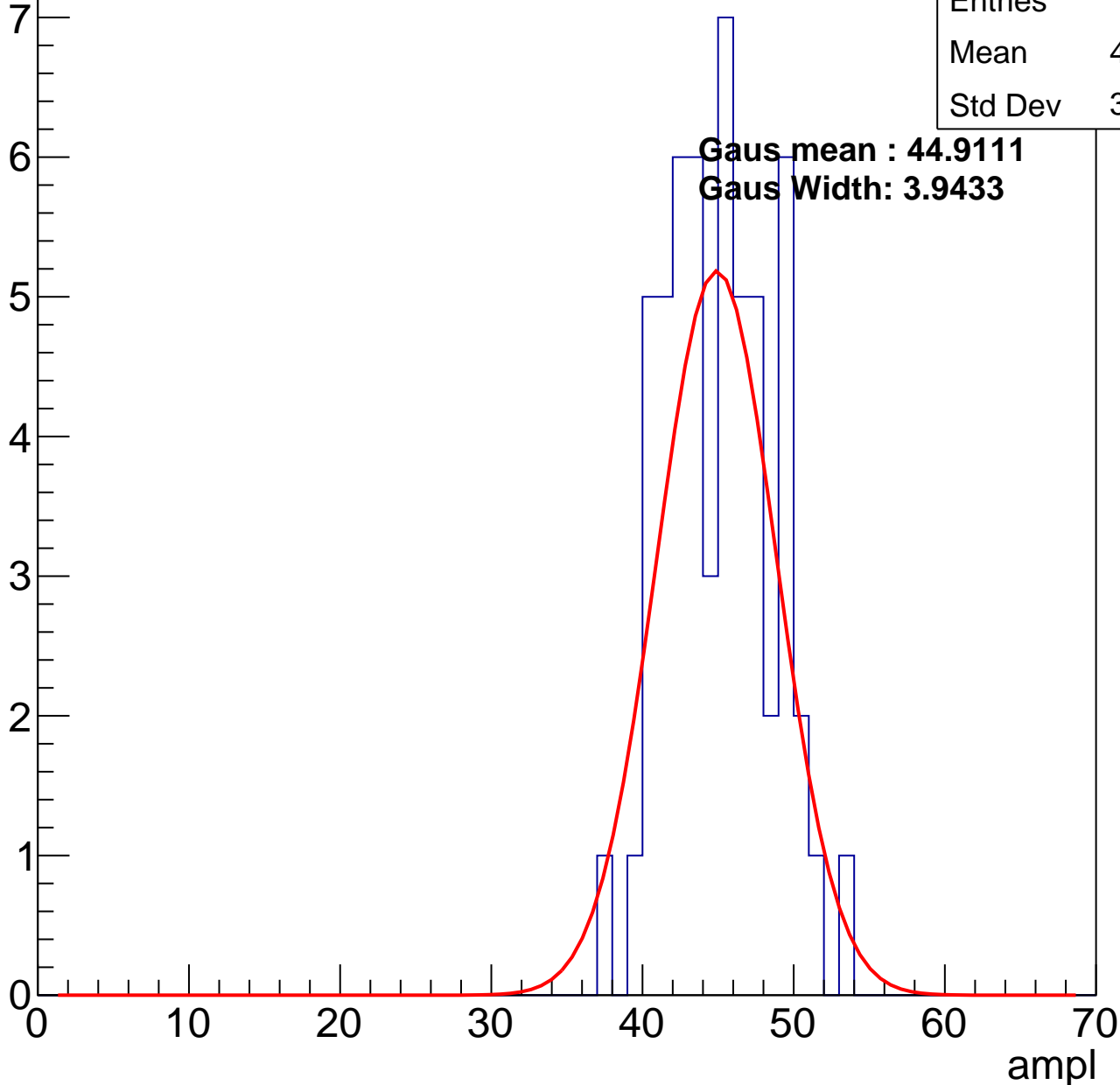
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	44.59
Std Dev	3.447

Gaus mean : 44.9111

Gaus Width: 3.9433

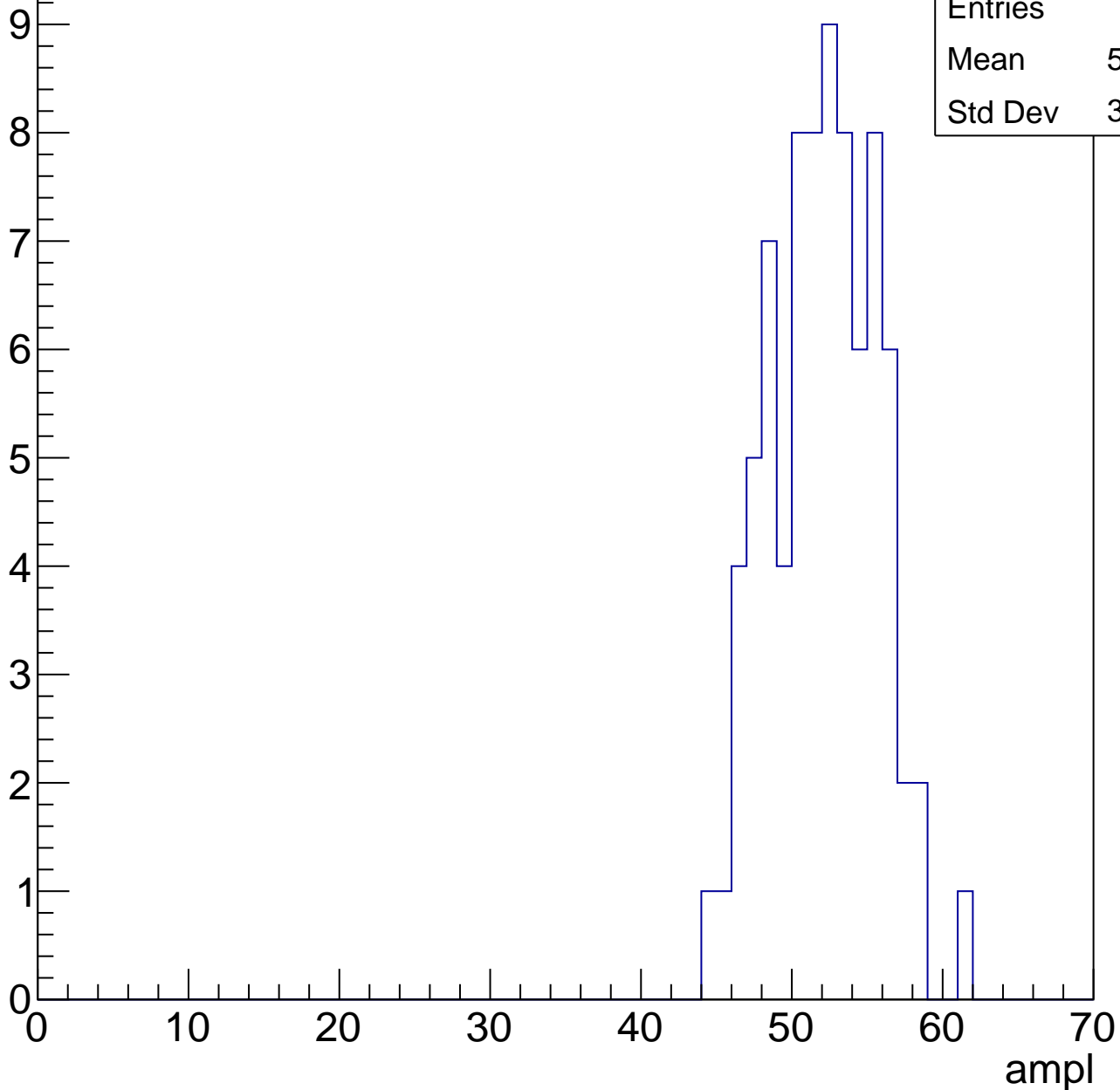


B0L002S, U26-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	51.64
Std Dev	3.469



B0L002S, U26-ch5, adc4

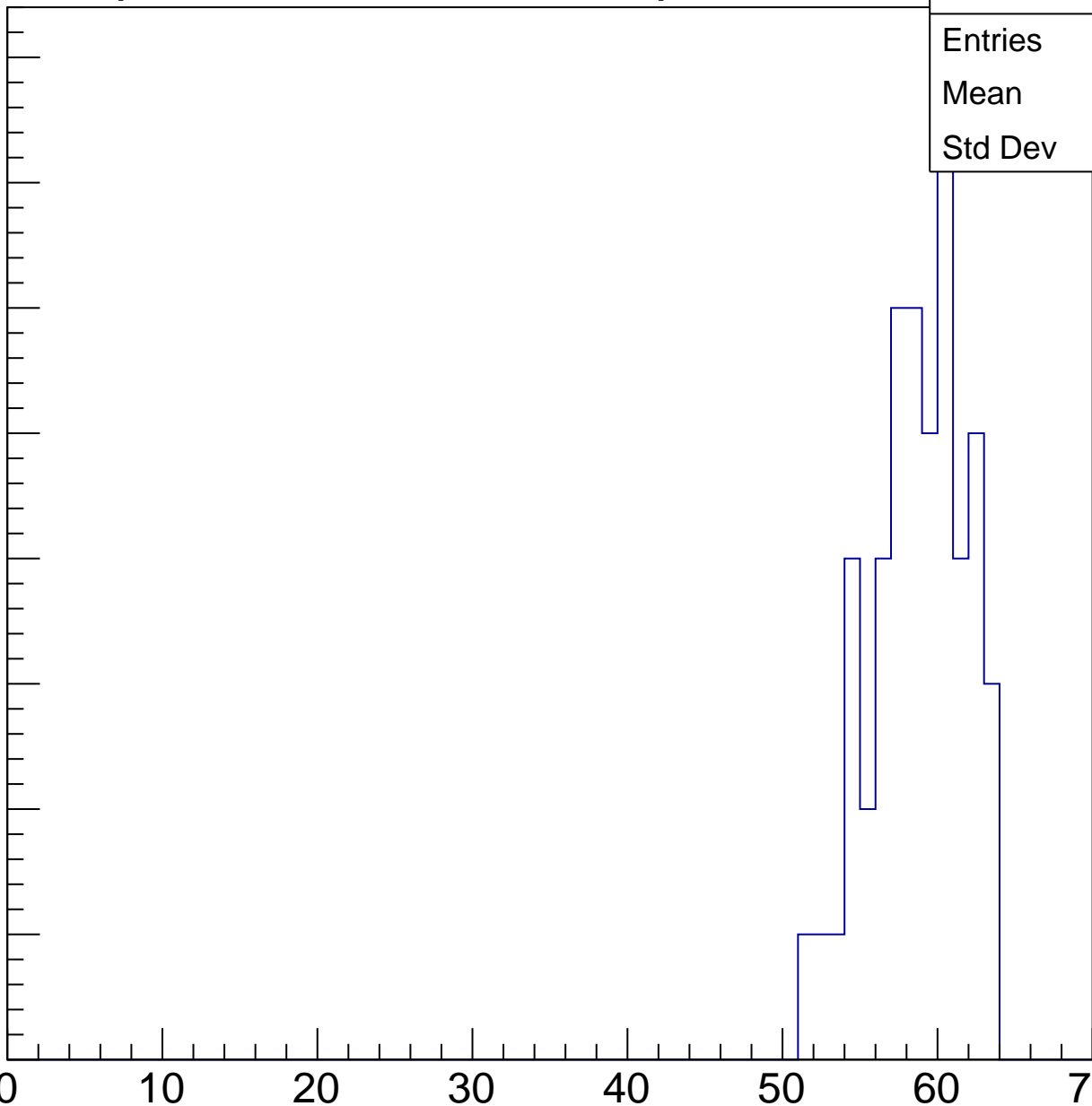
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.28
Std Dev	2.96

ampl

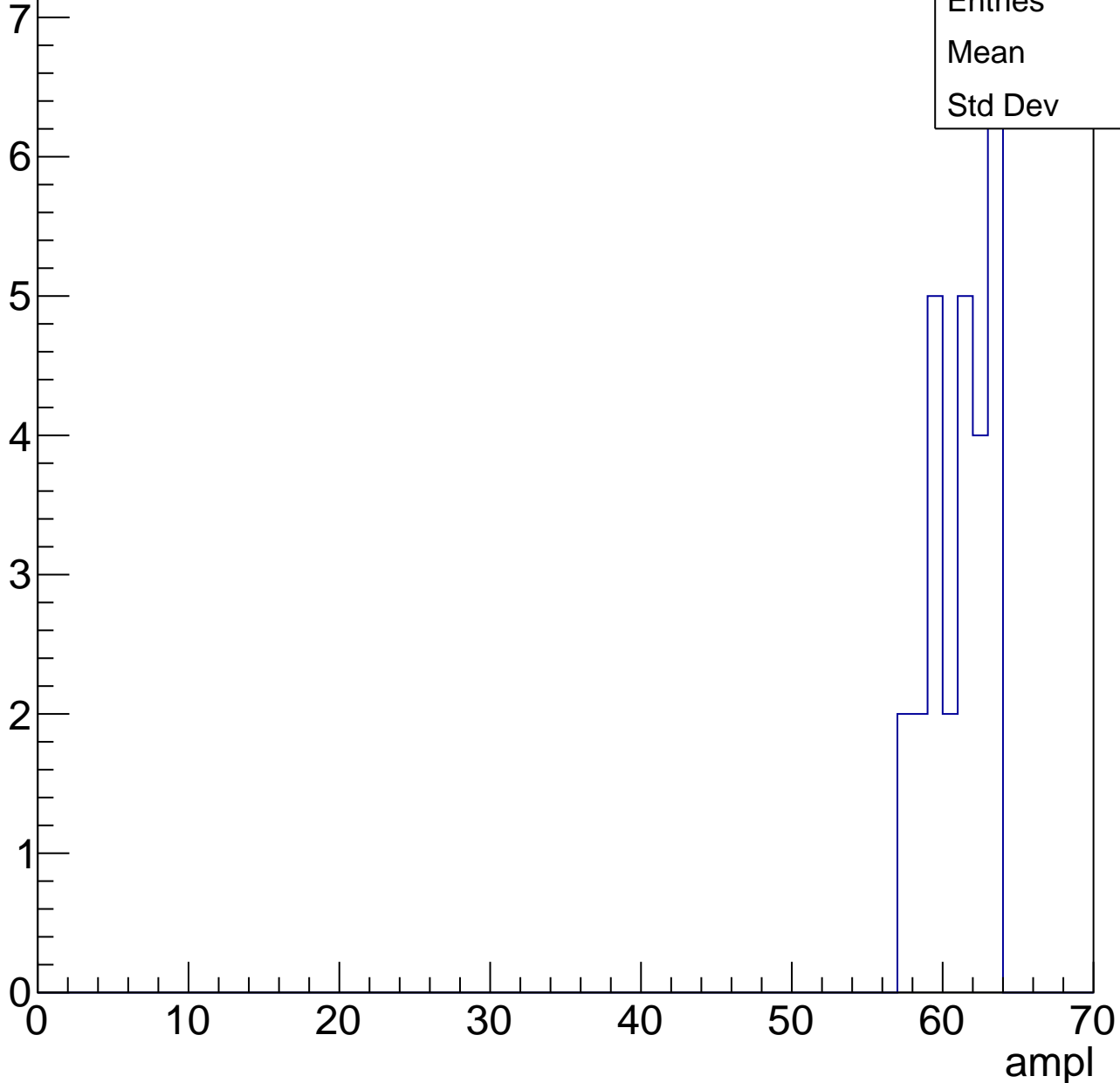


B0L002S, U26-ch5, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

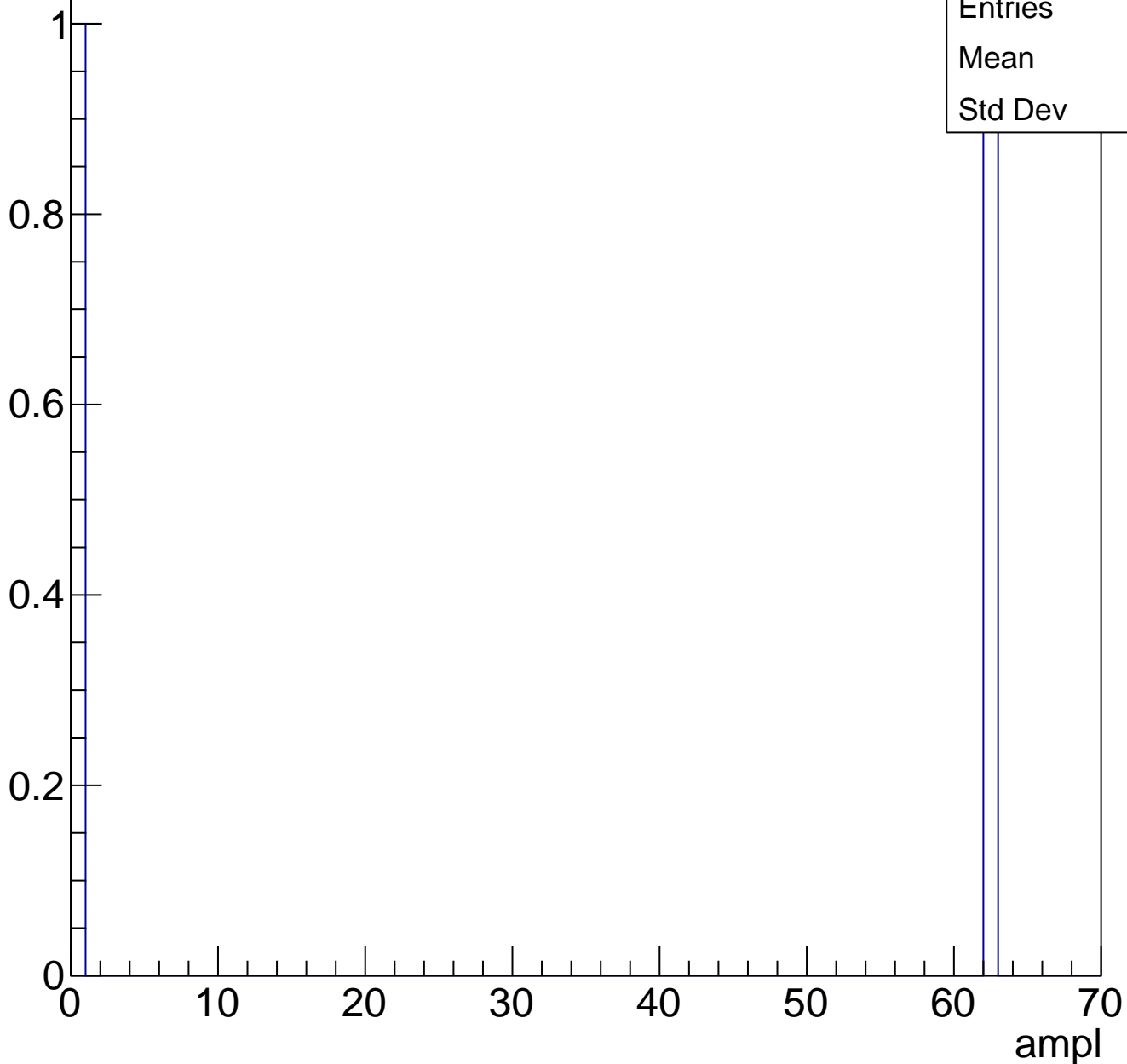
Entries	27
Mean	60.7
Std Dev	1.94



B0L002S, U26-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch6, adc0

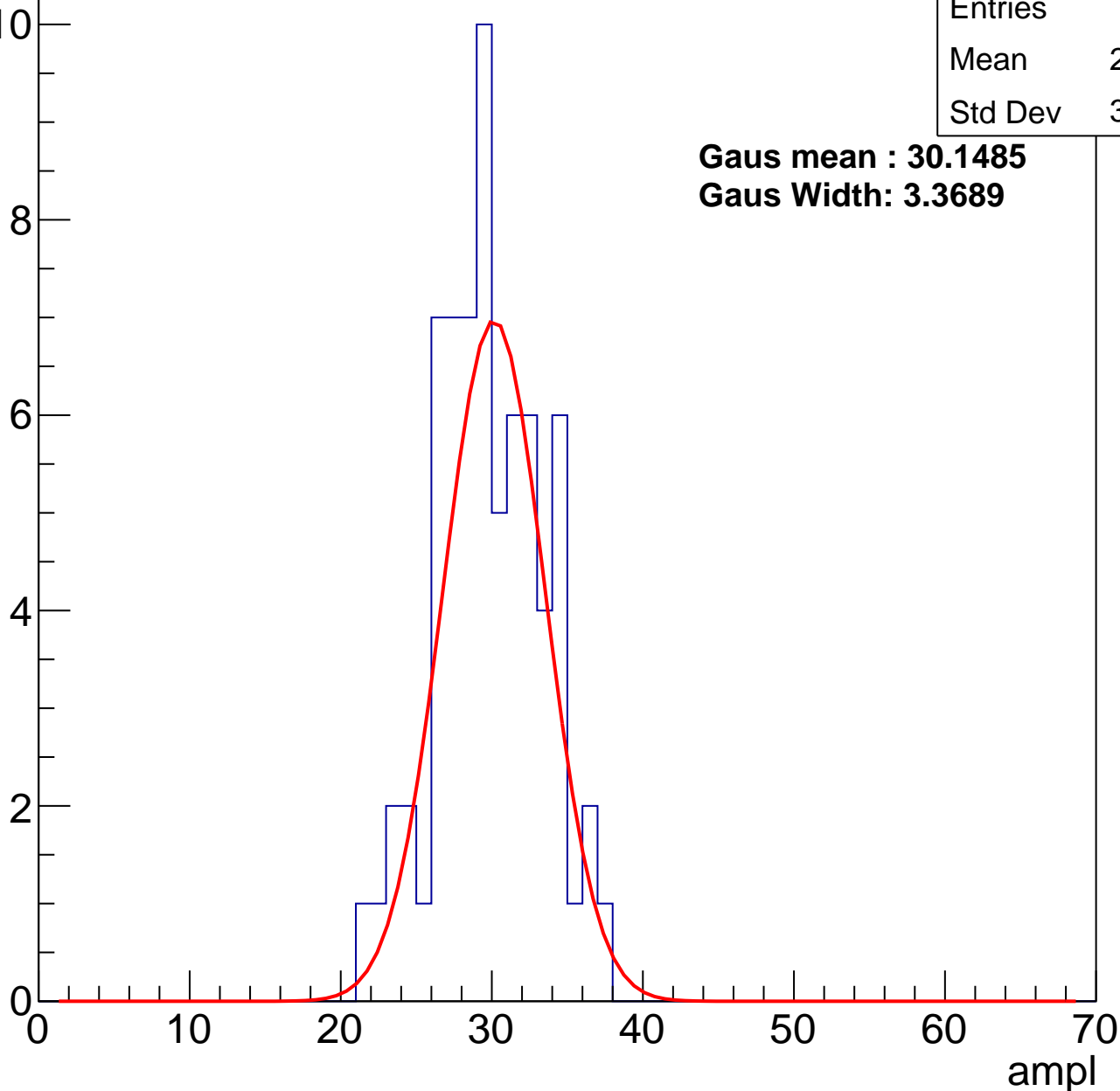
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.38
Std Dev	3.477

Gaus mean : 30.1485

Gaus Width: 3.3689



B0L002S, U26-ch6, adc1

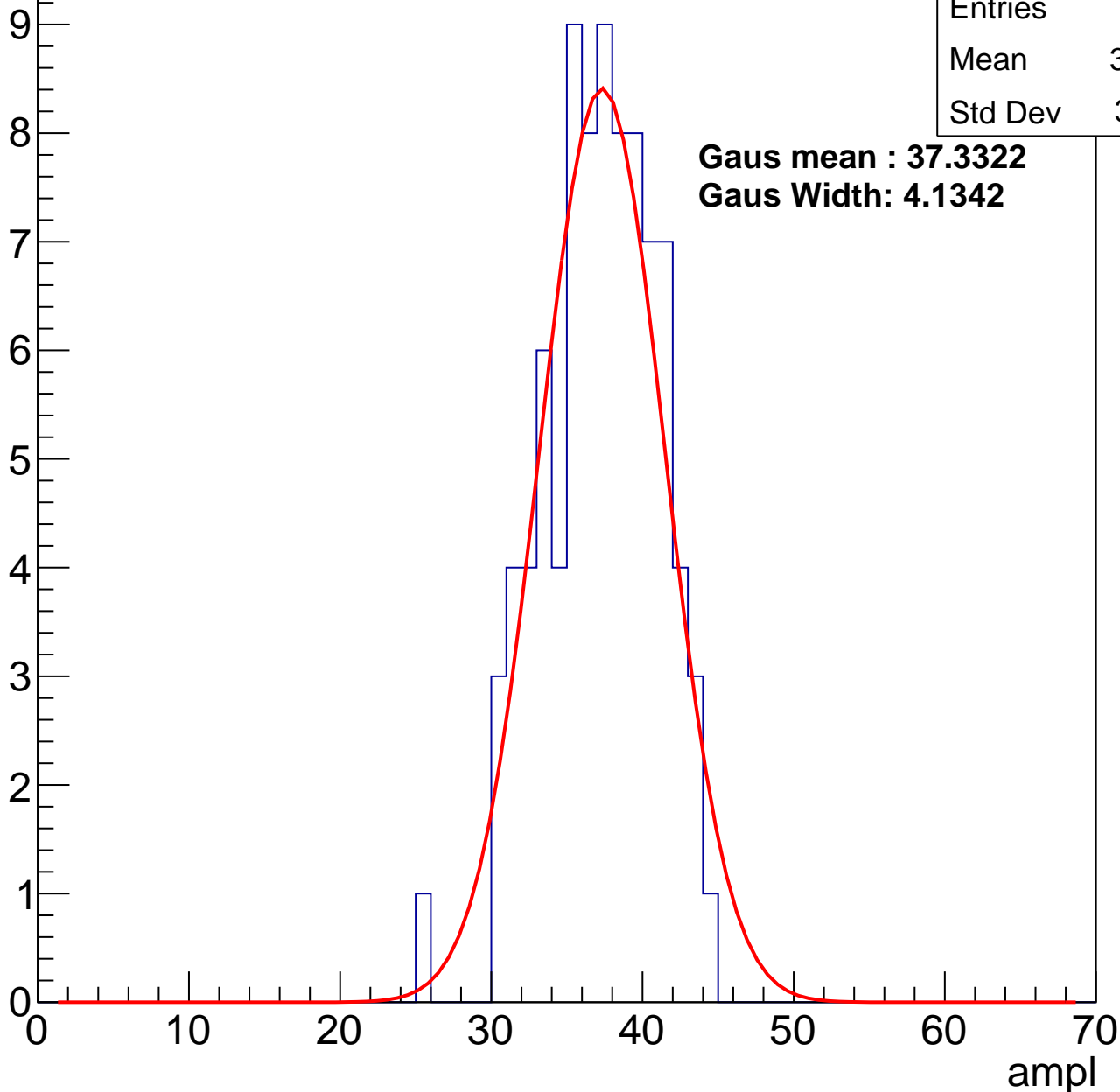
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	36.76
Std Dev	3.701

Gaus mean : 37.3322

Gaus Width: 4.1342



B0L002S, U26-ch6, adc2

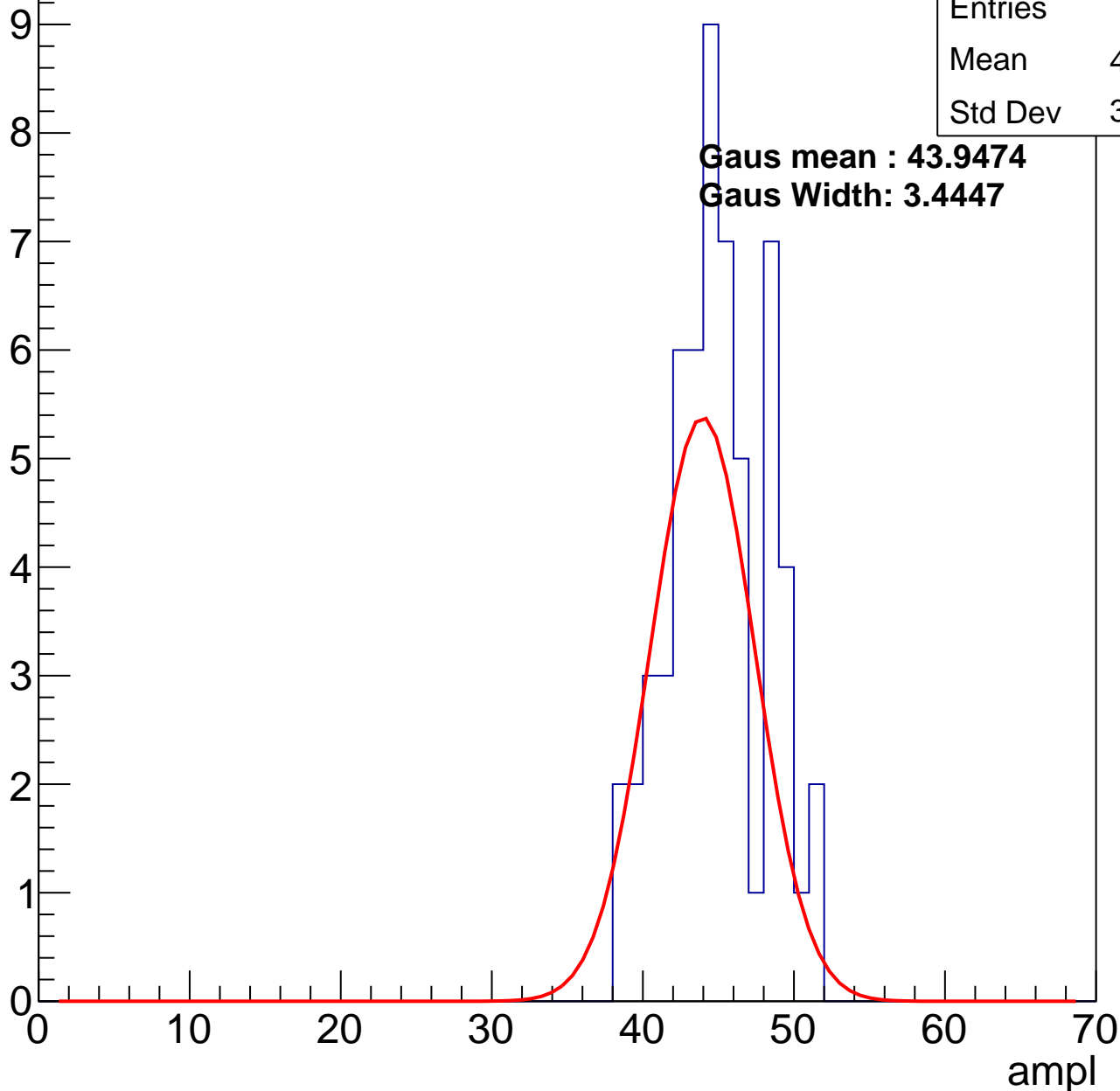
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.47
Std Dev	3.207

Gaus mean : 43.9474

Gaus Width: 3.4447

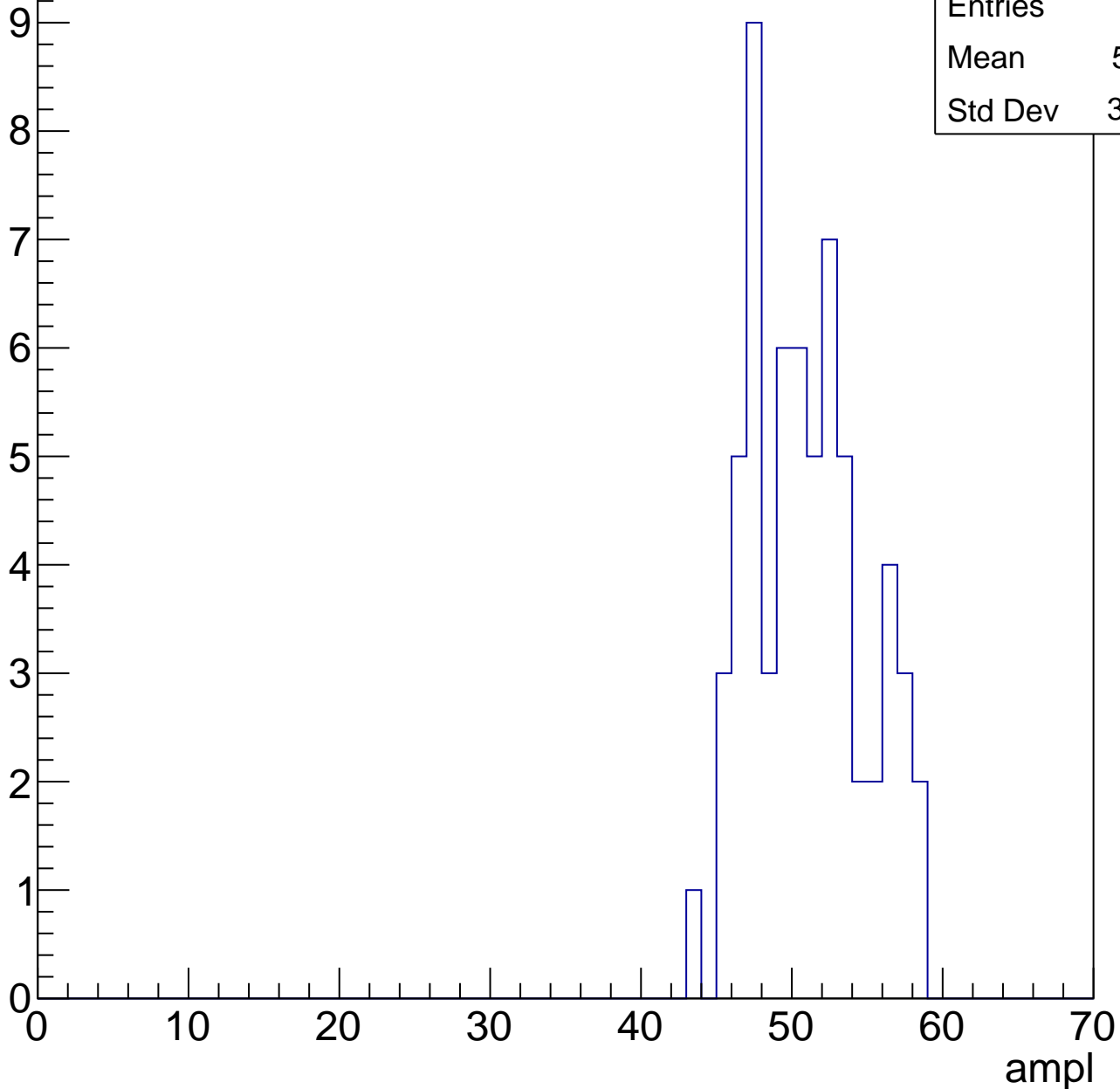


B0L002S, U26-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	50.51
Std Dev	3.707

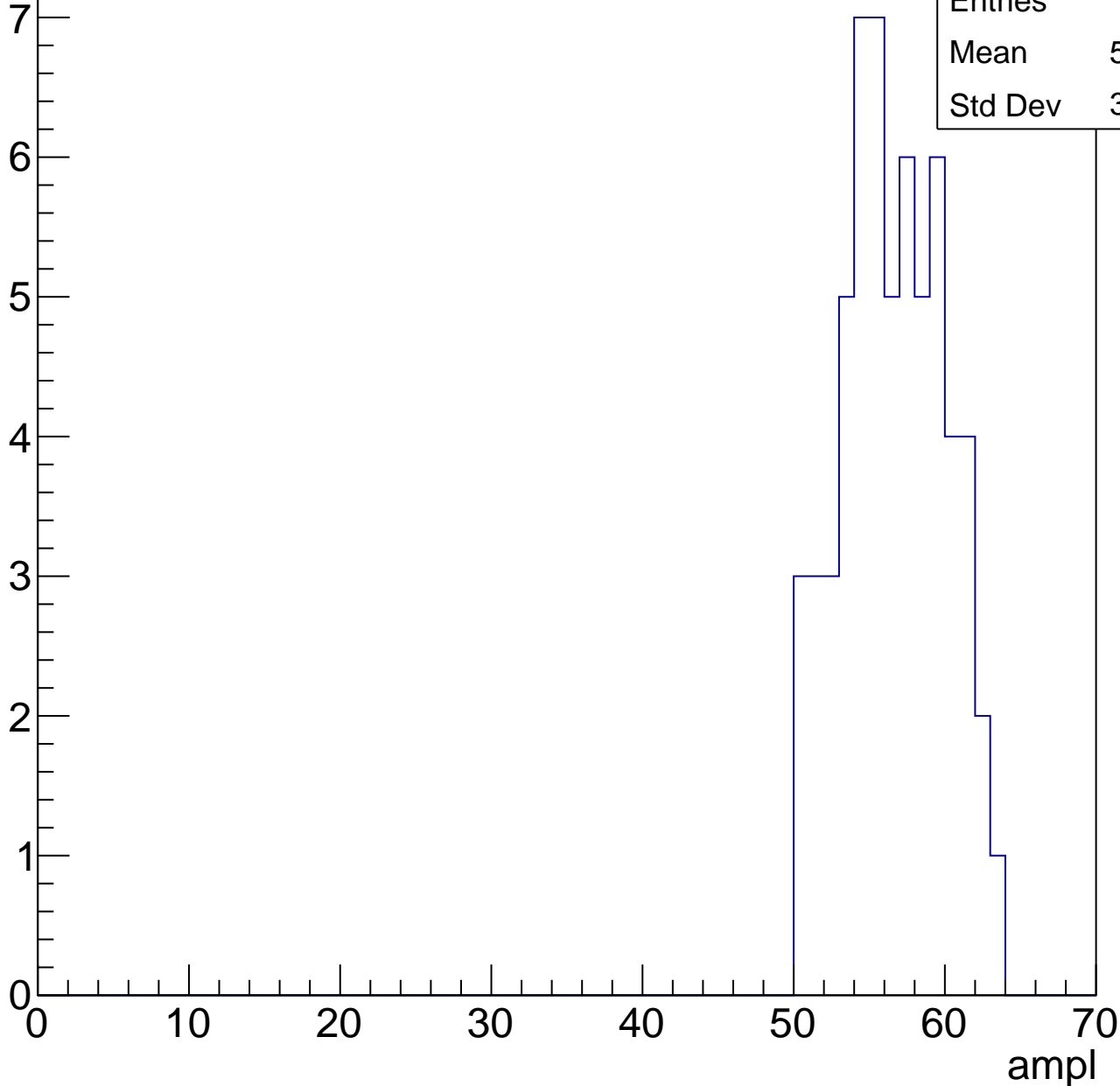


B0L002S, U26-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.13
Std Dev	3.326

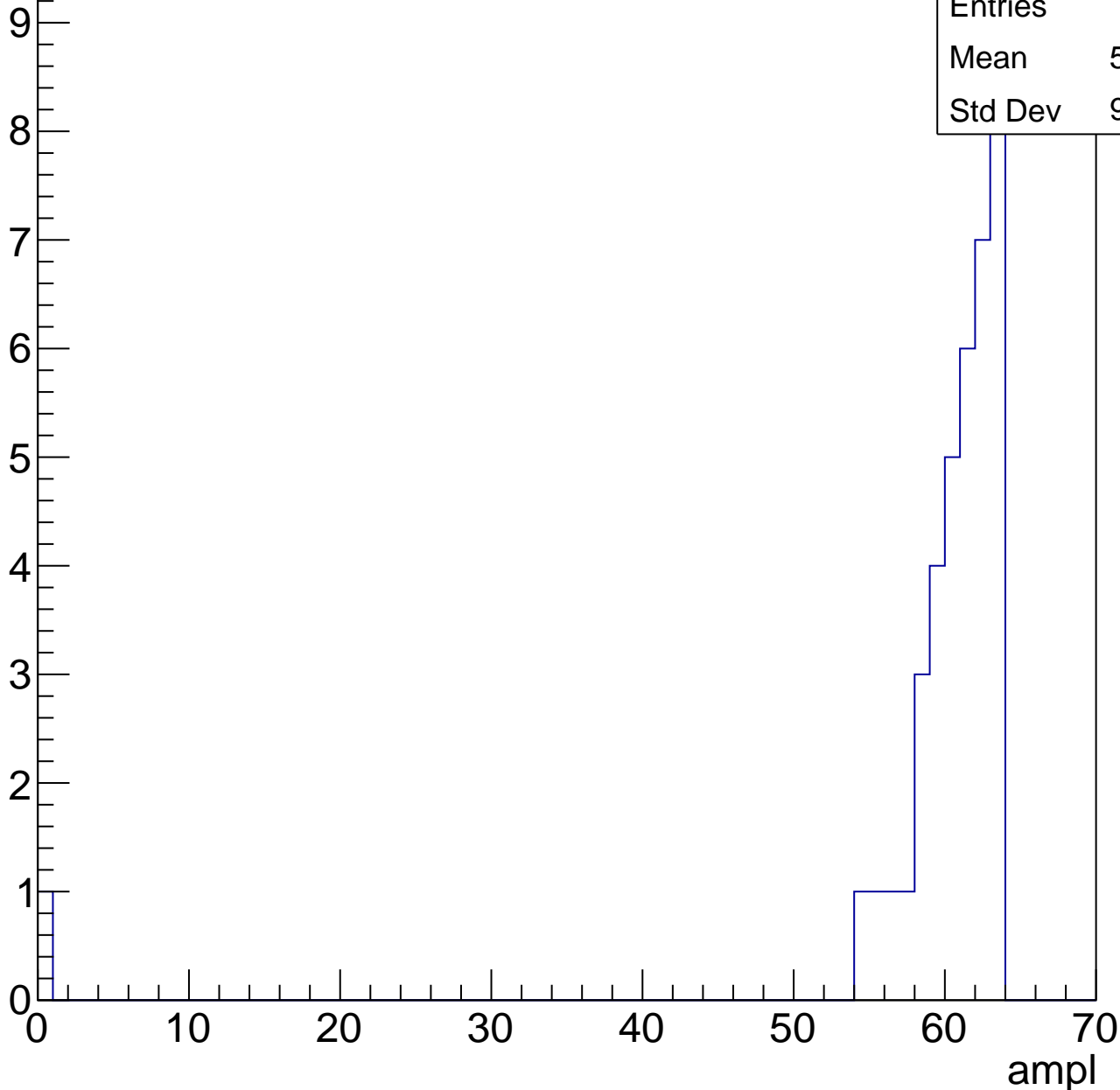


B0L002S, U26-ch6, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	58.95
Std Dev	9.837



B0L002S, U26-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

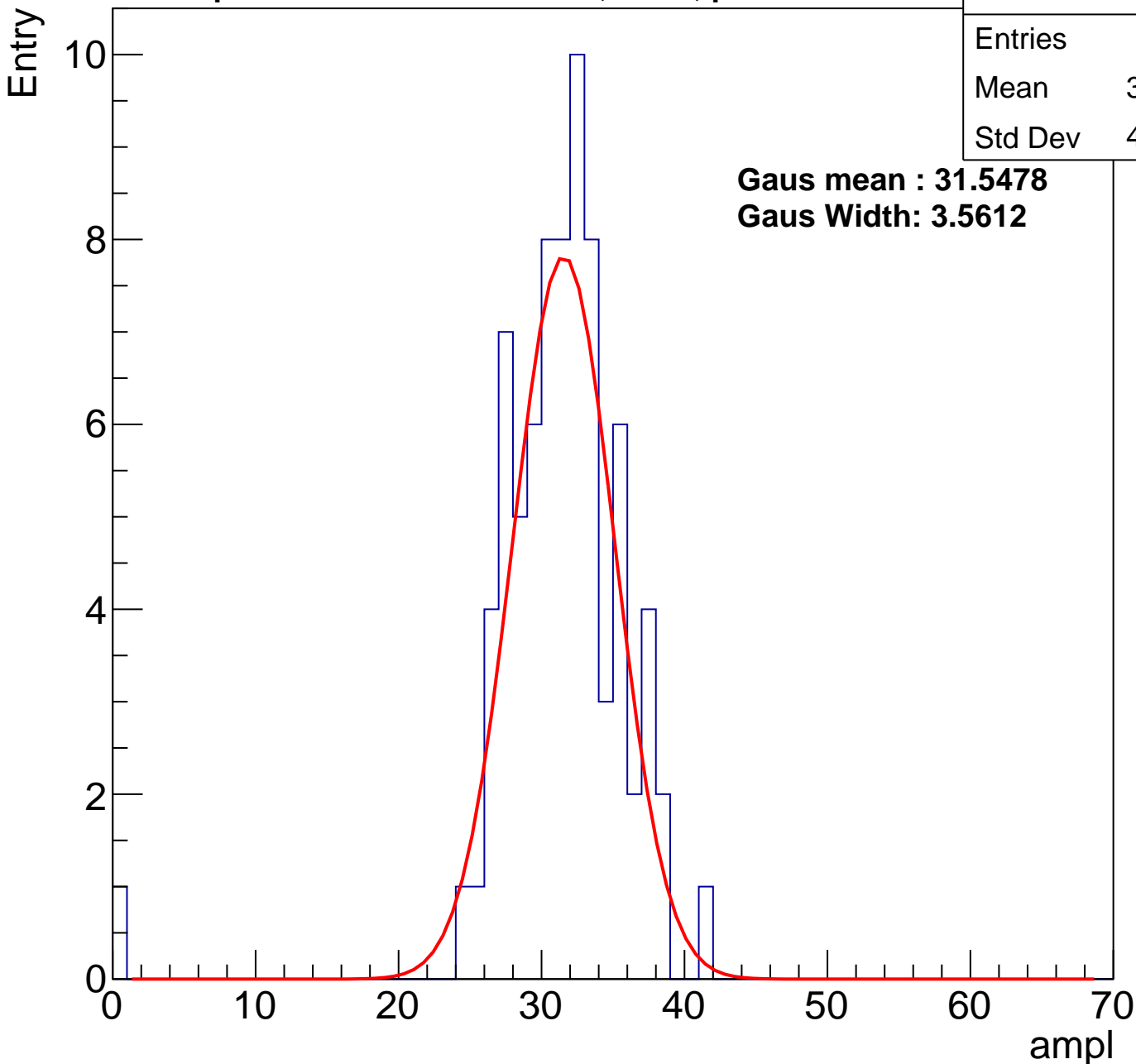
B0L002S, U26-ch7, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	30.87
Std Dev	4.955

Gaus mean : 31.5478

Gaus Width: 3.5612



B0L002S, U26-ch7, adc1

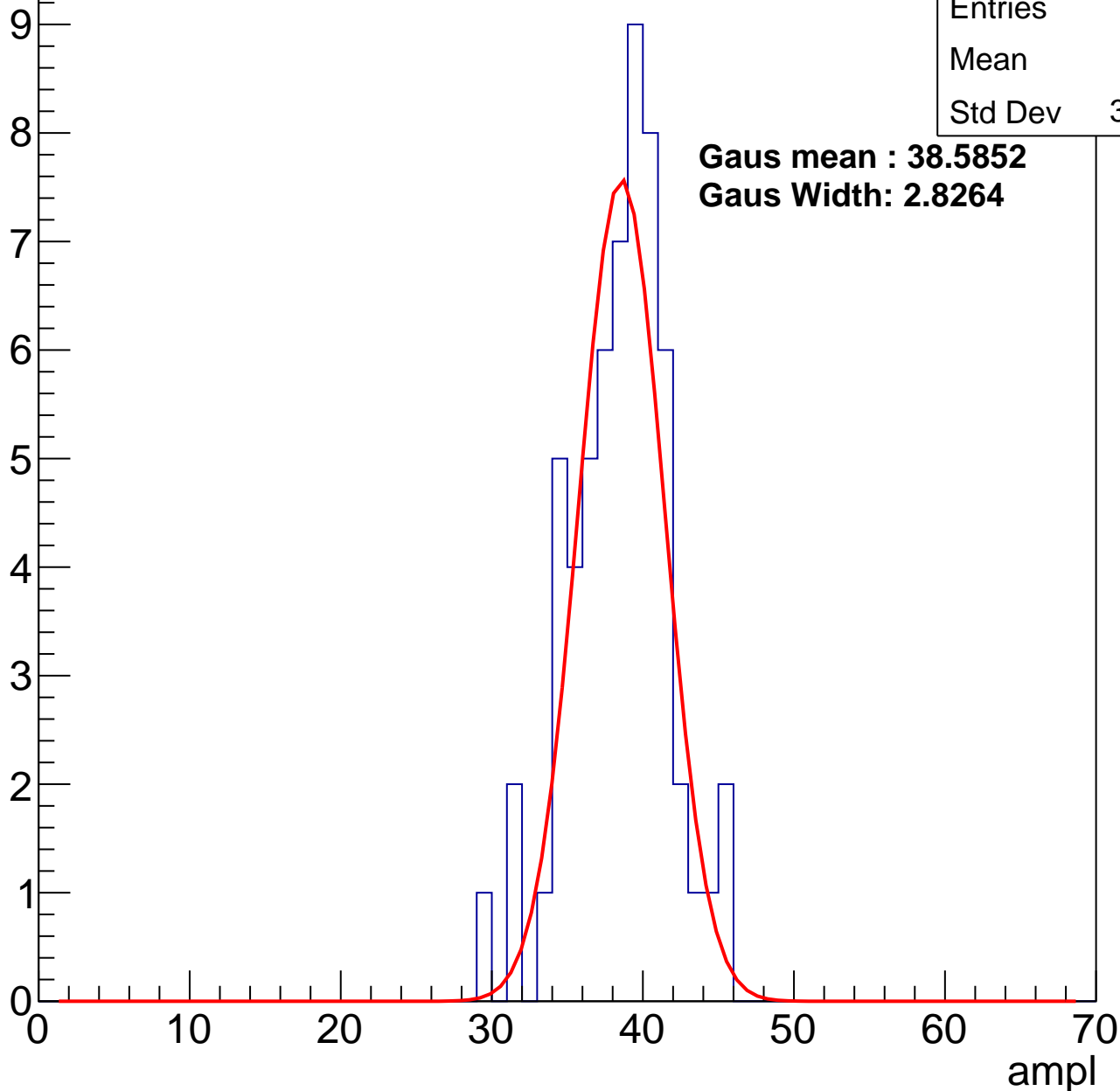
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	38
Std Dev	3.246

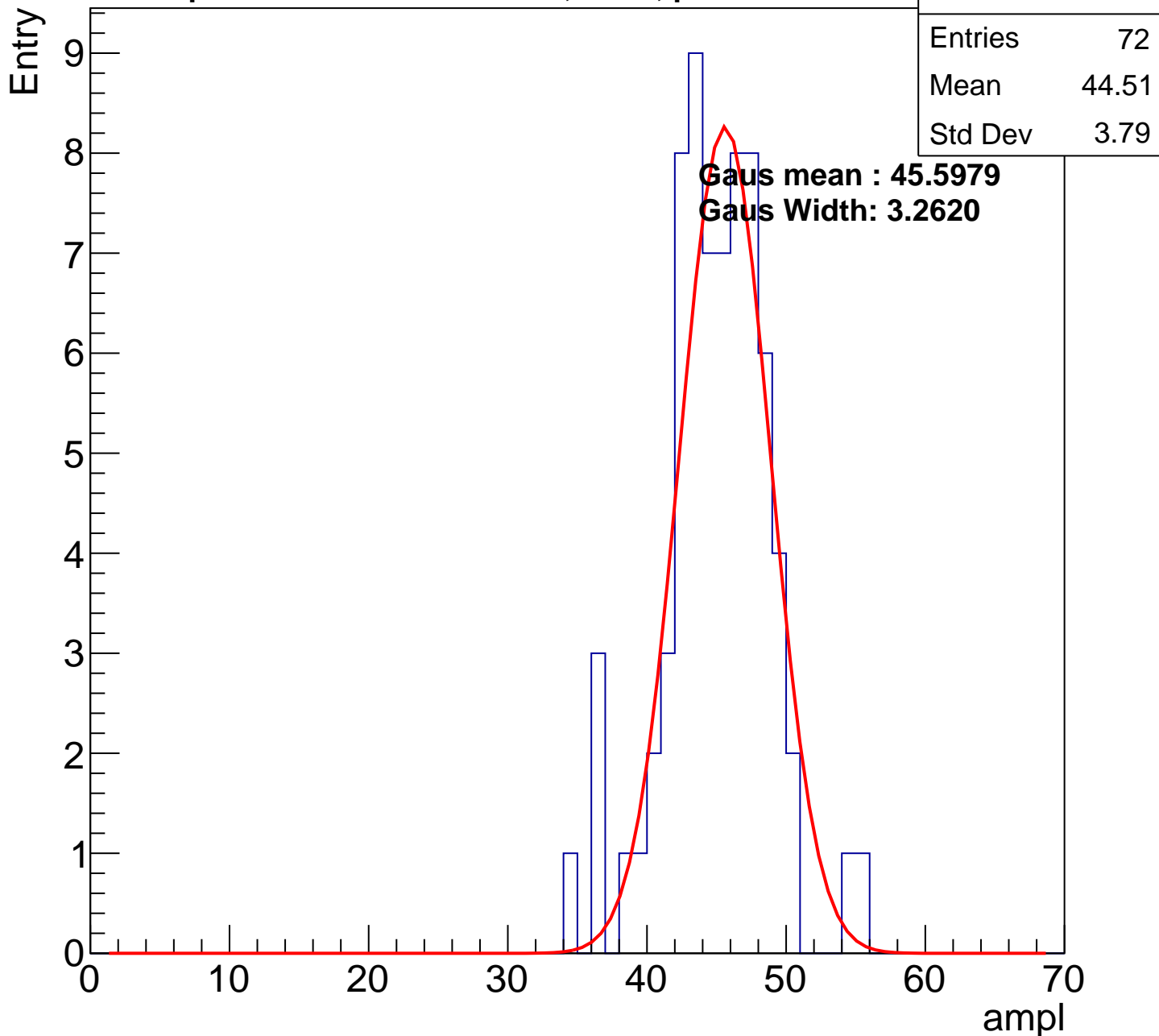
Gaus mean : 38.5852

Gaus Width: 2.8264



B0L002S, U26-ch7, adc2

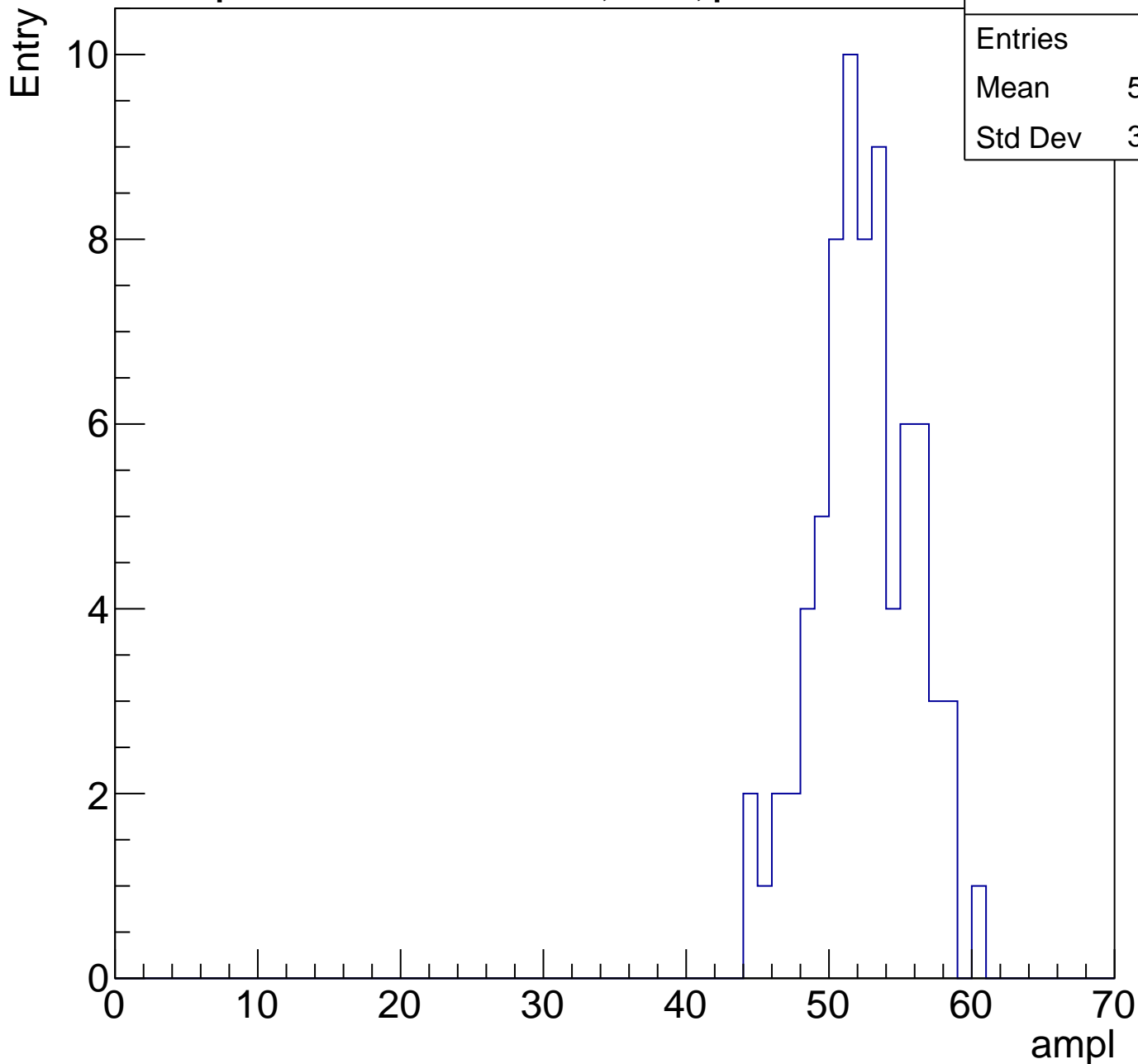
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U26-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

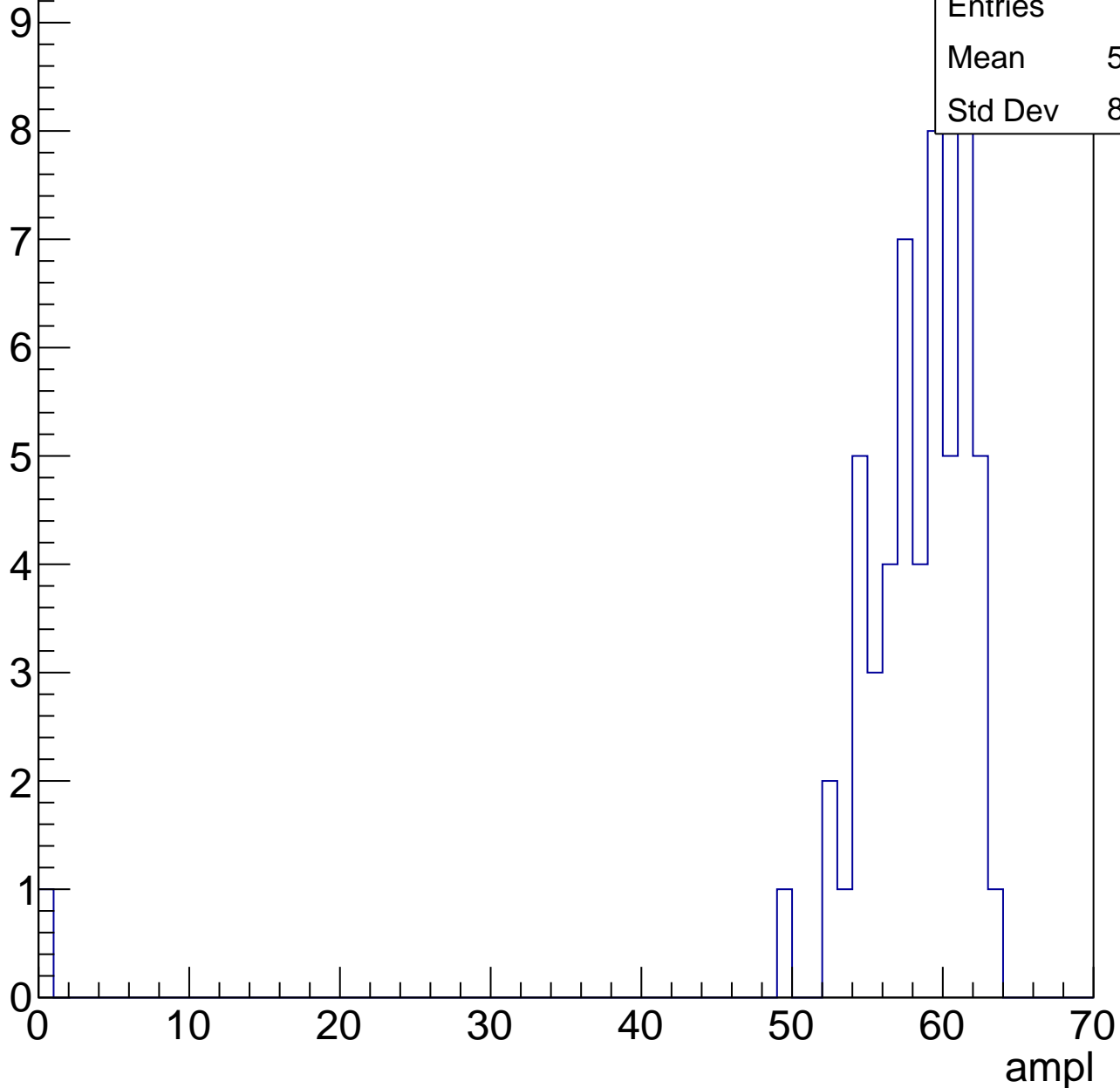
Entries	74
Mean	51.97
Std Dev	3.433



B0L002S, U26-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

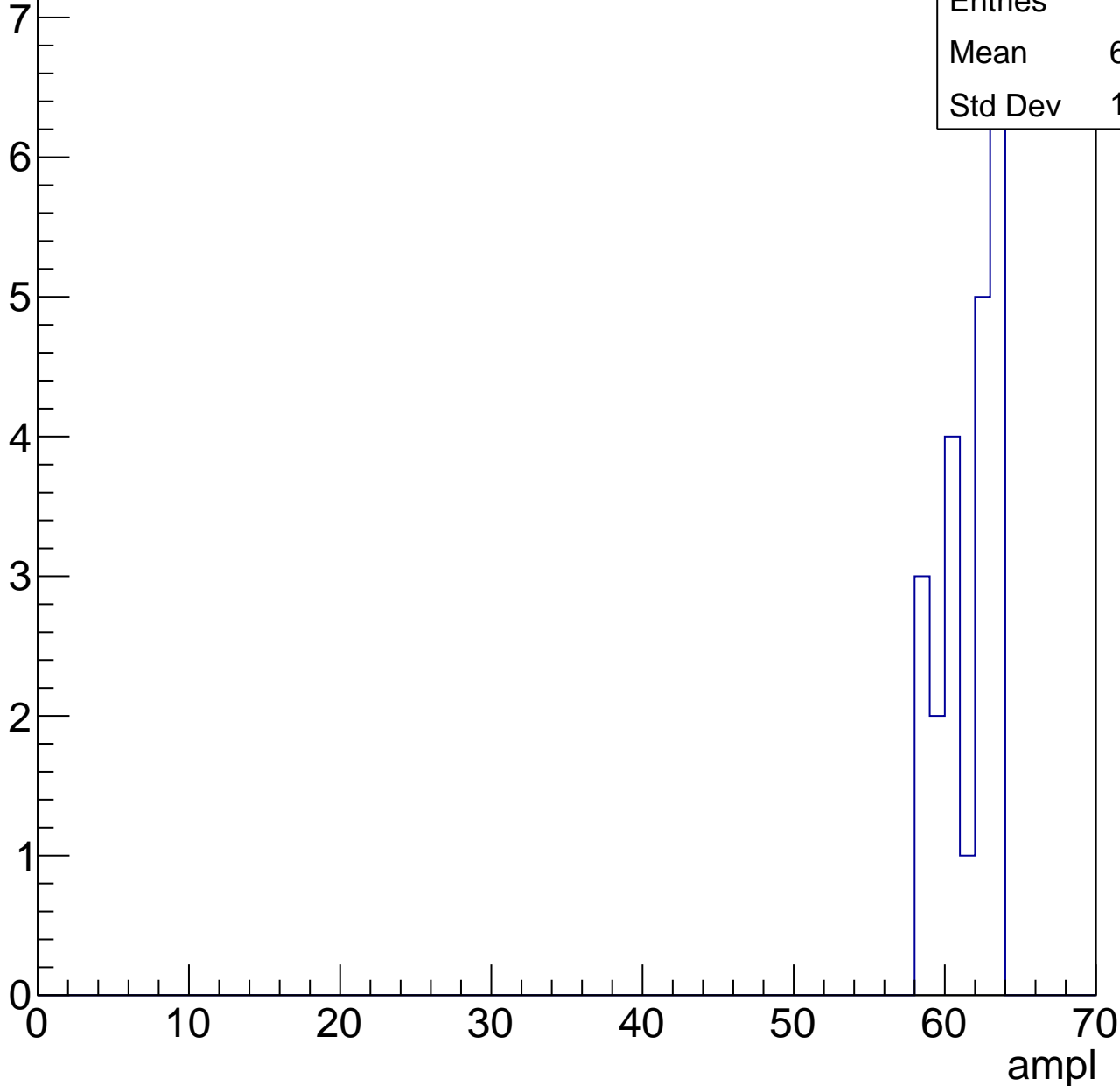


B0L002S, U26-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	61.09
Std Dev	1.807



B0L002S, U26-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L002S, U26-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch8, adc0

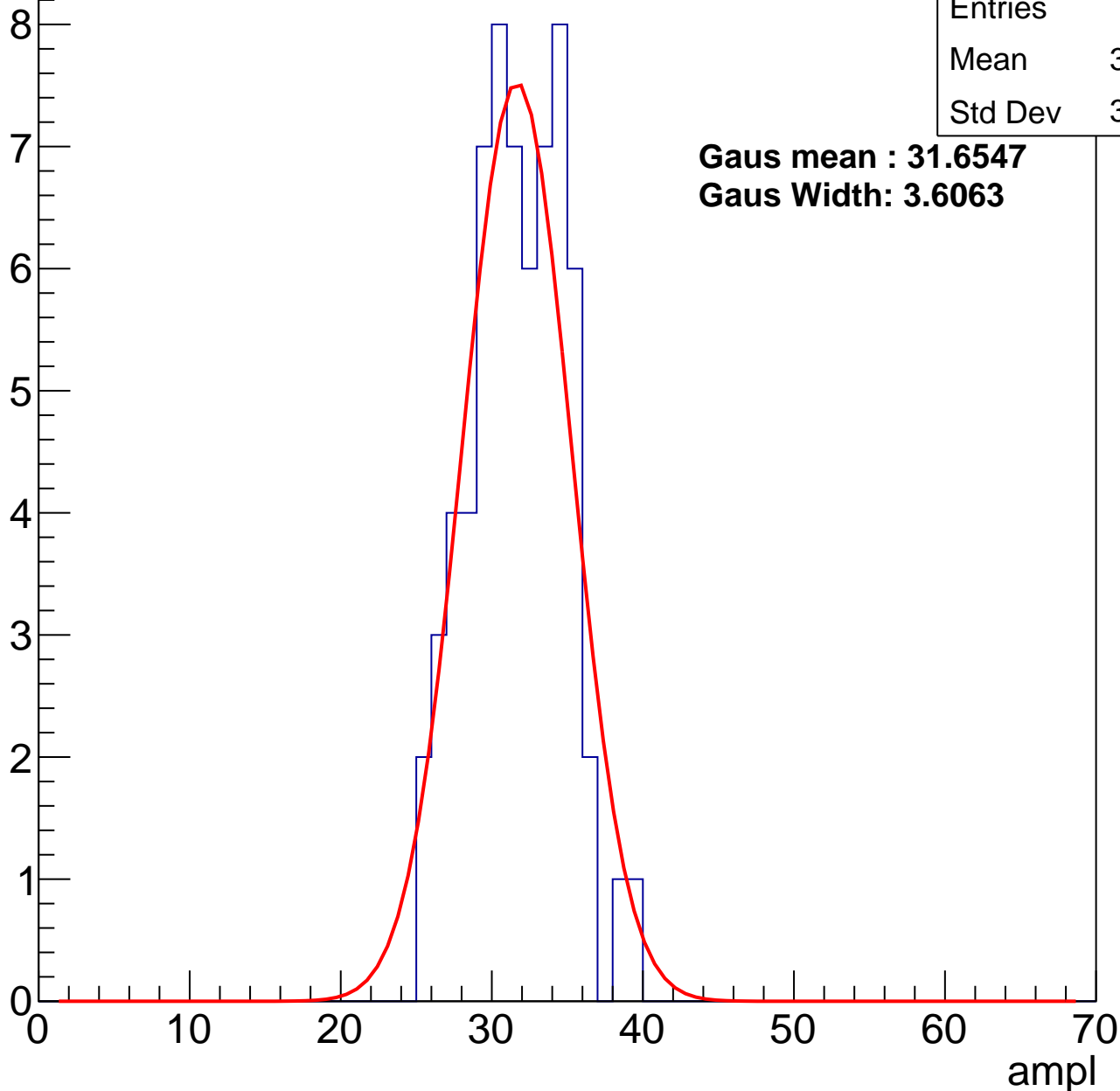
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	31.24
Std Dev	3.124

Gaus mean : 31.6547

Gaus Width: 3.6063



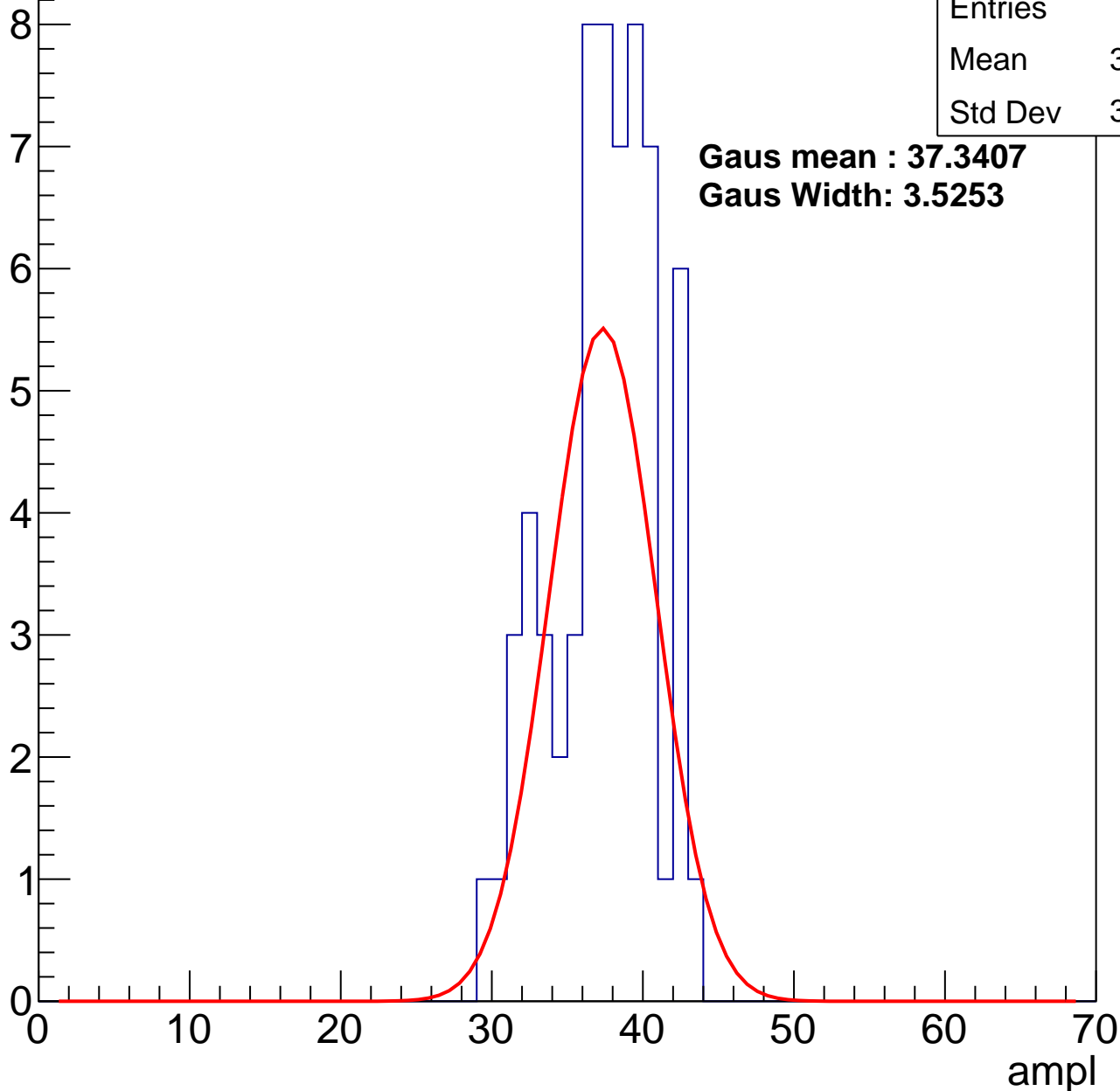
B0L002S, U26-ch8, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.98
Std Dev	3.359

Gaus mean : 37.3407
Gaus Width: 3.5253



B0L002S, U26-ch8, adc2

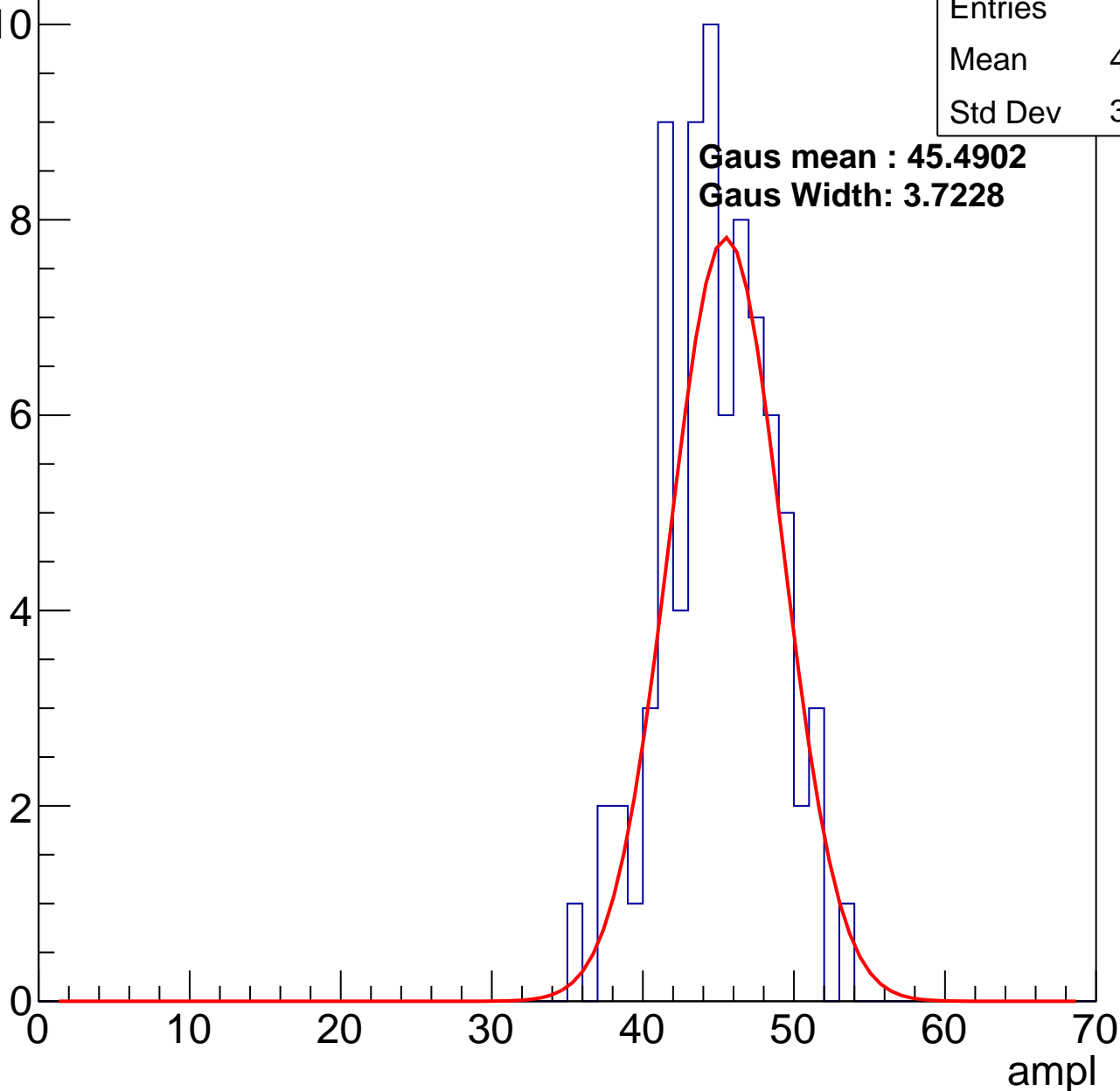
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	44.48
Std Dev	3.617

Gaus mean : 45.4902

Gaus Width: 3.7228

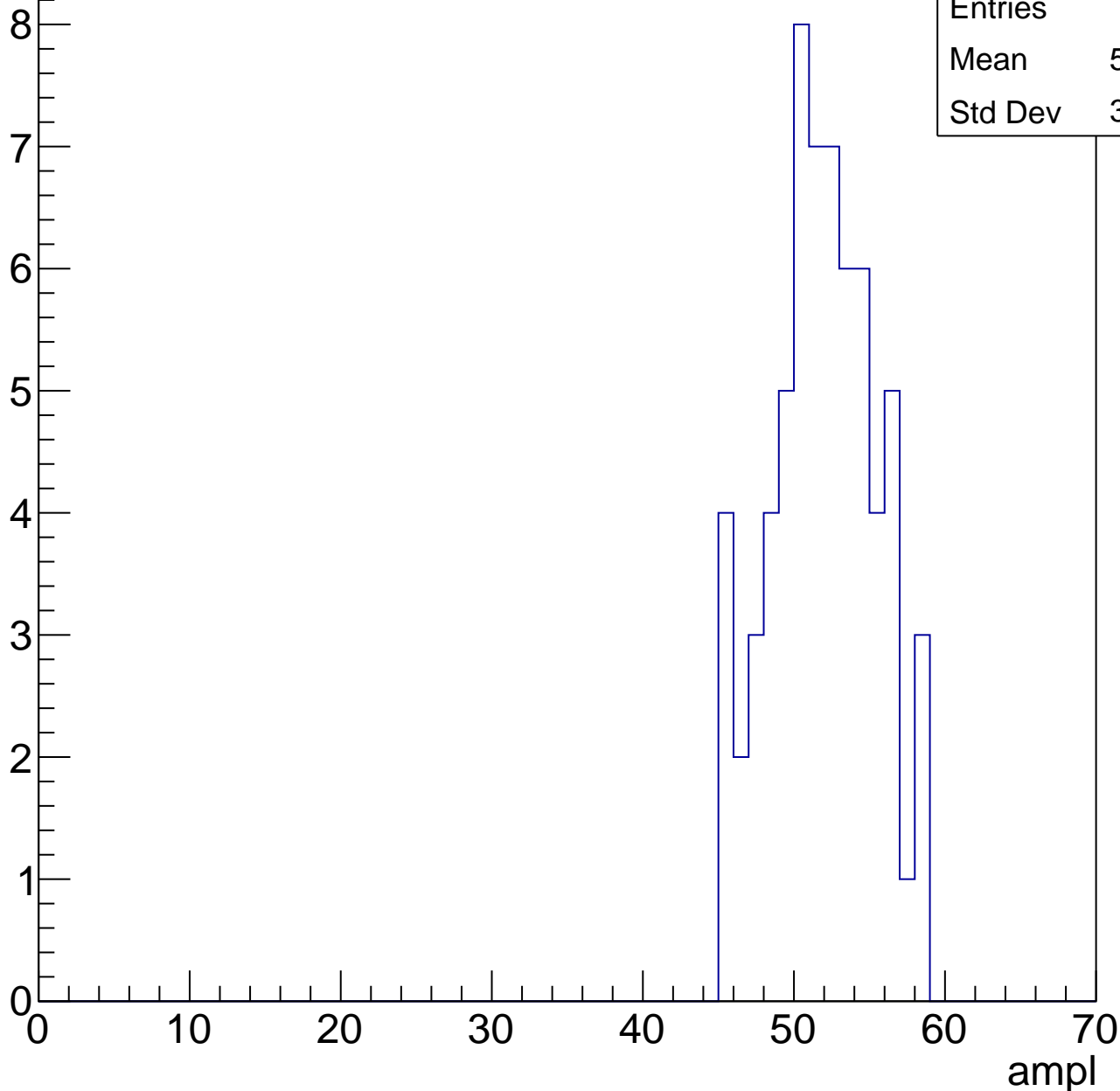


B0L002S, U26-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

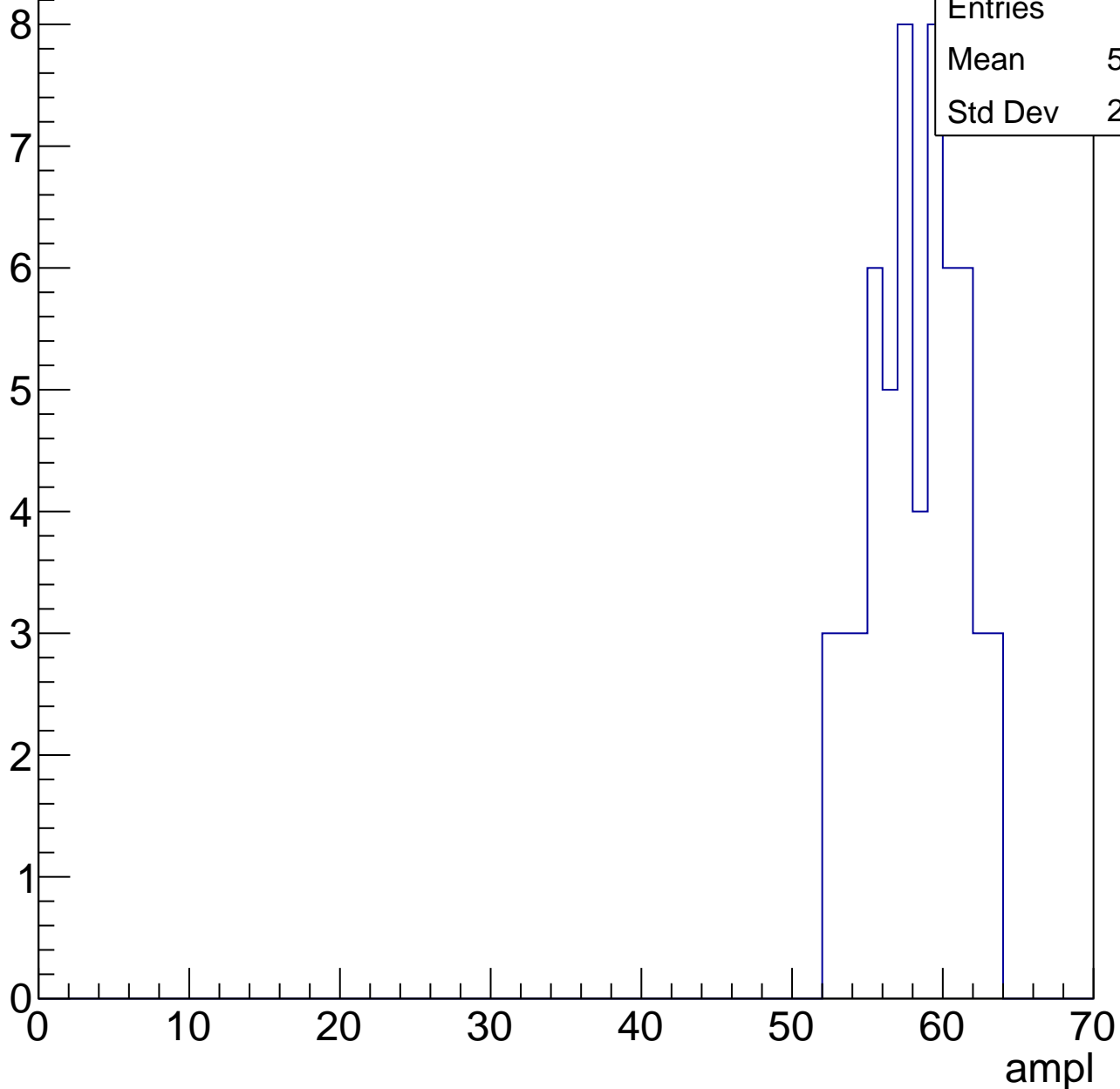
Entries	65
Mean	51.45
Std Dev	3.397



B0L002S, U26-ch8, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

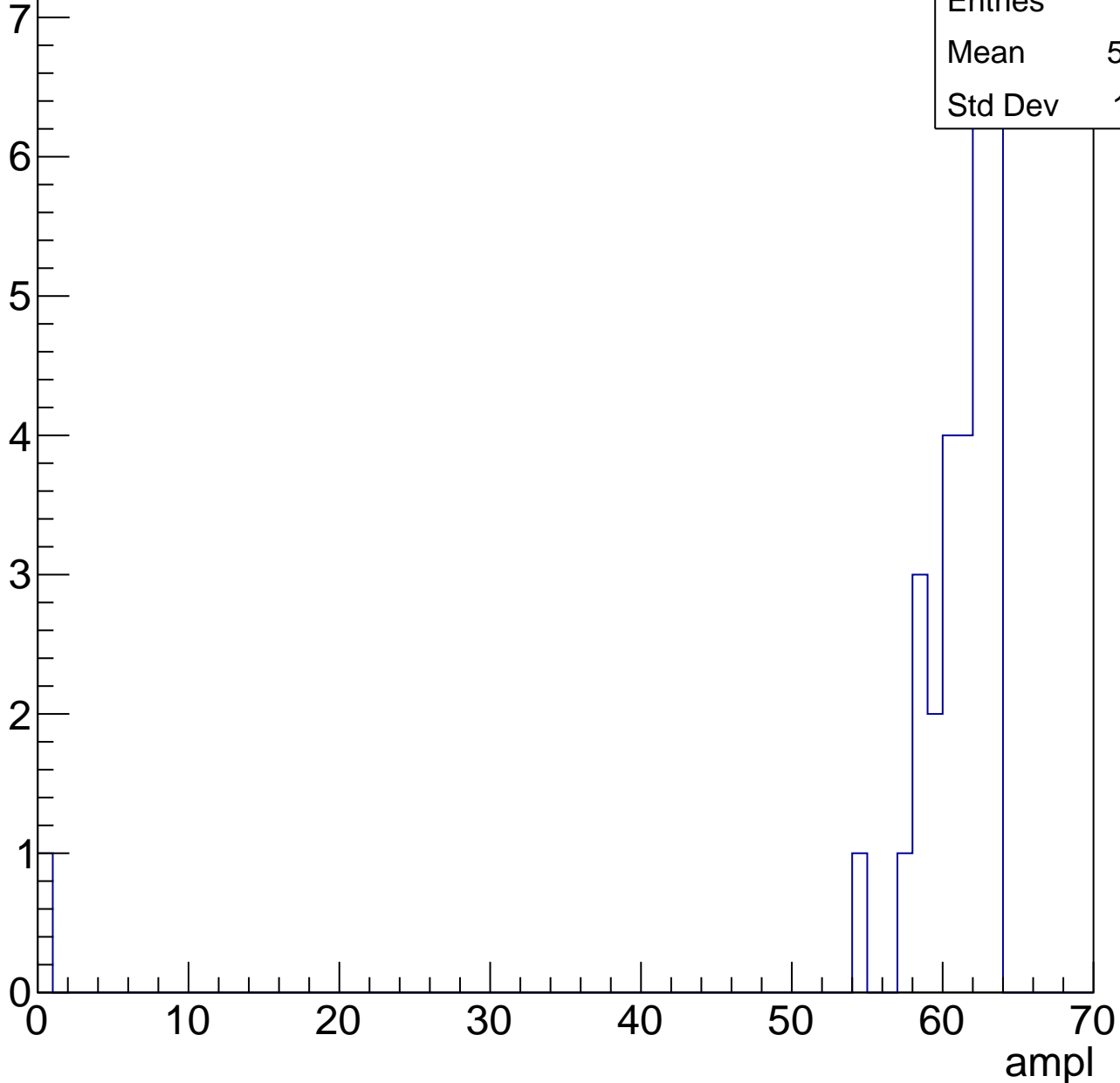


B0L002S, U26-ch8, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.73
Std Dev	11.11



B0L002S, U26-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch9, adc0

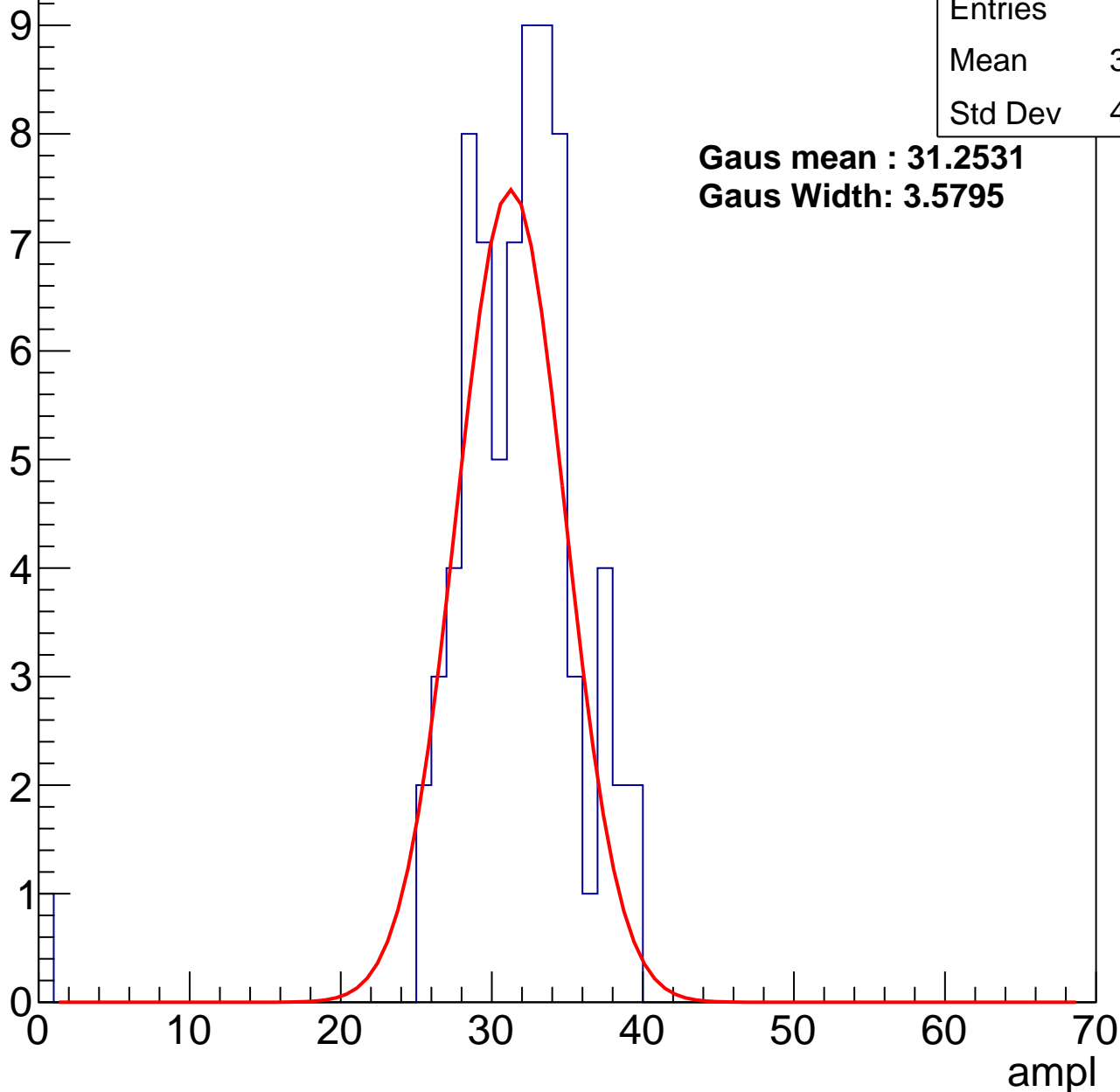
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.07
Std Dev	4.949

Gaus mean : 31.2531

Gaus Width: 3.5795



B0L002S, U26-ch9, adc1

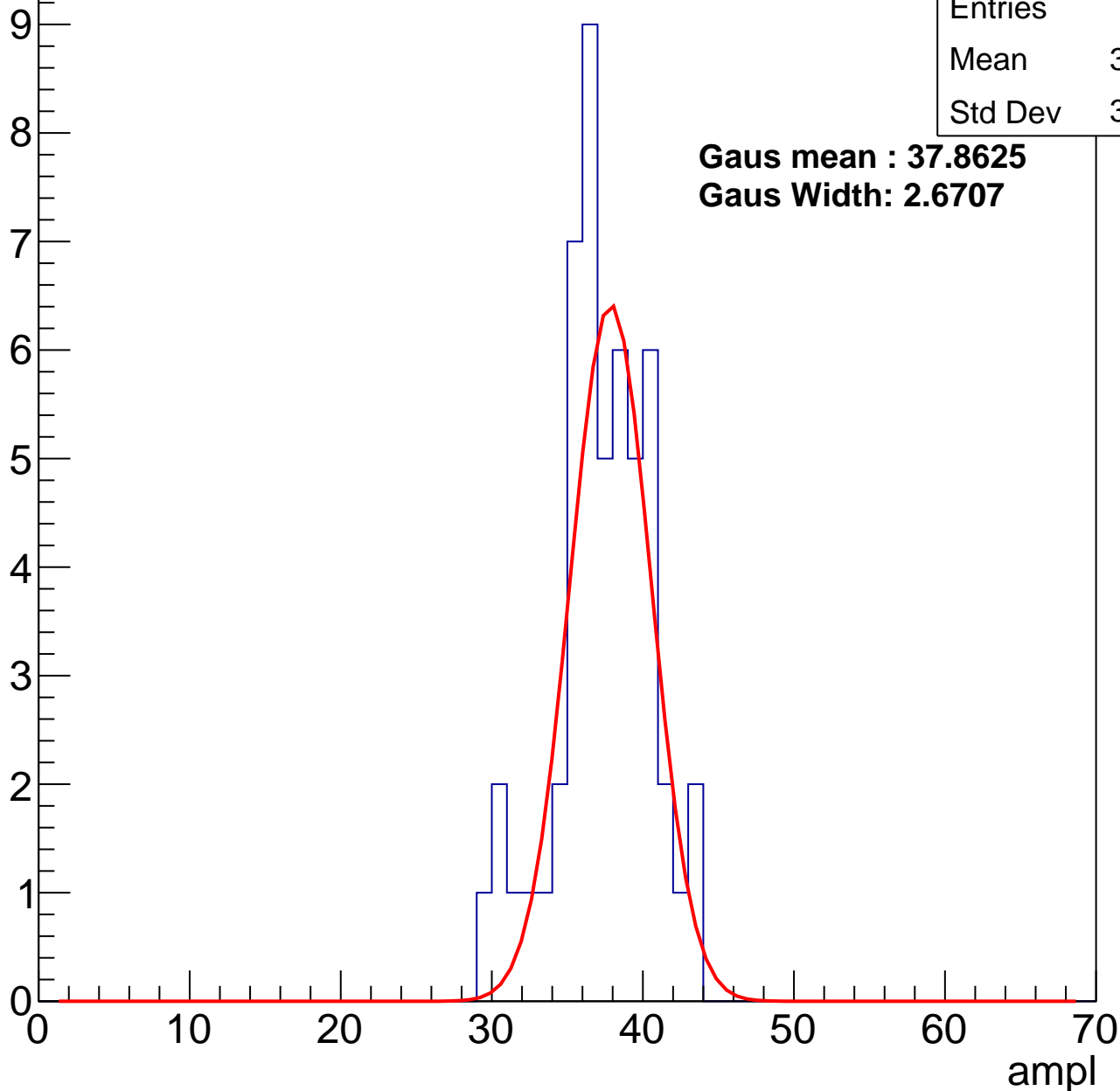
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	36.86
Std Dev	3.137

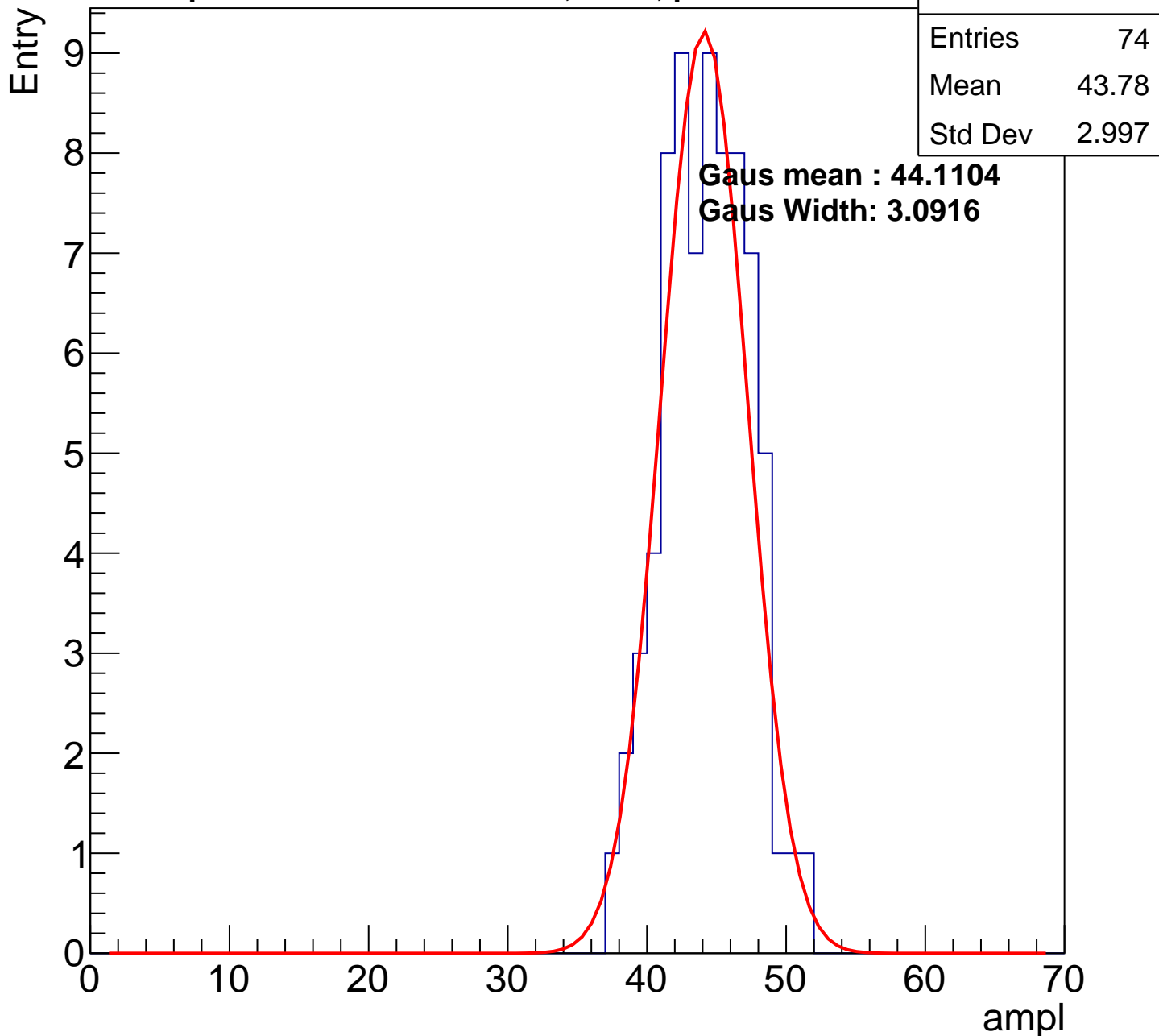
Gaus mean : 37.8625

Gaus Width: 2.6707



B0L002S, U26-ch9, adc2

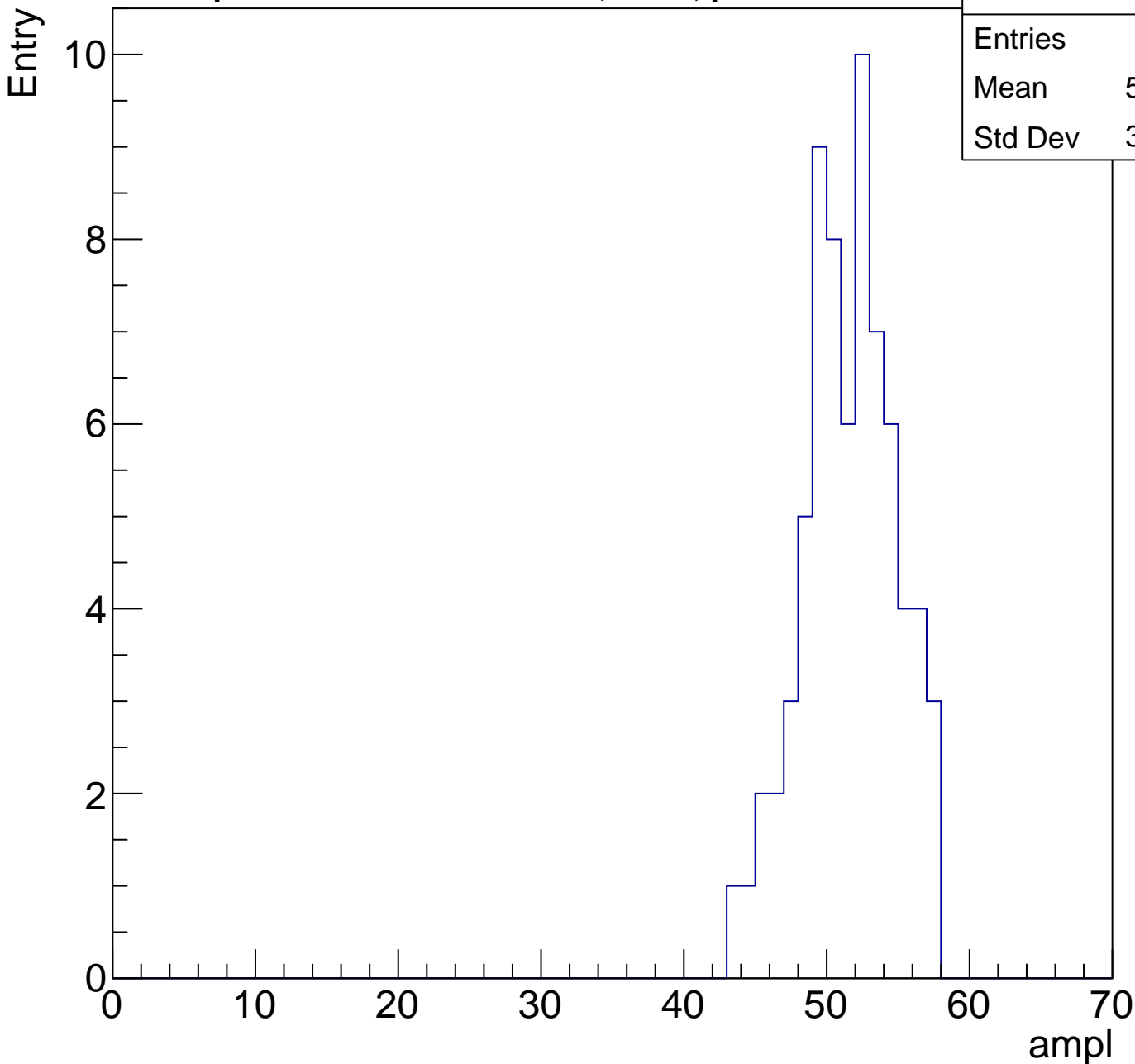
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U26-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	51.08
Std Dev	3.219



B0L002S, U26-ch9, adc4

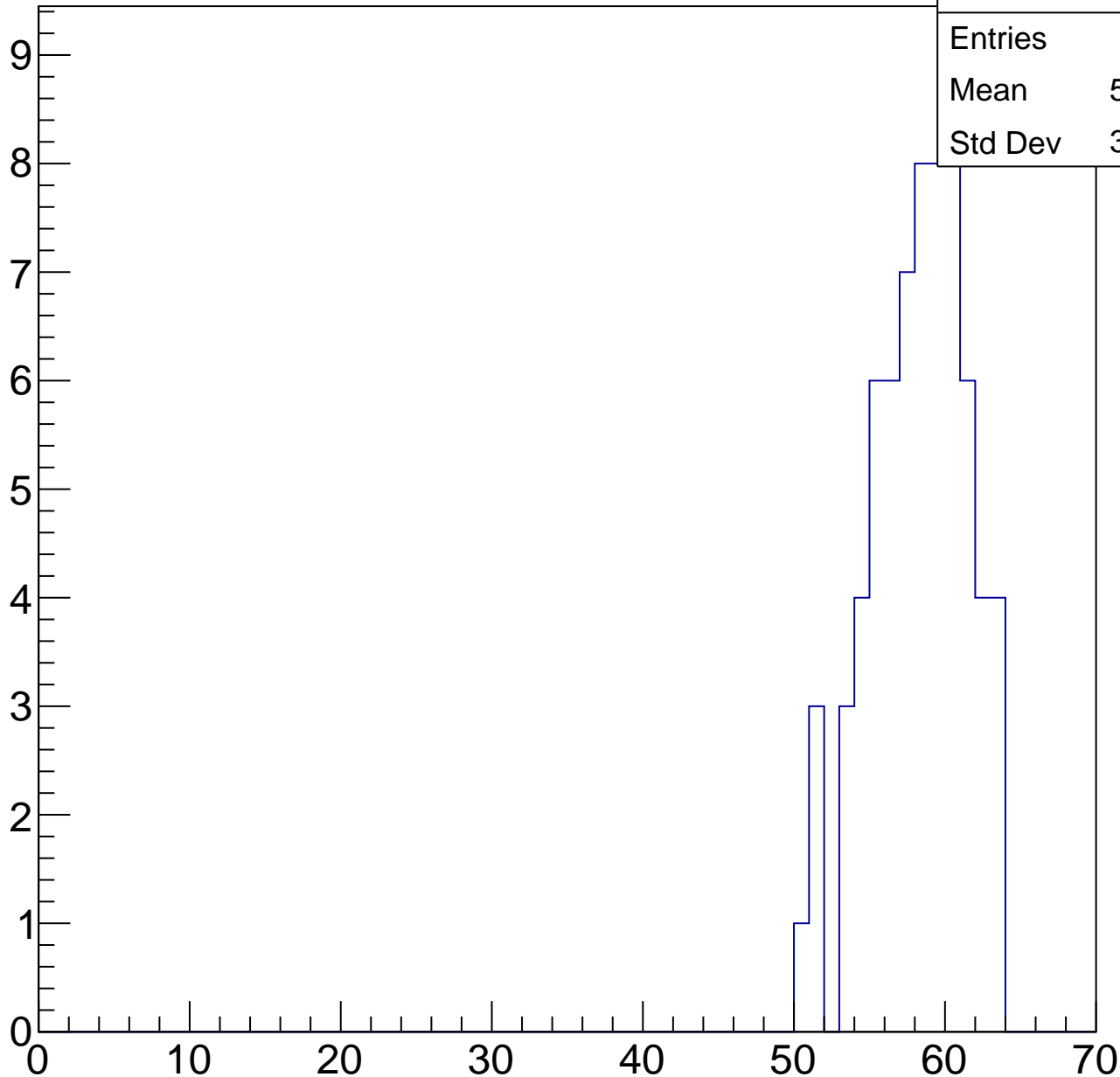
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	57.75
Std Dev	3.169

ampl

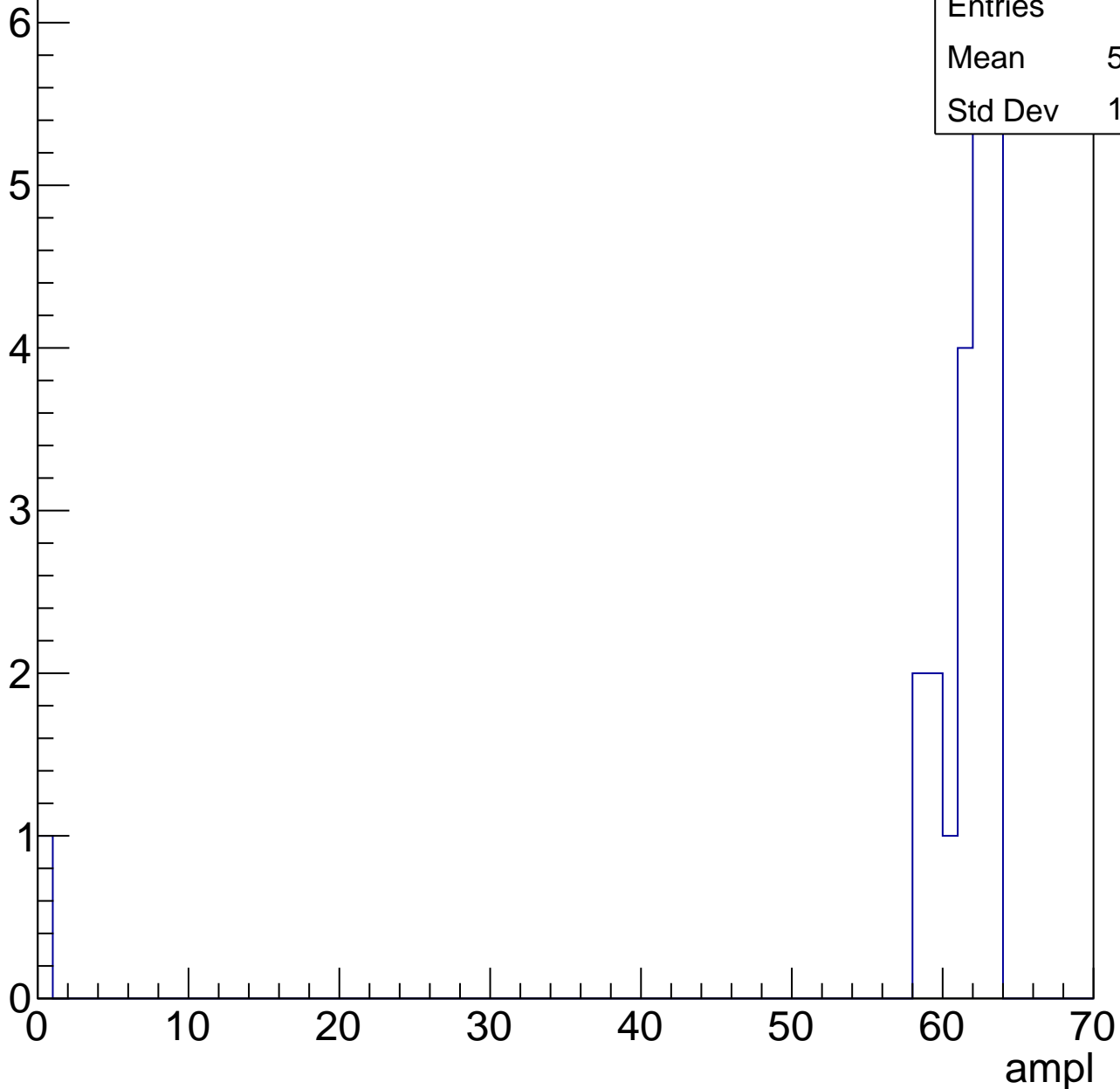


B0L002S, U26-ch9, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.55
Std Dev	12.87



B0L002S, U26-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

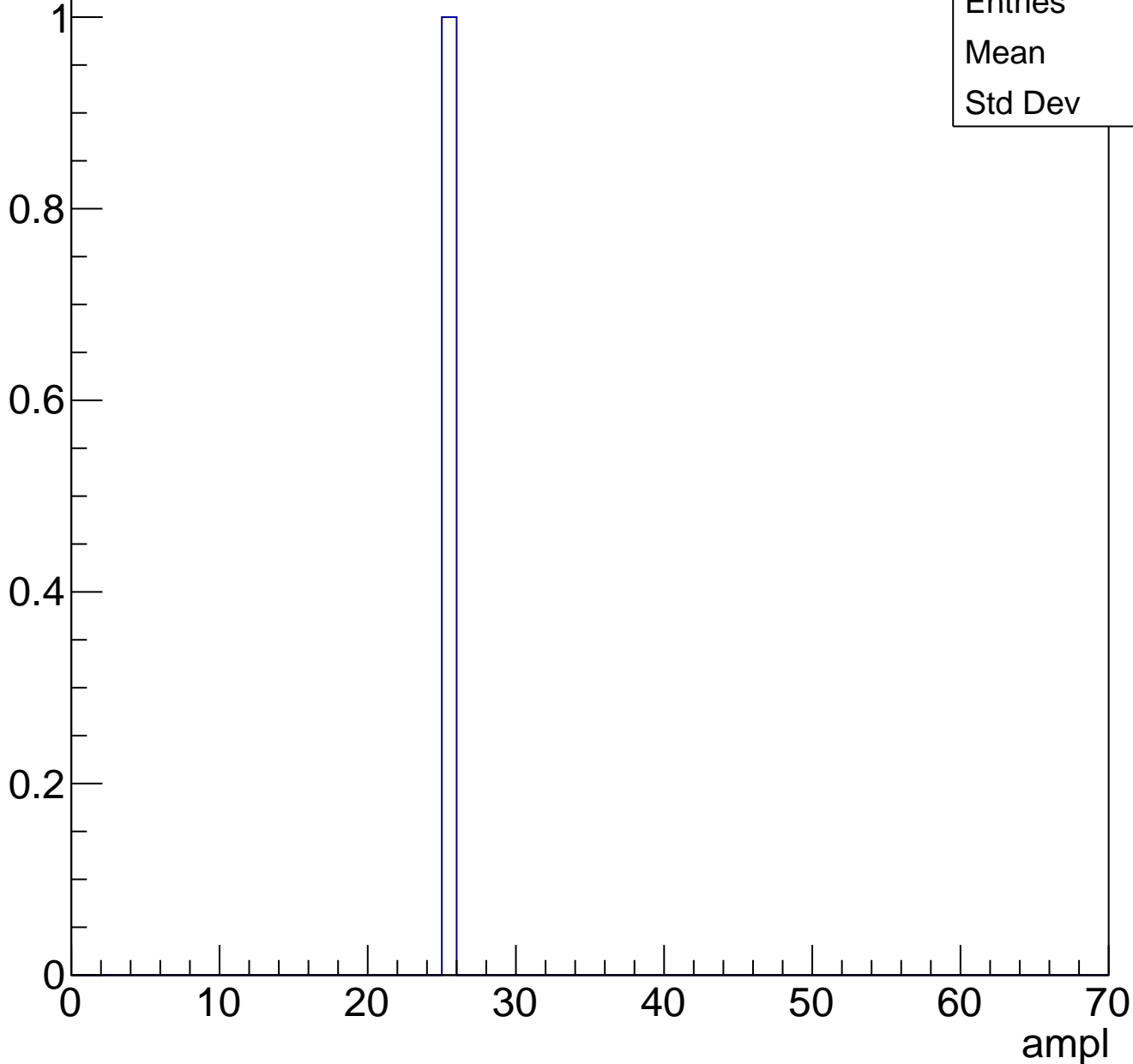


Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch10, adc0

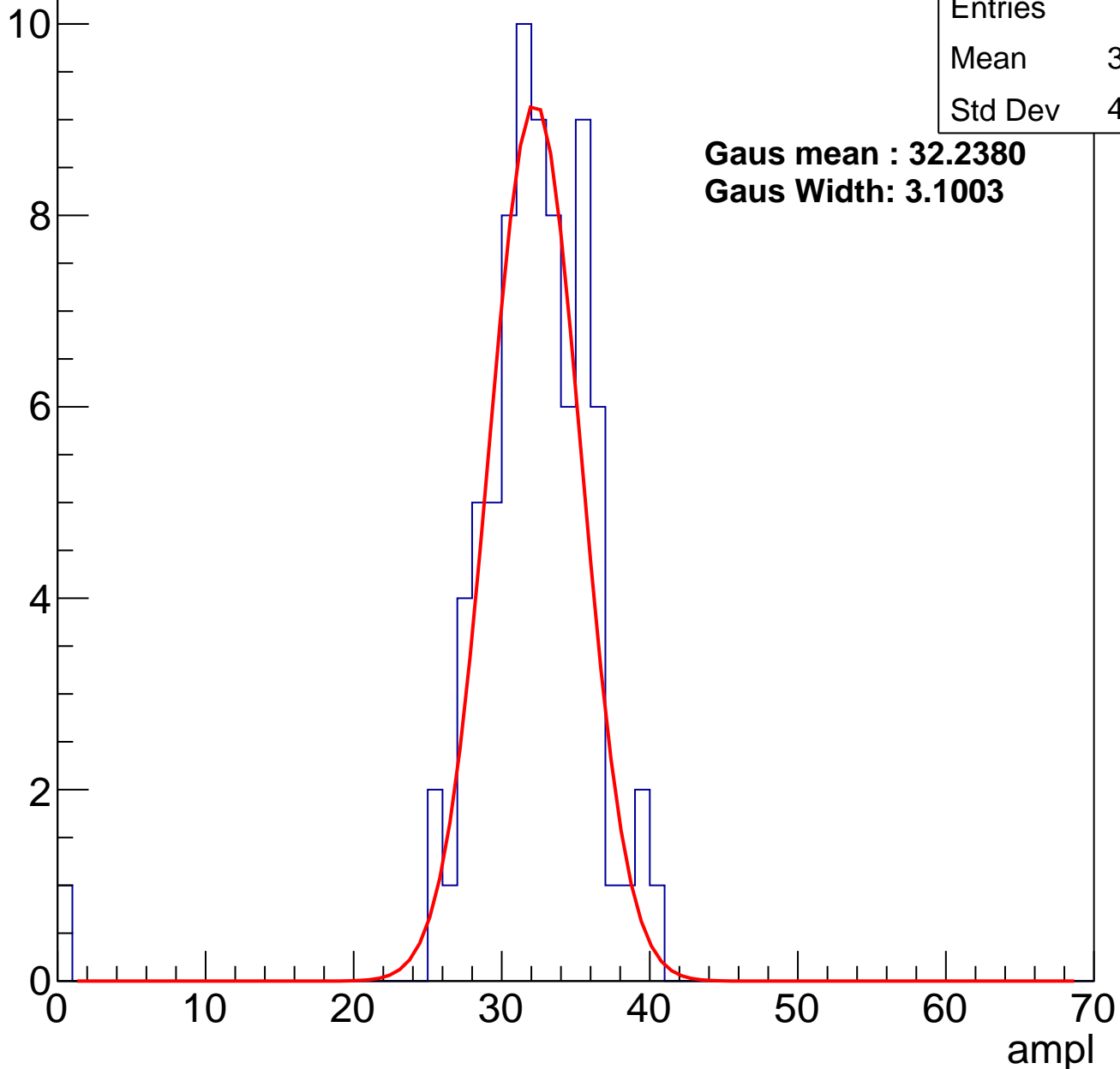
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	31.63
Std Dev	4.834

Gaus mean : 32.2380

Gaus Width: 3.1003

Entry



B0L002S, U26-ch10, adc1

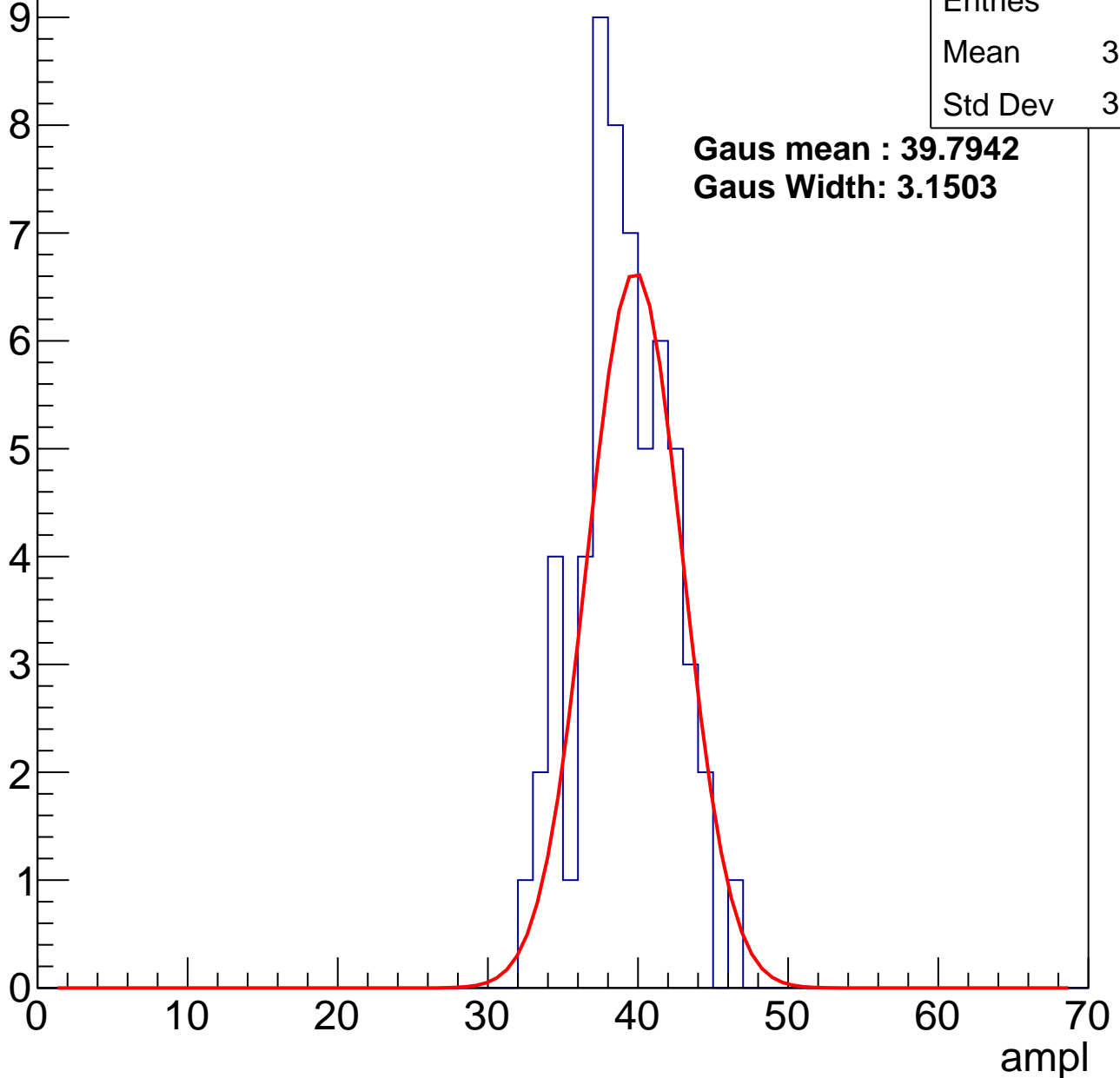
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	38.66
Std Dev	3.026

Gaus mean : 39.7942

Gaus Width: 3.1503



B0L002S, U26-ch10, adc2

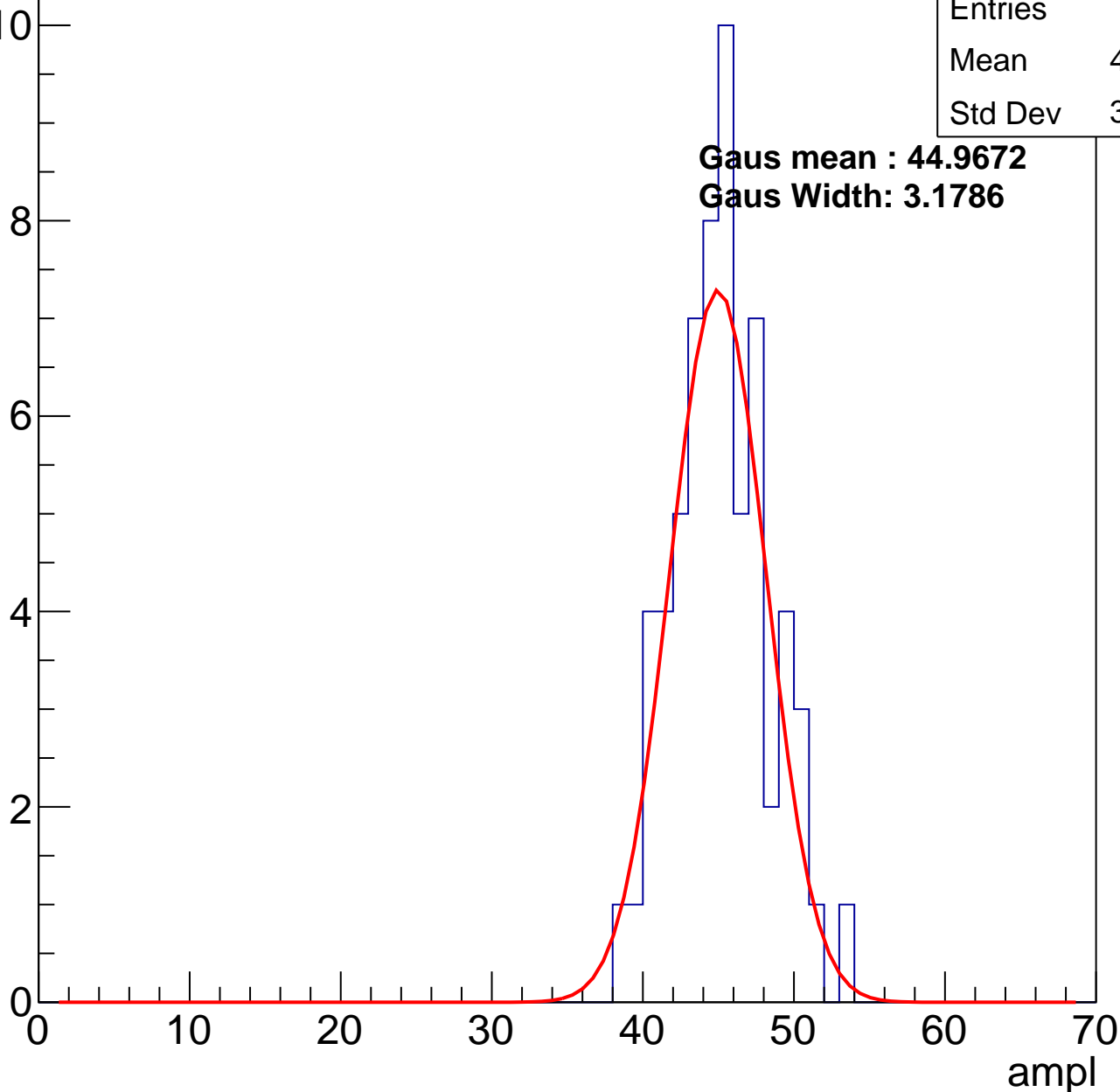
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.75
Std Dev	3.127

Gaus mean : 44.9672

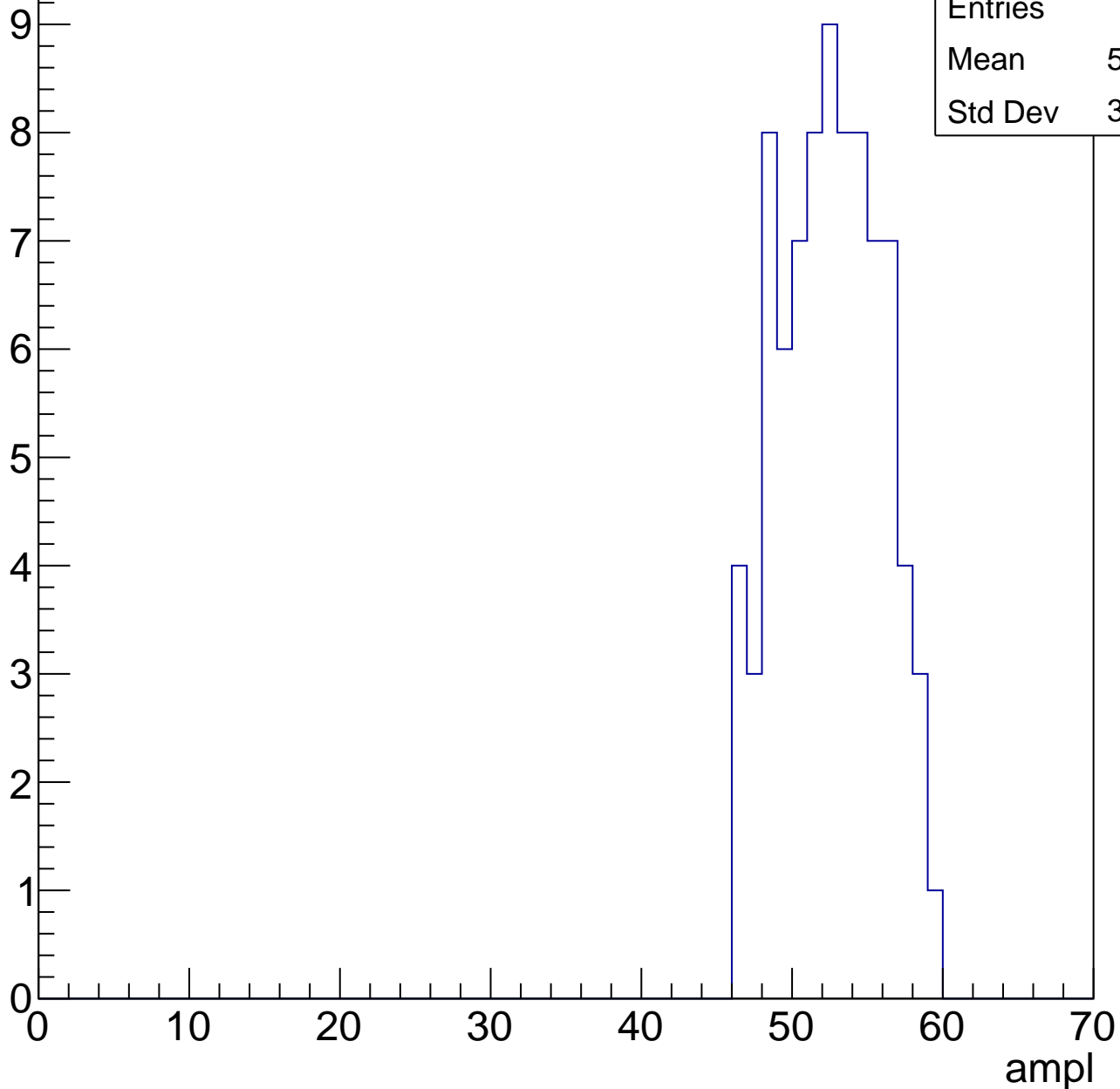
Gaus Width: 3.1786



B0L002S, U26-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



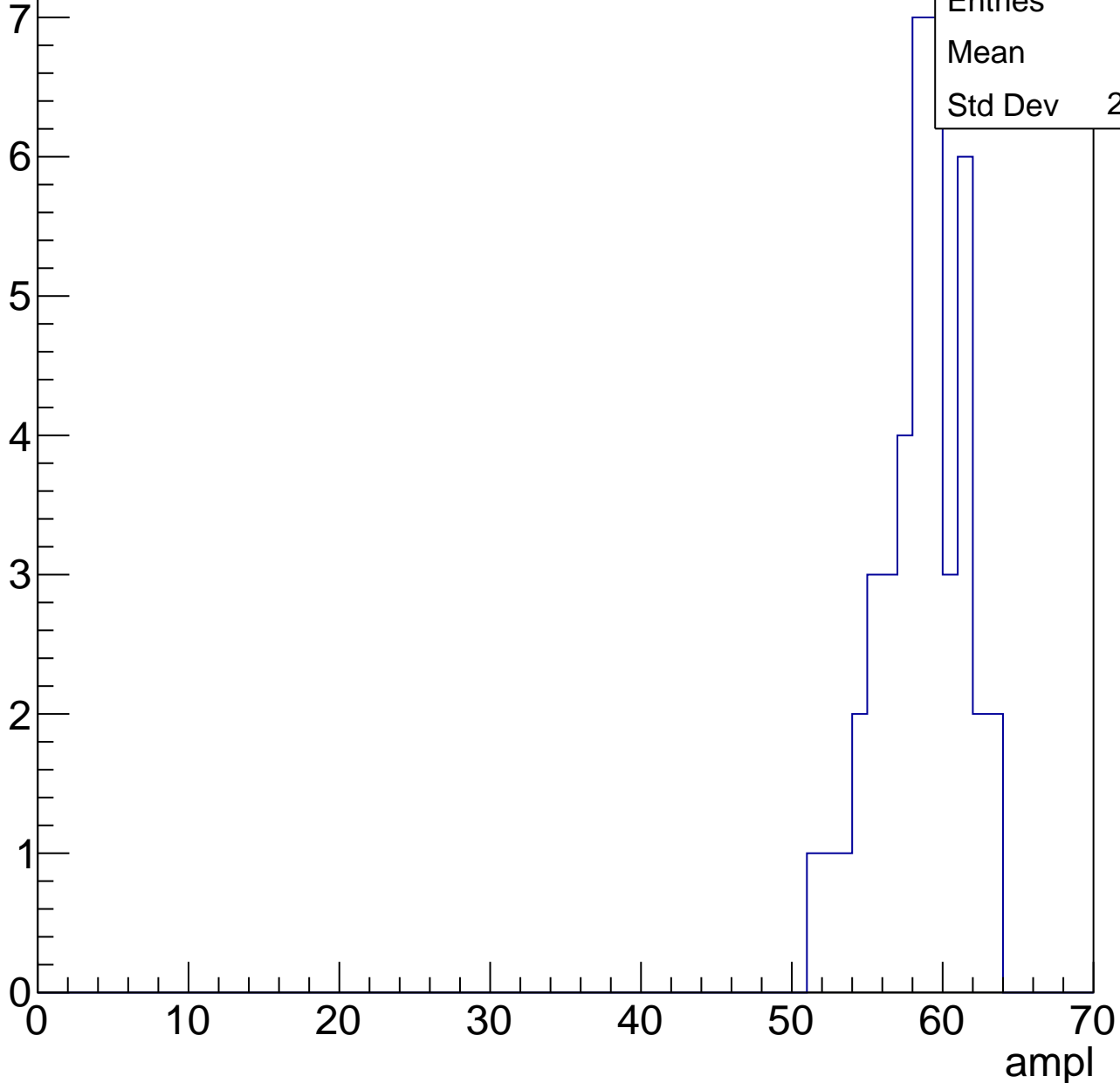
Entries	83
Mean	52.08
Std Dev	3.308

B0L002S, U26-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

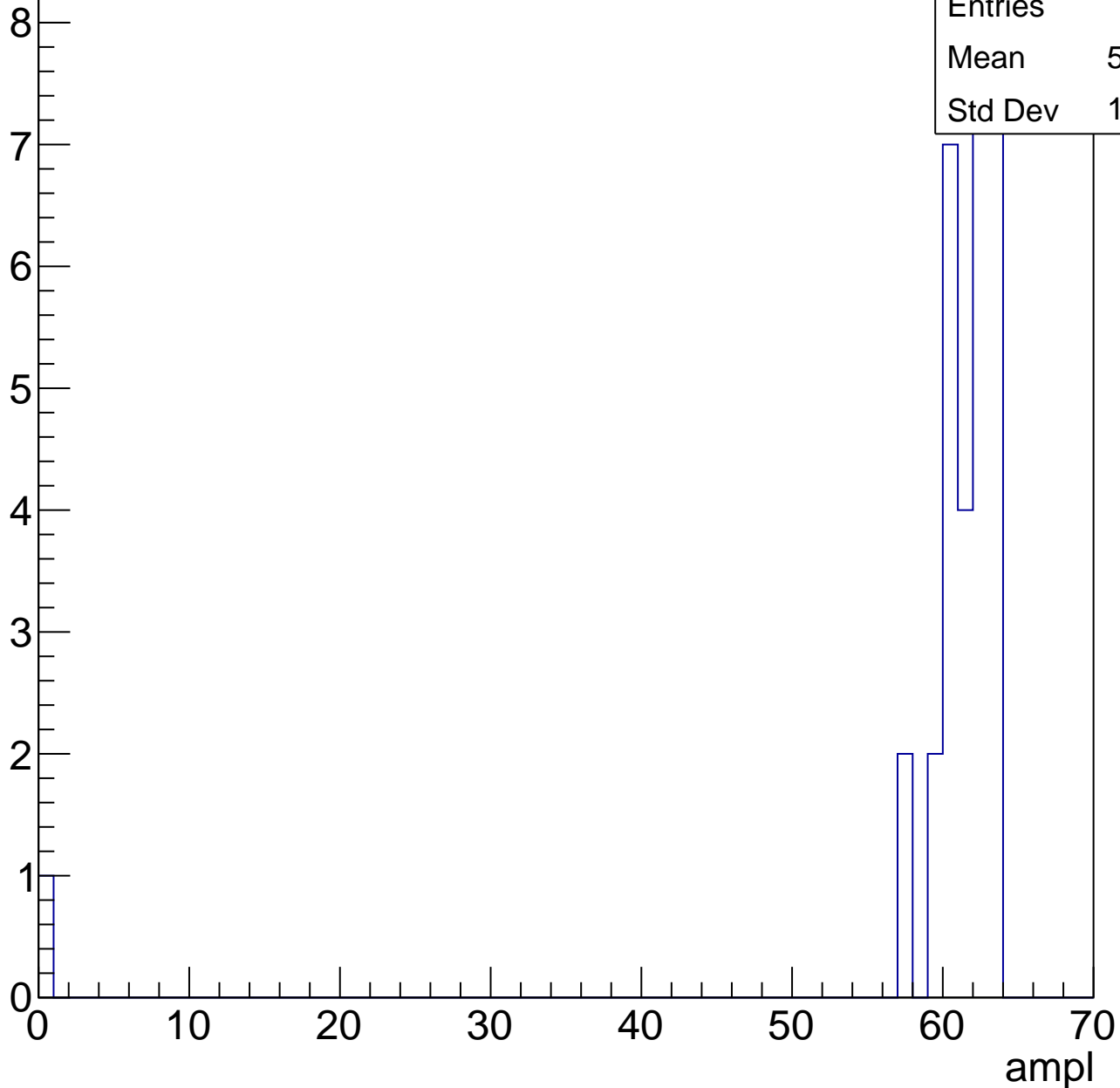
Entries	42
Mean	58.1
Std Dev	2.844



B0L002S, U26-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch11, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	28.15
Std Dev	5.688

Gaus mean : 29.5060

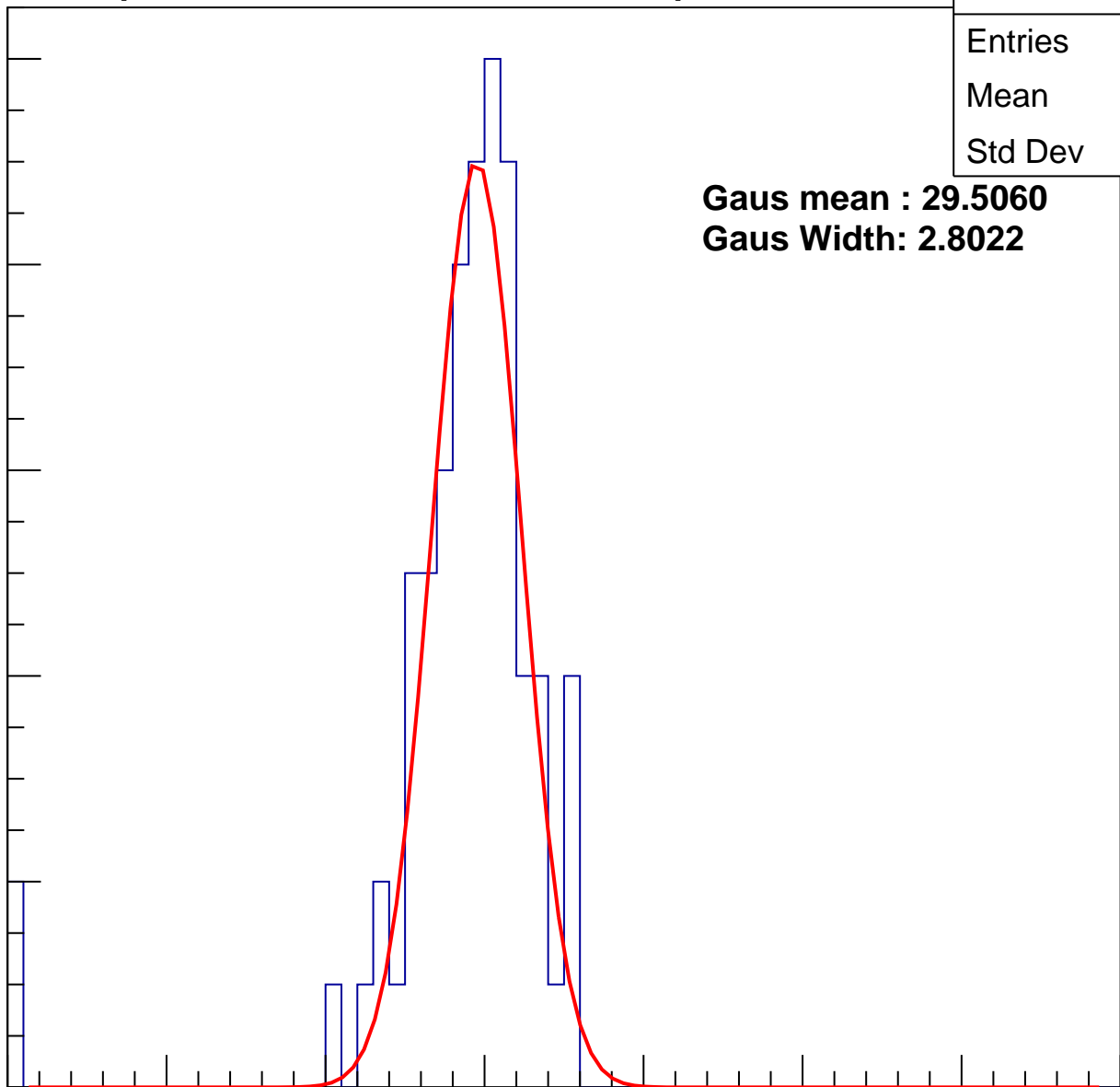
Gaus Width: 2.8022

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

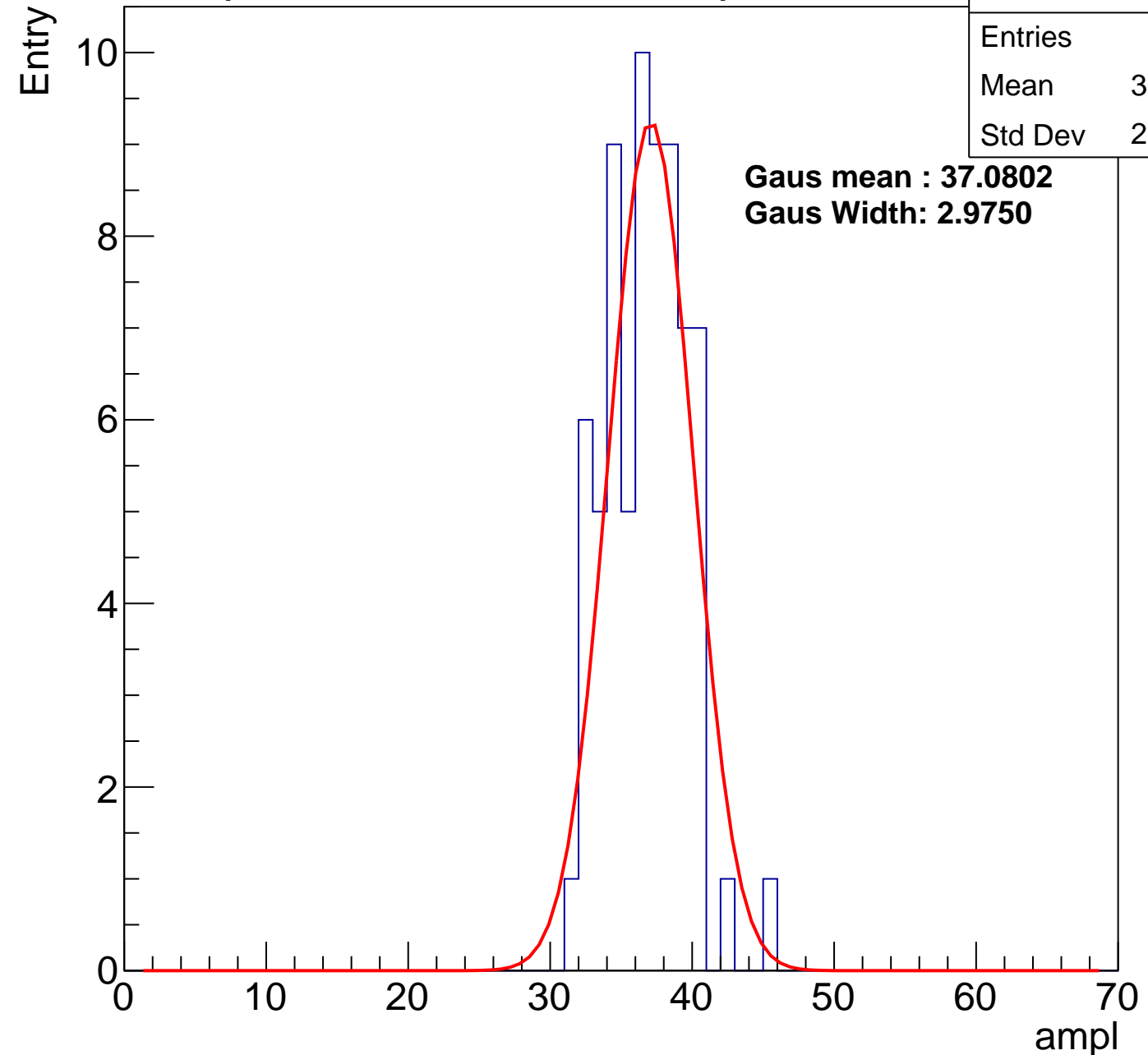


B0L002S, U26-ch11, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	36.34
Std Dev	2.767

Gaus mean : 37.0802
Gaus Width: 2.9750



B0L002S, U26-ch11, adc2

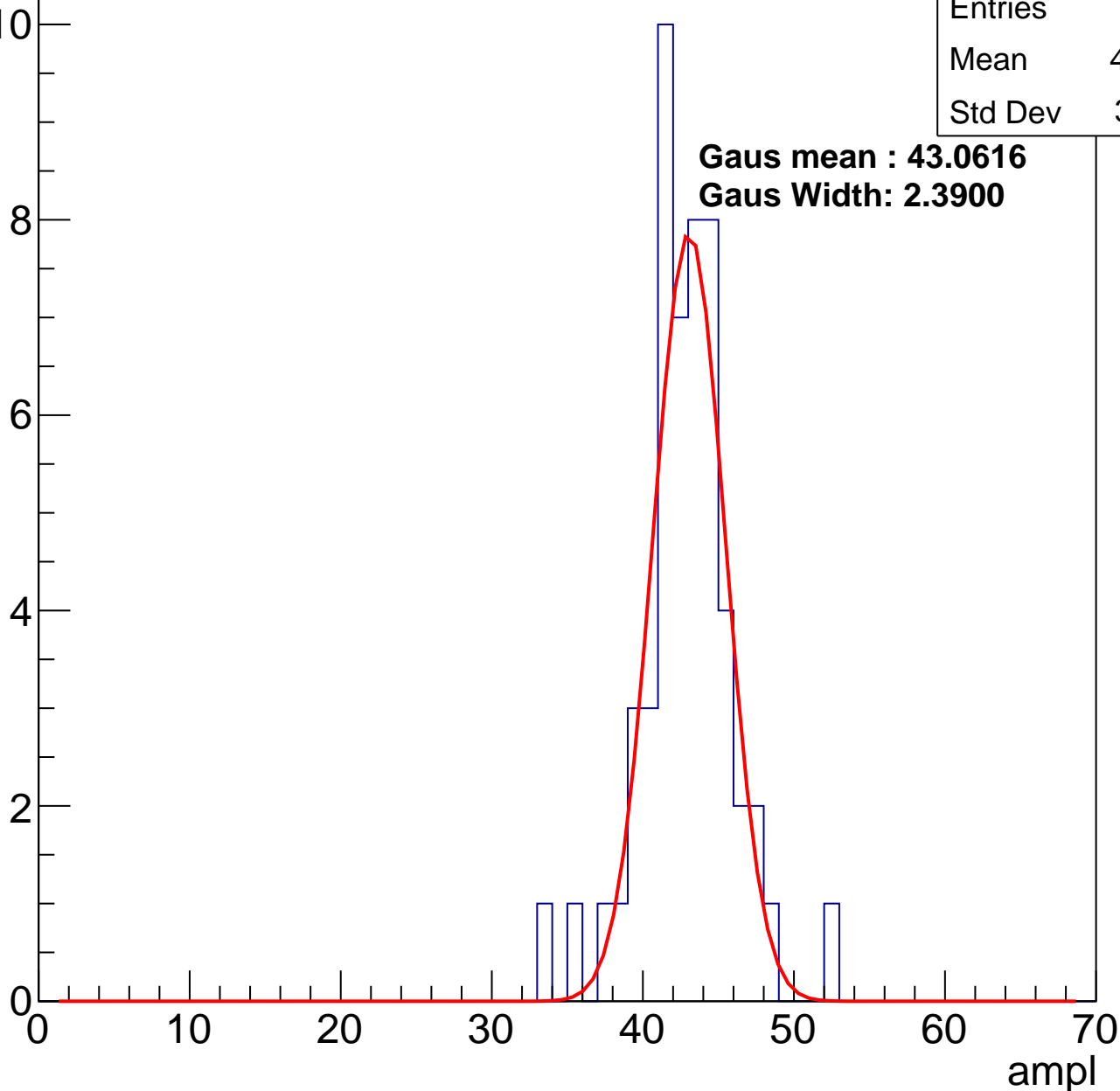
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	42.38
Std Dev	3.091

Gaus mean : 43.0616

Gaus Width: 2.3900



B0L002S, U26-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

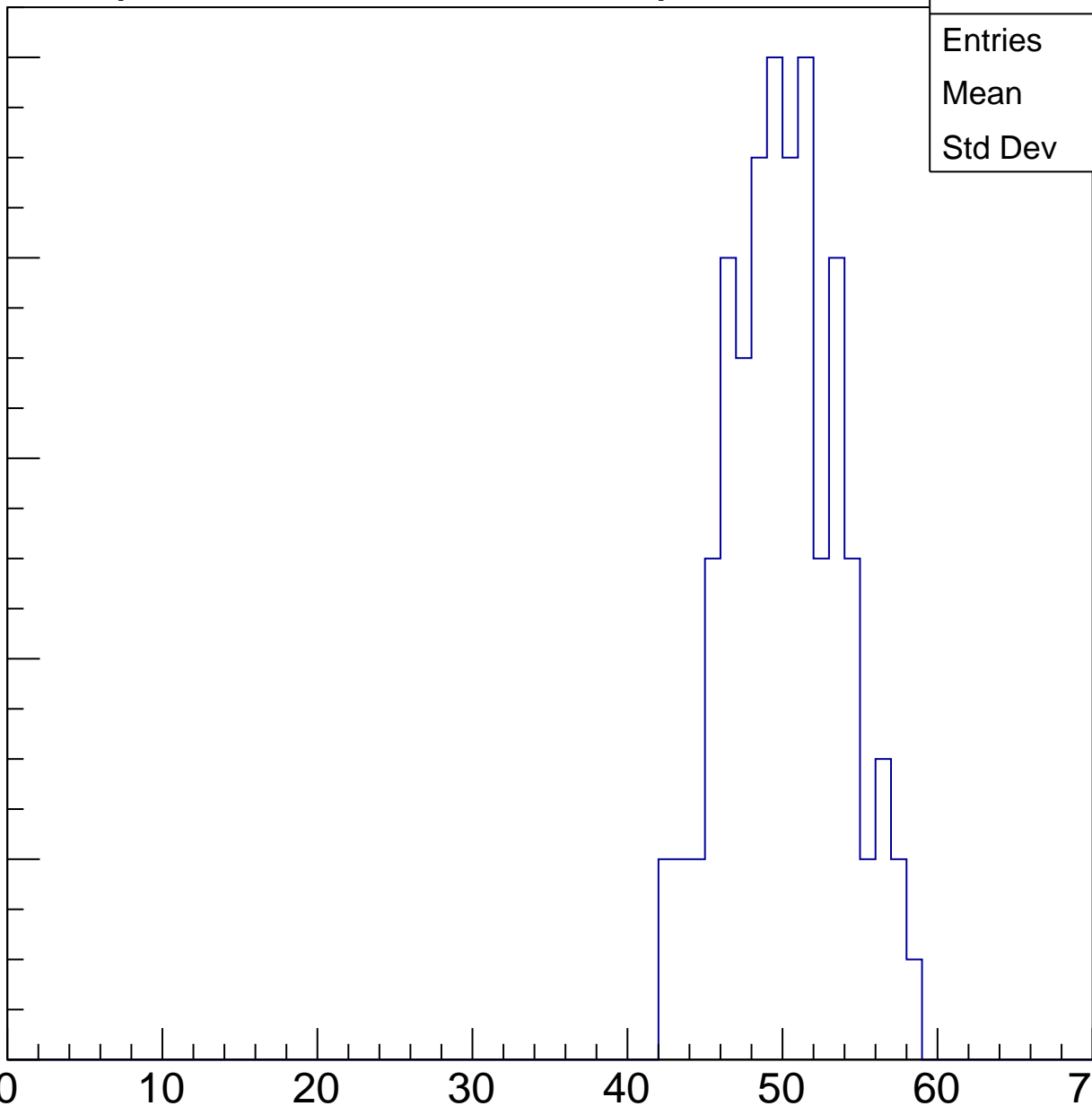
Entries	90
Mean	49.62
Std Dev	3.573

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

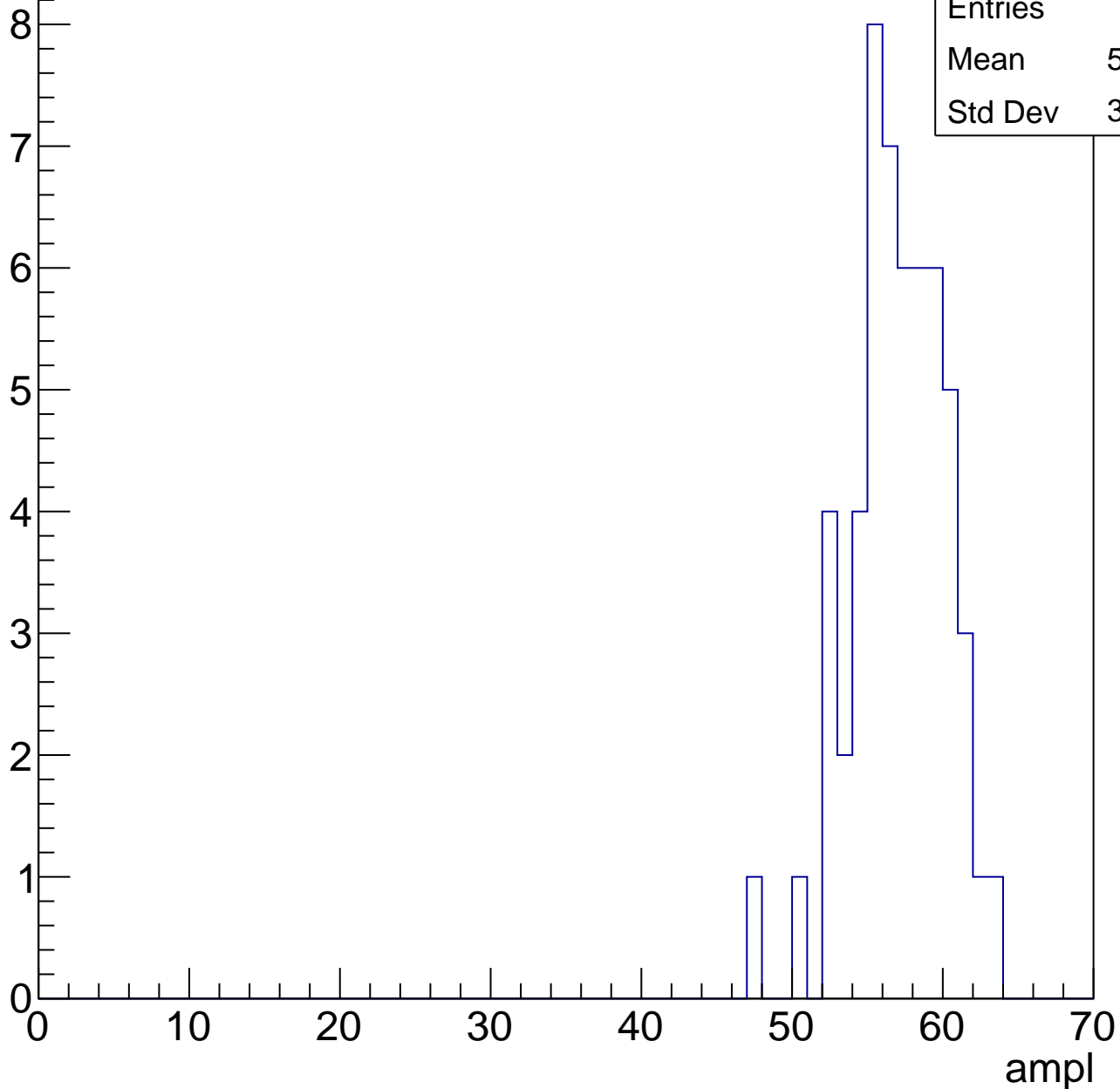


B0L002S, U26-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.56
Std Dev	3.103

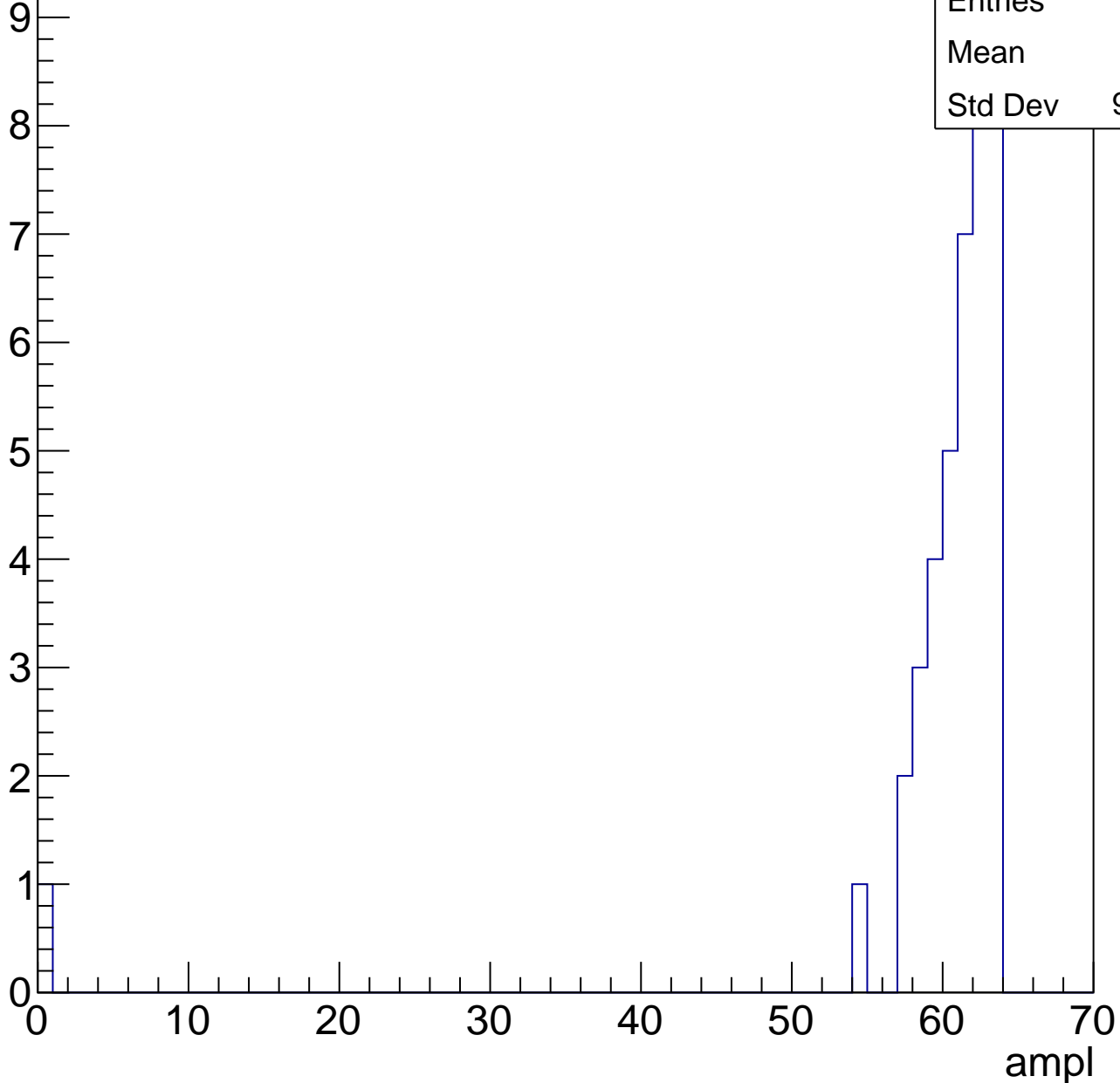


B0L002S, U26-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.2
Std Dev	9.701



B0L002S, U26-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch12, adc0

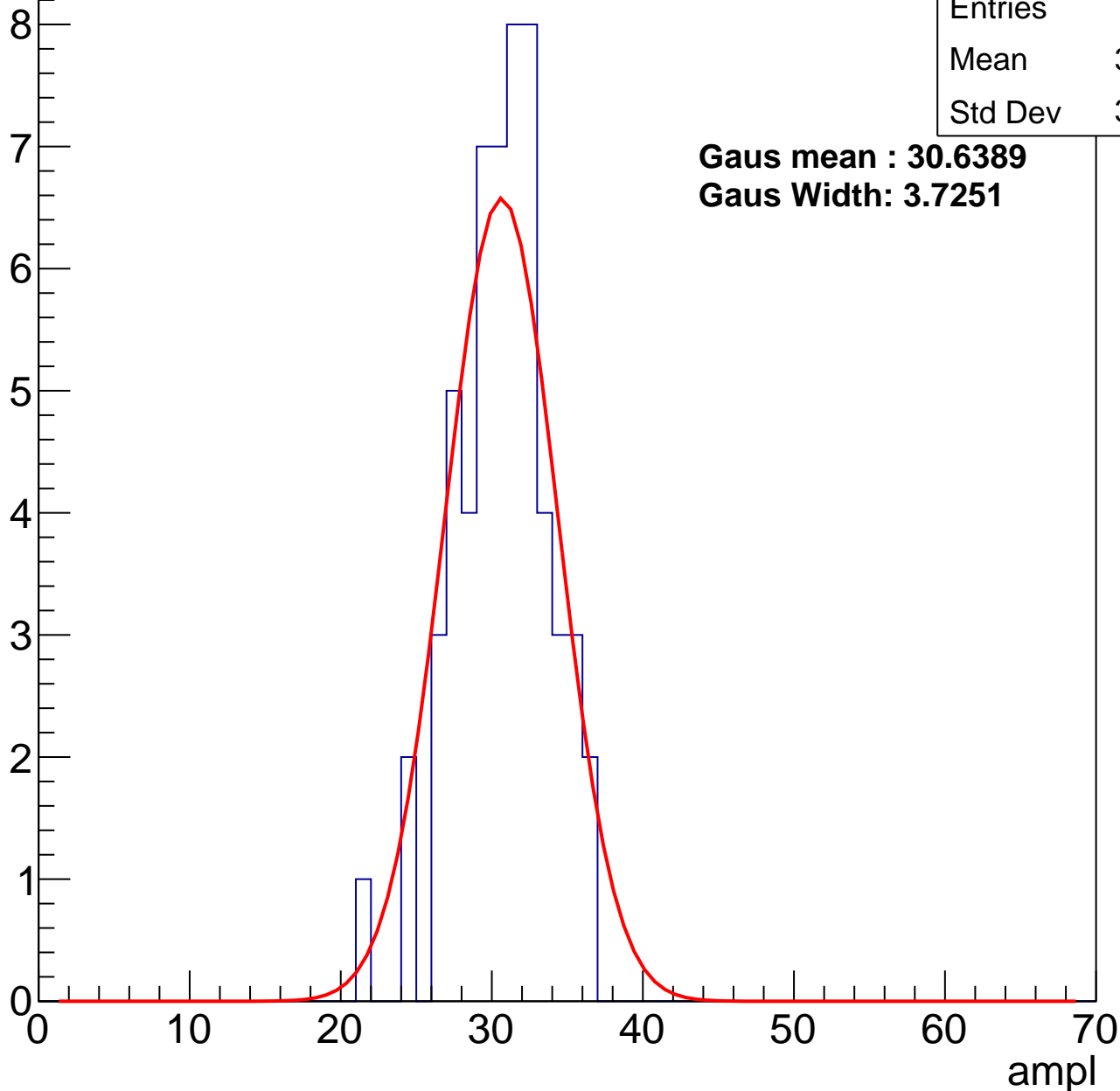
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	30.21
Std Dev	3.071

Gaus mean : 30.6389

Gaus Width: 3.7251

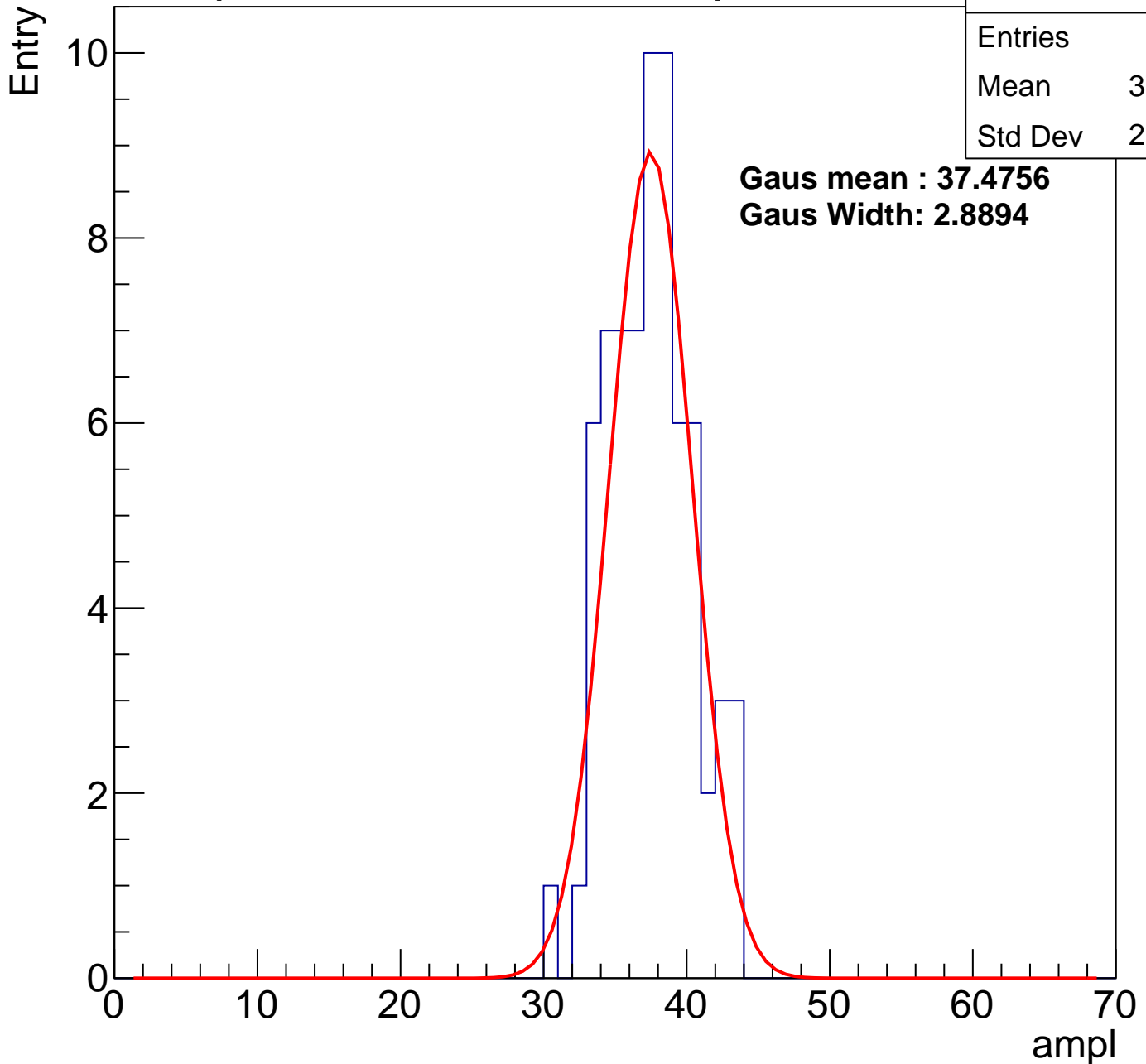


B0L002S, U26-ch12, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	37.04
Std Dev	2.876

Gaus mean : 37.4756
Gaus Width: 2.8894



B0L002S, U26-ch12, adc2

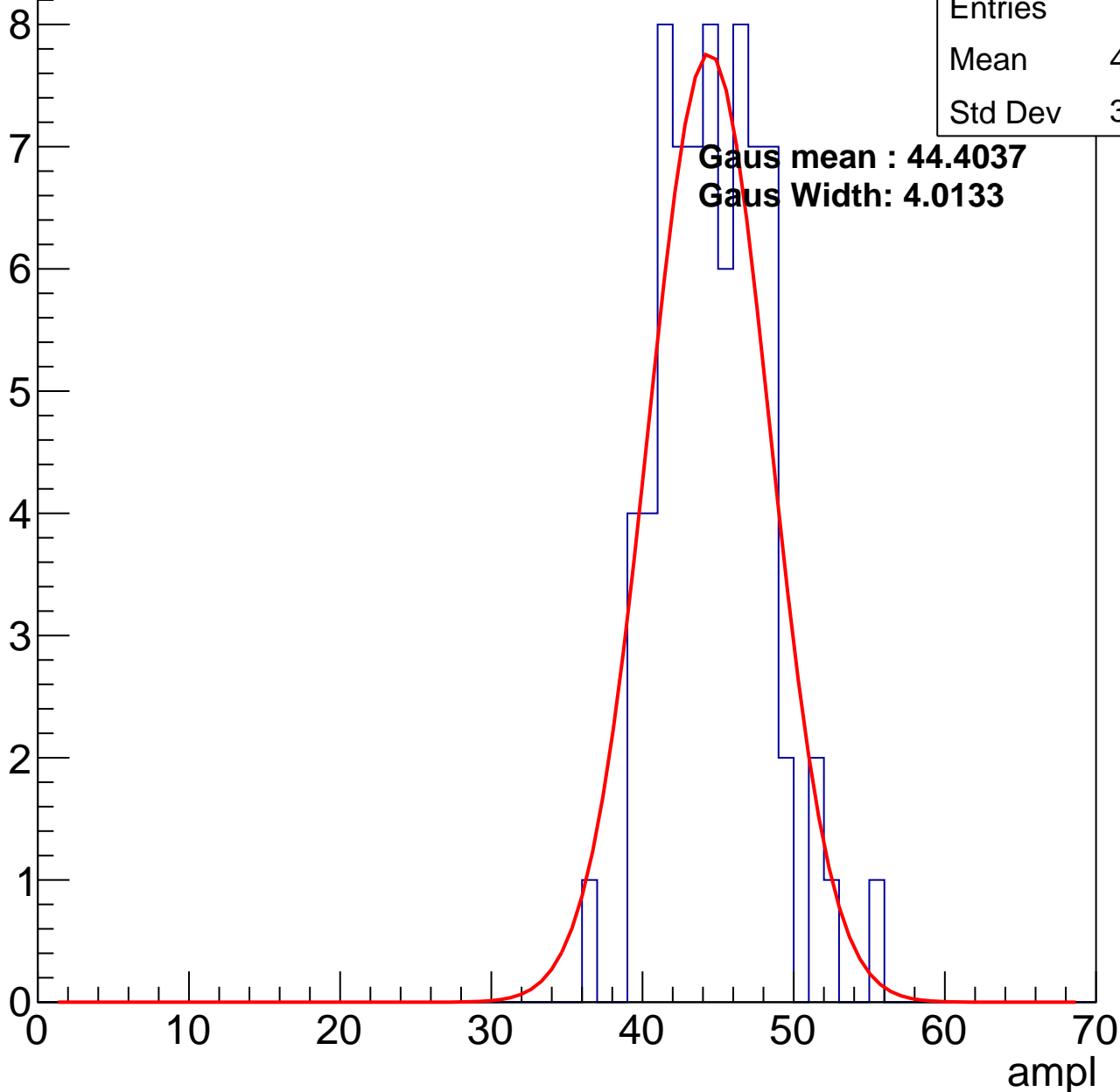
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.34
Std Dev	3.457

Gaus mean : 44.4037

Gaus Width: 4.0133

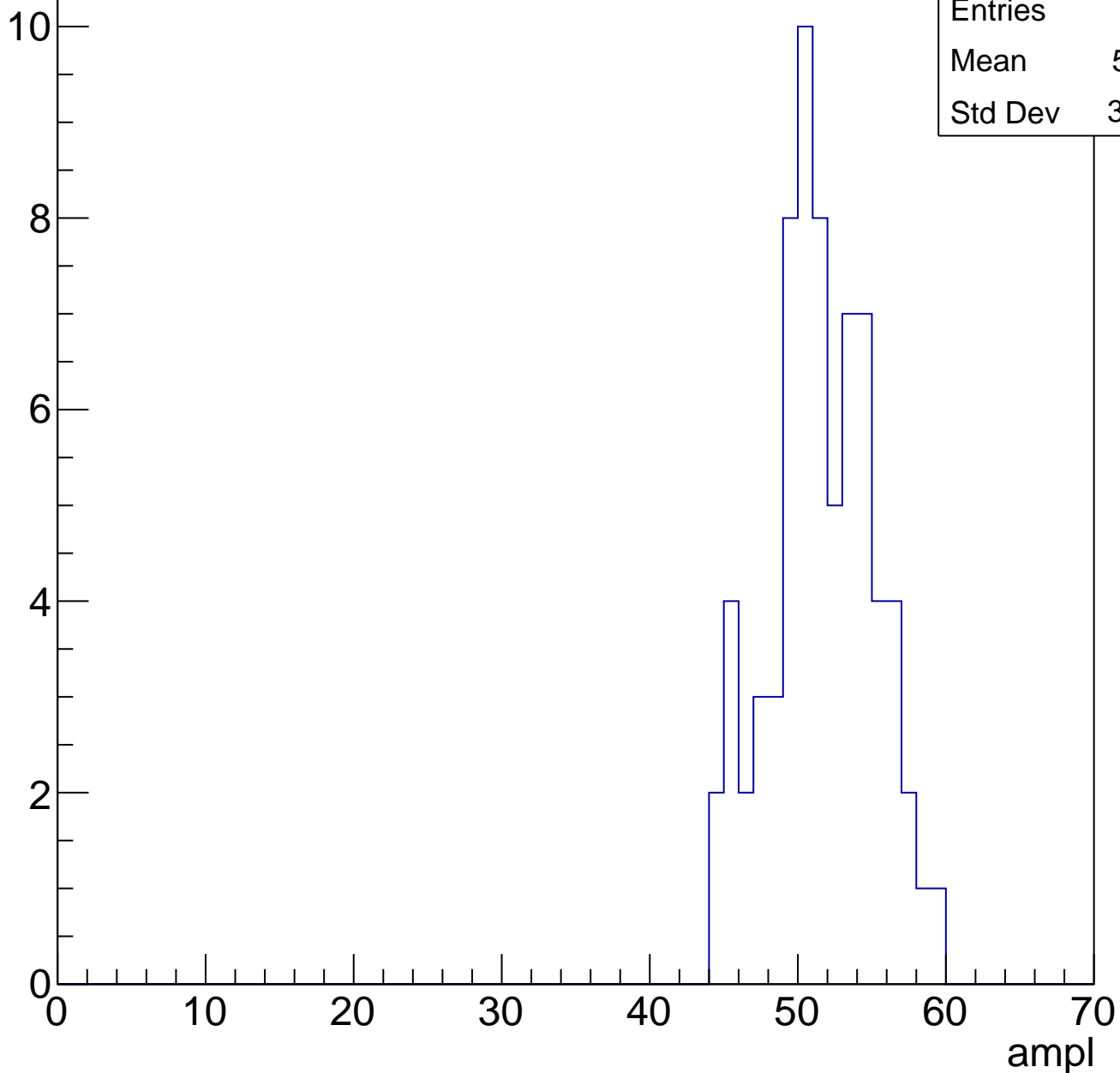


B0L002S, U26-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	51.11
Std Dev	3.466

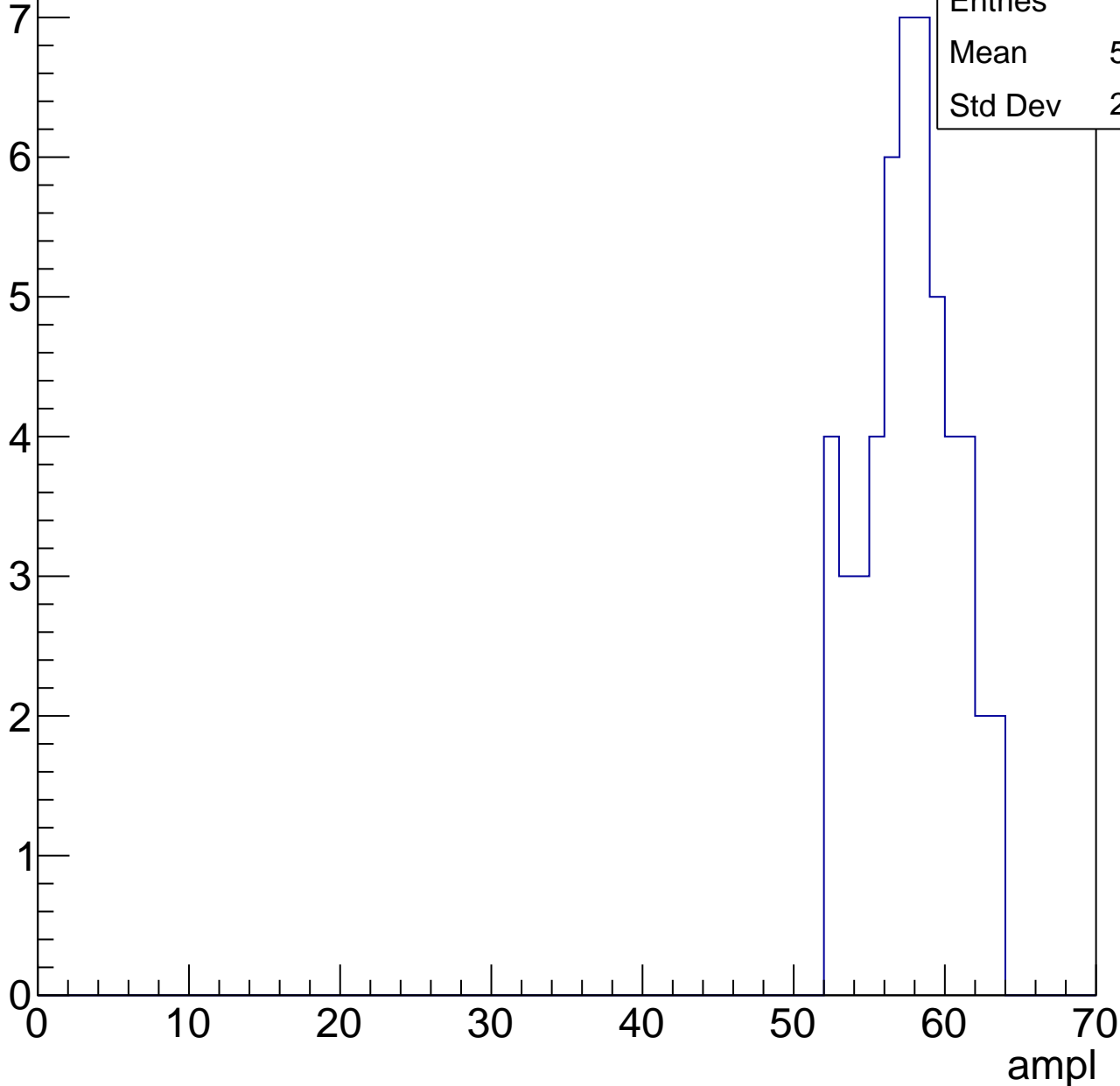


B0L002S, U26-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57.24
Std Dev	2.948

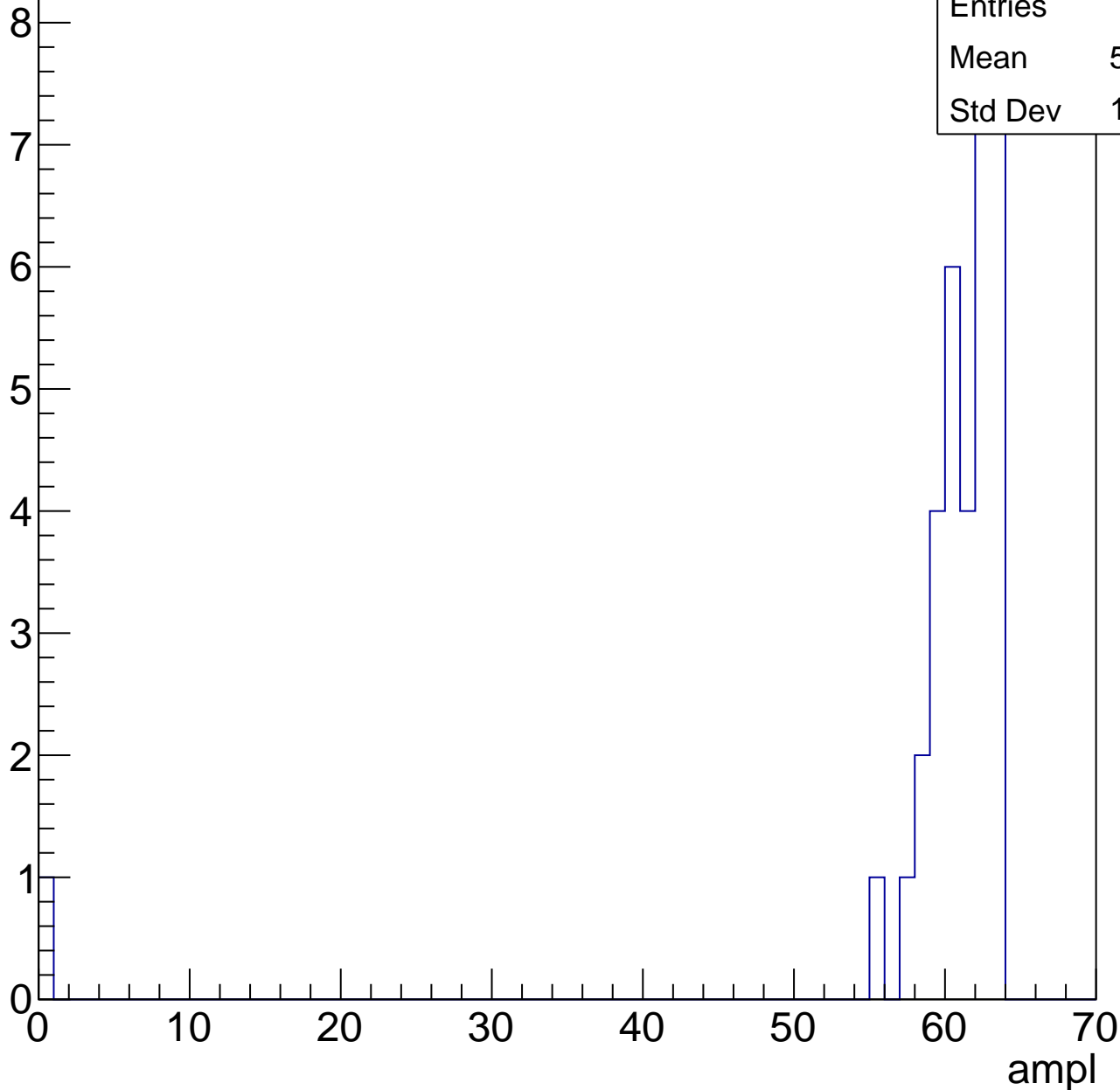


B0L002S, U26-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

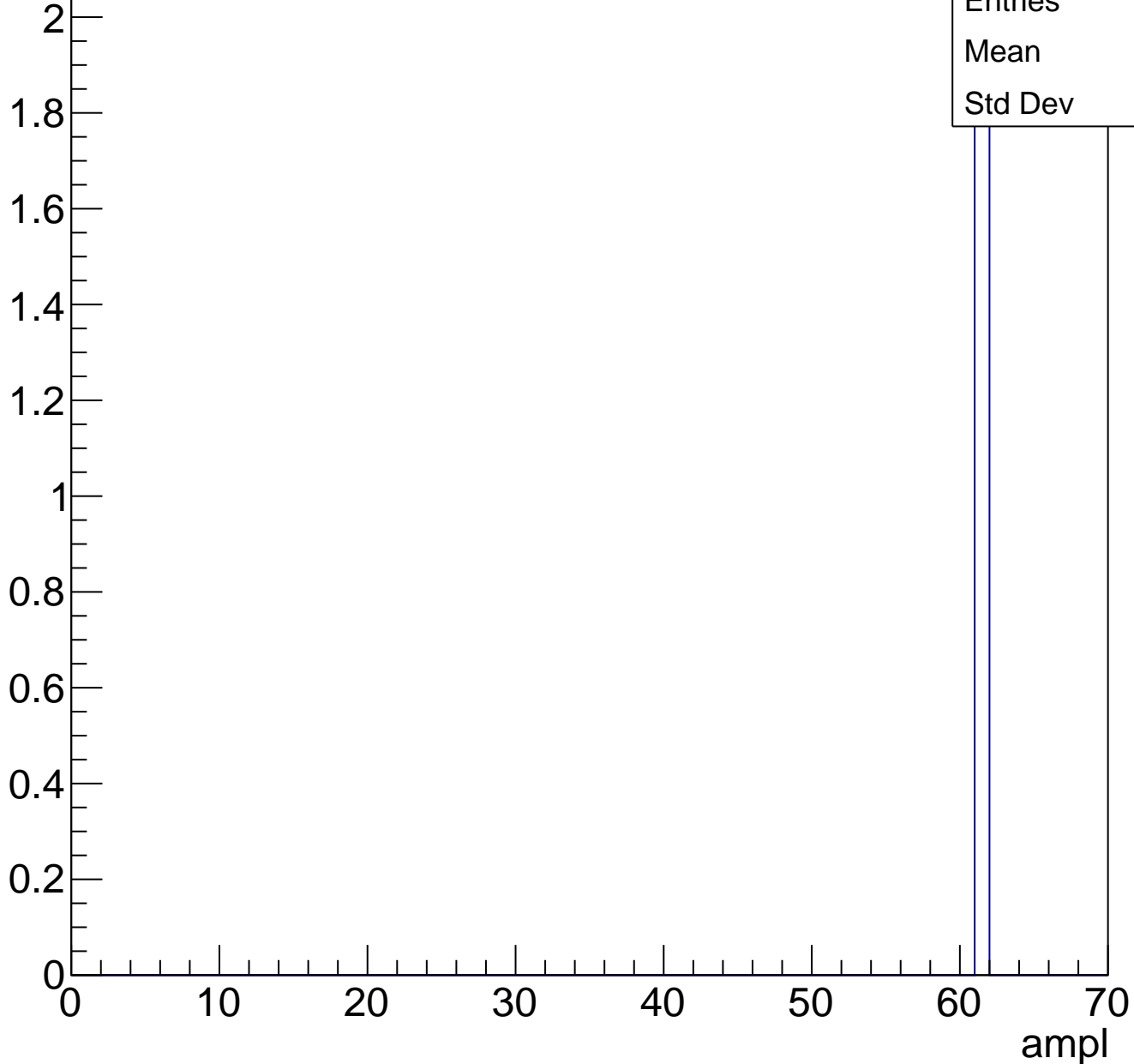
Entries	35
Mean	59.09
Std Dev	10.32



B0L002S, U26-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch13, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	90
Mean	31.38
Std Dev	3.764

Gaus mean : 32.1712

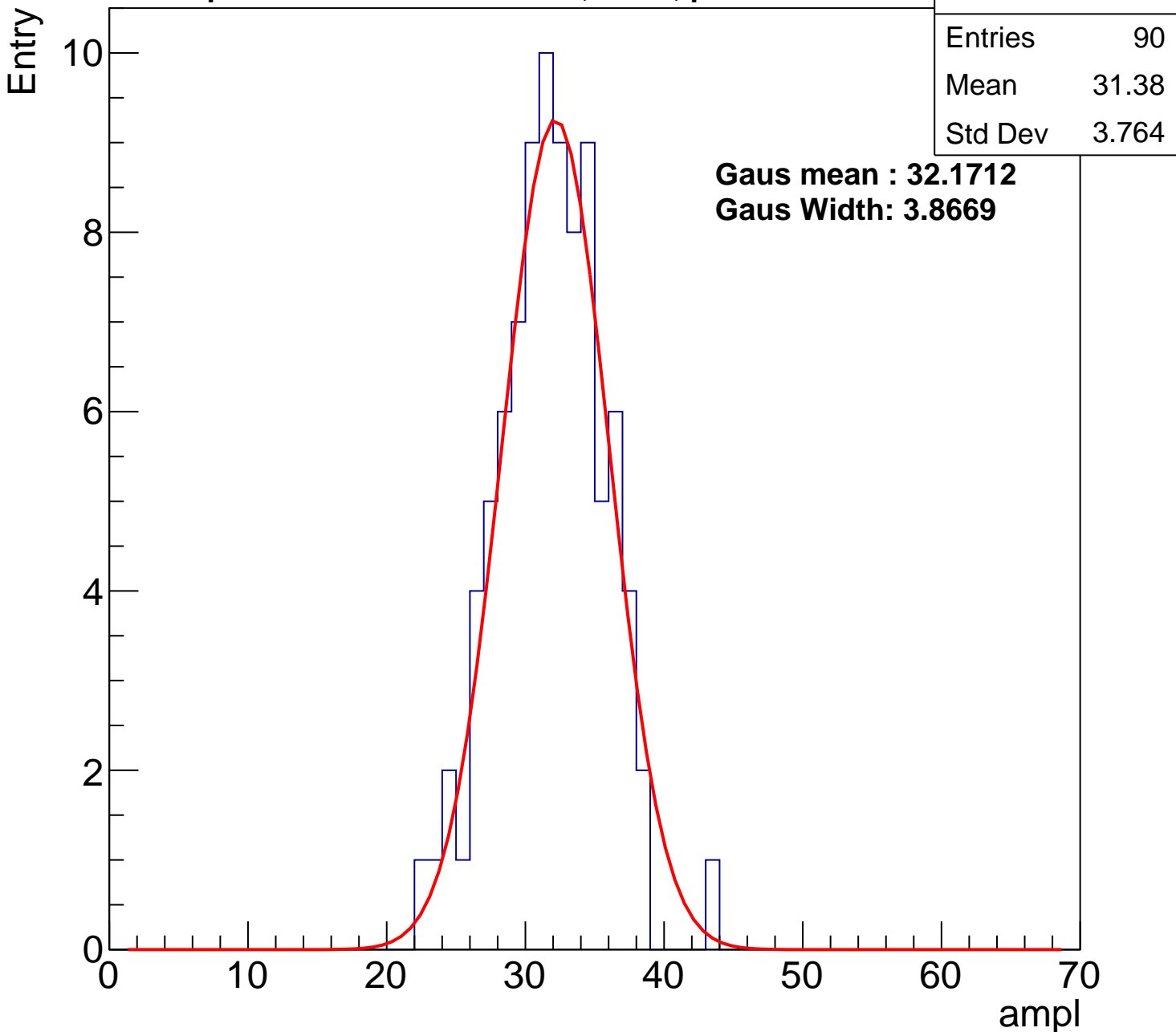
Gaus Width: 3.8669

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



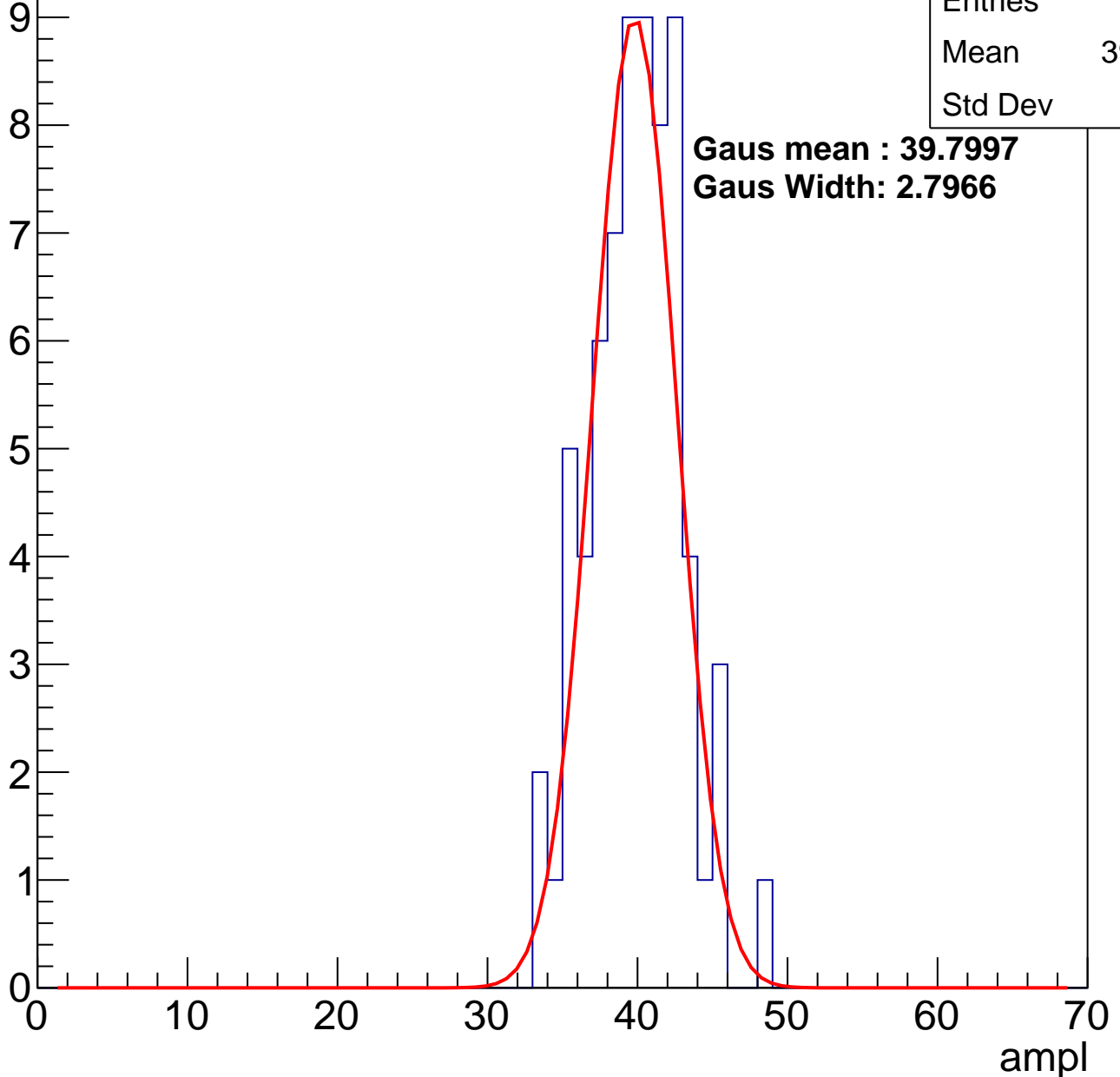
B0L002S, U26-ch13, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	39.46
Std Dev	3.02

Gaus mean : 39.7997
Gaus Width: 2.7966



B0L002S, U26-ch13, adc2

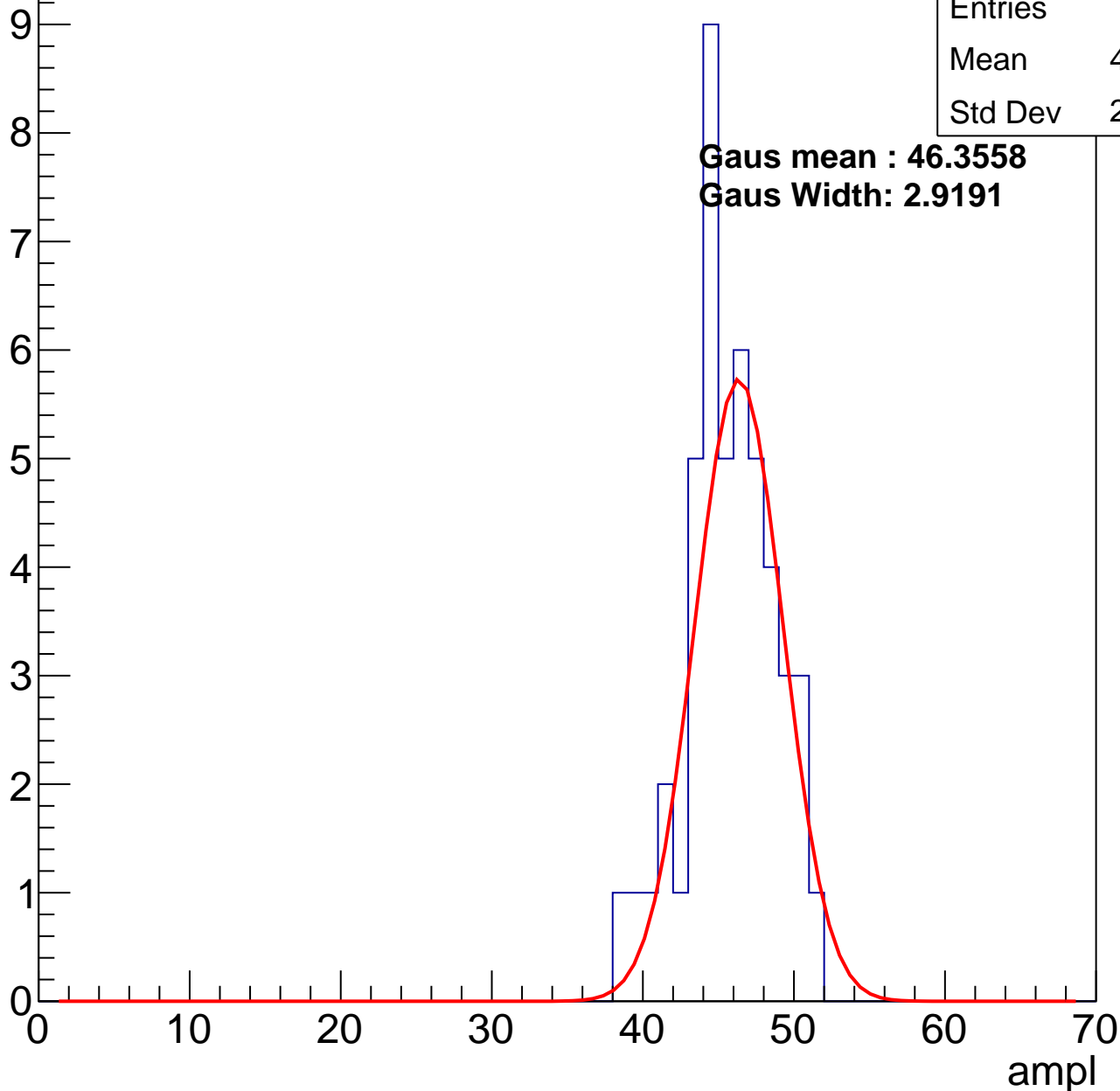
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	45.28
Std Dev	2.908

Gaus mean : 46.3558

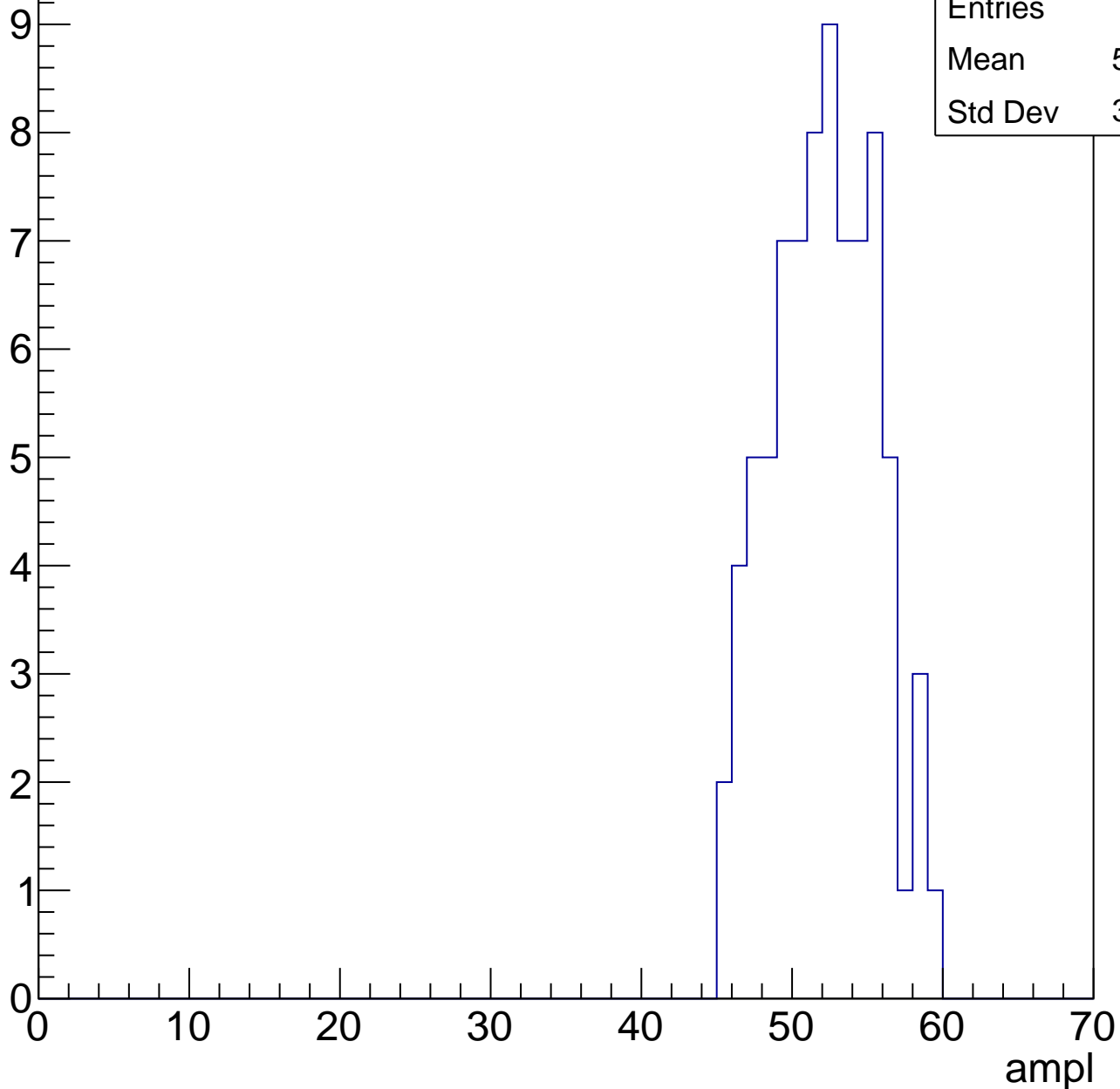
Gaus Width: 2.9191



B0L002S, U26-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

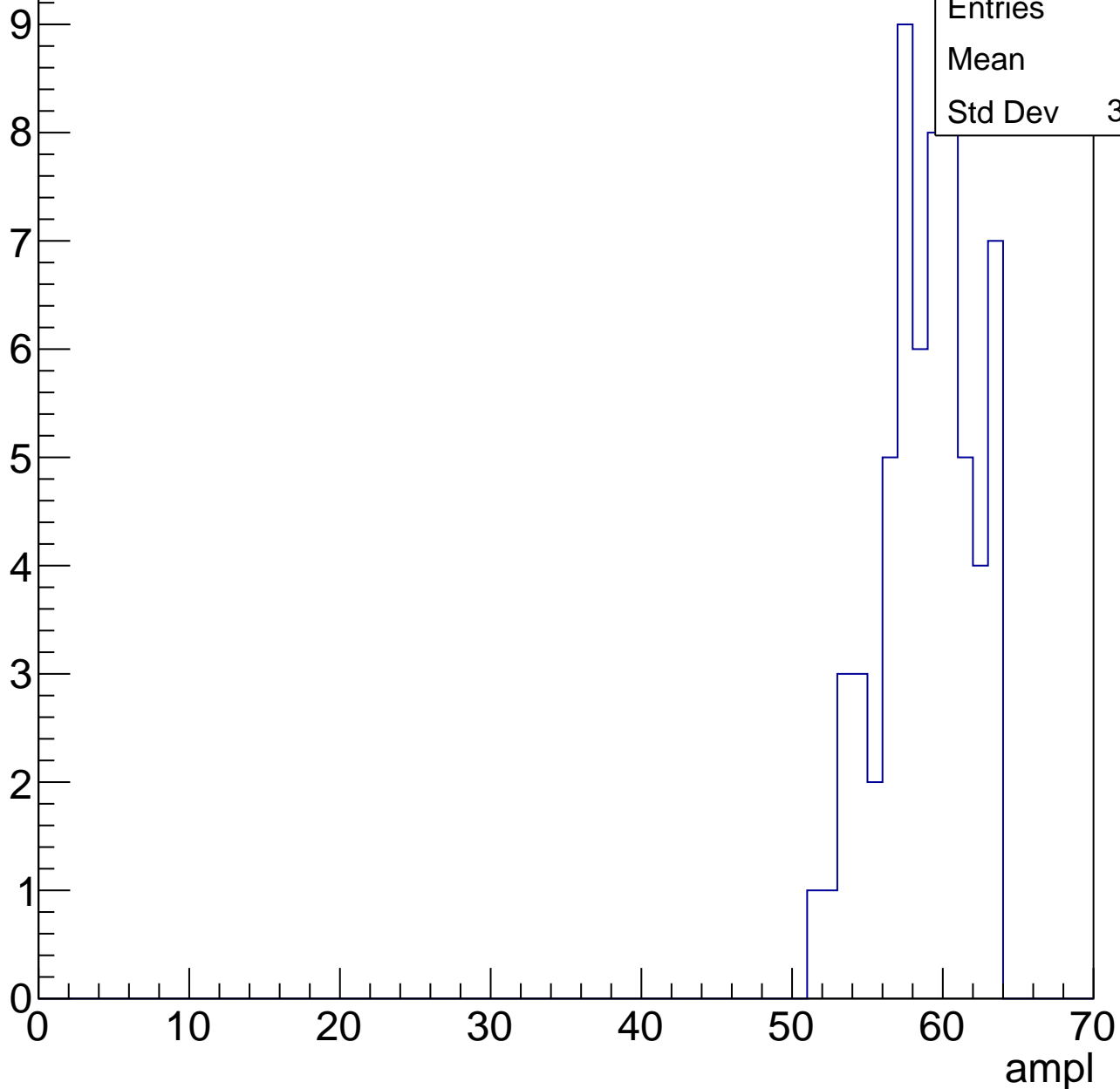
Entry



B0L002S, U26-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



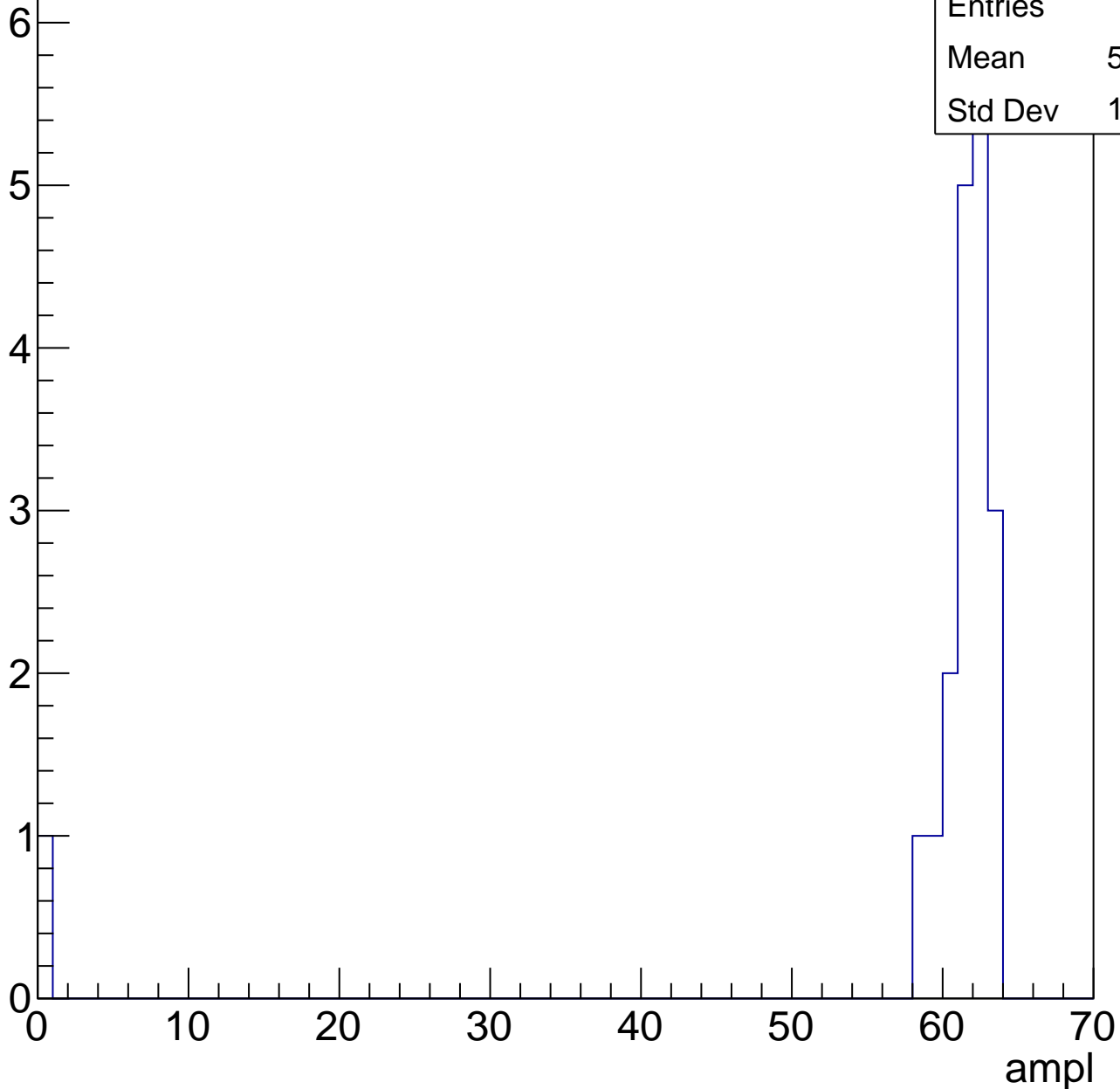
Entries	62
Mean	58.4
Std Dev	3.029

B0L002S, U26-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	58.05
Std Dev	13.74



B0L002S, U26-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch14, adc0

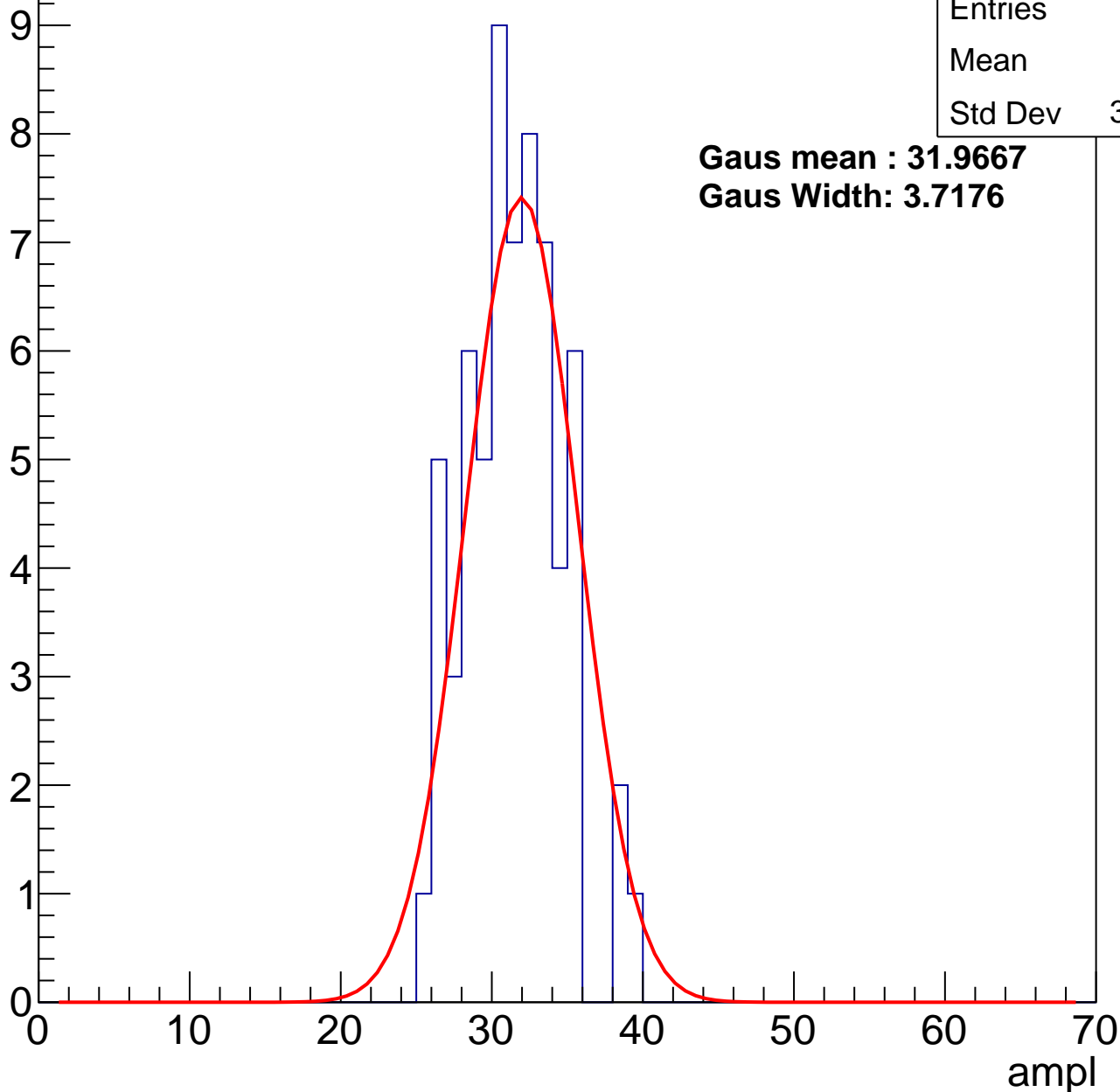
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31
Std Dev	3.117

Gaus mean : 31.9667

Gaus Width: 3.7176



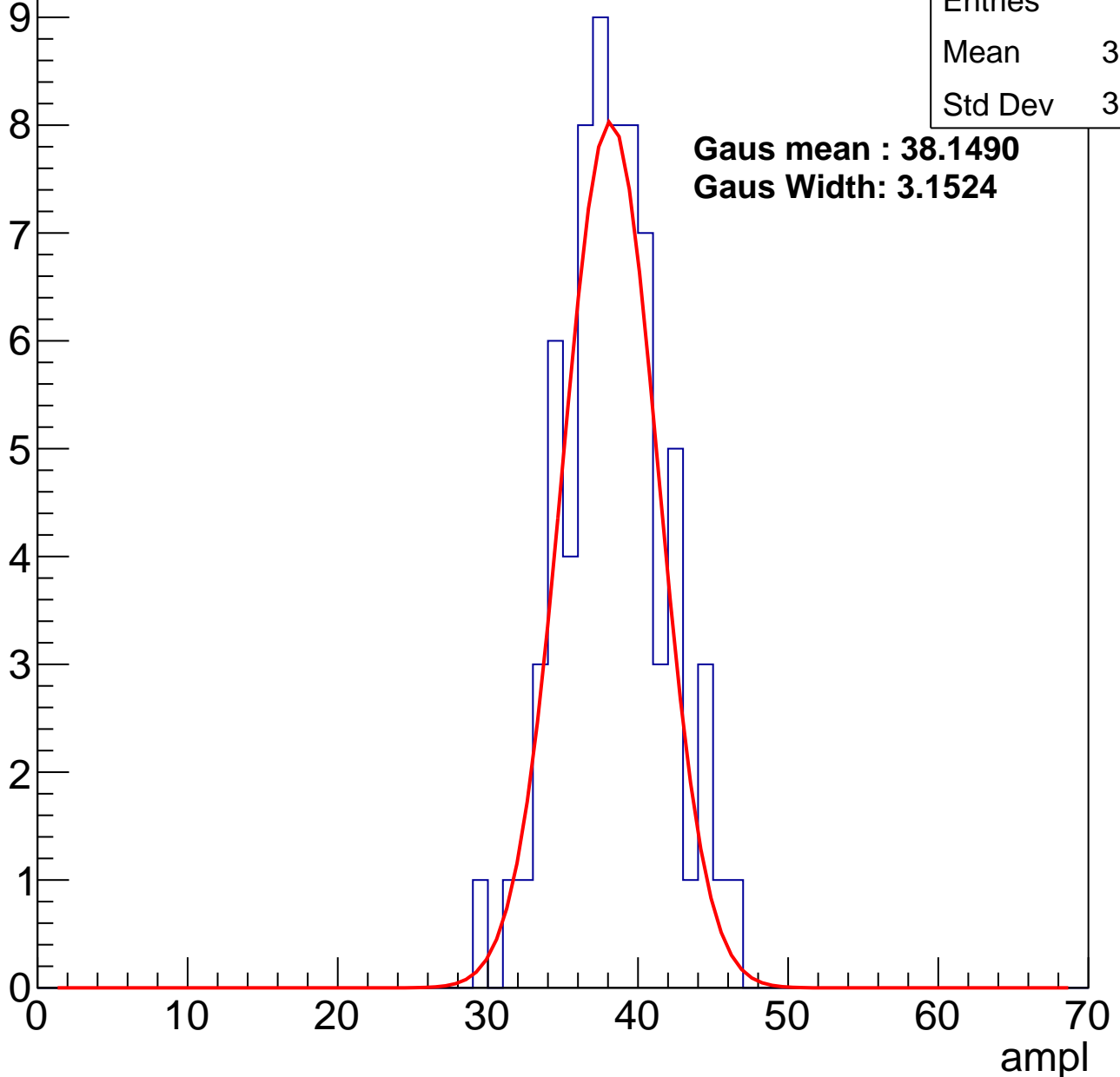
B0L002S, U26-ch14, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.87
Std Dev	3.385

Gaus mean : 38.1490
Gaus Width: 3.1524



B0L002S, U26-ch14, adc2

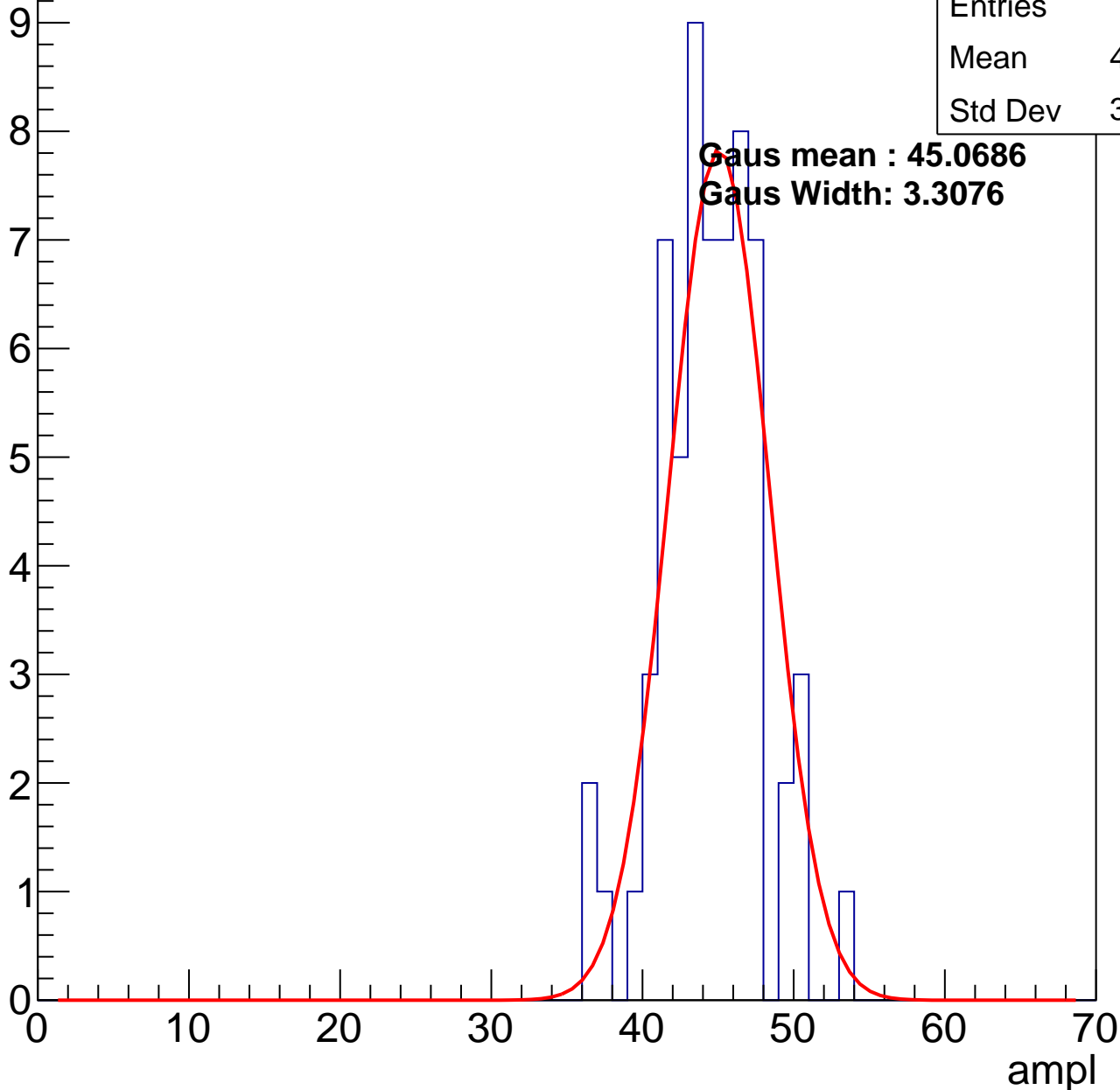
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.02
Std Dev	3.293

Gaus mean : 45.0686

Gaus Width: 3.3076

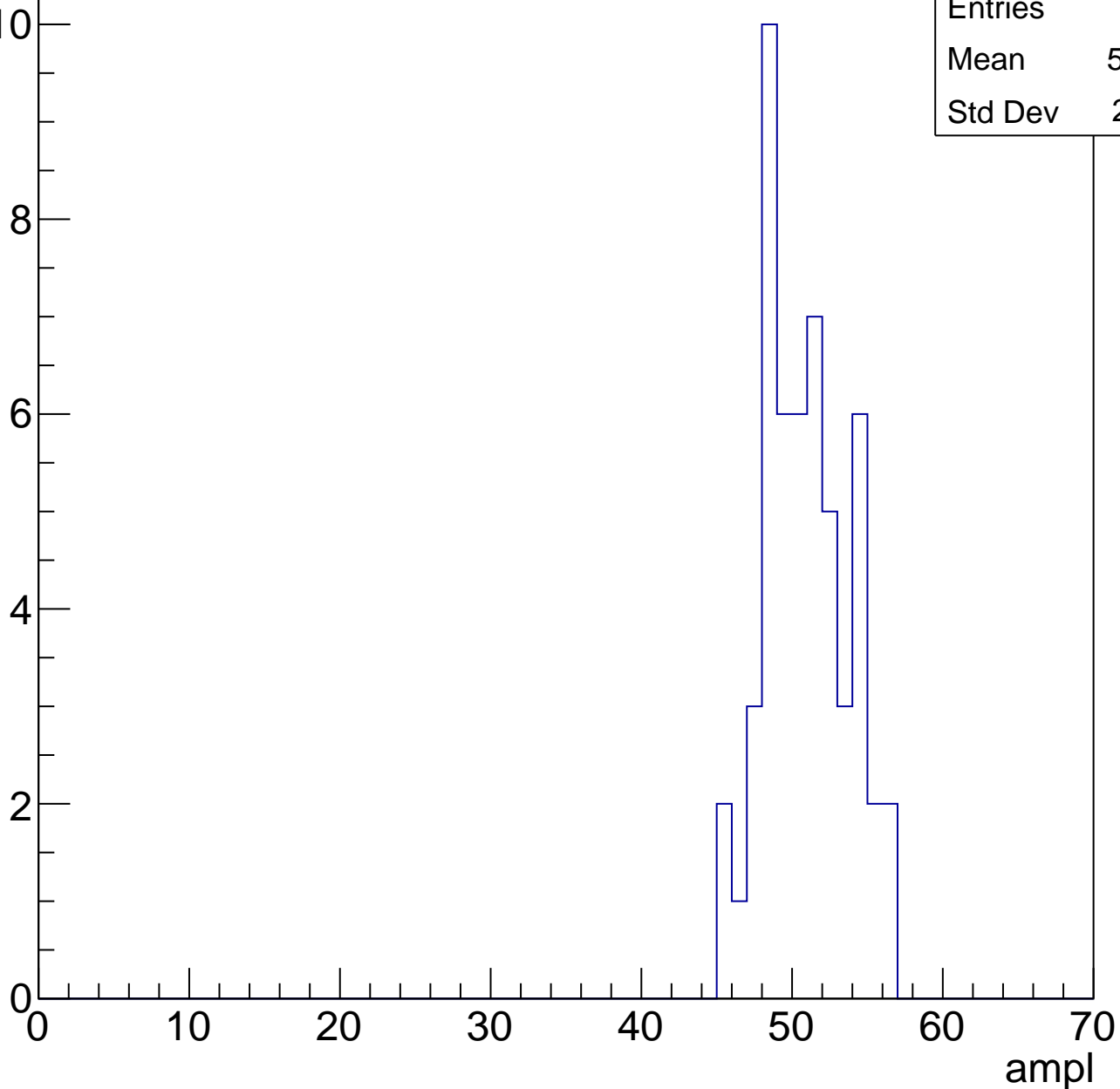


B0L002S, U26-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	50.43
Std Dev	2.751



B0L002S, U26-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	53
Mean	55.28
Std Dev	2.916

Entry

10

8

6

4

2

0

0

10

20

30

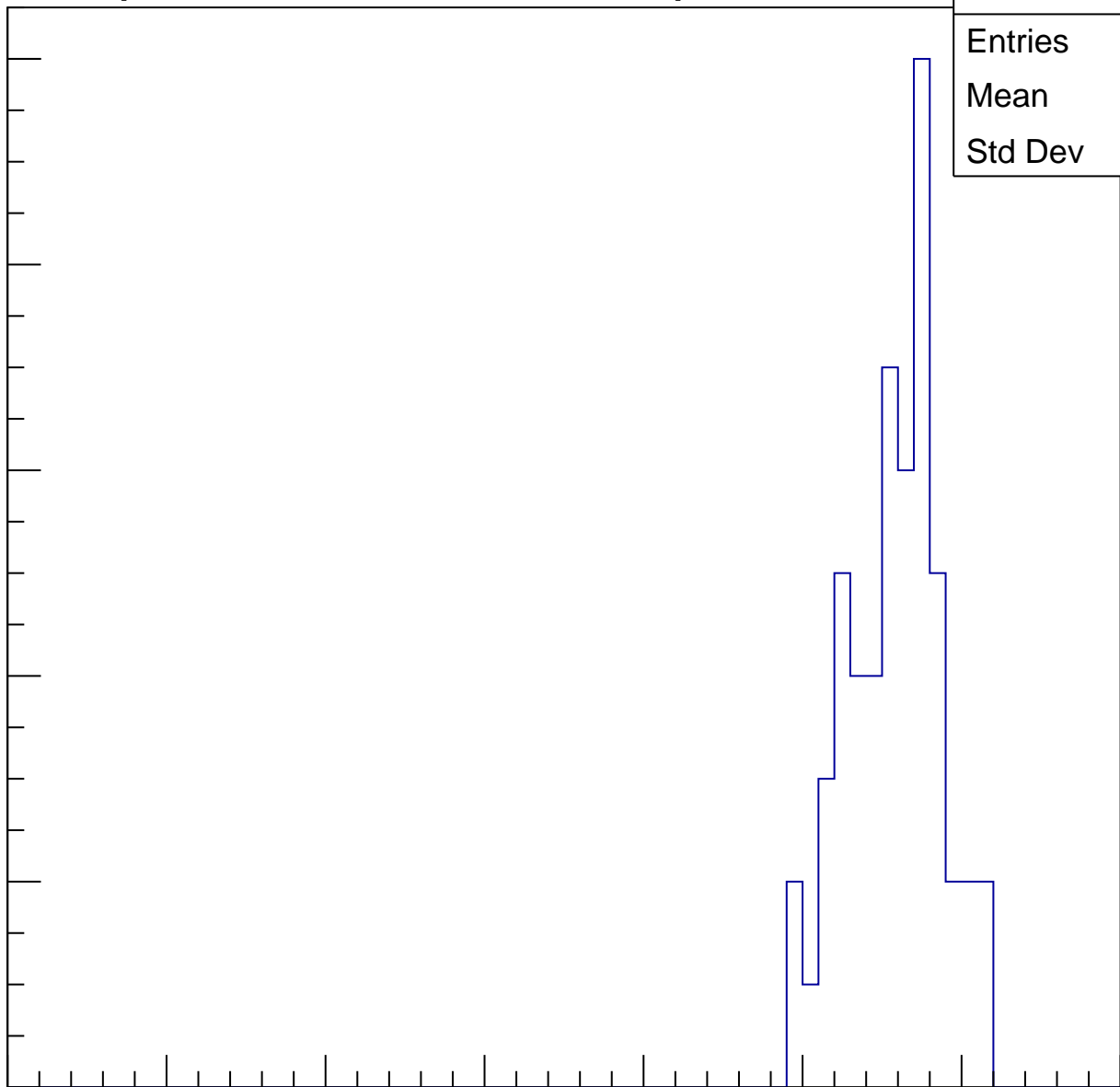
40

50

60

70

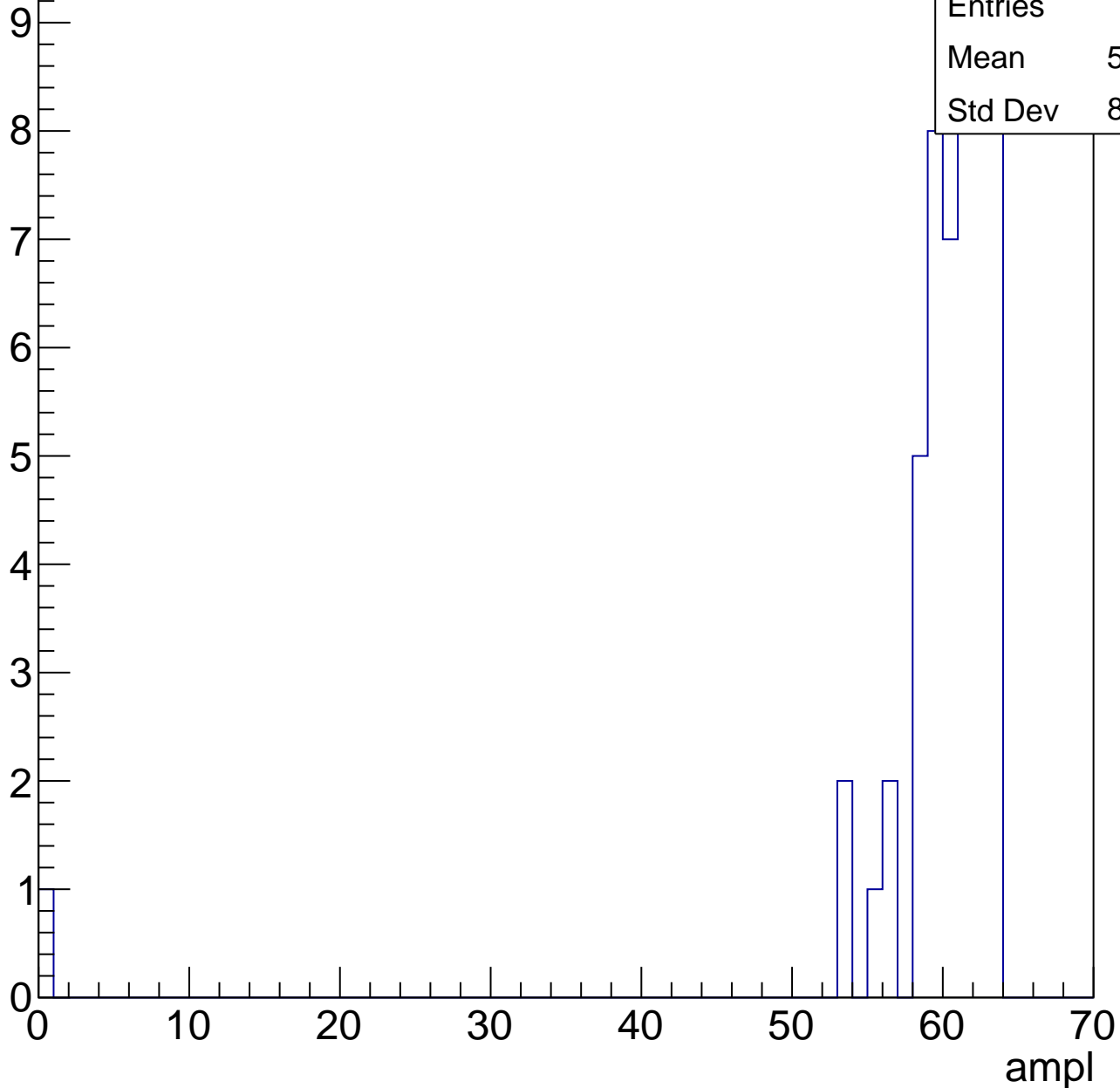
ampl



B0L002S, U26-ch14, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U26-ch15, adc0

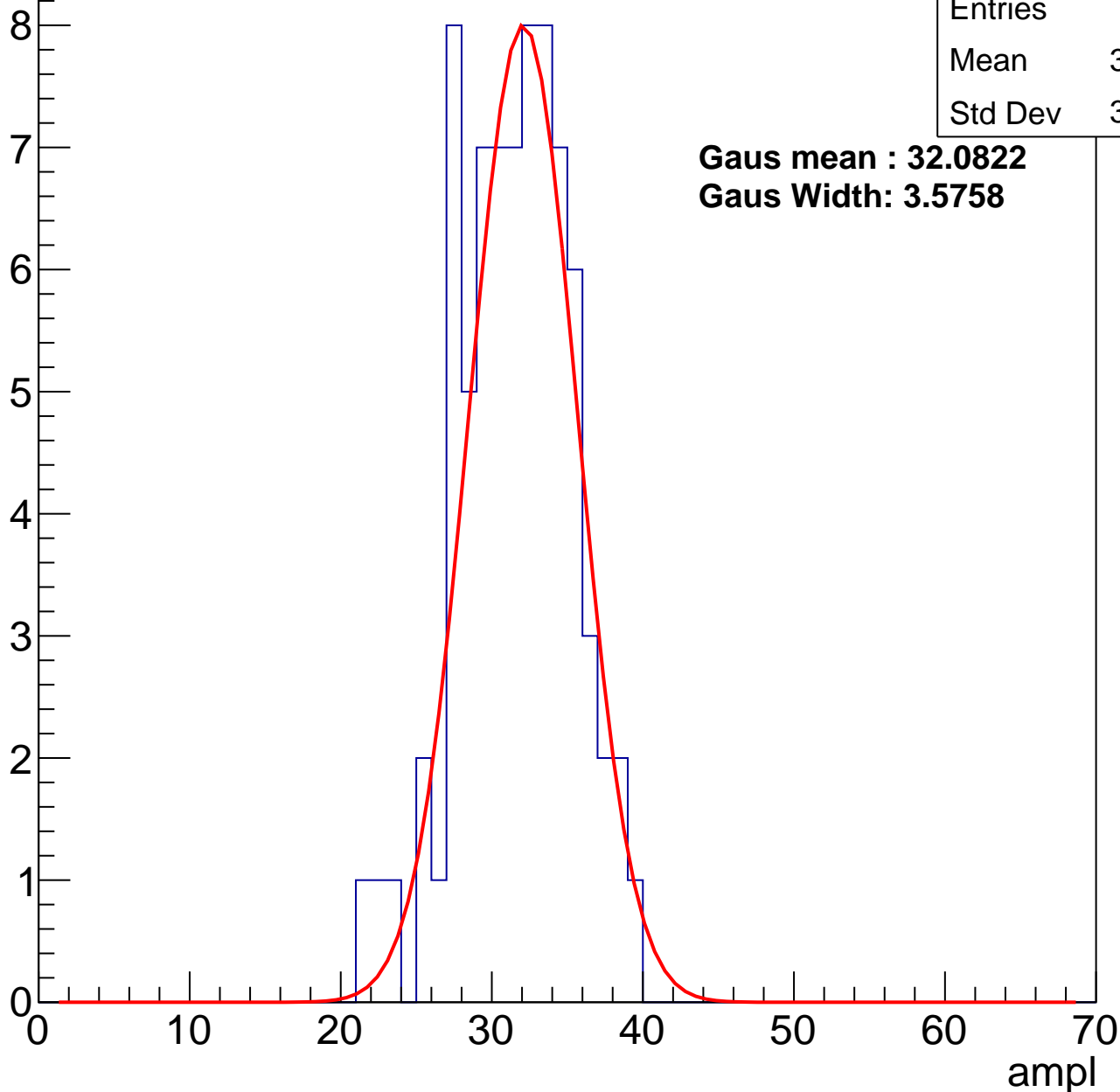
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	31.08
Std Dev	3.706

Gaus mean : 32.0822

Gaus Width: 3.5758



B0L002S, U26-ch15, adc1

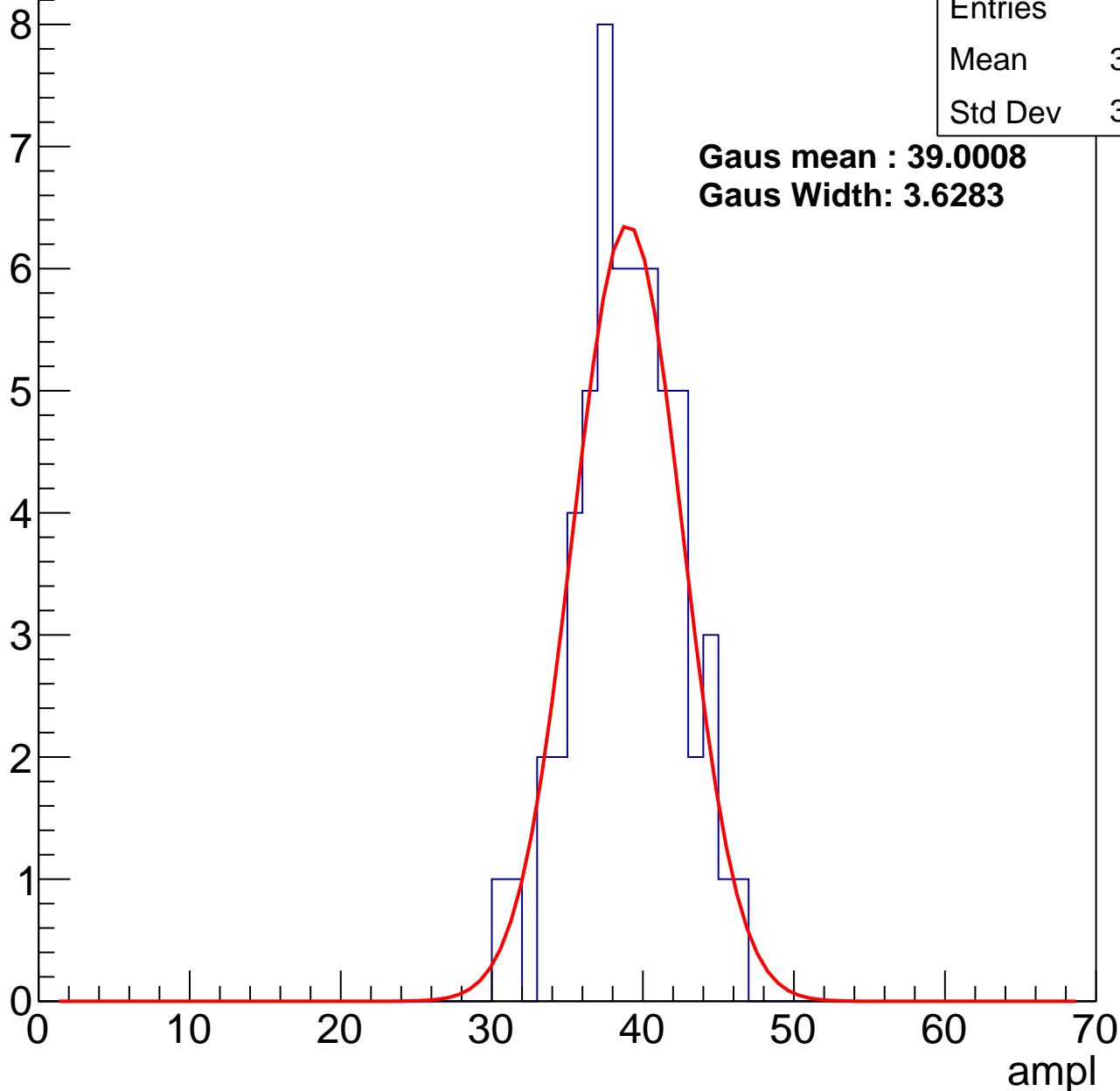
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	38.57
Std Dev	3.389

Gaus mean : 39.0008

Gaus Width: 3.6283



B0L002S, U26-ch15, adc2

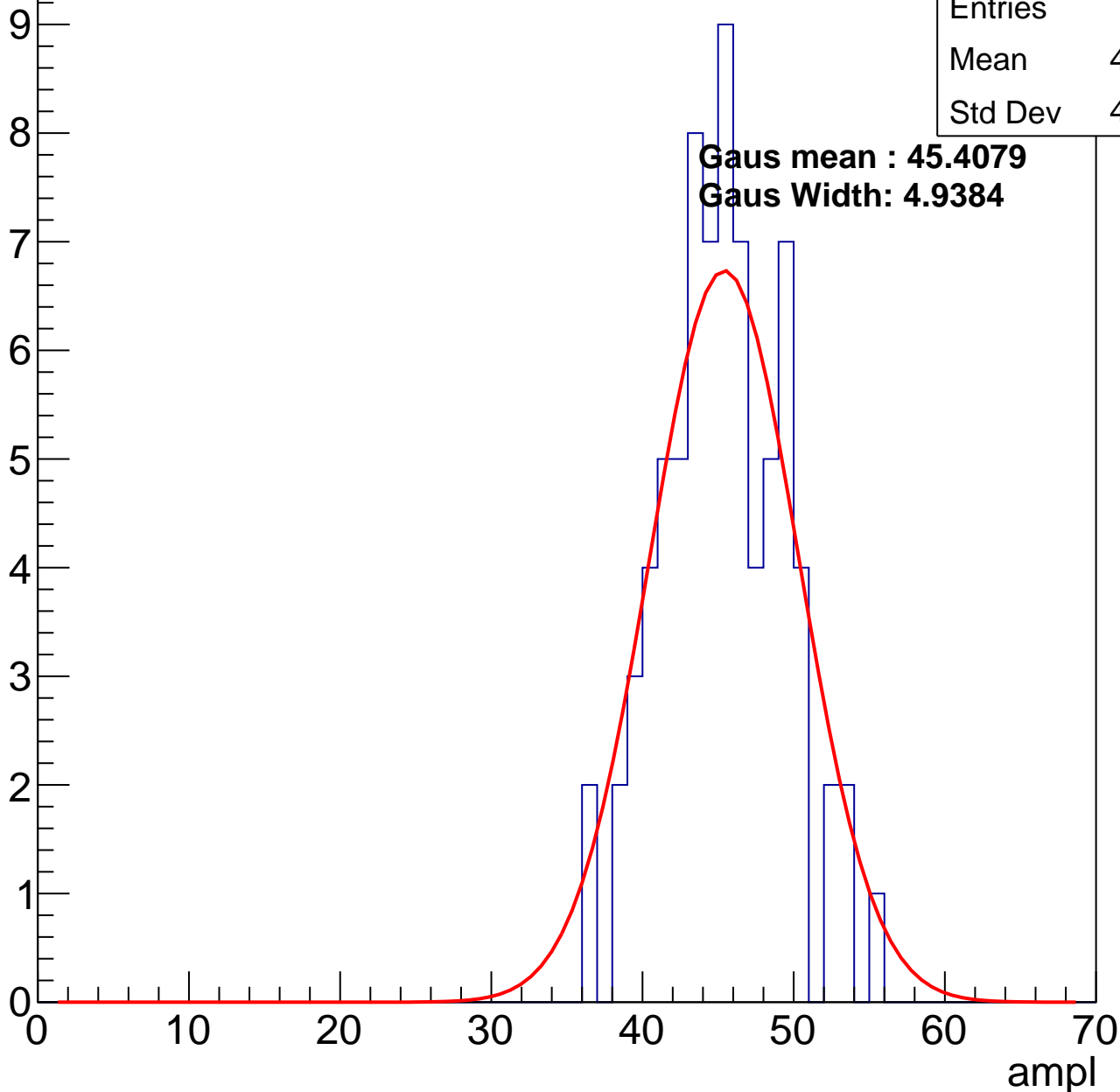
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	44.87
Std Dev	4.008

Gaus mean : 45.4079

Gaus Width: 4.9384



B0L002S, U26-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	52.21
Std Dev	3.304

Entry

10

8

6

4

2

0

0

10

20

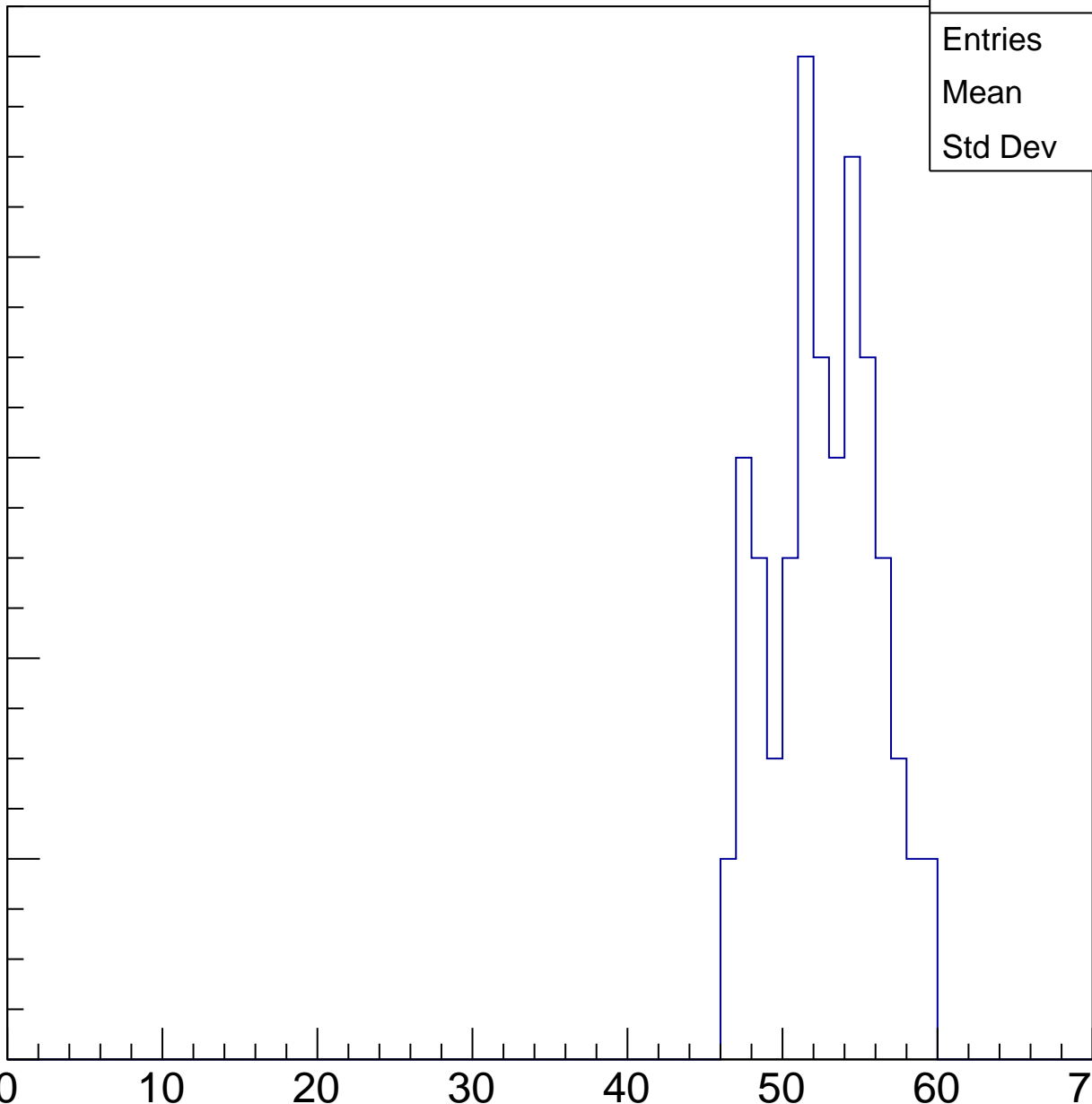
30

40

50

60

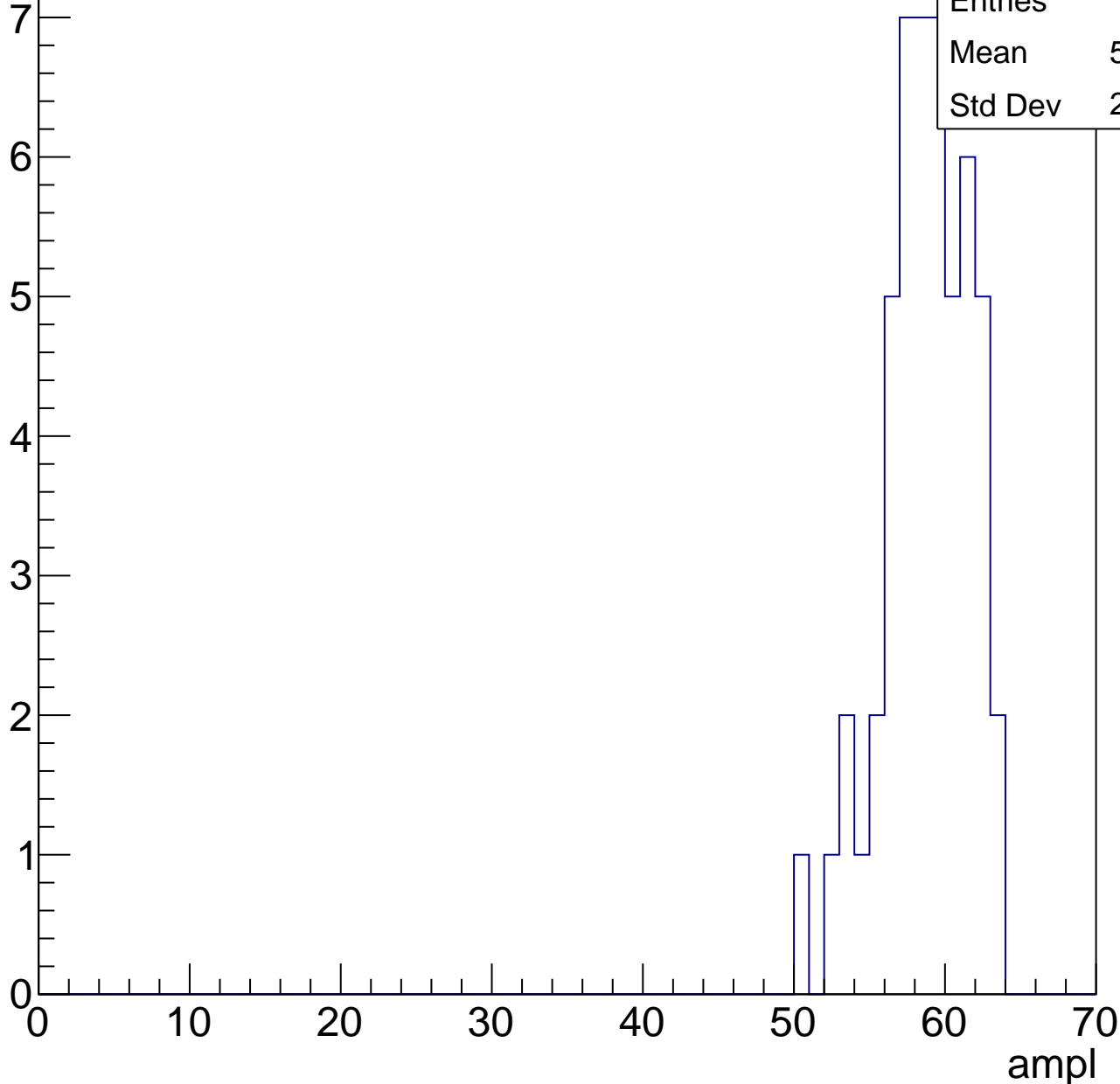
ampl



B0L002S, U26-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

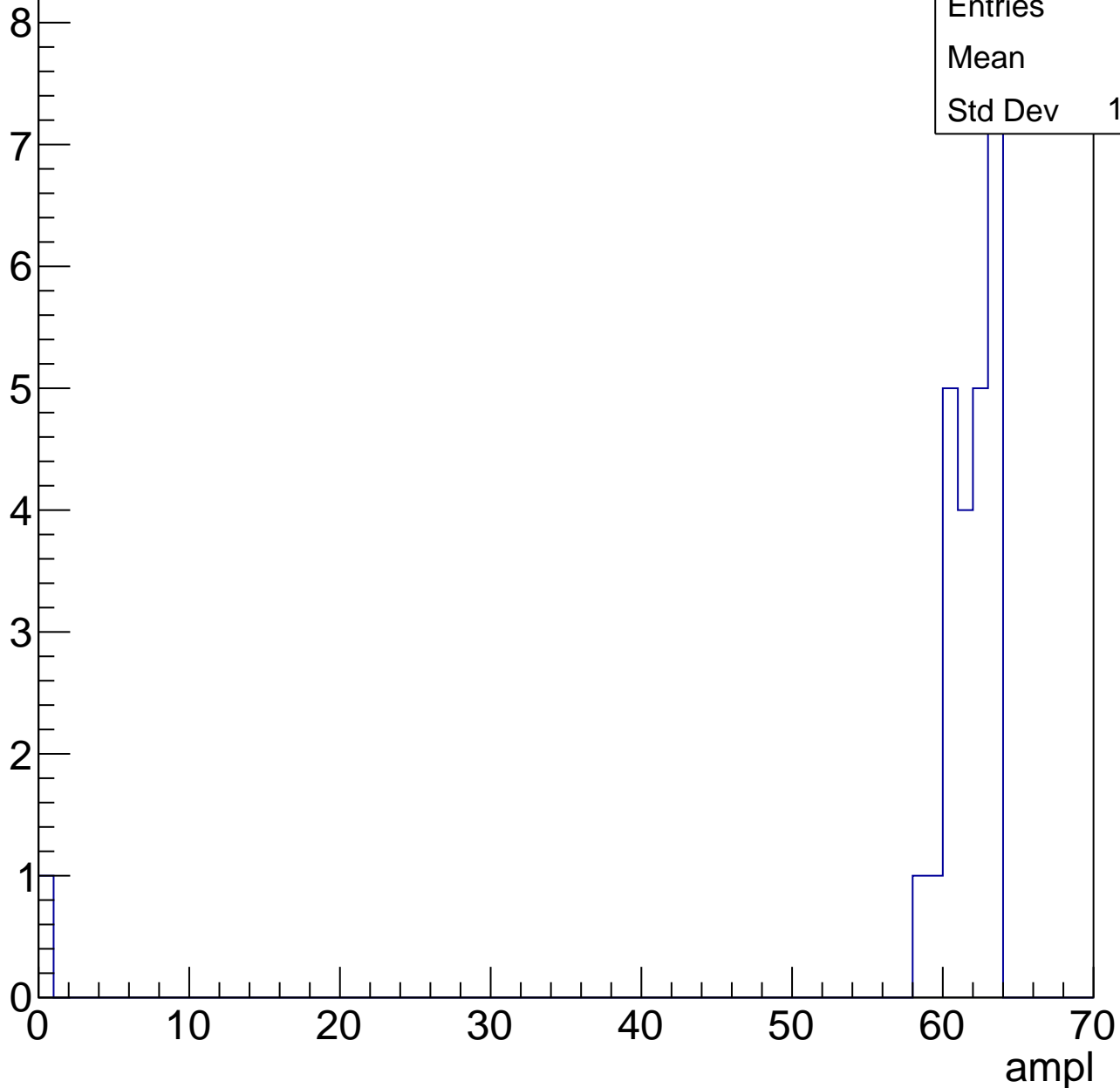


B0L002S, U26-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	59
Std Dev	12.13



B0L002S, U26-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch16, adc0

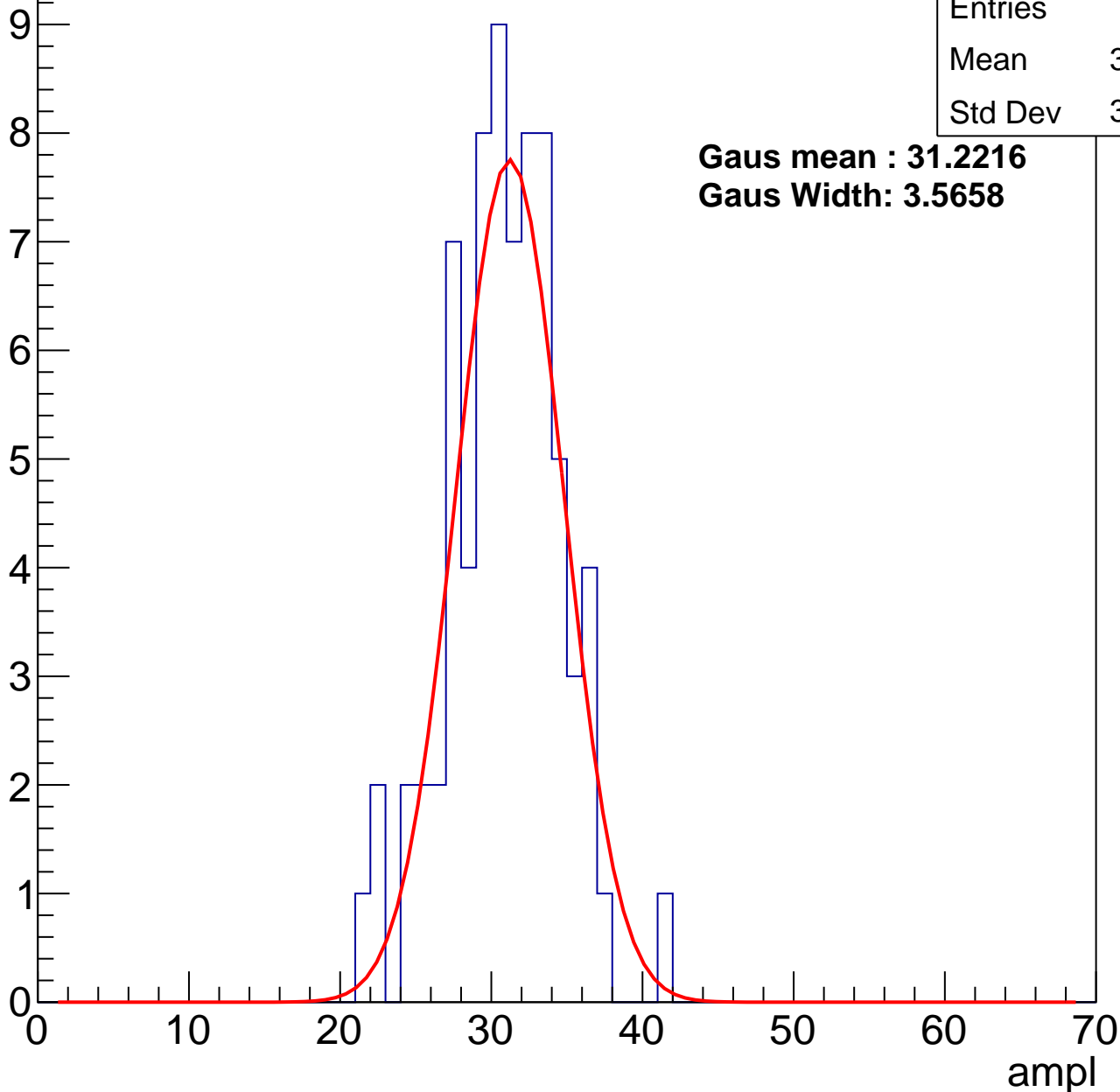
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	30.43
Std Dev	3.706

Gaus mean : 31.2216

Gaus Width: 3.5658



B0L002S, U26-ch16, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	37.42
Std Dev	3.048

Gaus mean : 38.0705

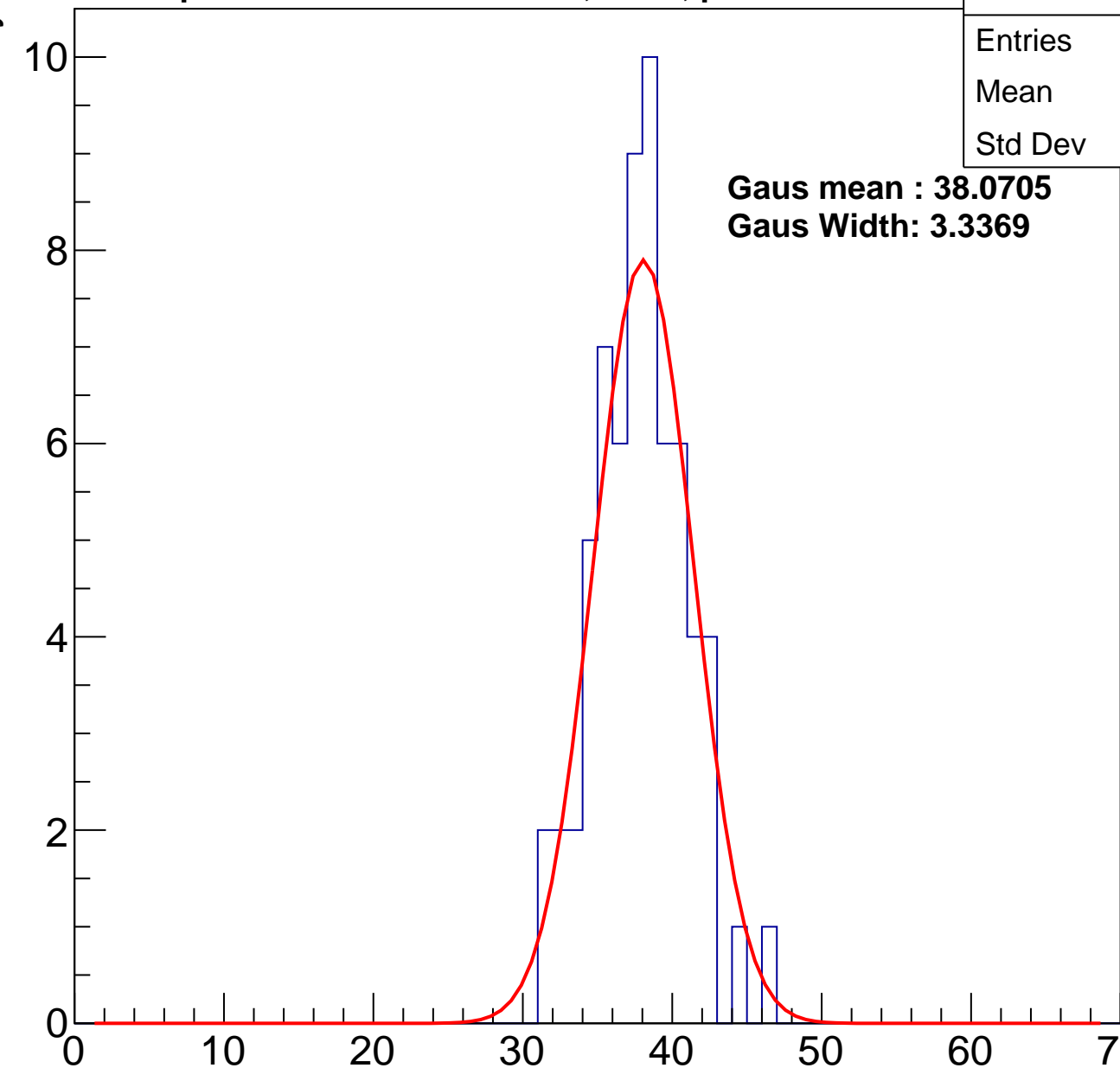
Gaus Width: 3.3369

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch16, adc2

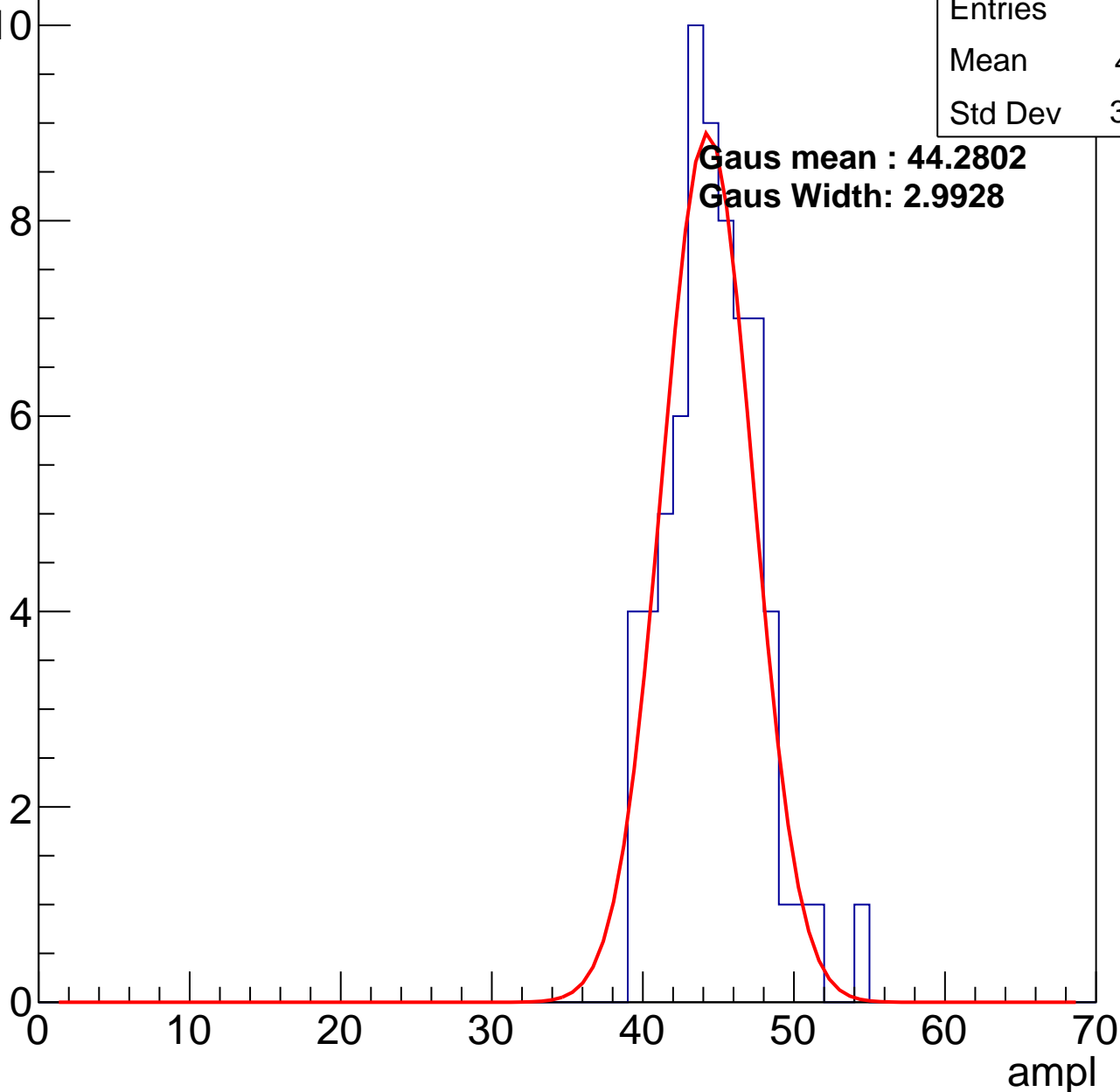
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	44.21
Std Dev	3.003

Gaus mean : 44.2802

Gaus Width: 2.9928

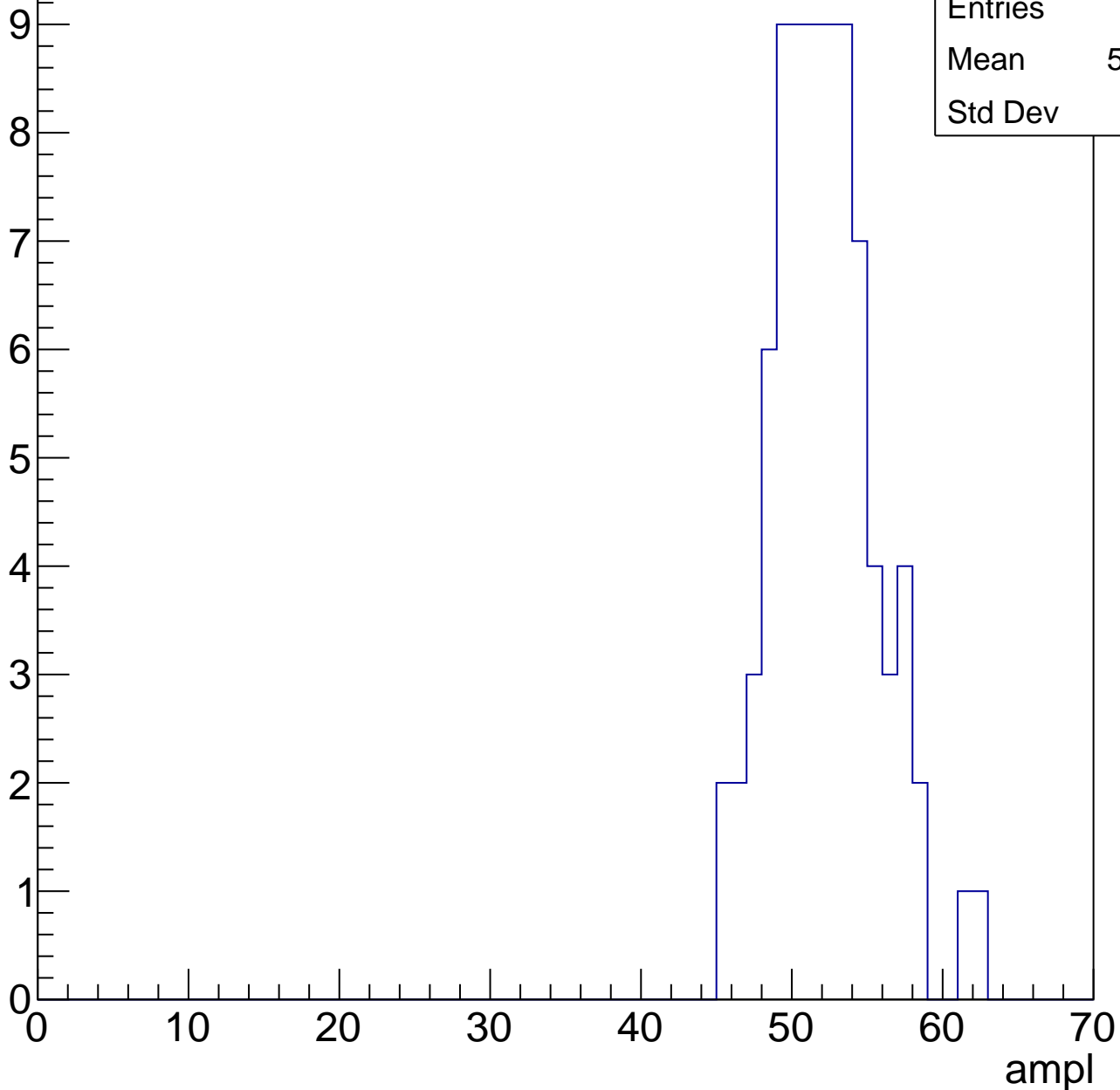


B0L002S, U26-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

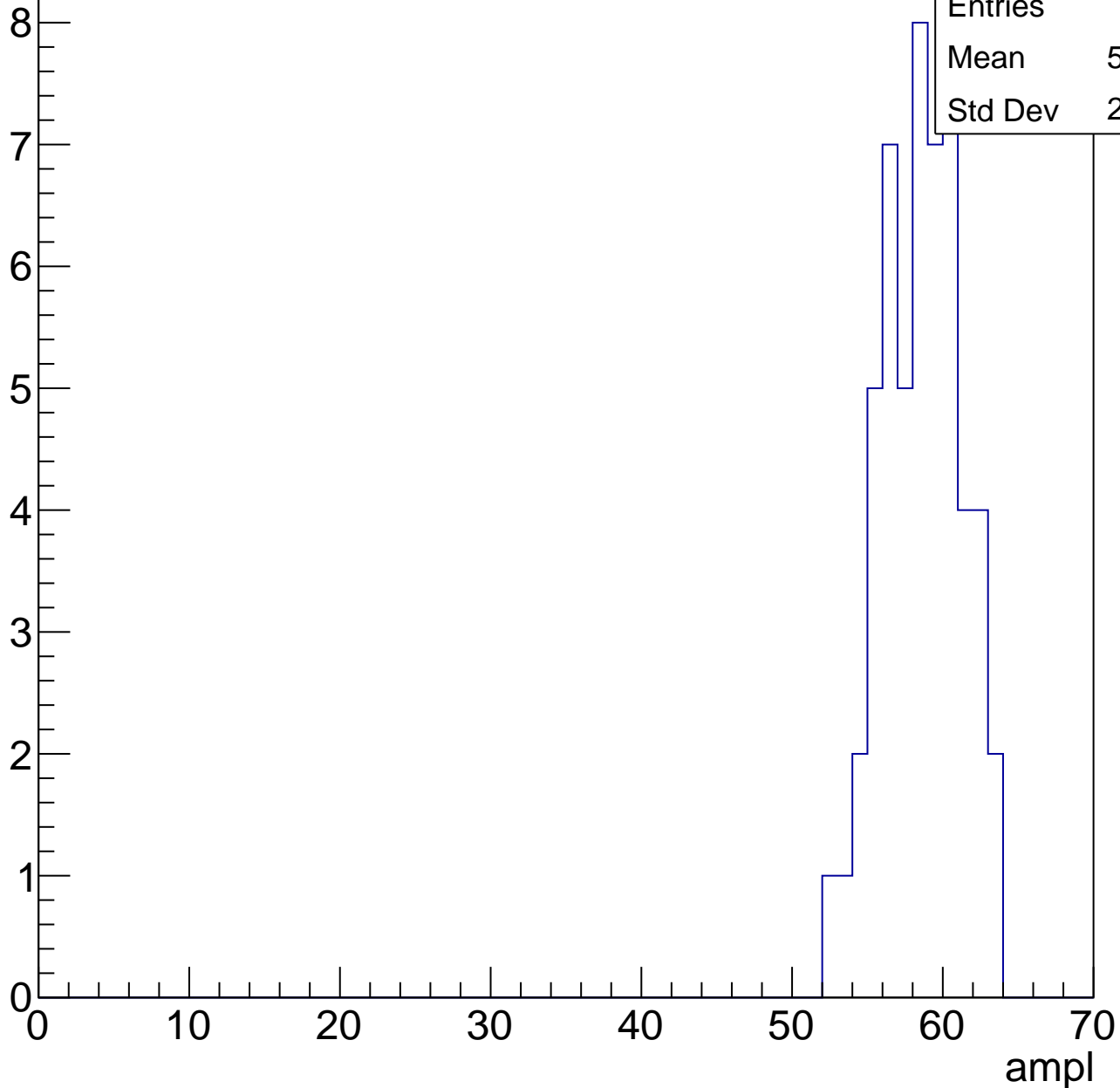
Entries	80
Mean	51.74
Std Dev	3.42



B0L002S, U26-ch16, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



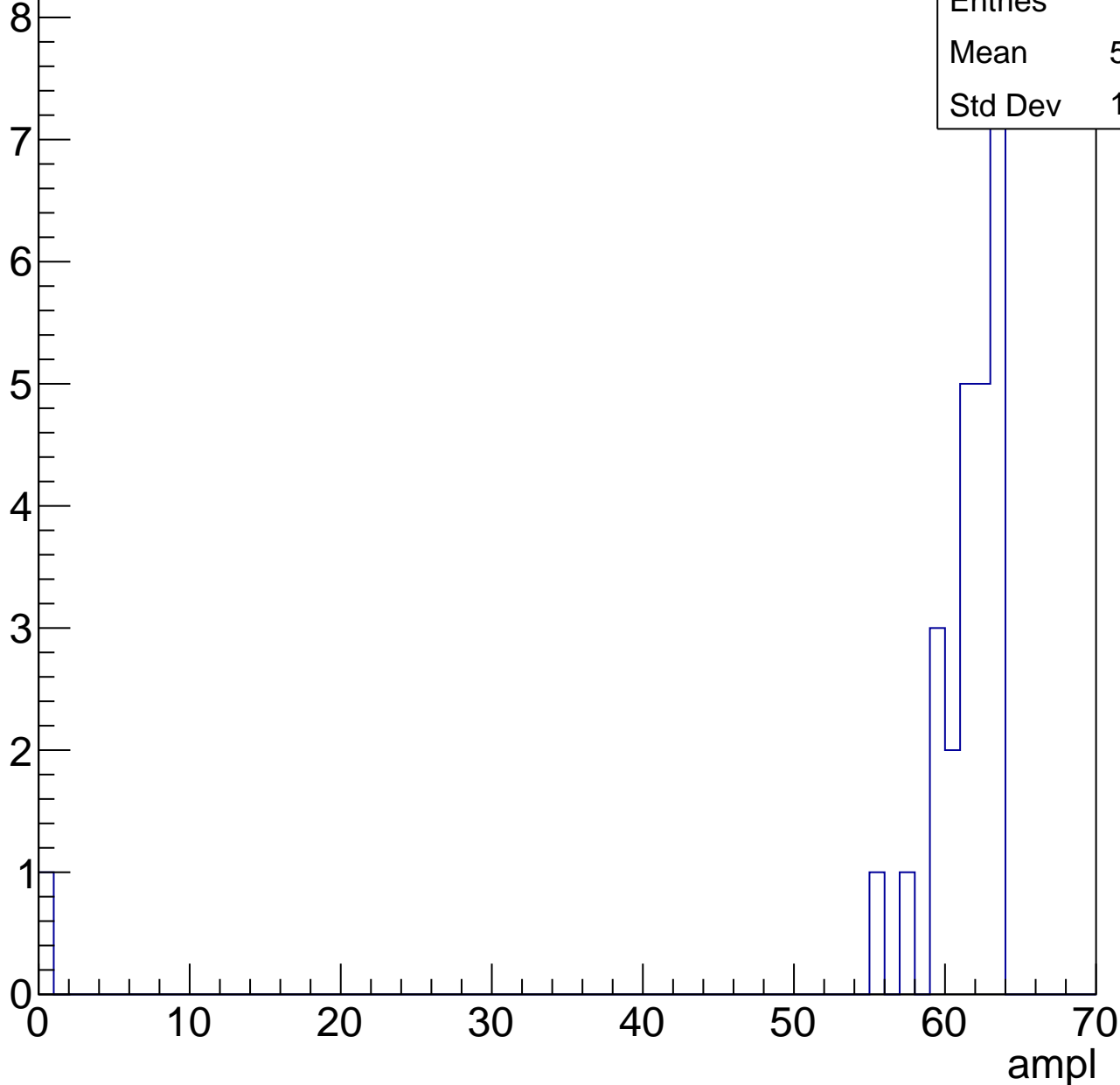
Entries	54
Mean	58.15
Std Dev	2.578

B0L002S, U26-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	58.77
Std Dev	11.92



B0L002S, U26-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L002S, U26-ch17, adc0

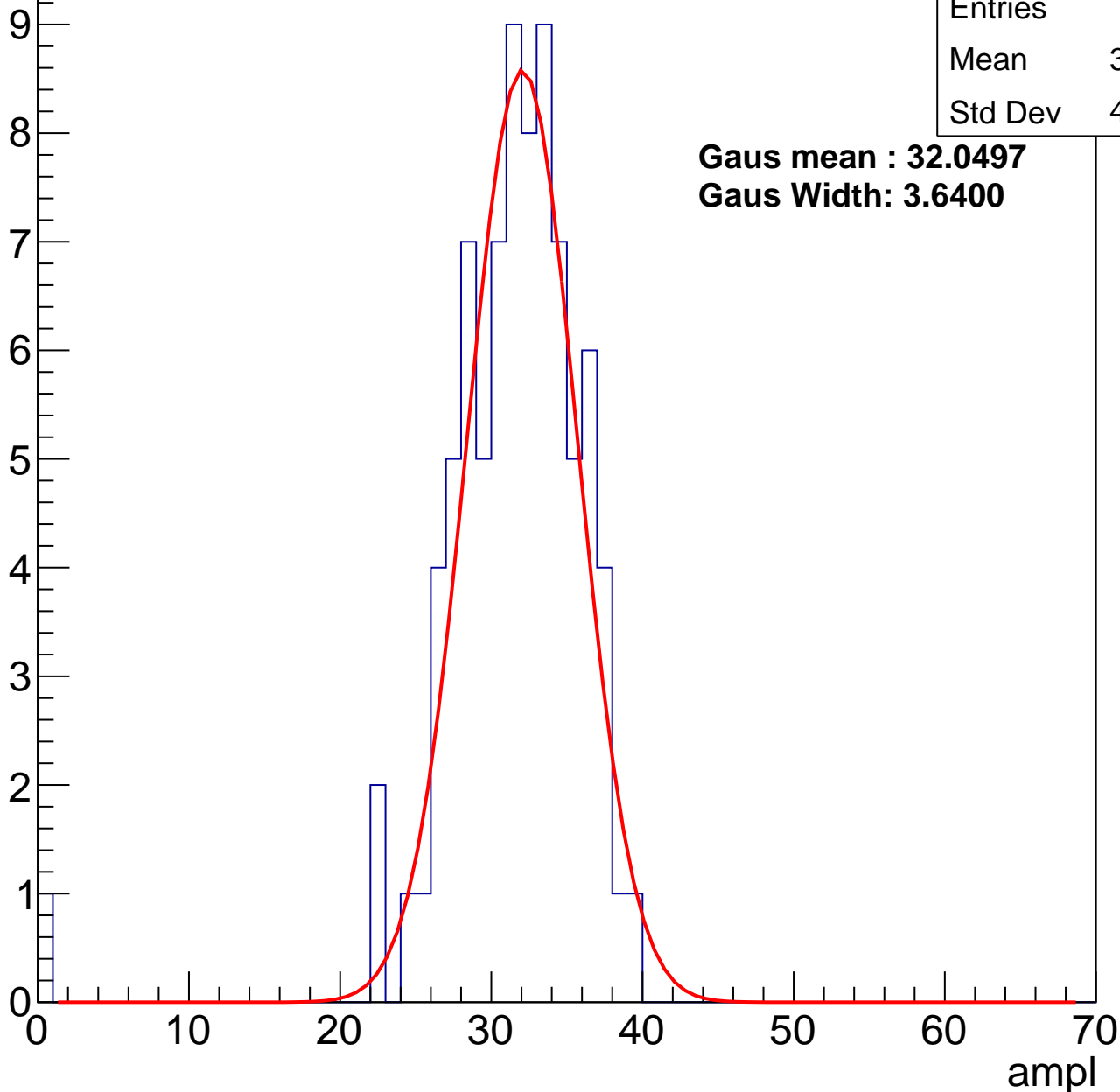
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	30.95
Std Dev	4.996

Gaus mean : 32.0497

Gaus Width: 3.6400



B0L002S, U26-ch17, adc1

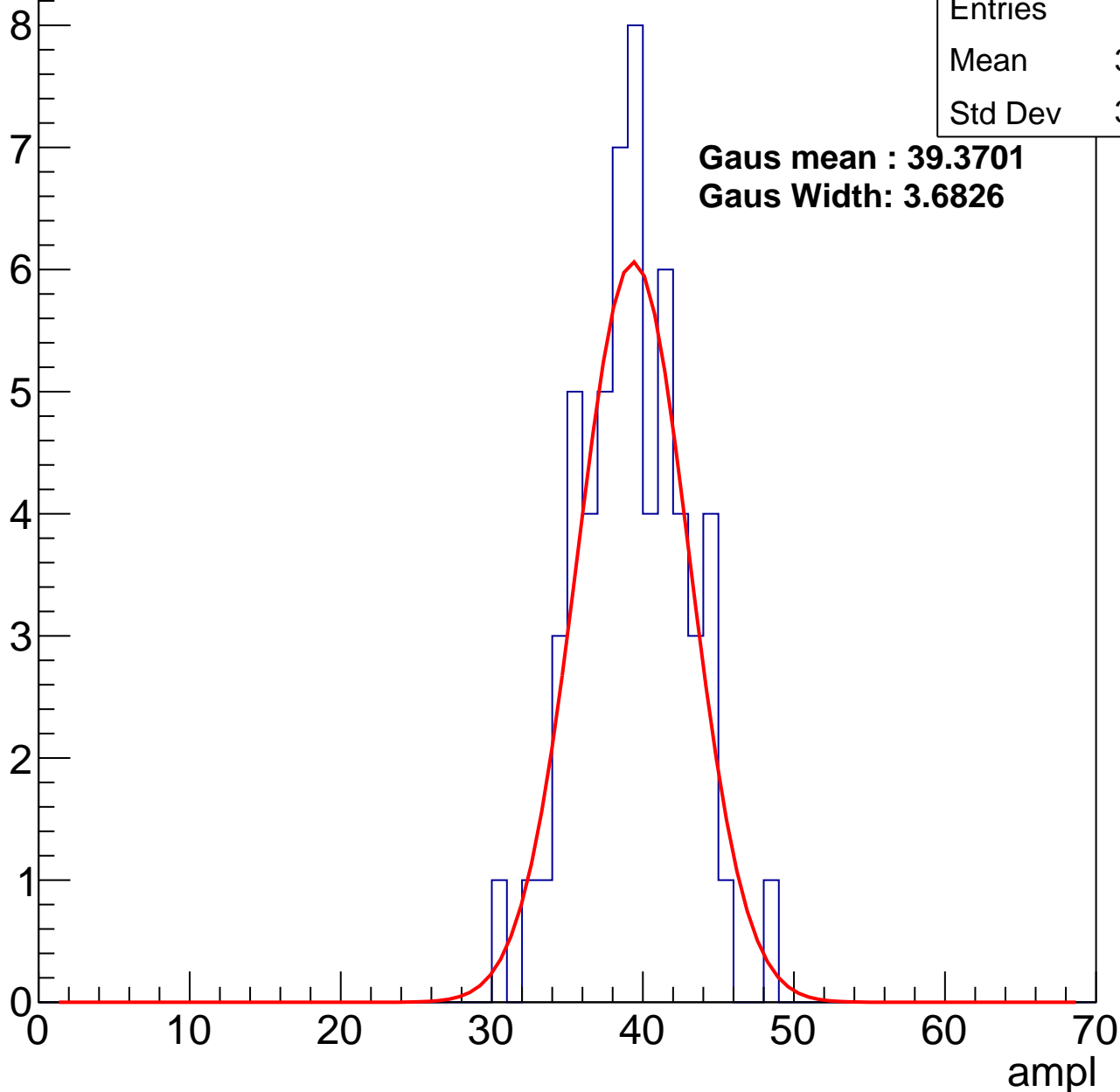
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	38.81
Std Dev	3.501

Gaus mean : 39.3701

Gaus Width: 3.6826



B0L002S, U26-ch17, adc2

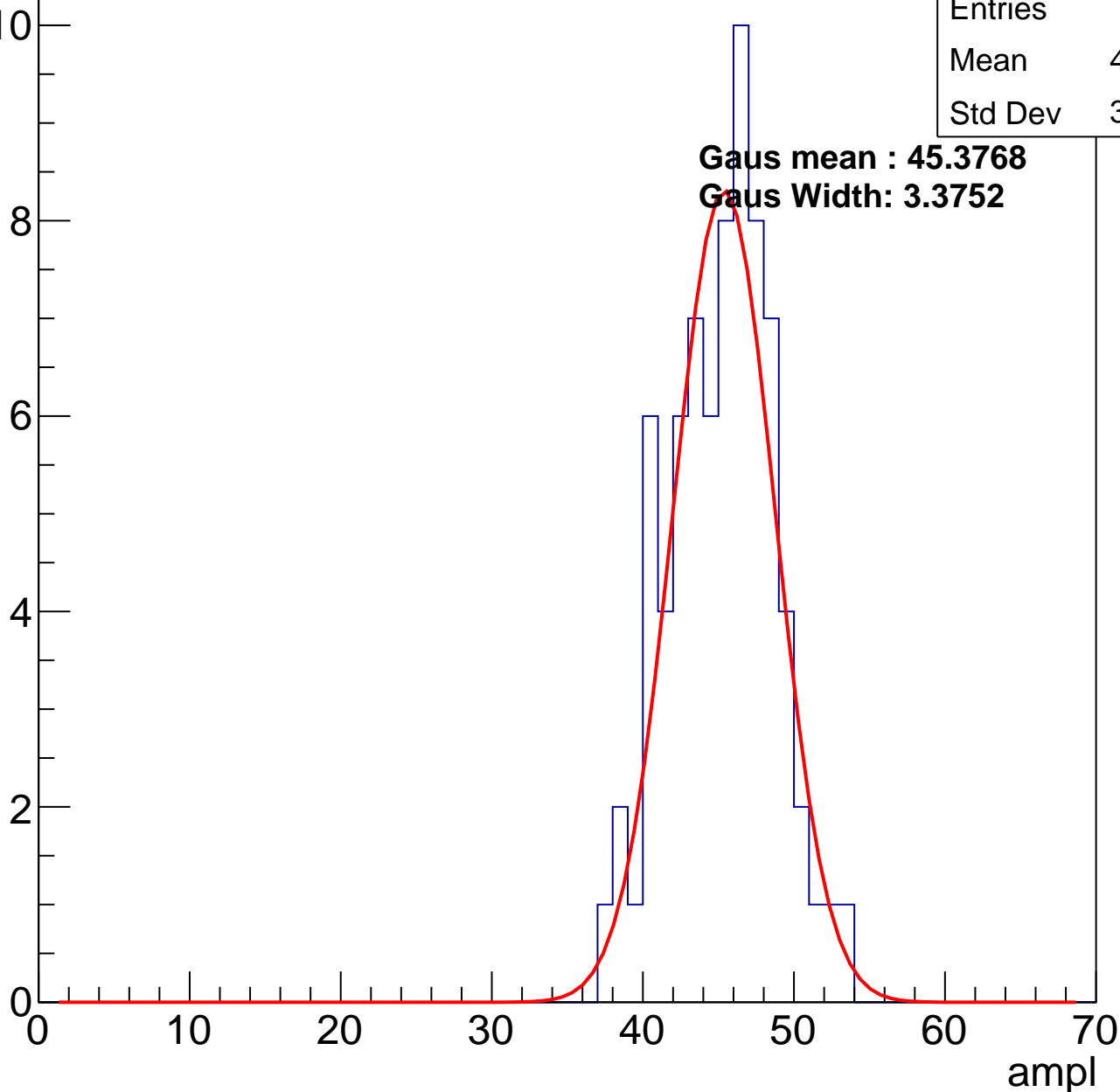
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.76
Std Dev	3.393

Gaus mean : 45.3768

Gaus Width: 3.3752

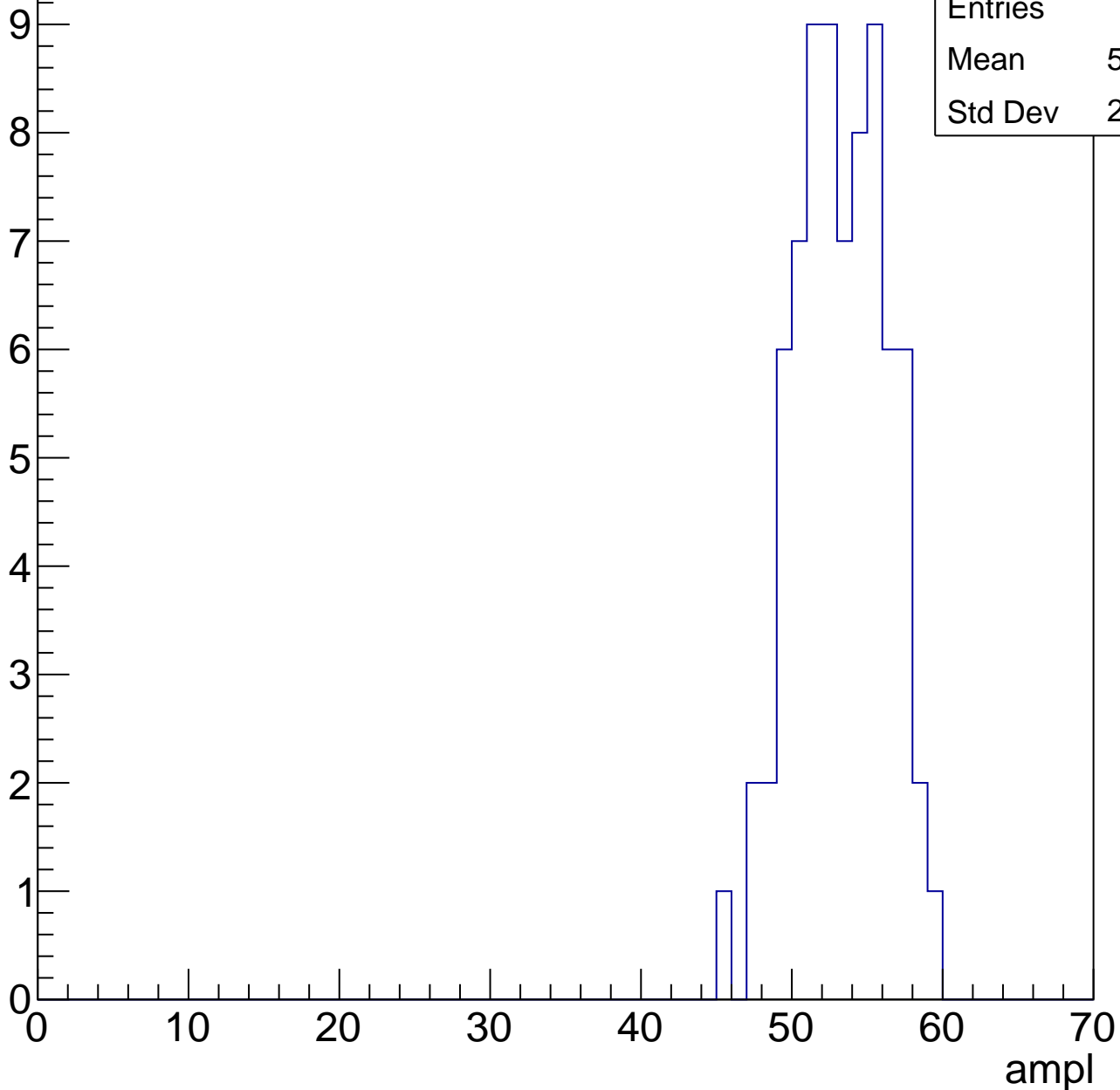


B0L002S, U26-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

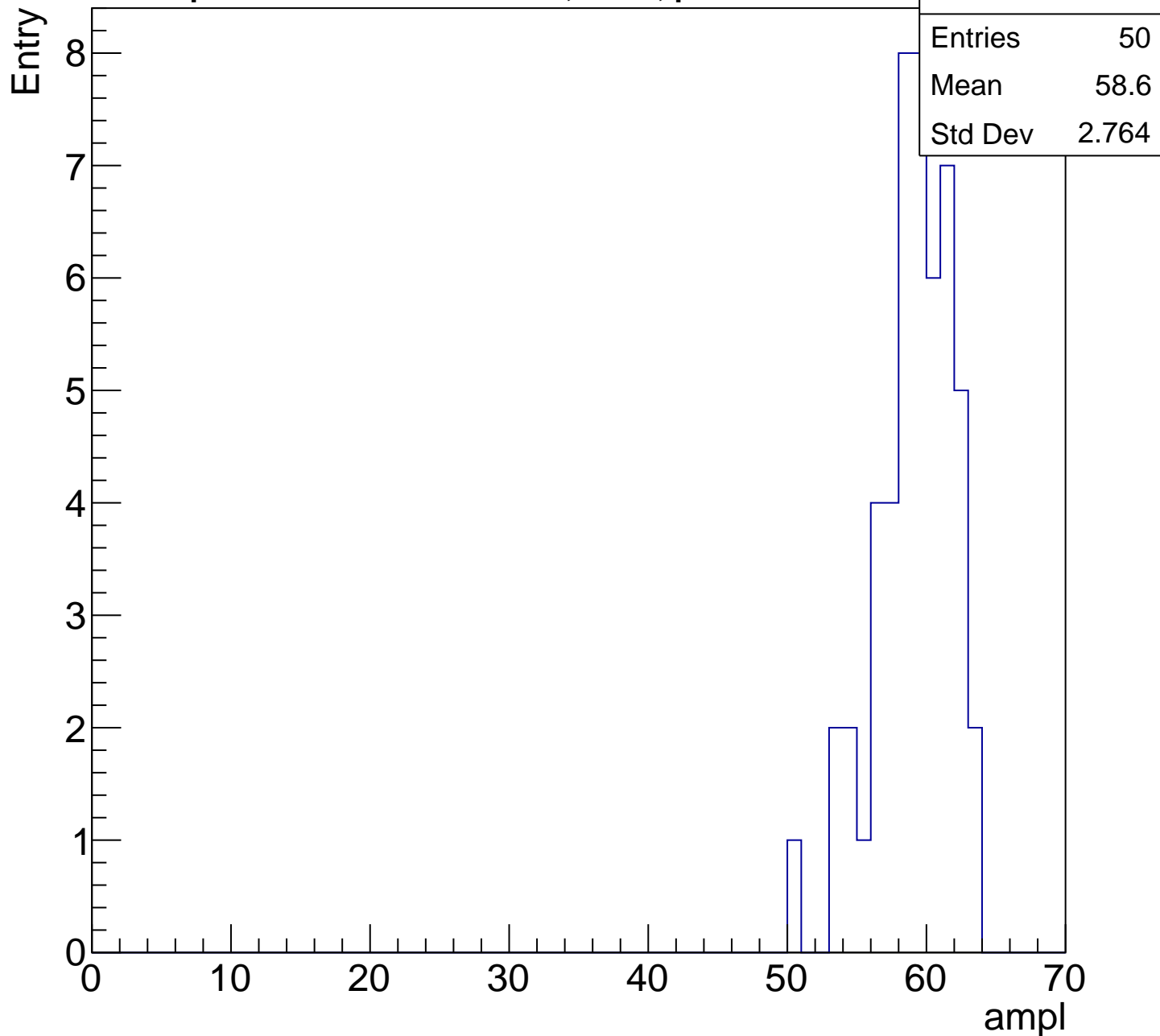
Entry

Entries	75
Mean	52.76
Std Dev	2.979



B0L002S, U26-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

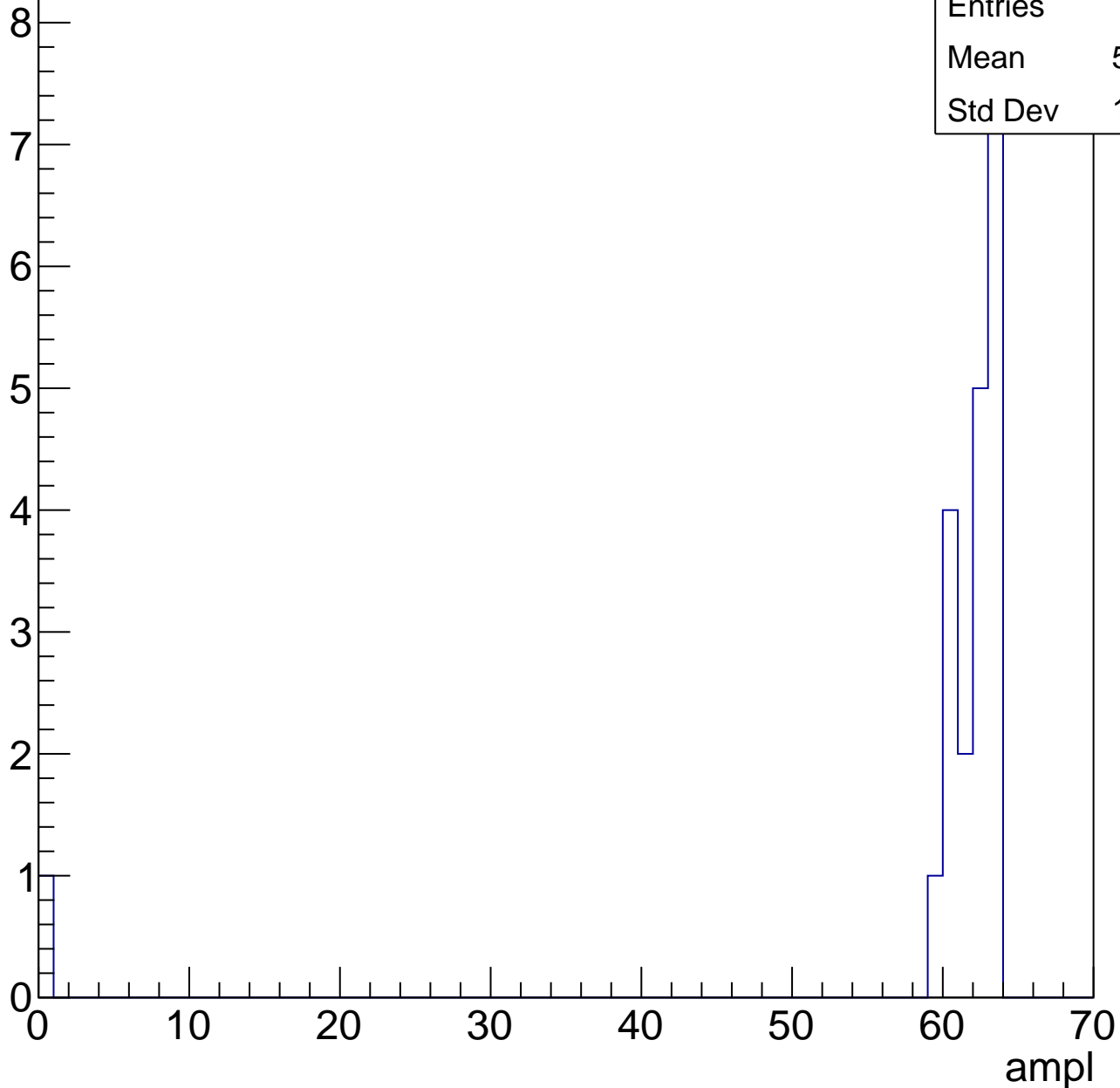


B0L002S, U26-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.81
Std Dev	13.21



B0L002S, U26-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch18, adc0

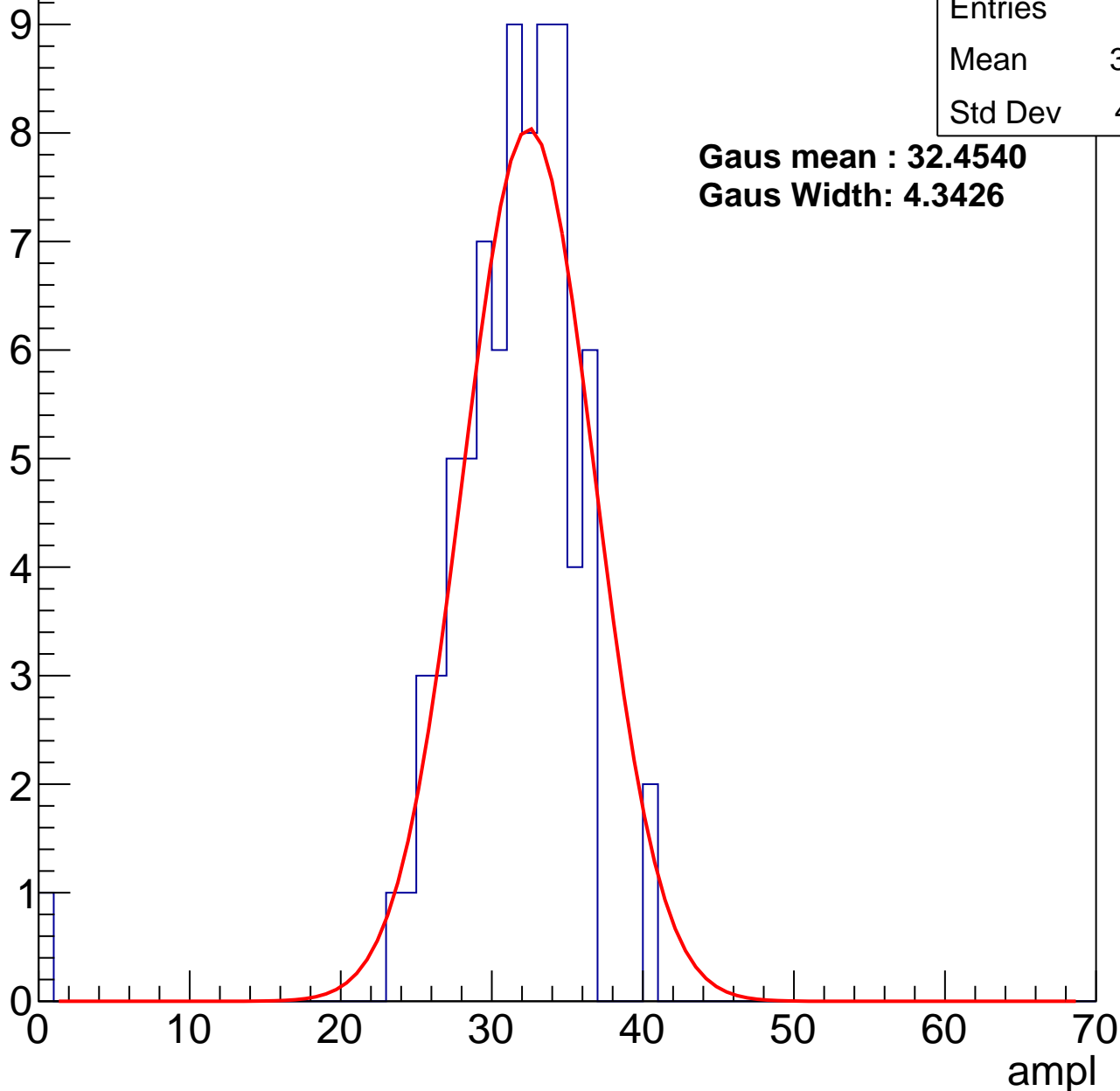
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.78
Std Dev	4.921

Gaus mean : 32.4540

Gaus Width: 4.3426



B0L002S, U26-ch18, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	52
Mean	38.21
Std Dev	2.706

Gaus mean : 38.4858

Gaus Width: 2.3874

Entry

10

8

6

4

2

0

0

10

20

30

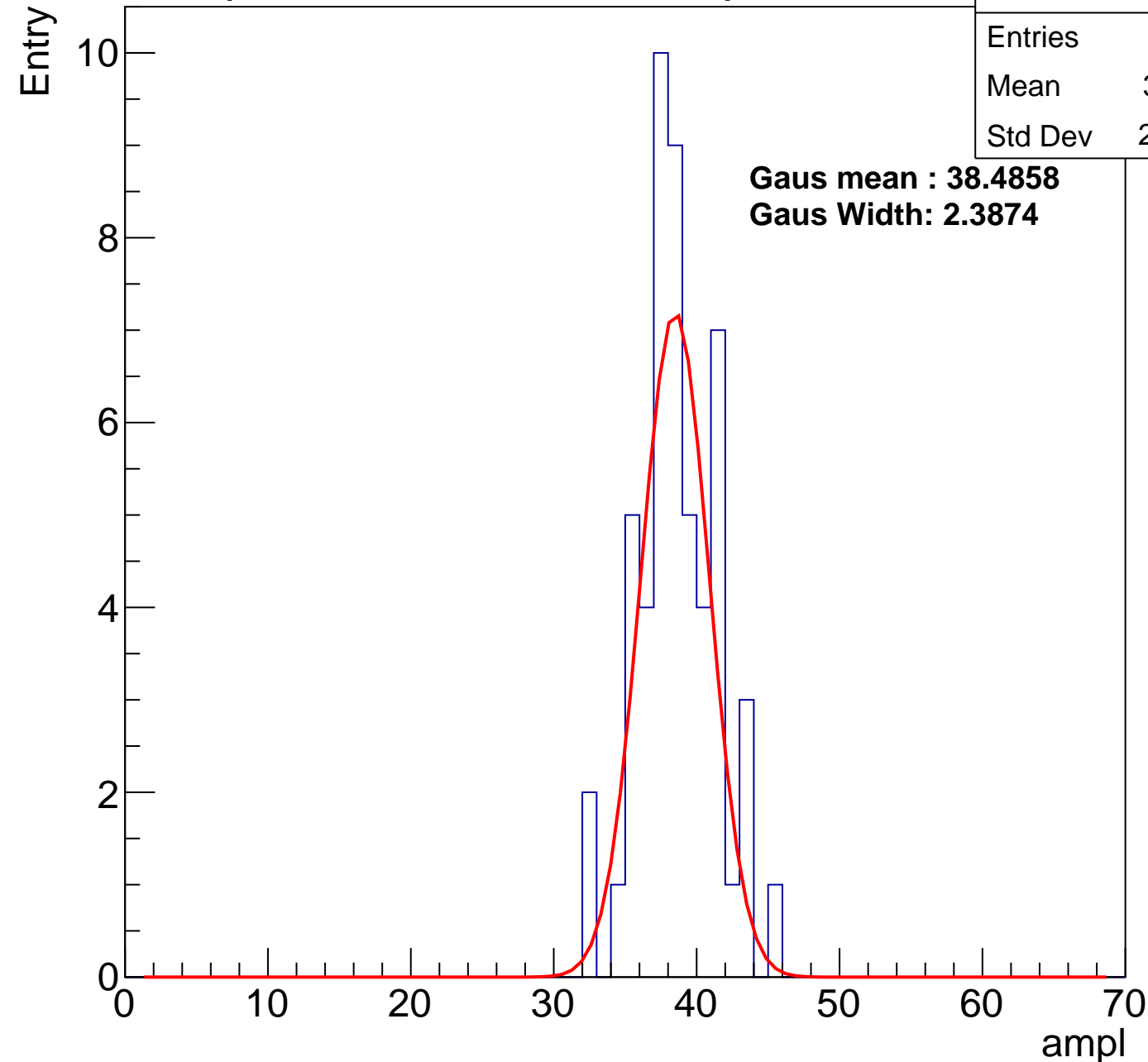
40

50

60

70

ampl



B0L002S, U26-ch18, adc2

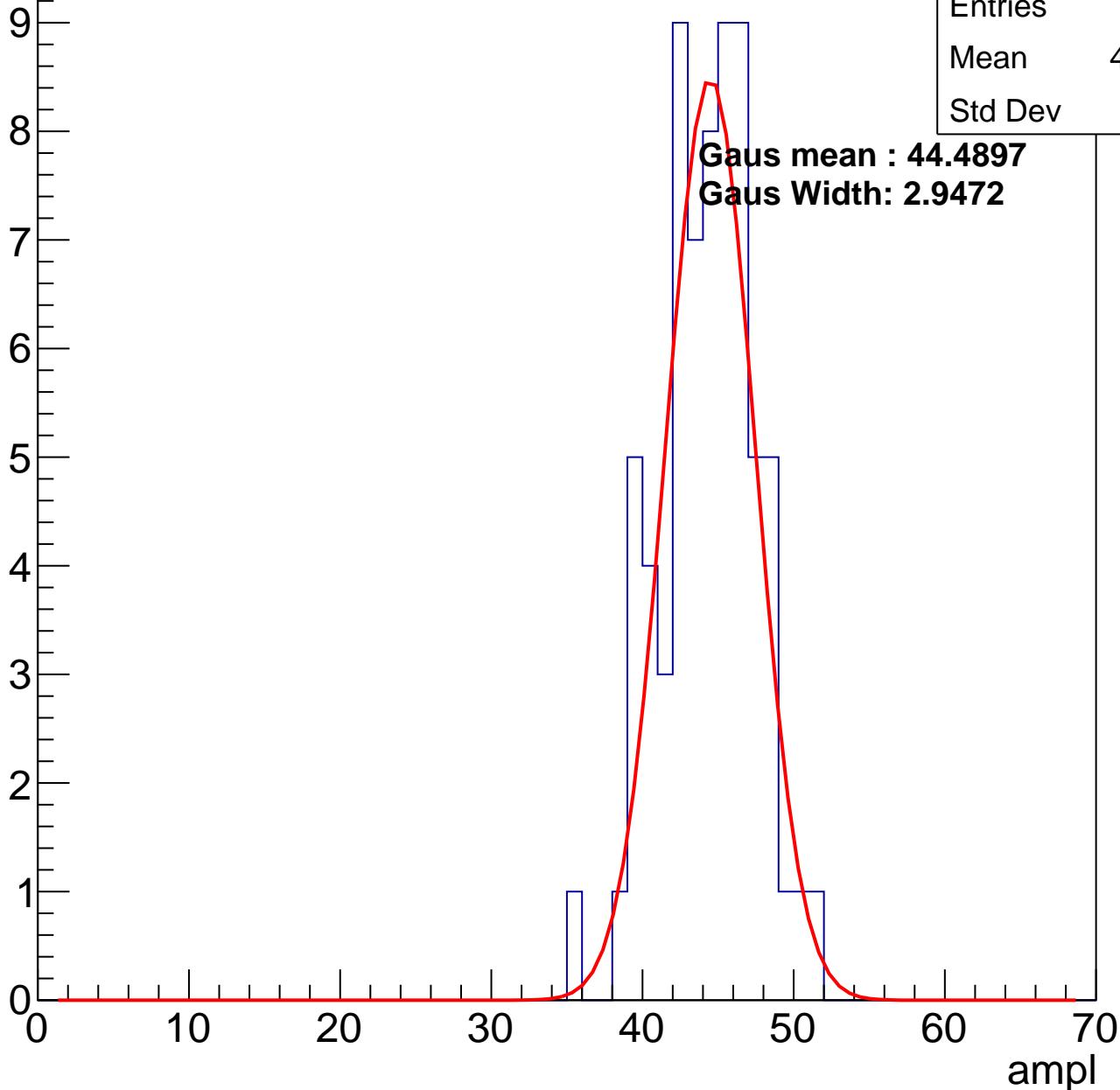
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.86
Std Dev	3.08

Gaus mean : 44.4897

Gaus Width: 2.9472

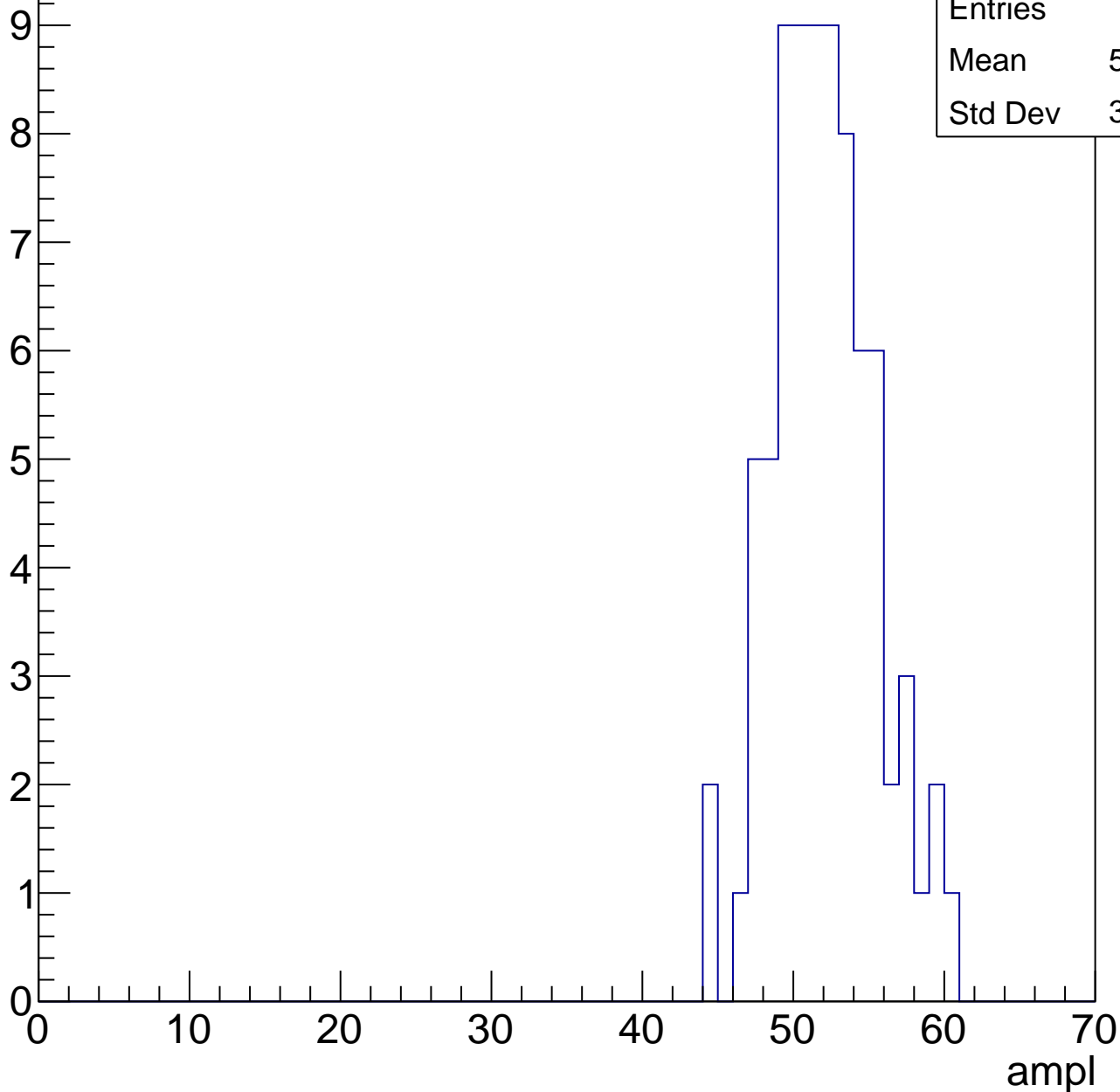


B0L002S, U26-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	51.59
Std Dev	3.345

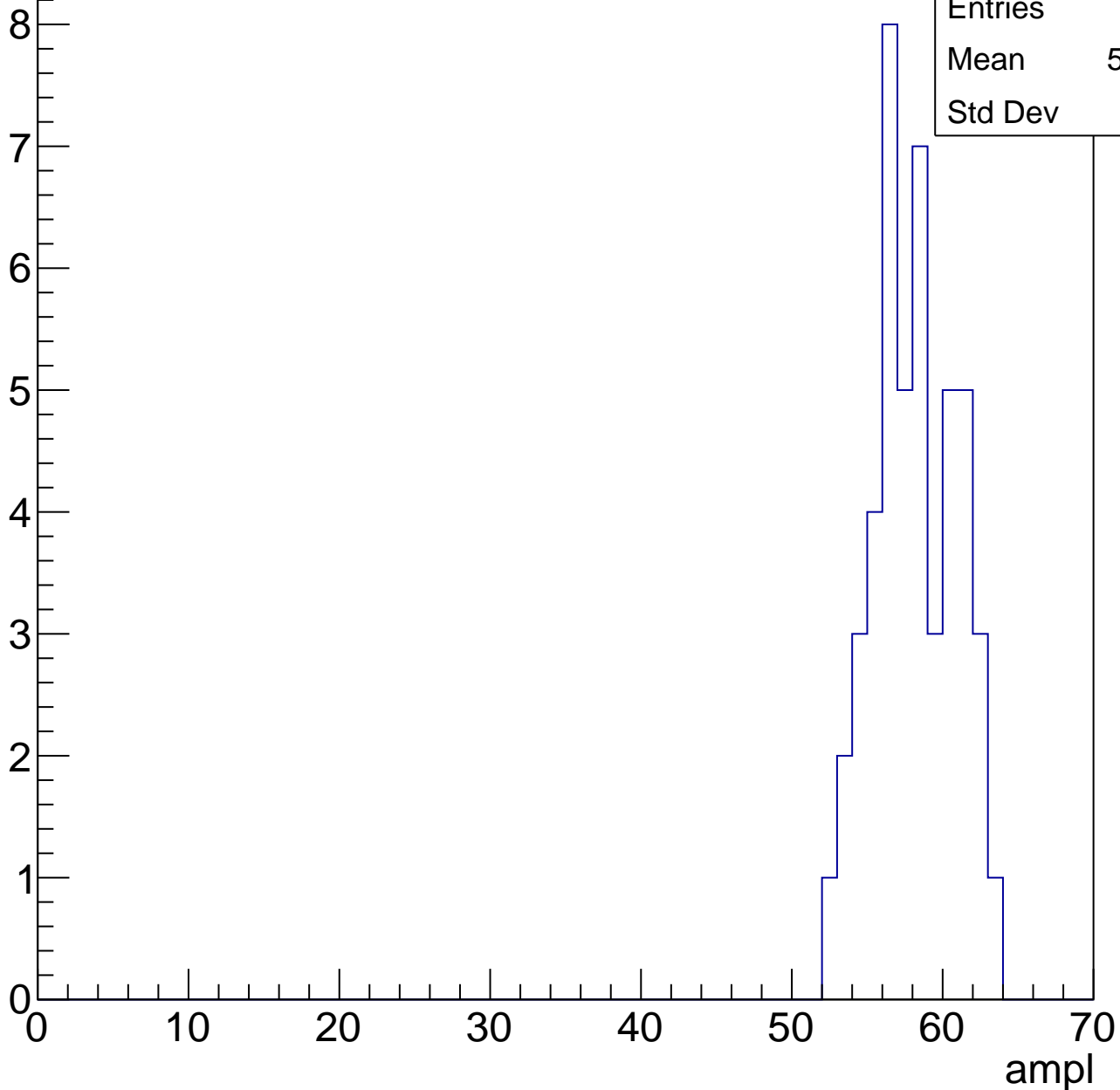


B0L002S, U26-ch18, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	57.66
Std Dev	2.7

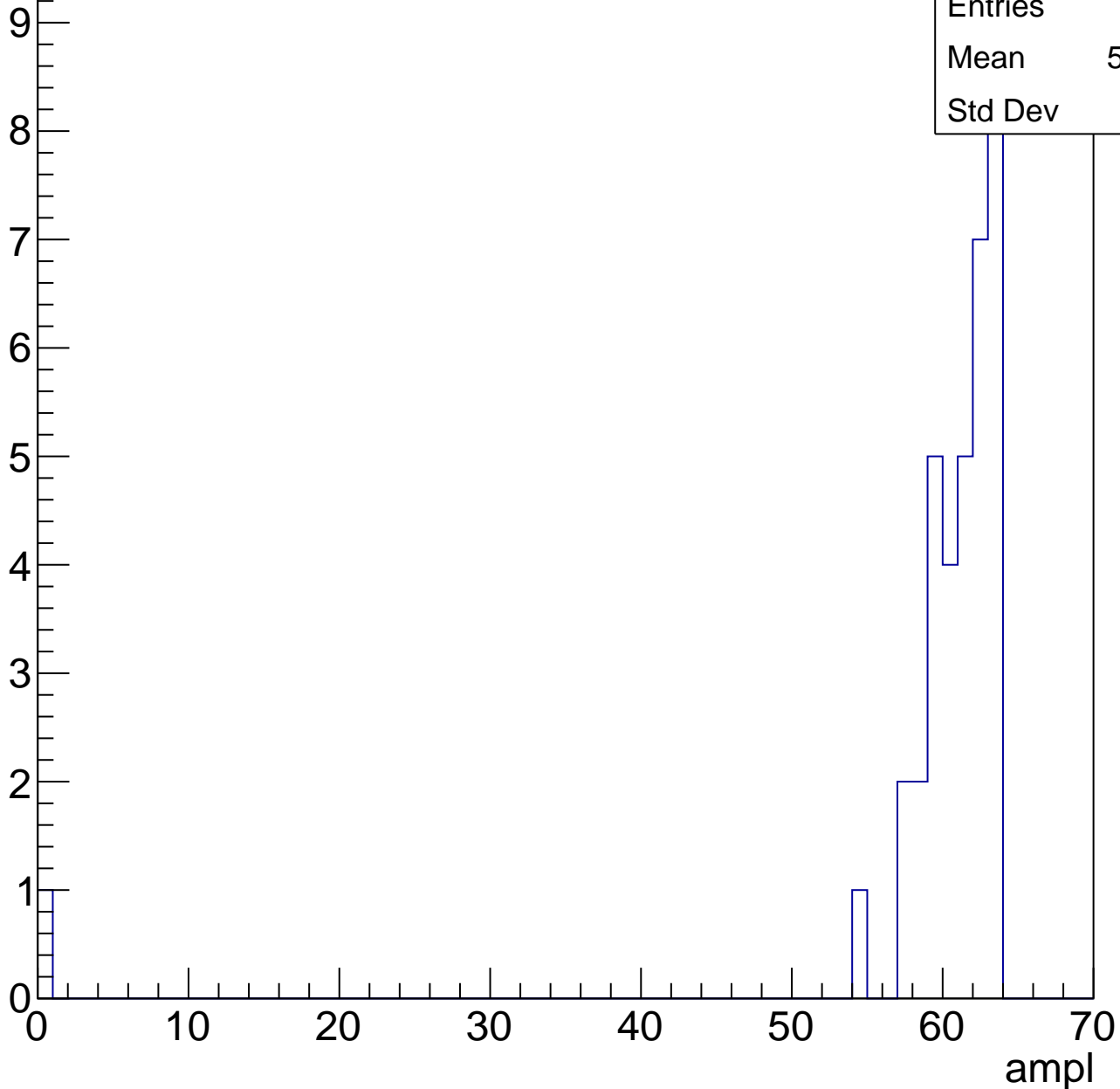


B0L002S, U26-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.03
Std Dev	10.2



B0L002S, U26-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch19, adc0

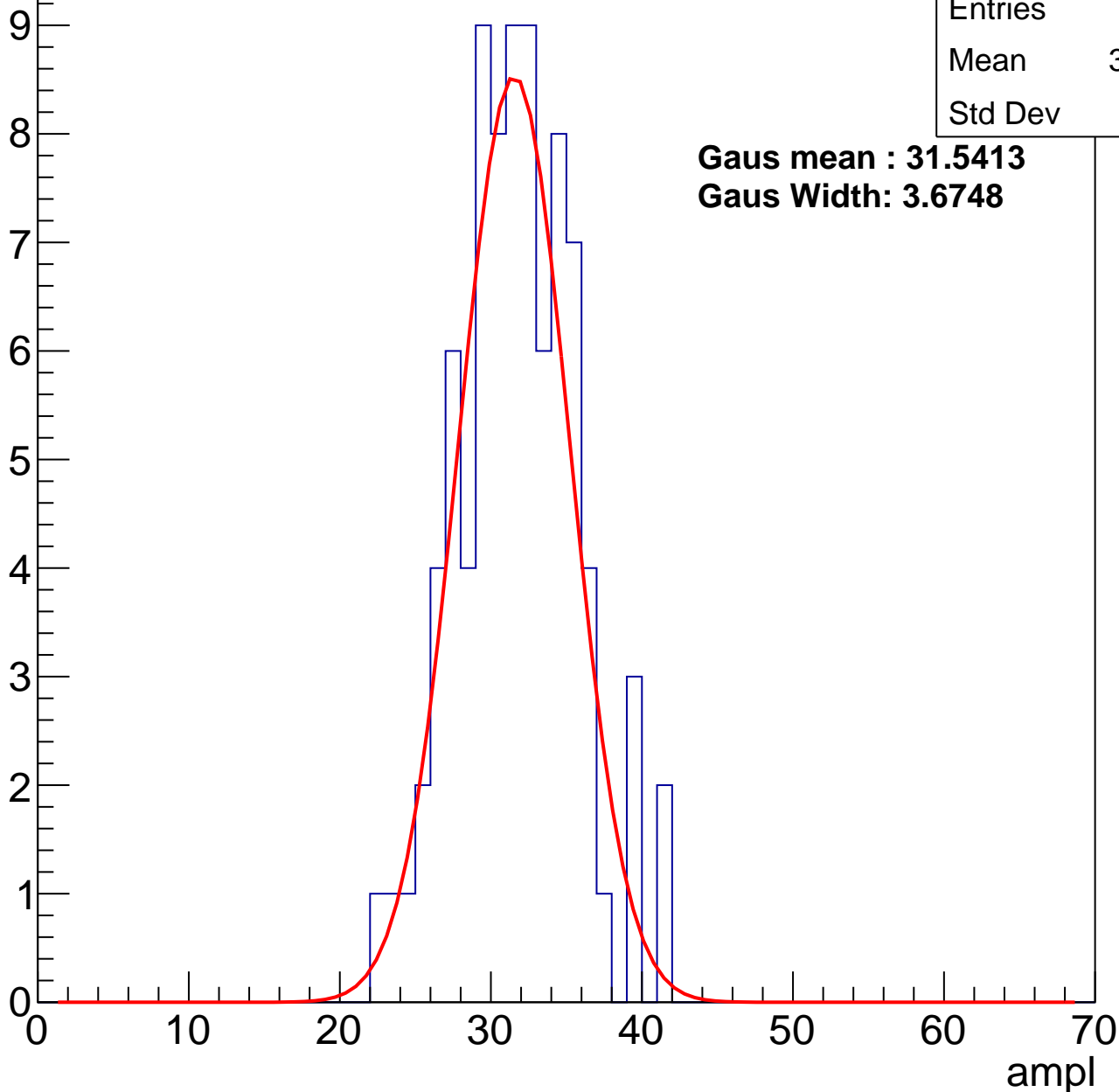
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	31.29
Std Dev	3.88

Gaus mean : 31.5413

Gaus Width: 3.6748



B0L002S, U26-ch19, adc1

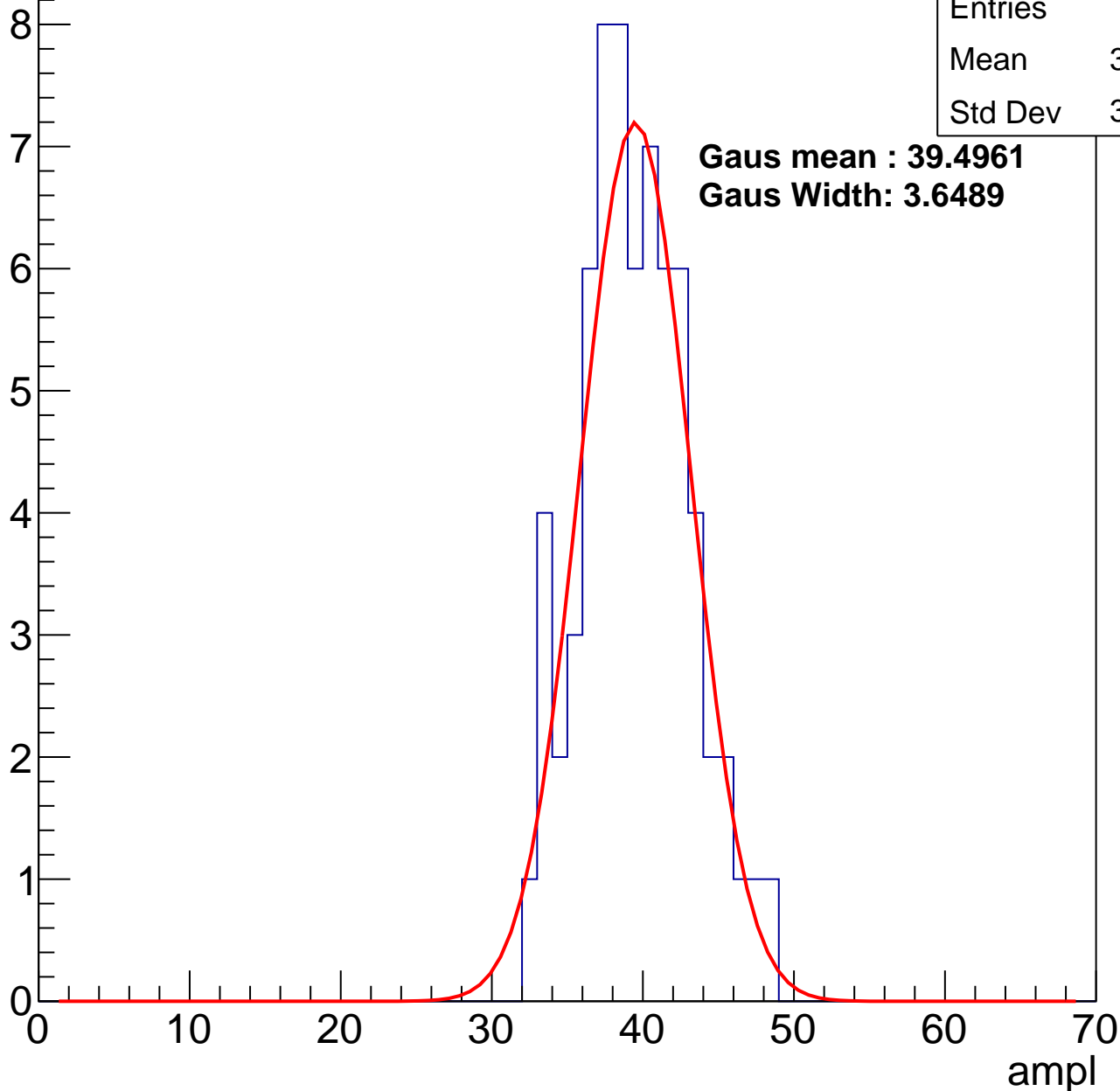
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	39.06
Std Dev	3.535

Gaus mean : 39.4961

Gaus Width: 3.6489



B0L002S, U26-ch19, adc2

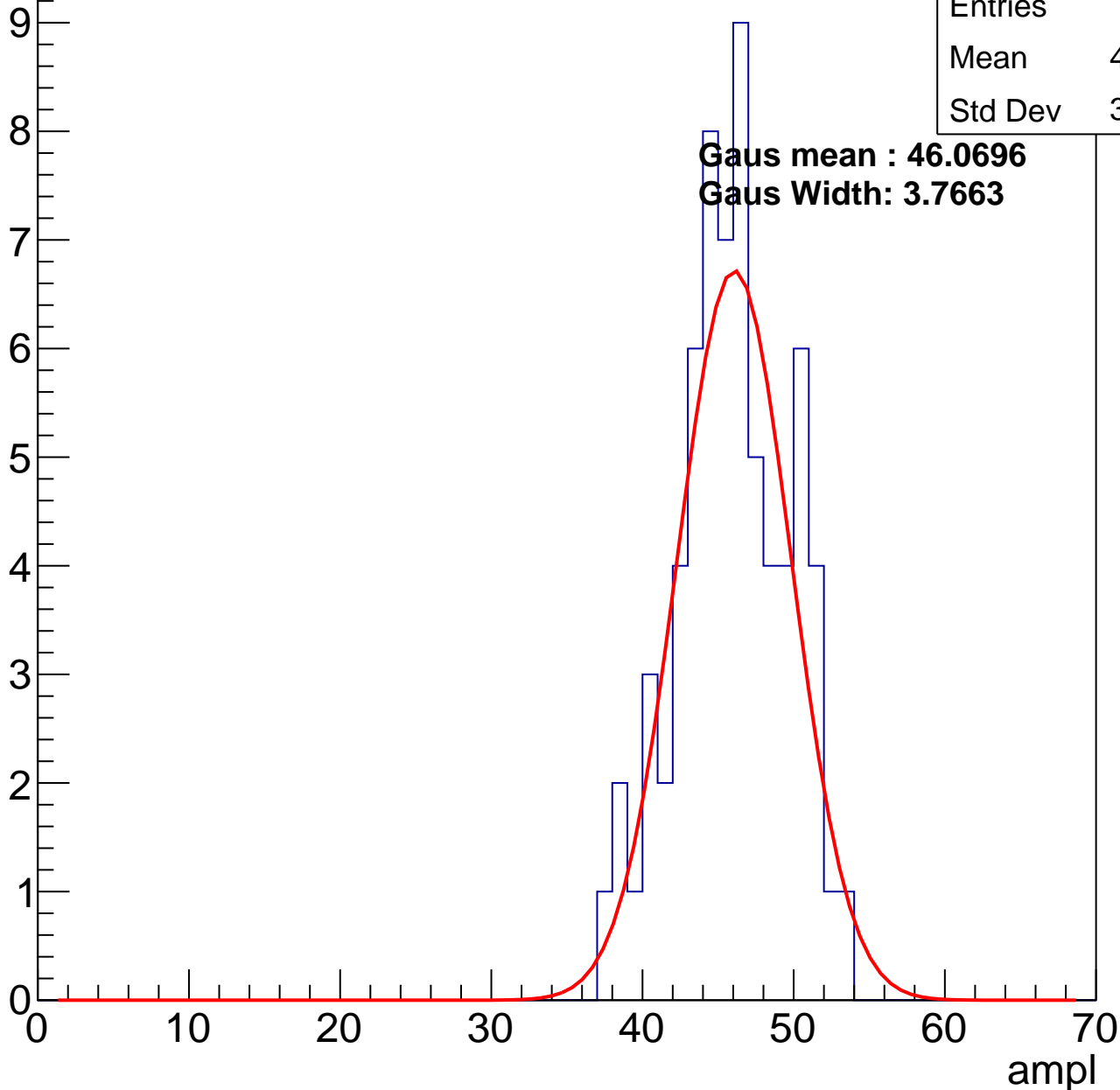
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.49
Std Dev	3.624

Gaus mean : 46.0696

Gaus Width: 3.7663

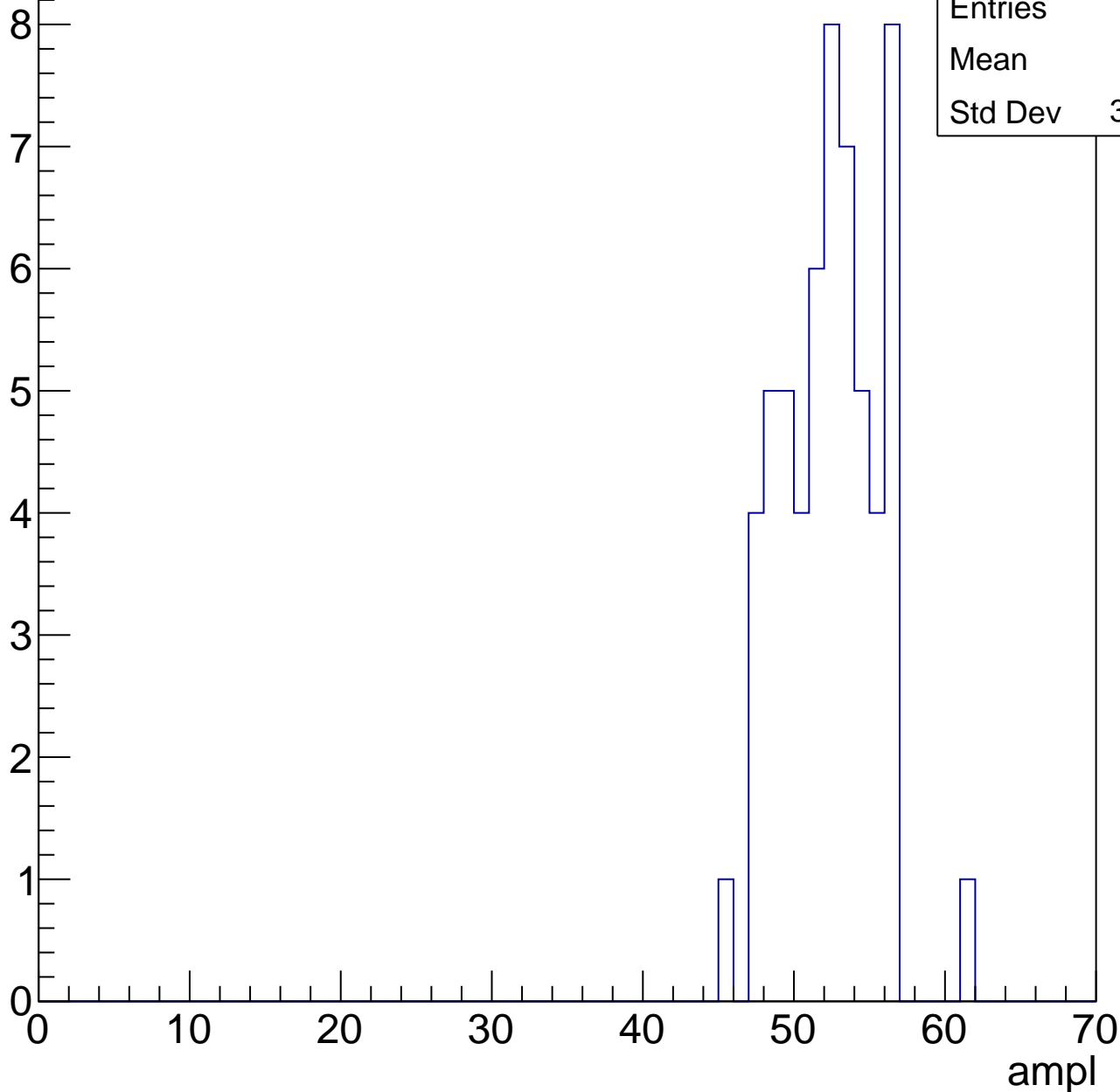


B0L002S, U26-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	51.9
Std Dev	3.128

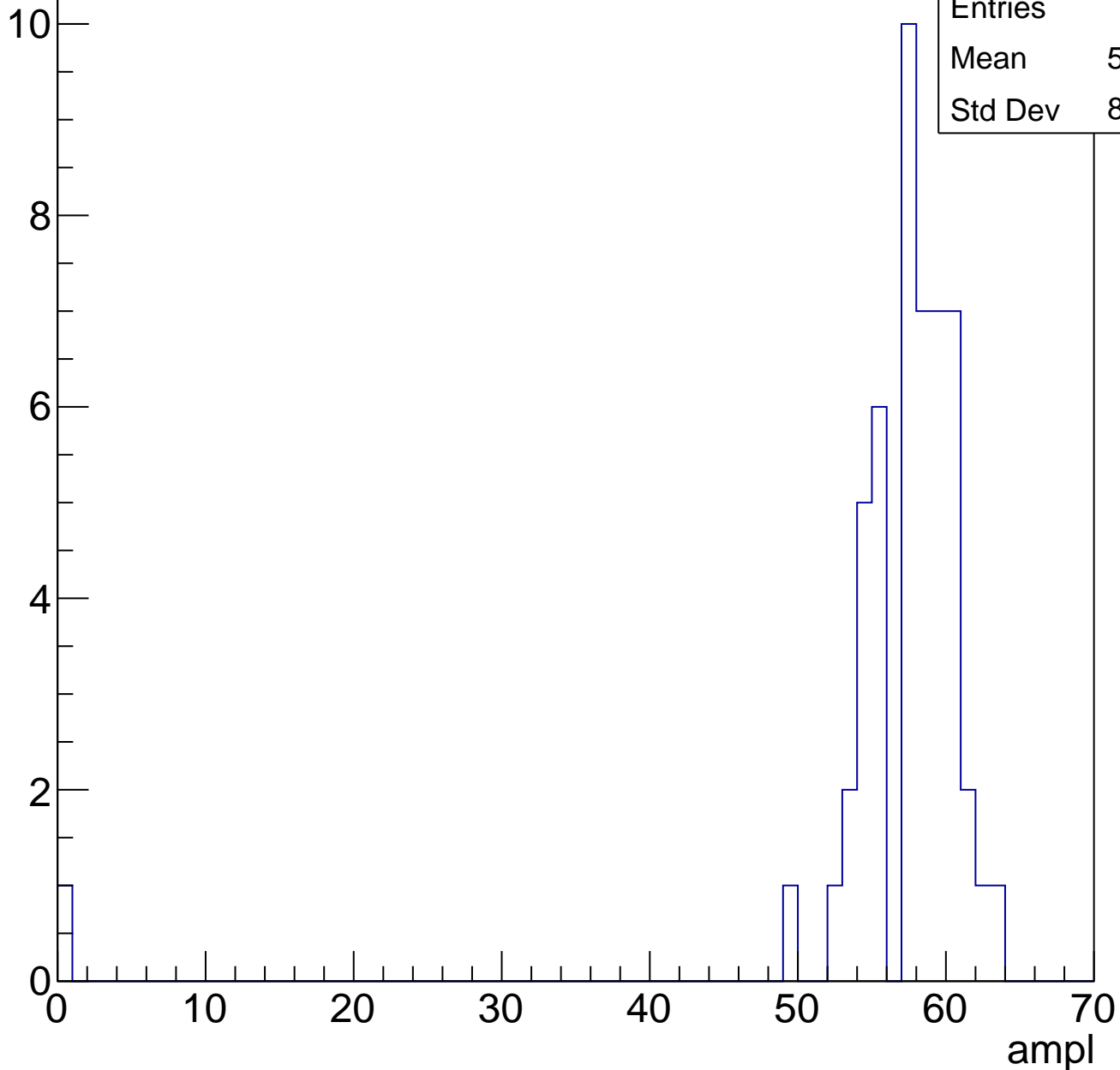


B0L002S, U26-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	51
Mean	56.14
Std Dev	8.392

Entry



B0L002S, U26-ch19, adc5

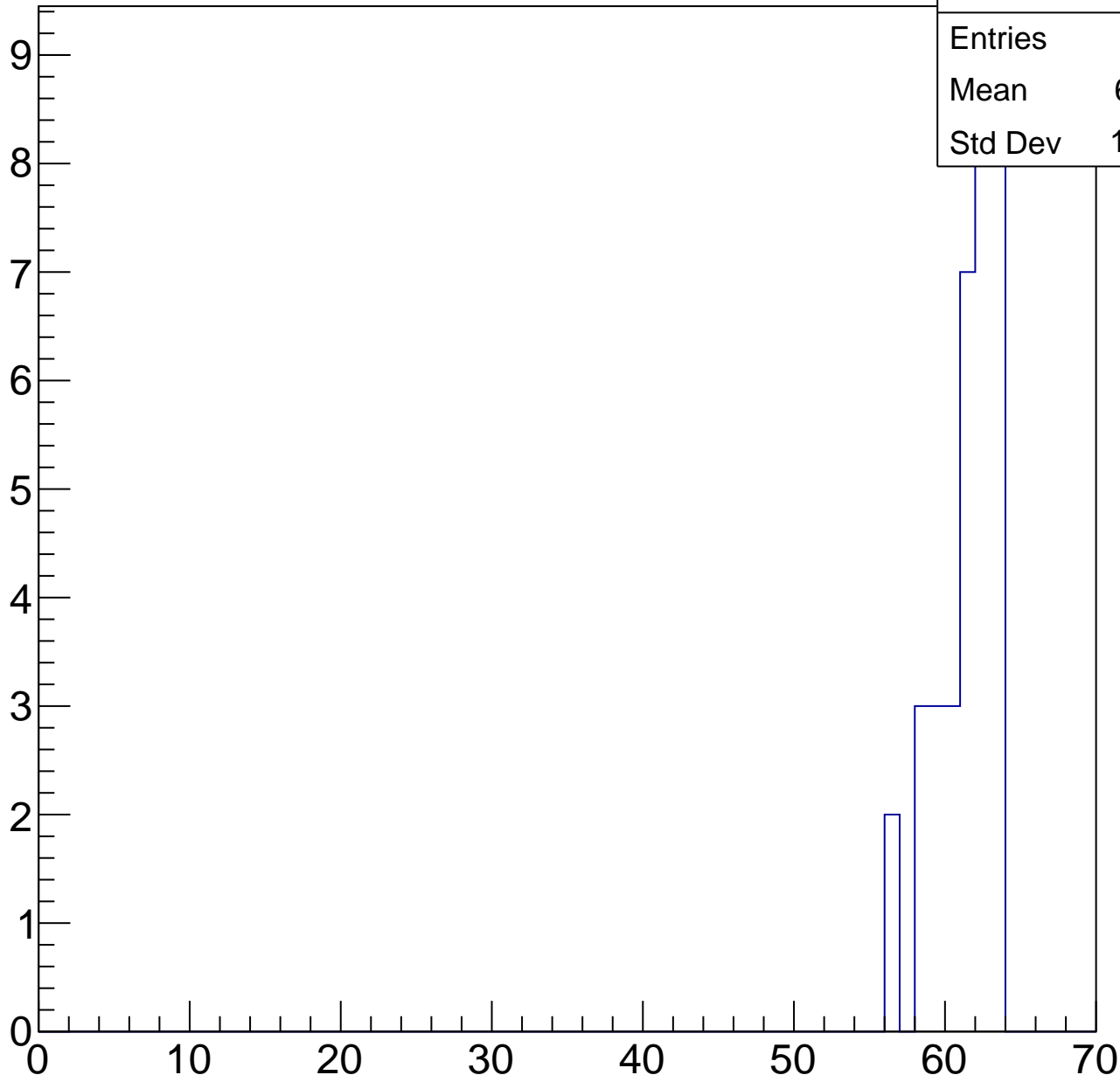
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.91
Std Dev	1.947

ampl



B0L002S, U26-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch20, adc0

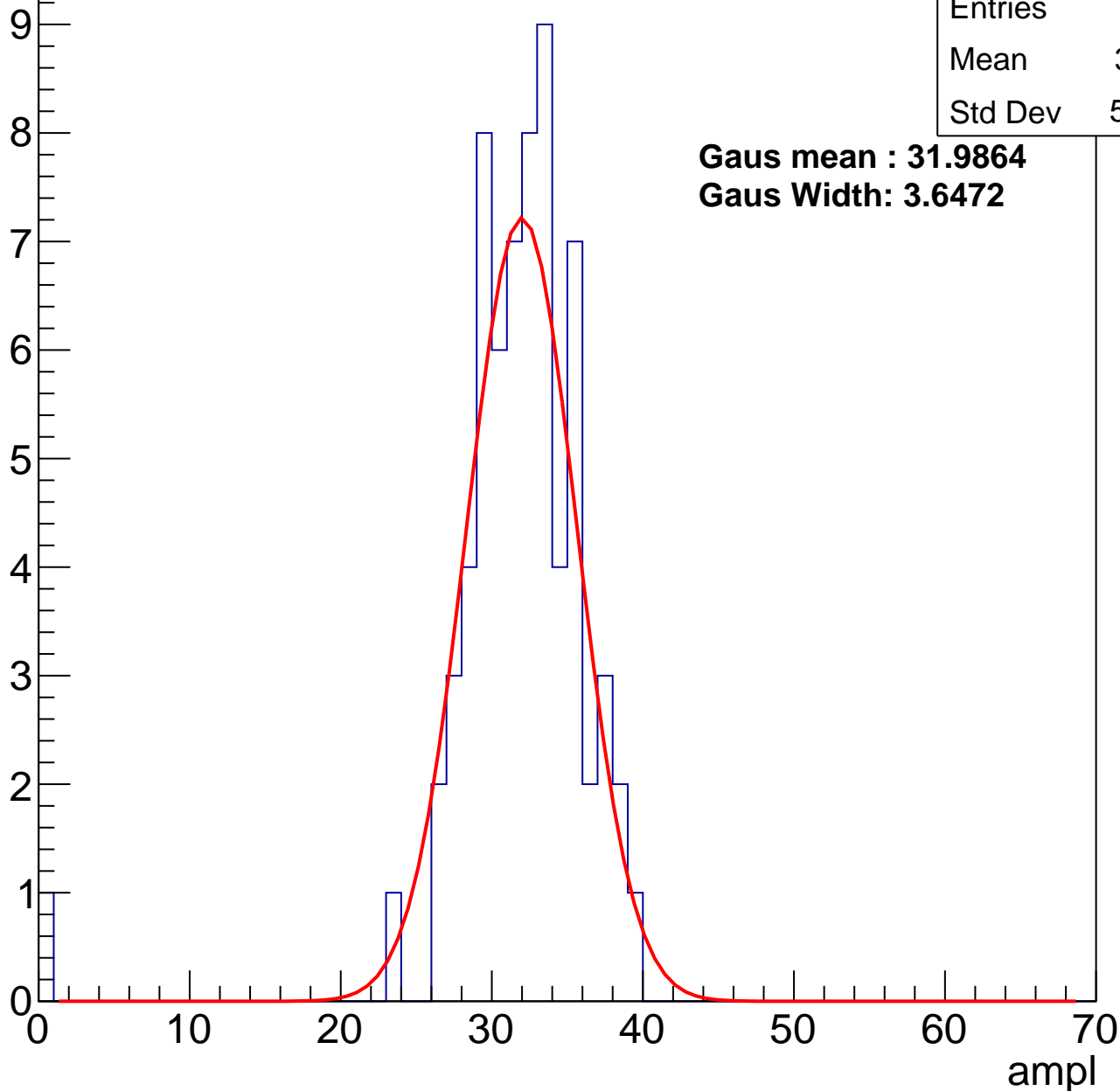
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.31
Std Dev	5.004

Gaus mean : 31.9864

Gaus Width: 3.6472



B0L002S, U26-ch20, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	38.79
Std Dev	3.629

Gaus mean : 39.4454

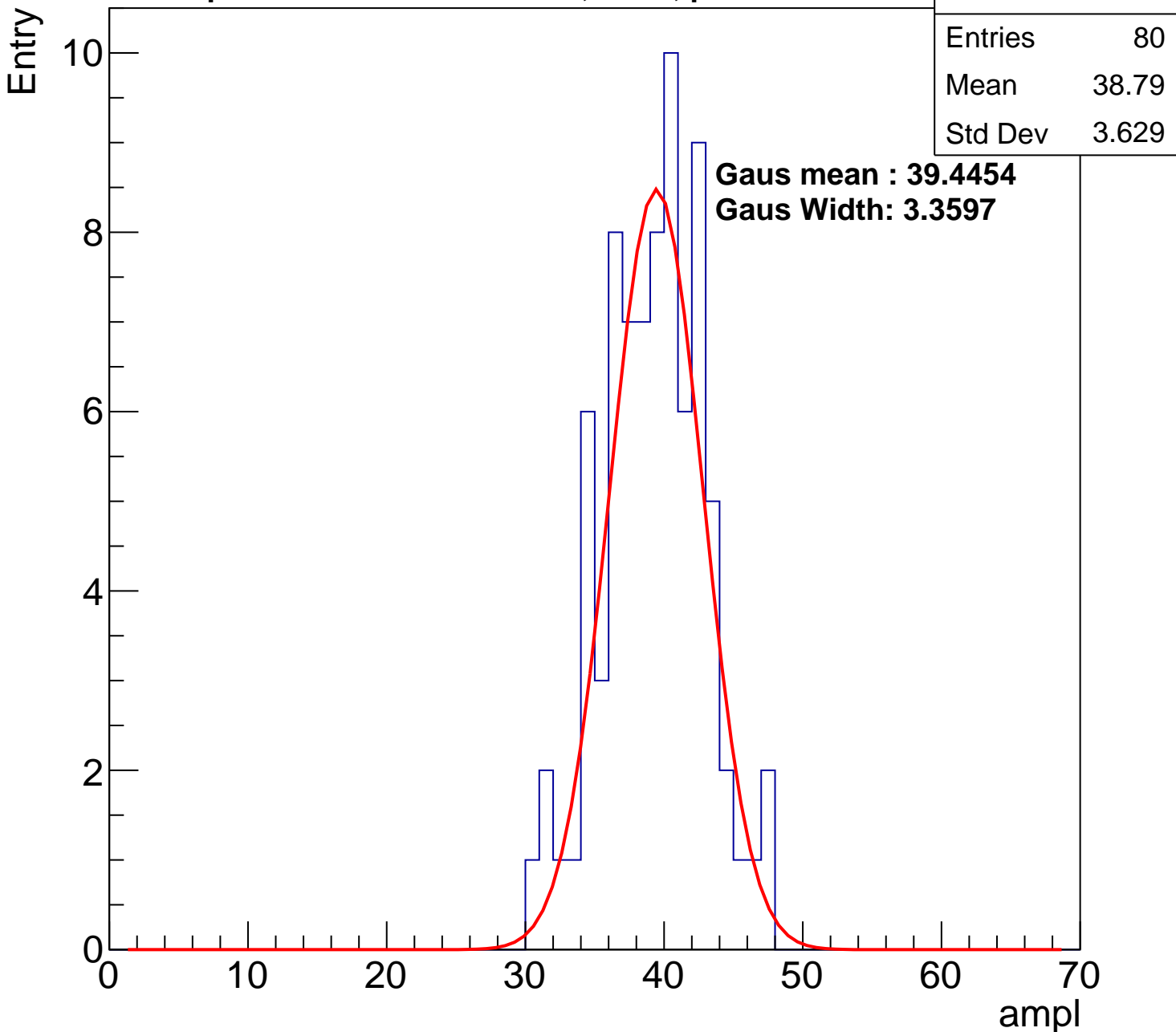
Gaus Width: 3.3597

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch20, adc2

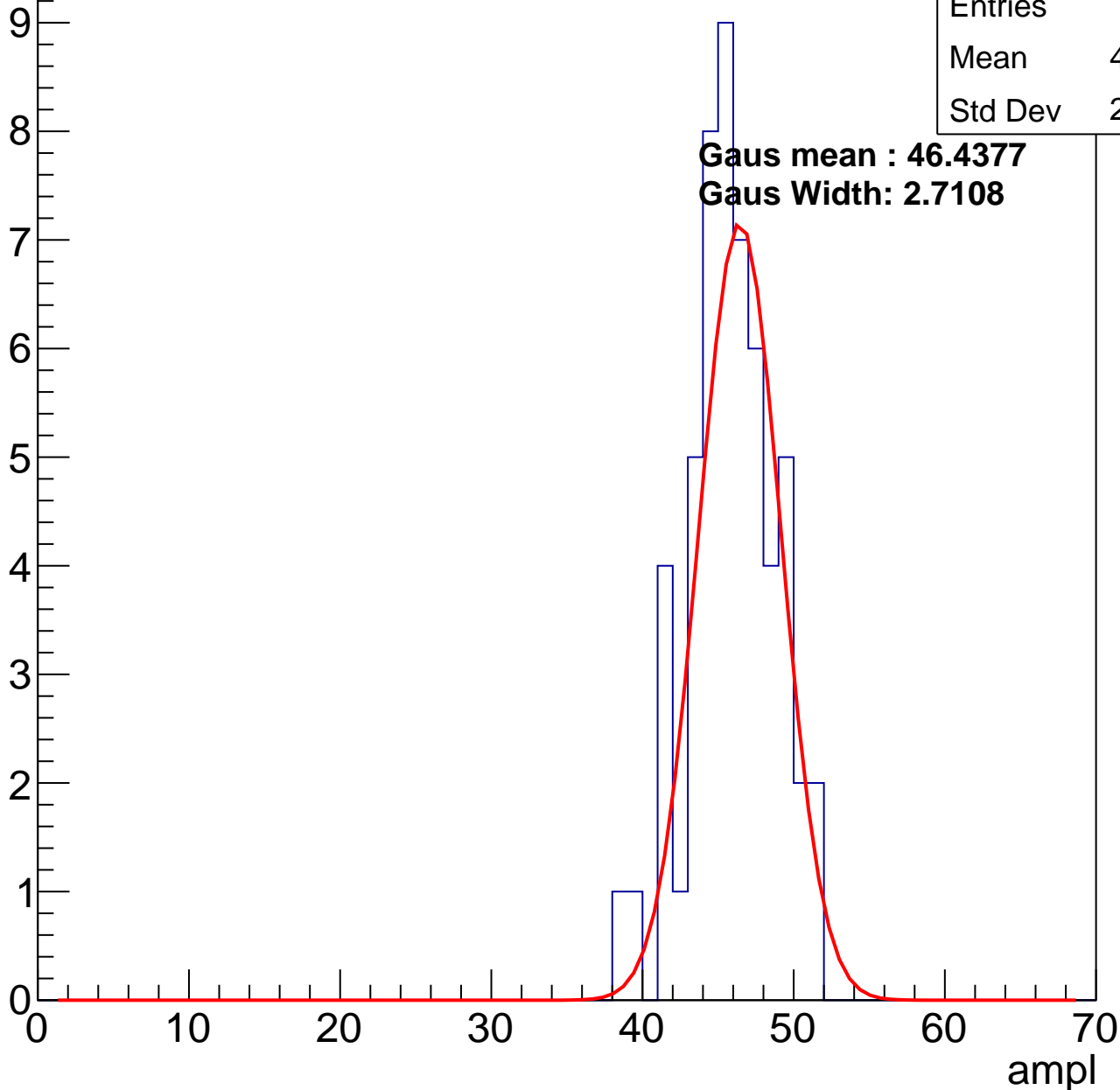
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	45.42
Std Dev	2.846

Gaus mean : 46.4377

Gaus Width: 2.7108

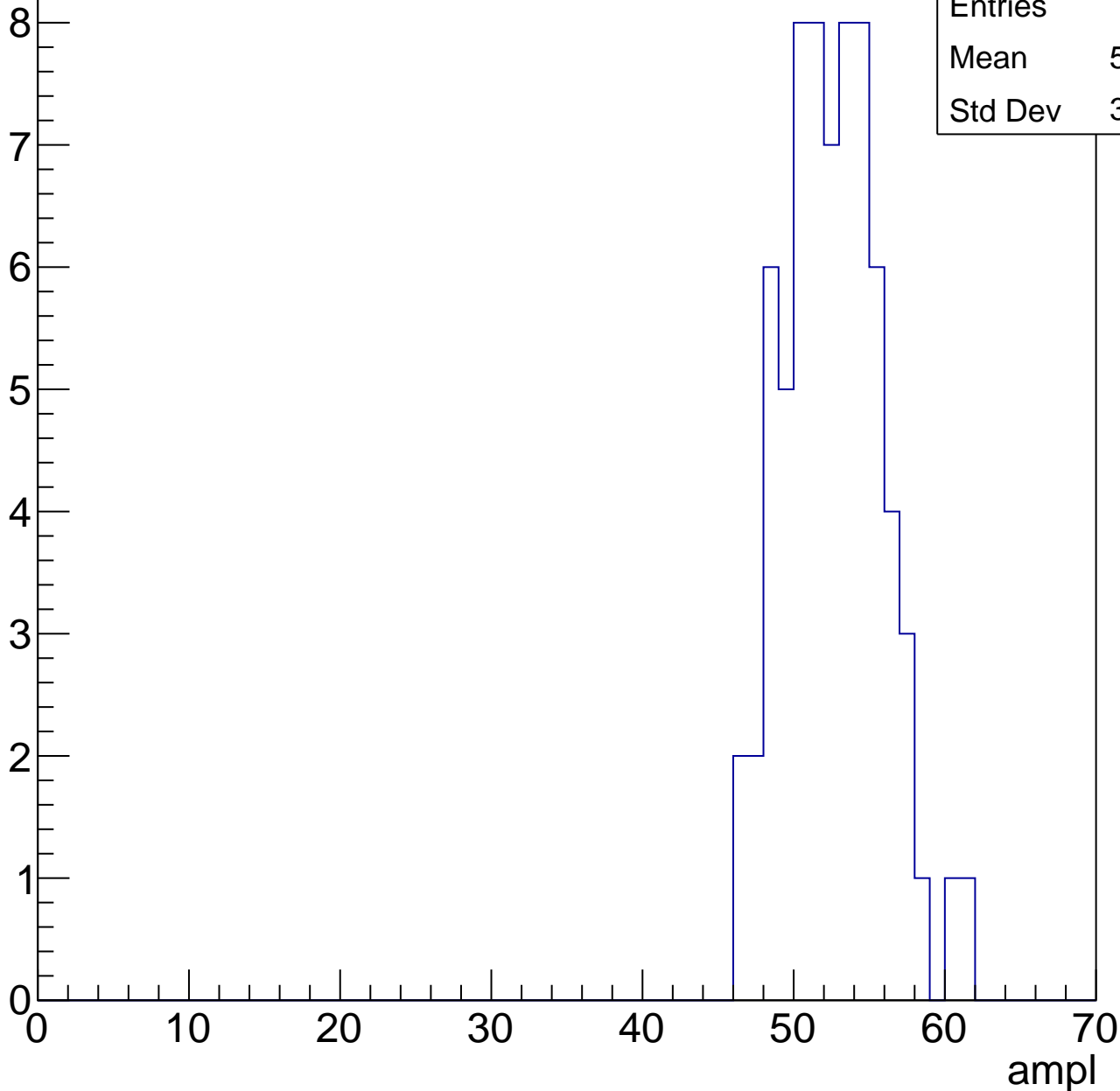


B0L002S, U26-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	52.16
Std Dev	3.197

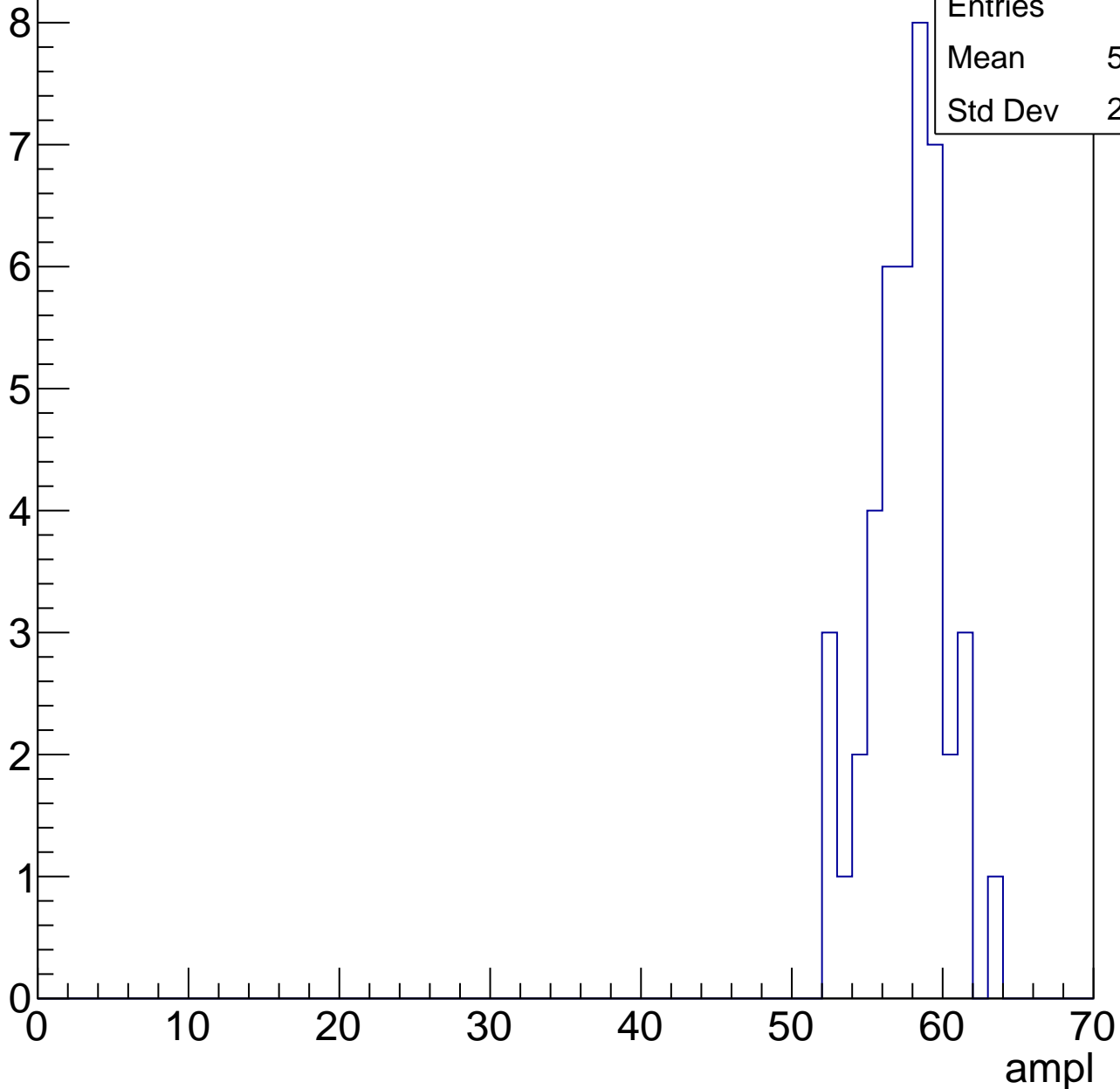


B0L002S, U26-ch20, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	57.16
Std Dev	2.496

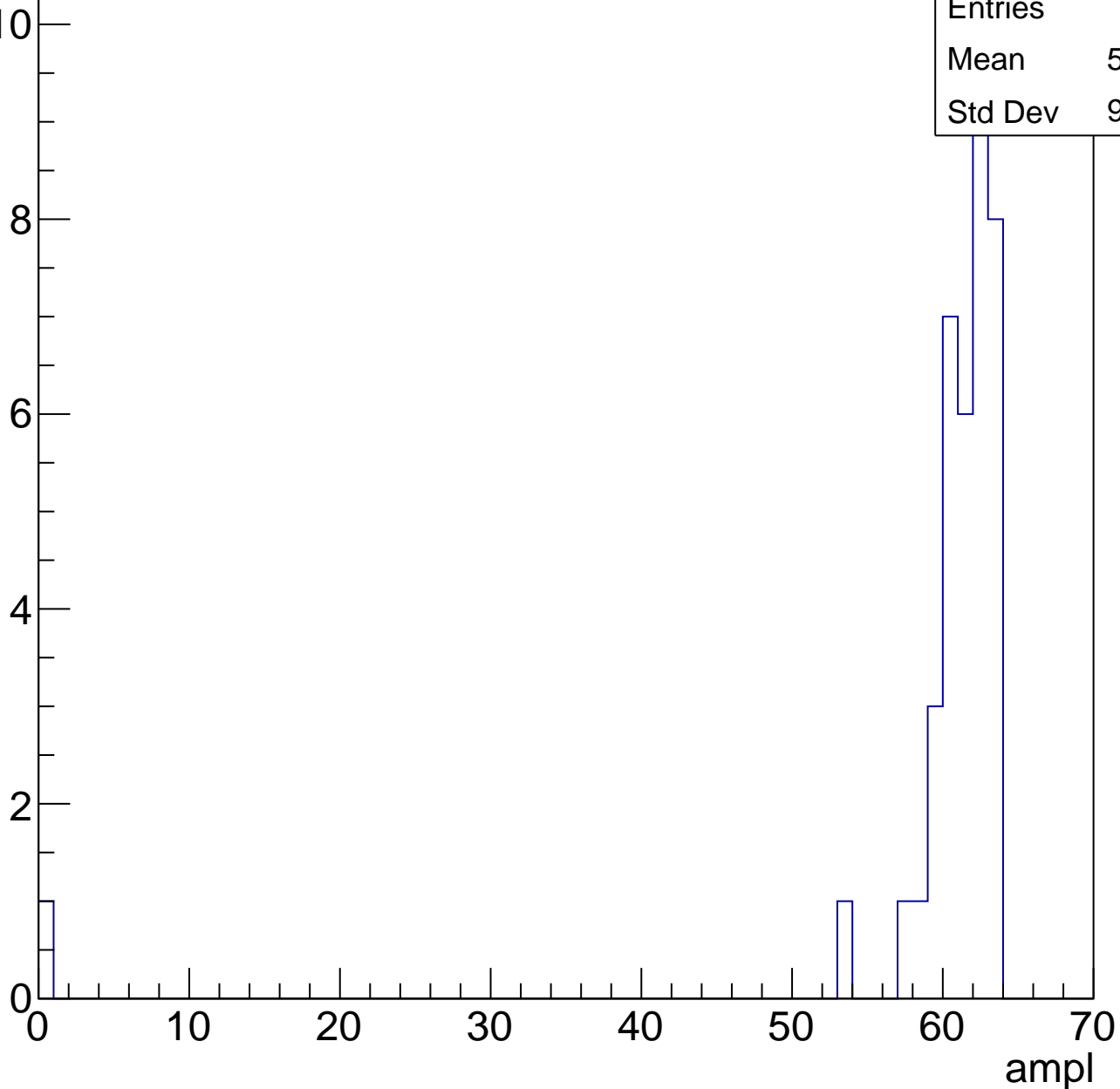


B0L002S, U26-ch20, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.34
Std Dev	9.956



B0L002S, U26-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch21, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	95
Mean	32.16
Std Dev	3.988

Gaus mean : 32.6023

Gaus Width: 3.9296

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L002S, U26-ch21, adc1

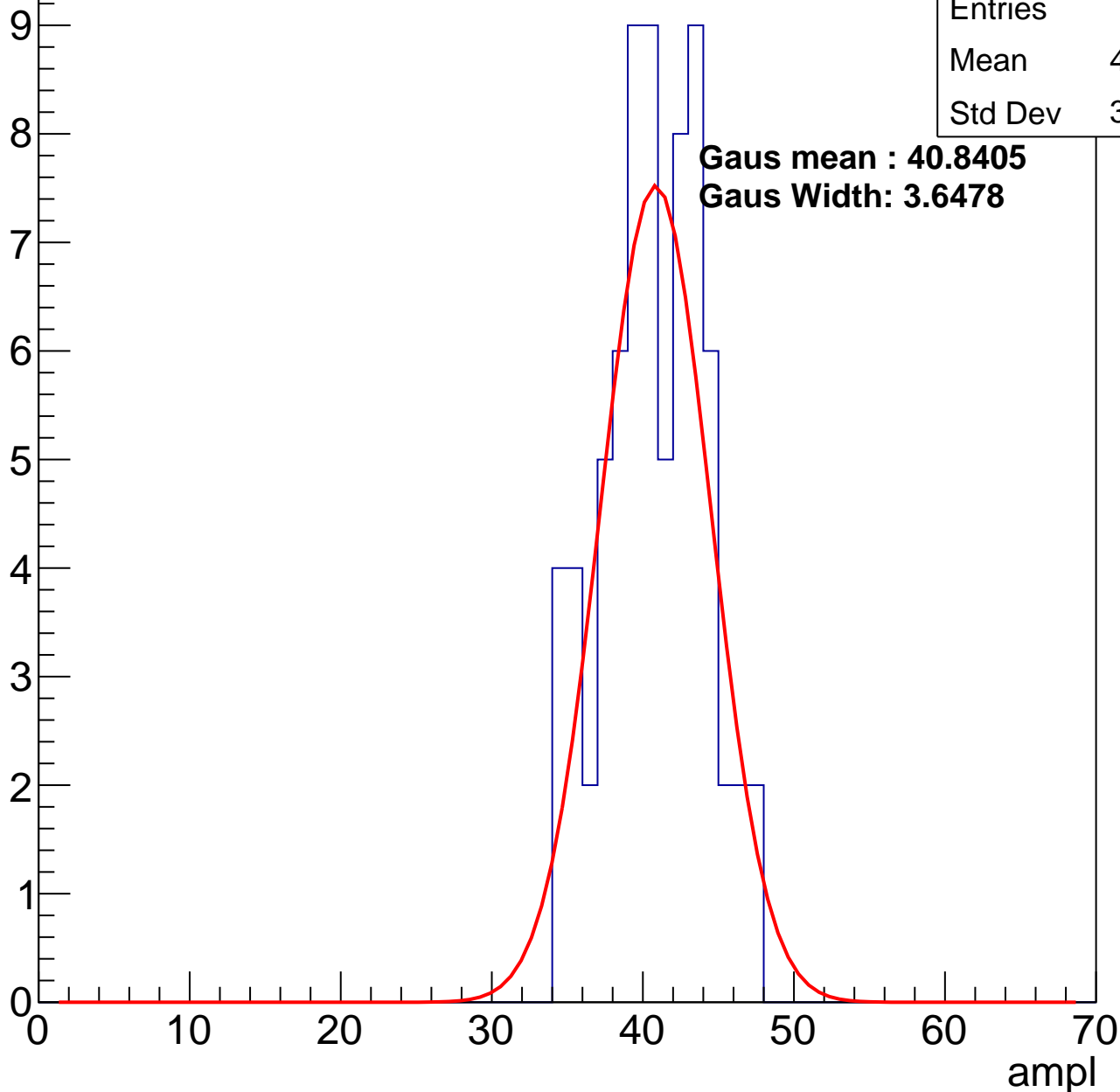
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	40.27
Std Dev	3.274

Gaus mean : 40.8405

Gaus Width: 3.6478



B0L002S, U26-ch21, adc2

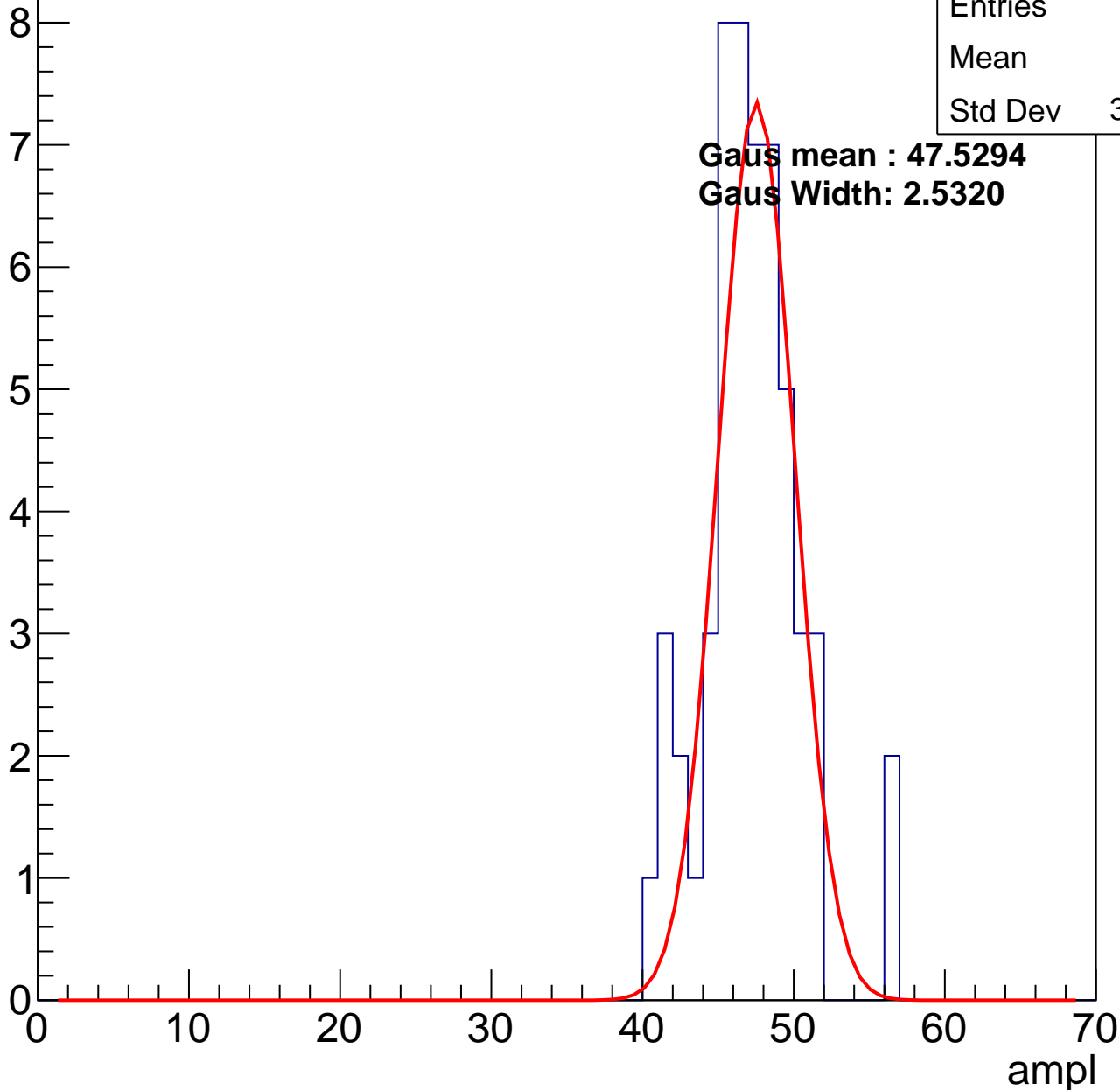
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	46.7
Std Dev	3.219

Gaus mean : 47.5294

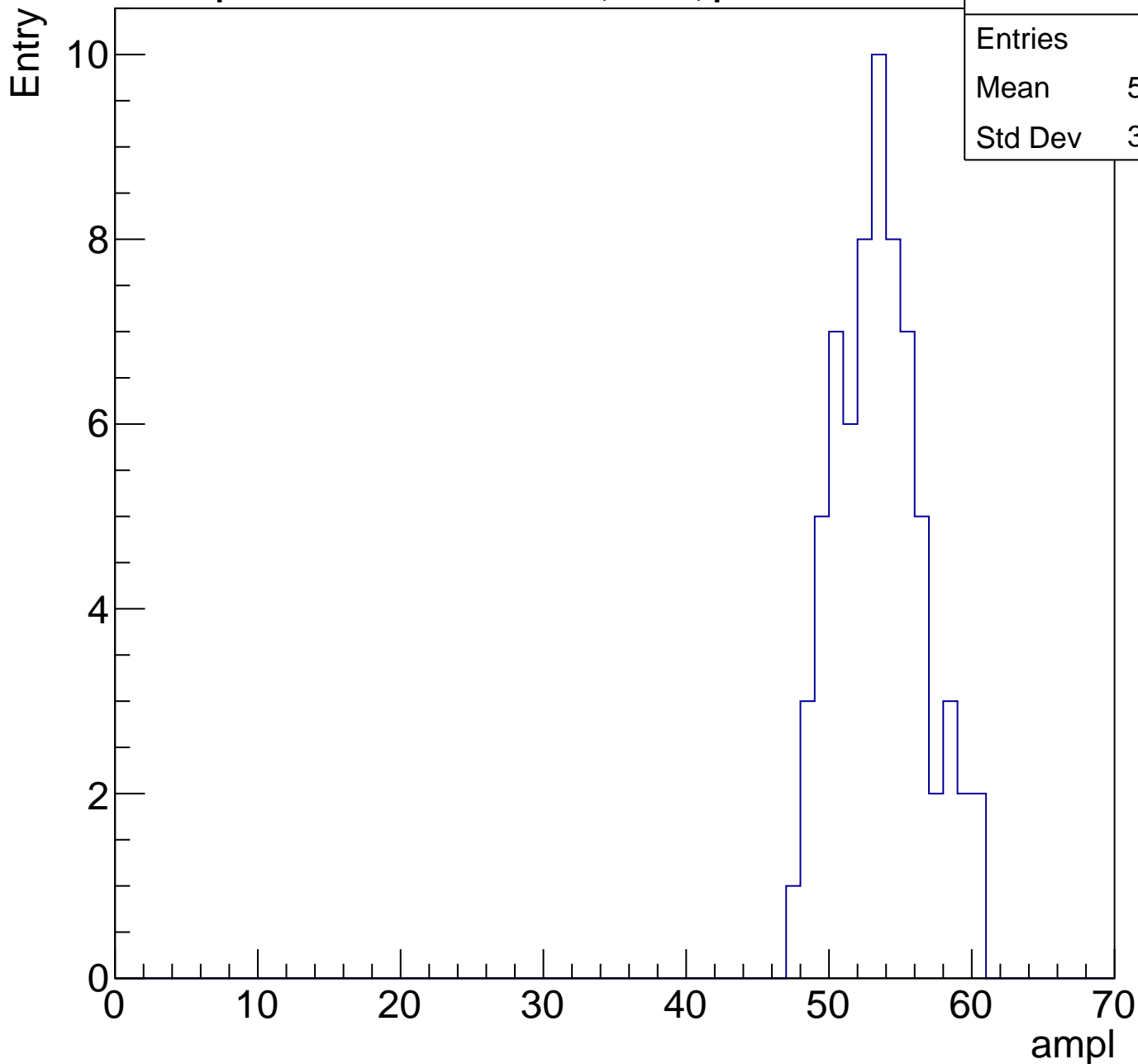
Gaus Width: 2.5320



B0L002S, U26-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	53.06
Std Dev	3.055

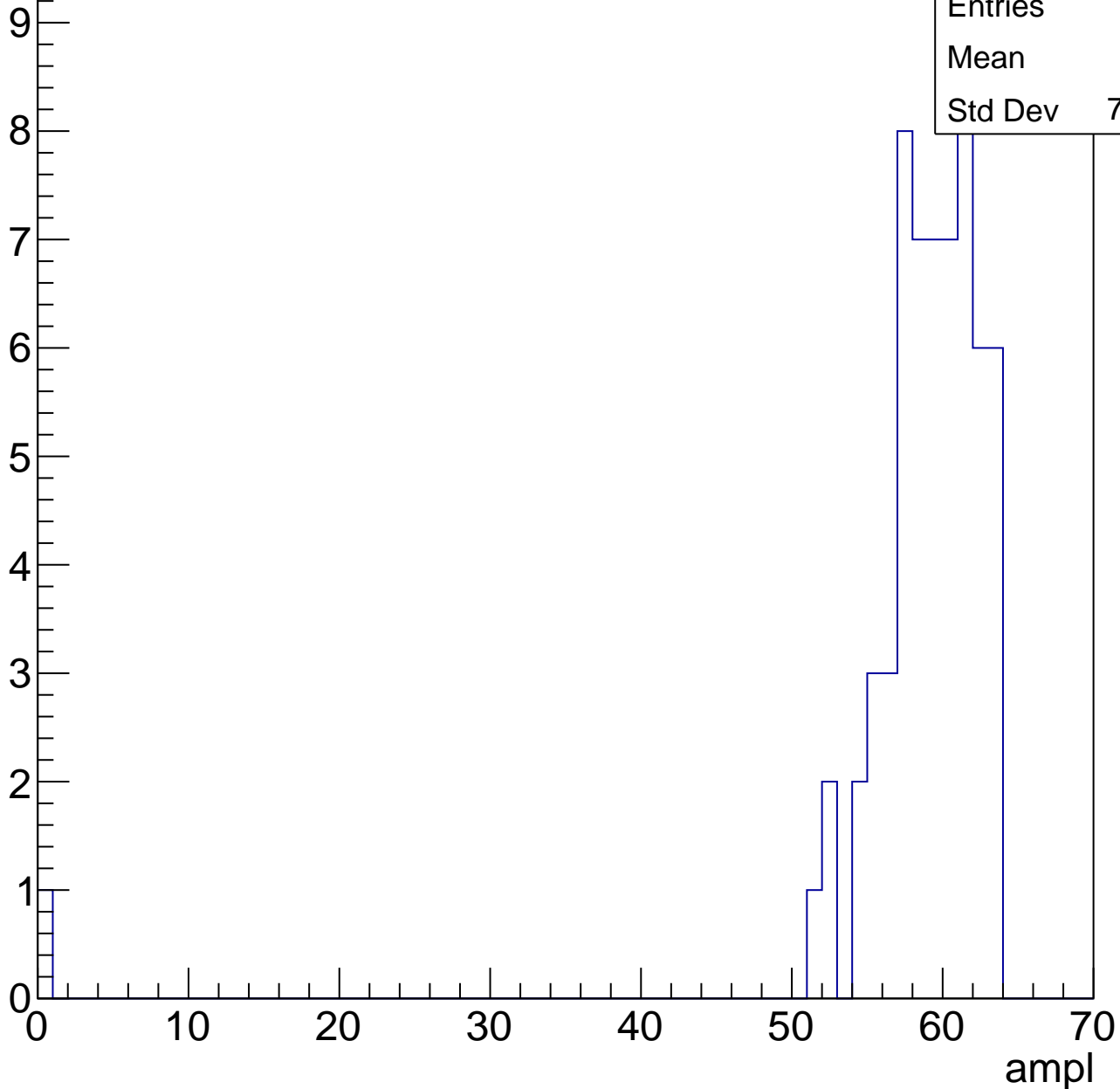


B0L002S, U26-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

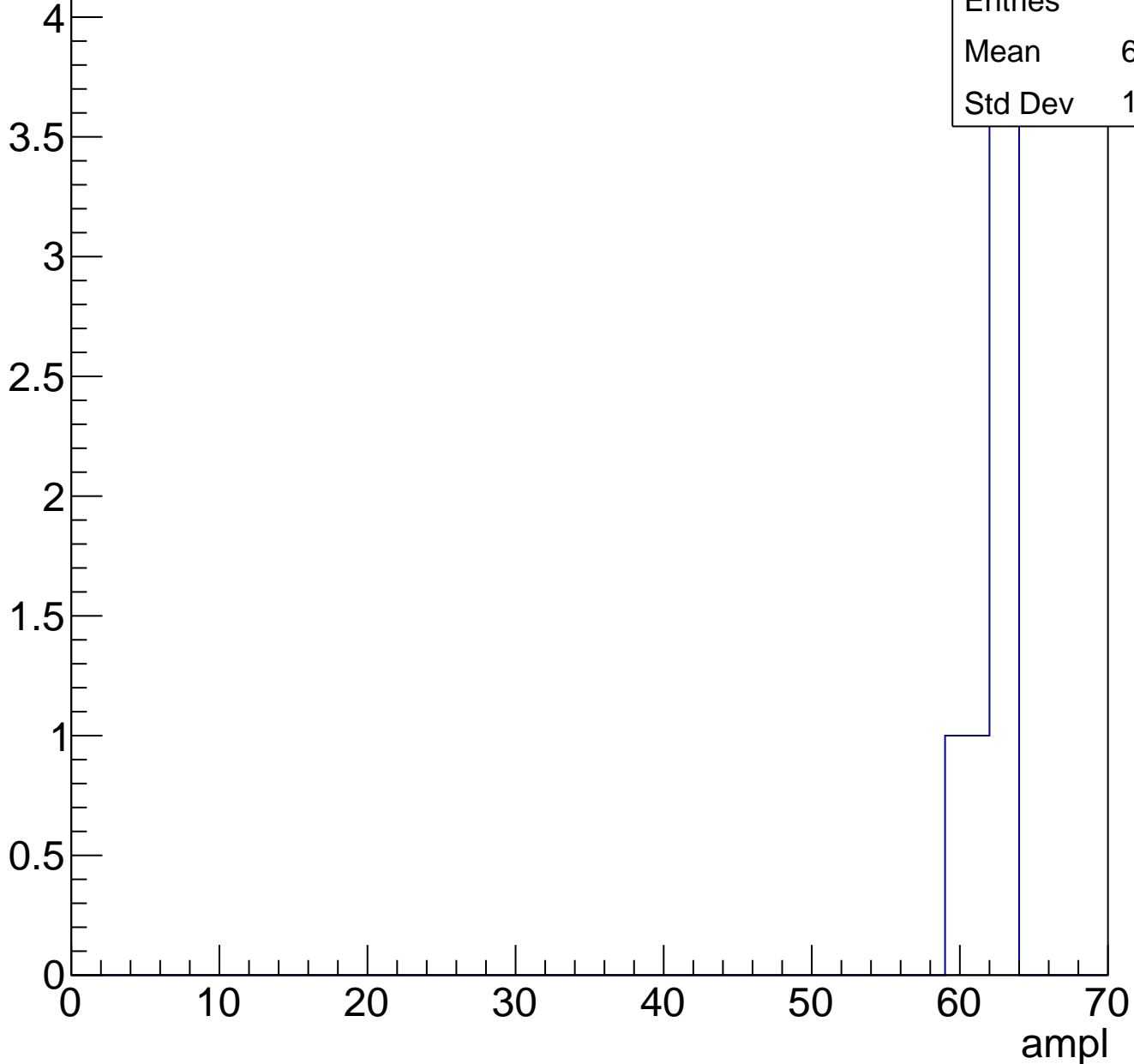
Entries	62
Mean	57.9
Std Dev	7.957



B0L002S, U26-ch21, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L002S, U26-ch22, adc0

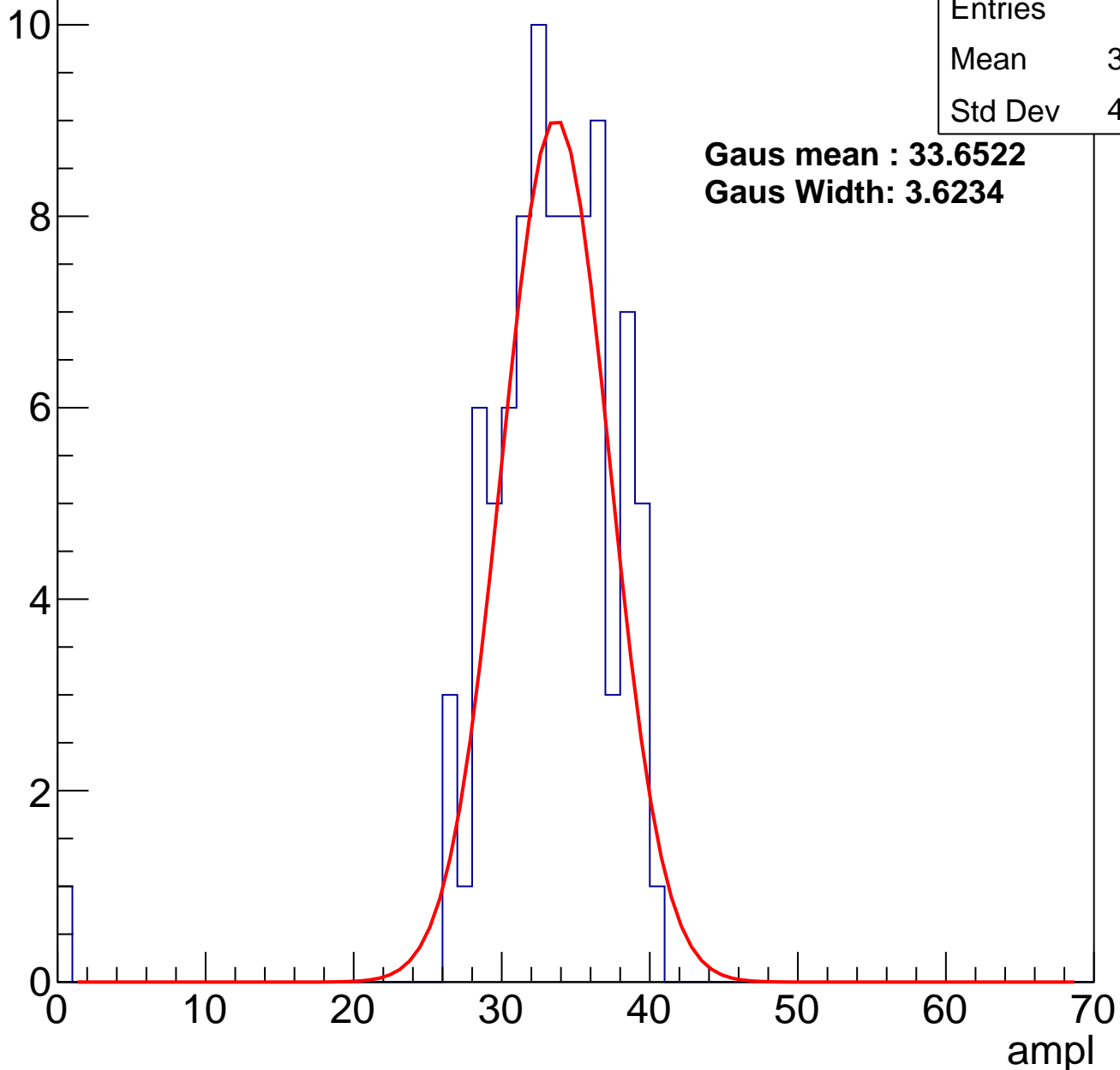
calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	32.79
Std Dev	4.934

Gaus mean : 33.6522

Gaus Width: 3.6234

Entry



B0L002S, U26-ch22, adc1

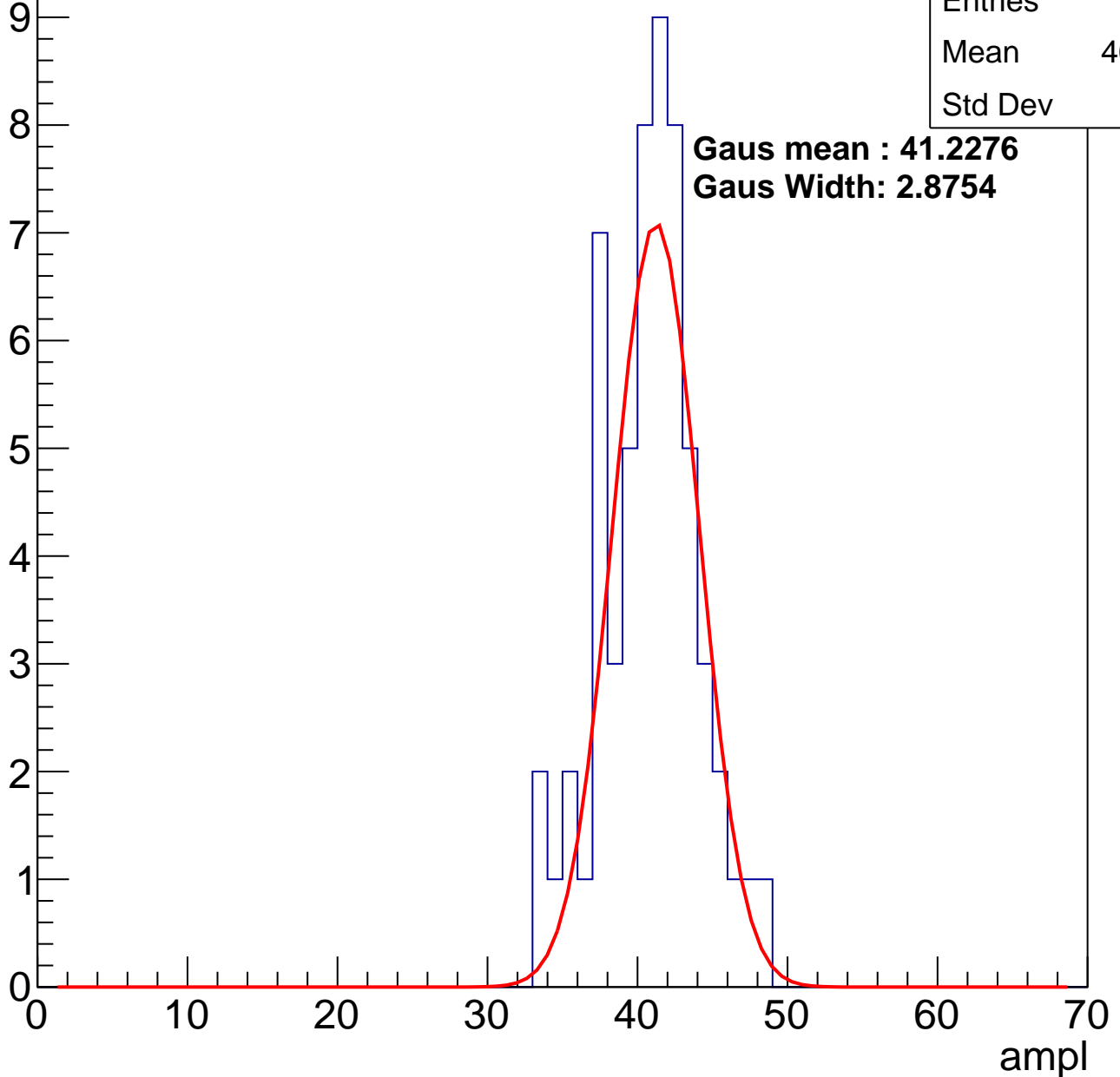
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	40.29
Std Dev	3.21

Gaus mean : 41.2276

Gaus Width: 2.8754



B0L002S, U26-ch22, adc2

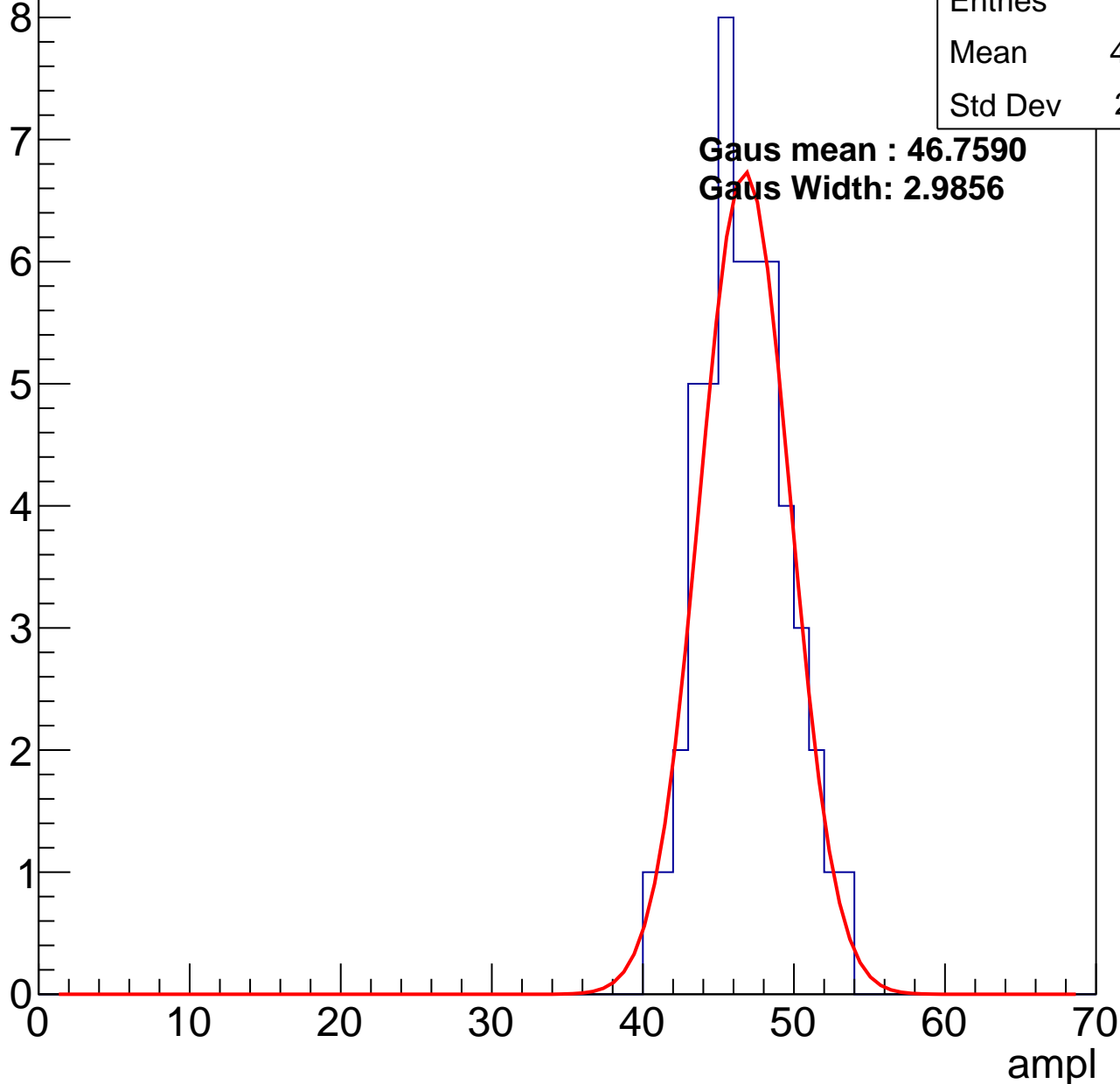
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	46.25
Std Dev	2.841

Gaus mean : 46.7590

Gaus Width: 2.9856

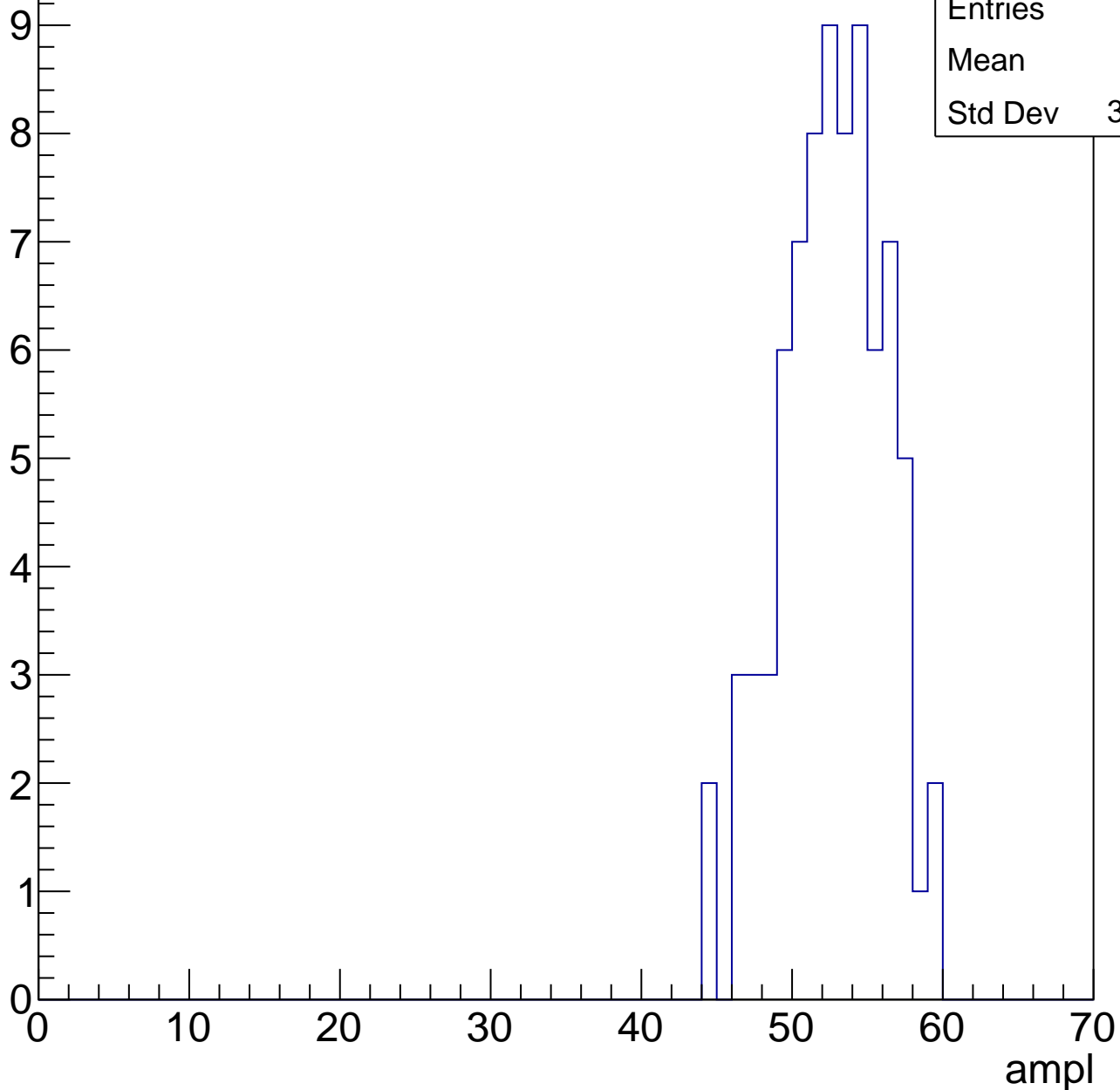


B0L002S, U26-ch22, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	52.2
Std Dev	3.403

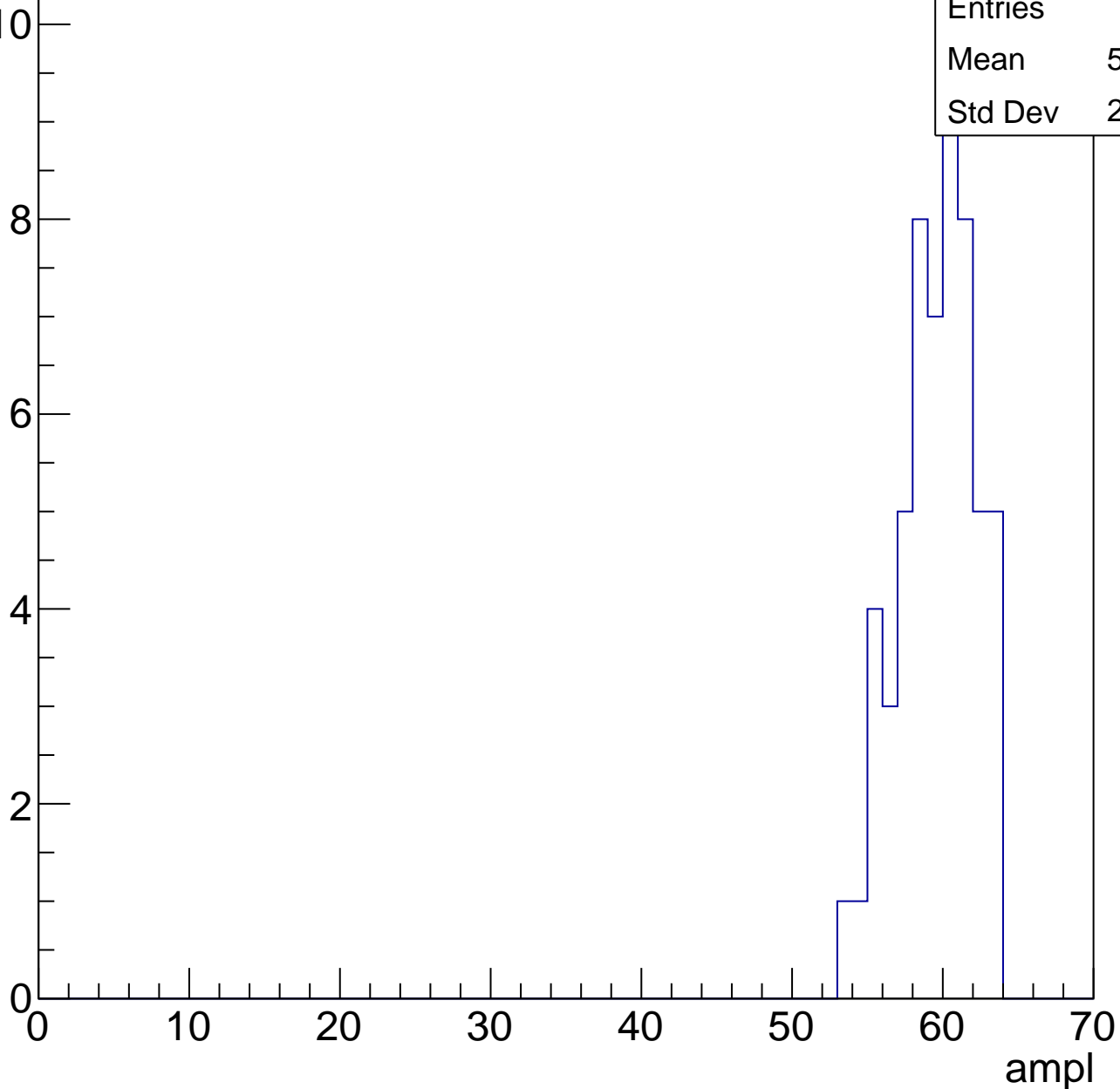


B0L002S, U26-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	59.12
Std Dev	2.464

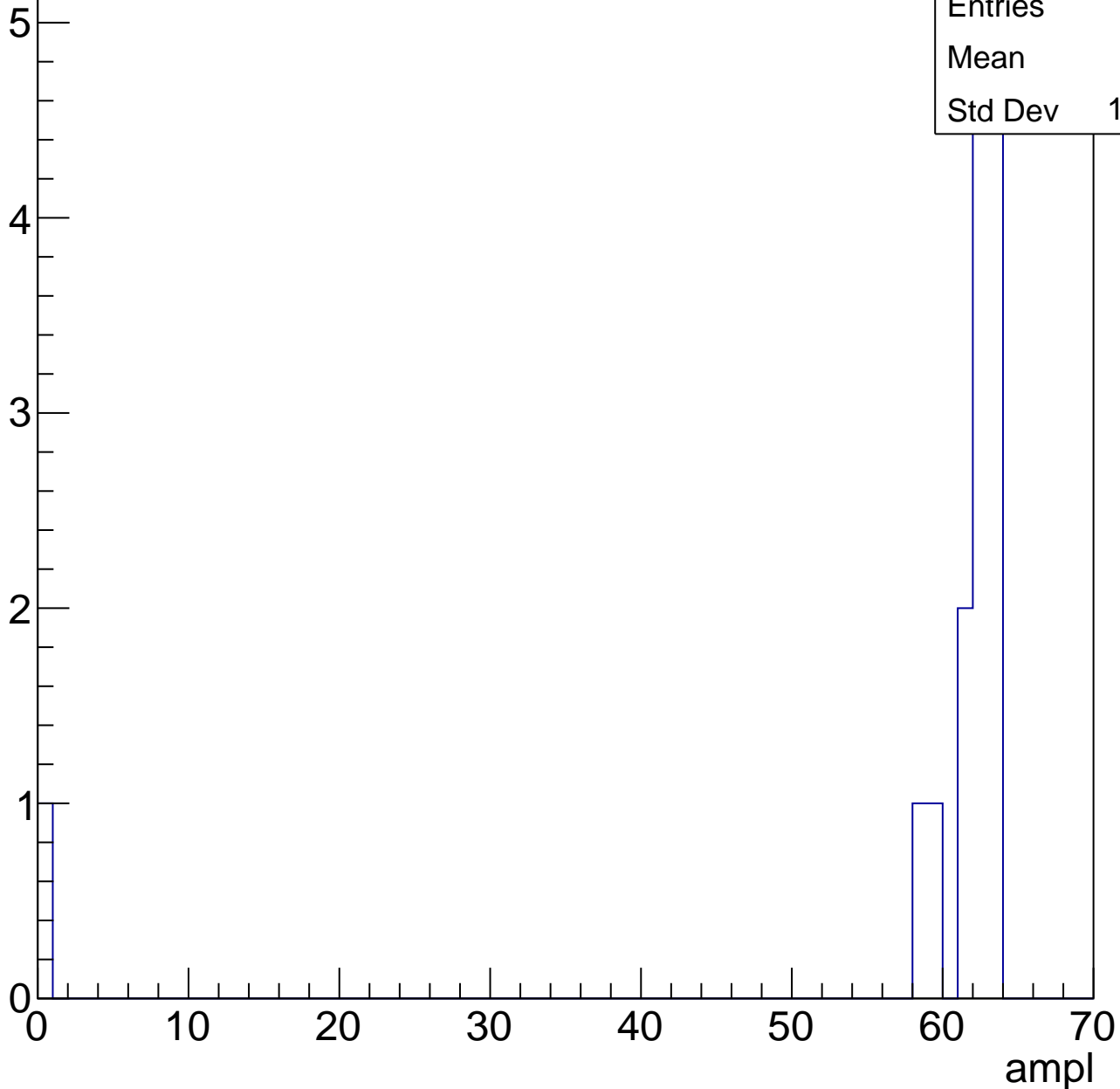


B0L002S, U26-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	15
Mean	57.6
Std Dev	15.46



B0L002S, U26-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch23, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	30.76
Std Dev	3.68

Gaus mean : 31.2581

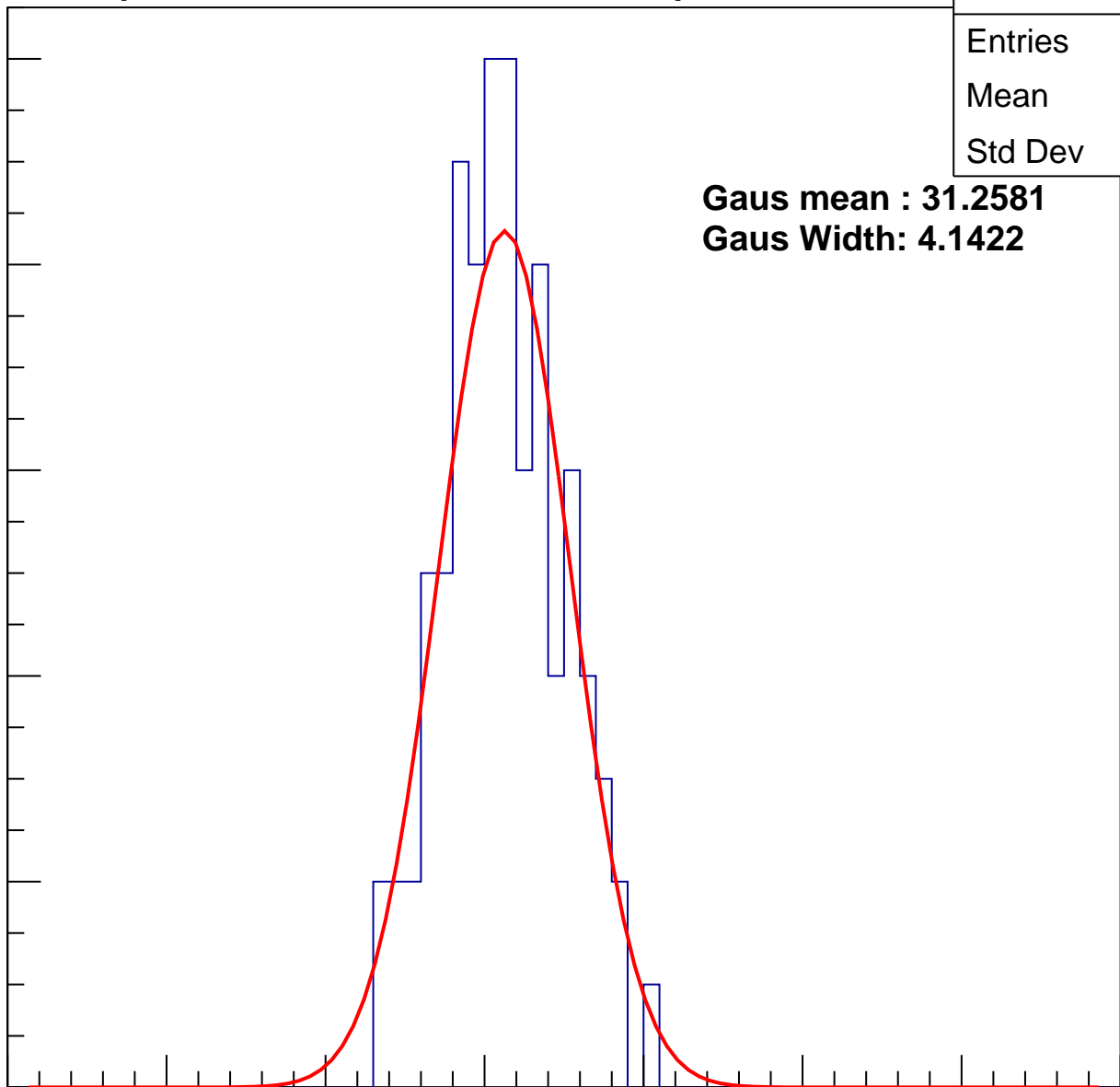
Gaus Width: 4.1422

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch23, adc1

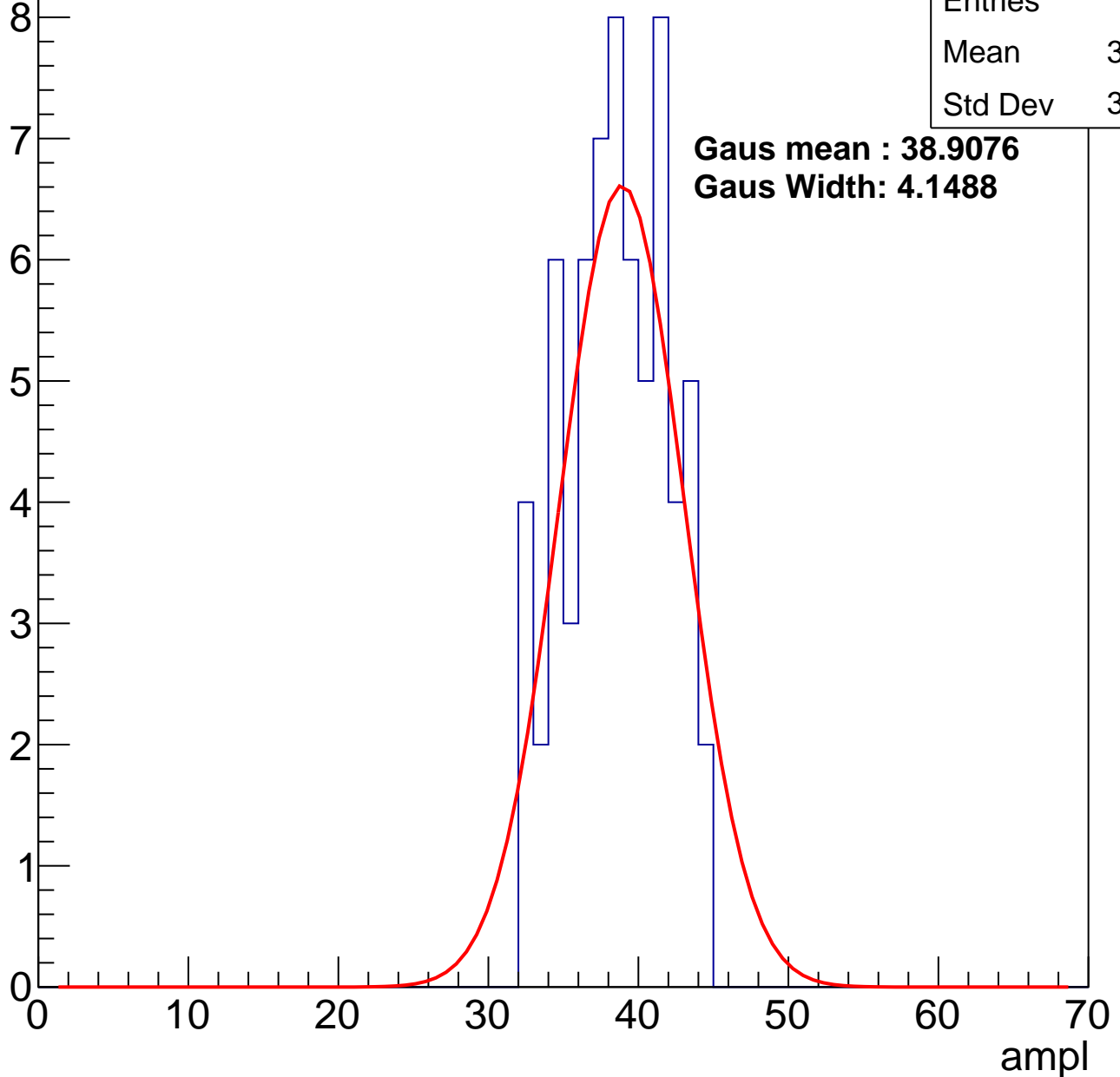
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.11
Std Dev	3.271

Gaus mean : 38.9076

Gaus Width: 4.1488



B0L002S, U26-ch23, adc2

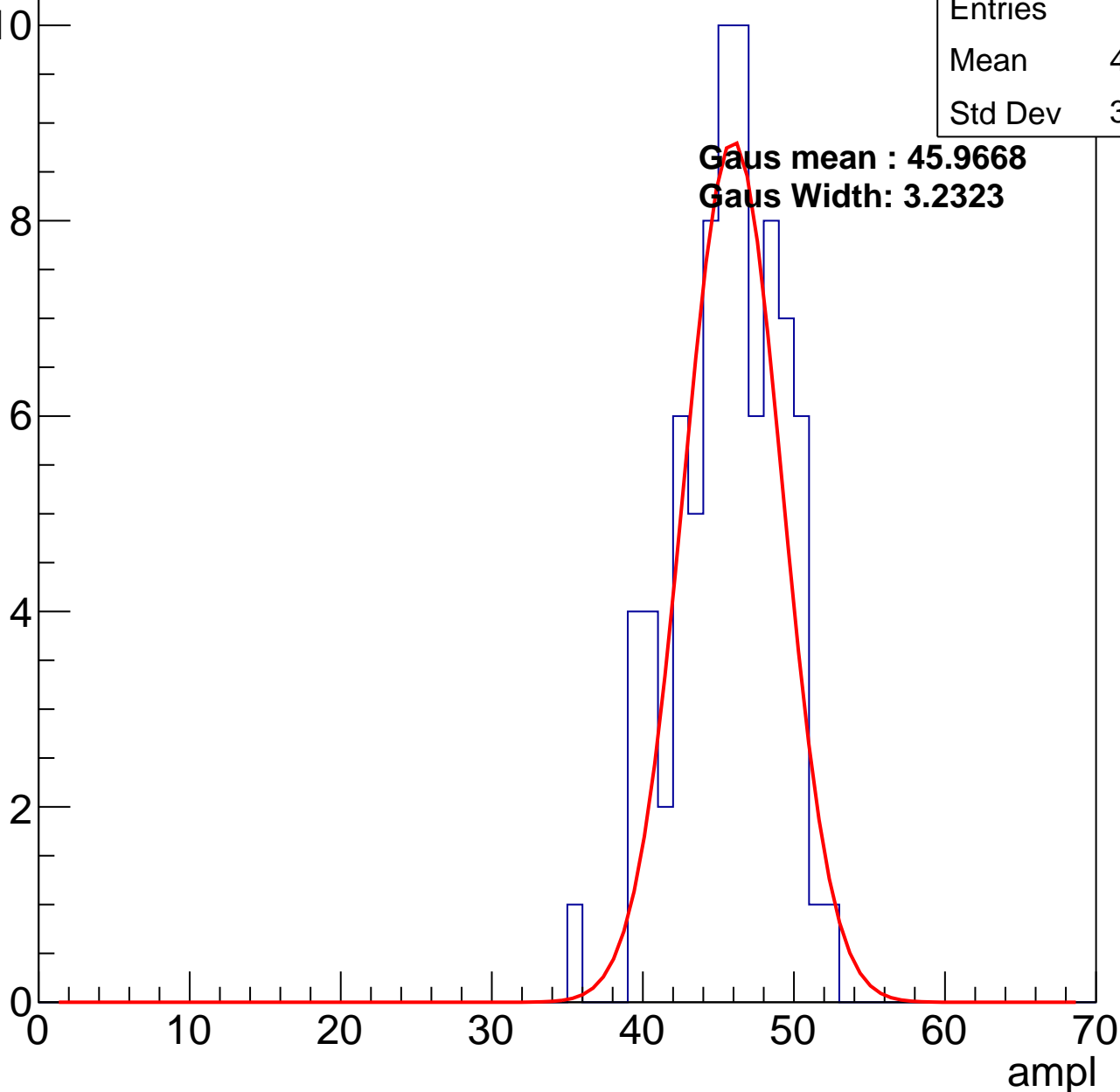
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	45.24
Std Dev	3.387

Gaus mean : 45.9668

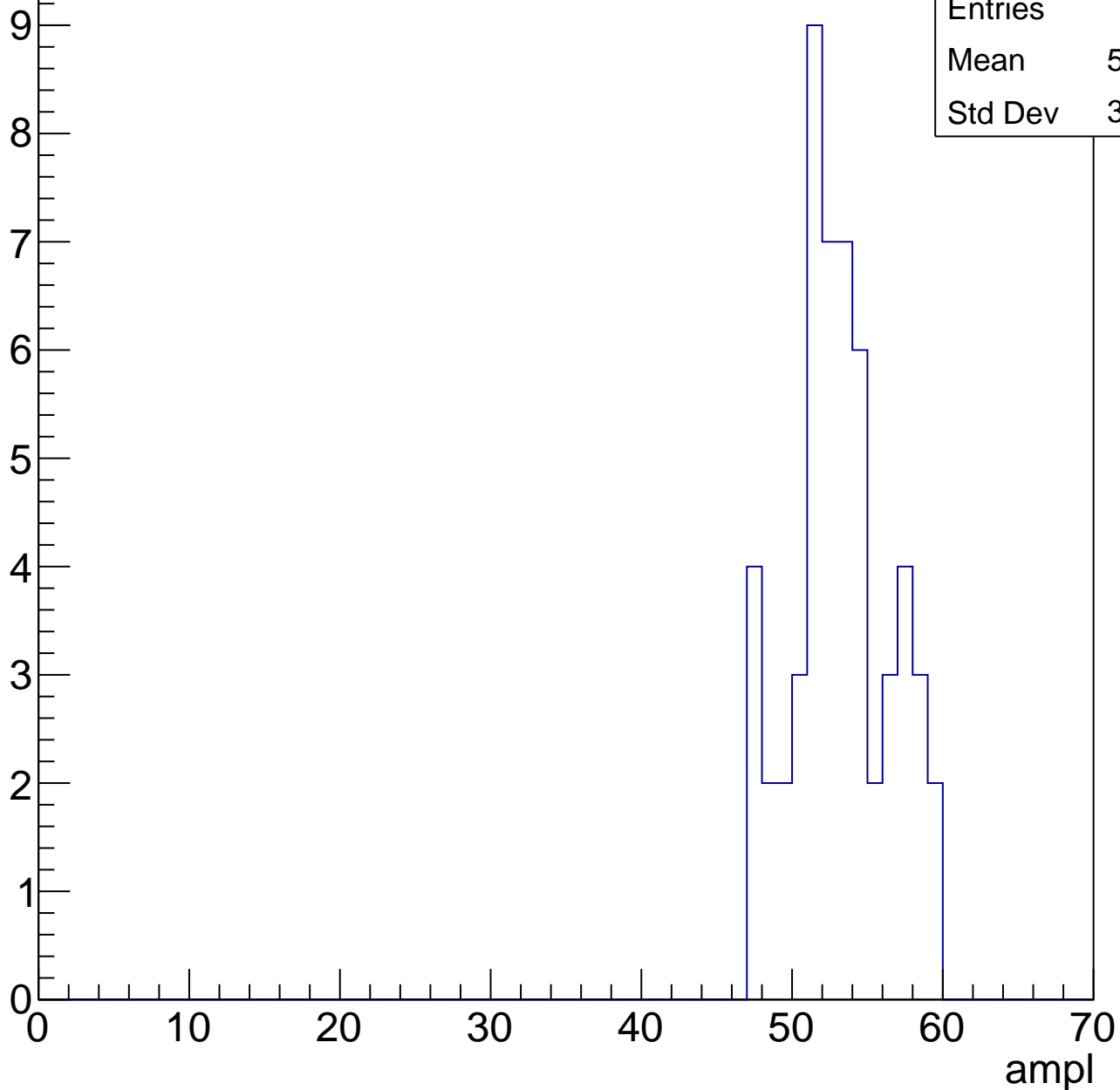
Gaus Width: 3.2323



B0L002S, U26-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

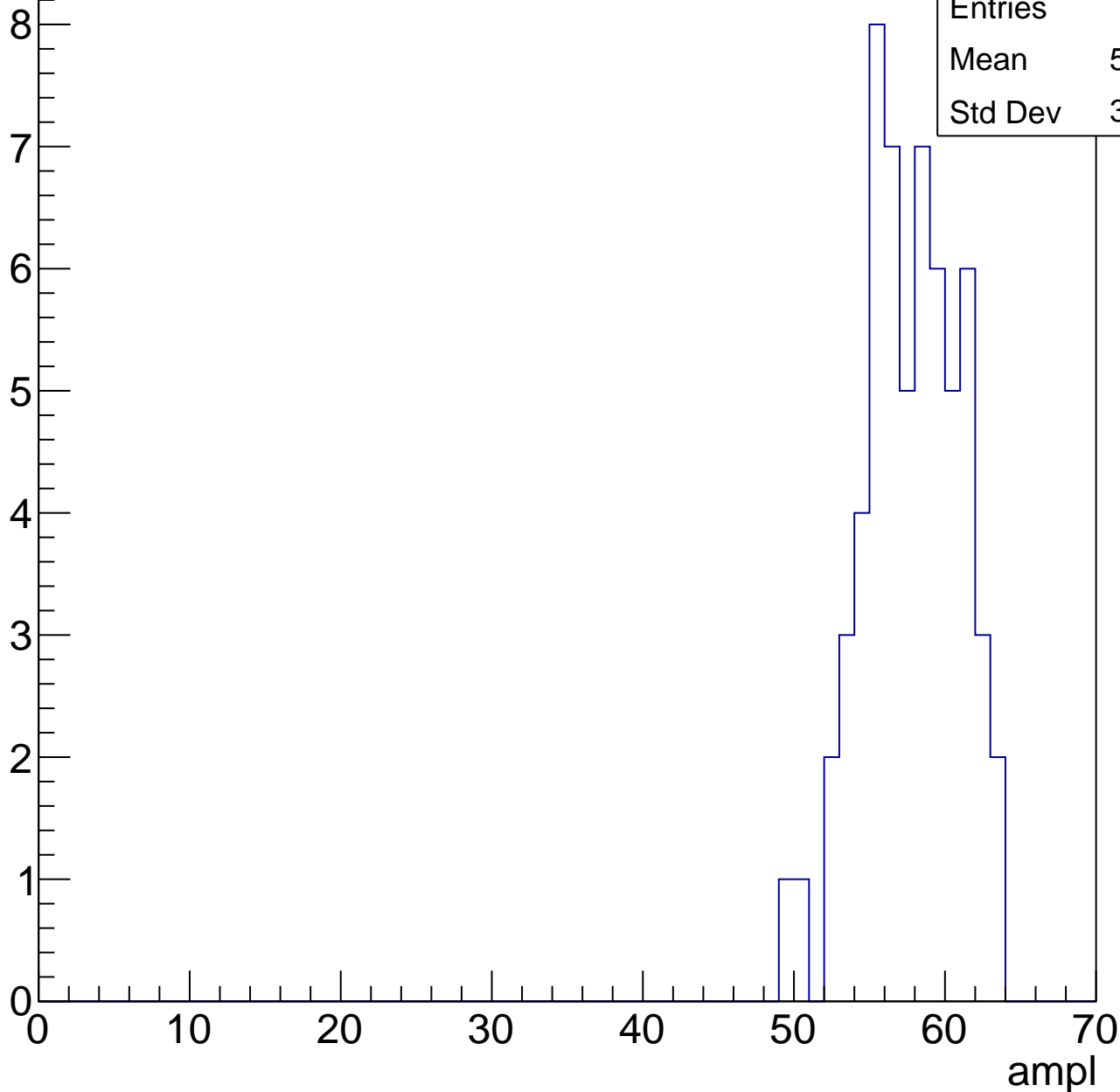


B0L002S, U26-ch23, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	57.22
Std Dev	3.168

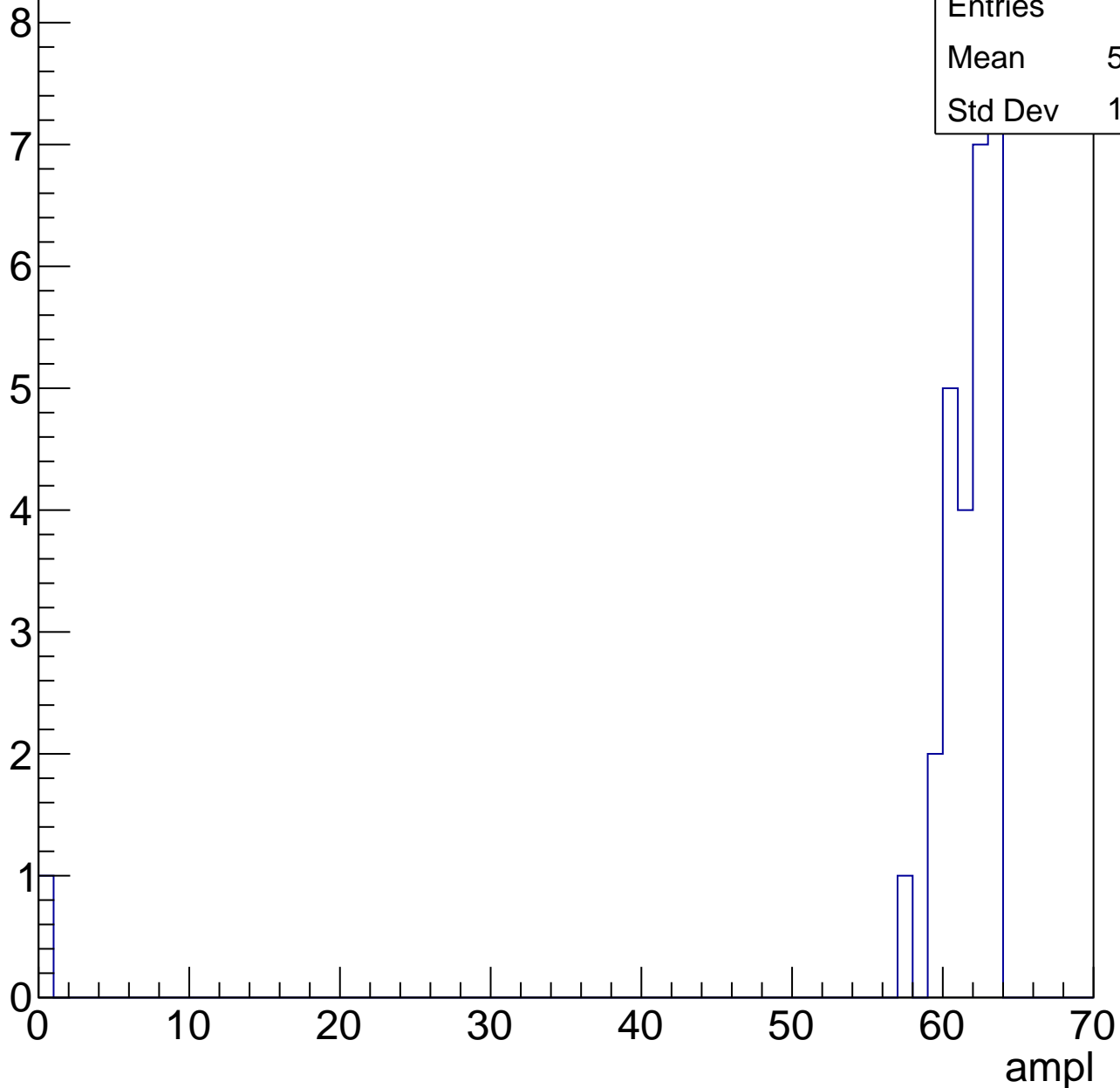


B0L002S, U26-ch23, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	59.18
Std Dev	11.49



B0L002S, U26-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U26-ch24, adc0

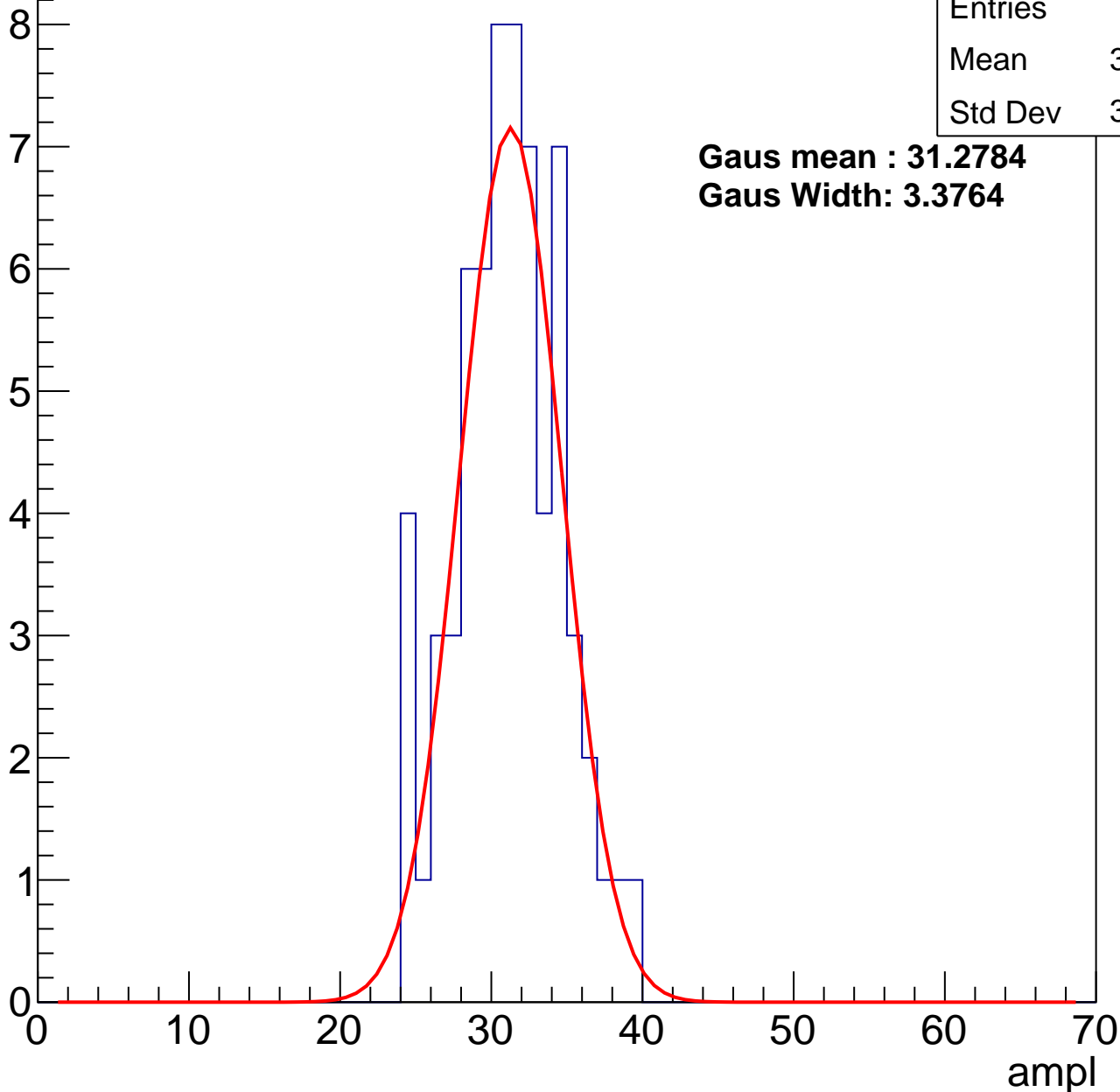
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.69
Std Dev	3.437

Gaus mean : 31.2784

Gaus Width: 3.3764



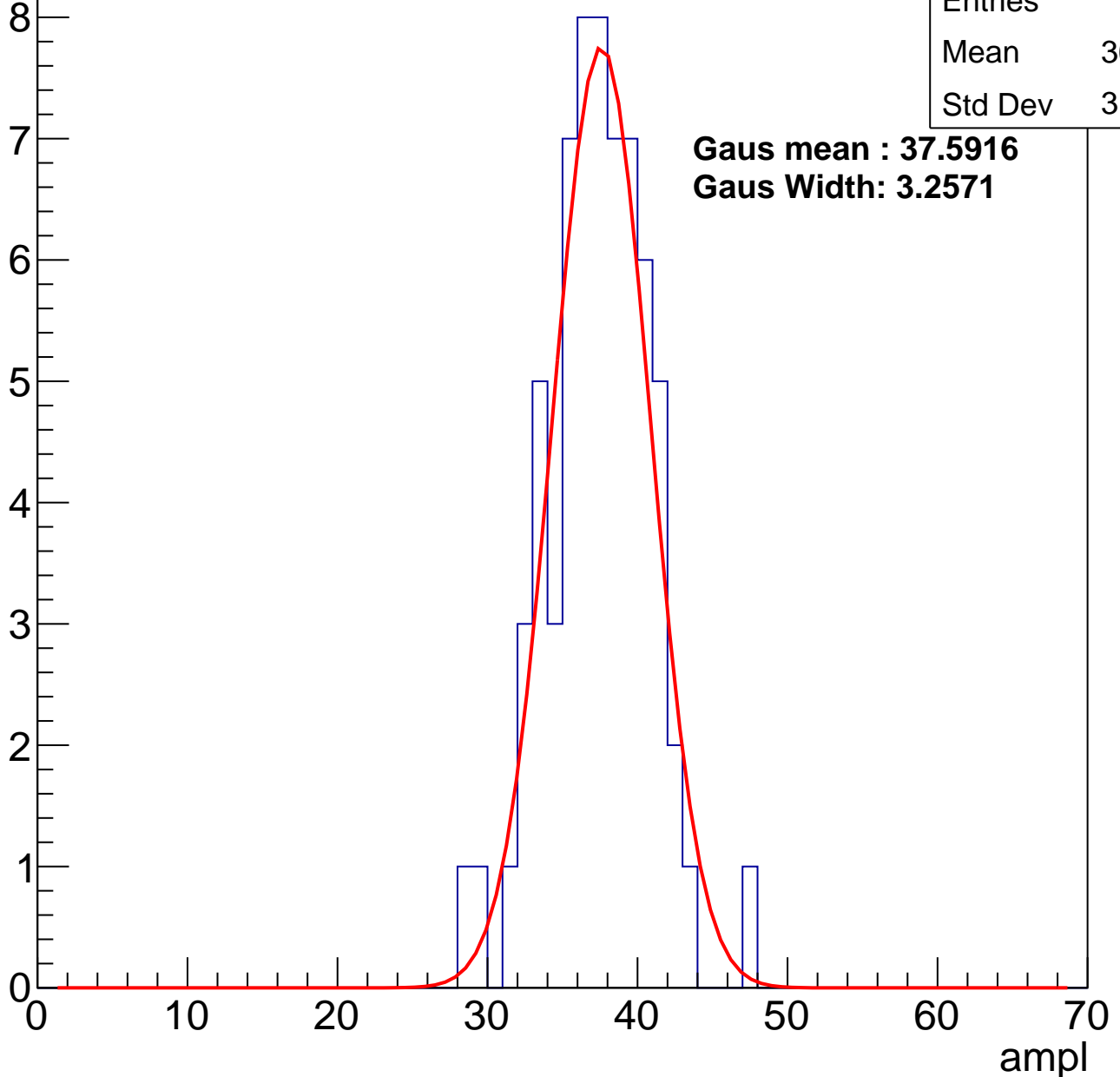
B0L002S, U26-ch24, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.94
Std Dev	3.379

Gaus mean : 37.5916
Gaus Width: 3.2571



B0L002S, U26-ch24, adc2

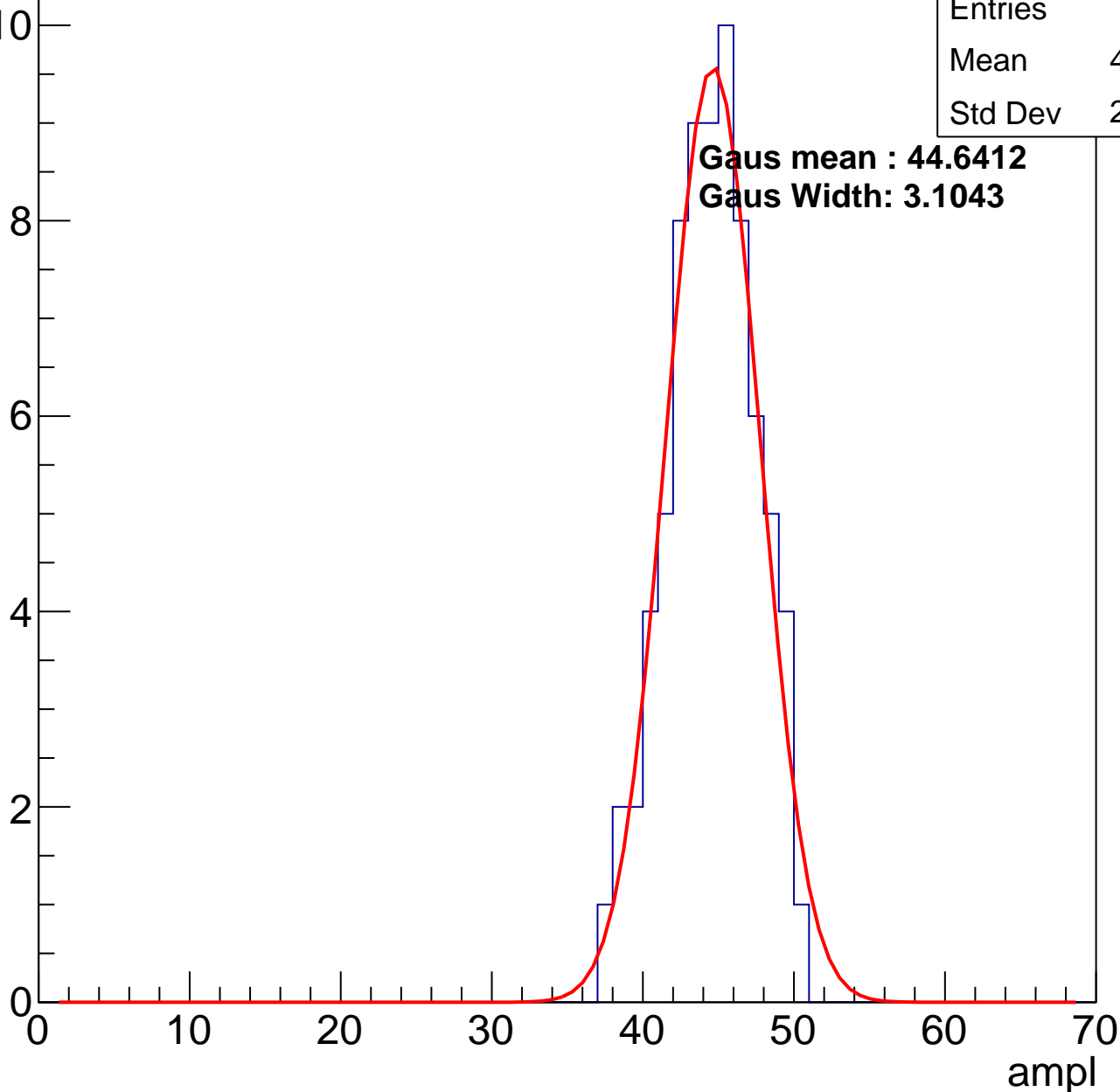
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	44.07
Std Dev	2.924

Gaus mean : 44.6412

Gaus Width: 3.1043

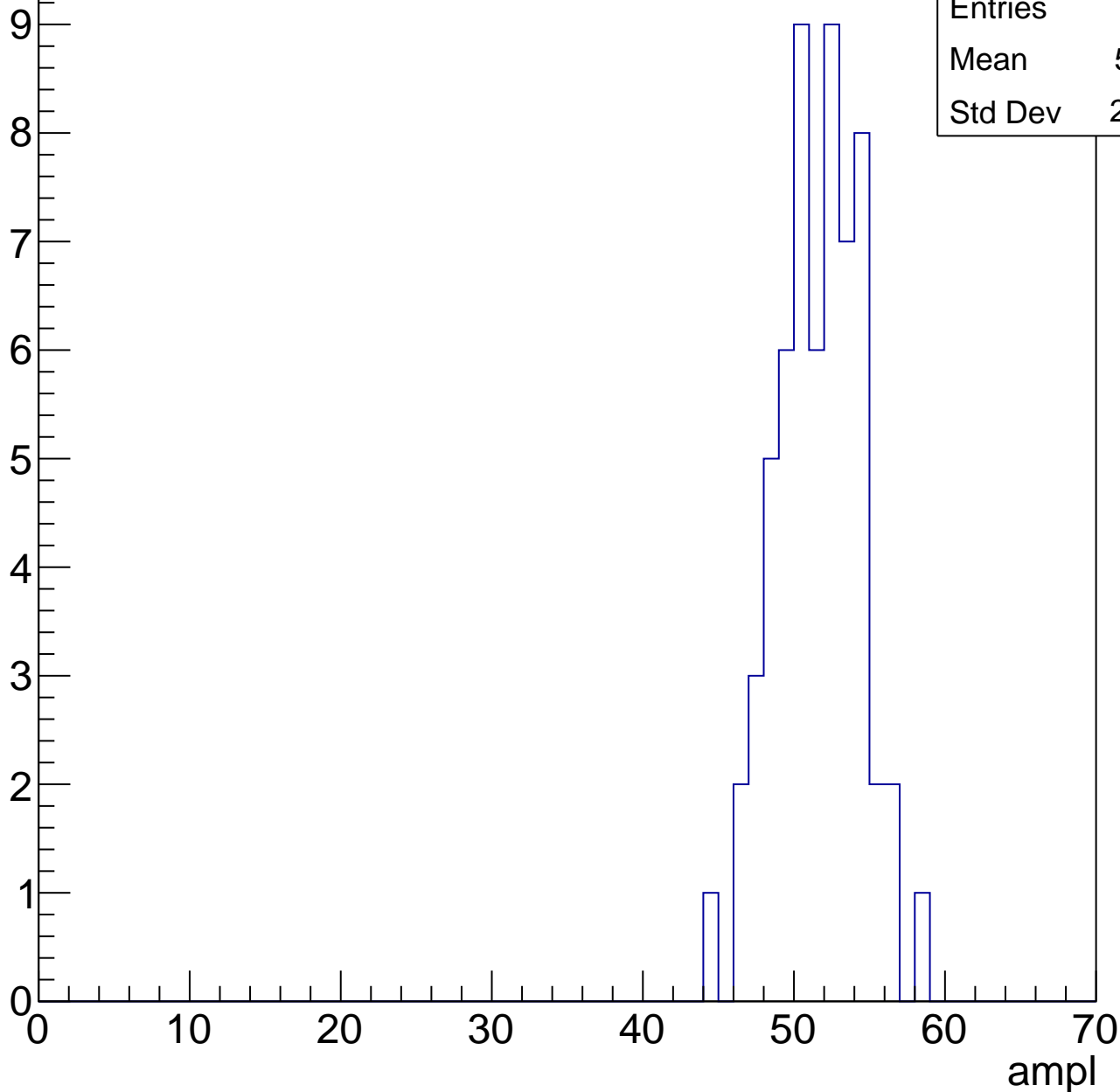


B0L002S, U26-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	51.11
Std Dev	2.759



B0L002S, U26-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	56.43
Std Dev	7.476

Entry

10

8

6

4

2

0

0

10

20

30

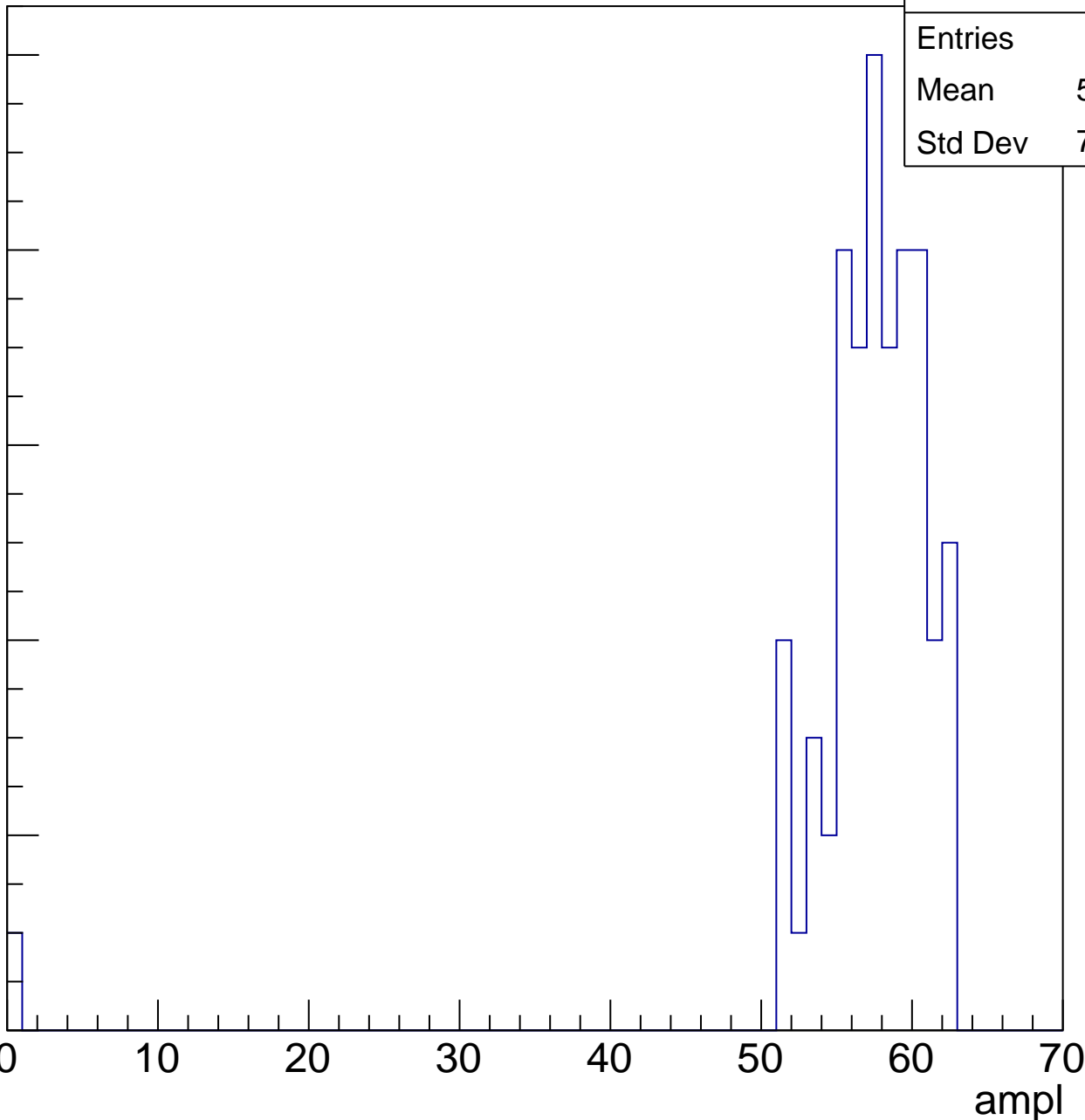
40

50

60

70

ampl

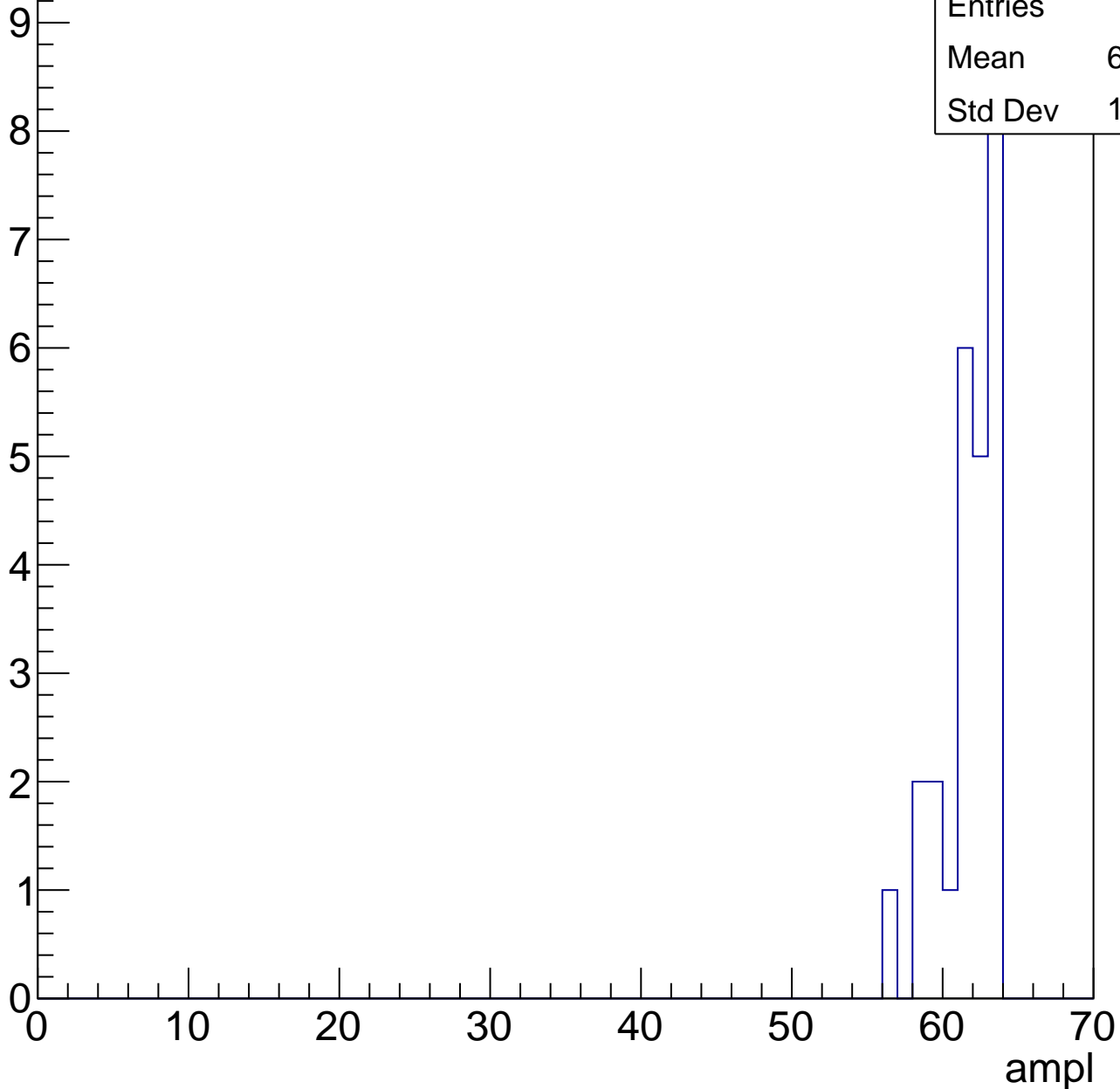


B0L002S, U26-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	61.27
Std Dev	1.872



B0L002S, U26-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch25, adc0

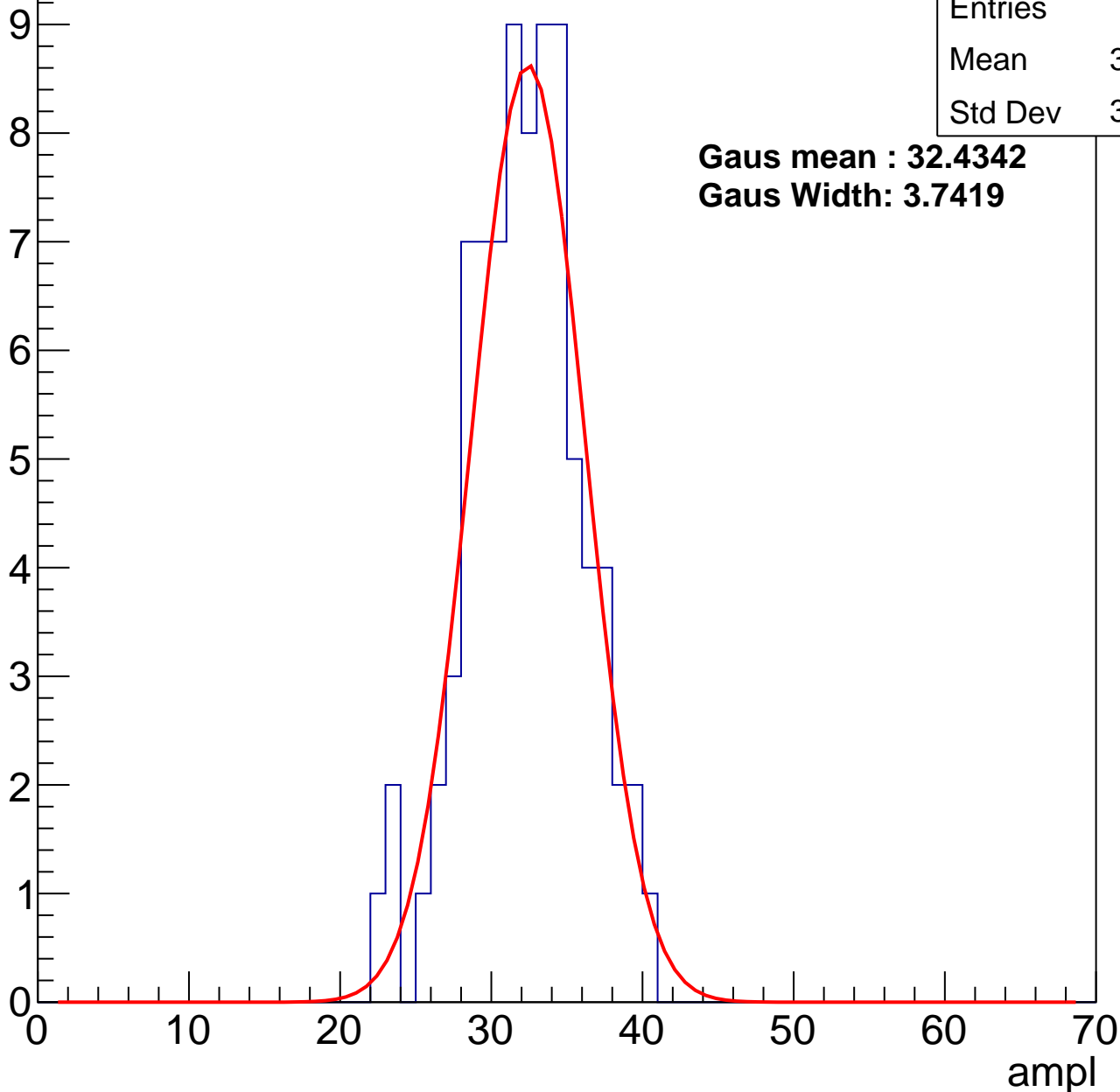
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	31.73
Std Dev	3.726

Gaus mean : 32.4342

Gaus Width: 3.7419



B0L002S, U26-ch25, adc1

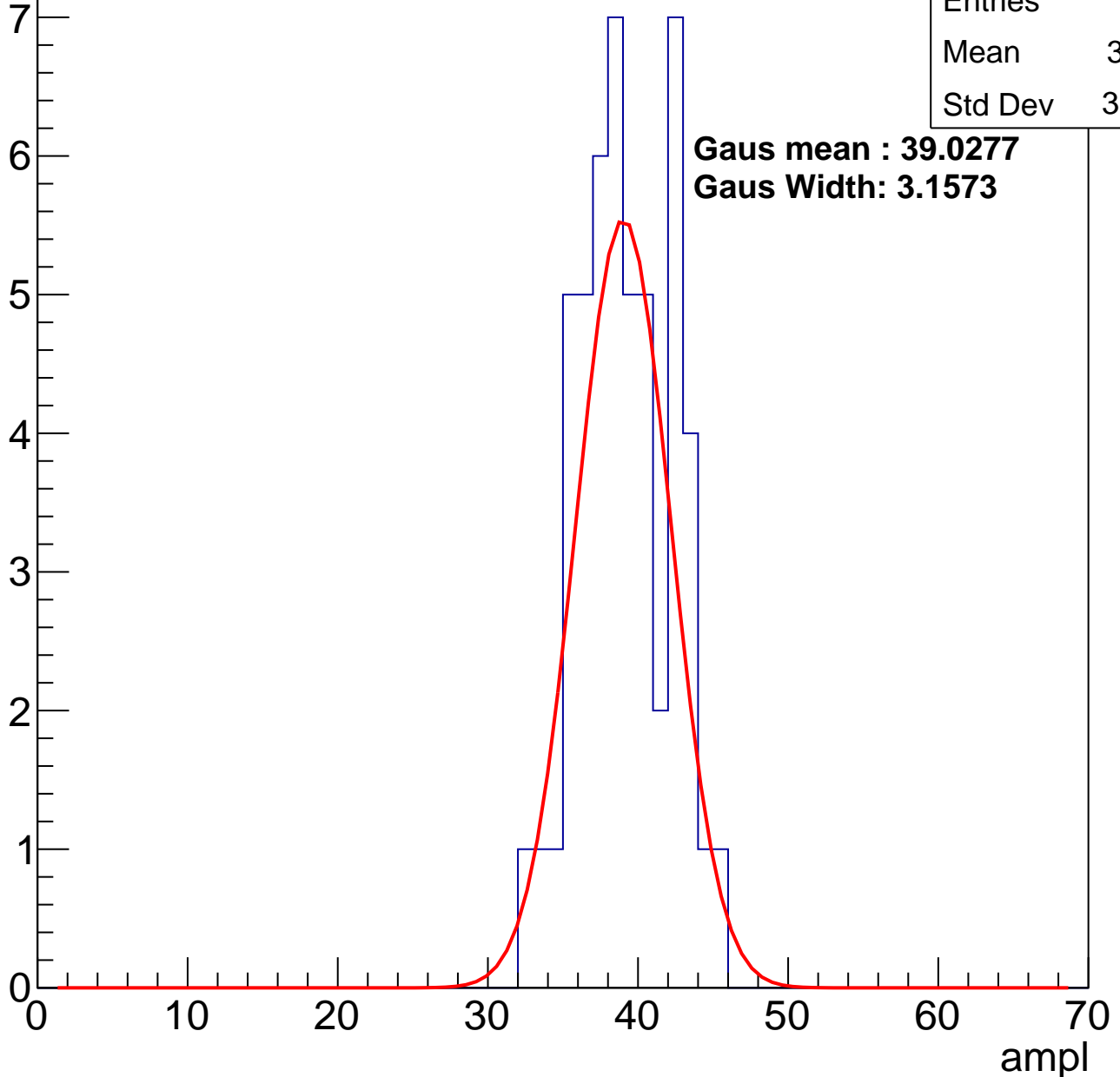
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	38.71
Std Dev	3.012

Gaus mean : 39.0277

Gaus Width: 3.1573



B0L002S, U26-ch25, adc2

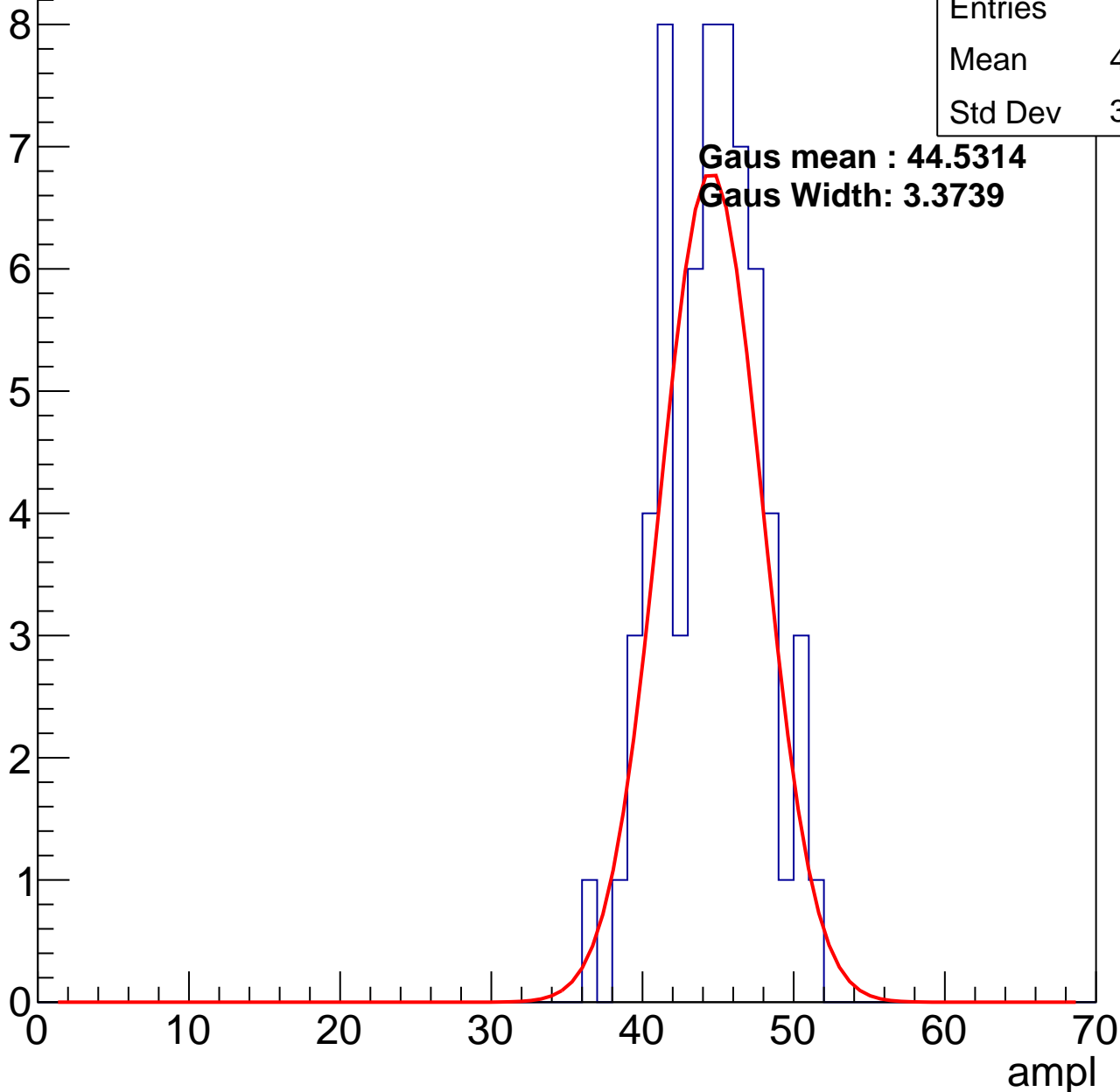
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.08
Std Dev	3.223

Gaus mean : 44.5314

Gaus Width: 3.3739

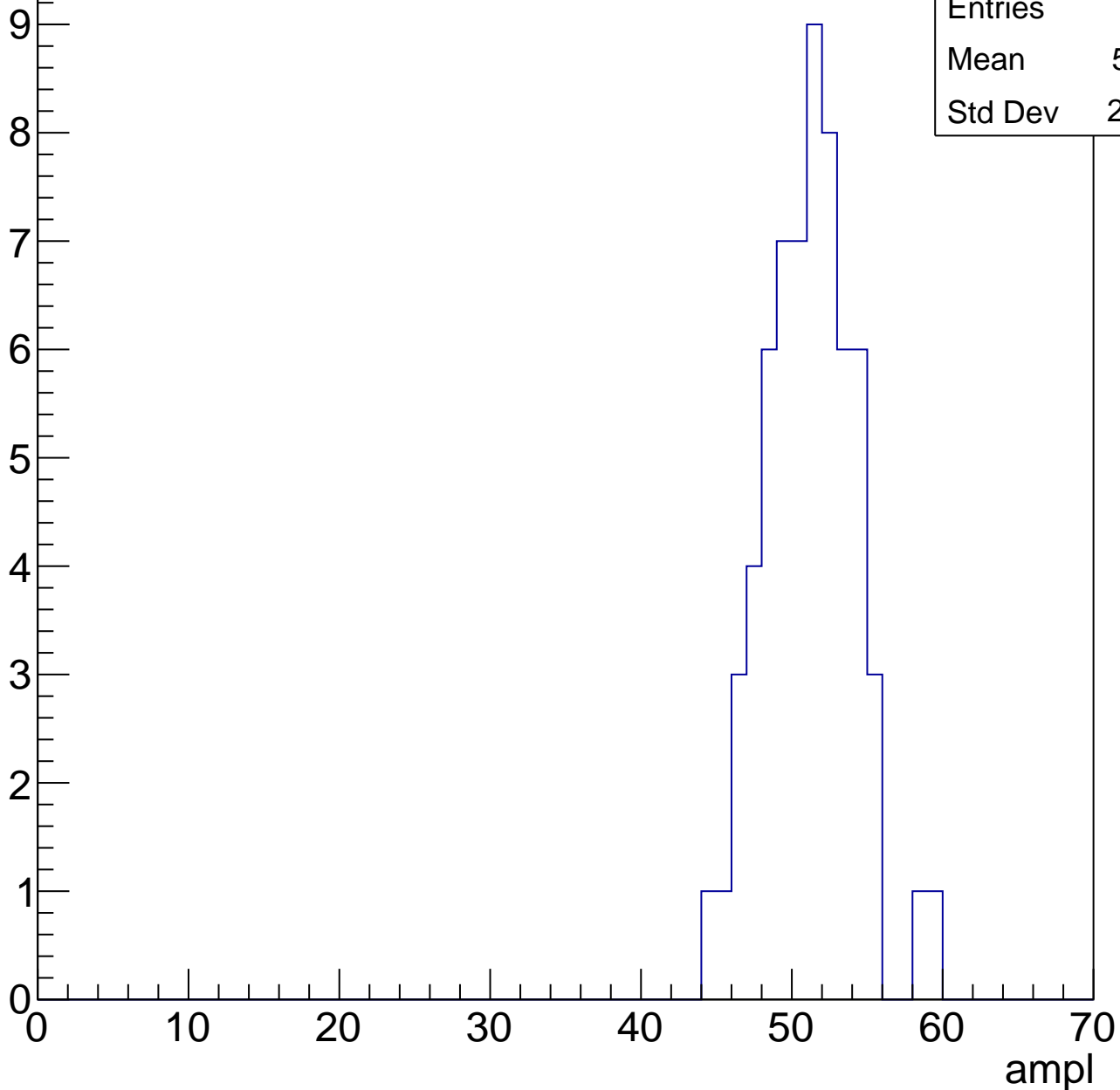


B0L002S, U26-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

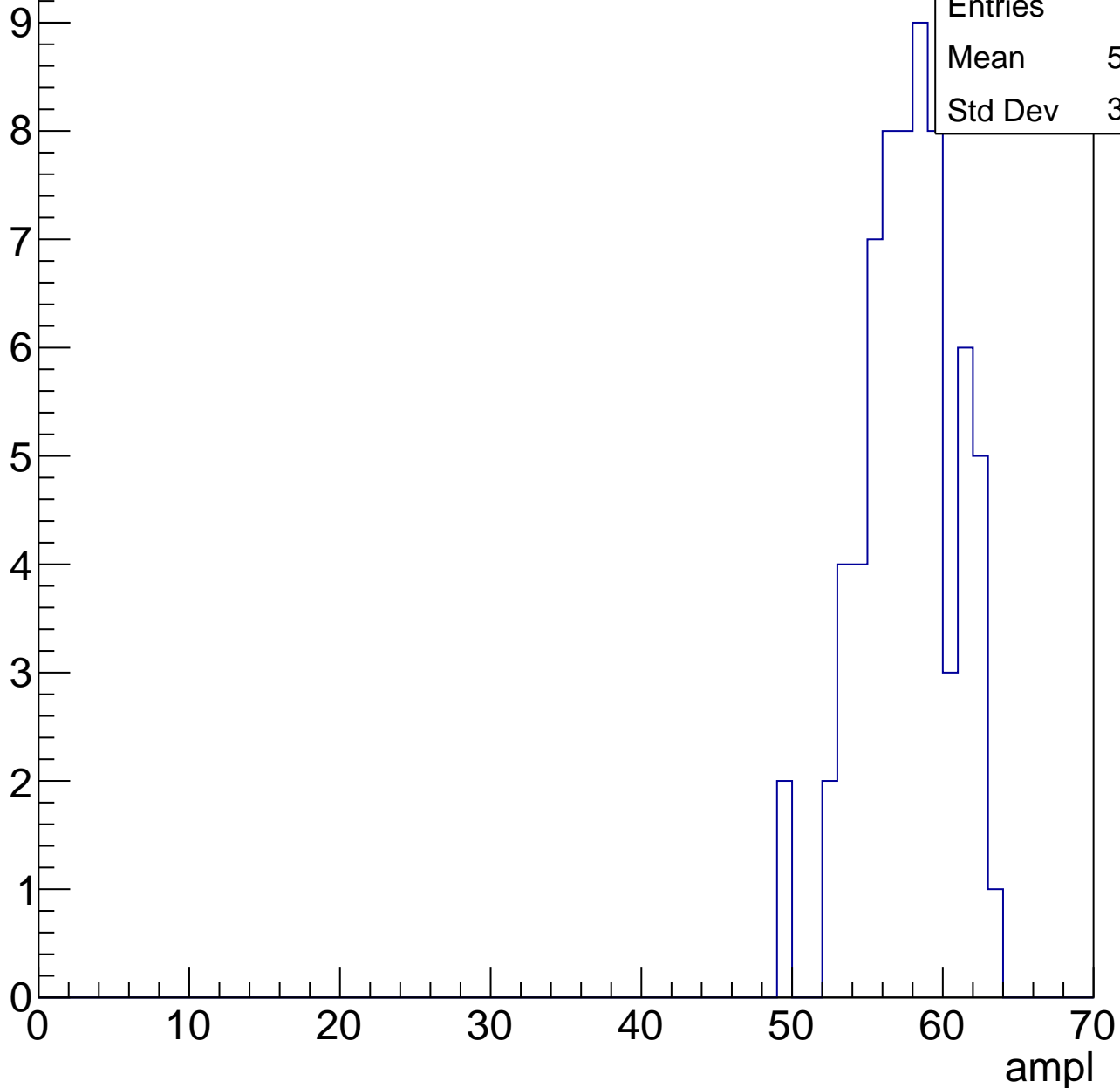
Entries	63
Mean	50.71
Std Dev	2.968



B0L002S, U26-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

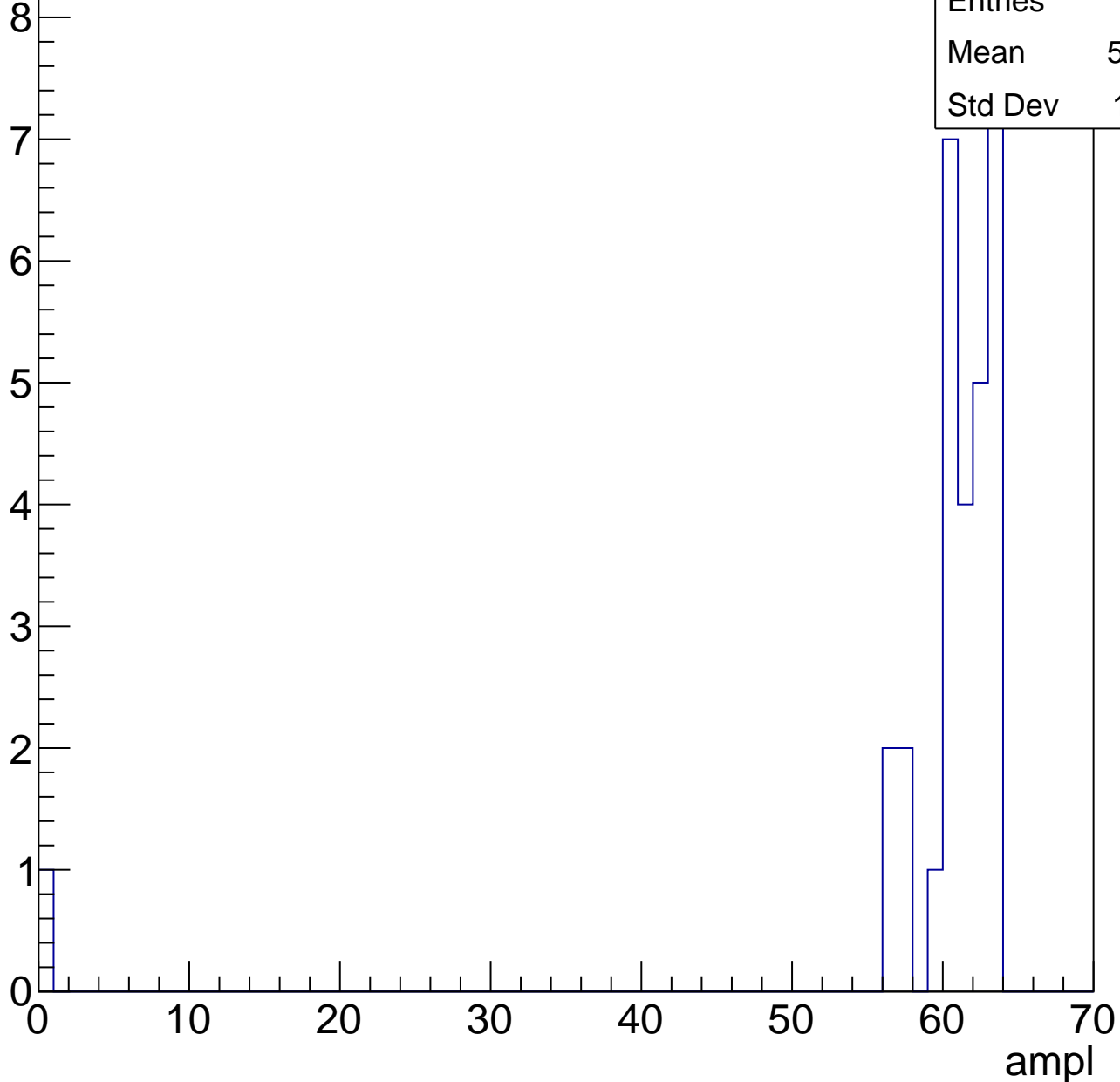
Entry



B0L002S, U26-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

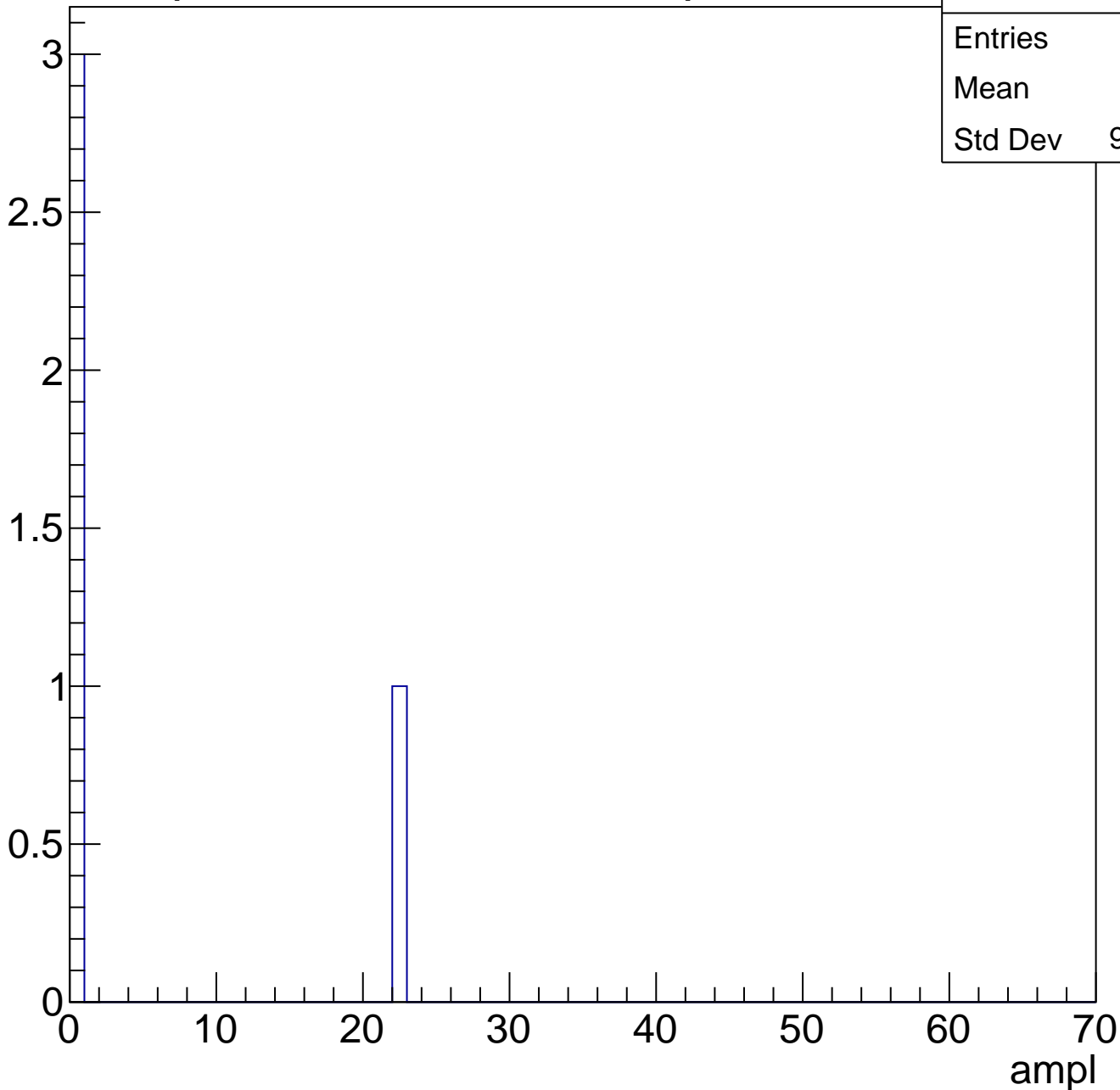


Entries	1
Mean	63
Std Dev	0

B0L002S, U26-ch25, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch26, adc0

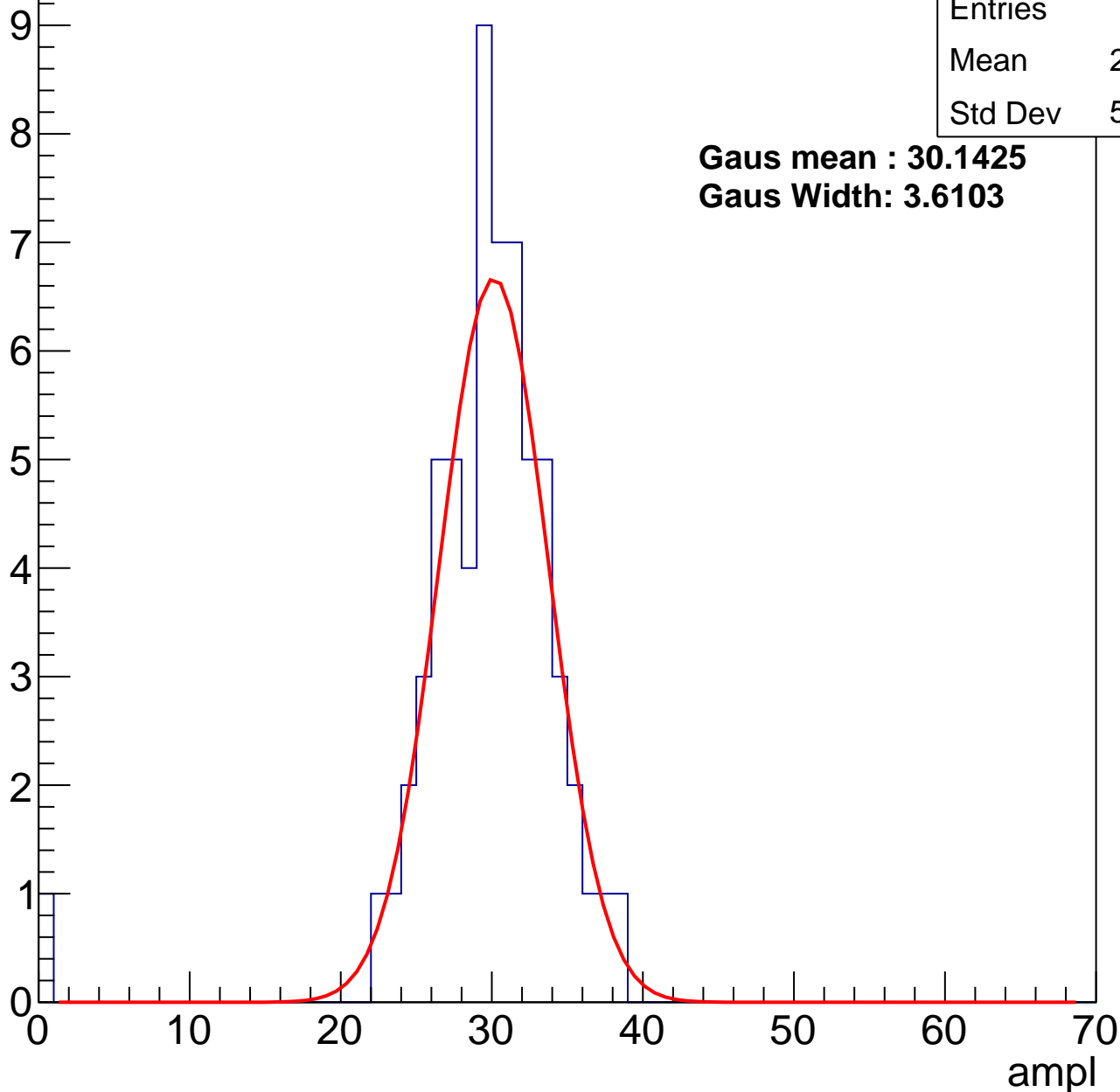
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.22
Std Dev	5.025

Gaus mean : 30.1425

Gaus Width: 3.6103



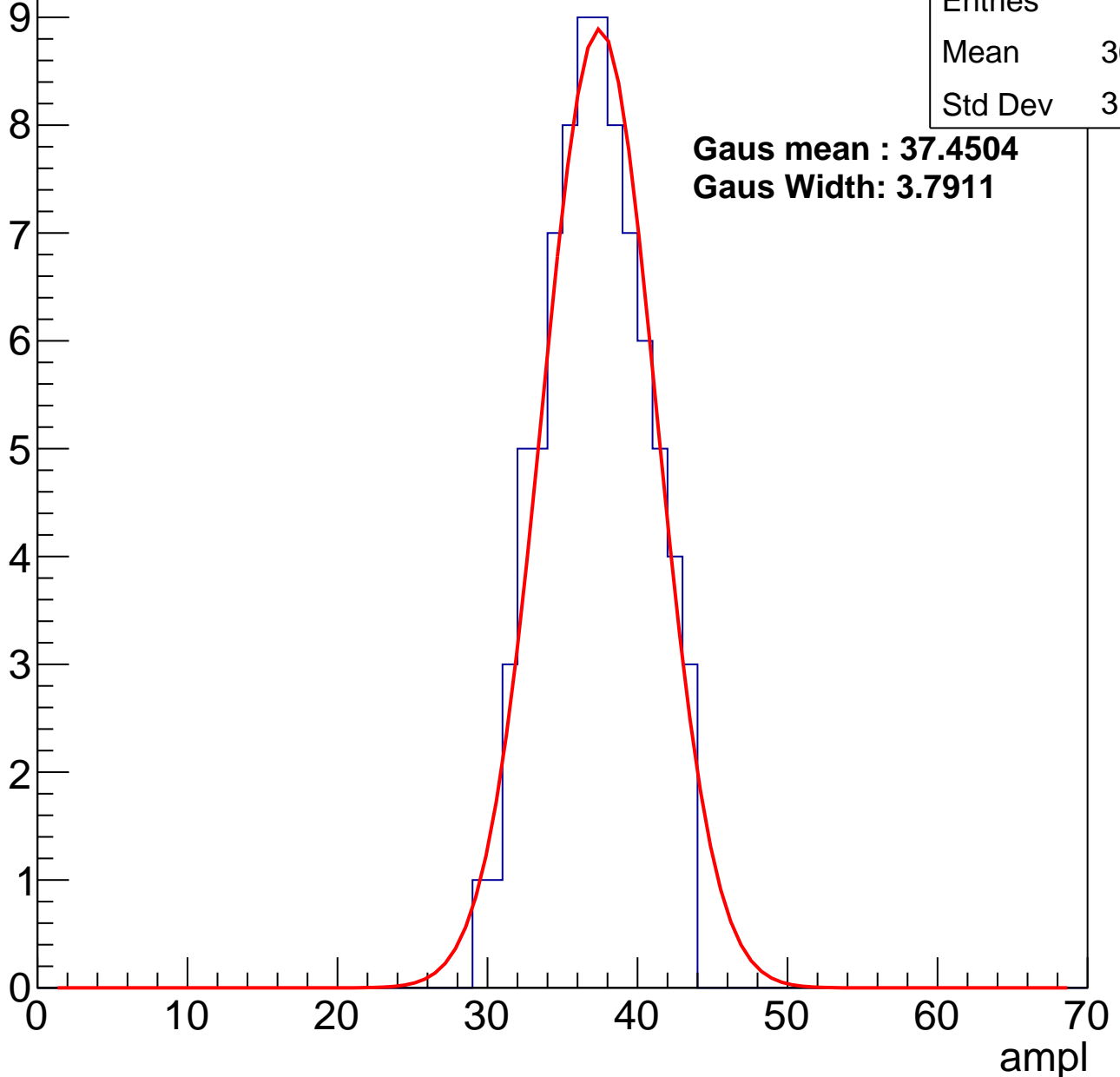
B0L002S, U26-ch26, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	36.68
Std Dev	3.333

Gaus mean : 37.4504
Gaus Width: 3.7911



B0L002S, U26-ch26, adc2

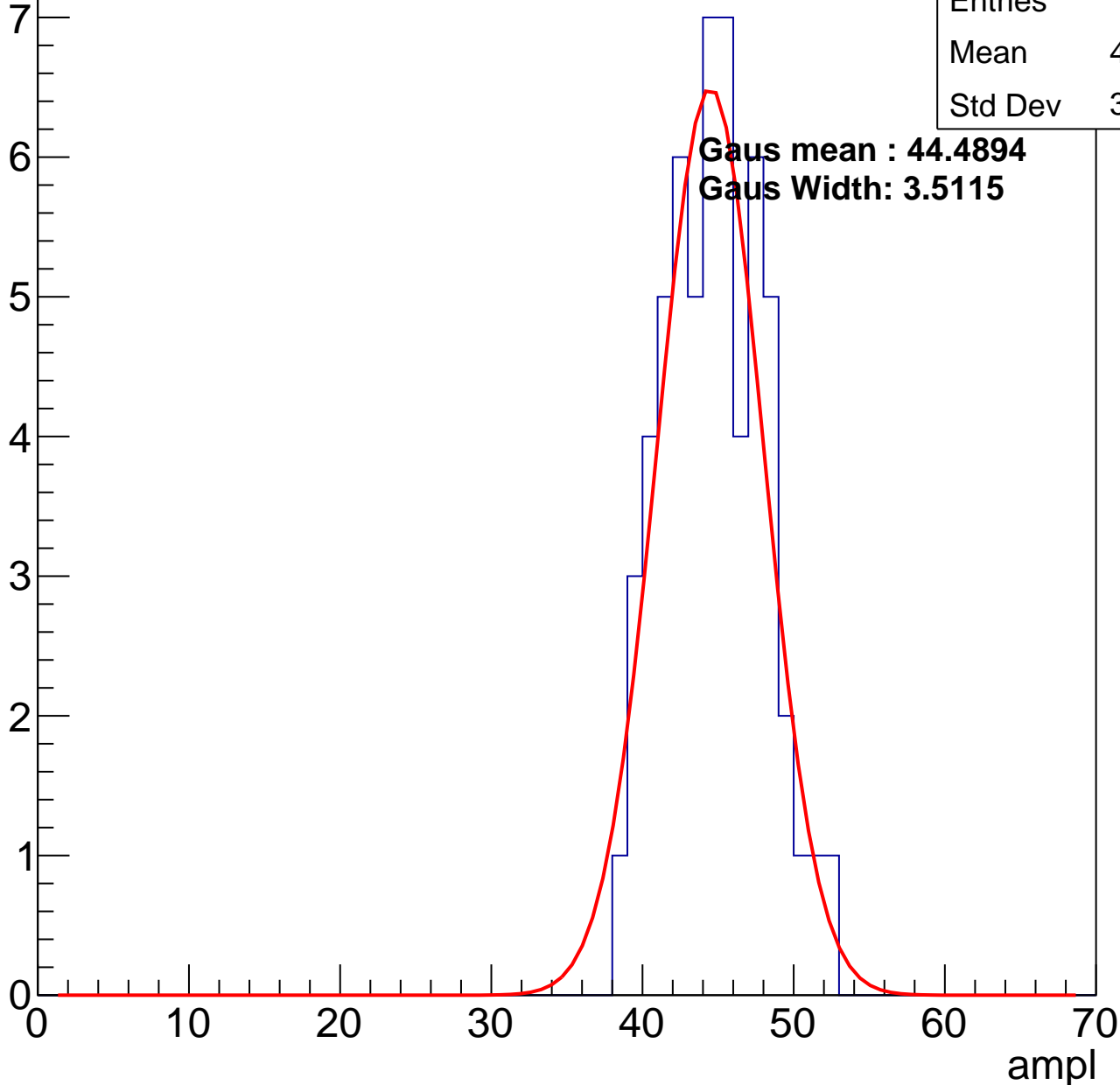
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.26
Std Dev	3.219

Gaus mean : 44.4894

Gaus Width: 3.5115

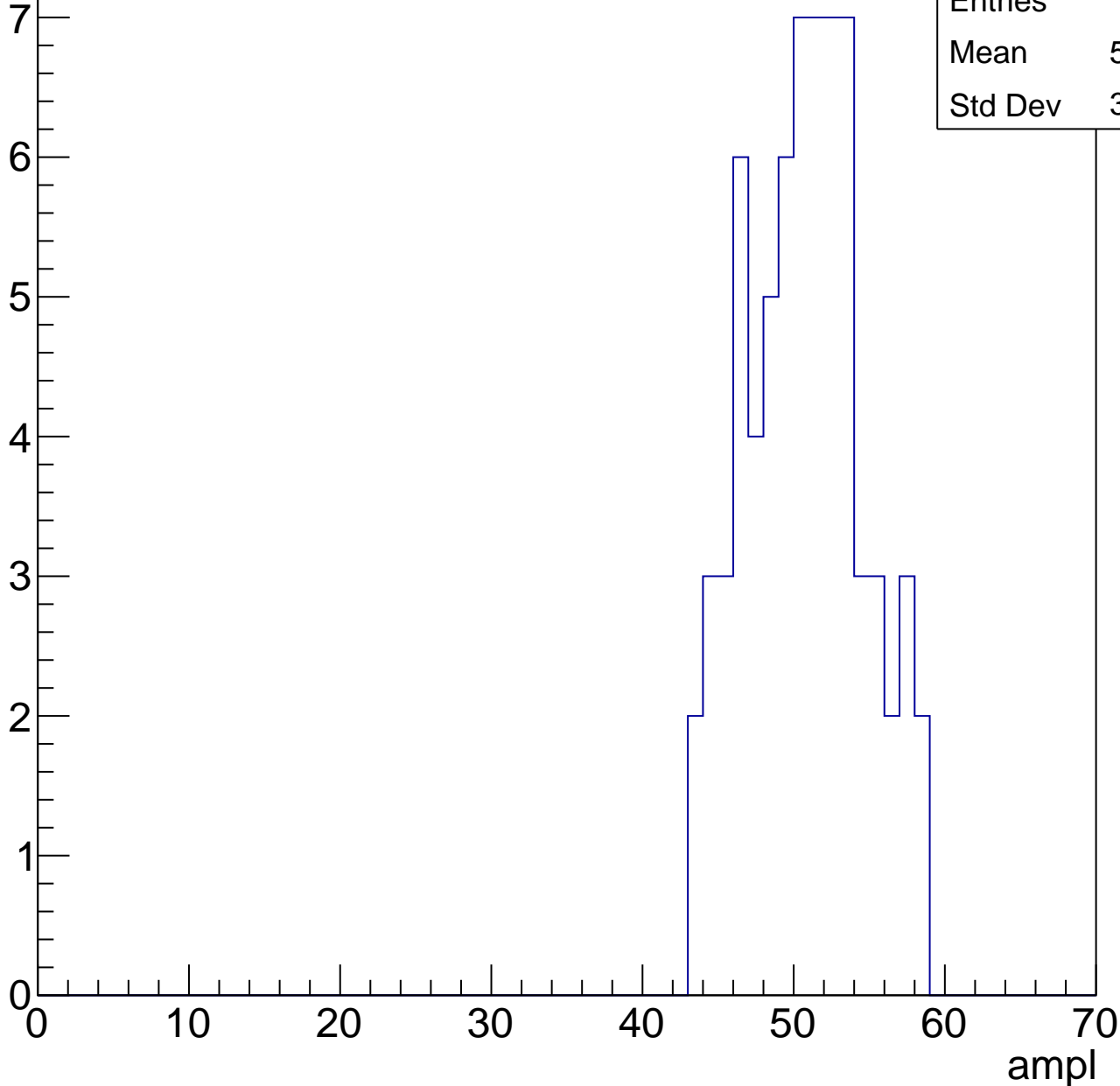


B0L002S, U26-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

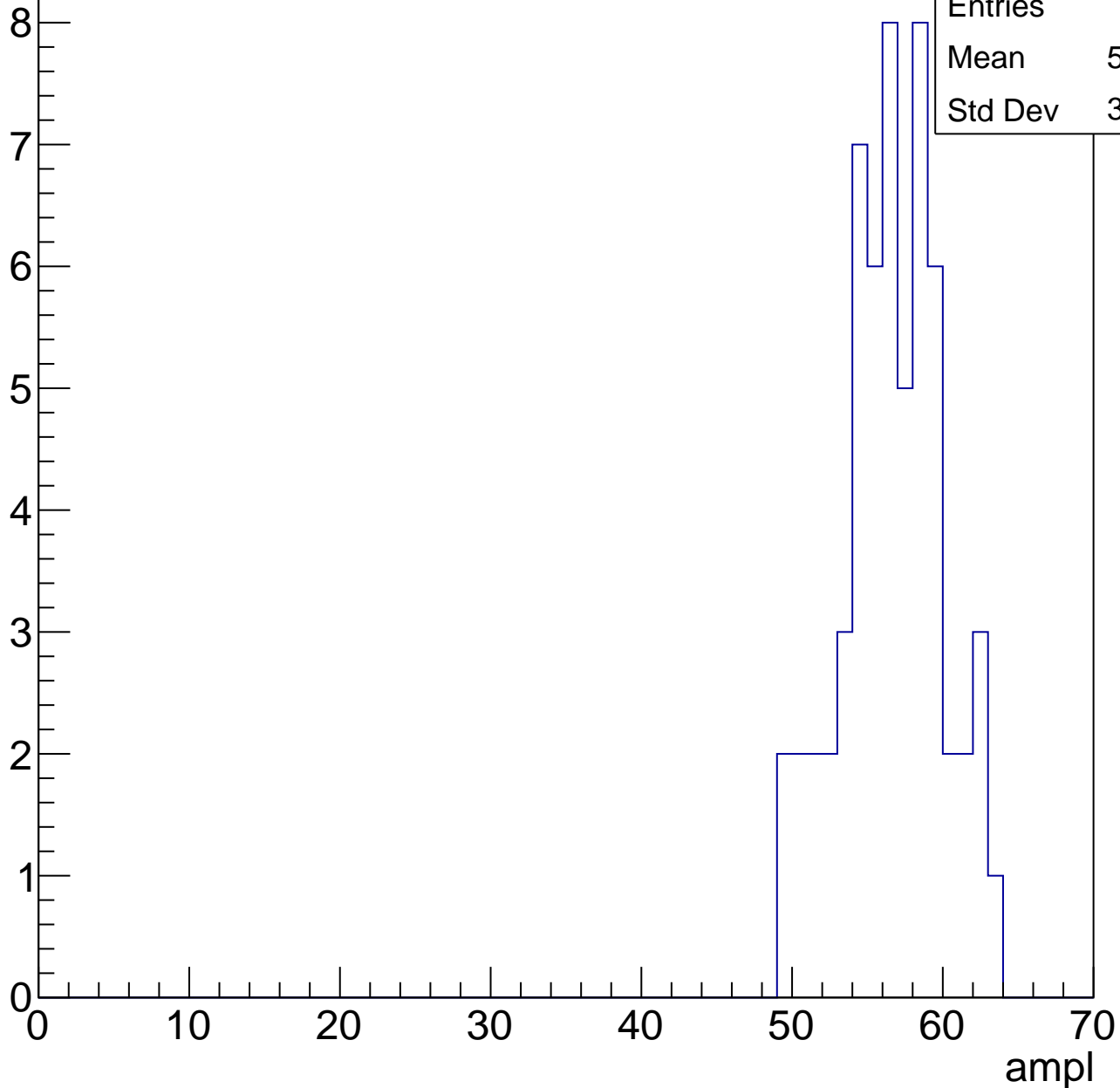
Entries	70
Mean	50.27
Std Dev	3.783



B0L002S, U26-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

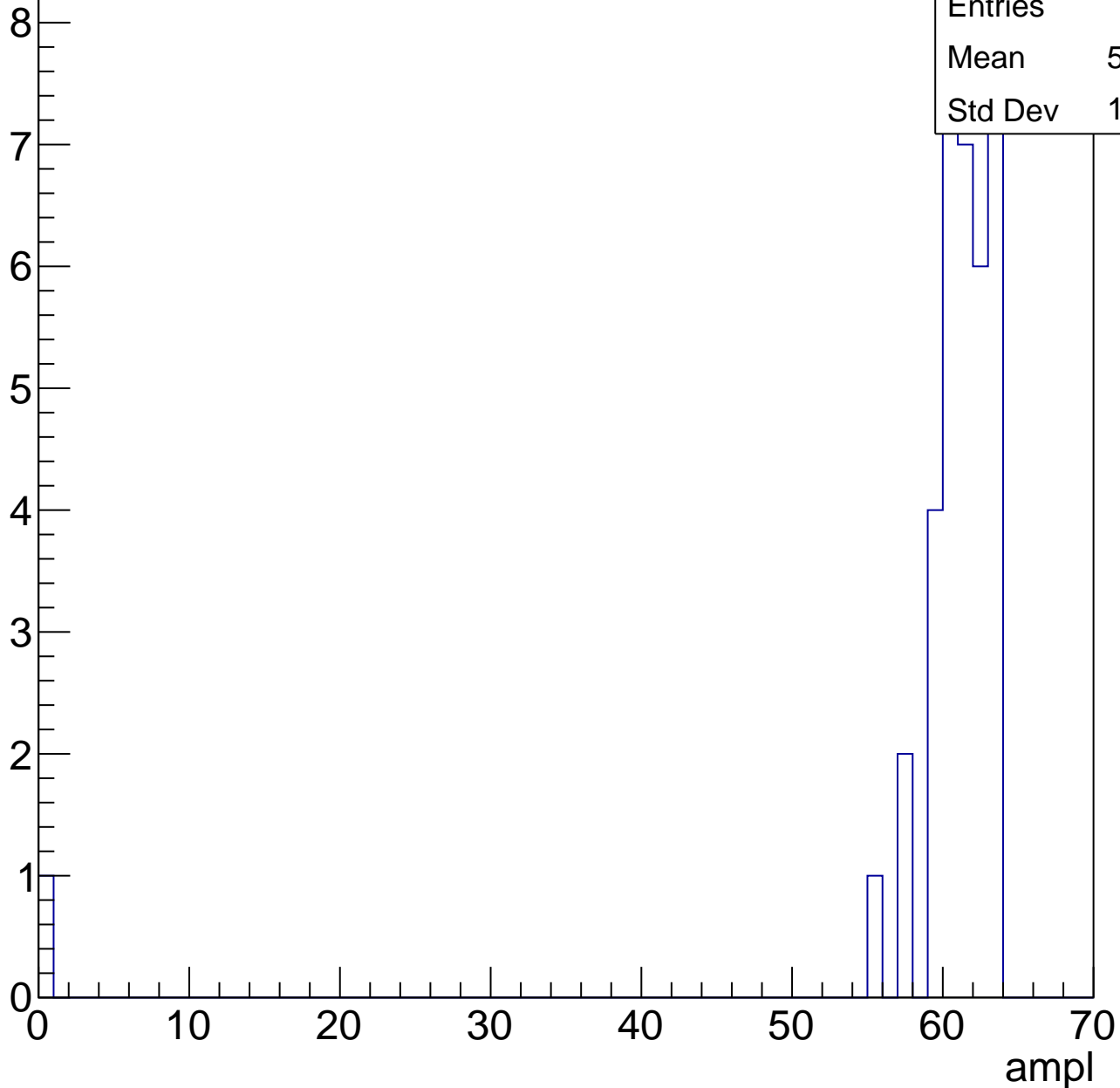


B0L002S, U26-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.14
Std Dev	10.03



B0L002S, U26-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L002S, U26-ch27, adc0

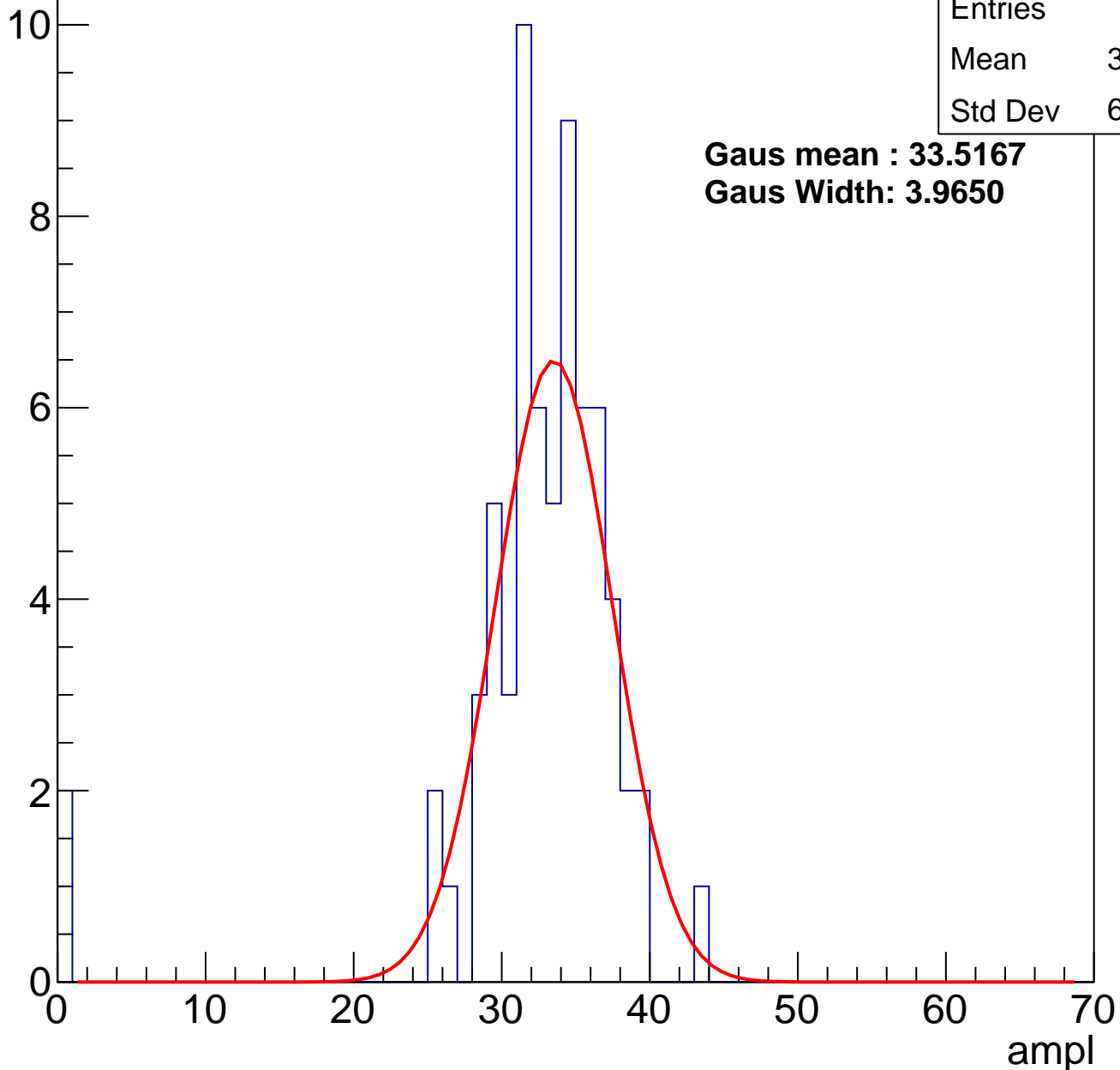
calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	31.93
Std Dev	6.555

Gaus mean : 33.5167

Gaus Width: 3.9650

Entry



B0L002S, U26-ch27, adc1

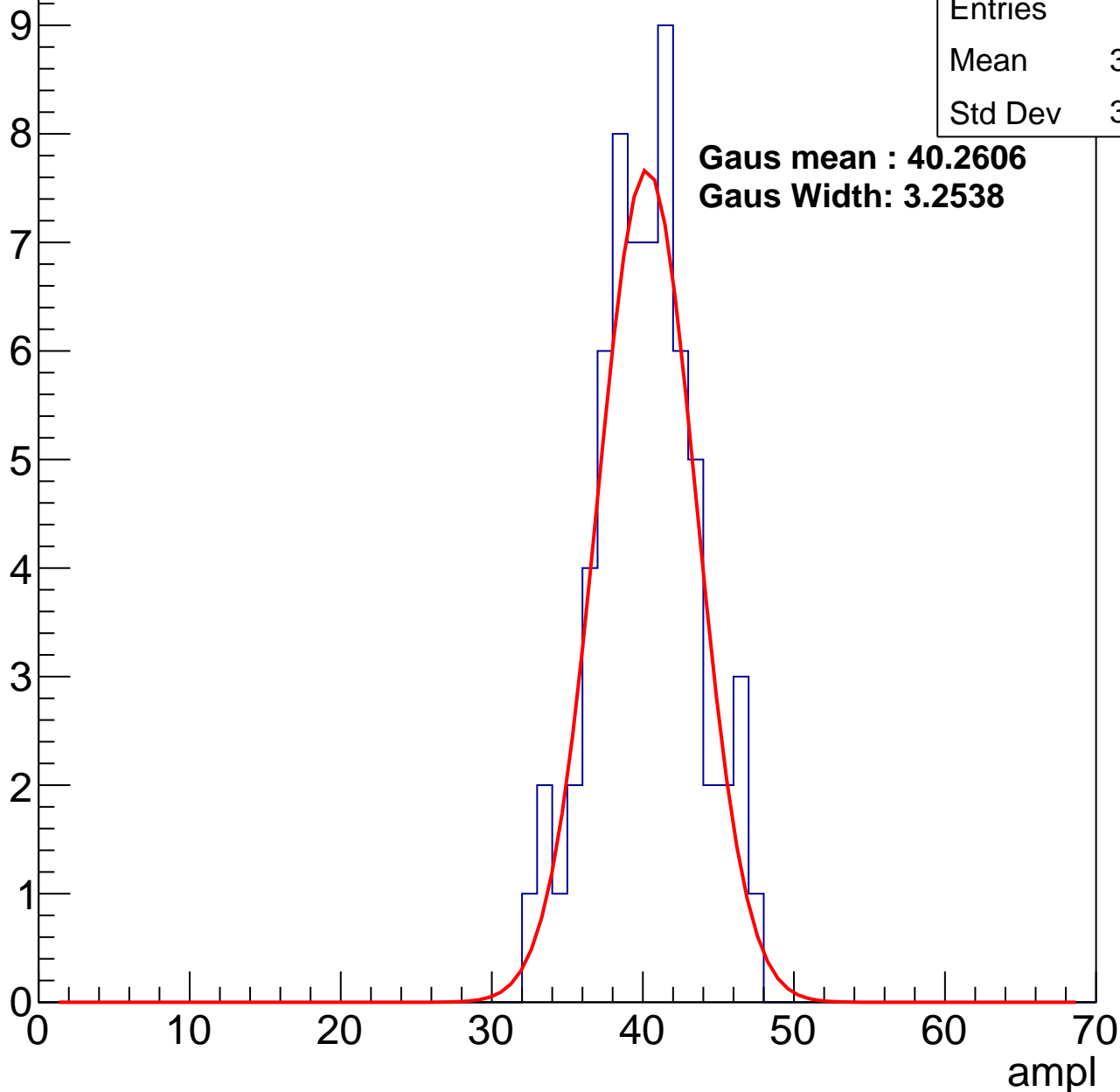
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	39.76
Std Dev	3.299

Gaus mean : 40.2606

Gaus Width: 3.2538



B0L002S, U26-ch27, adc2

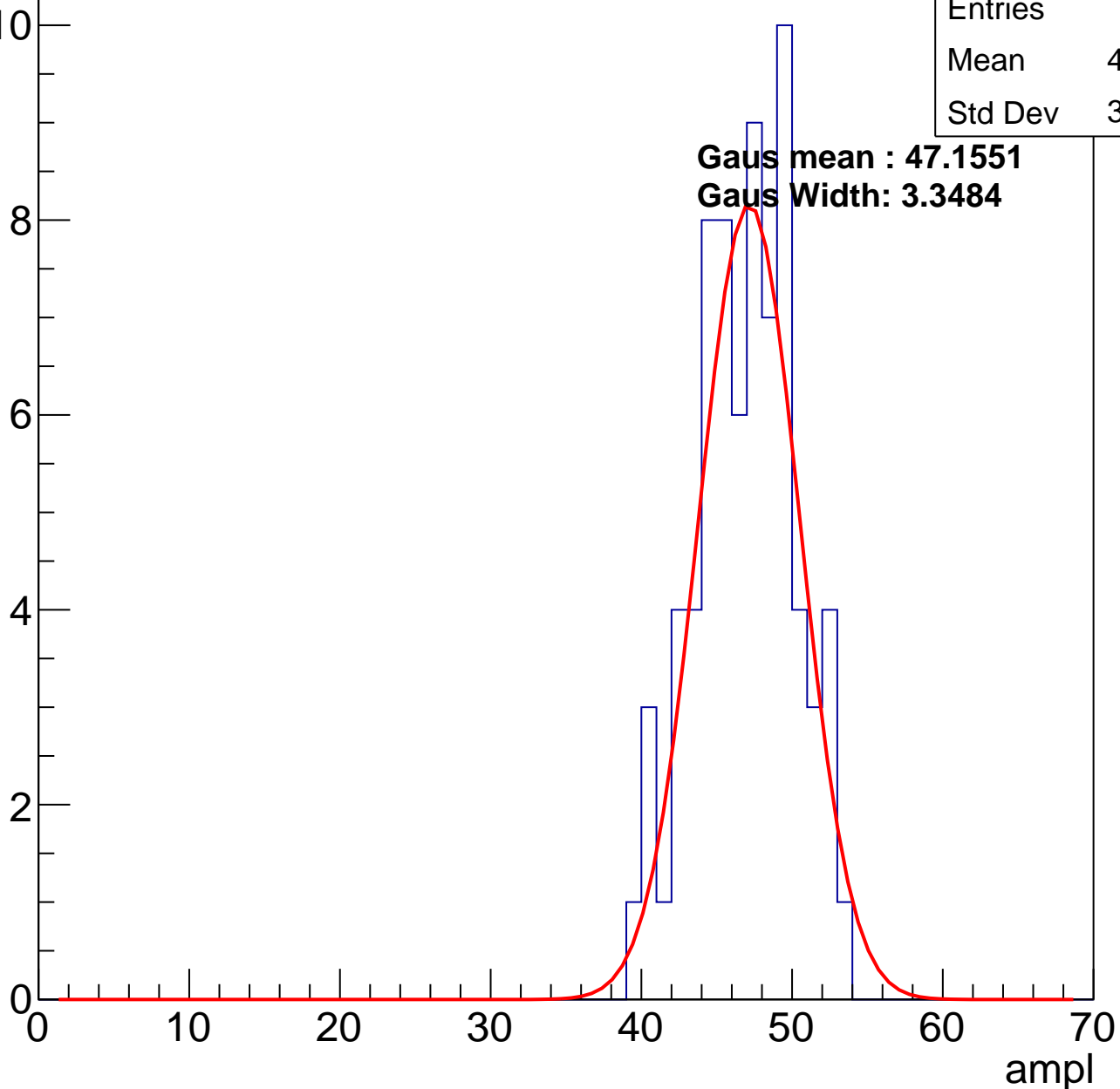
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	46.45
Std Dev	3.239

Gaus mean : 47.1551

Gaus Width: 3.3484

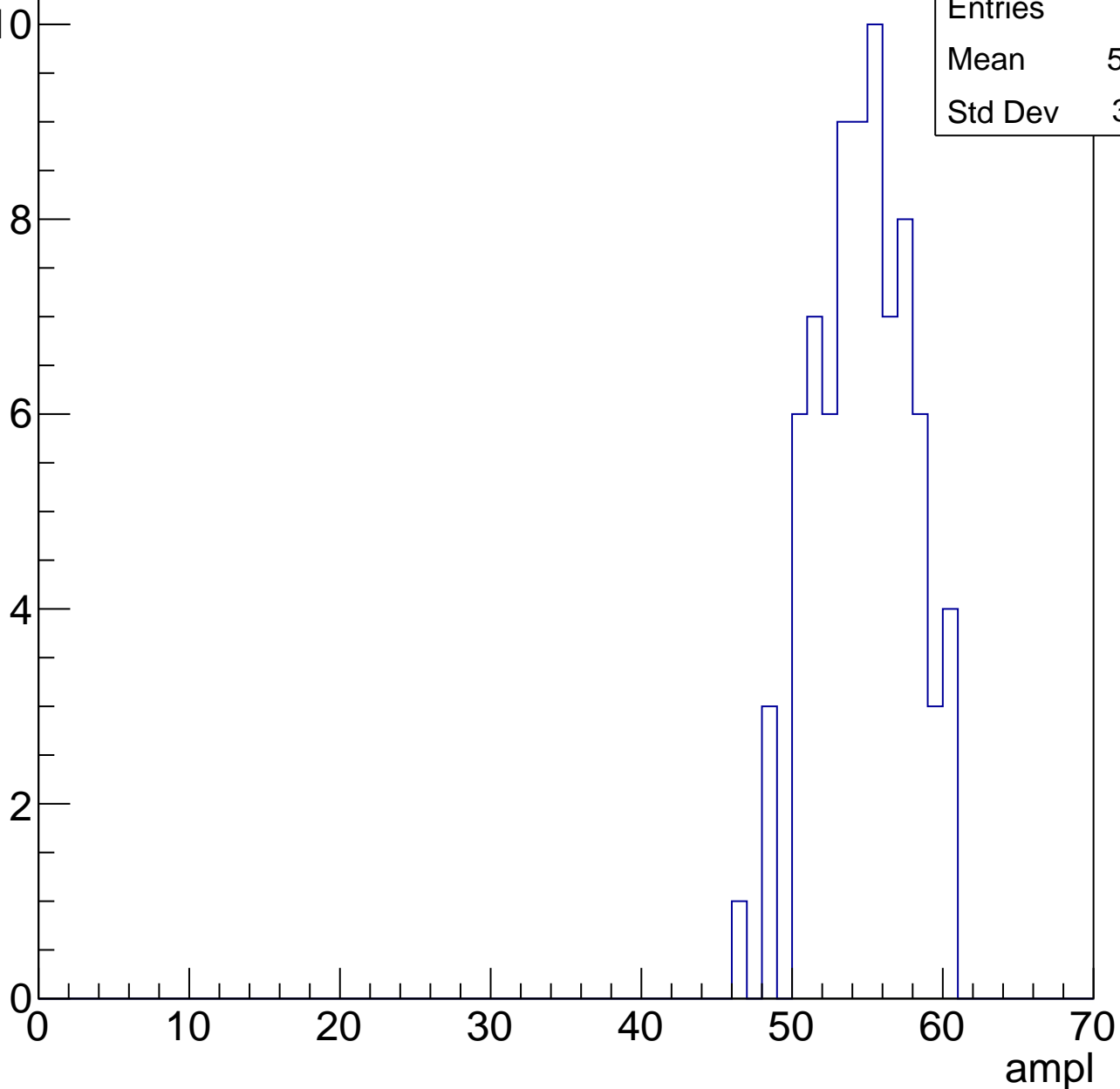


B0L002S, U26-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	54.24
Std Dev	3.151

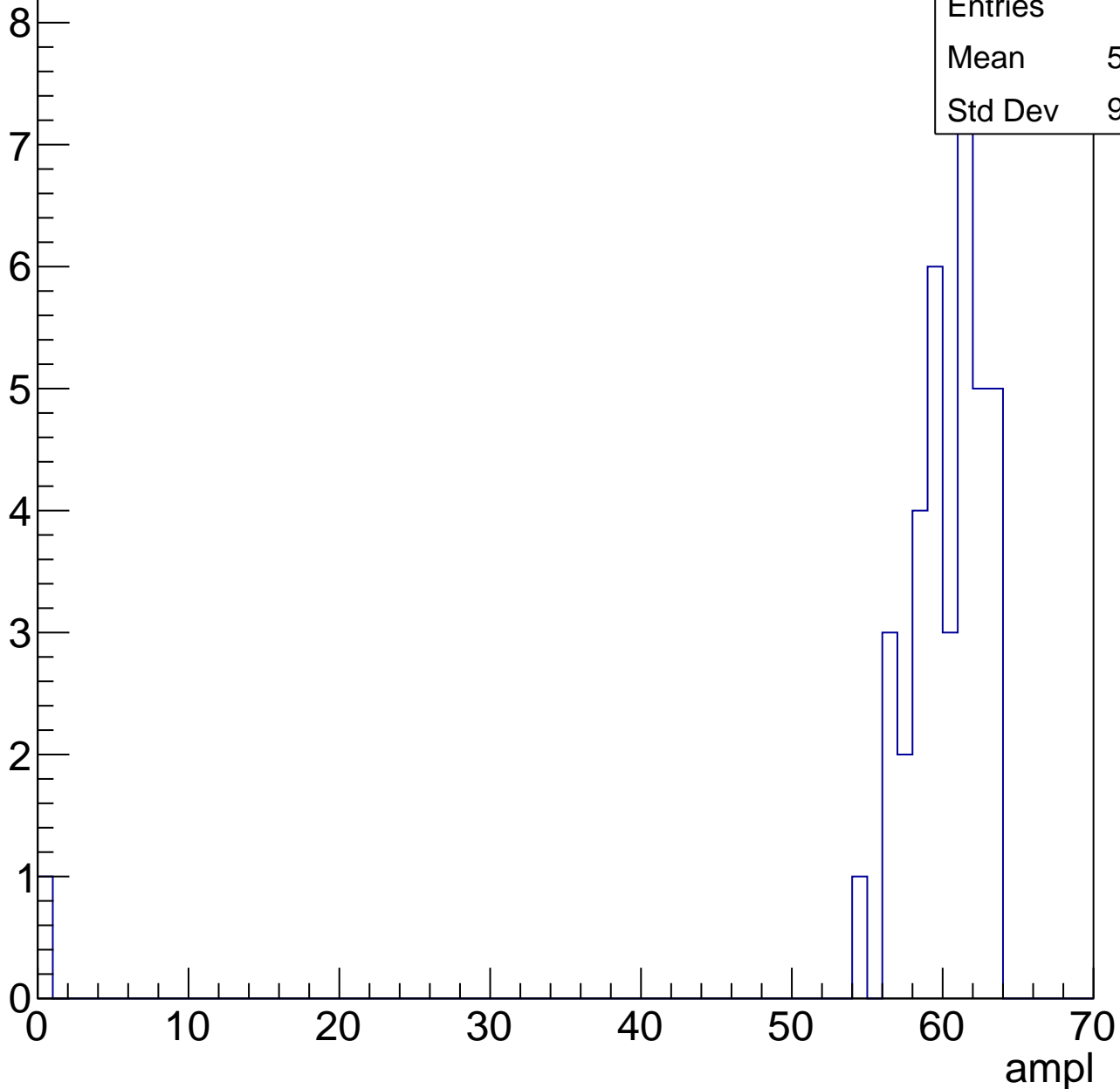


B0L002S, U26-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.29
Std Dev	9.849

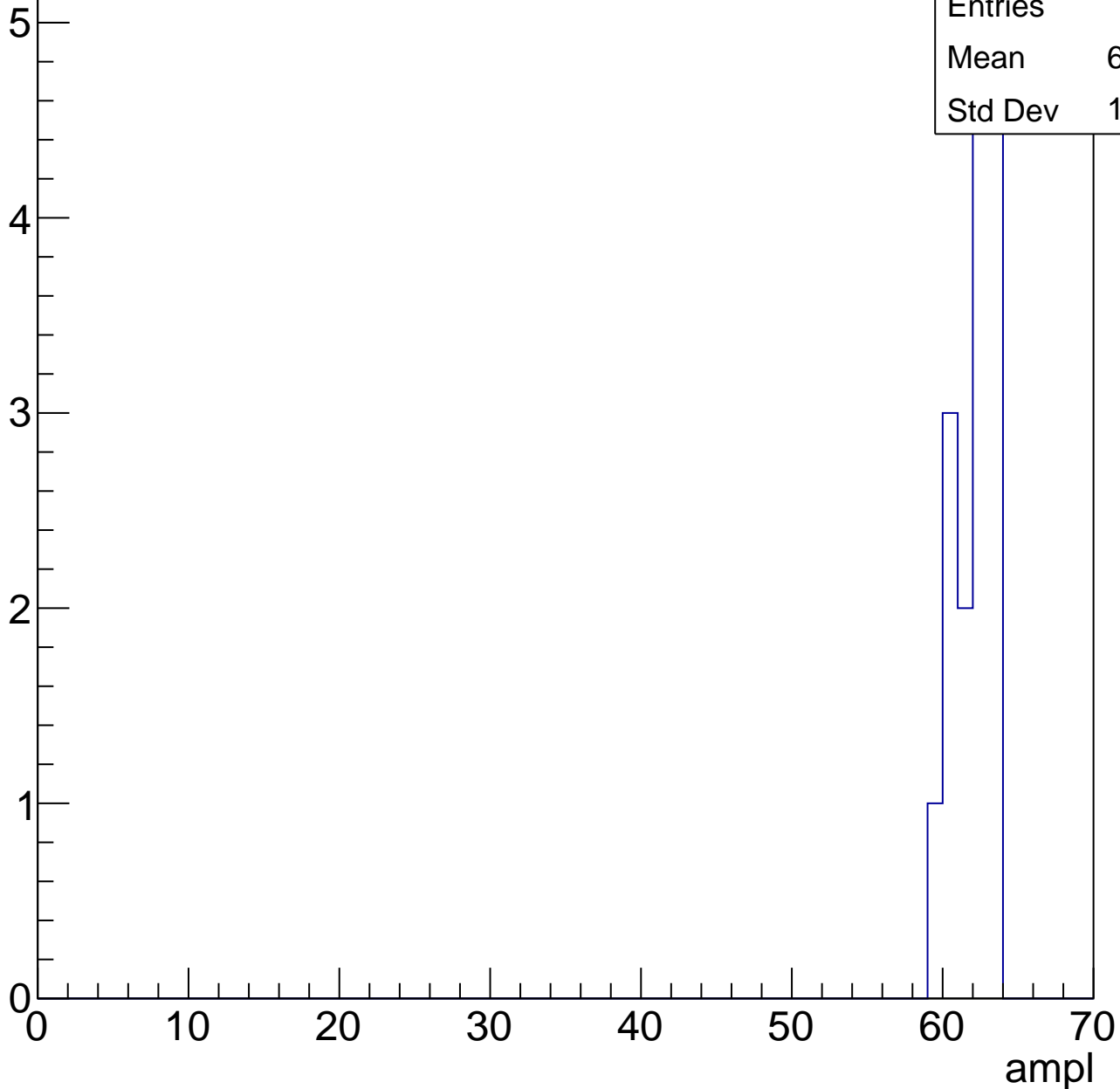


B0L002S, U26-ch27, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	16
Mean	61.62
Std Dev	1.269



B0L002S, U26-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch28, adc0

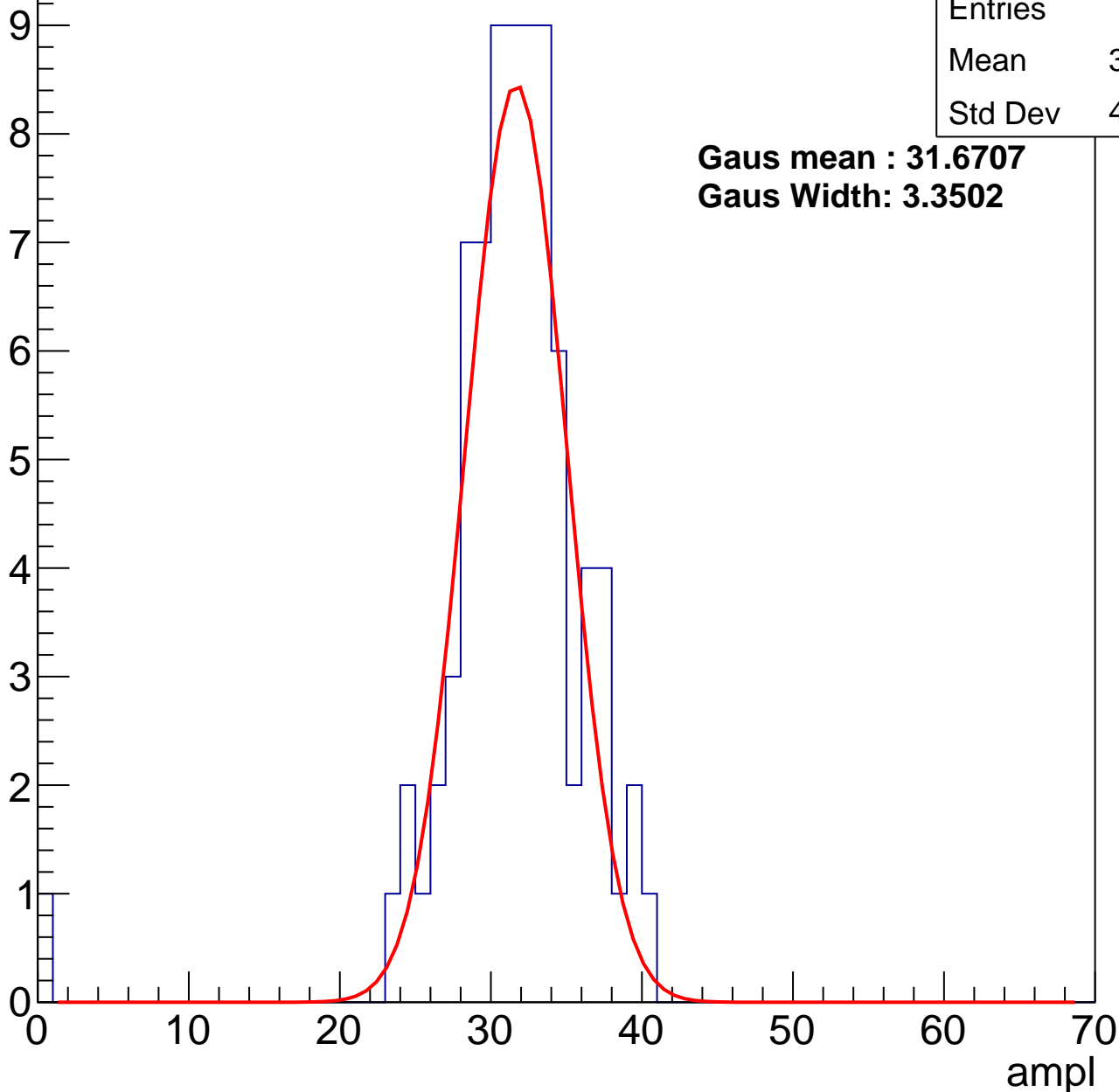
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	31.05
Std Dev	4.987

Gaus mean : 31.6707

Gaus Width: 3.3502



B0L002S, U26-ch28, adc1

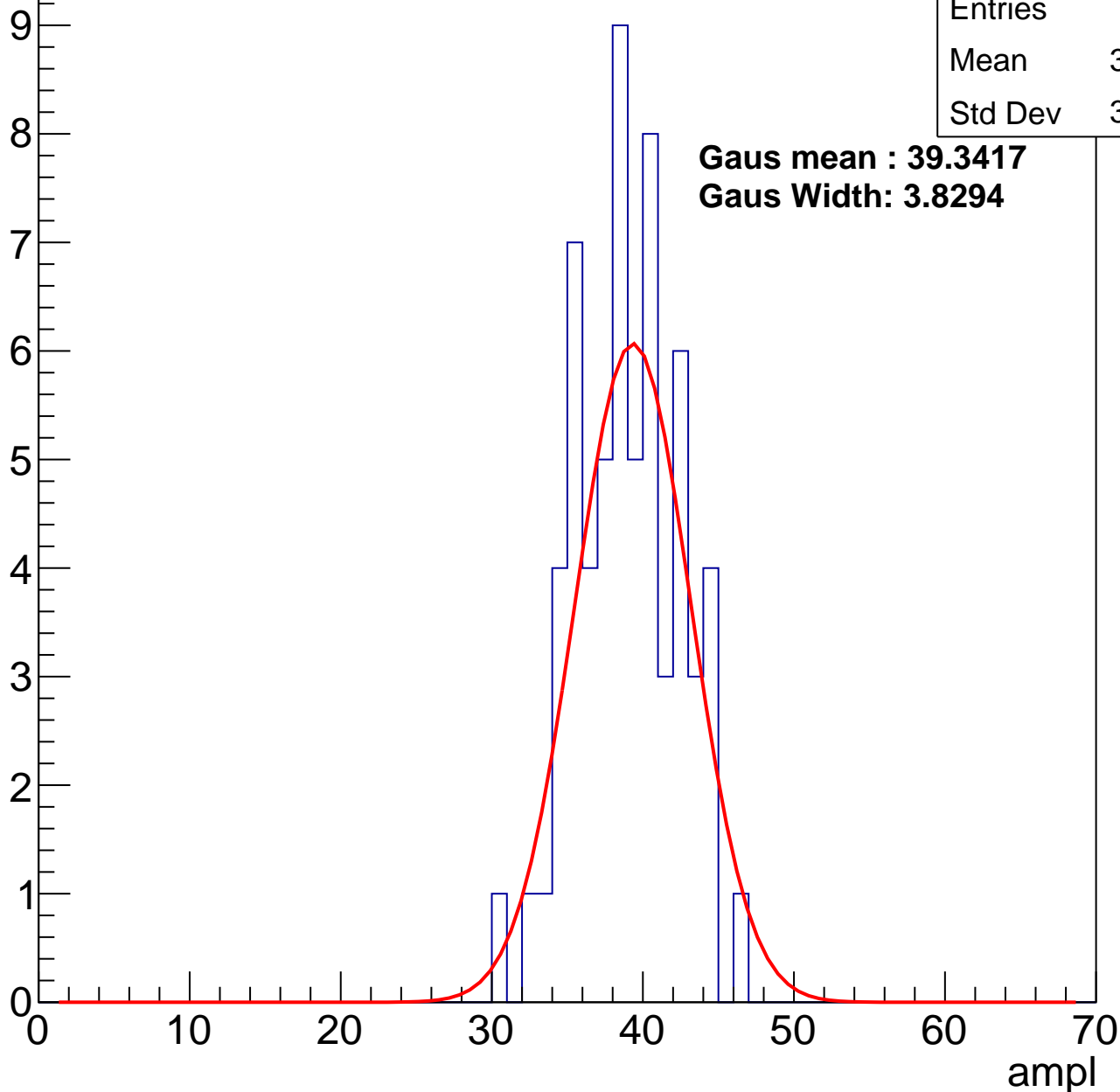
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.52
Std Dev	3.359

Gaus mean : 39.3417

Gaus Width: 3.8294



B0L002S, U26-ch28, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	45.16
Std Dev	3.731

Gaus mean : 46.1253

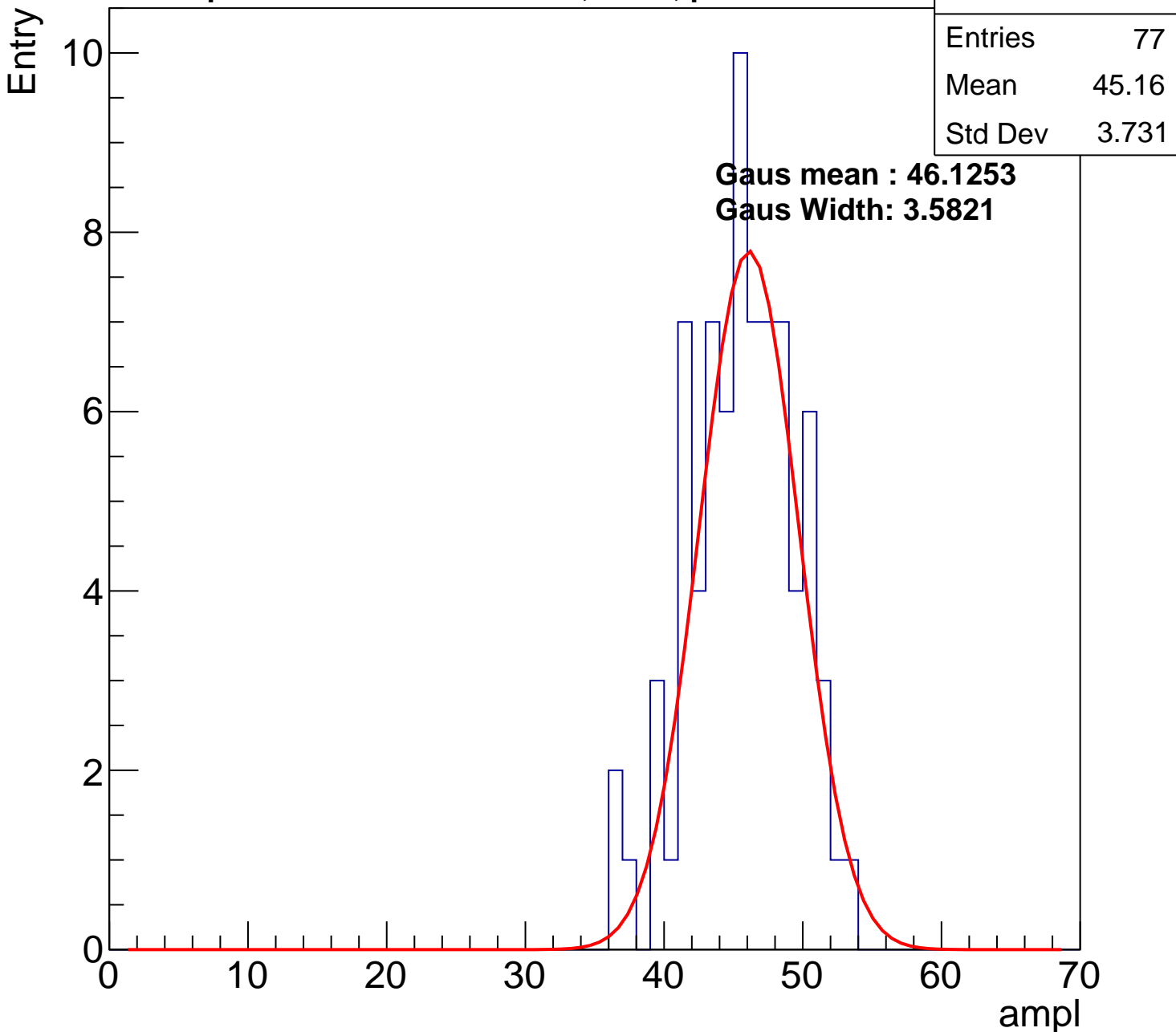
Gaus Width: 3.5821

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

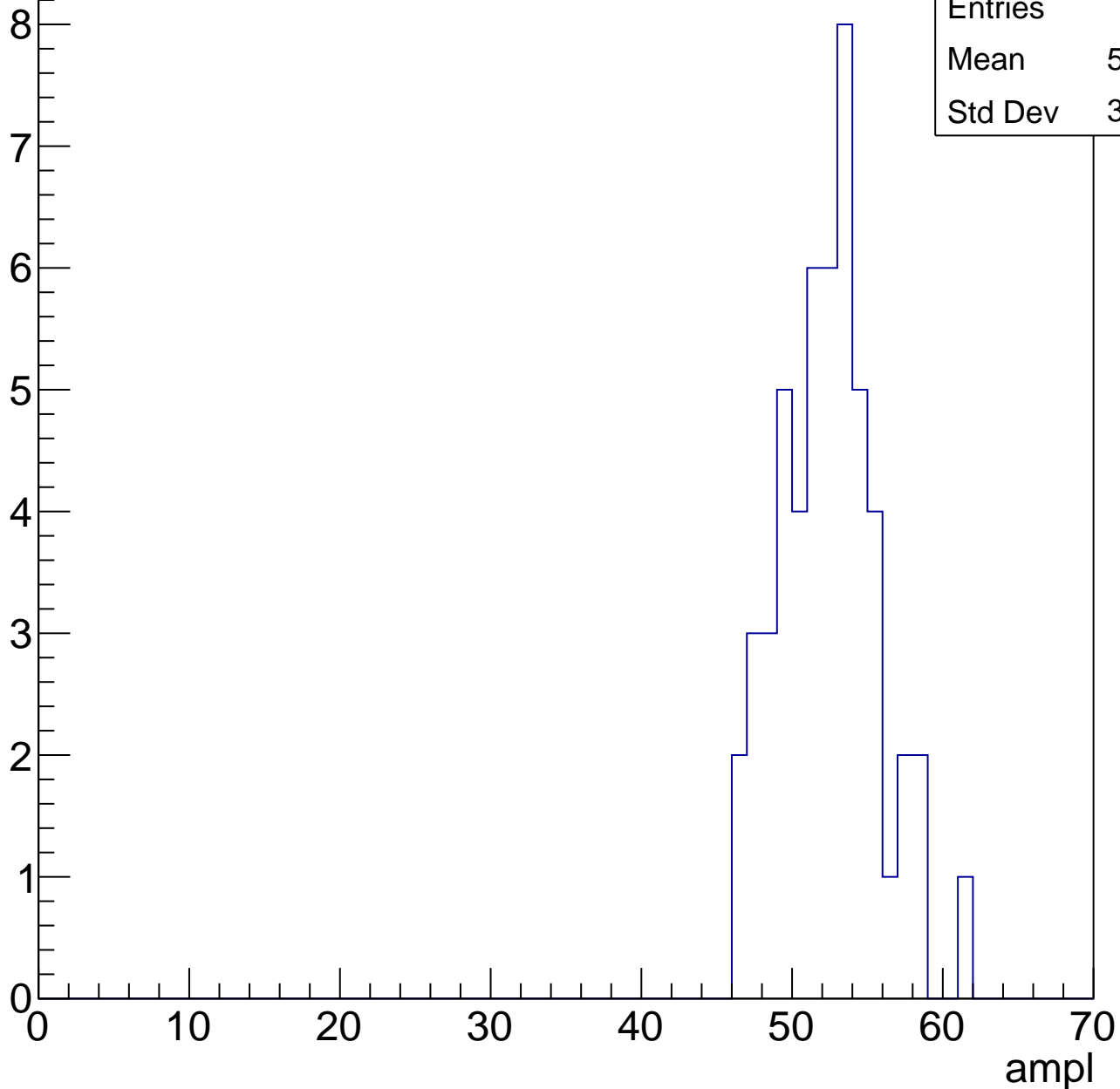


B0L002S, U26-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	51.94
Std Dev	3.237

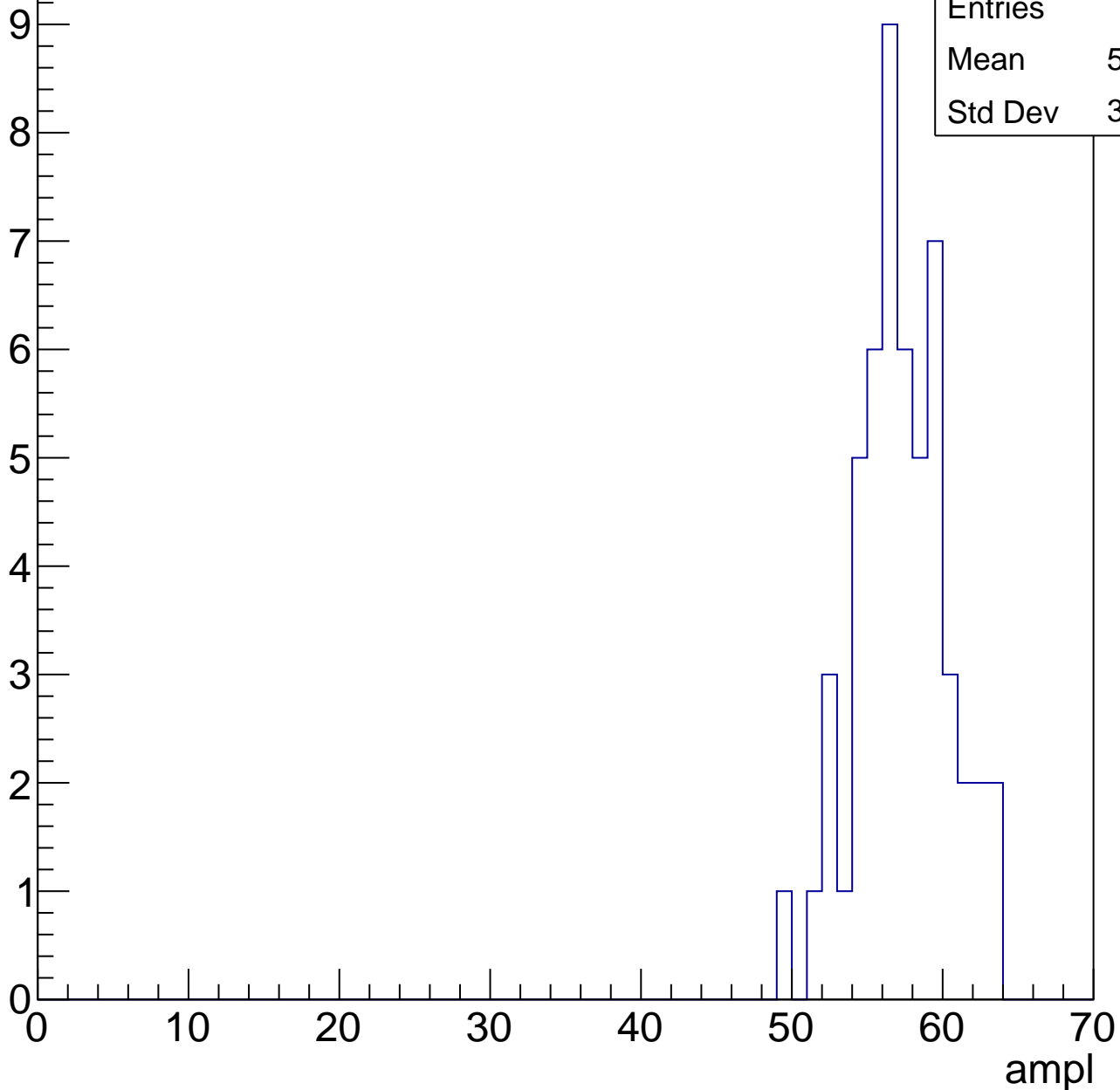


B0L002S, U26-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.79
Std Dev	3.012

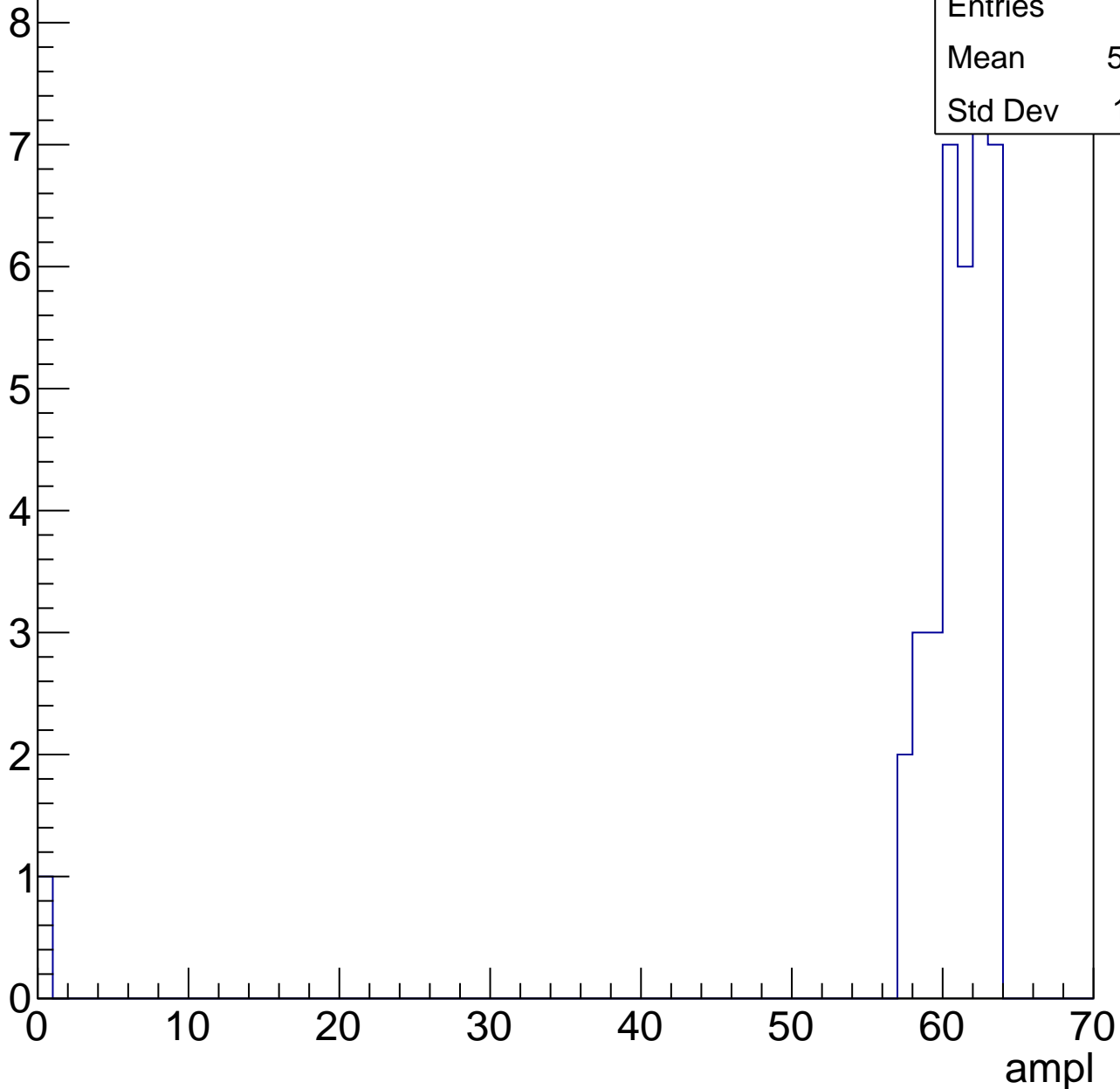


B0L002S, U26-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.14
Std Dev	10.01



B0L002S, U26-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch29, adc0

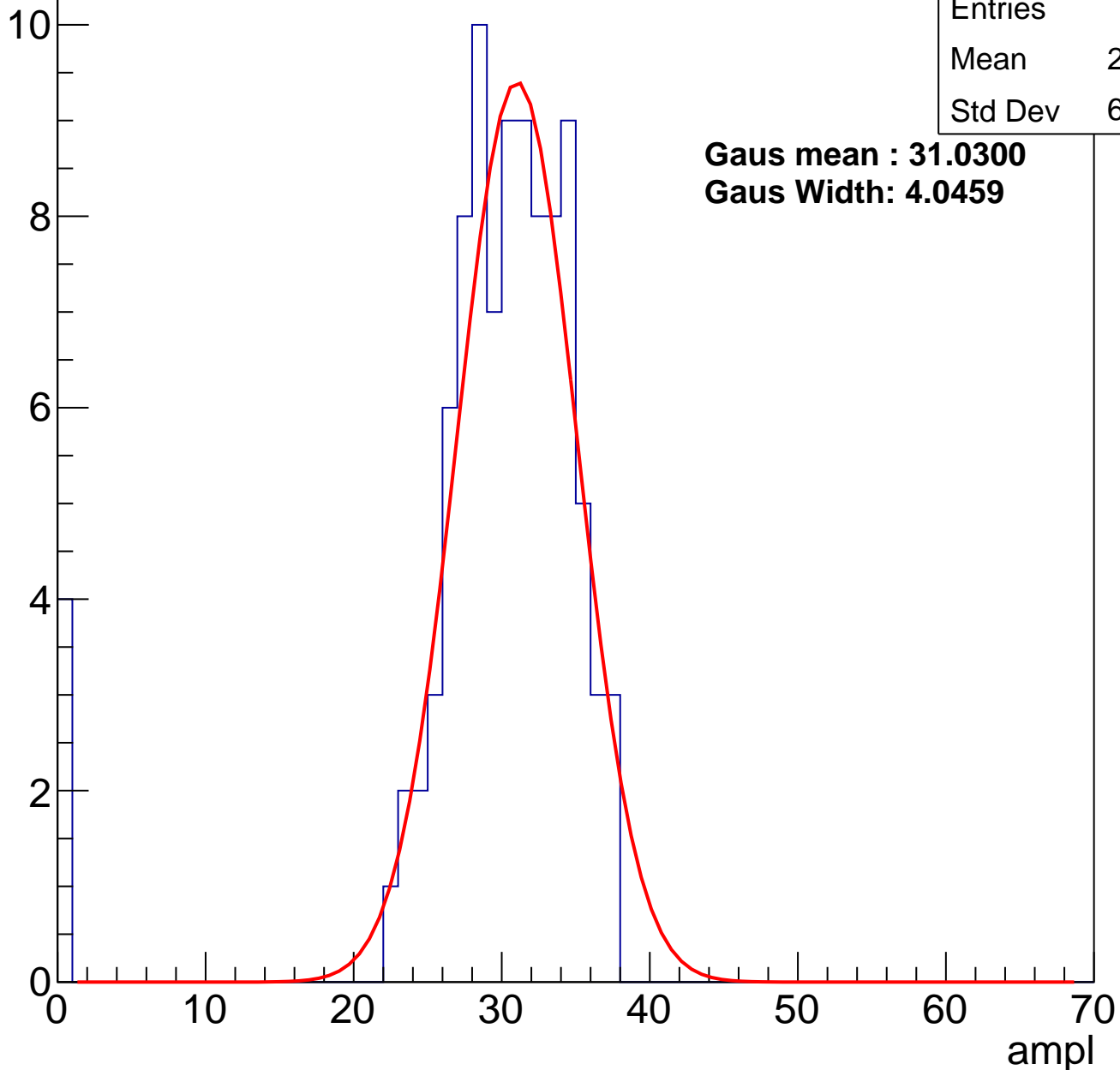
calib_packv5_042523_0143.root, FC#8, port C1

Entries	97
Mean	29.02
Std Dev	6.937

Gaus mean : 31.0300

Gaus Width: 4.0459

Entry



B0L002S, U26-ch29, adc1

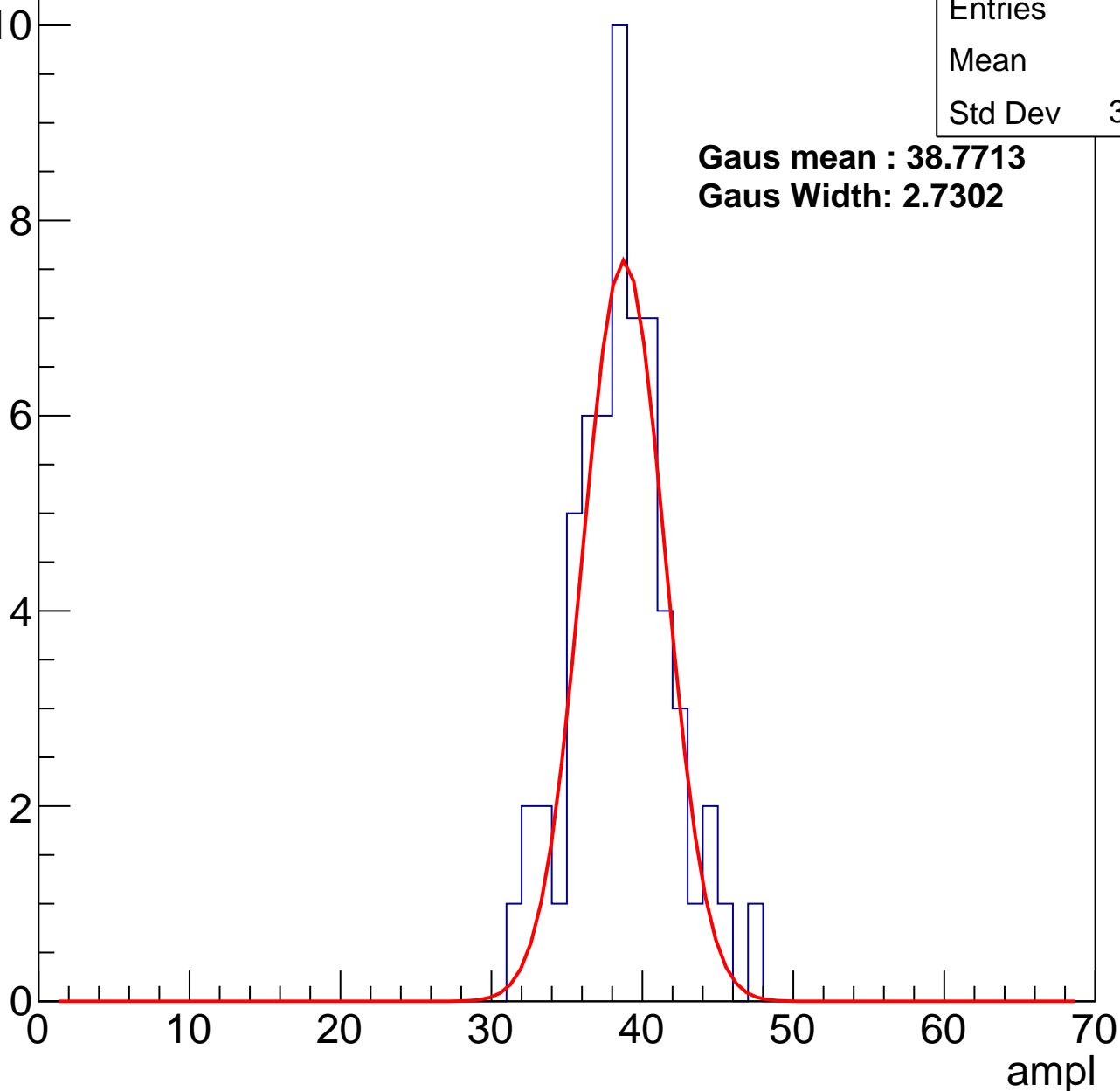
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38.2
Std Dev	3.204

Gaus mean : 38.7713

Gaus Width: 2.7302



B0L002S, U26-ch29, adc2

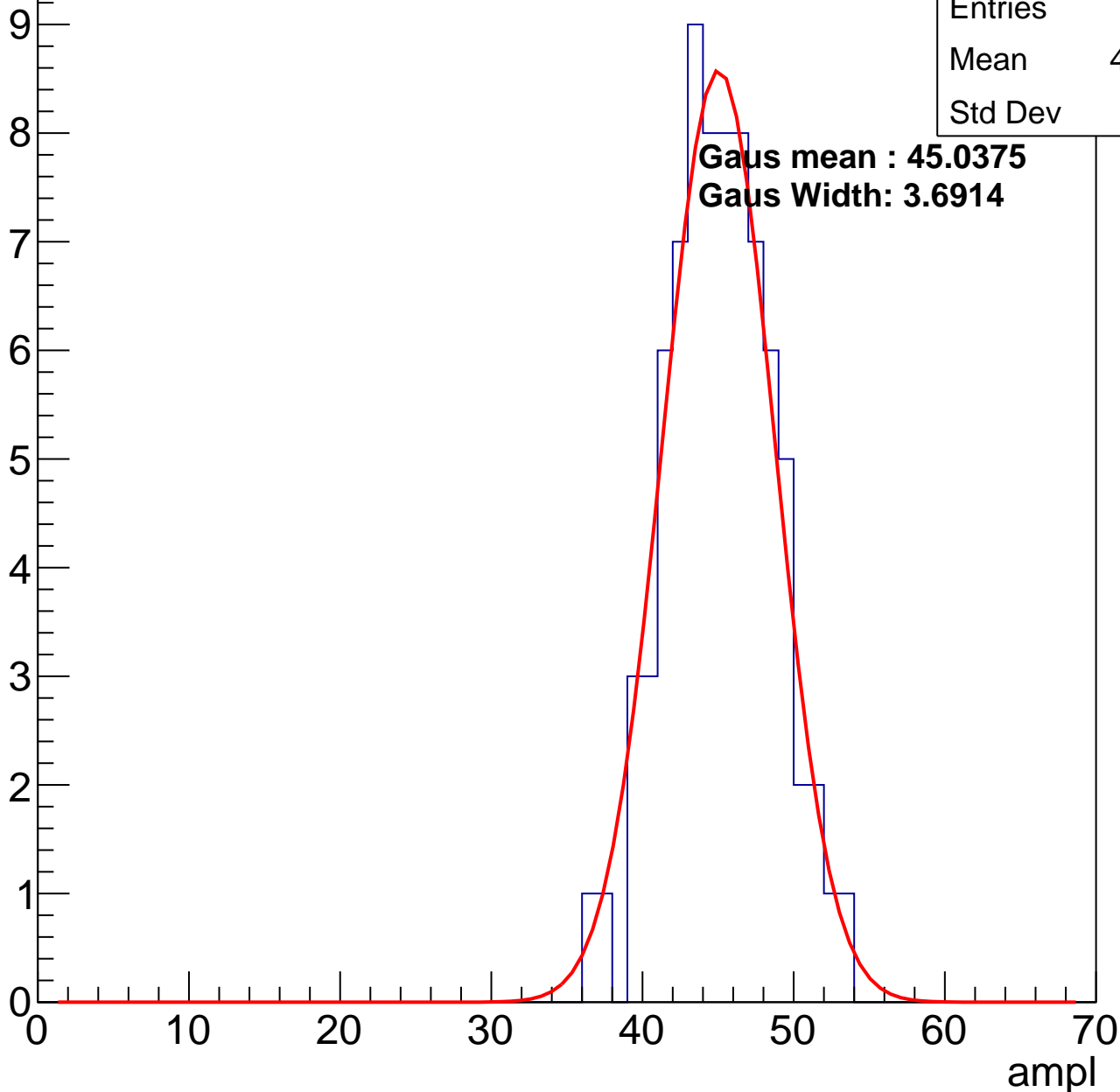
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	44.69
Std Dev	3.45

Gaus mean : 45.0375

Gaus Width: 3.6914

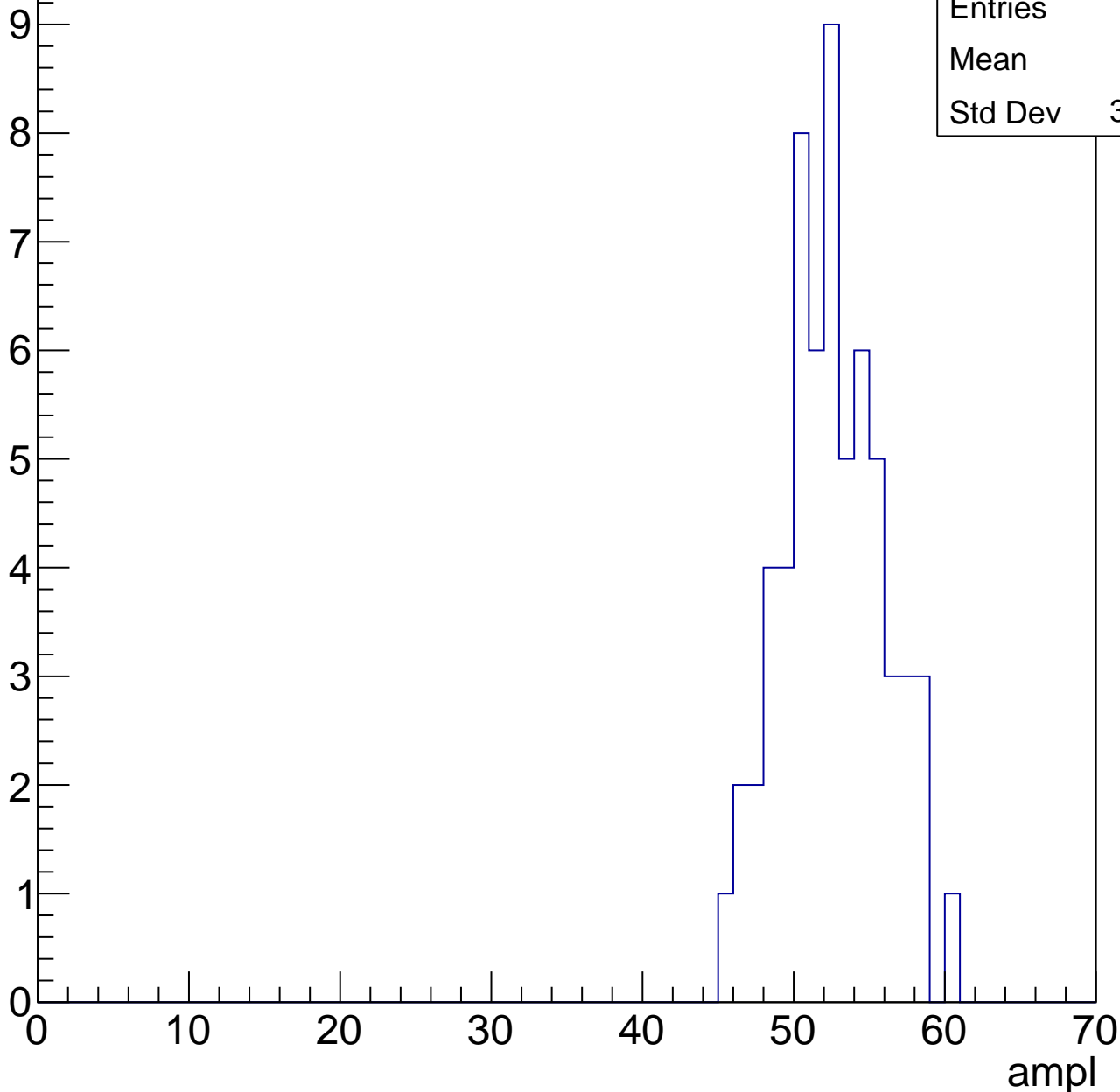


B0L002S, U26-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	52.1
Std Dev	3.305

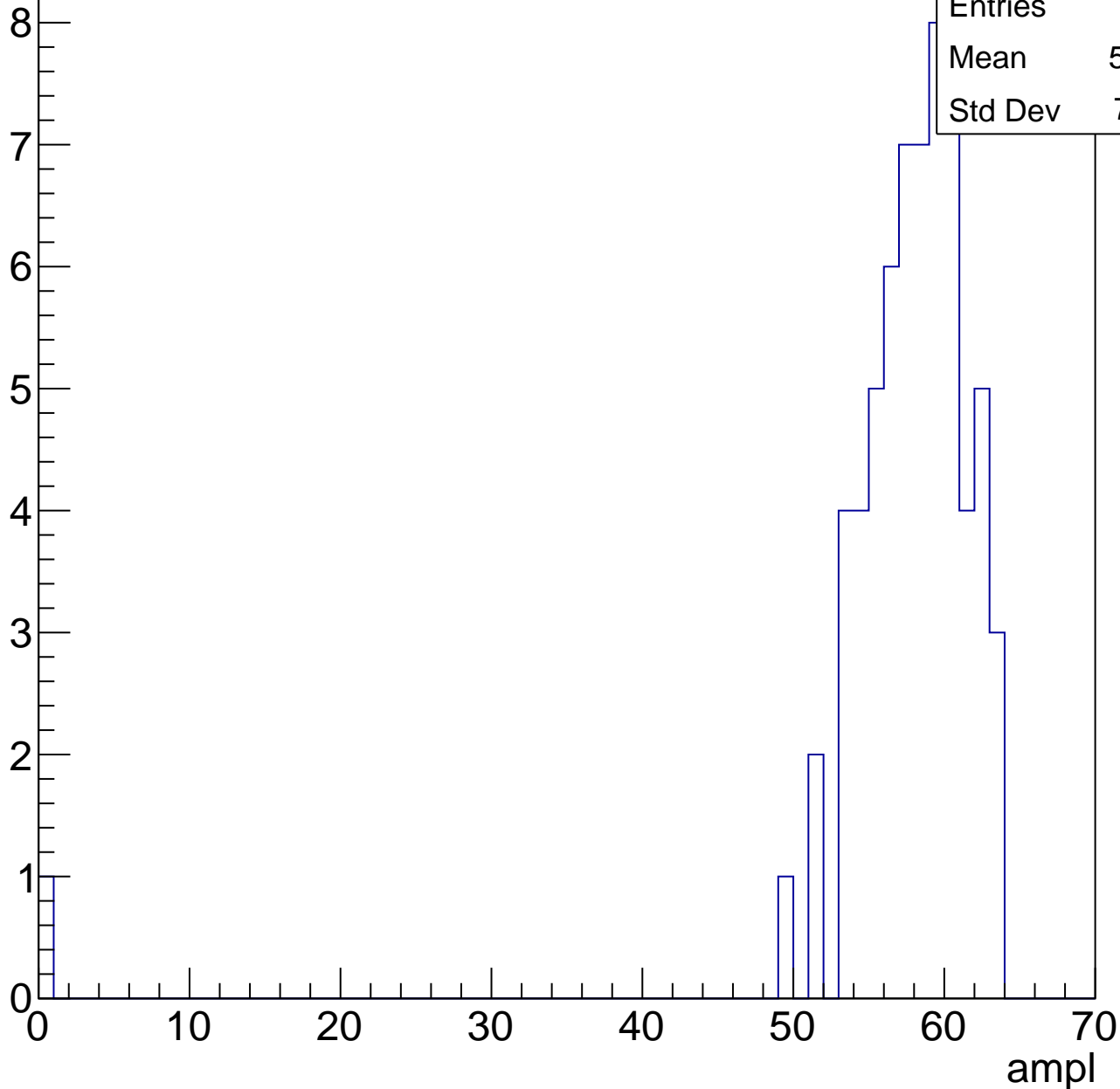


B0L002S, U26-ch29, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	56.77
Std Dev	7.761

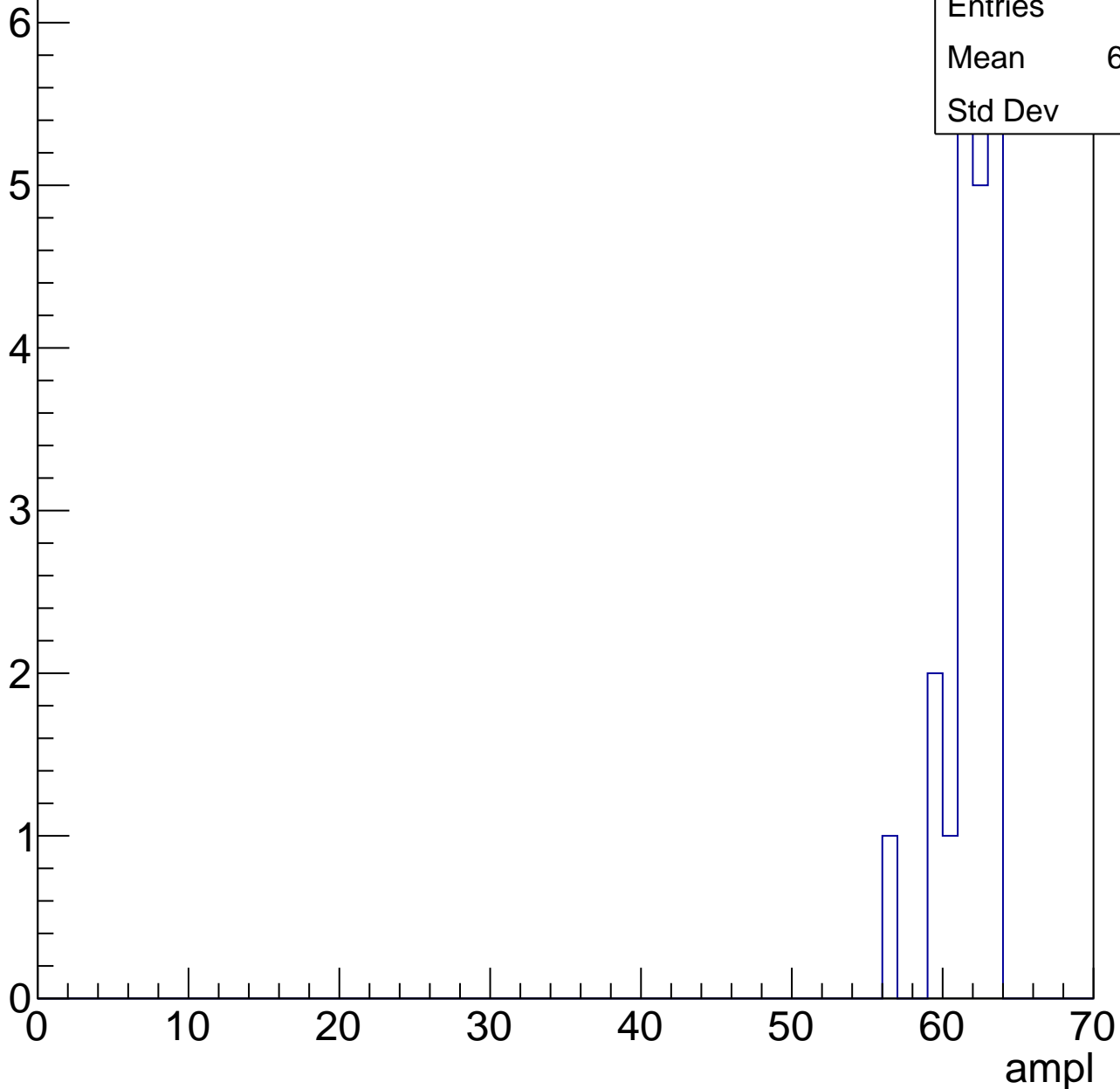


B0L002S, U26-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	61.33
Std Dev	1.7



B0L002S, U26-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch30, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	31.04
Std Dev	3.208

Gaus mean : 31.2594

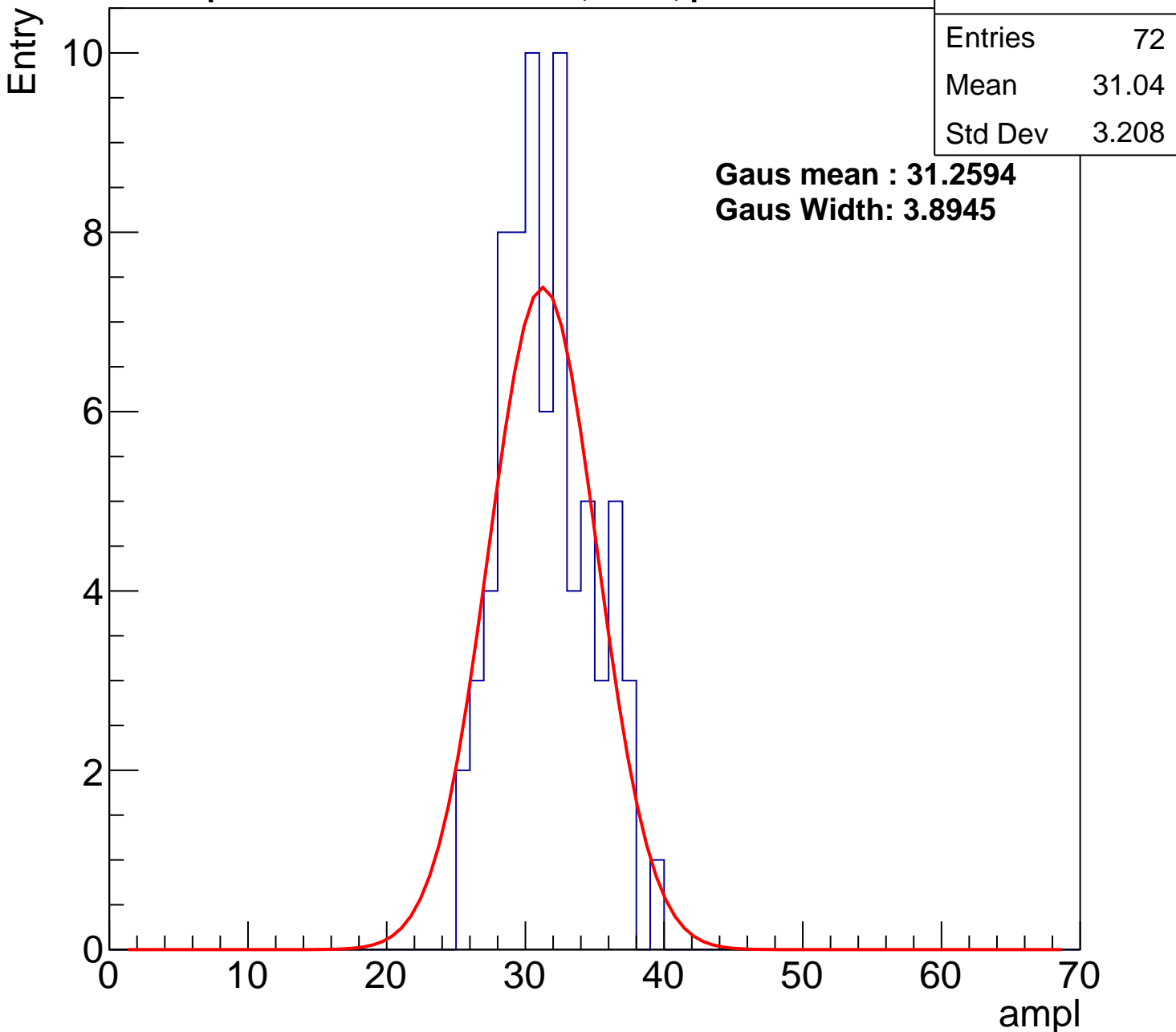
Gaus Width: 3.8945

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch30, adc1

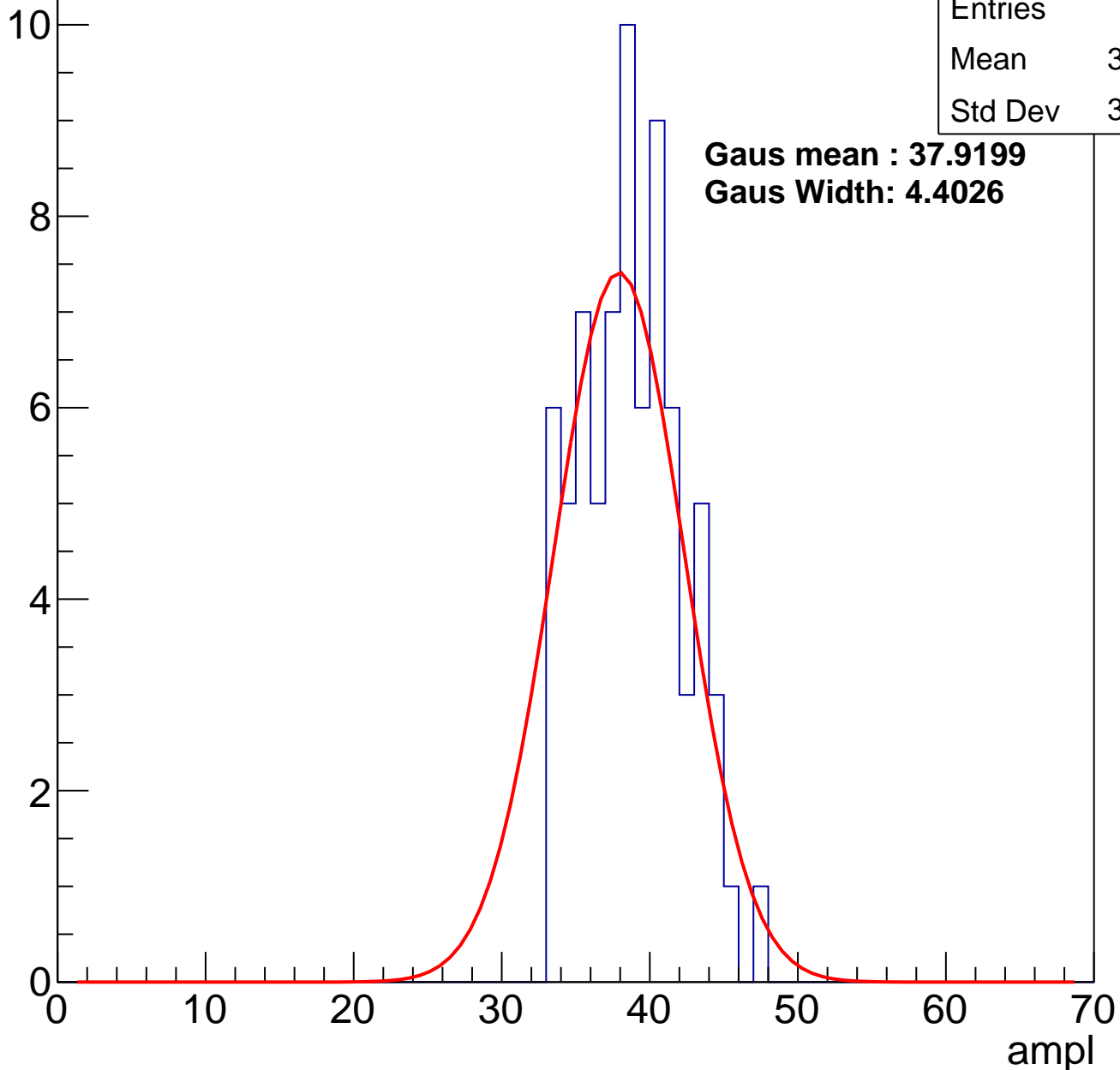
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	38.34
Std Dev	3.326

Gaus mean : 37.9199

Gaus Width: 4.4026

Entry



B0L002S, U26-ch30, adc2

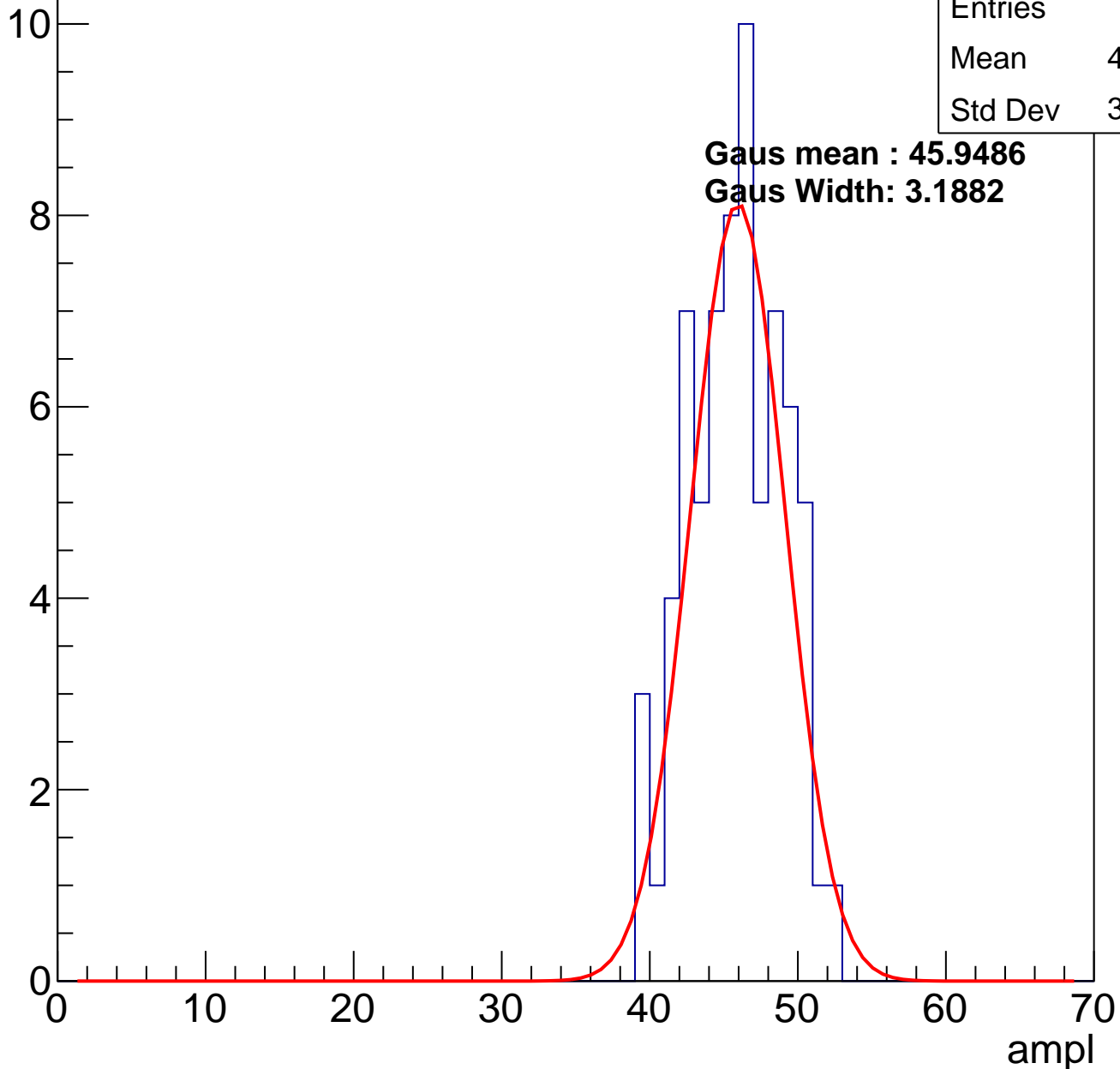
calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	45.37
Std Dev	3.108

Gaus mean : 45.9486

Gaus Width: 3.1882

Entry

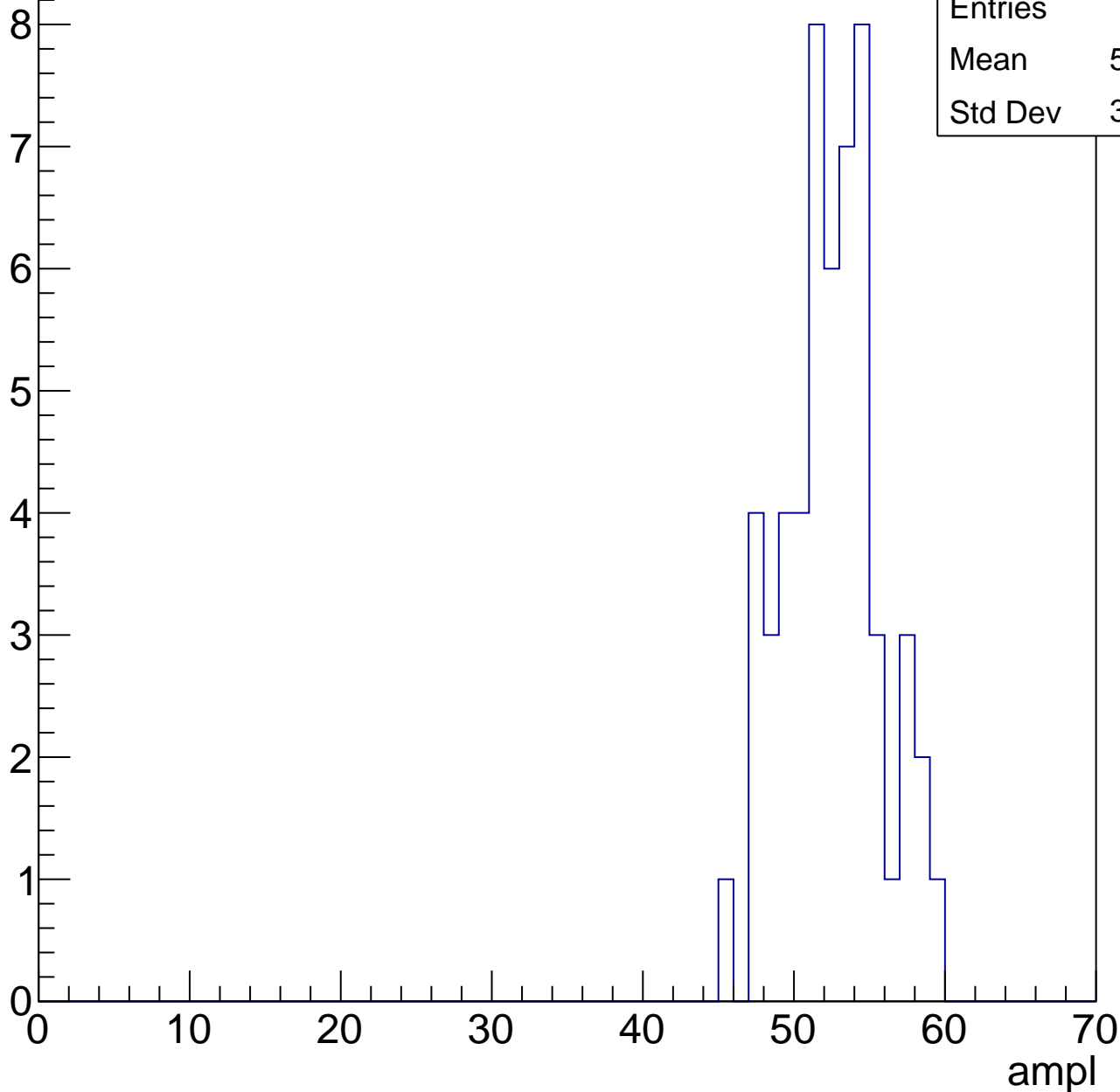


B0L002S, U26-ch30, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

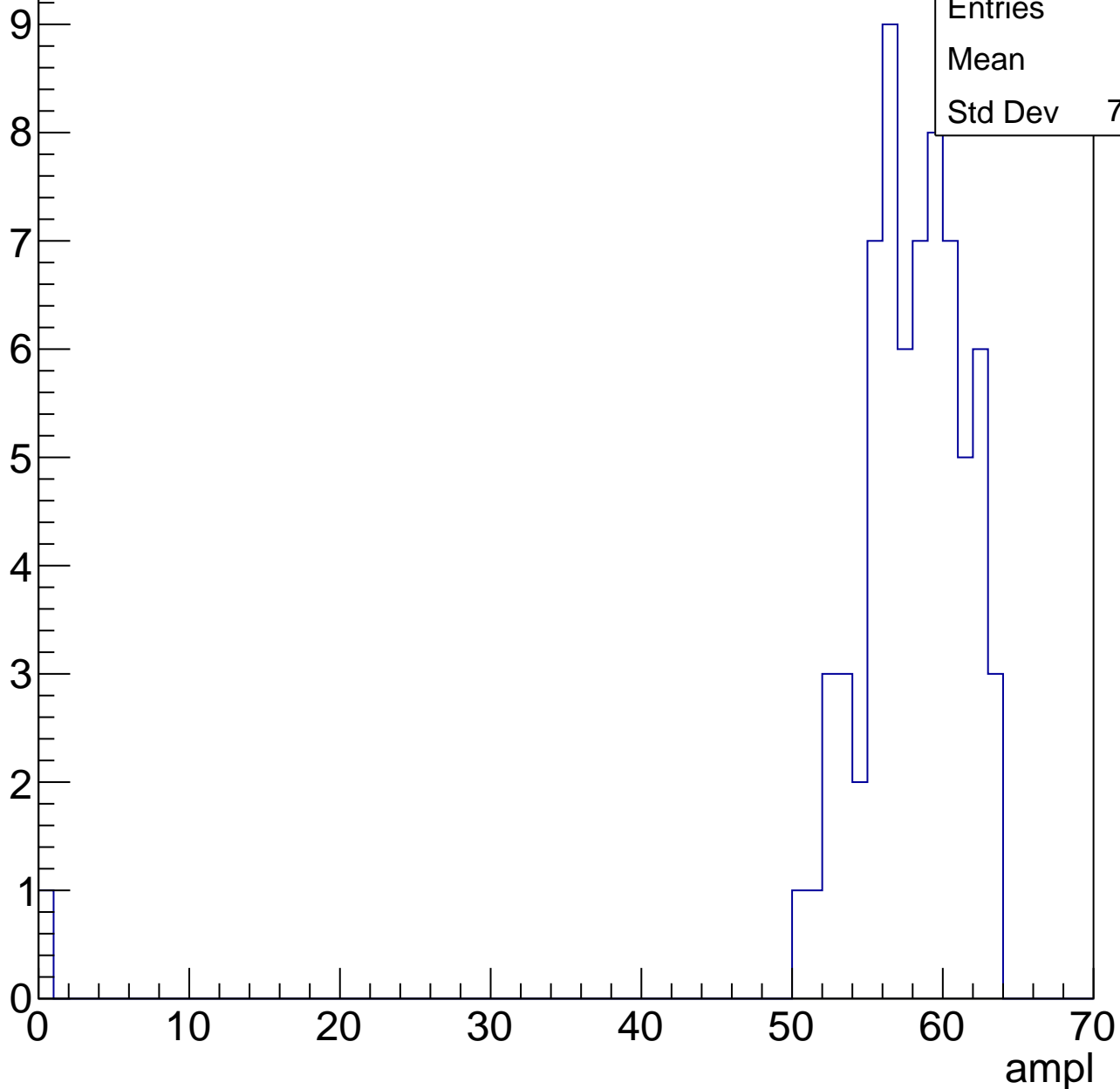
Entries	55
Mean	52.05
Std Dev	3.118



B0L002S, U26-ch30, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

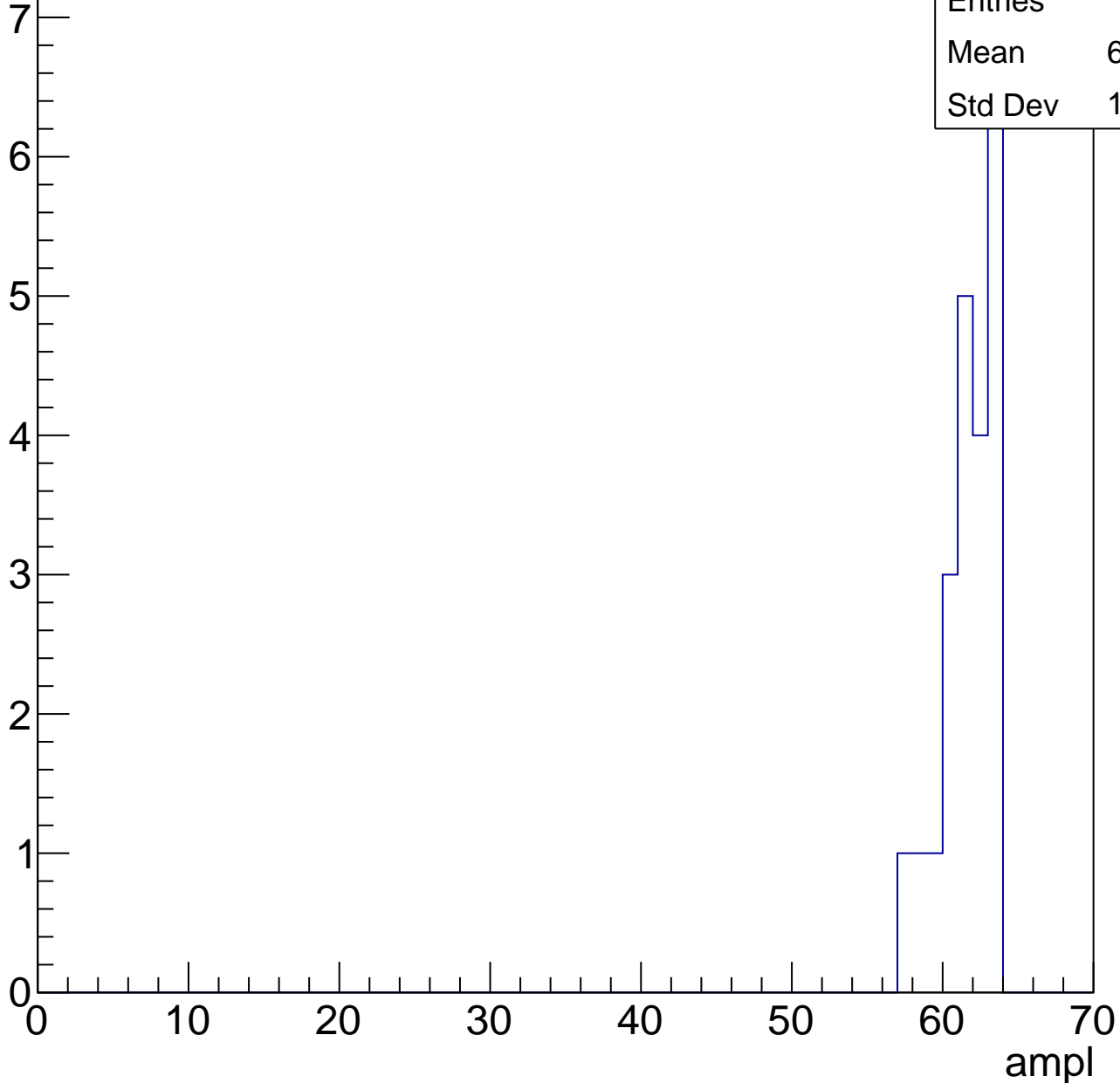


B0L002S, U26-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	61.27
Std Dev	1.684



B0L002S, U26-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch31, adc0

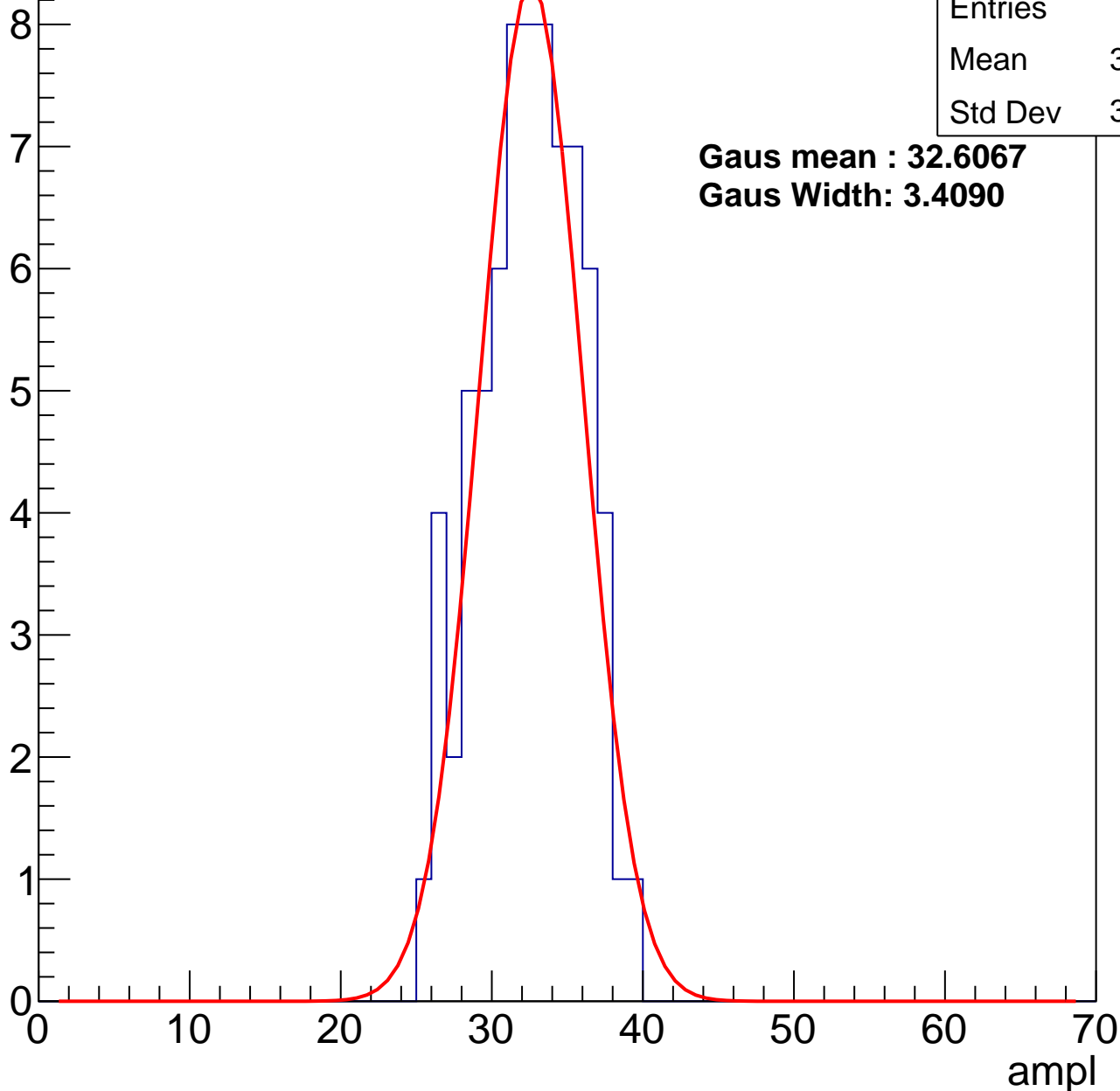
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	32.05
Std Dev	3.268

Gaus mean : 32.6067

Gaus Width: 3.4090



B0L002S, U26-ch31, adc1

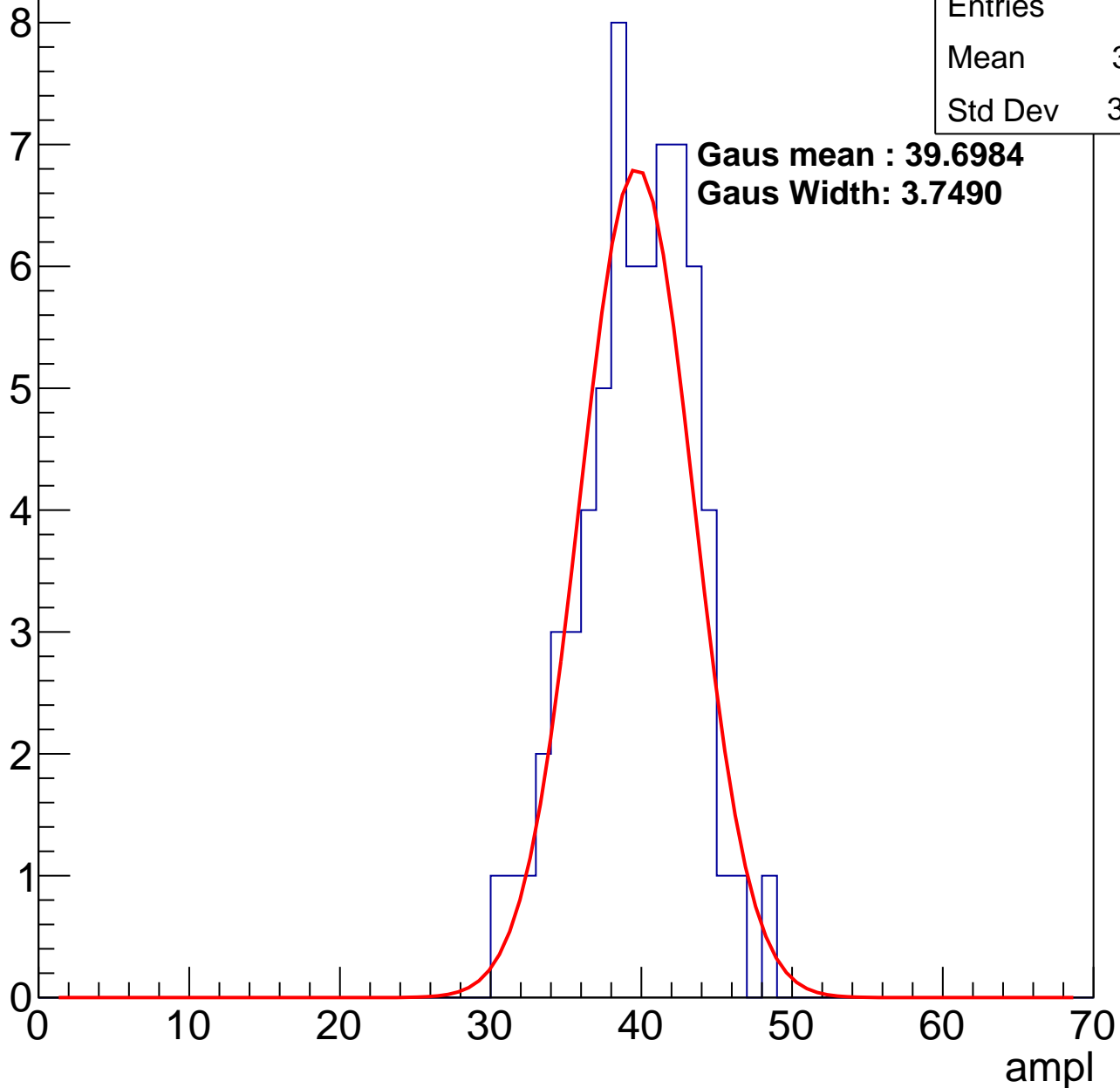
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	39.21
Std Dev	3.684

Gaus mean : 39.6984

Gaus Width: 3.7490



B0L002S, U26-ch31, adc2

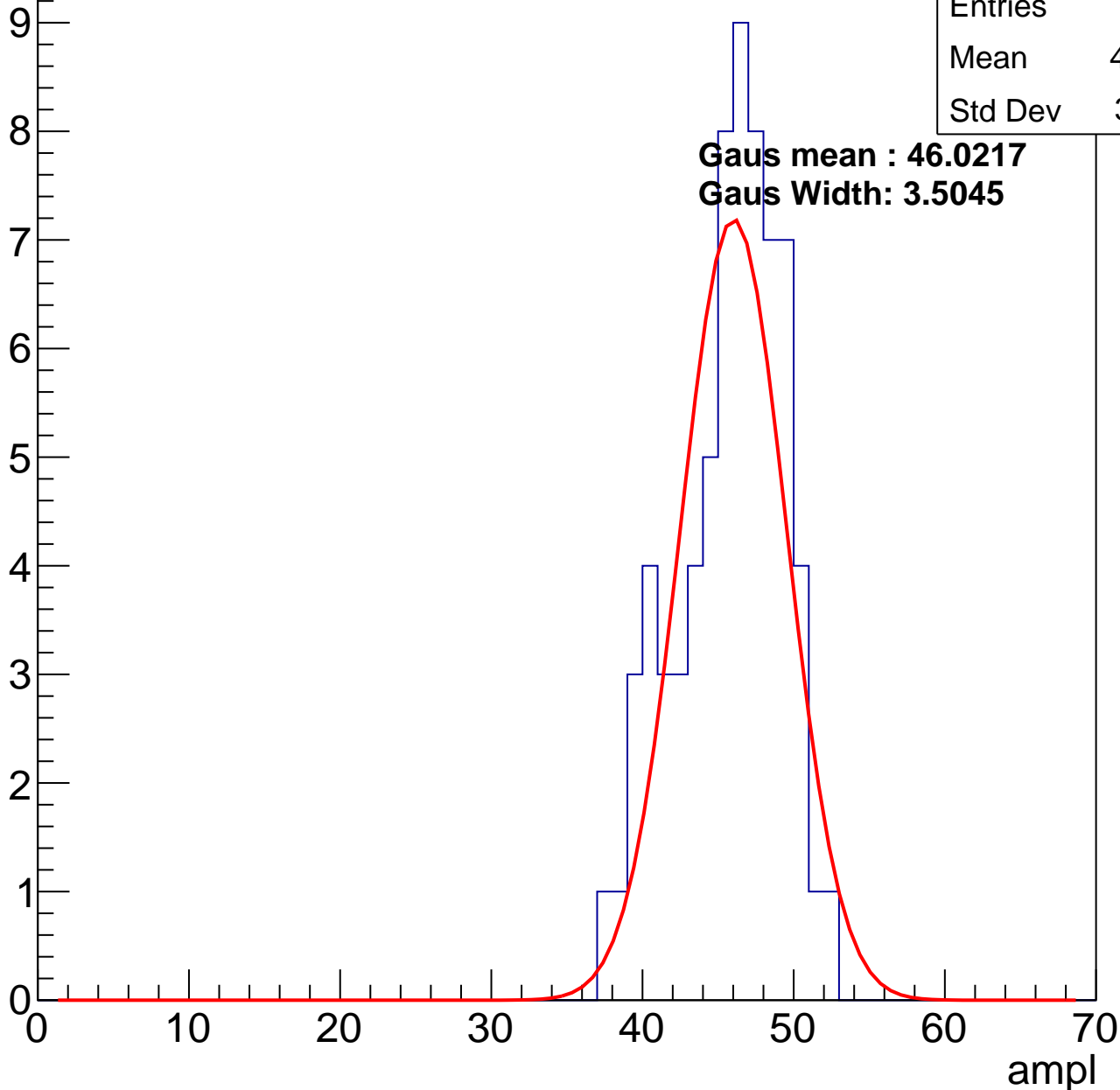
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	45.29
Std Dev	3.431

Gaus mean : 46.0217

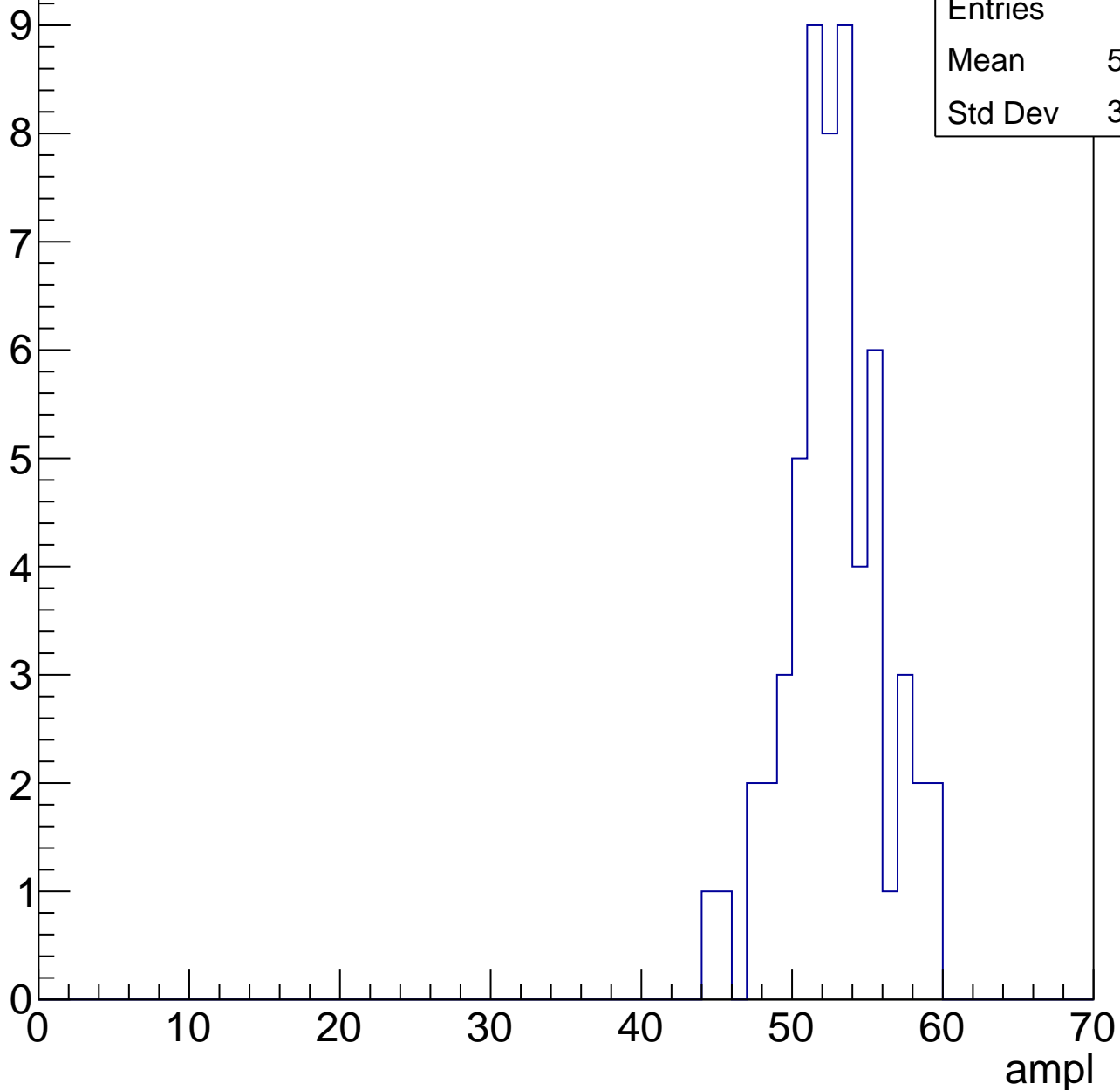
Gaus Width: 3.5045



B0L002S, U26-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

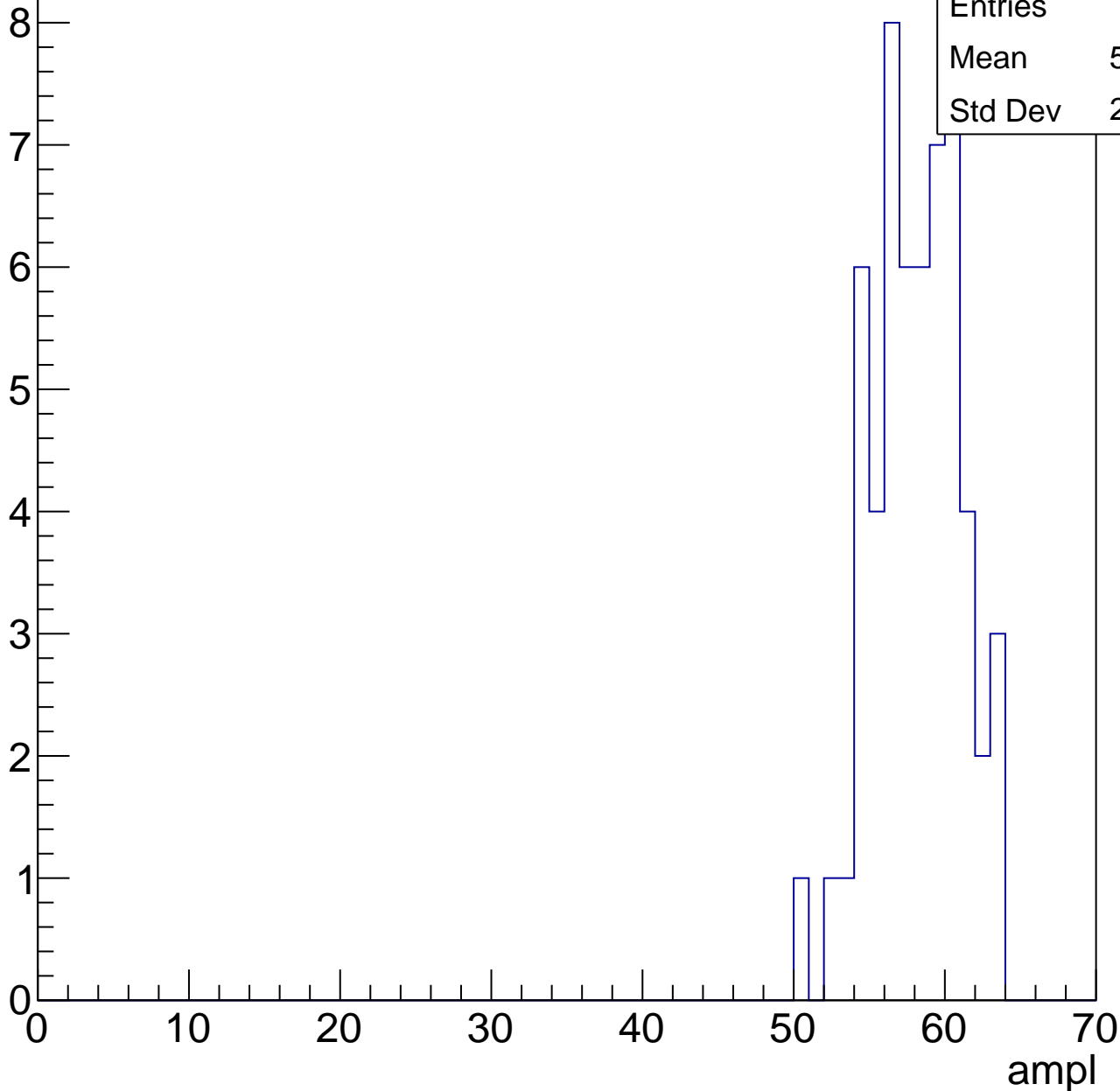


B0L002S, U26-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	57.67
Std Dev	2.874

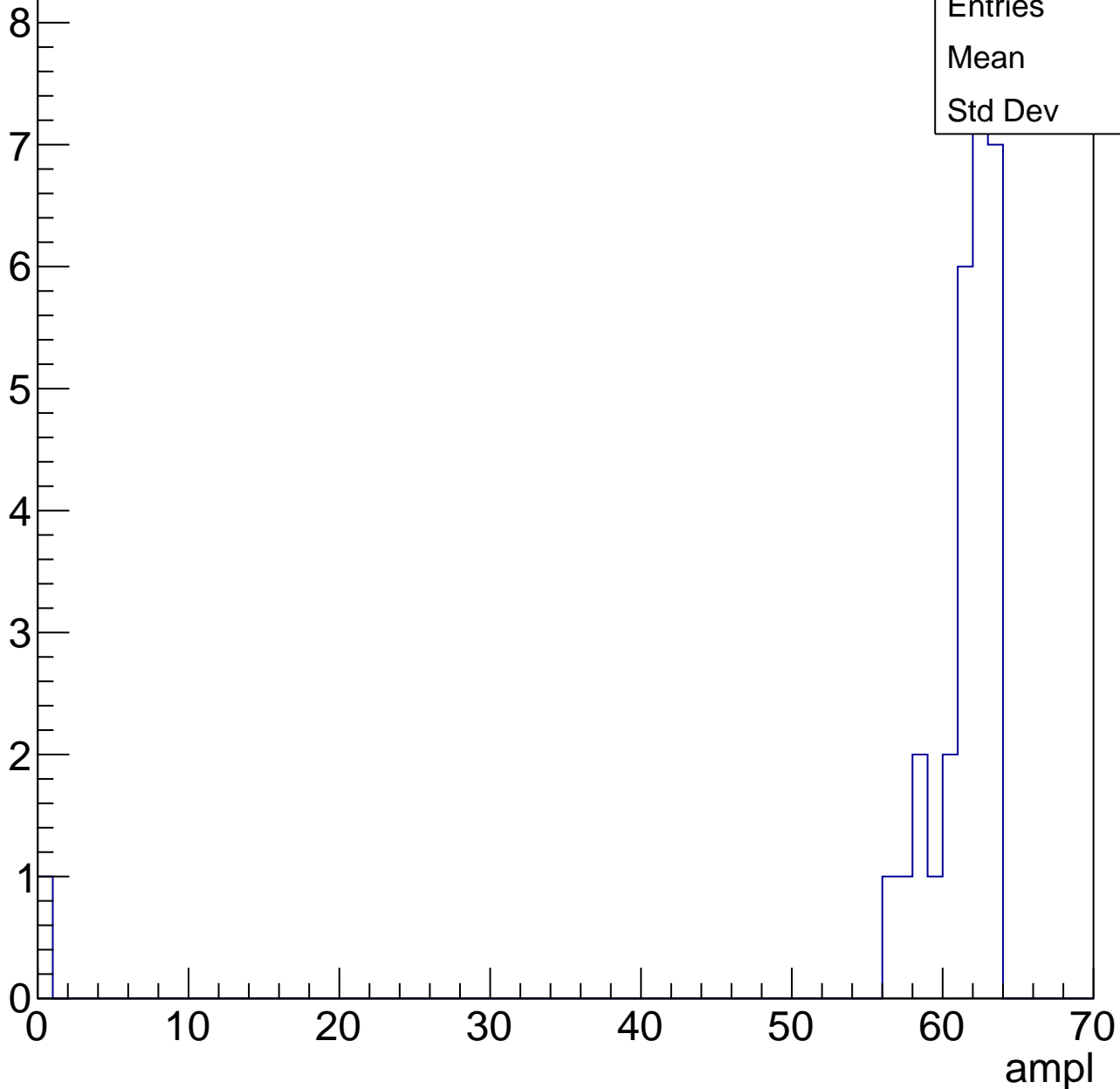


B0L002S, U26-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59
Std Dev	11.3



B0L002S, U26-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch32, adc0

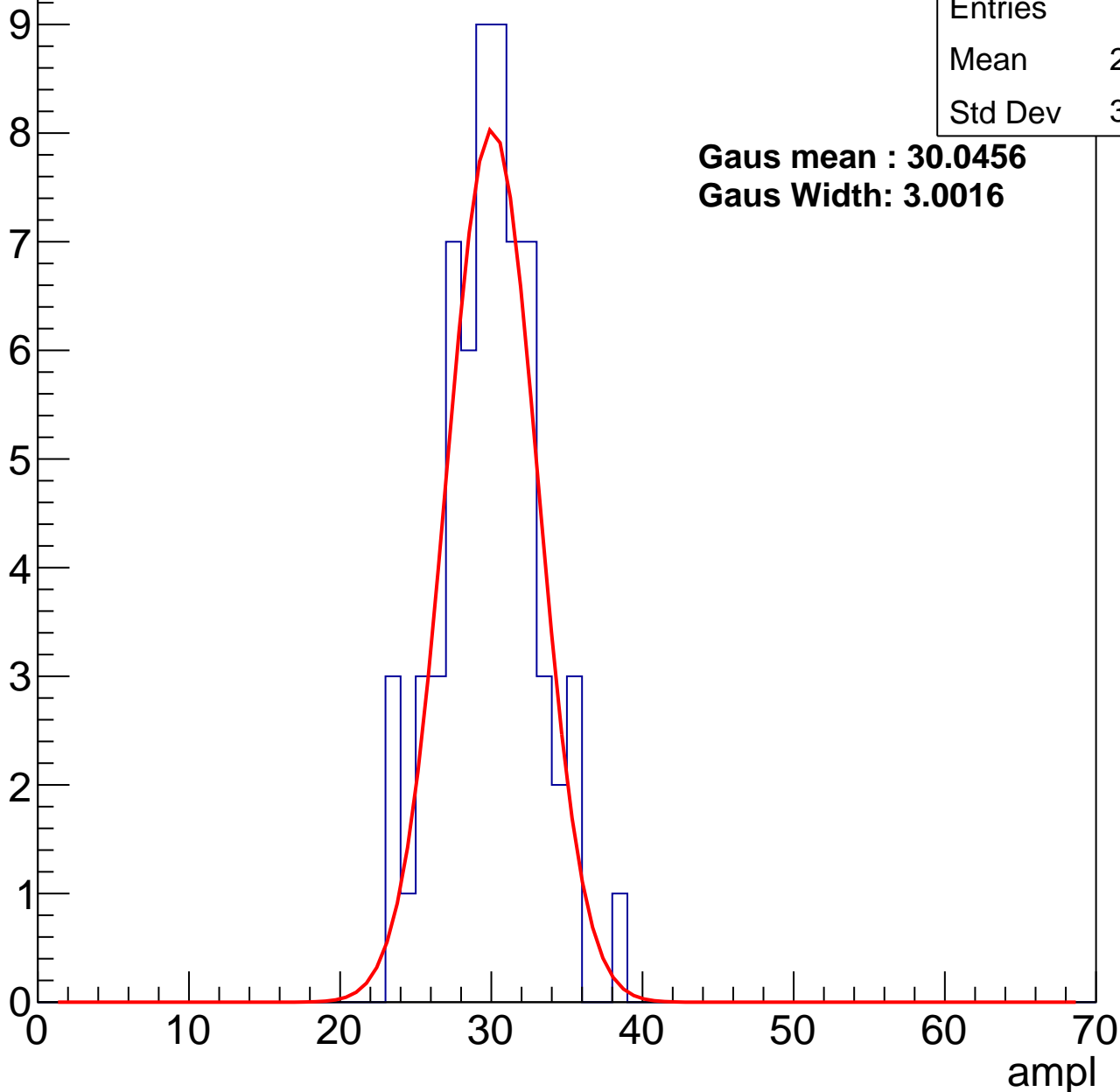
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.45
Std Dev	3.102

Gaus mean : 30.0456

Gaus Width: 3.0016



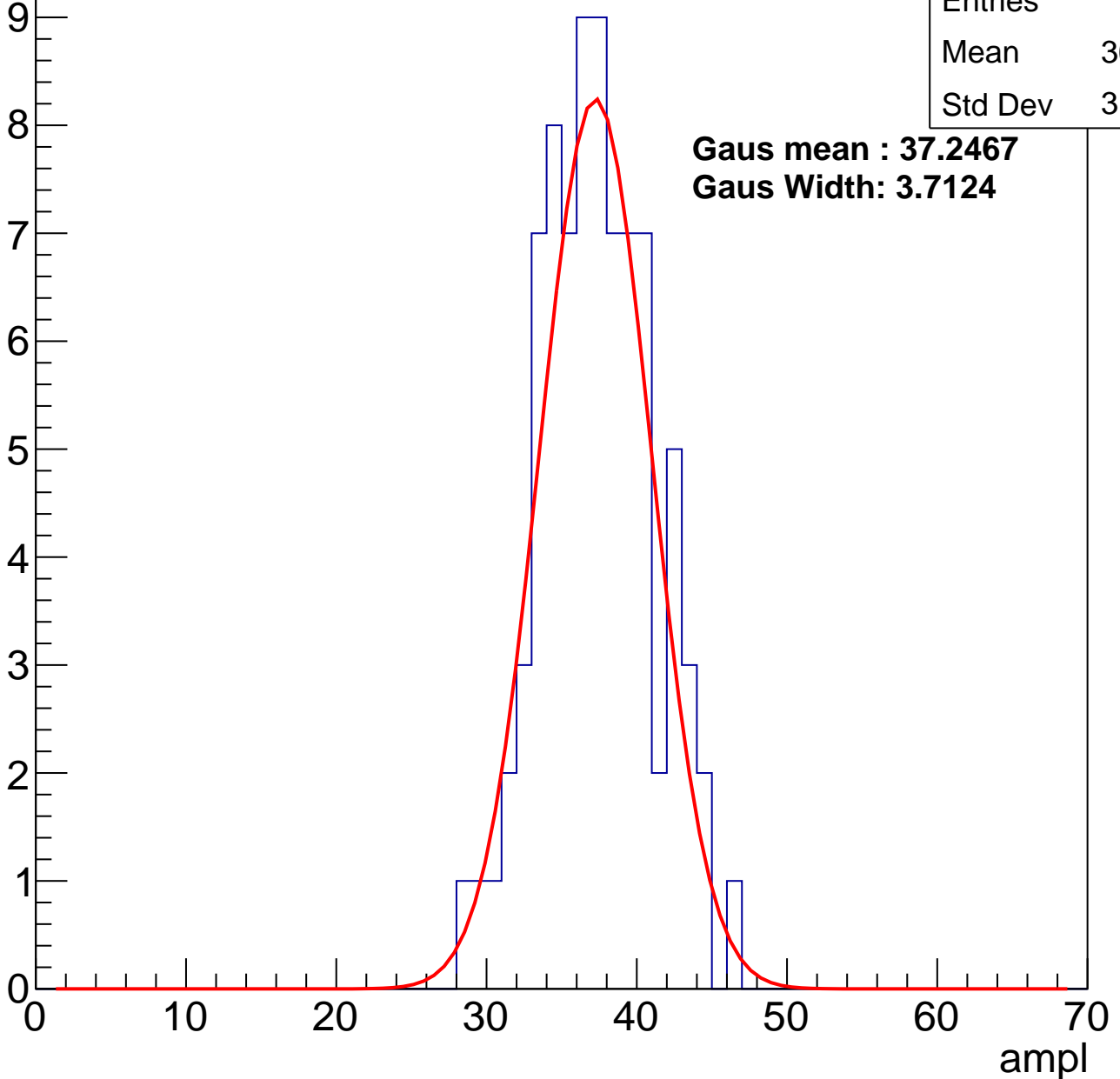
B0L002S, U26-ch32, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	36.88
Std Dev	3.667

Gaus mean : 37.2467
Gaus Width: 3.7124



B0L002S, U26-ch32, adc2

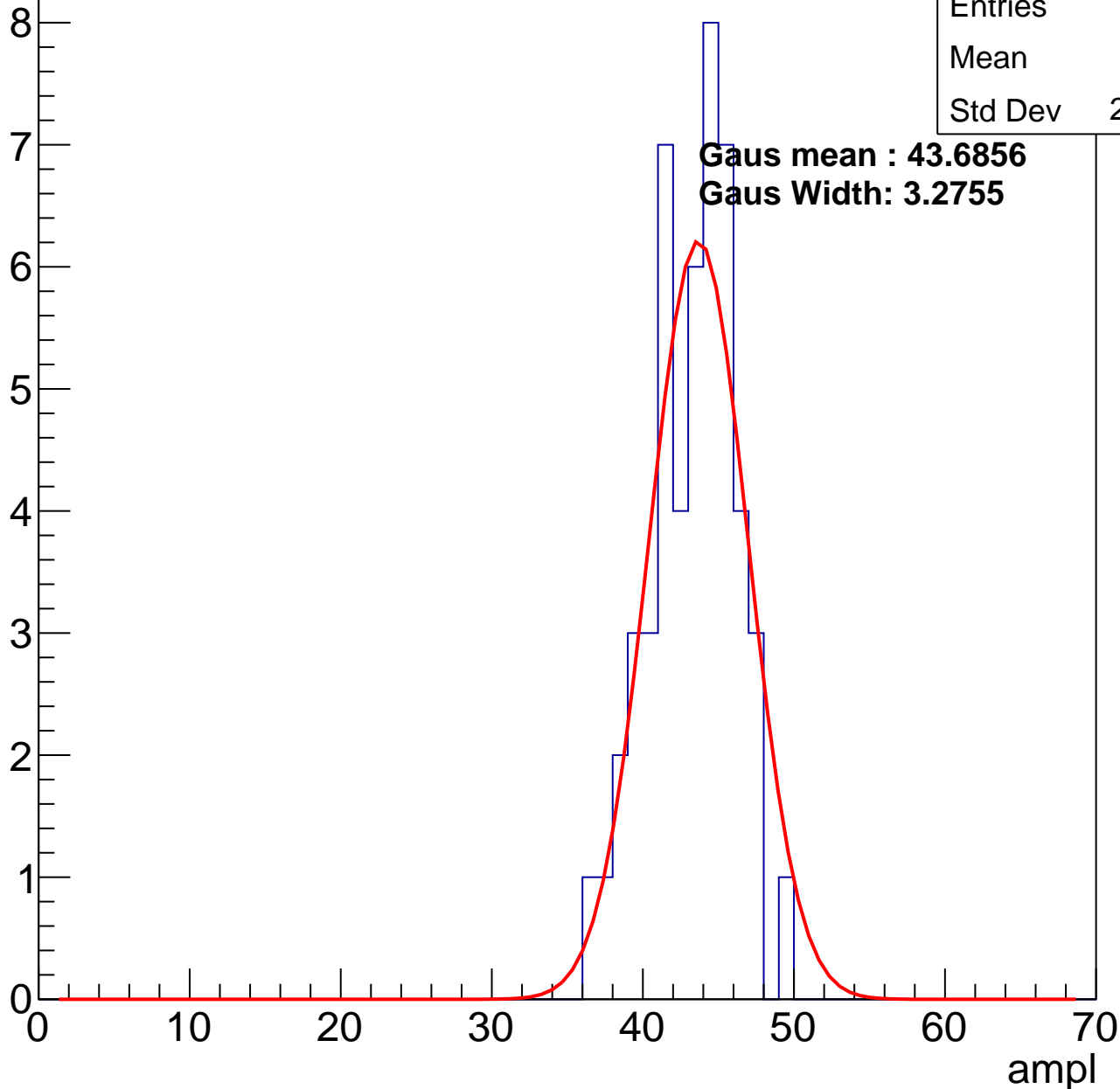
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	42.8
Std Dev	2.814

Gaus mean : 43.6856

Gaus Width: 3.2755



B0L002S, U26-ch32, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	49.85
Std Dev	3.51

Entry

10

8

6

4

2

0

0

10

20

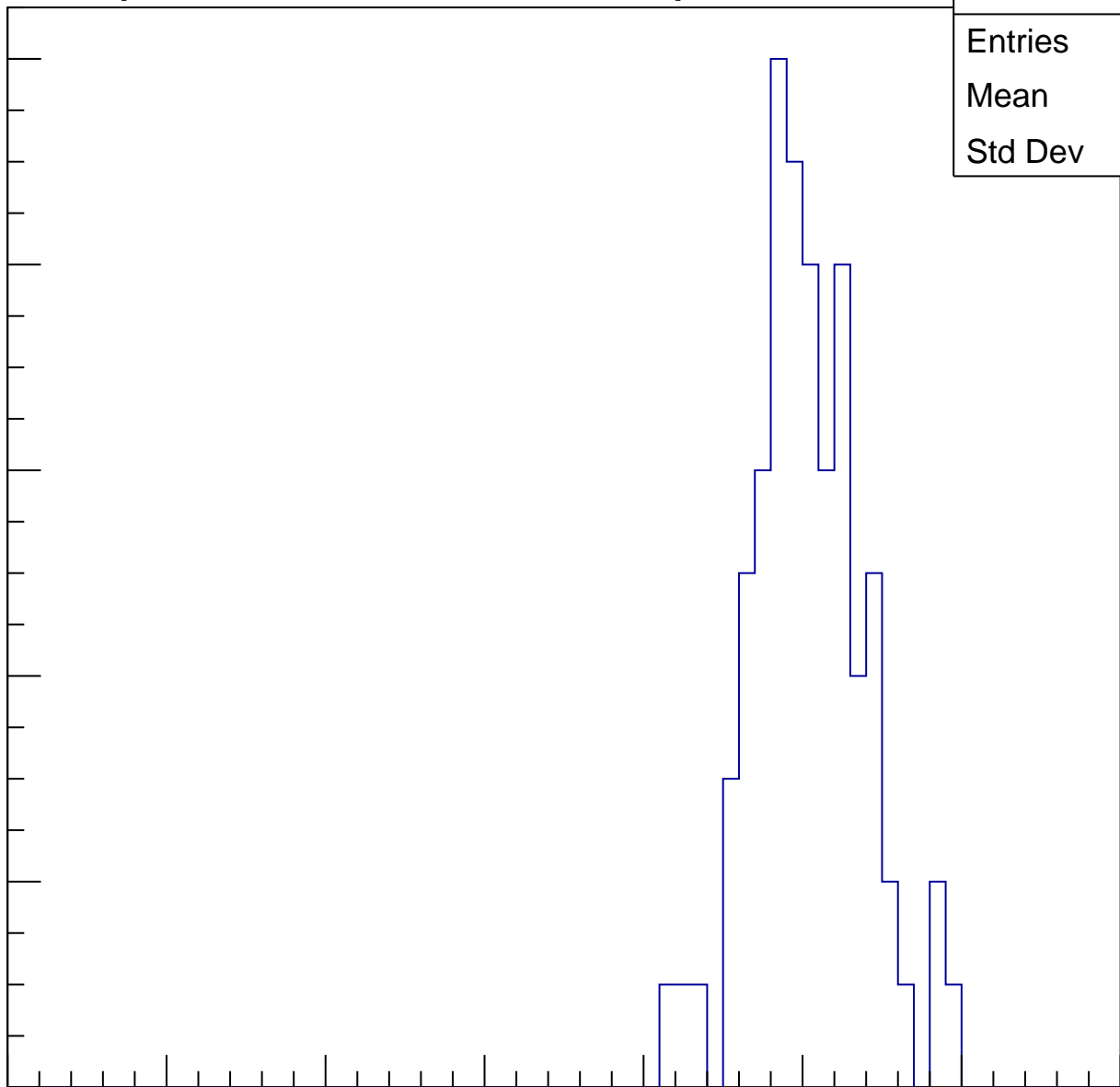
30

40

50

60

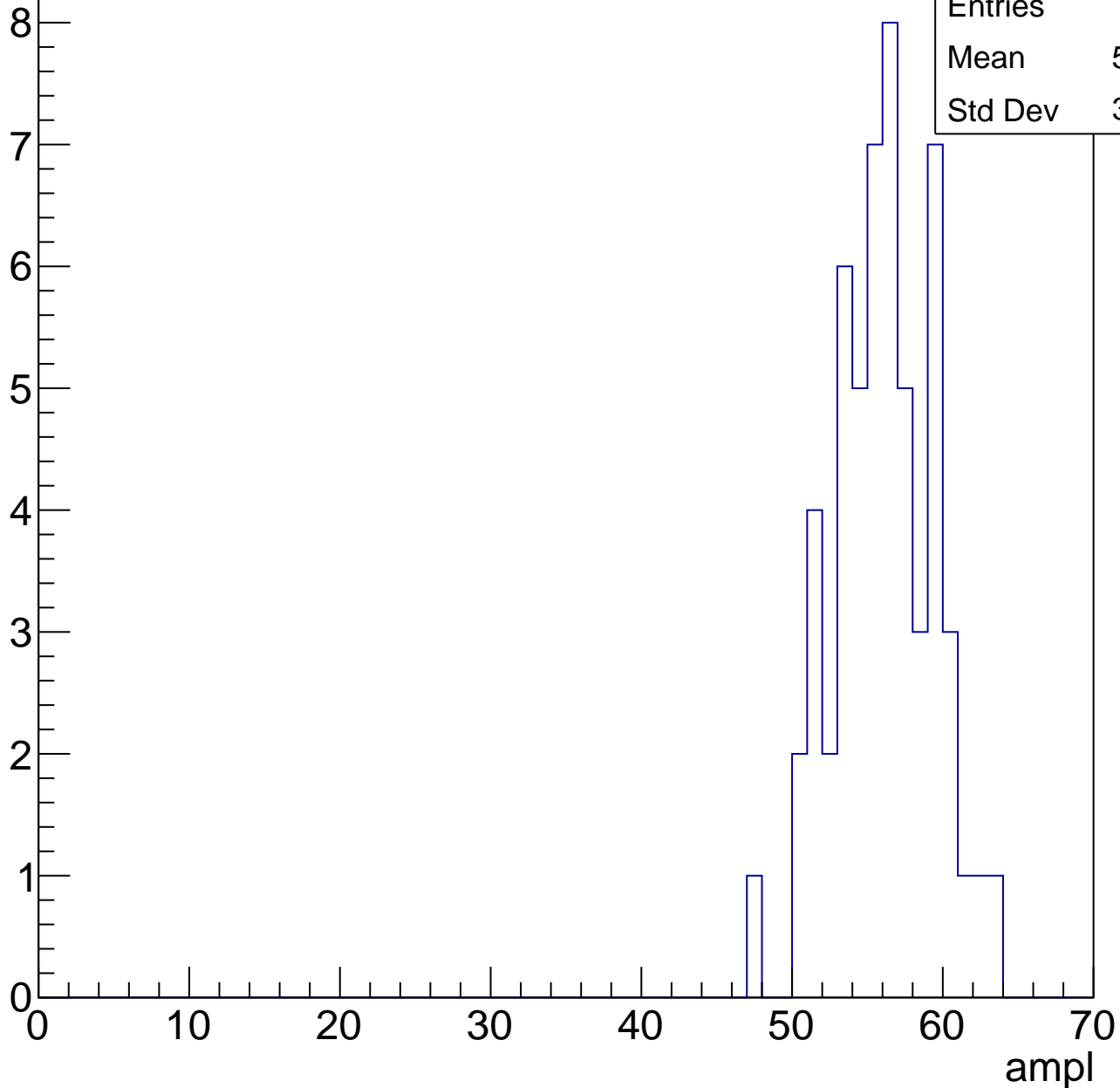
ampl



B0L002S, U26-ch32, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

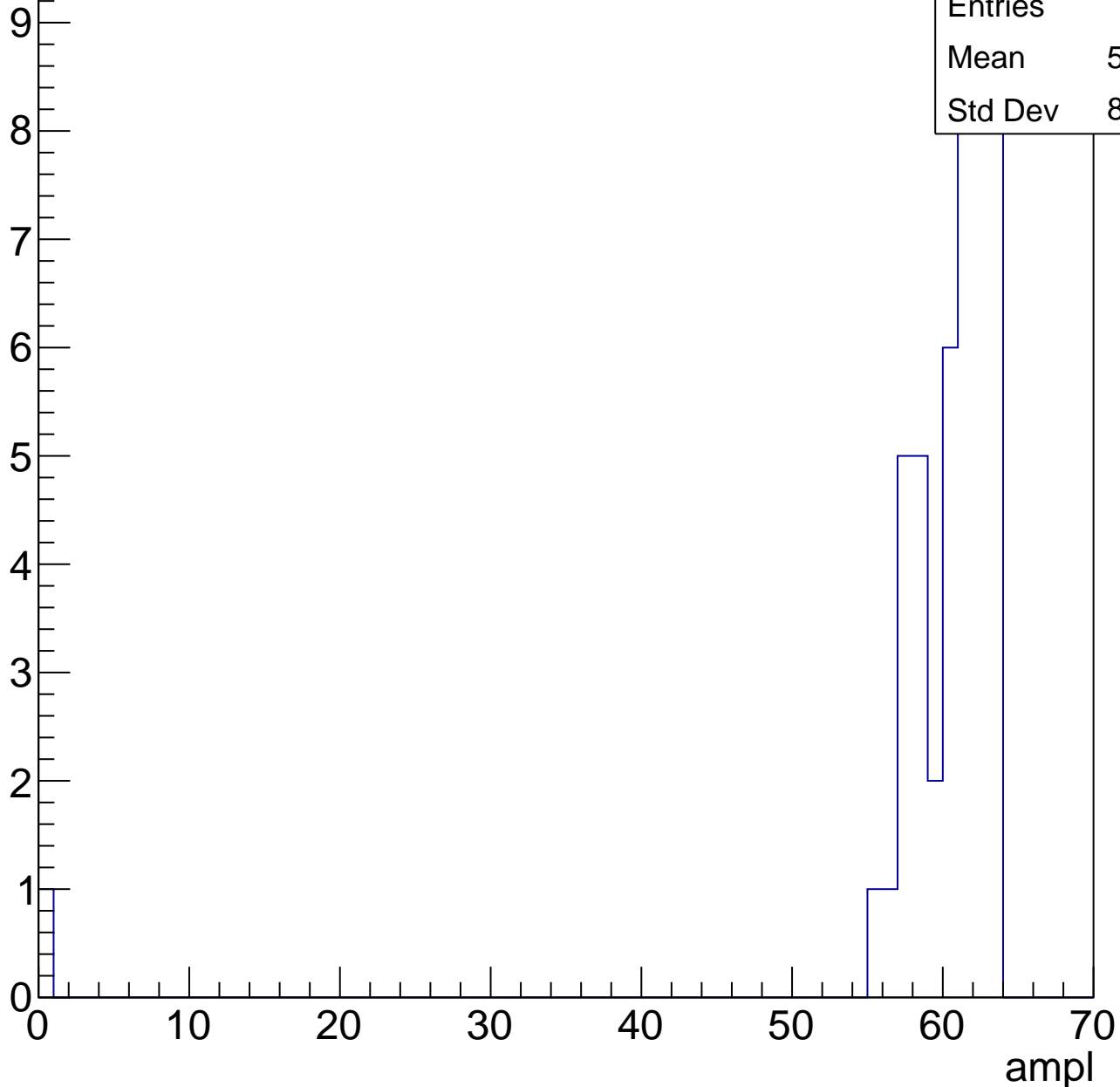


Entries	56
Mean	55.61
Std Dev	3.261

B0L002S, U26-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch33, adc0

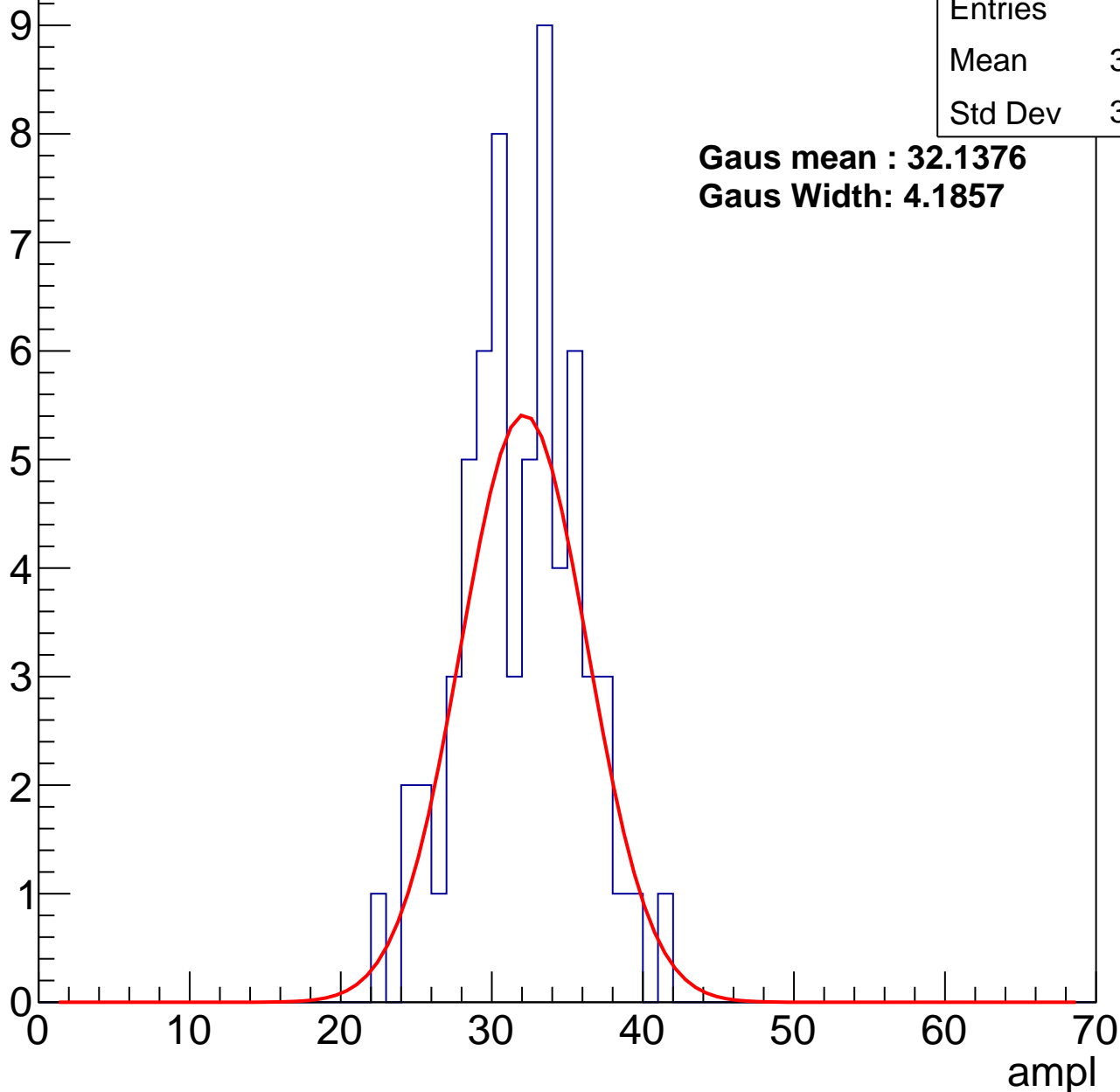
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.47
Std Dev	3.865

Gaus mean : 32.1376

Gaus Width: 4.1857



B0L002S, U26-ch33, adc1

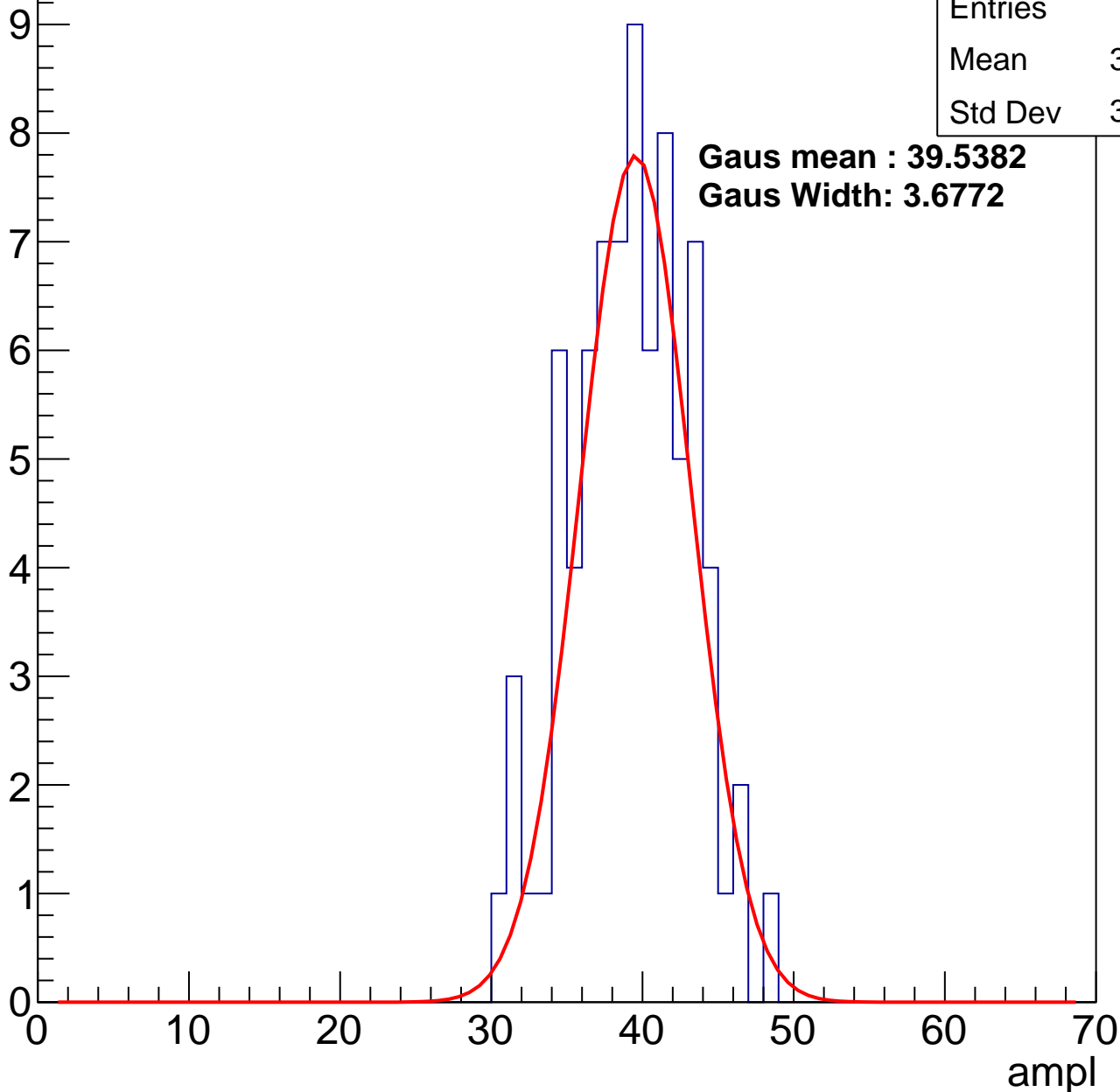
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	38.78
Std Dev	3.844

Gaus mean : 39.5382

Gaus Width: 3.6772



B0L002S, U26-ch33, adc2

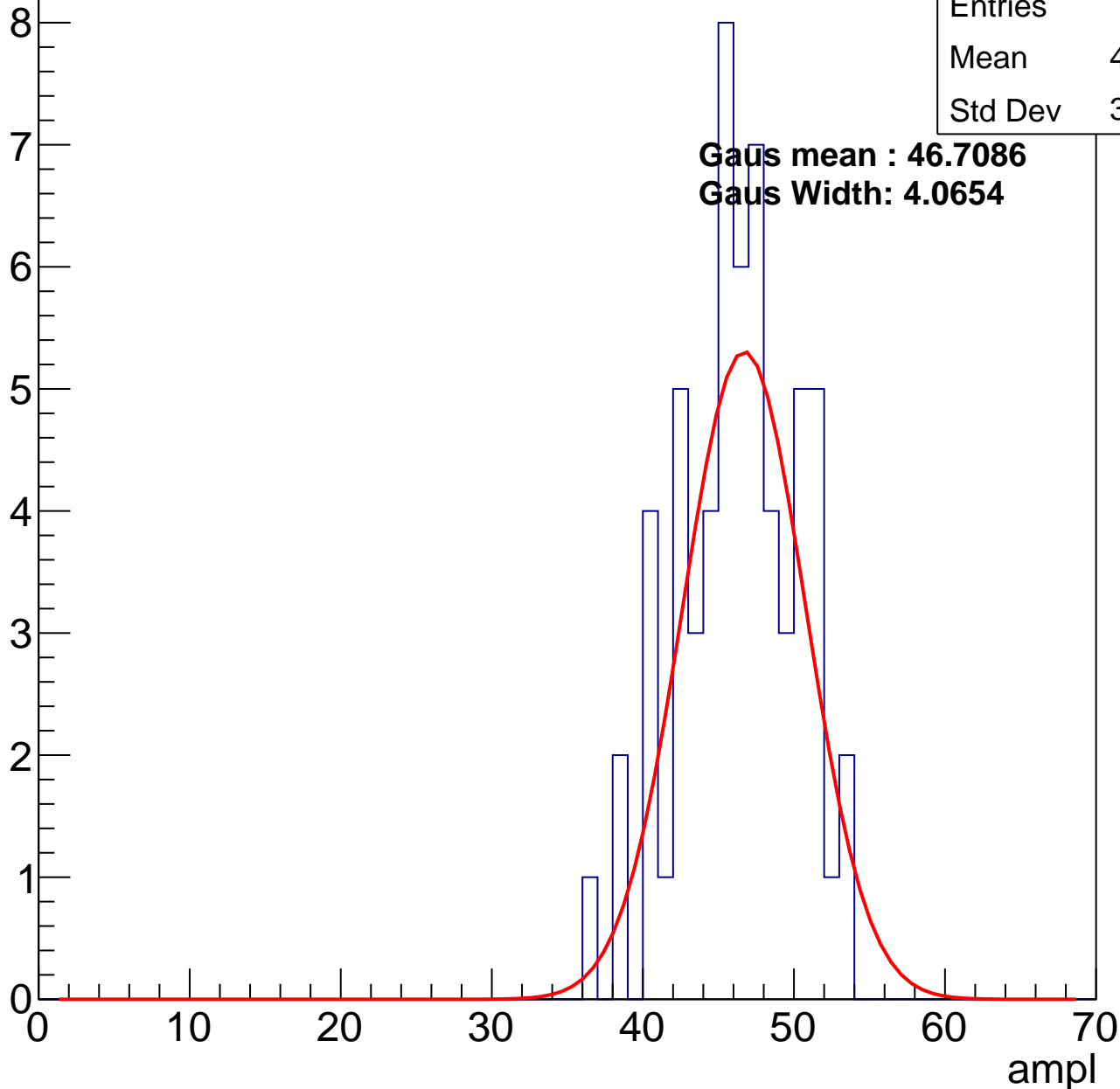
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	45.82
Std Dev	3.873

Gaus mean : 46.7086

Gaus Width: 4.0654

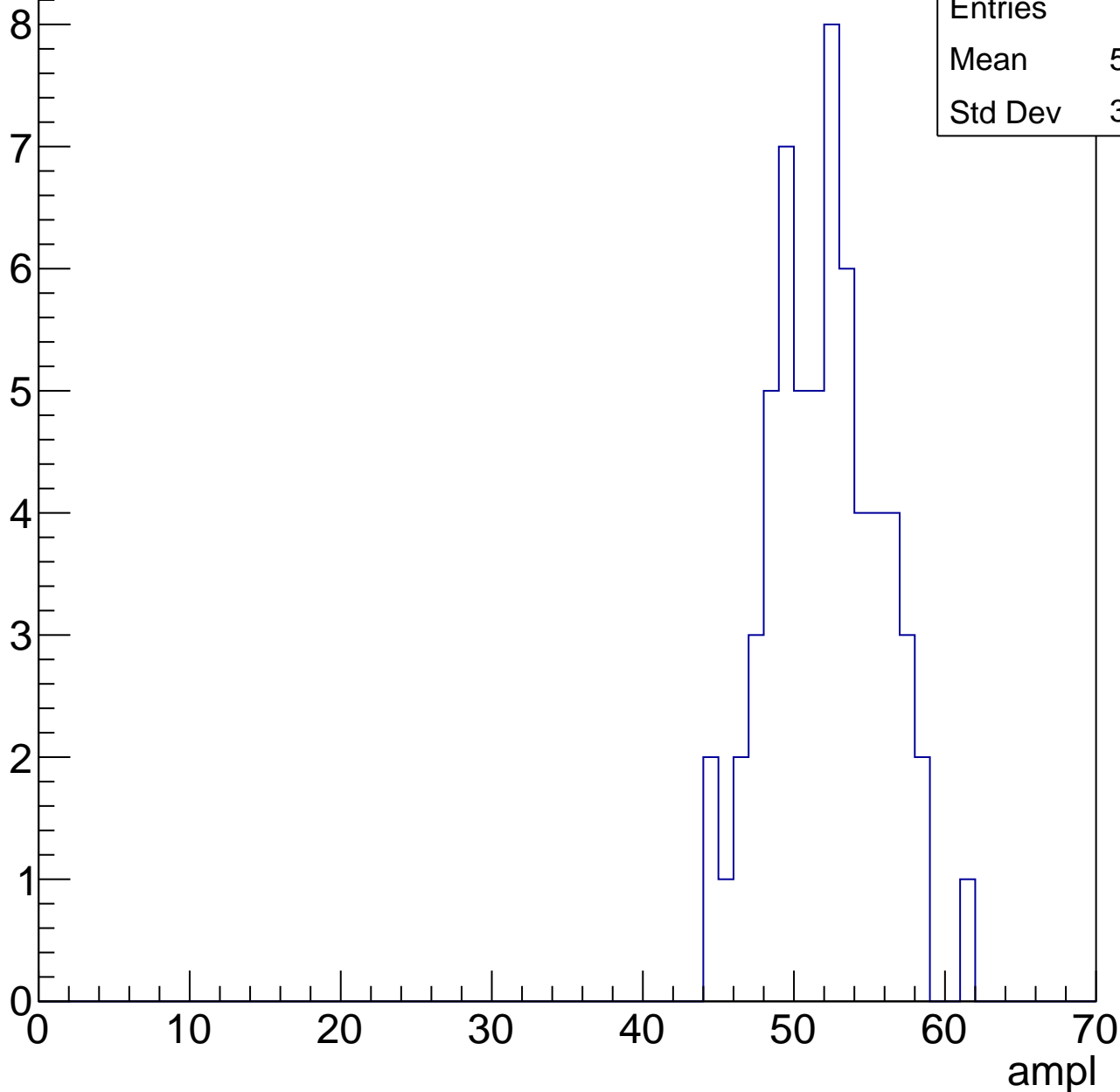


B0L002S, U26-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.55
Std Dev	3.657

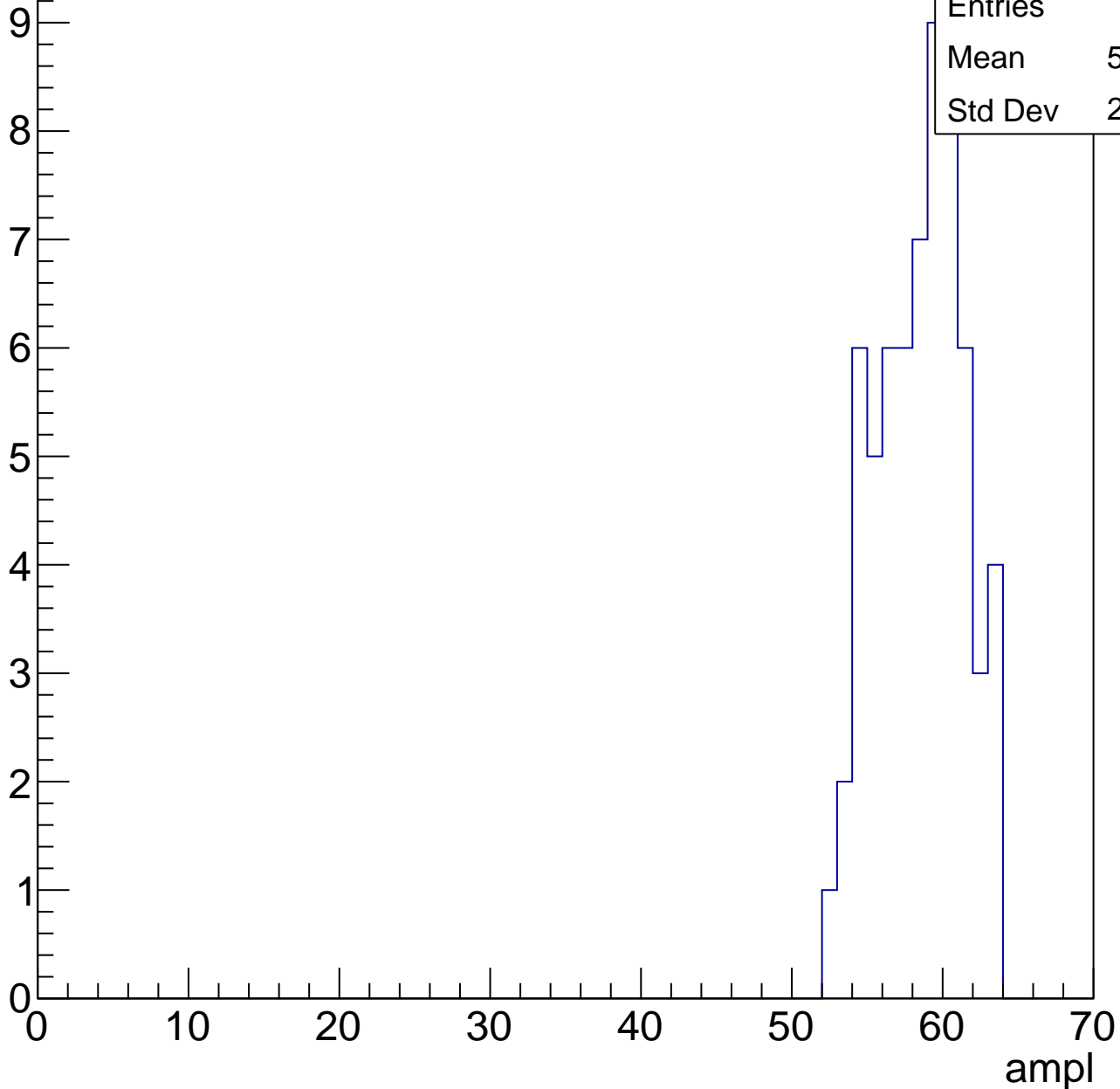


B0L002S, U26-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	58.03
Std Dev	2.817

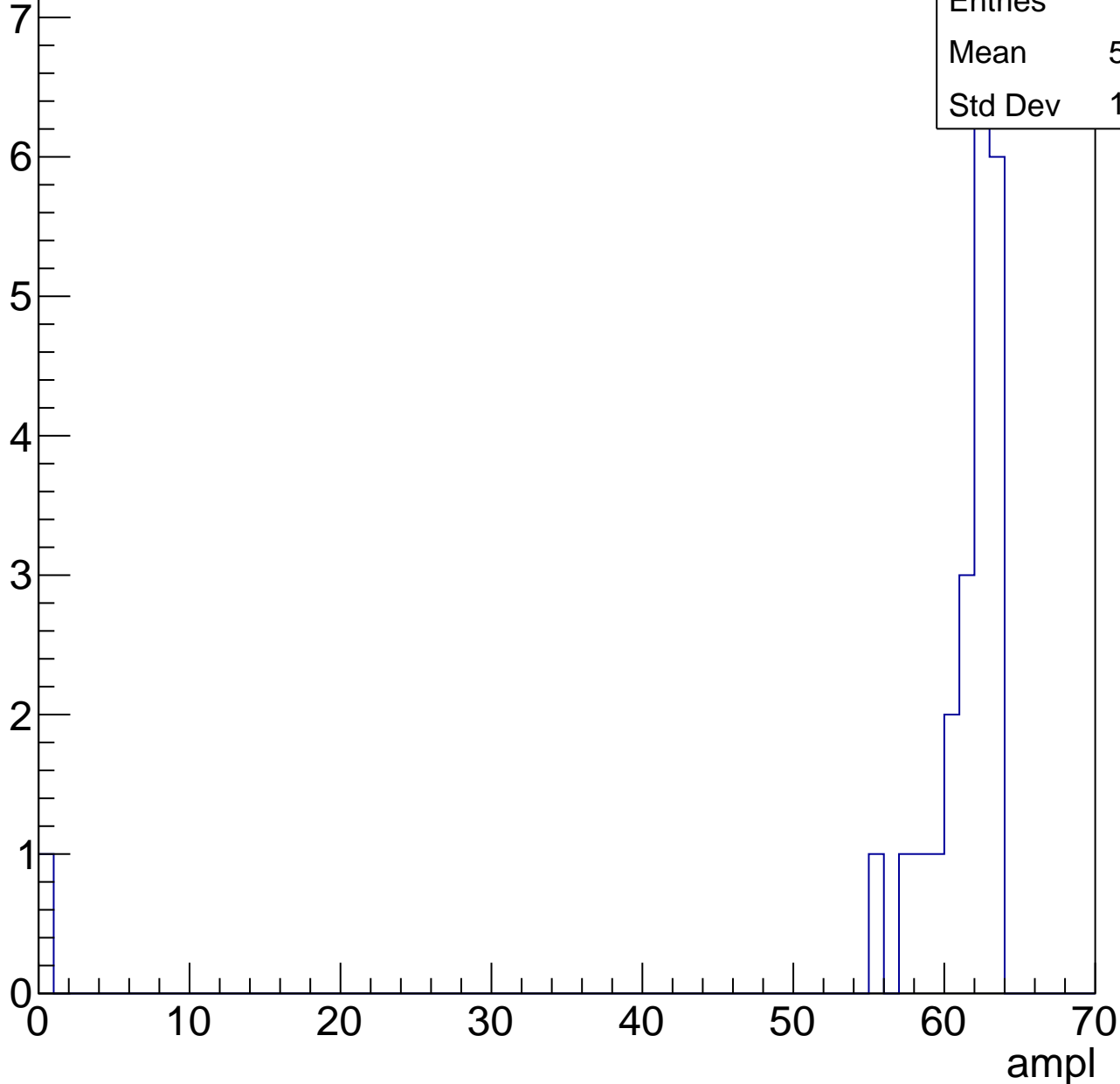


B0L002S, U26-ch33, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.43
Std Dev	12.63



B0L002S, U26-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch34, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	31.63
Std Dev	3.228

Gaus mean : 31.8847

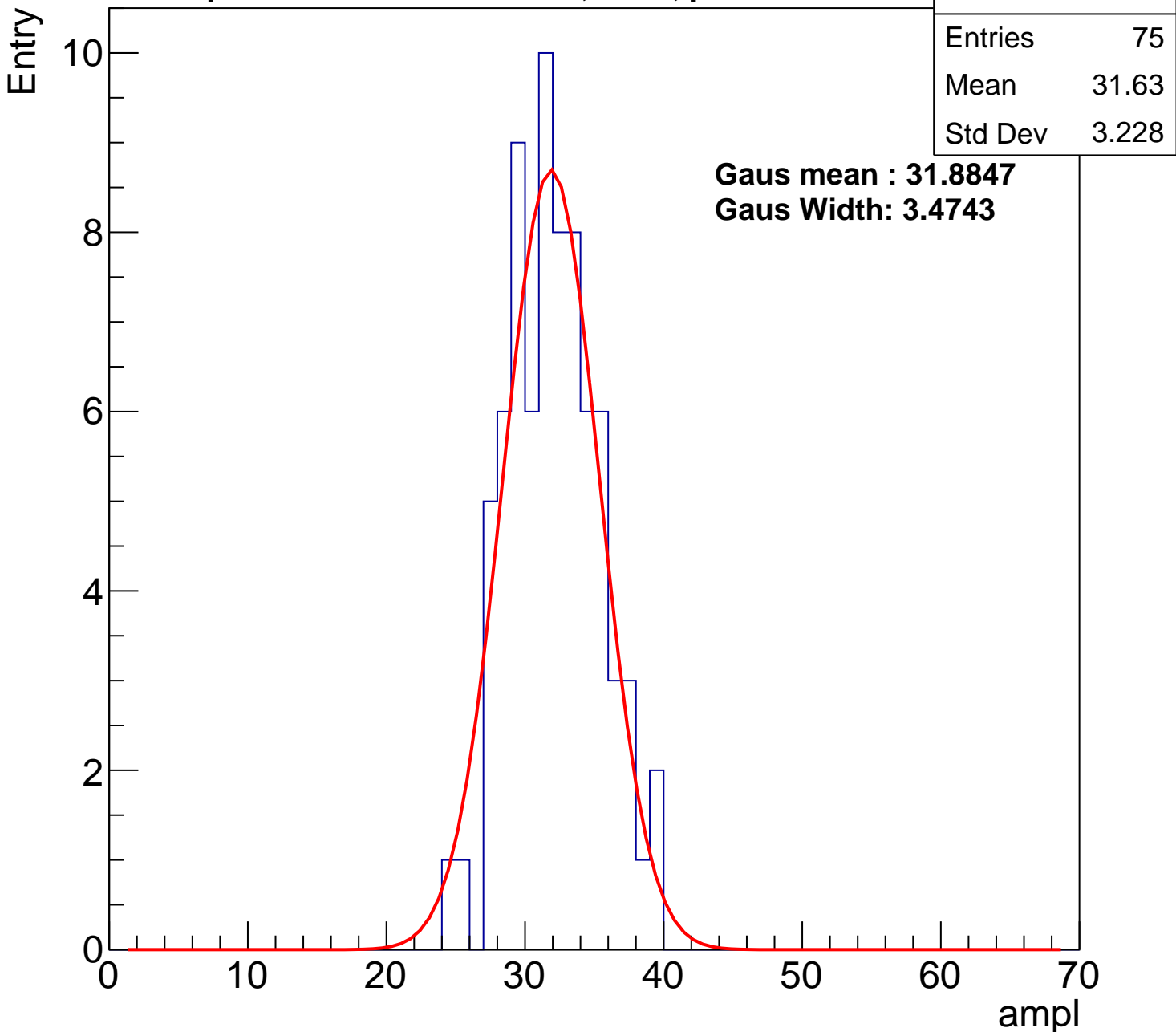
Gaus Width: 3.4743

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch34, adc1

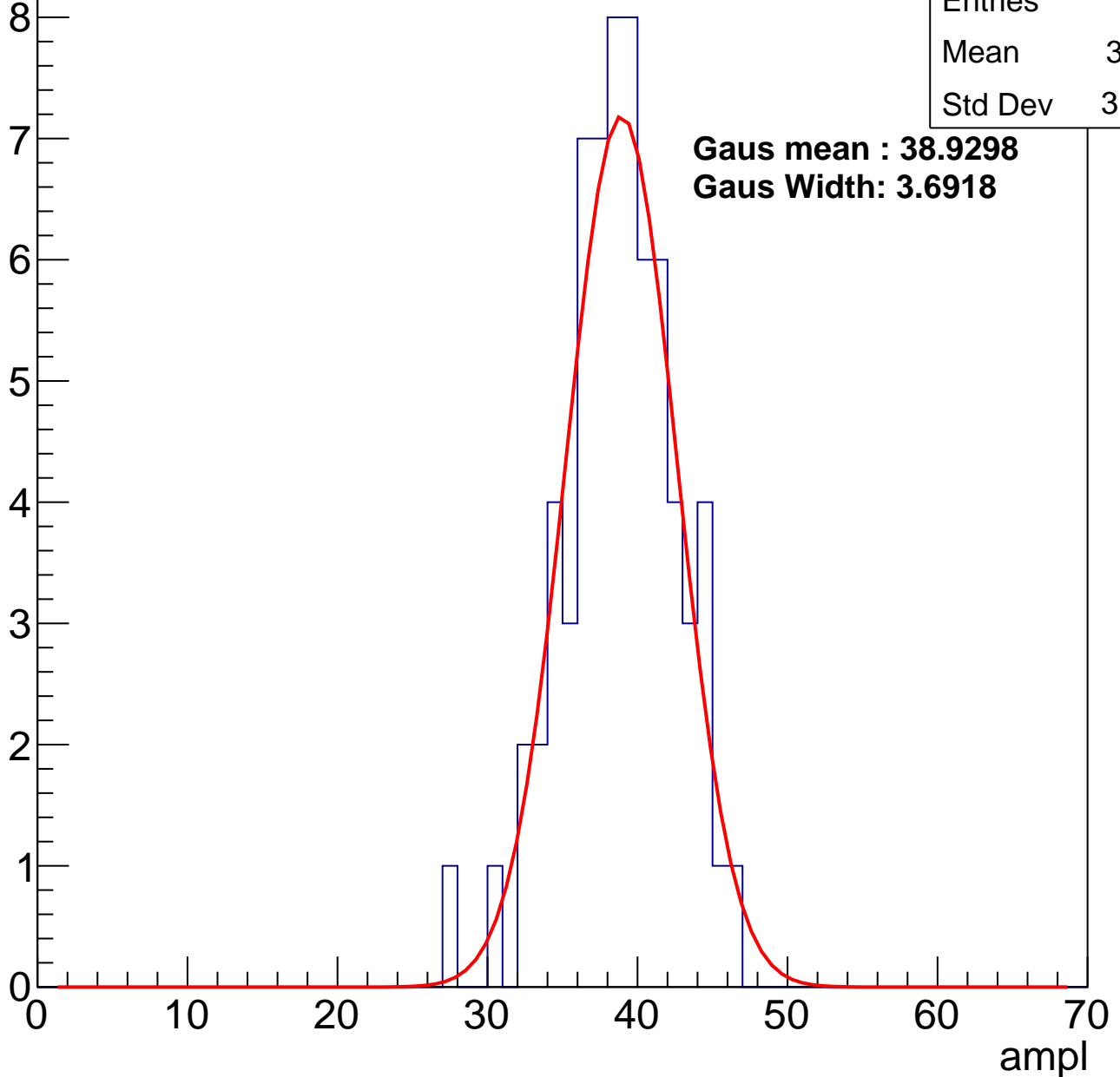
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38.31
Std Dev	3.655

Gaus mean : 38.9298

Gaus Width: 3.6918



B0L002S, U26-ch34, adc2

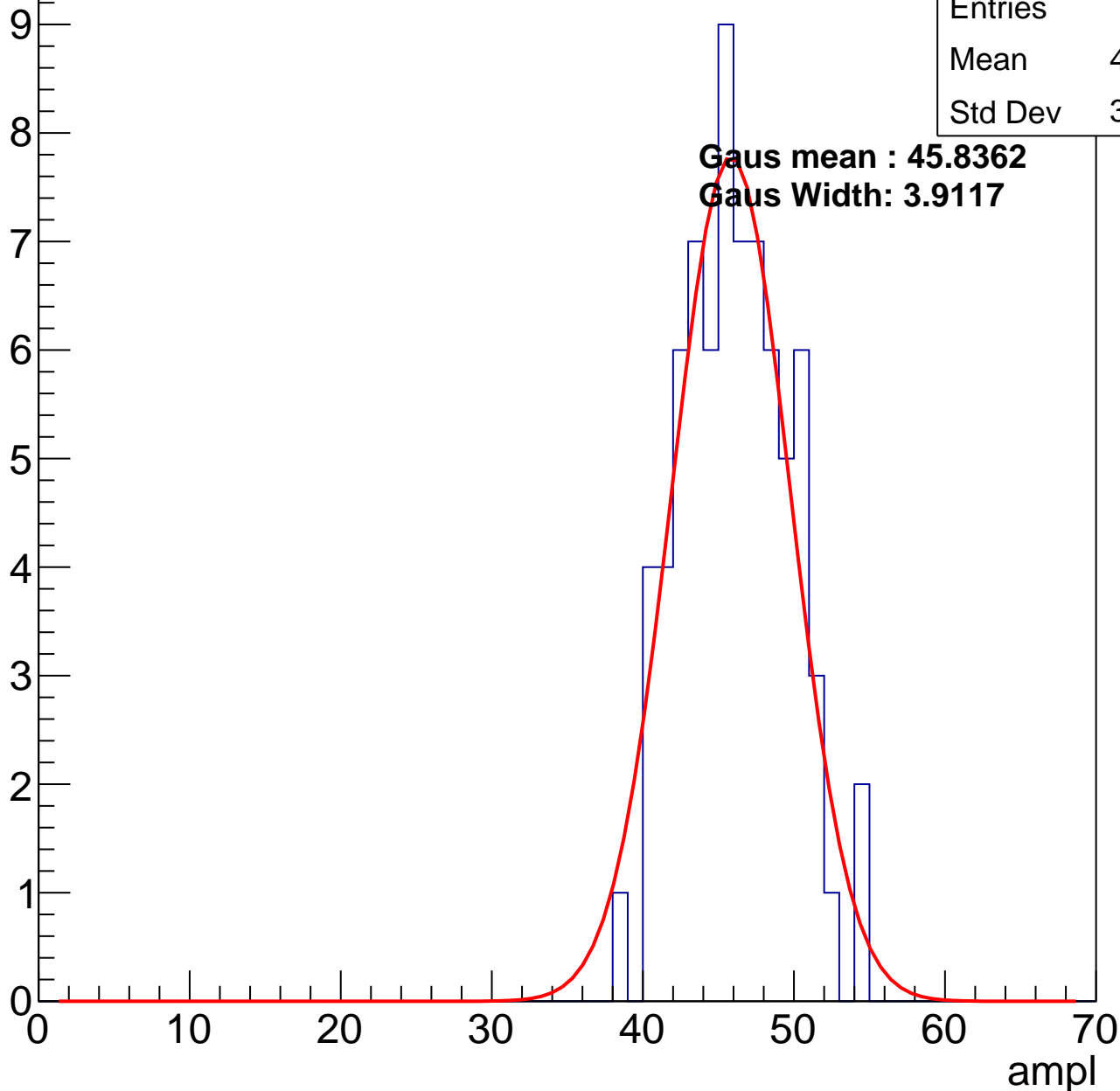
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	45.69
Std Dev	3.495

Gaus mean : 45.8362

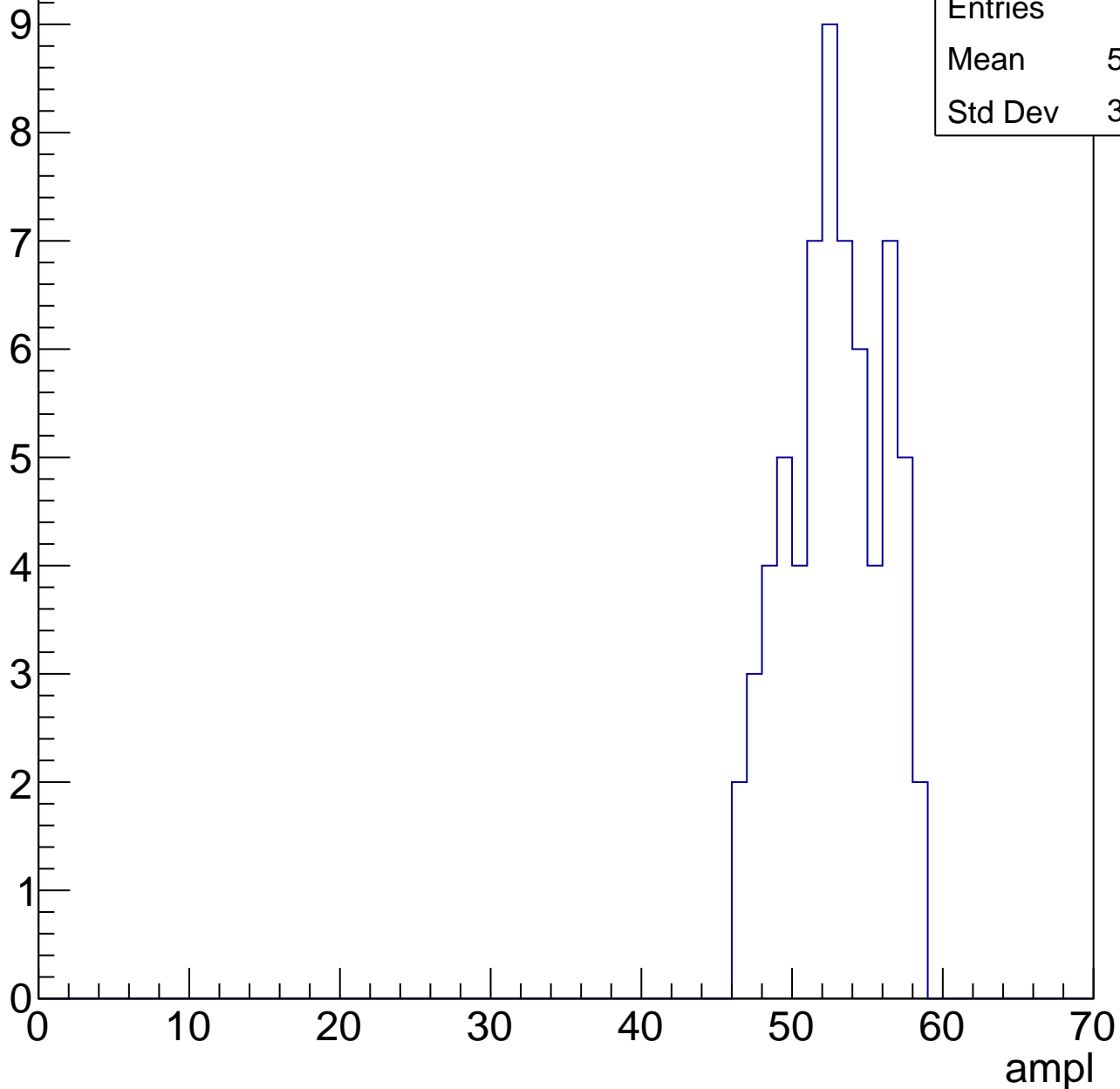
Gaus Width: 3.9117



B0L002S, U26-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	65
Mean	52.35
Std Dev	3.155

B0L002S, U26-ch34, adc4

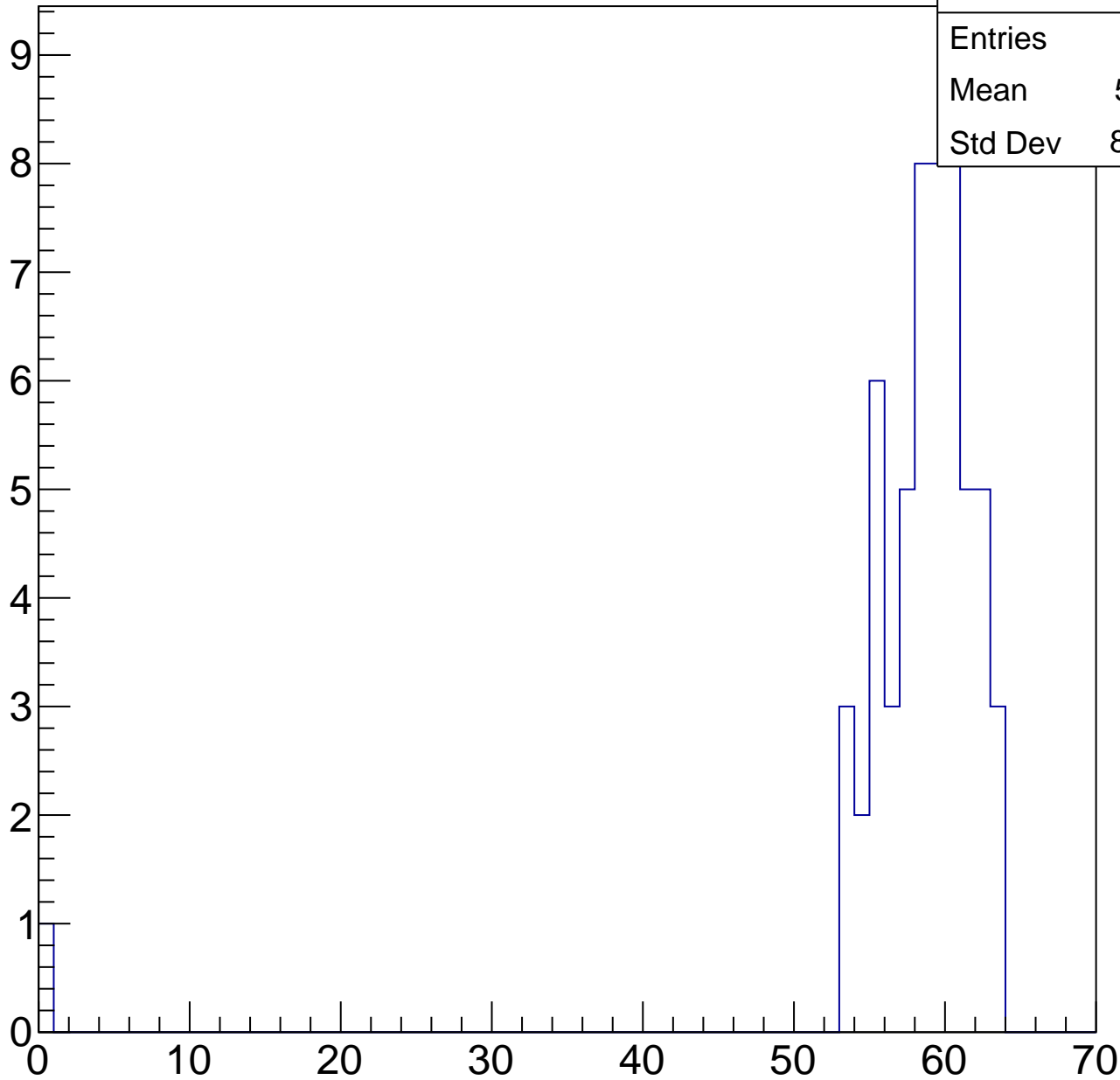
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	57.41
Std Dev	8.058

ampl

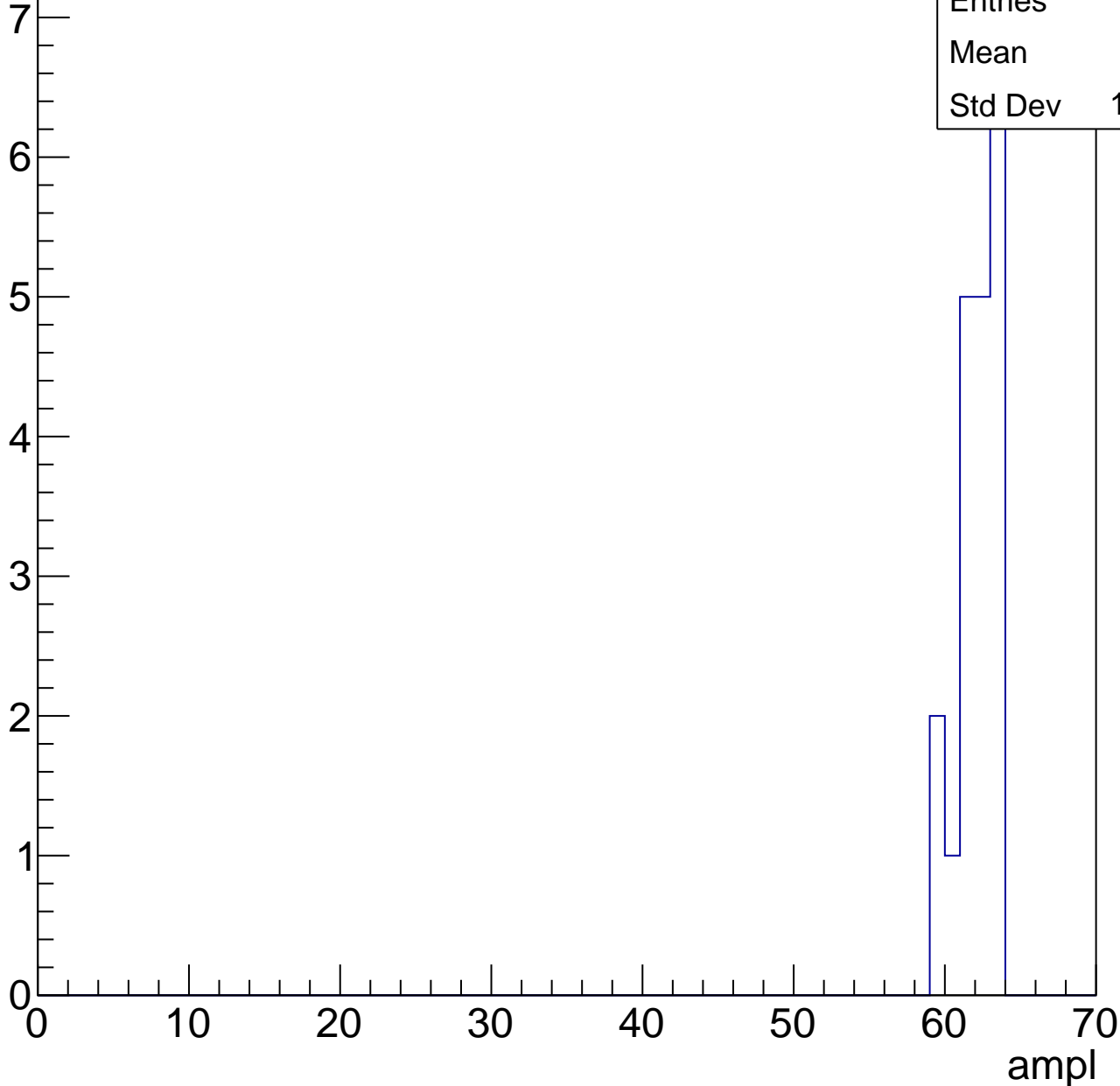


B0L002S, U26-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	61.7
Std Dev	1.269



B0L002S, U26-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch35, adc0

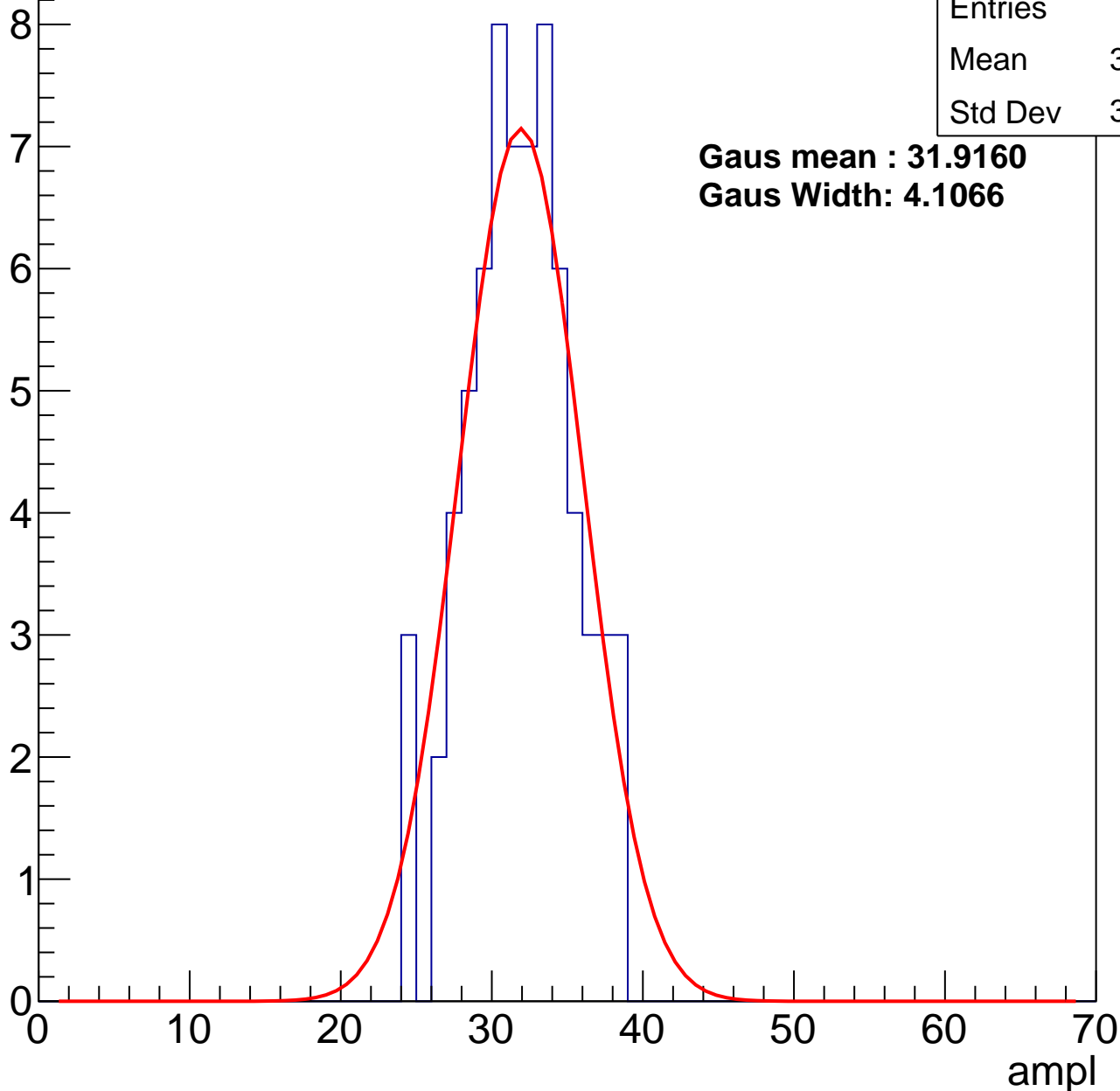
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	31.42
Std Dev	3.432

Gaus mean : 31.9160

Gaus Width: 4.1066



B0L002S, U26-ch35, adc1

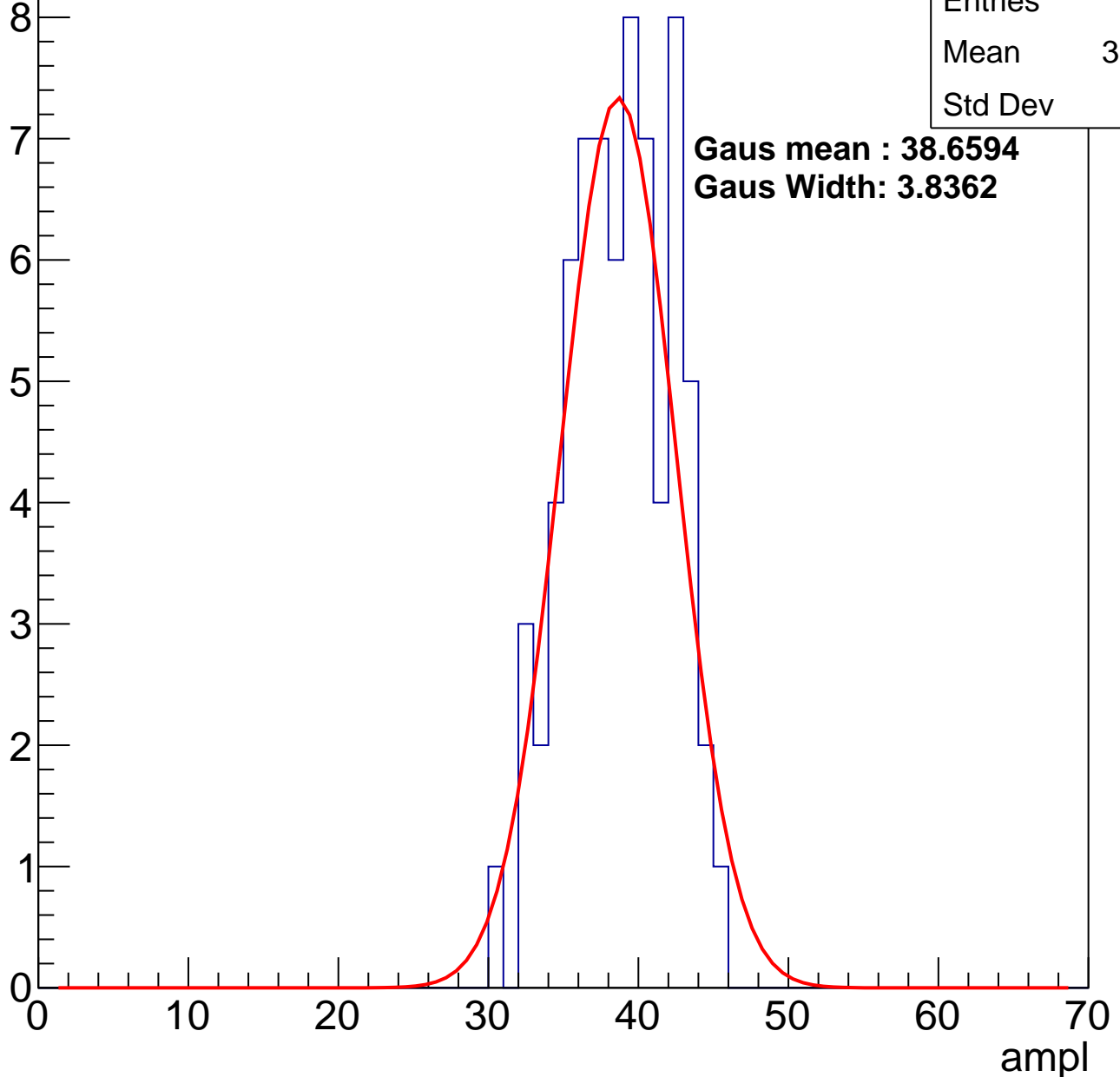
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38.27
Std Dev	3.39

Gaus mean : 38.6594

Gaus Width: 3.8362



B0L002S, U26-ch35, adc2

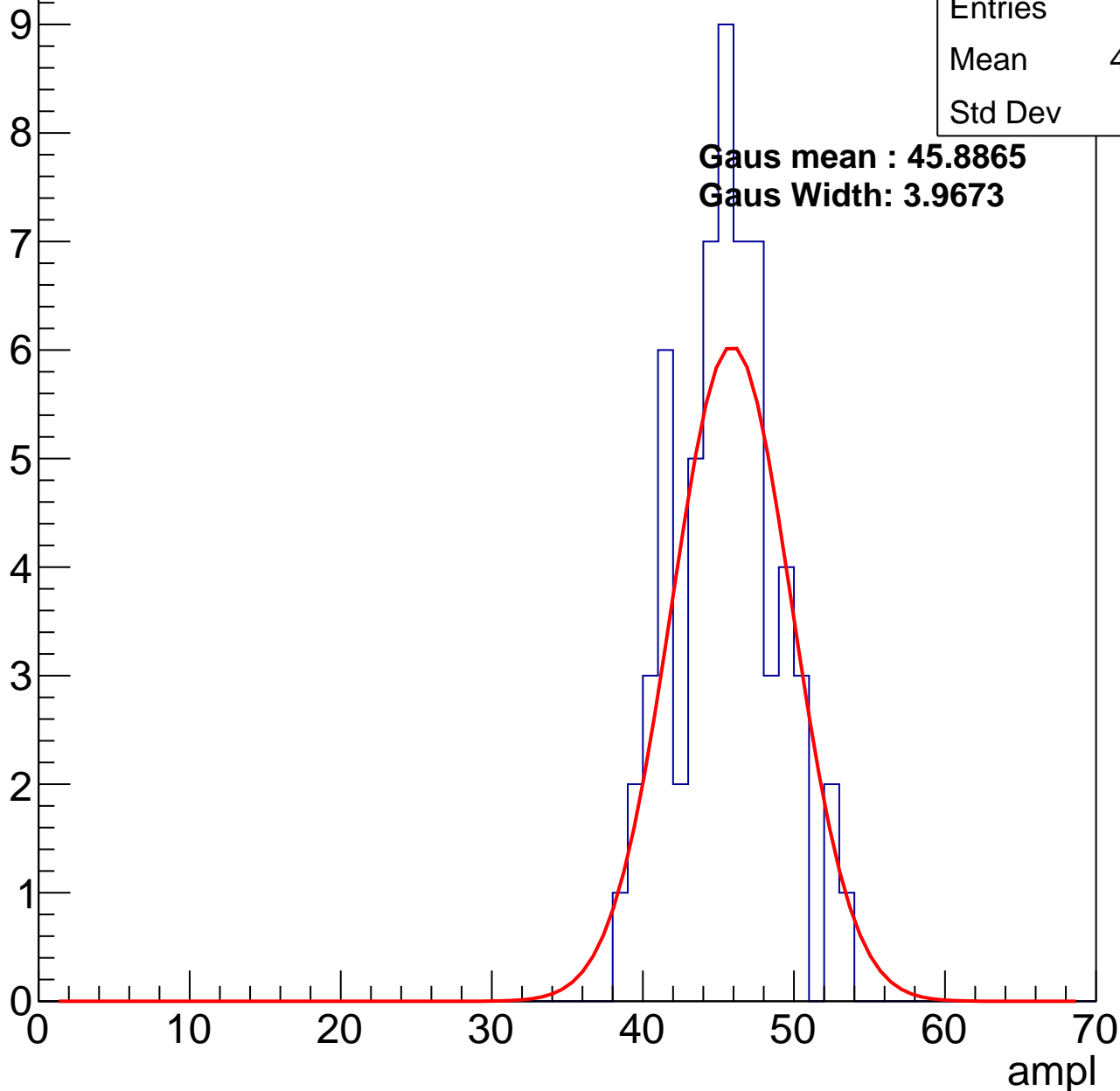
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.03
Std Dev	3.36

Gaus mean : 45.8865

Gaus Width: 3.9673



B0L002S, U26-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	52.25
Std Dev	3.467

Entry

10

8

6

4

2

0

0

10

20

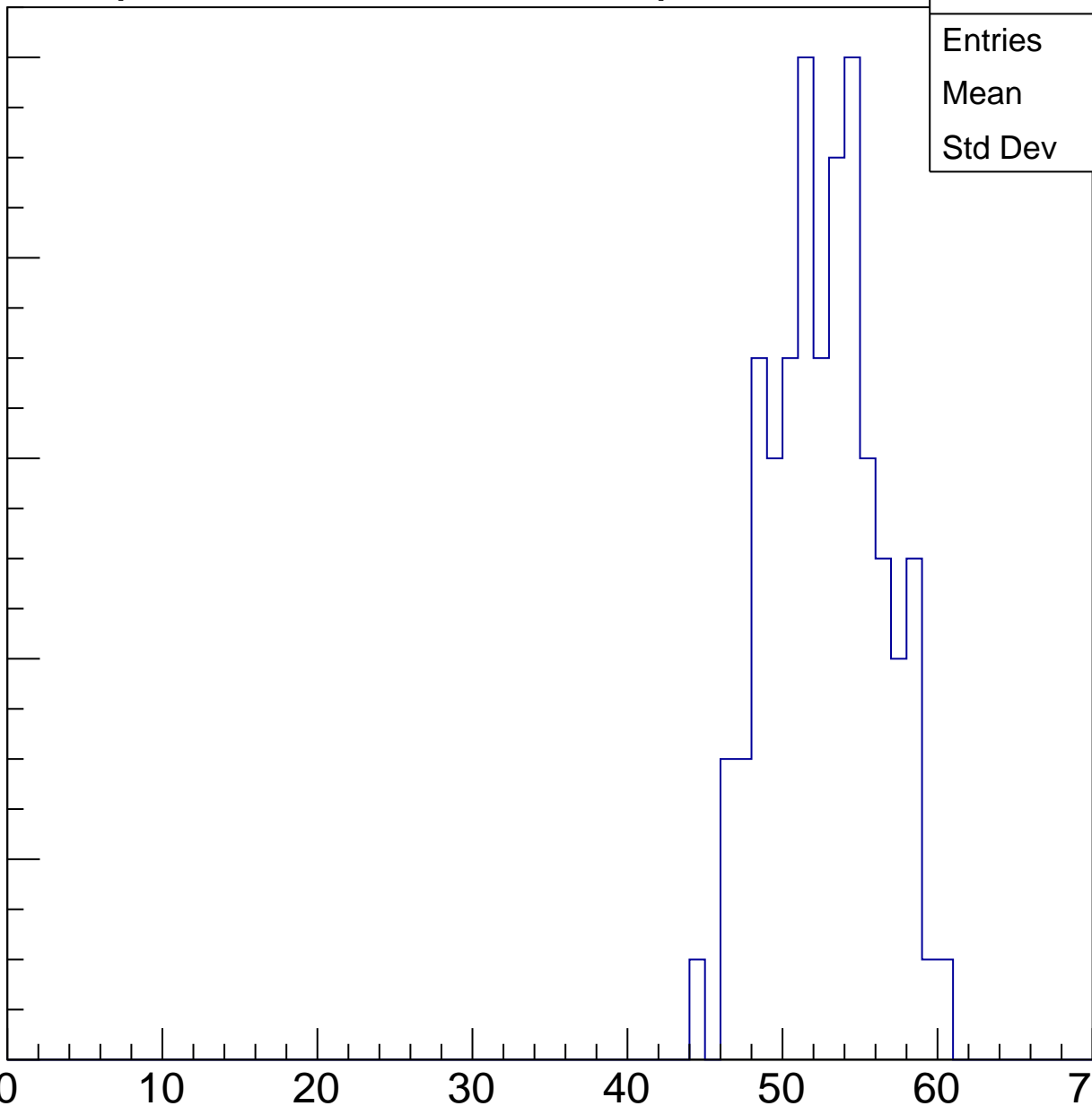
30

40

50

60

ampl

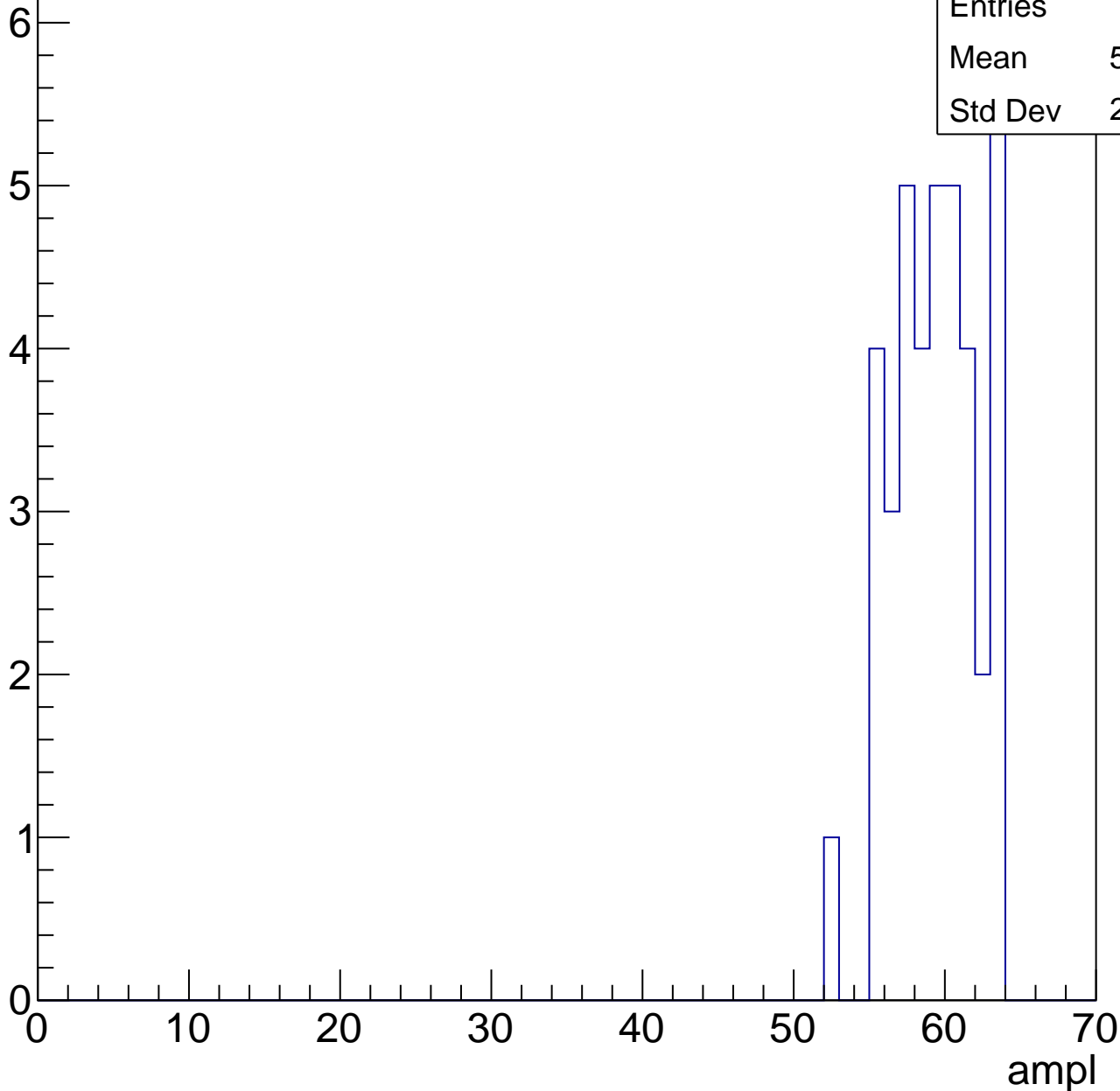


B0L002S, U26-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	58.92
Std Dev	2.768

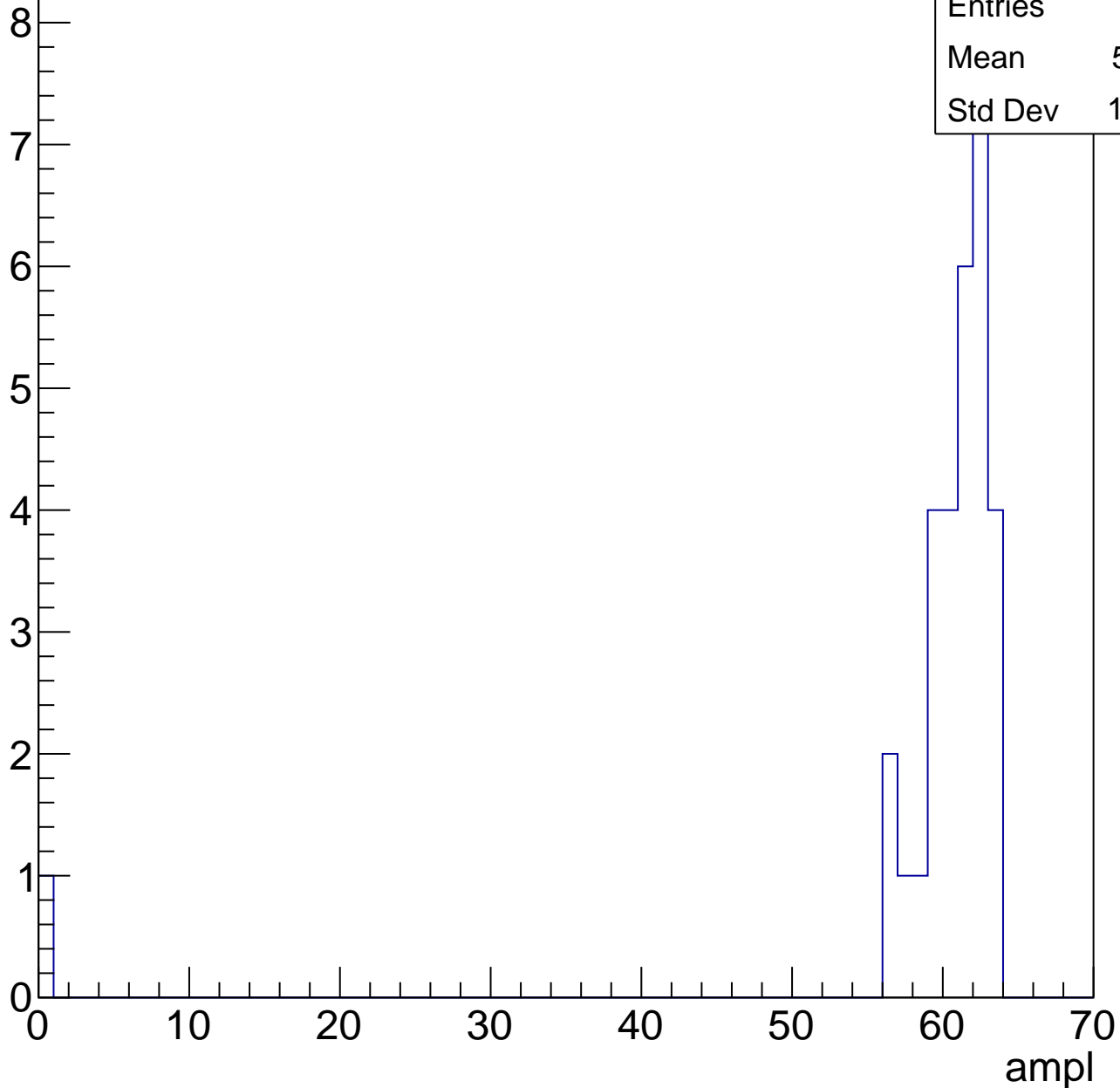


B0L002S, U26-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.61
Std Dev	10.87



B0L002S, U26-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch36, adc0

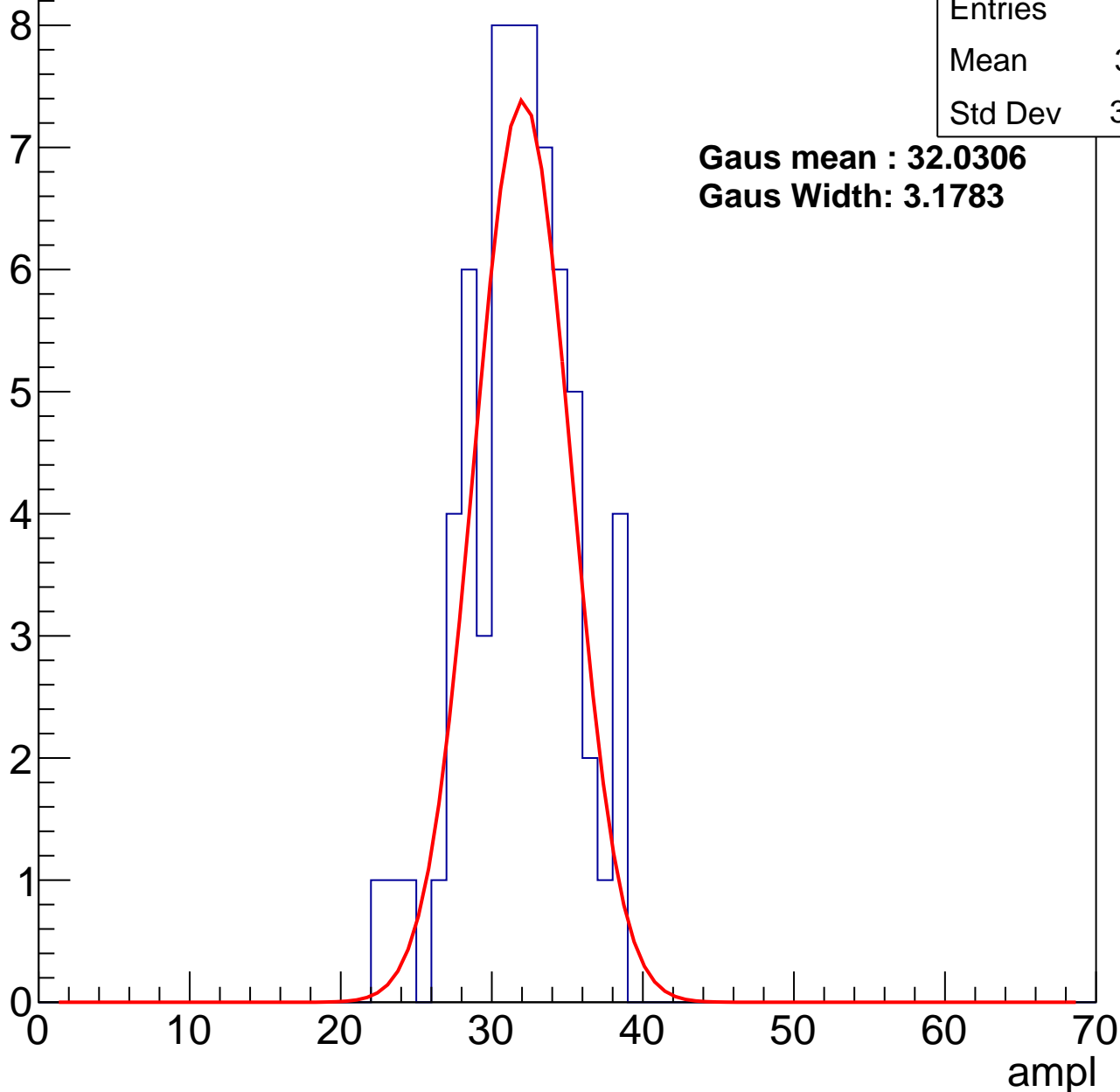
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	31.41
Std Dev	3.477

Gaus mean : 32.0306

Gaus Width: 3.1783



B0L002S, U26-ch36, adc1

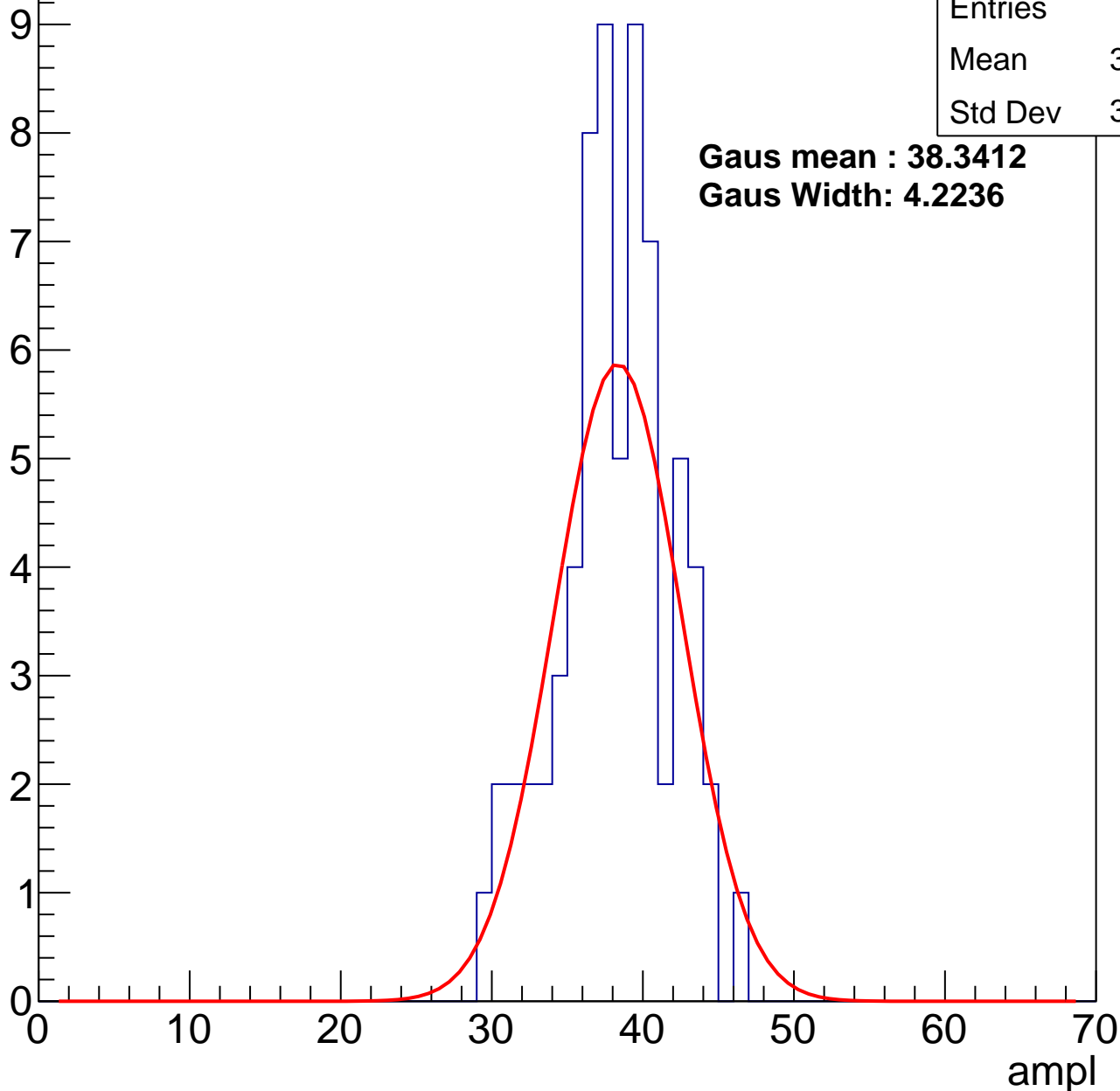
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.69
Std Dev	3.675

Gaus mean : 38.3412

Gaus Width: 4.2236



B0L002S, U26-ch36, adc2

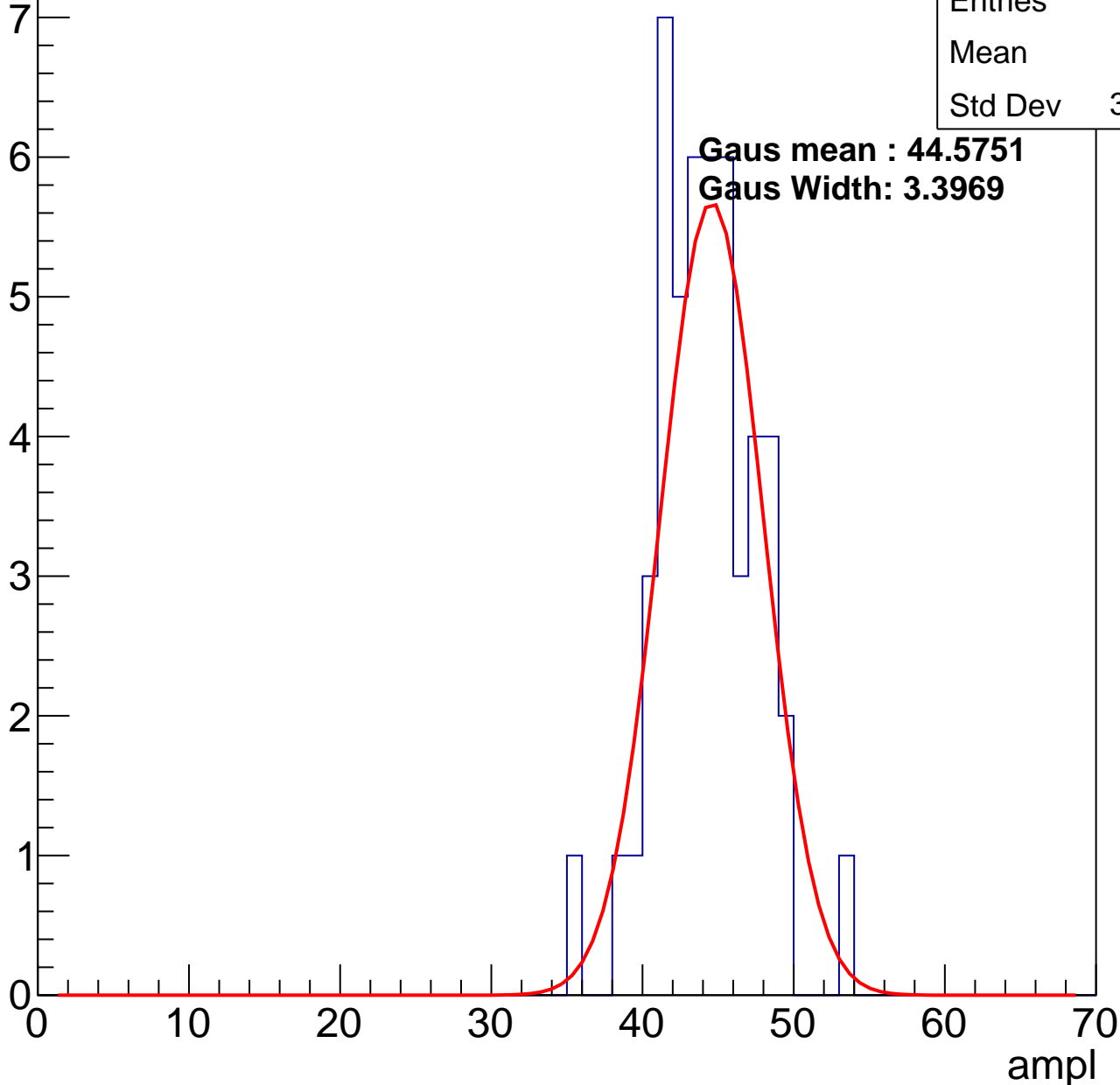
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	43.8
Std Dev	3.243

Gaus mean : 44.5751

Gaus Width: 3.3969

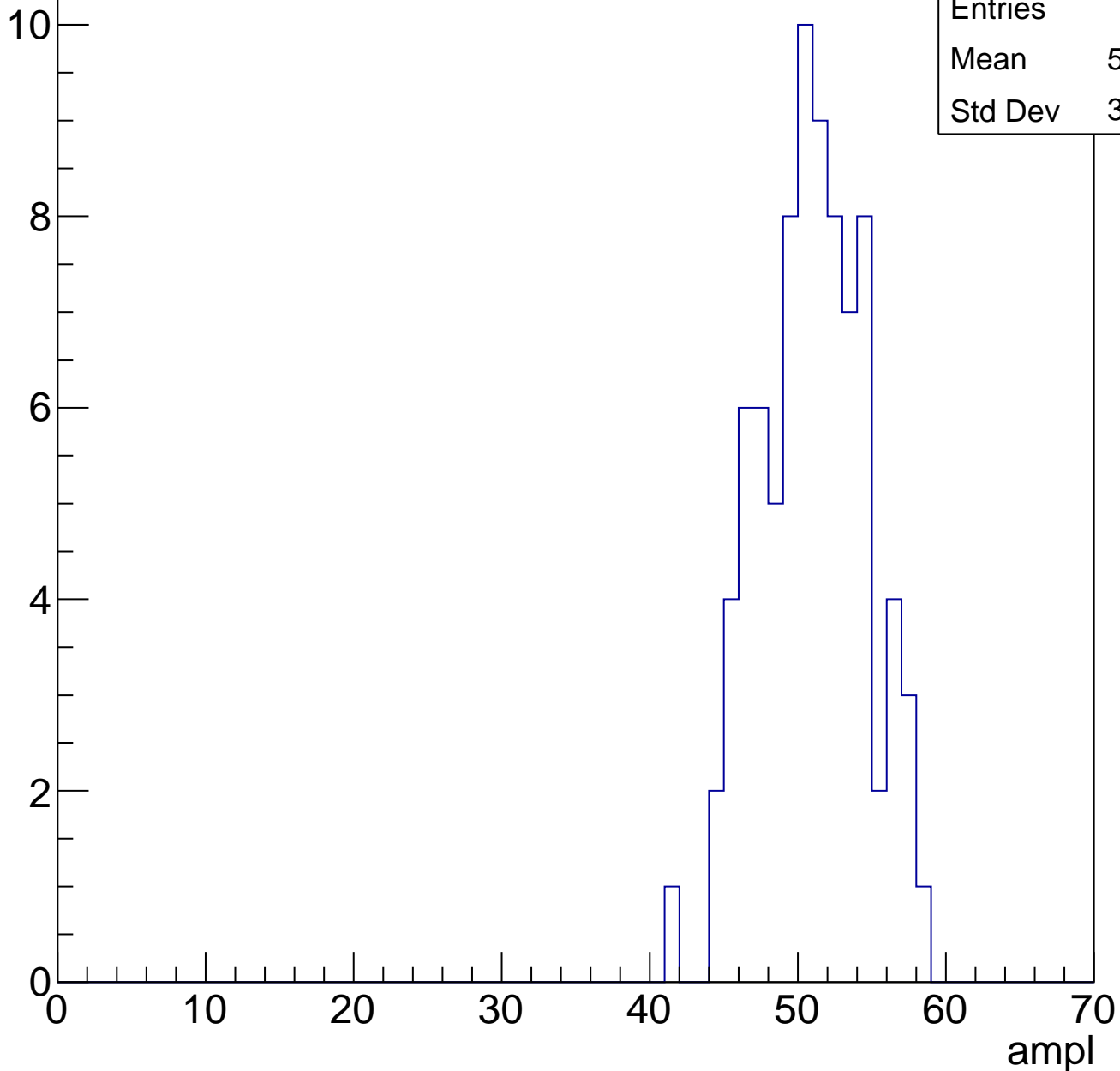


B0L002S, U26-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	50.48
Std Dev	3.534

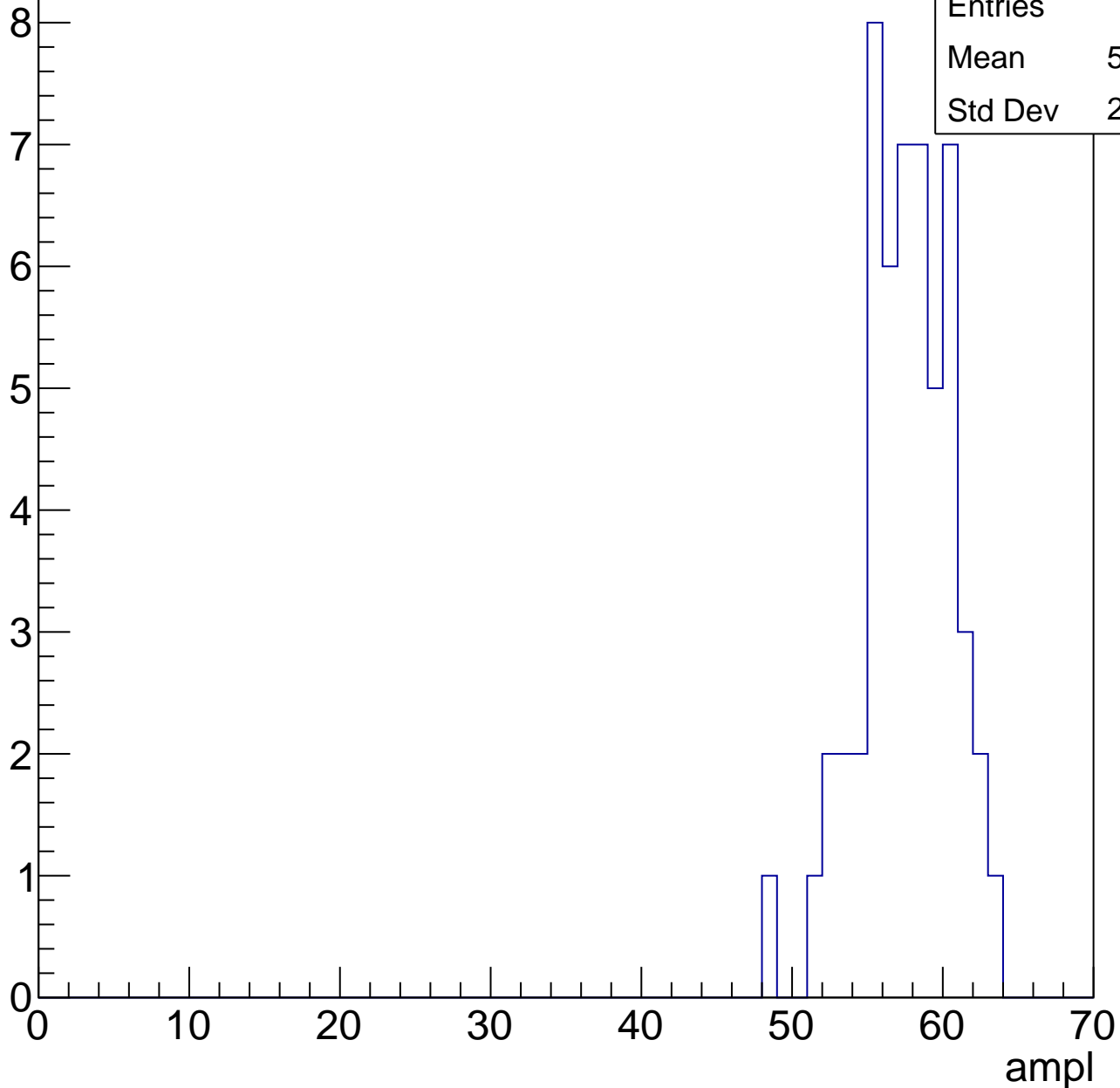
Entry



B0L002S, U26-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



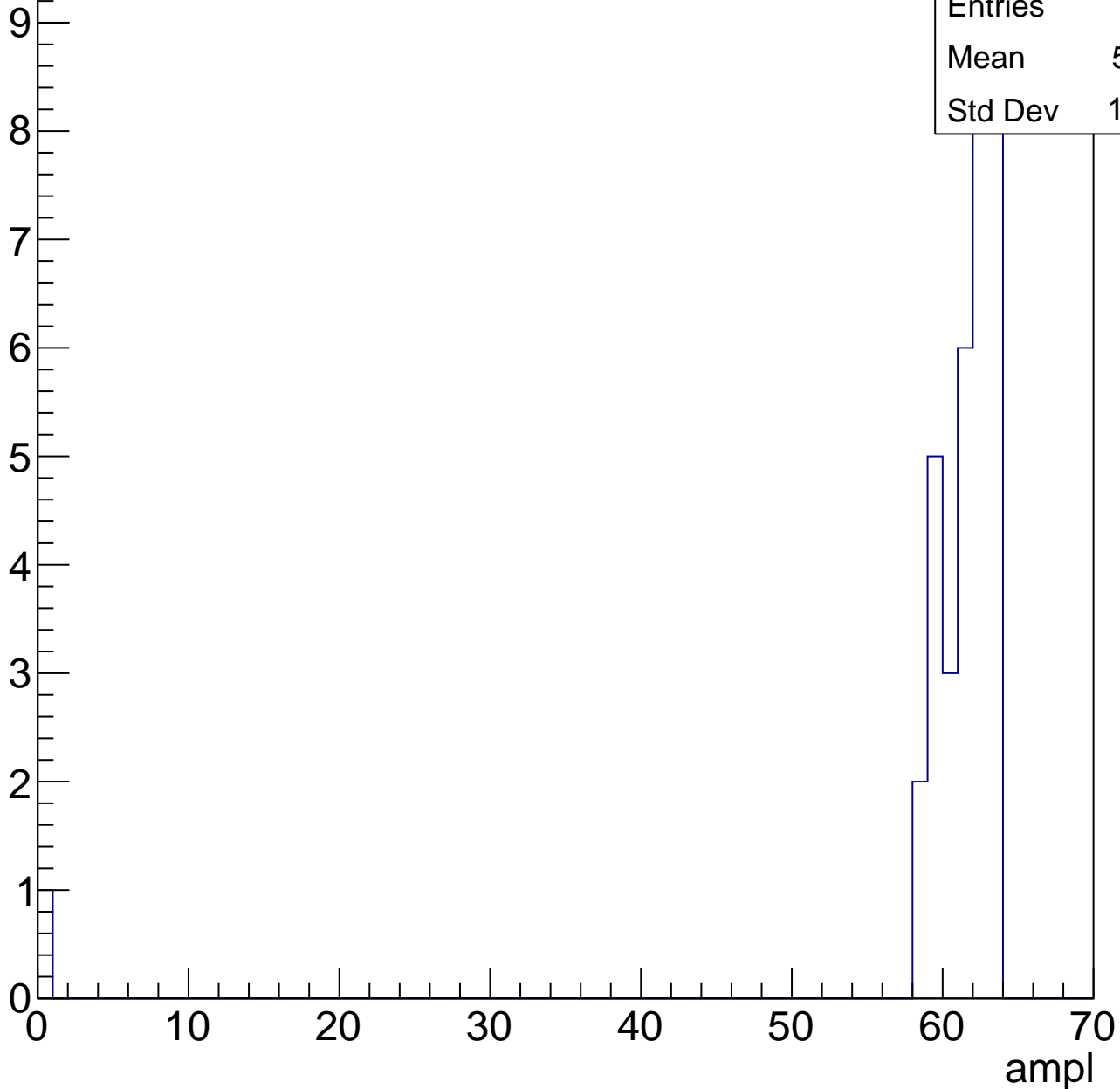
Entries	54
Mean	57.09
Std Dev	2.977

B0L002S, U26-ch36, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.41
Std Dev	10.46



B0L002S, U26-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	61
Std Dev	0

B0L002S, U26-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch37, adc0

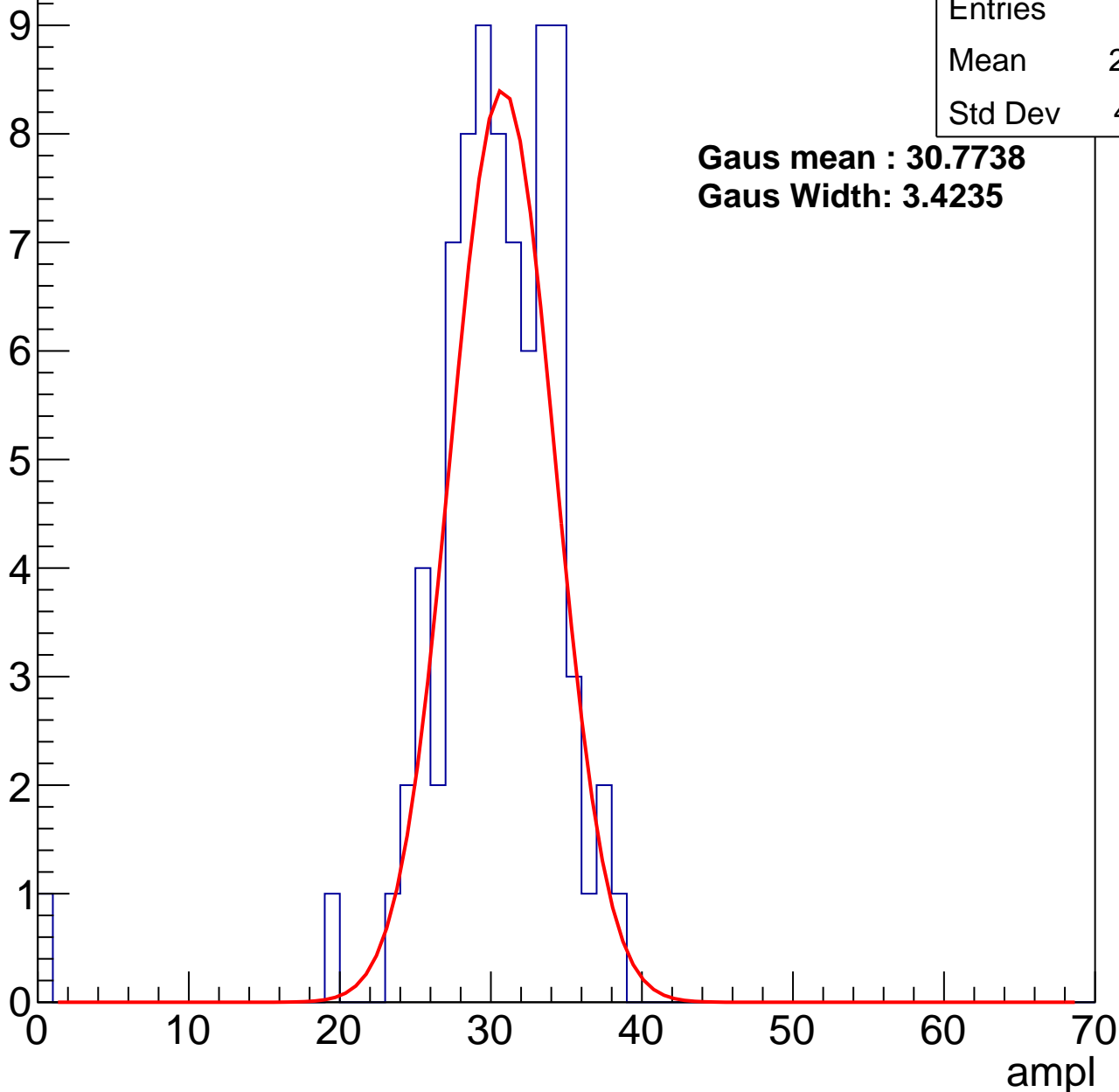
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	29.89
Std Dev	4.851

Gaus mean : 30.7738

Gaus Width: 3.4235



B0L002S, U26-ch37, adc1

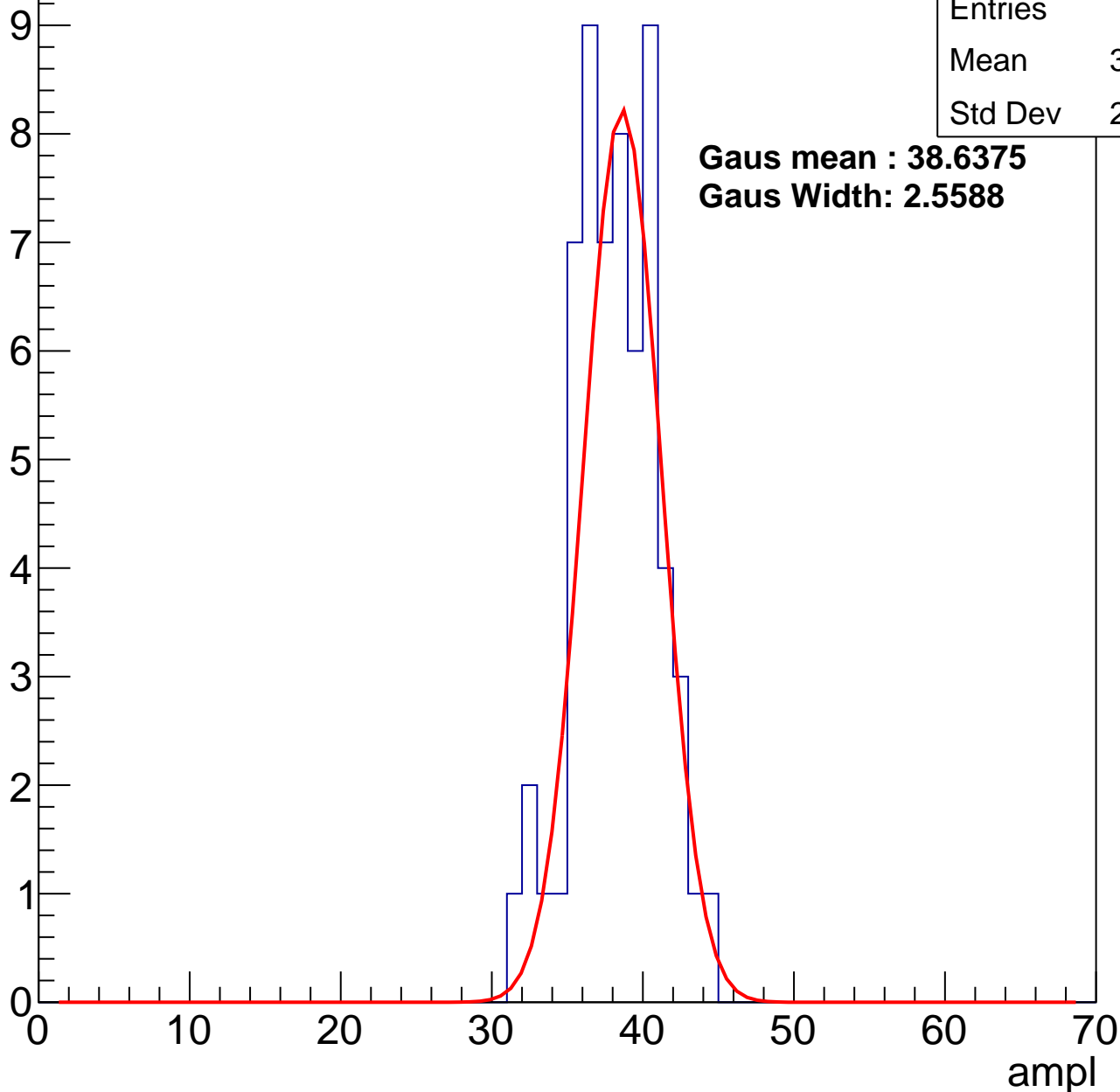
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.75
Std Dev	2.742

Gaus mean : 38.6375

Gaus Width: 2.5588



B0L002S, U26-ch37, adc2

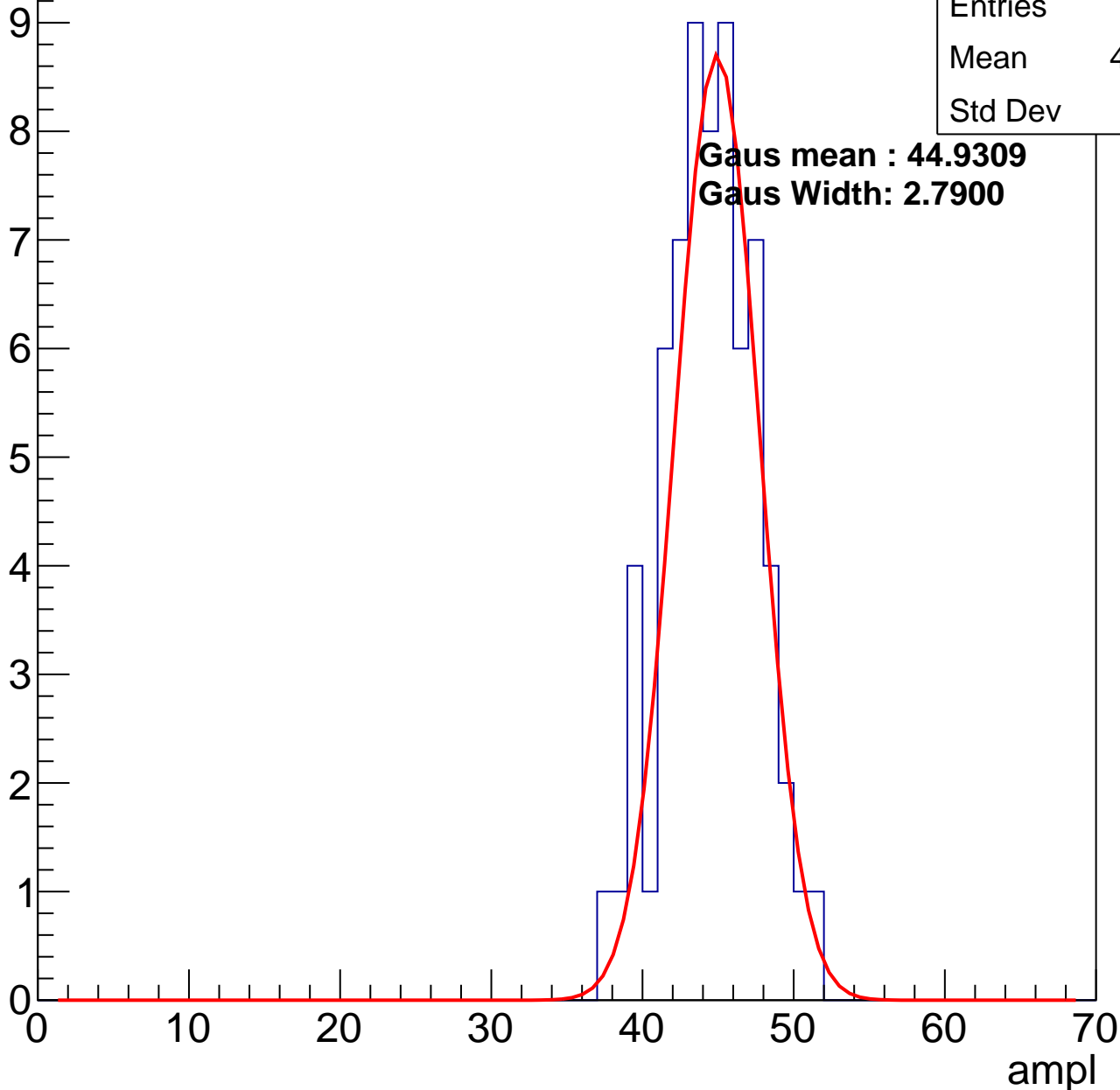
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.04
Std Dev	2.96

Gaus mean : 44.9309

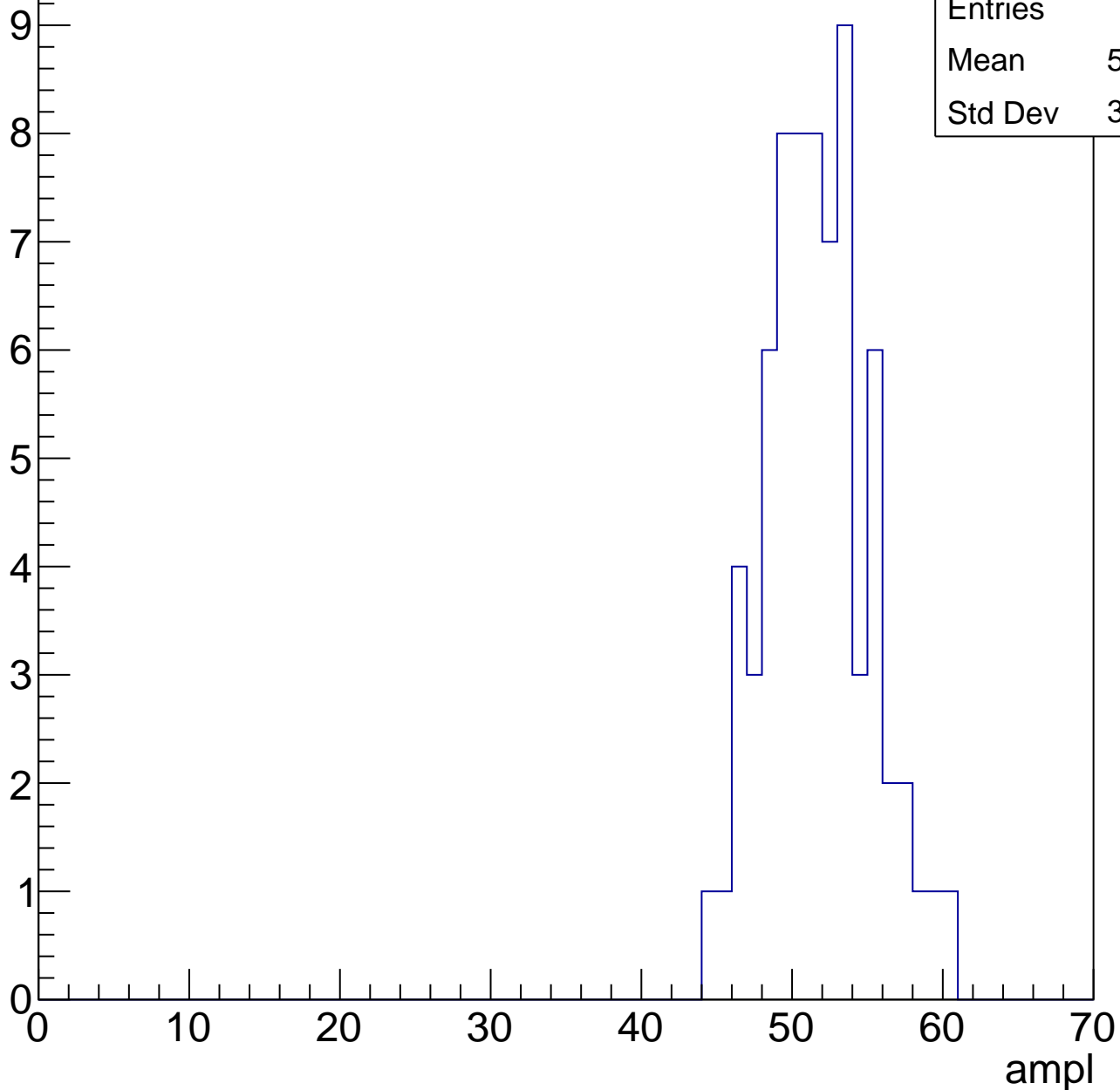
Gaus Width: 2.7900



B0L002S, U26-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



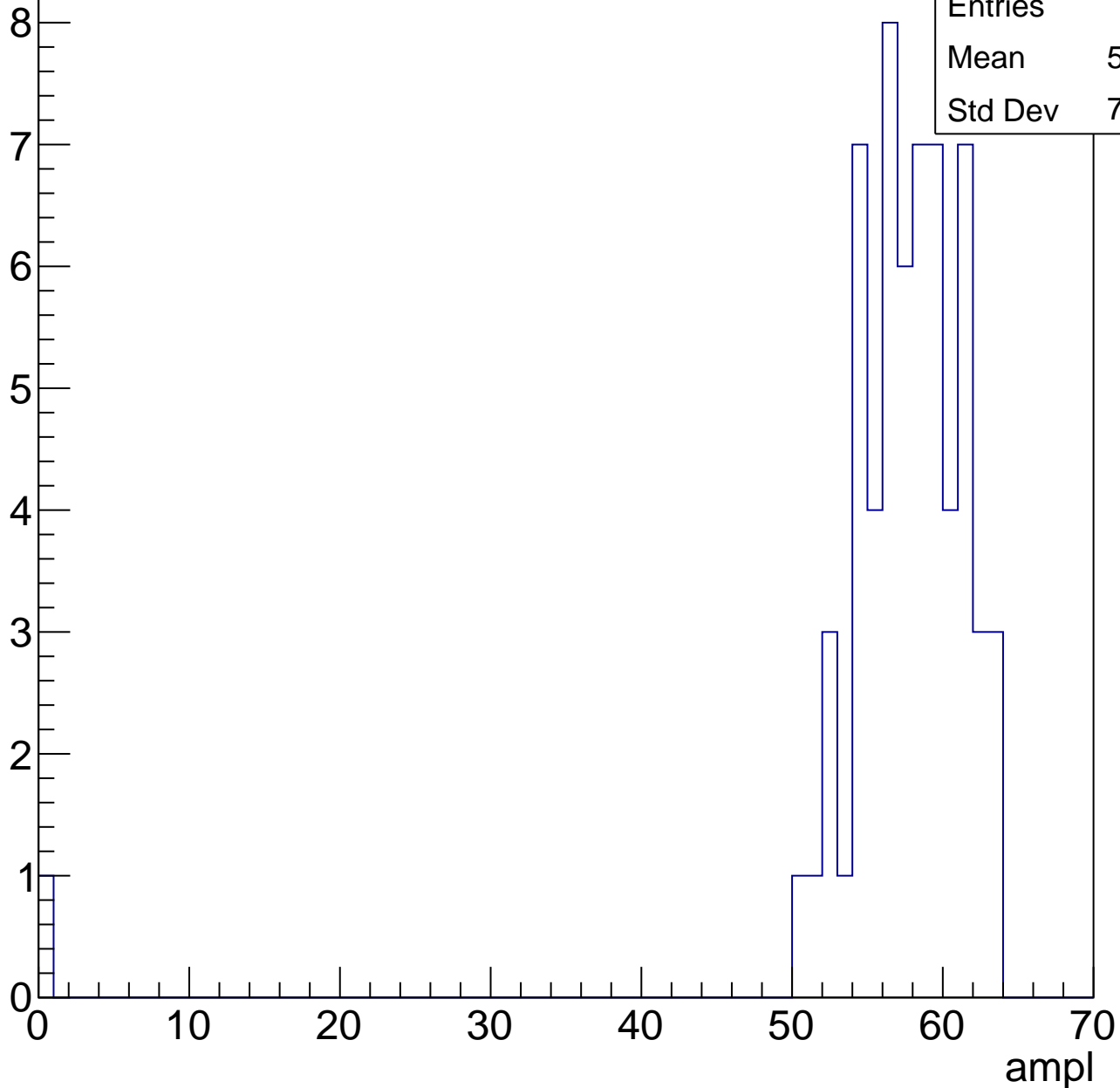
Entries	71
Mean	51.24
Std Dev	3.367

B0L002S, U26-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

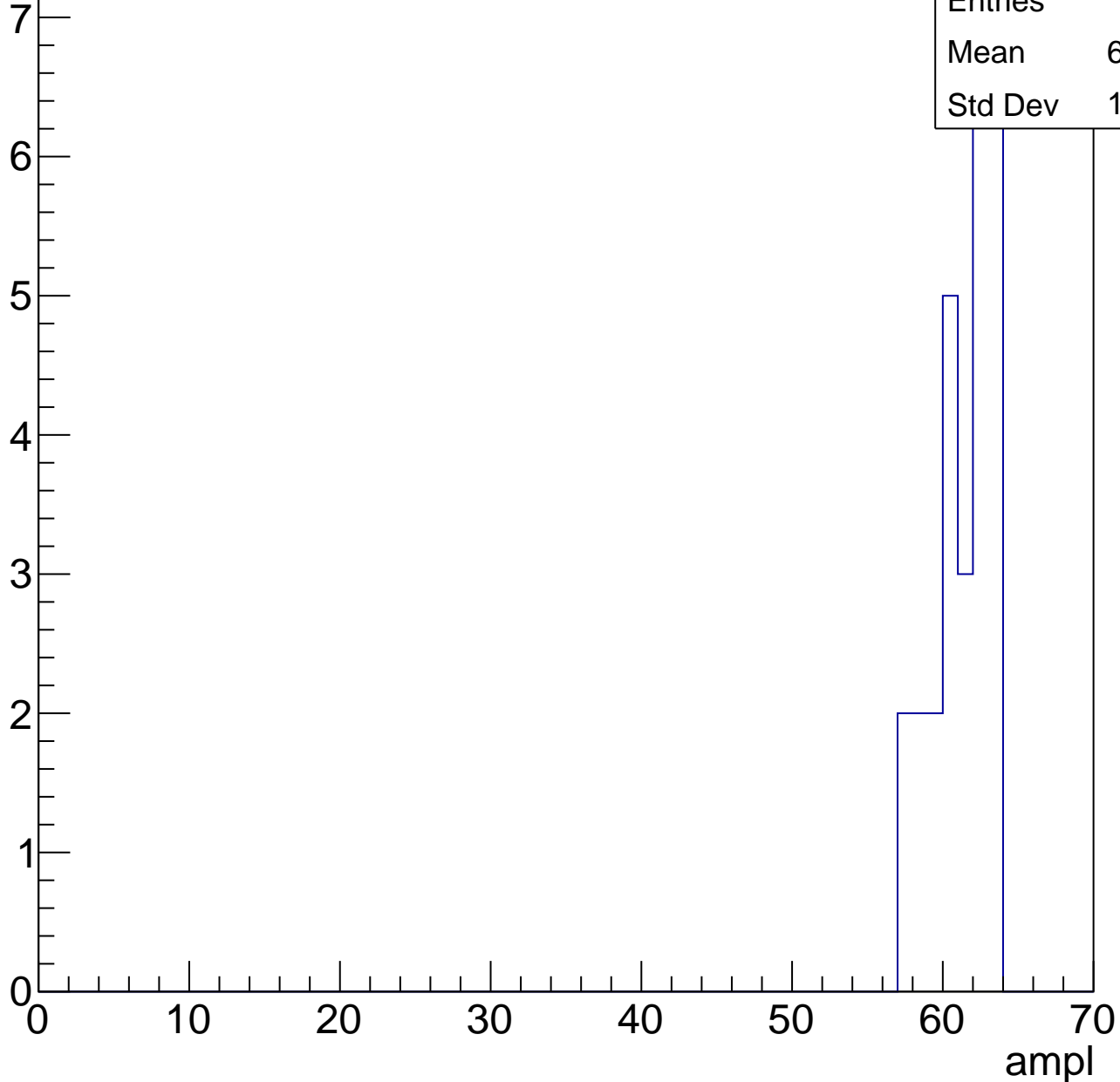
Entries	63
Mean	56.49
Std Dev	7.829



B0L002S, U26-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch38, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	29.76
Std Dev	4.757

Gaus mean : 30.5303

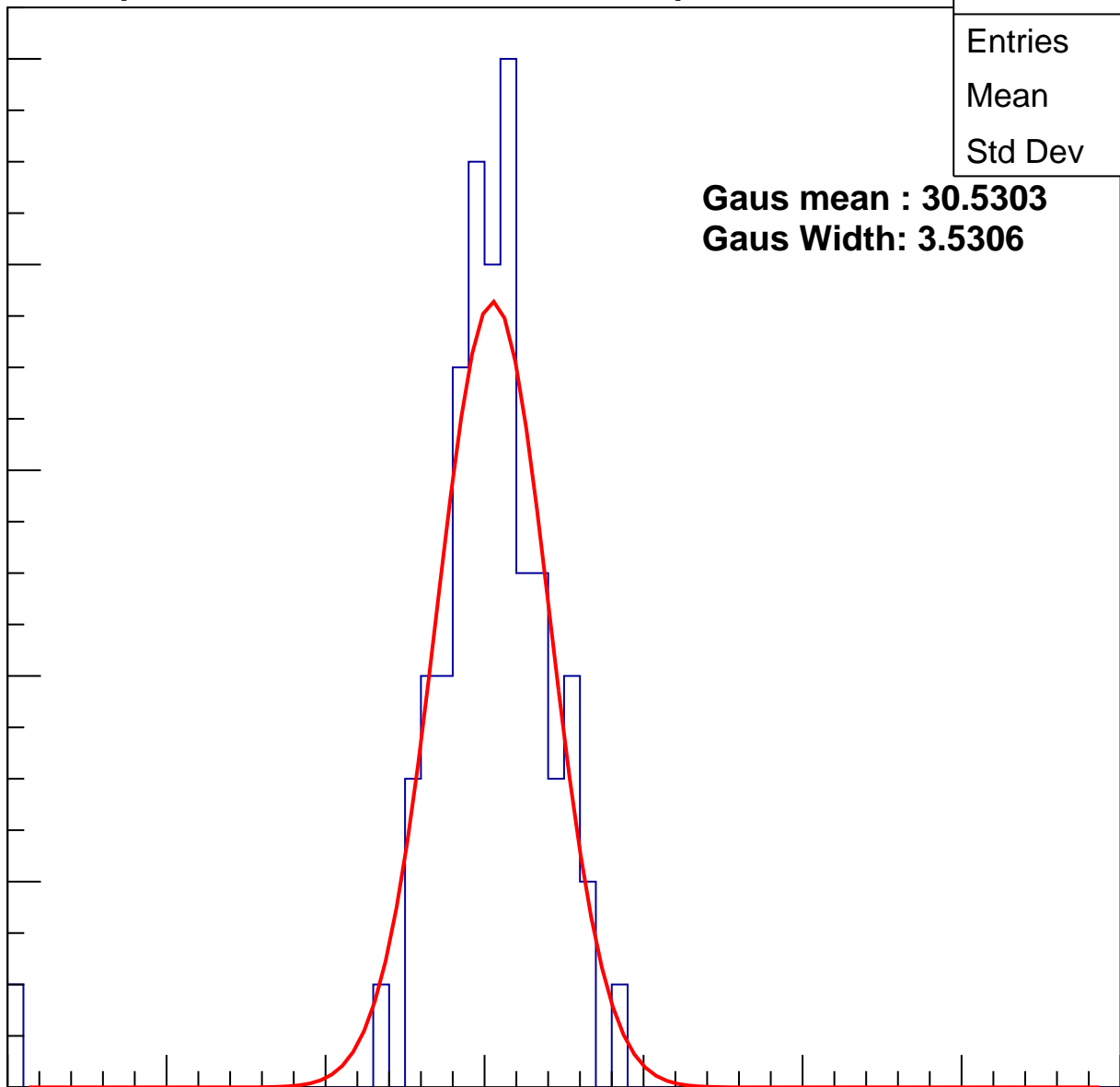
Gaus Width: 3.5306

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch38, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	36.51
Std Dev	3.17

Gaus mean : 37.3008

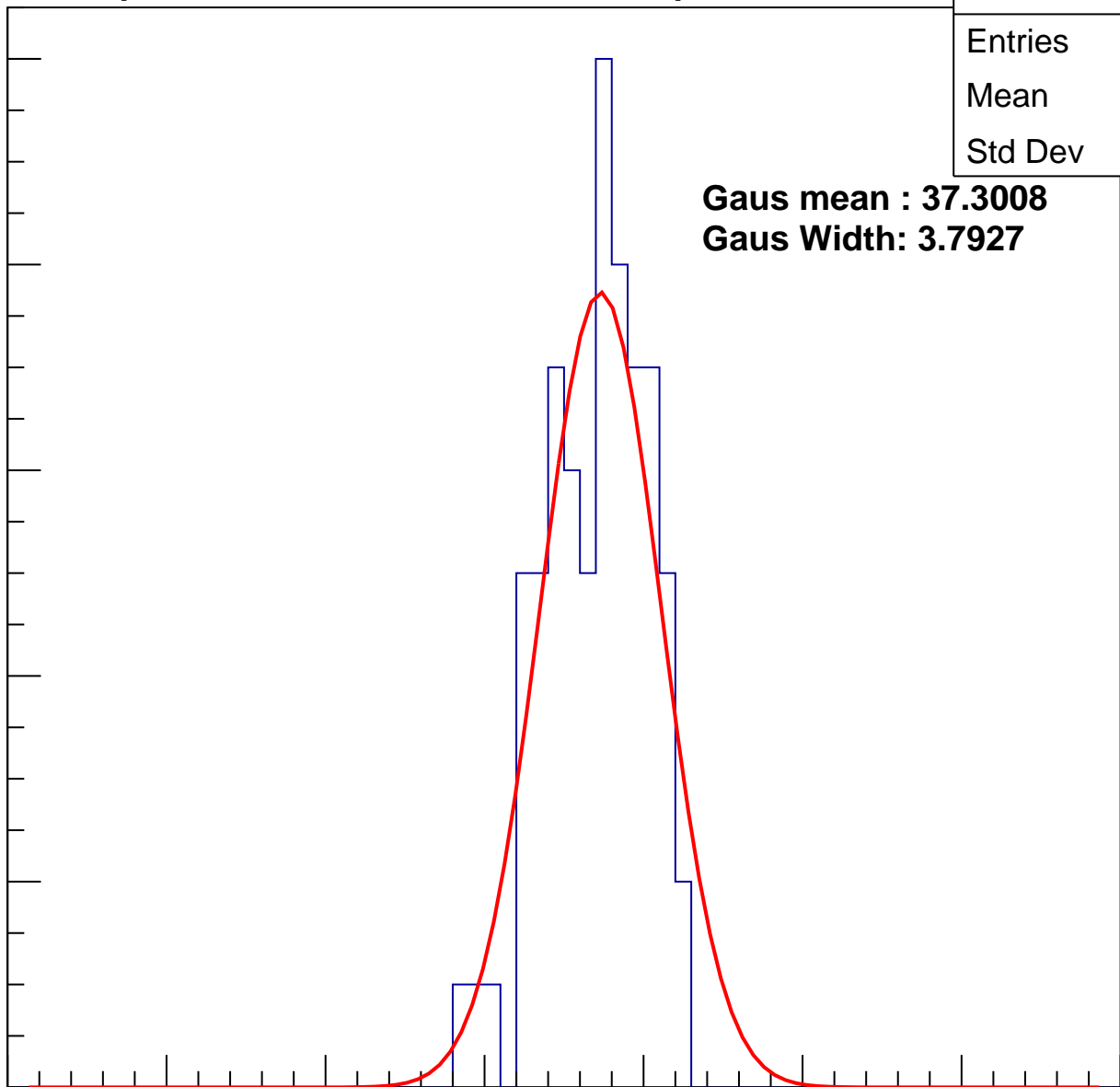
Gaus Width: 3.7927

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch38, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	43.7
Std Dev	3.218

Gaus mean : 44.5173

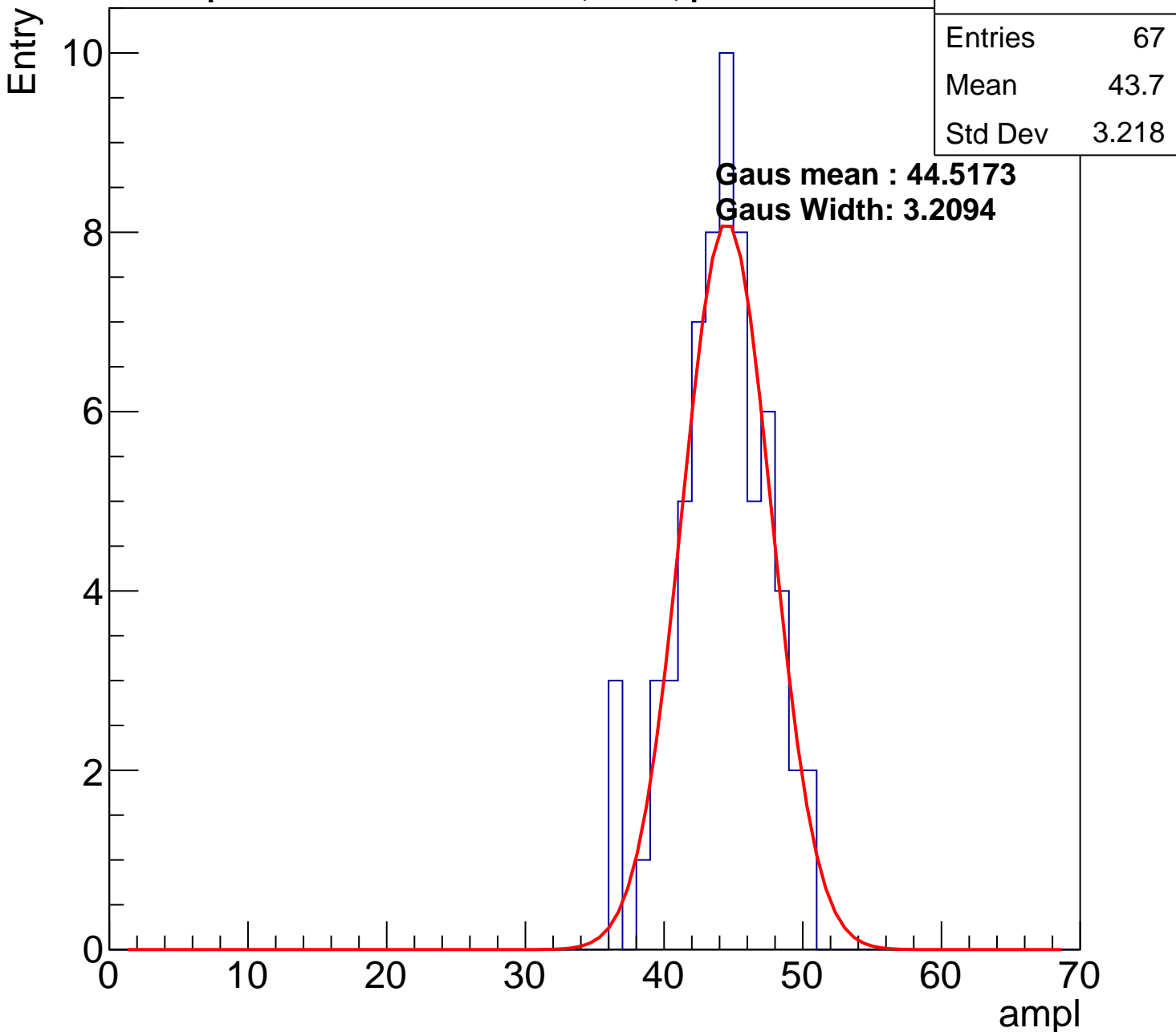
Gaus Width: 3.2094

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

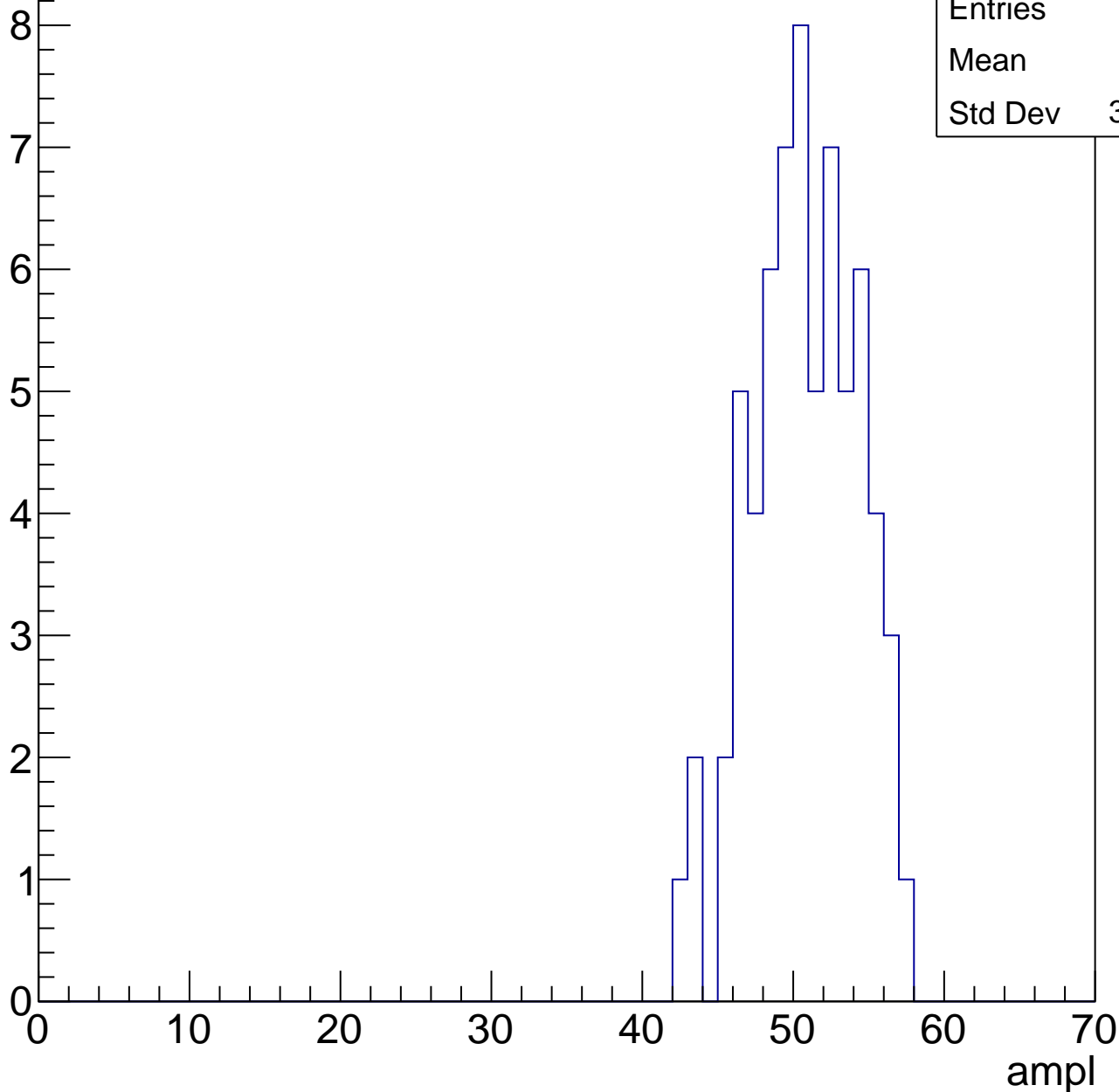


B0L002S, U26-ch38, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.3
Std Dev	3.446

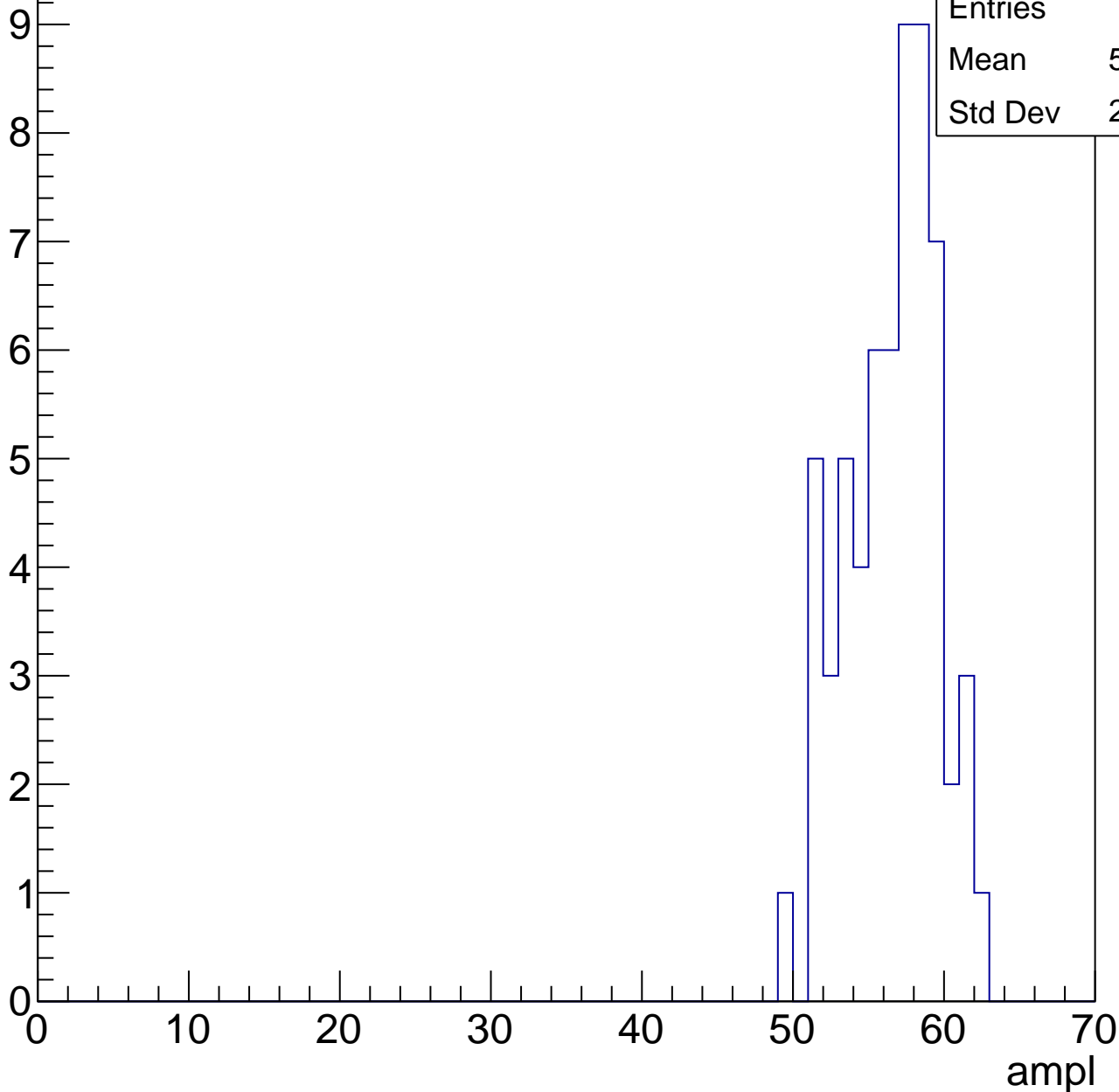


B0L002S, U26-ch38, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.07
Std Dev	2.975

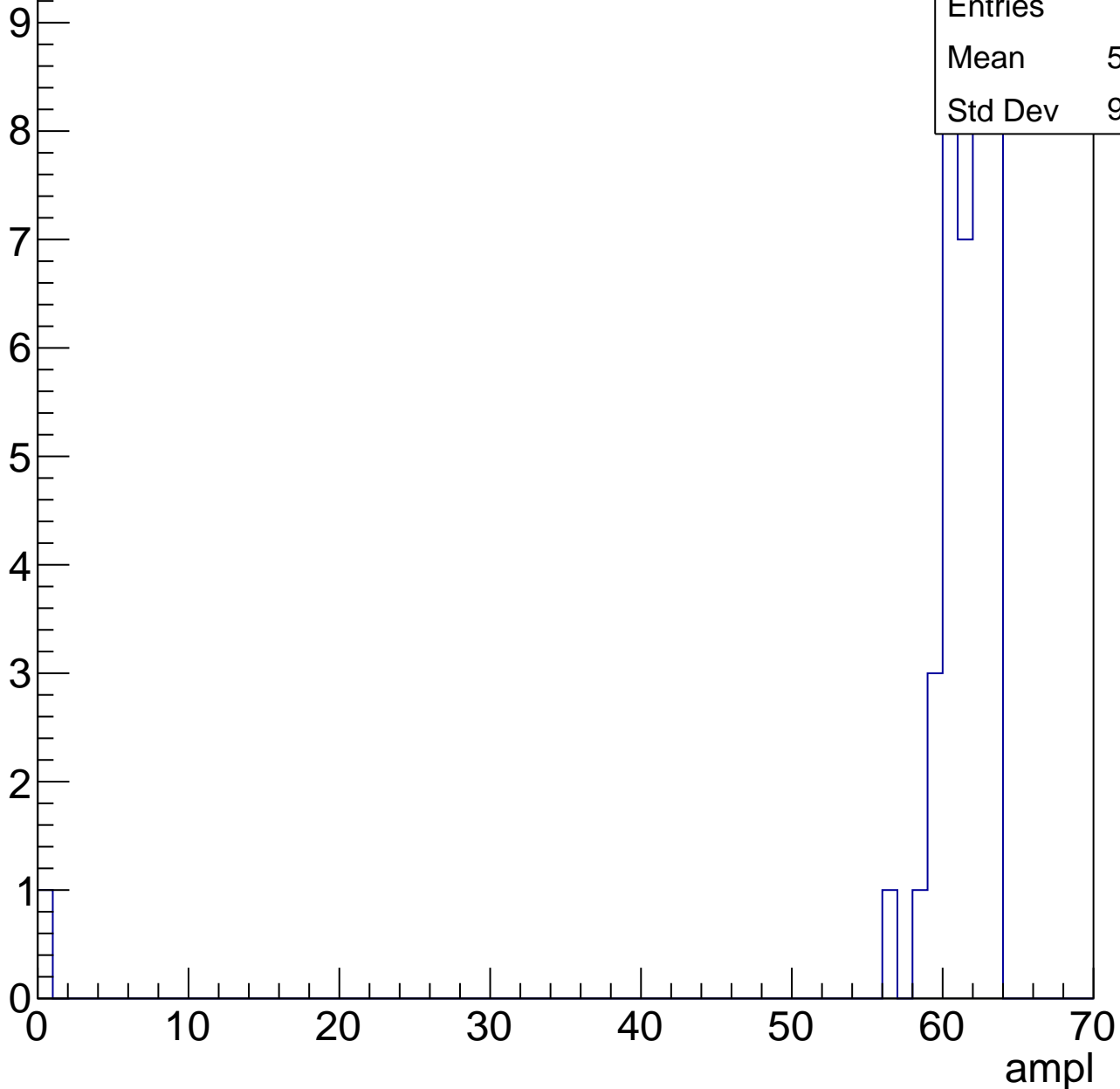


B0L002S, U26-ch38, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.47
Std Dev	9.904



B0L002S, U26-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

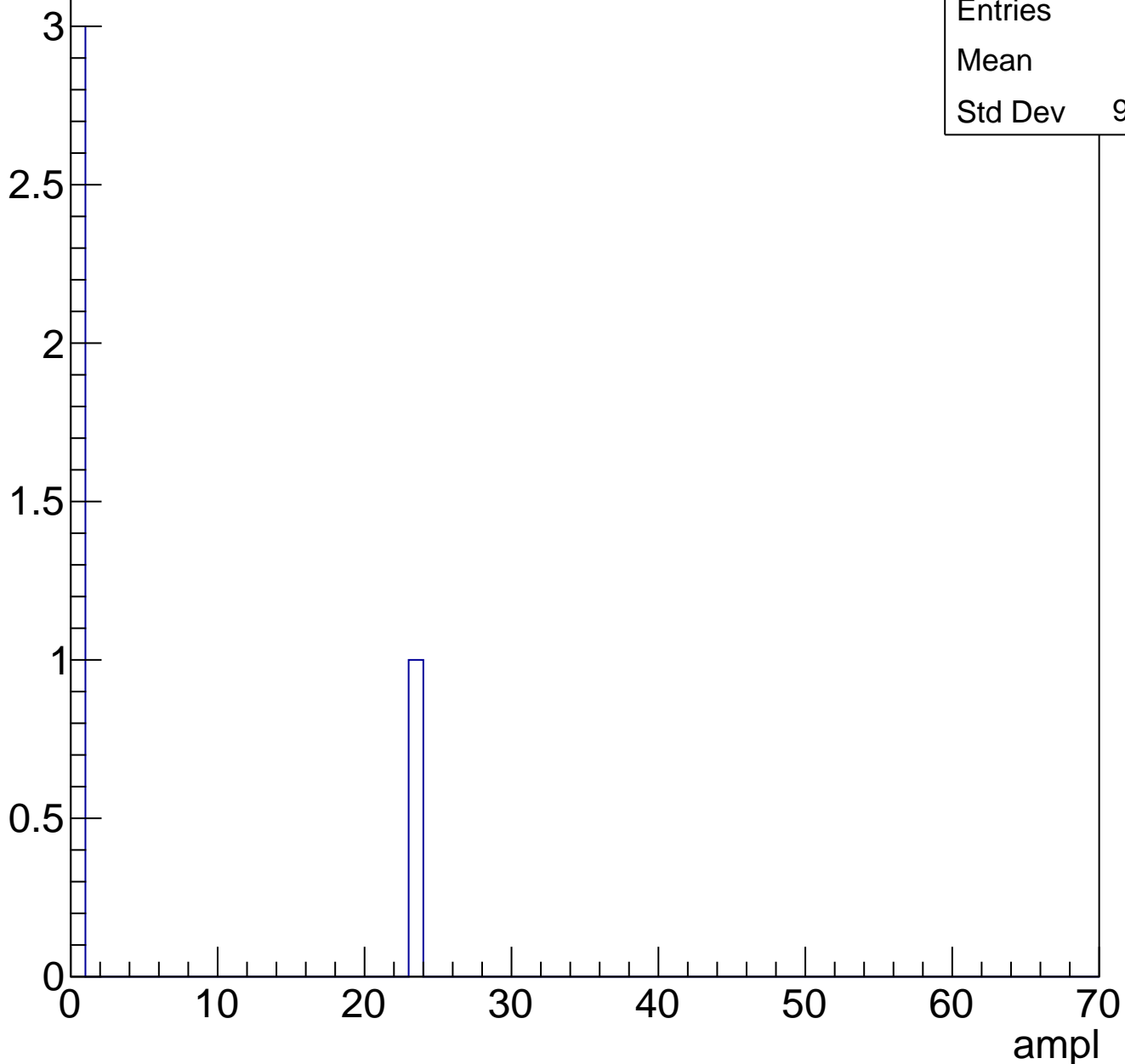
0 10 20 30 40 50 60 70

ampl

B0L002S, U26-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	5.75
Std Dev	9.959

B0L002S, U26-ch39, adc0

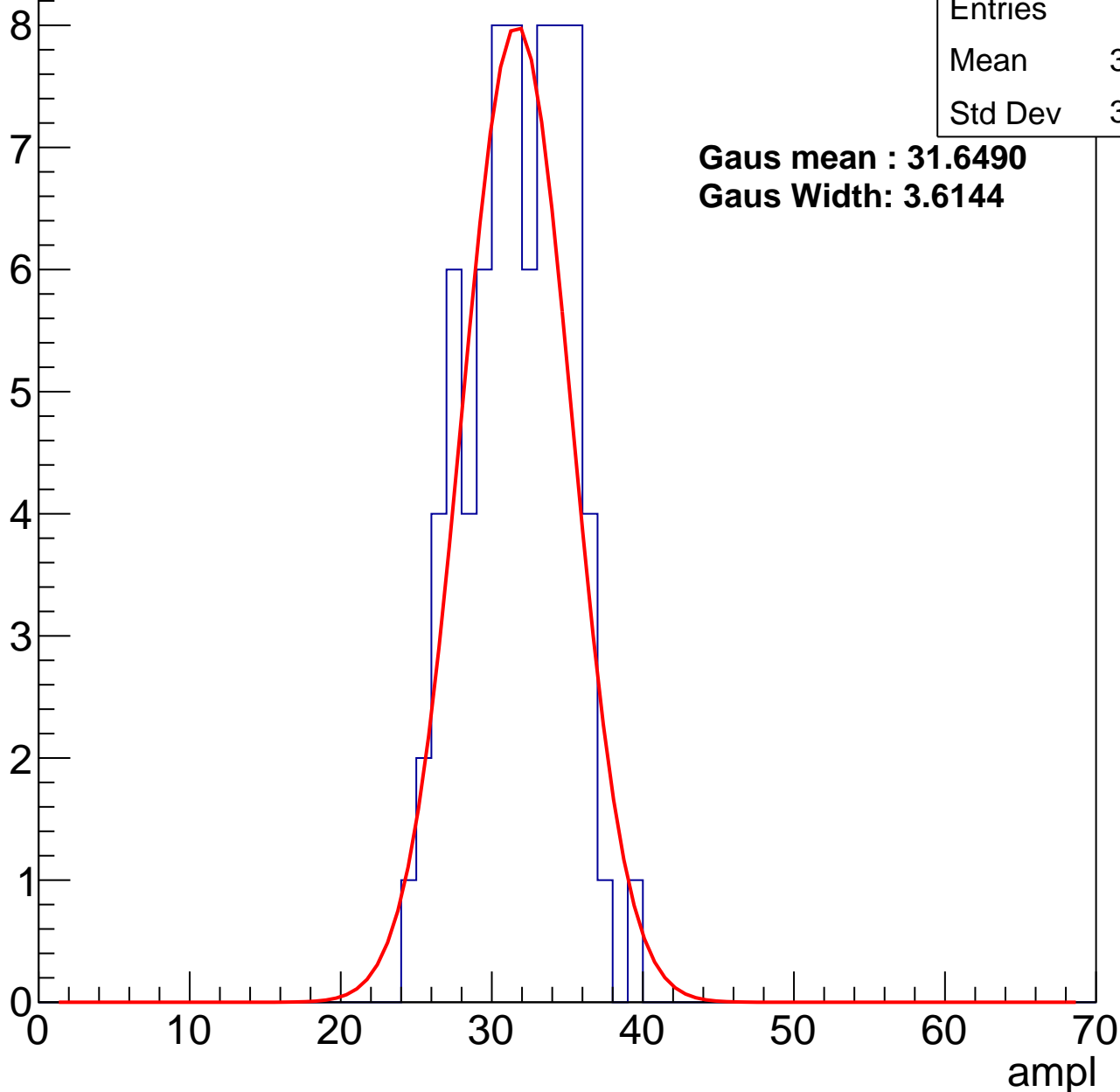
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.23
Std Dev	3.305

Gaus mean : 31.6490

Gaus Width: 3.6144



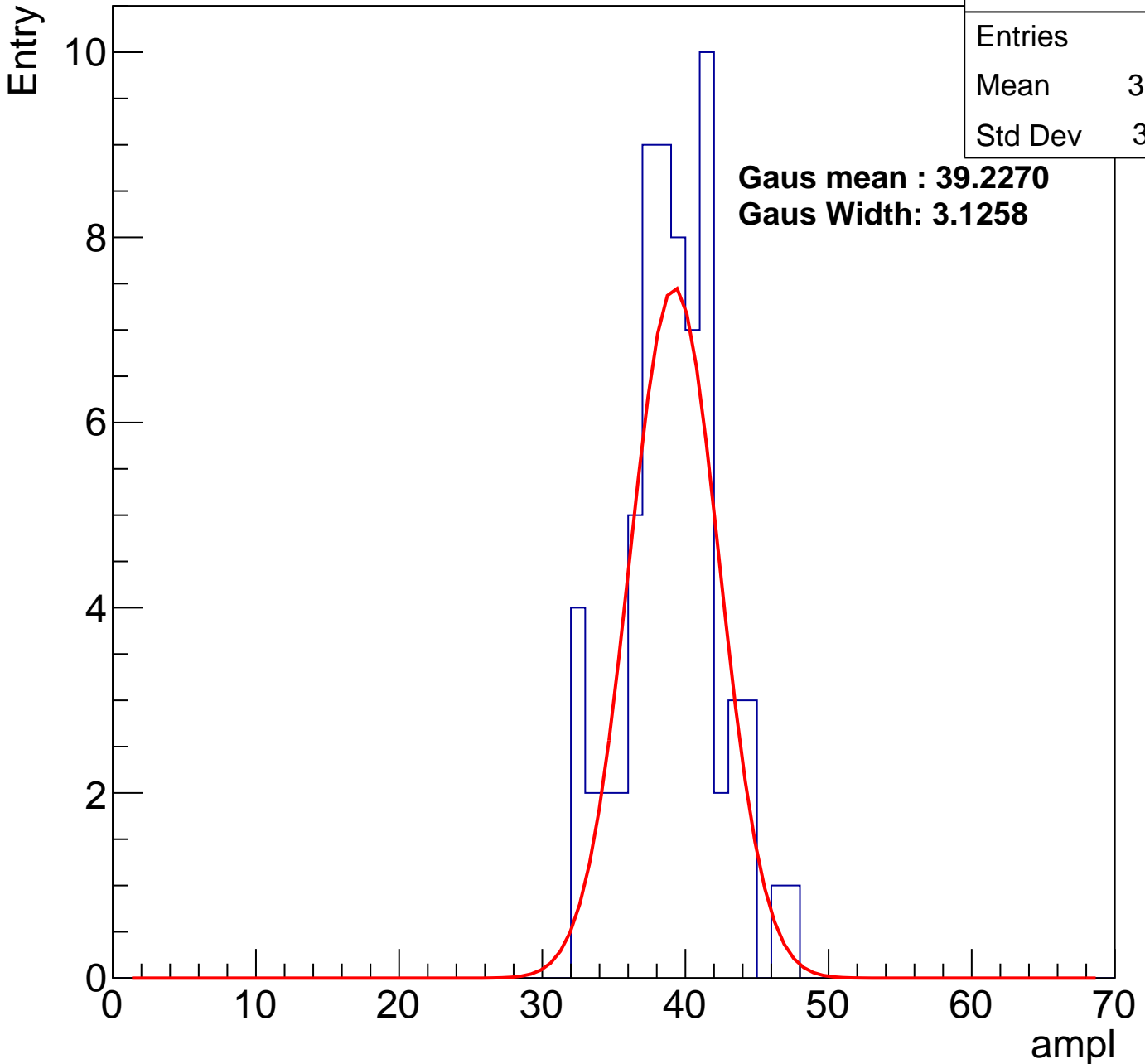
B0L002S, U26-ch39, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	38.63
Std Dev	3.281

Gaus mean : 39.2270

Gaus Width: 3.1258



B0L002S, U26-ch39, adc2

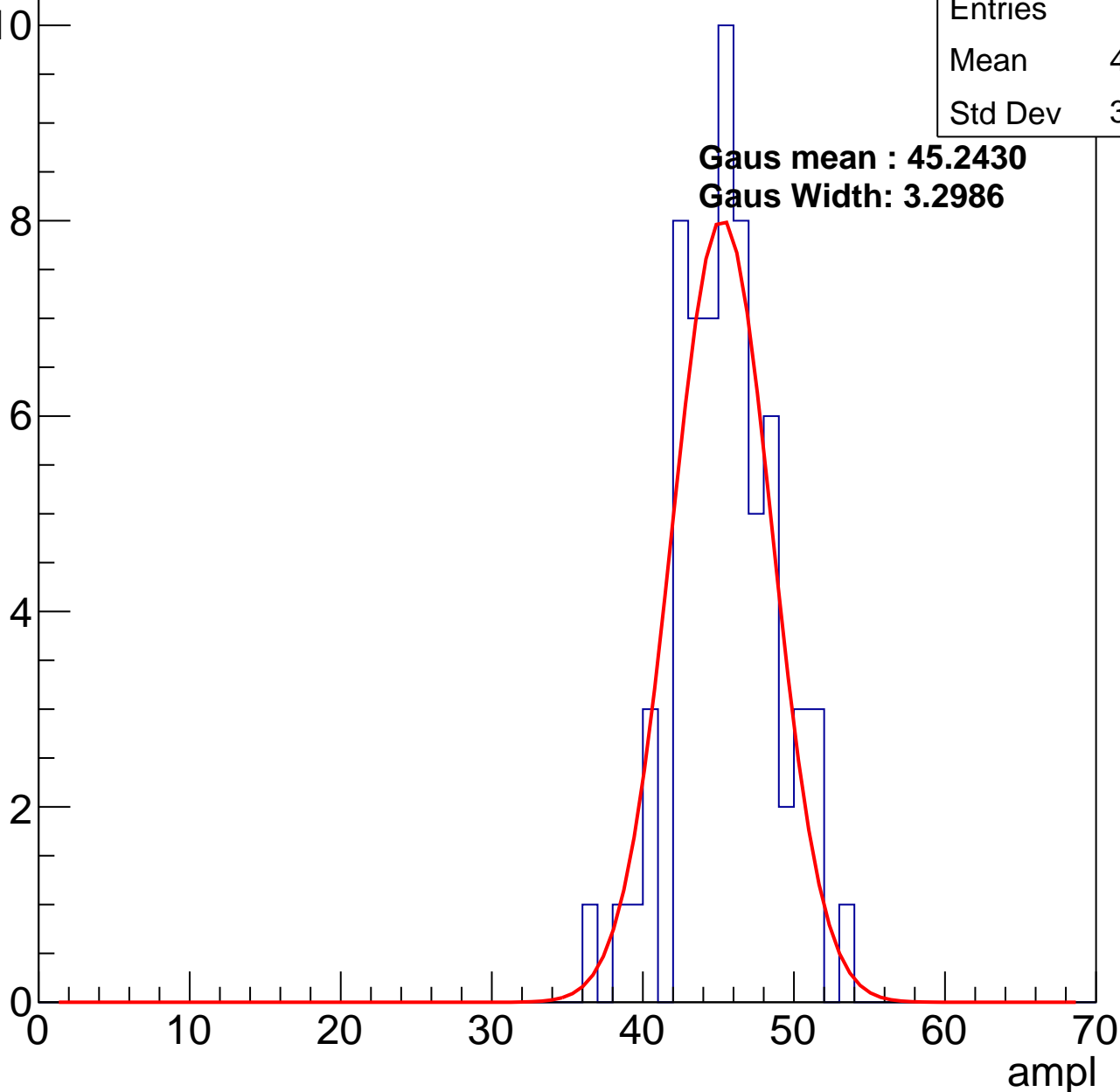
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	45.05
Std Dev	3.277

Gaus mean : 45.2430

Gaus Width: 3.2986

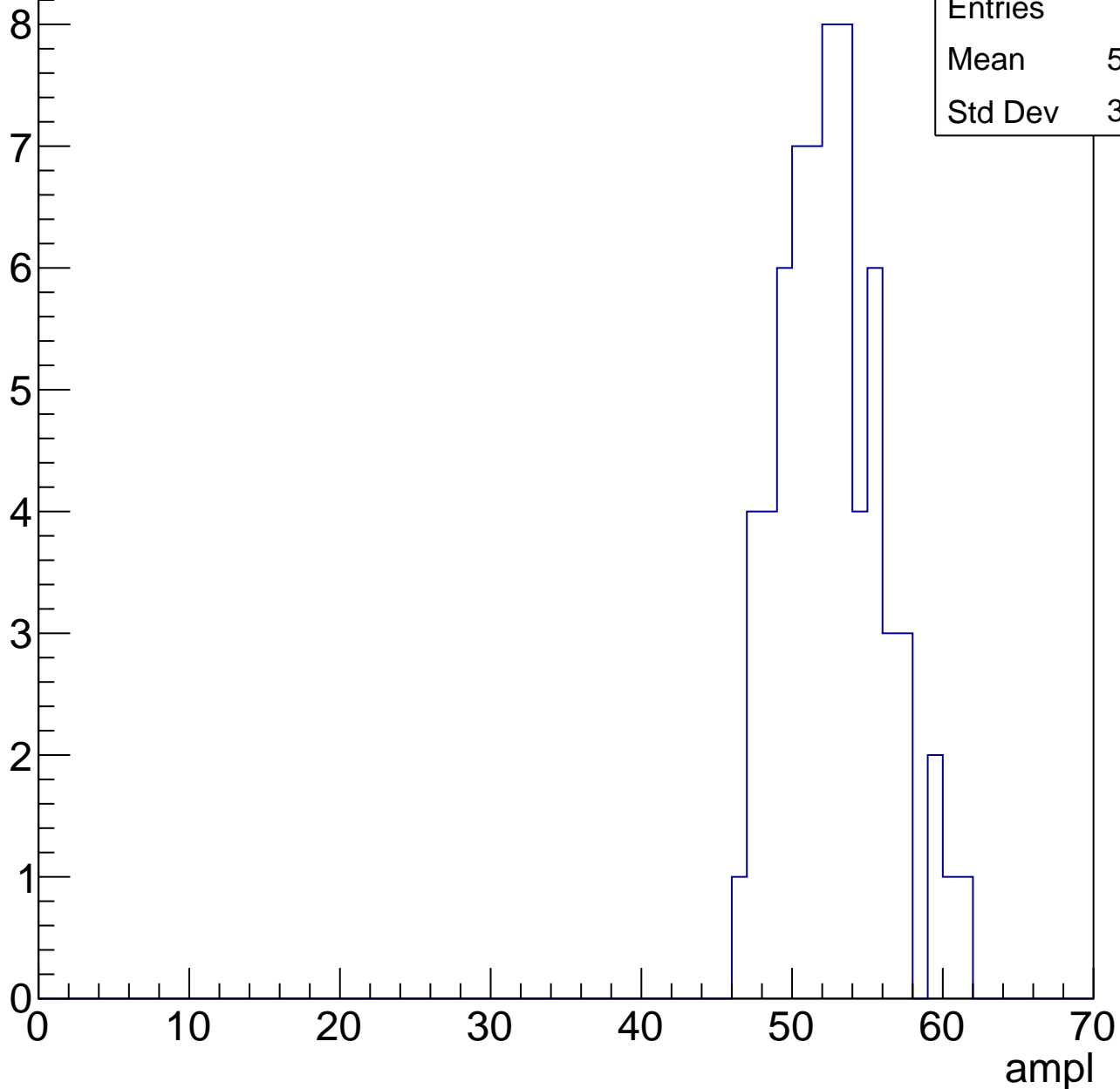


B0L002S, U26-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	52.17
Std Dev	3.354

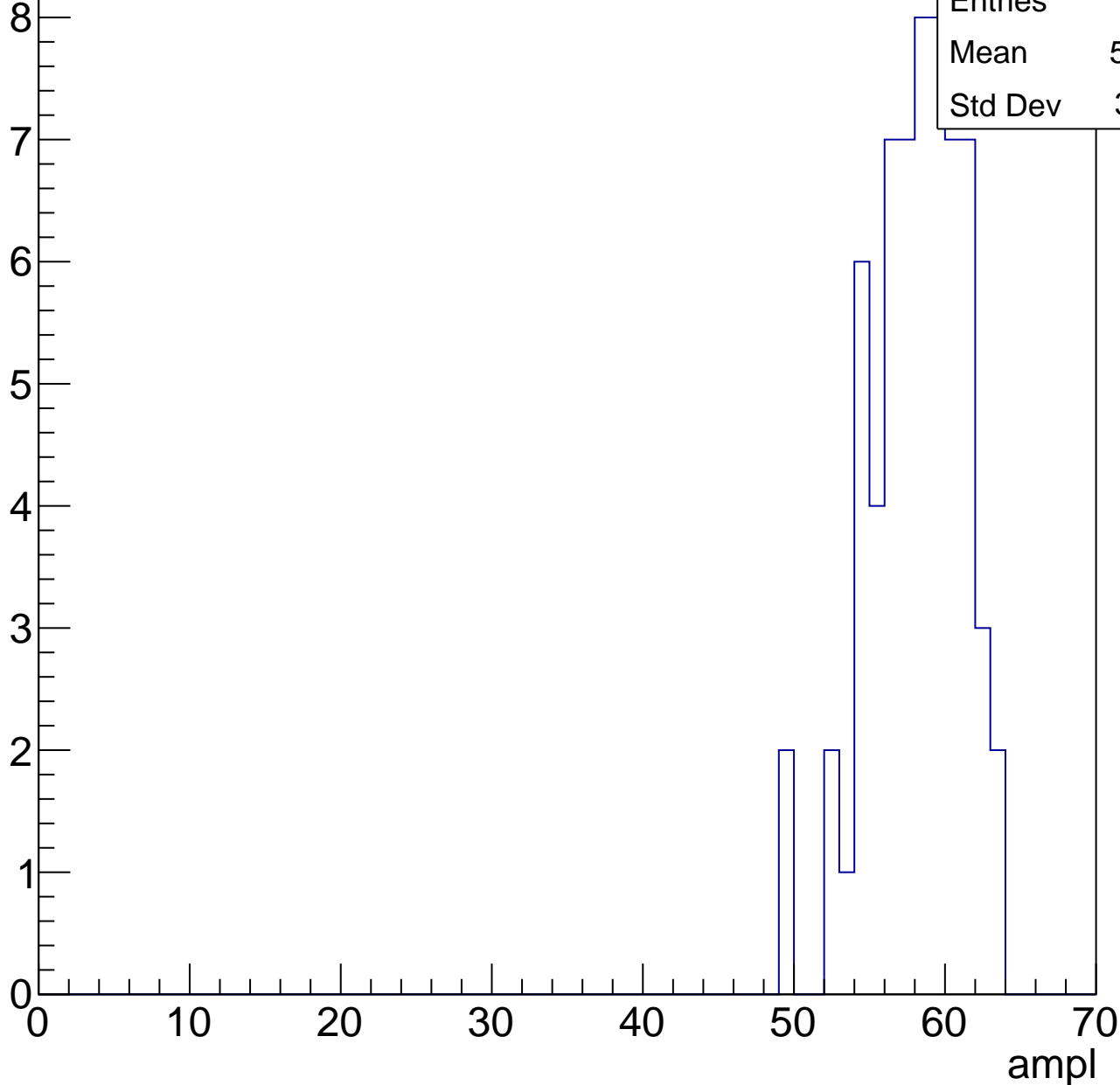


B0L002S, U26-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	57.58
Std Dev	3.091

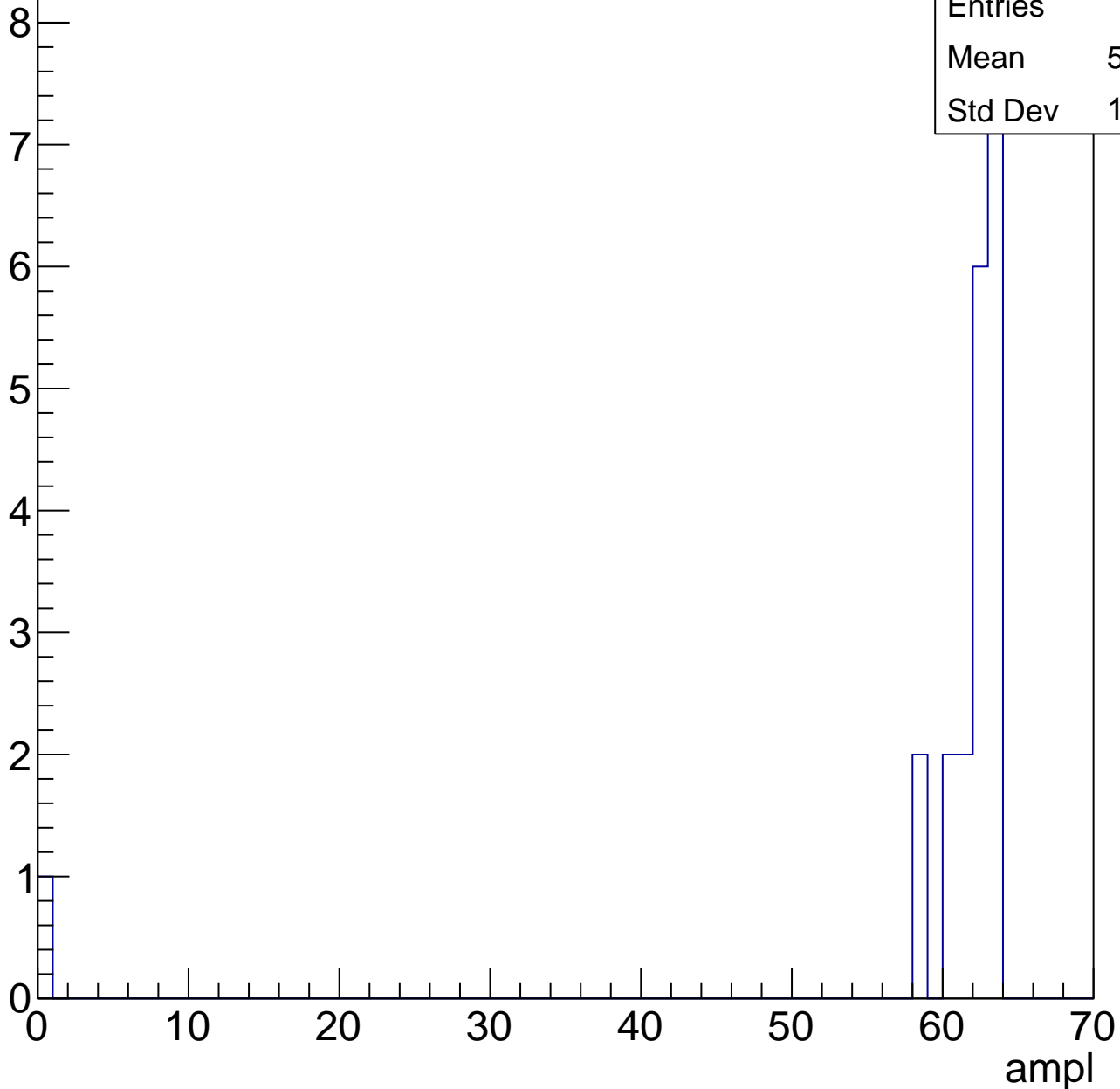


B0L002S, U26-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.76
Std Dev	13.23



B0L002S, U26-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch40, adc0

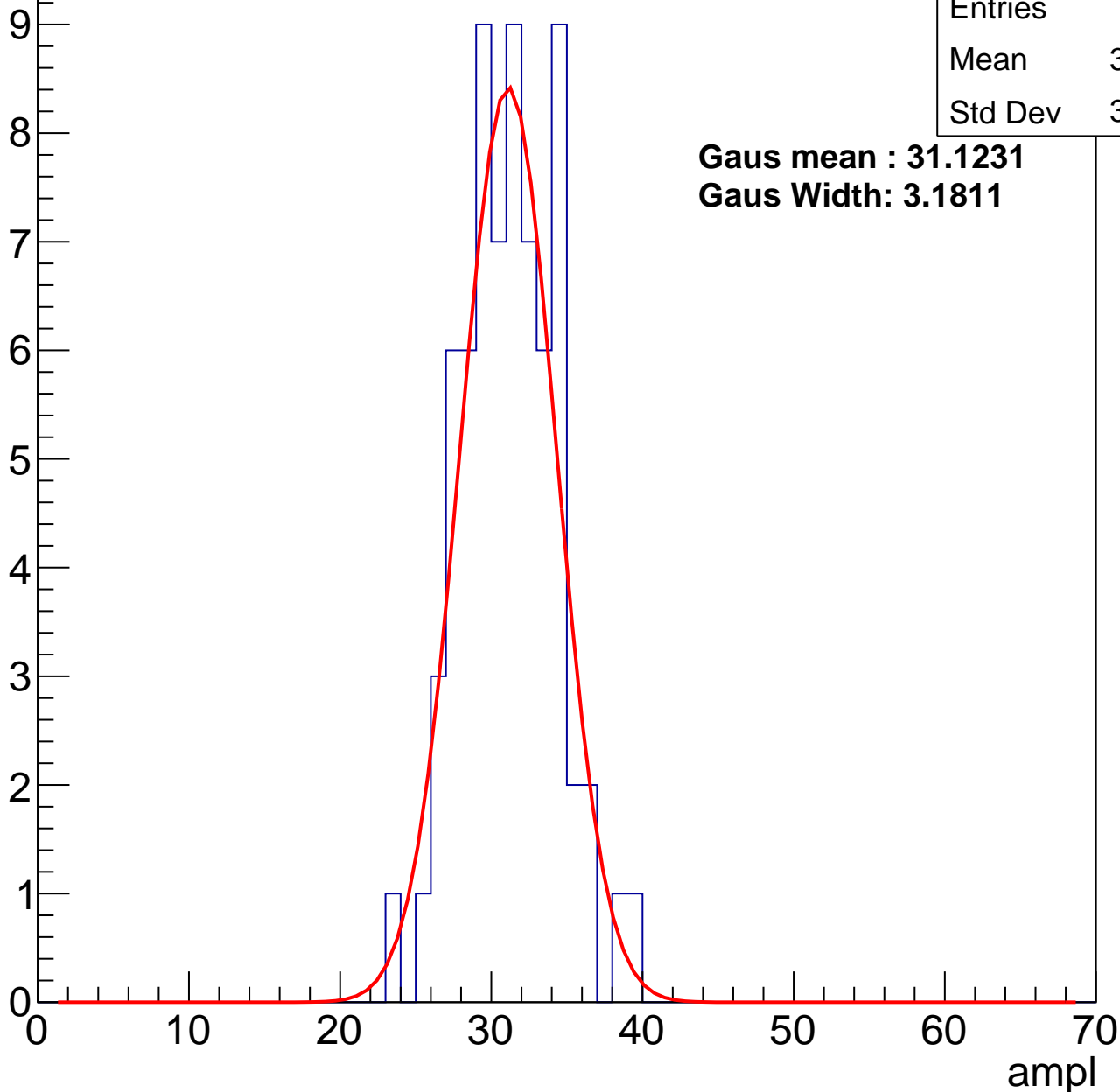
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.76
Std Dev	3.096

Gaus mean : 31.1231

Gaus Width: 3.1811



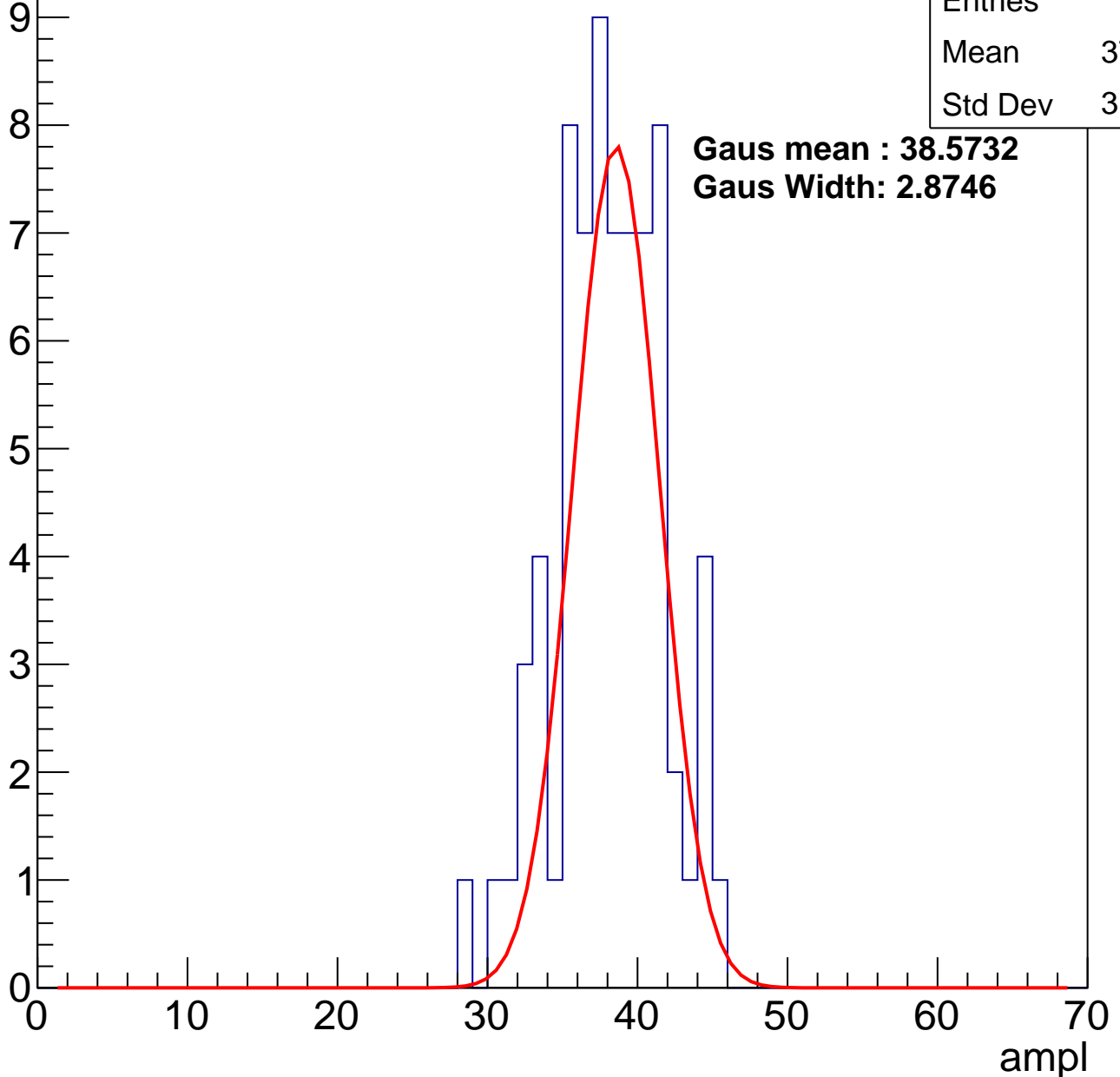
B0L002S, U26-ch40, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	37.65
Std Dev	3.524

Gaus mean : 38.5732
Gaus Width: 2.8746



B0L002S, U26-ch40, adc2

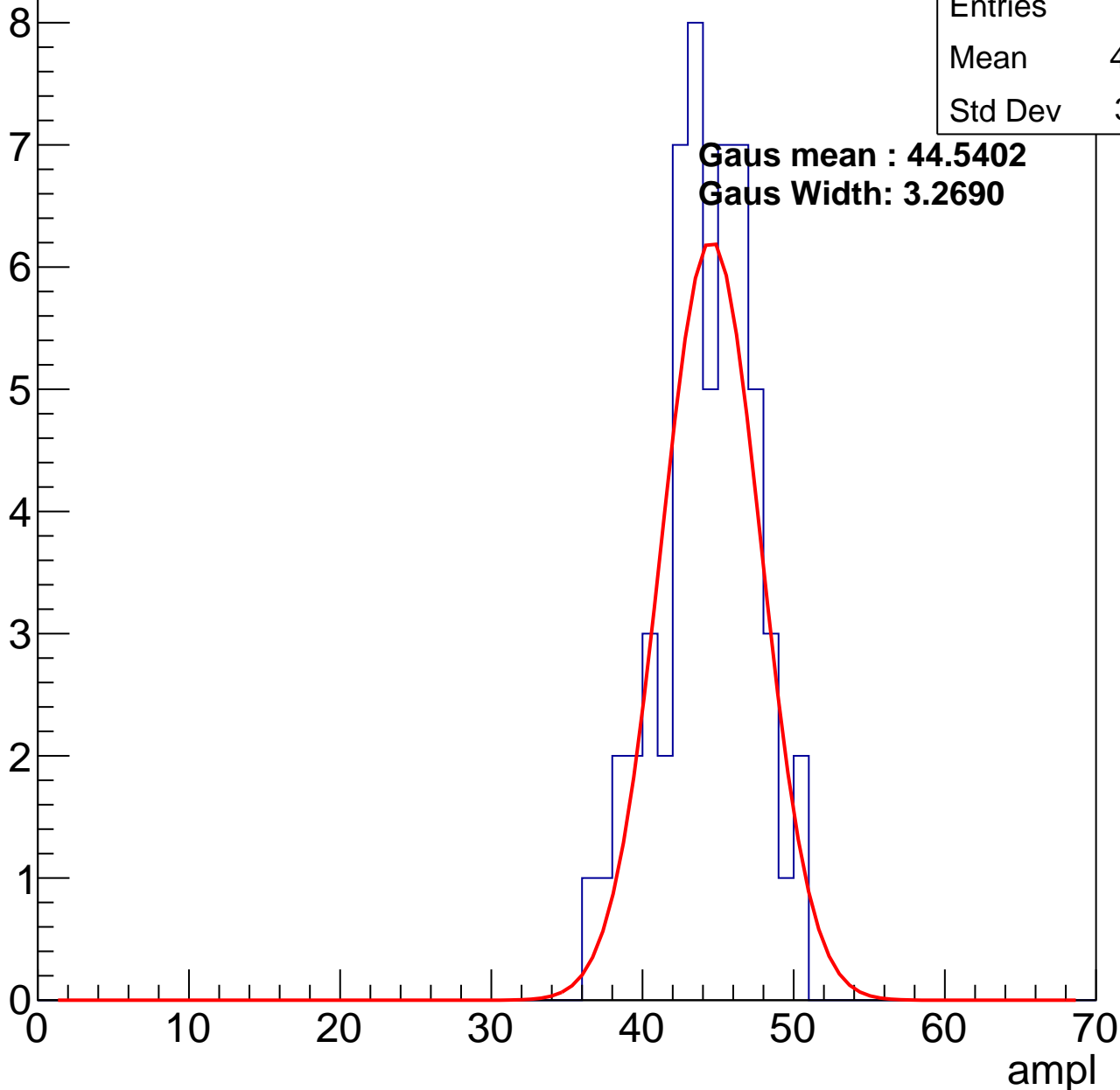
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.79
Std Dev	3.161

Gaus mean : 44.5402

Gaus Width: 3.2690



B0L002S, U26-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

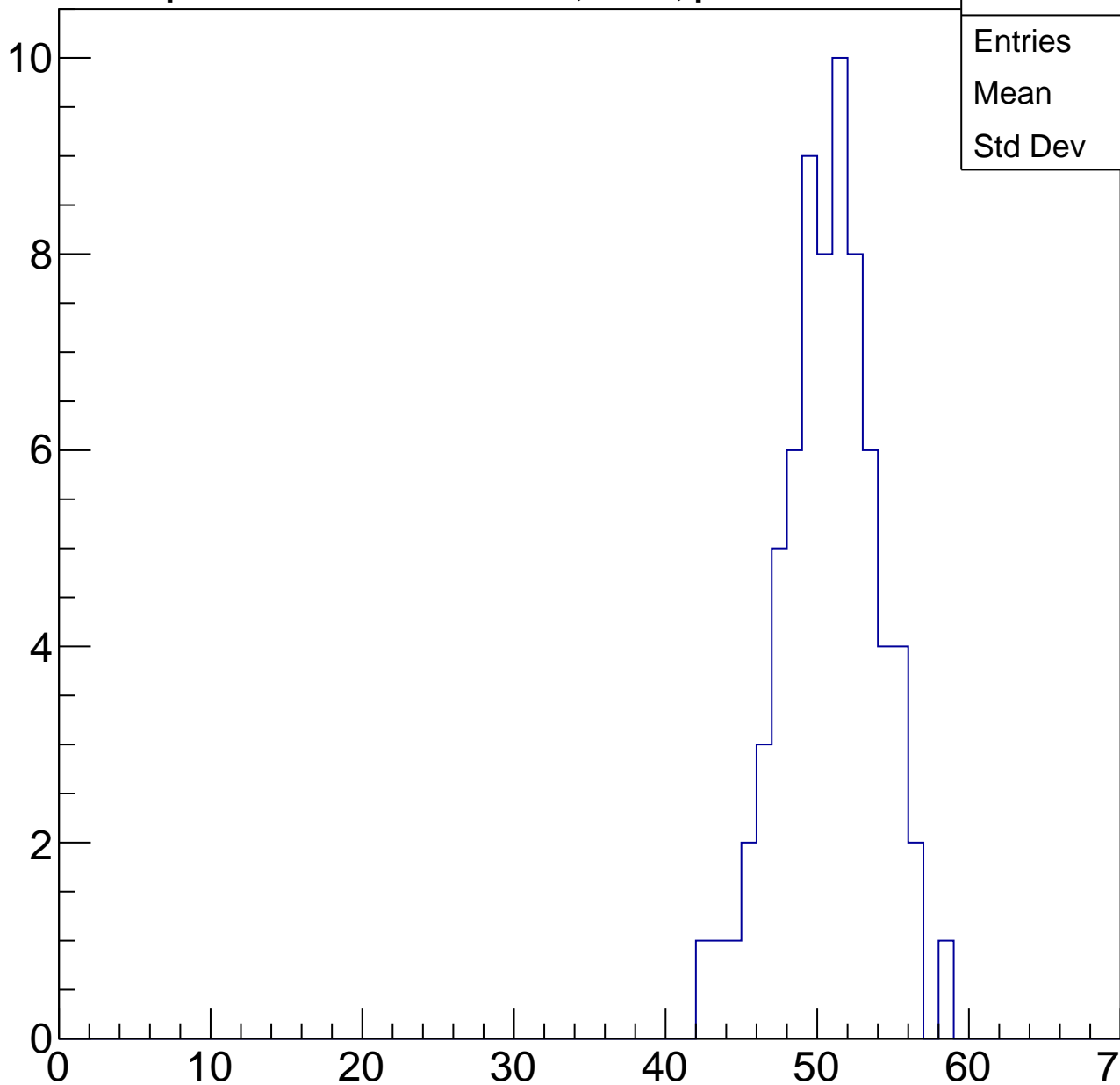
Entries	71
Mean	50.3
Std Dev	3.173

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

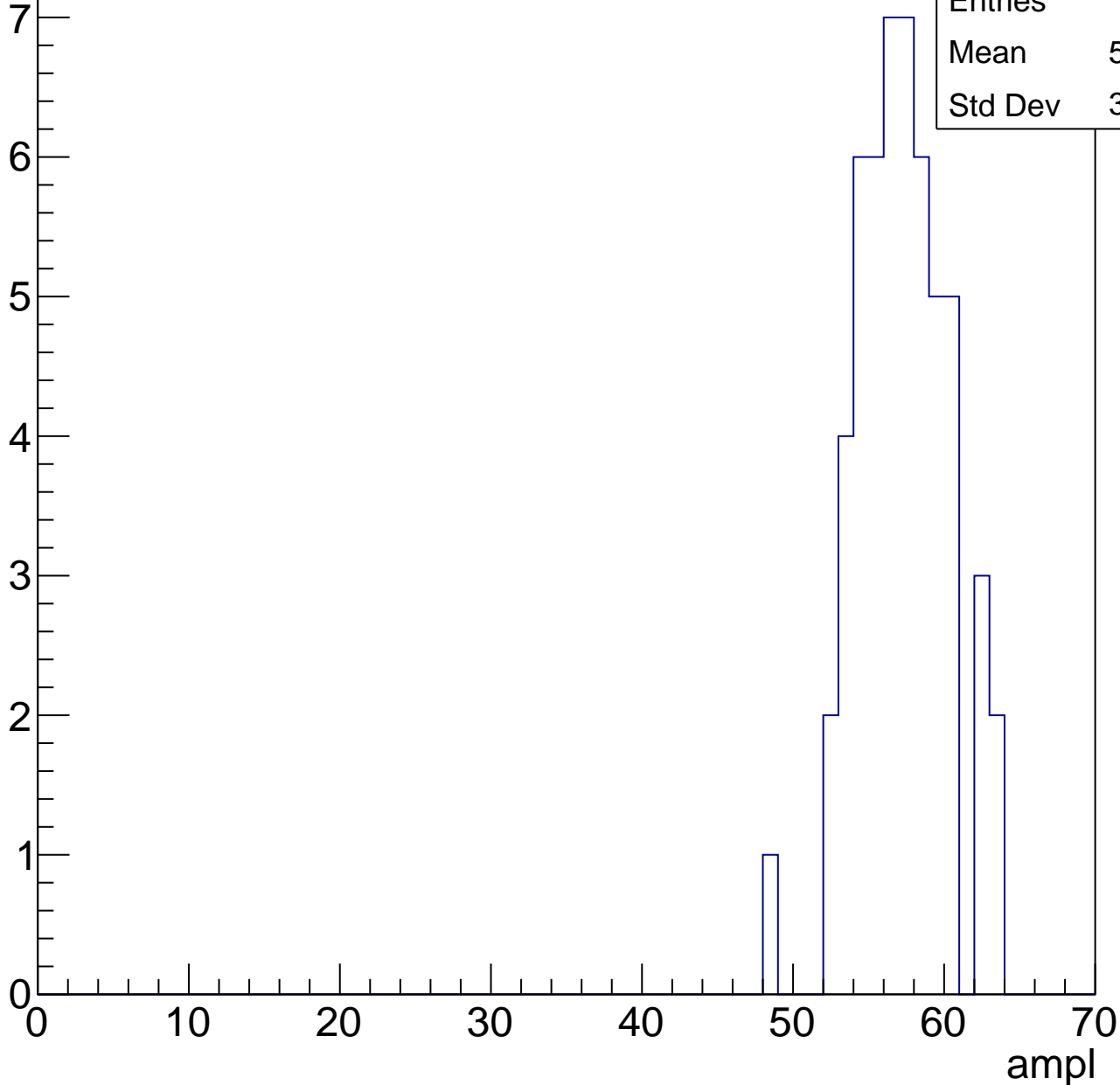


B0L002S, U26-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	56.74
Std Dev	3.026

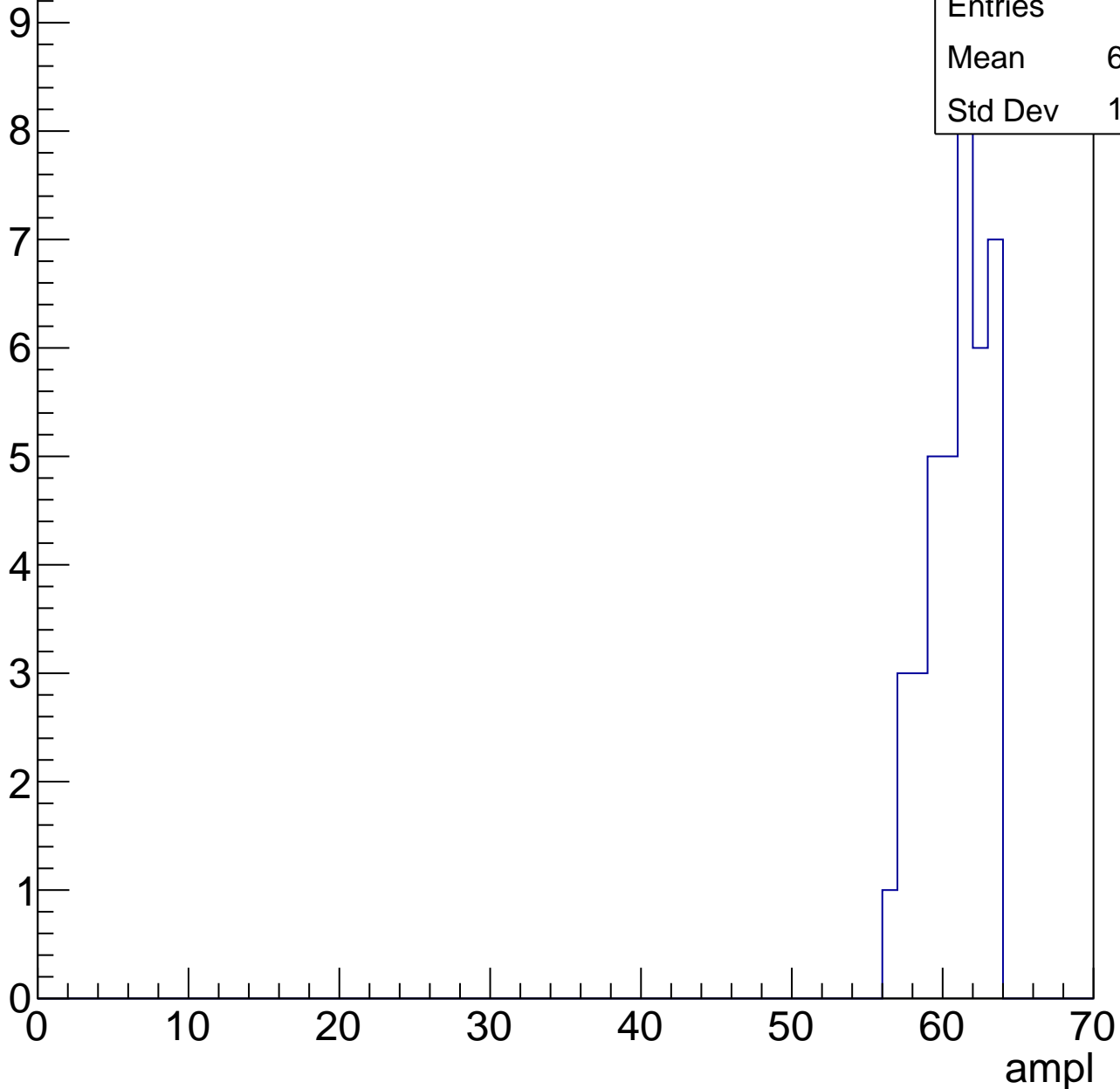


B0L002S, U26-ch40, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	60.46
Std Dev	1.946



B0L002S, U26-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L002S, U26-ch41, adc0

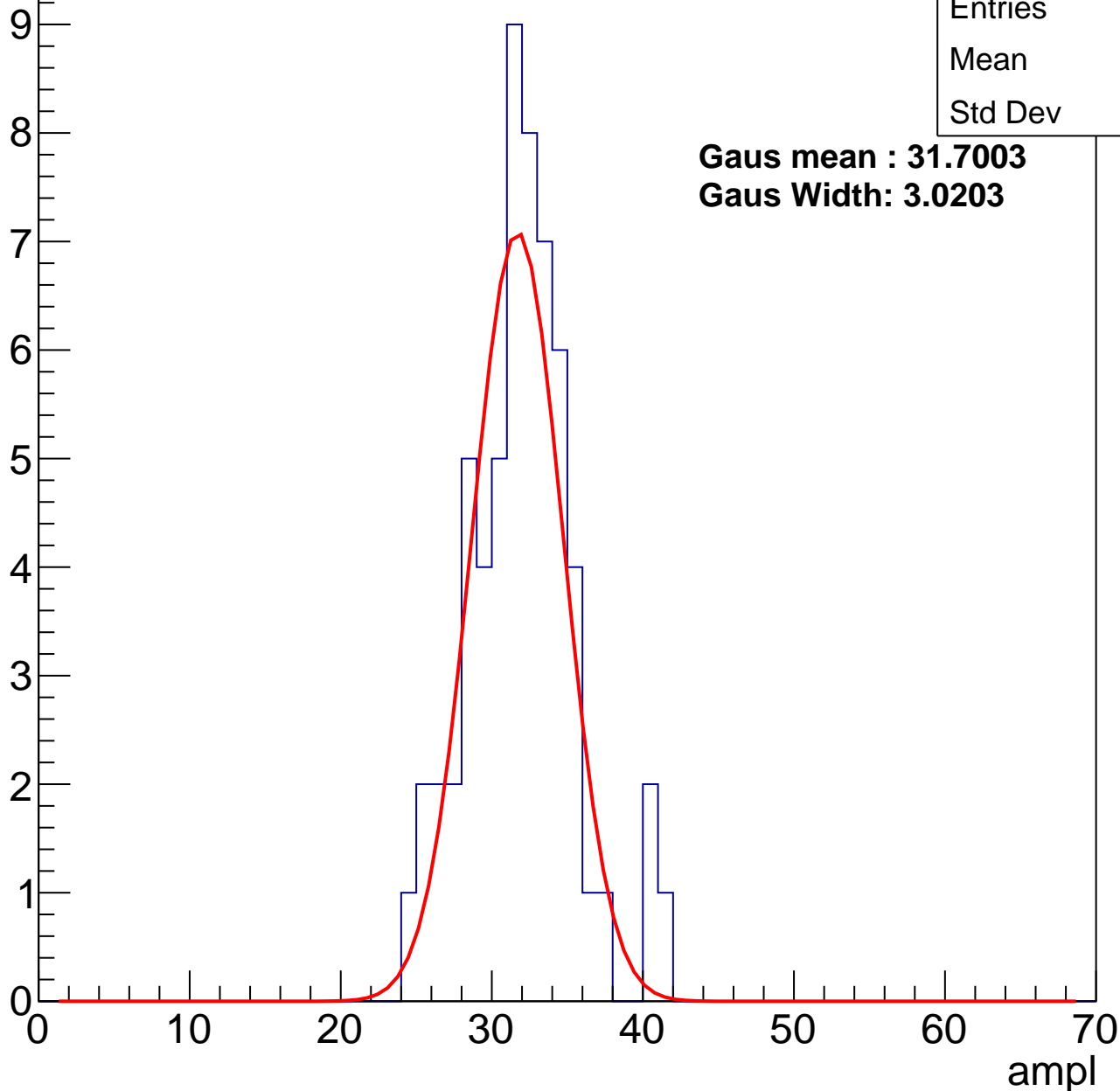
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	31.5
Std Dev	3.49

Gaus mean : 31.7003

Gaus Width: 3.0203



B0L002S, U26-ch41, adc1

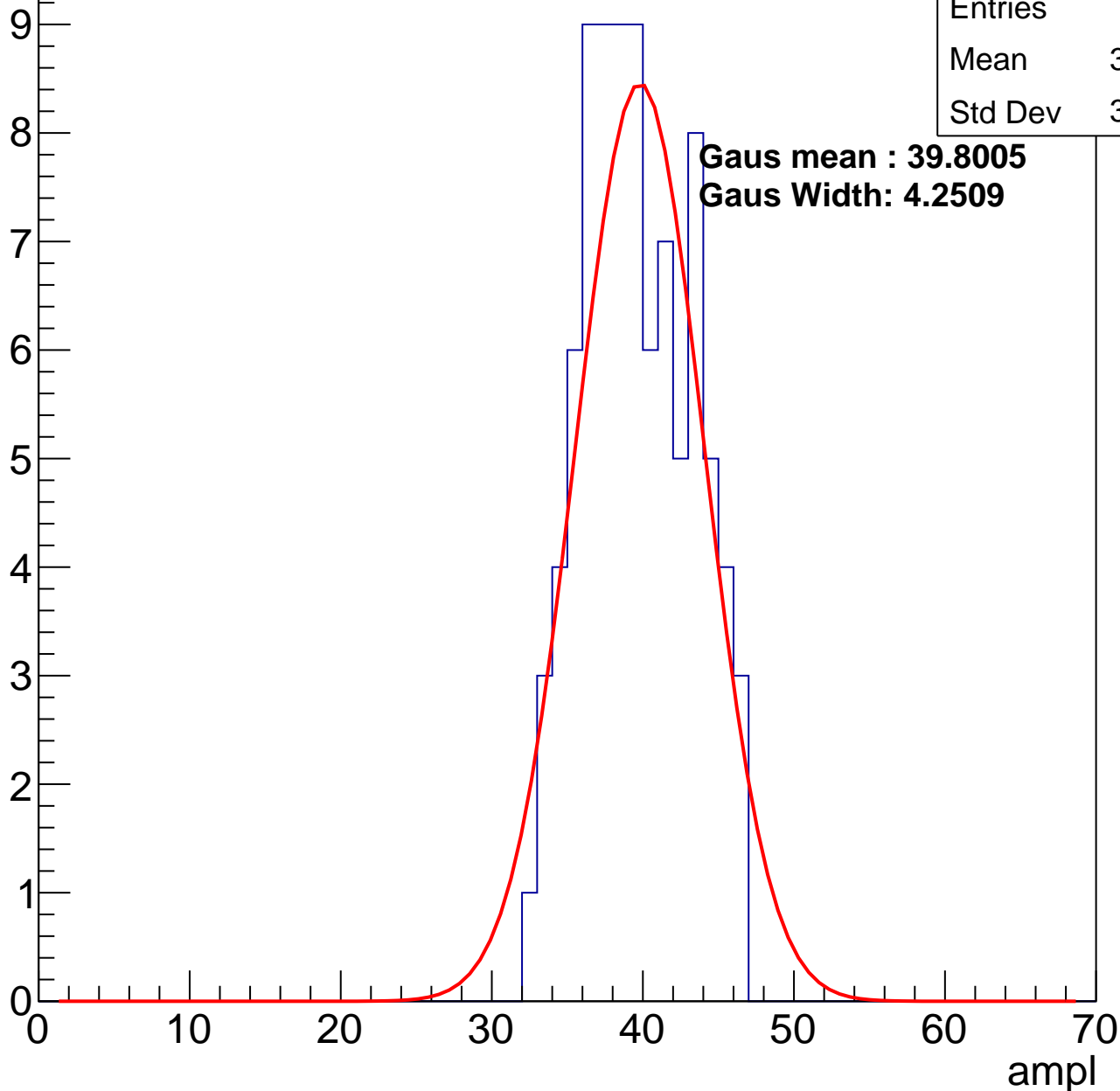
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	88
Mean	39.16
Std Dev	3.535

Gaus mean : 39.8005

Gaus Width: 4.2509



B0L002S, U26-ch41, adc2

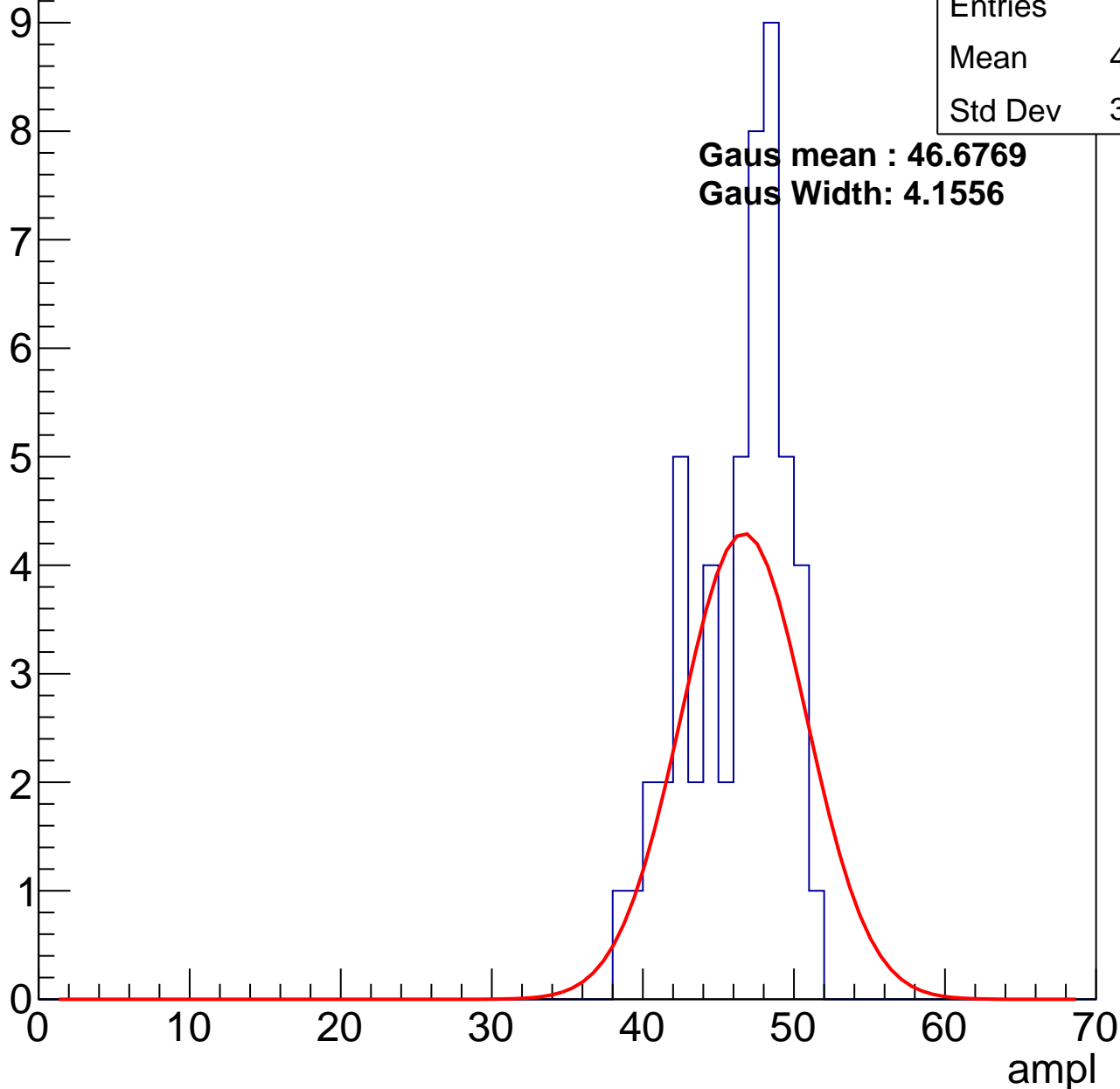
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.78
Std Dev	3.213

Gaus mean : 46.6769

Gaus Width: 4.1556

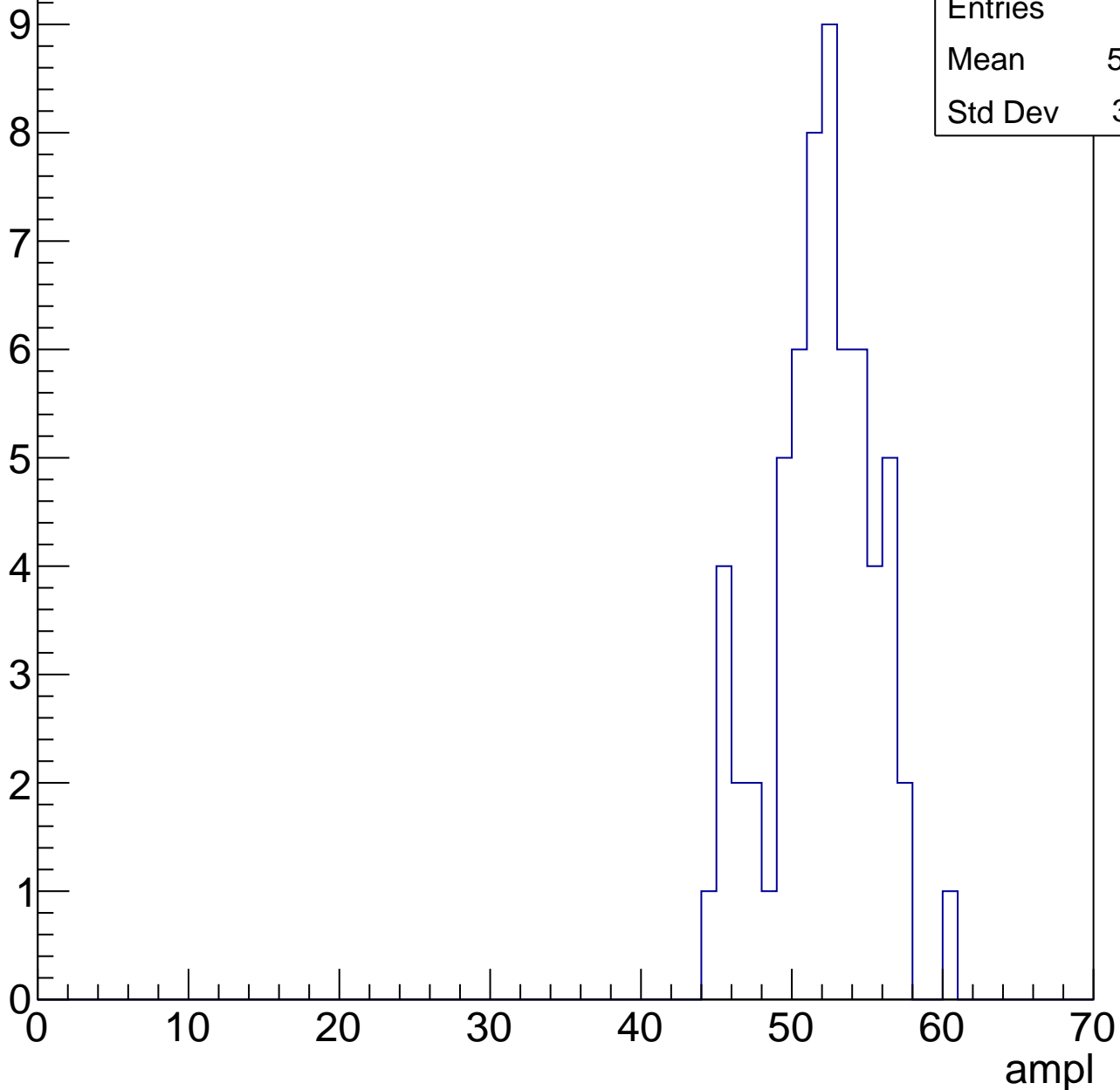


B0L002S, U26-ch41, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

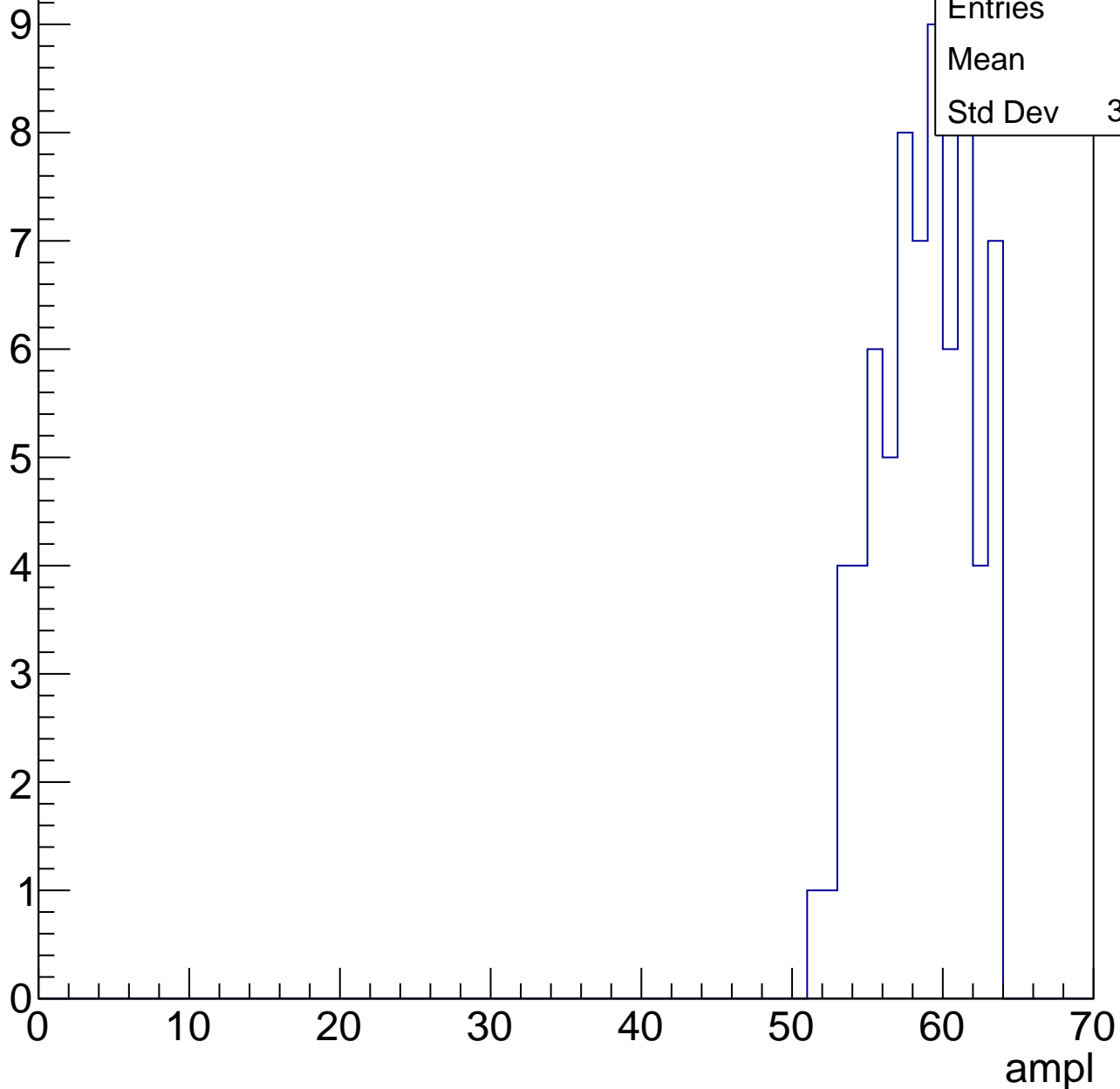
Entries	62
Mean	51.53
Std Dev	3.411



B0L002S, U26-ch41, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

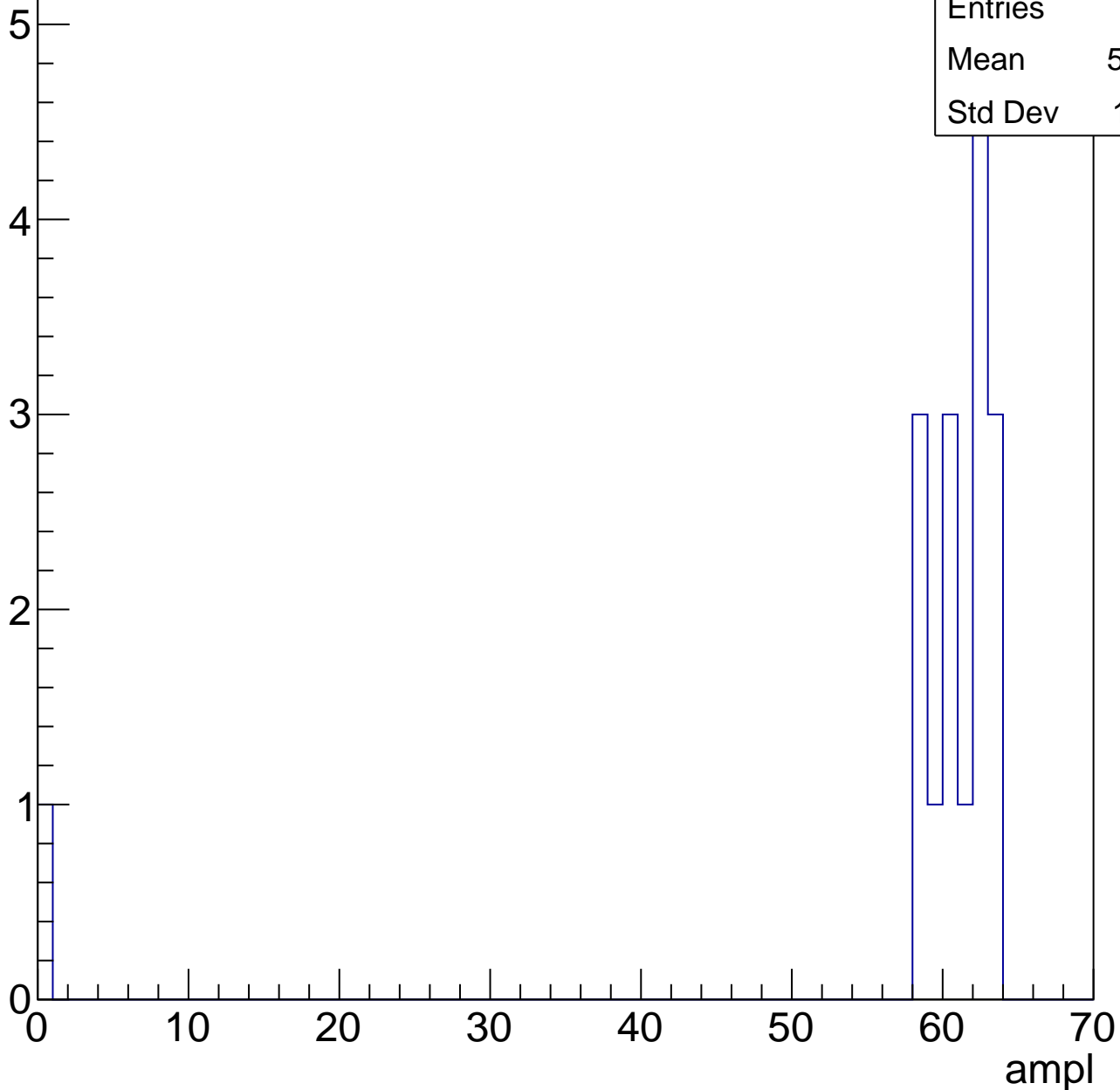


B0L002S, U26-ch41, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	57.24
Std Dev	14.41



B0L002S, U26-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch42, adc0

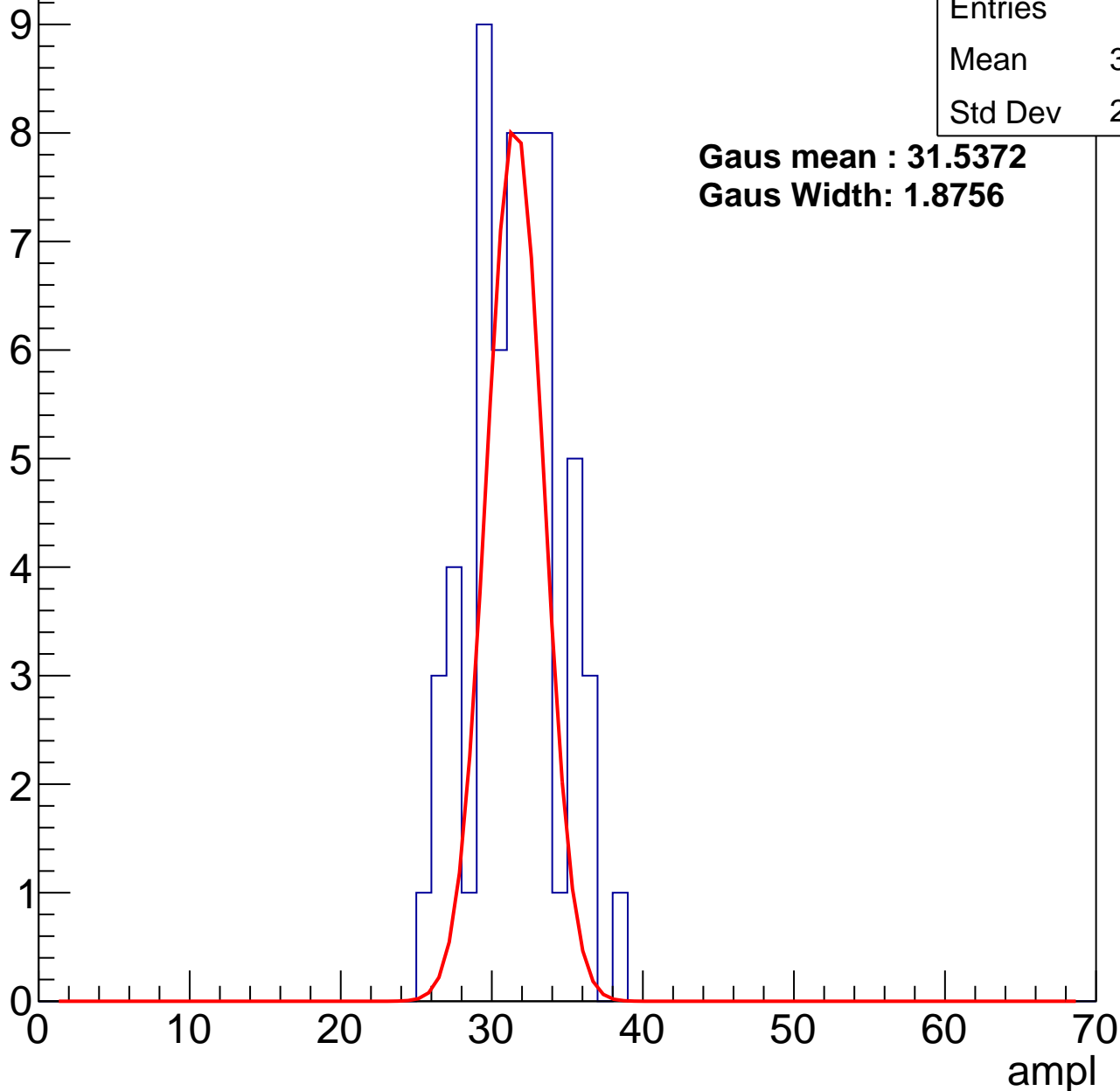
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.09
Std Dev	2.872

Gaus mean : 31.5372

Gaus Width: 1.8756



B0L002S, U26-ch42, adc1

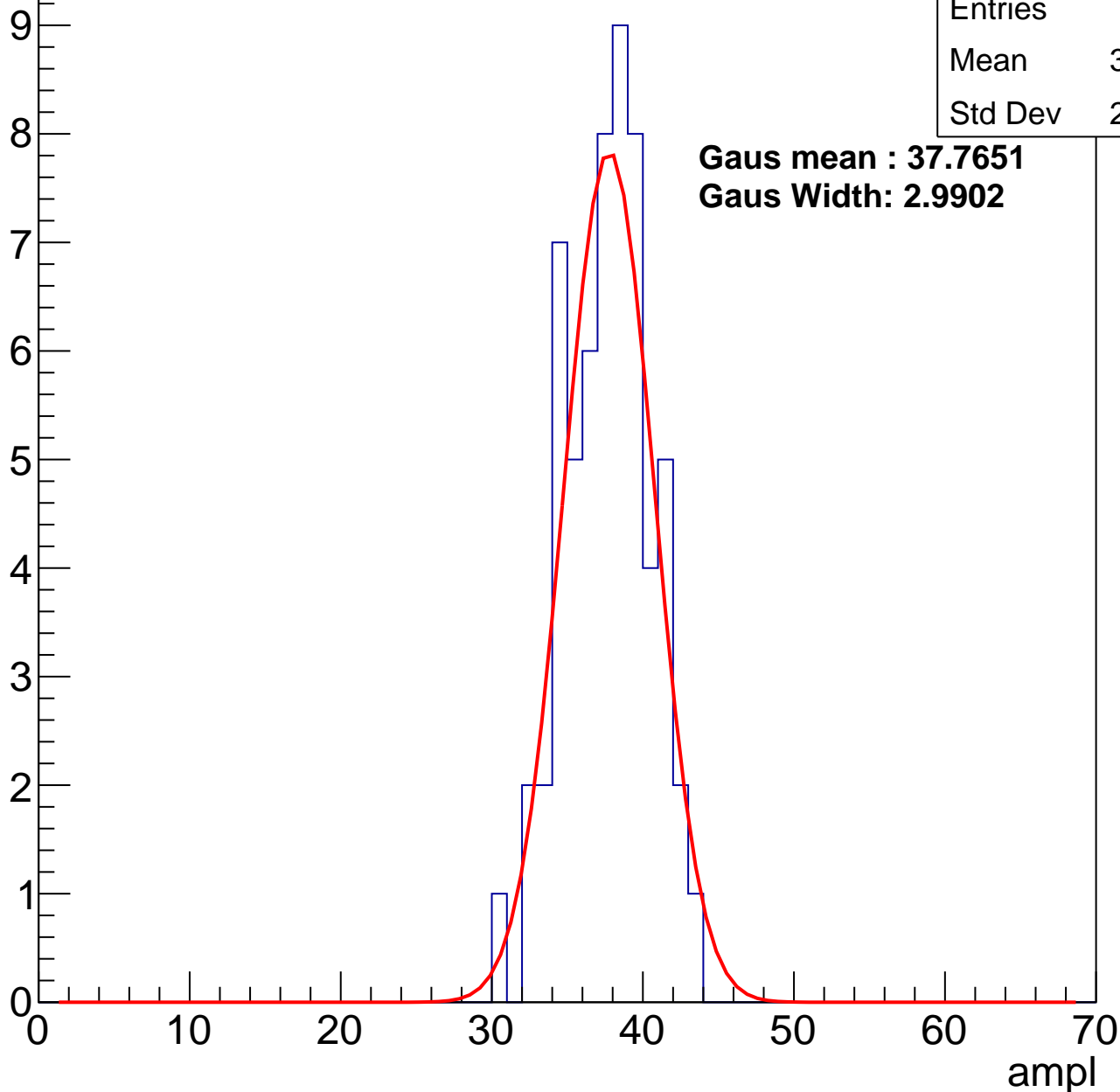
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.18
Std Dev	2.772

Gaus mean : 37.7651

Gaus Width: 2.9902



B0L002S, U26-ch42, adc2

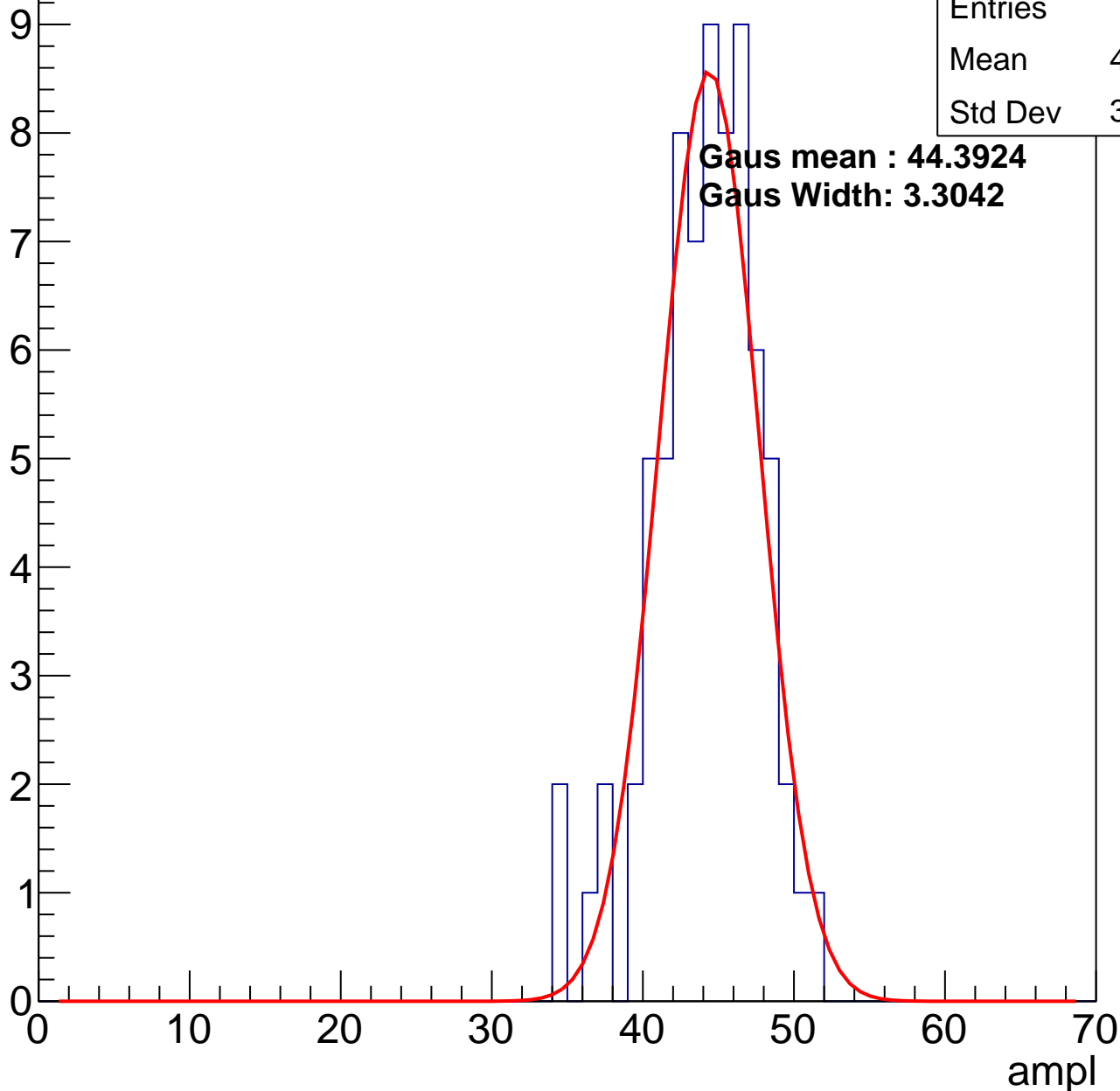
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	43.68
Std Dev	3.475

Gaus mean : 44.3924

Gaus Width: 3.3042

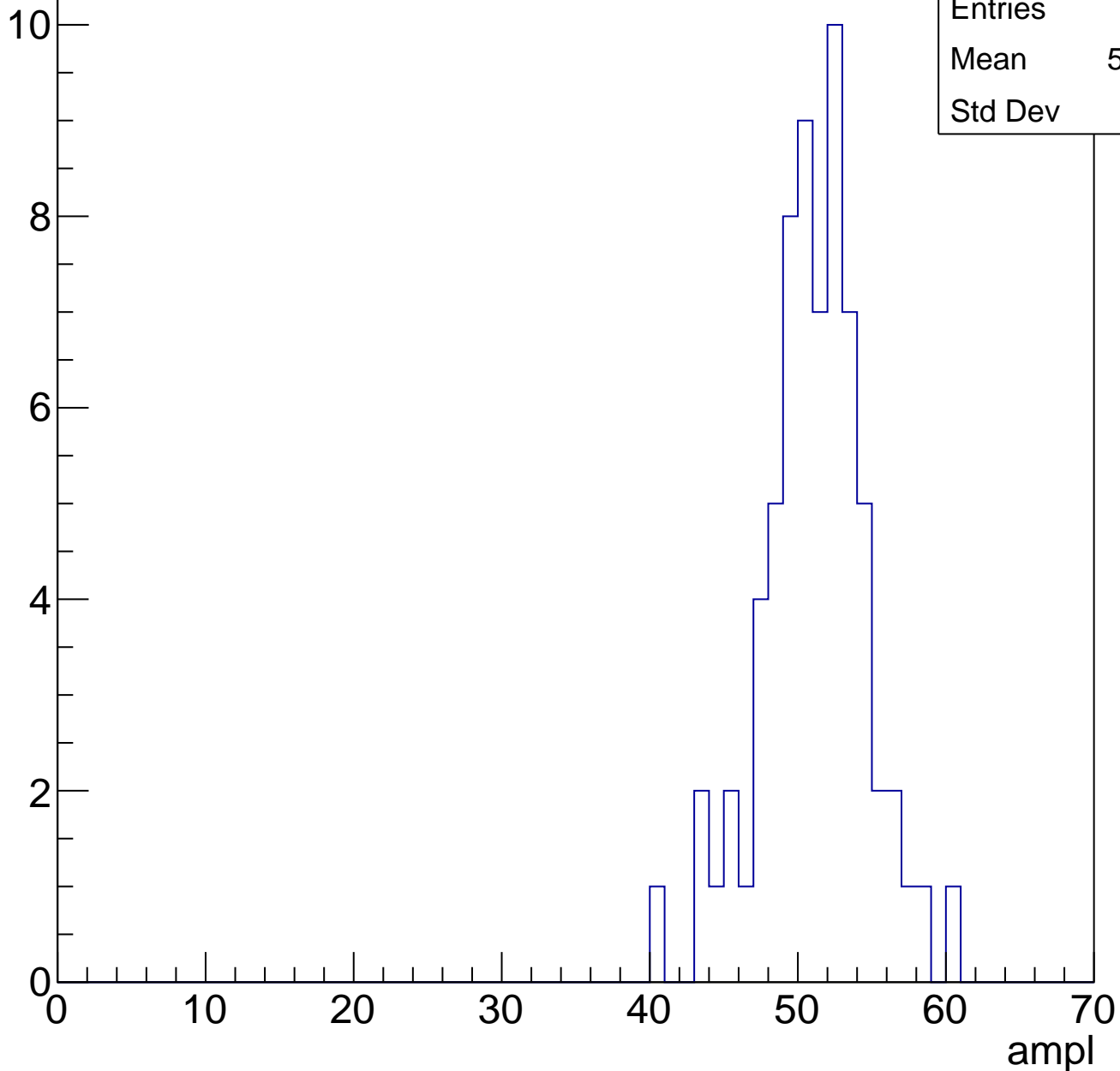


B0L002S, U26-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	50.59
Std Dev	3.54

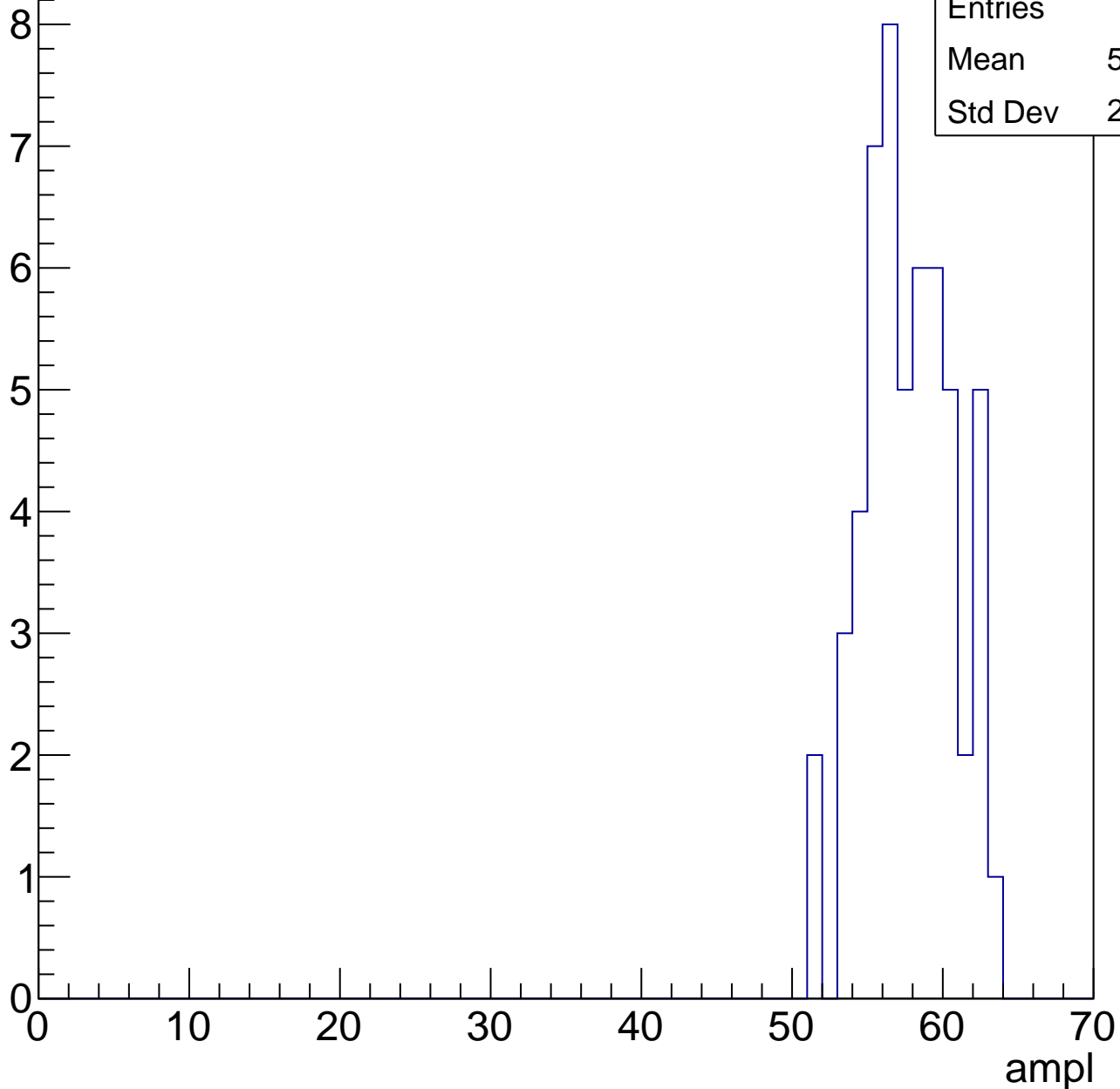
Entry



B0L002S, U26-ch42, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



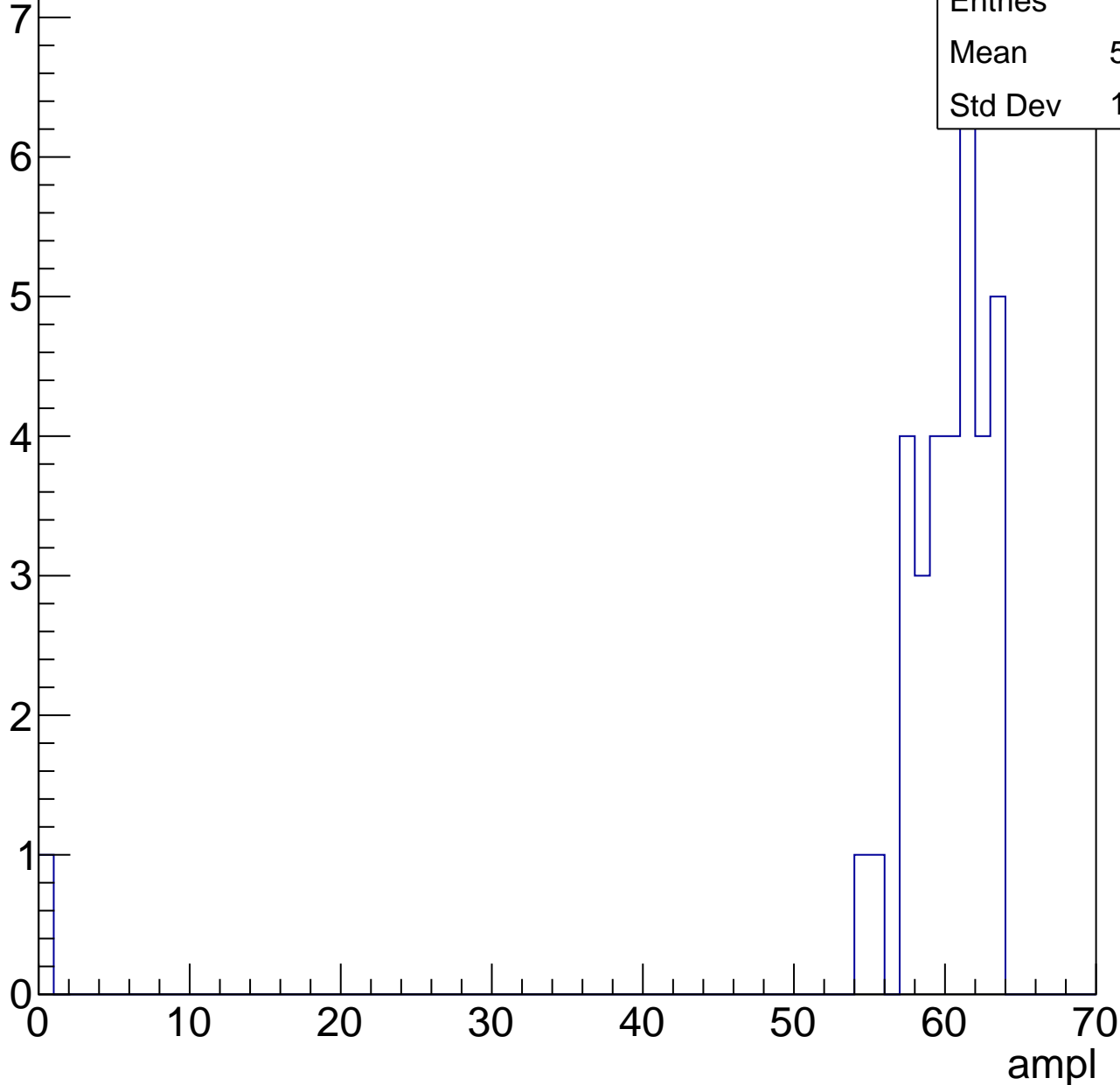
Entries	54
Mean	57.26
Std Dev	2.907

B0L002S, U26-ch42, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.15
Std Dev	10.38



B0L002S, U26-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch43, adc0

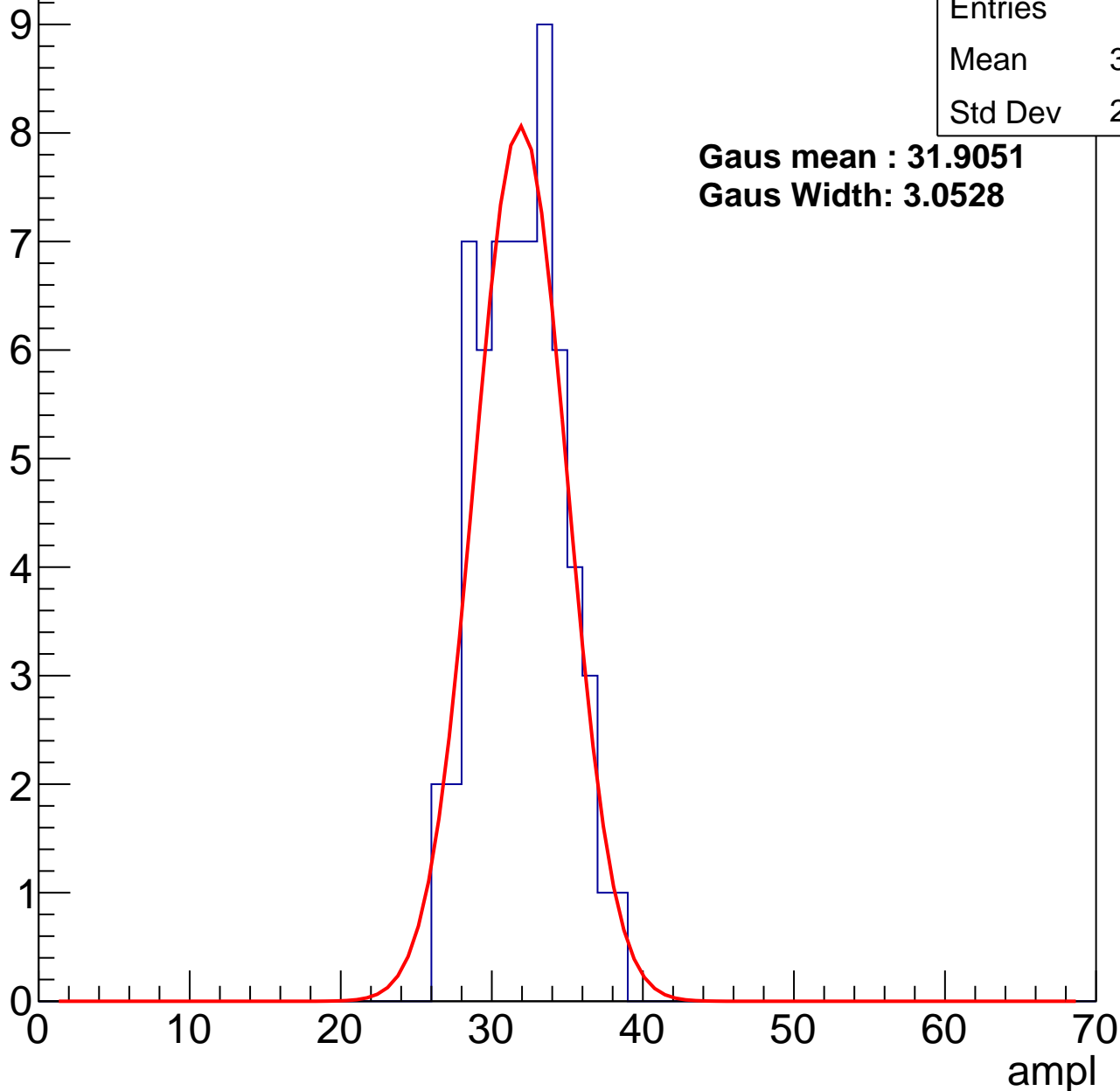
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.47
Std Dev	2.792

Gaus mean : 31.9051

Gaus Width: 3.0528



B0L002S, U26-ch43, adc1

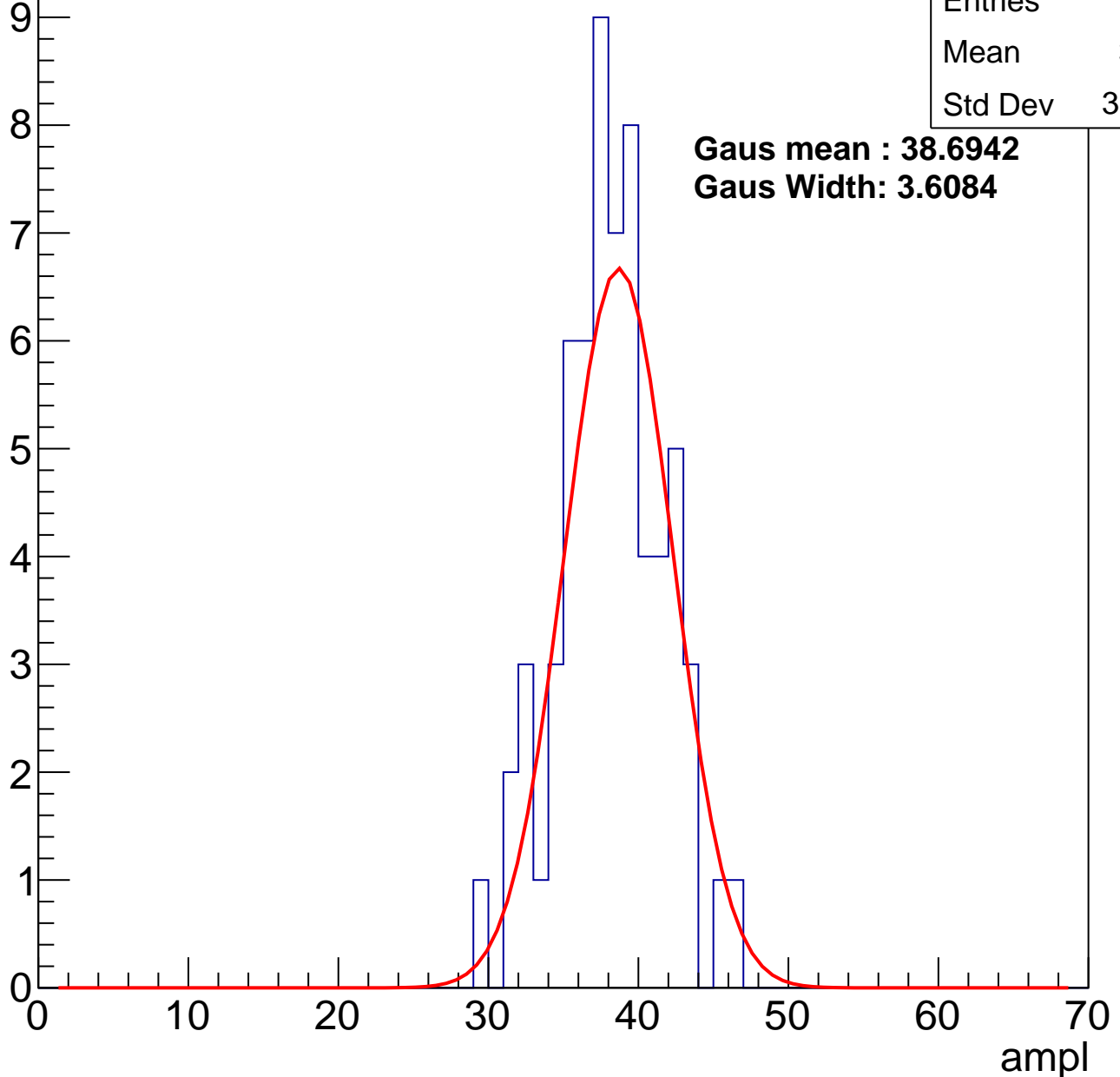
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.7
Std Dev	3.467

Gaus mean : 38.6942

Gaus Width: 3.6084



B0L002S, U26-ch43, adc2

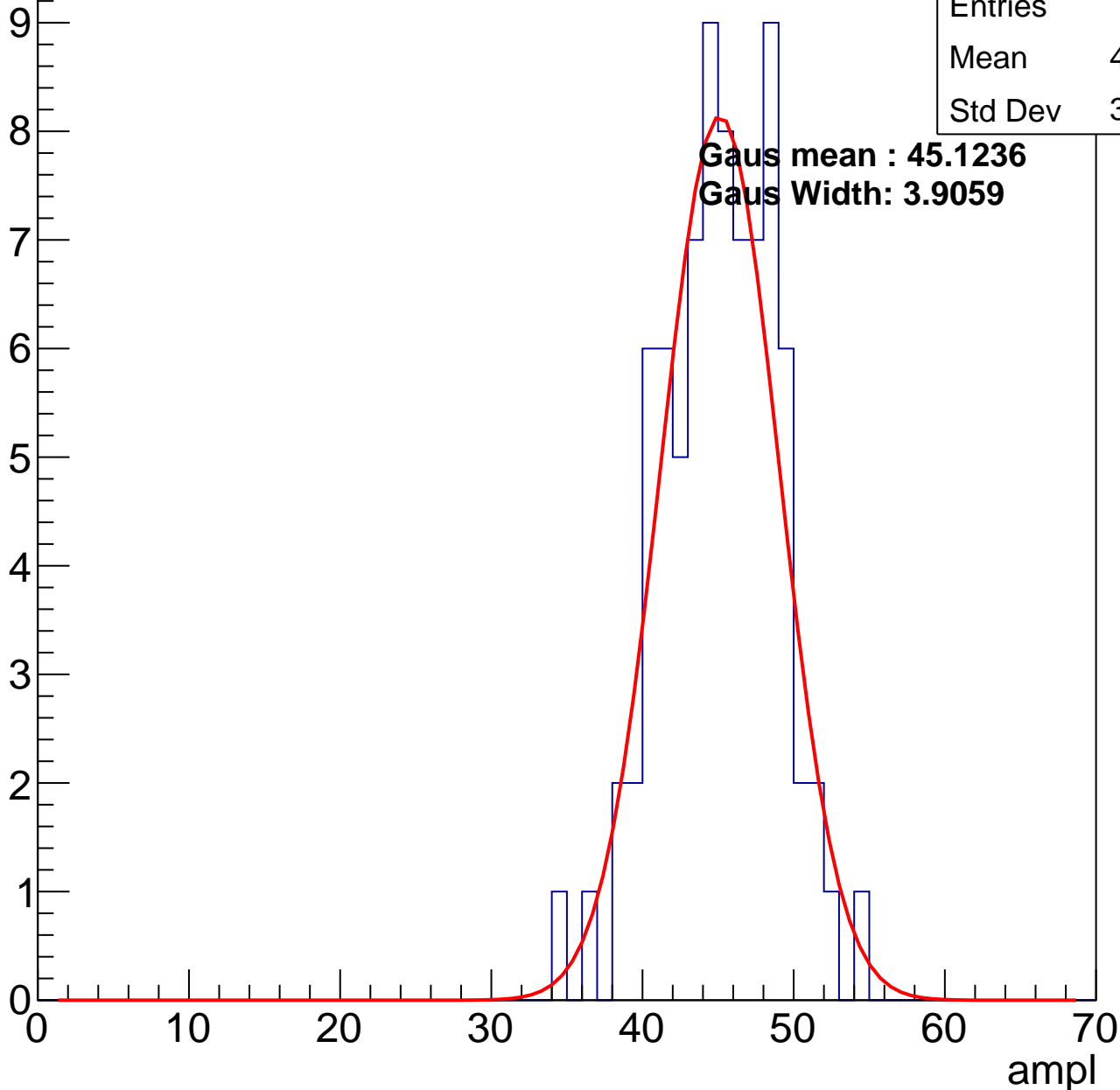
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	44.66
Std Dev	3.752

Gaus mean : 45.1236

Gaus Width: 3.9059



B0L002S, U26-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1

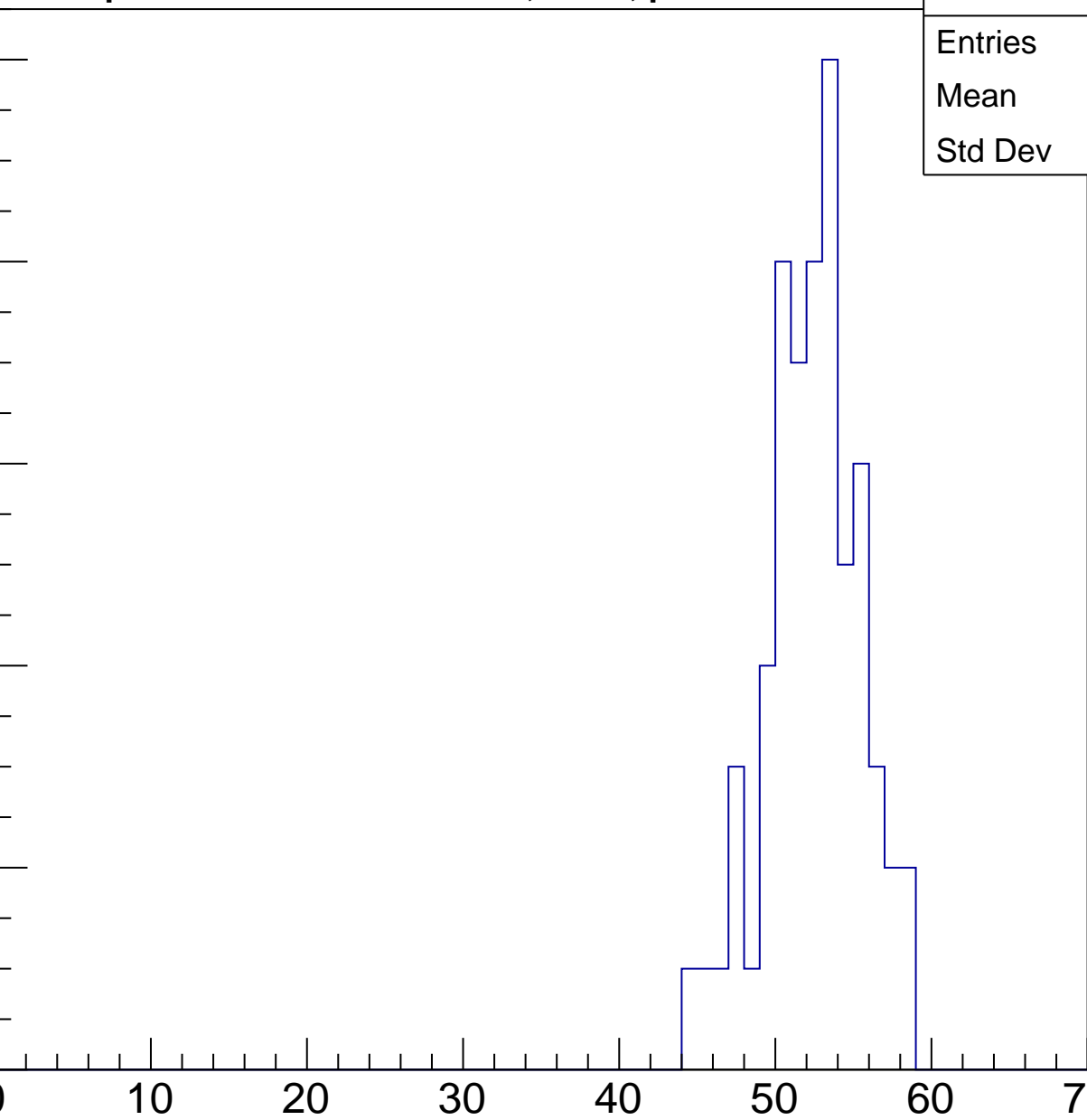
Entries	62
Mean	51.95
Std Dev	3.029

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

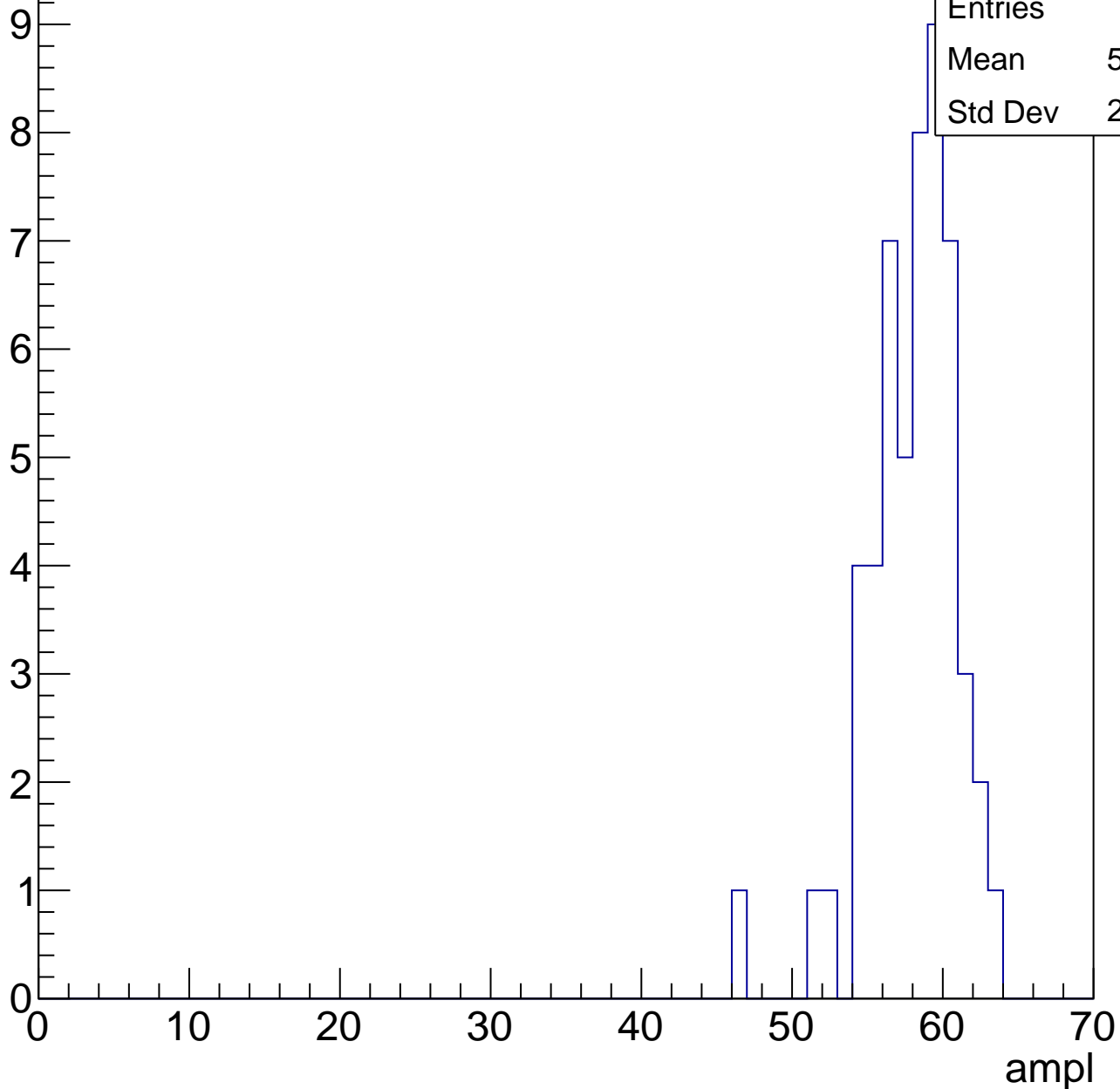


B0L002S, U26-ch43, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	57.49
Std Dev	2.976

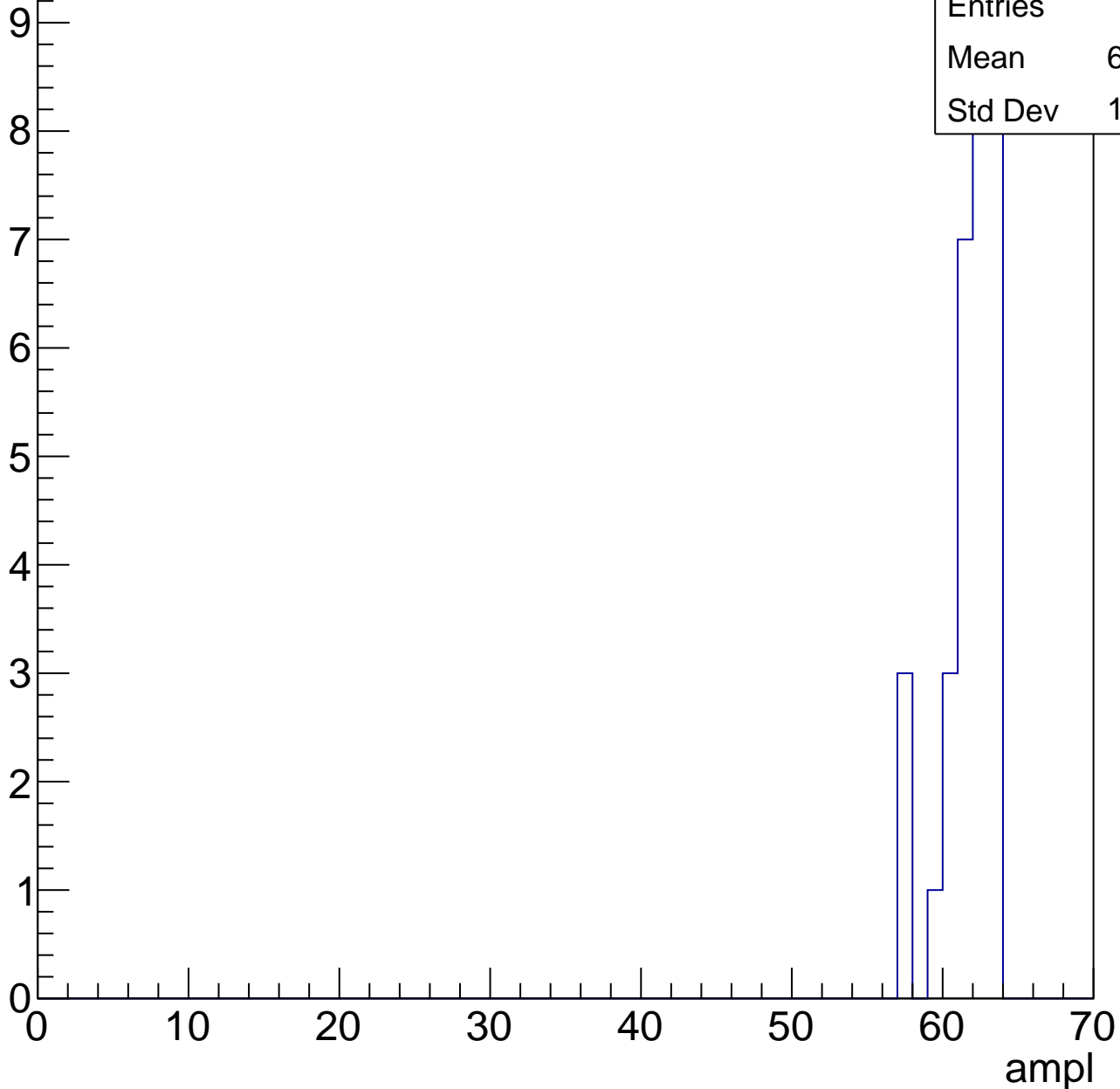


B0L002S, U26-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	61.29
Std Dev	1.763



B0L002S, U26-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

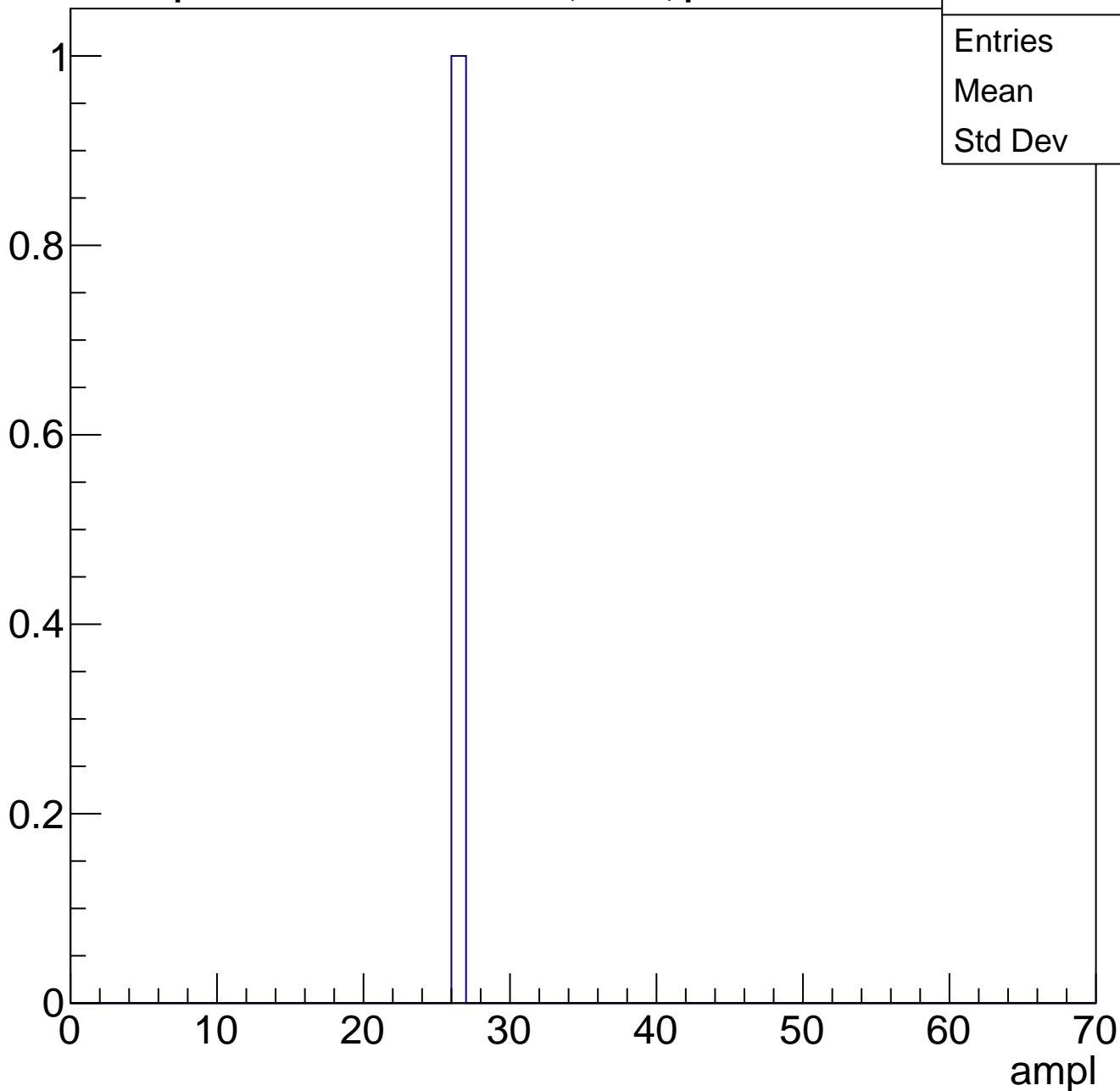
Entry



B0L002S, U26-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	26
Std Dev	0

B0L002S, U26-ch44, adc0

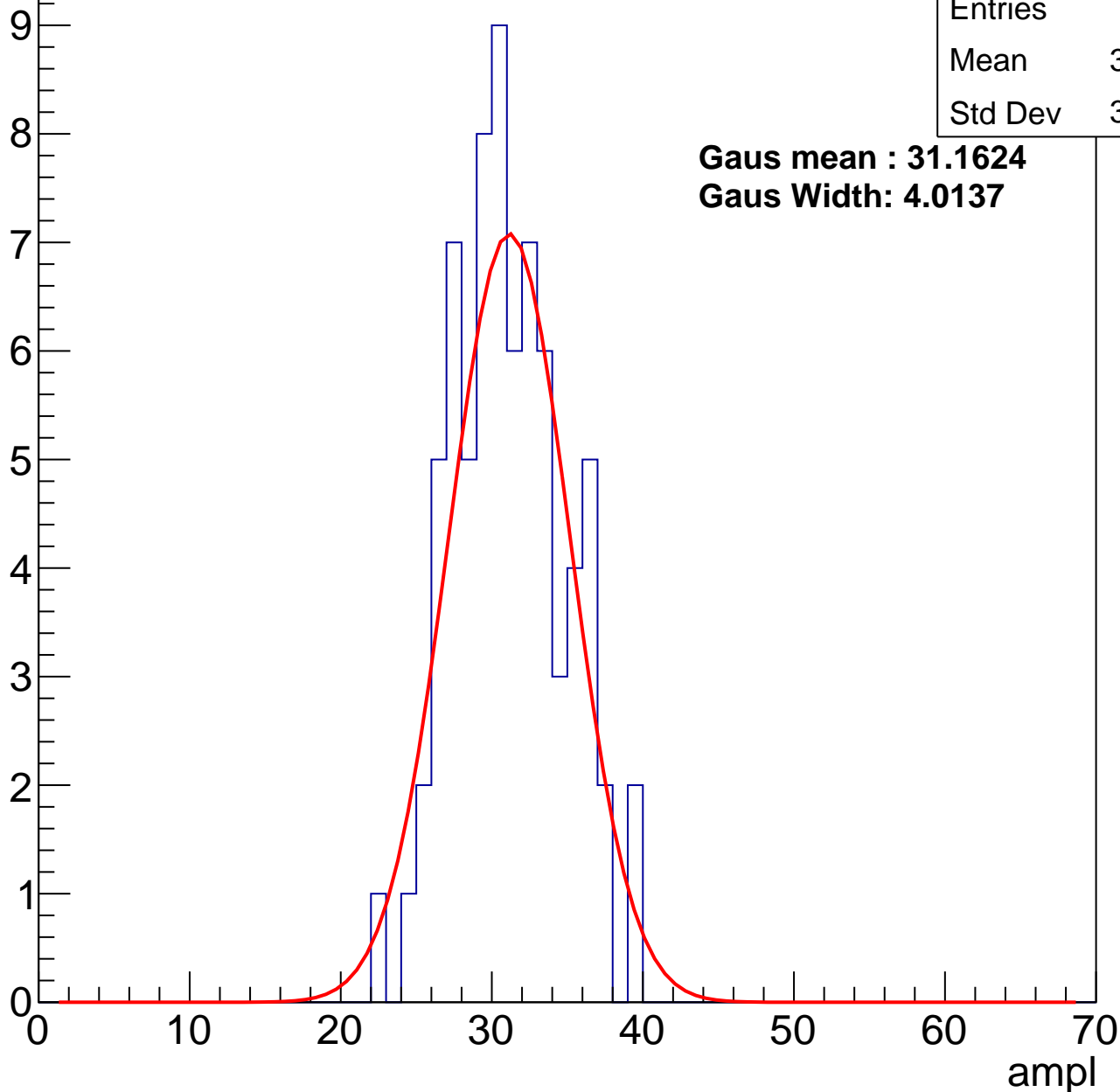
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.67
Std Dev	3.634

Gaus mean : 31.1624

Gaus Width: 4.0137



B0L002S, U26-ch44, adc1

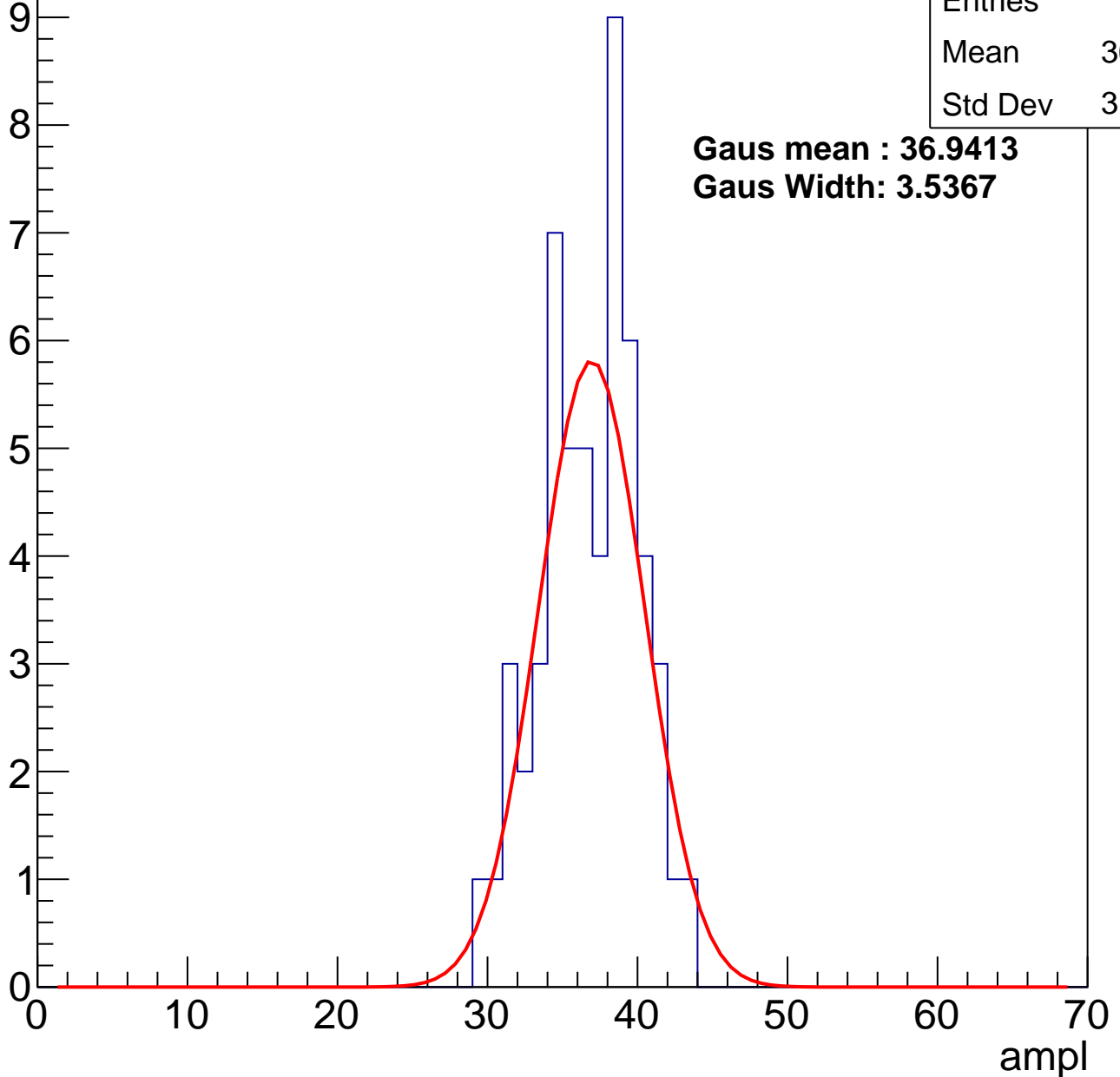
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.36
Std Dev	3.199

Gaus mean : 36.9413

Gaus Width: 3.5367



B0L002S, U26-ch44, adc2

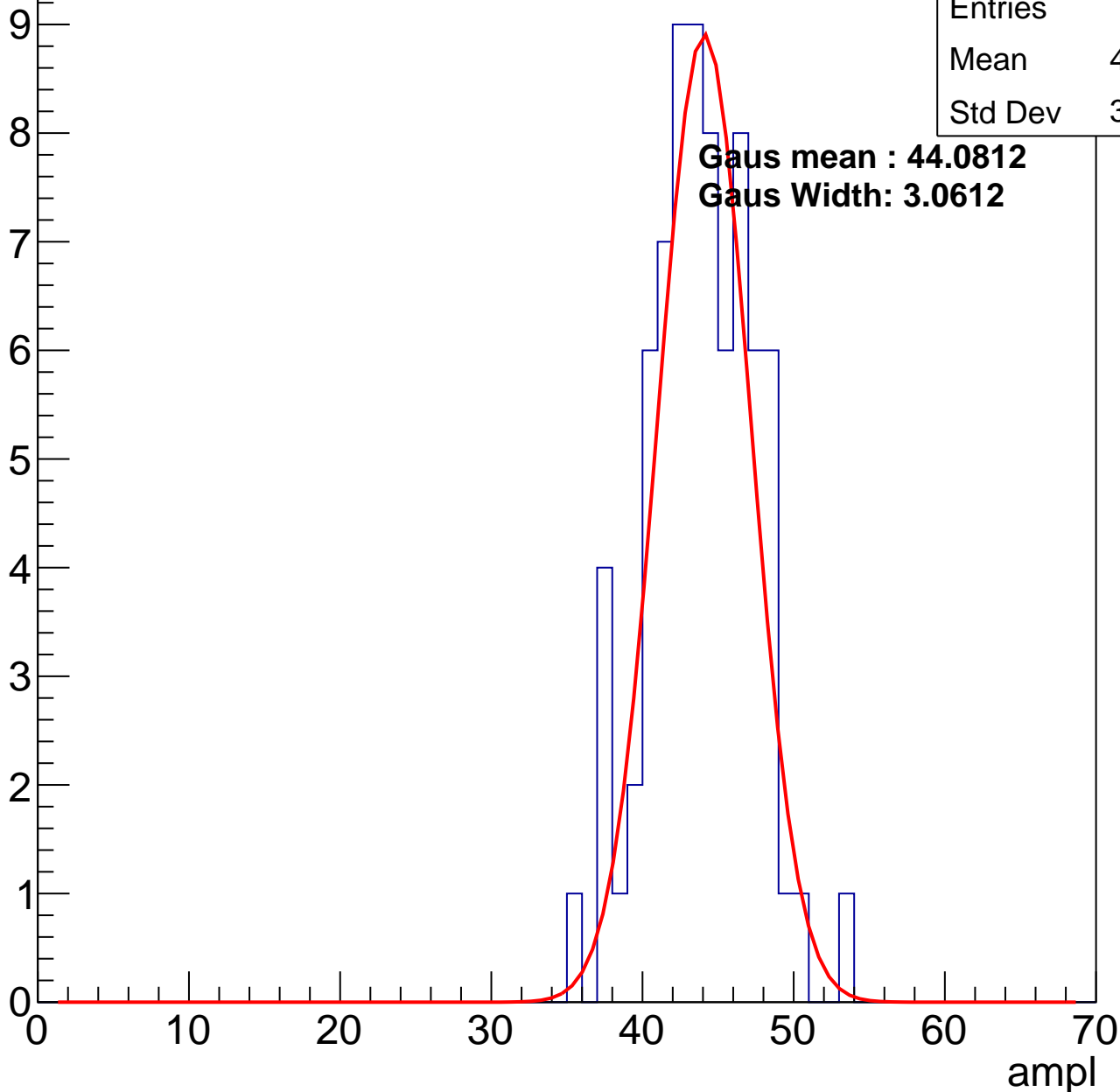
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	43.46
Std Dev	3.393

Gaus mean : 44.0812

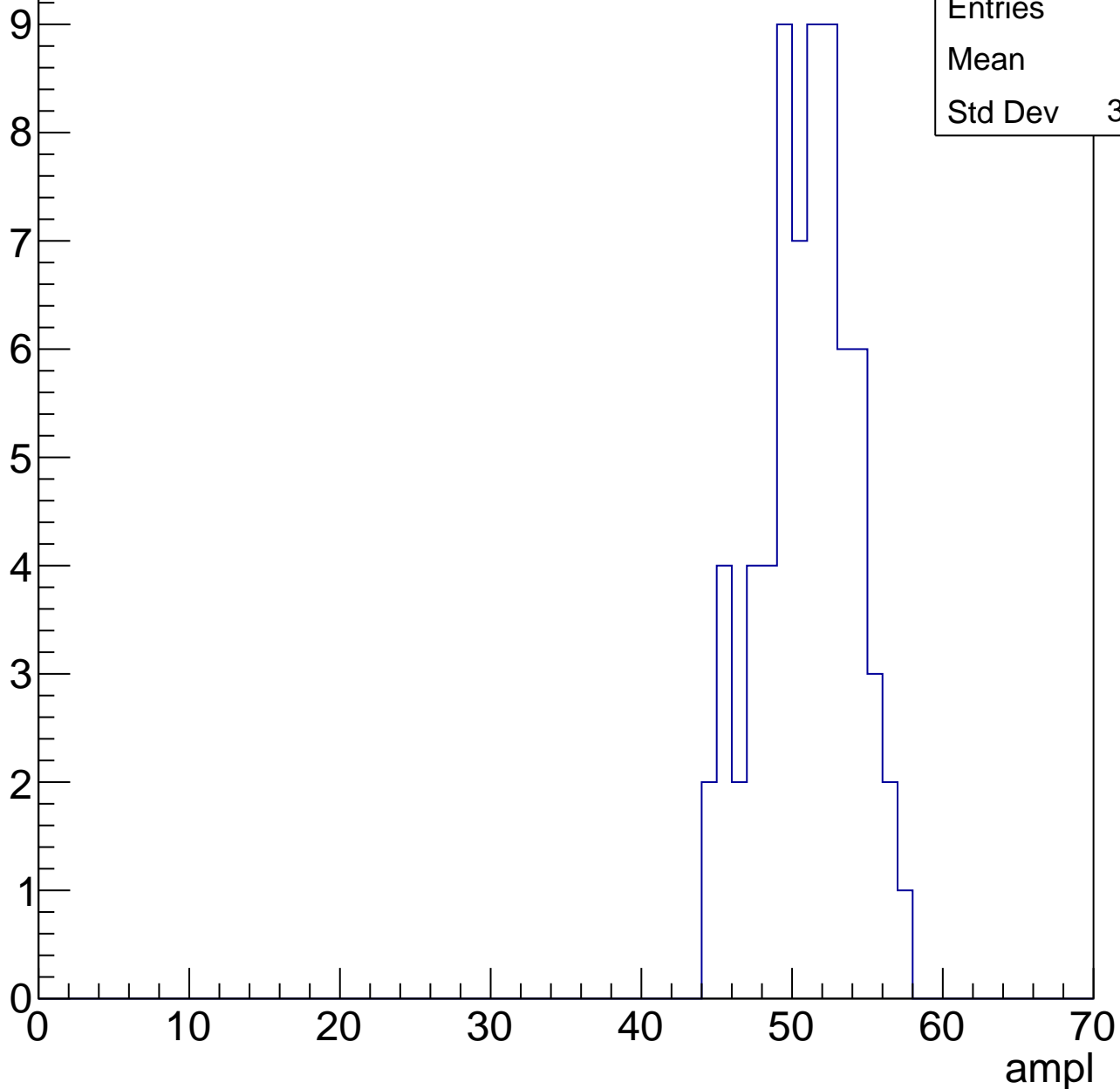
Gaus Width: 3.0612



B0L002S, U26-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



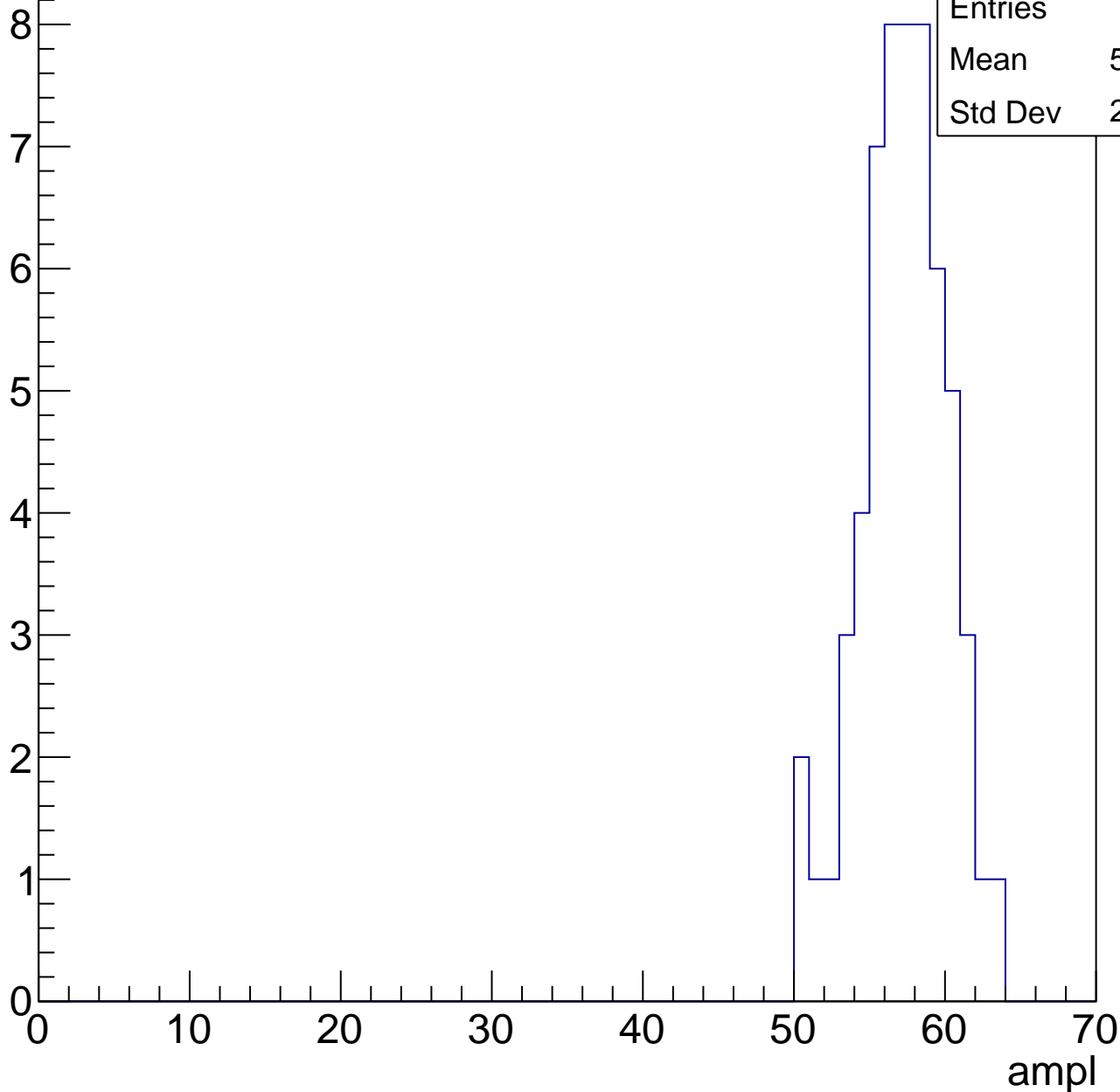
Entries	68
Mean	50.5
Std Dev	3.065

B0L002S, U26-ch44, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.78
Std Dev	2.823

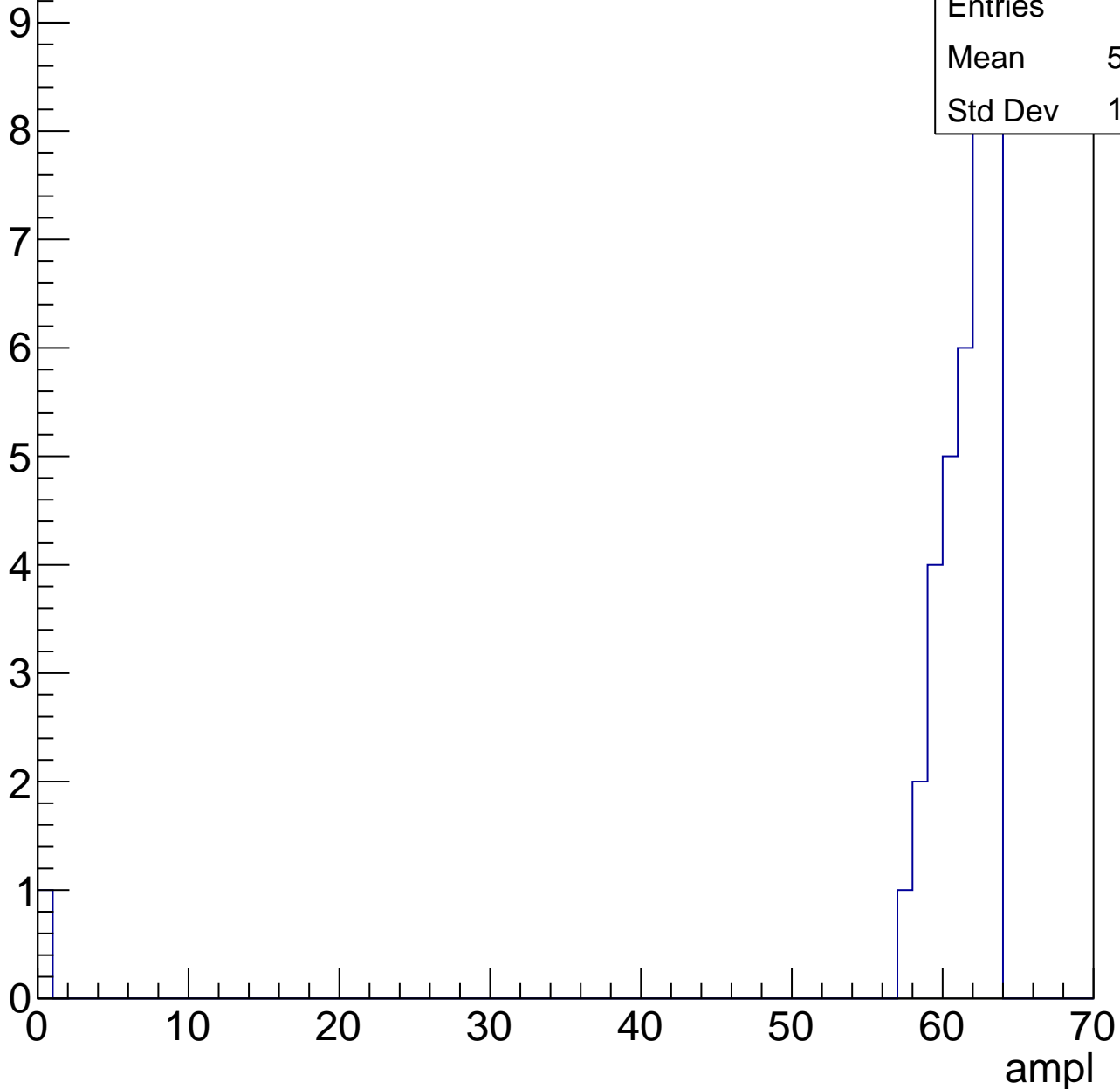


B0L002S, U26-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.36
Std Dev	10.17



B0L002S, U26-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch45, adc0

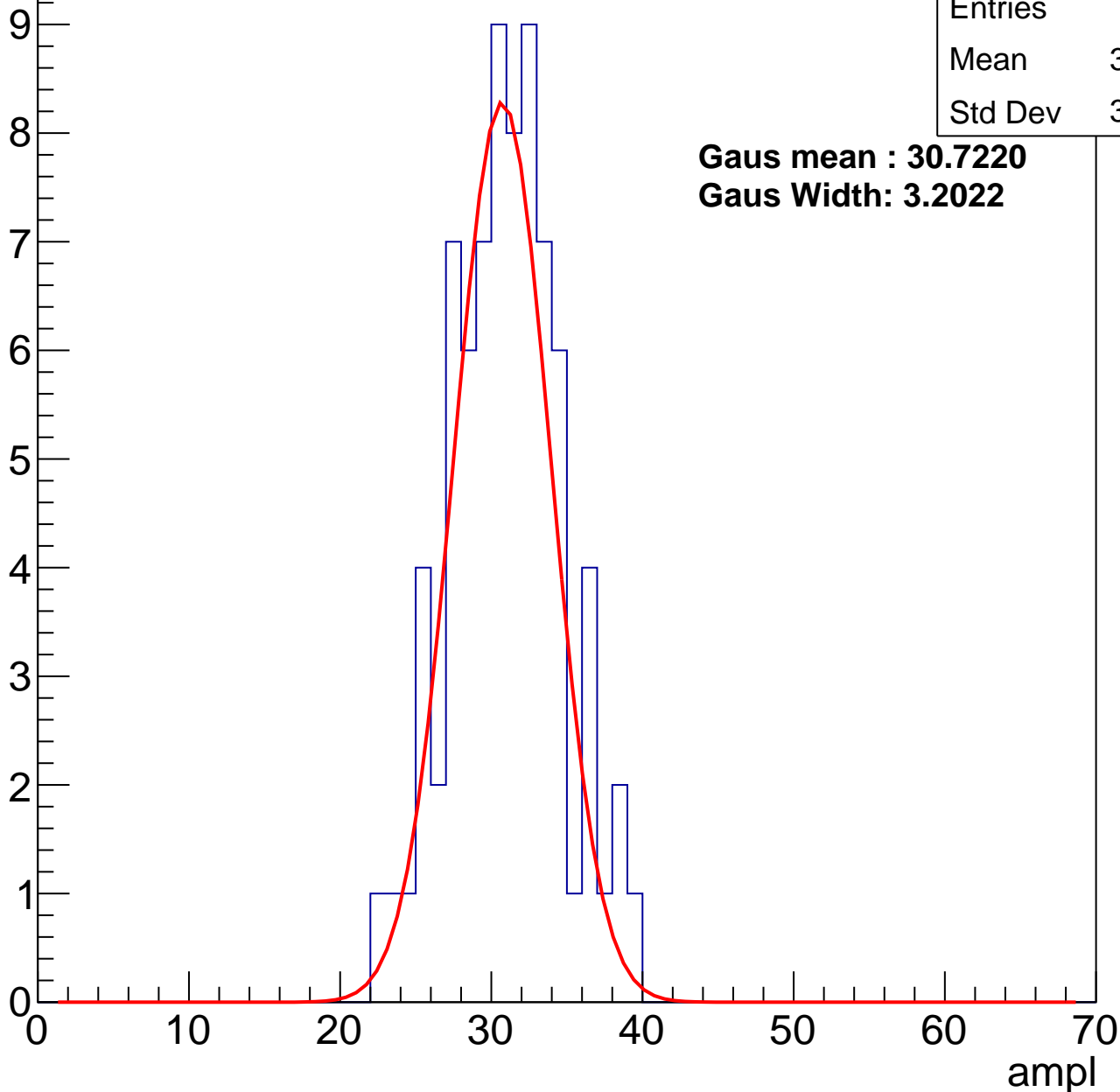
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.56
Std Dev	3.569

Gaus mean : 30.7220

Gaus Width: 3.2022



B0L002S, U26-ch45, adc1

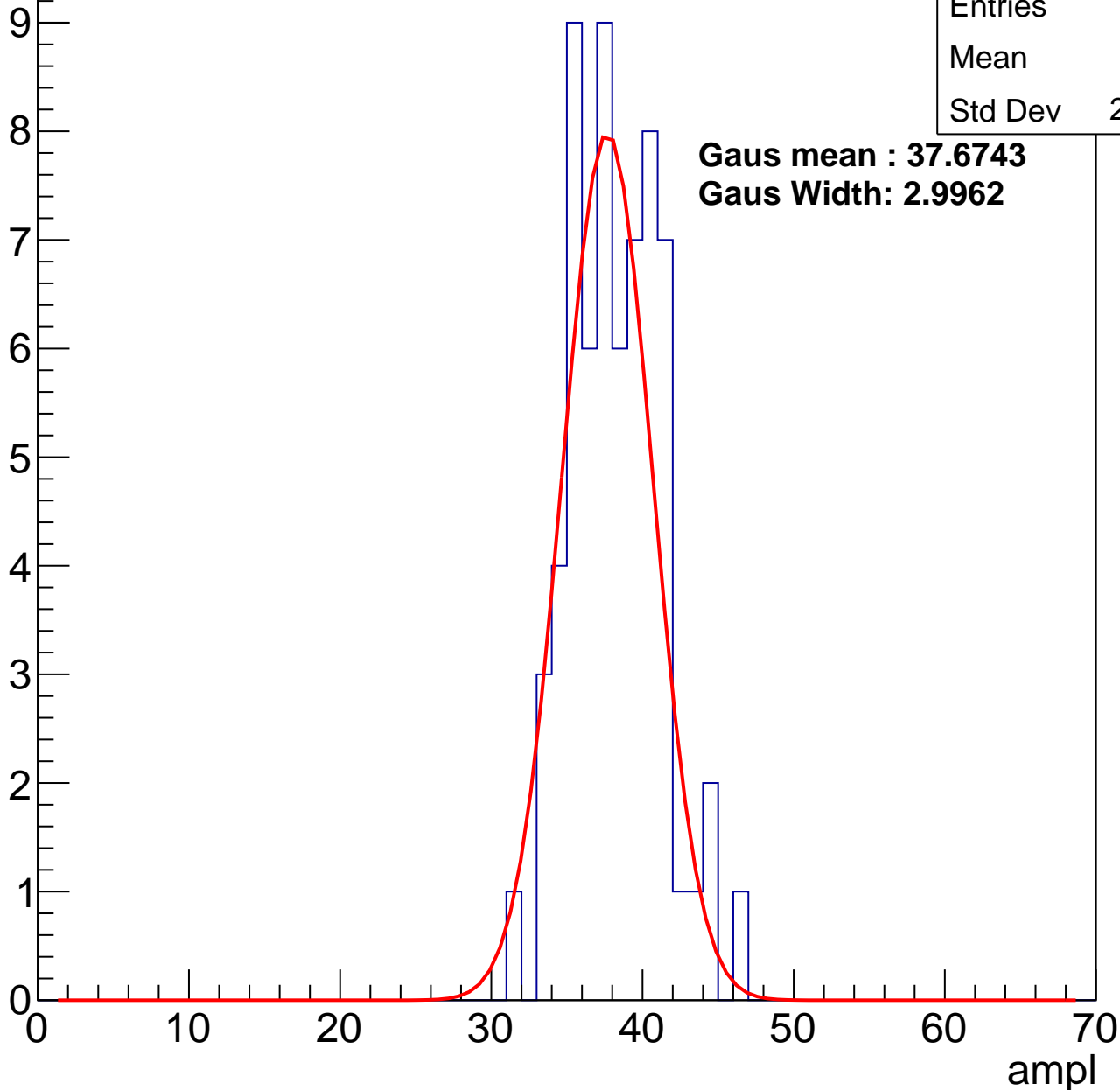
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.8
Std Dev	2.983

Gaus mean : 37.6743

Gaus Width: 2.9962



B0L002S, U26-ch45, adc2

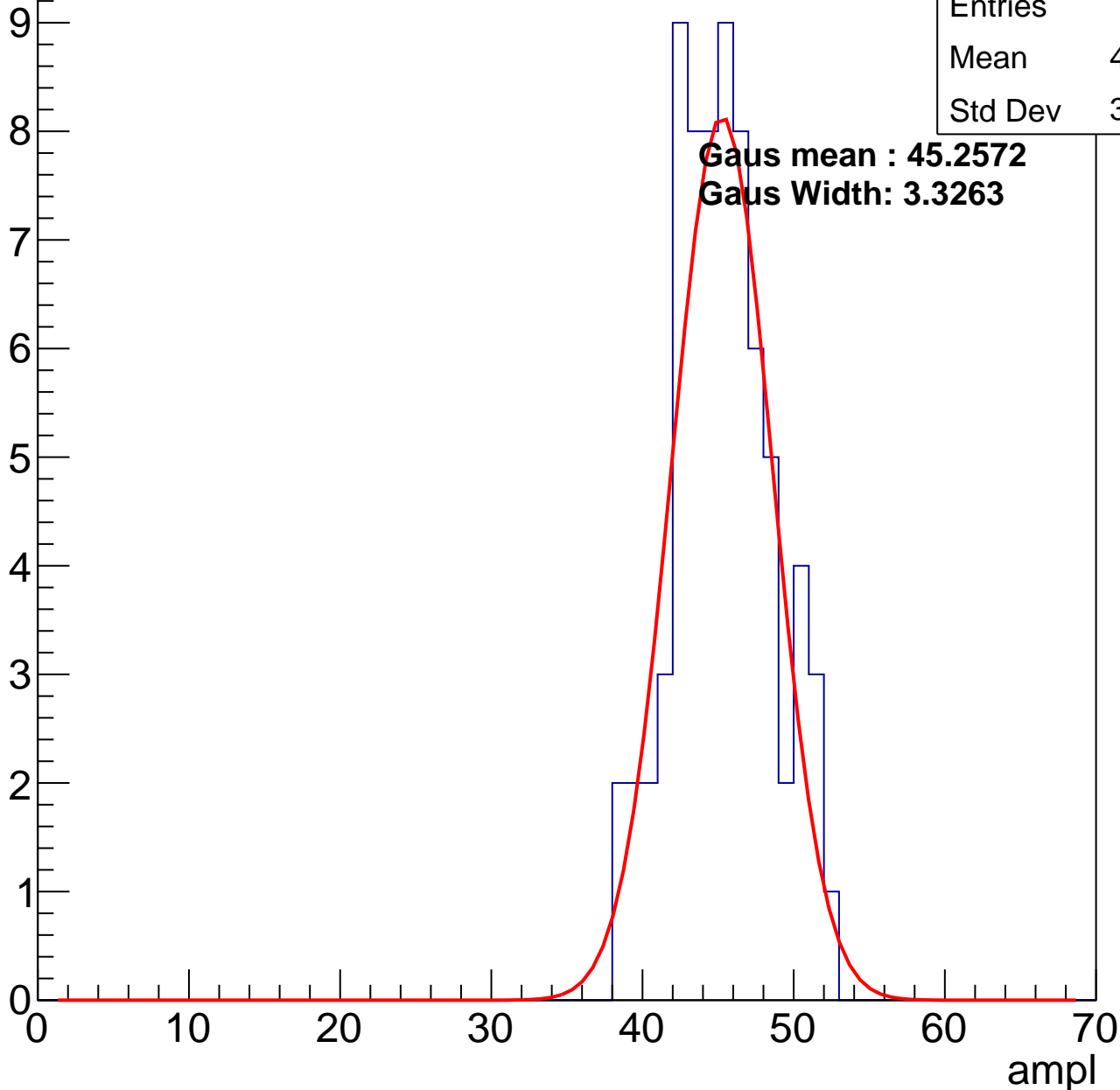
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.85
Std Dev	3.235

Gaus mean : 45.2572

Gaus Width: 3.3263

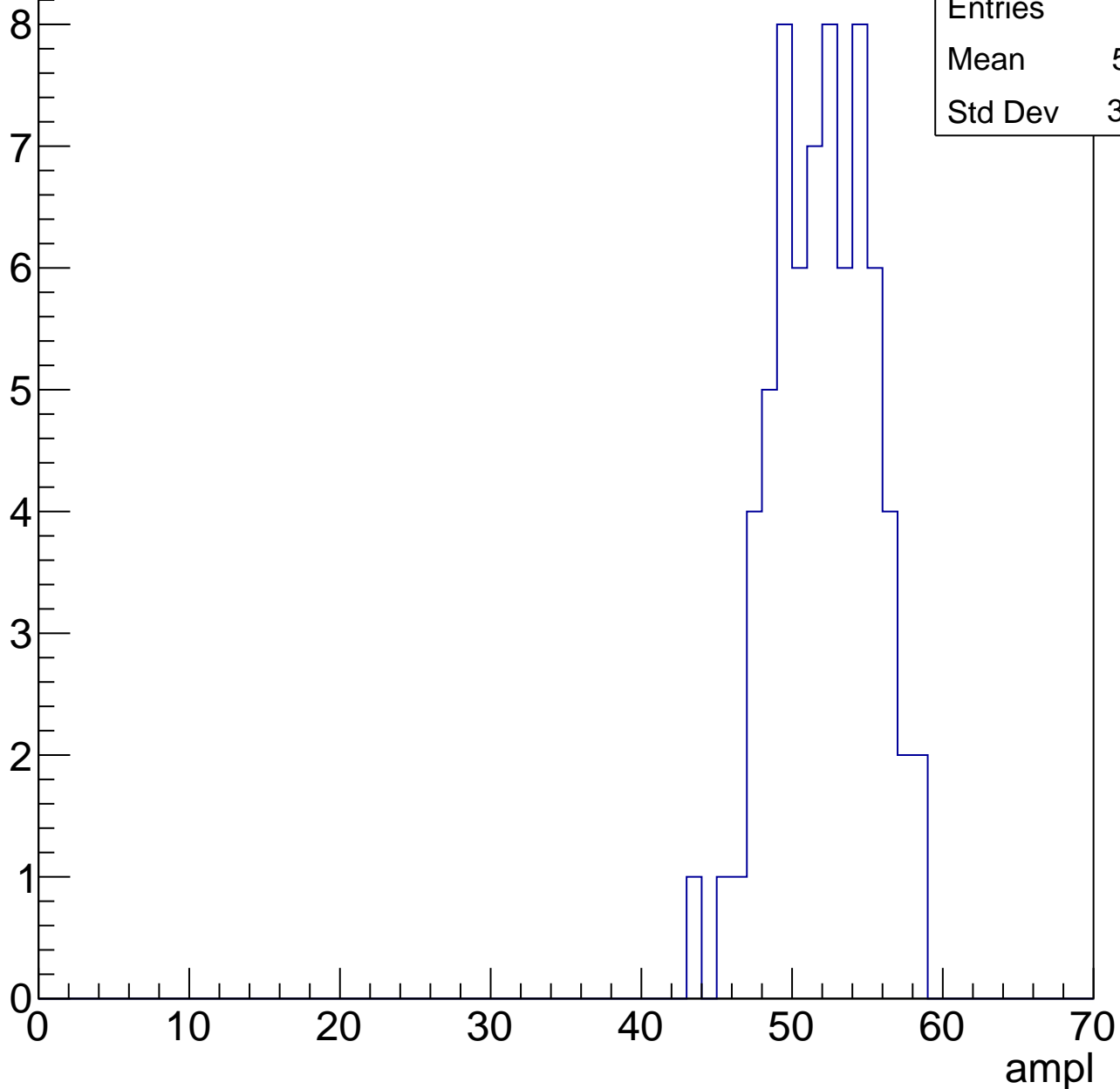


B0L002S, U26-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	51.61
Std Dev	3.222

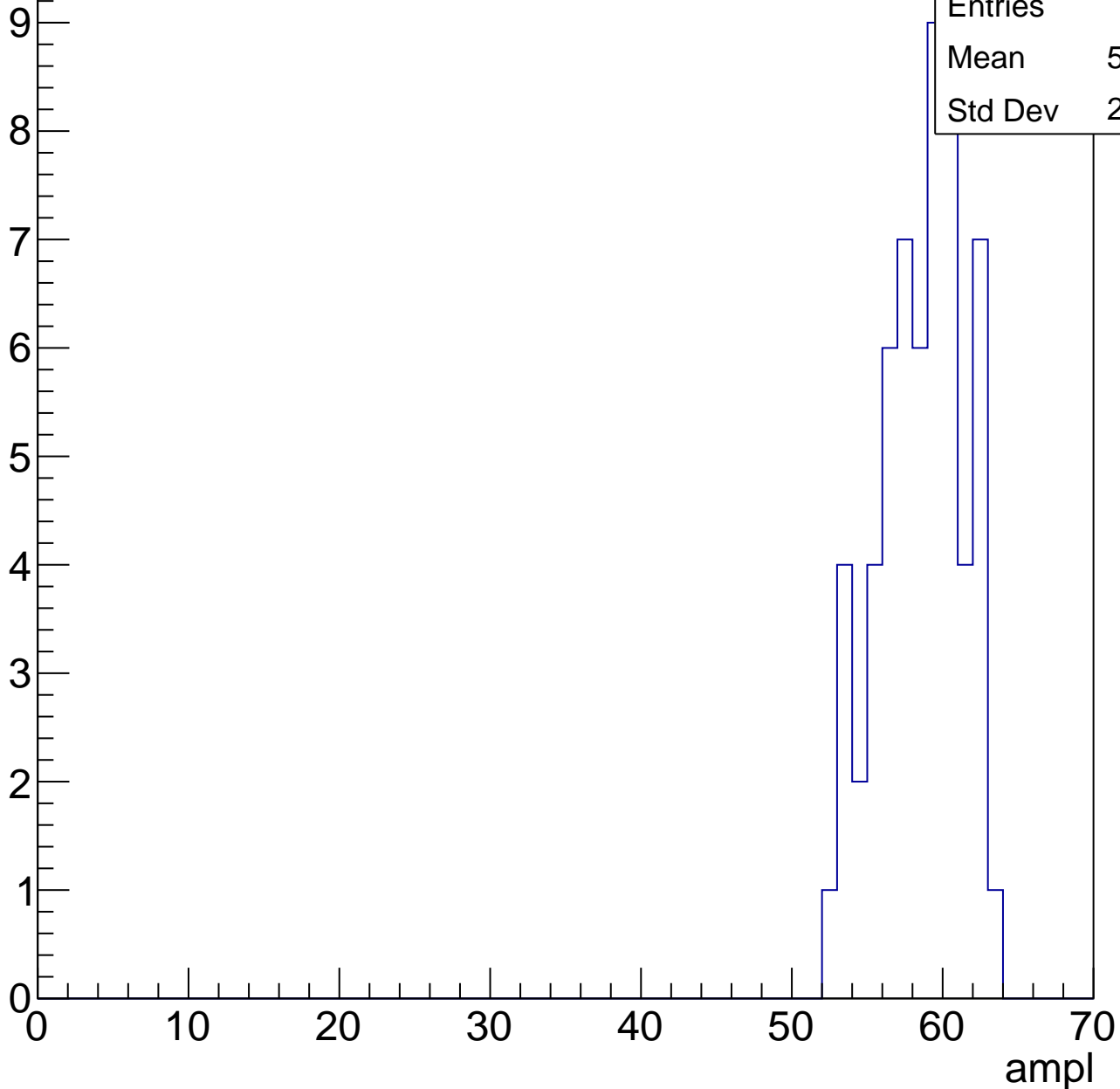


B0L002S, U26-ch45, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	58.12
Std Dev	2.745

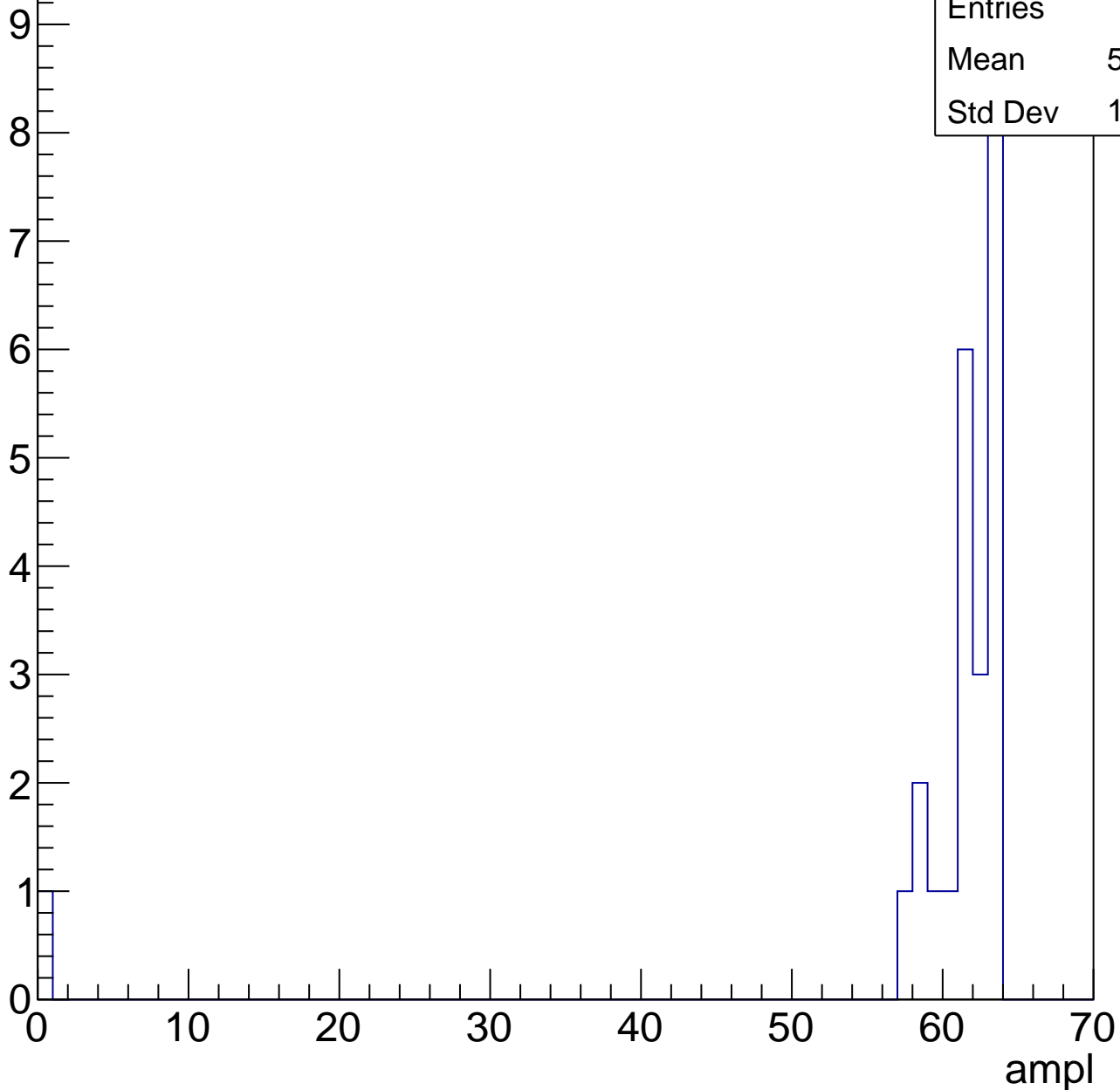


B0L002S, U26-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.79
Std Dev	12.39



B0L002S, U26-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch46, adc0

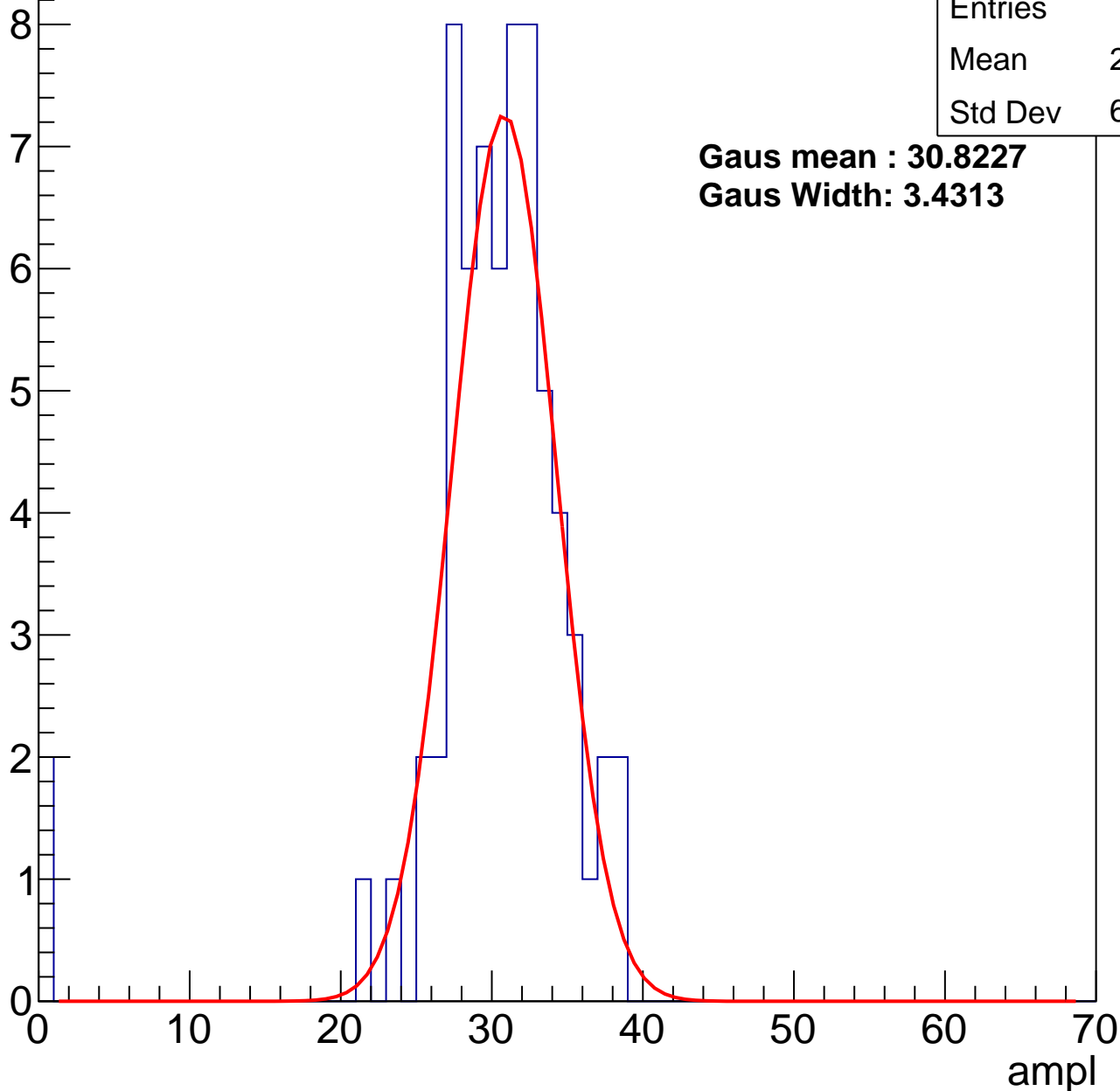
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.54
Std Dev	6.168

Gaus mean : 30.8227

Gaus Width: 3.4313



B0L002S, U26-ch46, adc1

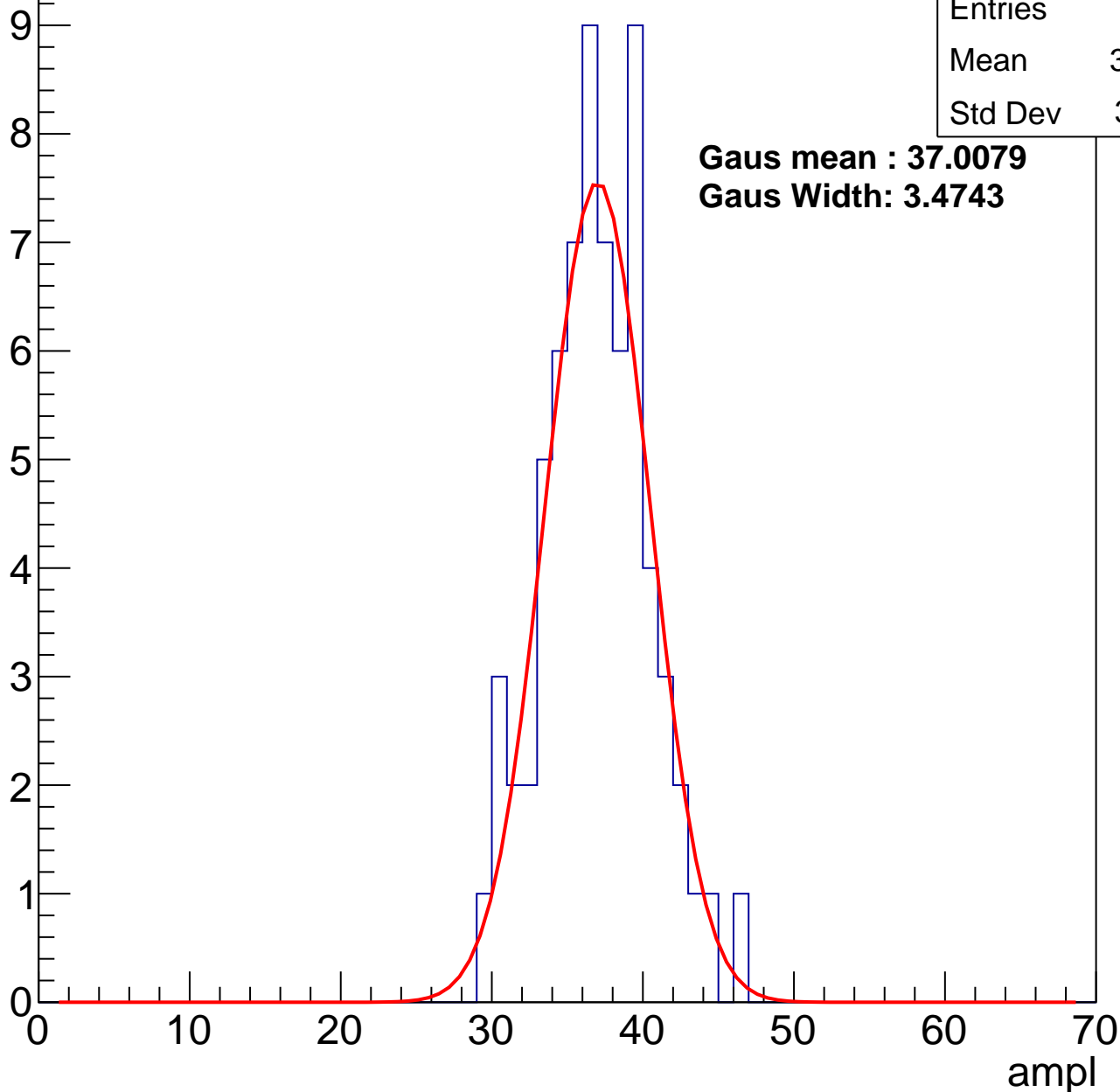
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.54
Std Dev	3.471

Gaus mean : 37.0079

Gaus Width: 3.4743



B0L002S, U26-ch46, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	43.77
Std Dev	3.059

Gaus mean : 44.1247

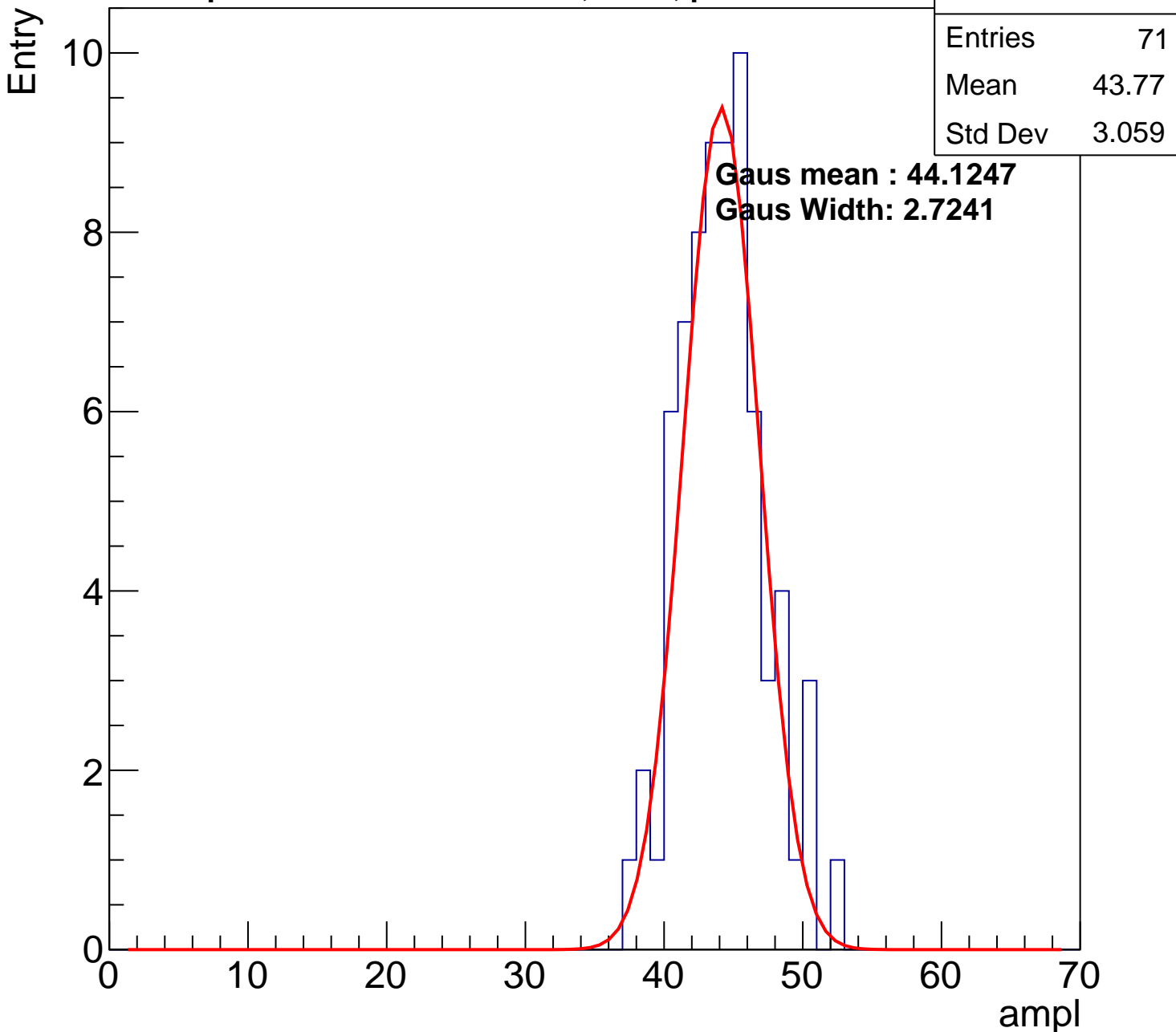
Gaus Width: 2.7241

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

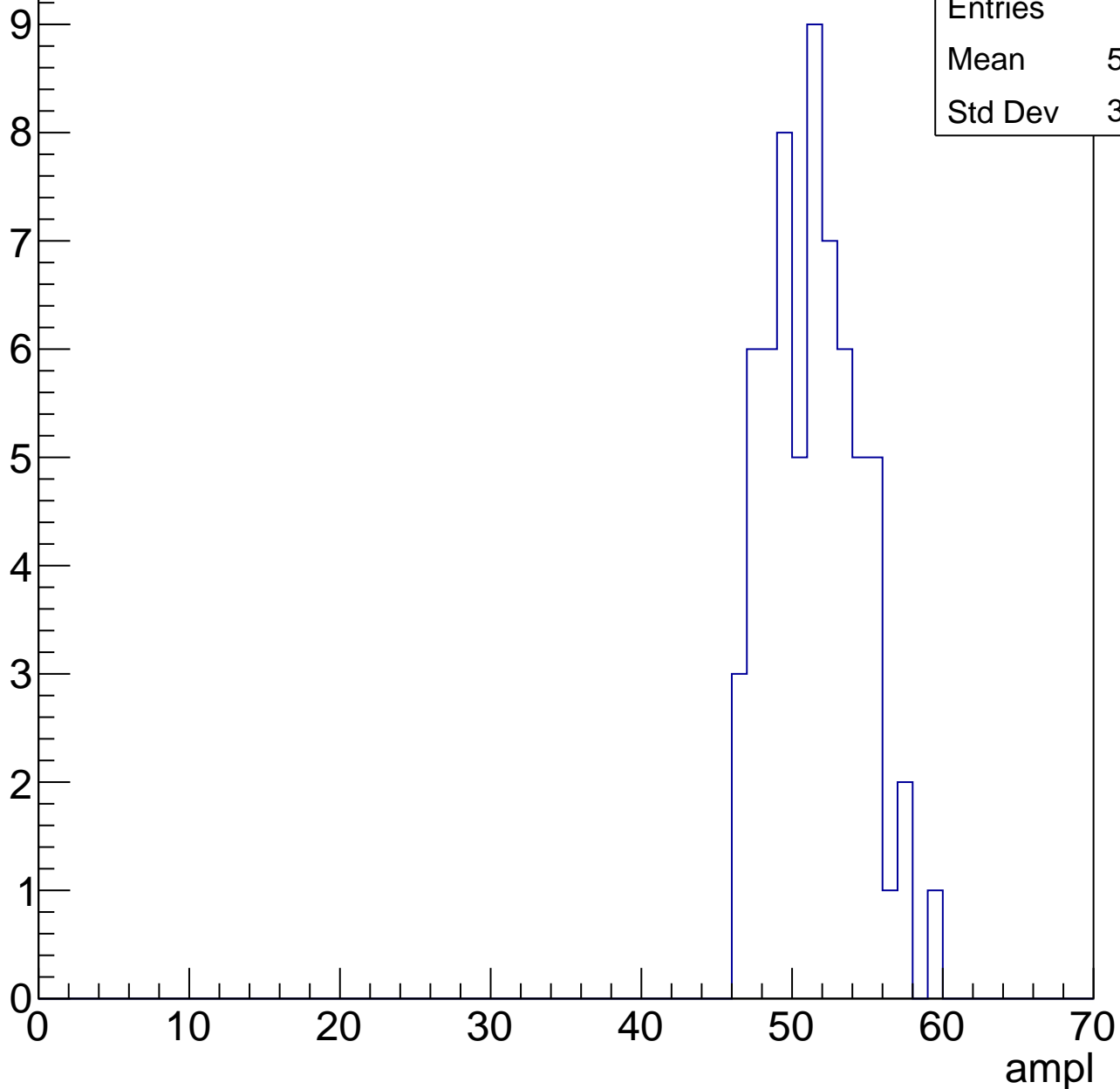


B0L002S, U26-ch46, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.02
Std Dev	3.008

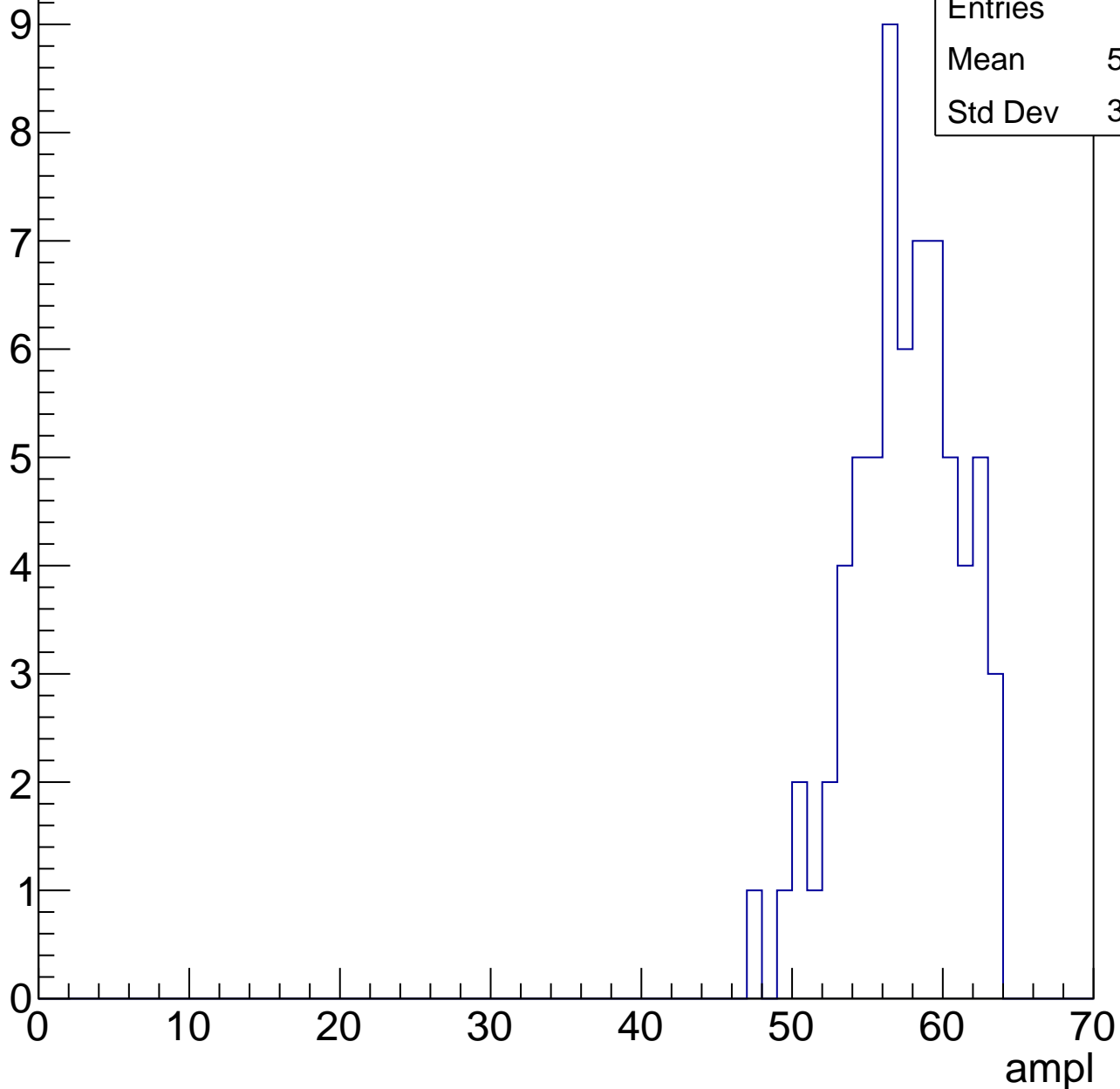


B0L002S, U26-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	56.96
Std Dev	3.589

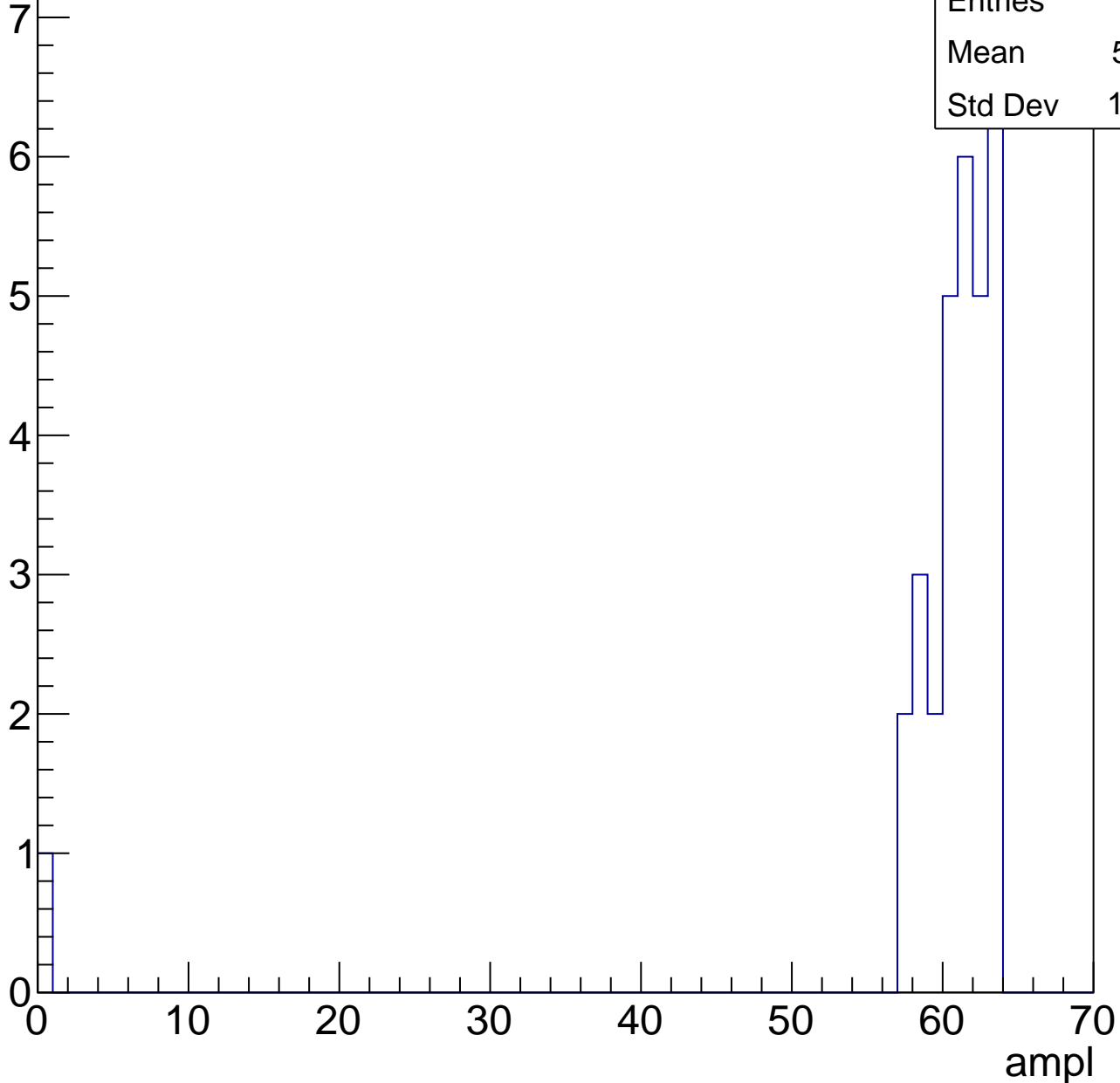


B0L002S, U26-ch46, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.81
Std Dev	10.89



B0L002S, U26-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch47, adc0

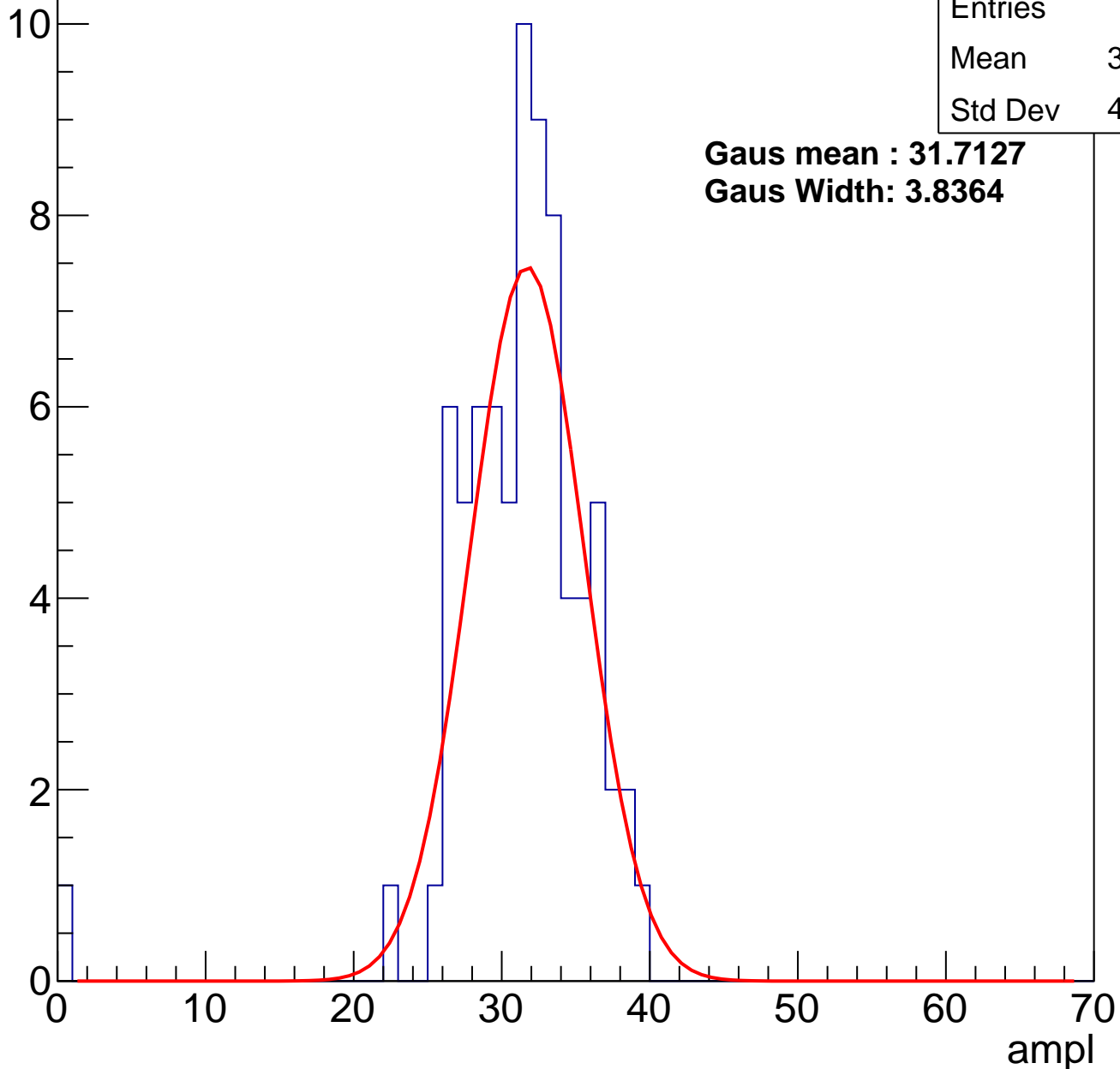
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	30.75
Std Dev	4.982

Gaus mean : 31.7127

Gaus Width: 3.8364

Entry



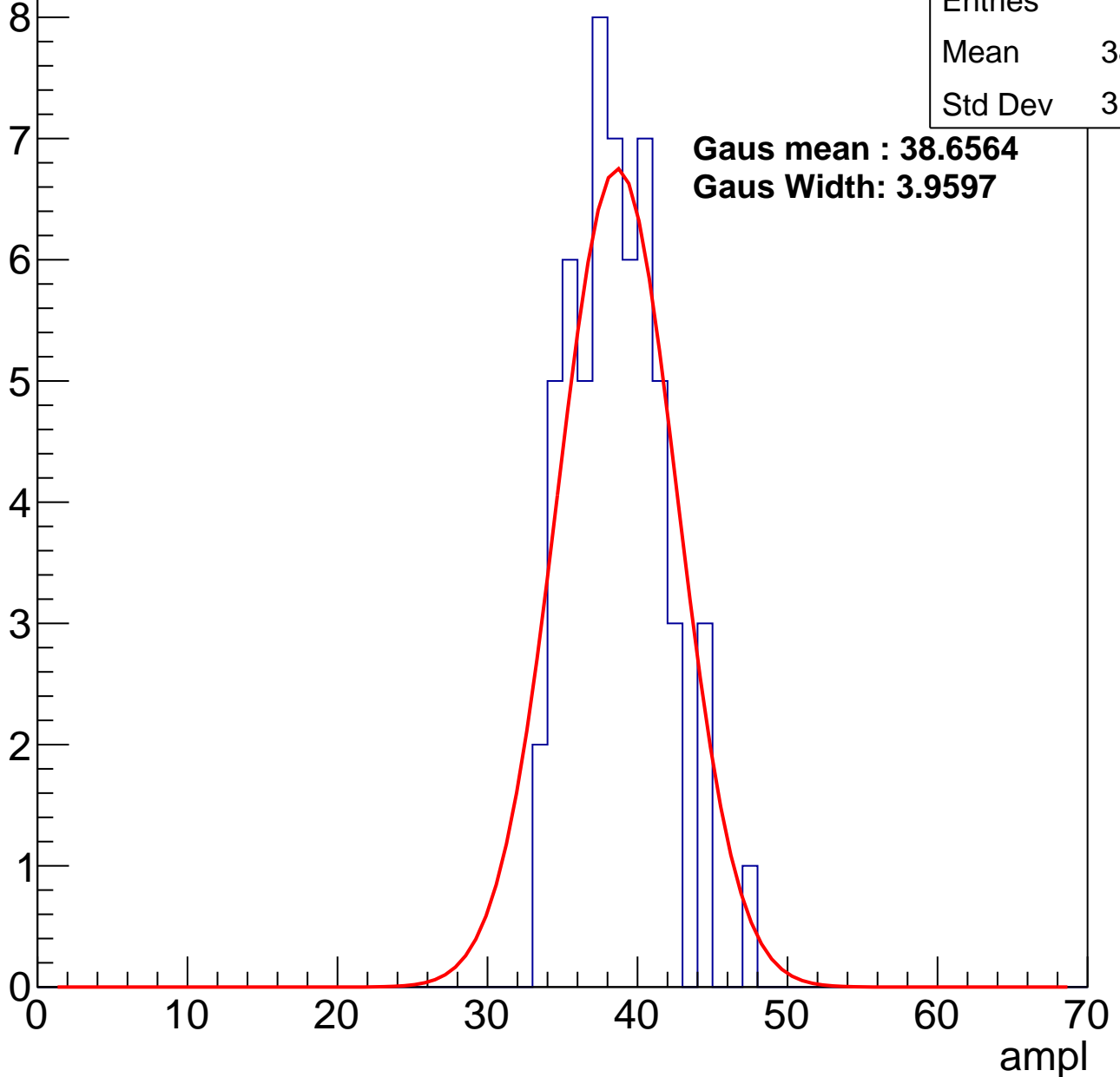
B0L002S, U26-ch47, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	38.14
Std Dev	3.014

Gaus mean : 38.6564
Gaus Width: 3.9597



B0L002S, U26-ch47, adc2

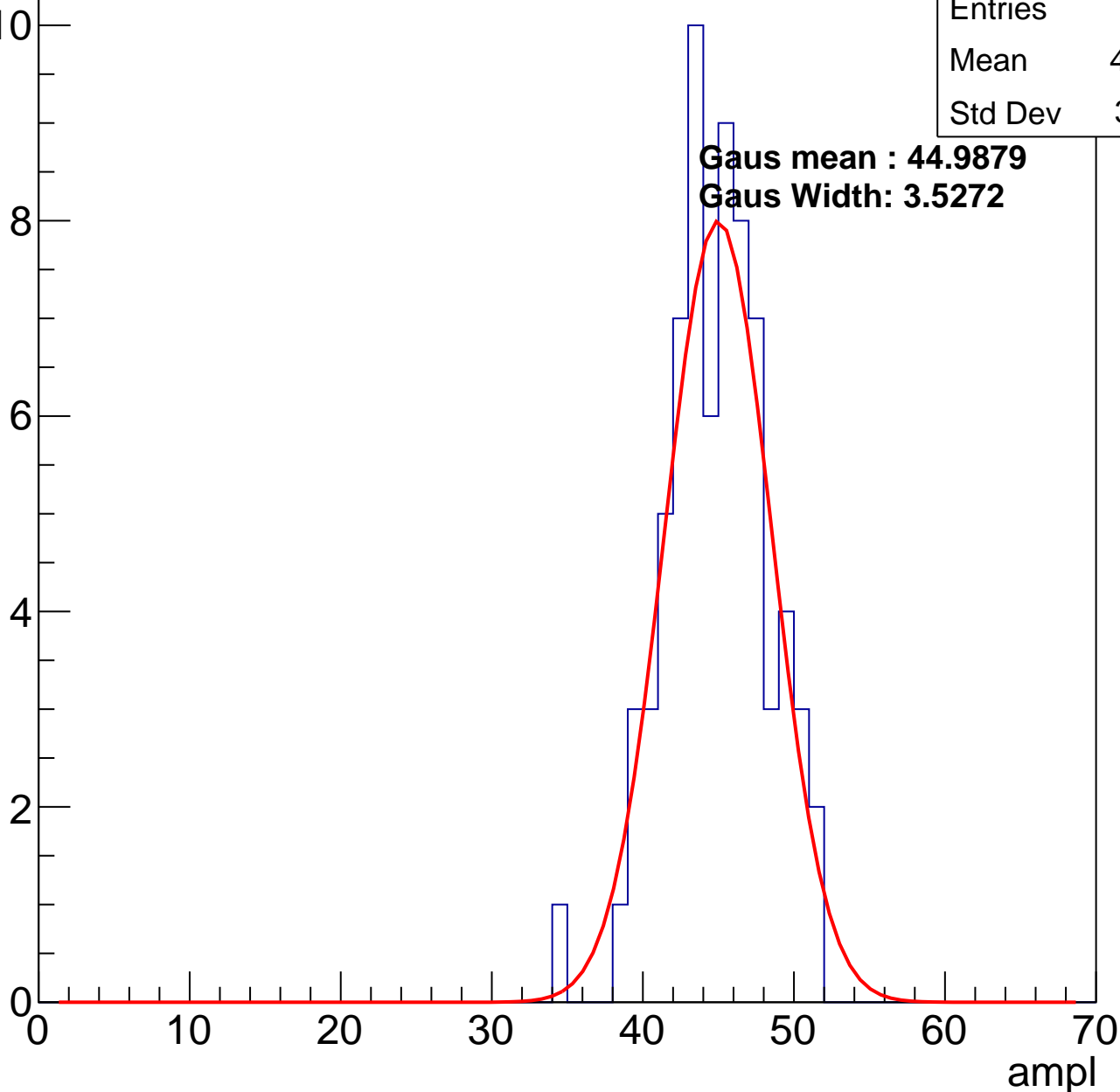
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.39
Std Dev	3.311

Gaus mean : 44.9879

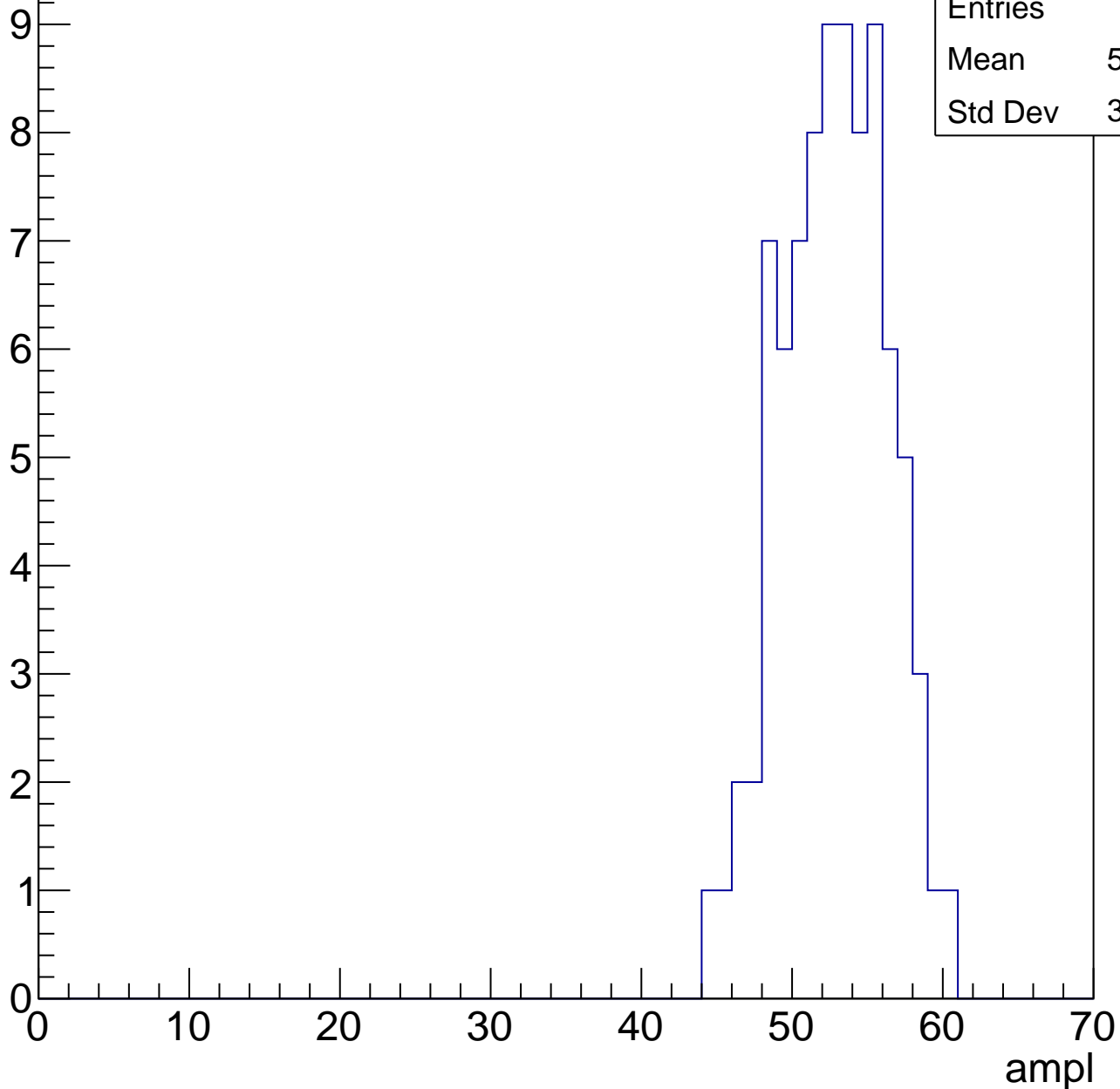
Gaus Width: 3.5272



B0L002S, U26-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch47, adc4

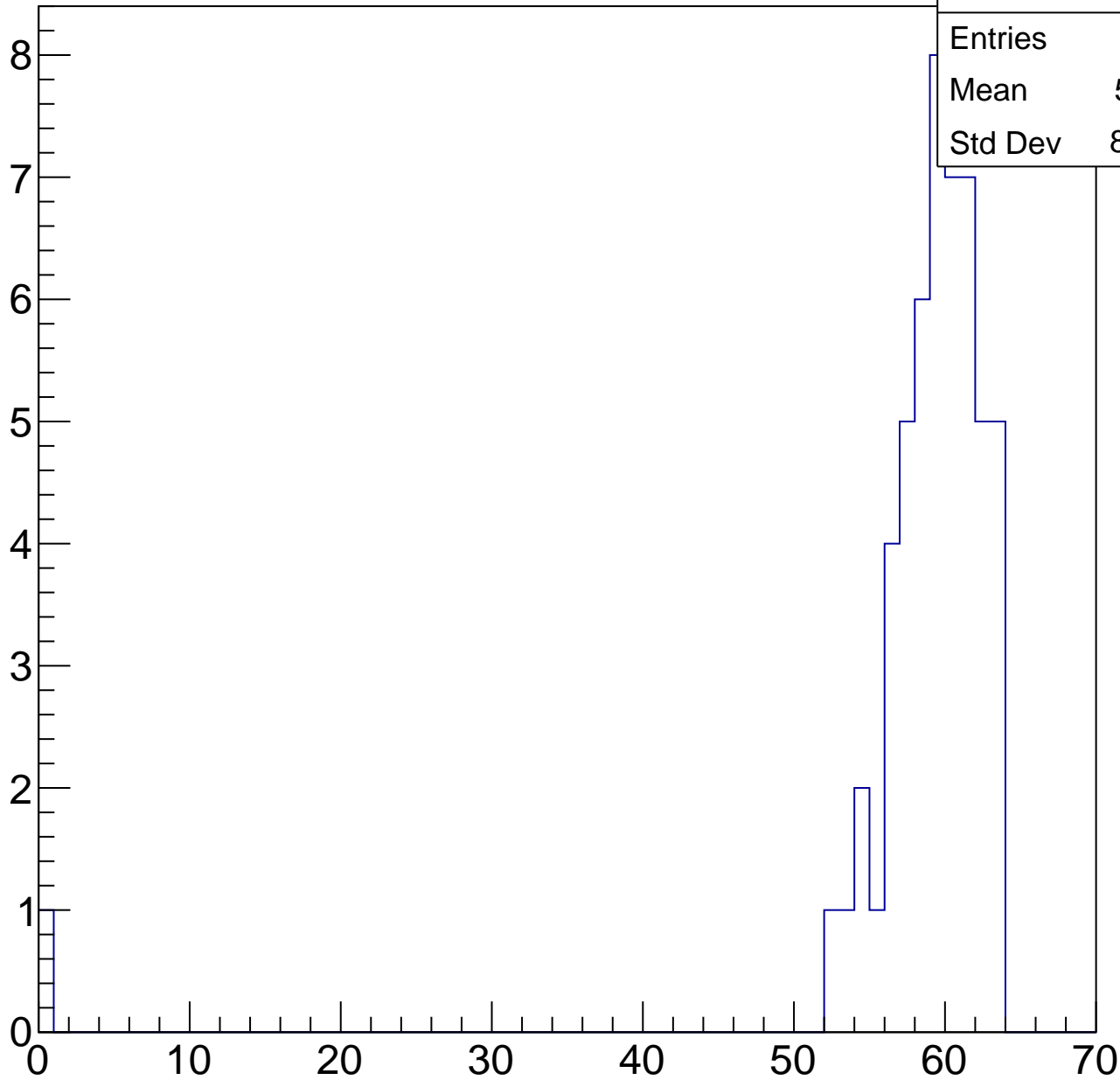
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	57.91
Std Dev	8.457

ampl

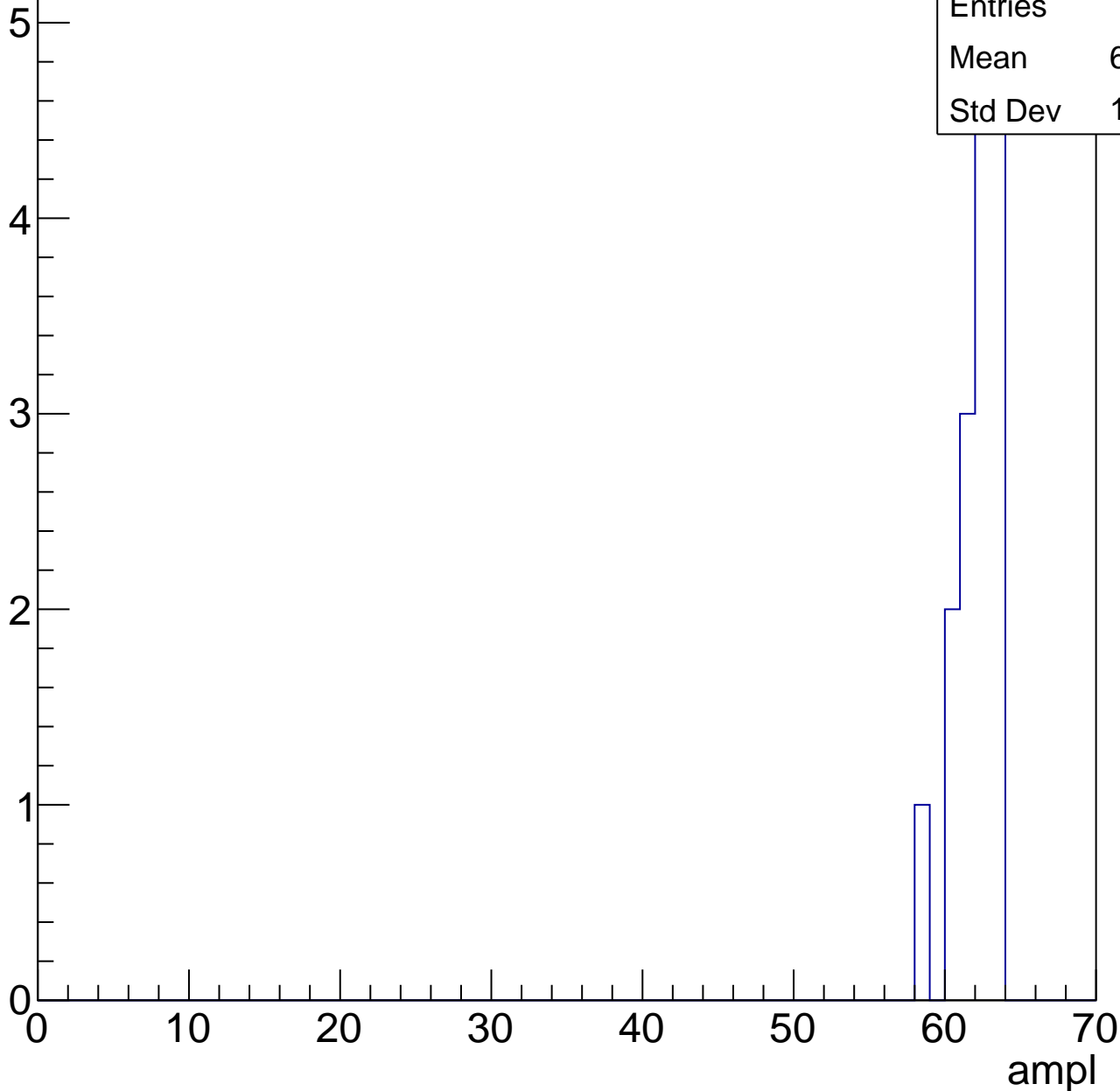


B0L002S, U26-ch47, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

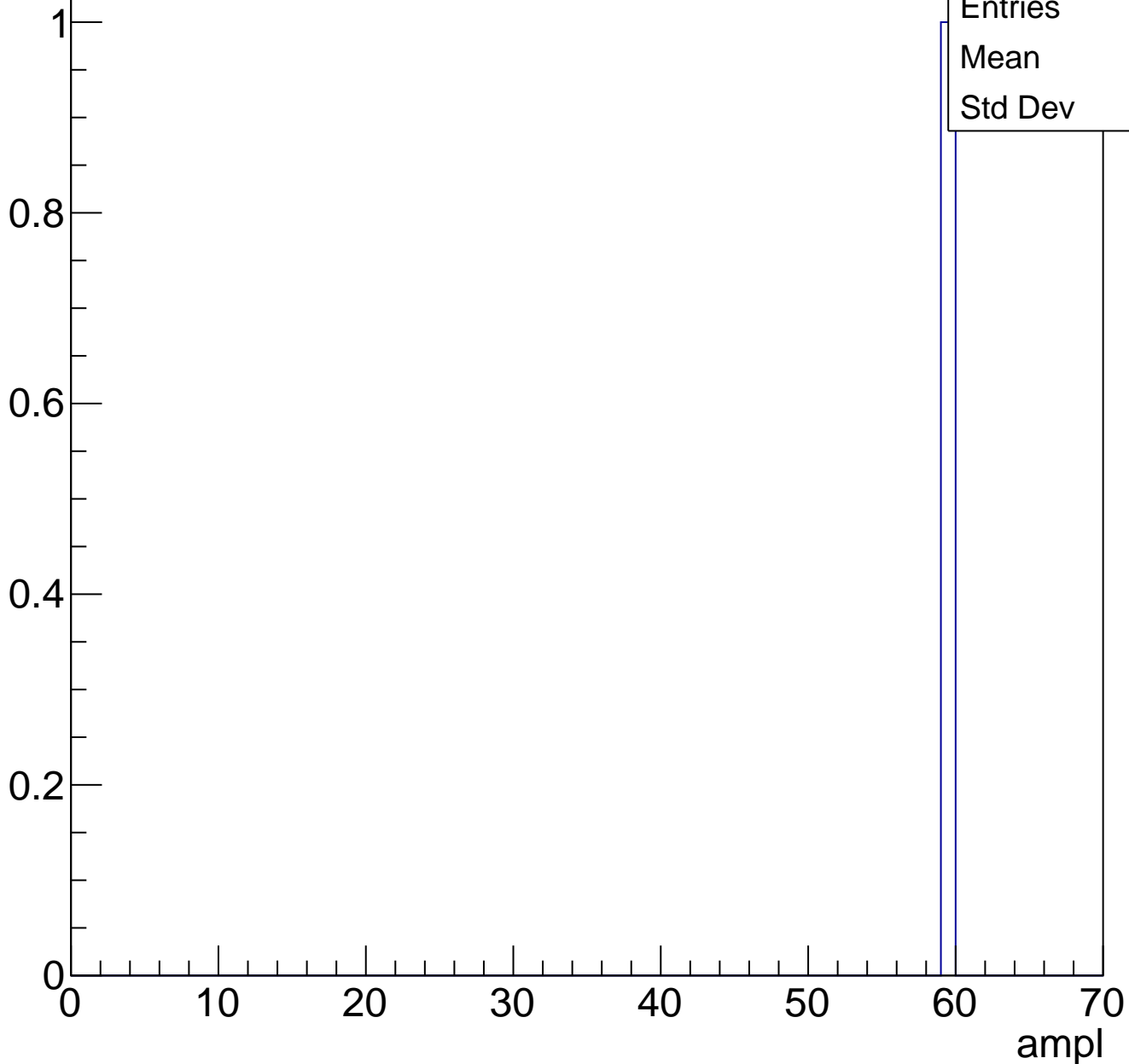
Entries	16
Mean	61.62
Std Dev	1.364



B0L002S, U26-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch48, adc0

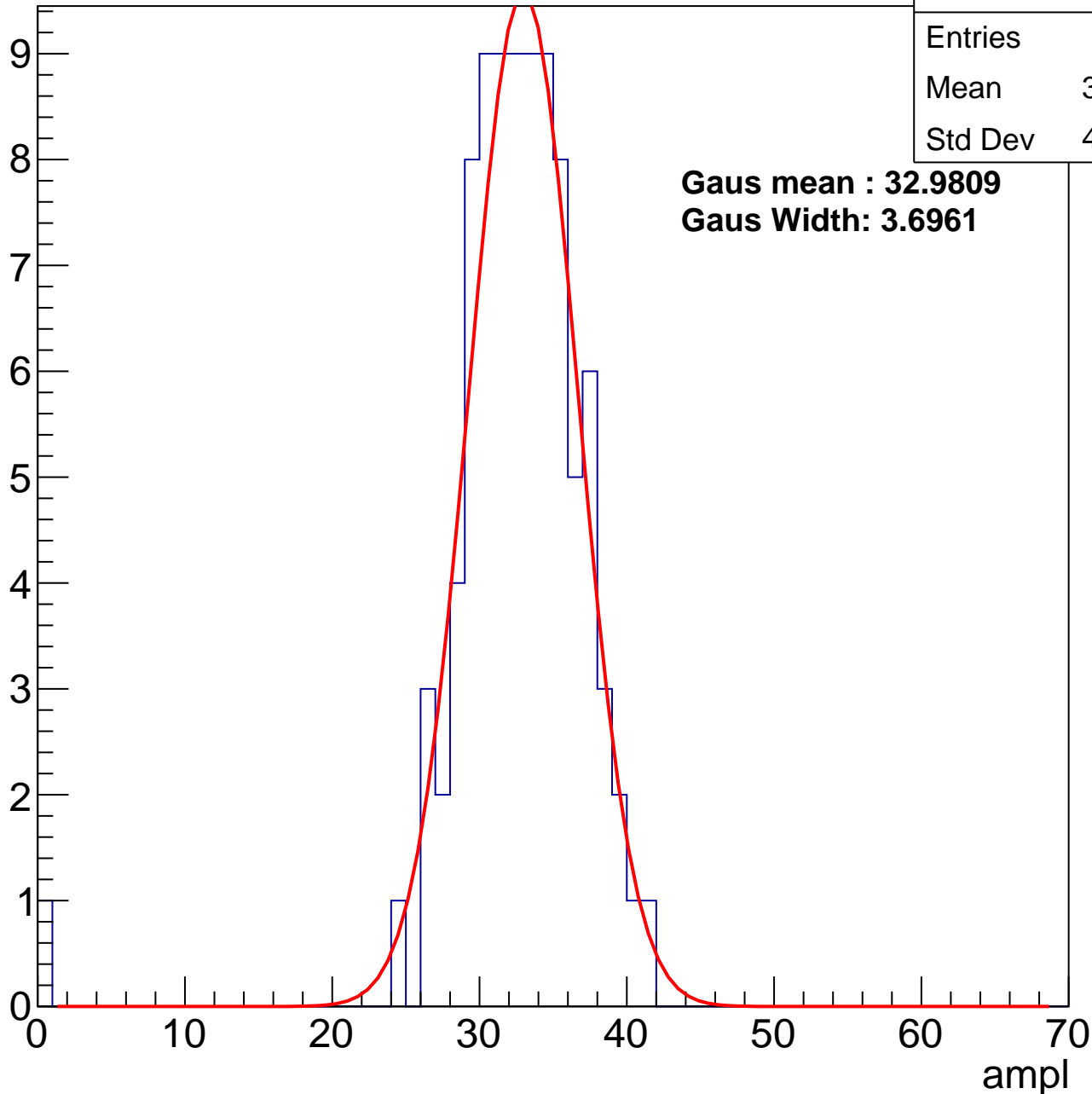
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	90
Mean	32.17
Std Dev	4.856

Gaus mean : 32.9809

Gaus Width: 3.6961



B0L002S, U26-ch48, adc1

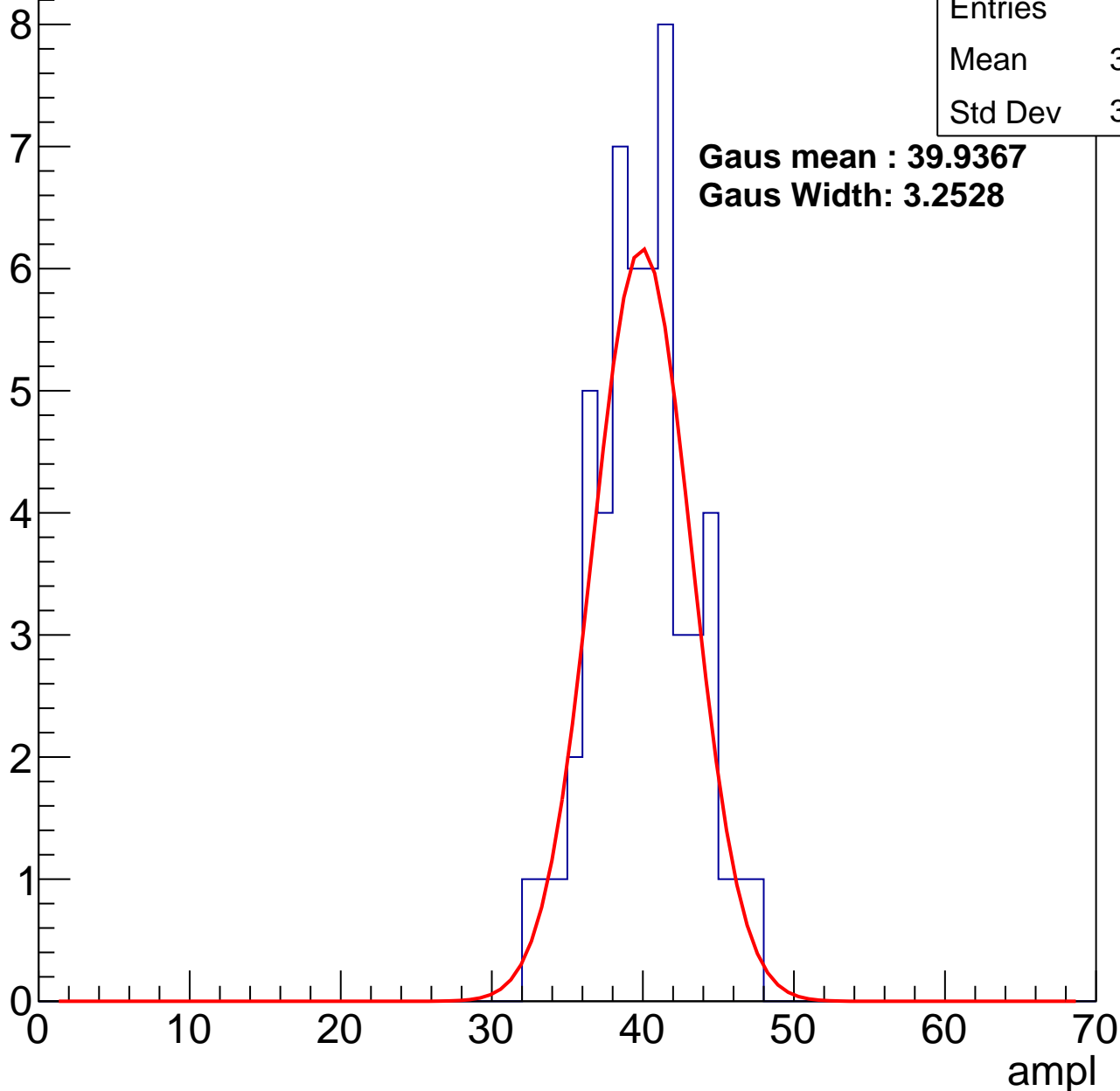
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	39.52
Std Dev	3.213

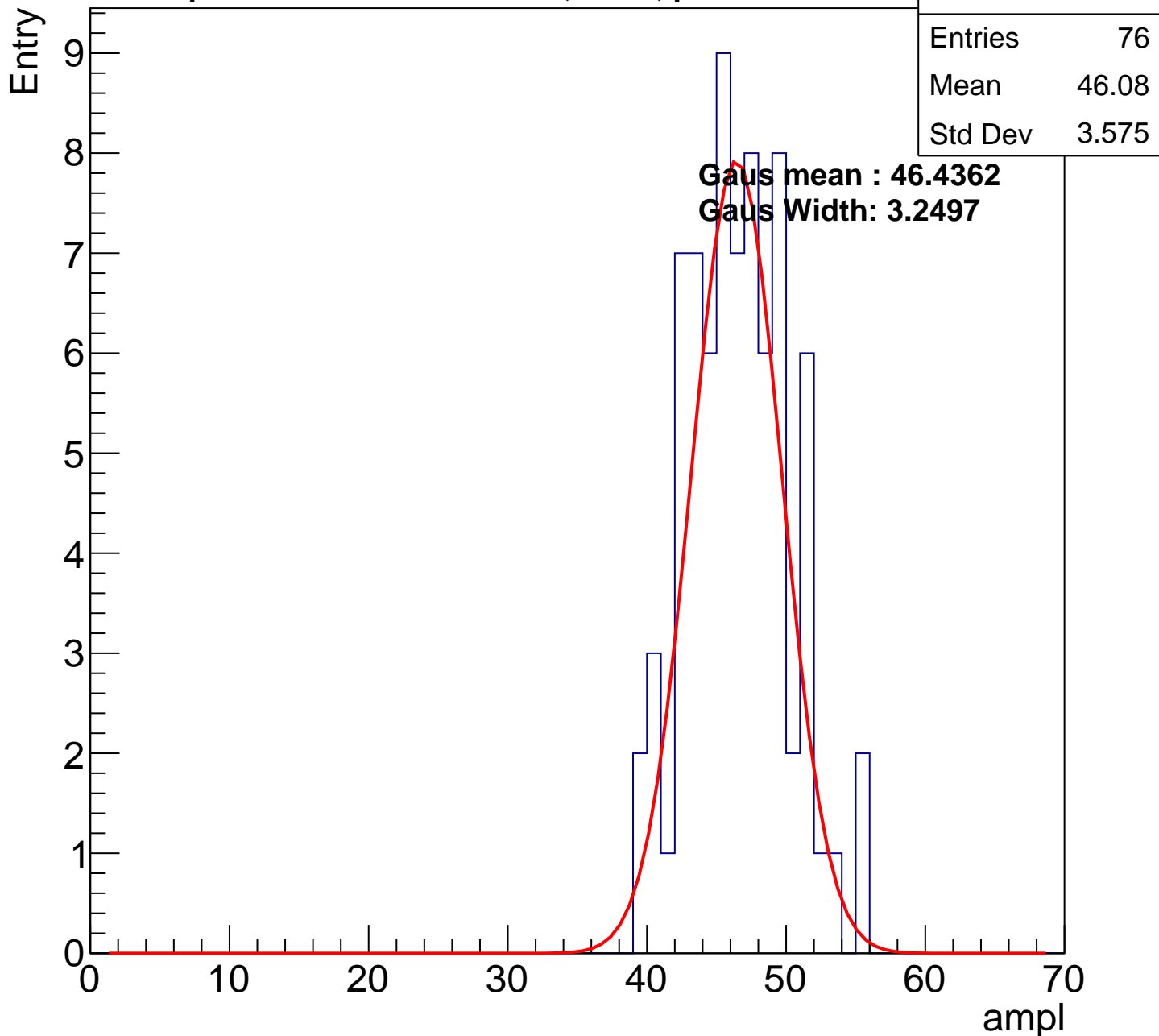
Gaus mean : 39.9367

Gaus Width: 3.2528



B0L002S, U26-ch48, adc2

calib_packv5_042523_0143.root, FC#8, port C1

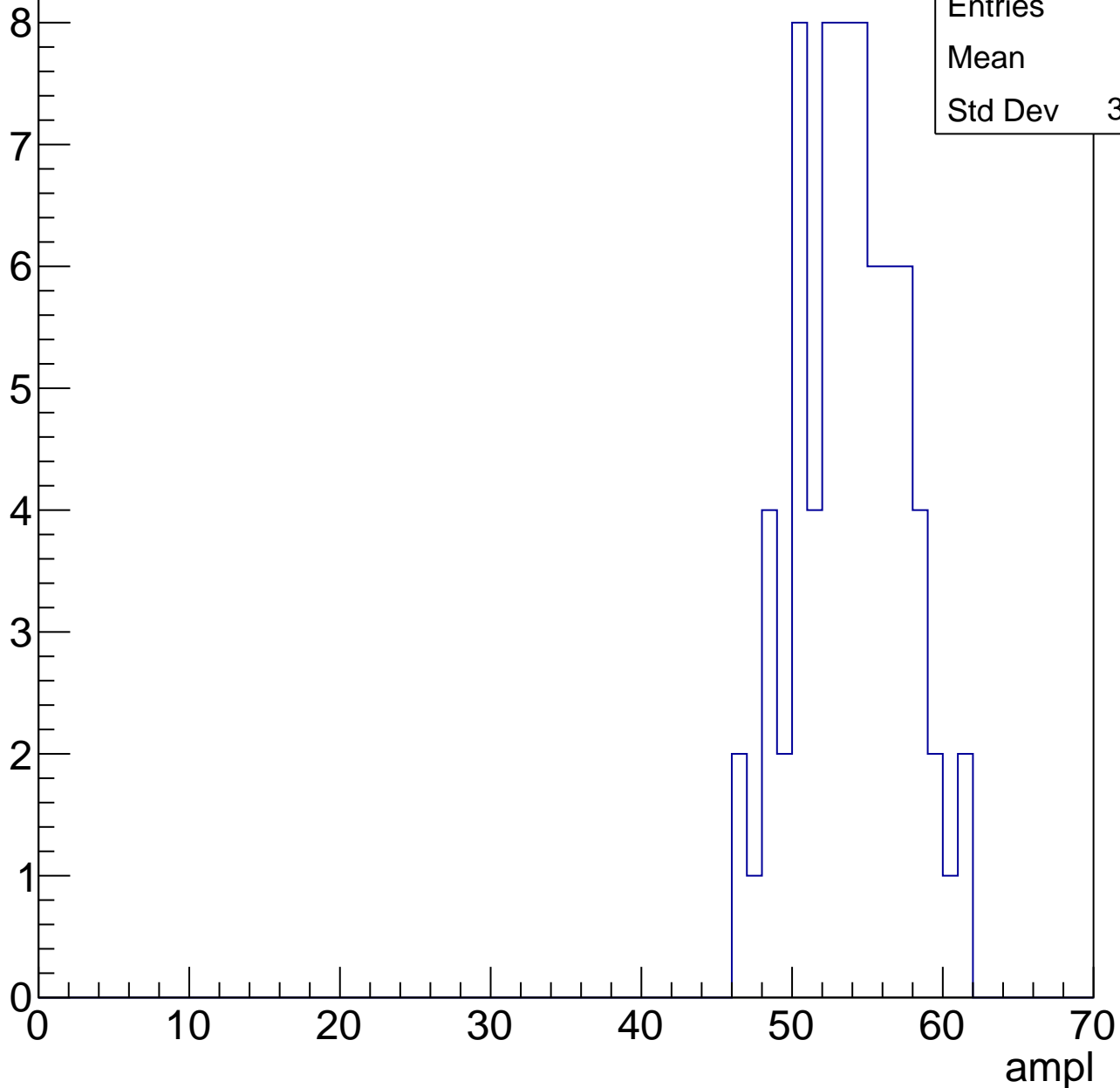


B0L002S, U26-ch48, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

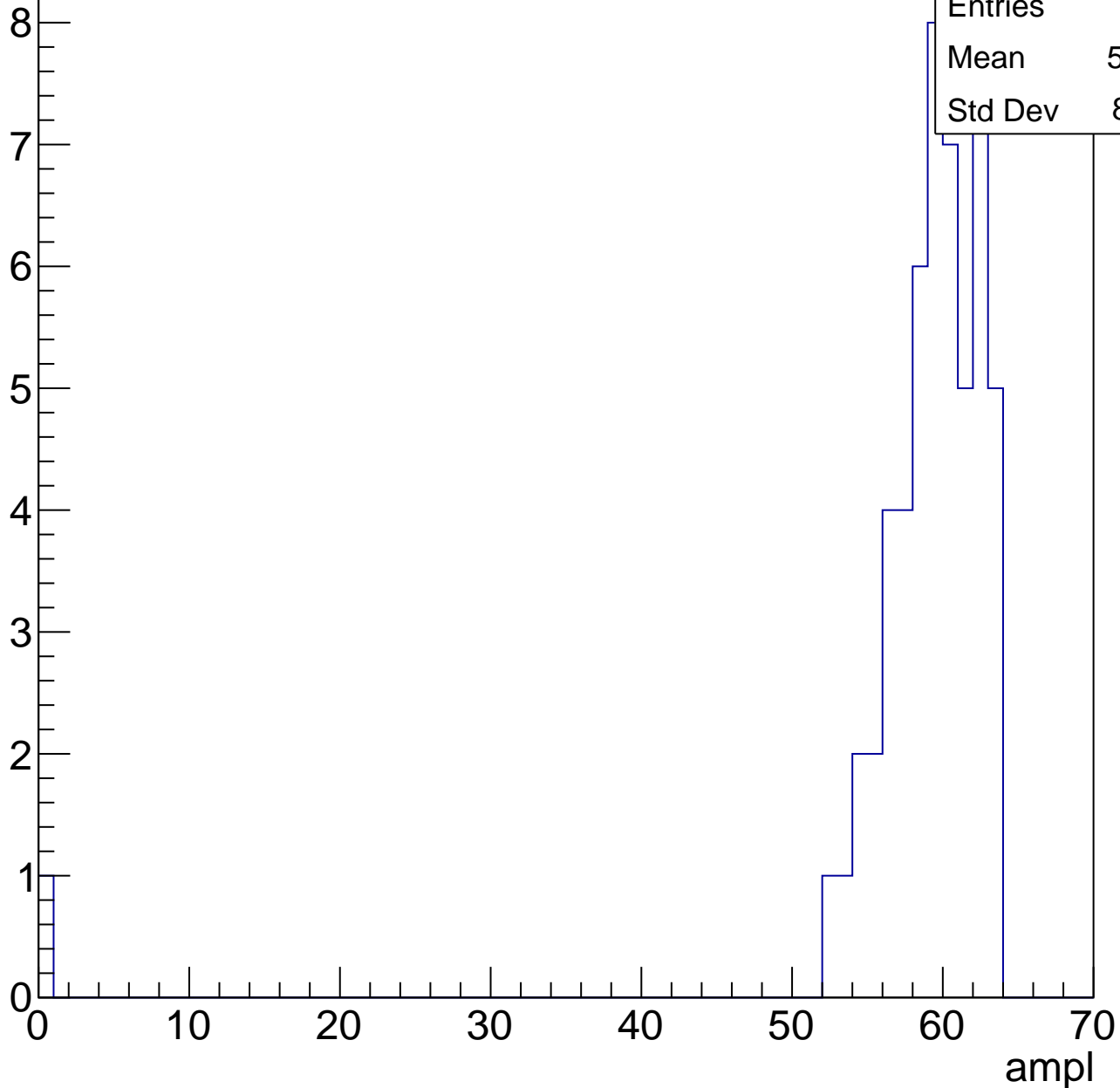
Entries	72
Mean	53.4
Std Dev	3.499



B0L002S, U26-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1

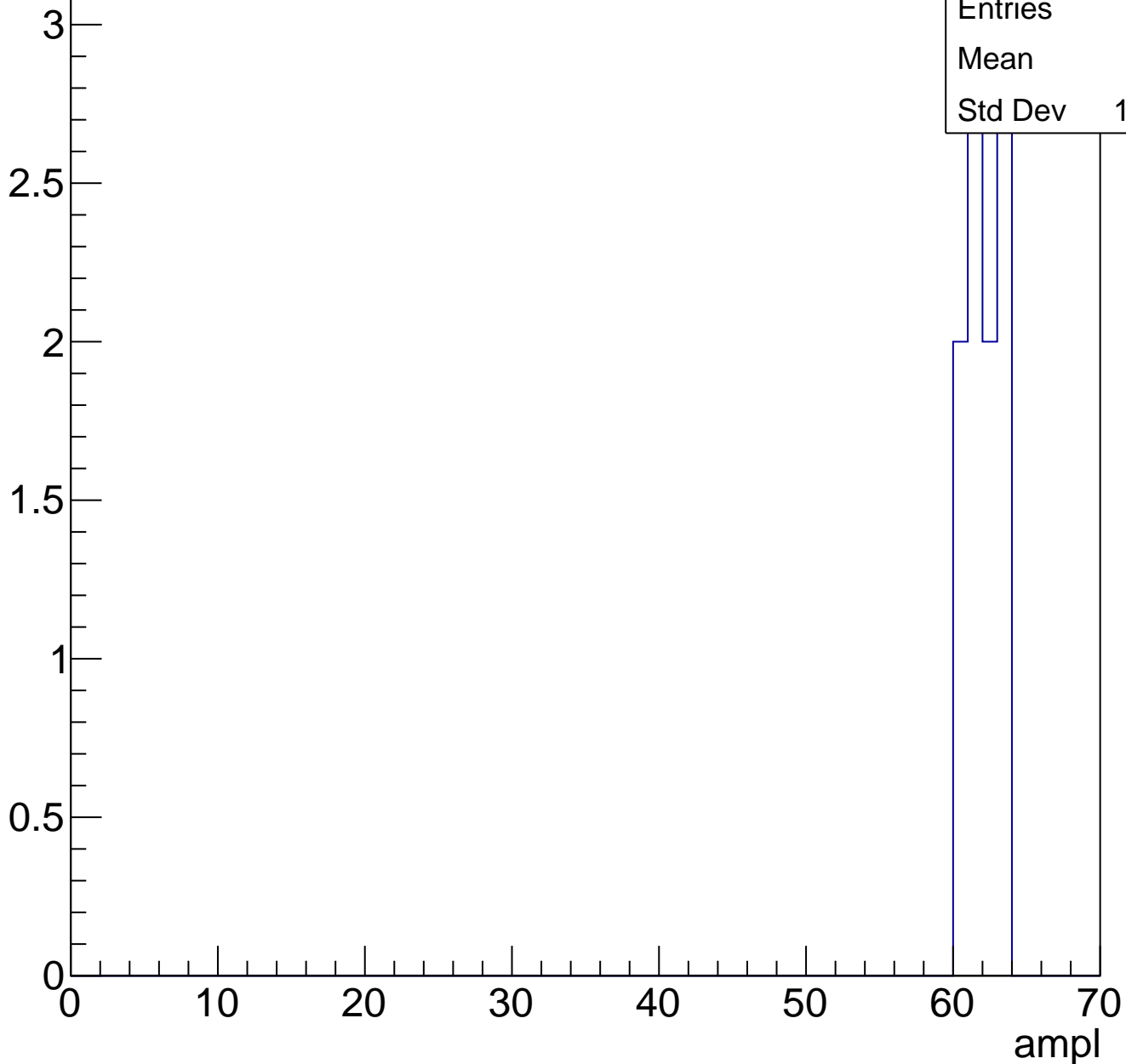
Entry



B0L002S, U26-ch48, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch49, adc0

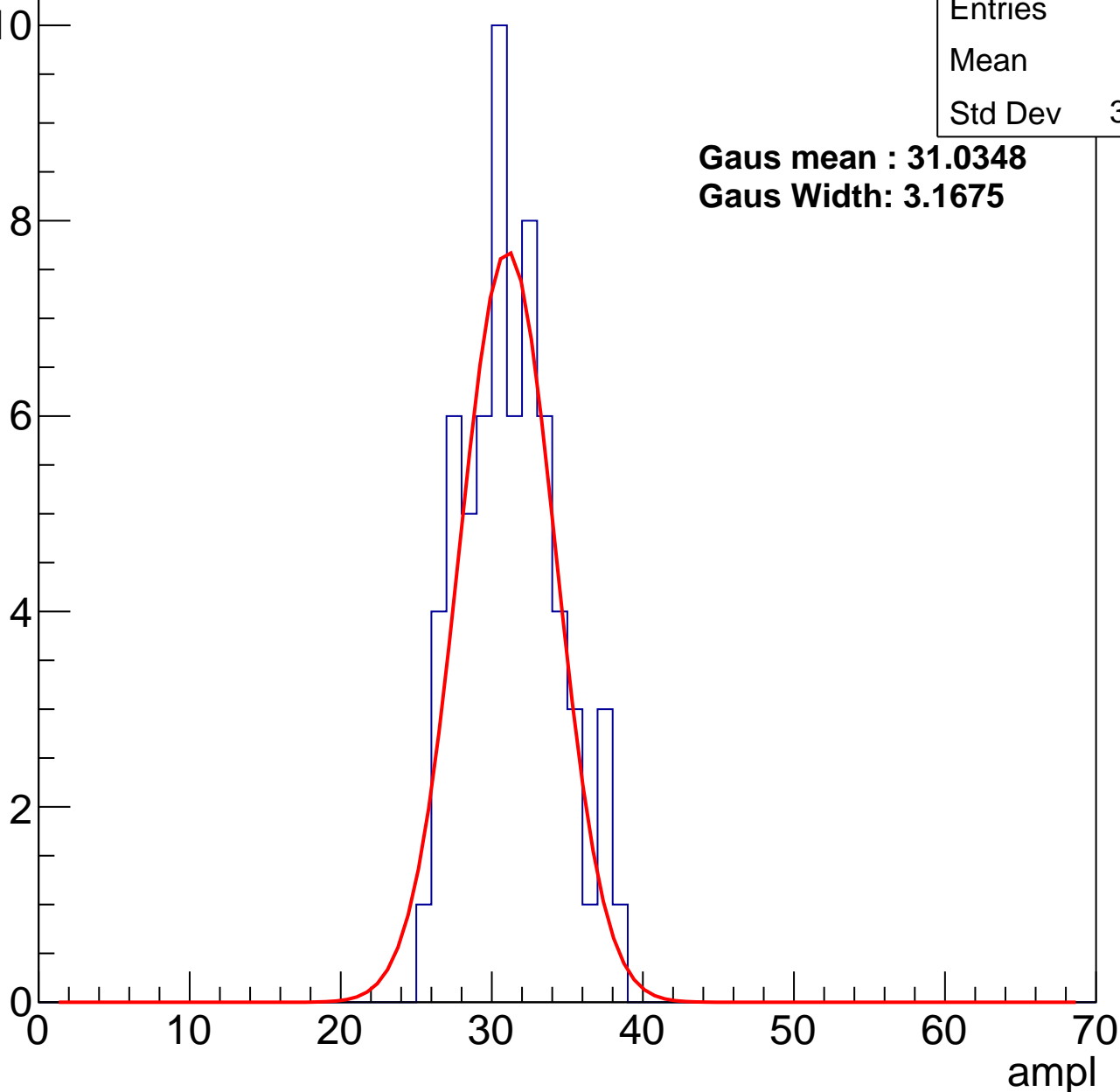
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.8
Std Dev	3.078

Gaus mean : 31.0348

Gaus Width: 3.1675



B0L002S, U26-ch49, adc1

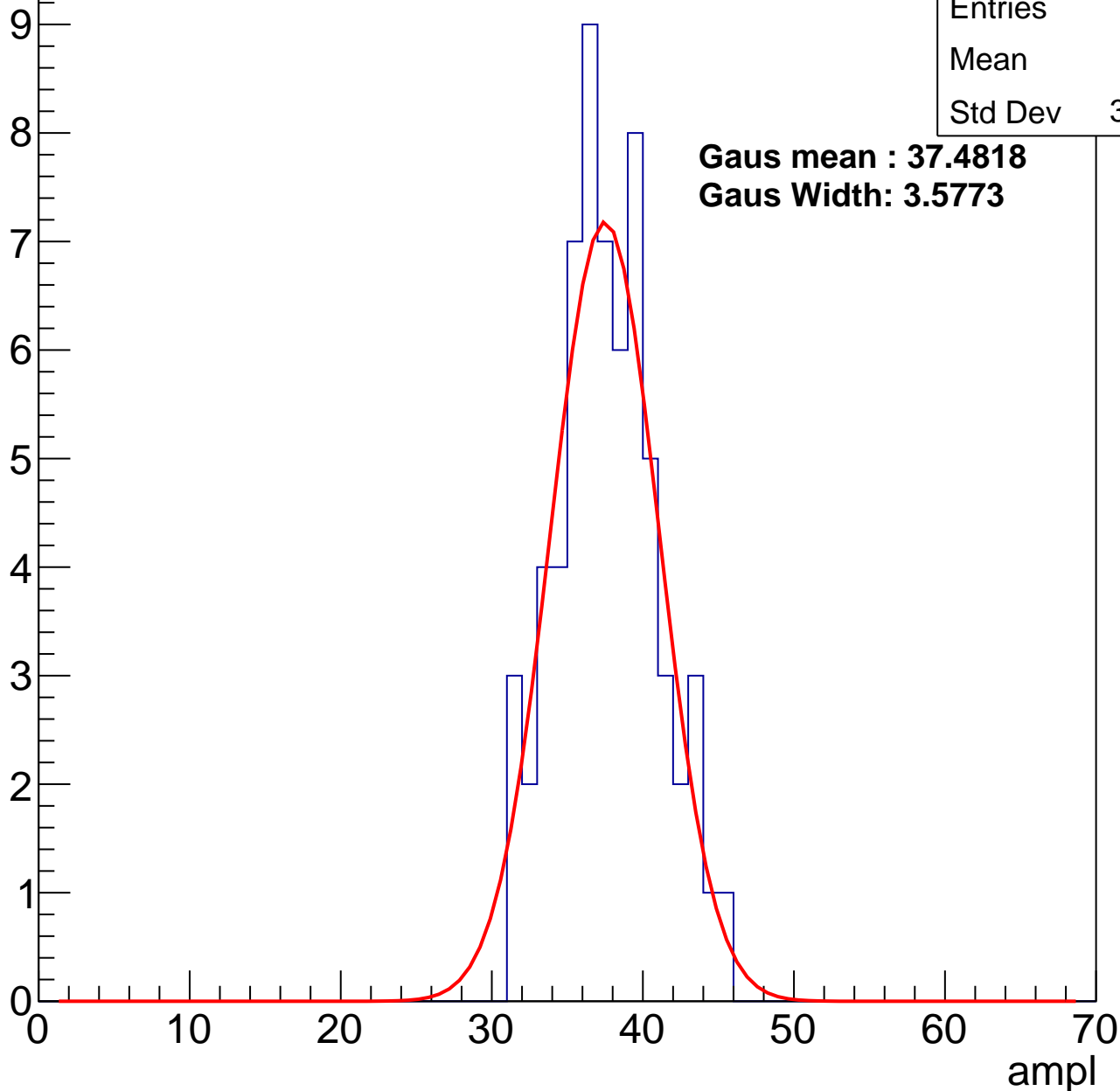
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.2
Std Dev	3.268

Gaus mean : 37.4818

Gaus Width: 3.5773



B0L002S, U26-ch49, adc2

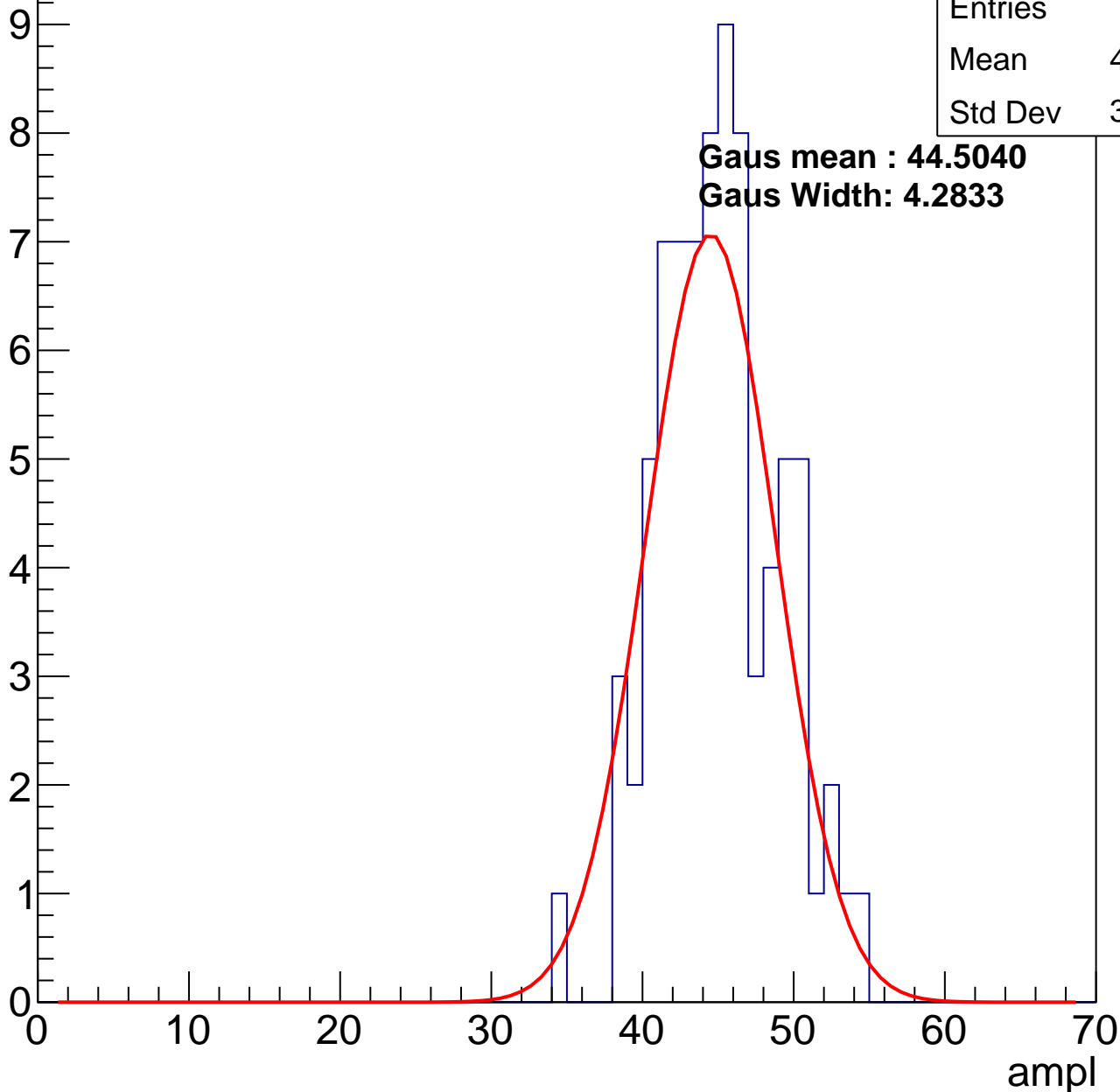
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	44.59
Std Dev	3.902

Gaus mean : 44.5040

Gaus Width: 4.2833

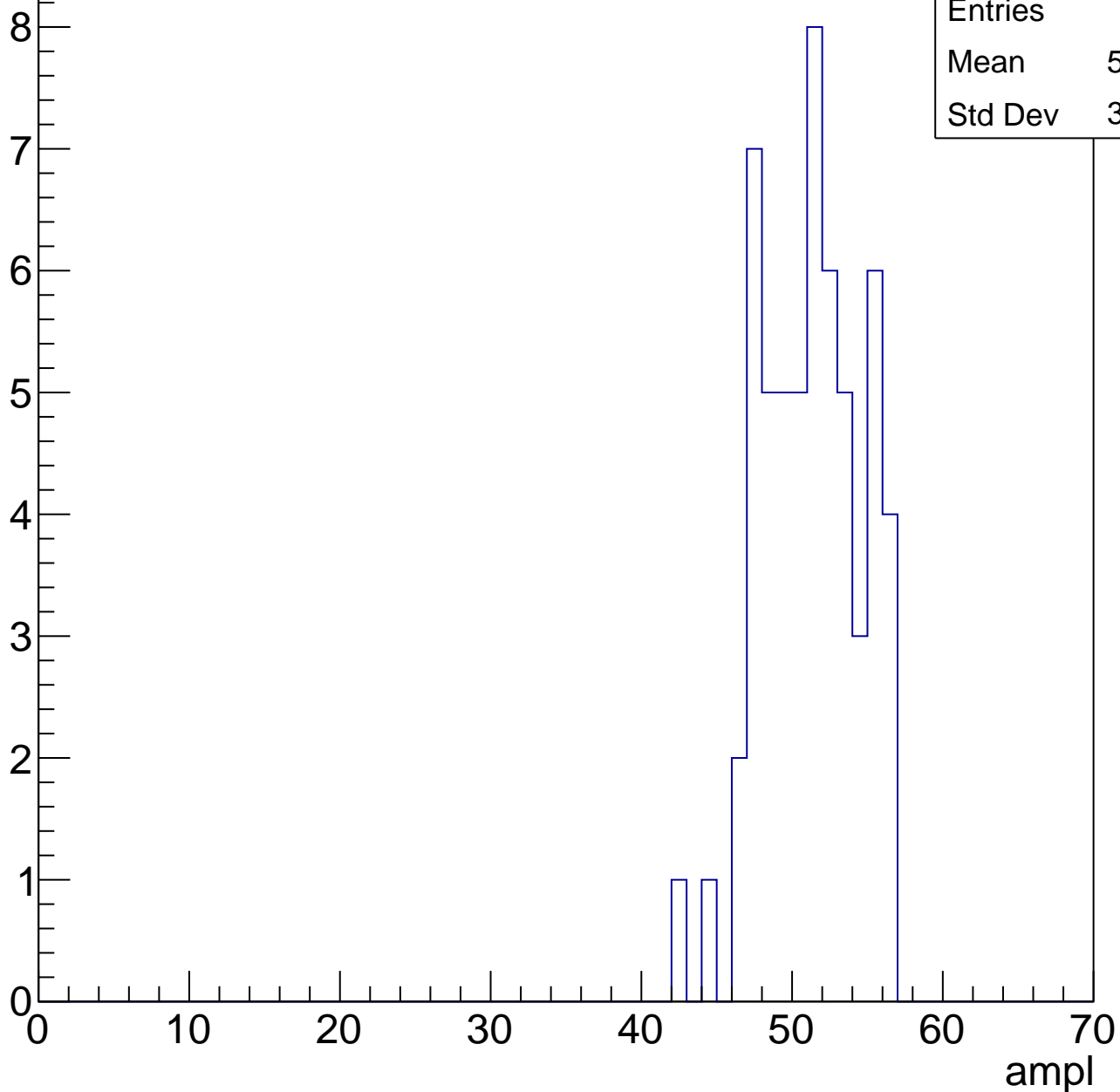


B0L002S, U26-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	50.74
Std Dev	3.235

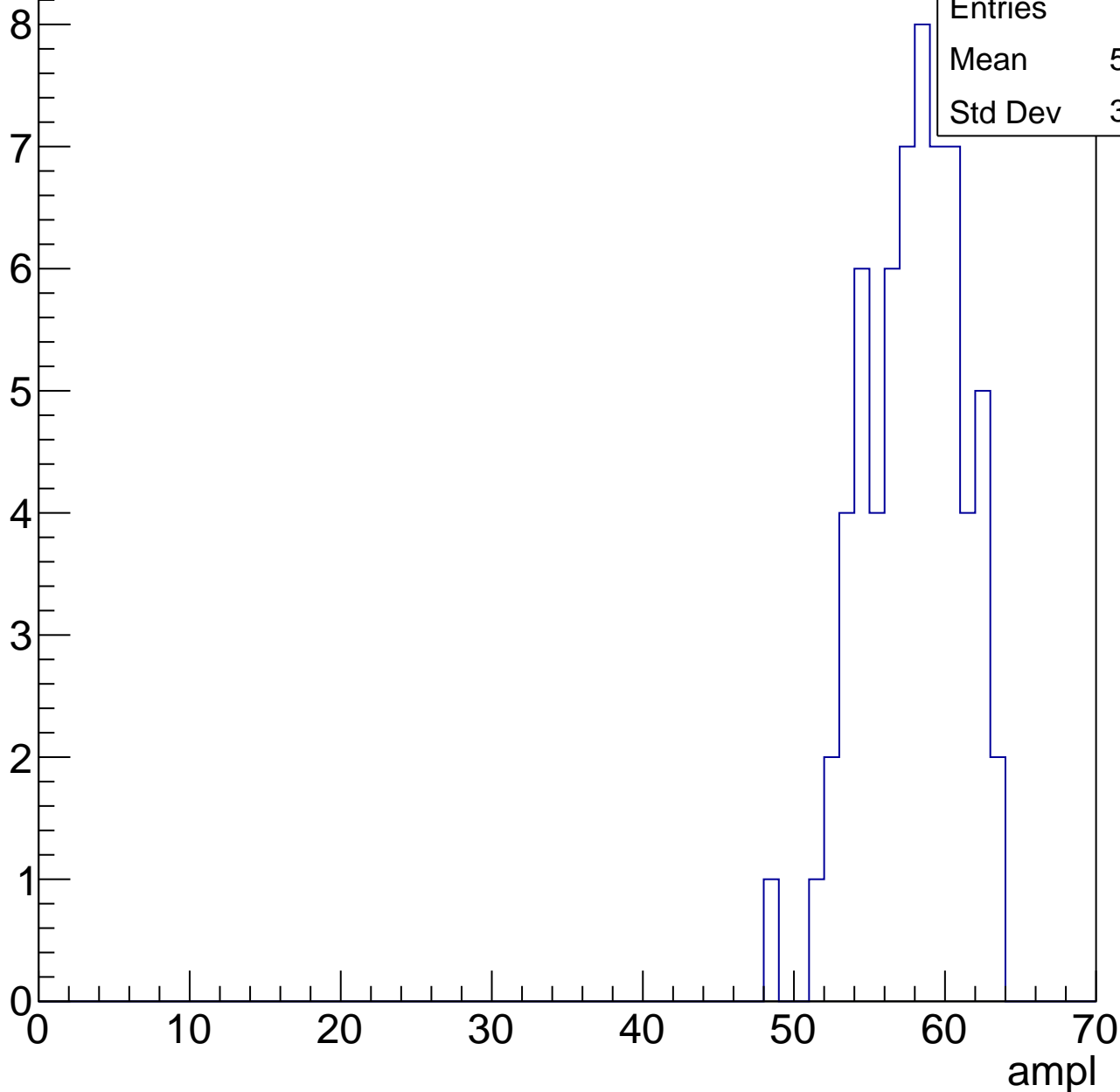


B0L002S, U26-ch49, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	57.36
Std Dev	3.208

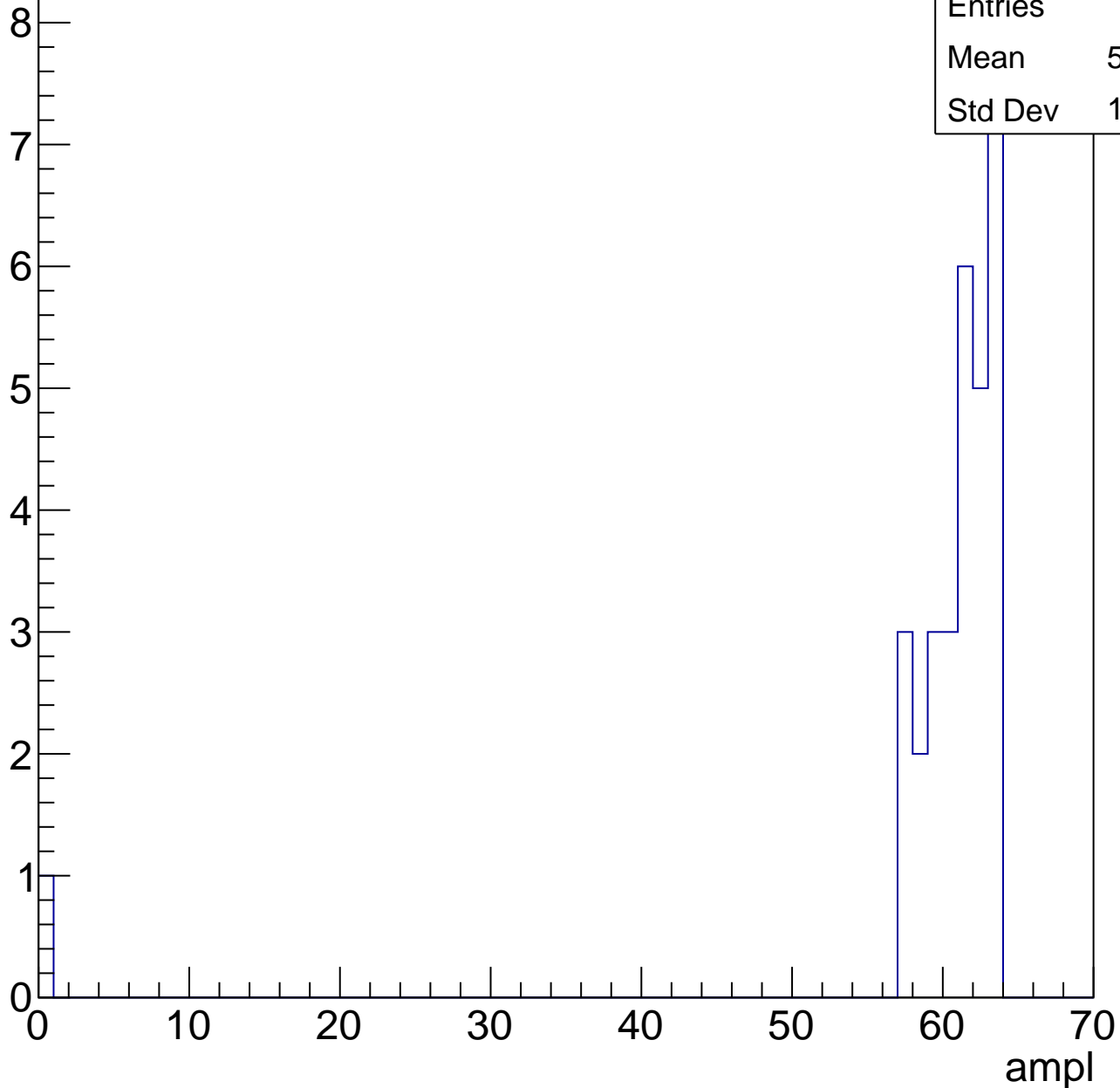


B0L002S, U26-ch49, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.84
Std Dev	10.92



B0L002S, U26-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch50, adc0

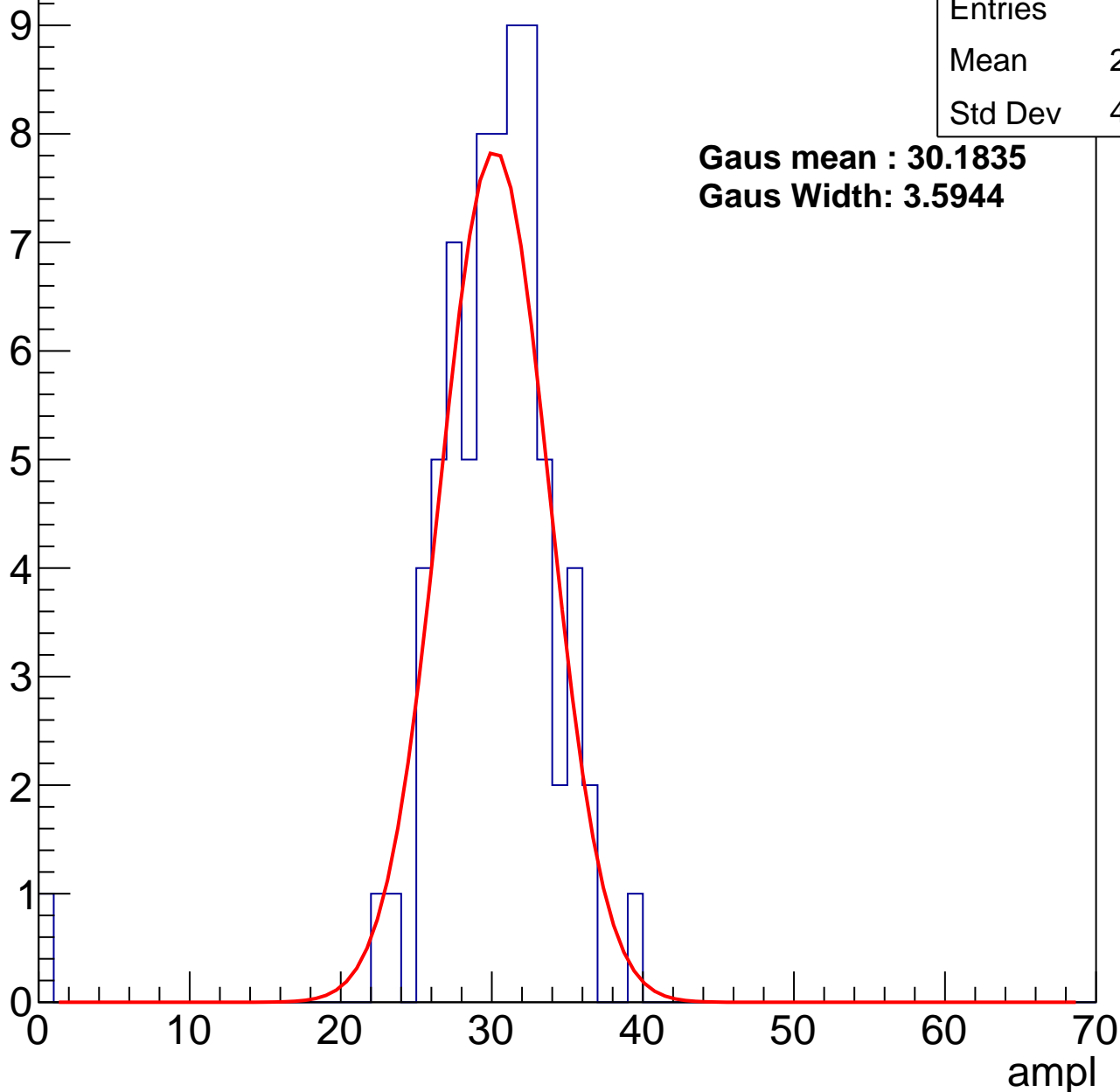
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	29.54
Std Dev	4.778

Gaus mean : 30.1835

Gaus Width: 3.5944



B0L002S, U26-ch50, adc1

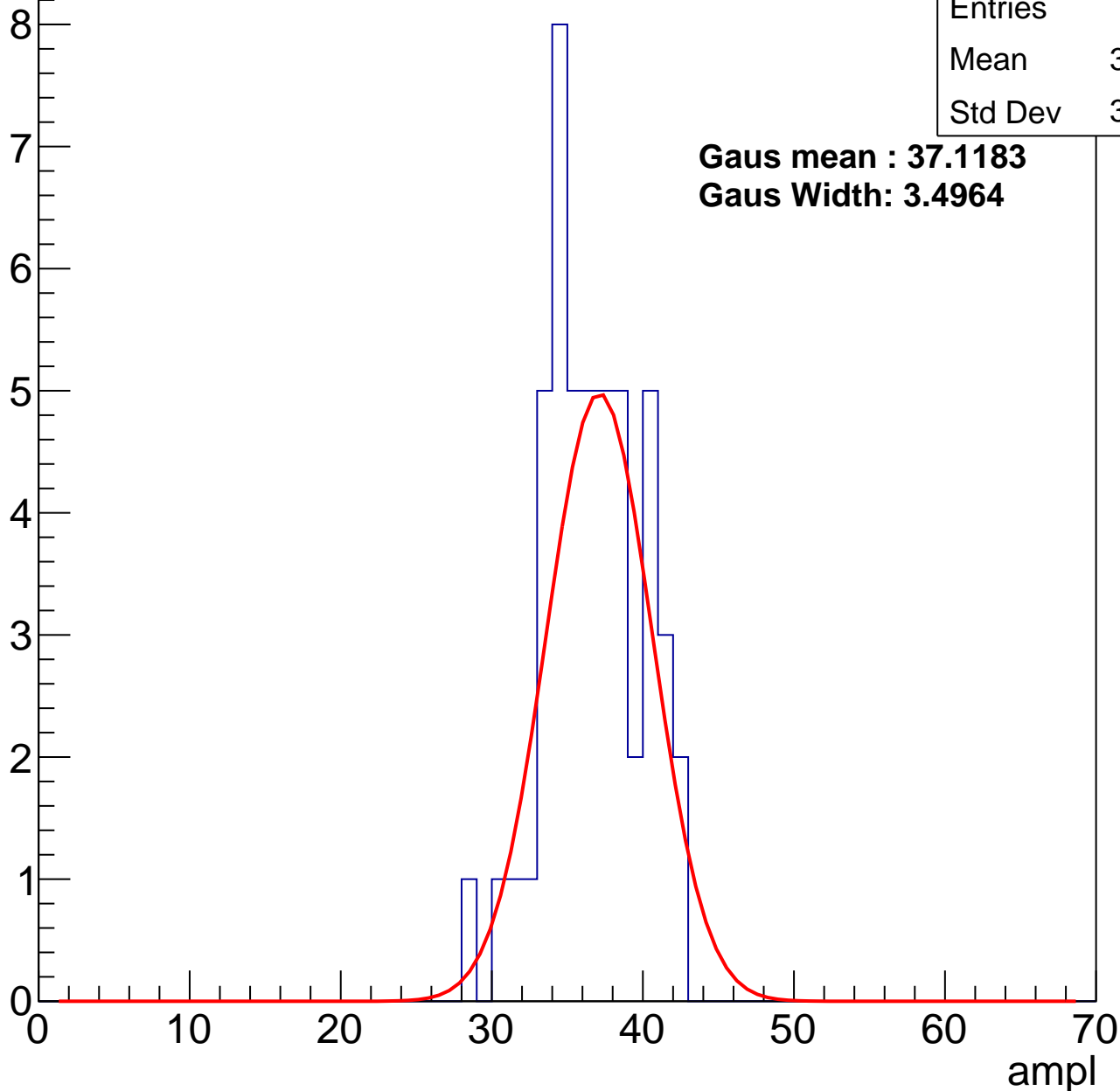
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	36.18
Std Dev	3.167

Gaus mean : 37.1183

Gaus Width: 3.4964



B0L002S, U26-ch50, adc2

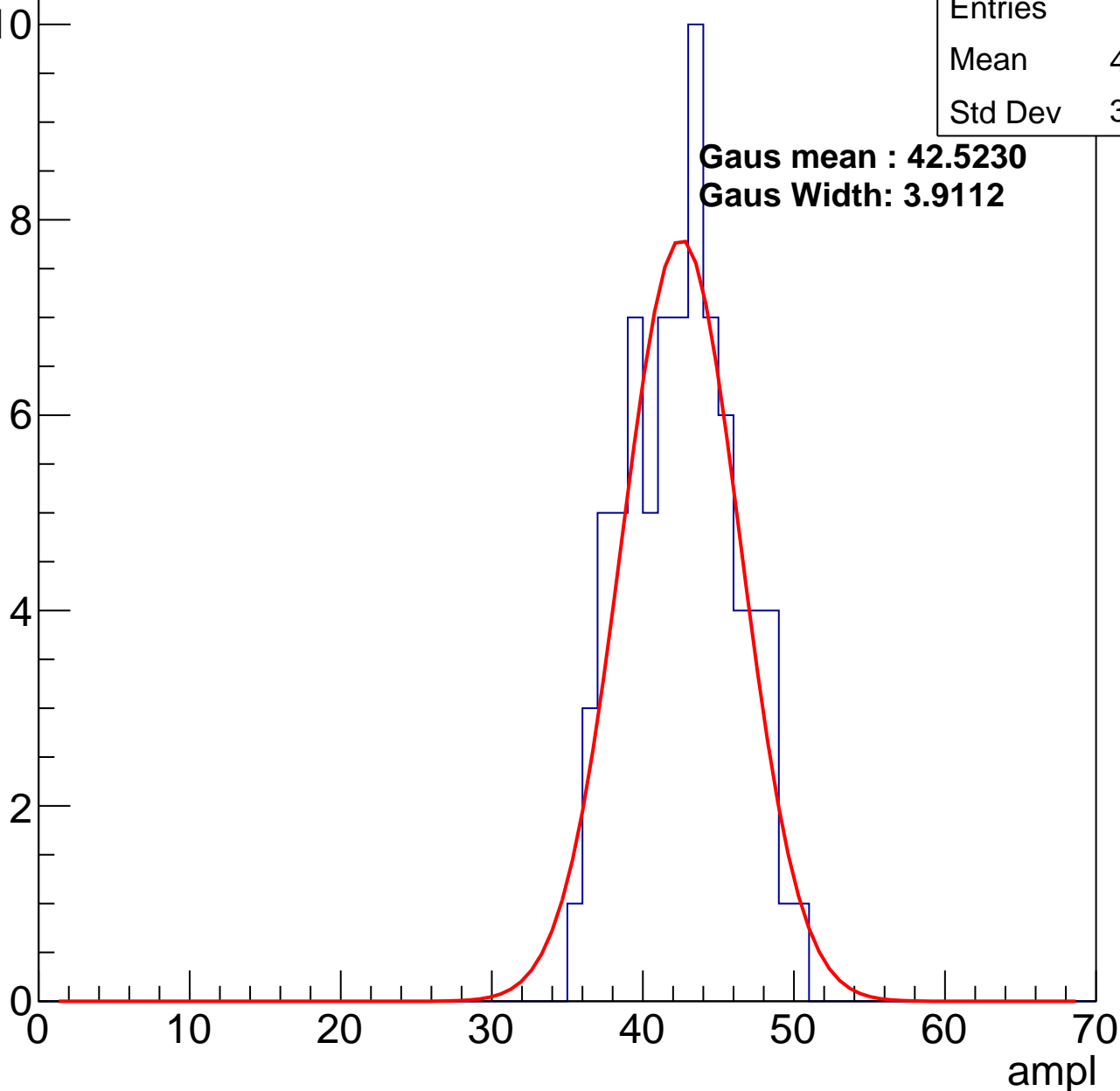
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	42.12
Std Dev	3.538

Gaus mean : 42.5230

Gaus Width: 3.9112

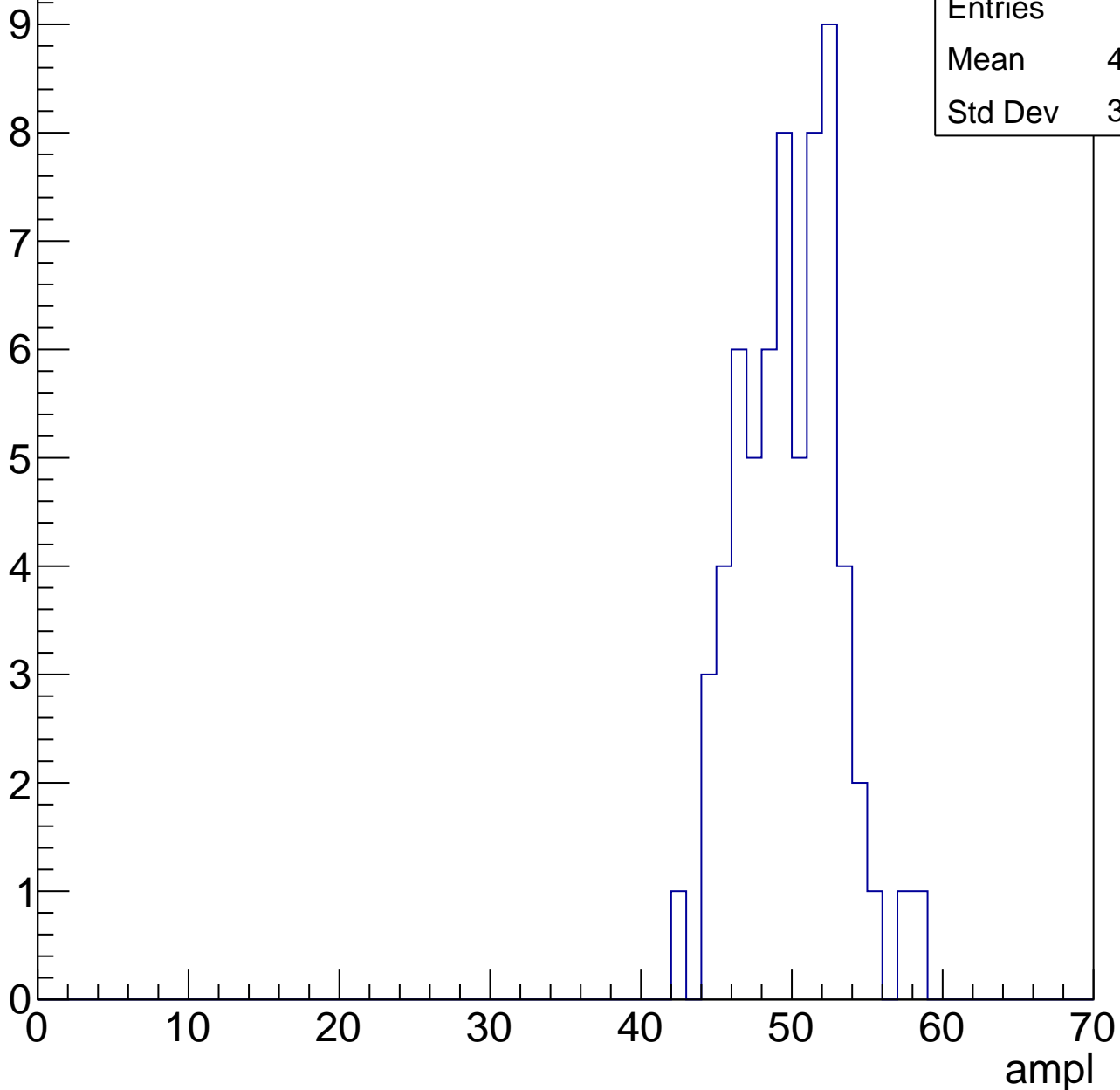


B0L002S, U26-ch50, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	49.39
Std Dev	3.239

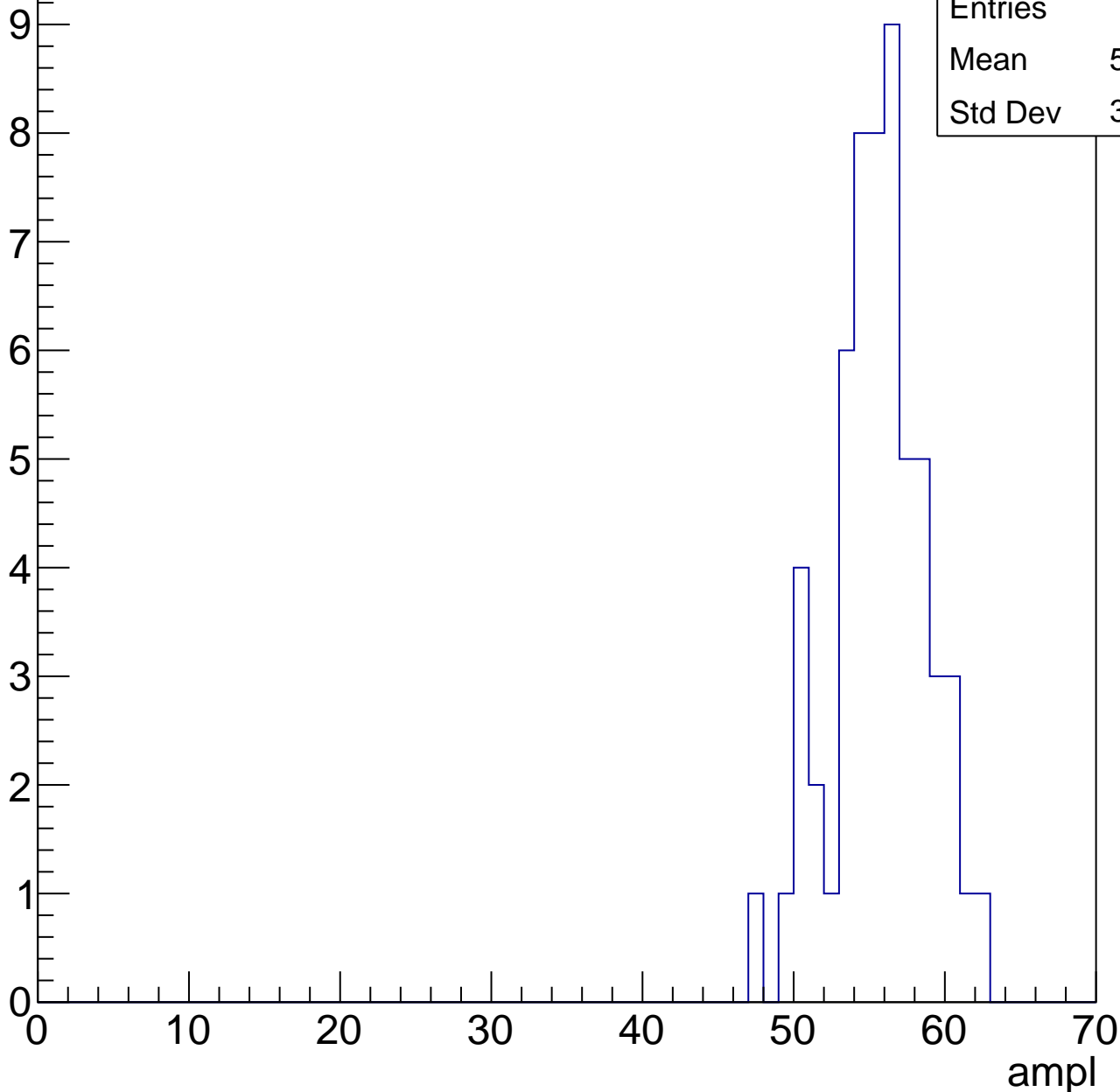


B0L002S, U26-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	55.16
Std Dev	3.089

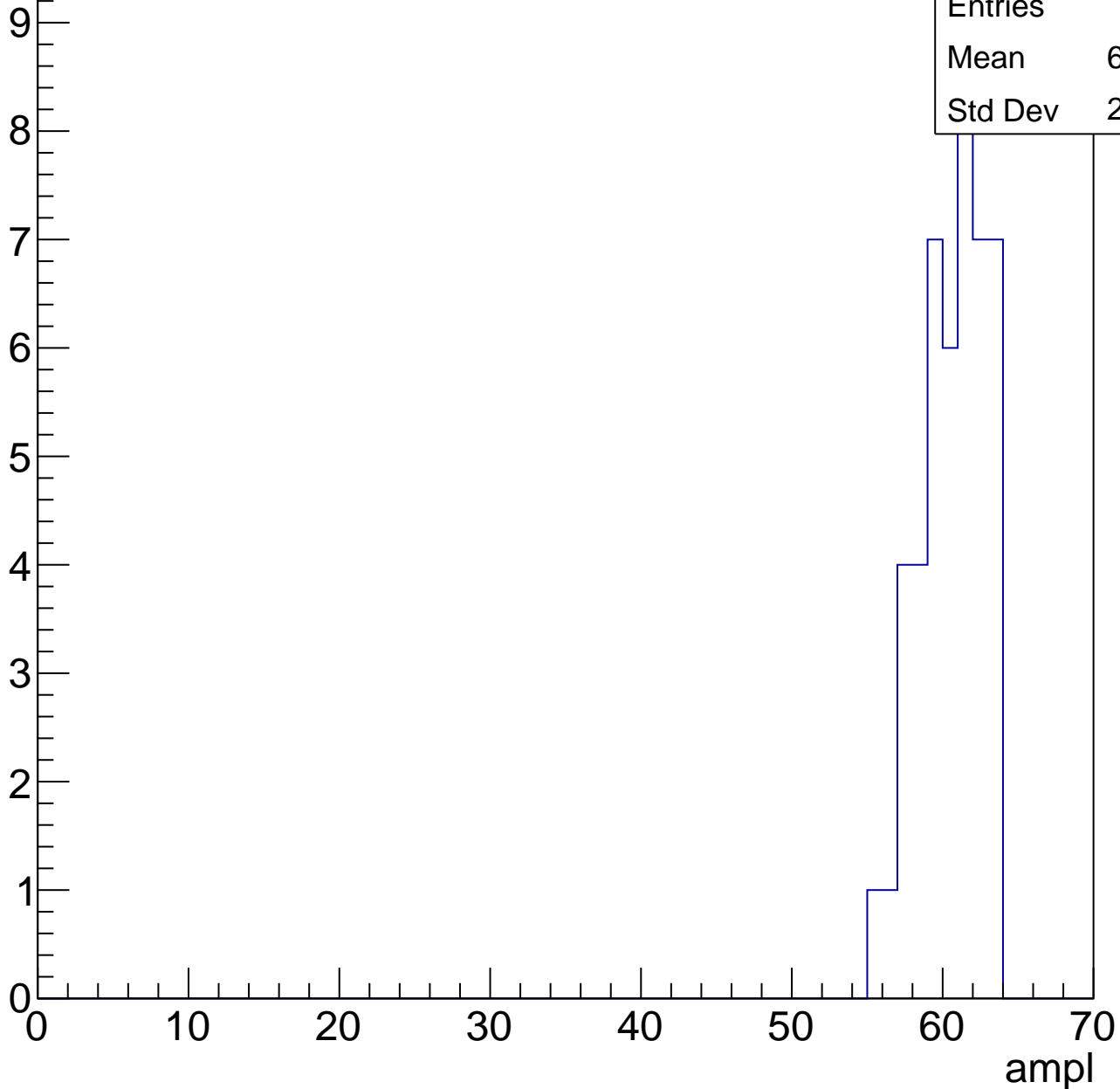


B0L002S, U26-ch50, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

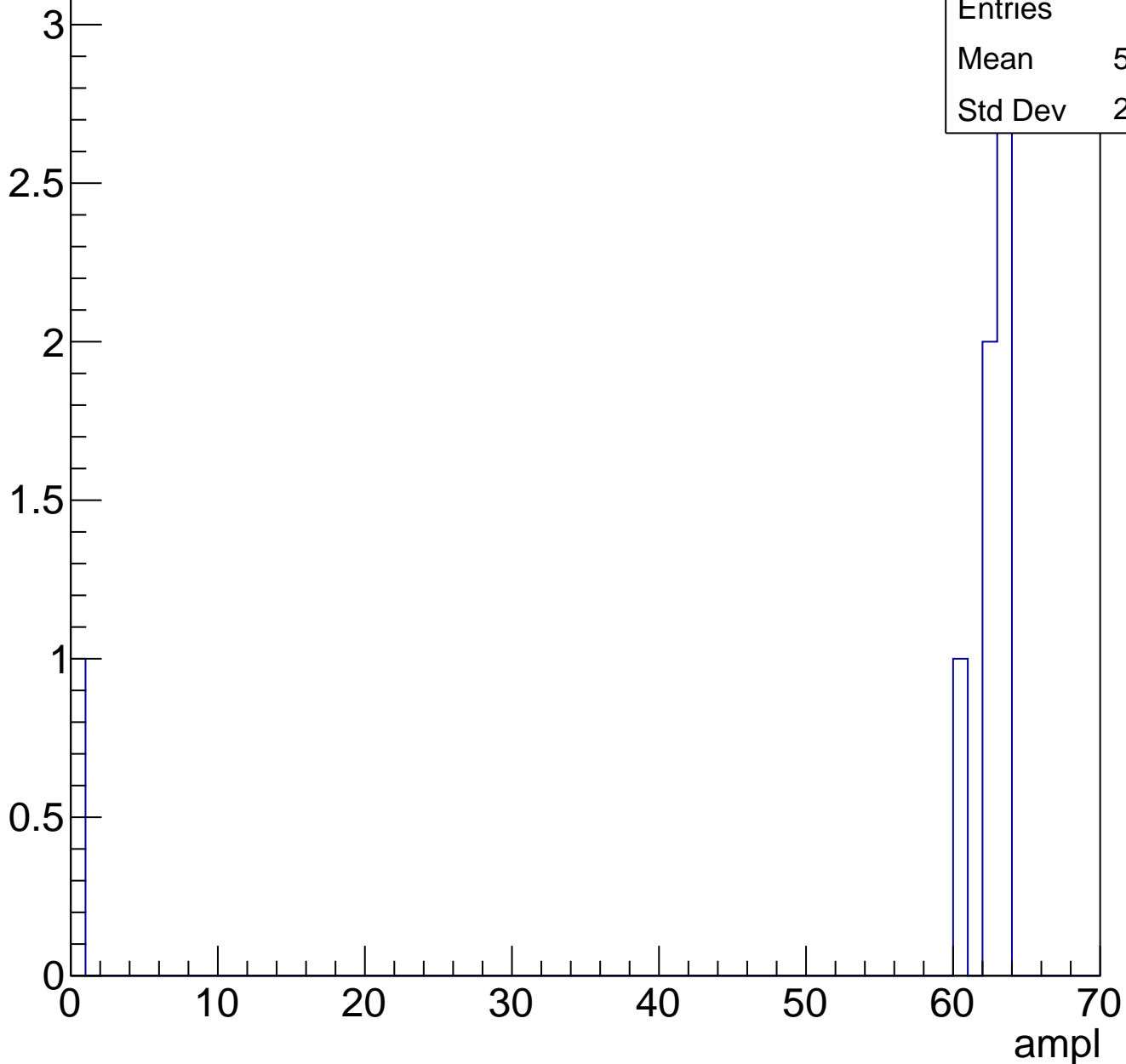
Entries	46
Mean	60.17
Std Dev	2.078



B0L002S, U26-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch51, adc0

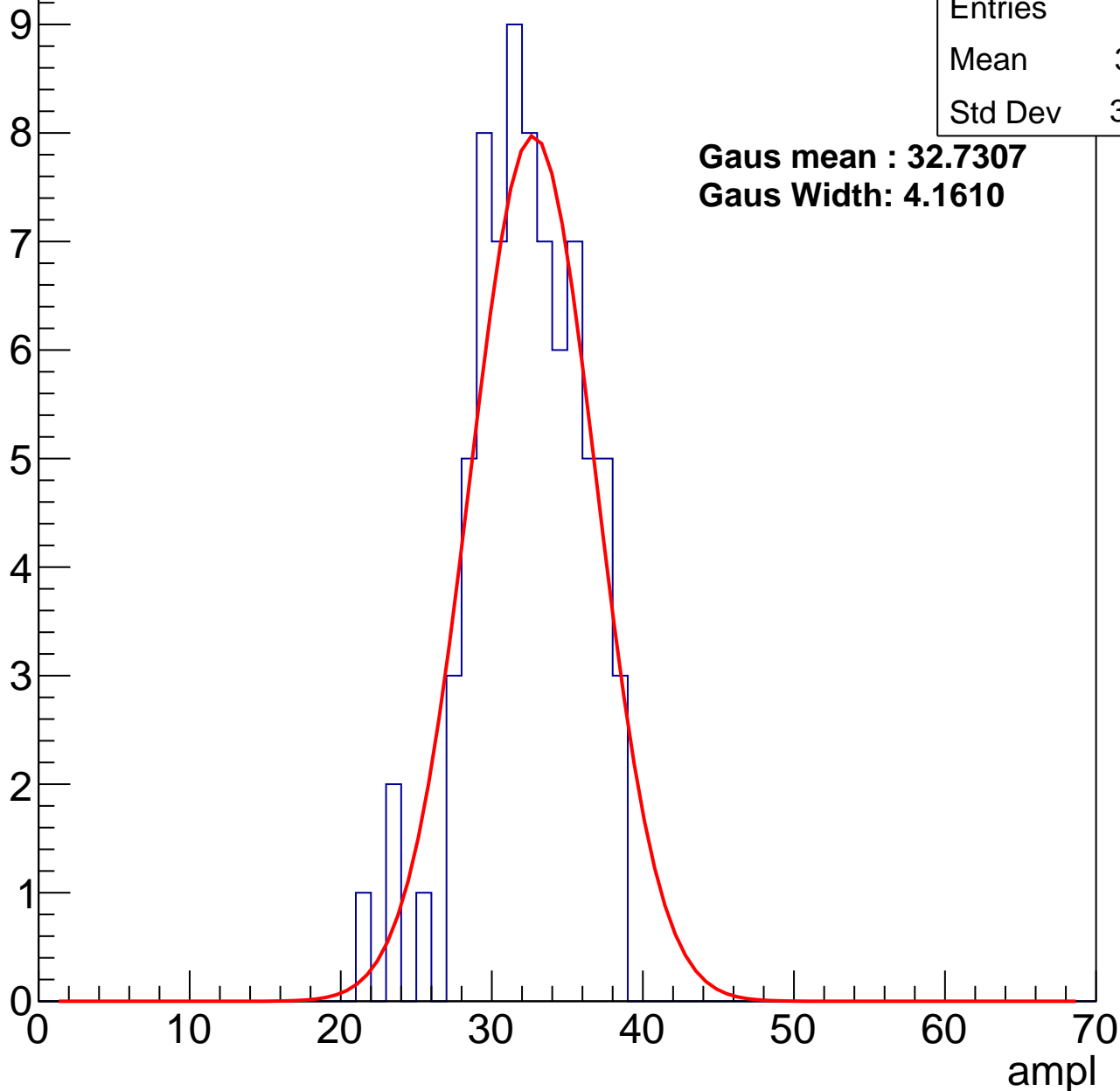
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	31.81
Std Dev	3.604

Gaus mean : 32.7307

Gaus Width: 4.1610



B0L002S, U26-ch51, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	39.64
Std Dev	3.592

Gaus mean : 40.5810

Gaus Width: 4.1962

10

8

6

4

2

0

0

10

20

30

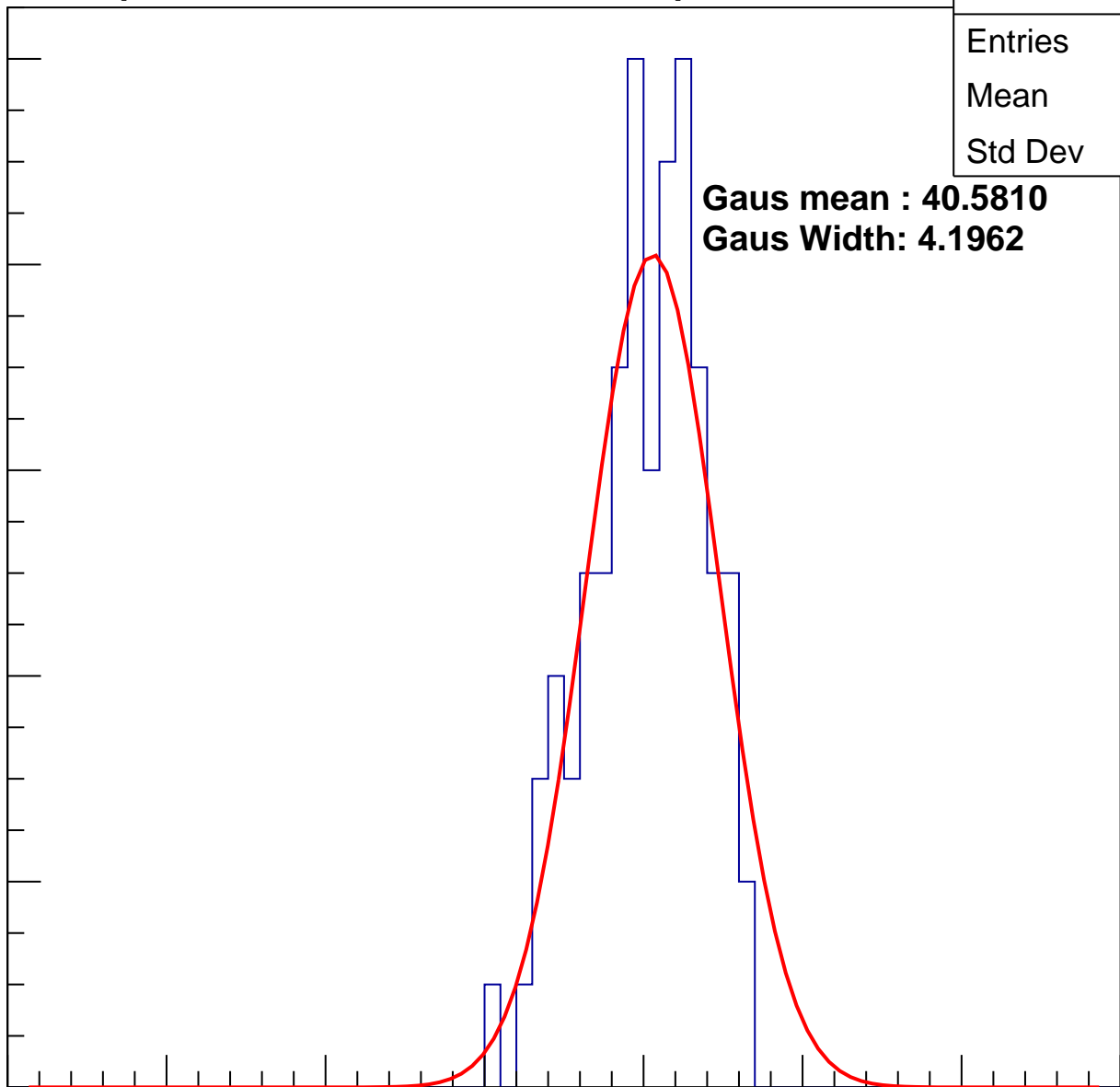
40

50

60

70

ampl



B0L002S, U26-ch51, adc2

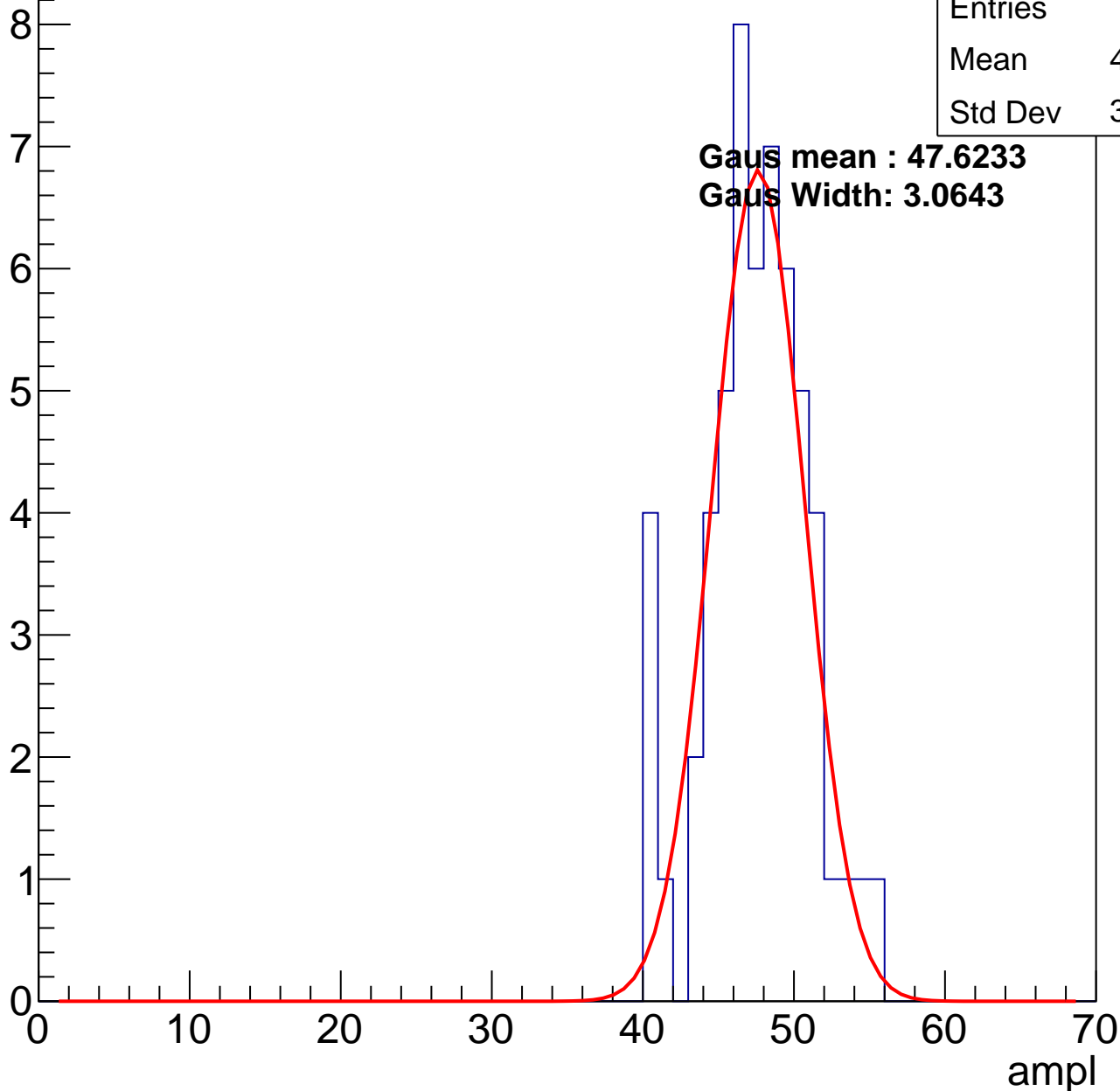
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	47.07
Std Dev	3.385

Gaus mean : 47.6233

Gaus Width: 3.0643

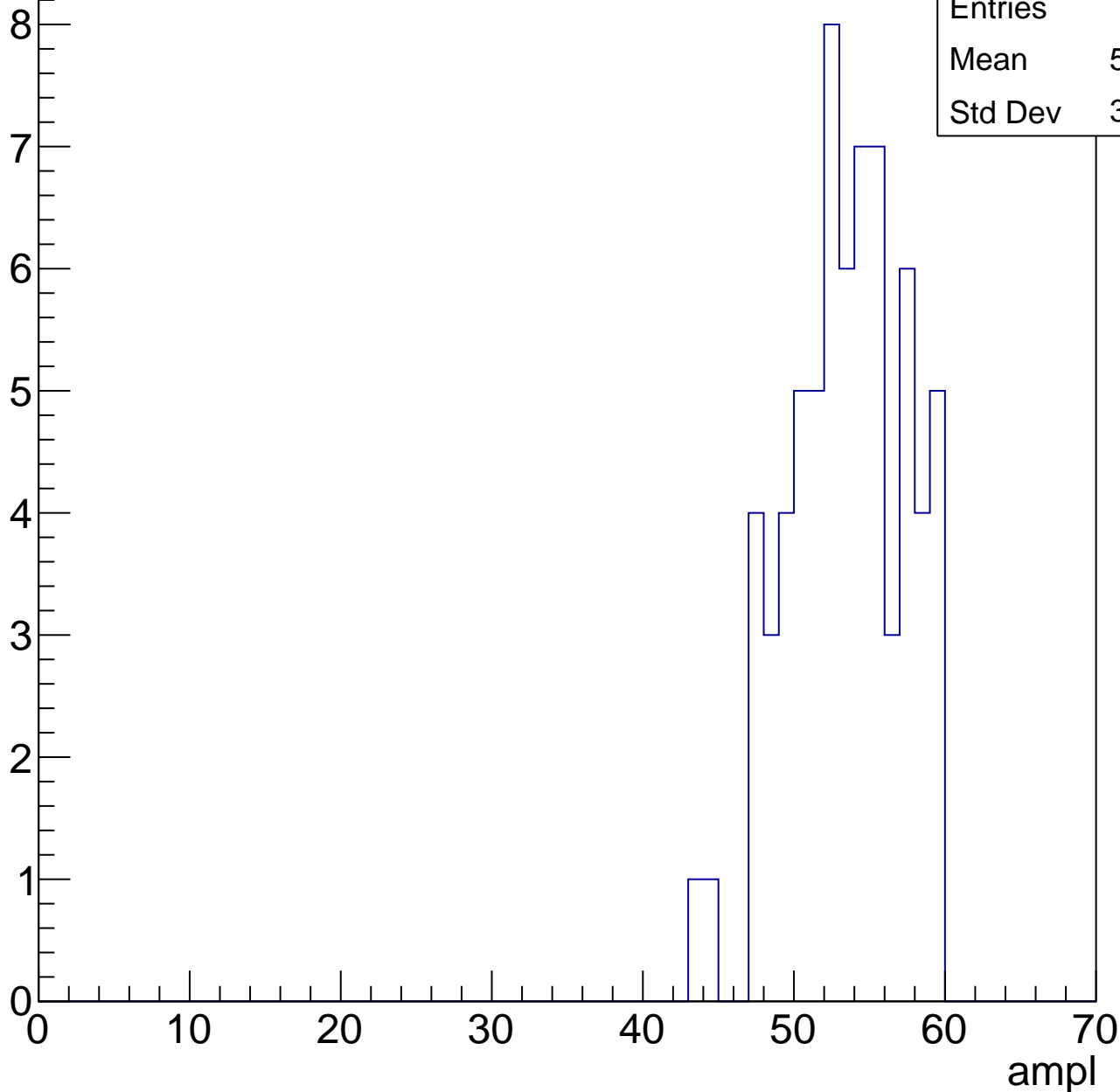


B0L002S, U26-ch51, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	52.96
Std Dev	3.759

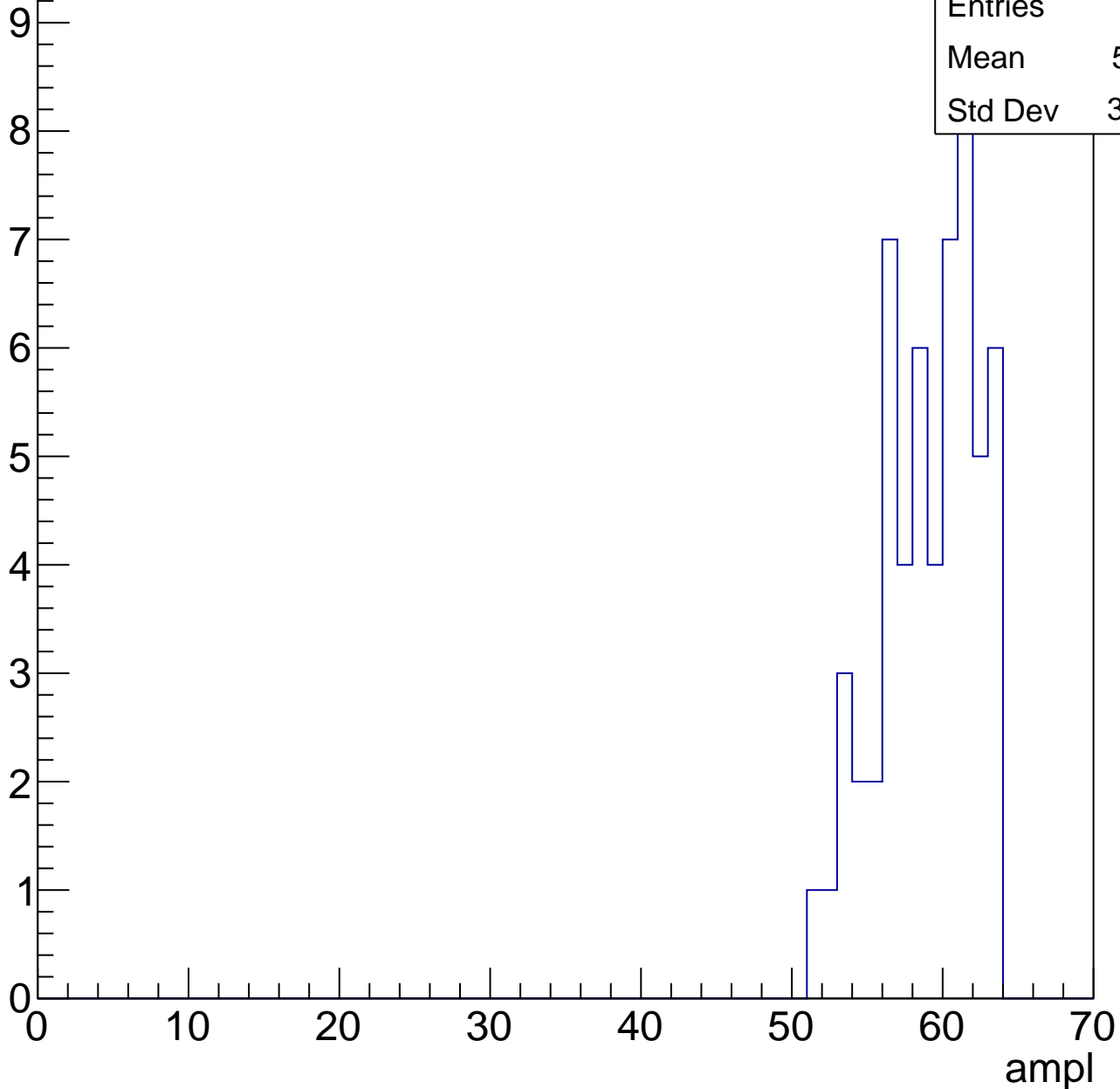


B0L002S, U26-ch51, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	58.61
Std Dev	3.144

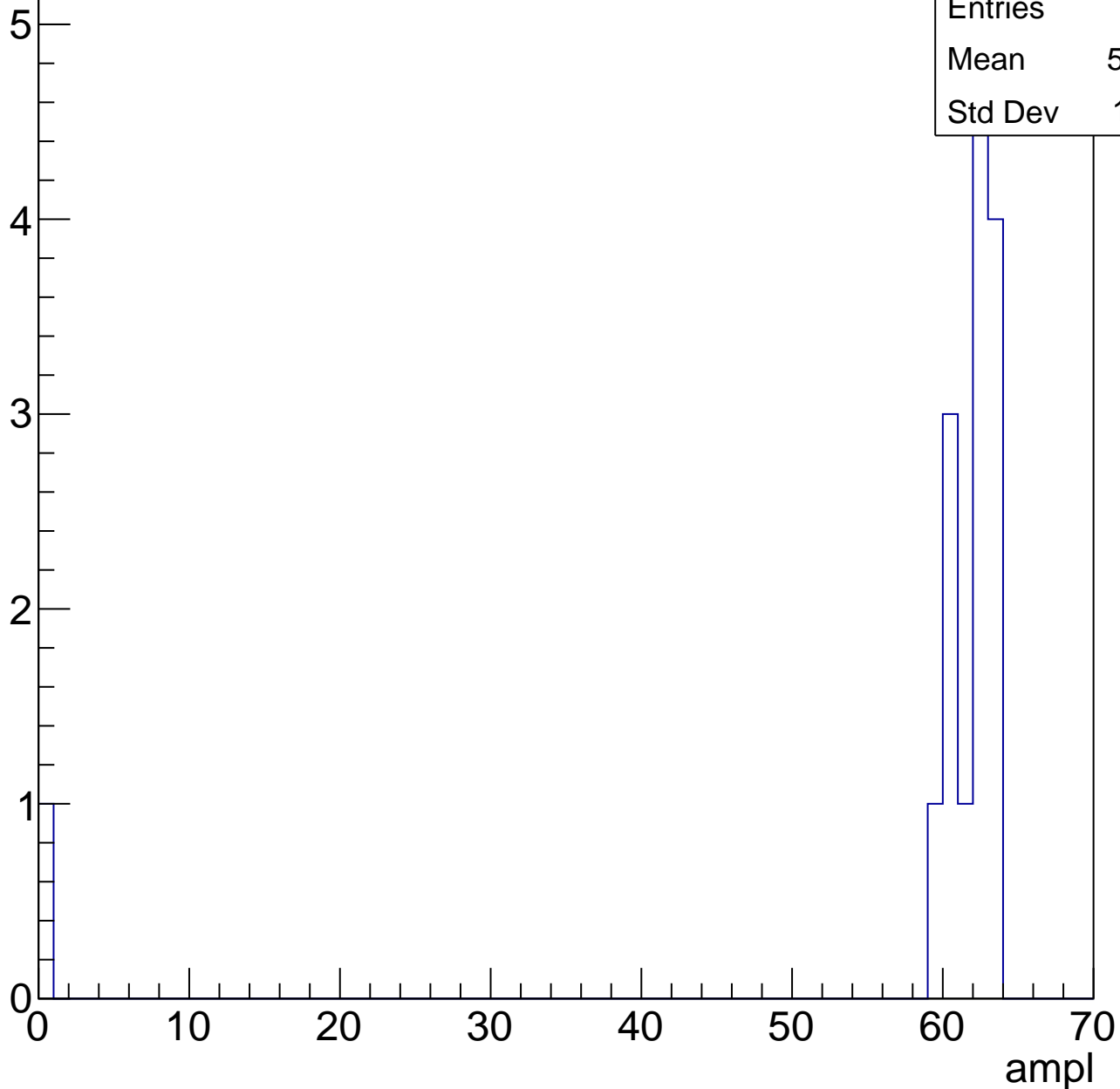


B0L002S, U26-ch51, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	15
Mean	57.47
Std Dev	15.41



B0L002S, U26-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch52, adc0

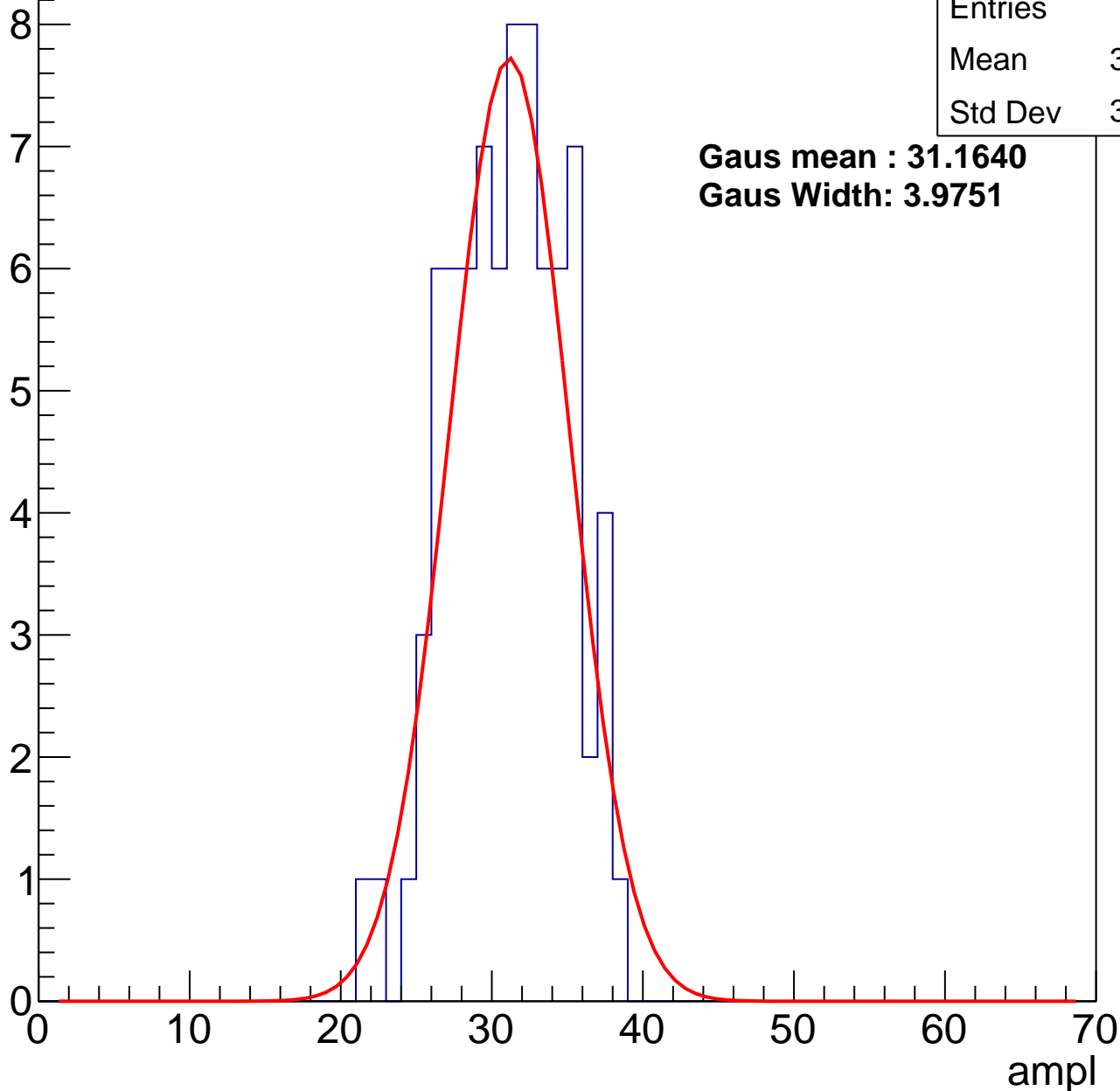
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.63
Std Dev	3.735

Gaus mean : 31.1640

Gaus Width: 3.9751



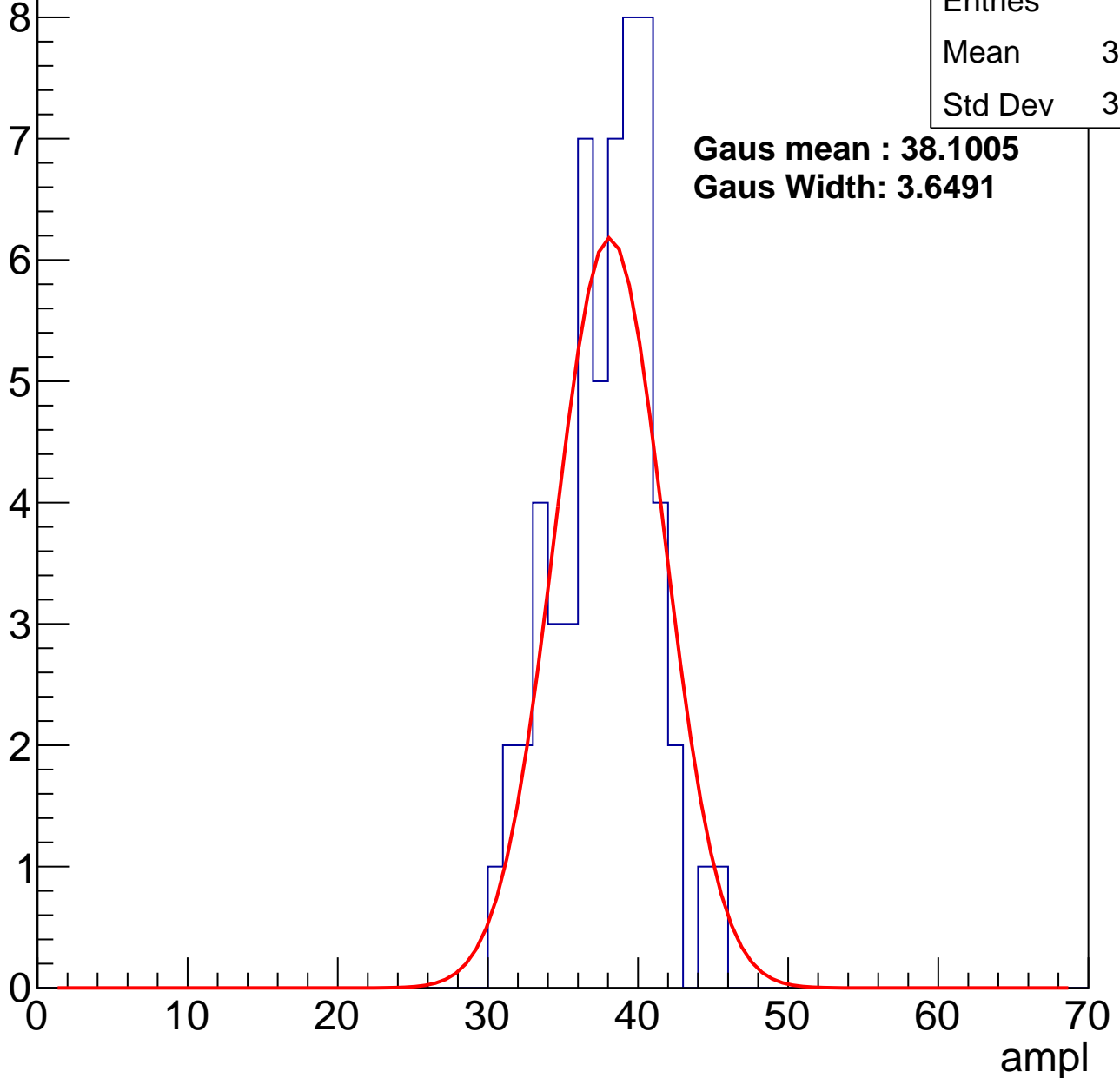
B0L002S, U26-ch52, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.36
Std Dev	3.247

Gaus mean : 38.1005
Gaus Width: 3.6491



B0L002S, U26-ch52, adc2

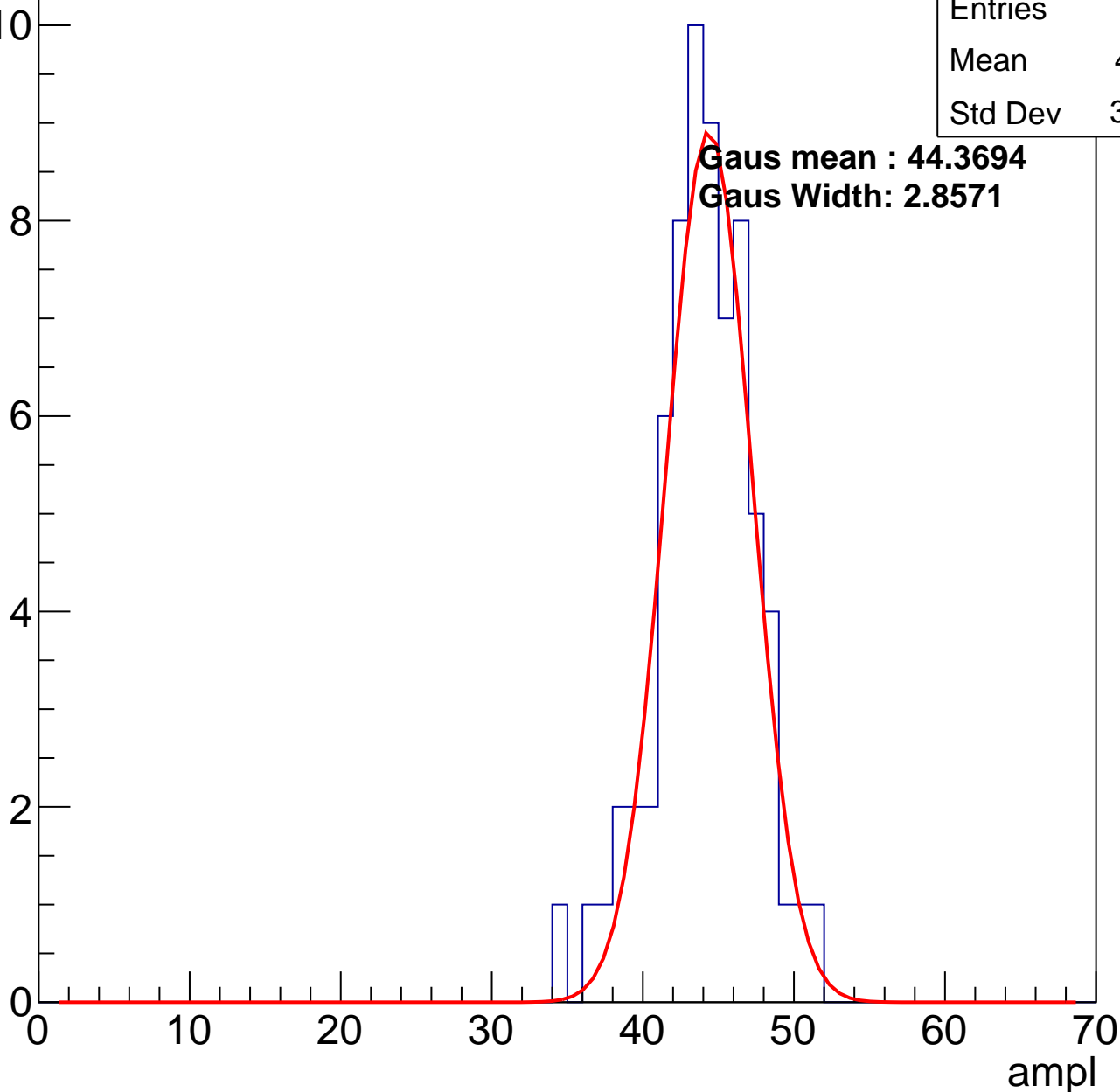
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.61
Std Dev	3.209

Gaus mean : 44.3694

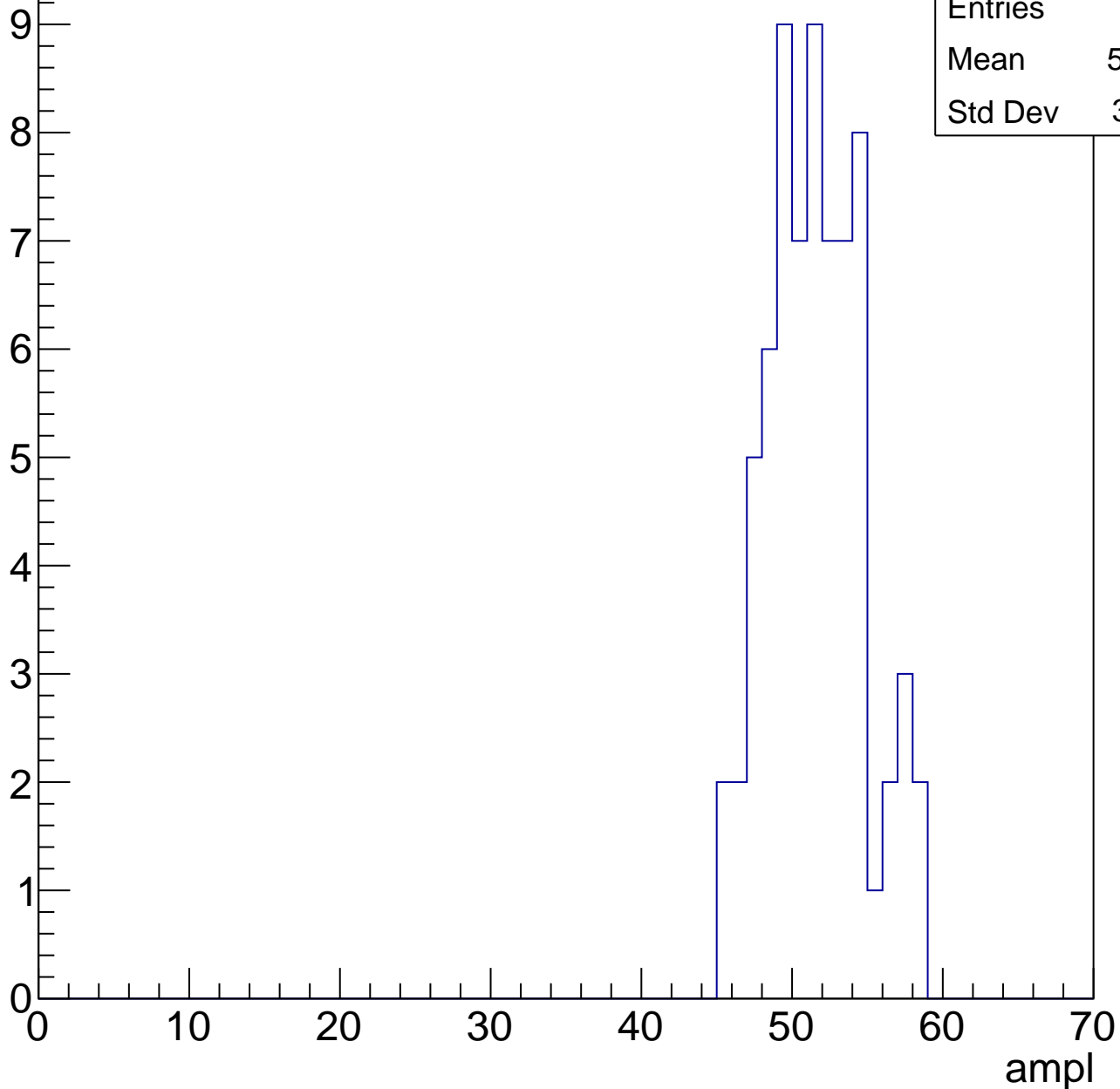
Gaus Width: 2.8571



B0L002S, U26-ch52, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

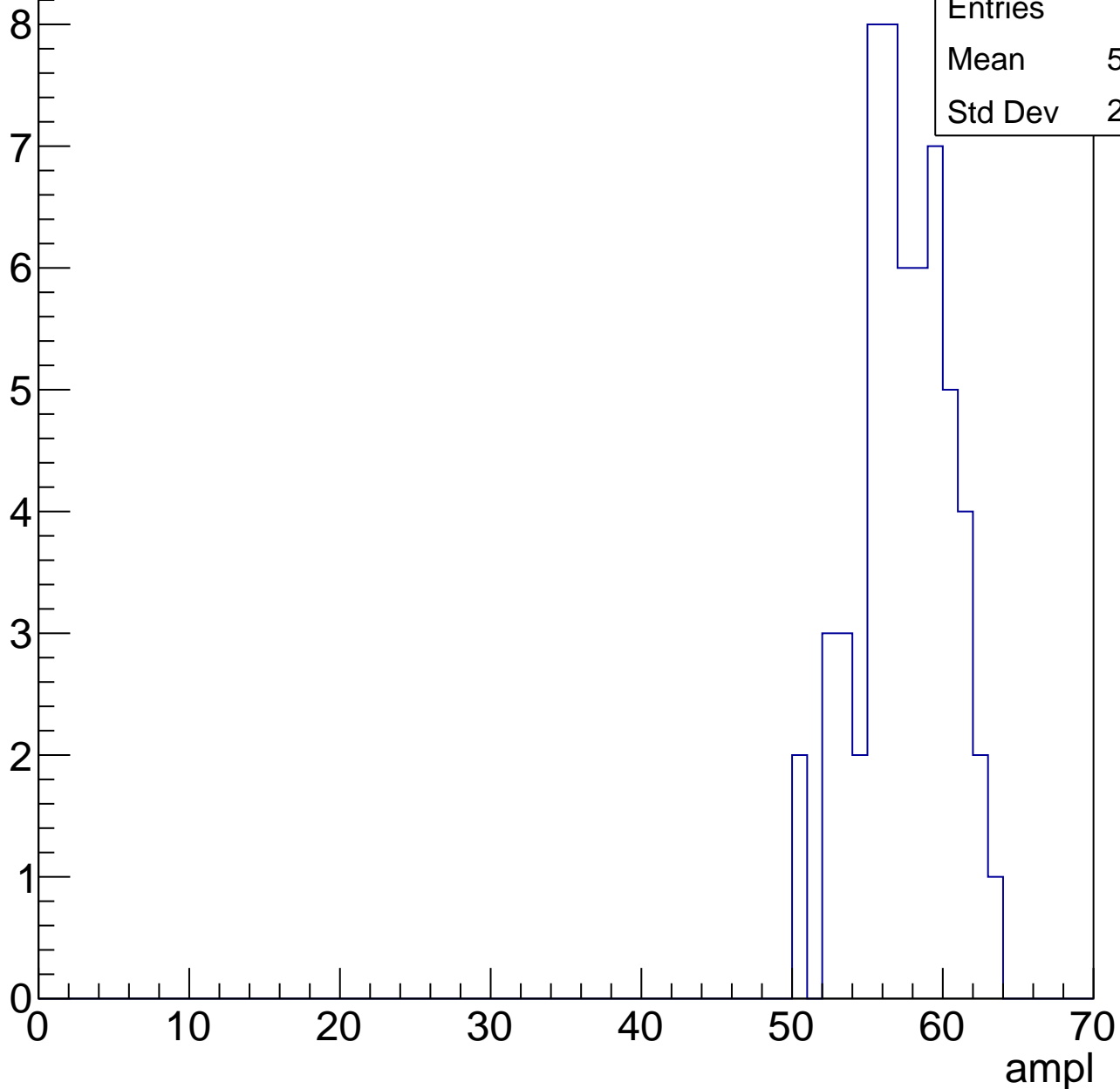


B0L002S, U26-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.93
Std Dev	2.985

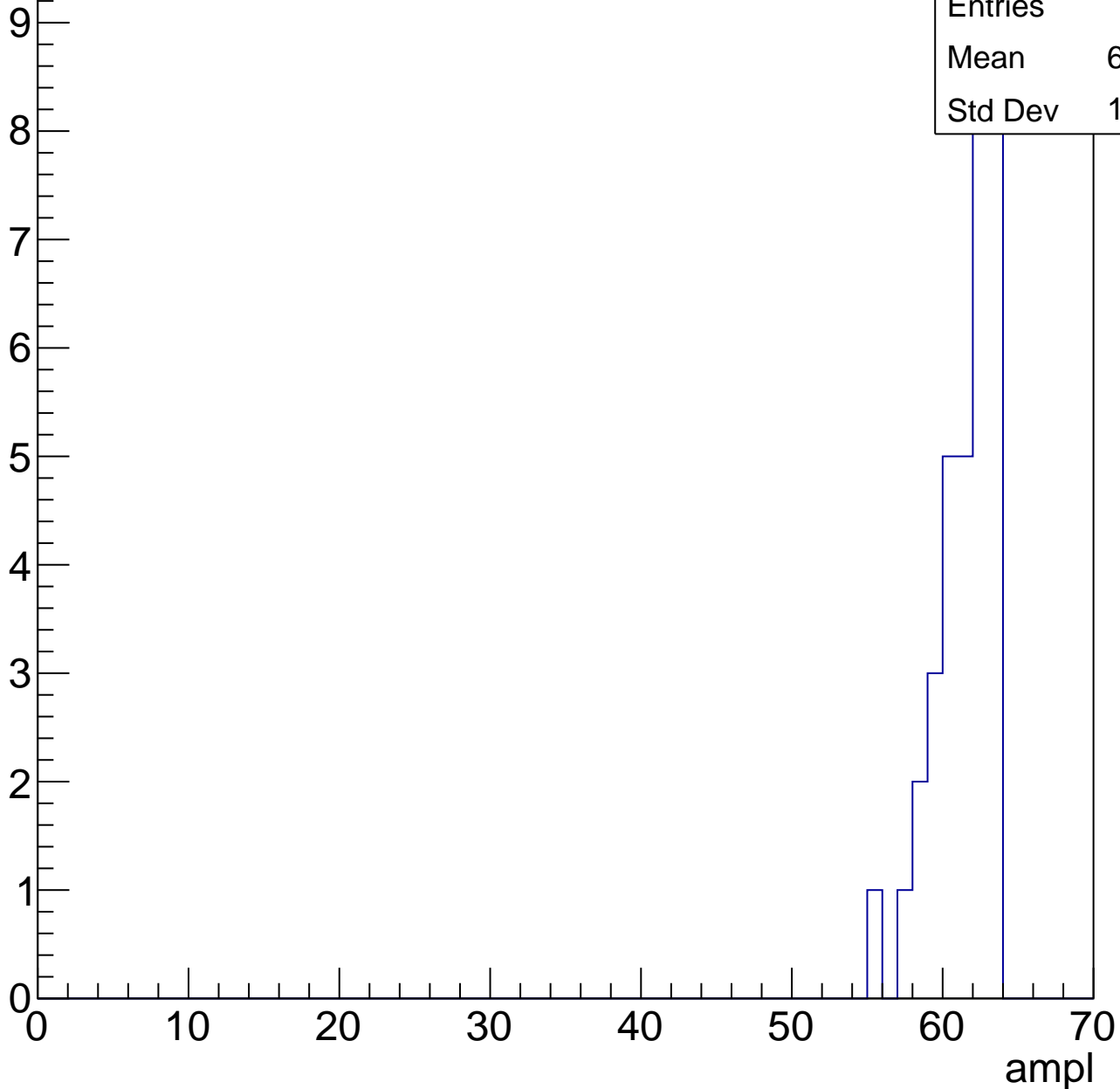


B0L002S, U26-ch52, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

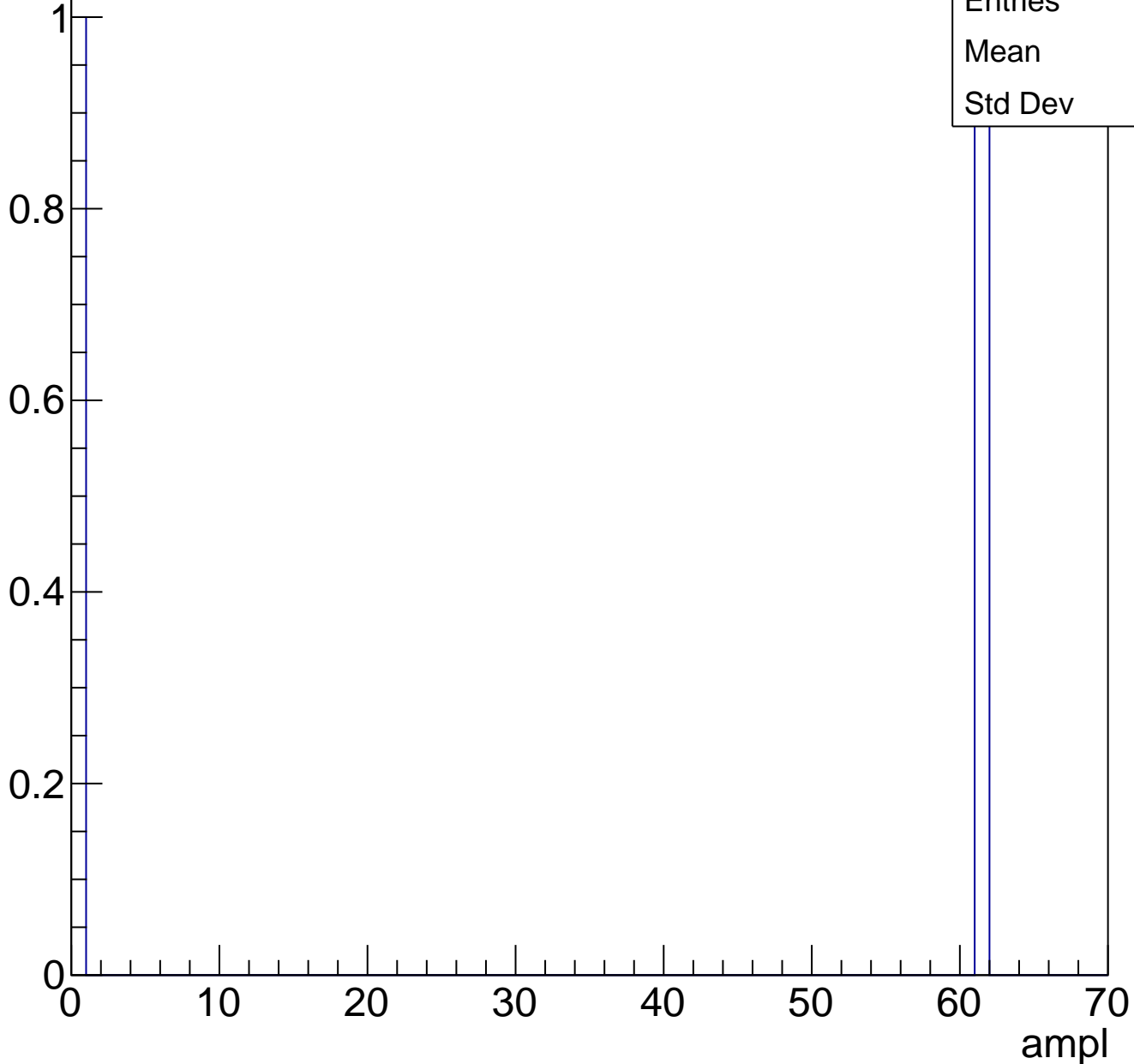
Entries	34
Mean	60.97
Std Dev	1.963



B0L002S, U26-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch52, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch53, adc0

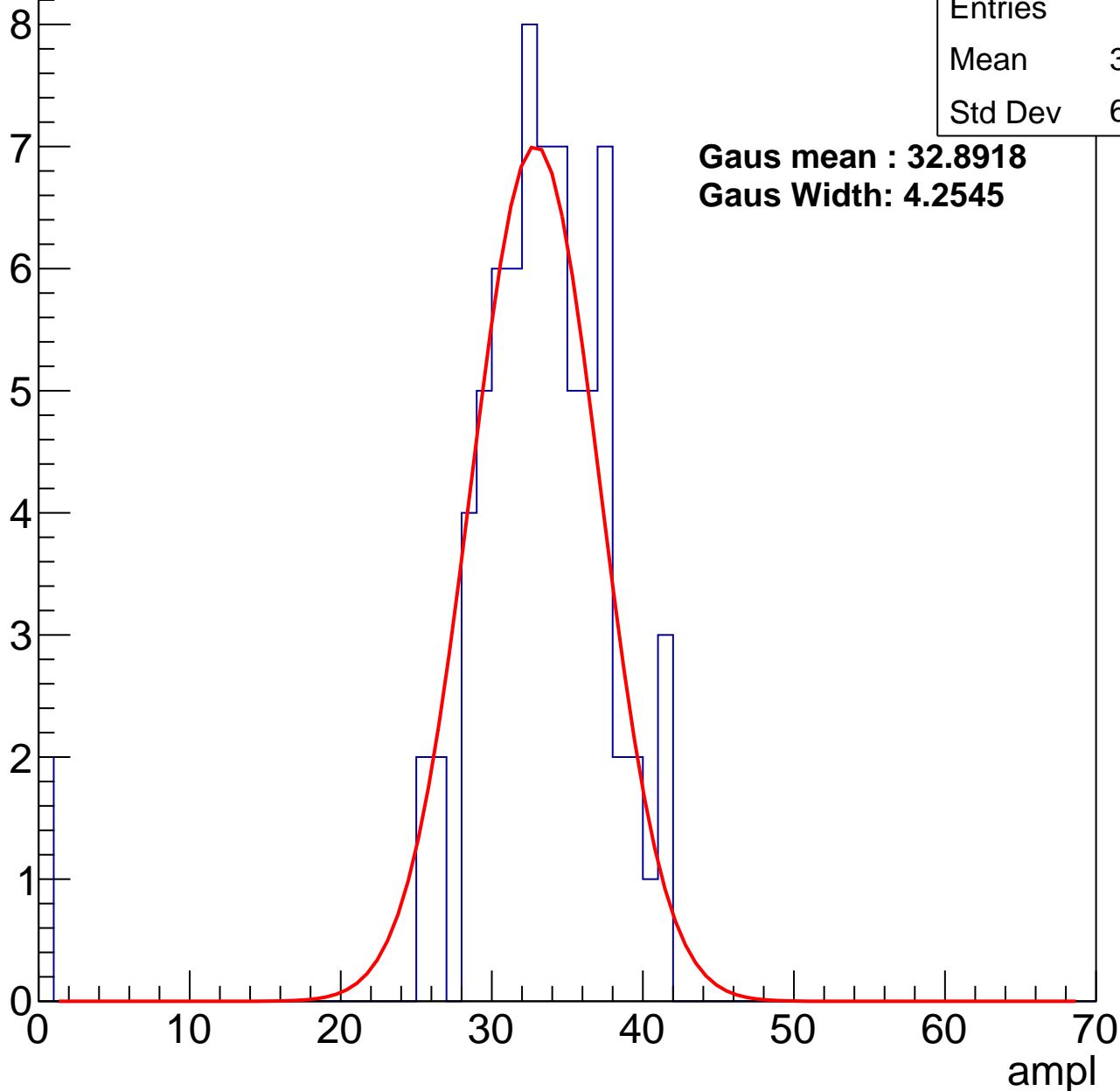
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	32.18
Std Dev	6.542

Gaus mean : 32.8918

Gaus Width: 4.2545



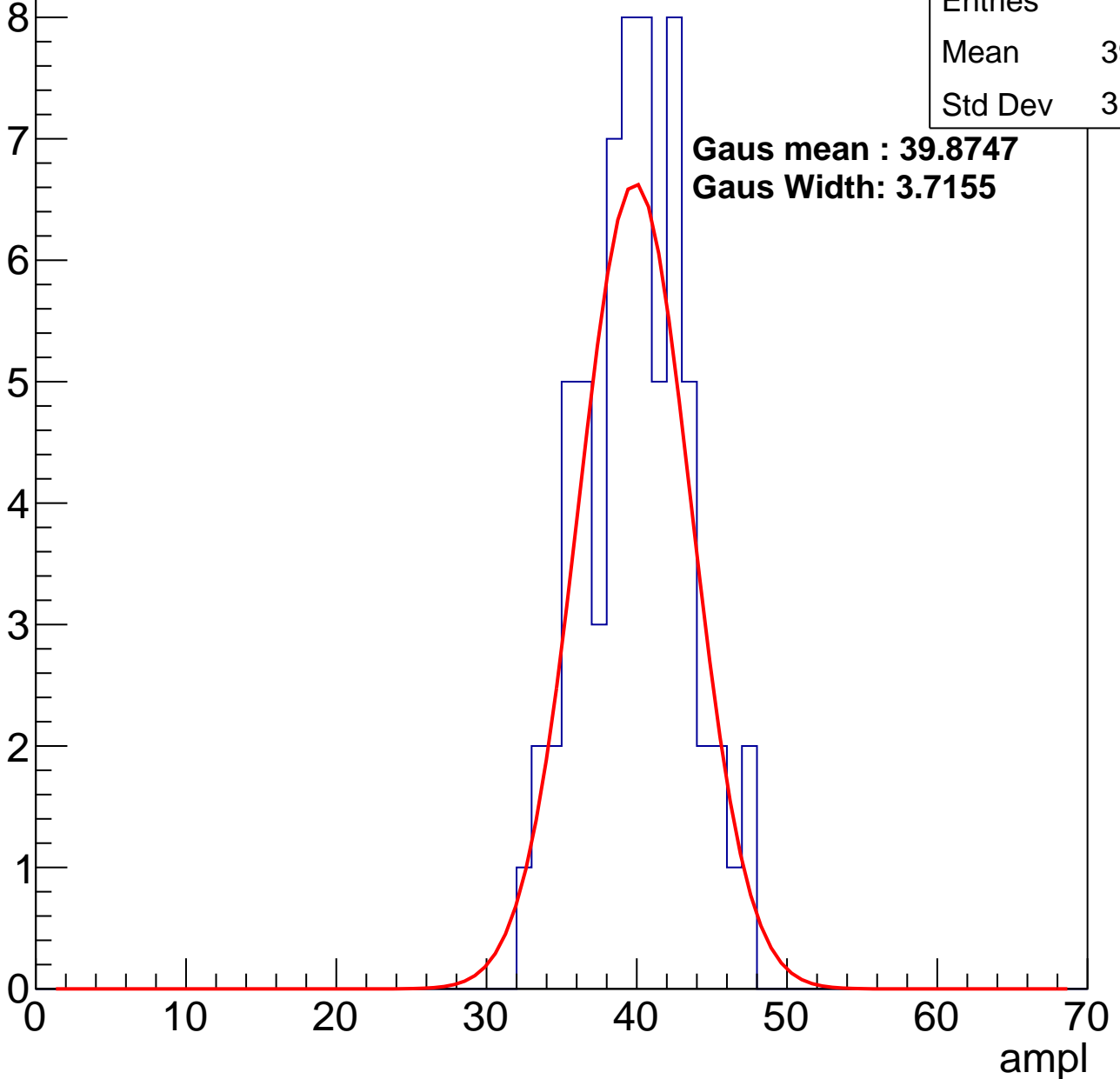
B0L002S, U26-ch53, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	39.45
Std Dev	3.439

Gaus mean : 39.8747
Gaus Width: 3.7155



B0L002S, U26-ch53, adc2

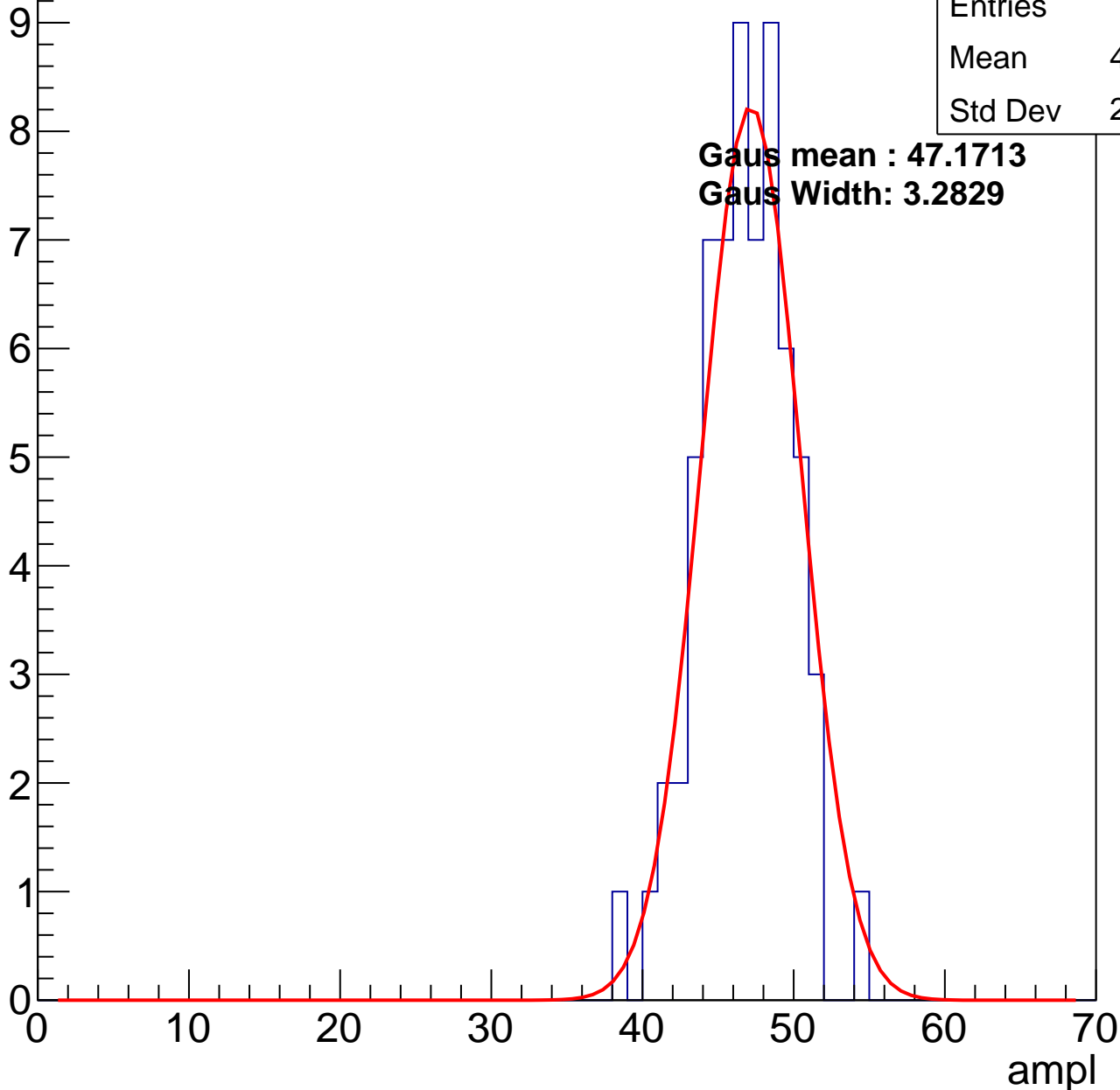
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	46.28
Std Dev	2.969

Gaus mean : 47.1713

Gaus Width: 3.2829

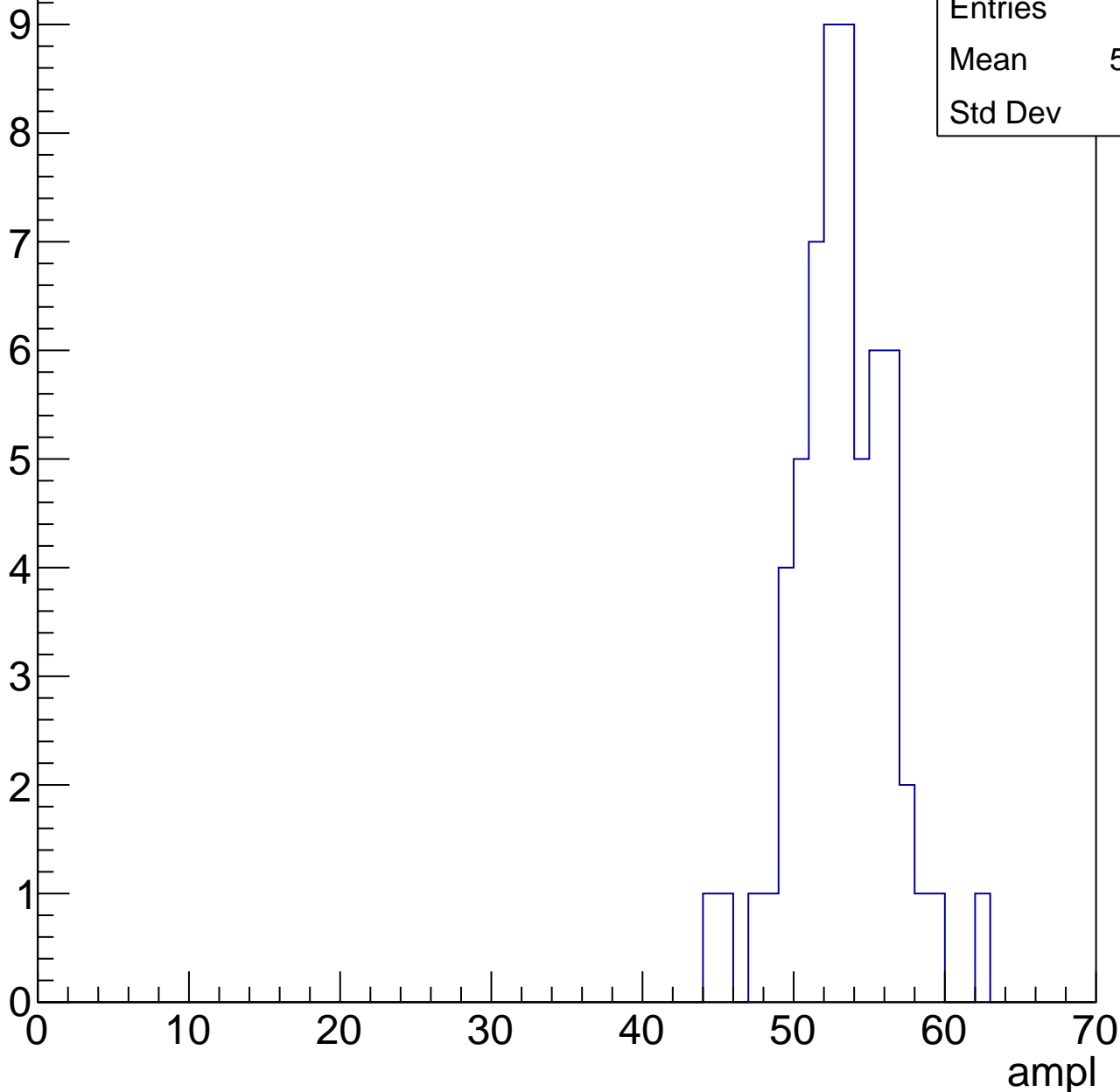


B0L002S, U26-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

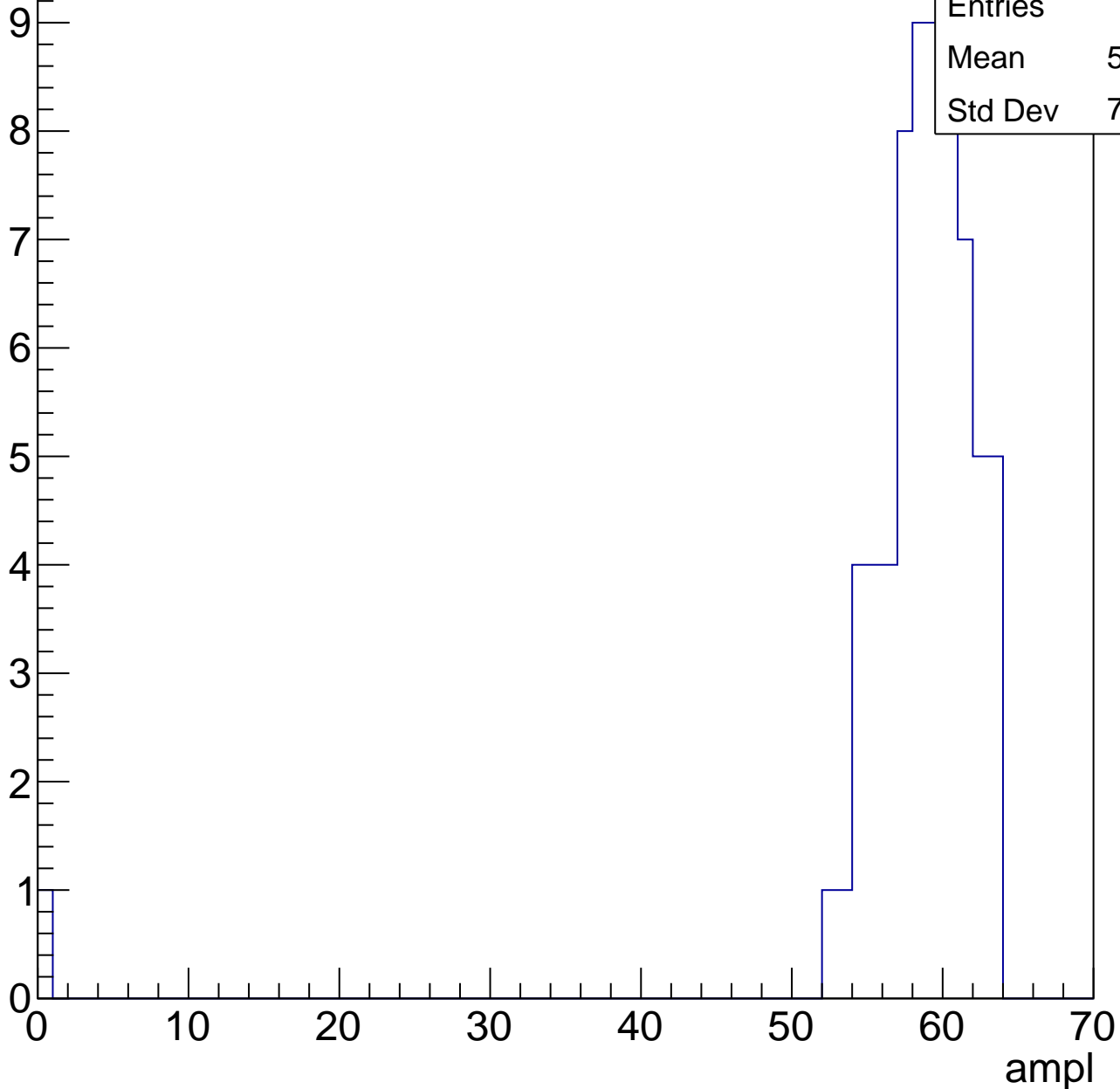
Entries	60
Mean	52.68
Std Dev	3.17



B0L002S, U26-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

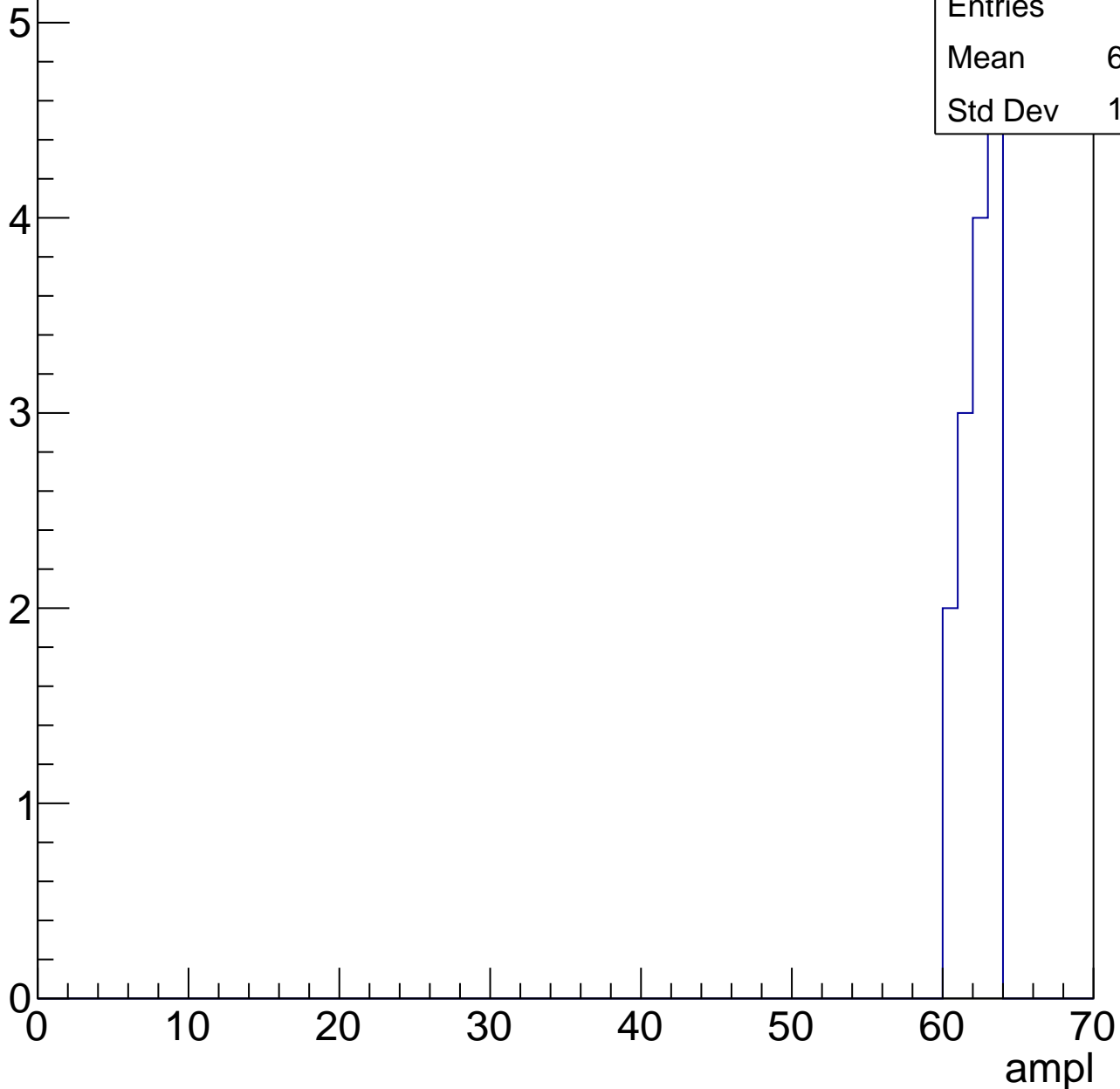


B0L002S, U26-ch53, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	14
Mean	61.86
Std Dev	1.059



B0L002S, U26-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch54, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	28.99
Std Dev	4.578

Gaus mean : 29.7961

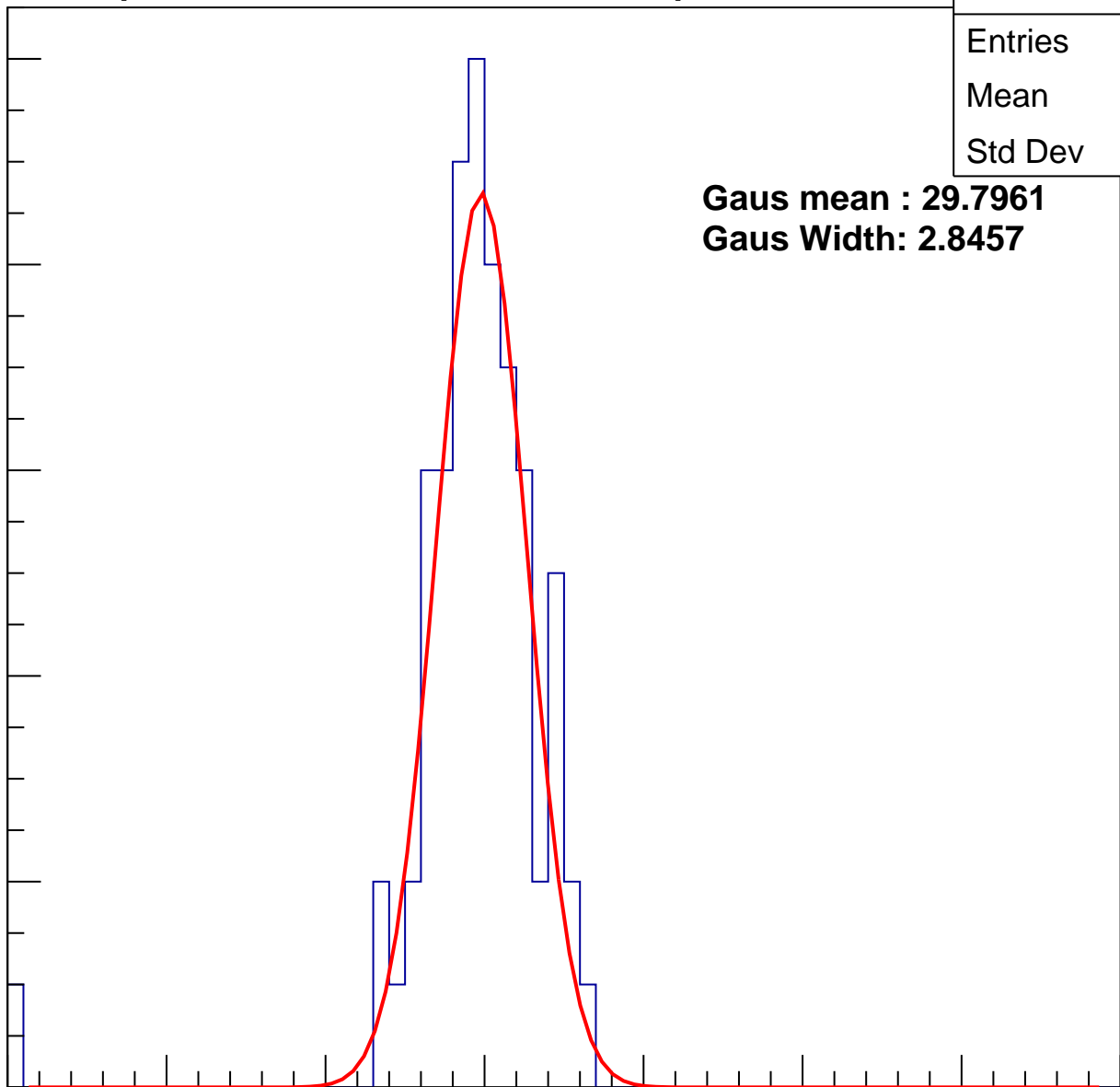
Gaus Width: 2.8457

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch54, adc1

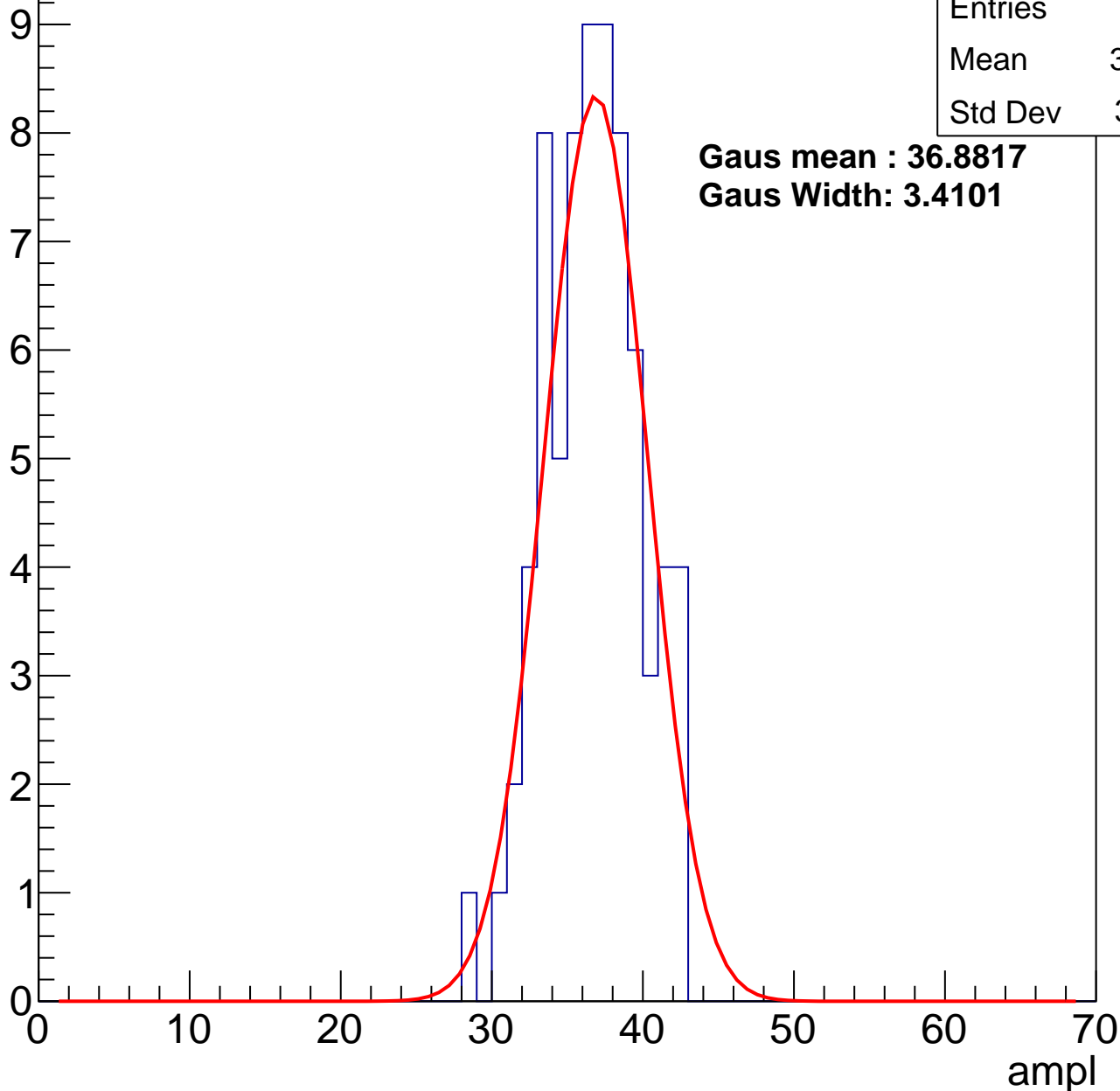
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.24
Std Dev	3.111

Gaus mean : 36.8817

Gaus Width: 3.4101



B0L002S, U26-ch54, adc2

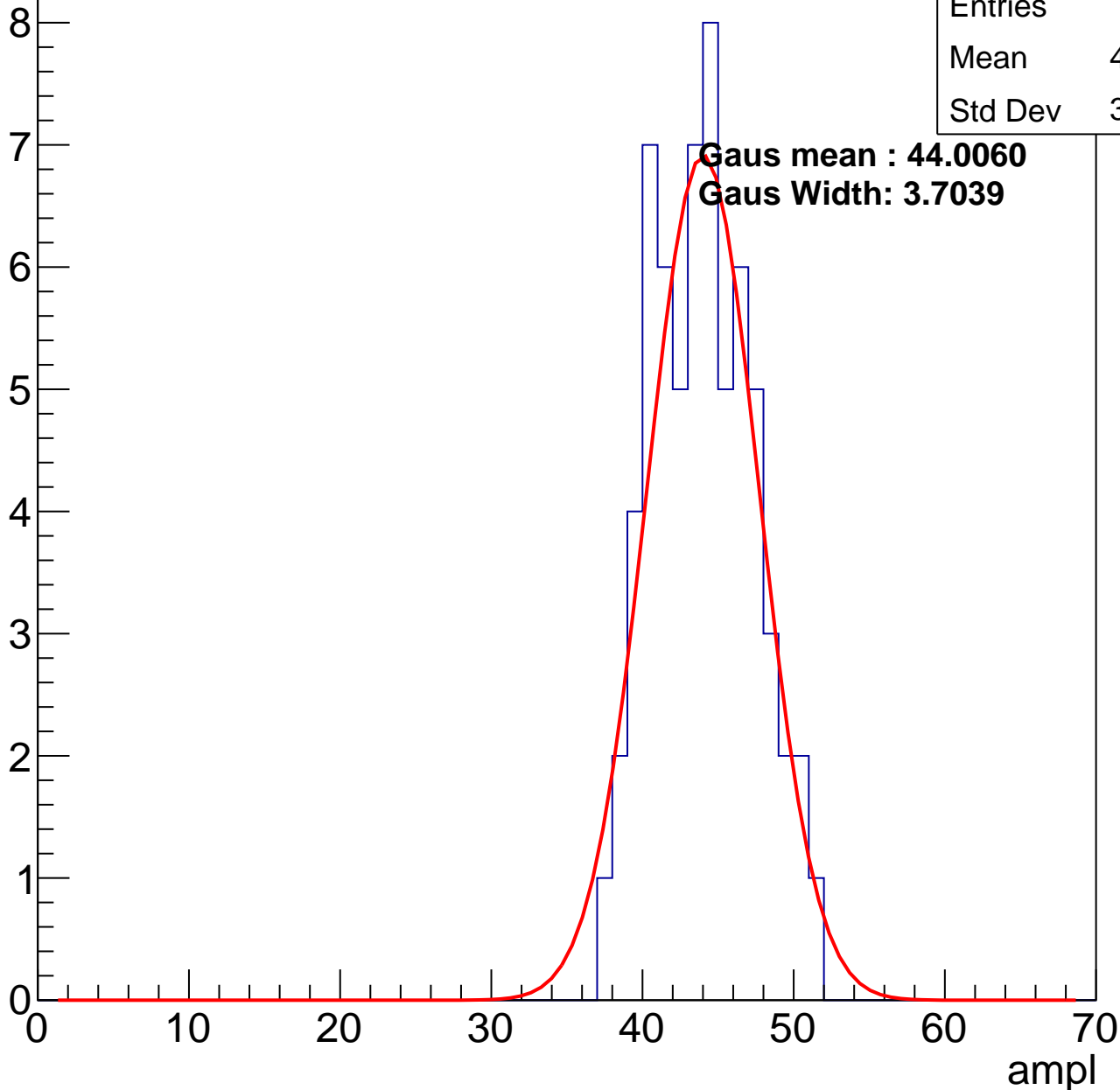
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.55
Std Dev	3.293

Gaus mean : 44.0060

Gaus Width: 3.7039

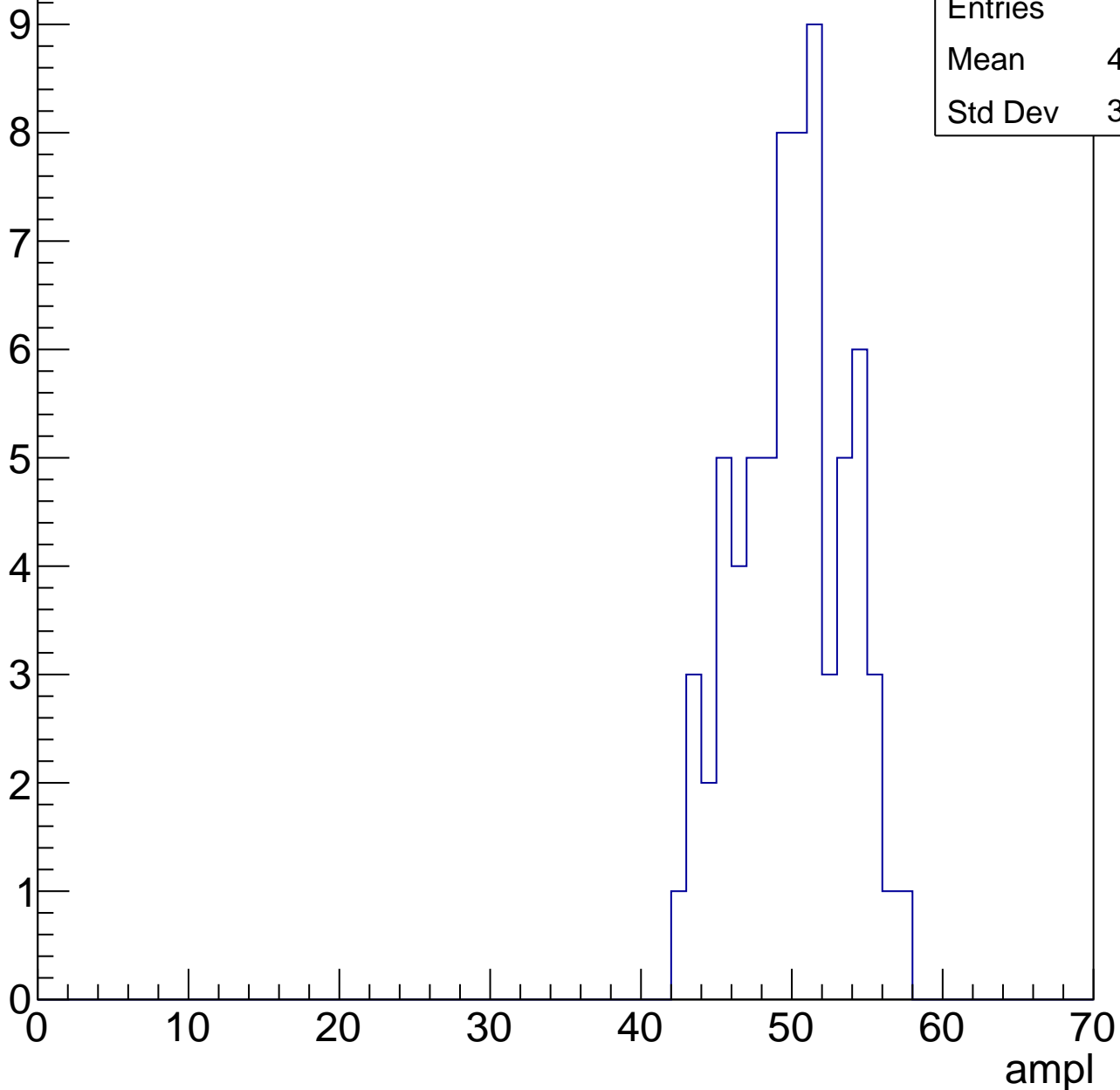


B0L002S, U26-ch54, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.52
Std Dev	3.512

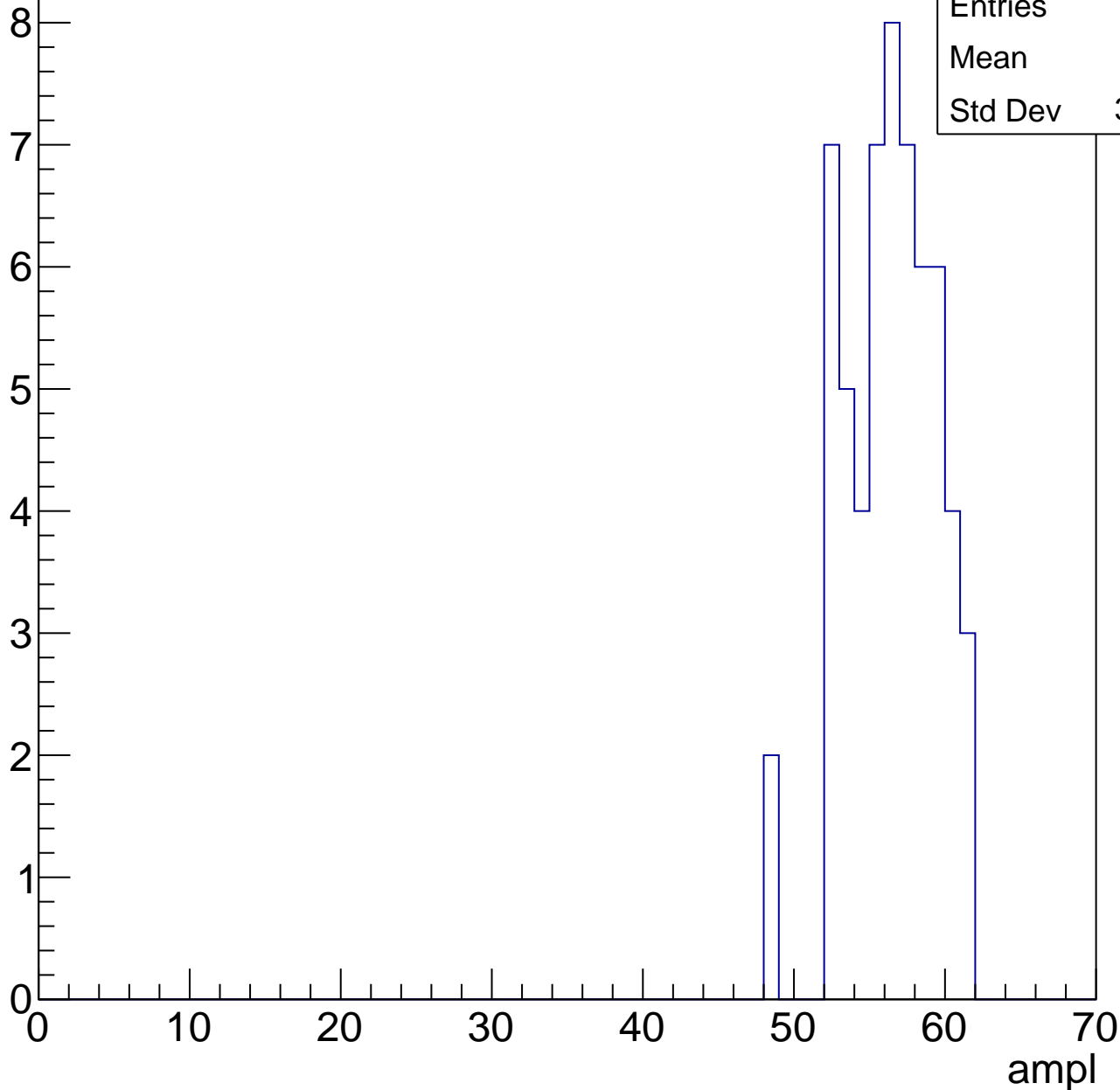


B0L002S, U26-ch54, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	55.9
Std Dev	3.001

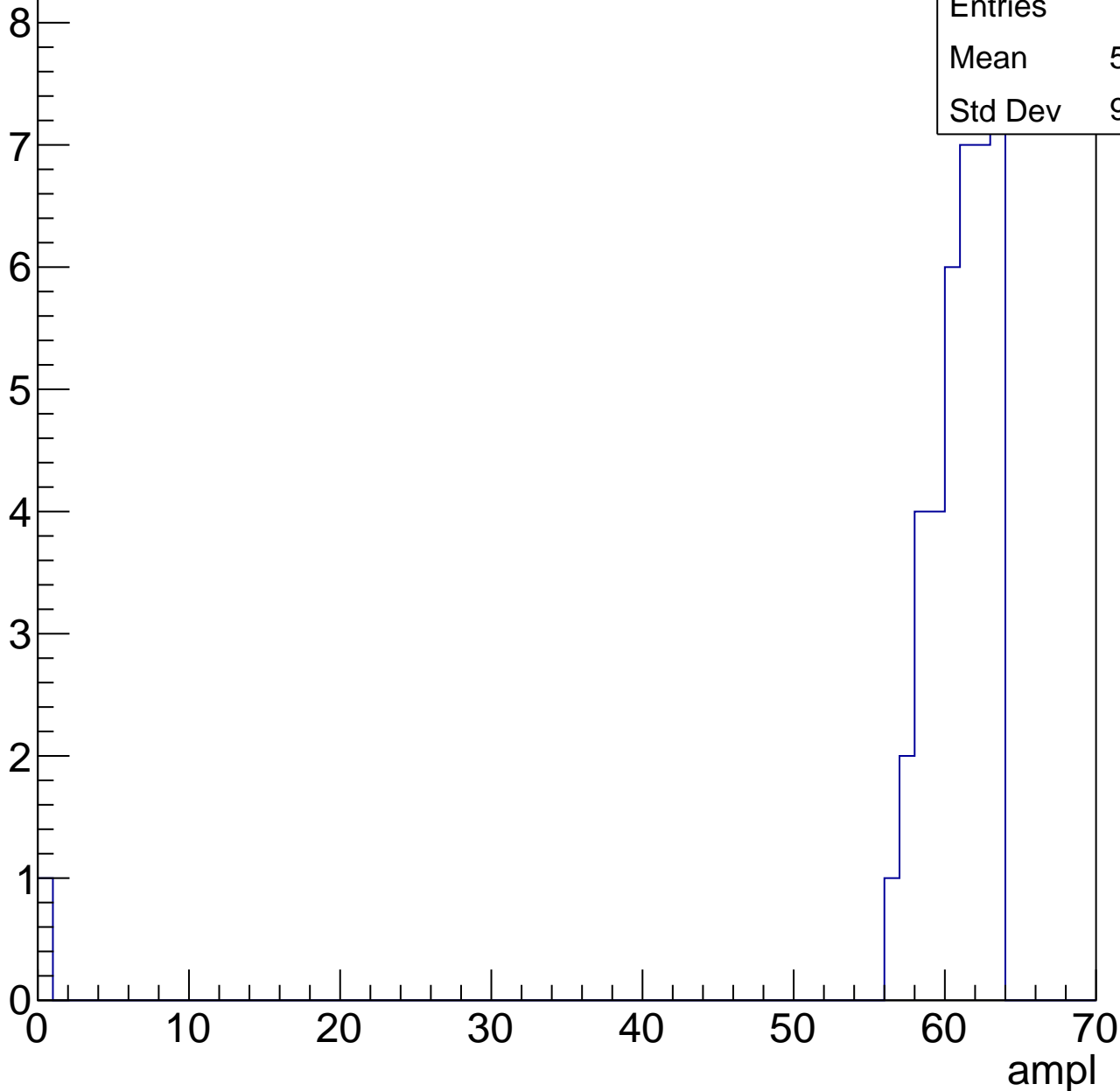


B0L002S, U26-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.08
Std Dev	9.652



B0L002S, U26-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch55, adc0

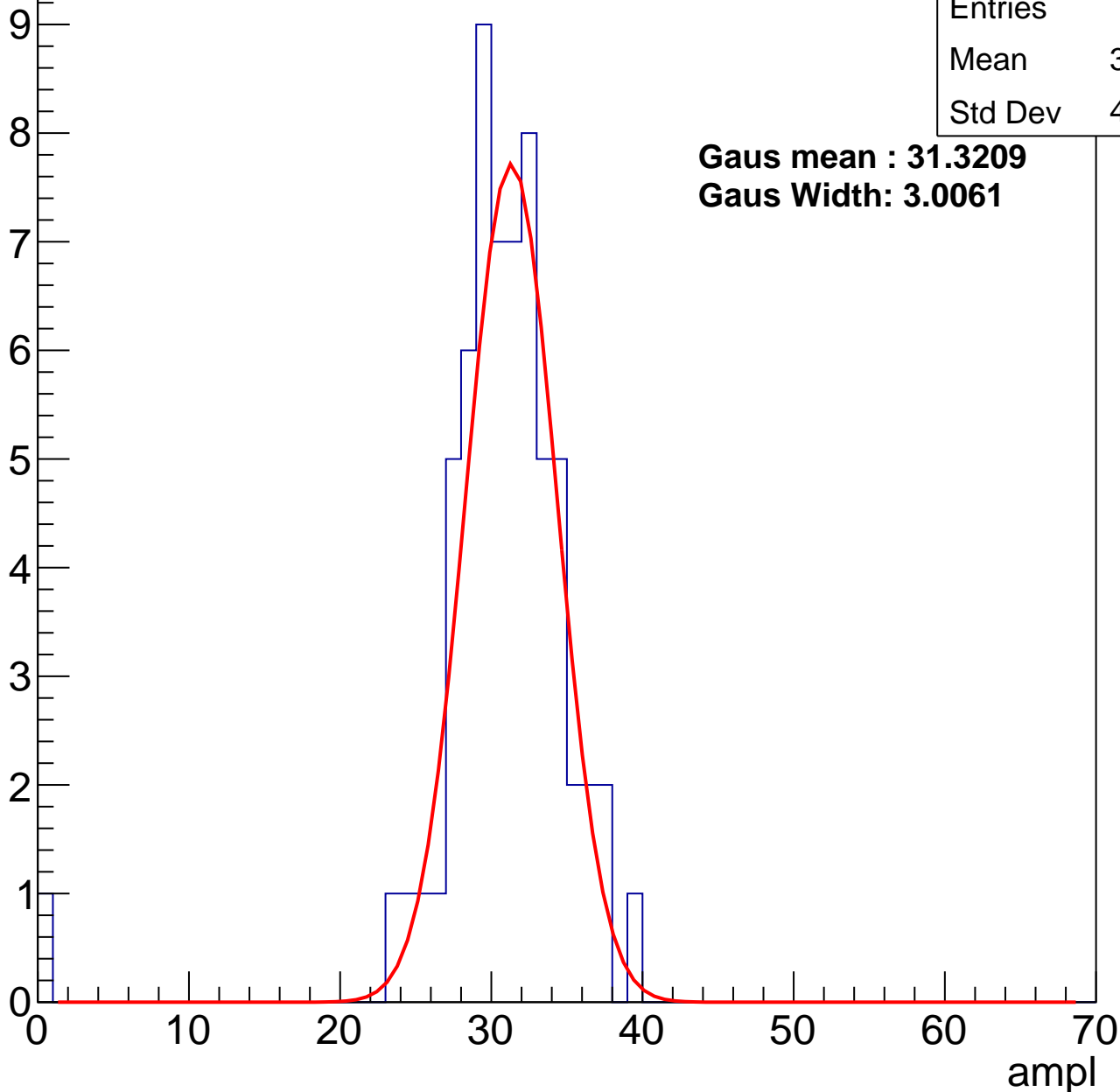
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.23
Std Dev	4.946

Gaus mean : 31.3209

Gaus Width: 3.0061



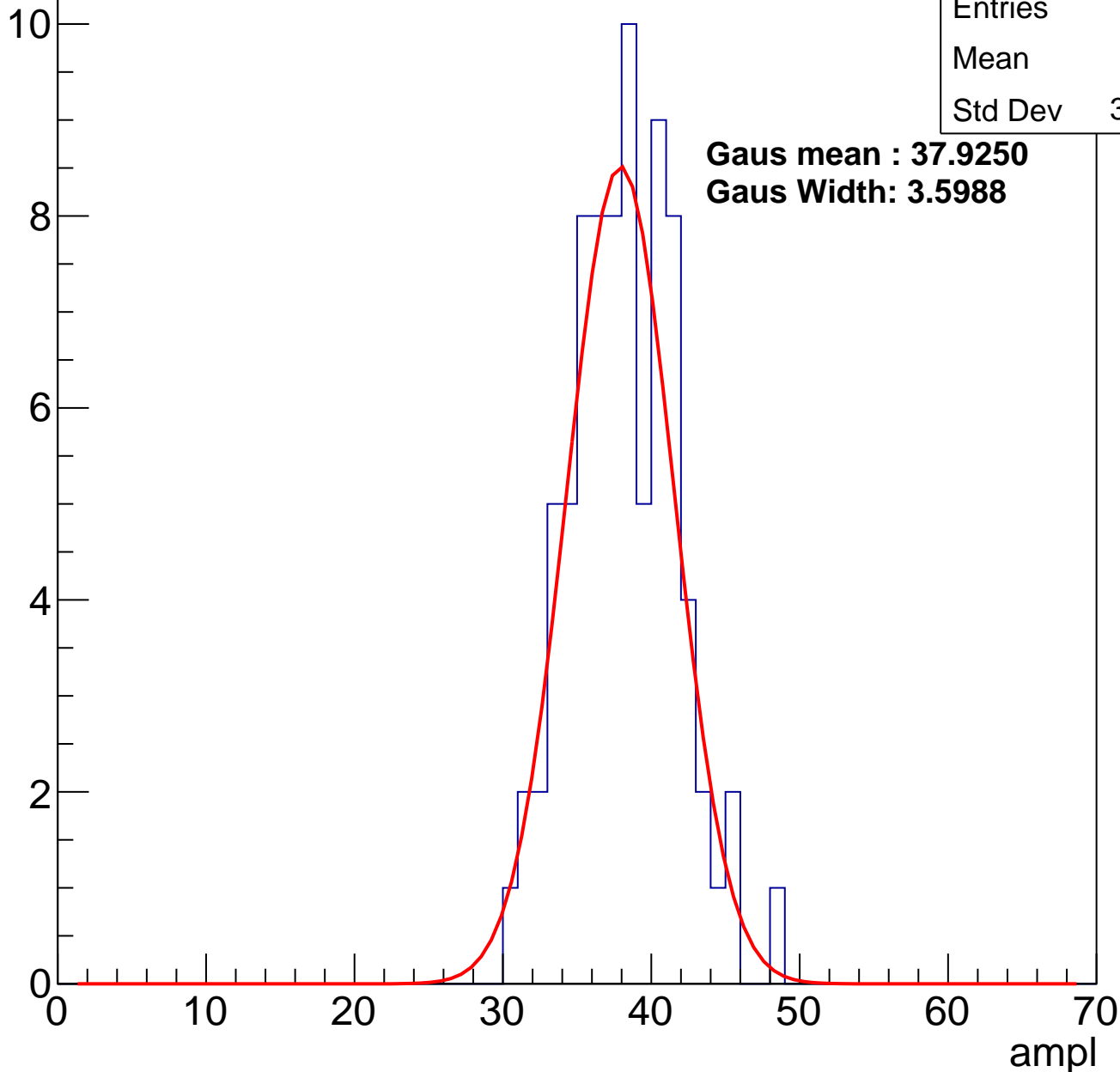
B0L002S, U26-ch55, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	37.7
Std Dev	3.522

Gaus mean : 37.9250
Gaus Width: 3.5988

Entry



B0L002S, U26-ch55, adc2

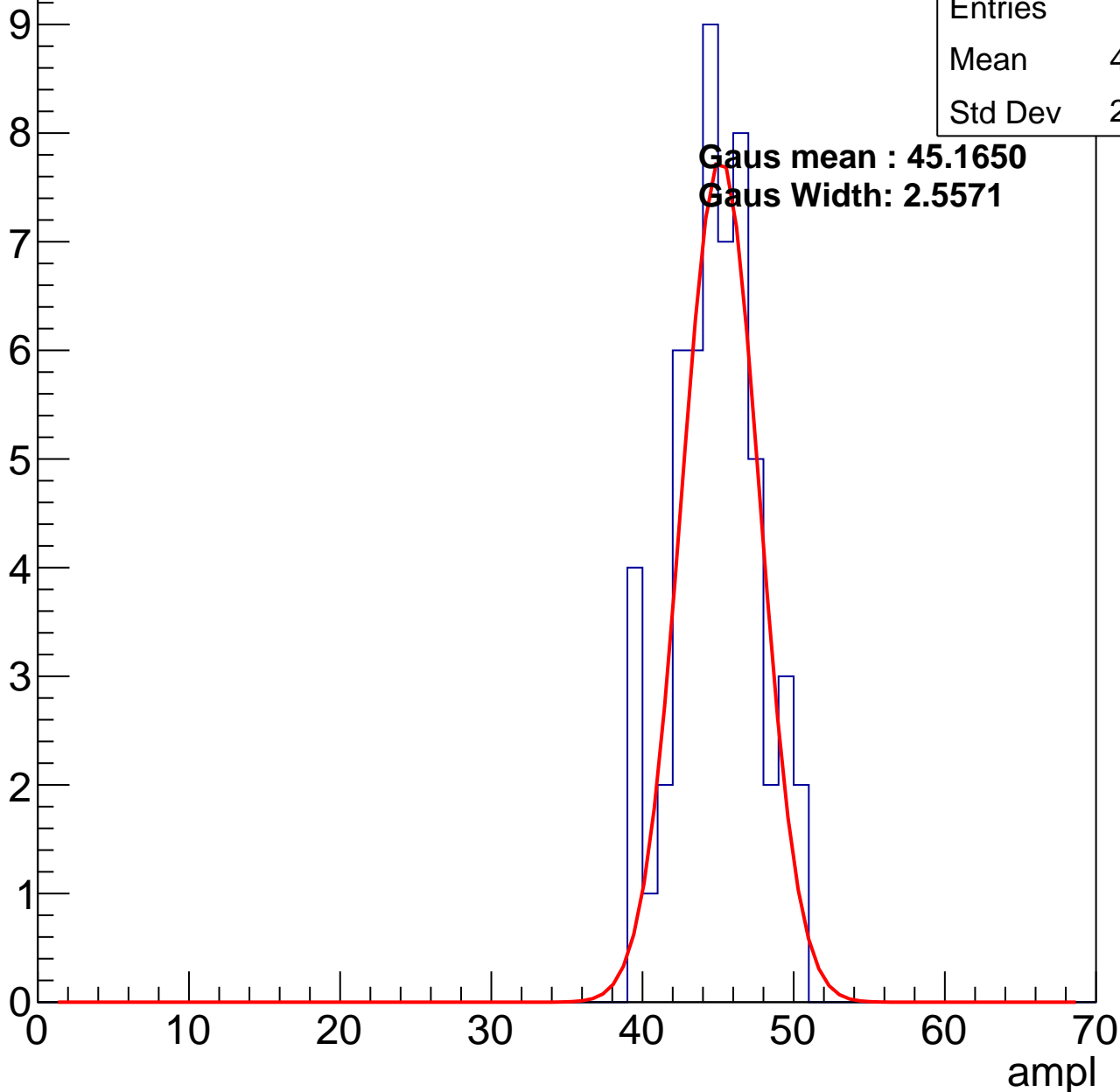
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.45
Std Dev	2.749

Gaus mean : 45.1650

Gaus Width: 2.5571



B0L002S, U26-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	51.54
Std Dev	3.517

Entry

10

8

6

4

2

0

0

10

20

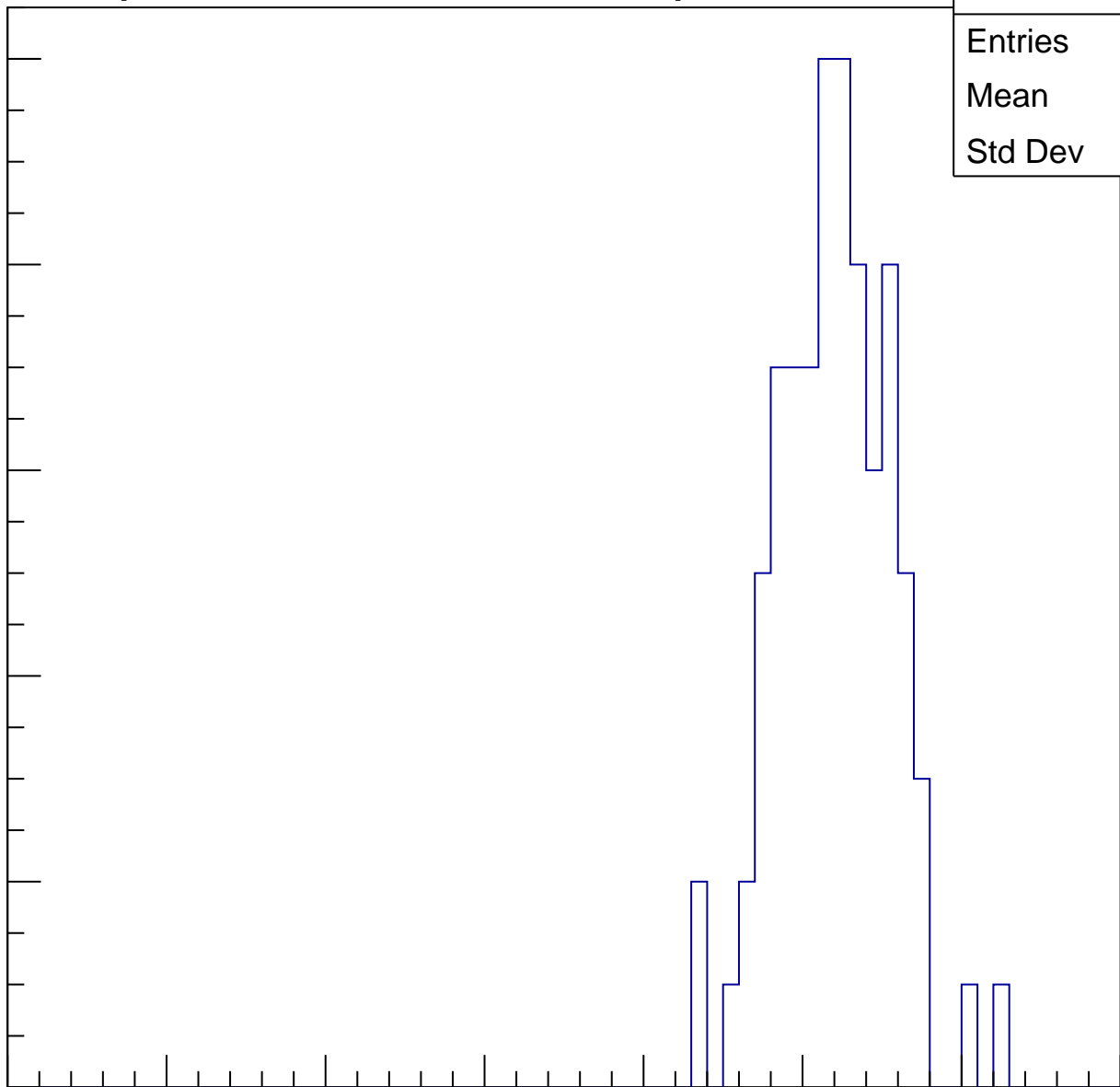
30

40

50

60

ampl



B0L002S, U26-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

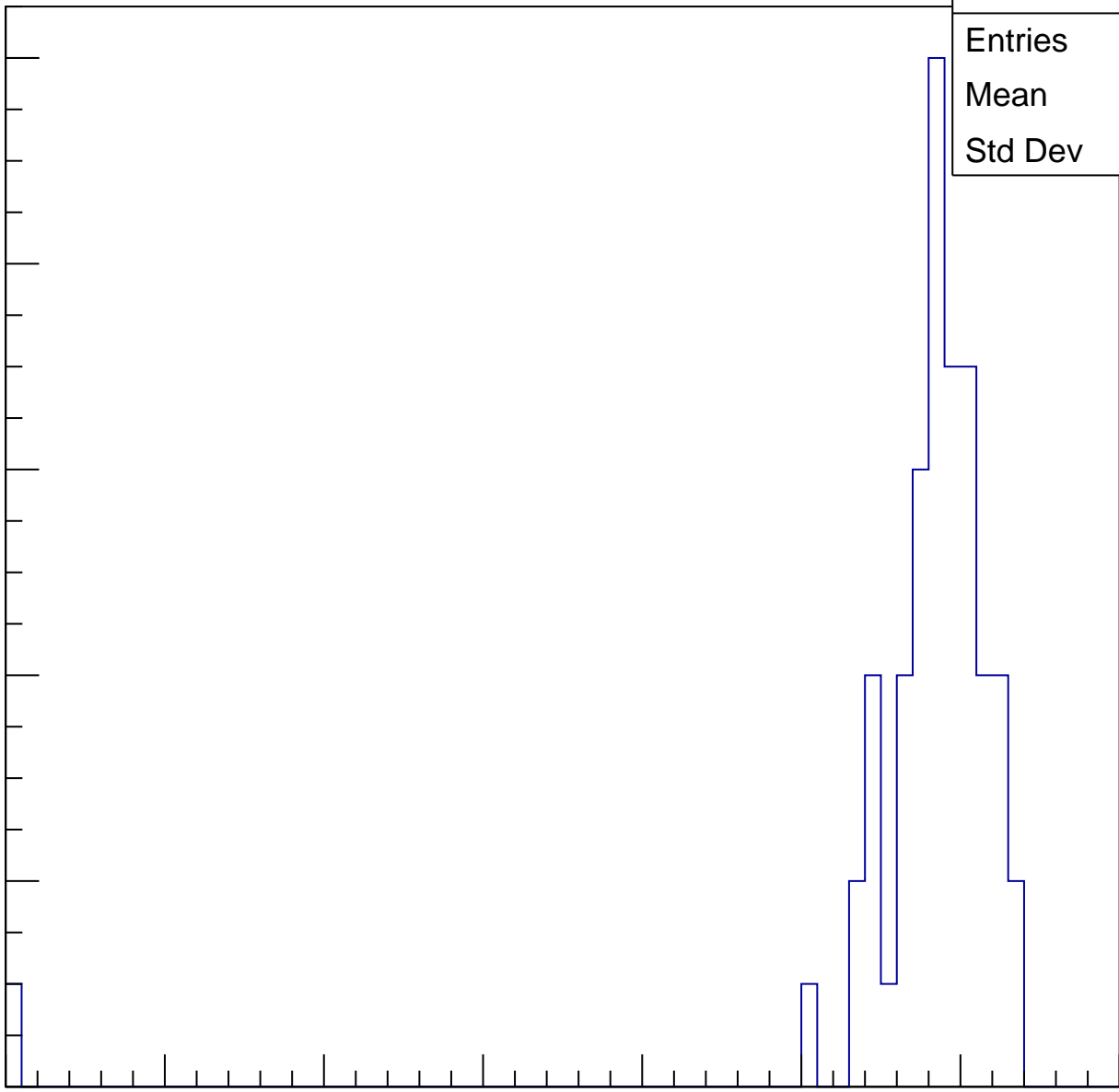
Entries	53
Mean	57.06
Std Dev	8.368

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

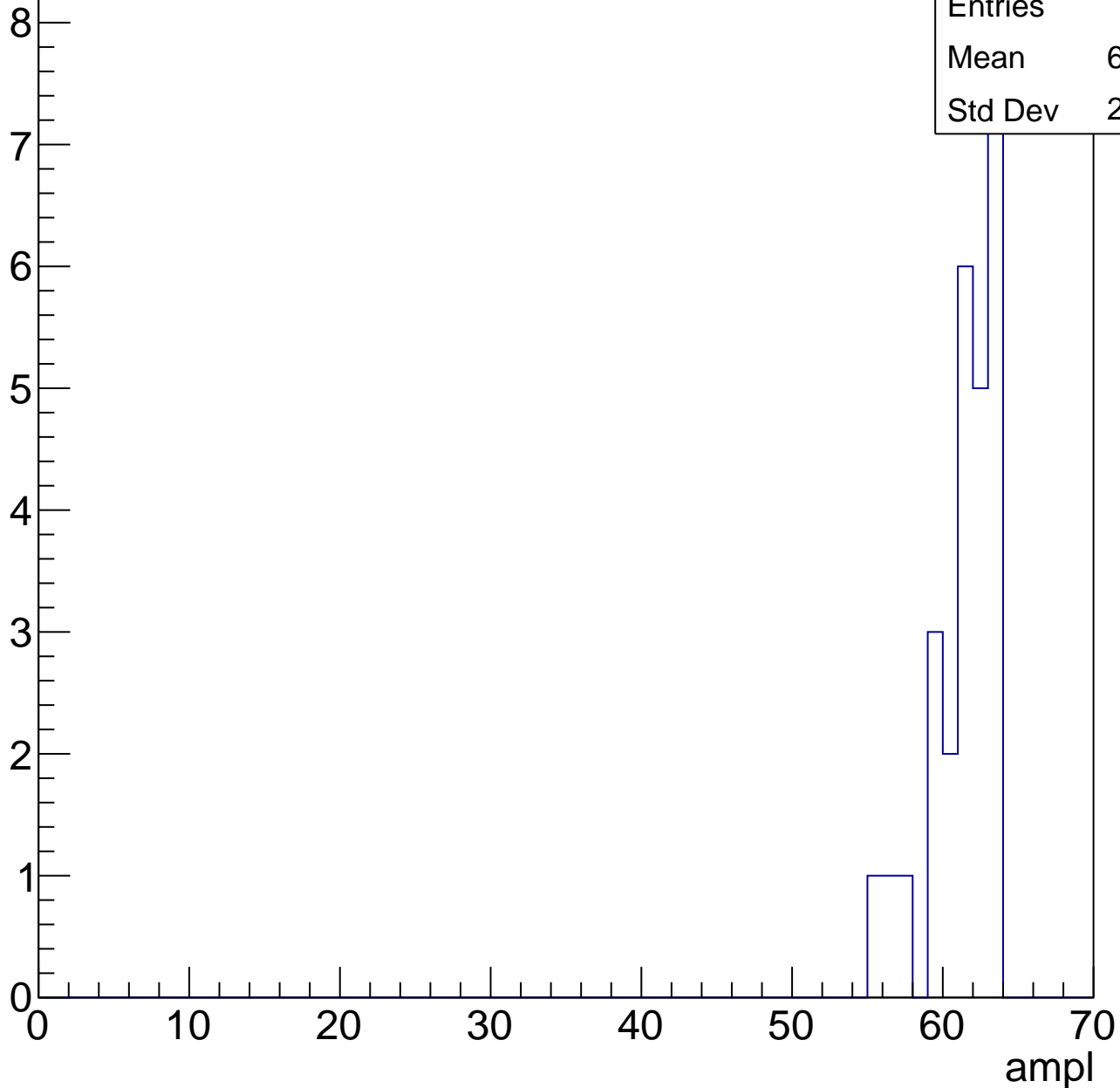


B0L002S, U26-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	60.93
Std Dev	2.176



B0L002S, U26-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch56, adc0

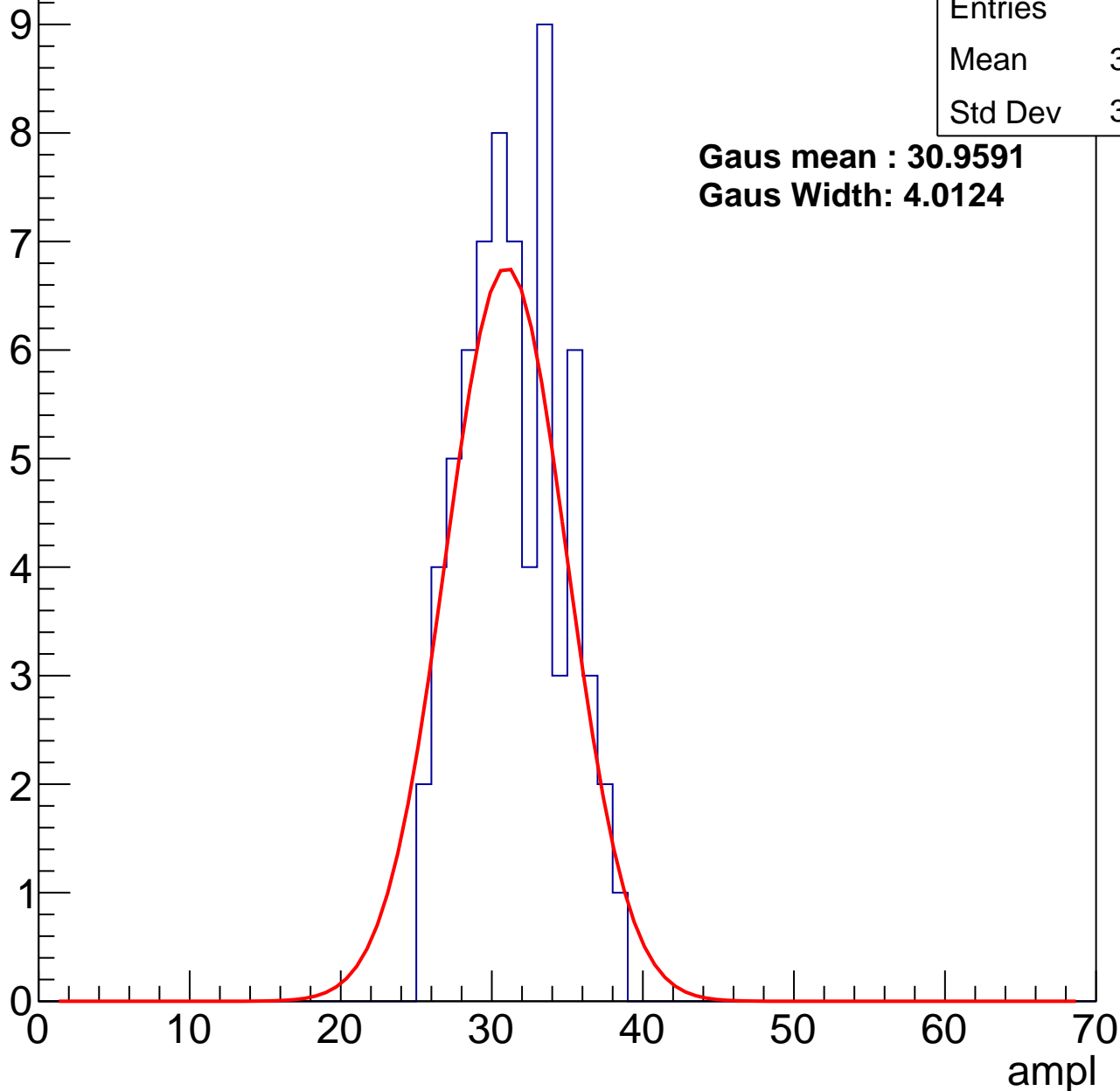
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.96
Std Dev	3.234

Gaus mean : 30.9591

Gaus Width: 4.0124



B0L002S, U26-ch56, adc1

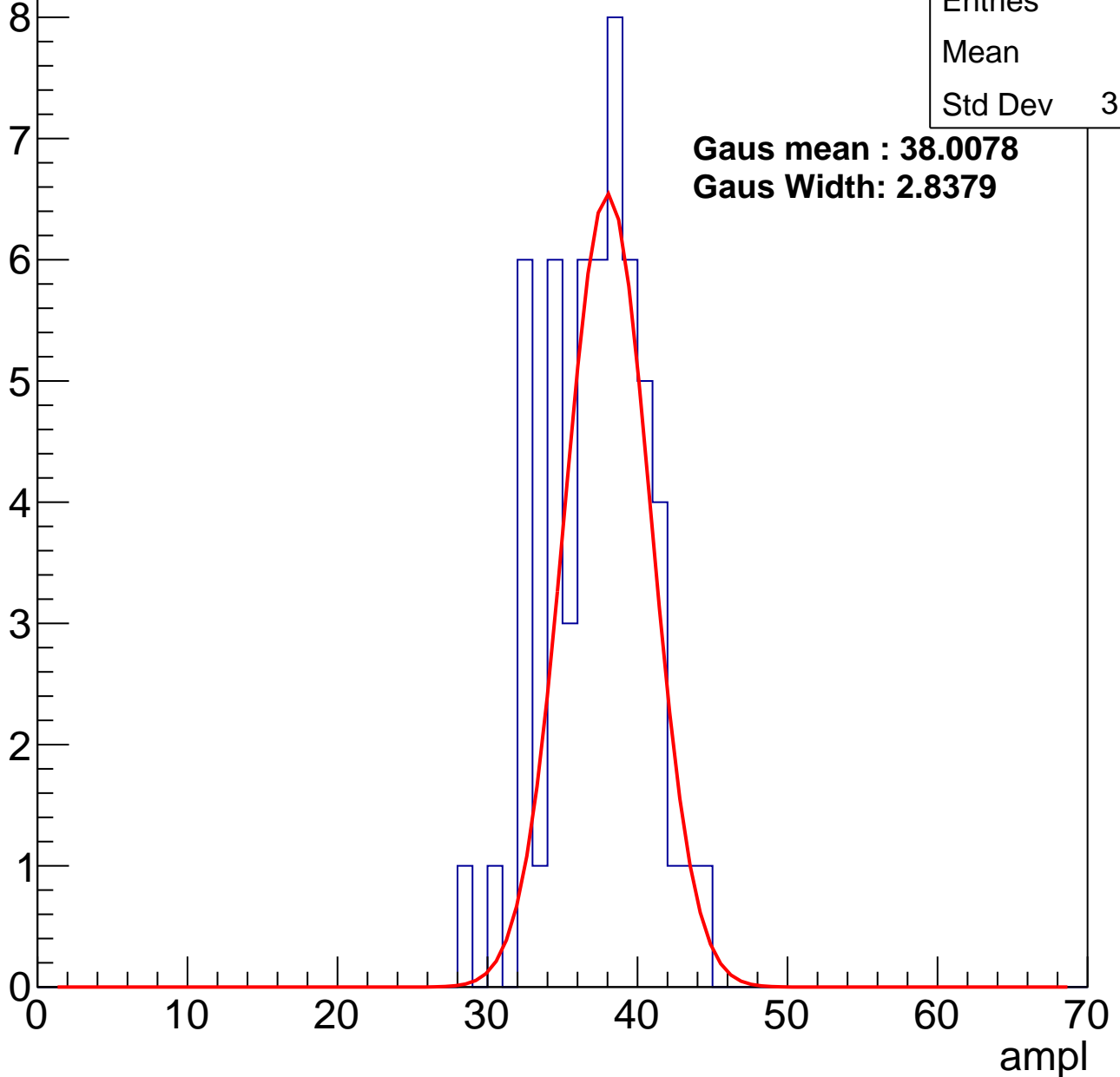
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.8
Std Dev	3.319

Gaus mean : 38.0078

Gaus Width: 2.8379



B0L002S, U26-ch56, adc2

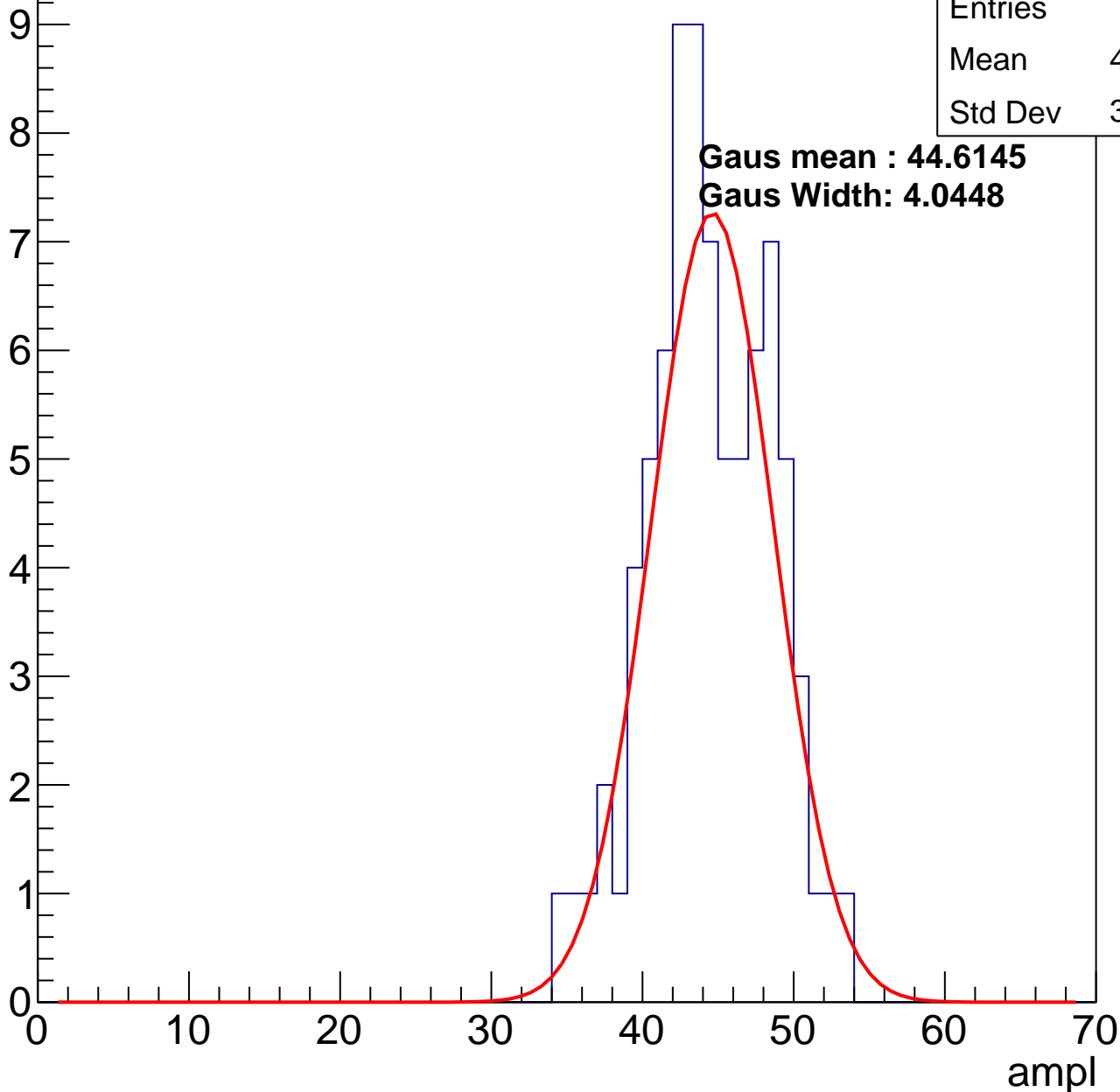
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	43.95
Std Dev	3.993

Gaus mean : 44.6145

Gaus Width: 4.0448

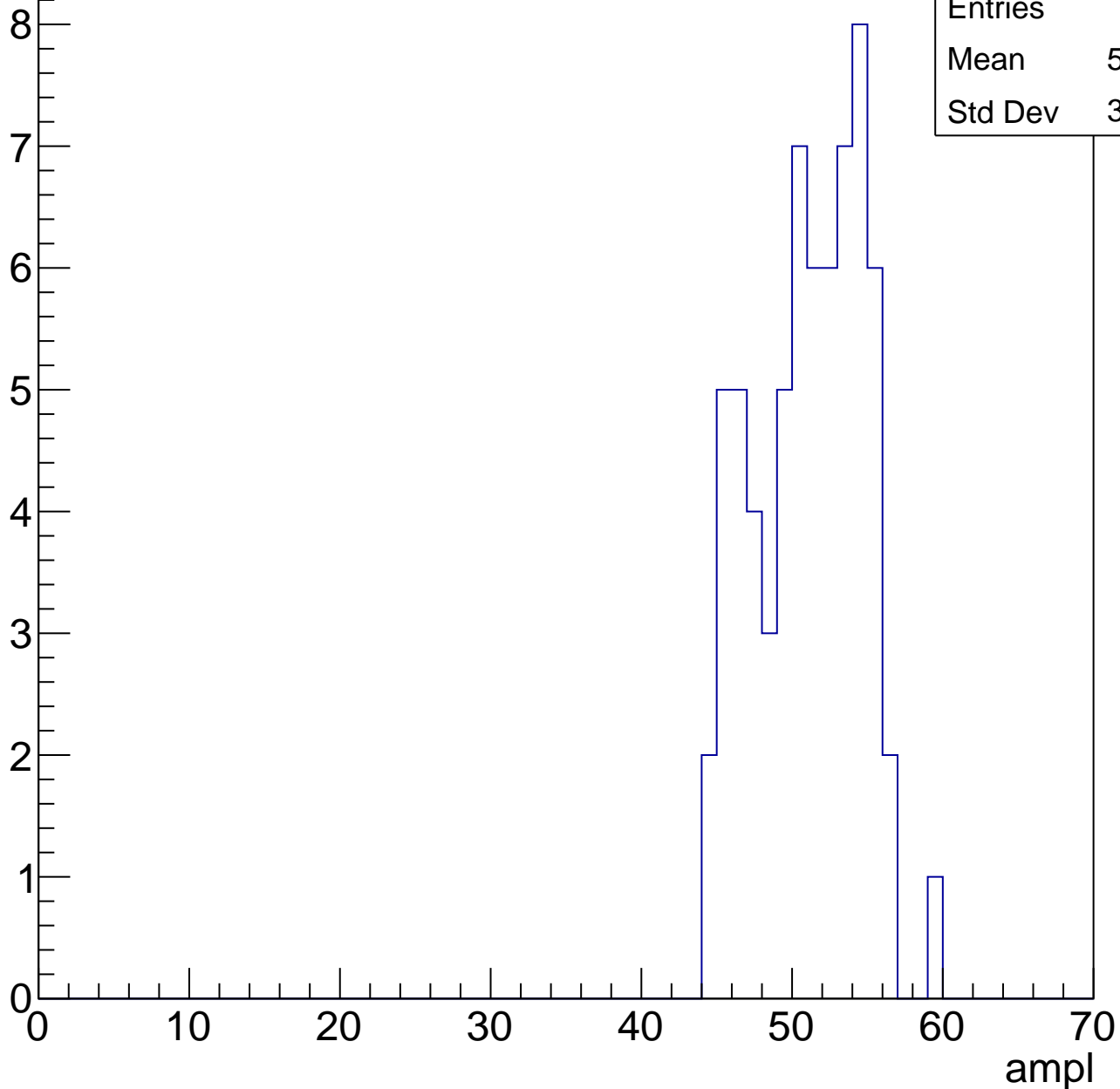


B0L002S, U26-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.63
Std Dev	3.515

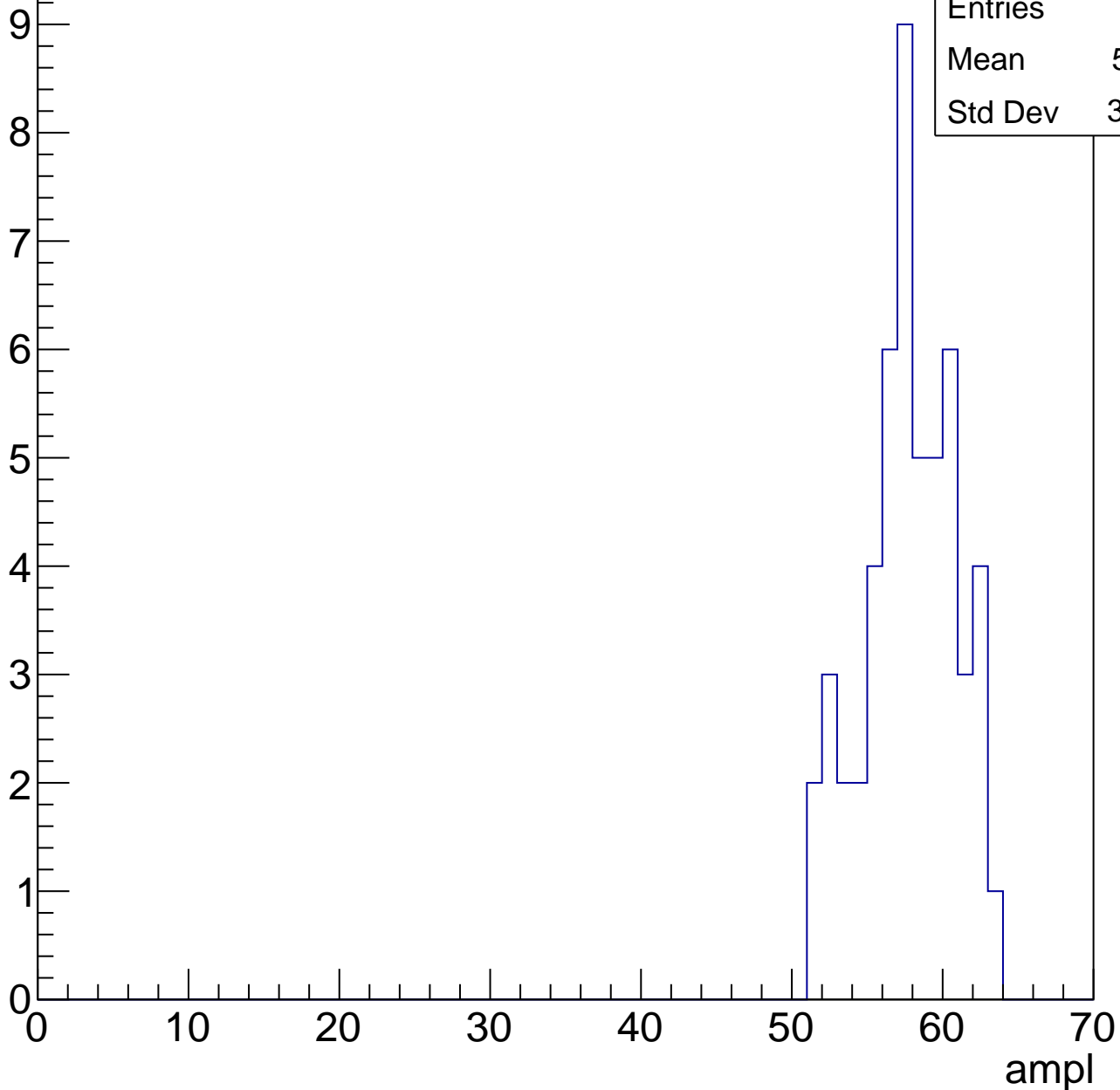


B0L002S, U26-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	57.31
Std Dev	3.029

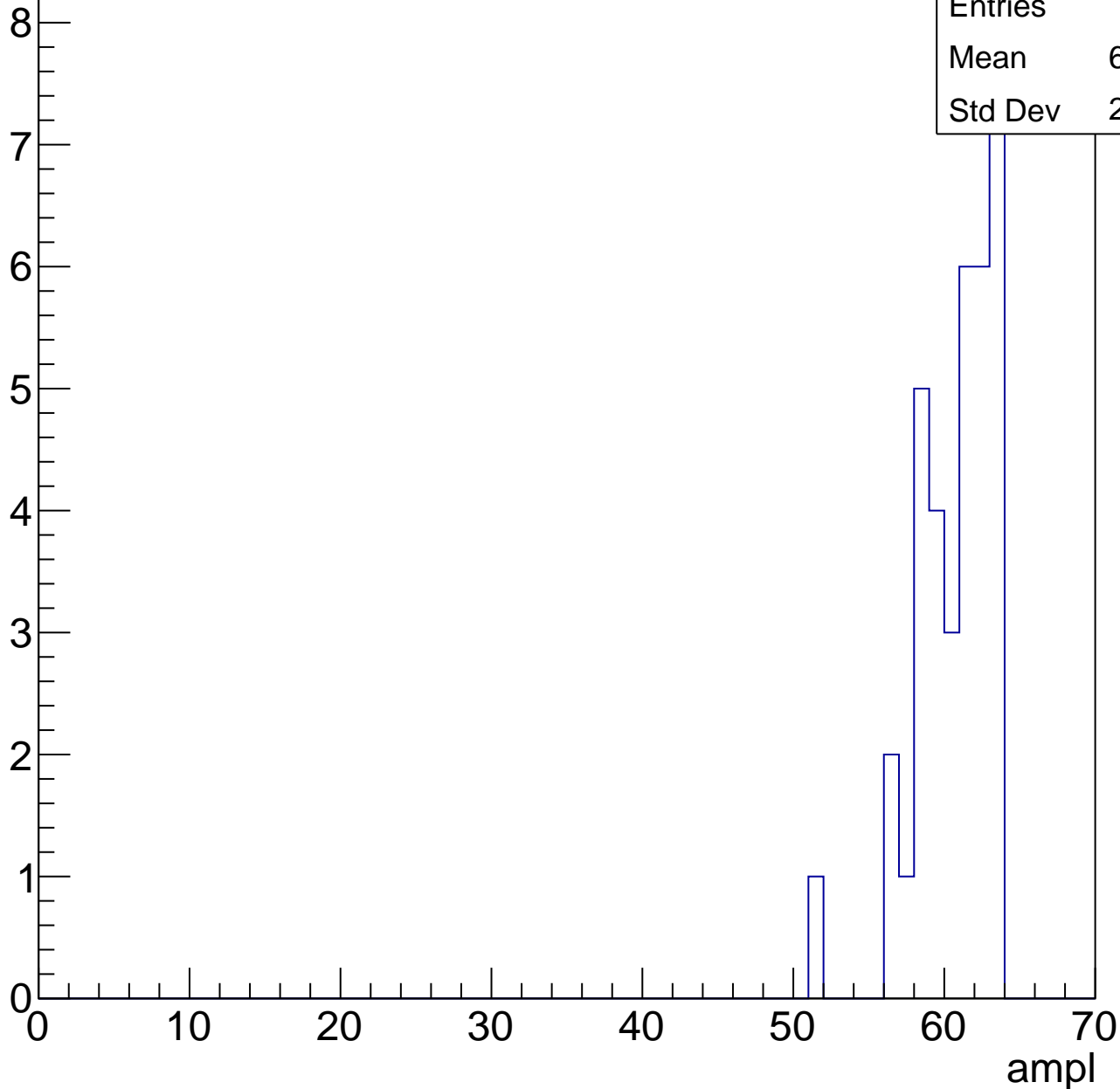


B0L002S, U26-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

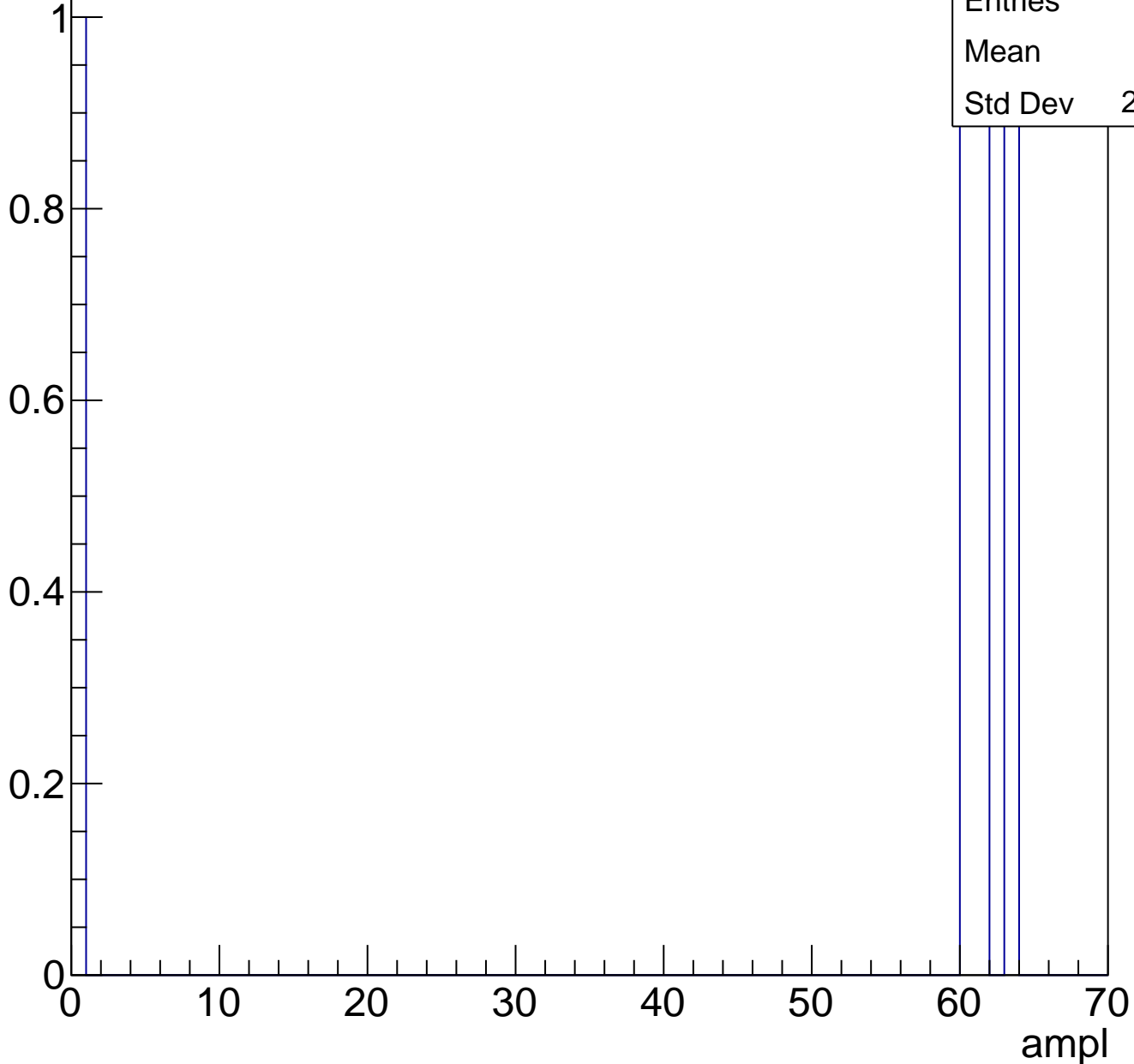
Entries	36
Mean	60.22
Std Dev	2.615



B0L002S, U26-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U26-ch57, adc0

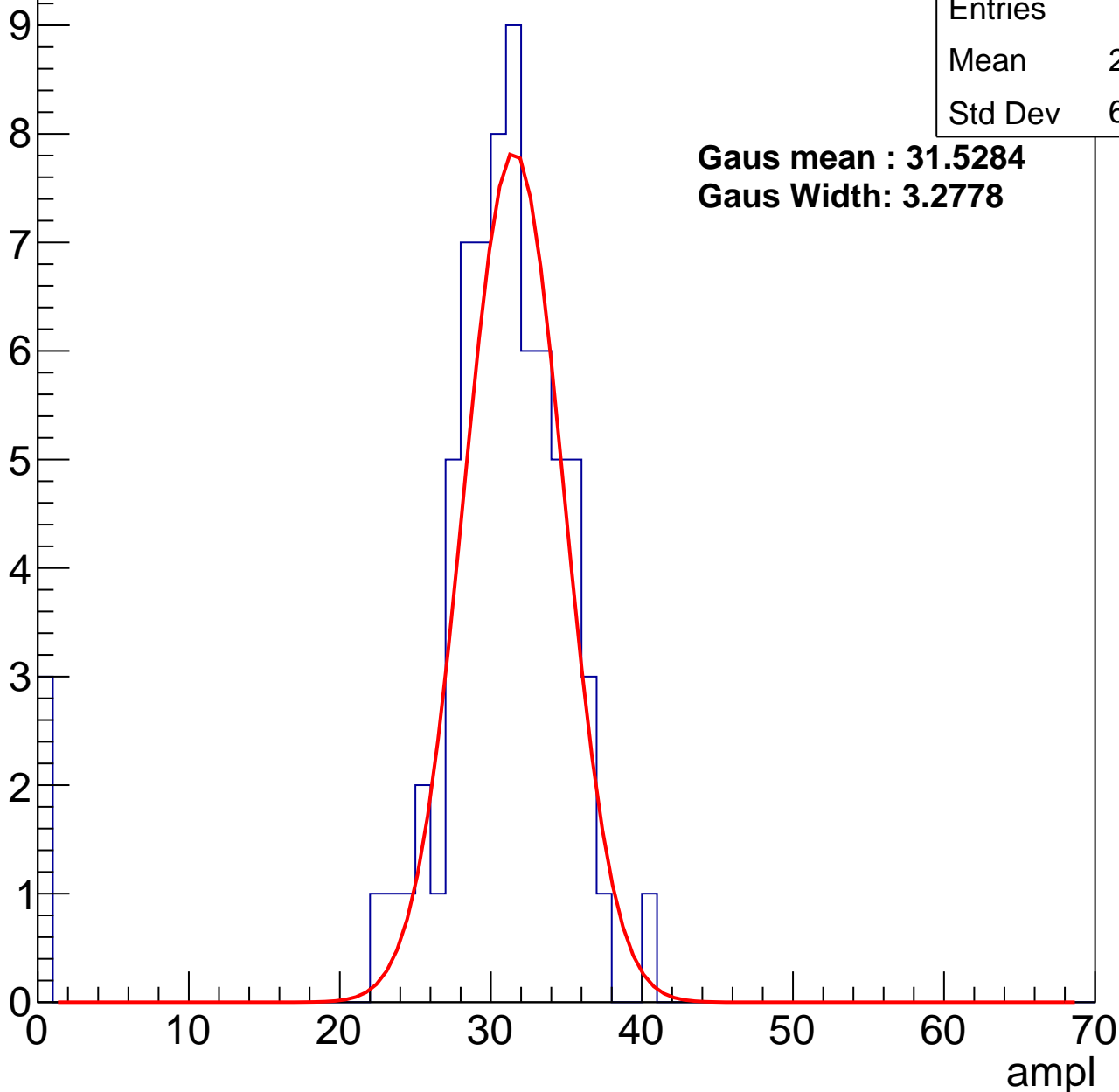
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	29.42
Std Dev	6.992

Gaus mean : 31.5284

Gaus Width: 3.2778

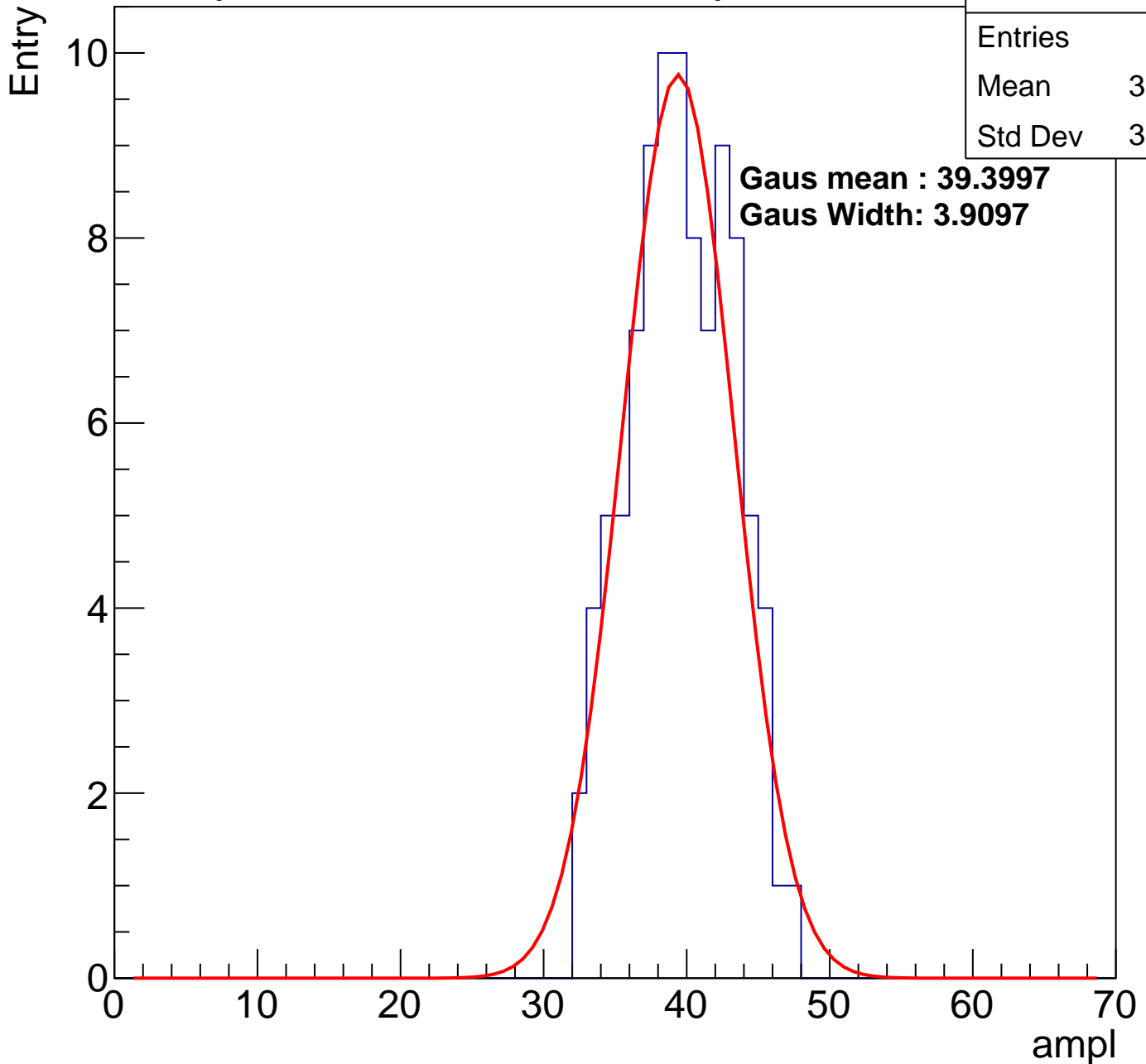


B0L002S, U26-ch57, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	95
Mean	39.14
Std Dev	3.526

Gaus mean : 39.3997
Gaus Width: 3.9097



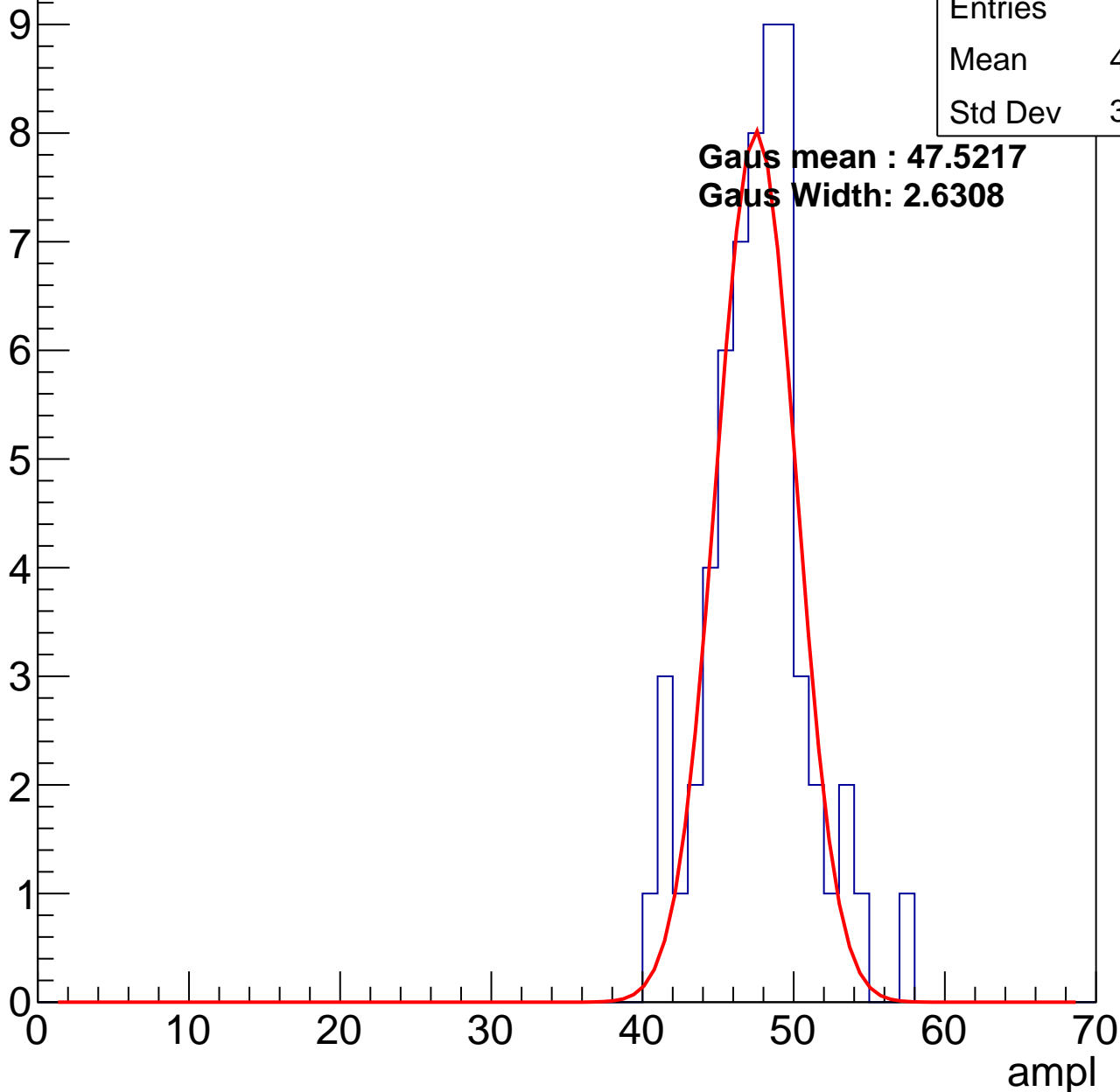
B0L002S, U26-ch57, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	47.15
Std Dev	3.239

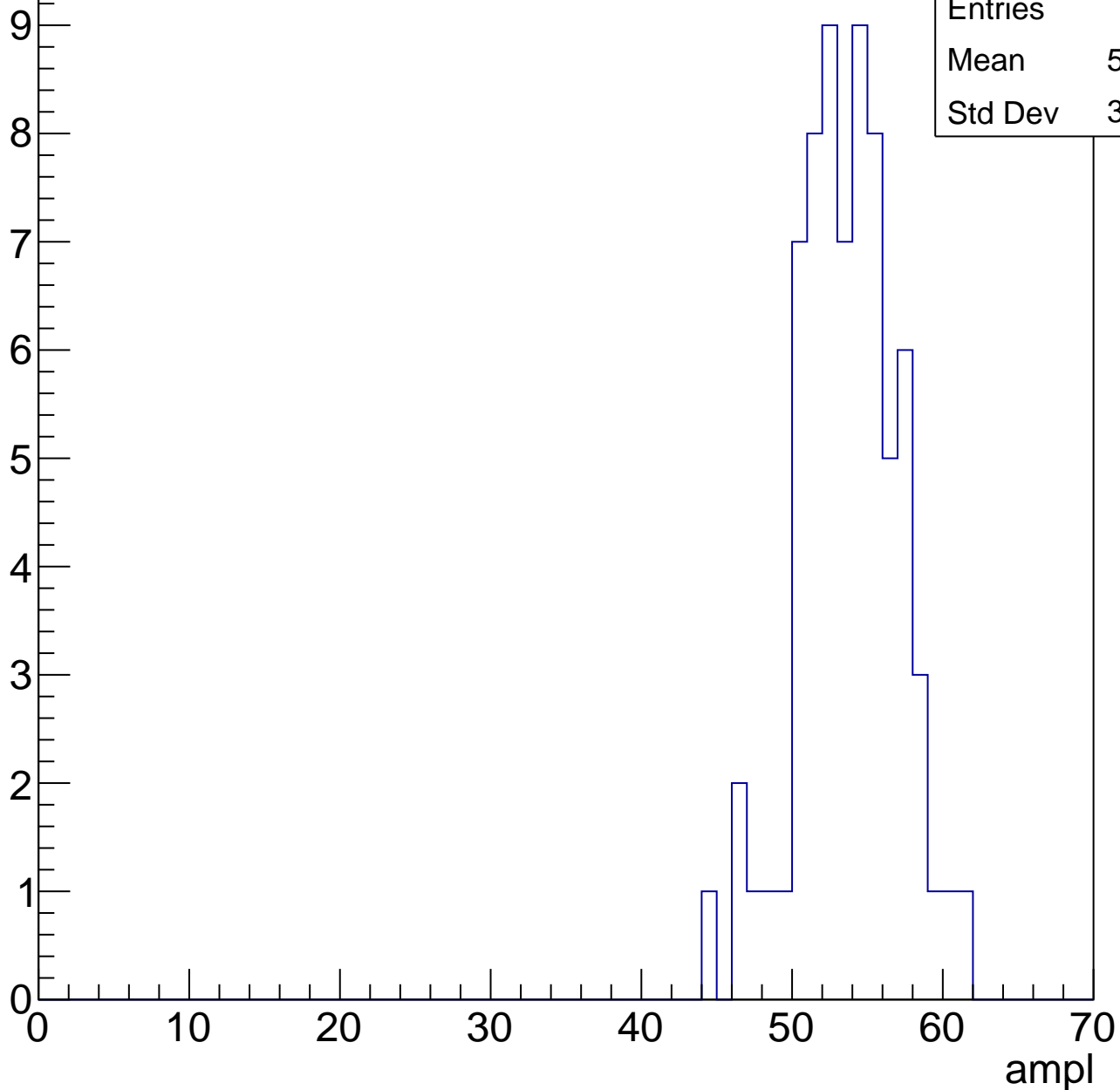
Gaus mean : 47.5217
Gaus Width: 2.6308



B0L002S, U26-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

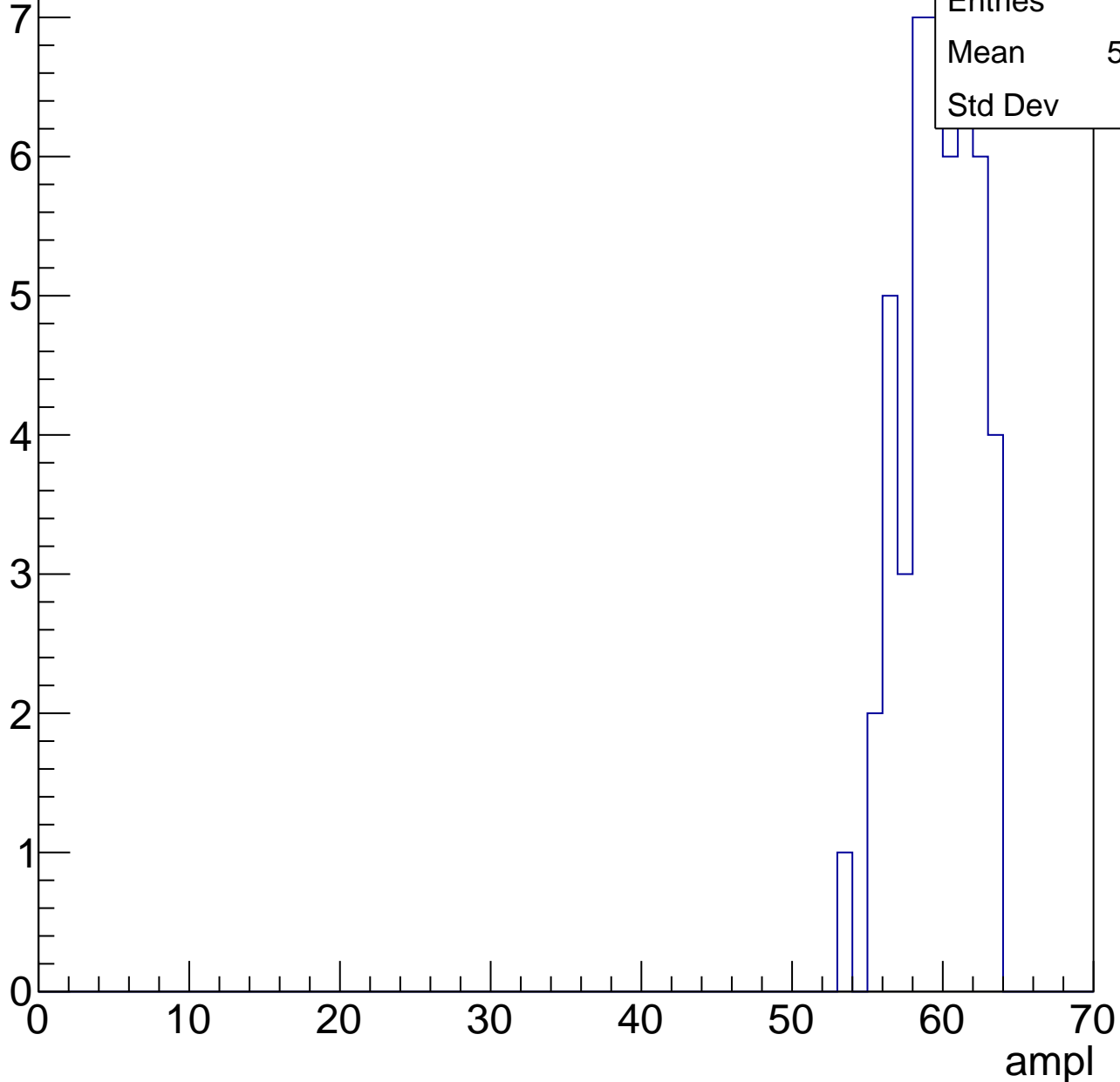


Entries	71
Mean	53.23
Std Dev	3.277

B0L002S, U26-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

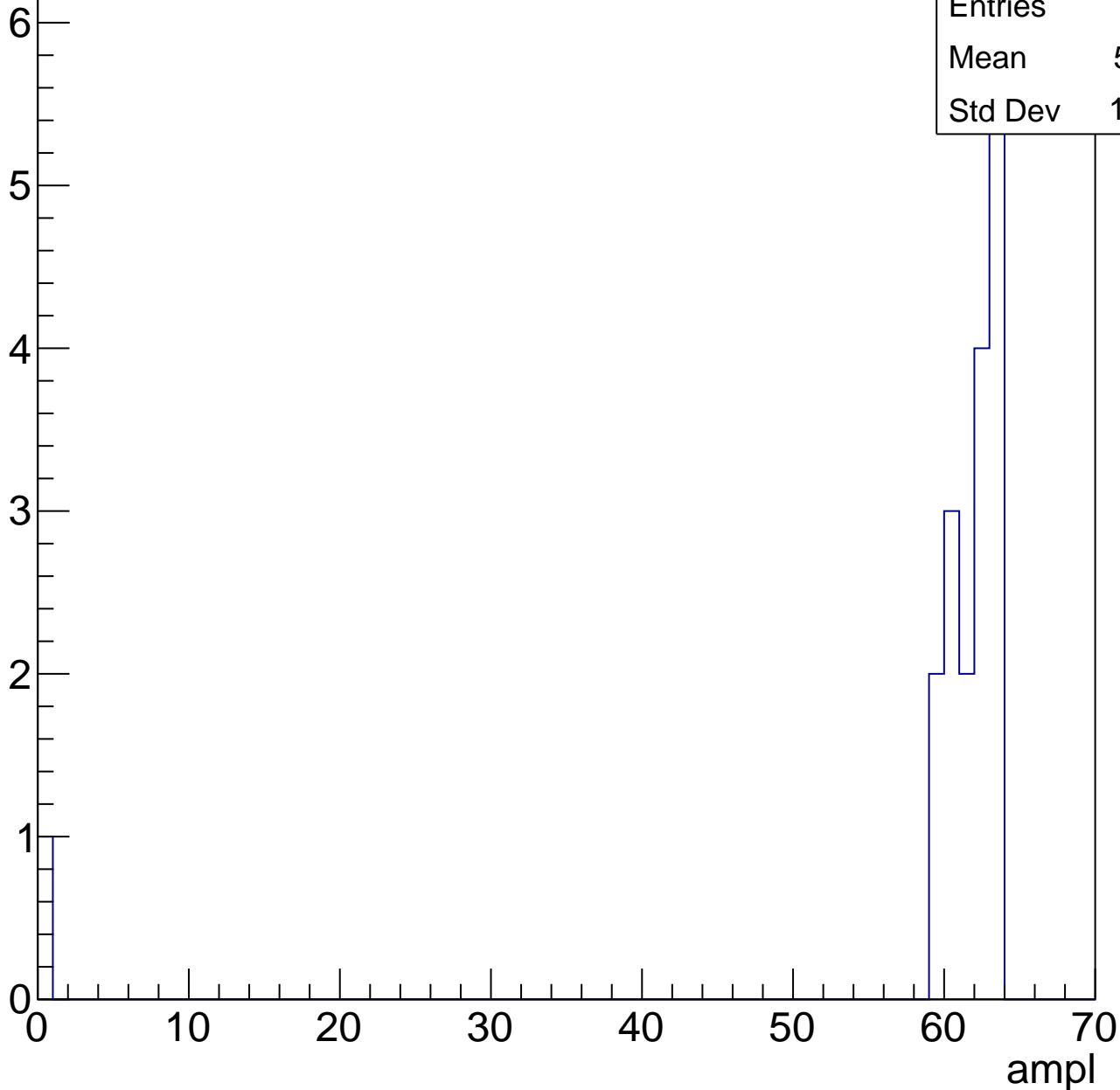


B0L002S, U26-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	18
Mean	58.11
Std Dev	14.16



B0L002S, U26-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch58, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	86
Mean	29.76
Std Dev	5.661

Gaus mean : 30.8080

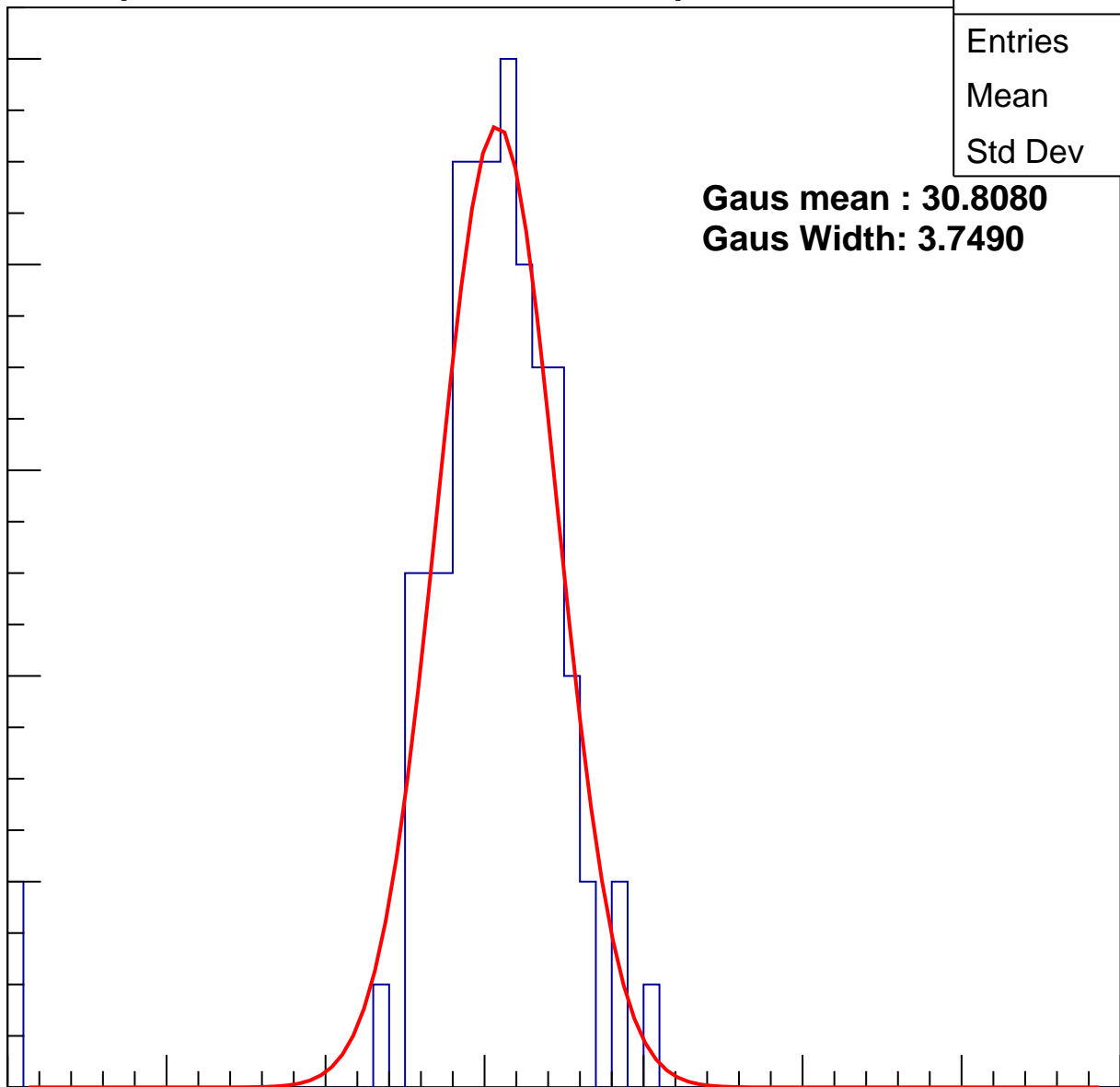
Gaus Width: 3.7490

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch58, adc1

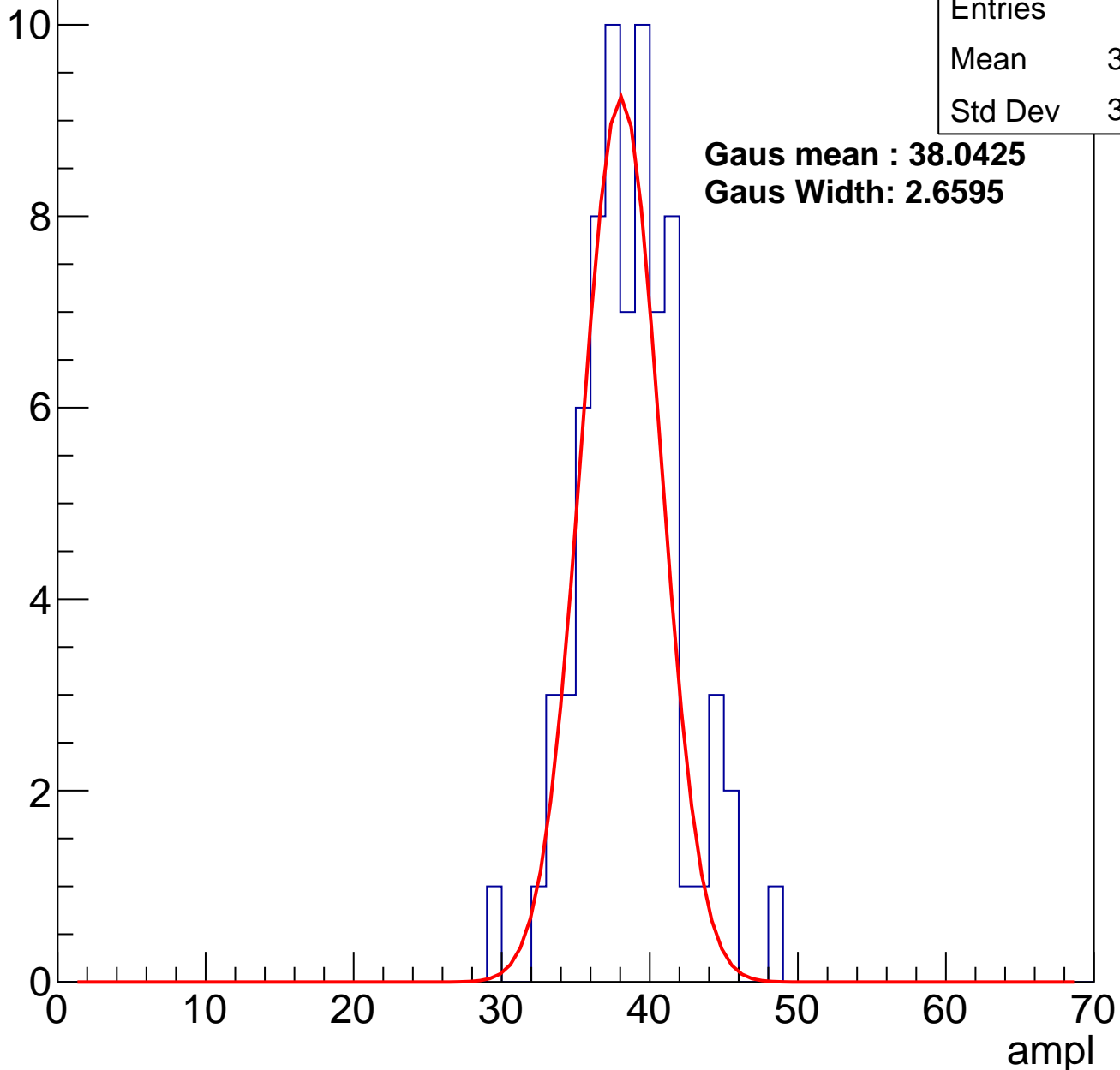
calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	38.18
Std Dev	3.314

Gaus mean : 38.0425

Gaus Width: 2.6595

Entry



B0L002S, U26-ch58, adc2

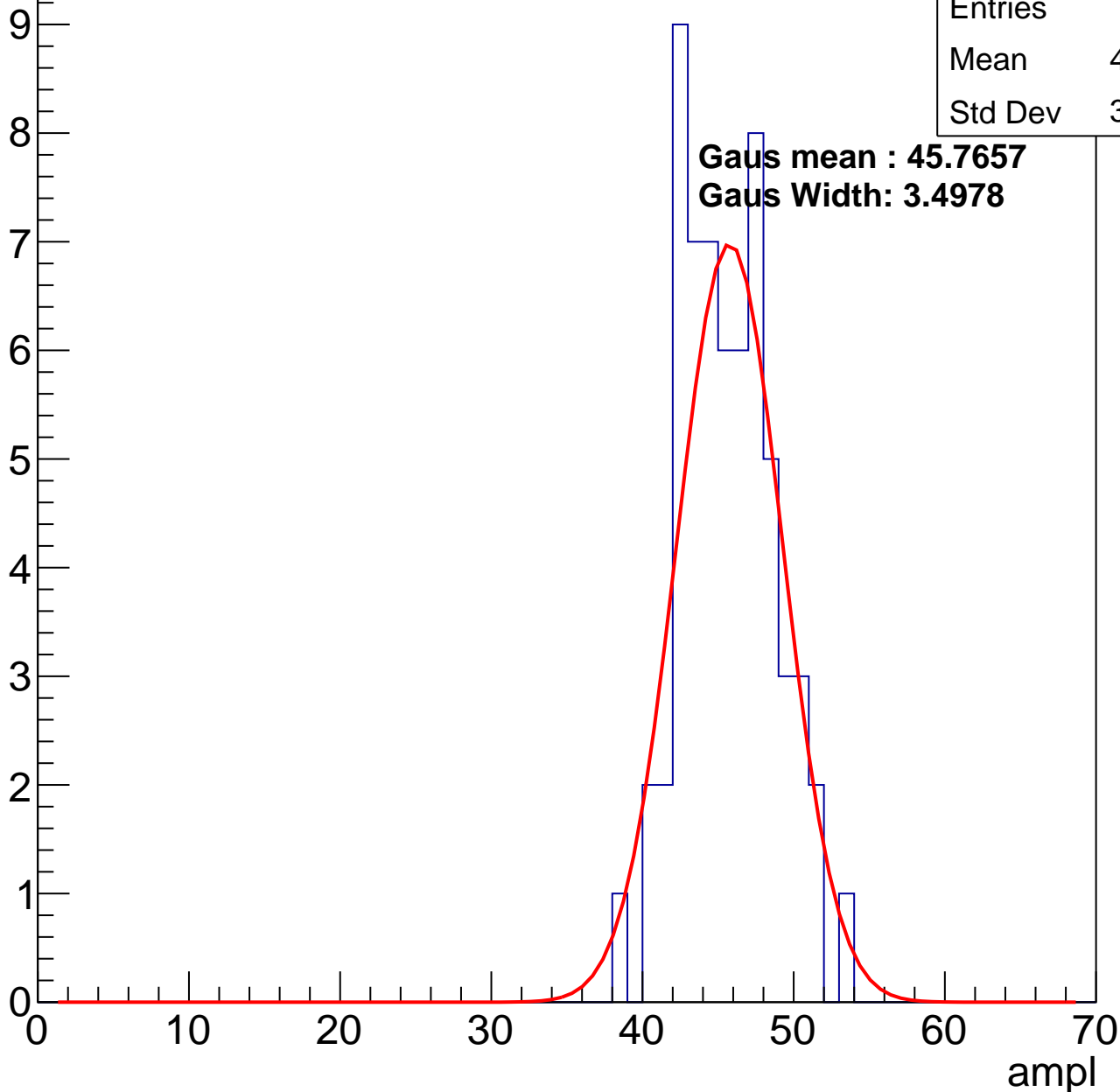
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.18
Std Dev	3.077

Gaus mean : 45.7657

Gaus Width: 3.4978

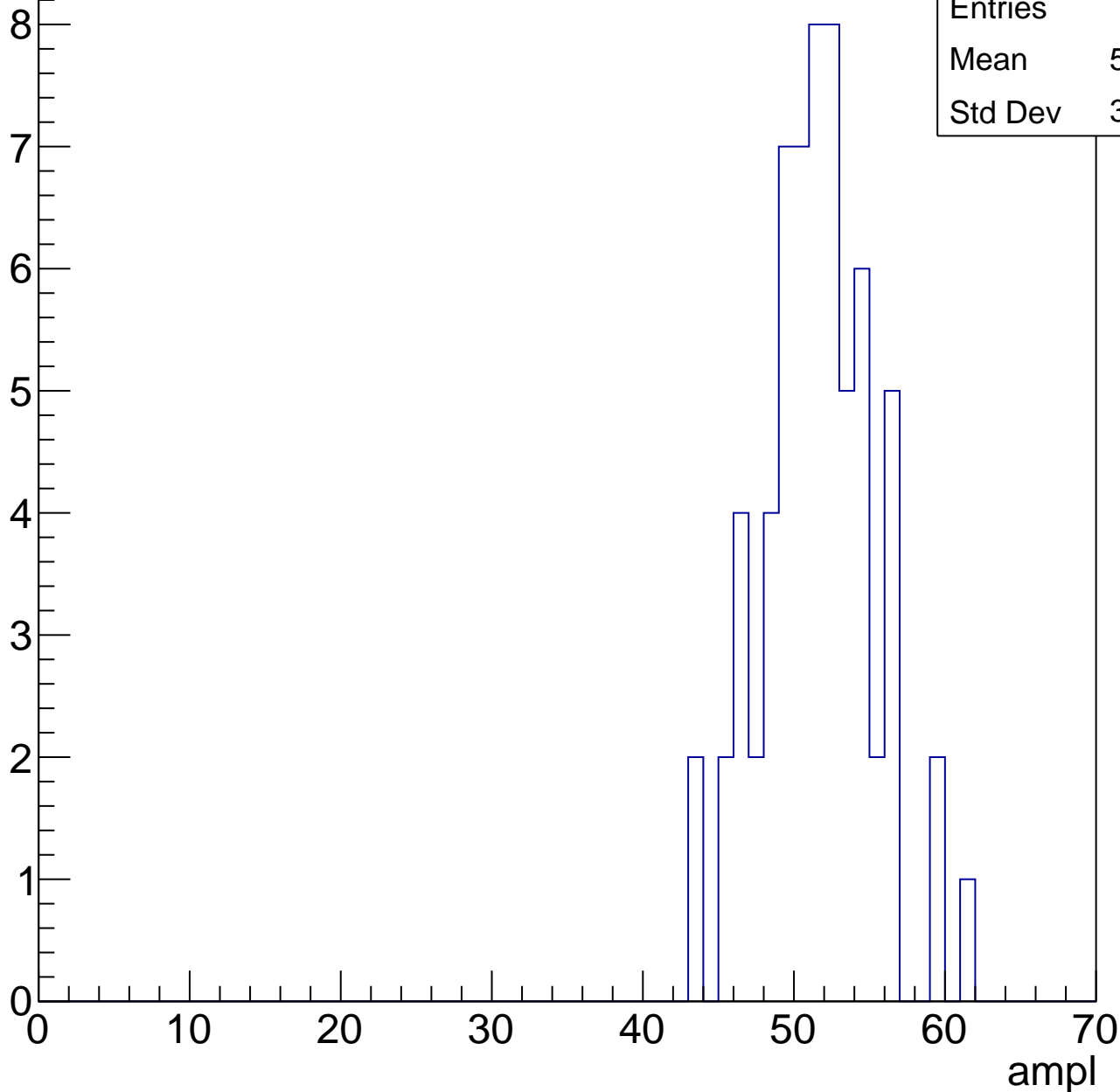


B0L002S, U26-ch58, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

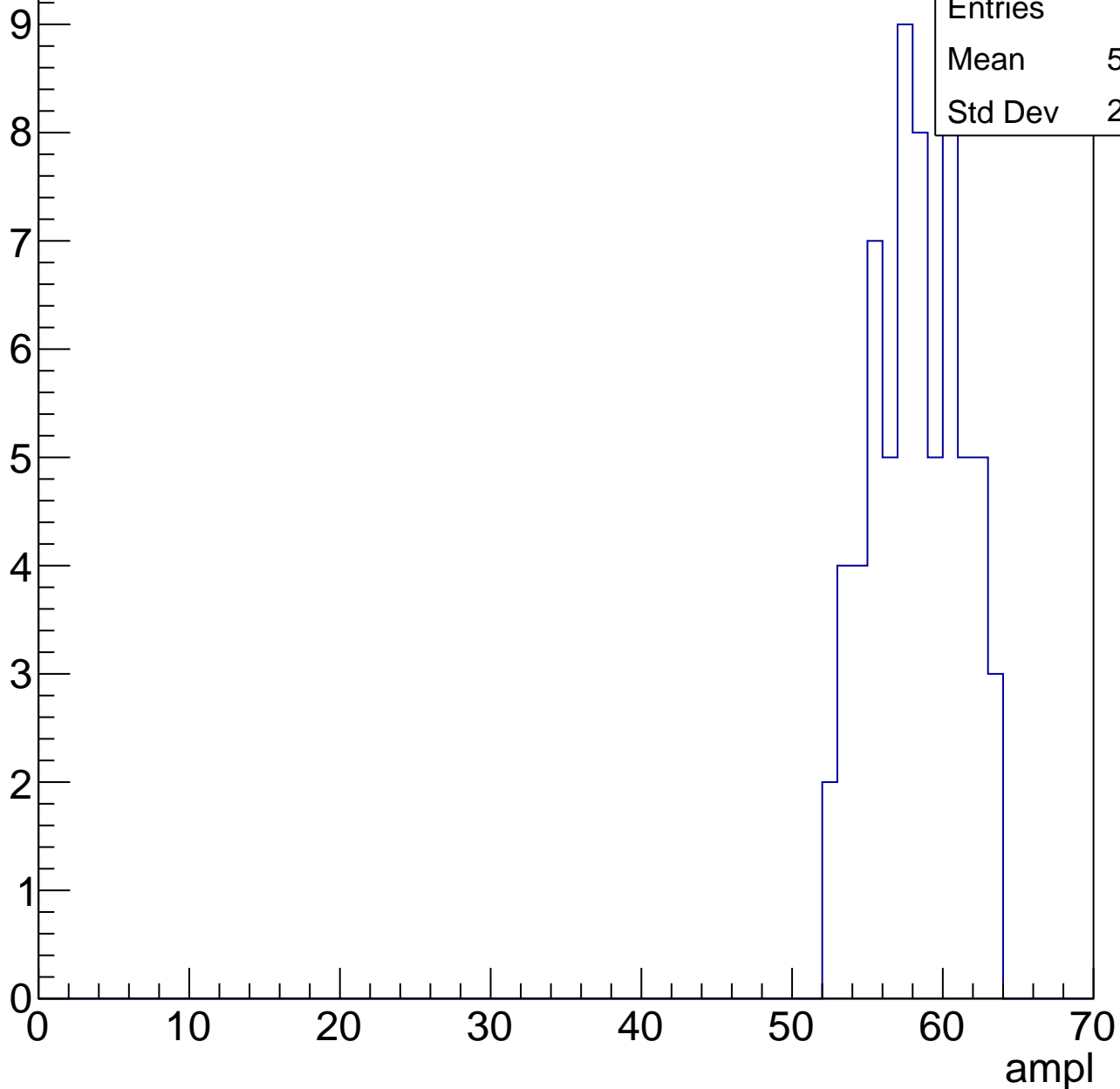
Entries	65
Mean	51.09
Std Dev	3.657



B0L002S, U26-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



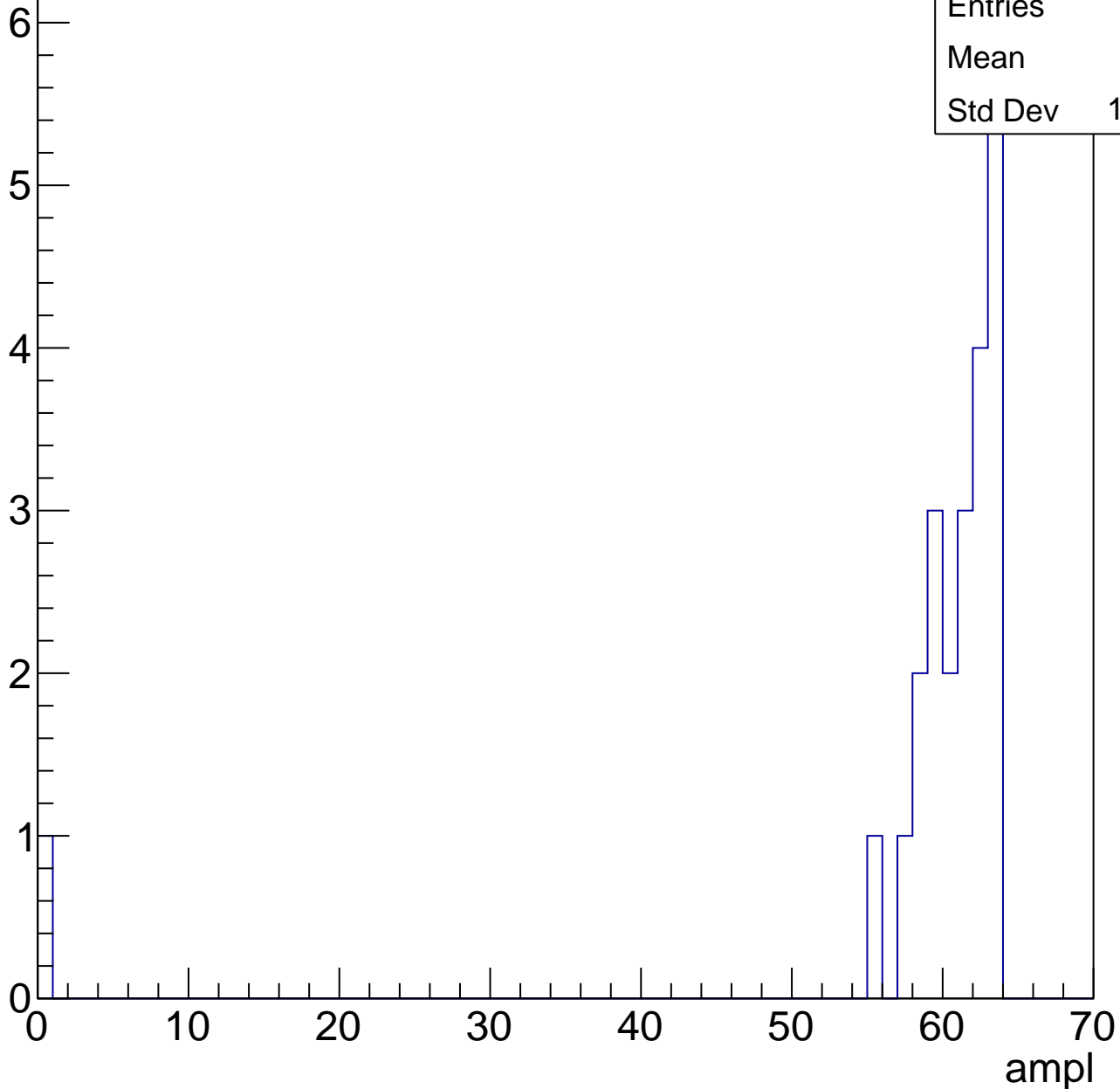
Entries	65
Mean	57.74
Std Dev	2.937

B0L002S, U26-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58
Std Dev	12.56



B0L002S, U26-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch59, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	31.19
Std Dev	4.924

Gaus mean : 32.1210

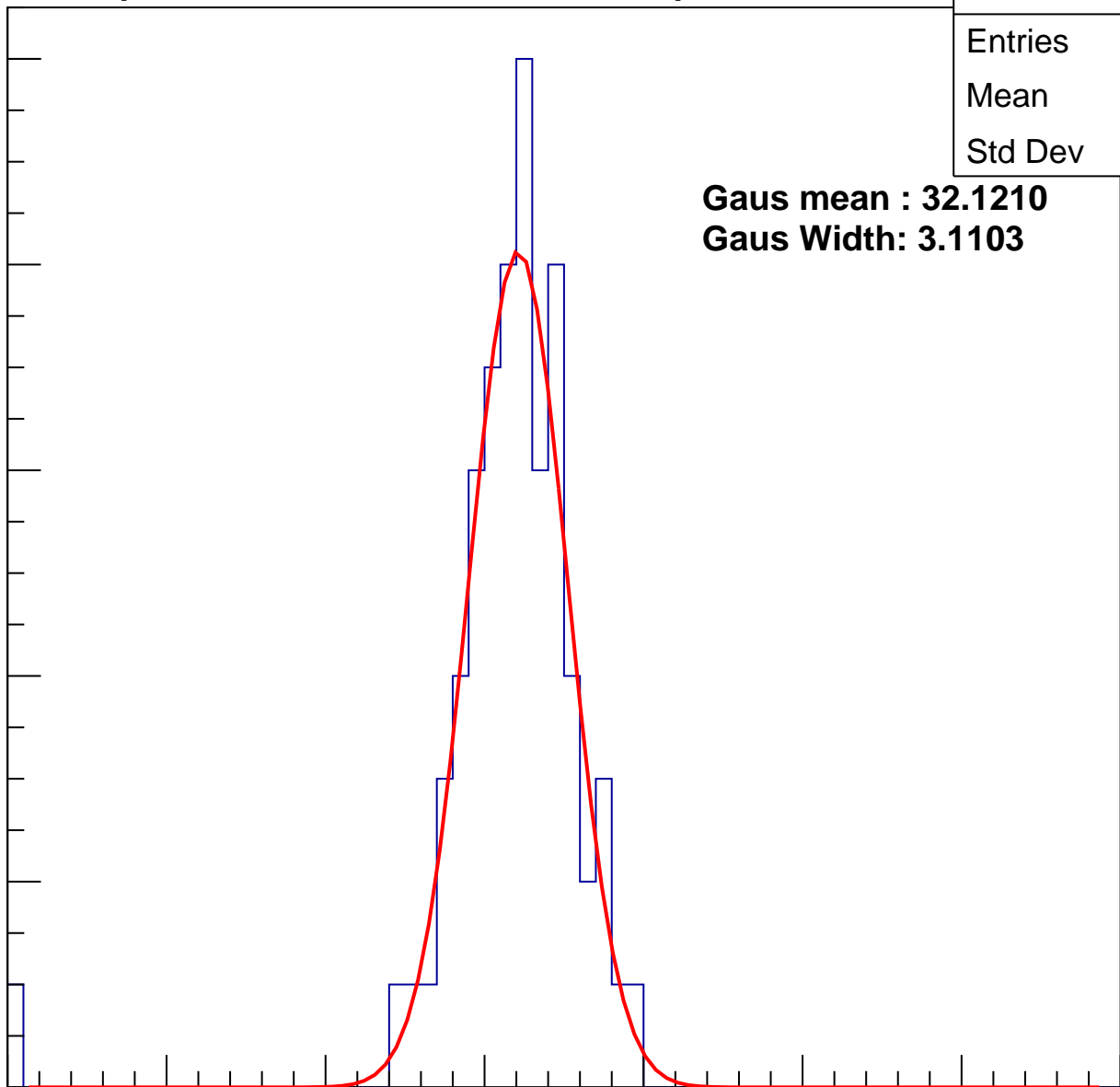
Gaus Width: 3.1103

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch59, adc1

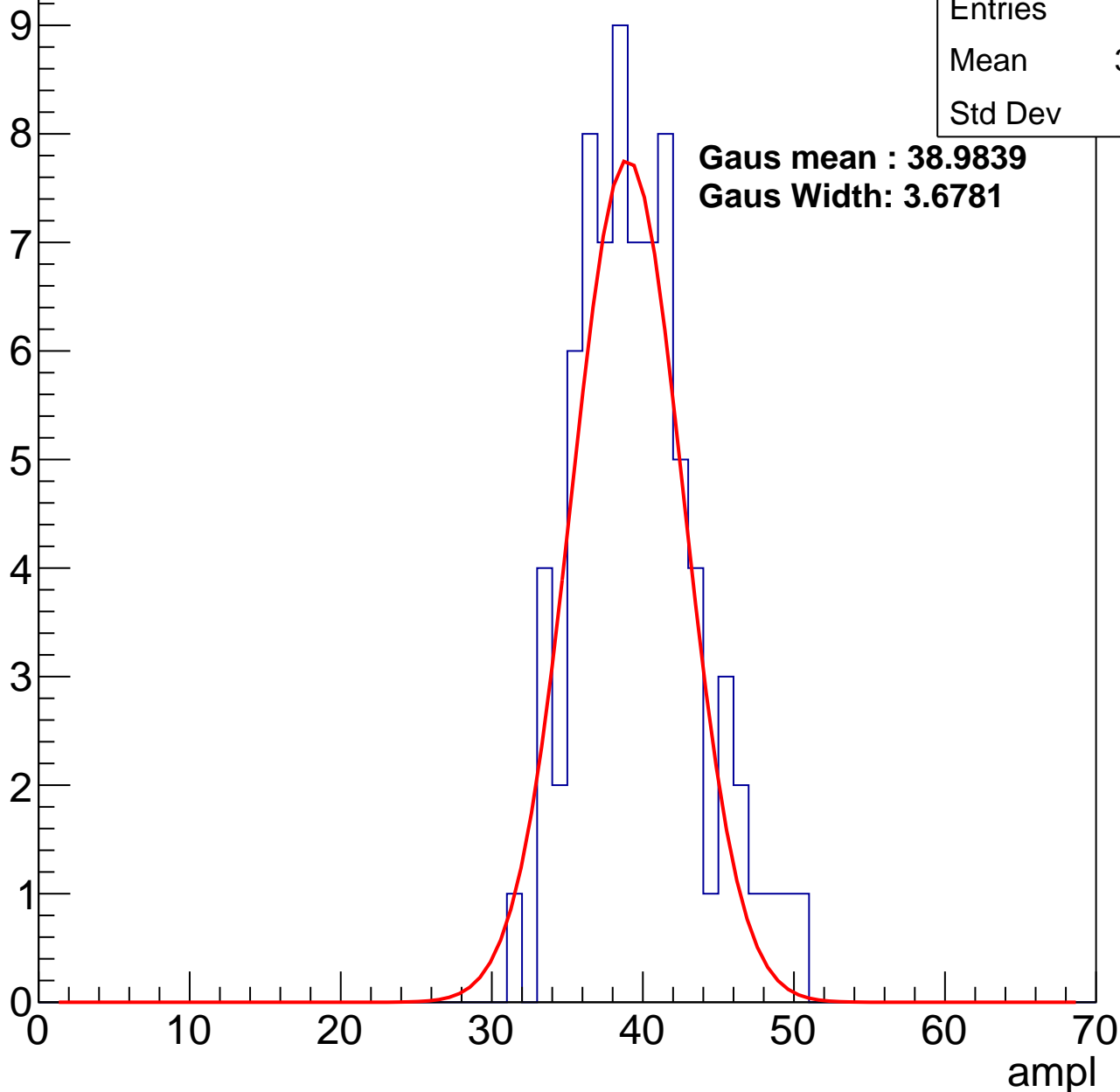
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	39.21
Std Dev	3.93

Gaus mean : 38.9839

Gaus Width: 3.6781



B0L002S, U26-ch59, adc2

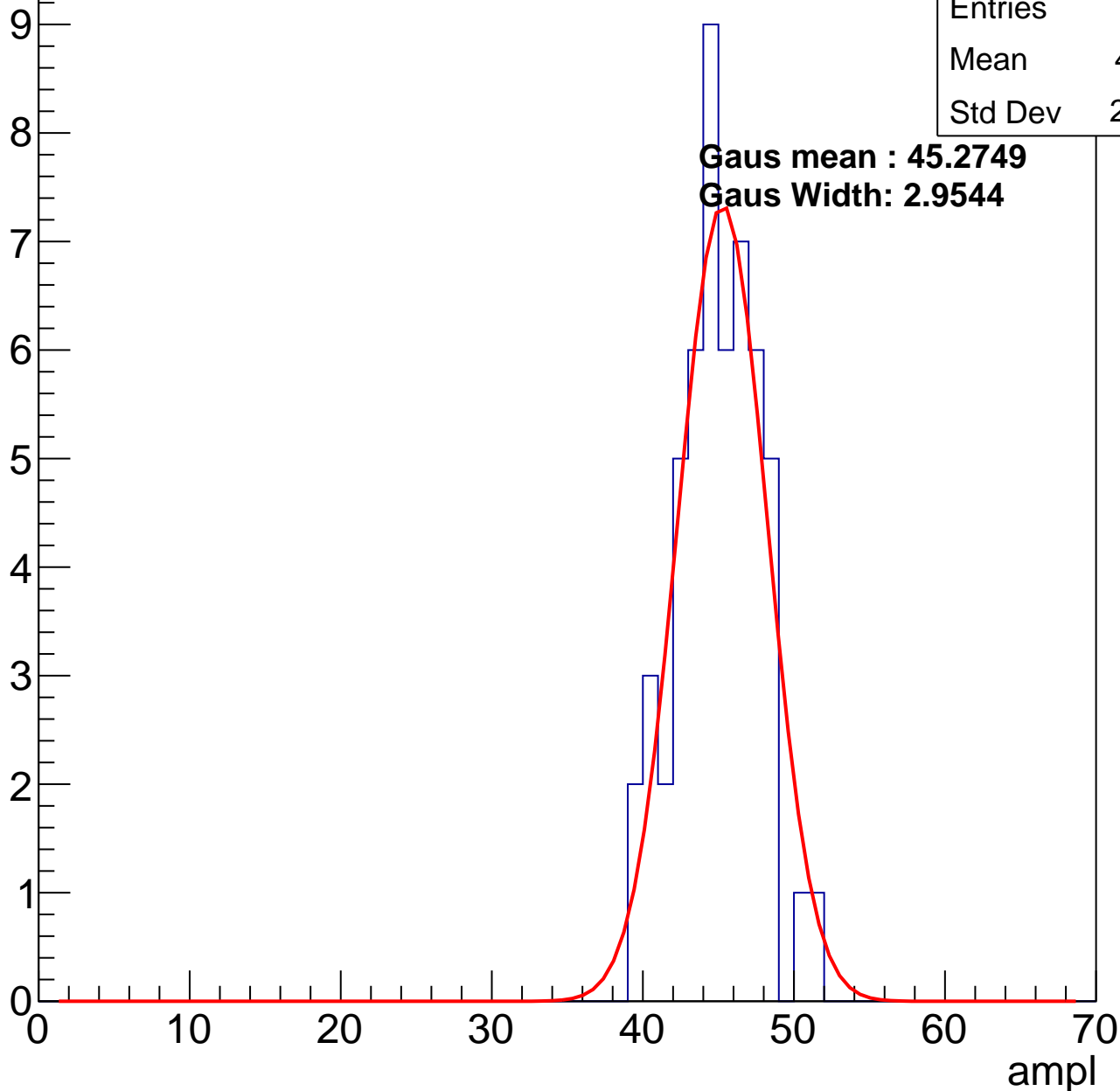
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.51
Std Dev	2.682

Gaus mean : 45.2749

Gaus Width: 2.9544



B0L002S, U26-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

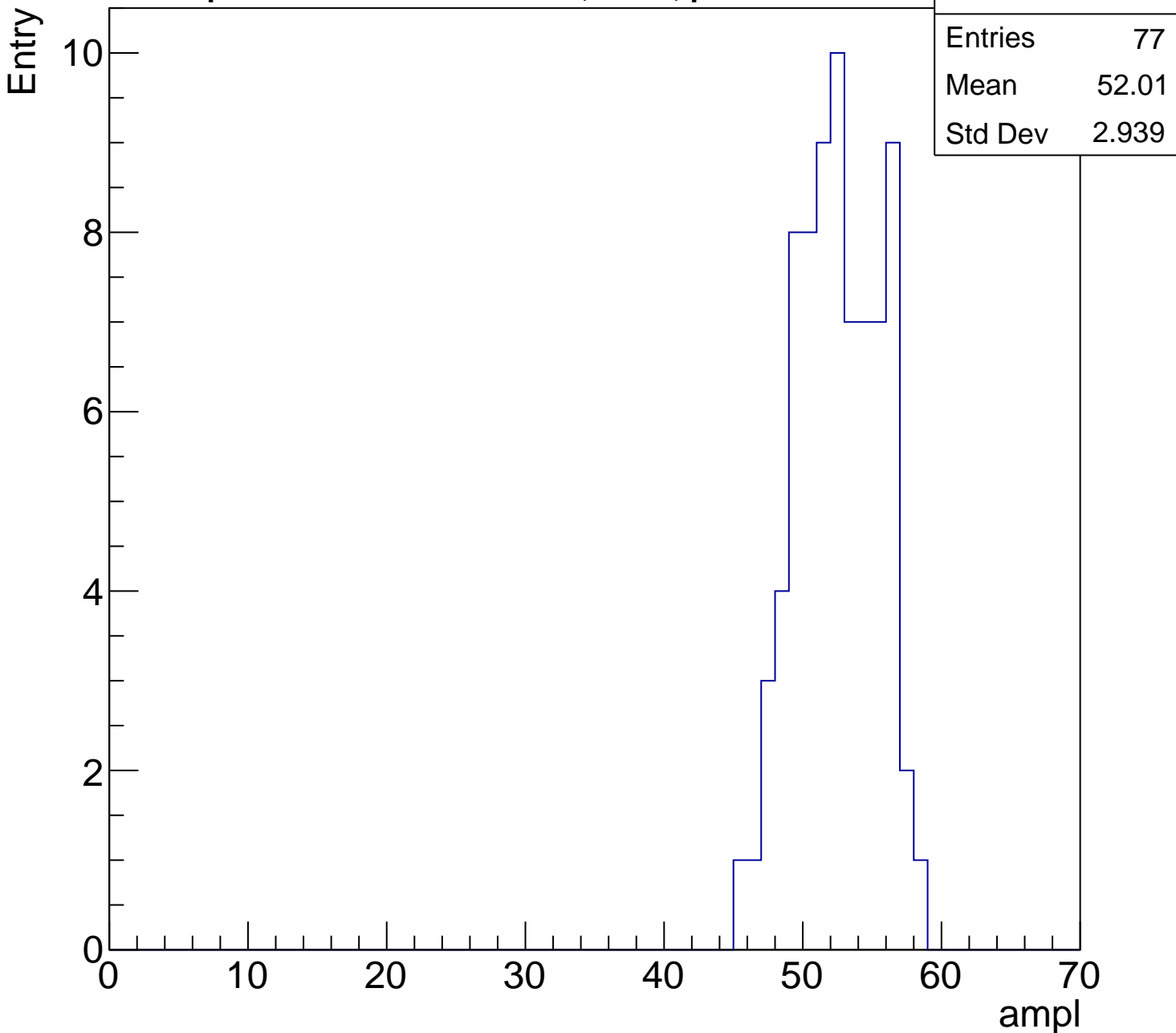
Entries	77
Mean	52.01
Std Dev	2.939

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

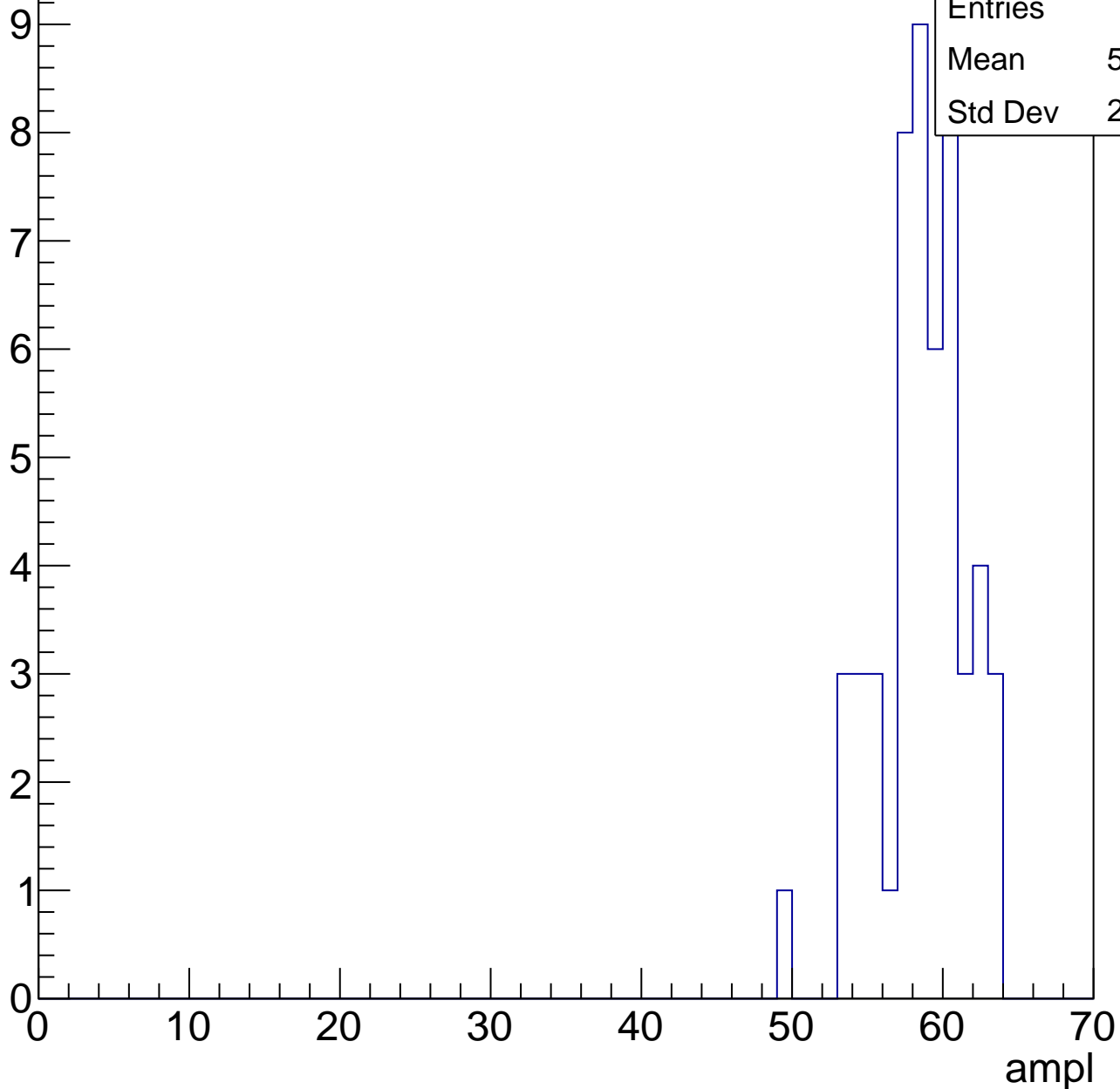
ampl



B0L002S, U26-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1

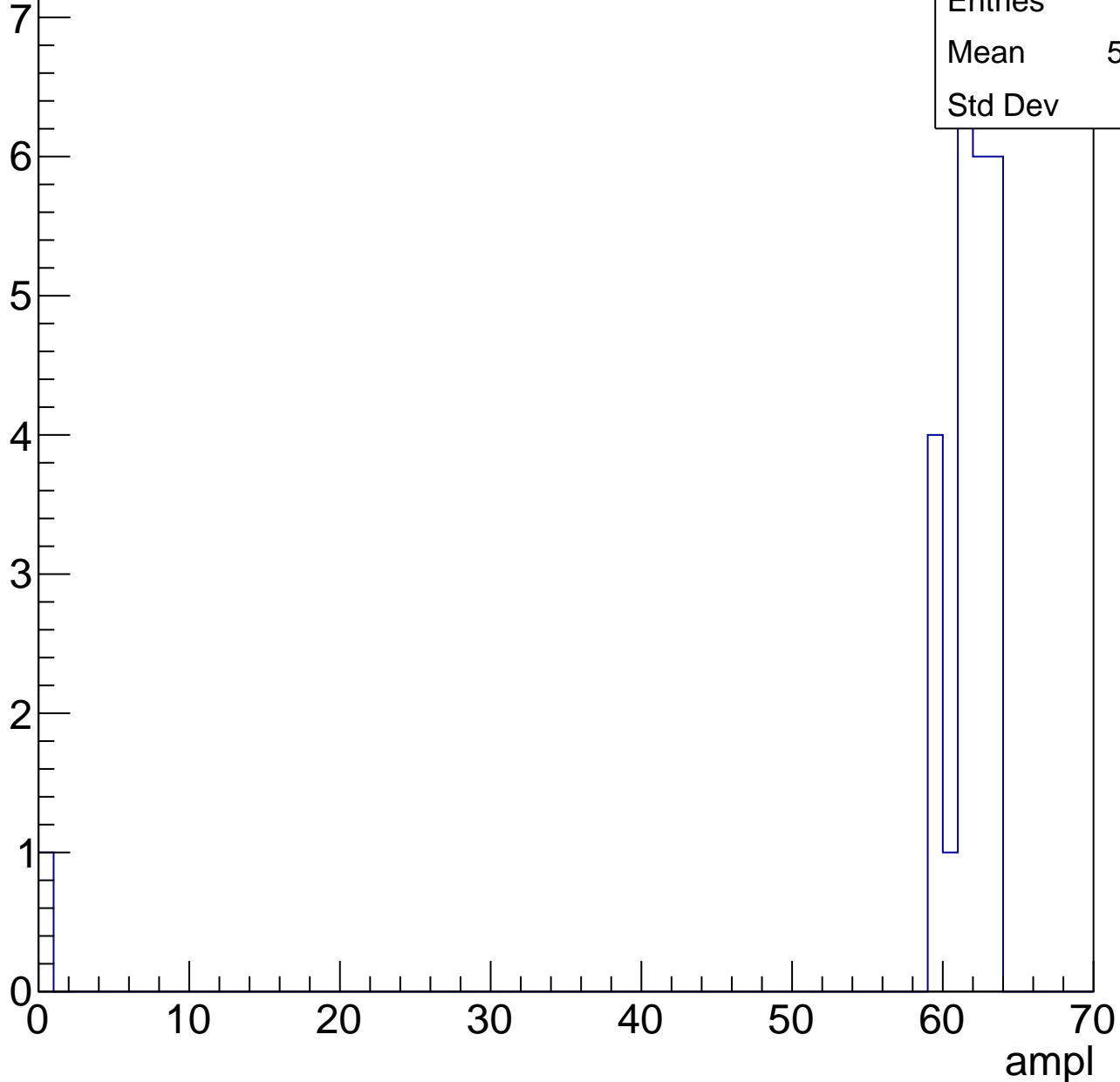
Entry



B0L002S, U26-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch60, adc0

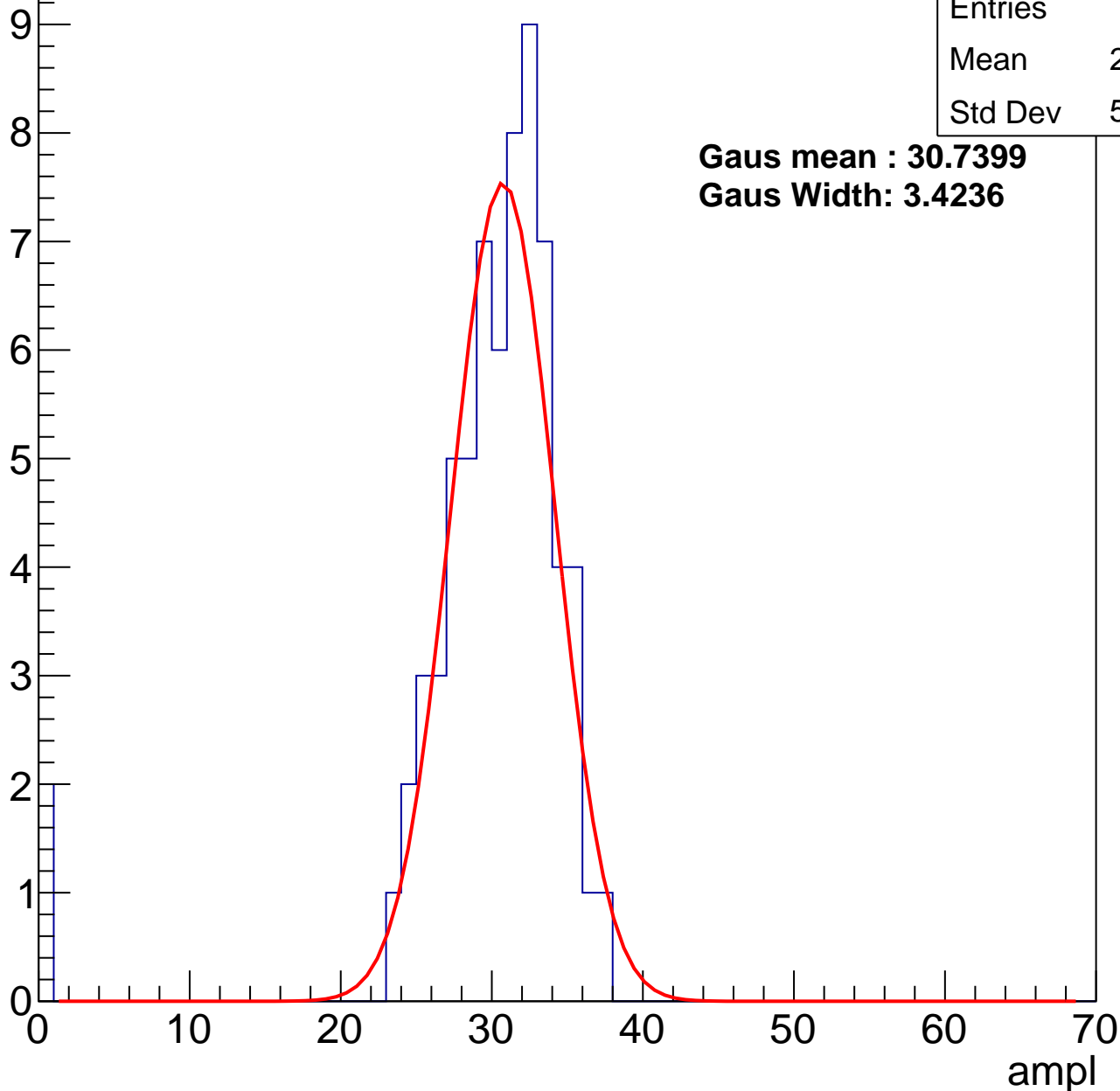
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.38
Std Dev	5.998

Gaus mean : 30.7399

Gaus Width: 3.4236



B0L002S, U26-ch60, adc1

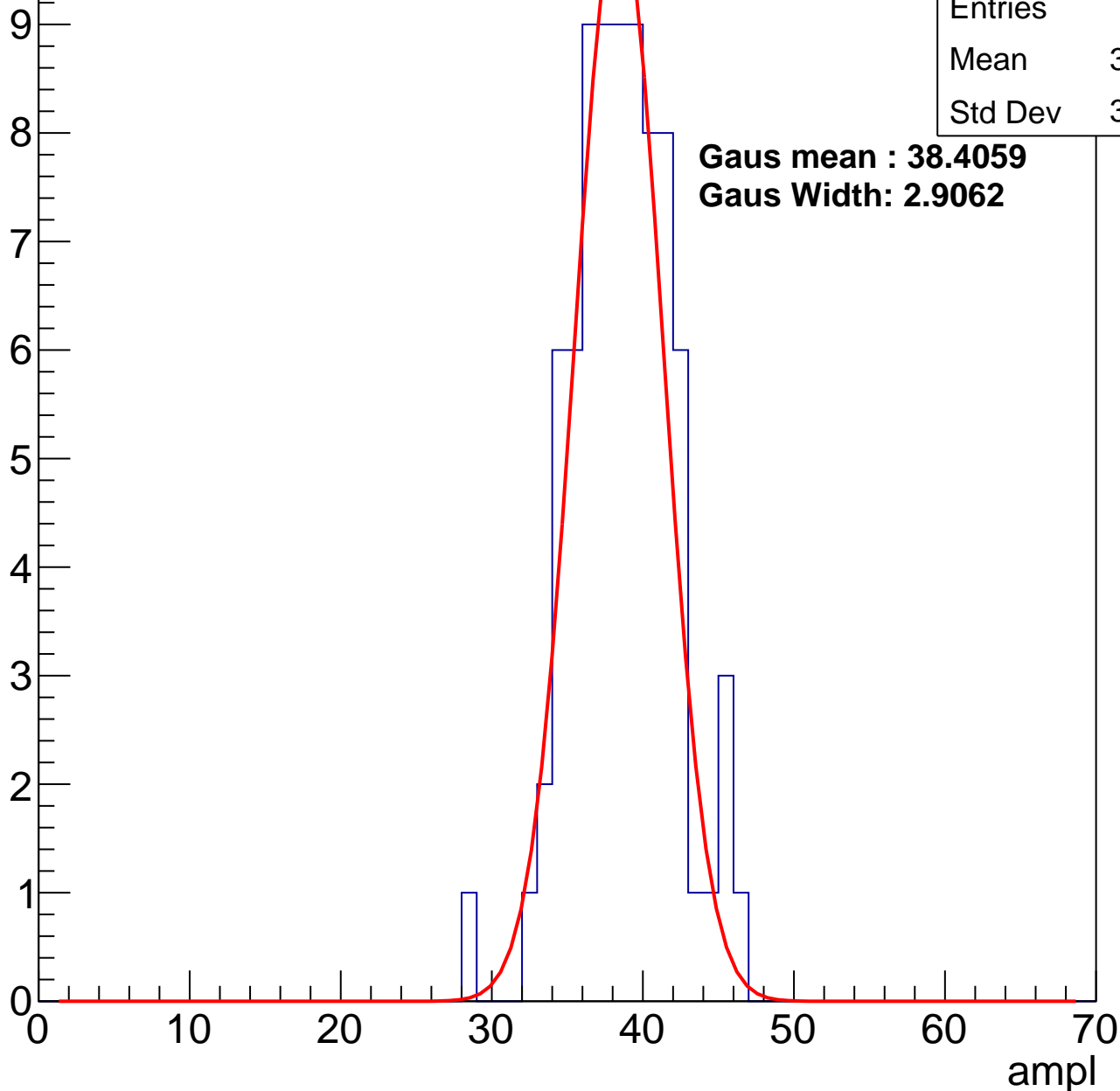
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	38.23
Std Dev	3.275

Gaus mean : 38.4059

Gaus Width: 2.9062



B0L002S, U26-ch60, adc2

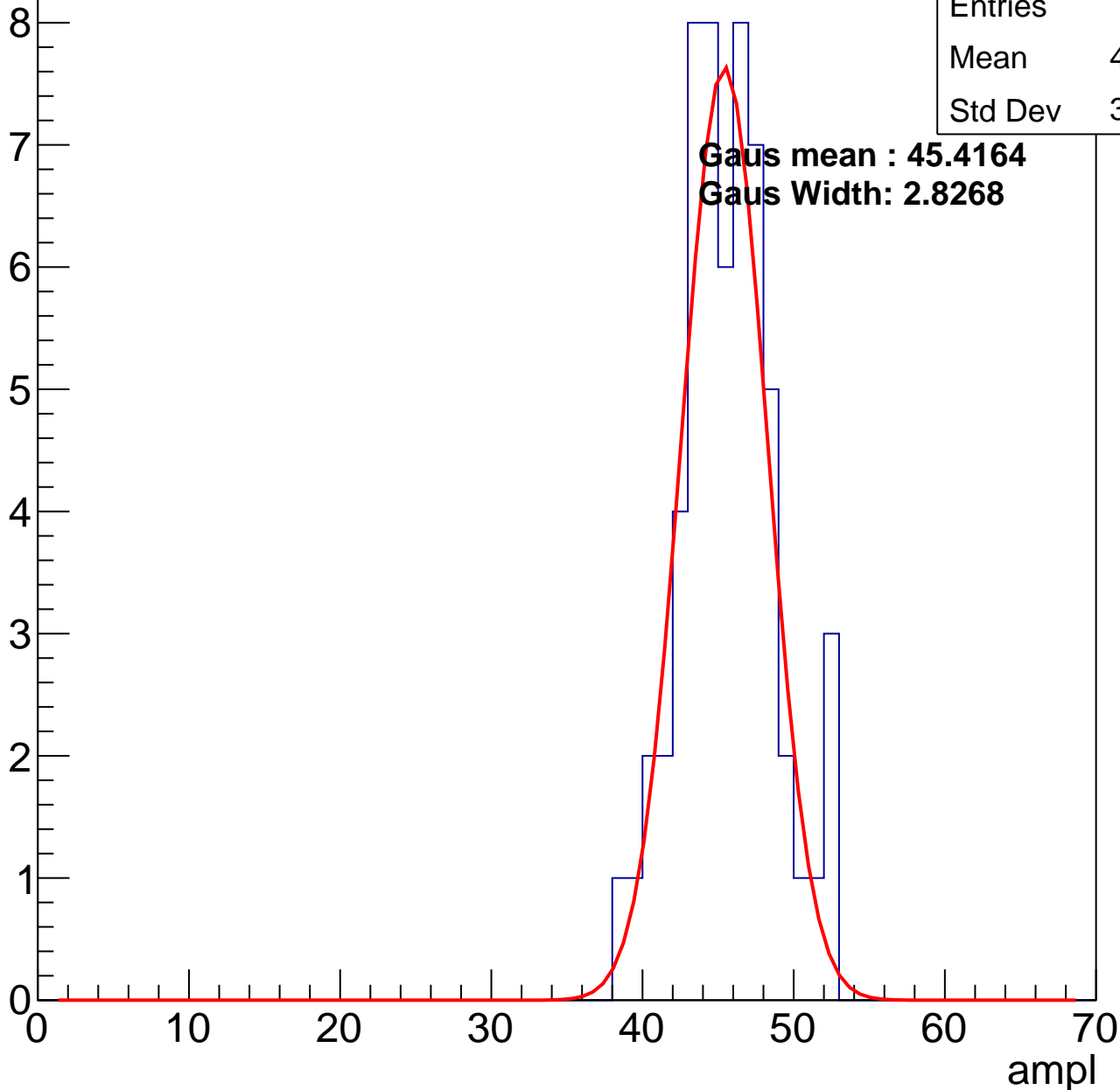
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	45.17
Std Dev	3.087

Gaus mean : 45.4164

Gaus Width: 2.8268

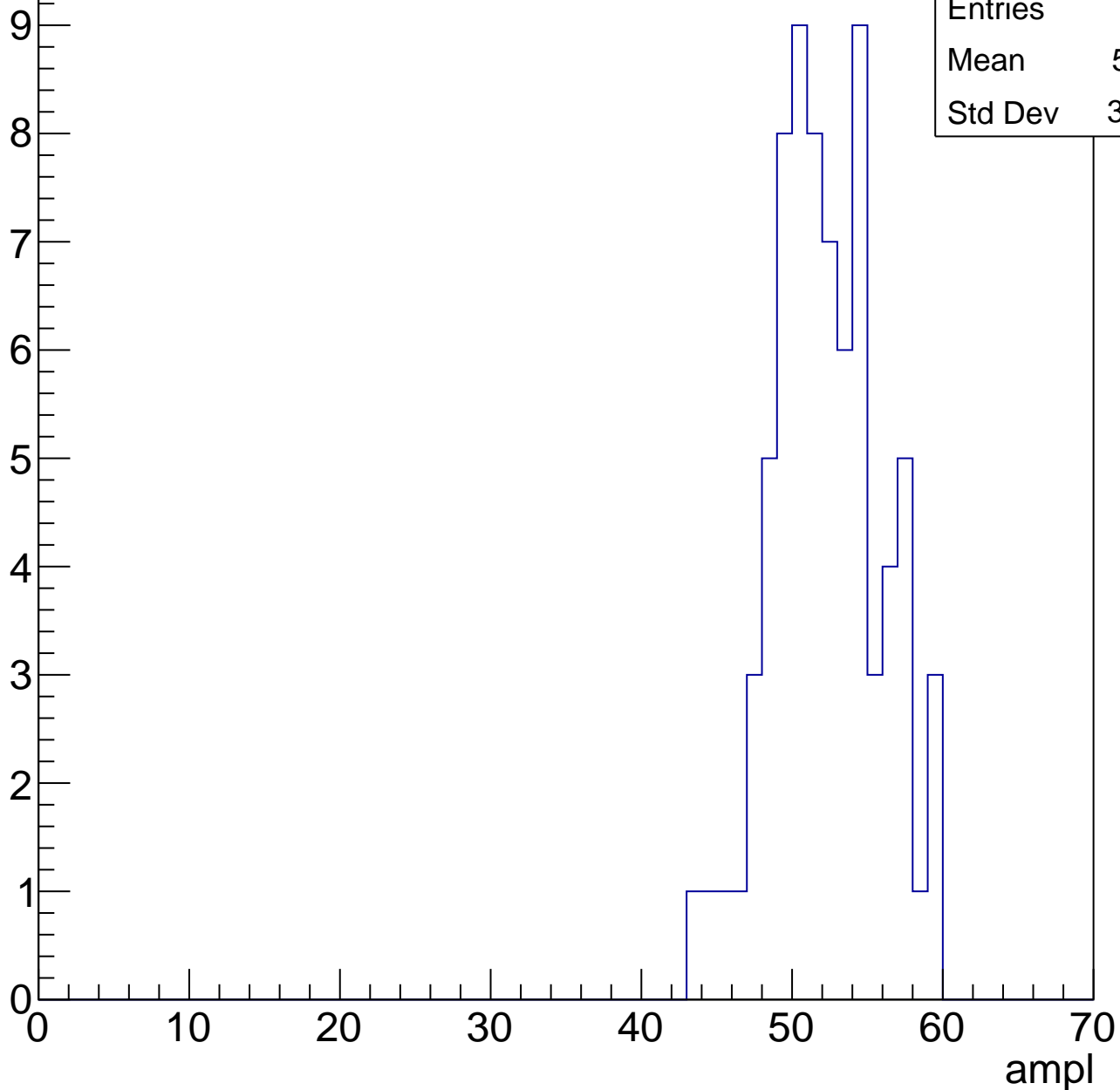


B0L002S, U26-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	51.81
Std Dev	3.539

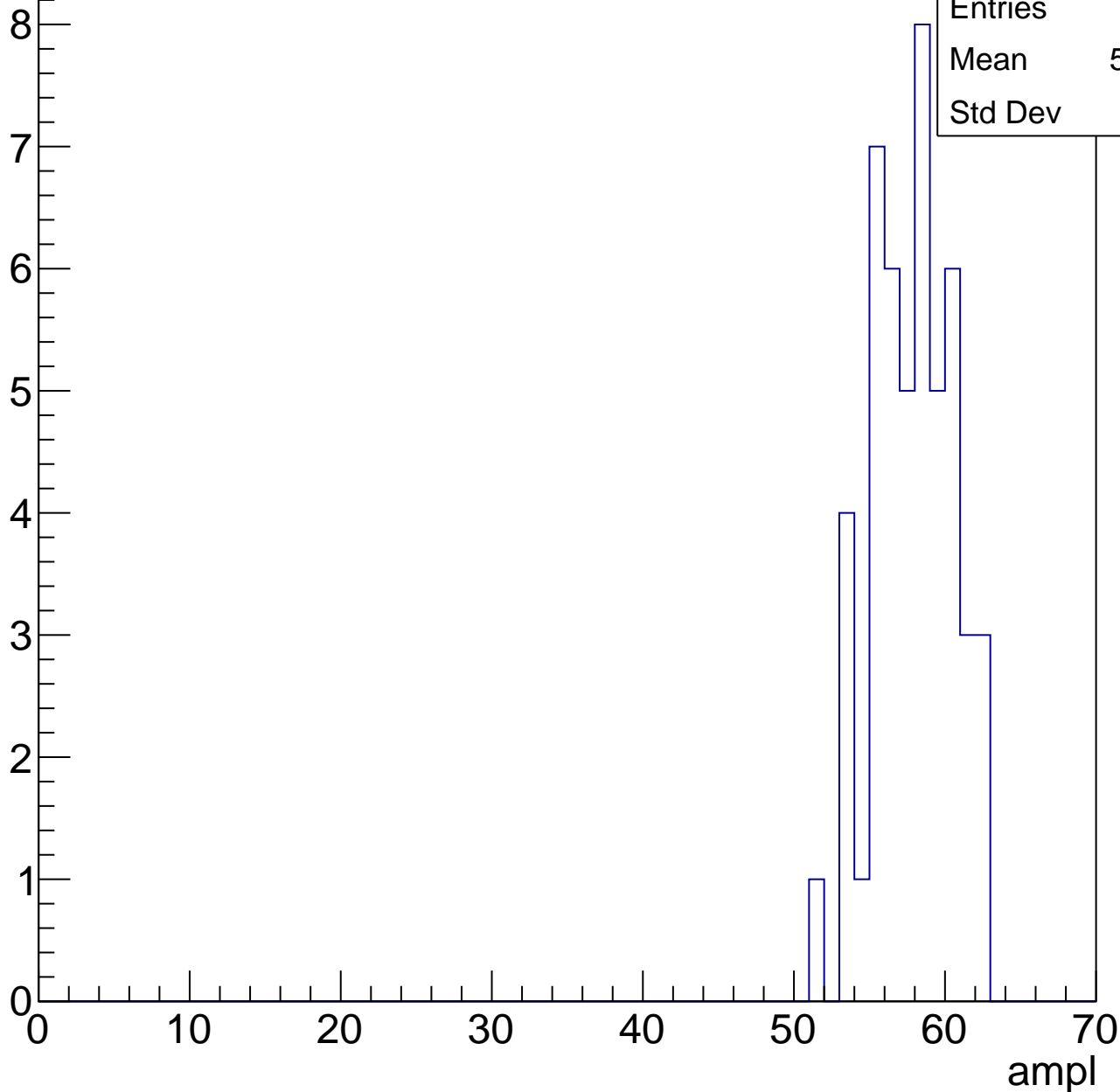


B0L002S, U26-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	57.37
Std Dev	2.64

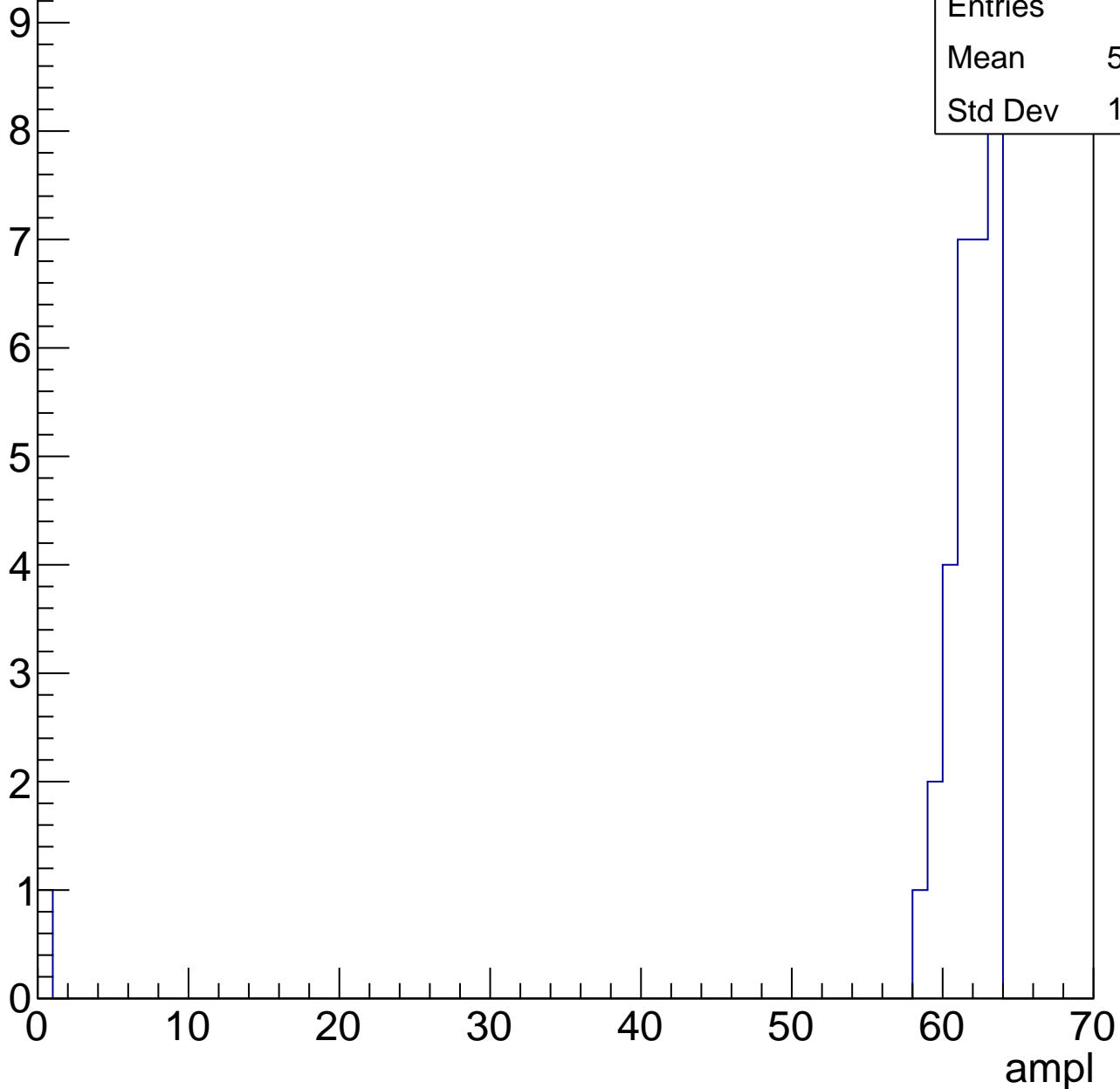


B0L002S, U26-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.48
Std Dev	10.95



B0L002S, U26-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

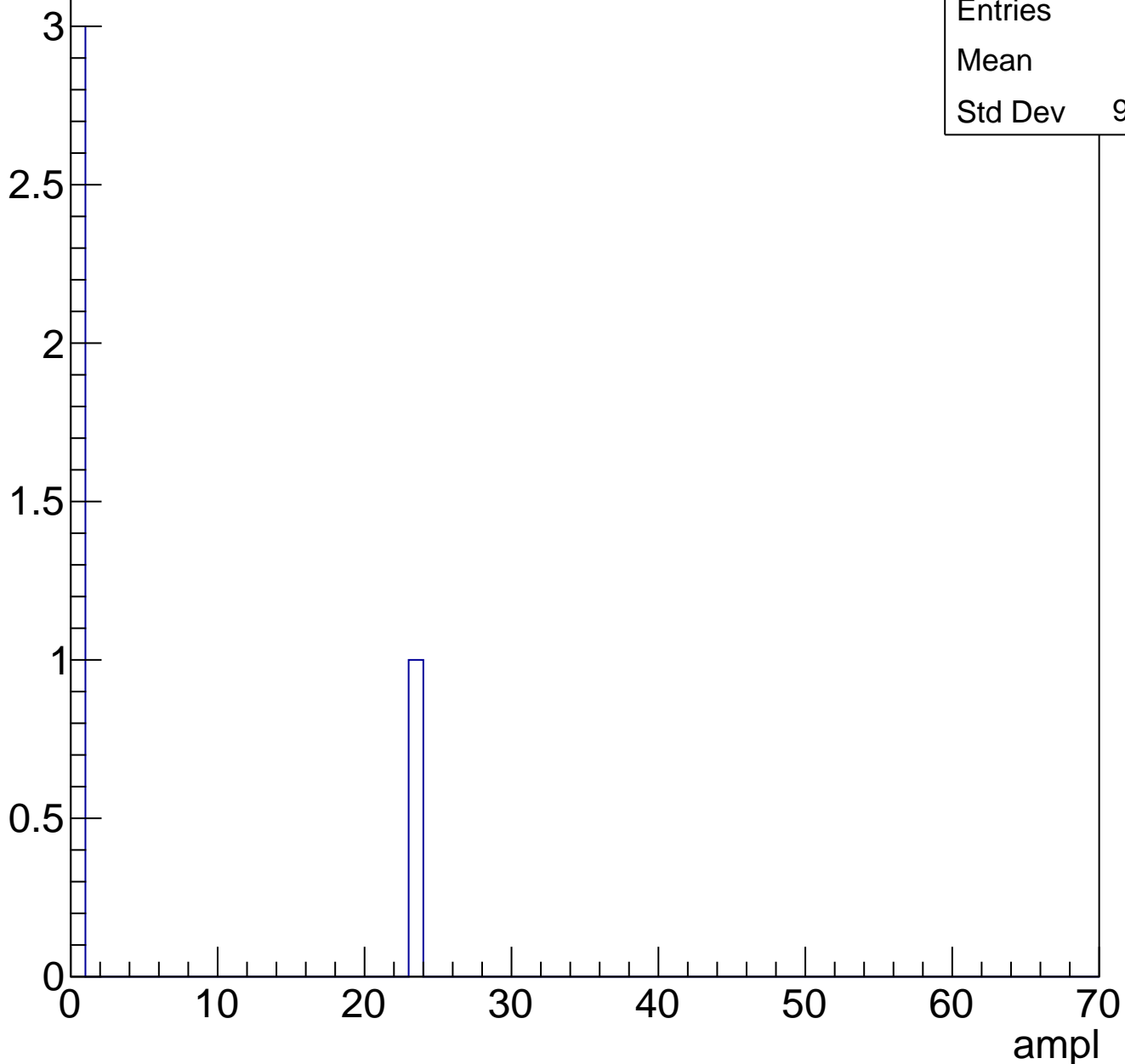
Entry



B0L002S, U26-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	5.75
Std Dev	9.959

B0L002S, U26-ch61, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	31.32
Std Dev	3.249

Gaus mean : 31.8097

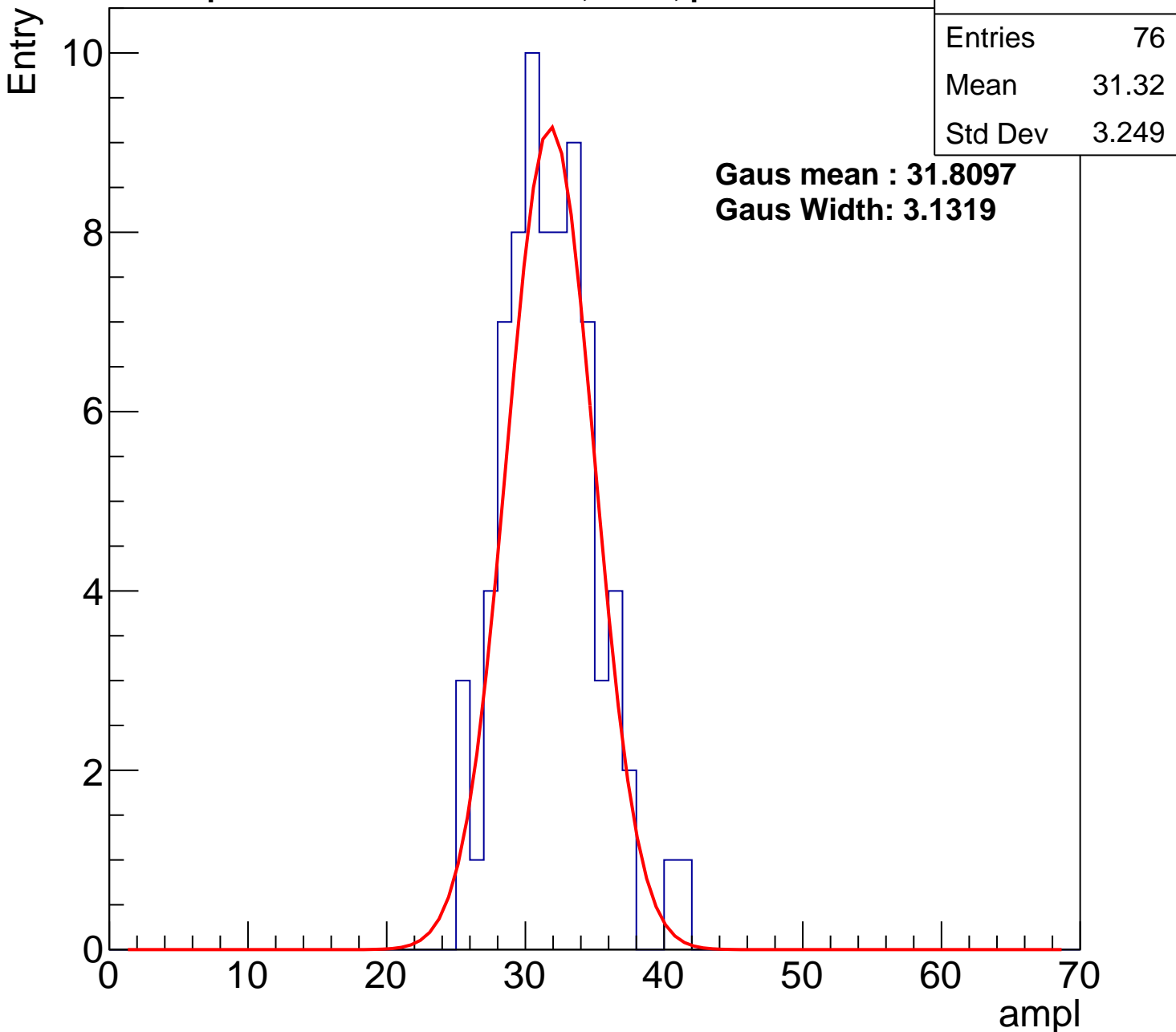
Gaus Width: 3.1319

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch61, adc1

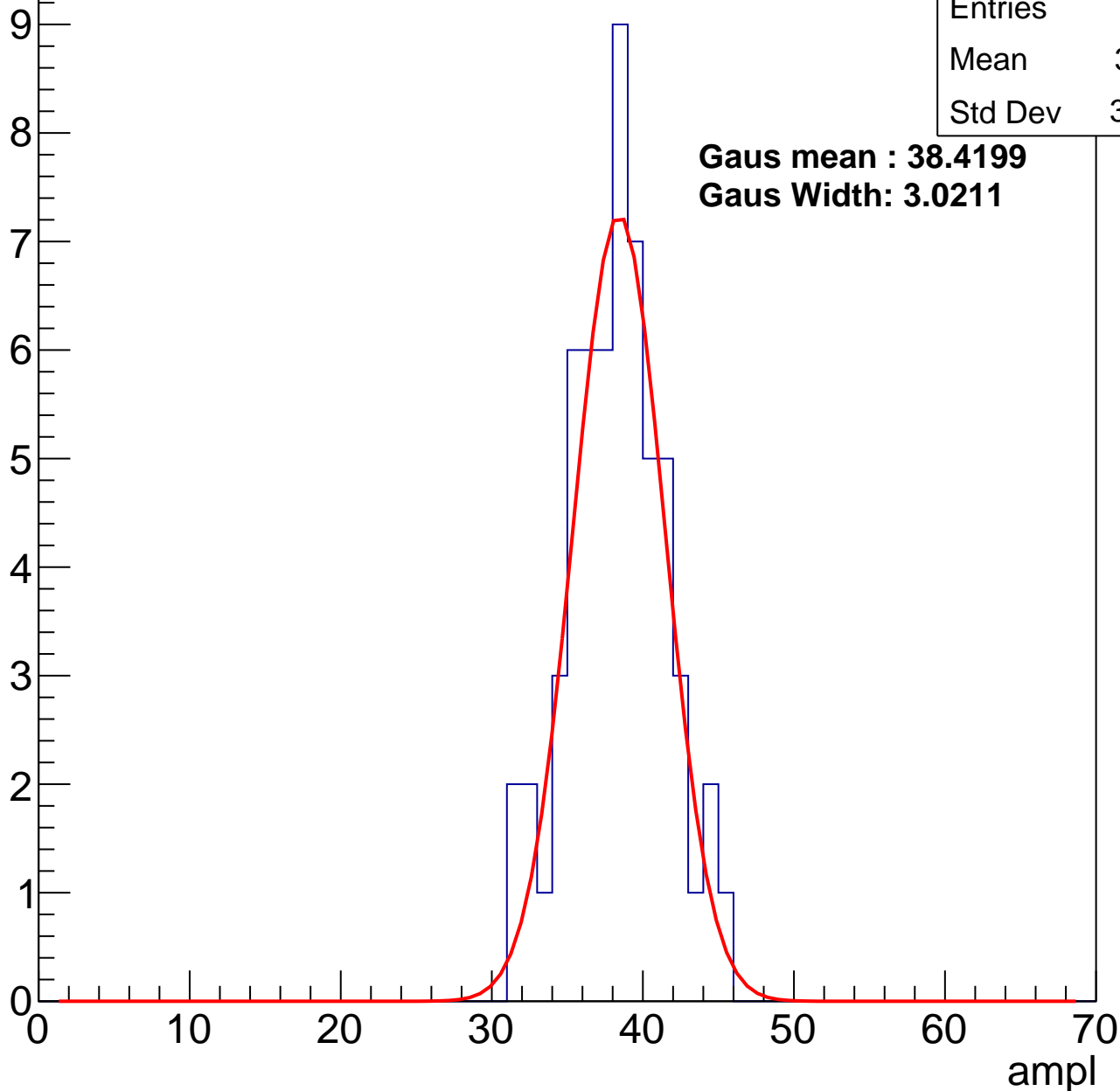
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	37.81
Std Dev	3.165

Gaus mean : 38.4199

Gaus Width: 3.0211



B0L002S, U26-ch61, adc2

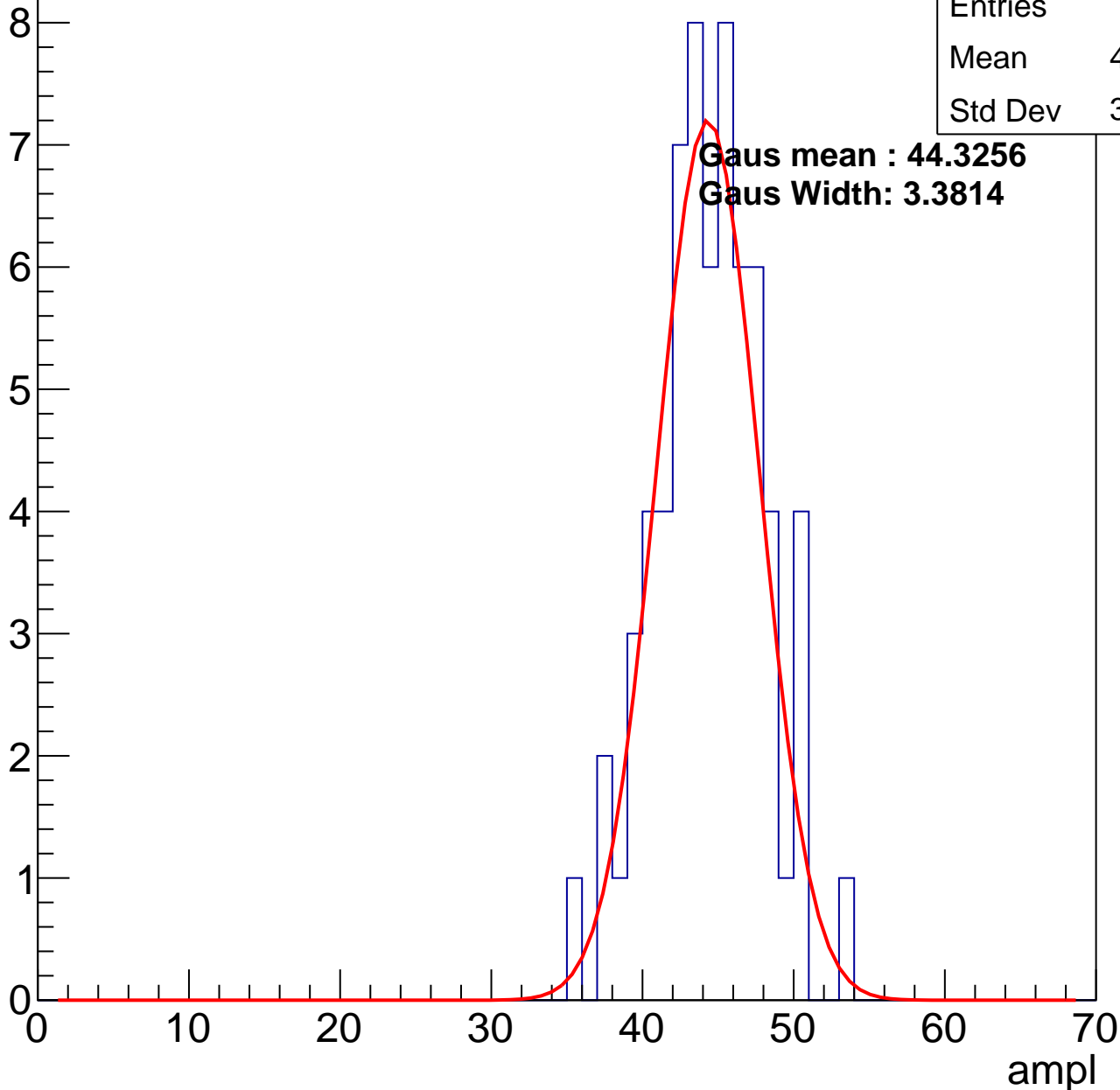
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.97
Std Dev	3.538

Gaus mean : 44.3256

Gaus Width: 3.3814

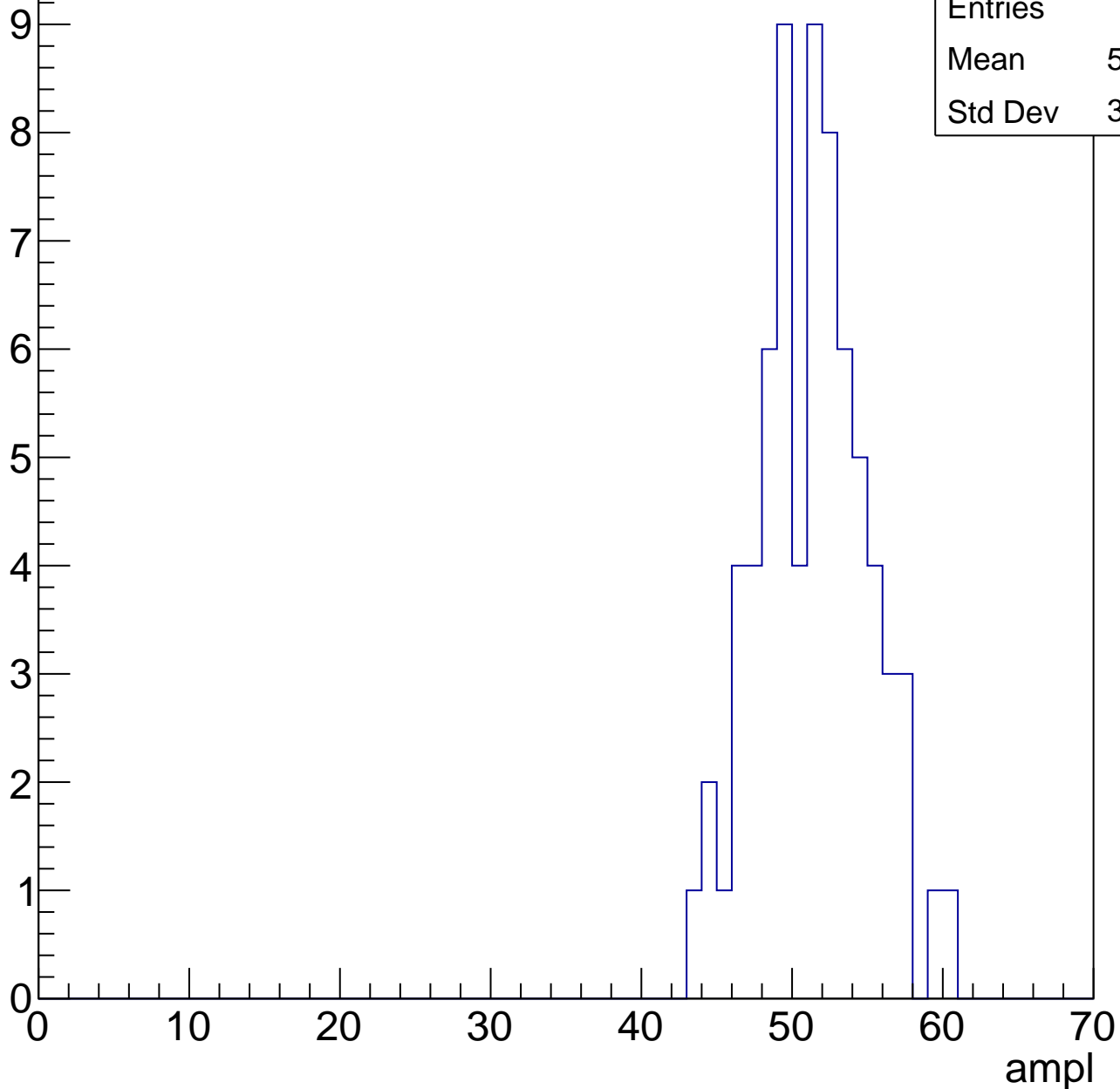


B0L002S, U26-ch61, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

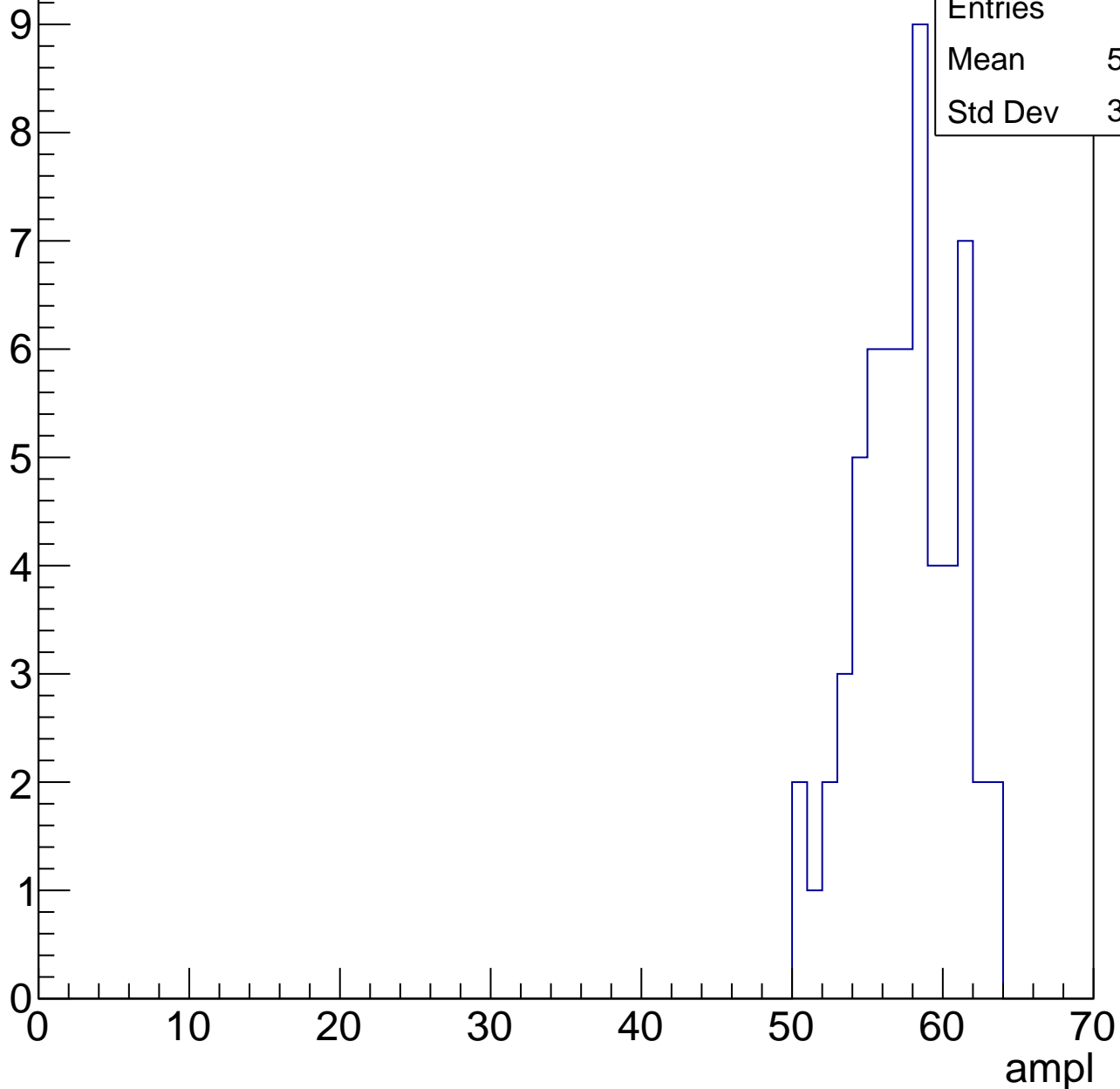
Entries	71
Mean	50.96
Std Dev	3.609



B0L002S, U26-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

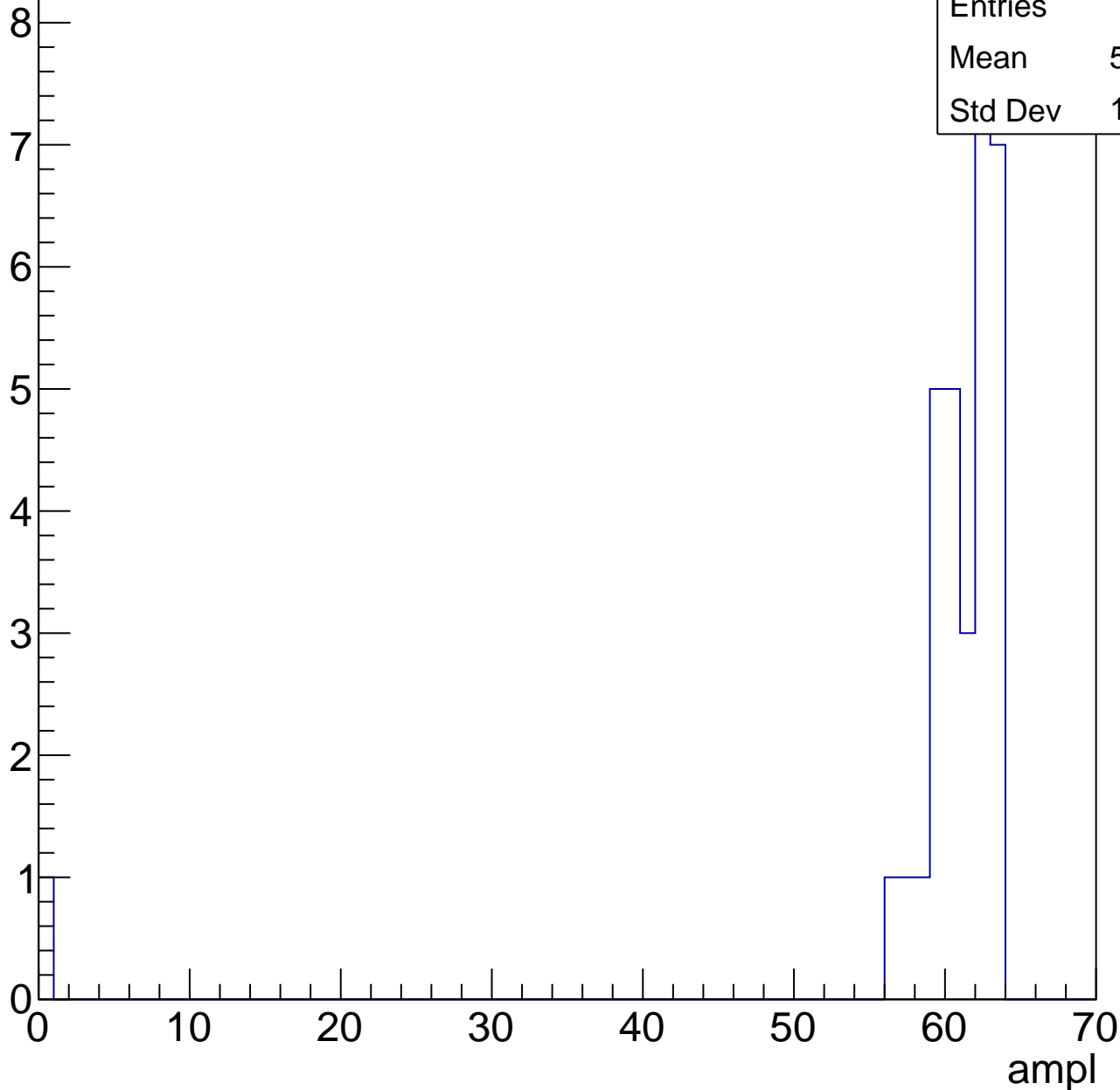


B0L002S, U26-ch61, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	58.94
Std Dev	10.75



B0L002S, U26-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U26-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch62, adc0

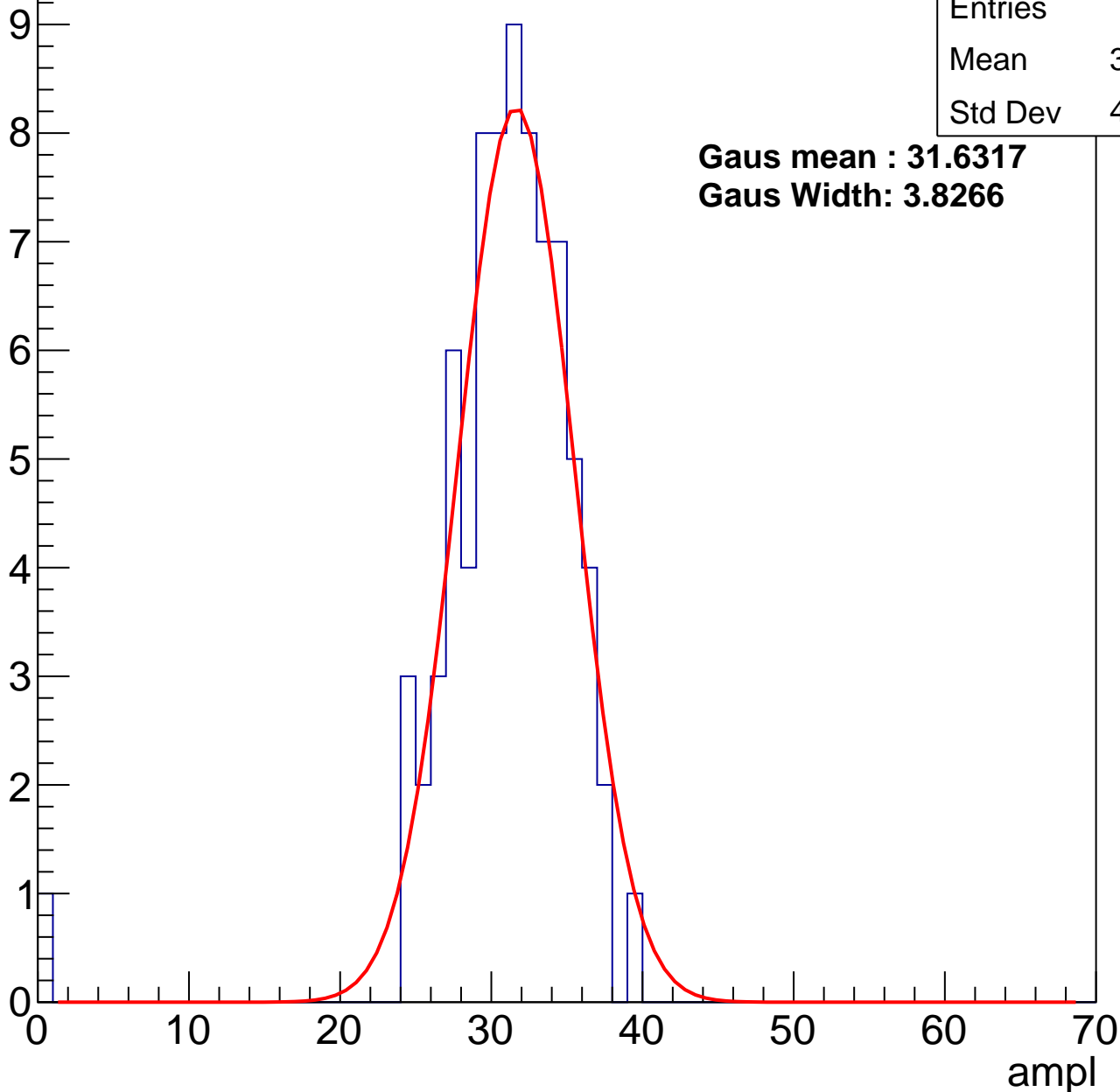
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	30.54
Std Dev	4.838

Gaus mean : 31.6317

Gaus Width: 3.8266



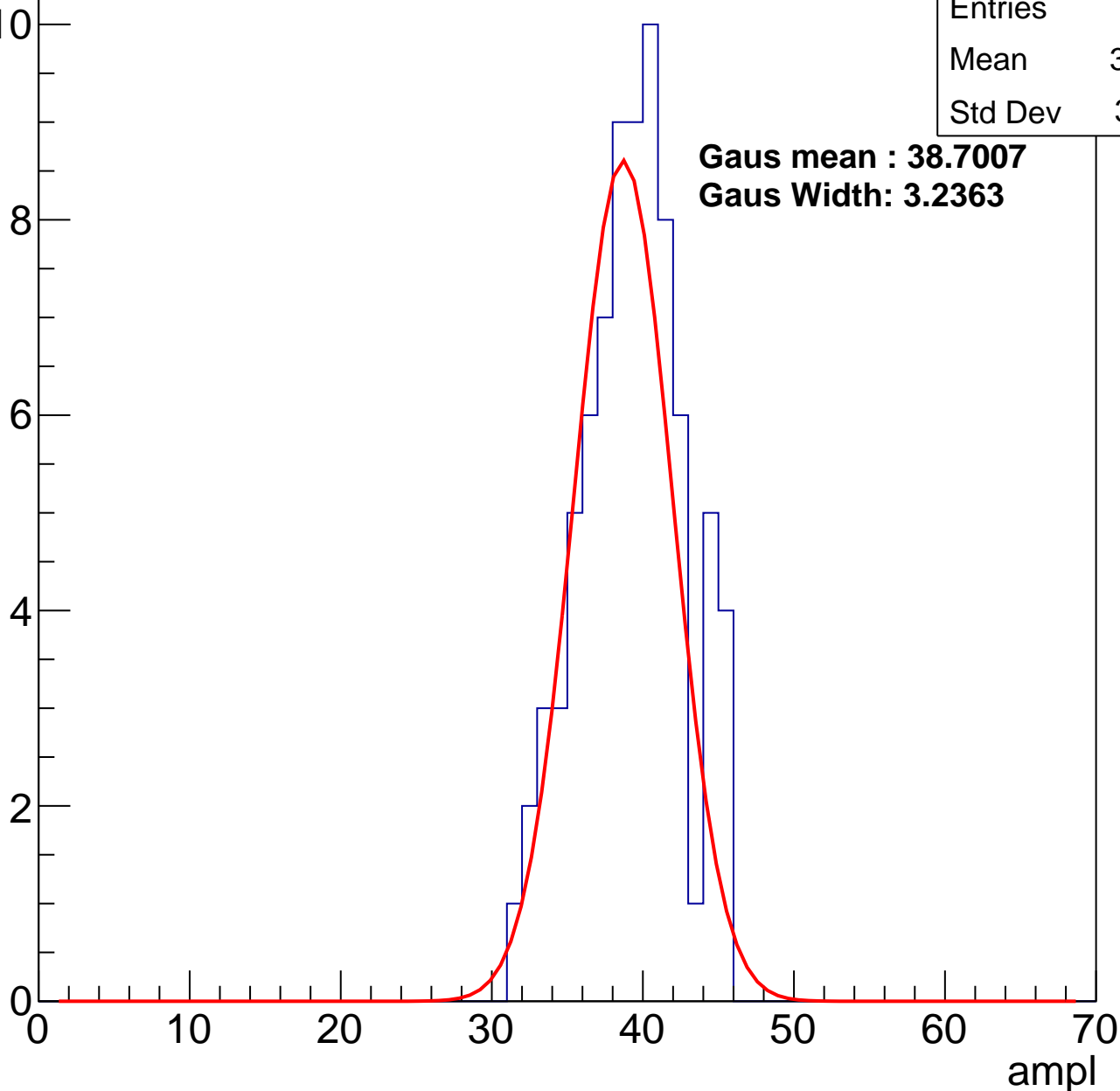
B0L002S, U26-ch62, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	38.76
Std Dev	3.361

Gaus mean : 38.7007
Gaus Width: 3.2363



B0L002S, U26-ch62, adc2

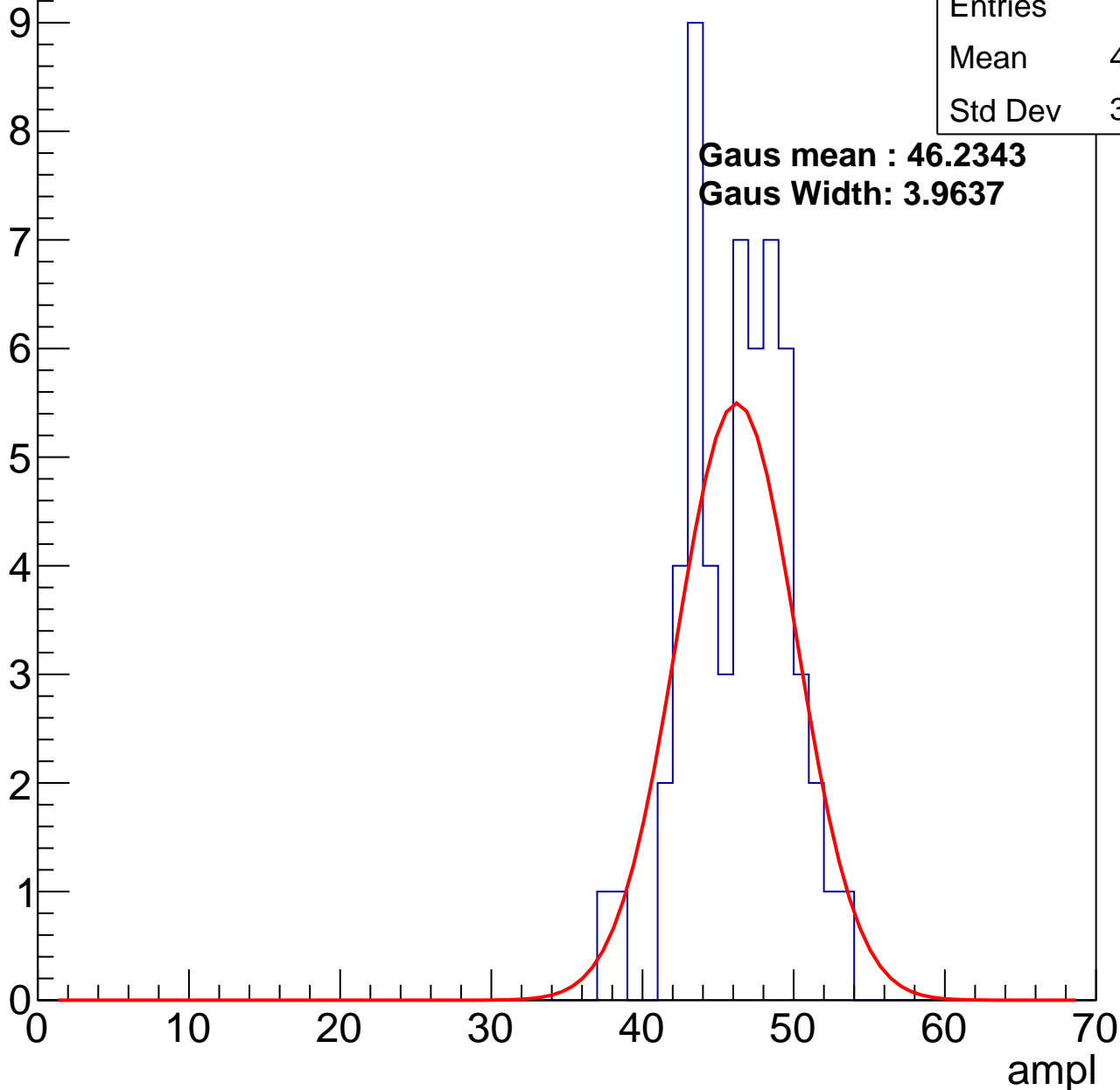
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	45.86
Std Dev	3.322

Gaus mean : 46.2343

Gaus Width: 3.9637

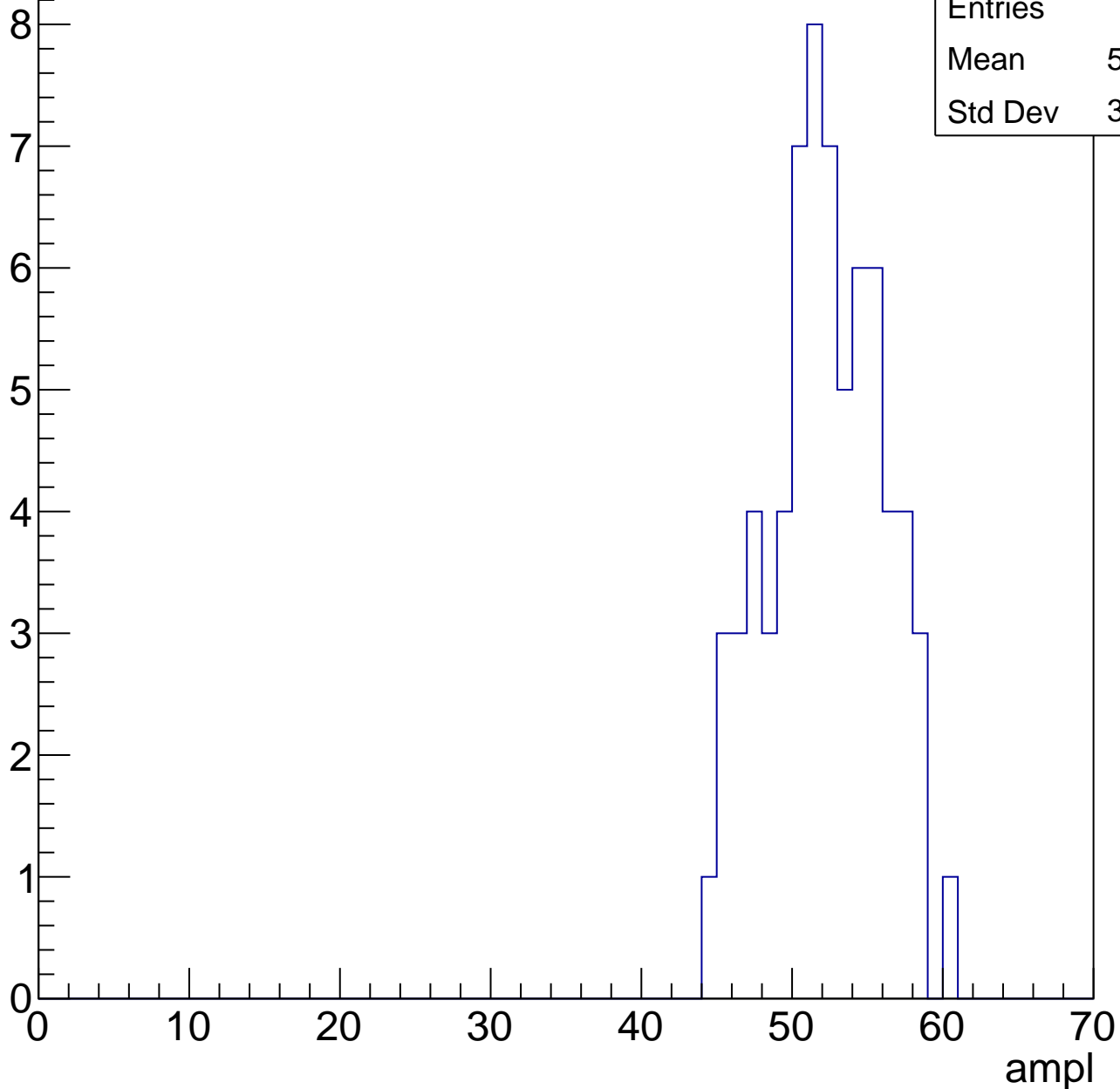


B0L002S, U26-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

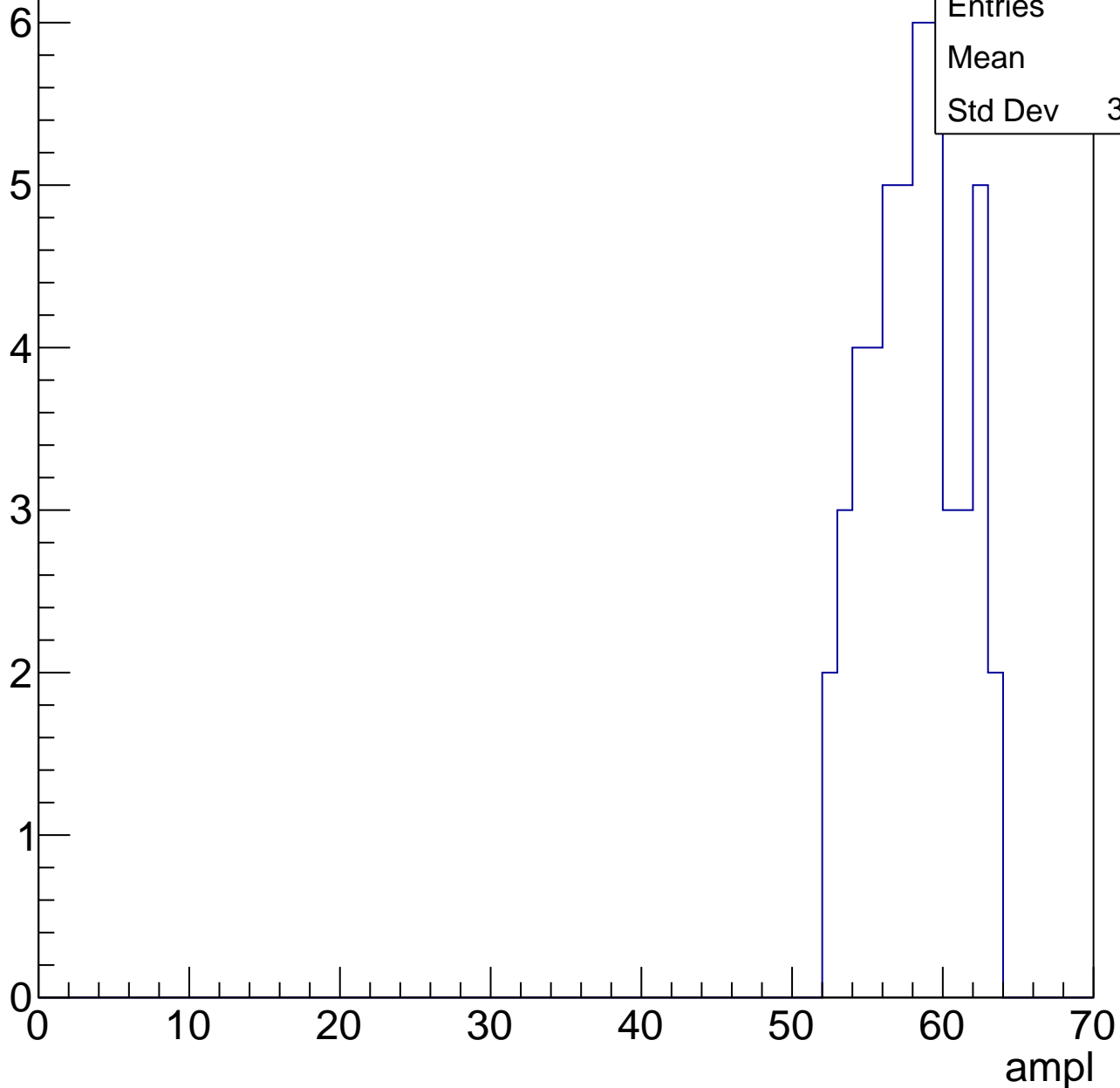
Entries	69
Mean	51.77
Std Dev	3.719



B0L002S, U26-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

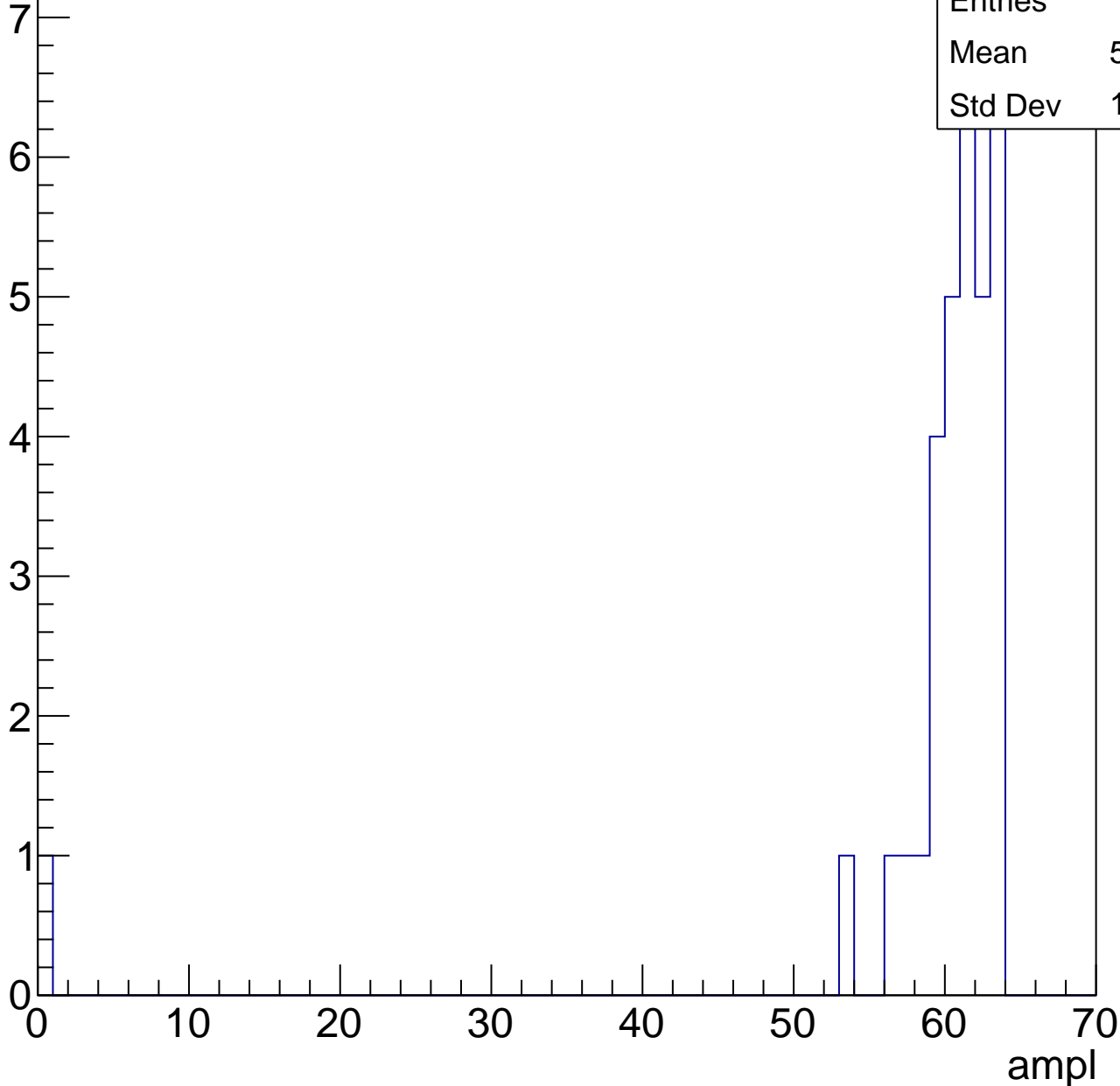


B0L002S, U26-ch62, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.73
Std Dev	10.62



B0L002S, U26-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

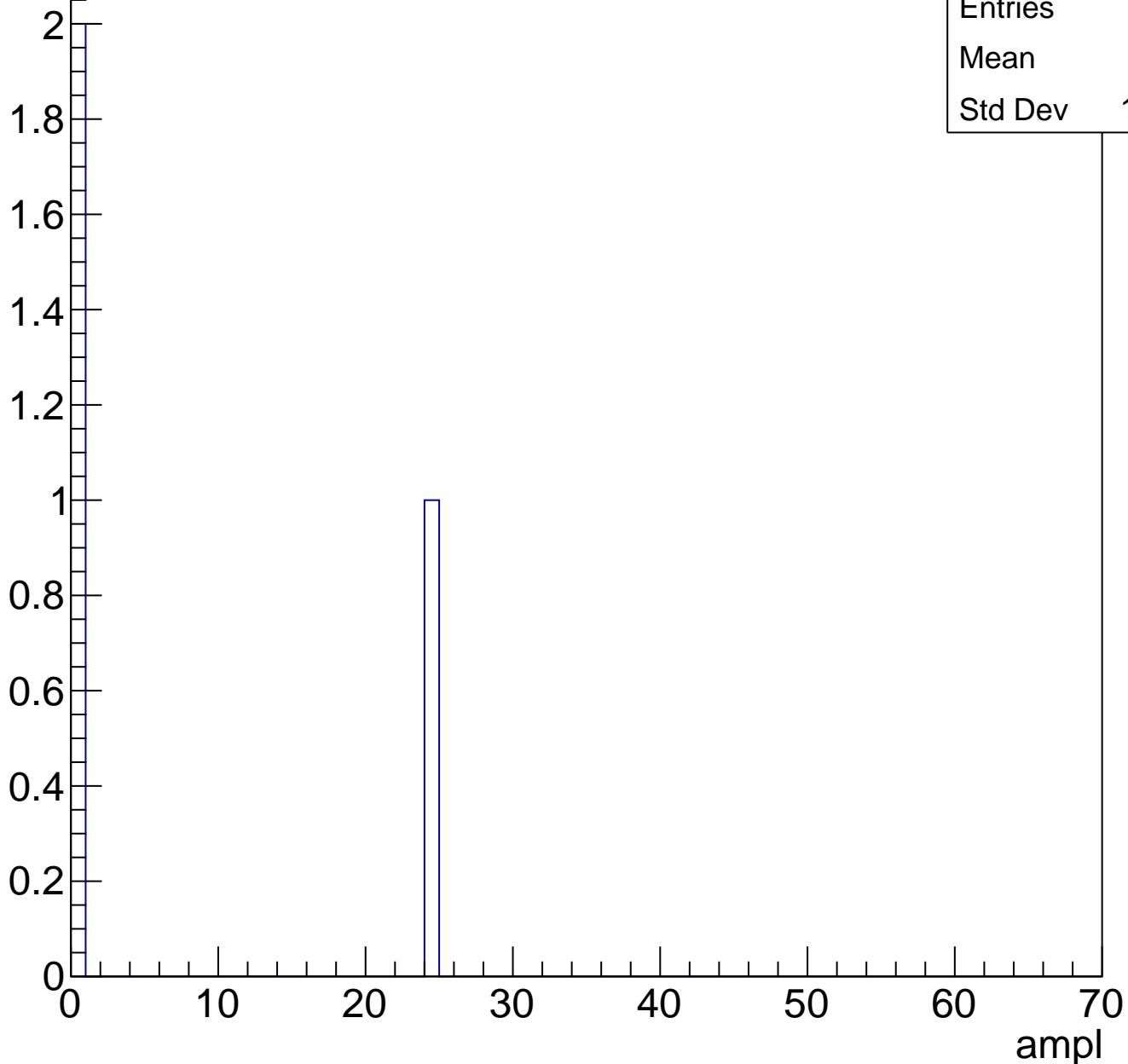
Entry



B0L002S, U26-ch62, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8
Std Dev	11.31

B0L002S, U26-ch63, adc0

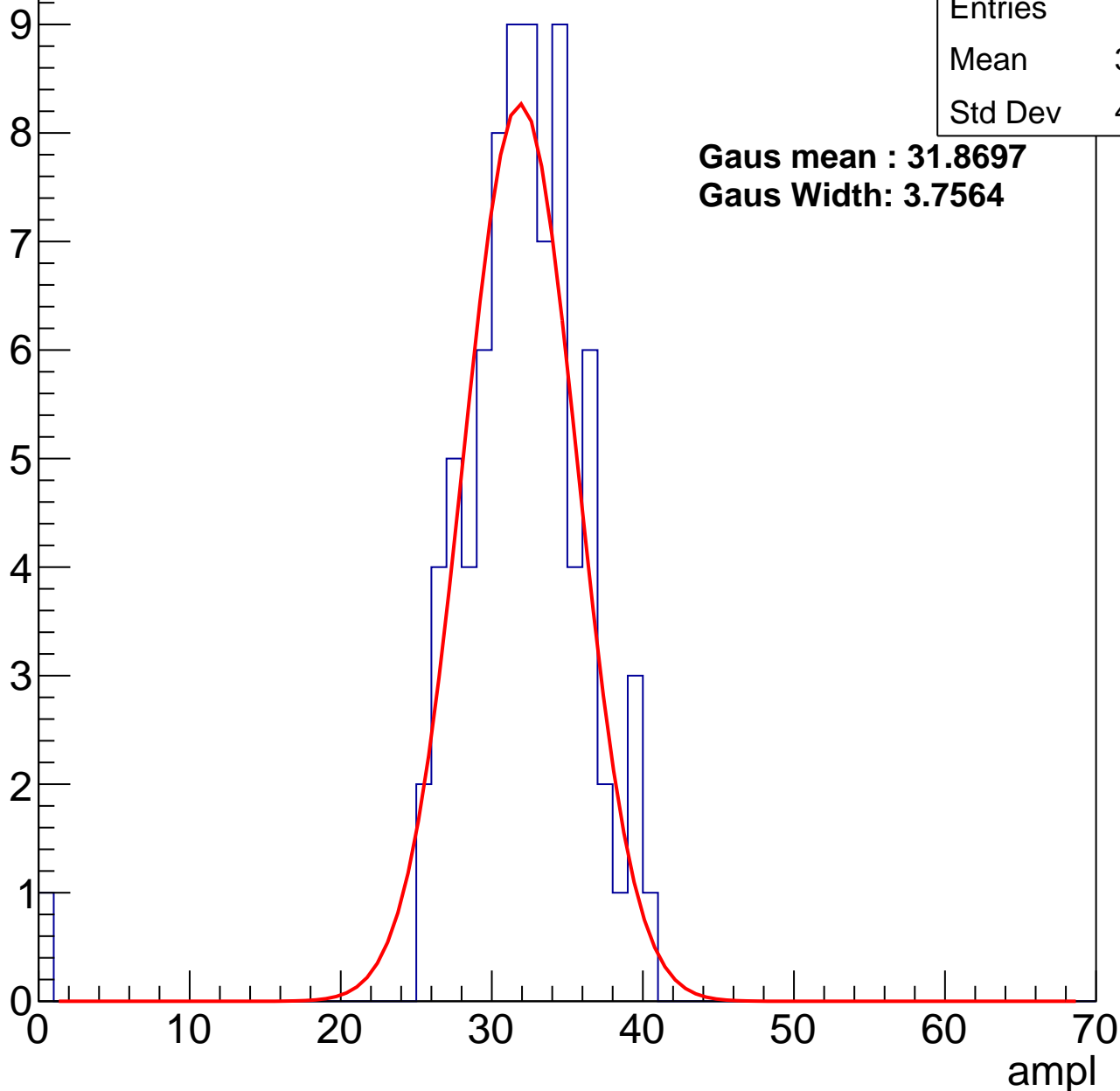
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	31.41
Std Dev	4.961

Gaus mean : 31.8697

Gaus Width: 3.7564



B0L002S, U26-ch63, adc1

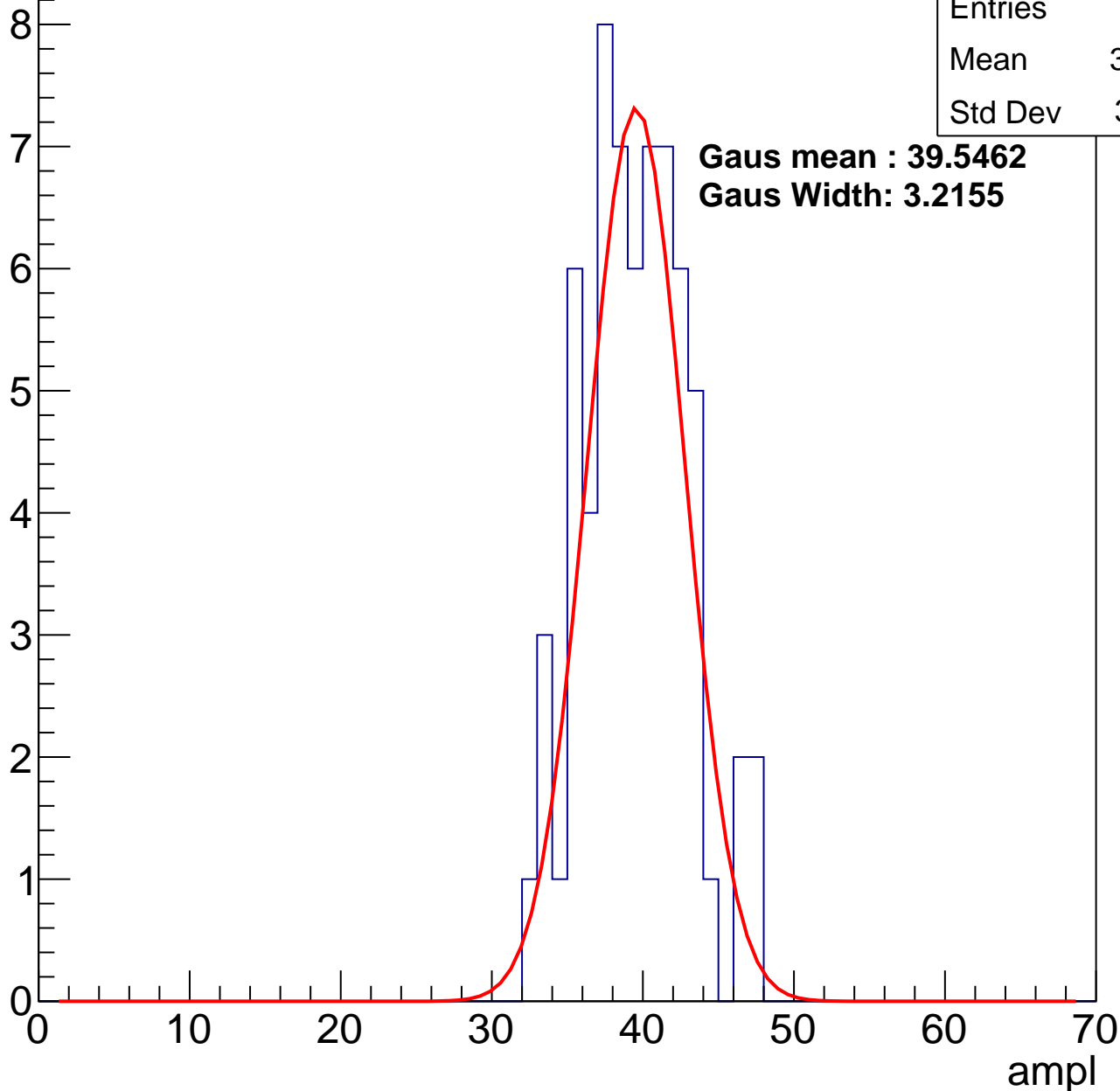
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	39.08
Std Dev	3.421

Gaus mean : 39.5462

Gaus Width: 3.2155



B0L002S, U26-ch63, adc2

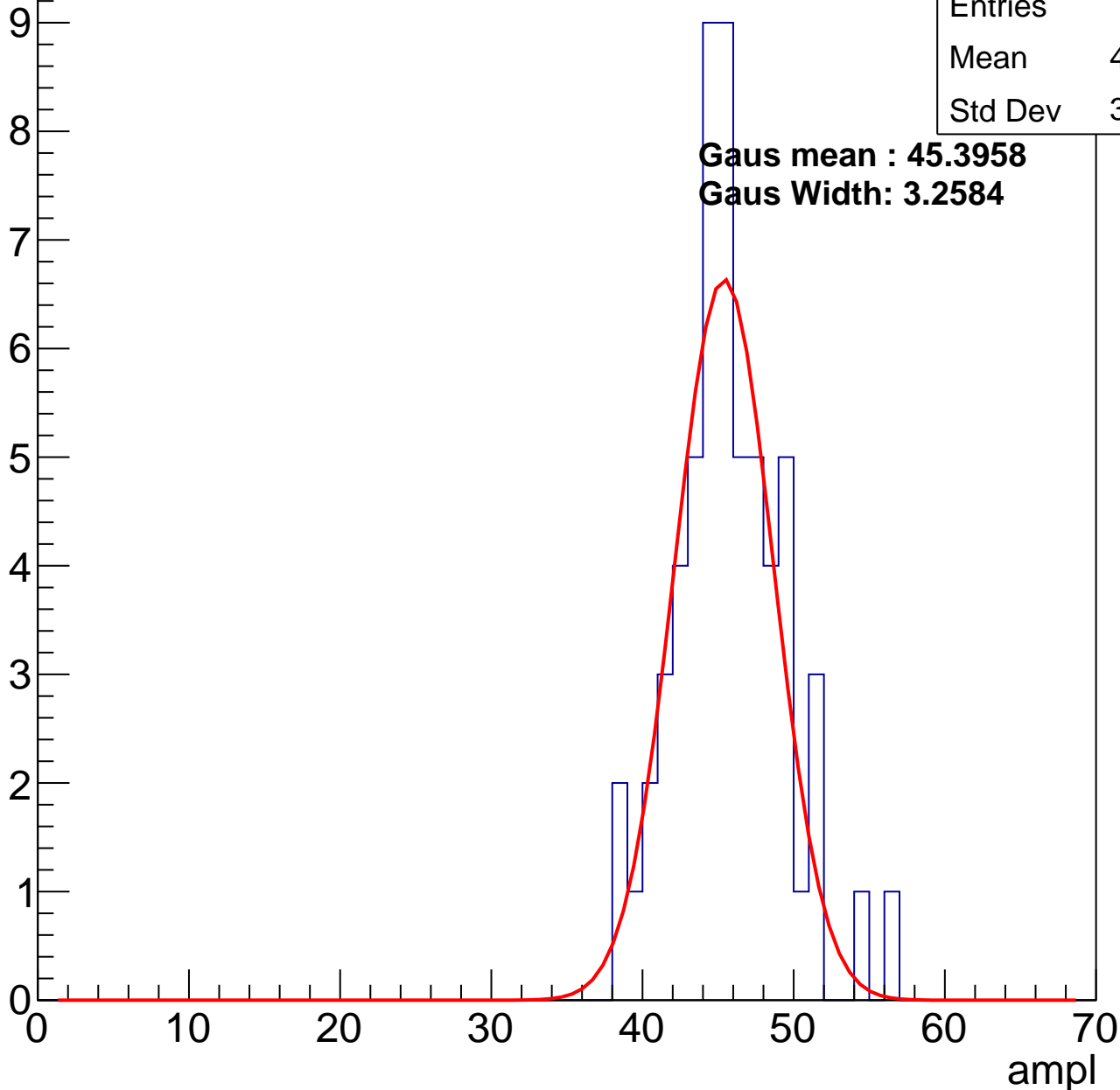
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	45.28
Std Dev	3.578

Gaus mean : 45.3958

Gaus Width: 3.2584

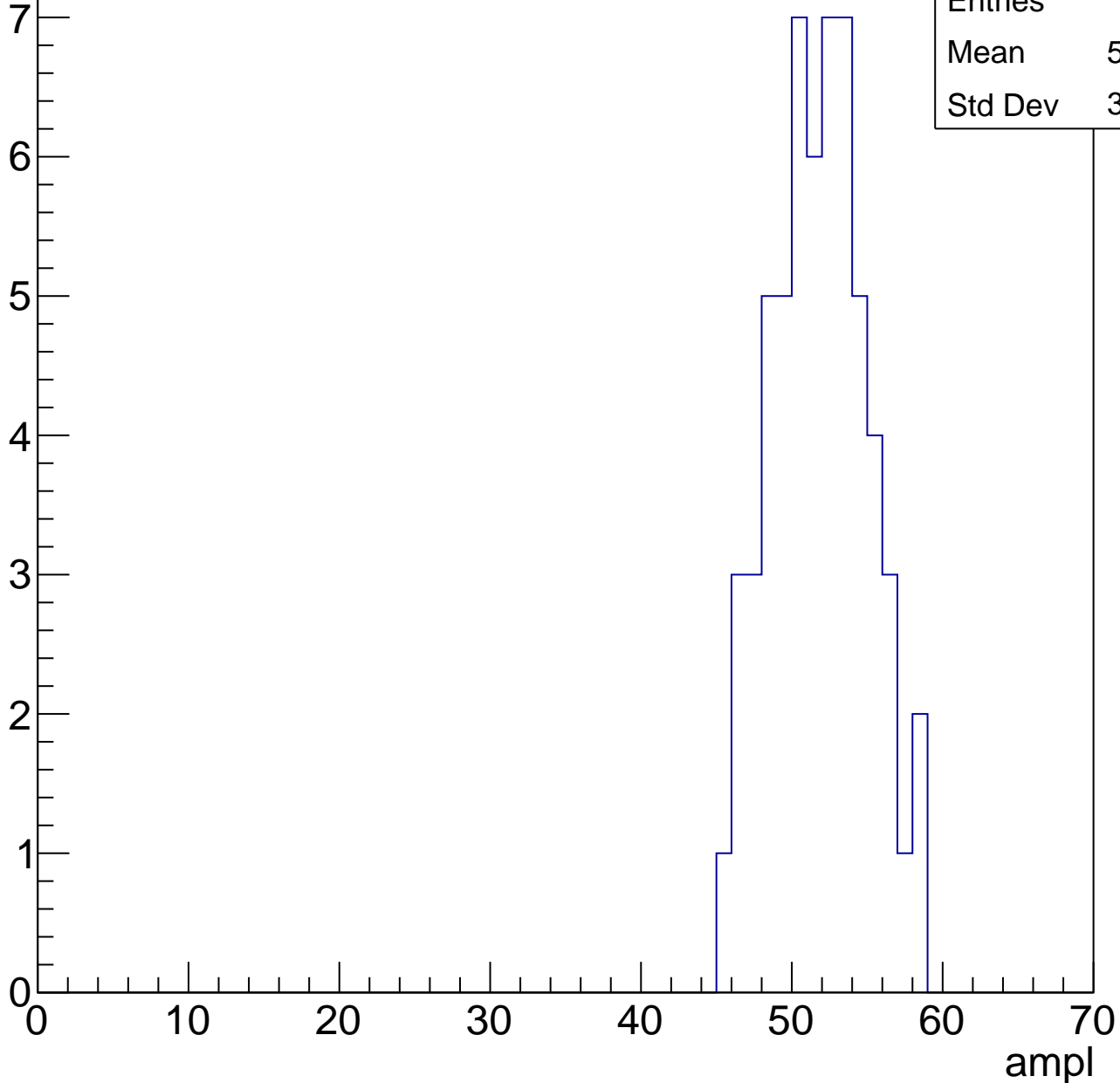


B0L002S, U26-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

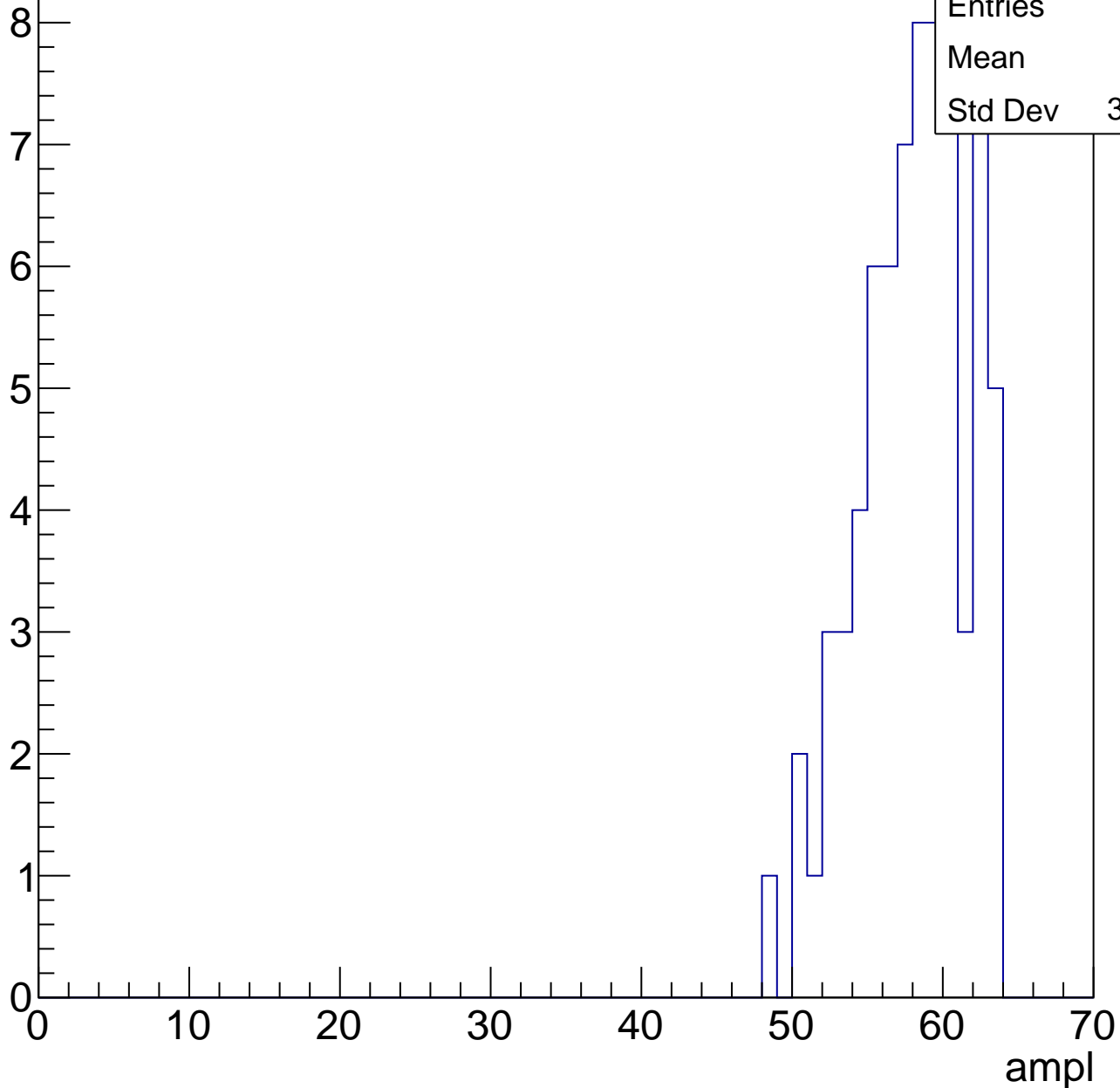
Entries	59
Mean	51.37
Std Dev	3.124



B0L002S, U26-ch63, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

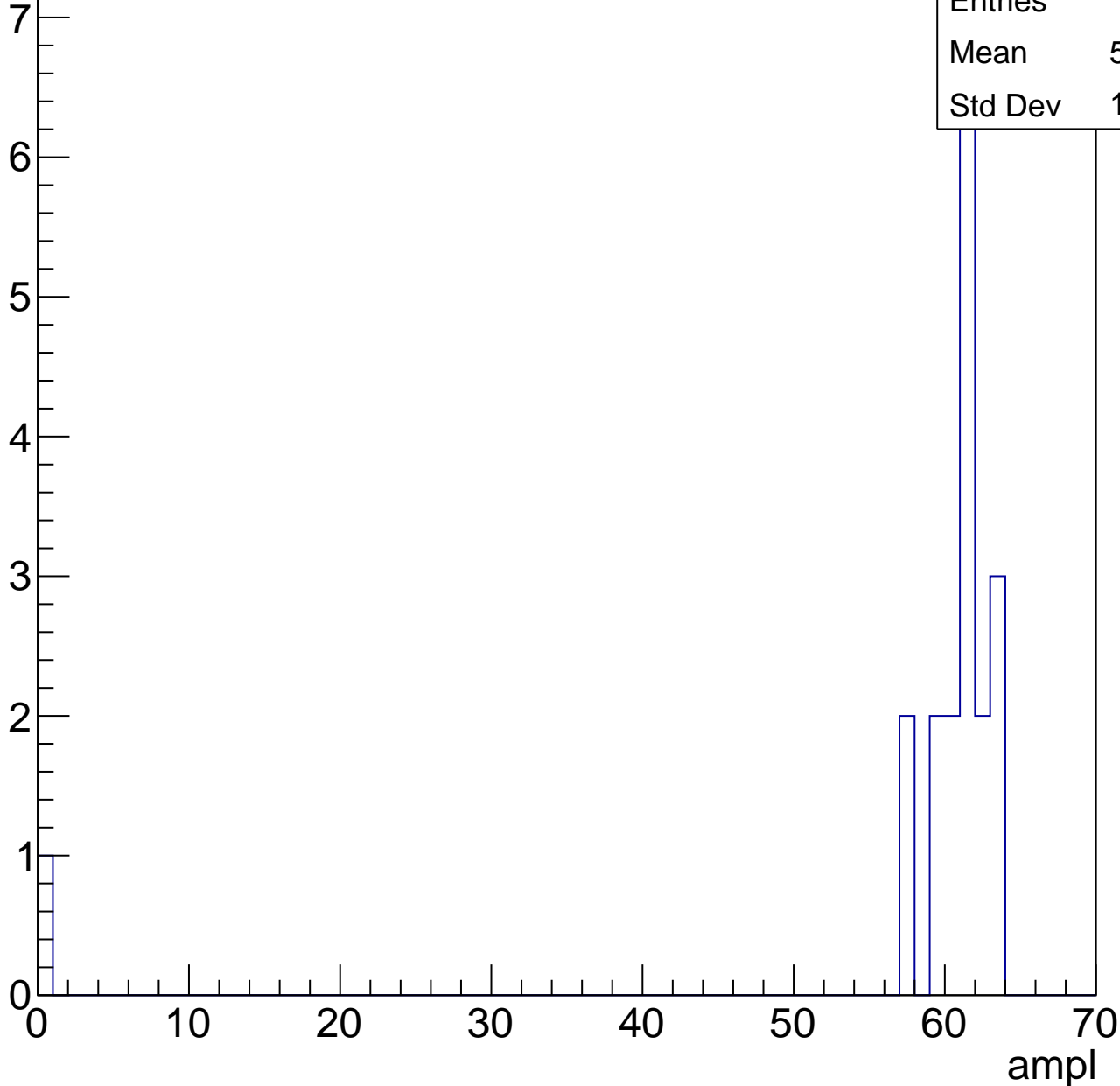


B0L002S, U26-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	57.47
Std Dev	13.65



B0L002S, U26-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

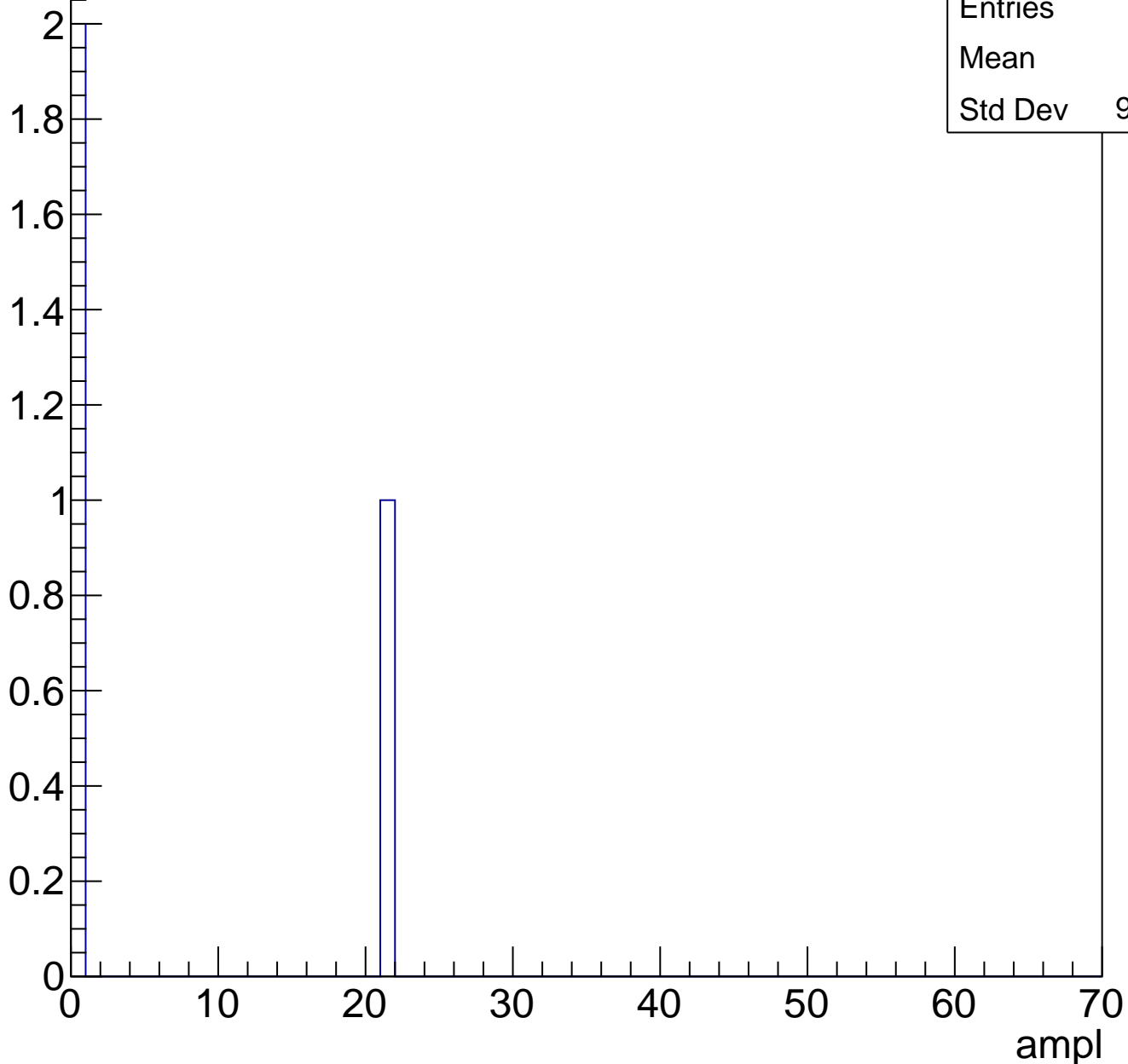
Entry



B0L002S, U26-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L002S, U26-ch64, adc0

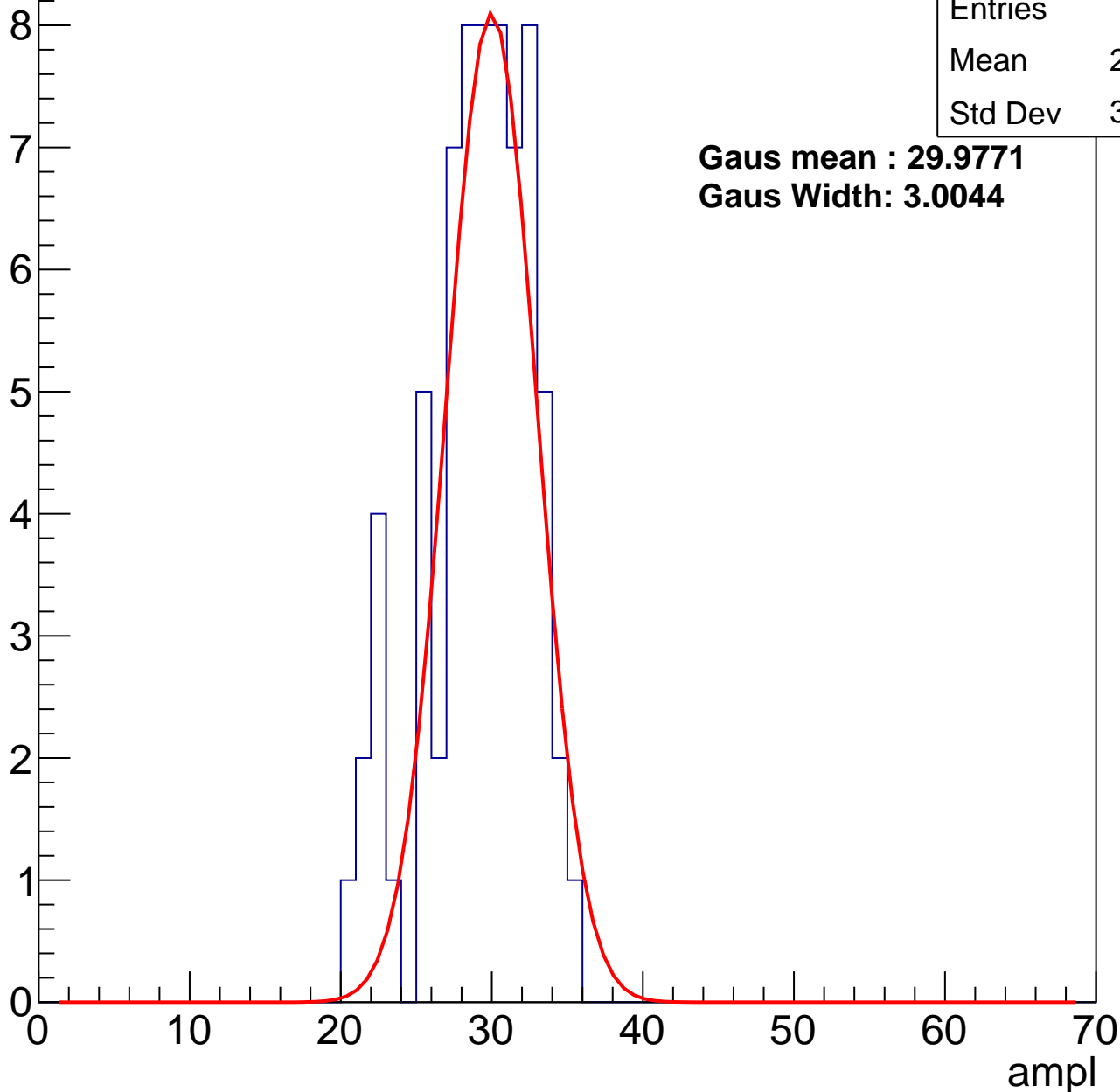
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	28.64
Std Dev	3.489

Gaus mean : 29.9771

Gaus Width: 3.0044



B0L002S, U26-ch64, adc1

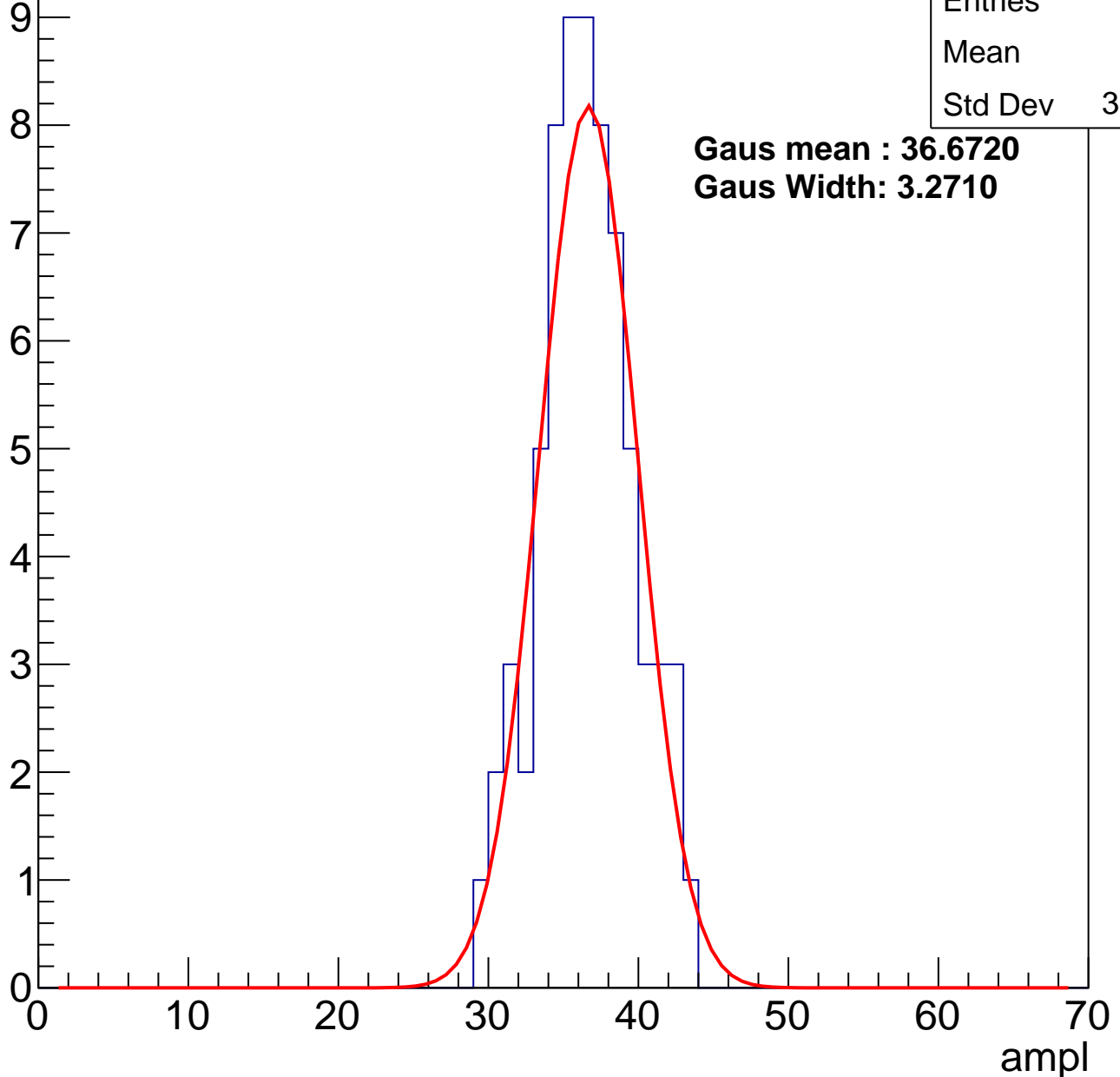
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.1
Std Dev	3.126

Gaus mean : 36.6720

Gaus Width: 3.2710



B0L002S, U26-ch64, adc2

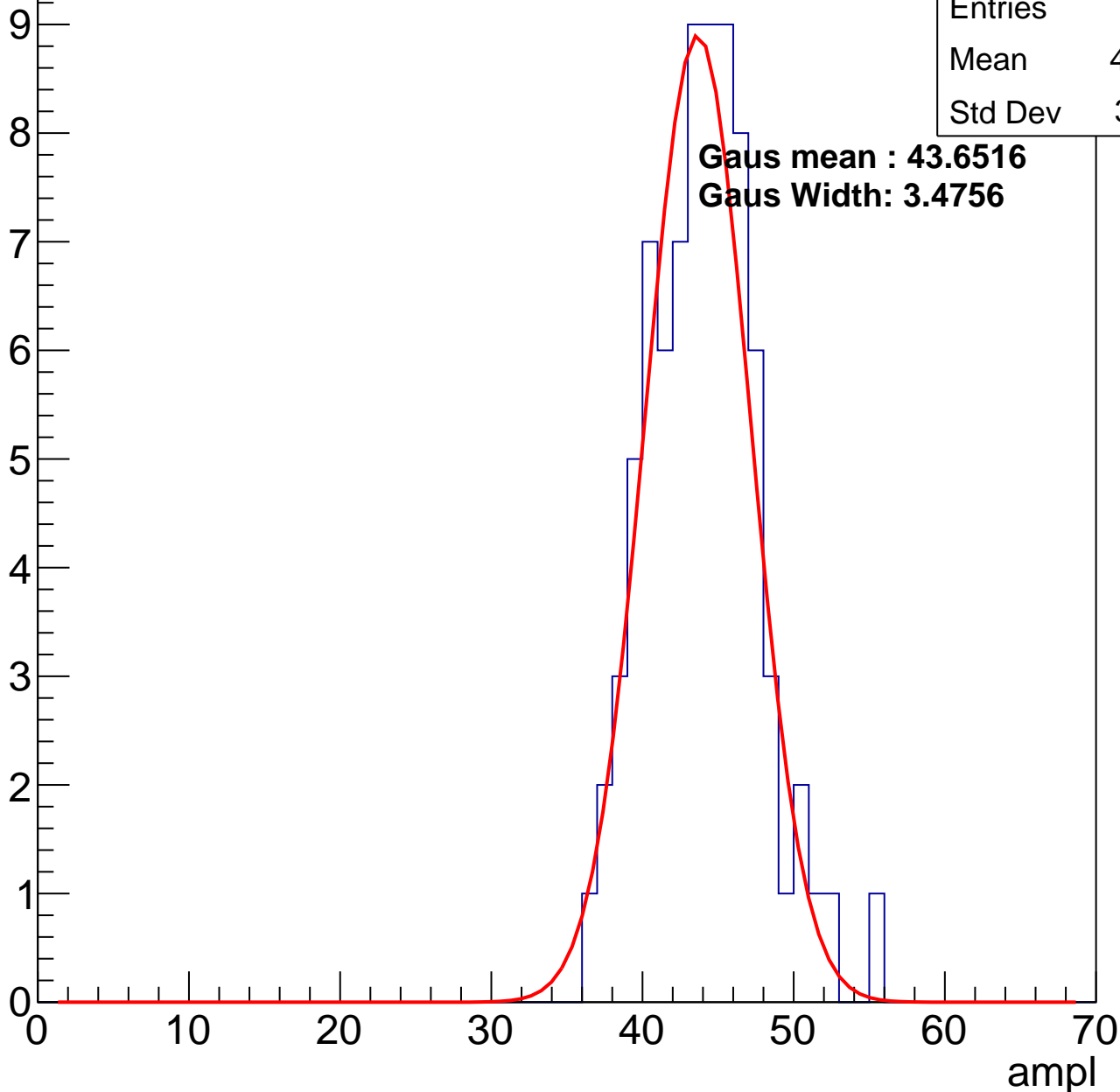
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	43.56
Std Dev	3.611

Gaus mean : 43.6516

Gaus Width: 3.4756

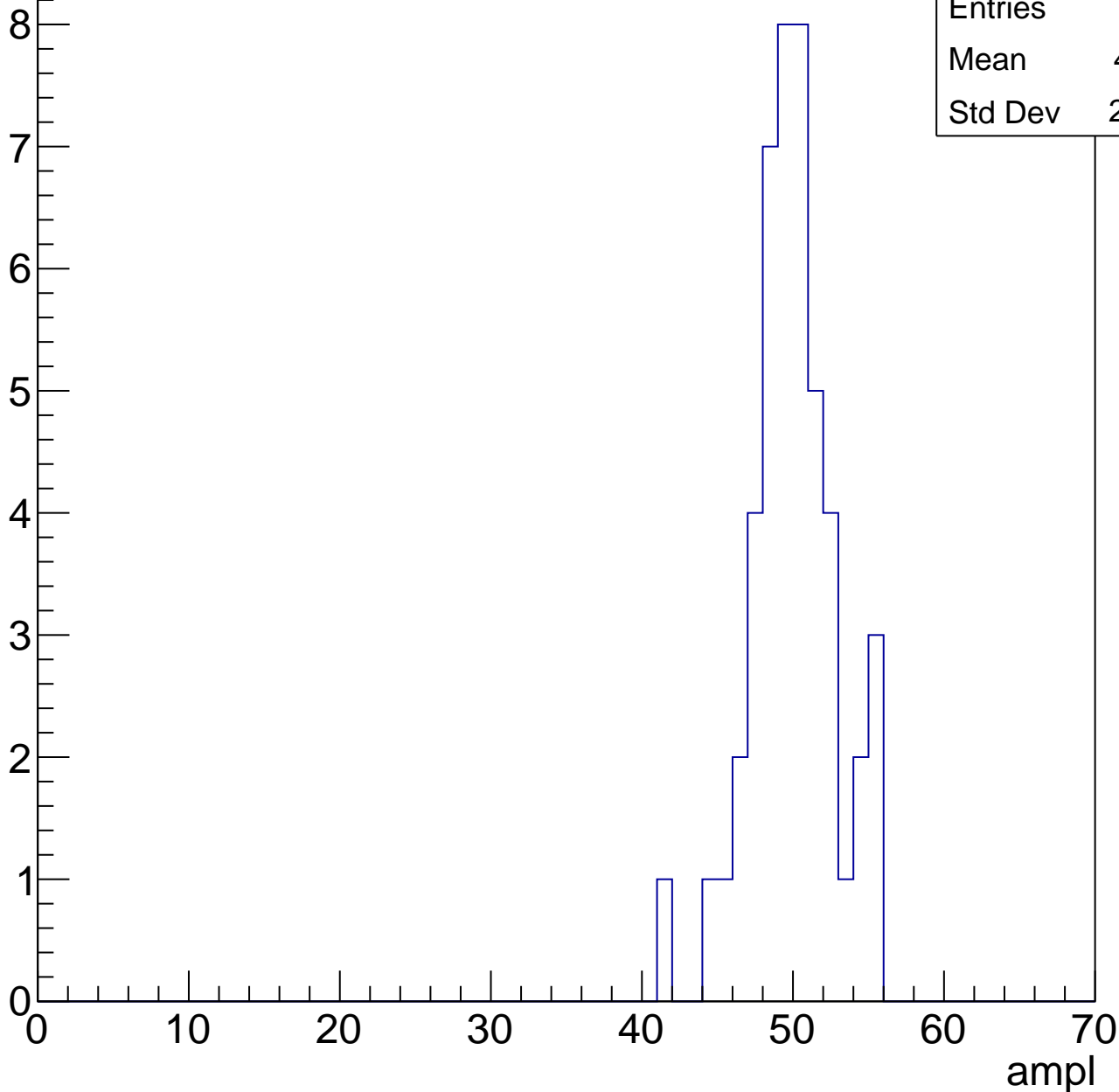


B0L002S, U26-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	49.51
Std Dev	2.812



B0L002S, U26-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

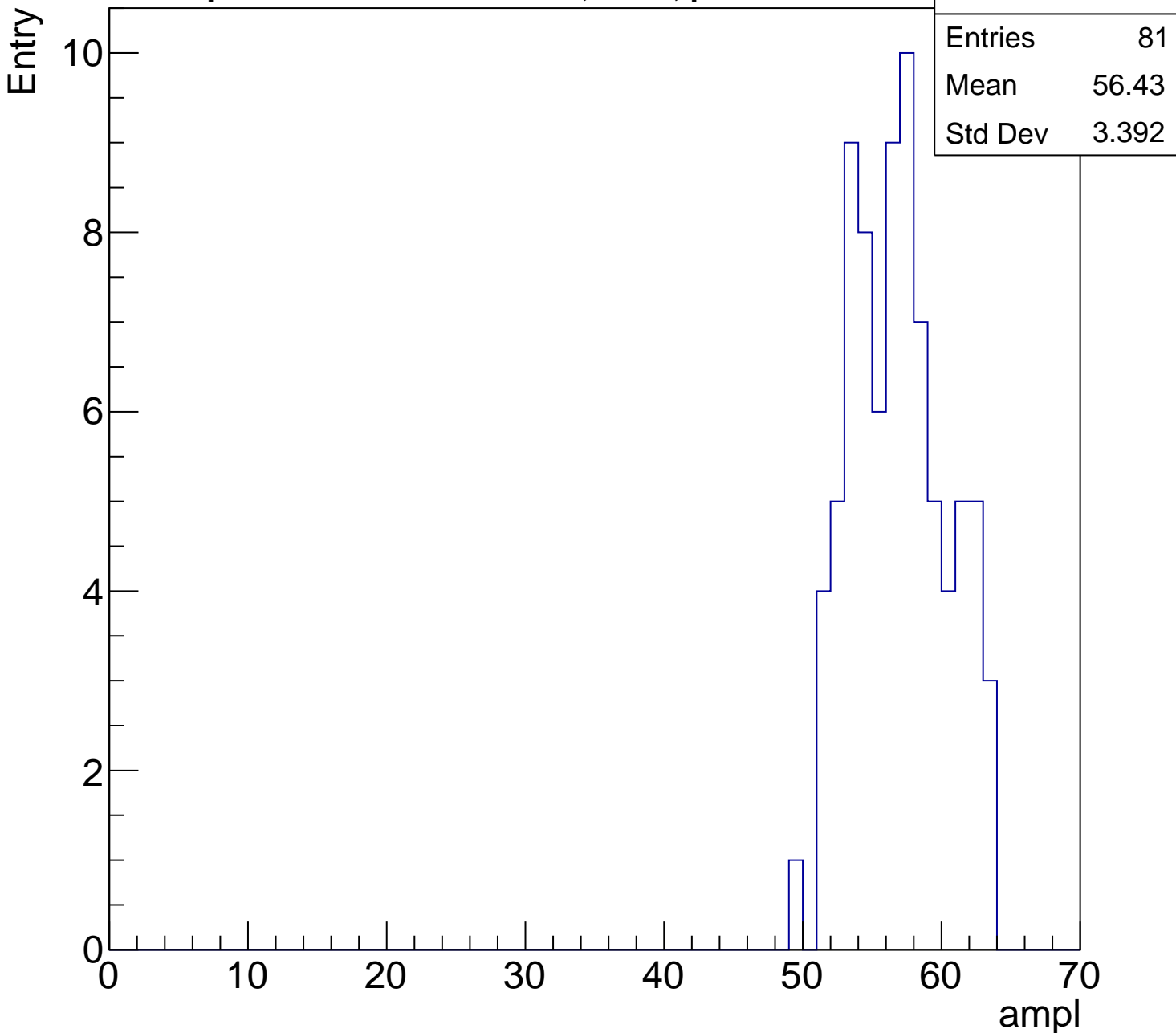
Entries	81
Mean	56.43
Std Dev	3.392

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch64, adc5

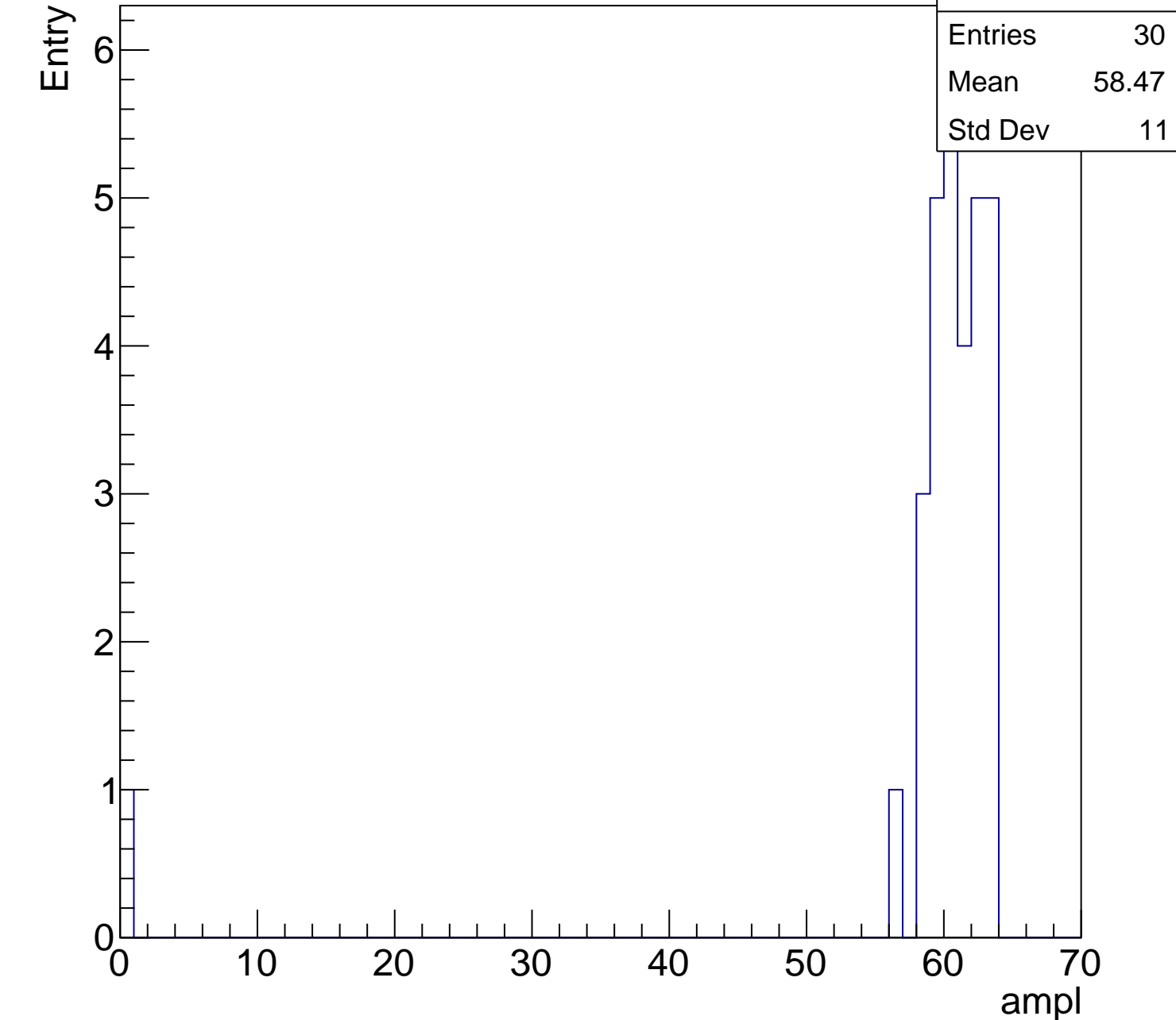
calib_packv5_042523_0143.root, FC#8, port C1

Entry

6
5
4
3
2
1
0

Entries	30
Mean	58.47
Std Dev	11

ampl



B0L002S, U26-ch64, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch65, adc0

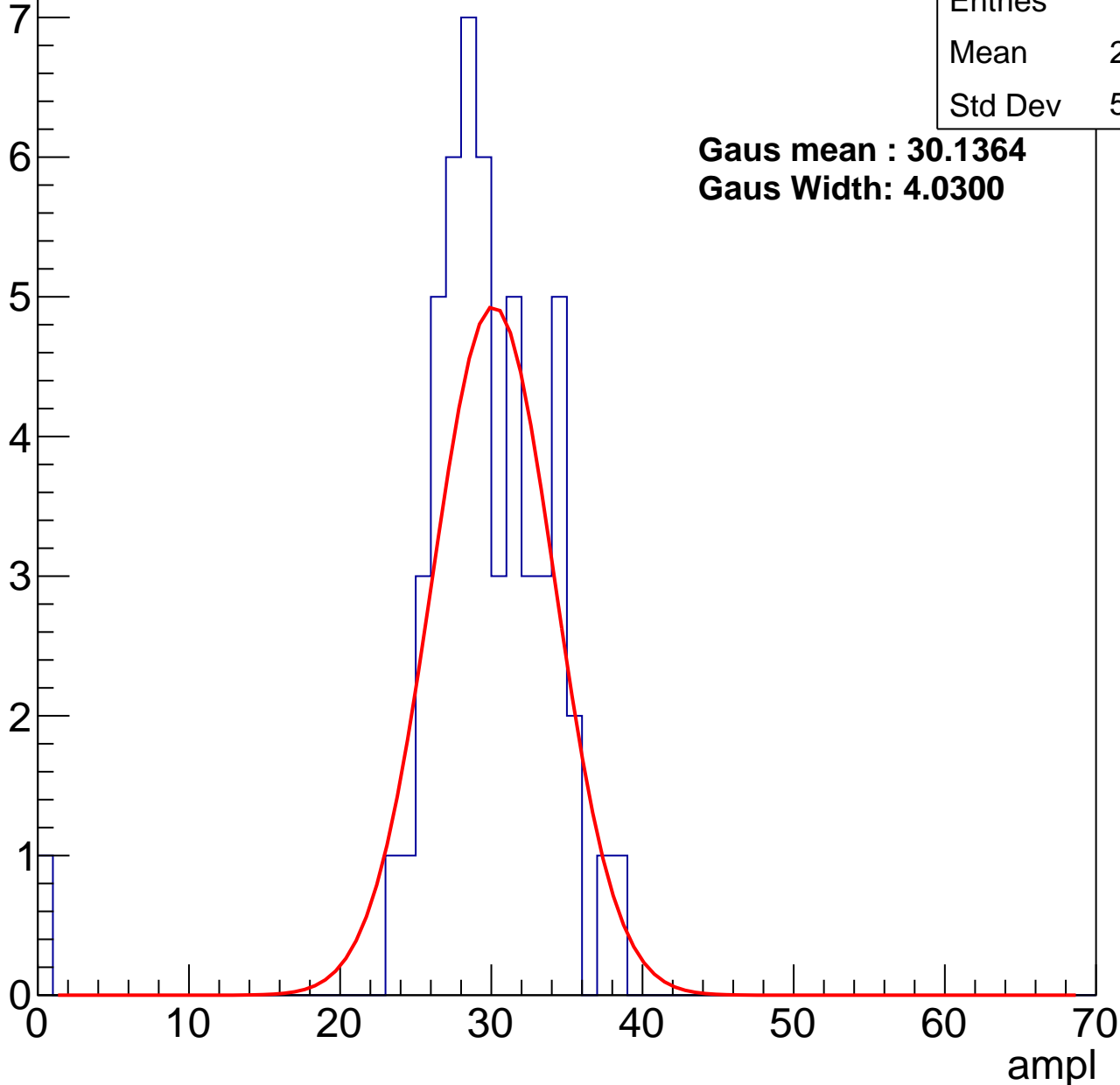
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	29.04
Std Dev	5.259

Gaus mean : 30.1364

Gaus Width: 4.0300



B0L002S, U26-ch65, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	86
Mean	35.78
Std Dev	3.823

Gaus mean : 36.1334

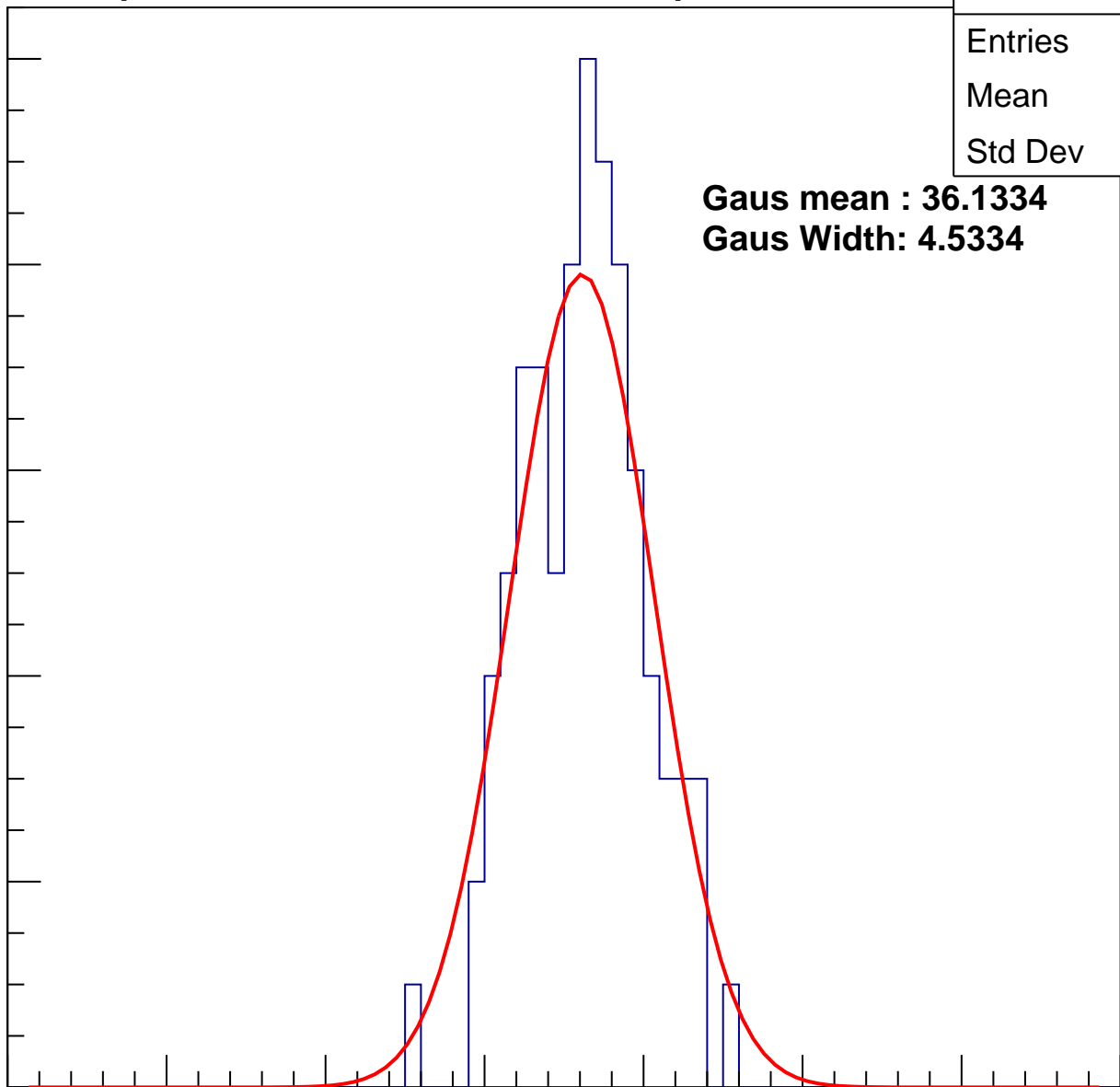
Gaus Width: 4.5334

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch65, adc2

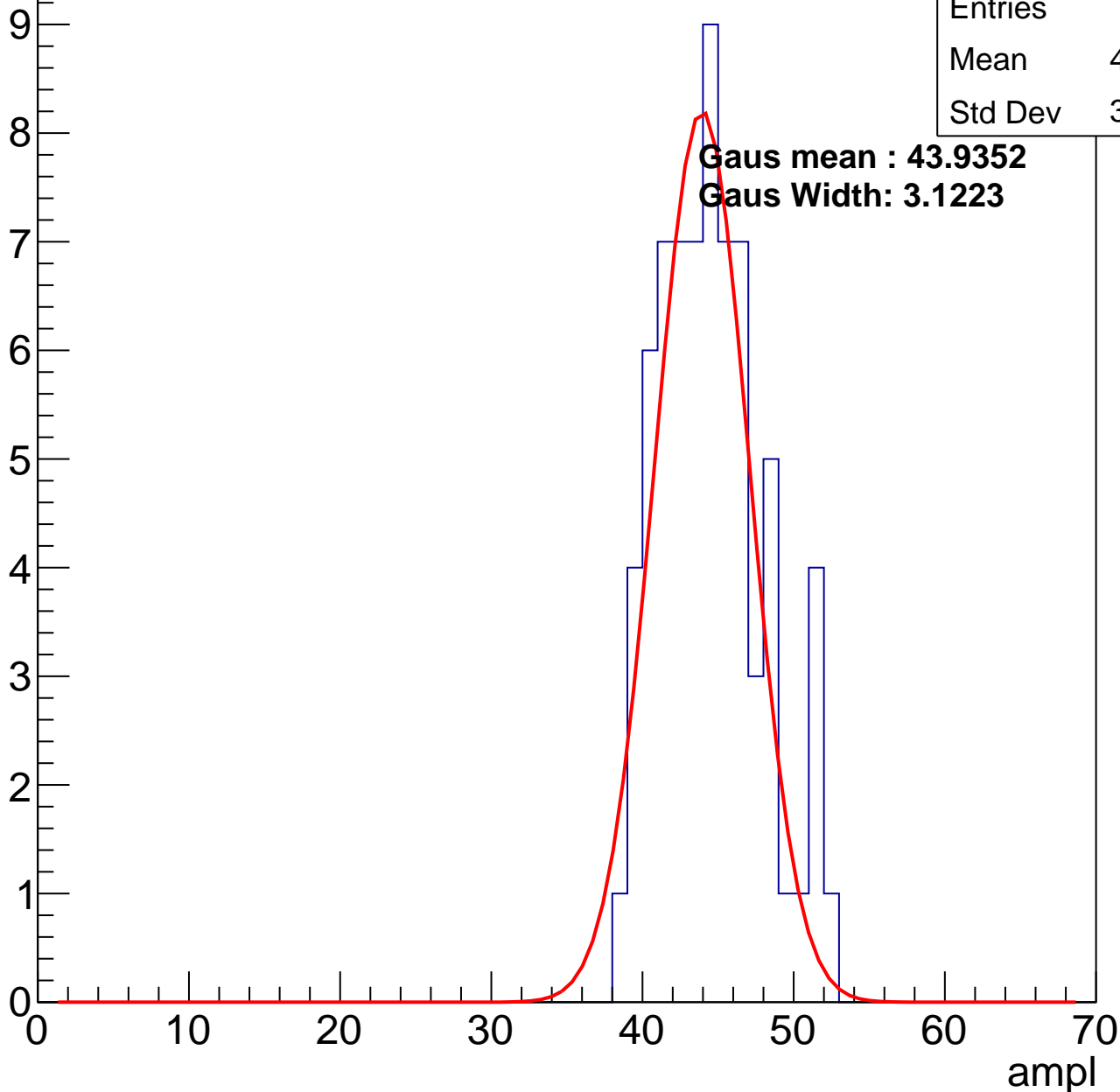
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.07
Std Dev	3.365

Gaus mean : 43.9352

Gaus Width: 3.1223



B0L002S, U26-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

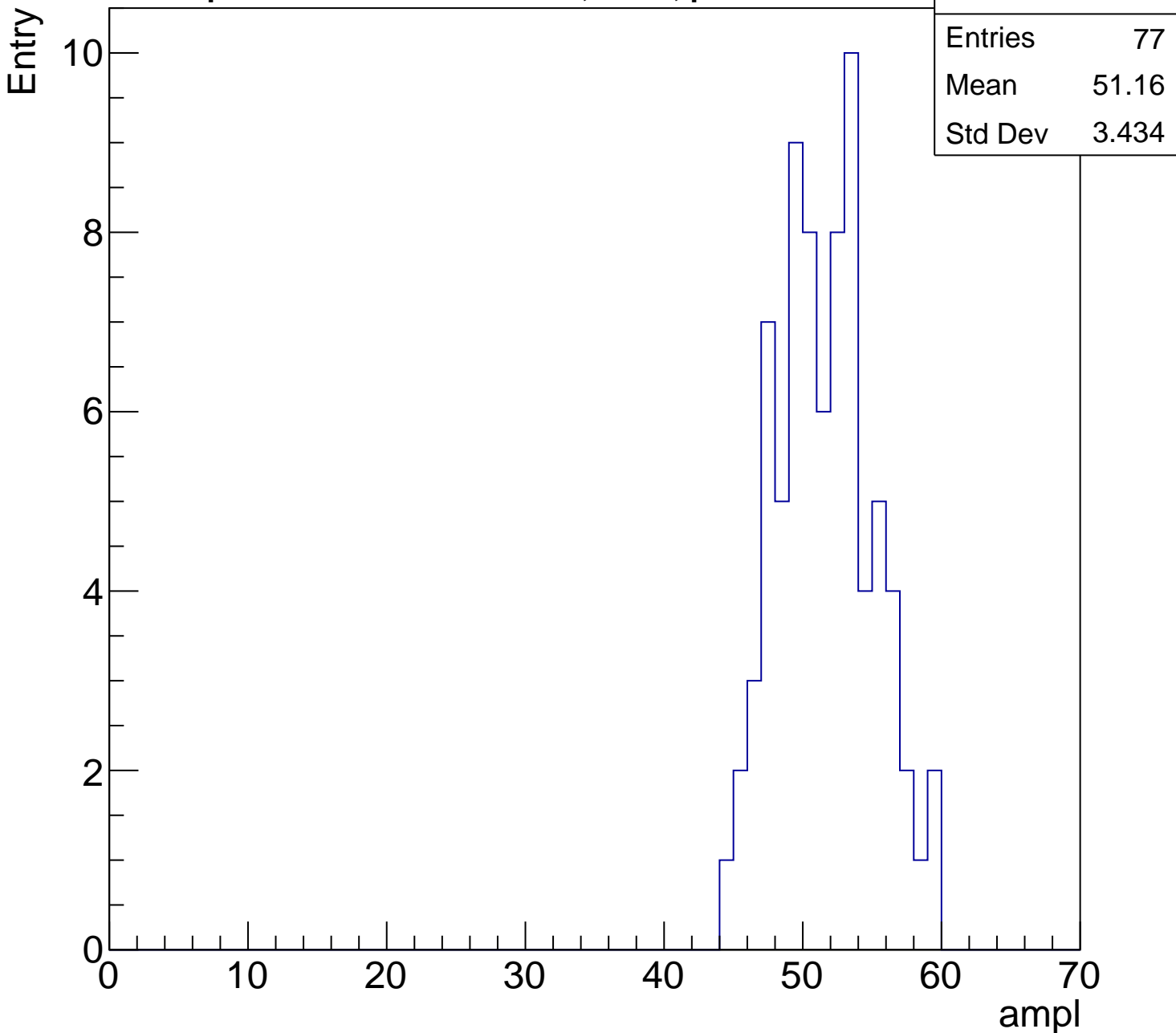
Entries	77
Mean	51.16
Std Dev	3.434

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

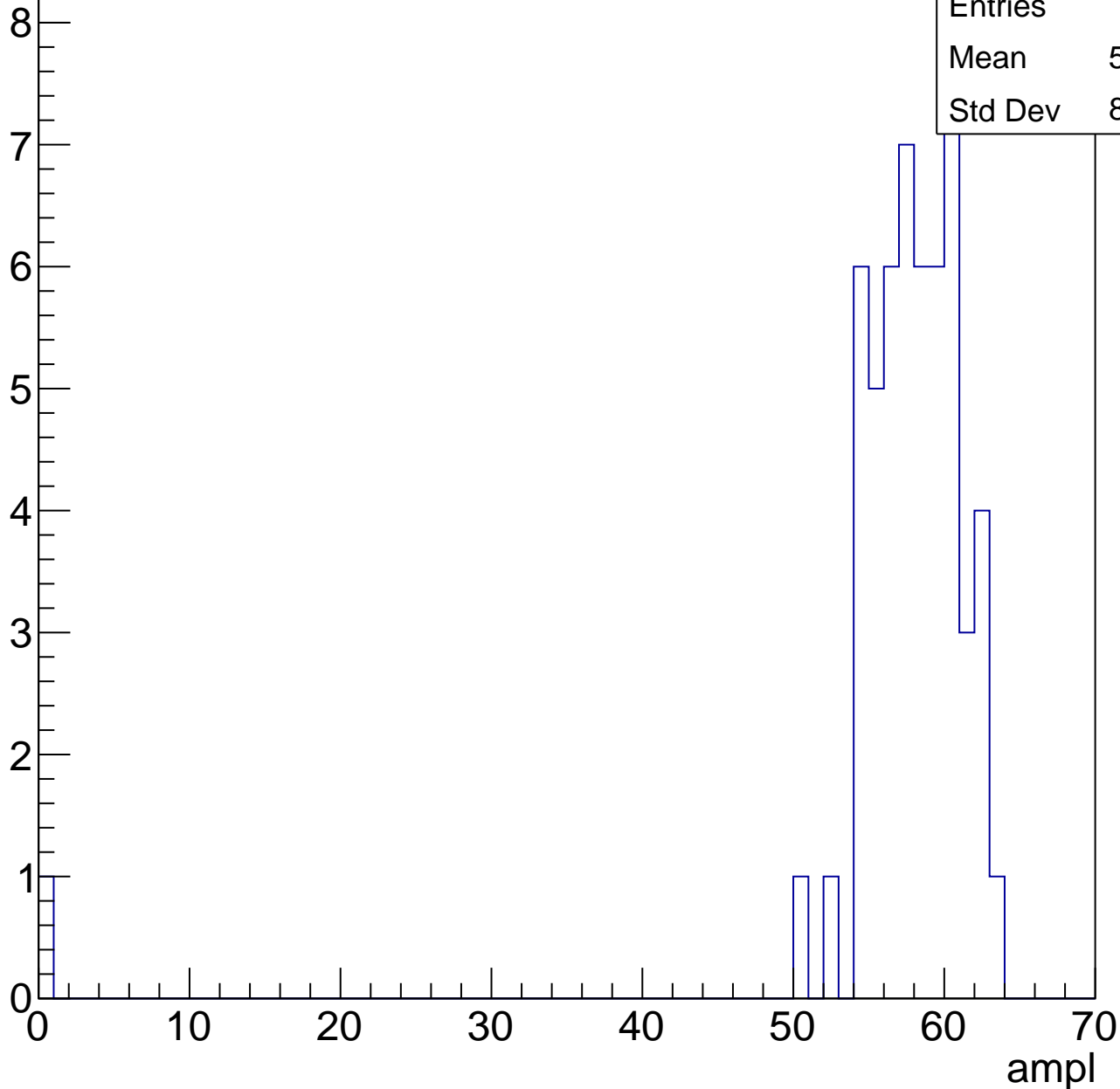


B0L002S, U26-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.58
Std Dev	8.179

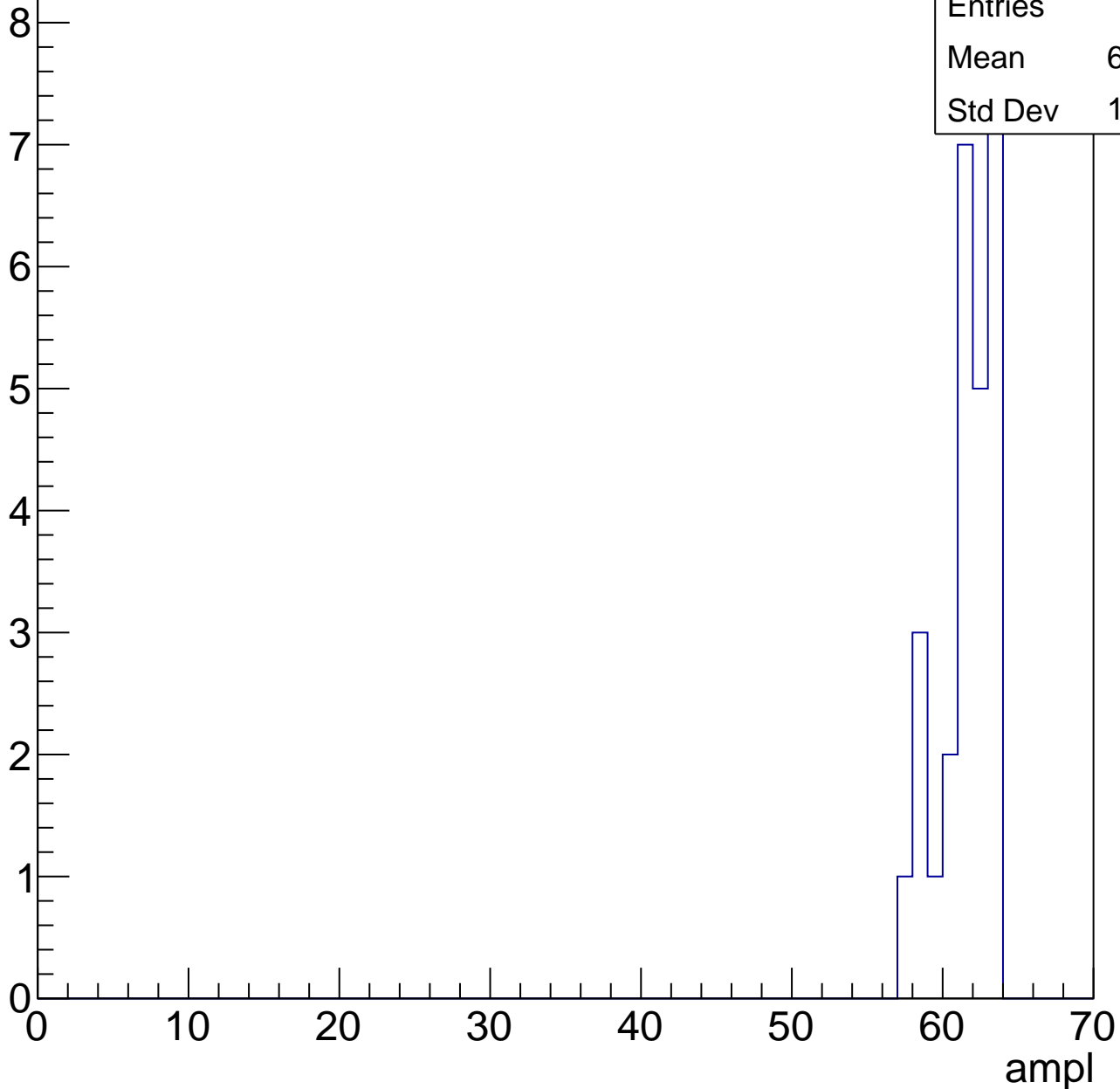


B0L002S, U26-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	61.15
Std Dev	1.779



B0L002S, U26-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U26-ch66, adc0

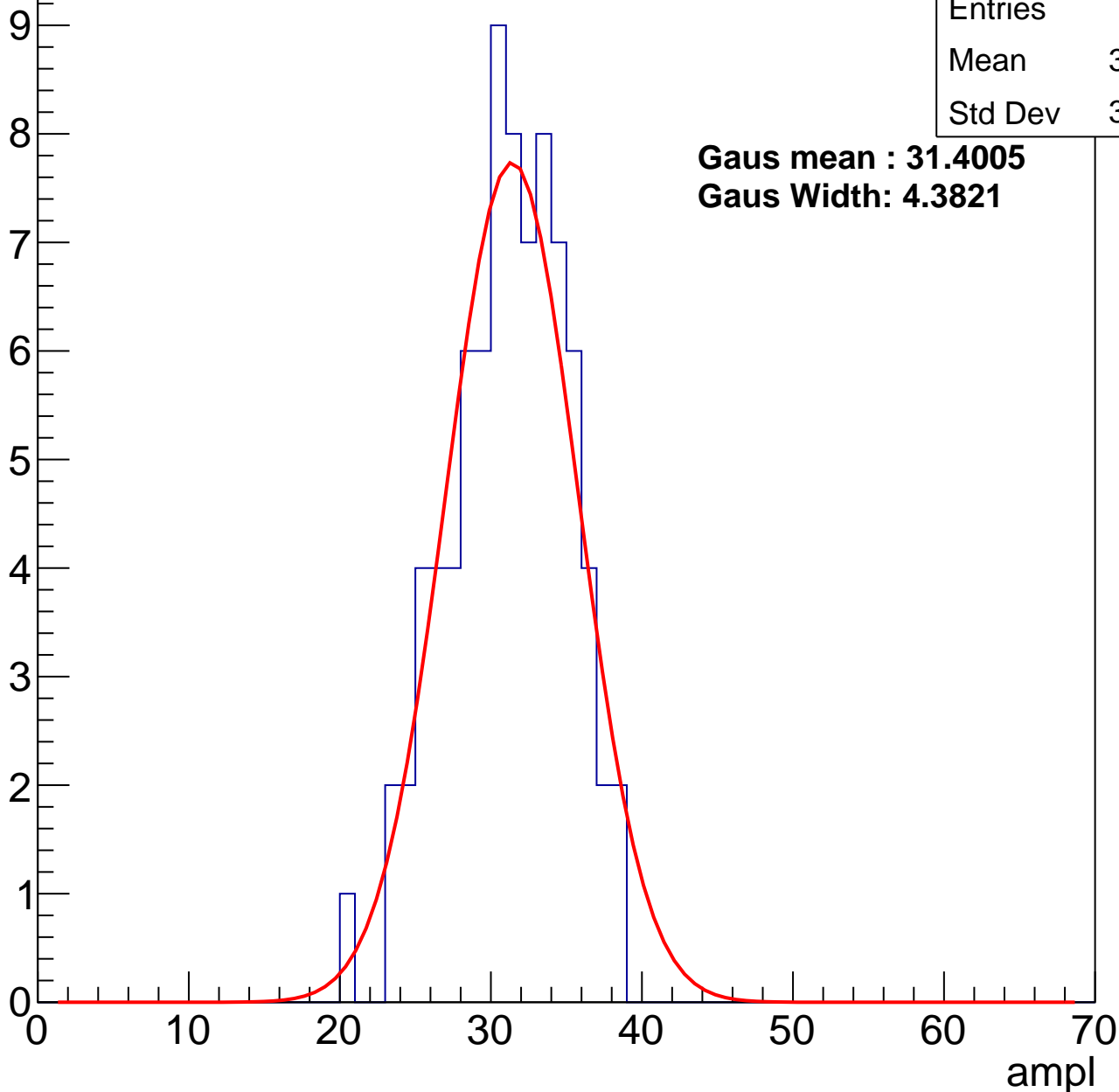
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	30.68
Std Dev	3.828

Gaus mean : 31.4005

Gaus Width: 4.3821



B0L002S, U26-ch66, adc1

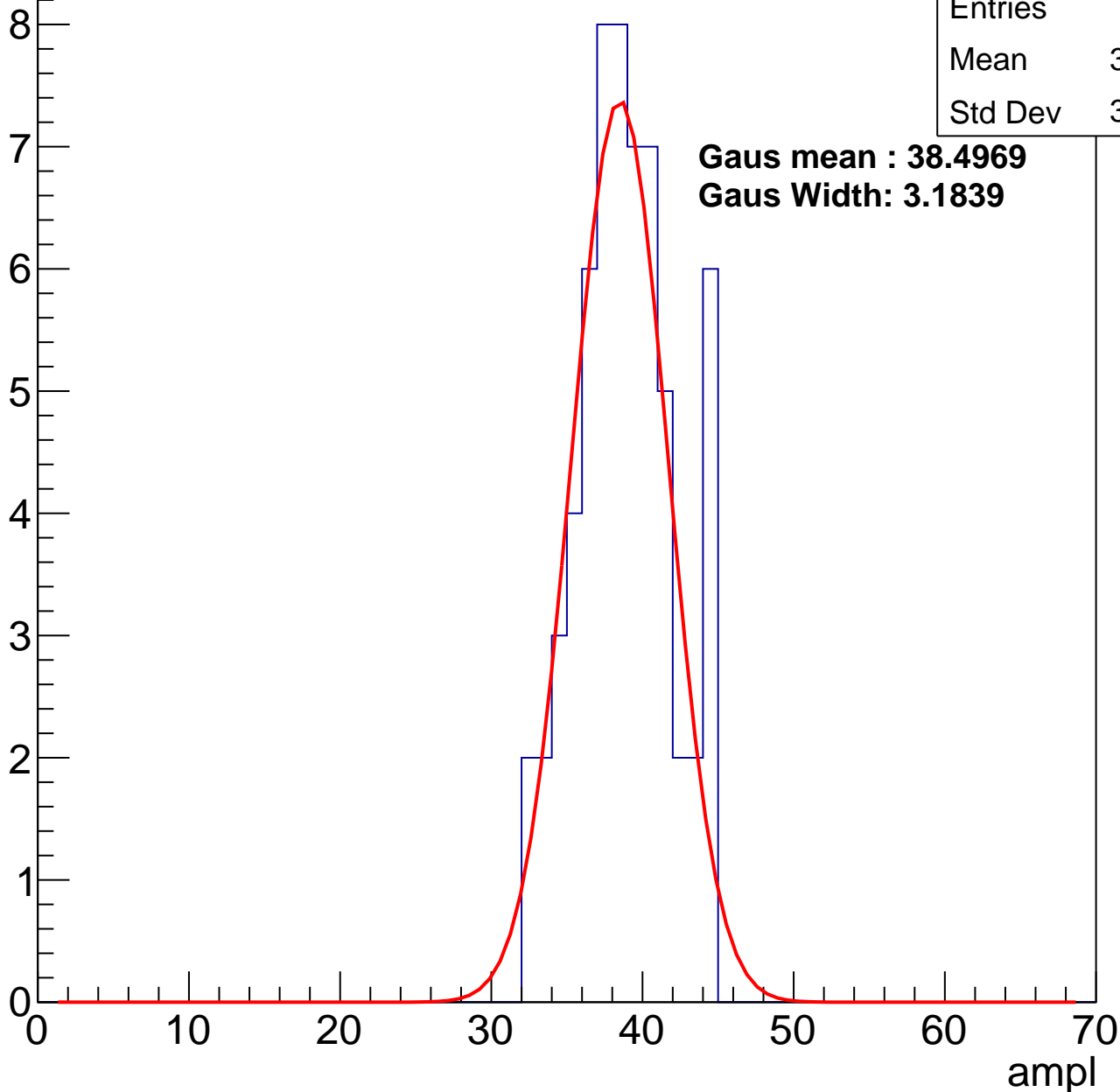
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.39
Std Dev	3.128

Gaus mean : 38.4969

Gaus Width: 3.1839



B0L002S, U26-ch66, adc2

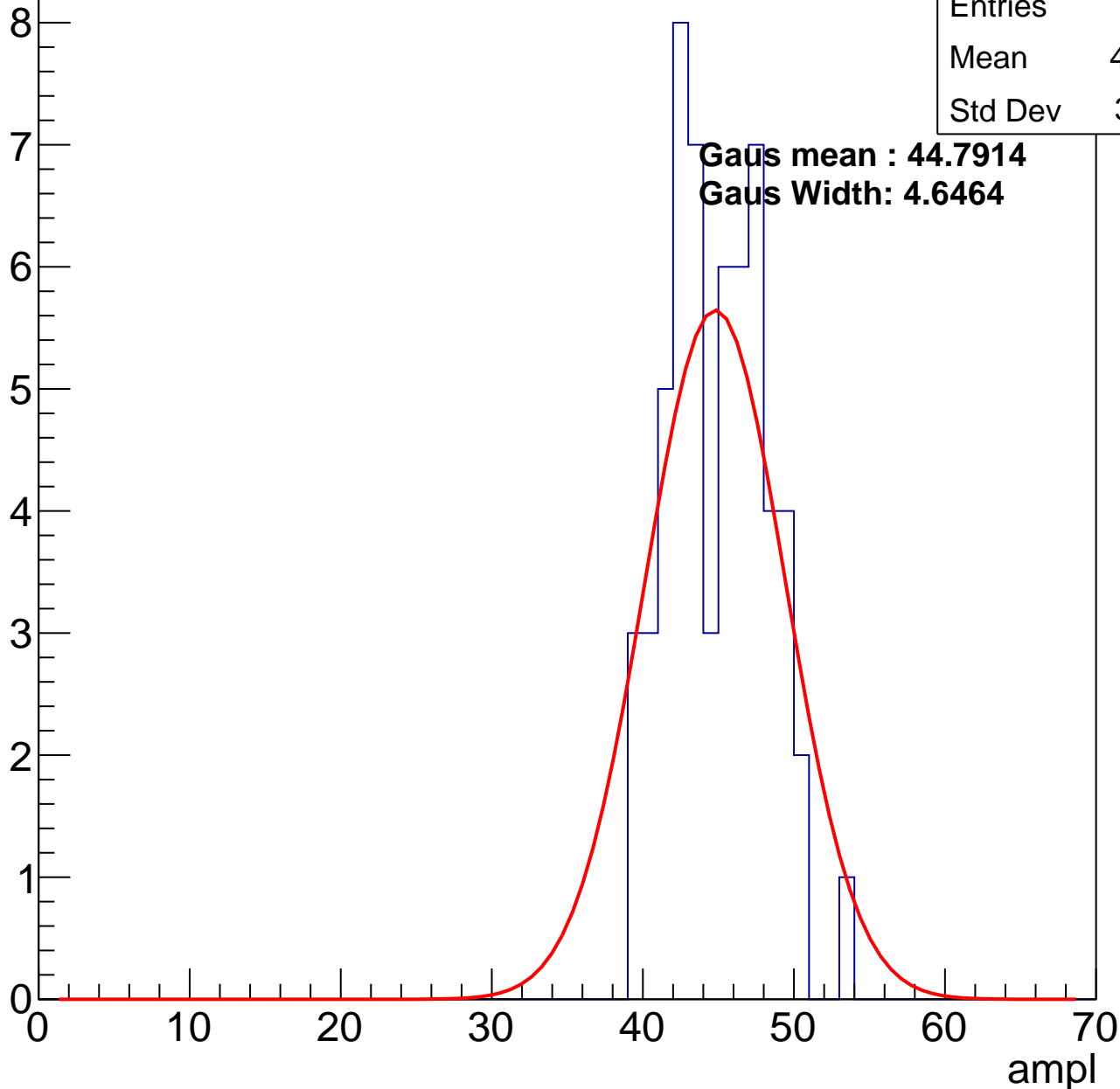
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44.53
Std Dev	3.191

Gaus mean : 44.7914

Gaus Width: 4.6464



B0L002S, U26-ch66, adc3

calib_packv5_042523_0143.root, FC#8, port C1

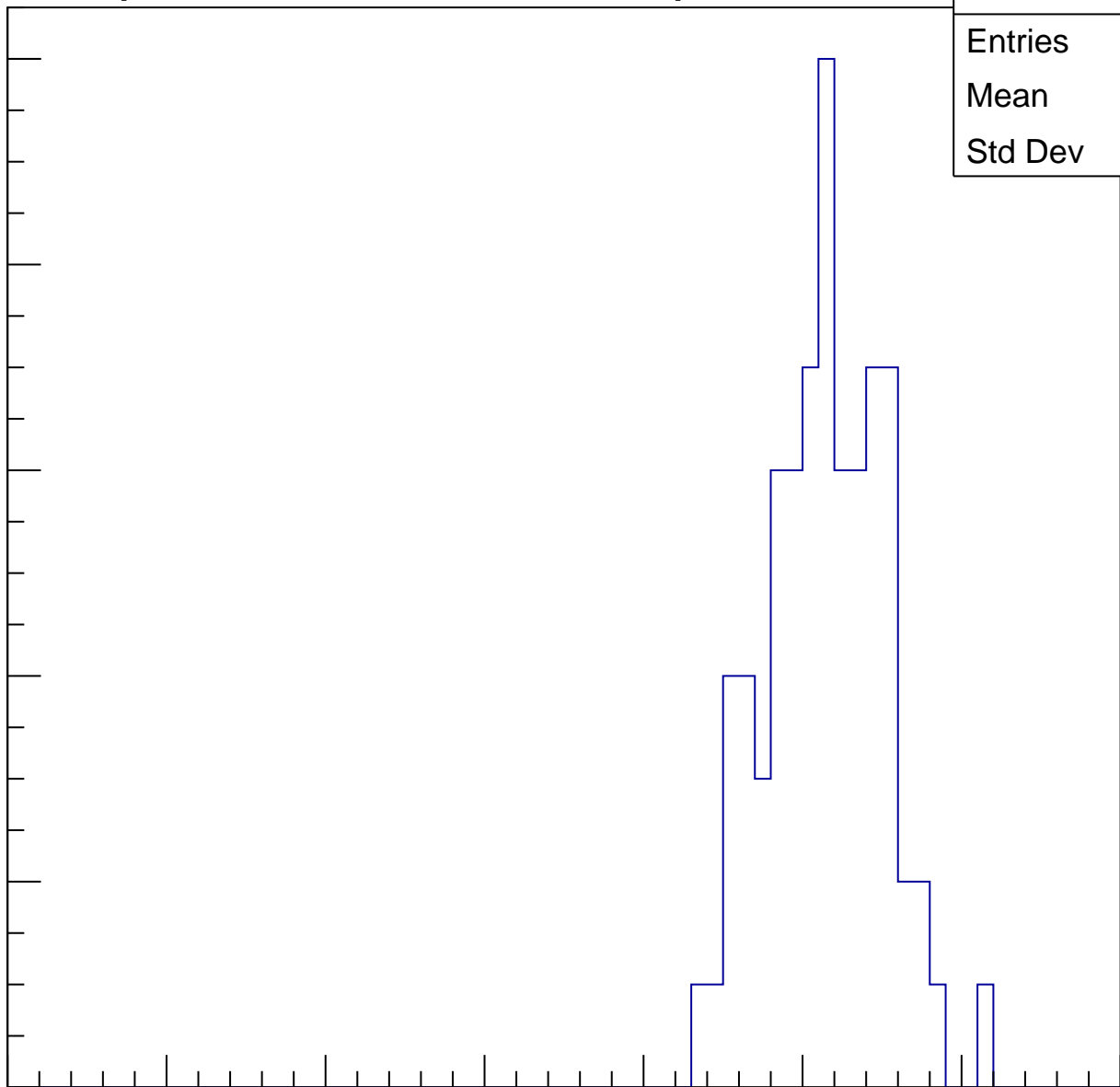
Entries	74
Mean	50.97
Std Dev	3.613

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

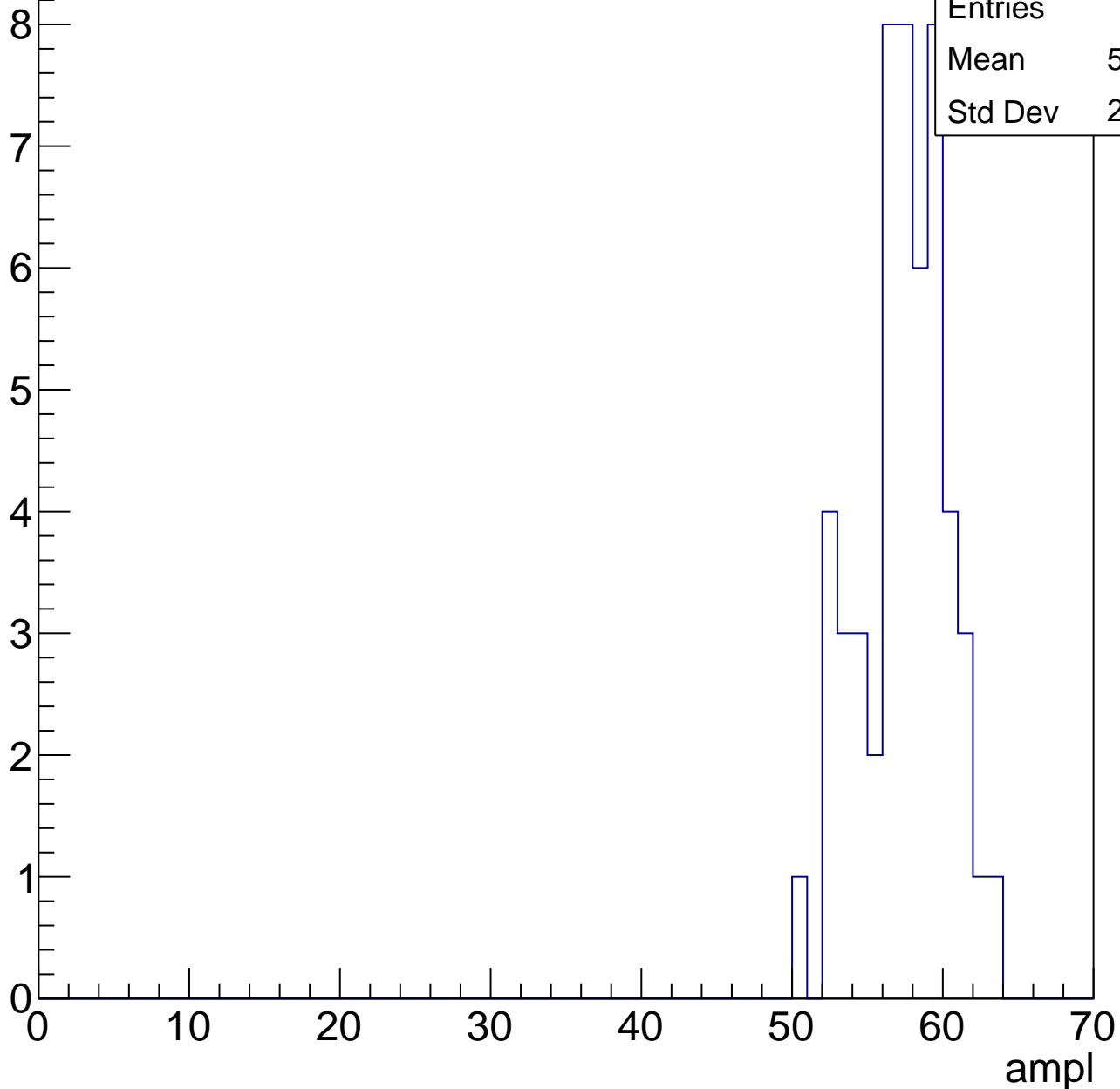


B0L002S, U26-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.94
Std Dev	2.852

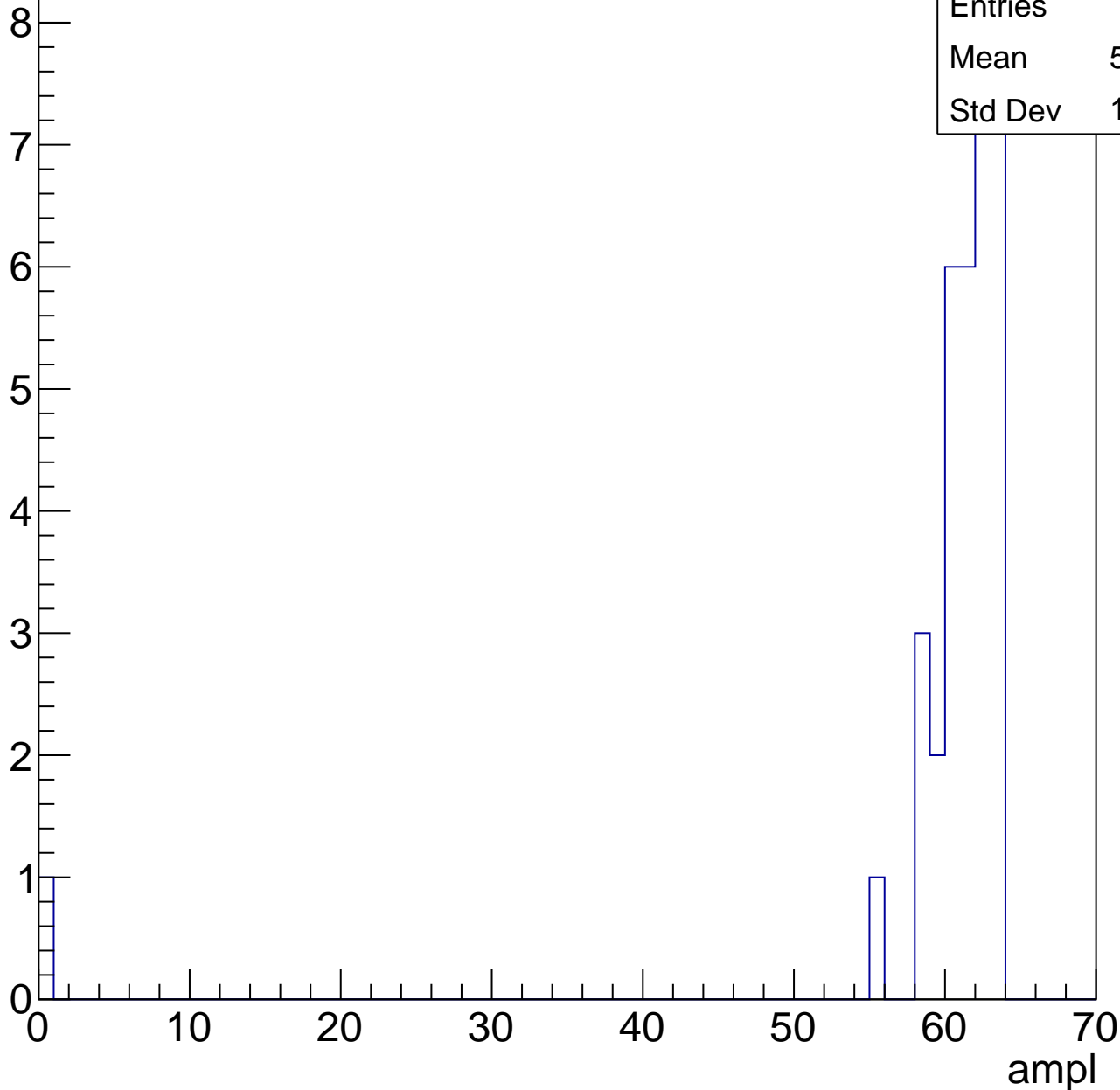


B0L002S, U26-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.23
Std Dev	10.32



B0L002S, U26-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch67, adc0

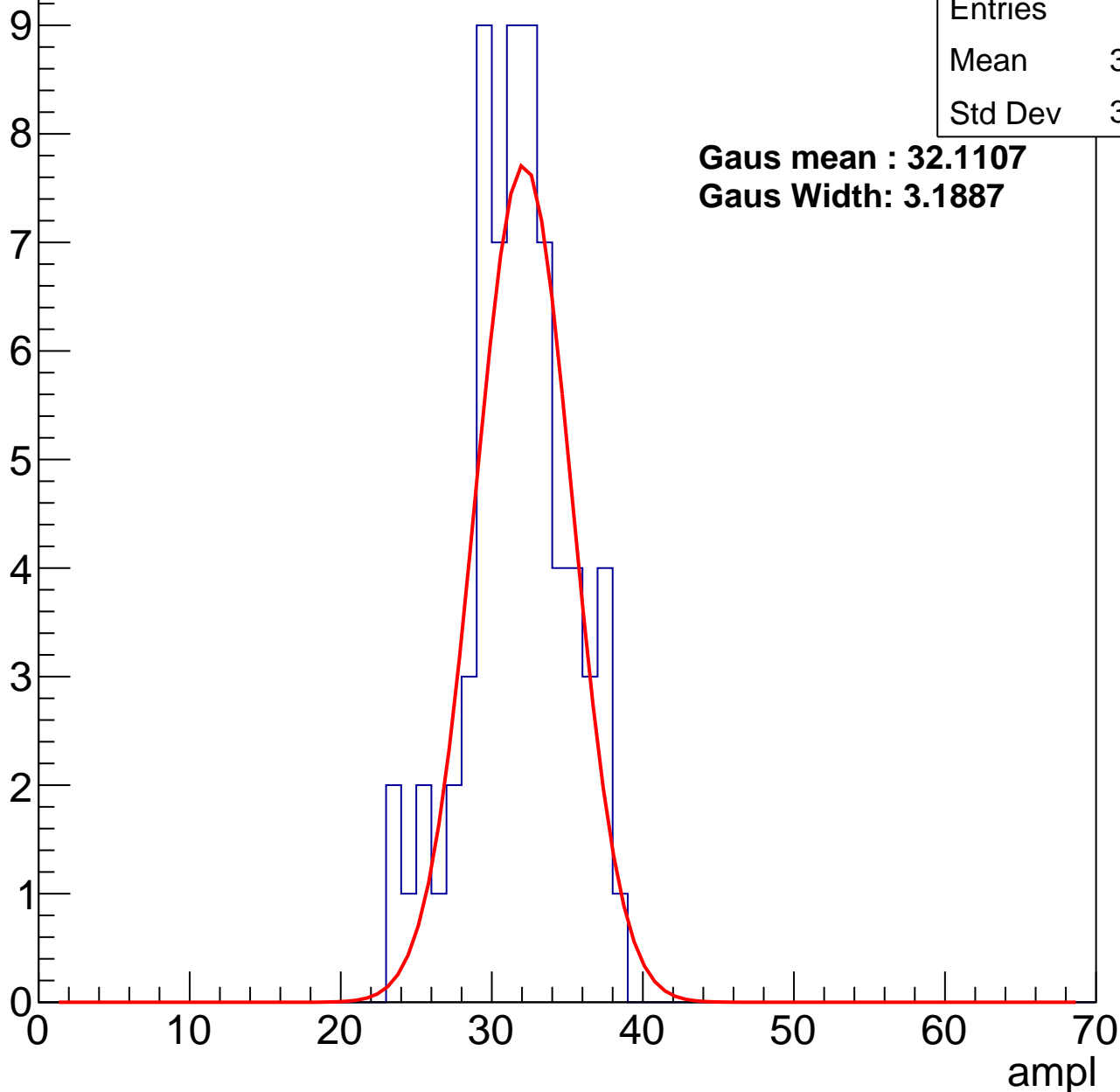
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.22
Std Dev	3.382

Gaus mean : 32.1107

Gaus Width: 3.1887



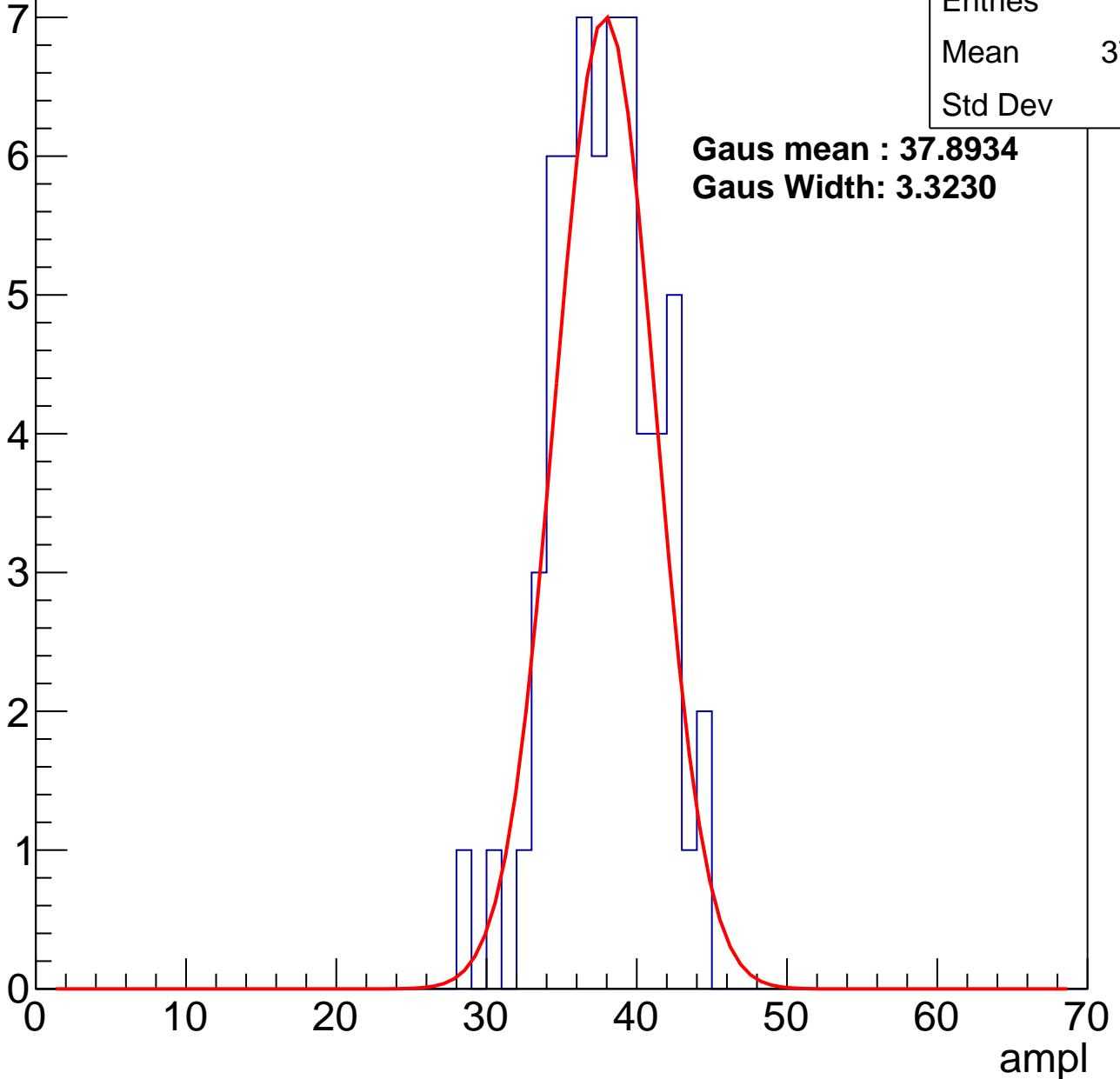
B0L002S, U26-ch67, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.39
Std Dev	3.32

Gaus mean : 37.8934
Gaus Width: 3.3230



B0L002S, U26-ch67, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	86
Mean	45.12
Std Dev	3.765

Gaus mean : 45.0762

Gaus Width: 4.0452

Entry

10

8

6

4

2

0

0

10

20

30

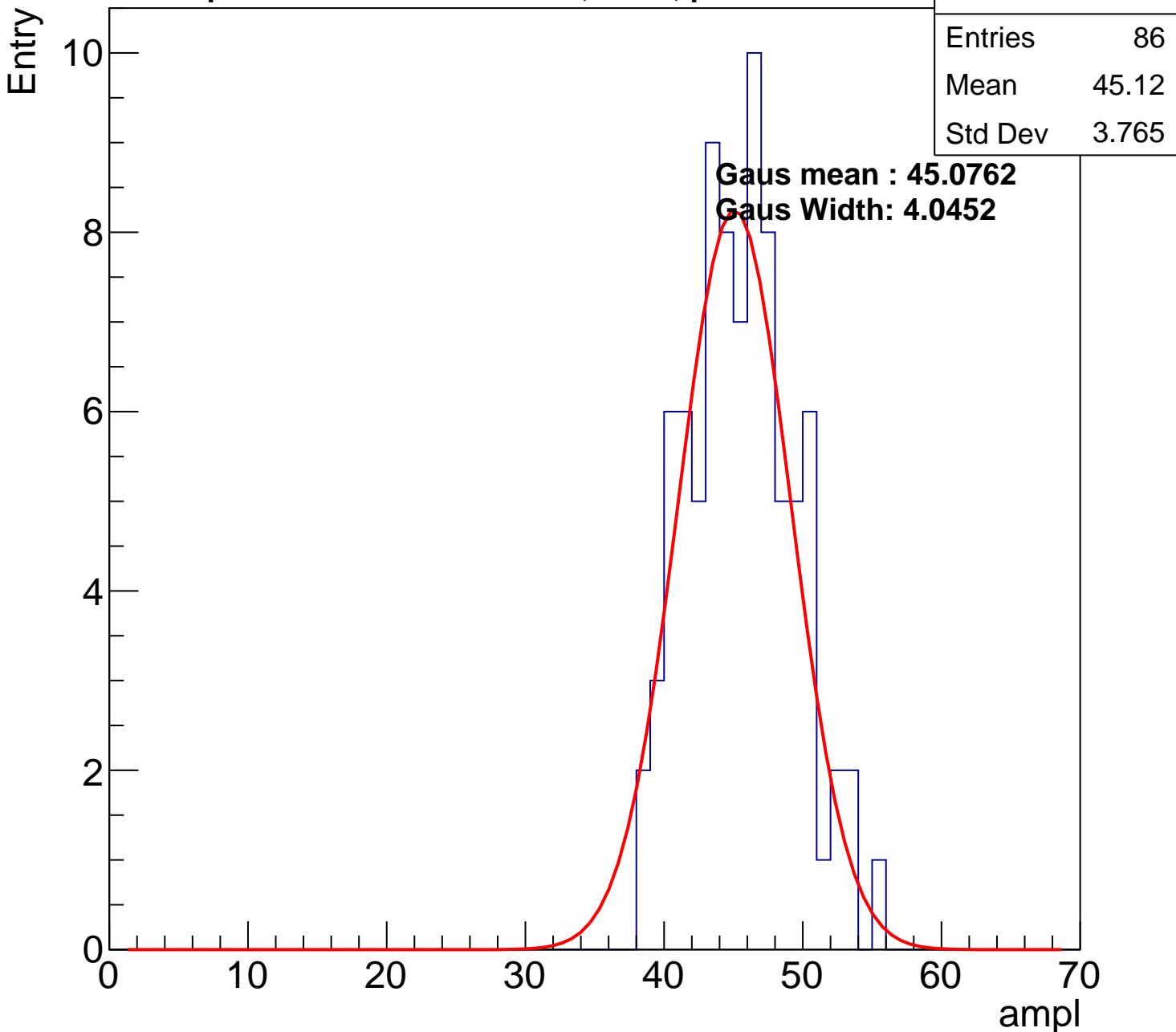
40

50

60

70

ampl

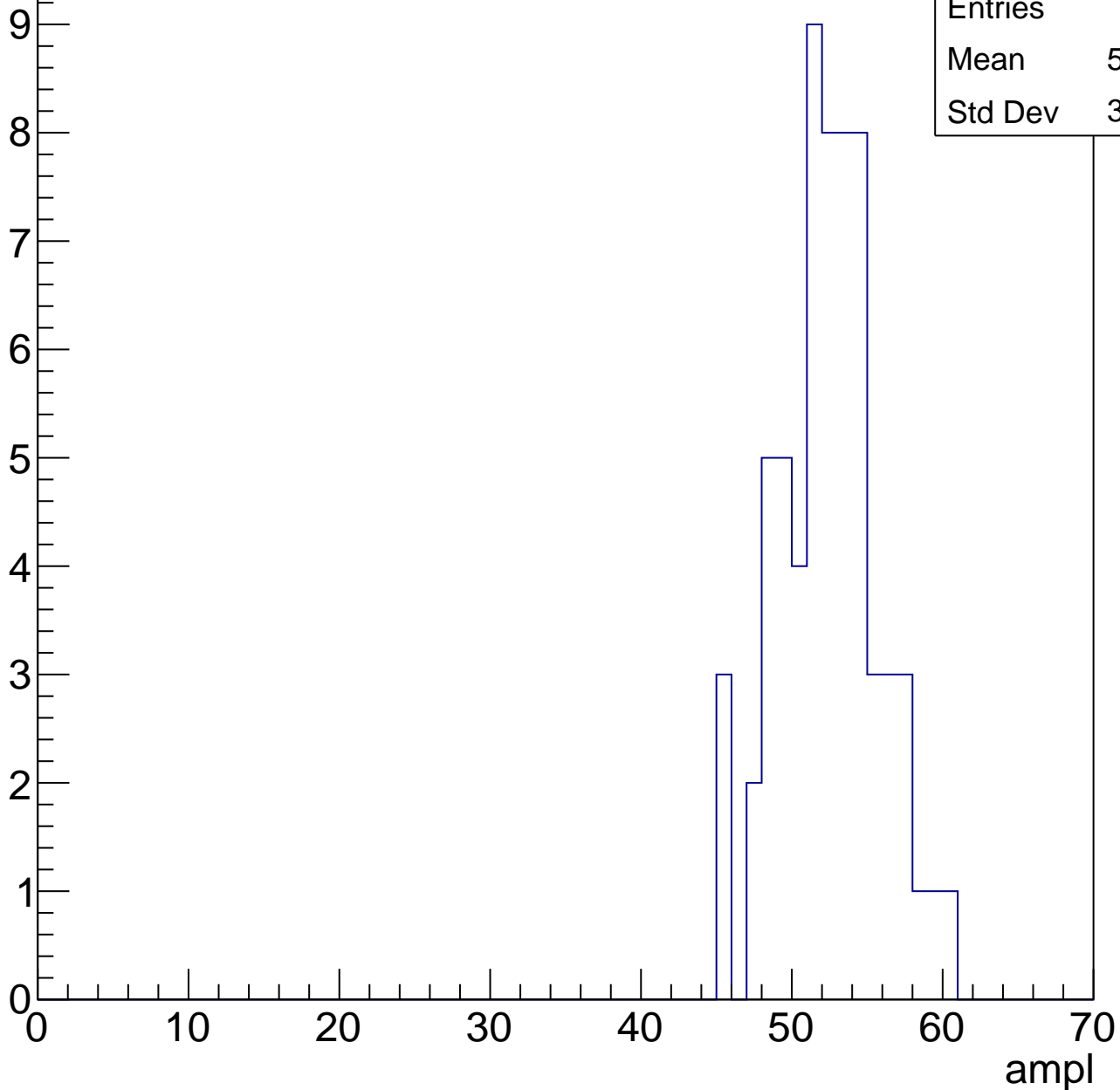


B0L002S, U26-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

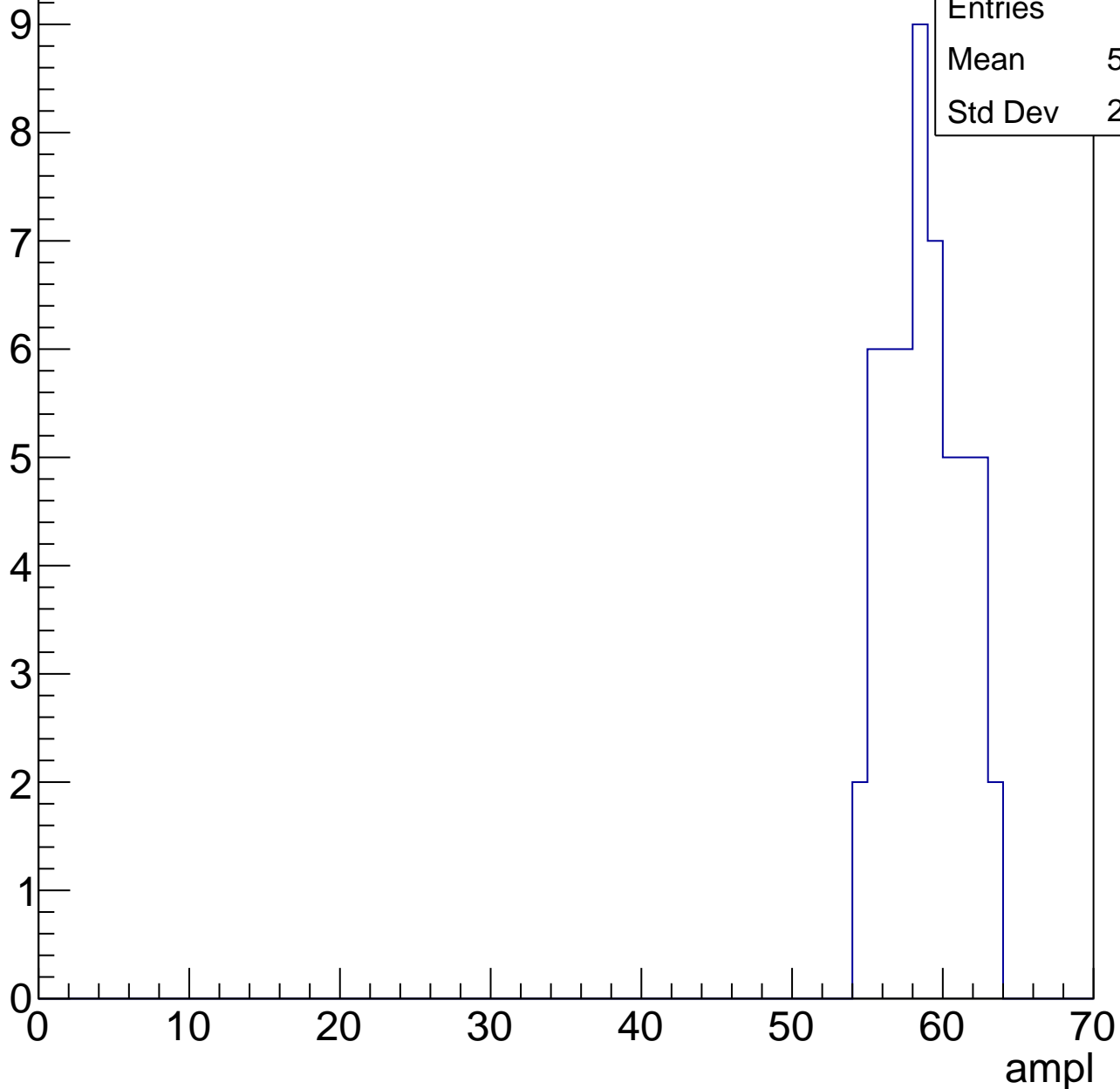
Entries	64
Mean	51.97
Std Dev	3.274



B0L002S, U26-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



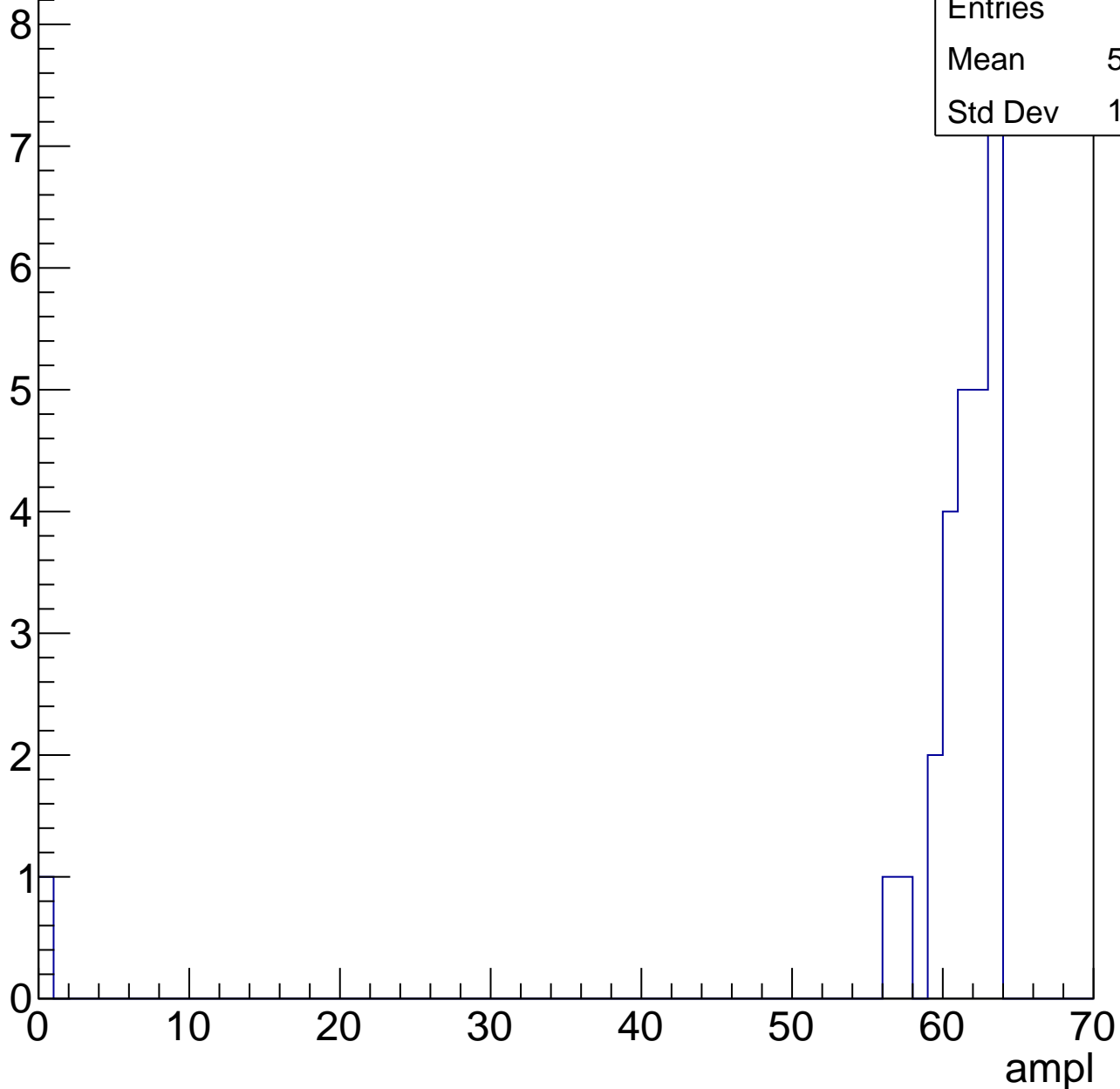
Entries	53
Mean	58.34
Std Dev	2.426

B0L002S, U26-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.89
Std Dev	11.69



B0L002S, U26-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch68, adc0

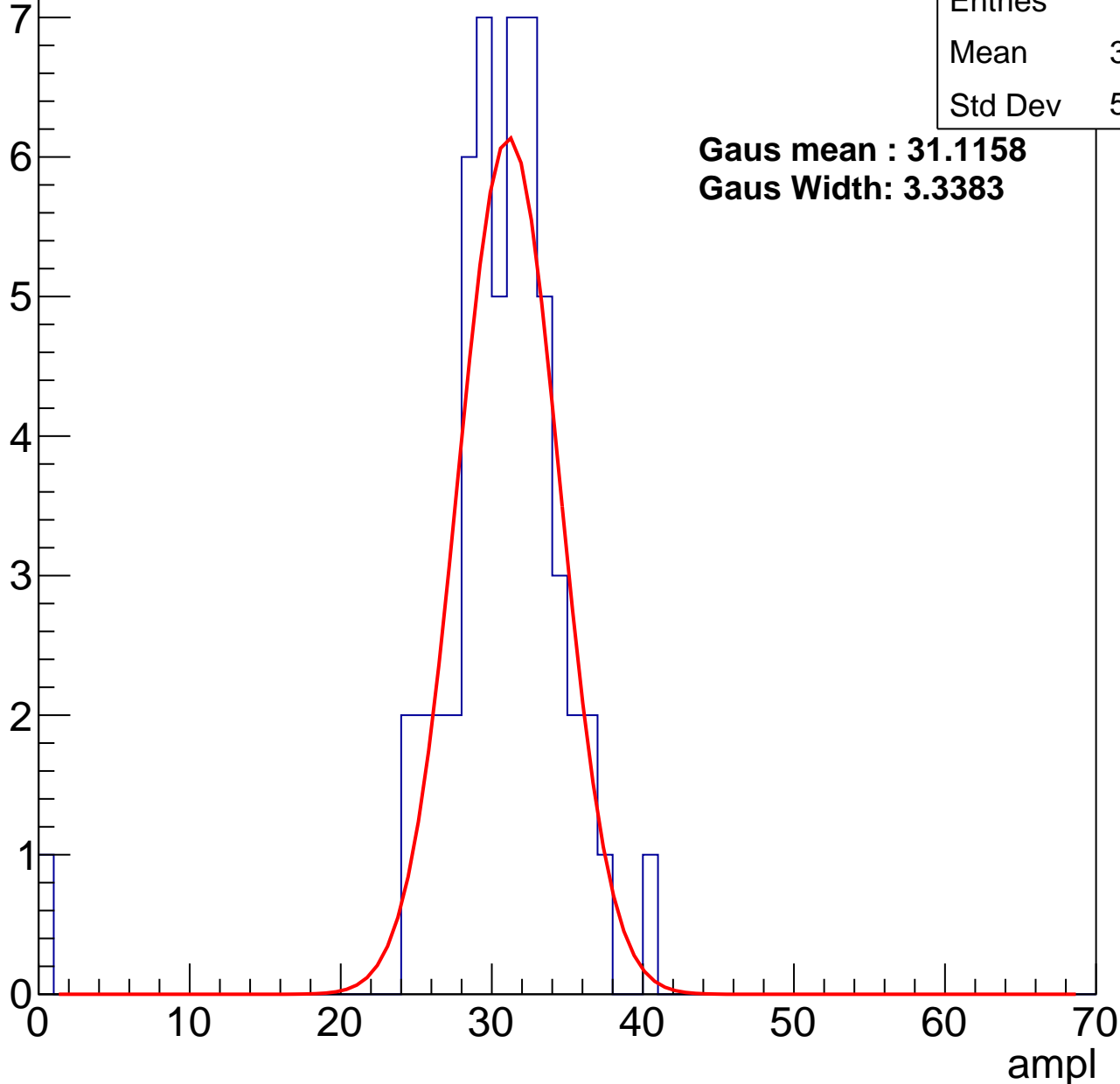
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	30.04
Std Dev	5.226

Gaus mean : 31.1158

Gaus Width: 3.3383



B0L002S, U26-ch68, adc1

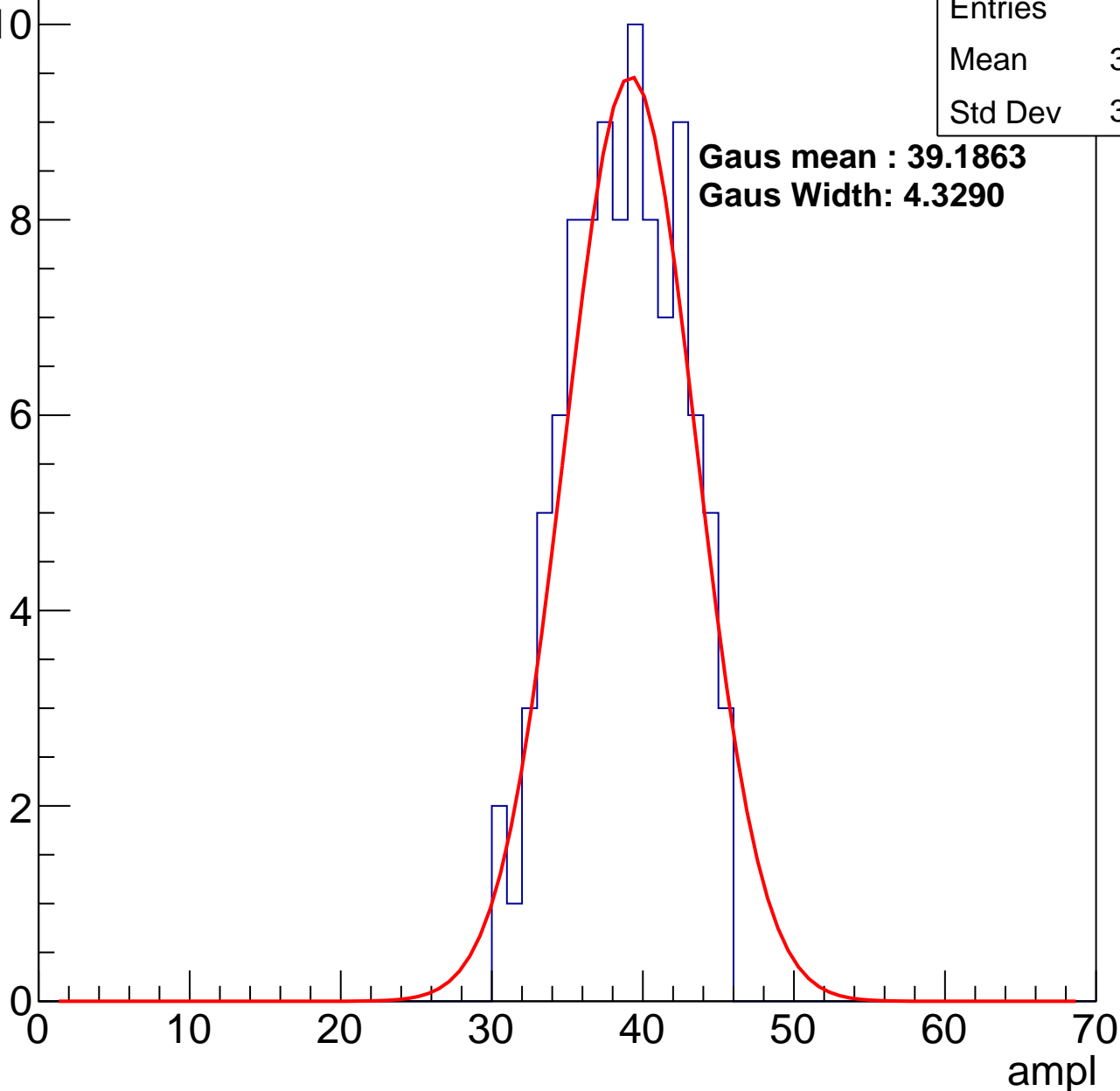
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	98
Mean	38.26
Std Dev	3.696

Gaus mean : 39.1863

Gaus Width: 4.3290



B0L002S, U26-ch68, adc2

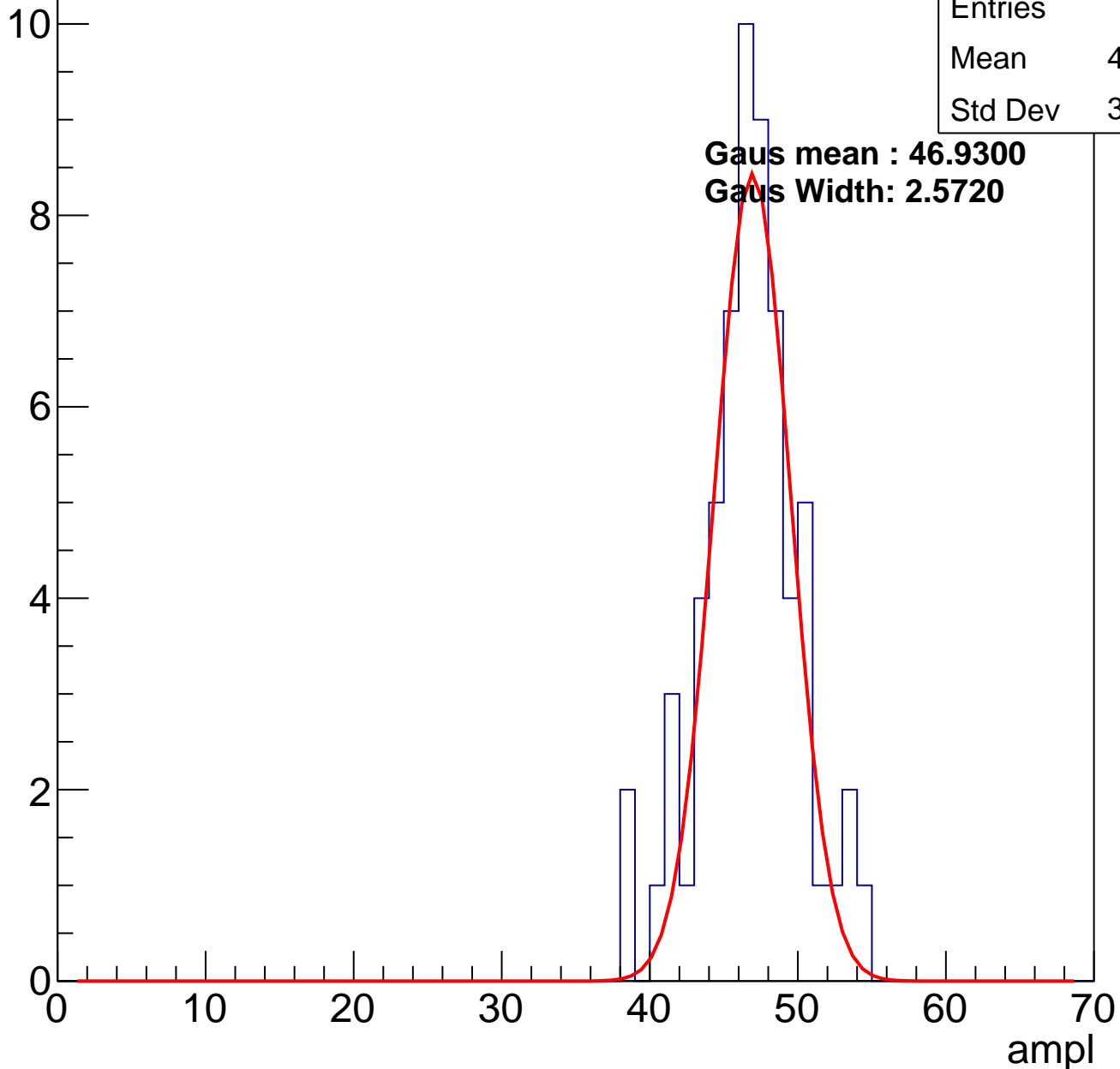
calib_packv5_042523_0143.root, FC#8, port C1

Entries	63
Mean	46.29
Std Dev	3.307

Gaus mean : 46.9300

Gaus Width: 2.5720

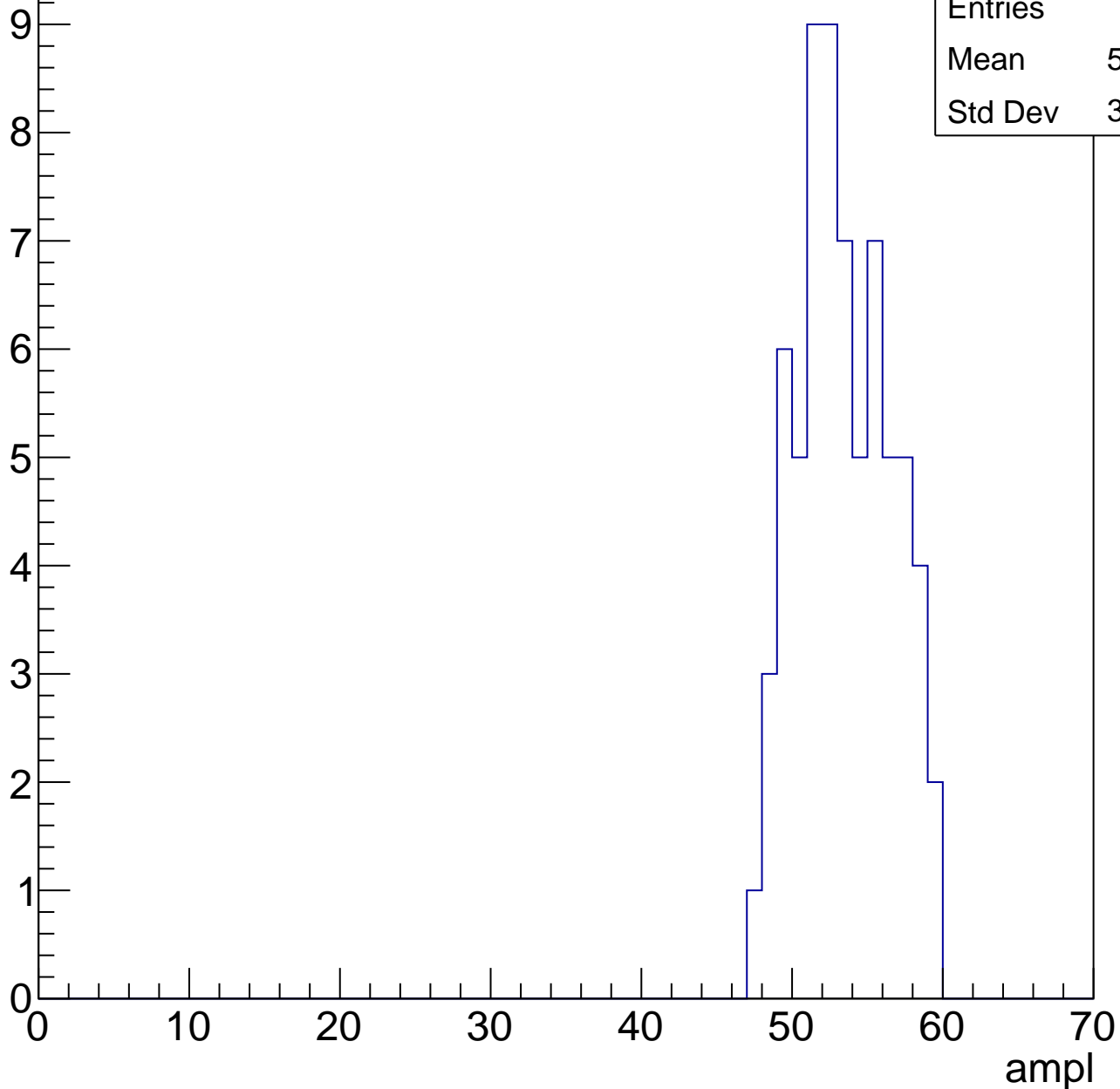
Entry



B0L002S, U26-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch68, adc4

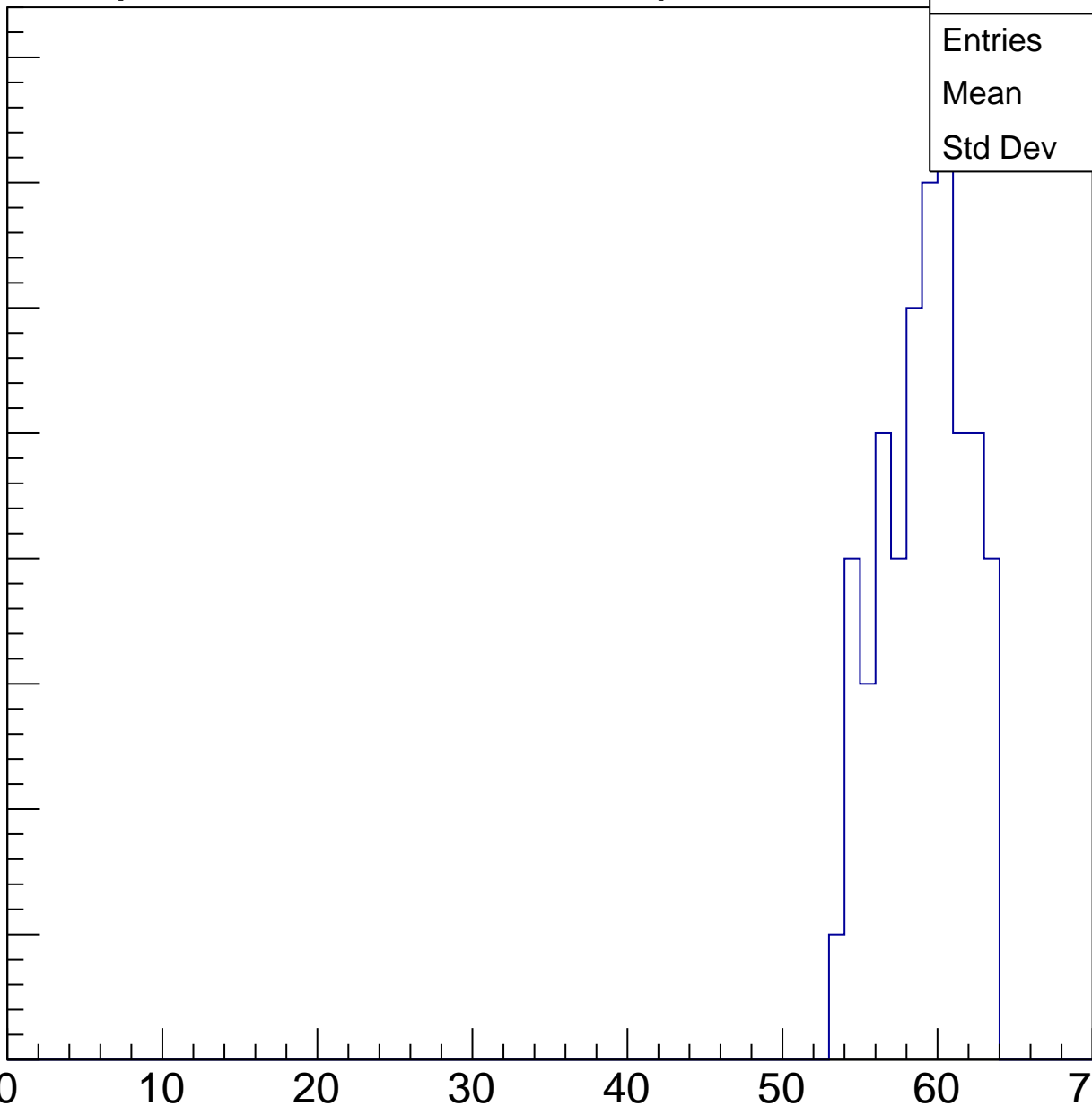
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.65
Std Dev	2.71

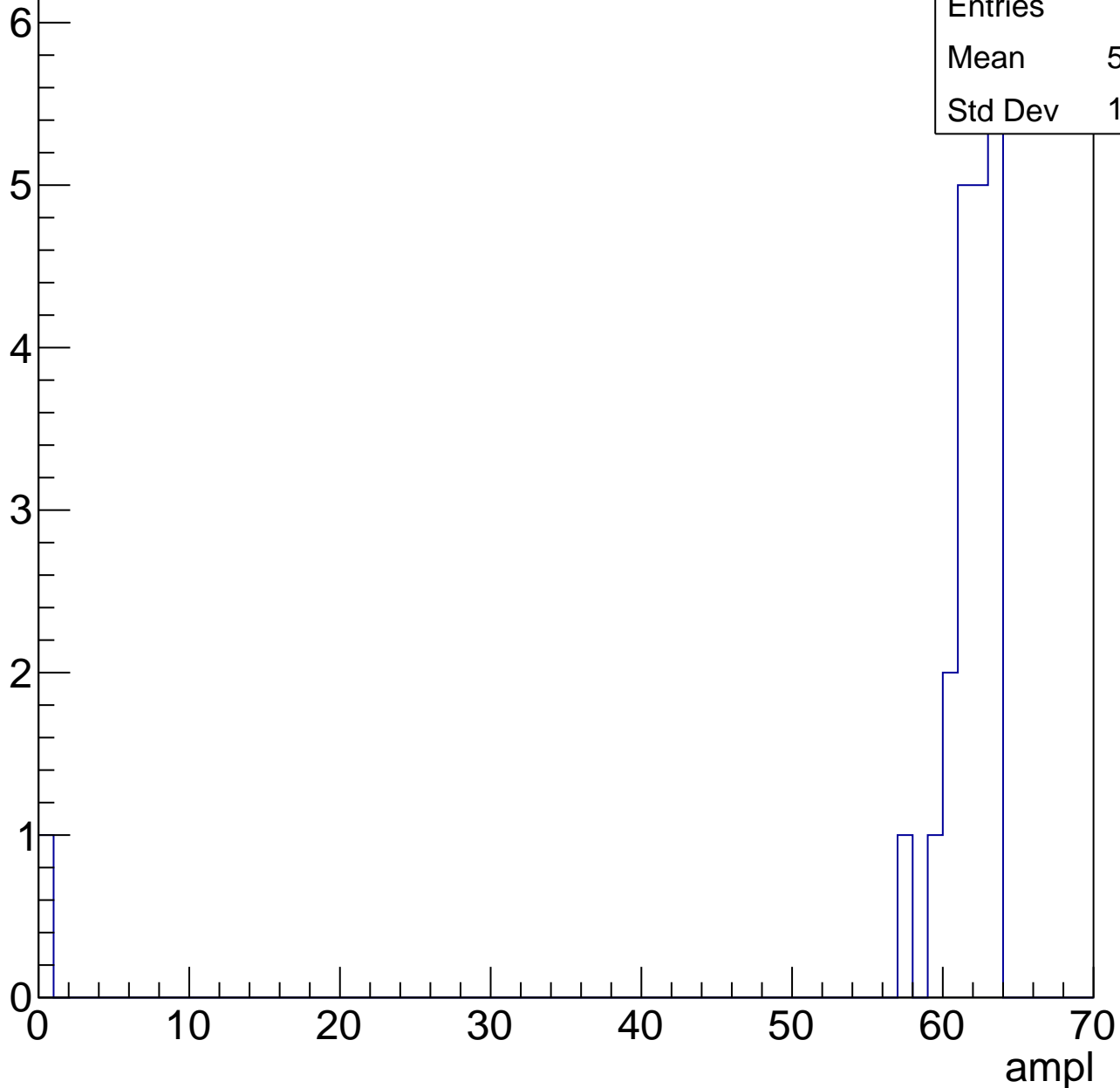
ampl



B0L002S, U26-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch69, adc0

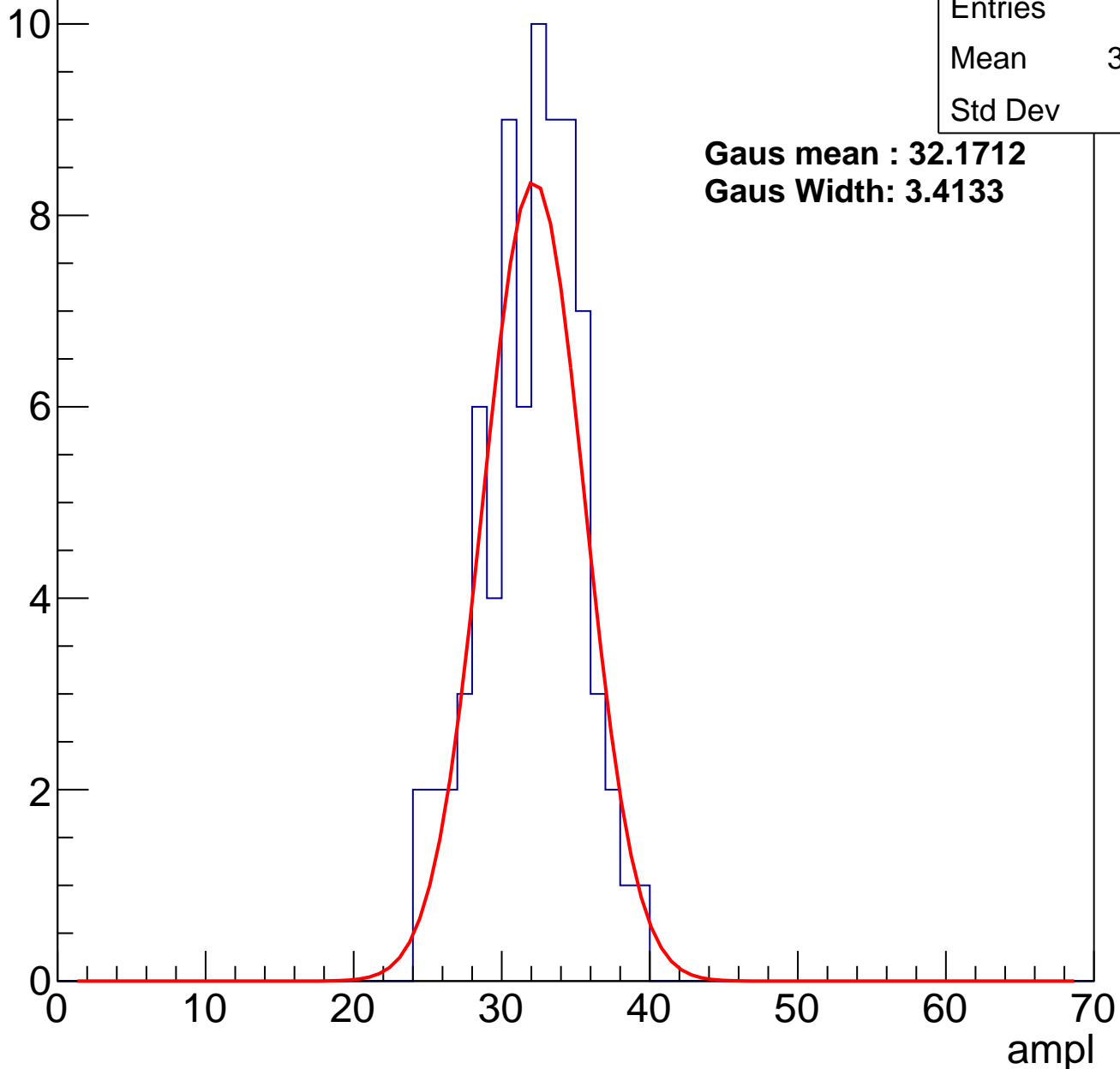
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	31.55
Std Dev	3.29

Gaus mean : 32.1712

Gaus Width: 3.4133

Entry



B0L002S, U26-ch69, adc1

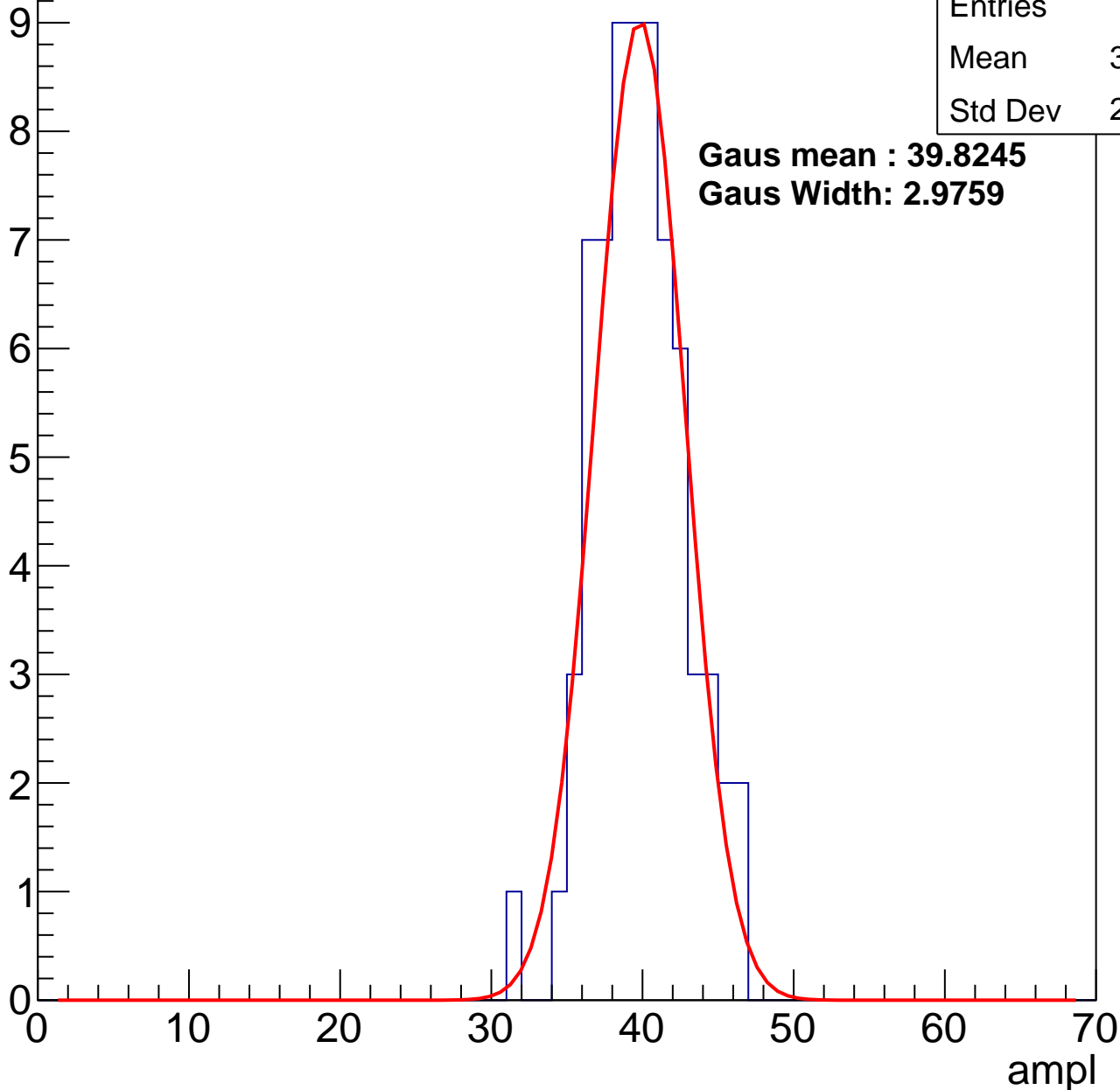
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	39.36
Std Dev	2.978

Gaus mean : 39.8245

Gaus Width: 2.9759



B0L002S, U26-ch69, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	45.63
Std Dev	2.958

Gaus mean : 46.2474

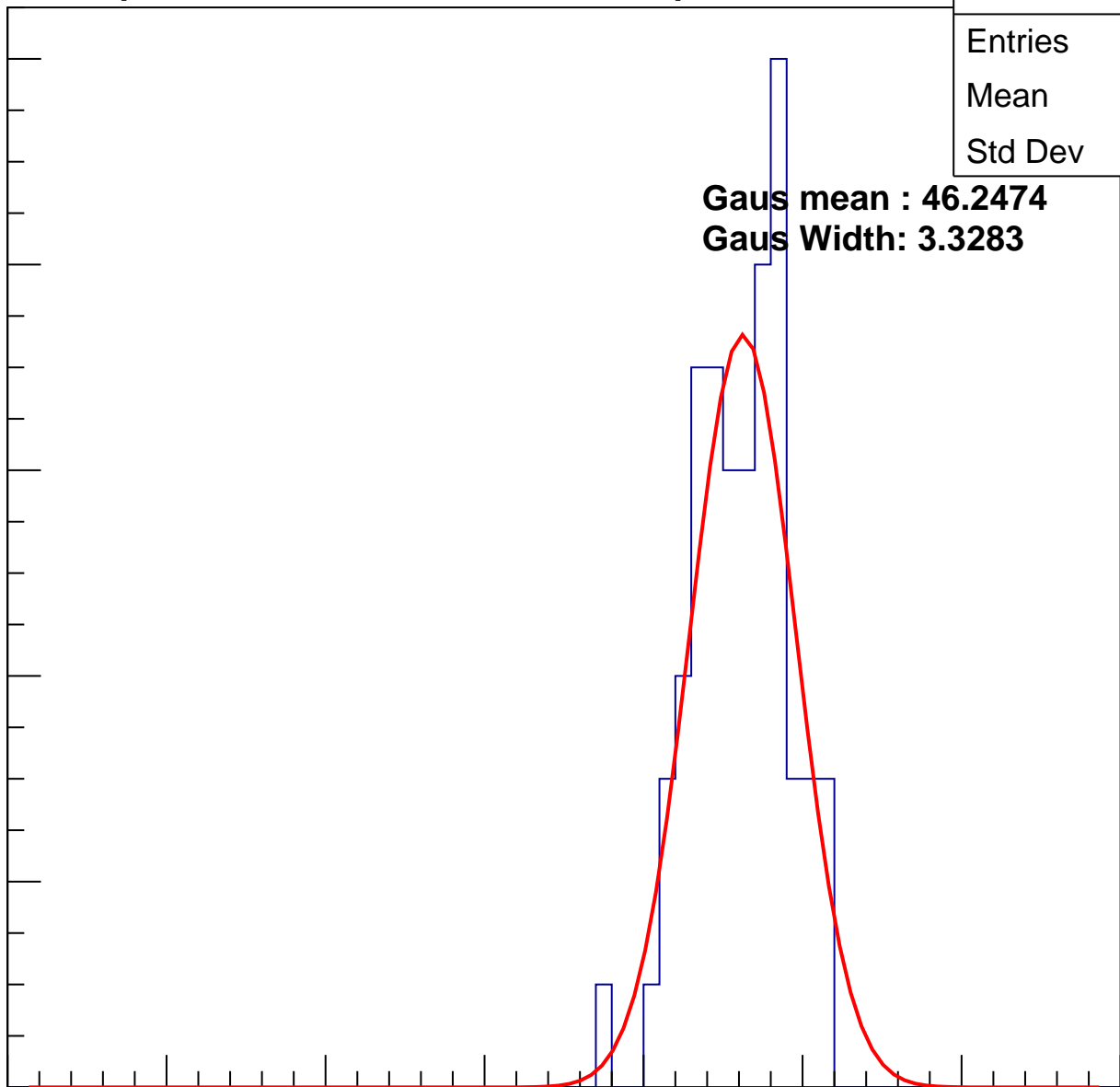
Gaus Width: 3.3283

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

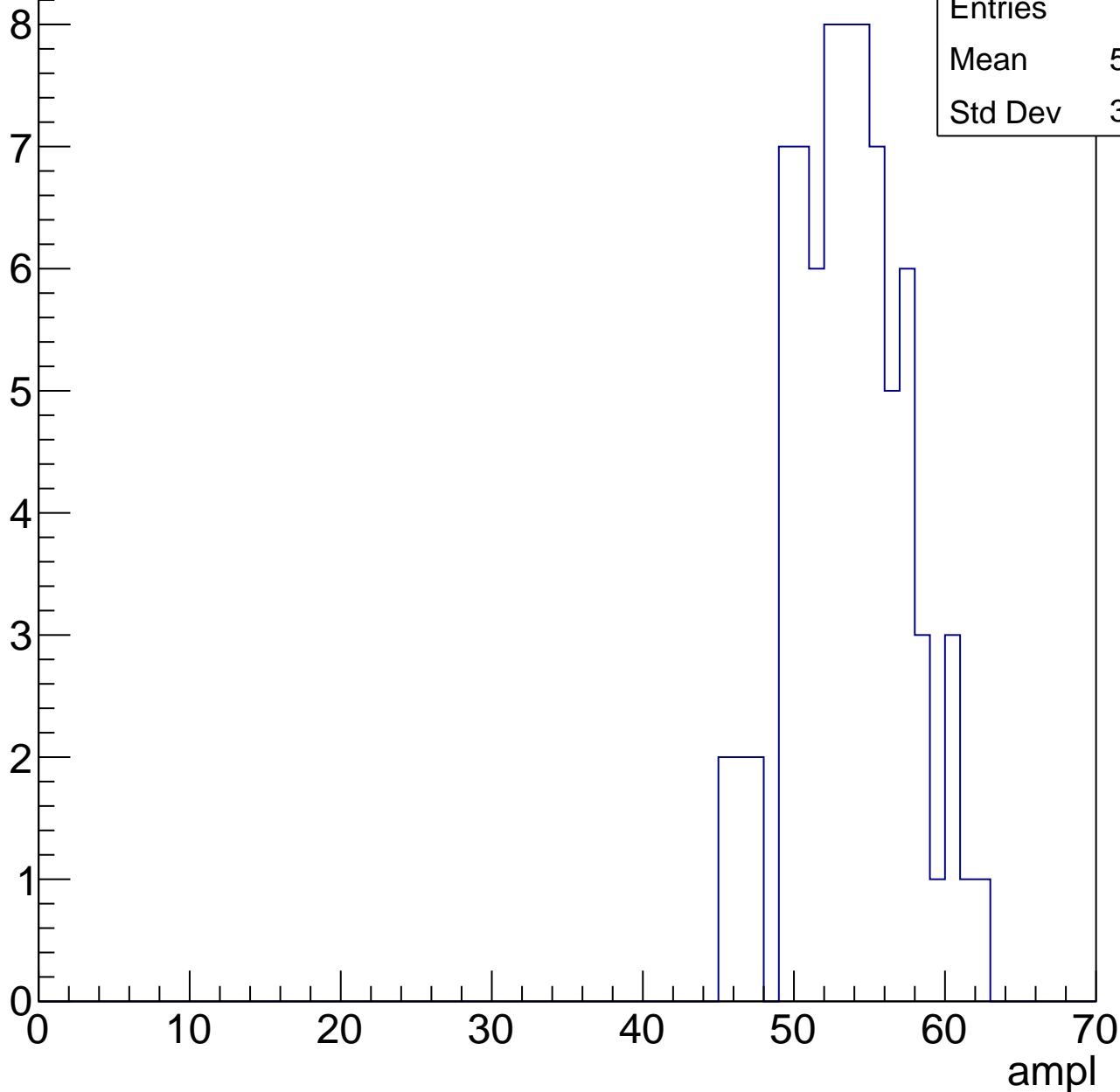


B0L002S, U26-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	53.12
Std Dev	3.752



B0L002S, U26-ch69, adc4

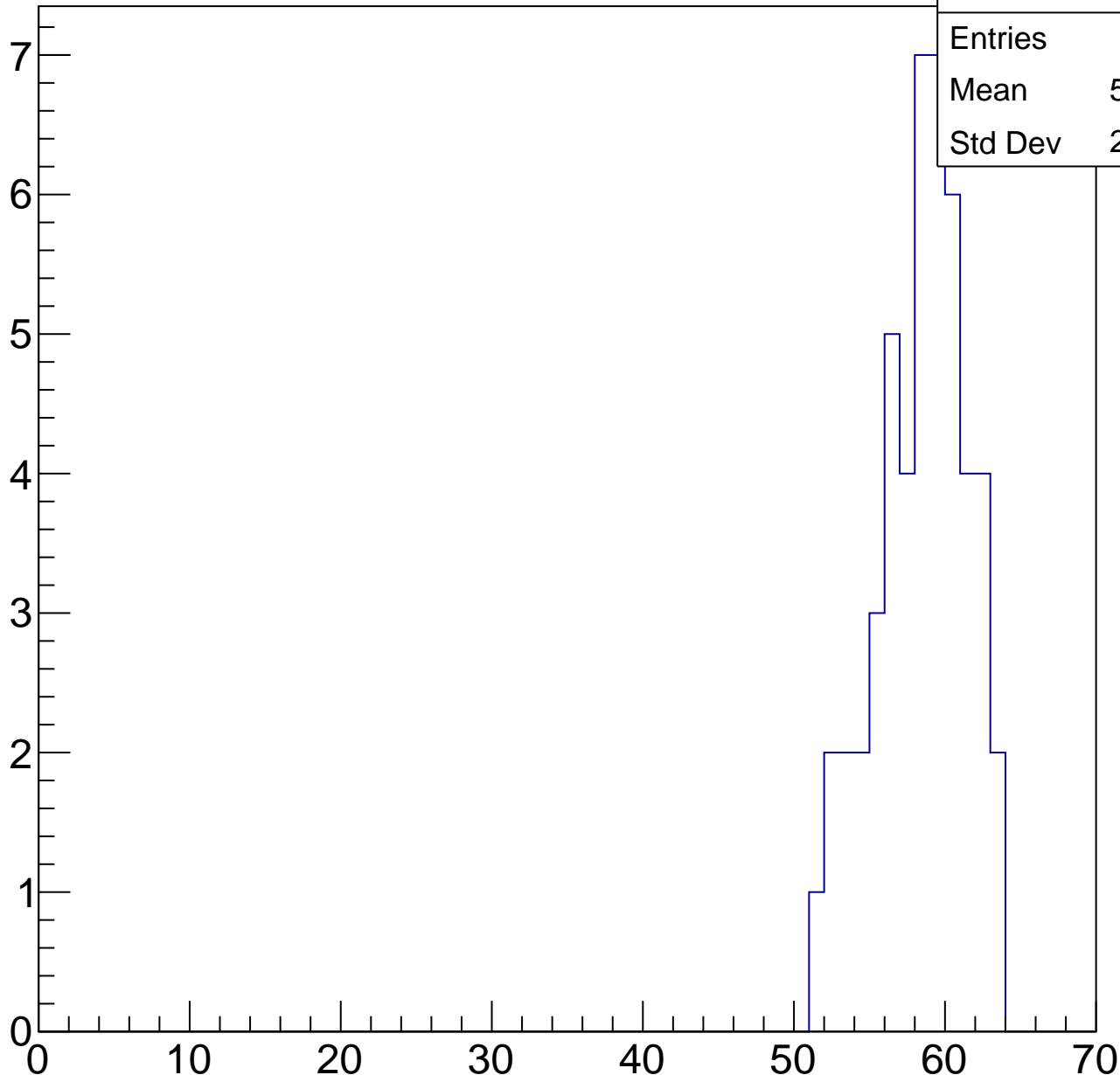
calib_packv5_042523_0143.root, FC#8, port C1

Entry

7
6
5
4
3
2
1
0

Entries	49
Mean	57.94
Std Dev	2.979

ampl

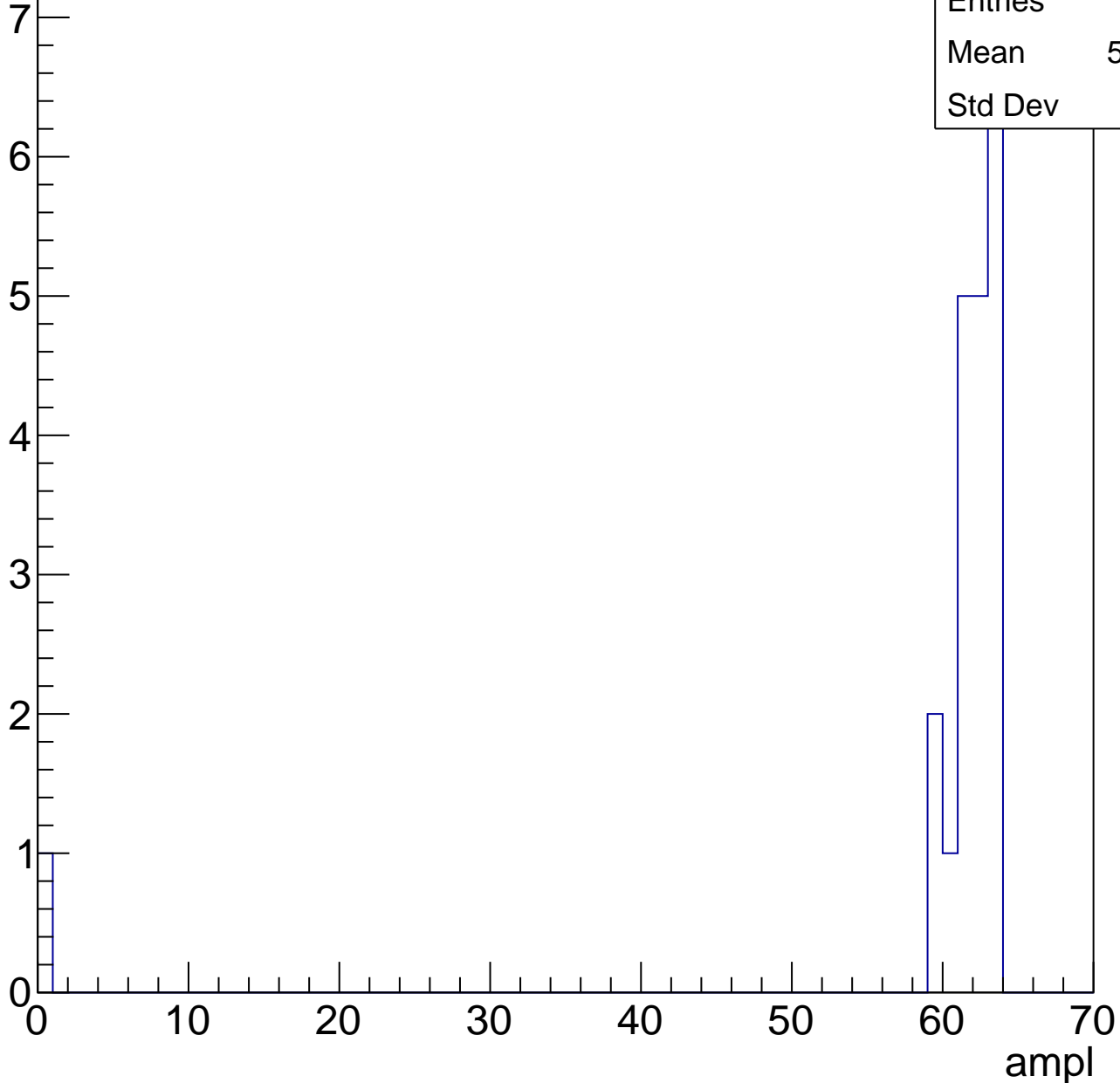


B0L002S, U26-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.76
Std Dev	13.2



B0L002S, U26-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch70, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	29.97
Std Dev	4.606

Gaus mean : 30.7378

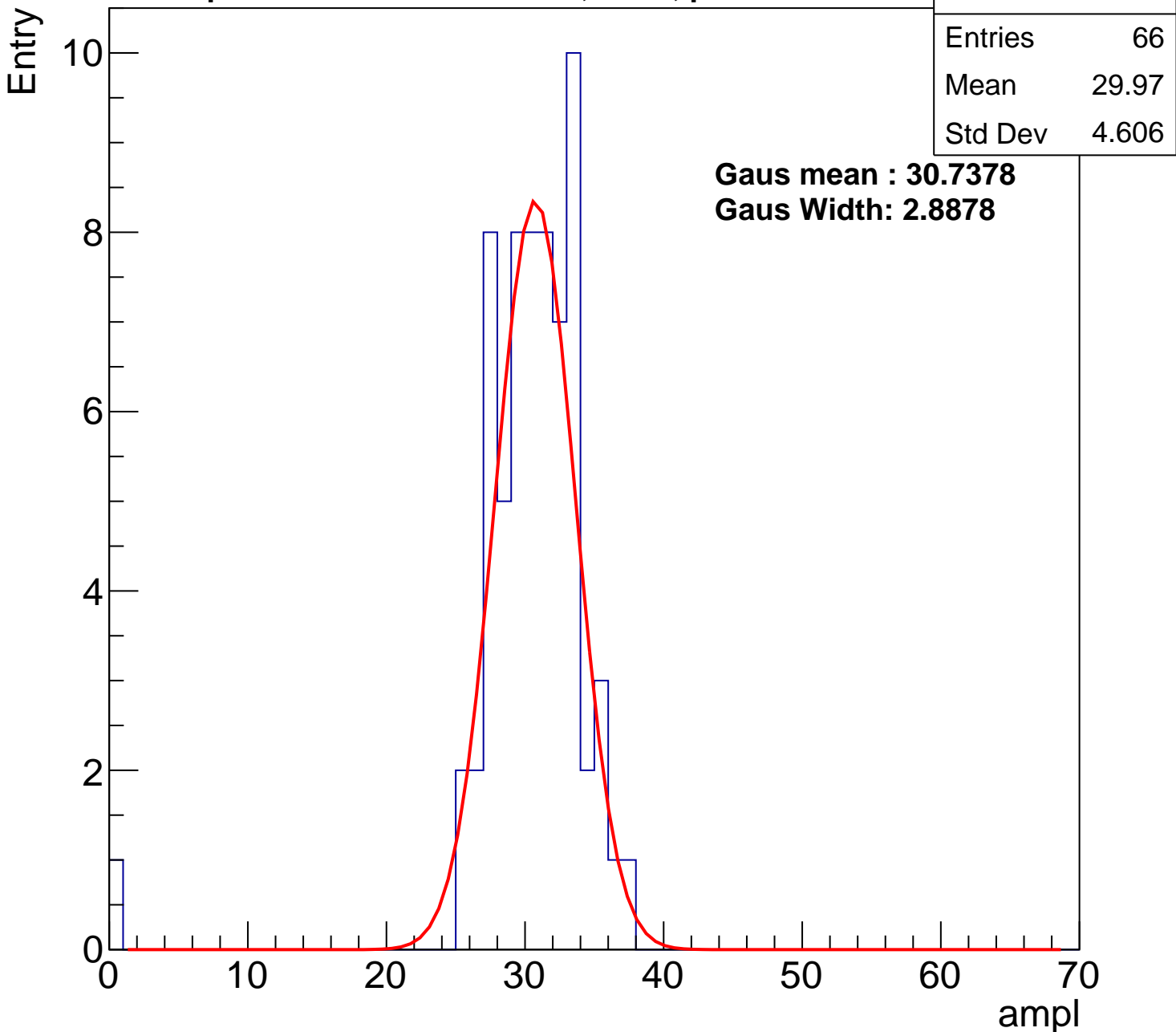
Gaus Width: 2.8878

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch70, adc1

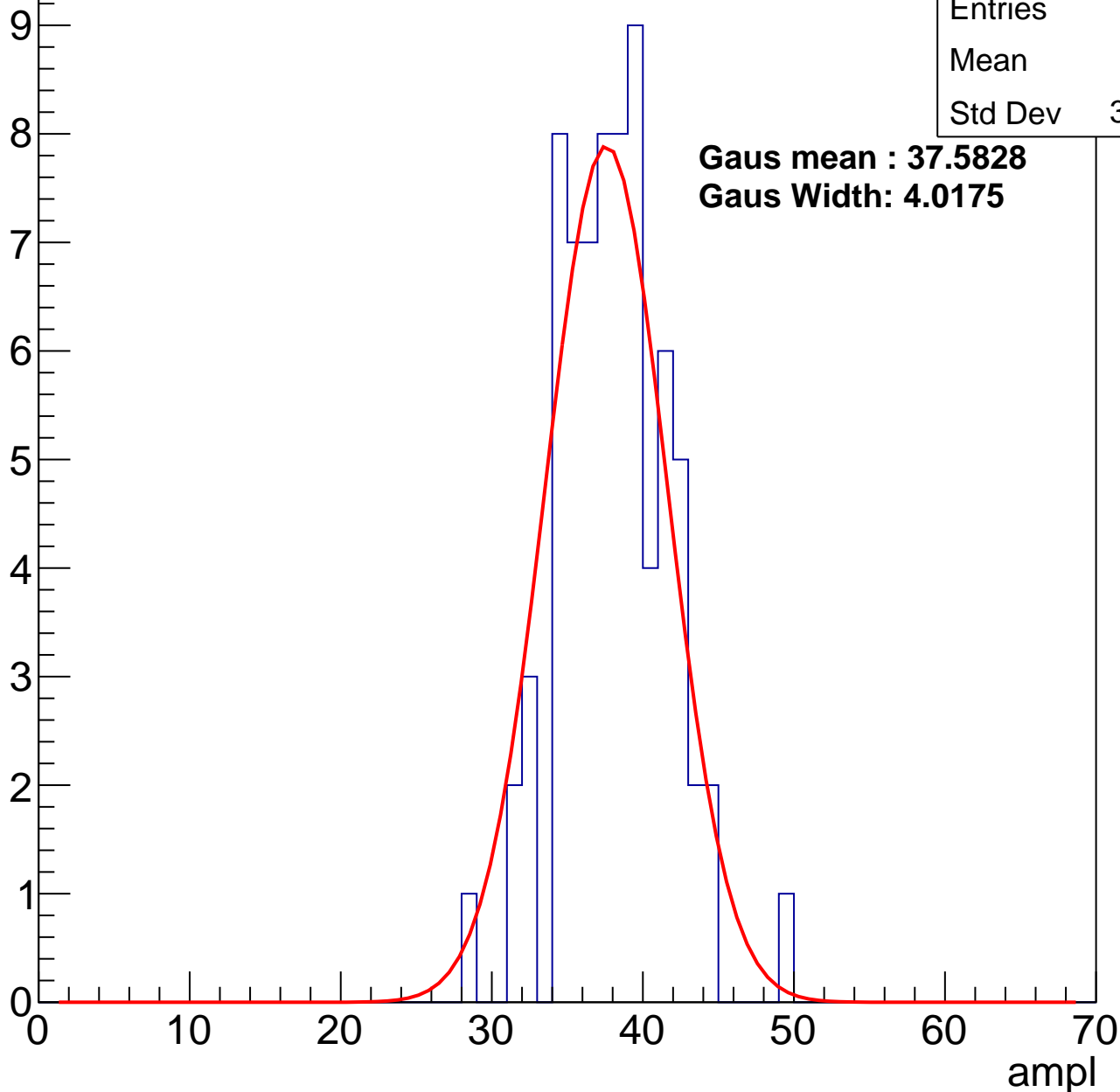
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.6
Std Dev	3.557

Gaus mean : 37.5828

Gaus Width: 4.0175



B0L002S, U26-ch70, adc2

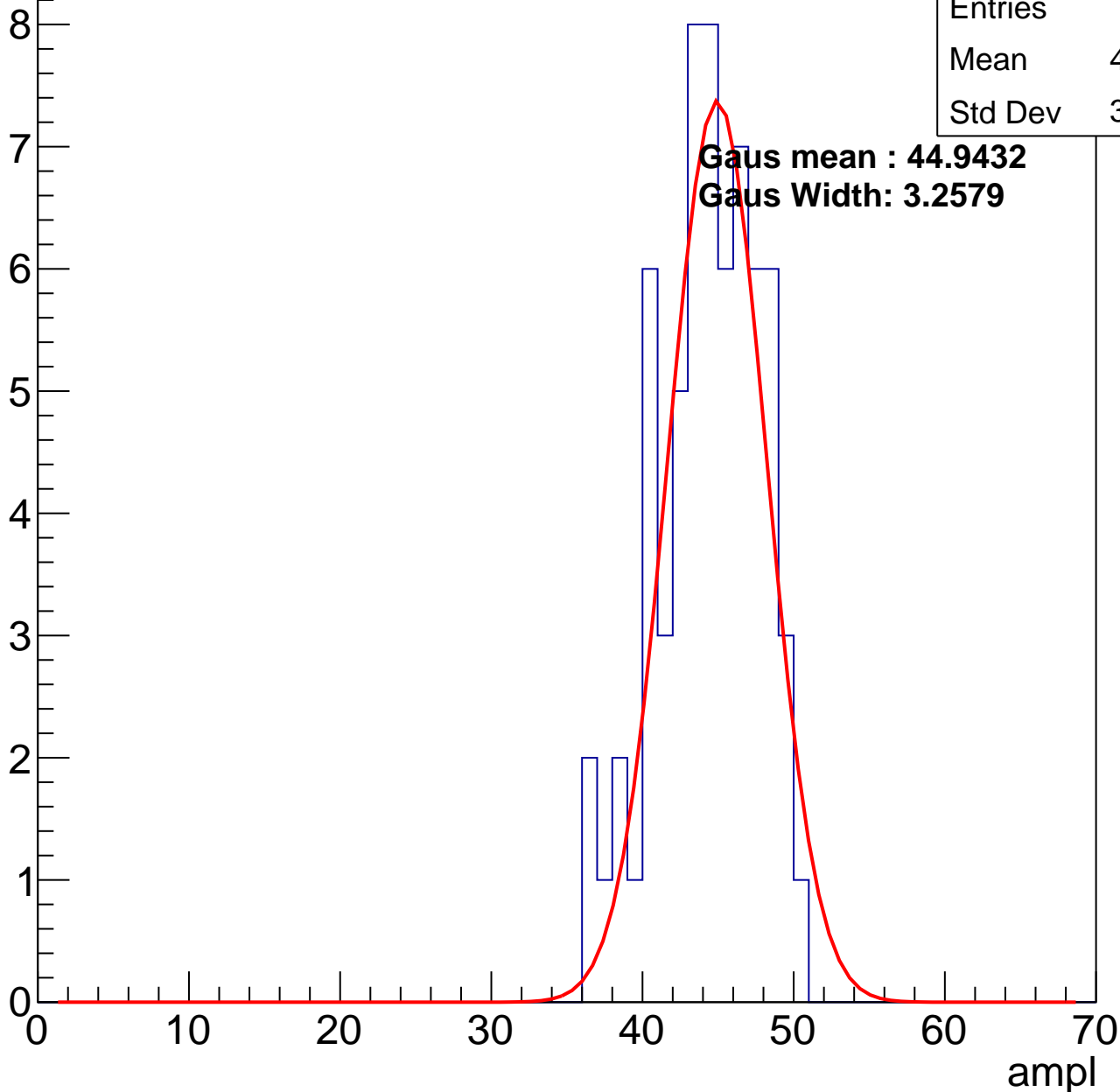
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.88
Std Dev	3.326

Gaus mean : 44.9432

Gaus Width: 3.2579

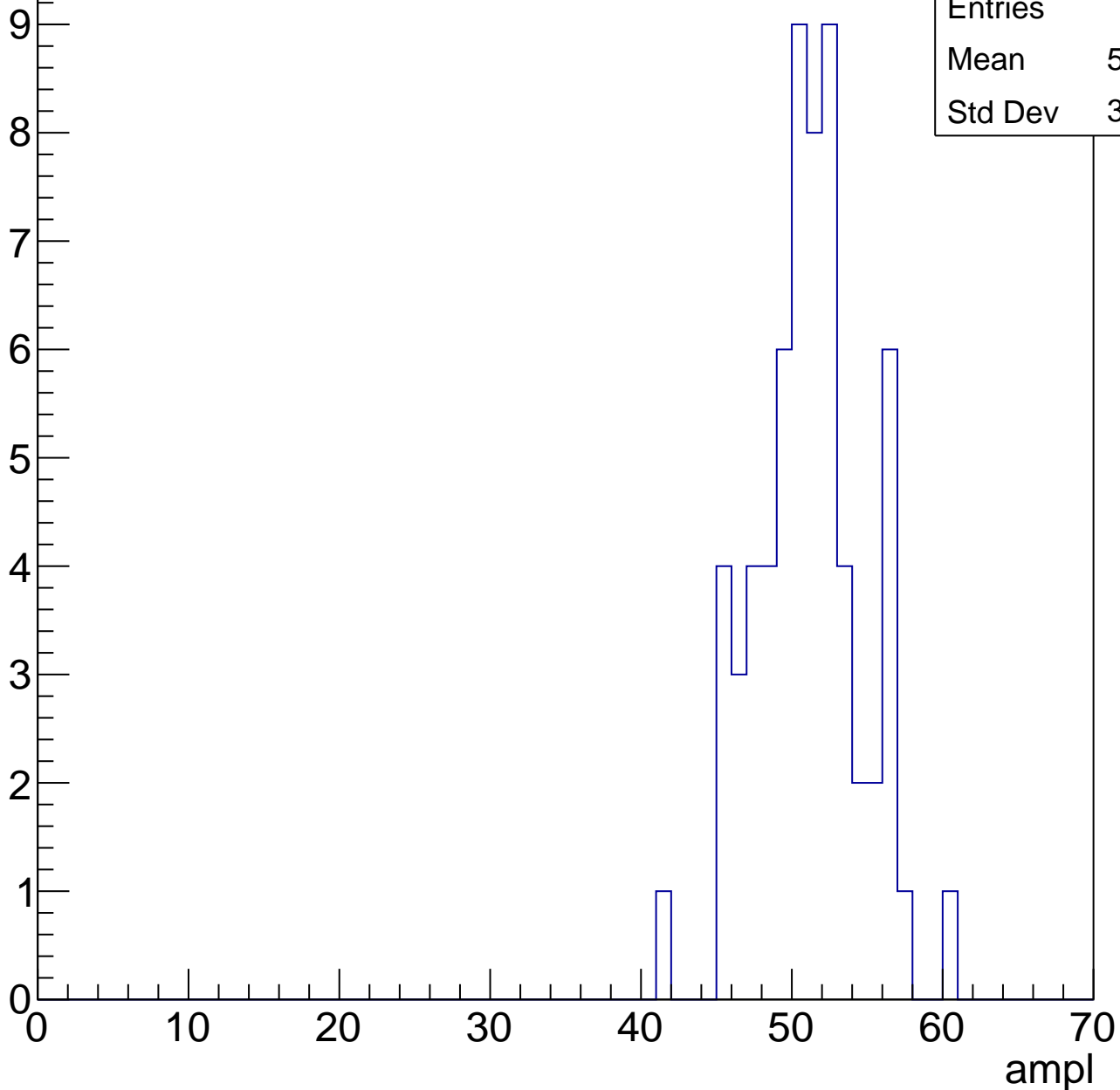


B0L002S, U26-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	50.66
Std Dev	3.505

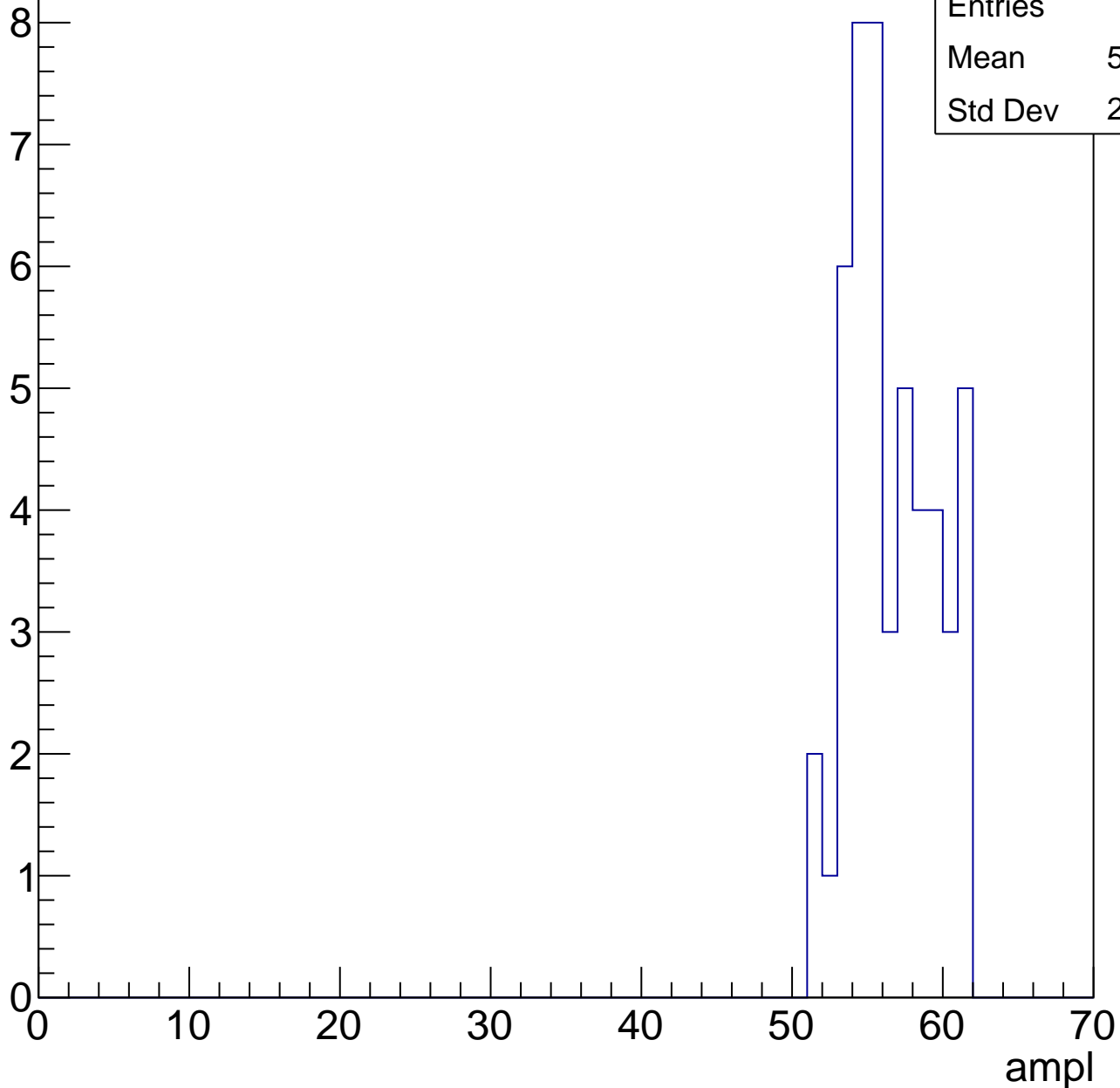


B0L002S, U26-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	56.12
Std Dev	2.819

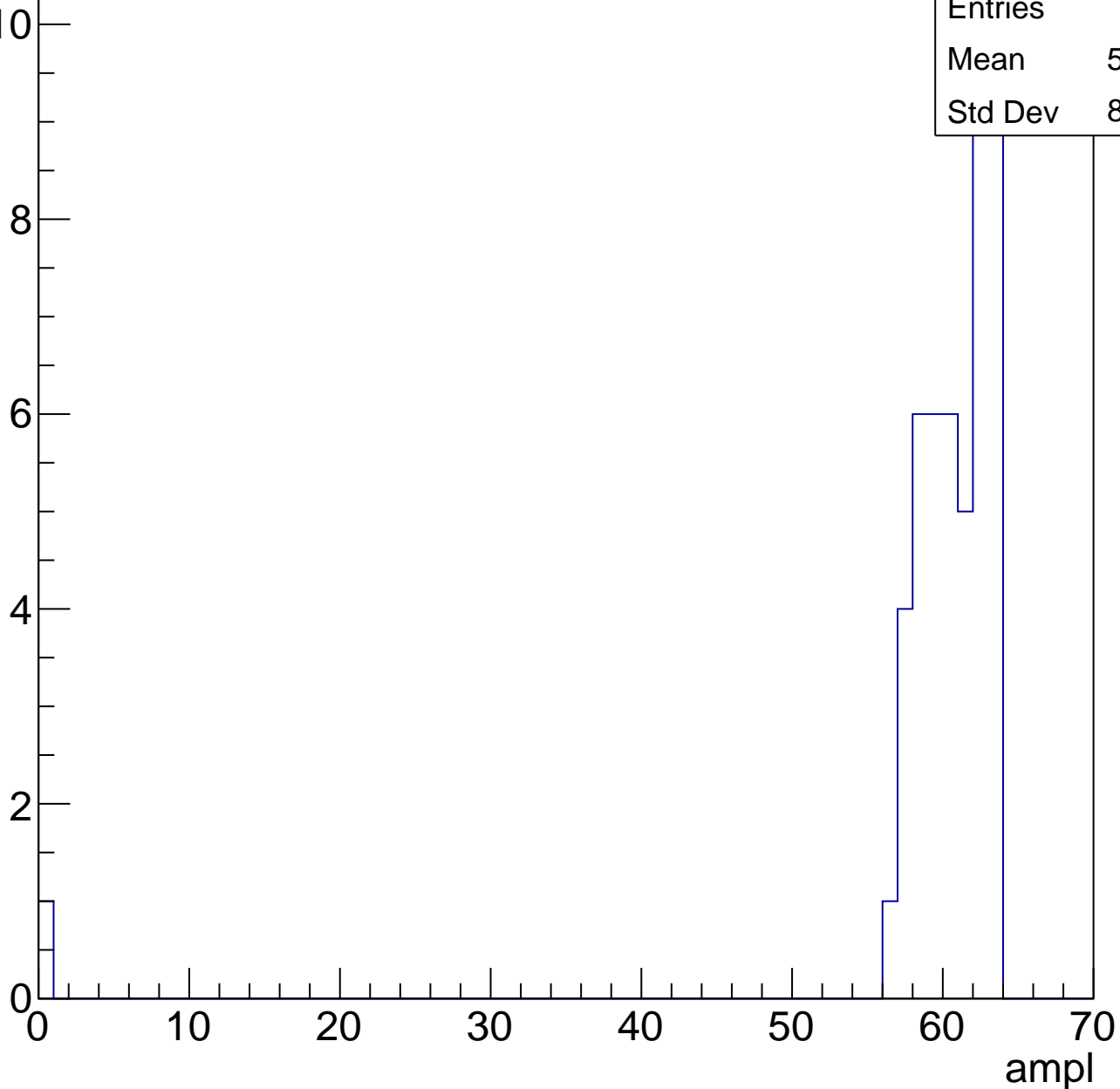


B0L002S, U26-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	59.12
Std Dev	8.864



B0L002S, U26-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch71, adc0

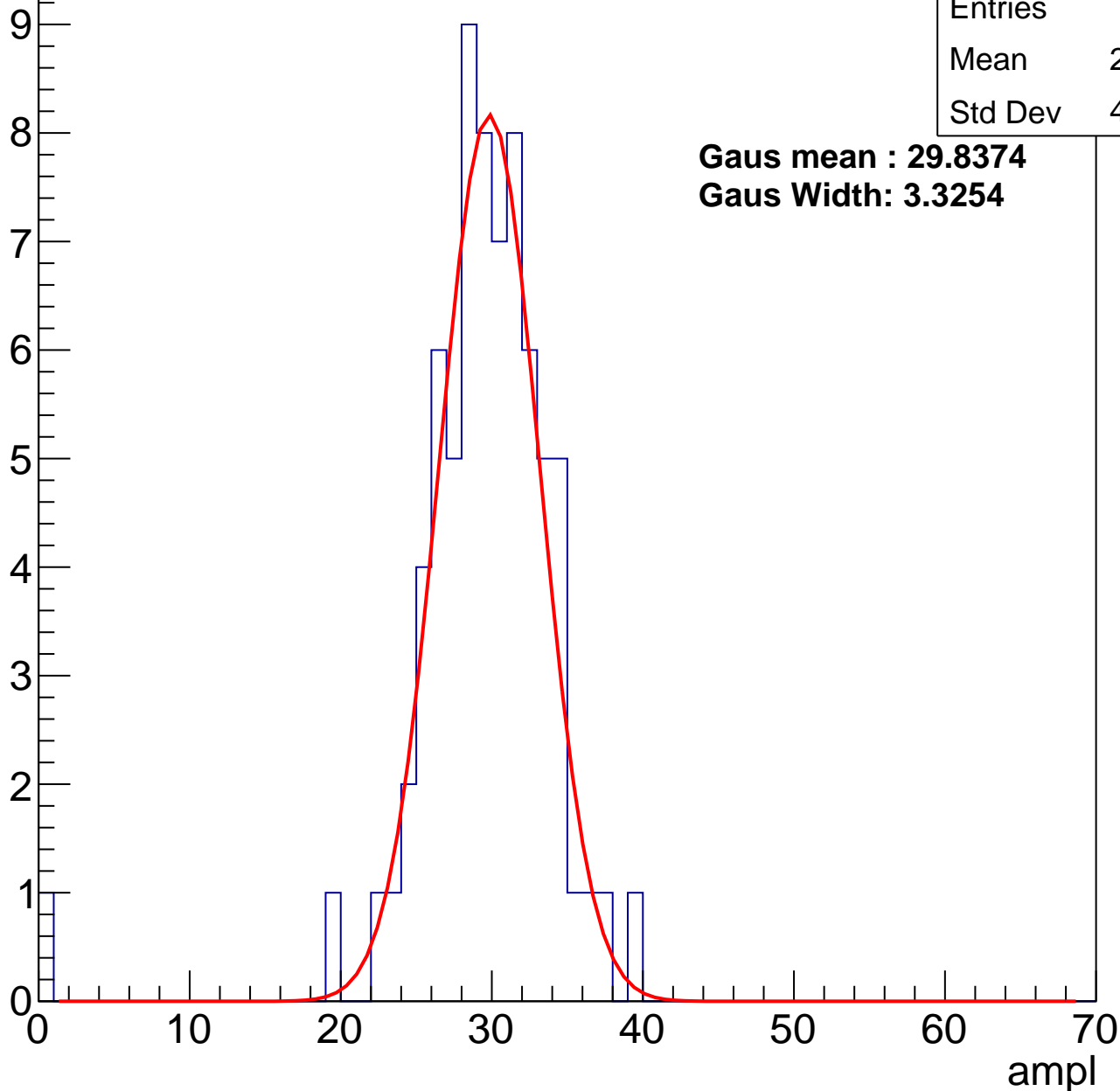
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.03
Std Dev	4.924

Gaus mean : 29.8374

Gaus Width: 3.3254



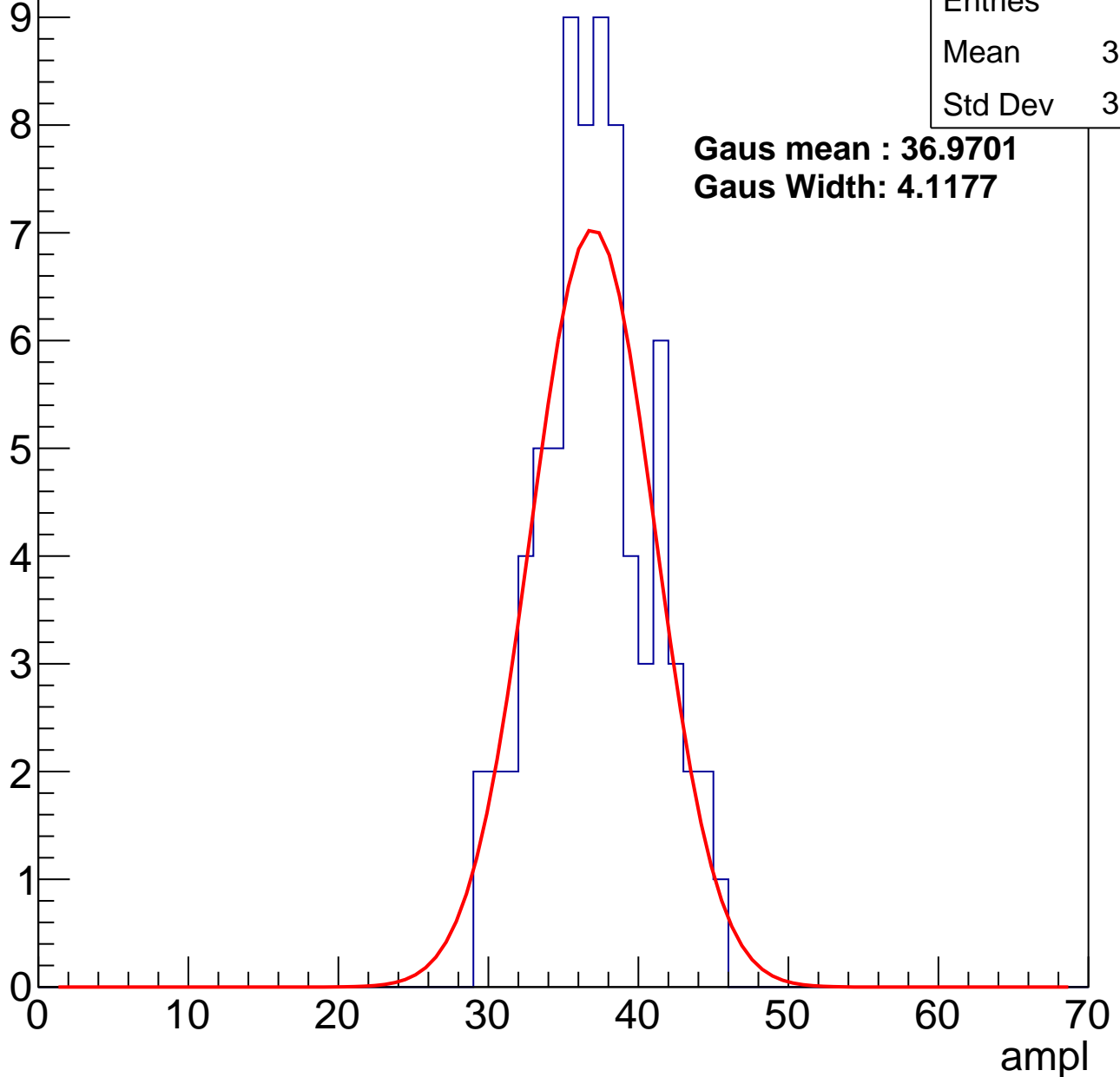
B0L002S, U26-ch71, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	36.67
Std Dev	3.678

Gaus mean : 36.9701
Gaus Width: 4.1177



B0L002S, U26-ch71, adc2

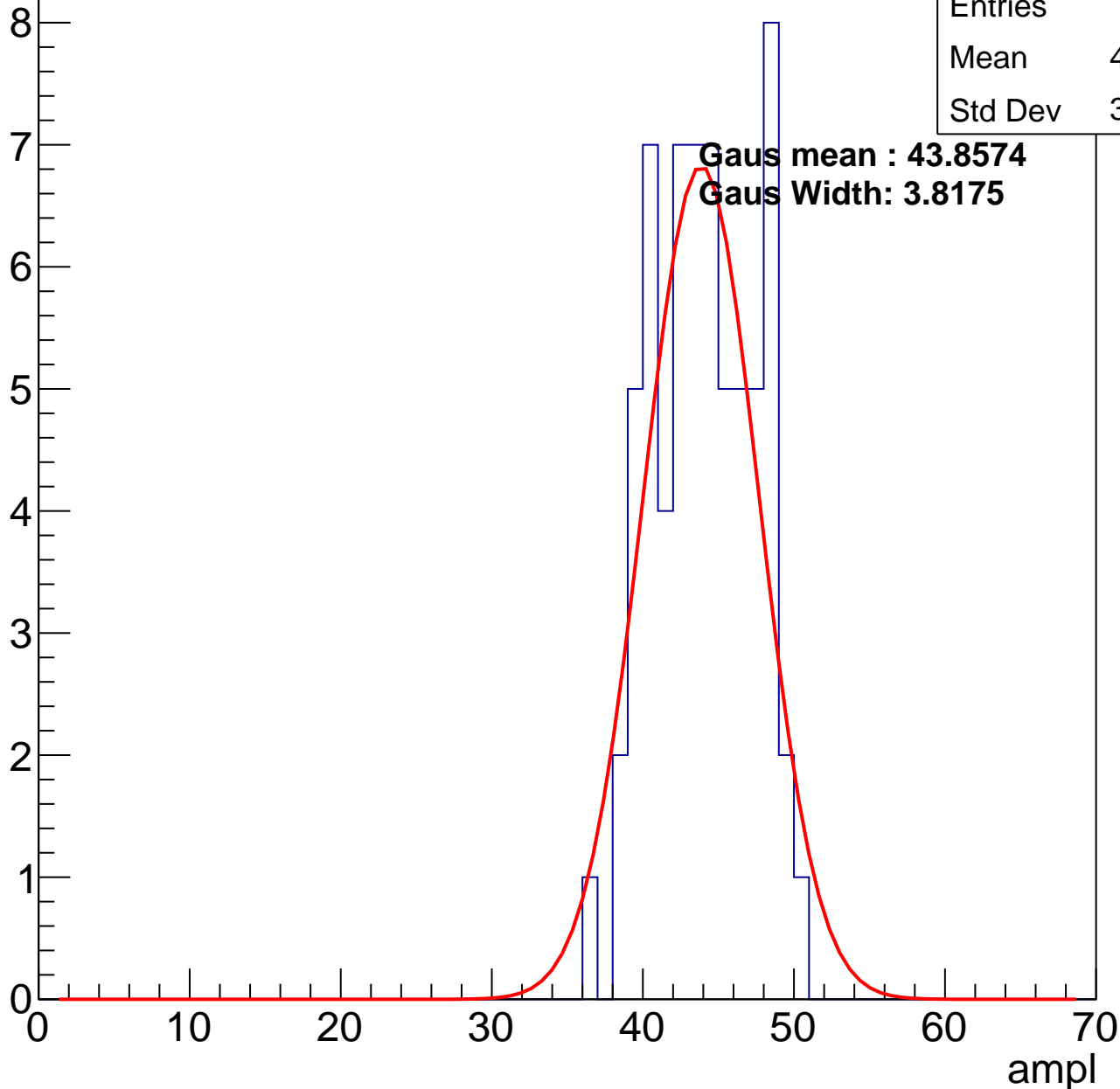
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.58
Std Dev	3.294

Gaus mean : 43.8574

Gaus Width: 3.8175



B0L002S, U26-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

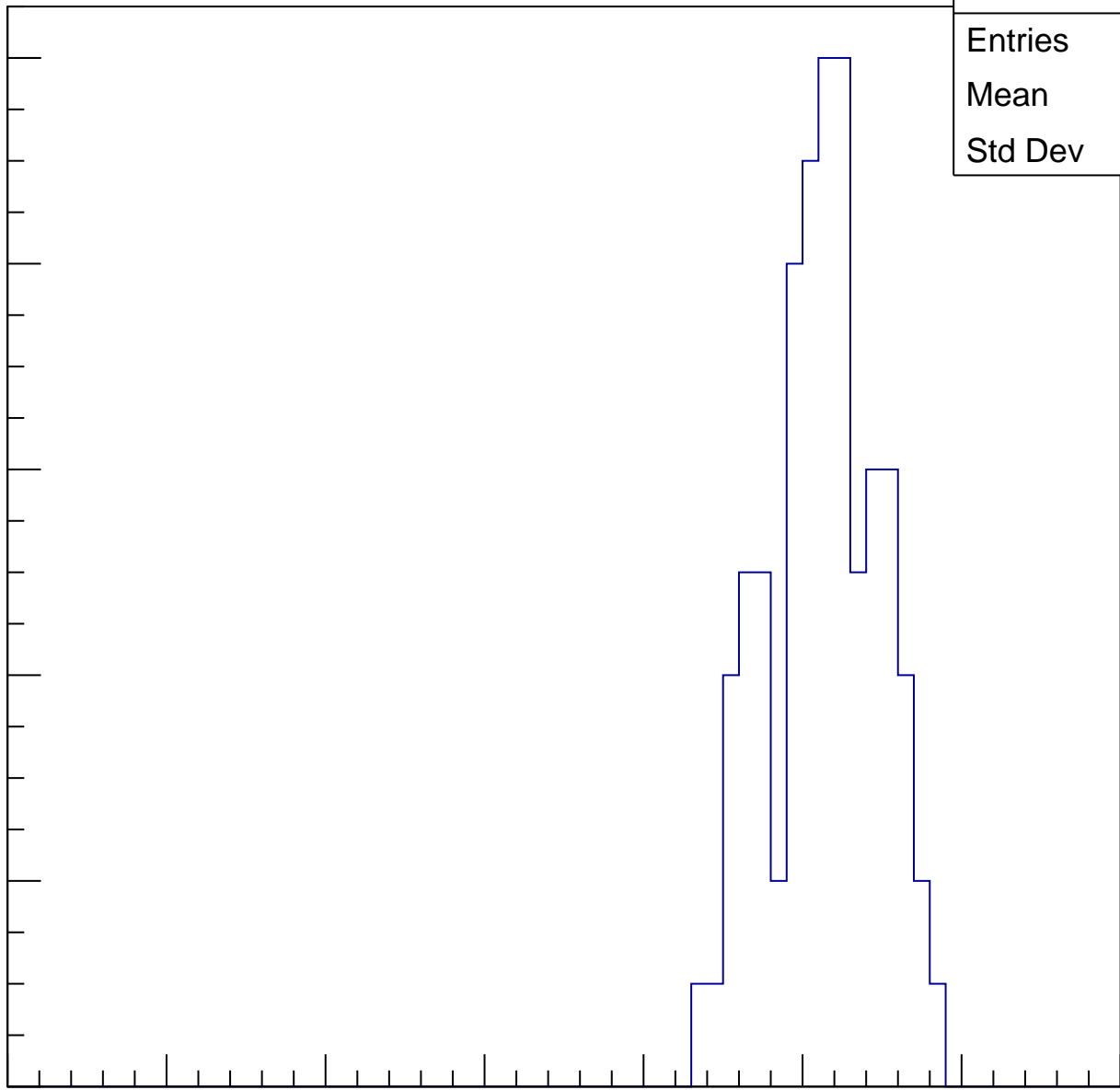
Entries	79
Mean	50.82
Std Dev	3.412

Entry

10
8
6
4
2
0

ampl

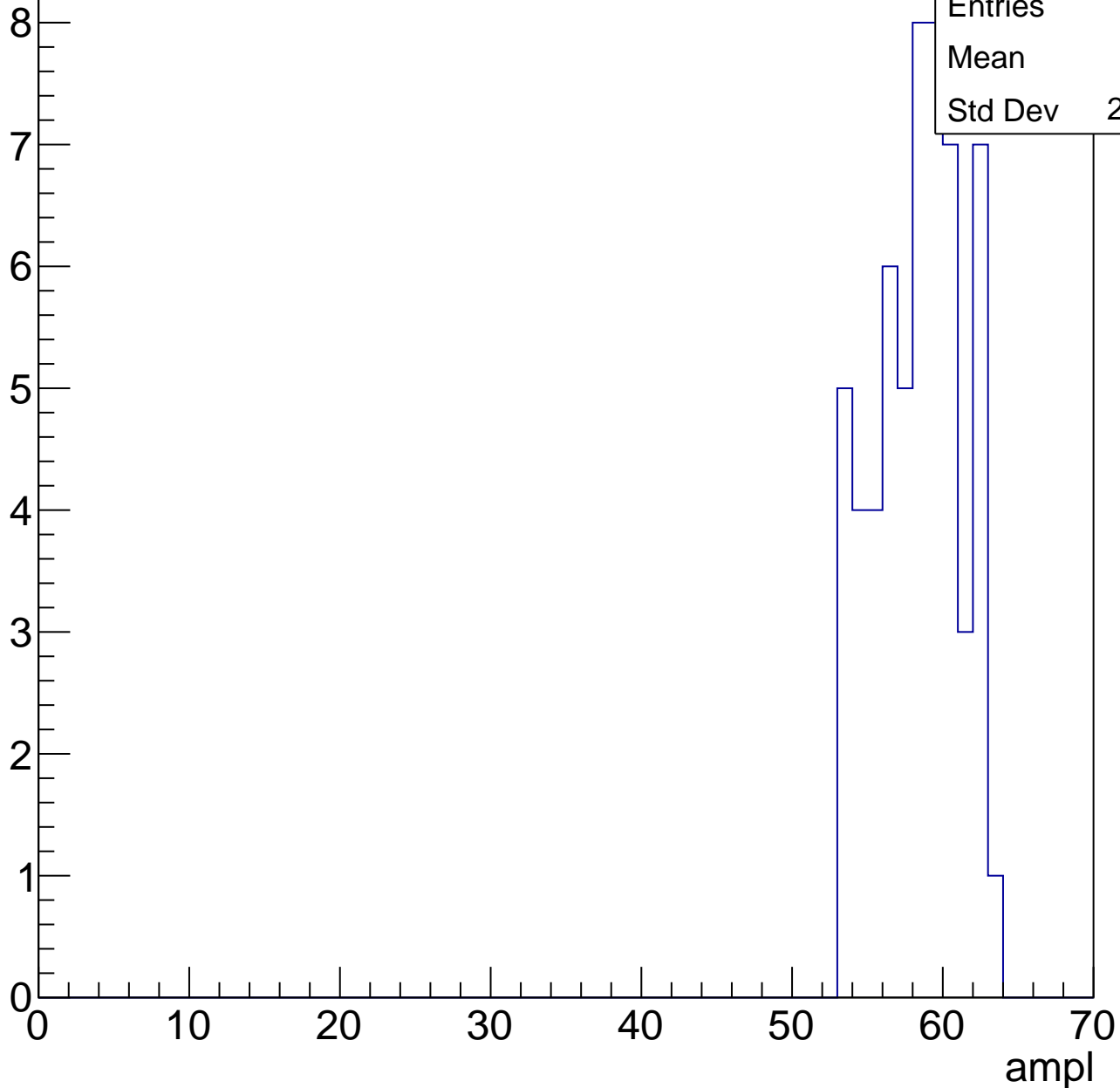
0 10 20 30 40 50 60 70



B0L002S, U26-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

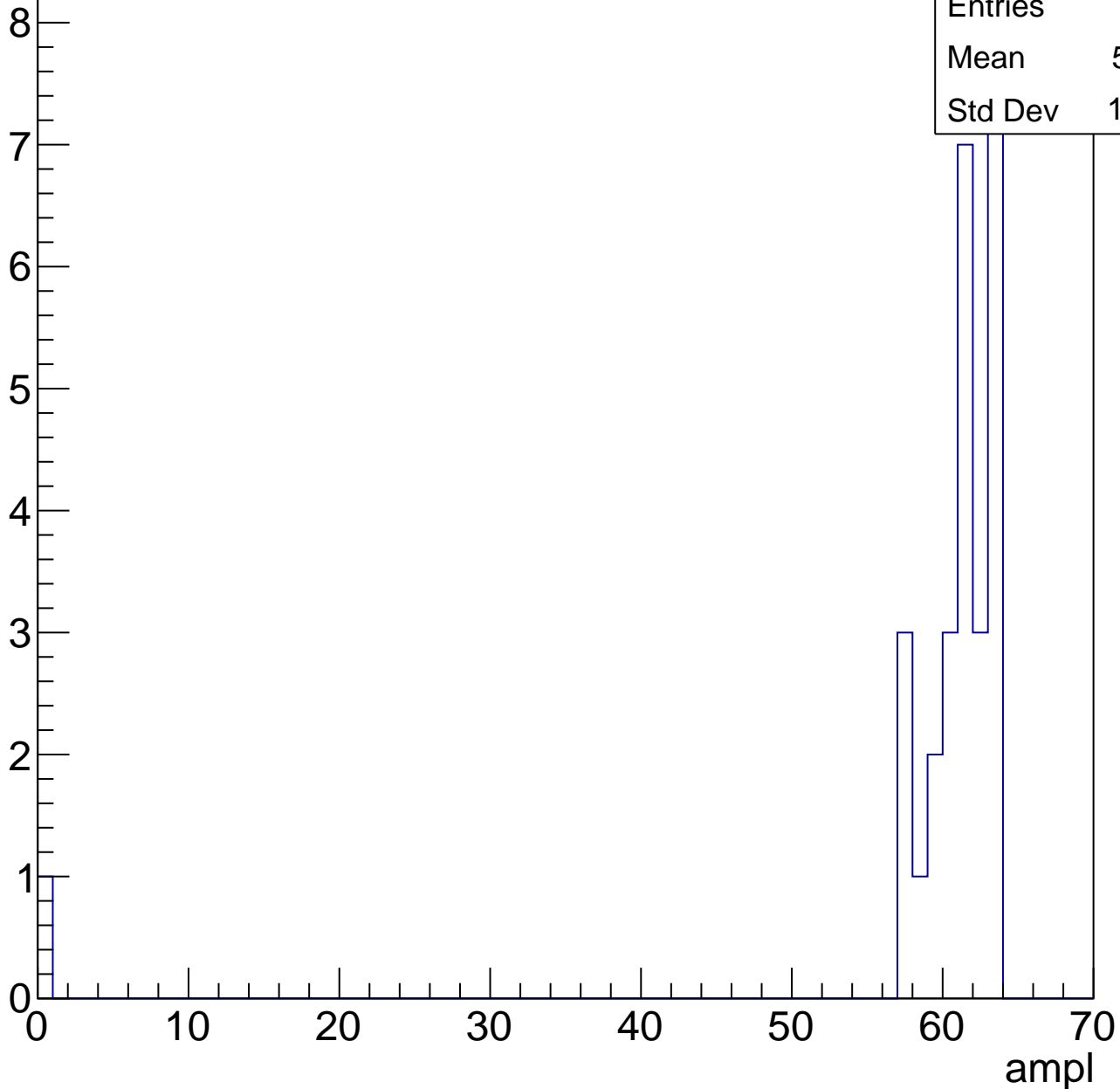


B0L002S, U26-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.71
Std Dev	11.46



B0L002S, U26-ch71, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

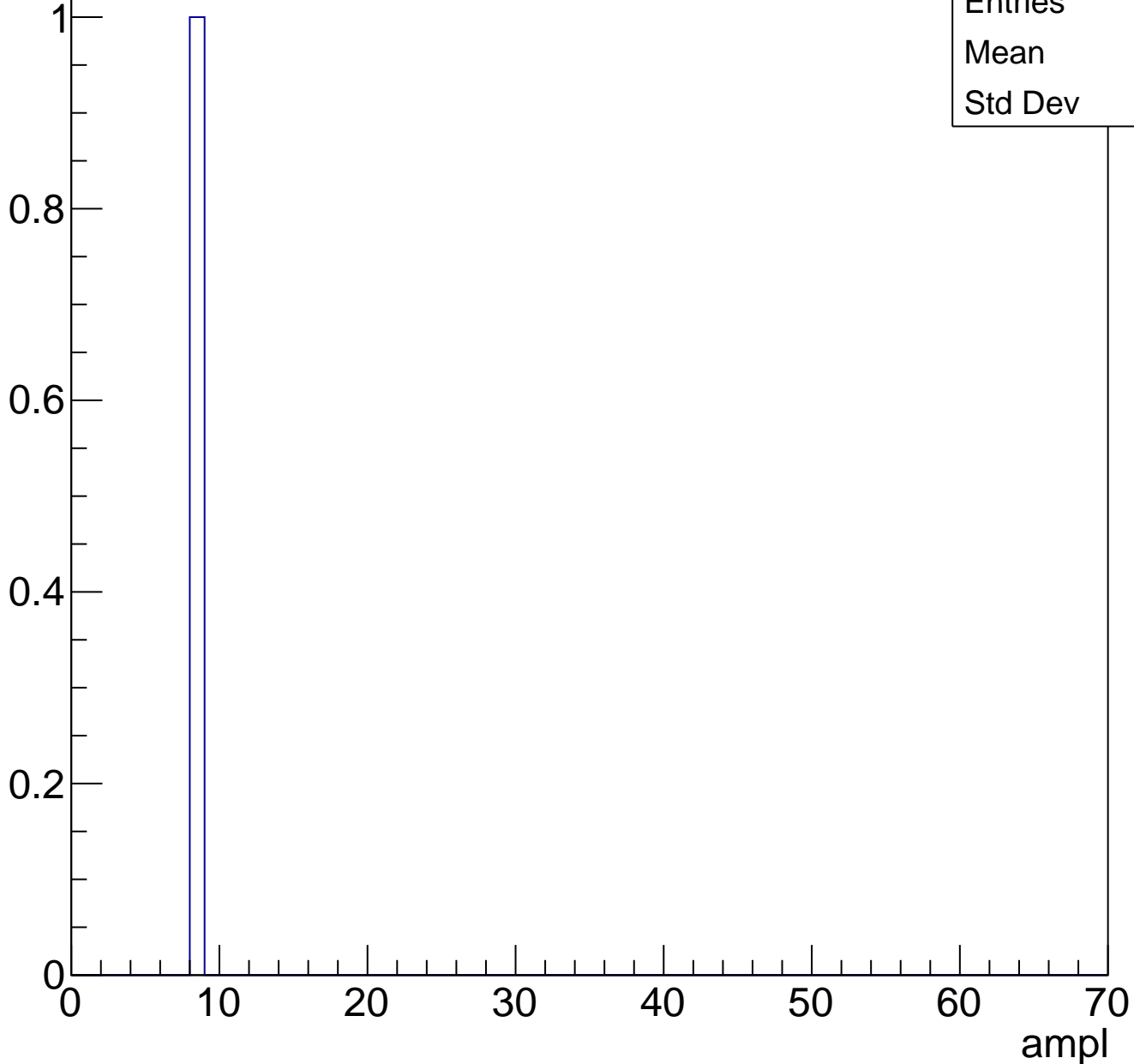
Std Dev

0

B0L002S, U26-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch72, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	30.7
Std Dev	3.451

Gaus mean : 30.5749

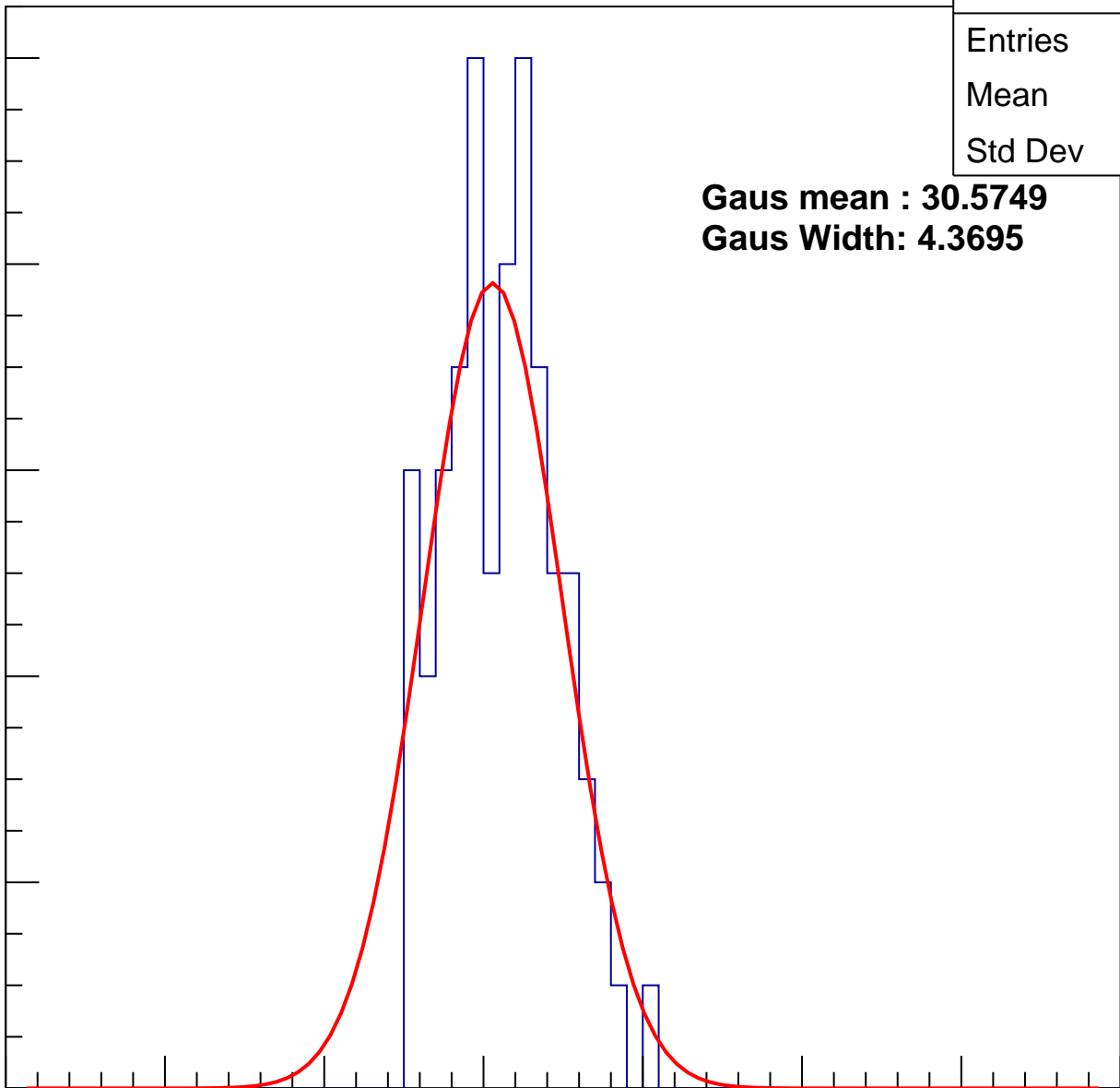
Gaus Width: 4.3695

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch72, adc1

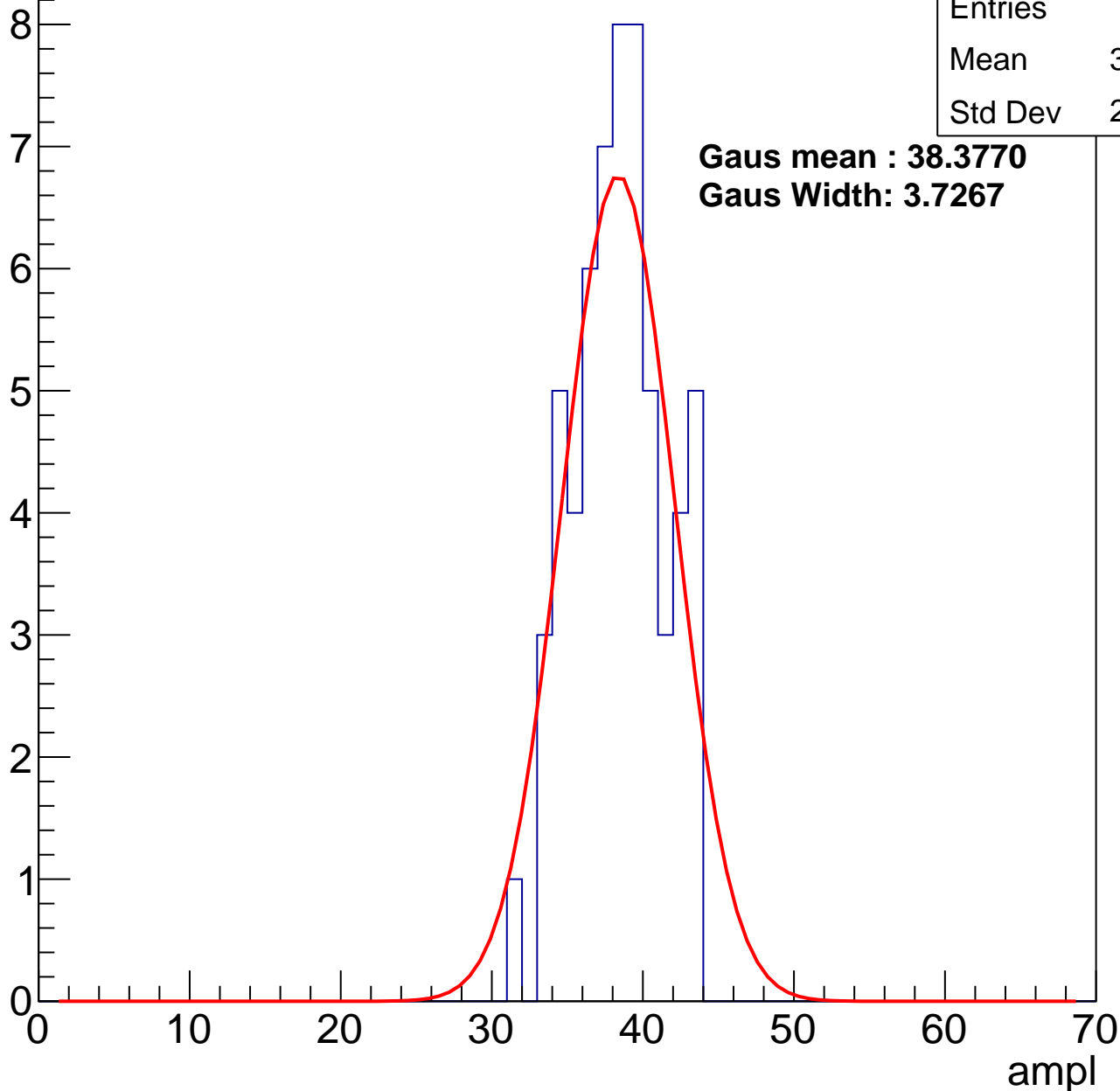
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	37.92
Std Dev	2.953

Gaus mean : 38.3770

Gaus Width: 3.7267



B0L002S, U26-ch72, adc2

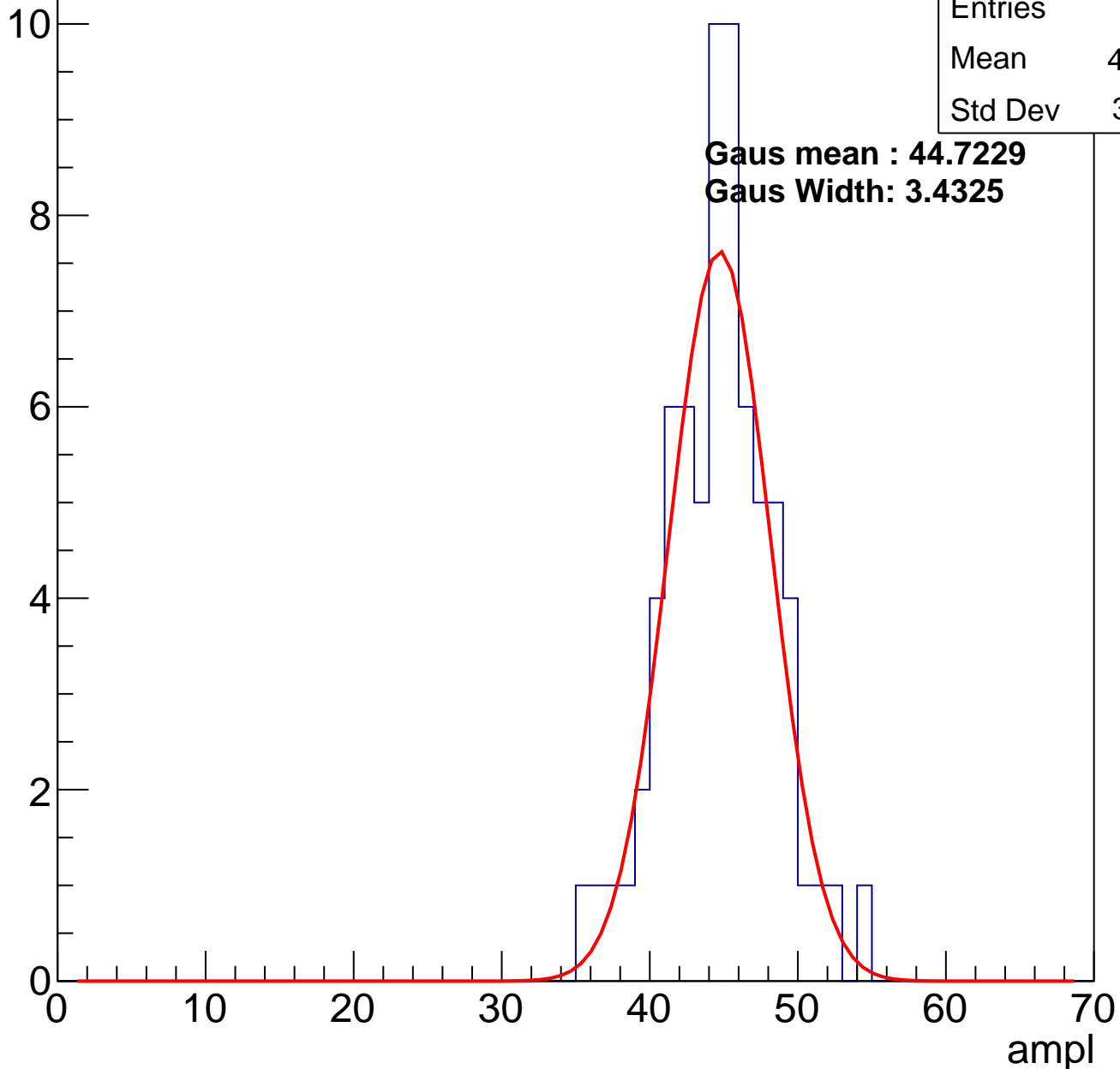
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	44.24
Std Dev	3.621

Gaus mean : 44.7229

Gaus Width: 3.4325

Entry

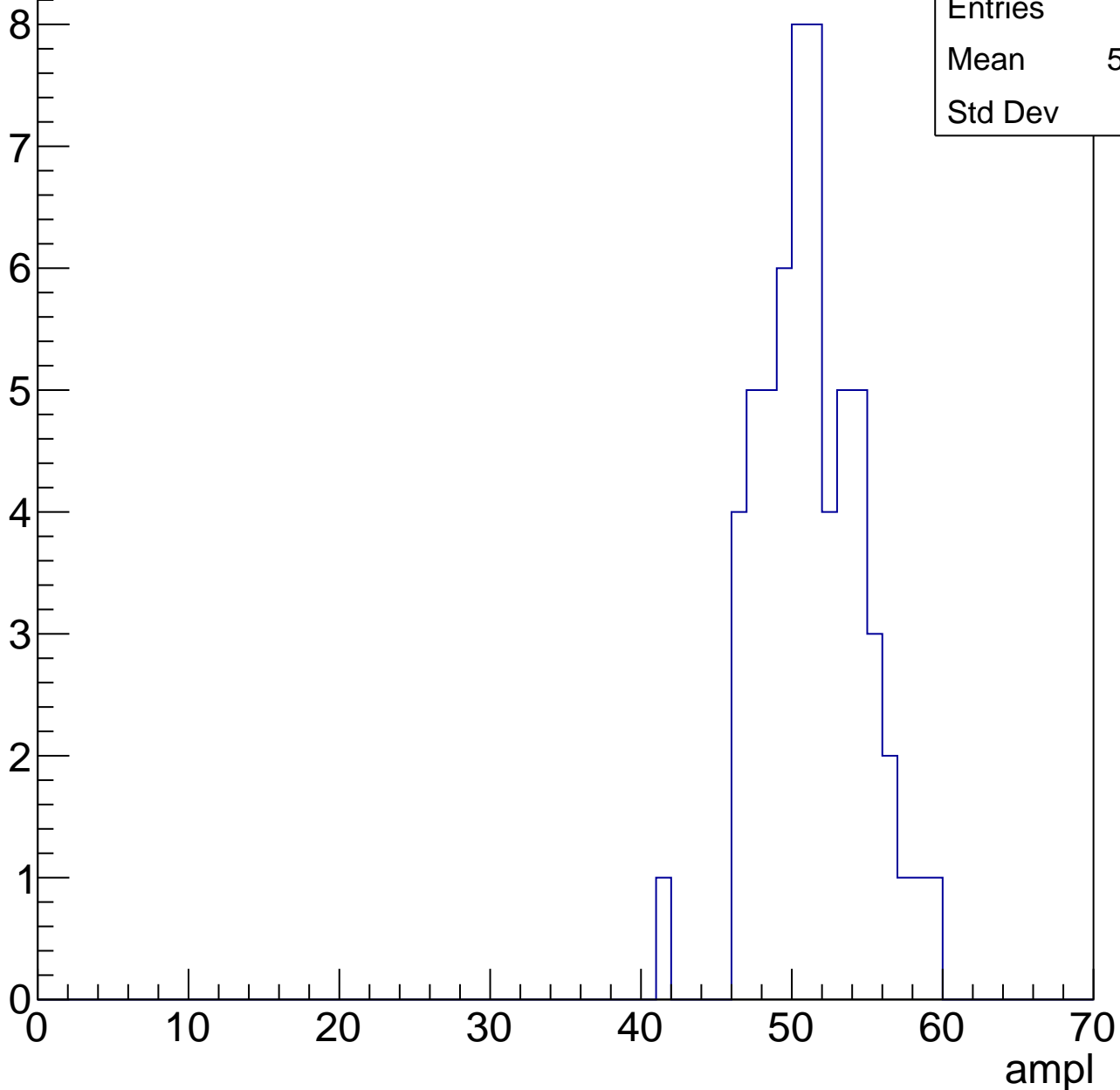


B0L002S, U26-ch72, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

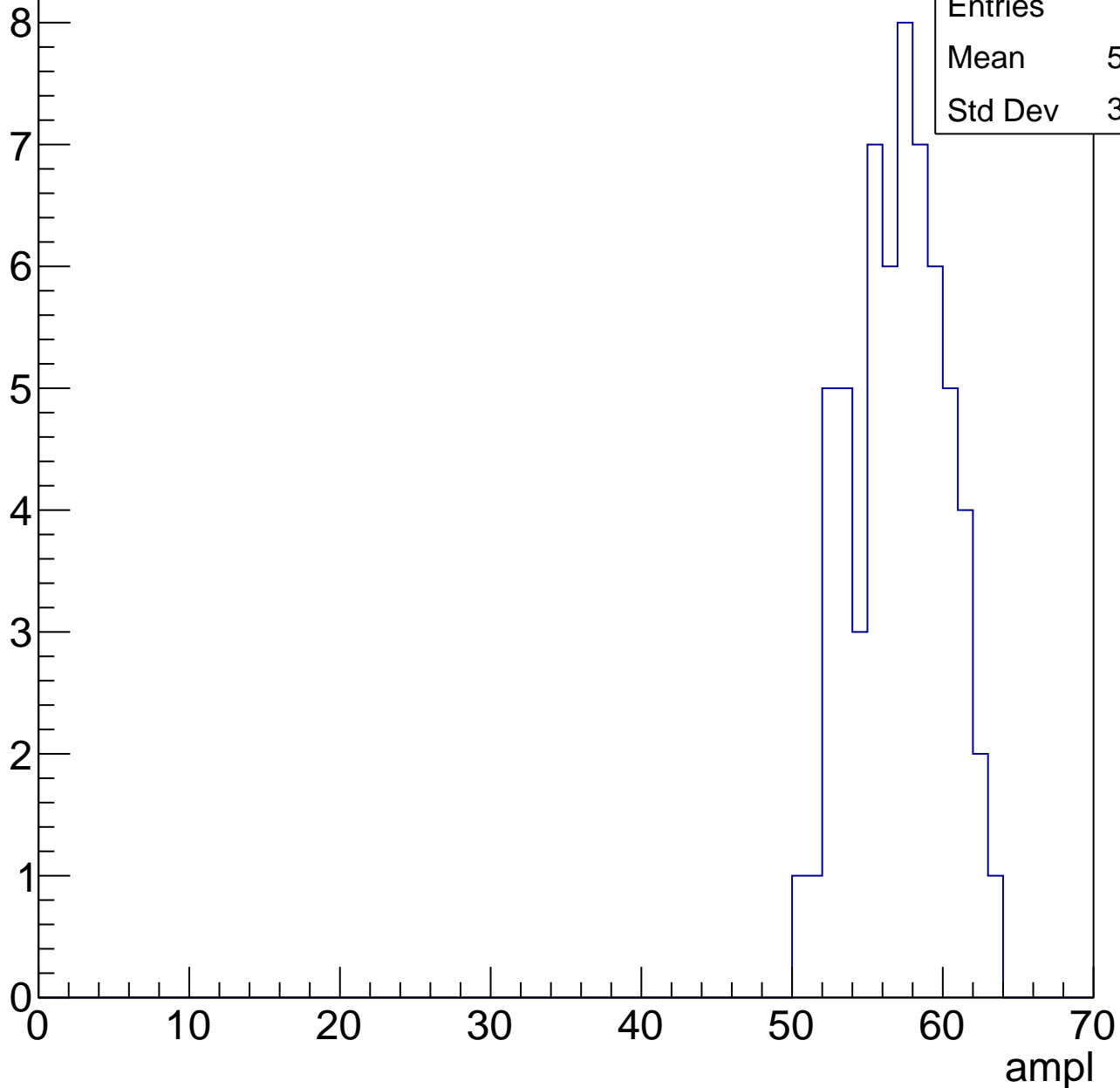
Entries	59
Mean	50.78
Std Dev	3.37



B0L002S, U26-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

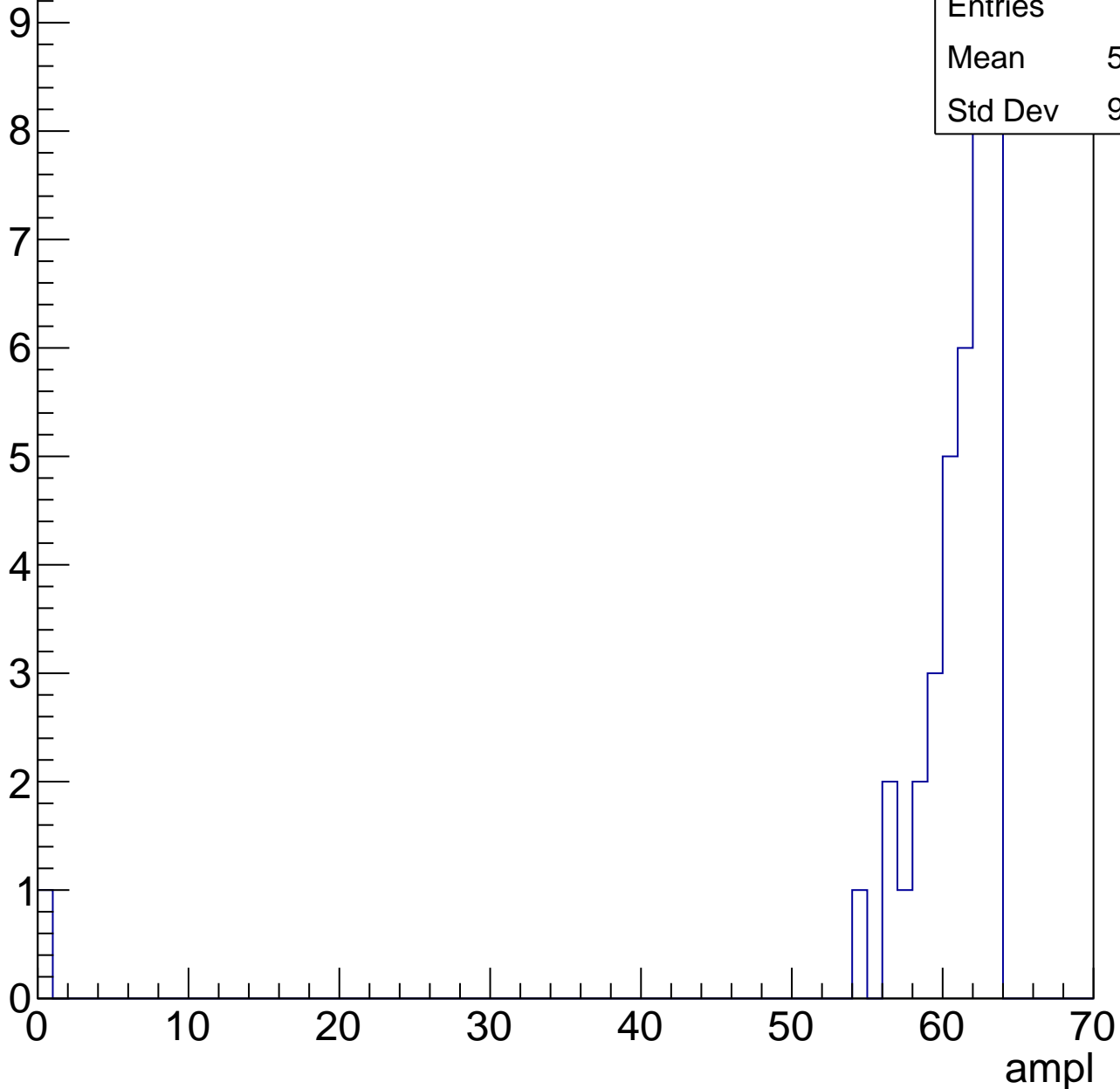


B0L002S, U26-ch72, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.08
Std Dev	9.967



B0L002S, U26-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12
Std Dev	12

B0L002S, U26-ch73, adc0

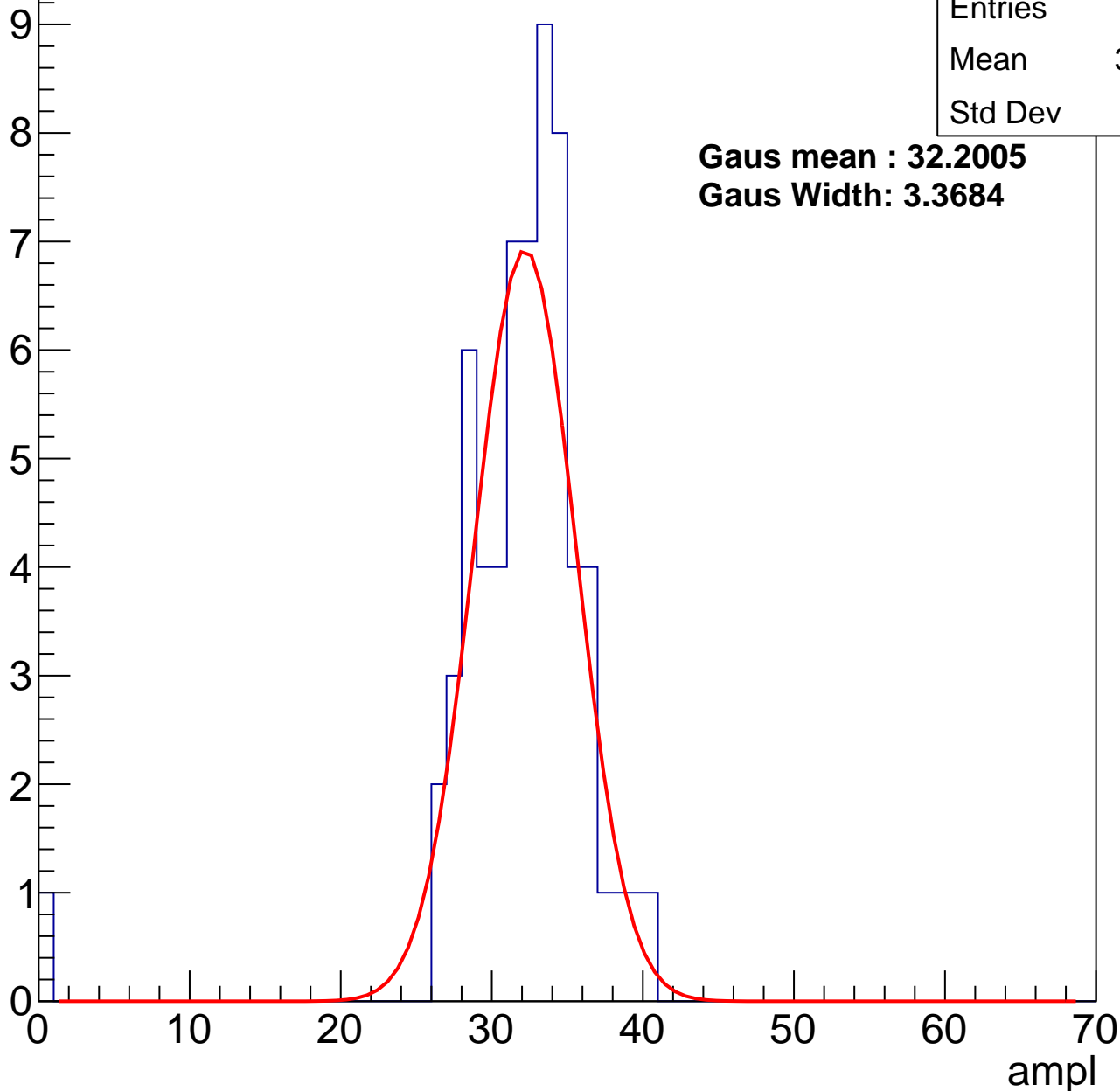
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	31.51
Std Dev	5.08

Gaus mean : 32.2005

Gaus Width: 3.3684



B0L002S, U26-ch73, adc1

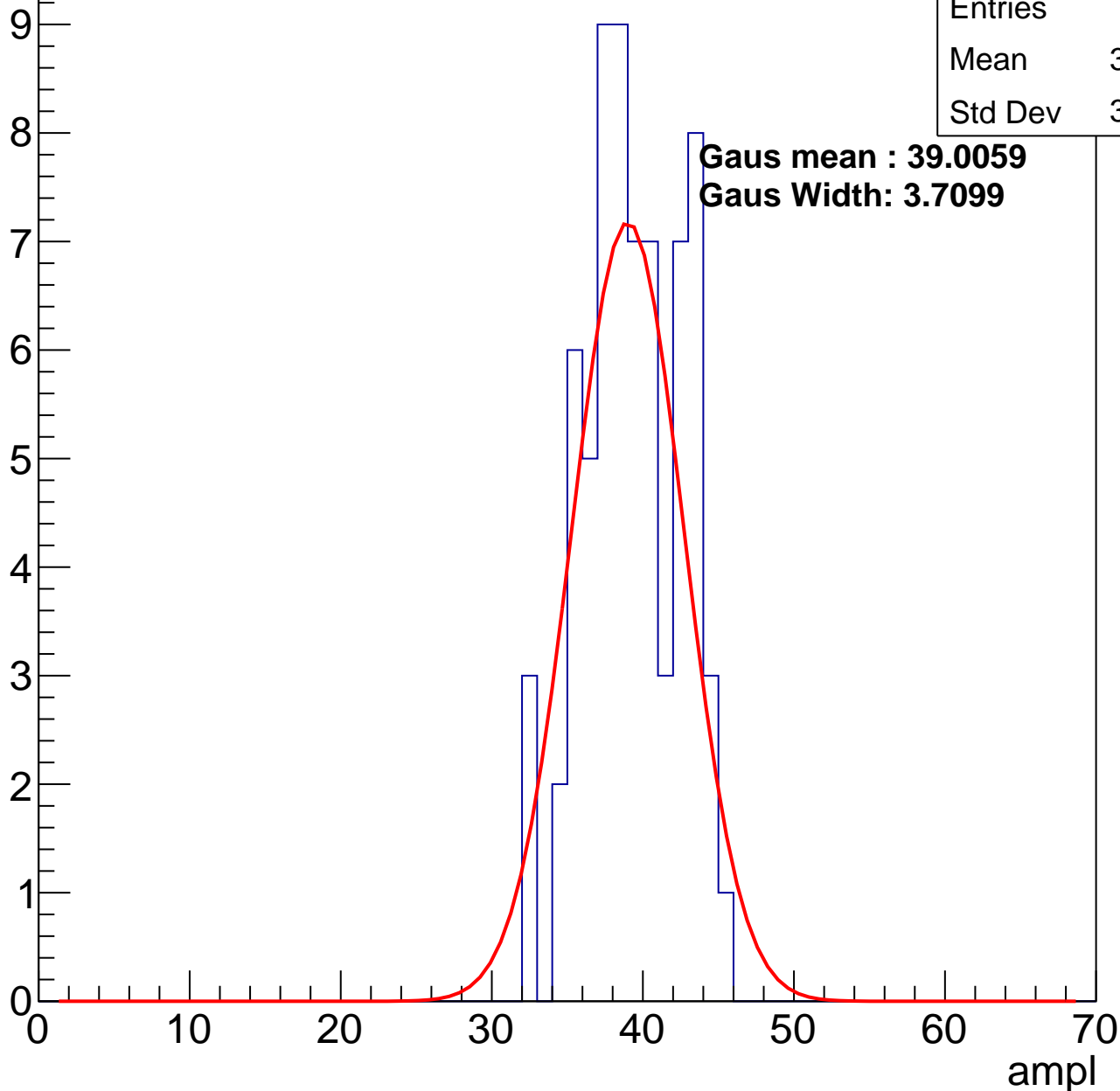
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	38.86
Std Dev	3.168

Gaus mean : 39.0059

Gaus Width: 3.7099



B0L002S, U26-ch73, adc2

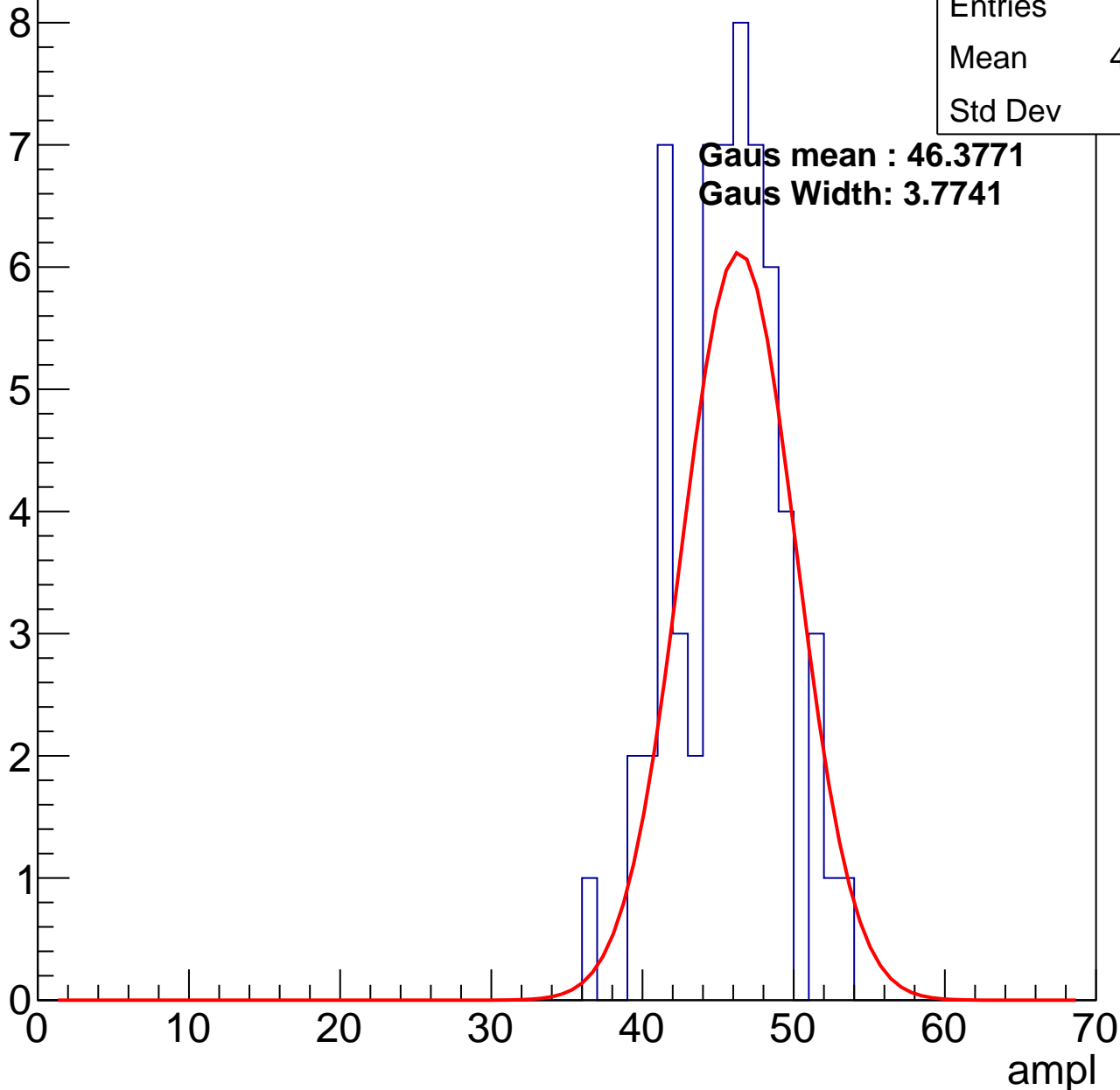
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	45.16
Std Dev	3.46

Gaus mean : 46.3771

Gaus Width: 3.7741



B0L002S, U26-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

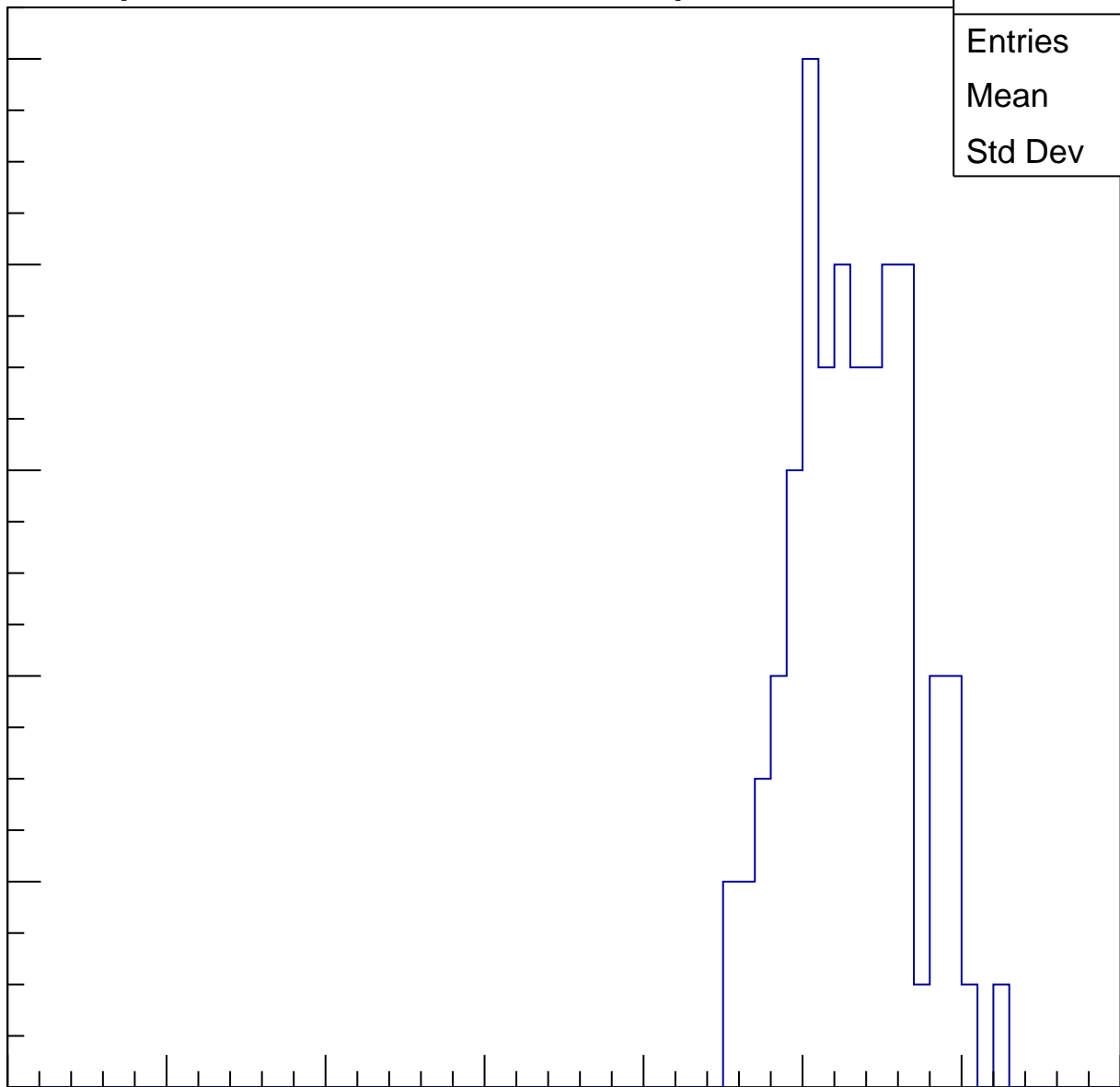
Entries	83
Mean	52.6
Std Dev	3.712

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

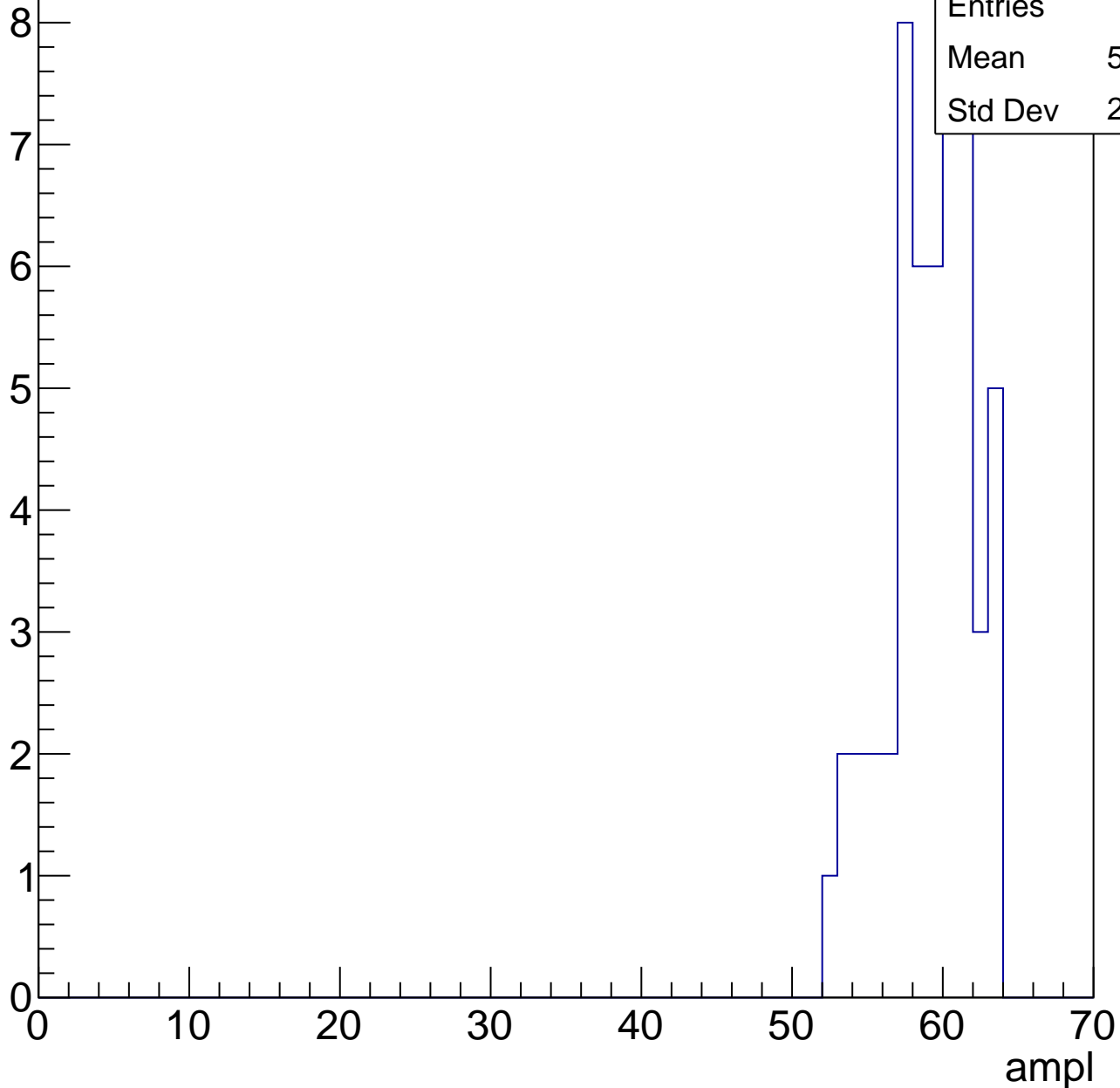
ampl



B0L002S, U26-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

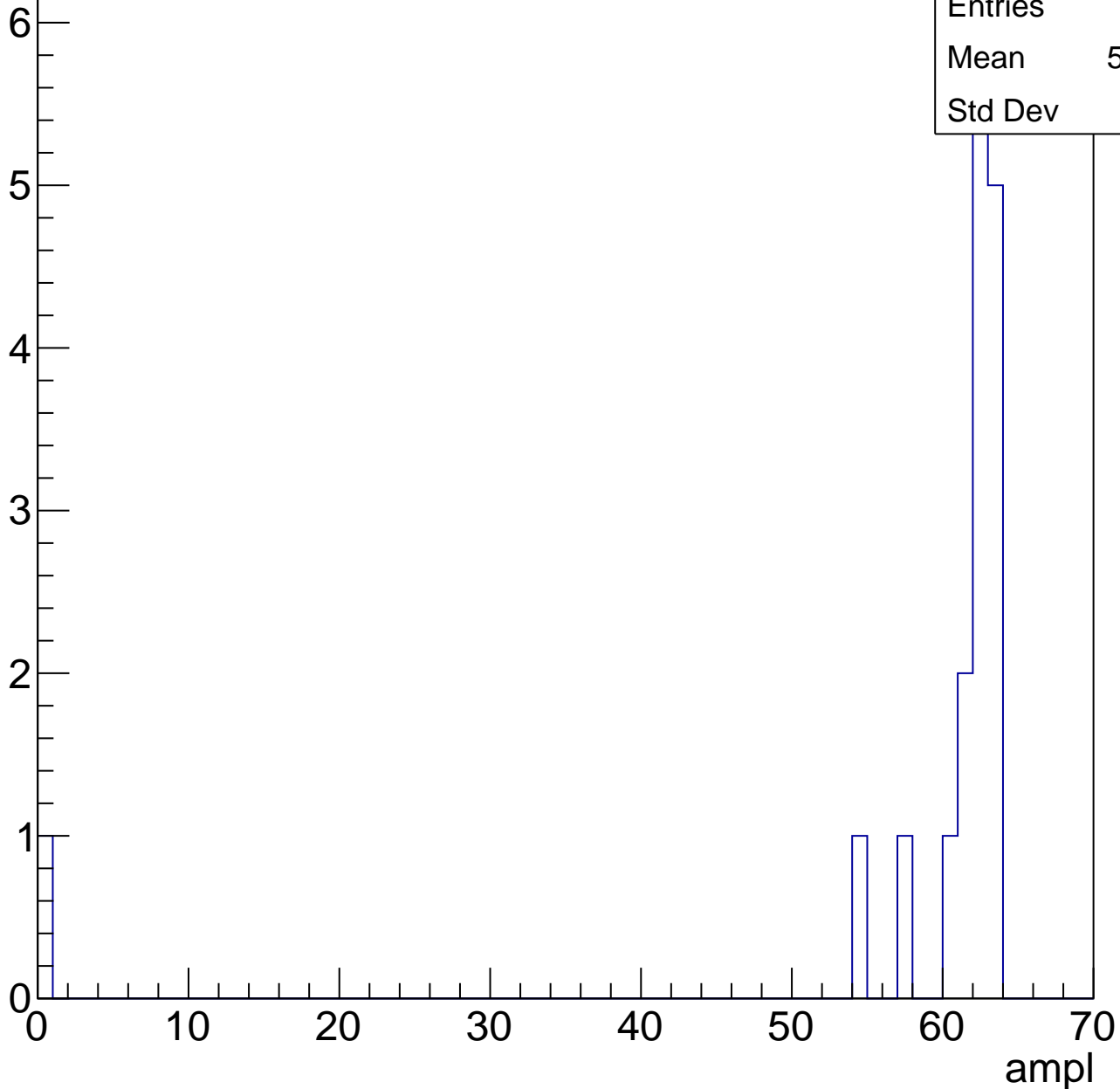


B0L002S, U26-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	57.65
Std Dev	14.6



B0L002S, U26-ch73, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U26-ch74, adc0

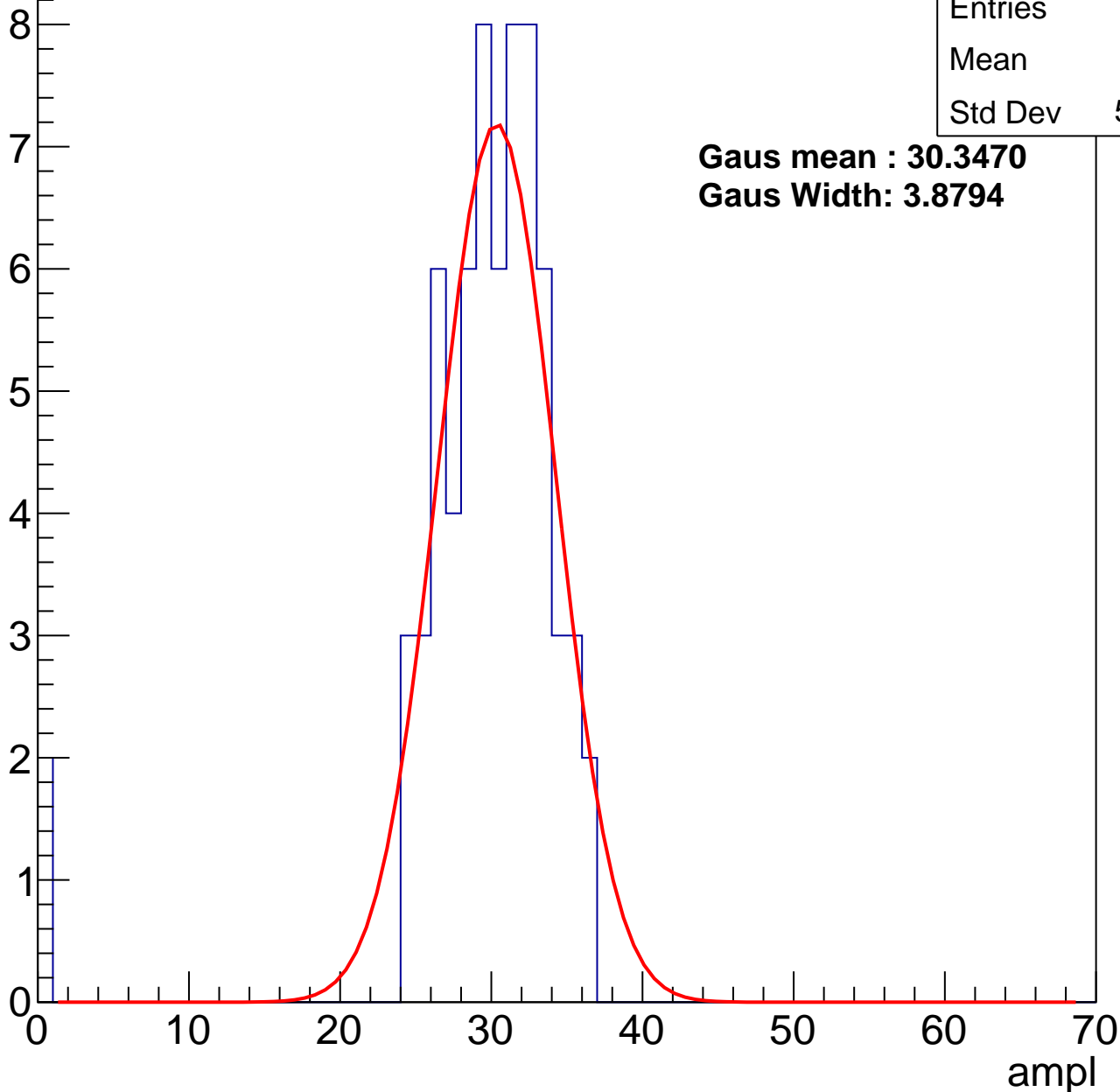
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29
Std Dev	5.901

Gaus mean : 30.3470

Gaus Width: 3.8794



B0L002S, U26-ch74, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	36.4
Std Dev	3.611

Gaus mean : 37.0530

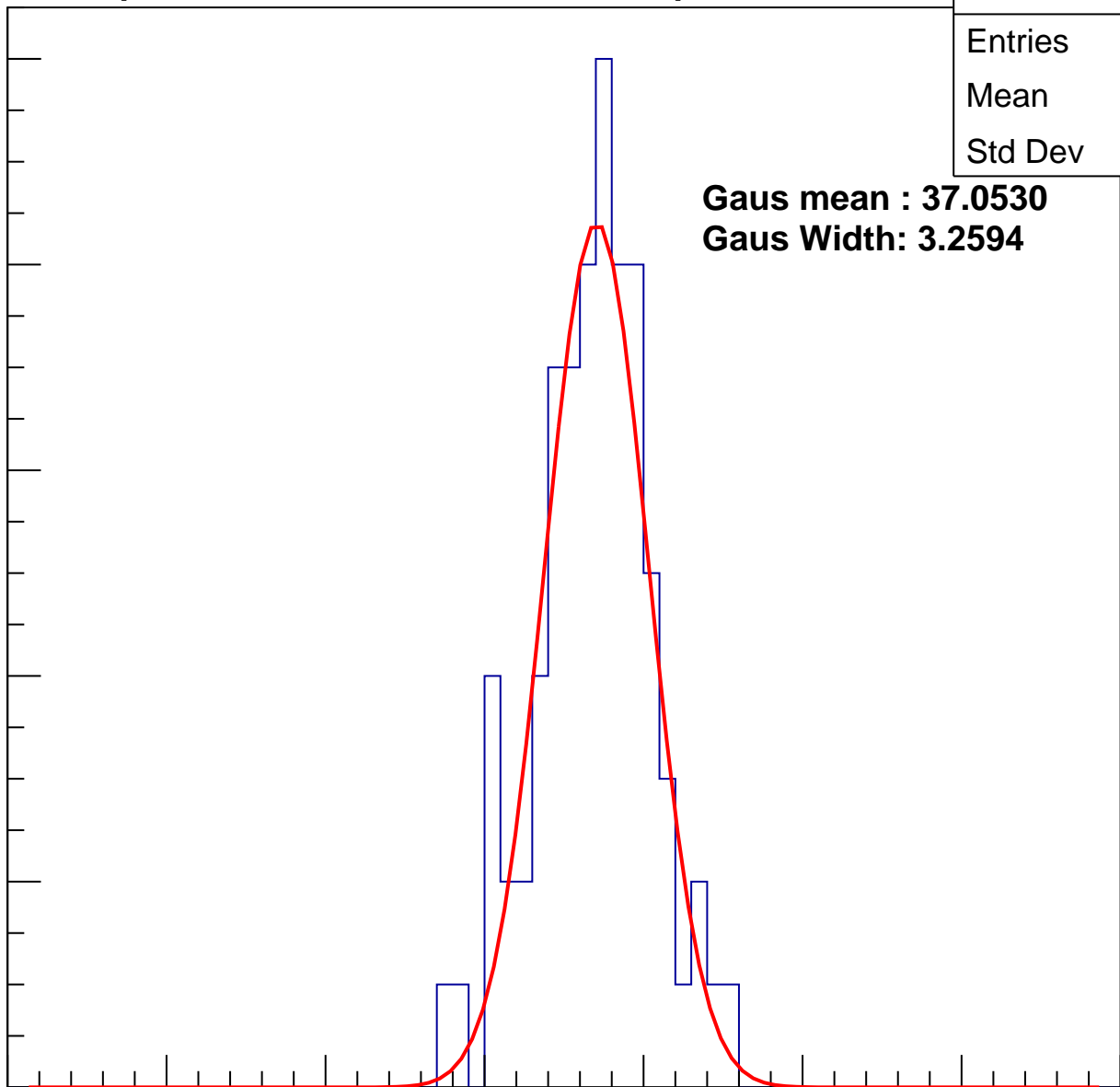
Gaus Width: 3.2594

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch74, adc2

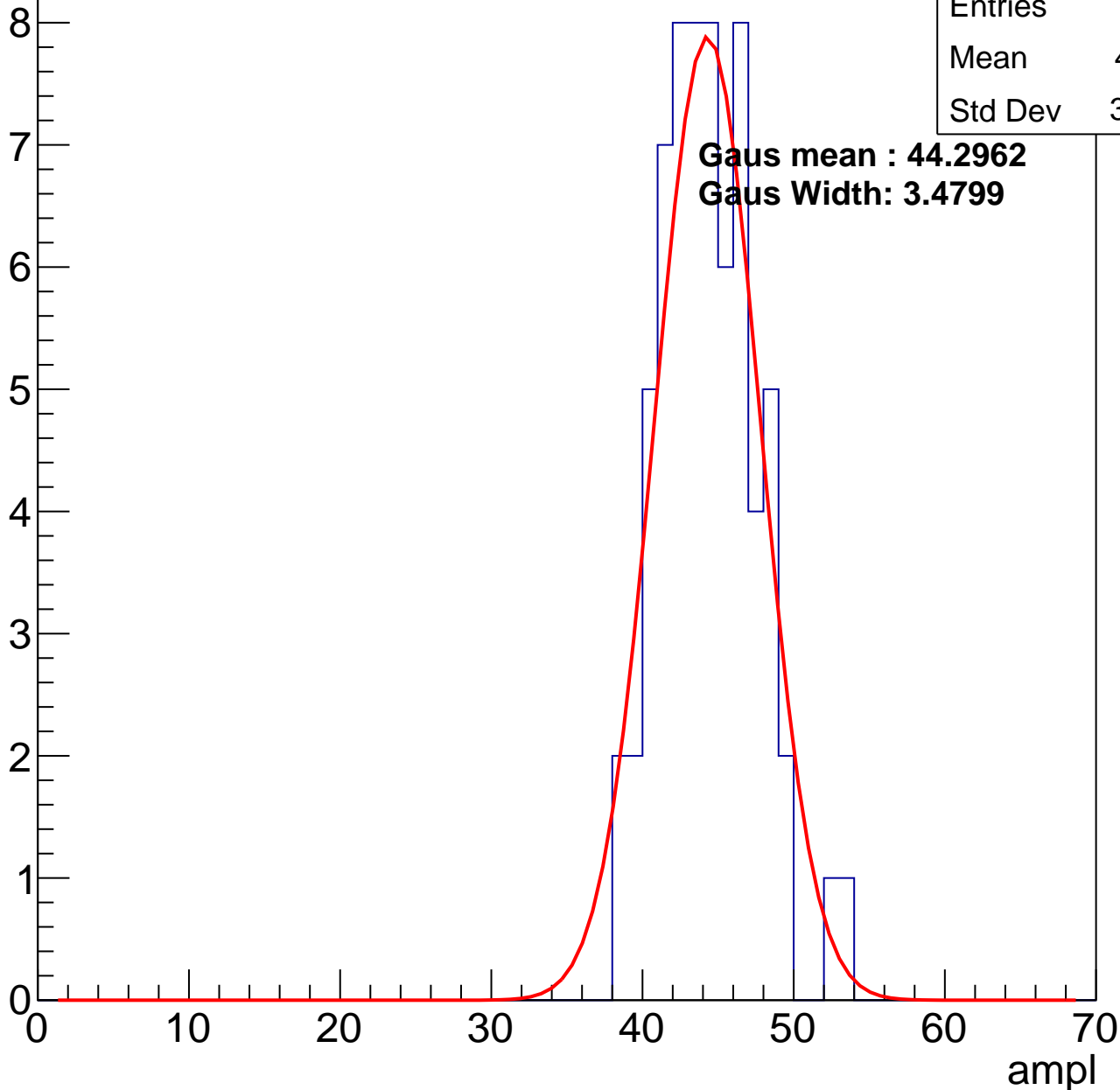
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.91
Std Dev	3.123

Gaus mean : 44.2962

Gaus Width: 3.4799



B0L002S, U26-ch74, adc3

calib_packv5_042523_0143.root, FC#8, port C1

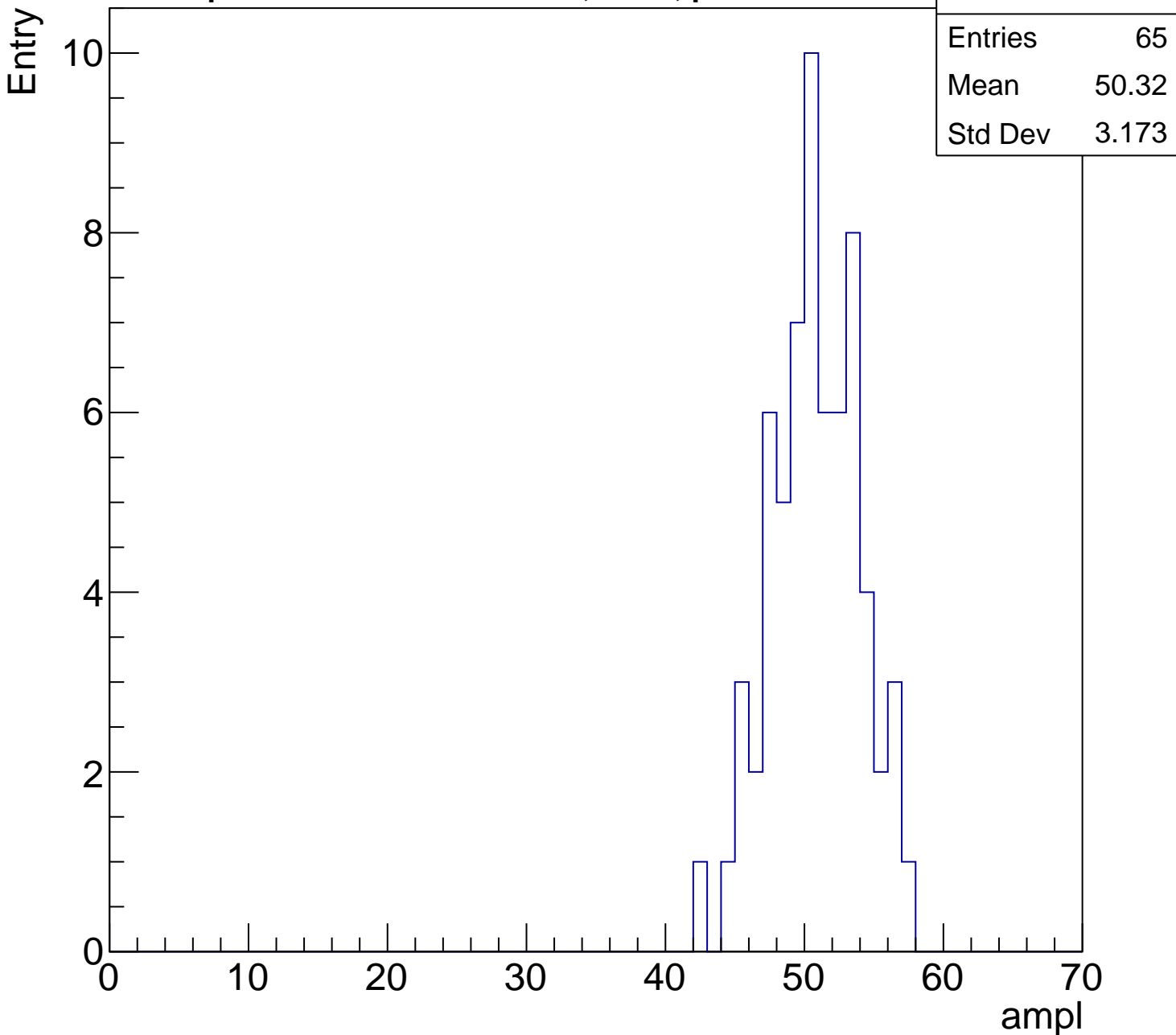
Entries	65
Mean	50.32
Std Dev	3.173

Entry

10
8
6
4
2
0

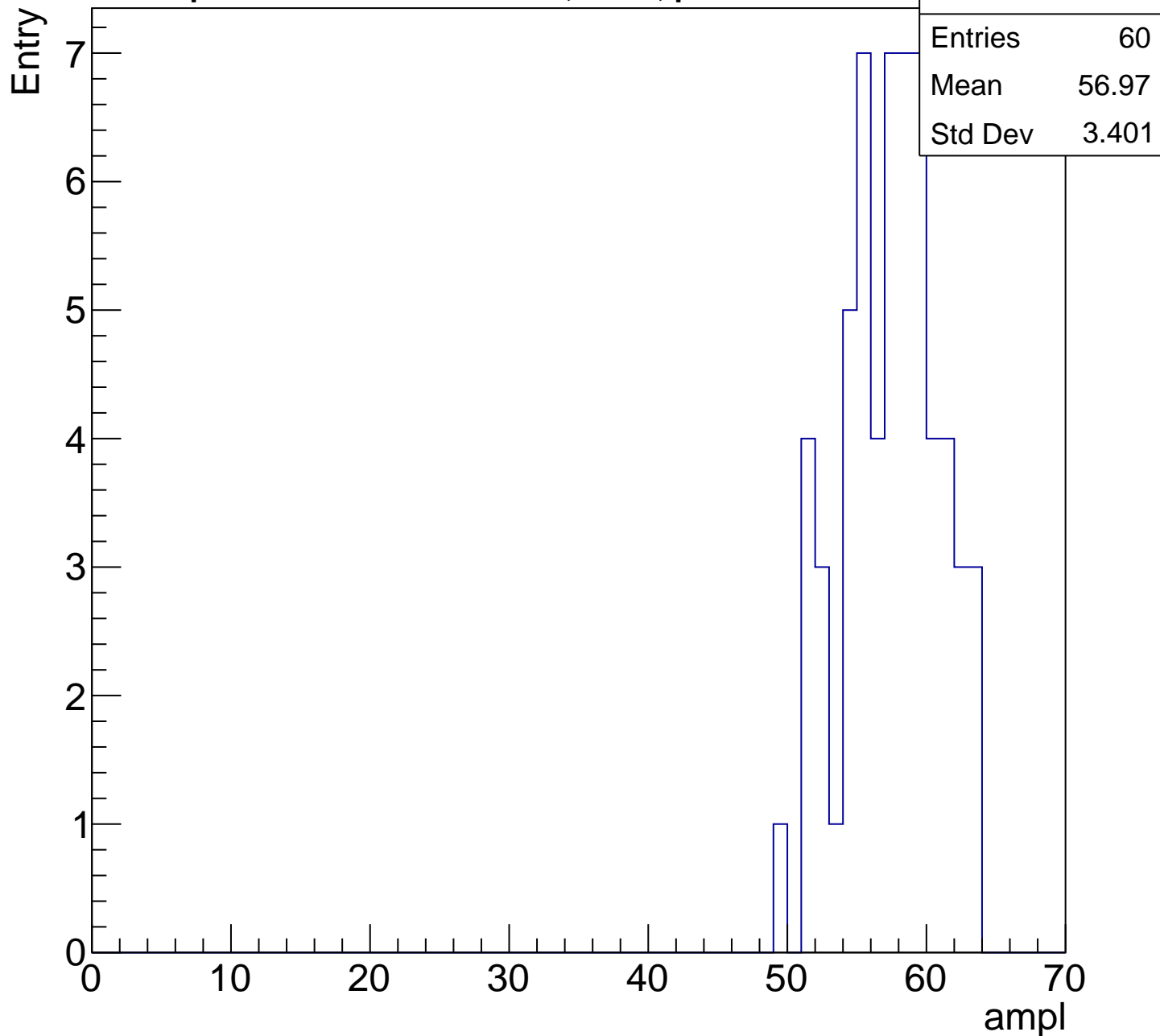
0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U26-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

6
5
4
3
2
1
0

Entries	38
Mean	58.37
Std Dev	9.901

ampl

0

10

20

30

40

50

60

70

B0L002S, U26-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch75, adc0

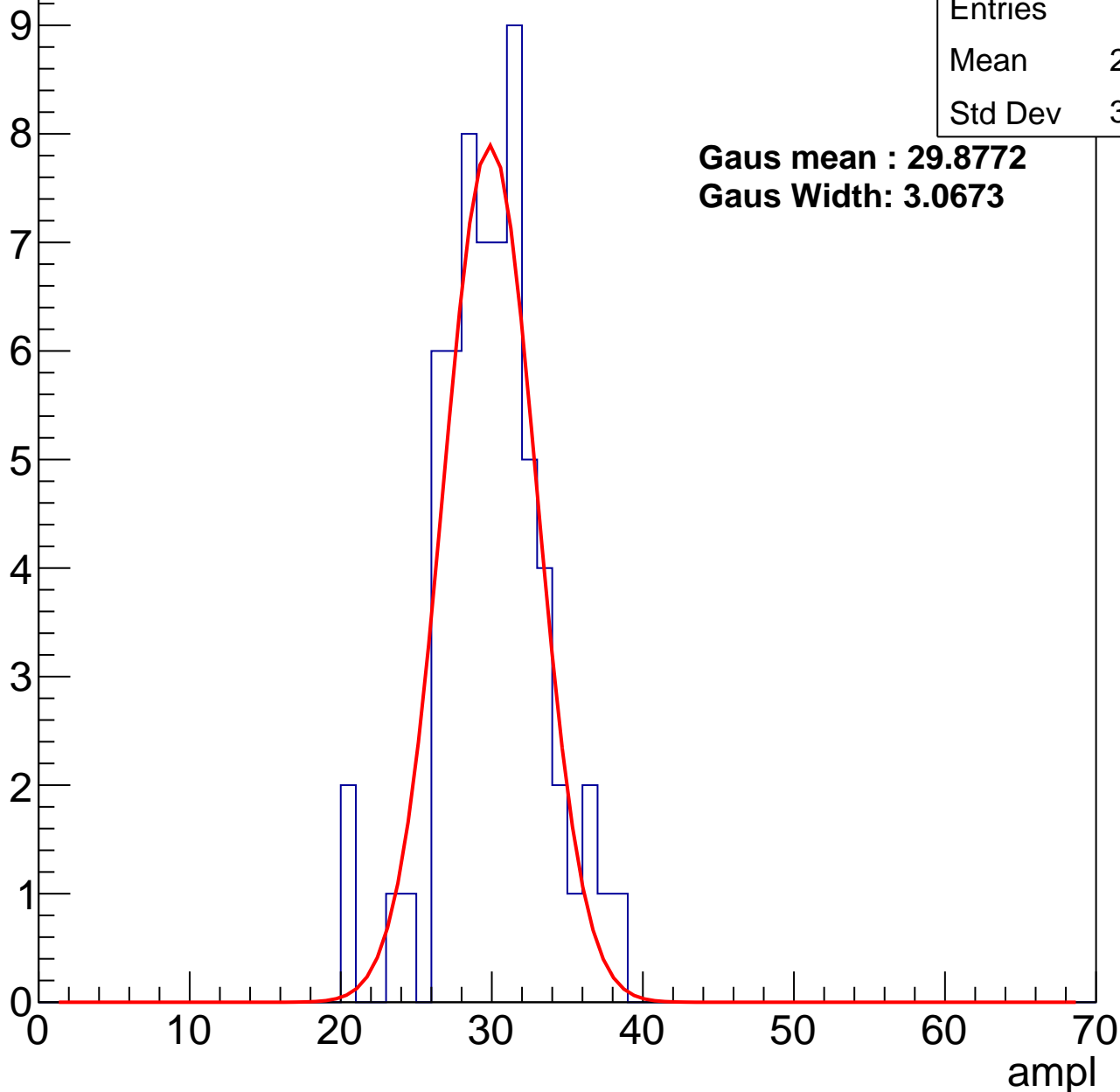
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.57
Std Dev	3.495

Gaus mean : 29.8772

Gaus Width: 3.0673



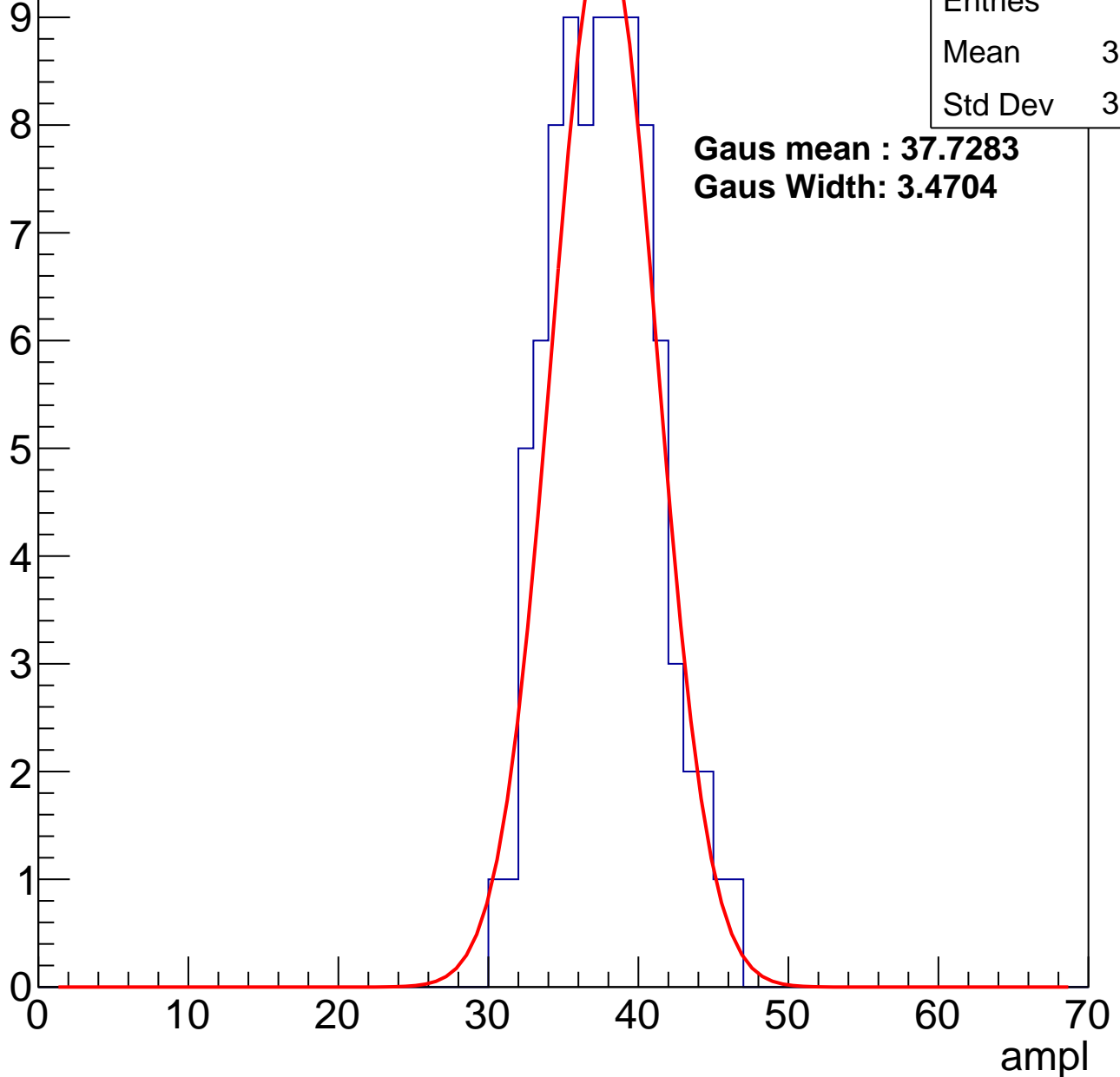
B0L002S, U26-ch75, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	88
Mean	37.24
Std Dev	3.405

Gaus mean : 37.7283
Gaus Width: 3.4704



B0L002S, U26-ch75, adc2

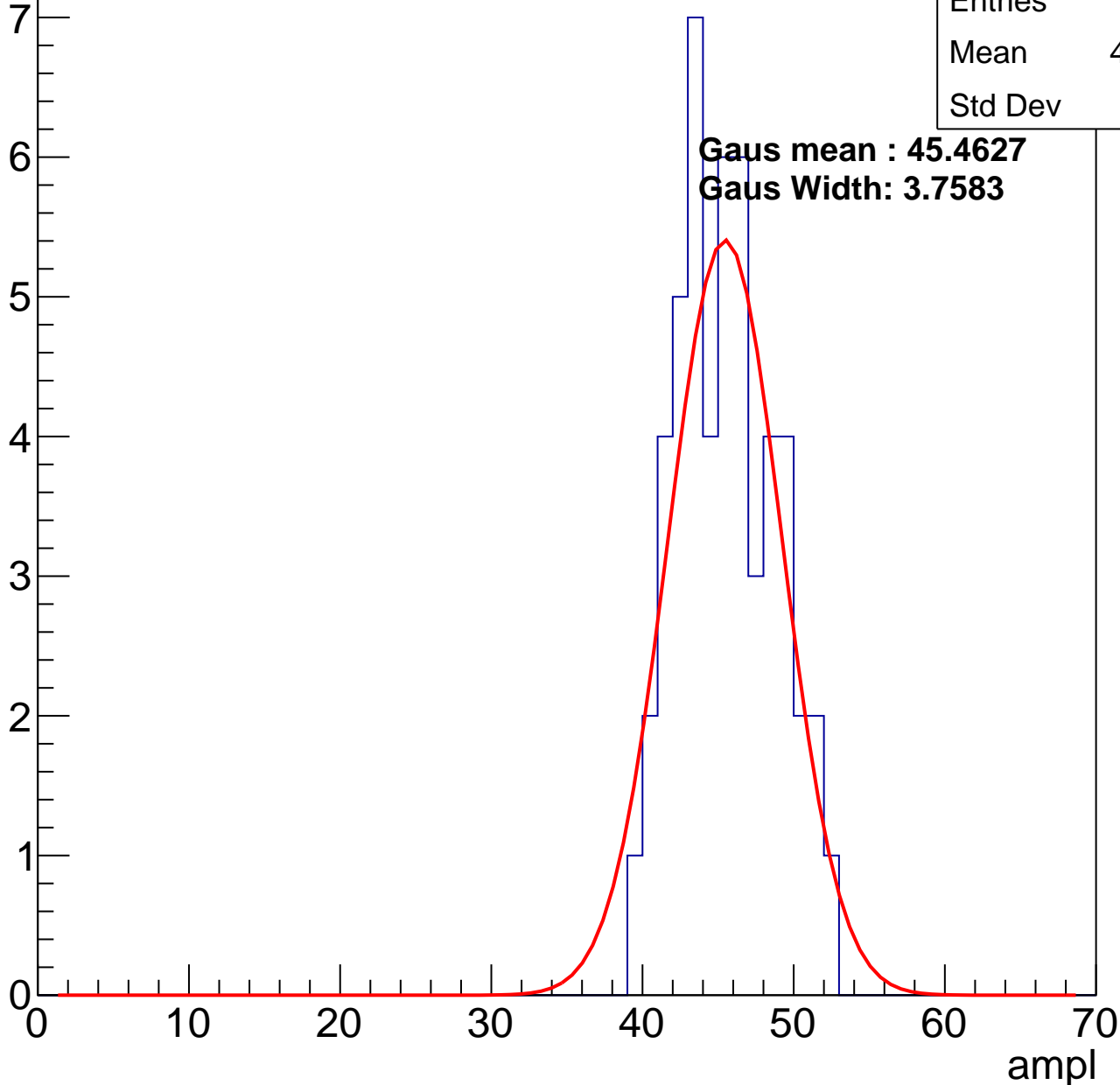
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.08
Std Dev	3.18

Gaus mean : 45.4627

Gaus Width: 3.7583

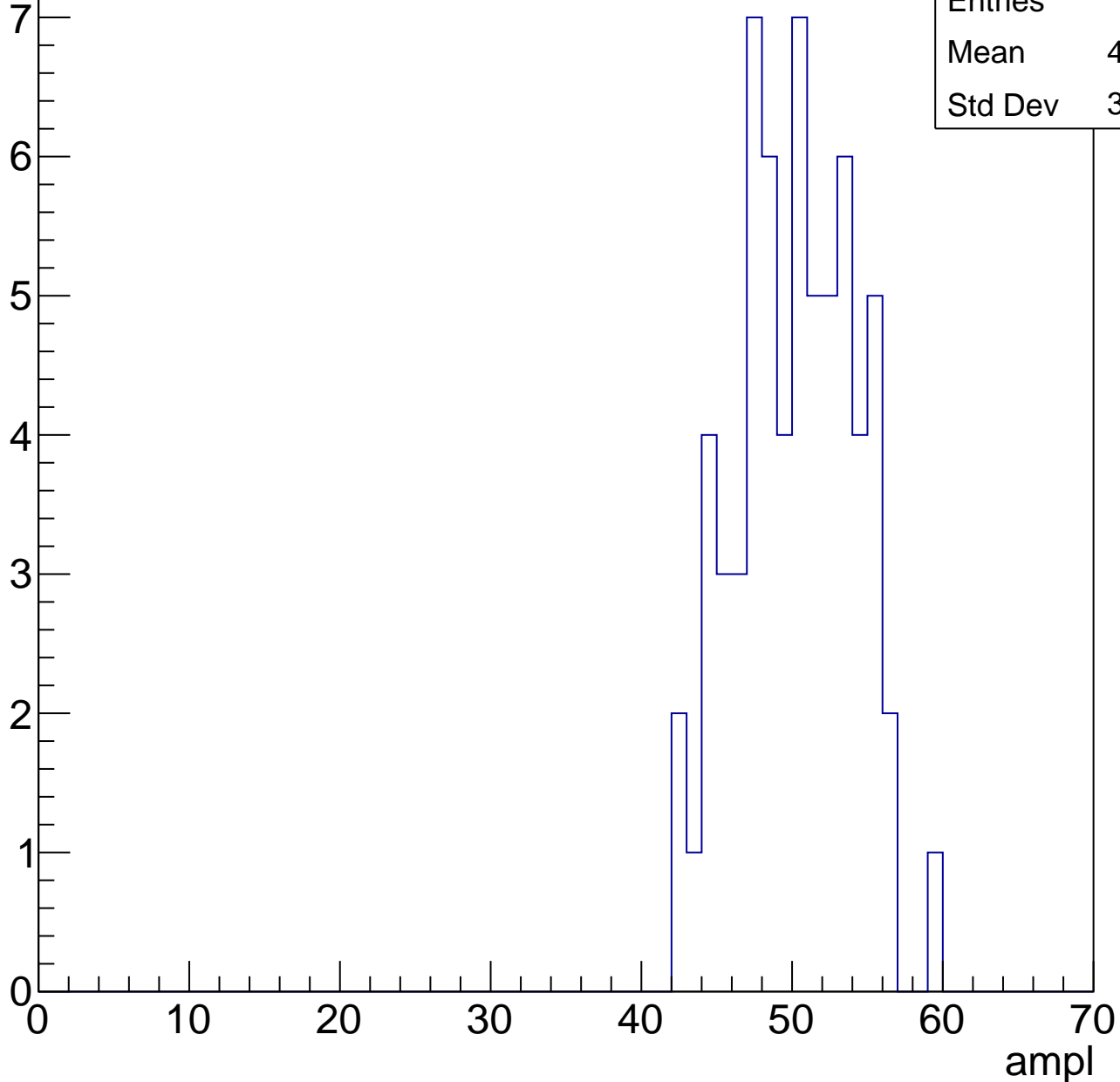


B0L002S, U26-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.75
Std Dev	3.827

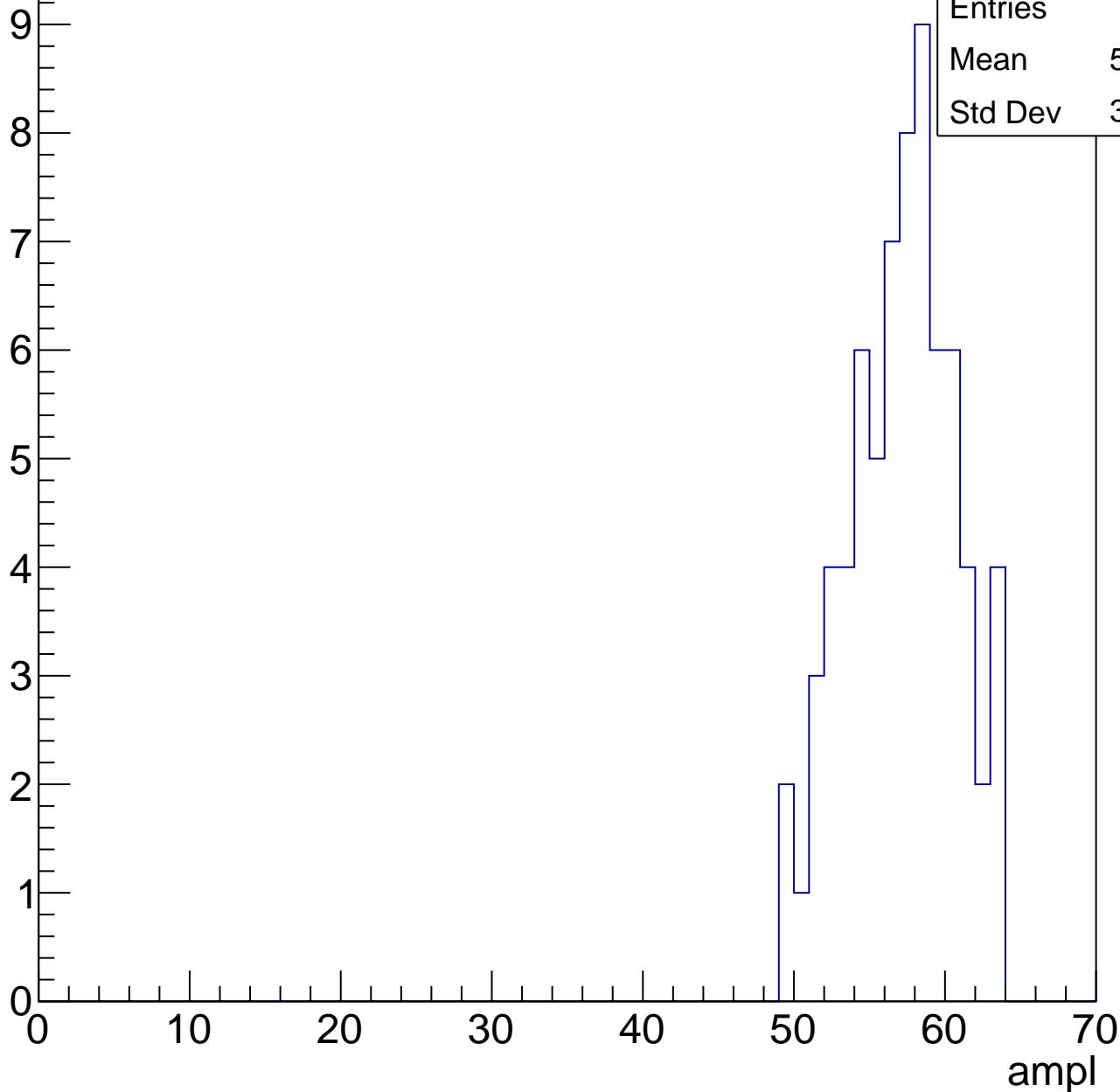


B0L002S, U26-ch75, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	56.68
Std Dev	3.496

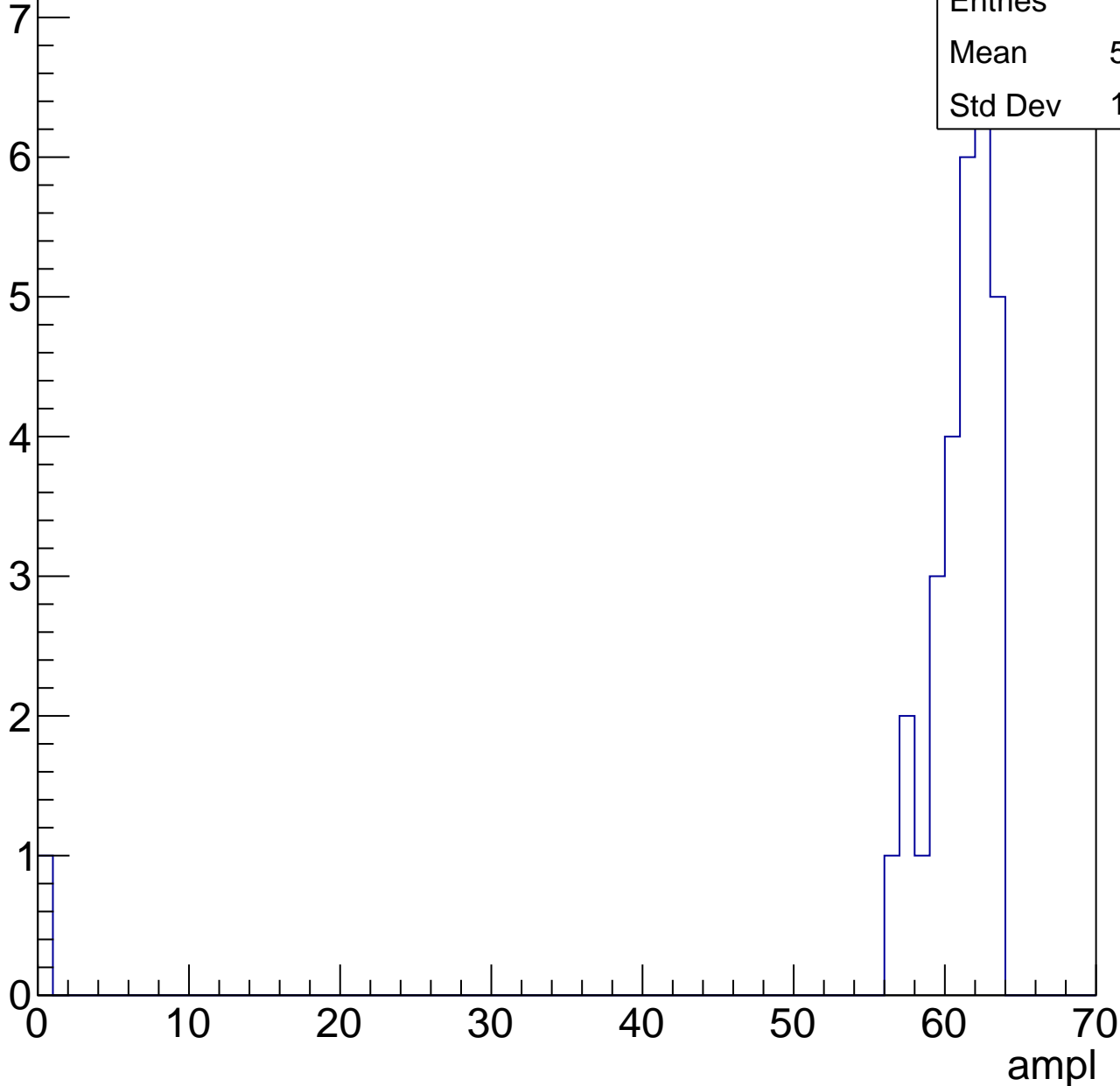


B0L002S, U26-ch75, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.67
Std Dev	11.06



B0L002S, U26-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch76, adc0

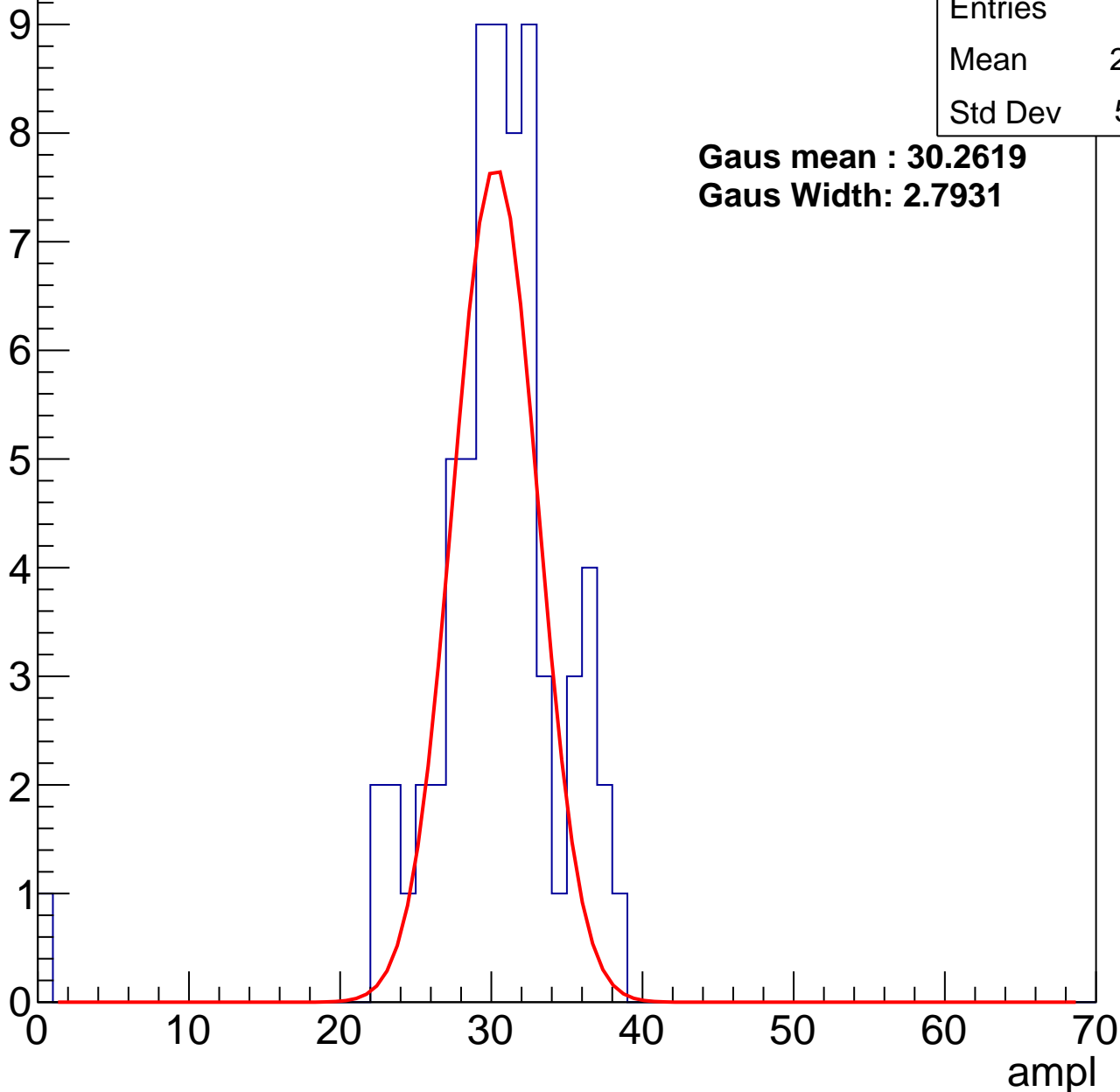
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.74
Std Dev	5.081

Gaus mean : 30.2619

Gaus Width: 2.7931



B0L002S, U26-ch76, adc1

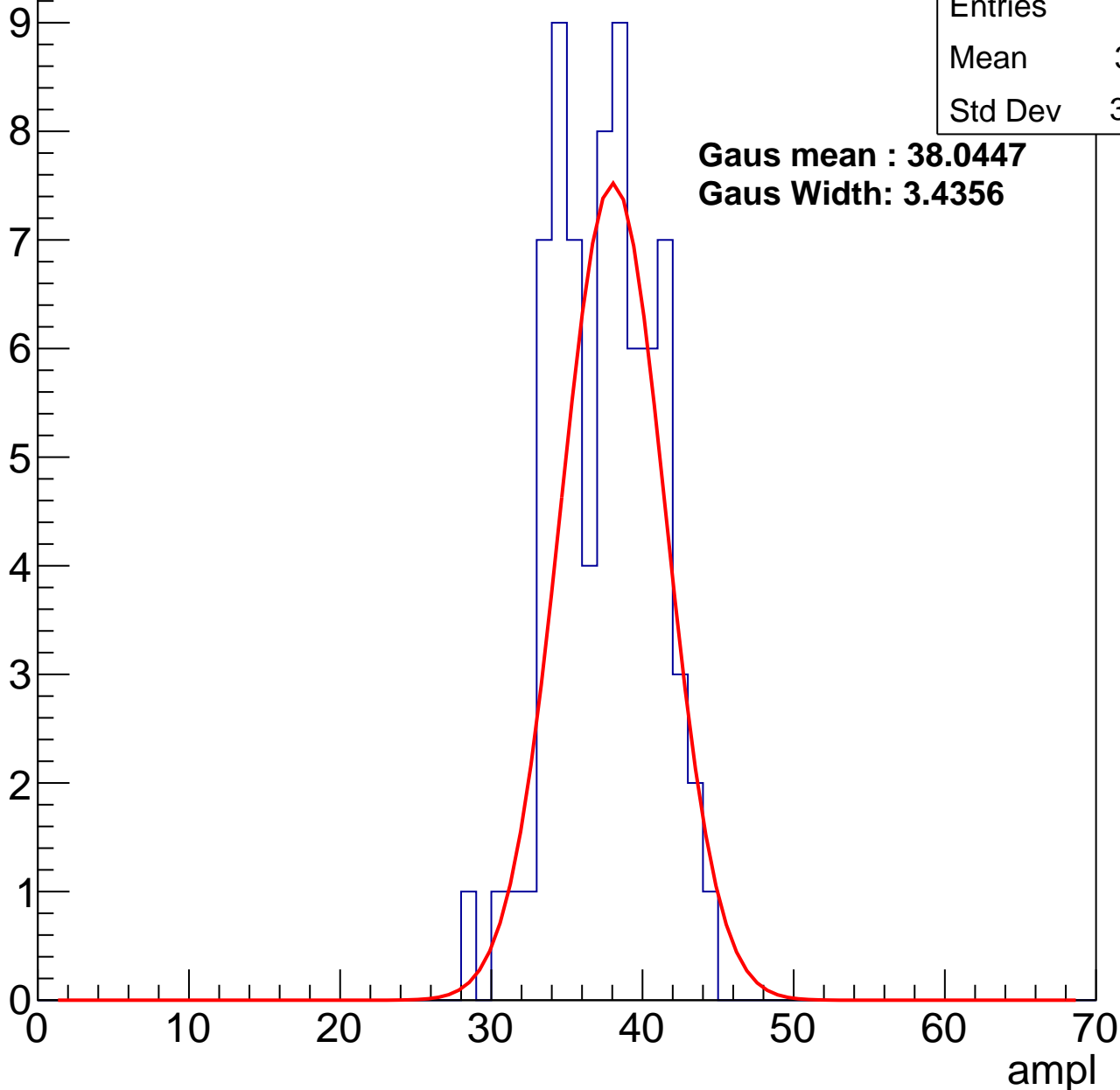
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.01
Std Dev	3.333

Gaus mean : 38.0447

Gaus Width: 3.4356



B0L002S, U26-ch76, adc2

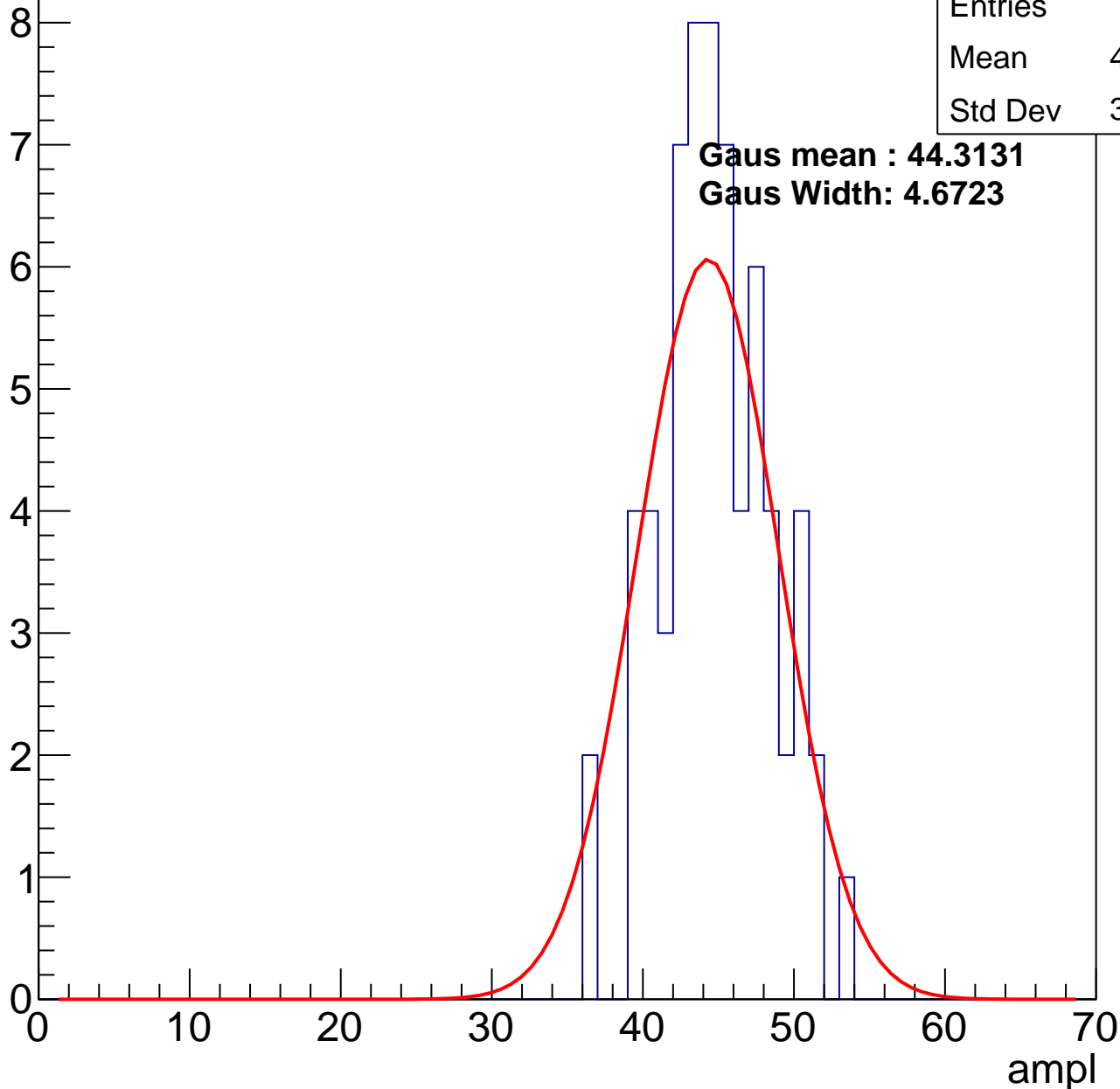
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.35
Std Dev	3.612

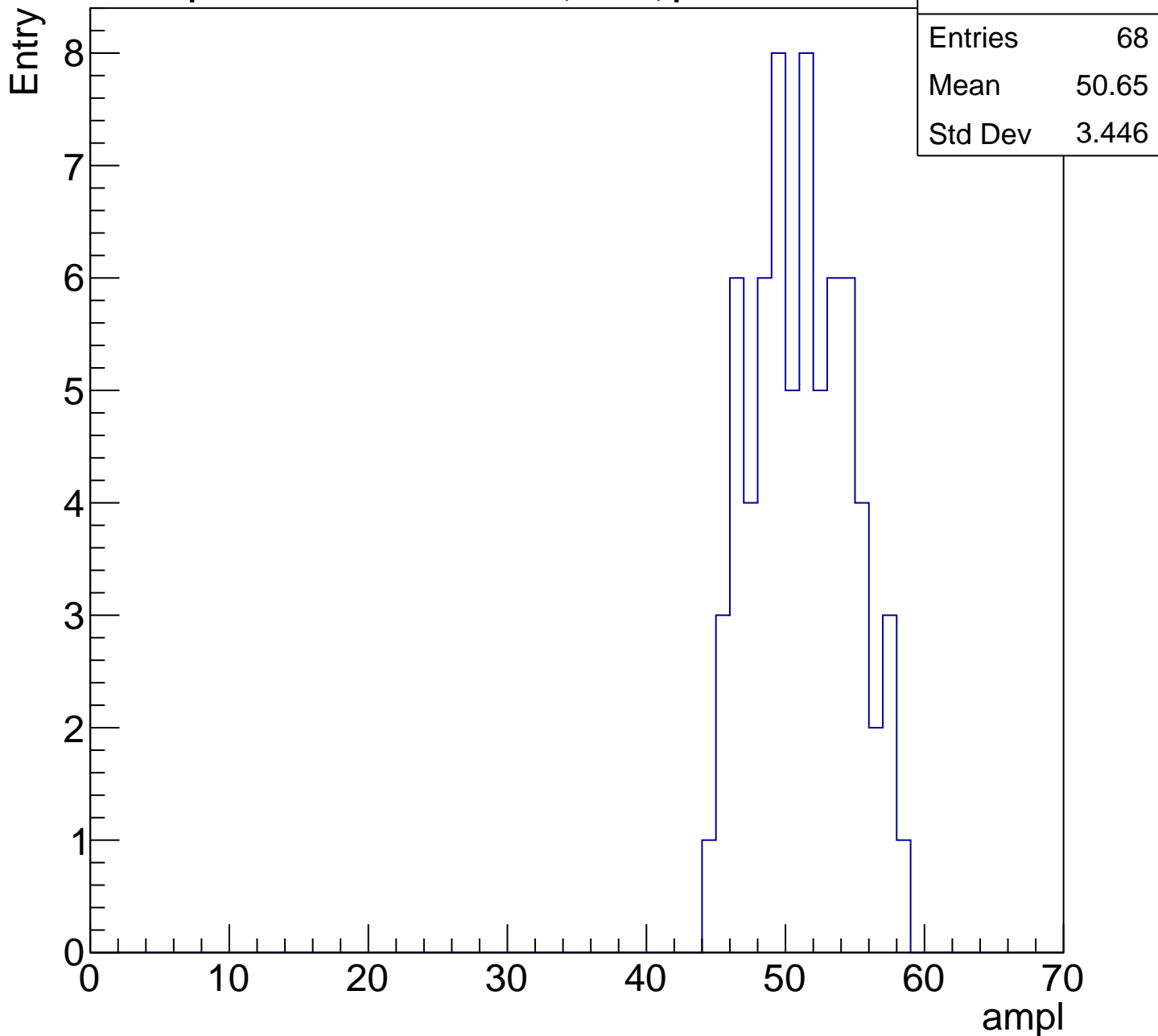
Gaus mean : 44.3131

Gaus Width: 4.6723



B0L002S, U26-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

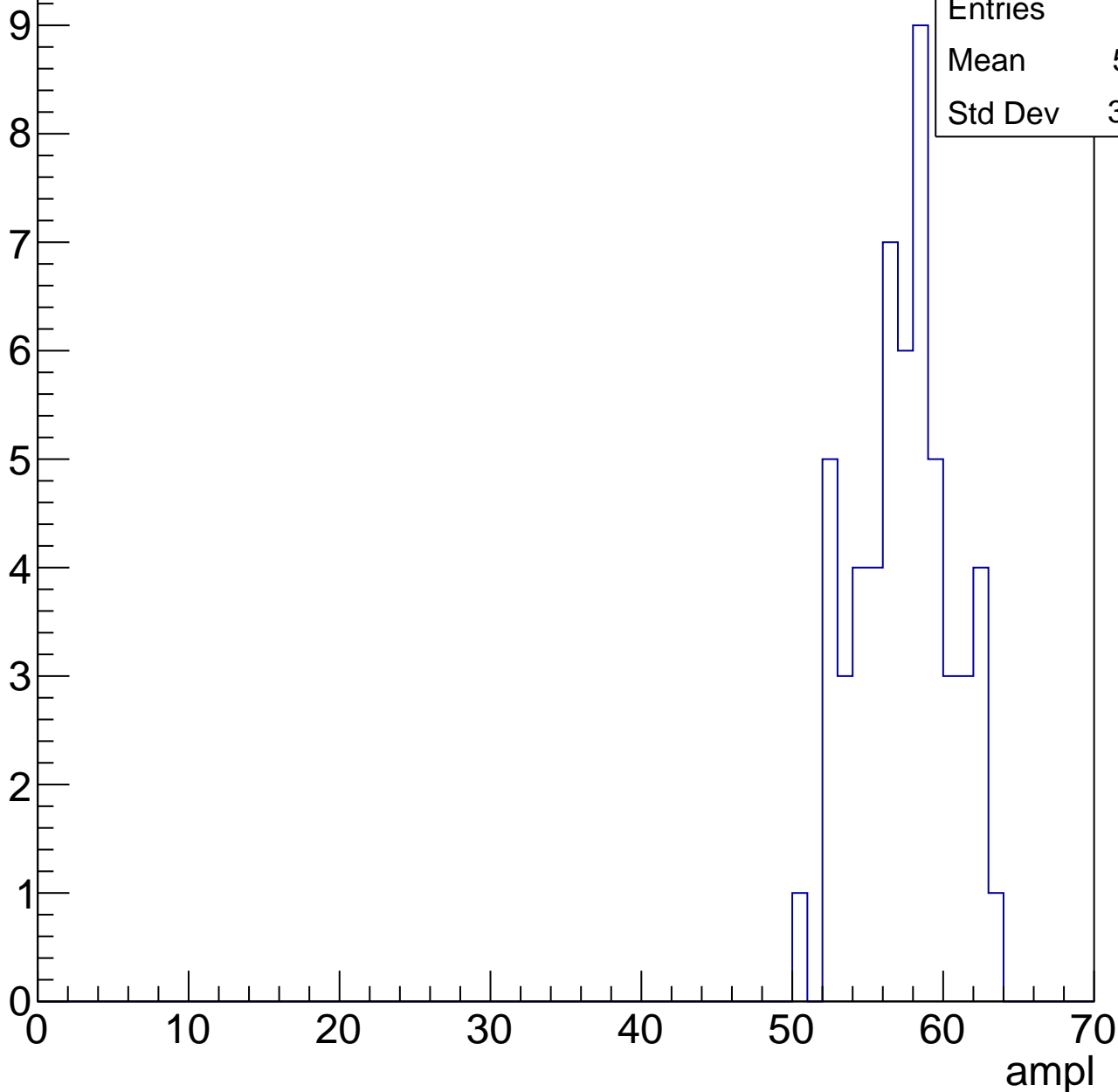


B0L002S, U26-ch76, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.91
Std Dev	3.076

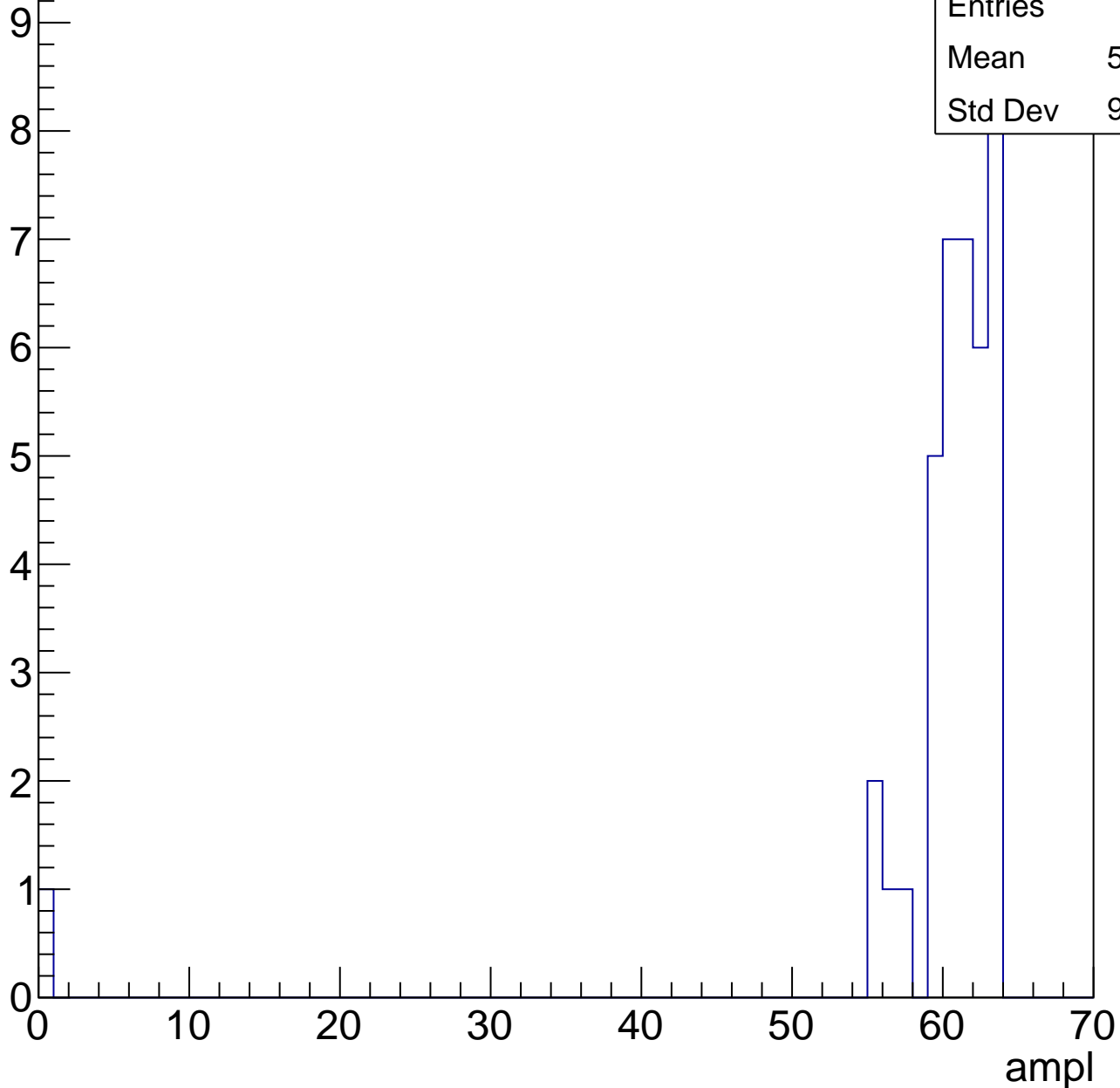


B0L002S, U26-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.08
Std Dev	9.817



B0L002S, U26-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch77, adc0

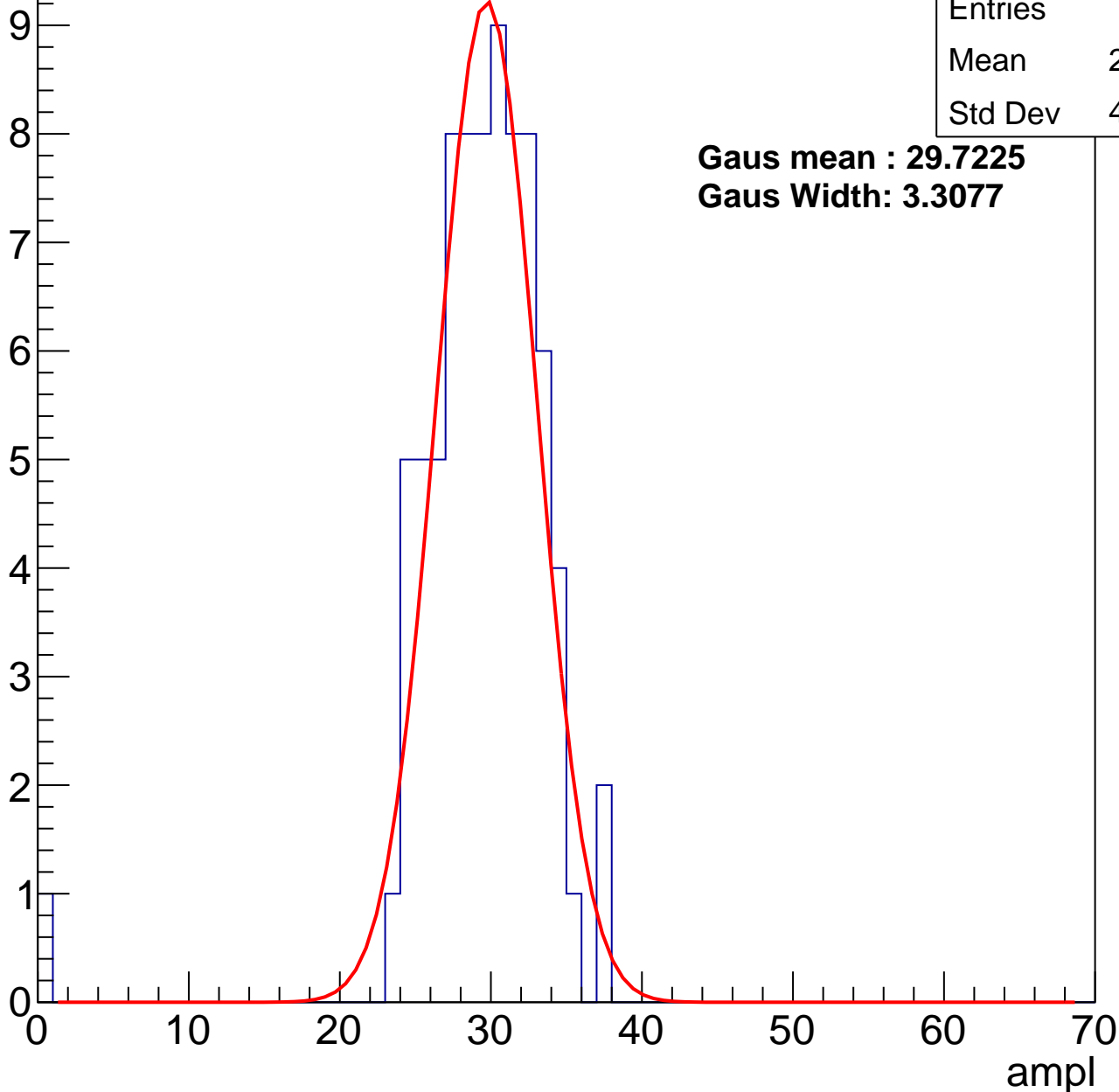
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	28.95
Std Dev	4.556

Gaus mean : 29.7225

Gaus Width: 3.3077



B0L002S, U26-ch77, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	36.55
Std Dev	3.267

Gaus mean : 37.4405

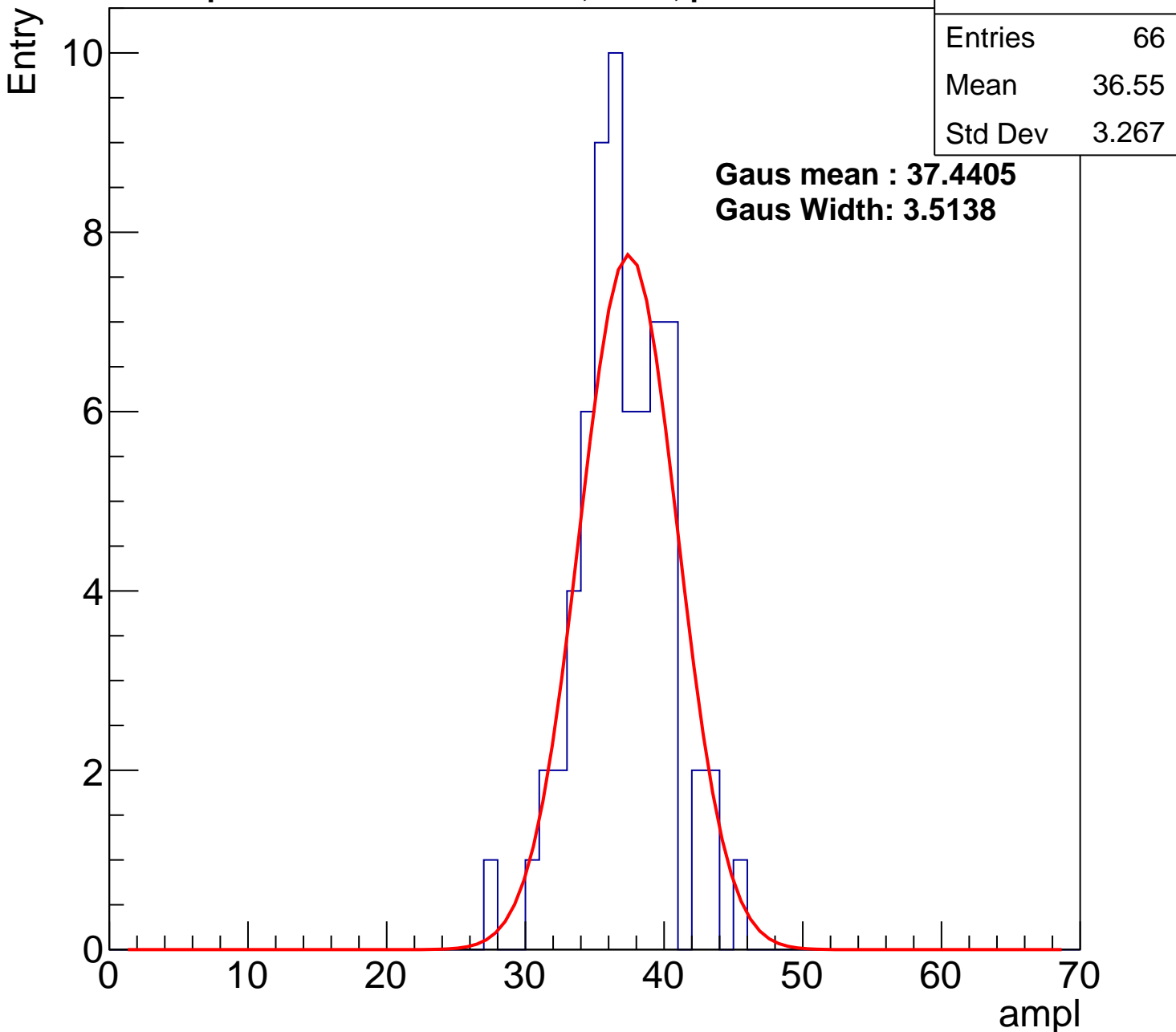
Gaus Width: 3.5138

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch77, adc2

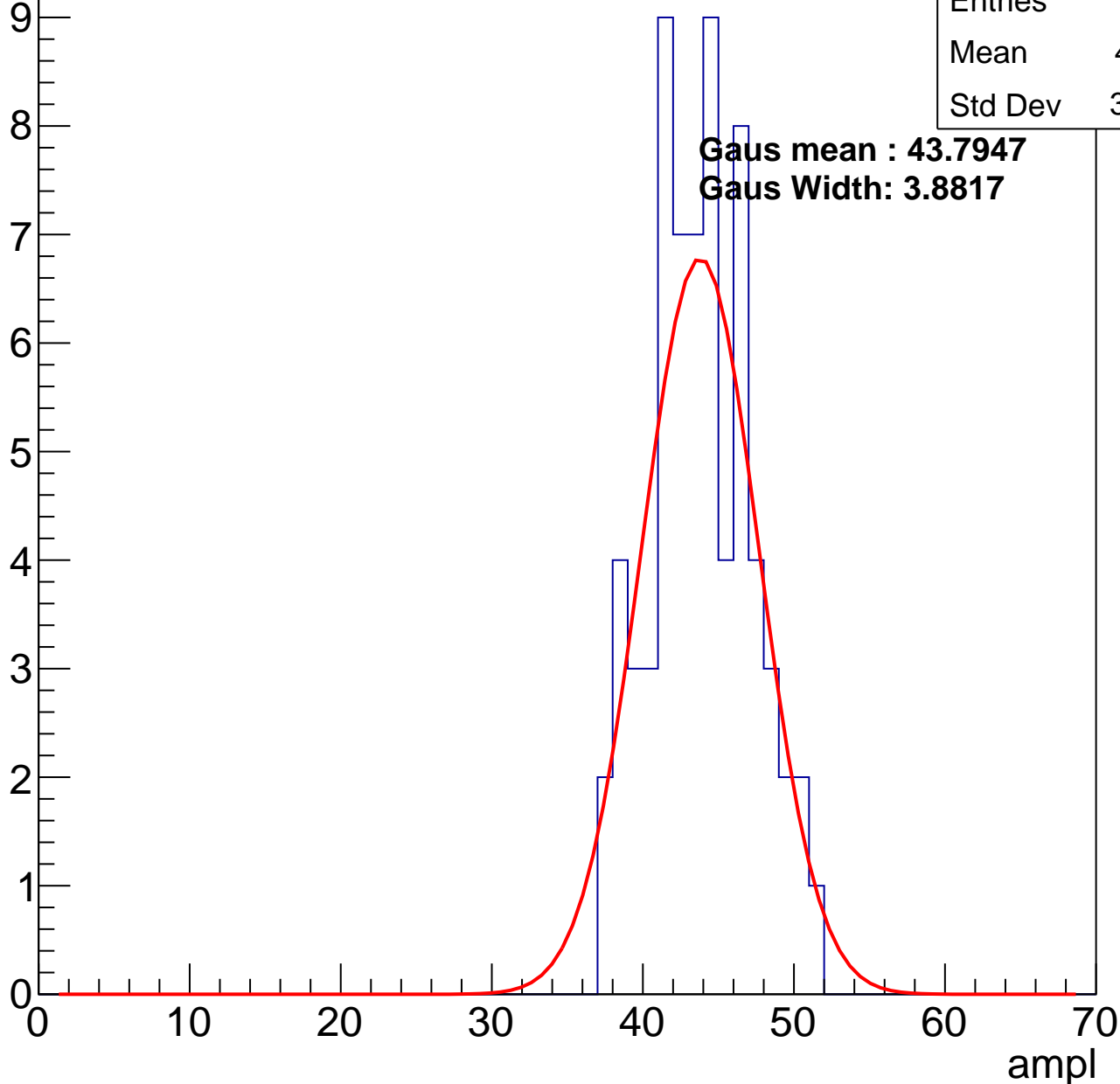
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.41
Std Dev	3.318

Gaus mean : 43.7947

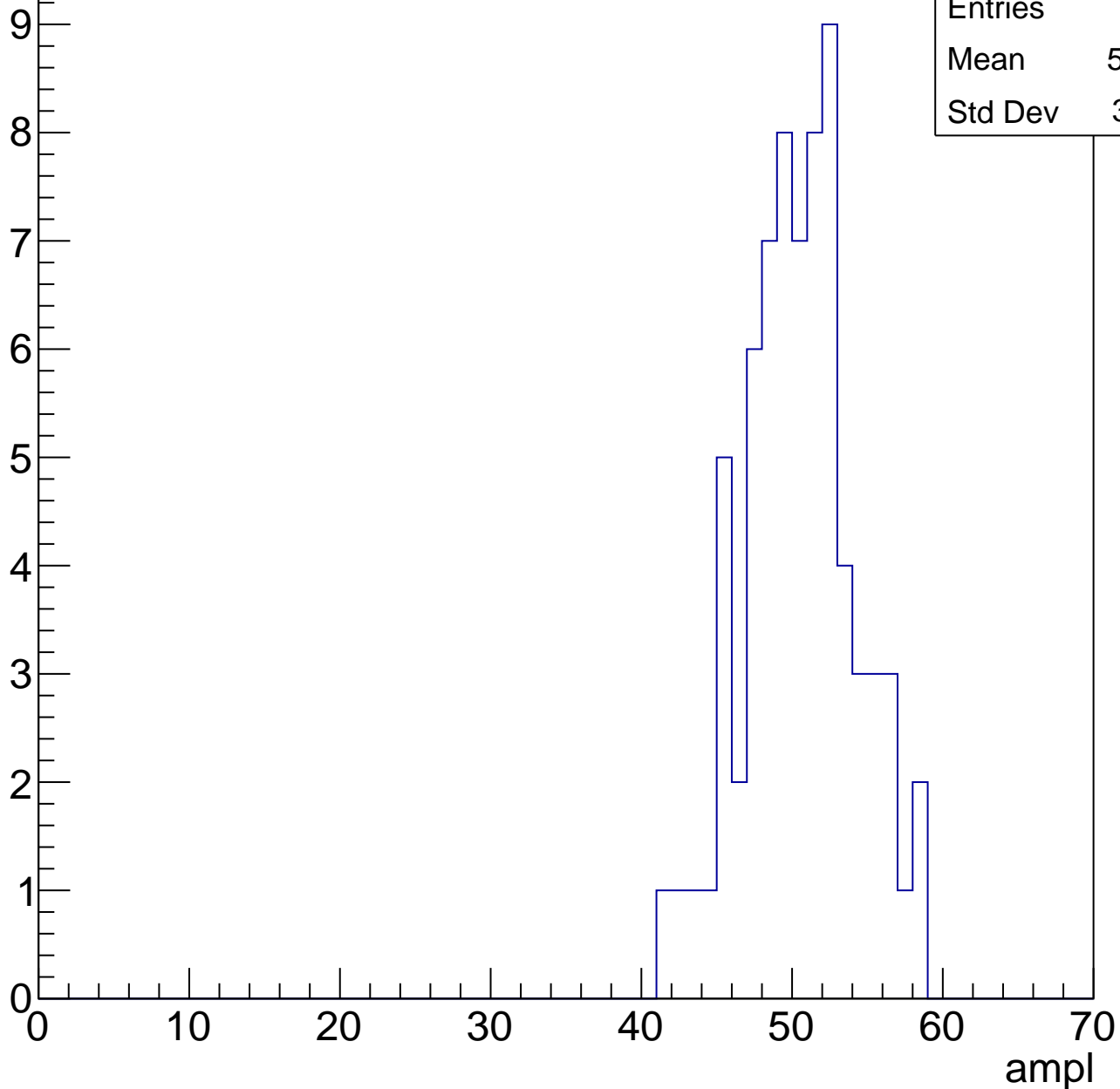
Gaus Width: 3.8817



B0L002S, U26-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

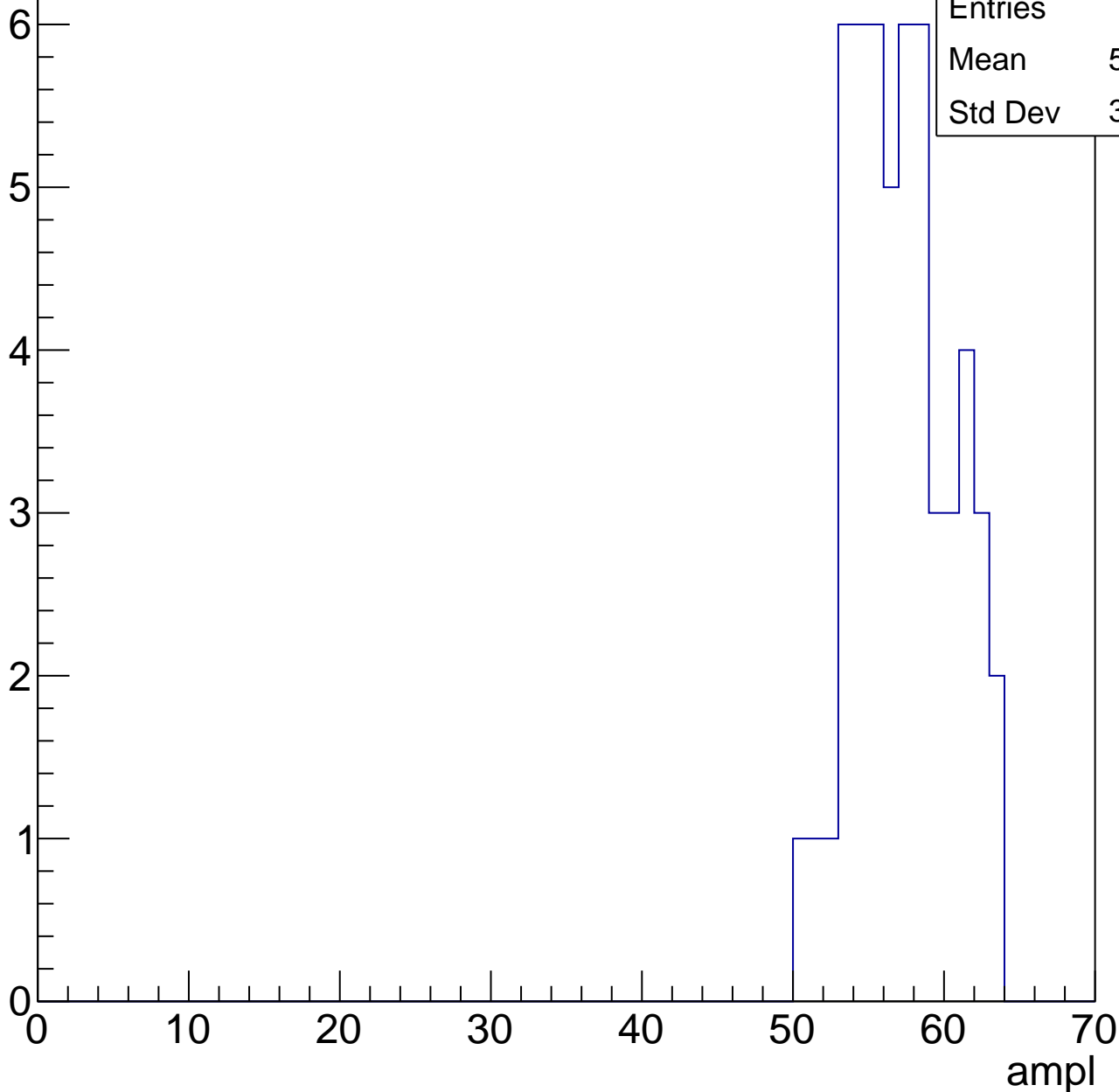


B0L002S, U26-ch77, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.75
Std Dev	3.197



B0L002S, U26-ch77, adc5

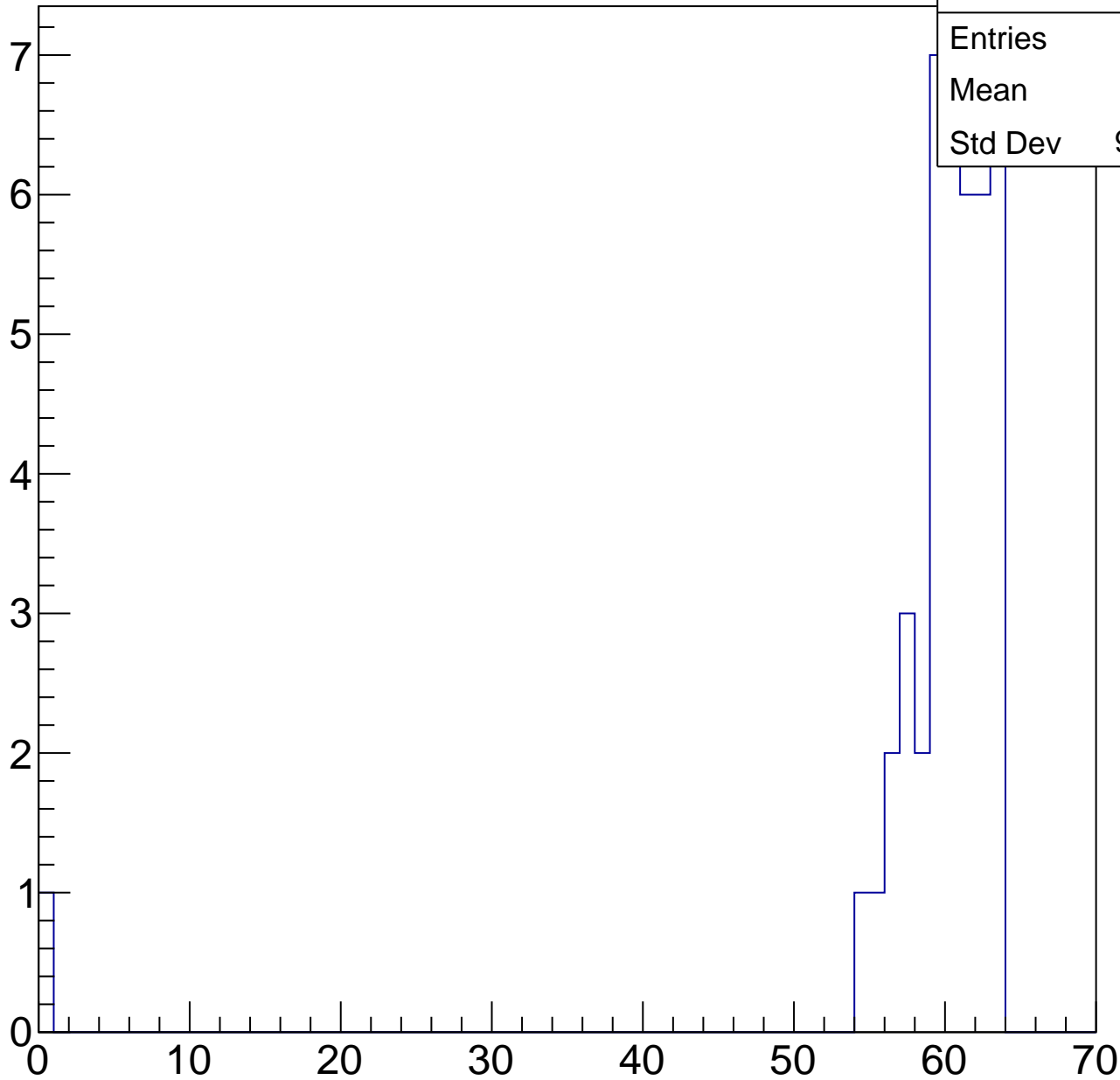
calib_packv5_042523_0143.root, FC#8, port C1

Entry

7
6
5
4
3
2
1
0

Entries	43
Mean	58.6
Std Dev	9.331

ampl



B0L002S, U26-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

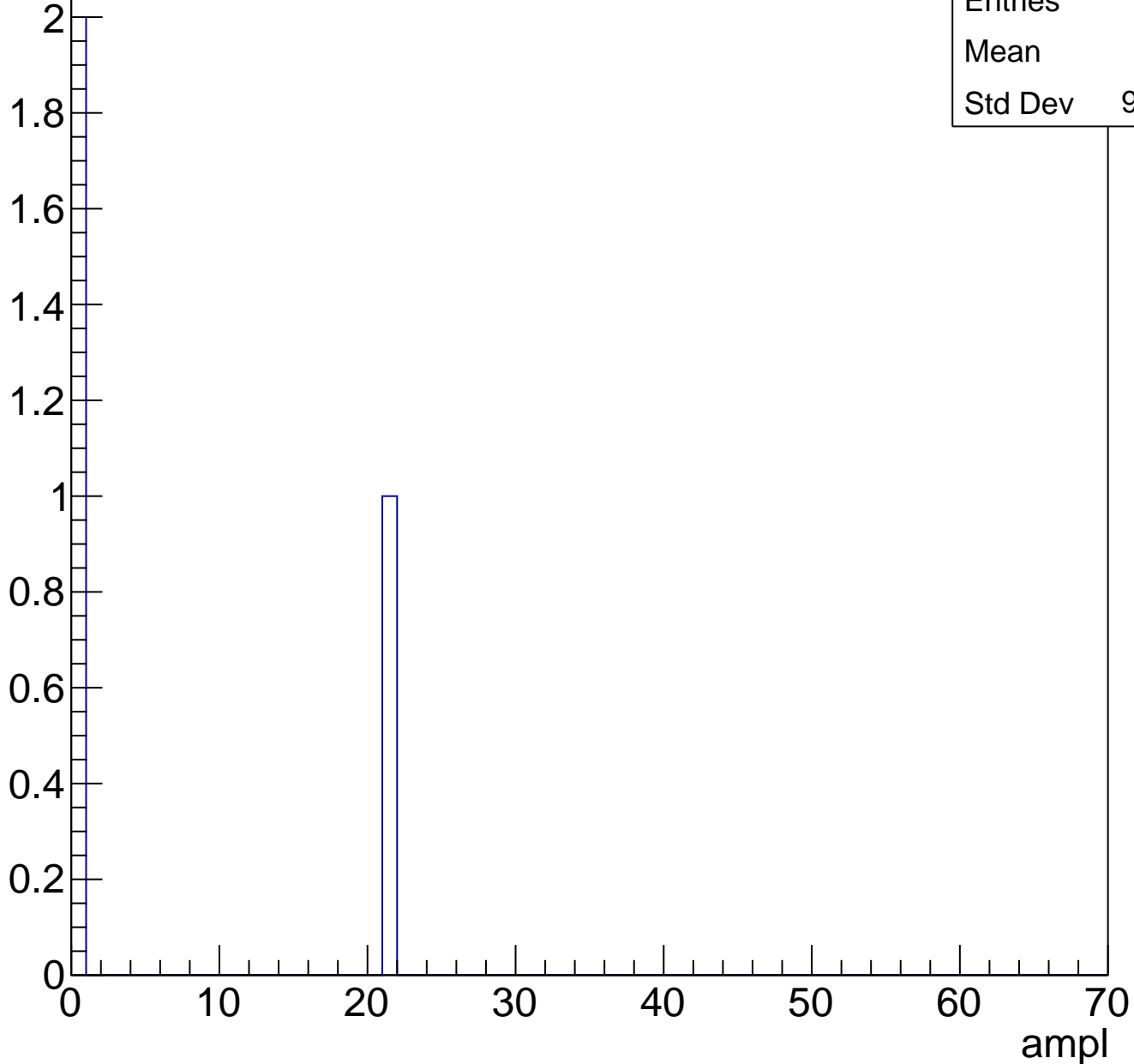
Entry



B0L002S, U26-ch77, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L002S, U26-ch78, adc0

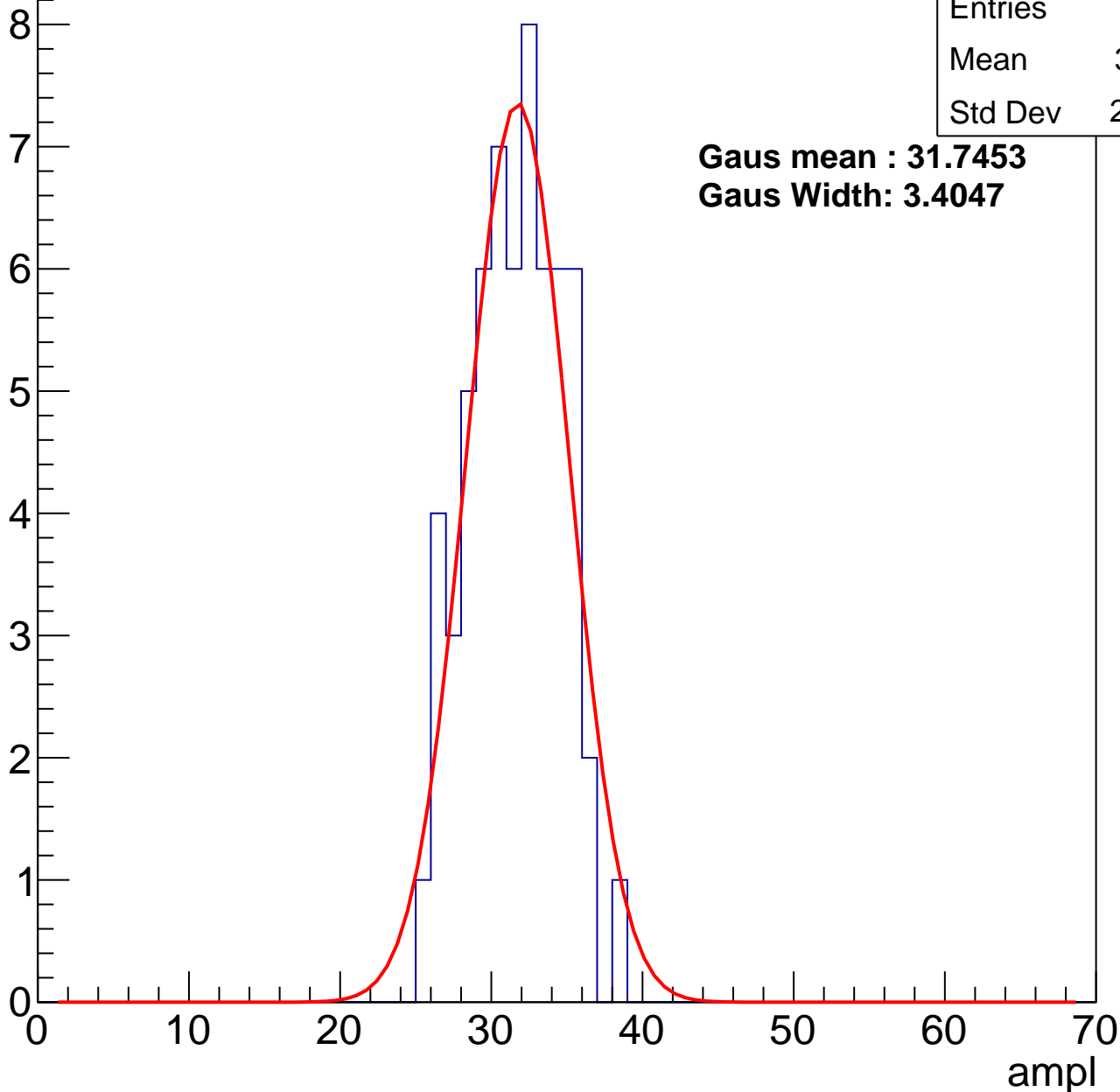
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	31.11
Std Dev	2.976

Gaus mean : 31.7453

Gaus Width: 3.4047



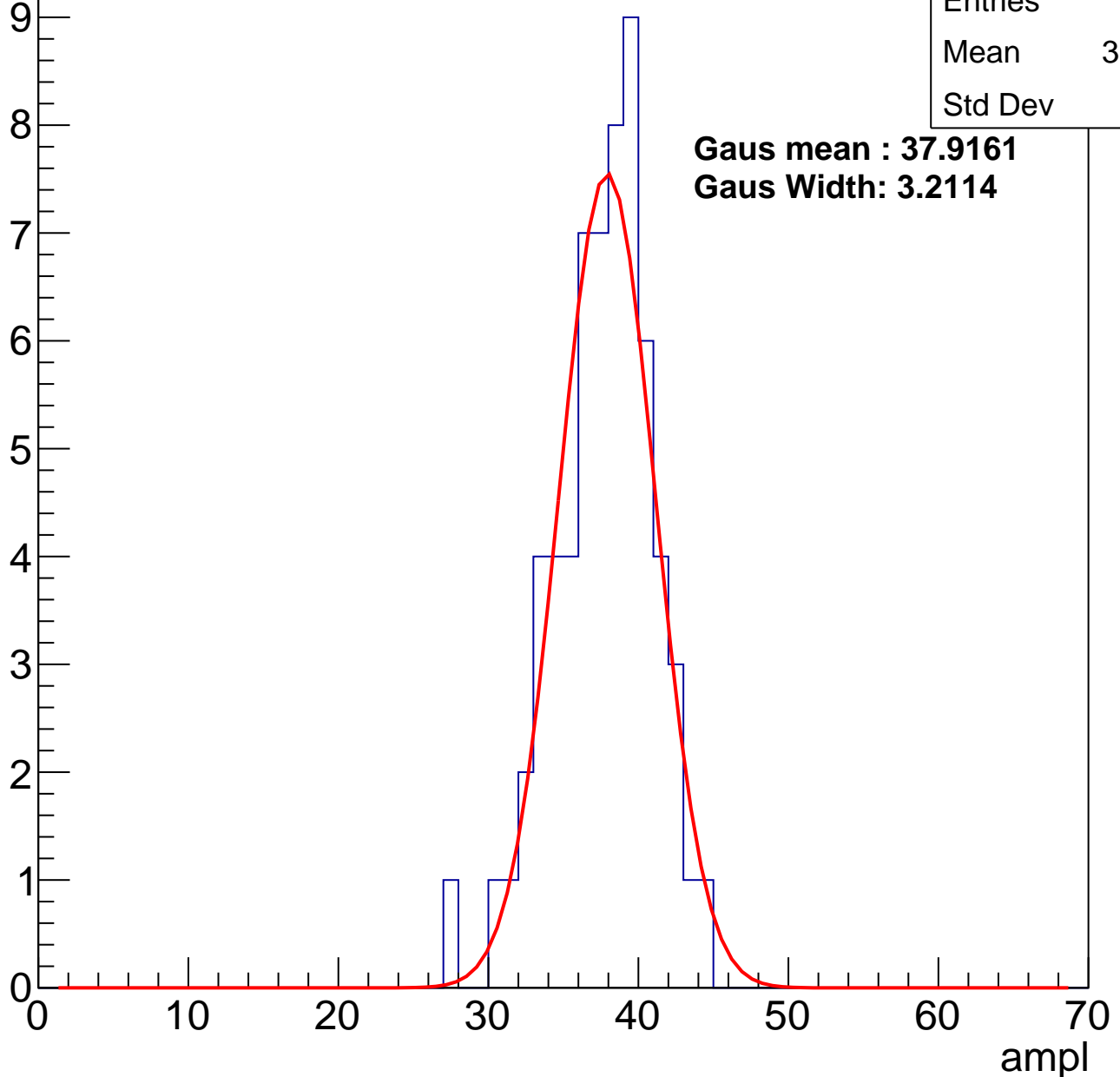
B0L002S, U26-ch78, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.19
Std Dev	3.28

Gaus mean : 37.9161
Gaus Width: 3.2114



B0L002S, U26-ch78, adc2

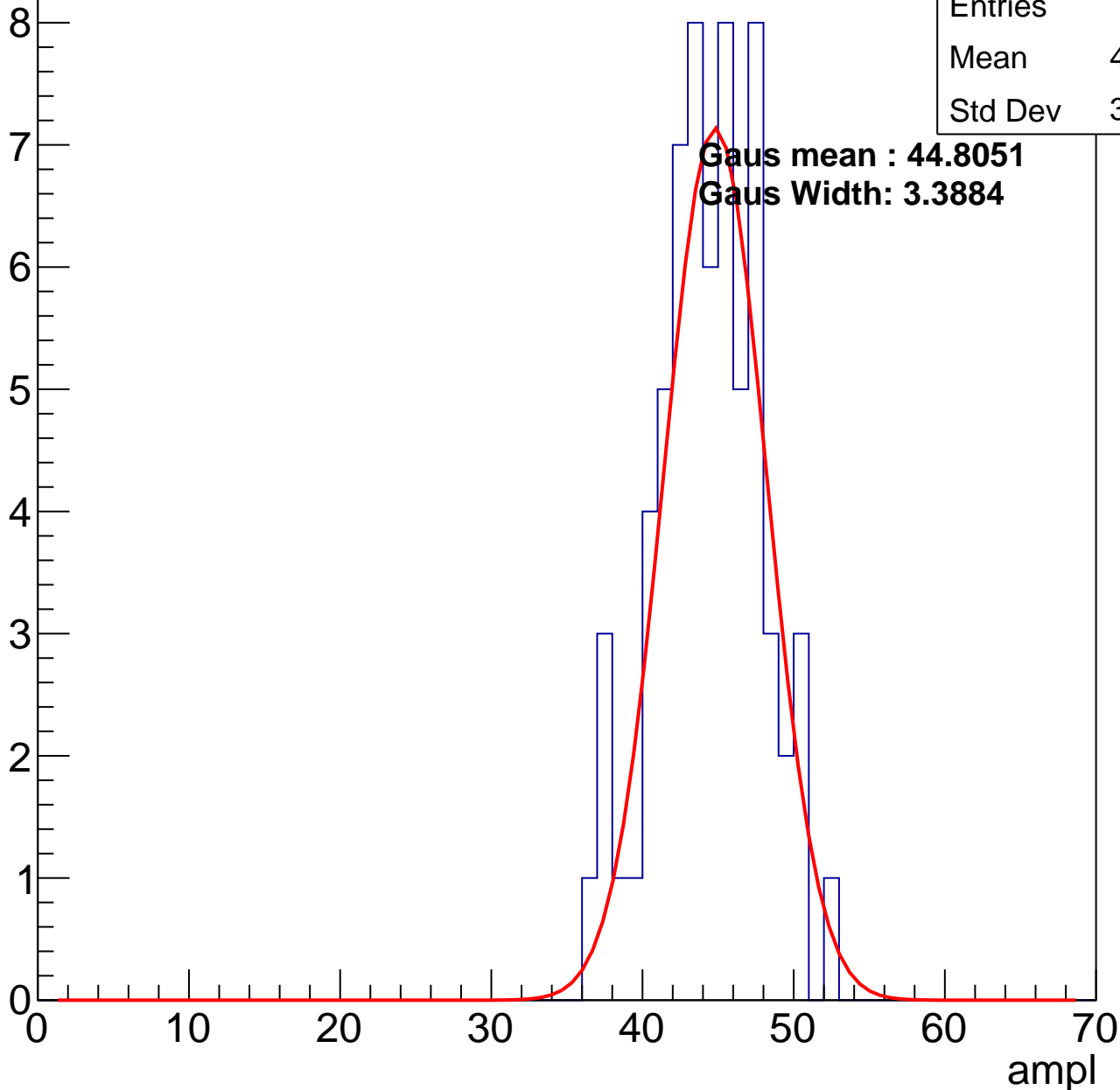
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.95
Std Dev	3.453

Gaus mean : 44.8051

Gaus Width: 3.3884

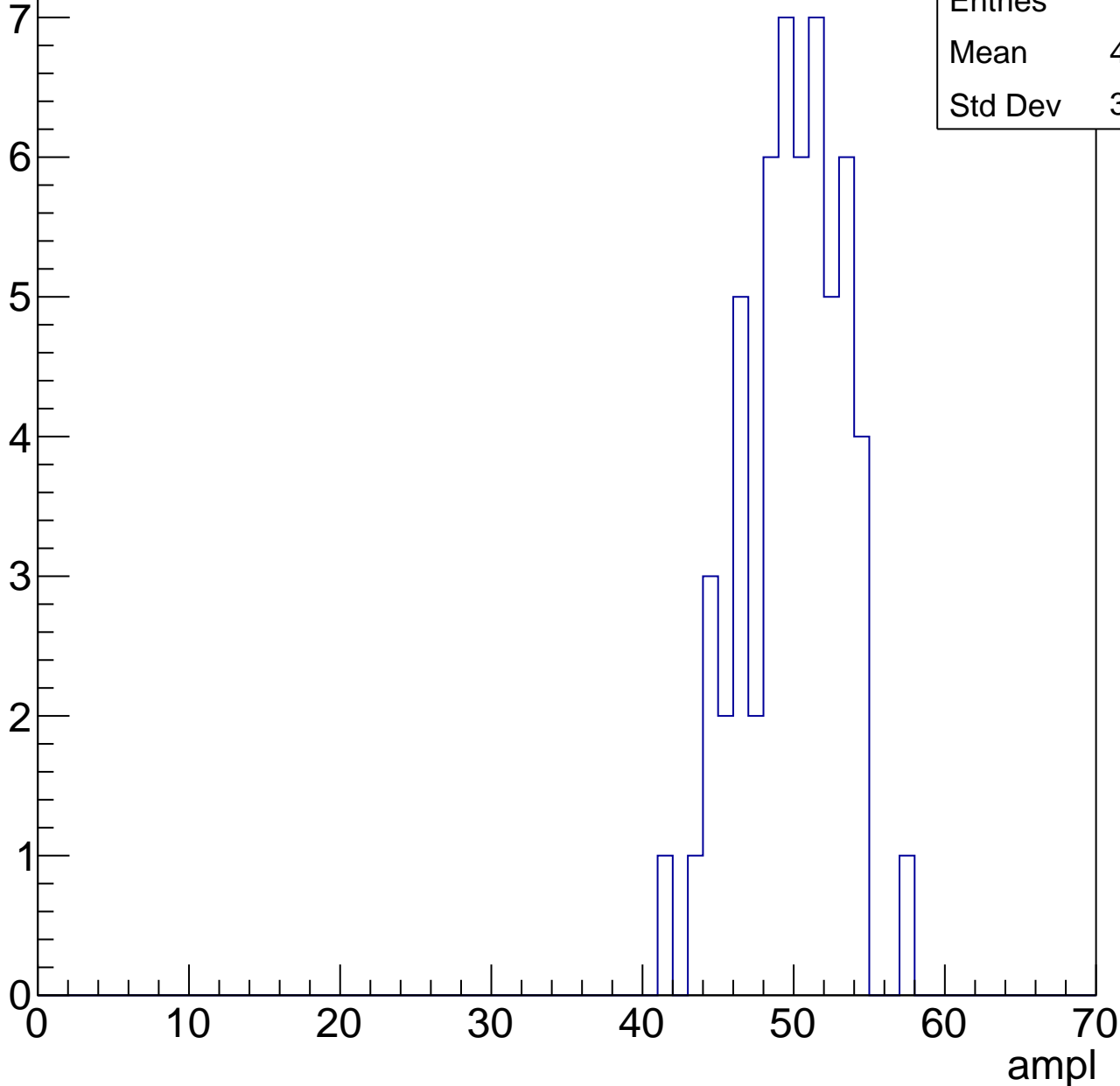


B0L002S, U26-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	49.45
Std Dev	3.256



B0L002S, U26-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

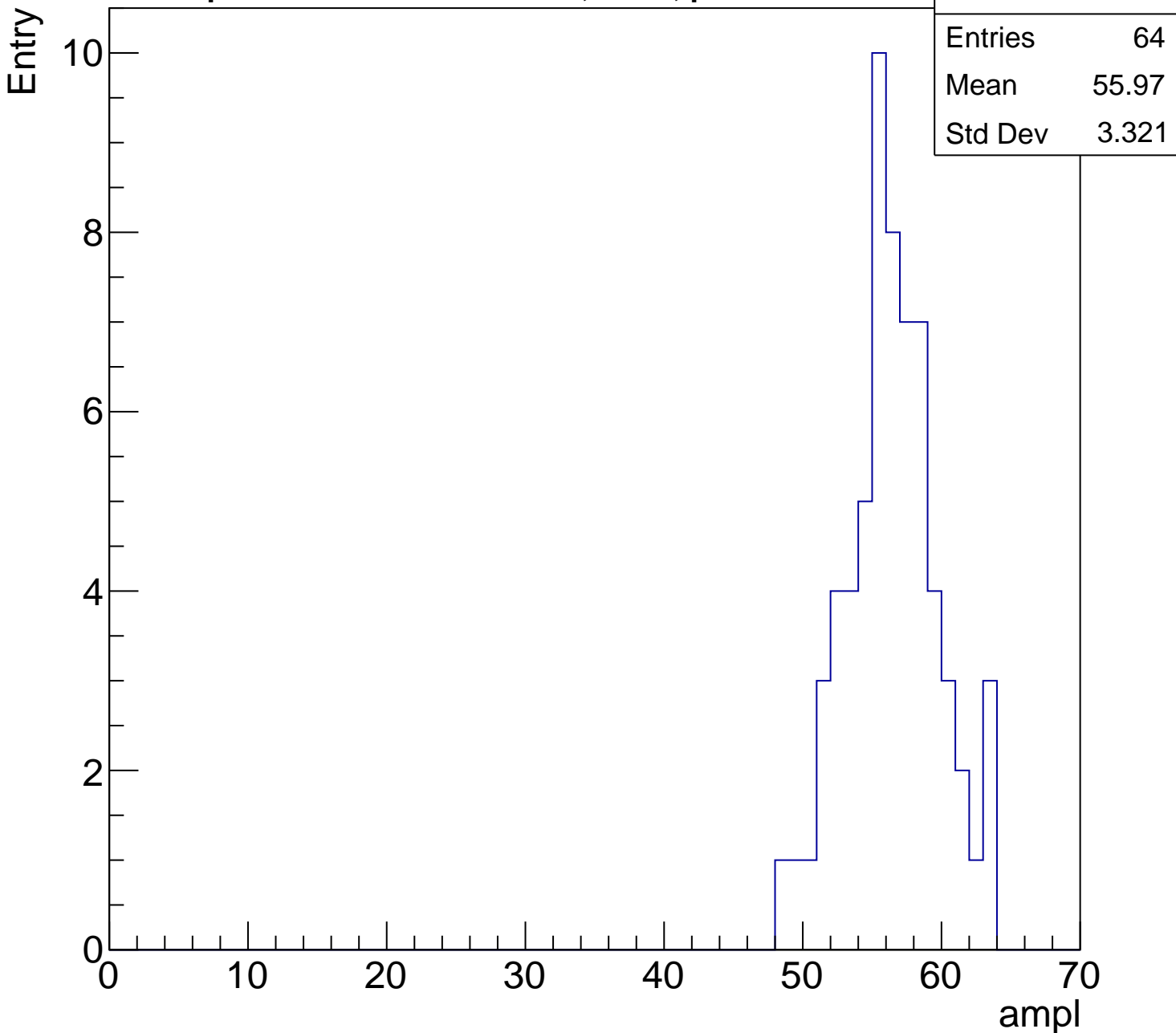
Entries	64
Mean	55.97
Std Dev	3.321

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

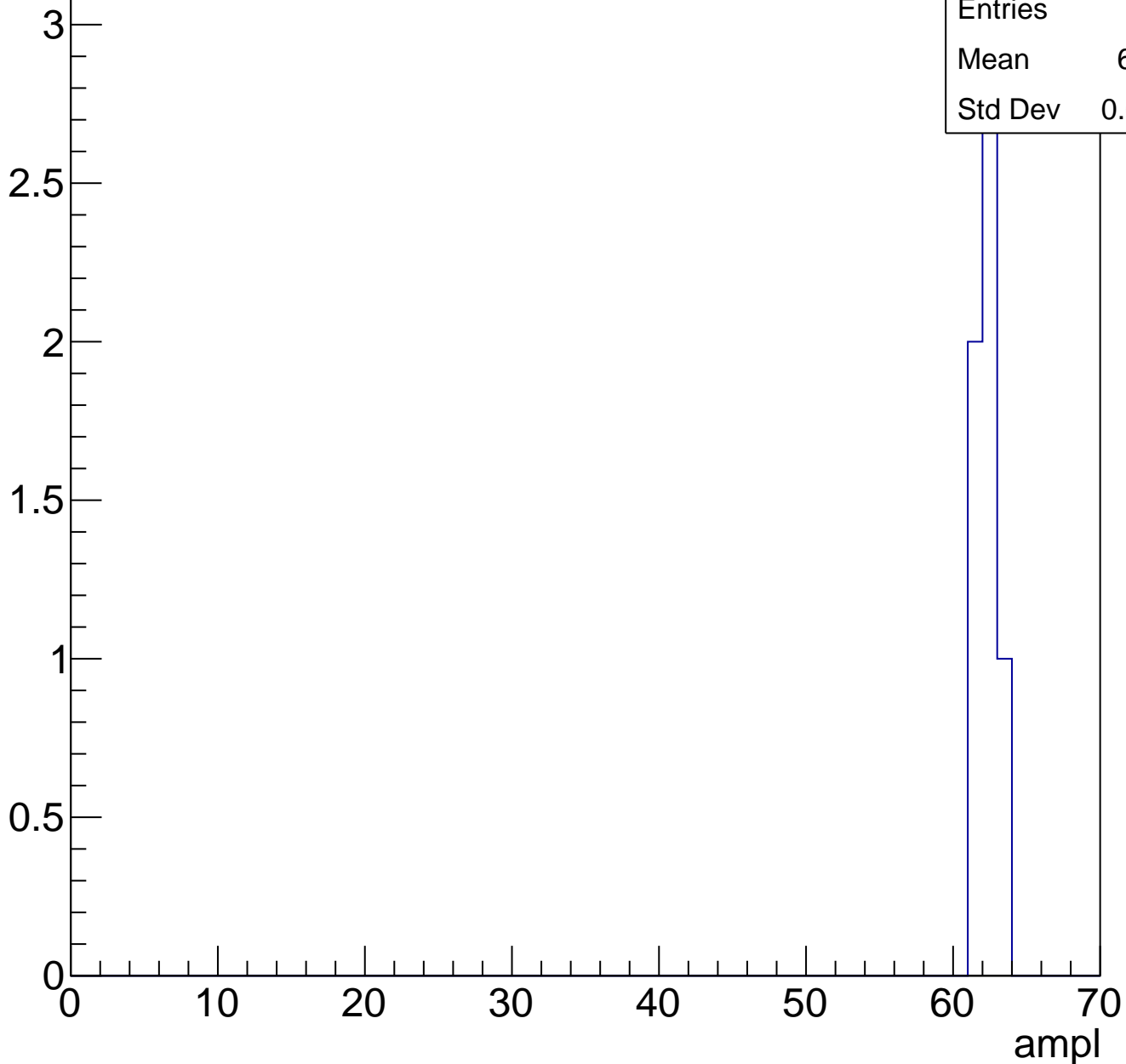
Entries	40
Mean	58.6
Std Dev	9.63

ampl

B0L002S, U26-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch79, adc0

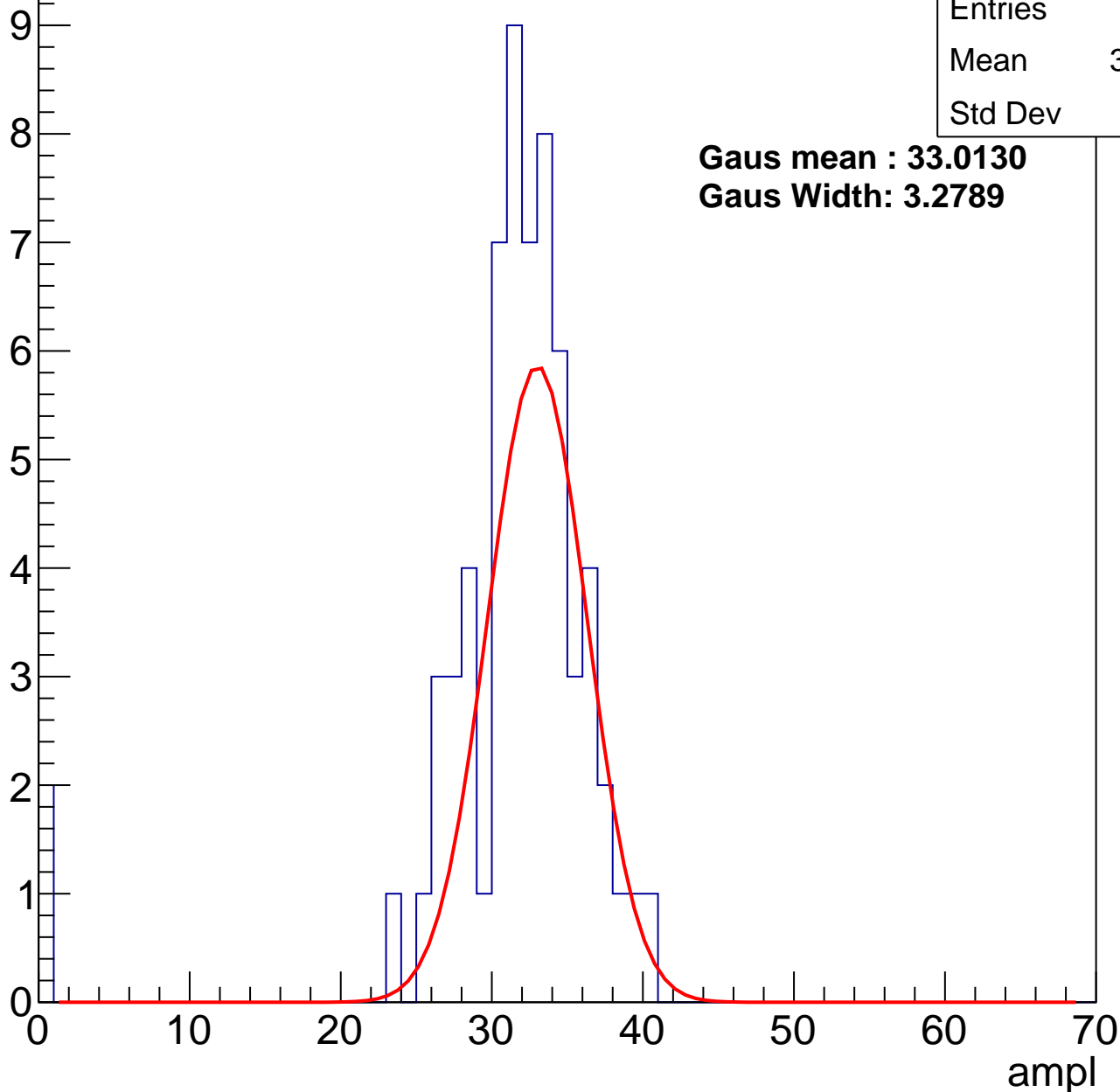
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.77
Std Dev	6.49

Gaus mean : 33.0130

Gaus Width: 3.2789



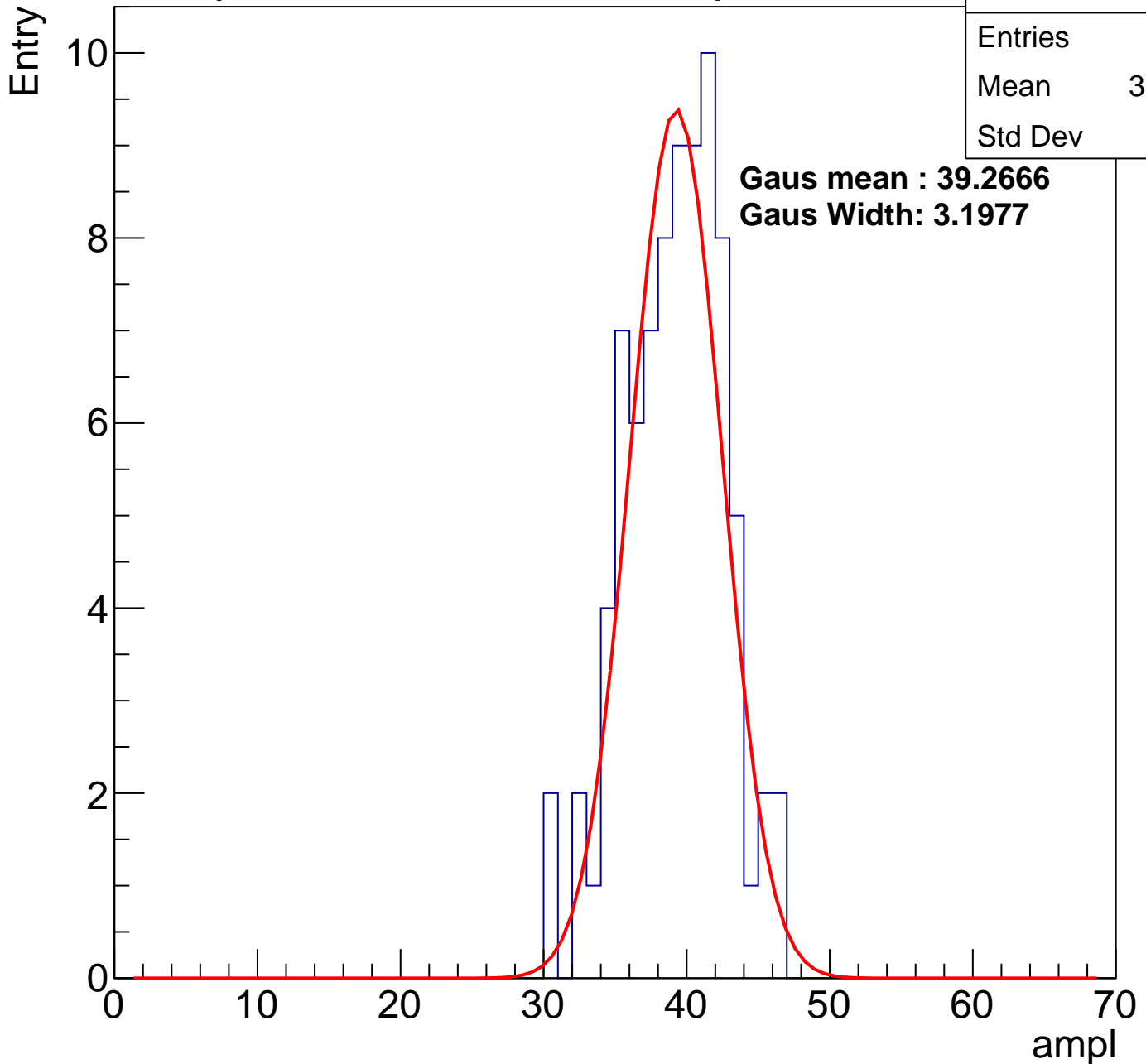
B0L002S, U26-ch79, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	38.73
Std Dev	3.45

Gaus mean : 39.2666

Gaus Width: 3.1977



B0L002S, U26-ch79, adc2

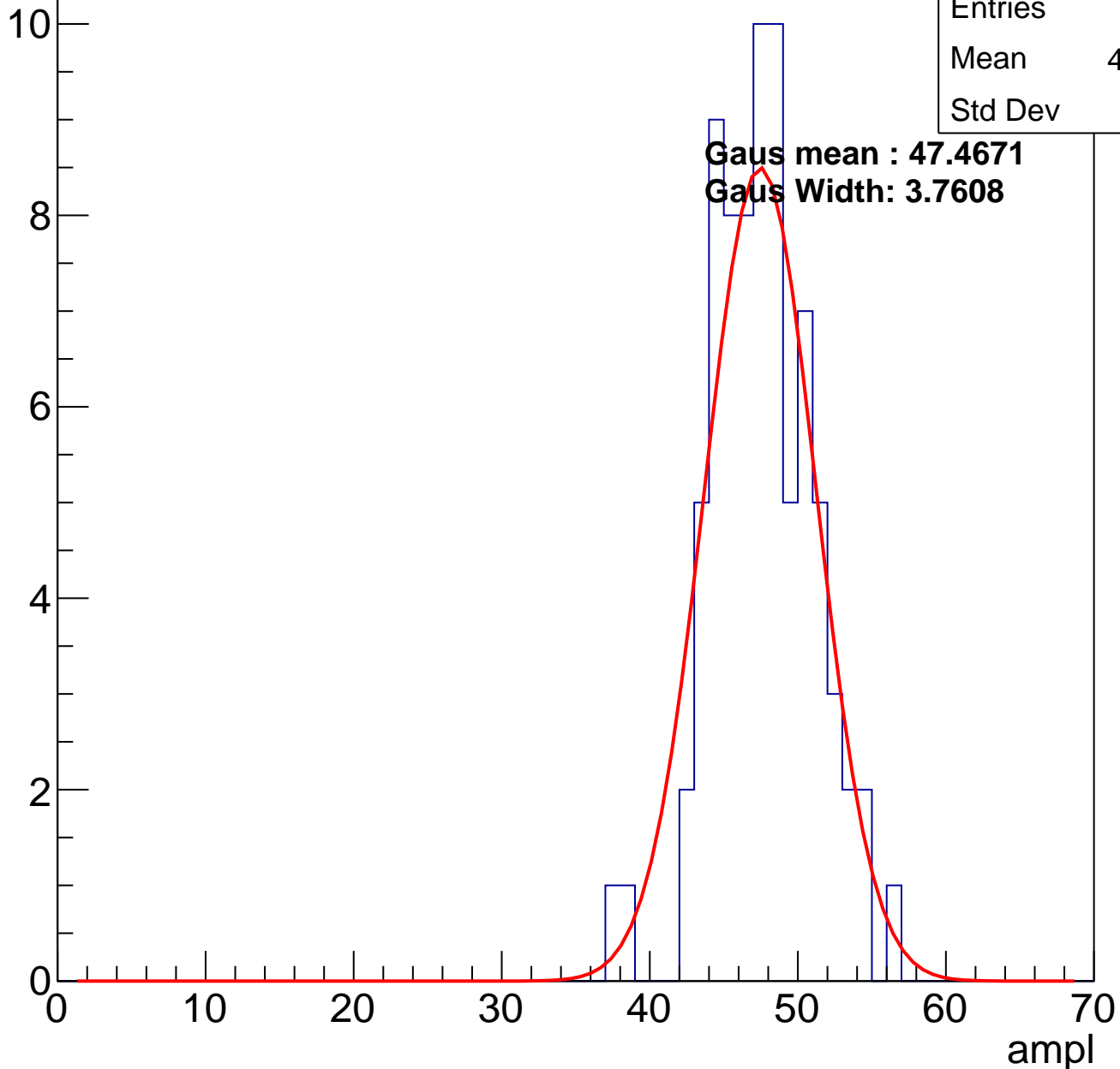
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	47.14
Std Dev	3.43

Gaus mean : 47.4671

Gaus Width: 3.7608

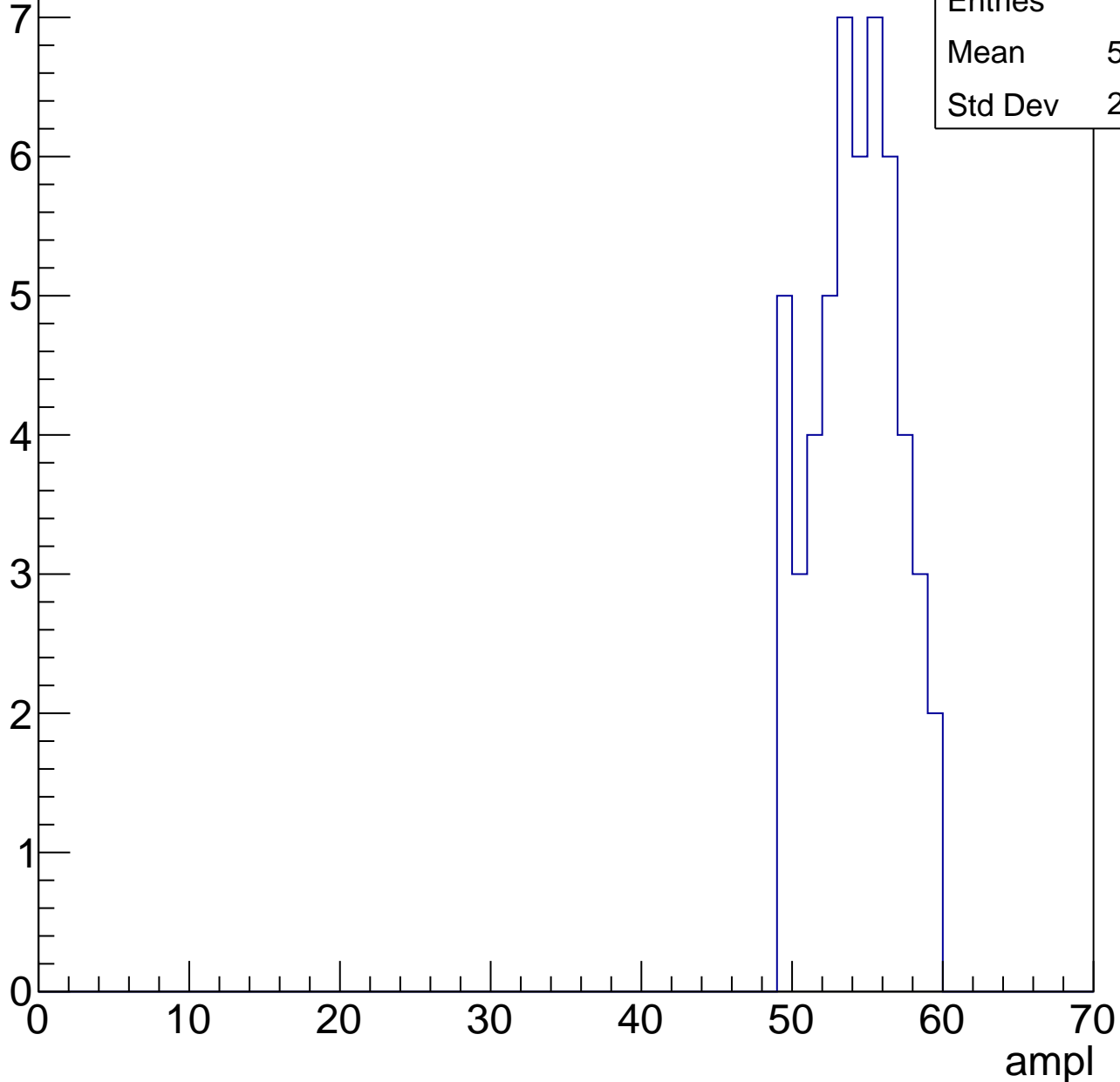
Entry



B0L002S, U26-ch79, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

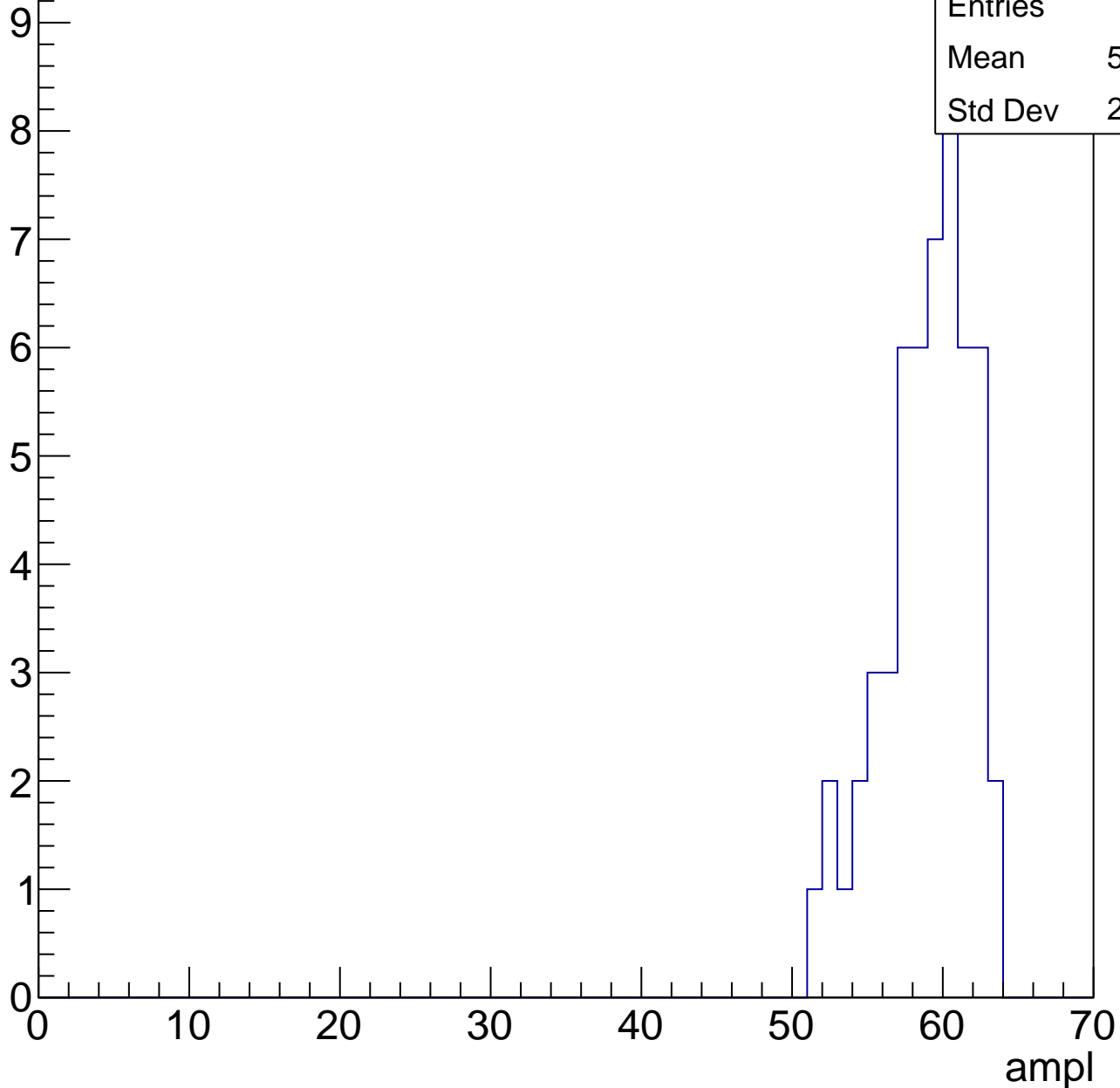


Entries	52
Mean	53.75
Std Dev	2.766

B0L002S, U26-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

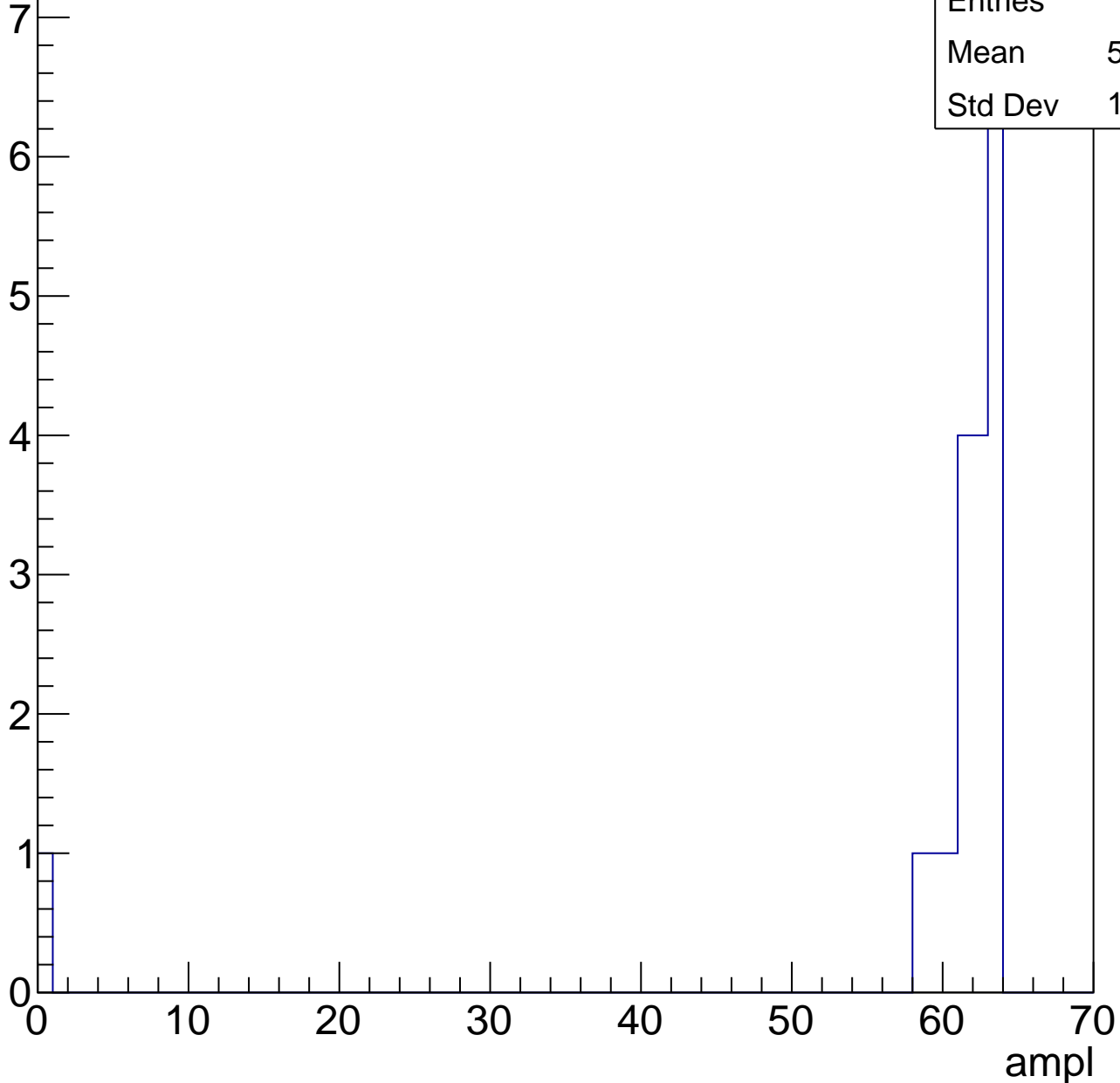


B0L002S, U26-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	58.42
Std Dev	13.84



B0L002S, U26-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U26-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch80, adc0

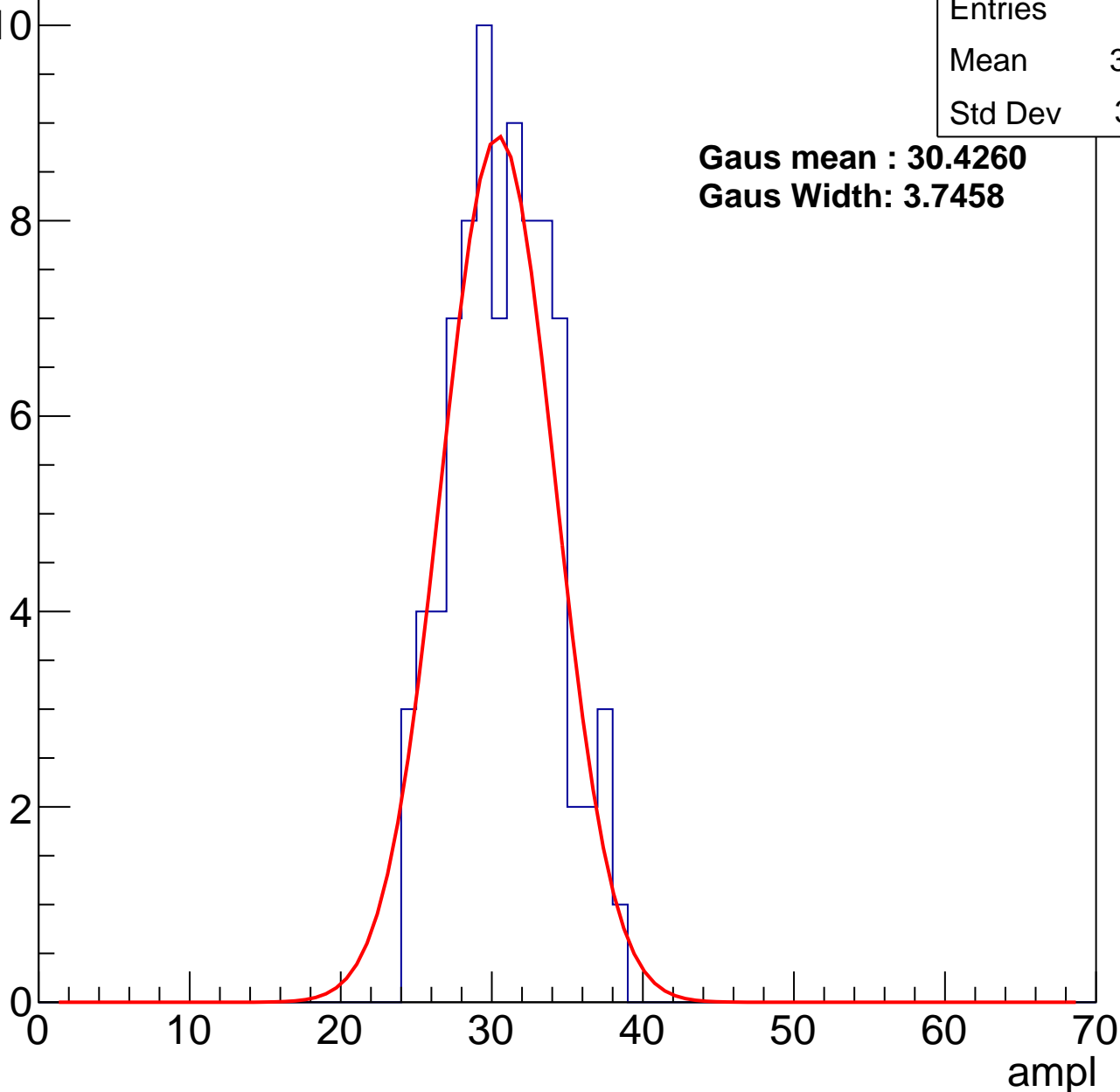
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	30.33
Std Dev	3.341

Gaus mean : 30.4260

Gaus Width: 3.7458



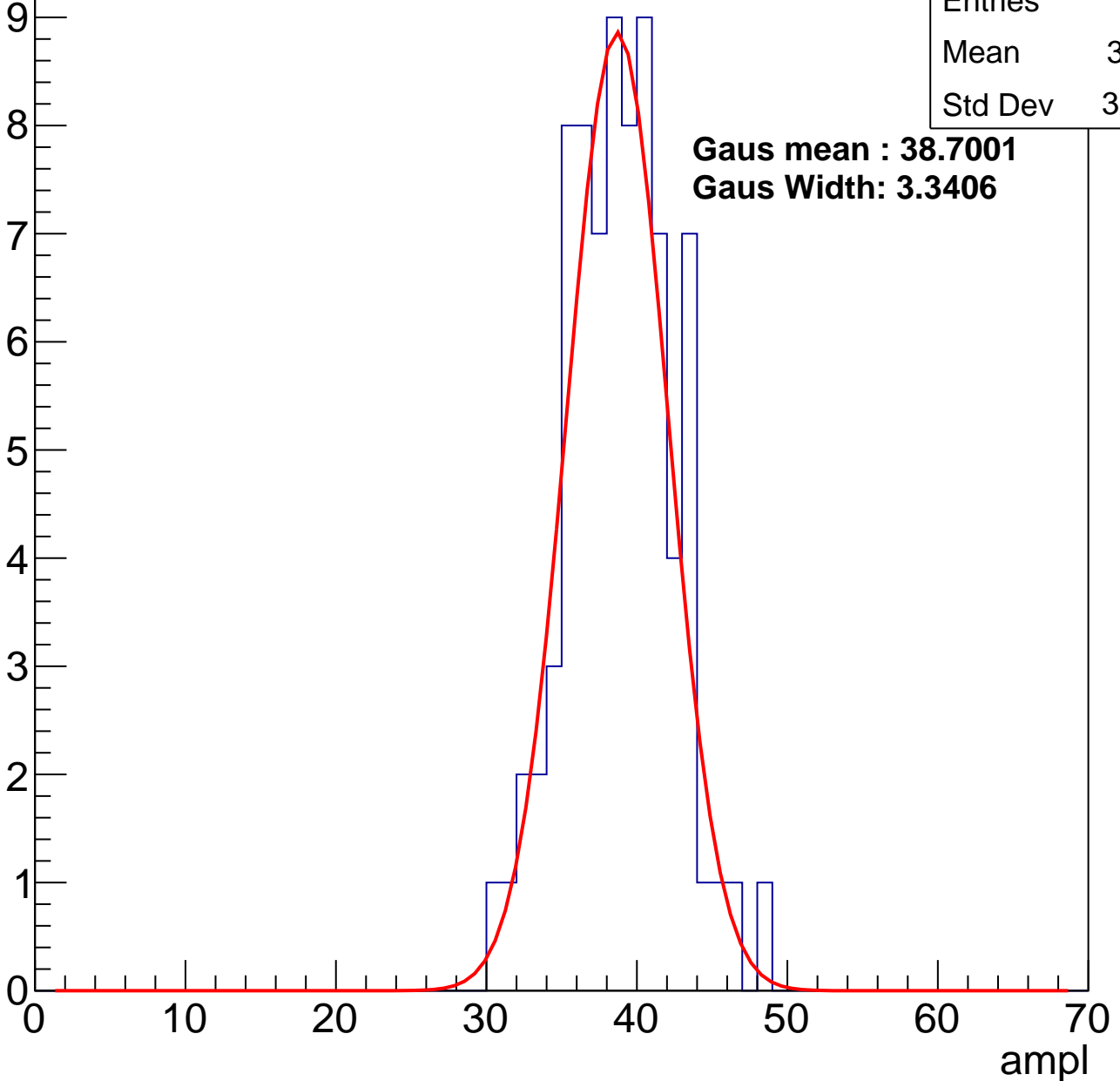
B0L002S, U26-ch80, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	38.41
Std Dev	3.492

Gaus mean : 38.7001
Gaus Width: 3.3406



B0L002S, U26-ch80, adc2

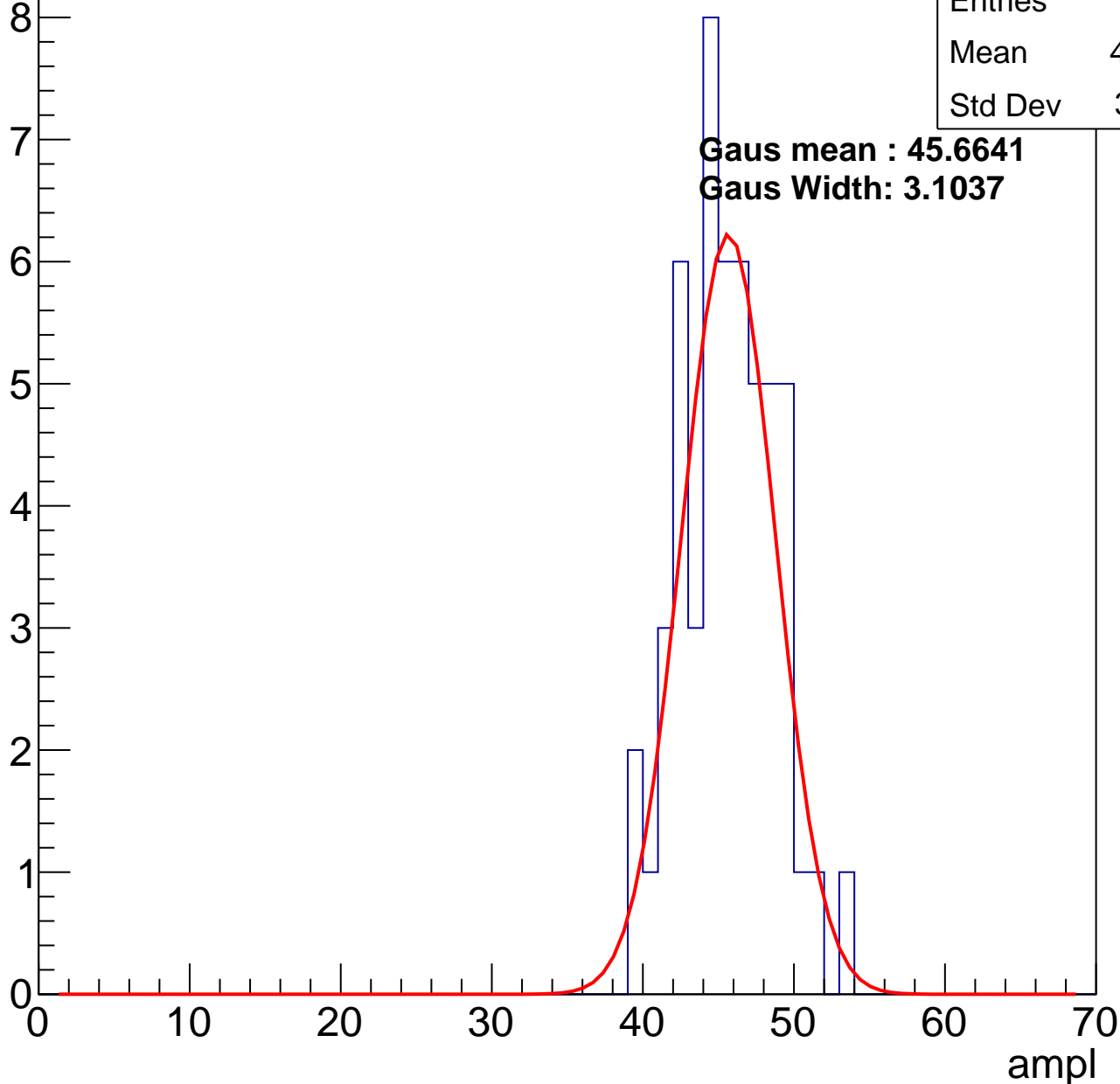
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	45.17
Std Dev	3.051

Gaus mean : 45.6641

Gaus Width: 3.1037

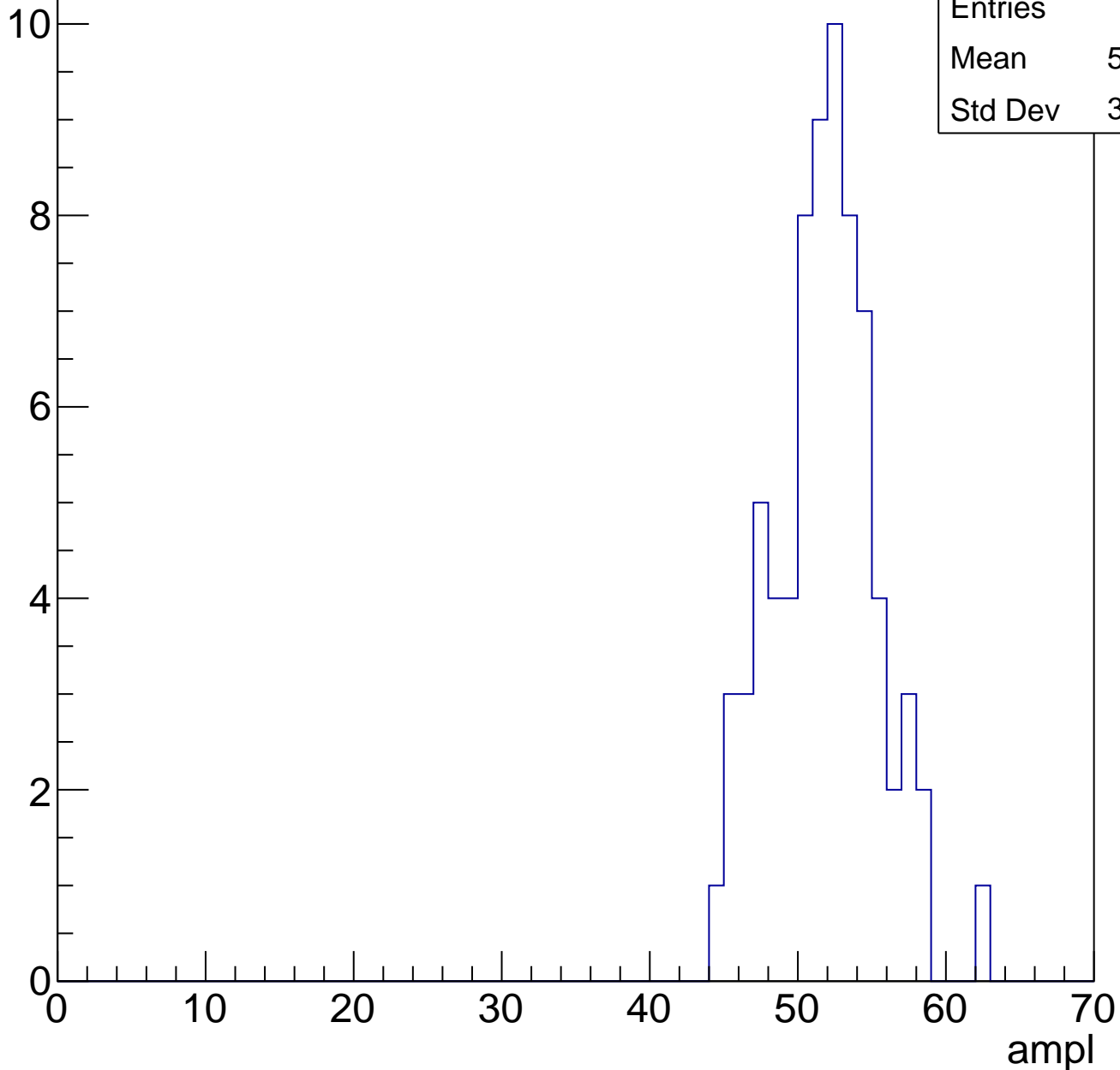


B0L002S, U26-ch80, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	51.38
Std Dev	3.502

Entry

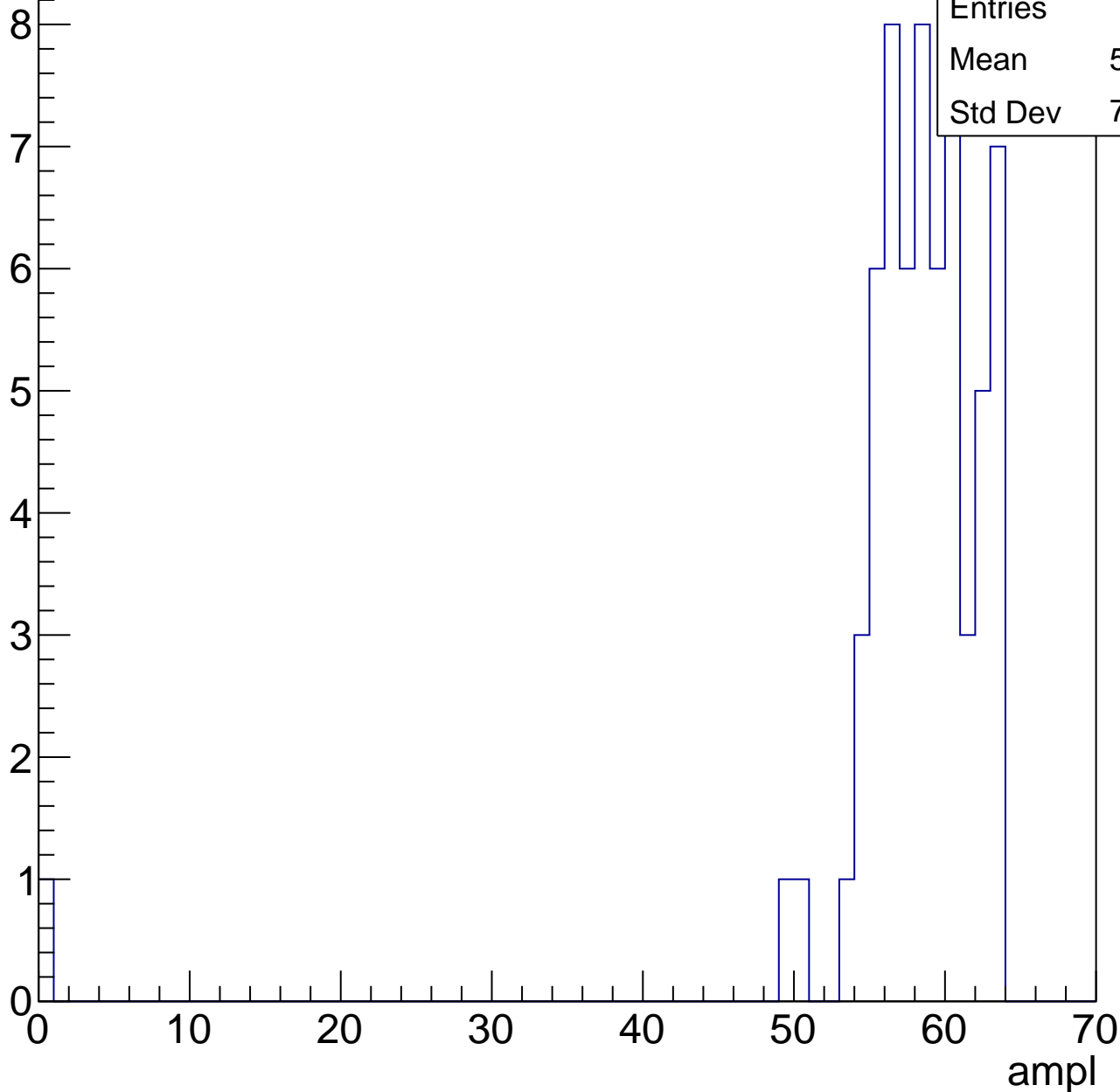


B0L002S, U26-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	57.28
Std Dev	7.867

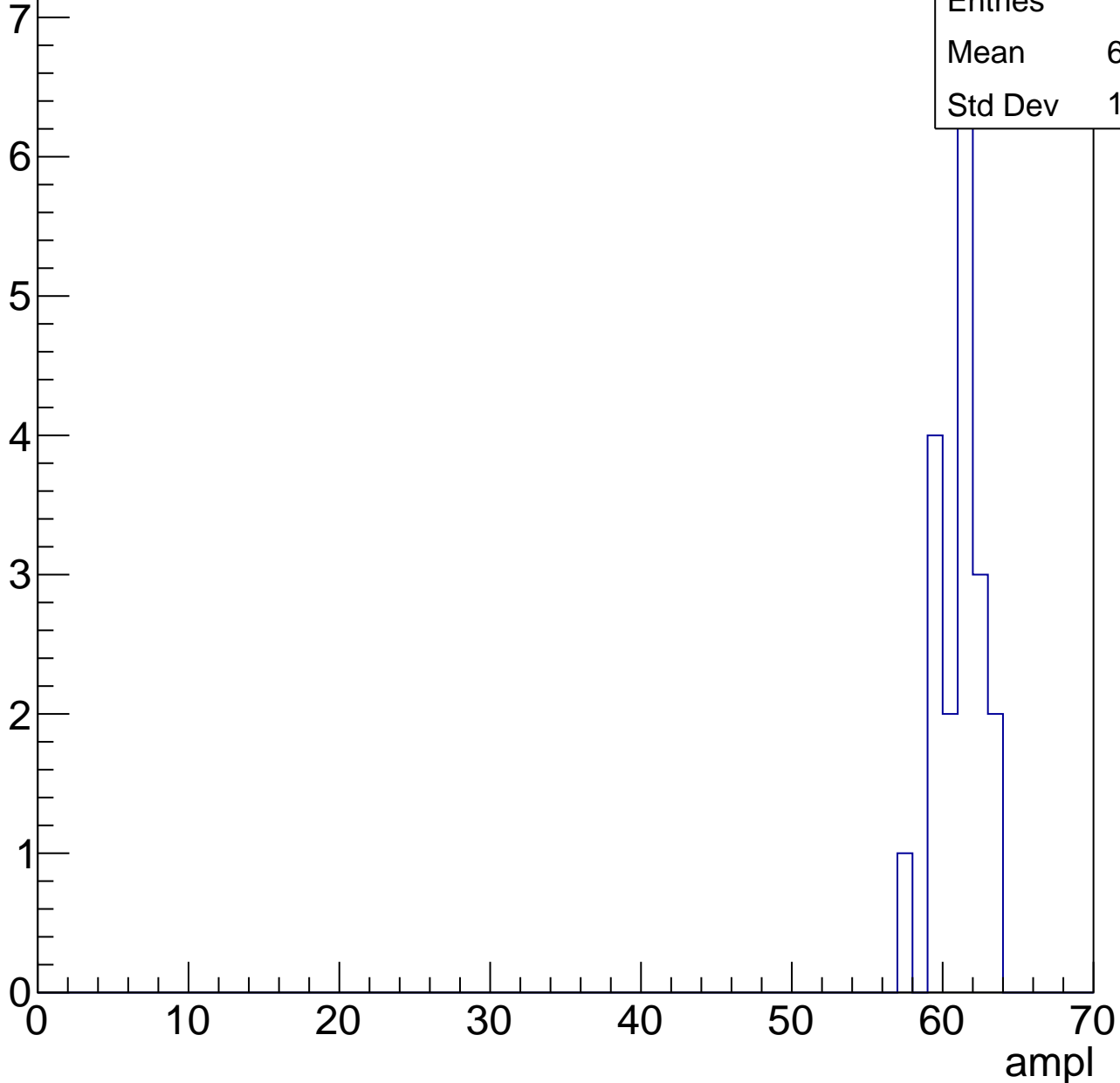


B0L002S, U26-ch80, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	60.63
Std Dev	1.494



B0L002S, U26-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

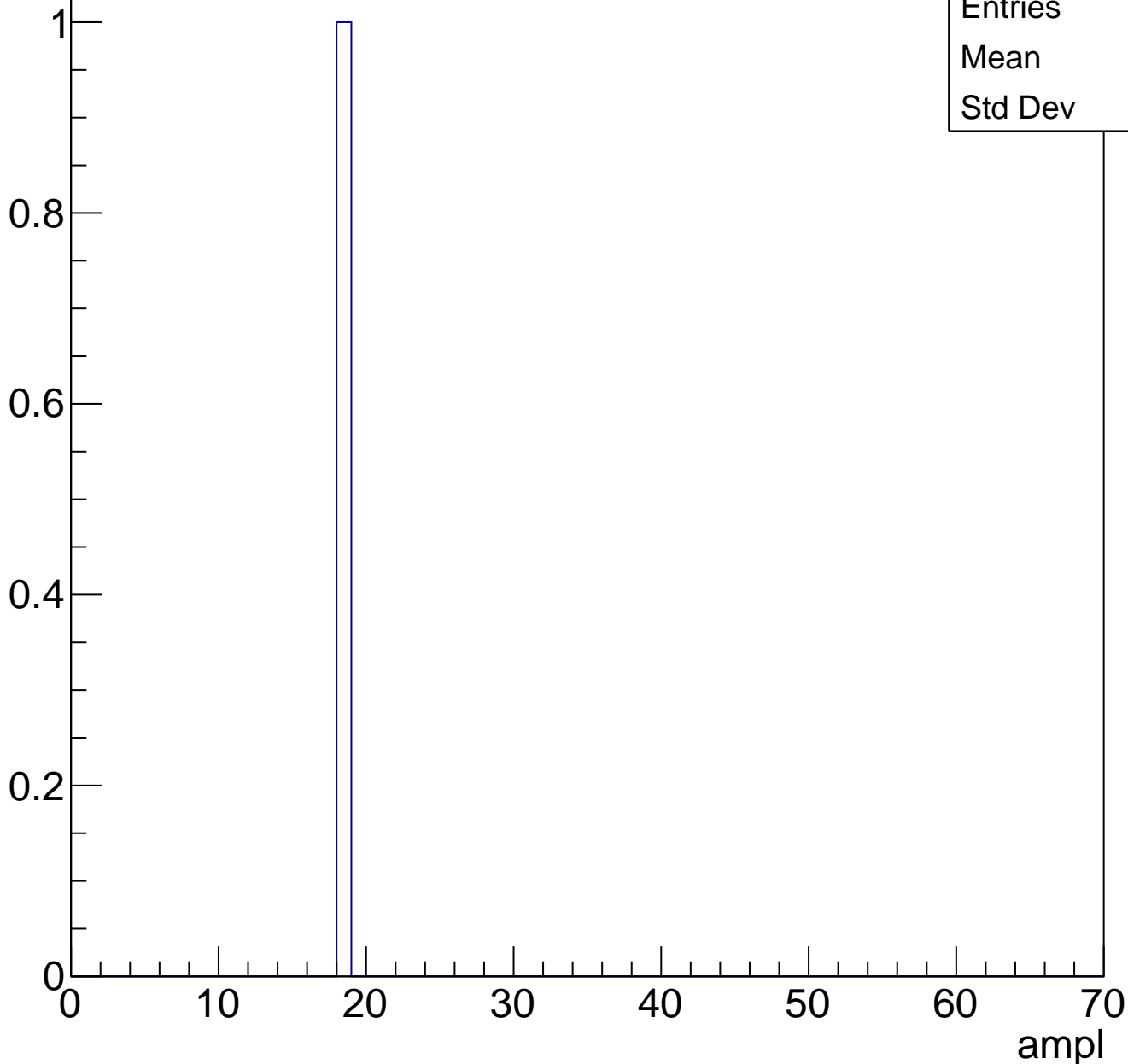
Entry



B0L002S, U26-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch81, adc0

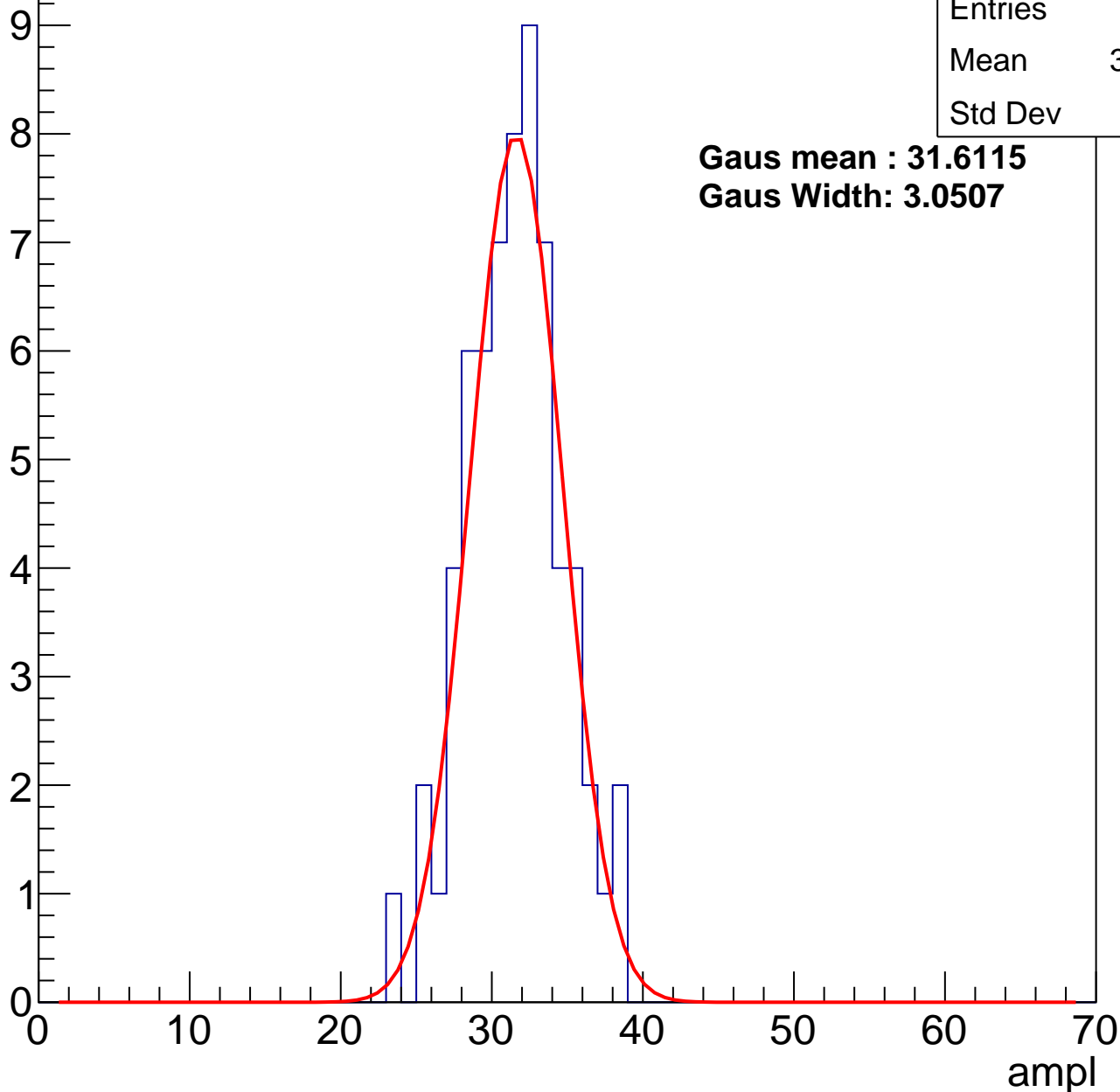
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.05
Std Dev	3.14

Gaus mean : 31.6115

Gaus Width: 3.0507



B0L002S, U26-ch81, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	39.04
Std Dev	3.807

Gaus mean : 39.2078

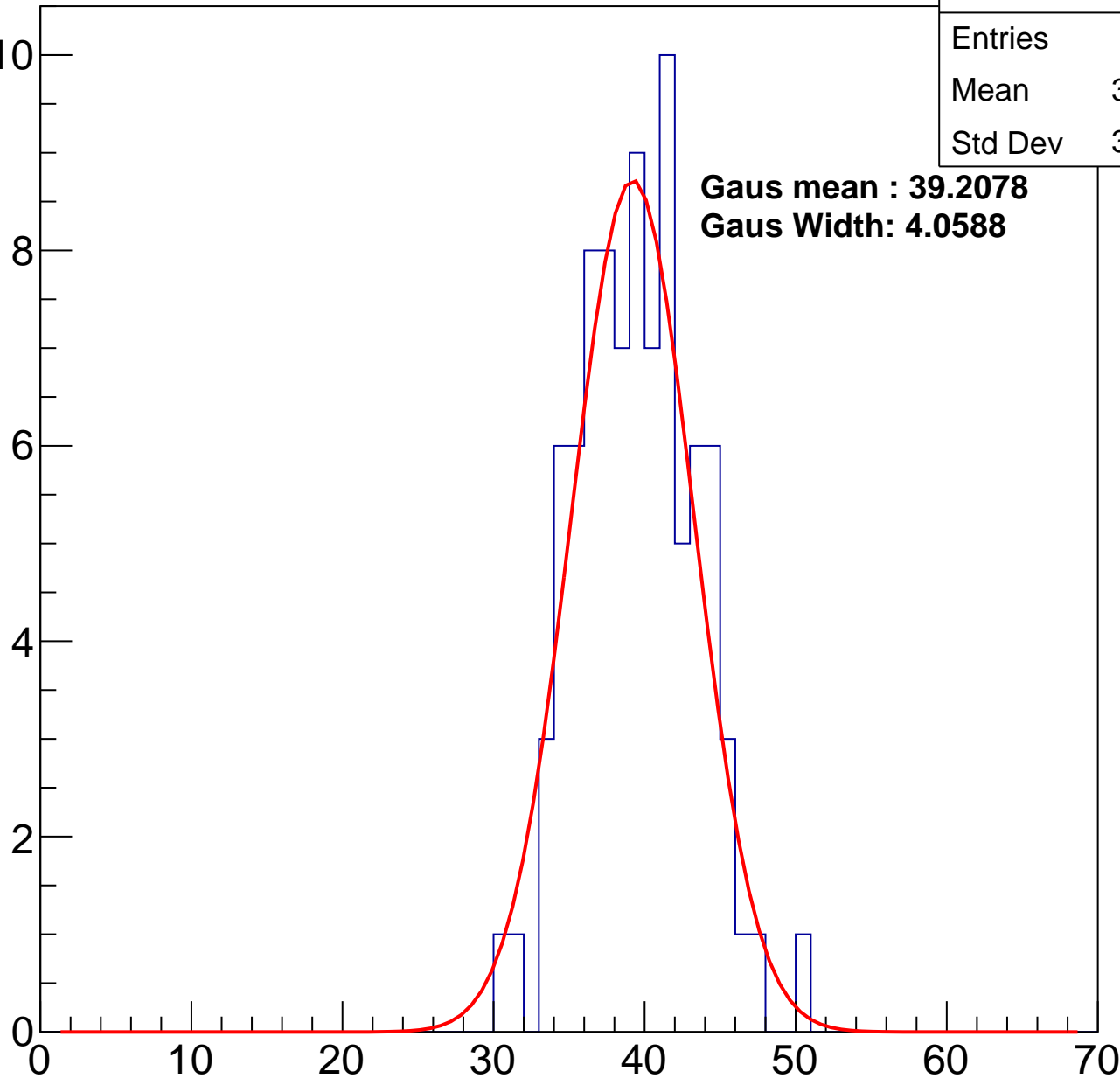
Gaus Width: 4.0588

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch81, adc2

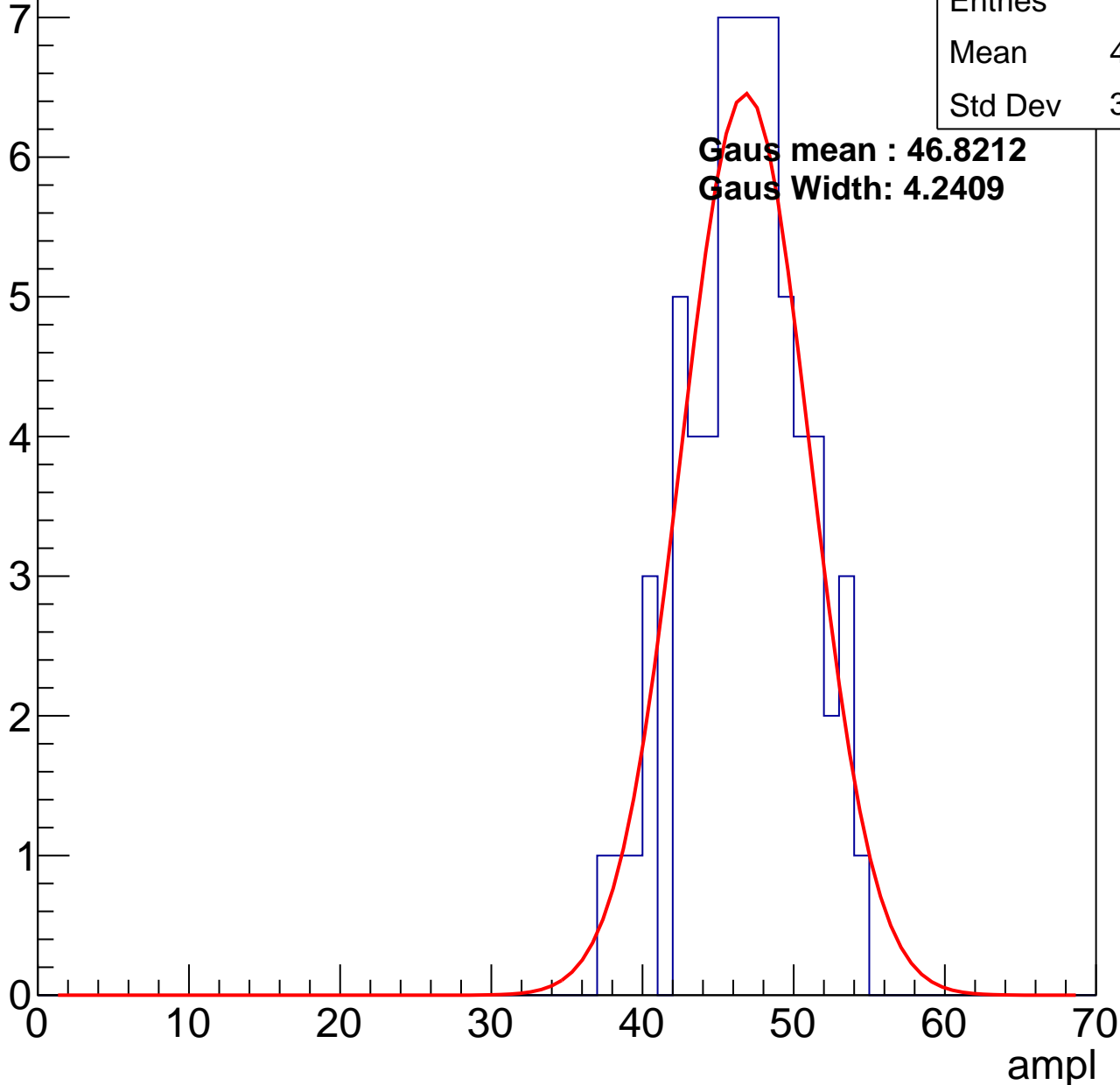
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	46.36
Std Dev	3.812

Gaus mean : 46.8212

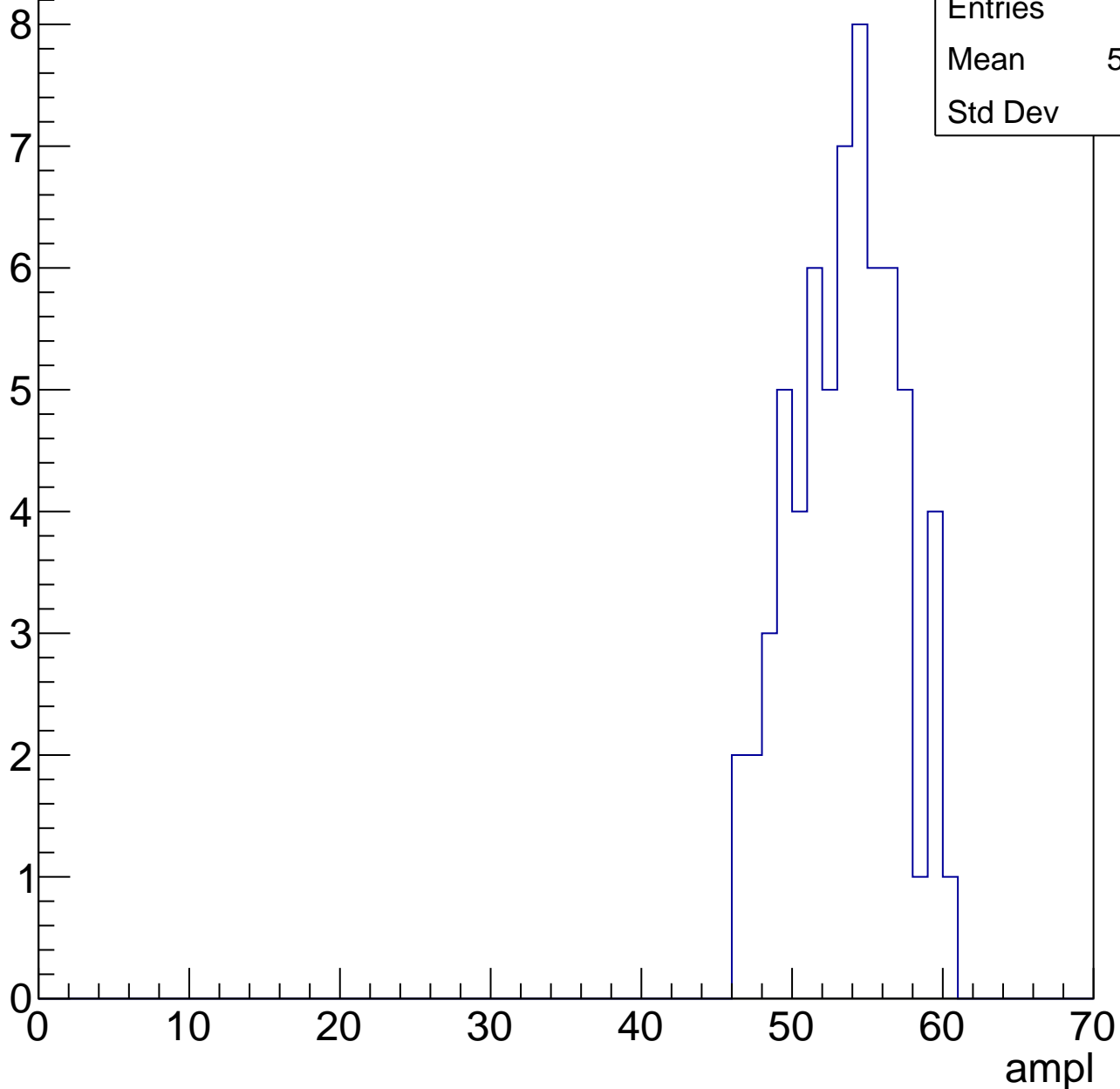
Gaus Width: 4.2409



B0L002S, U26-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch81, adc4

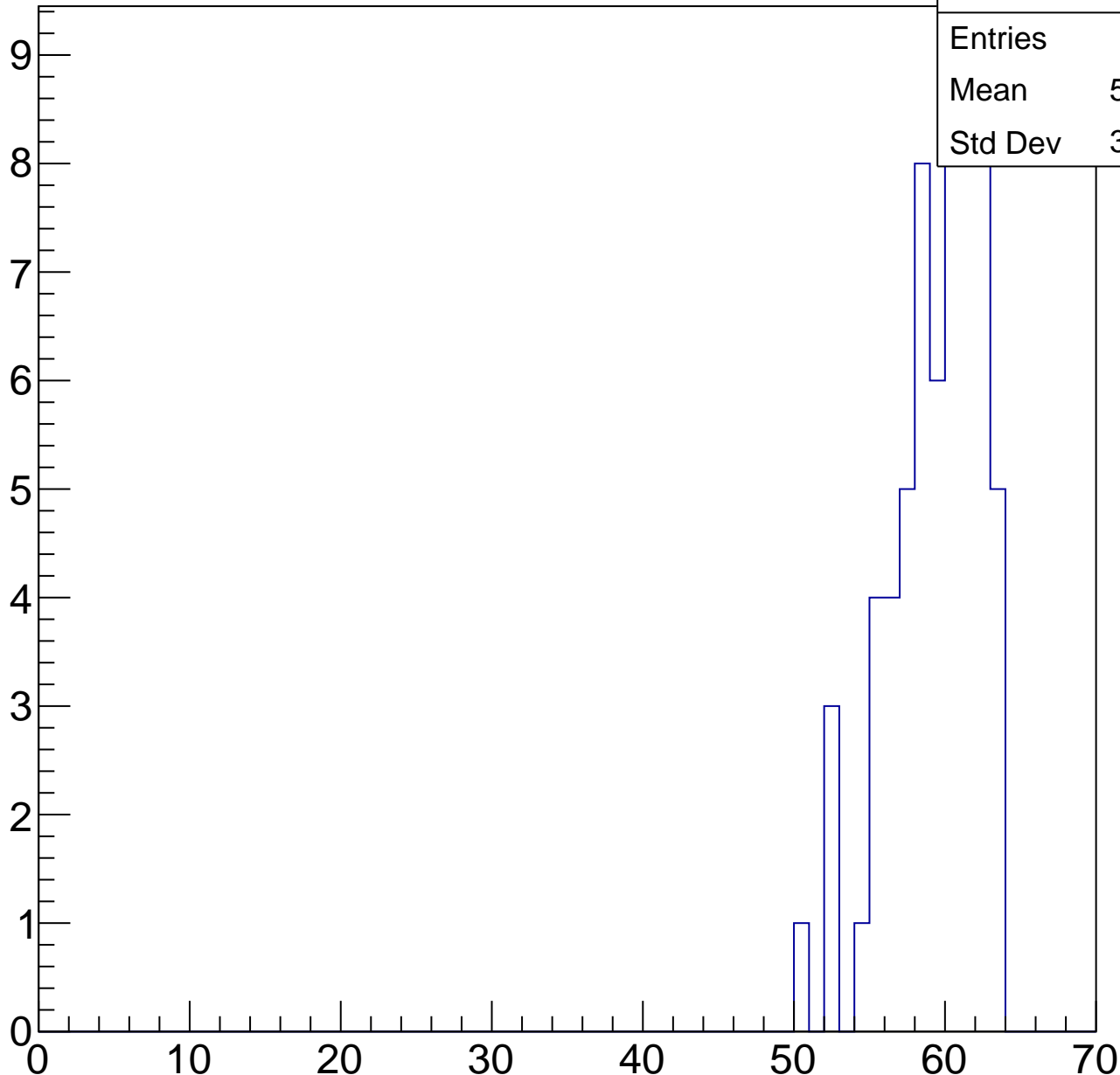
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	58.84
Std Dev	3.025

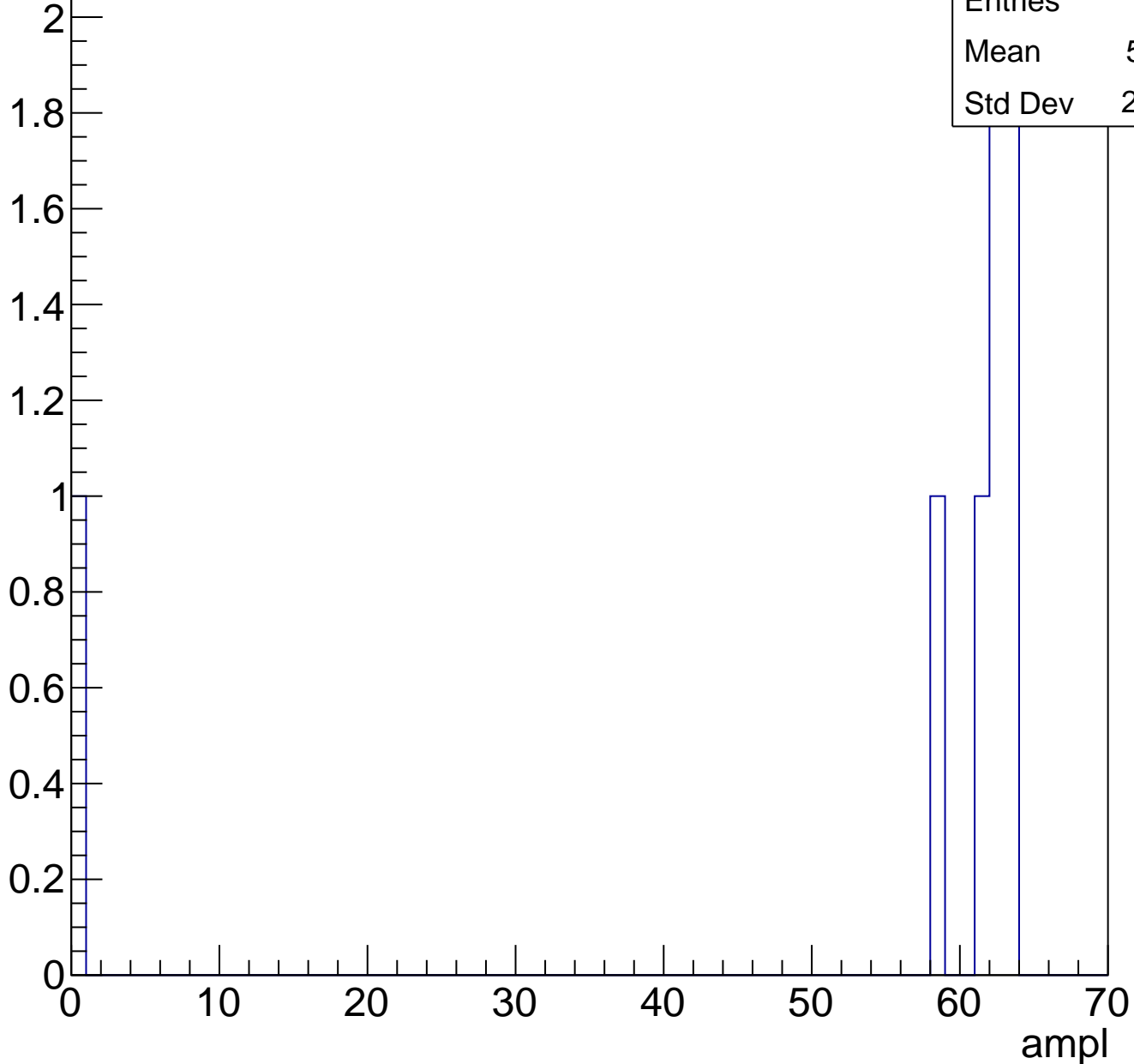
ampl



B0L002S, U26-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch82, adc0

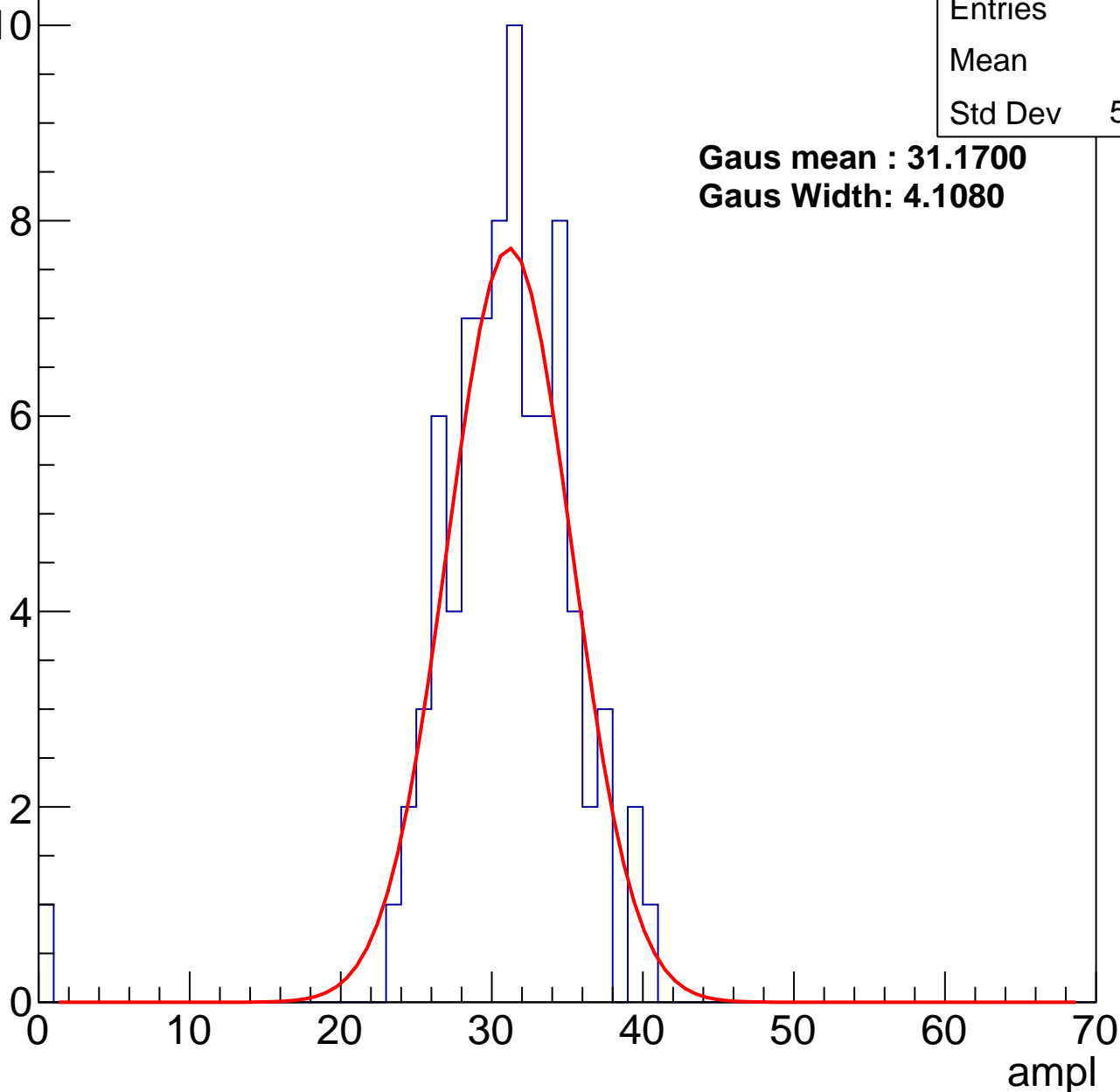
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	30.4
Std Dev	5.028

Gaus mean : 31.1700

Gaus Width: 4.1080



B0L002S, U26-ch82, adc1

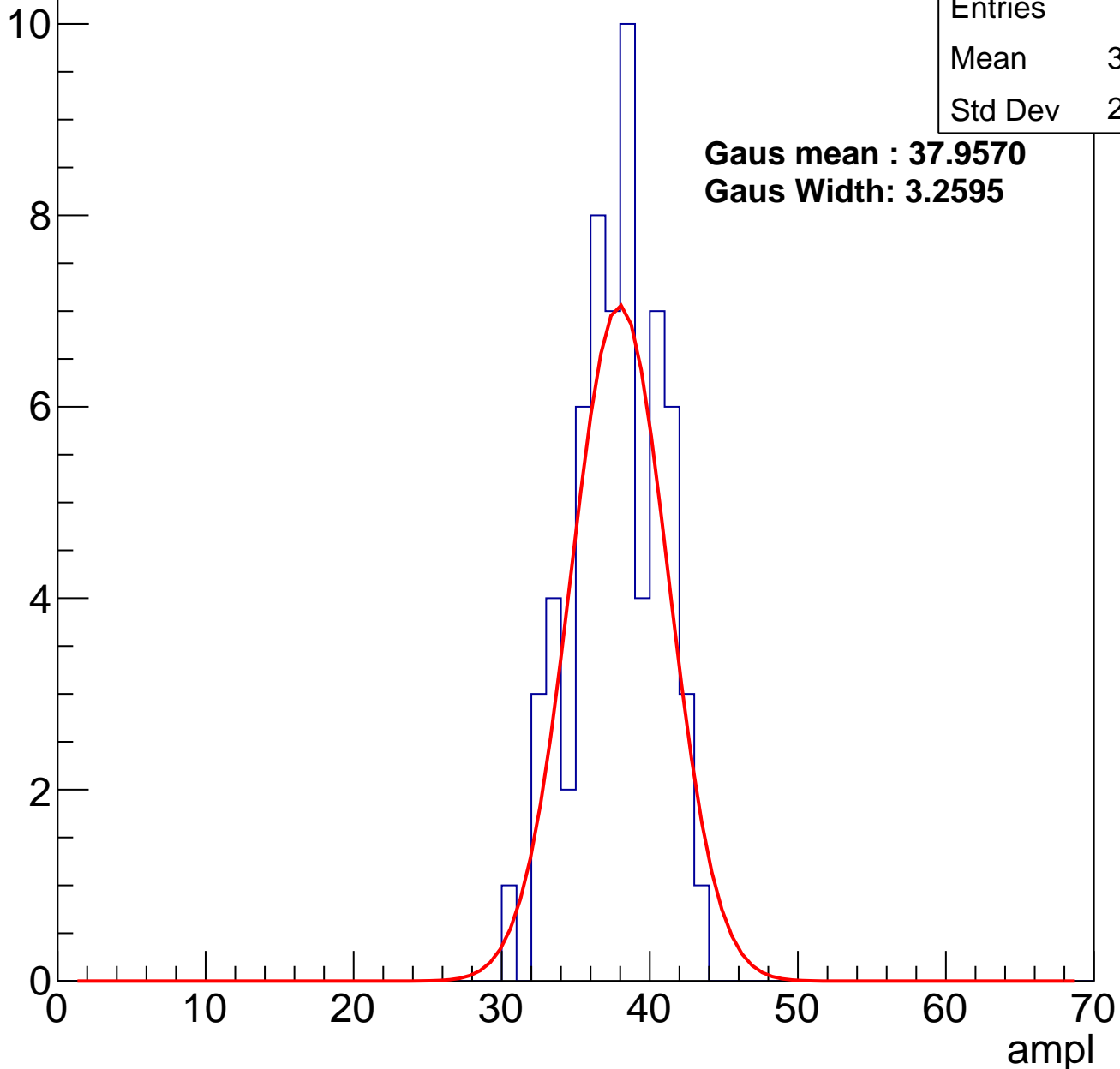
calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	37.32
Std Dev	2.917

Gaus mean : 37.9570

Gaus Width: 3.2595

Entry



B0L002S, U26-ch82, adc2

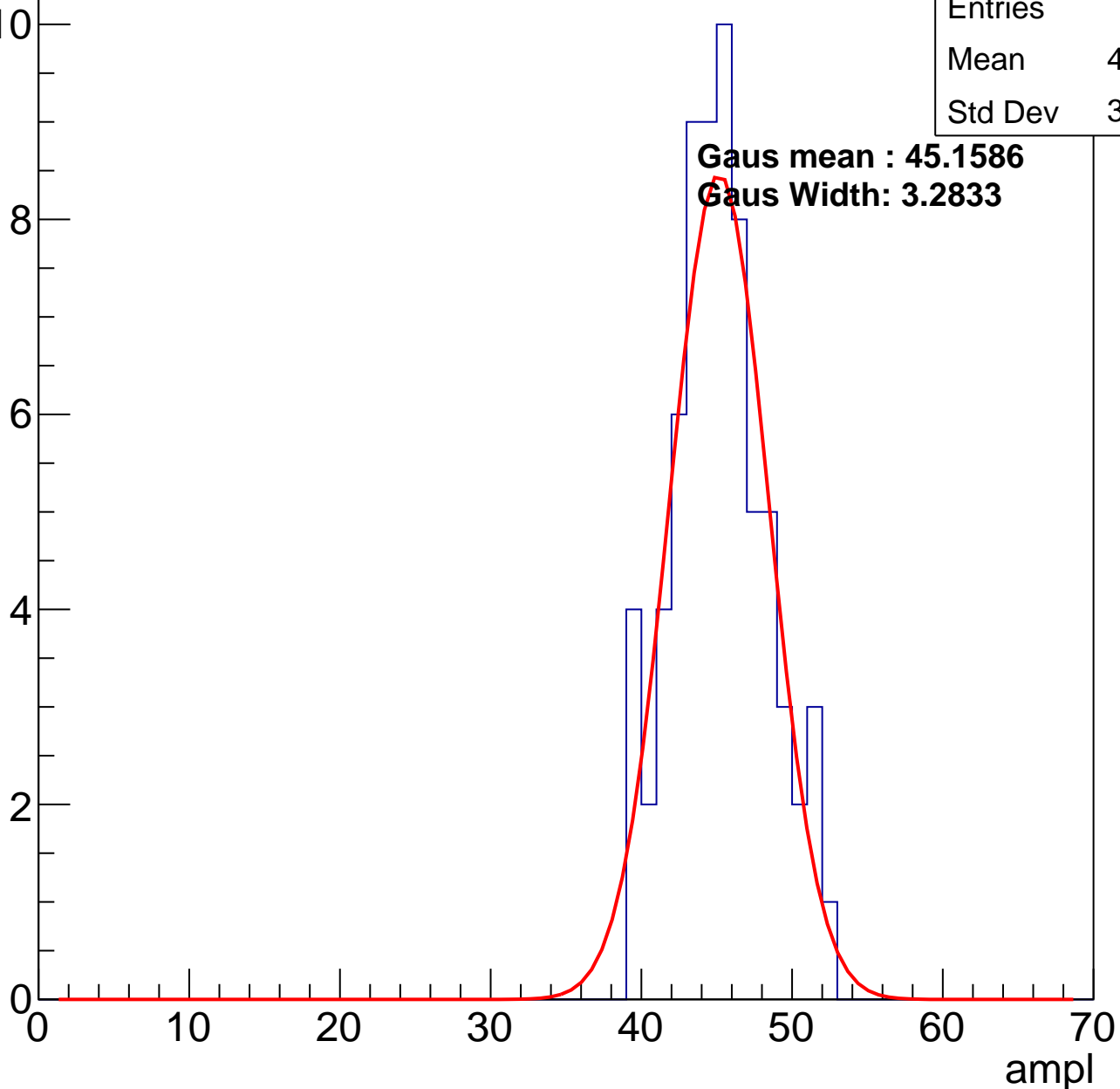
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.79
Std Dev	3.099

Gaus mean : 45.1586

Gaus Width: 3.2833

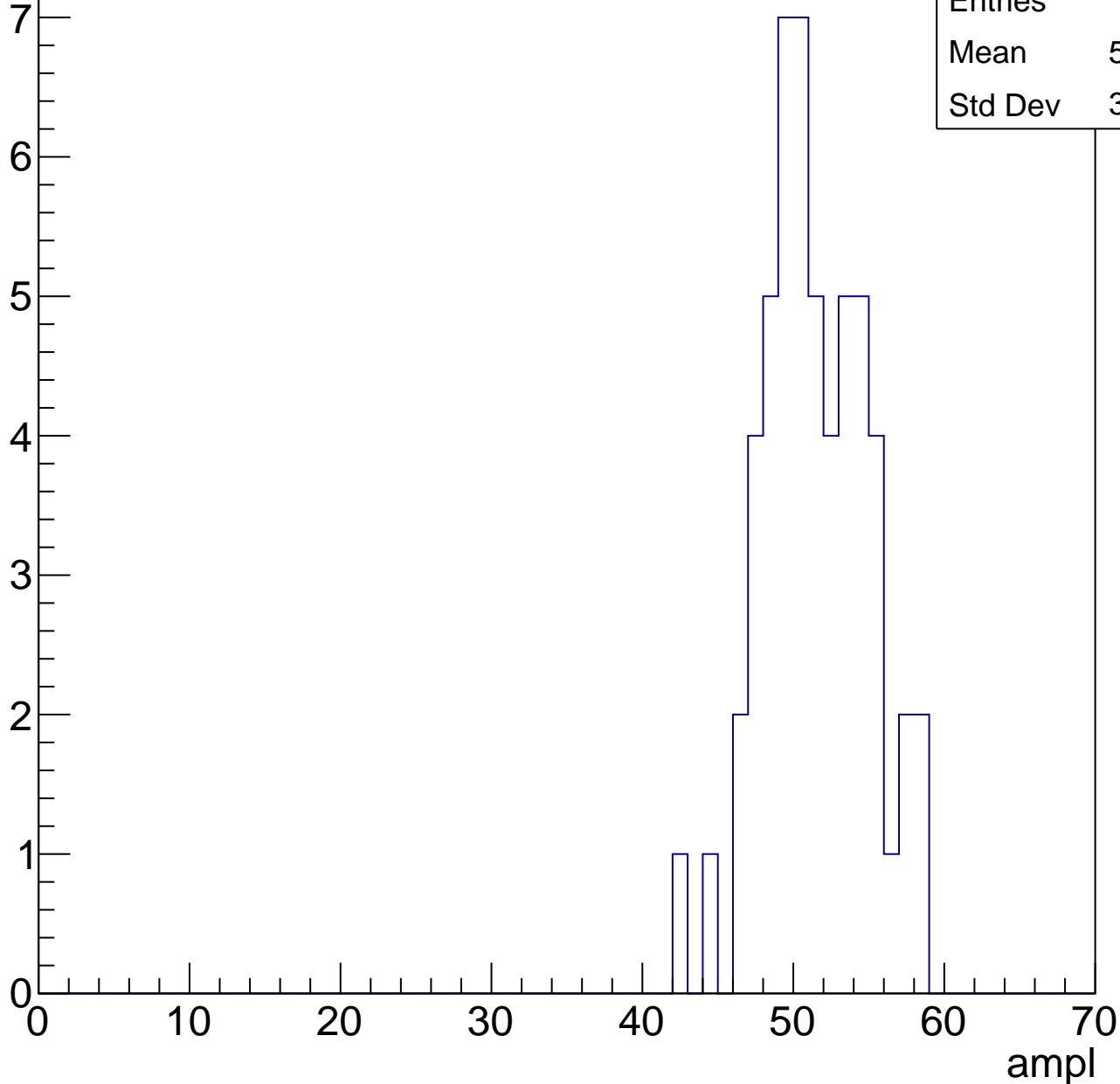


B0L002S, U26-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

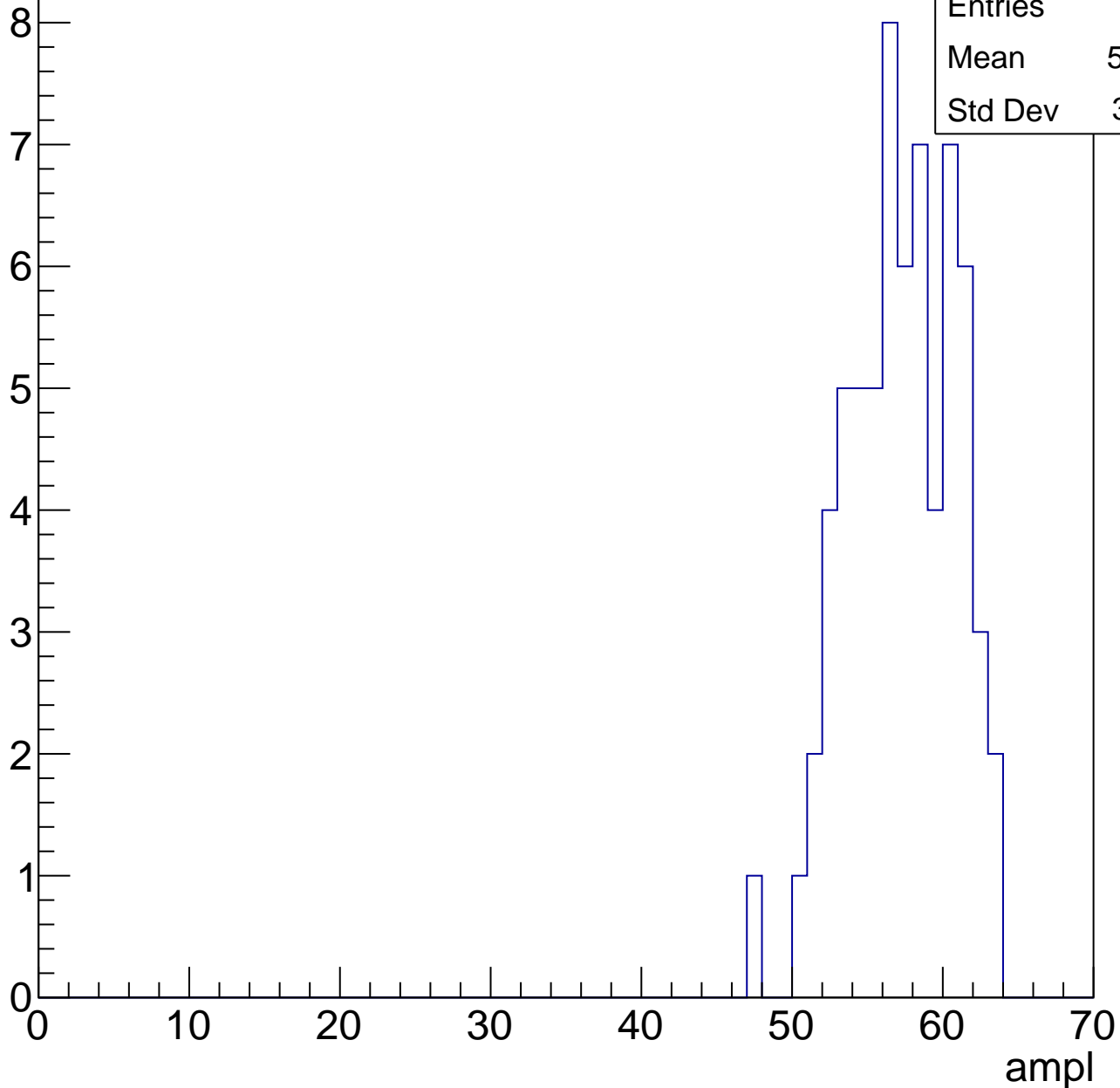
Entries	55
Mean	50.96
Std Dev	3.443



B0L002S, U26-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



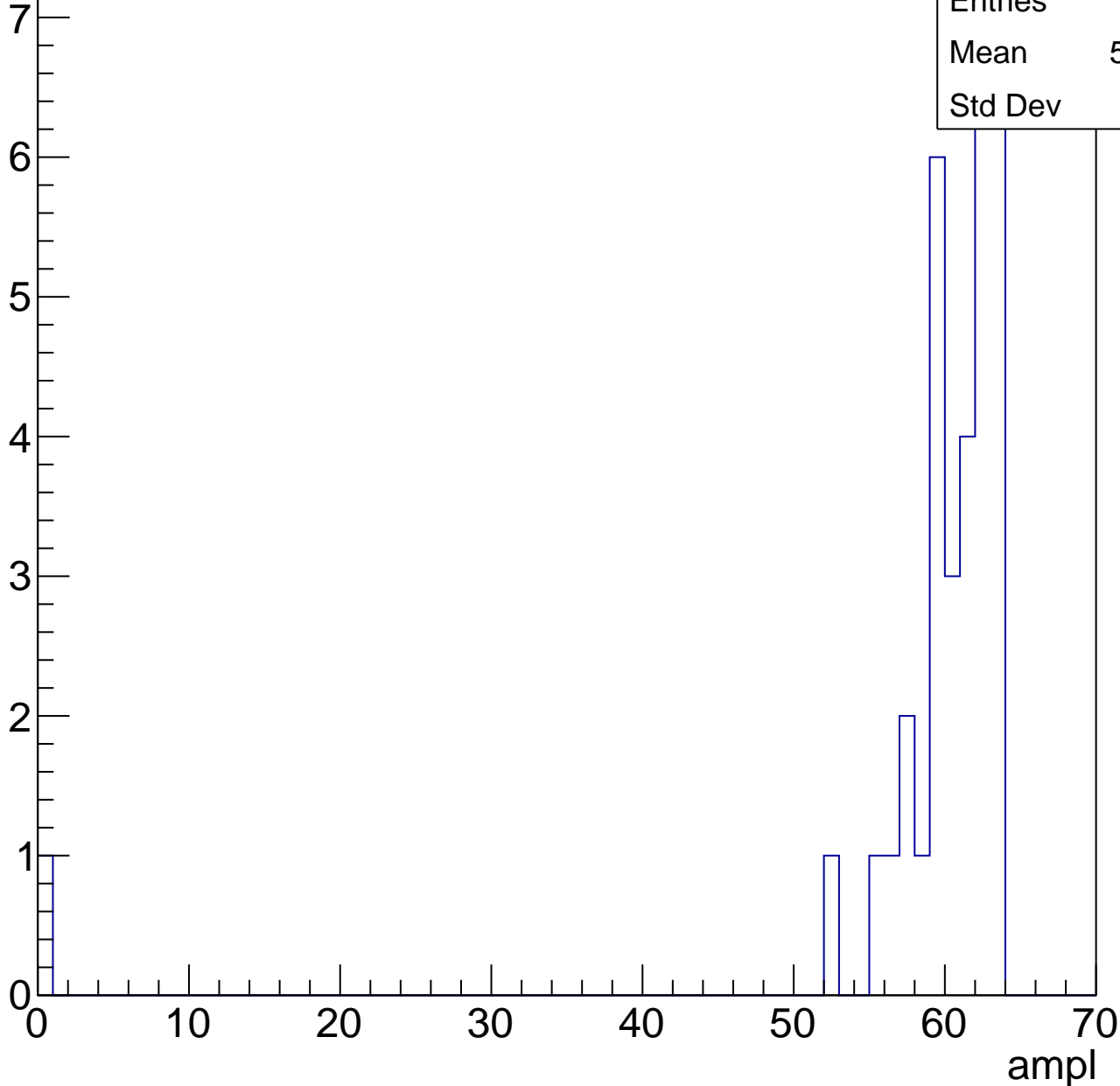
Entries	66
Mean	56.77
Std Dev	3.481

B0L002S, U26-ch82, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.47
Std Dev	10.5



B0L002S, U26-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U26-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch83, adc0

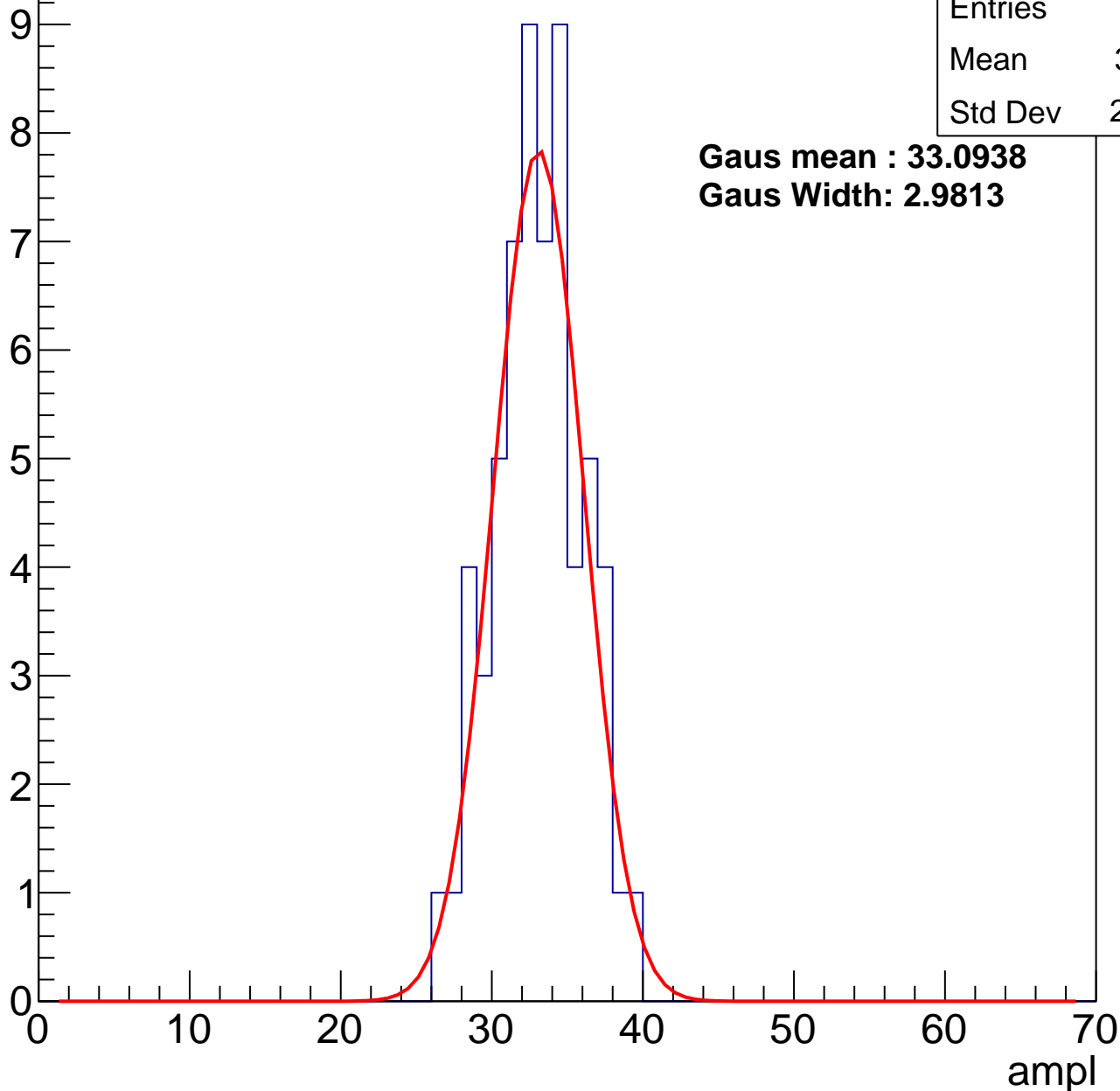
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	32.61
Std Dev	2.865

Gaus mean : 33.0938

Gaus Width: 2.9813



B0L002S, U26-ch83, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	39.12
Std Dev	3.097

Gaus mean : 39.4423

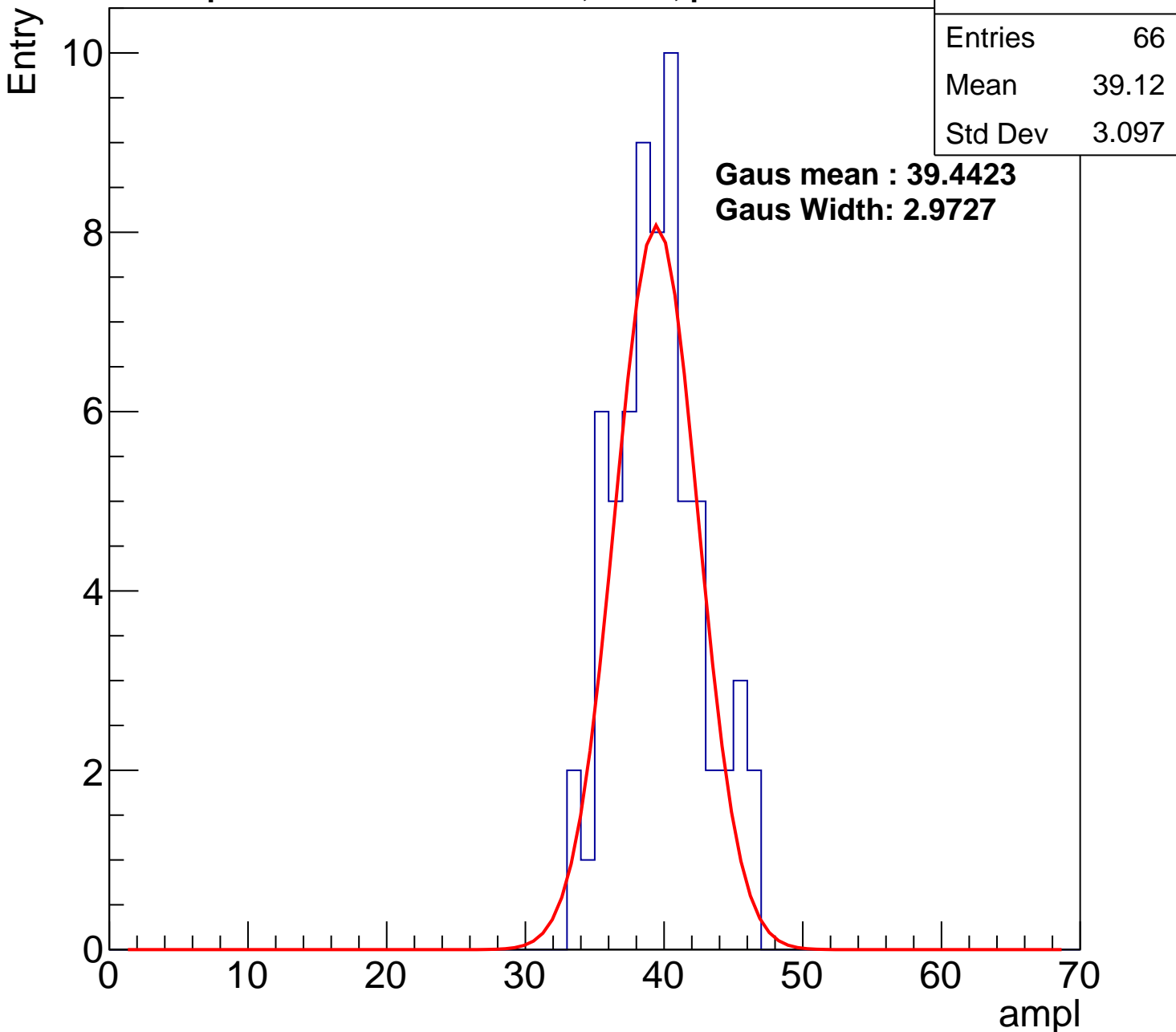
Gaus Width: 2.9727

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch83, adc2

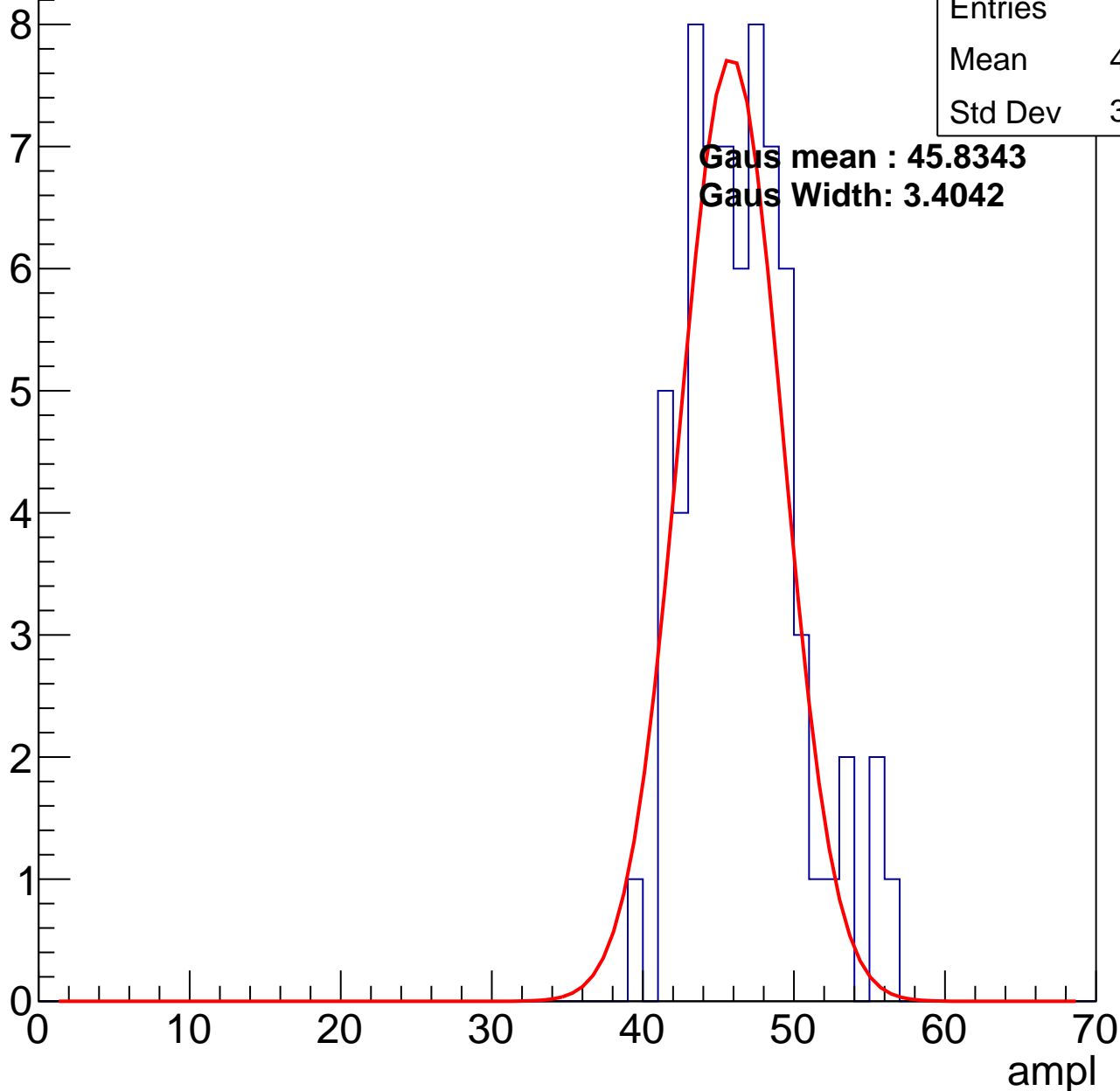
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	46.17
Std Dev	3.615

Gaus mean : 45.8343

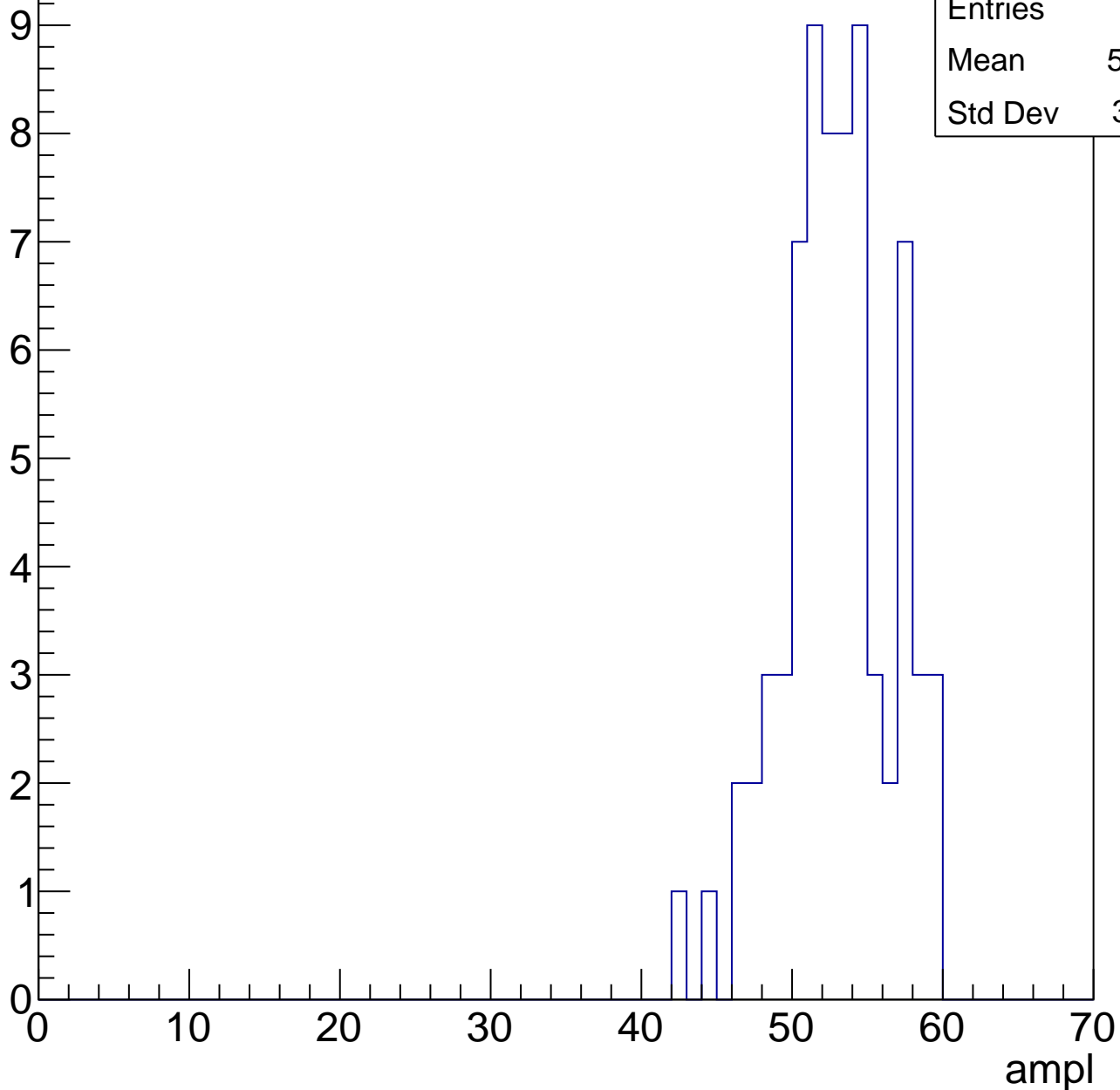
Gaus Width: 3.4042



B0L002S, U26-ch83, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



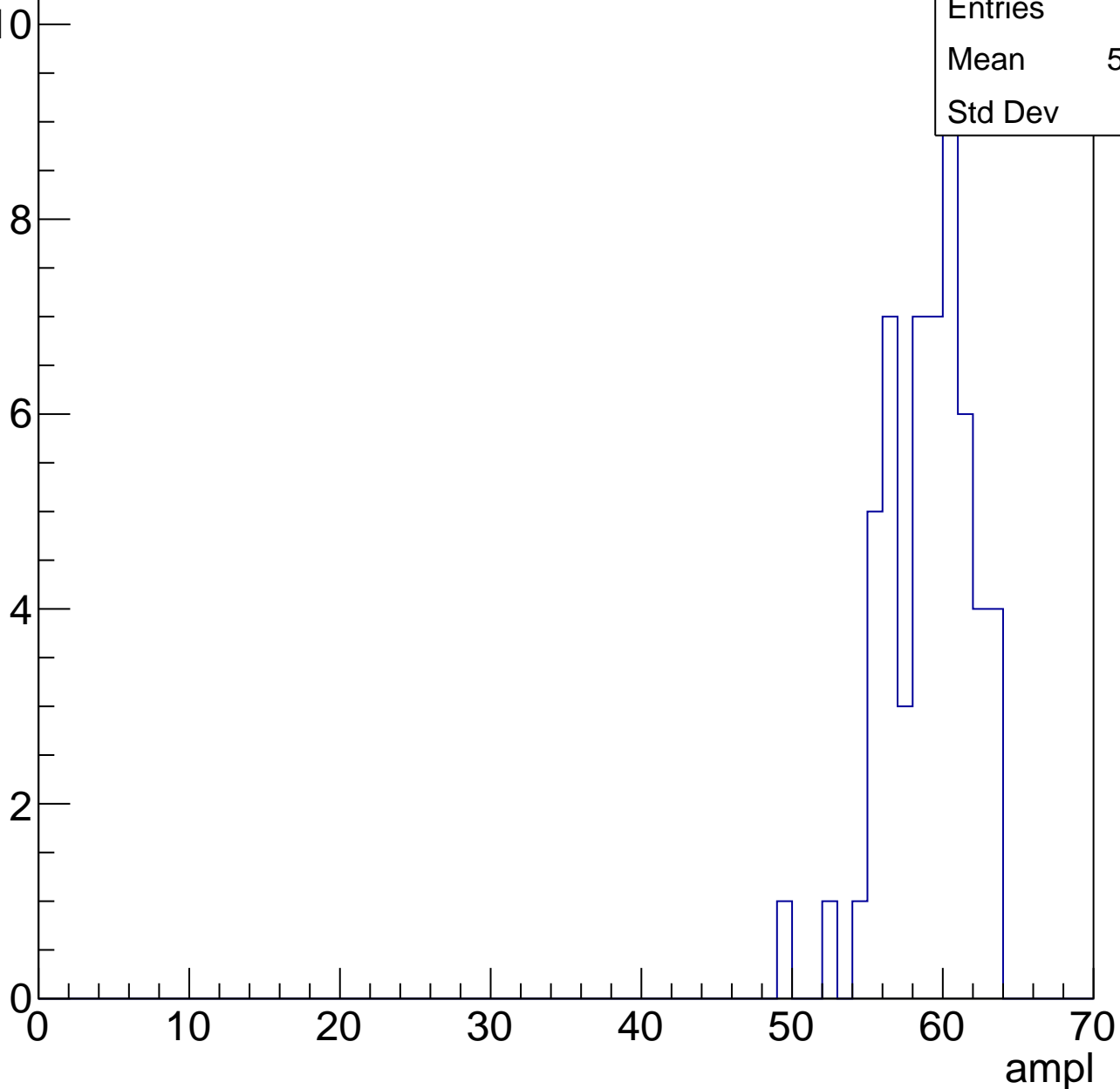
Entries	71
Mean	52.46
Std Dev	3.591

B0L002S, U26-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	58.54
Std Dev	2.86

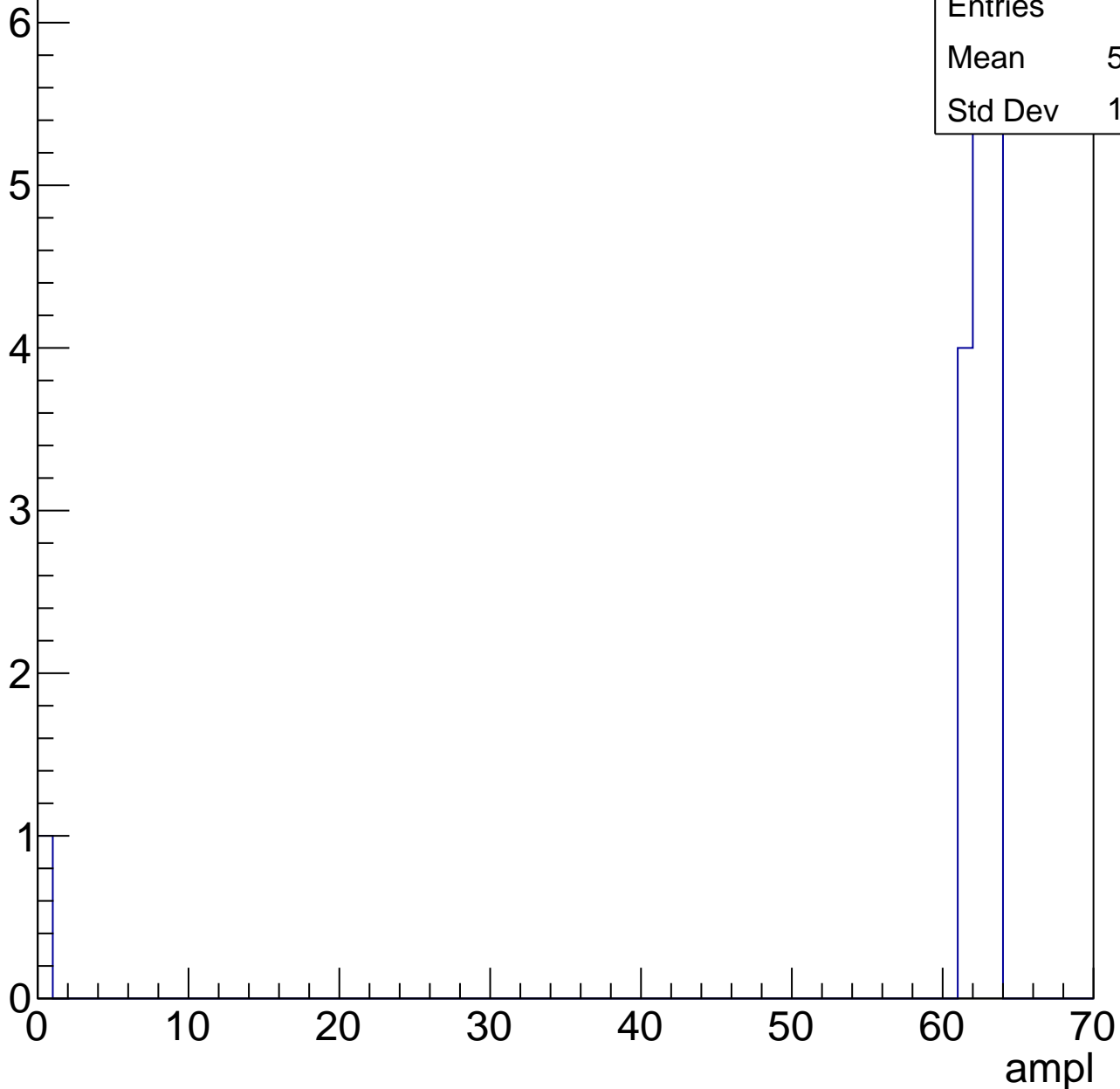


B0L002S, U26-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	58.47
Std Dev	14.64



B0L002S, U26-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

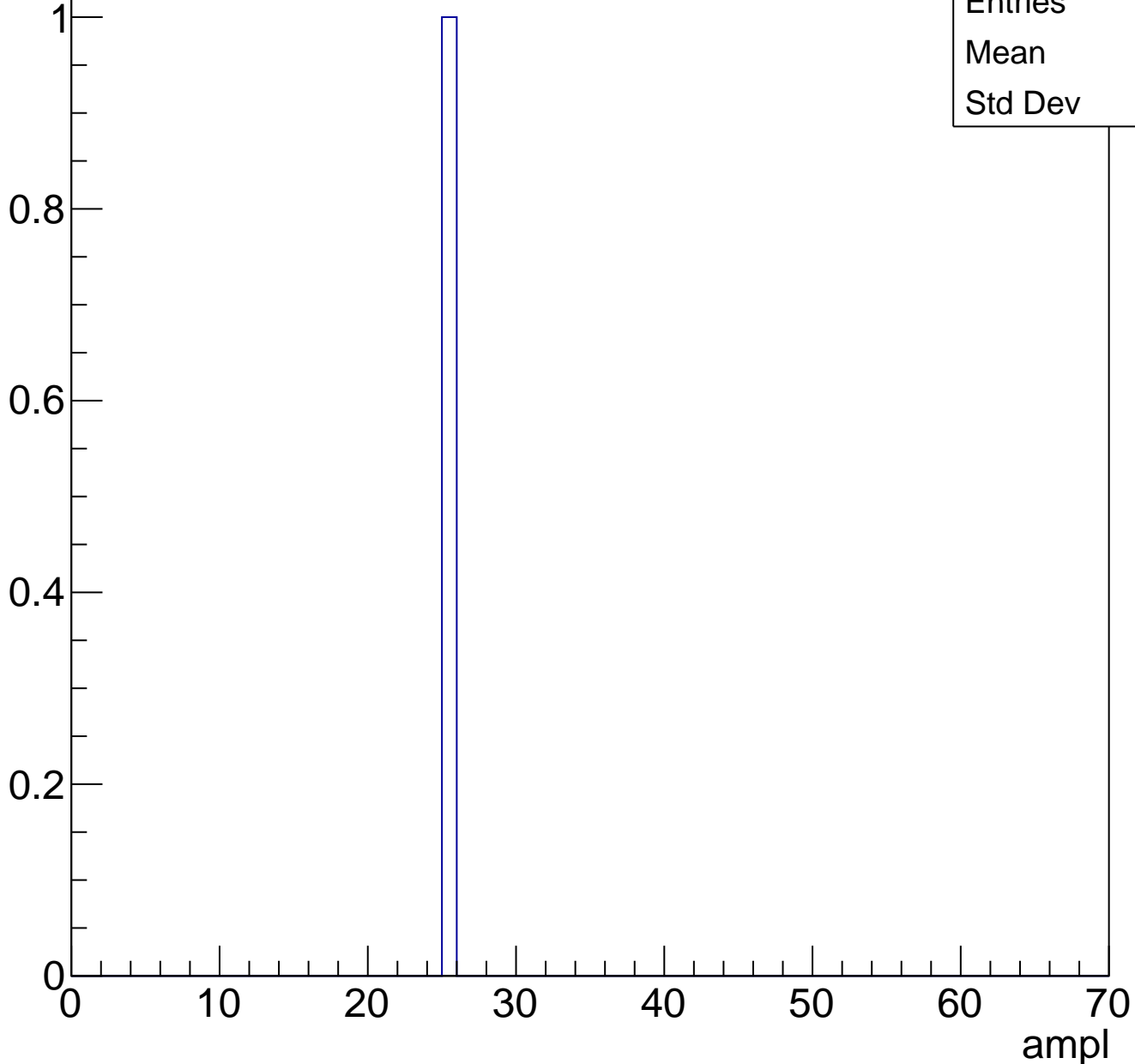


Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	25
Std Dev	0

B0L002S, U26-ch84, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	31.15
Std Dev	4.845

Gaus mean : 32.1000

Gaus Width: 2.5280

Entry

10
8
6
4
2
0

ampl

0

10

20

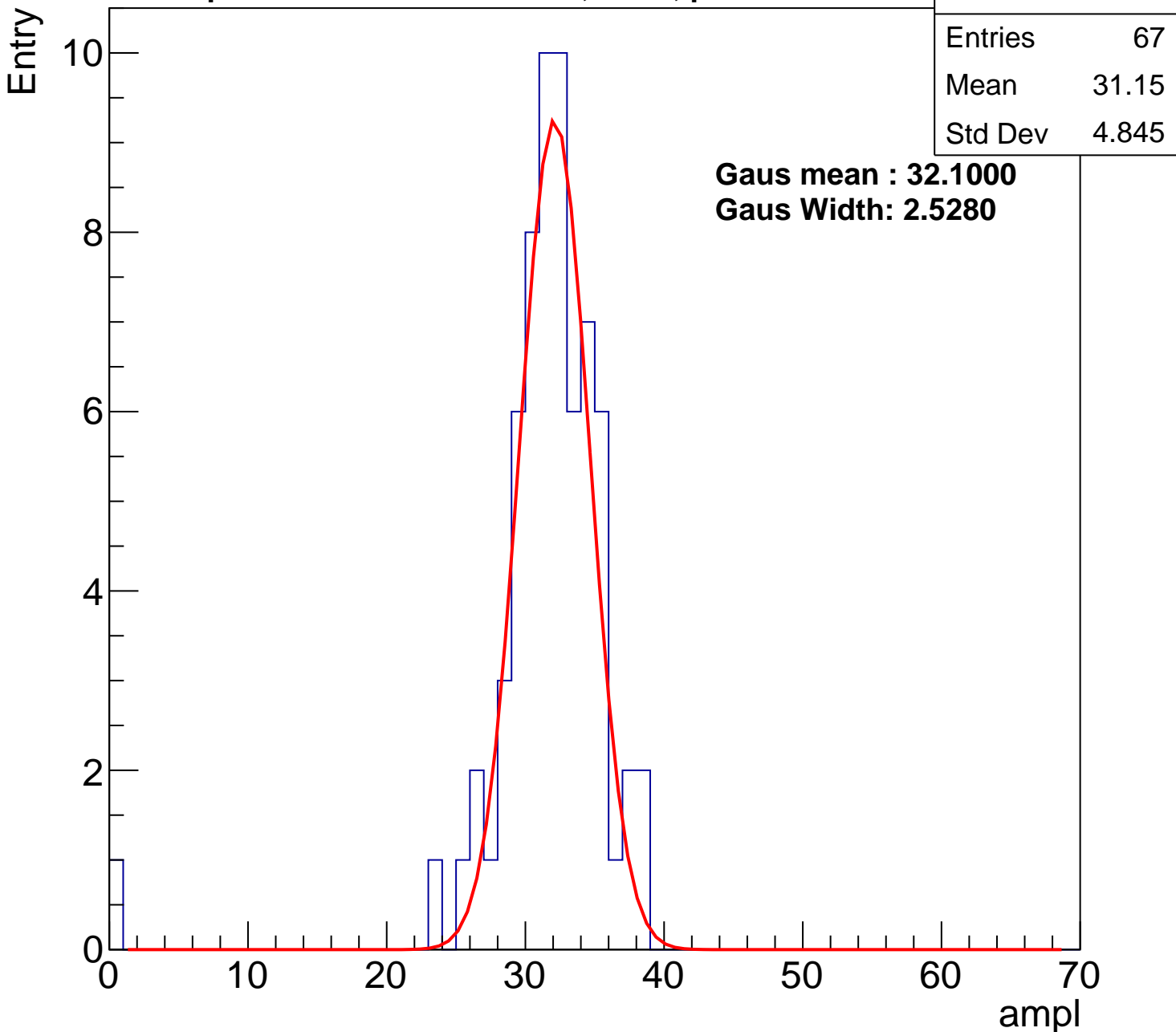
30

40

50

60

70



B0L002S, U26-ch84, adc1

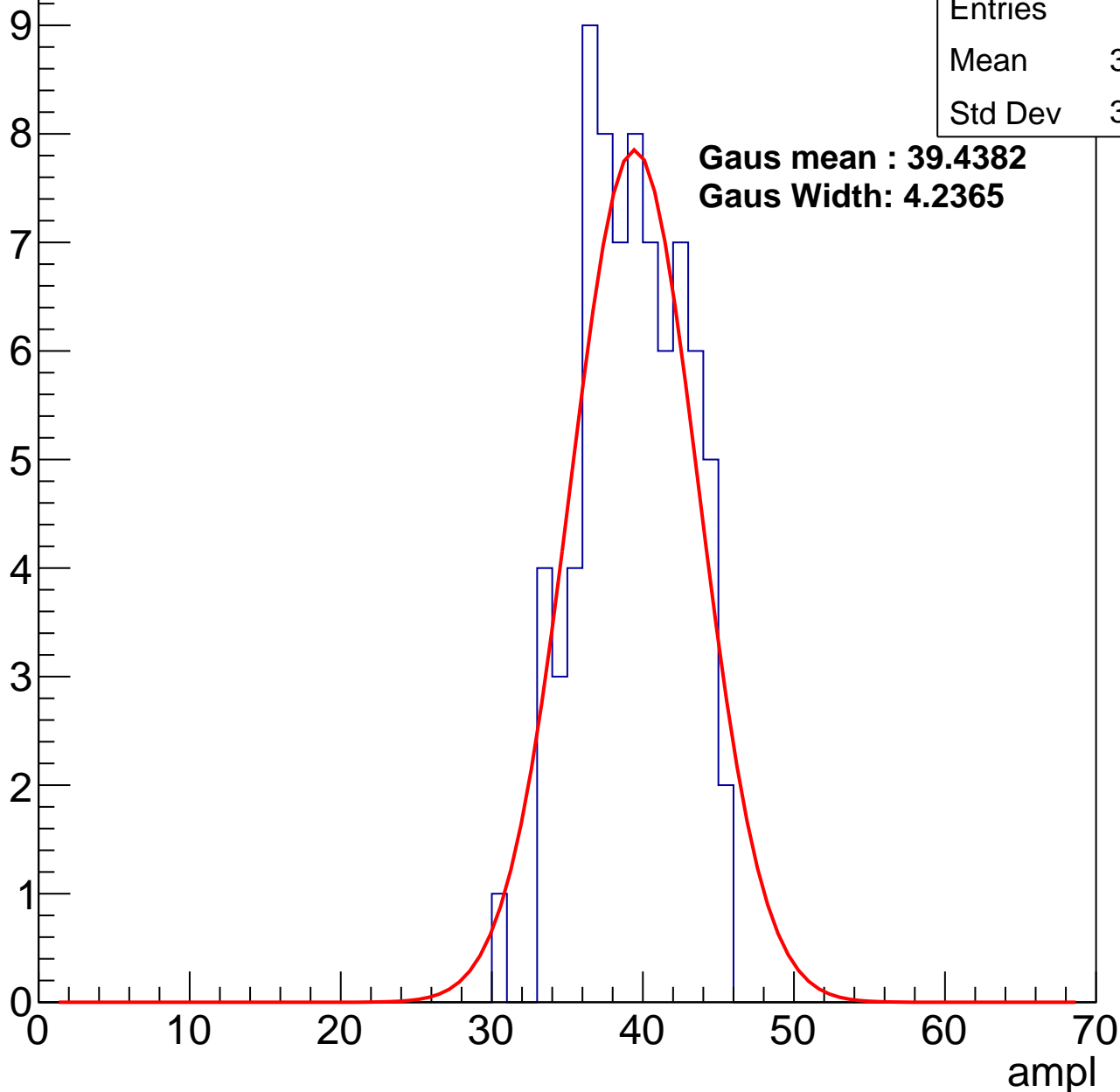
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	38.83
Std Dev	3.359

Gaus mean : 39.4382

Gaus Width: 4.2365



B0L002S, U26-ch84, adc2

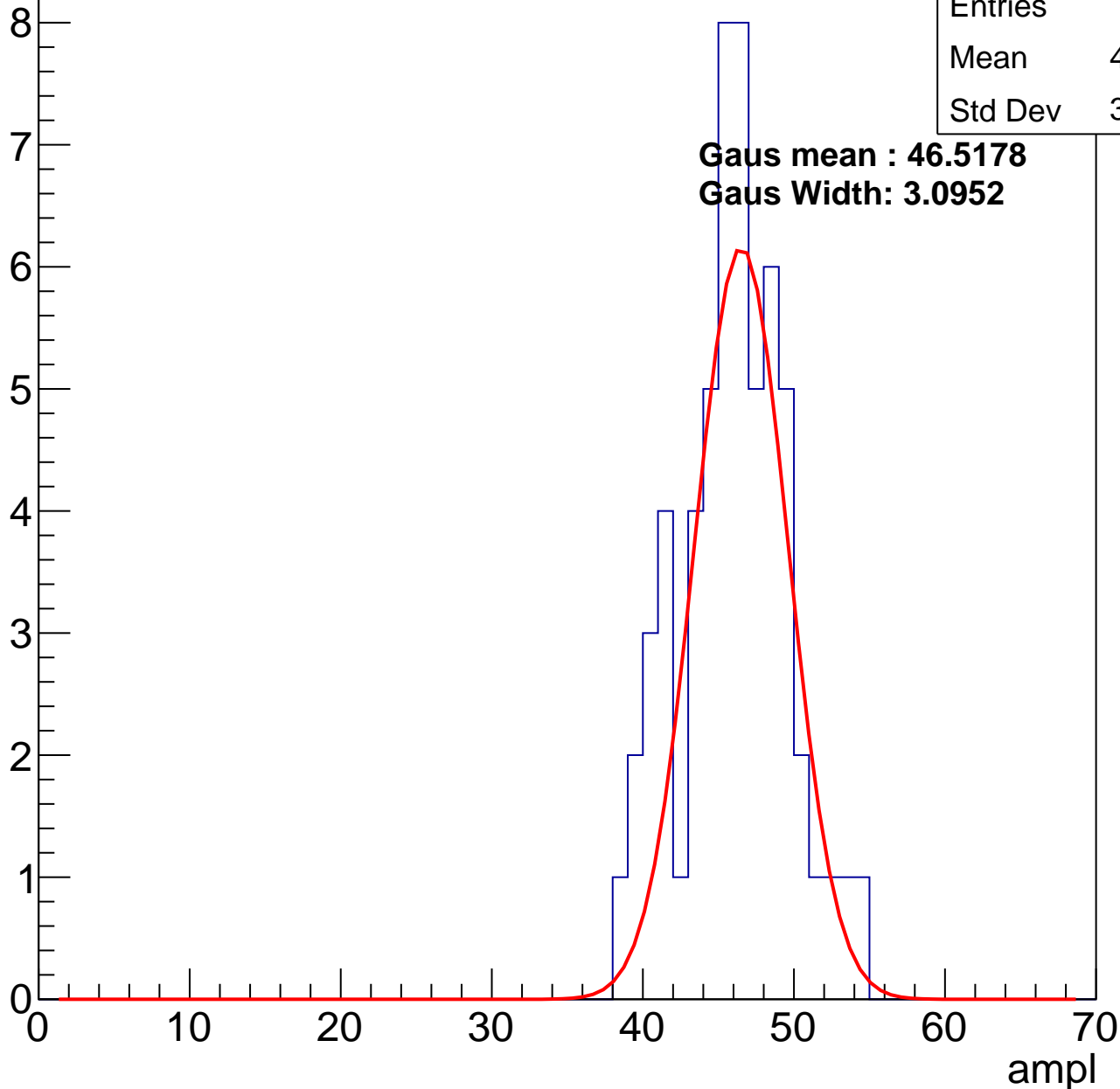
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	45.52
Std Dev	3.515

Gaus mean : 46.5178

Gaus Width: 3.0952

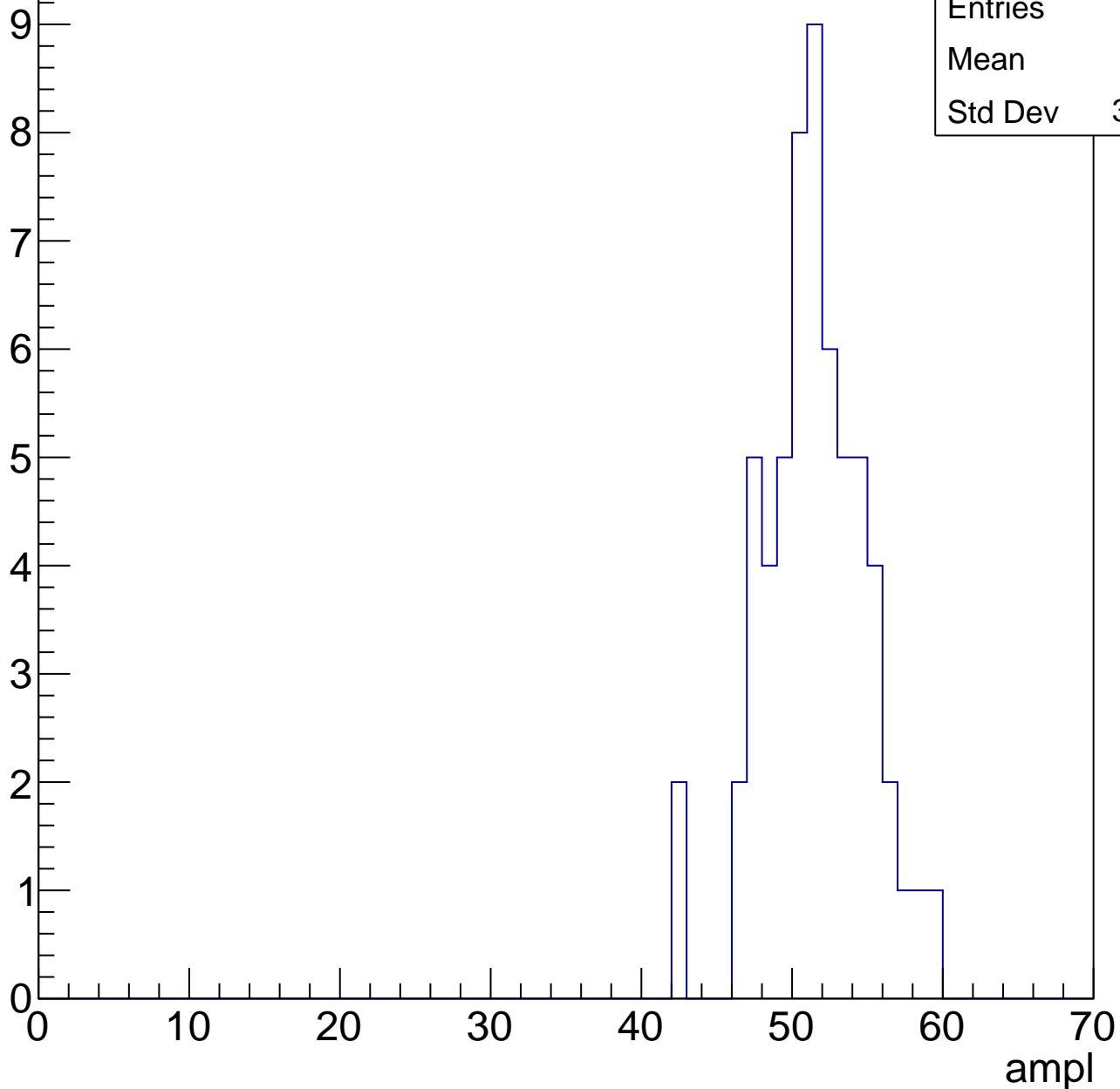


B0L002S, U26-ch84, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	51
Std Dev	3.391

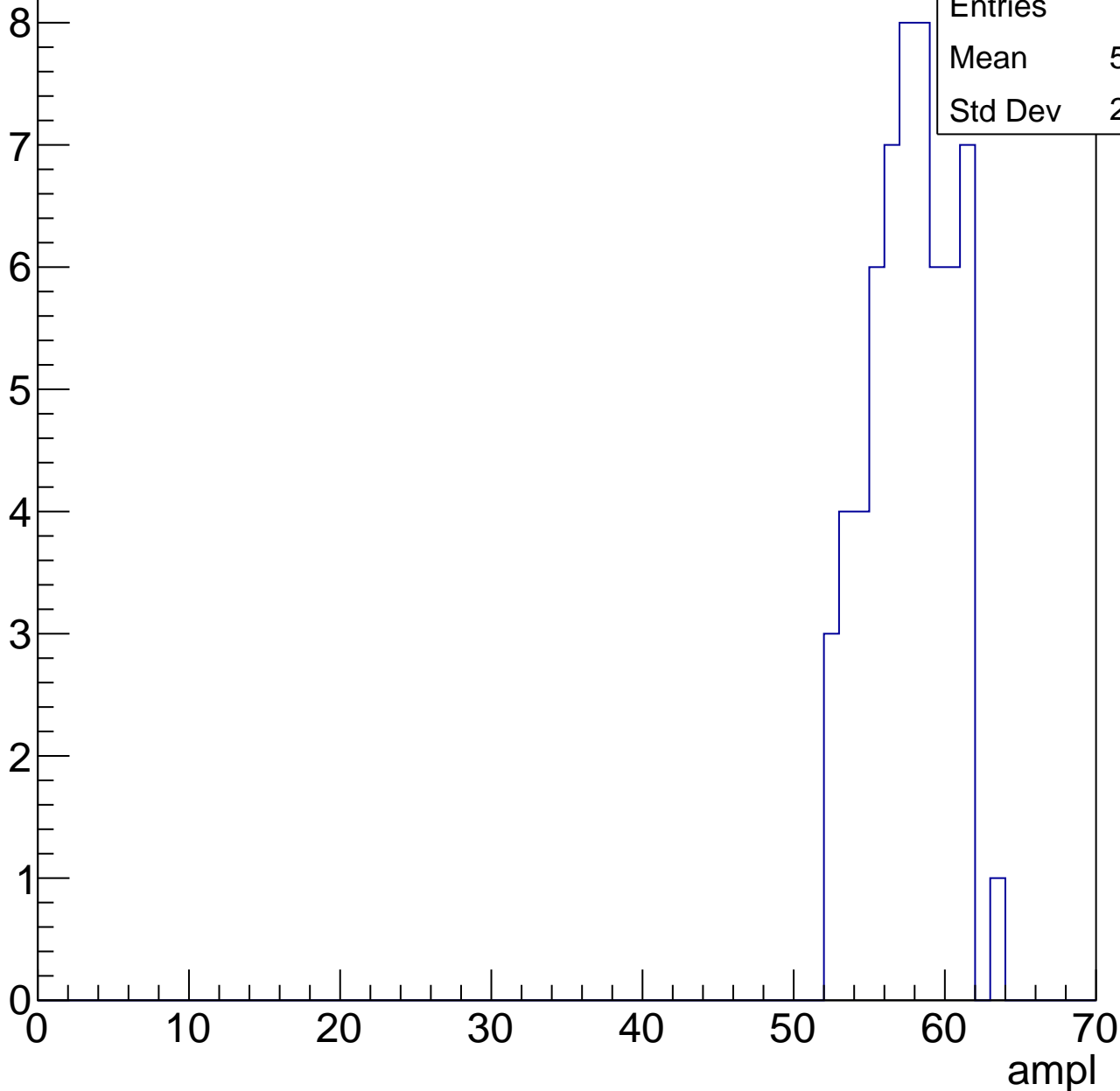


B0L002S, U26-ch84, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	57.17
Std Dev	2.703

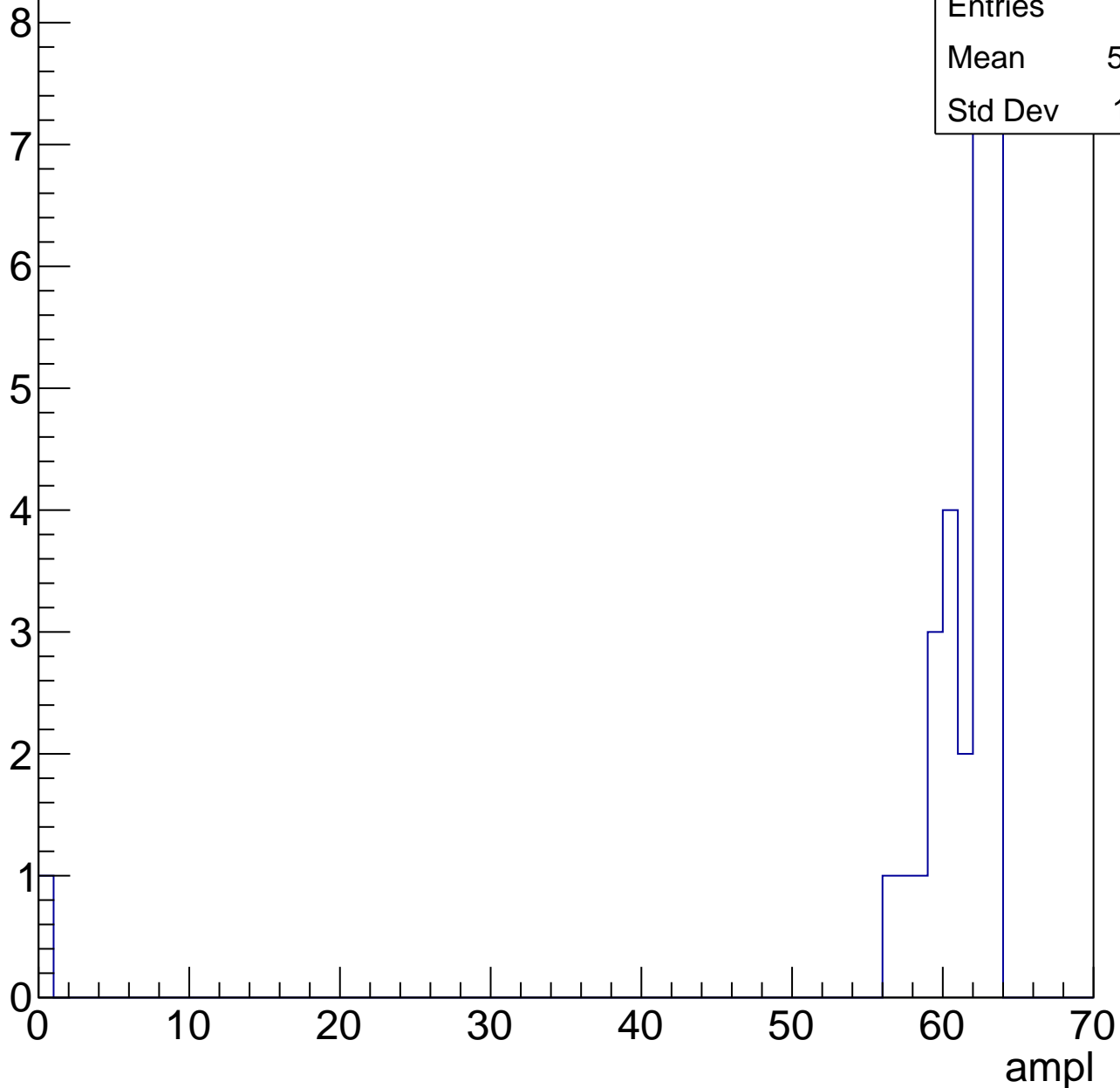


B0L002S, U26-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.97
Std Dev	11.31



B0L002S, U26-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B0L002S, U26-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch85, adc0

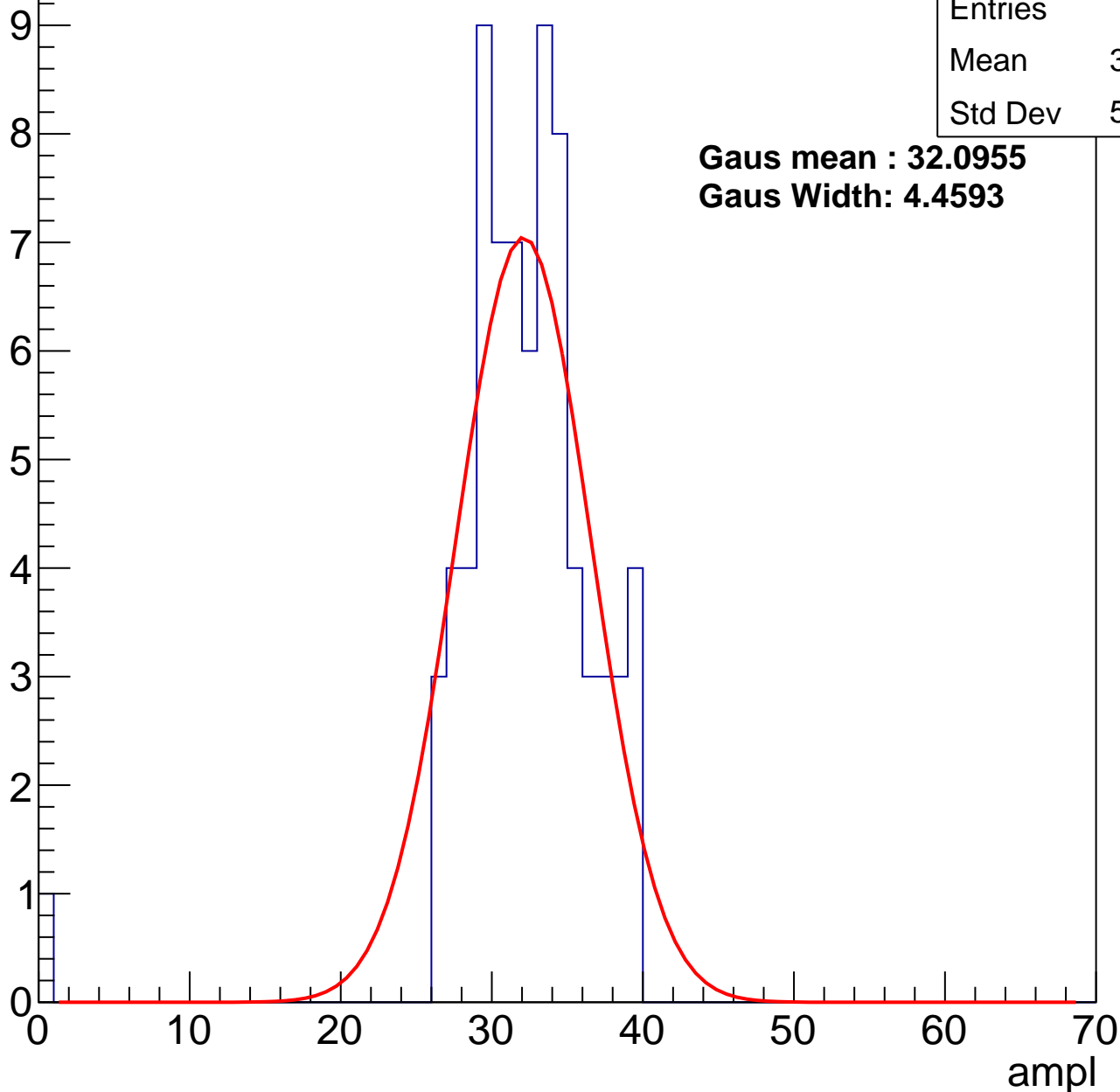
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.68
Std Dev	5.044

Gaus mean : 32.0955

Gaus Width: 4.4593

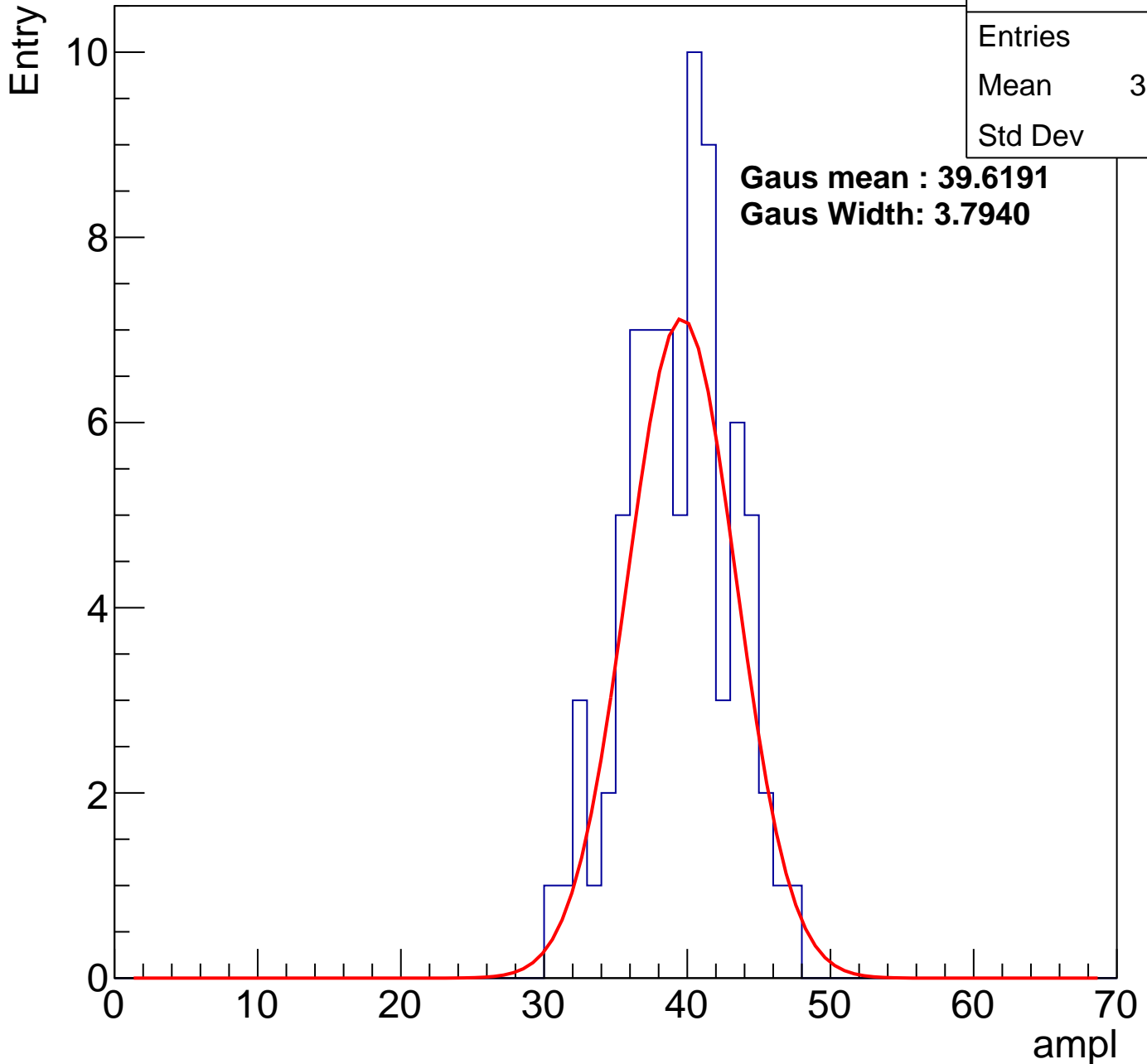


B0L002S, U26-ch85, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	38.96
Std Dev	3.69

Gaus mean : 39.6191
Gaus Width: 3.7940



B0L002S, U26-ch85, adc2

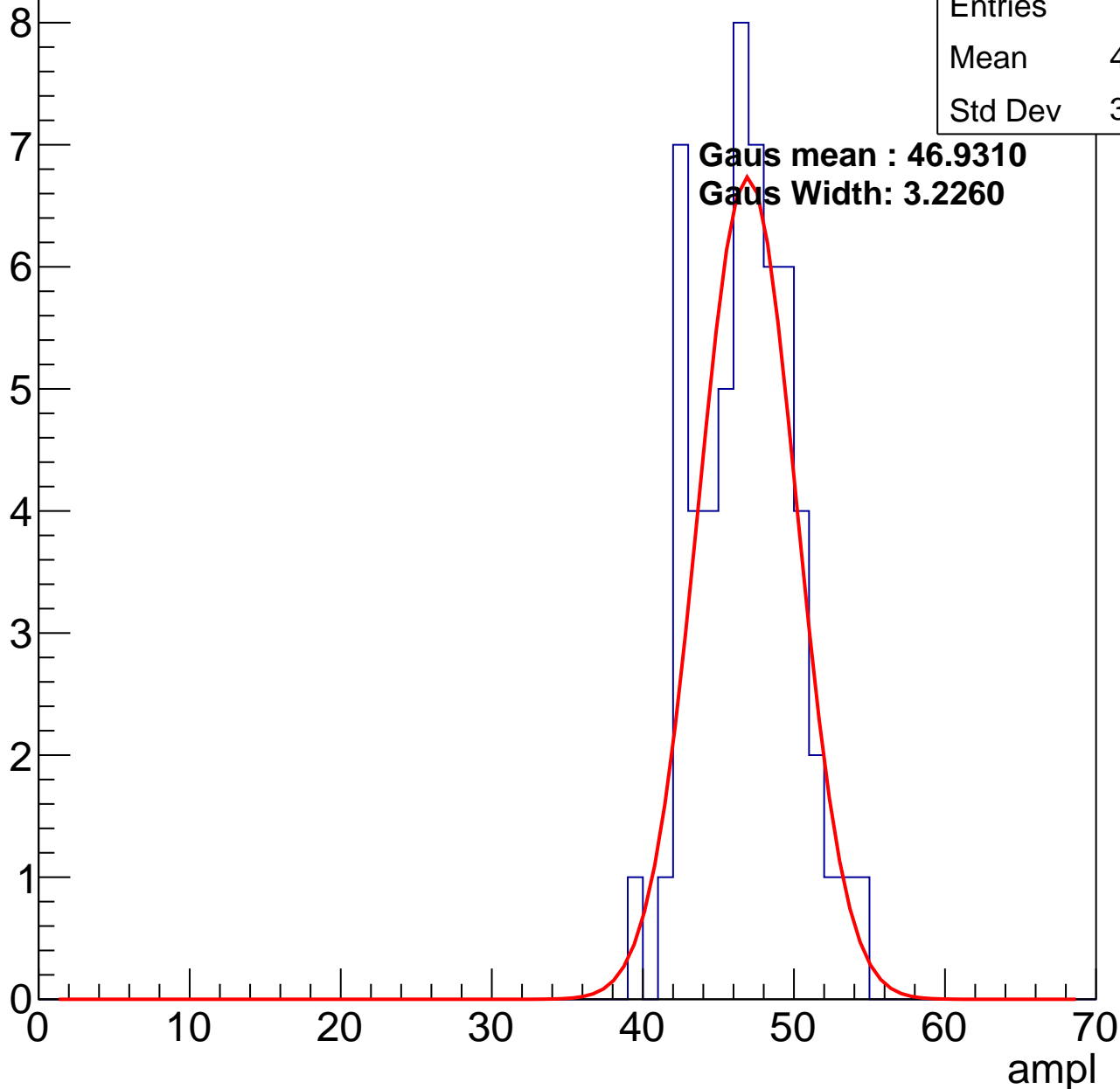
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	46.33
Std Dev	3.175

Gaus mean : 46.9310

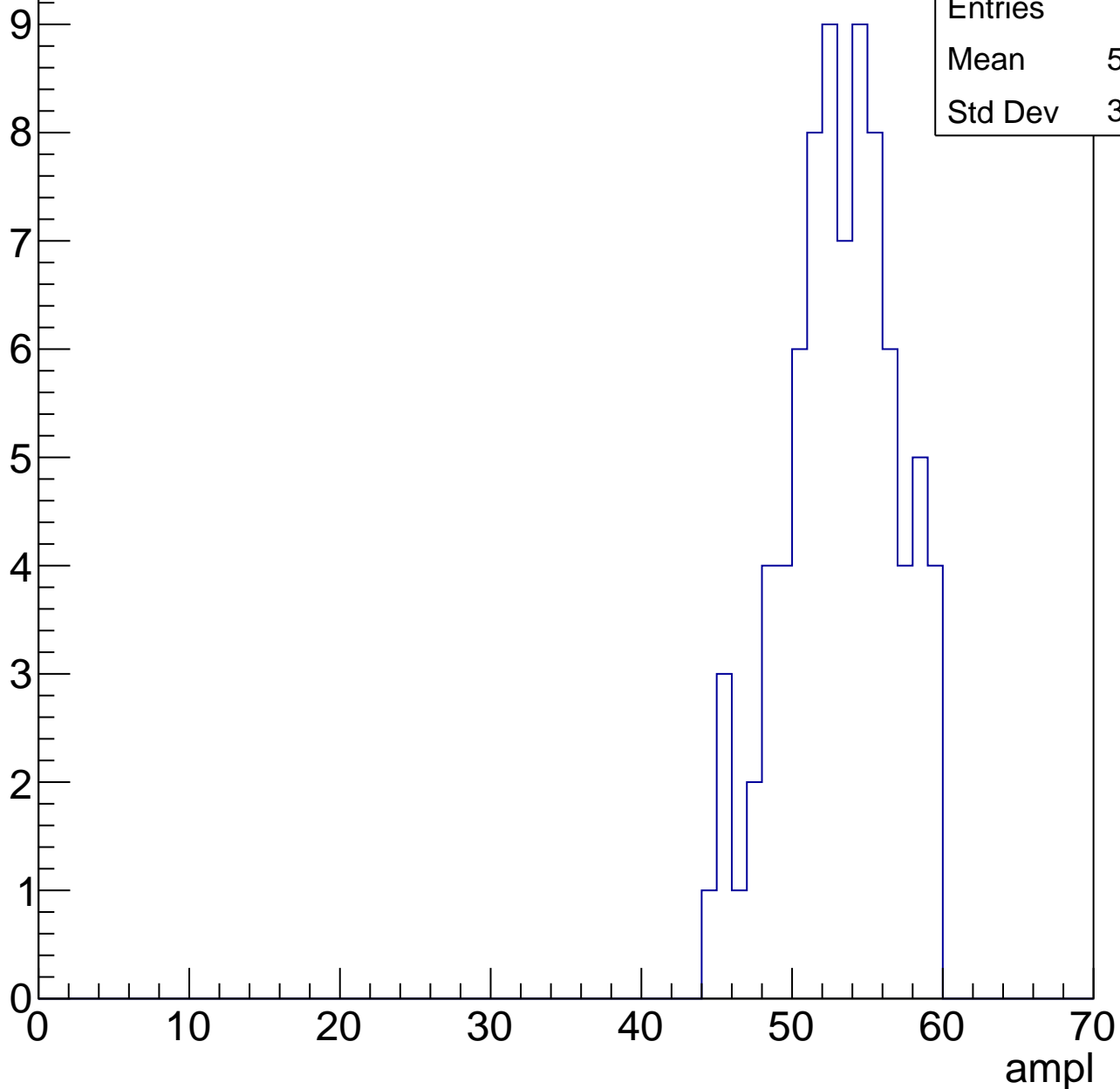
Gaus Width: 3.2260



B0L002S, U26-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



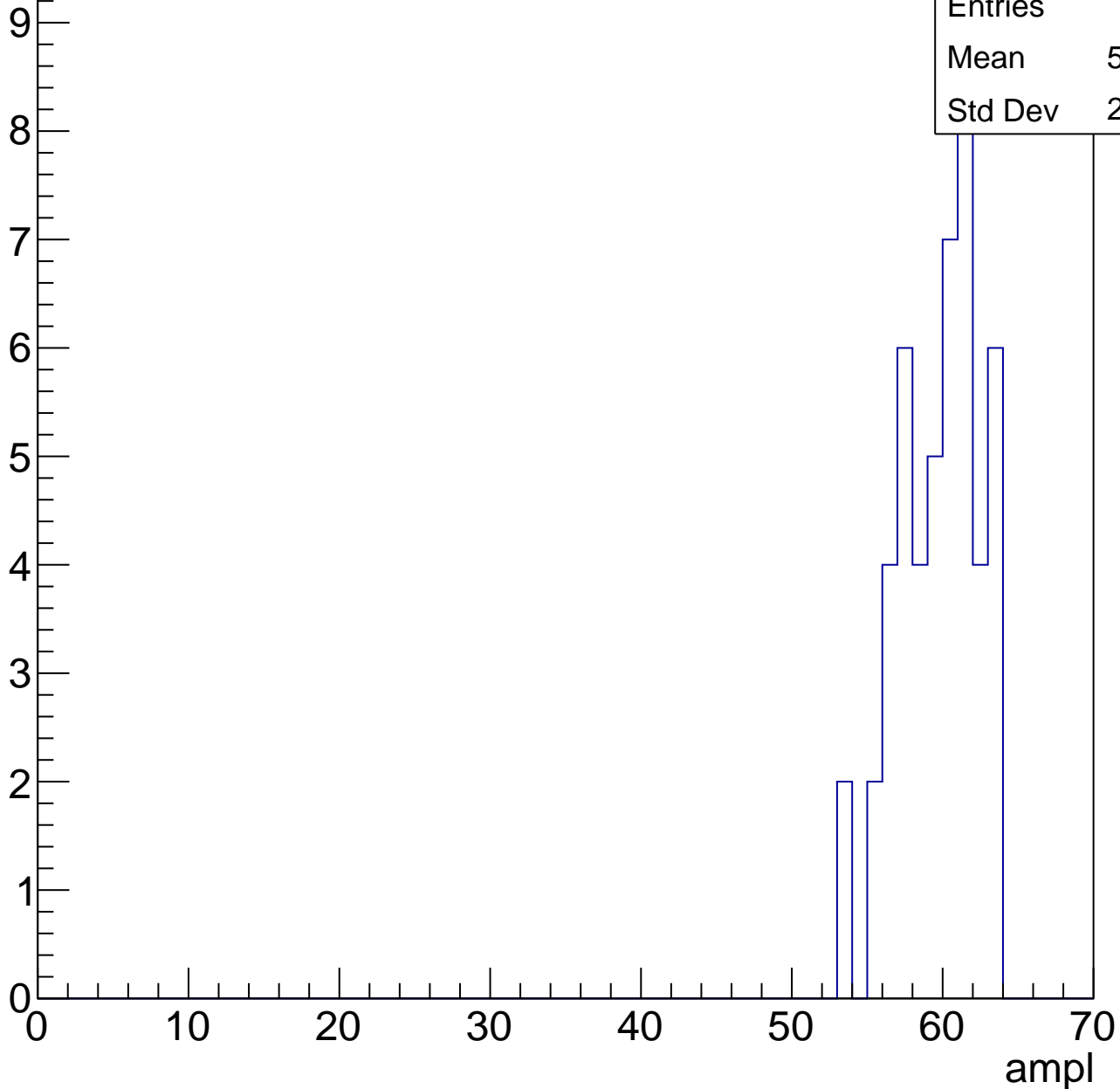
Entries	81
Mean	52.72
Std Dev	3.642

B0L002S, U26-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	59.27
Std Dev	2.632

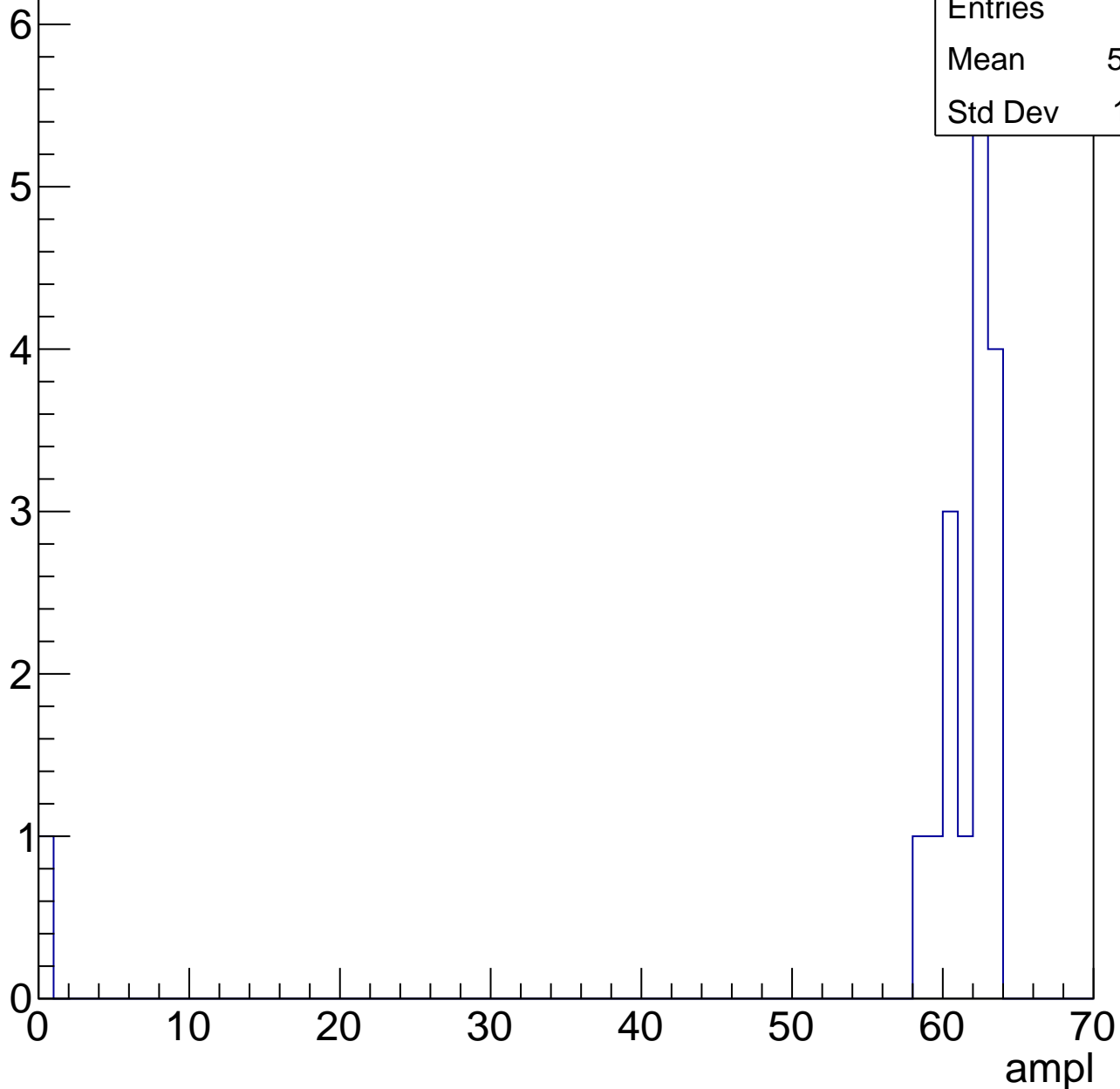


B0L002S, U26-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	57.76
Std Dev	14.51



B0L002S, U26-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

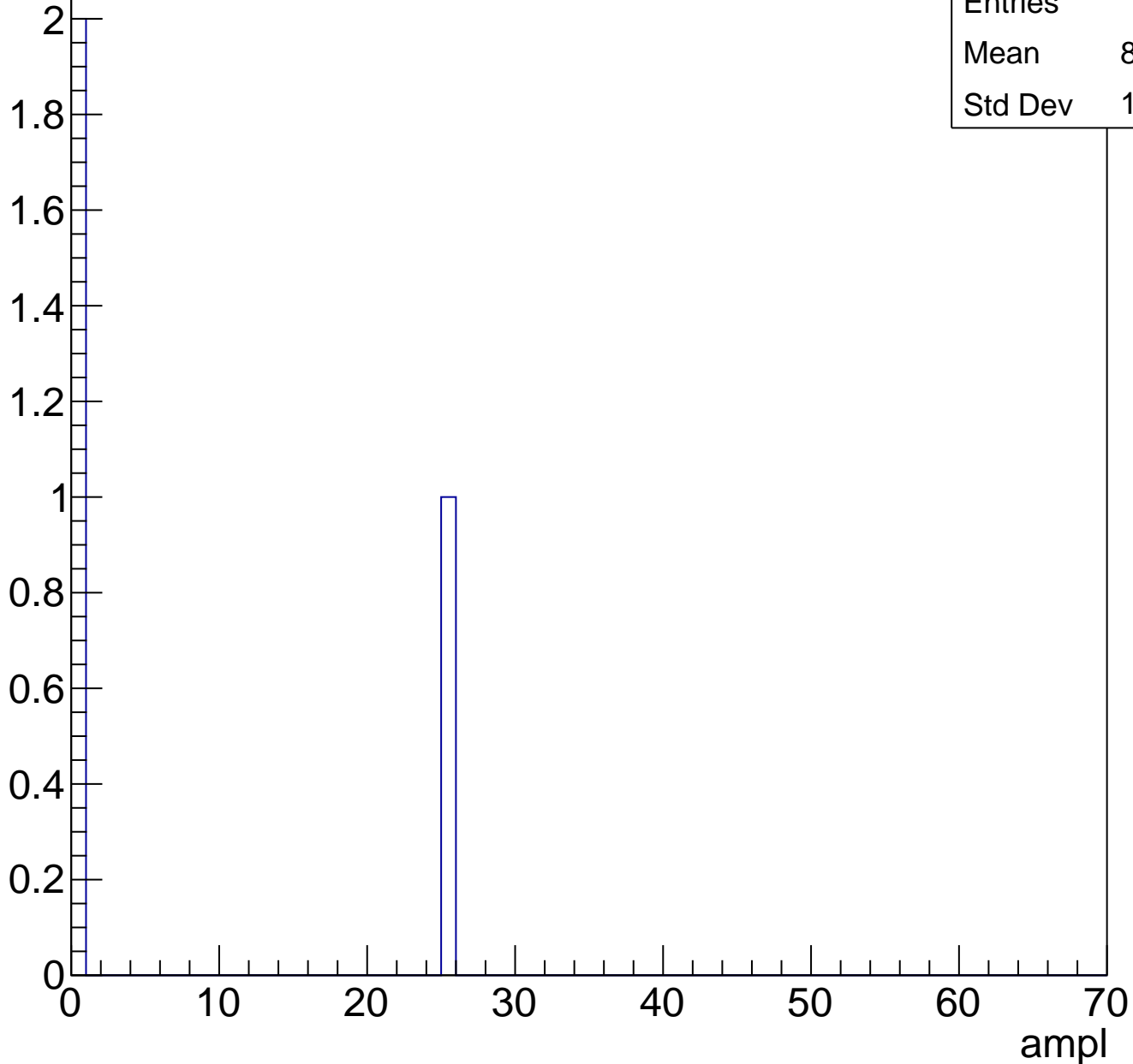


Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L002S, U26-ch86, adc0

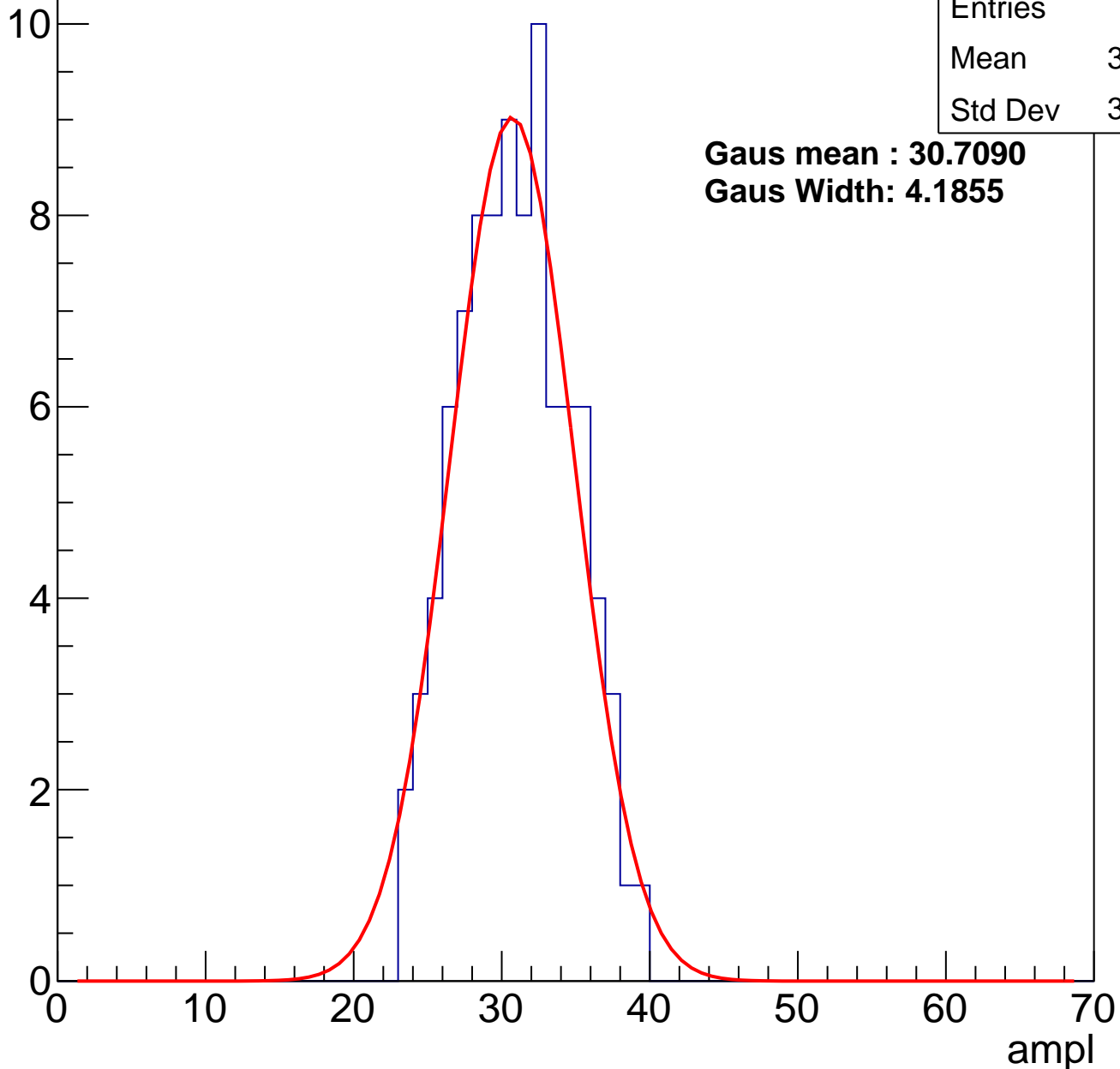
calib_packv5_042523_0143.root, FC#8, port C1

Entries	92
Mean	30.45
Std Dev	3.716

Gaus mean : 30.7090

Gaus Width: 4.1855

Entry



B0L002S, U26-ch86, adc1

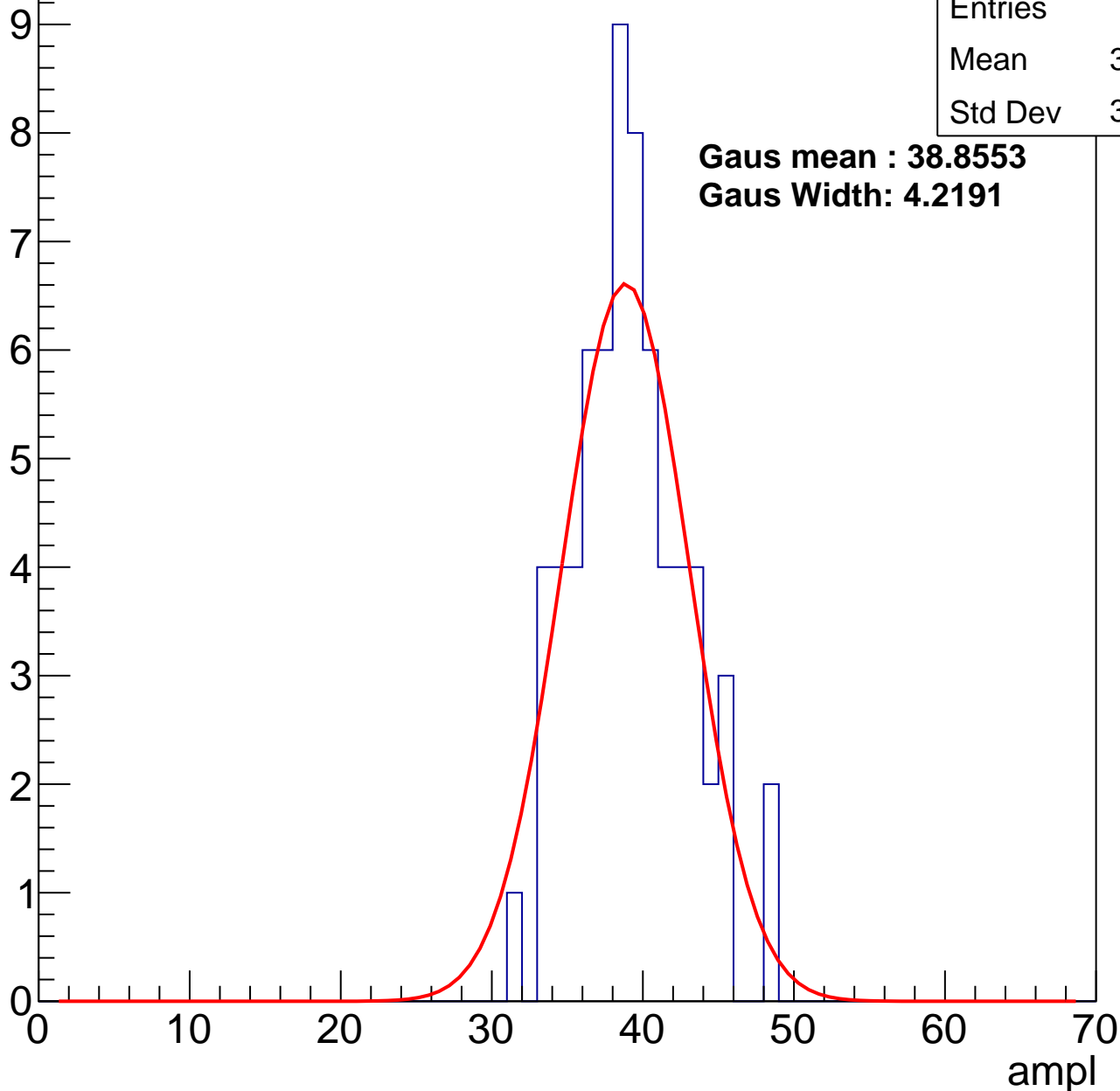
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	38.72
Std Dev	3.656

Gaus mean : 38.8553

Gaus Width: 4.2191



B0L002S, U26-ch86, adc2

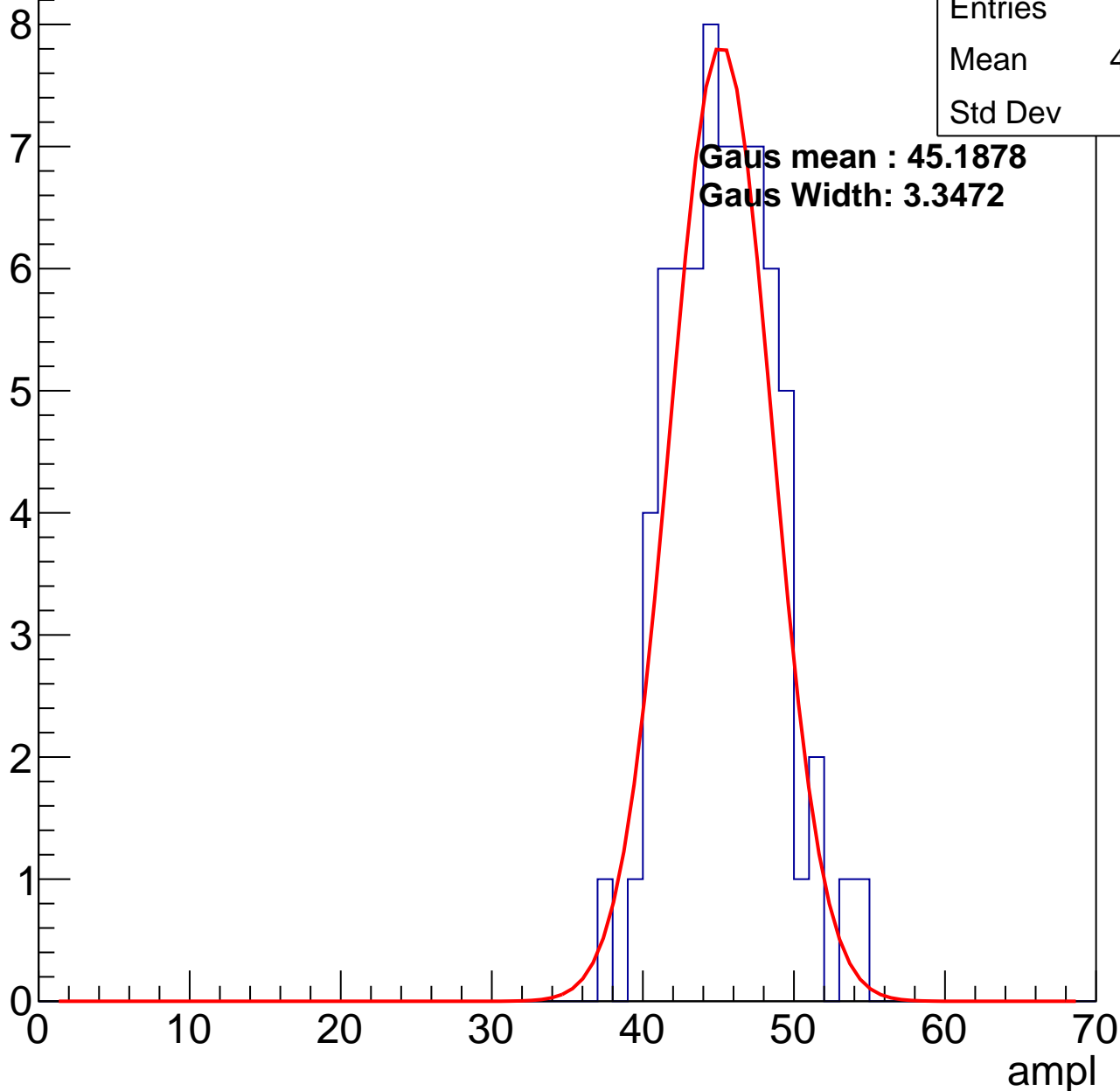
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.96
Std Dev	3.39

Gaus mean : 45.1878

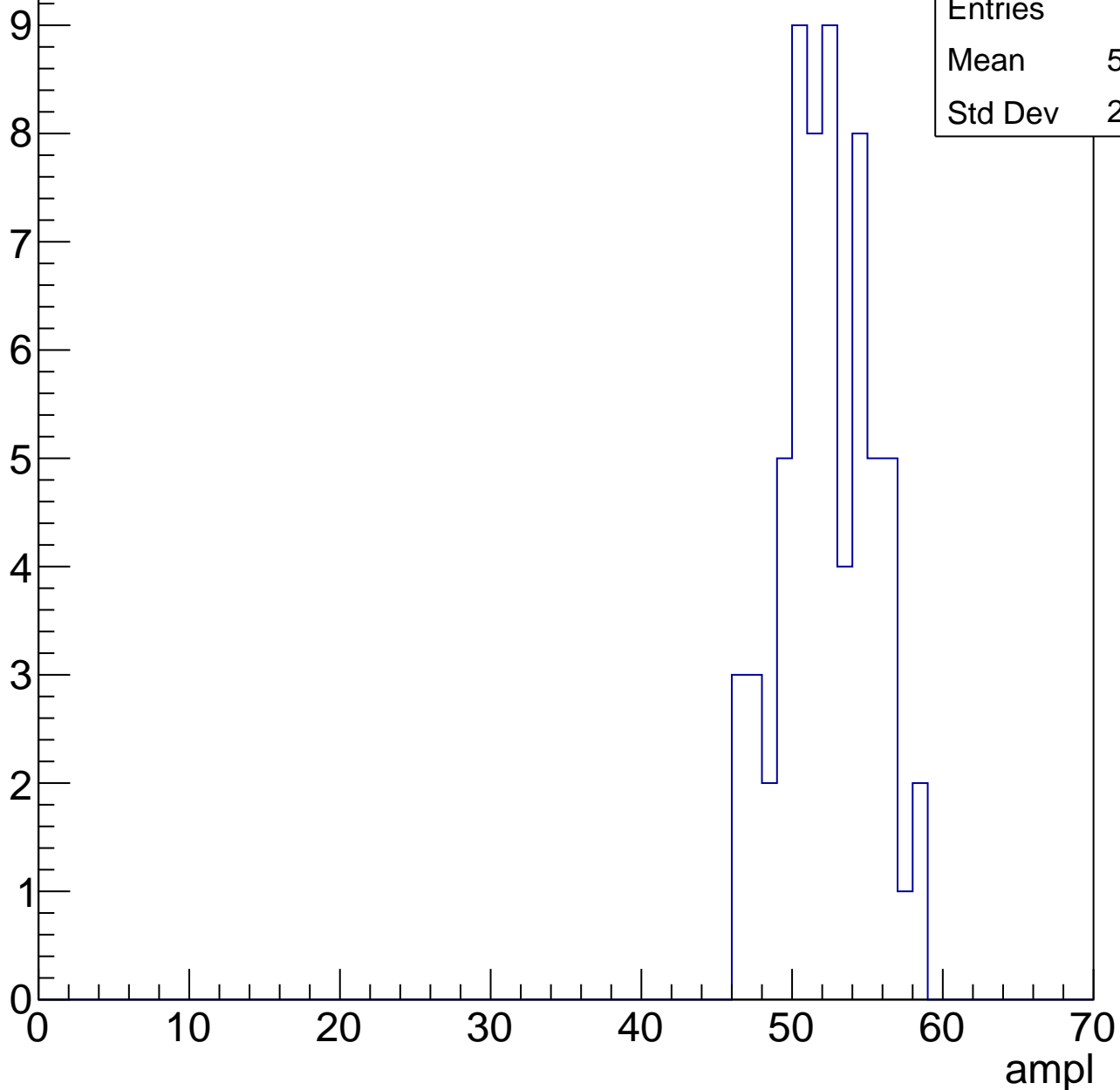
Gaus Width: 3.3472



B0L002S, U26-ch86, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

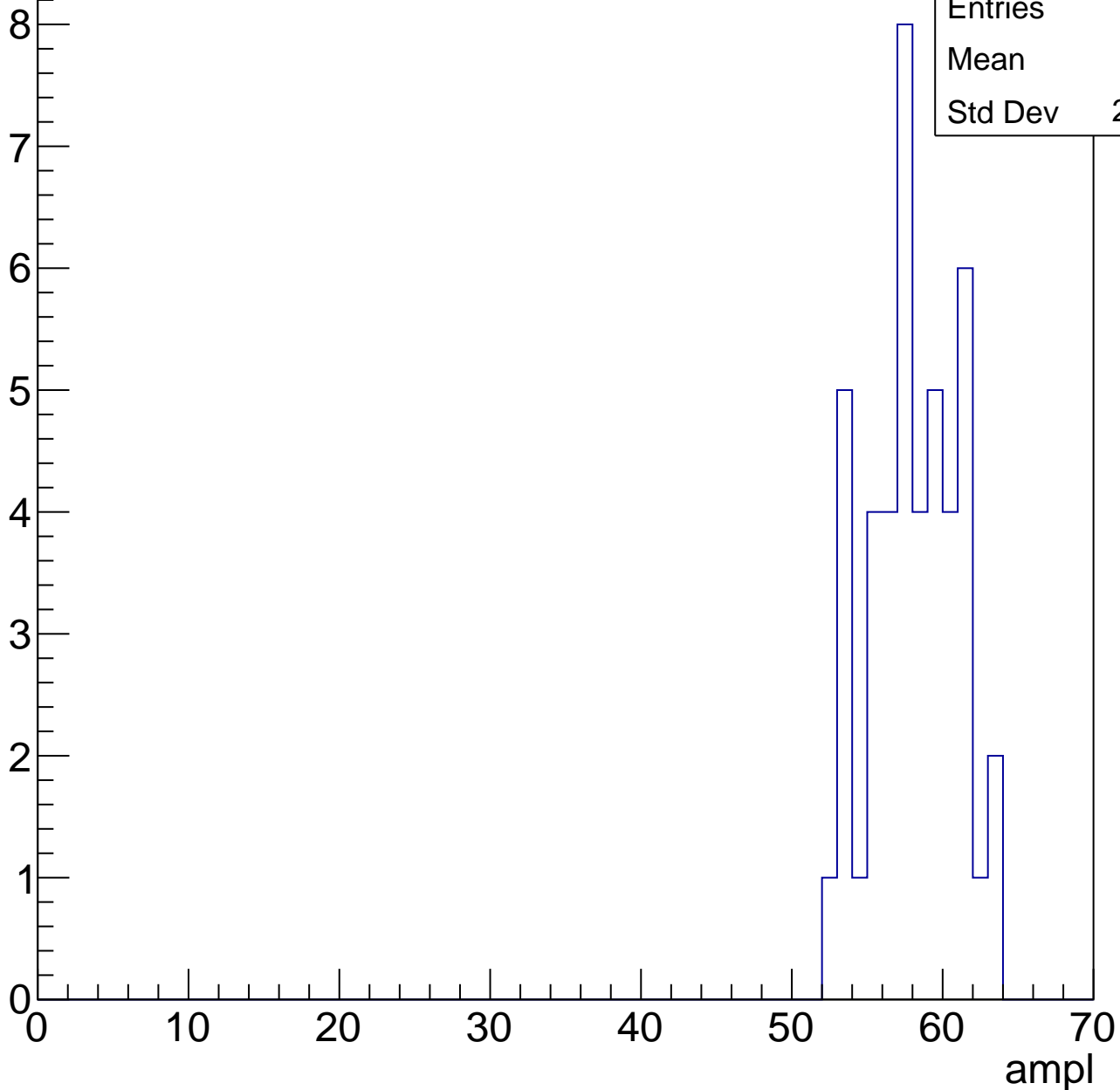


B0L002S, U26-ch86, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	57.6
Std Dev	2.871

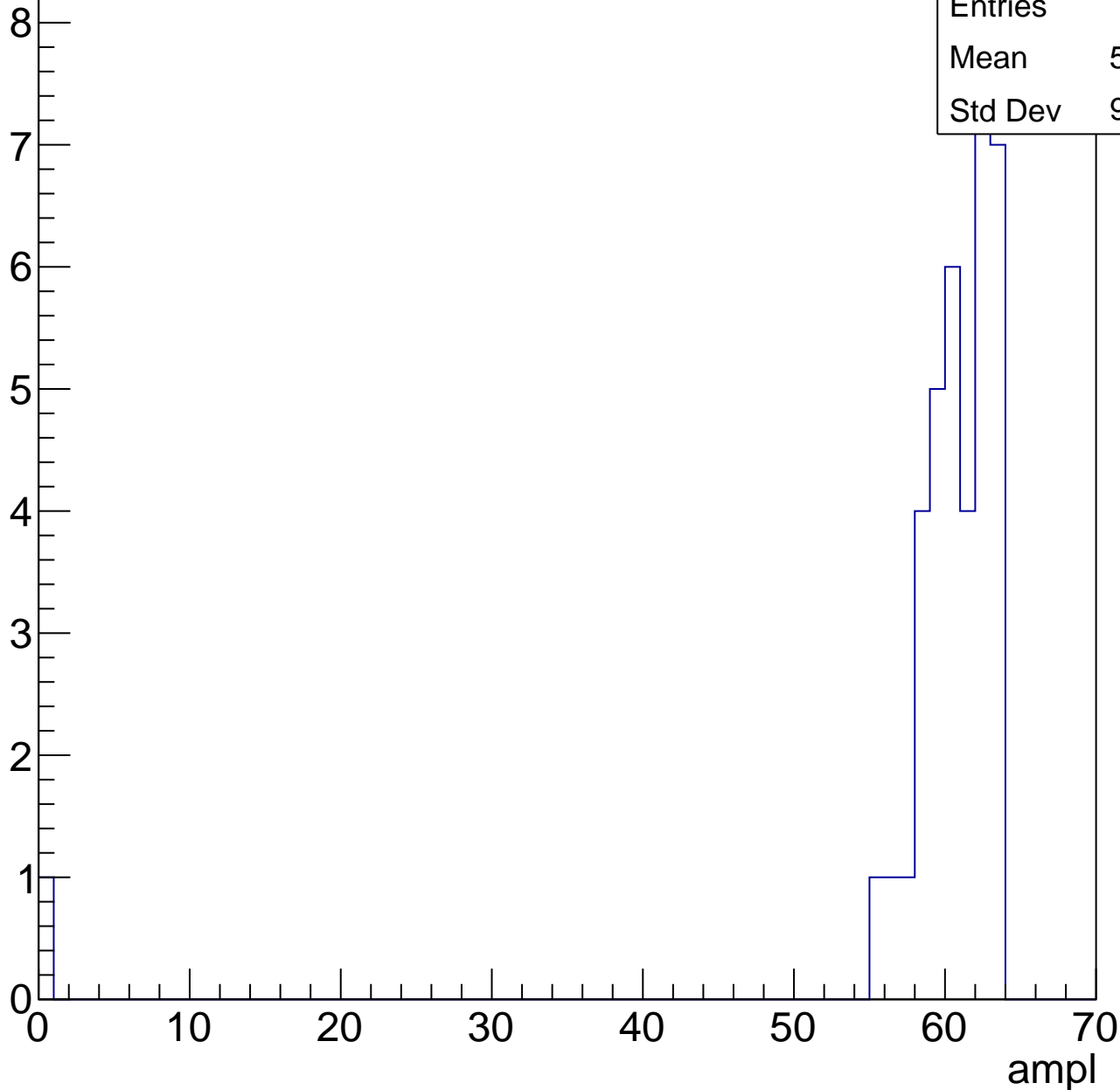


B0L002S, U26-ch86, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.84
Std Dev	9.893



B0L002S, U26-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch87, adc0

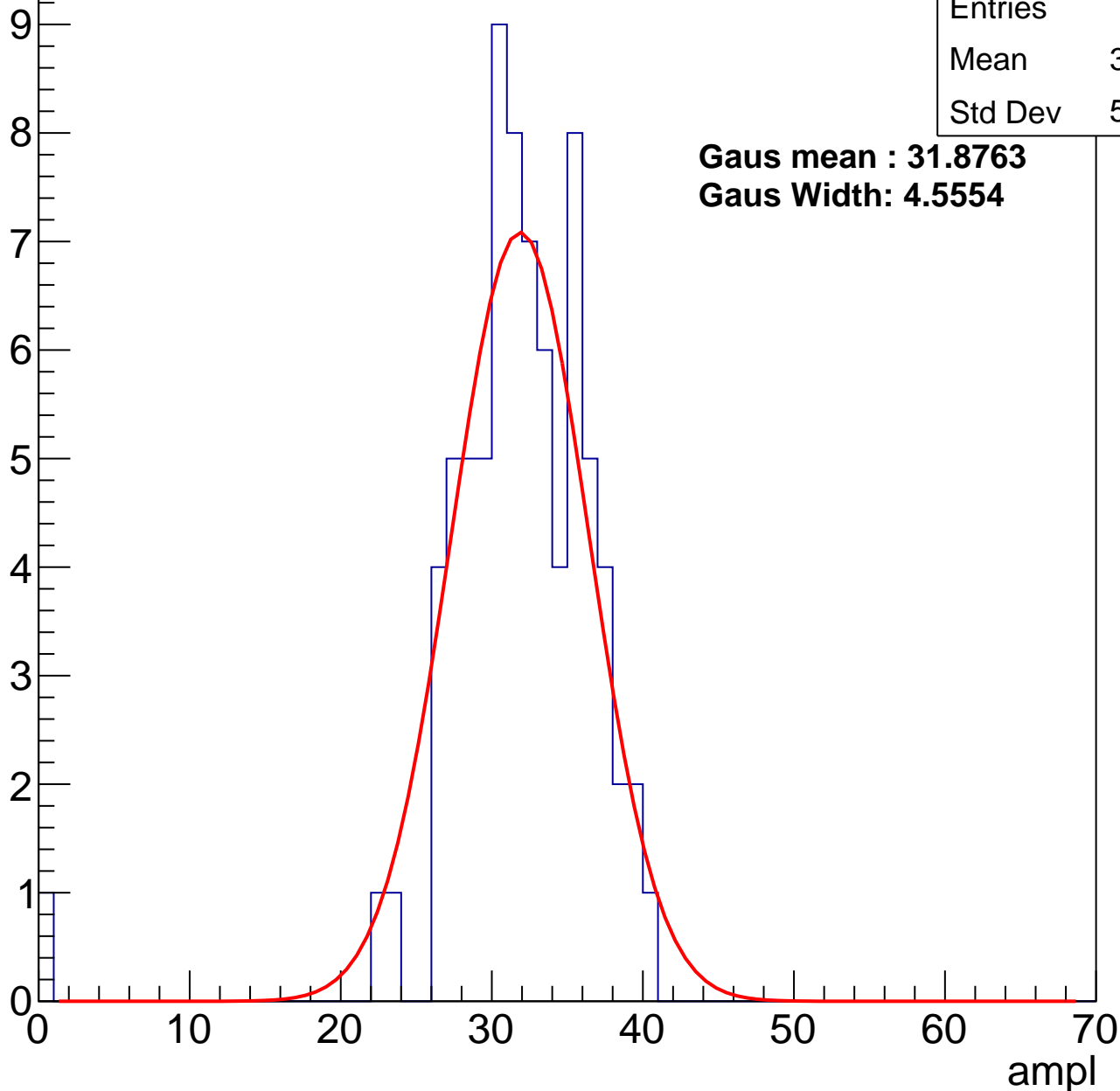
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	31.37
Std Dev	5.214

Gaus mean : 31.8763

Gaus Width: 4.5554



B0L002S, U26-ch87, adc1

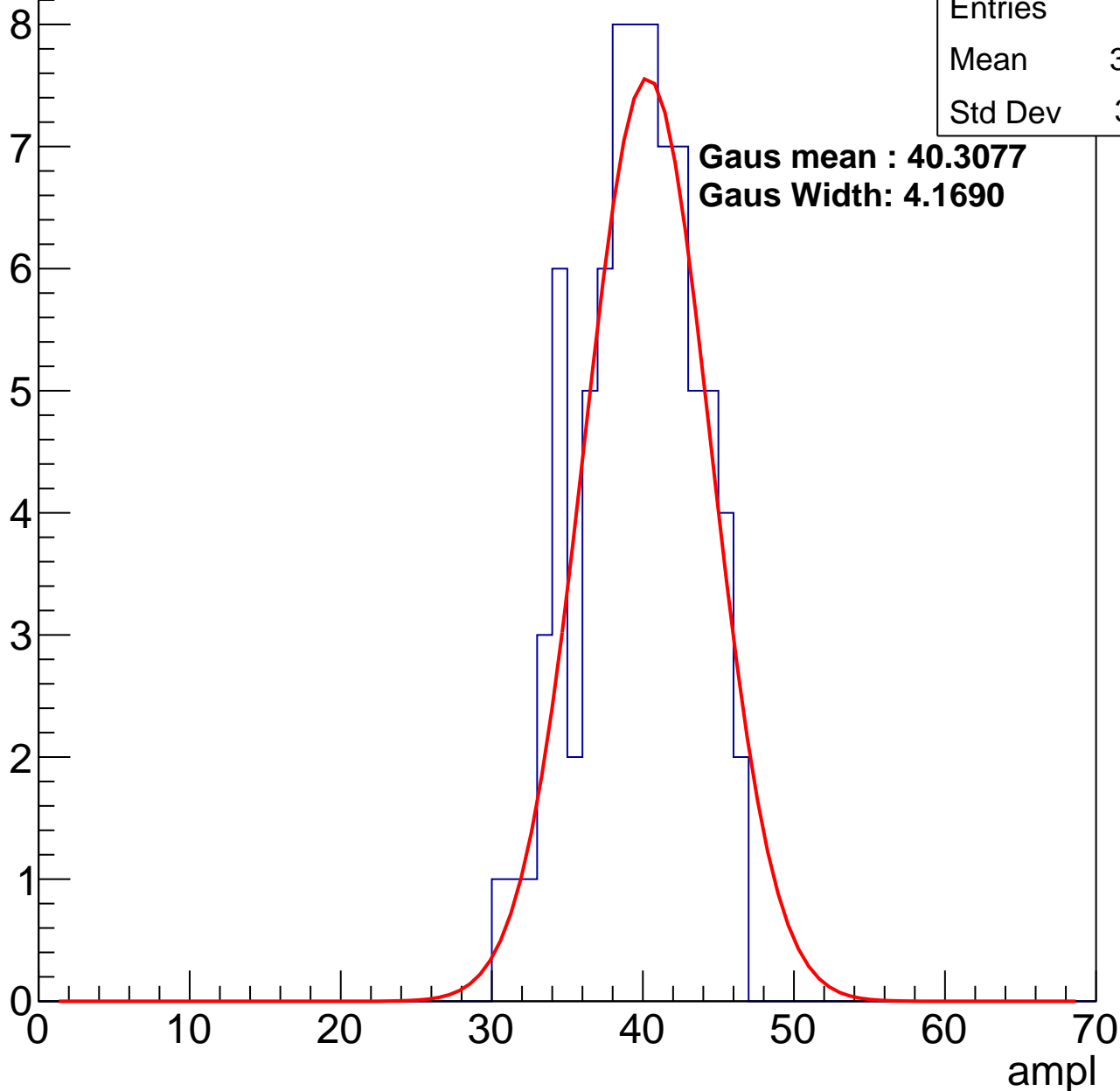
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	39.14
Std Dev	3.741

Gaus mean : 40.3077

Gaus Width: 4.1690



B0L002S, U26-ch87, adc2

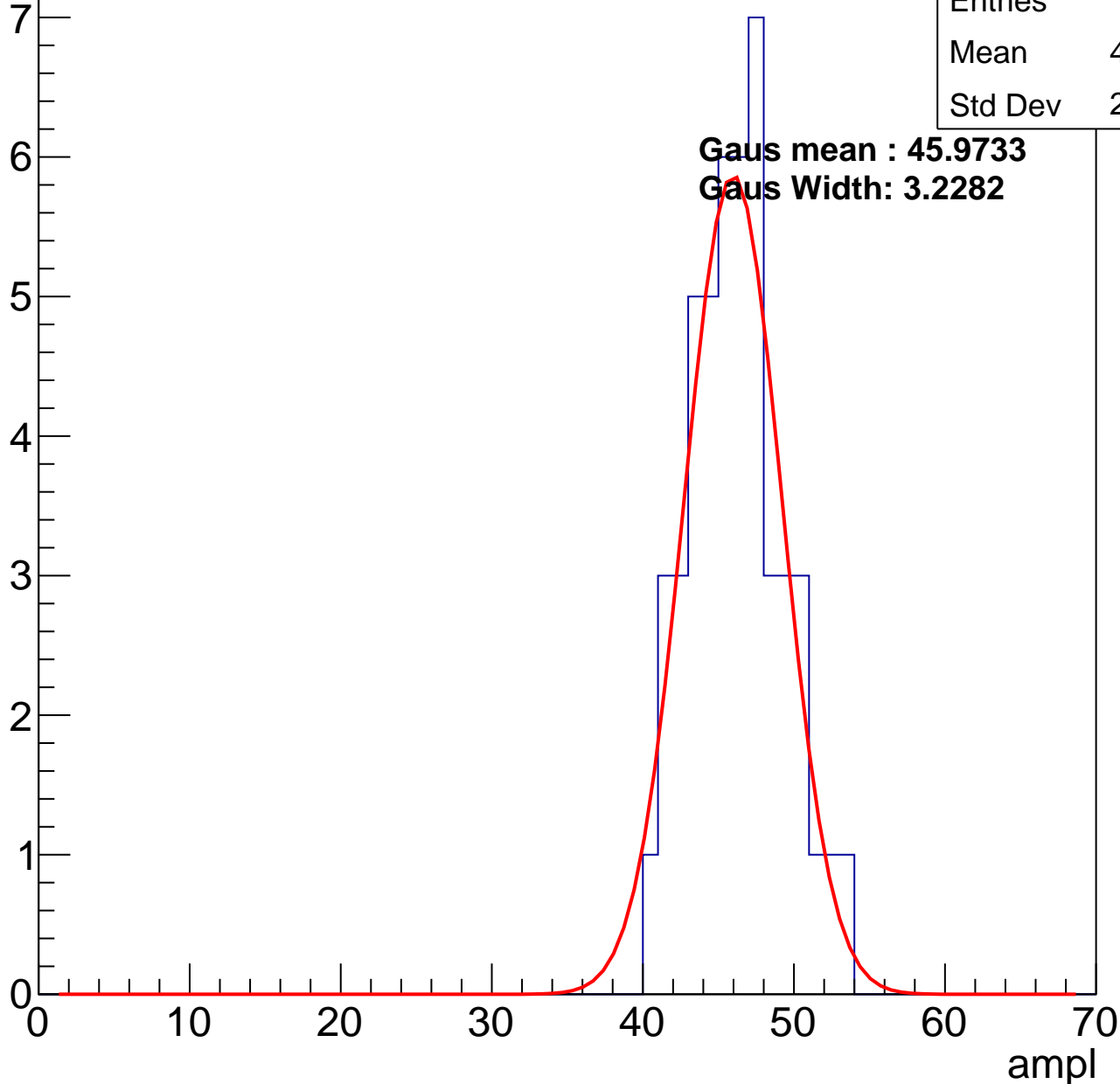
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	45.75
Std Dev	2.997

Gaus mean : 45.9733

Gaus Width: 3.2282



B0L002S, U26-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

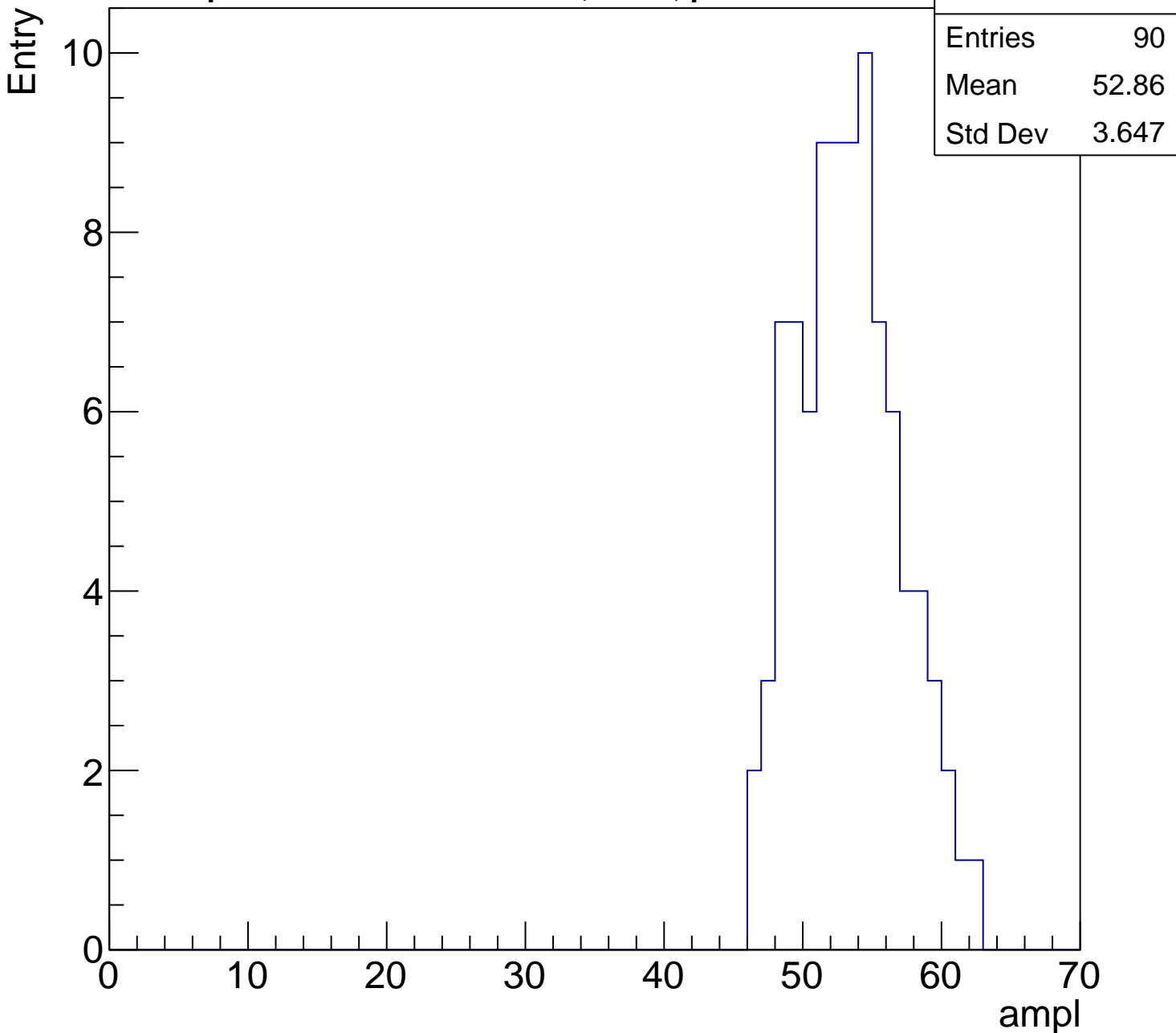
Entries	90
Mean	52.86
Std Dev	3.647

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

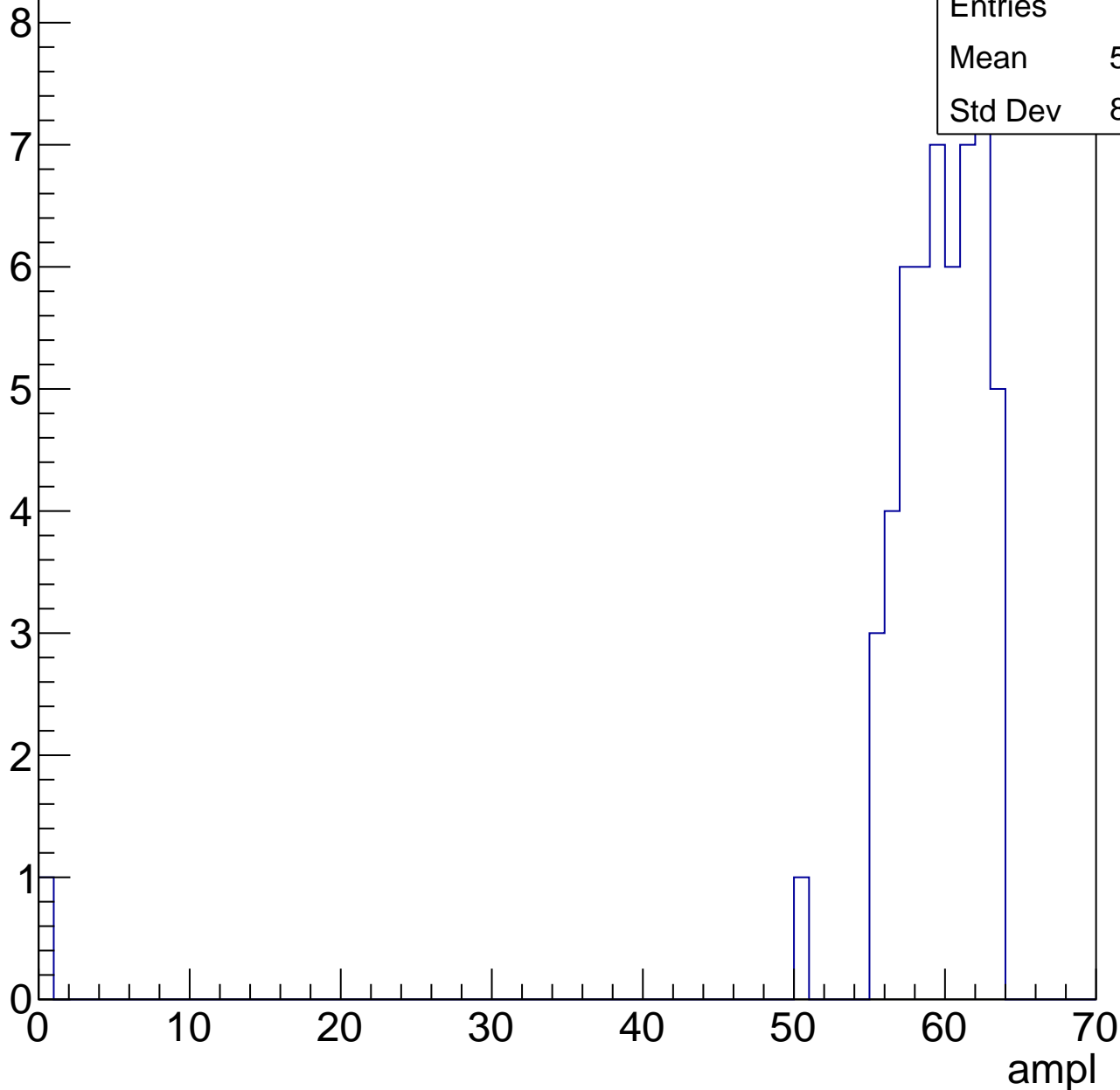


B0L002S, U26-ch87, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.15
Std Dev	8.414

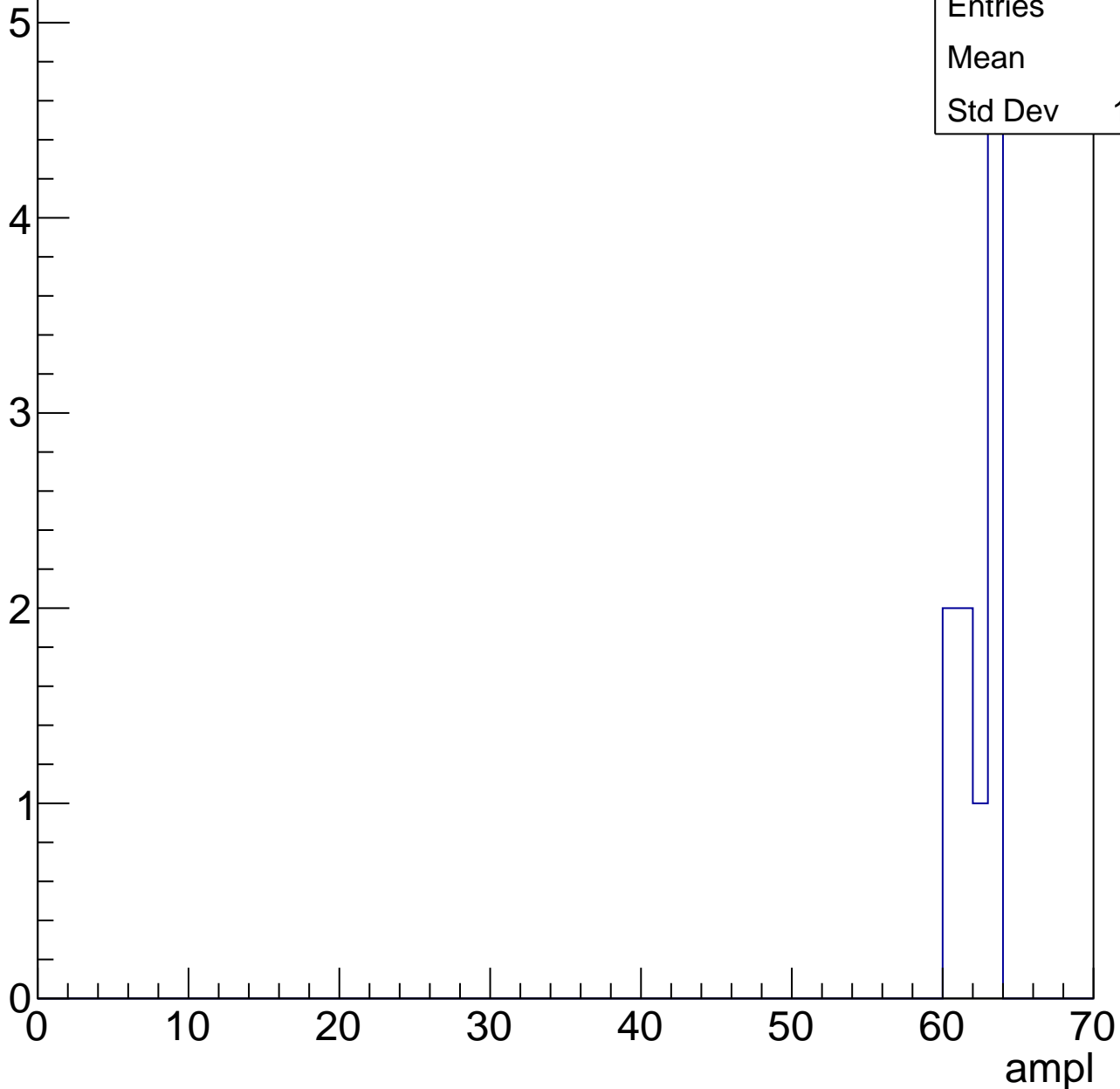


B0L002S, U26-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	61.9
Std Dev	1.221



B0L002S, U26-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch88, adc0

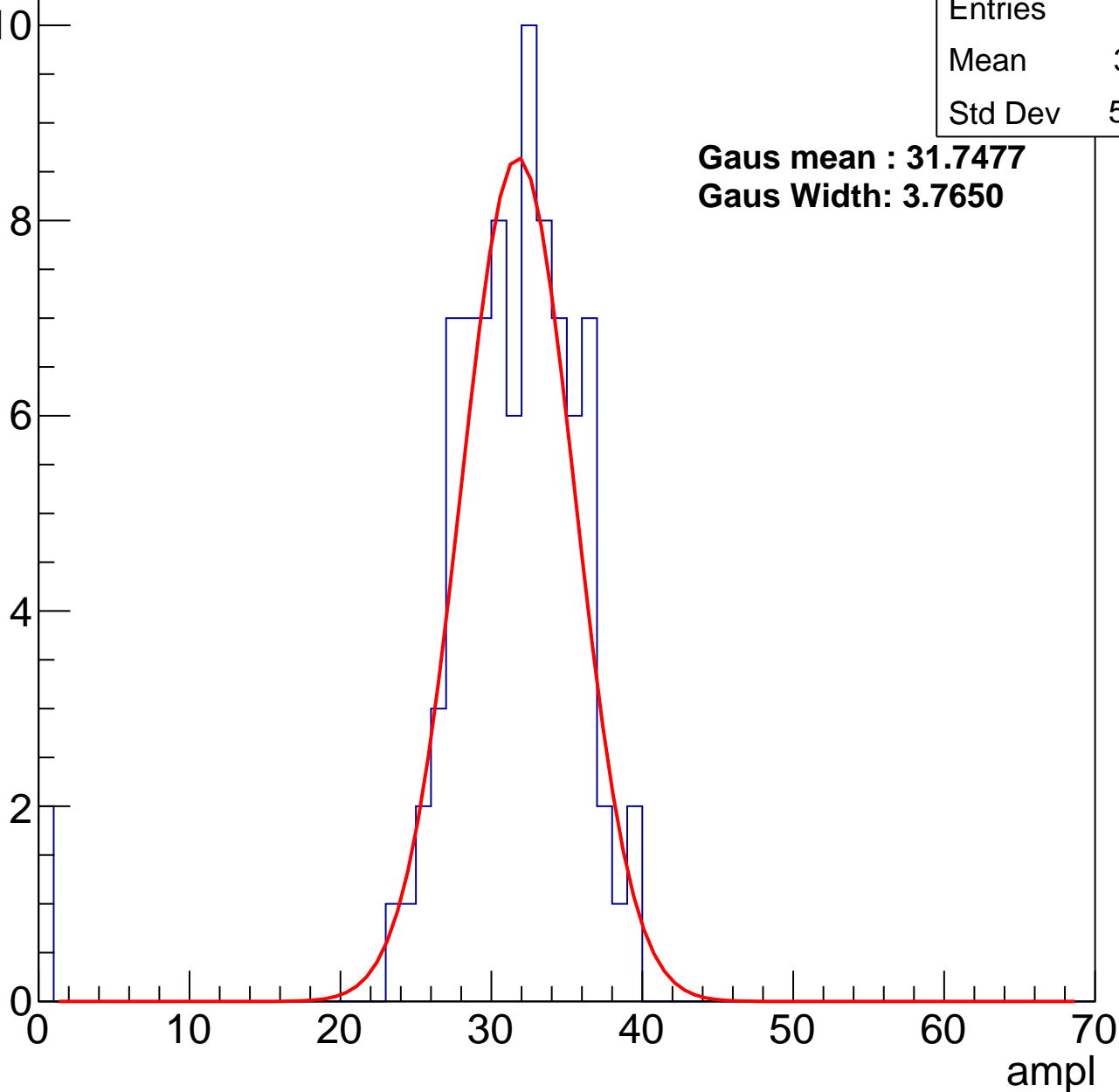
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	30.61
Std Dev	5.877

Gaus mean : 31.7477

Gaus Width: 3.7650



B0L002S, U26-ch88, adc1

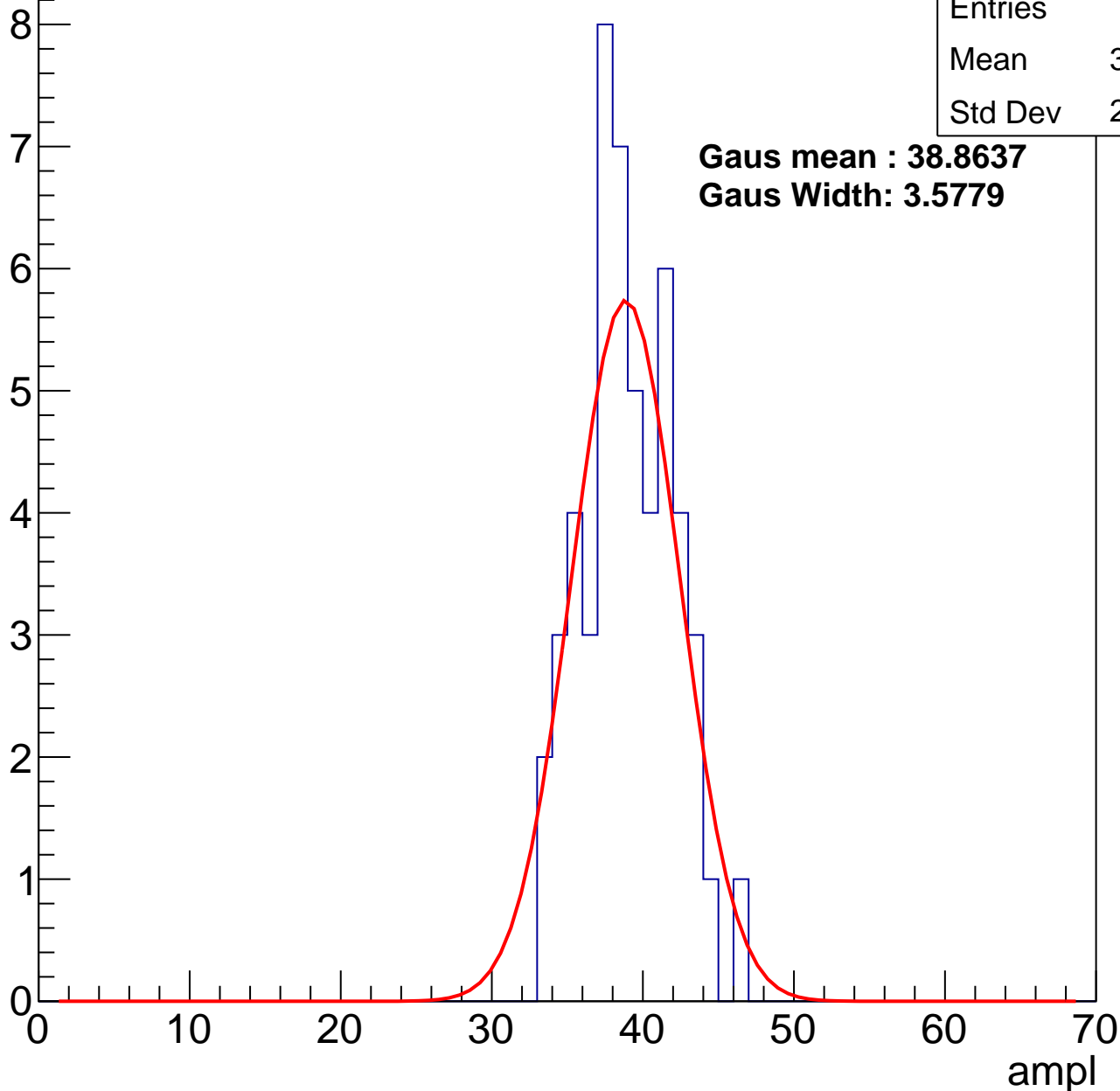
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	38.55
Std Dev	2.979

Gaus mean : 38.8637

Gaus Width: 3.5779

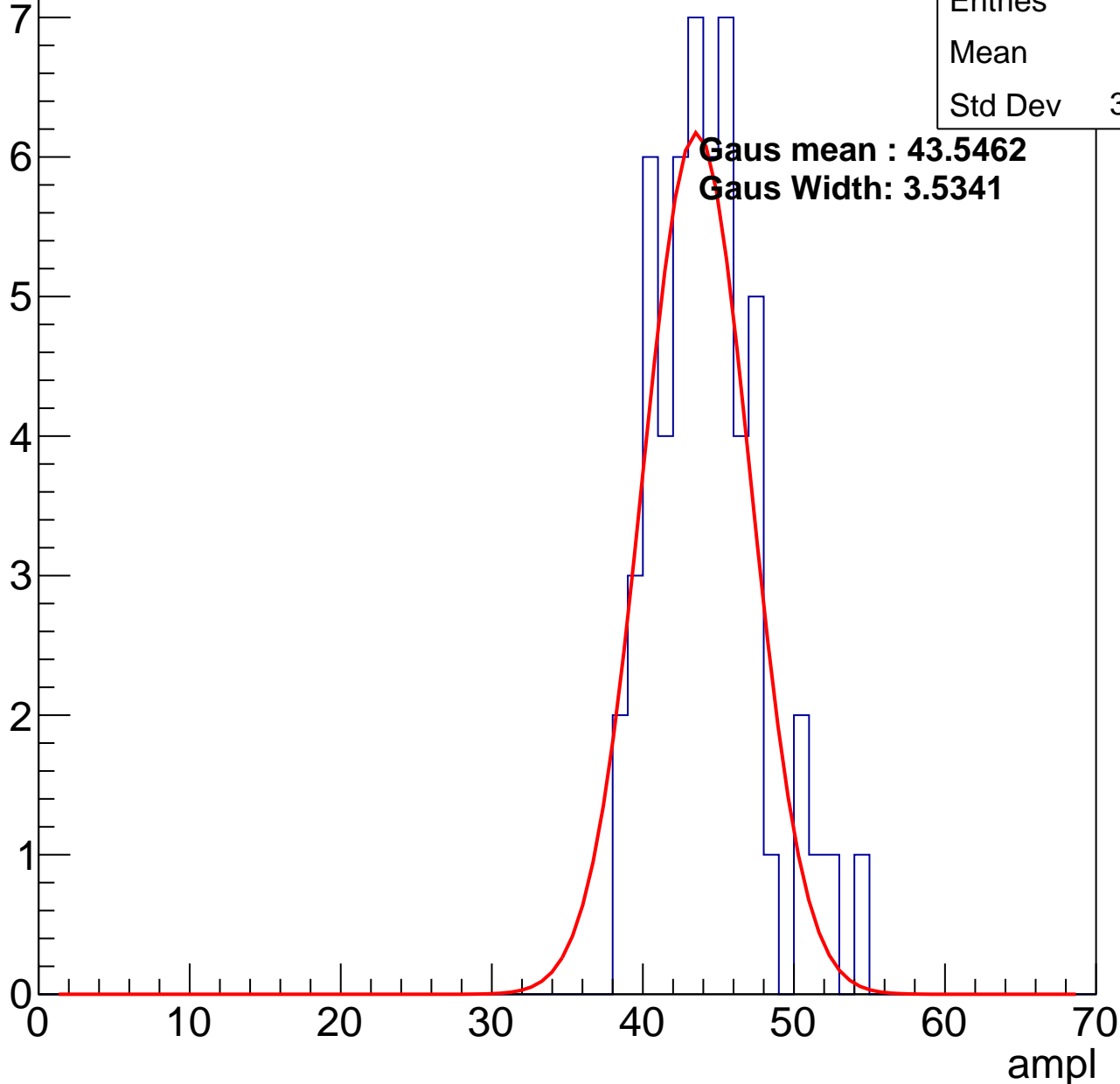


B0L002S, U26-ch88, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.8
Std Dev	3.477



B0L002S, U26-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	50.46
Std Dev	3.287

Entry

10

8

6

4

2

0

0

10

20

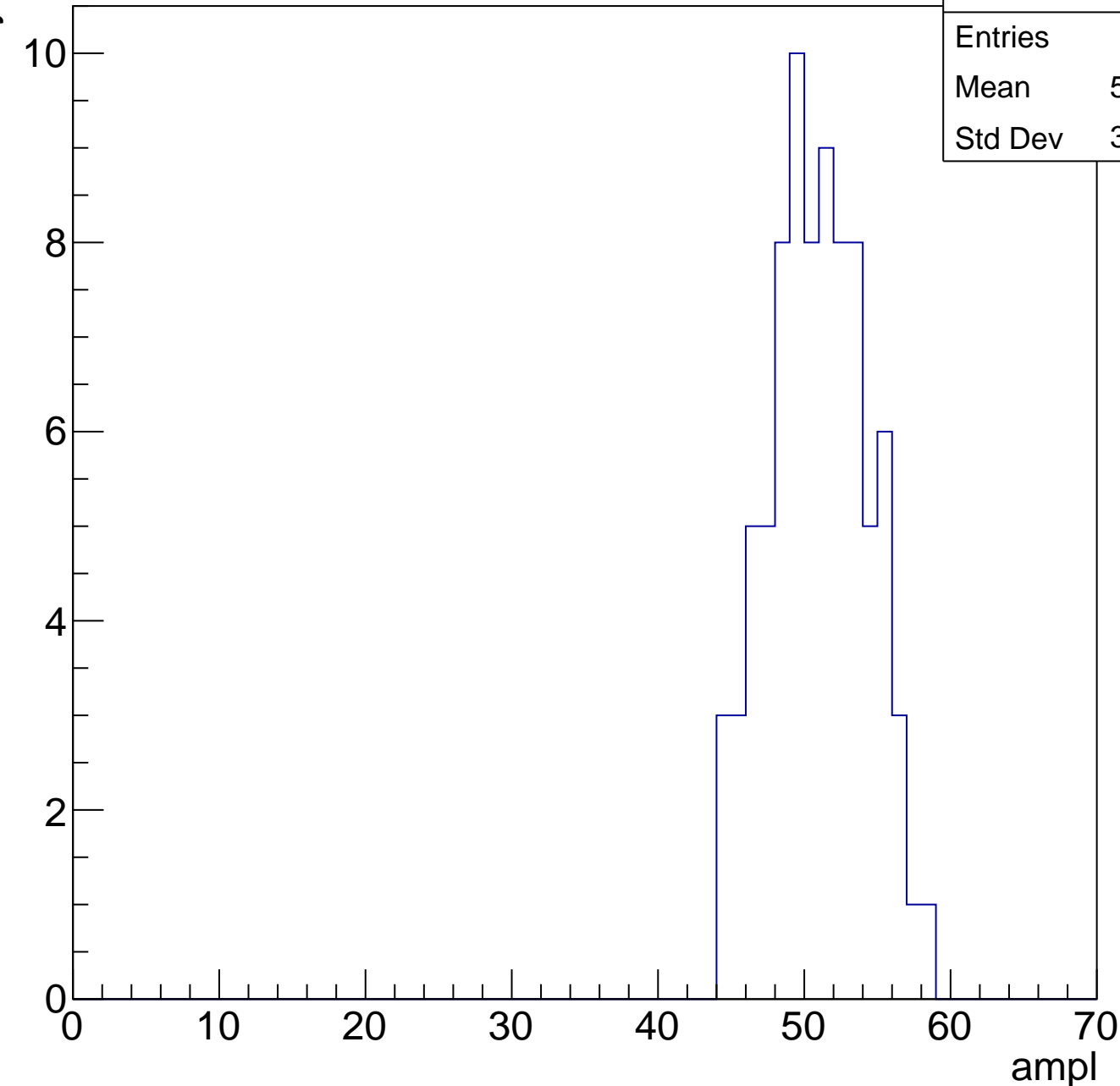
30

40

50

60

ampl

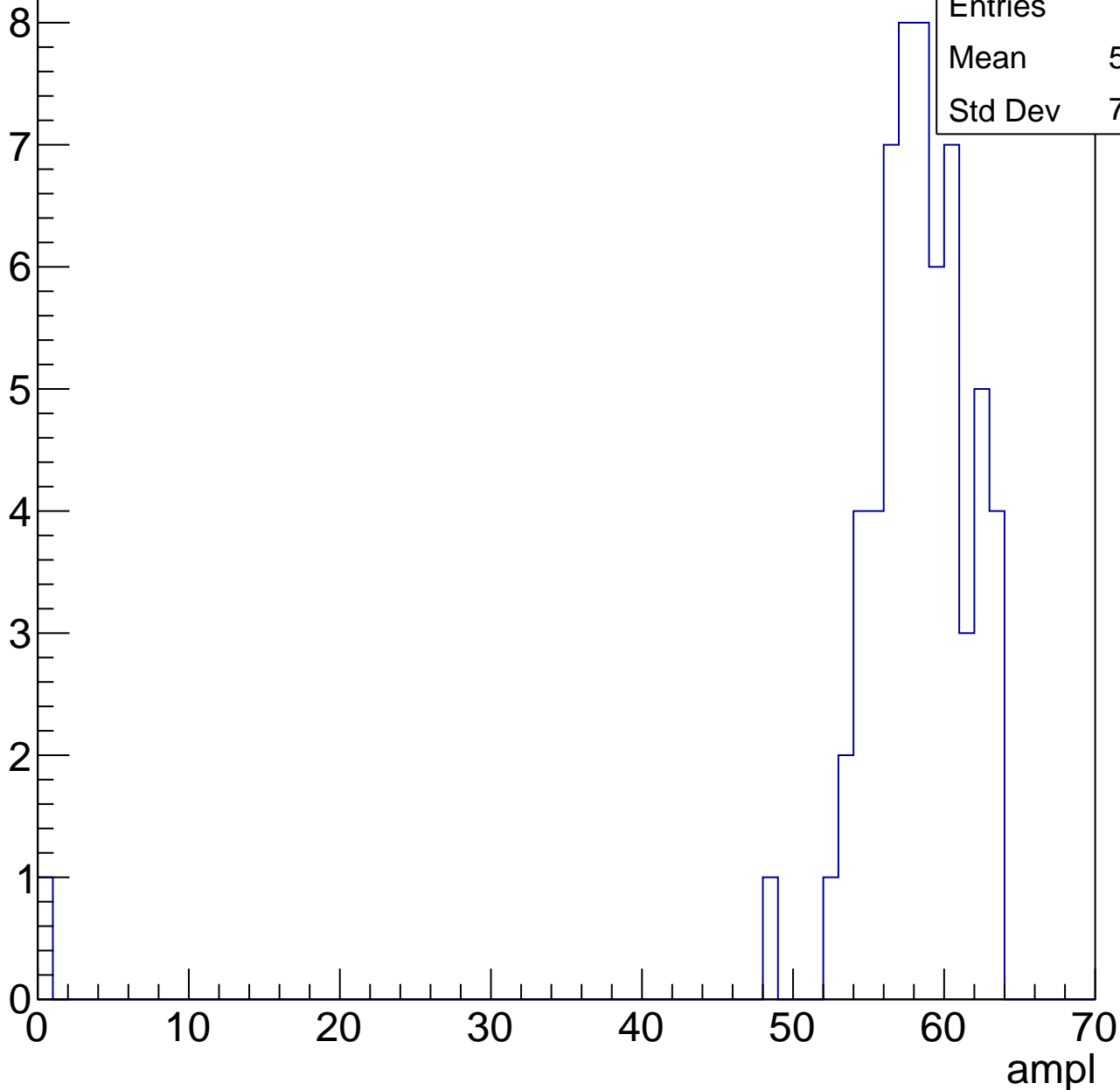


B0L002S, U26-ch88, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

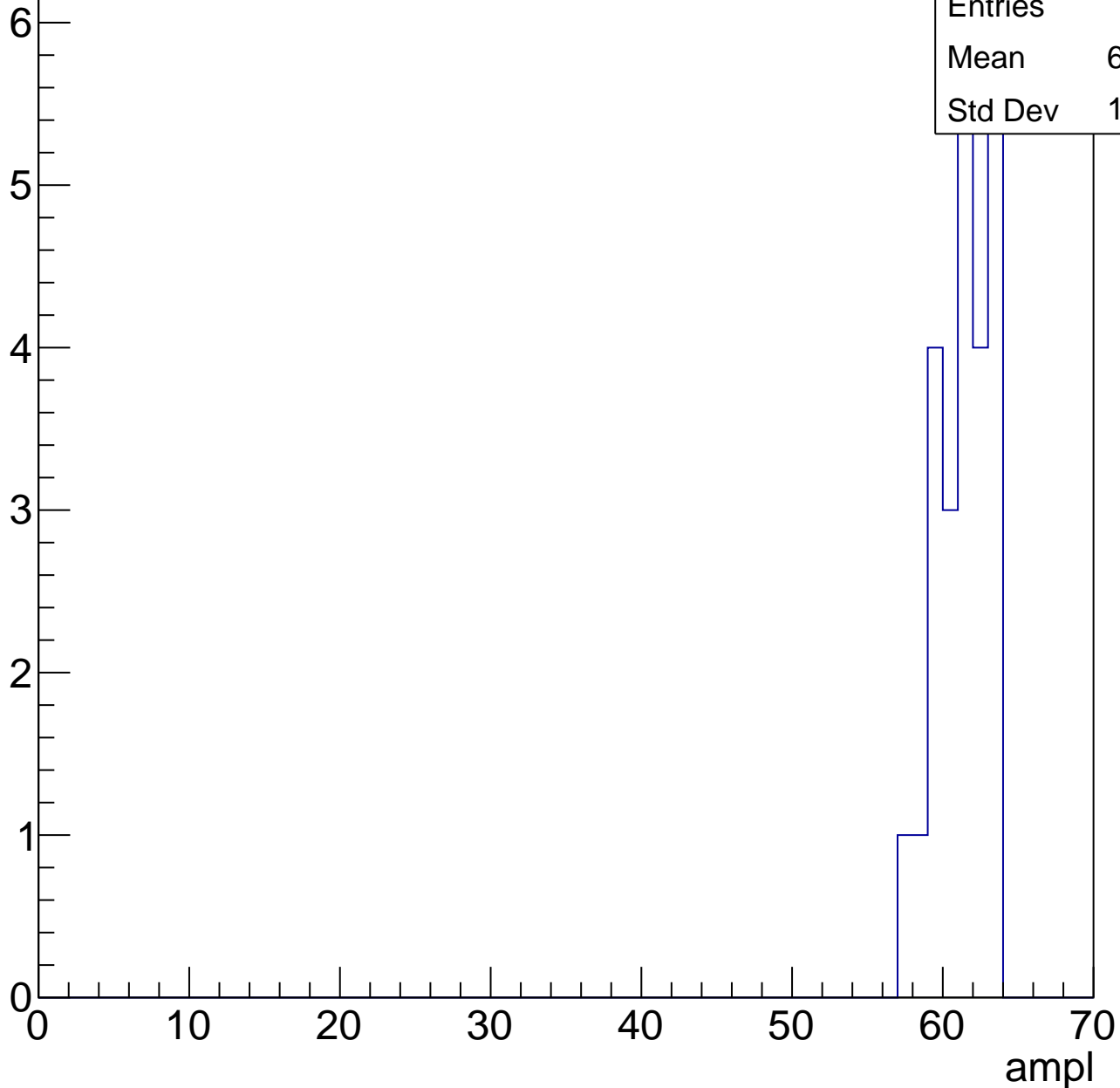
Entries	61
Mean	56.93
Std Dev	7.952



B0L002S, U26-ch88, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch89, adc0

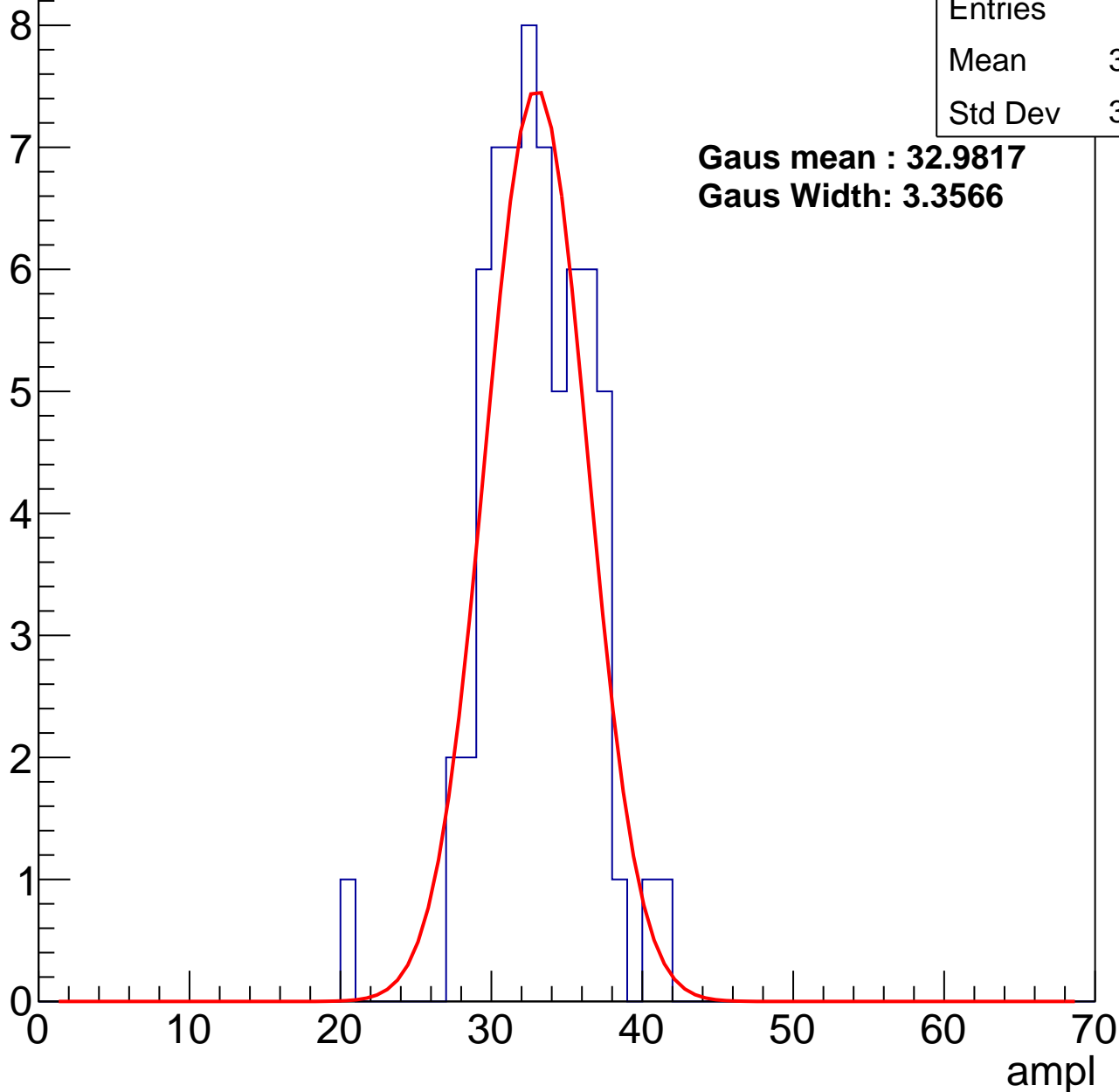
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	32.58
Std Dev	3.455

Gaus mean : 32.9817

Gaus Width: 3.3566



B0L002S, U26-ch89, adc1

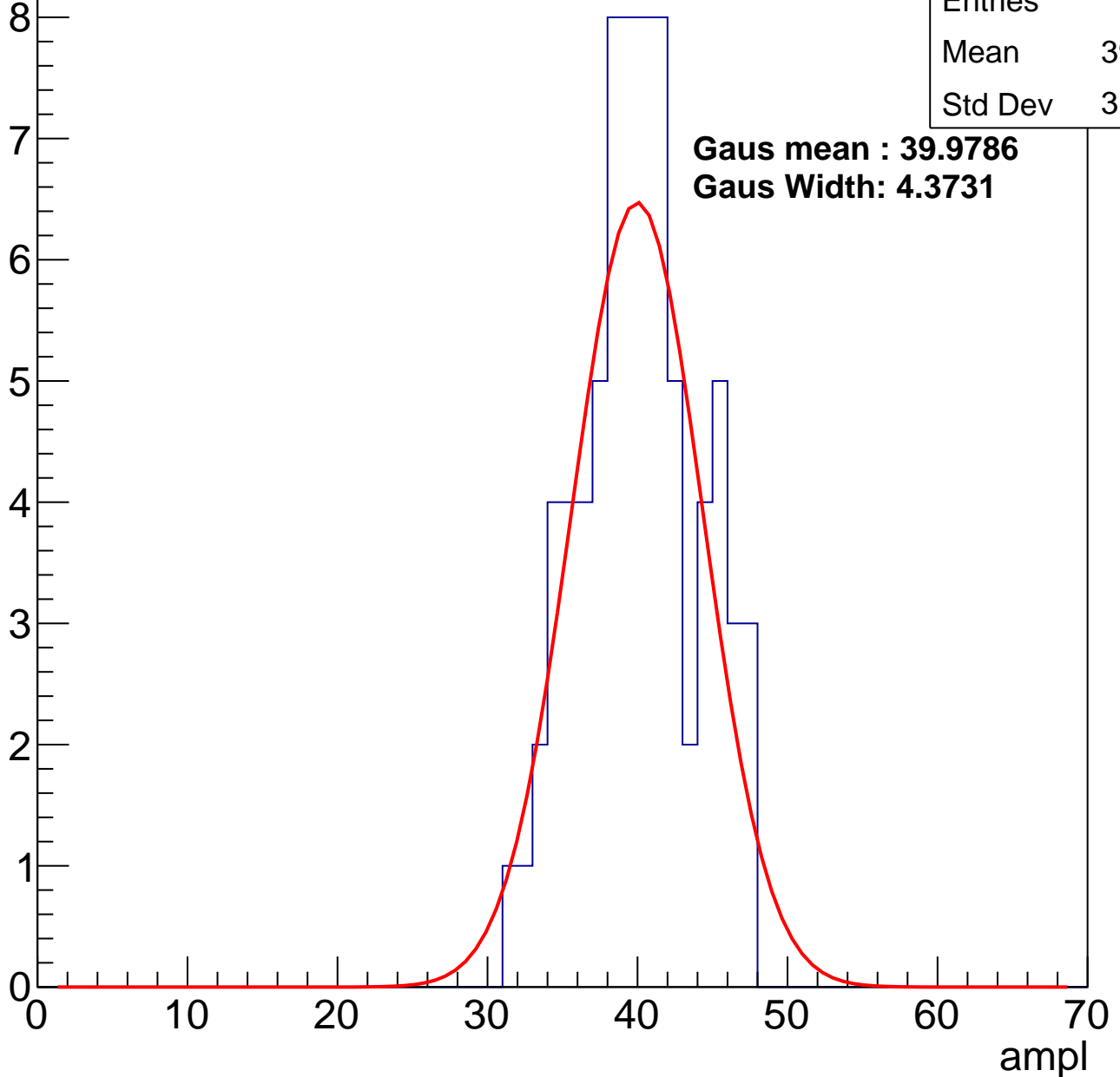
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	39.65
Std Dev	3.859

Gaus mean : 39.9786

Gaus Width: 4.3731



B0L002S, U26-ch89, adc2

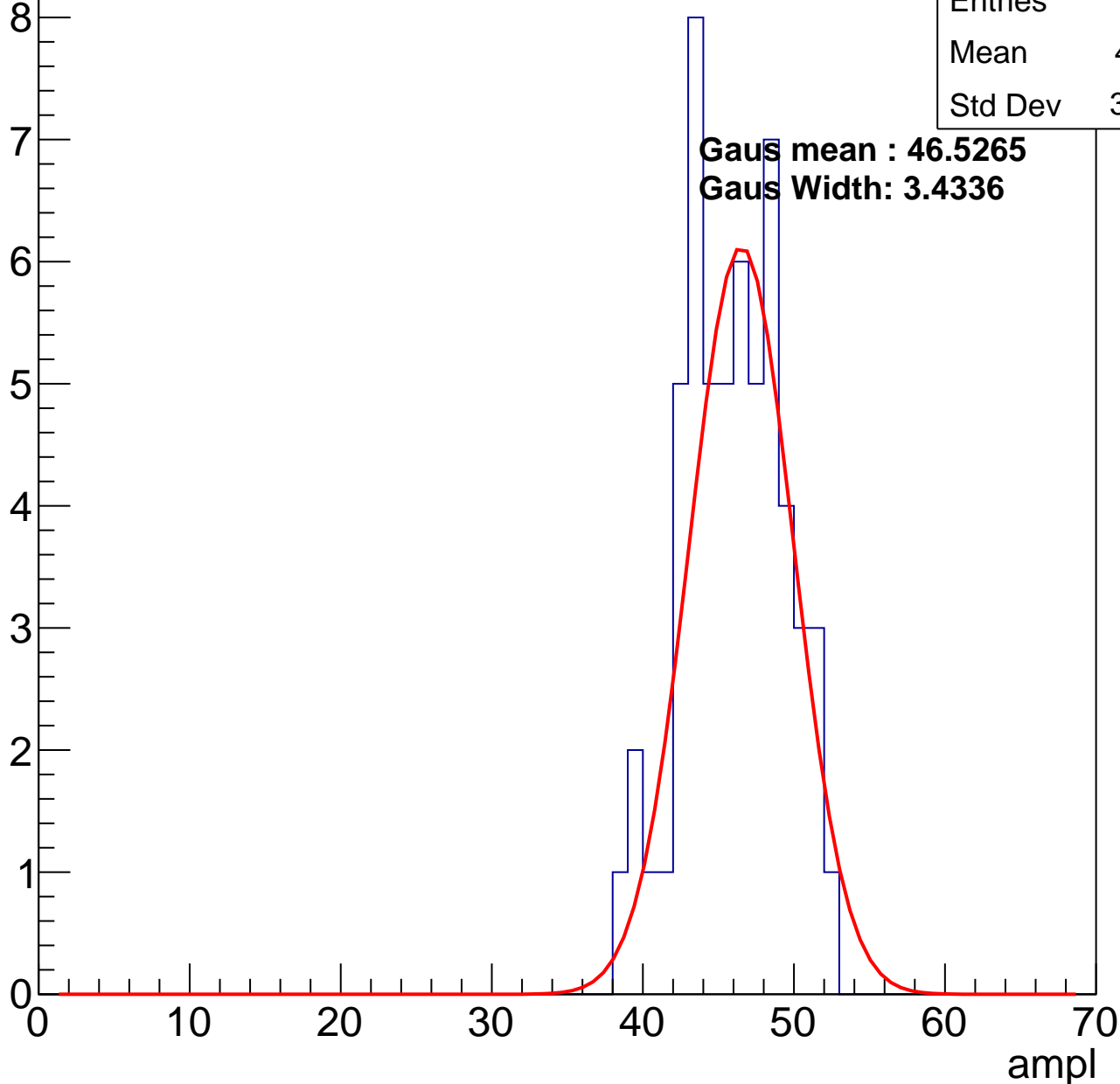
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	45.51
Std Dev	3.283

Gaus mean : 46.5265

Gaus Width: 3.4336

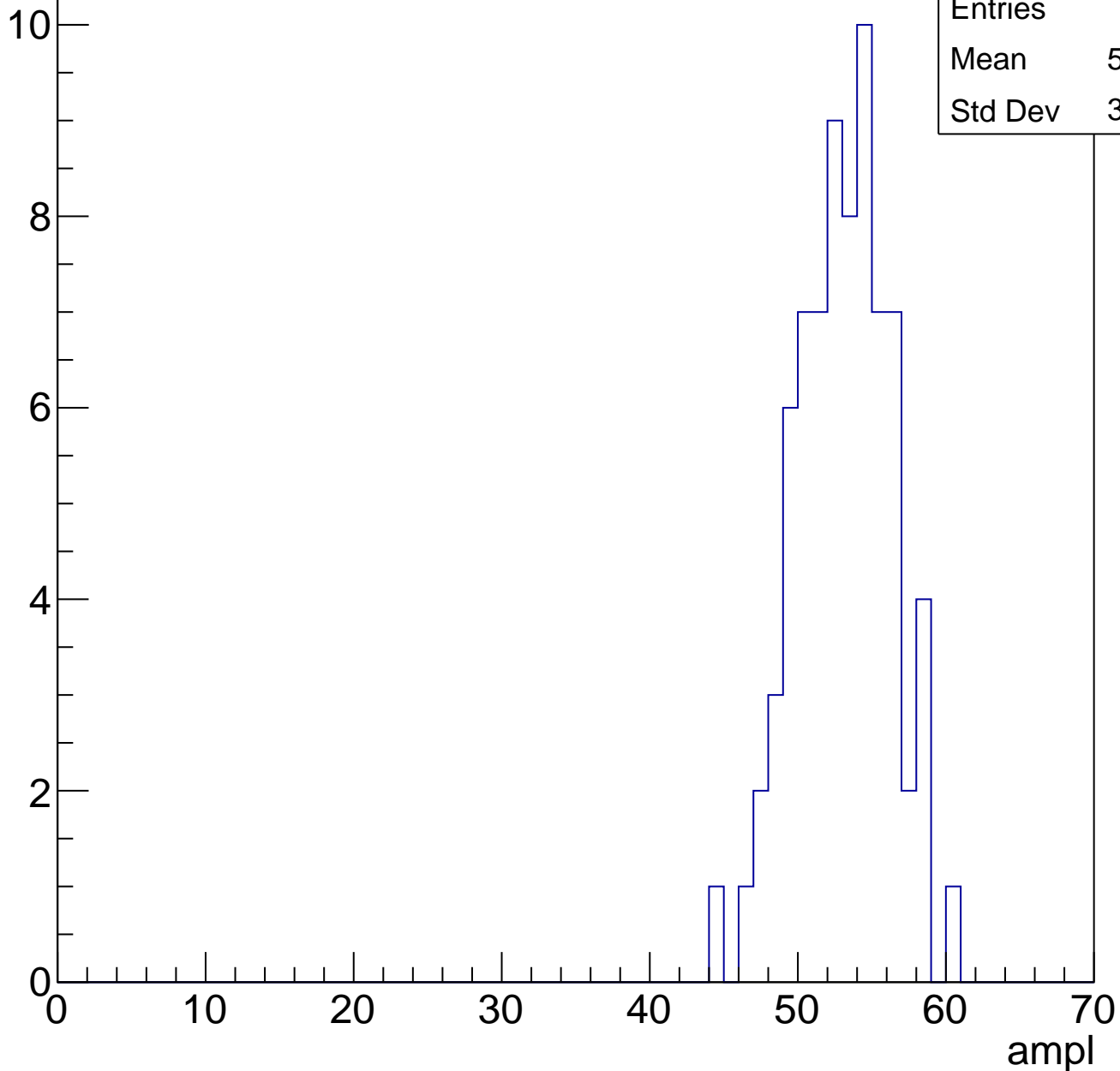


B0L002S, U26-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	52.59
Std Dev	3.146

Entry

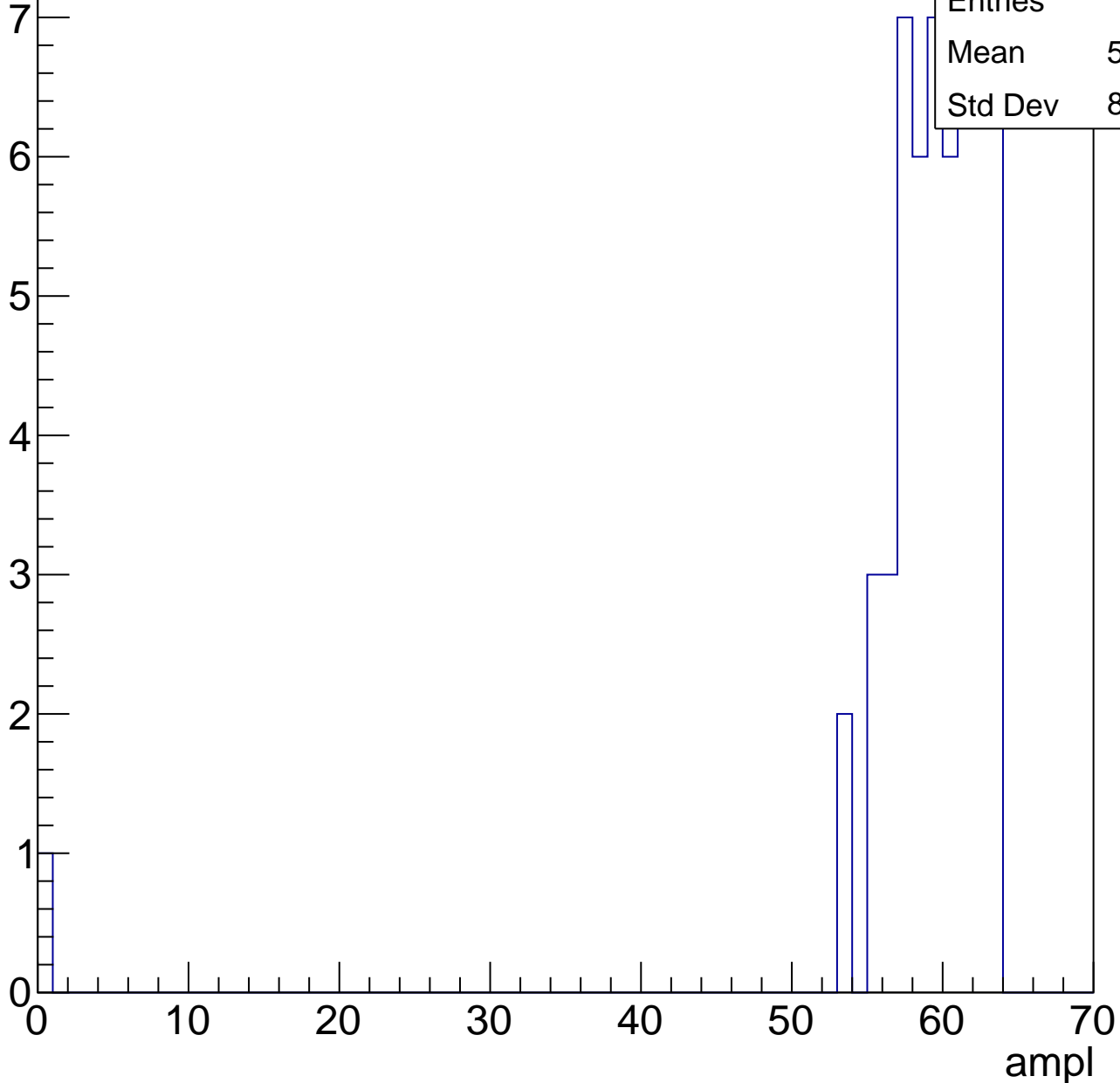


B0L002S, U26-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

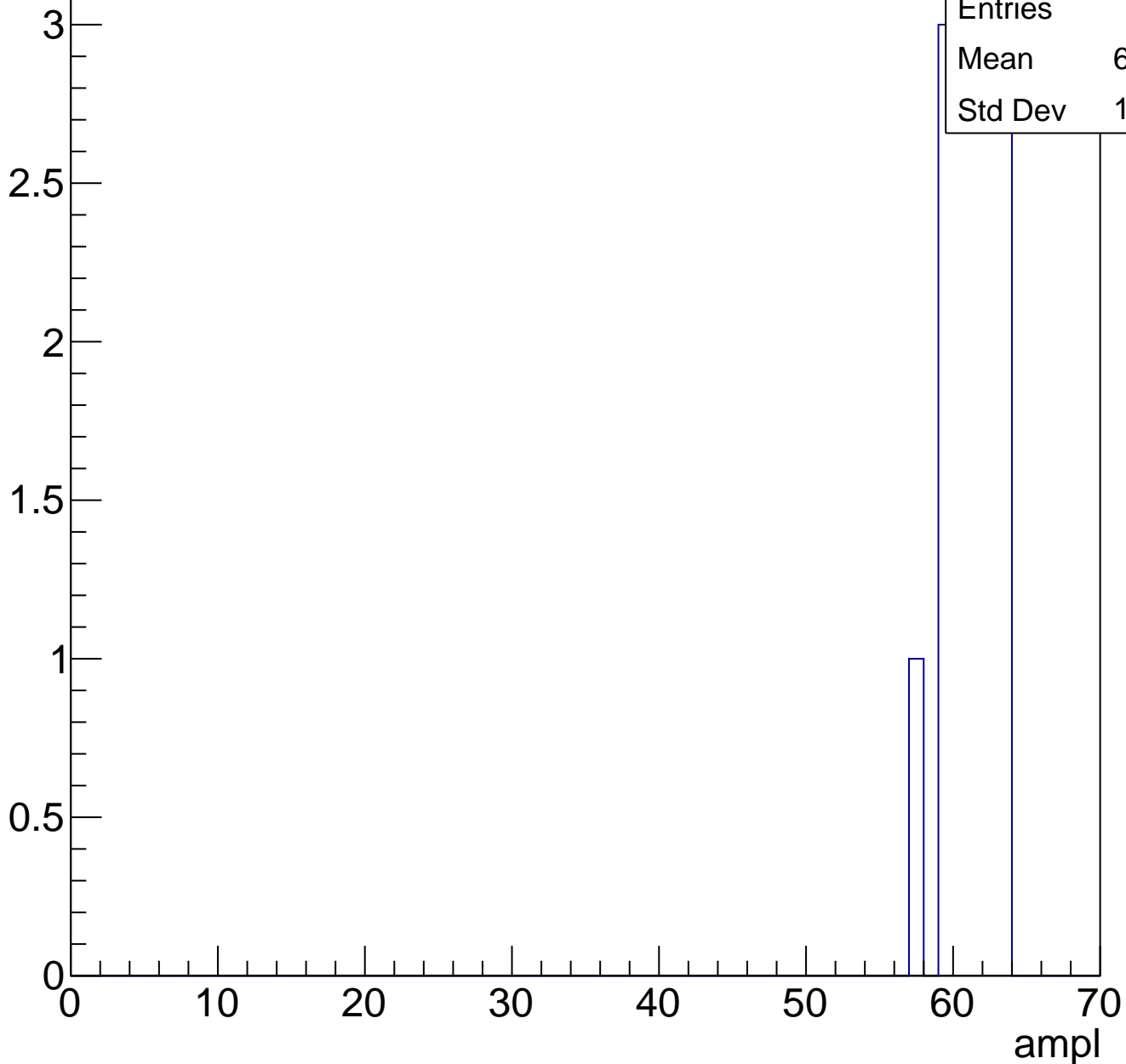
Entries	56
Mean	58.23
Std Dev	8.279



B0L002S, U26-ch89, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch90, adc0

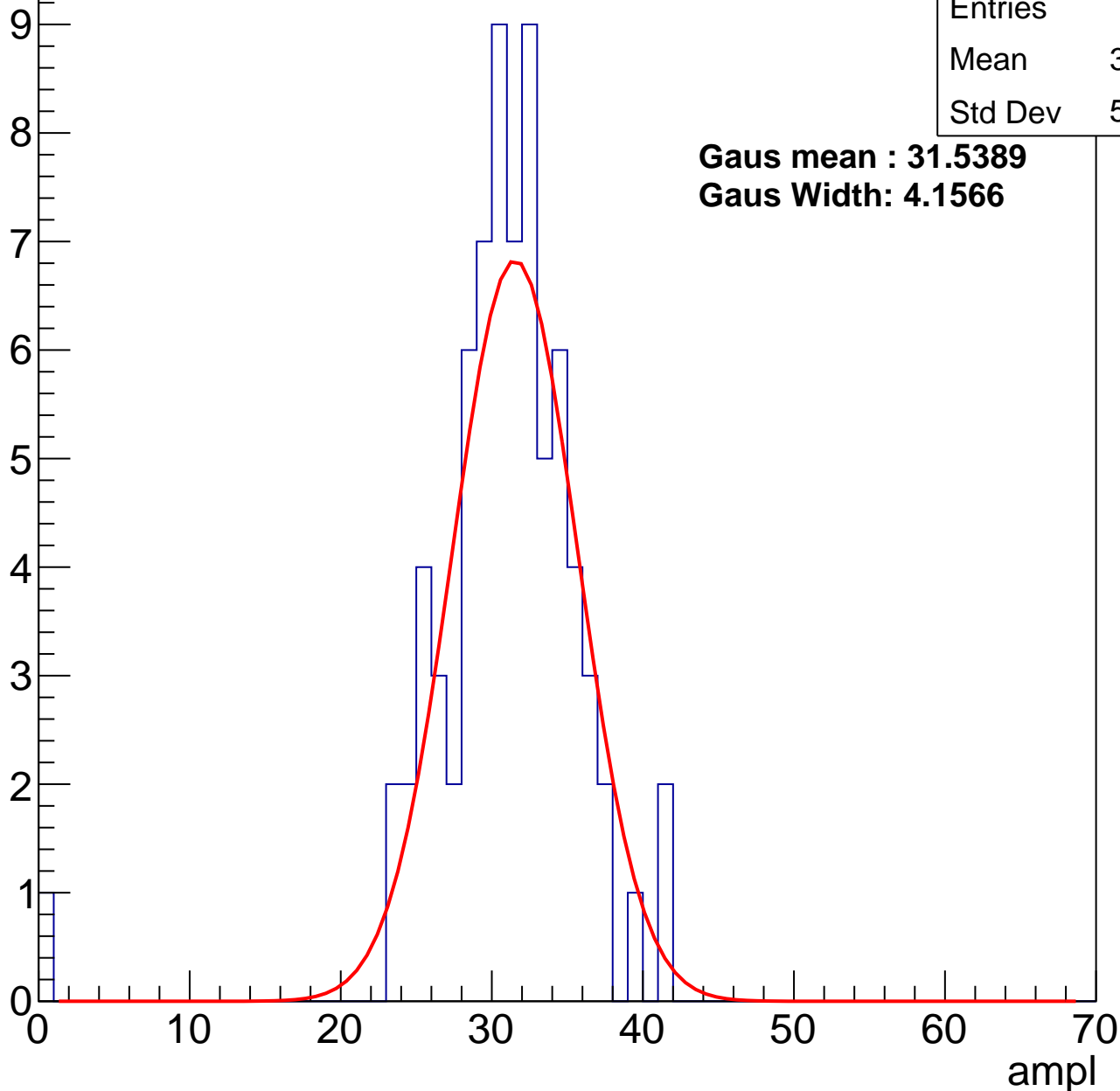
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.45
Std Dev	5.247

Gaus mean : 31.5389

Gaus Width: 4.1566



B0L002S, U26-ch90, adc1

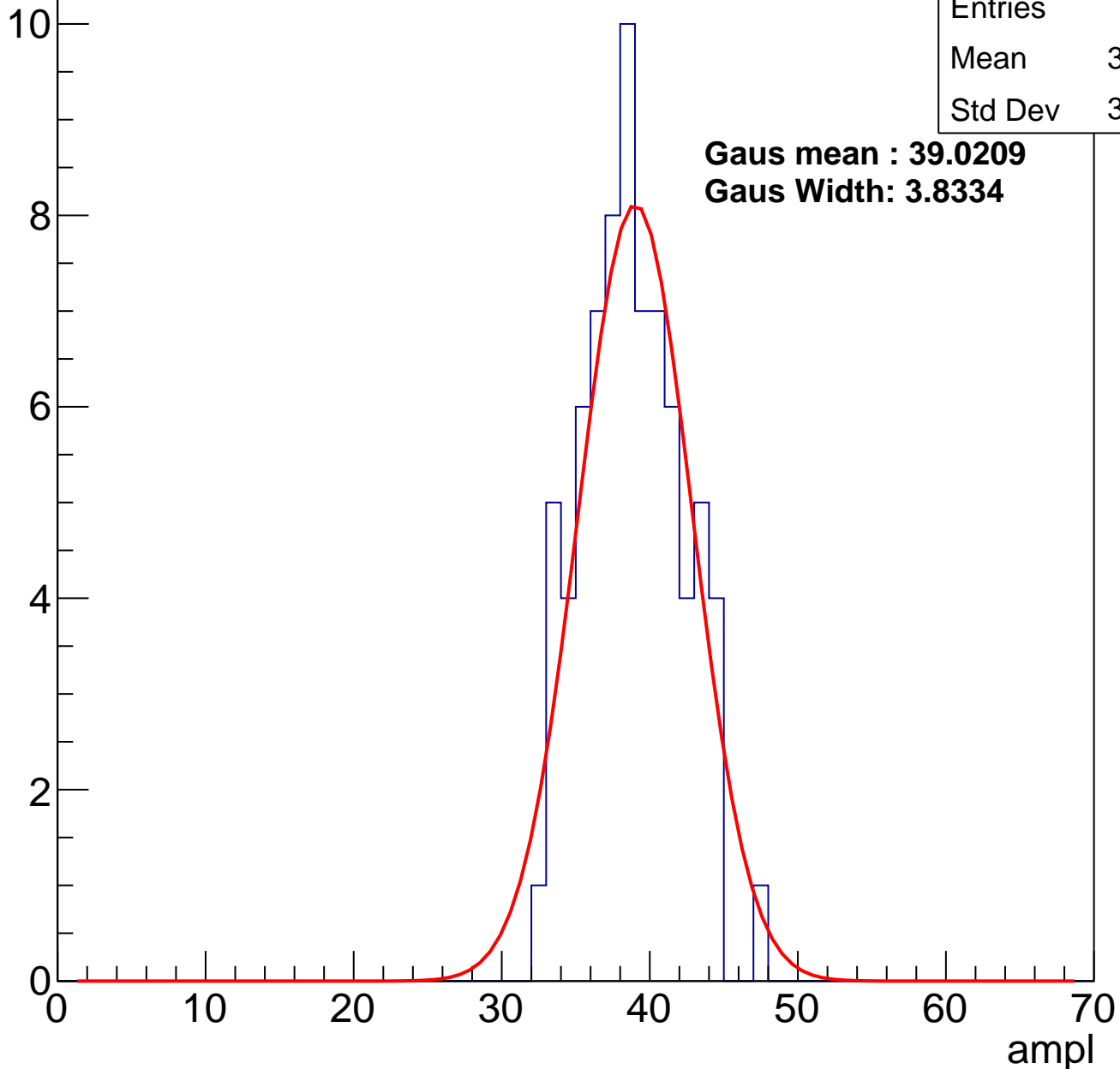
calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	38.35
Std Dev	3.284

Gaus mean : 39.0209

Gaus Width: 3.8334

Entry



B0L002S, U26-ch90, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	45.66
Std Dev	3.557

Gaus mean : 45.8204

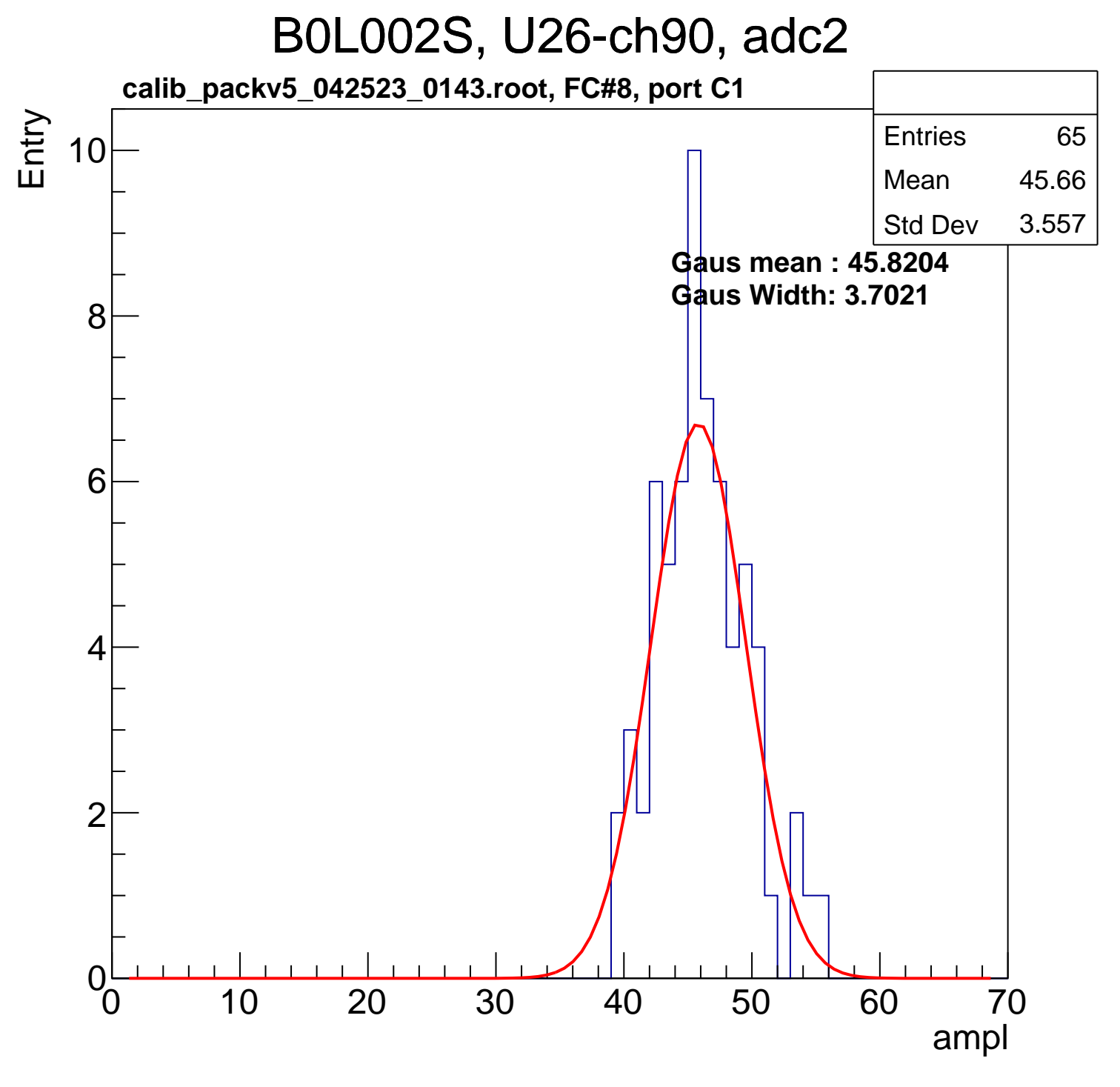
Gaus Width: 3.7021

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

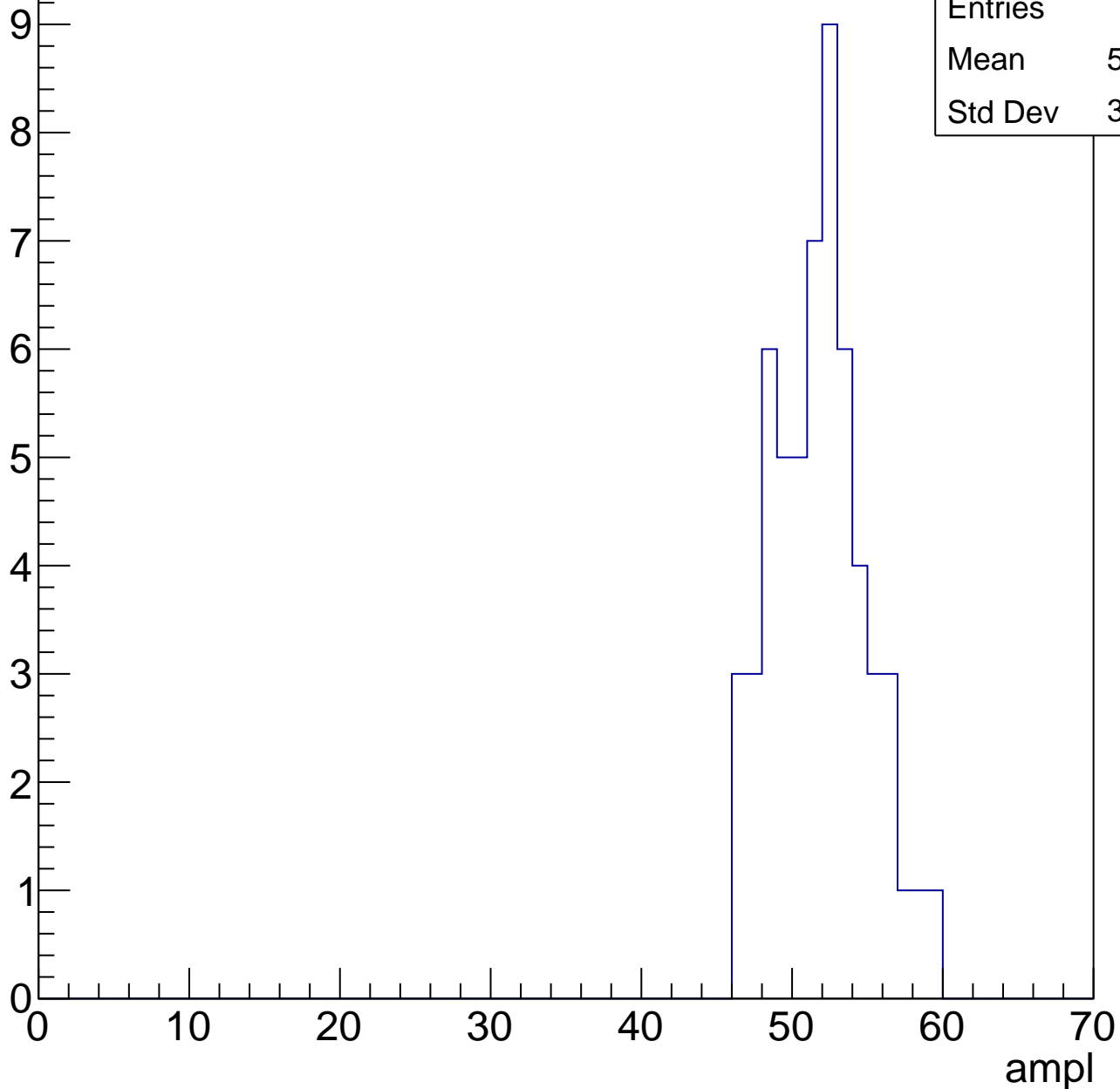


B0L002S, U26-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

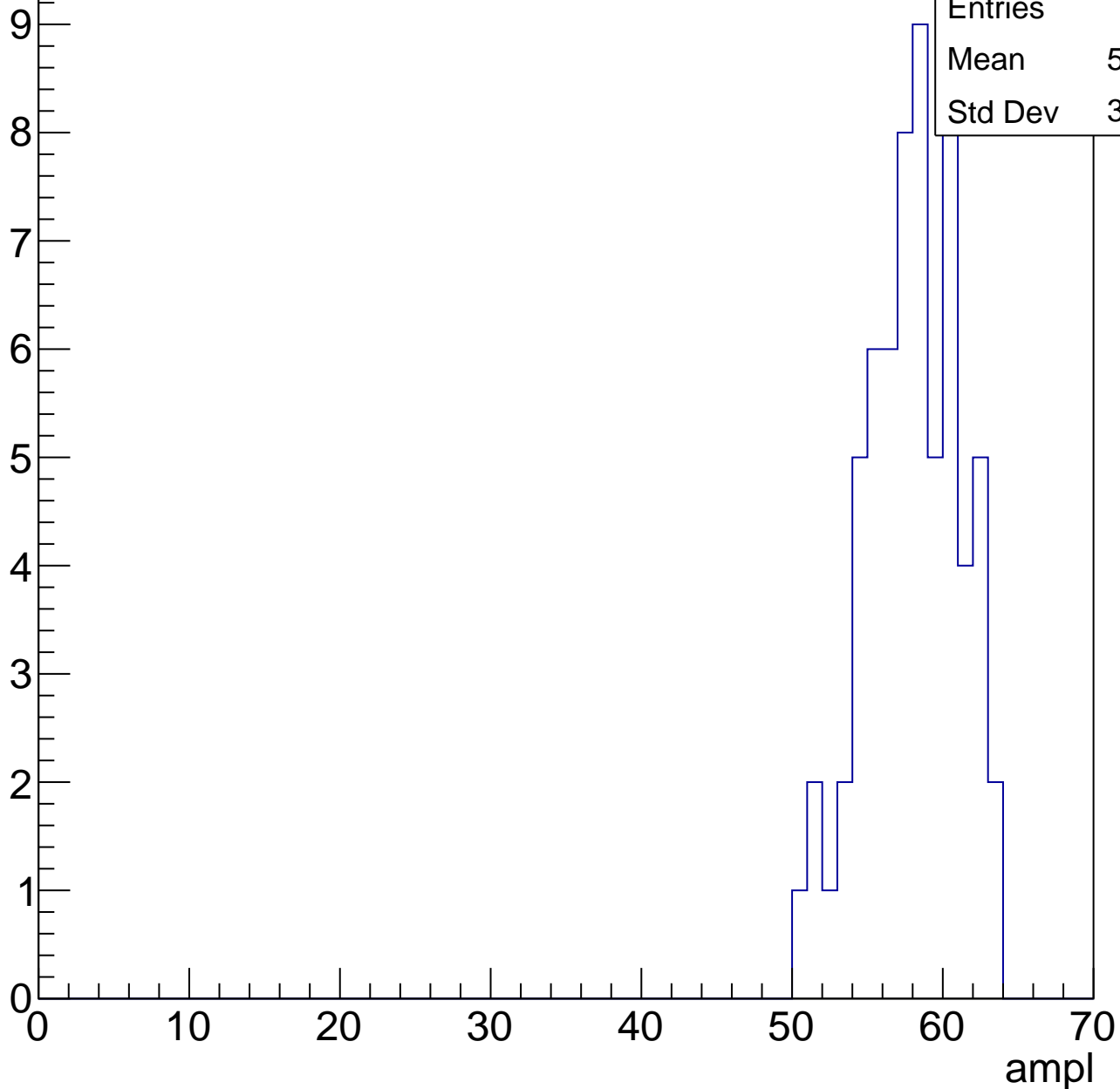
Entries	57
Mean	51.37
Std Dev	3.064



B0L002S, U26-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

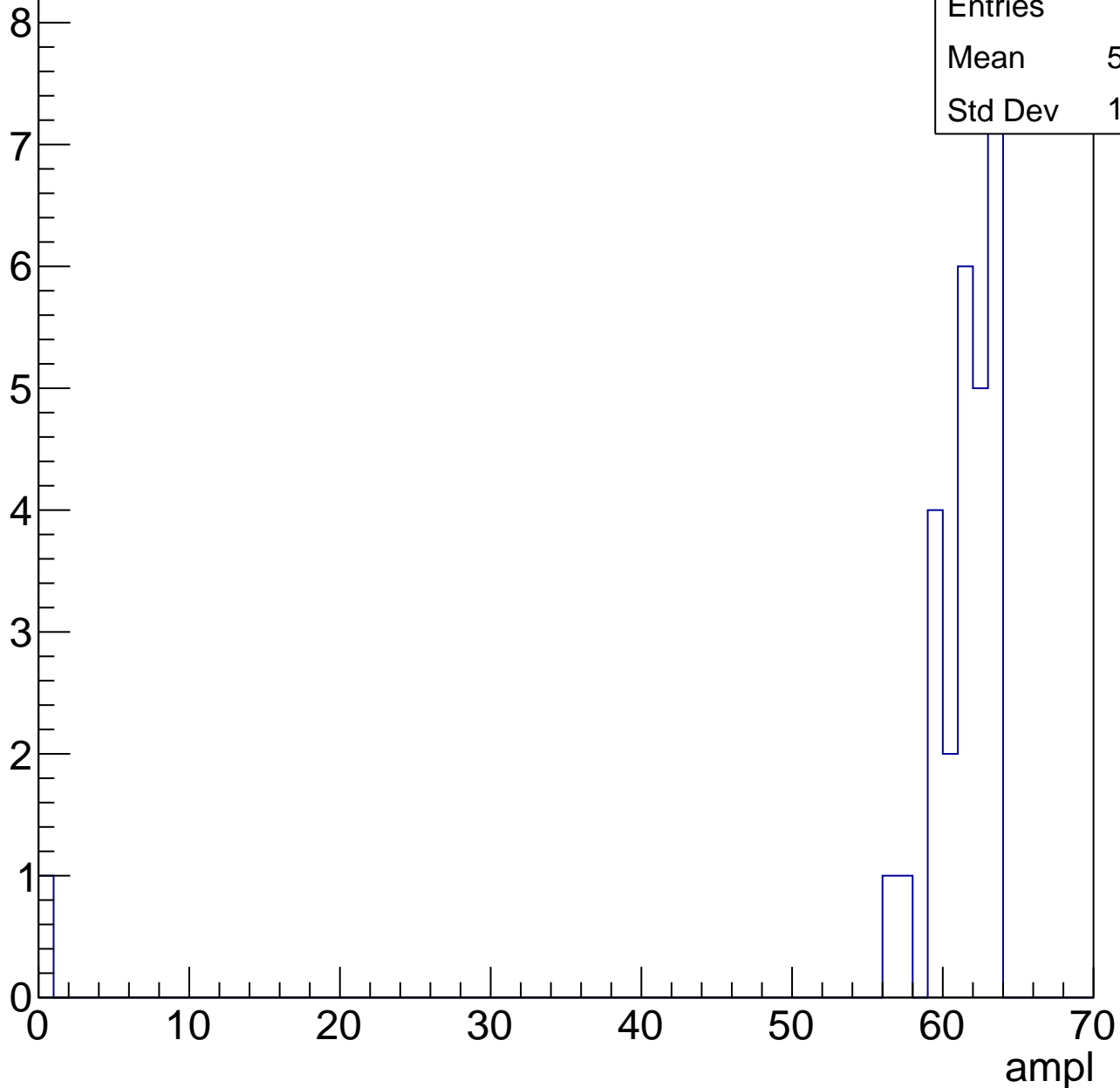


B0L002S, U26-ch90, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.89
Std Dev	11.48



B0L002S, U26-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U26-ch91, adc0

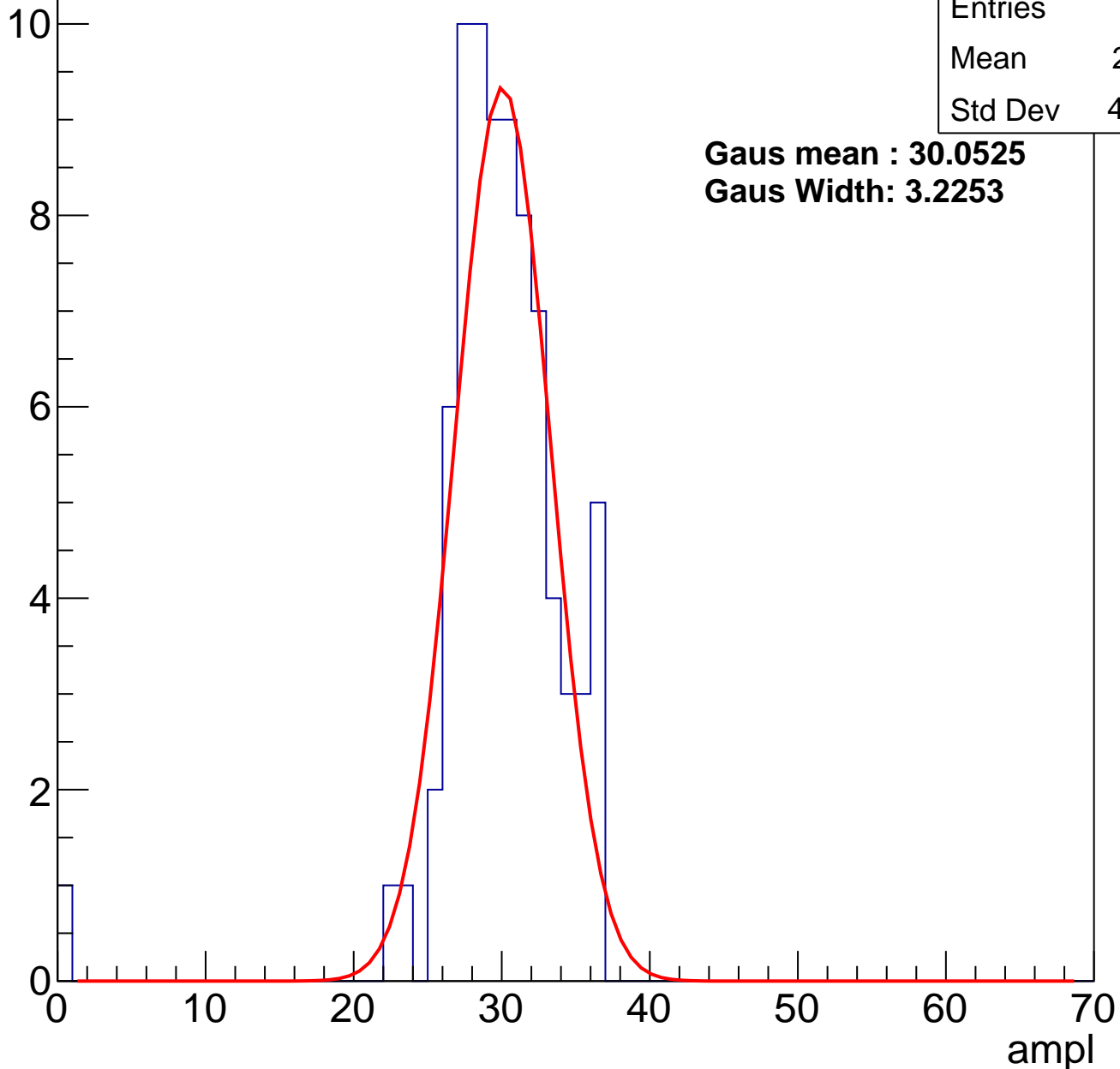
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	29.41
Std Dev	4.563

Gaus mean : 30.0525

Gaus Width: 3.2253

Entry



B0L002S, U26-ch91, adc1

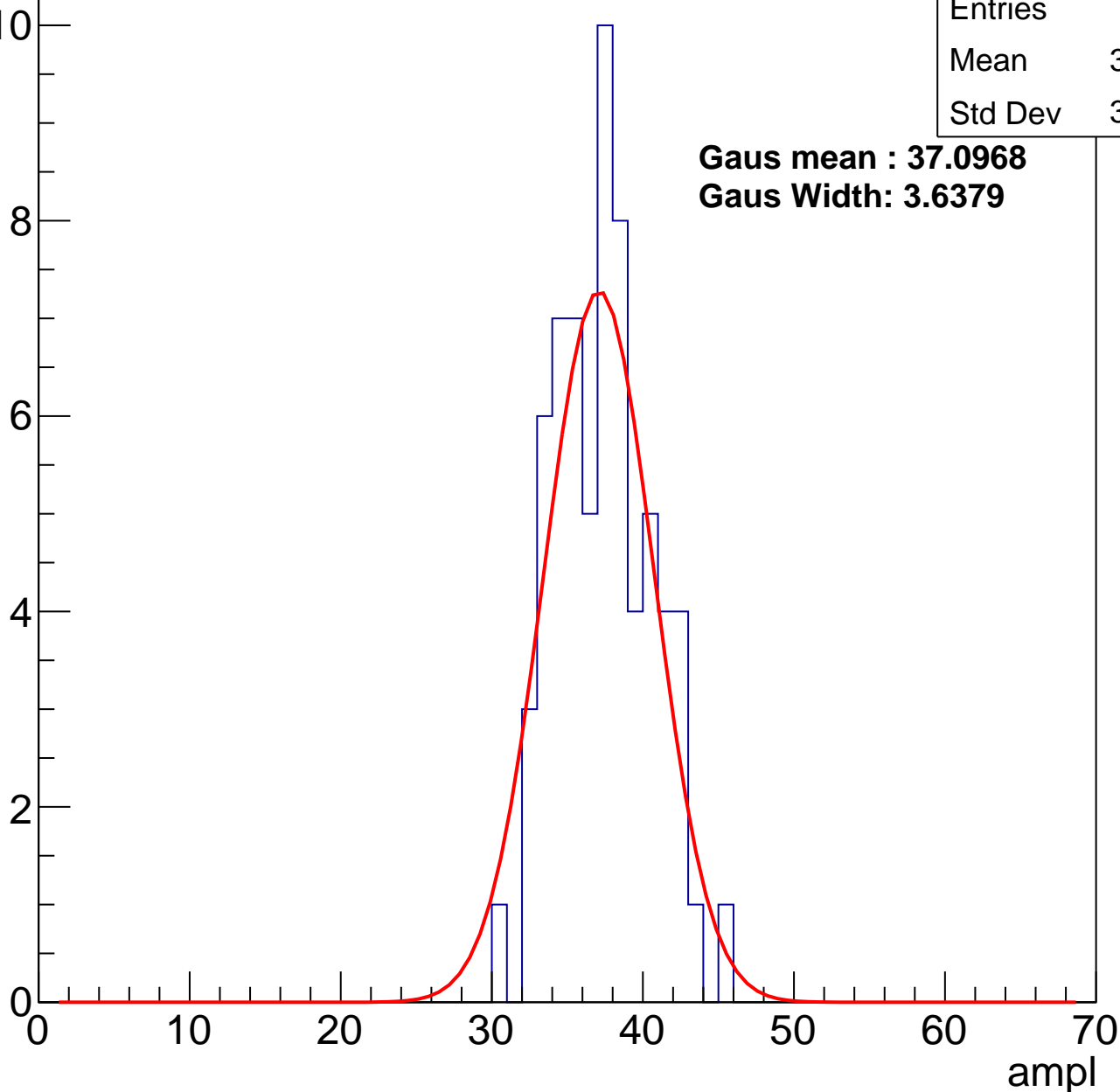
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.92
Std Dev	3.135

Gaus mean : 37.0968

Gaus Width: 3.6379



B0L002S, U26-ch91, adc2

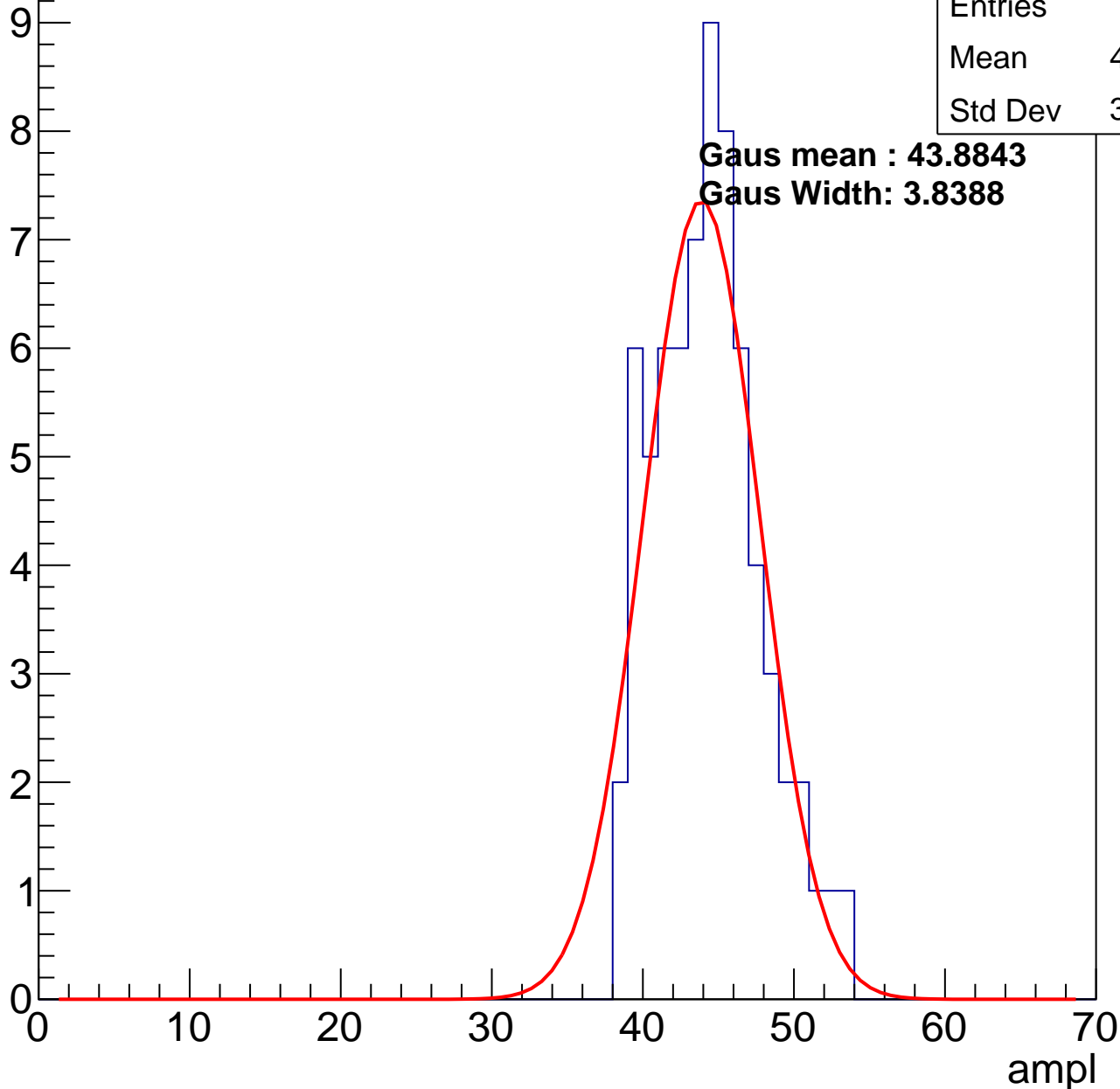
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.87
Std Dev	3.443

Gaus mean : 43.8843

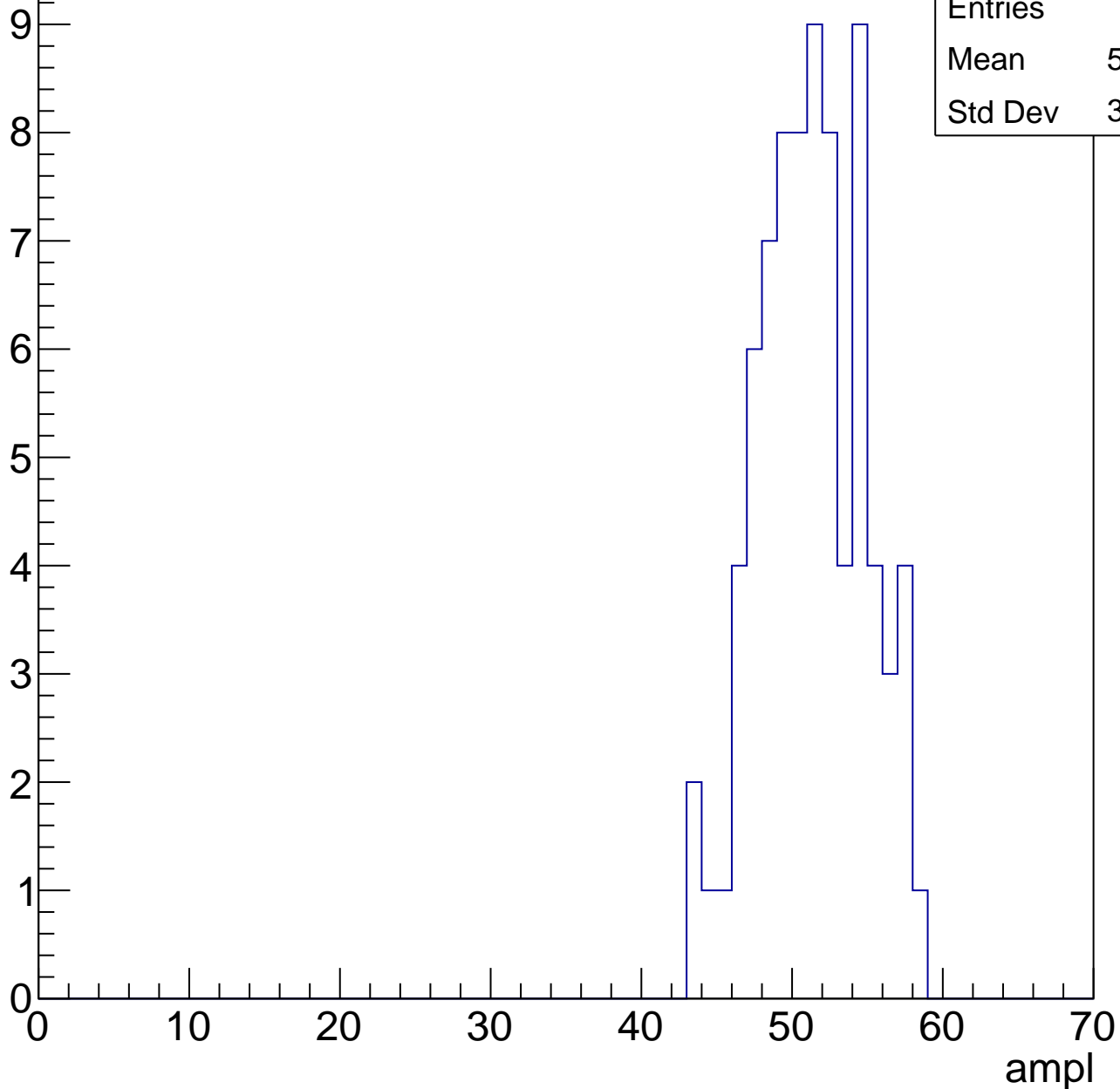
Gaus Width: 3.8388



B0L002S, U26-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



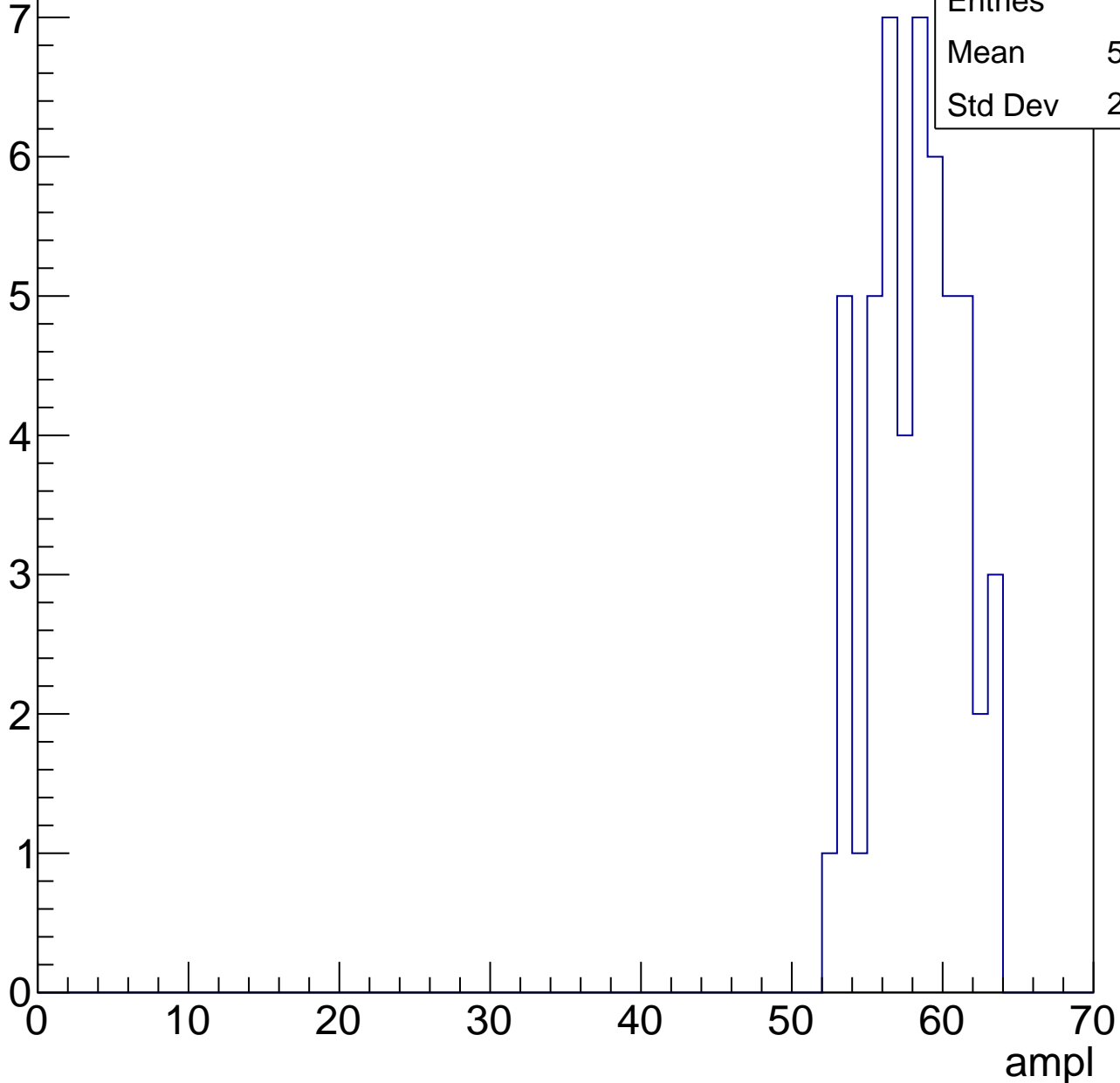
Entries	79
Mean	50.84
Std Dev	3.462

B0L002S, U26-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57.73
Std Dev	2.897

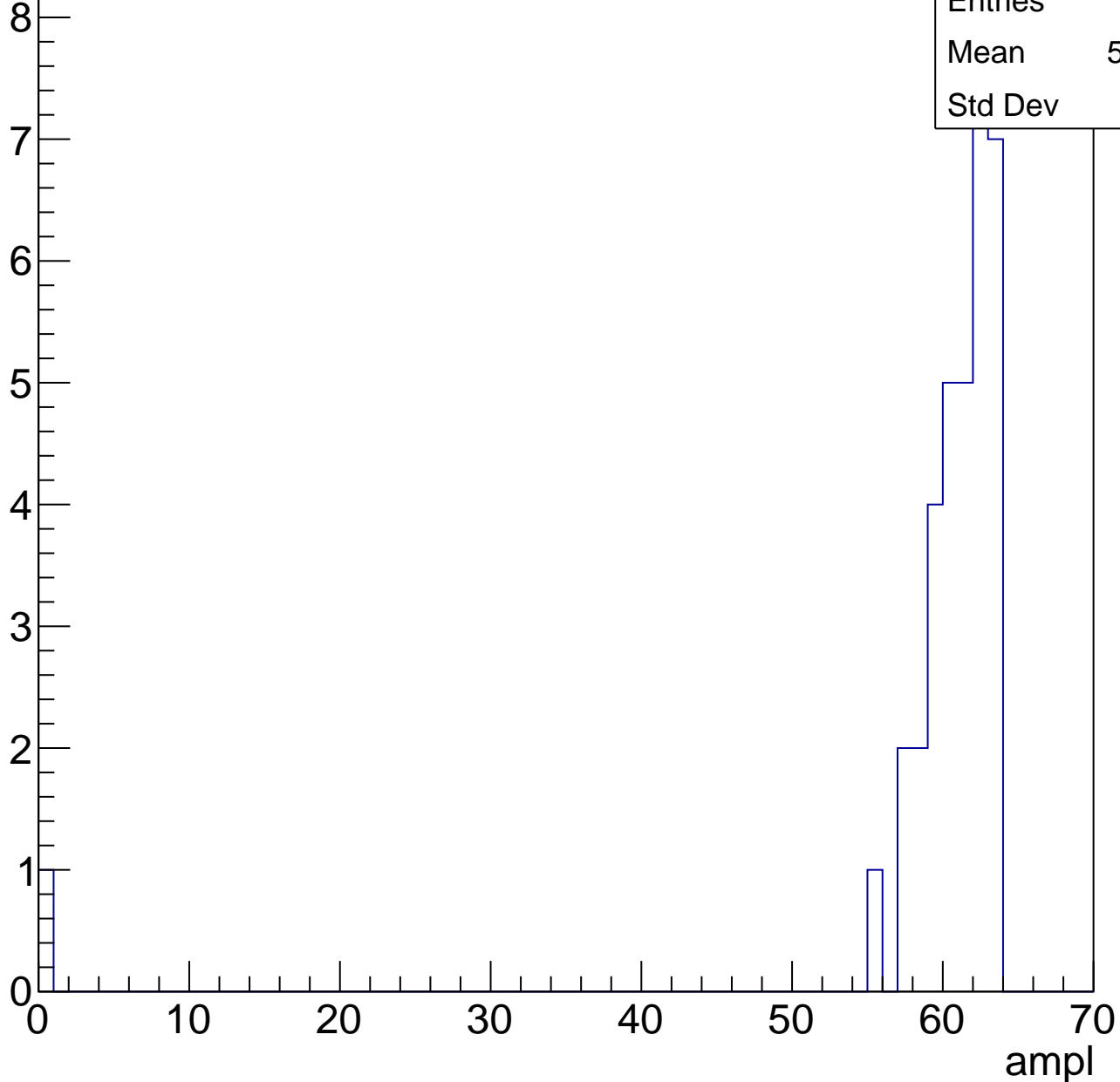


B0L002S, U26-ch91, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.94
Std Dev	10.3



B0L002S, U26-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch92, adc0

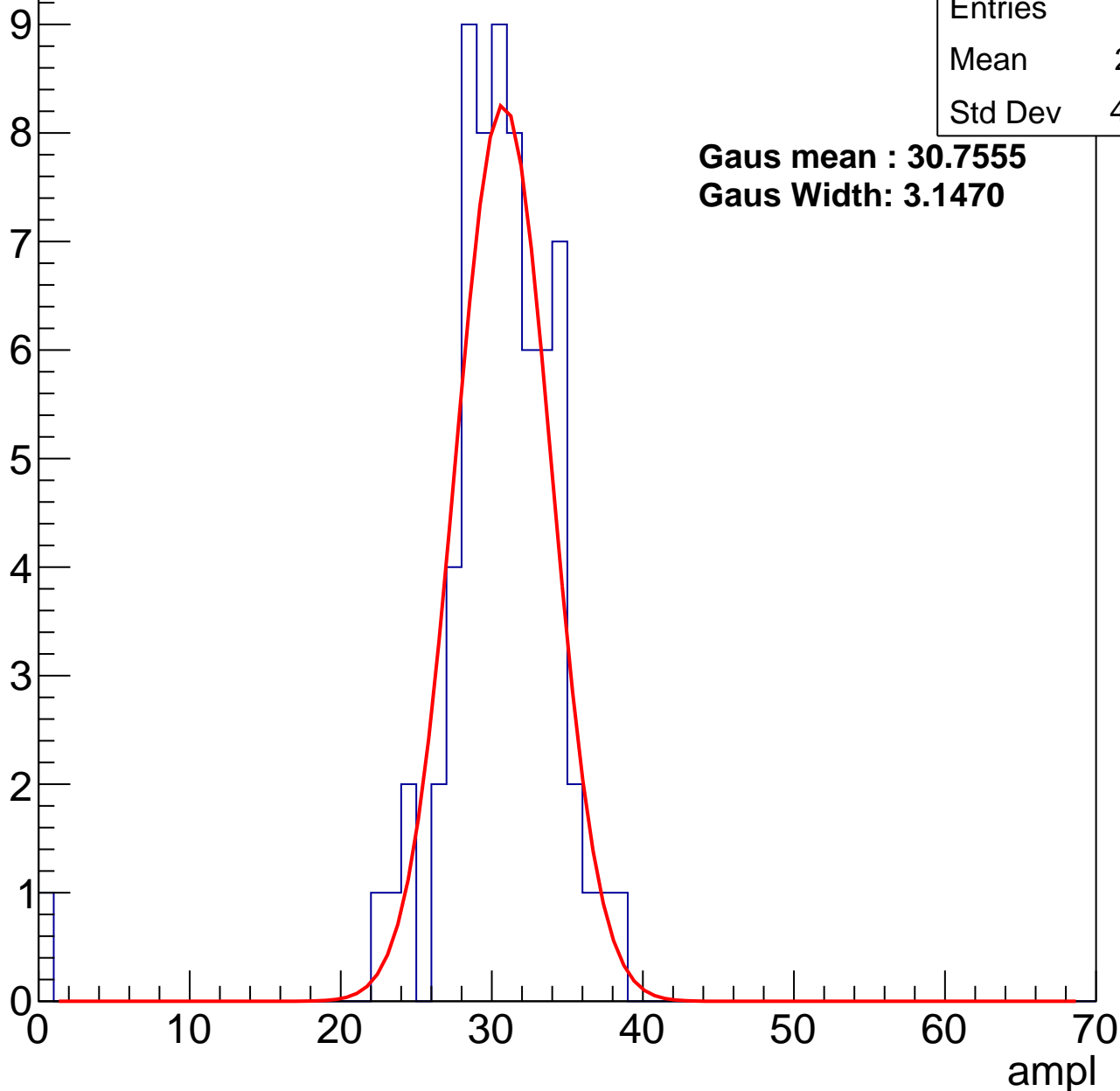
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.91
Std Dev	4.806

Gaus mean : 30.7555

Gaus Width: 3.1470



B0L002S, U26-ch92, adc1

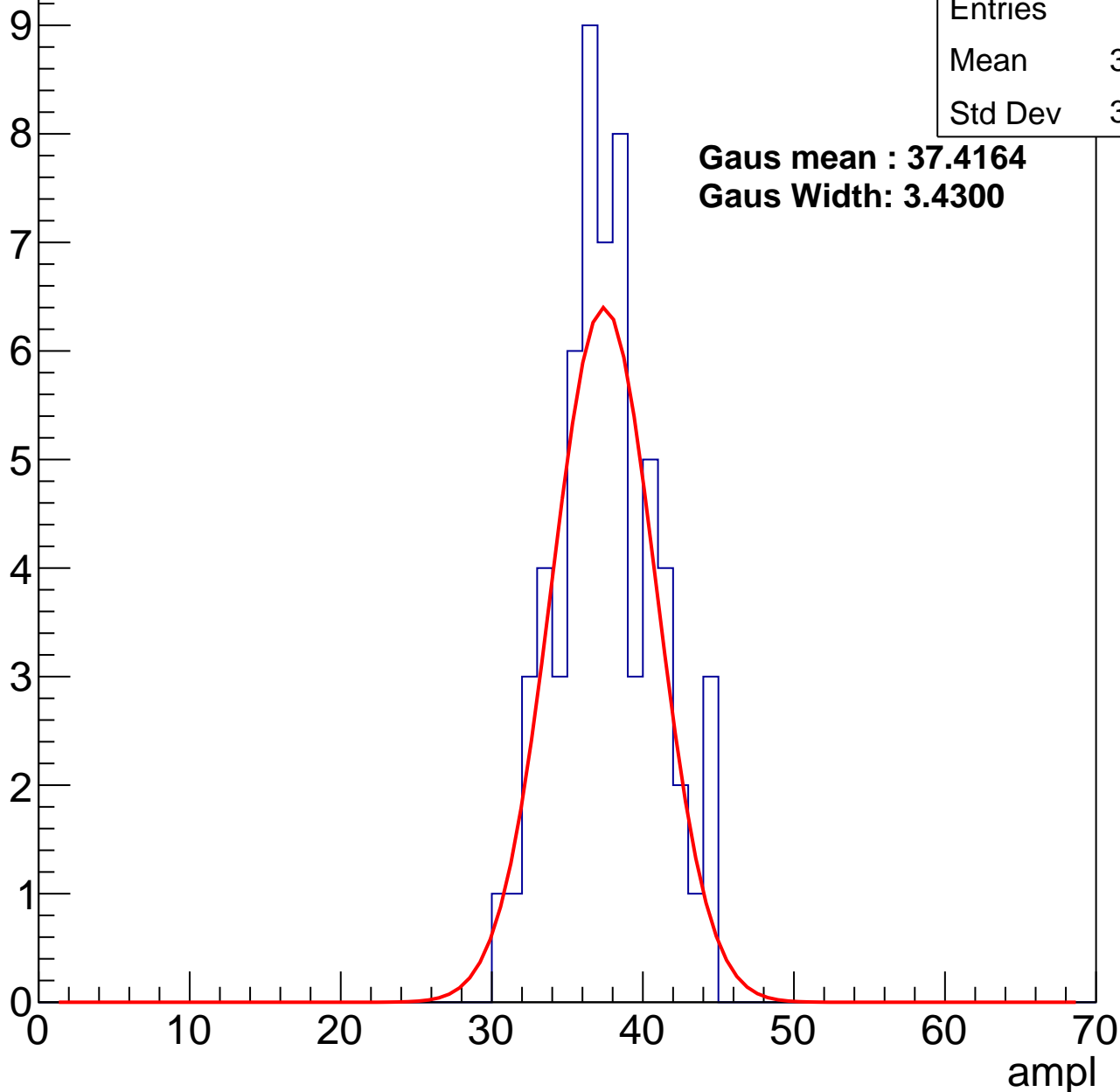
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.13
Std Dev	3.279

Gaus mean : 37.4164

Gaus Width: 3.4300



B0L002S, U26-ch92, adc2

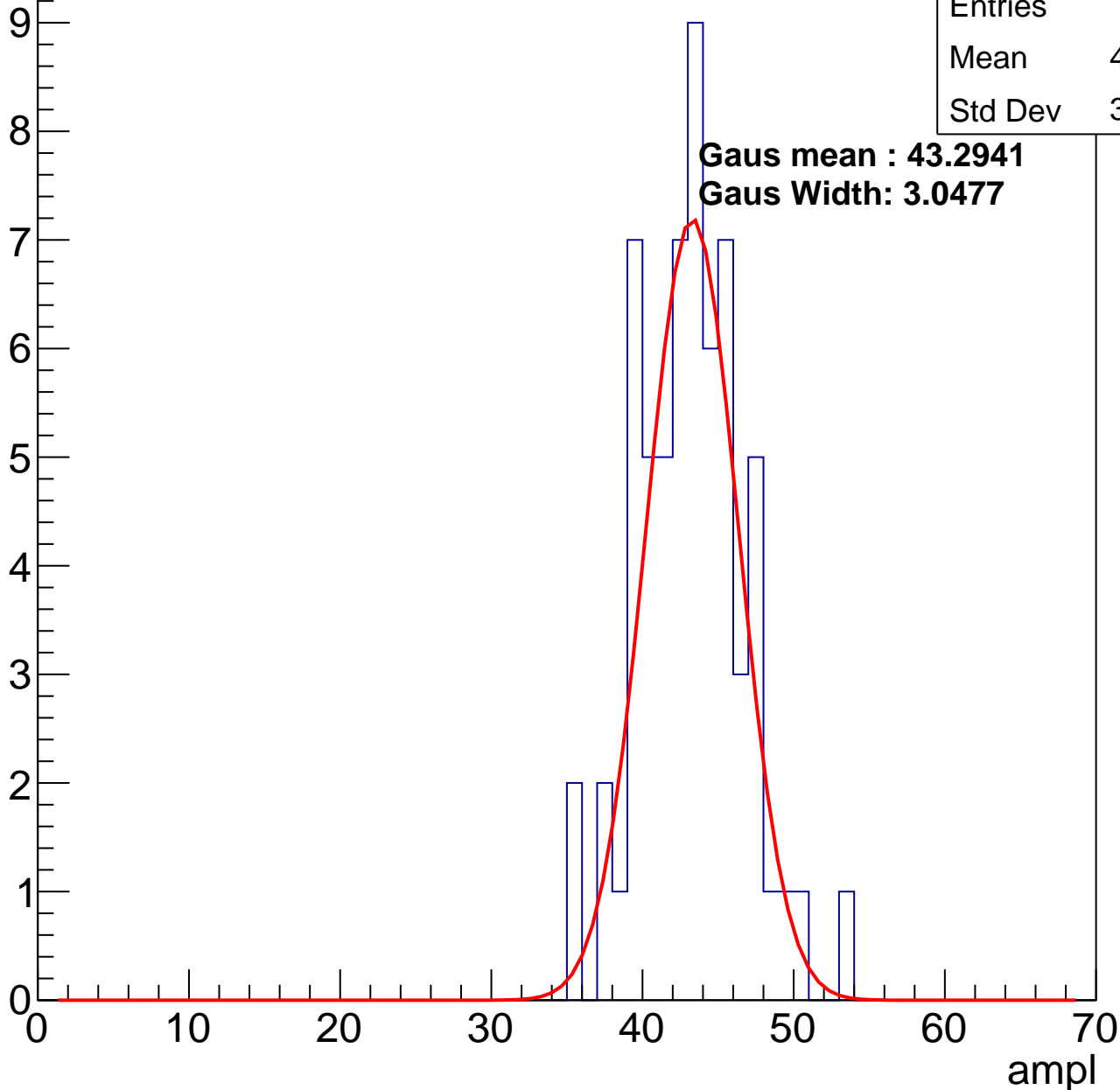
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.75
Std Dev	3.464

Gaus mean : 43.2941

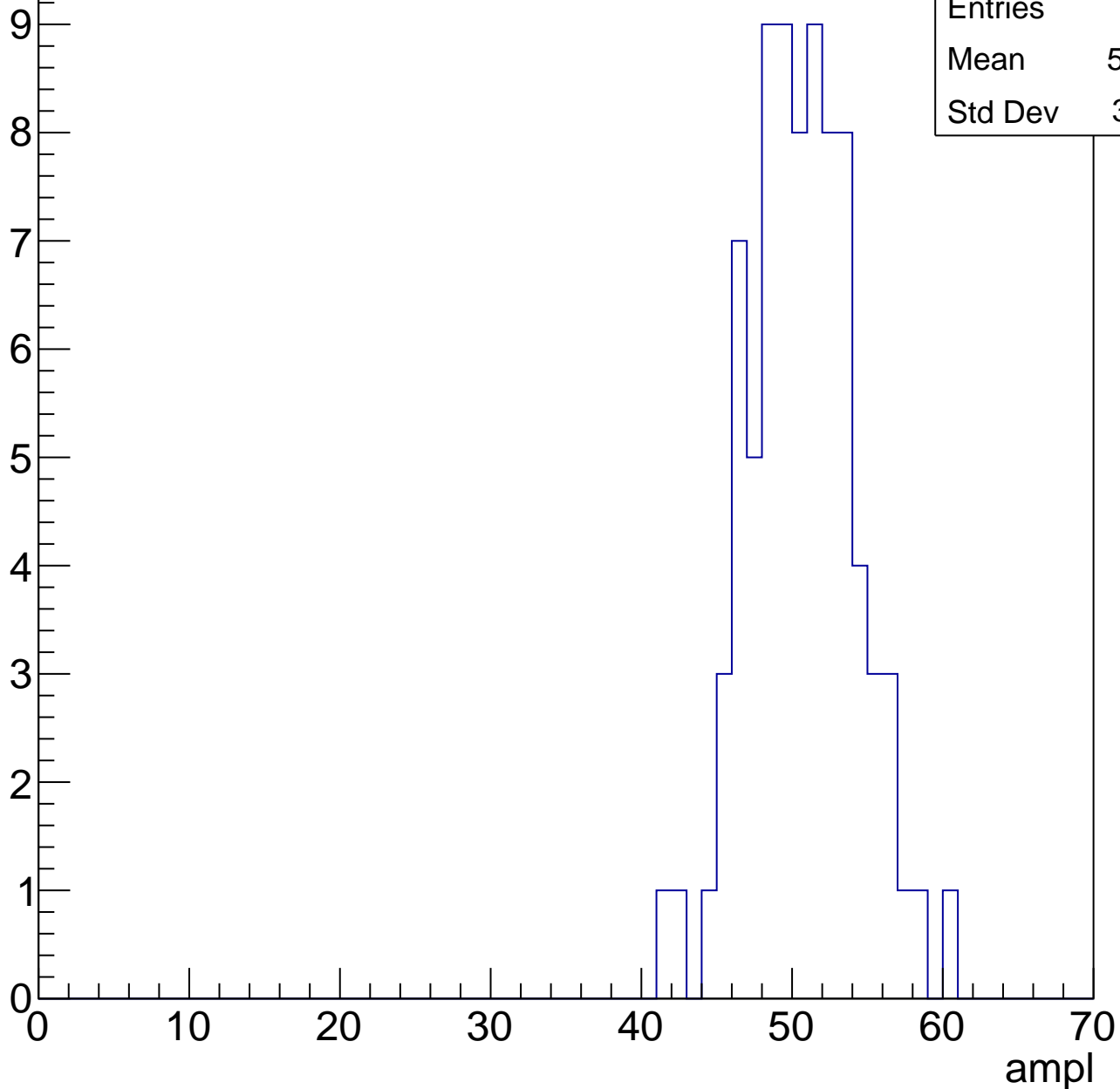
Gaus Width: 3.0477



B0L002S, U26-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

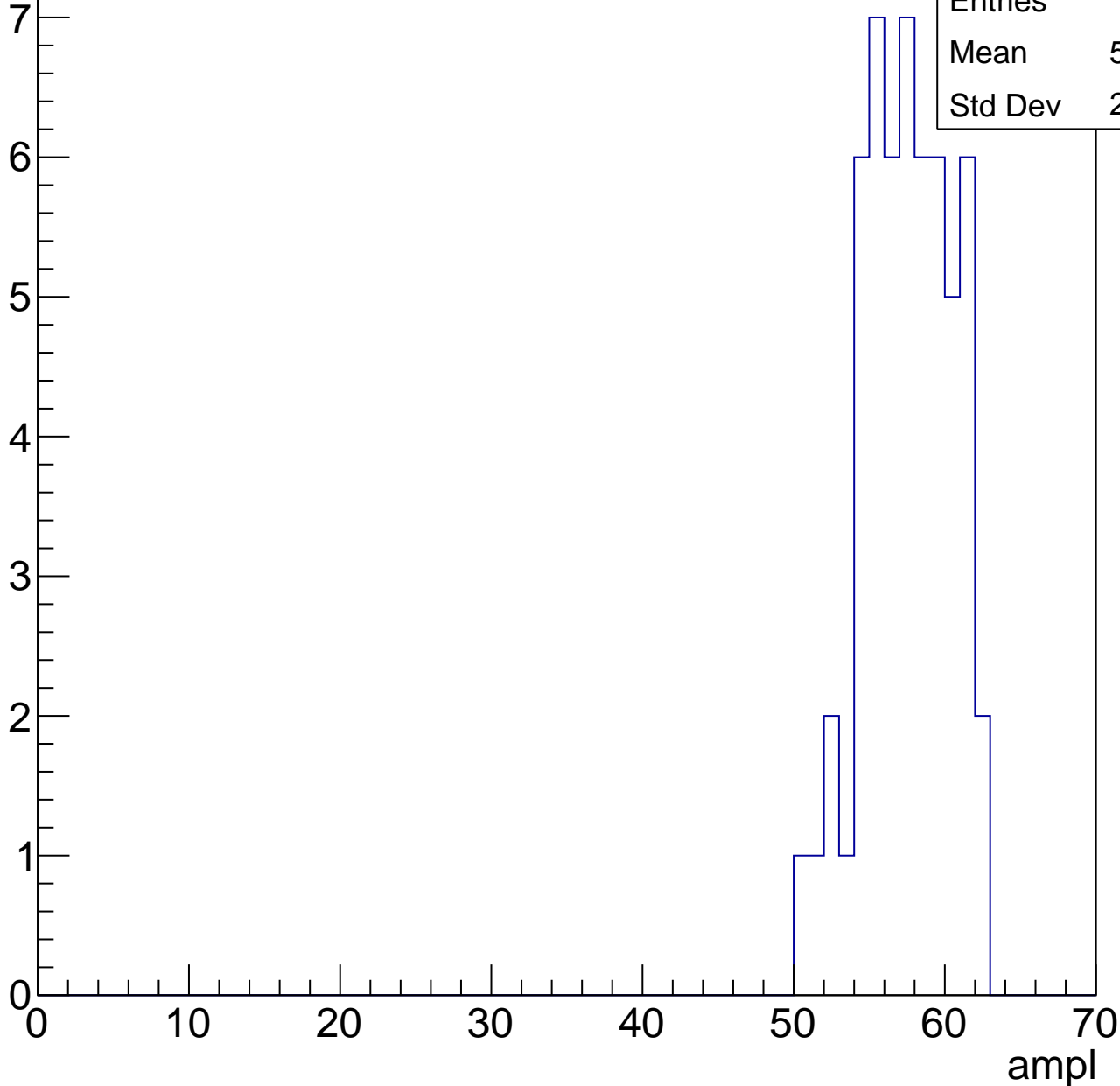


B0L002S, U26-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	57.04
Std Dev	2.866

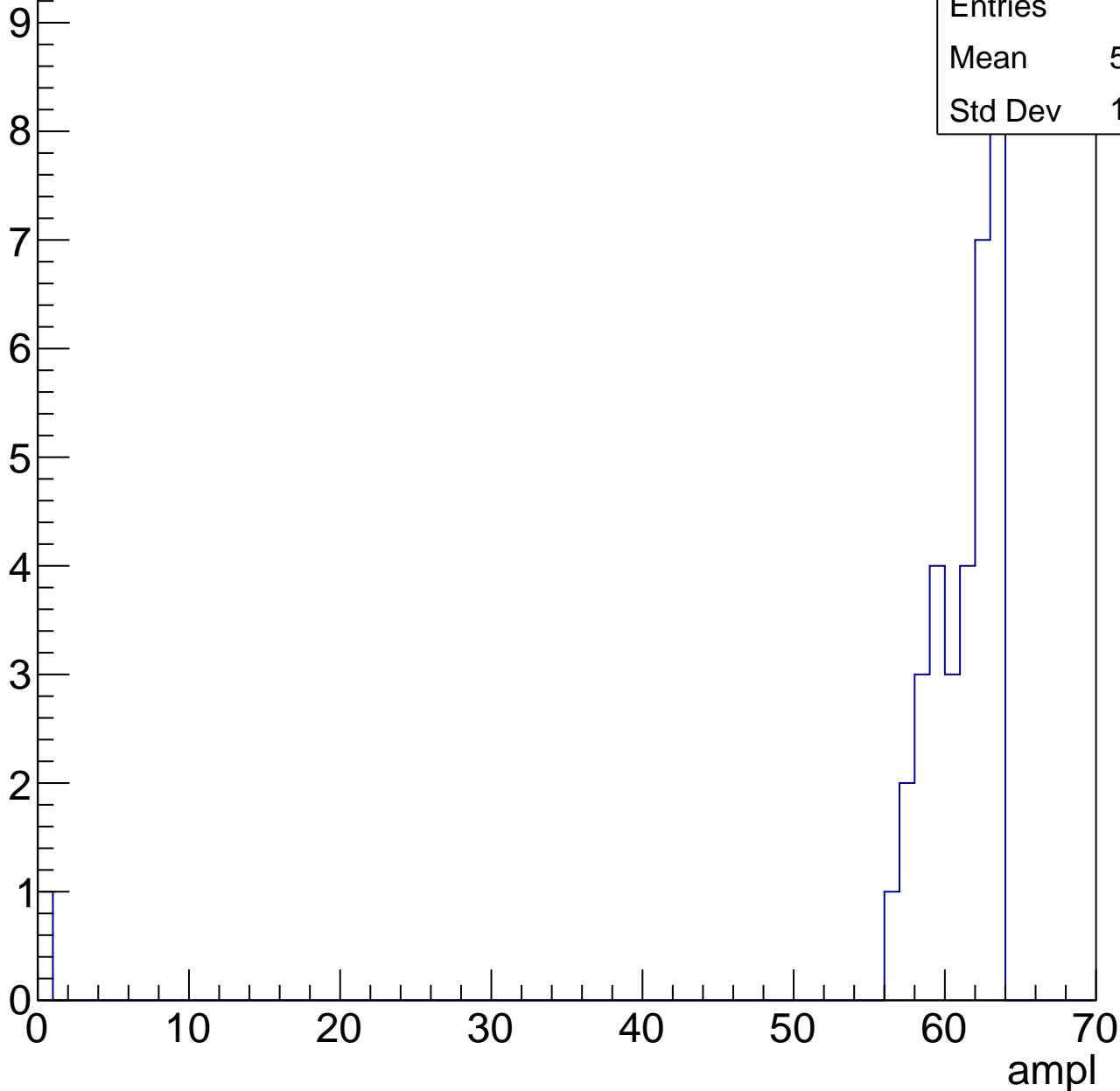


B0L002S, U26-ch92, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

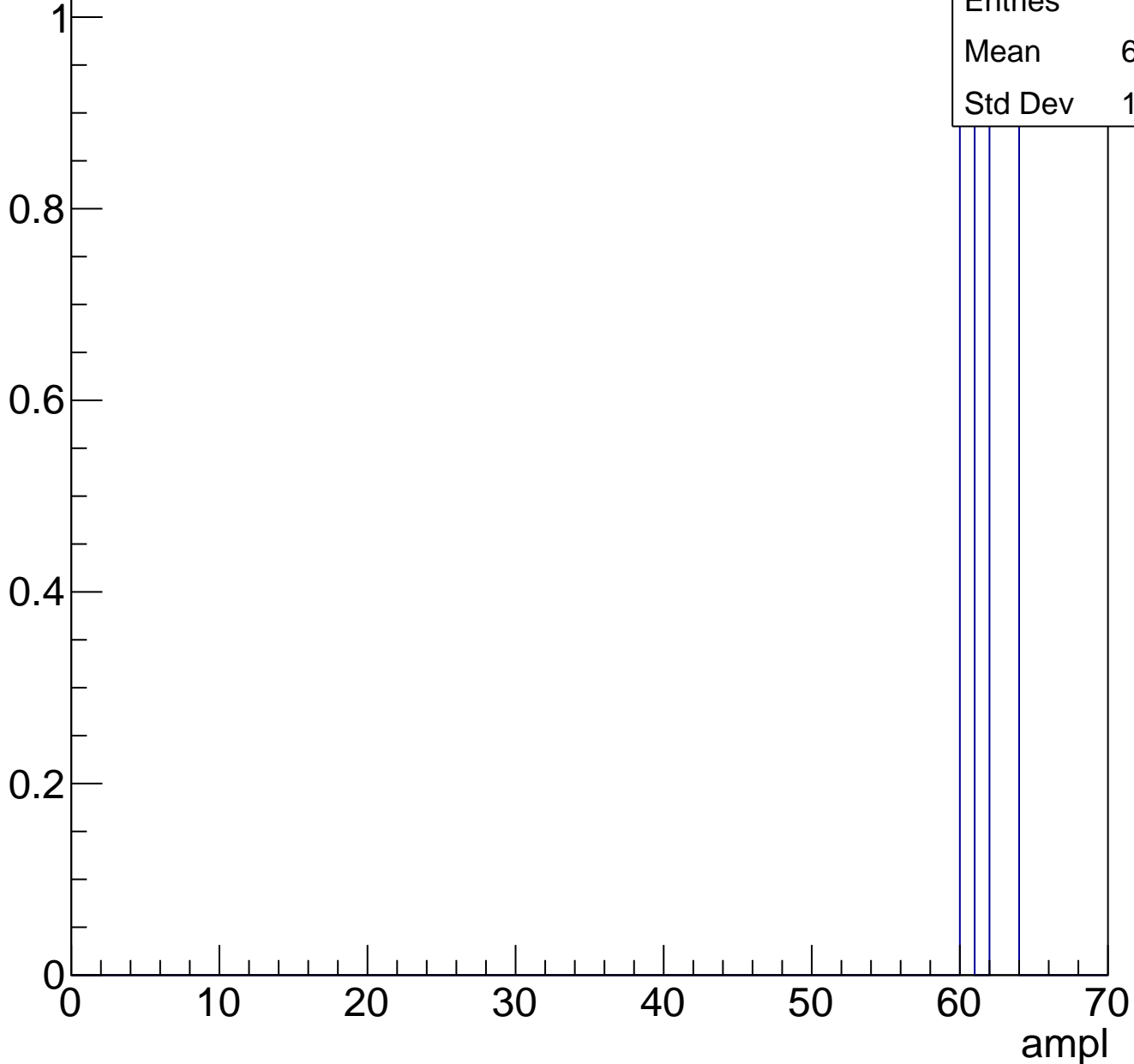
Entries	34
Mean	58.97
Std Dev	10.47



B0L002S, U26-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch93, adc0

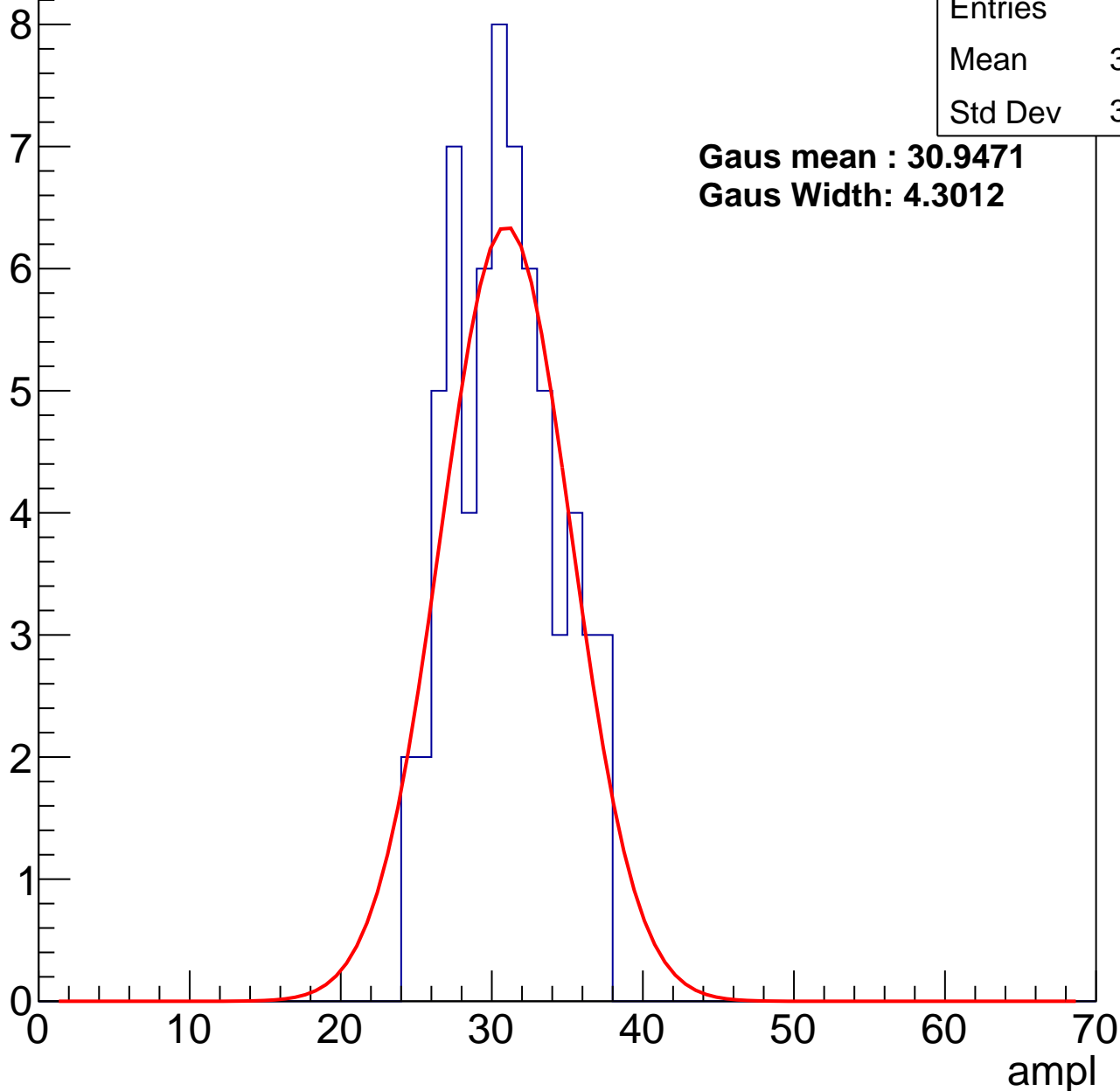
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.43
Std Dev	3.406

Gaus mean : 30.9471

Gaus Width: 4.3012



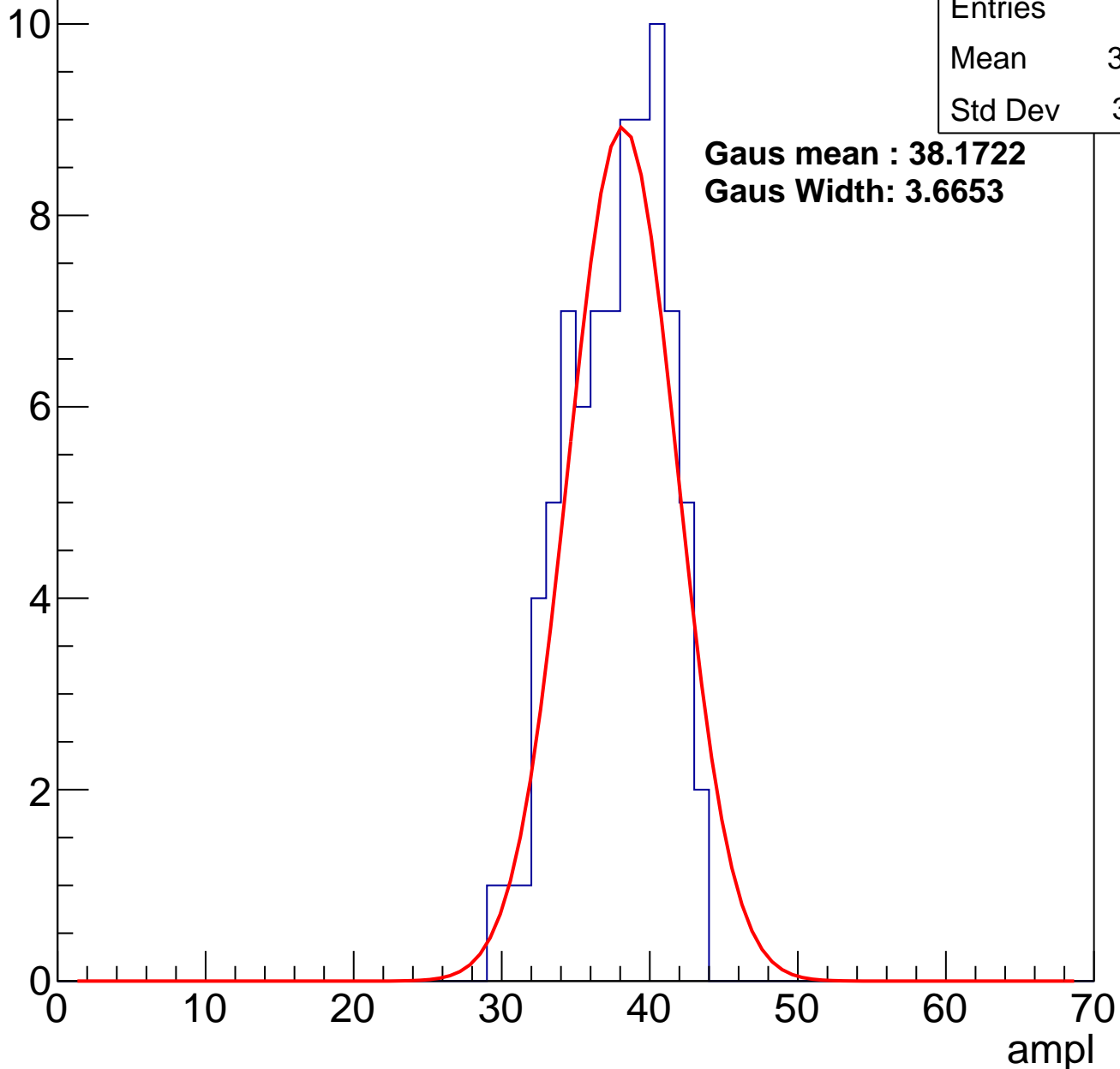
B0L002S, U26-ch93, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	37.26
Std Dev	3.261

Gaus mean : 38.1722
Gaus Width: 3.6653

Entry



B0L002S, U26-ch93, adc2

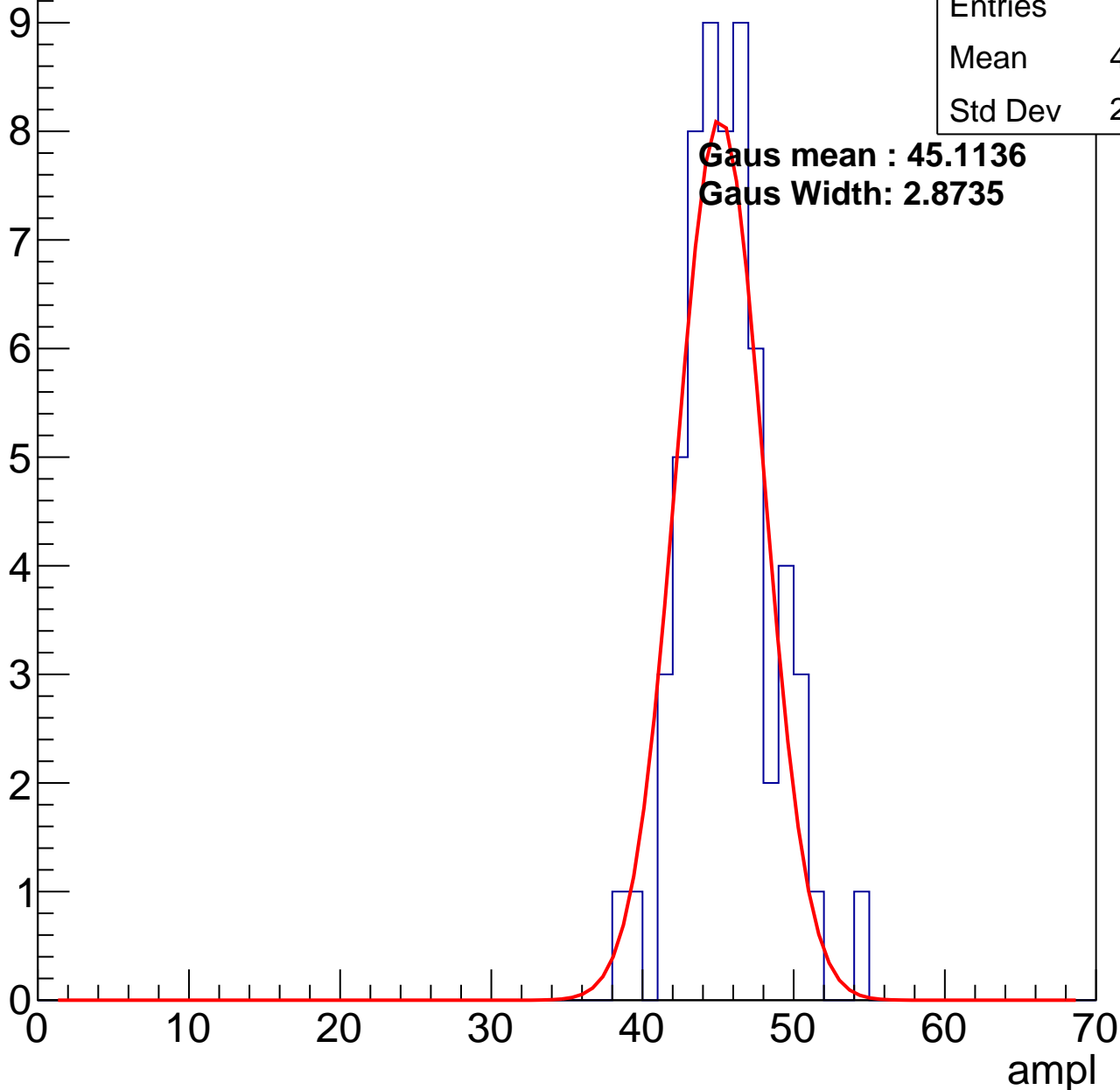
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	45.13
Std Dev	2.934

Gaus mean : 45.1136

Gaus Width: 2.8735

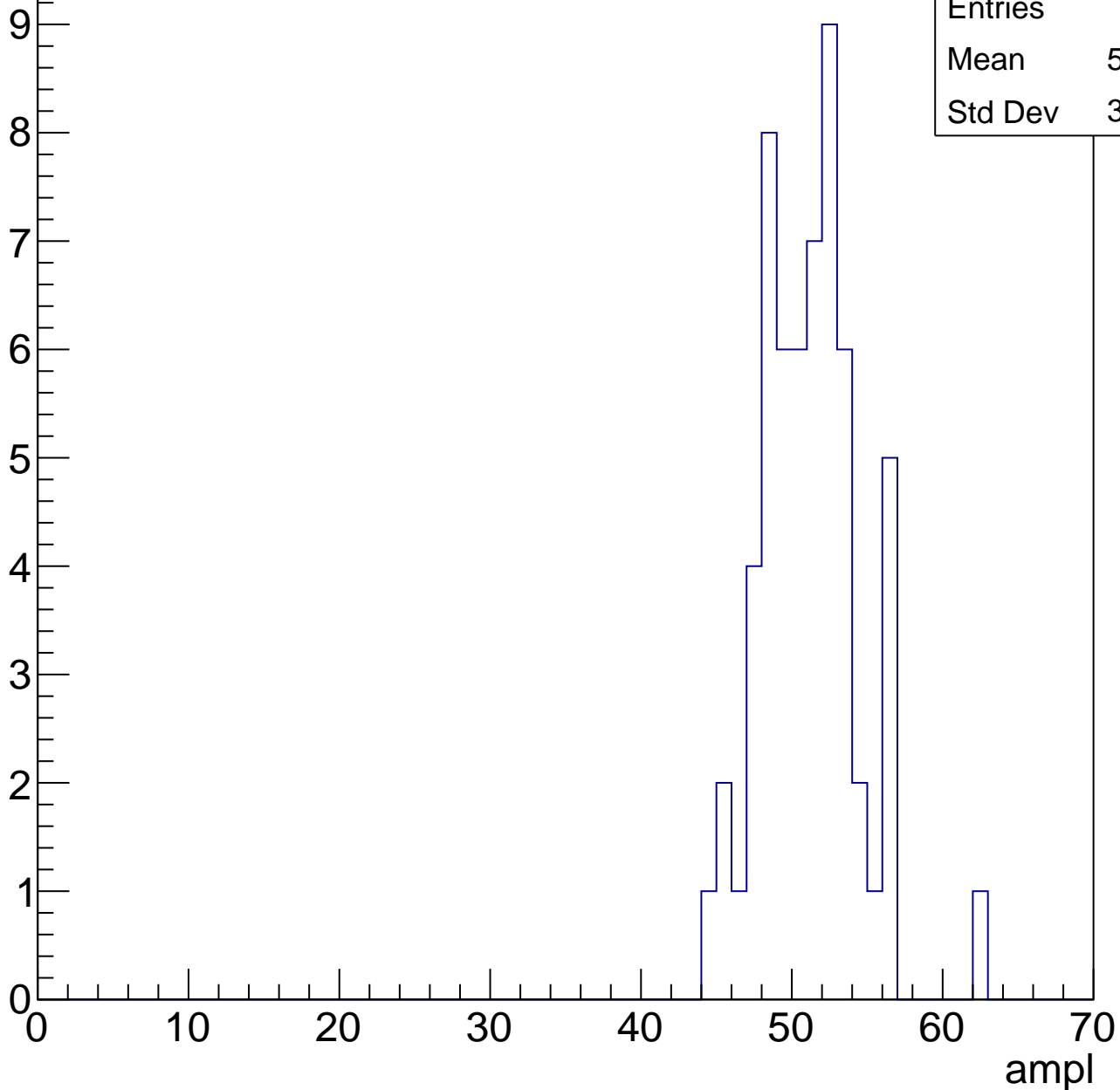


B0L002S, U26-ch93, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

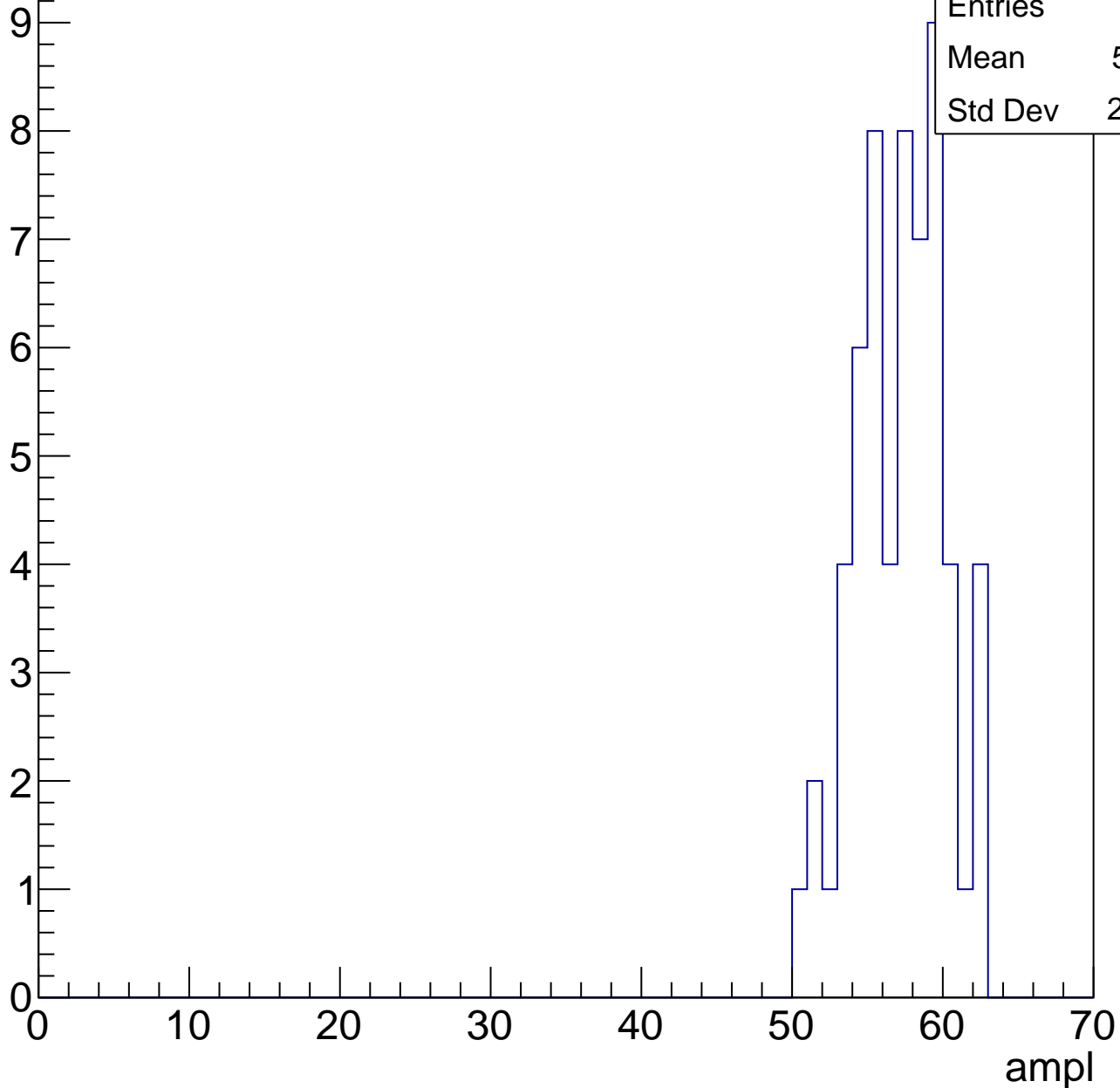
Entries	59
Mean	50.75
Std Dev	3.245



B0L002S, U26-ch93, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

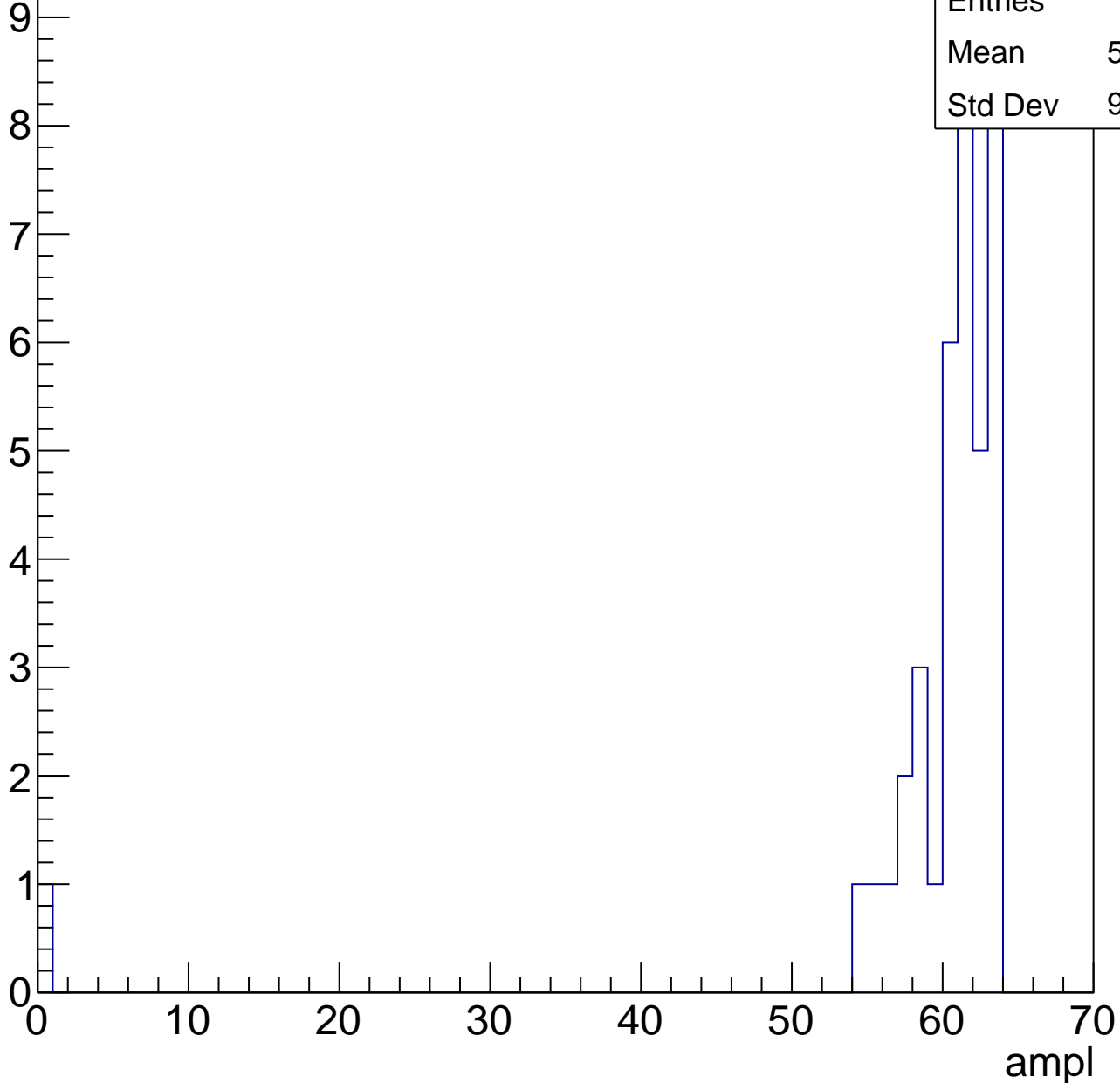


B0L002S, U26-ch93, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	58.92
Std Dev	9.836



B0L002S, U26-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U26-ch94, adc0

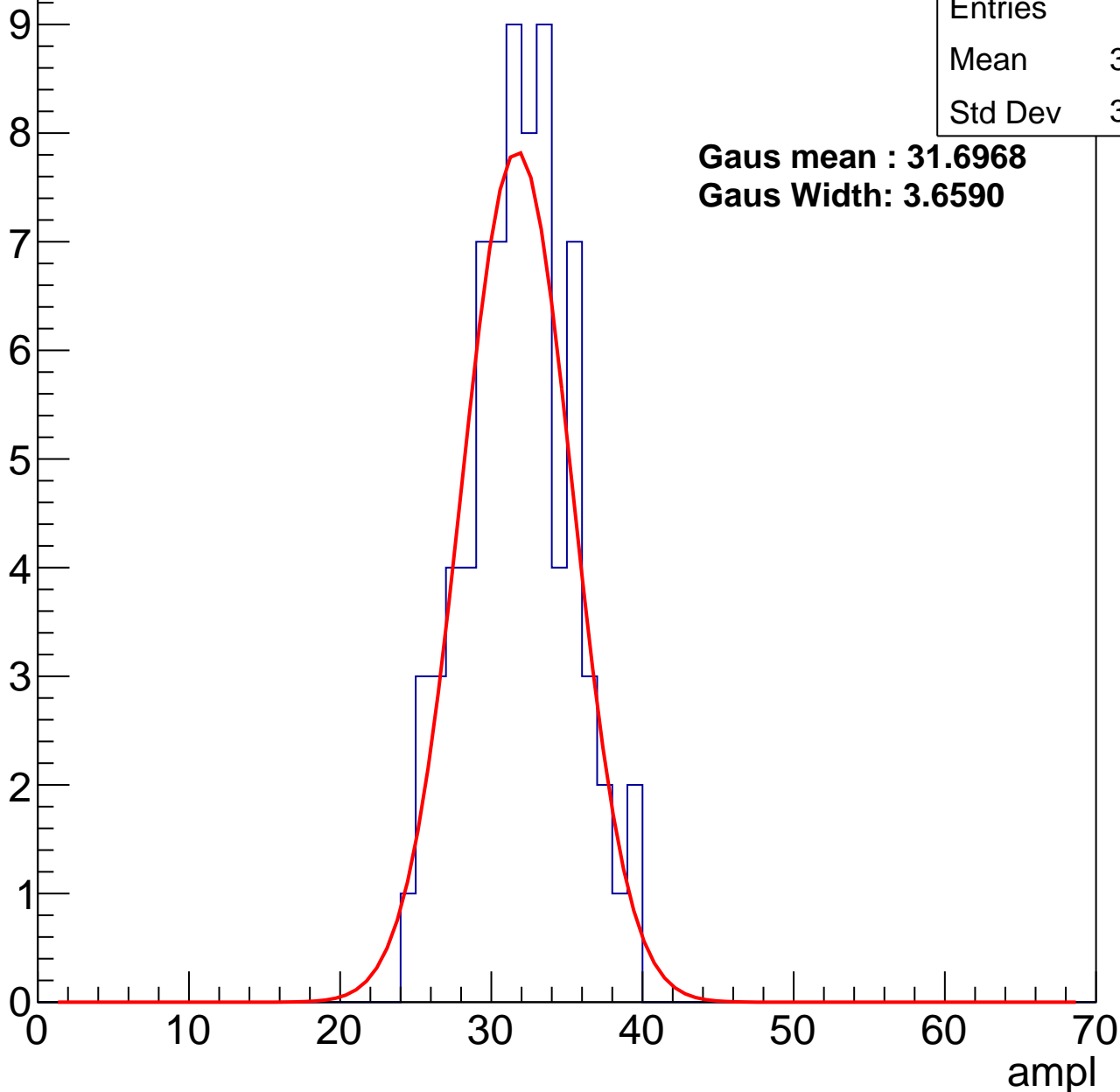
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	31.36
Std Dev	3.435

Gaus mean : 31.6968

Gaus Width: 3.6590



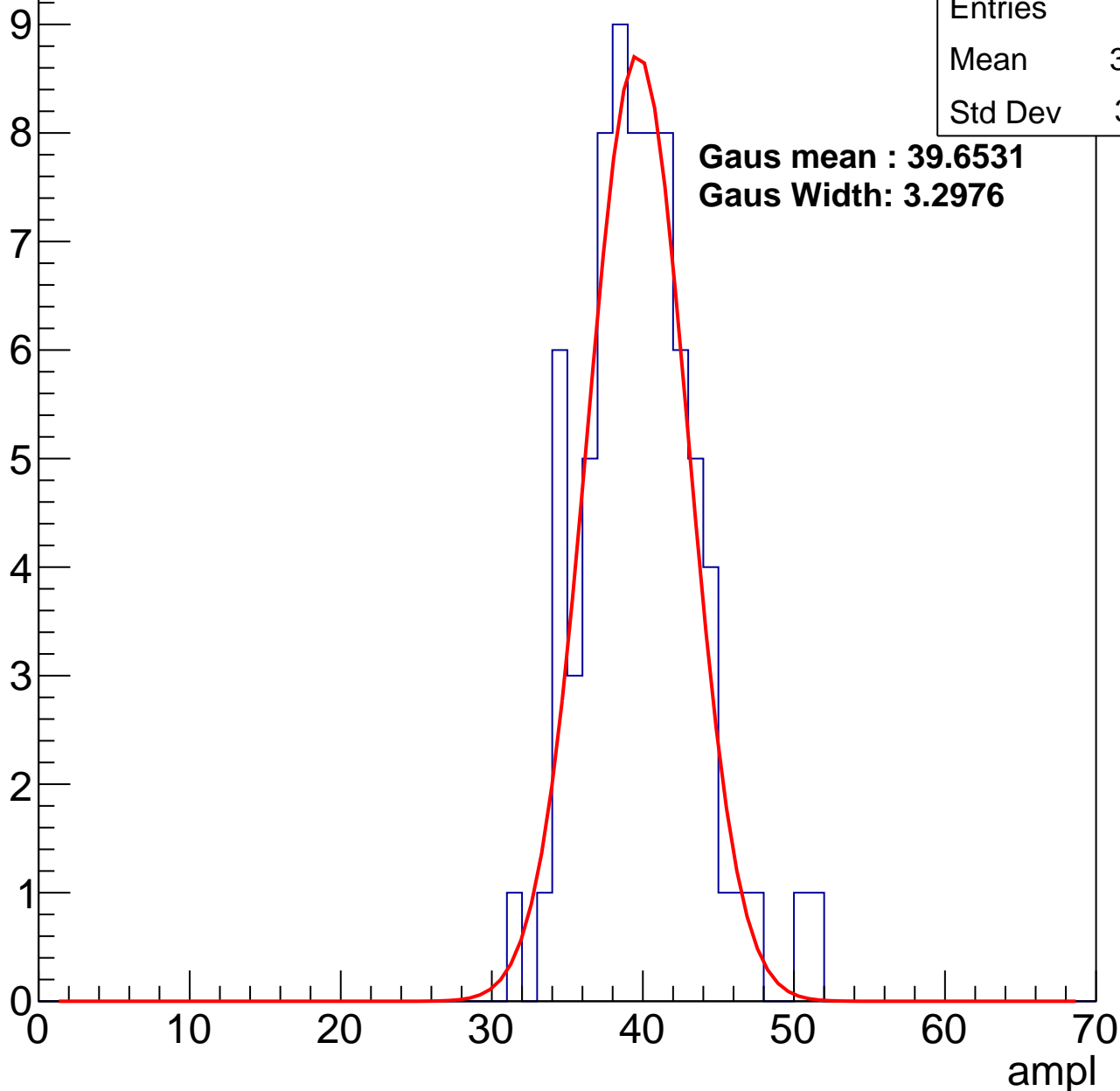
B0L002S, U26-ch94, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	39.39
Std Dev	3.711

Gaus mean : 39.6531
Gaus Width: 3.2976



B0L002S, U26-ch94, adc2

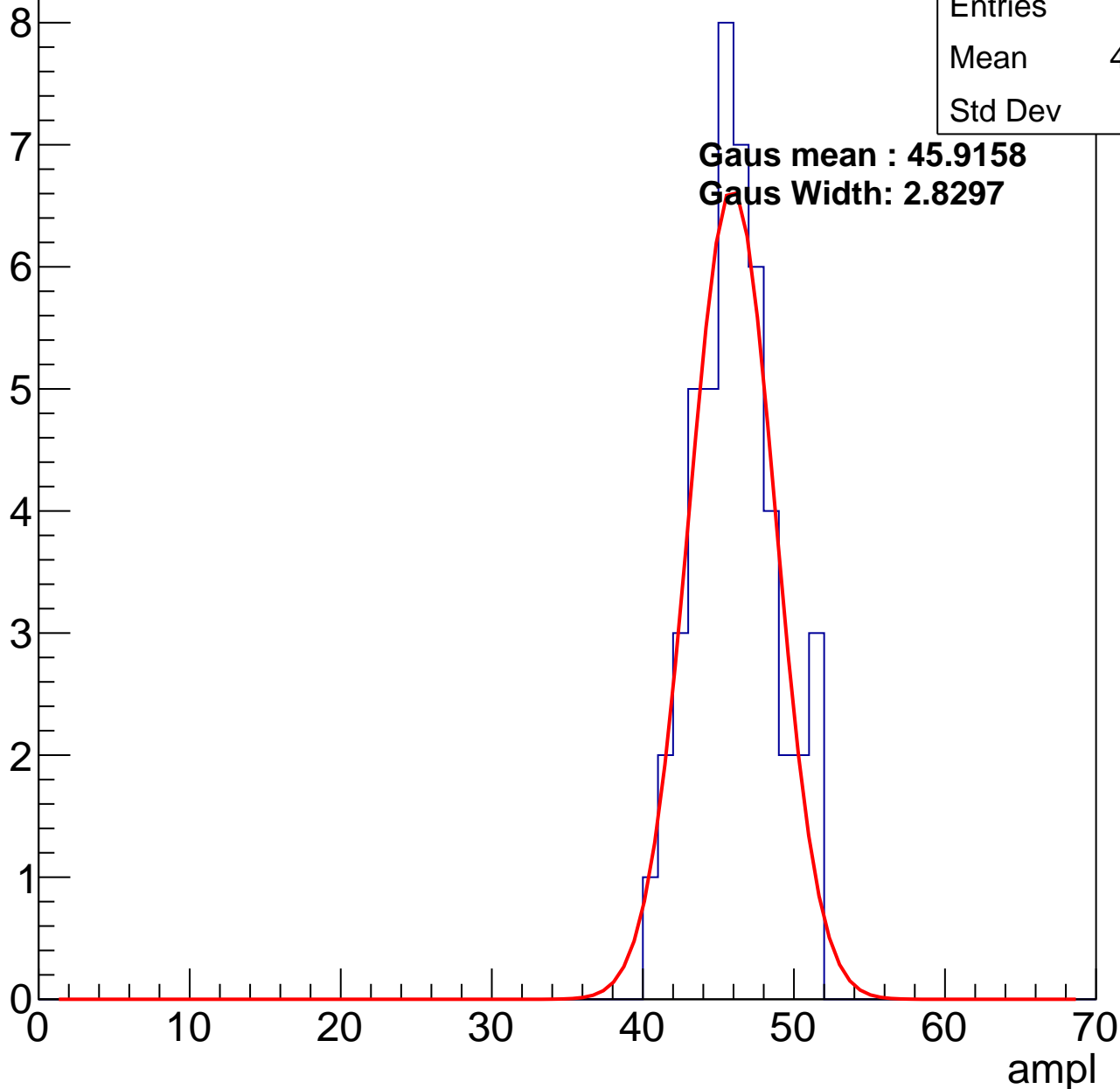
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	45.62
Std Dev	2.69

Gaus mean : 45.9158

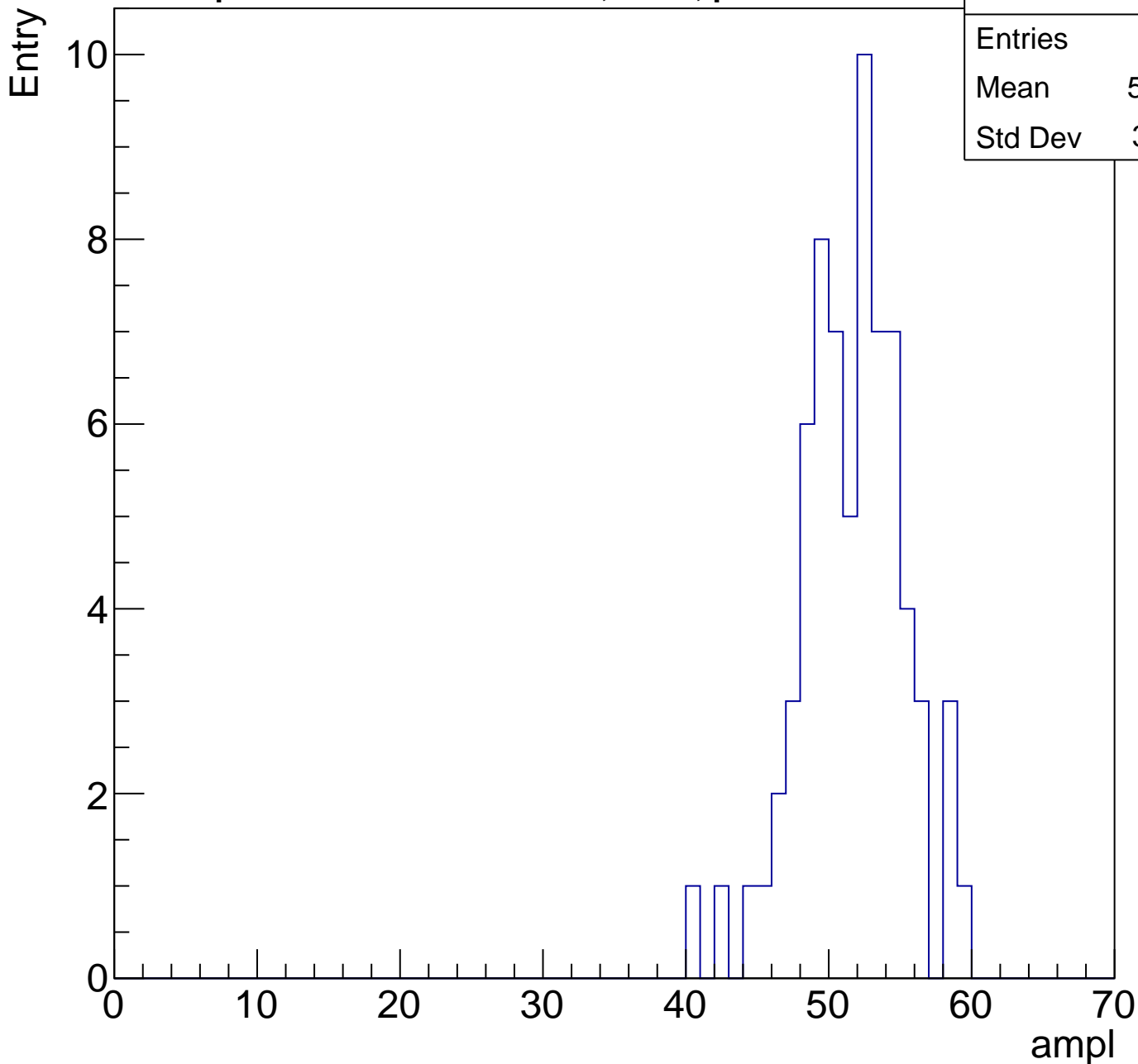
Gaus Width: 2.8297



B0L002S, U26-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

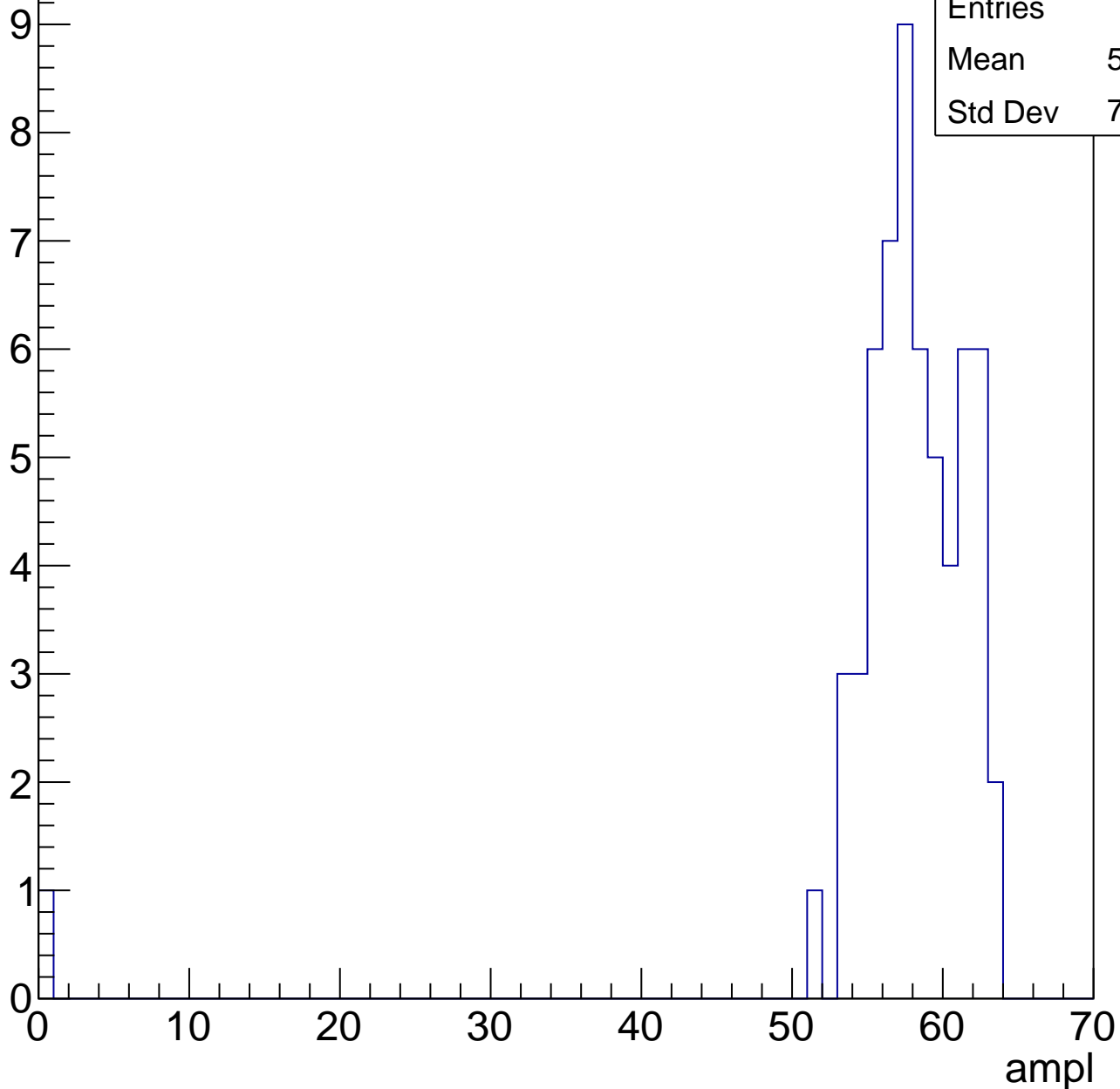
Entries	70
Mean	51.13
Std Dev	3.641



B0L002S, U26-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

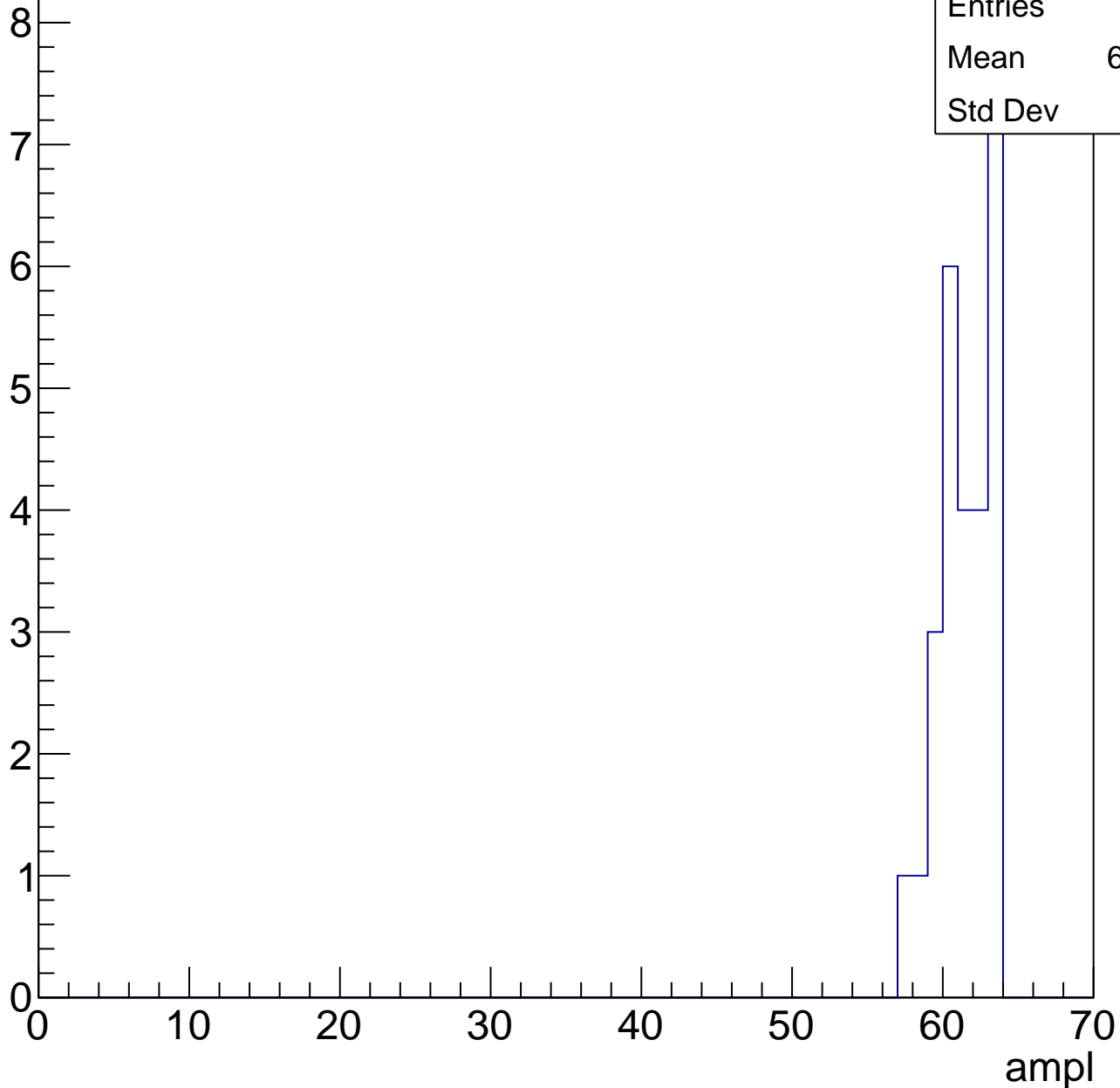


B0L002S, U26-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

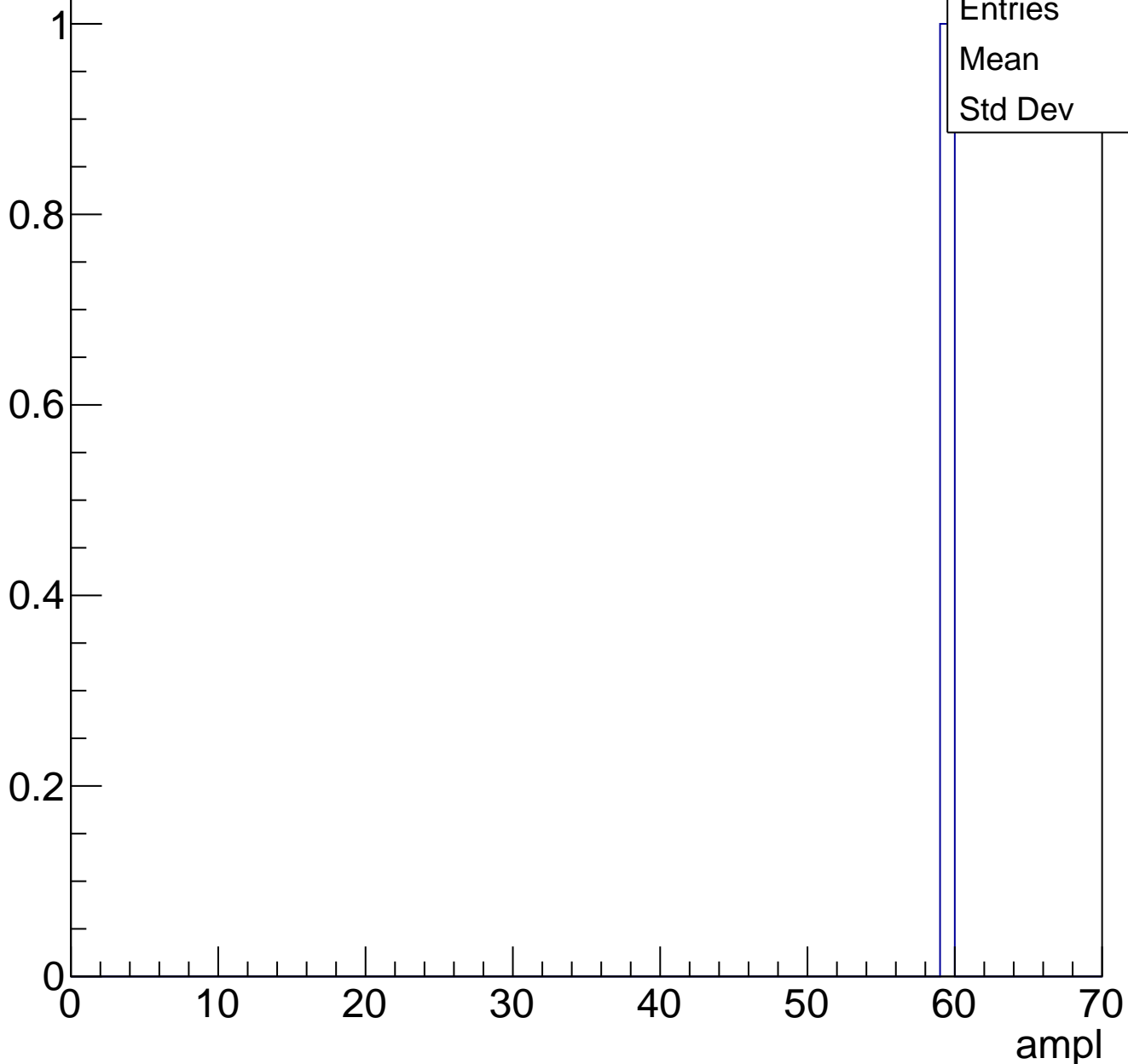
Entries	27
Mean	61.04
Std Dev	1.71



B0L002S, U26-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	59
Std Dev	0

B0L002S, U26-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch95, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	30.7
Std Dev	3.805

Gaus mean : 30.6547

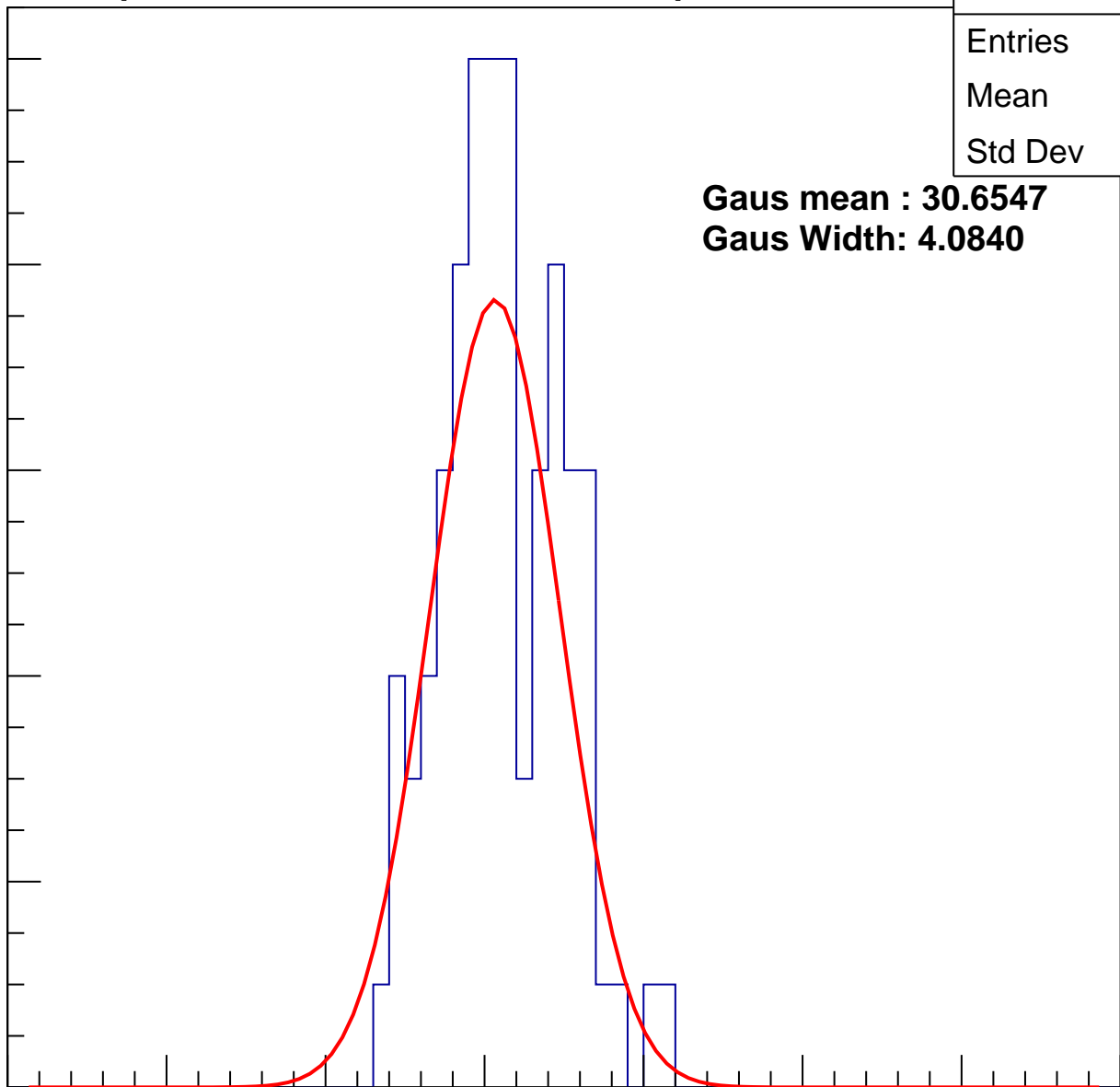
Gaus Width: 4.0840

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch95, adc1

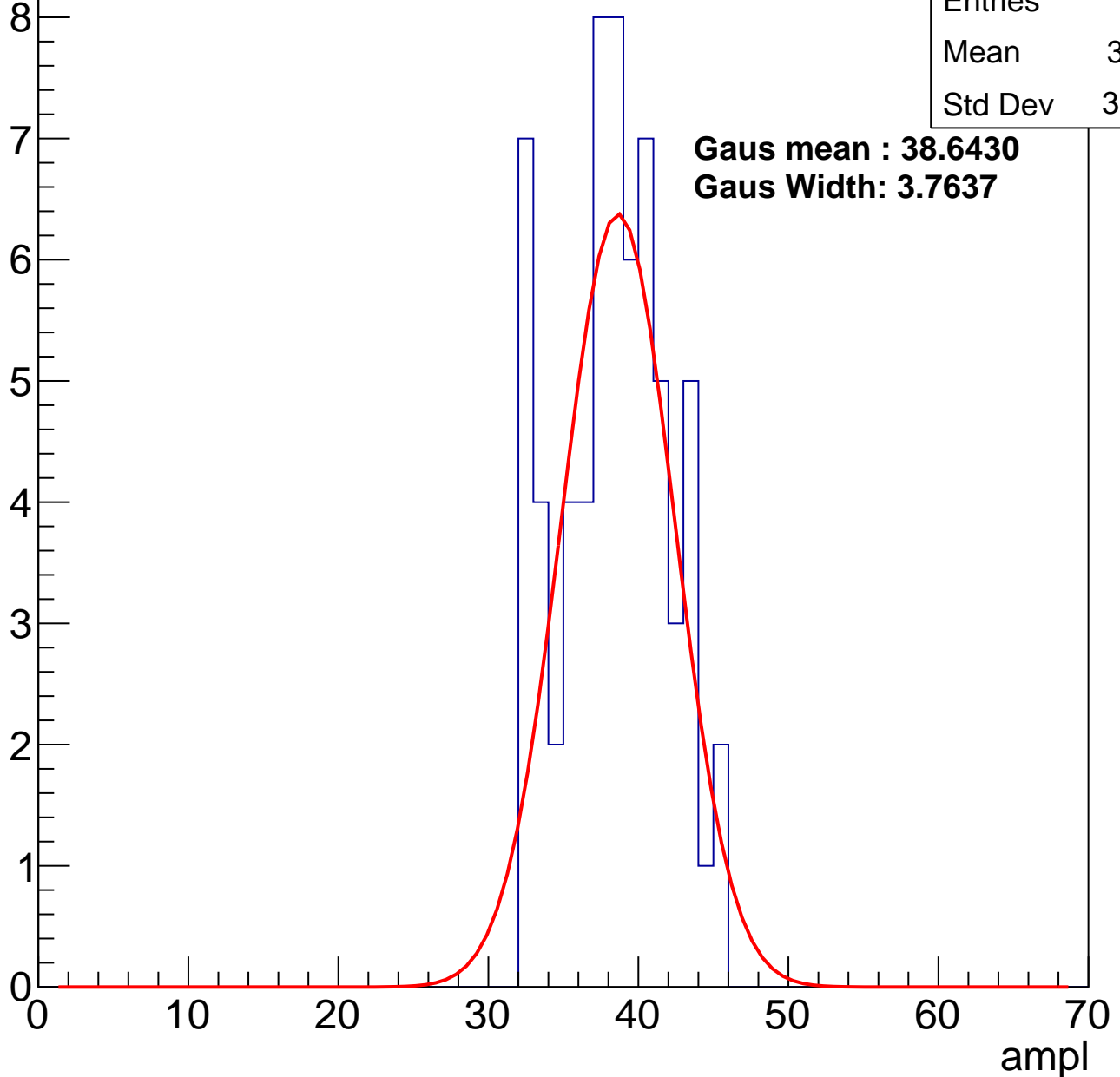
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.91
Std Dev	3.545

Gaus mean : 38.6430

Gaus Width: 3.7637

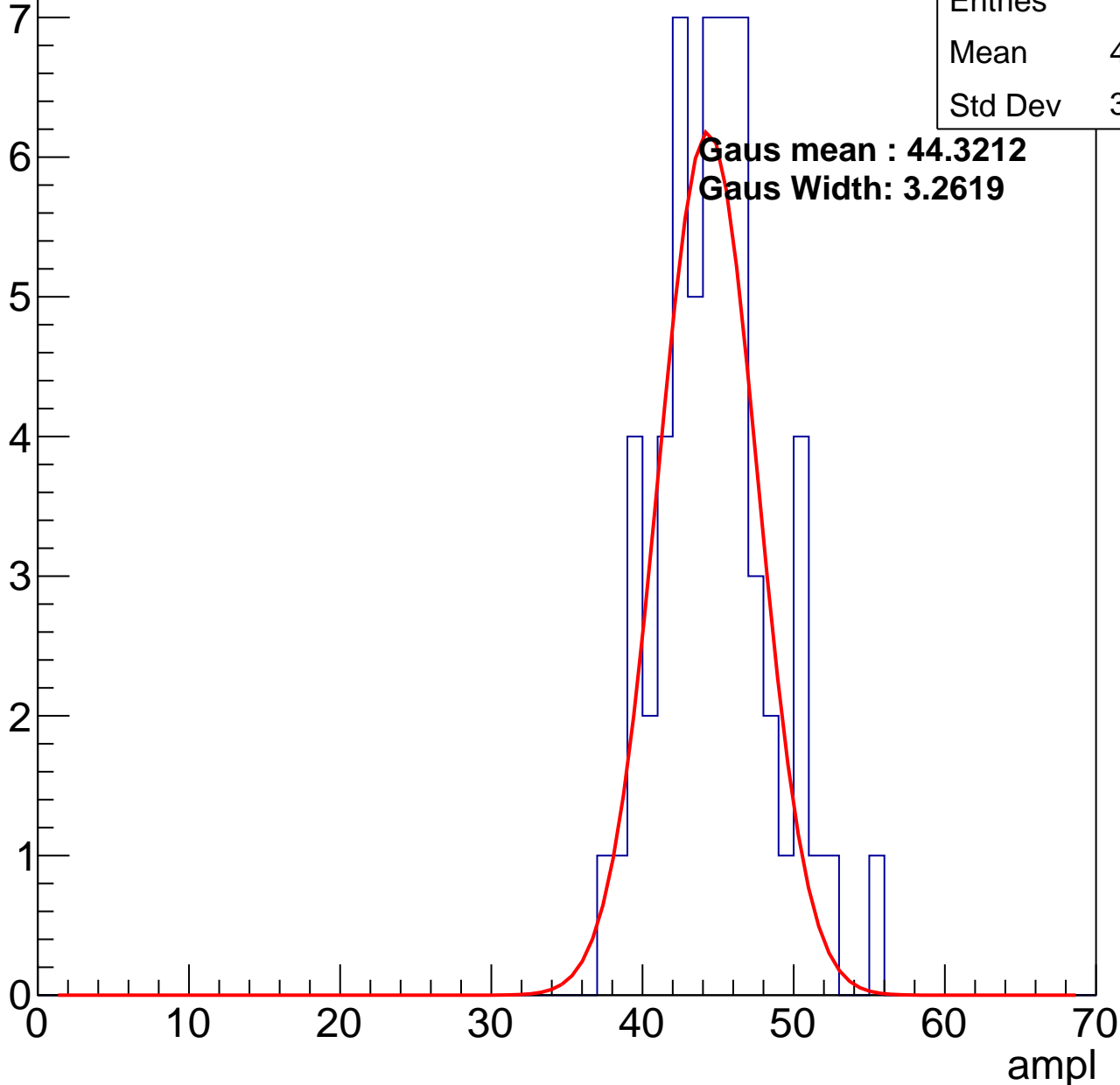


B0L002S, U26-ch95, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.36
Std Dev	3.656

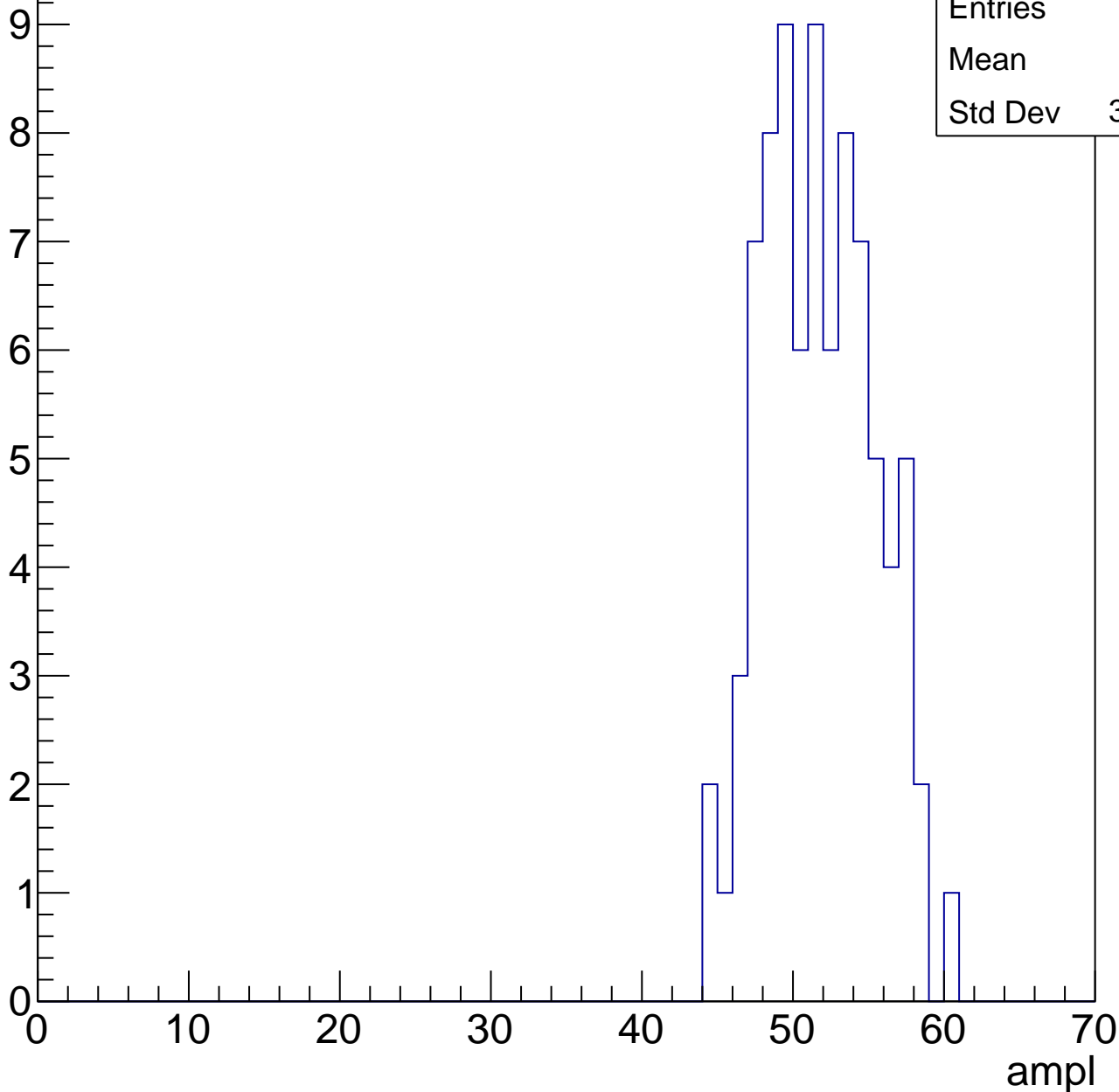


B0L002S, U26-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	51.3
Std Dev	3.586

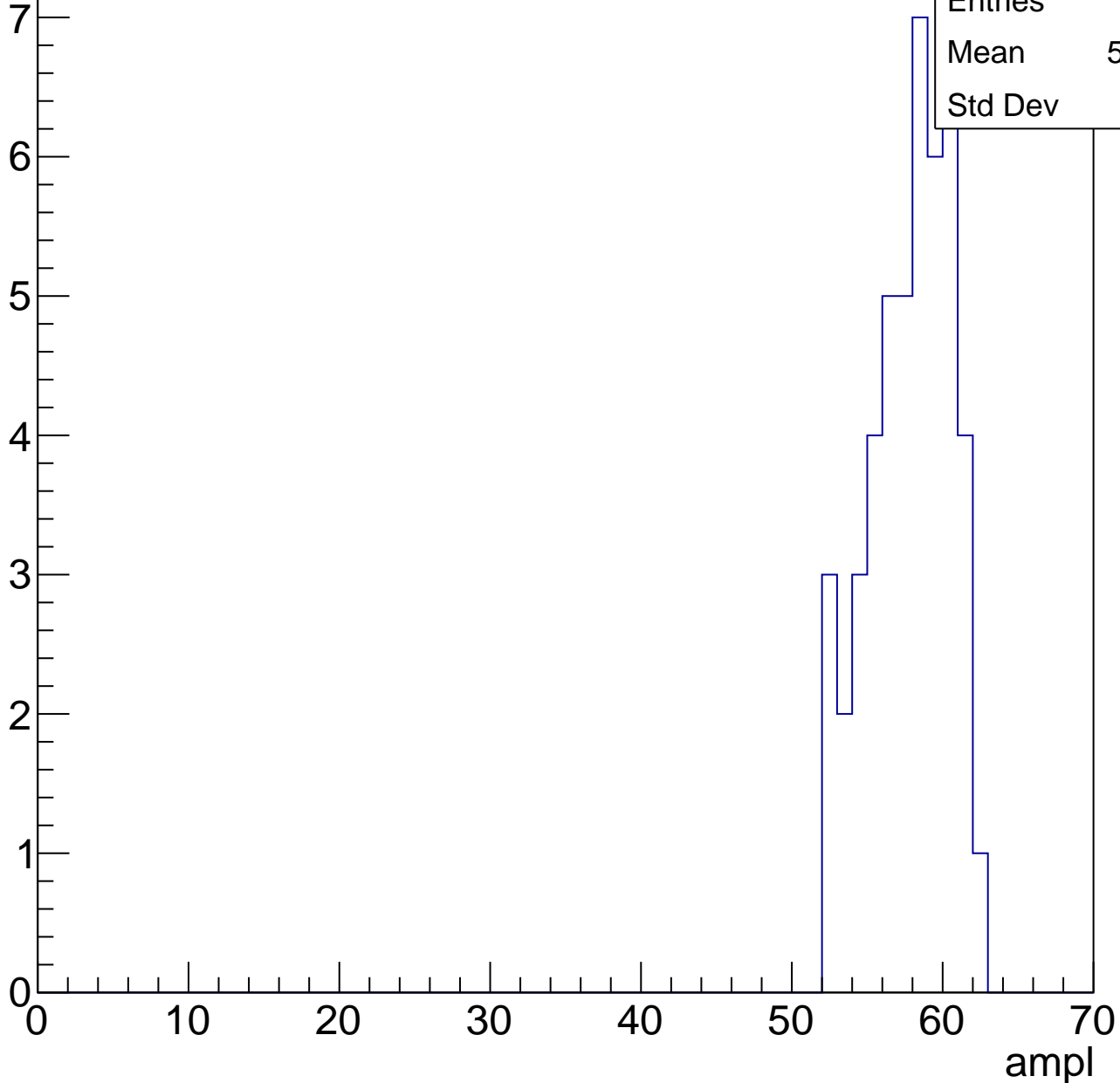


B0L002S, U26-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	57.34
Std Dev	2.66

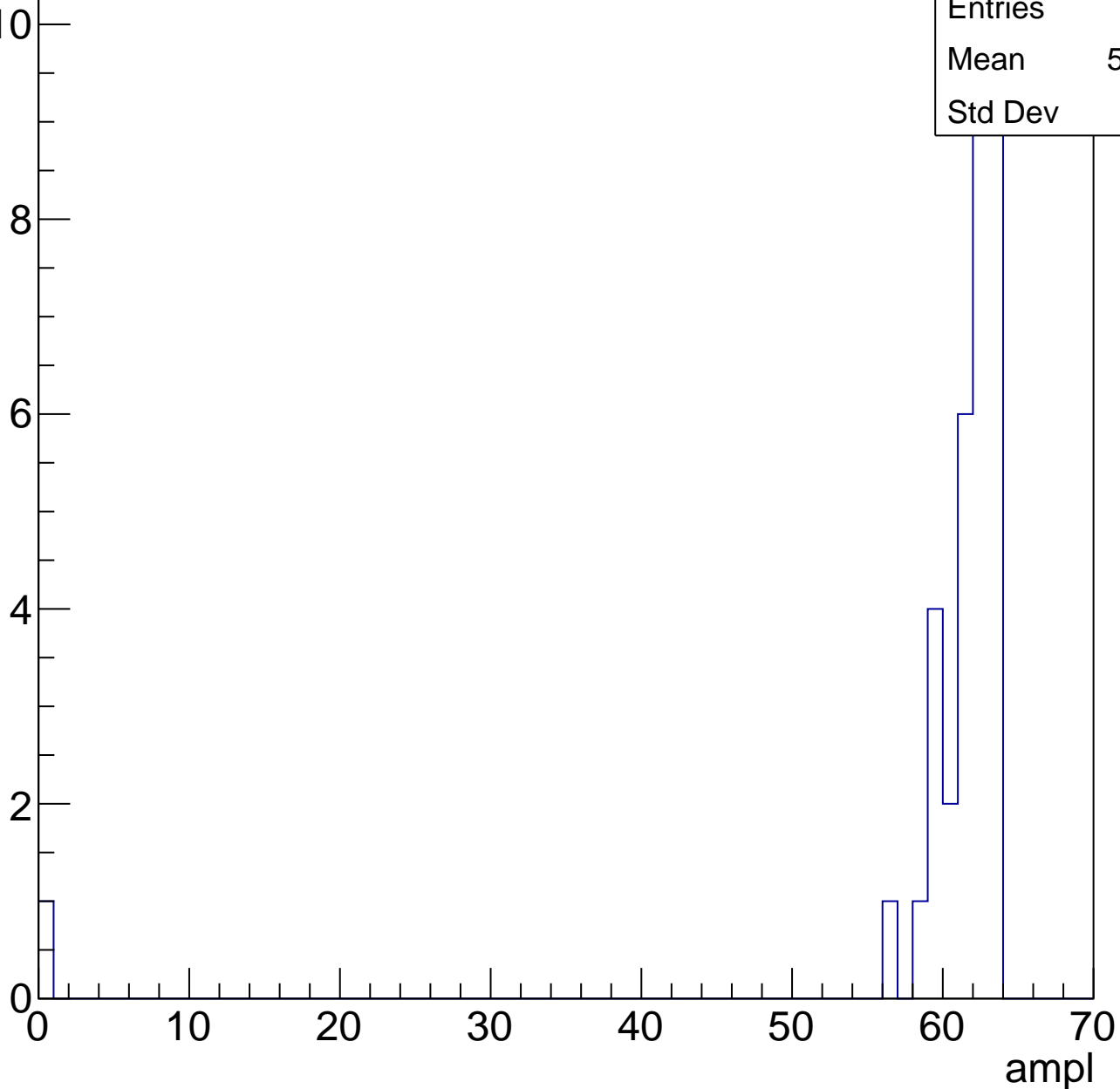


B0L002S, U26-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.53
Std Dev	10.5



B0L002S, U26-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch96, adc0

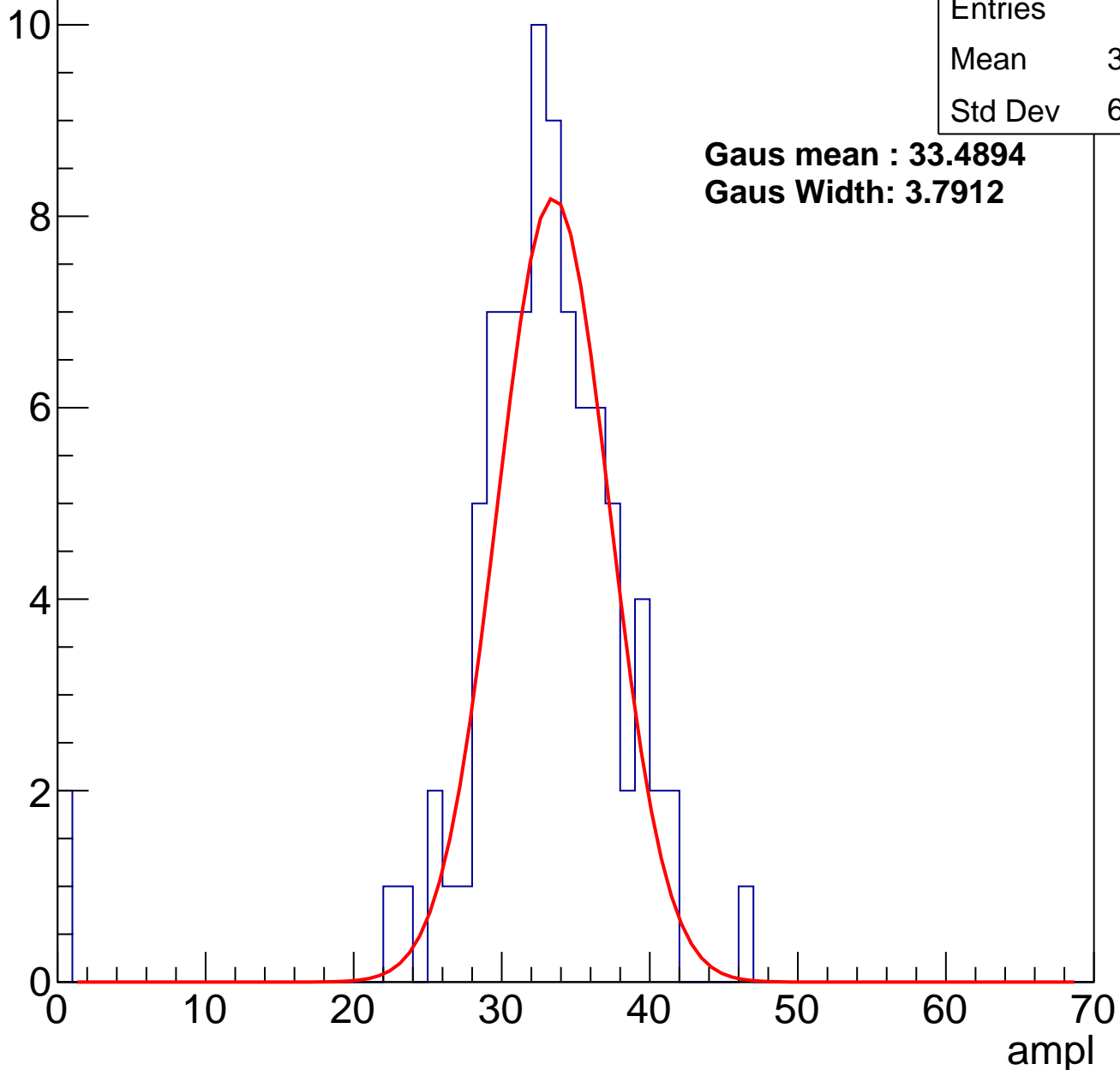
calib_packv5_042523_0143.root, FC#8, port C1

Entries	88
Mean	32.09
Std Dev	6.424

Gaus mean : 33.4894

Gaus Width: 3.7912

Entry



B0L002S, U26-ch96, adc1

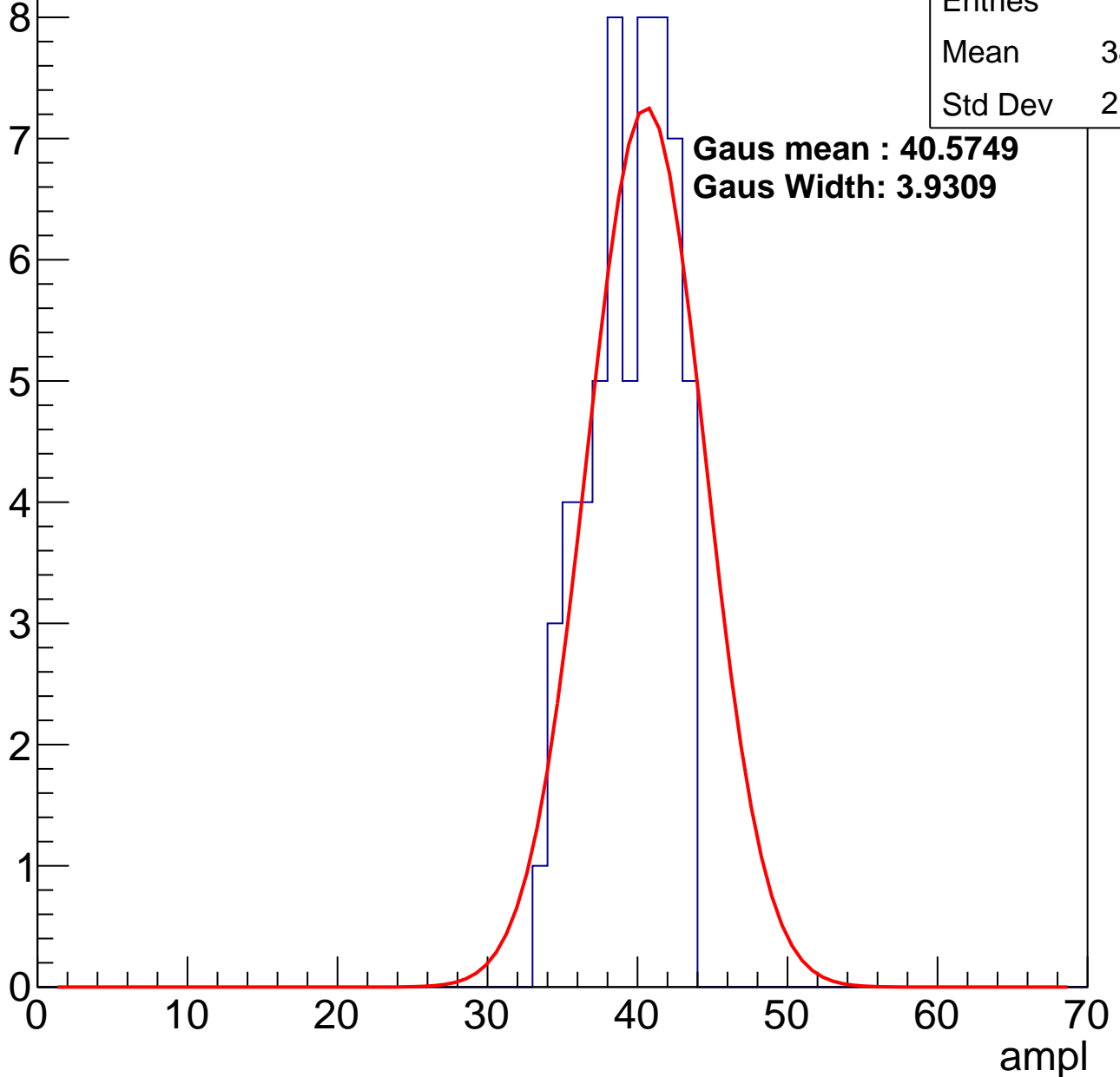
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	38.97
Std Dev	2.697

Gaus mean : 40.5749

Gaus Width: 3.9309



B0L002S, U26-ch96, adc2

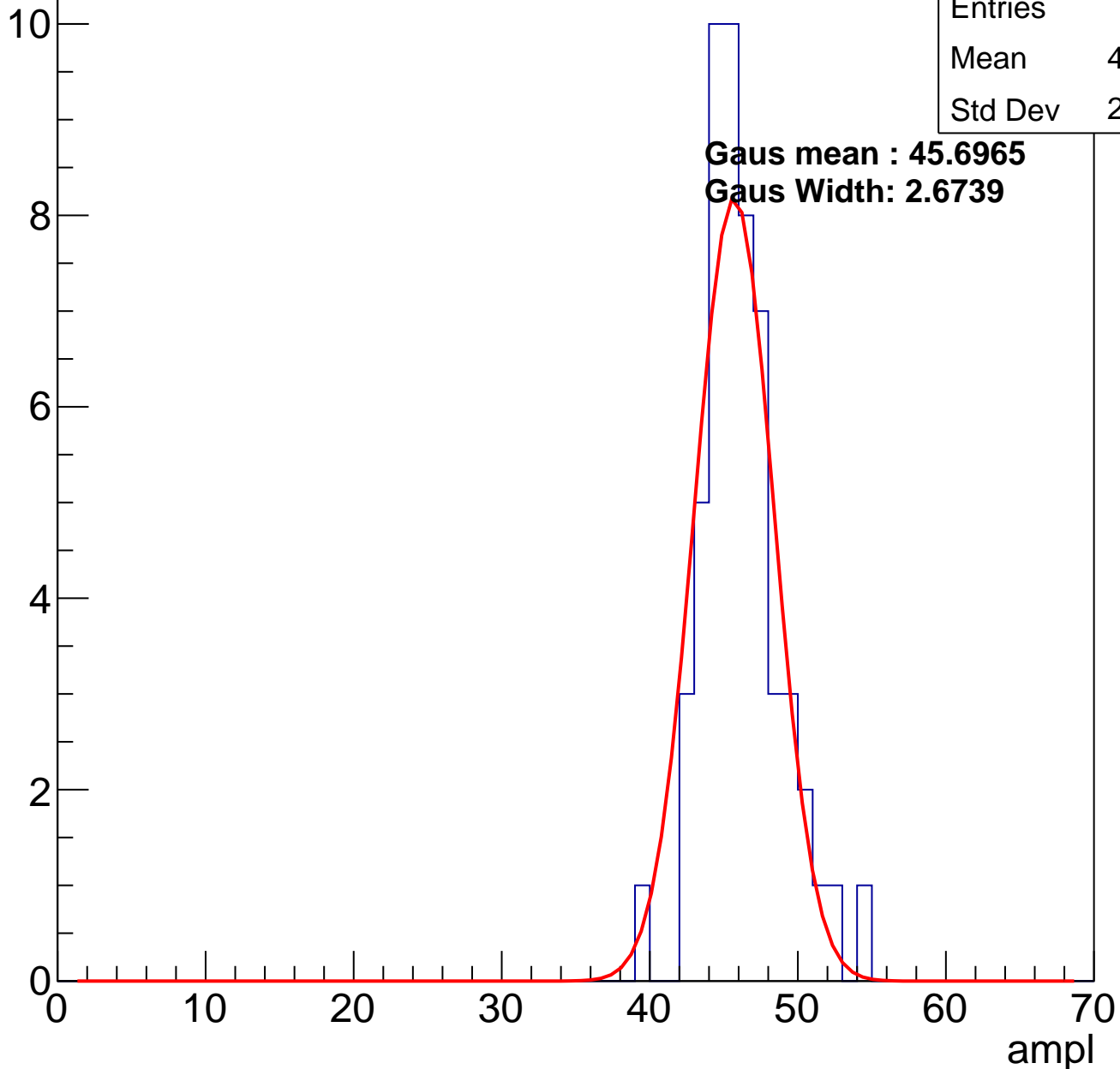
calib_packv5_042523_0143.root, FC#8, port C1

Entries	55
Mean	45.73
Std Dev	2.666

Gaus mean : 45.6965

Gaus Width: 2.6739

Entry

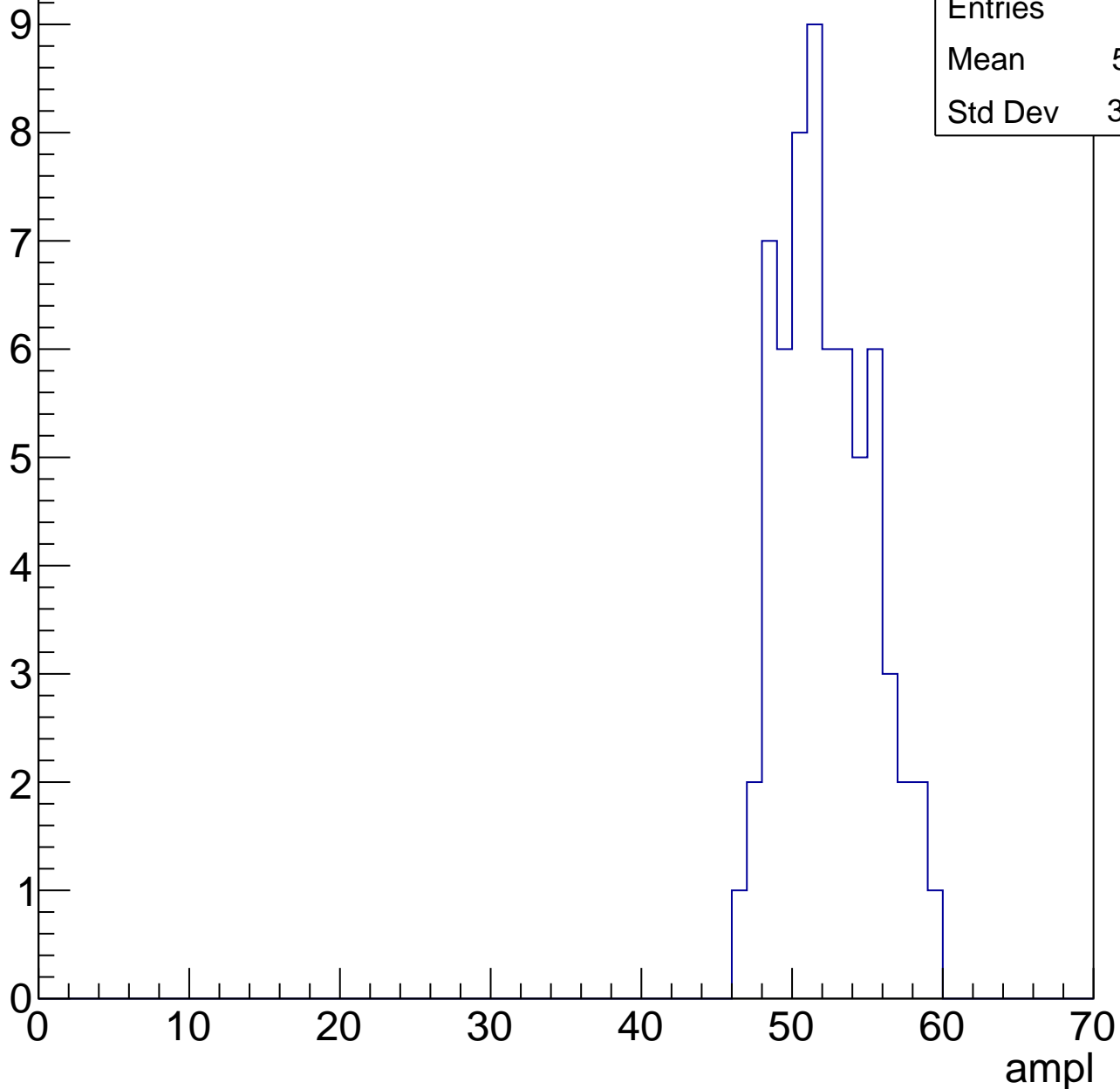


B0L002S, U26-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.81
Std Dev	3.036

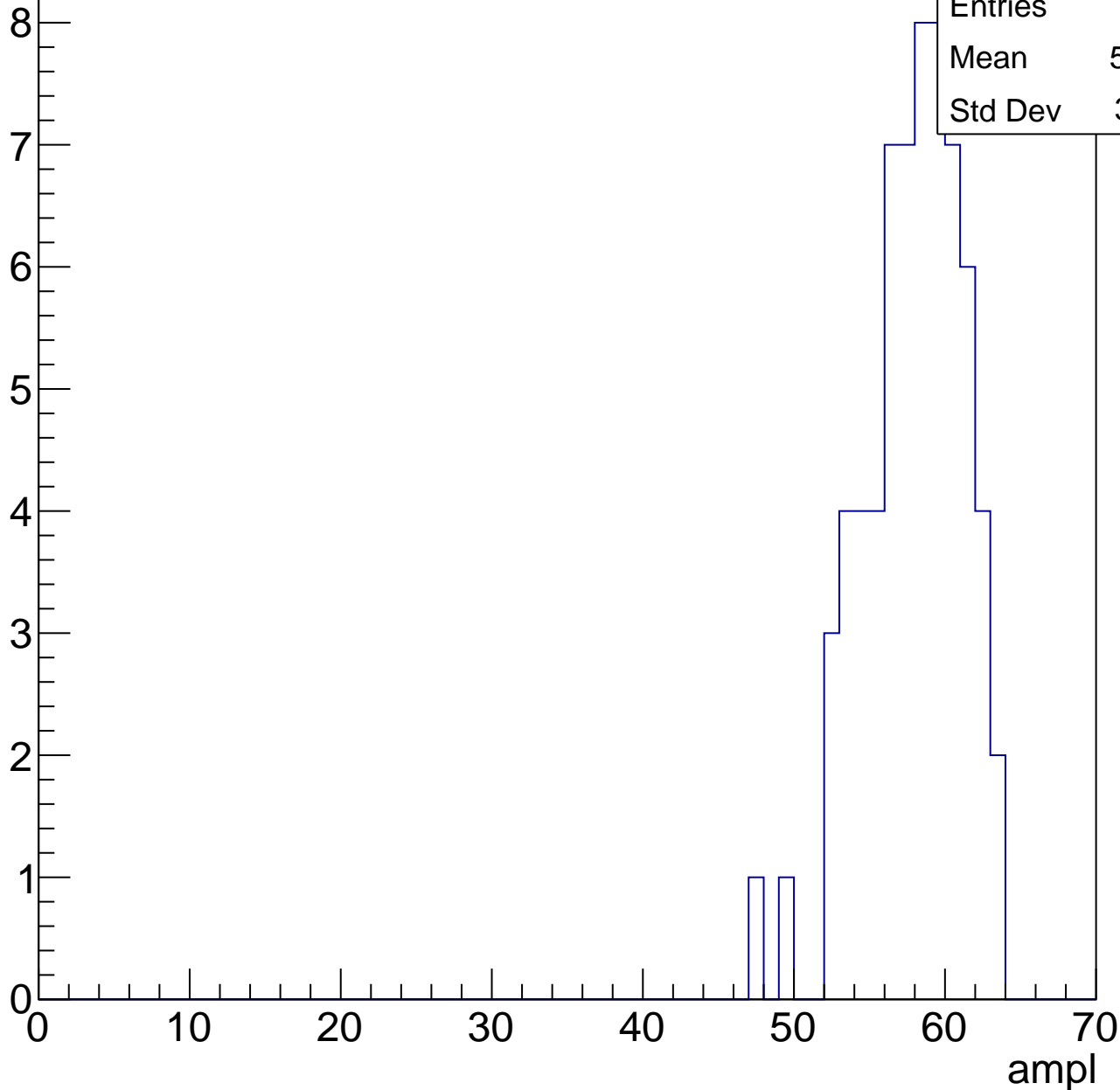


B0L002S, U26-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	57.38
Std Dev	3.311



B0L002S, U26-ch96, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 23

Mean 58.83

Std Dev 12.63

ampl

0

10

20

30

40

50

60

70

B0L002S, U26-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch97, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	31.82
Std Dev	3.064

Gaus mean : 32.3830

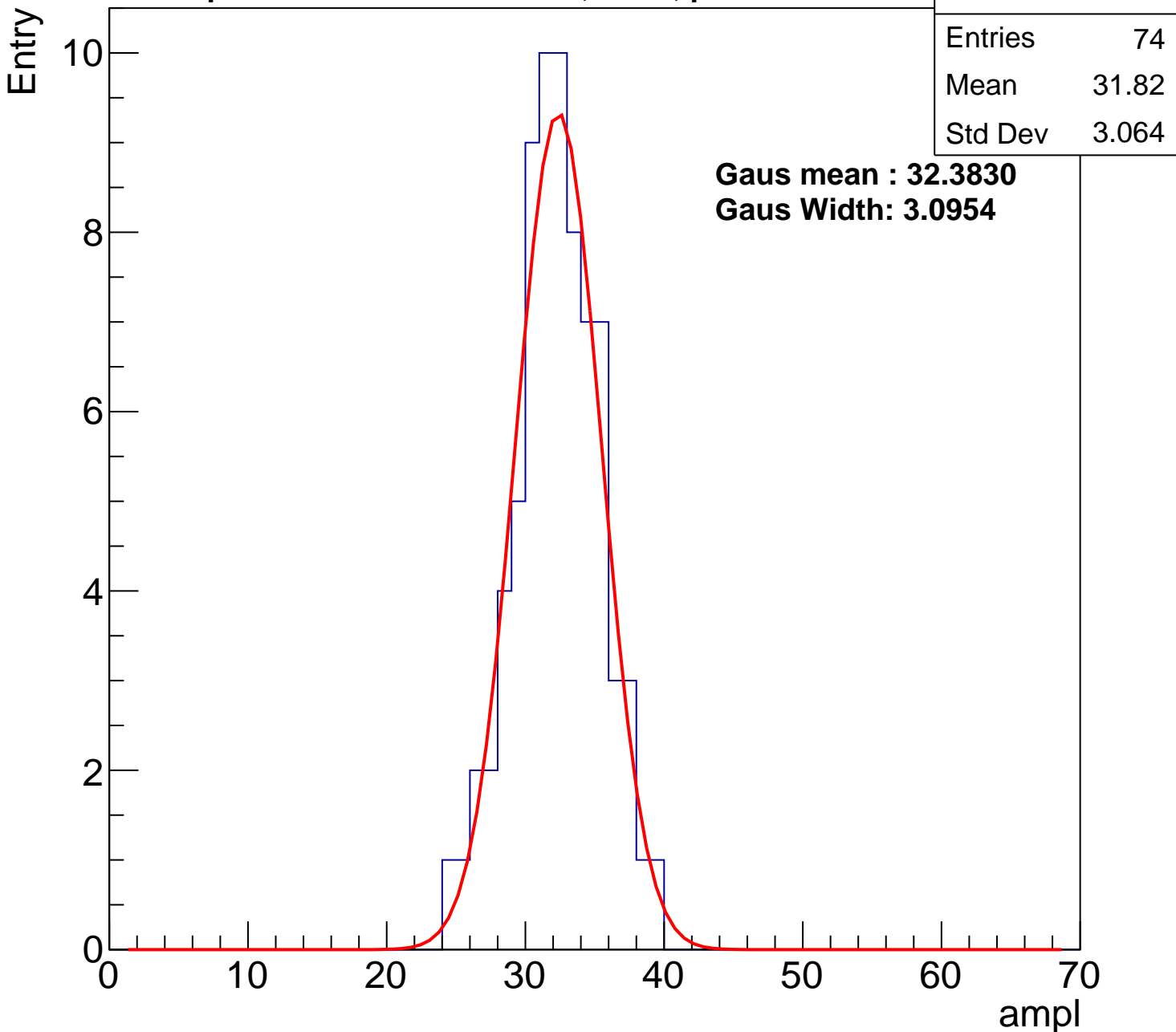
Gaus Width: 3.0954

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



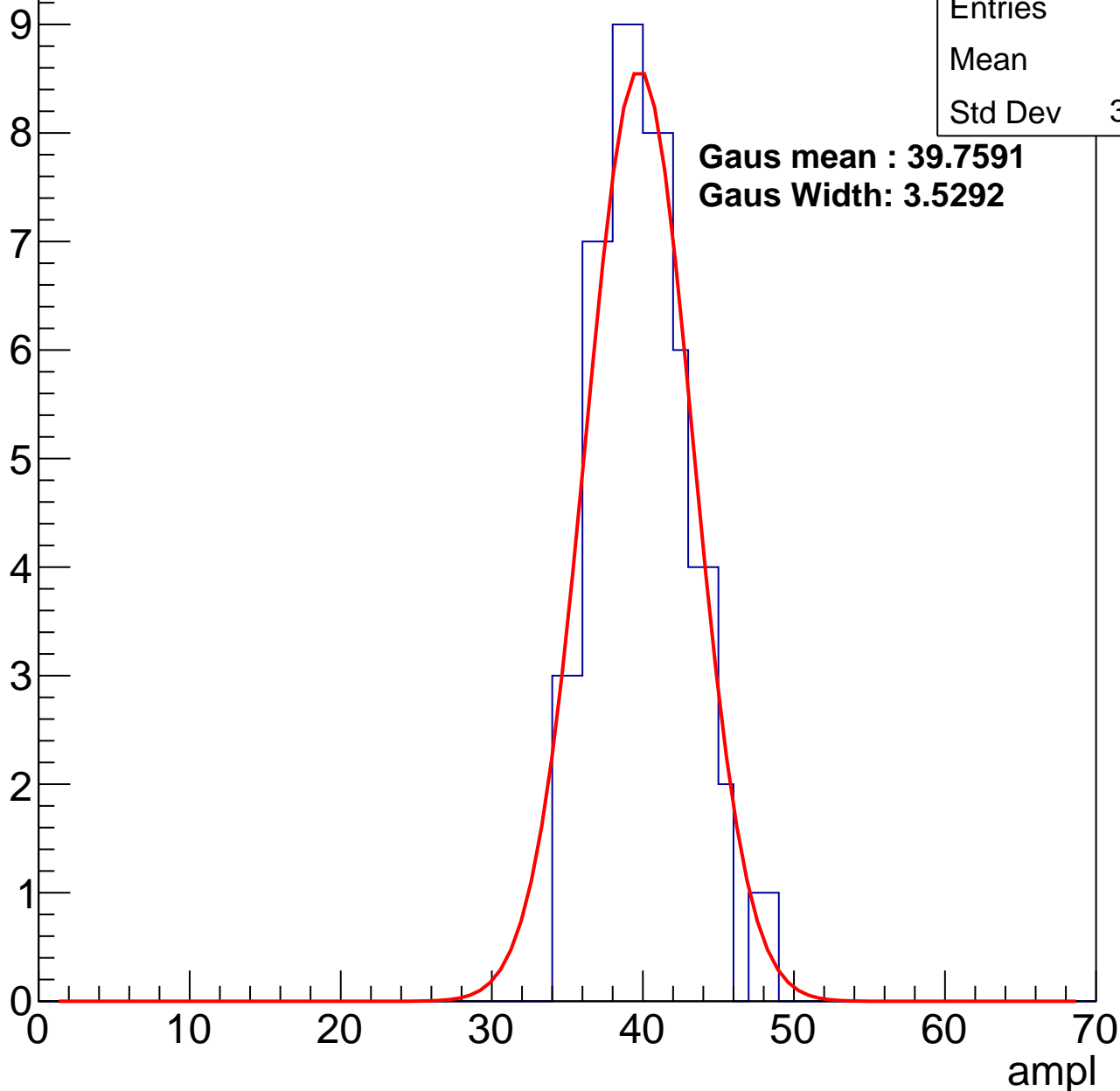
B0L002S, U26-ch97, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	39.5
Std Dev	3.073

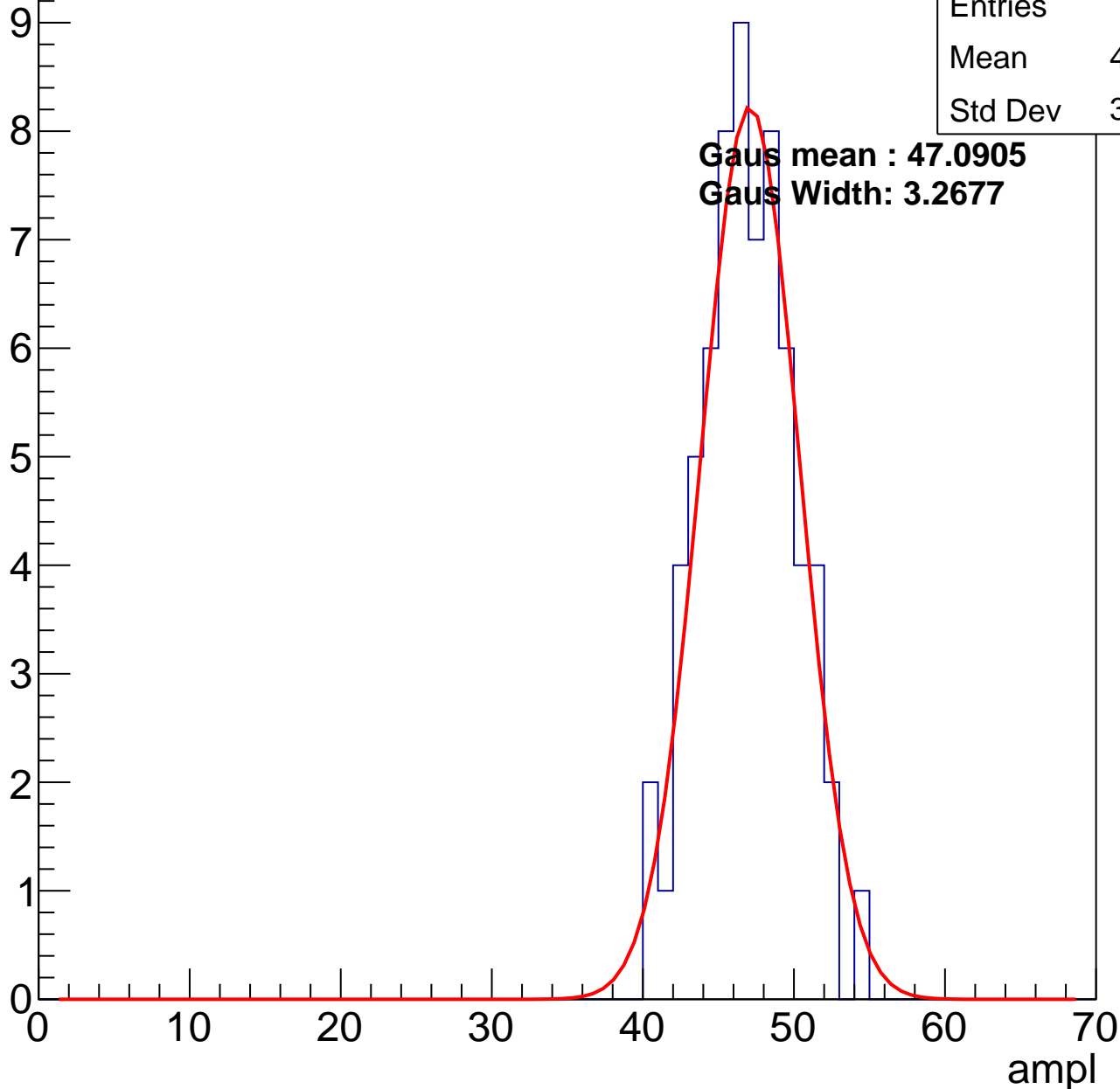
Gaus mean : 39.7591
Gaus Width: 3.5292



B0L002S, U26-ch97, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

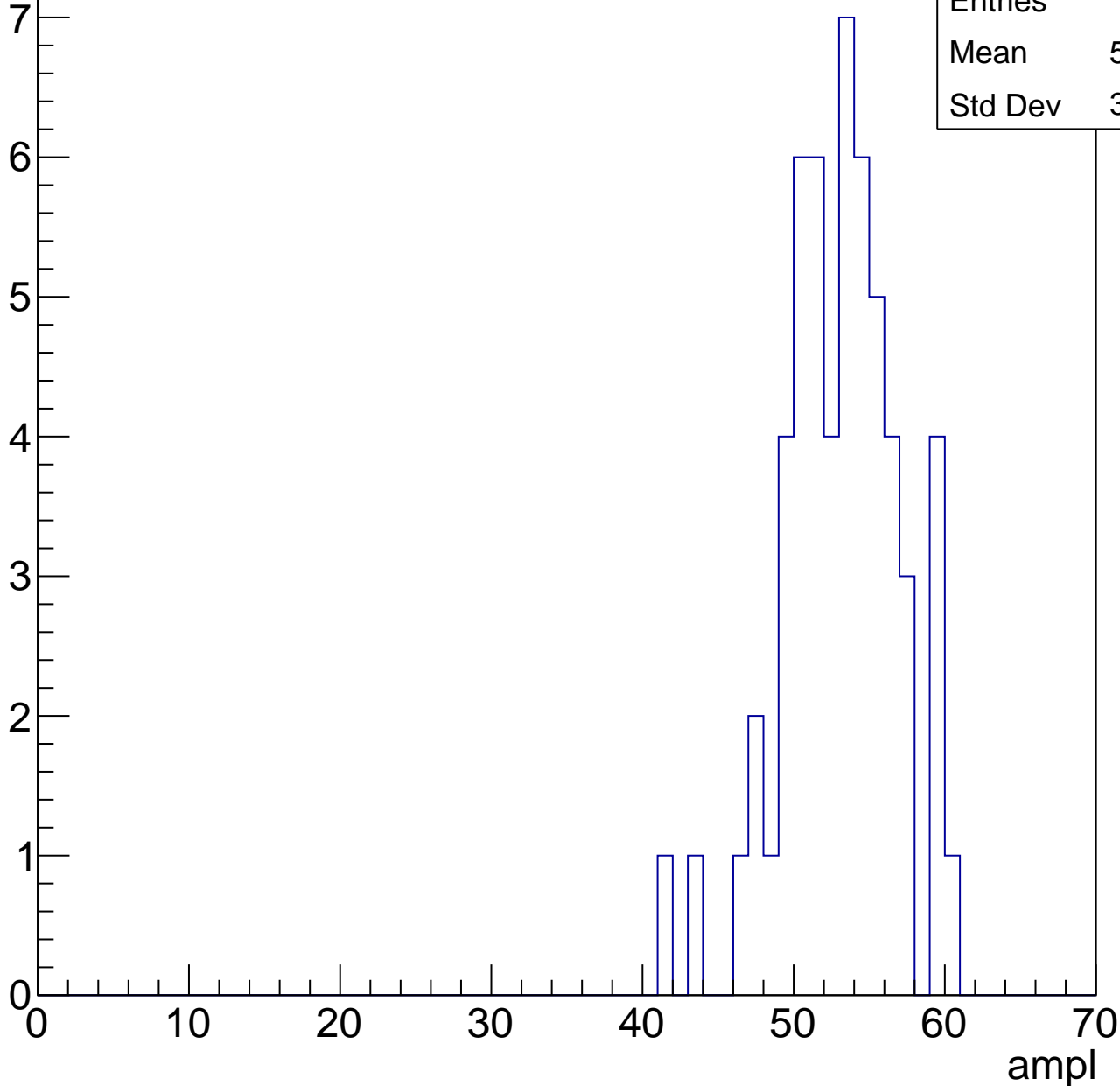


B0L002S, U26-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	52.55
Std Dev	3.845

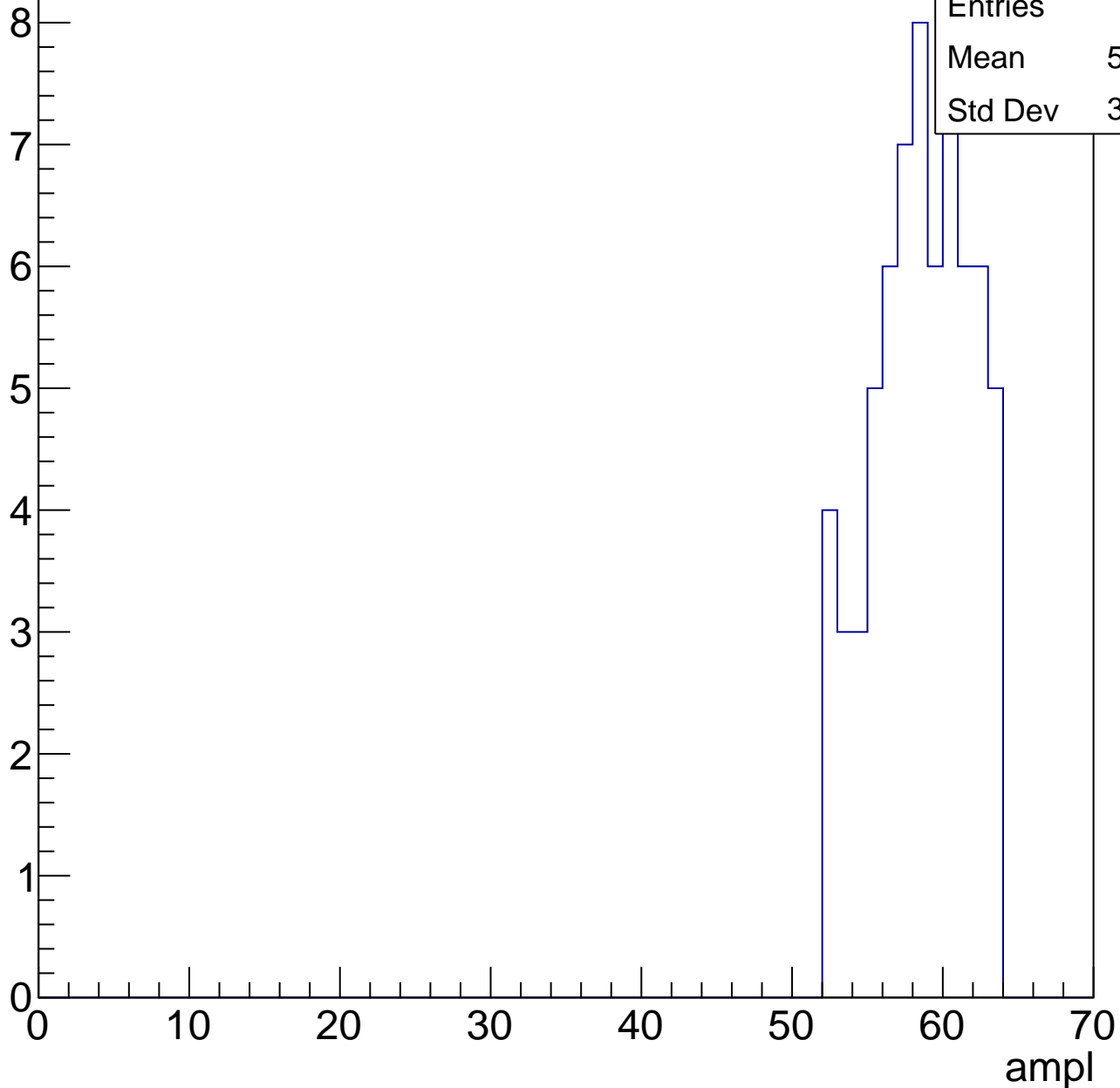


B0L002S, U26-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

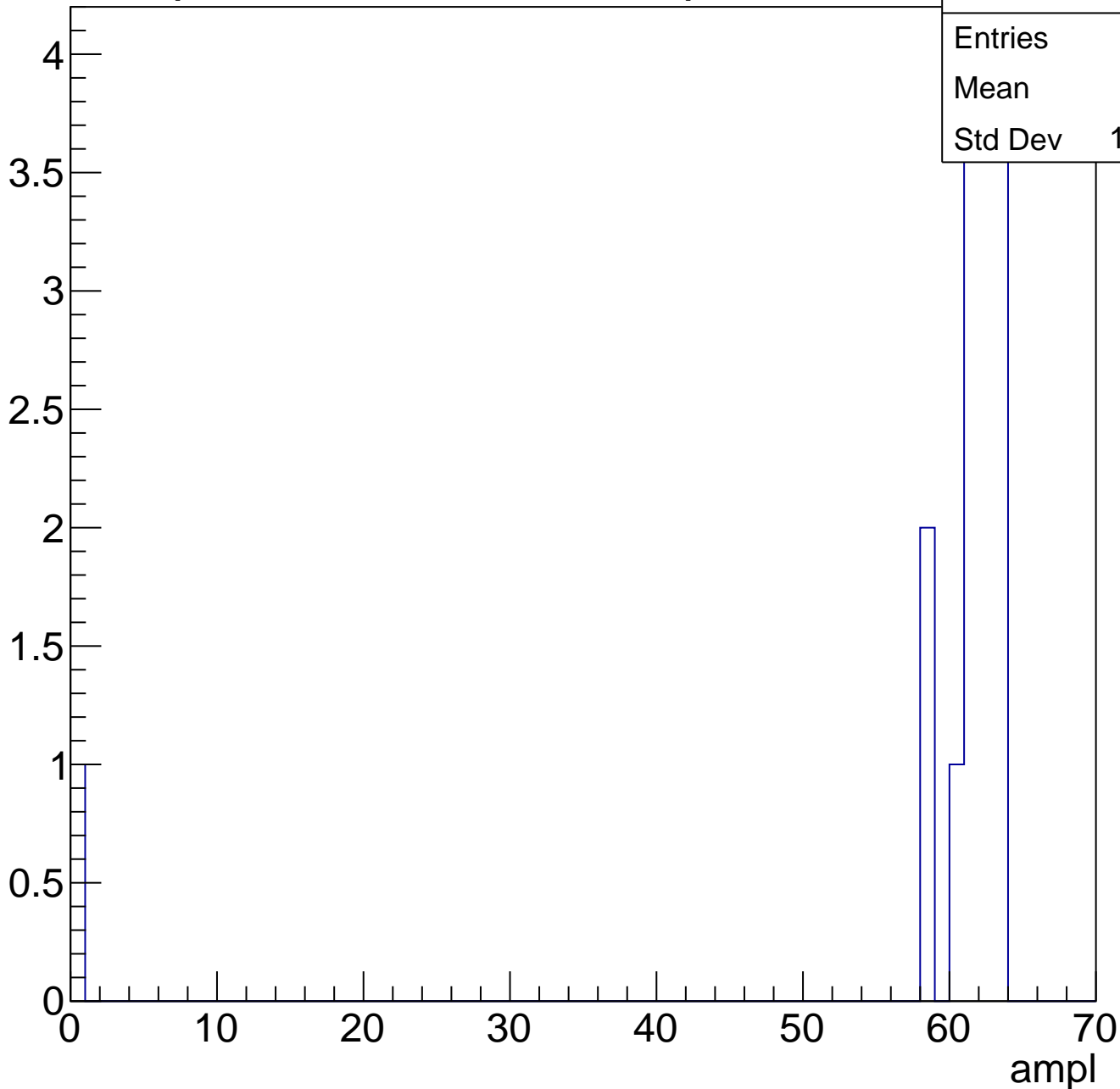
Entries	67
Mean	58.06
Std Dev	3.128



B0L002S, U26-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	16
Mean	57.5
Std Dev	14.92

B0L002S, U26-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B0L002S, U26-ch98, adc0

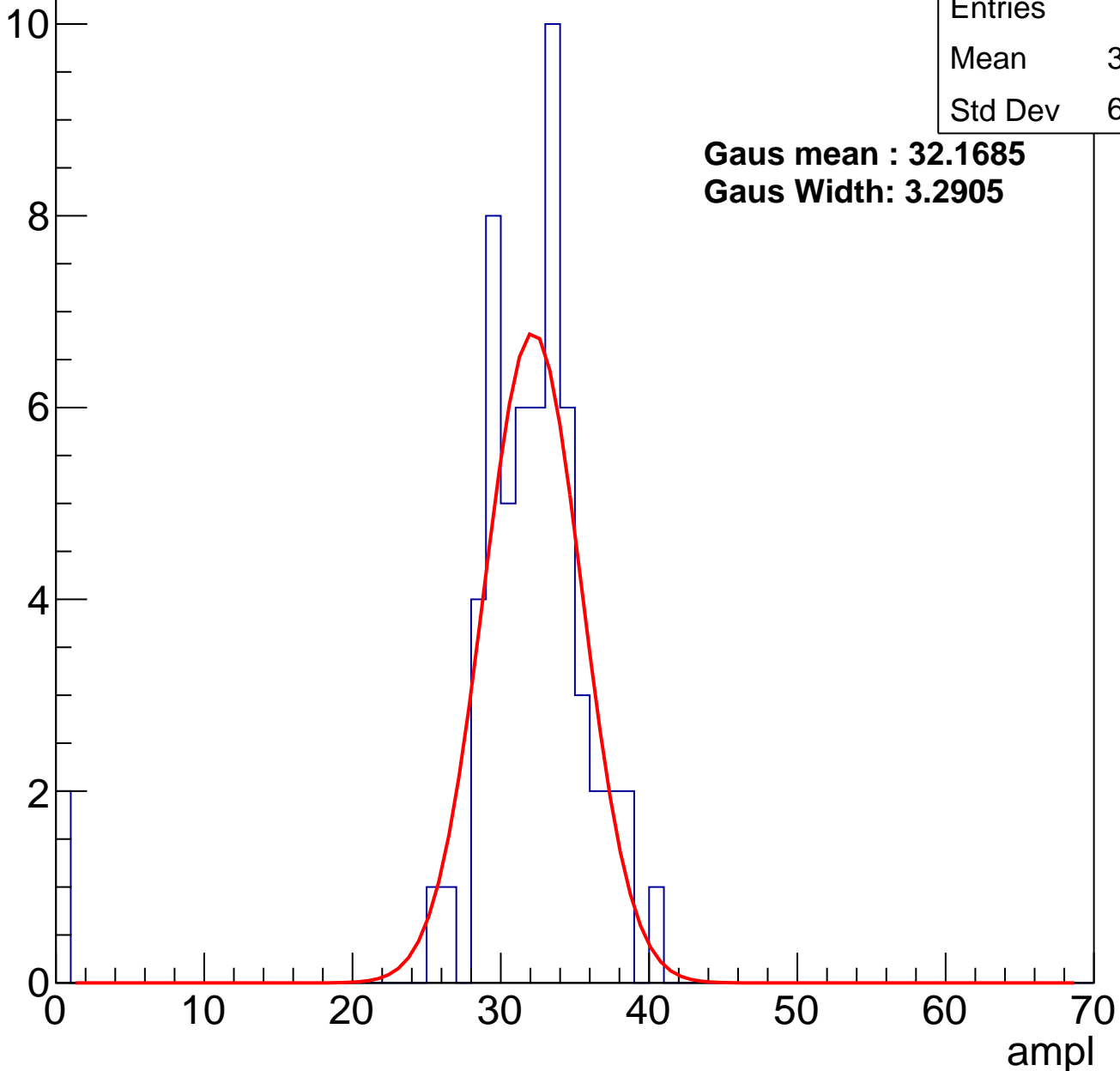
calib_packv5_042523_0143.root, FC#8, port C1

Entries	59
Mean	30.92
Std Dev	6.515

Gaus mean : 32.1685

Gaus Width: 3.2905

Entry



B0L002S, U26-ch98, adc1

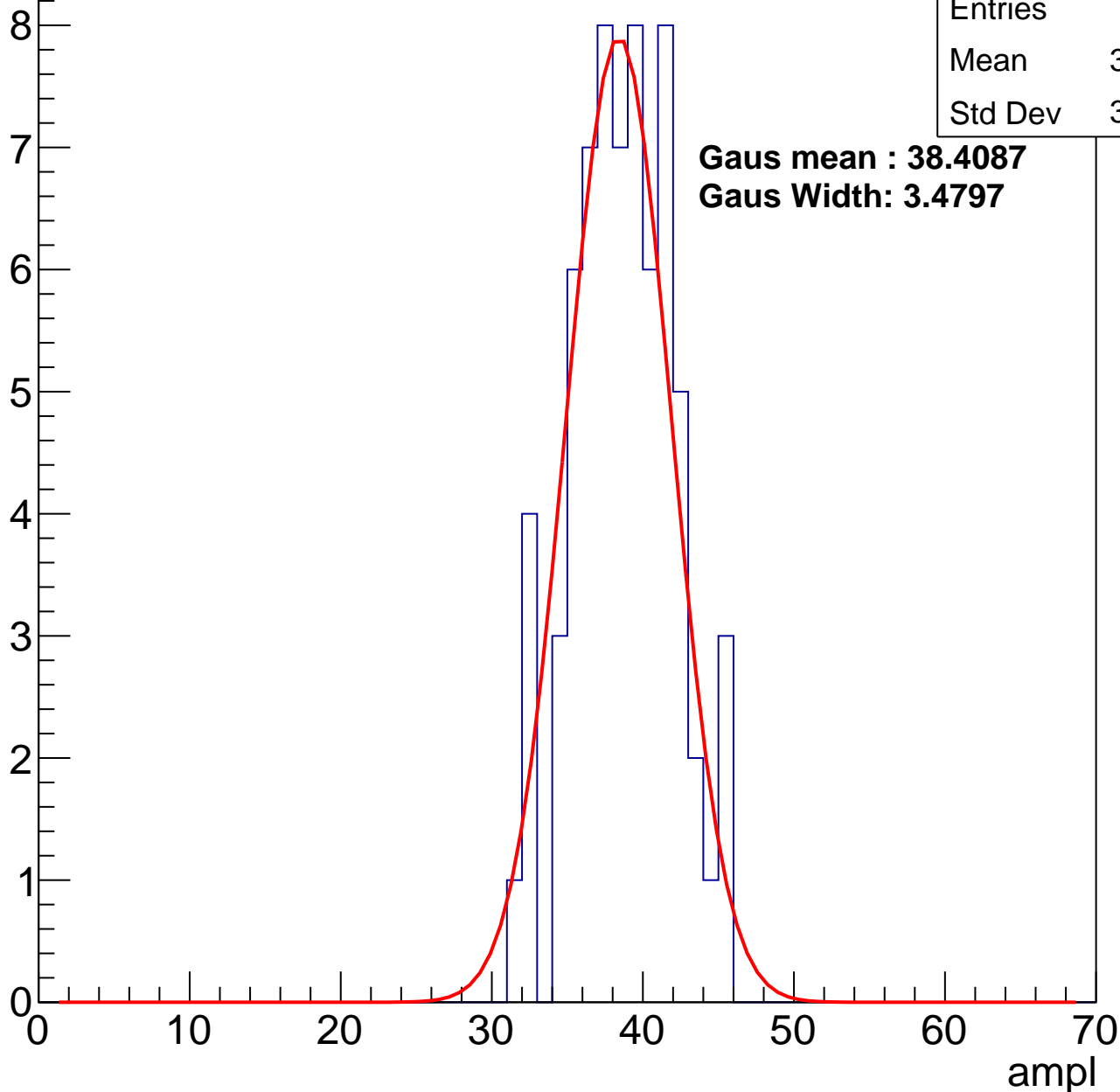
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	38.26
Std Dev	3.282

Gaus mean : 38.4087

Gaus Width: 3.4797



B0L002S, U26-ch98, adc2

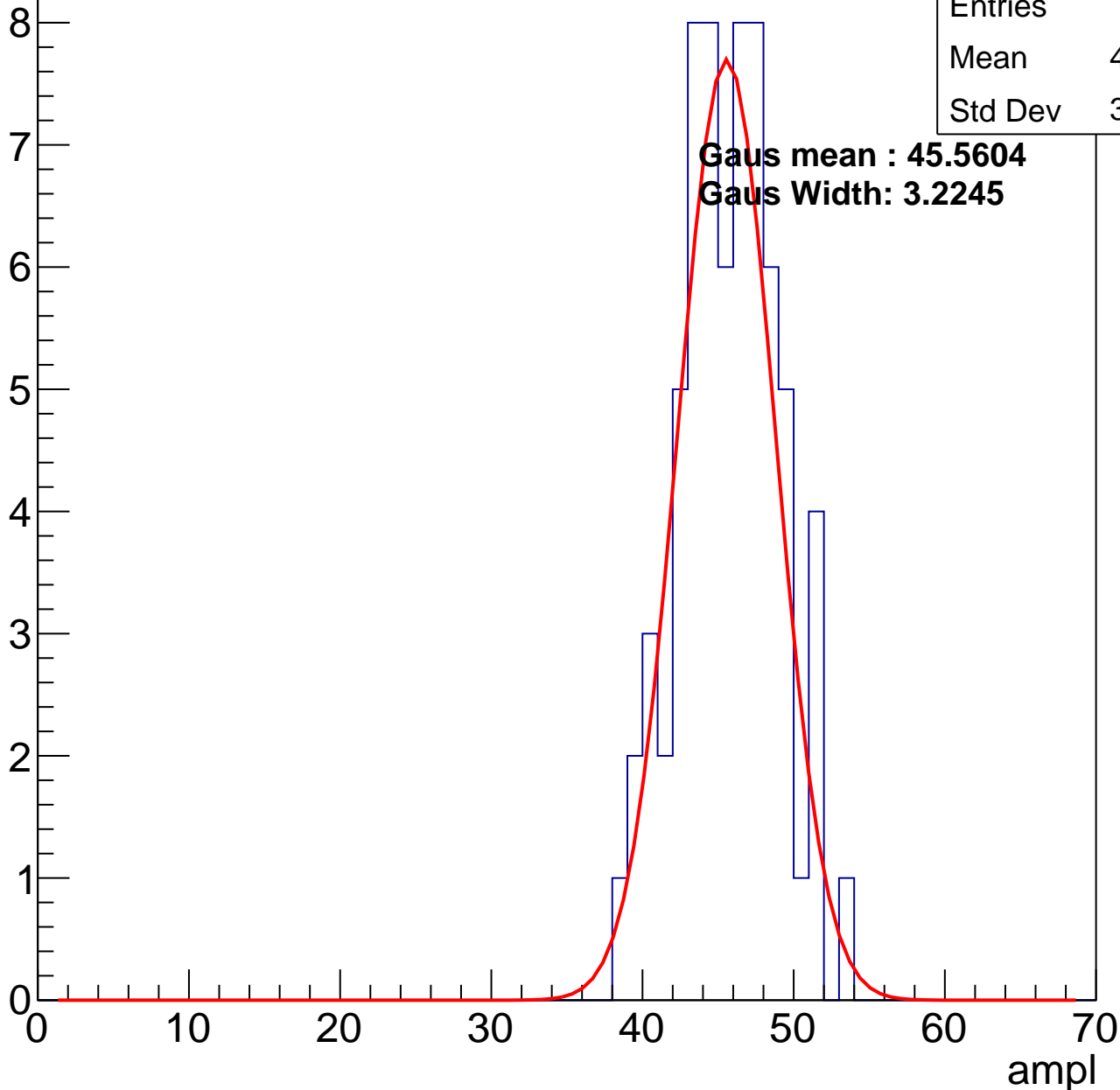
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.26
Std Dev	3.243

Gaus mean : 45.5604

Gaus Width: 3.2245

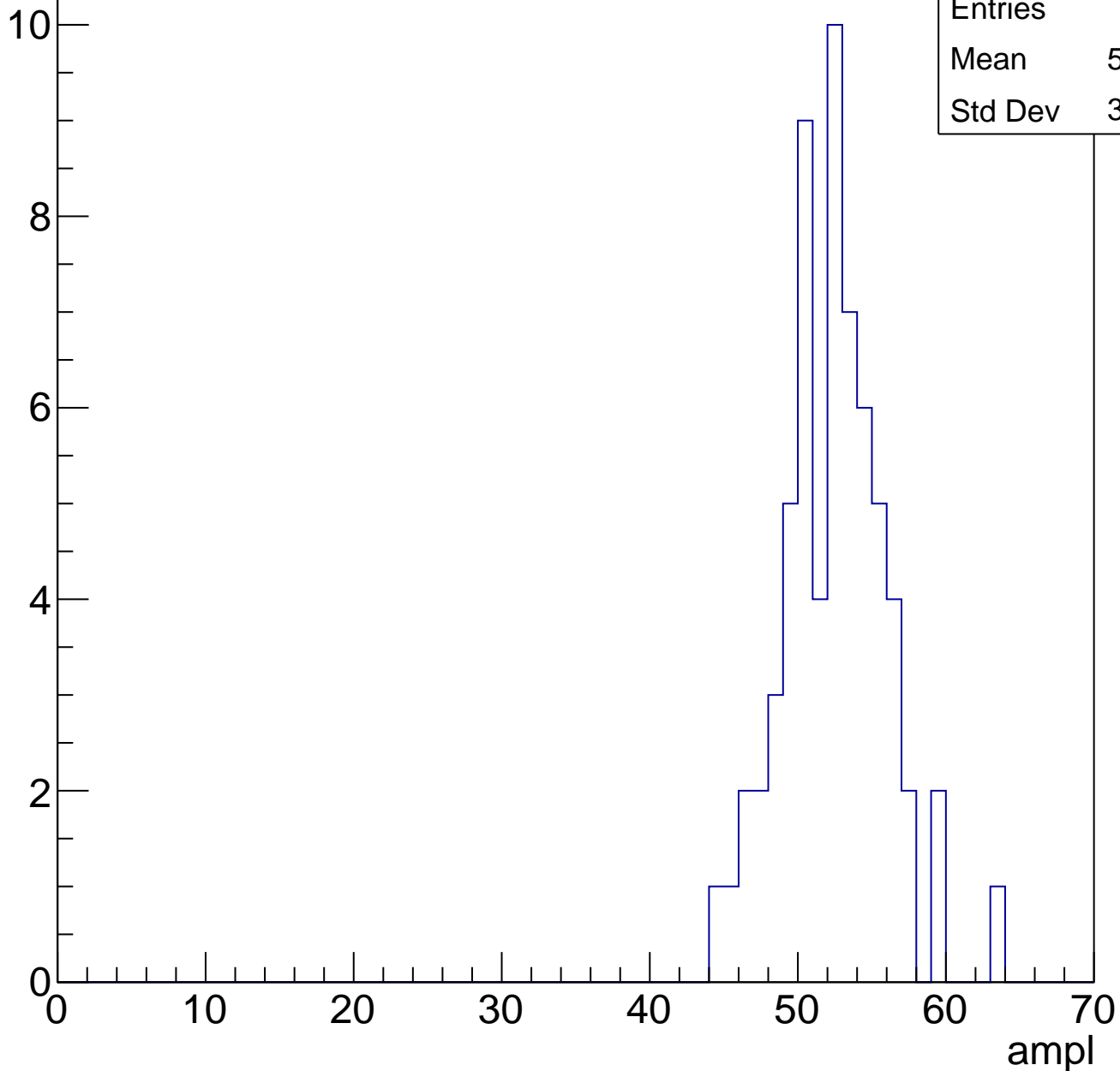


B0L002S, U26-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.98
Std Dev	3.484

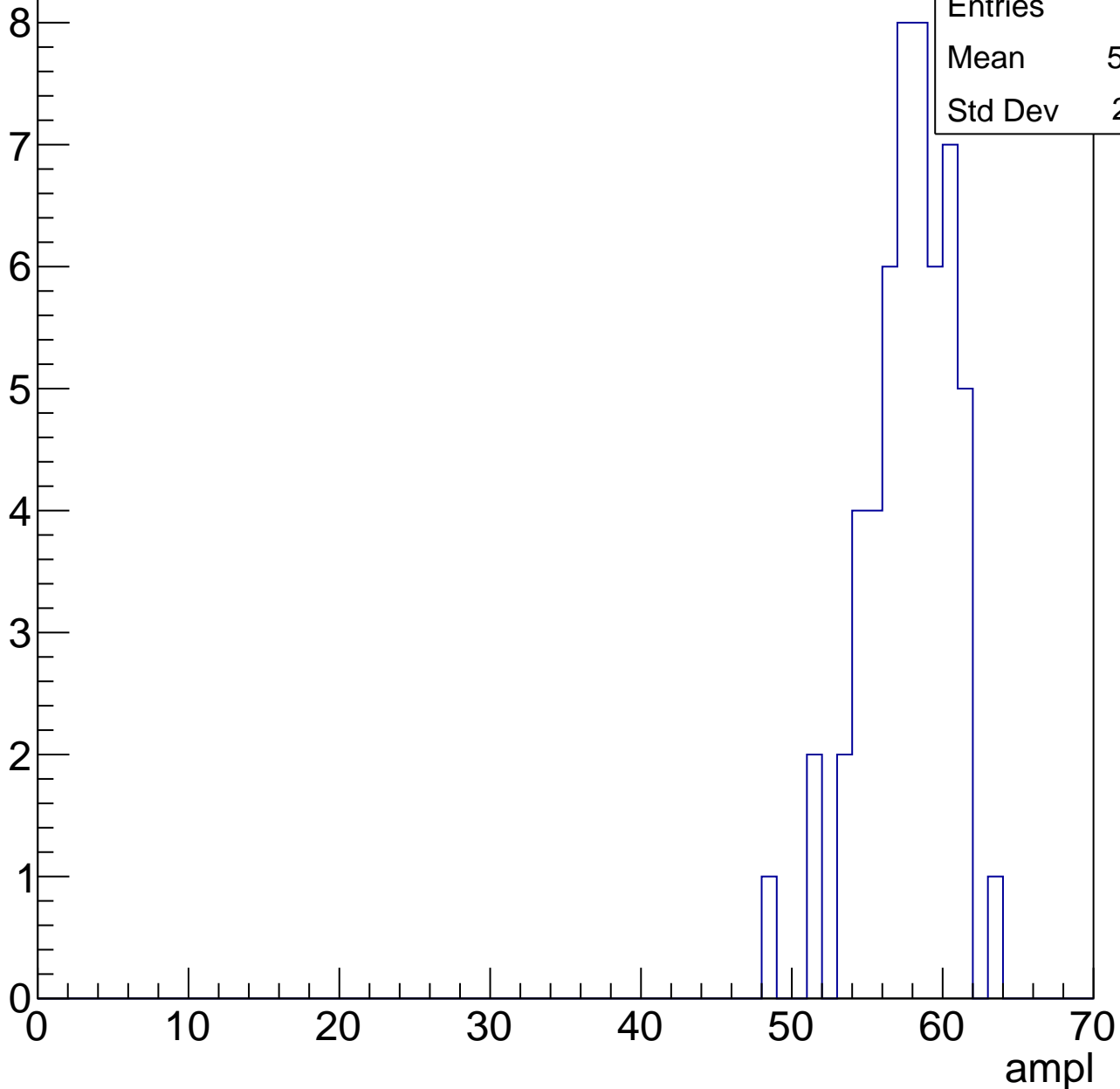


B0L002S, U26-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	57.22
Std Dev	2.891

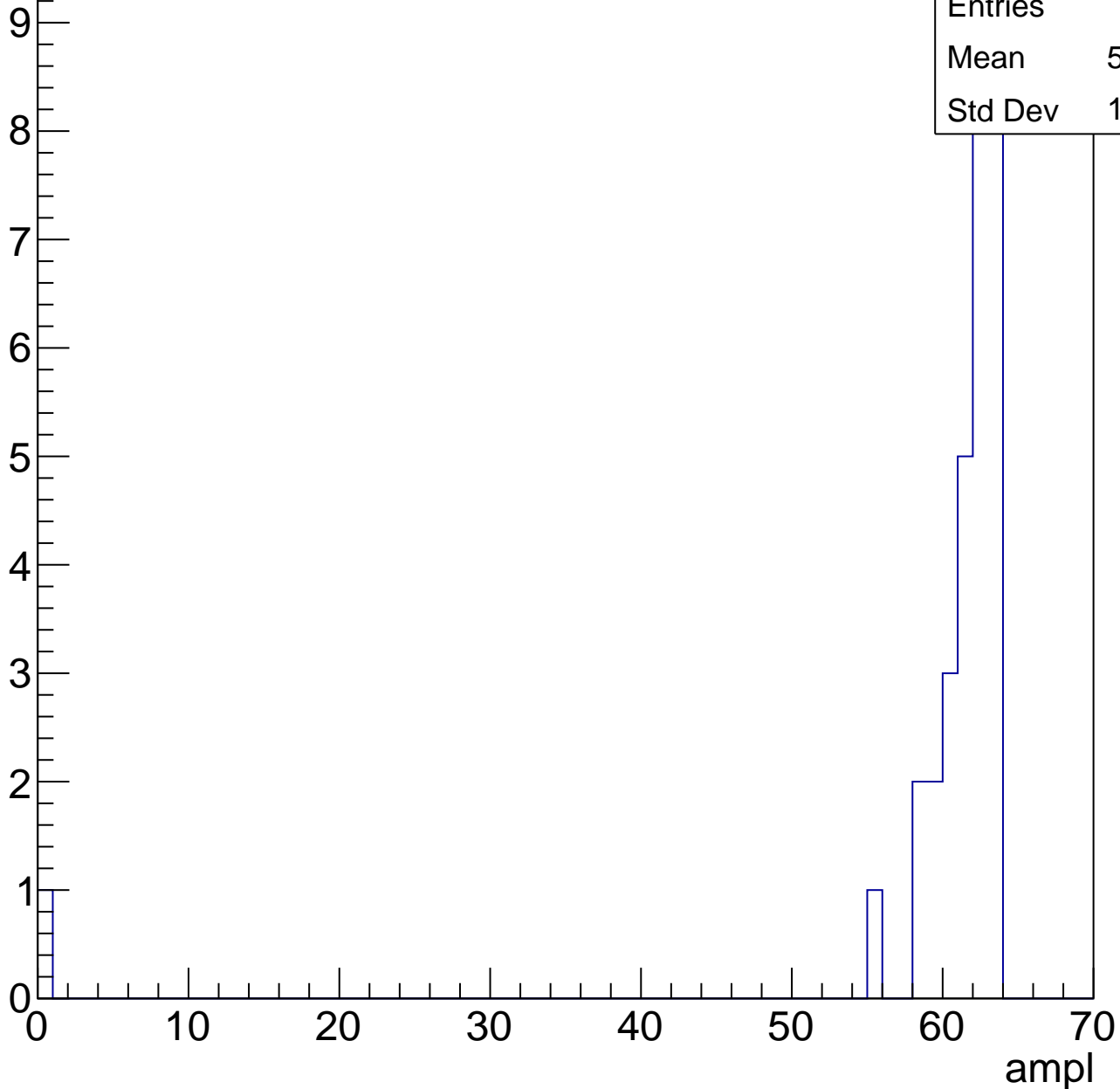


B0L002S, U26-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.23
Std Dev	10.97



B0L002S, U26-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch99, adc0

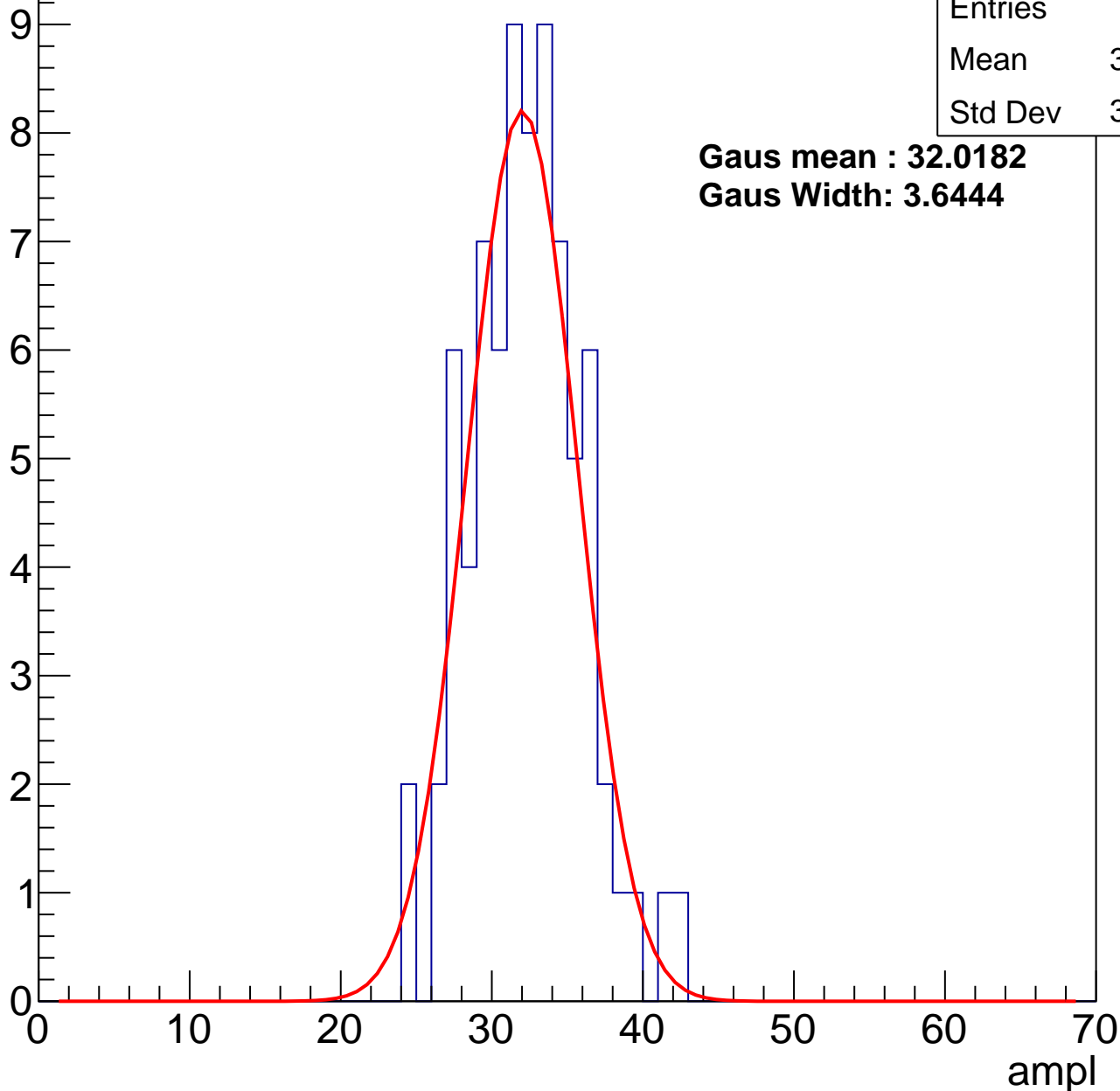
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	31.84
Std Dev	3.608

Gaus mean : 32.0182

Gaus Width: 3.6444



B0L002S, U26-ch99, adc1

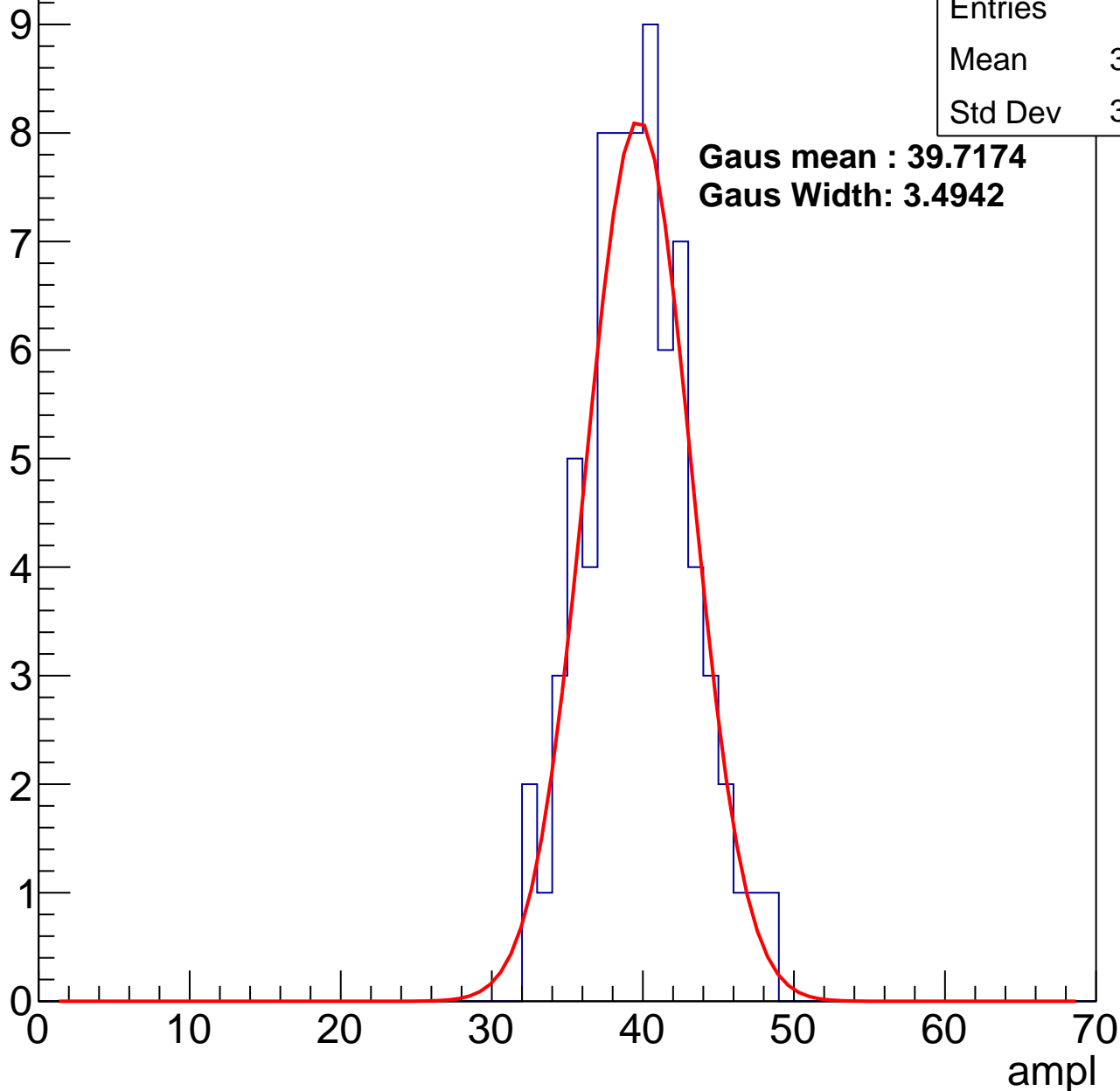
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	39.25
Std Dev	3.435

Gaus mean : 39.7174

Gaus Width: 3.4942



B0L002S, U26-ch99, adc2

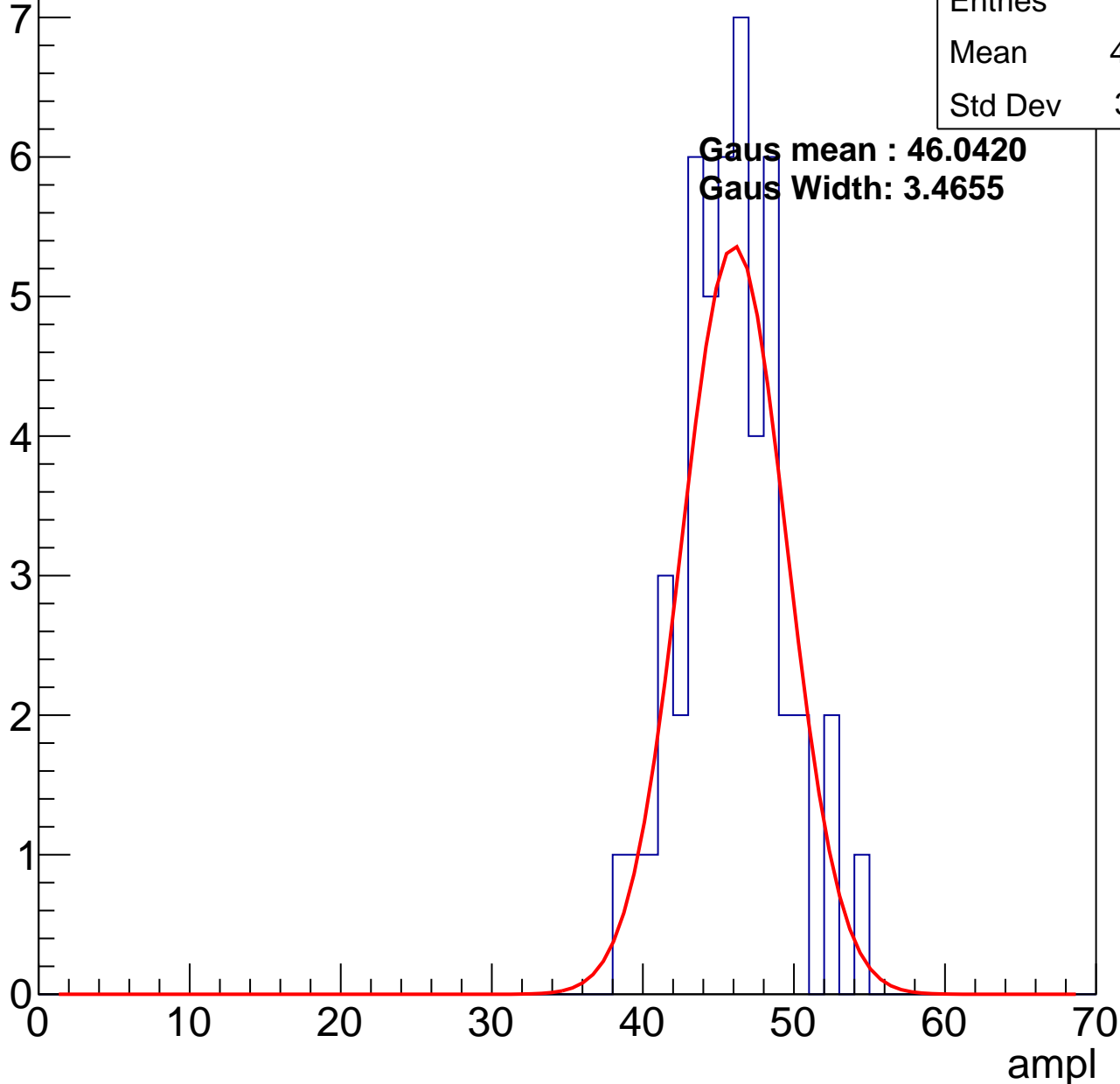
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	45.43
Std Dev	3.301

Gaus mean : 46.0420

Gaus Width: 3.4655

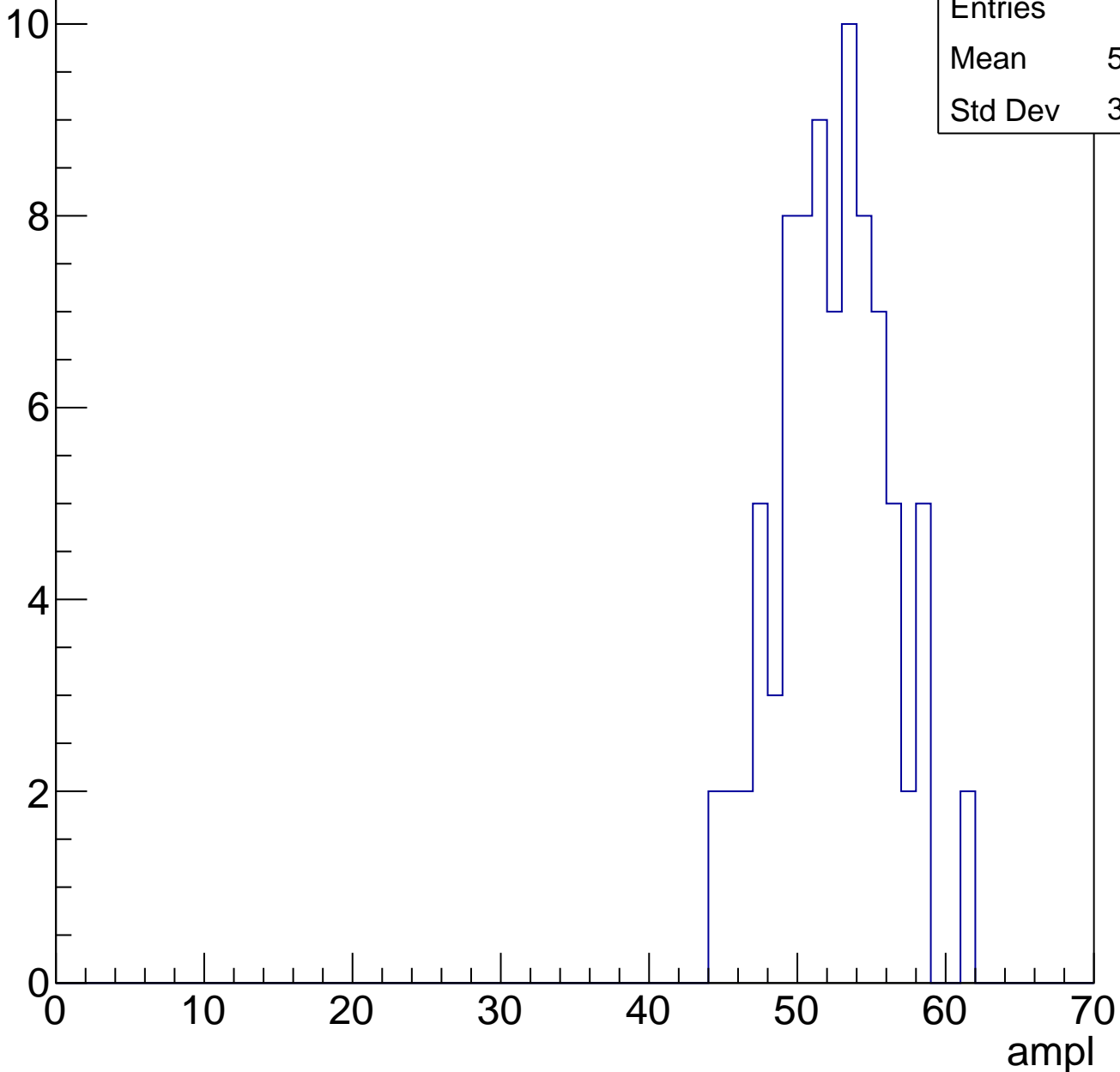


B0L002S, U26-ch99, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	51.96
Std Dev	3.696

Entry

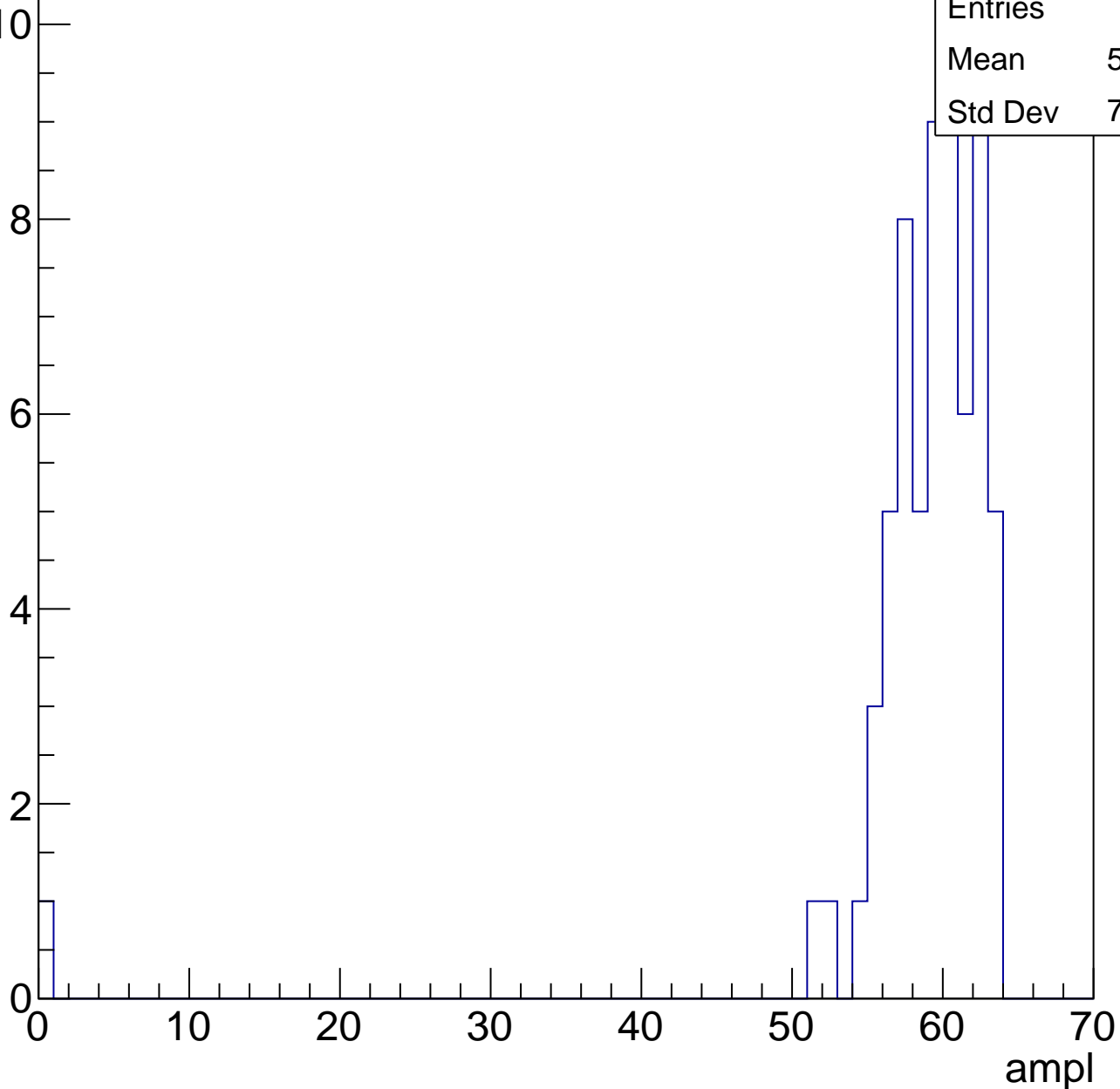


B0L002S, U26-ch99, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	58.09
Std Dev	7.798

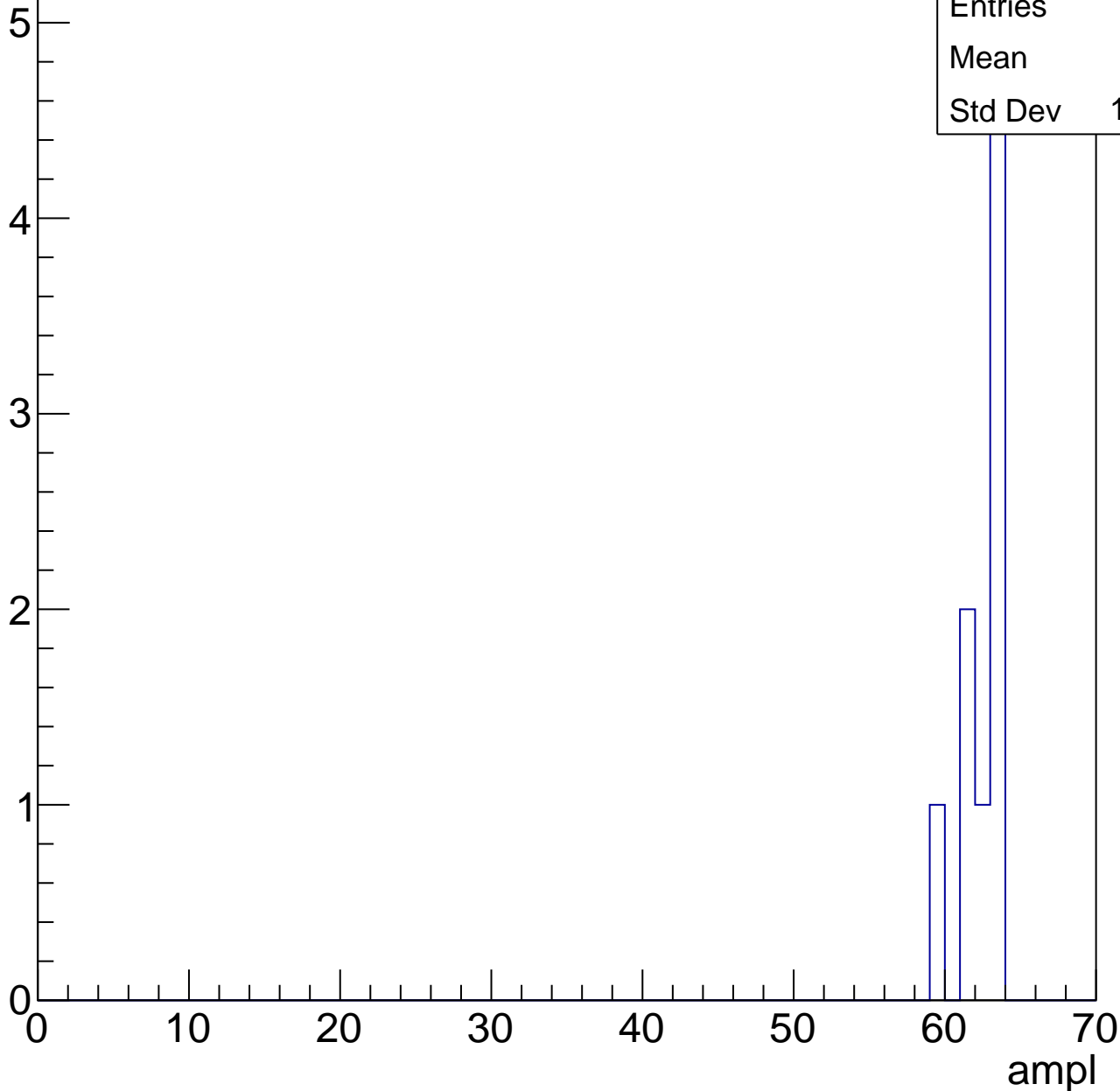


B0L002S, U26-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	62
Std Dev	1.333



B0L002S, U26-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

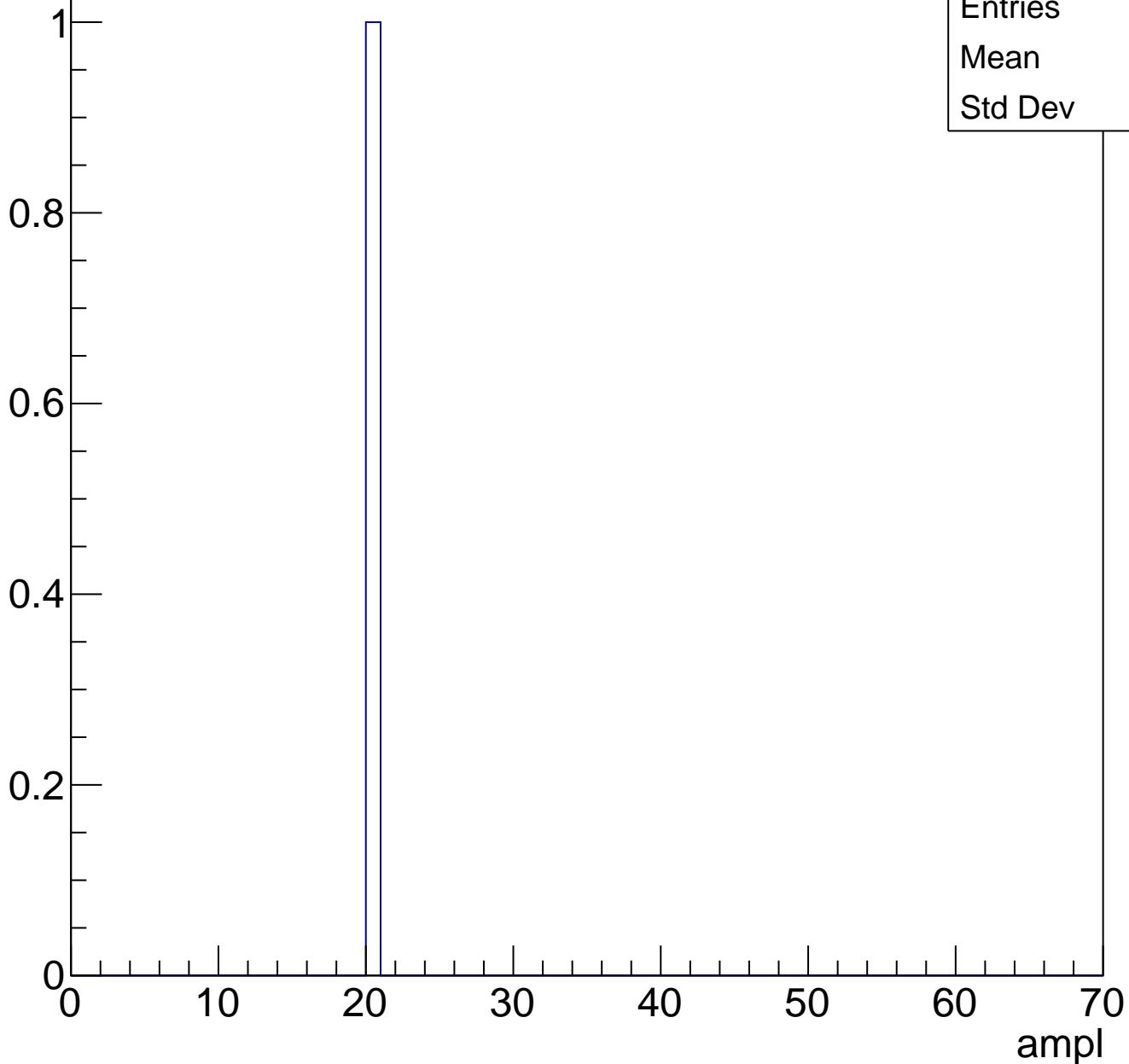


Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch100, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	28.37
Std Dev	4.767

Gaus mean : 29.165

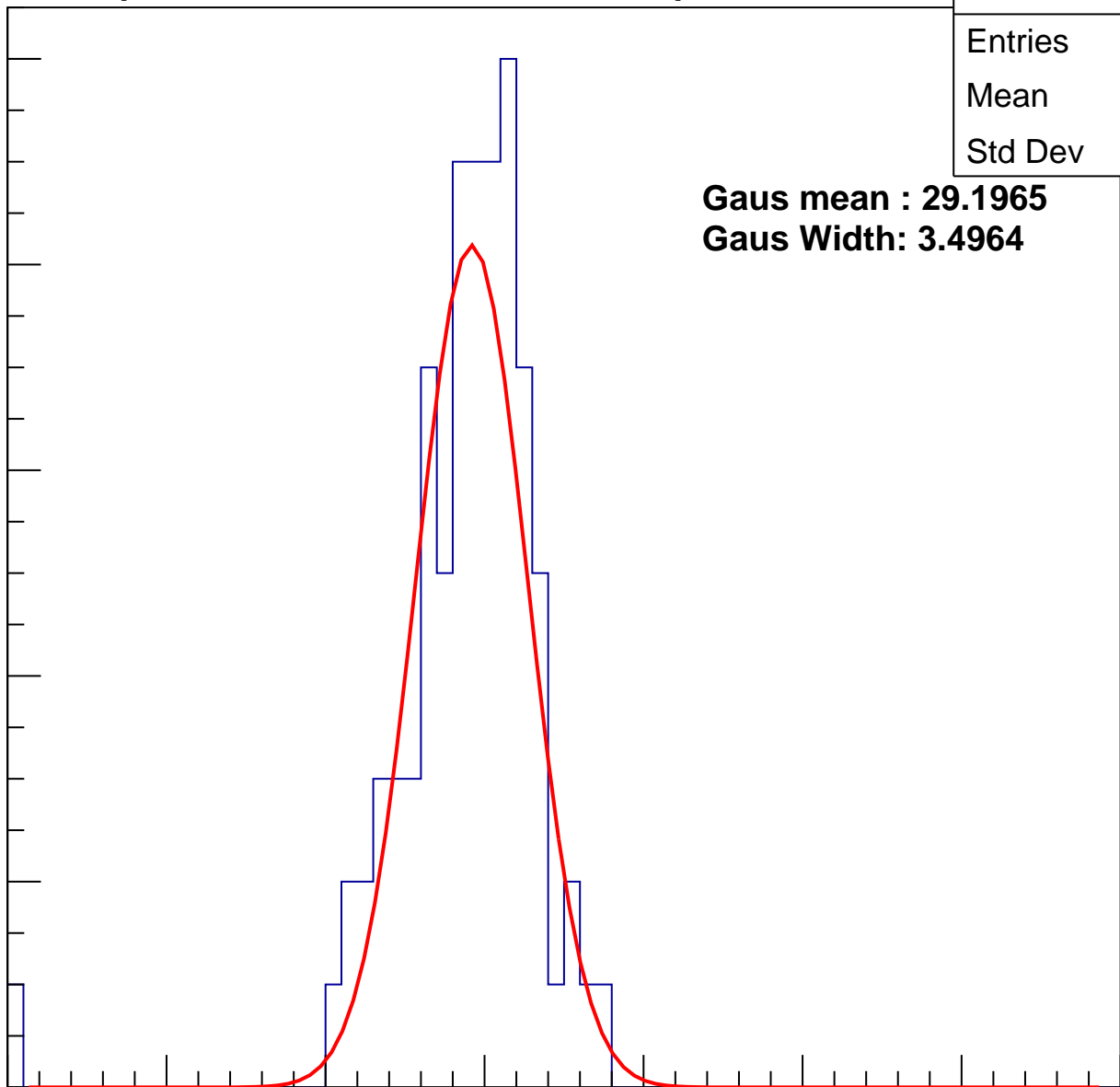
Gaus Width: 3.4964

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



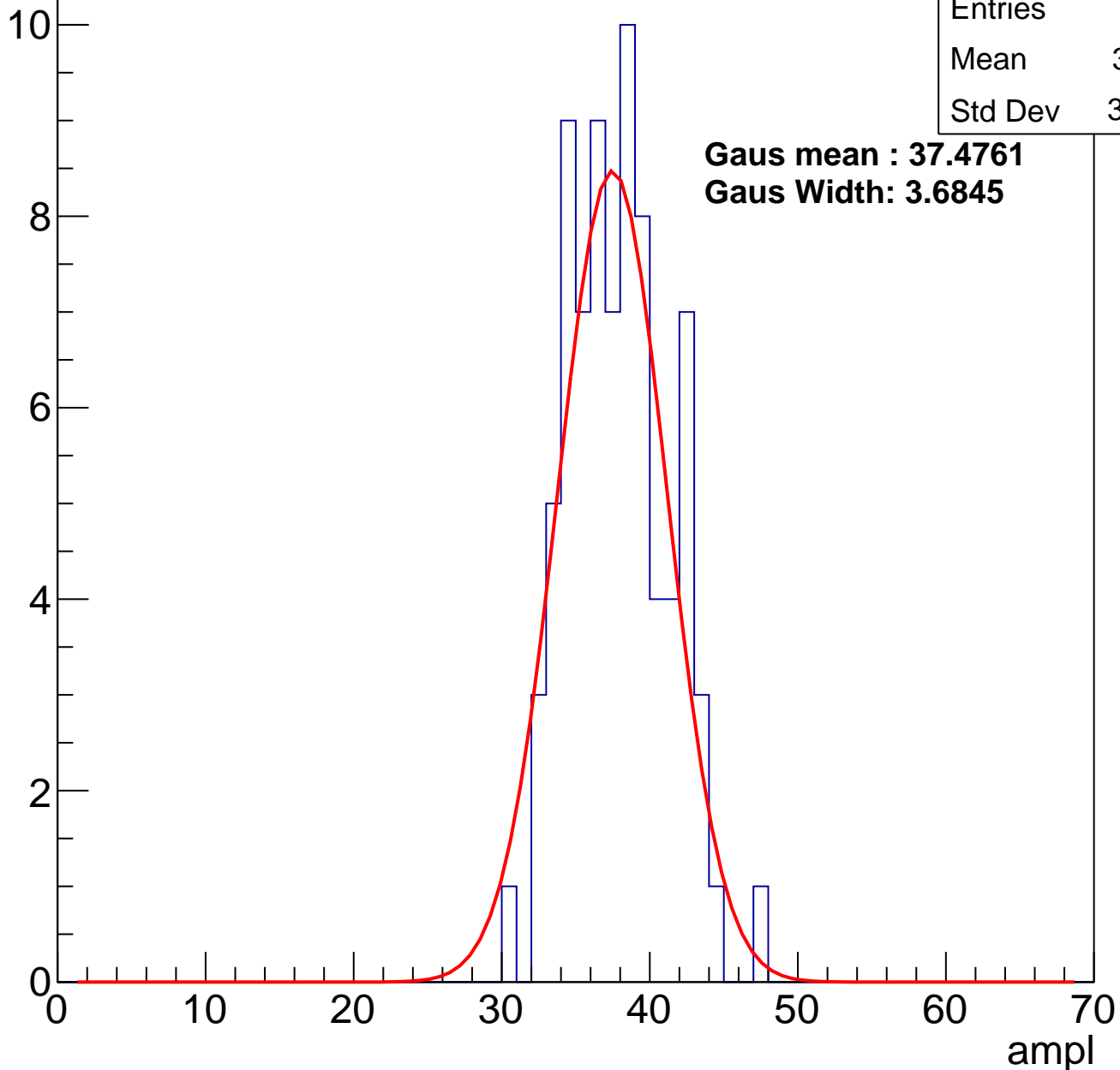
B0L002S, U26-ch100, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	37.41
Std Dev	3.343

Gaus mean : 37.4761
Gaus Width: 3.6845

Entry



B0L002S, U26-ch100, adc2

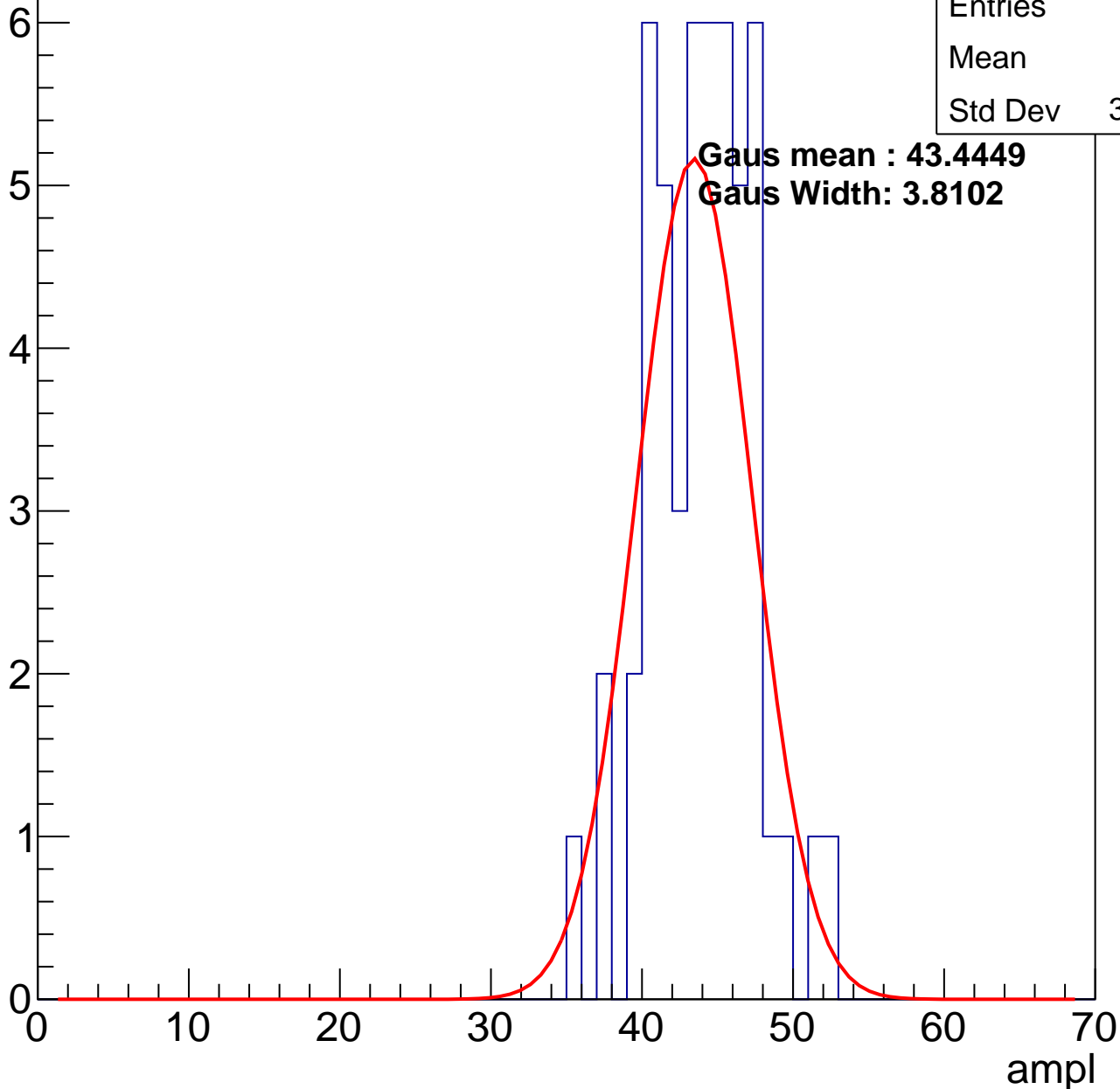
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	43.5
Std Dev	3.422

Gaus mean : 43.4449

Gaus Width: 3.8102

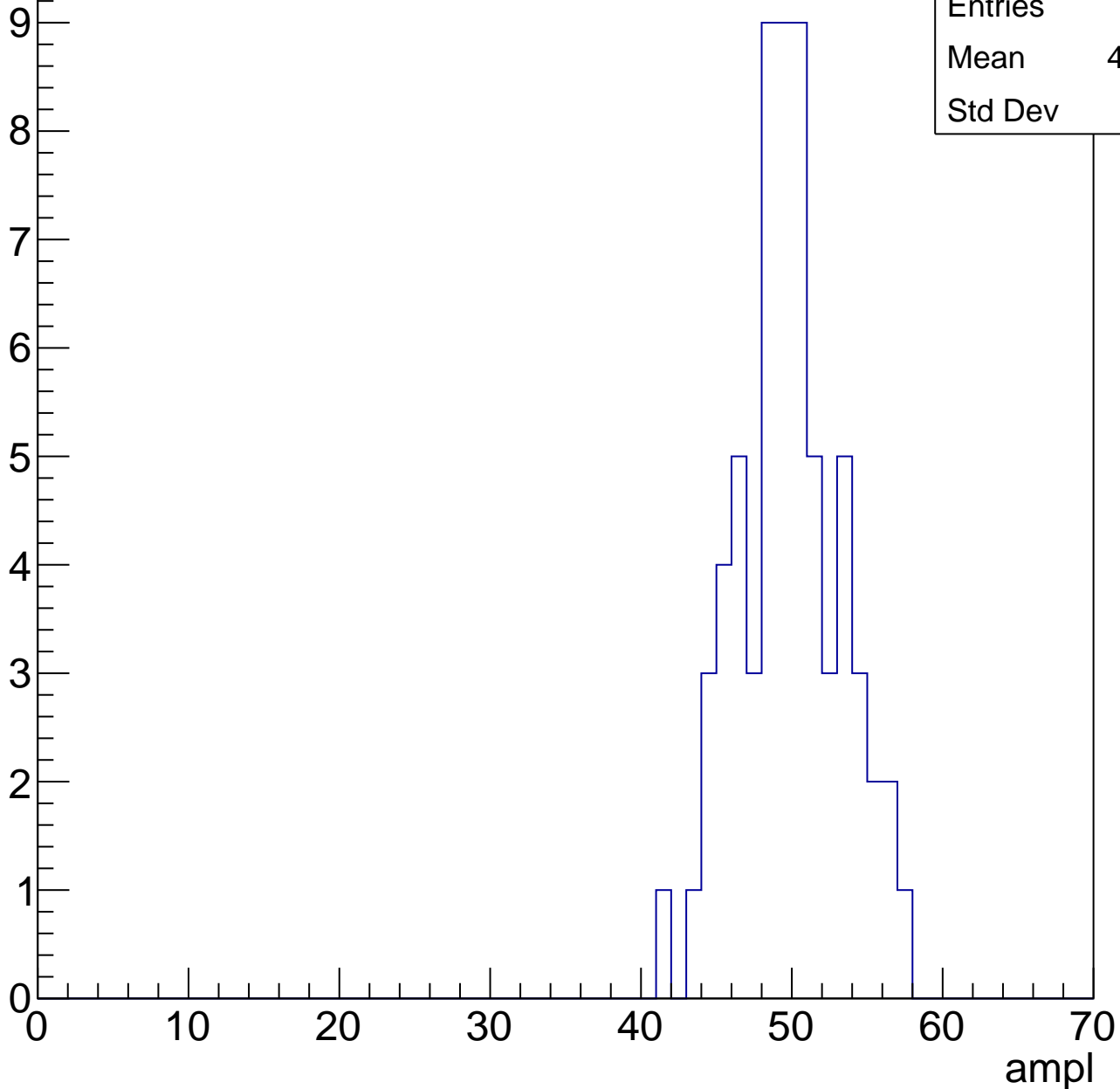


B0L002S, U26-ch100, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.34
Std Dev	3.38

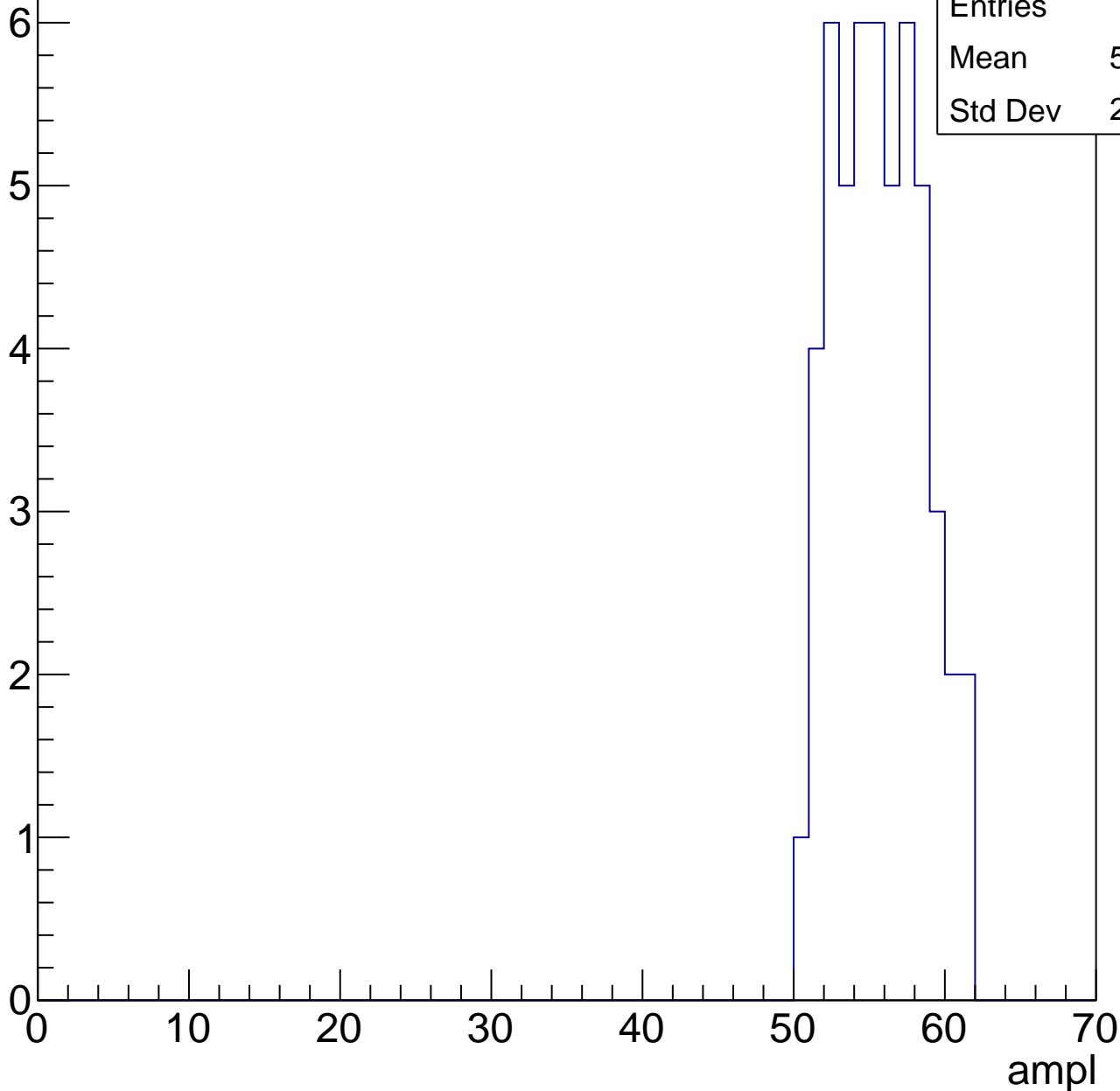


B0L002S, U26-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.22
Std Dev	2.838

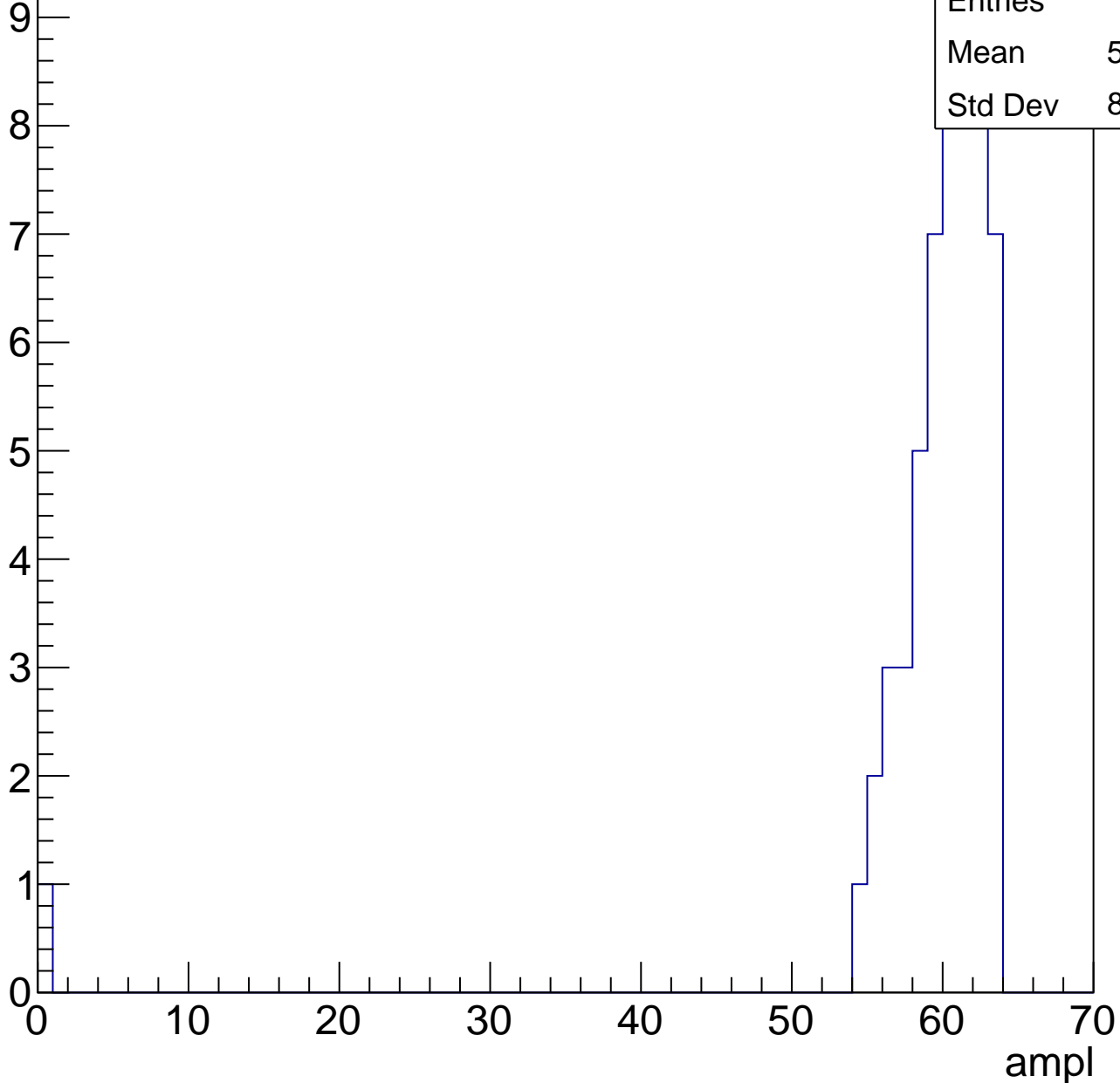


B0L002S, U26-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.76
Std Dev	8.402



B0L002S, U26-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.75
Std Dev	0.433

B0L002S, U26-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch101, adc0

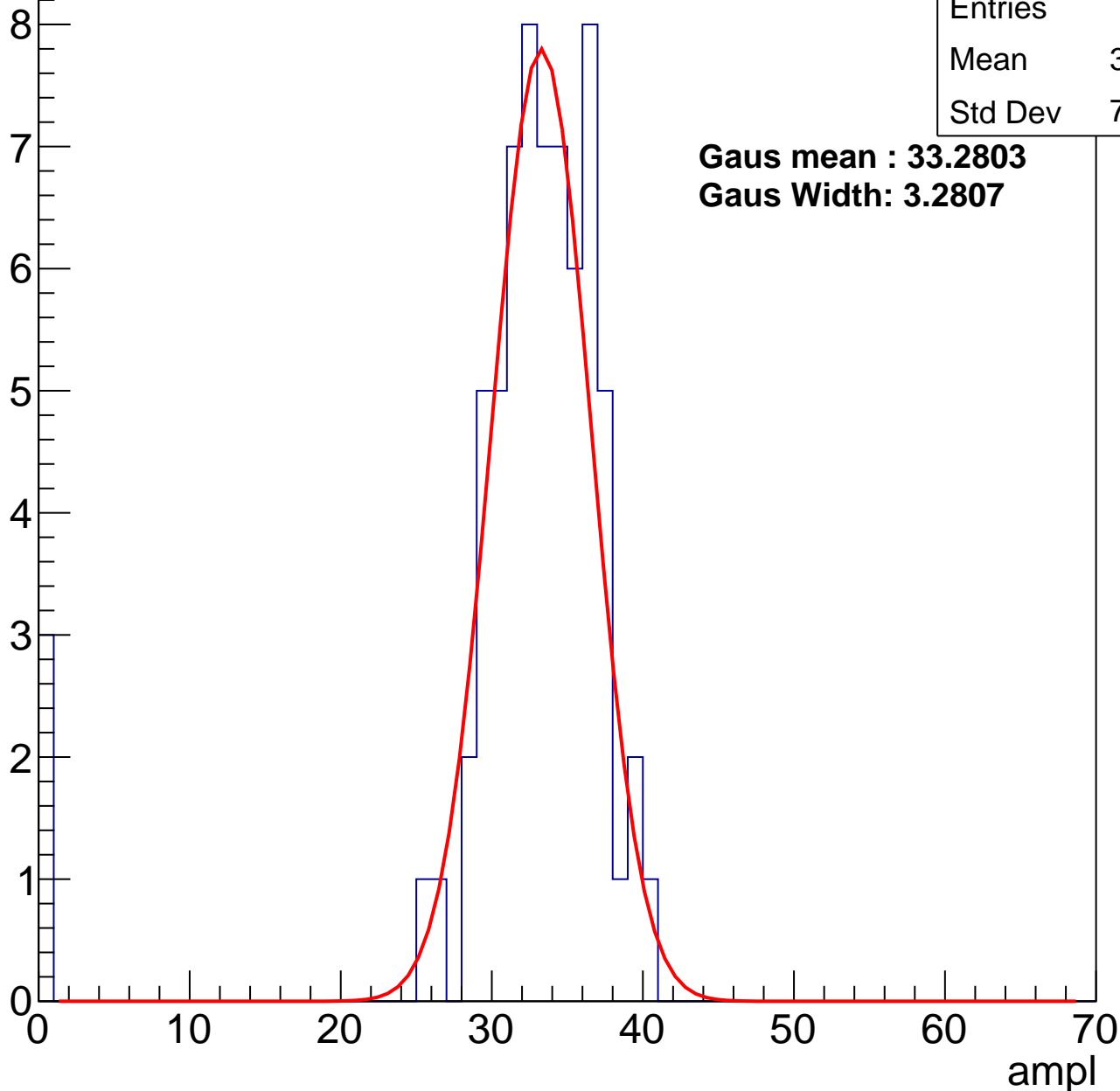
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	31.64
Std Dev	7.413

Gaus mean : 33.2803

Gaus Width: 3.2807



B0L002S, U26-ch101, adc1

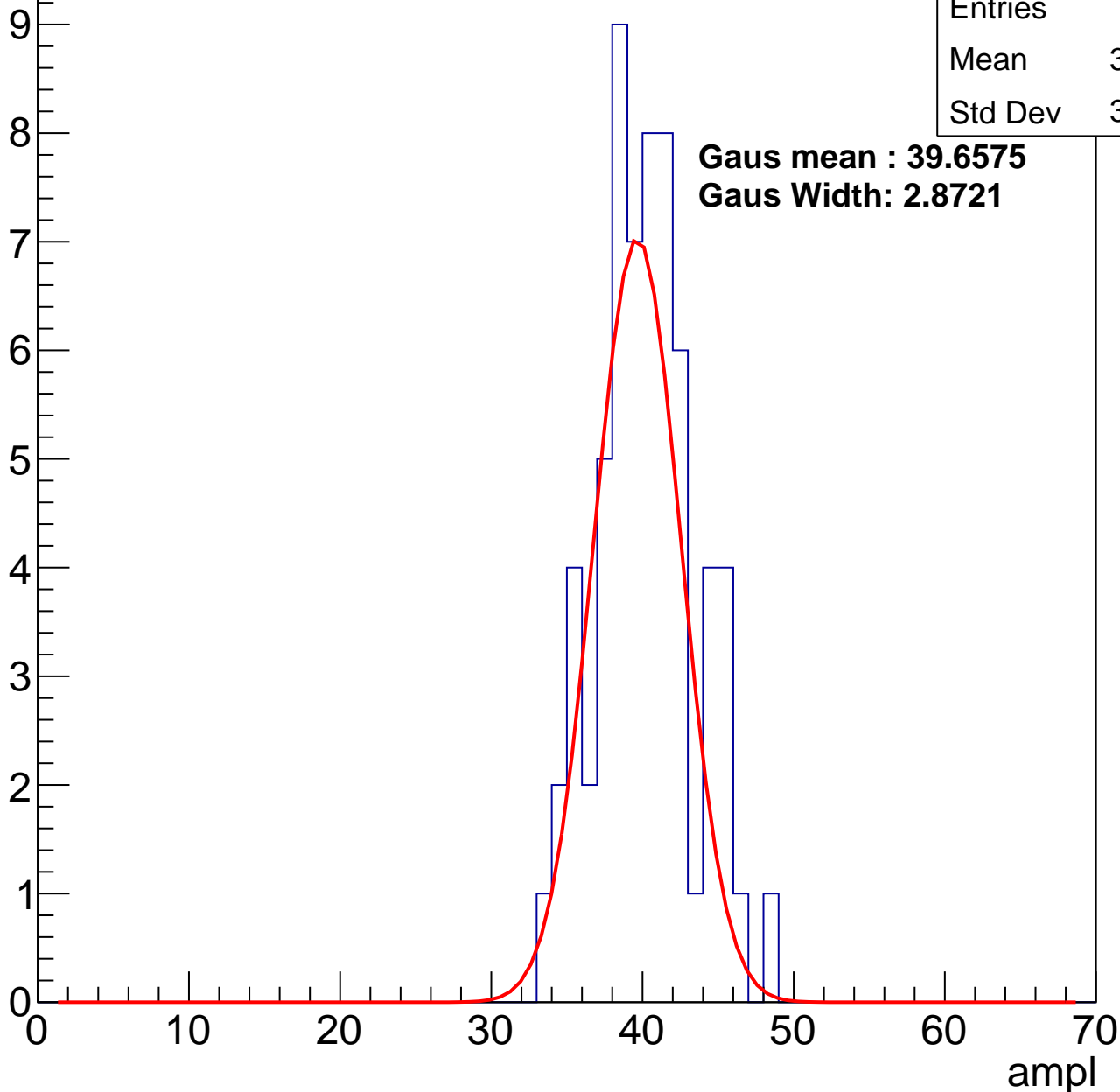
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	39.78
Std Dev	3.194

Gaus mean : 39.6575

Gaus Width: 2.8721



B0L002S, U26-ch101, adc2

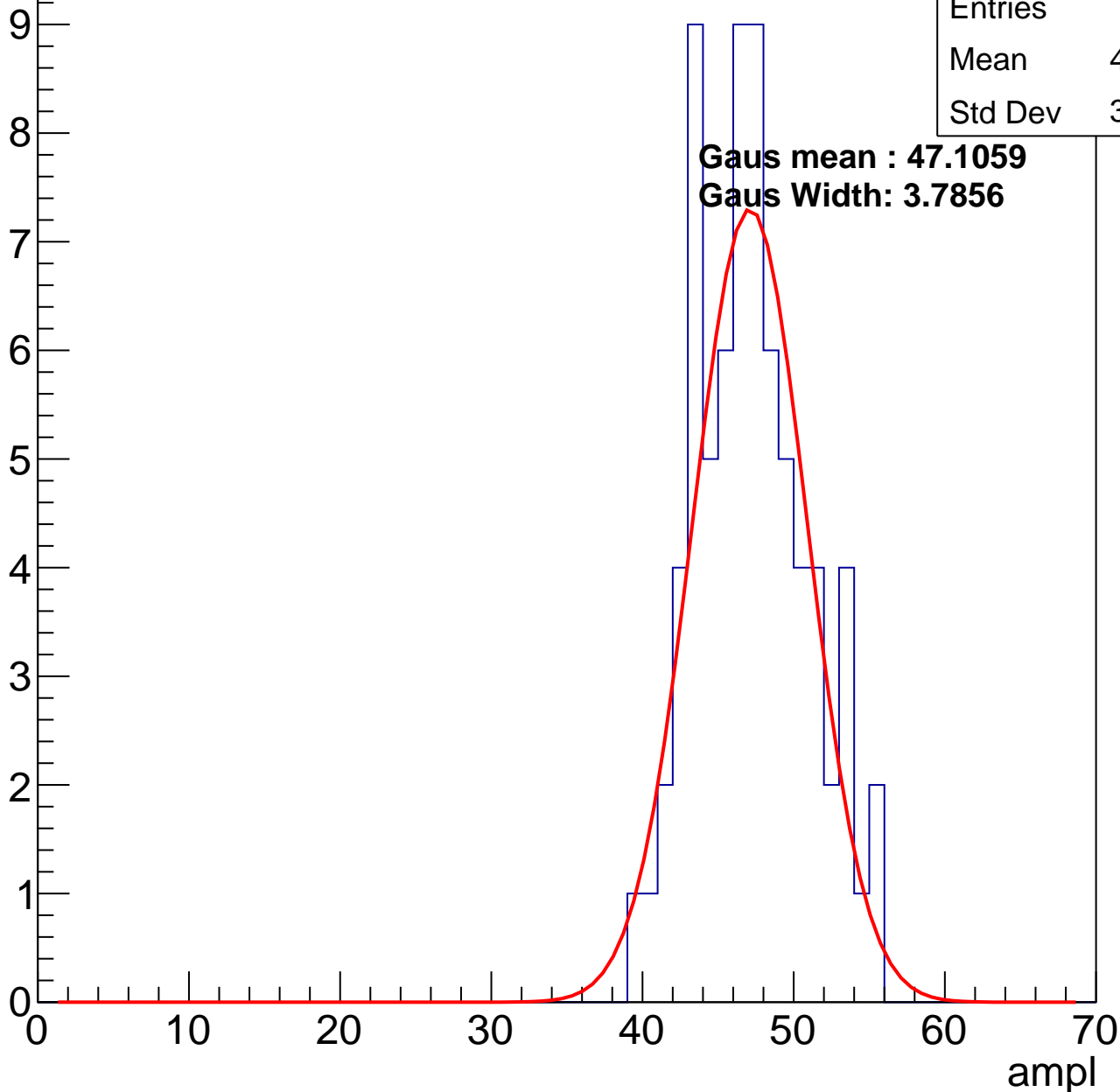
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	46.76
Std Dev	3.675

Gaus mean : 47.1059

Gaus Width: 3.7856

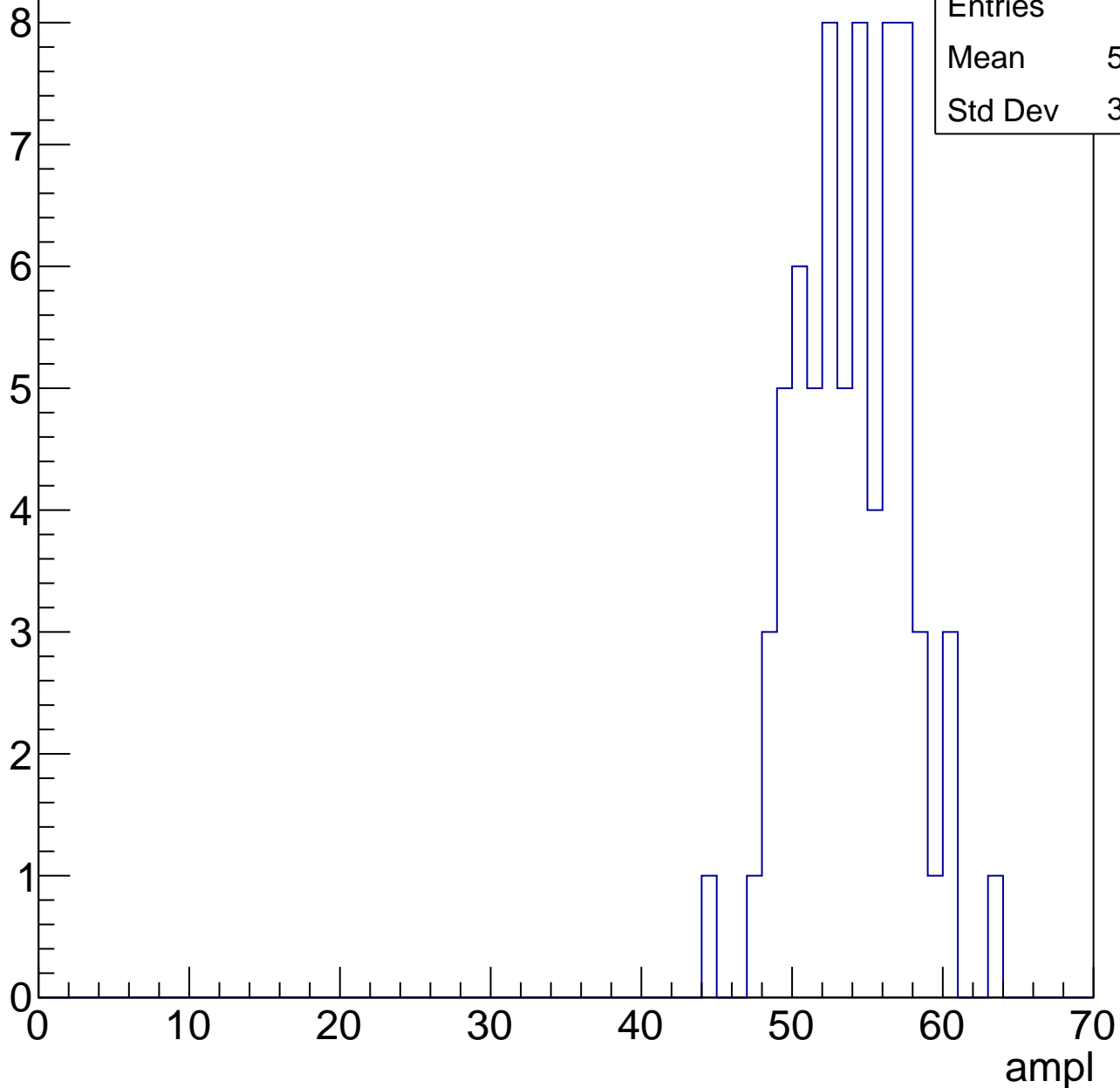


B0L002S, U26-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	53.54
Std Dev	3.616

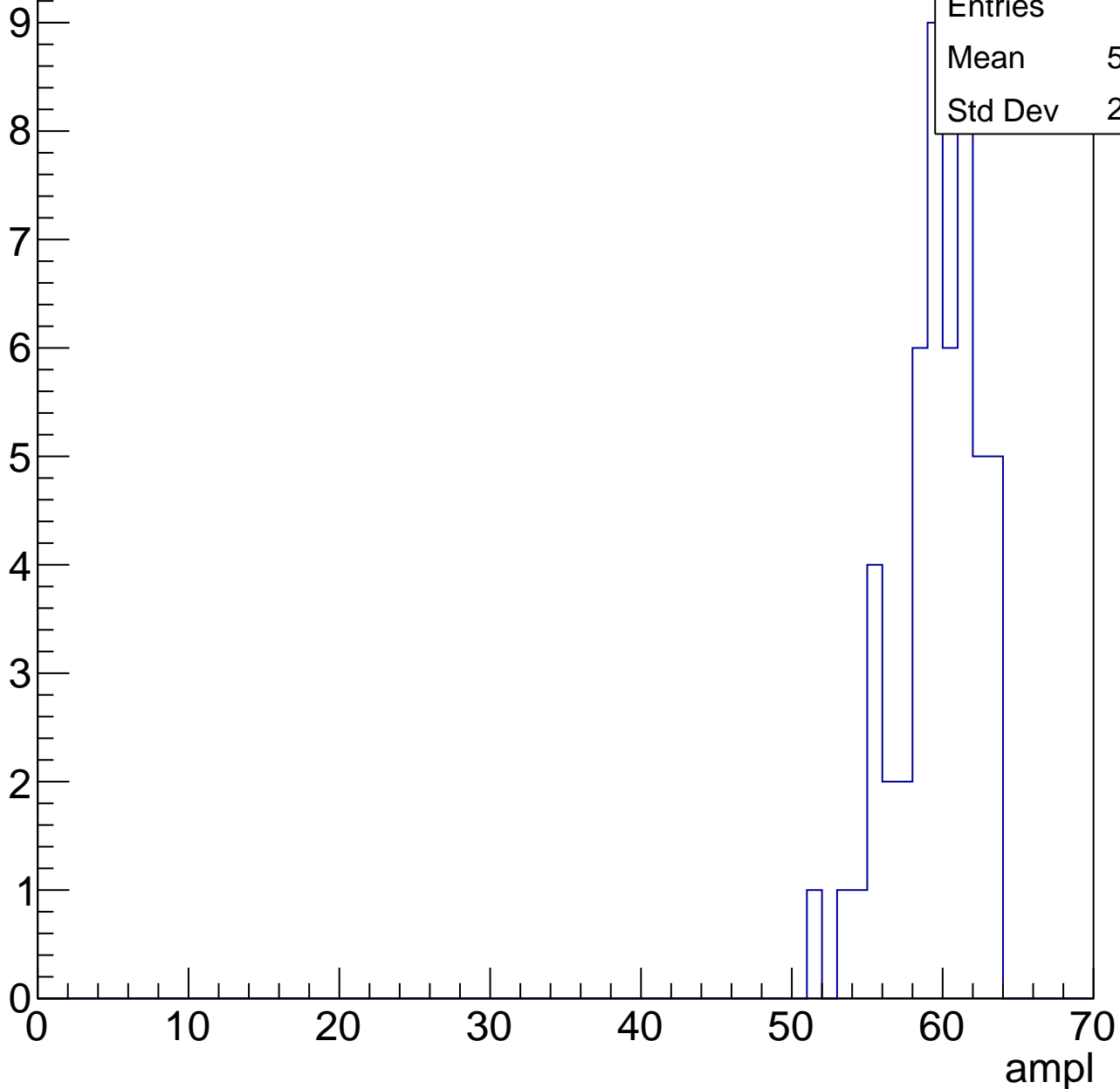


B0L002S, U26-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	59.12
Std Dev	2.769

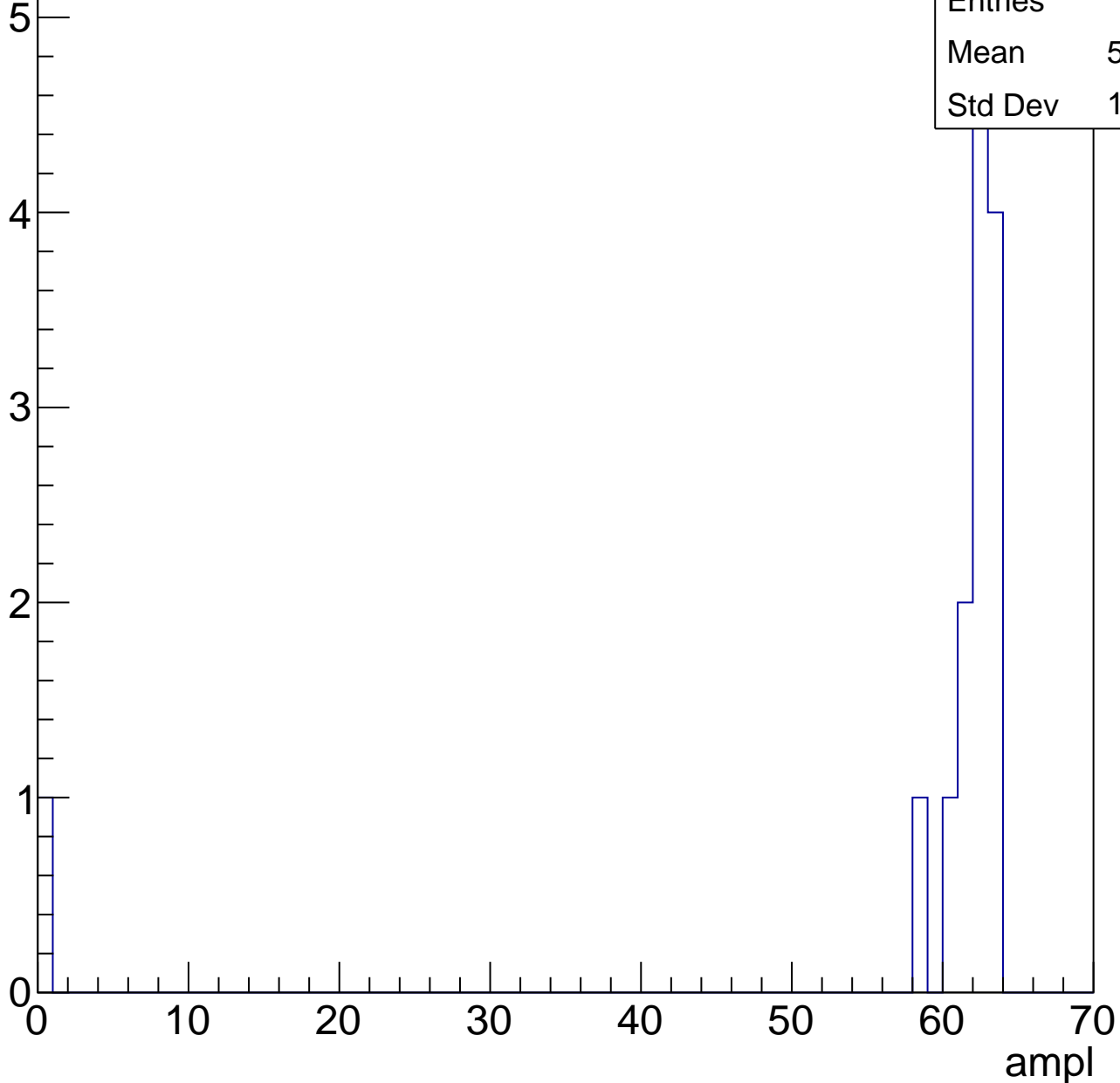


B0L002S, U26-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	14
Mean	57.29
Std Dev	15.94



B0L002S, U26-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch102, adc0

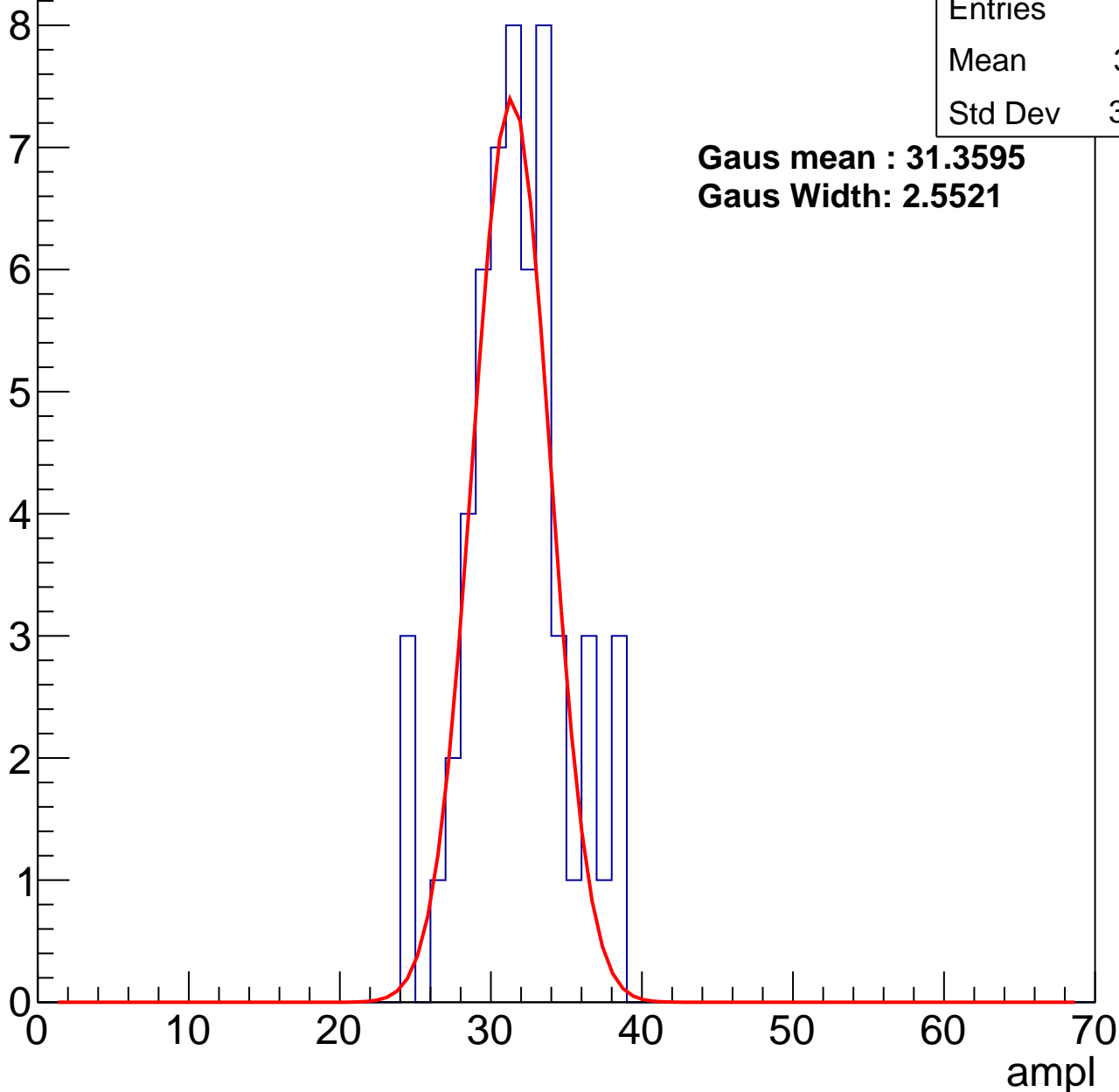
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	31.21
Std Dev	3.293

Gaus mean : 31.3595

Gaus Width: 2.5521



B0L002S, U26-ch102, adc1

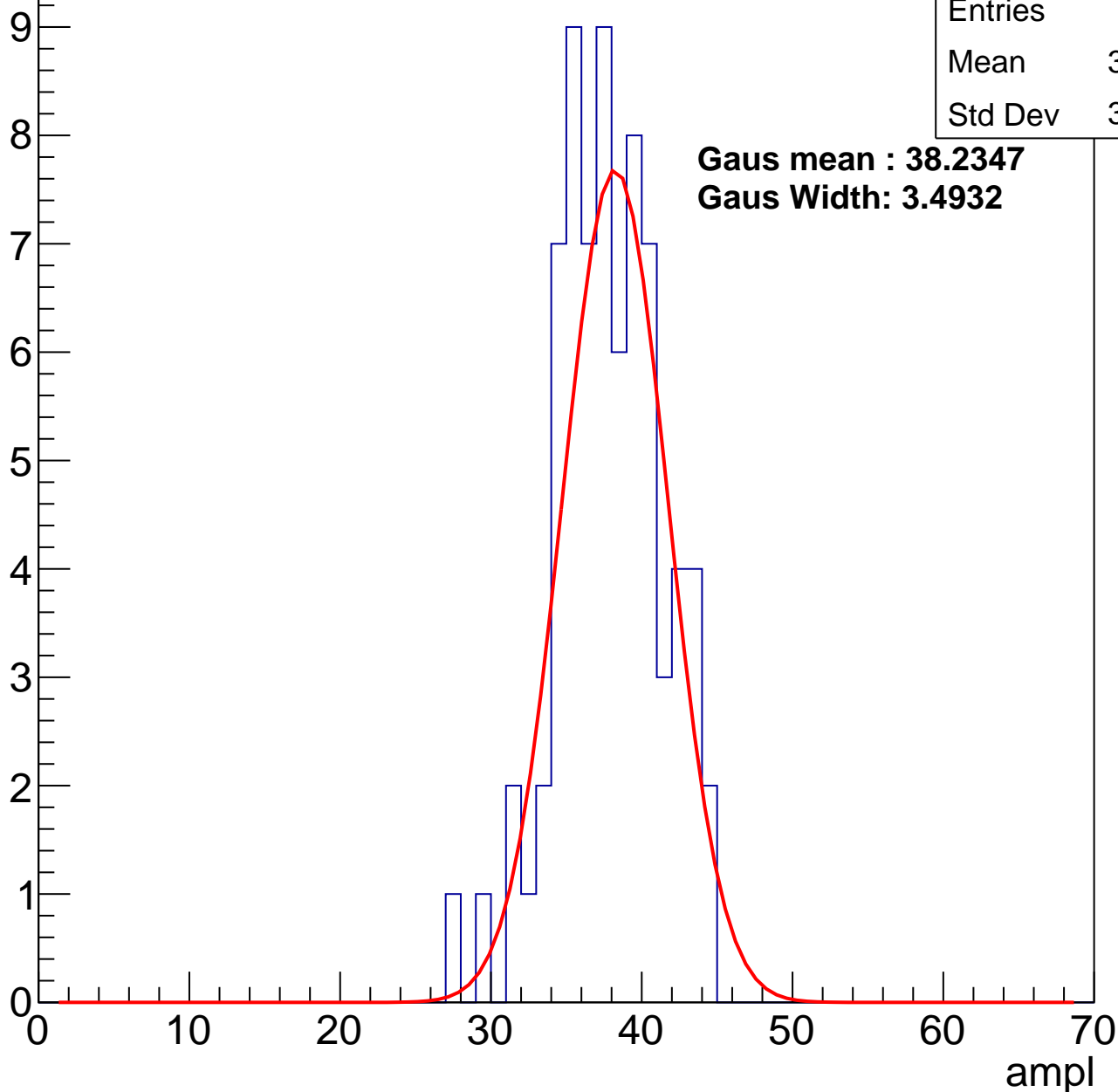
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.33
Std Dev	3.476

Gaus mean : 38.2347

Gaus Width: 3.4932

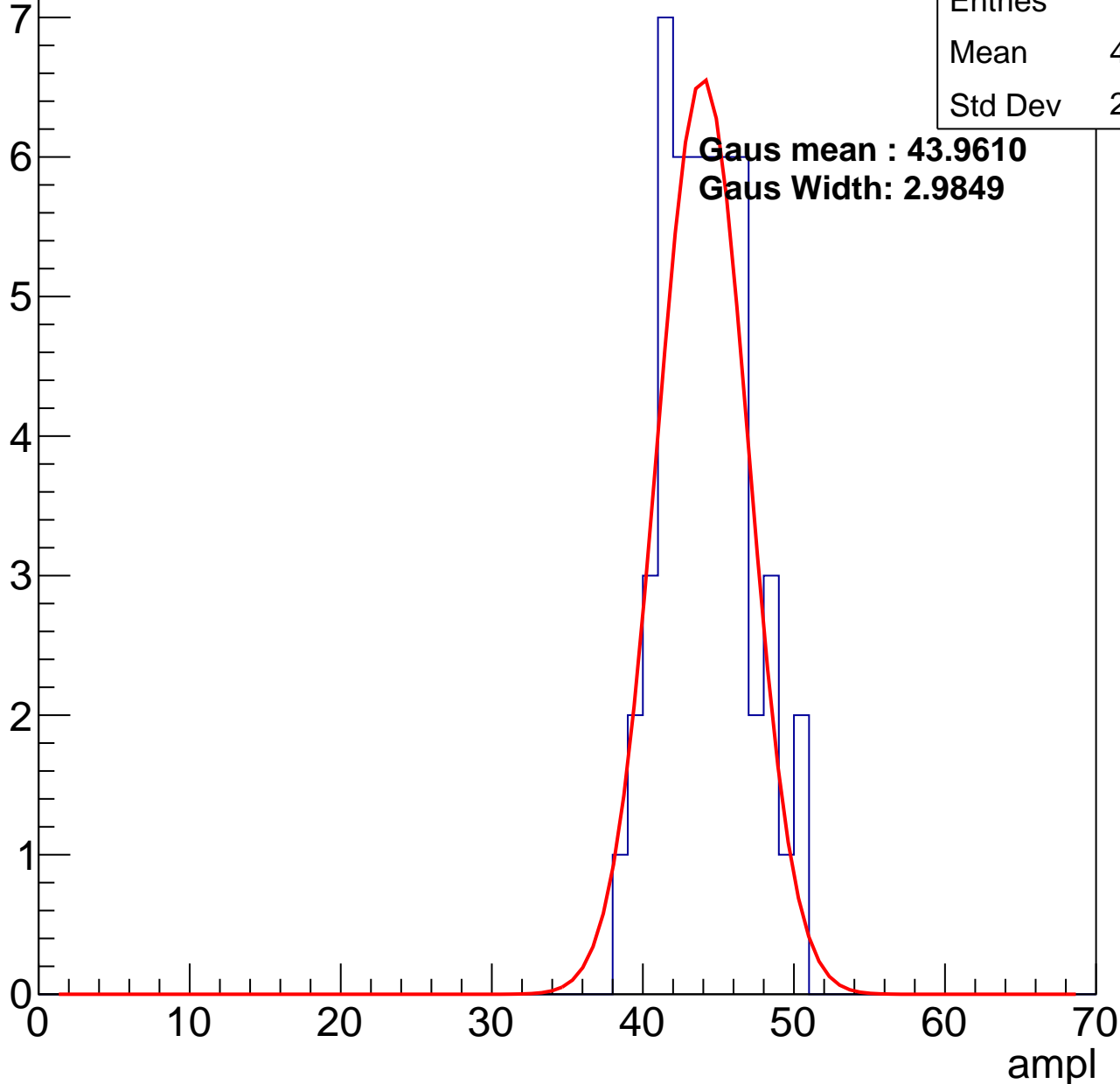


B0L002S, U26-ch102, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	43.73
Std Dev	2.857



B0L002S, U26-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	50.23
Std Dev	3.218

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

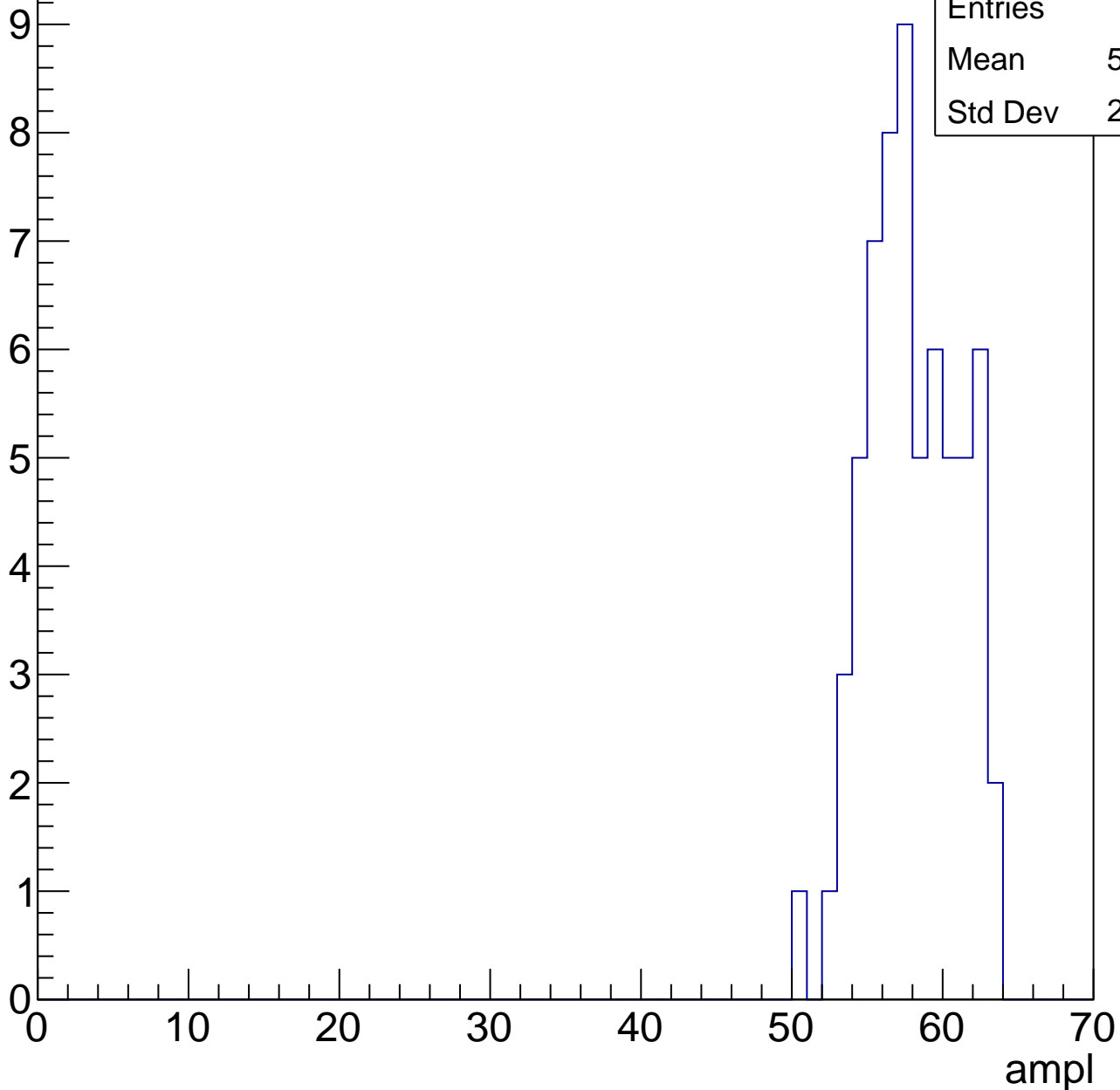
70

B0L002S, U26-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	57.52
Std Dev	2.986

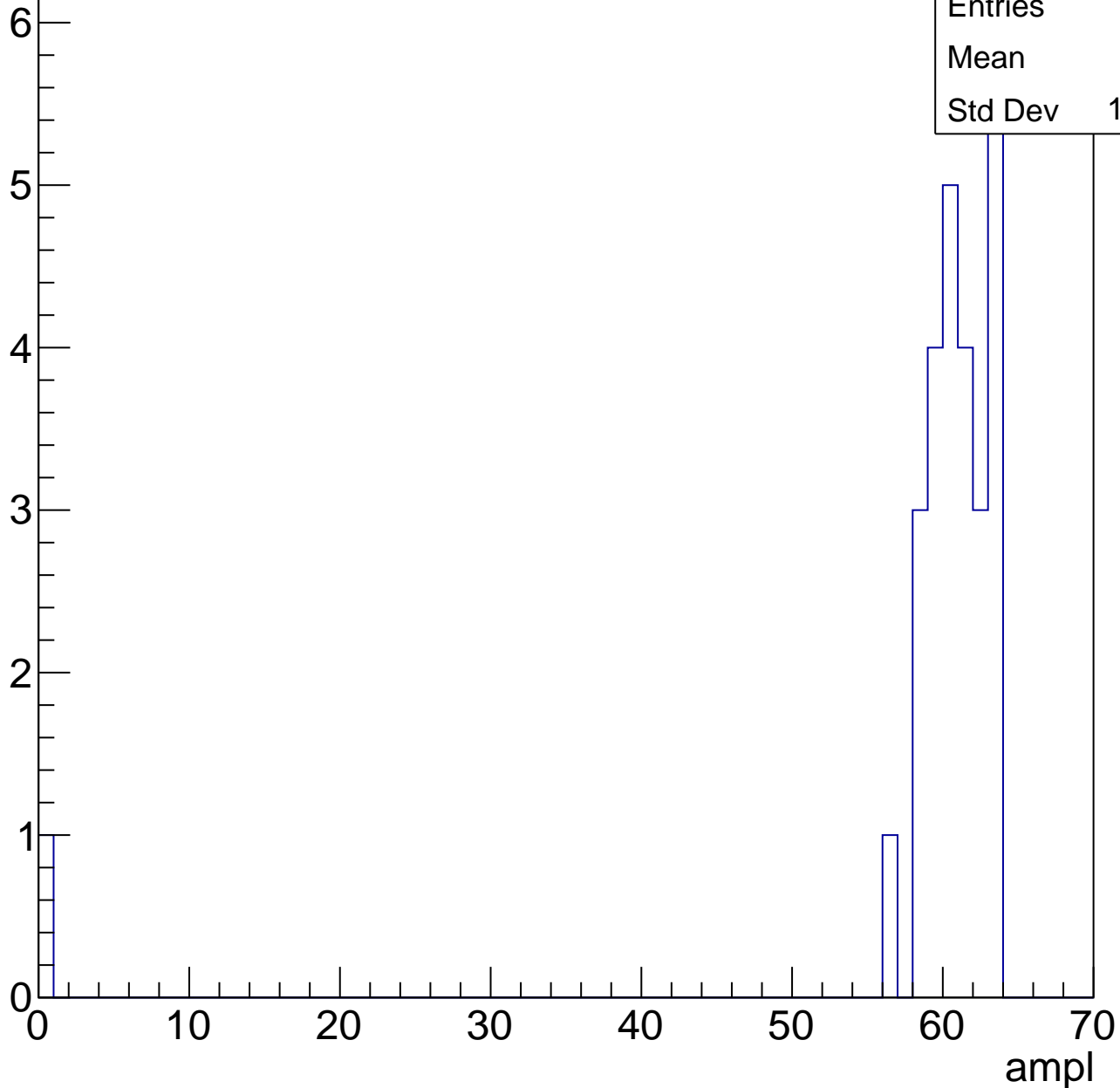


B0L002S, U26-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.3
Std Dev	11.58



B0L002S, U26-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B0L002S, U26-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch103, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	31.84
Std Dev	4.806

Gaus mean : 32.5786

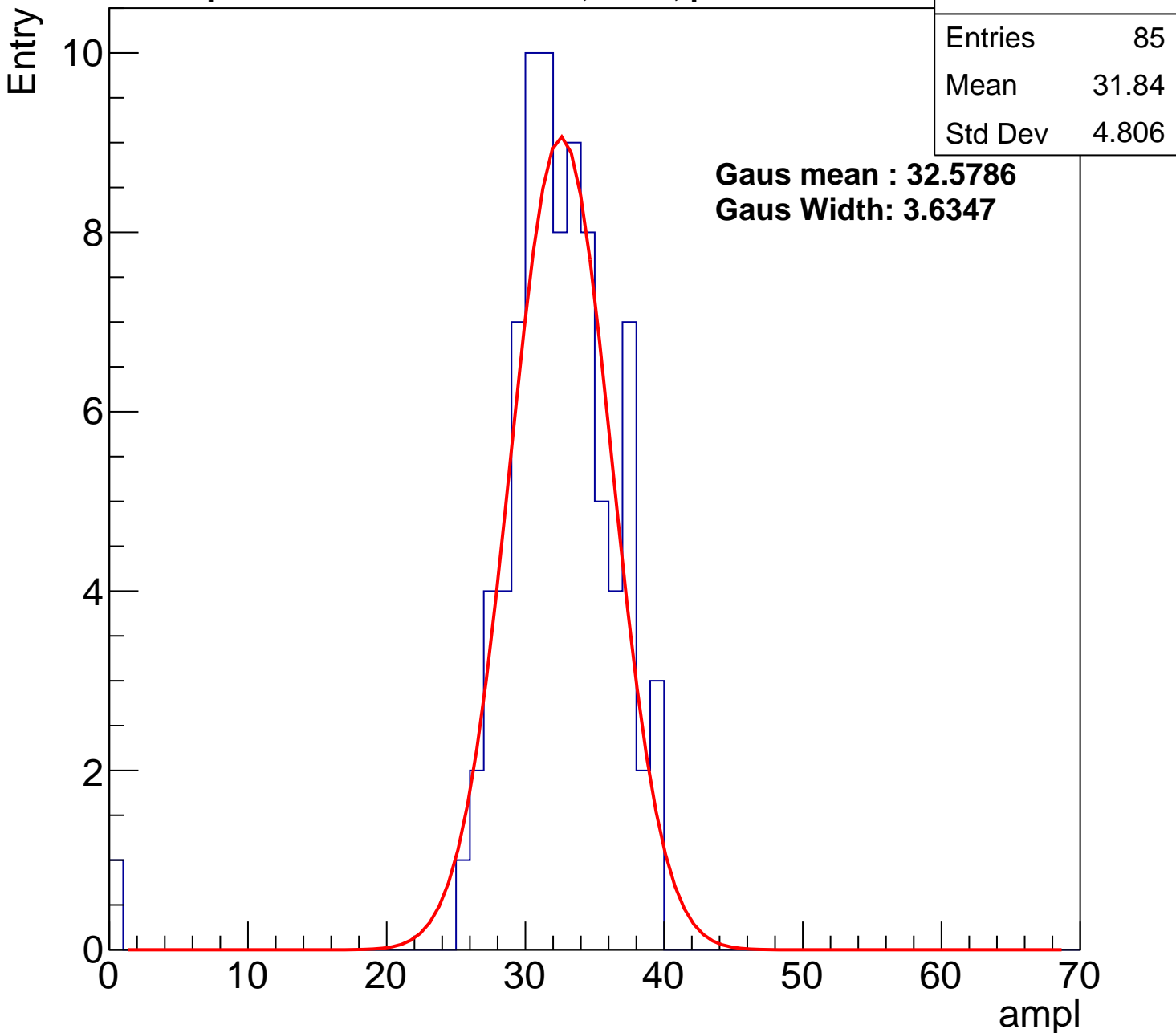
Gaus Width: 3.6347

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch103, adc1

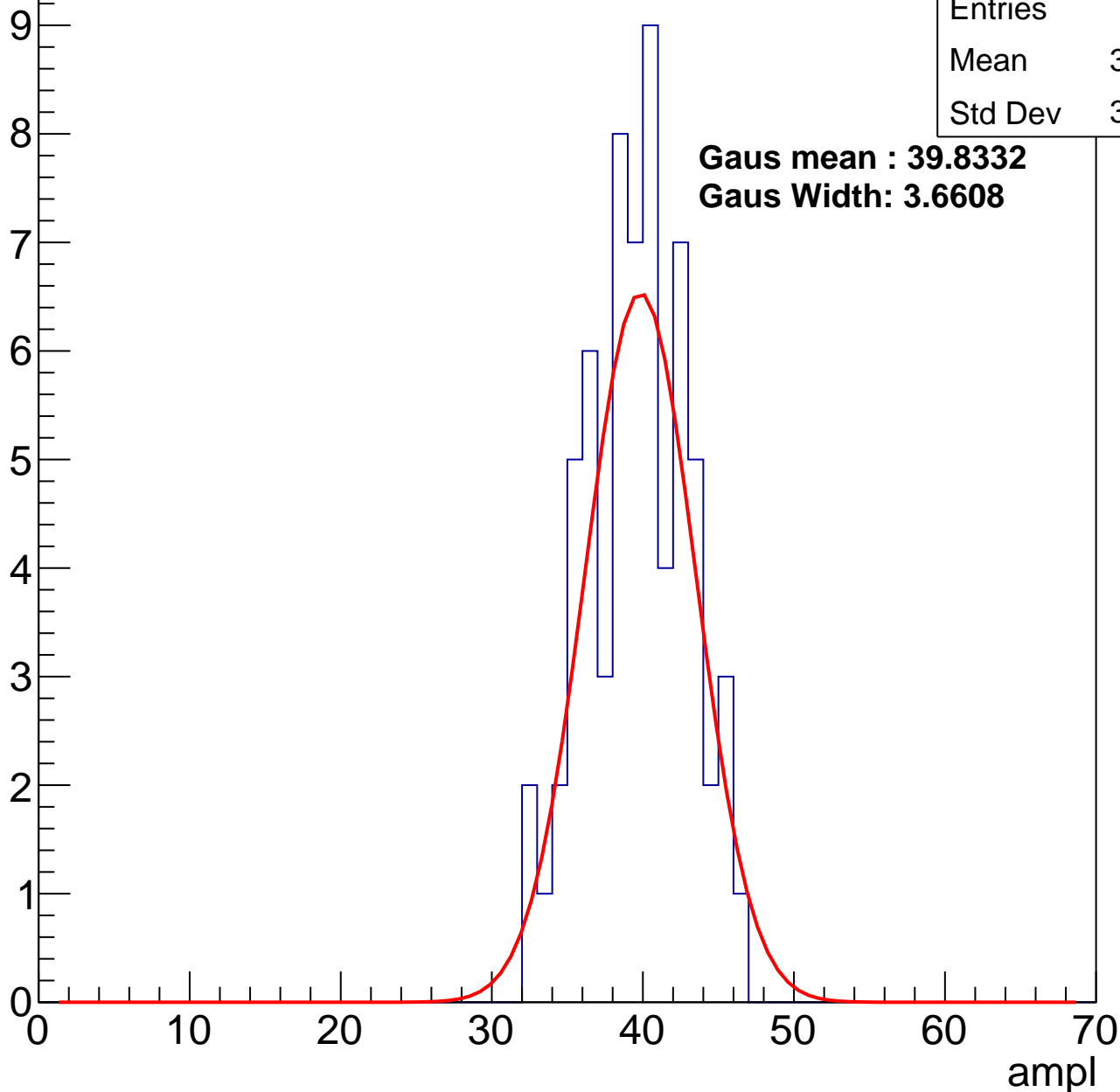
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	39.17
Std Dev	3.308

Gaus mean : 39.8332

Gaus Width: 3.6608



B0L002S, U26-ch103, adc2

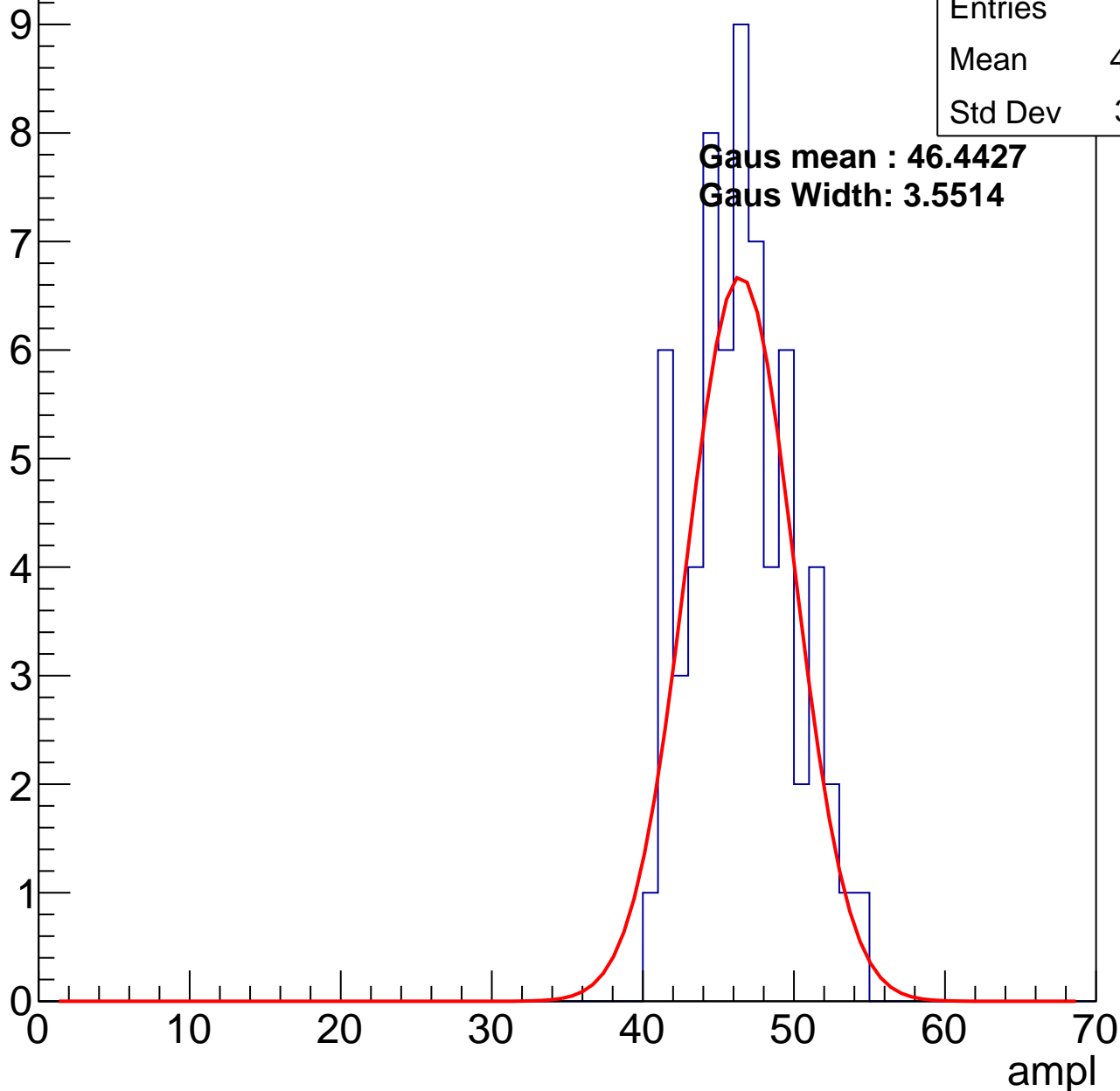
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	46.09
Std Dev	3.311

Gaus mean : 46.4427

Gaus Width: 3.5514

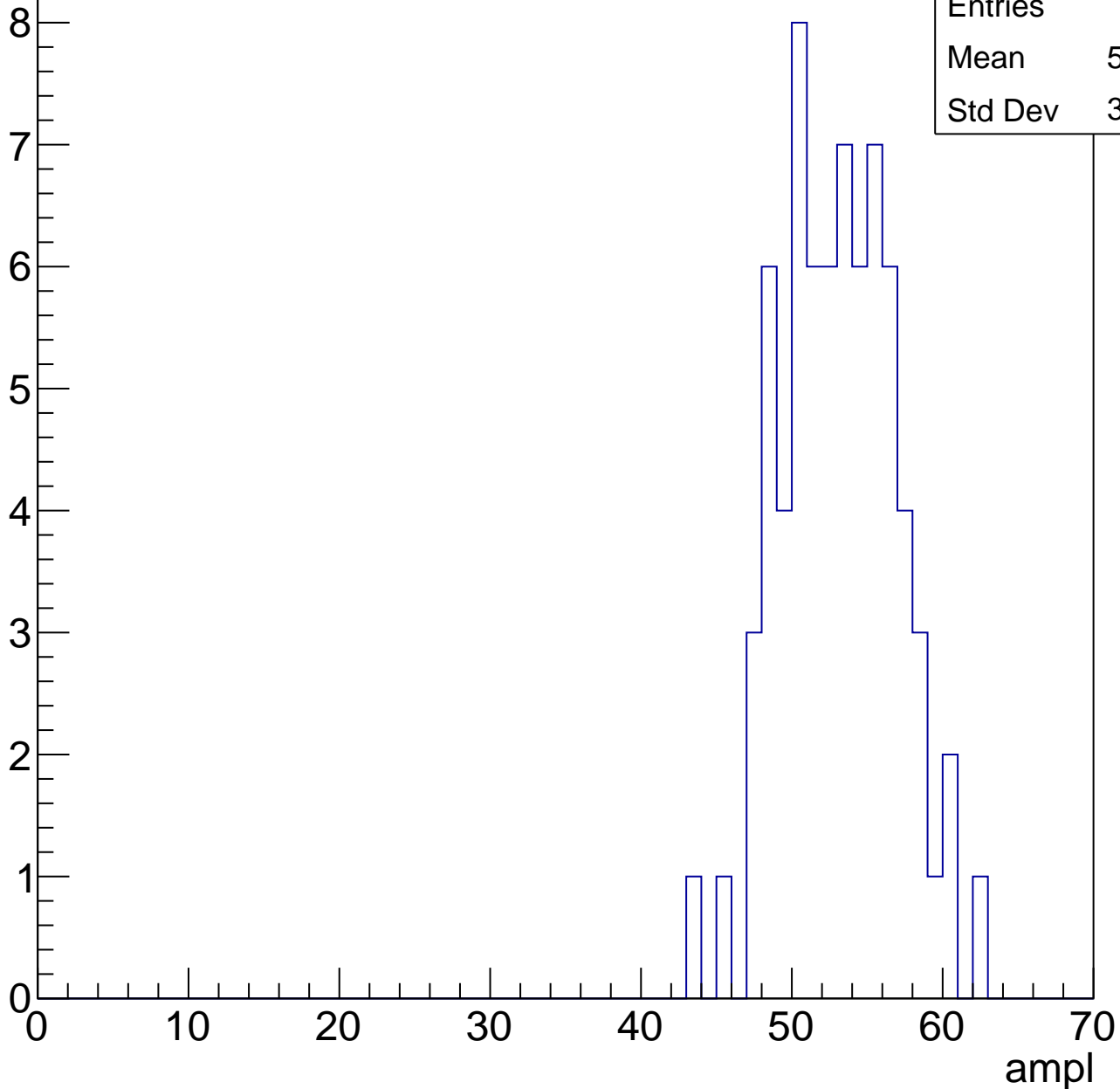


B0L002S, U26-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	52.64
Std Dev	3.765

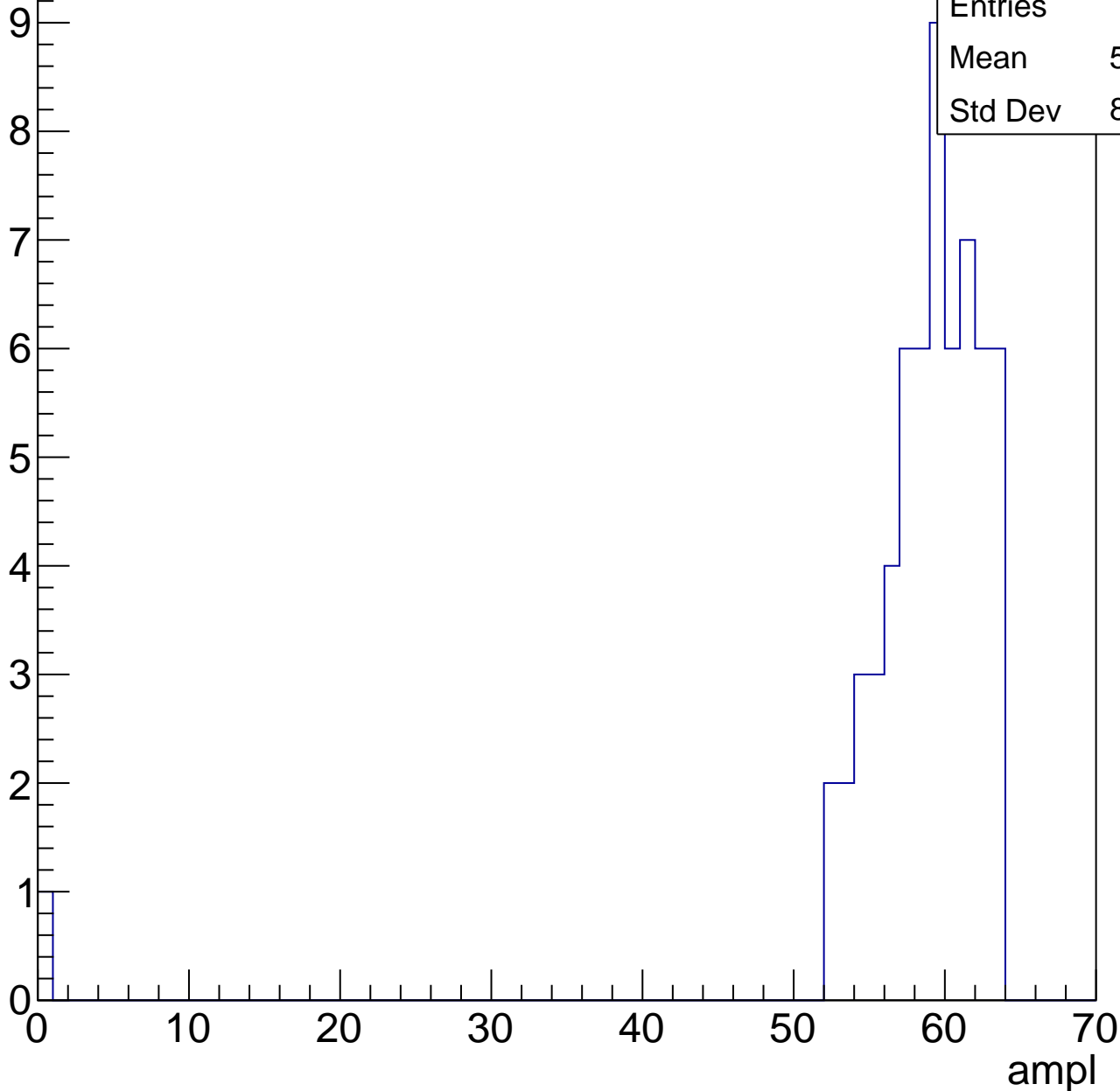


B0L002S, U26-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

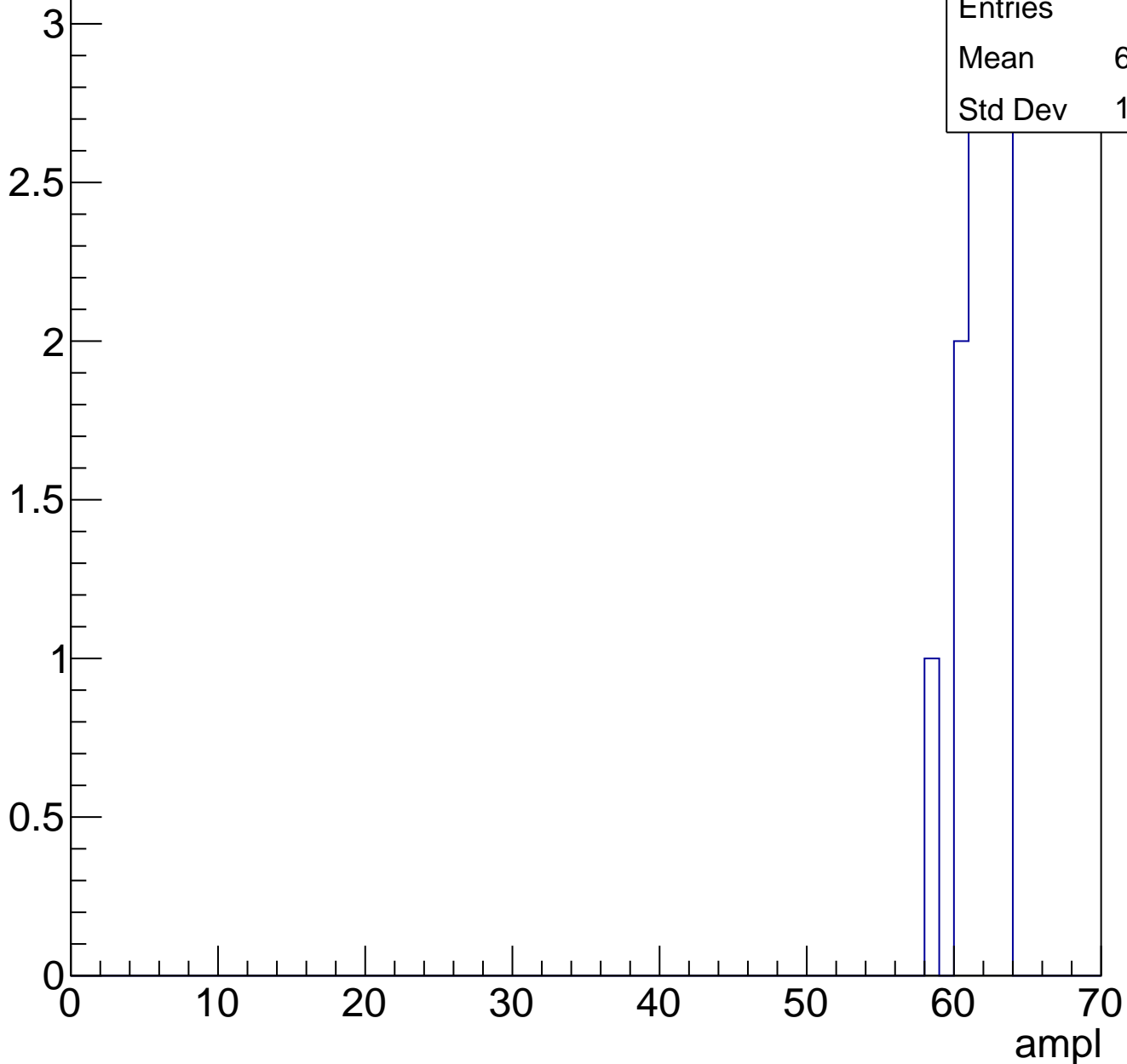
Entries	61
Mean	57.69
Std Dev	8.015



B0L002S, U26-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U26-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U26-ch104, adc0

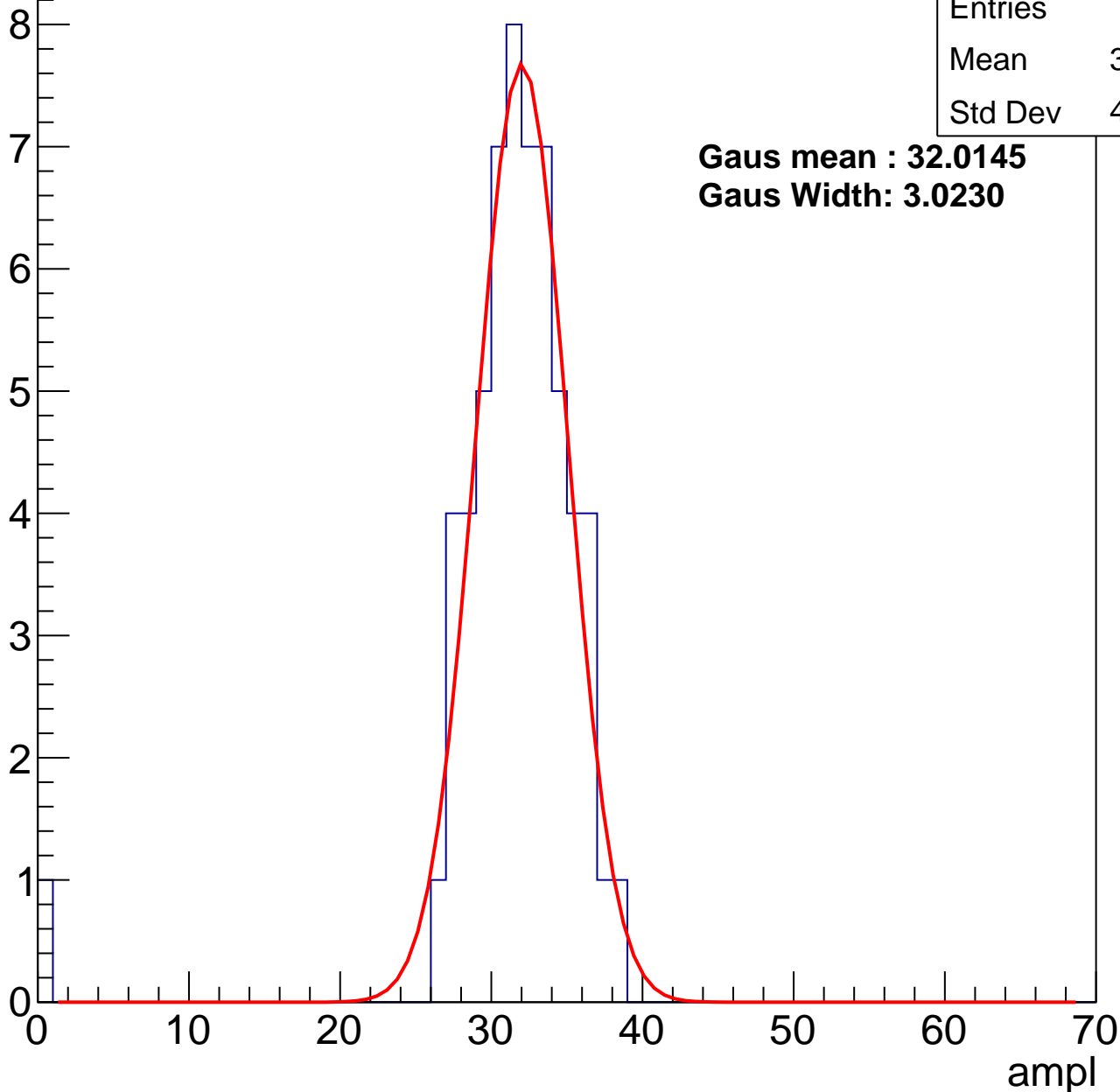
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	31.07
Std Dev	4.943

Gaus mean : 32.0145

Gaus Width: 3.0230



B0L002S, U26-ch104, adc1

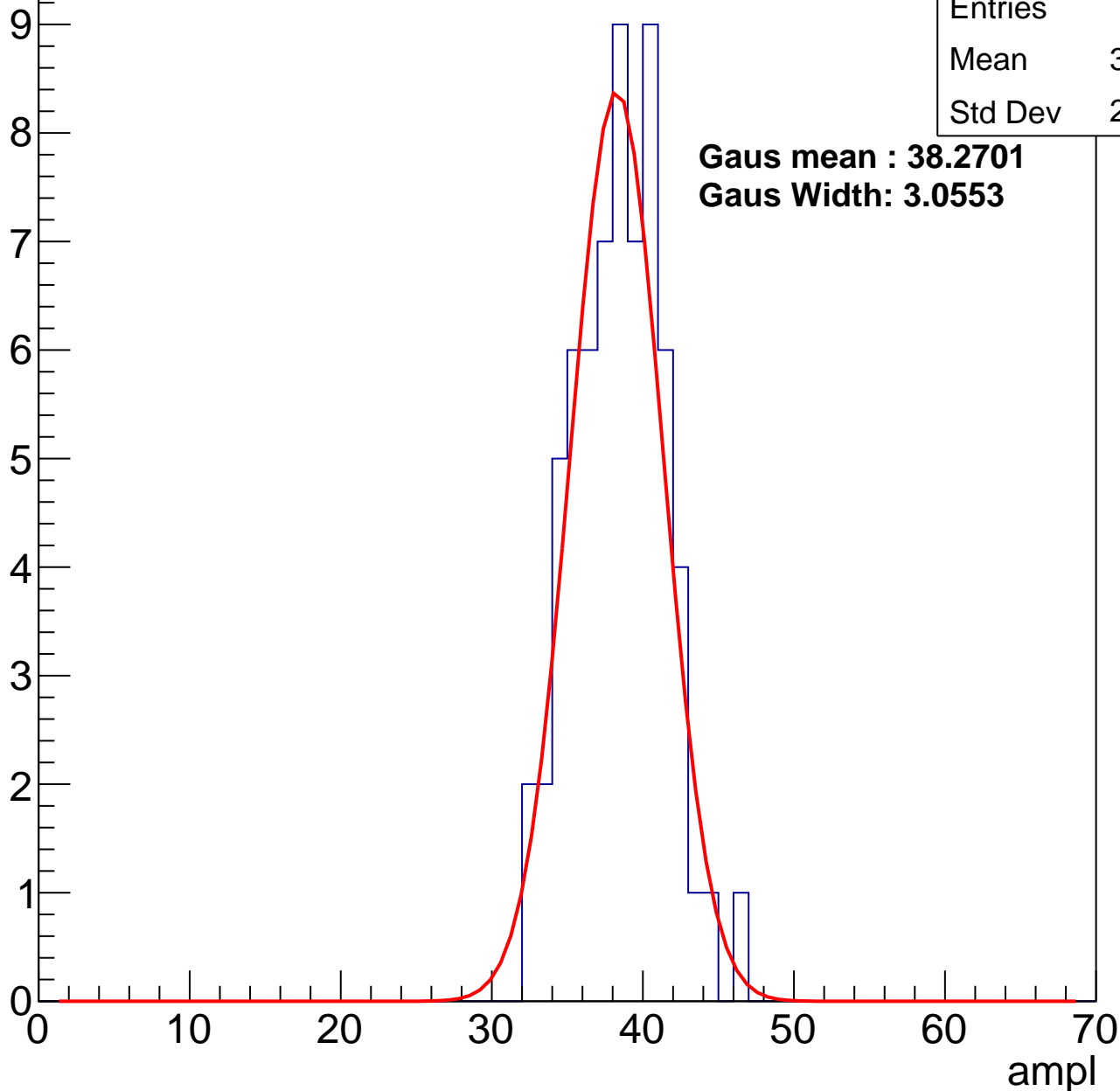
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.98
Std Dev	2.946

Gaus mean : 38.2701

Gaus Width: 3.0553



B0L002S, U26-ch104, adc2

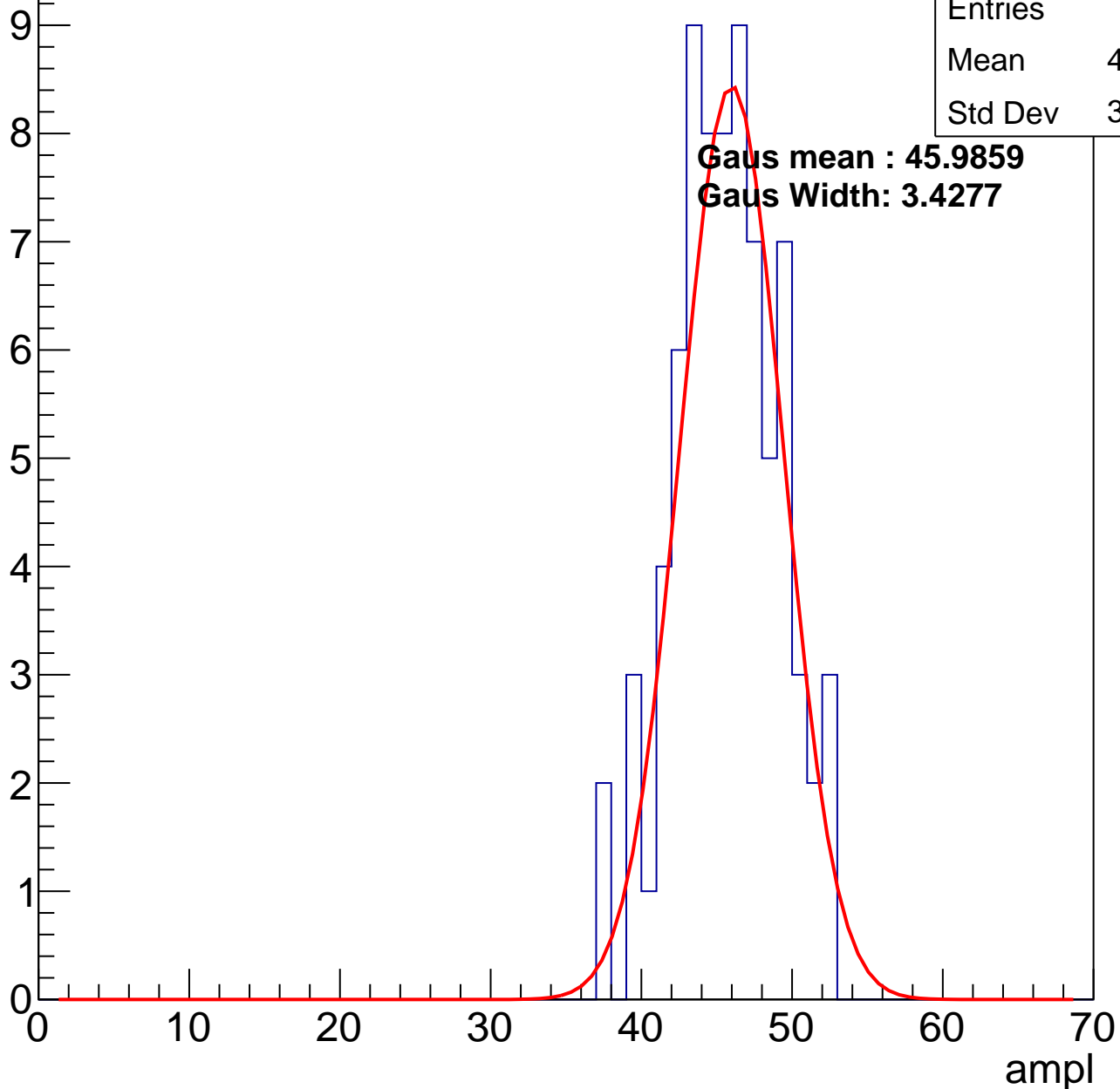
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	45.19
Std Dev	3.434

Gaus mean : 45.9859

Gaus Width: 3.4277

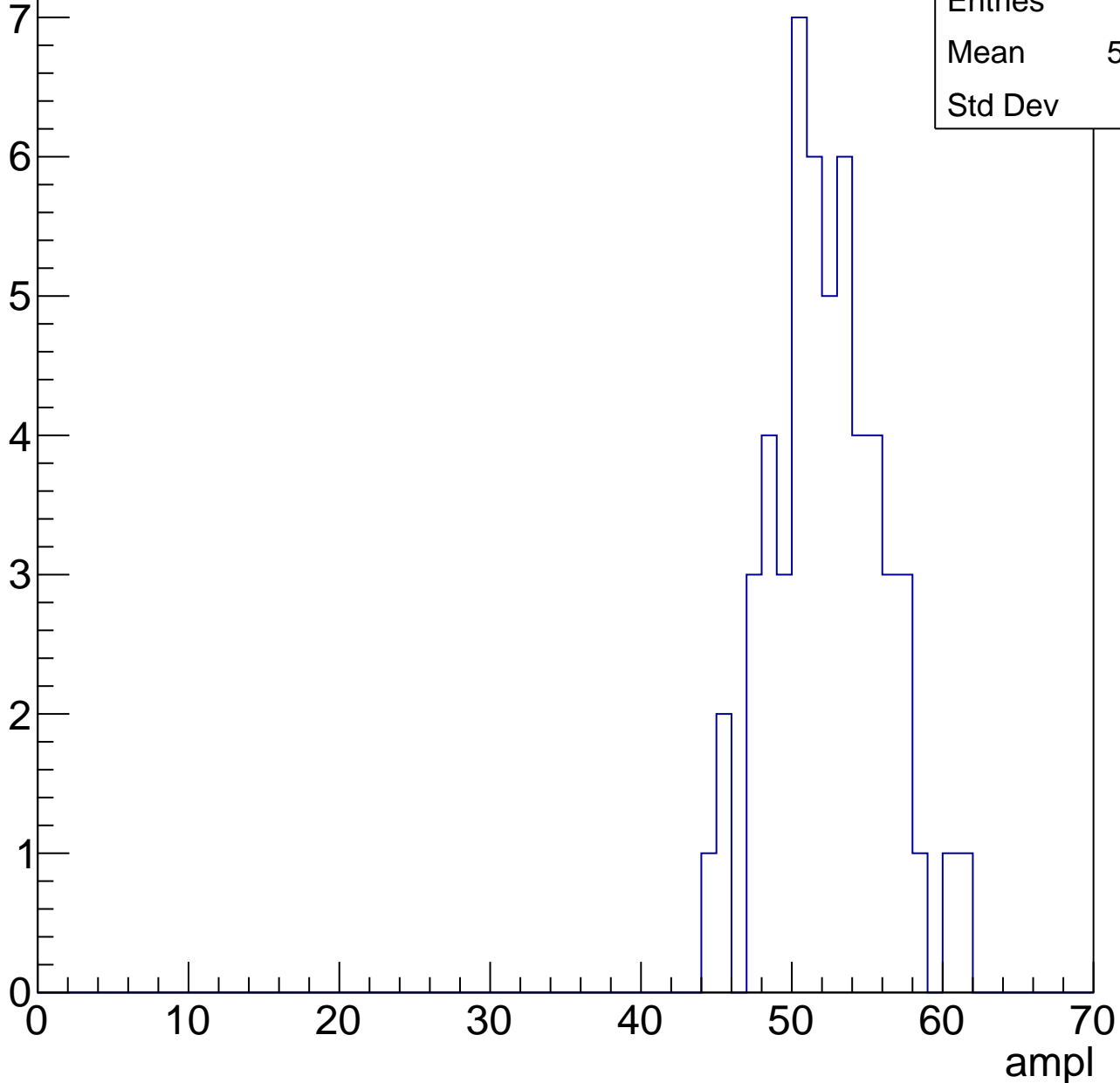


B0L002S, U26-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	51.89
Std Dev	3.66

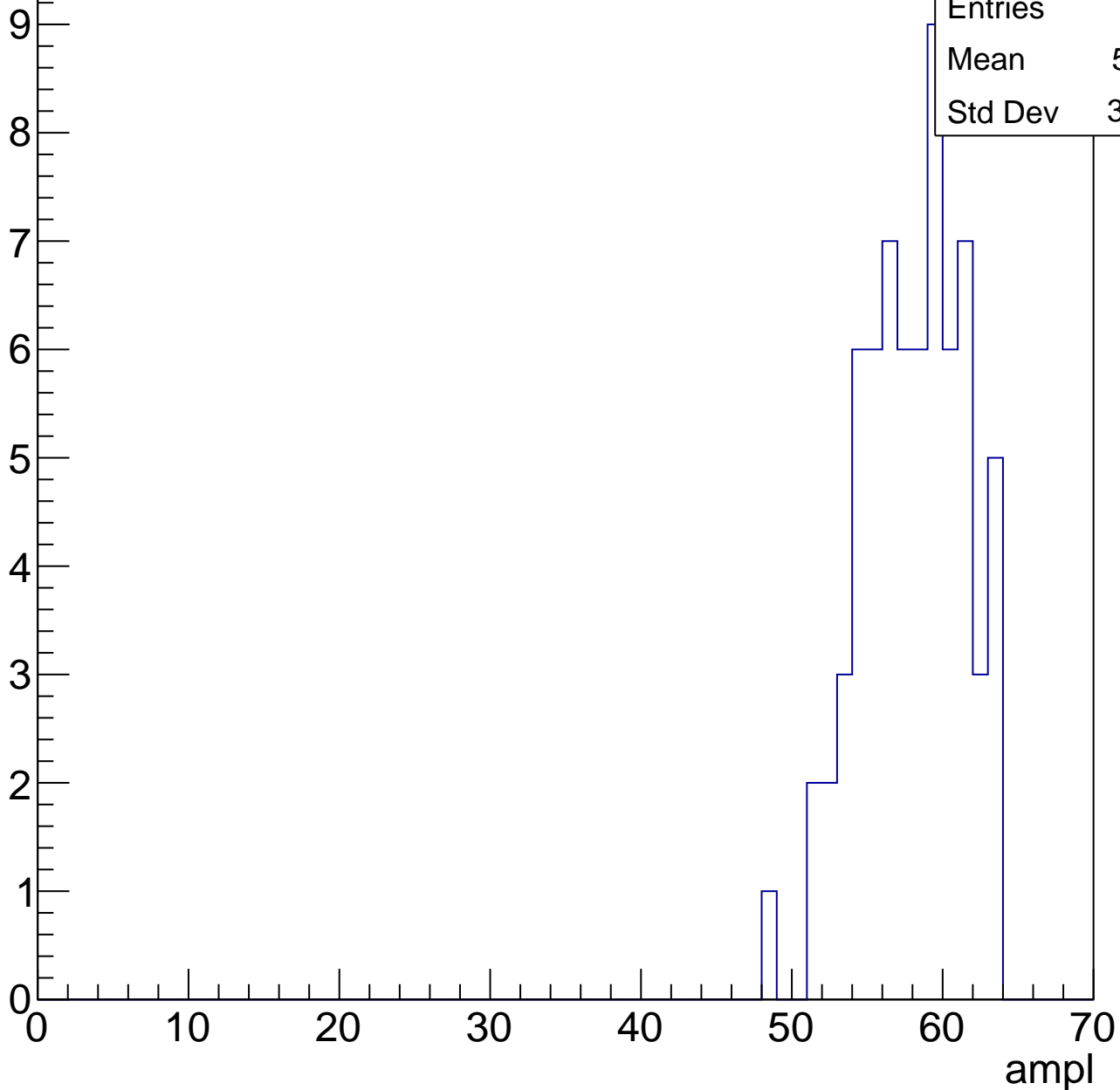


B0L002S, U26-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	57.51
Std Dev	3.365

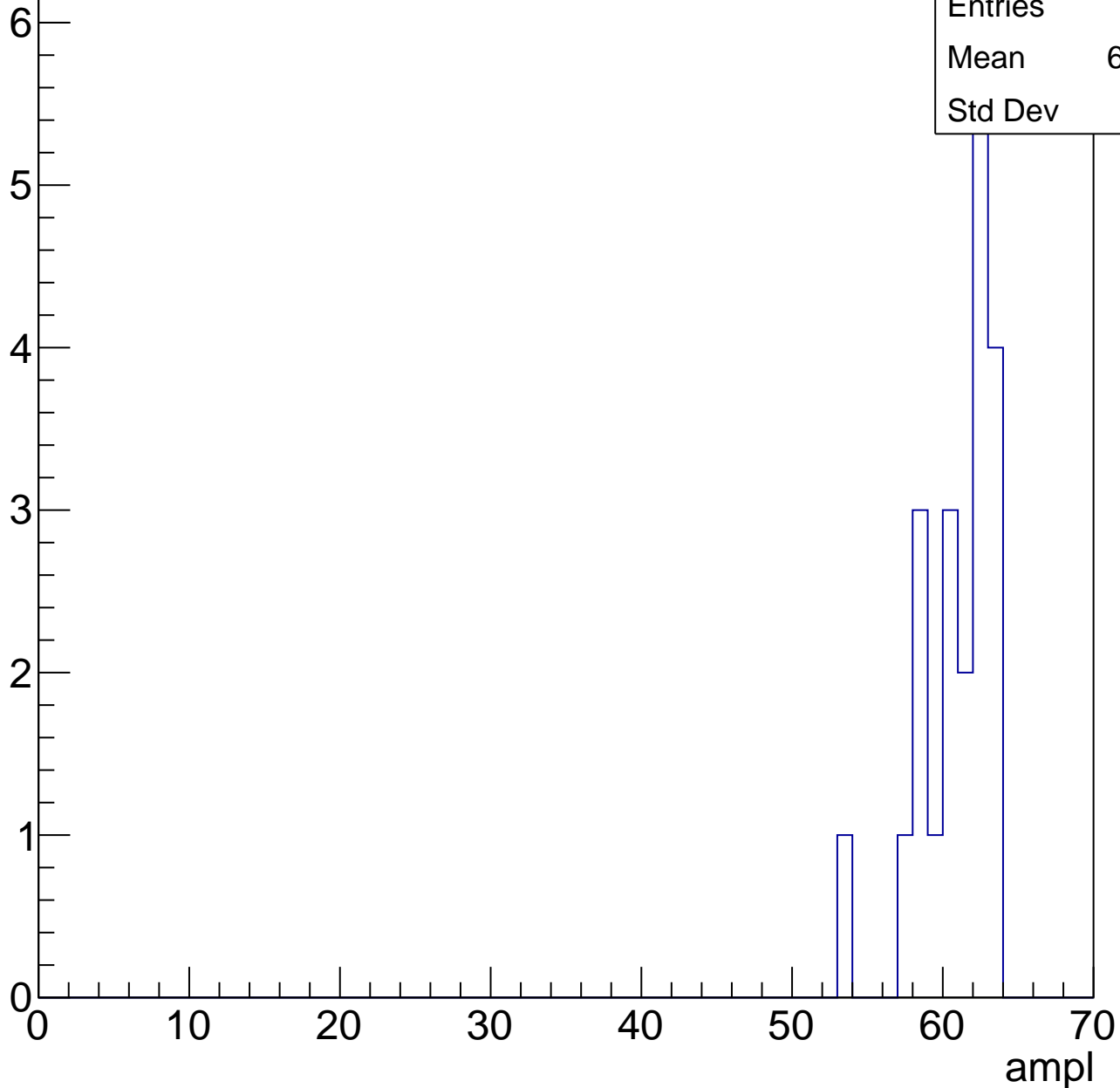


B0L002S, U26-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	60.43
Std Dev	2.48



B0L002S, U26-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl

B0L002S, U26-ch105, adc0

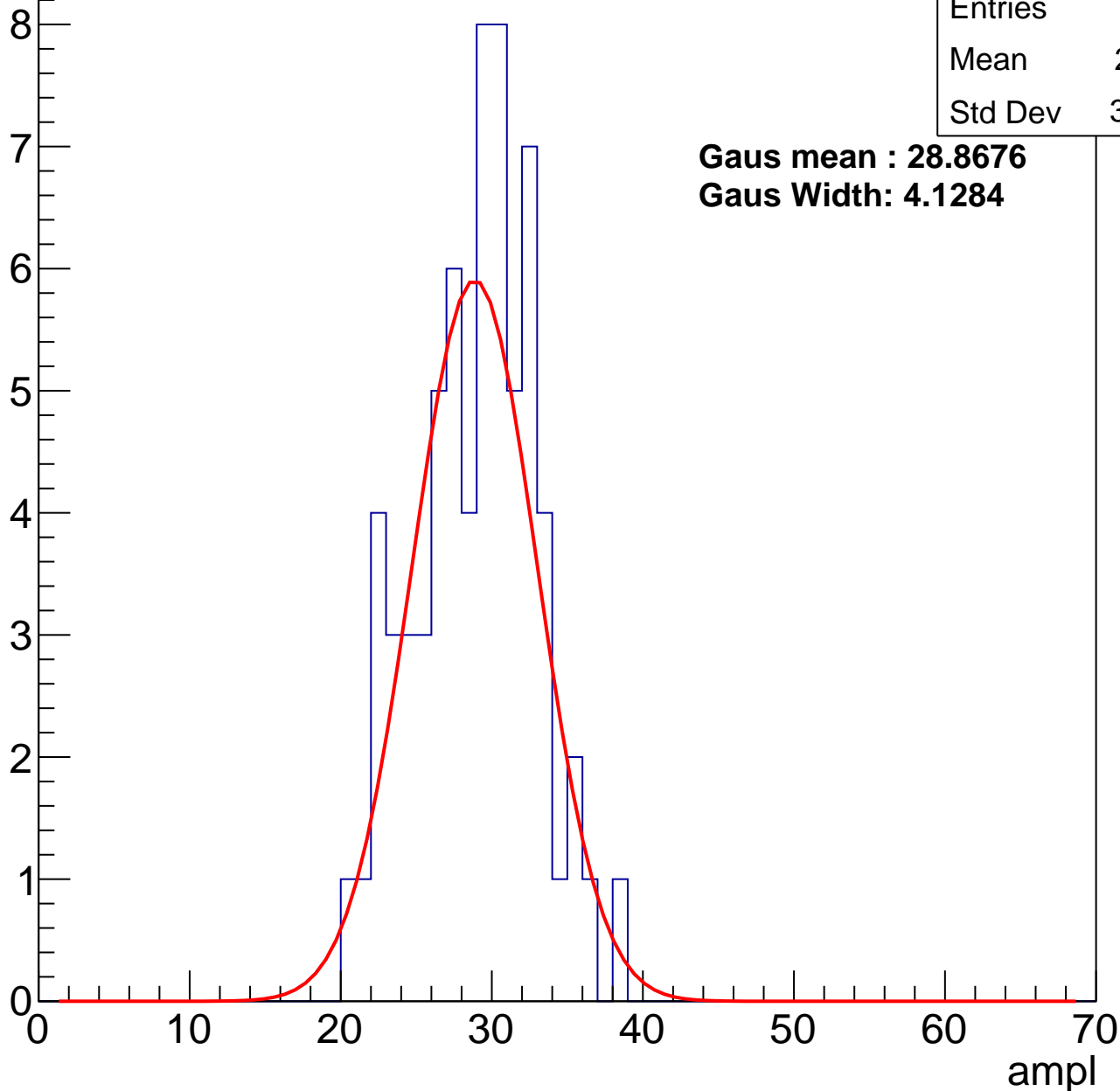
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	28.51
Std Dev	3.884

Gaus mean : 28.8676

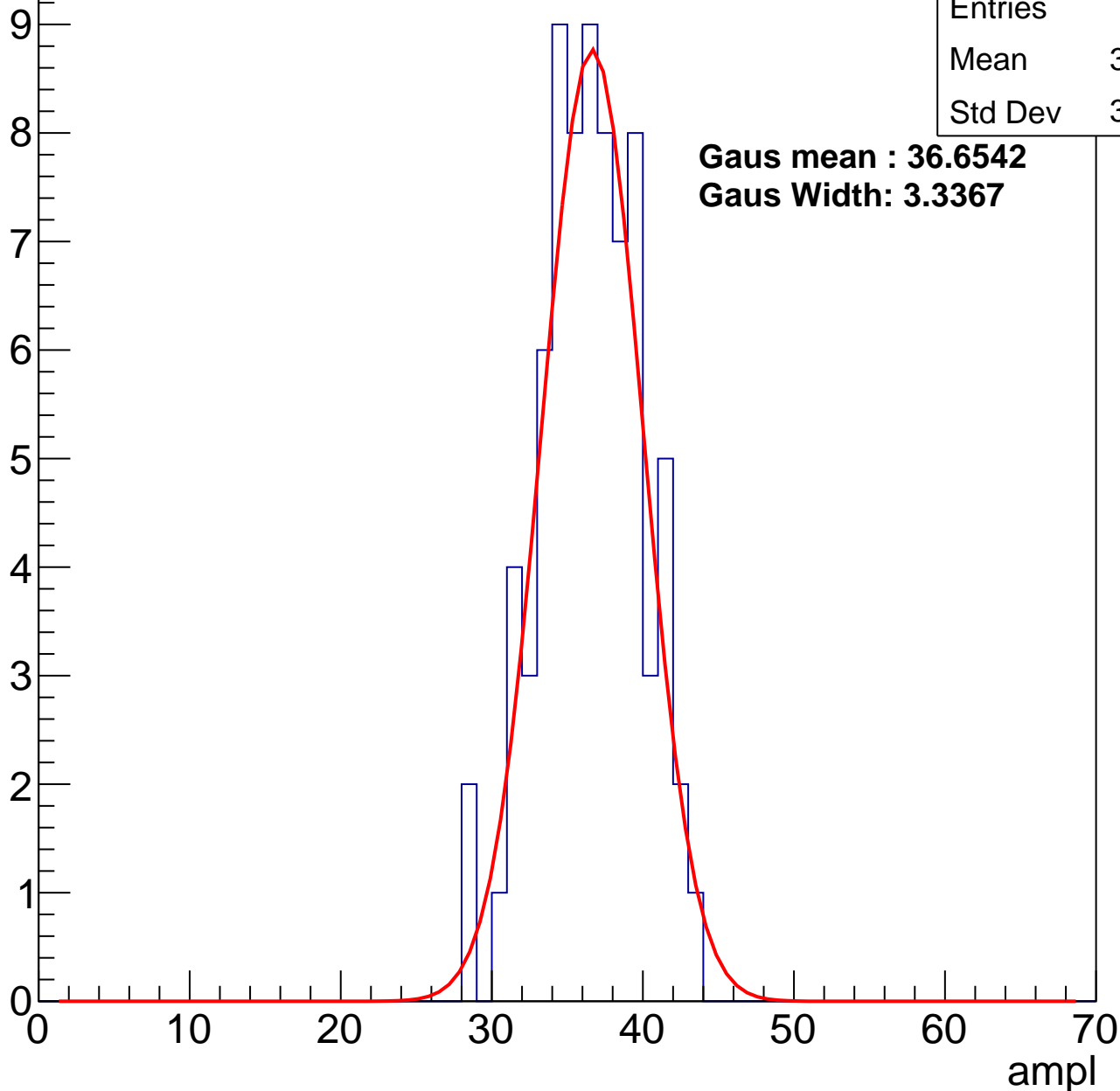
Gaus Width: 4.1284



B0L002S, U26-ch105, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	76
Mean	36.05
Std Dev	3.268

B0L002S, U26-ch105, adc2

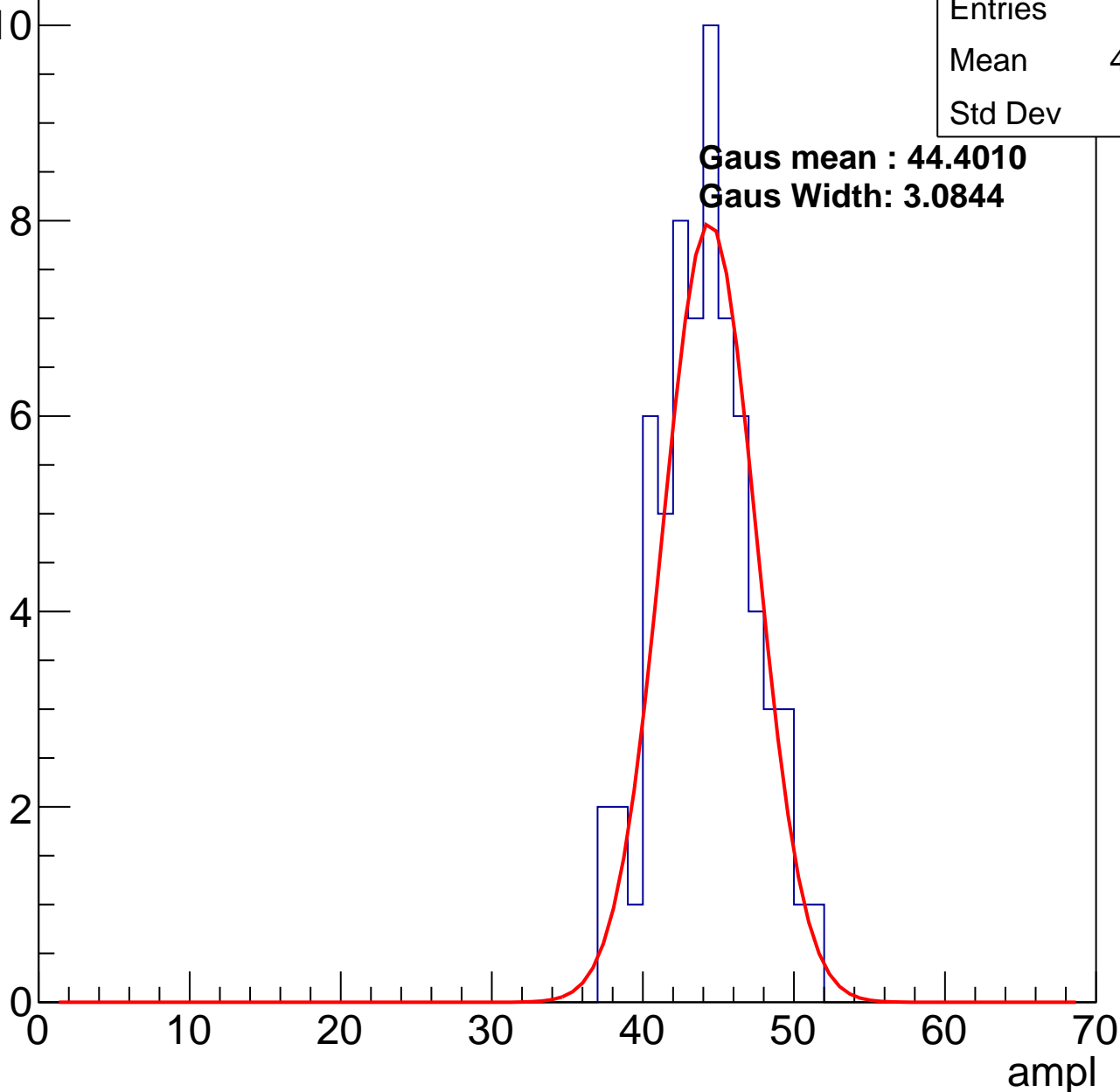
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.67
Std Dev	3.12

Gaus mean : 44.4010

Gaus Width: 3.0844

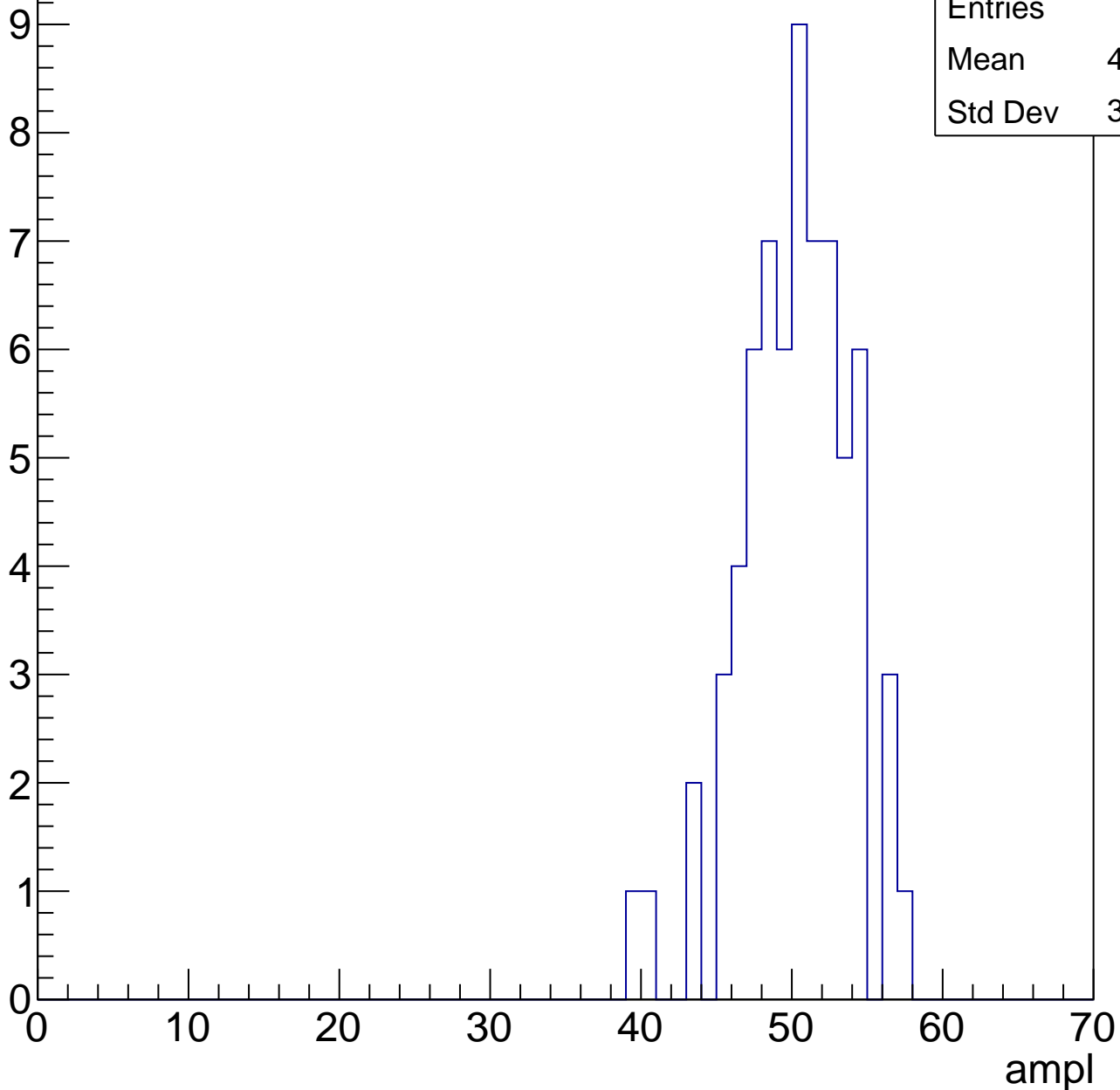


B0L002S, U26-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	49.72
Std Dev	3.584

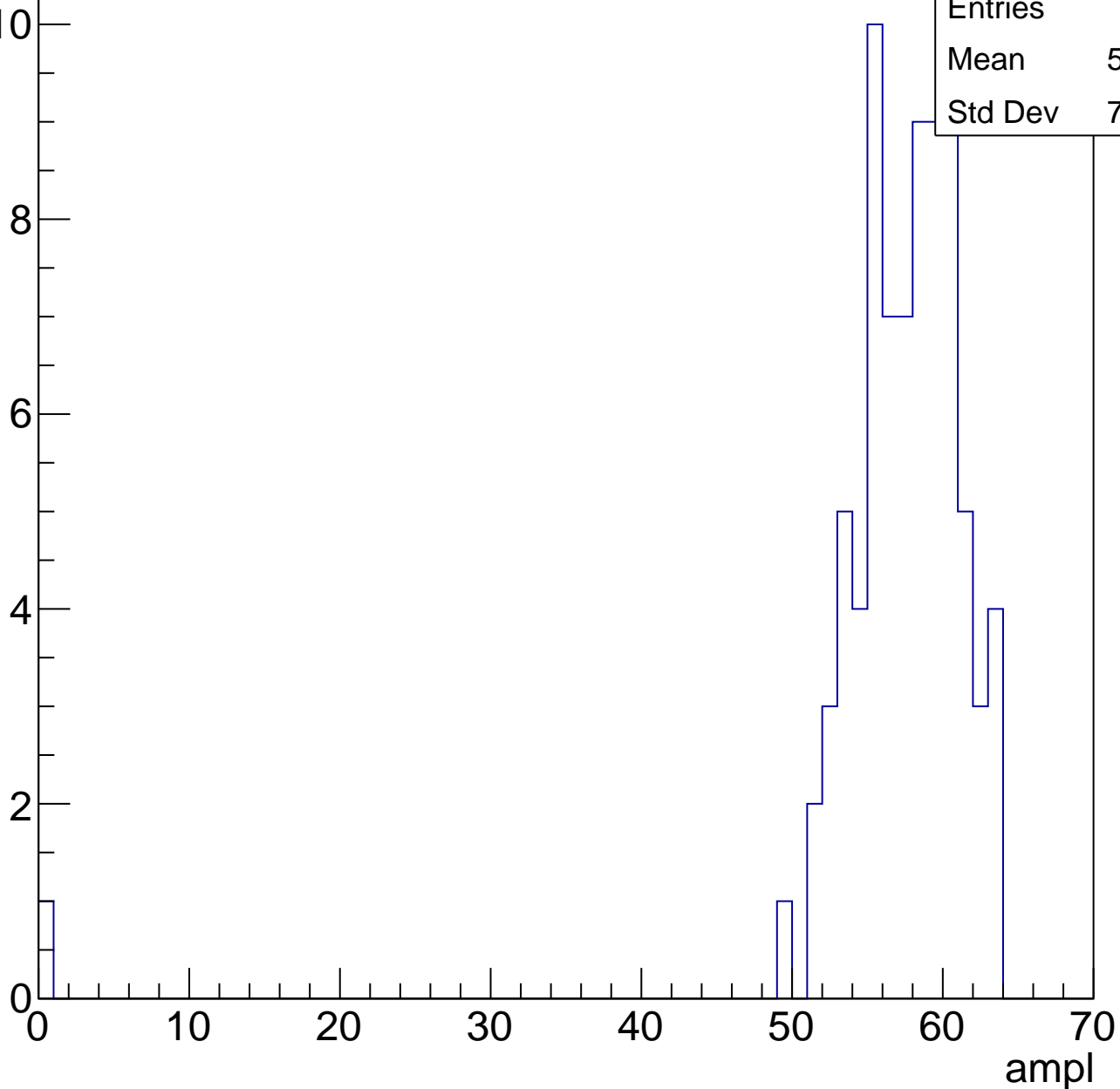


B0L002S, U26-ch105, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	56.52
Std Dev	7.142

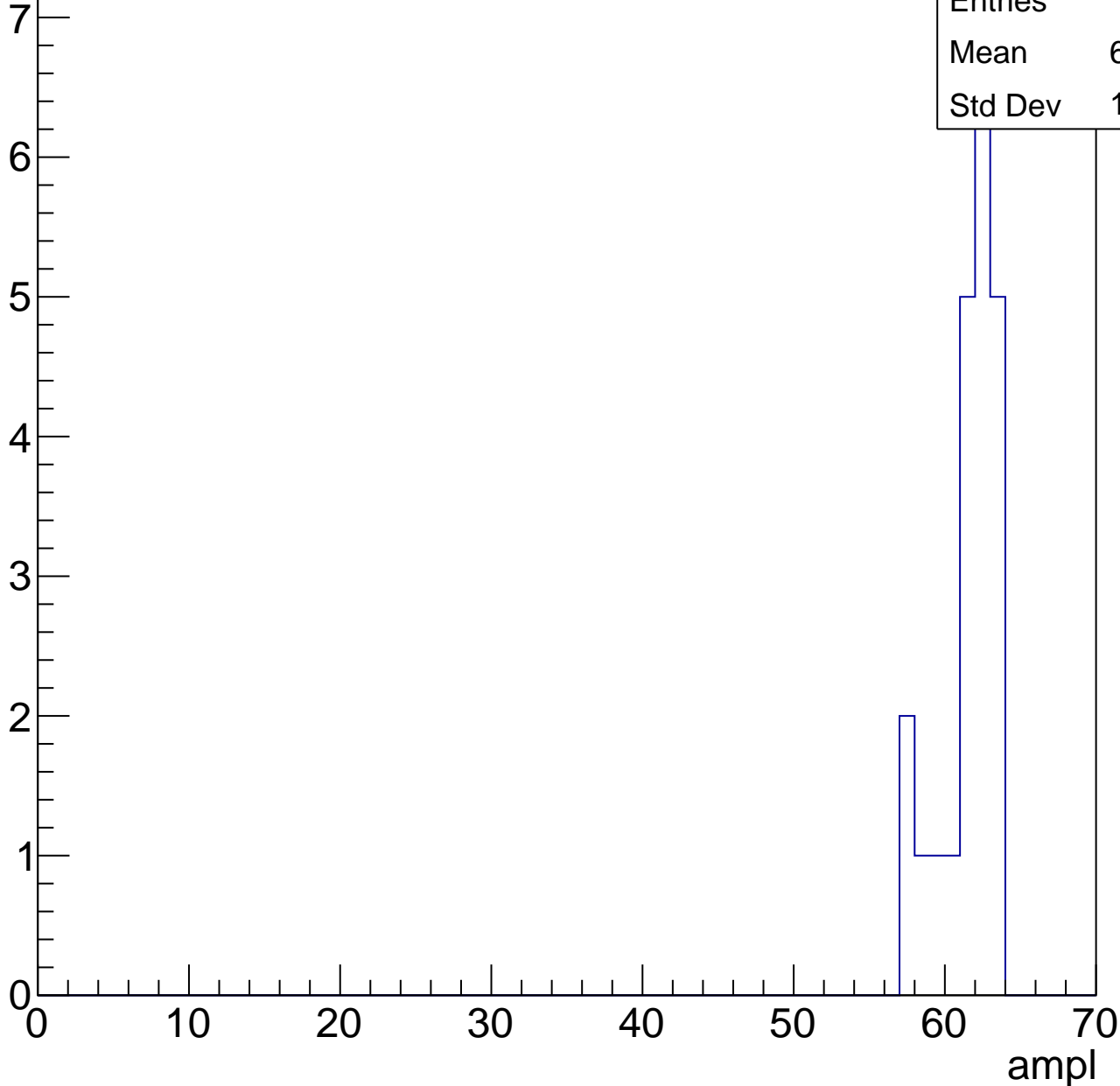


B0L002S, U26-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	61.14
Std Dev	1.816



B0L002S, U26-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch106, adc0

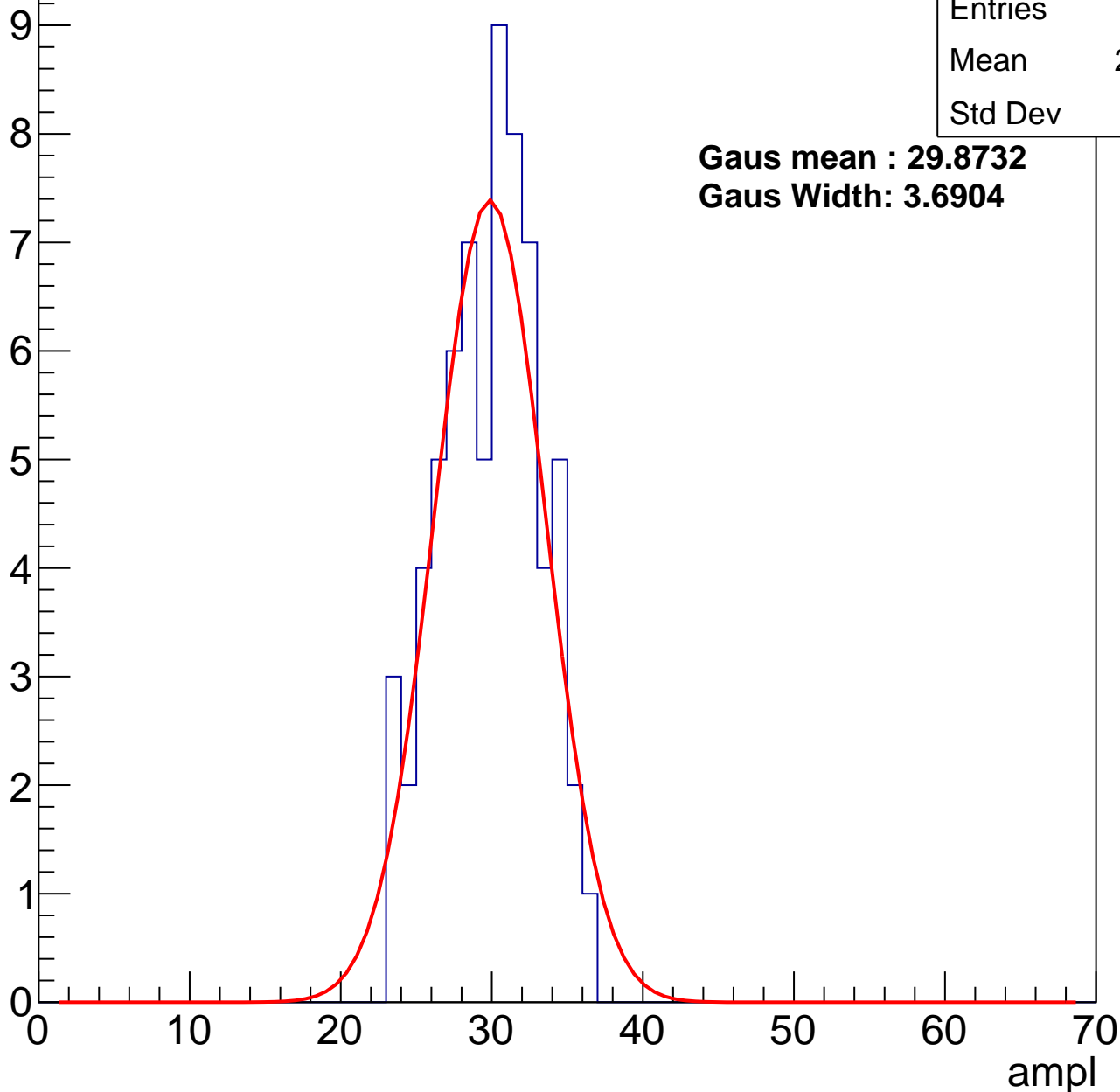
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.41
Std Dev	3.21

Gaus mean : 29.8732

Gaus Width: 3.6904



B0L002S, U26-ch106, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	36.55
Std Dev	3.562

Gaus mean : 36.7536

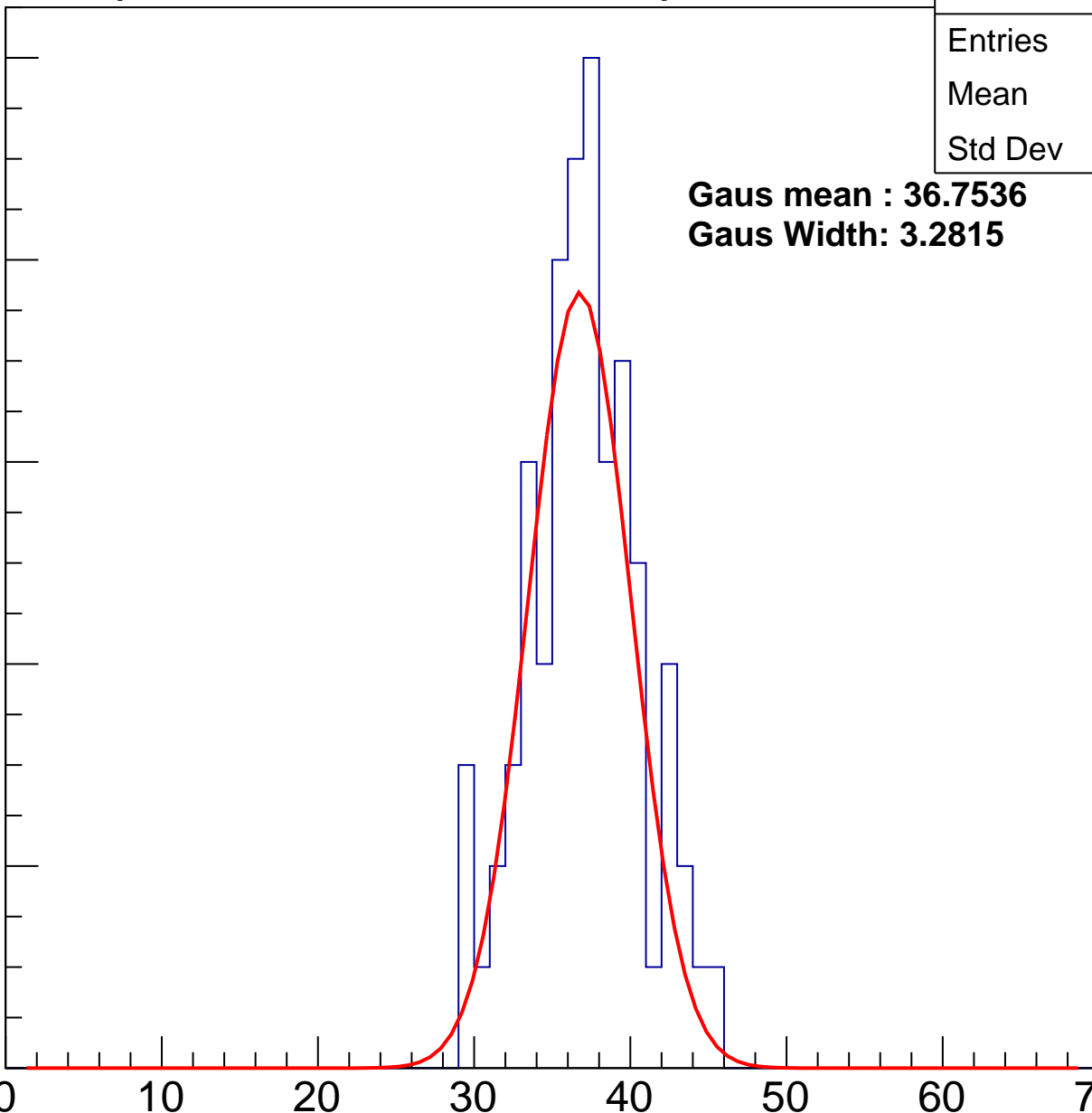
Gaus Width: 3.2815

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch106, adc2

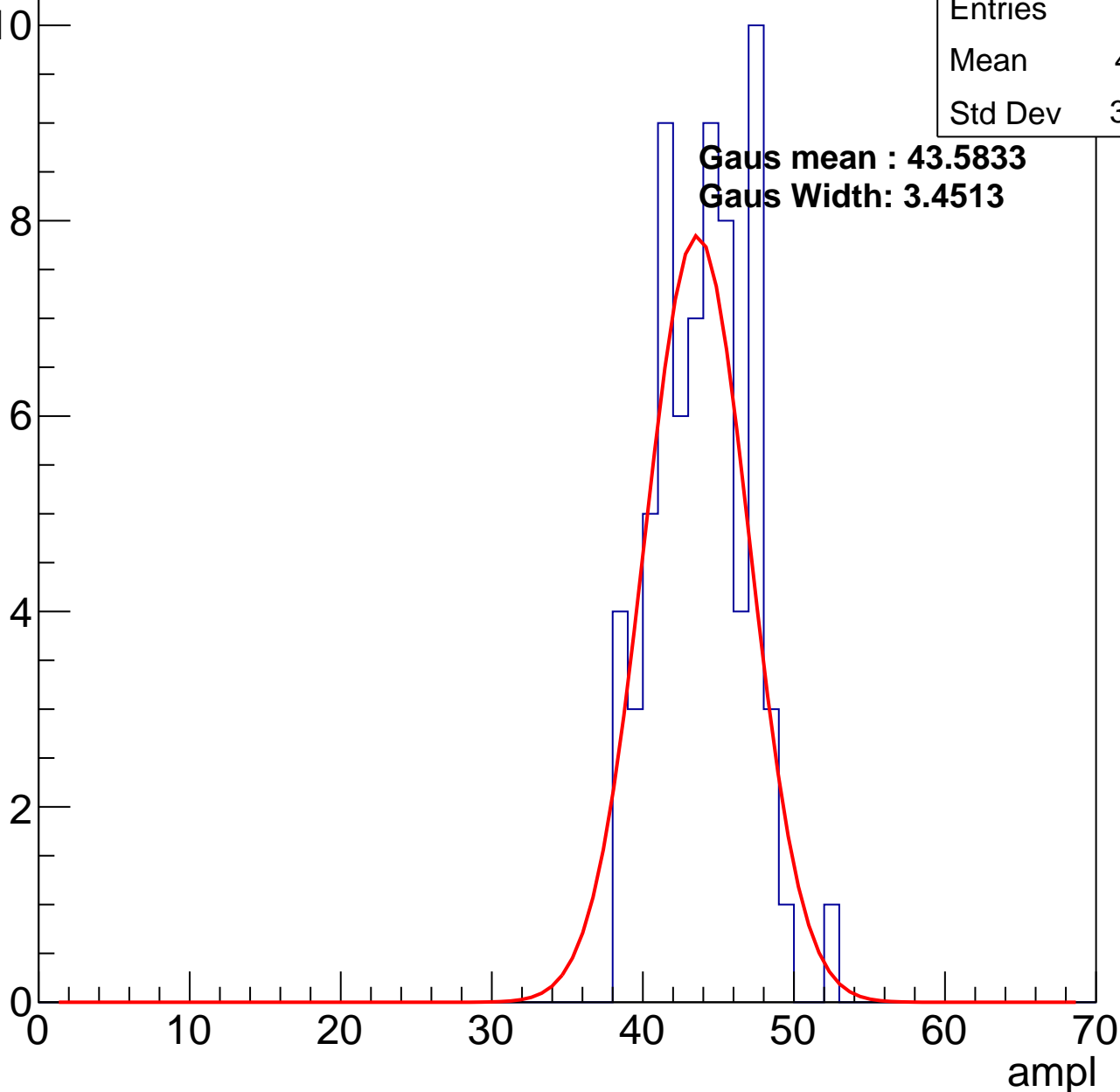
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.51
Std Dev	3.037

Gaus mean : 43.5833

Gaus Width: 3.4513

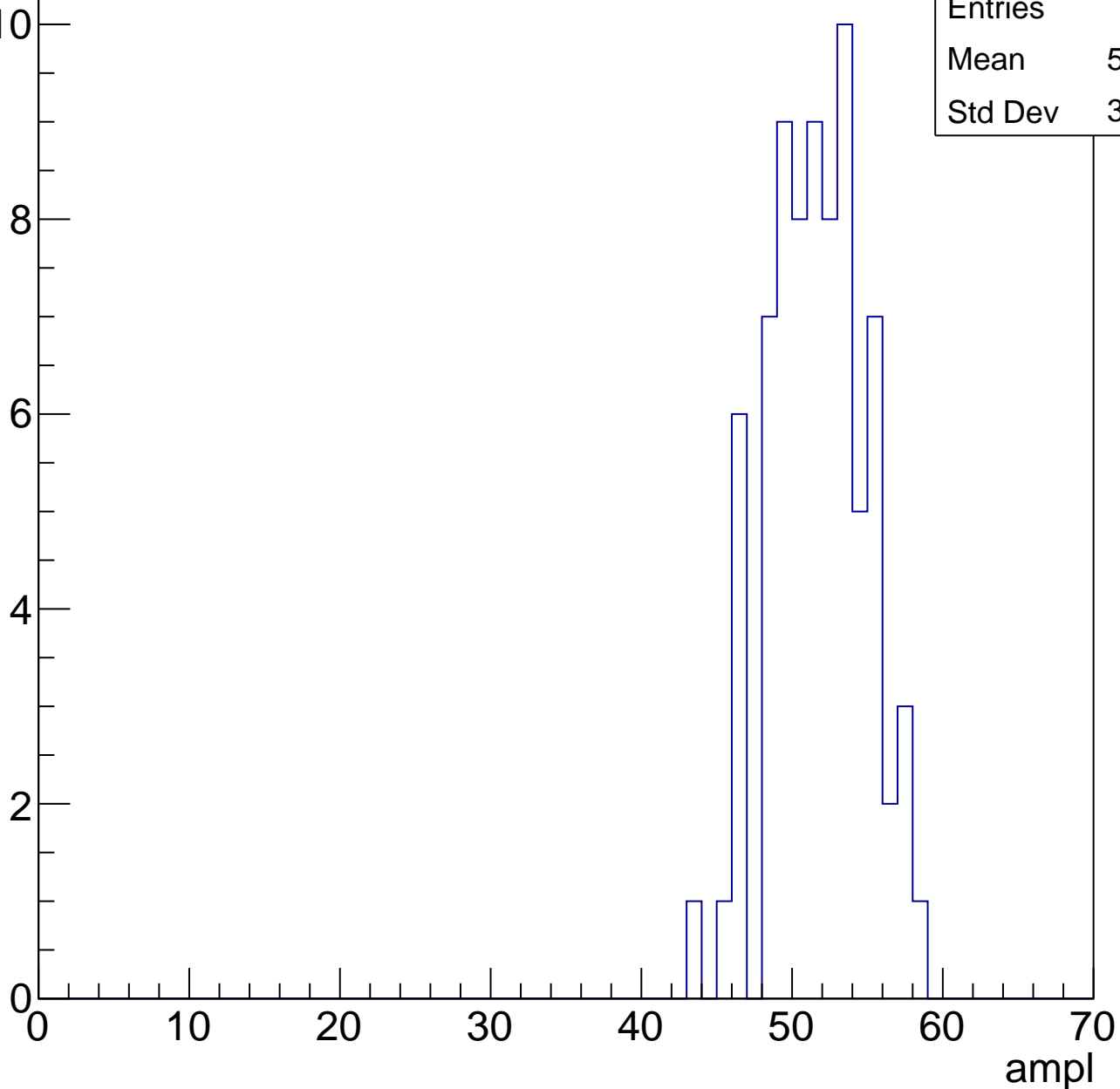


B0L002S, U26-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	51.19
Std Dev	3.154

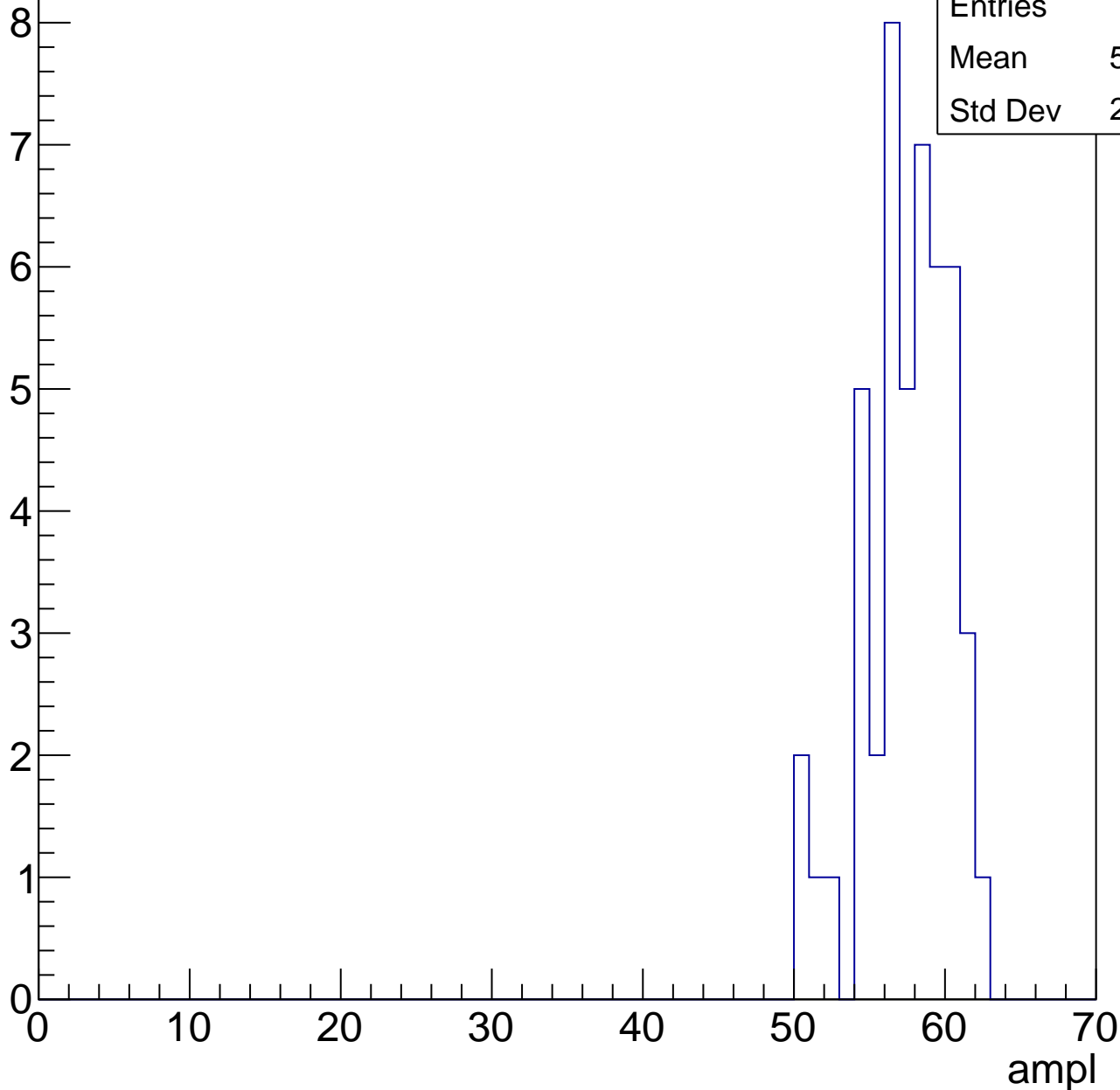


B0L002S, U26-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	57.04
Std Dev	2.836

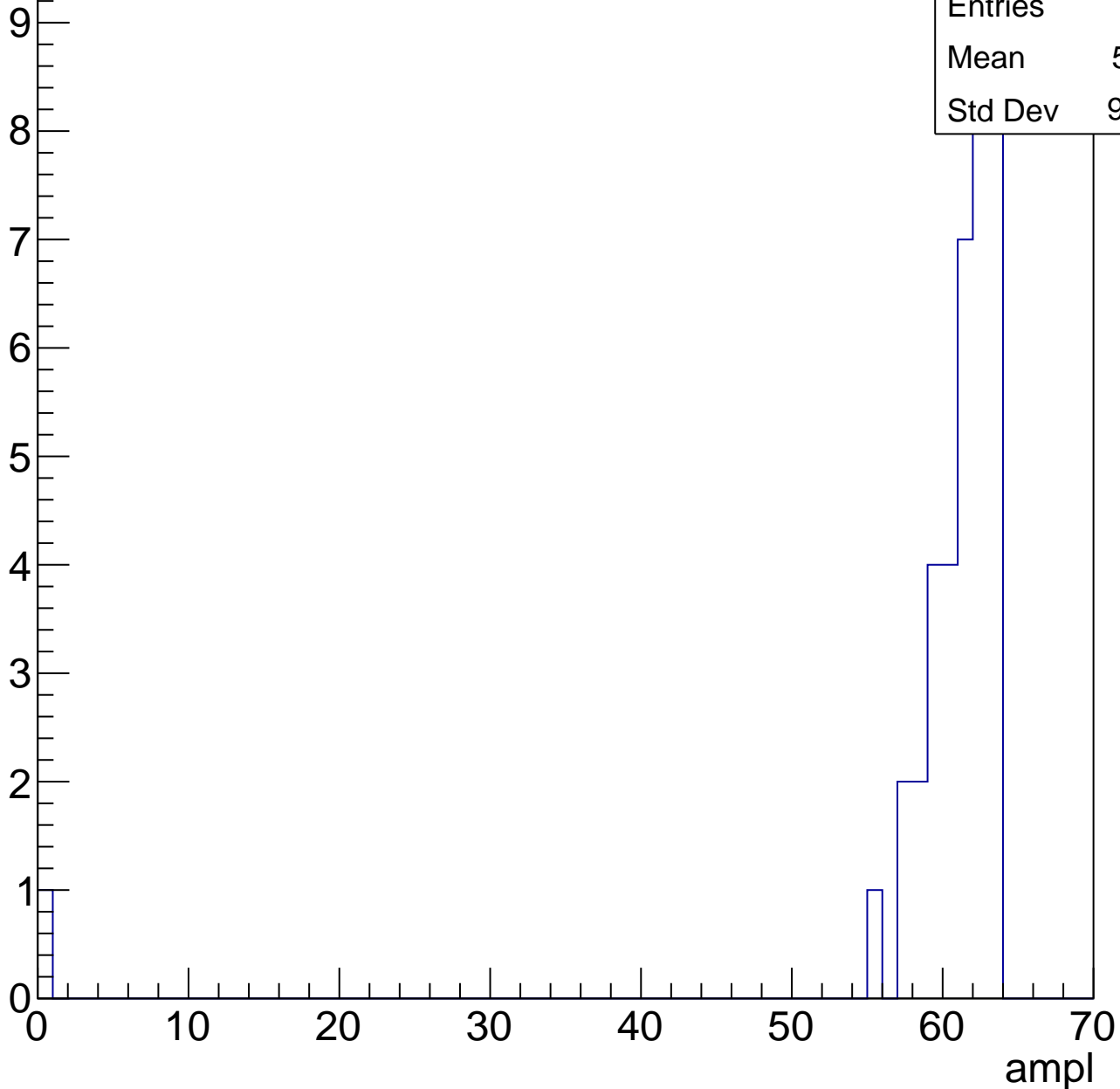


B0L002S, U26-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.31
Std Dev	9.819



B0L002S, U26-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L002S, U26-ch107, adc0

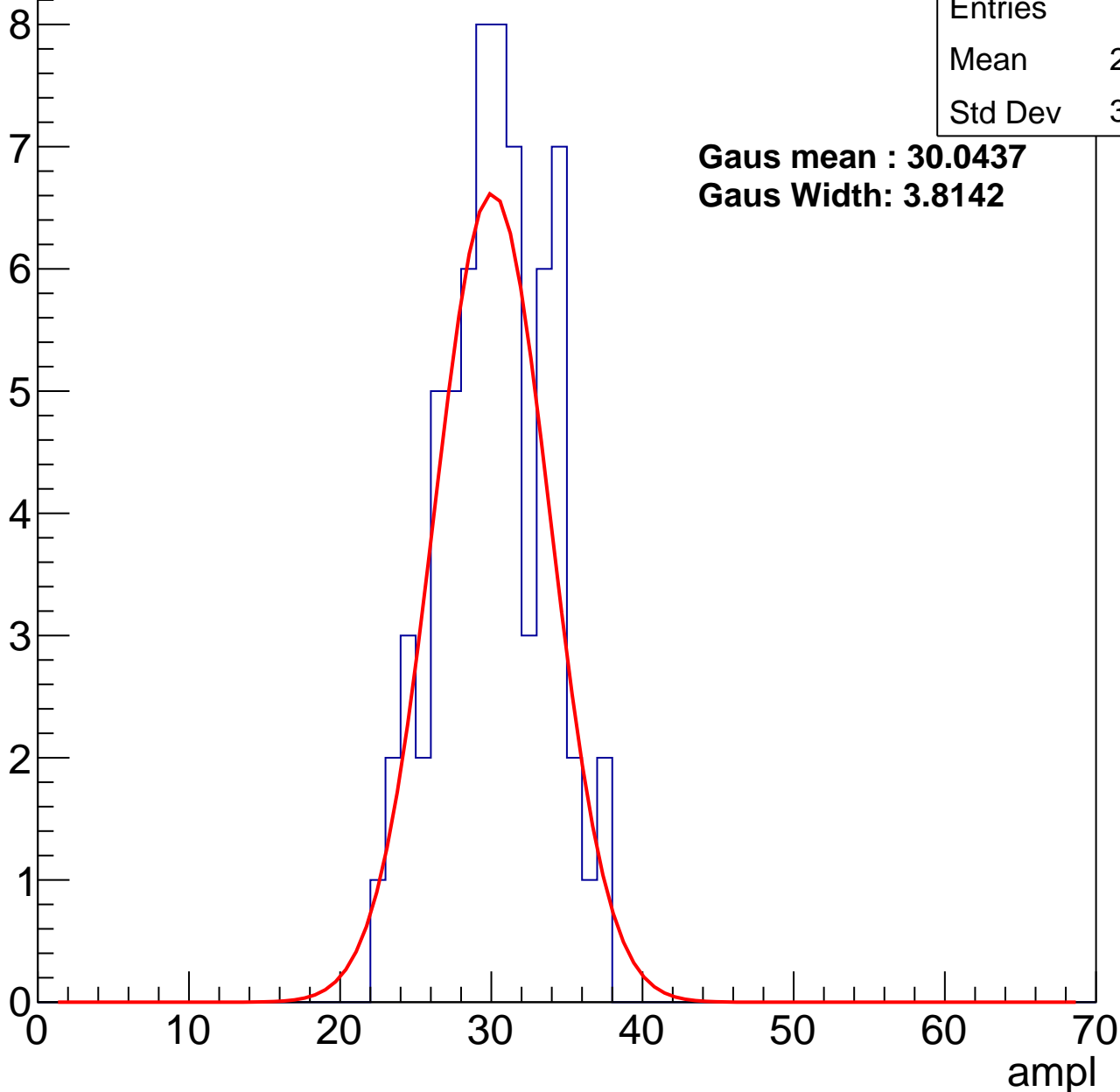
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.76
Std Dev	3.519

Gaus mean : 30.0437

Gaus Width: 3.8142



B0L002S, U26-ch107, adc1

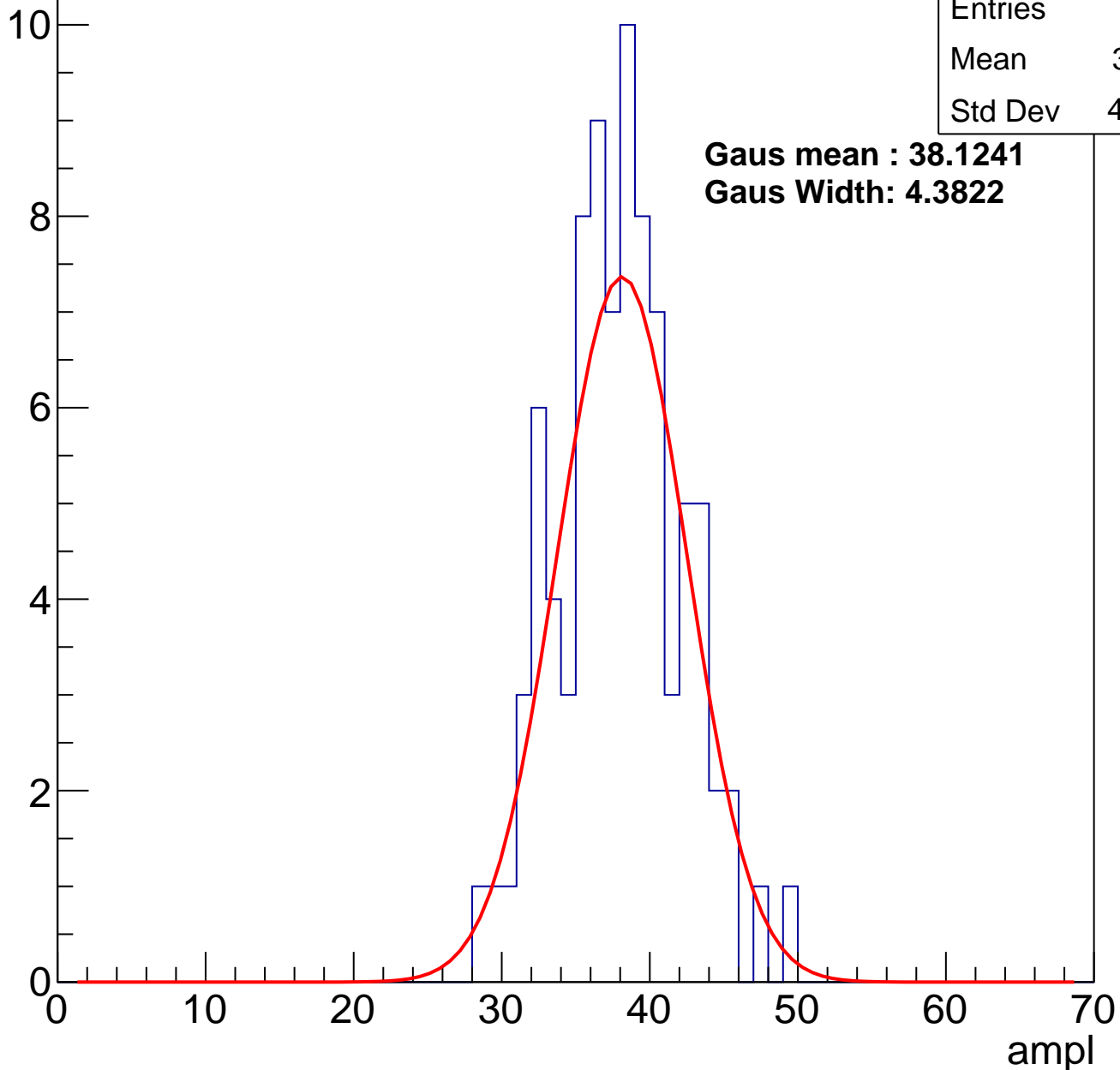
calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	37.51
Std Dev	4.138

Gaus mean : 38.1241

Gaus Width: 4.3822

Entry



B0L002S, U26-ch107, adc2

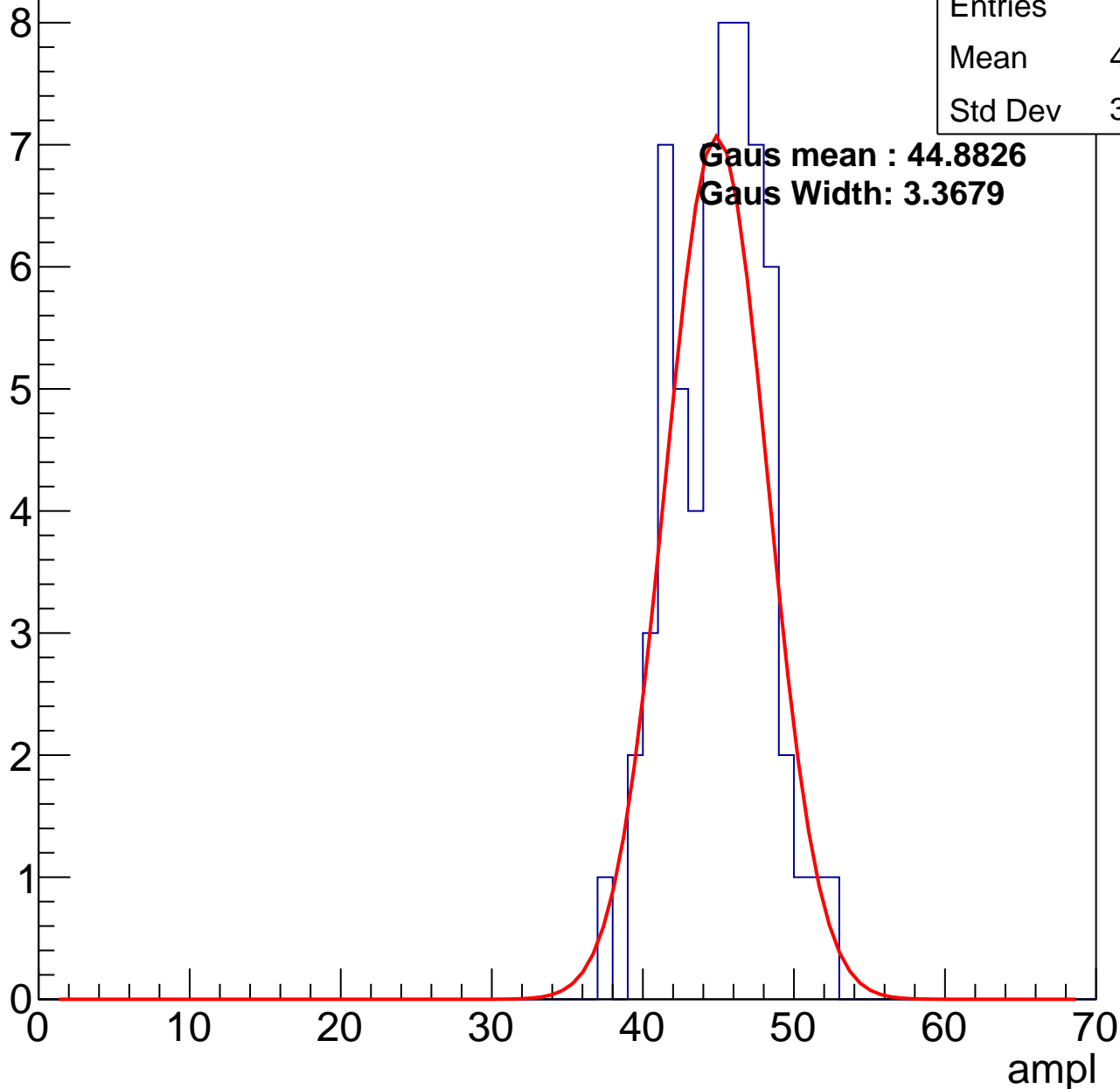
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.57
Std Dev	3.115

Gaus mean : 44.8826

Gaus Width: 3.3679

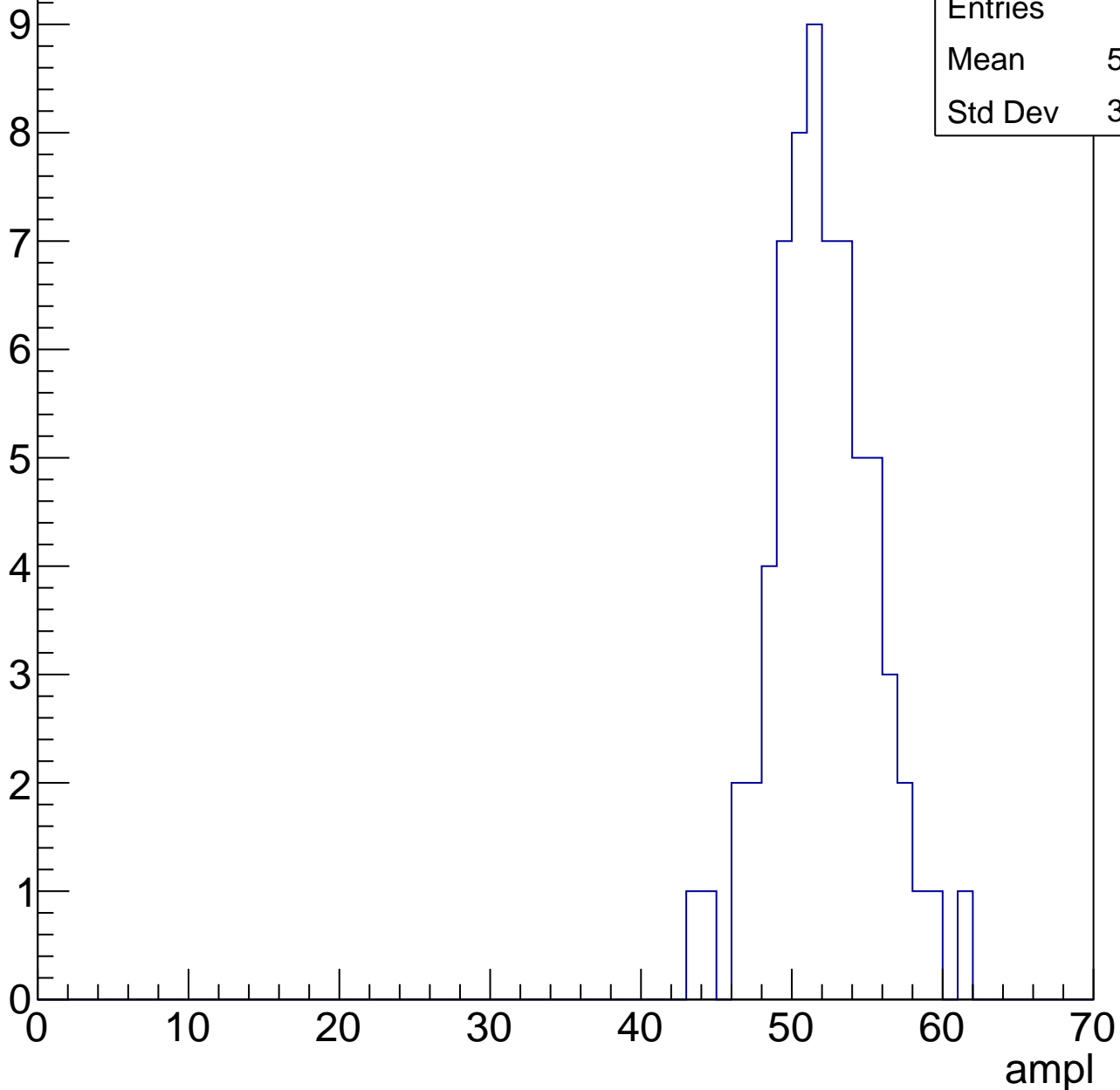


B0L002S, U26-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

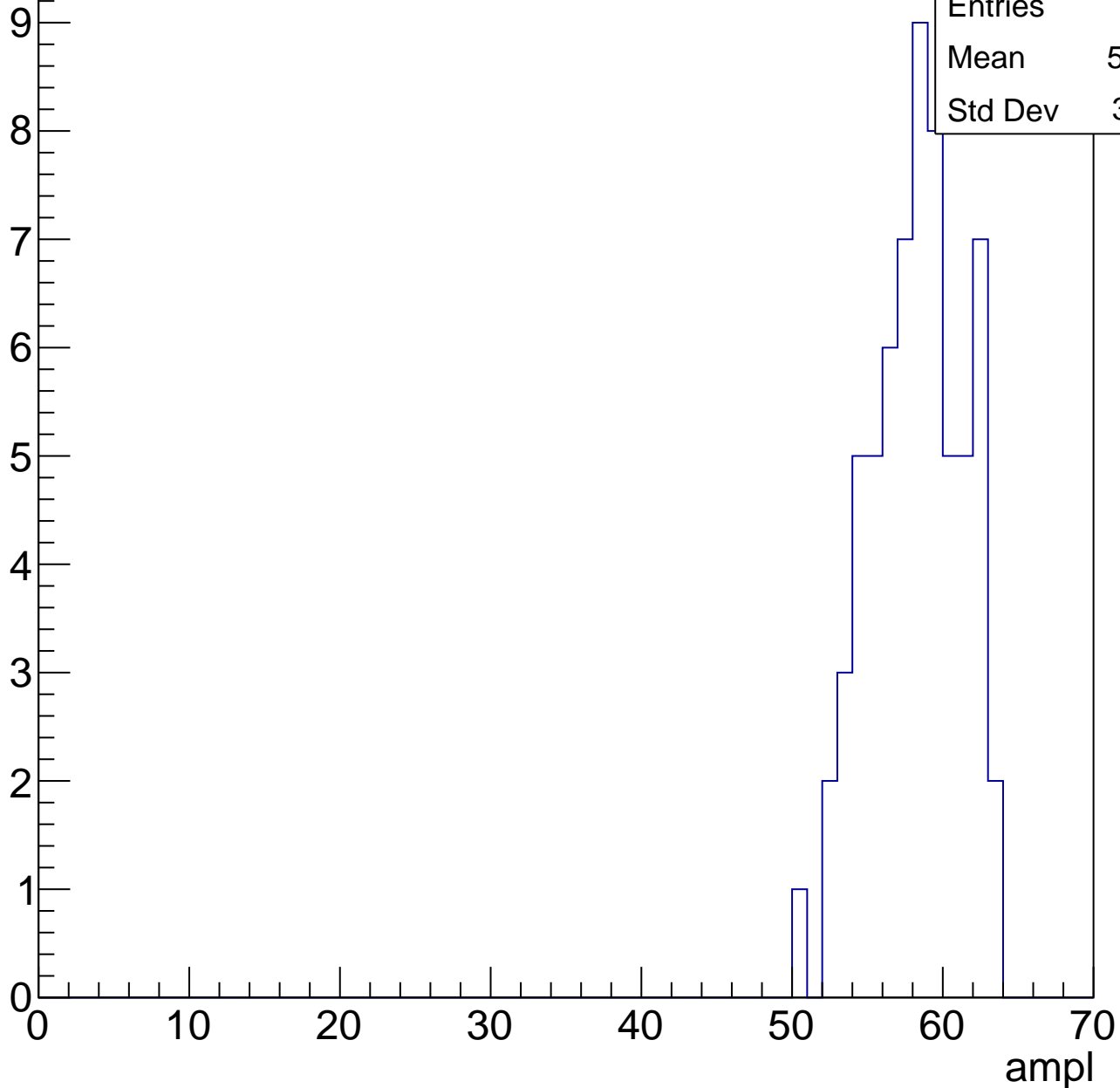
Entries	66
Mean	51.62
Std Dev	3.397



B0L002S, U26-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

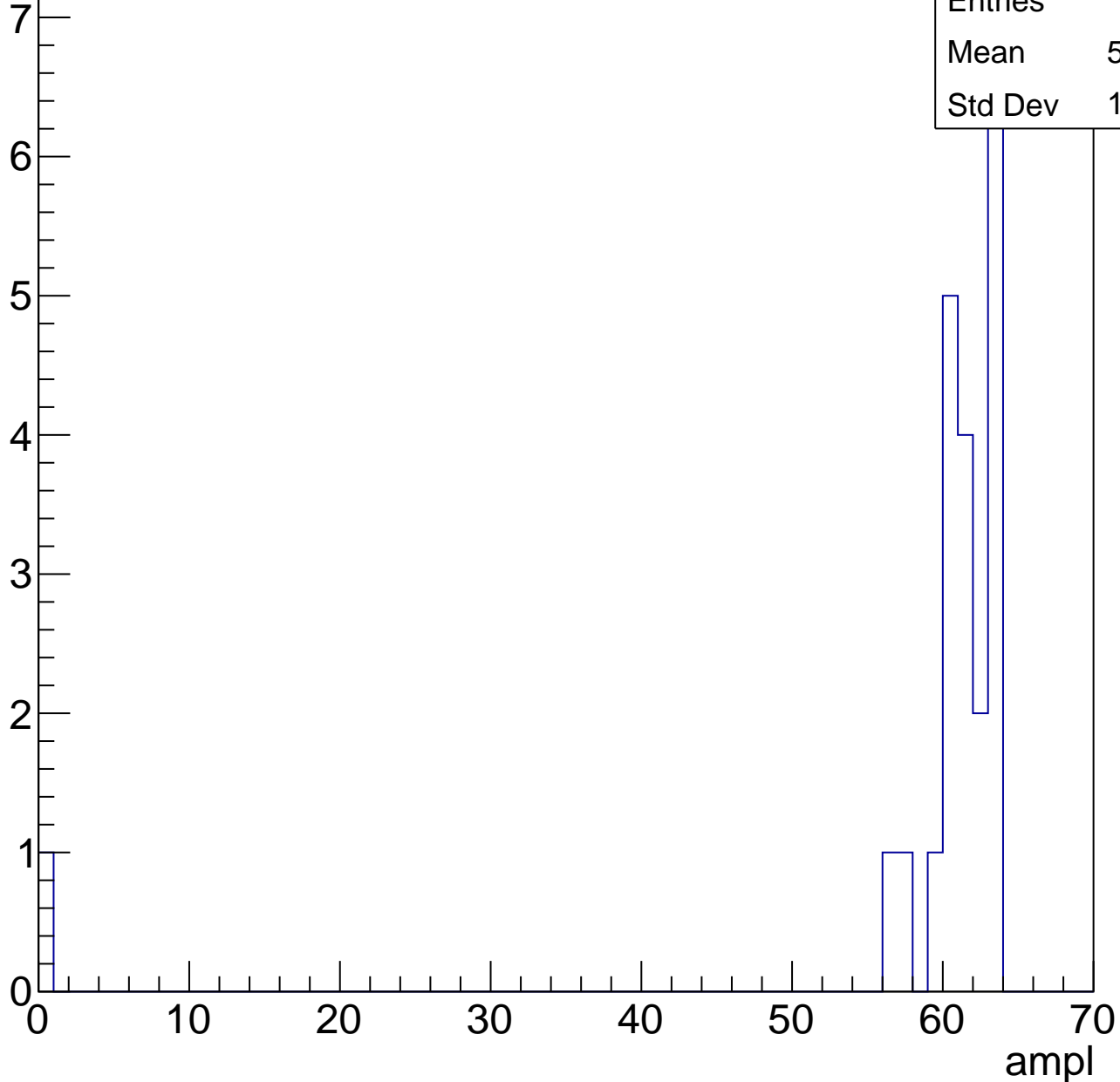


B0L002S, U26-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.23
Std Dev	12.85



B0L002S, U26-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch108, adc0

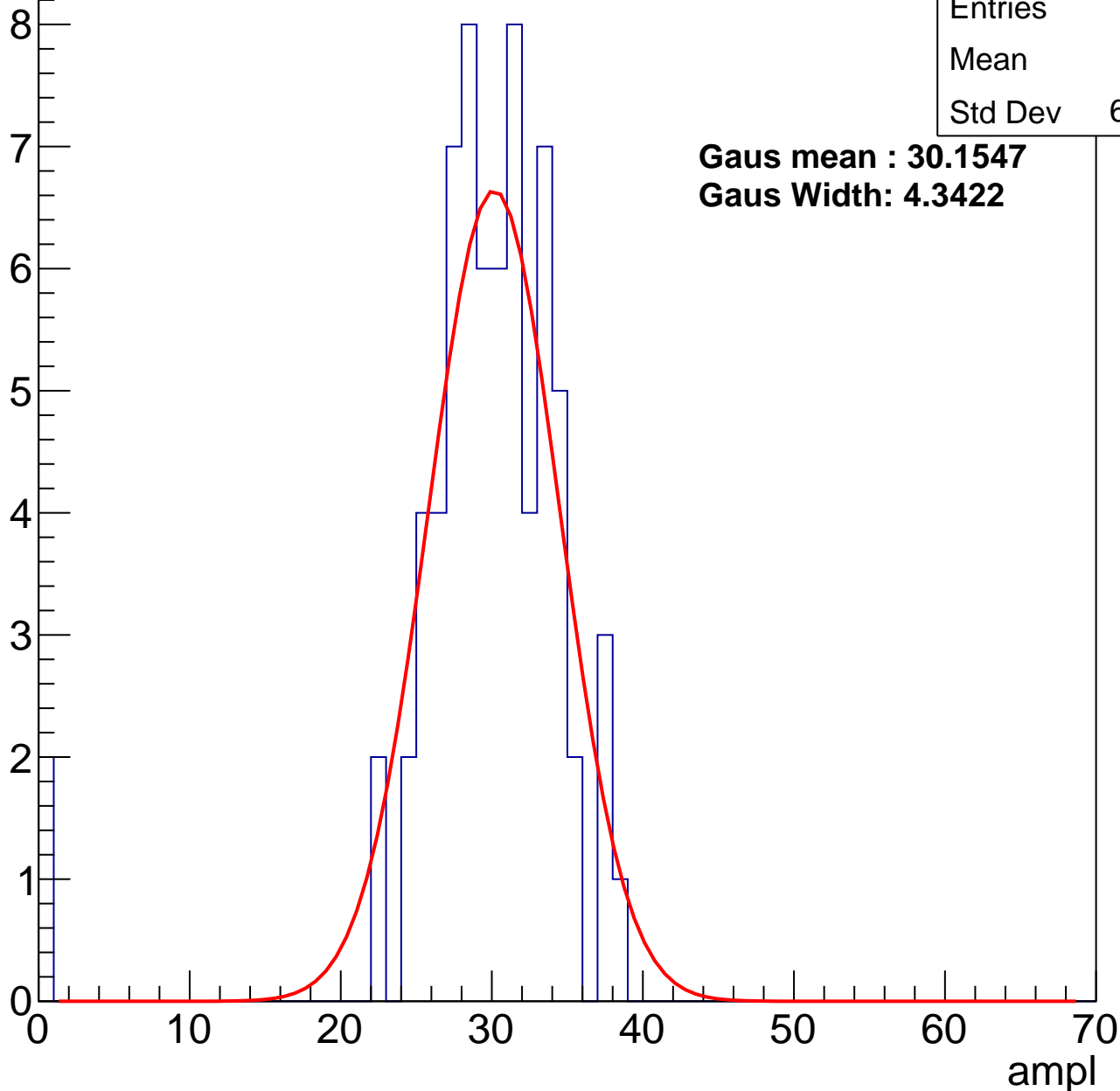
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29
Std Dev	6.077

Gaus mean : 30.1547

Gaus Width: 4.3422



B0L002S, U26-ch108, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	37.09
Std Dev	3.189

Gaus mean : 37.3920

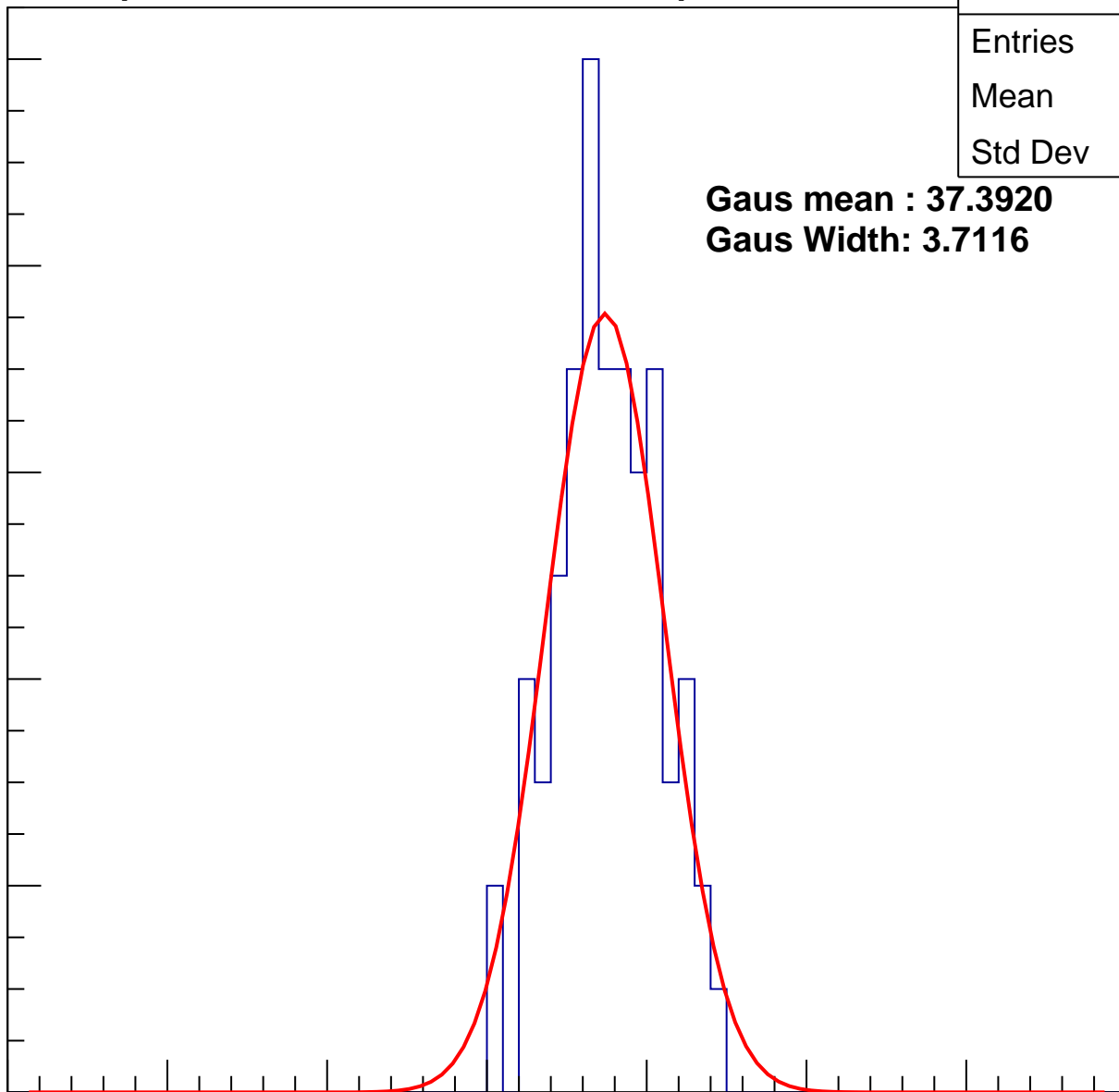
Gaus Width: 3.7116

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch108, adc2

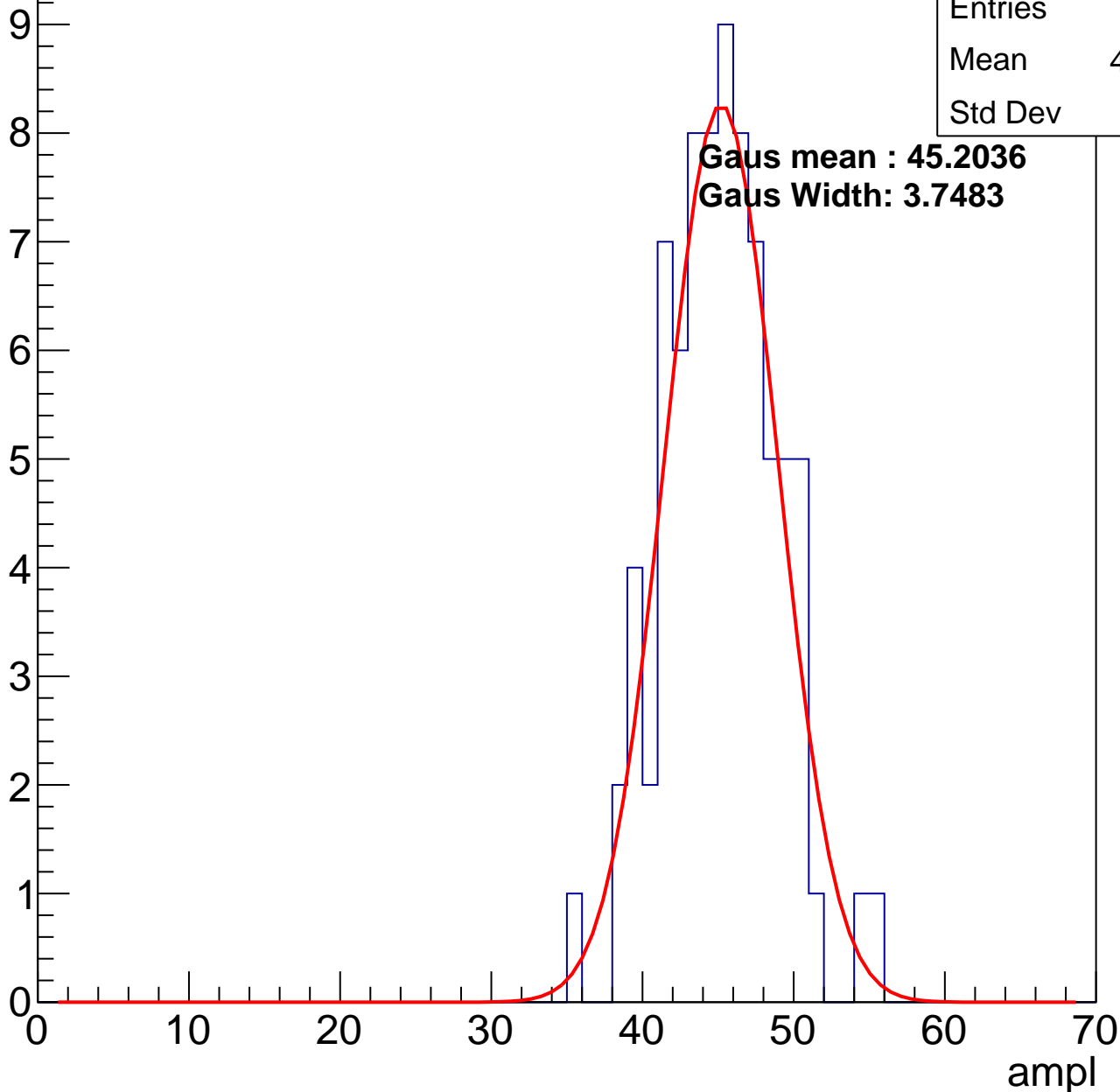
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	44.74
Std Dev	3.7

Gaus mean : 45.2036

Gaus Width: 3.7483

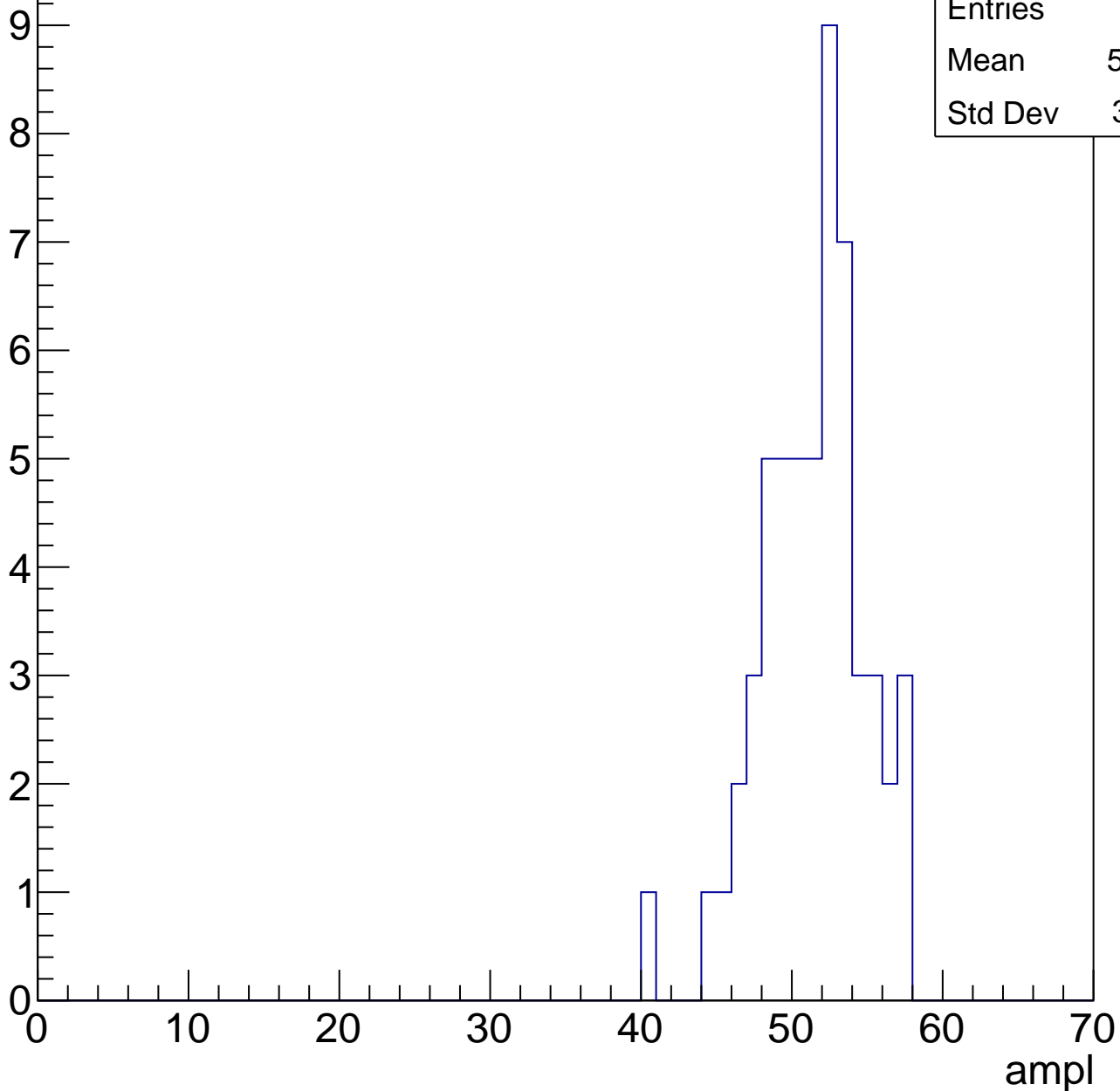


B0L002S, U26-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	50.93
Std Dev	3.421

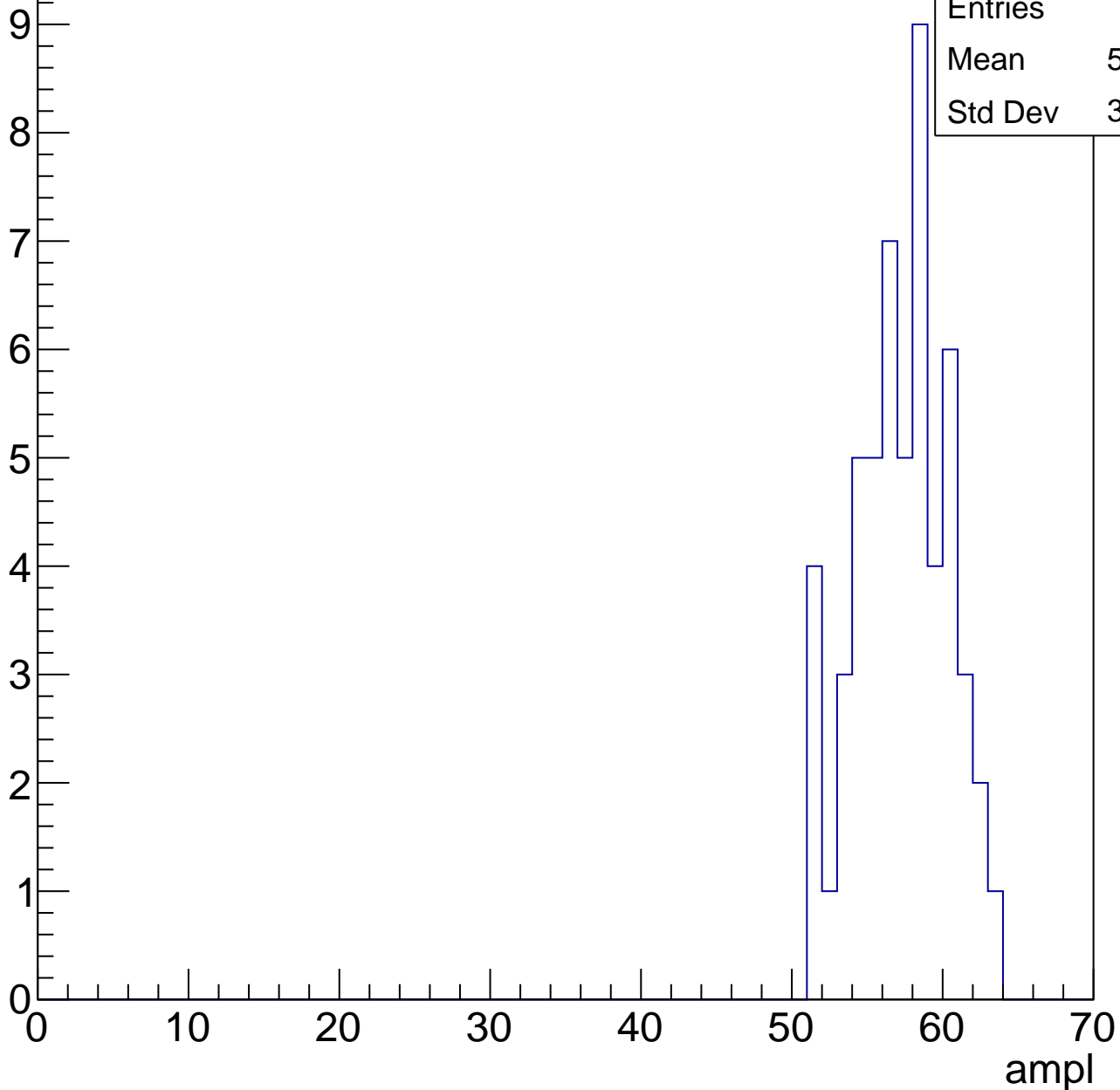


B0L002S, U26-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.82
Std Dev	3.016

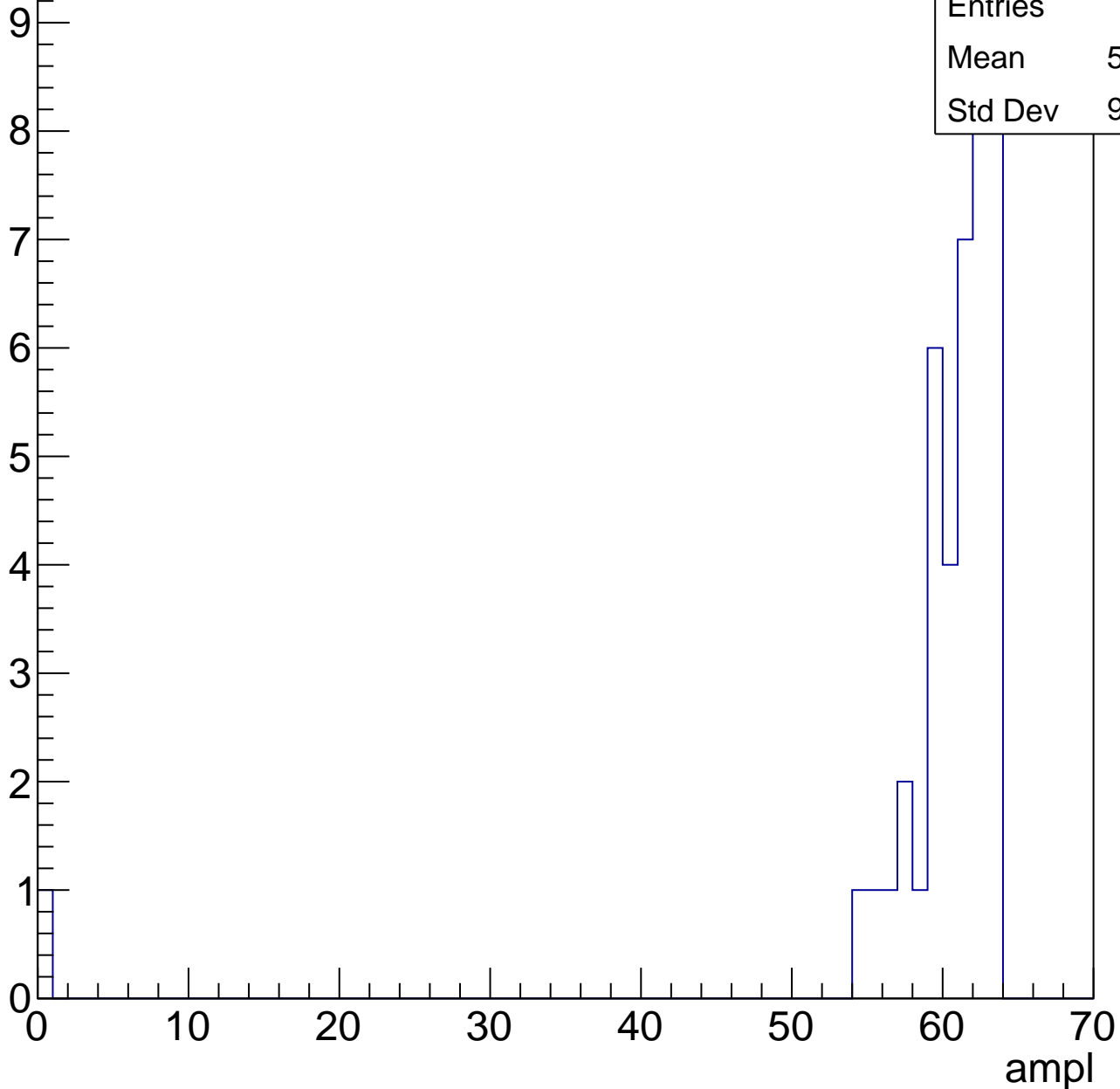


B0L002S, U26-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	59.05
Std Dev	9.612



B0L002S, U26-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch109, adc0

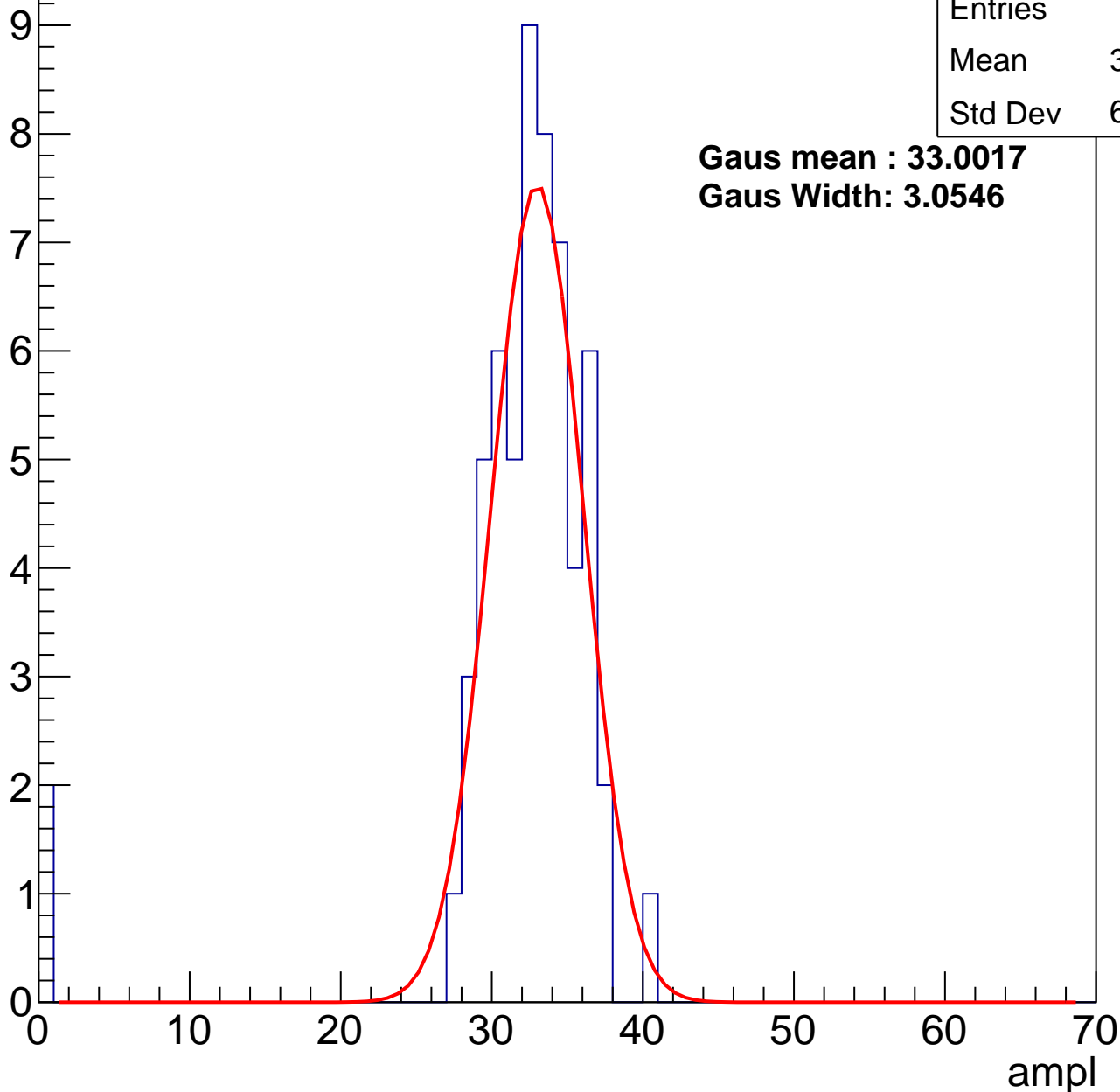
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	31.37
Std Dev	6.449

Gaus mean : 33.0017

Gaus Width: 3.0546



B0L002S, U26-ch109, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	39.58
Std Dev	3.496

Gaus mean : 40.5432

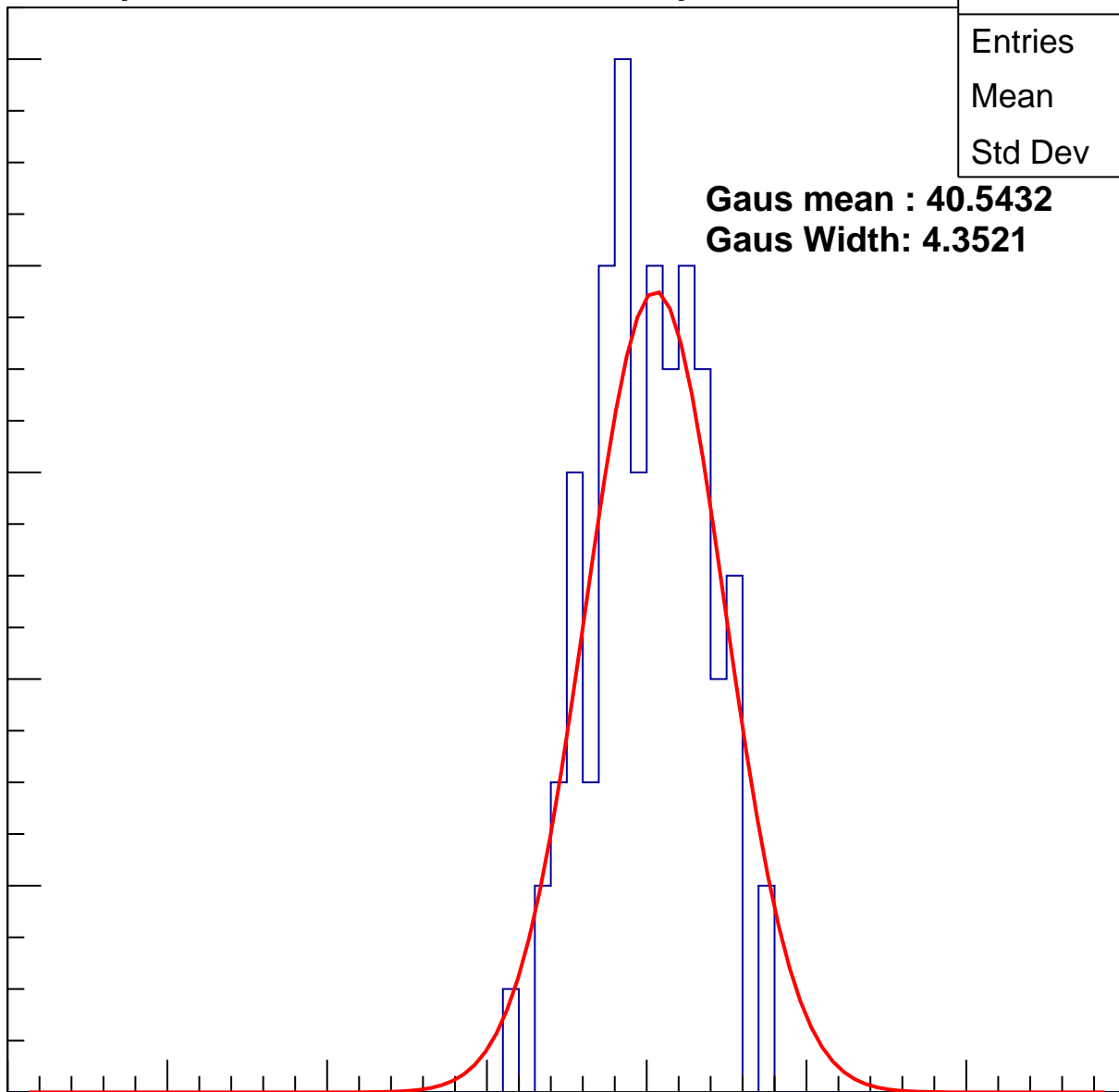
Gaus Width: 4.3521

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch109, adc2

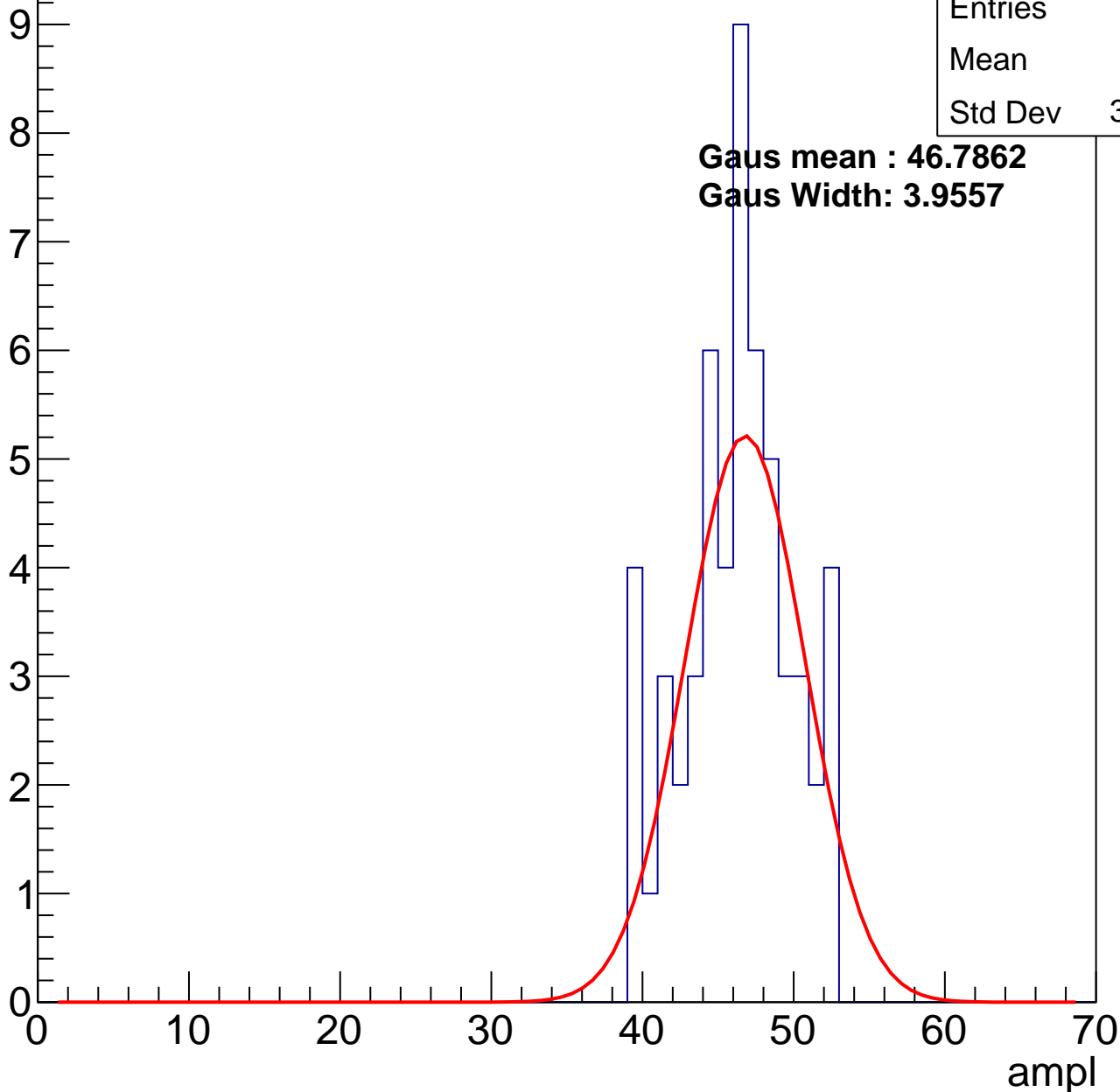
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	45.8
Std Dev	3.534

Gaus mean : 46.7862

Gaus Width: 3.9557

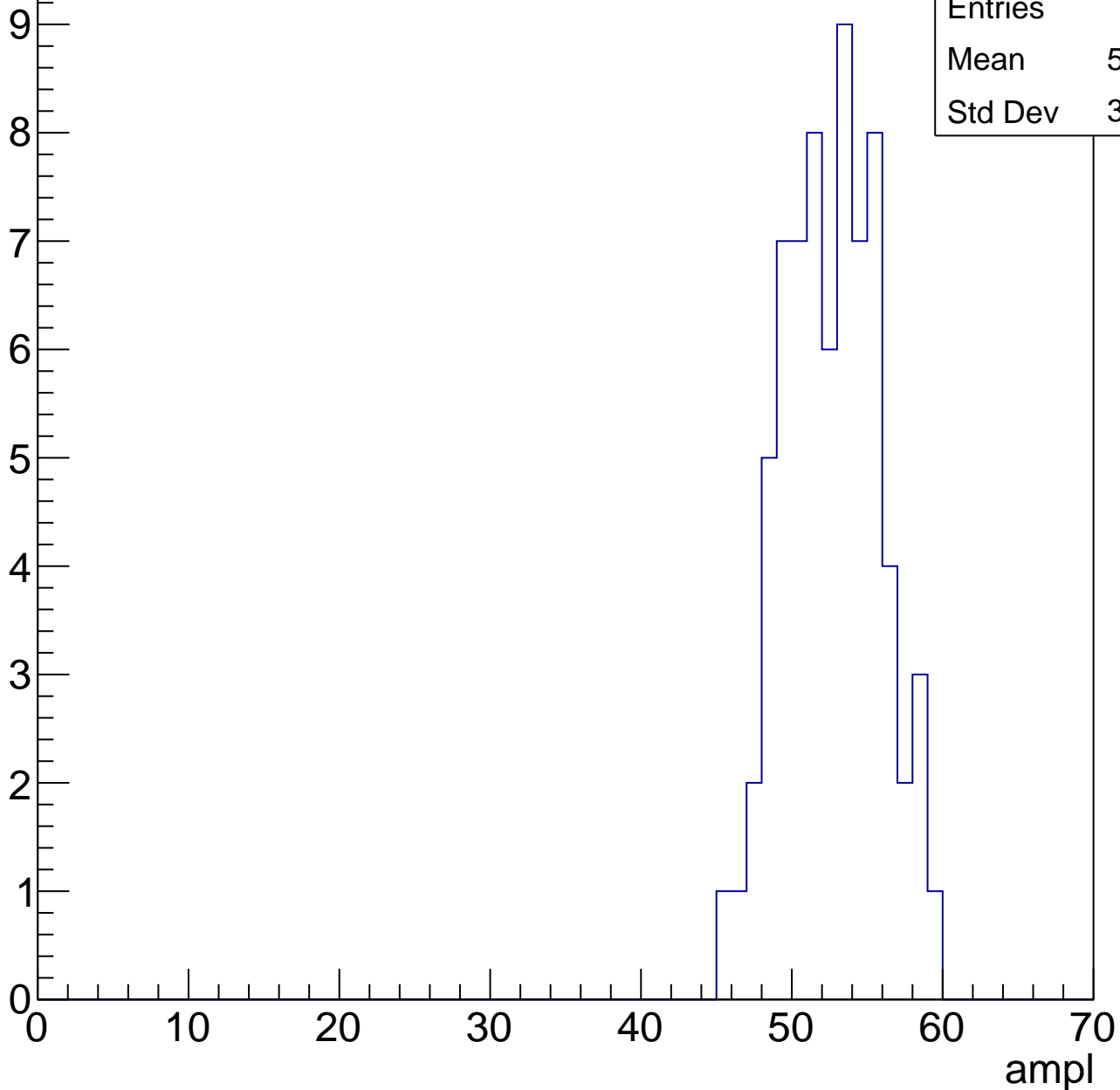


B0L002S, U26-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

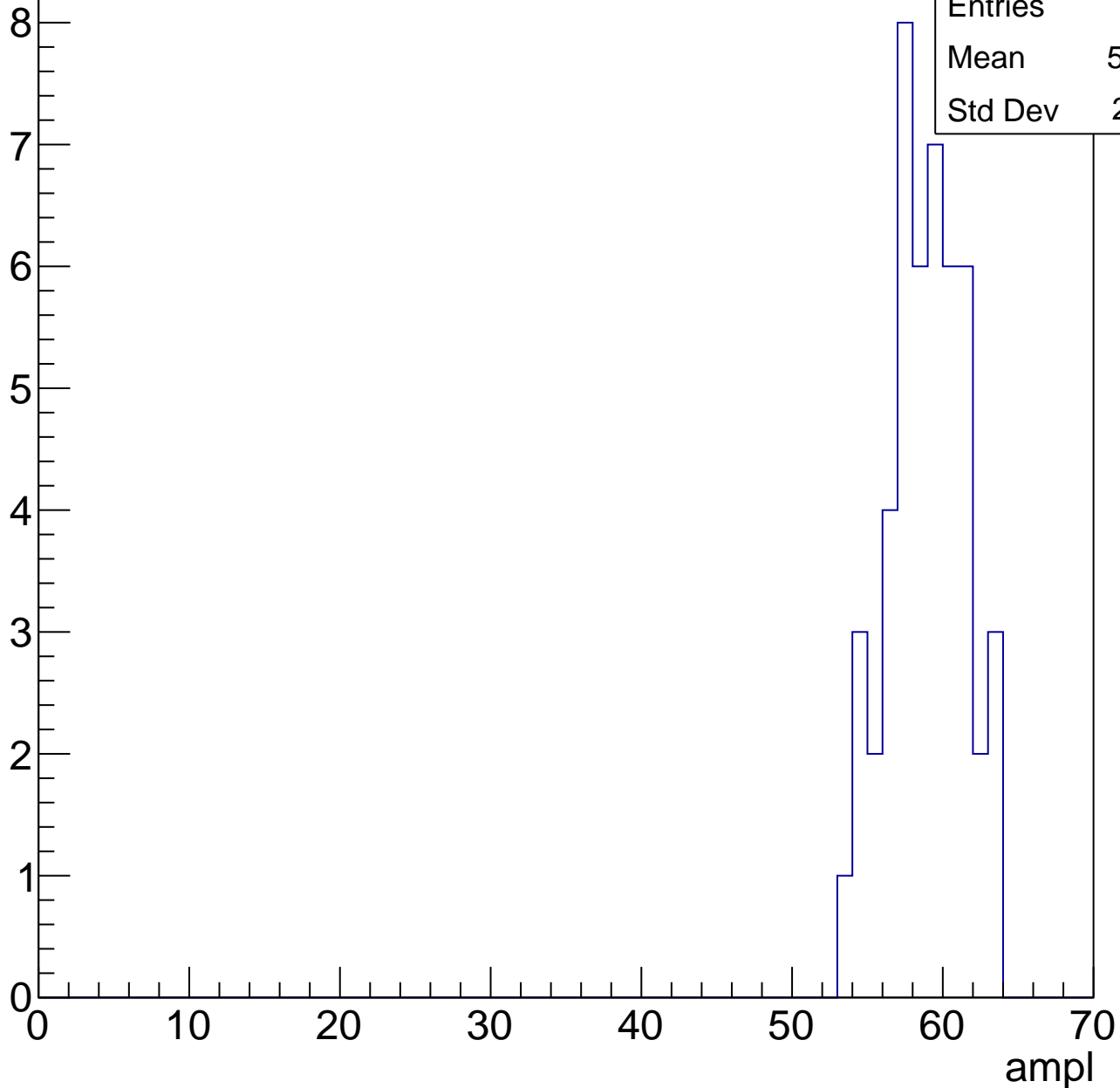
Entries	71
Mean	52.17
Std Dev	3.122



B0L002S, U26-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

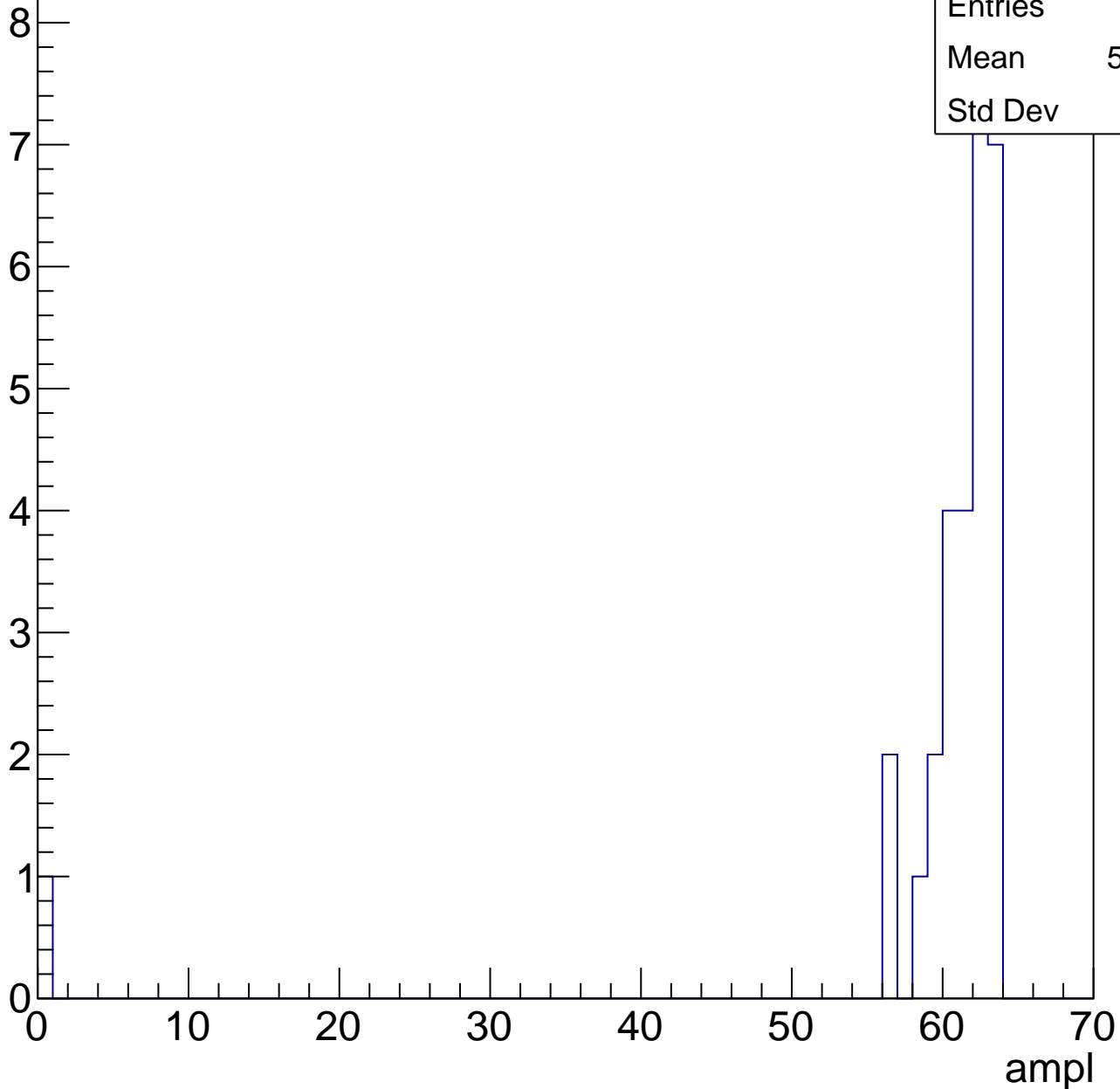


B0L002S, U26-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.93
Std Dev	11.3



B0L002S, U26-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch109, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U26-ch110, adc0

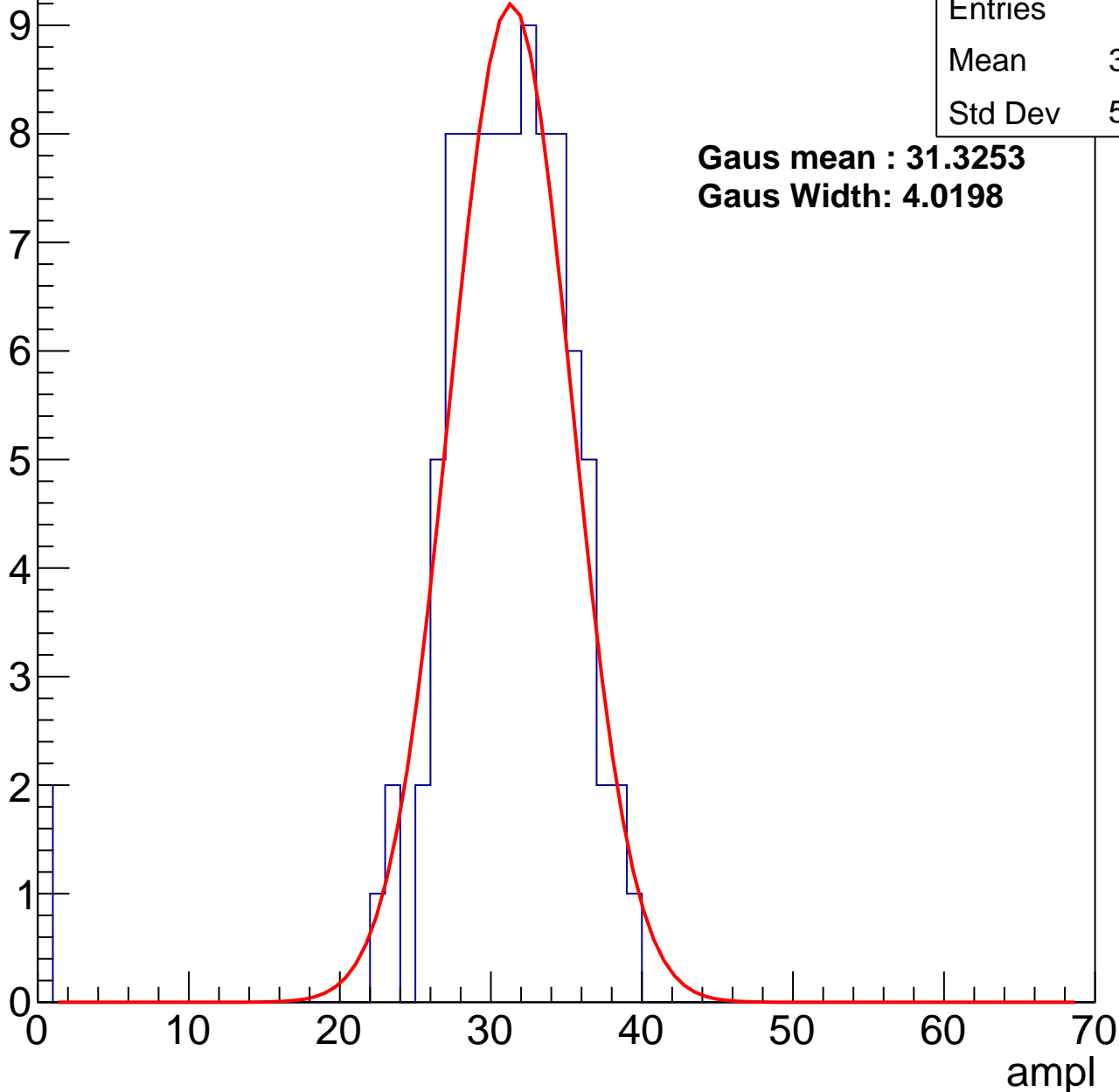
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	93
Mean	30.23
Std Dev	5.755

Gaus mean : 31.3253

Gaus Width: 4.0198



B0L002S, U26-ch110, adc1

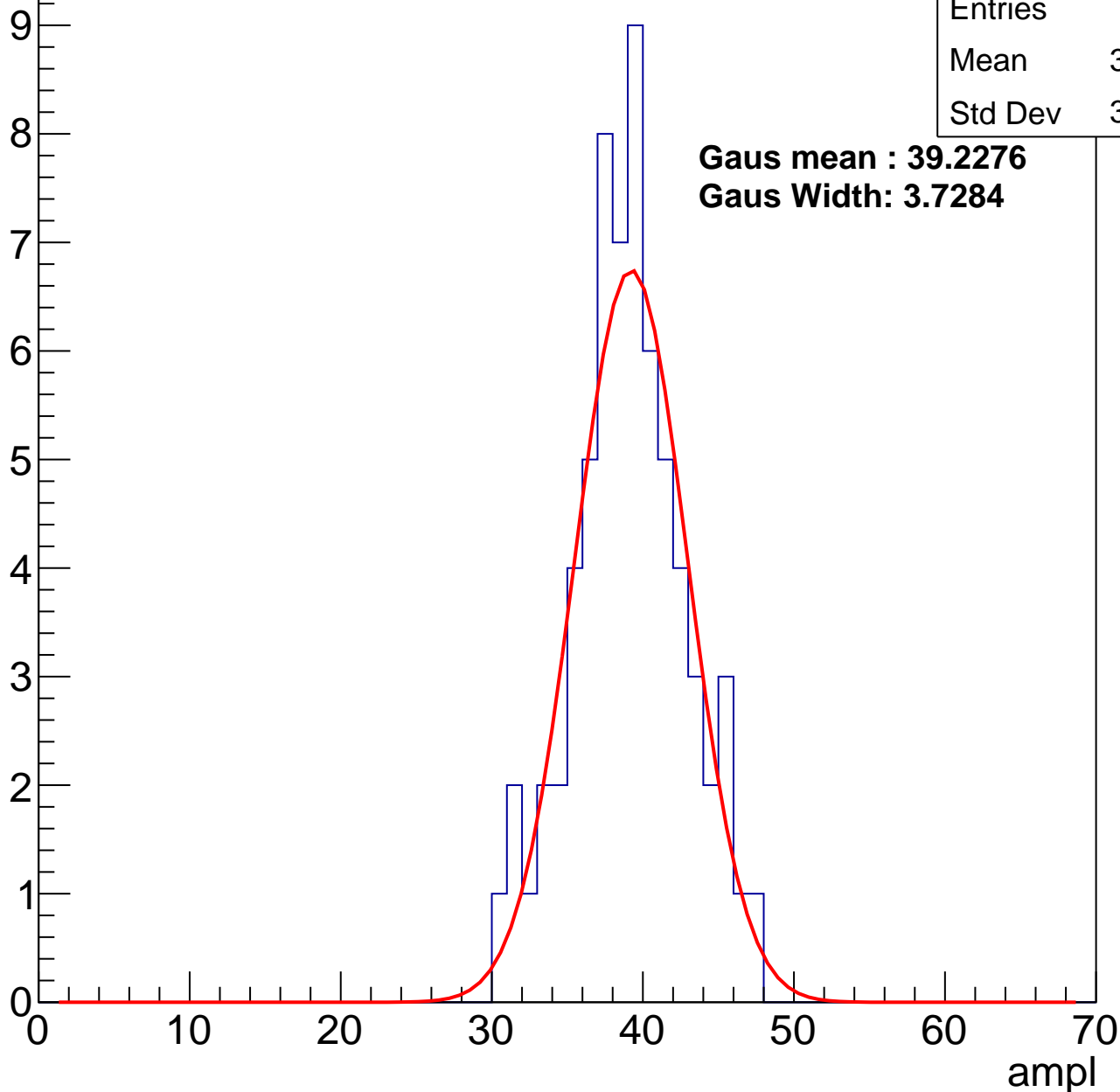
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.62
Std Dev	3.692

Gaus mean : 39.2276

Gaus Width: 3.7284



B0L002S, U26-ch110, adc2

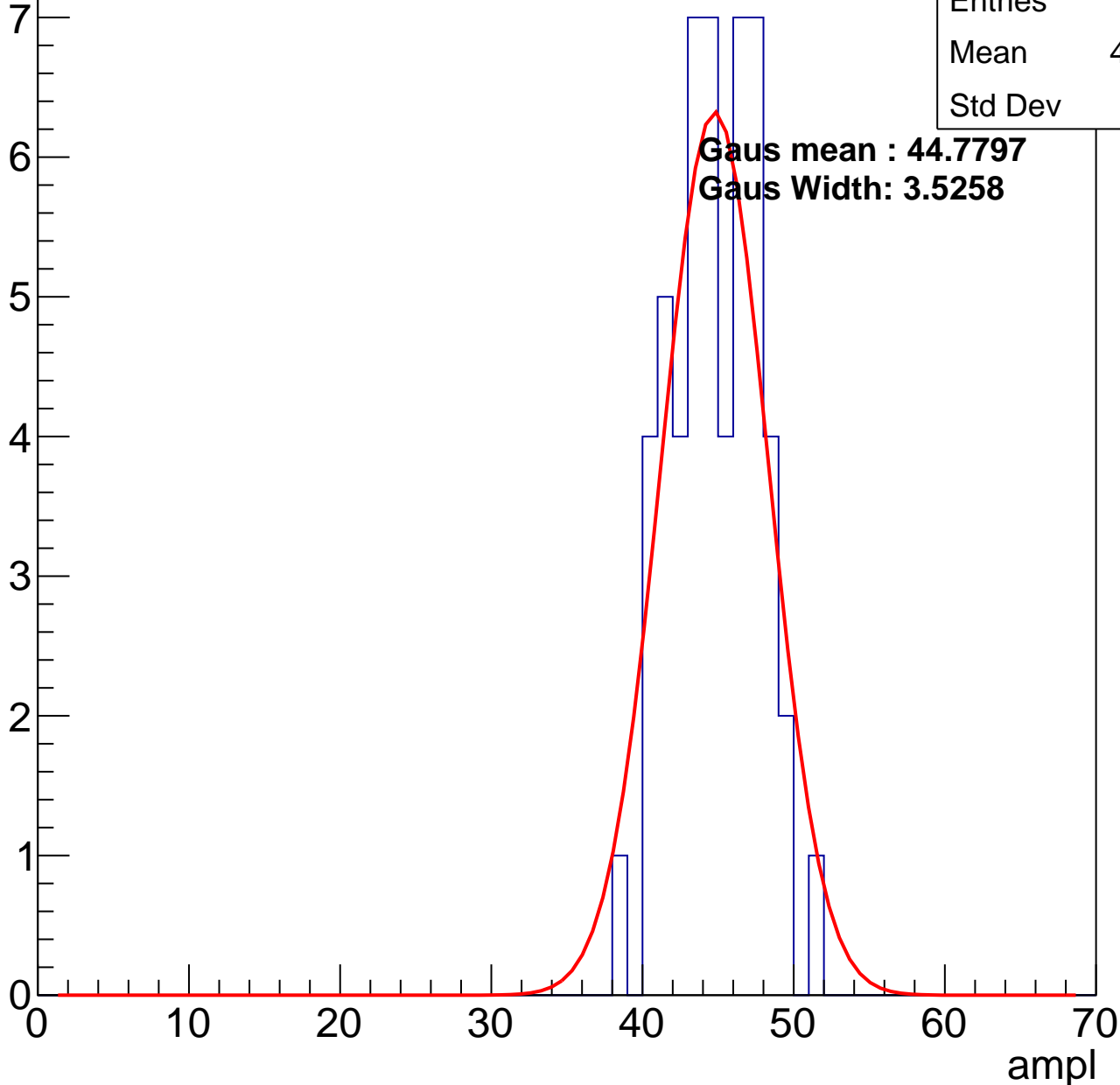
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.38
Std Dev	2.81

Gaus mean : 44.7797

Gaus Width: 3.5258



B0L002S, U26-ch110, adc3

calib_packv5_042523_0143.root, FC#8, port C1

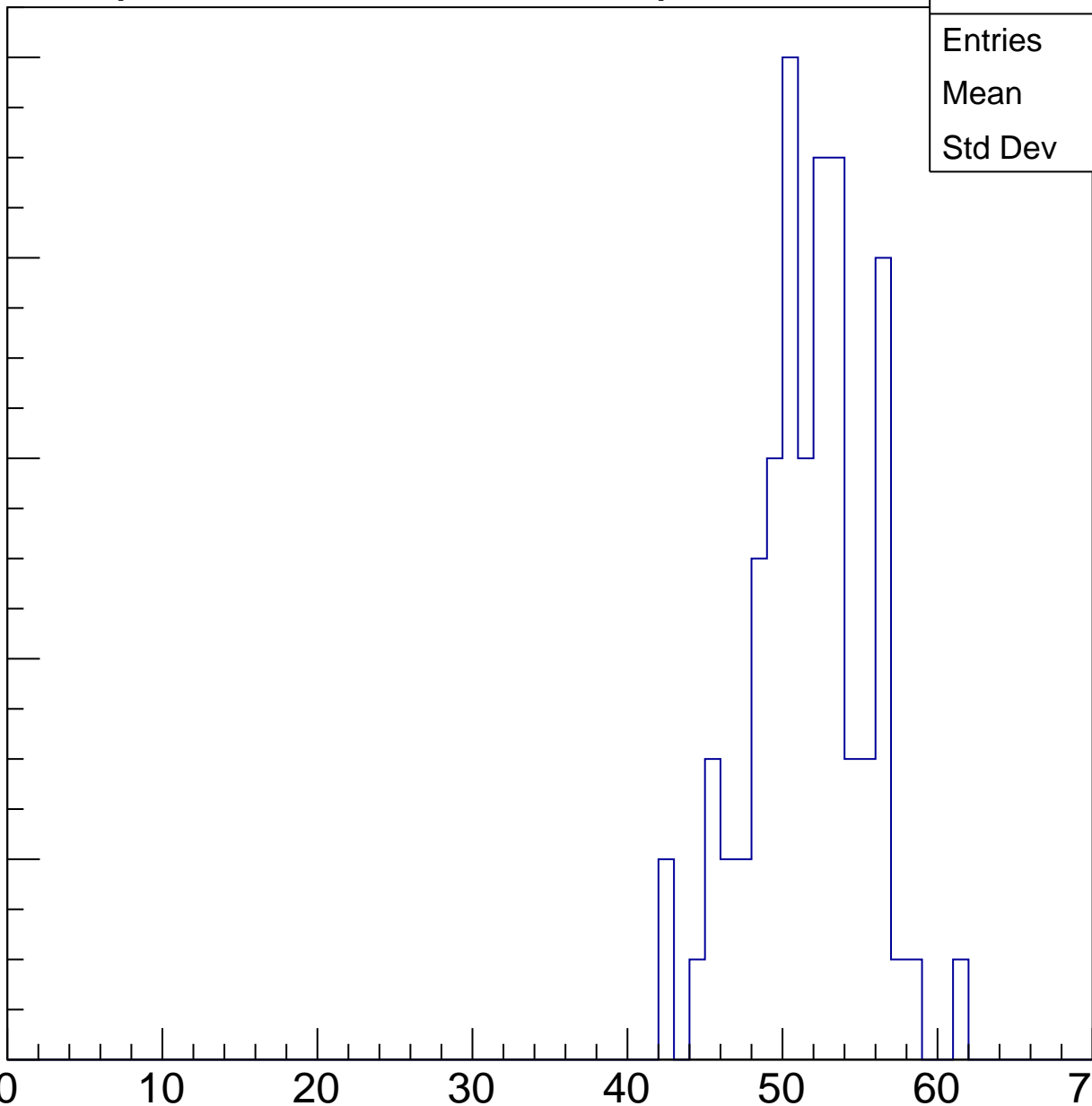
Entries	72
Mean	51.18
Std Dev	3.694

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

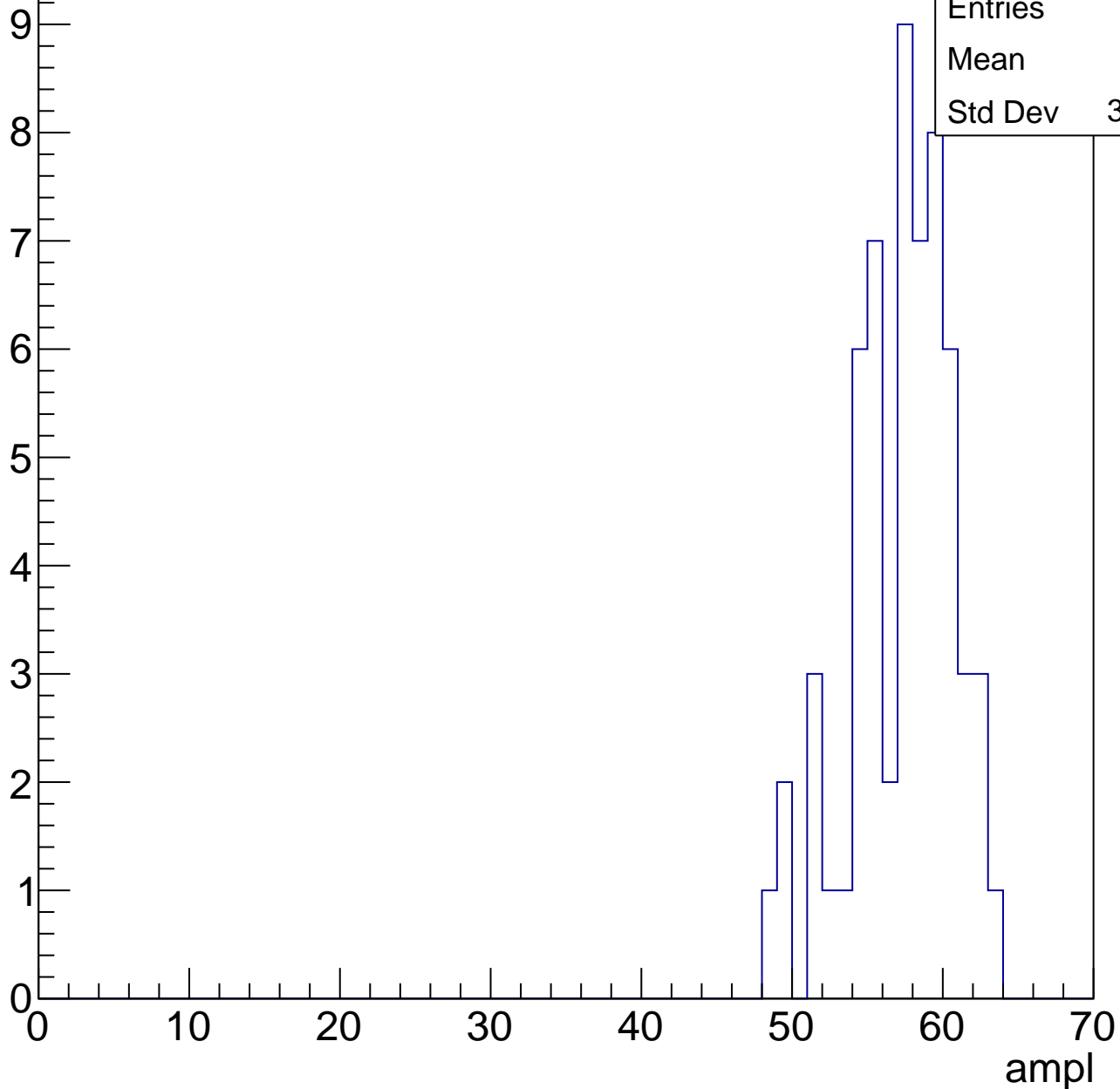
ampl



B0L002S, U26-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

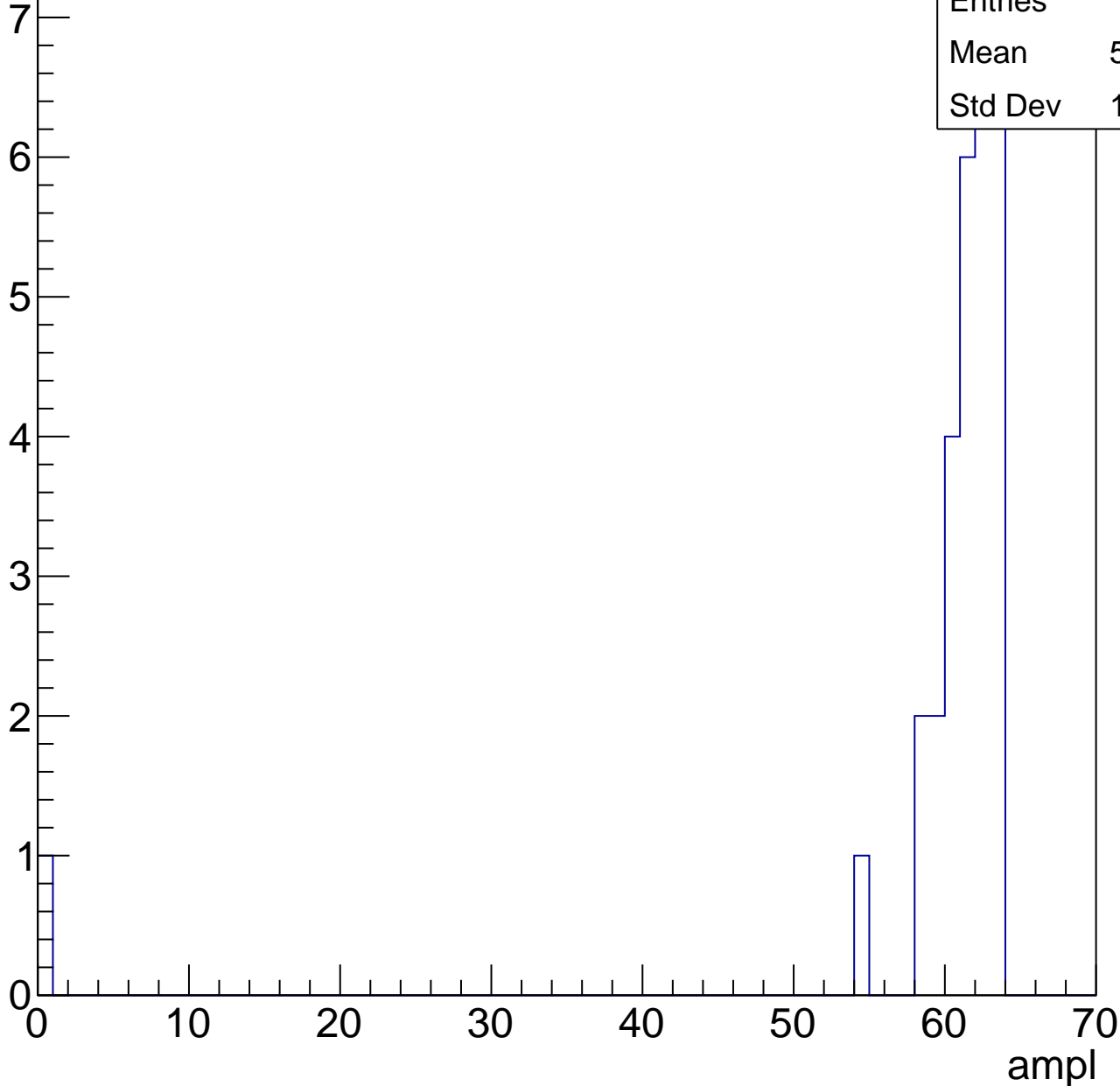


B0L002S, U26-ch110, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.97
Std Dev	11.12



B0L002S, U26-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch110, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U26-ch111, adc0

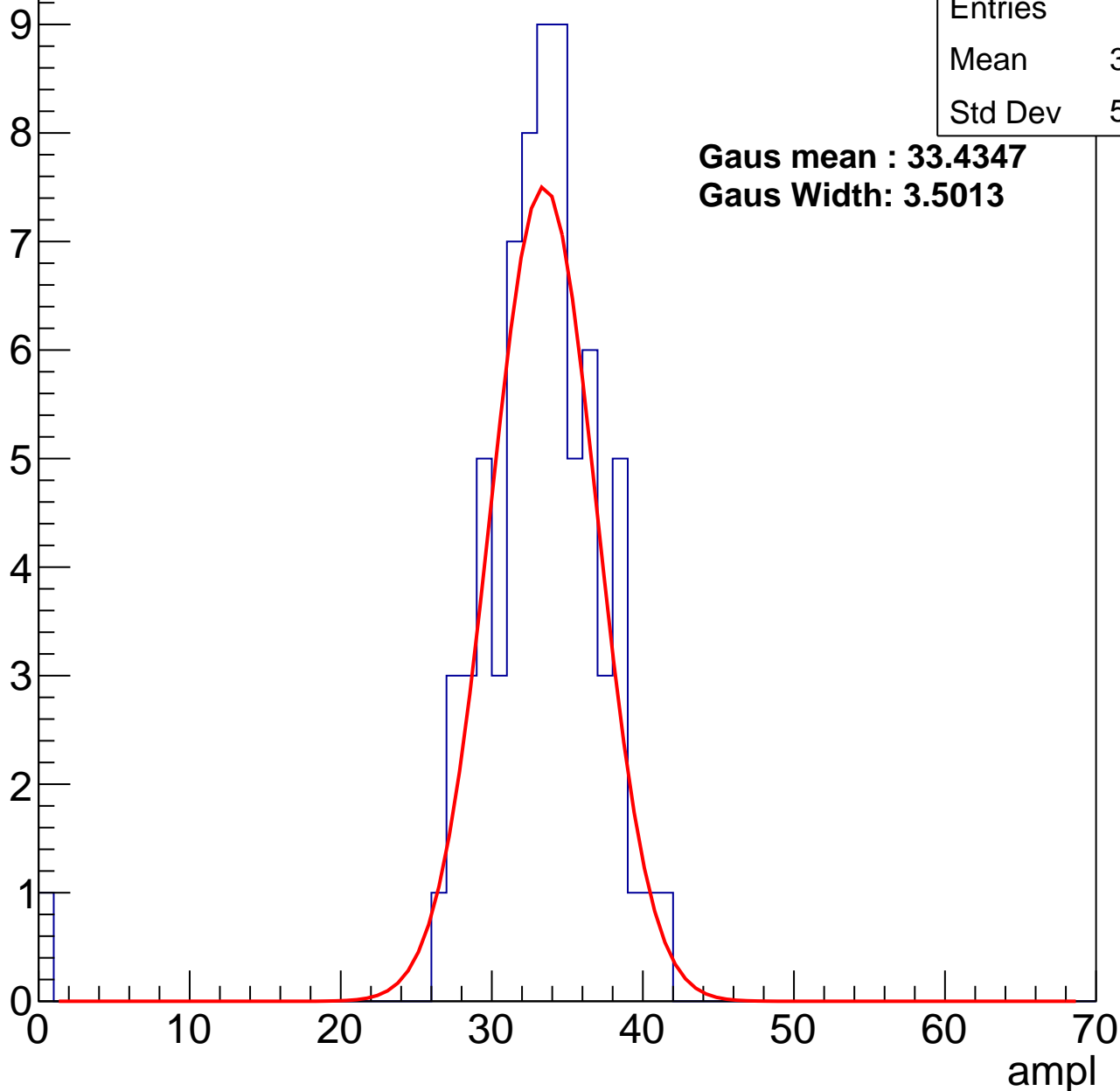
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	32.59
Std Dev	5.114

Gaus mean : 33.4347

Gaus Width: 3.5013



B0L002S, U26-ch111, adc1

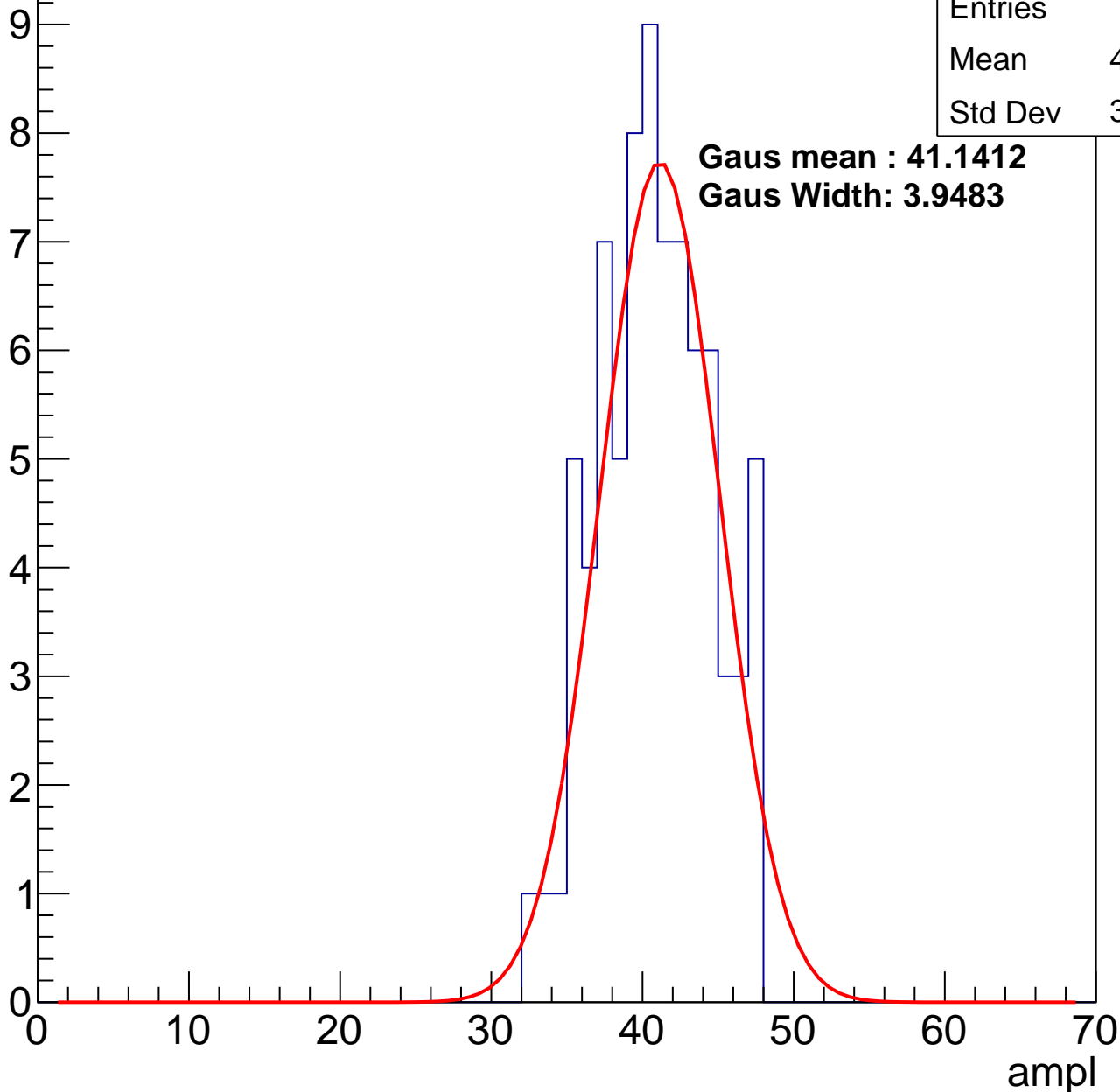
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	40.38
Std Dev	3.635

Gaus mean : 41.1412

Gaus Width: 3.9483



B0L002S, U26-ch111, adc2

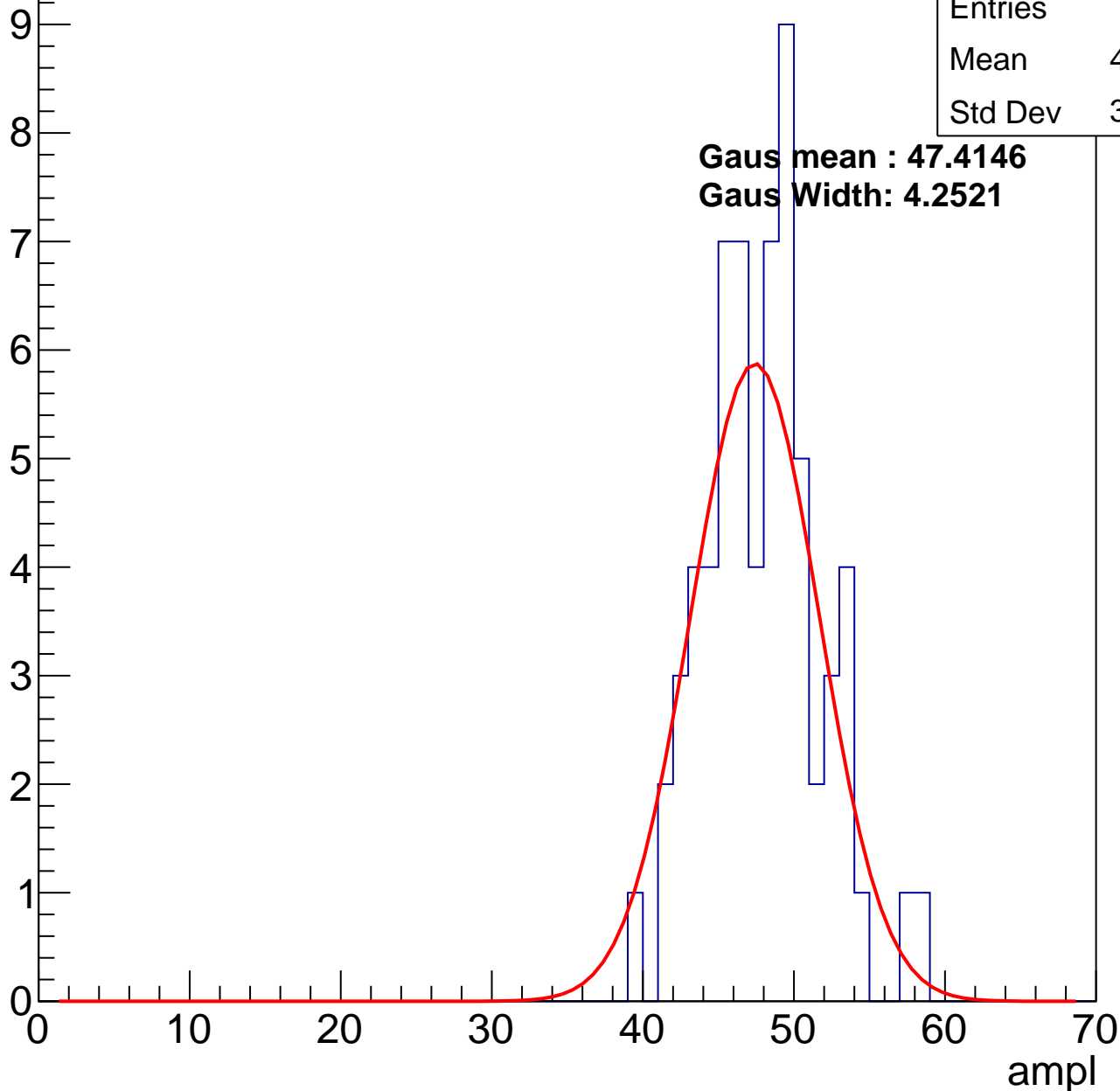
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	47.48
Std Dev	3.803

Gaus mean : 47.4146

Gaus Width: 4.2521

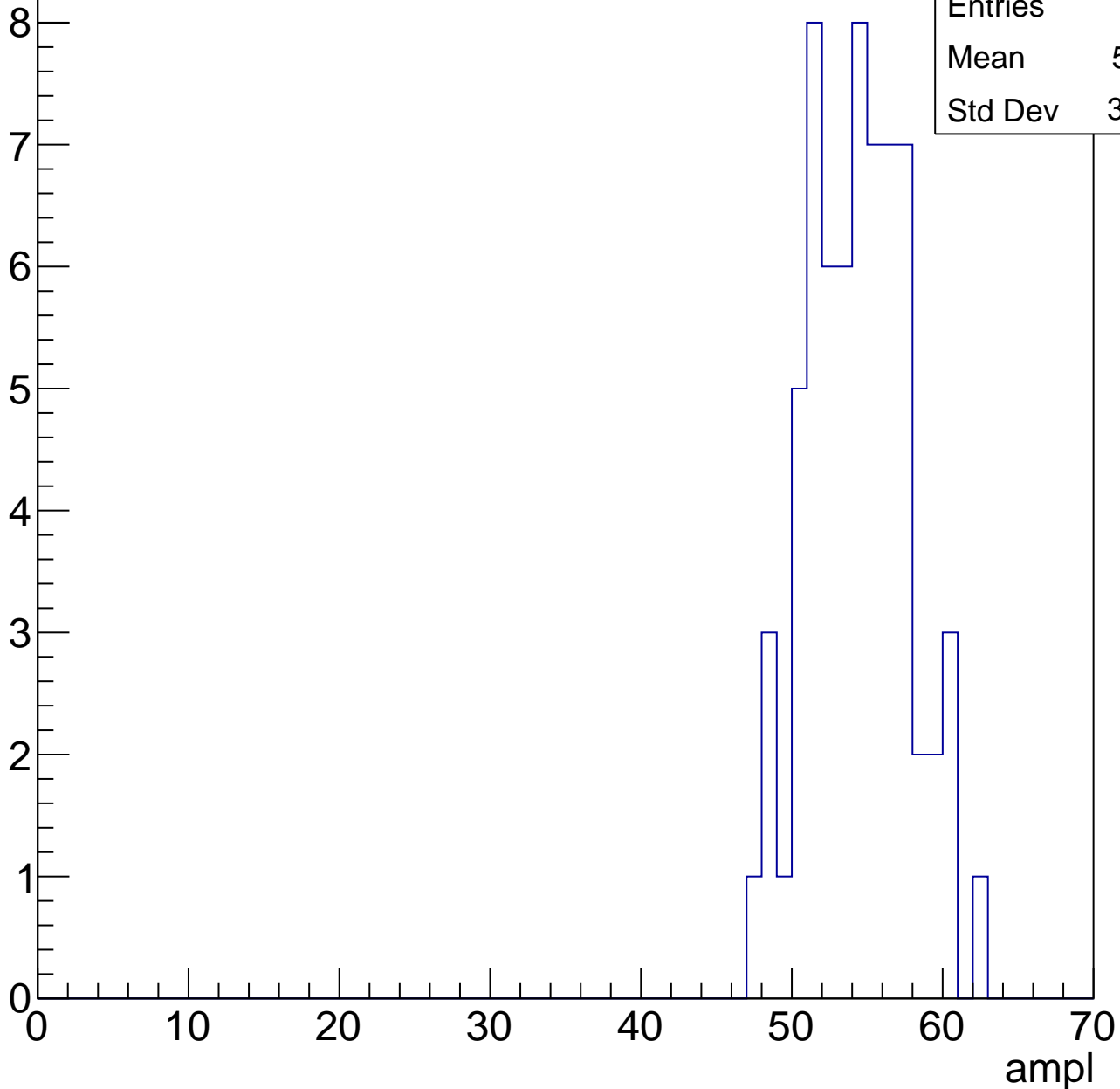


B0L002S, U26-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	53.91
Std Dev	3.268

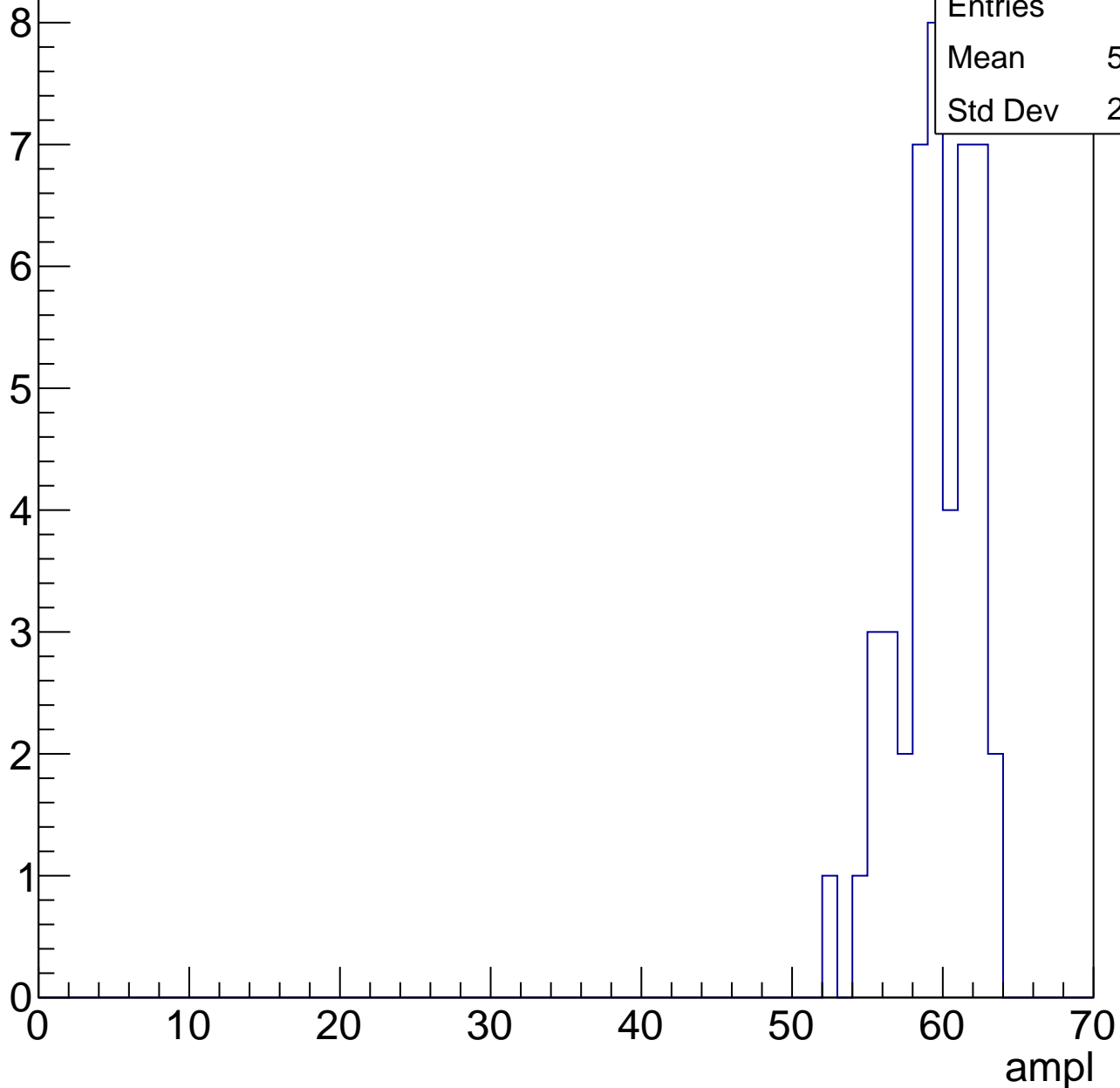


B0L002S, U26-ch111, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.07
Std Dev	2.542

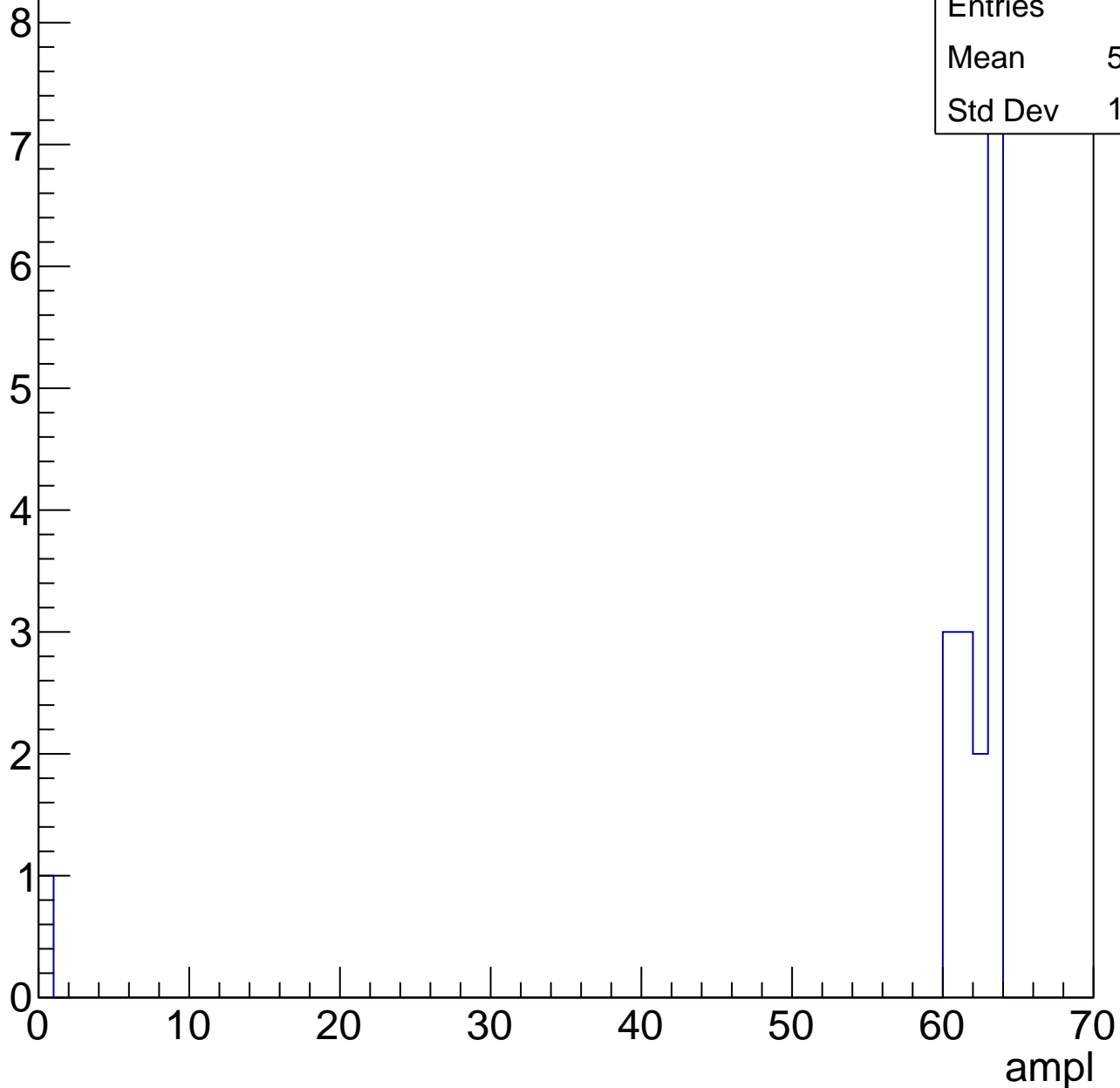


B0L002S, U26-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	58.29
Std Dev	14.62



B0L002S, U26-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch112, adc0

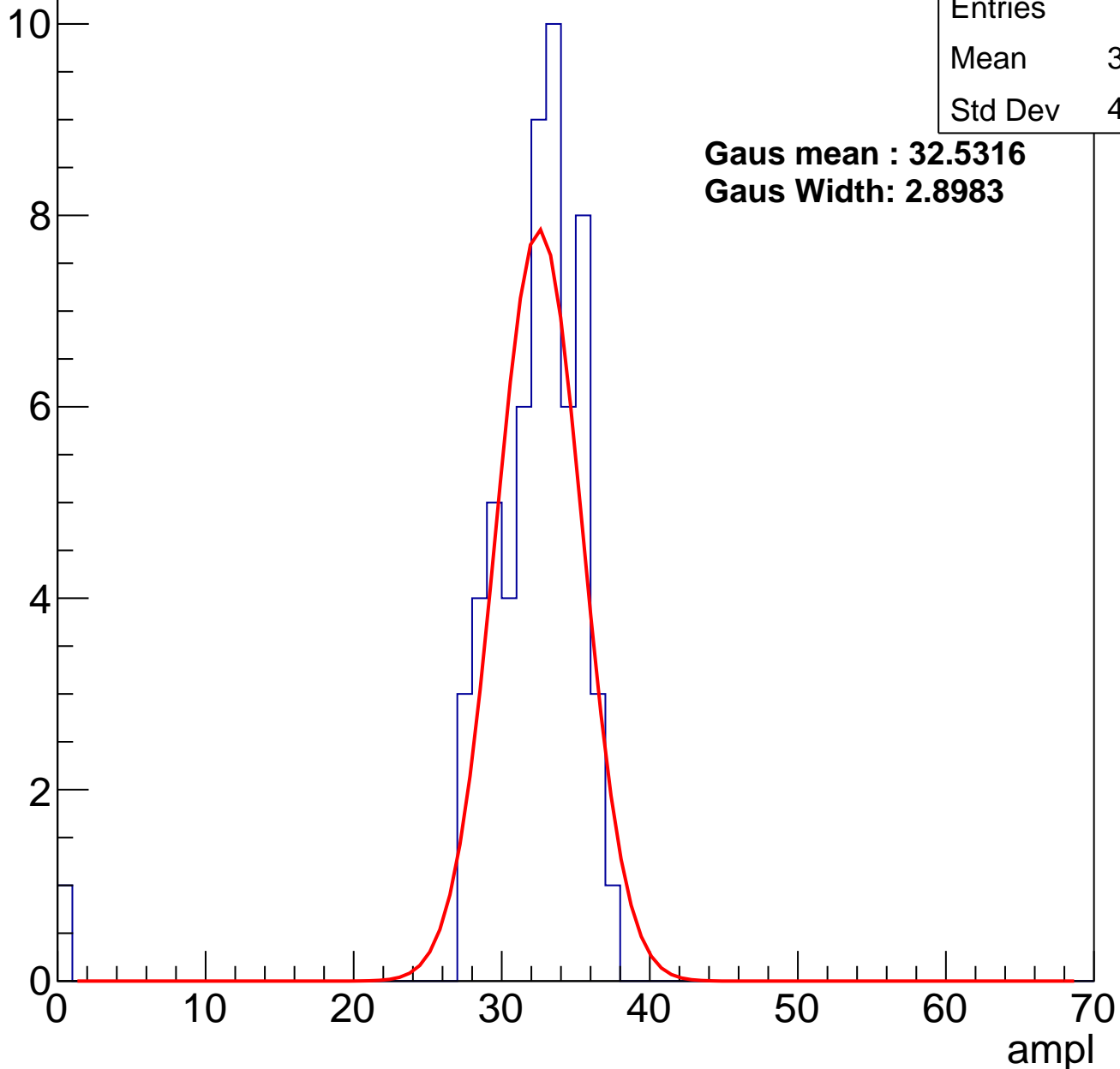
calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	31.52
Std Dev	4.822

Gaus mean : 32.5316

Gaus Width: 2.8983

Entry



B0L002S, U26-ch112, adc1

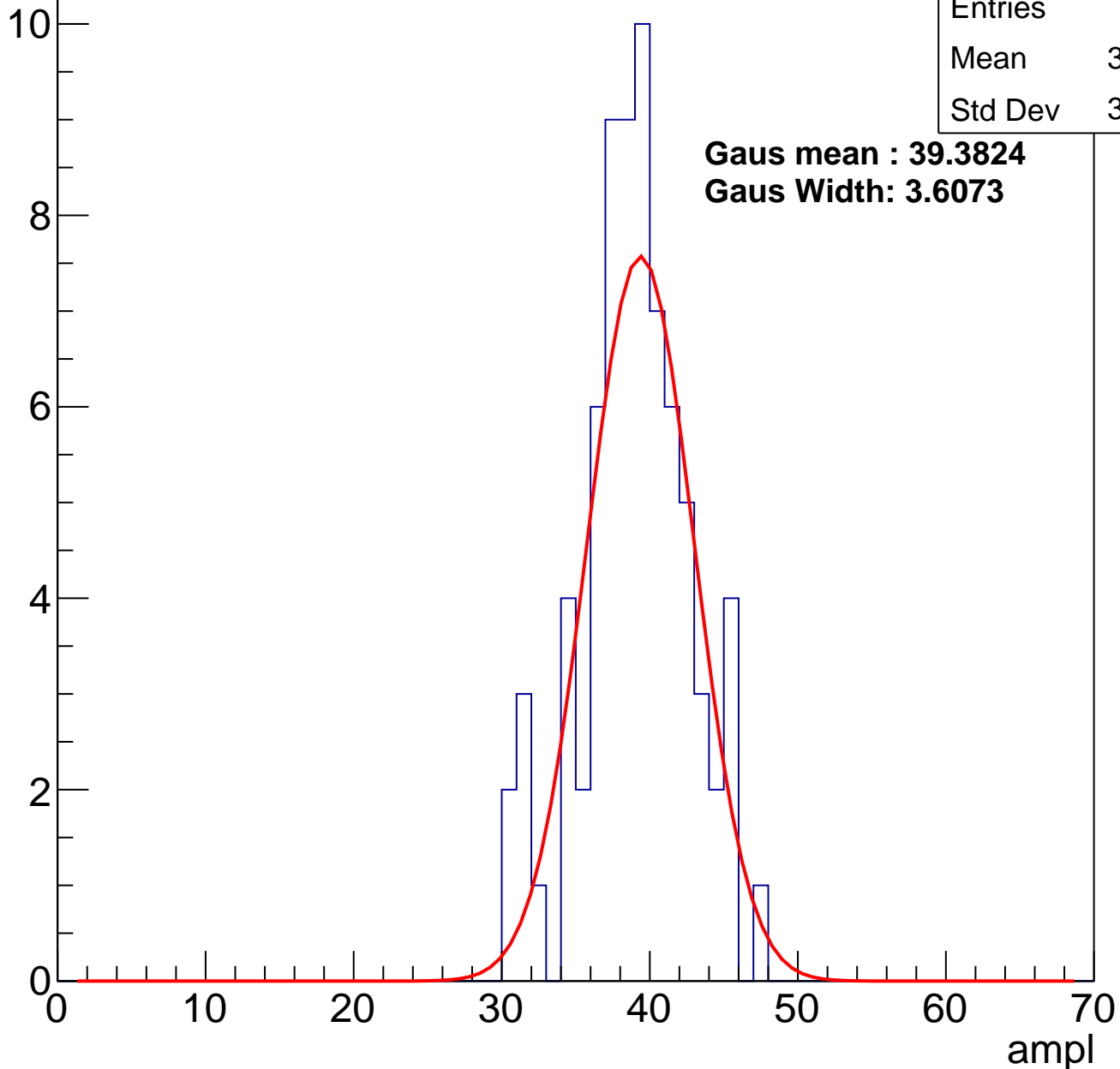
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	38.54
Std Dev	3.677

Gaus mean : 39.3824

Gaus Width: 3.6073

Entry



B0L002S, U26-ch112, adc2

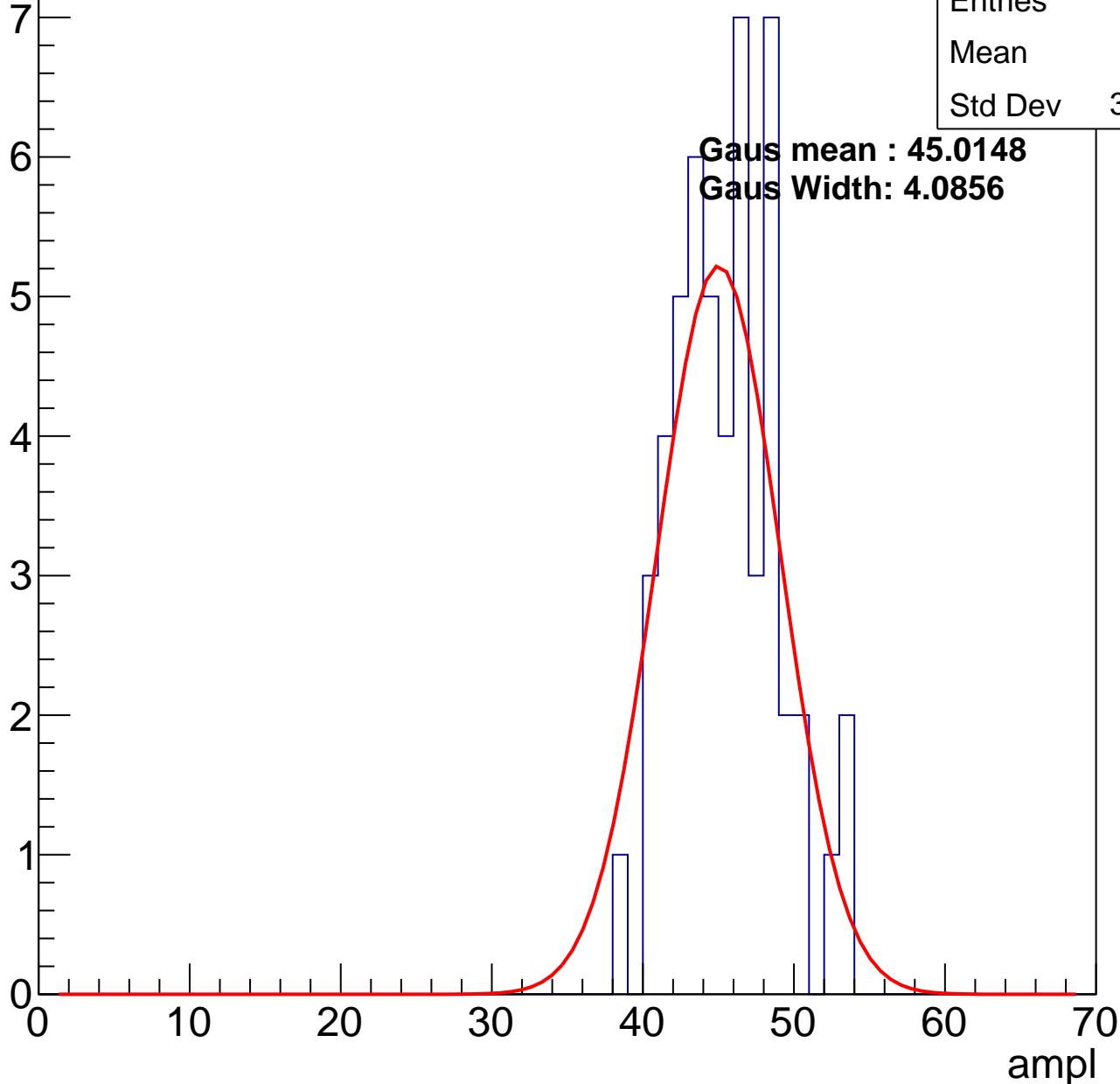
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	45.1
Std Dev	3.404

Gaus mean : 45.0148

Gaus Width: 4.0856

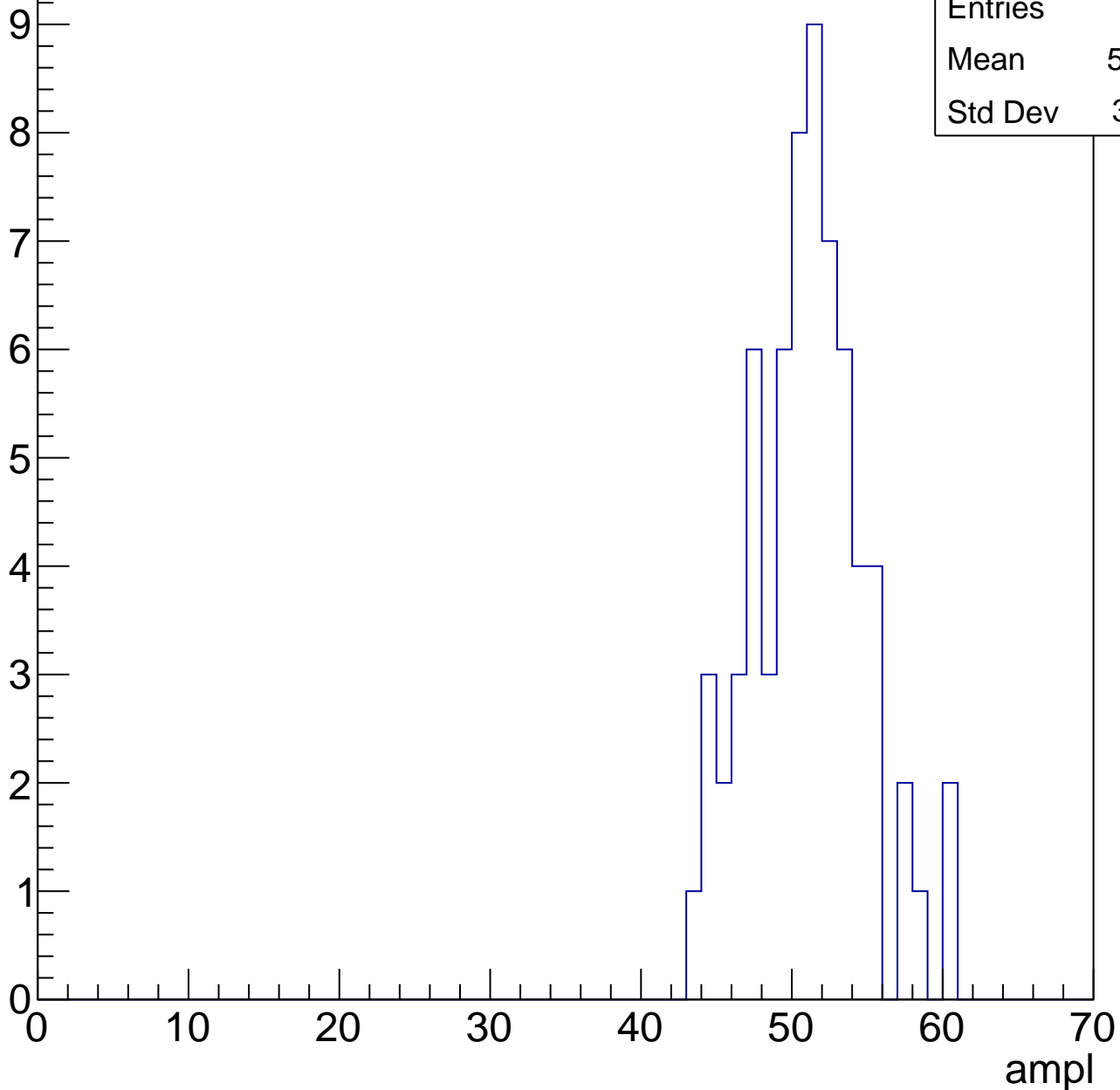


B0L002S, U26-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.63
Std Dev	3.701

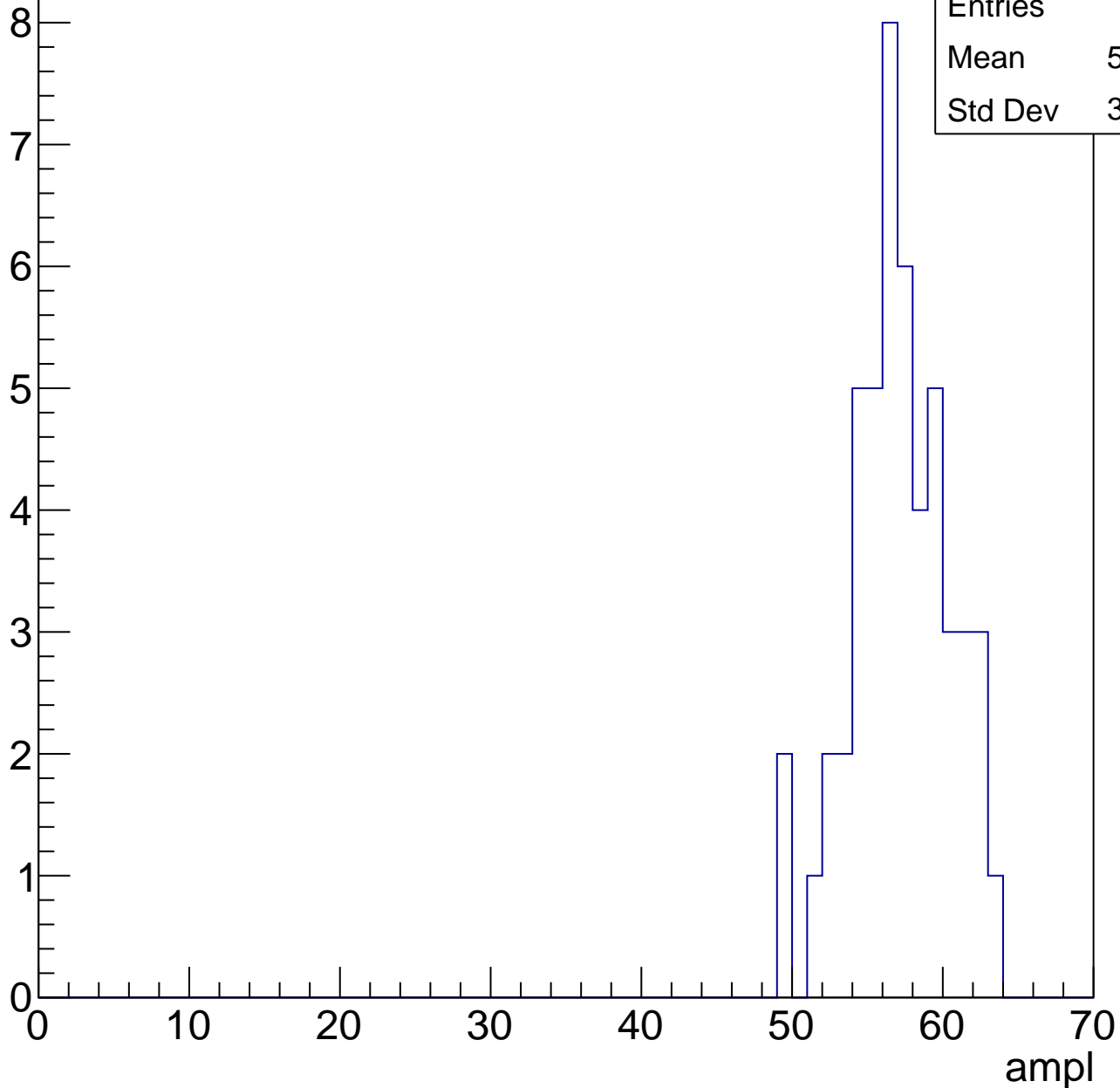


B0L002S, U26-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.66
Std Dev	3.235

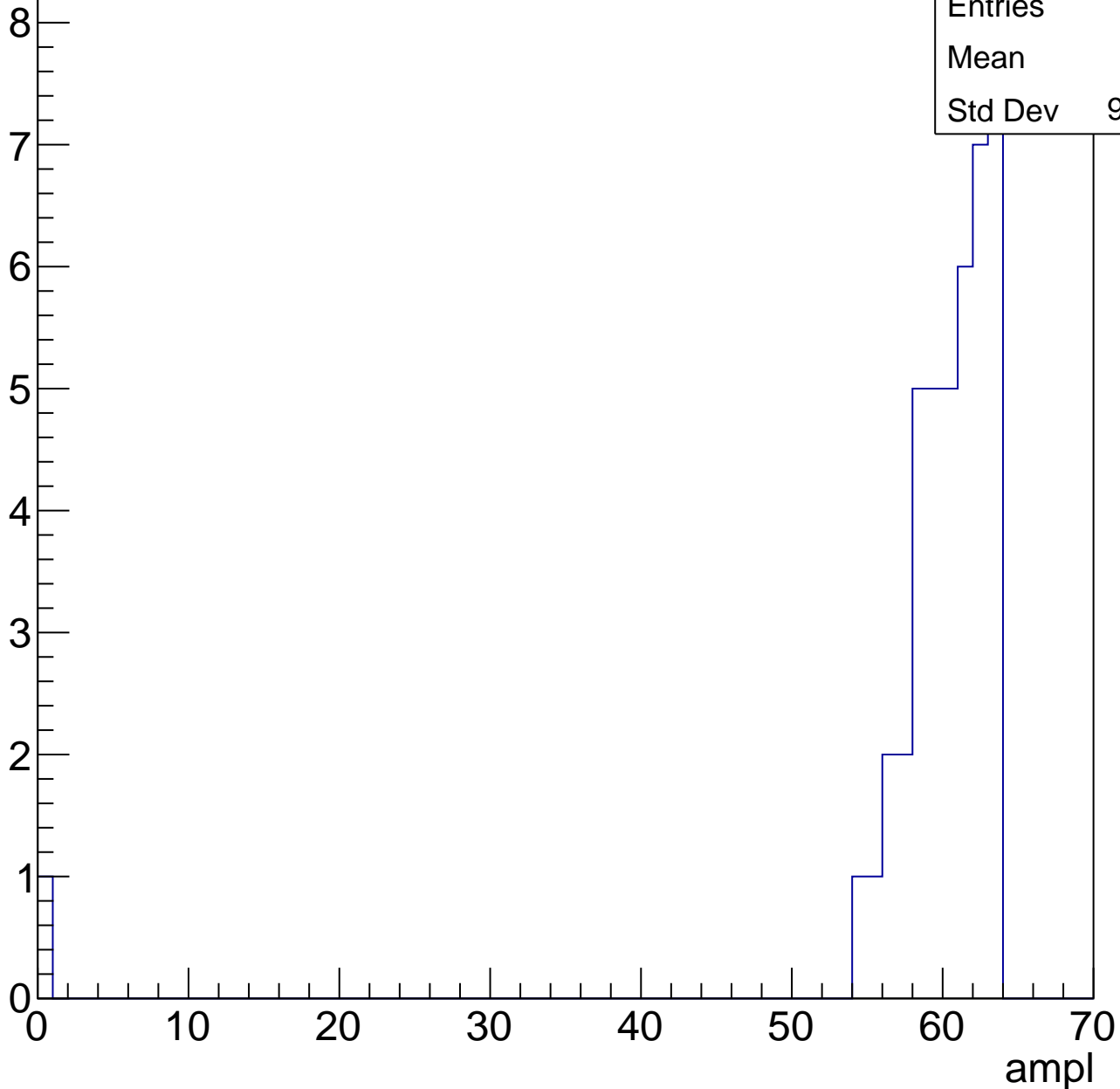


B0L002S, U26-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	58.7
Std Dev	9.362



B0L002S, U26-ch112, adc6

calib_packv5_042523_0143.root, FC#8, port C1

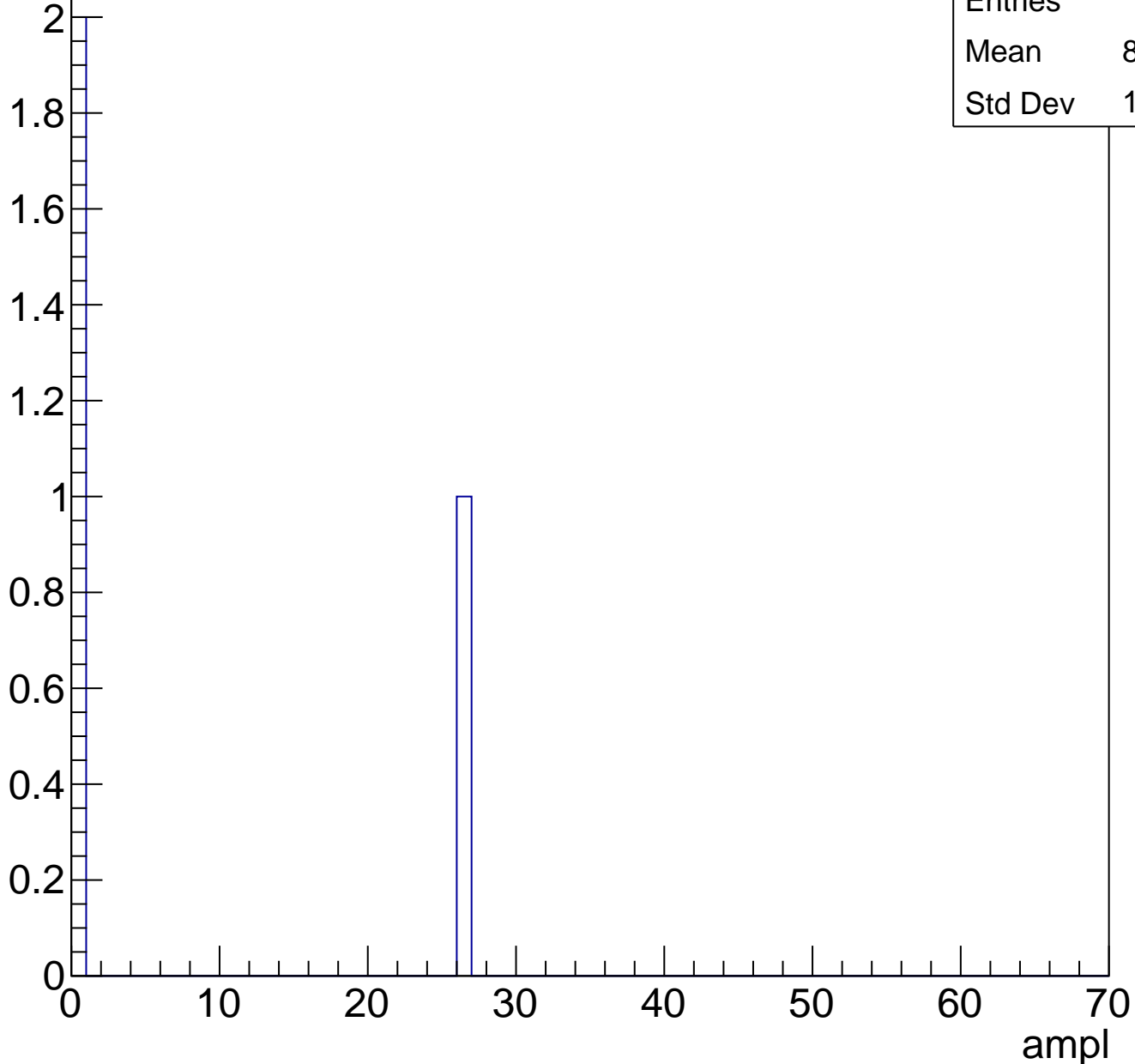
Entry



B0L002S, U26-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8.667
Std Dev	12.26

B0L002S, U26-ch113, adc0

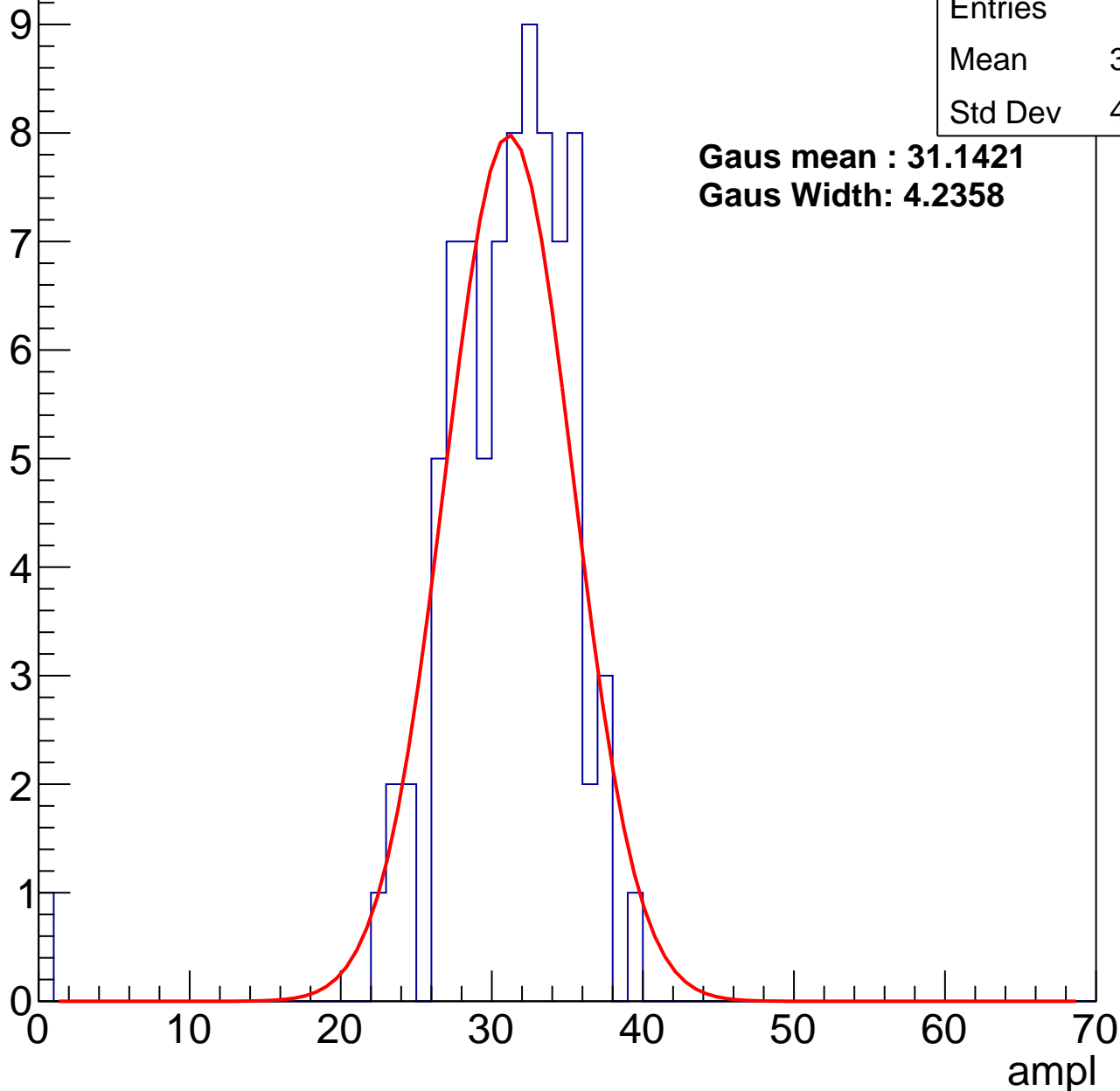
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	30.43
Std Dev	4.936

Gaus mean : 31.1421

Gaus Width: 4.2358



B0L002S, U26-ch113, adc1

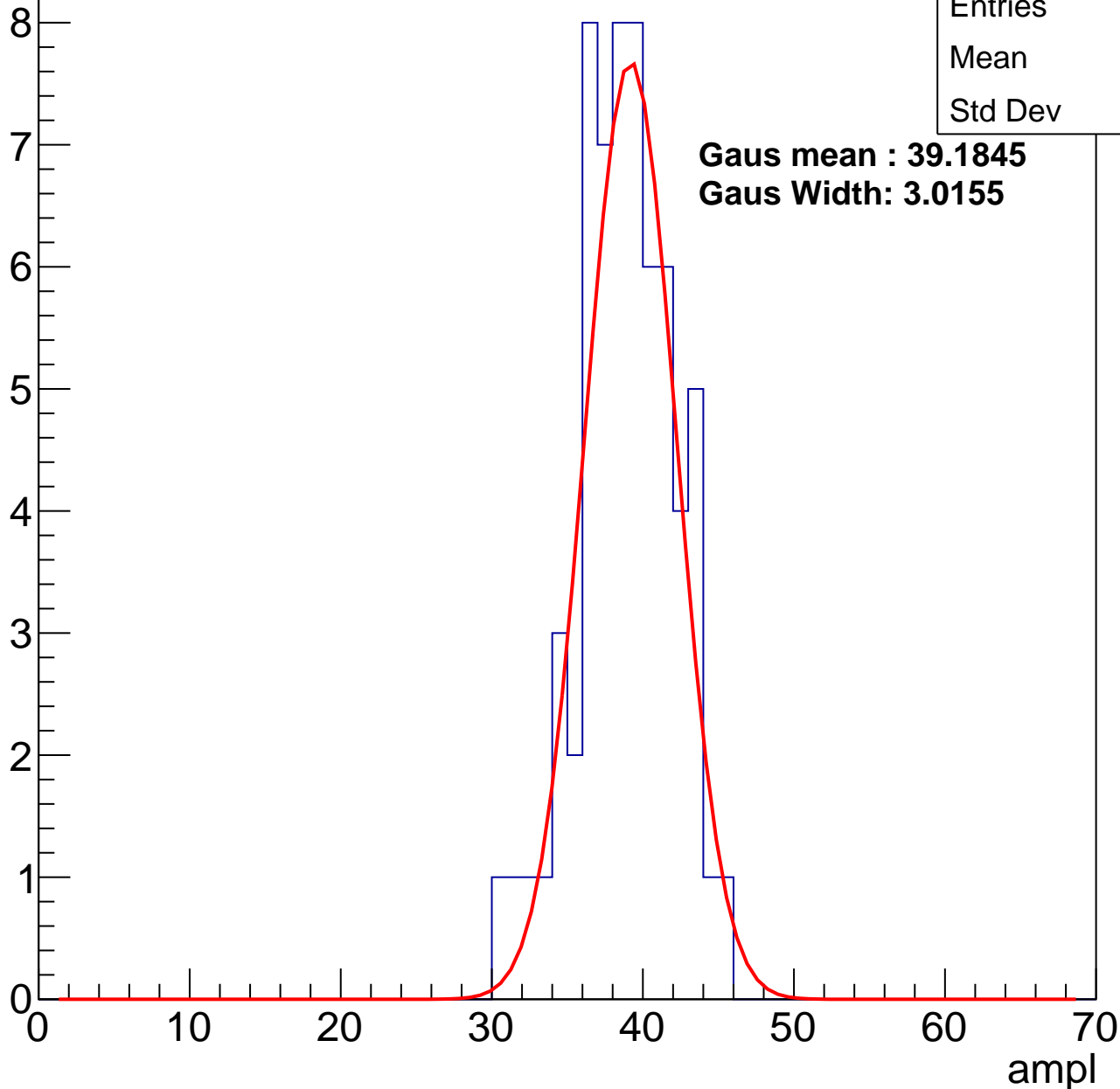
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.4
Std Dev	3.16

Gaus mean : 39.1845

Gaus Width: 3.0155



B0L002S, U26-ch113, adc2

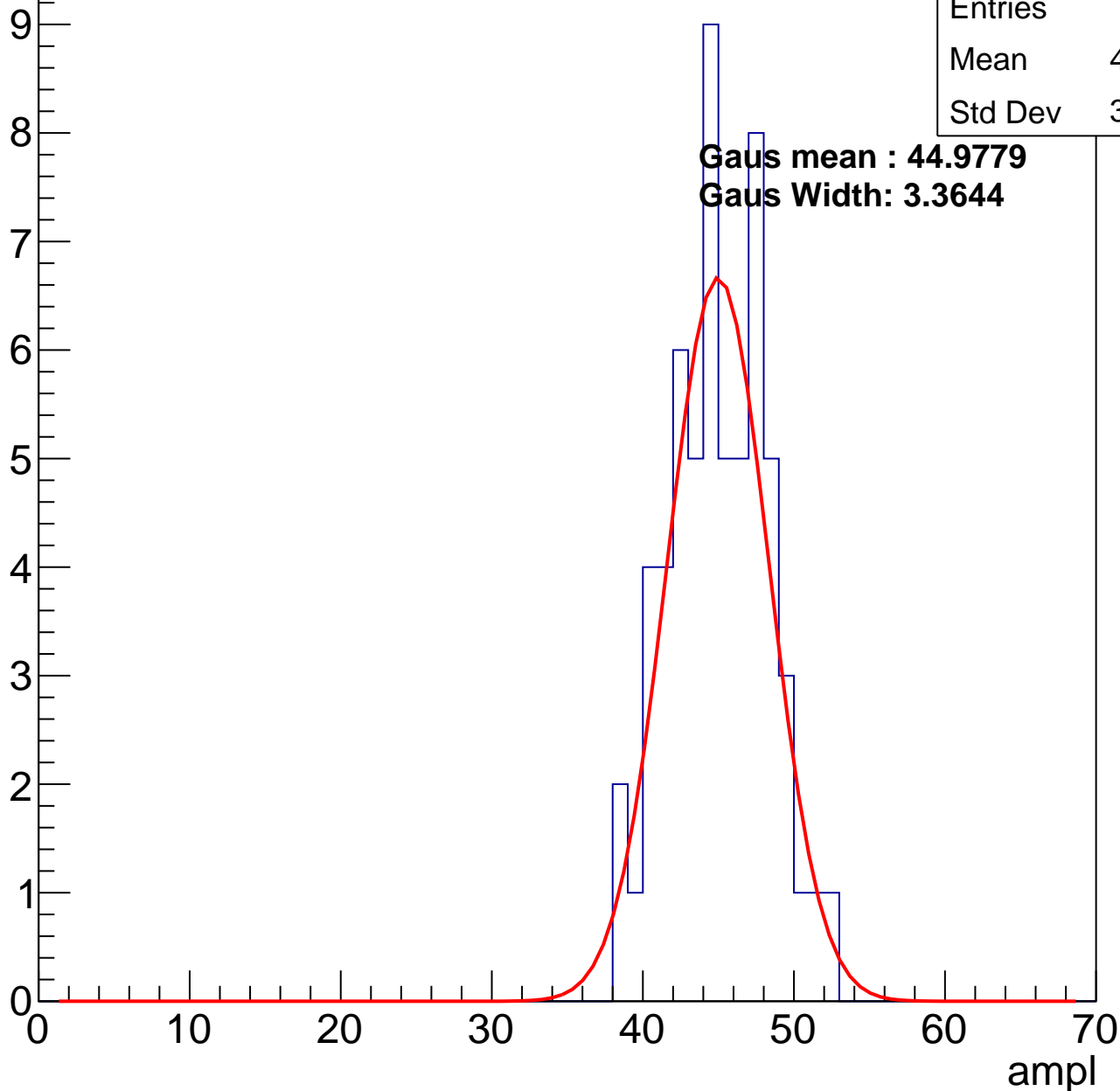
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.55
Std Dev	3.186

Gaus mean : 44.9779

Gaus Width: 3.3644

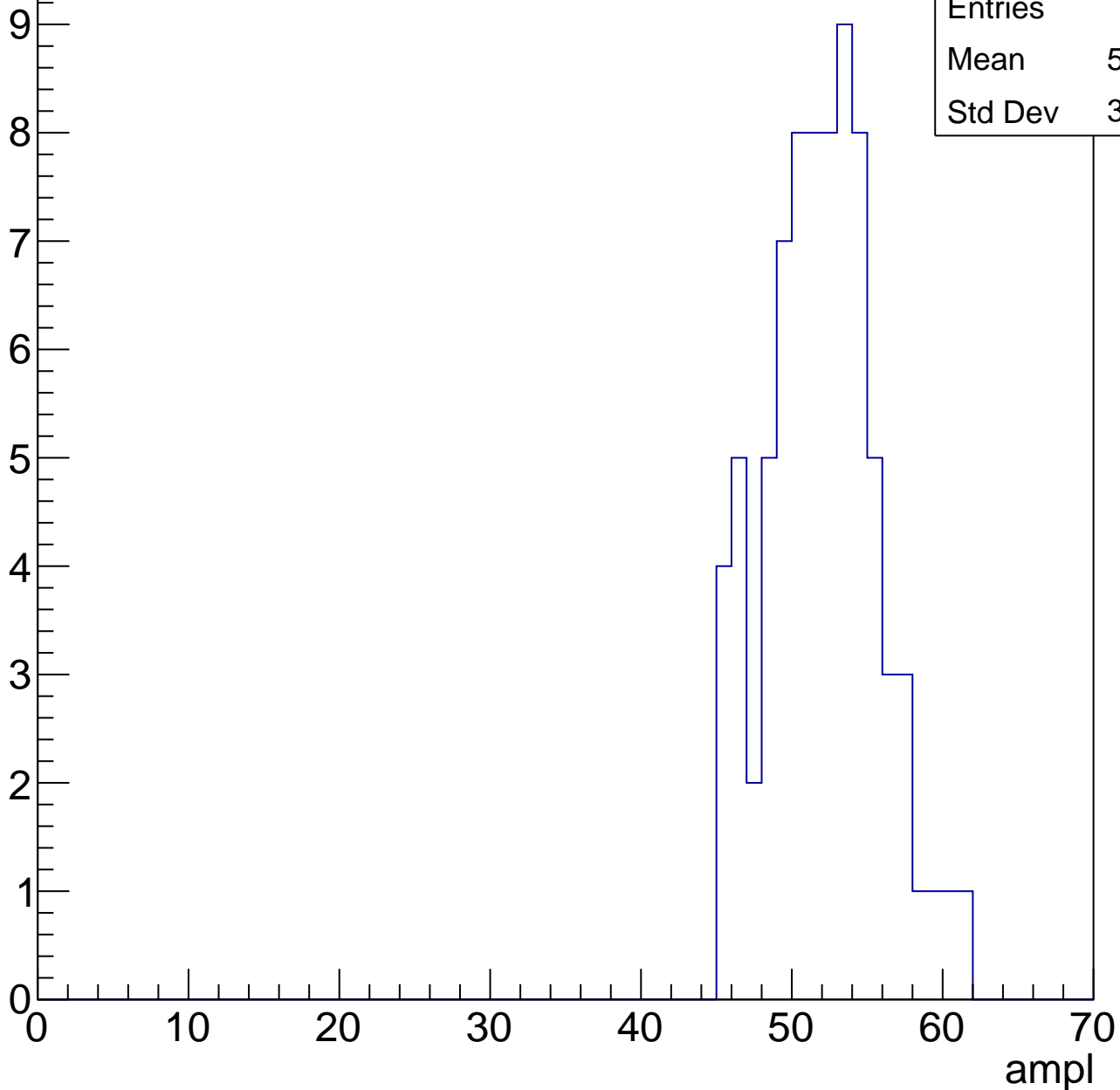


B0L002S, U26-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

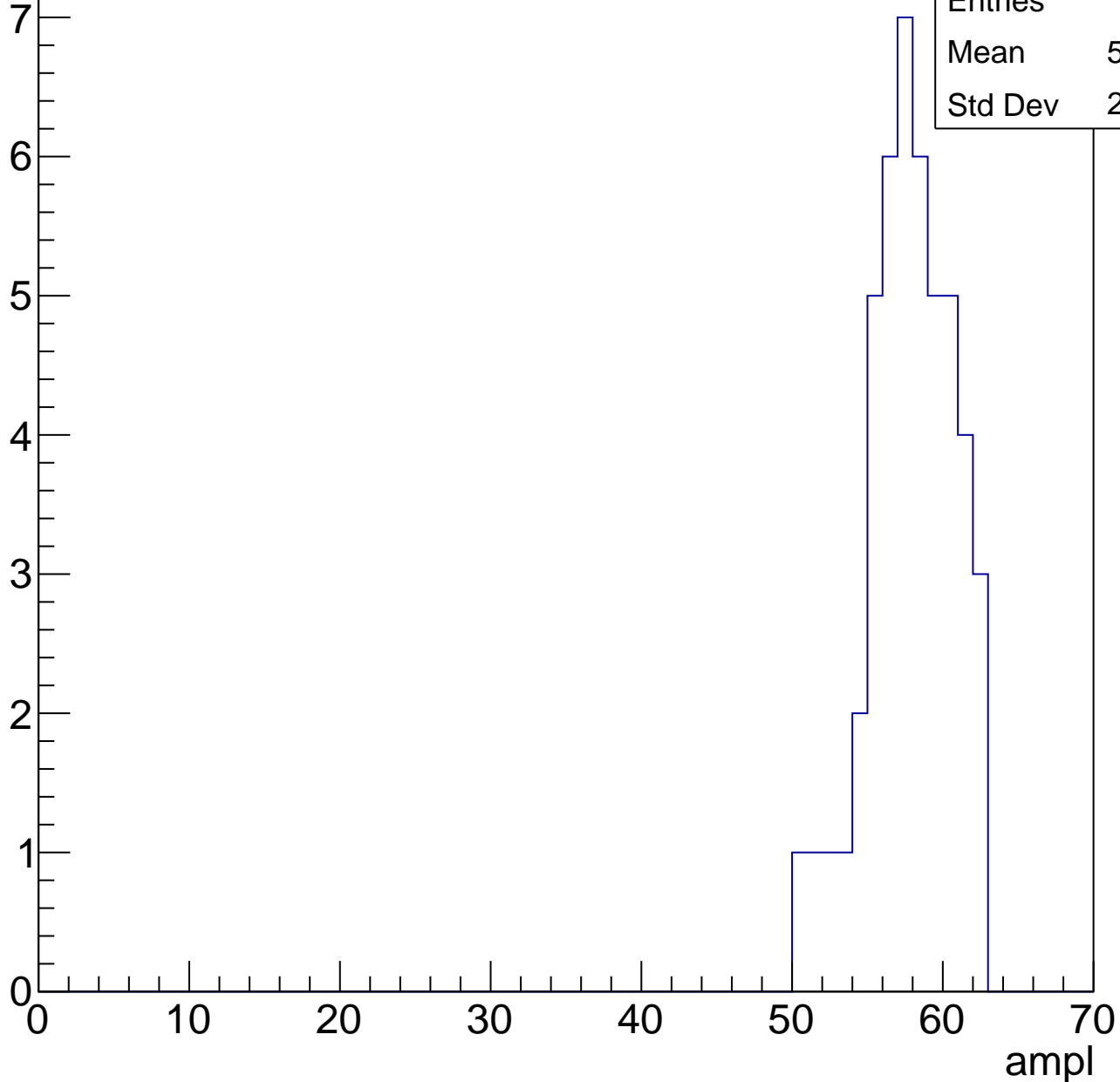
Entries	79
Mean	51.54
Std Dev	3.614



B0L002S, U26-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	47
Mean	57.38
Std Dev	2.818

B0L002S, U26-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	35
Mean	59.29
Std Dev	10.33

Entry

10

8

6

4

2

0

0

10

20

30

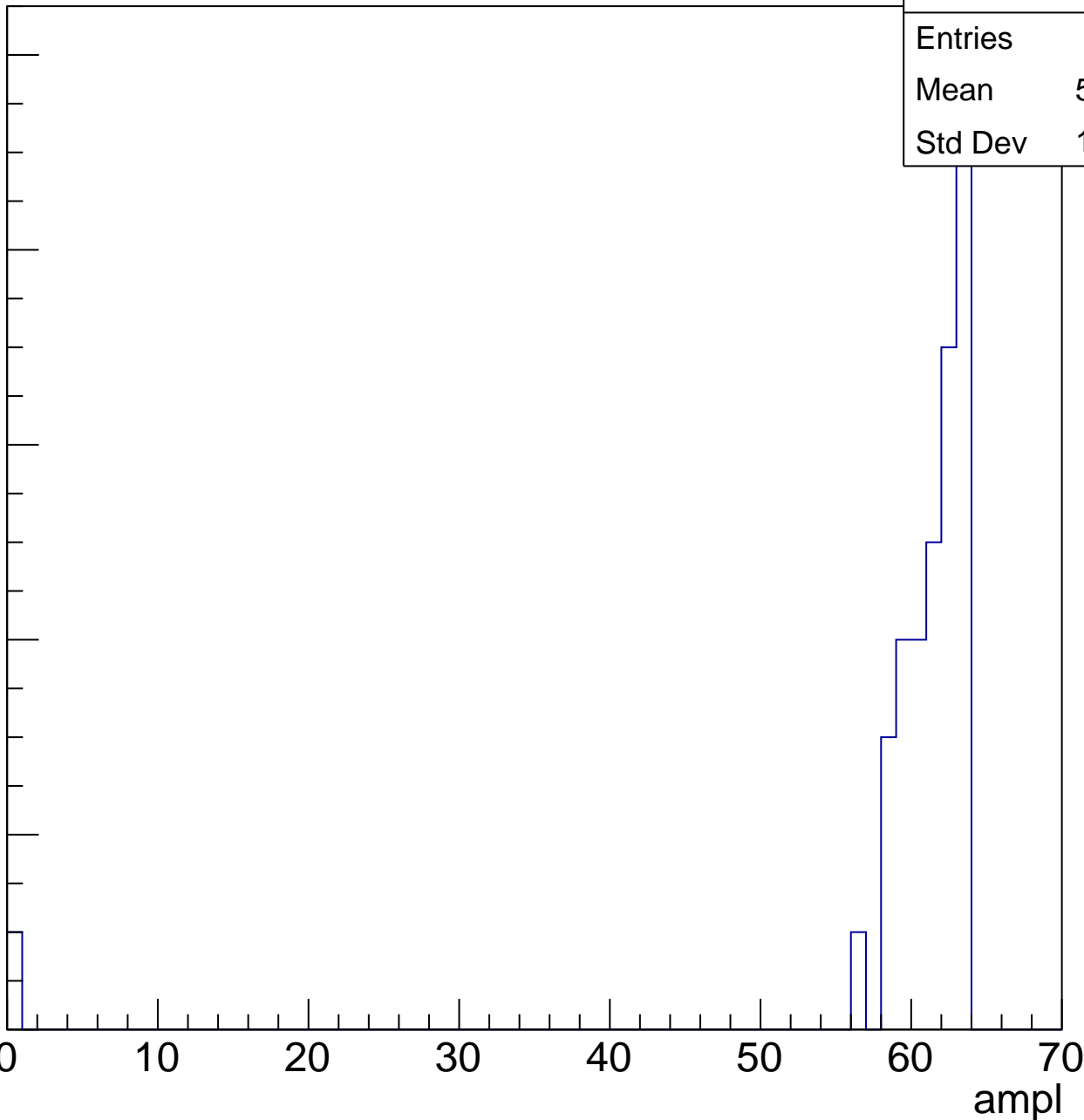
40

50

60

70

ampl



B0L002S, U26-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U26-ch114, adc0

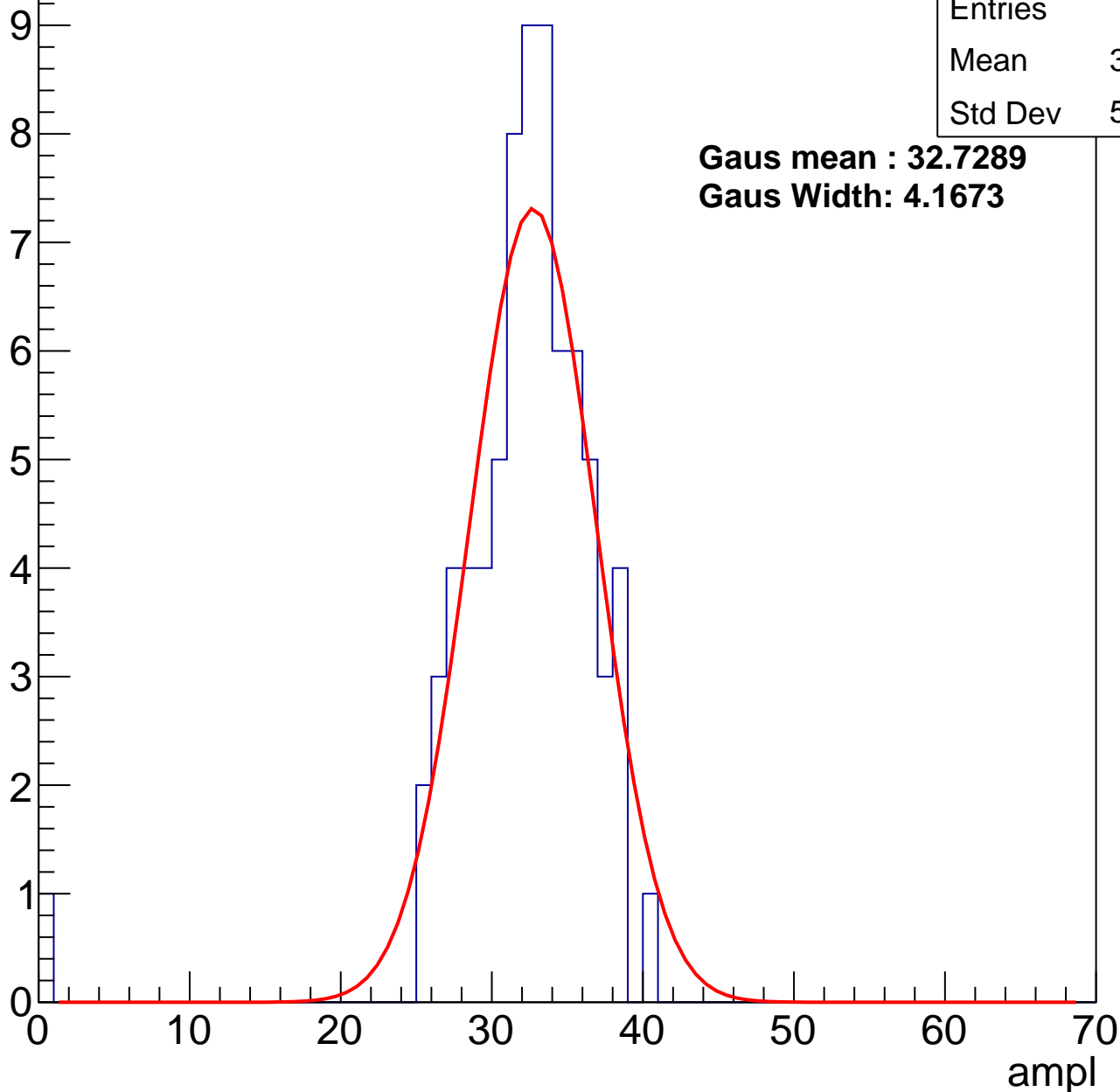
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	31.68
Std Dev	5.067

Gaus mean : 32.7289

Gaus Width: 4.1673



B0L002S, U26-ch114, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	39.88
Std Dev	3.146

Gaus mean : 40.5481

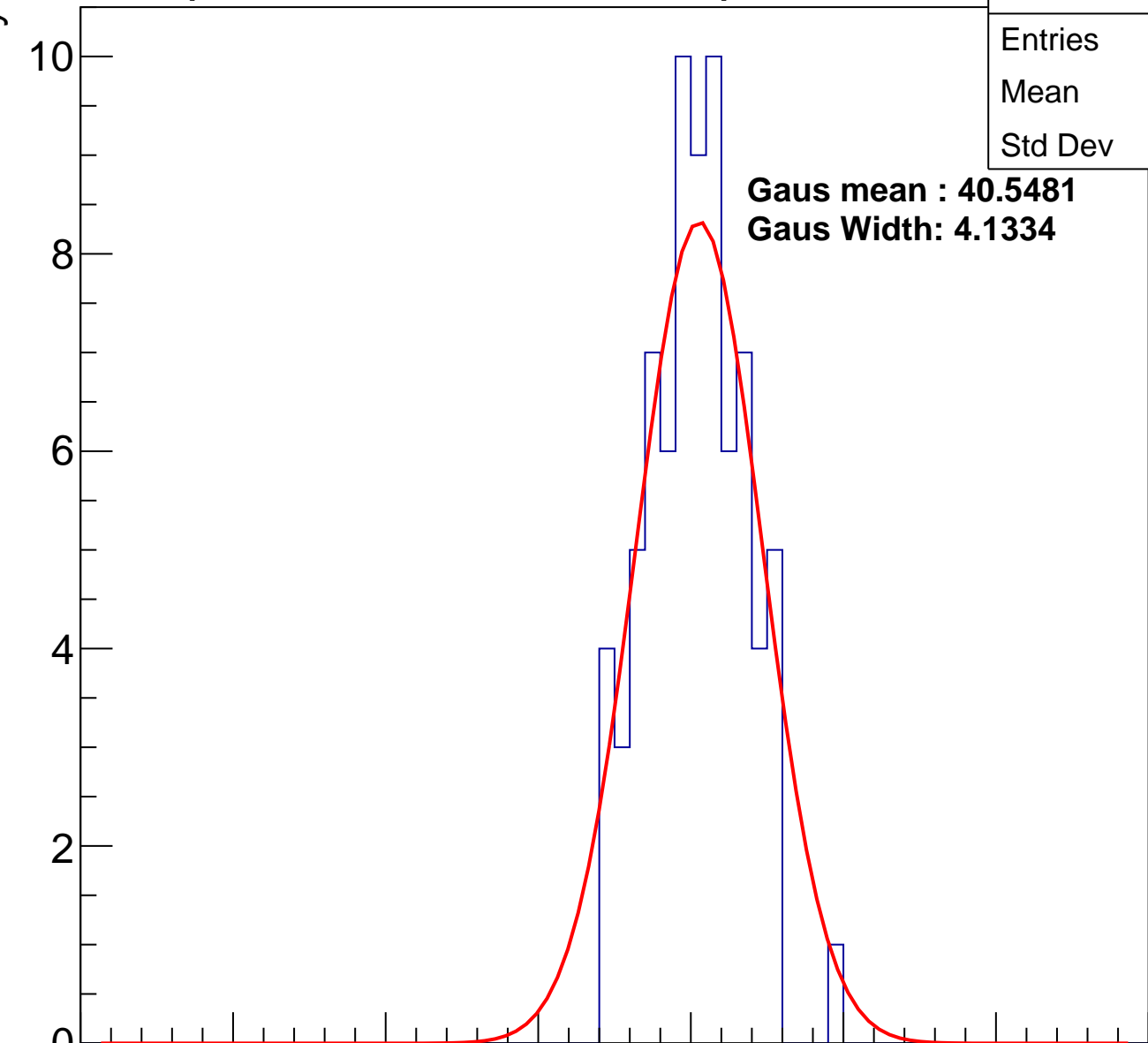
Gaus Width: 4.1334

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U26-ch114, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	46.84
Std Dev	2.65

Gaus mean : 47.0574

Gaus Width: 2.6312

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

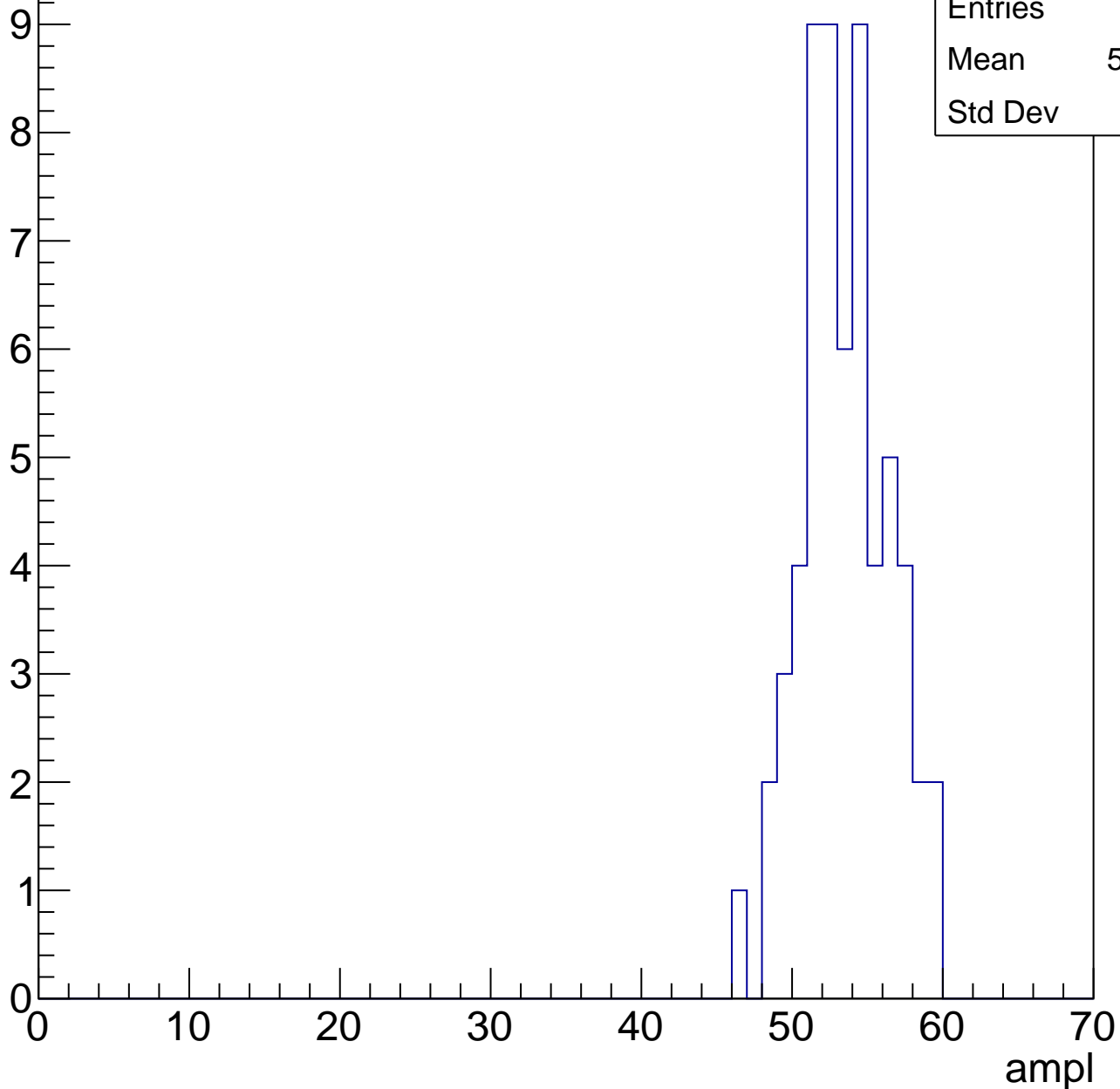
70

B0L002S, U26-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	53.03
Std Dev	2.84



B0L002S, U26-ch114, adc4

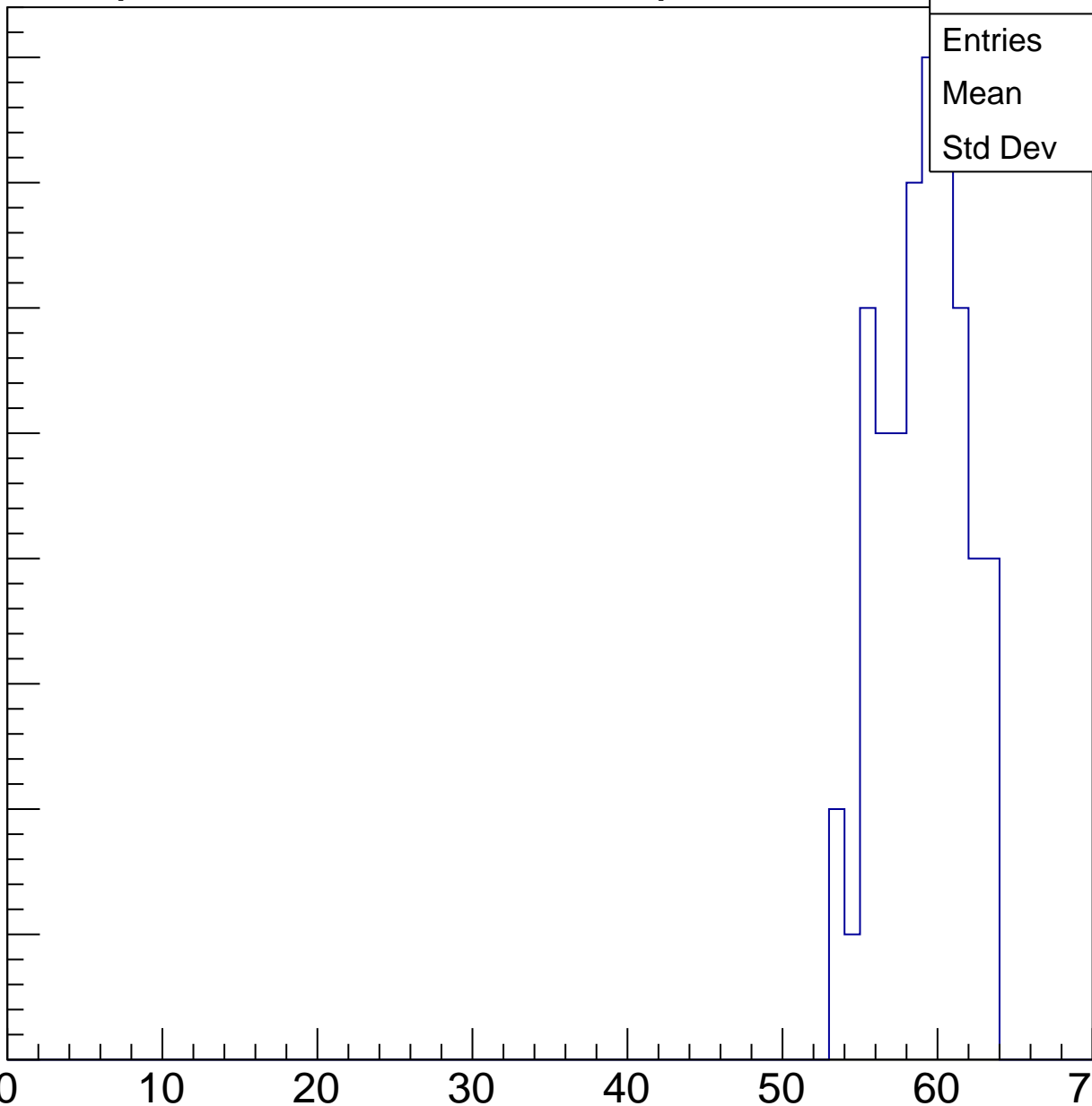
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.55
Std Dev	2.625

ampl

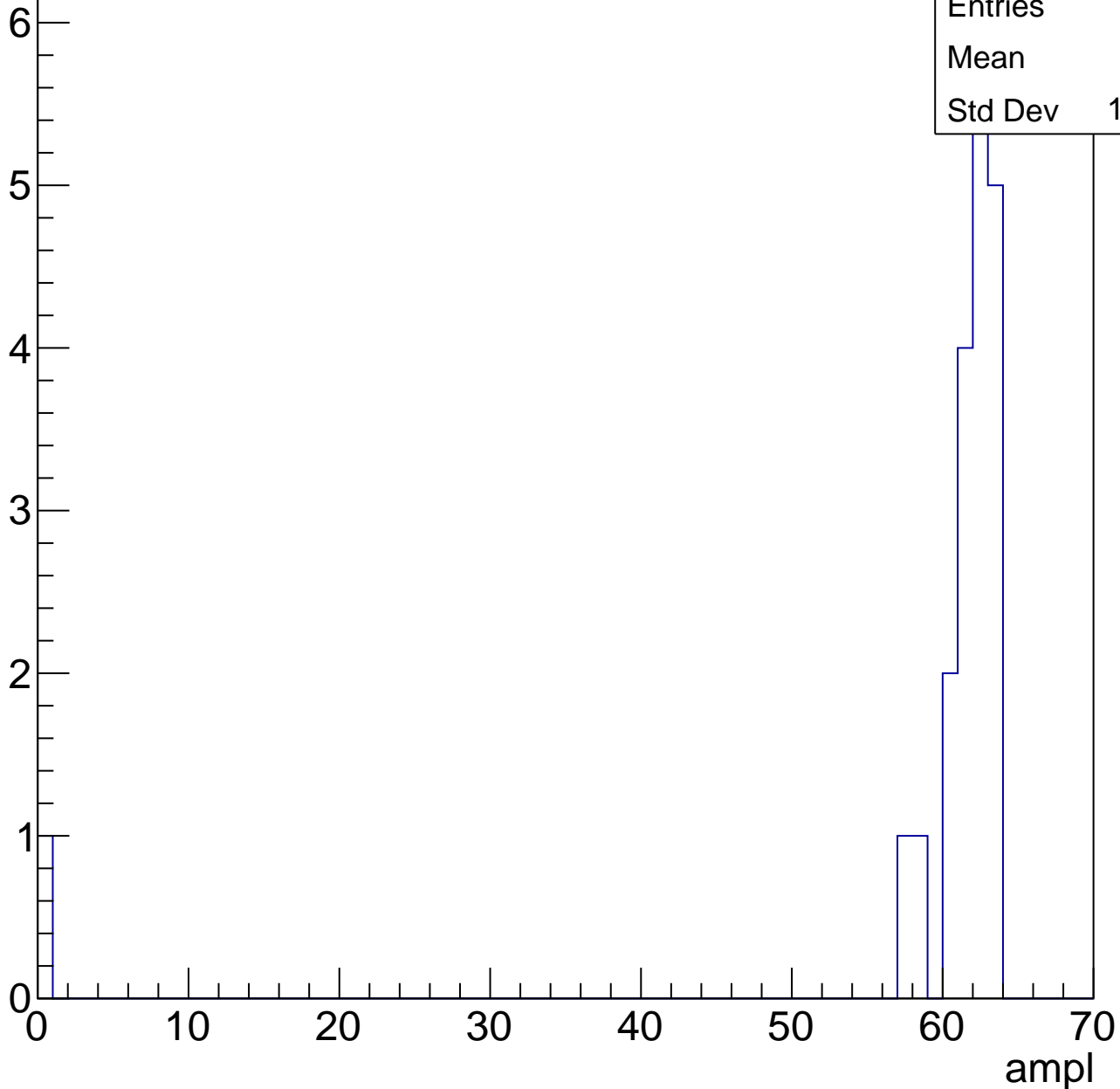


B0L002S, U26-ch114, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58.3
Std Dev	13.47



B0L002S, U26-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U26-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L002S, U26-ch115, adc0

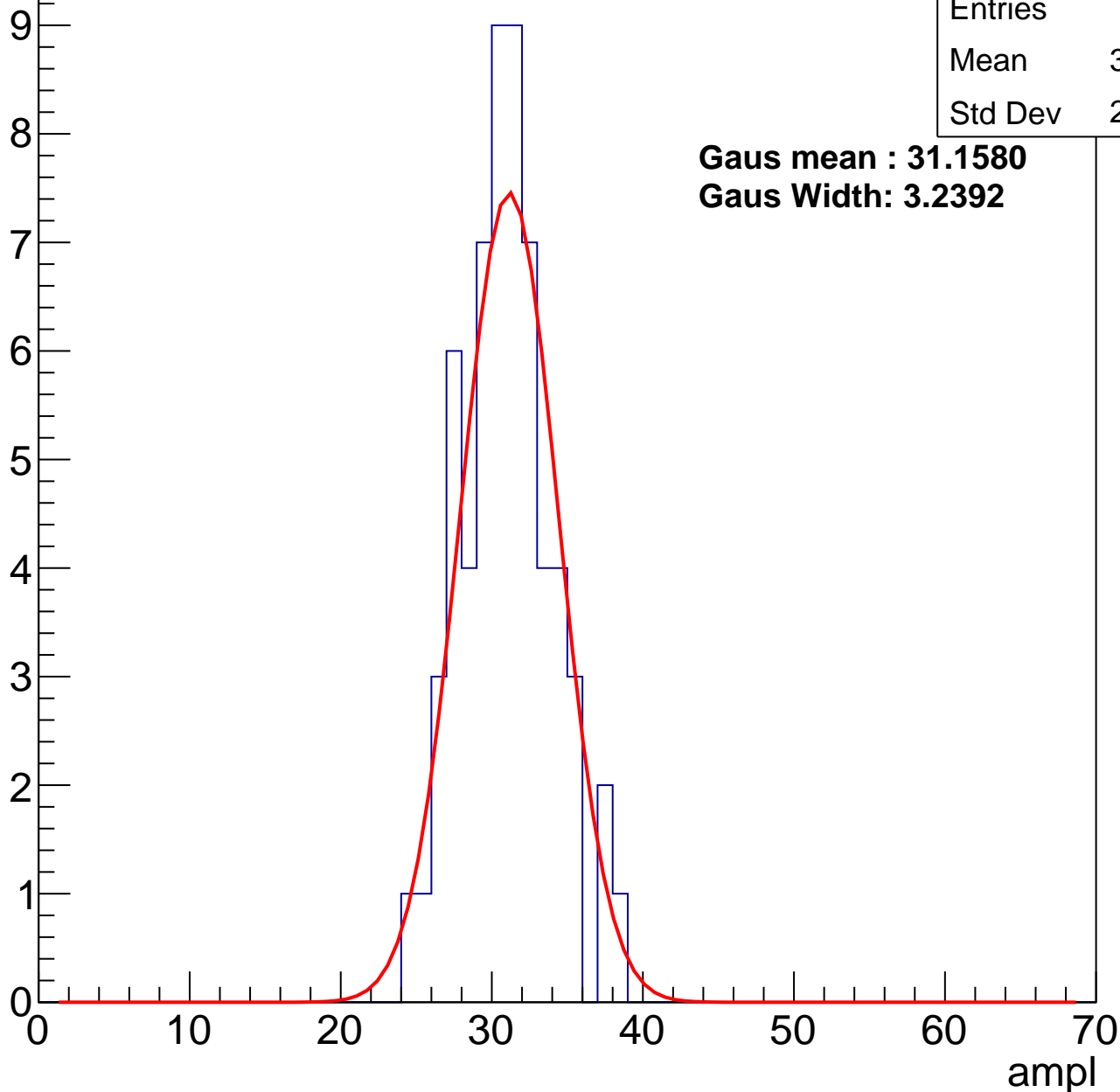
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	30.52
Std Dev	2.984

Gaus mean : 31.1580

Gaus Width: 3.2392



B0L002S, U26-ch115, adc1

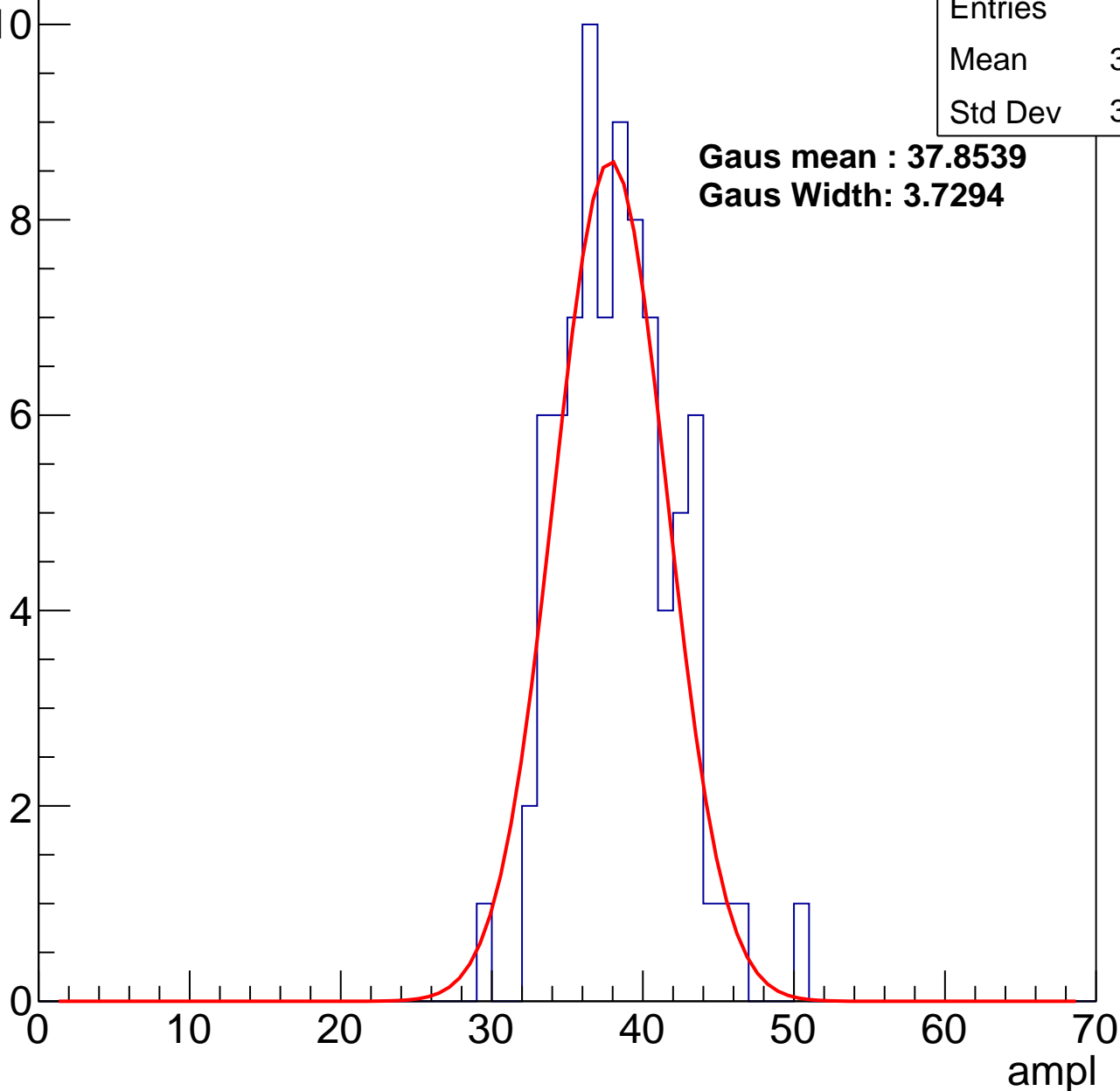
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	37.93
Std Dev	3.669

Gaus mean : 37.8539

Gaus Width: 3.7294



B0L002S, U26-ch115, adc2

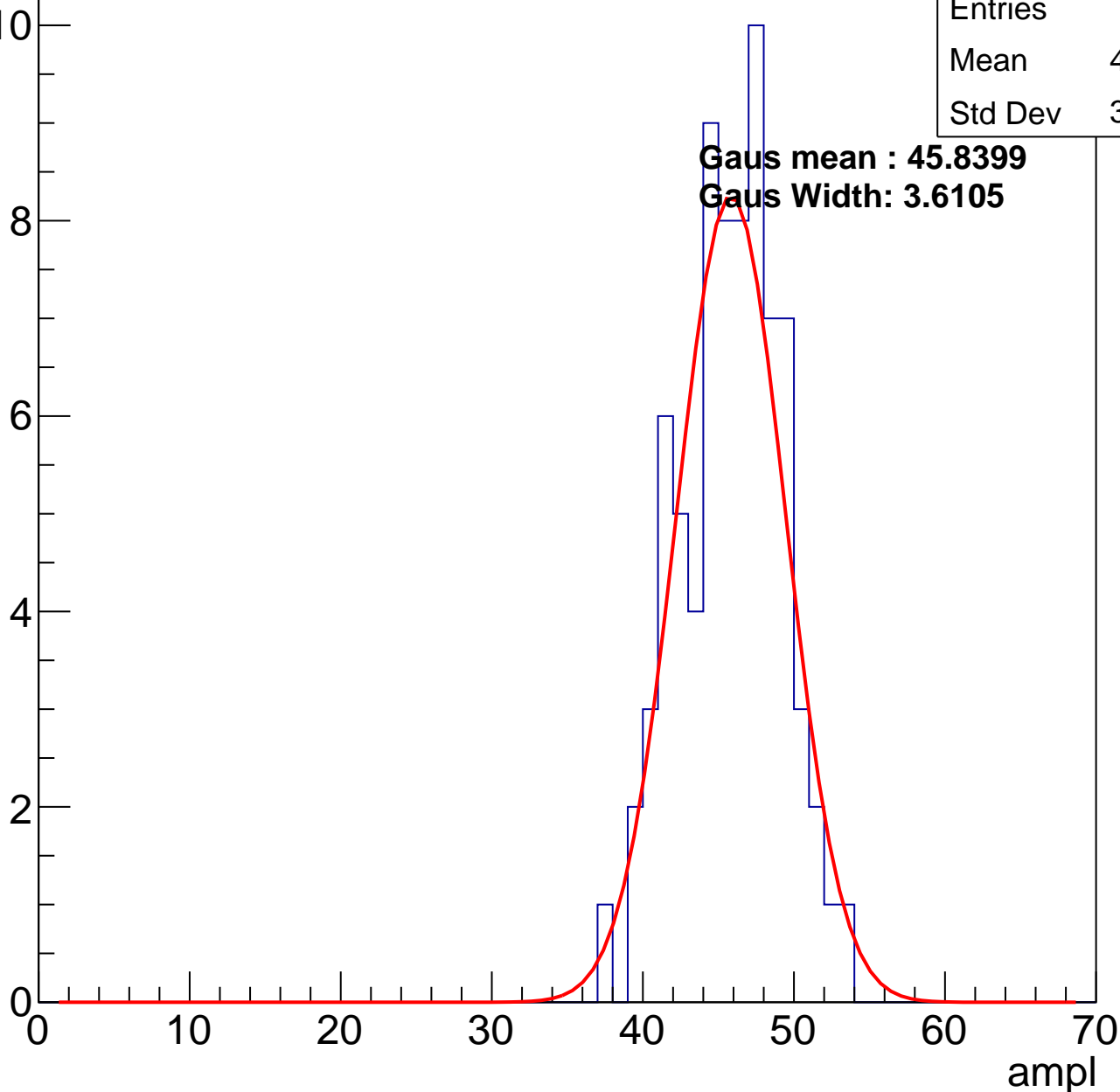
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	45.36
Std Dev	3.322

Gaus mean : 45.8399

Gaus Width: 3.6105

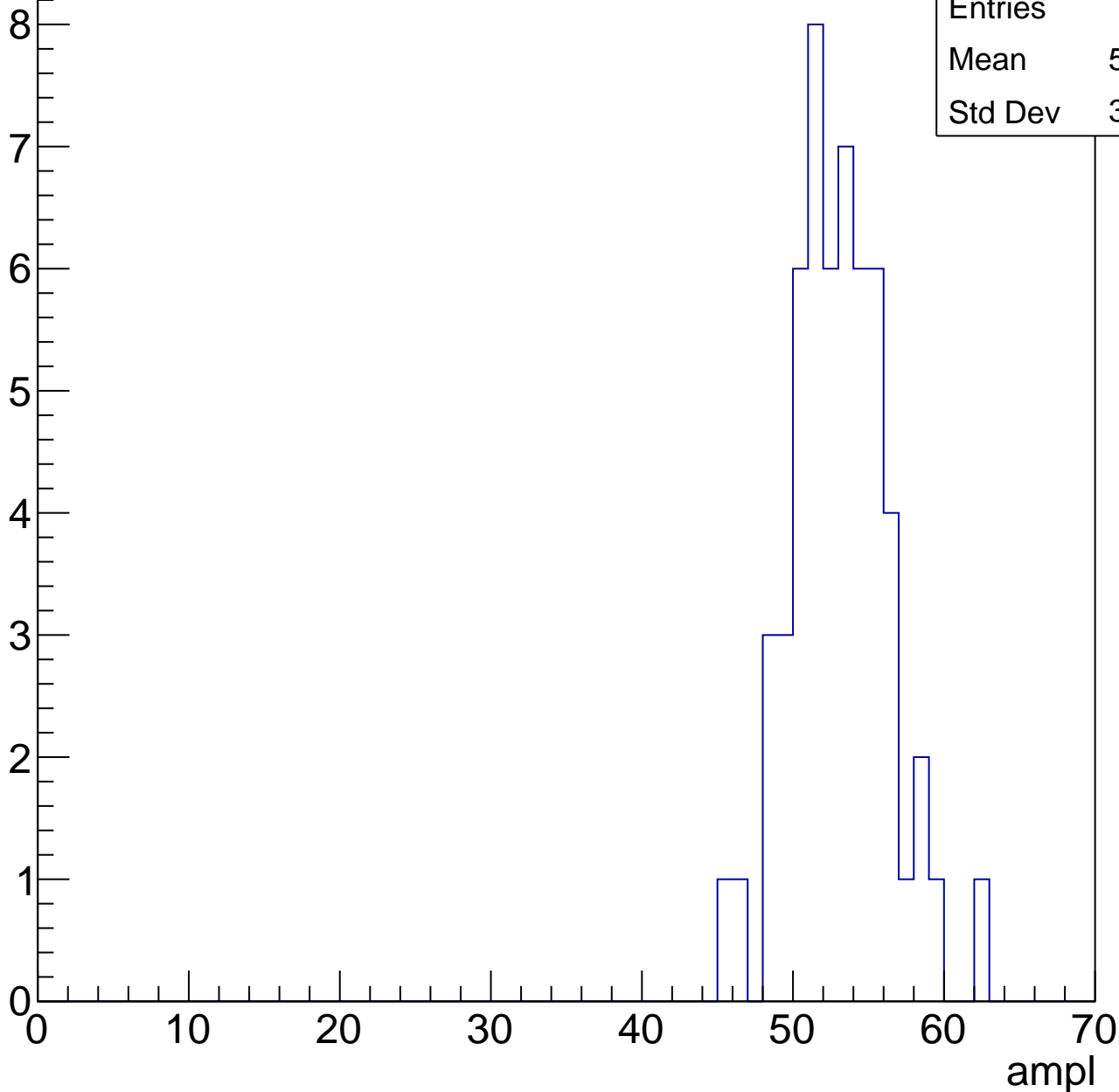


B0L002S, U26-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	52.59
Std Dev	3.184

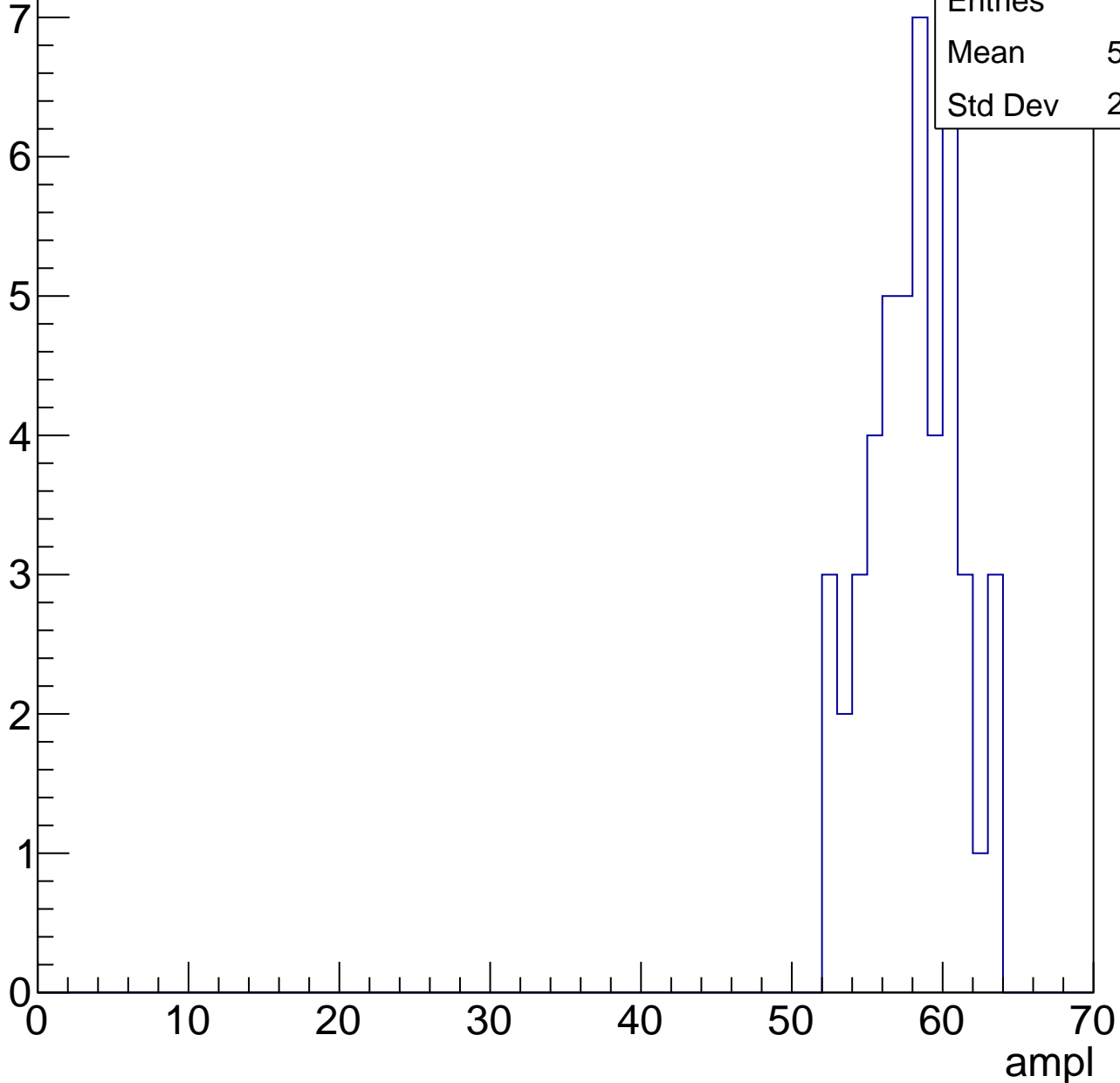


B0L002S, U26-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	57.55
Std Dev	2.945

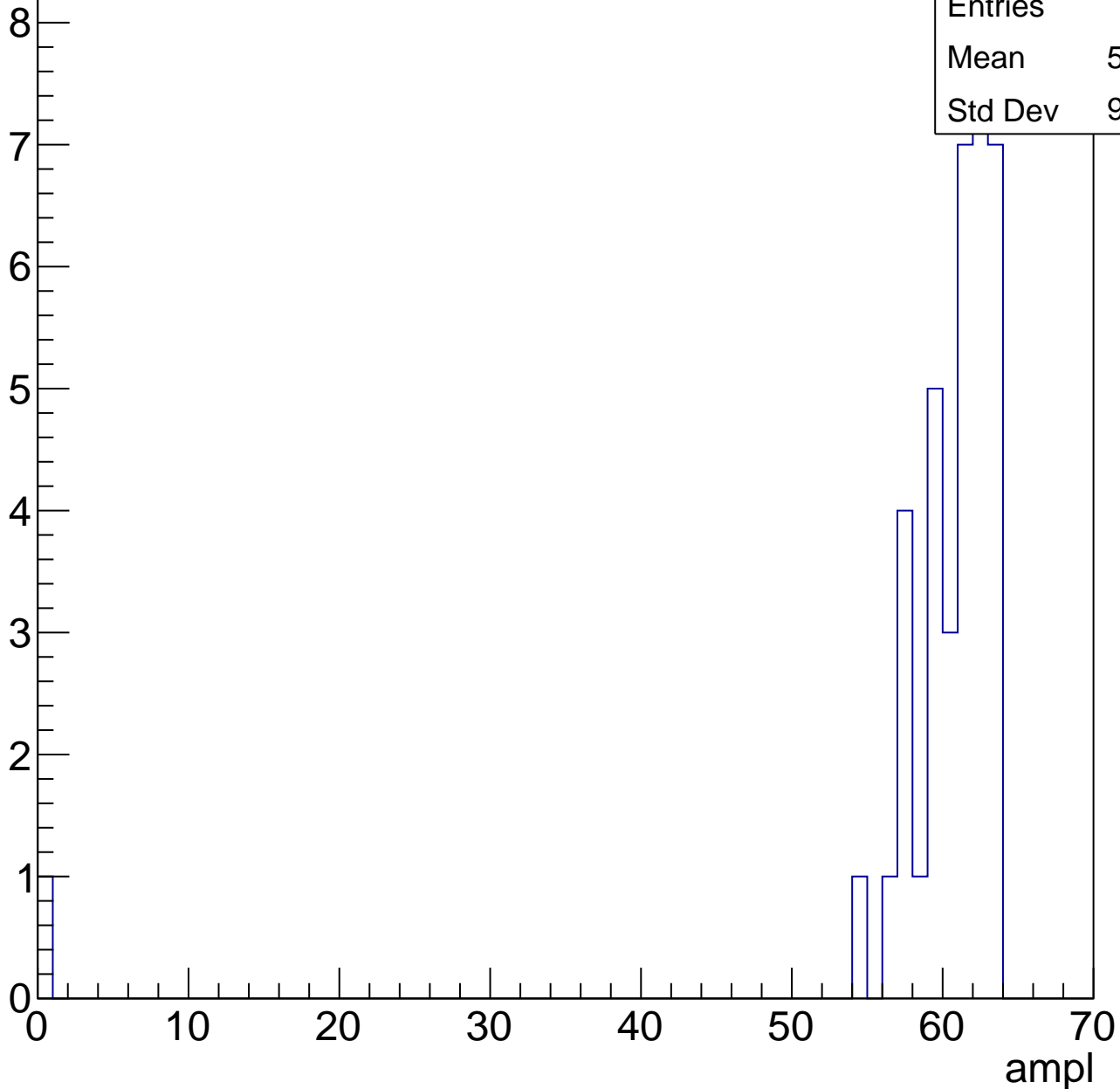


B0L002S, U26-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.82
Std Dev	9.928



B0L002S, U26-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch116, adc0

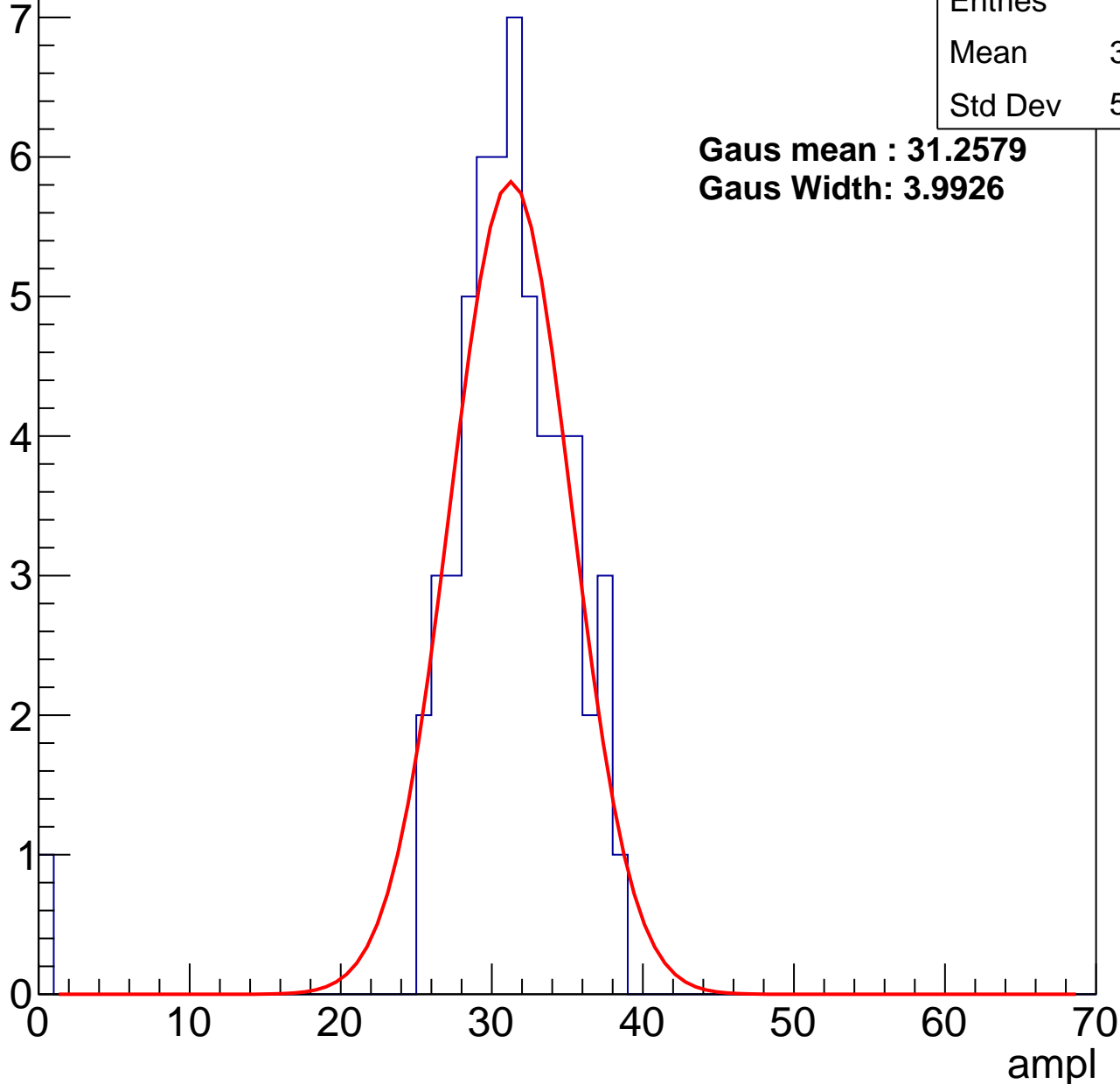
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	30.52
Std Dev	5.254

Gaus mean : 31.2579

Gaus Width: 3.9926



B0L002S, U26-ch116, adc1

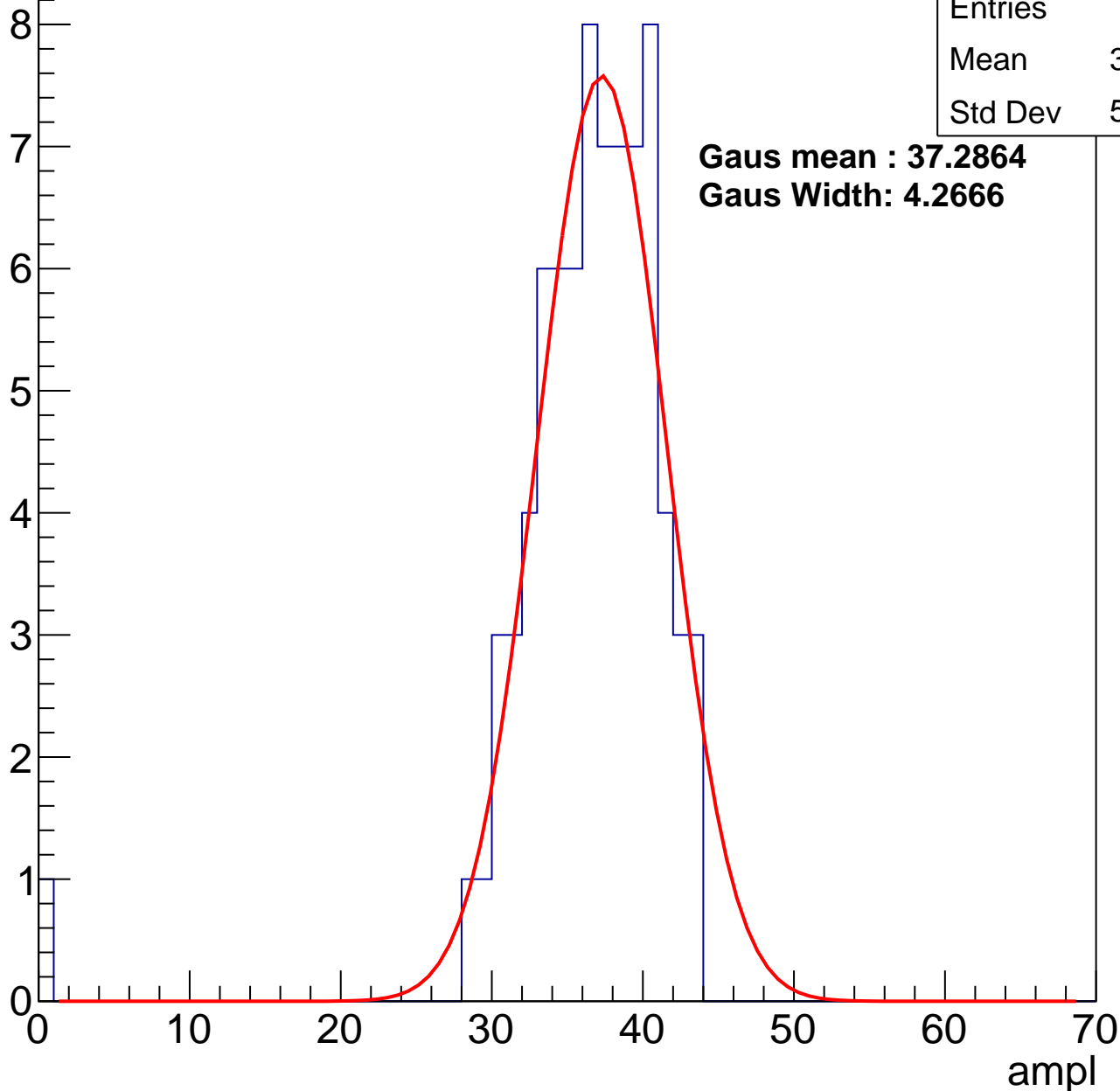
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	35.96
Std Dev	5.455

Gaus mean : 37.2864

Gaus Width: 4.2666



B0L002S, U26-ch116, adc2

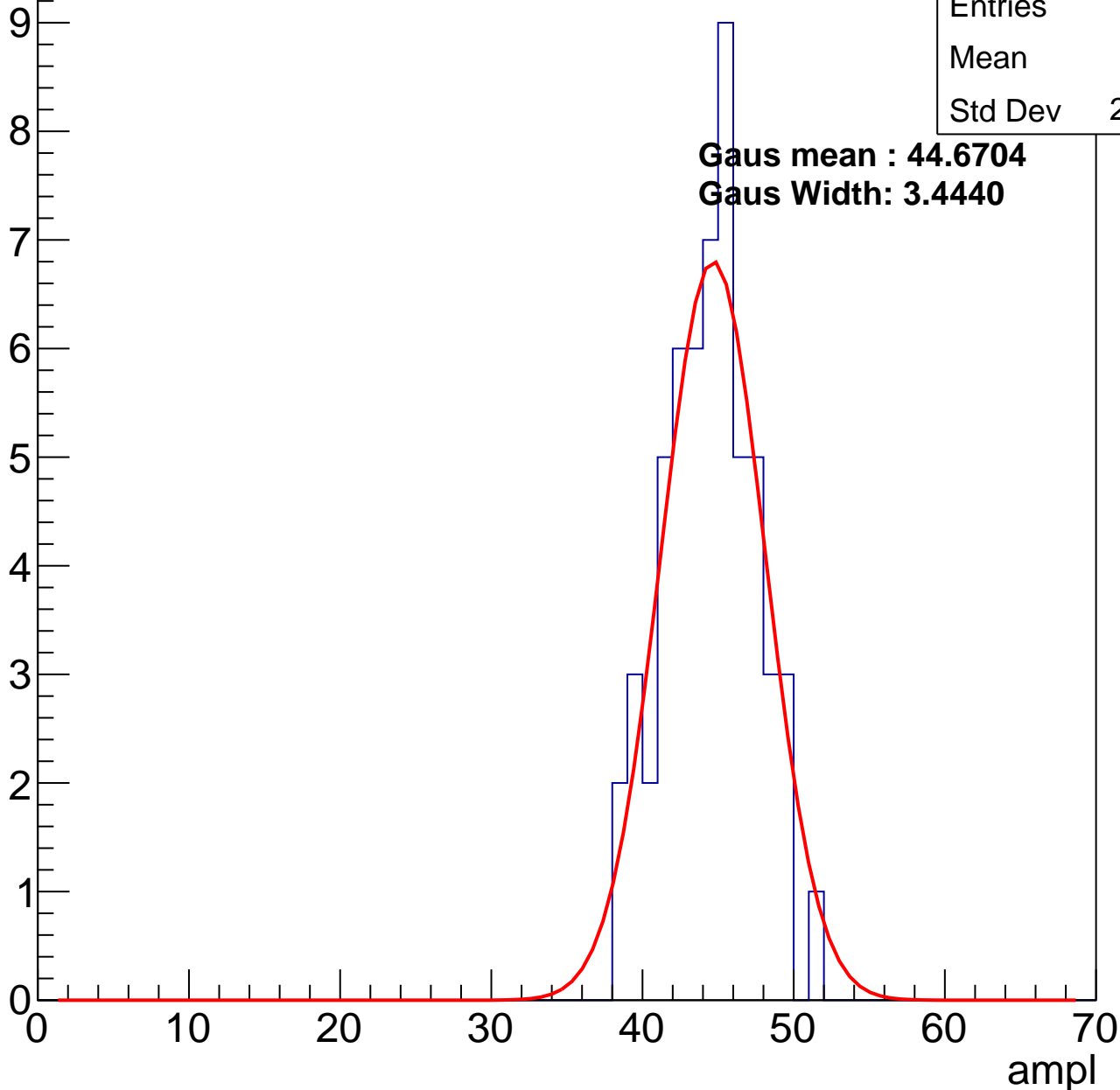
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44
Std Dev	2.962

Gaus mean : 44.6704

Gaus Width: 3.4440

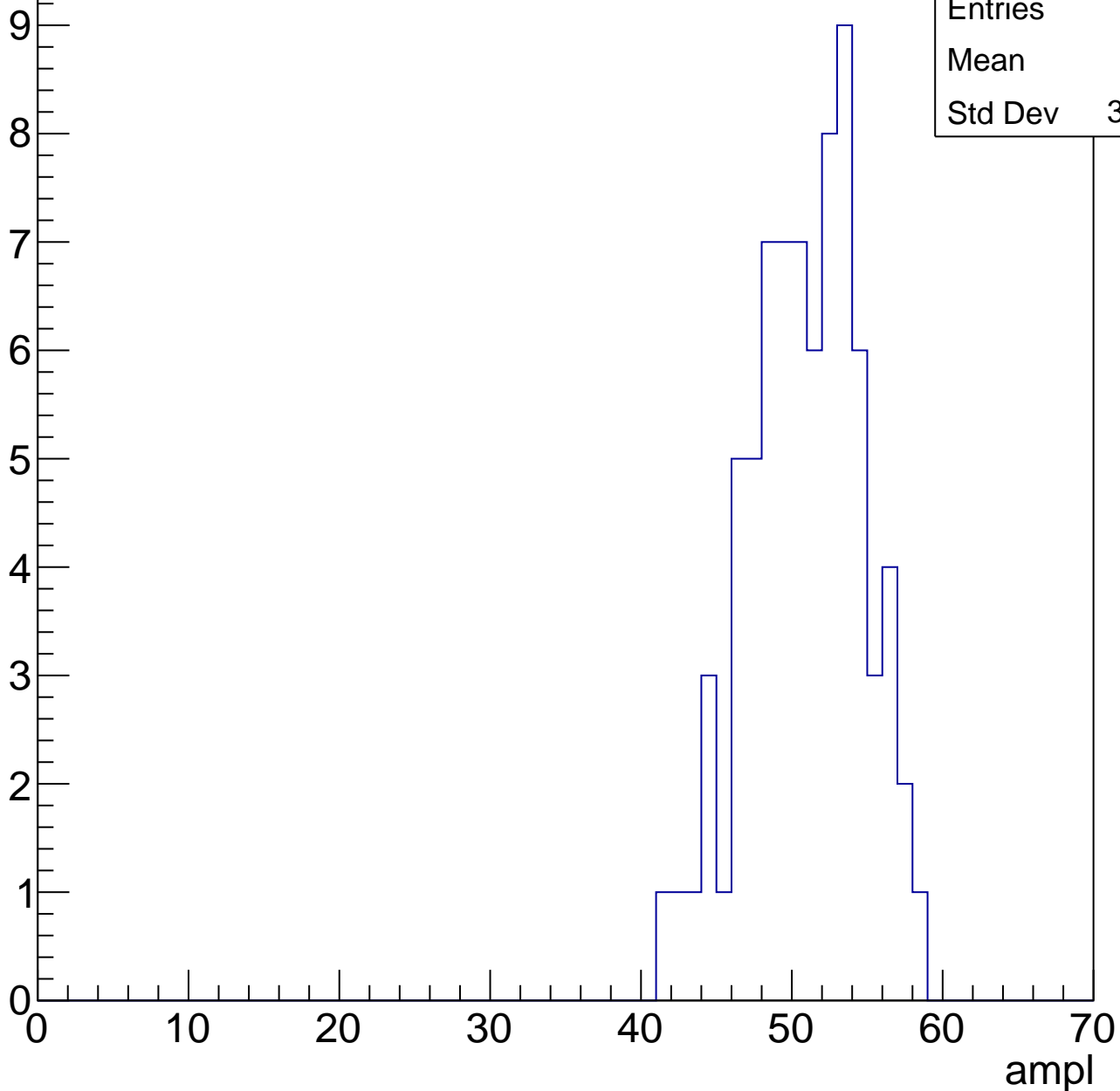


B0L002S, U26-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

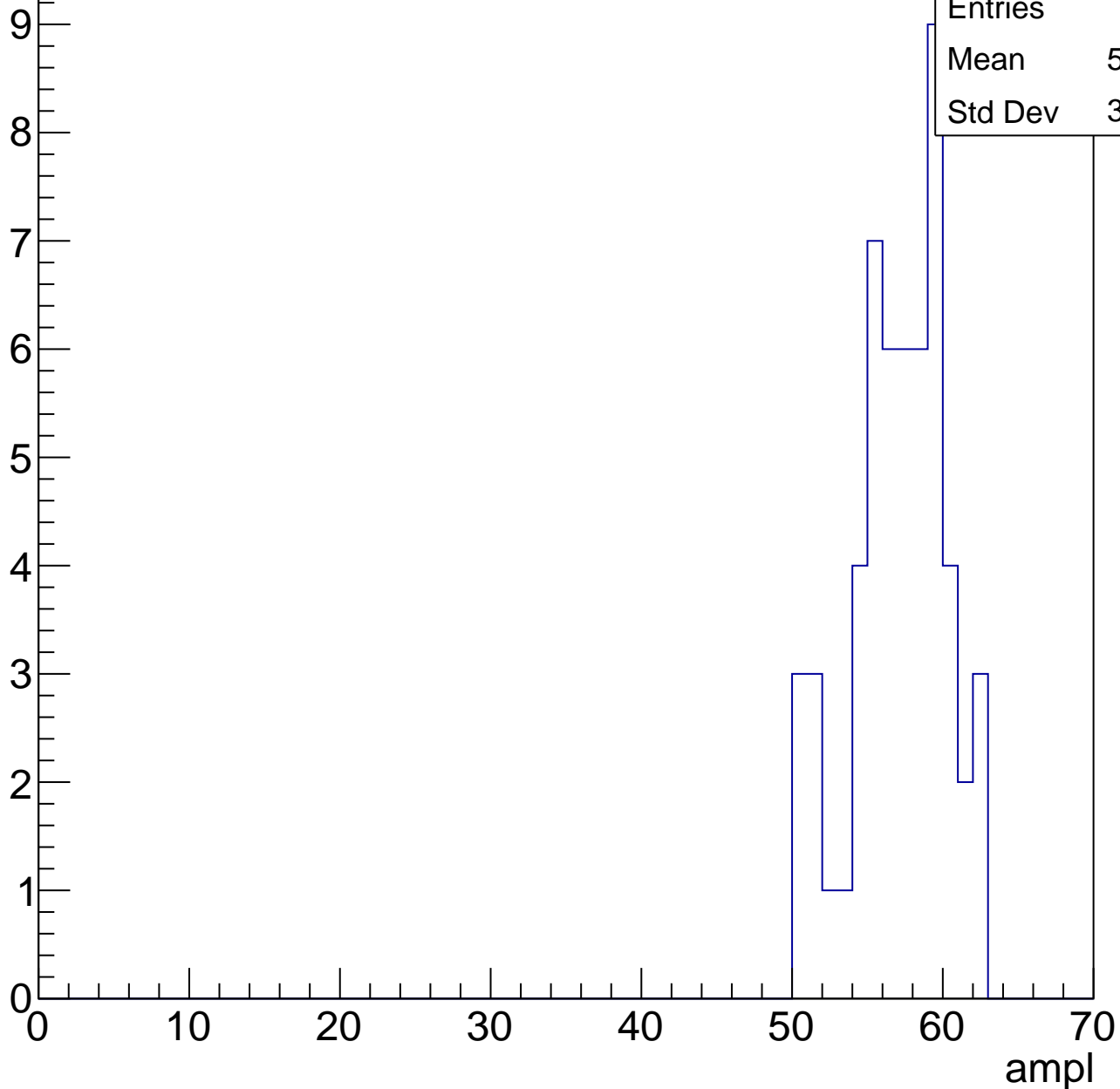
Entries	77
Mean	50.4
Std Dev	3.729



B0L002S, U26-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



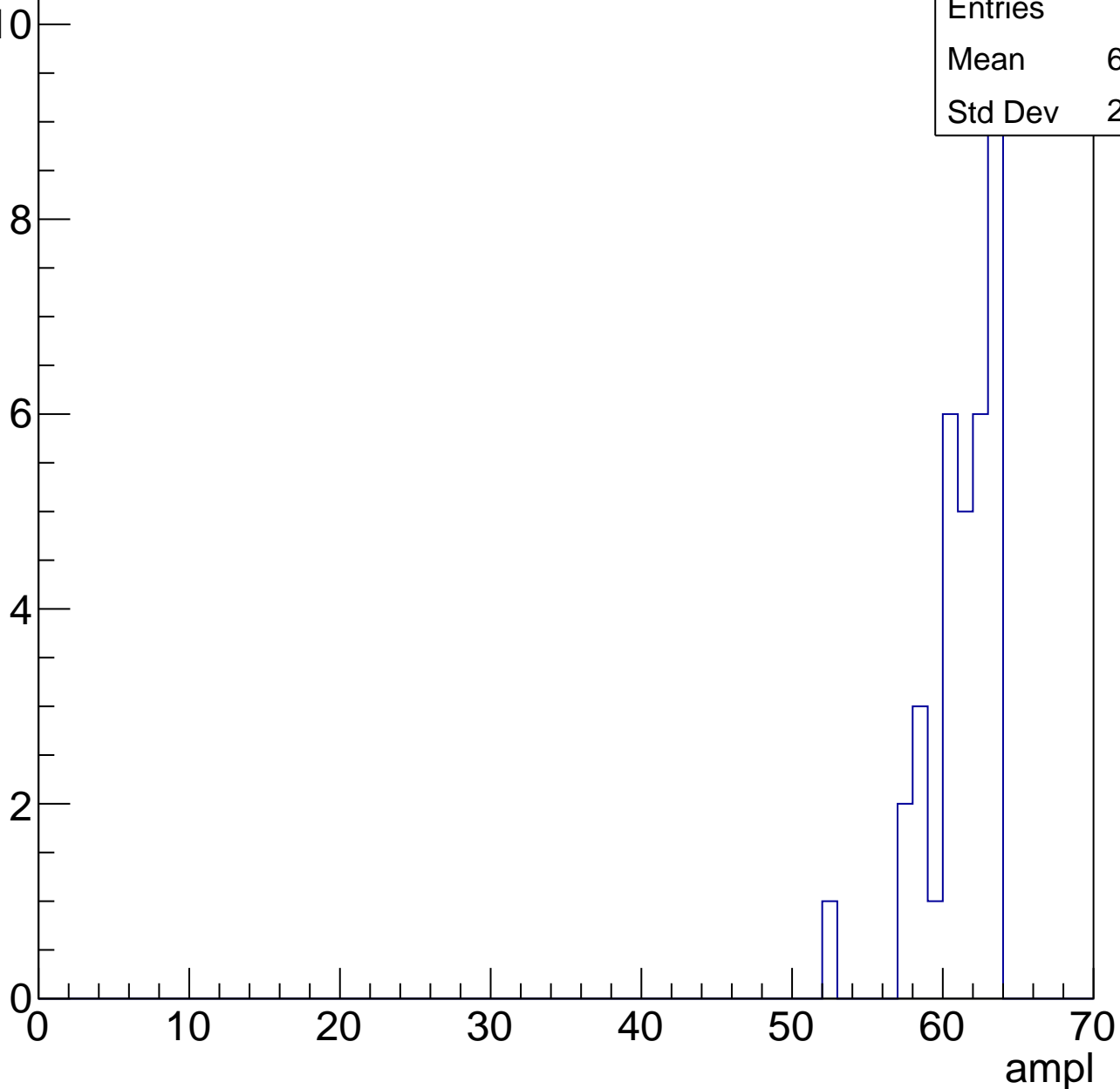
Entries	55
Mean	56.62
Std Dev	3.142

B0L002S, U26-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

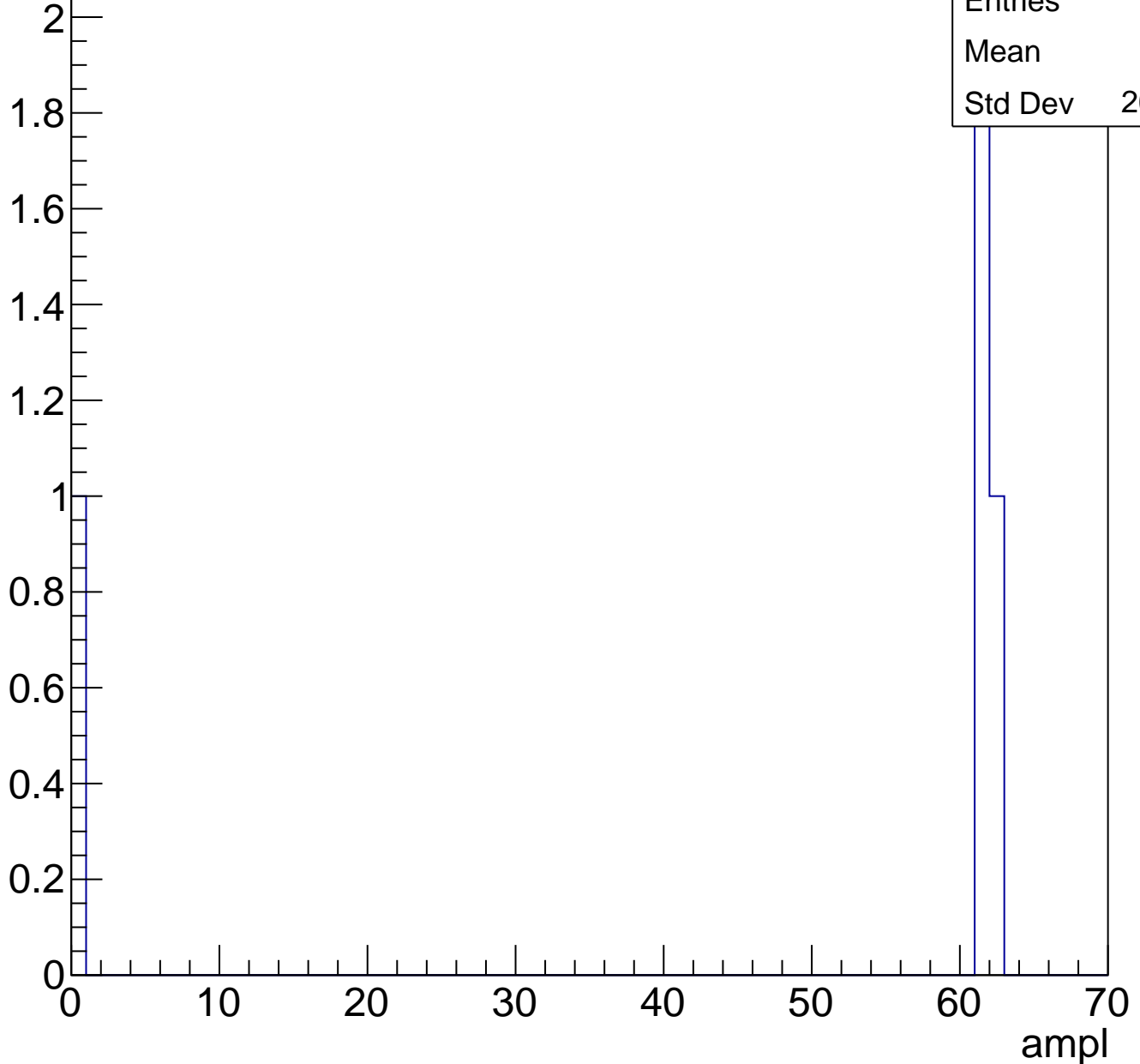
Entries	34
Mean	60.76
Std Dev	2.389



B0L002S, U26-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

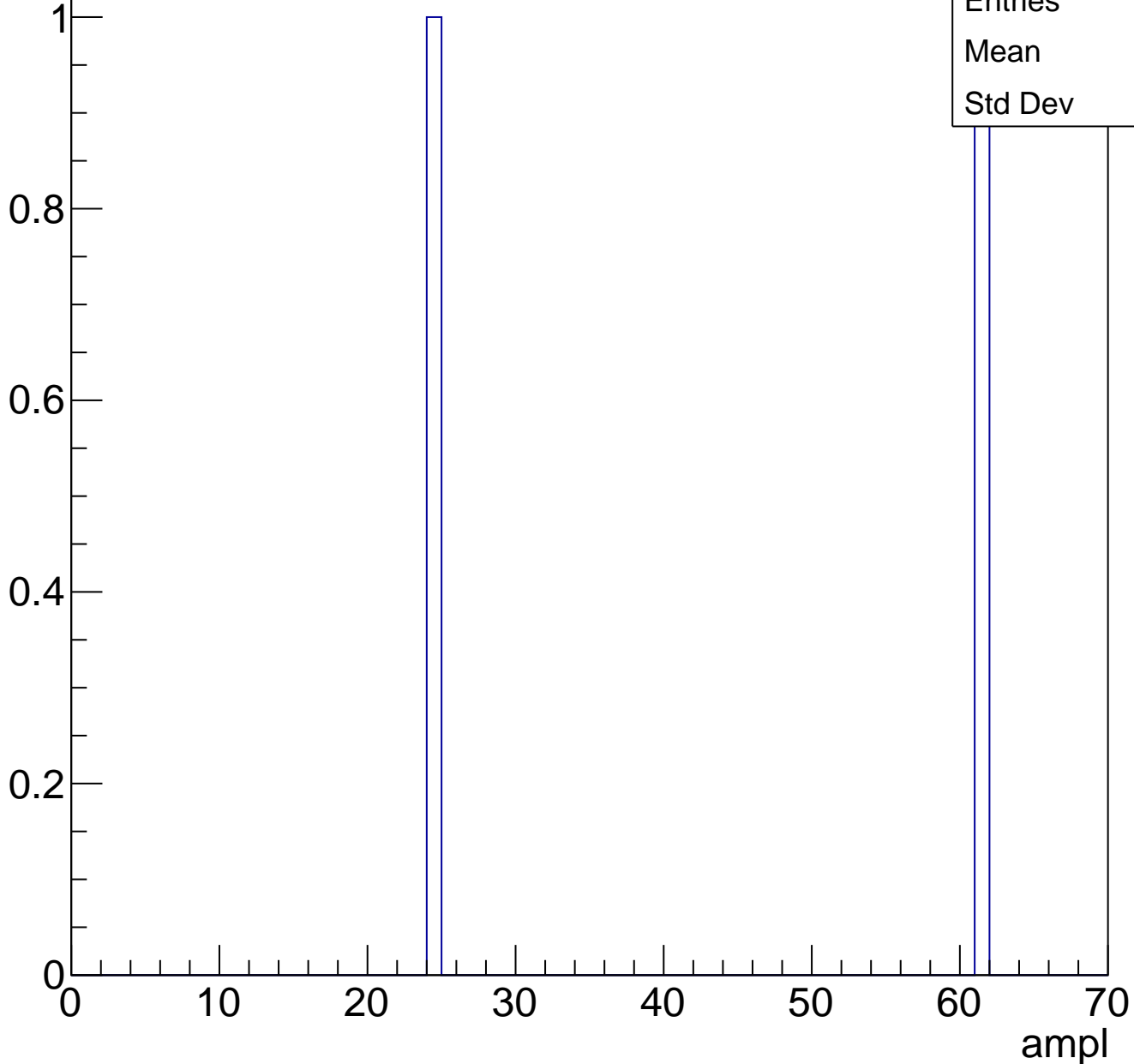
Entry



B0L002S, U26-ch116, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch117, adc0

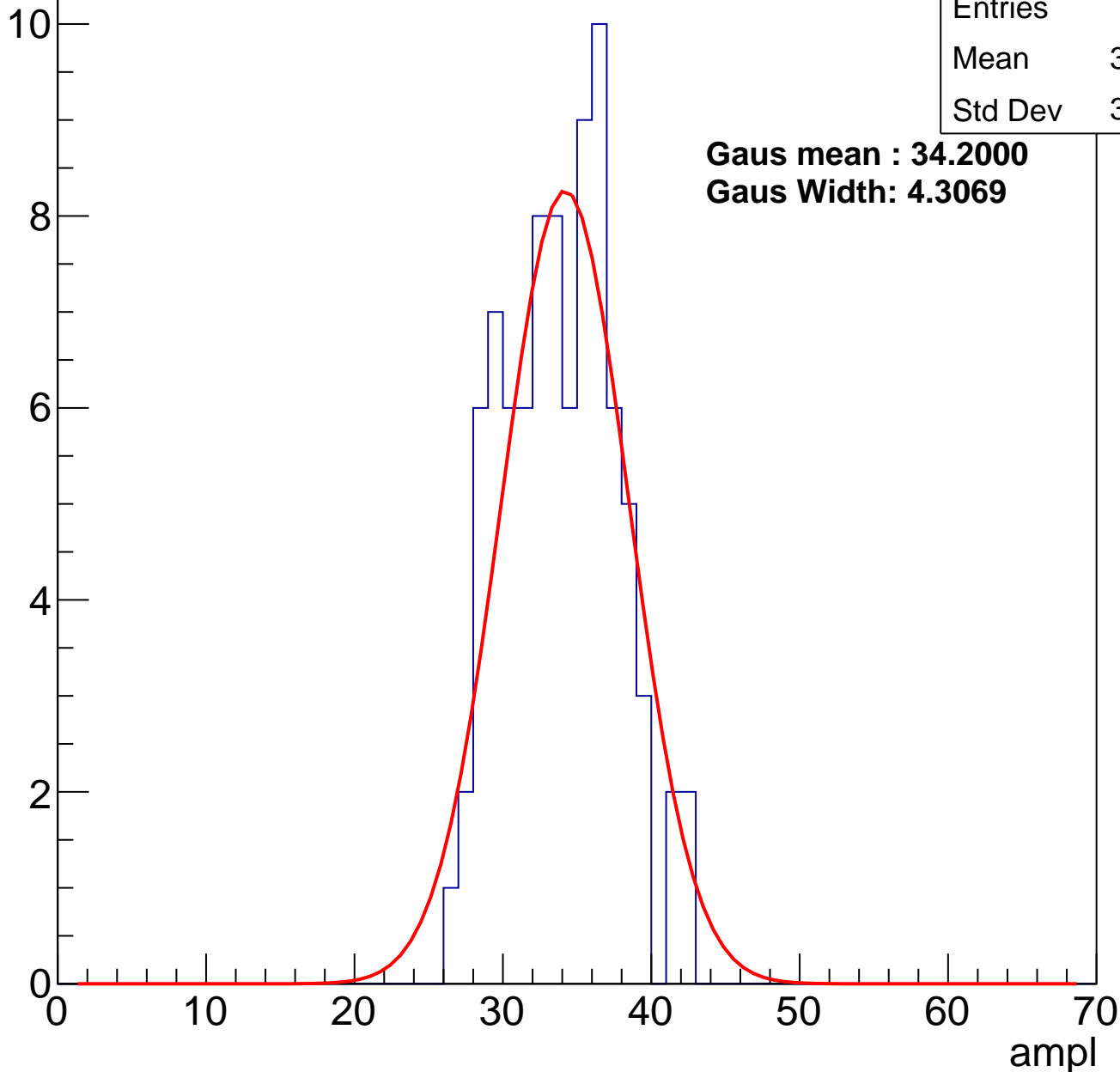
calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	33.46
Std Dev	3.716

Gaus mean : 34.2000

Gaus Width: 4.3069

Entry



B0L002S, U26-ch117, adc1

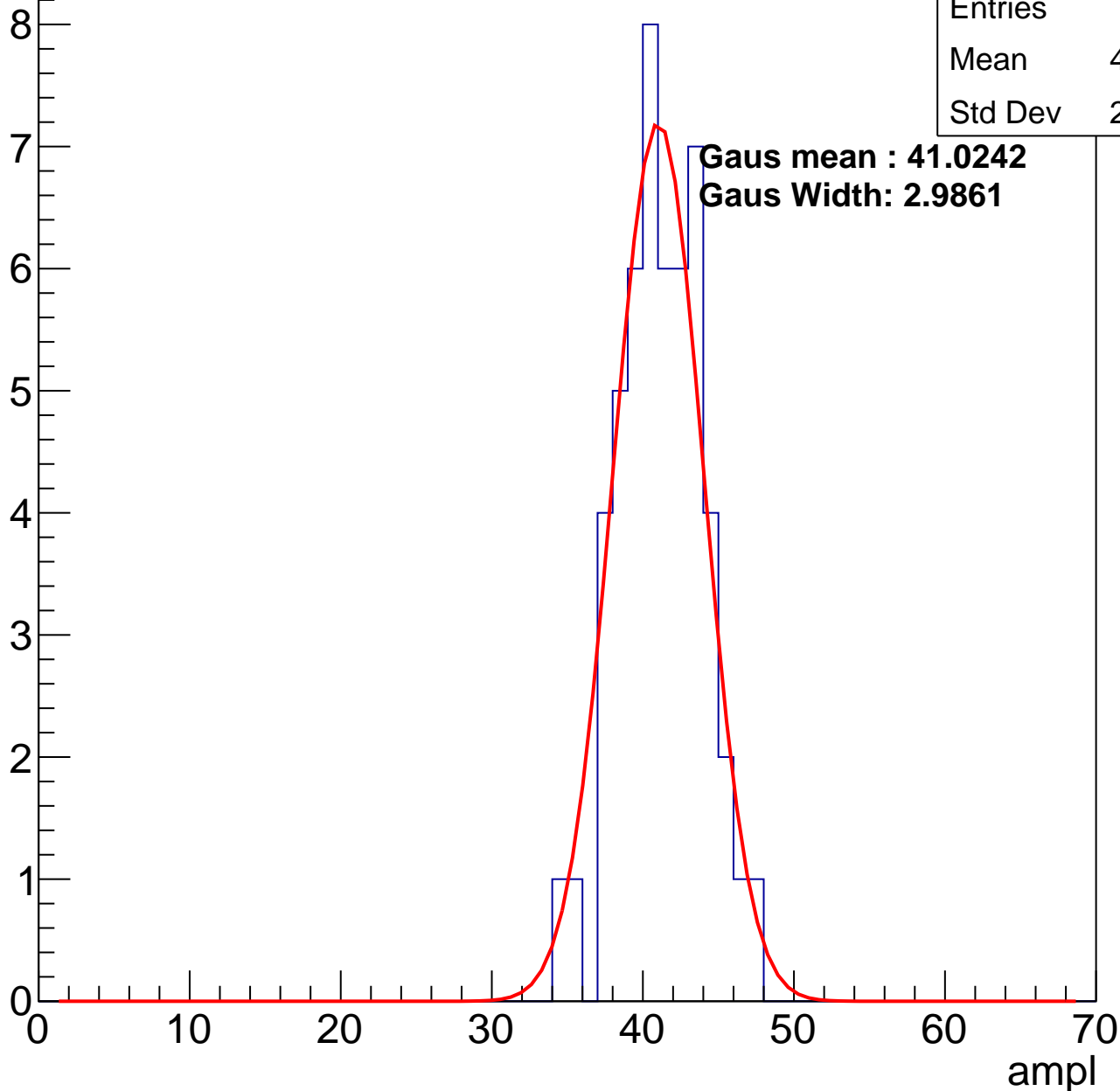
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	40.75
Std Dev	2.724

Gaus mean : 41.0242

Gaus Width: 2.9861



B0L002S, U26-ch117, adc2

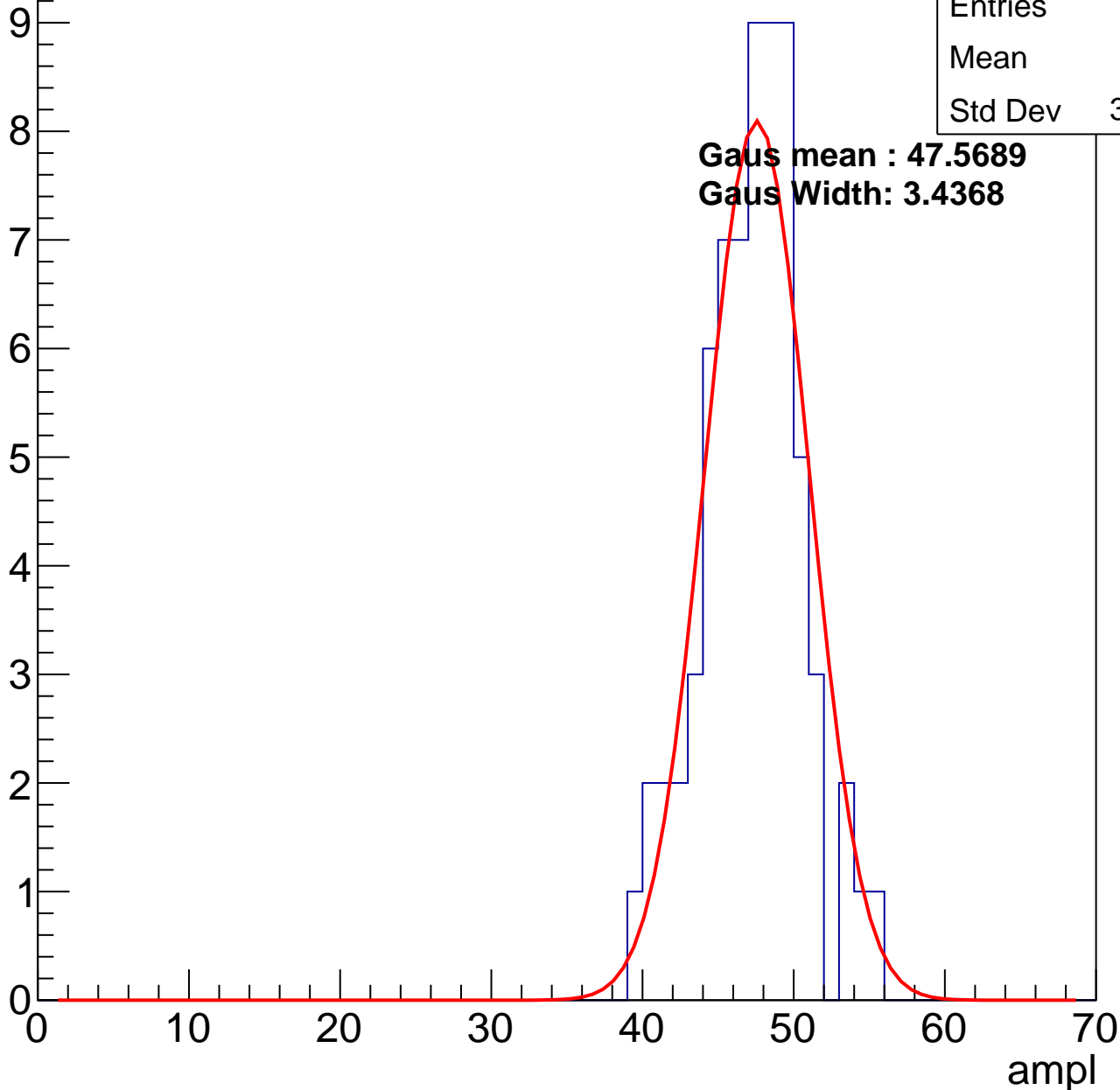
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	46.8
Std Dev	3.277

Gaus mean : 47.5689

Gaus Width: 3.4368



B0L002S, U26-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

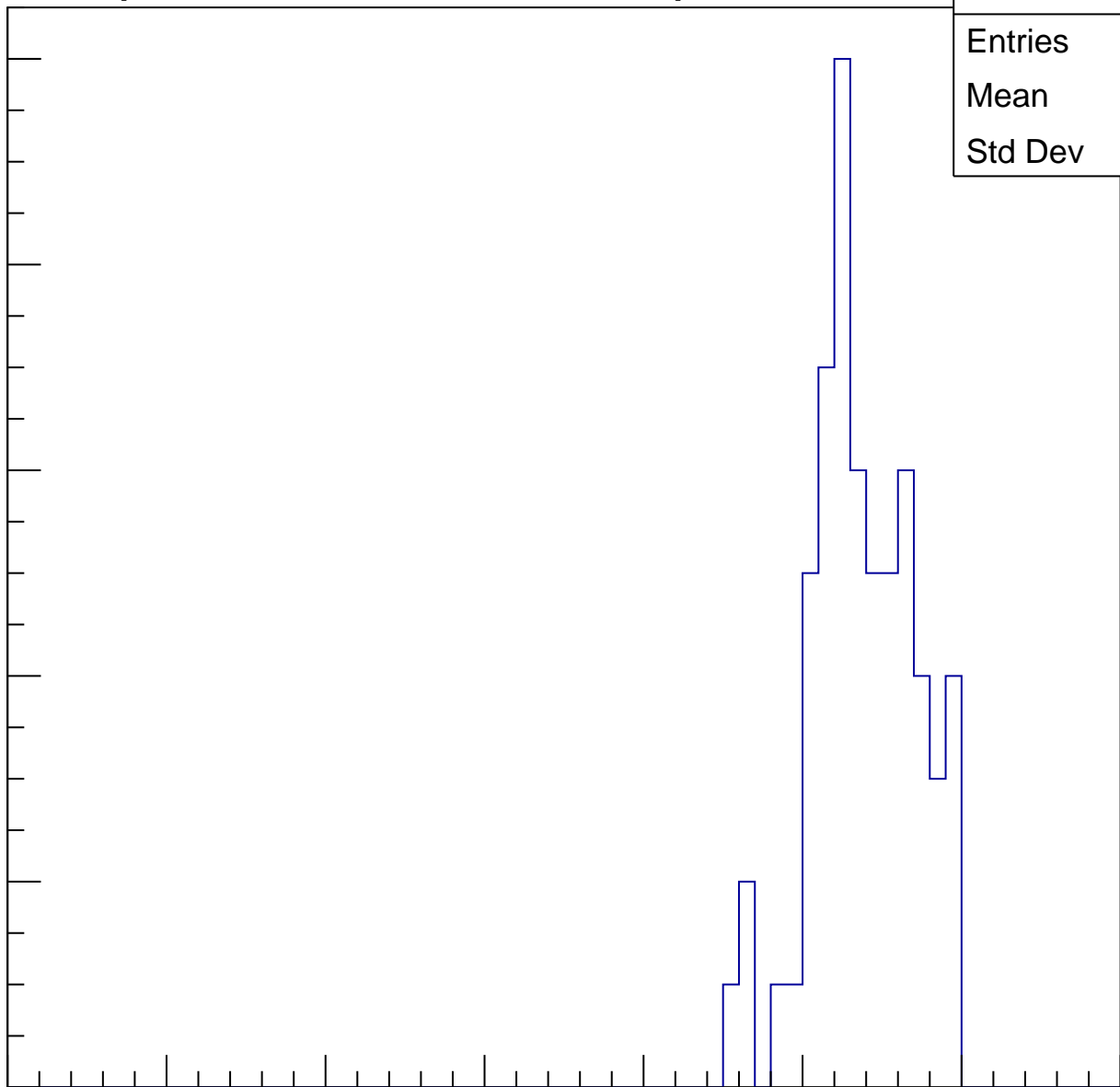
Entries	60
Mean	53.3
Std Dev	3.273

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

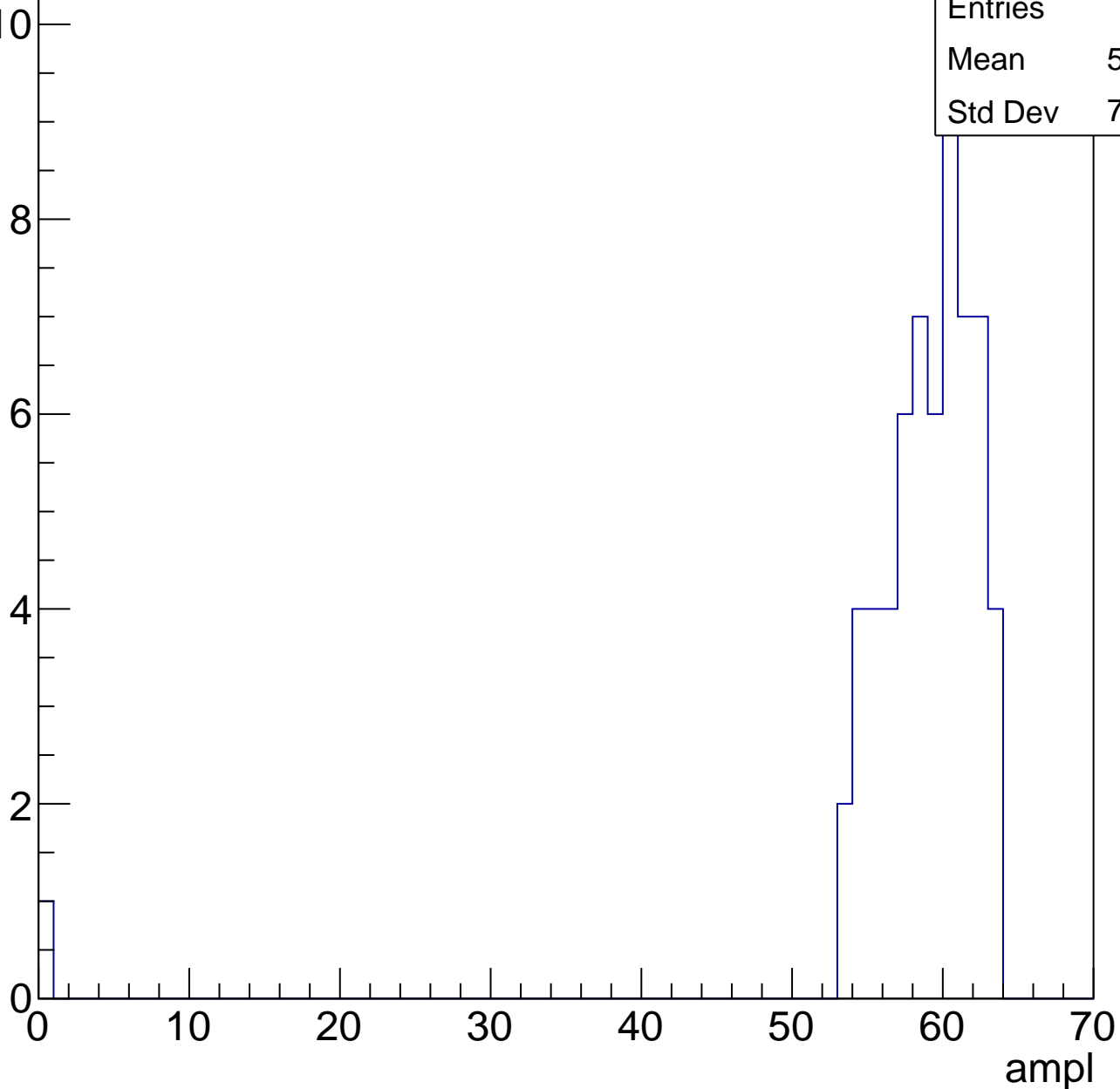


B0L002S, U26-ch117, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	57.76
Std Dev	7.884

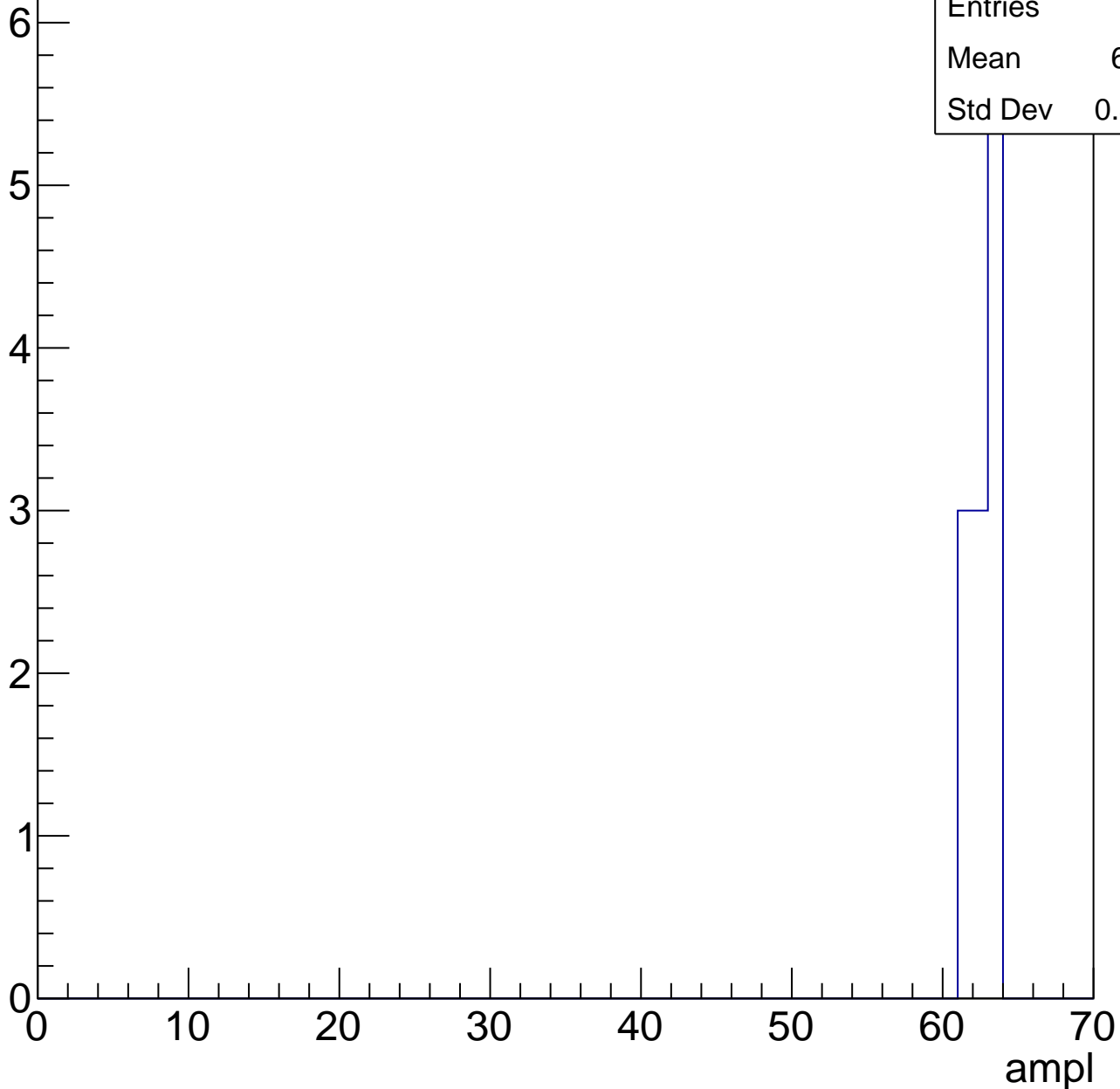


B0L002S, U26-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	62.25
Std Dev	0.8292



B0L002S, U26-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch118, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	32.85
Std Dev	3.716

Gaus mean : 33.3808

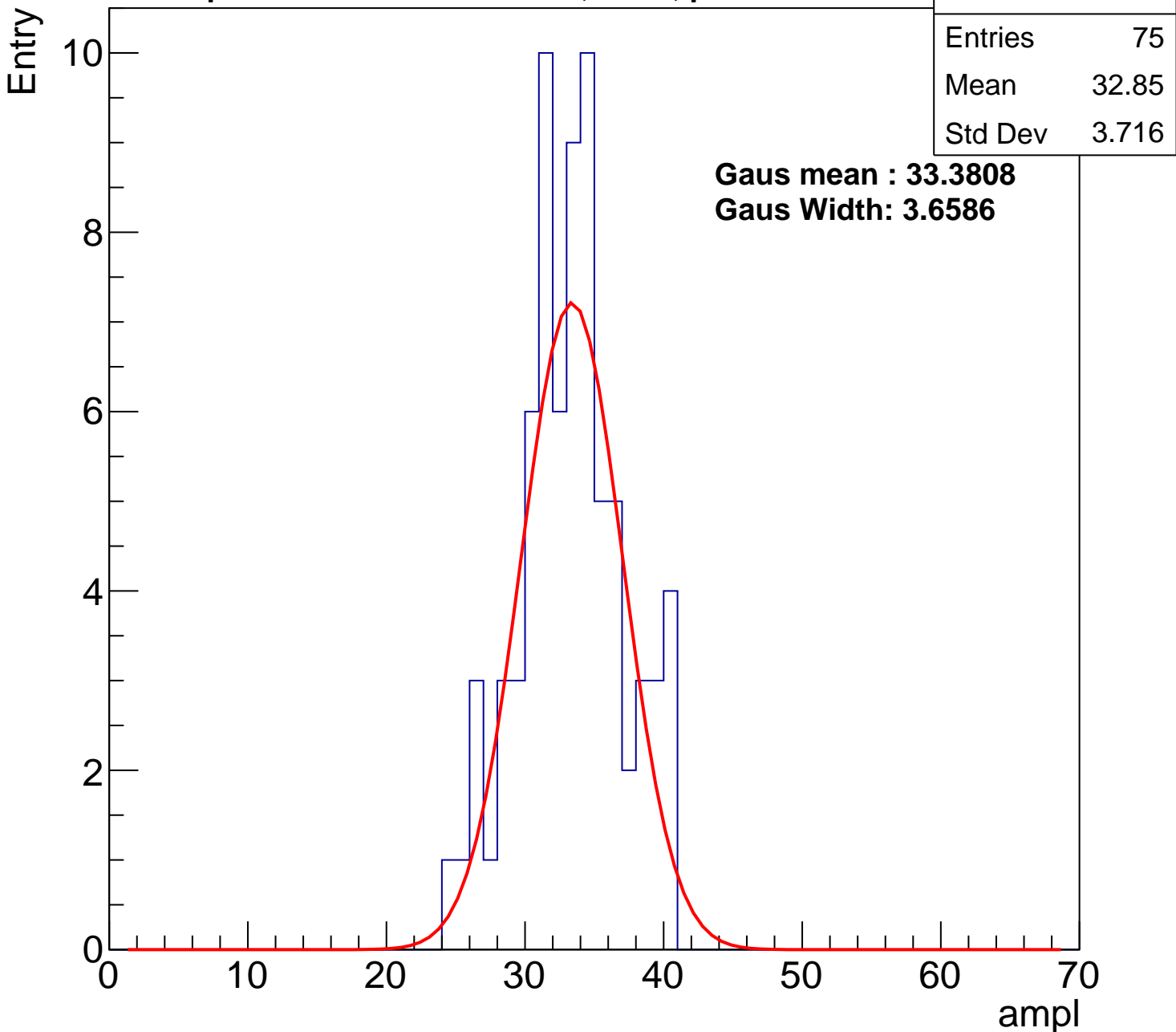
Gaus Width: 3.6586

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch118, adc1

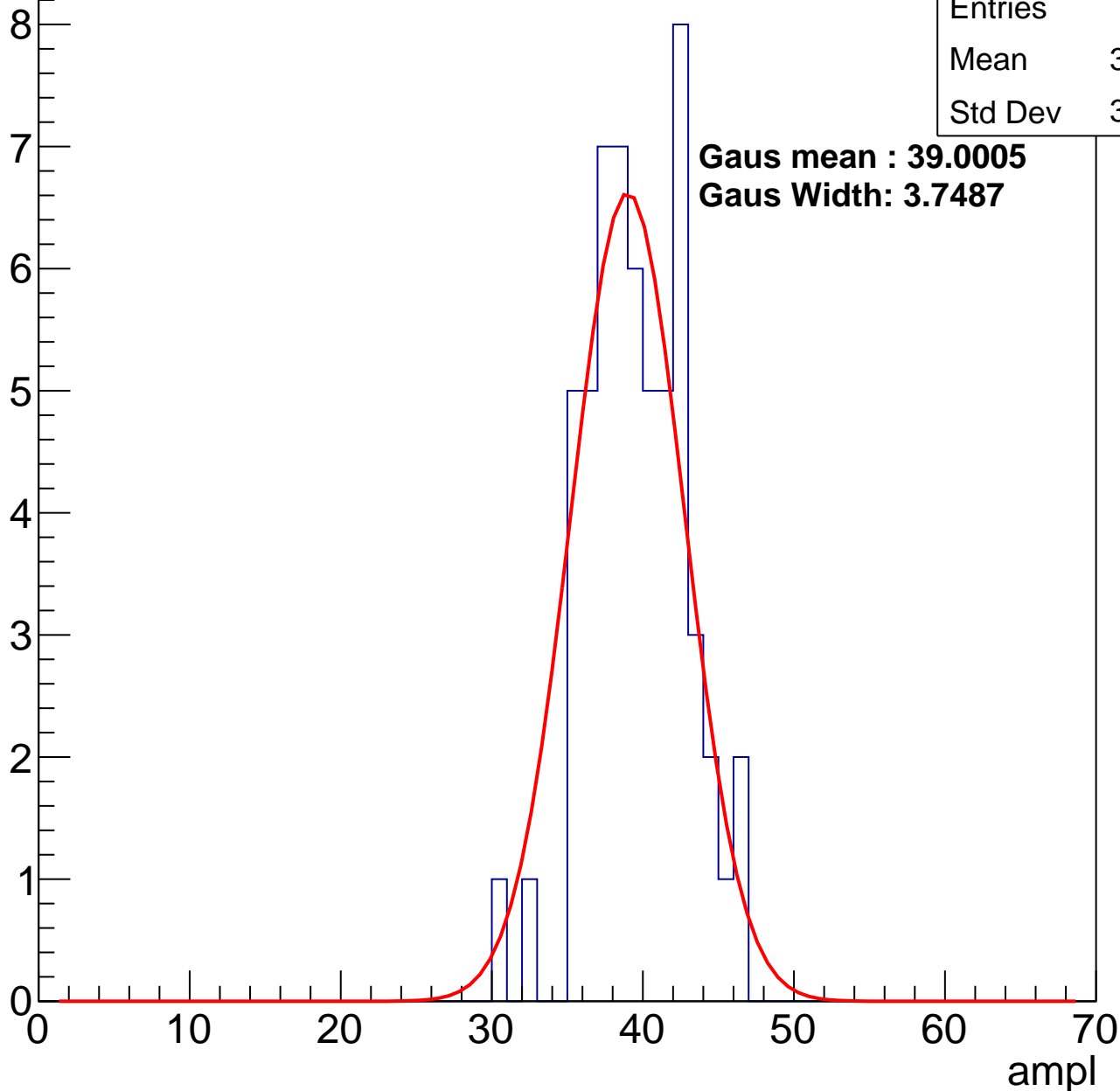
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	39.16
Std Dev	3.263

Gaus mean : 39.0005

Gaus Width: 3.7487



B0L002S, U26-ch118, adc2

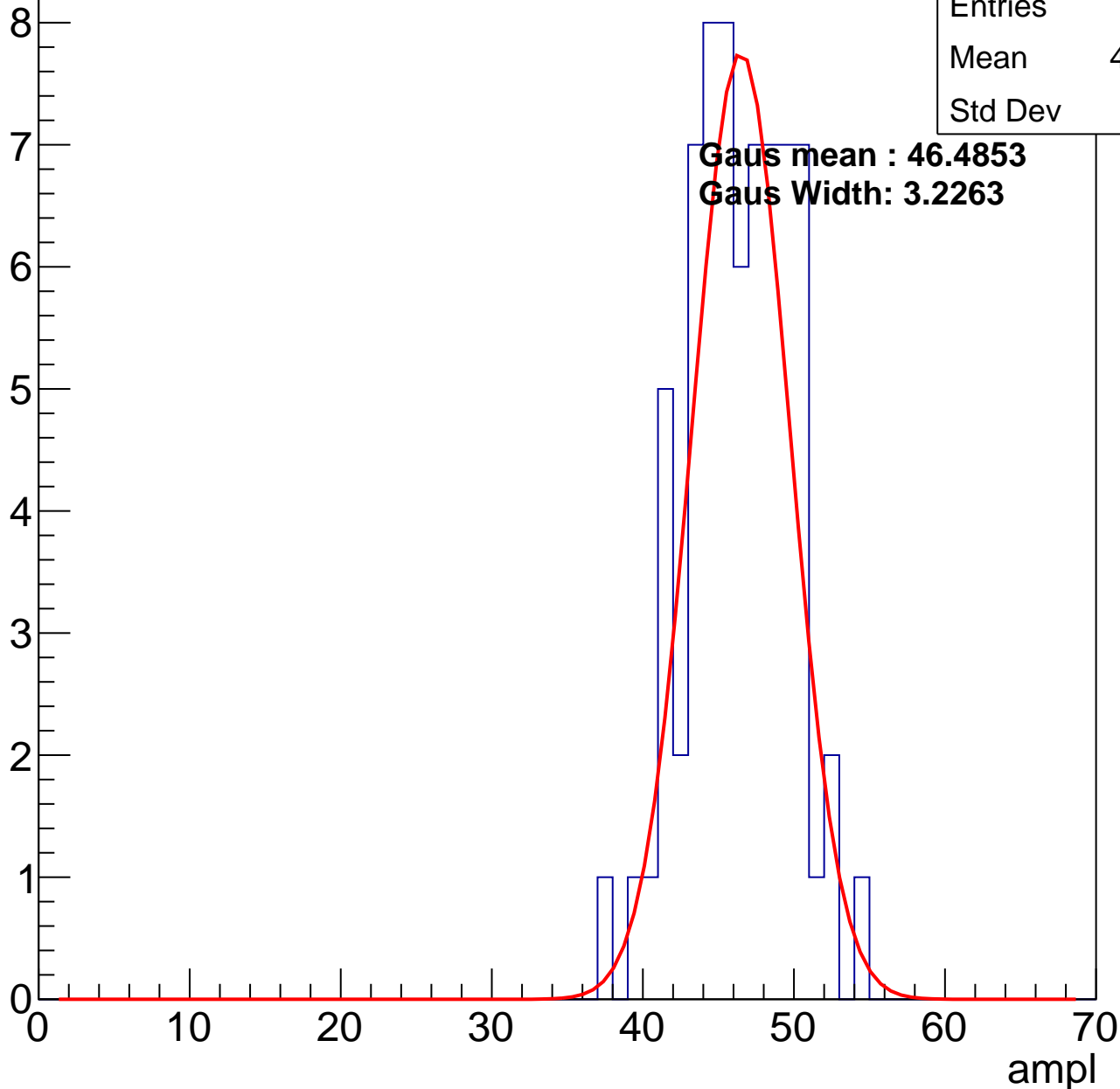
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	45.93
Std Dev	3.35

Gaus mean : 46.4853

Gaus Width: 3.2263

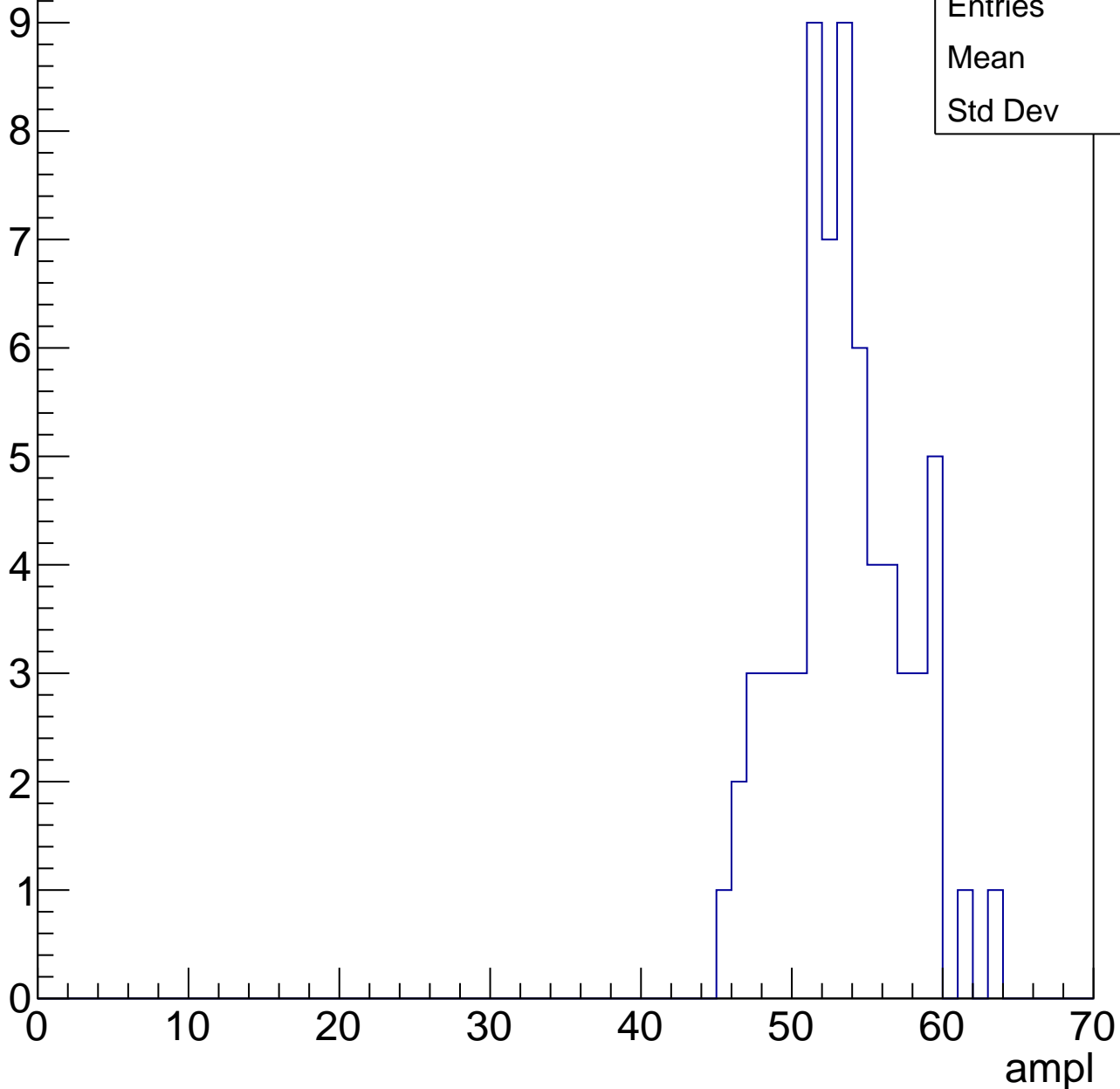


B0L002S, U26-ch118, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

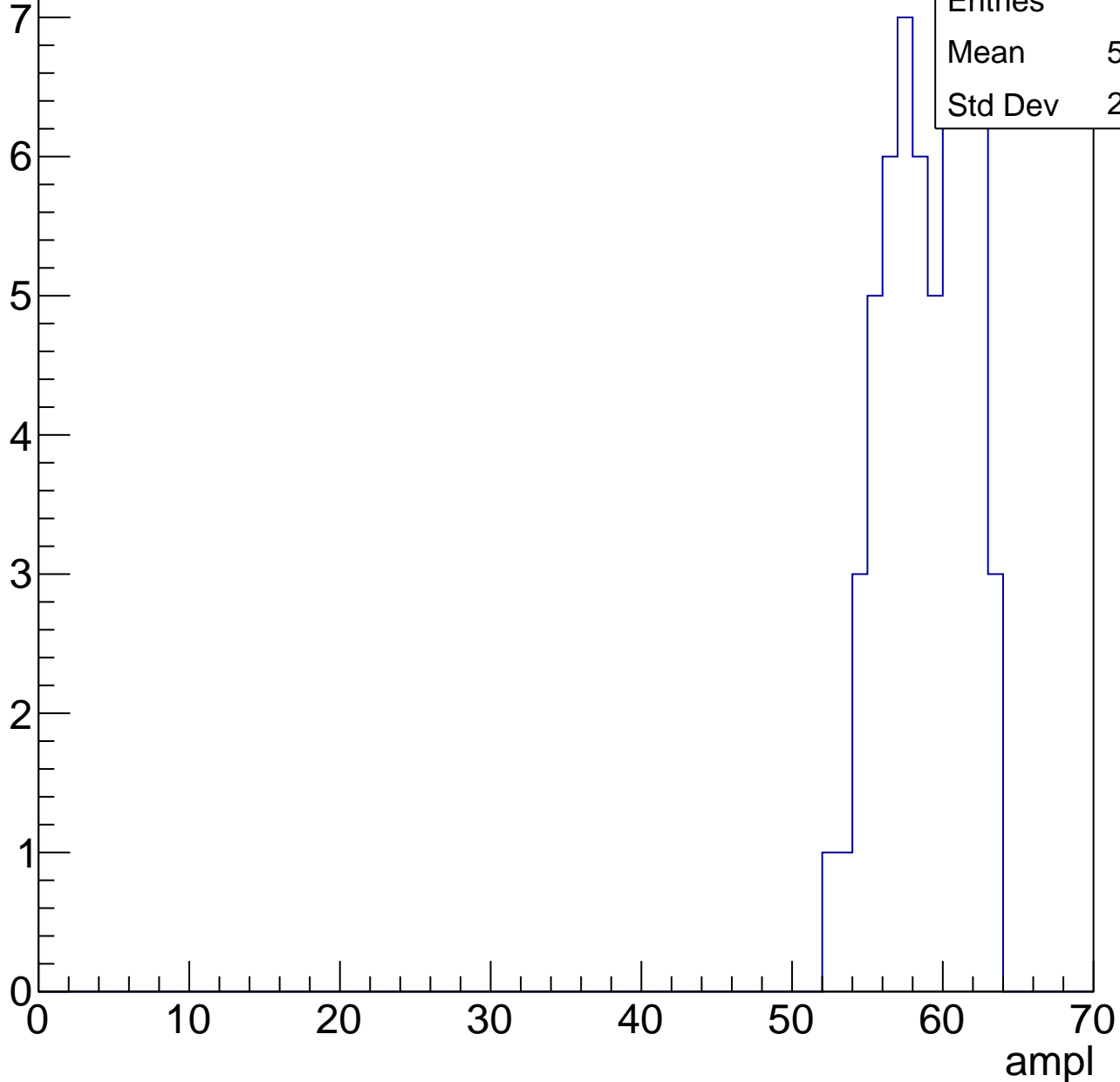
Entries	67
Mean	53
Std Dev	3.84



B0L002S, U26-ch118, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



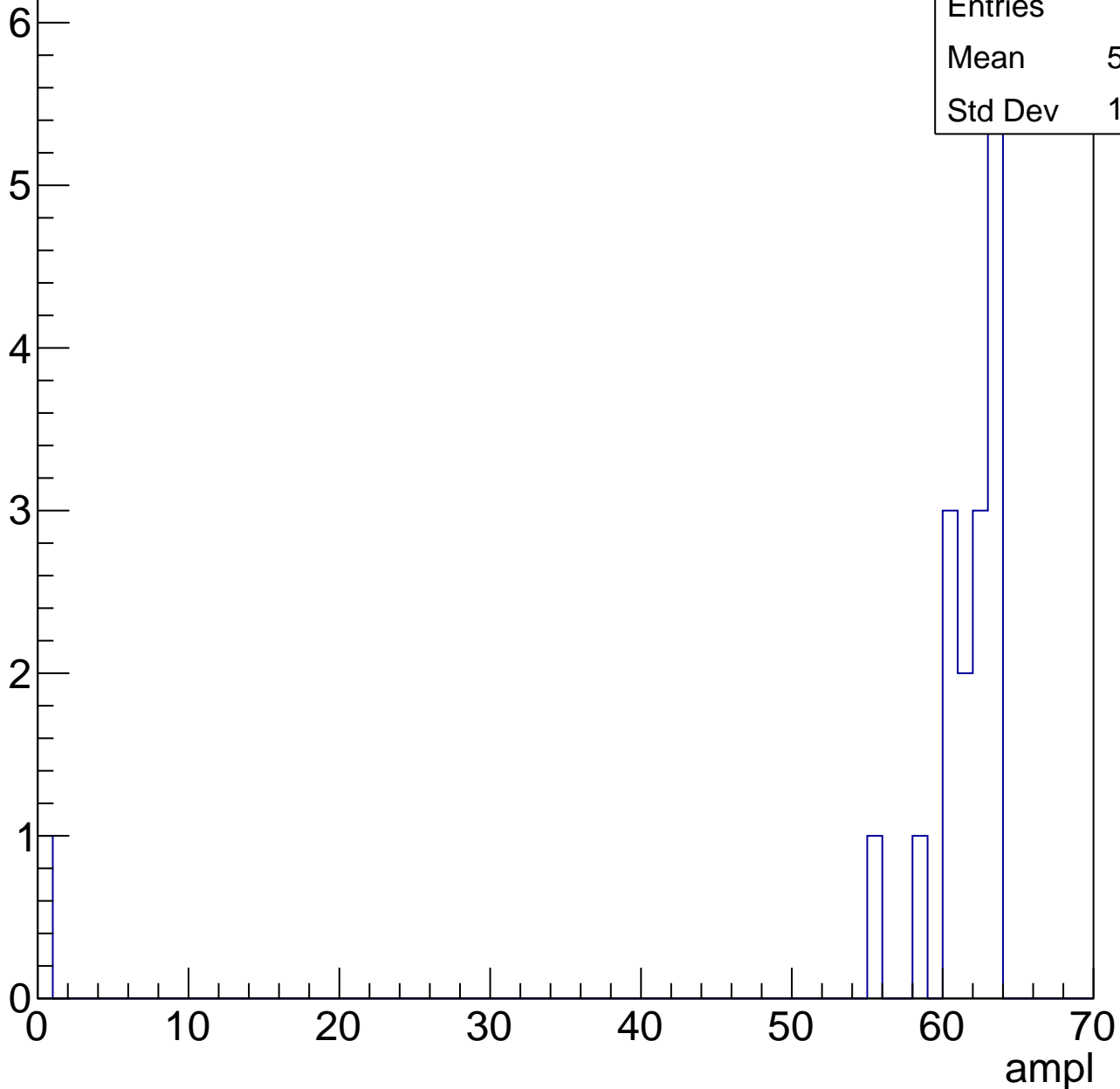
Entries	58
Mean	58.45
Std Dev	2.805

B0L002S, U26-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	57.59
Std Dev	14.55



B0L002S, U26-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch119, adc0

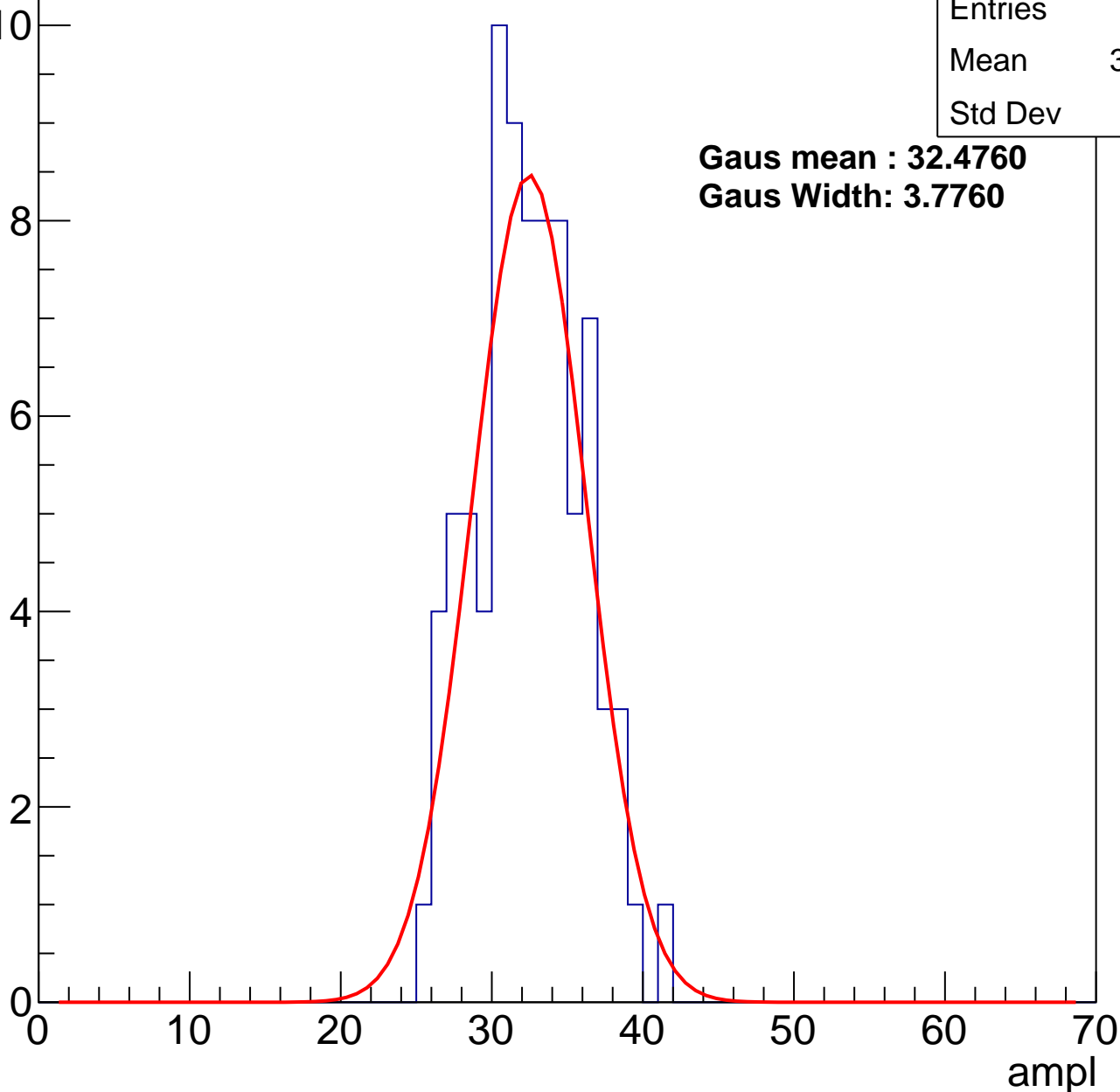
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	31.99
Std Dev	3.48

Gaus mean : 32.4760

Gaus Width: 3.7760



B0L002S, U26-ch119, adc1

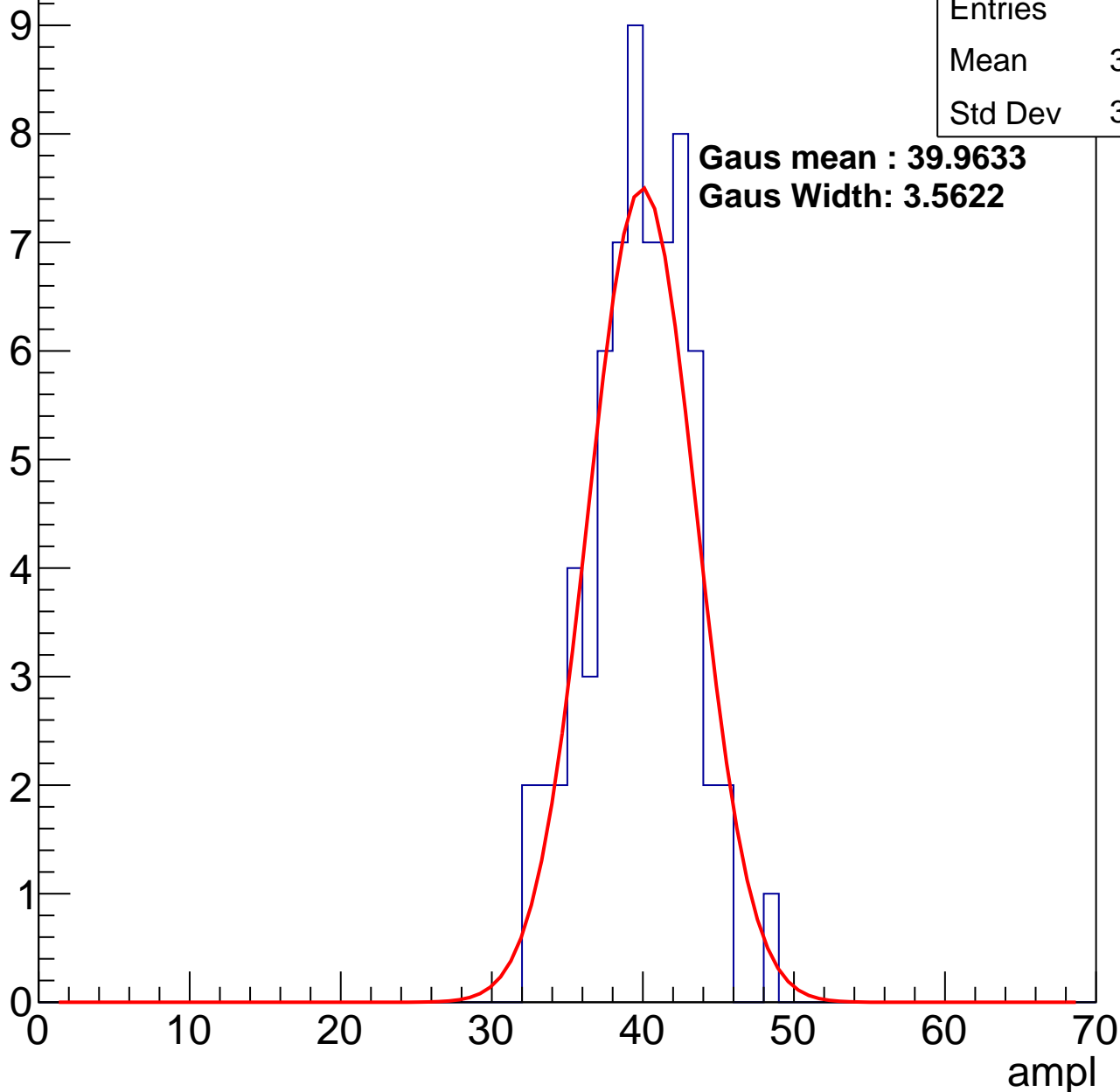
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	39.29
Std Dev	3.304

Gaus mean : 39.9633

Gaus Width: 3.5622



B0L002S, U26-ch119, adc2

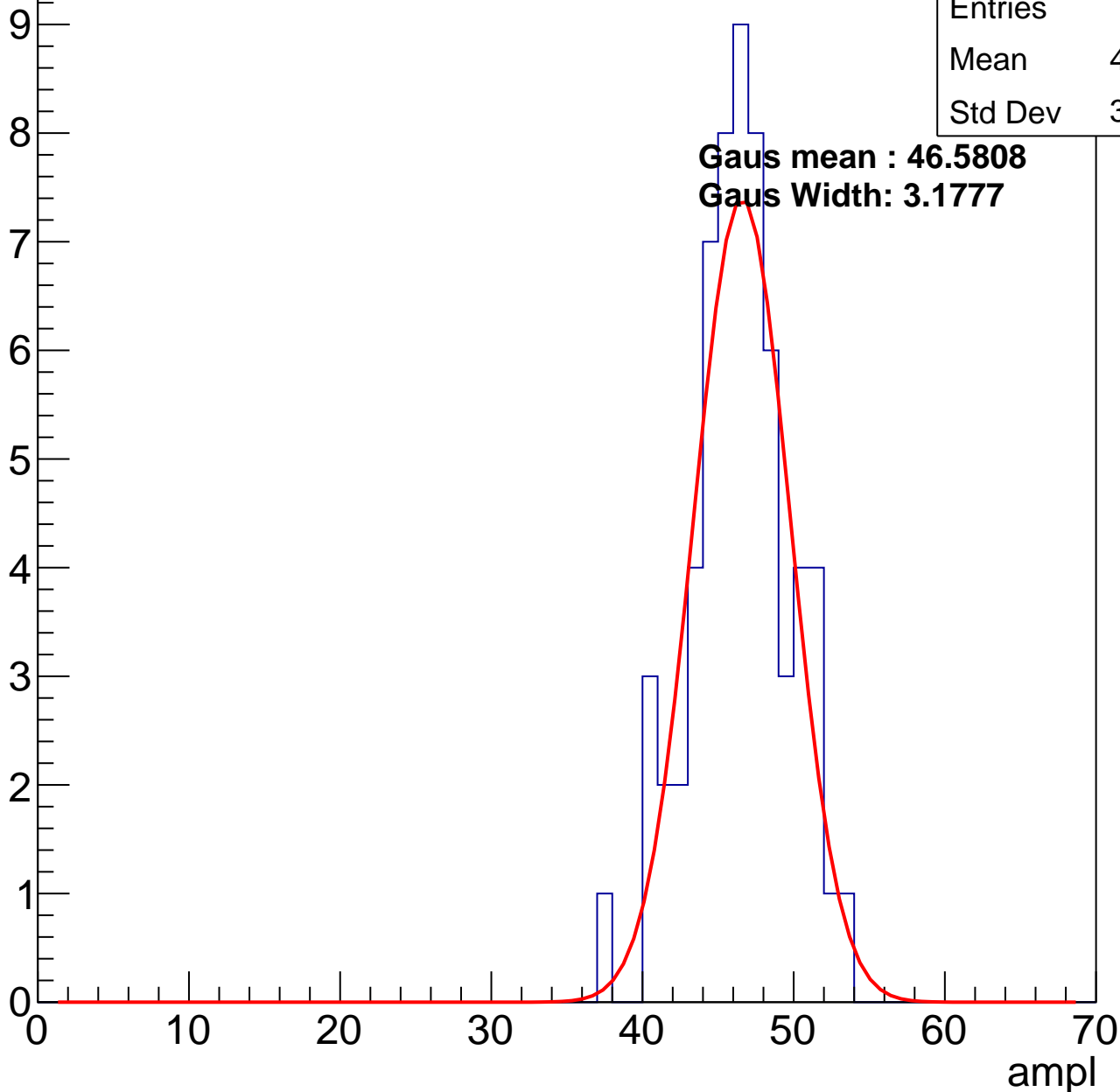
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	45.98
Std Dev	3.215

Gaus mean : 46.5808

Gaus Width: 3.1777

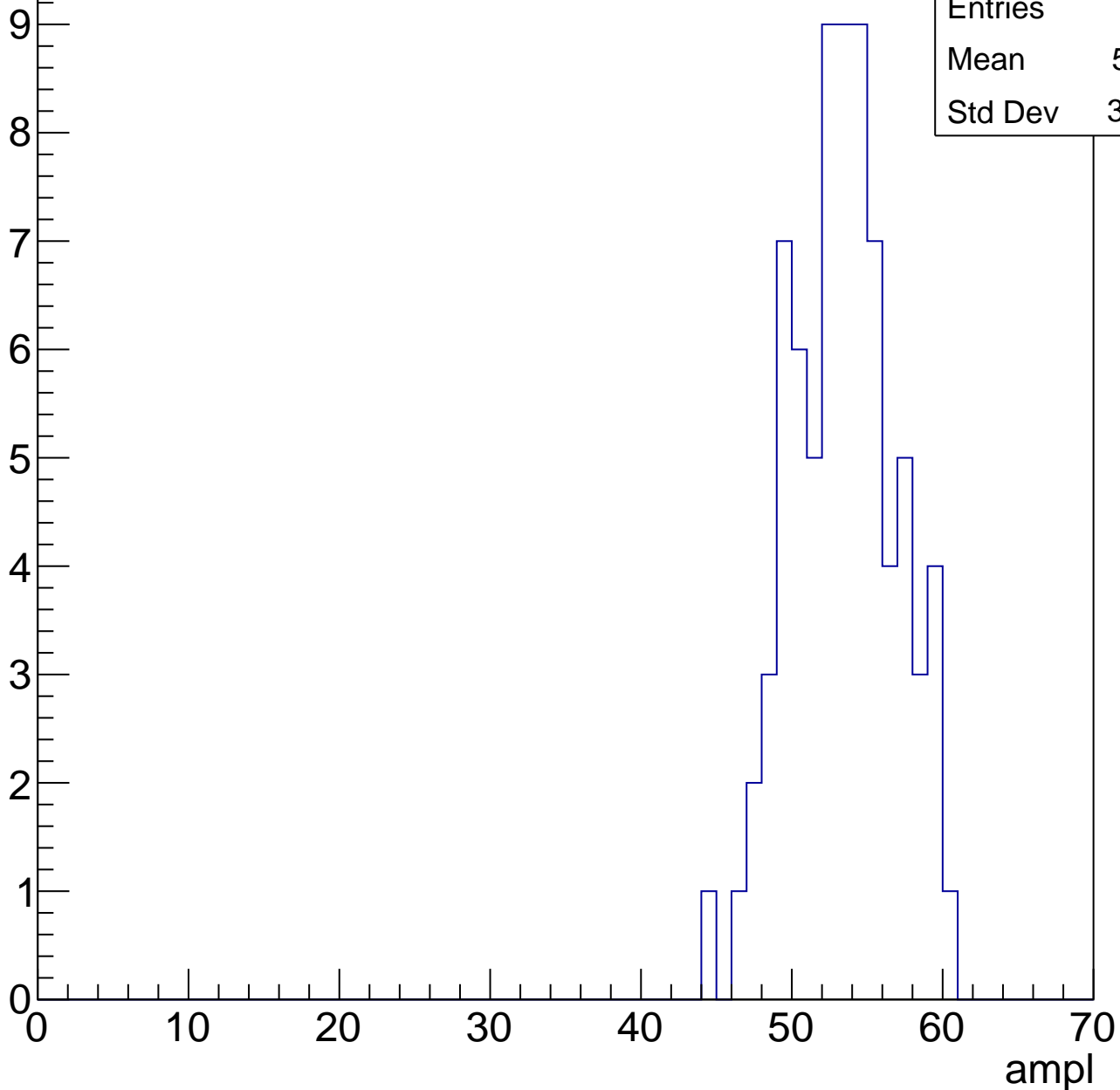


B0L002S, U26-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

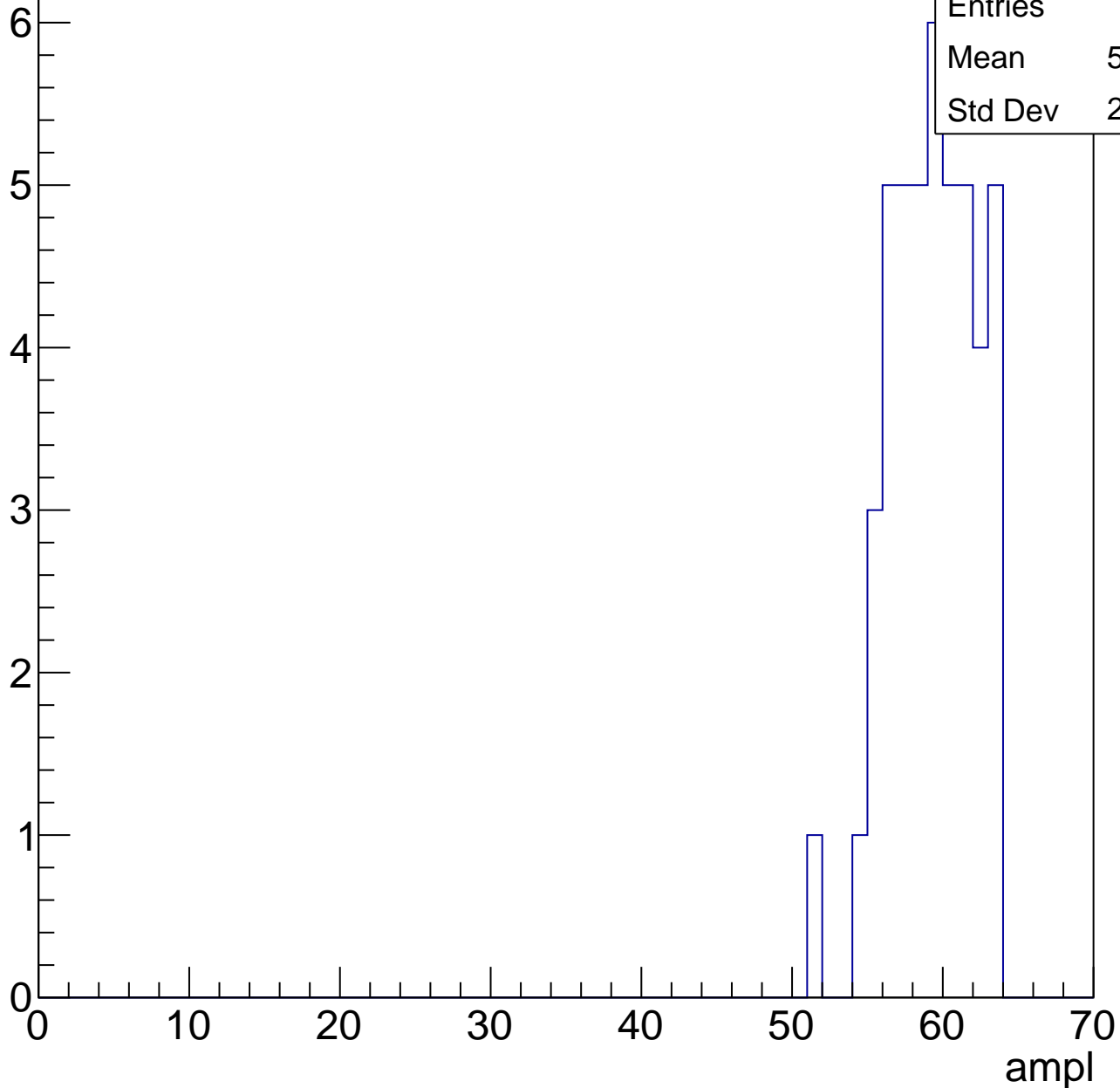
Entries	76
Mean	52.91
Std Dev	3.427



B0L002S, U26-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

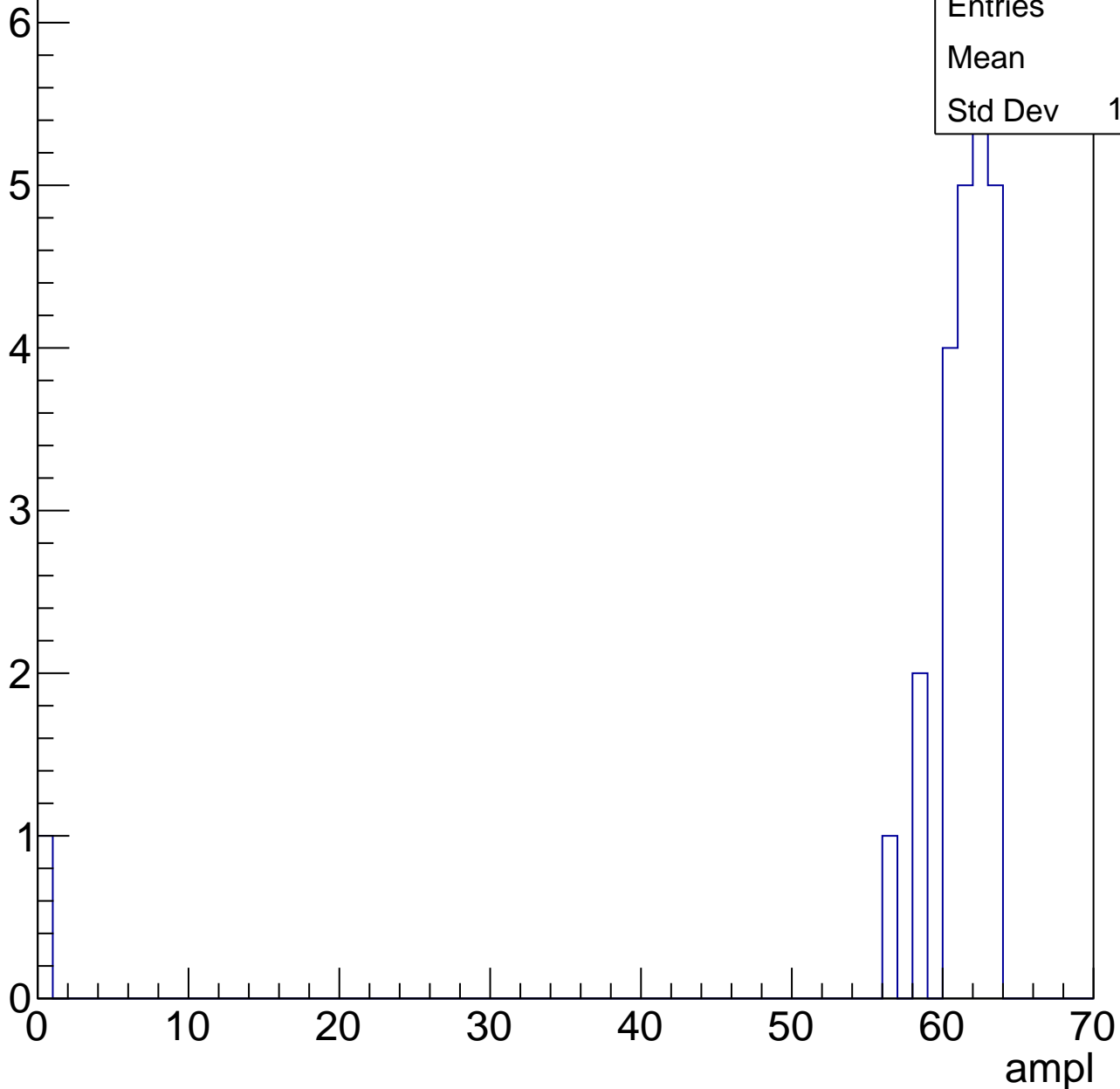


B0L002S, U26-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.5
Std Dev	12.32



B0L002S, U26-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U26-ch120, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	31.21
Std Dev	5.09

Gaus mean : 31.8941

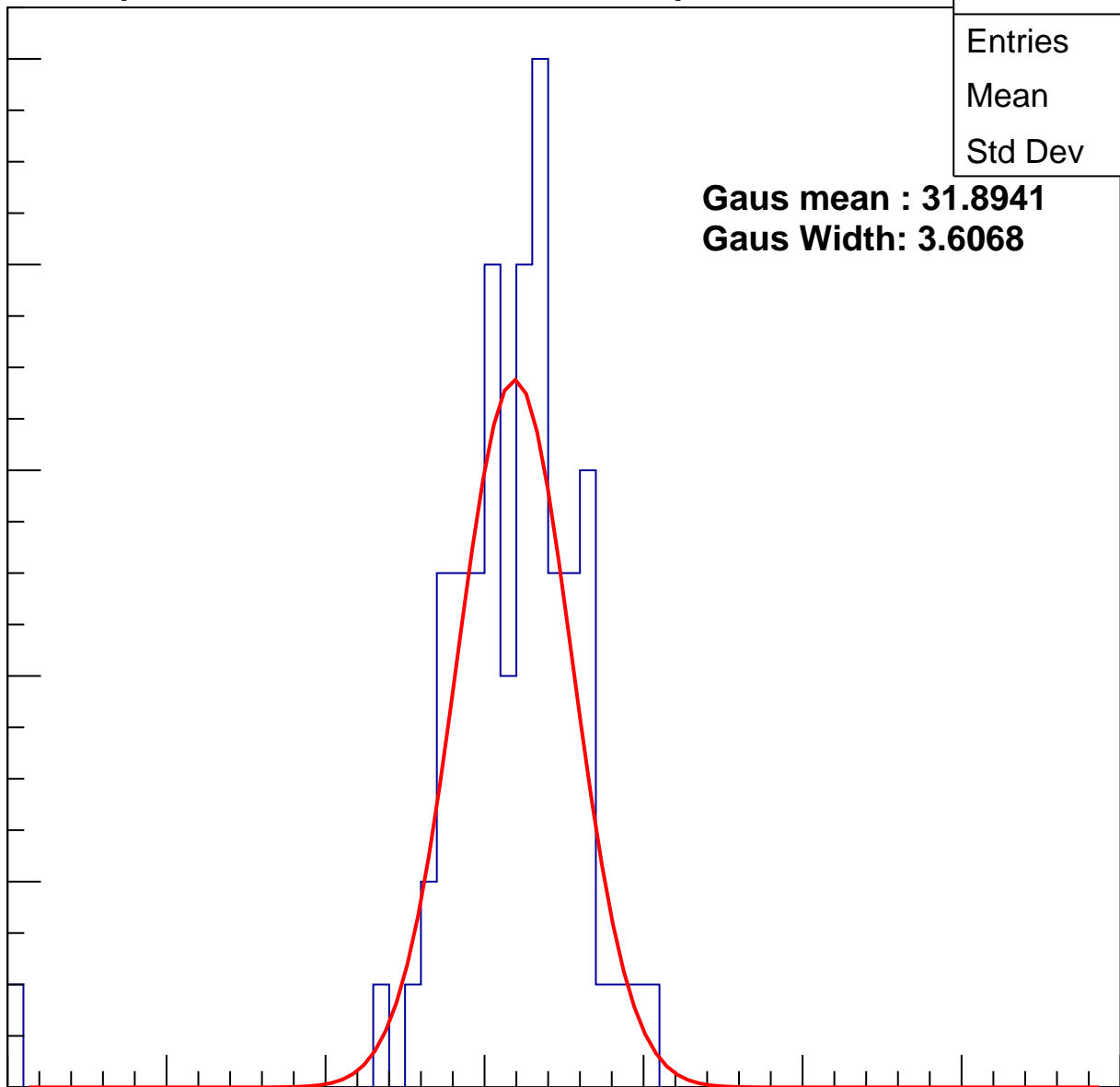
Gaus Width: 3.6068

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch120, adc1

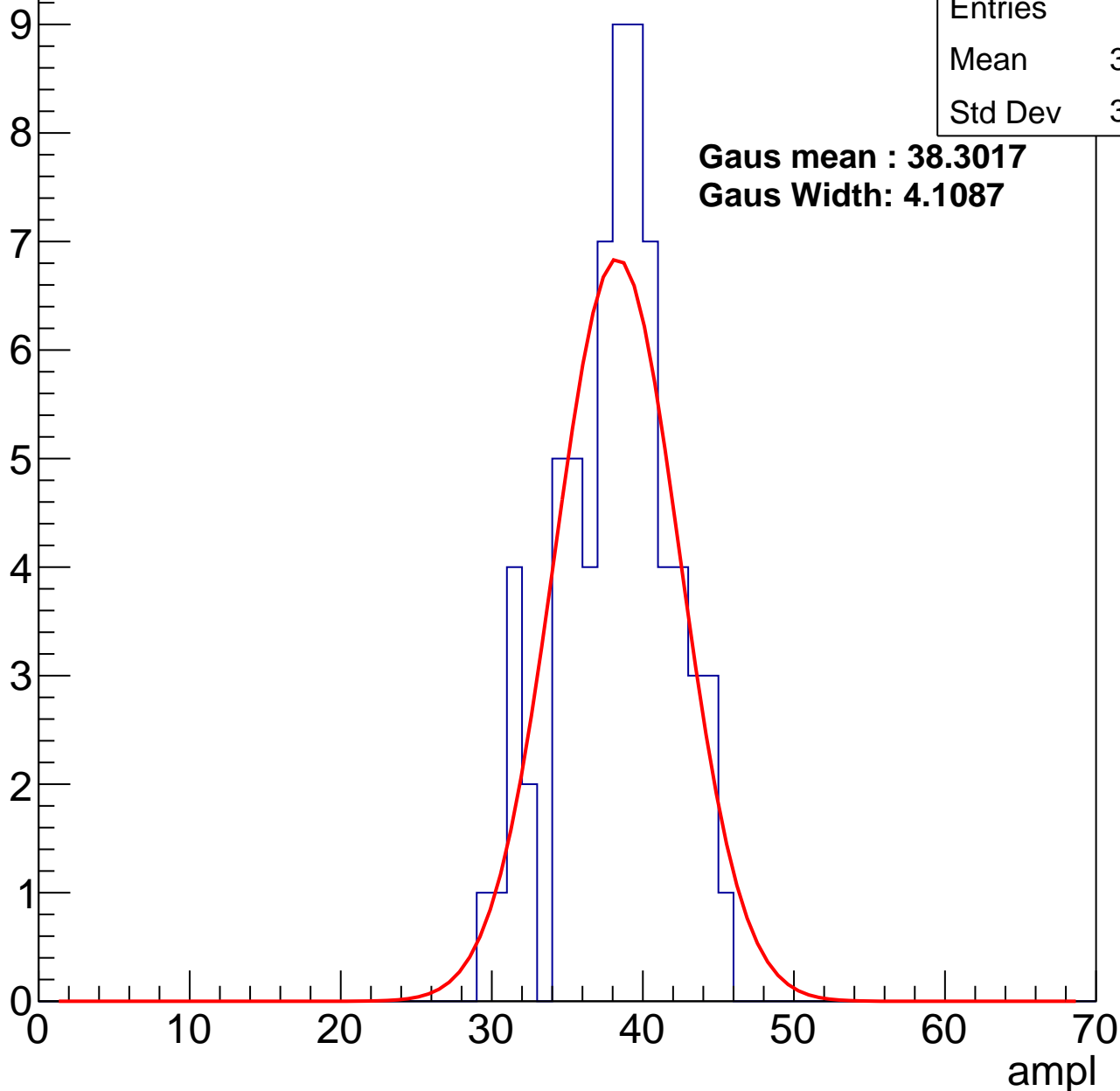
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.77
Std Dev	3.664

Gaus mean : 38.3017

Gaus Width: 4.1087



B0L002S, U26-ch120, adc2

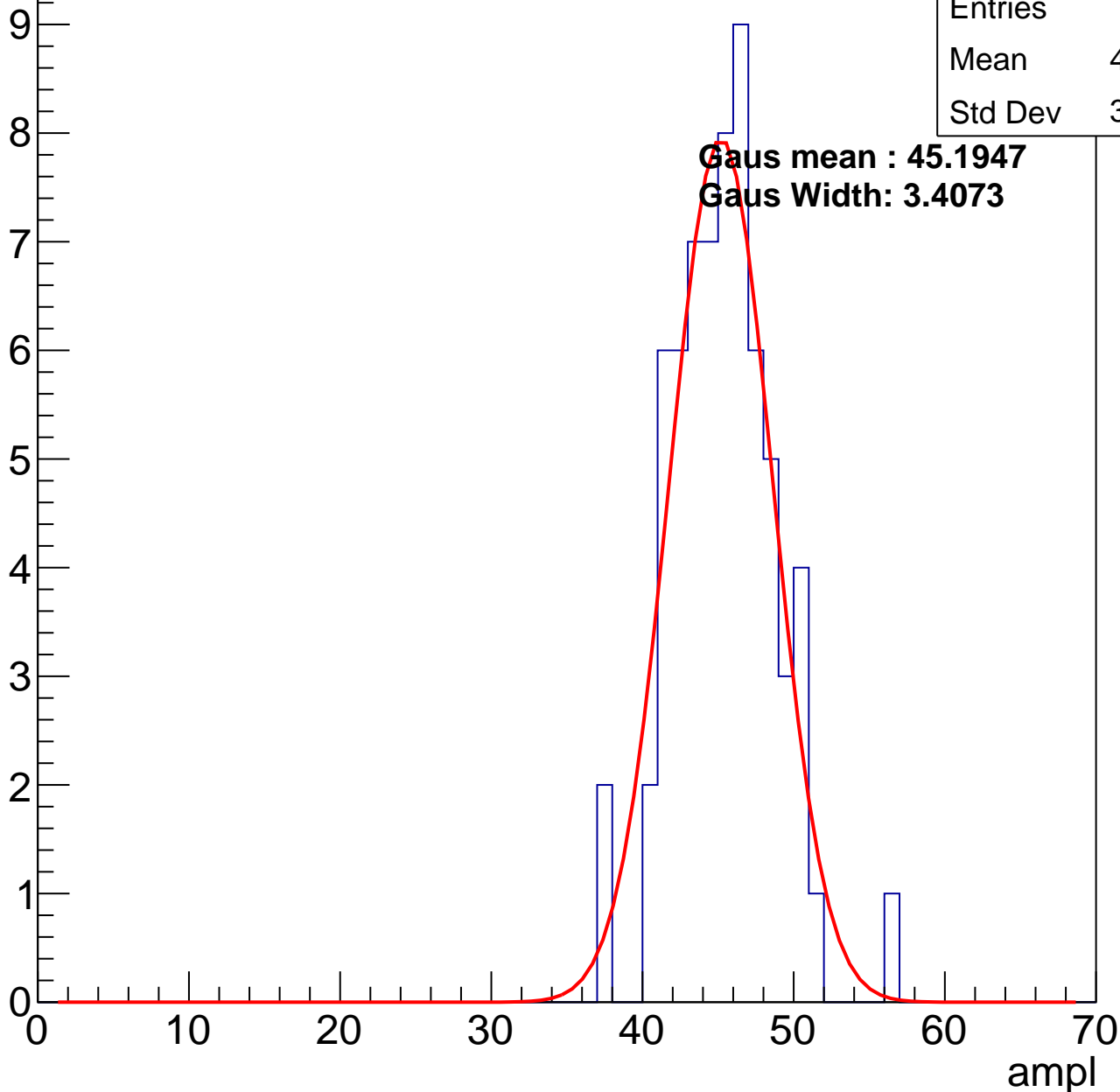
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.94
Std Dev	3.336

Gaus mean : 45.1947

Gaus Width: 3.4073

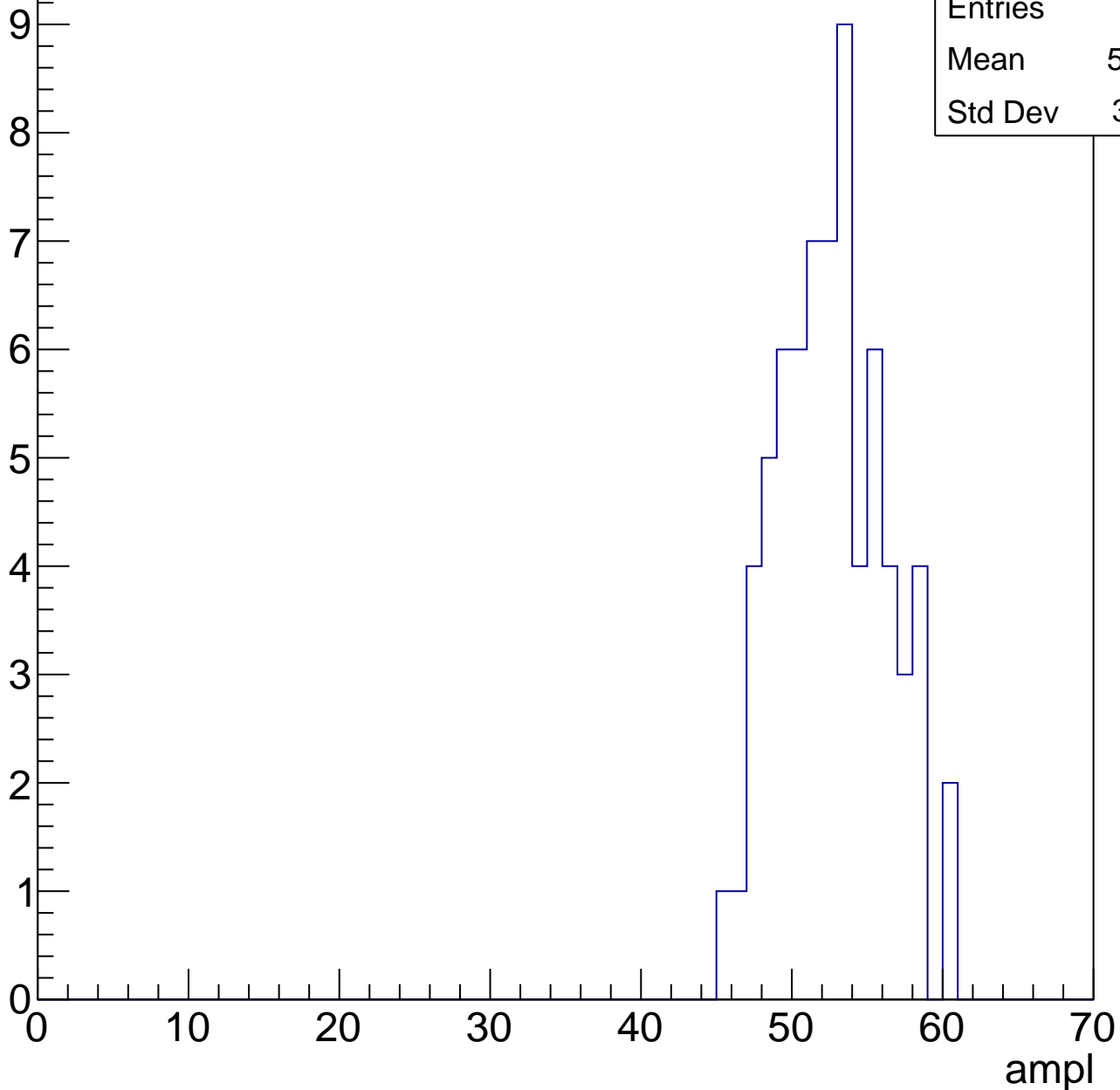


B0L002S, U26-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	52.23
Std Dev	3.481

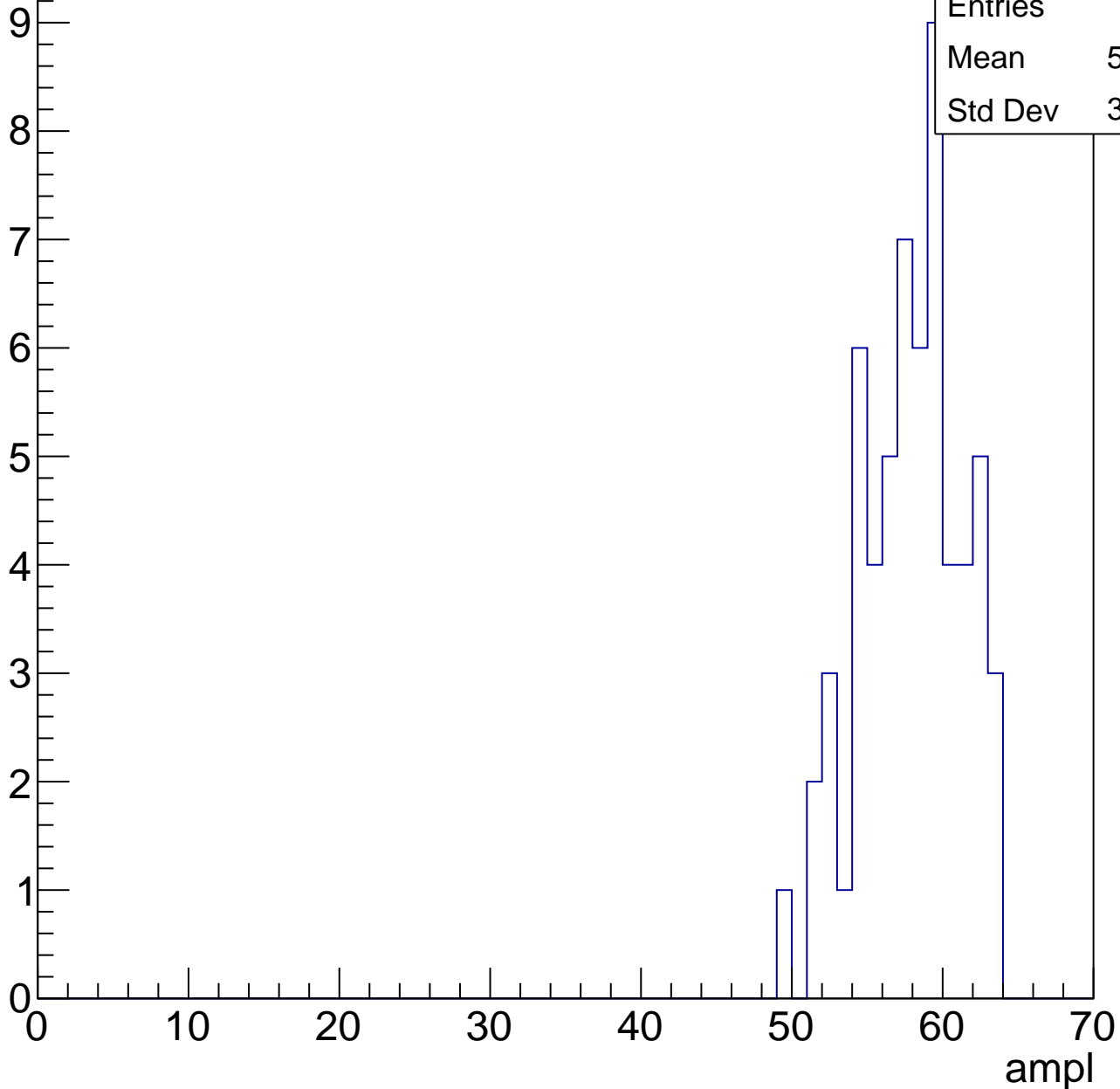


B0L002S, U26-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	57.42
Std Dev	3.333

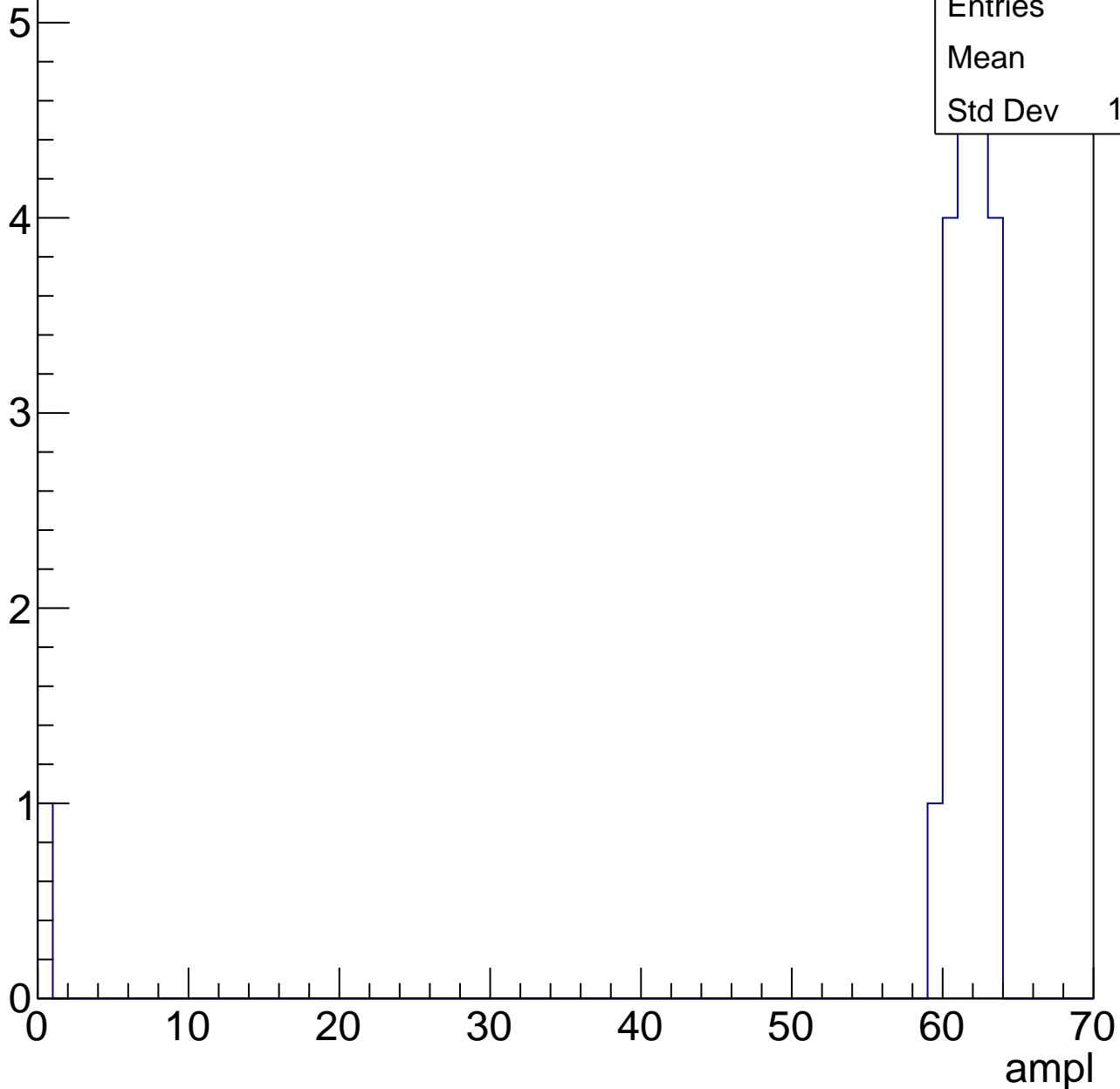


B0L002S, U26-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58.3
Std Dev	13.42



B0L002S, U26-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch121, adc0

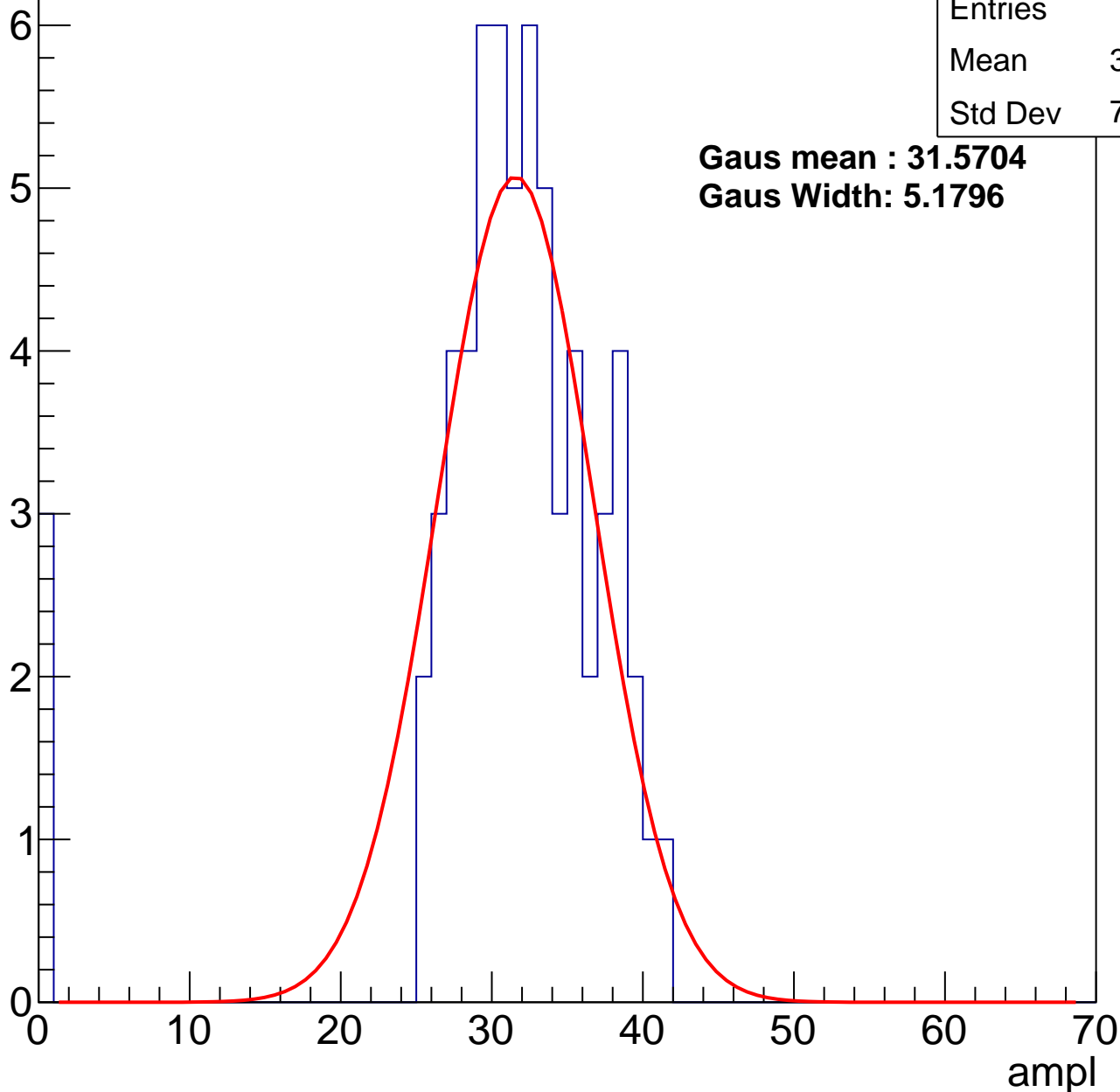
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.47
Std Dev	7.828

Gaus mean : 31.5704

Gaus Width: 5.1796



B0L002S, U26-ch121, adc1

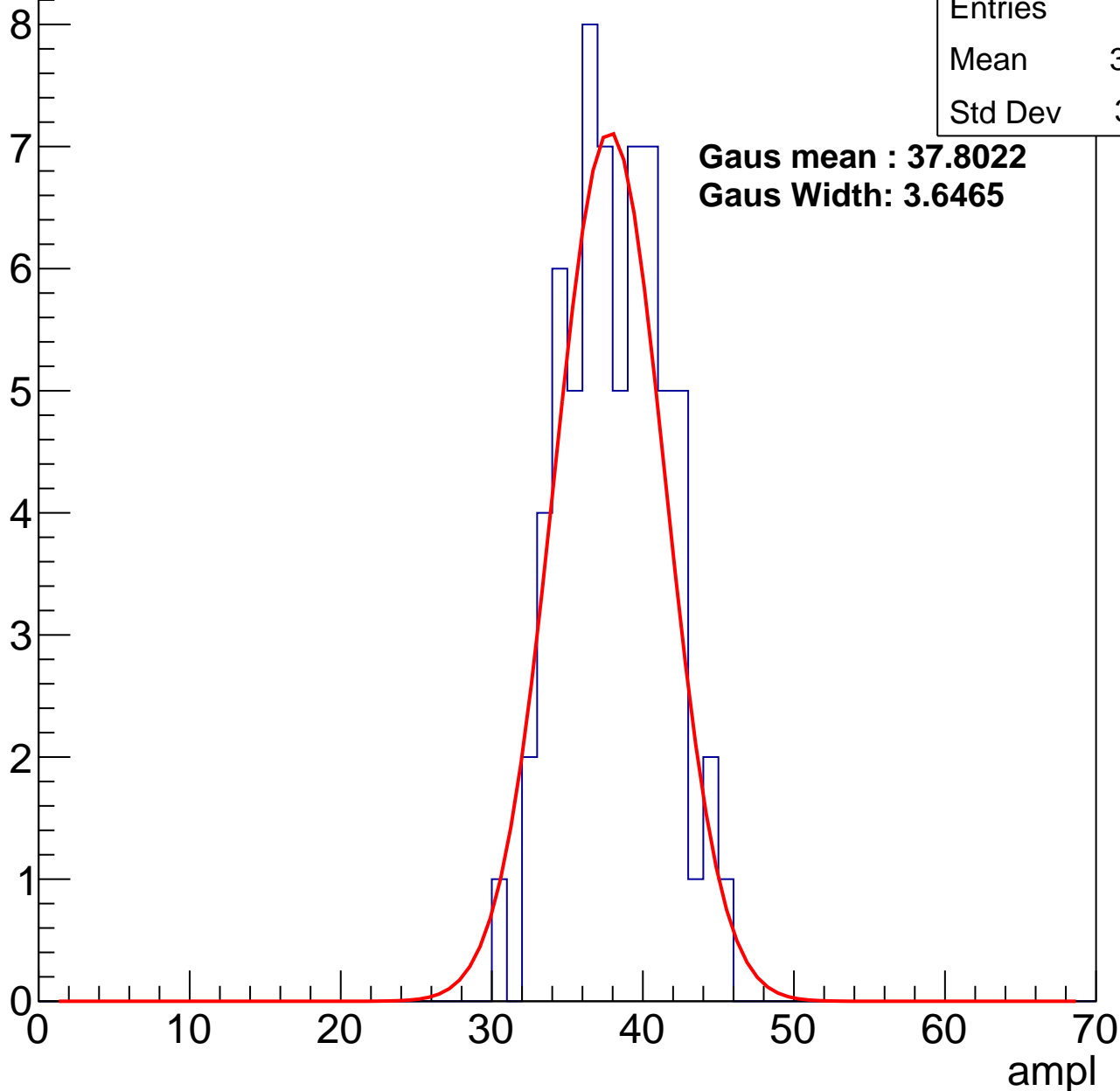
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.67
Std Dev	3.291

Gaus mean : 37.8022

Gaus Width: 3.6465



B0L002S, U26-ch121, adc2

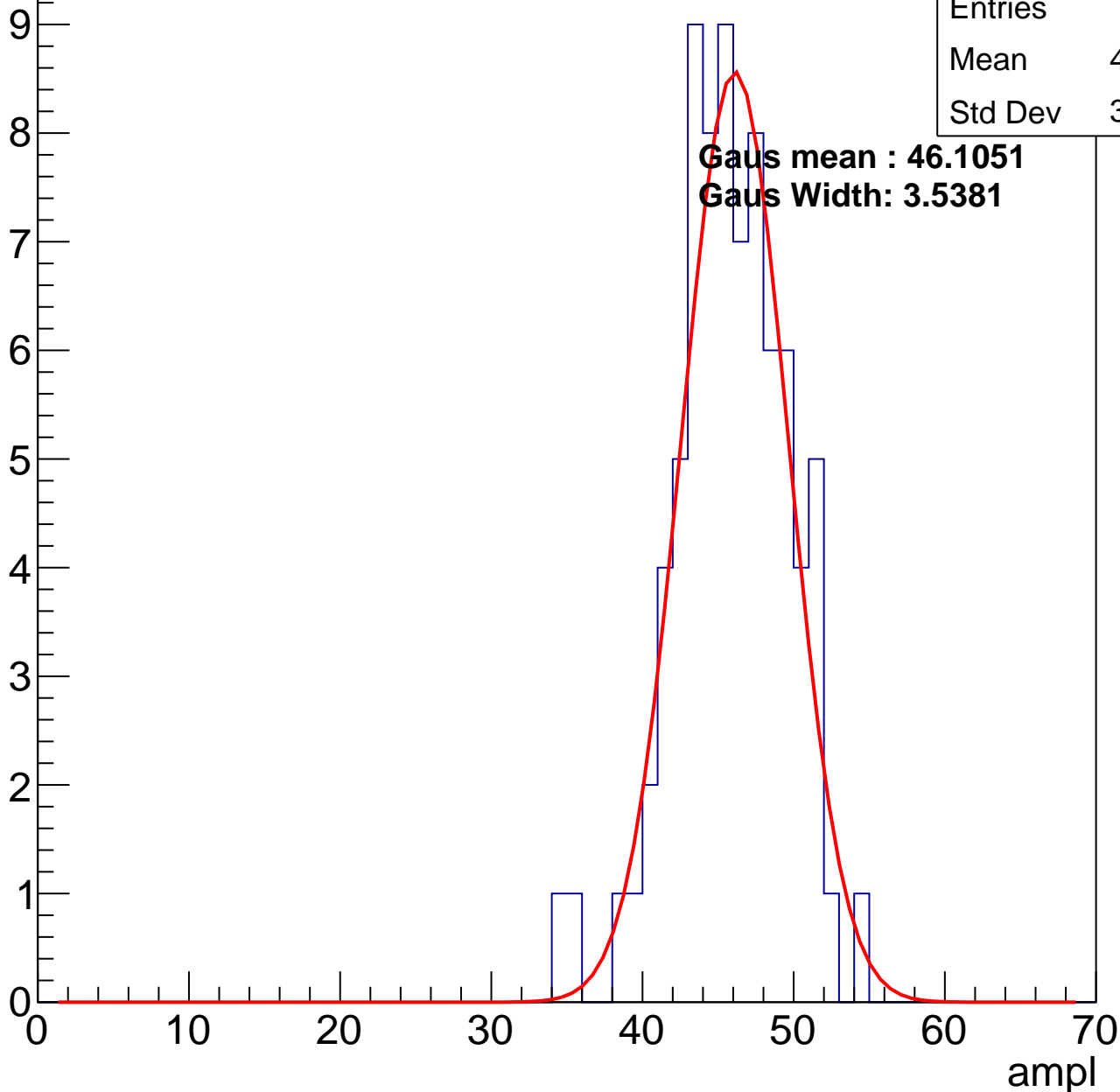
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	45.38
Std Dev	3.726

Gaus mean : 46.1051

Gaus Width: 3.5381

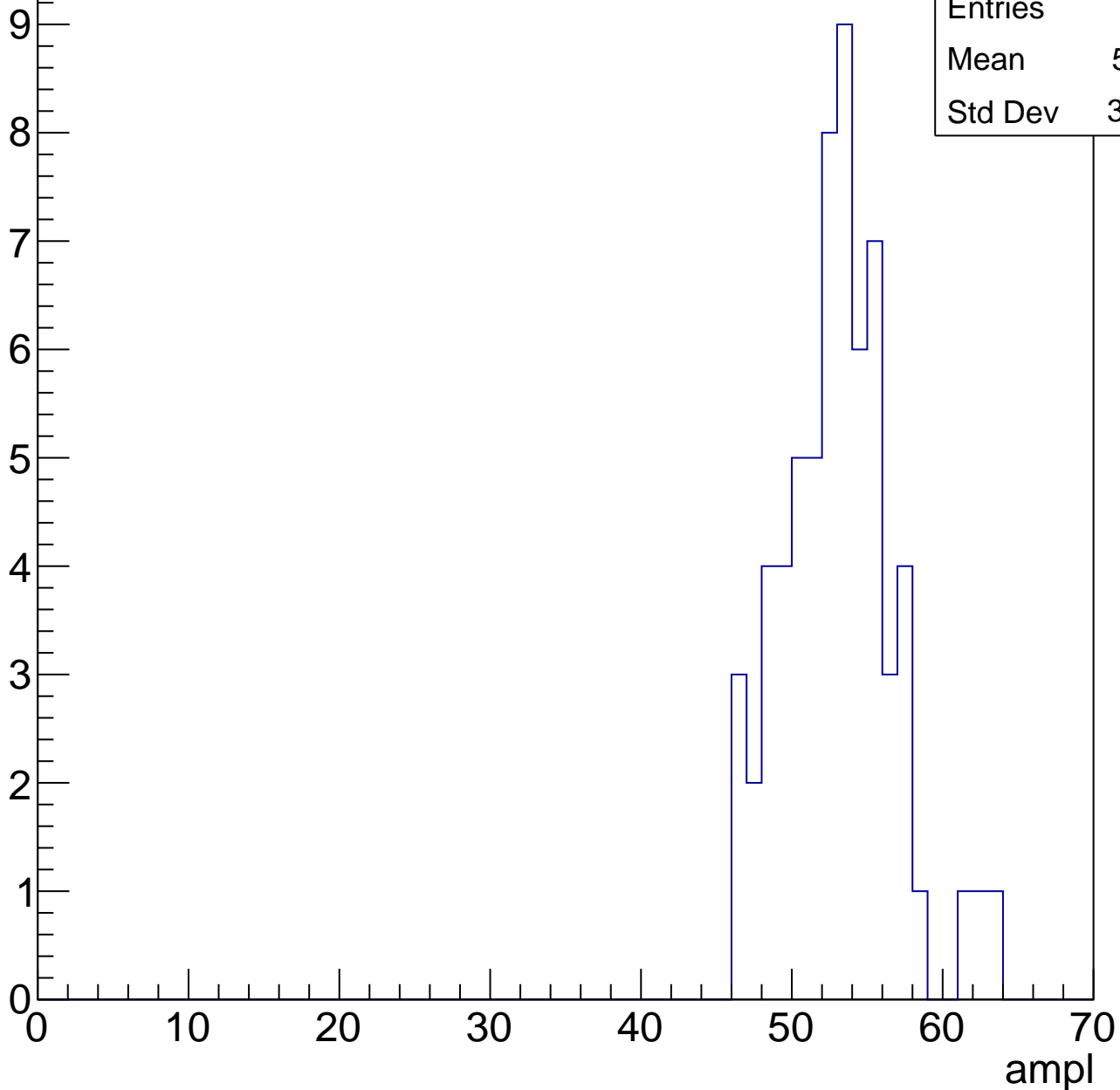


B0L002S, U26-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	52.61
Std Dev	3.626



B0L002S, U26-ch121, adc4

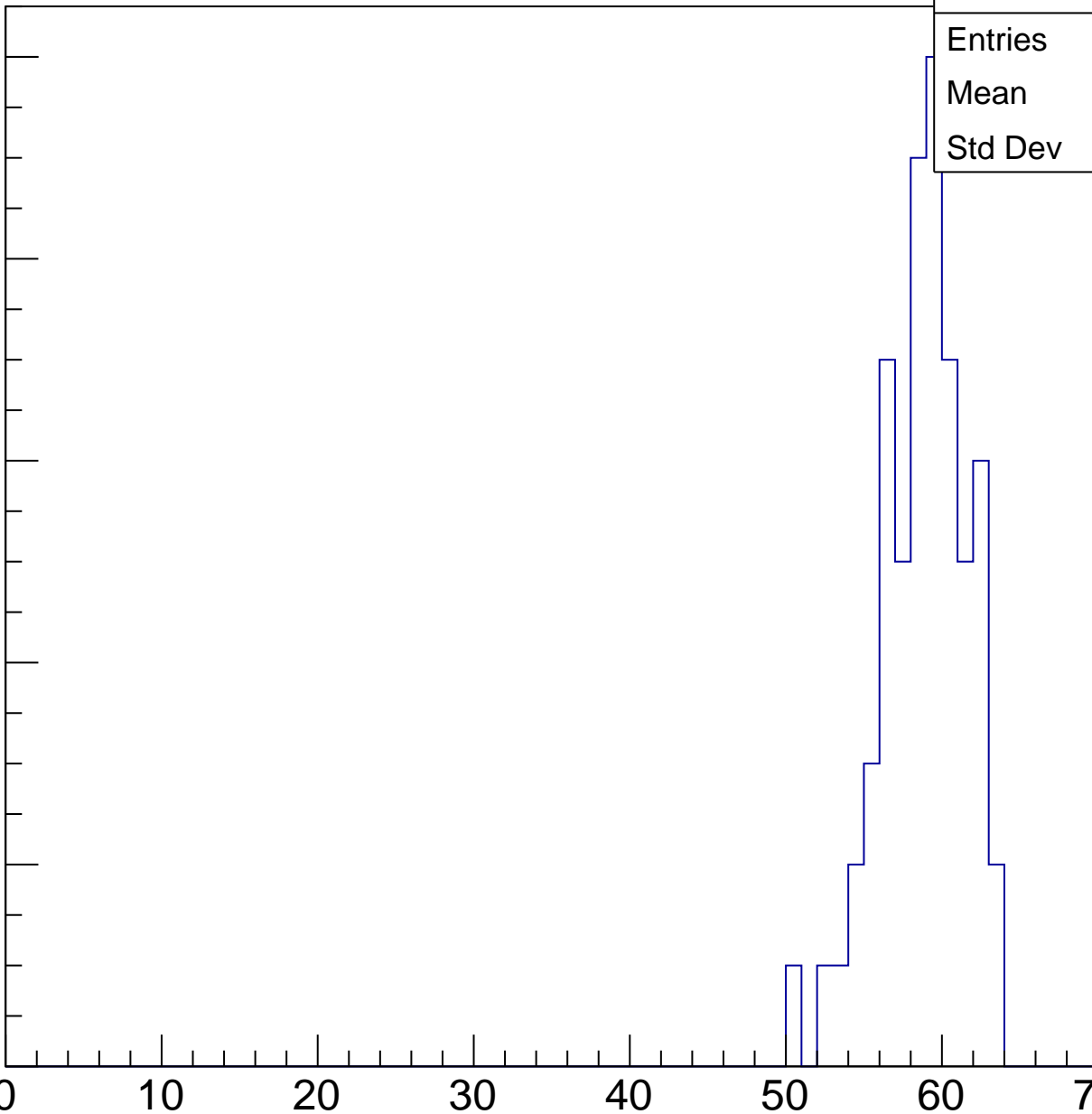
calib_packv5_042523_0143.root, FC#8, port C1

Entries	59
Mean	58.31
Std Dev	2.732

Entry

10
8
6
4
2
0

ampl

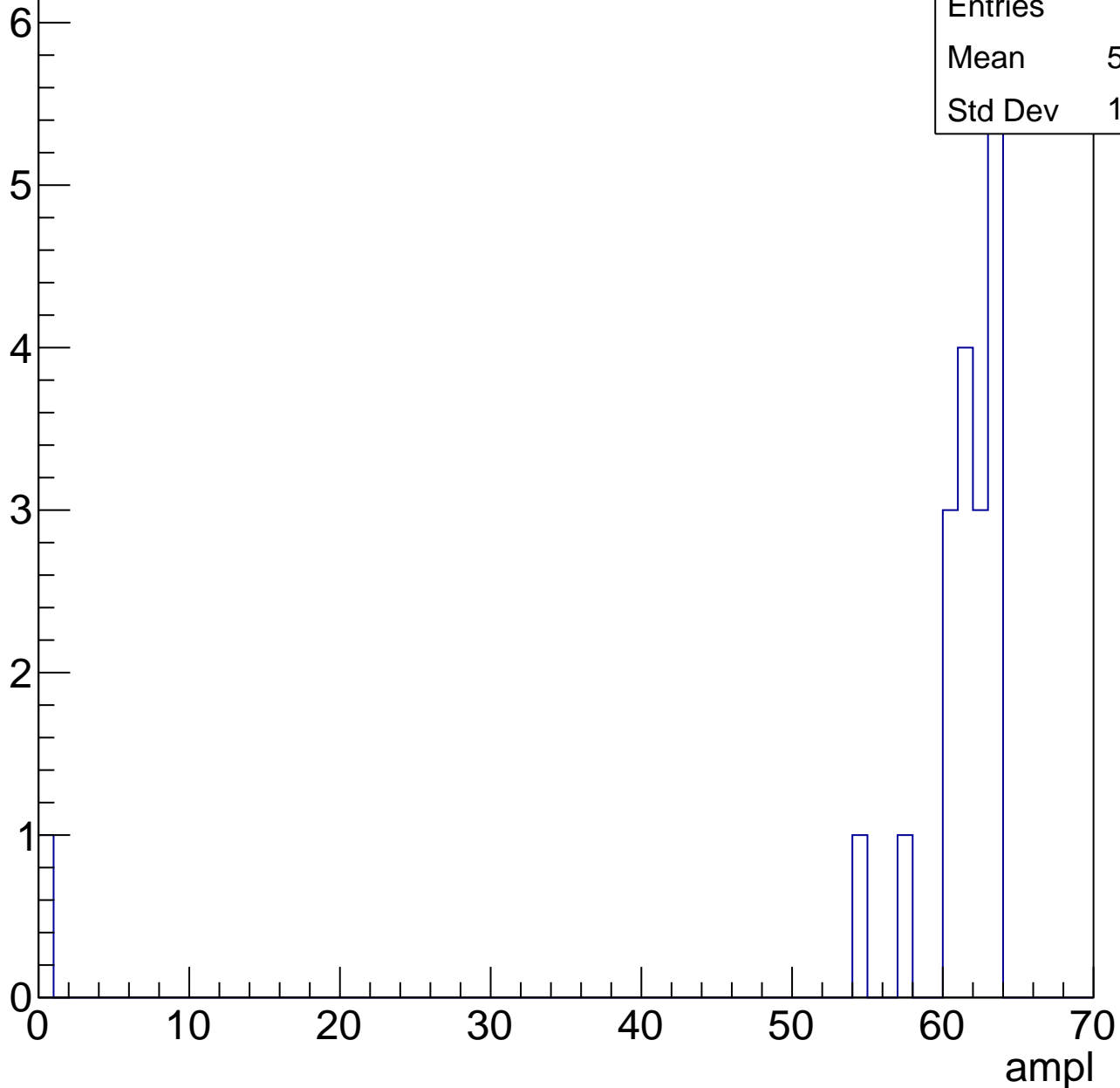


B0L002S, U26-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	57.84
Std Dev	13.82



B0L002S, U26-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U26-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch122, adc0

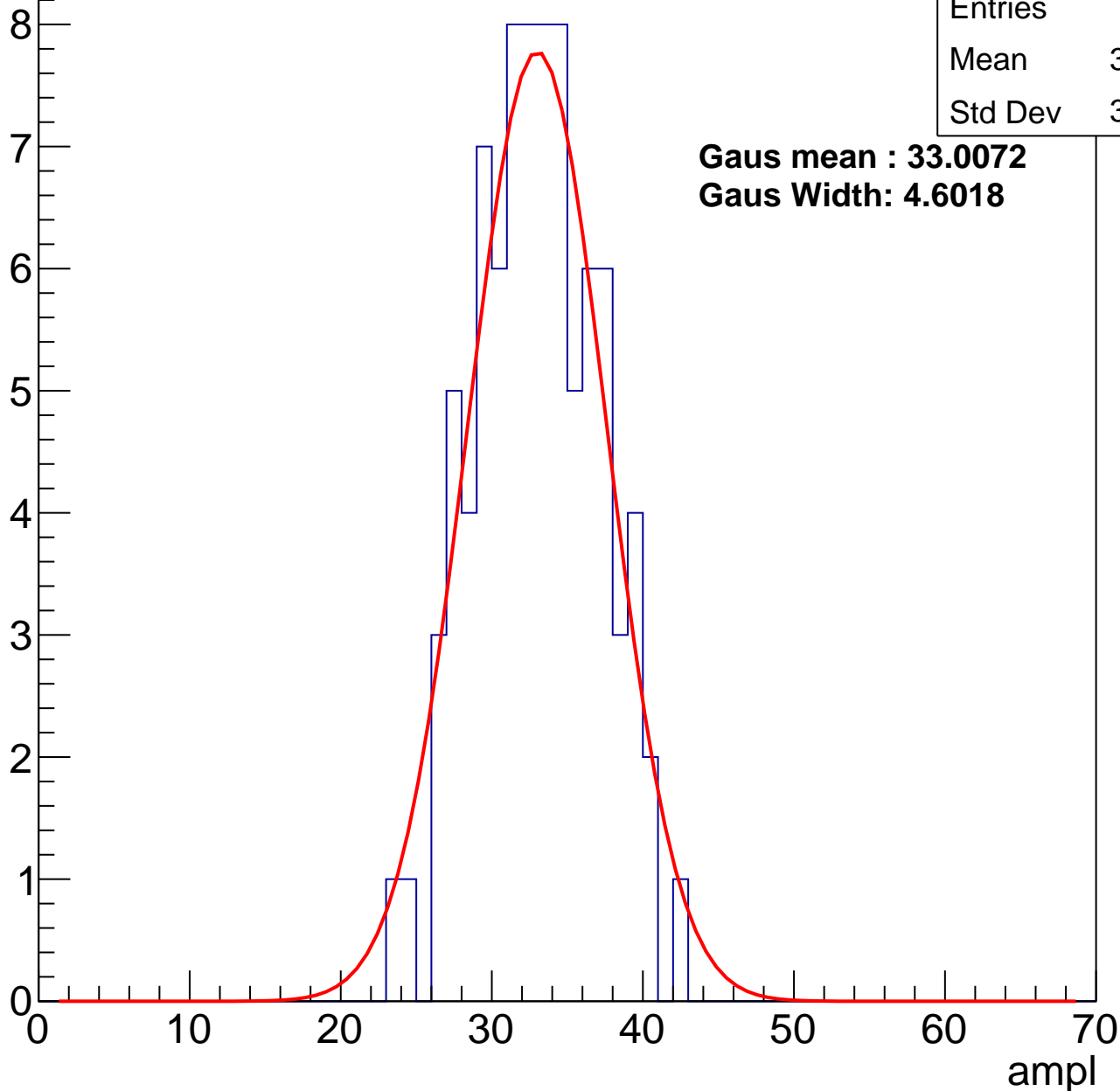
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	32.56
Std Dev	3.996

Gaus mean : 33.0072

Gaus Width: 4.6018



B0L002S, U26-ch122, adc1

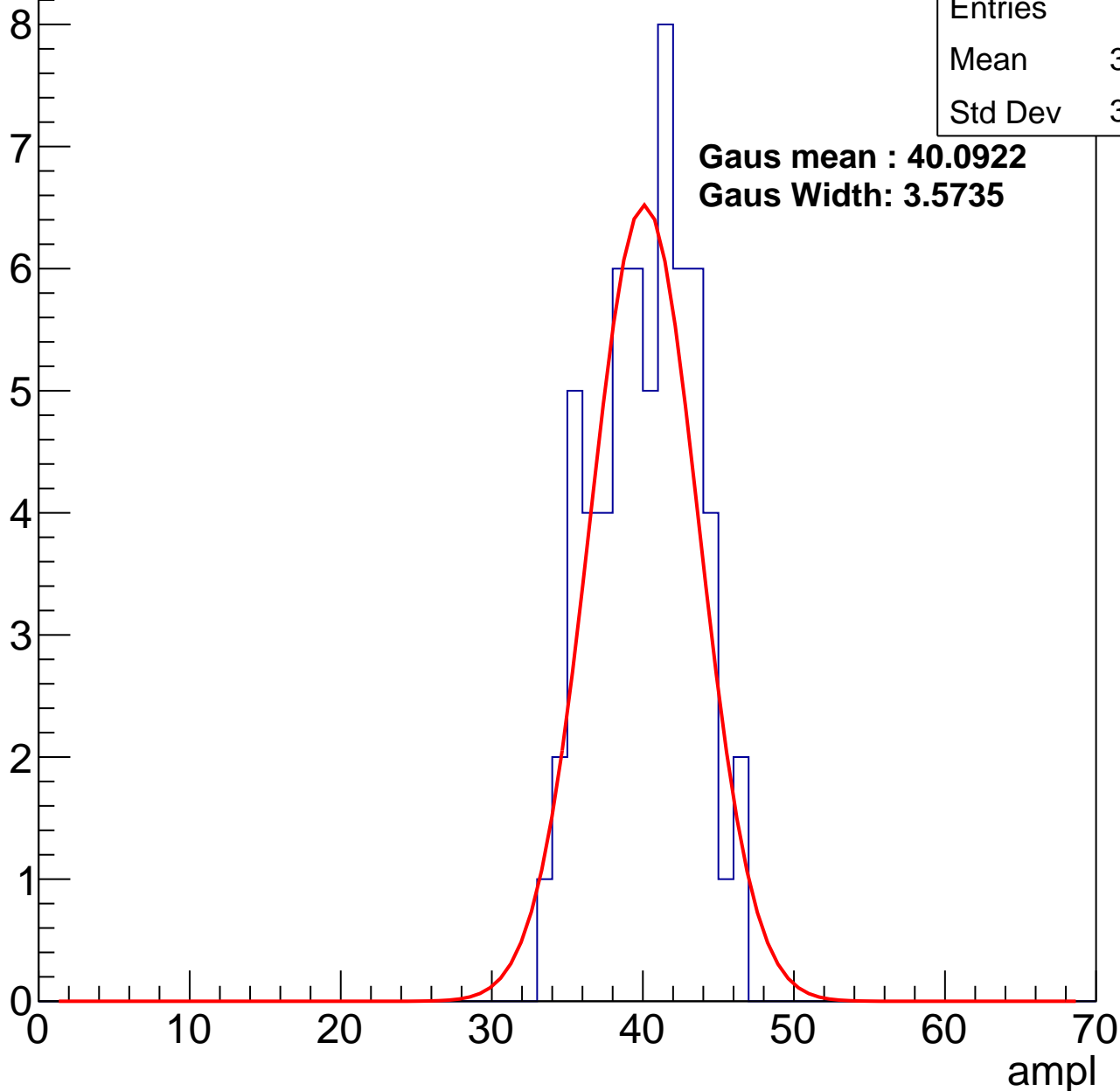
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	39.68
Std Dev	3.207

Gaus mean : 40.0922

Gaus Width: 3.5735



B0L002S, U26-ch122, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

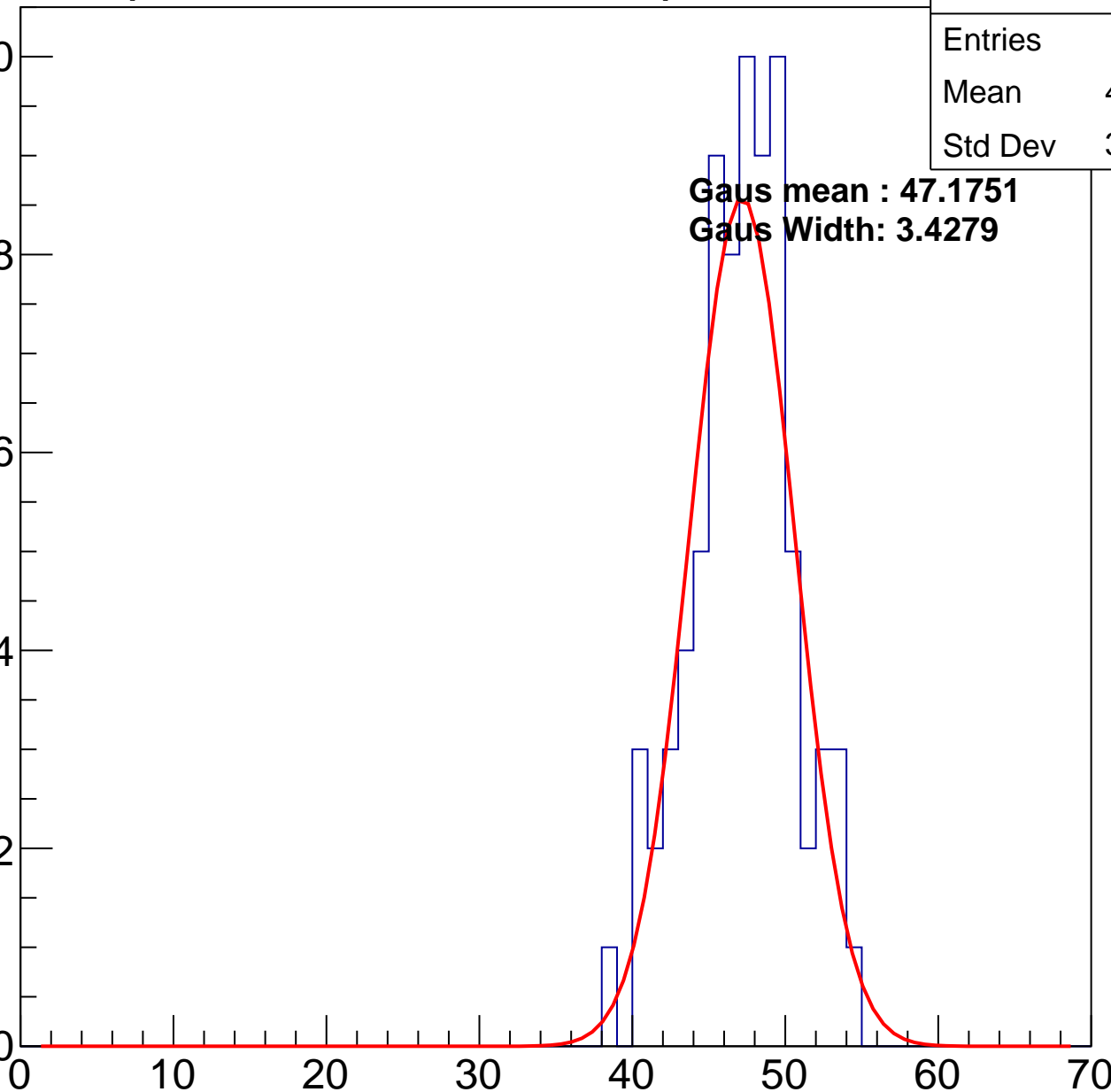
Entries	78
Mean	46.72
Std Dev	3.358

Gaus mean : 47.1751

Gaus Width: 3.4279

10
8
6
4
2
0

ampl

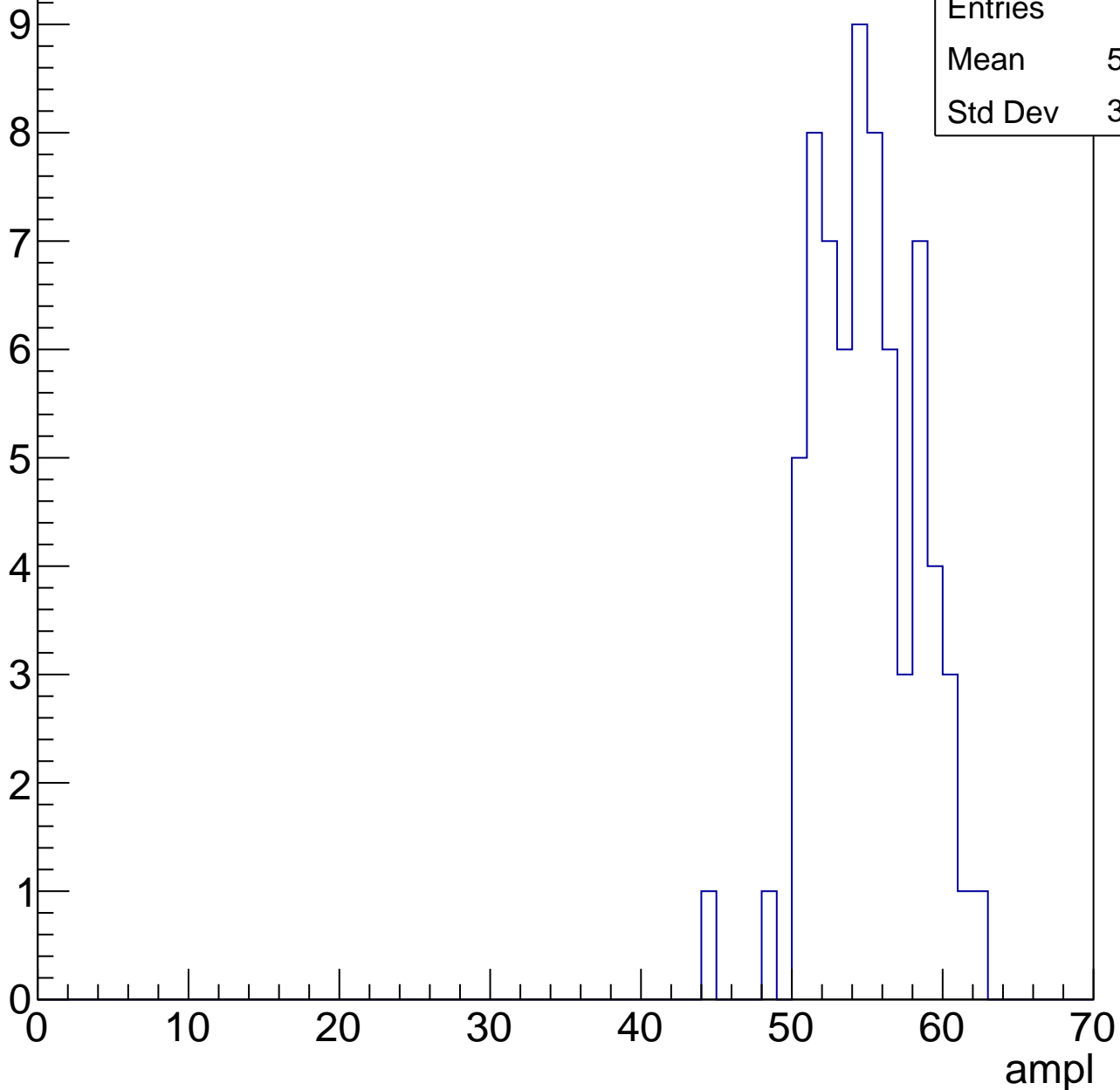


B0L002S, U26-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

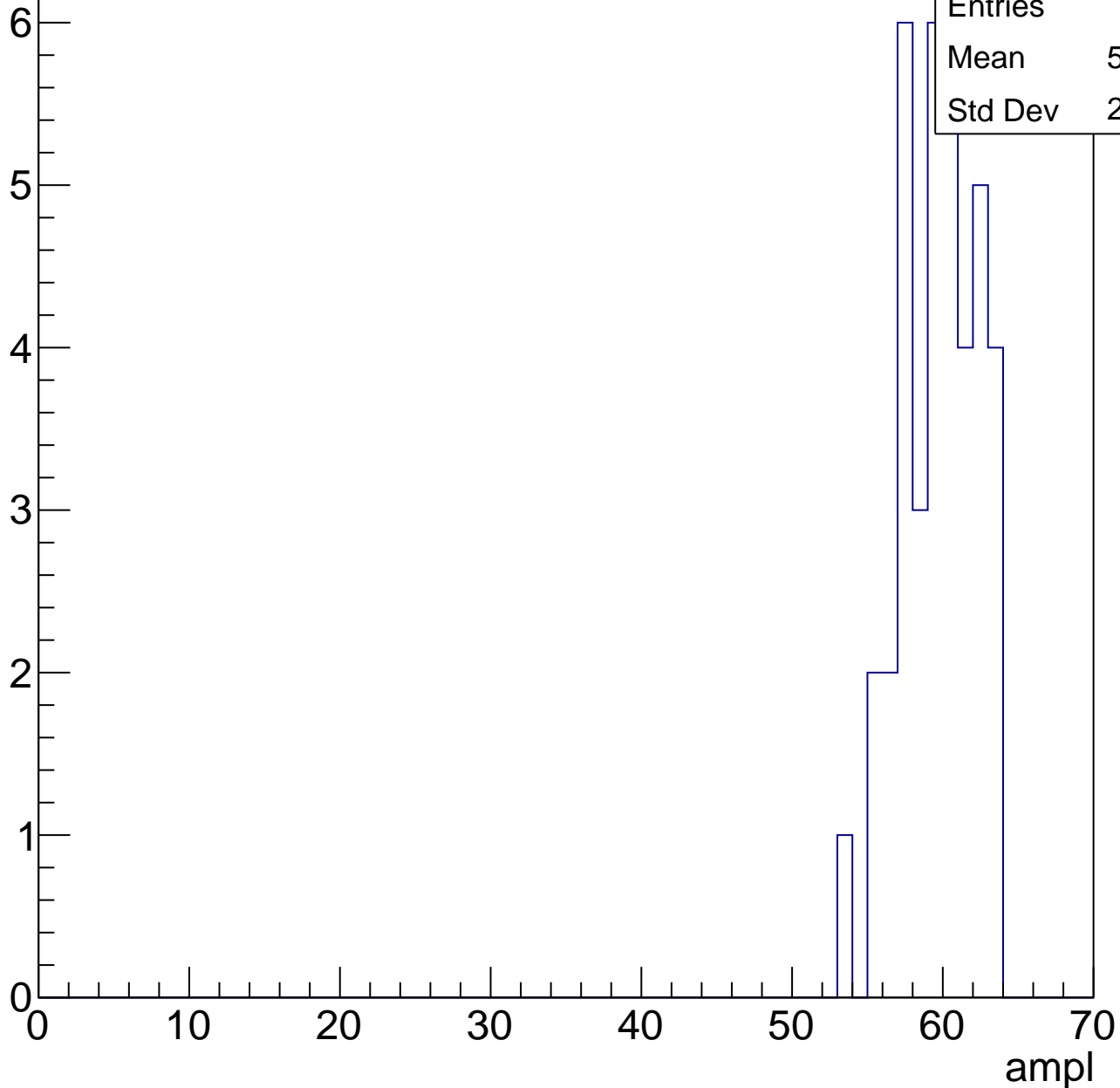
Entries	70
Mean	54.43
Std Dev	3.383



B0L002S, U26-ch122, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



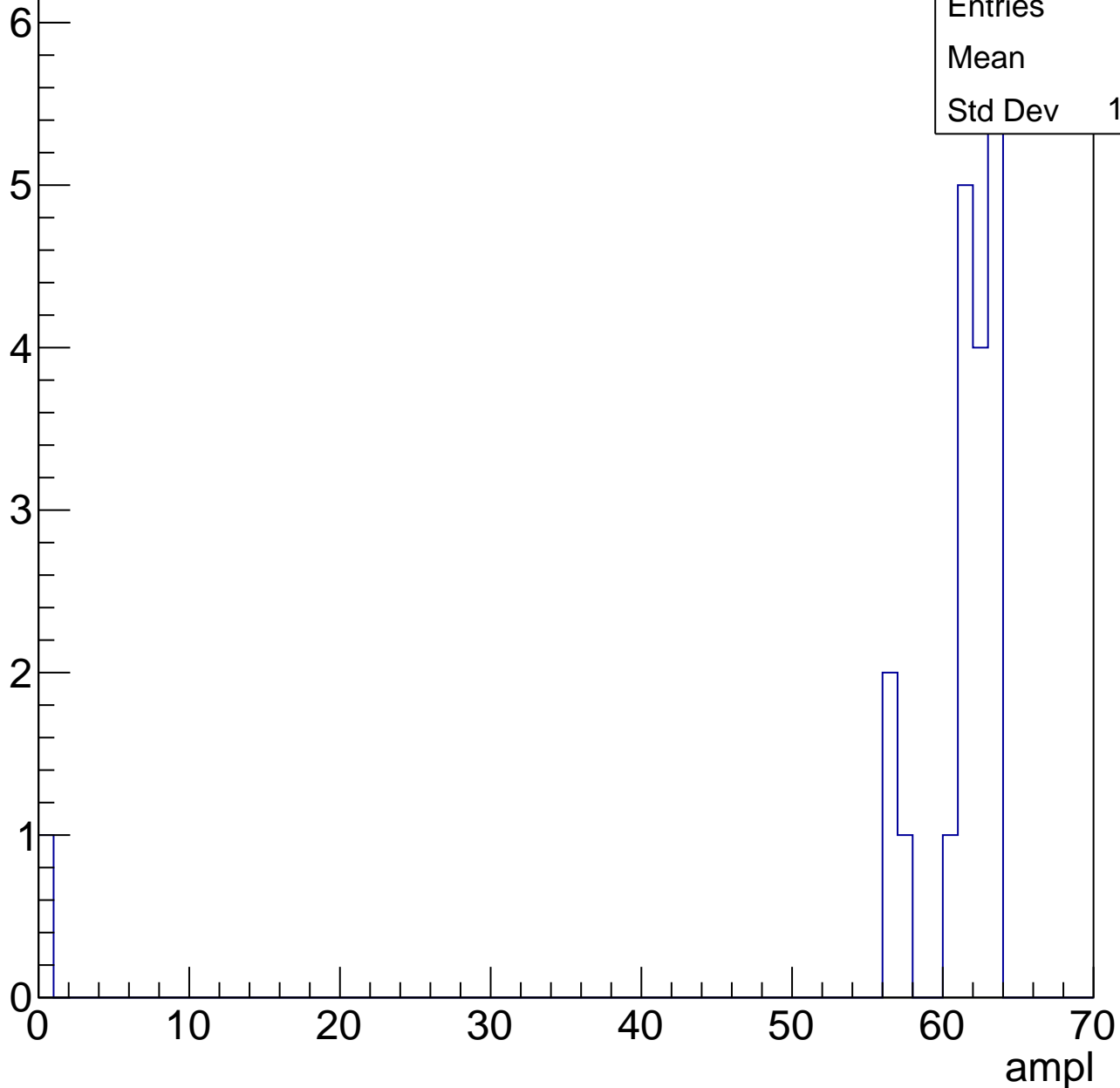
Entries	39
Mean	59.26
Std Dev	2.488

B0L002S, U26-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58
Std Dev	13.48



B0L002S, U26-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch123, adc0

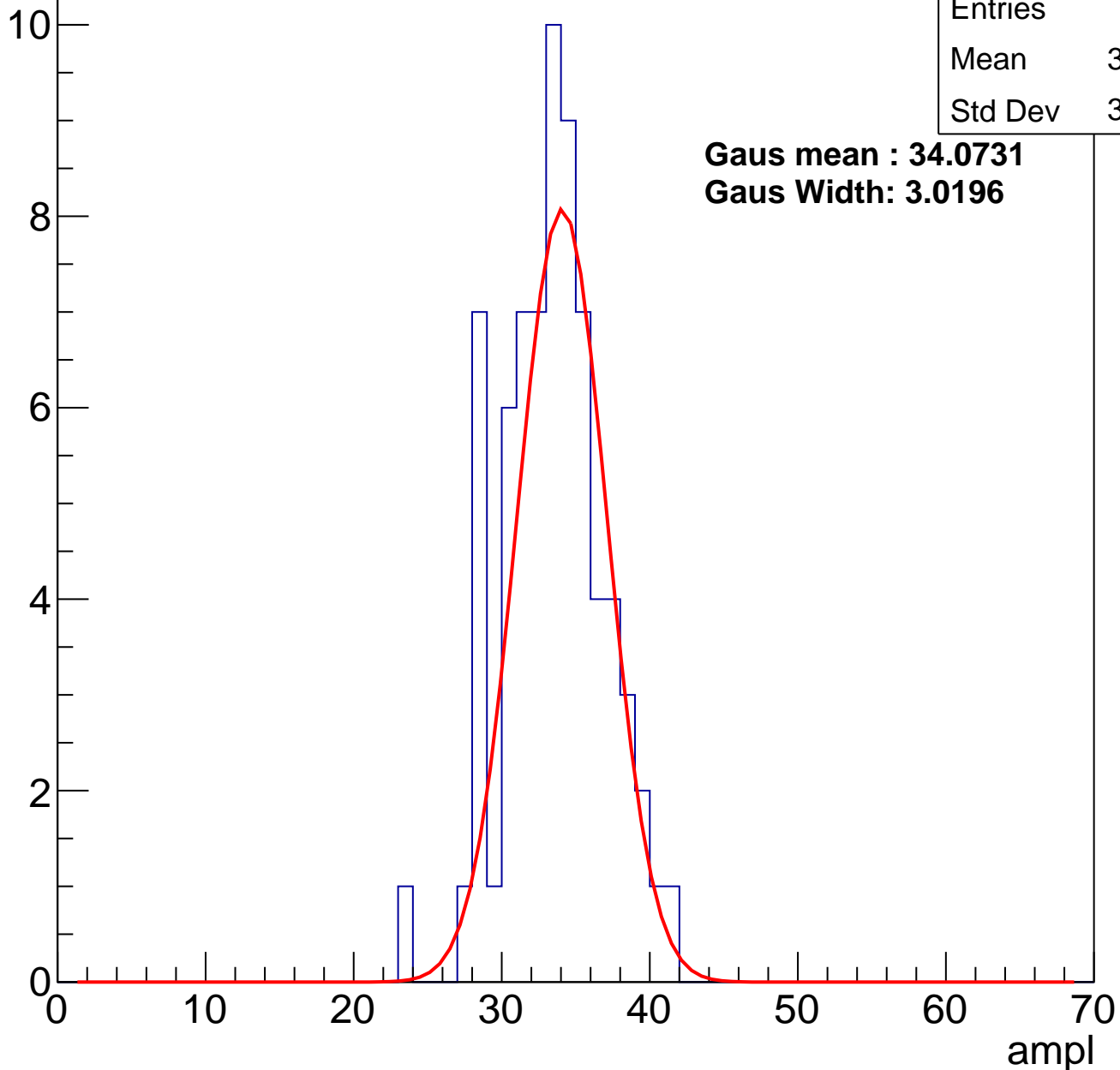
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	32.99
Std Dev	3.384

Gaus mean : 34.0731

Gaus Width: 3.0196

Entry



B0L002S, U26-ch123, adc1

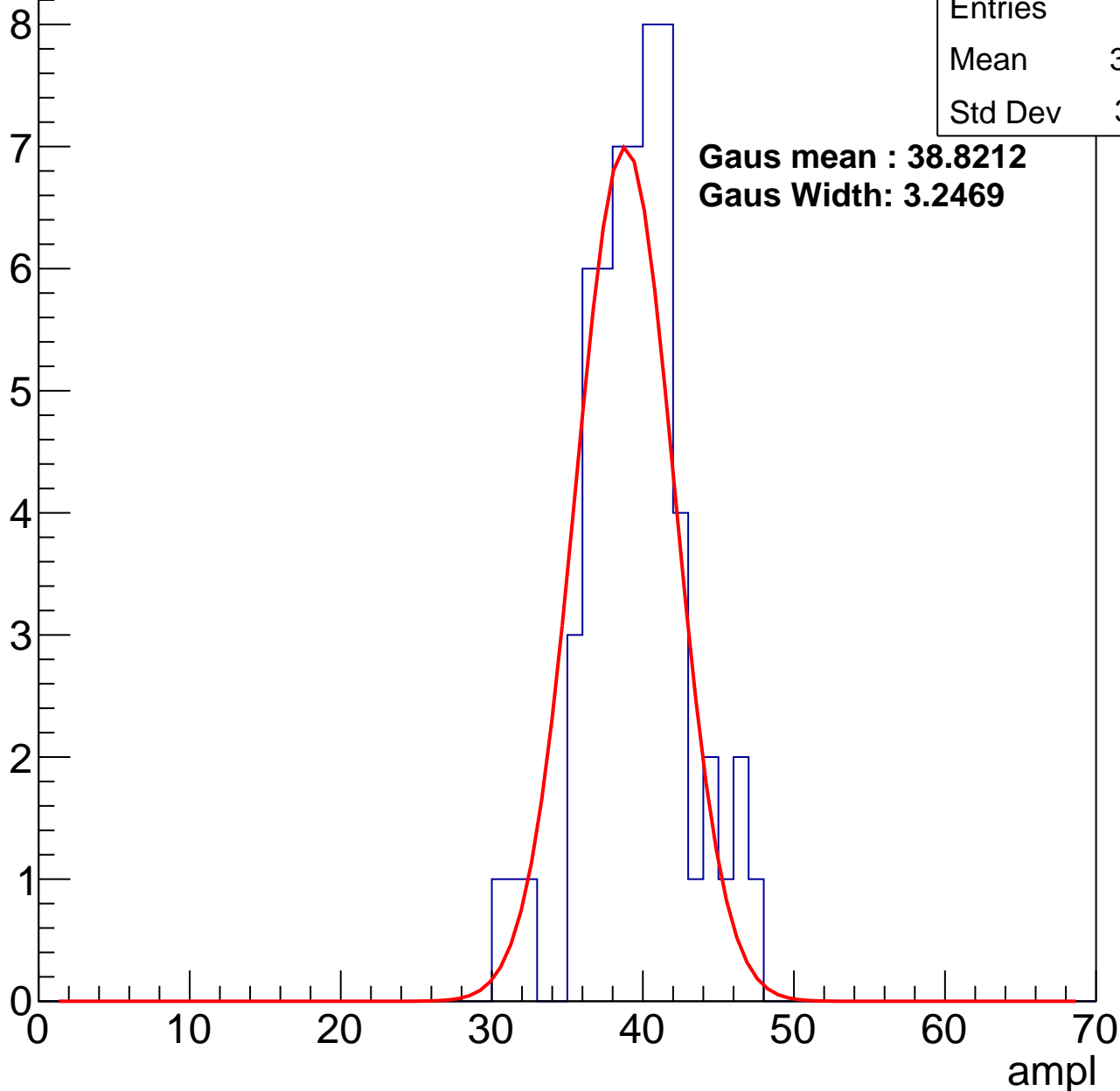
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	39.08
Std Dev	3.361

Gaus mean : 38.8212

Gaus Width: 3.2469



B0L002S, U26-ch123, adc2

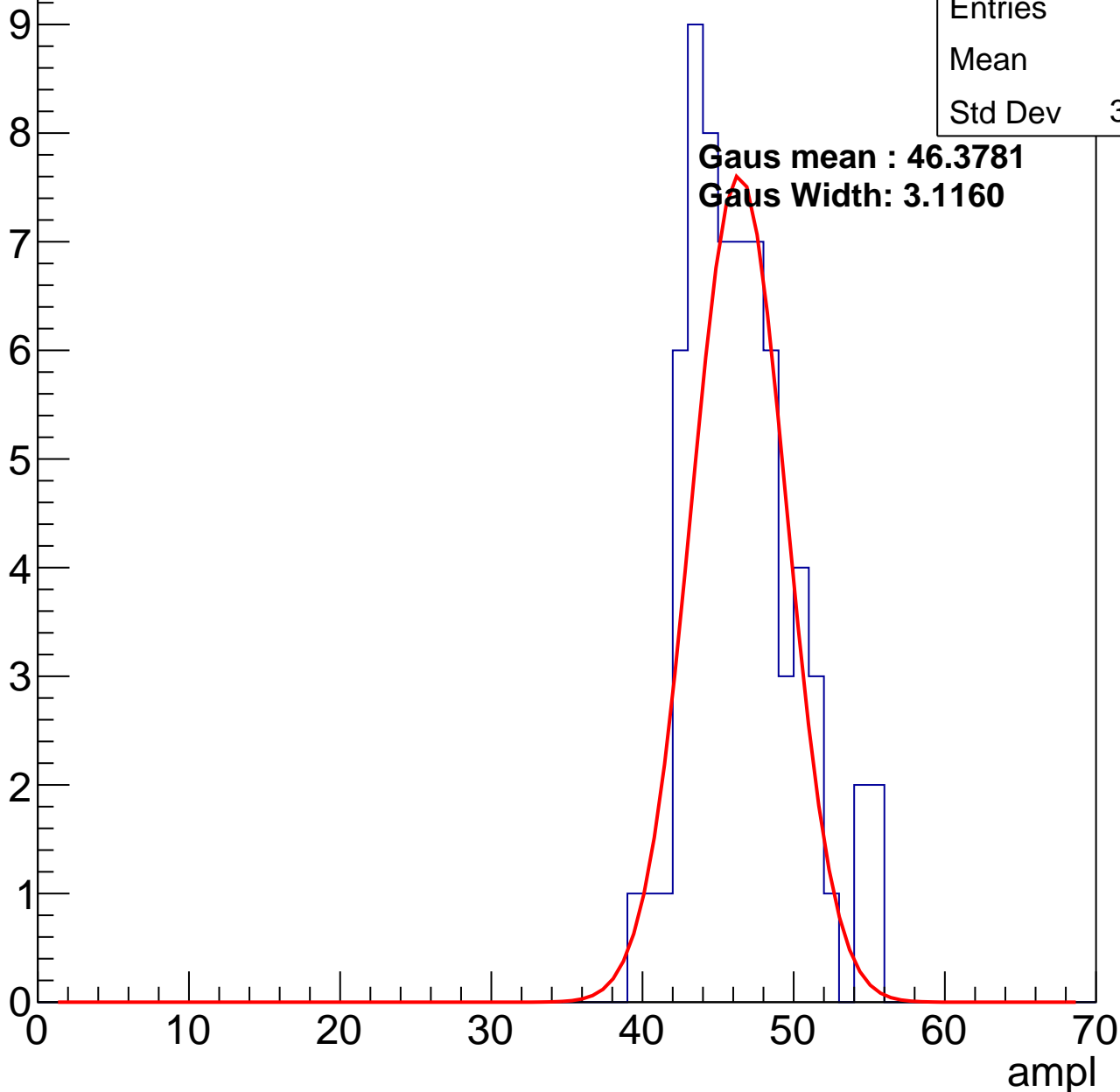
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	46.1
Std Dev	3.532

Gaus mean : 46.3781

Gaus Width: 3.1160

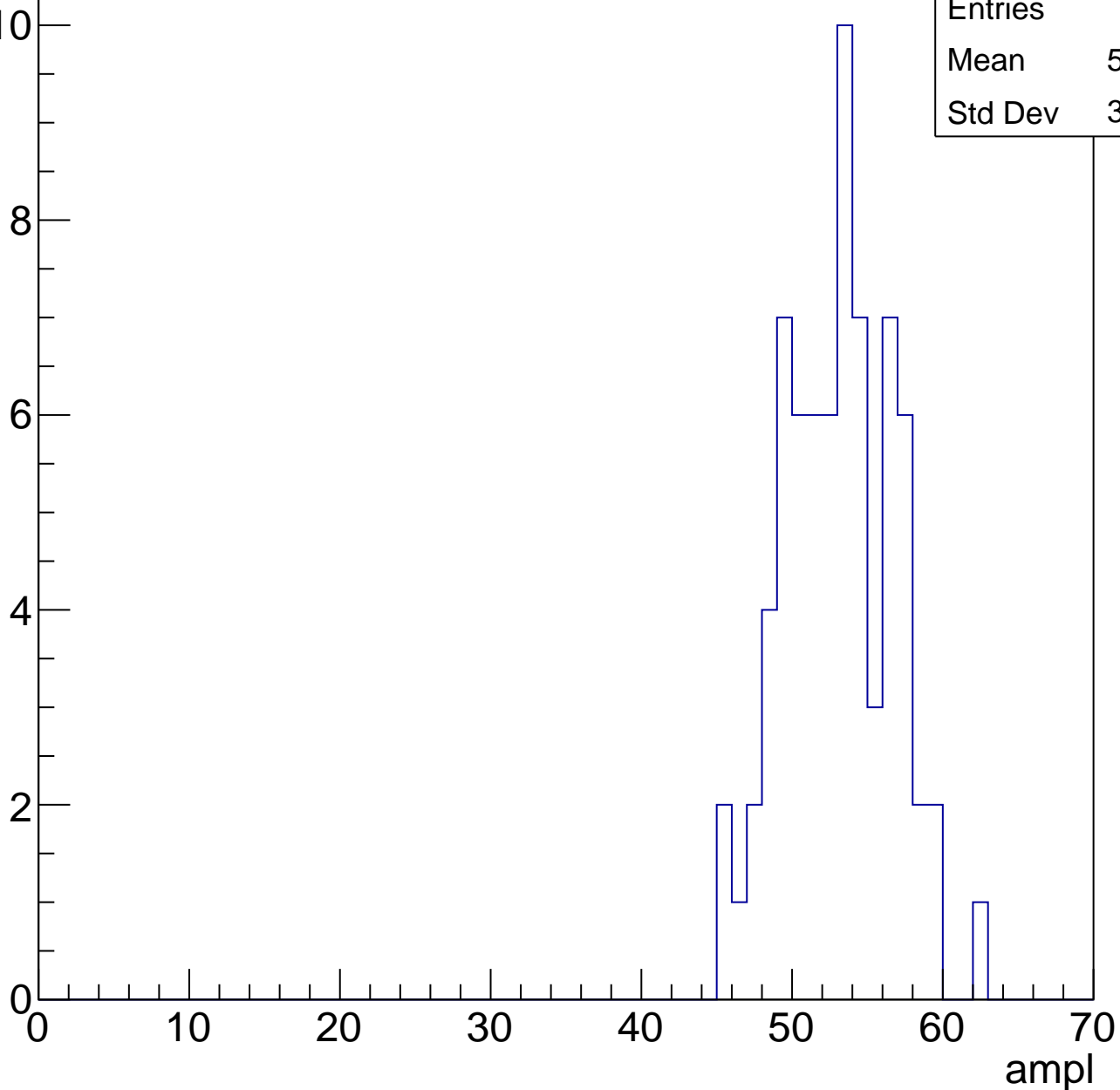


B0L002S, U26-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

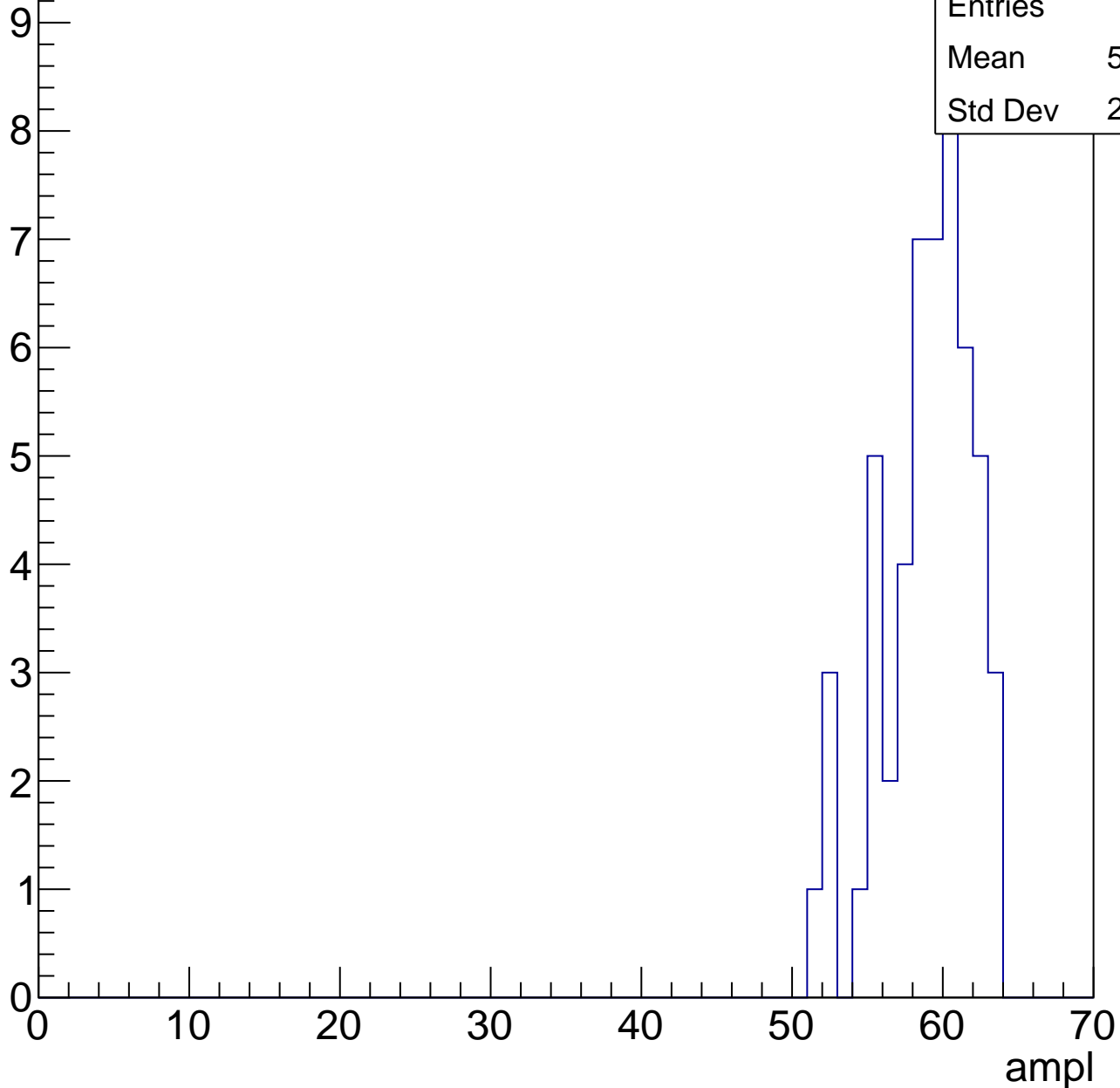
Entries	72
Mean	52.58
Std Dev	3.562



B0L002S, U26-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

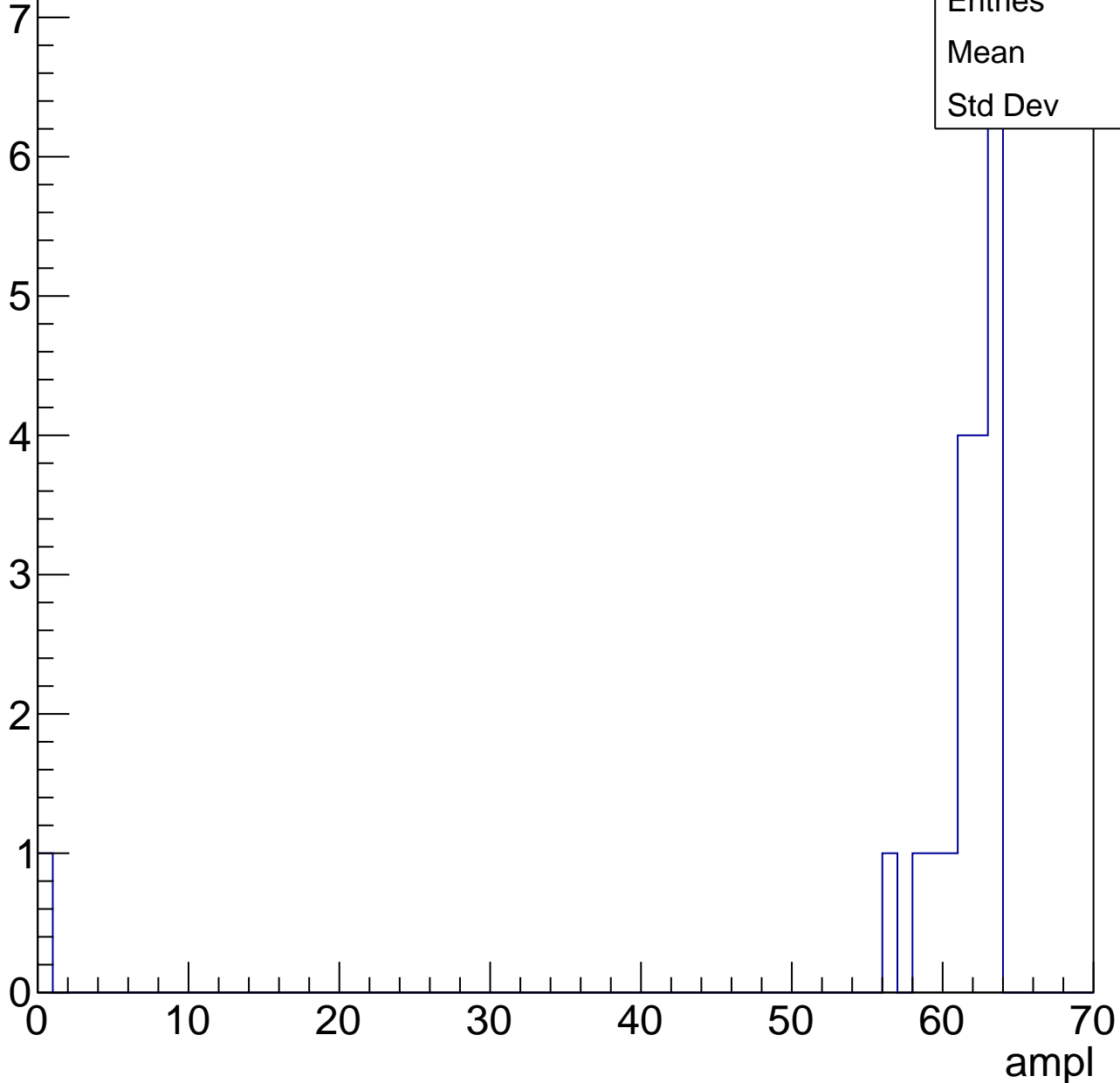


B0L002S, U26-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58.3
Std Dev	13.5



B0L002S, U26-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch124, adc0

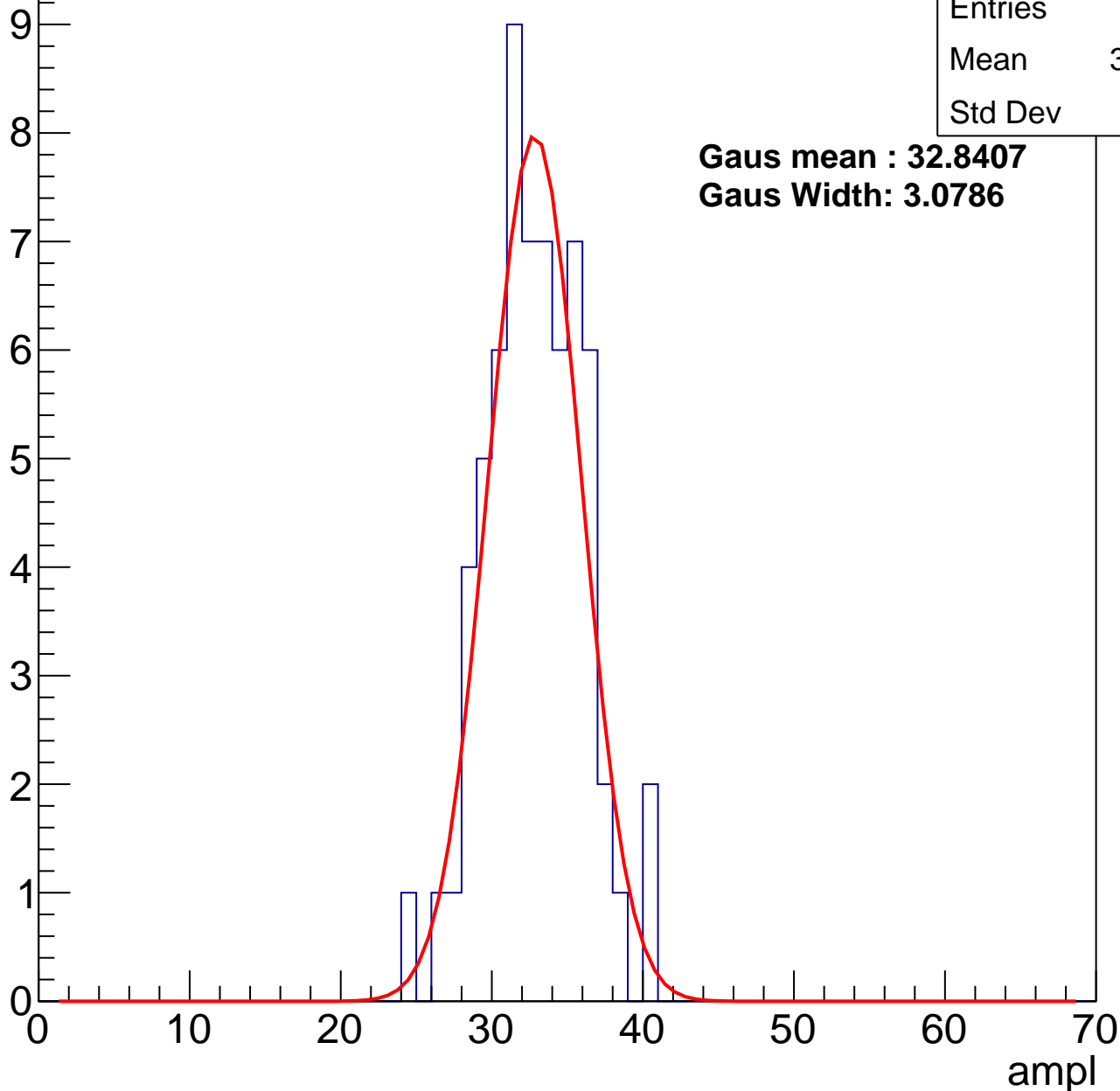
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	32.38
Std Dev	3.19

Gaus mean : 32.8407

Gaus Width: 3.0786



B0L002S, U26-ch124, adc1

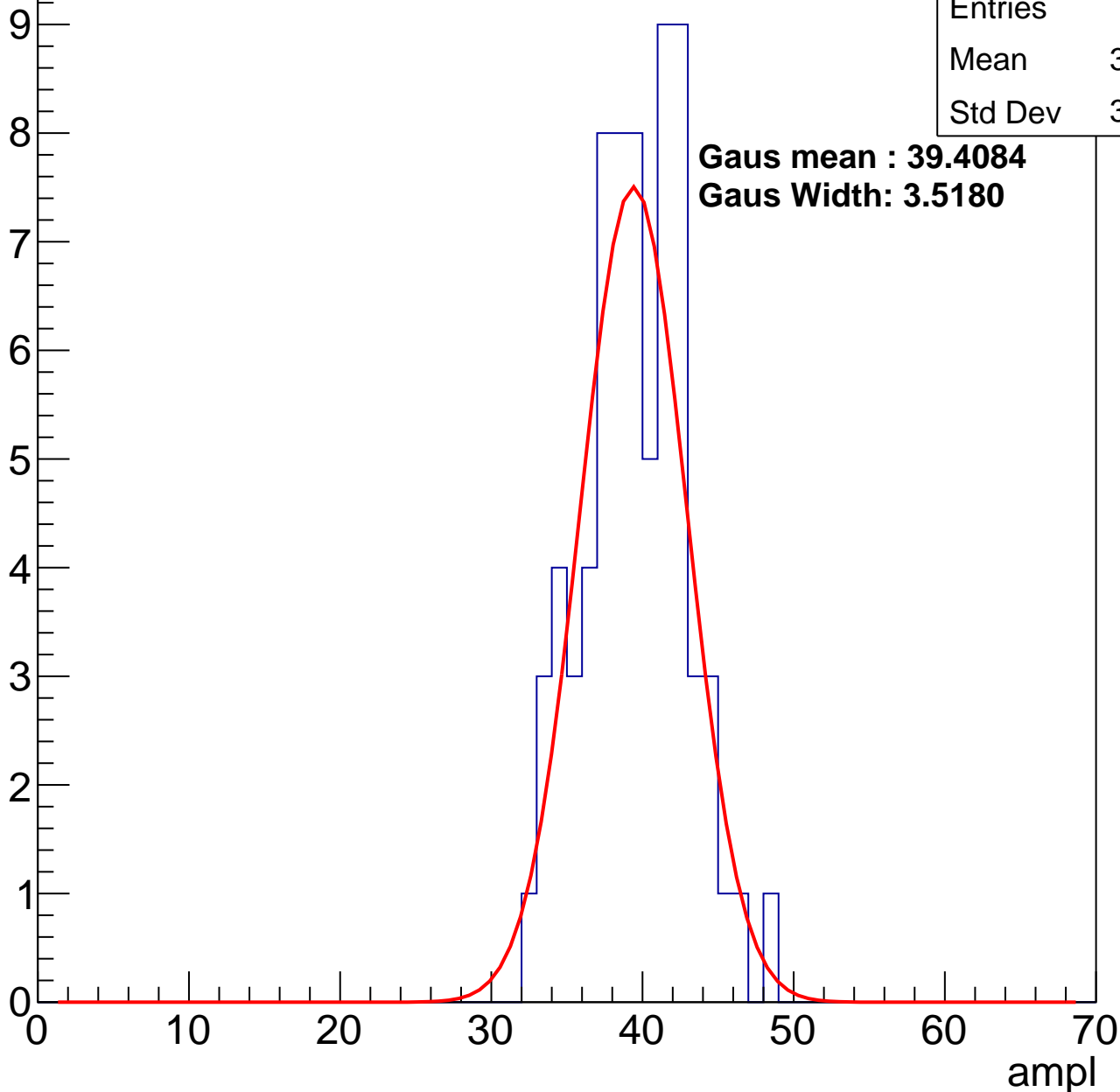
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	39.08
Std Dev	3.339

Gaus mean : 39.4084

Gaus Width: 3.5180



B0L002S, U26-ch124, adc2

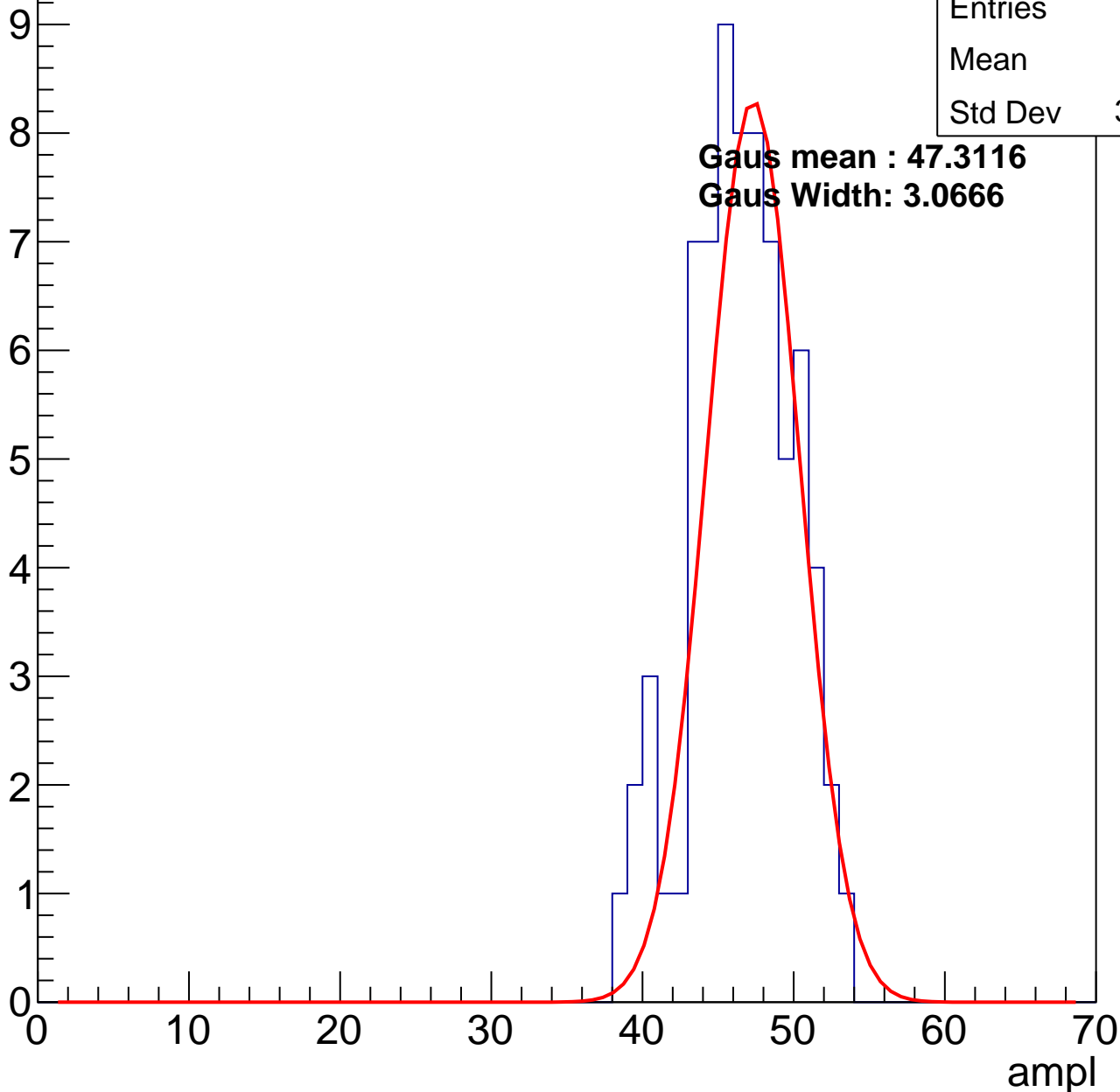
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	46.1
Std Dev	3.351

Gaus mean : 47.3116

Gaus Width: 3.0666

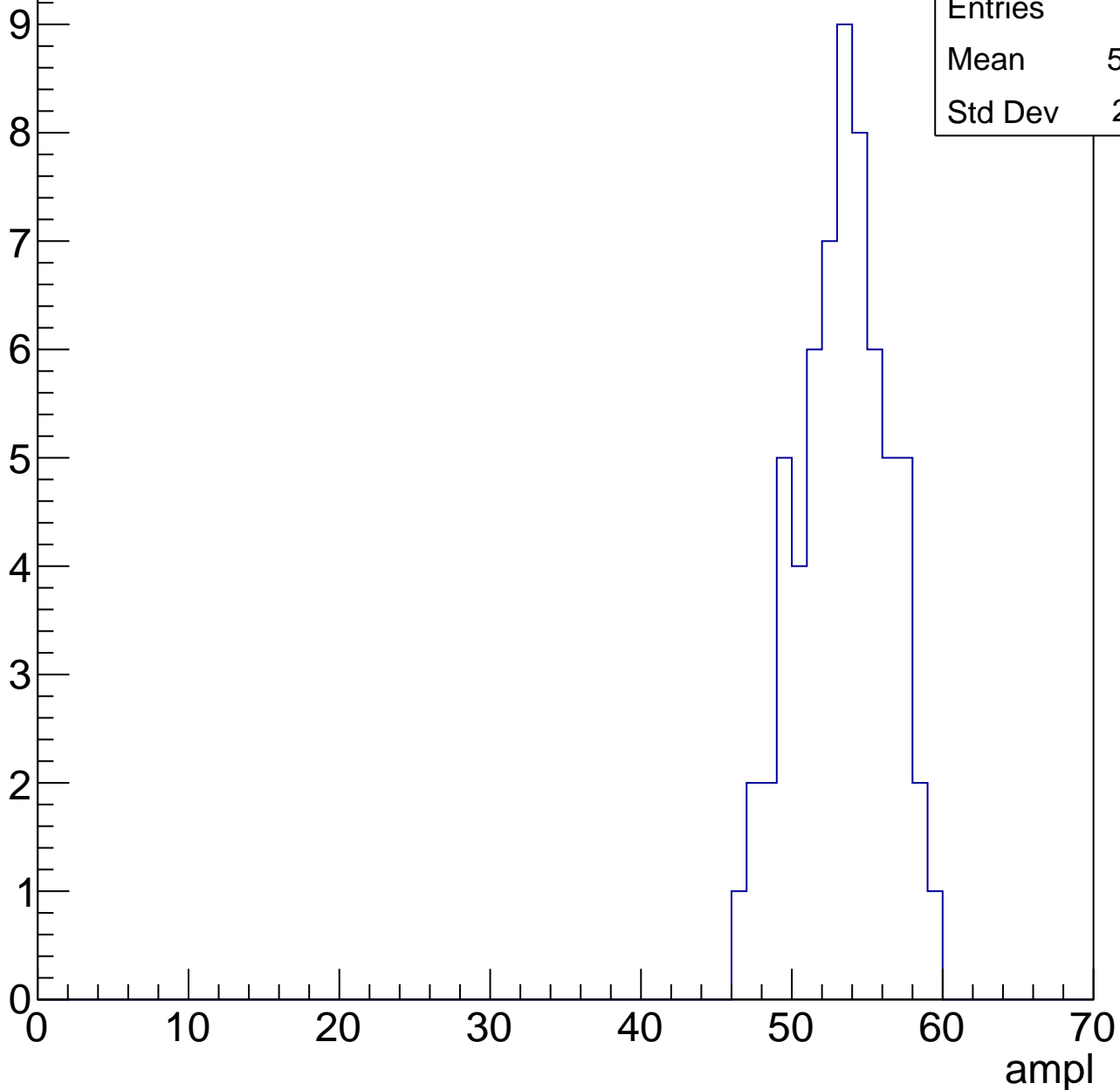


B0L002S, U26-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	52.86
Std Dev	2.981

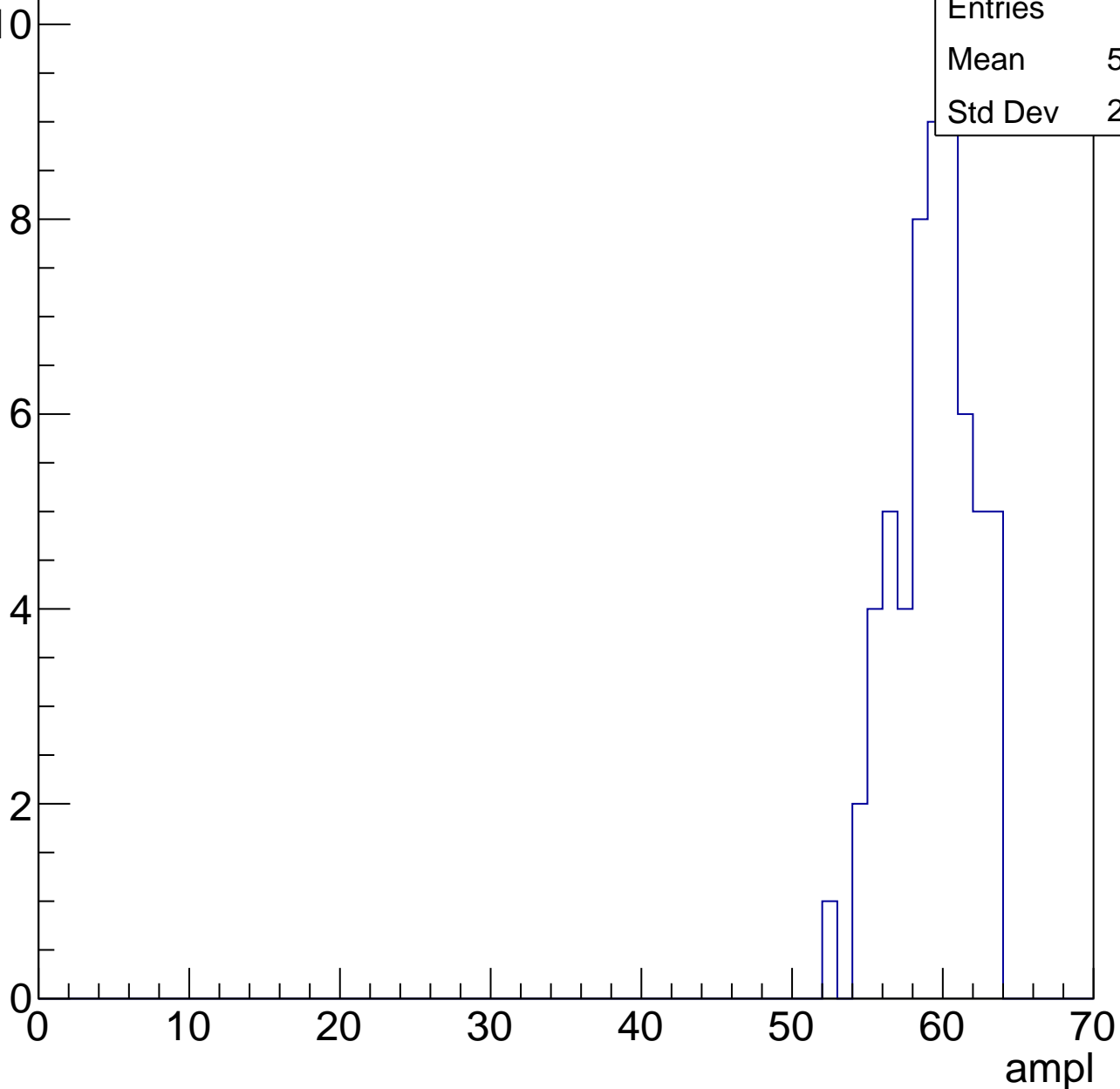


B0L002S, U26-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	58.88
Std Dev	2.572

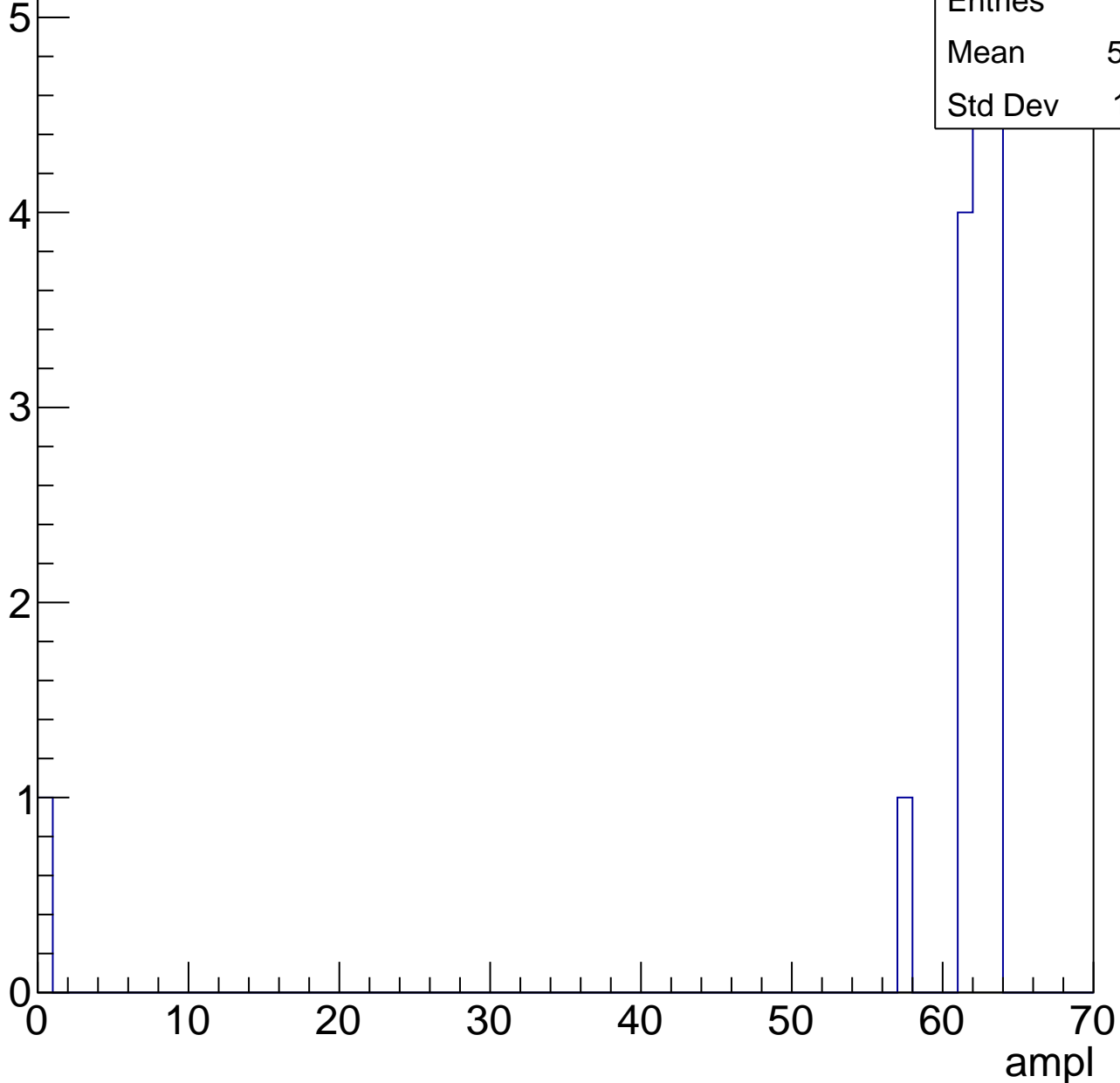


B0L002S, U26-ch124, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	16
Mean	57.88
Std Dev	15.01



B0L002S, U26-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch125, adc0

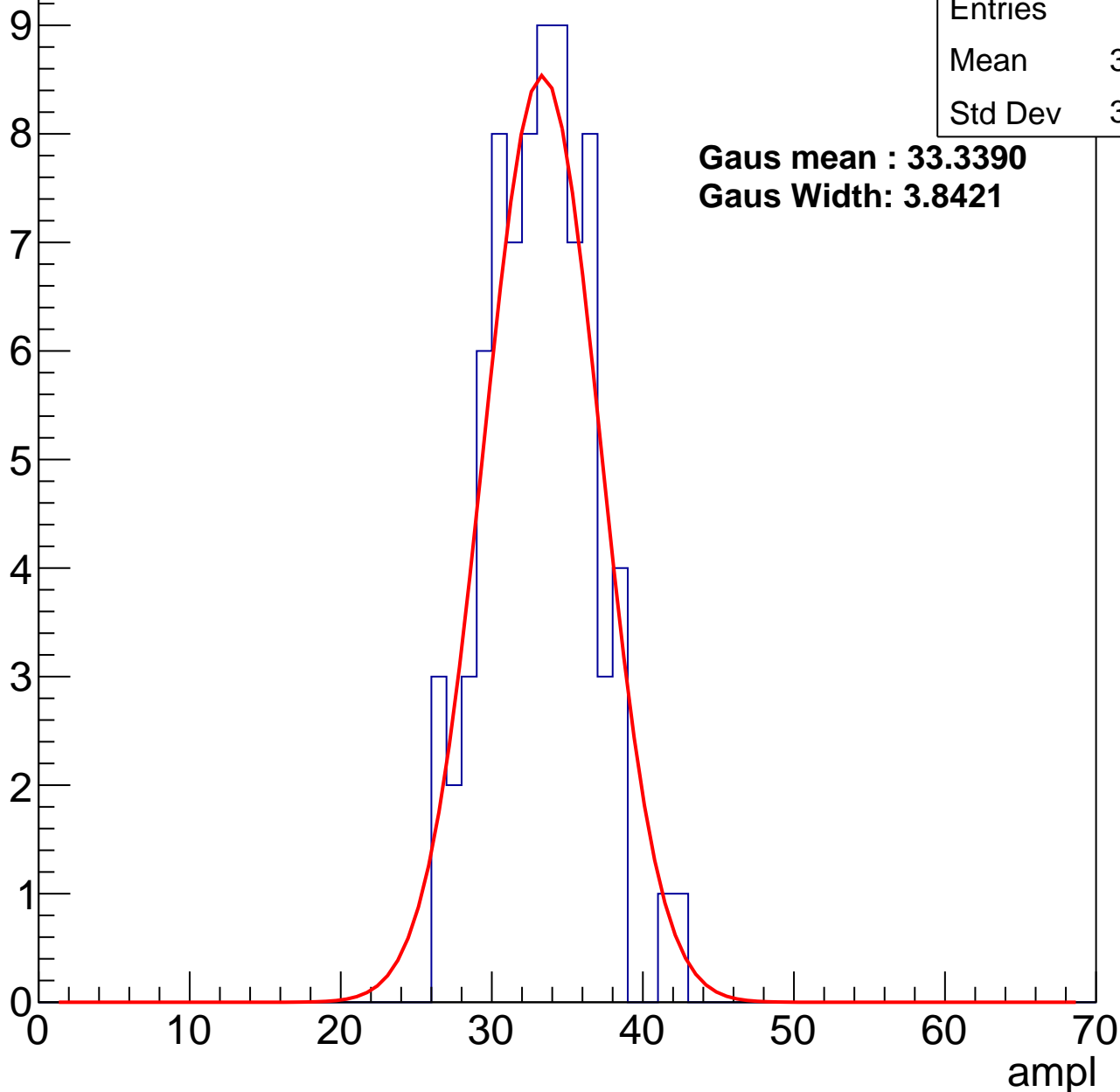
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	32.72
Std Dev	3.364

Gaus mean : 33.3390

Gaus Width: 3.8421



B0L002S, U26-ch125, adc1

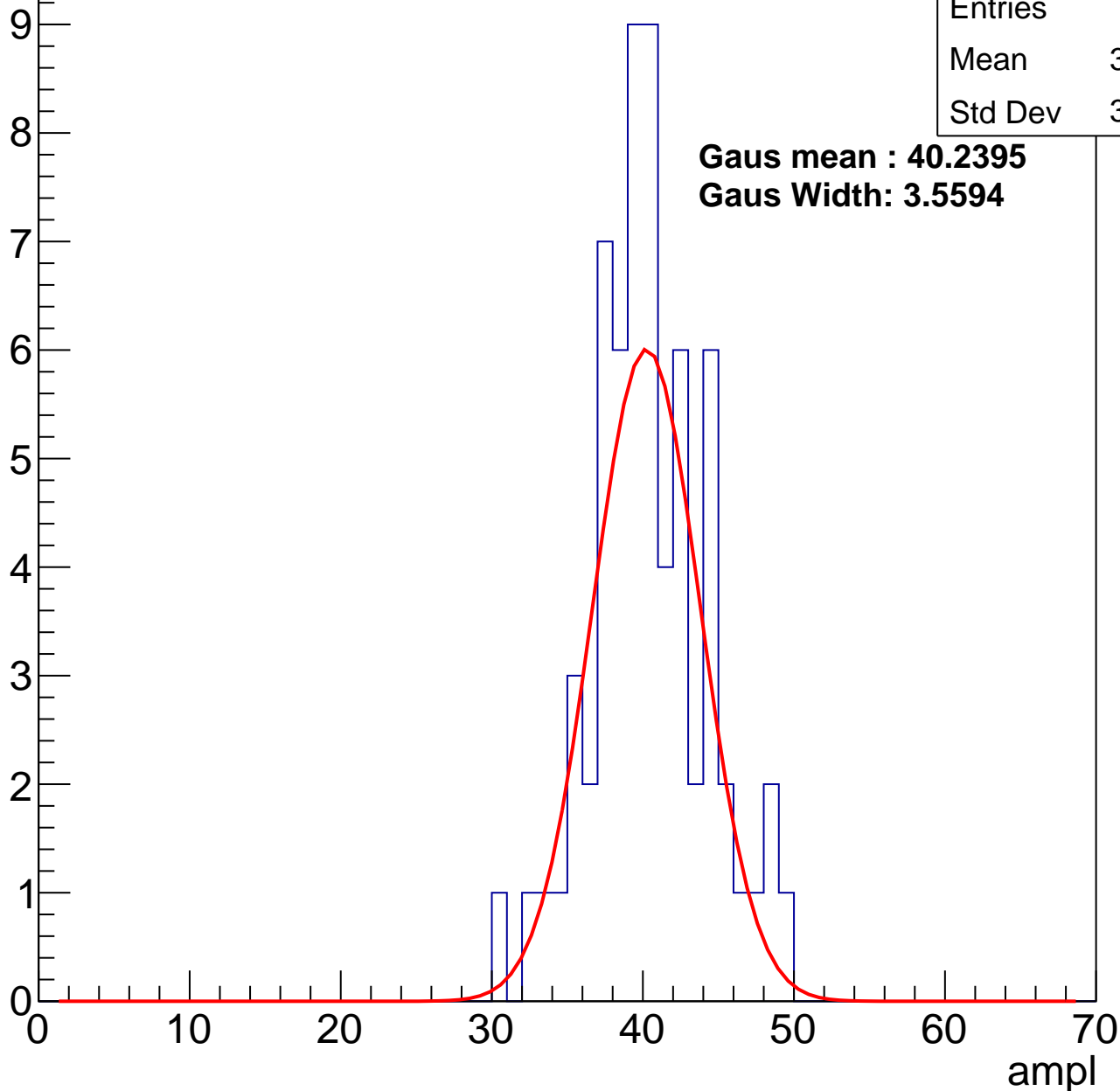
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	39.97
Std Dev	3.799

Gaus mean : 40.2395

Gaus Width: 3.5594



B0L002S, U26-ch125, adc2

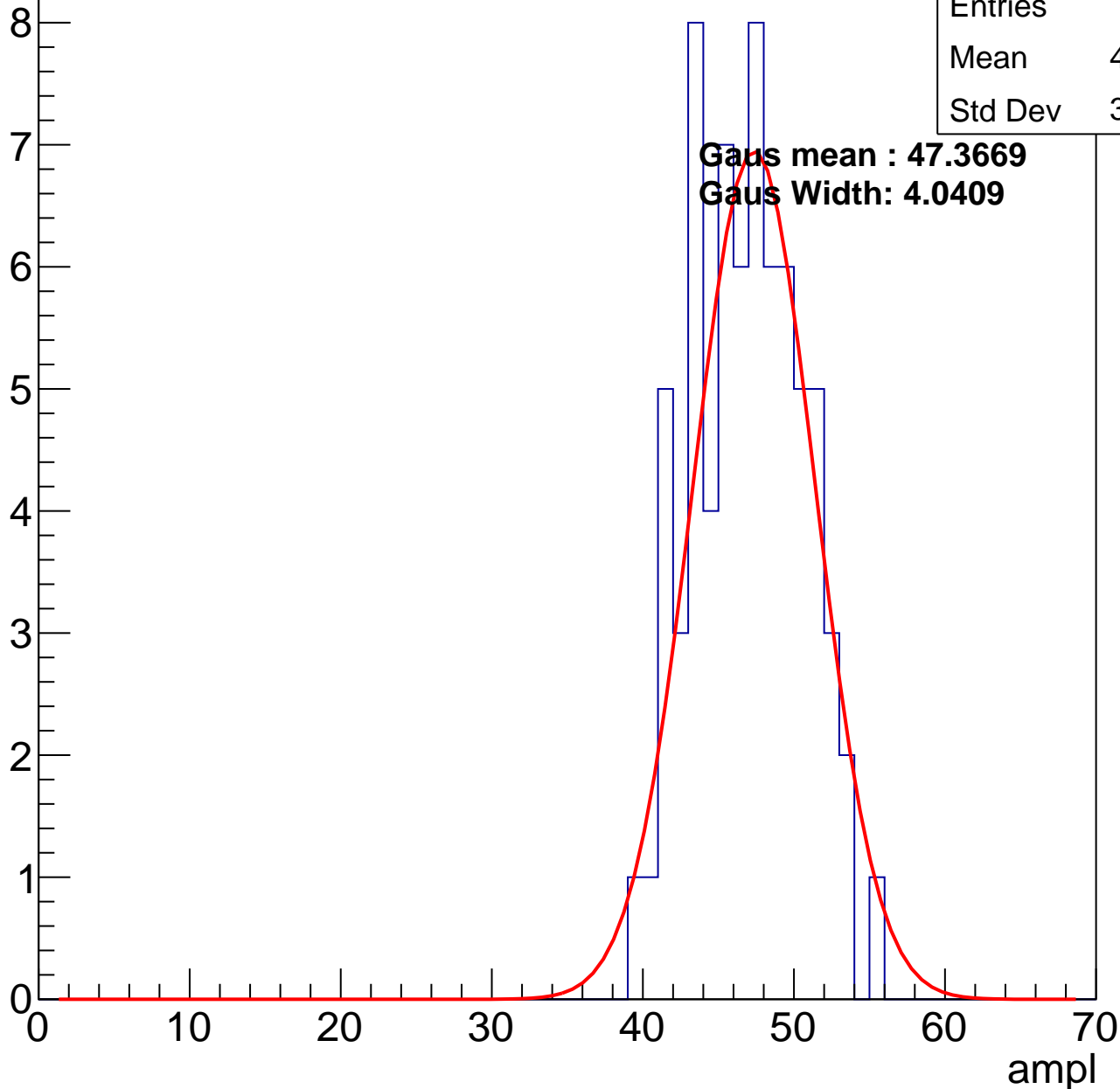
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	46.49
Std Dev	3.587

Gaus mean : 47.3669

Gaus Width: 4.0409



B0L002S, U26-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

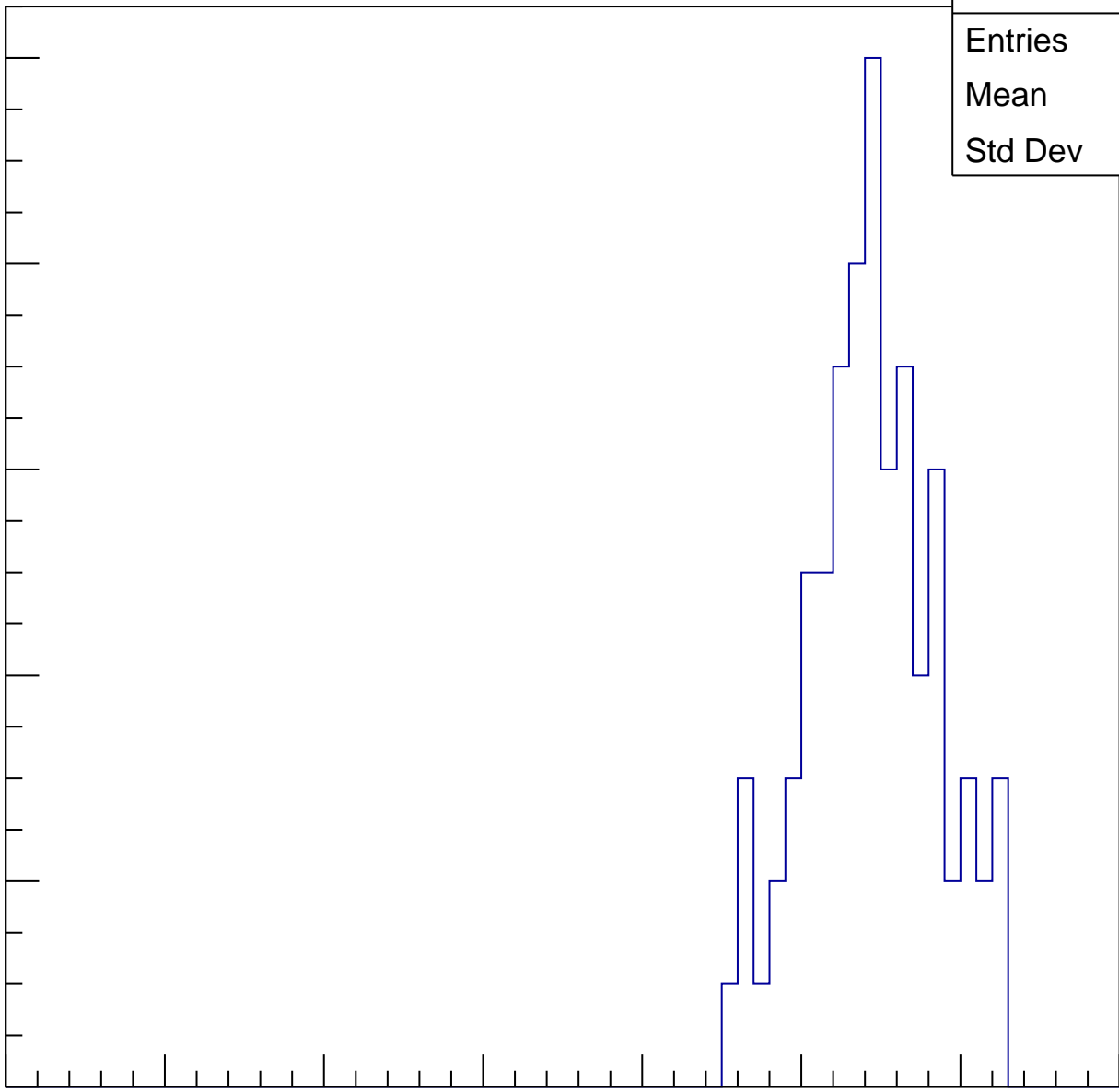
Entries	78
Mean	53.97
Std Dev	3.994

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

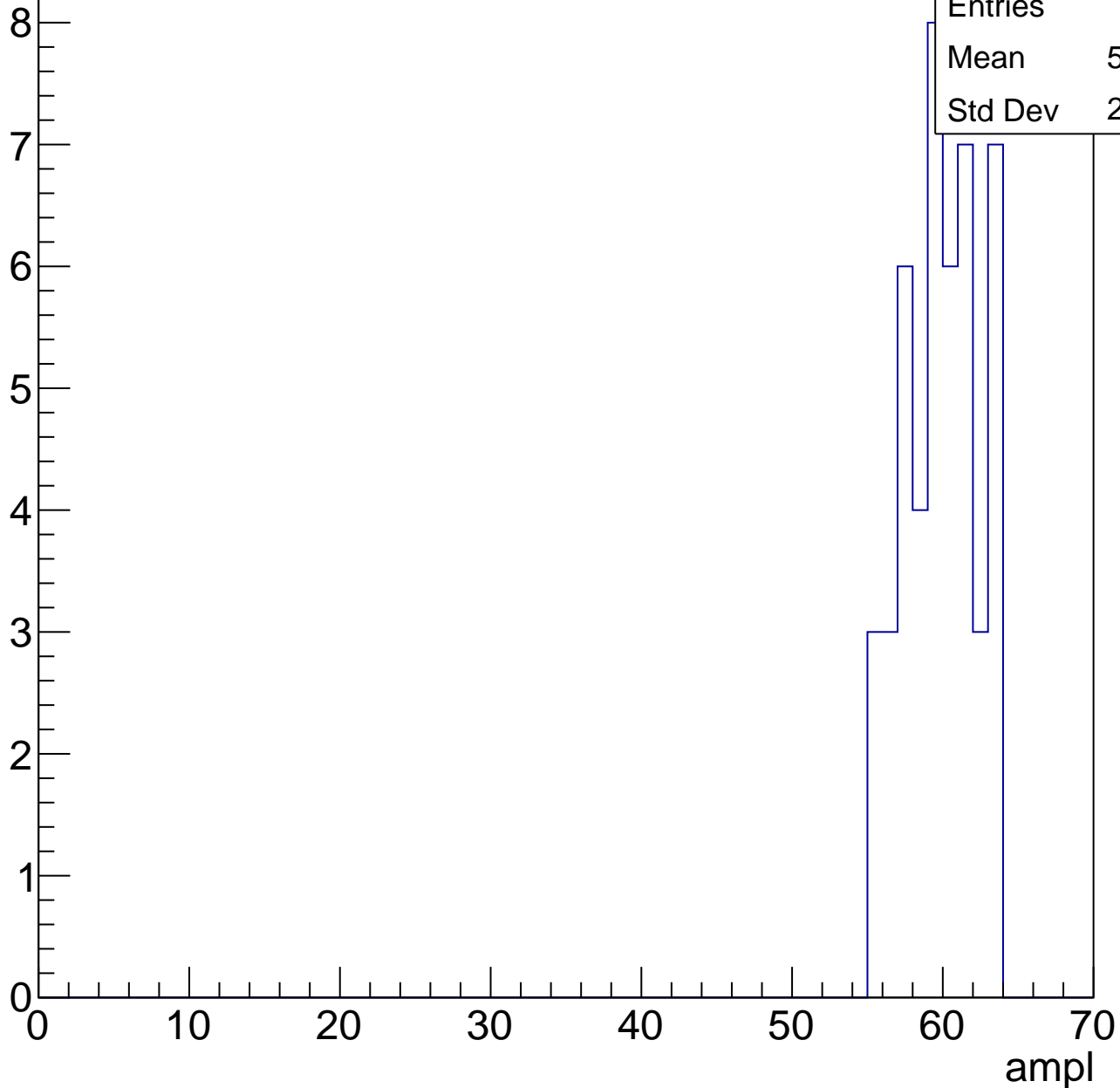
ampl



B0L002S, U26-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

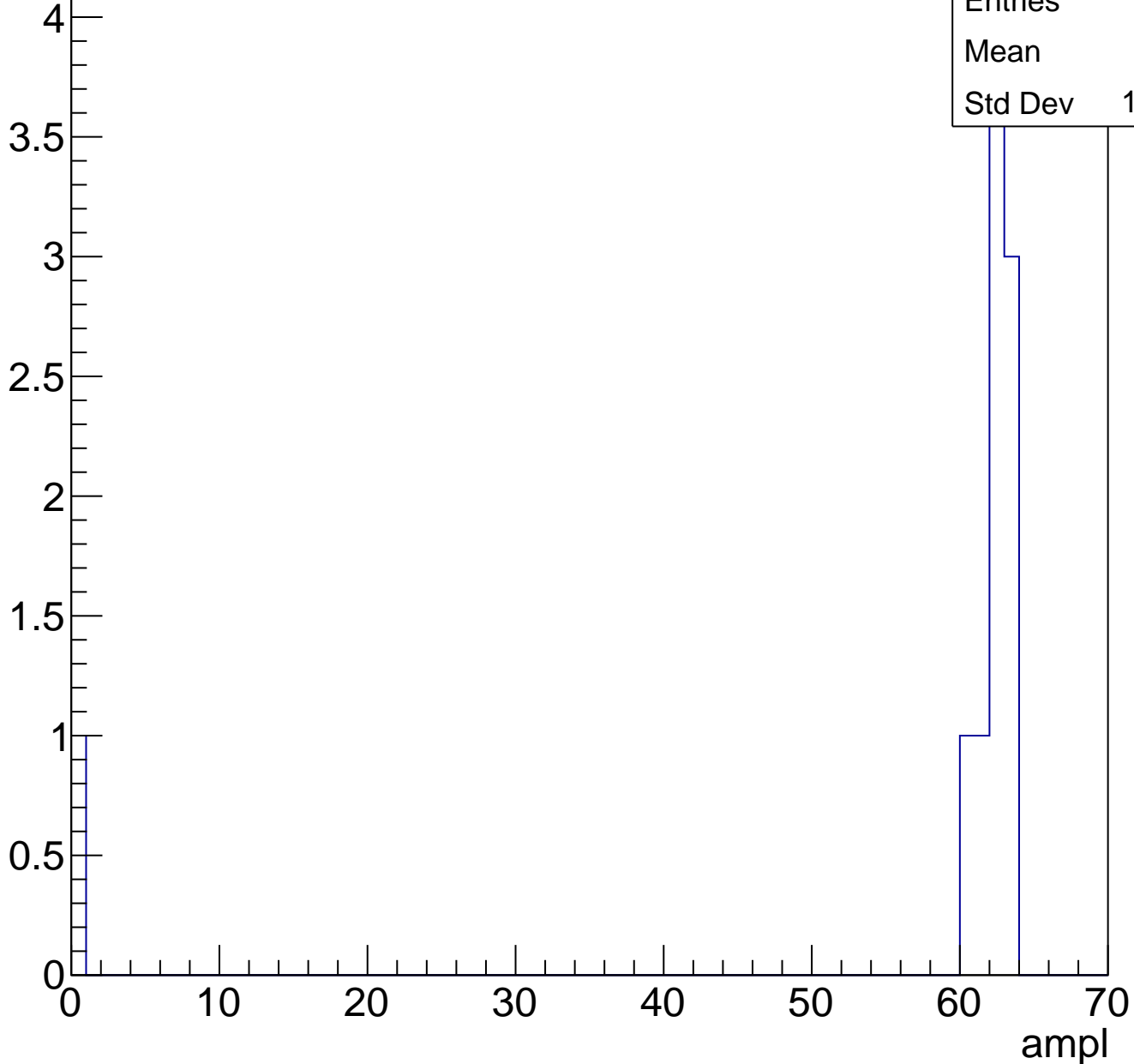
Entry



B0L002S, U26-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch126, adc0

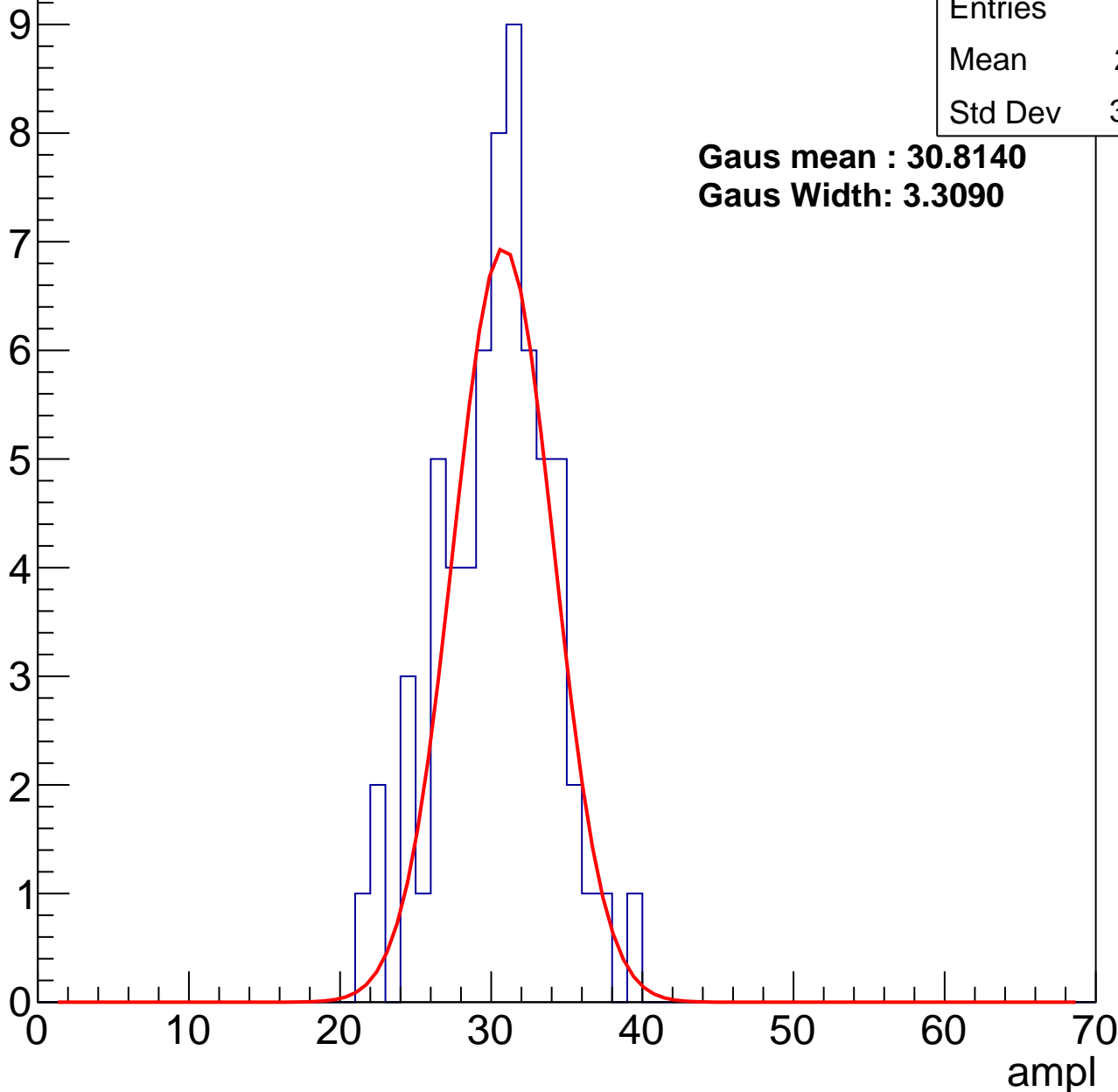
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.91
Std Dev	3.652

Gaus mean : 30.8140

Gaus Width: 3.3090



B0L002S, U26-ch126, adc1

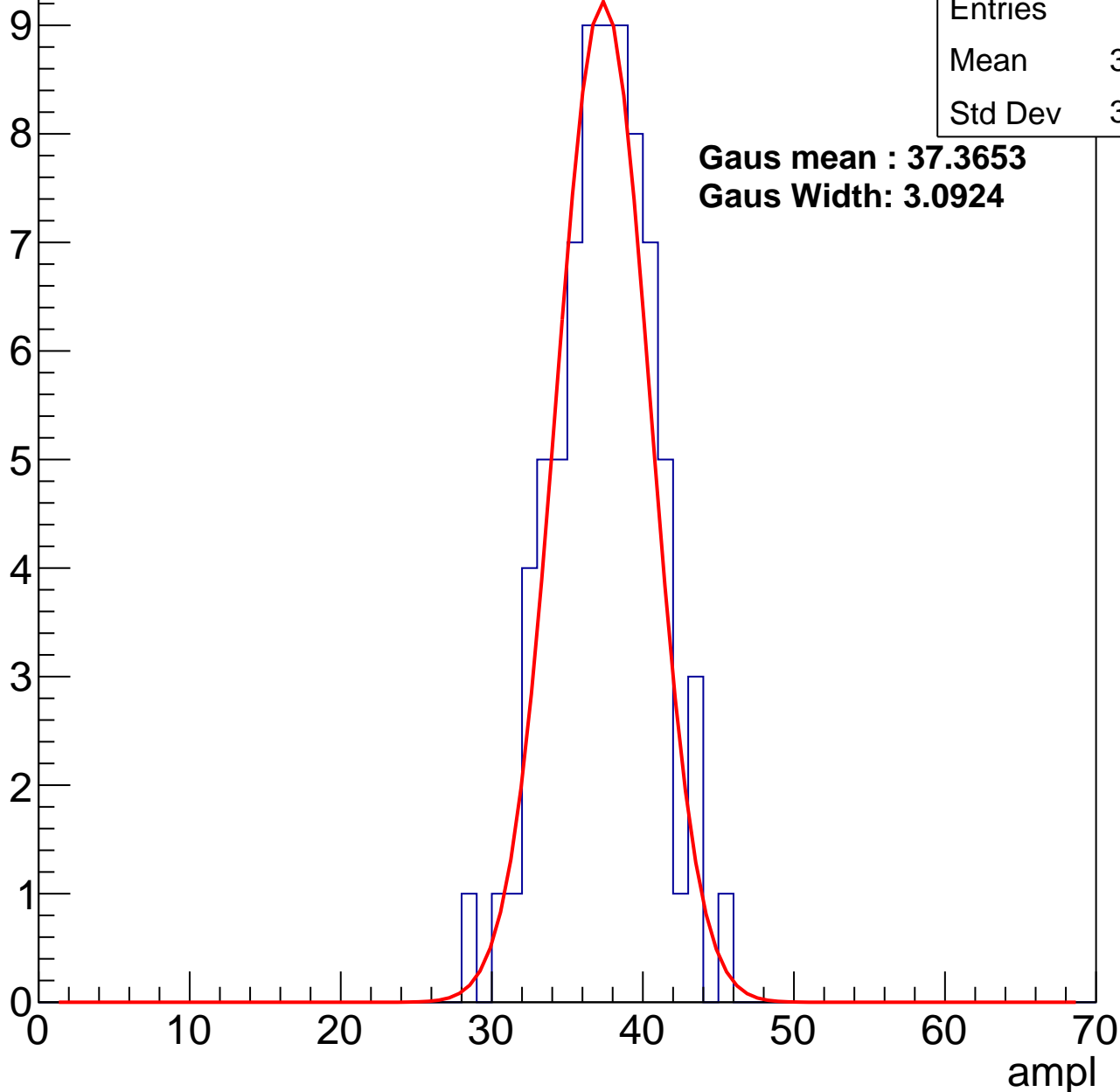
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.96
Std Dev	3.262

Gaus mean : 37.3653

Gaus Width: 3.0924



B0L002S, U26-ch126, adc2

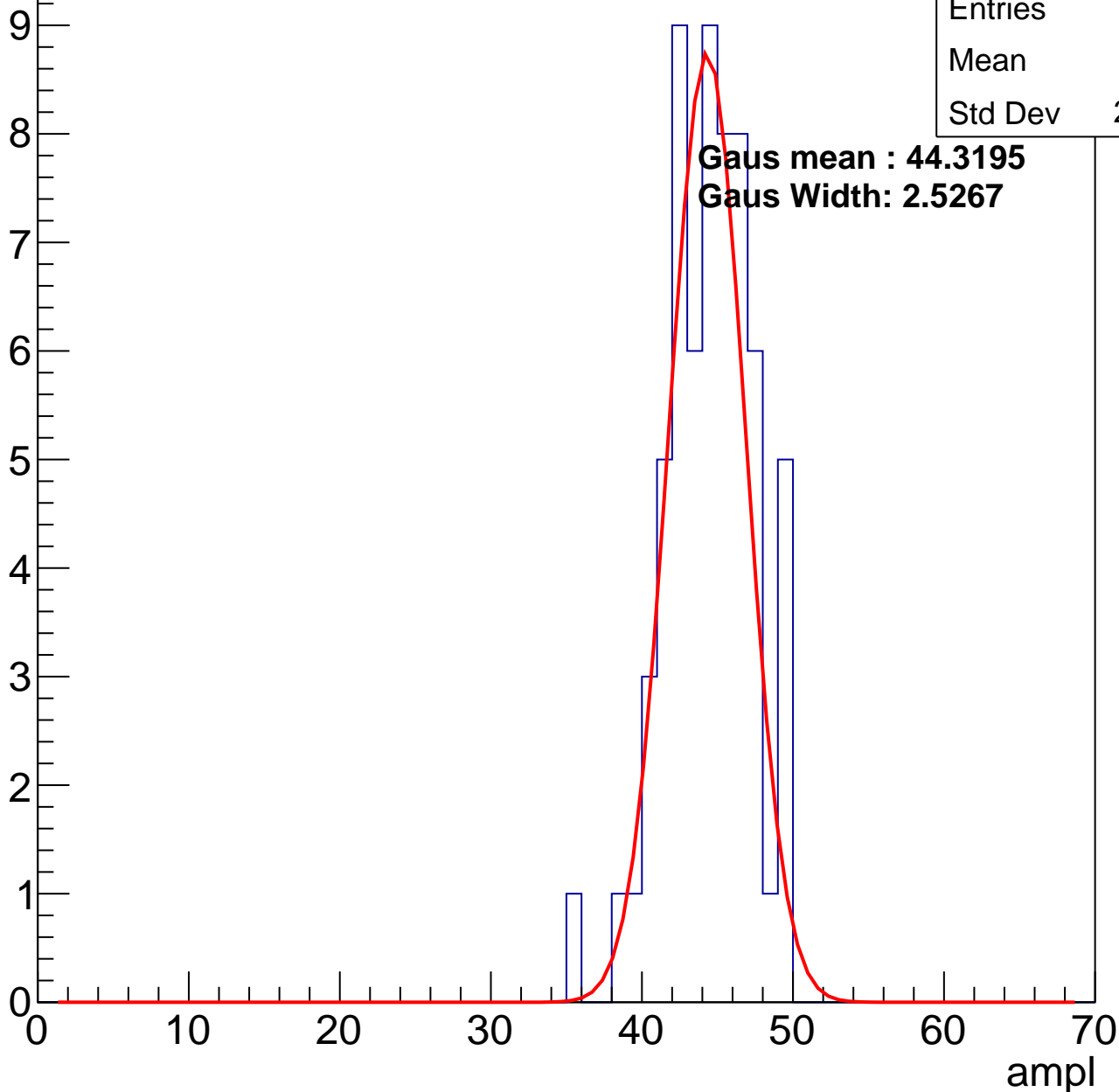
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44
Std Dev	2.851

Gaus mean : 44.3195

Gaus Width: 2.5267

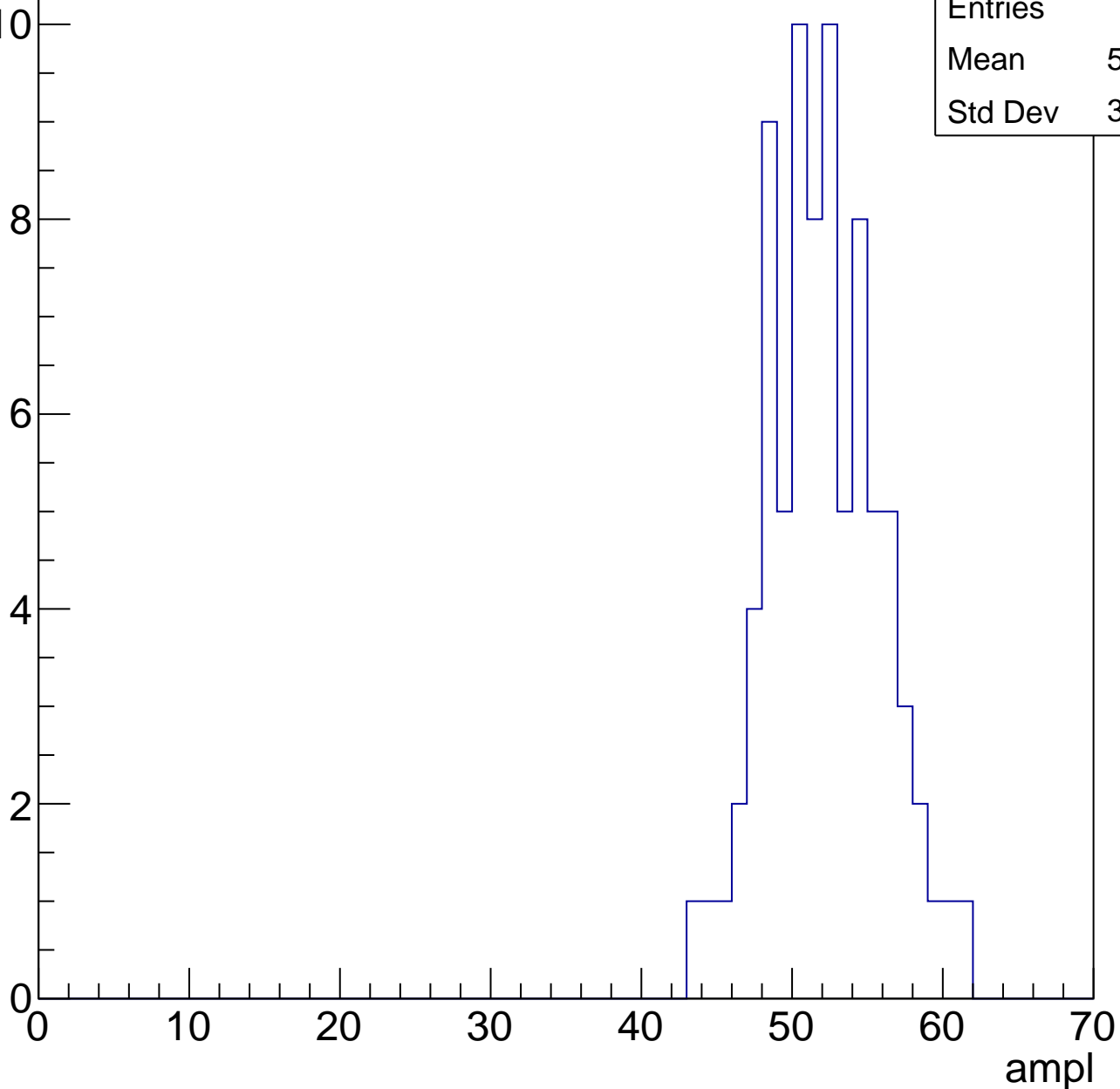


B0L002S, U26-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	51.66
Std Dev	3.657

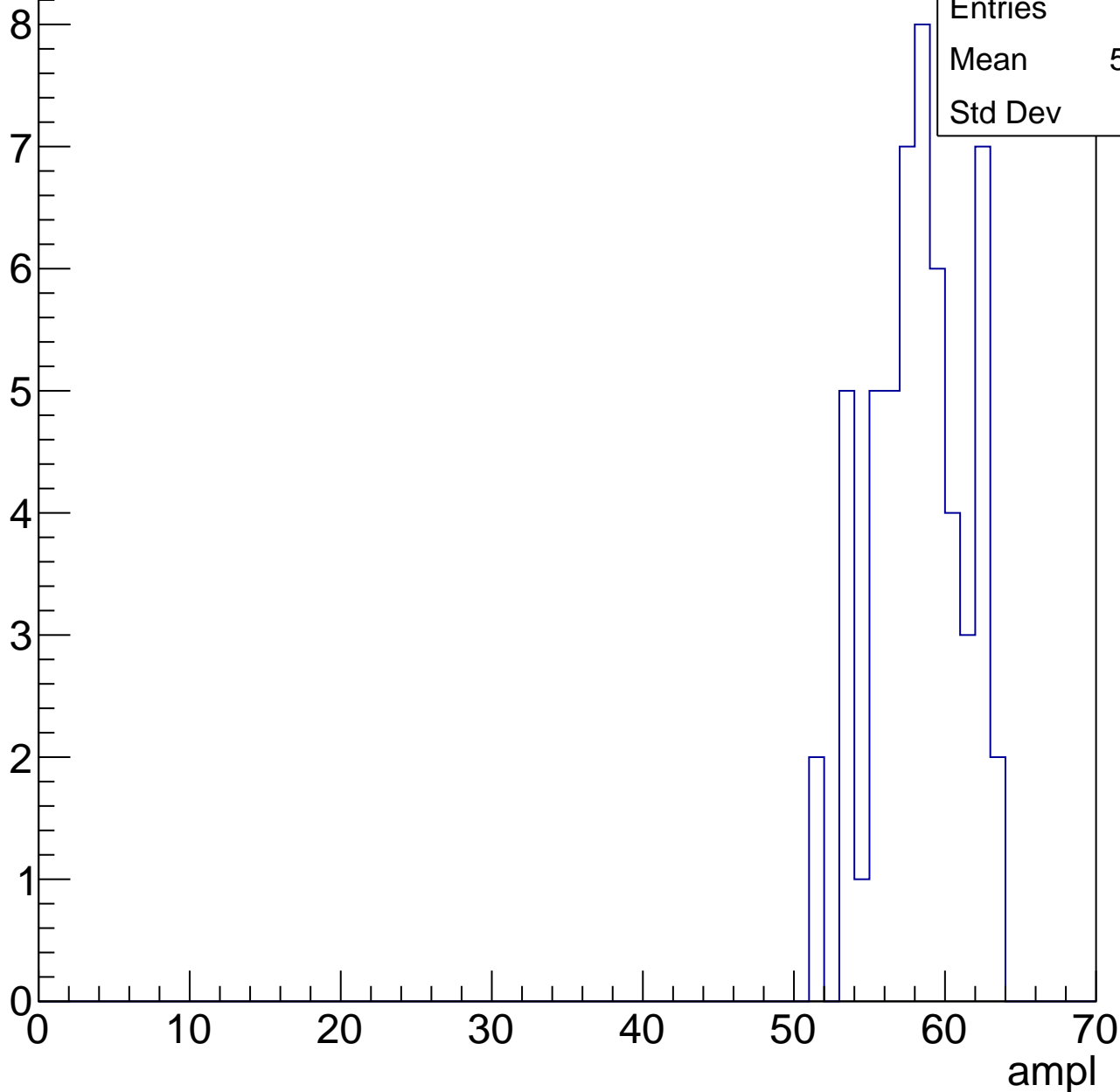


B0L002S, U26-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.75
Std Dev	3.07

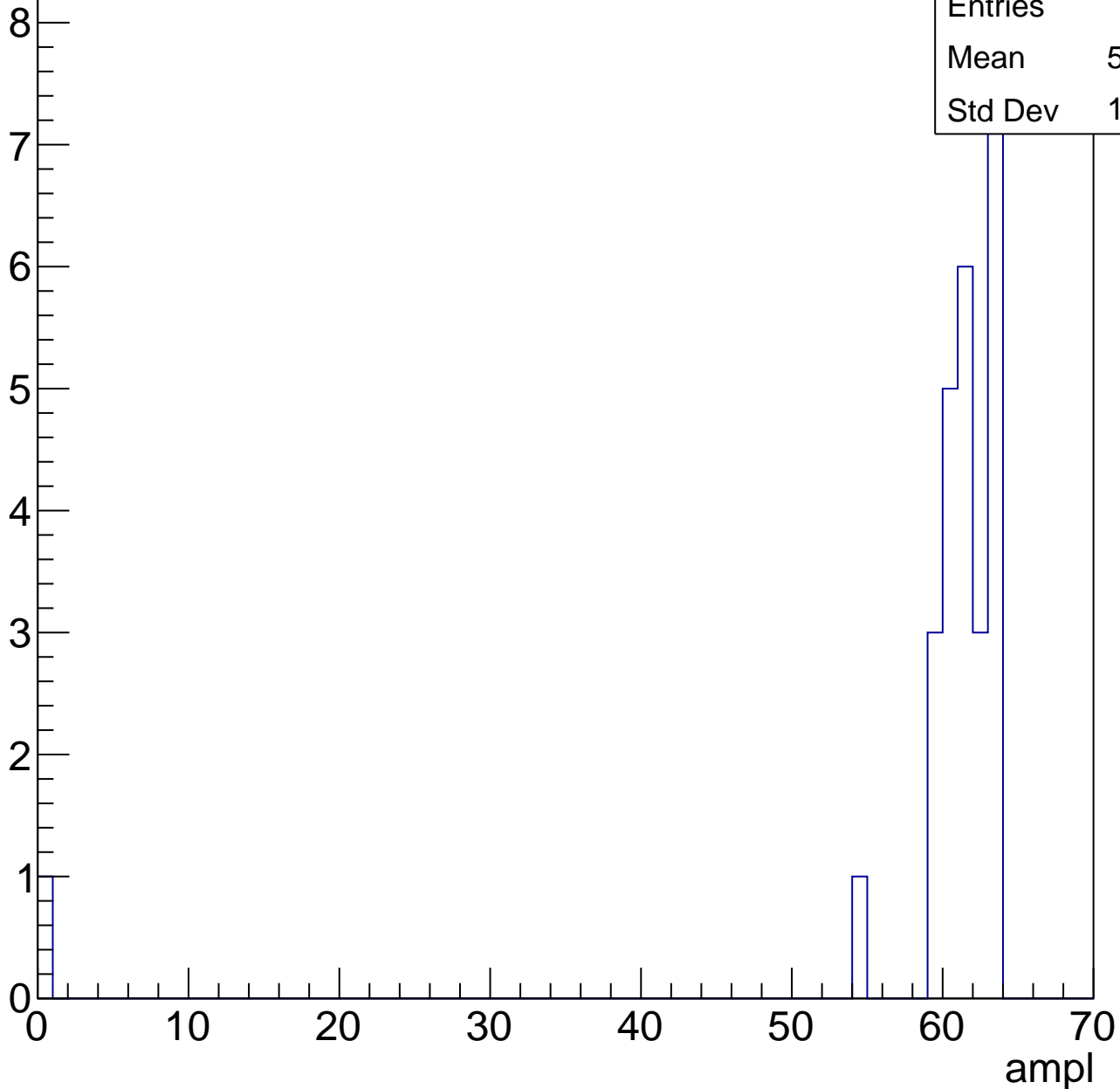


B0L002S, U26-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.78
Std Dev	11.69



B0L002S, U26-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U26-ch127, adc0

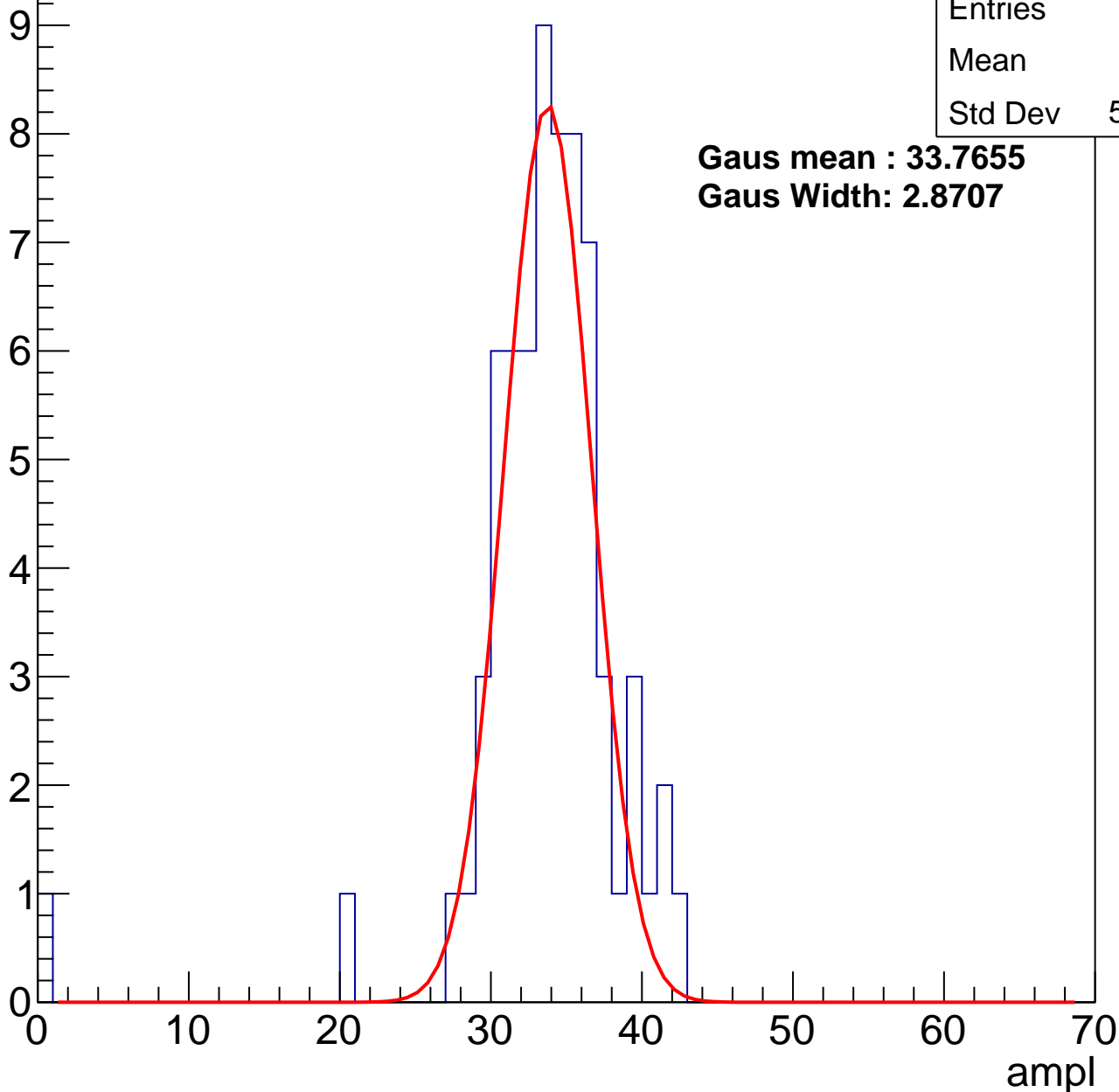
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	33.1
Std Dev	5.413

Gaus mean : 33.7655

Gaus Width: 2.8707



B0L002S, U26-ch127, adc1

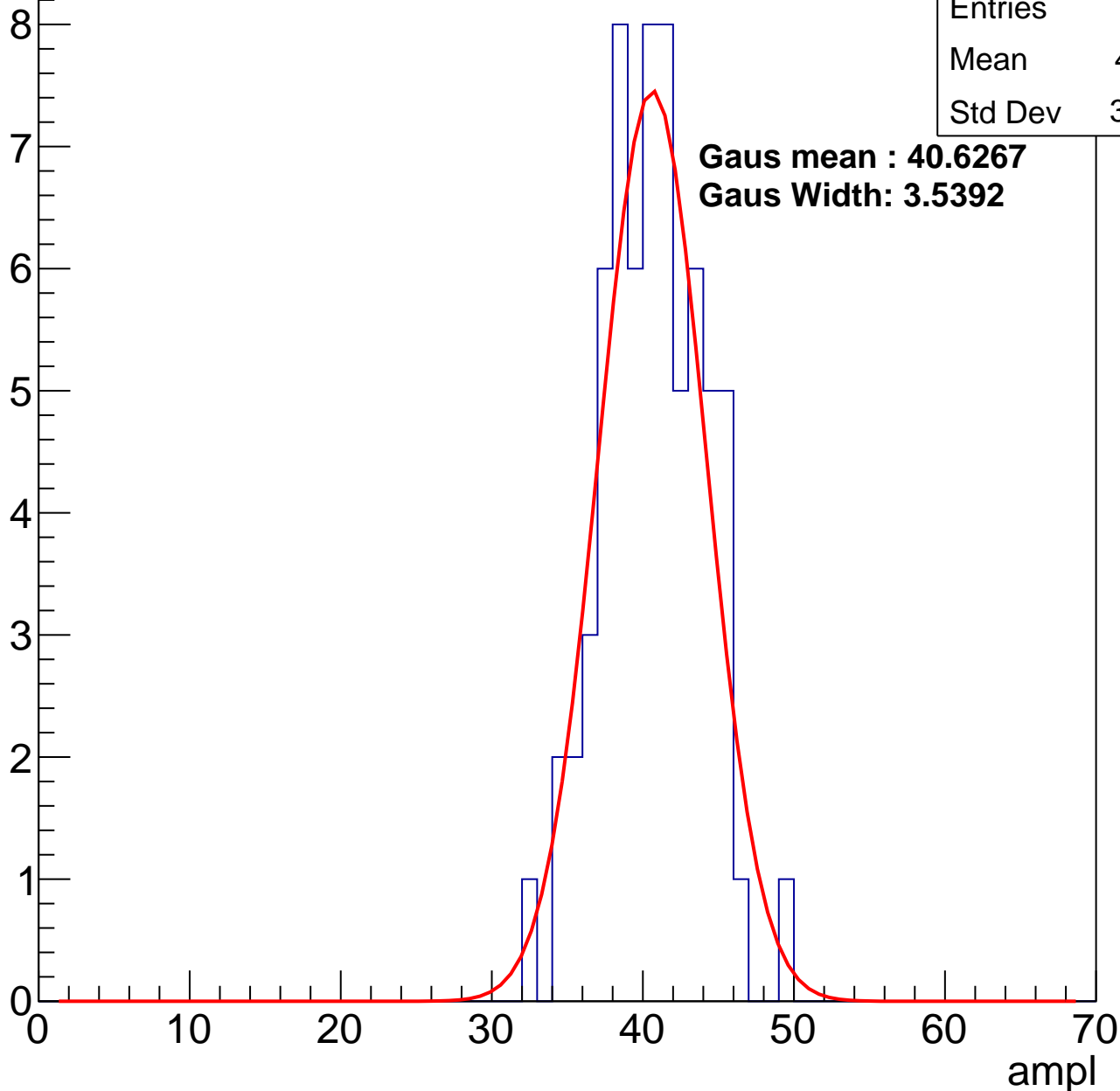
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	40.21
Std Dev	3.294

Gaus mean : 40.6267

Gaus Width: 3.5392



B0L002S, U26-ch127, adc2

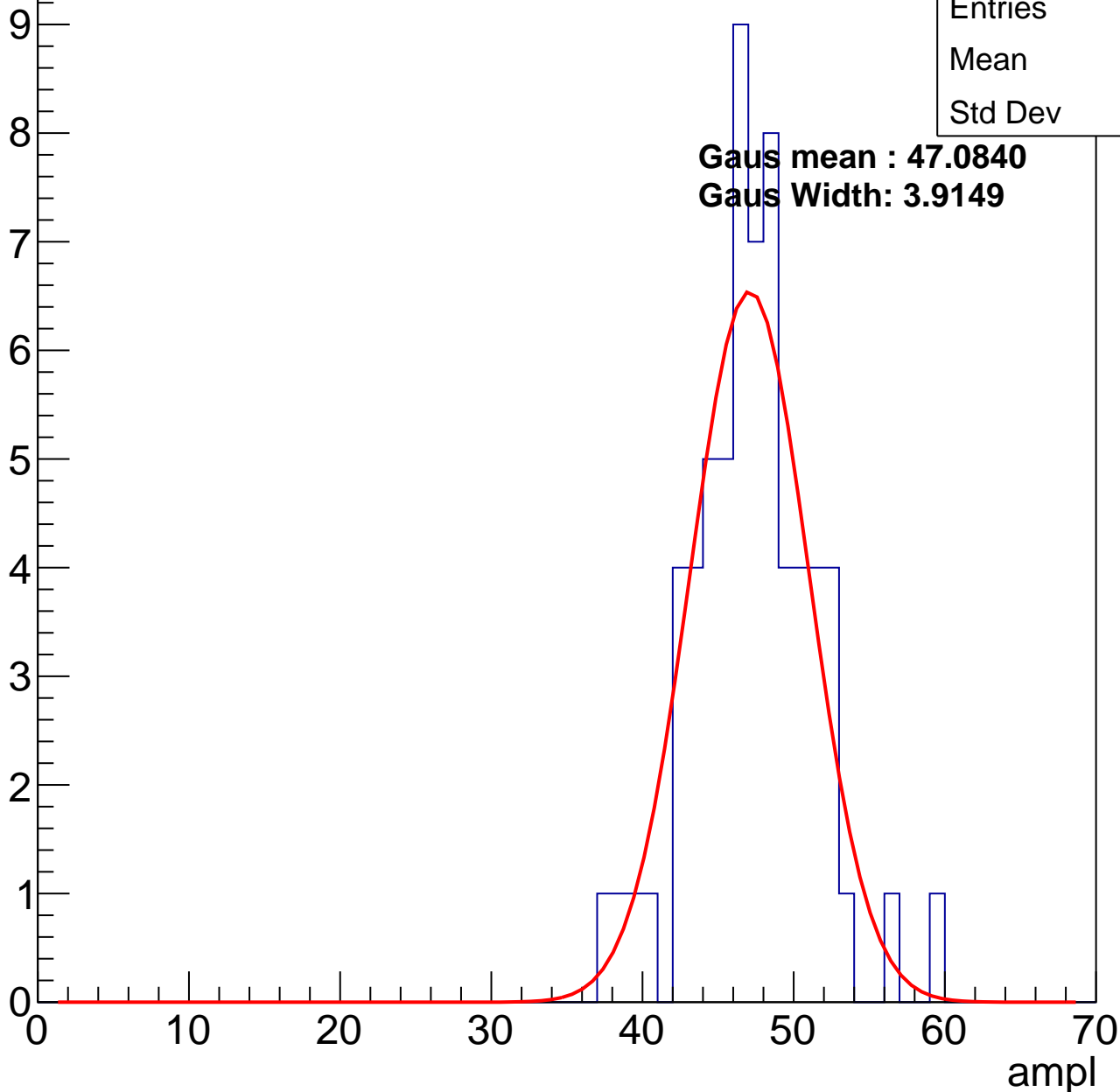
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	46.8
Std Dev	3.95

Gaus mean : 47.0840

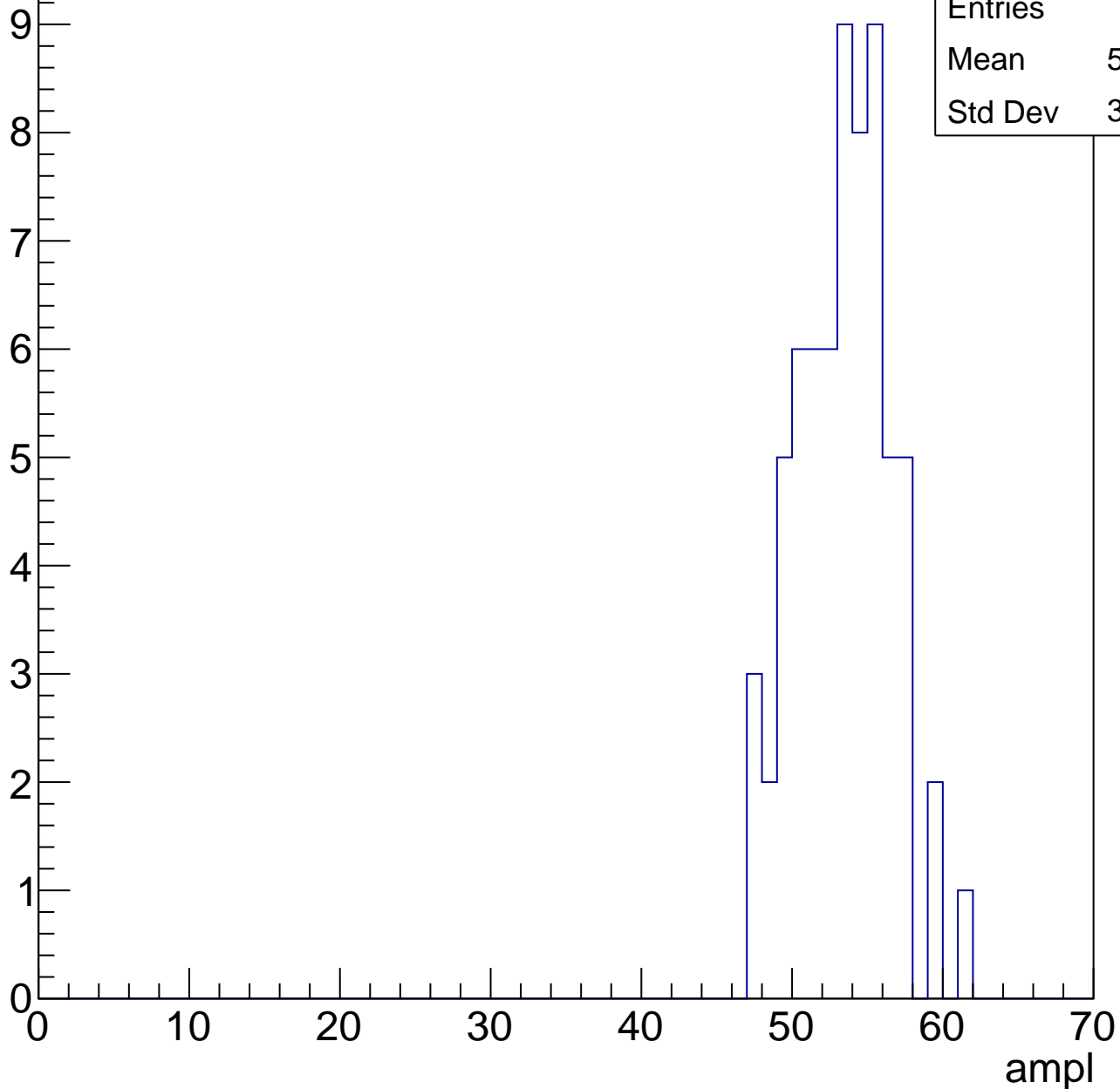
Gaus Width: 3.9149



B0L002S, U26-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	67
Mean	52.96
Std Dev	3.059

B0L002S, U26-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

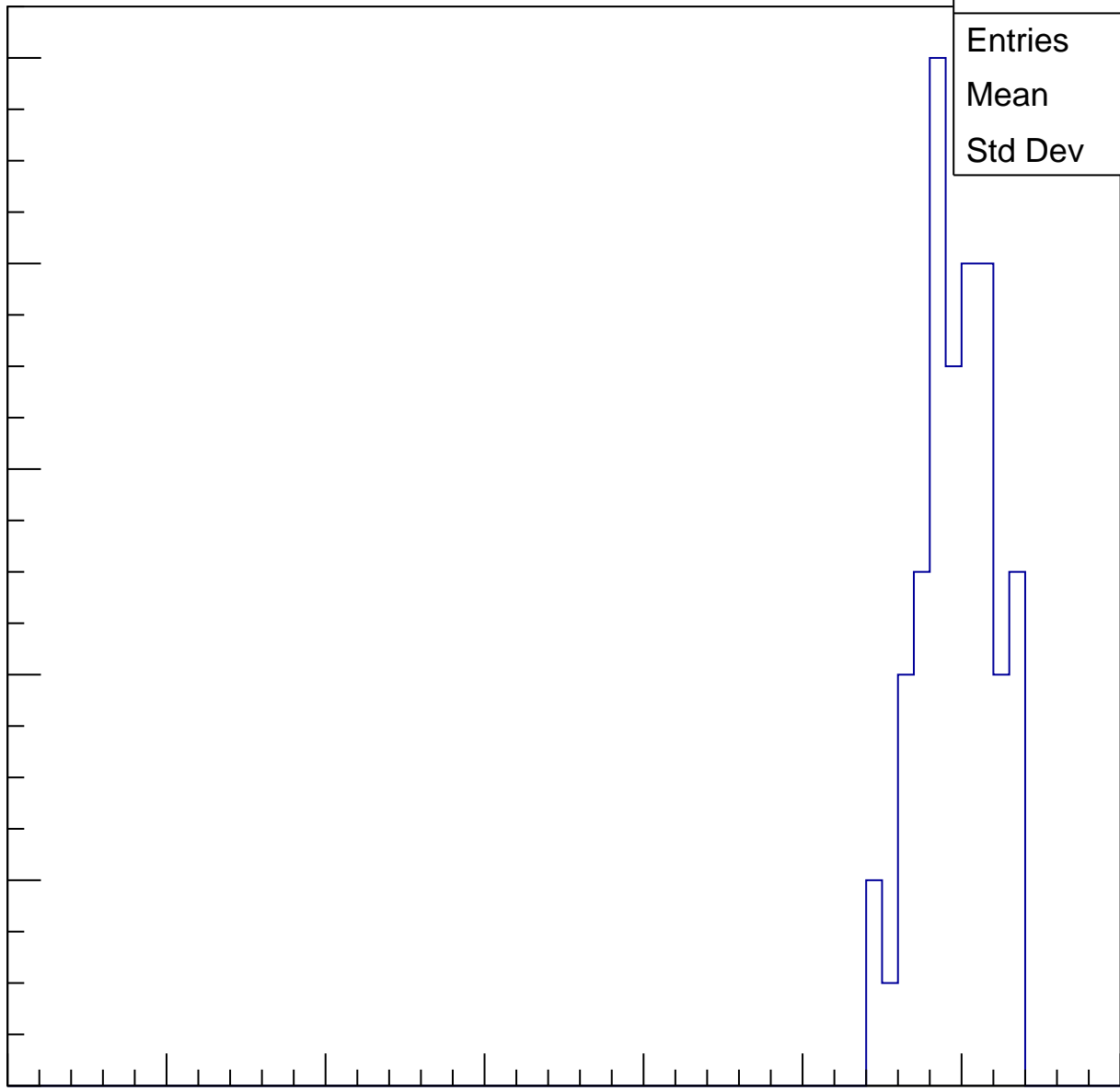
Entries	54
Mean	59.19
Std Dev	2.302

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

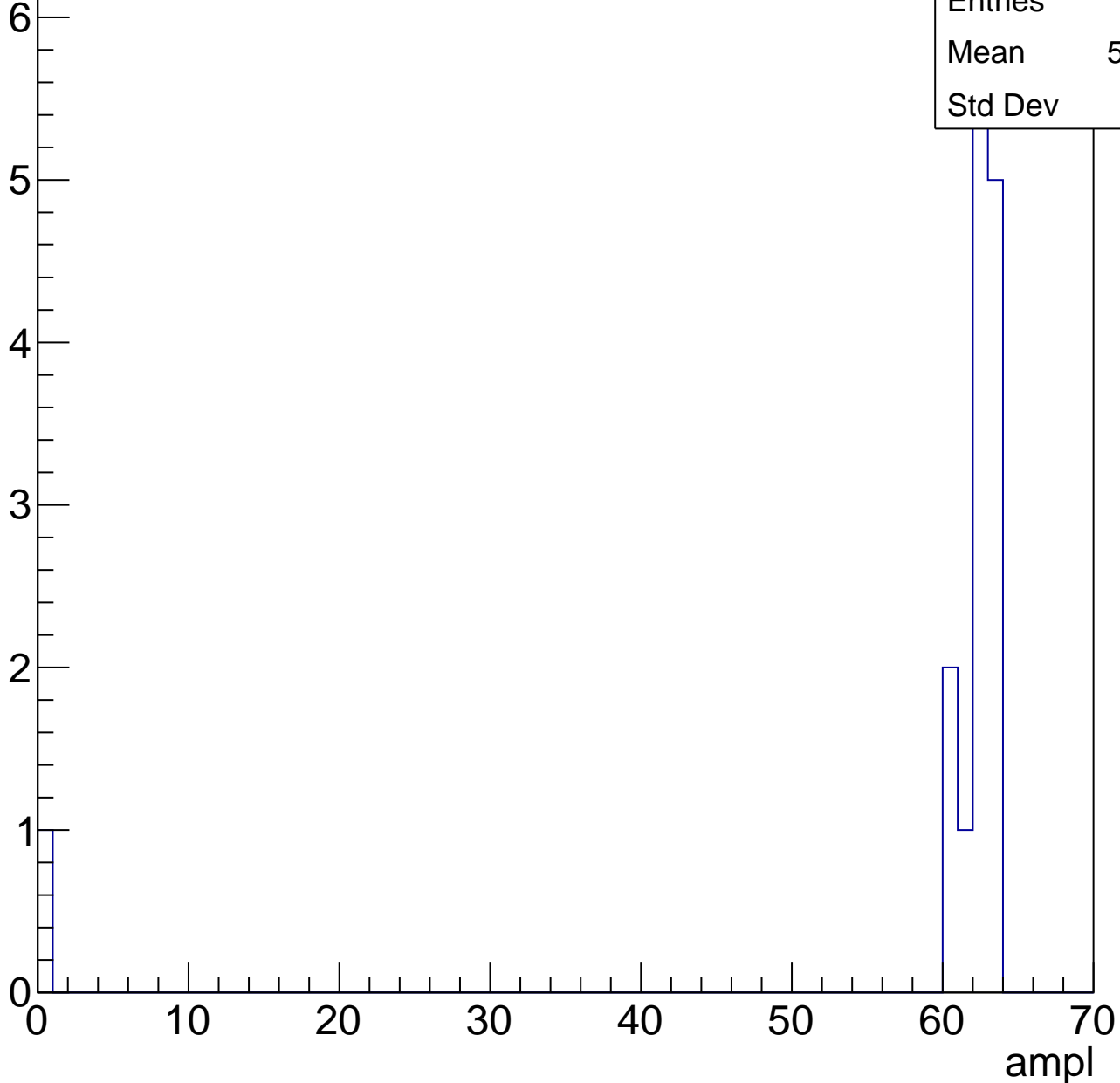


B0L002S, U26-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	15
Mean	57.87
Std Dev	15.5



B0L002S, U26-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

