

B1L000S, U17-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.8203

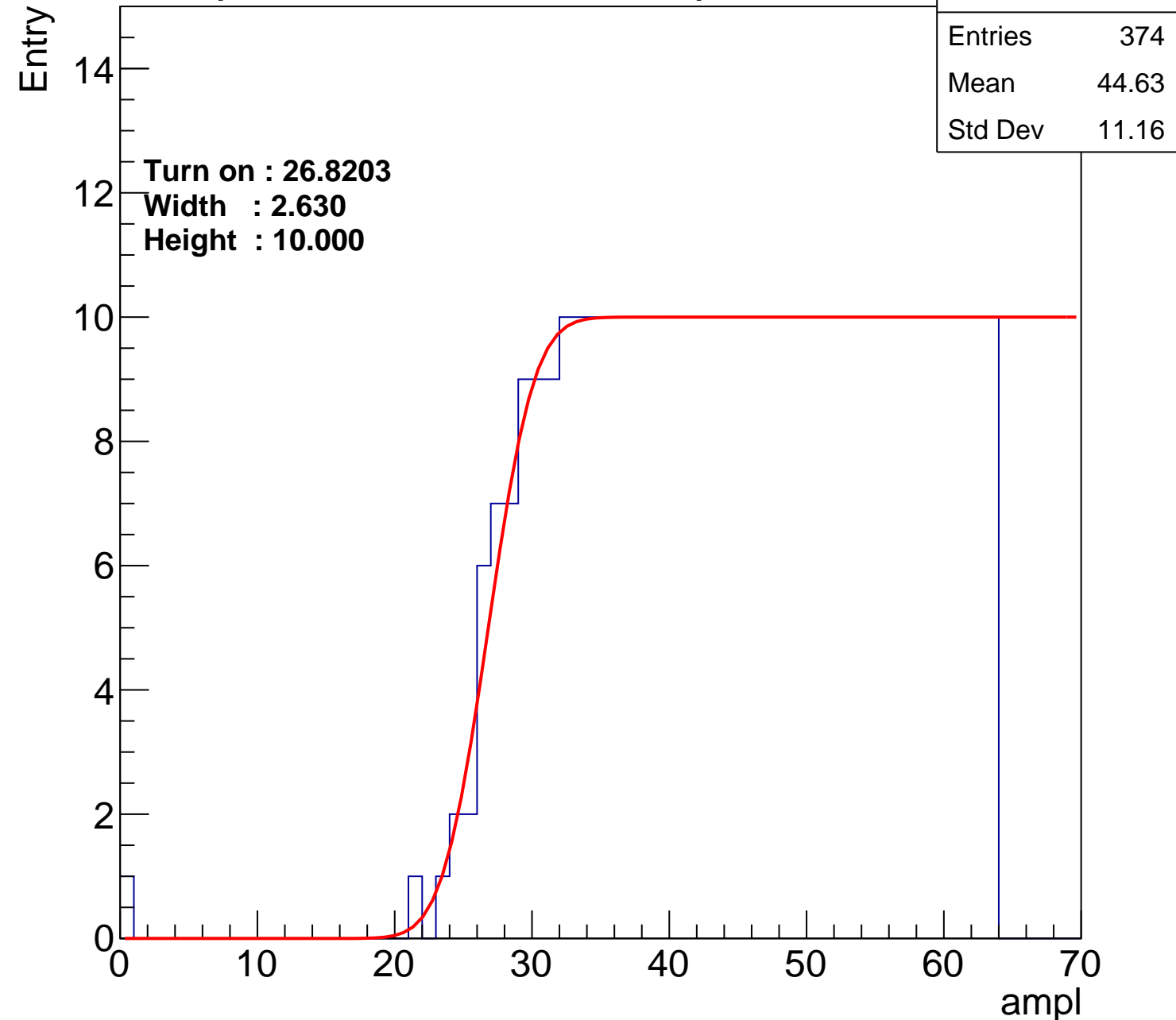
Width : 2.630

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.21
Std Dev	11.8

Turn on : 26.6383

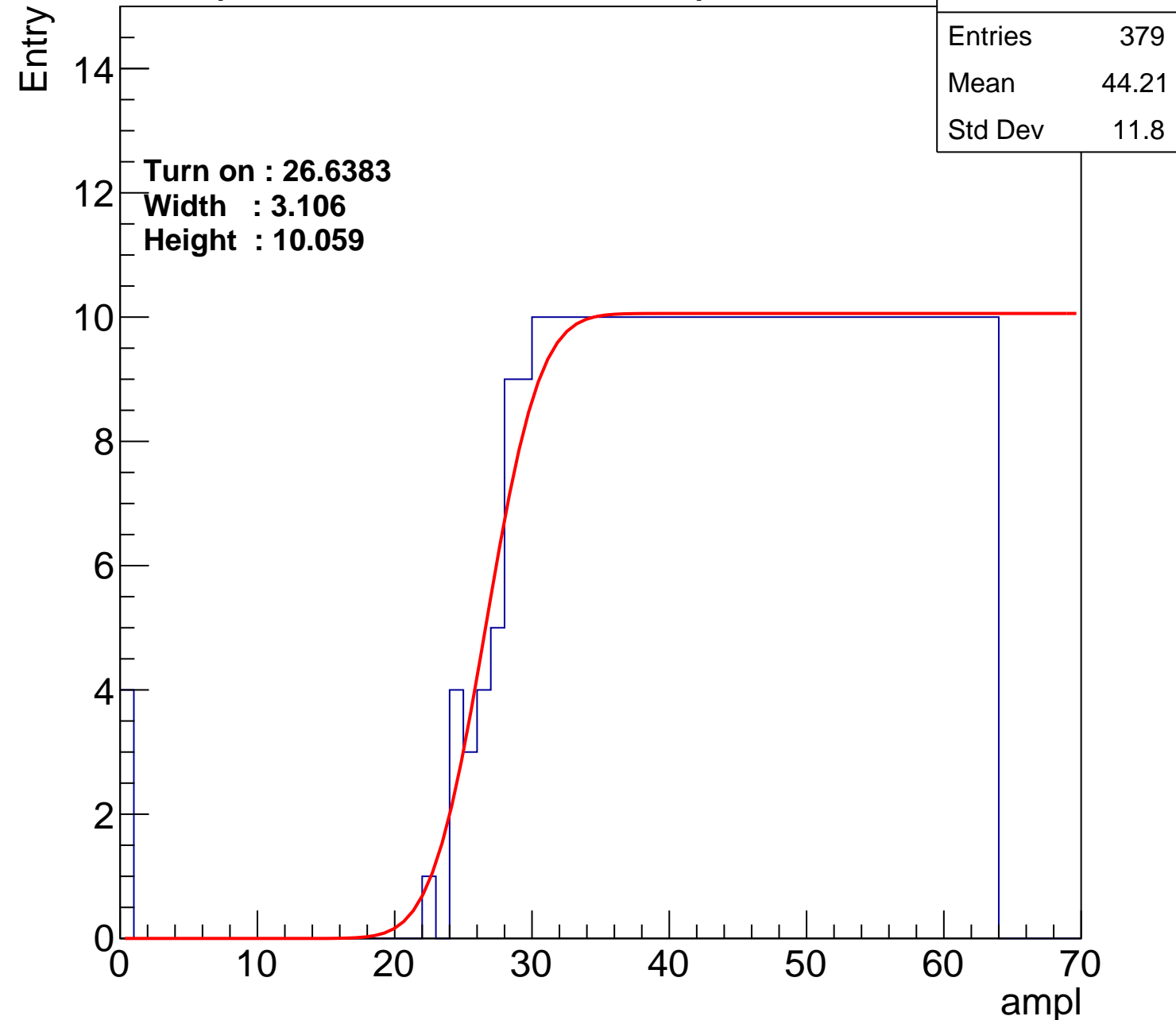
Width : 3.106

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch2

calib_packv5_042523_0143.root, FC#3, port B3

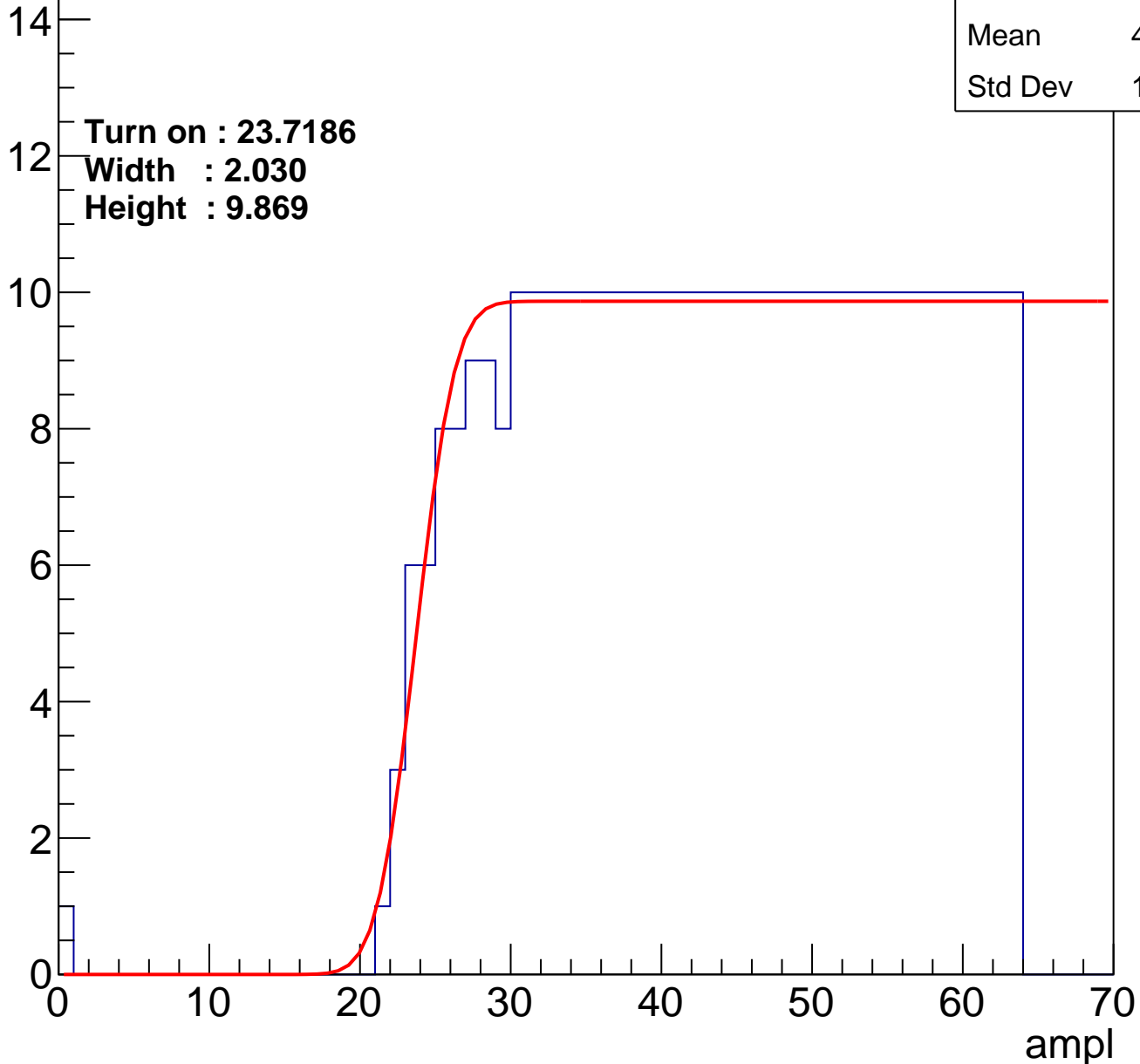
Entries	399
Mean	43.39
Std Dev	11.83

Turn on : 23.7186

Width : 2.030

Height : 9.869

Entry



B1L000S, U17-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.69
Std Dev	12.03

Turn on : 25.4296

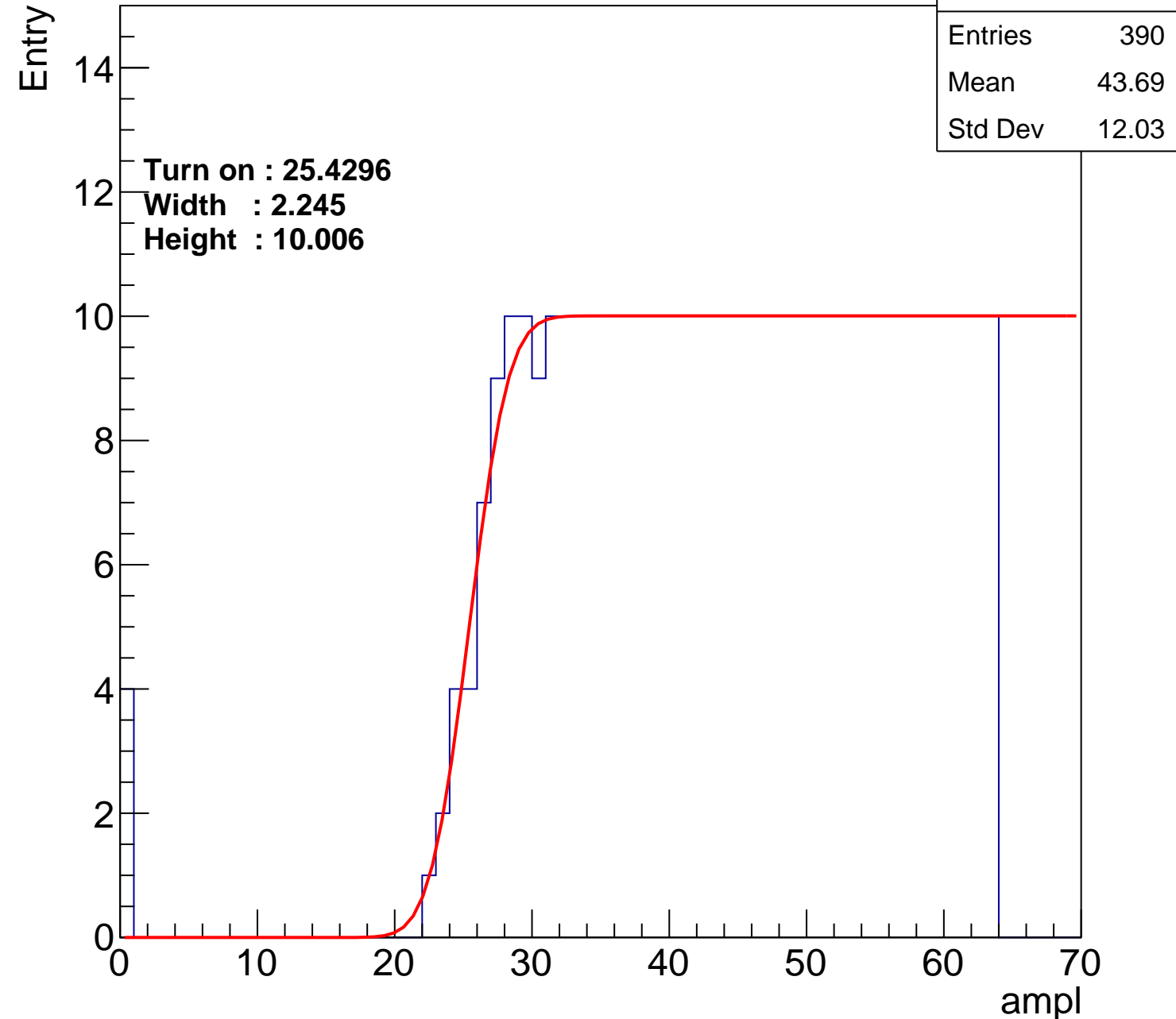
Width : 2.245

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.03
Std Dev	11.75

Turn on : 26.2660

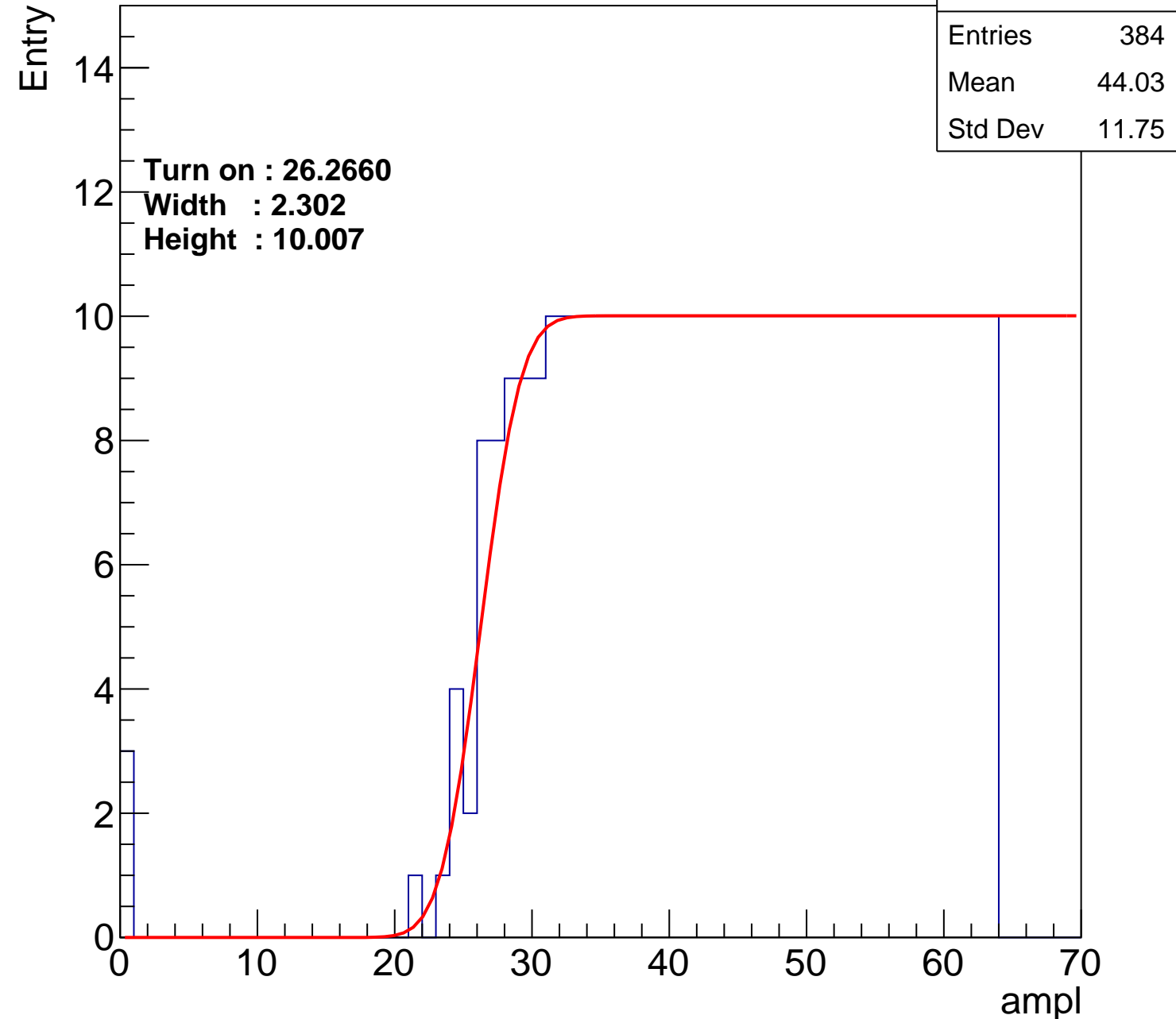
Width : 2.302

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 27.1055

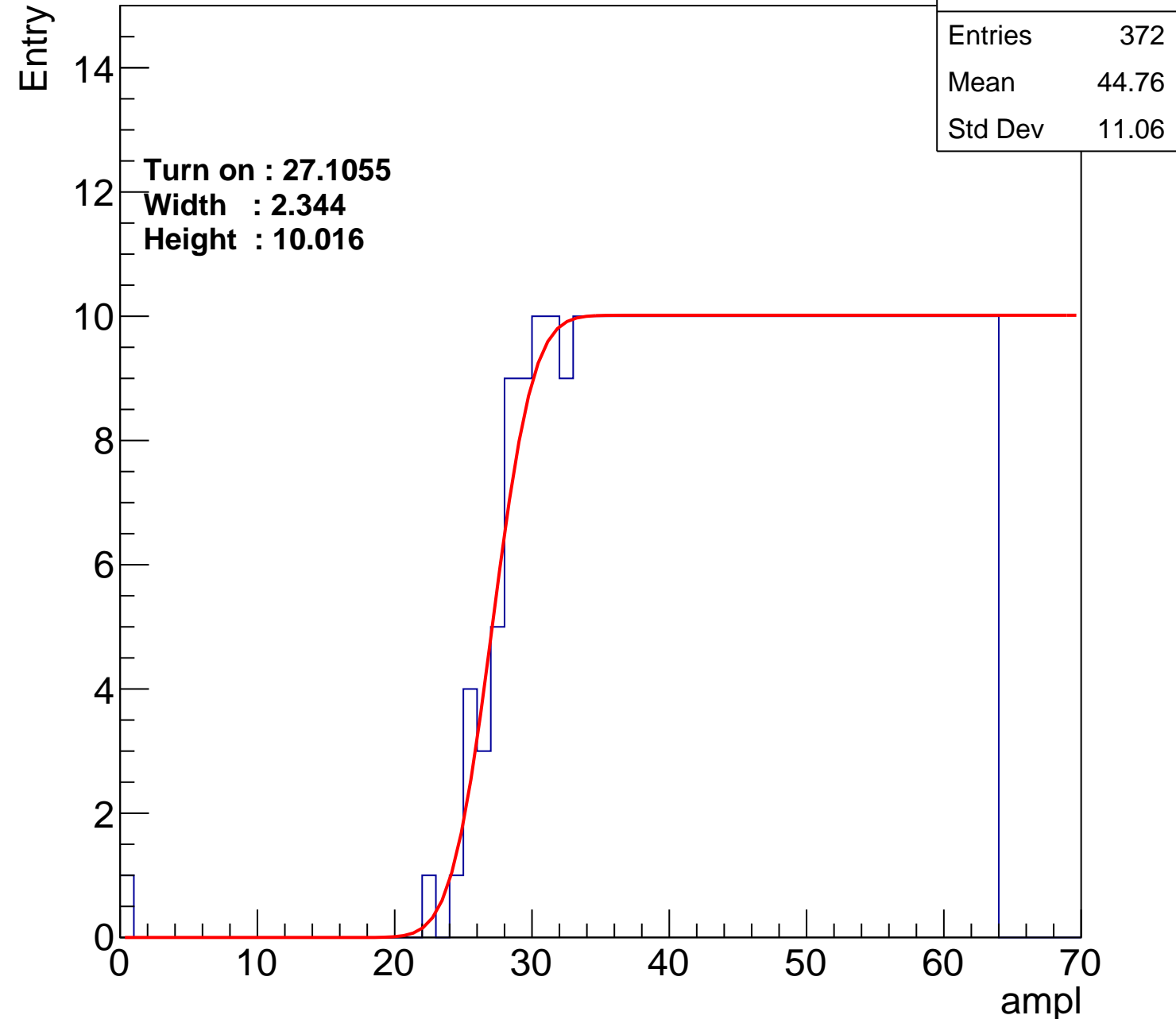
Width : 2.344

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.28
Std Dev	10.94

Turn on : 27.9105

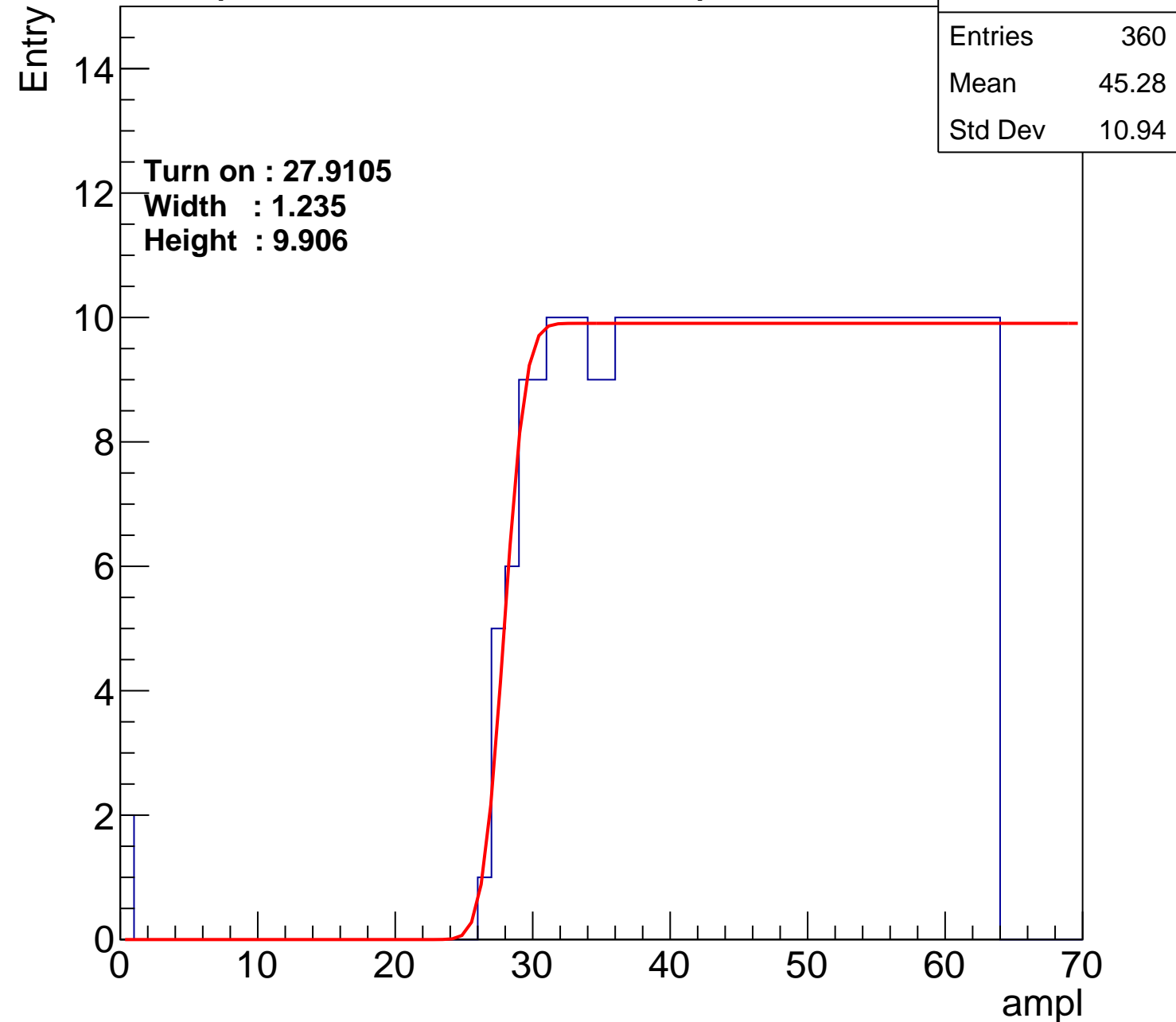
Width : 1.235

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.68
Std Dev	11.13

Turn on : 27.1904

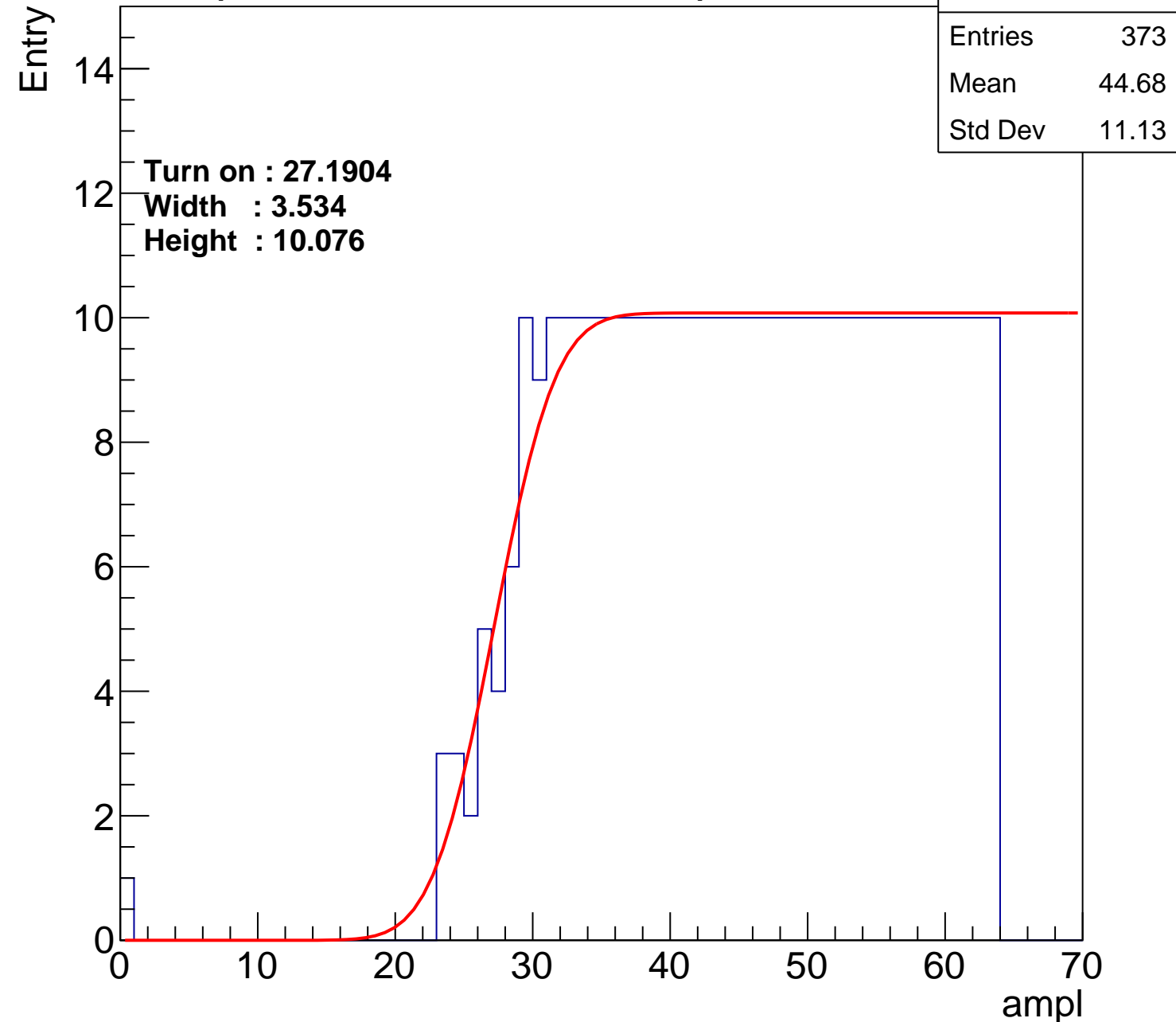
Width : 3.534

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.4921

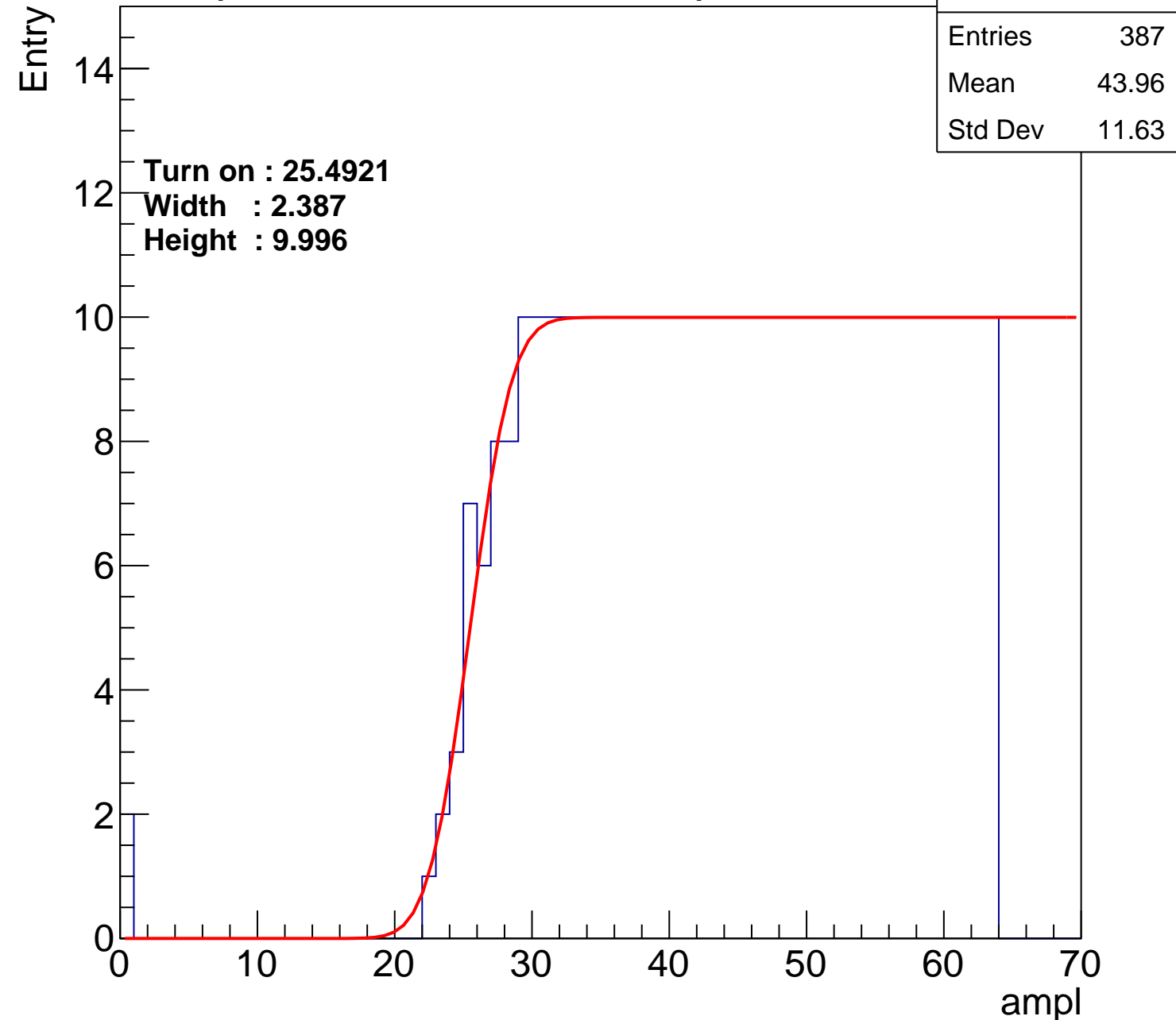
Width : 2.387

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.95
Std Dev	11.7

Turn on : 26.3896

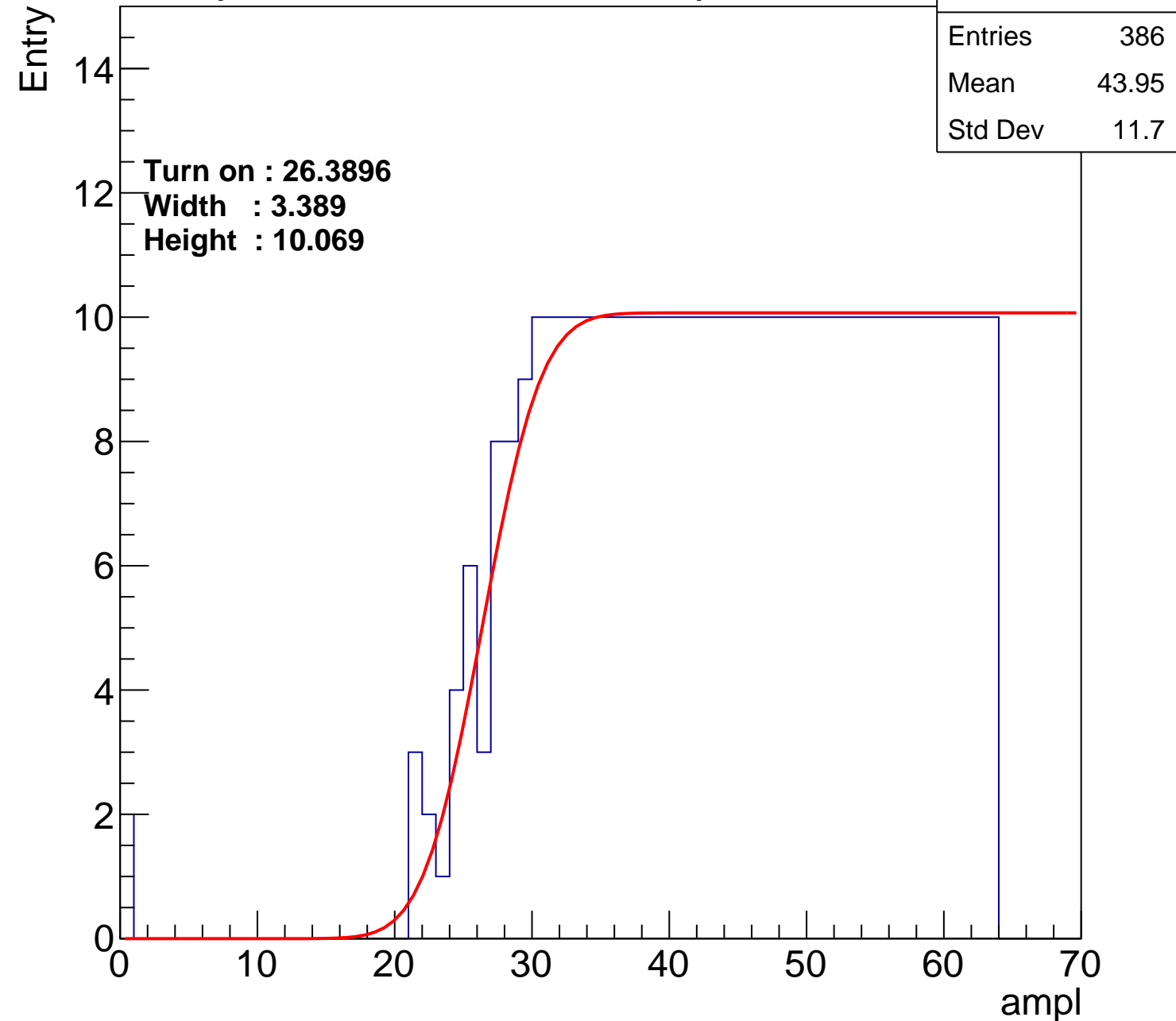
Width : 3.389

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch10

calib_packv5_042523_0143.root, FC#3, port B3

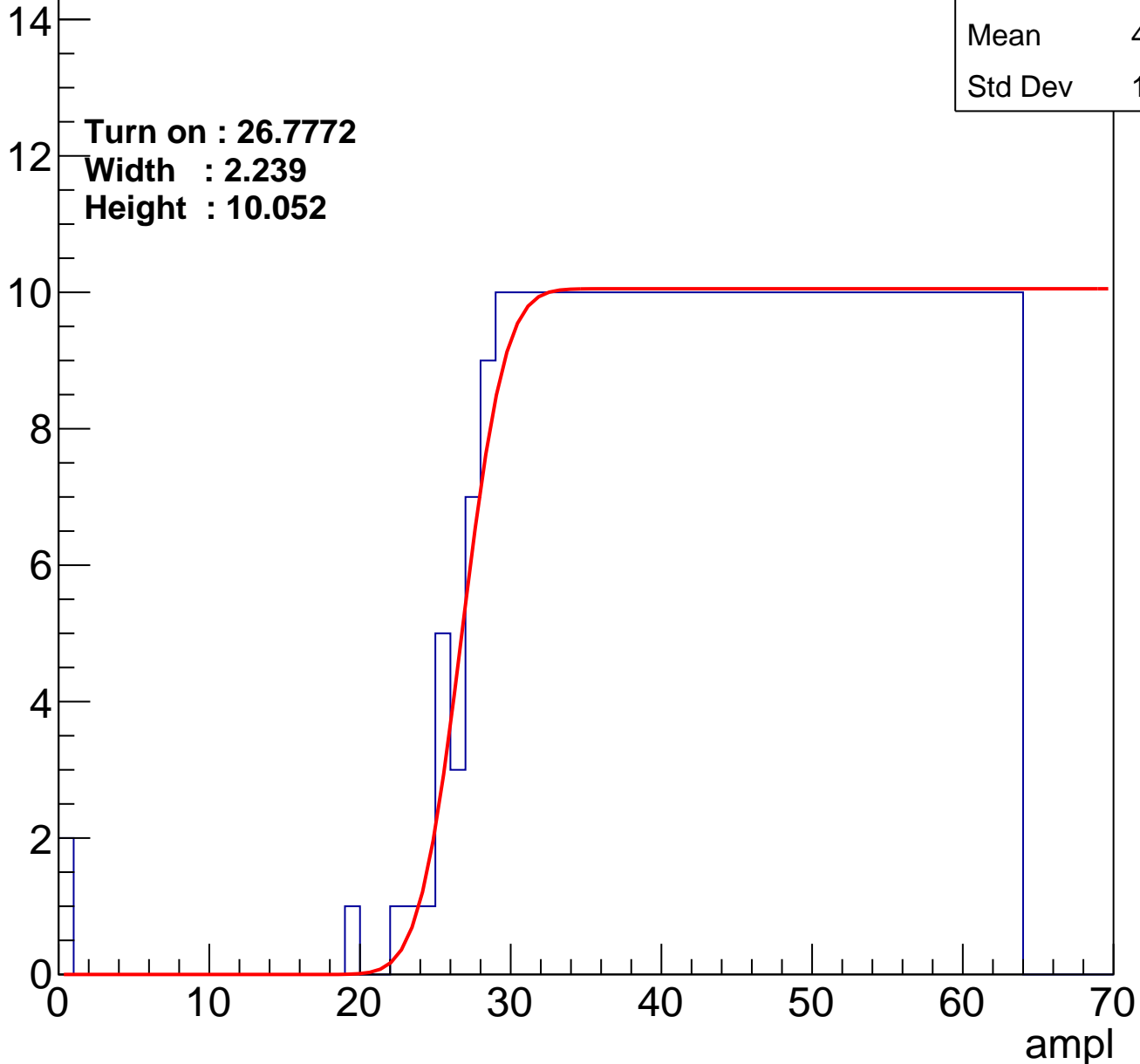
Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.7772

Width : 2.239

Height : 10.052

Entry



B1L000S, U17-ch11

calib_packv5_042523_0143.root, FC#3, port B3

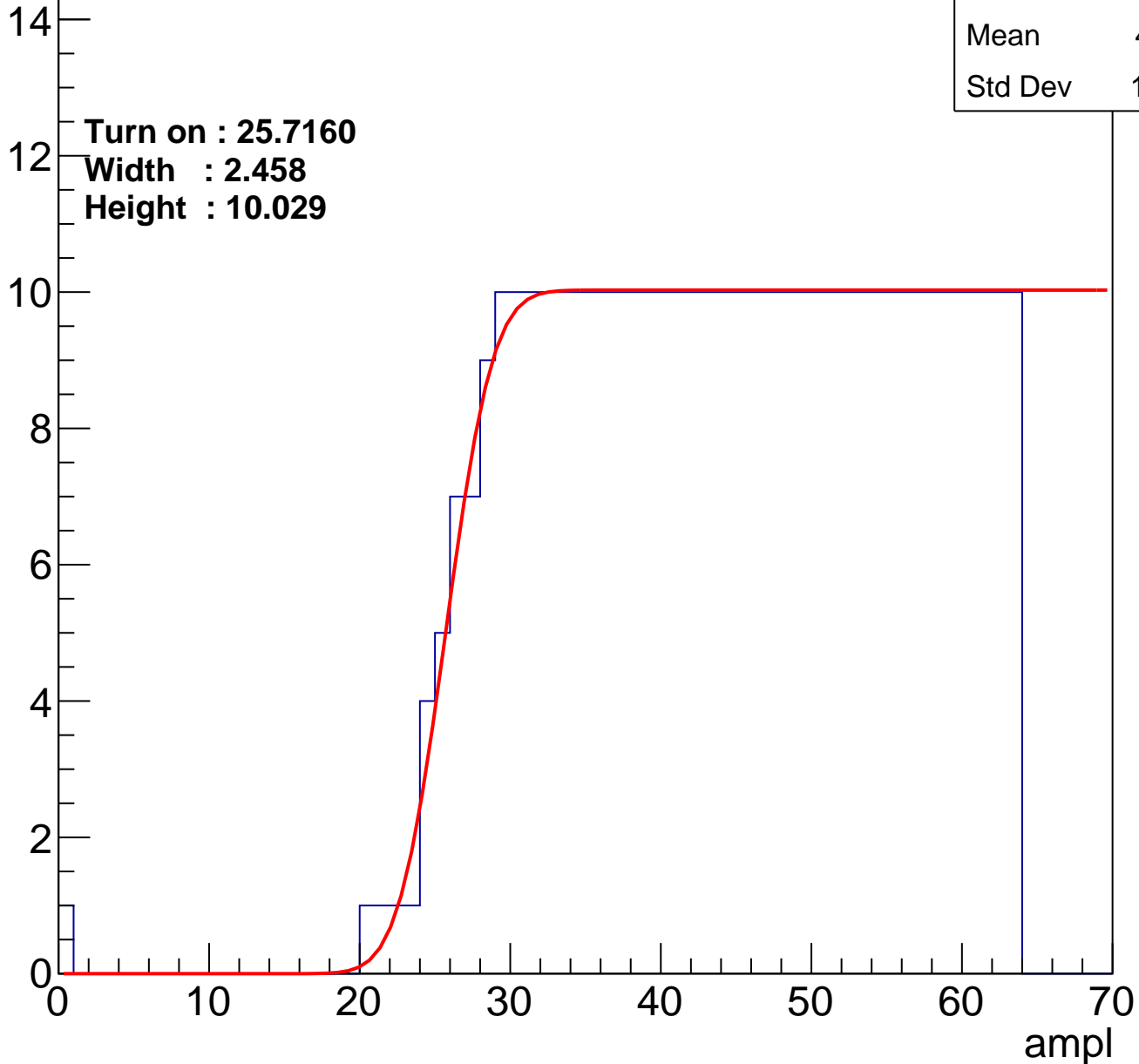
Entries	387
Mean	44.01
Std Dev	11.49

Turn on : 25.7160

Width : 2.458

Height : 10.029

Entry



B1L000S, U17-ch12

calib_packv5_042523_0143.root, FC#3, port B3

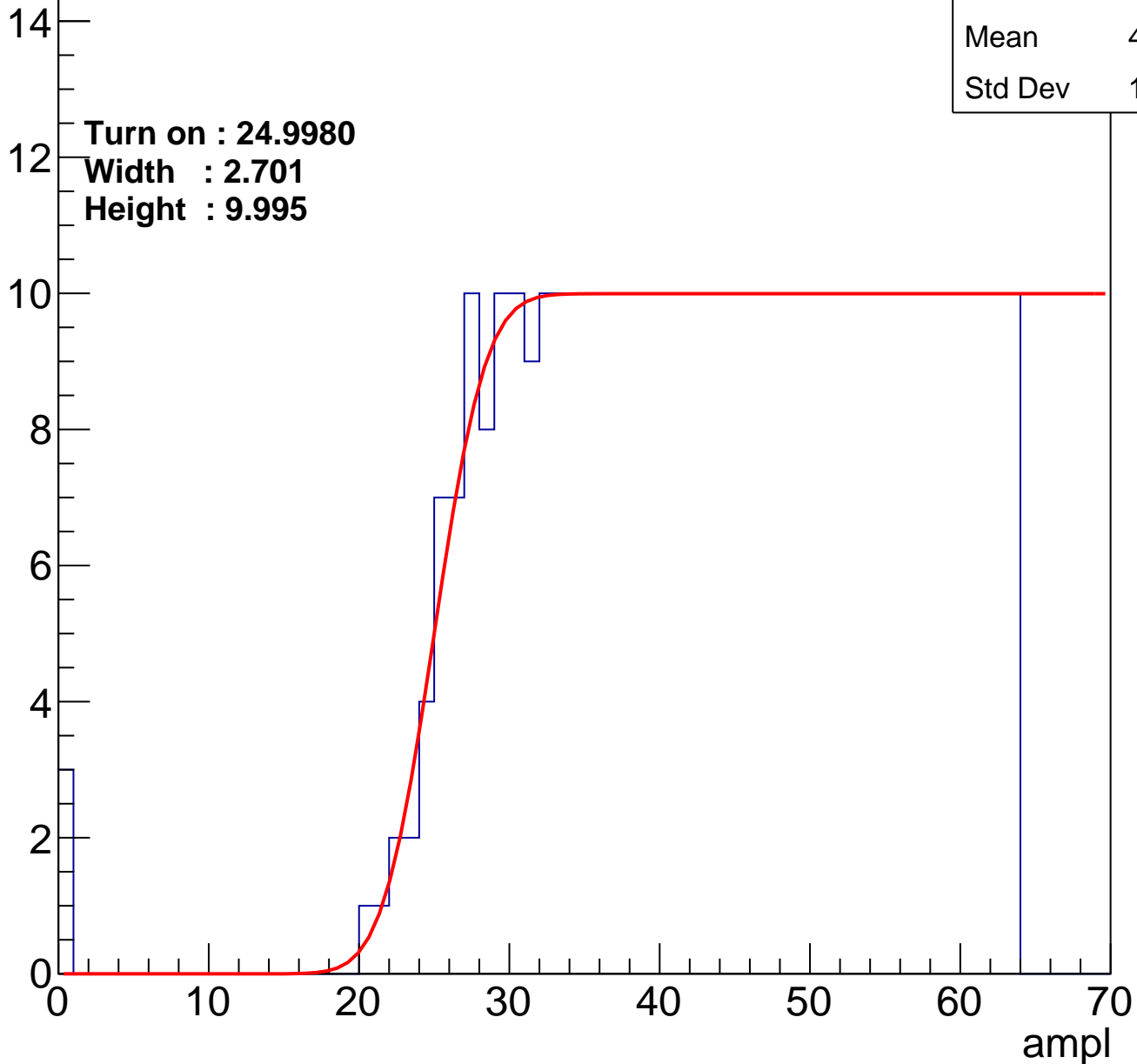
Entries	394
Mean	43.52
Std Dev	12.02

Turn on : 24.9980

Width : 2.701

Height : 9.995

Entry



B1L000S, U17-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.59
Std Dev	11.32

Turn on : 27.1045

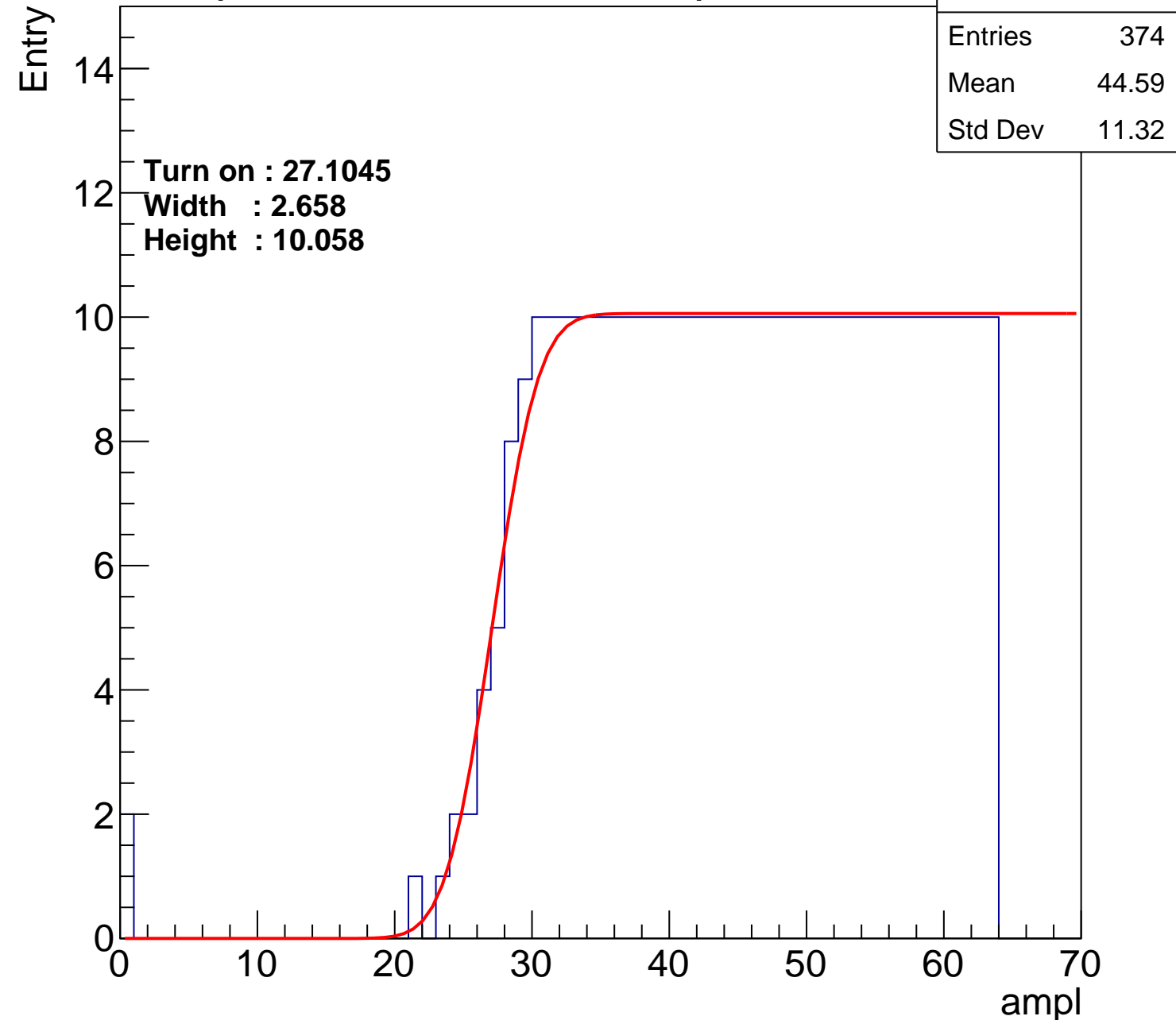
Width : 2.658

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch14

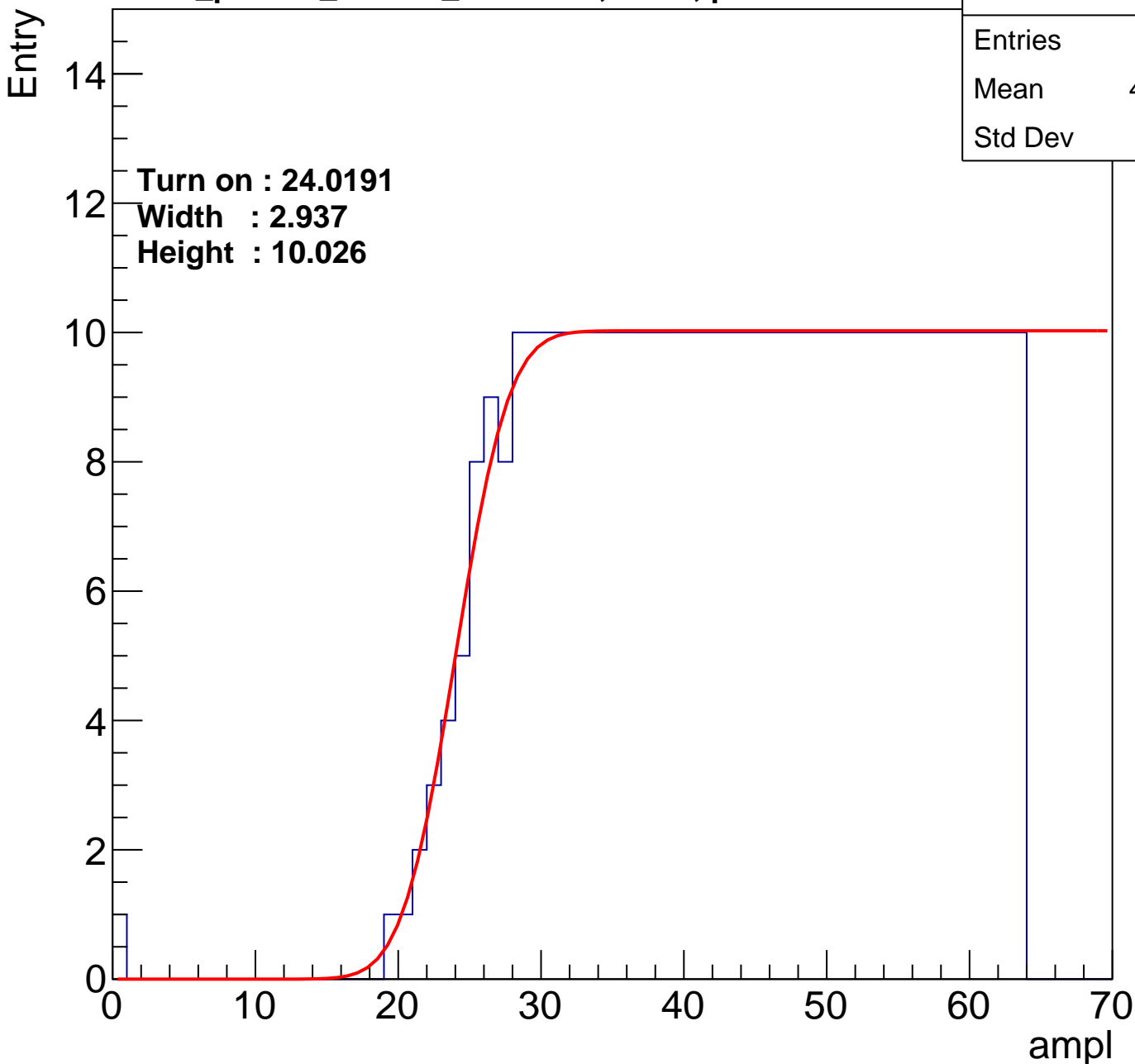
calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.26
Std Dev	11.9

Turn on : 24.0191

Width : 2.937

Height : 10.026



B1L000S, U17-ch15

calib_packv5_042523_0143.root, FC#3, port B3

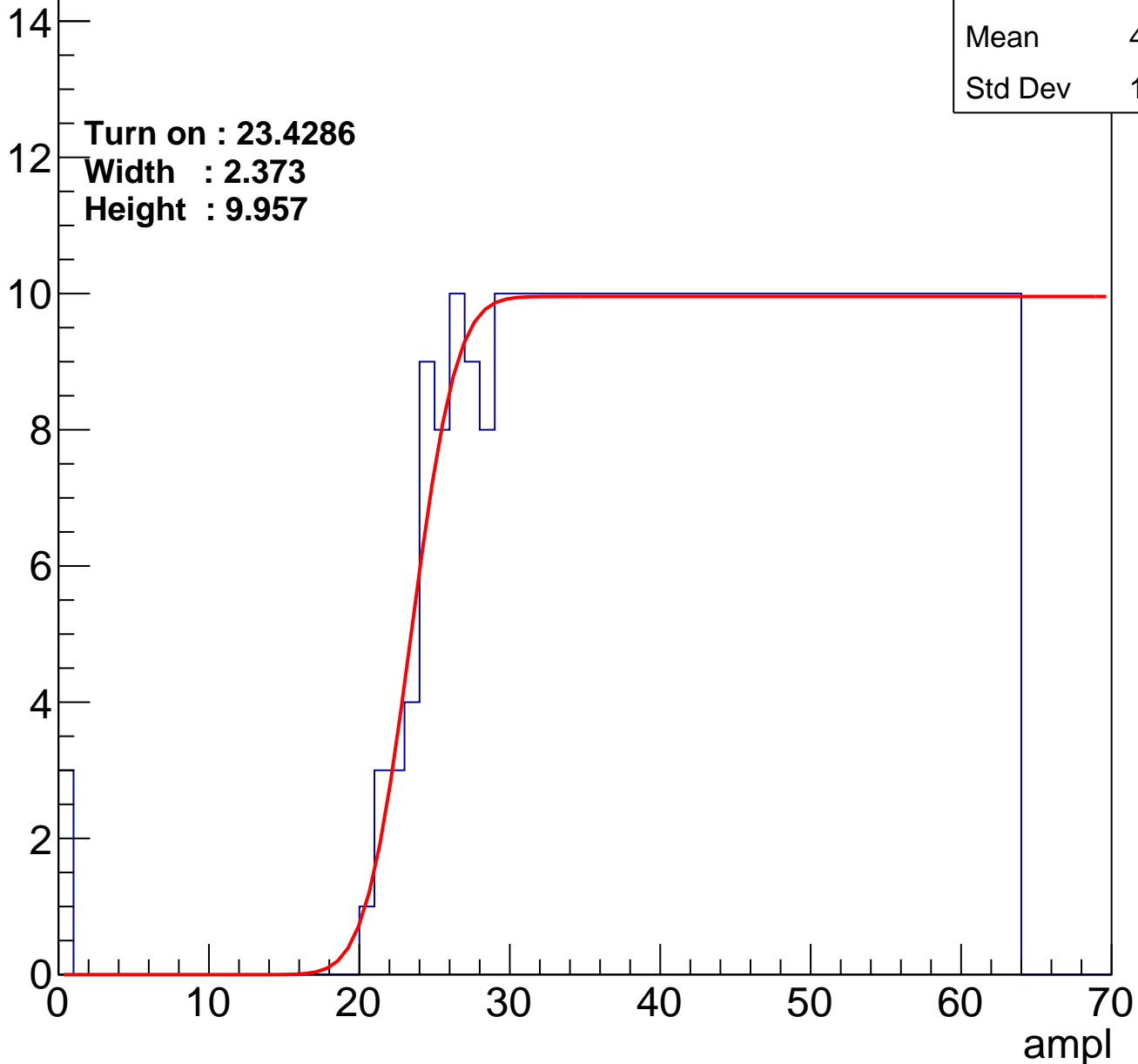
Entries	408
Mean	42.85
Std Dev	12.34

Turn on : 23.4286

Width : 2.373

Height : 9.957

Entry



B1L000S, U17-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.2143

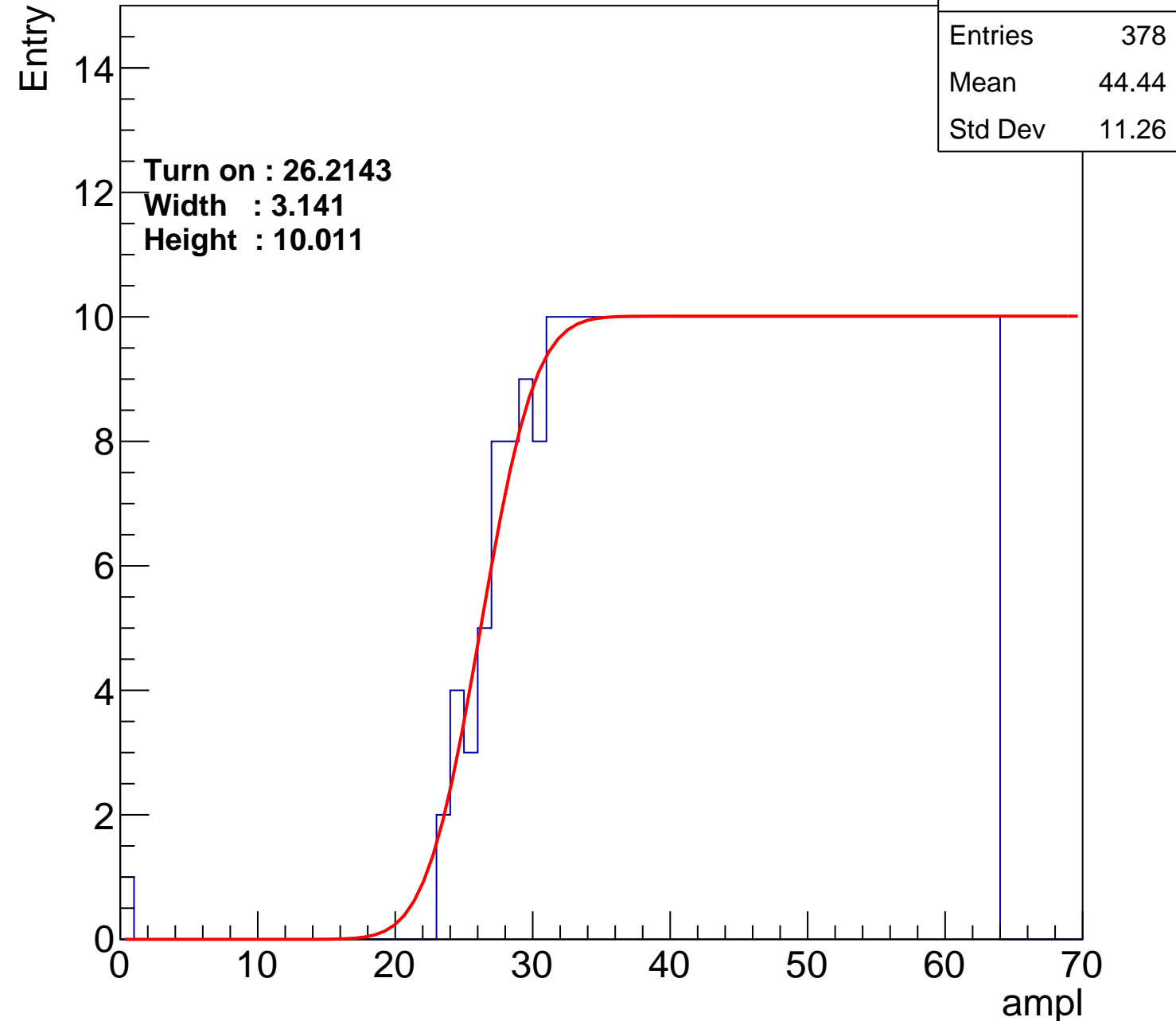
Width : 3.141

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch17

calib_packv5_042523_0143.root, FC#3, port B3

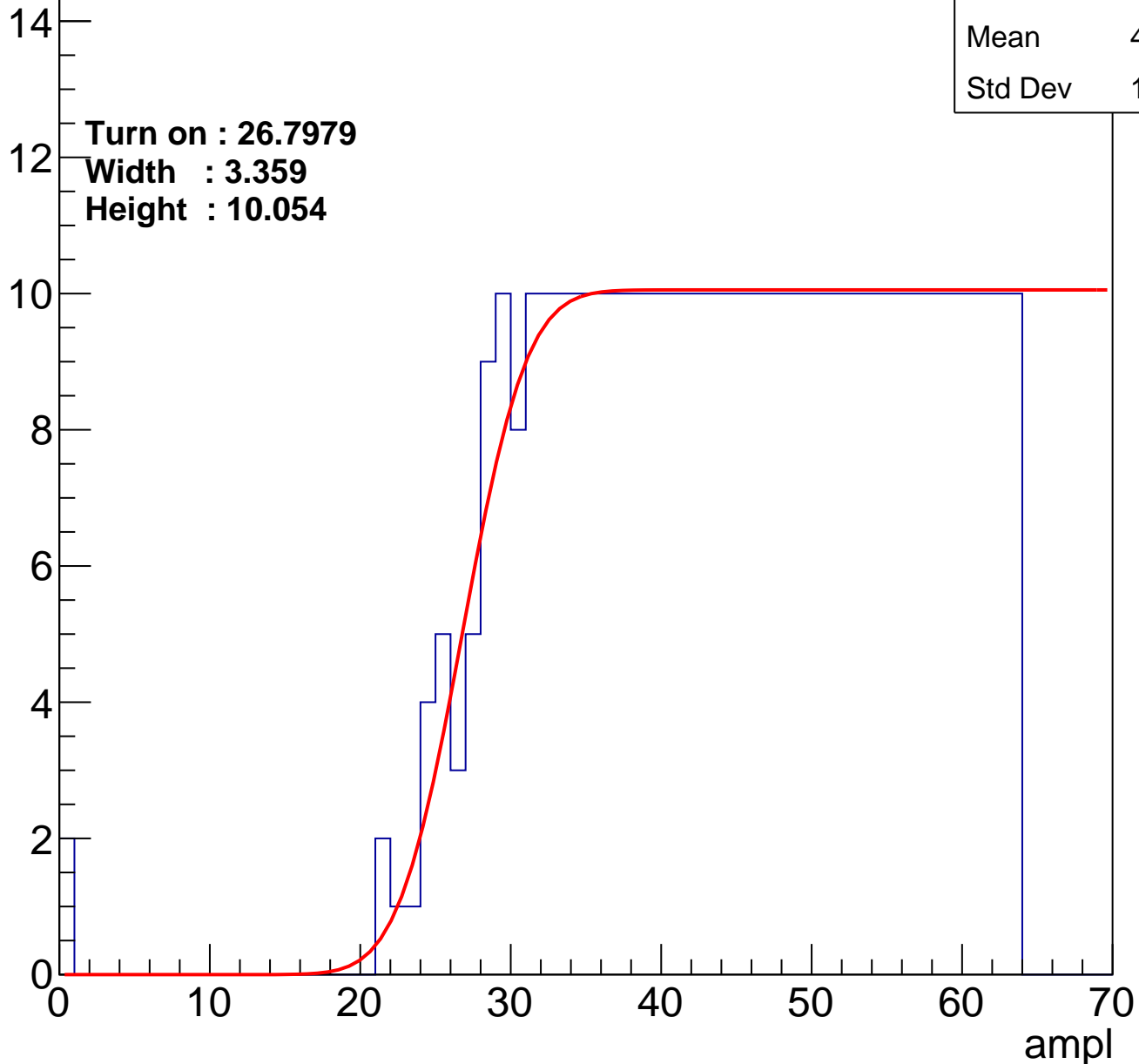
Entries	380
Mean	44.24
Std Dev	11.55

Turn on : 26.7979

Width : 3.359

Height : 10.054

Entry



B1L000S, U17-ch18

calib_packv5_042523_0143.root, FC#3, port B3

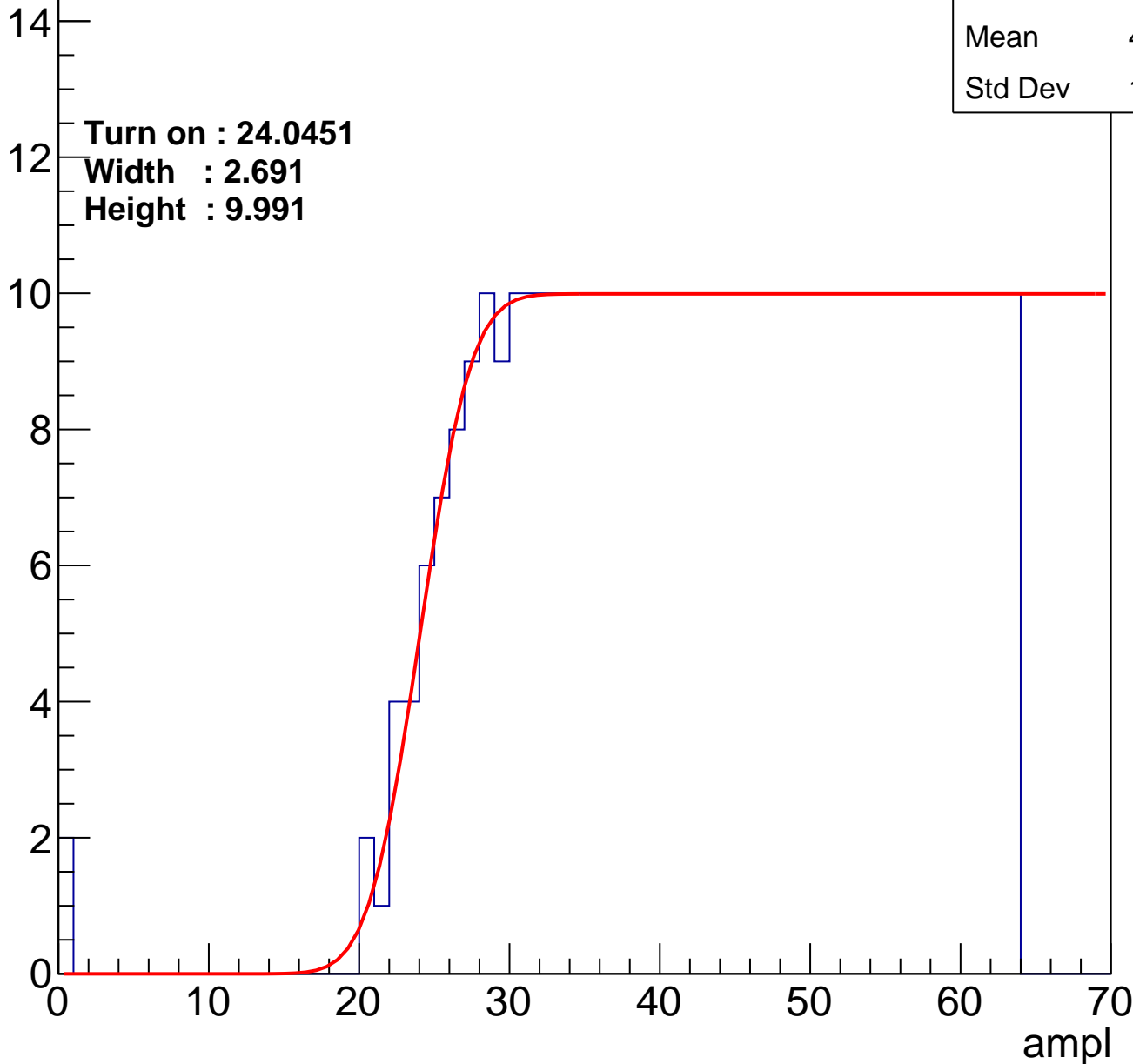
Entries	402
Mean	43.19
Std Dev	12.07

Turn on : 24.0451

Width : 2.691

Height : 9.991

Entry



B1L000S, U17-ch19

calib_packv5_042523_0143.root, FC#3, port B3

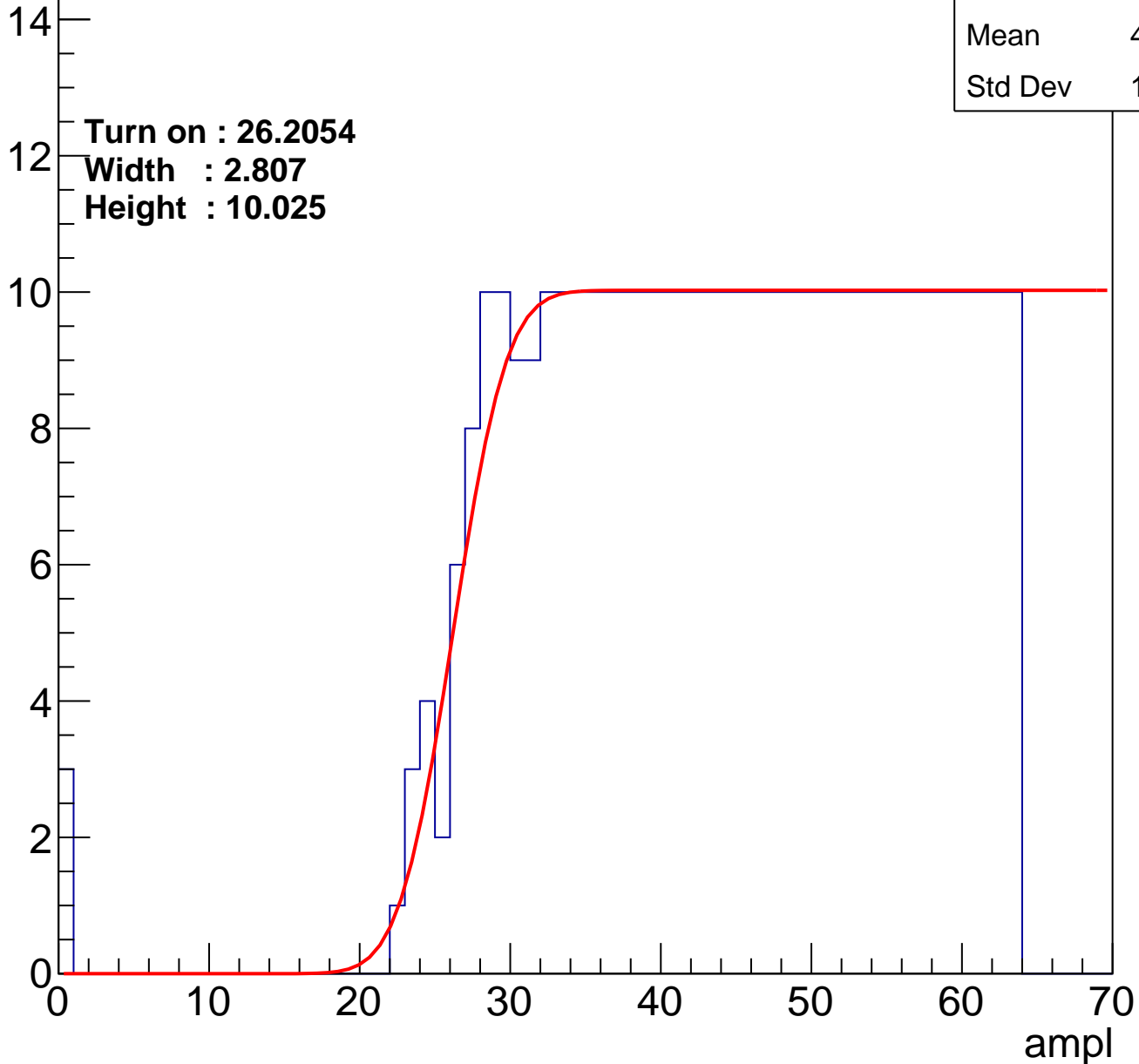
Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 26.2054

Width : 2.807

Height : 10.025

Entry



B1L000S, U17-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.38
Std Dev	12.37

Turn on : 25.8077

Width : 3.069

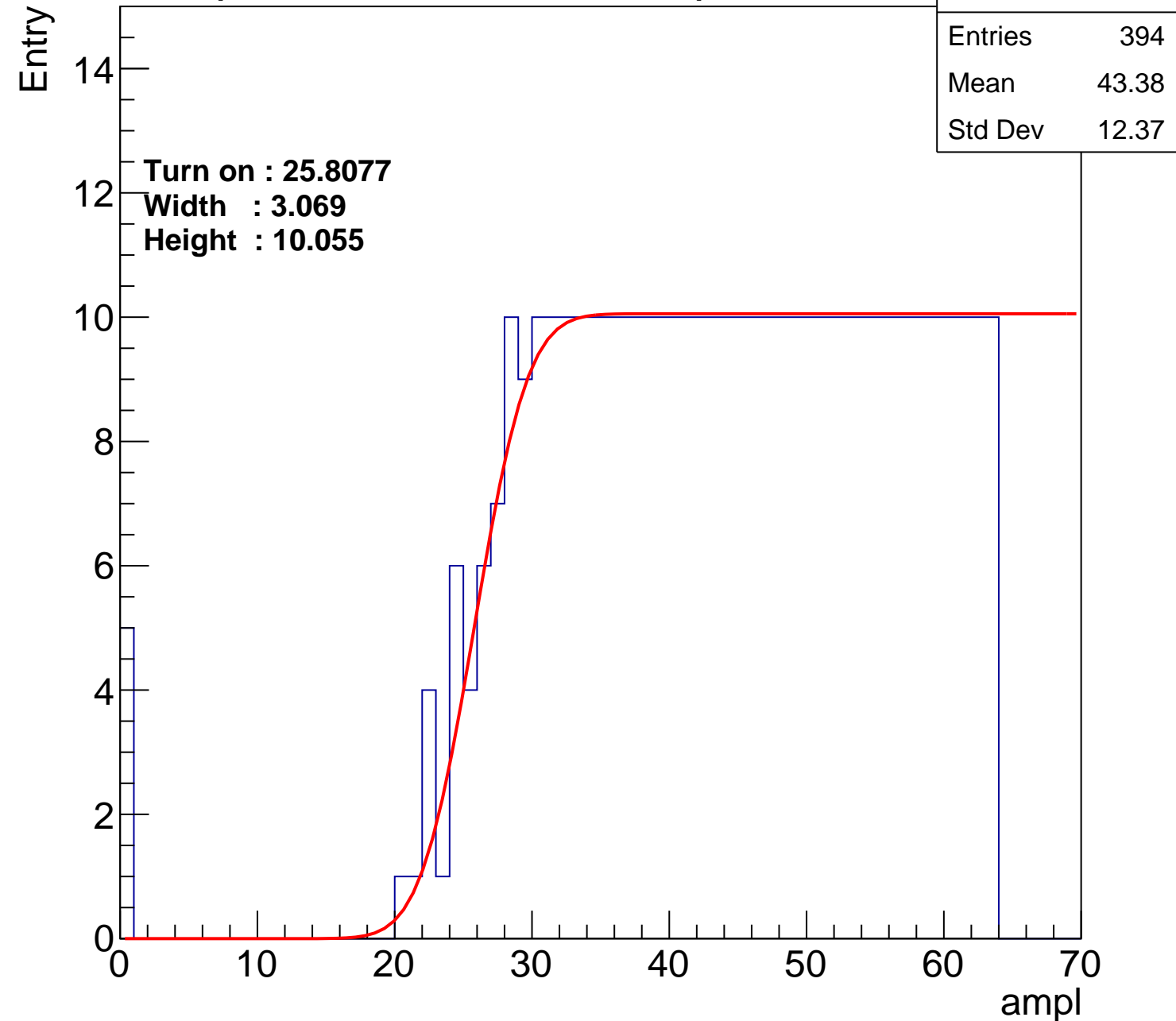
Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U17-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.86
Std Dev	11

Turn on : 27.2163

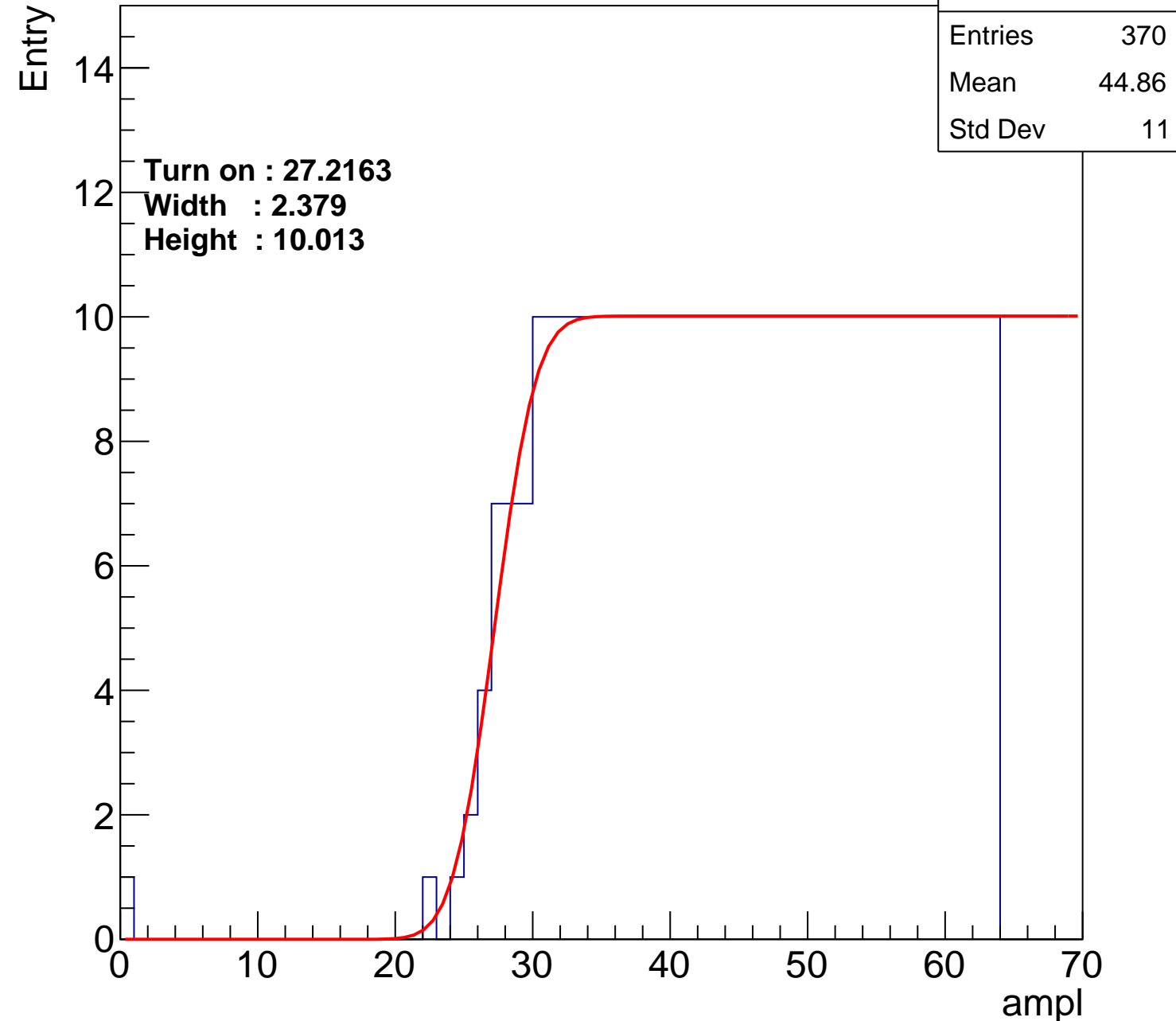
Width : 2.379

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.2
Std Dev	11.87

Turn on : 26.9204

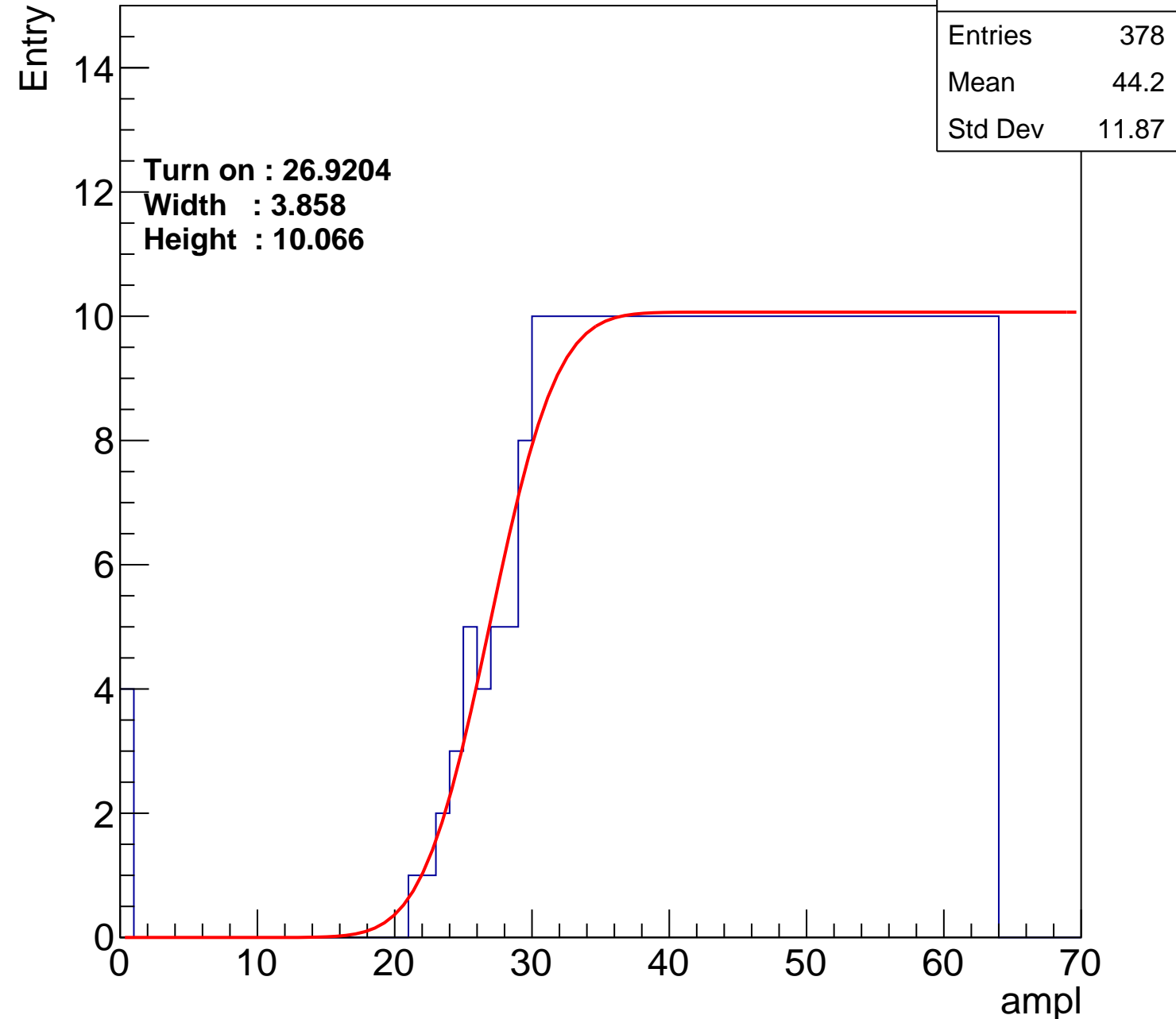
Width : 3.858

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

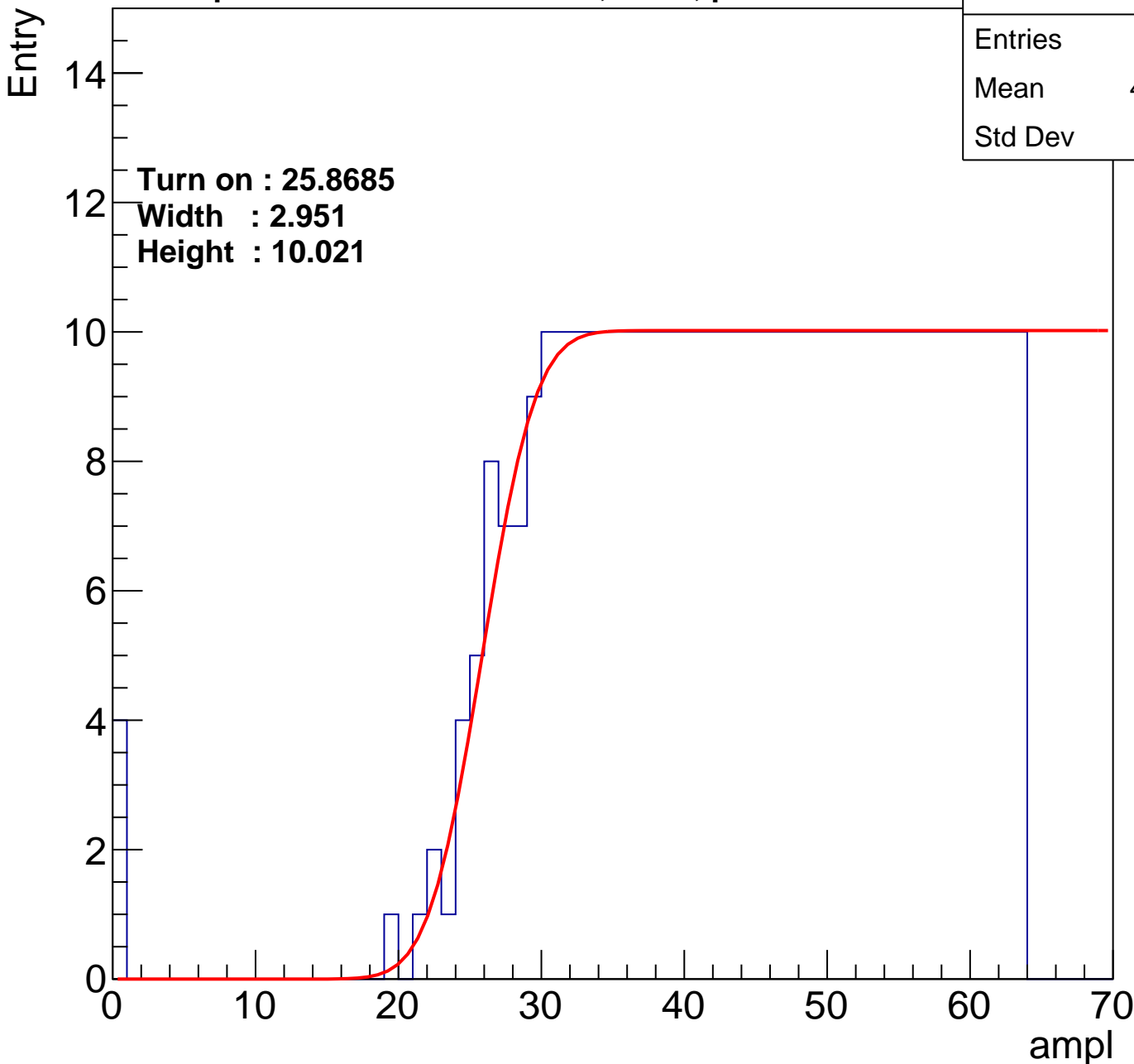
Entries	389
Mean	43.68
Std Dev	12.1

Mean	43.68
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Std Dev	12.1
---------	------

Width : 2.951

Height : 10.021



B1L000S, U17-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.55
Std Dev	11.86

Turn on : 25.0259

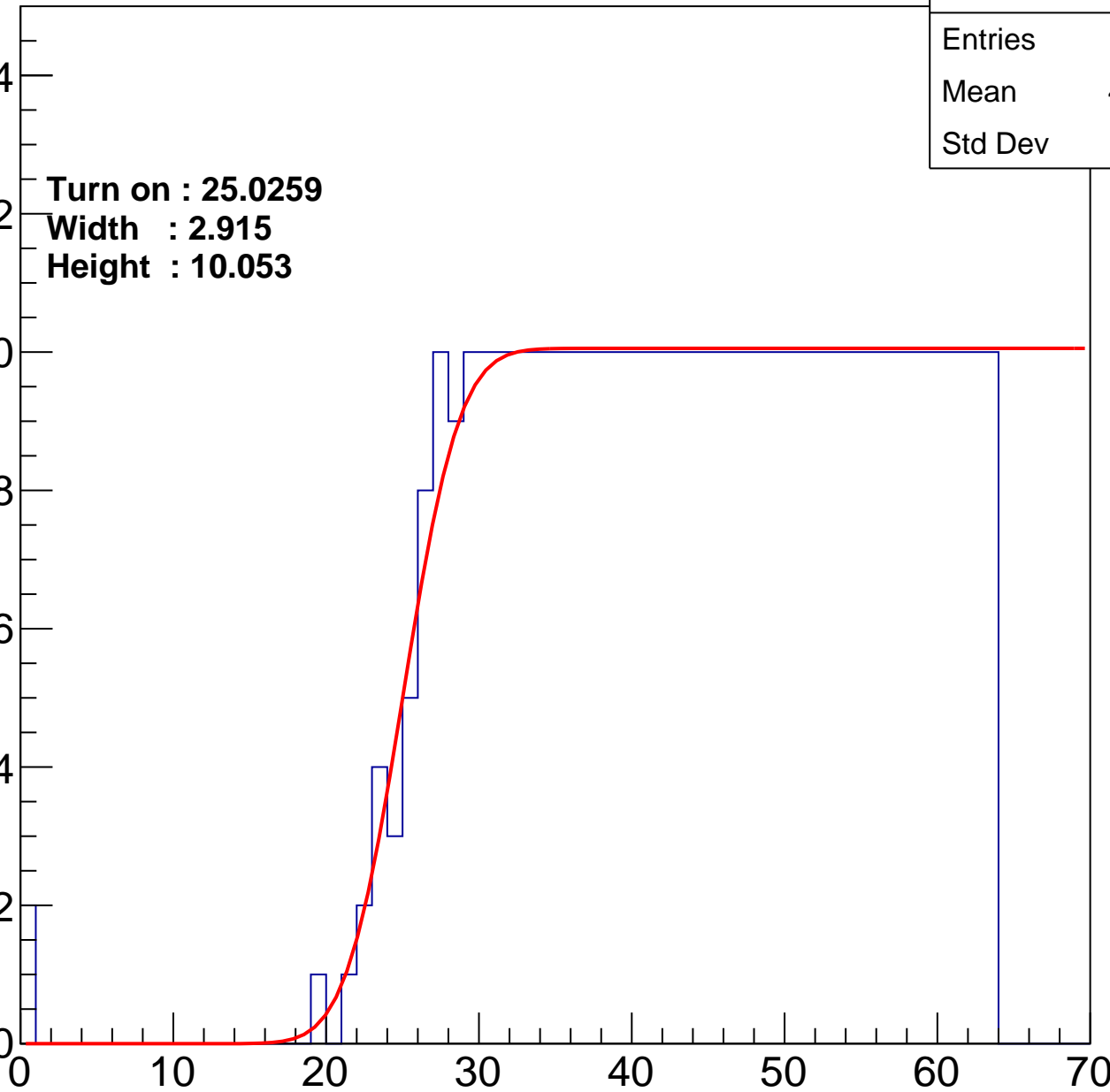
Width : 2.915

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 28.1247

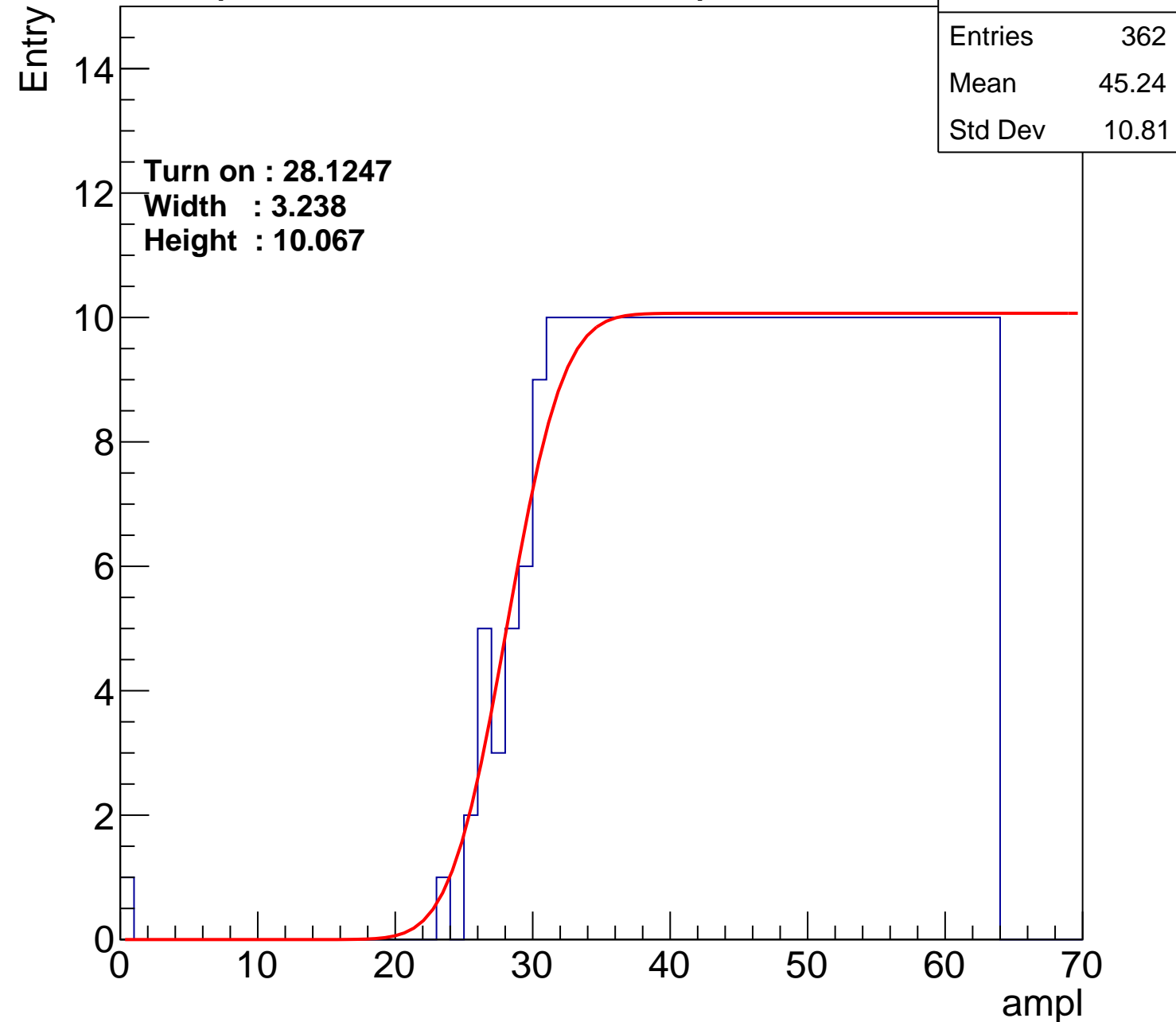
Width : 3.238

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.69
Std Dev	11.31

Turn on : 26.9726

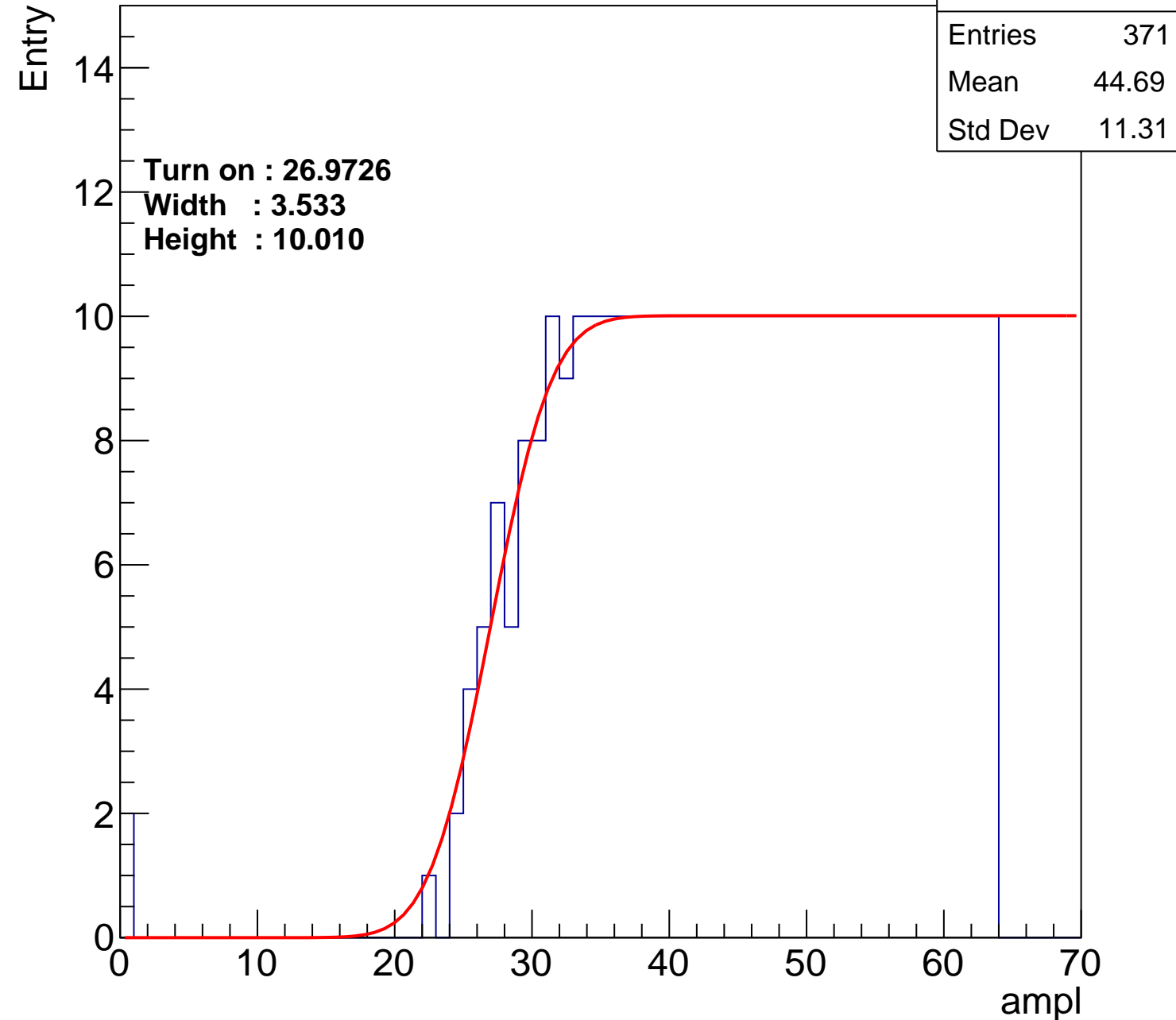
Width : 3.533

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.55
Std Dev	12.37

Turn on : 25.9877

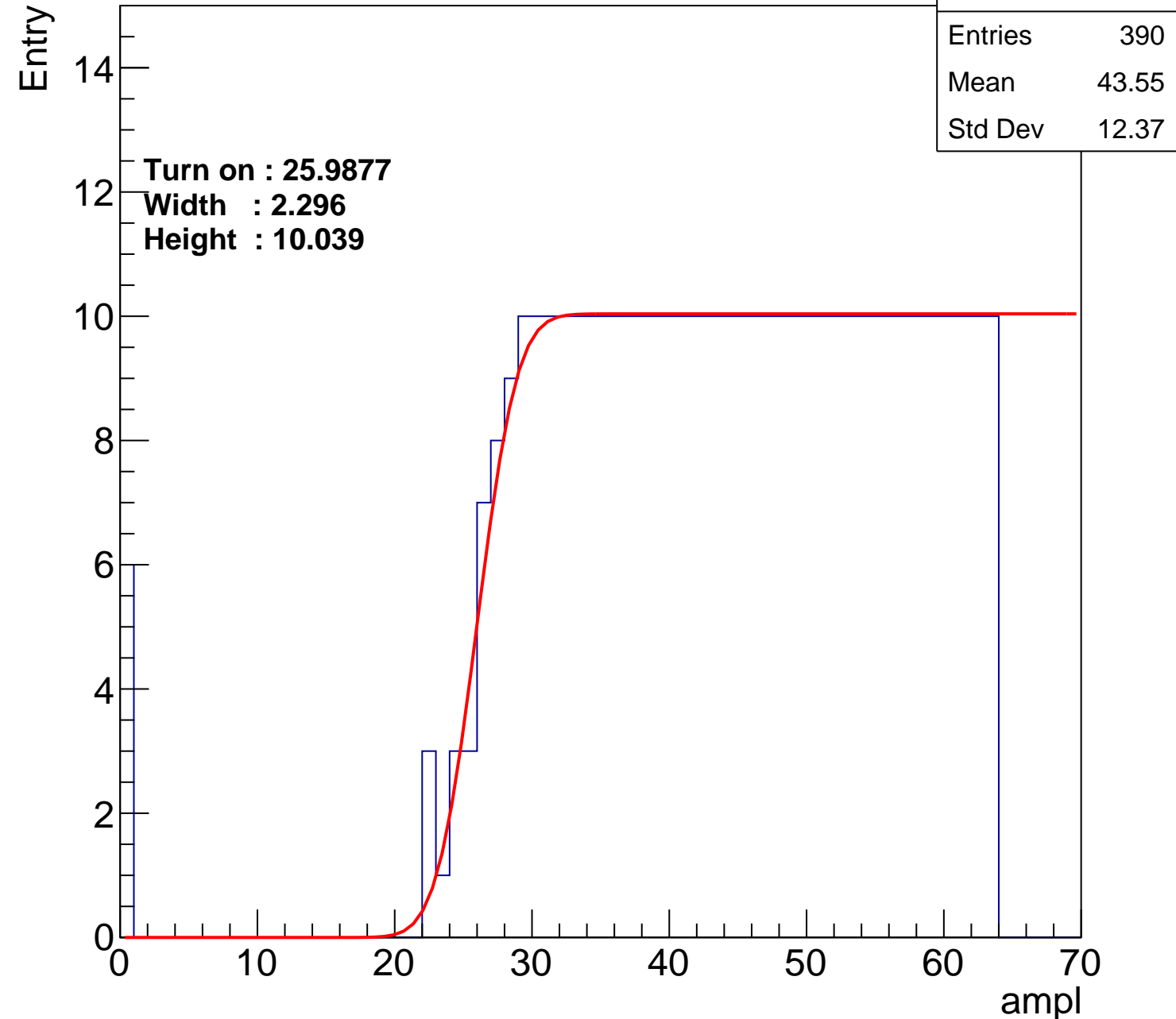
Width : 2.296

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch28

calib_packv5_042523_0143.root, FC#3, port B3

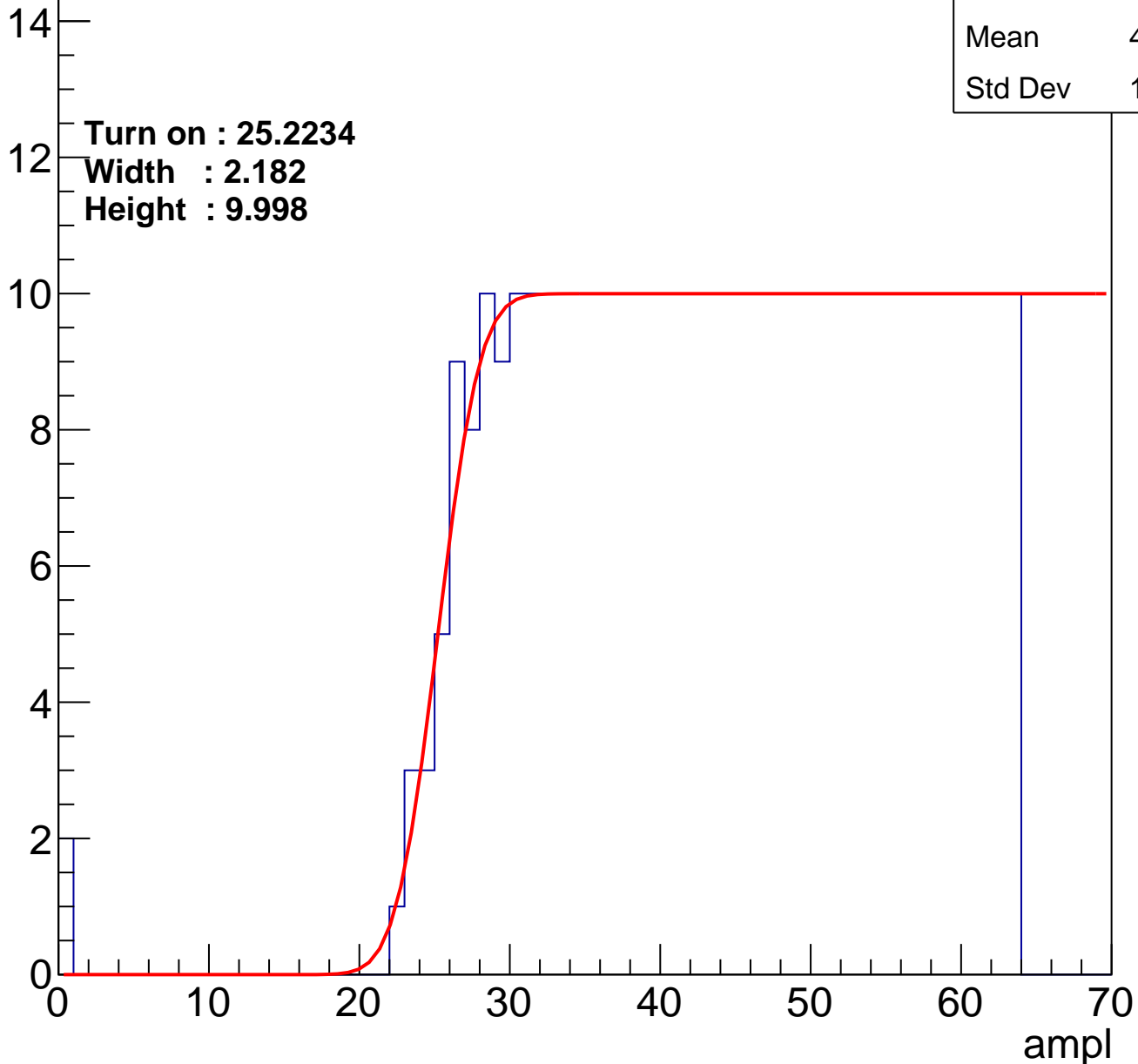
Entries	390
Mean	43.82
Std Dev	11.69

Turn on : 25.2234

Width : 2.182

Height : 9.998

Entry



B1L000S, U17-ch29

calib_packv5_042523_0143.root, FC#3, port B3

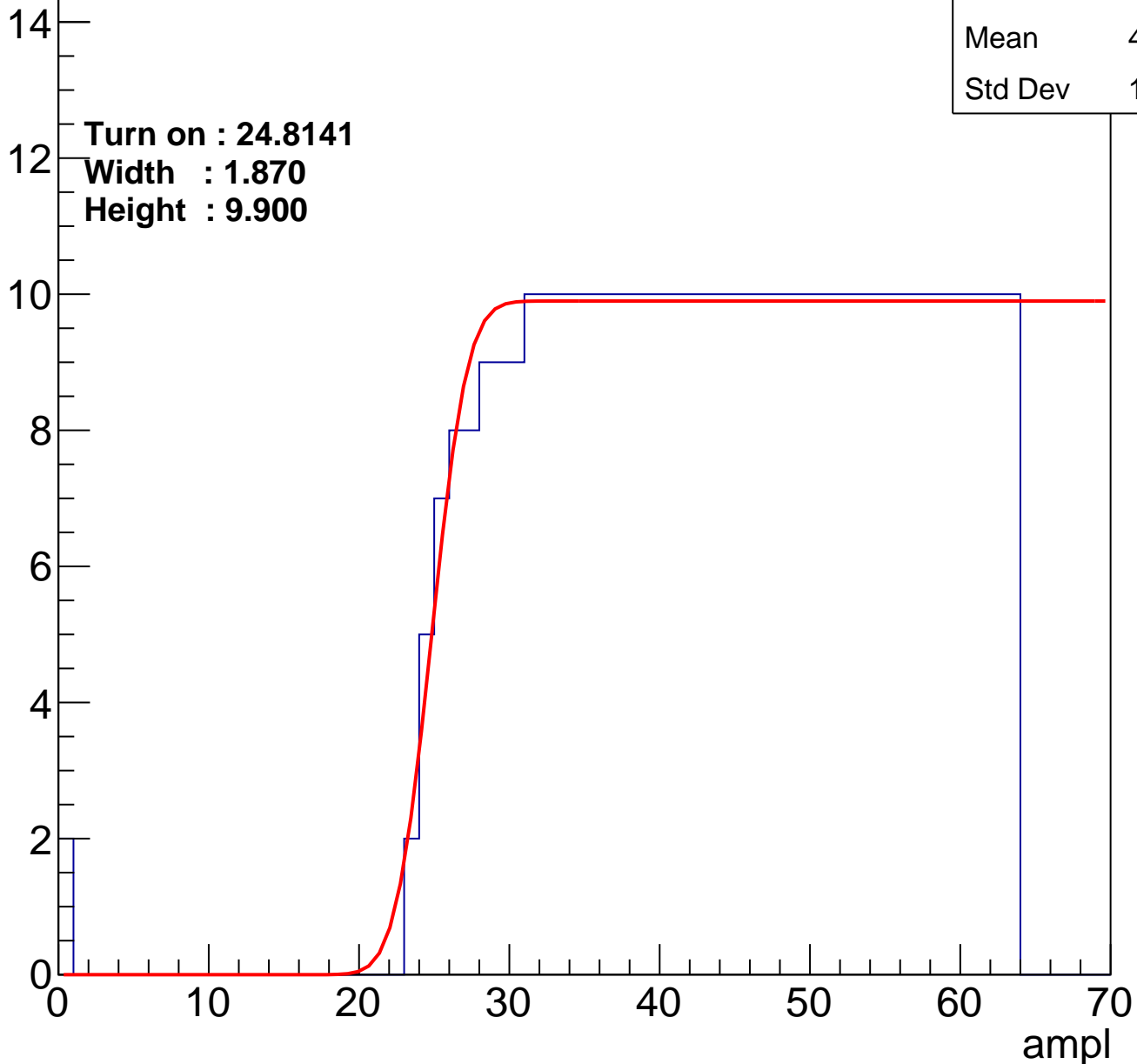
Entries	389
Mean	43.85
Std Dev	11.69

Turn on : 24.8141

Width : 1.870

Height : 9.900

Entry



B1L000S, U17-ch30

calib_packv5_042523_0143.root, FC#3, port B3

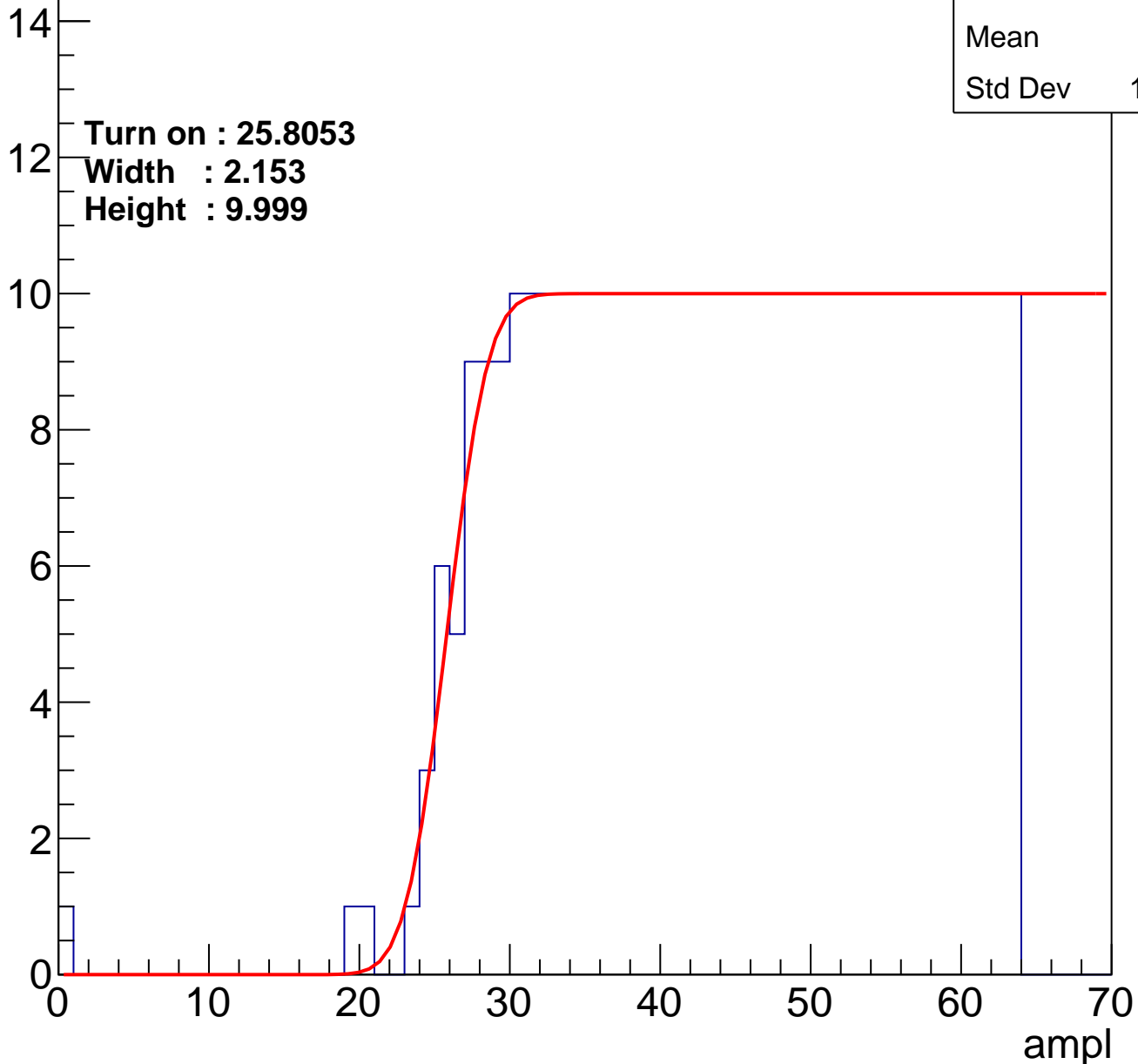
Entries	385
Mean	44.1
Std Dev	11.43

Turn on : 25.8053

Width : 2.153

Height : 9.999

Entry



B1L000S, U17-ch31

calib_packv5_042523_0143.root, FC#3, port B3

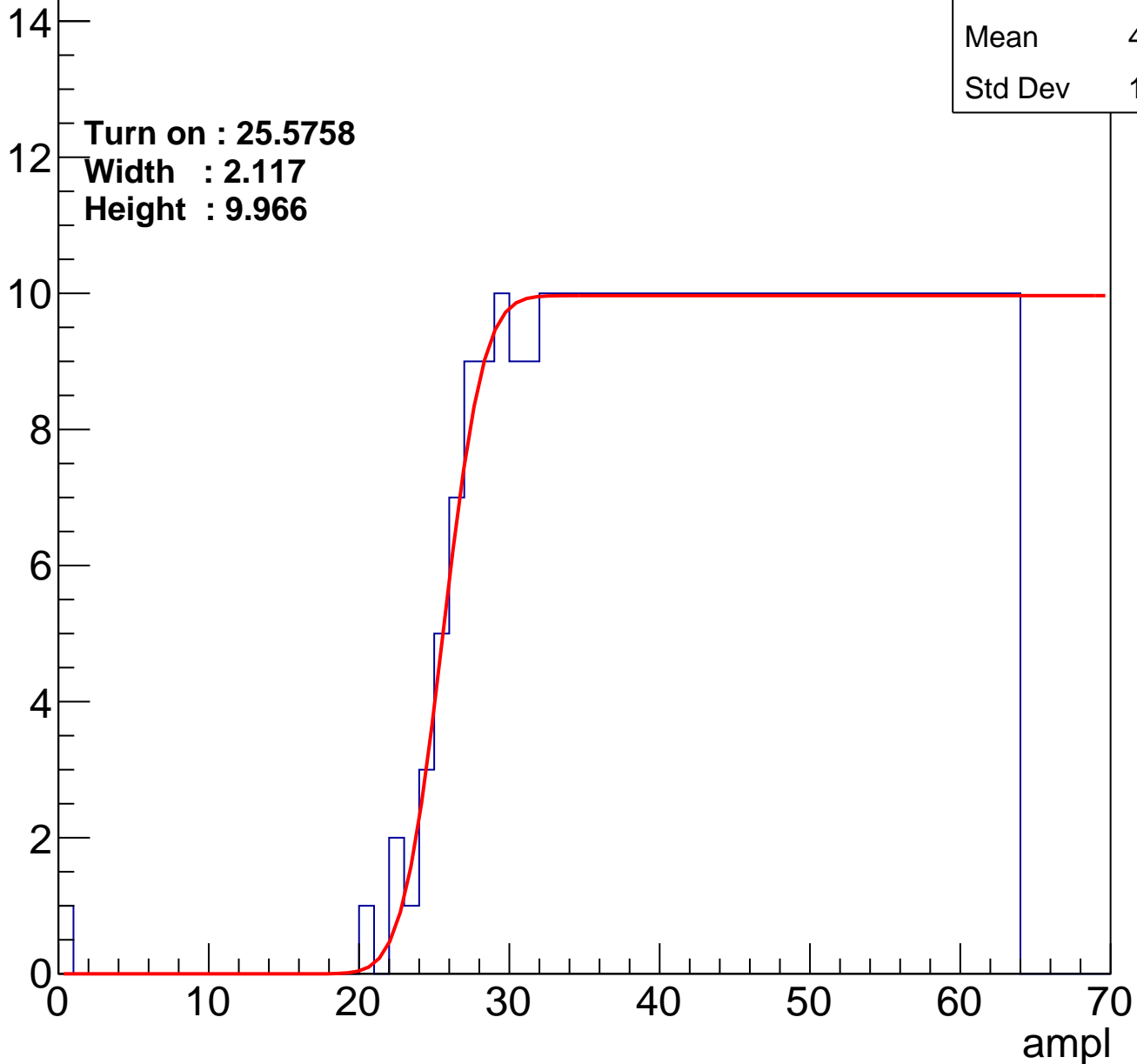
Entries	386
Mean	44.04
Std Dev	11.48

Turn on : 25.5758

Width : 2.117

Height : 9.966

Entry



B1L000S, U17-ch32

calib_packv5_042523_0143.root, FC#3, port B3

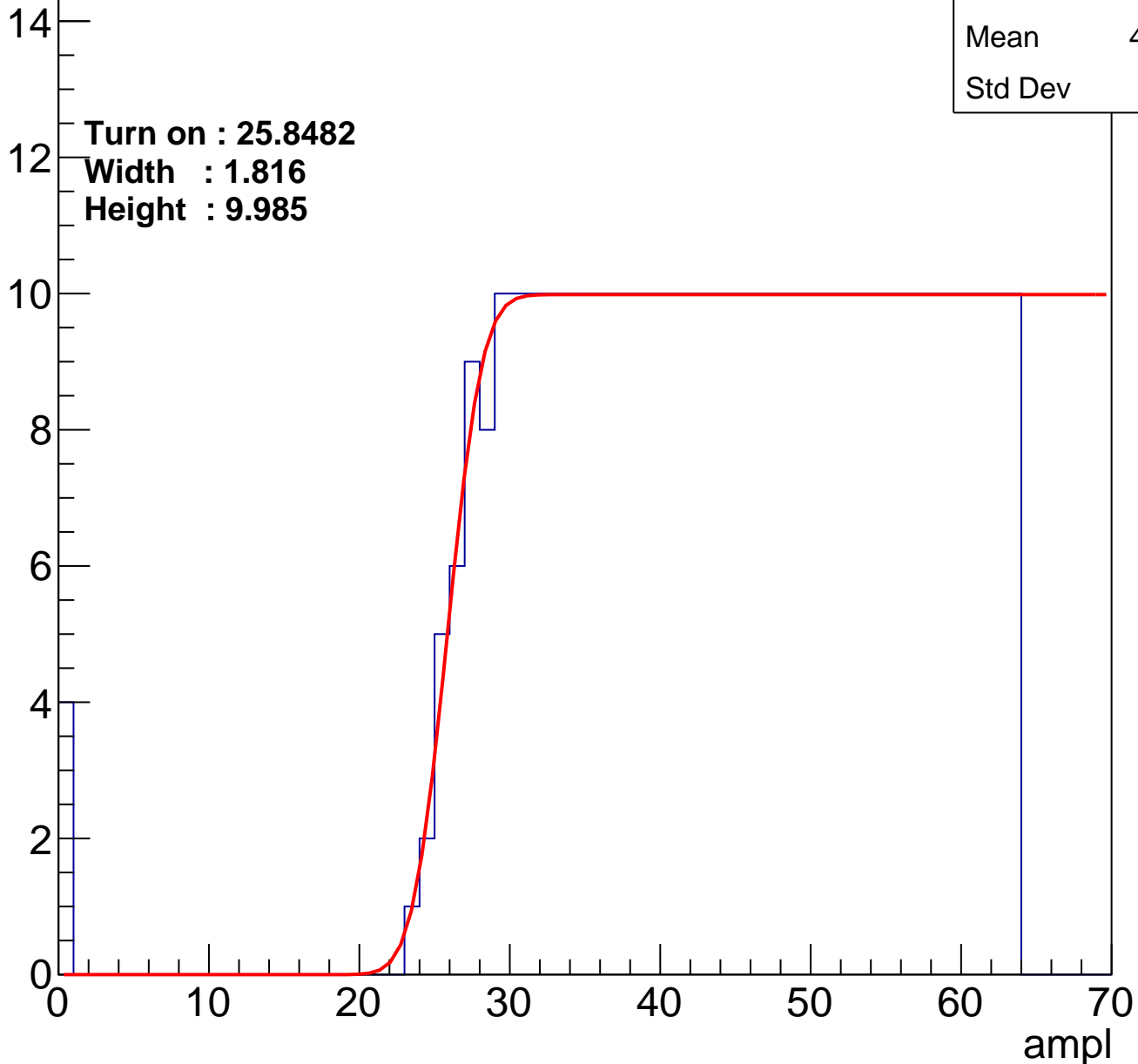
Entries	385
Mean	43.95
Std Dev	11.9

Turn on : 25.8482

Width : 1.816

Height : 9.985

Entry



B1L000S, U17-ch33

calib_packv5_042523_0143.root, FC#3, port B3

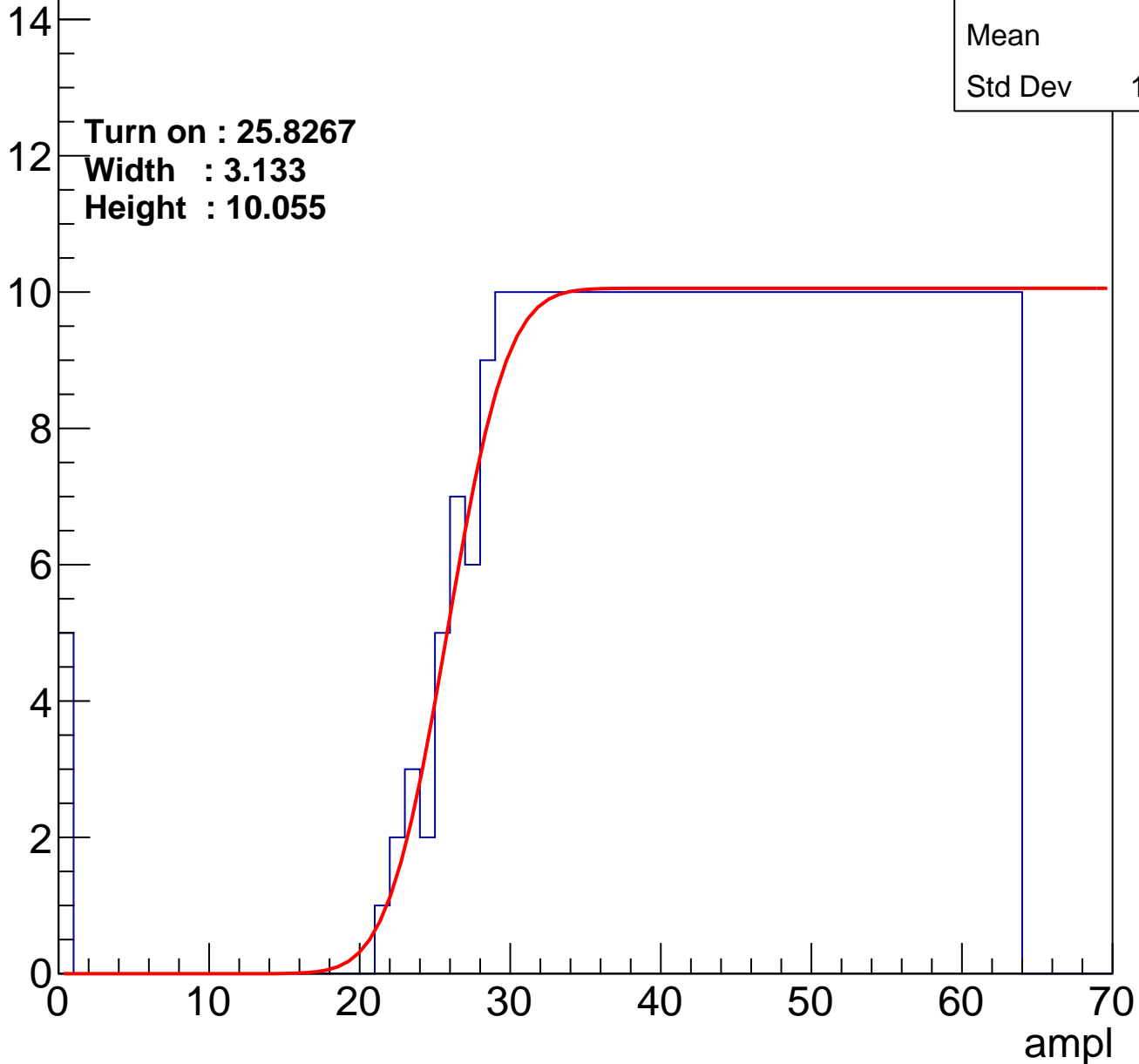
Entries	390
Mean	43.6
Std Dev	12.24

Turn on : 25.8267

Width : 3.133

Height : 10.055

Entry



B1L000S, U17-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.94
Std Dev	12.08

Turn on : 26.3039

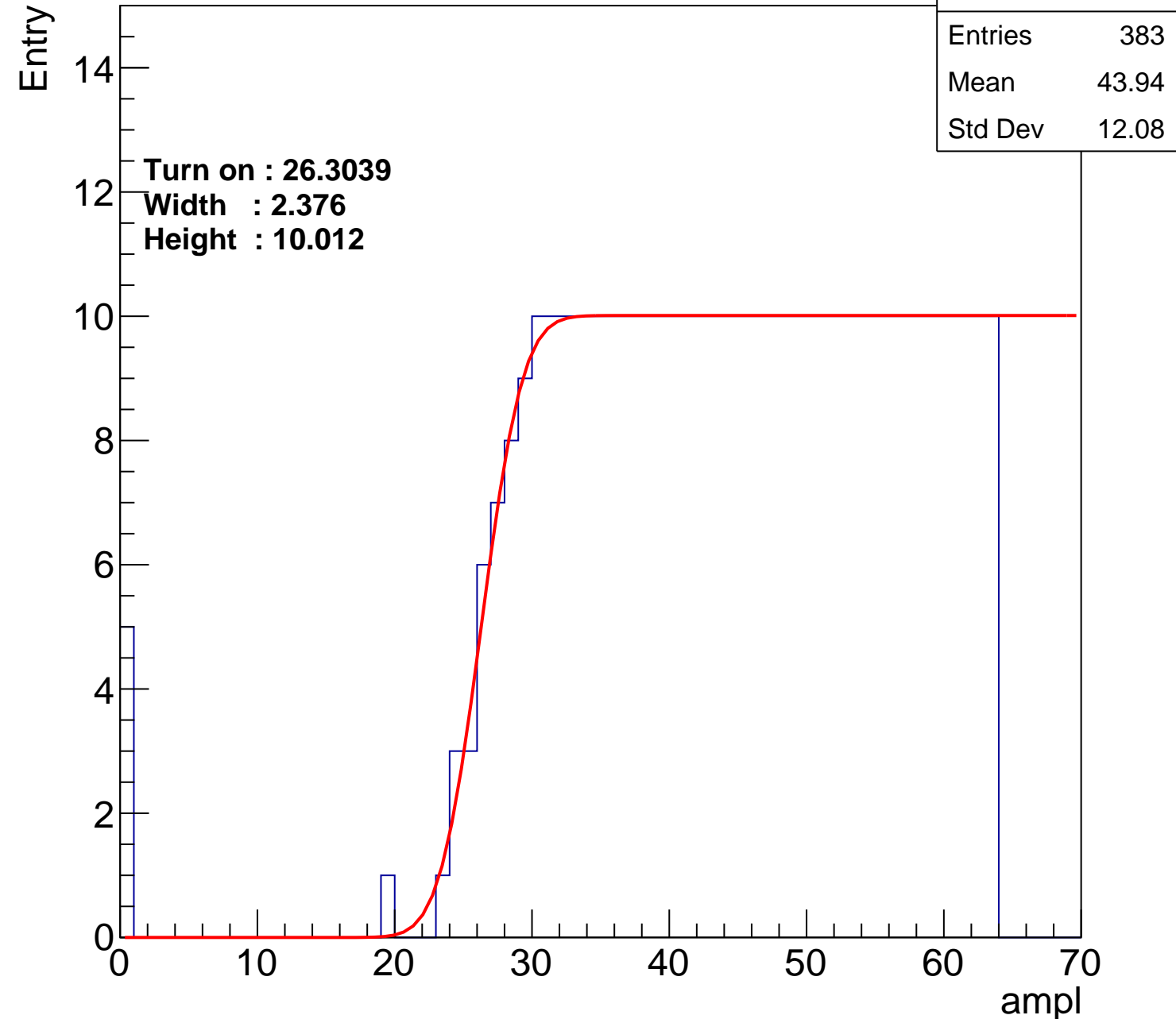
Width : 2.376

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 28.1784

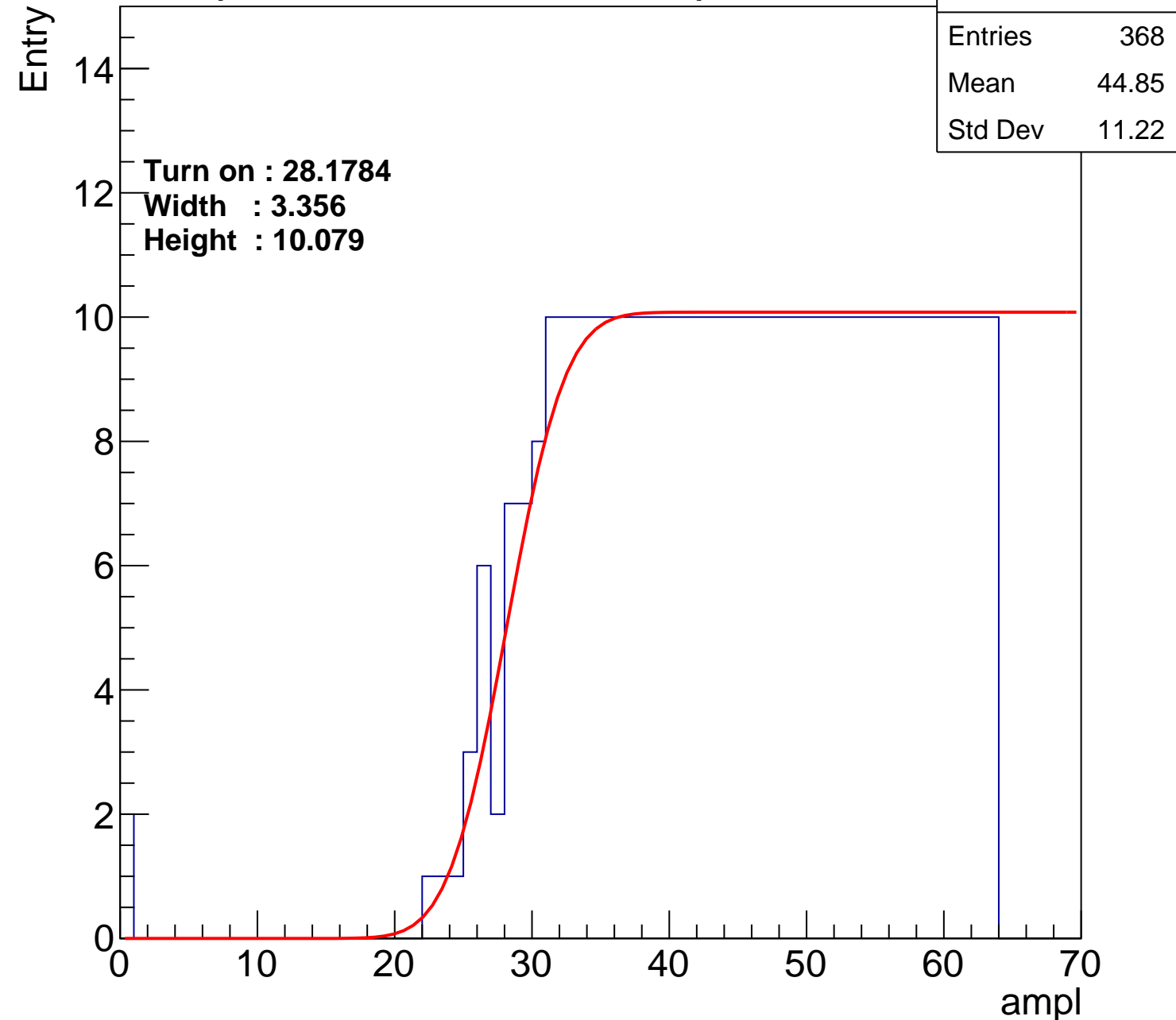
Width : 3.356

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	409
Mean	42.96
Std Dev	12.02

Turn on : 23.4101

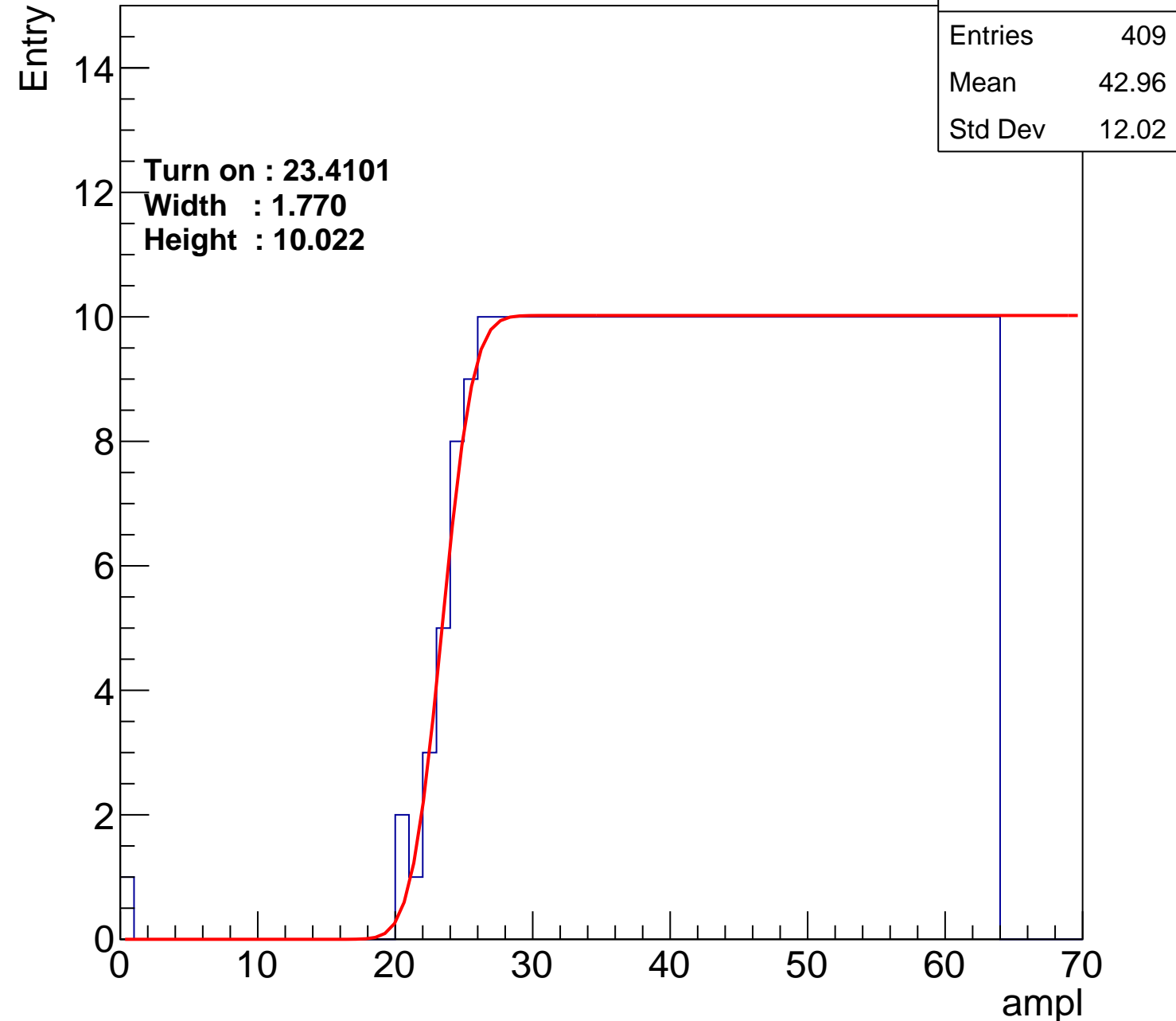
Width : 1.770

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 26.2685

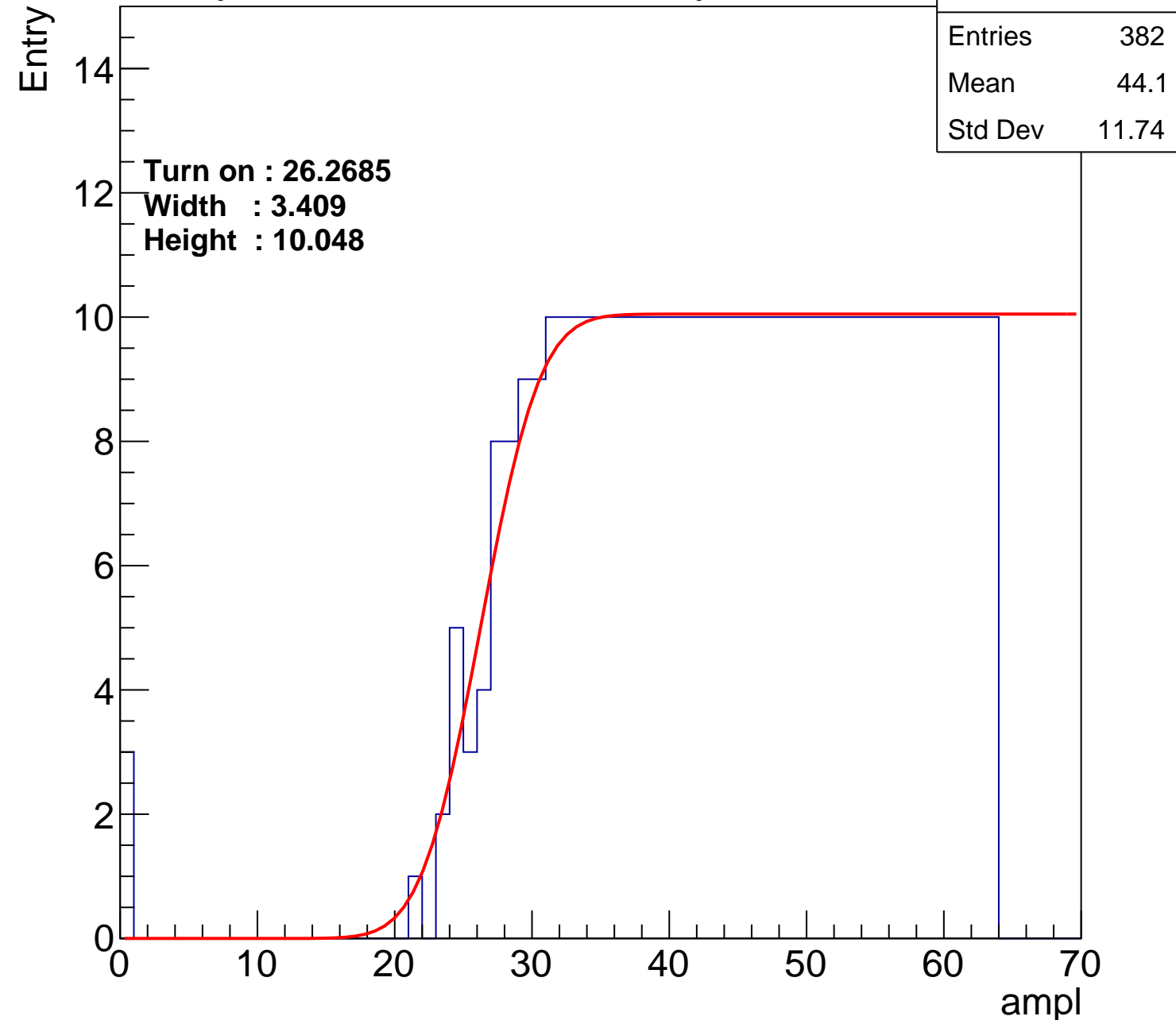
Width : 3.409

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.09
Std Dev	12.17

Turn on : 23.5287

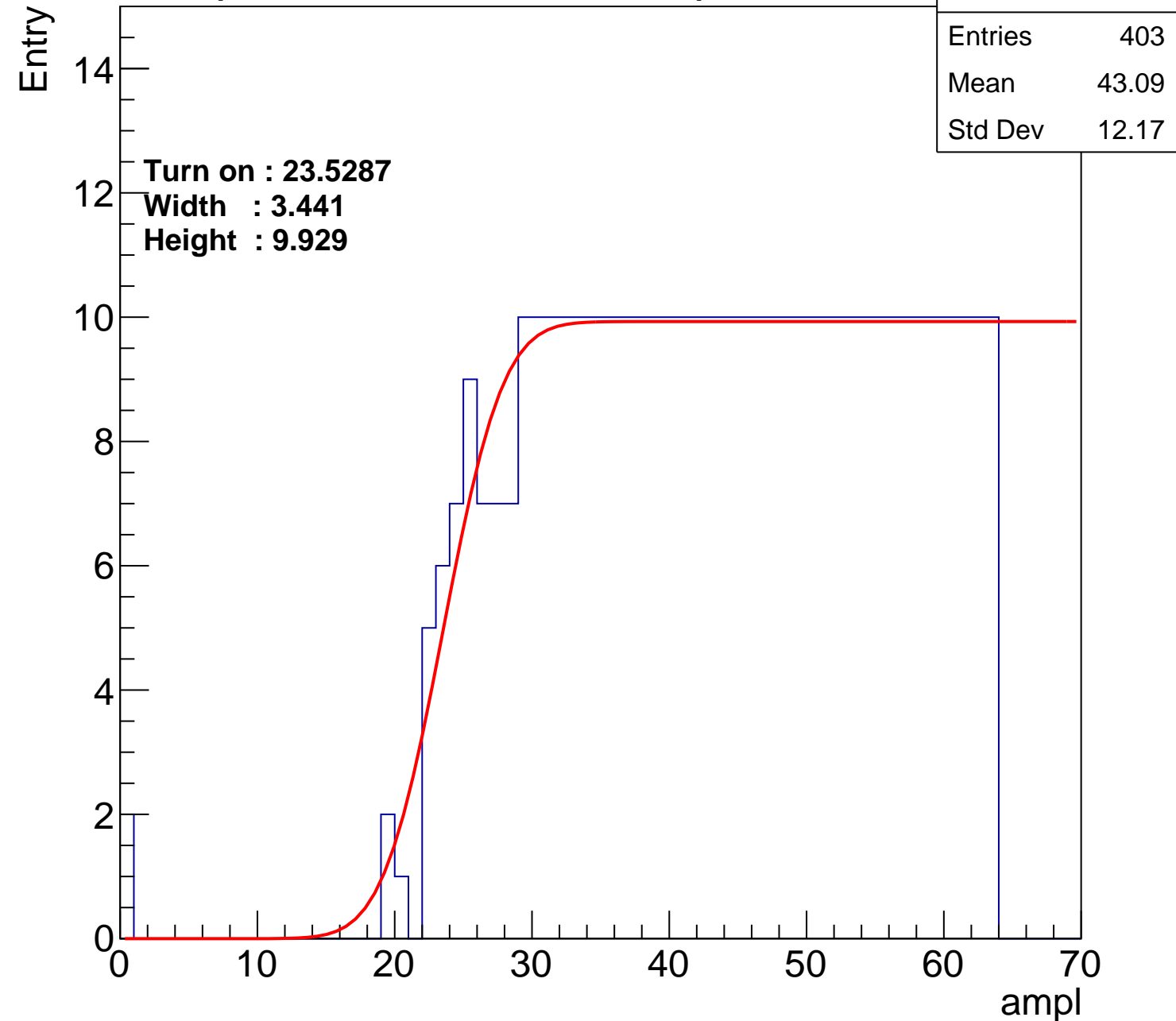
Width : 3.441

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 26.6509

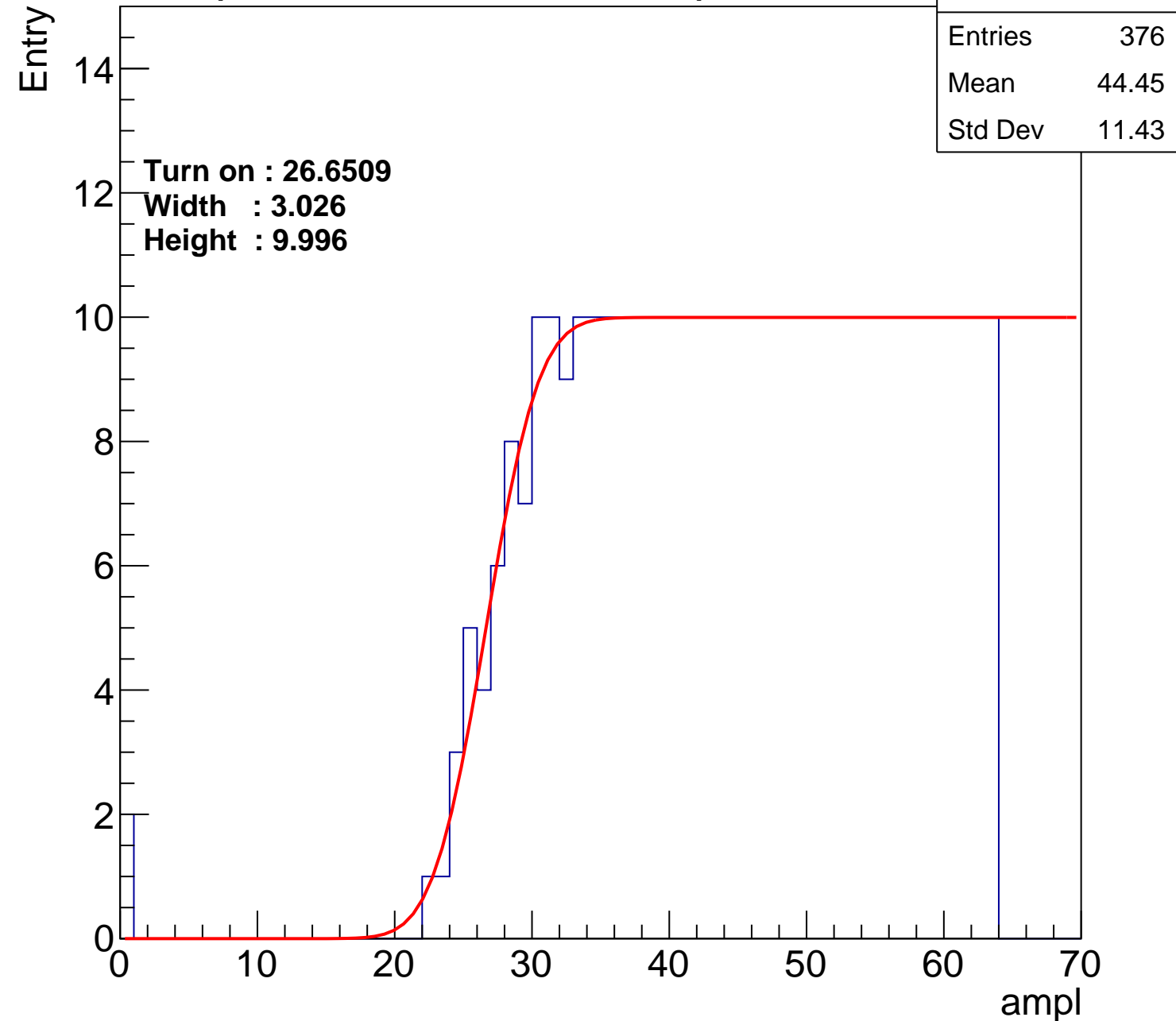
Width : 3.026

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.09
Std Dev	11.46

Turn on : 25.7285

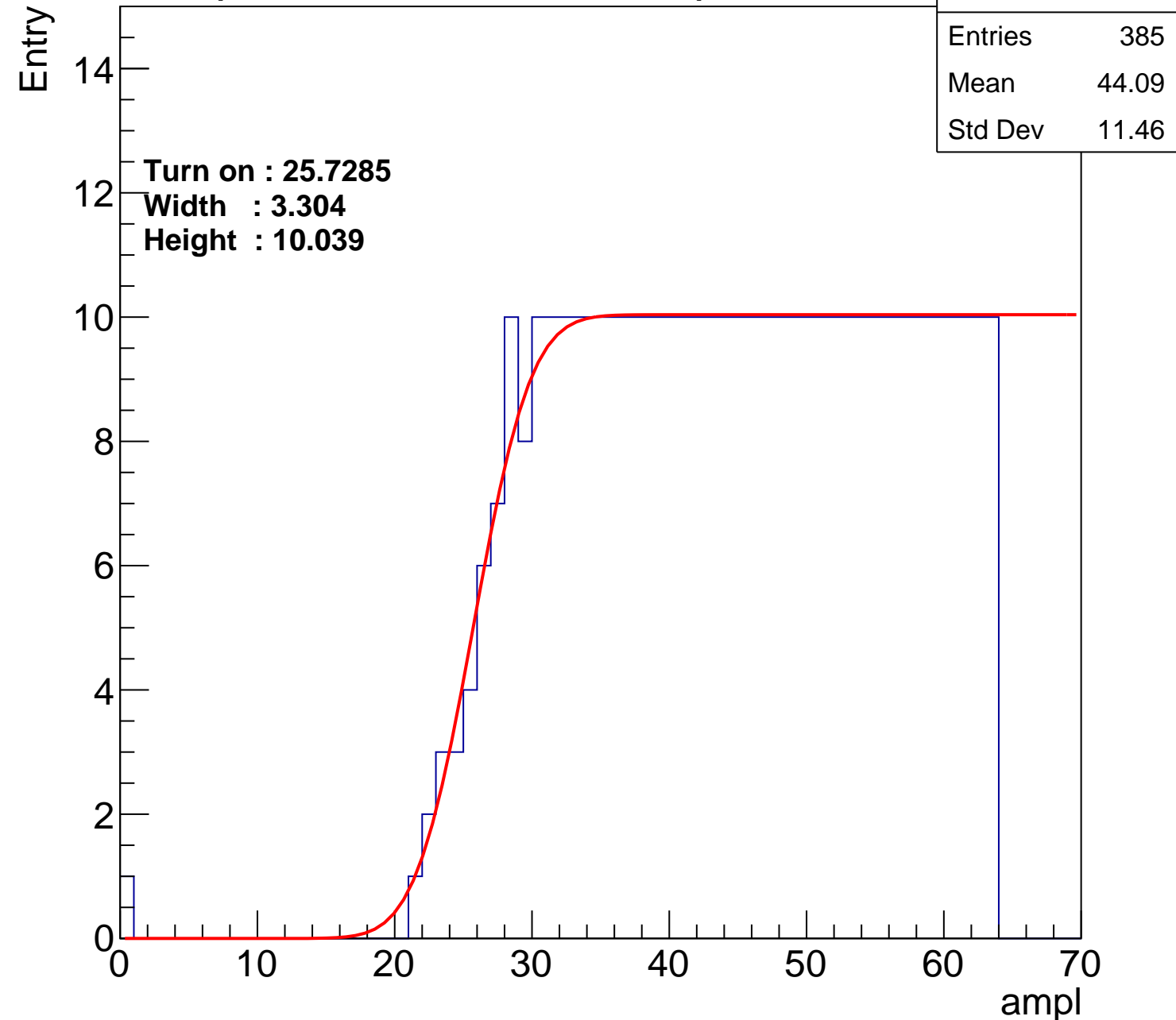
Width : 3.304

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch41

calib_packv5_042523_0143.root, FC#3, port B3

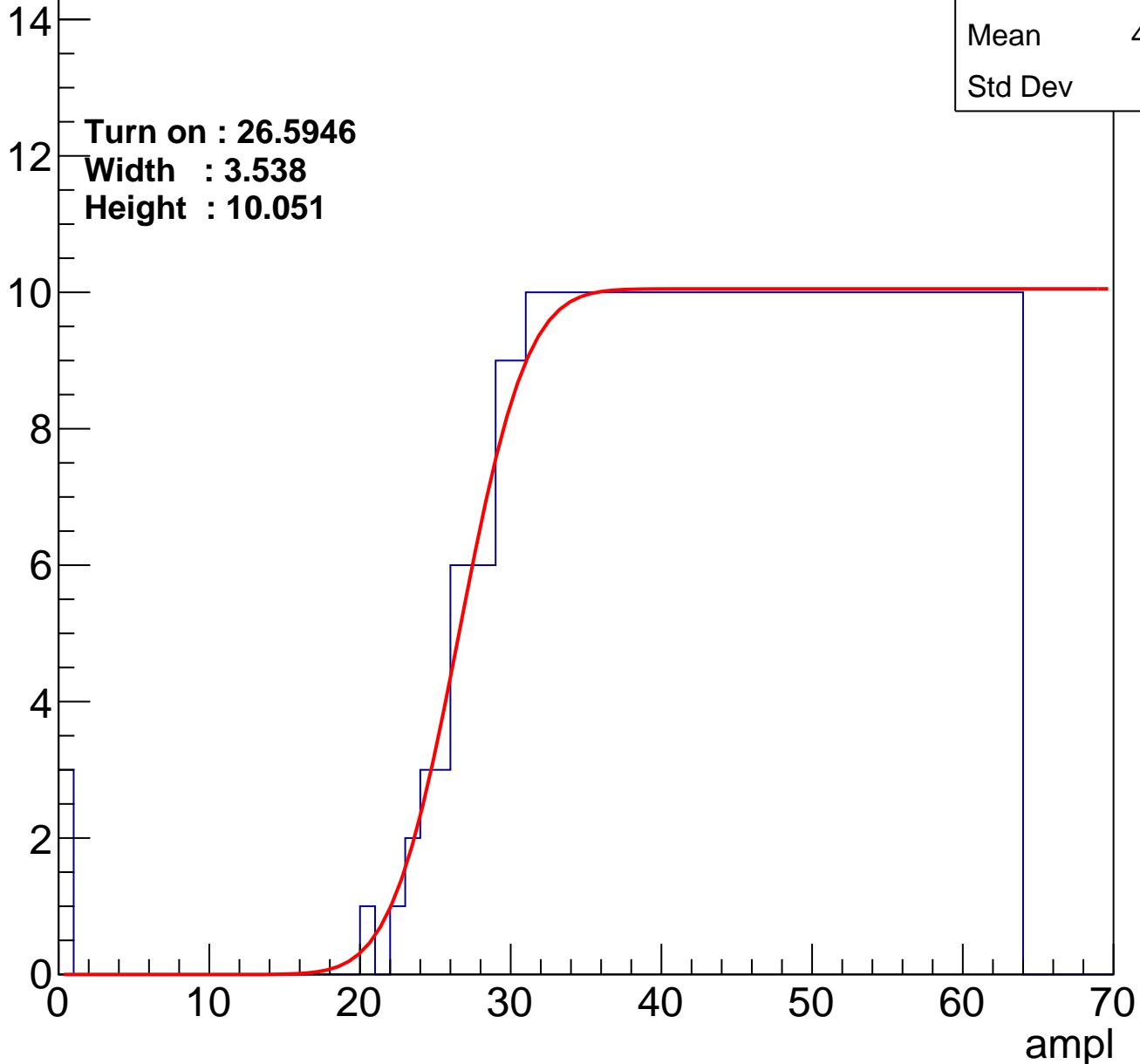
Entries	379
Mean	44.23
Std Dev	11.7

Turn on : 26.5946

Width : 3.538

Height : 10.051

Entry



B1L000S, U17-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.45
Std Dev	11.55

Turn on : 26.6791

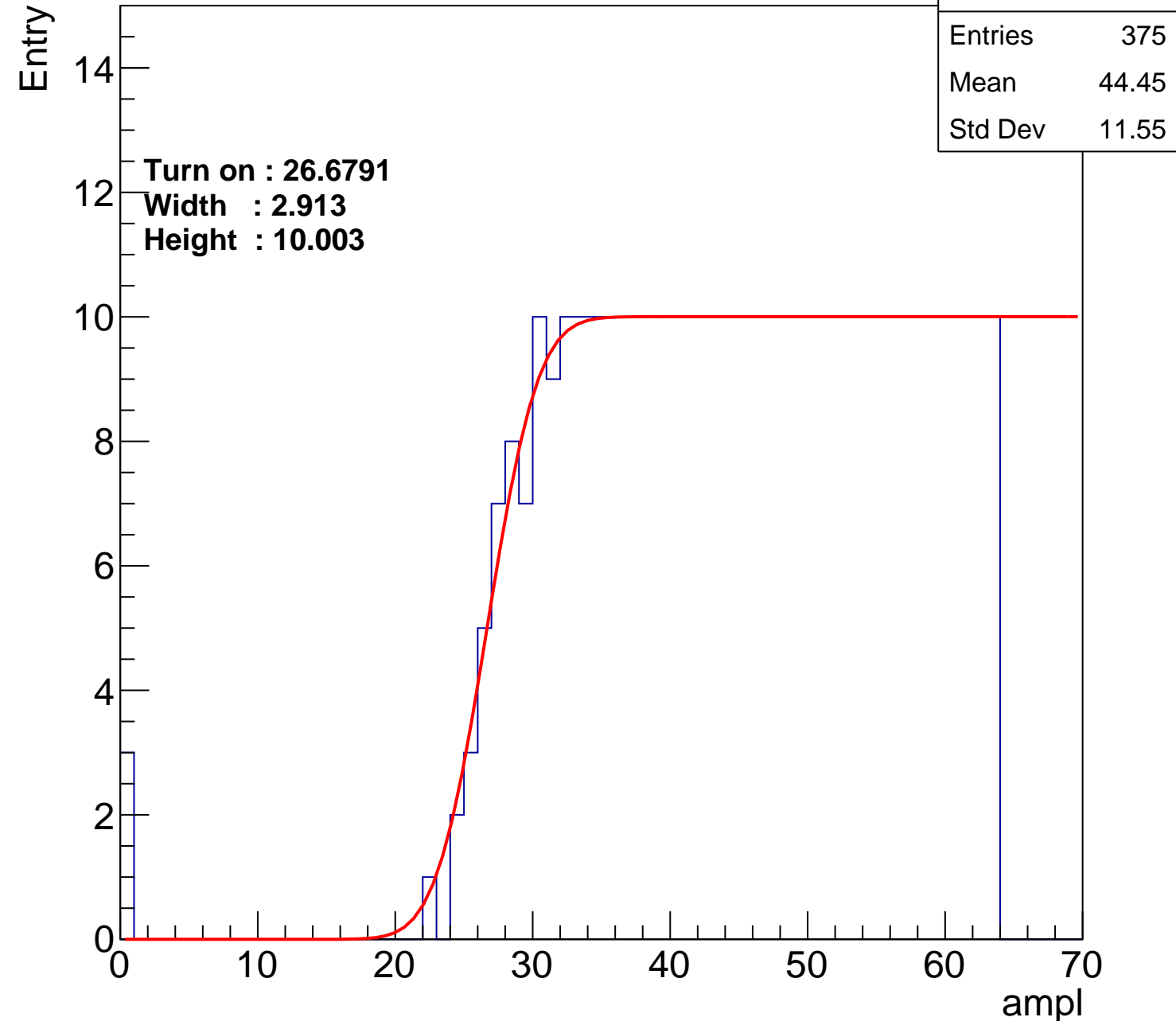
Width : 2.913

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.6447

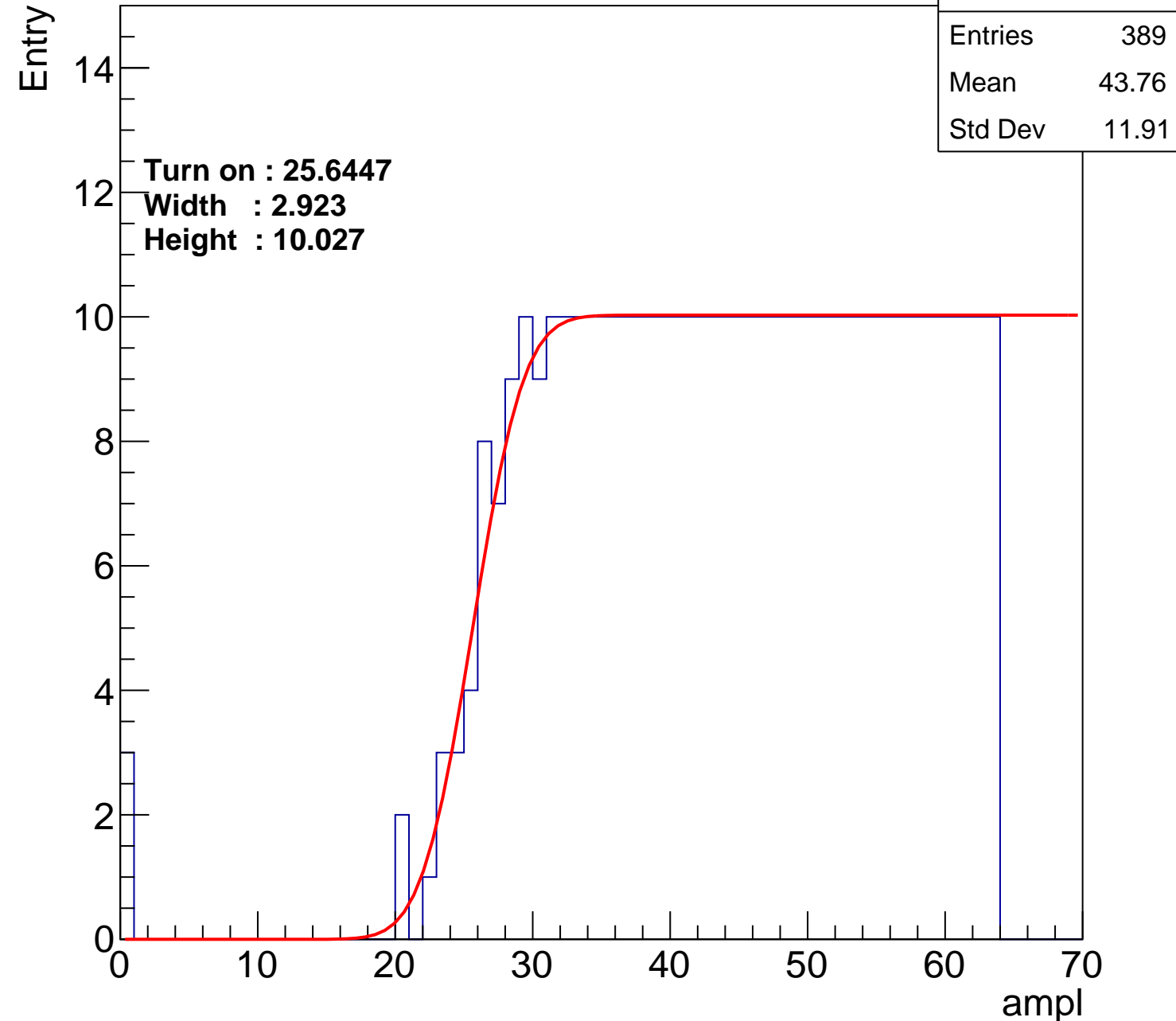
Width : 2.923

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.08
Std Dev	11.16

Turn on : 28.1885

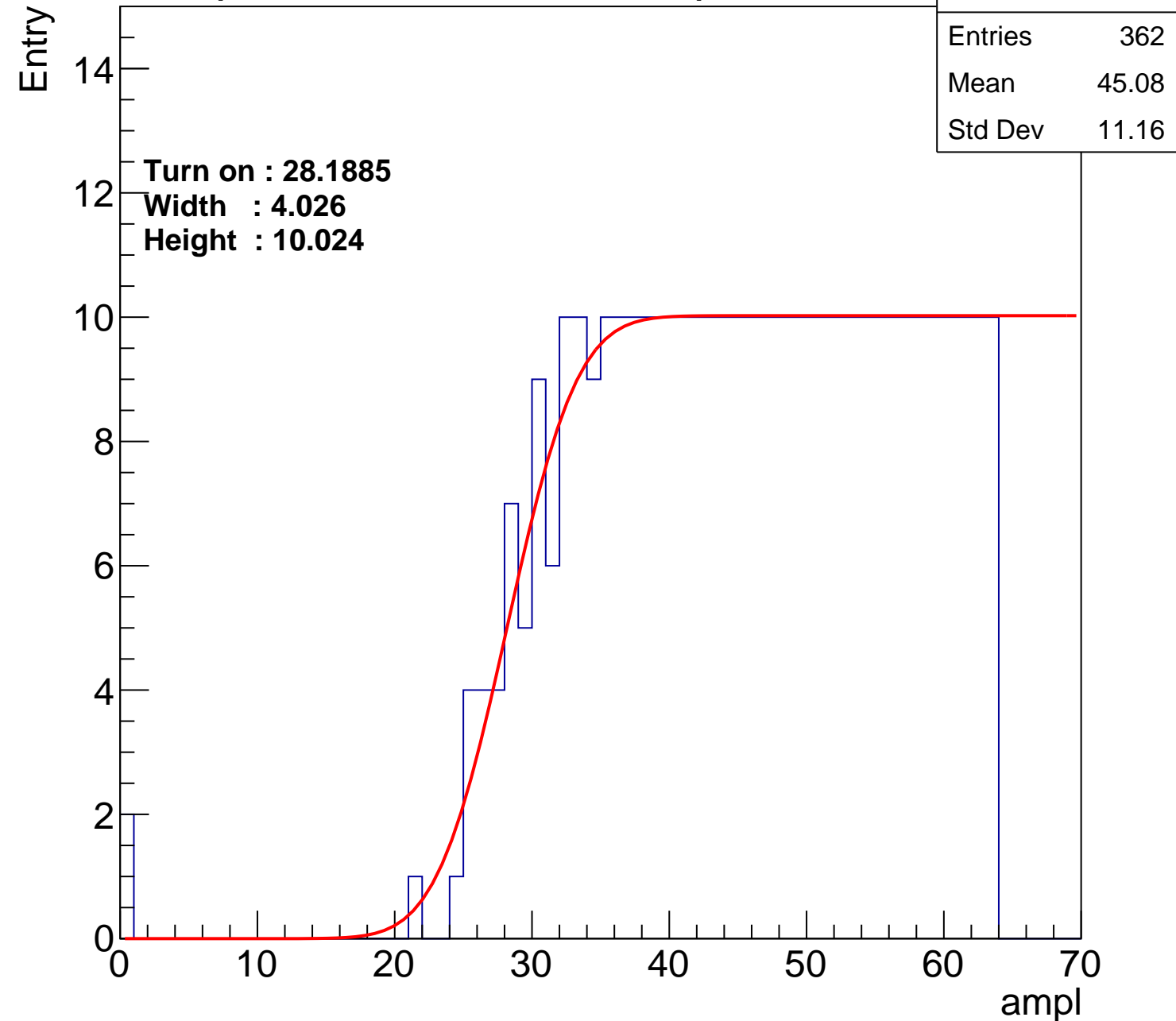
Width : 4.026

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch45

calib_packv5_042523_0143.root, FC#3, port B3

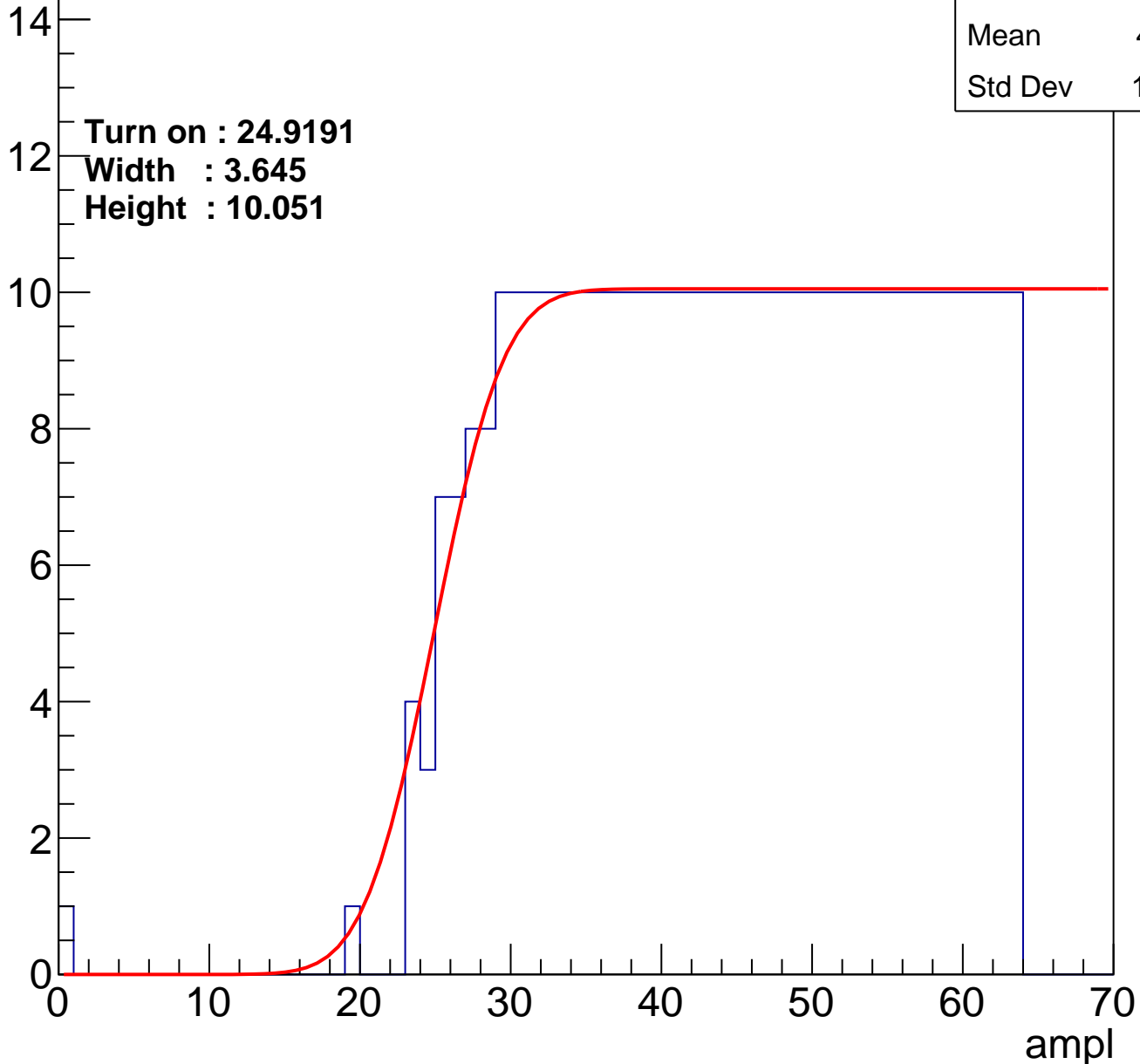
Entries	389
Mean	43.91
Std Dev	11.54

Turn on : 24.9191

Width : 3.645

Height : 10.051

Entry



B1L000S, U17-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.4714

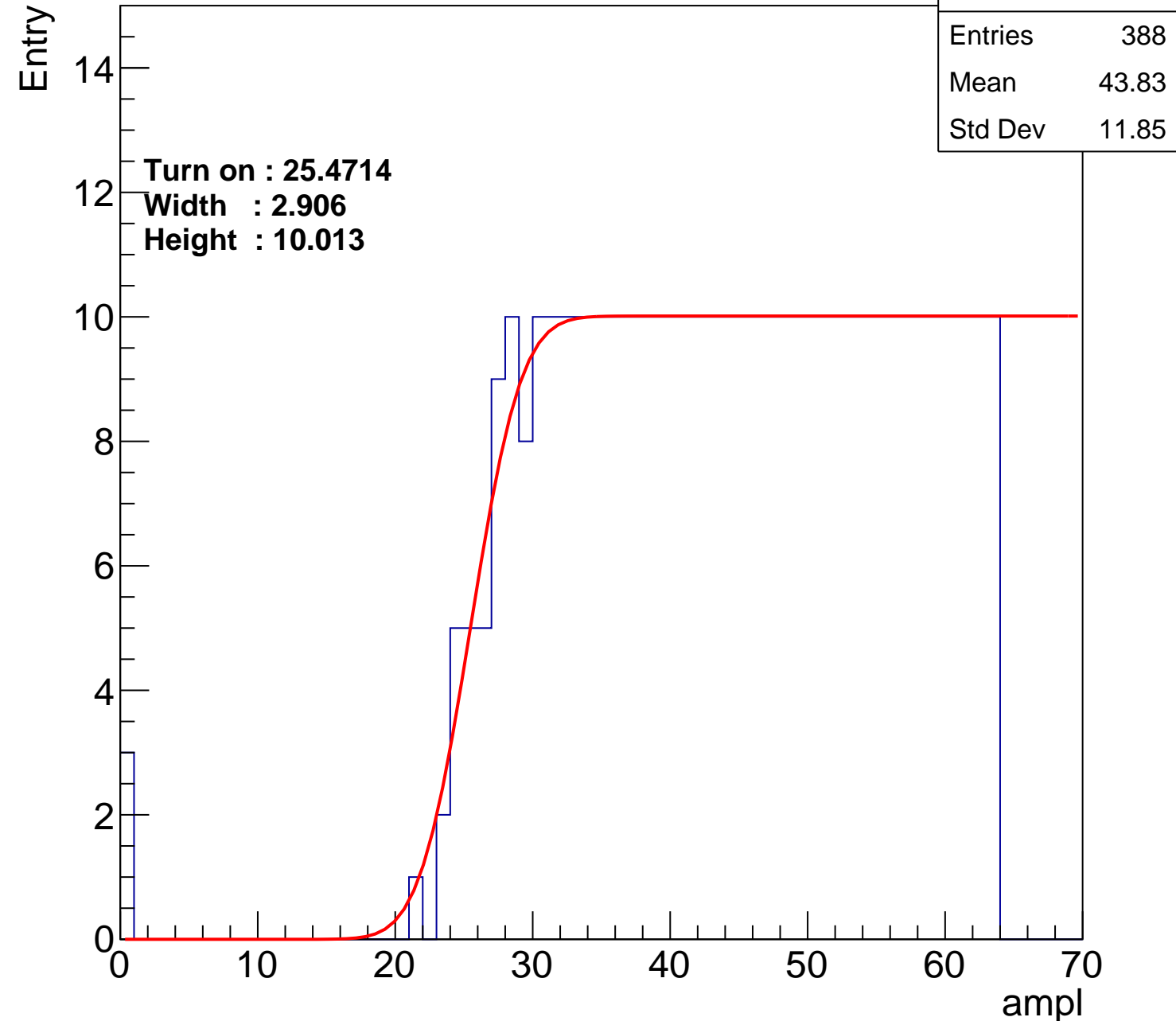
Width : 2.906

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 25.7325

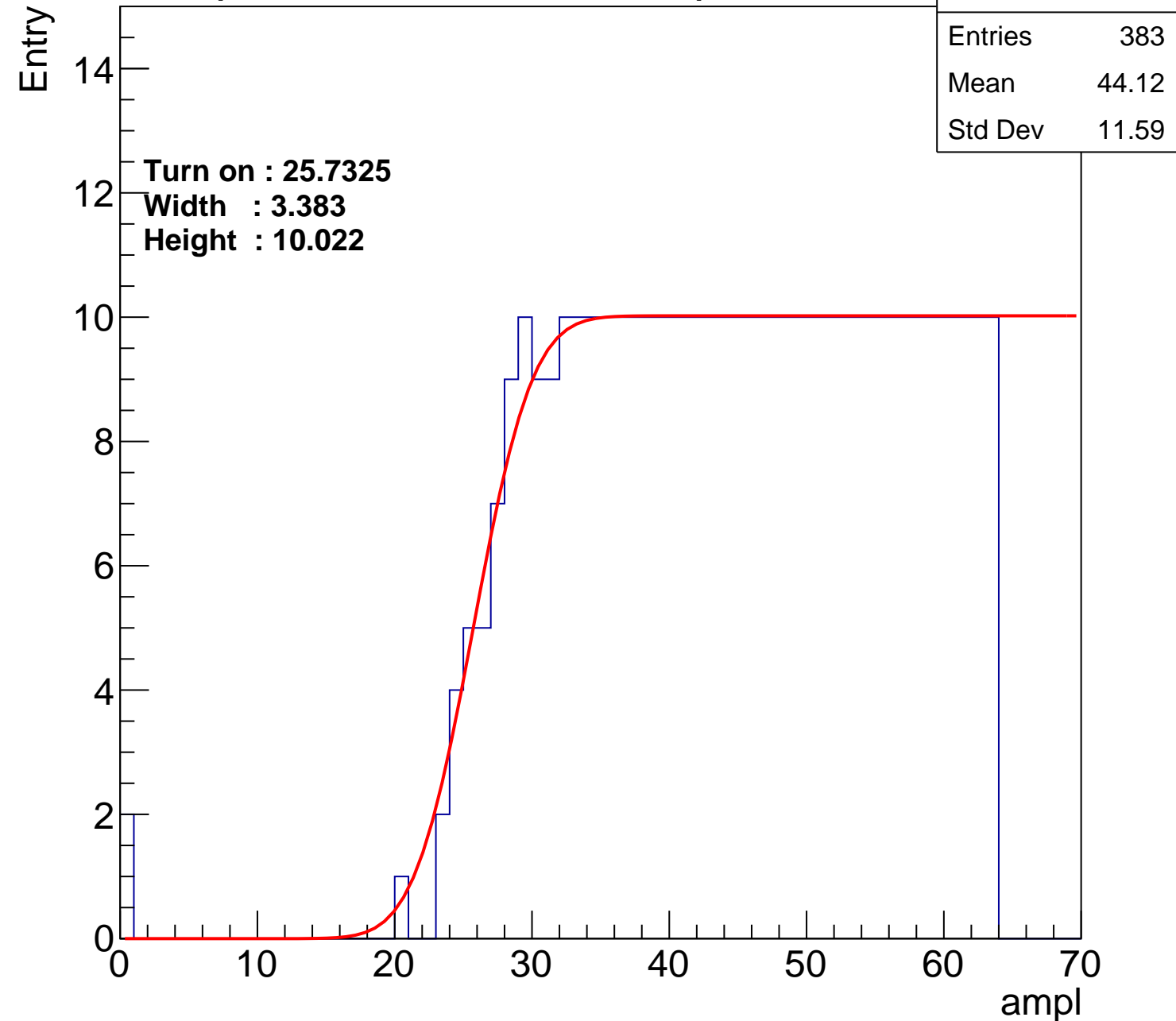
Width : 3.383

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch48

calib_packv5_042523_0143.root, FC#3, port B3

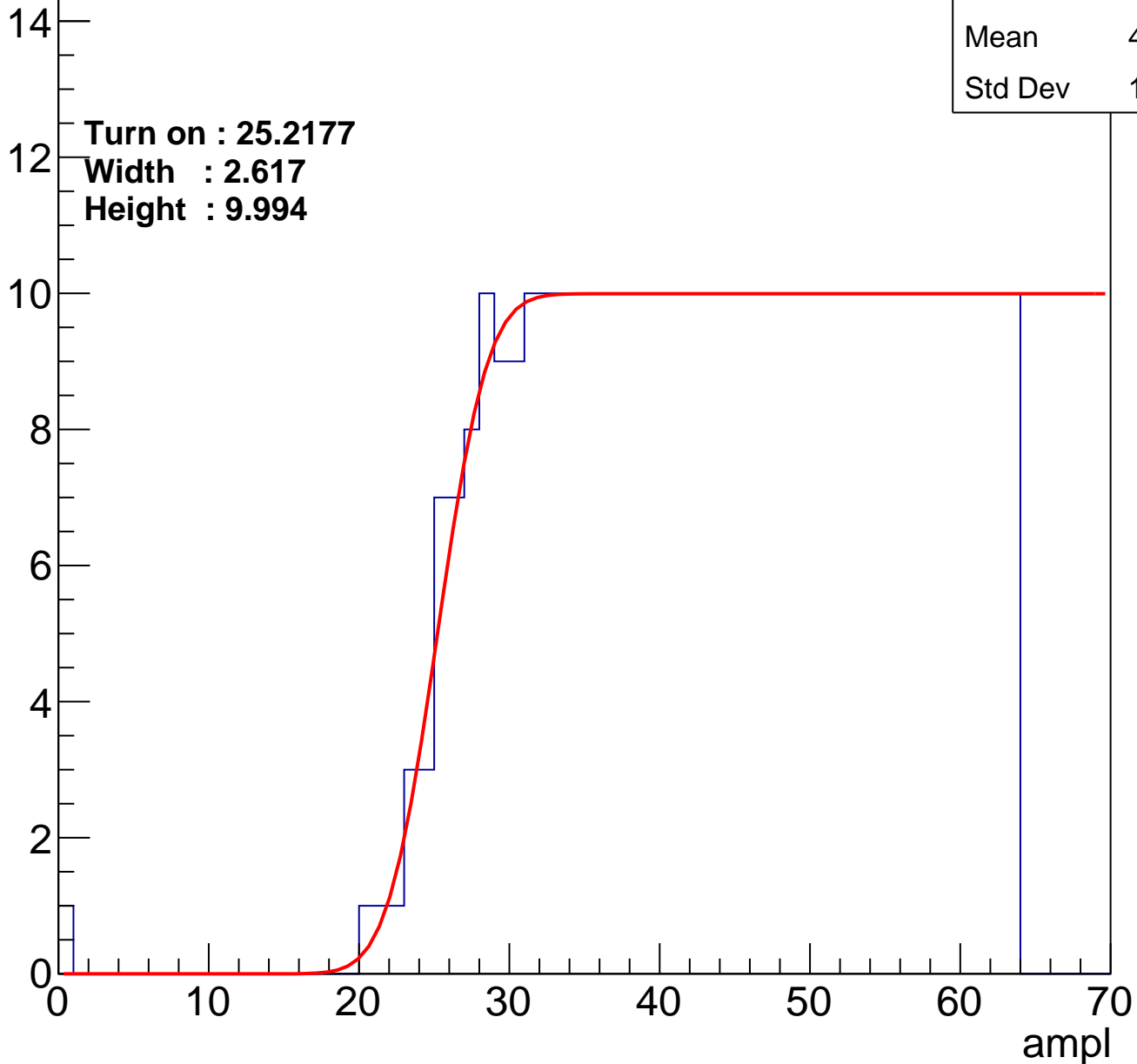
Entries	390
Mean	43.84
Std Dev	11.59

Turn on : 25.2177

Width : 2.617

Height : 9.994

Entry



B1L000S, U17-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.07
Std Dev	11.07

Turn on : 28.3133

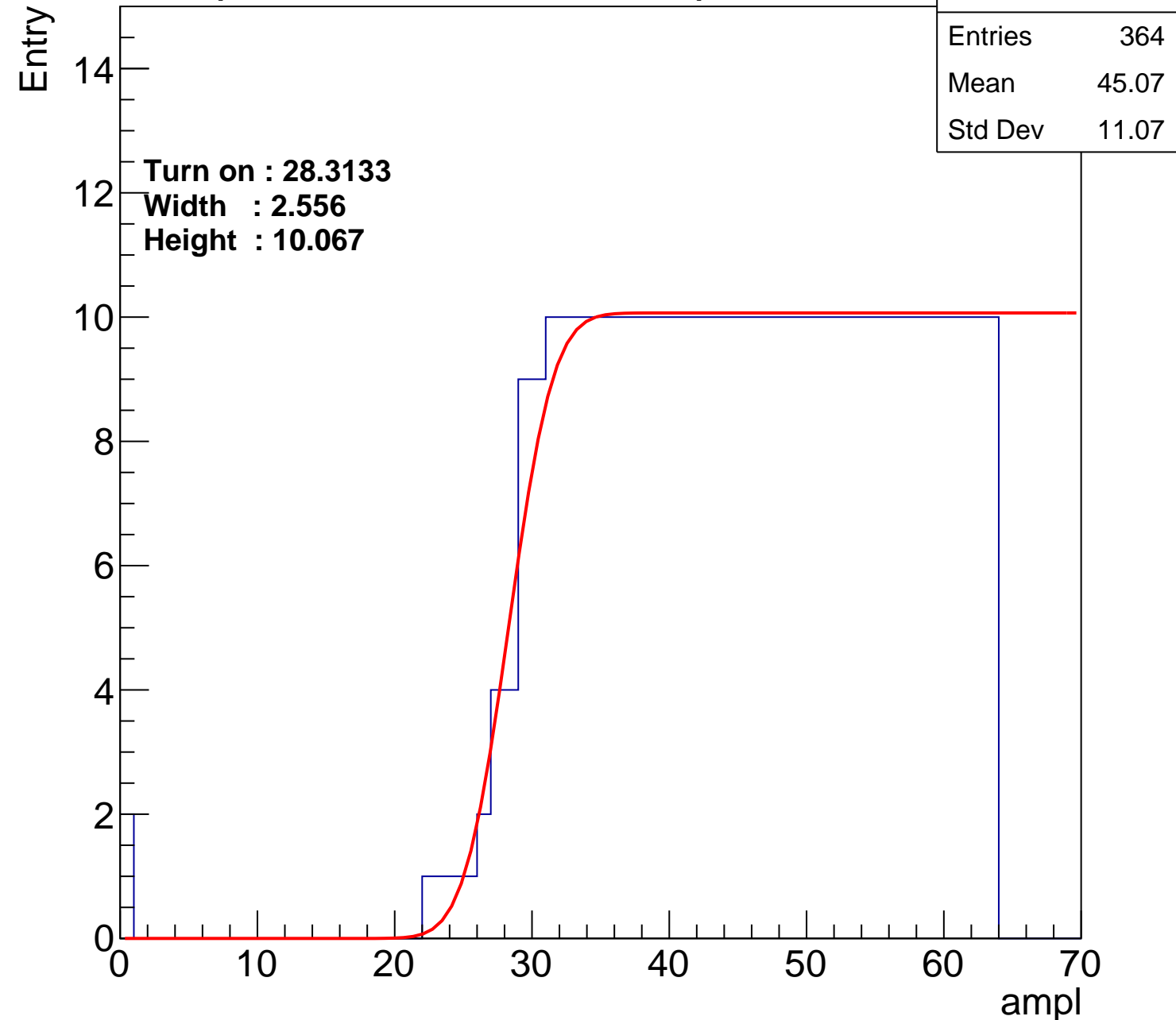
Width : 2.556

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.6
Std Dev	11.78

Turn on : 25.0129

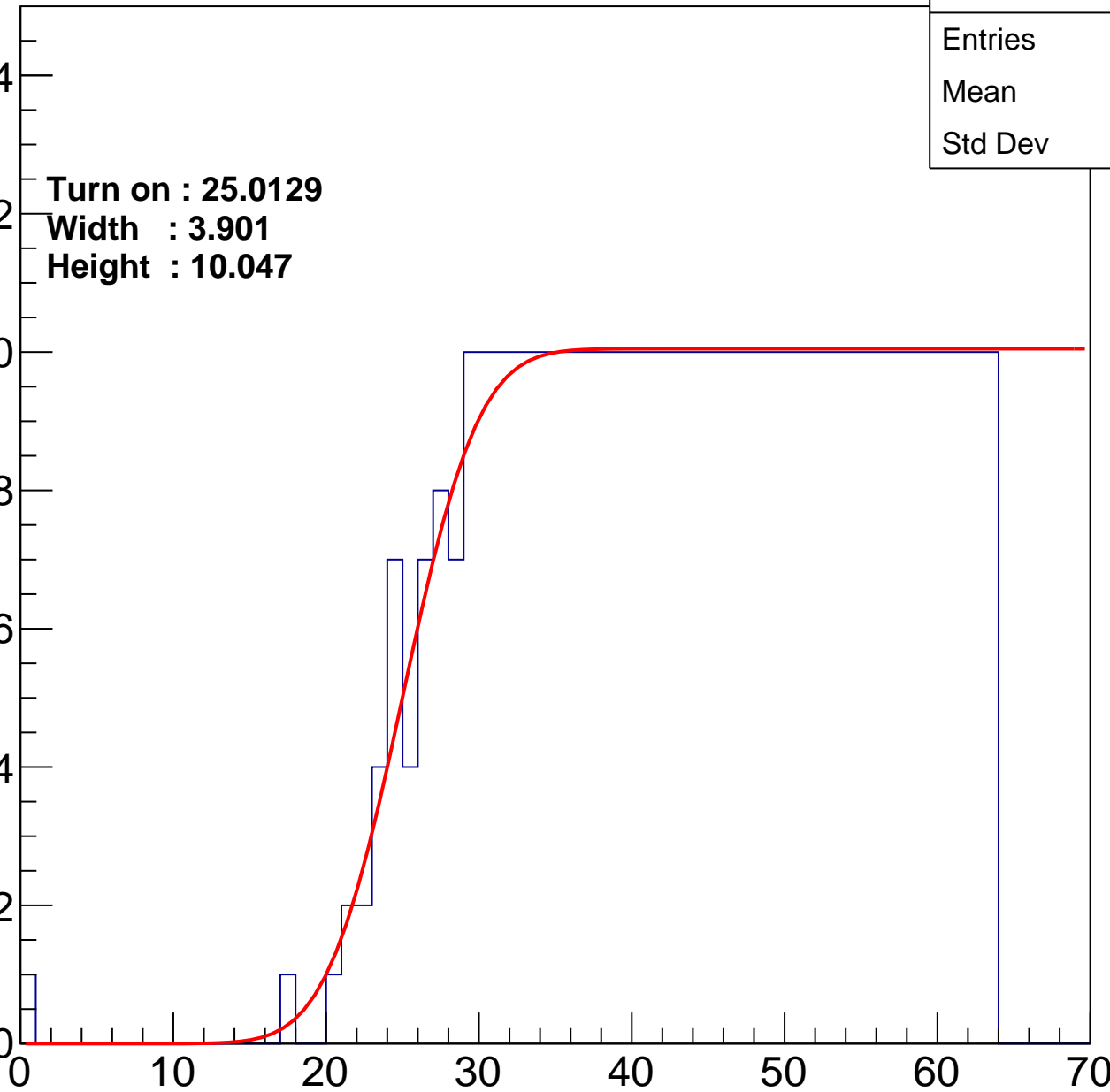
Width : 3.901

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 26.7888

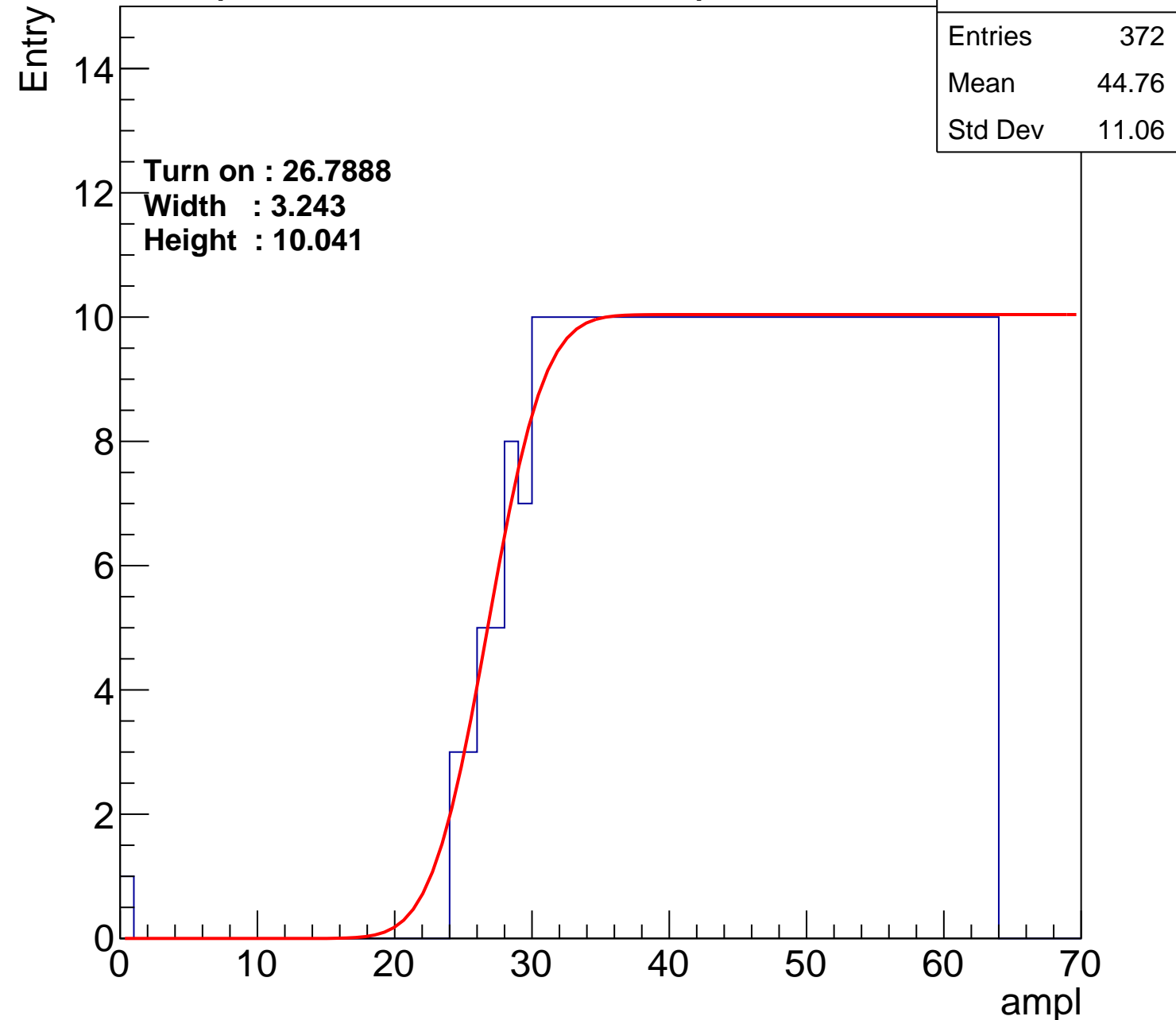
Width : 3.243

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.66
Std Dev	11.79

Turn on : 25.0231

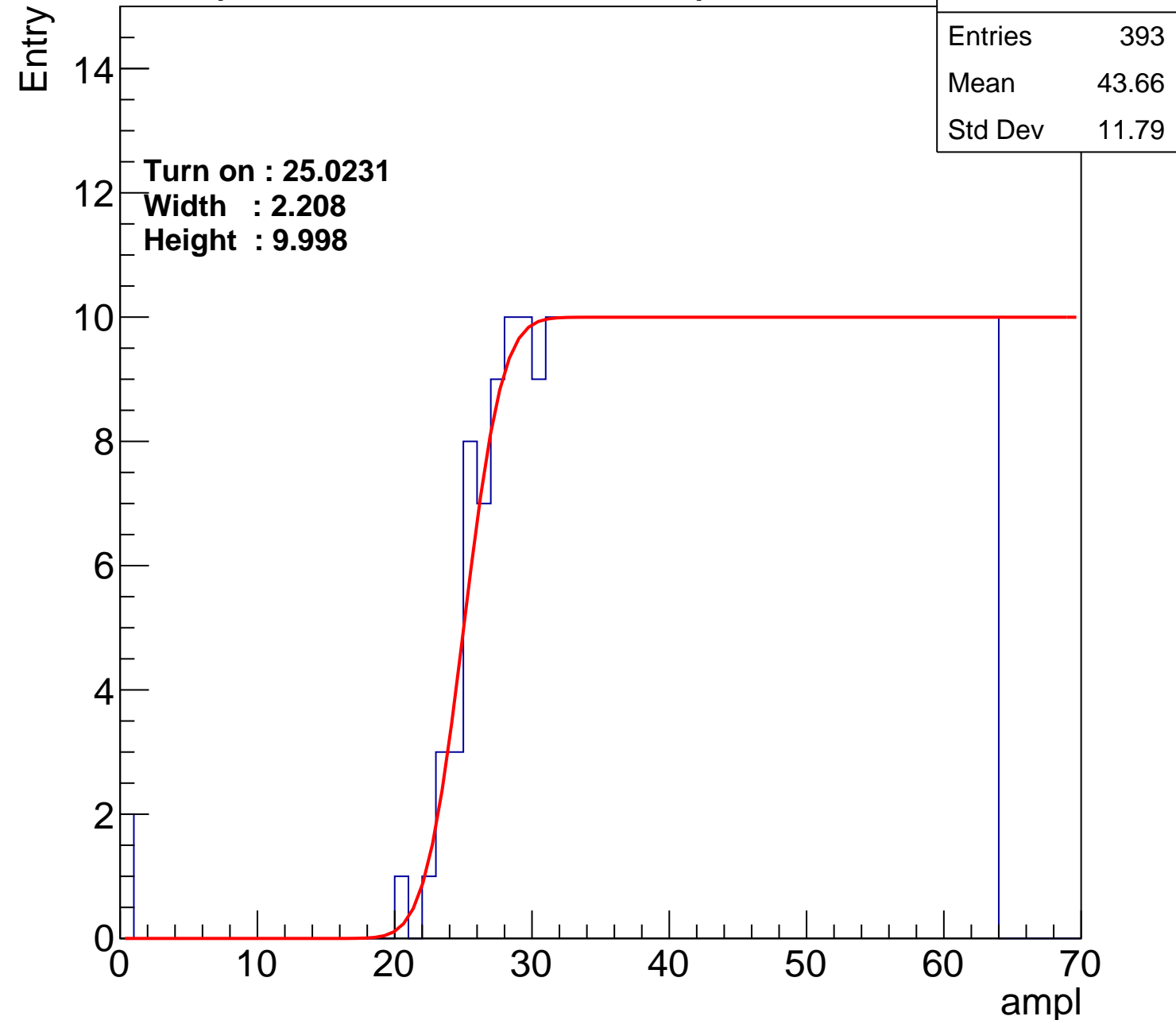
Width : 2.208

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.7746

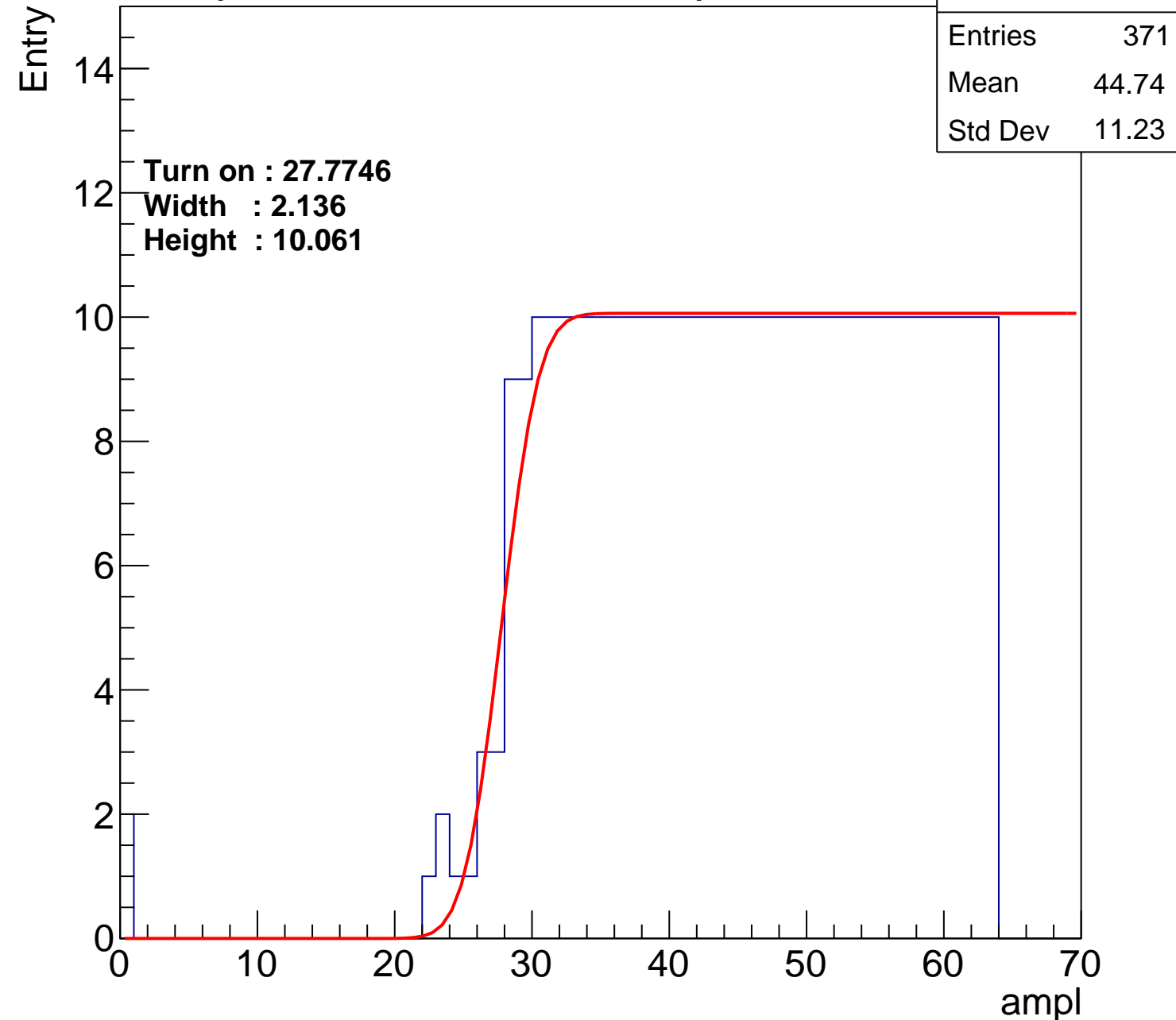
Width : 2.136

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.33
Std Dev	12.23

Turn on : 24.4643

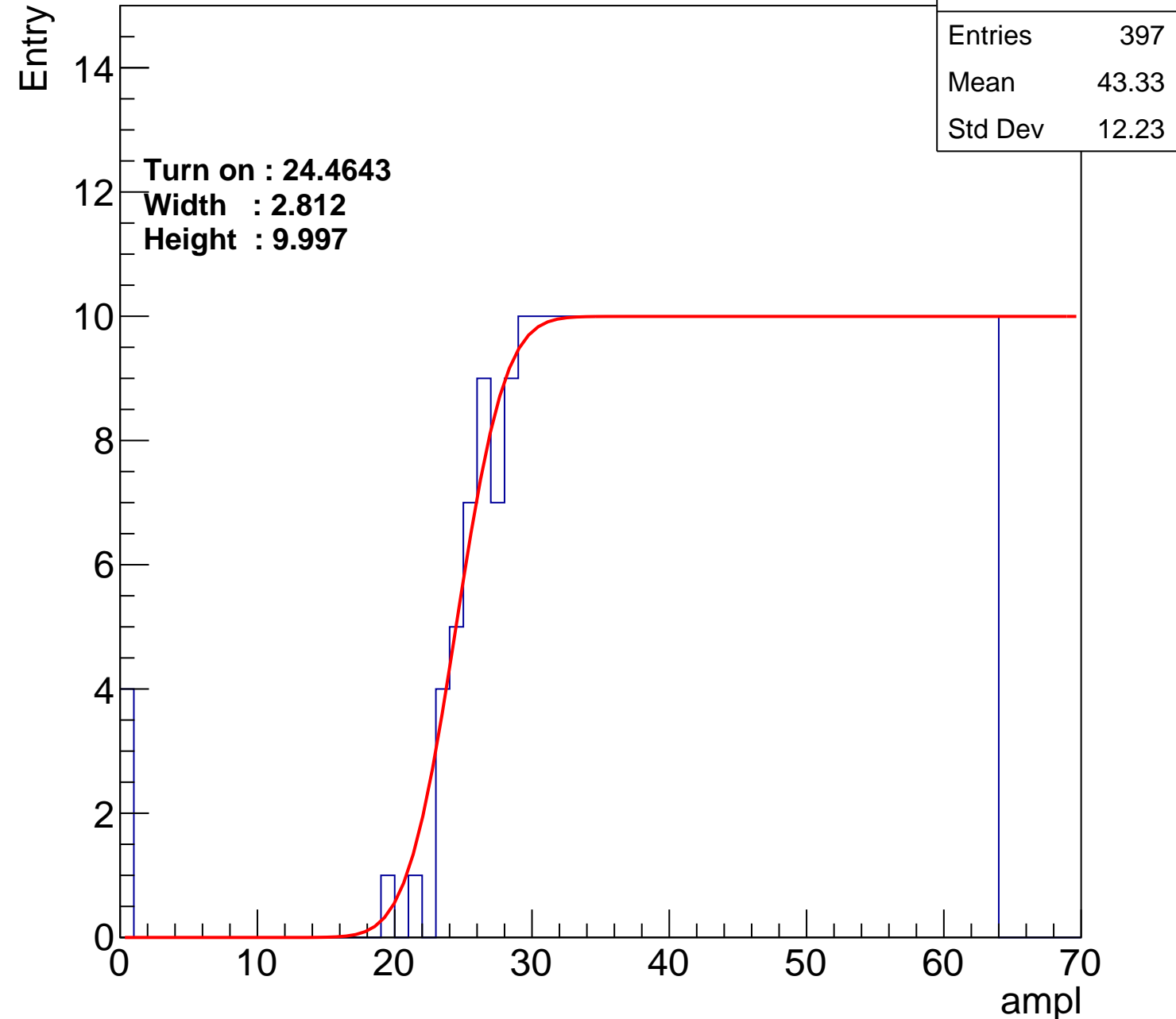
Width : 2.812

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 25.8128

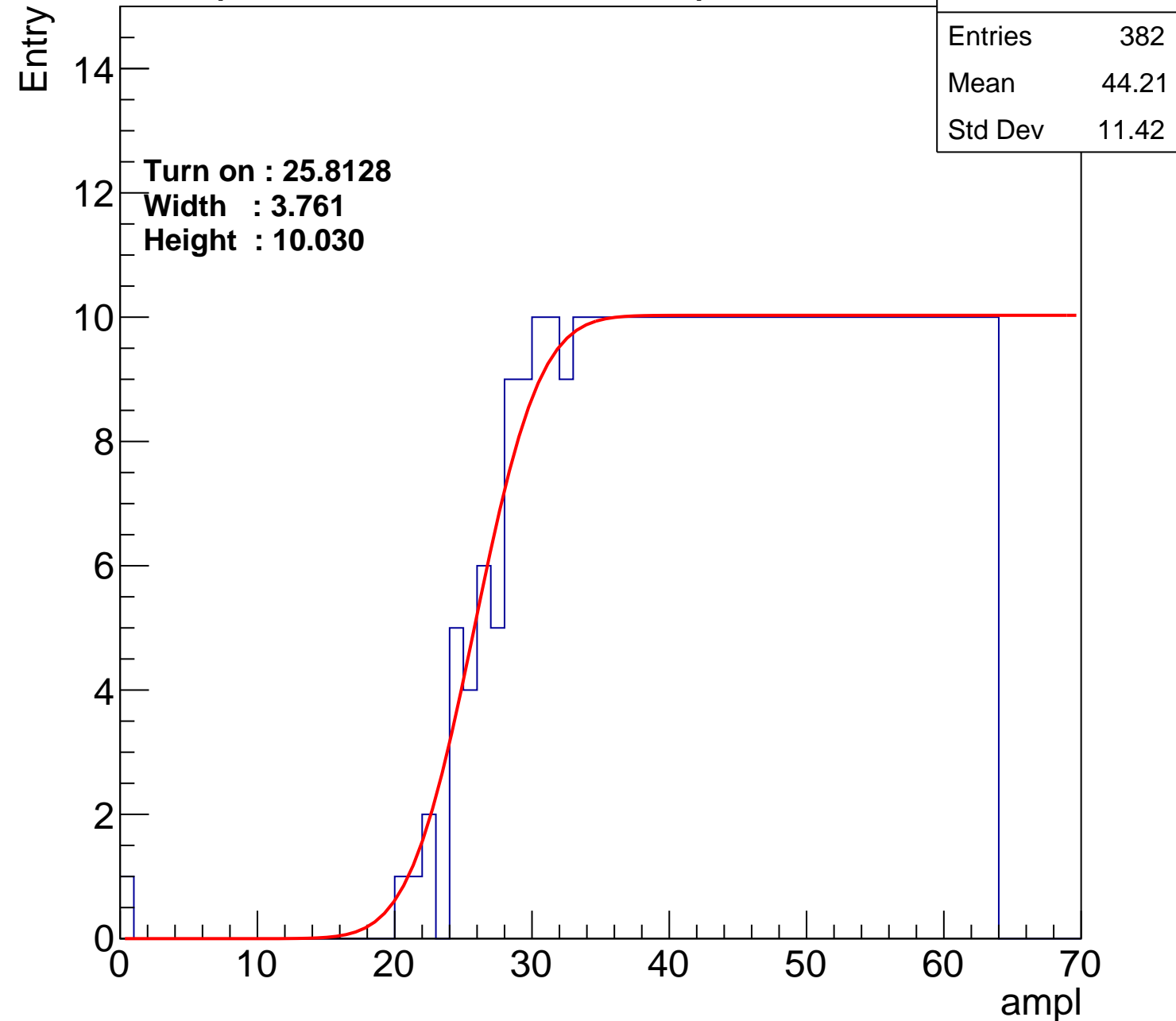
Width : 3.761

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.6121

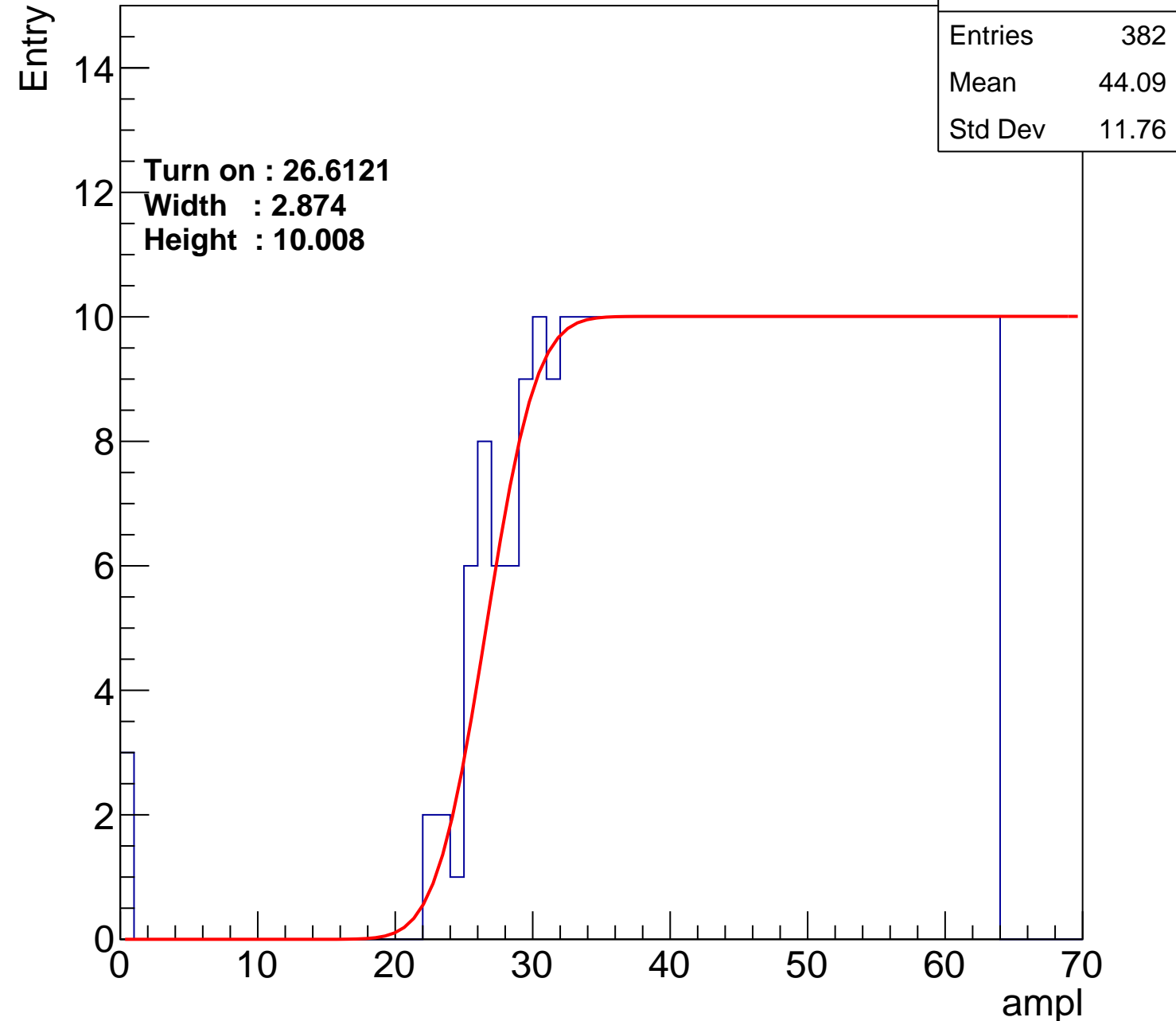
Width : 2.874

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.3056

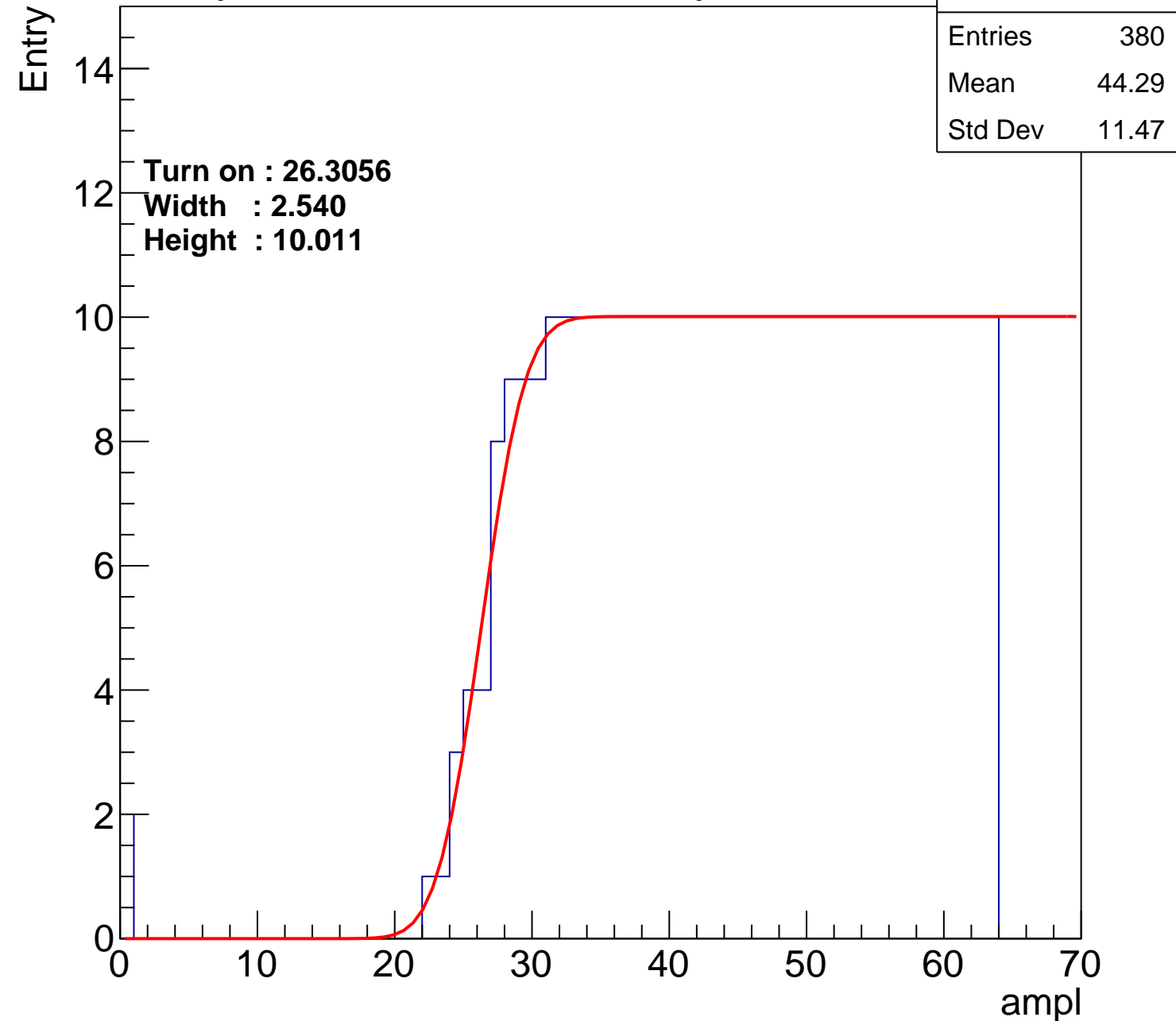
Width : 2.540

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.99
Std Dev	11.64

Turn on : 25.5981

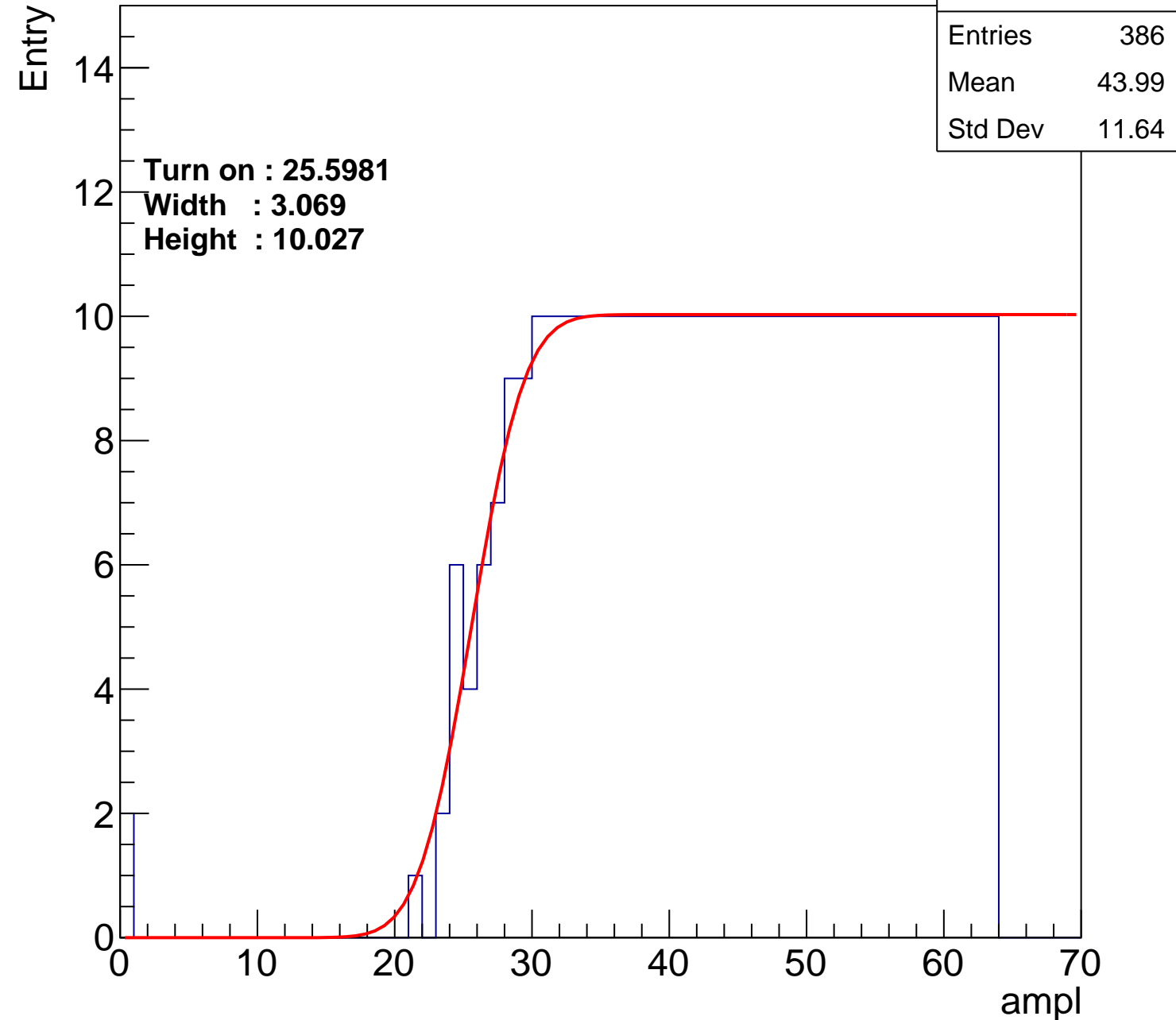
Width : 3.069

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.01
Std Dev	11.77

Turn on : 25.7778

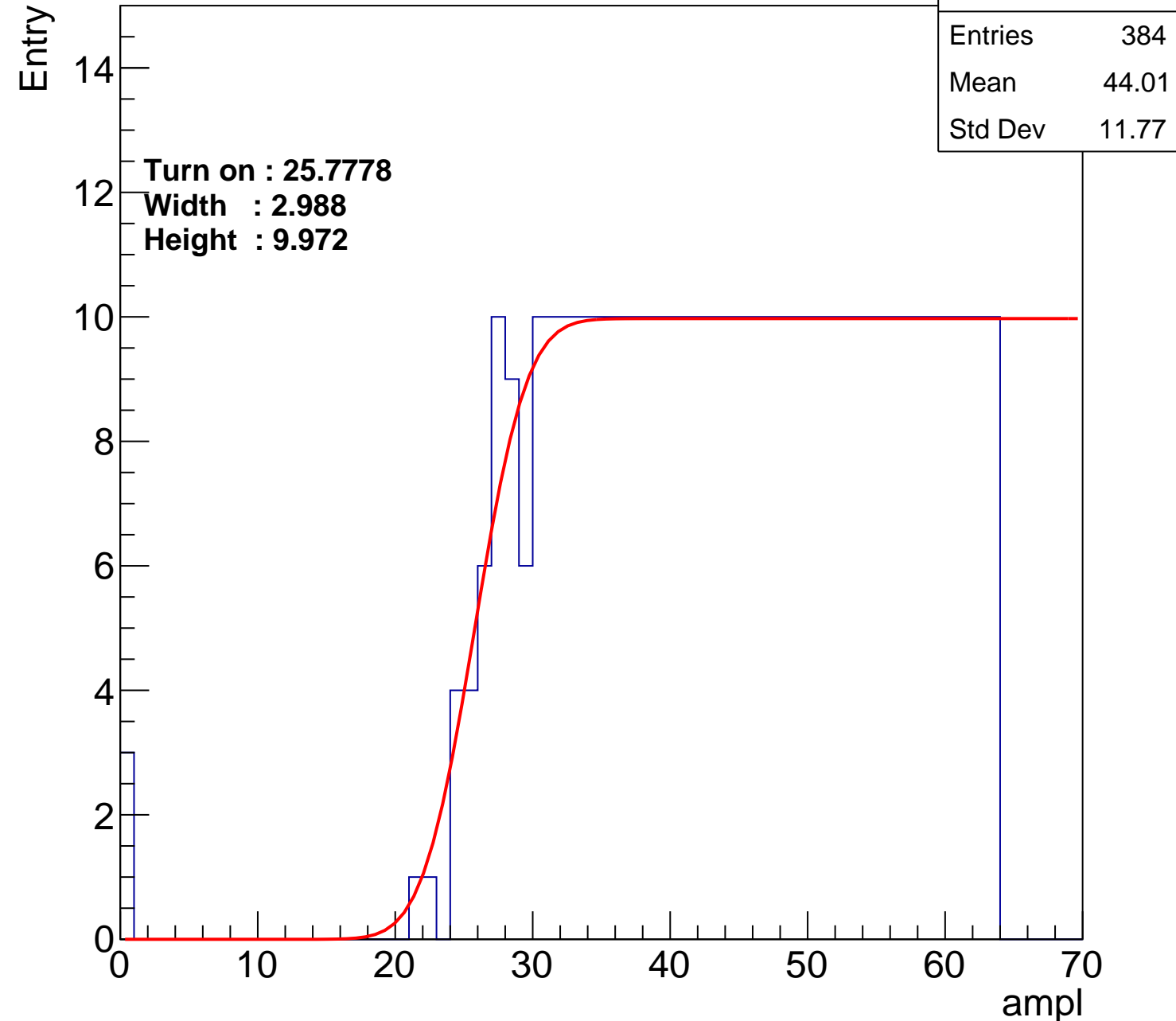
Width : 2.988

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch60

calib_packv5_042523_0143.root, FC#3, port B3

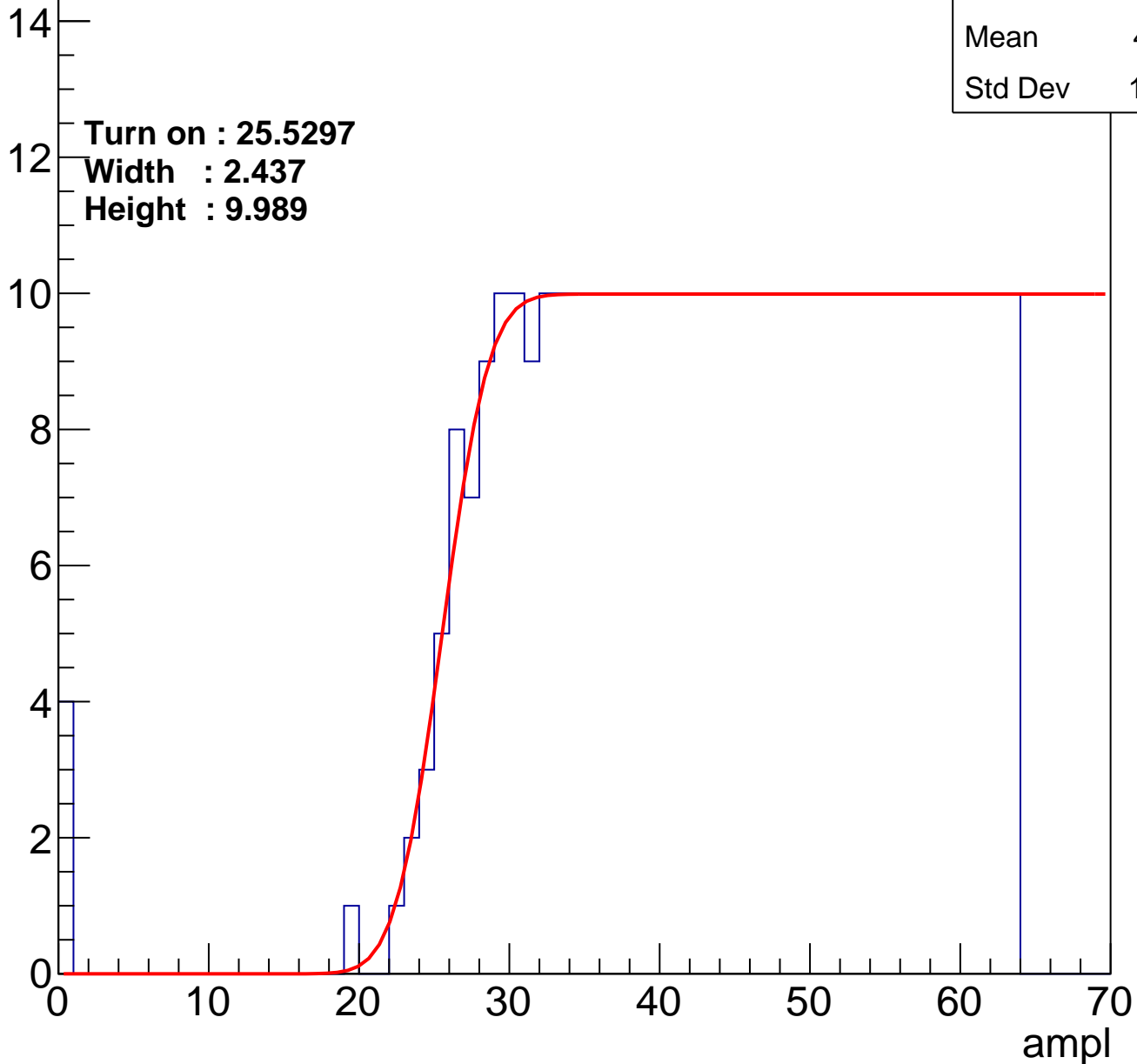
Entries	389
Mean	43.71
Std Dev	12.06

Turn on : 25.5297

Width : 2.437

Height : 9.989

Entry



B1L000S, U17-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.01
Std Dev	10.98

Turn on : 27.9613

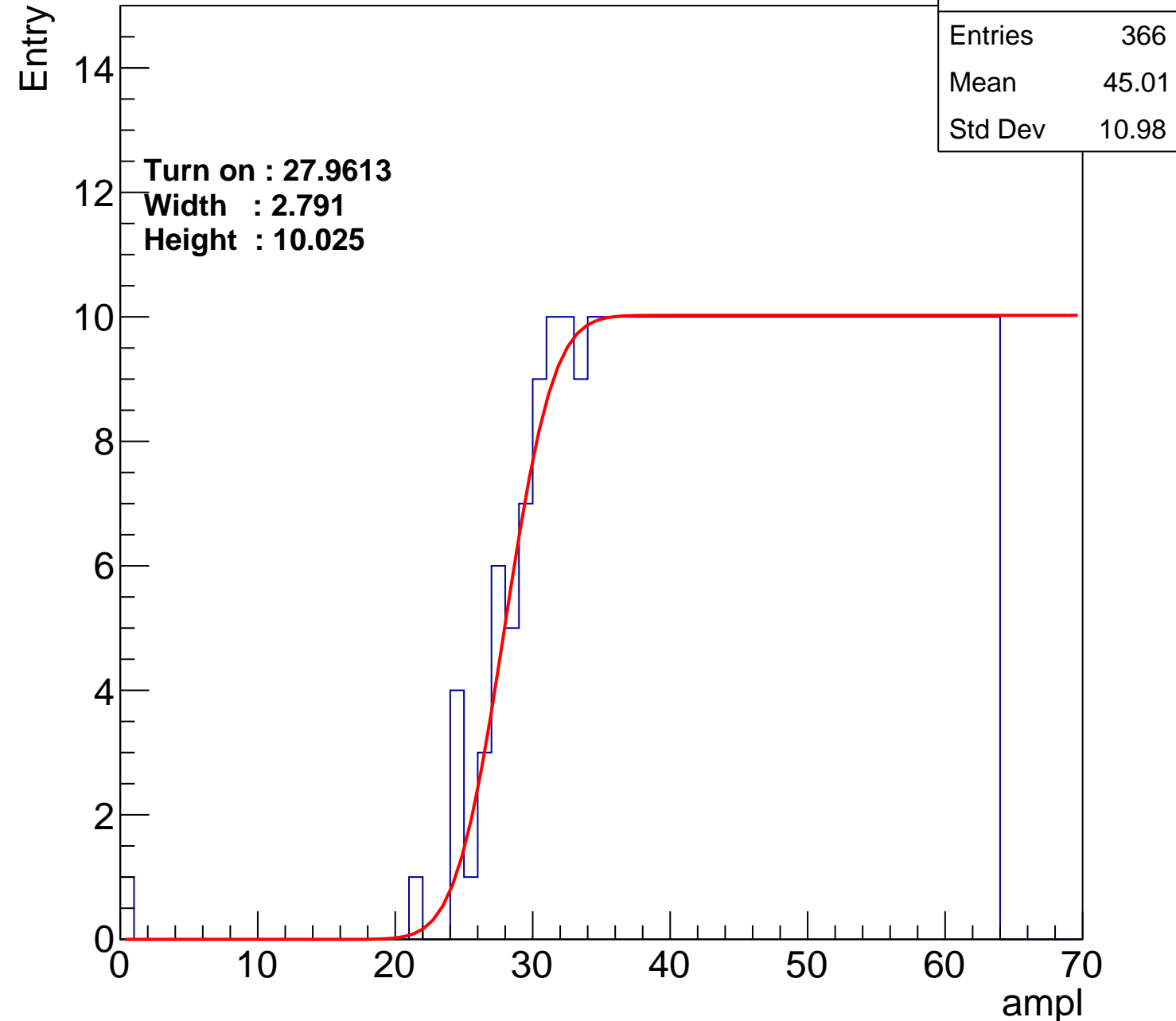
Width : 2.791

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.65
Std Dev	11.18

Turn on : 26.7346

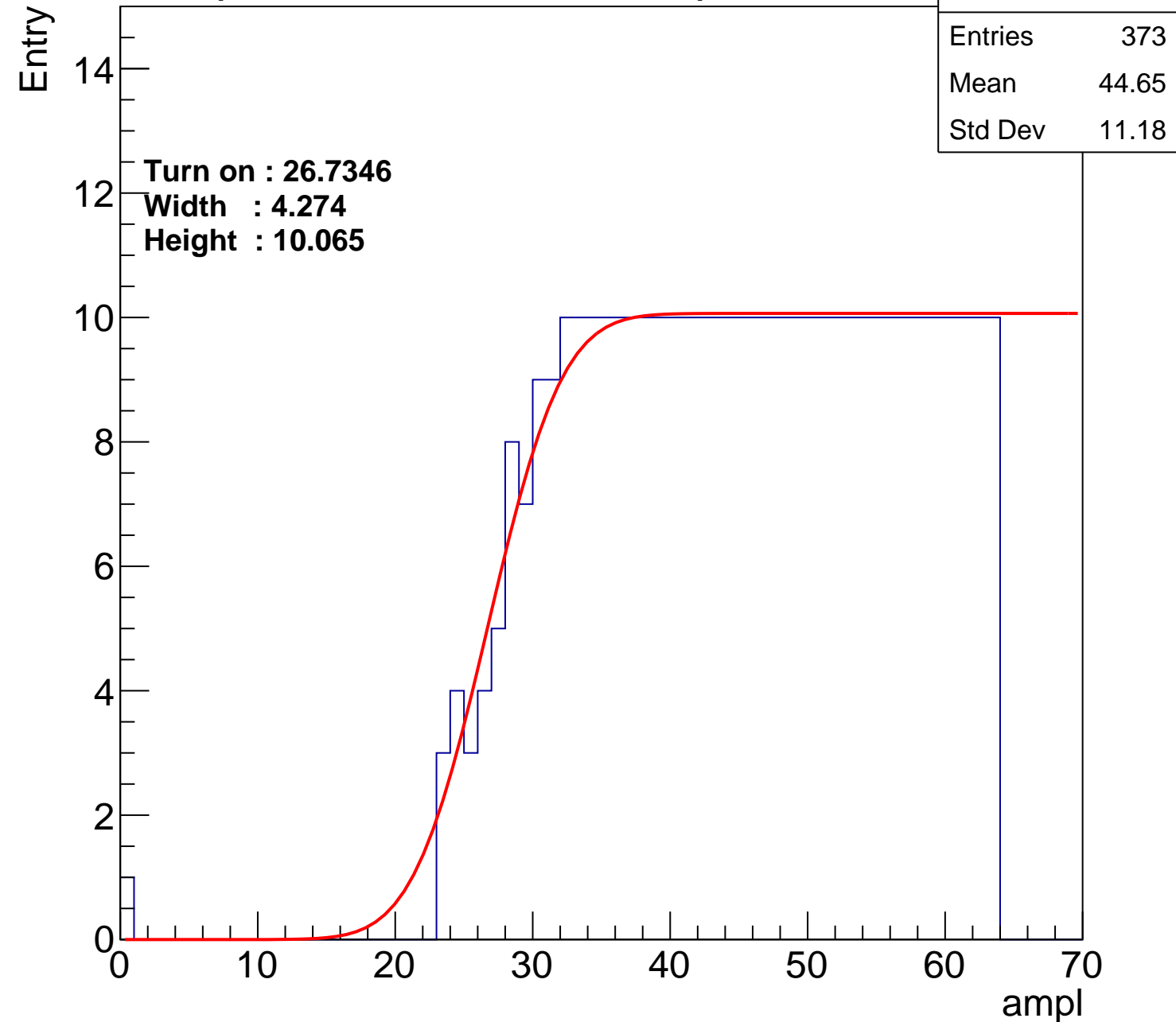
Width : 4.274

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	410
Mean	42.74
Std Dev	12.42

Turn on : 23.7937

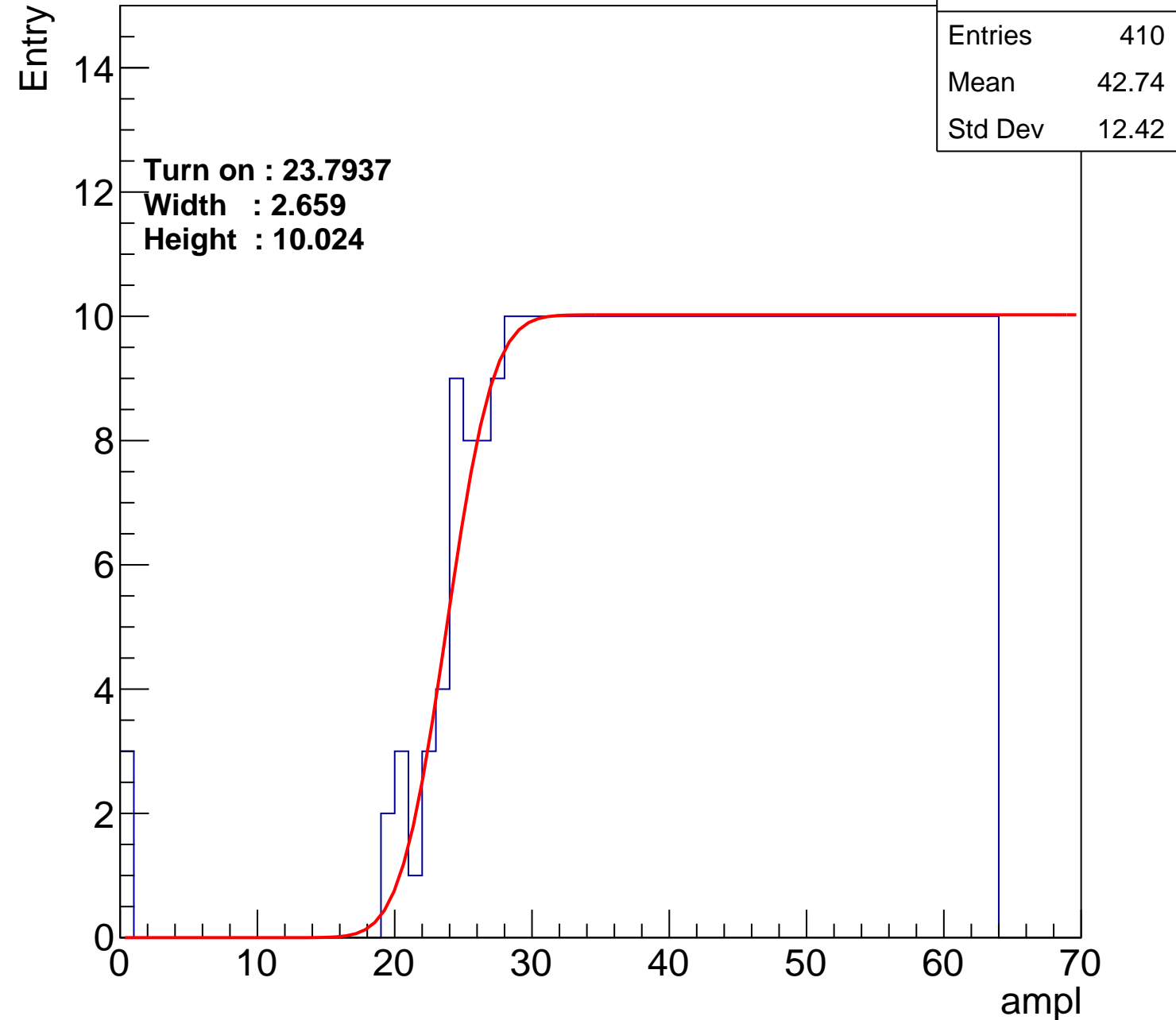
Width : 2.659

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.92
Std Dev	11.86

Turn on : 26.2089

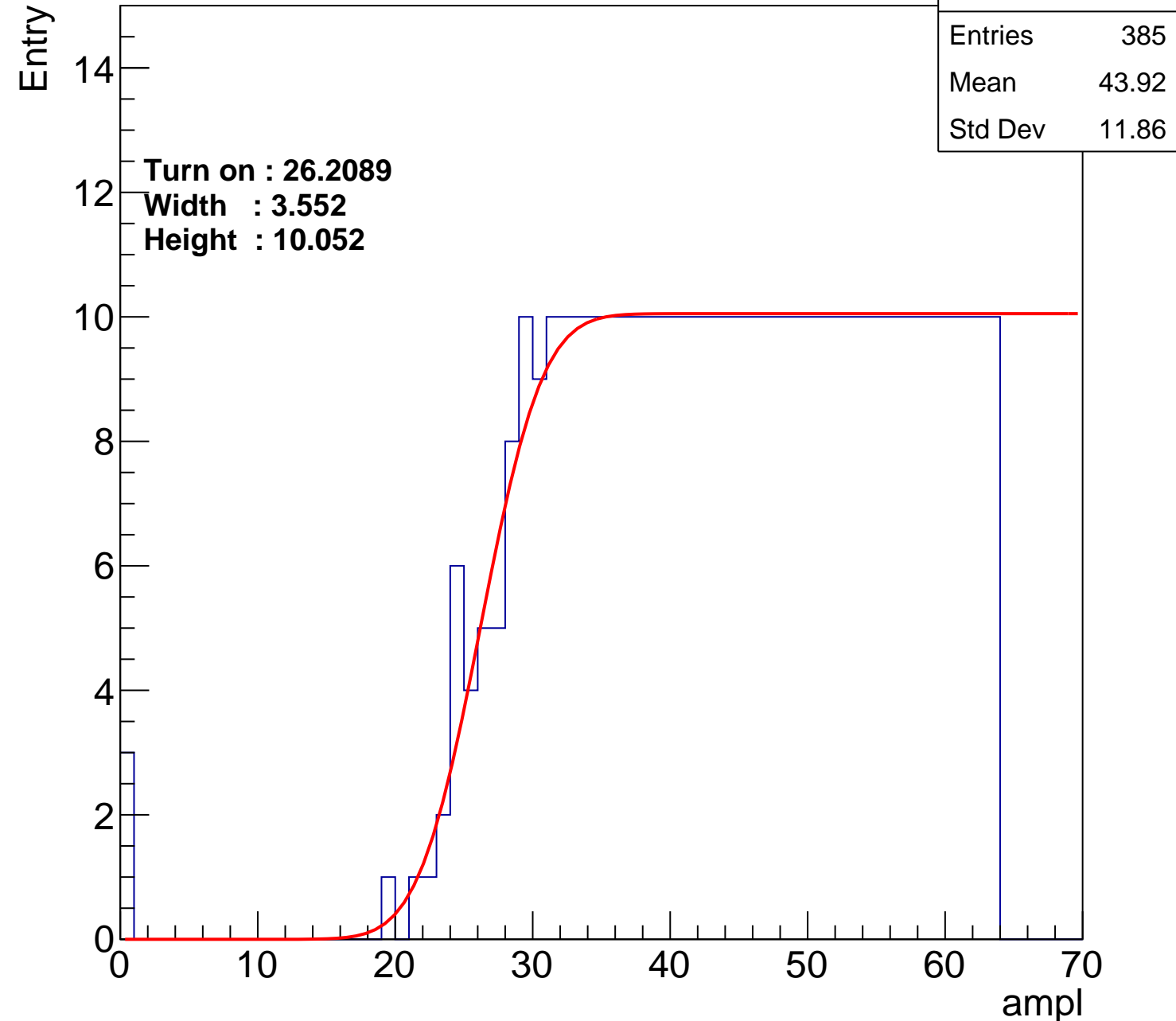
Width : 3.552

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.3737

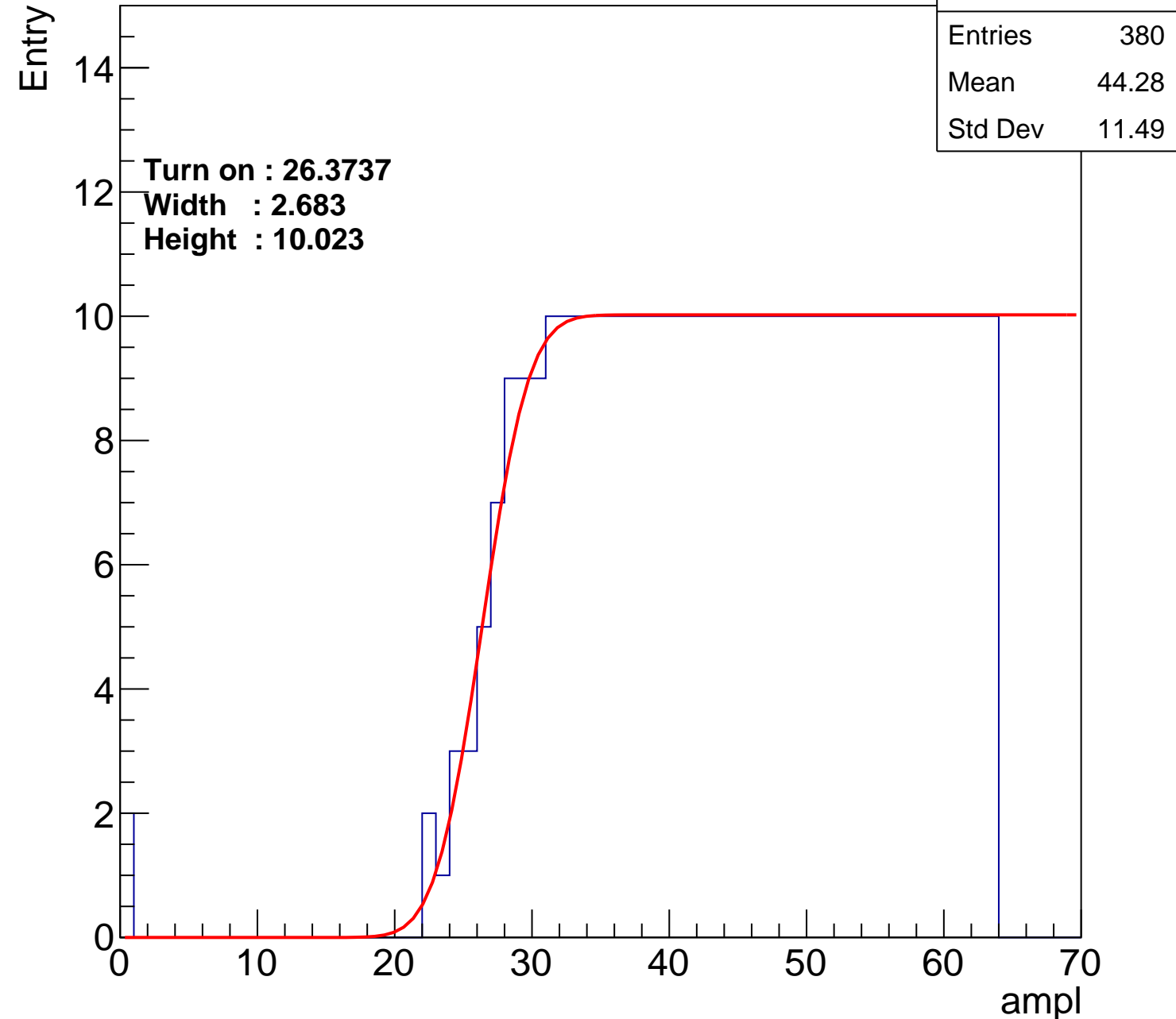
Width : 2.683

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.12
Std Dev	11.88

Turn on : 26.7090

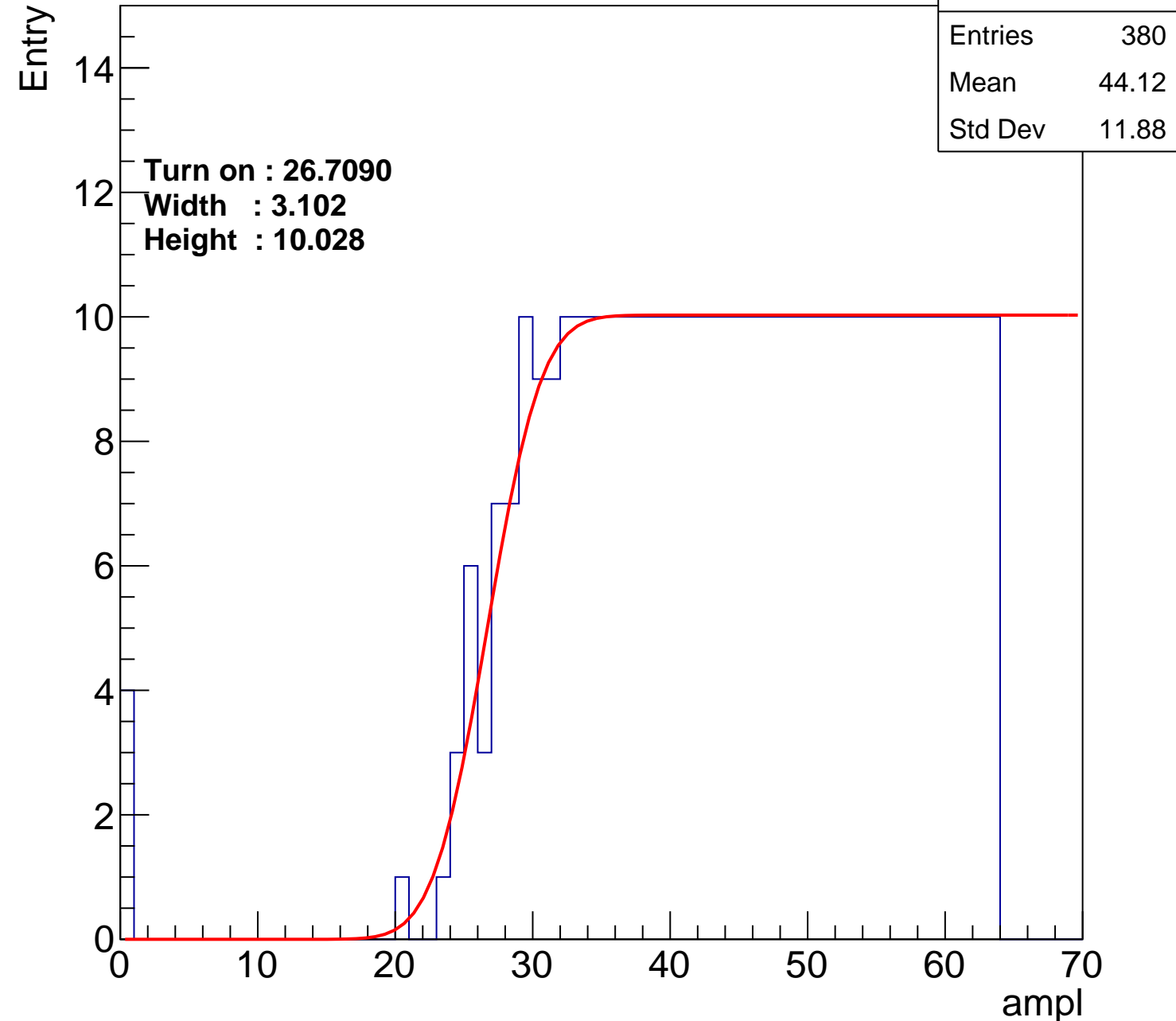
Width : 3.102

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch67

calib_packv5_042523_0143.root, FC#3, port B3

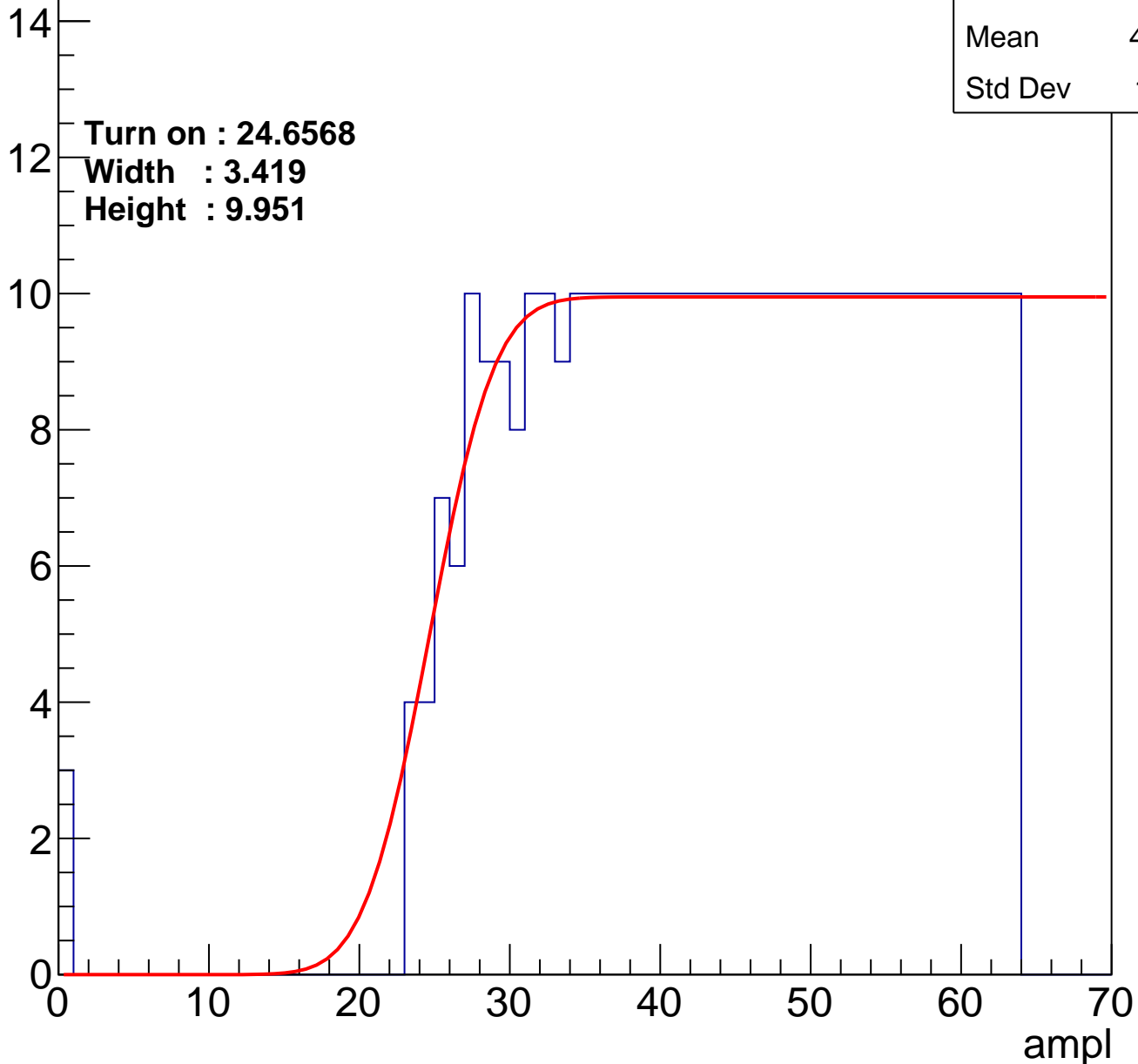
Entries	389
Mean	43.75
Std Dev	11.91

Turn on : 24.6568

Width : 3.419

Height : 9.951

Entry



B1L000S, U17-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.17
Std Dev	11.37

Turn on : 26.1248

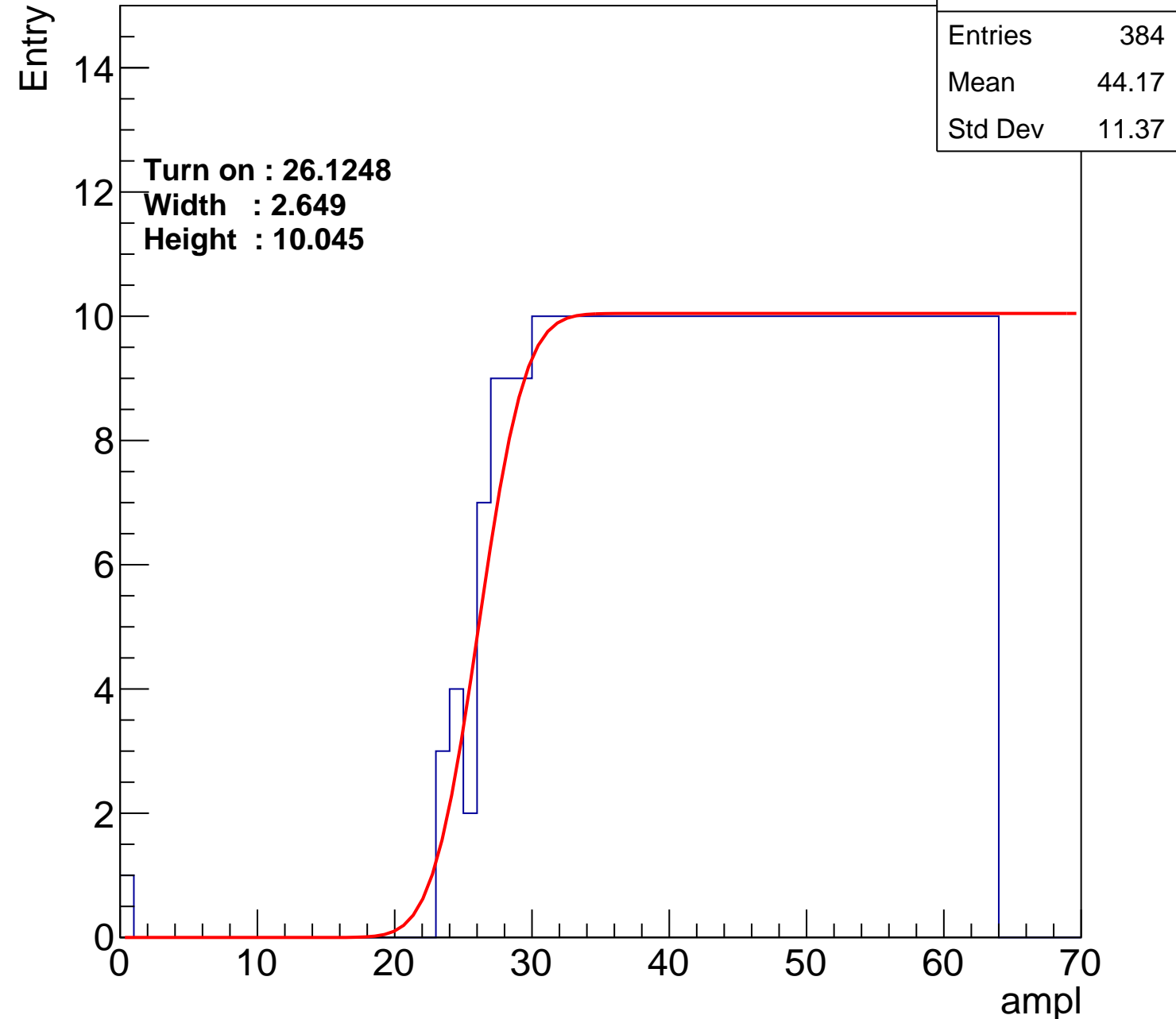
Width : 2.649

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	43.95
Std Dev	12.23

Turn on : 26.6555

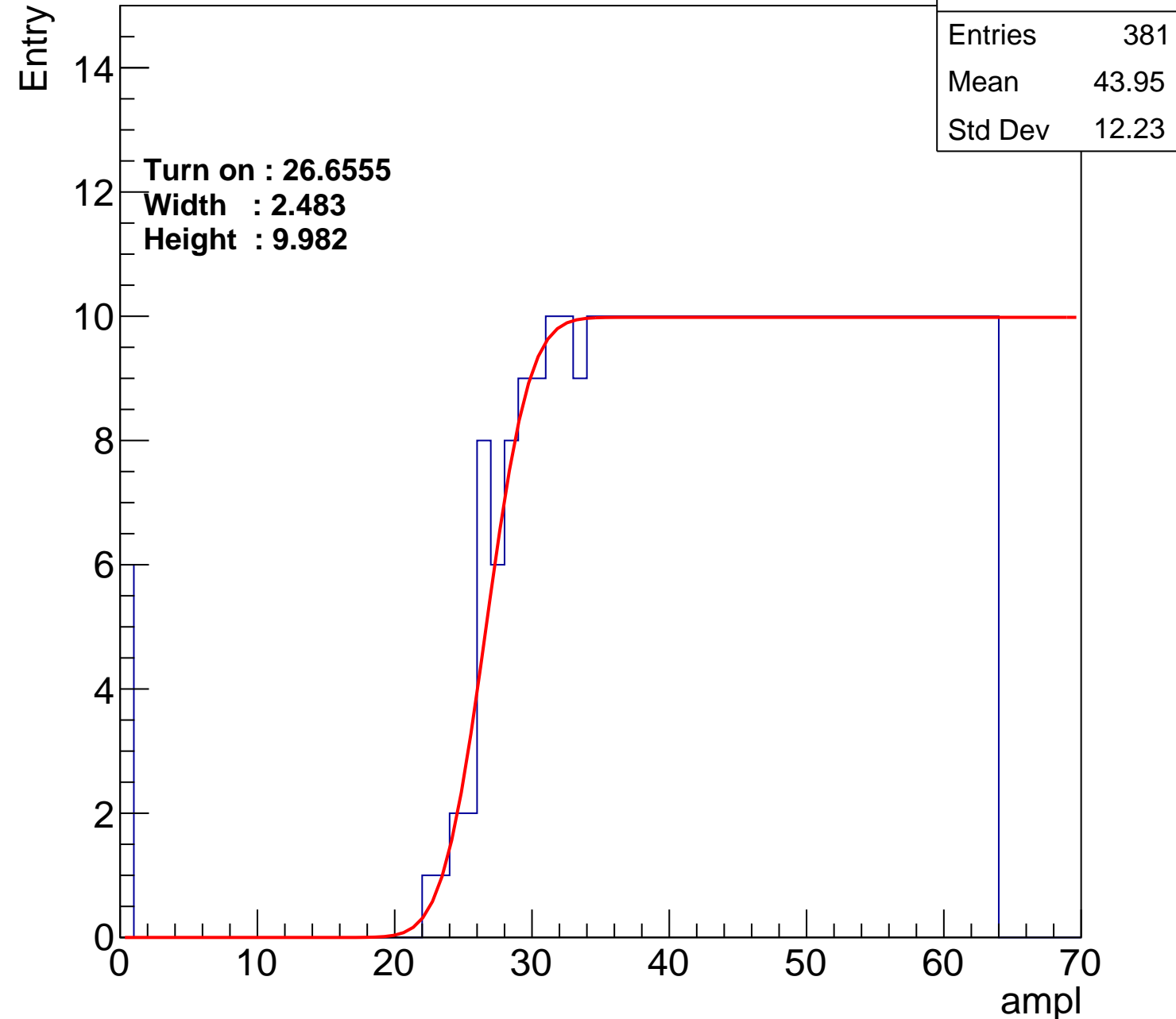
Width : 2.483

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.64
Std Dev	11.61

Turn on : 27.5441

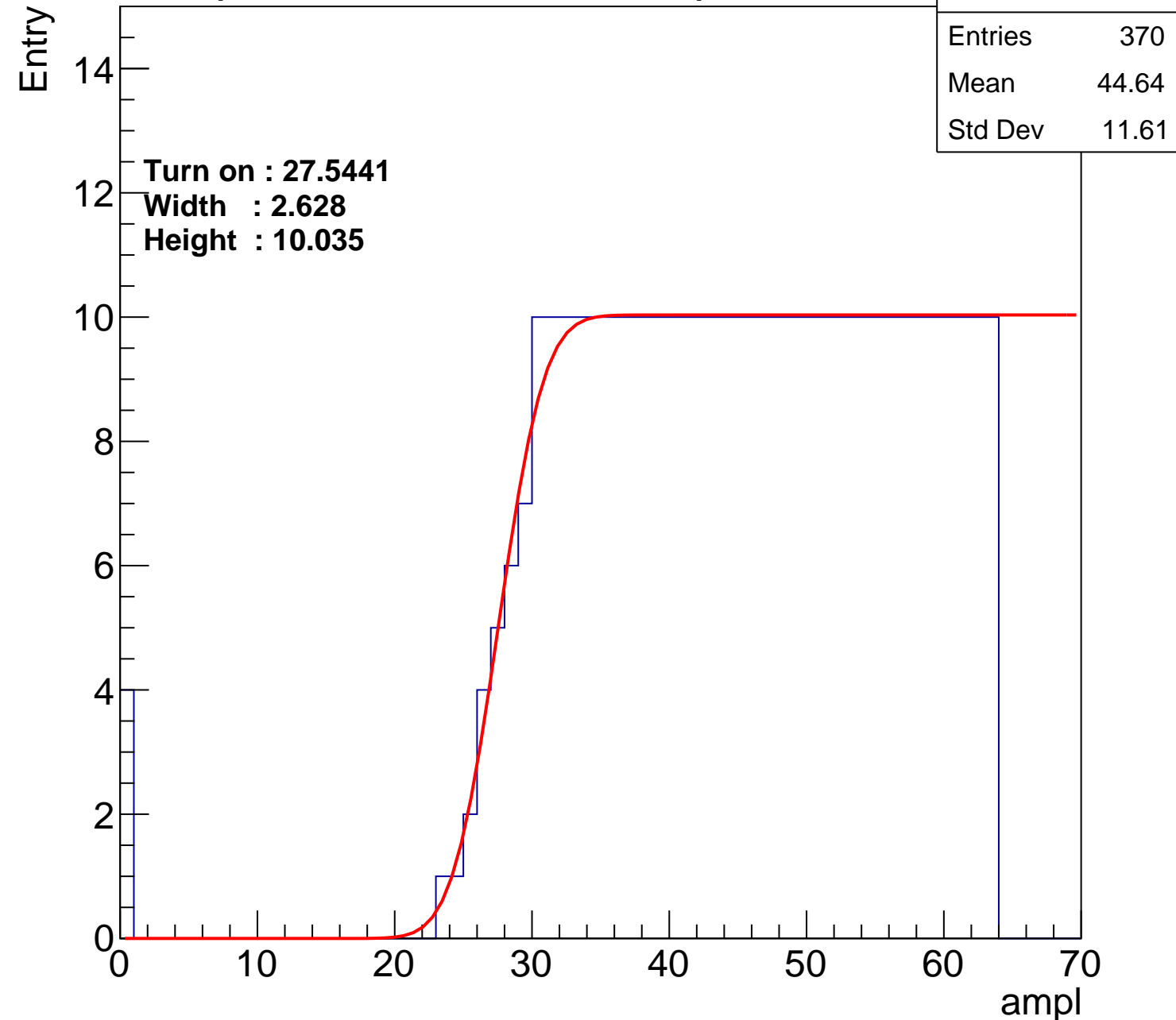
Width : 2.628

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.67
Std Dev	11.95

Turn on : 25.2125

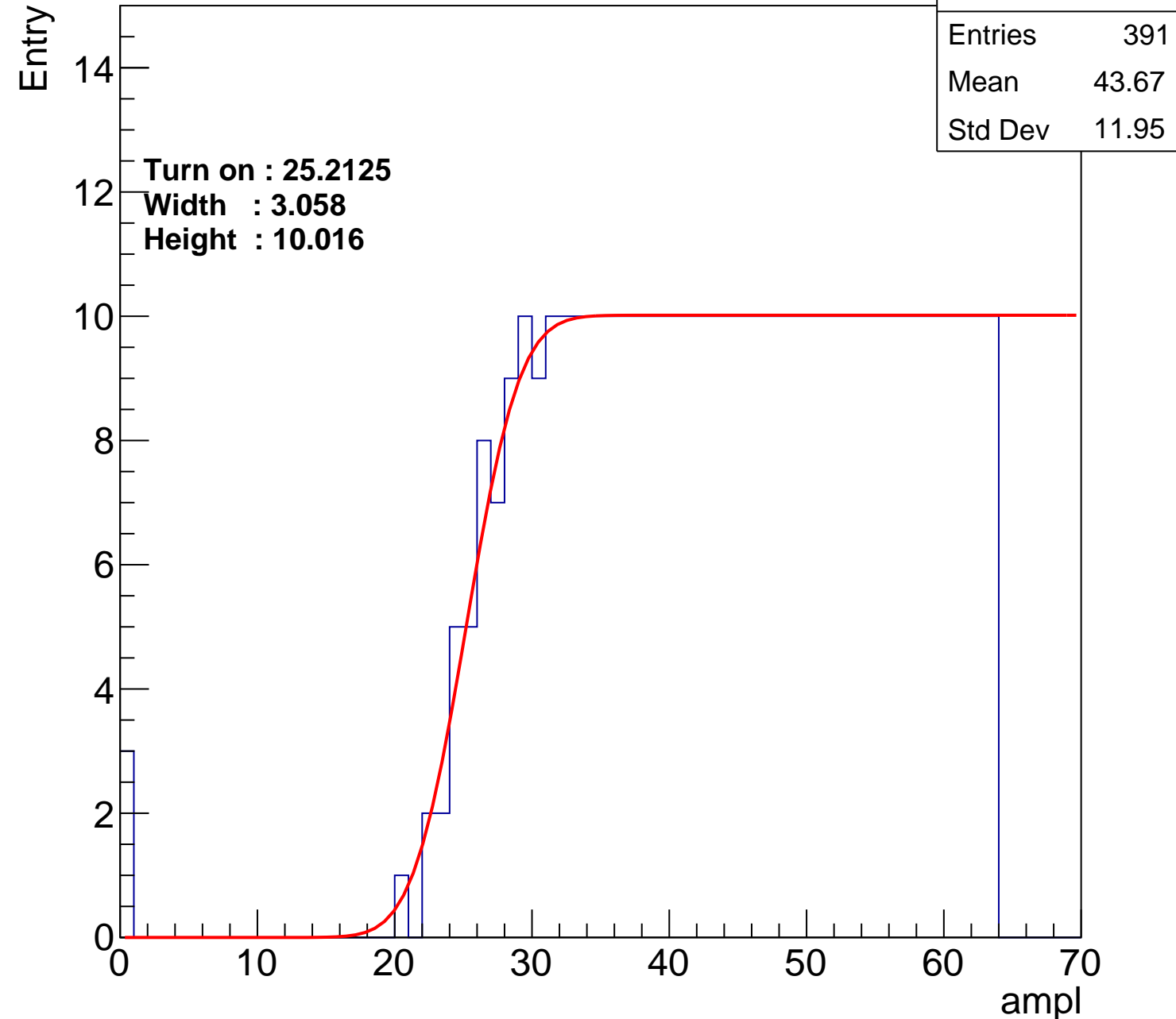
Width : 3.058

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.46
Std Dev	12.3

Turn on : 25.3564

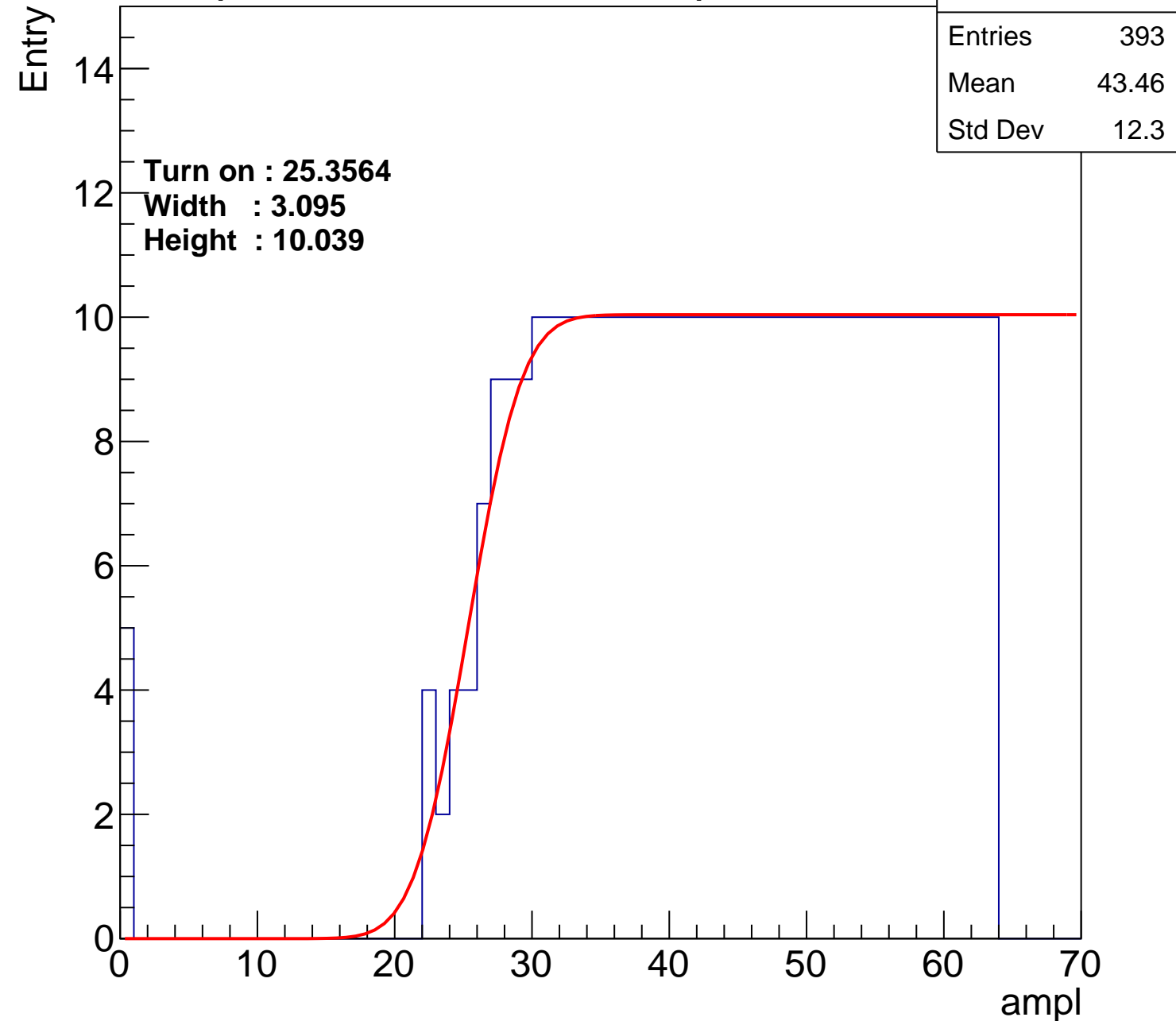
Width : 3.095

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.45
Std Dev	11.57

Turn on : 27.4915

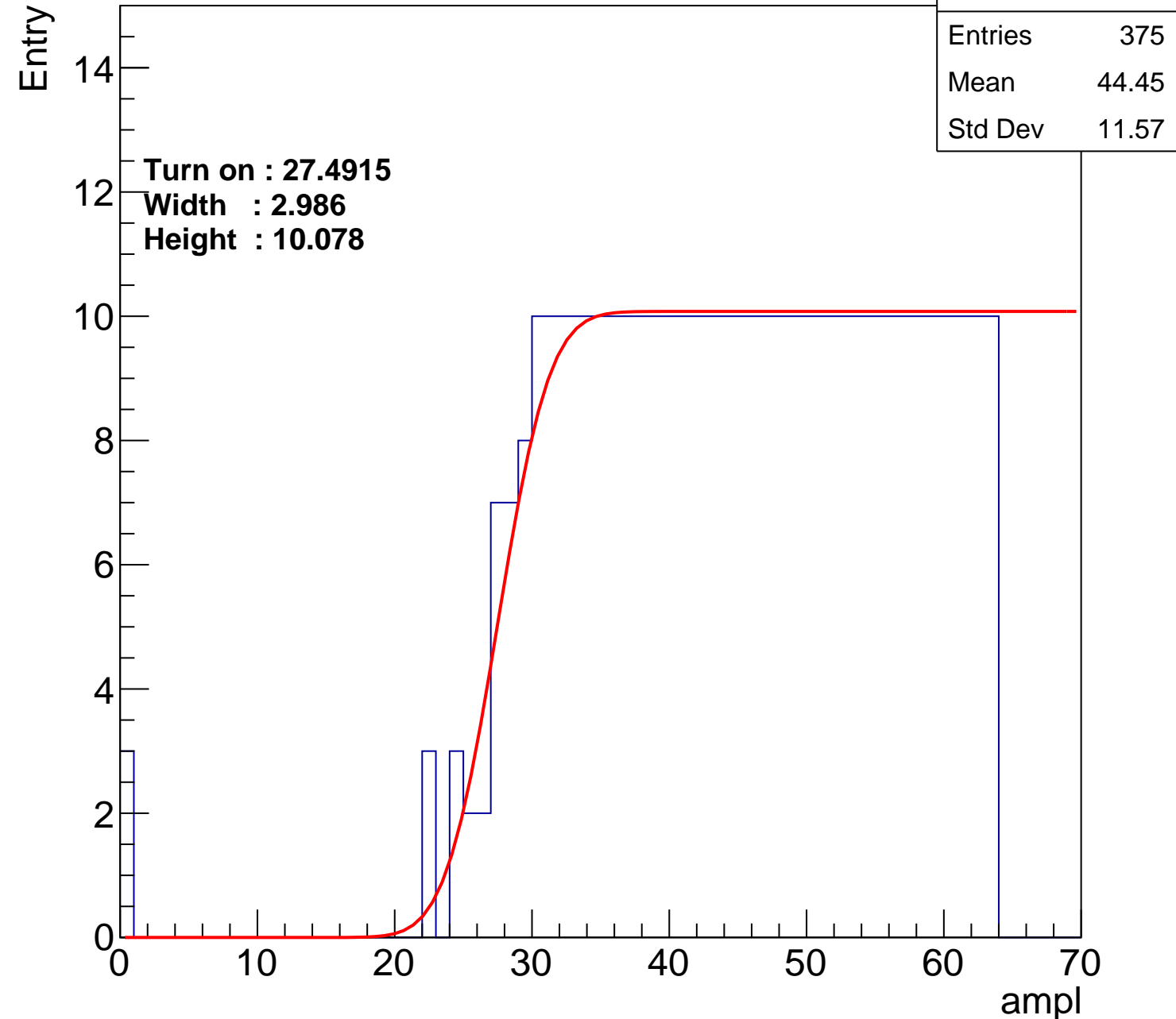
Width : 2.986

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	407
Mean	42.89
Std Dev	12.33

Turn on : 23.9221

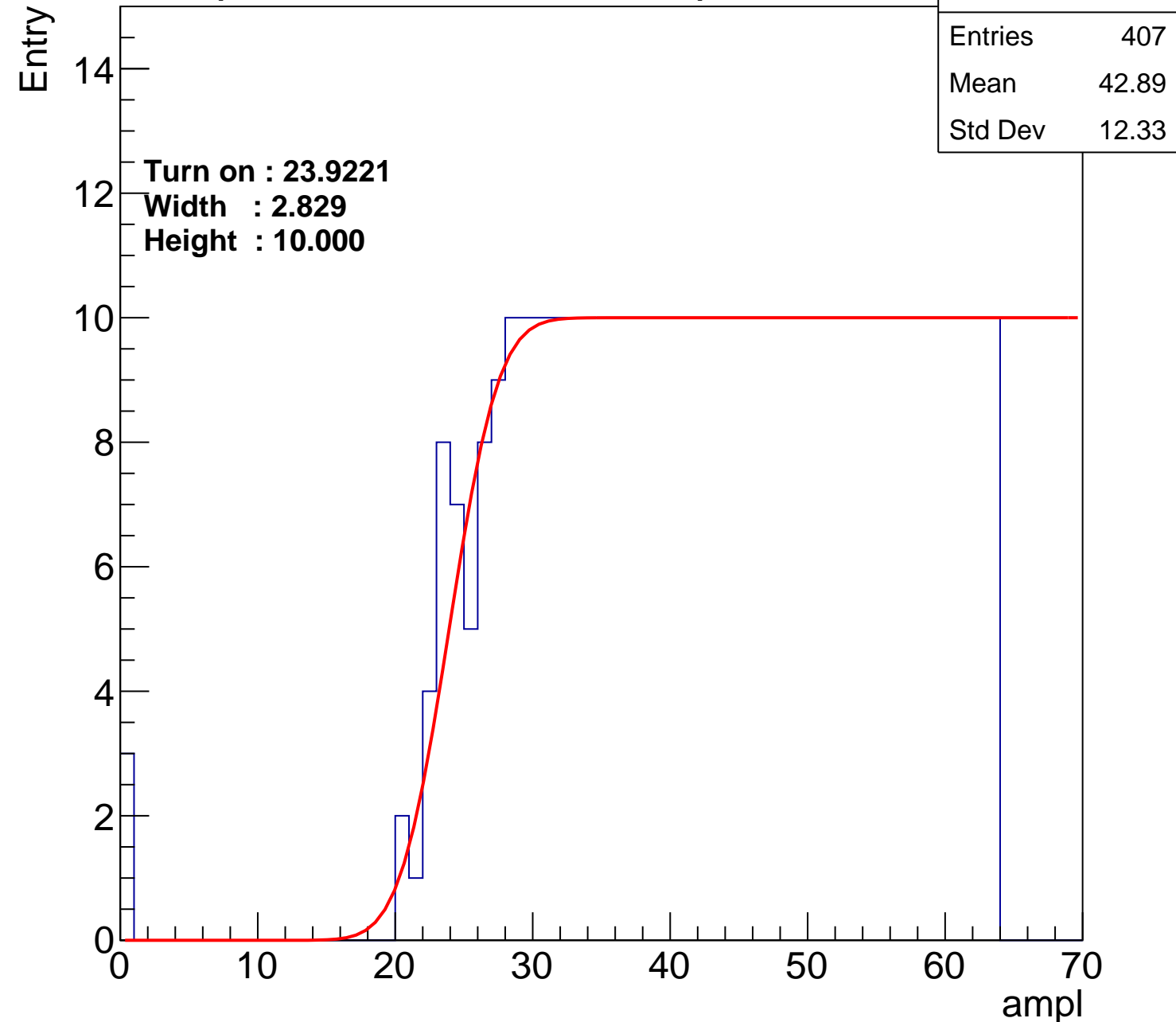
Width : 2.829

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch75

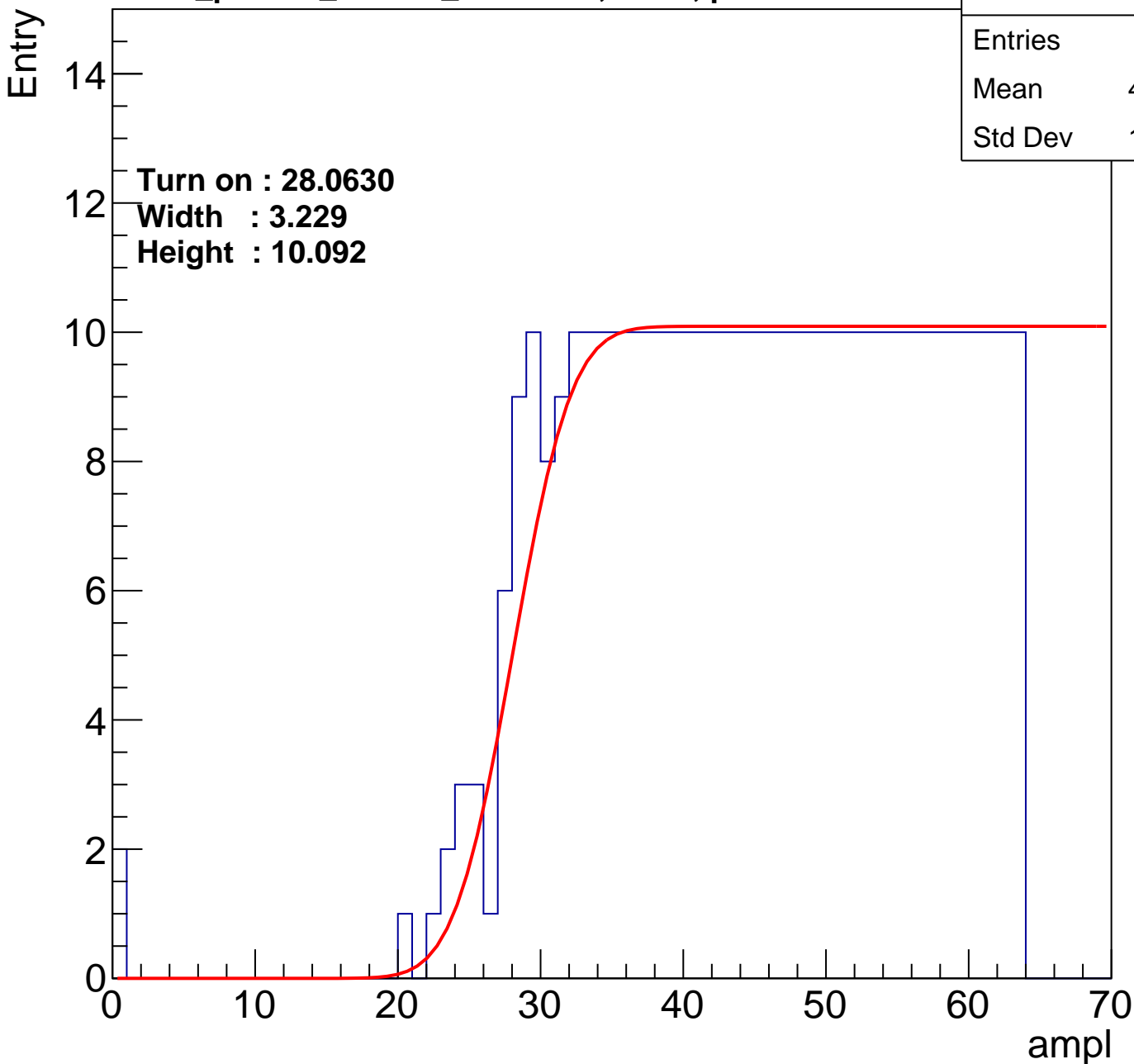
calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.49
Std Dev	11.42

Turn on : 28.0630

Width : 3.229

Height : 10.092



B1L000S, U17-ch76

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.03
Std Dev	12.05

Turn on : 26.6383

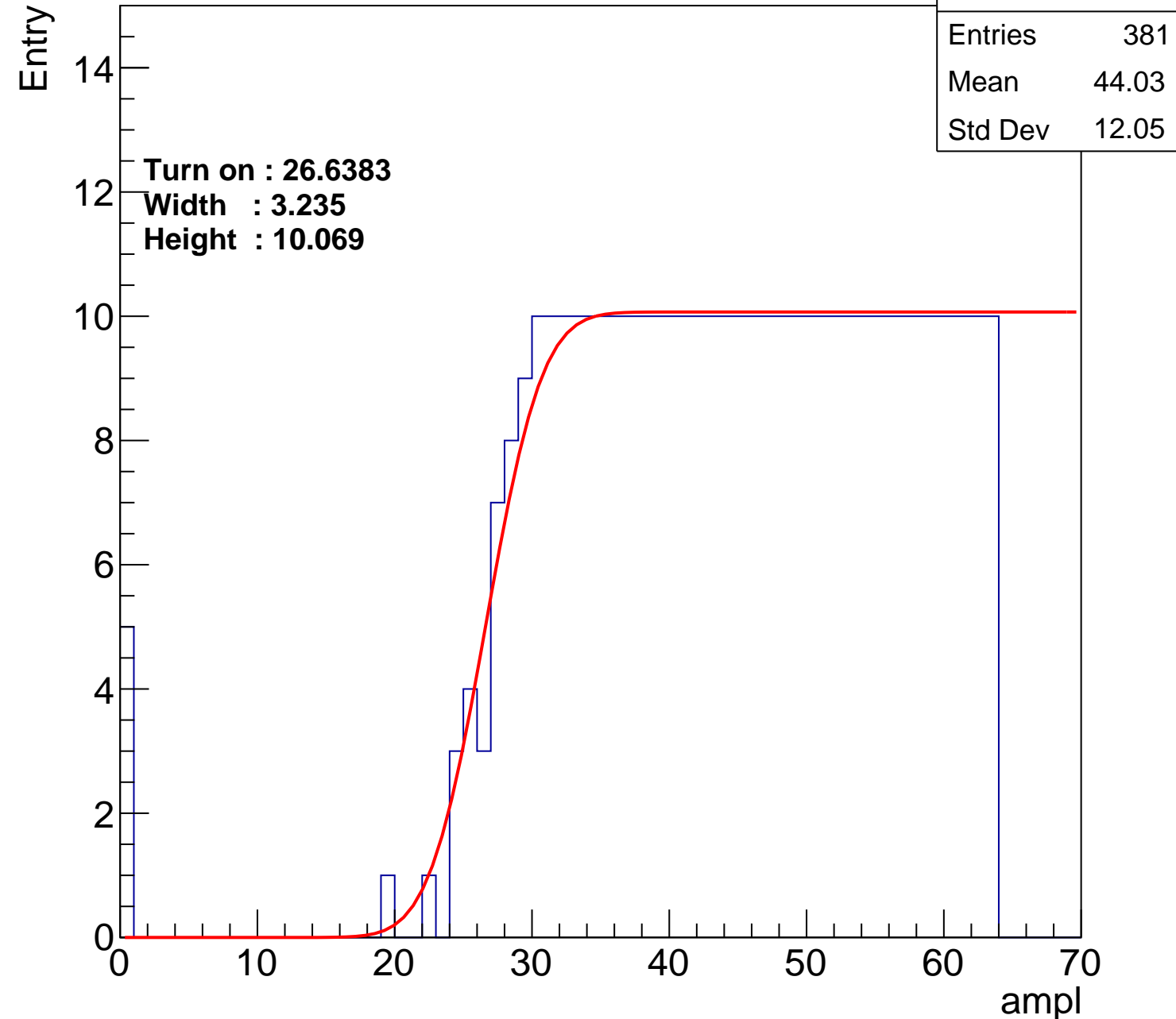
Width : 3.235

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch77

calib_packv5_042523_0143.root, FC#3, port B3

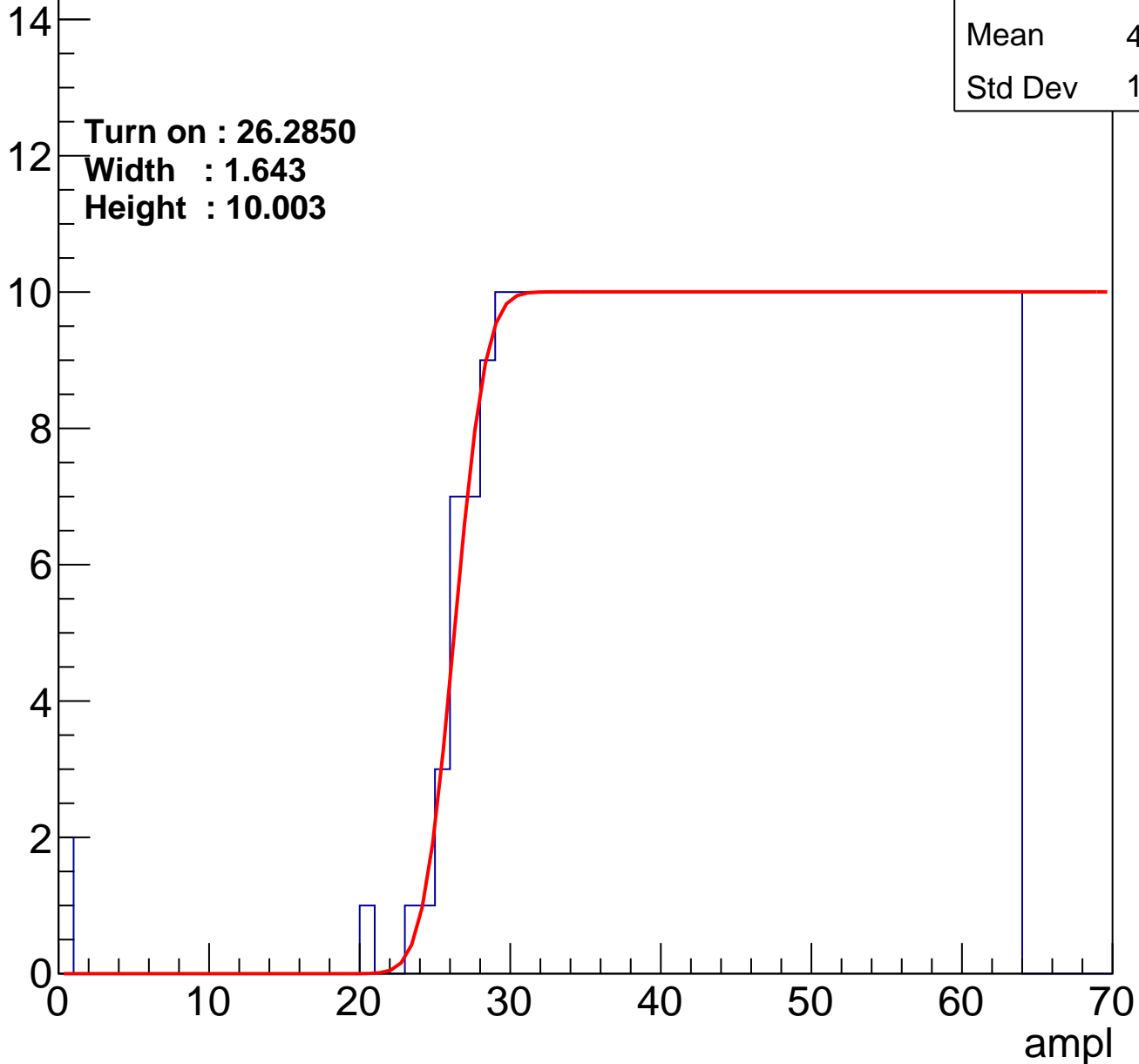
Entries	381
Mean	44.27
Std Dev	11.46

Turn on : 26.2850

Width : 1.643

Height : 10.003

Entry



B1L000S, U17-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.86
Std Dev	11.87

Turn on : 26.1631

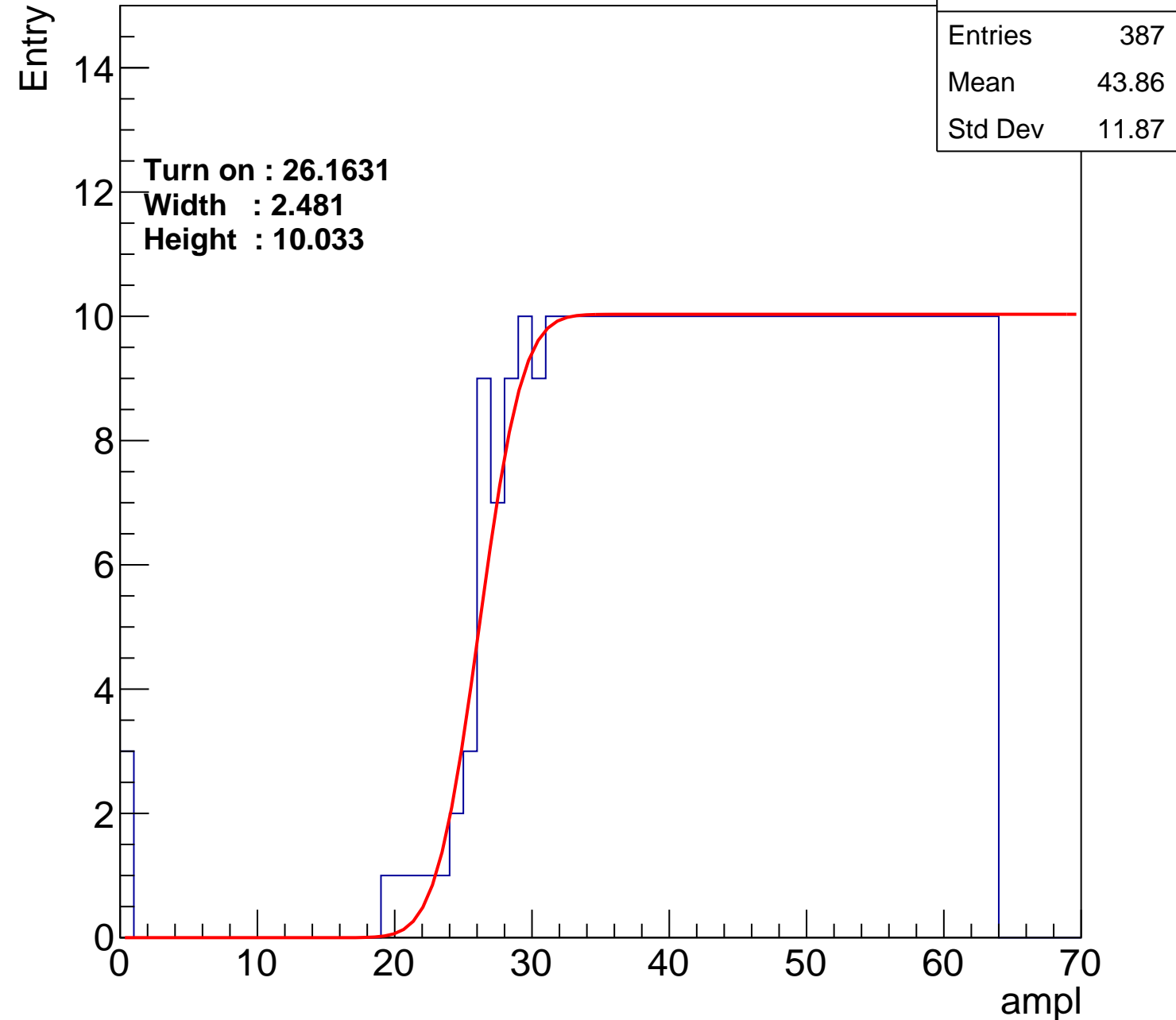
Width : 2.481

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.54
Std Dev	12.33

Turn on : 25.8002

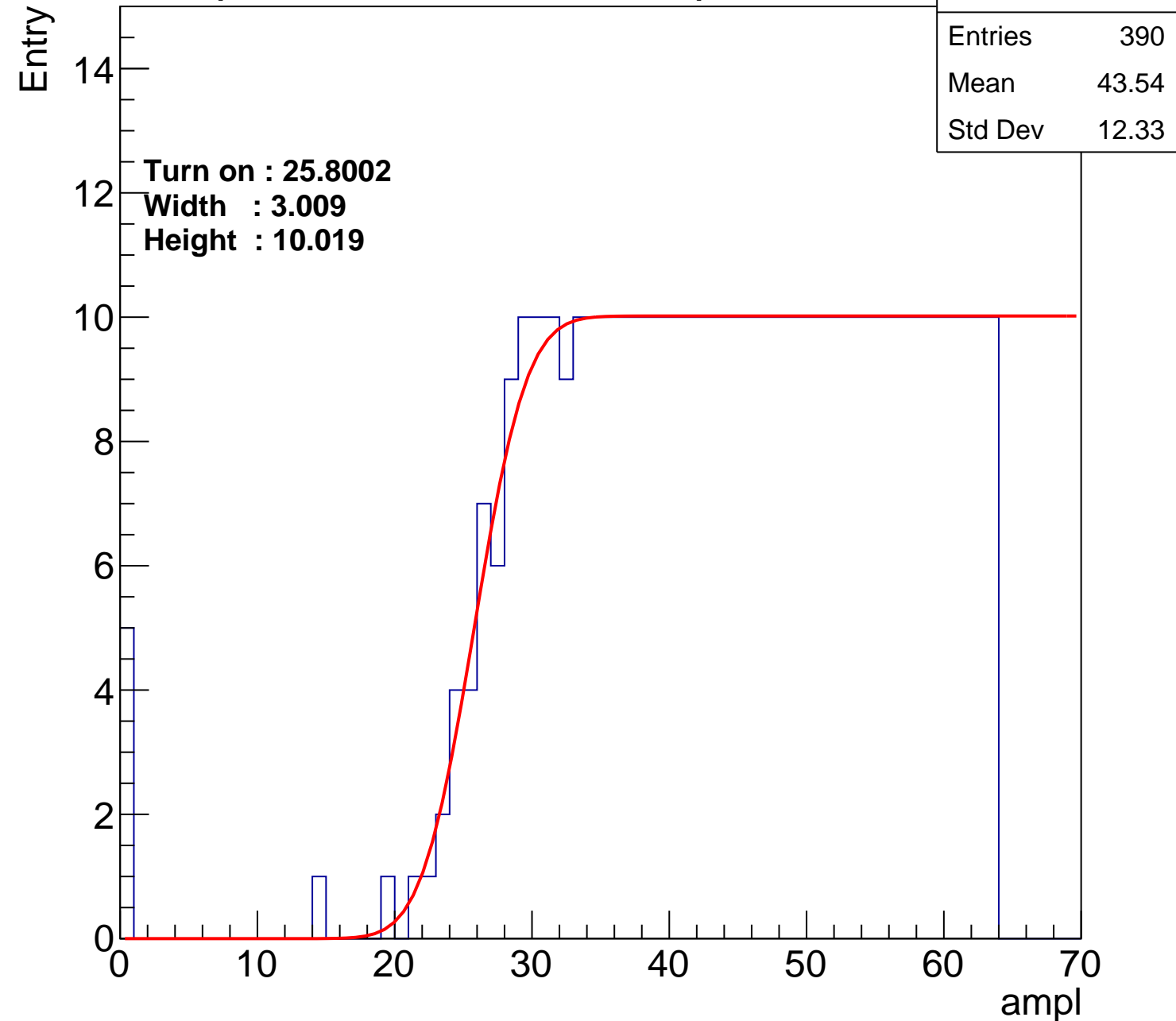
Width : 3.009

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.0662

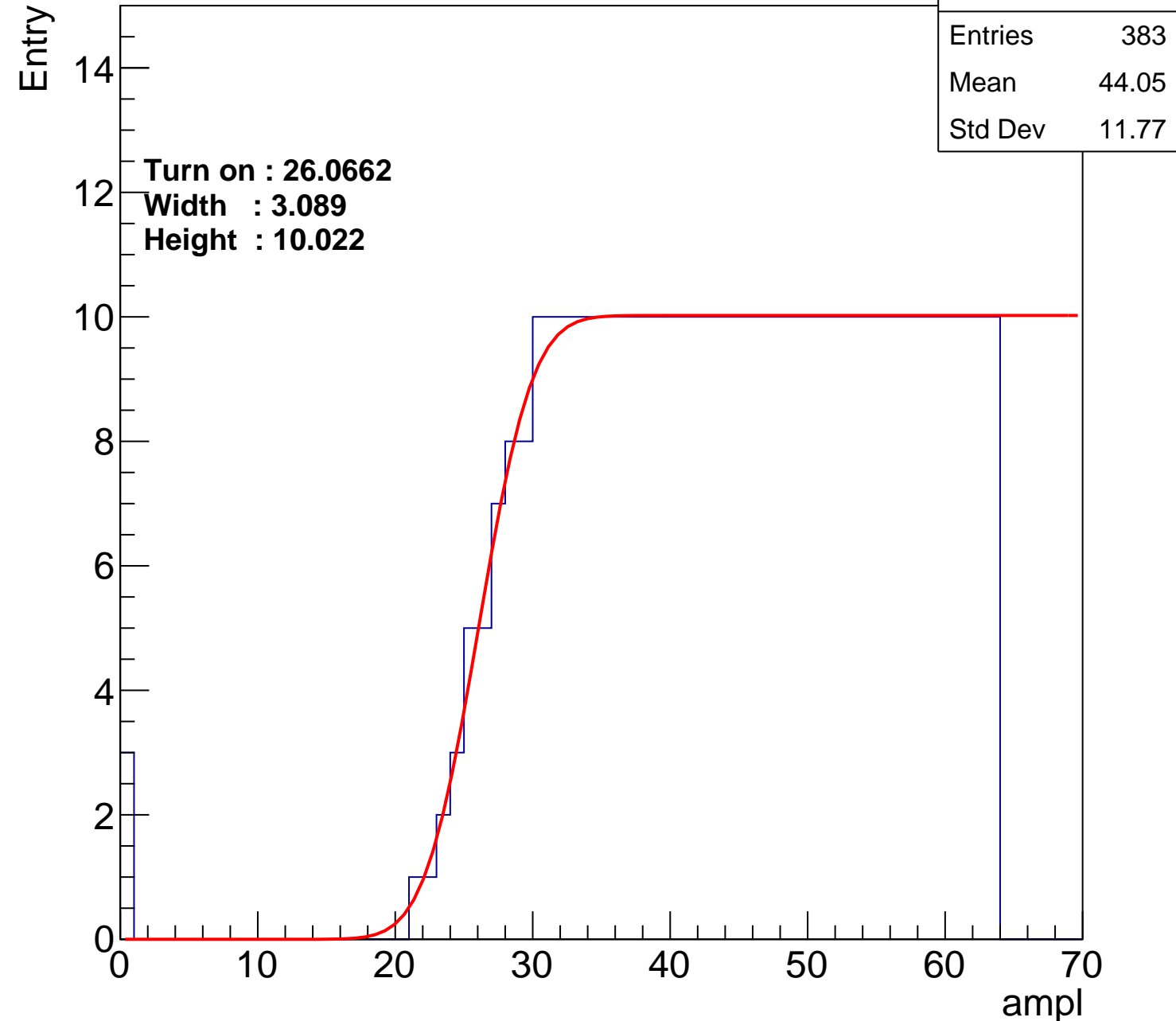
Width : 3.089

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch81

calib_packv5_042523_0143.root, FC#3, port B3

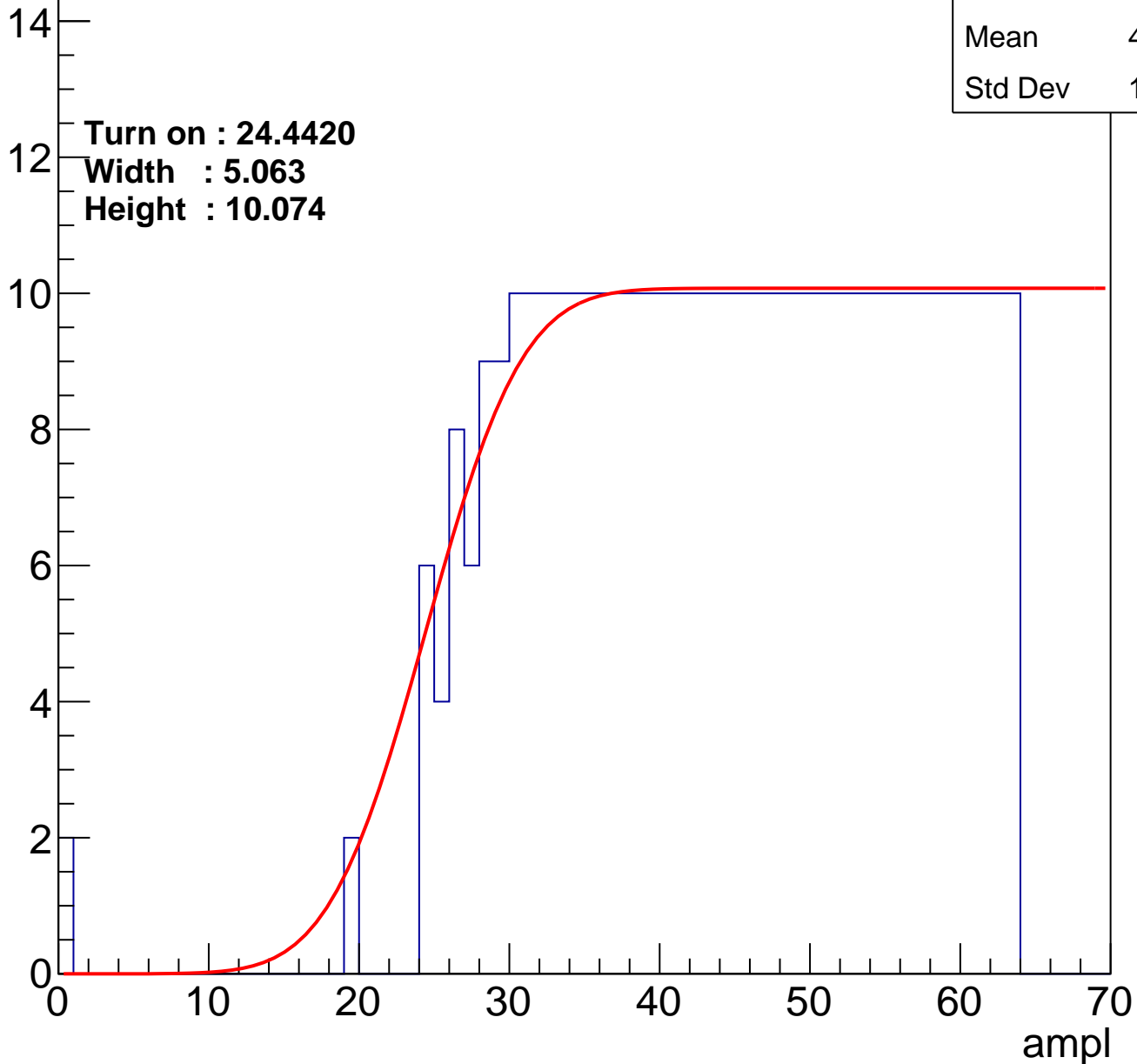
Entries	386
Mean	43.98
Std Dev	11.66

Turn on : 24.4420

Width : 5.063

Height : 10.074

Entry



B1L000S, U17-ch82

calib_packv5_042523_0143.root, FC#3, port B3

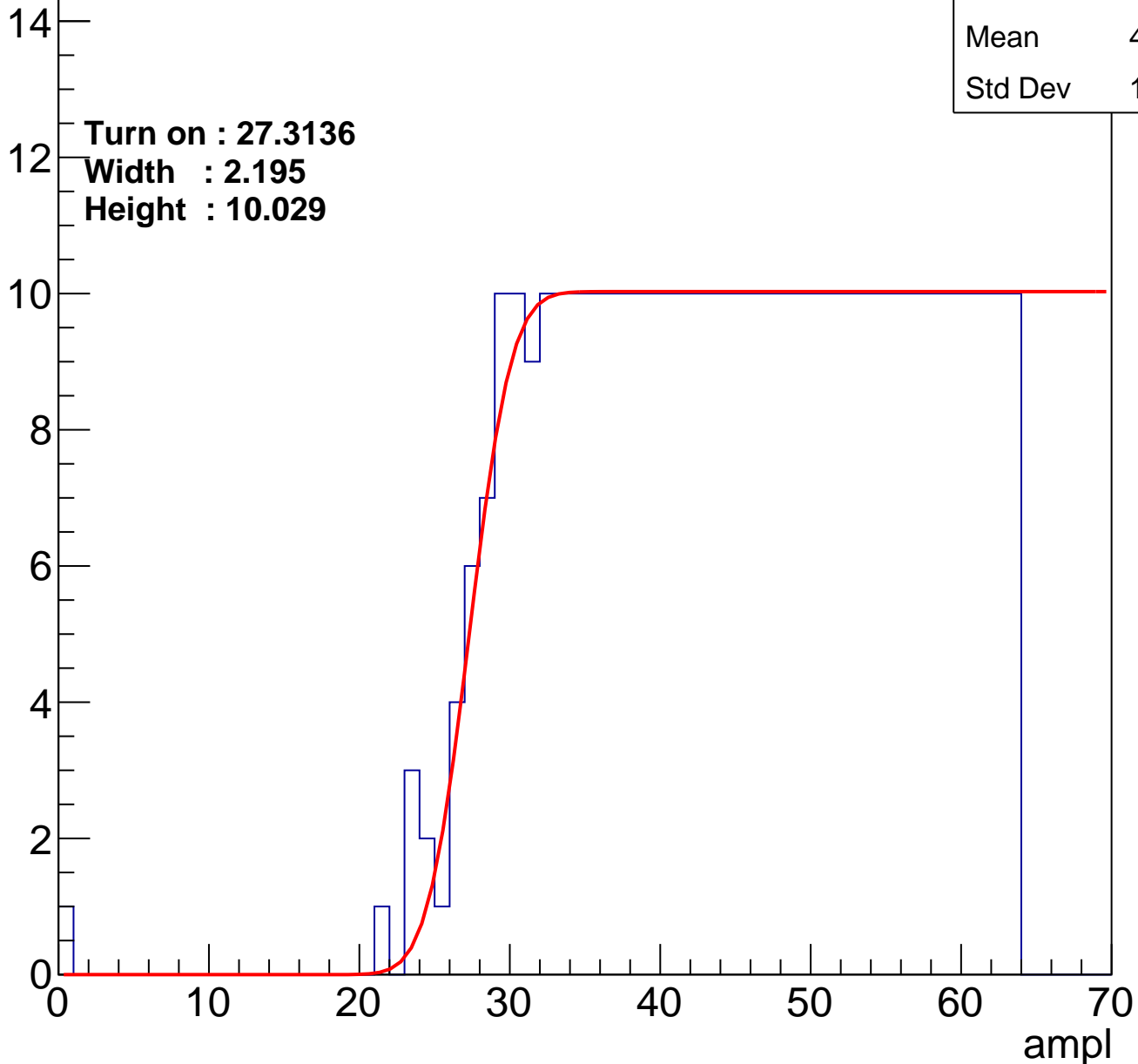
Entries	374
Mean	44.64
Std Dev	11.16

Turn on : 27.3136

Width : 2.195

Height : 10.029

Entry



B1L000S, U17-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.46
Std Dev	12.07

Turn on : 25.1010

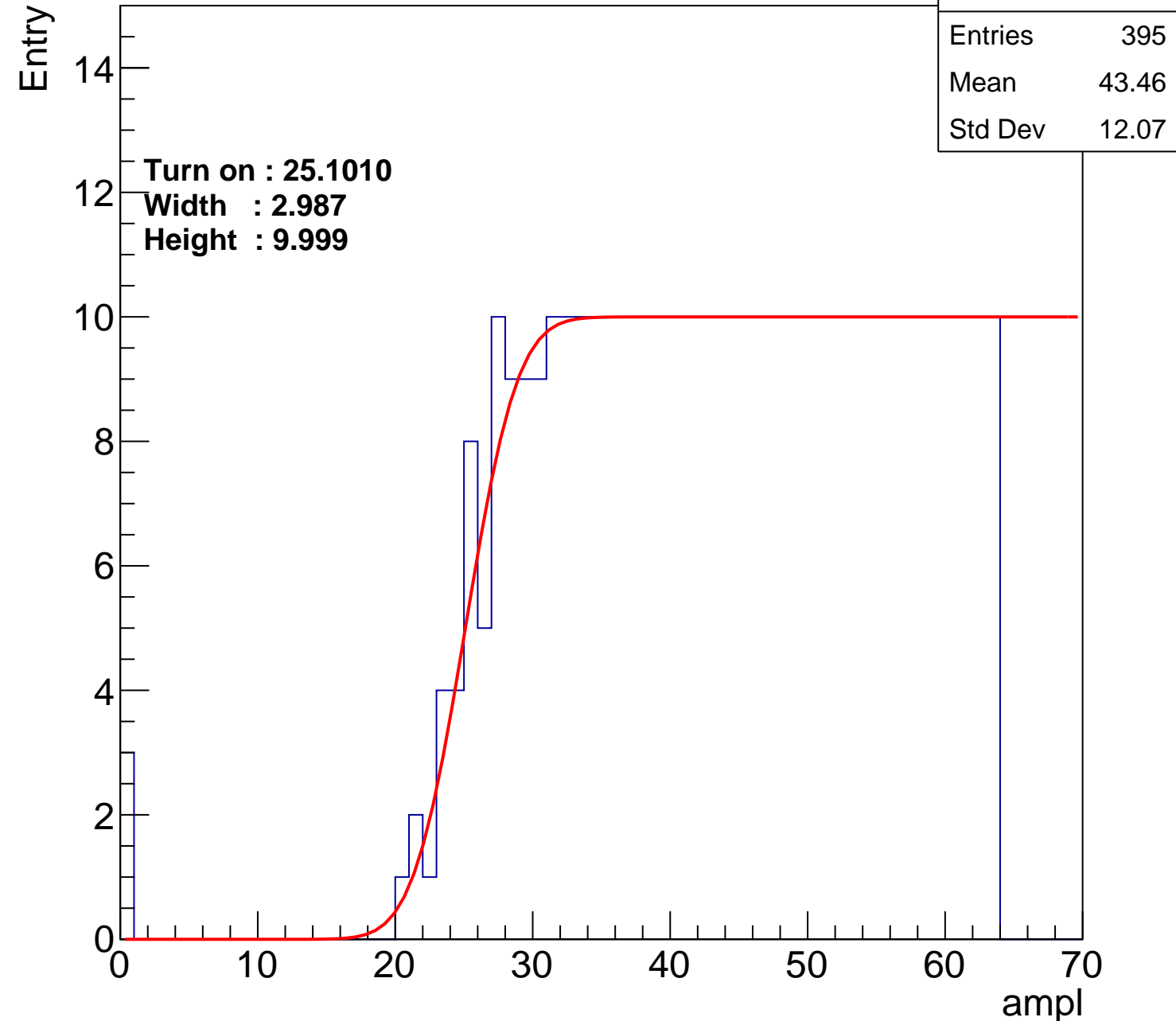
Width : 2.987

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch84

calib_packv5_042523_0143.root, FC#3, port B3

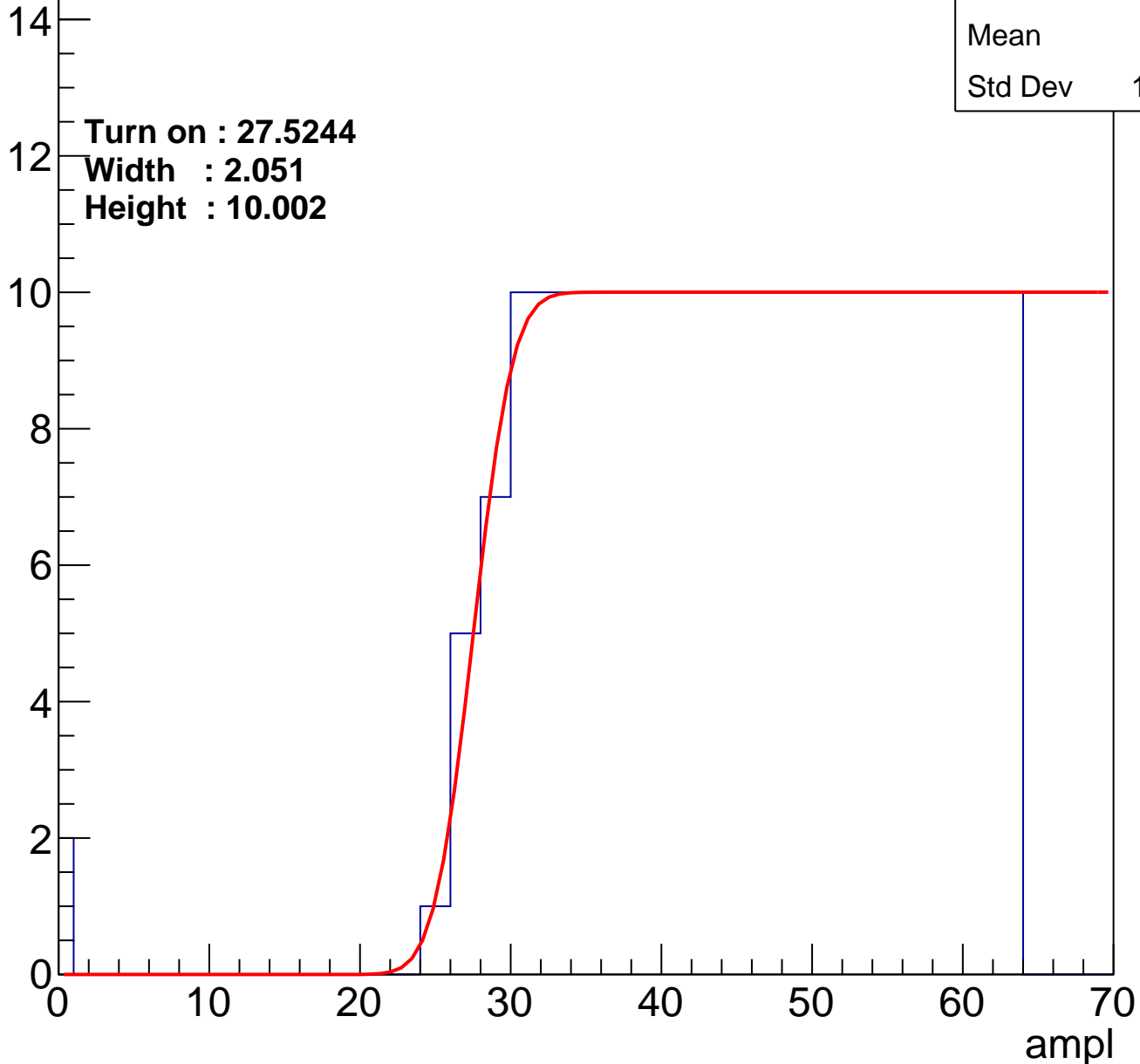
Entries	368
Mean	44.9
Std Dev	11.13

Turn on : 27.5244

Width : 2.051

Height : 10.002

Entry



B1L000S, U17-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.63
Std Dev	11.72

Turn on : 24.9156

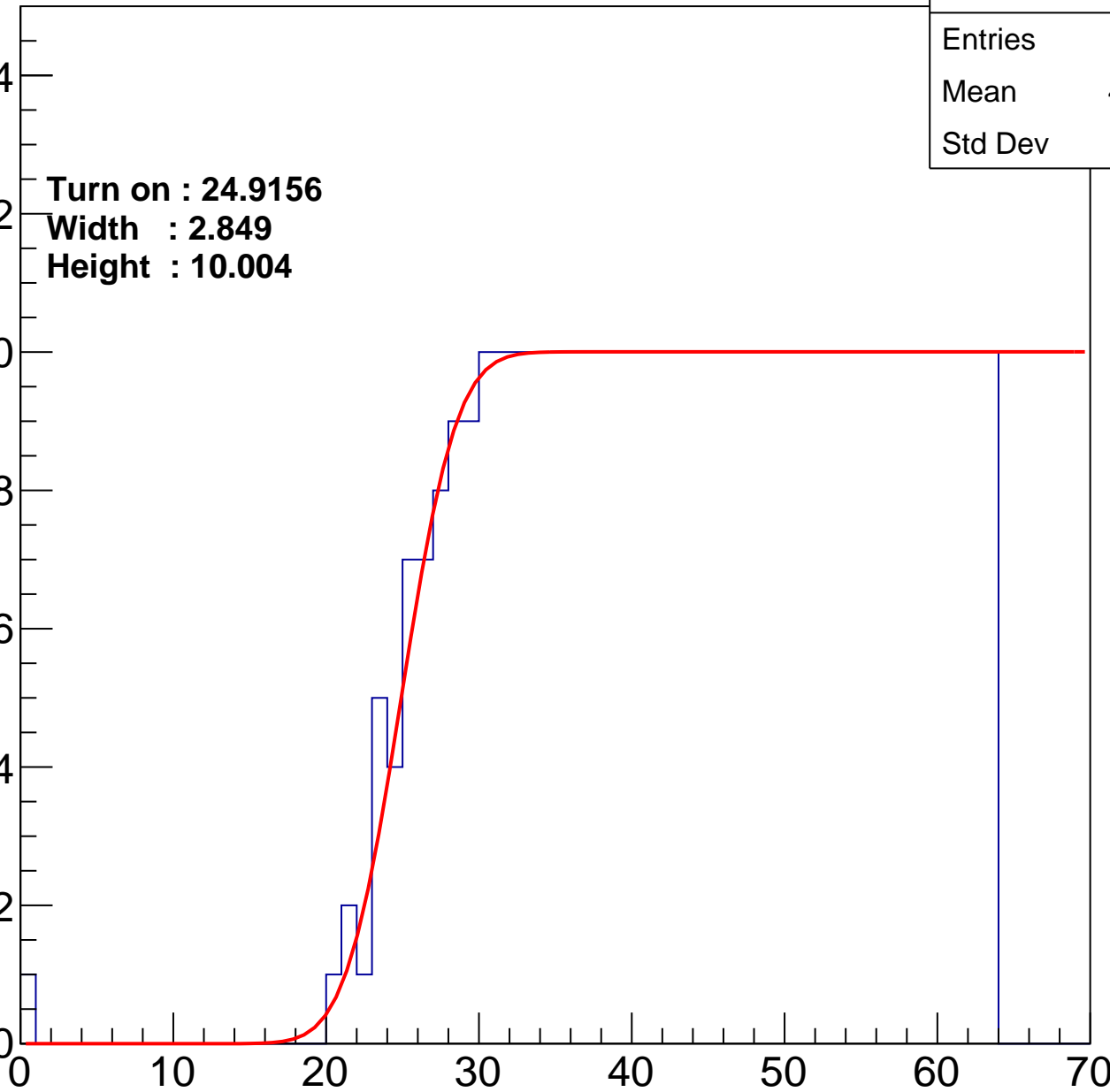
Width : 2.849

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.8639

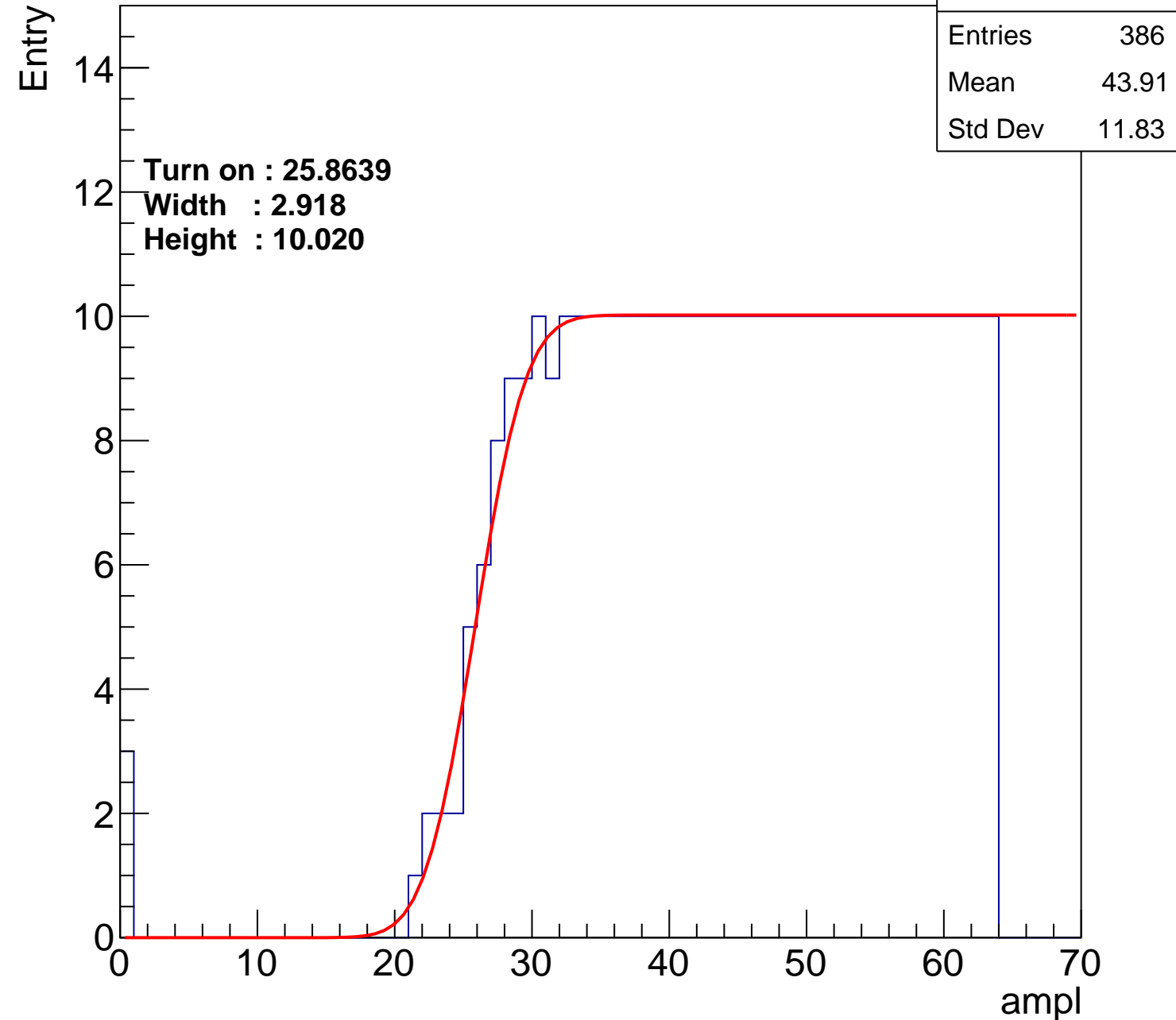
Width : 2.918

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch87

calib_packv5_042523_0143.root, FC#3, port B3

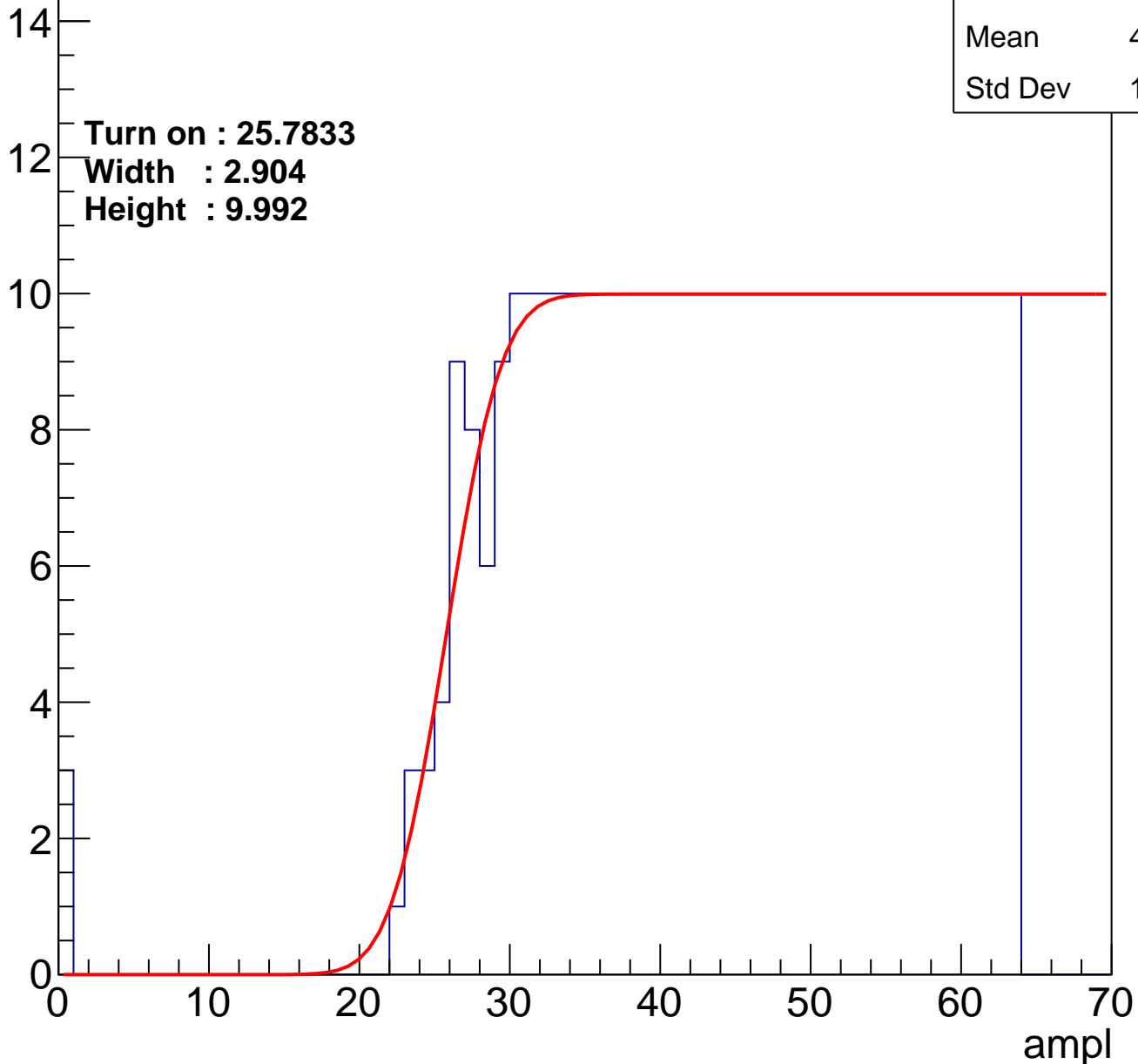
Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 25.7833

Width : 2.904

Height : 9.992

Entry



B1L000S, U17-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.8
Std Dev	11.77

Turn on : 25.3685

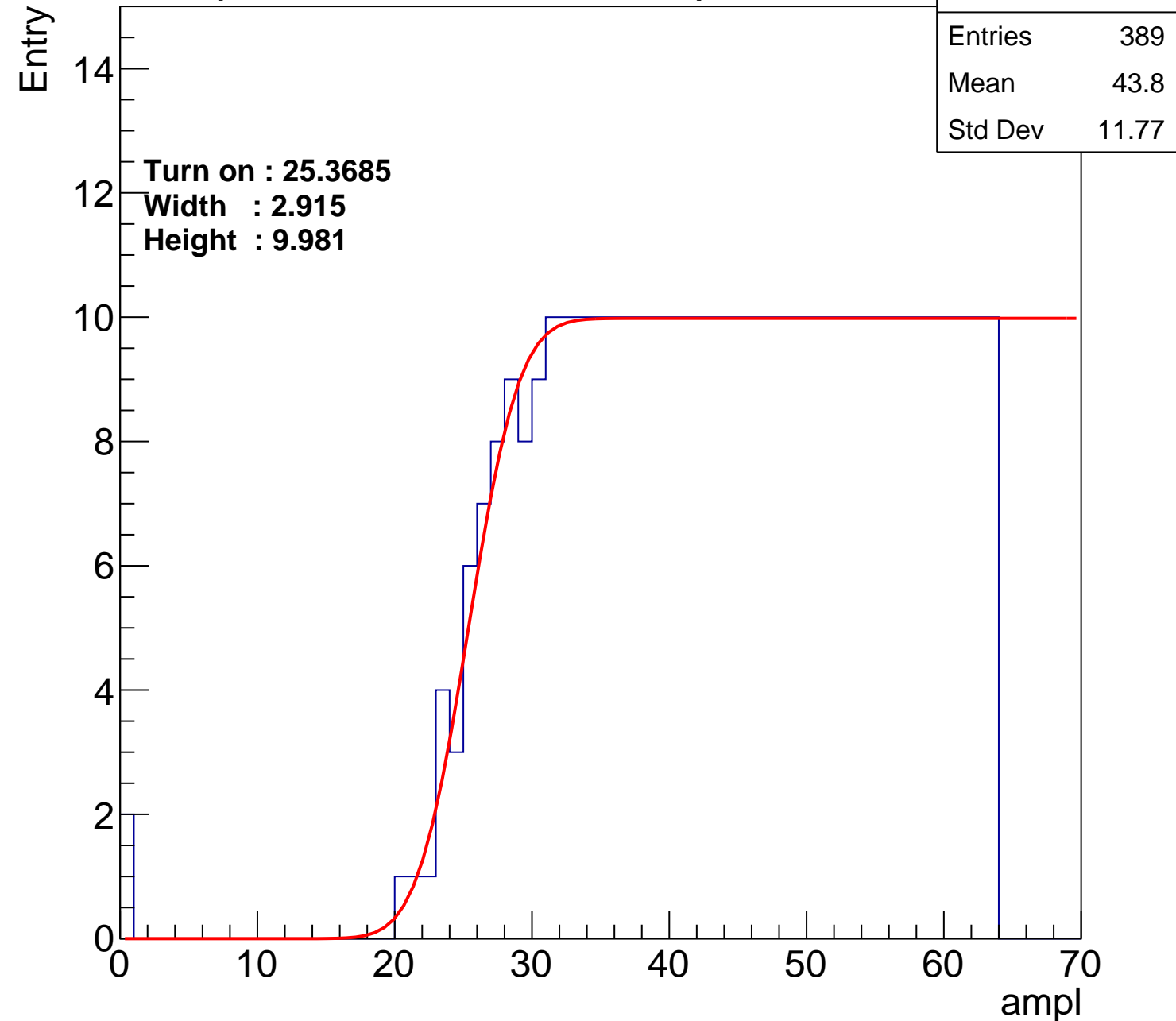
Width : 2.915

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch89

calib_packv5_042523_0143.root, FC#3, port B3

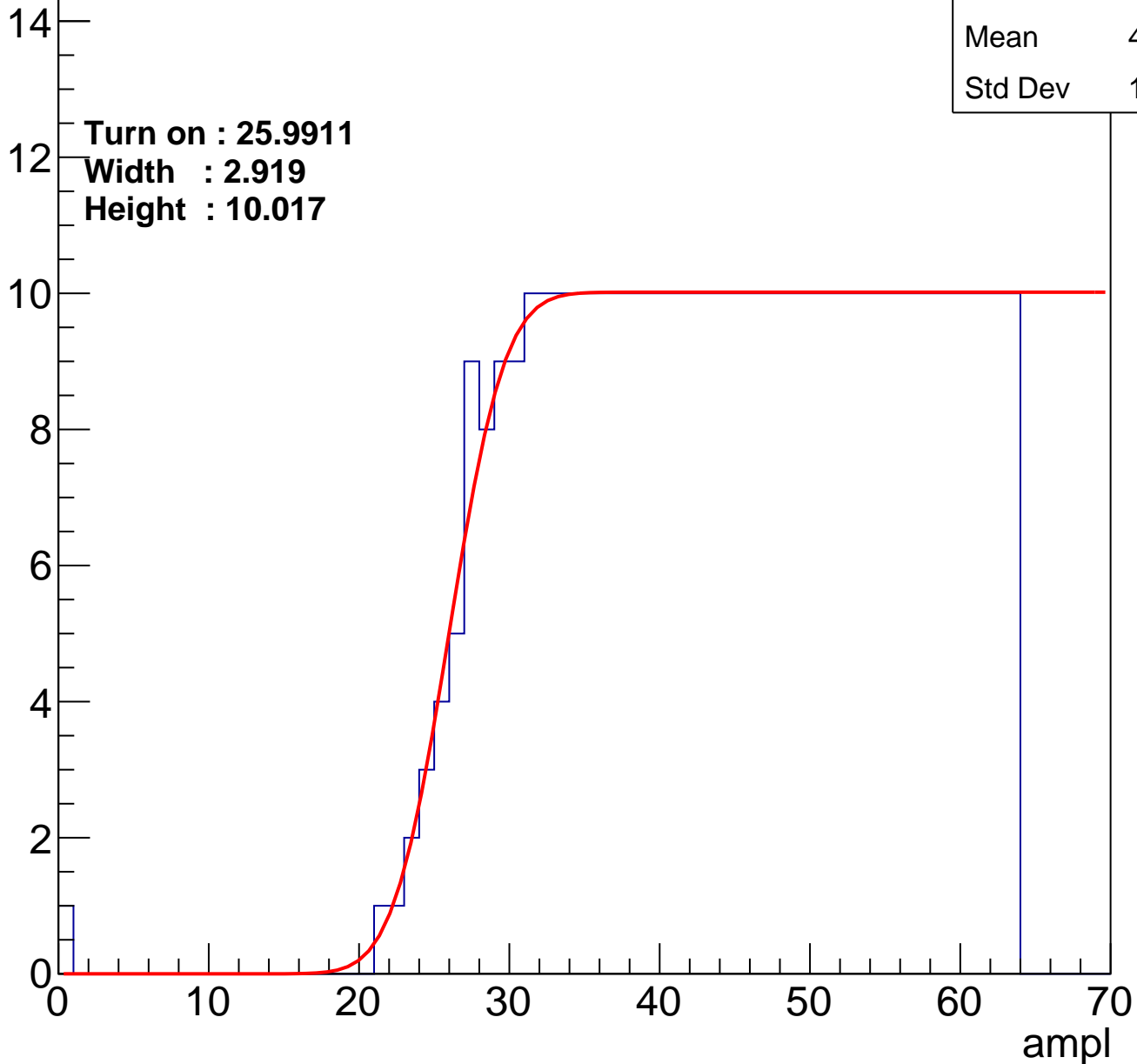
Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 25.9911

Width : 2.919

Height : 10.017

Entry



B1L000S, U17-ch90

calib_packv5_042523_0143.root, FC#3, port B3

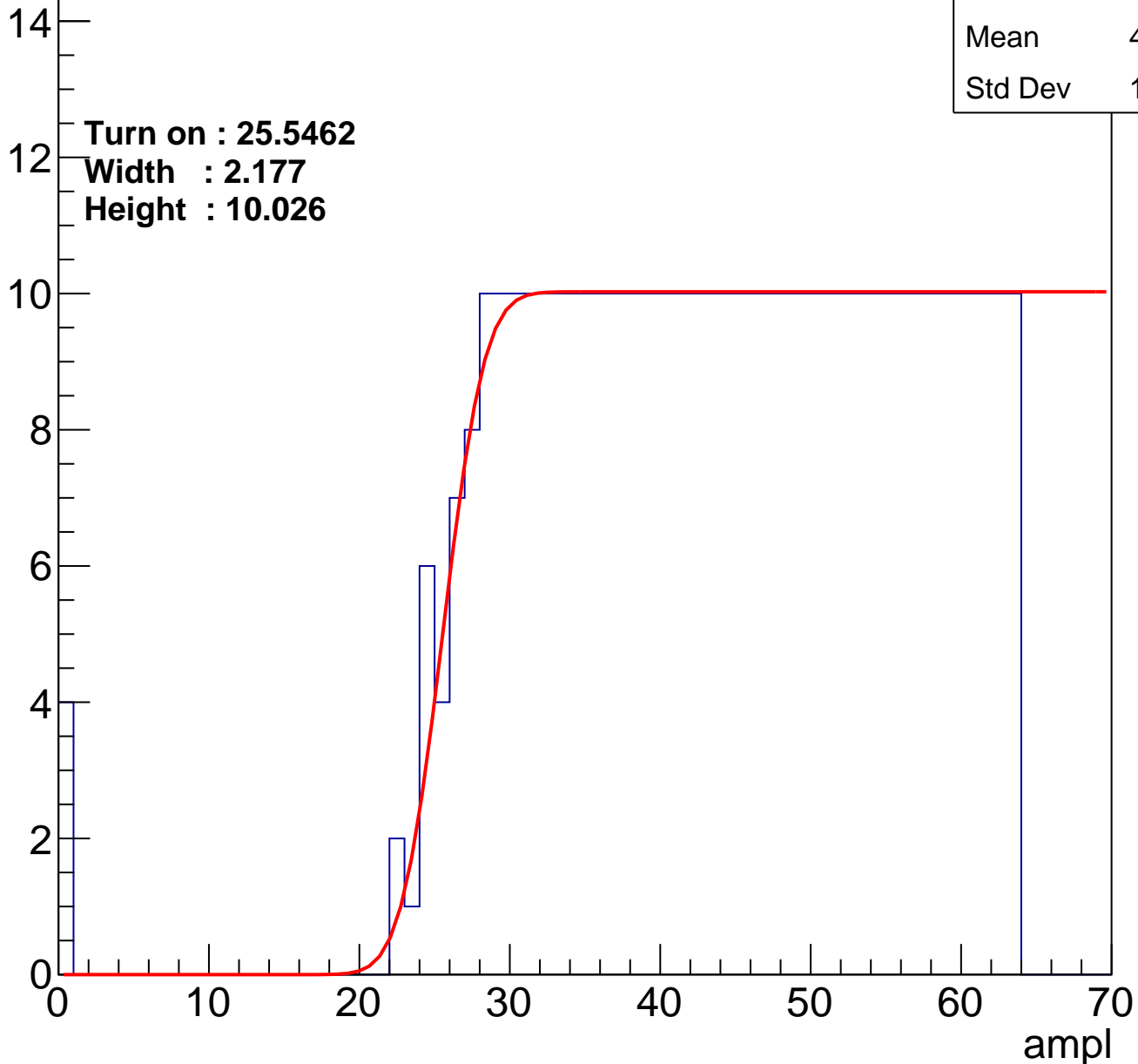
Entries	392
Mean	43.59
Std Dev	12.08

Turn on : 25.5462

Width : 2.177

Height : 10.026

Entry



B1L000S, U17-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.38
Std Dev	12.09

Turn on : 25.0114

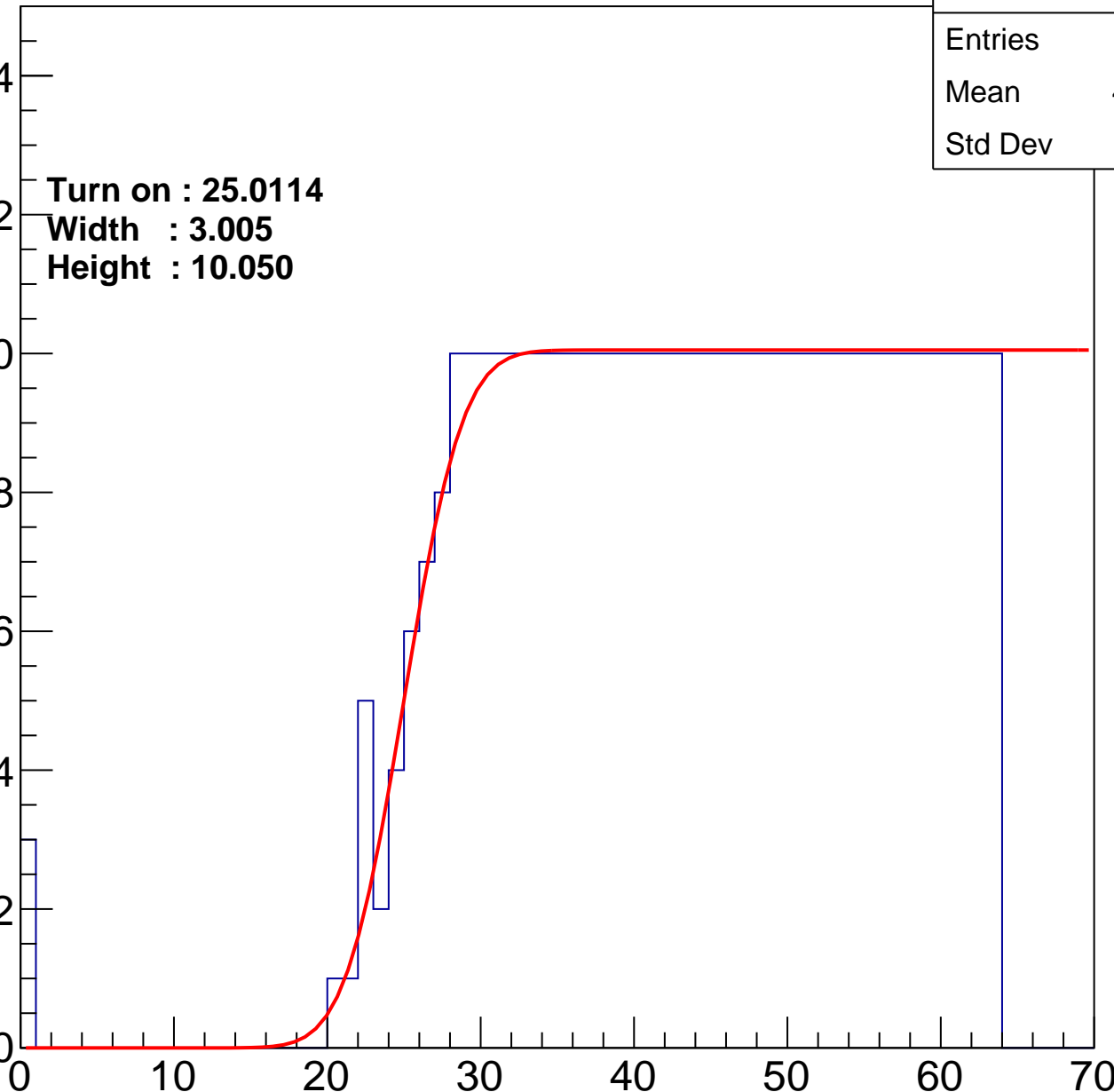
Width : 3.005

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch92

calib_packv5_042523_0143.root, FC#3, port B3

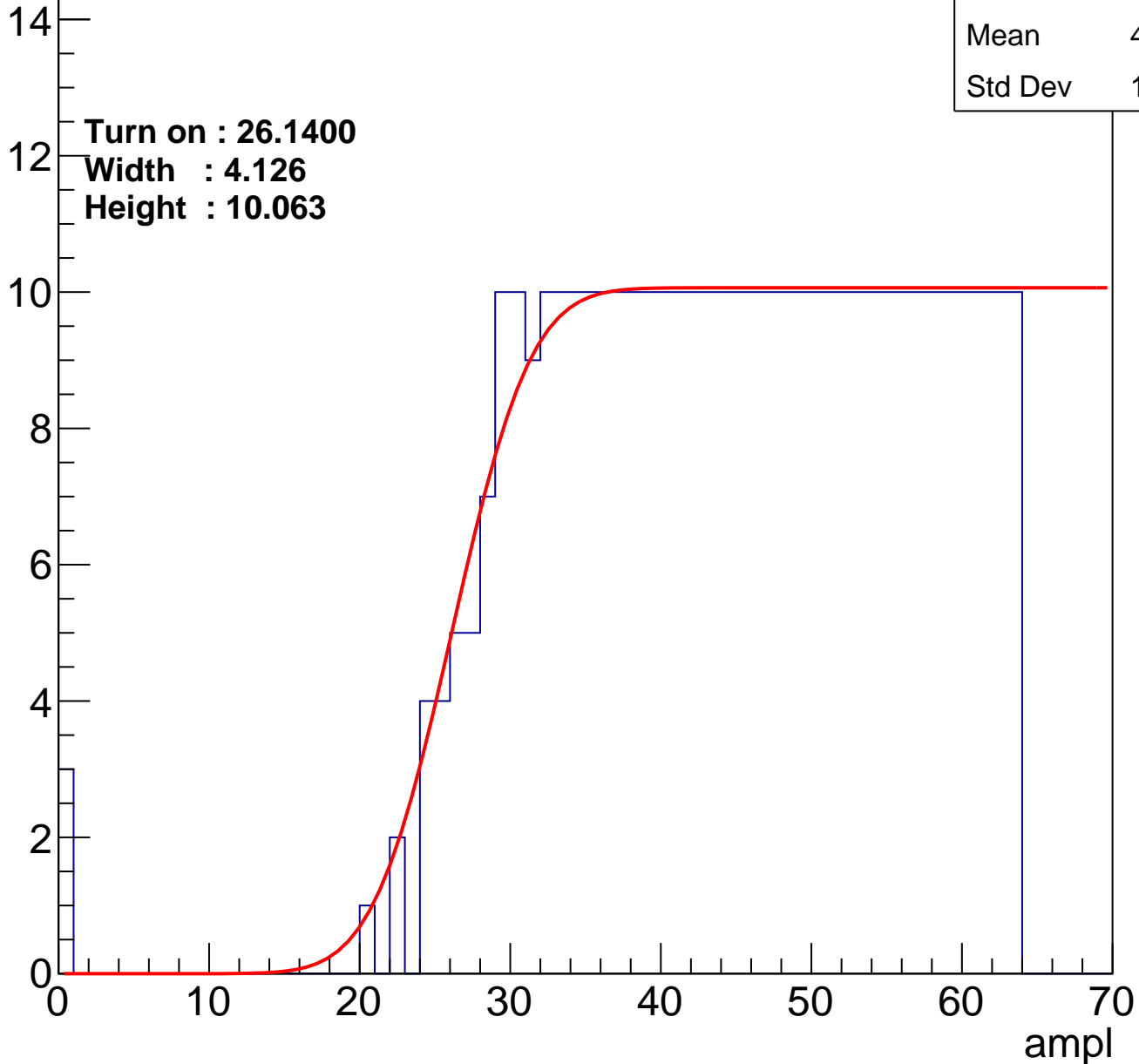
Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.1400

Width : 4.126

Height : 10.063

Entry



B1L000S, U17-ch93

calib_packv5_042523_0143.root, FC#3, port B3

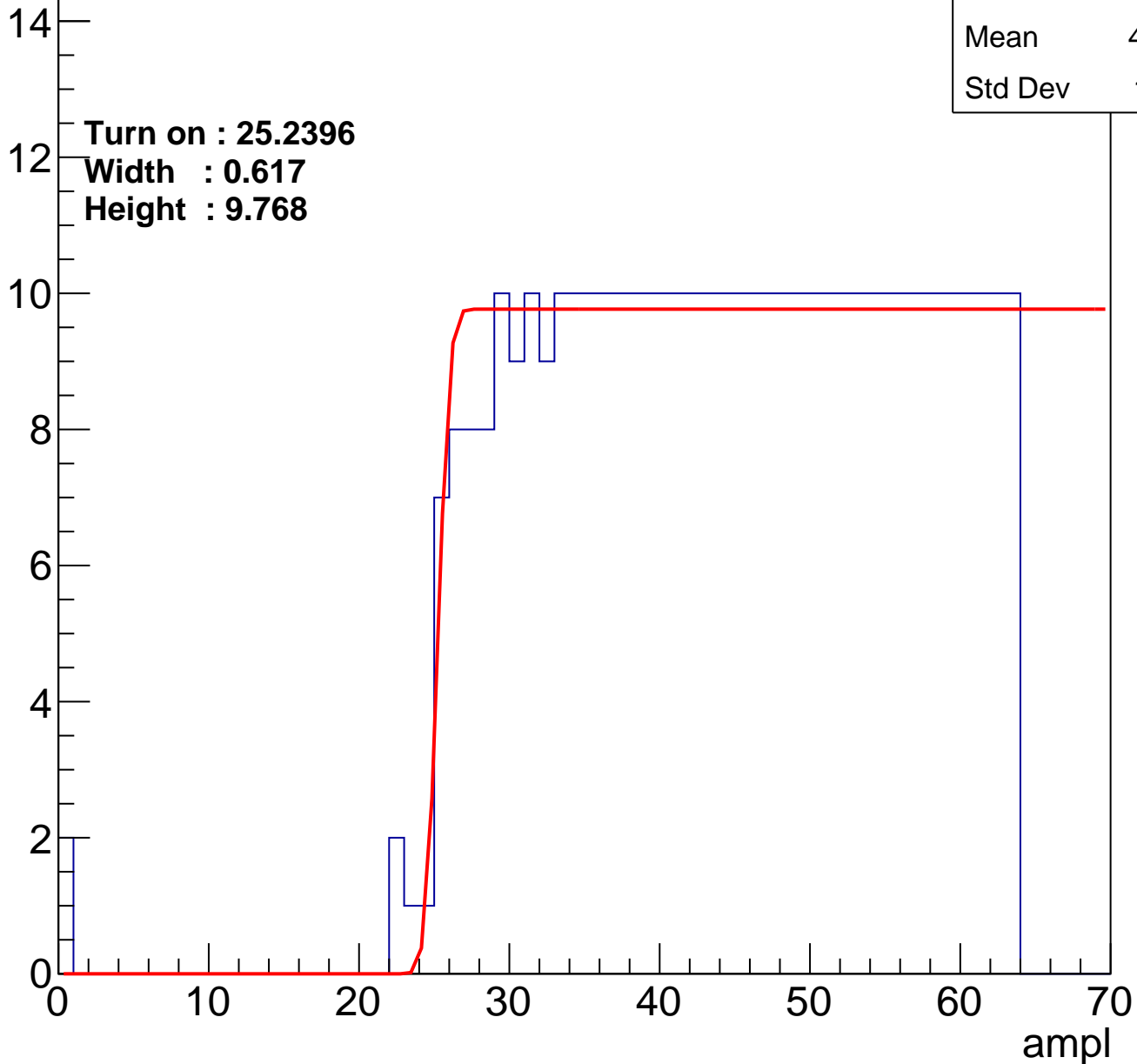
Entry

Entries	385
Mean	44.03
Std Dev	11.61

Turn on : 25.2396

Width : 0.617

Height : 9.768



B1L000S, U17-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.71
Std Dev	11.94

Turn on : 25.7961

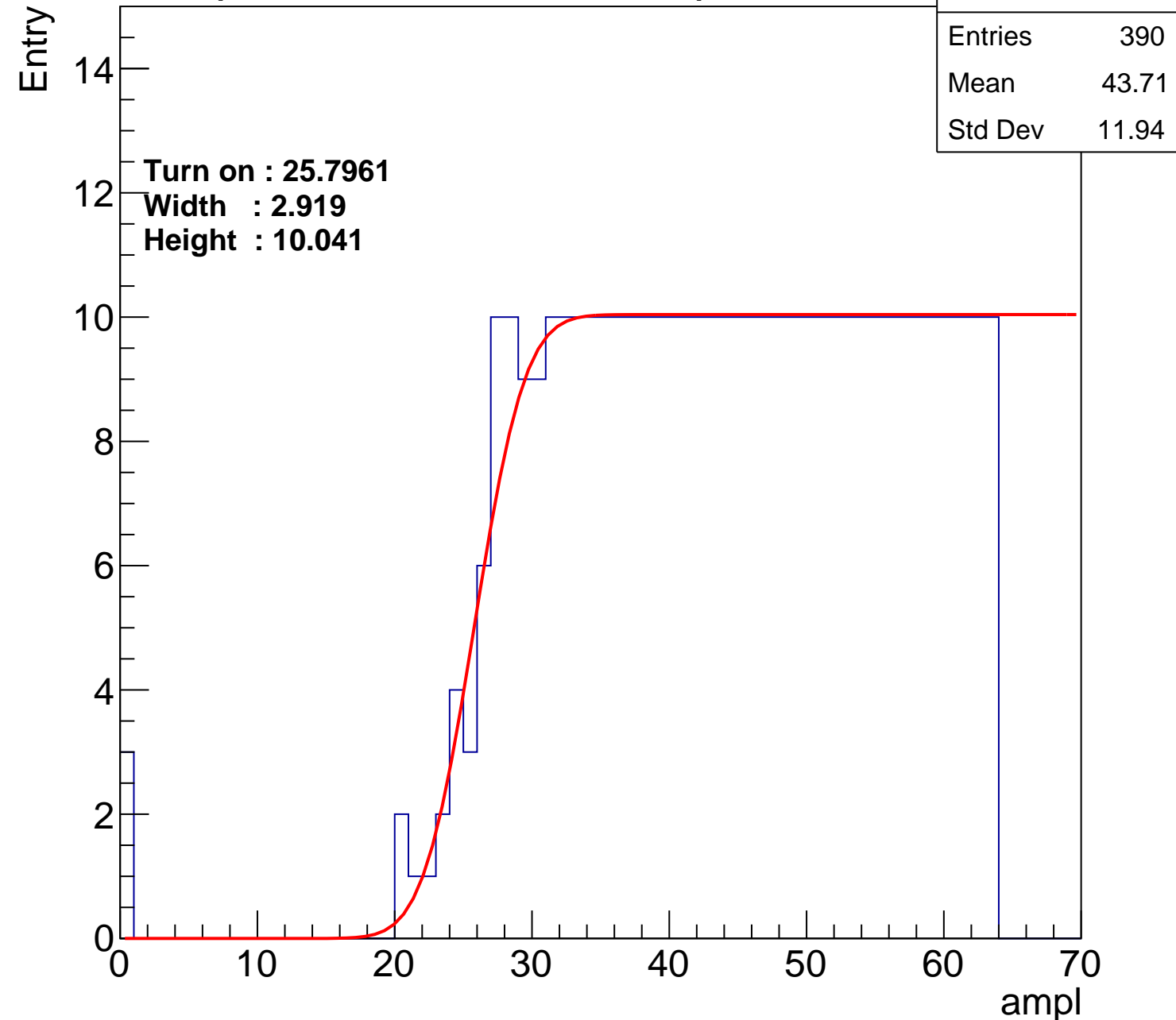
Width : 2.919

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch95

calib_packv5_042523_0143.root, FC#3, port B3

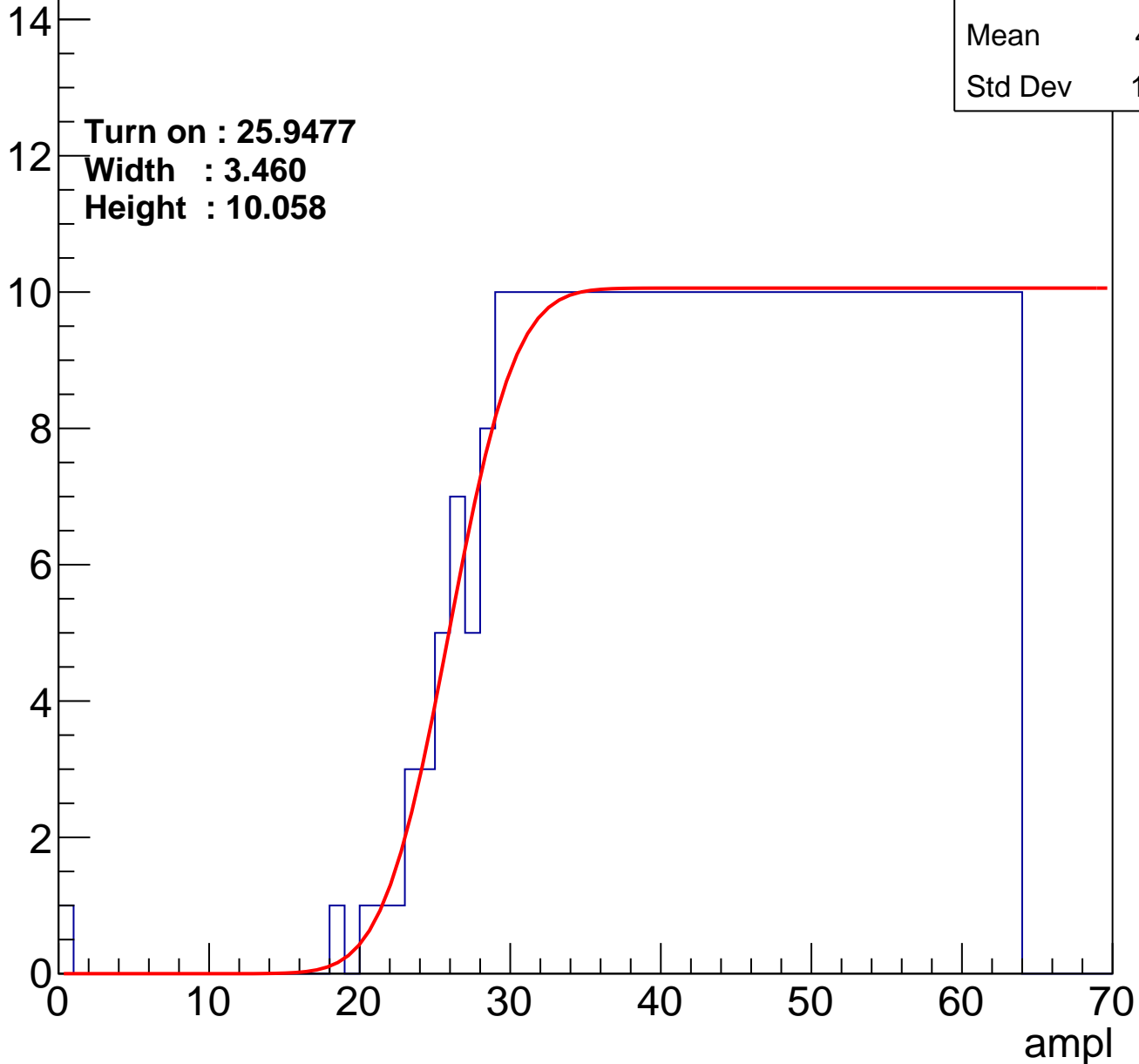
Entries	386
Mean	44.01
Std Dev	11.54

Turn on : 25.9477

Width : 3.460

Height : 10.058

Entry



B1L000S, U17-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.37
Std Dev	12.16

Turn on : 24.5270

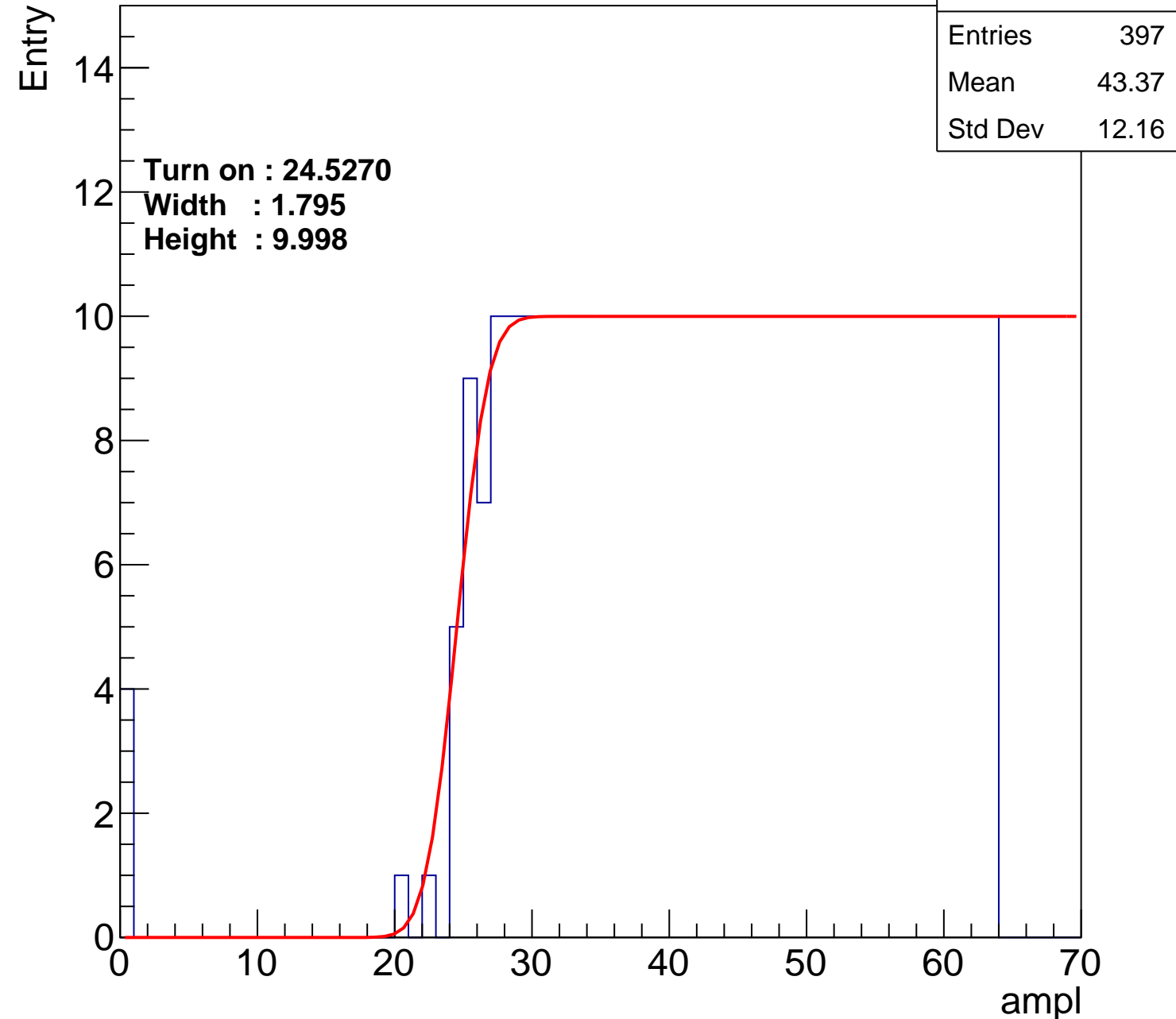
Width : 1.795

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch97

calib_packv5_042523_0143.root, FC#3, port B3

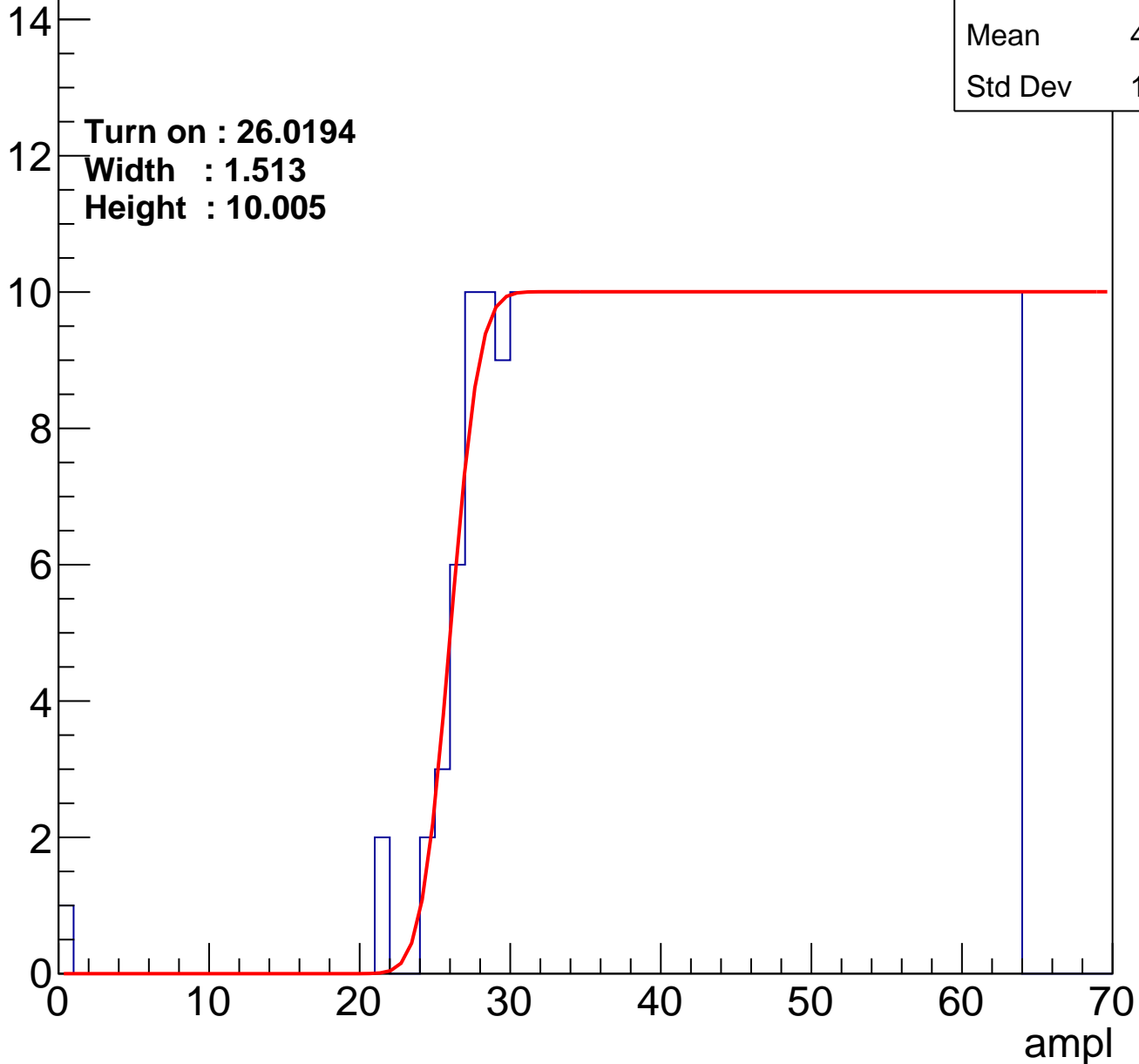
Entries	383
Mean	44.23
Std Dev	11.33

Turn on : 26.0194

Width : 1.513

Height : 10.005

Entry



B1L000S, U17-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.6755

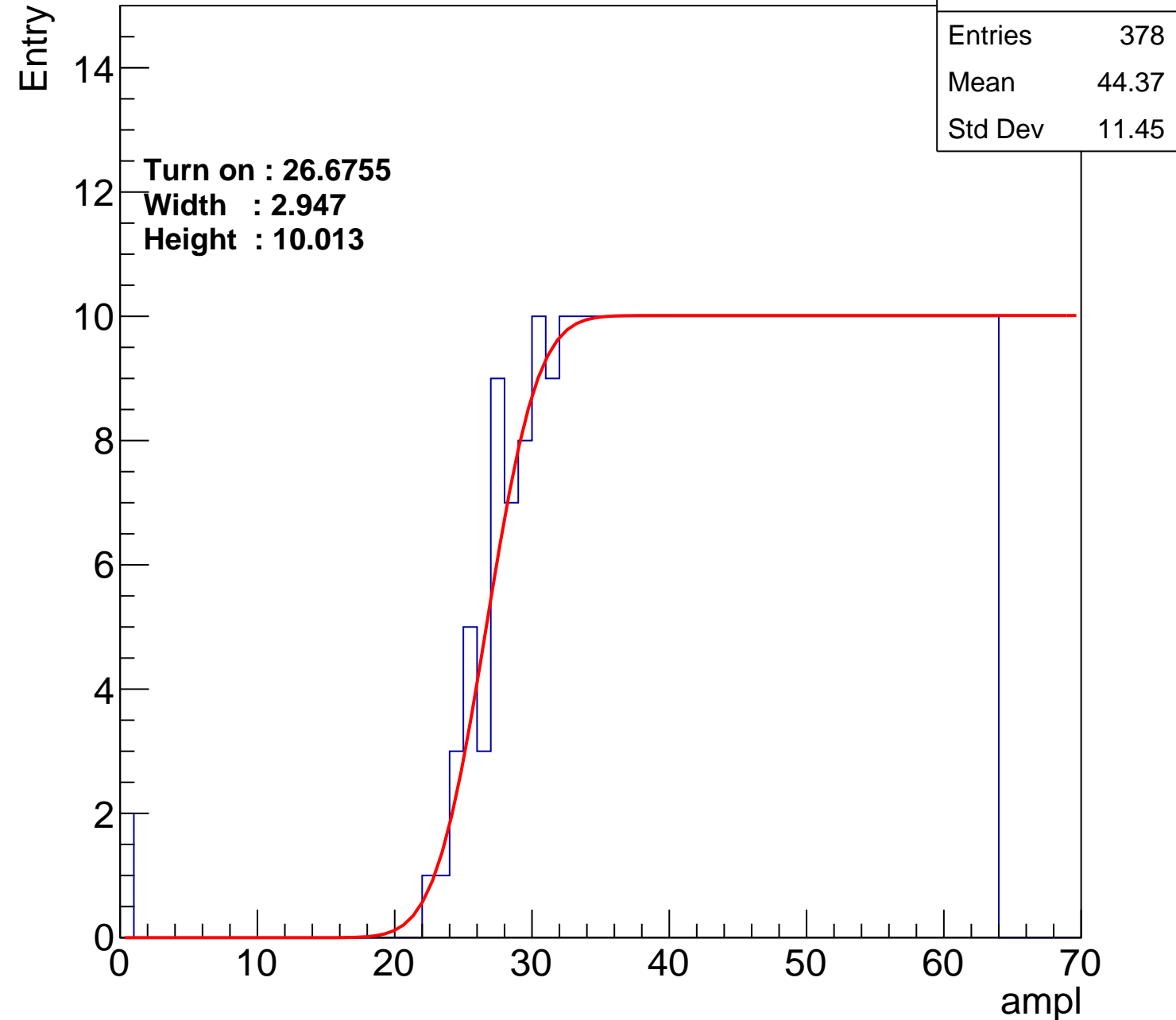
Width : 2.947

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.4
Std Dev	11.58

Turn on : 26.9711

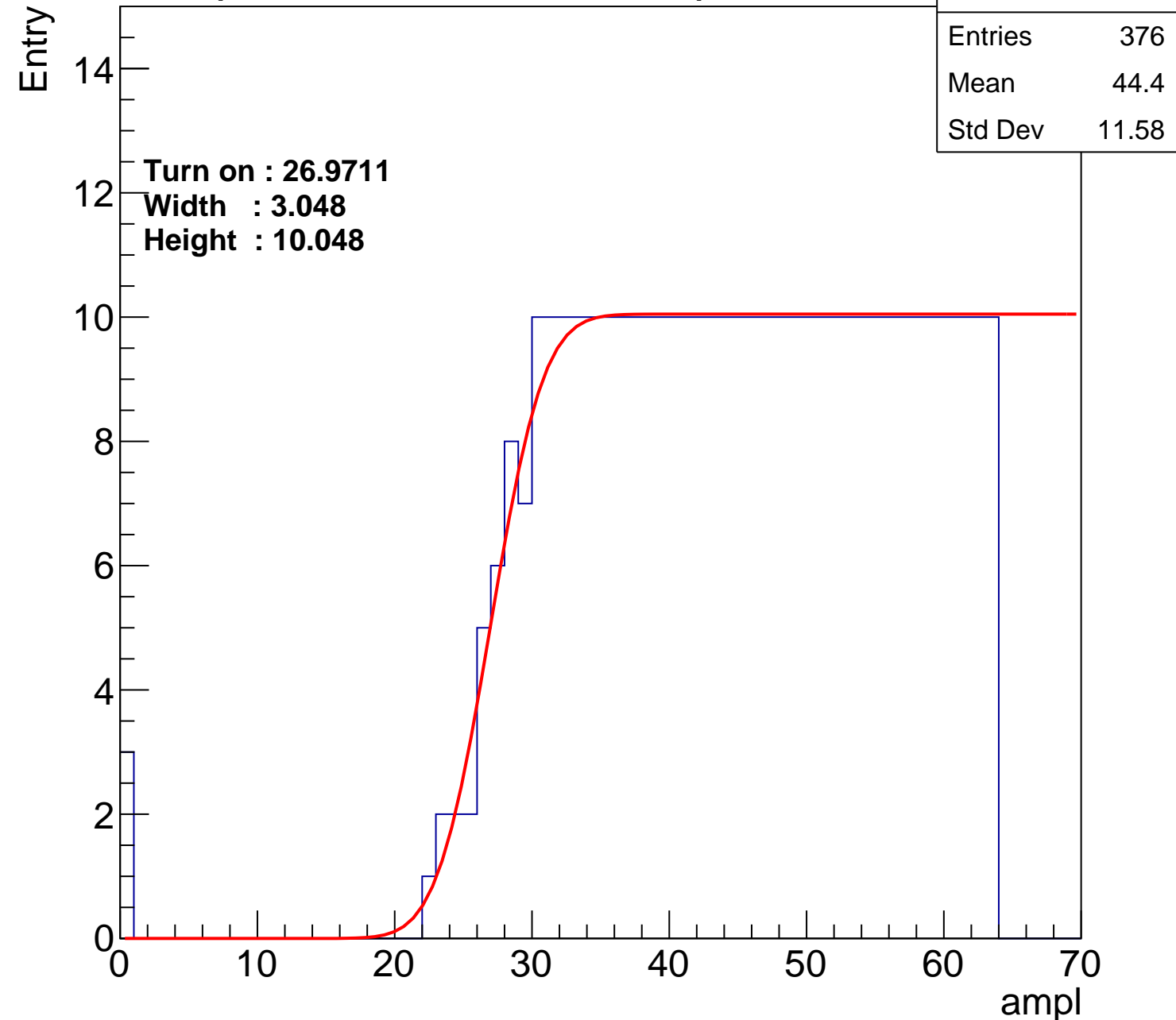
Width : 3.048

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch100

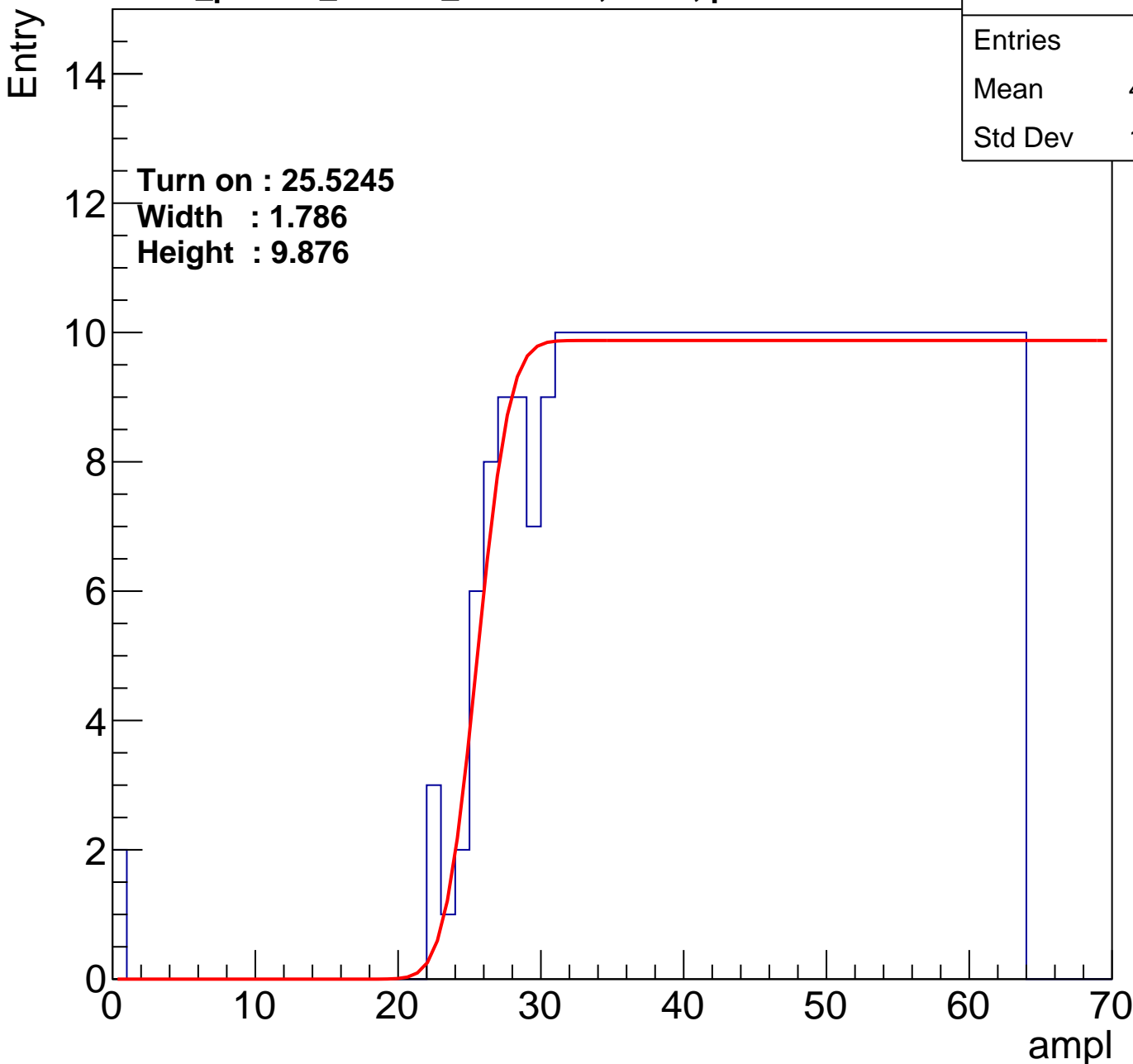
calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.97
Std Dev	11.66

Turn on : 25.5245

Width : 1.786

Height : 9.876



B1L000S, U17-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.8861

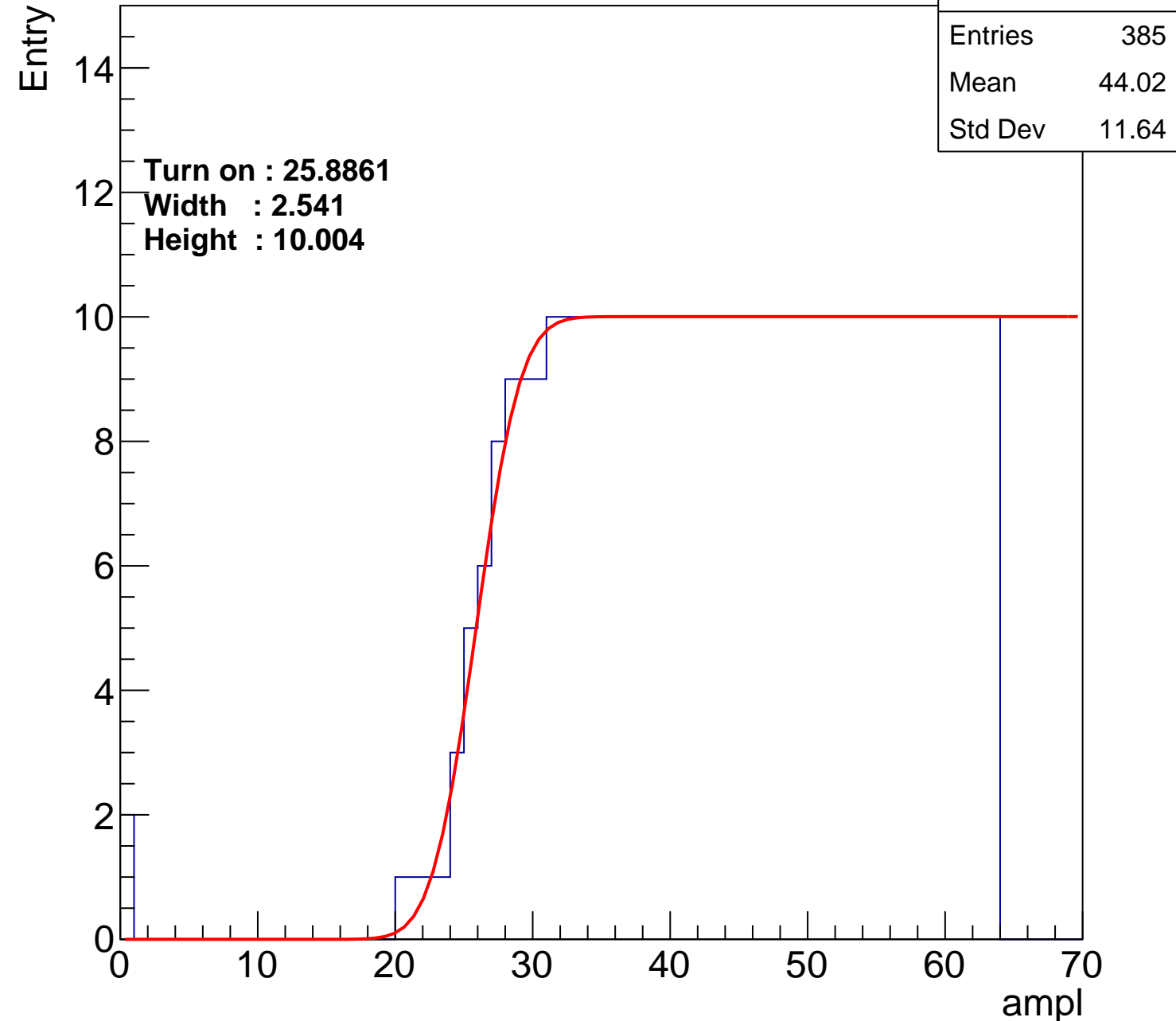
Width : 2.541

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch102

calib_packv5_042523_0143.root, FC#3, port B3

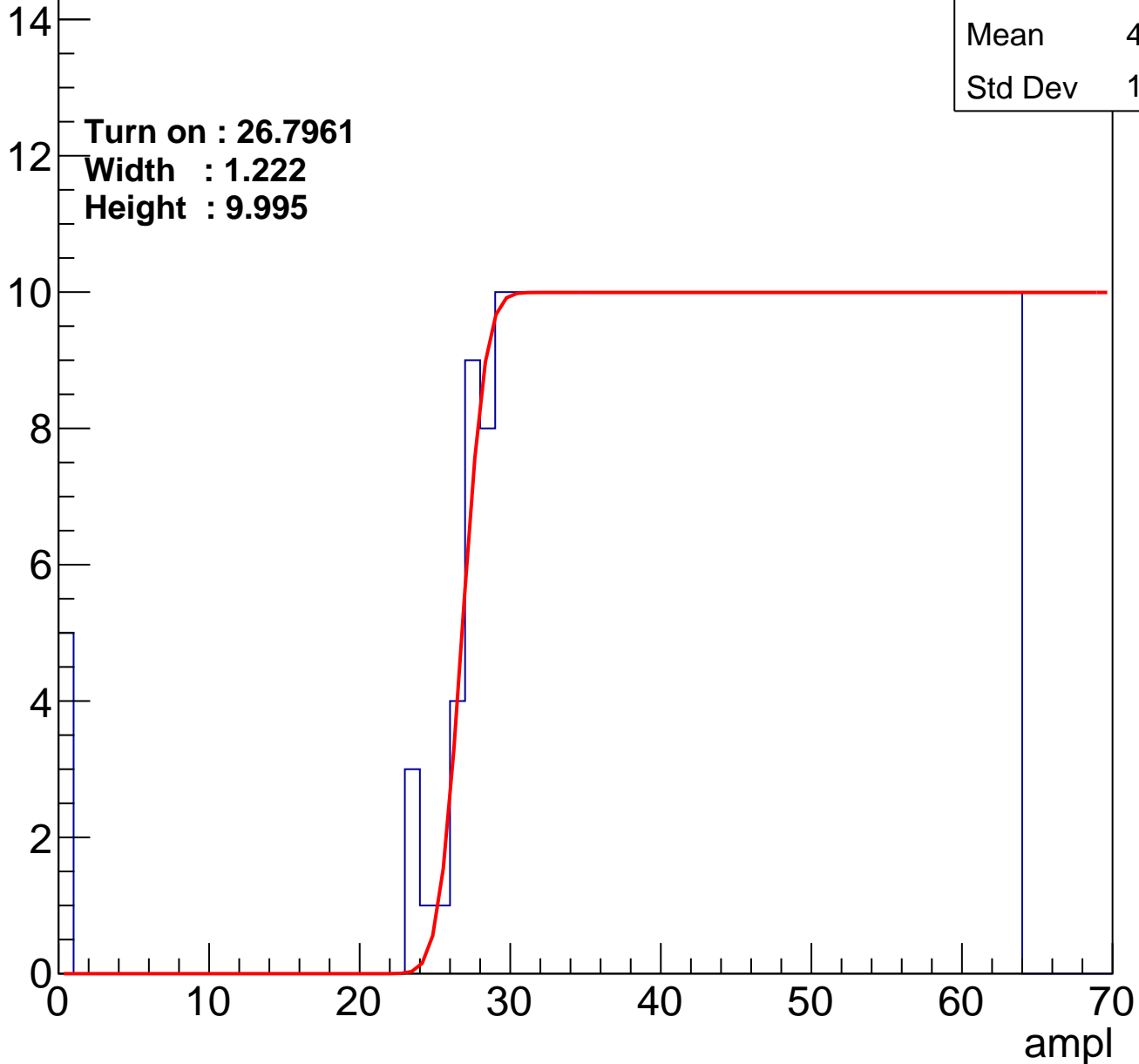
Entries	381
Mean	44.07
Std Dev	11.99

Turn on : 26.7961

Width : 1.222

Height : 9.995

Entry



B1L000S, U17-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.58
Std Dev	11.93

Turn on : 24.8741

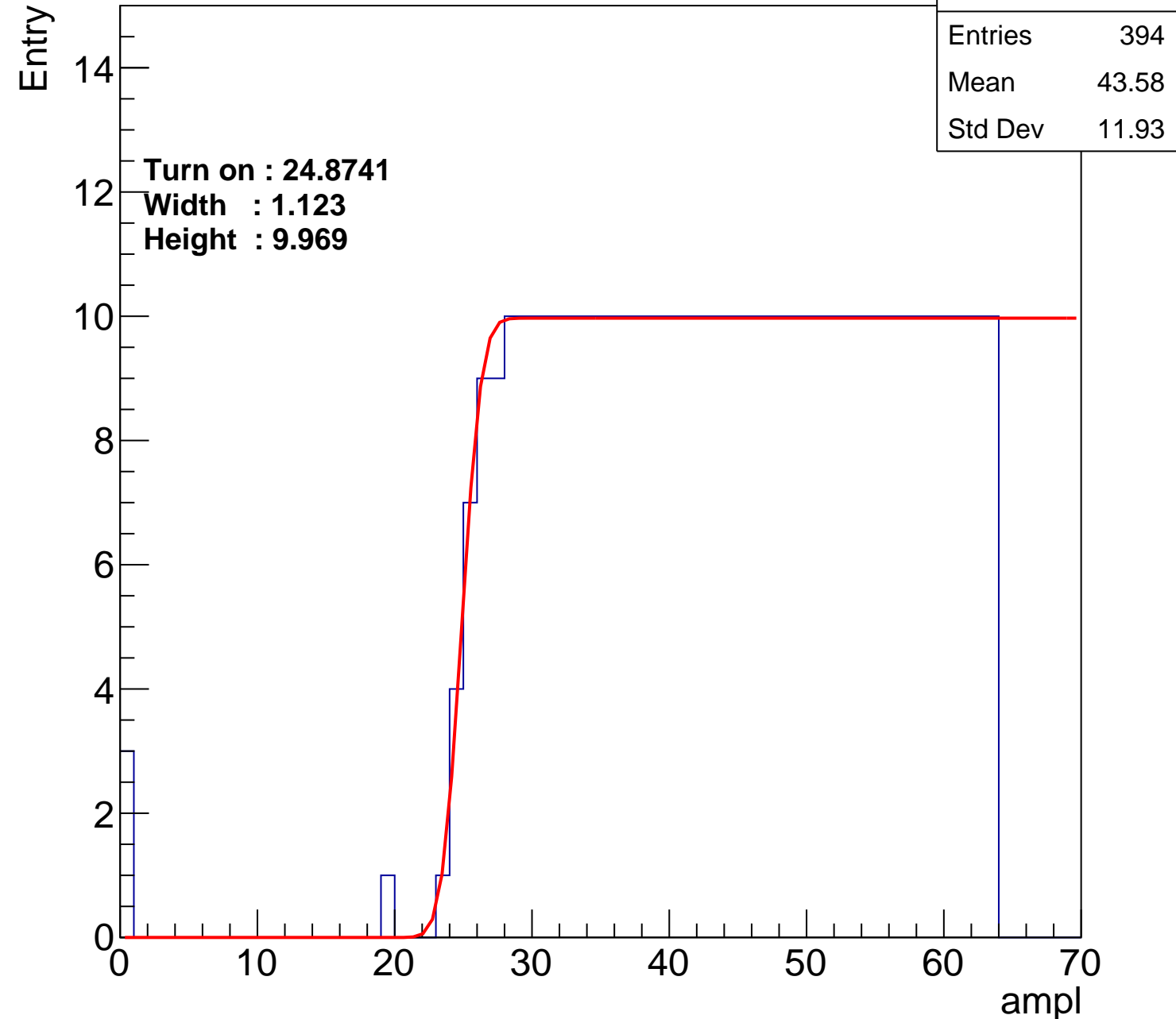
Width : 1.123

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 26.8475

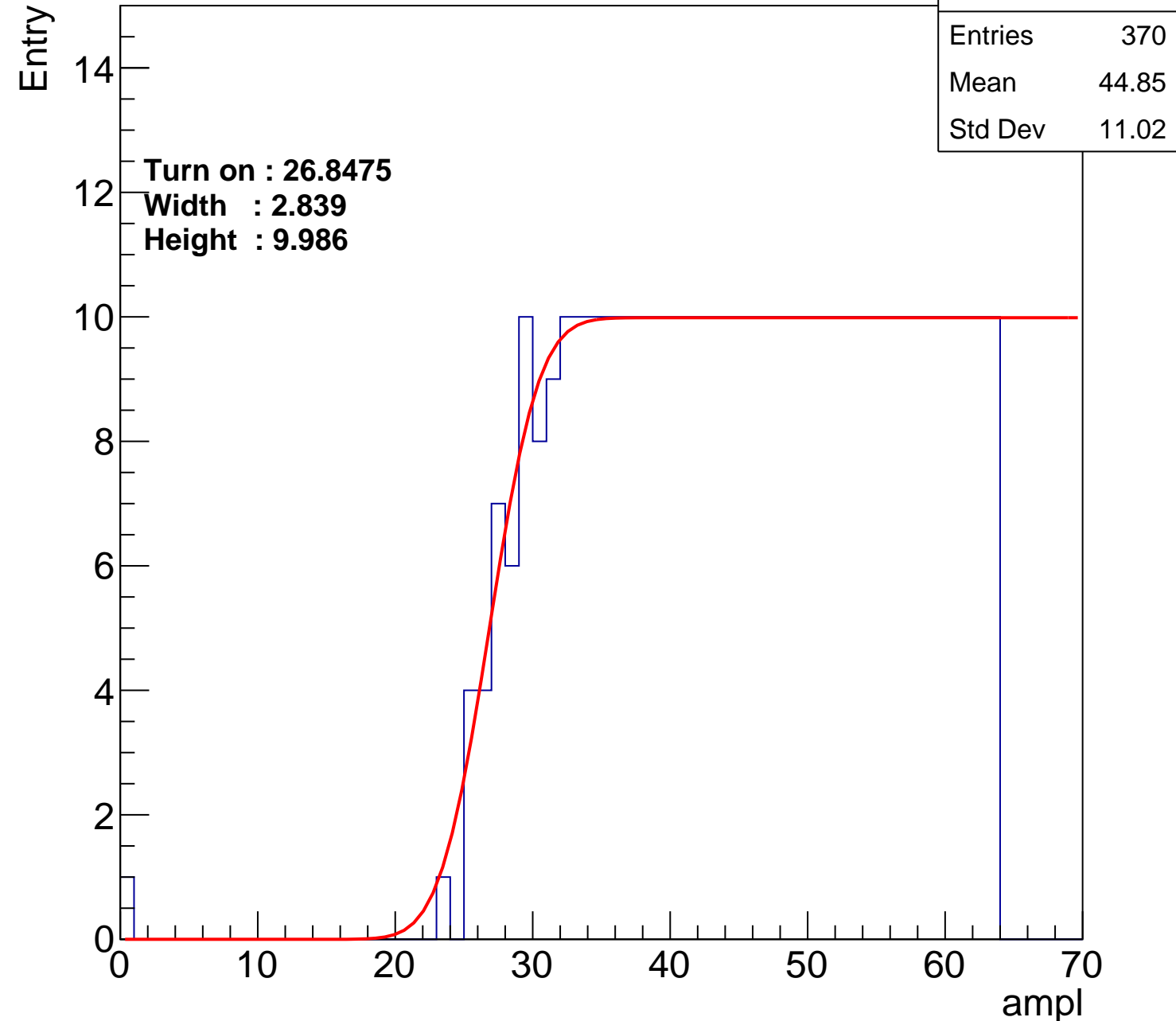
Width : 2.839

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.46
Std Dev	12.07

Turn on : 25.1711

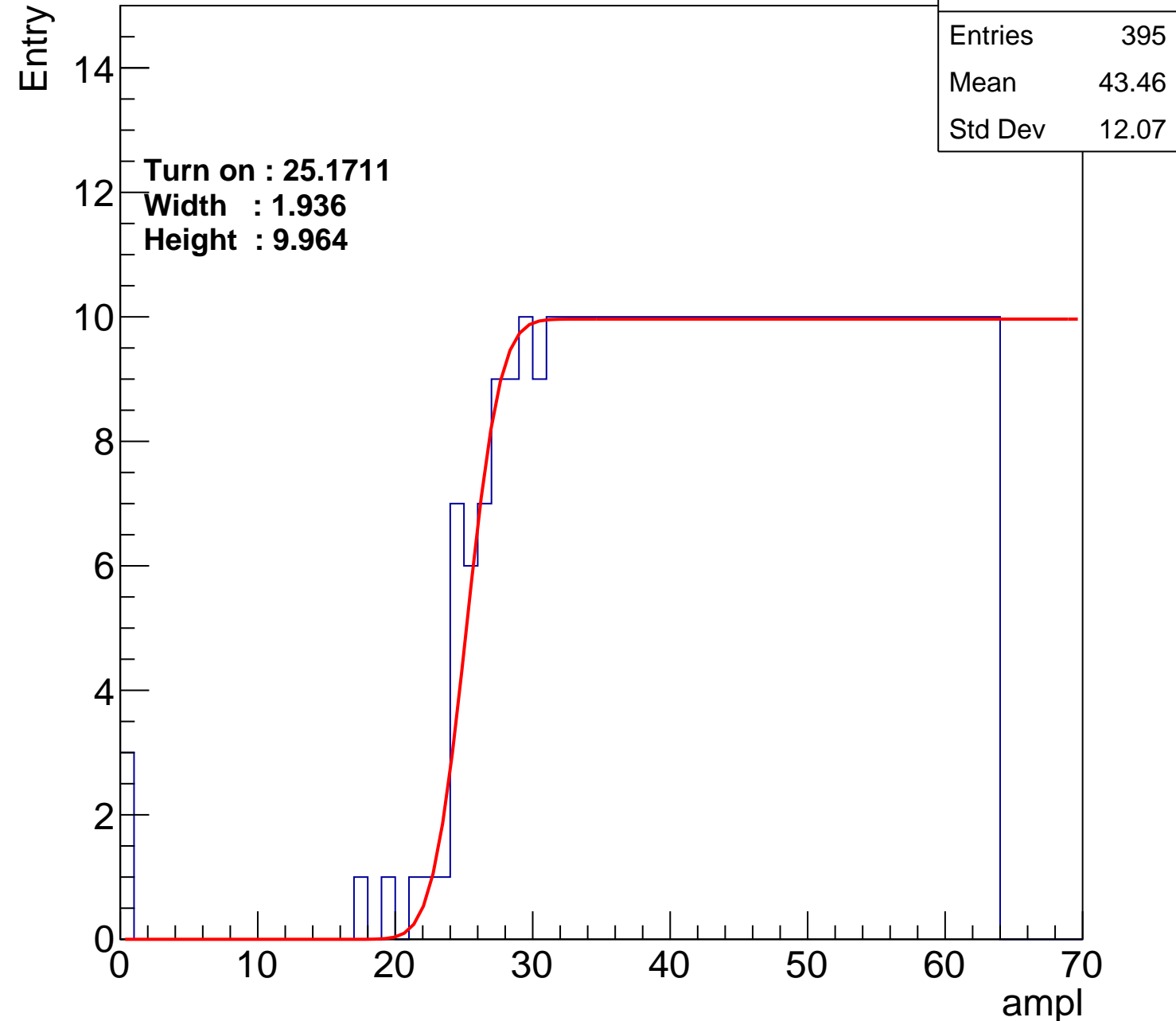
Width : 1.936

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.39
Std Dev	11.83

Turn on : 23.9779

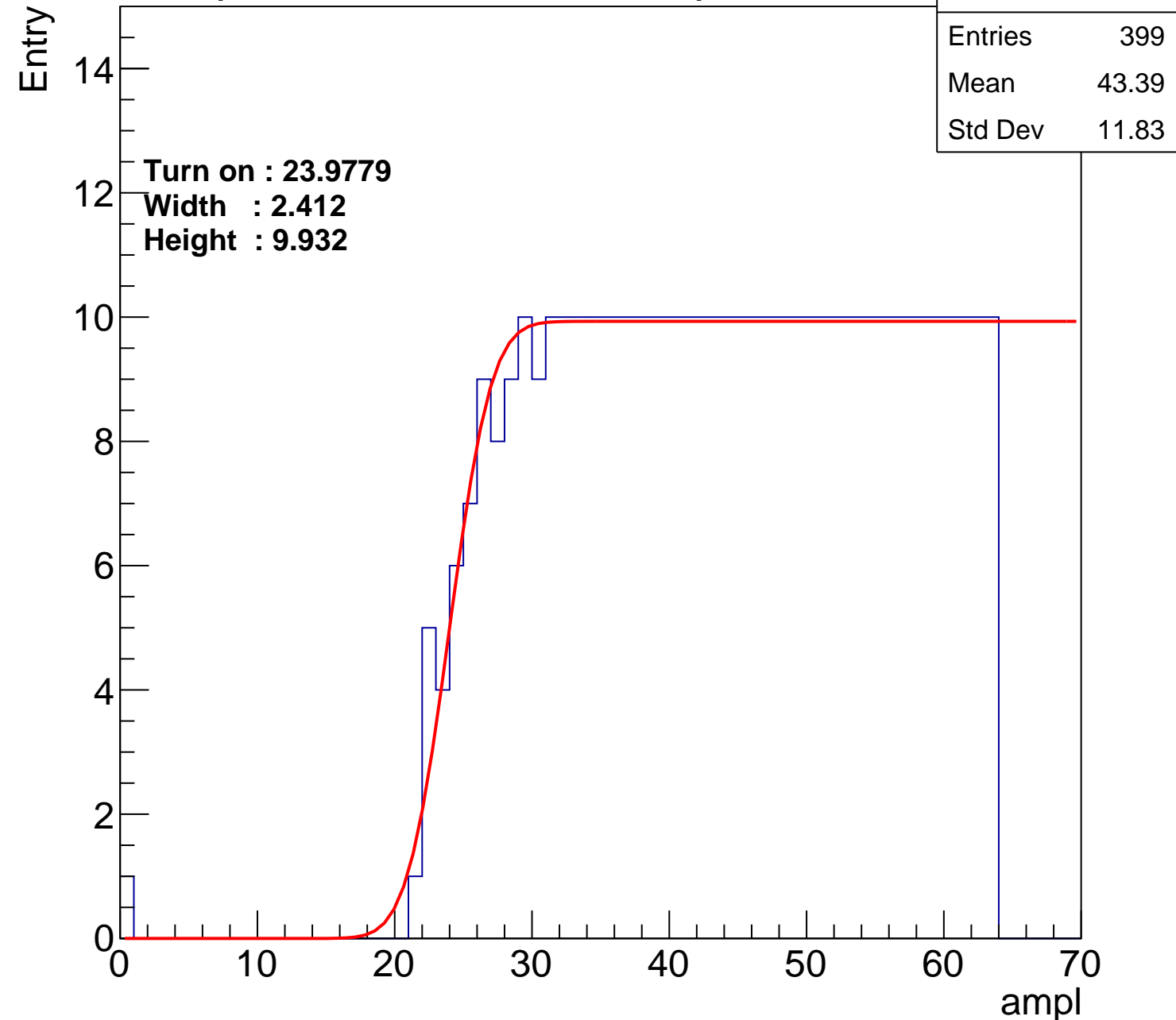
Width : 2.412

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch107

calib_packv5_042523_0143.root, FC#3, port B3

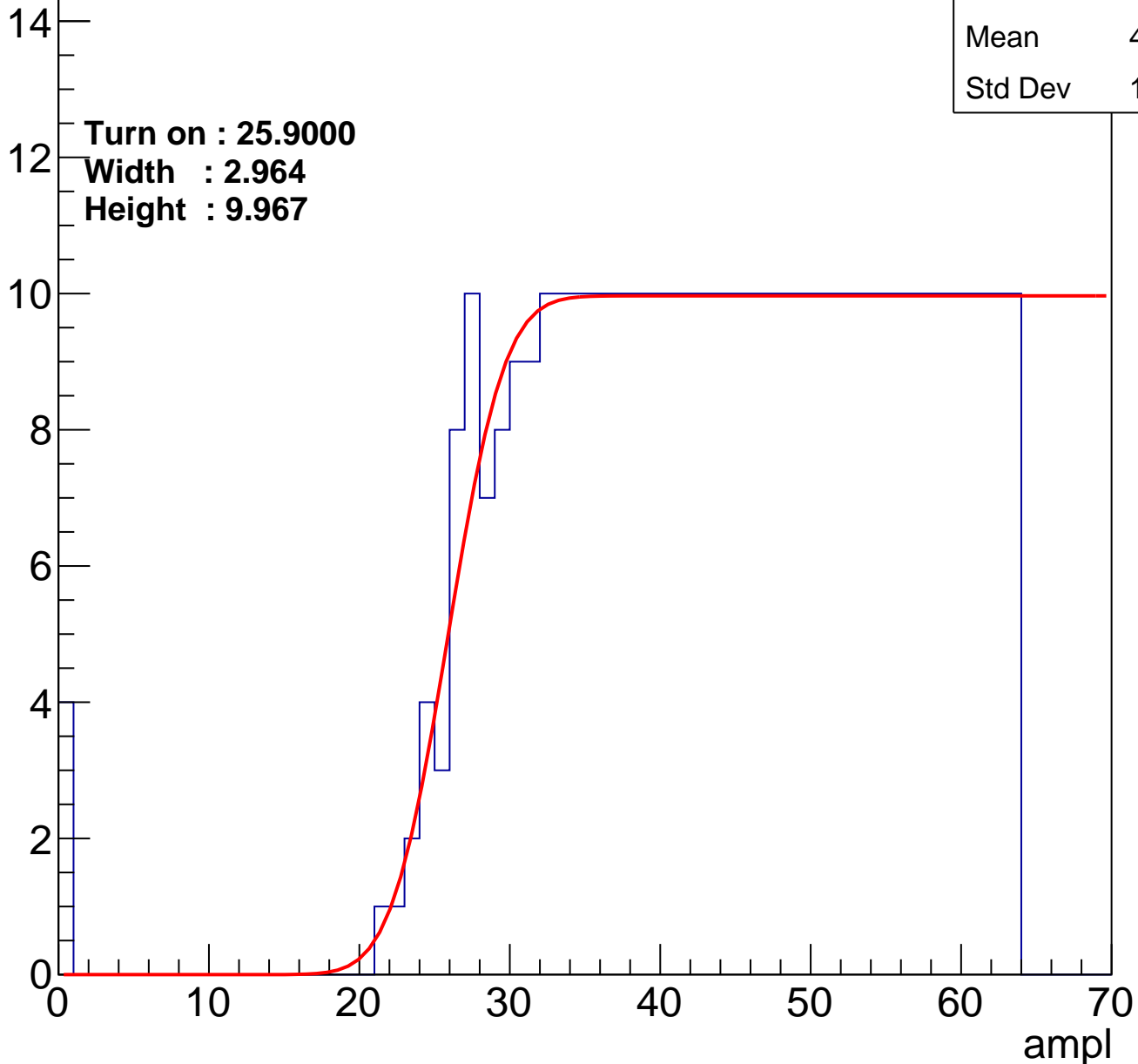
Entries	386
Mean	43.82
Std Dev	12.03

Turn on : 25.9000

Width : 2.964

Height : 9.967

Entry



B1L000S, U17-ch108

calib_packv5_042523_0143.root, FC#3, port B3

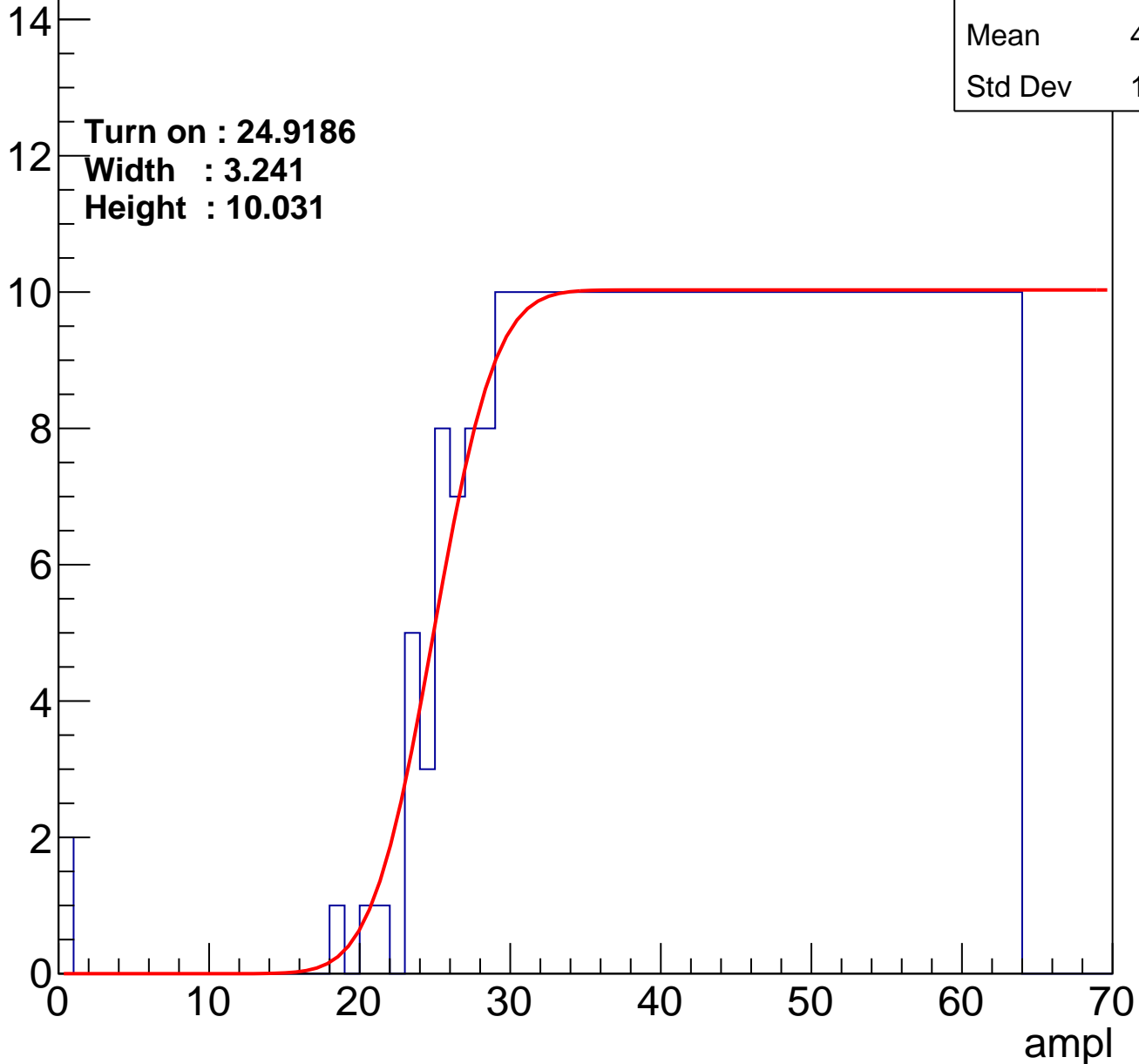
Entries	394
Mean	43.57
Std Dev	11.88

Turn on : 24.9186

Width : 3.241

Height : 10.031

Entry



B1L000S, U17-ch109

calib_packv5_042523_0143.root, FC#3, port B3

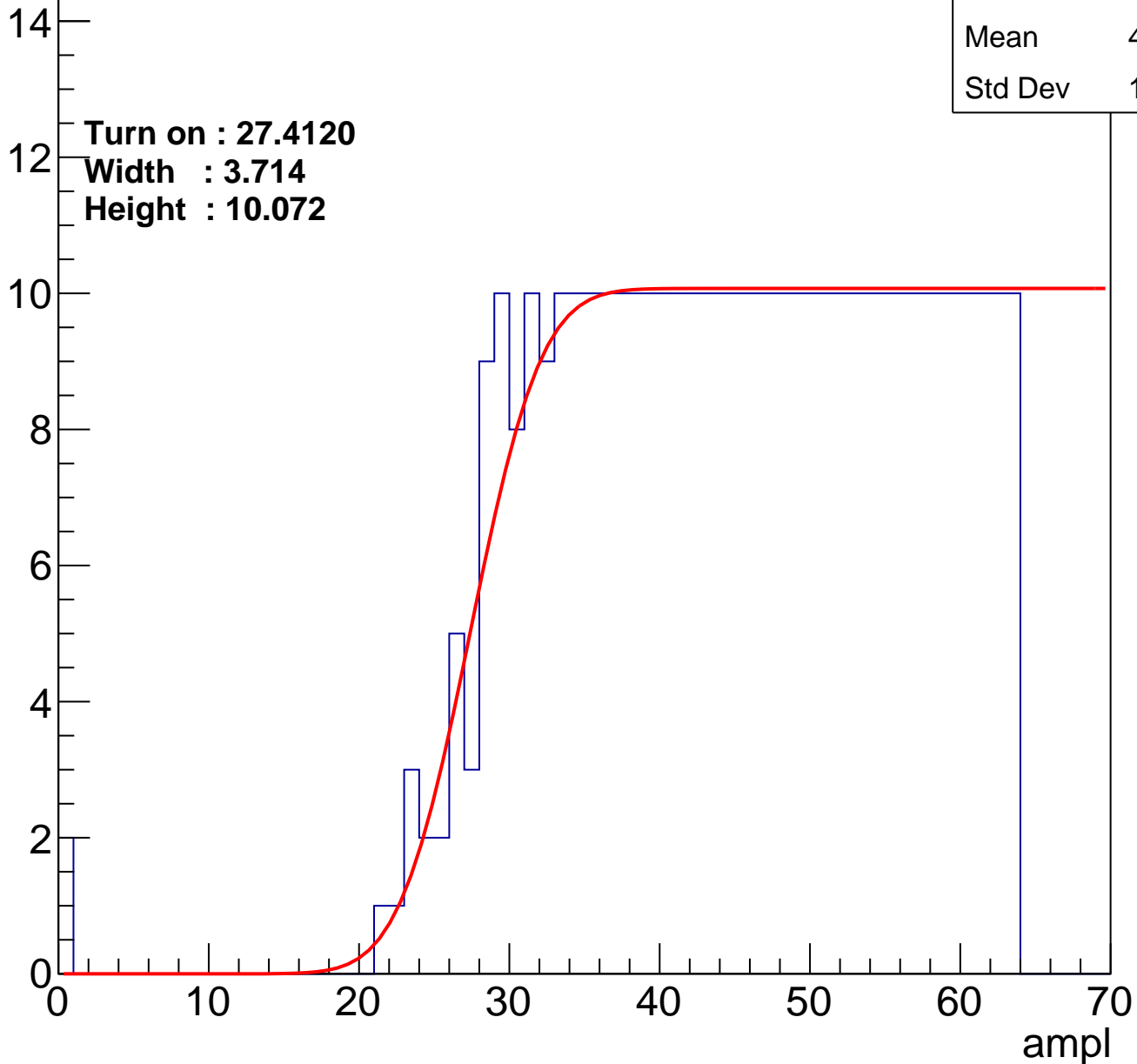
Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 27.4120

Width : 3.714

Height : 10.072

Entry



B1L000S, U17-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.8209

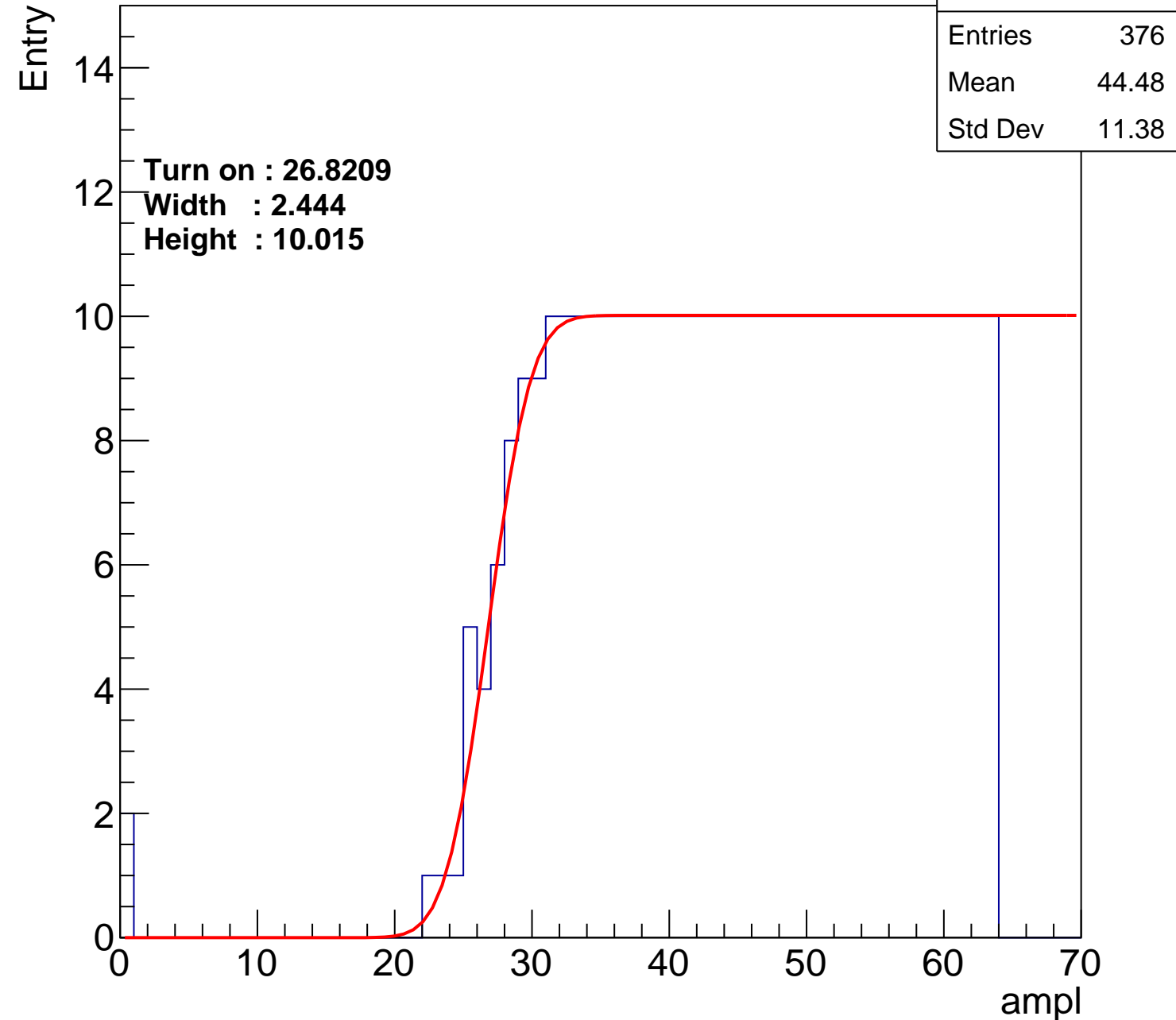
Width : 2.444

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.03
Std Dev	11.87

Turn on : 26.2198

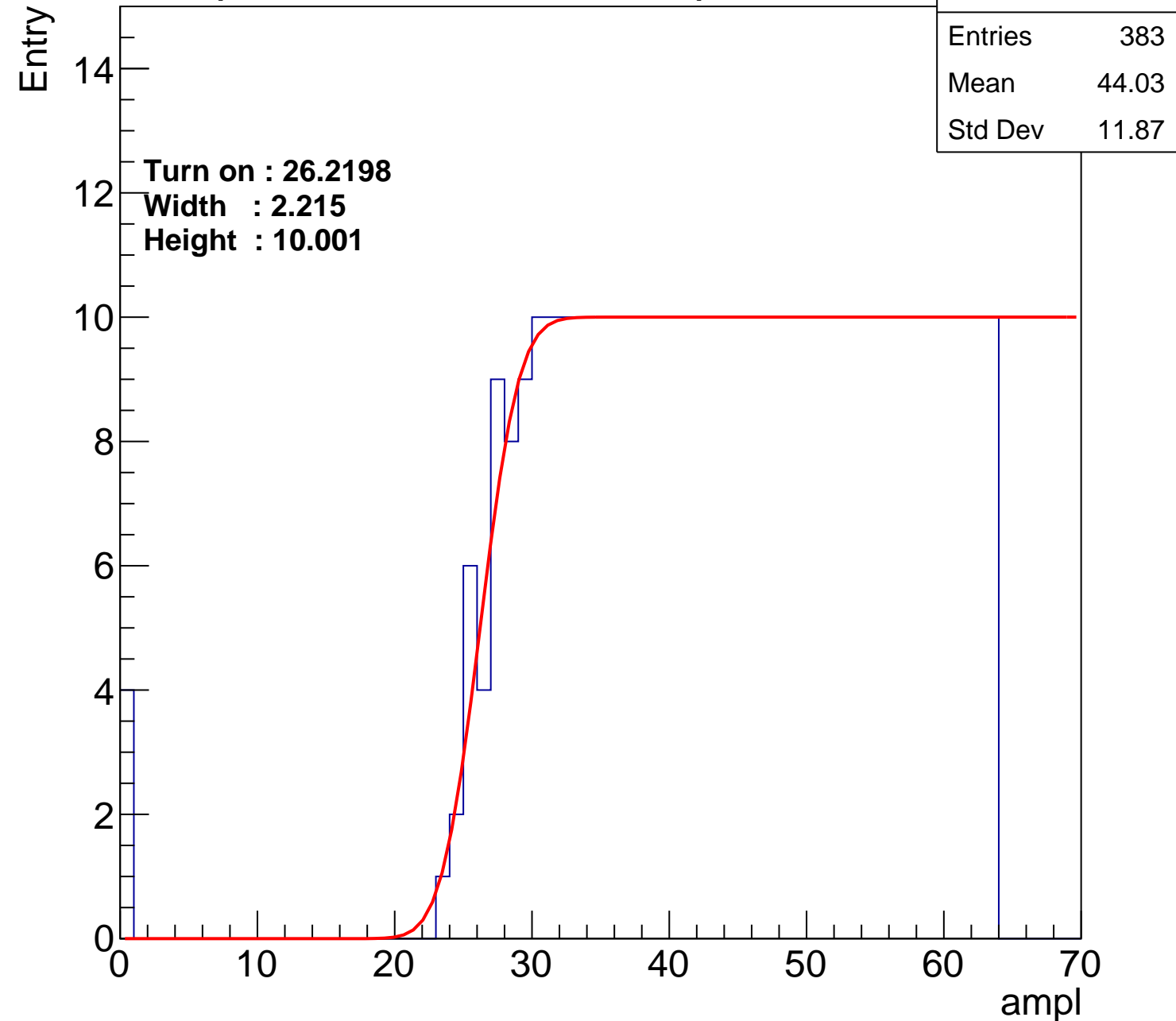
Width : 2.215

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.2
Std Dev	11.43

Turn on : 26.2052

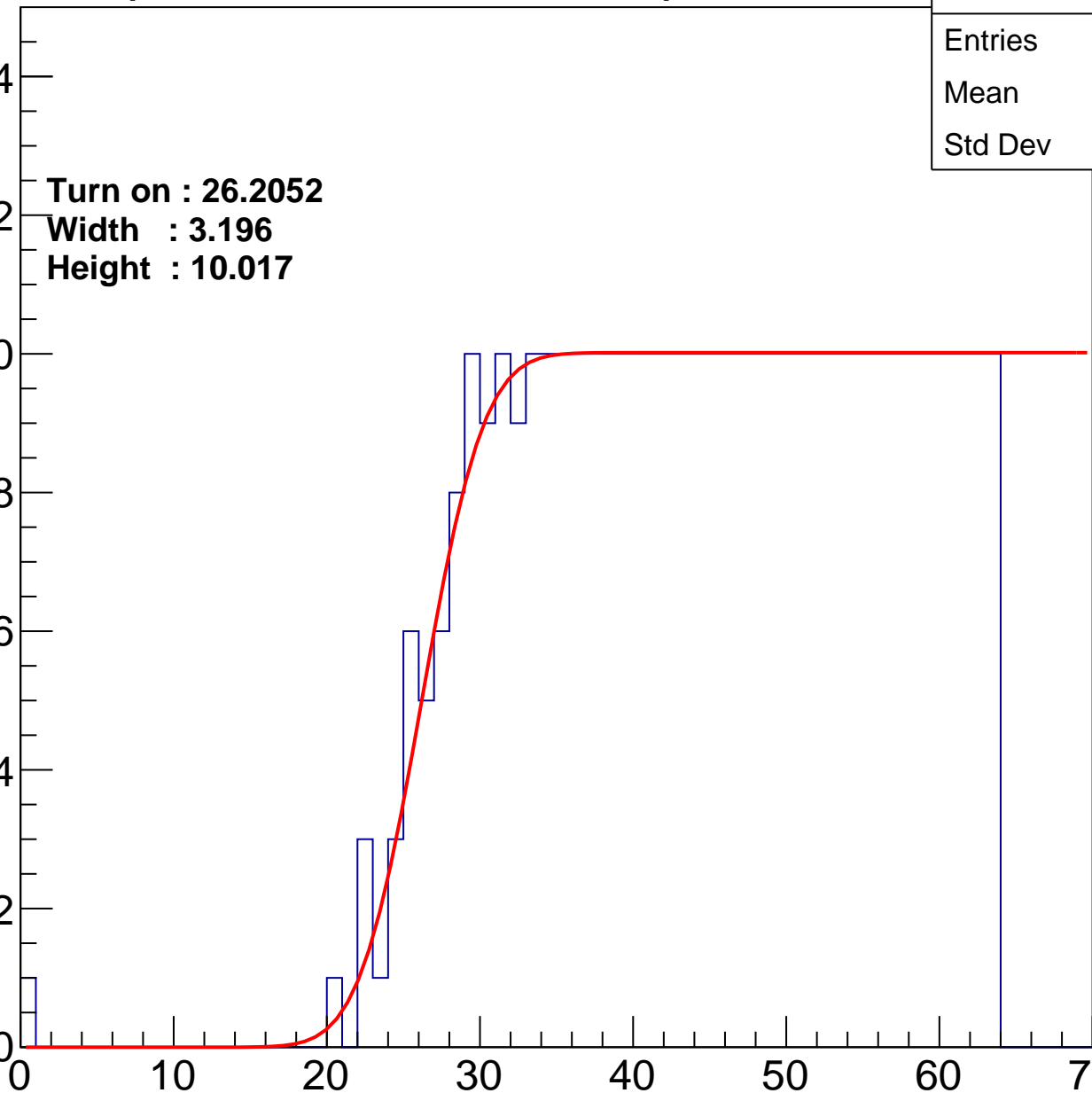
Width : 3.196

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 25.8420

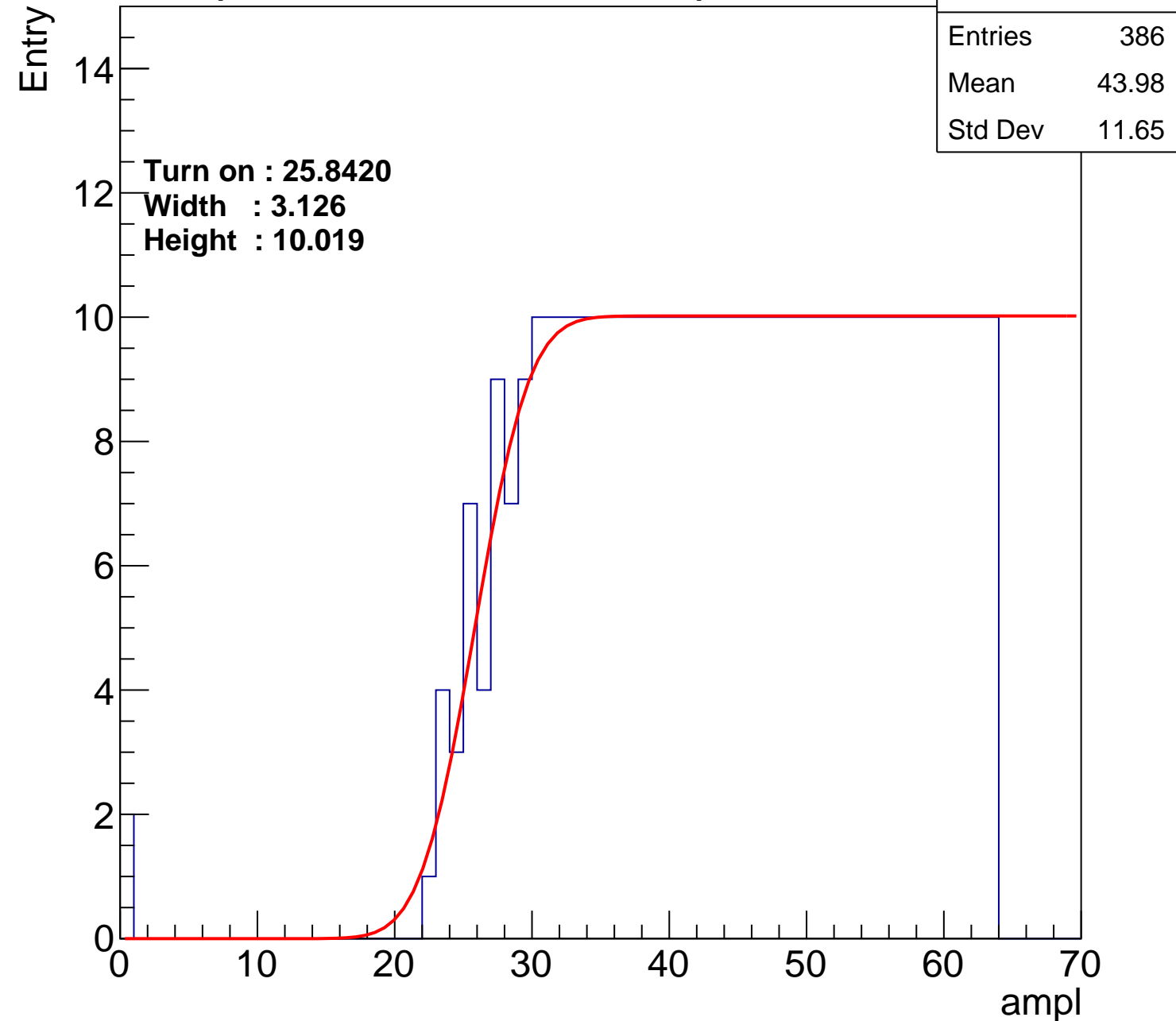
Width : 3.126

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.85
Std Dev	11.82

Turn on : 25.6323

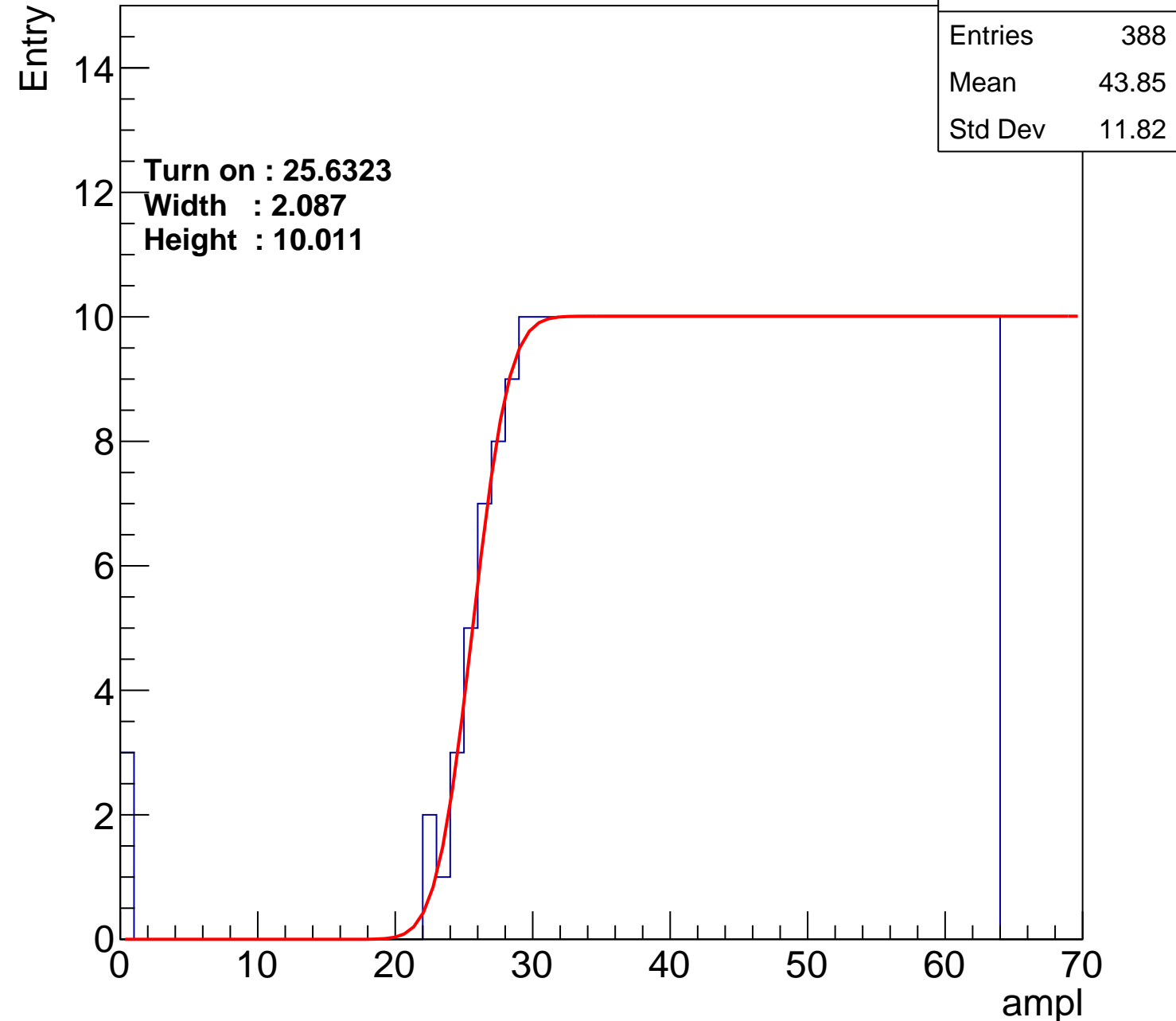
Width : 2.087

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch115

calib_packv5_042523_0143.root, FC#3, port B3

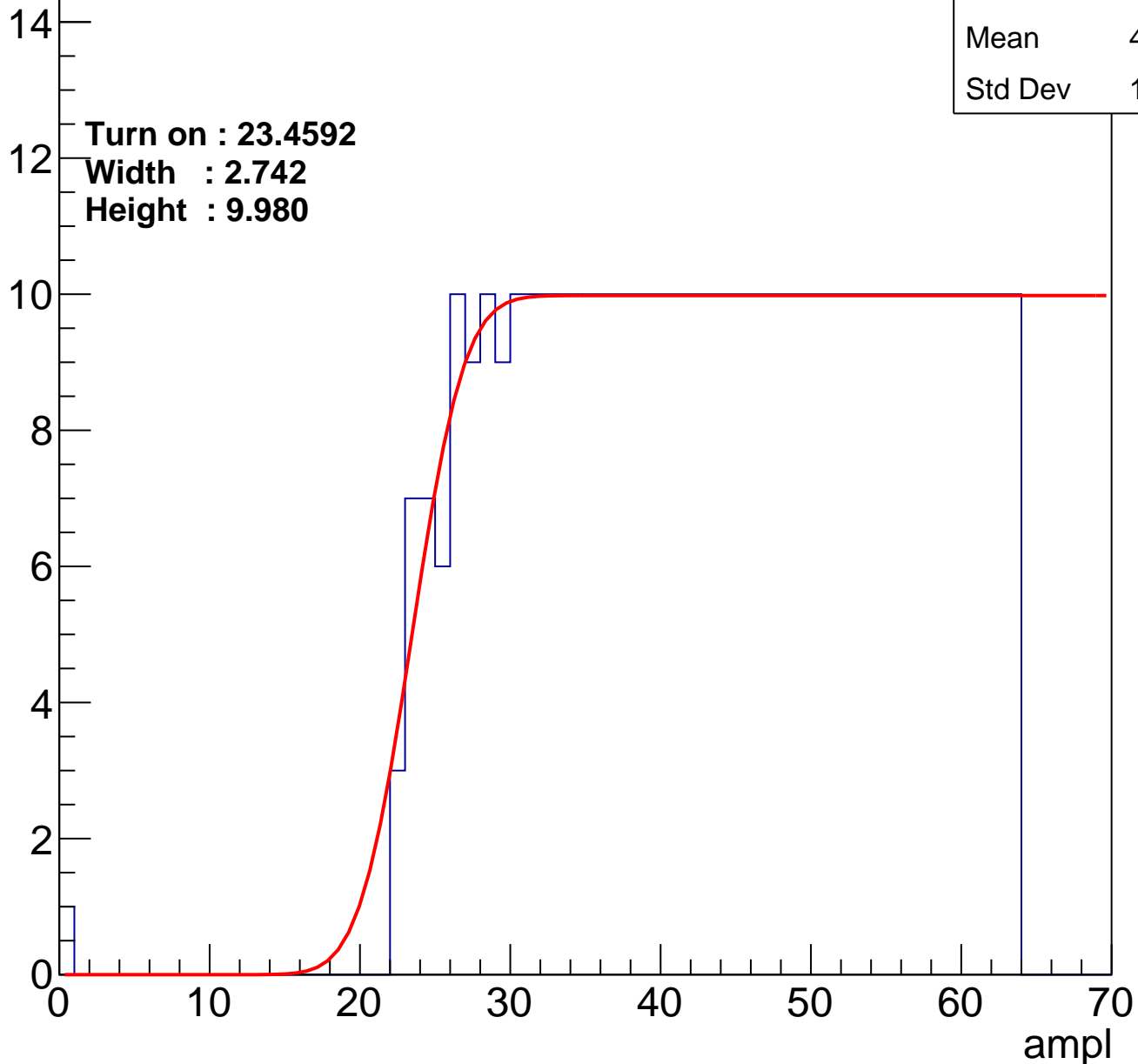
Entries	402
Mean	43.28
Std Dev	11.86

Turn on : 23.4592

Width : 2.742

Height : 9.980

Entry



B1L000S, U17-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.59
Std Dev	11.36

Turn on : 26.8556

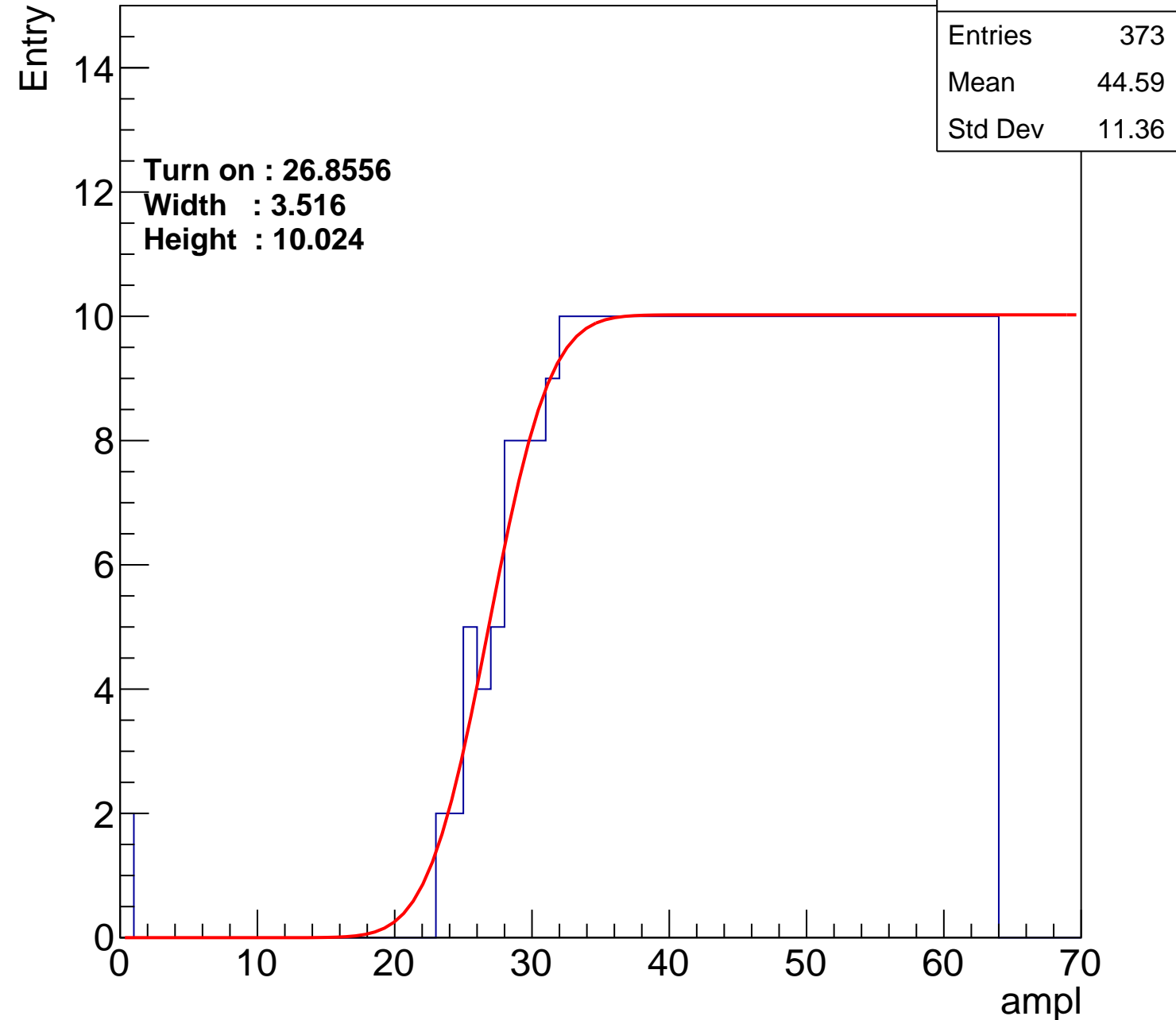
Width : 3.516

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 26.9547

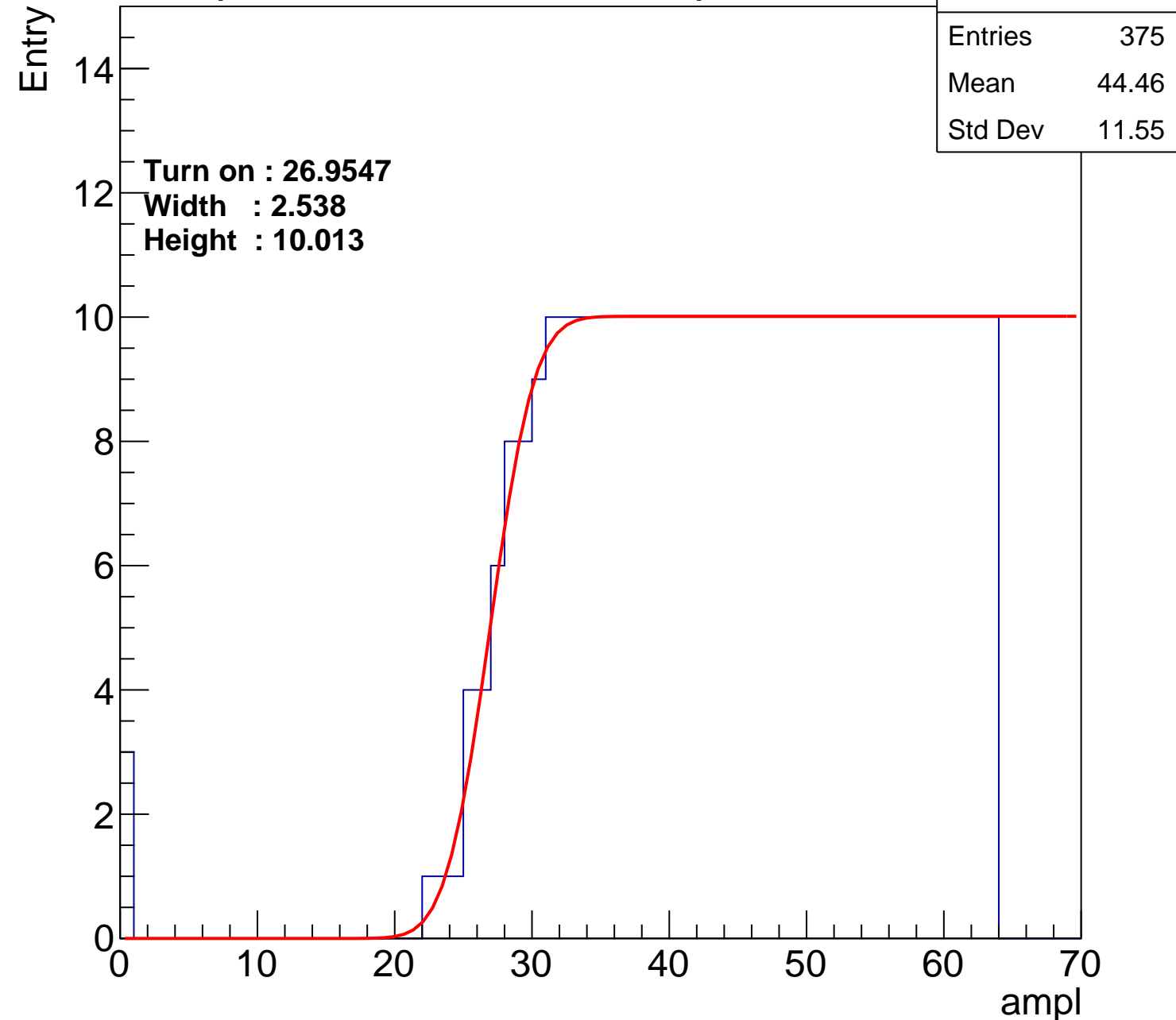
Width : 2.538

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch118

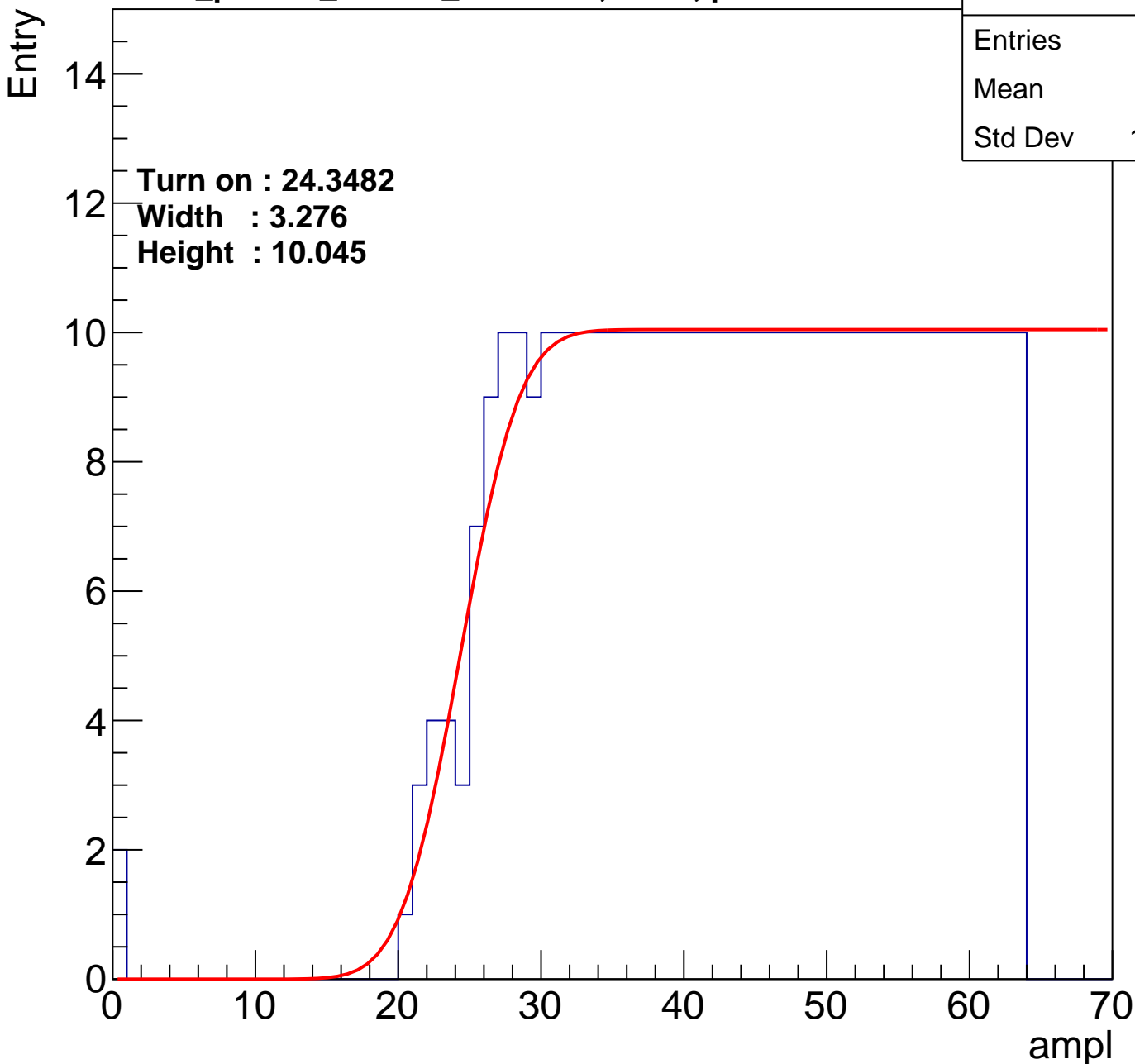
calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.2
Std Dev	12.06

Turn on : 24.3482

Width : 3.276

Height : 10.045



B1L000S, U17-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 25.9260

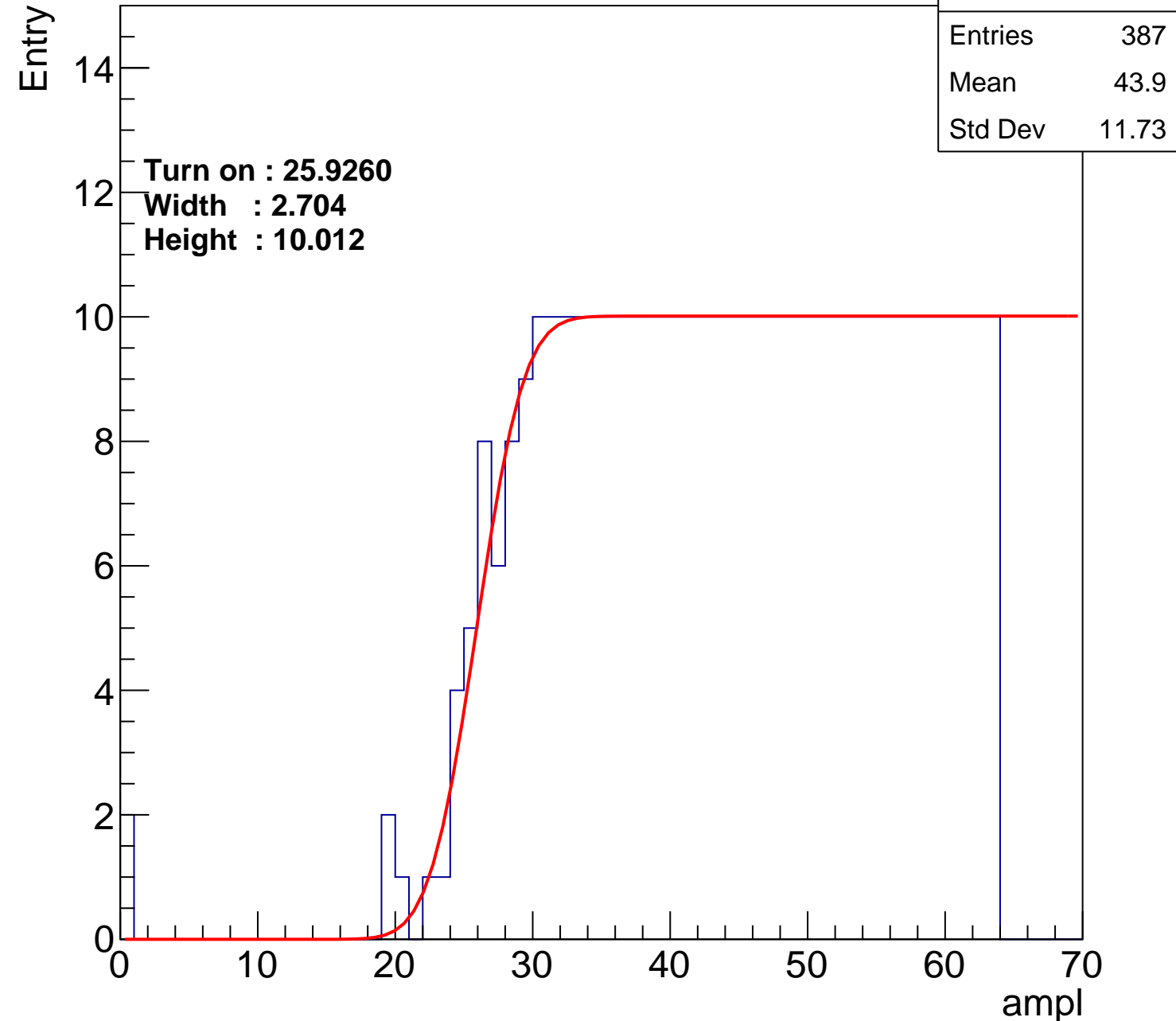
Width : 2.704

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.73
Std Dev	12.2

Turn on : 26.0350

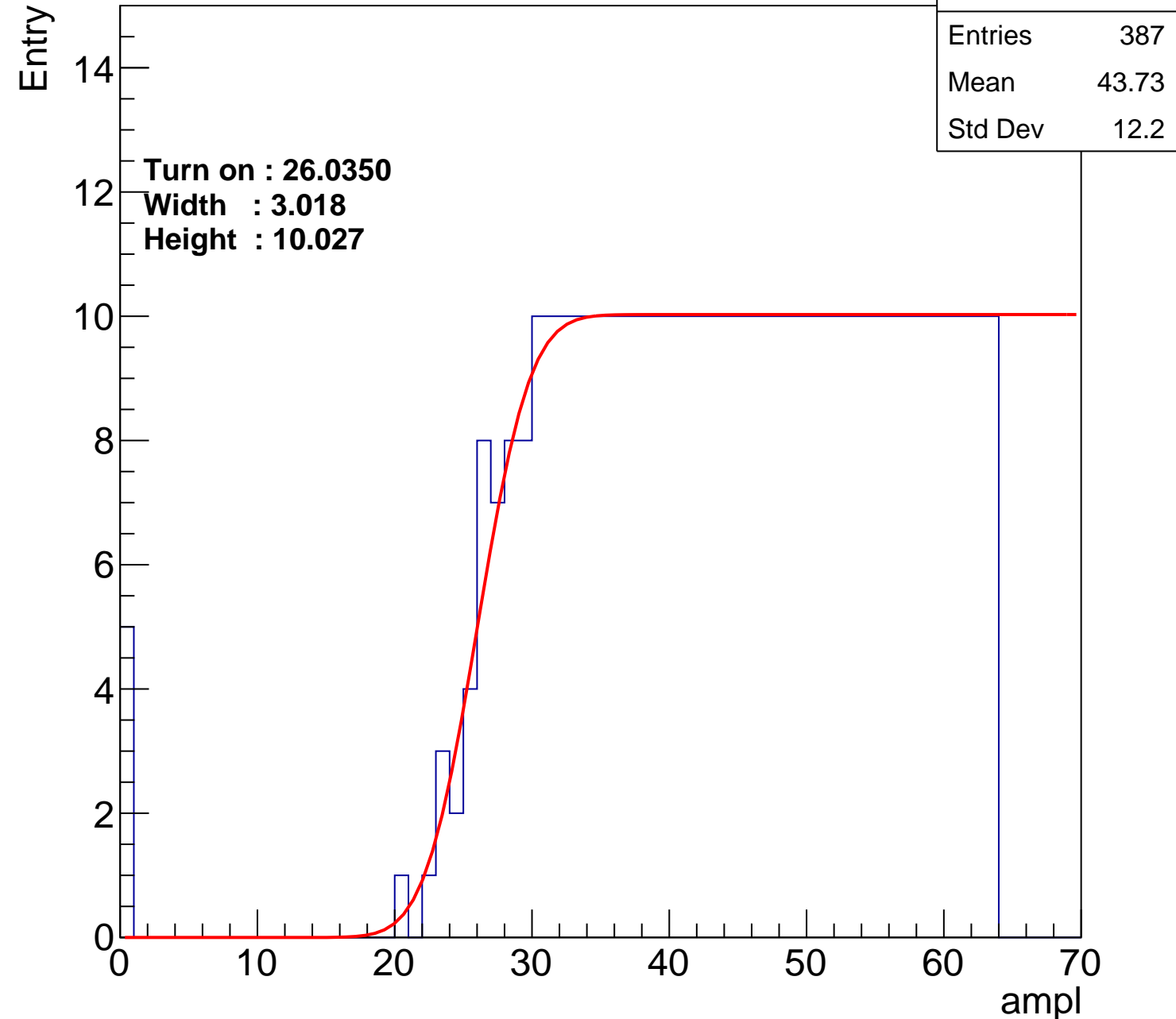
Width : 3.018

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.32
Std Dev	12.14

Turn on : 24.8619

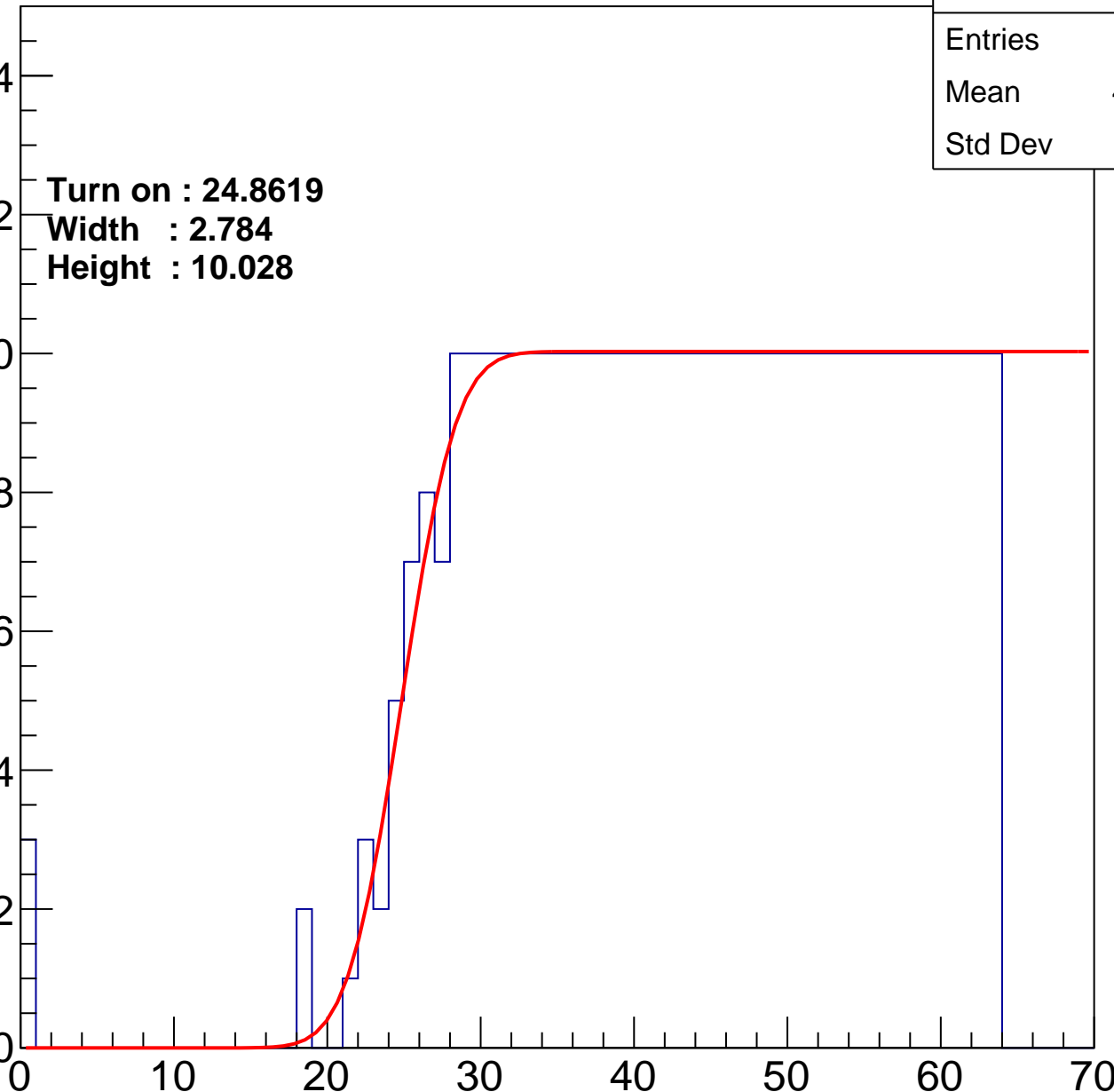
Width : 2.784

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch122

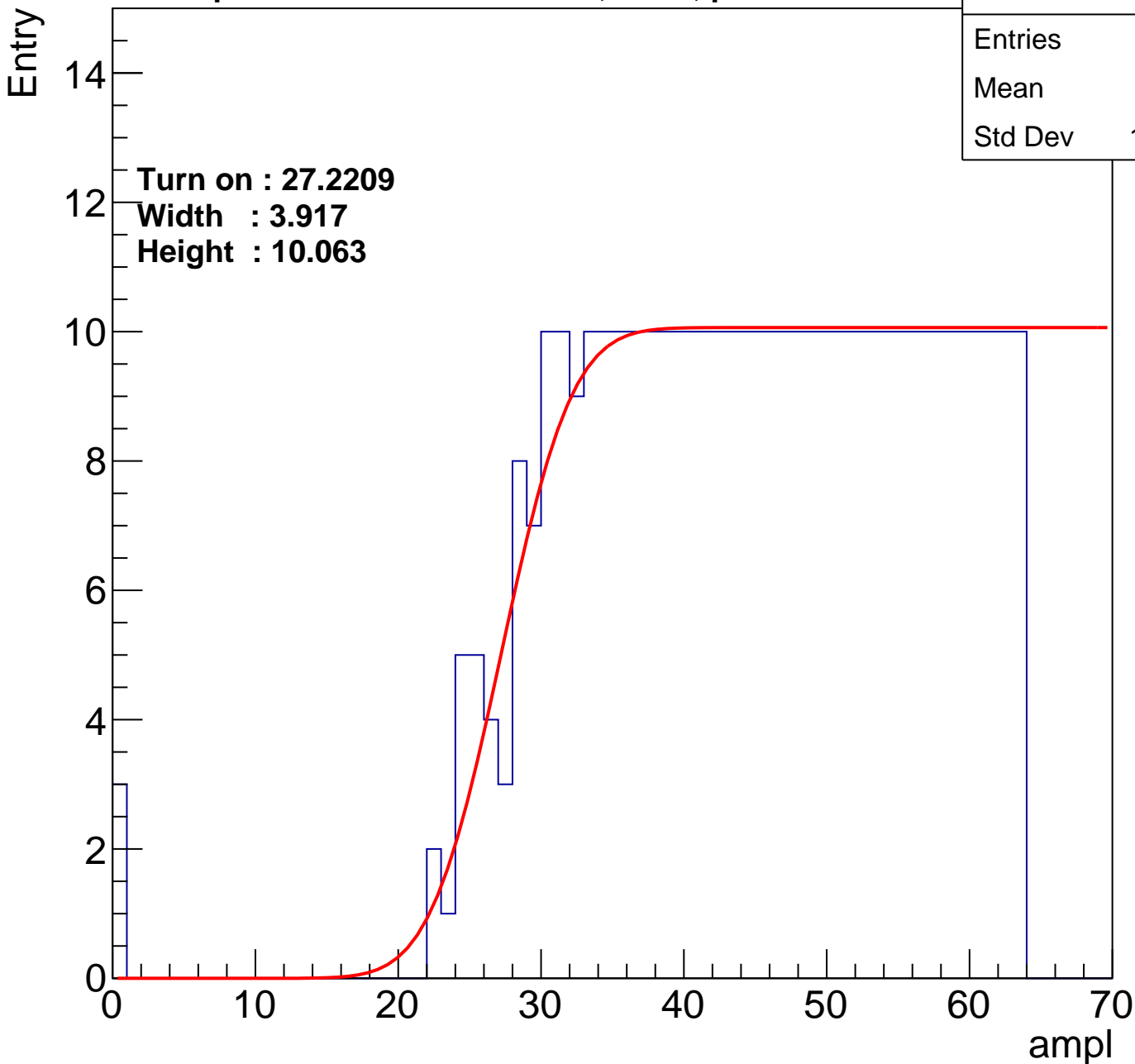
calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.3
Std Dev	11.69

Turn on : 27.2209

Width : 3.917

Height : 10.063



B1L000S, U17-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.79
Std Dev	11.79

Turn on : 25.2649

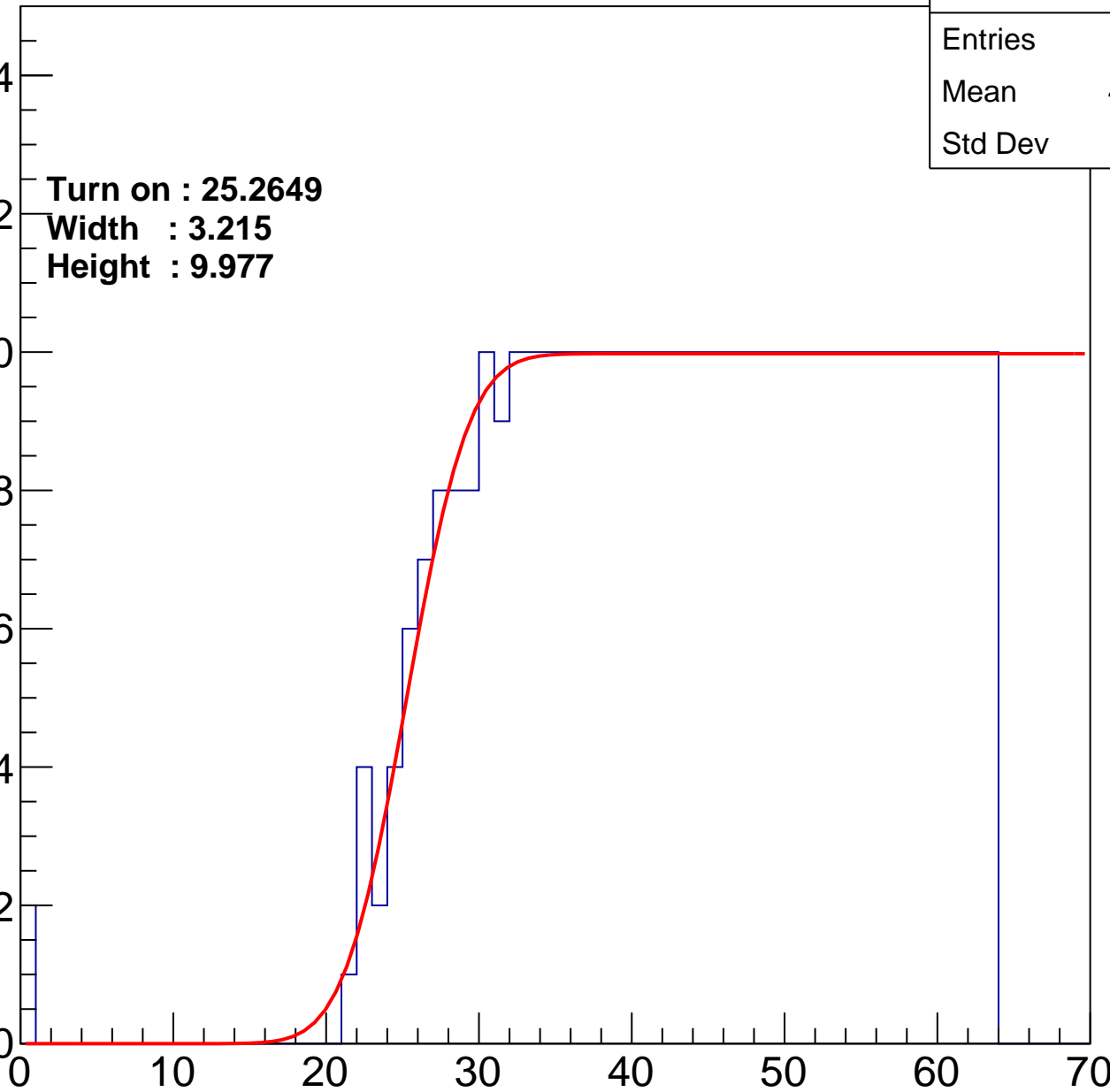
Width : 3.215

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U17-ch124

calib_packv5_042523_0143.root, FC#3, port B3

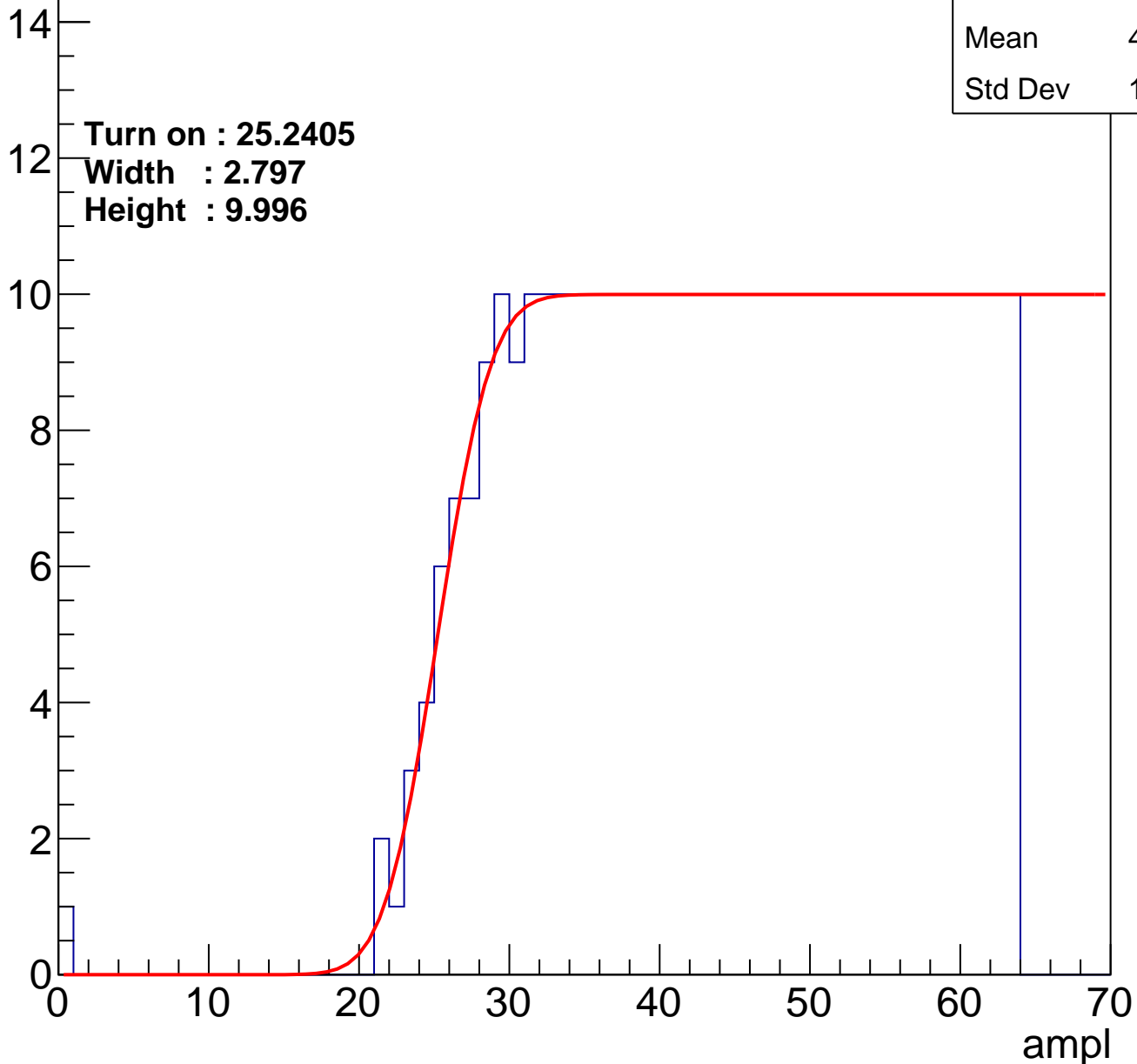
Entries	389
Mean	43.89
Std Dev	11.57

Turn on : 25.2405

Width : 2.797

Height : 9.996

Entry



B1L000S, U17-ch125

calib_packv5_042523_0143.root, FC#3, port B3

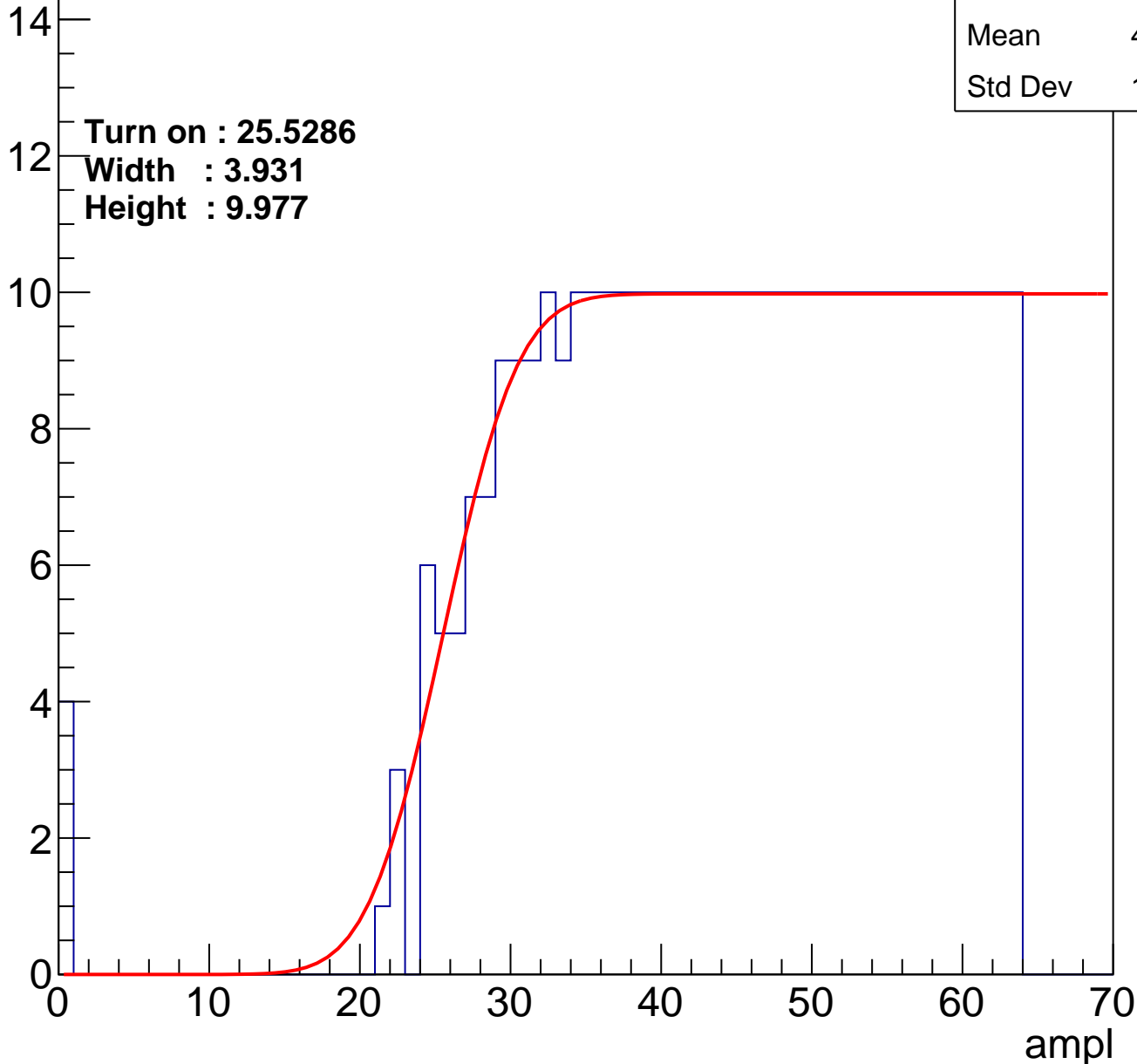
Entries	384
Mean	43.88
Std Dev	12.05

Turn on : 25.5286

Width : 3.931

Height : 9.977

Entry



B1L000S, U17-ch126

calib_packv5_042523_0143.root, FC#3, port B3

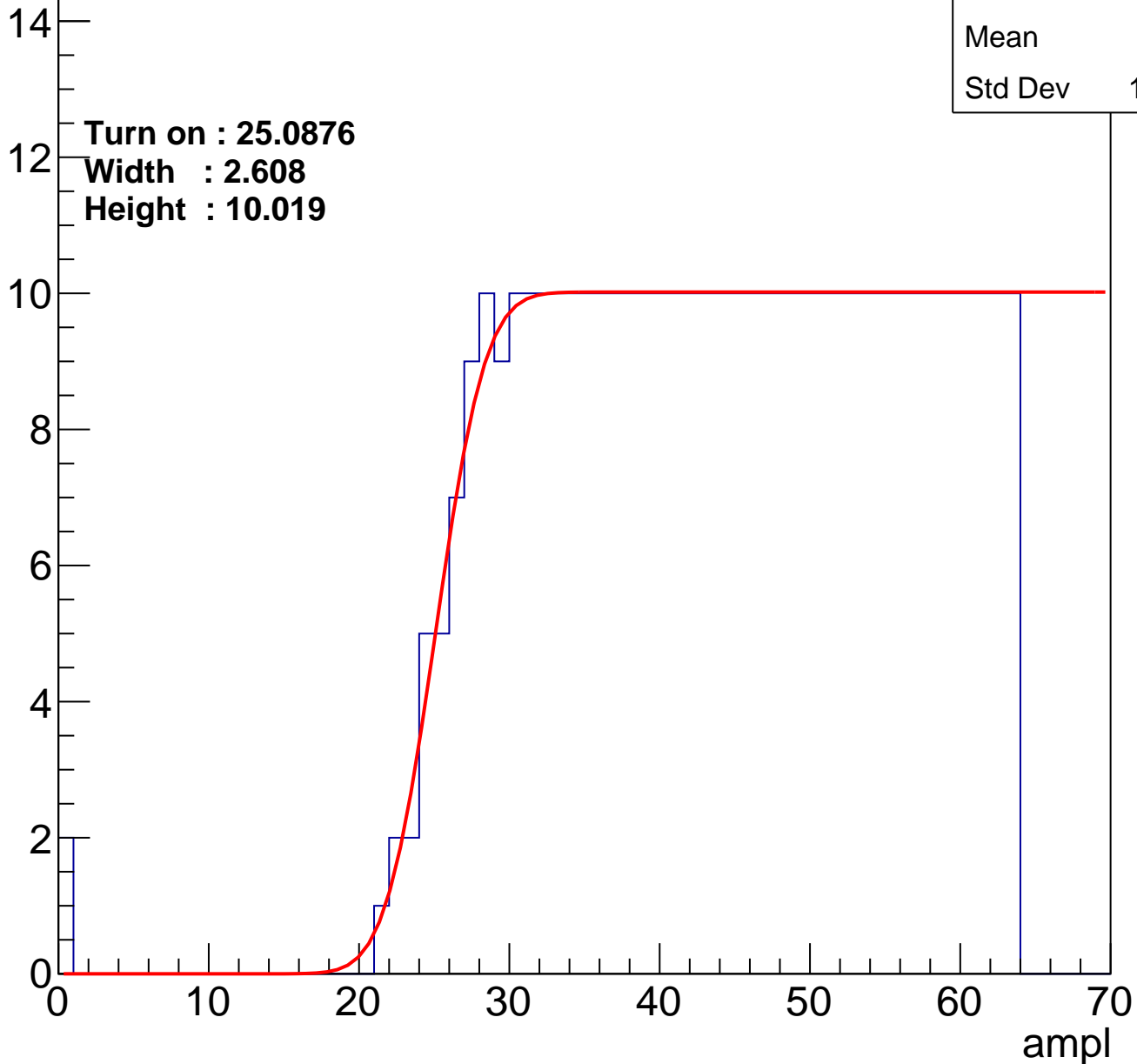
Entries	392
Mean	43.7
Std Dev	11.77

Turn on : 25.0876

Width : 2.608

Height : 10.019

Entry



B1L000S, U17-ch127

calib_packv5_042523_0143.root, FC#3, port B3

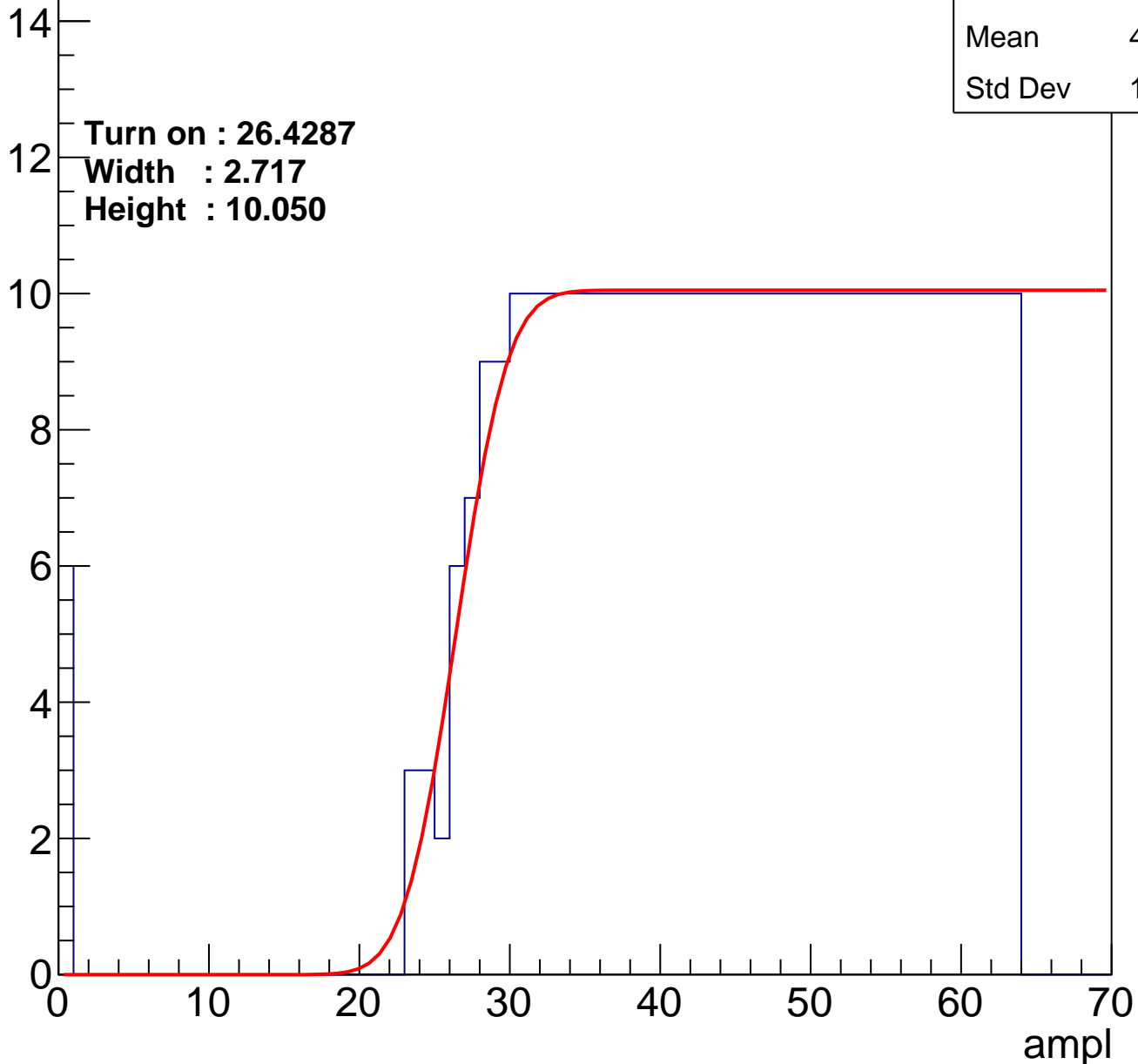
Entries	385
Mean	43.79
Std Dev	12.27

Turn on : 26.4287

Width : 2.717

Height : 10.050

Entry



B1L000S, U17-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.79
Std Dev	12.27

Turn on : 26.4287

Width : 2.717

Height : 10.050

Entry

