

B1L003S, U19-ch0, adc0

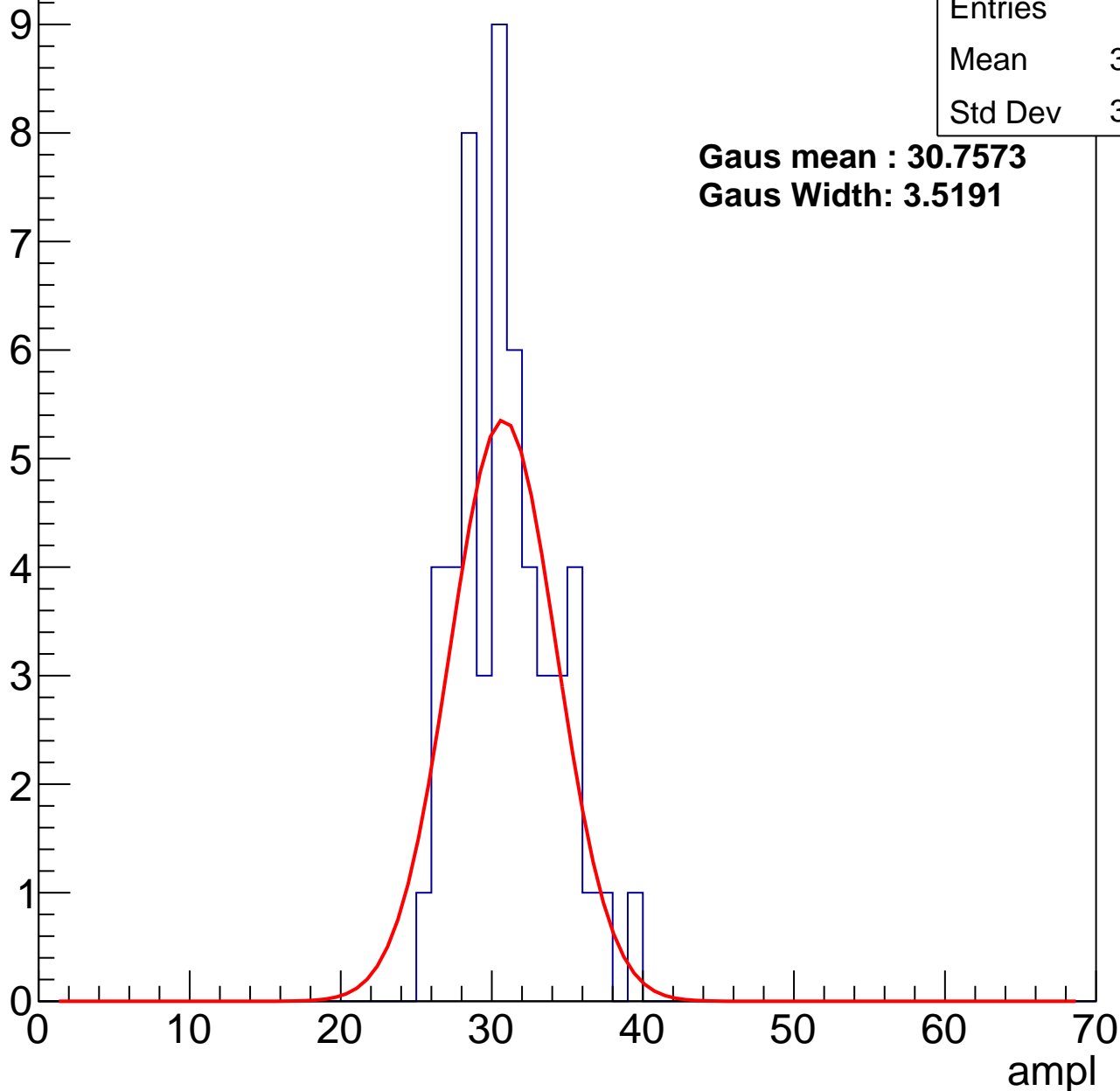
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	30.48
Std Dev	3.135

Gaus mean : 30.7573

Gaus Width: 3.5191



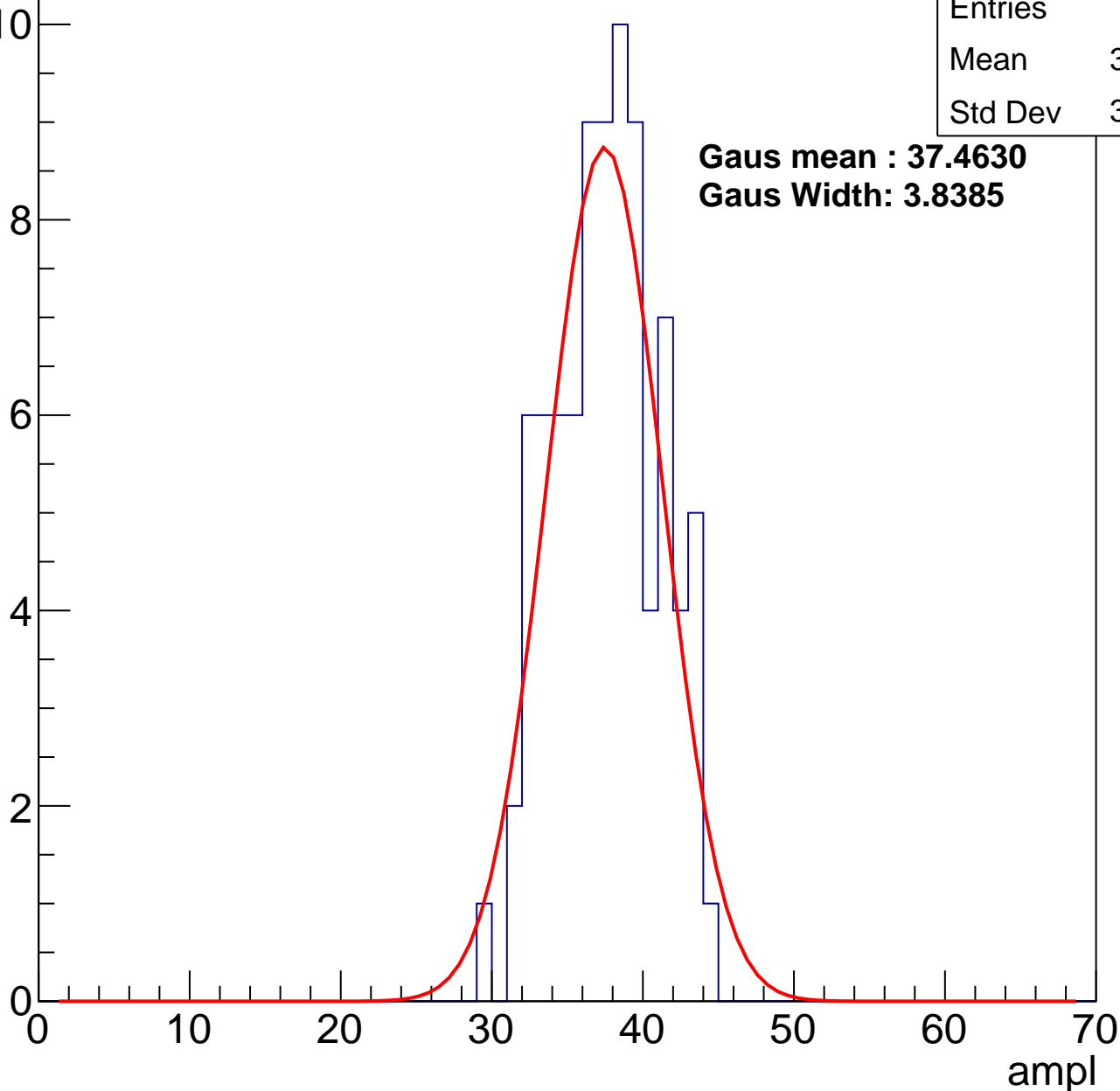
B1L003S, U19-ch0, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	37.14
Std Dev	3.413

Gaus mean : 37.4630
Gaus Width: 3.8385



B1L003S, U19-ch0, adc2

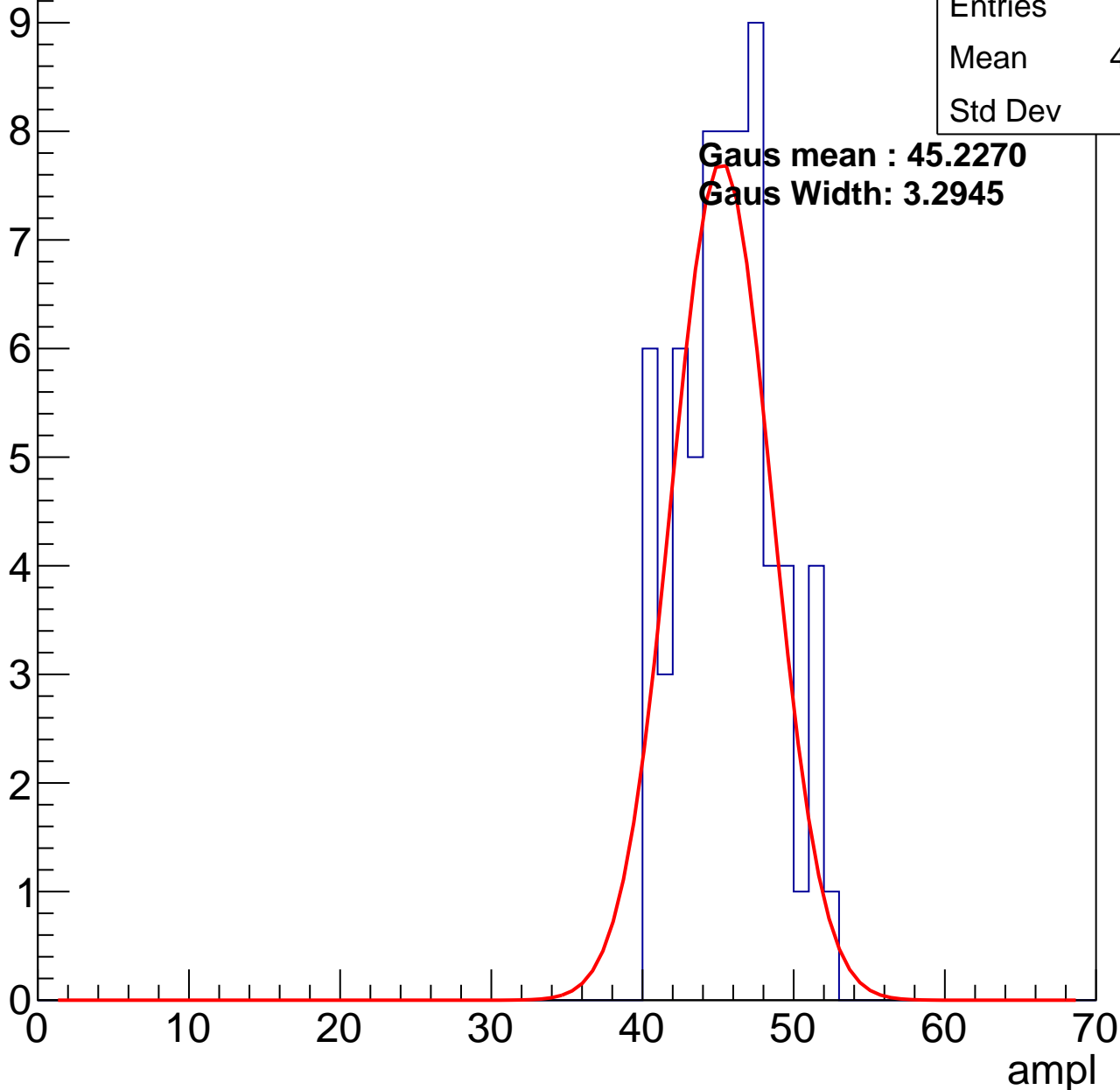
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	45.18
Std Dev	3.09

Gaus mean : 45.2270

Gaus Width: 3.2945

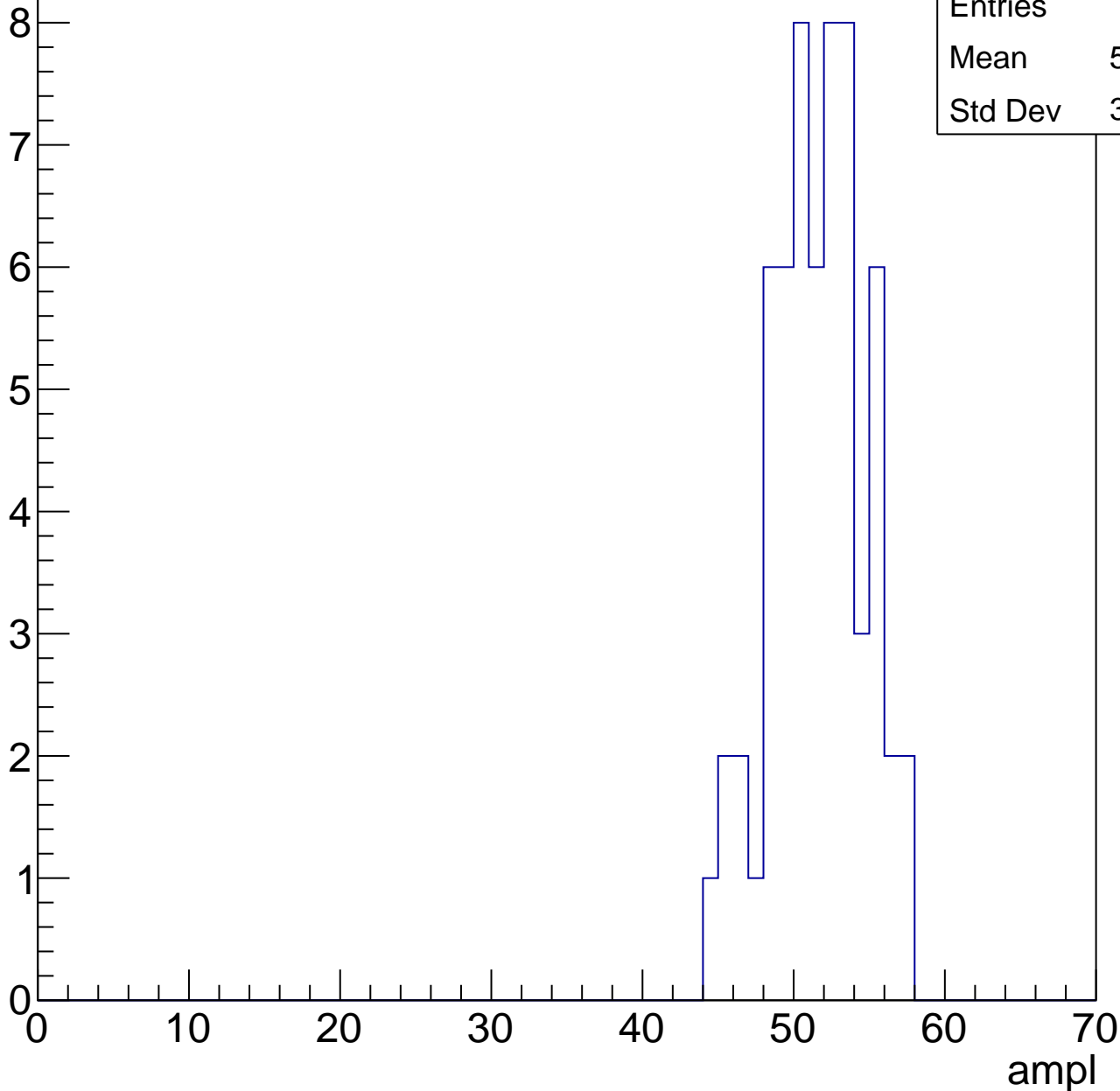


B1L003S, U19-ch0, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	51.13
Std Dev	3.022

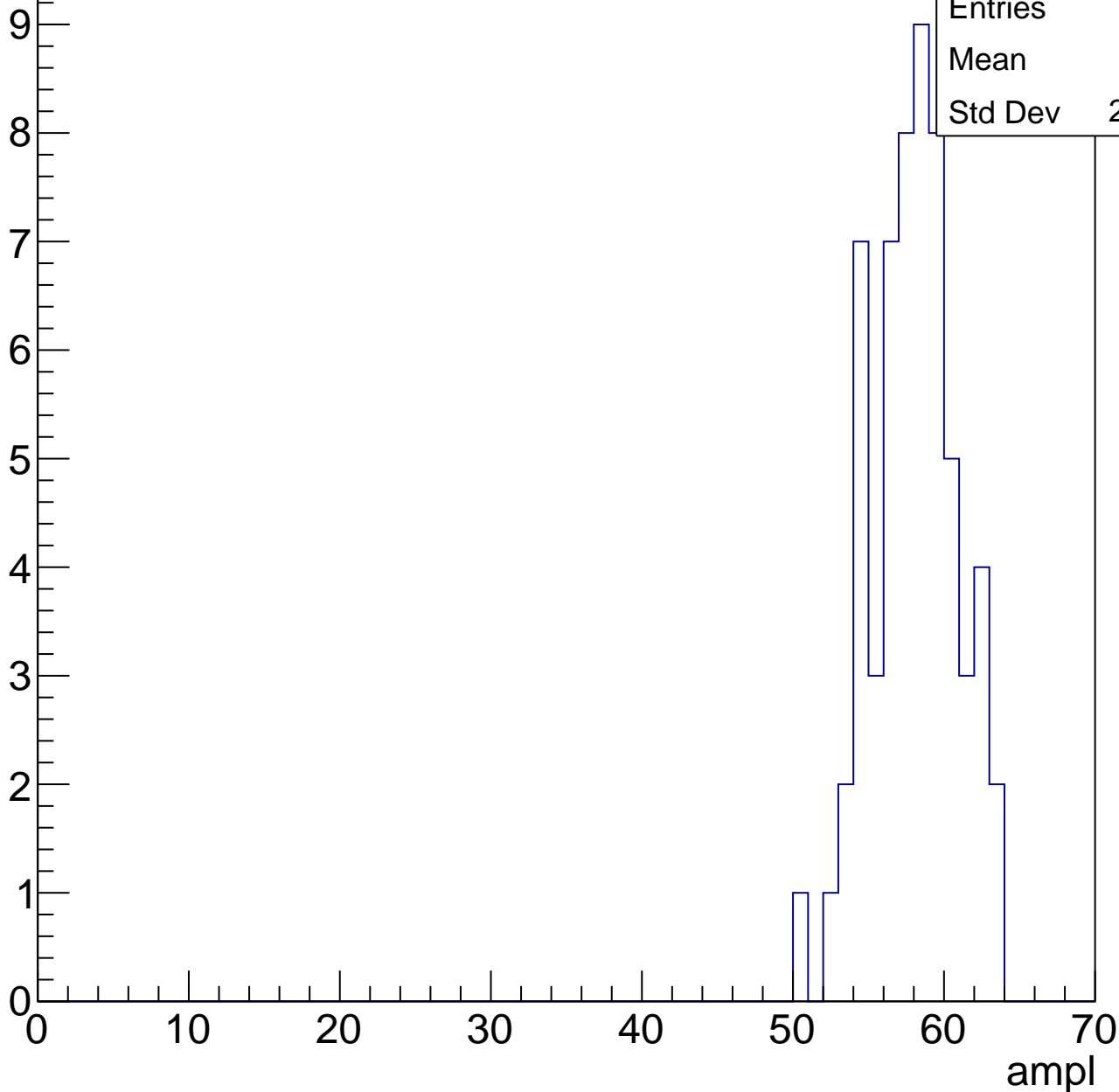


B1L003S, U19-ch0, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	57.5
Std Dev	2.825

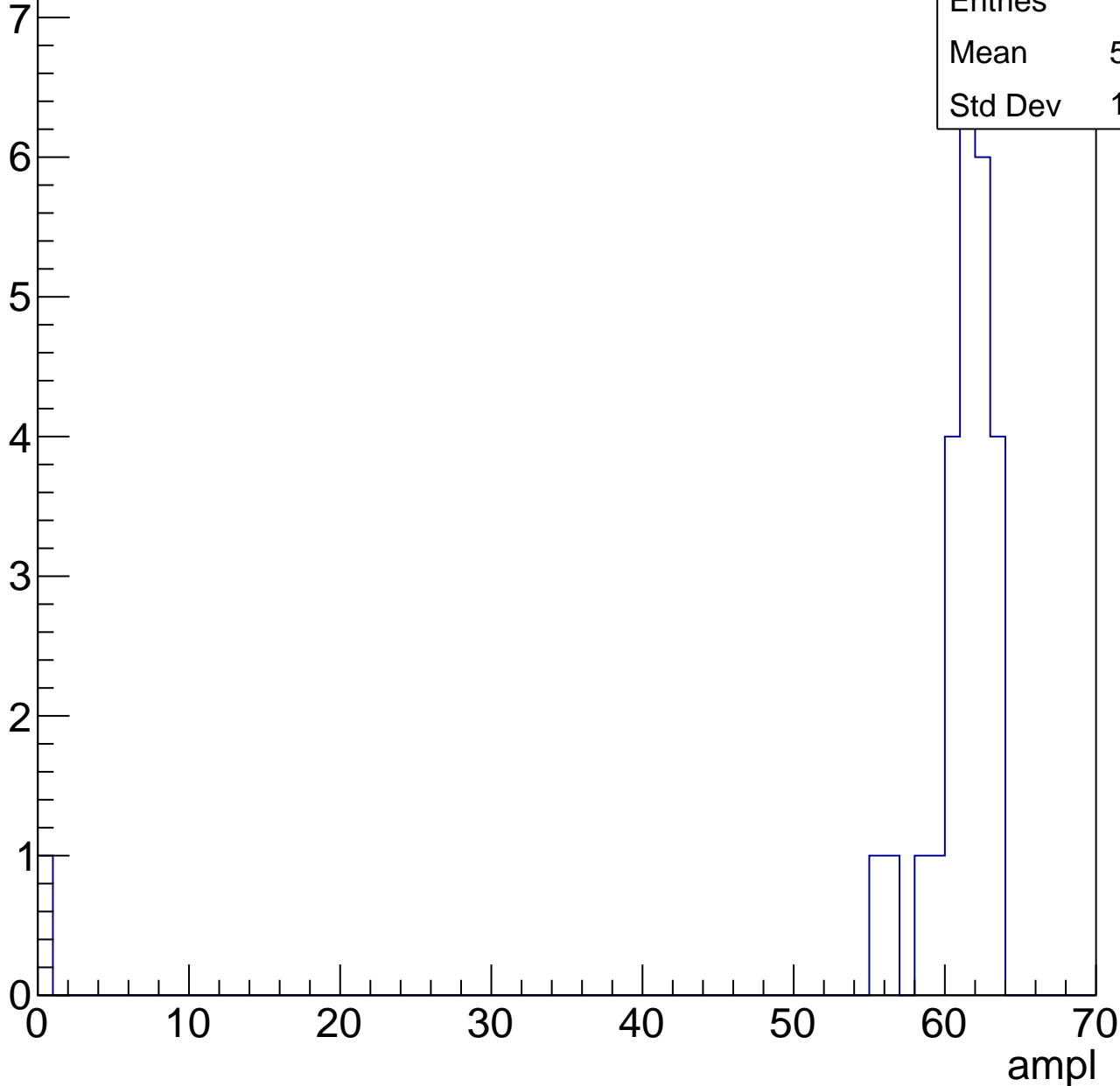


B1L003S, U19-ch0, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

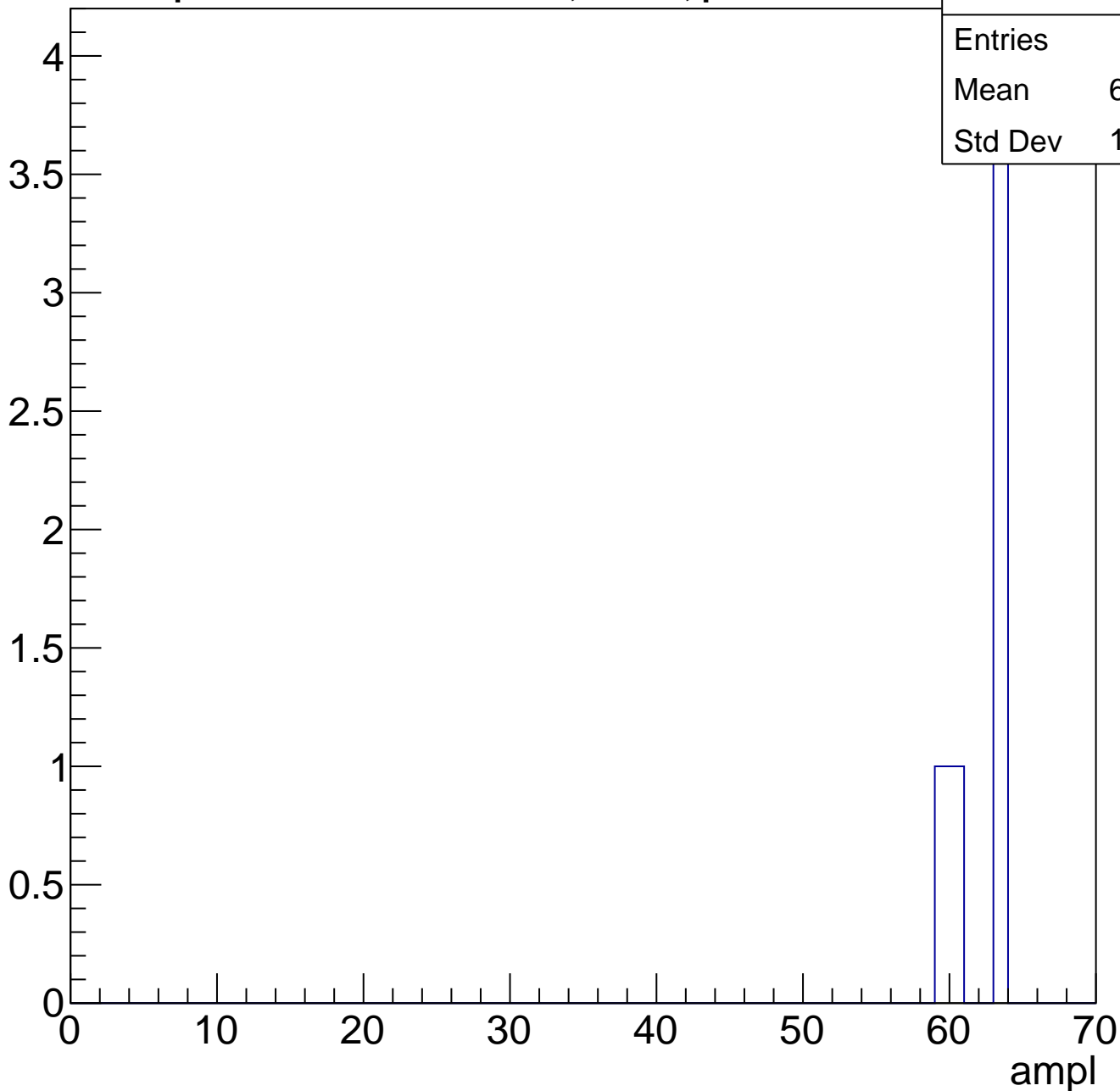
Entries	26
Mean	58.42
Std Dev	11.85



B1L003S, U19-ch0, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch0, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch1, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	30.18
Std Dev	4.89

Gaus mean : 31.7439

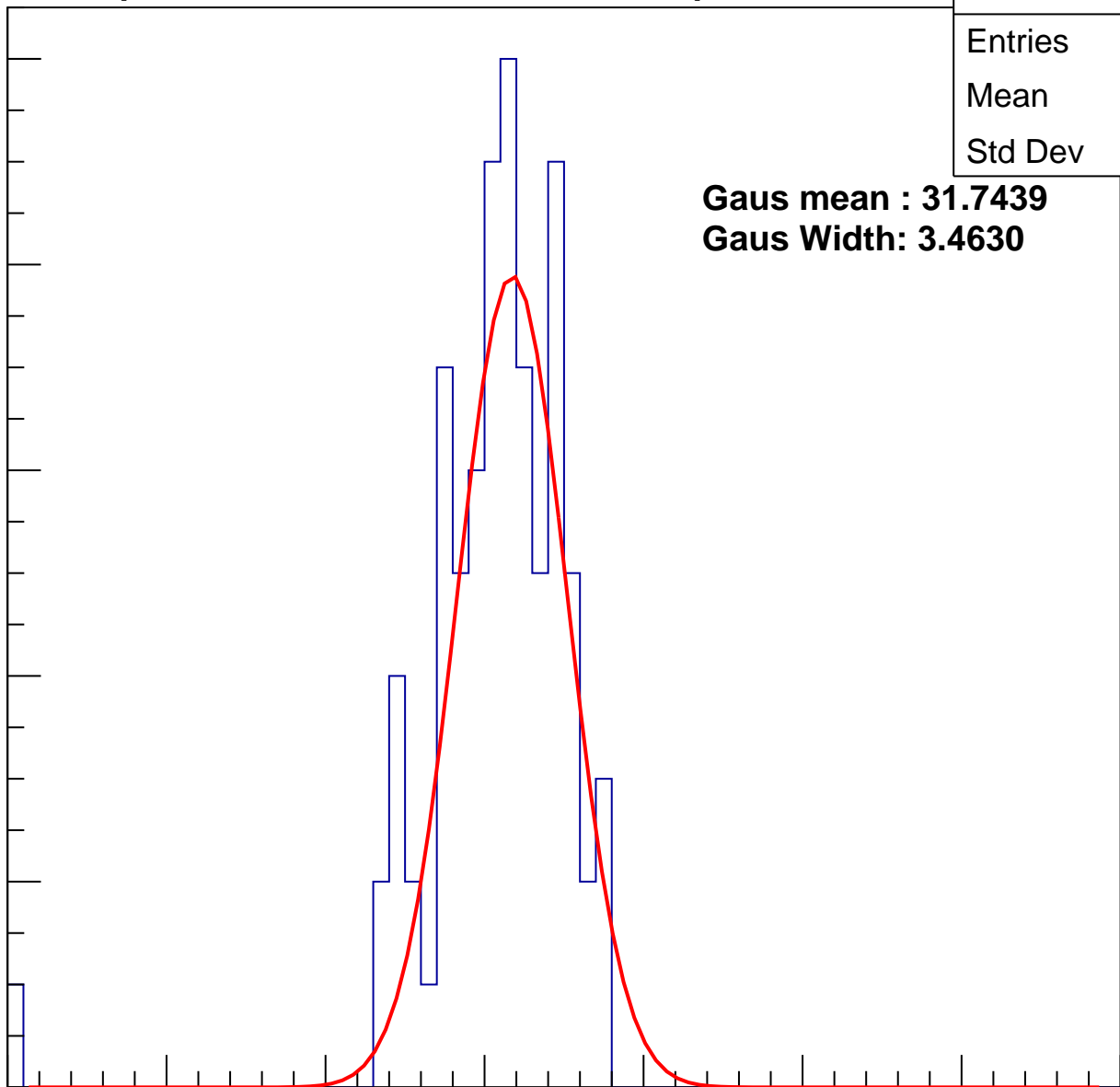
Gaus Width: 3.4630

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch1, adc1

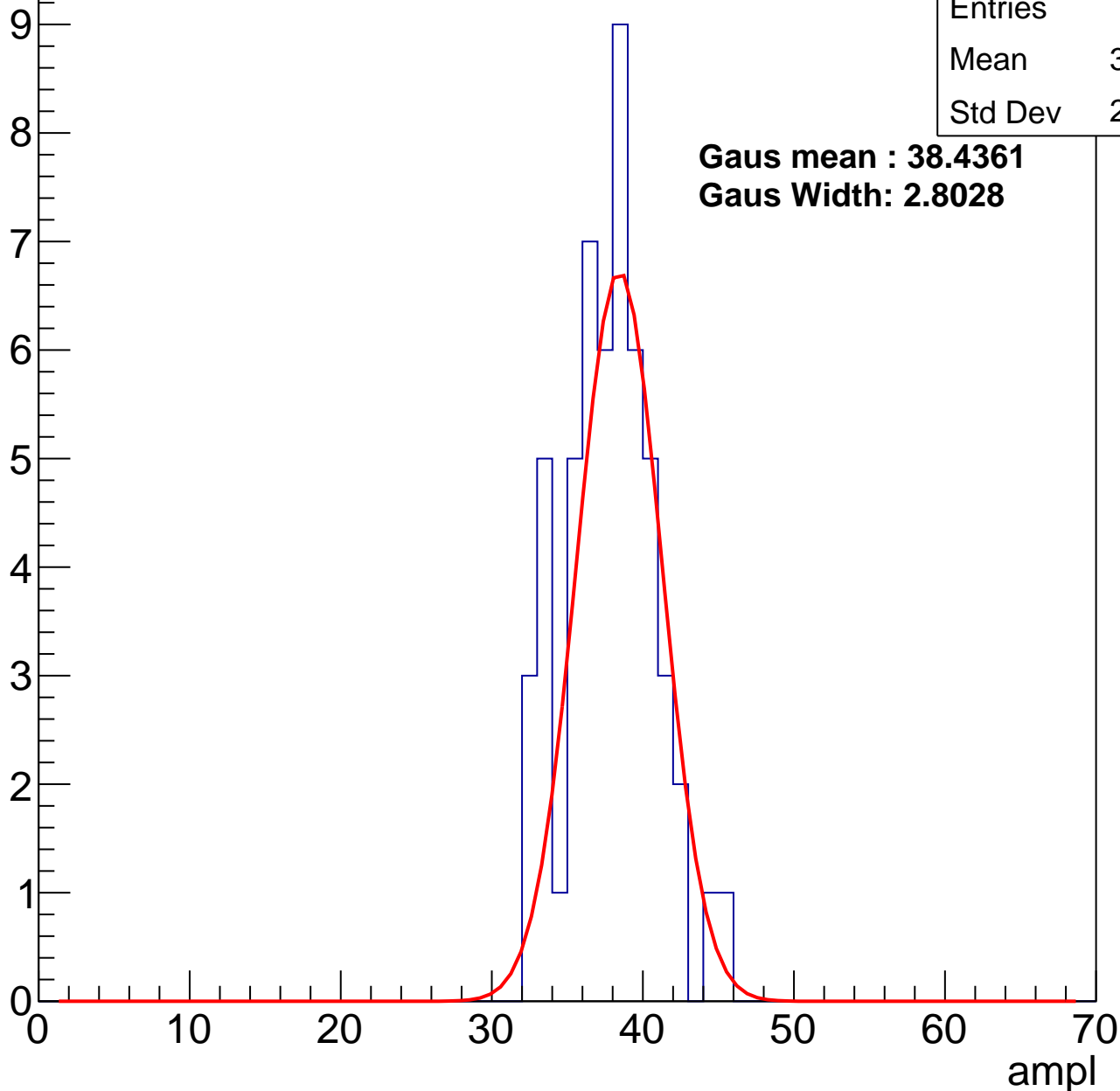
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	37.33
Std Dev	2.963

Gaus mean : 38.4361

Gaus Width: 2.8028



B1L003S, U19-ch1, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	79
Mean	44.03
Std Dev	3.28

Gaus mean : 44.2634

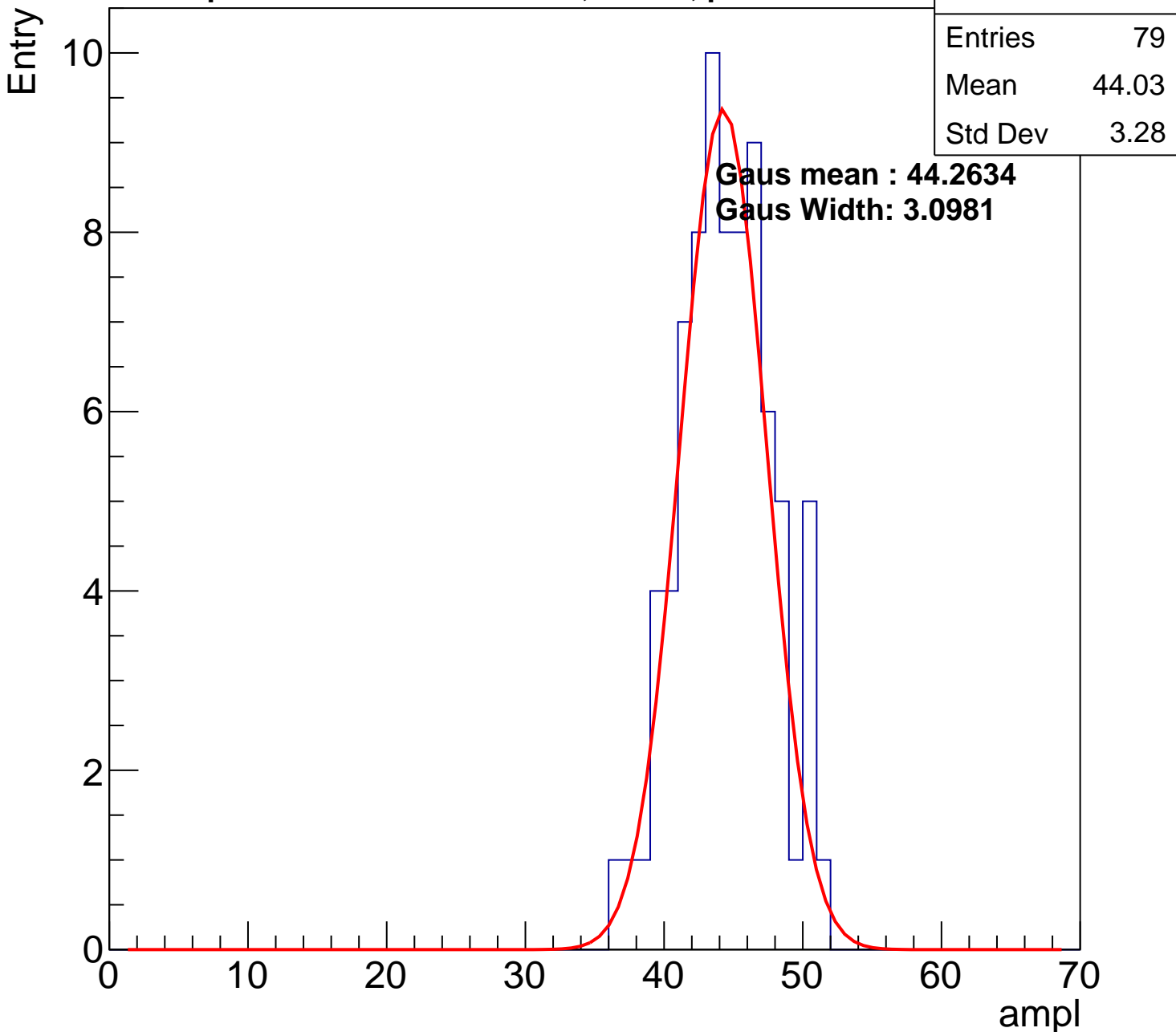
Gaus Width: 3.0981

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

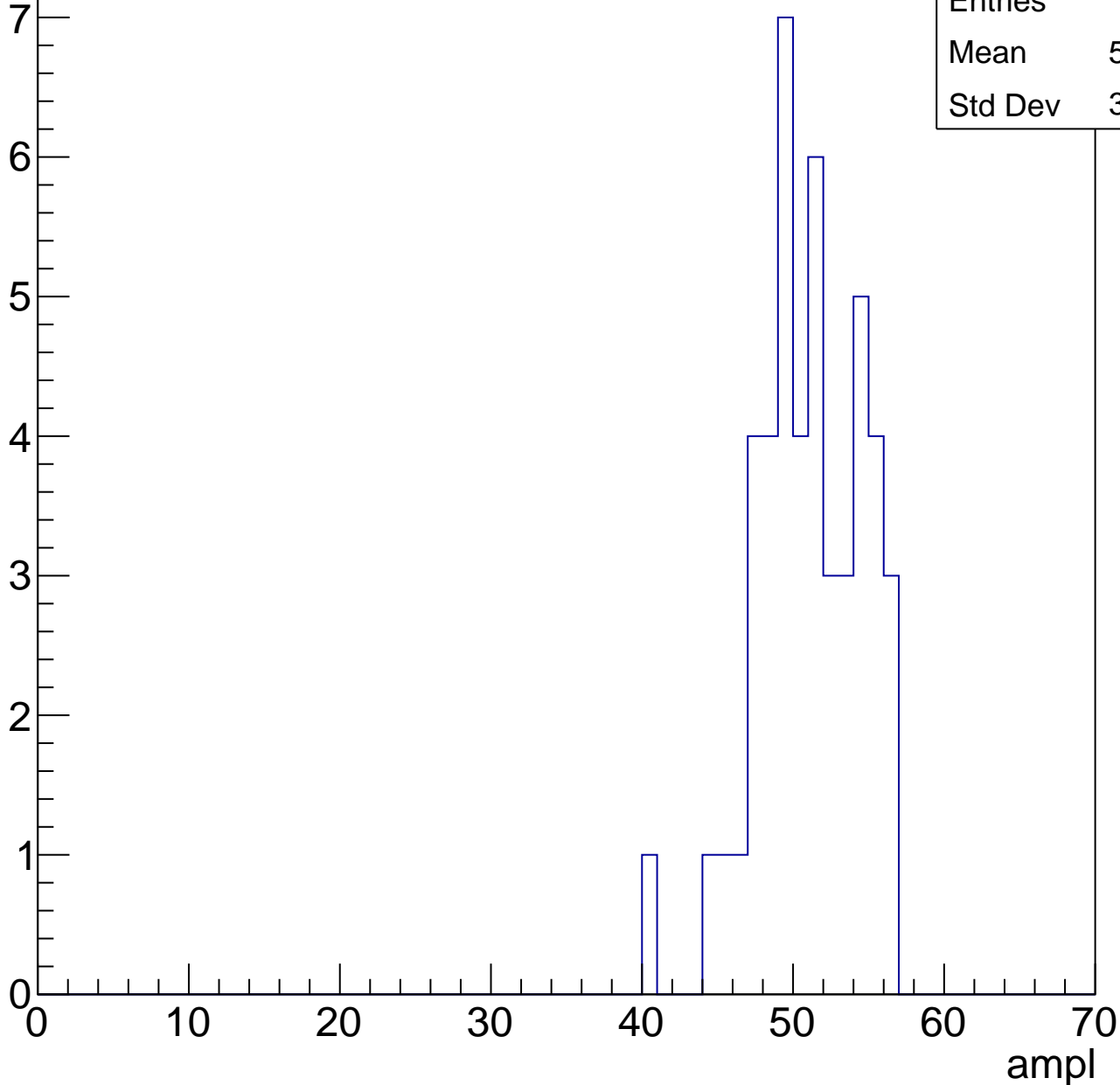


B1L003S, U19-ch1, adc3

calib_packv5_042523_0143.root, FC#13, port D2

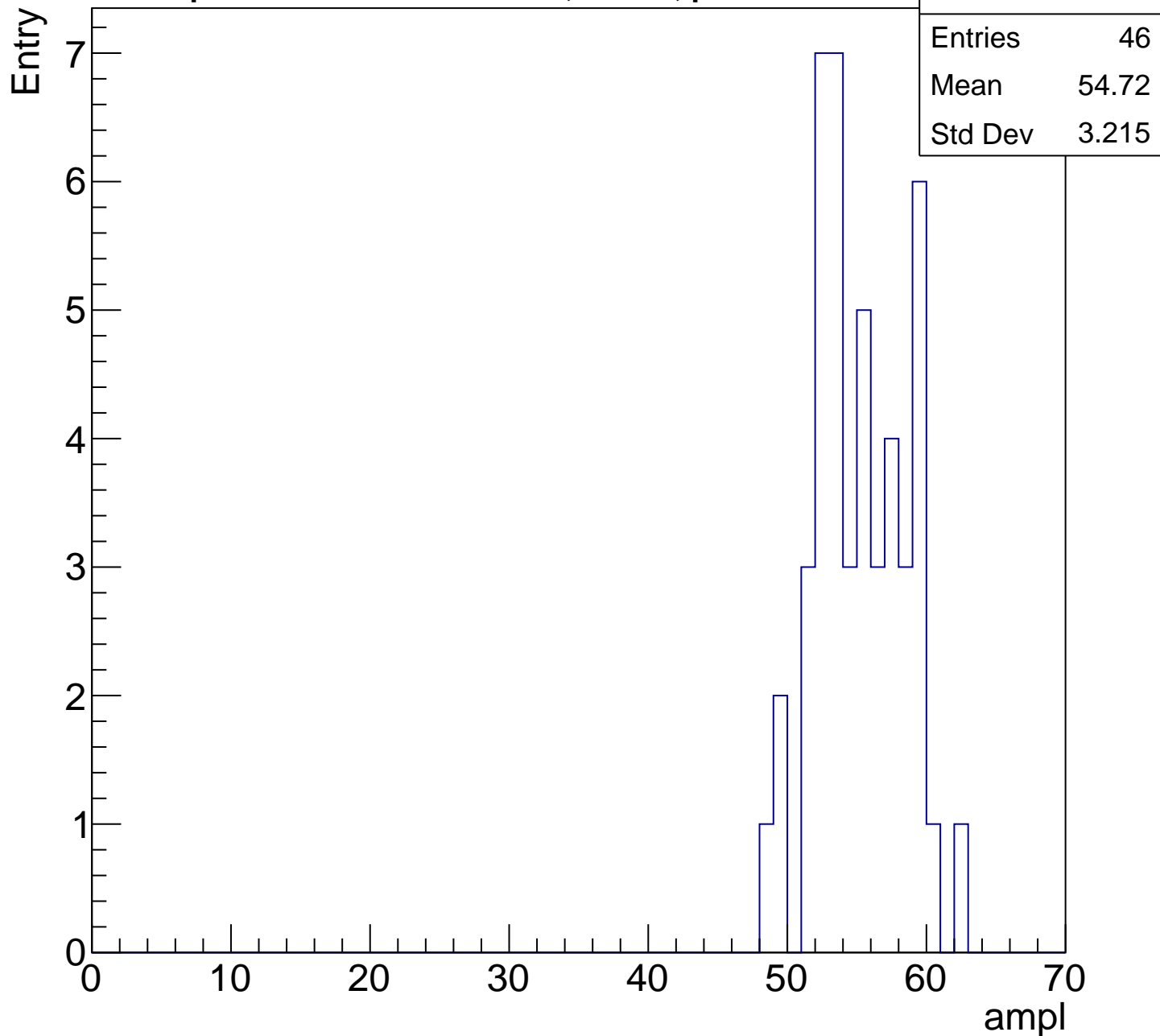
Entry

Entries	47
Mean	50.57
Std Dev	3.432



B1L003S, U19-ch1, adc4

calib_packv5_042523_0143.root, FC#13, port D2

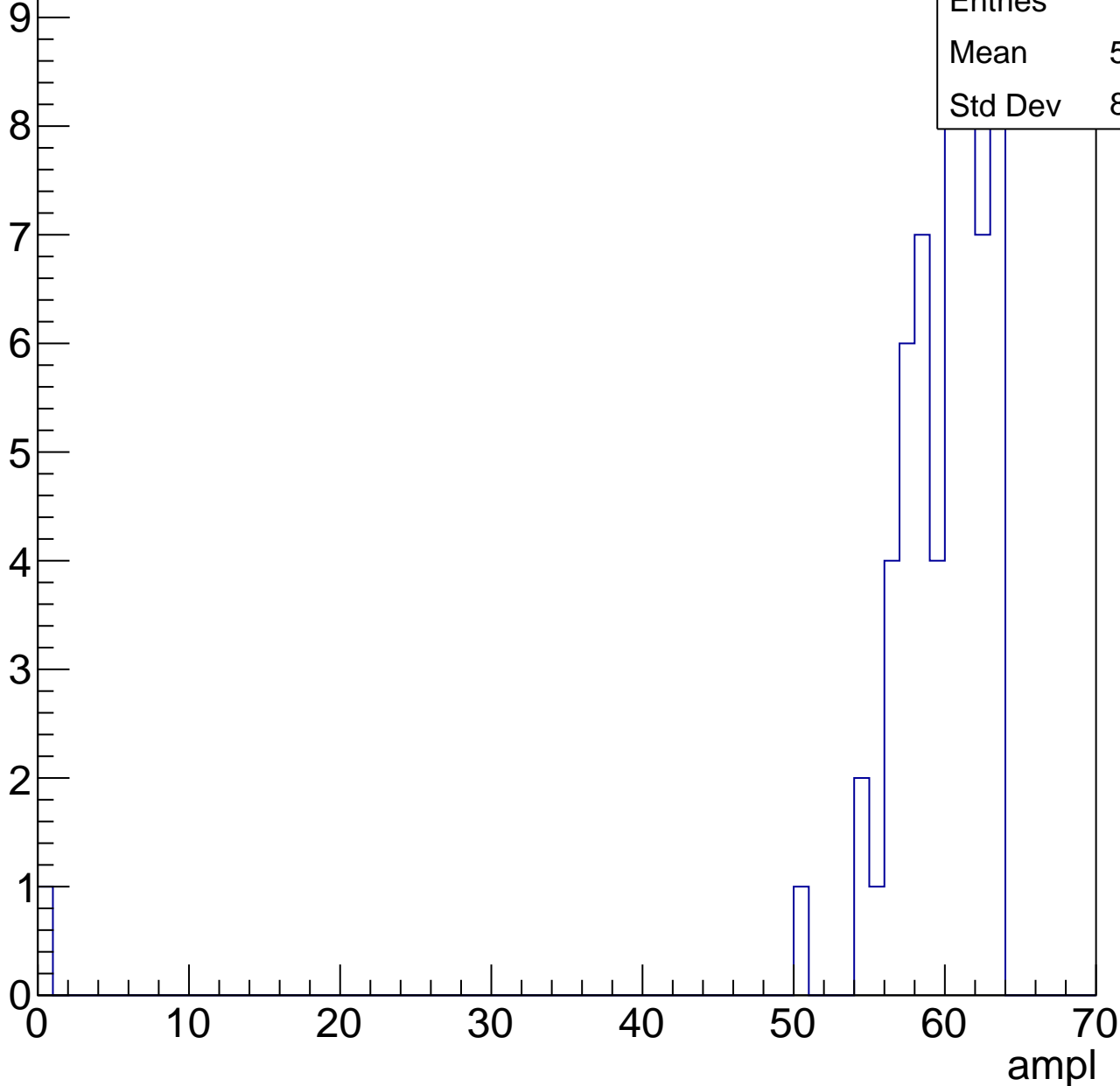


B1L003S, U19-ch1, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	58.52
Std Dev	8.096



B1L003S, U19-ch1, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	62
Std Dev	0.7071

B1L003S, U19-ch1, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L003S, U19-ch2, adc0

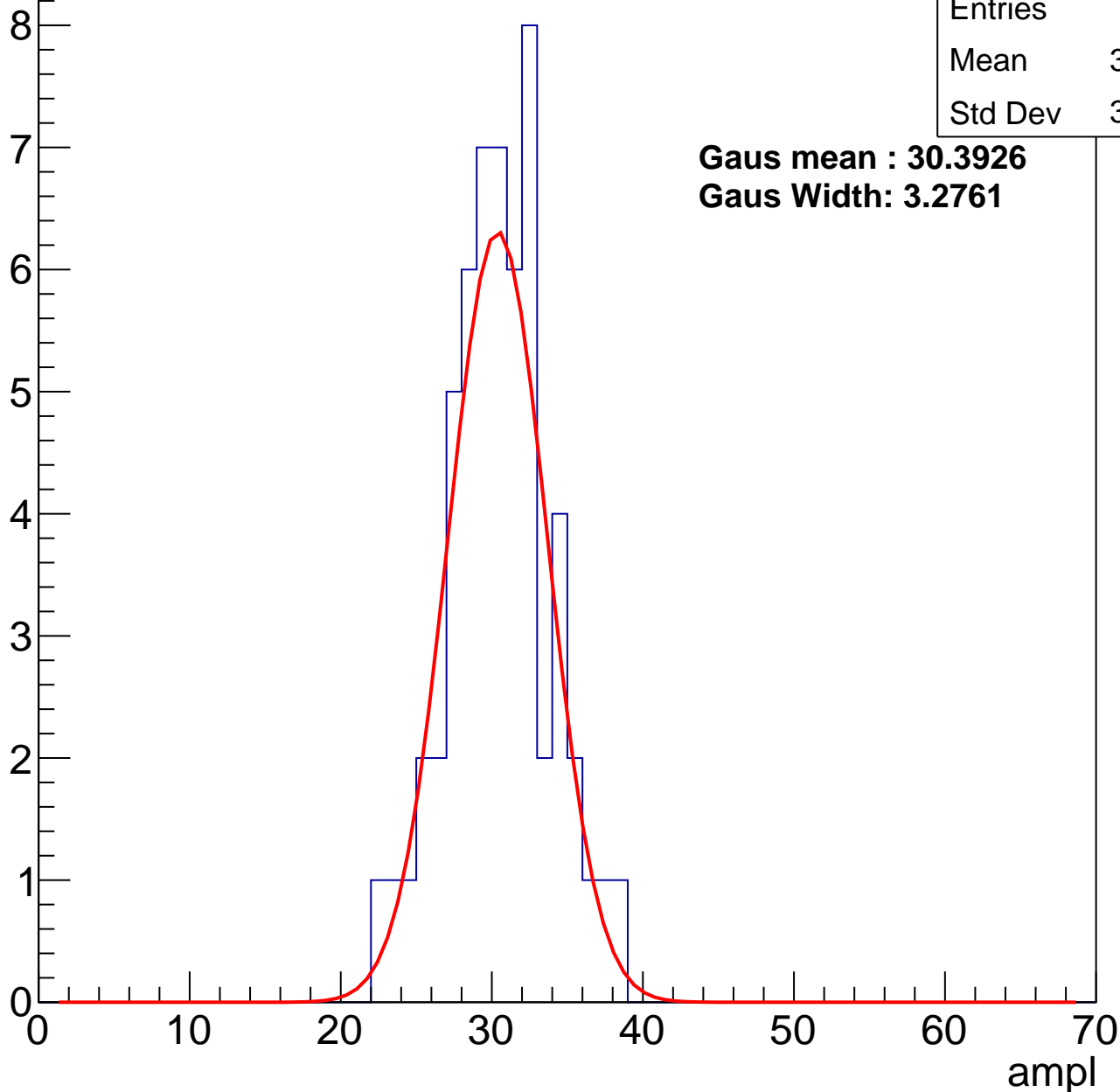
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	30.04
Std Dev	3.314

Gaus mean : 30.3926

Gaus Width: 3.2761



B1L003S, U19-ch2, adc1

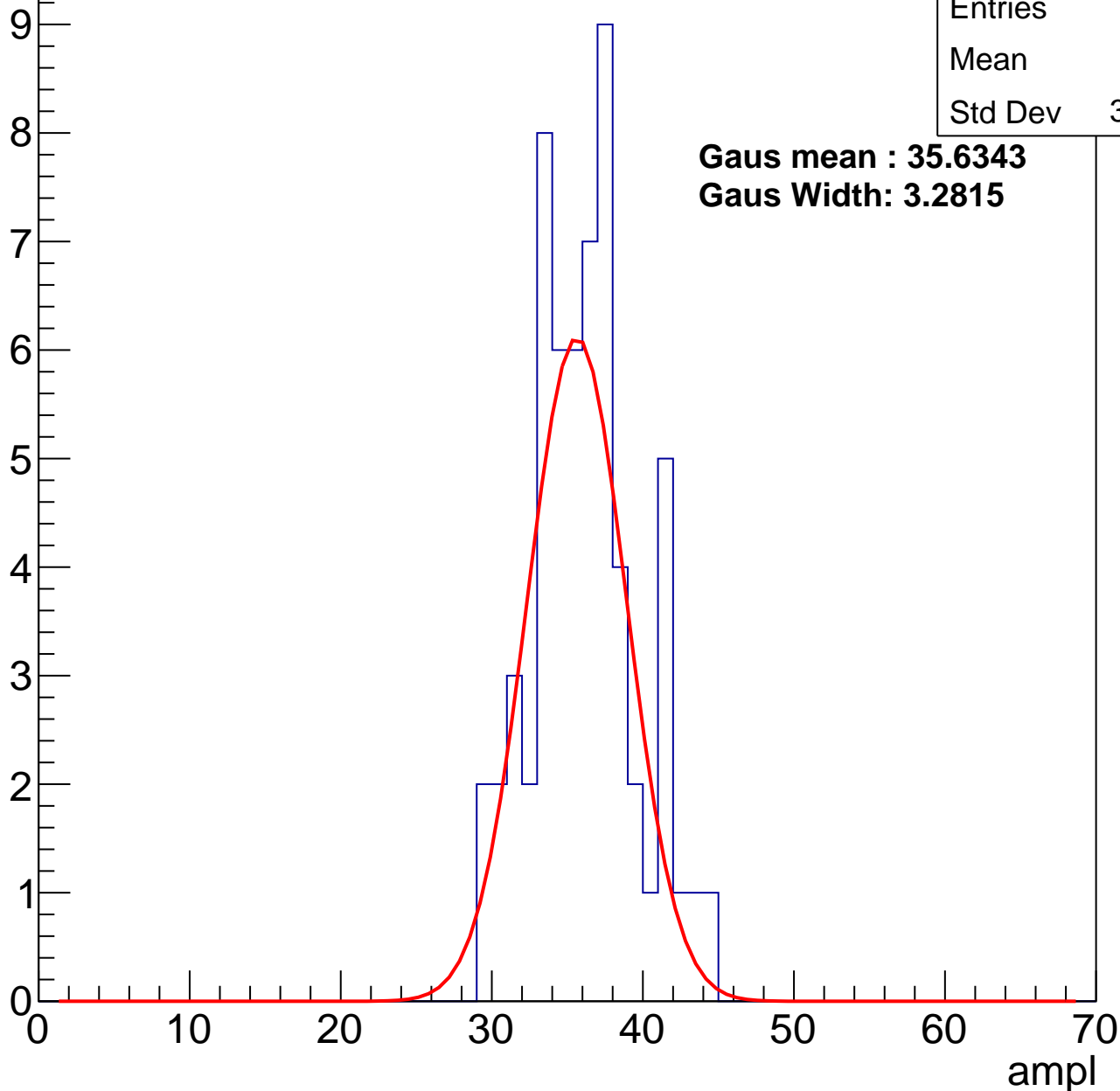
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.7
Std Dev	3.432

Gaus mean : 35.6343

Gaus Width: 3.2815



B1L003S, U19-ch2, adc2

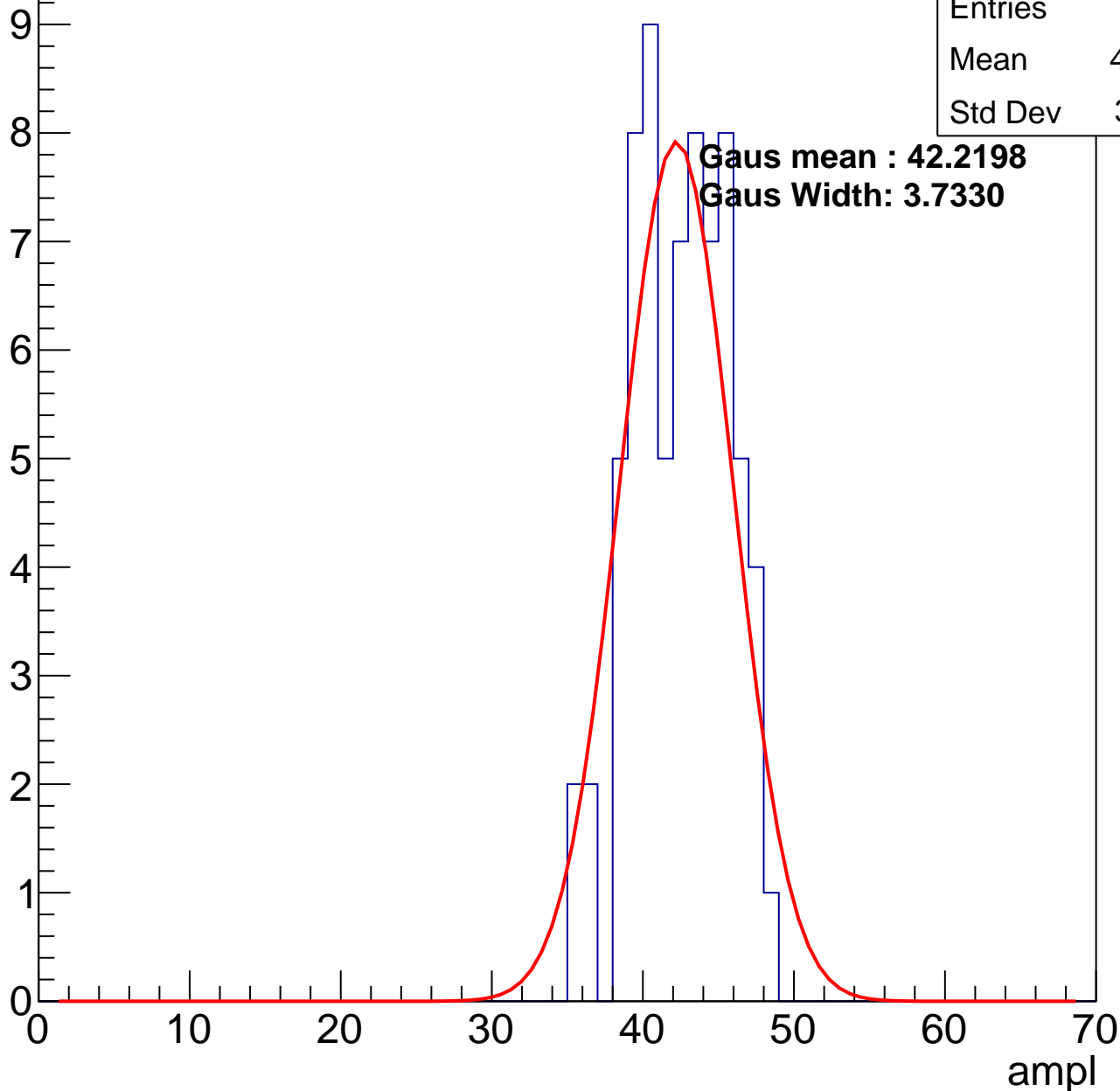
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	41.99
Std Dev	3.111

Gaus mean : 42.2198

Gaus Width: 3.7330



B1L003S, U19-ch2, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	49.11
Std Dev	3.123

Entry

10

8

6

4

2

0

0

10

20

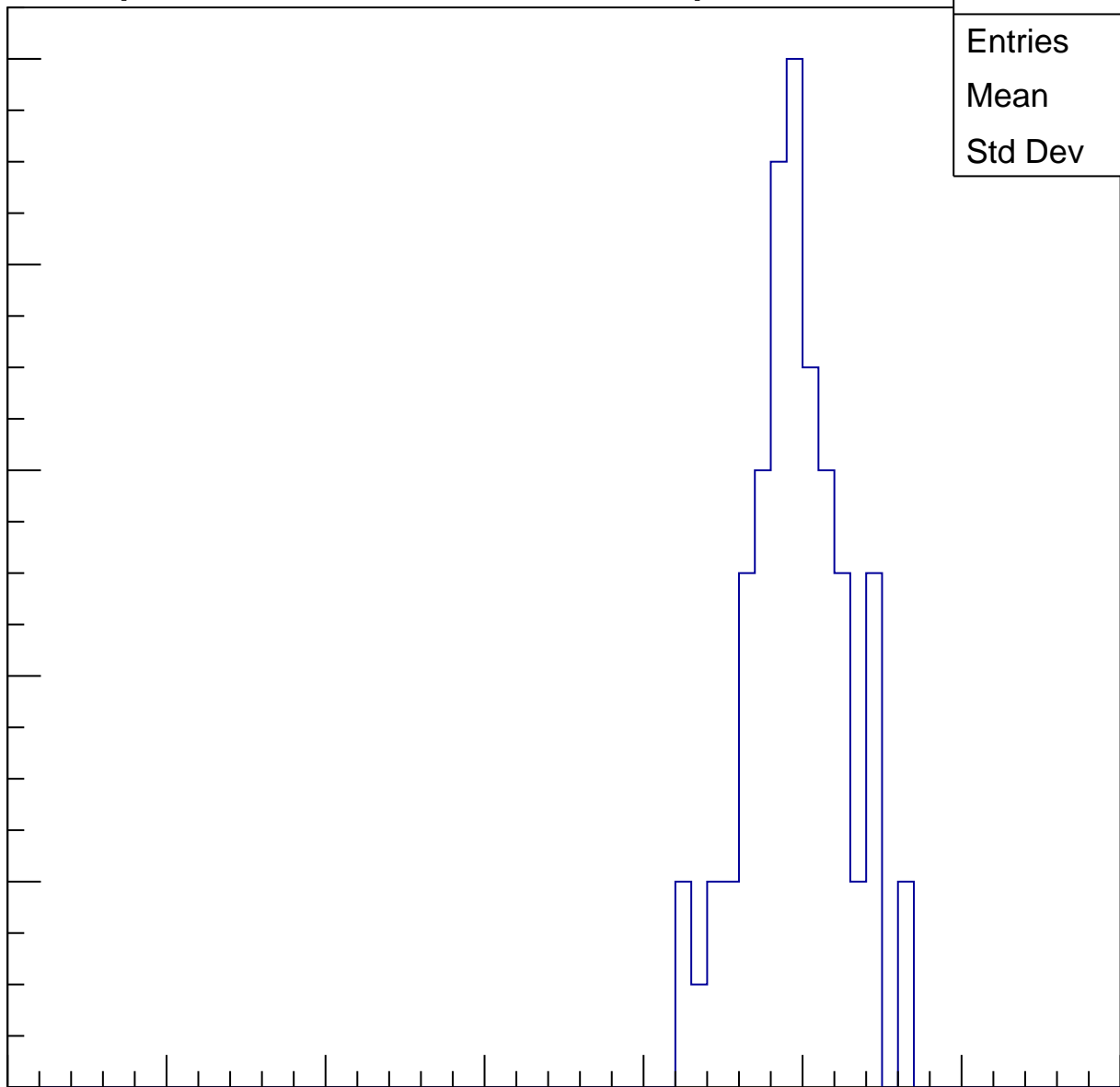
30

40

50

60

ampl

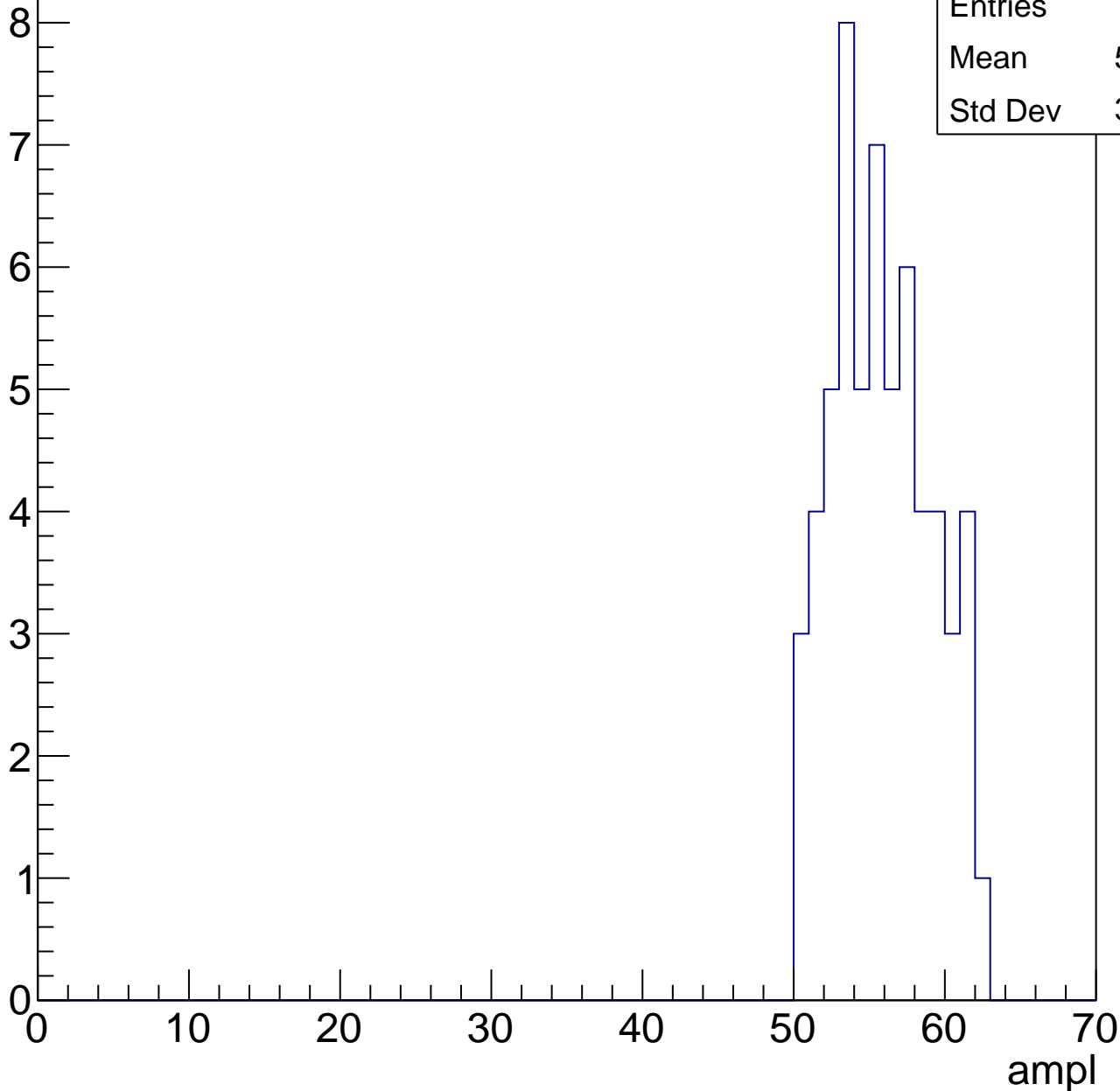


B1L003S, U19-ch2, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.41
Std Dev	3.211

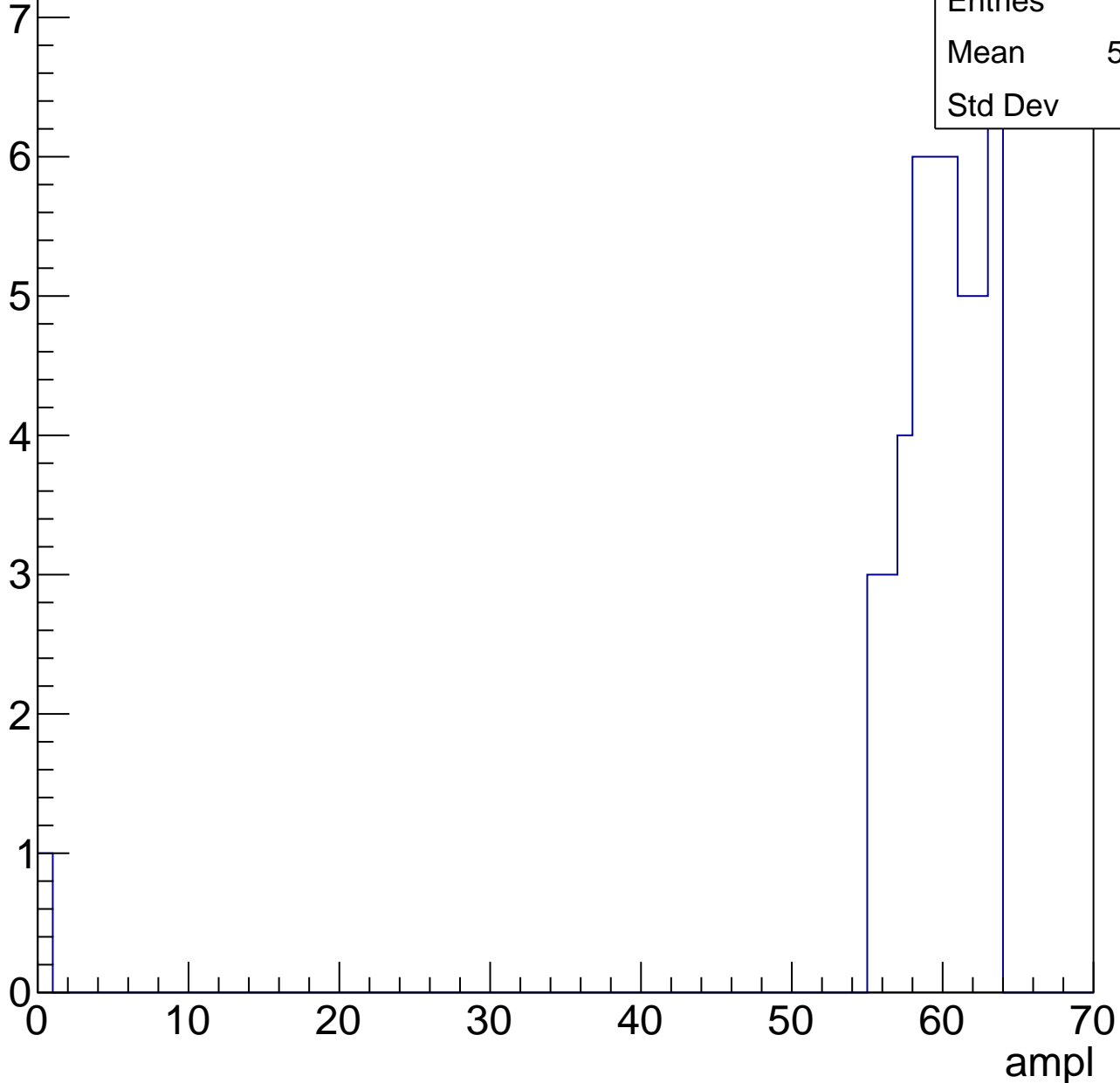


B1L003S, U19-ch2, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

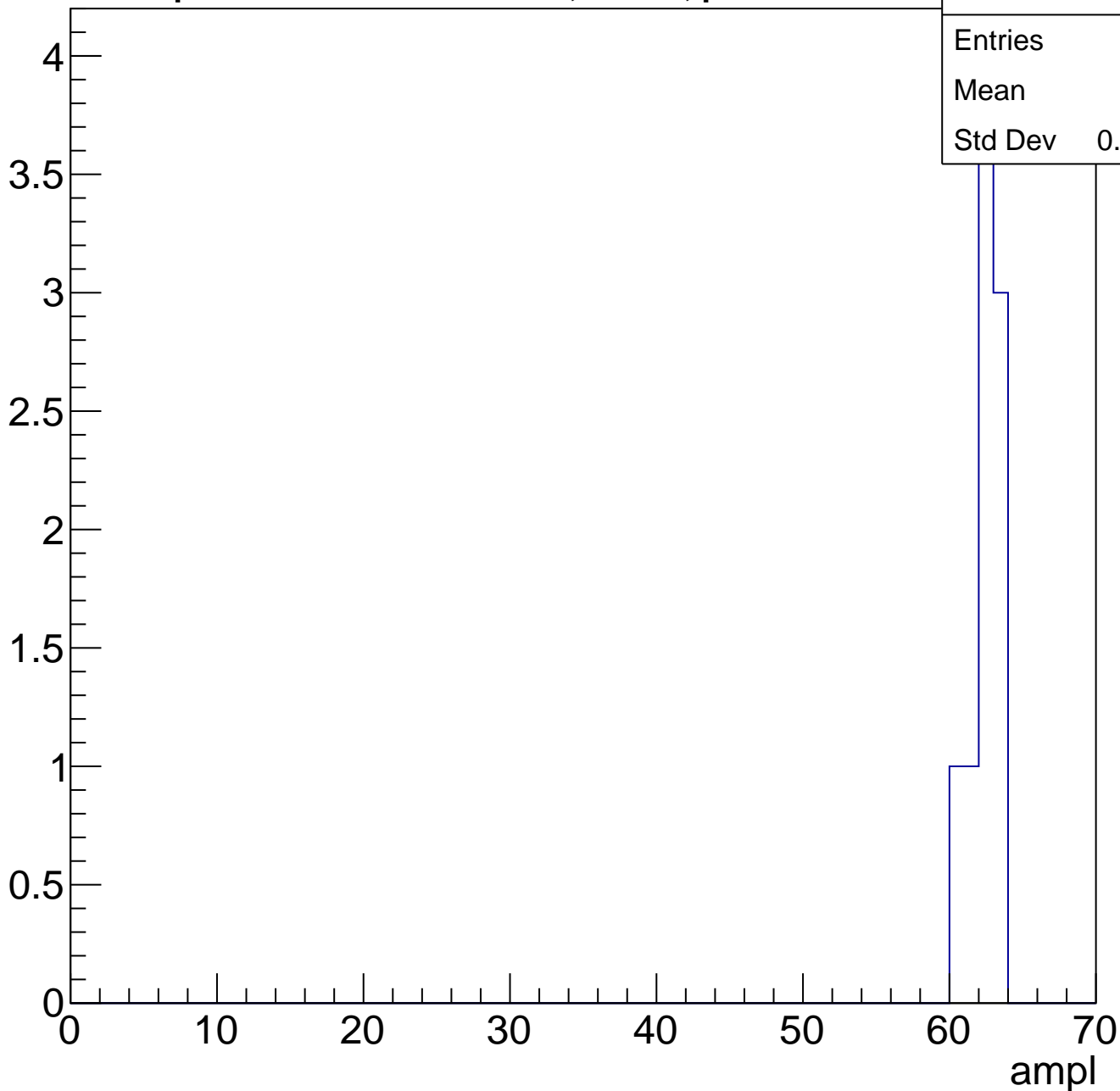
Entries	46
Mean	58.24
Std Dev	9.01



B1L003S, U19-ch2, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch2, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch3, adc0

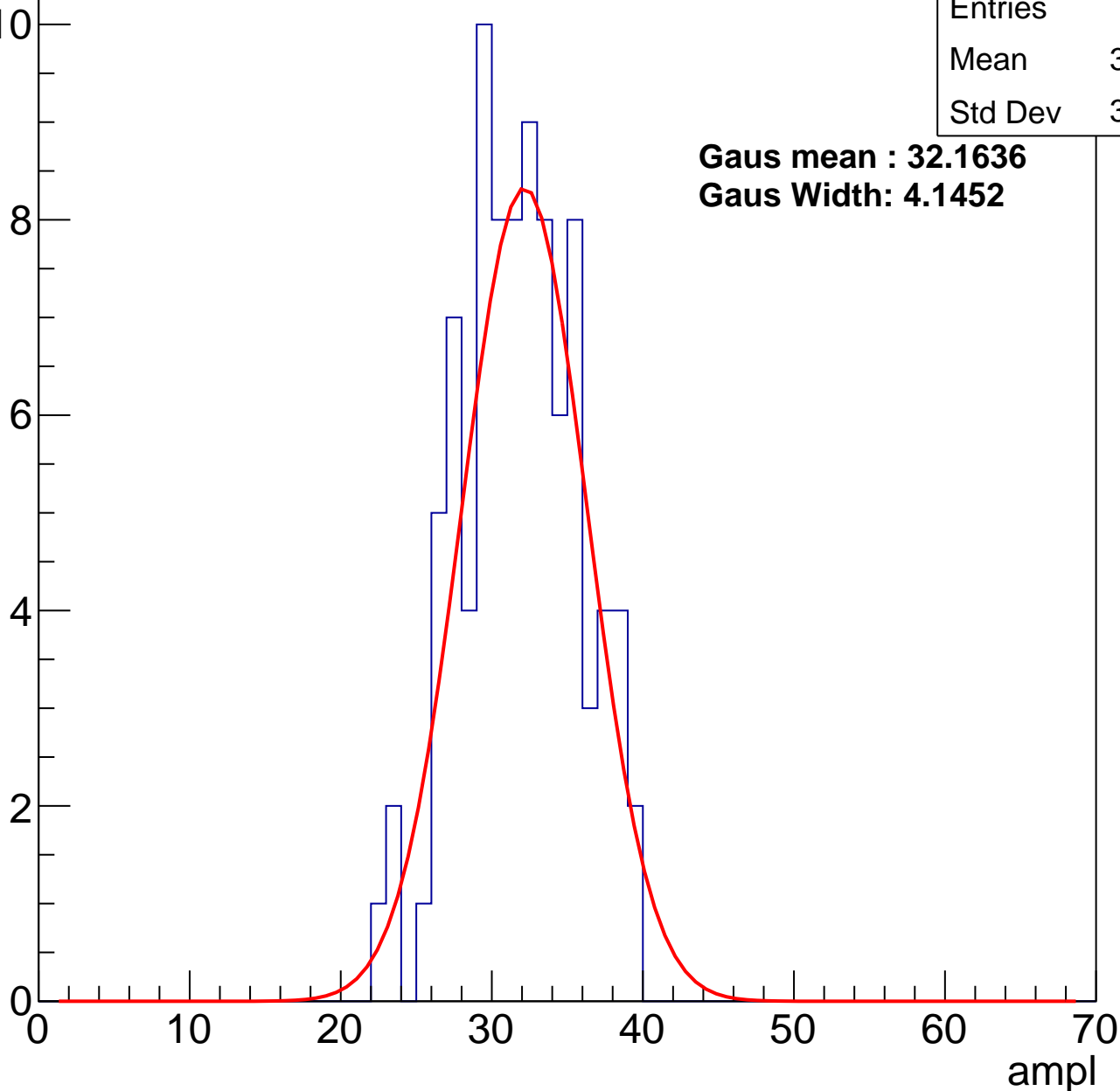
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	90
Mean	31.38
Std Dev	3.828

Gaus mean : 32.1636

Gaus Width: 4.1452



B1L003S, U19-ch3, adc1

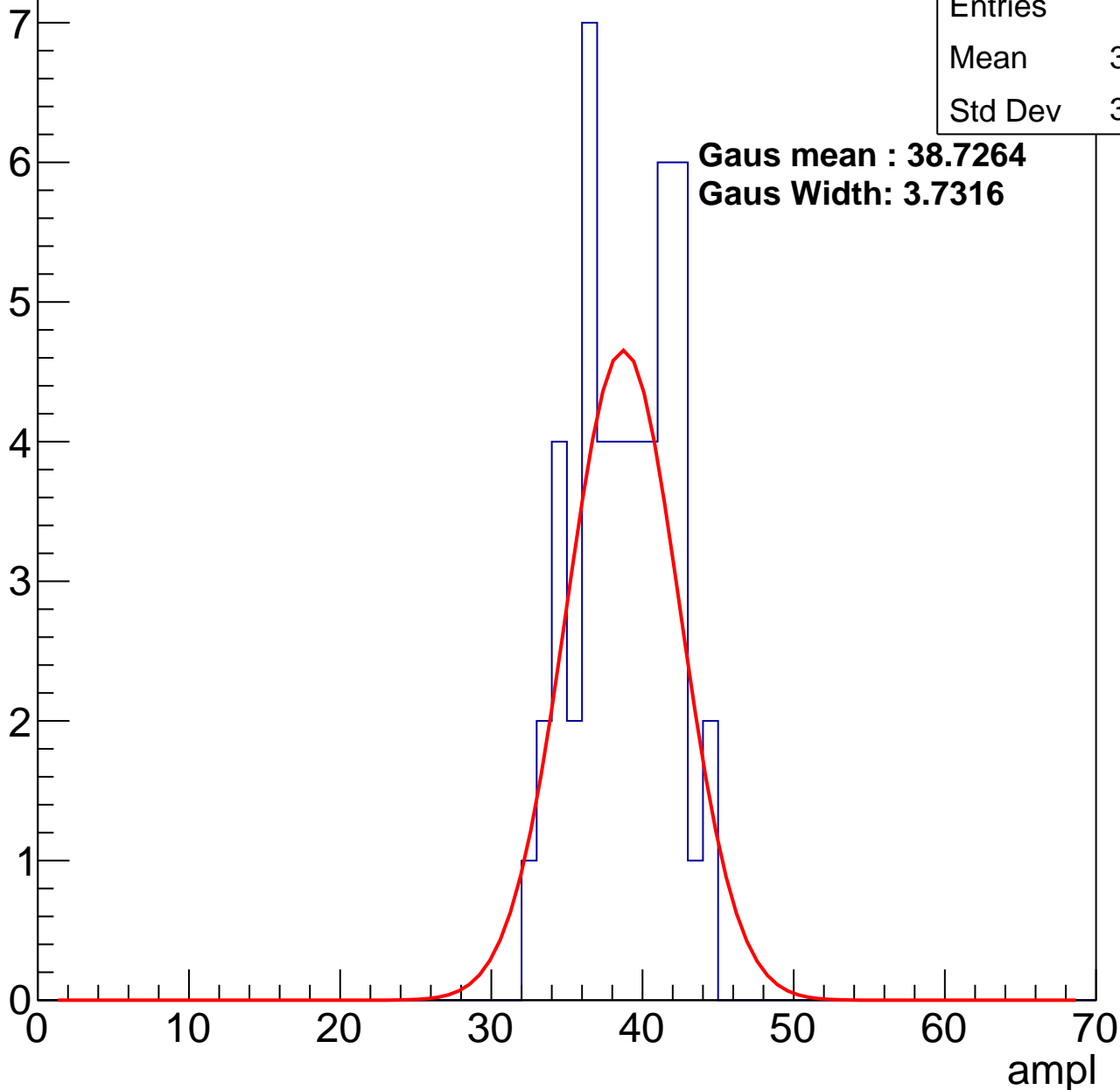
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	38.32
Std Dev	3.136

Gaus mean : 38.7264

Gaus Width: 3.7316



B1L003S, U19-ch3, adc2

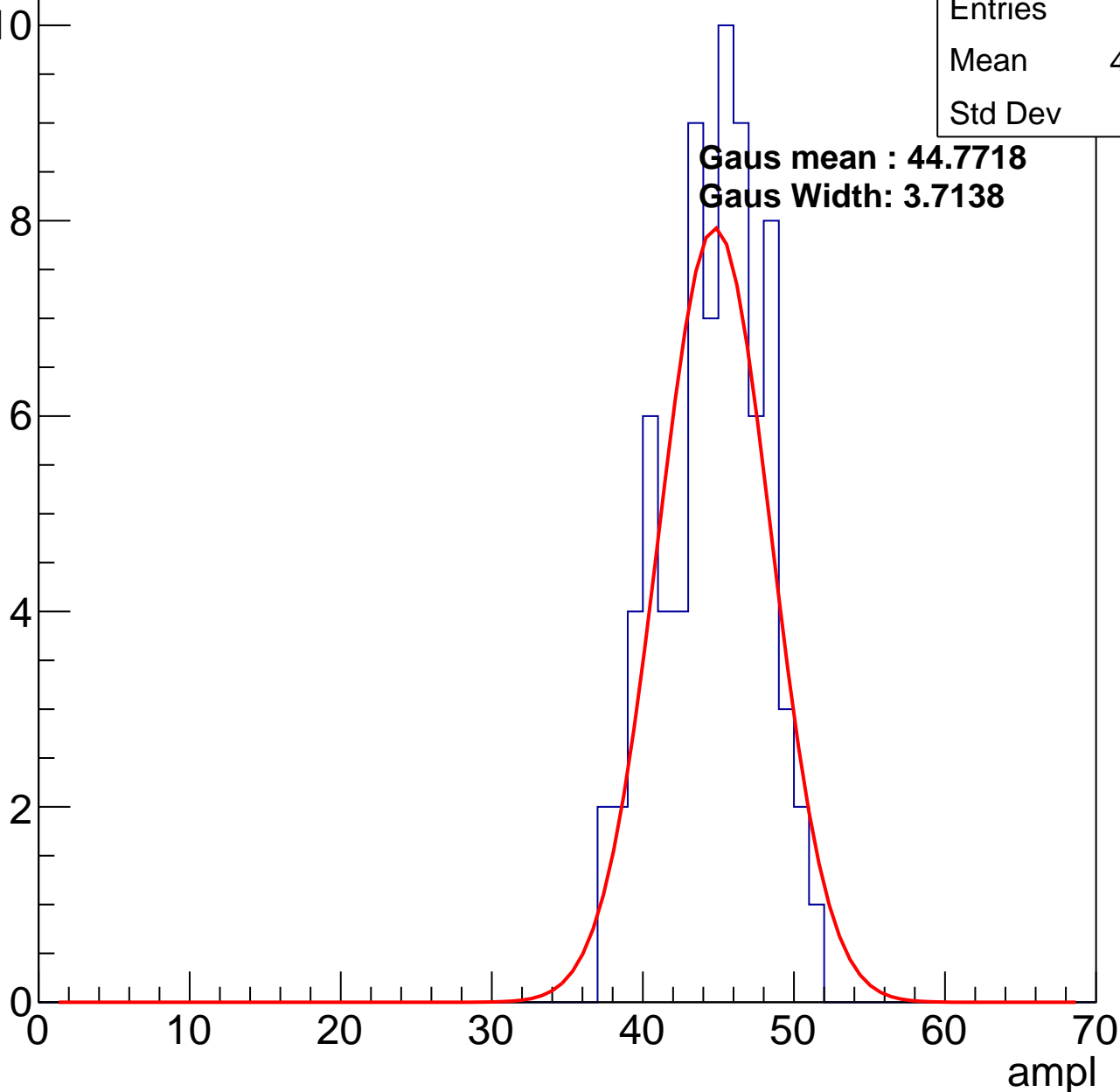
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	44.17
Std Dev	3.32

Gaus mean : 44.7718

Gaus Width: 3.7138

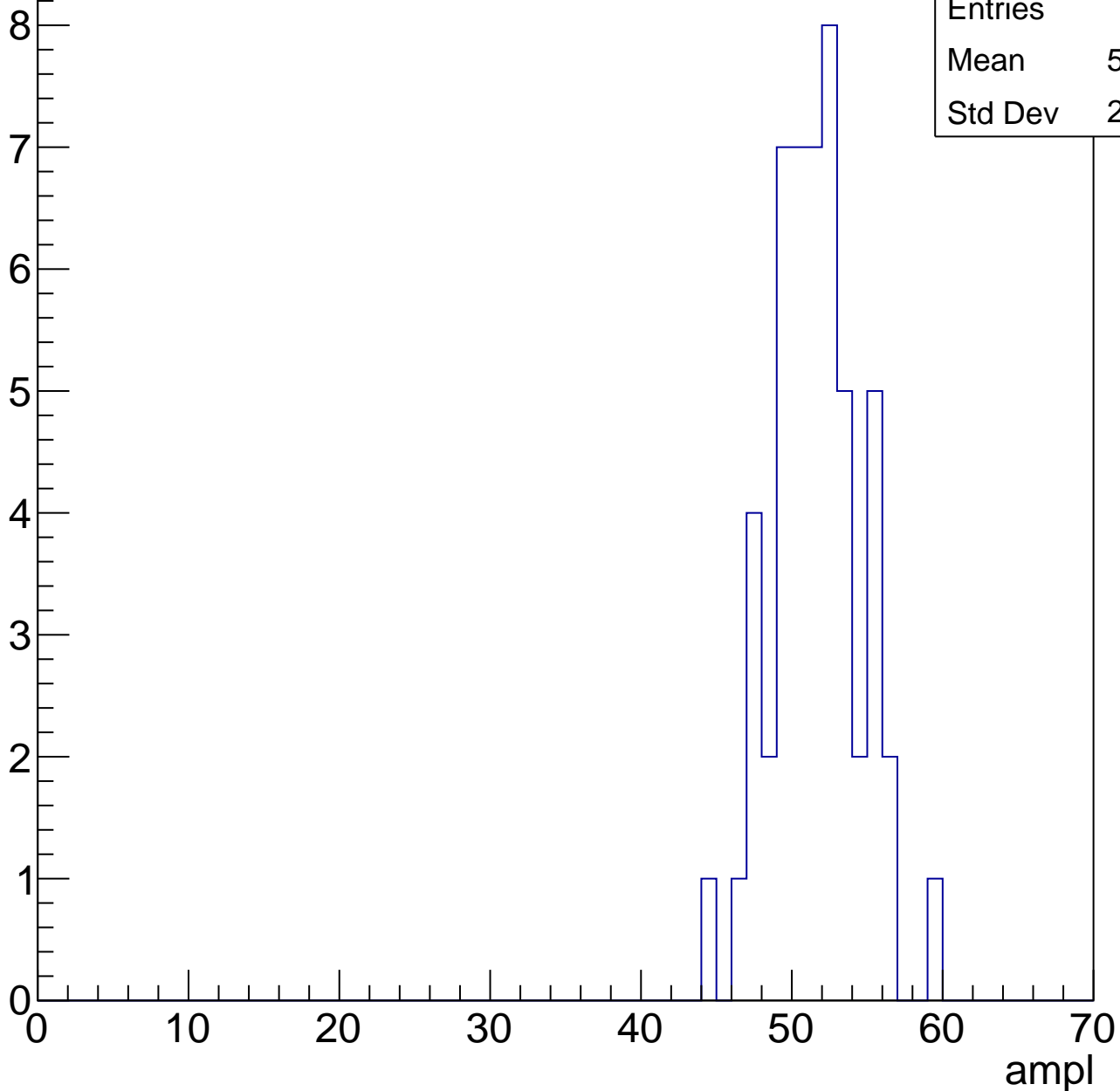


B1L003S, U19-ch3, adc3

calib_packv5_042523_0143.root, FC#13, port D2

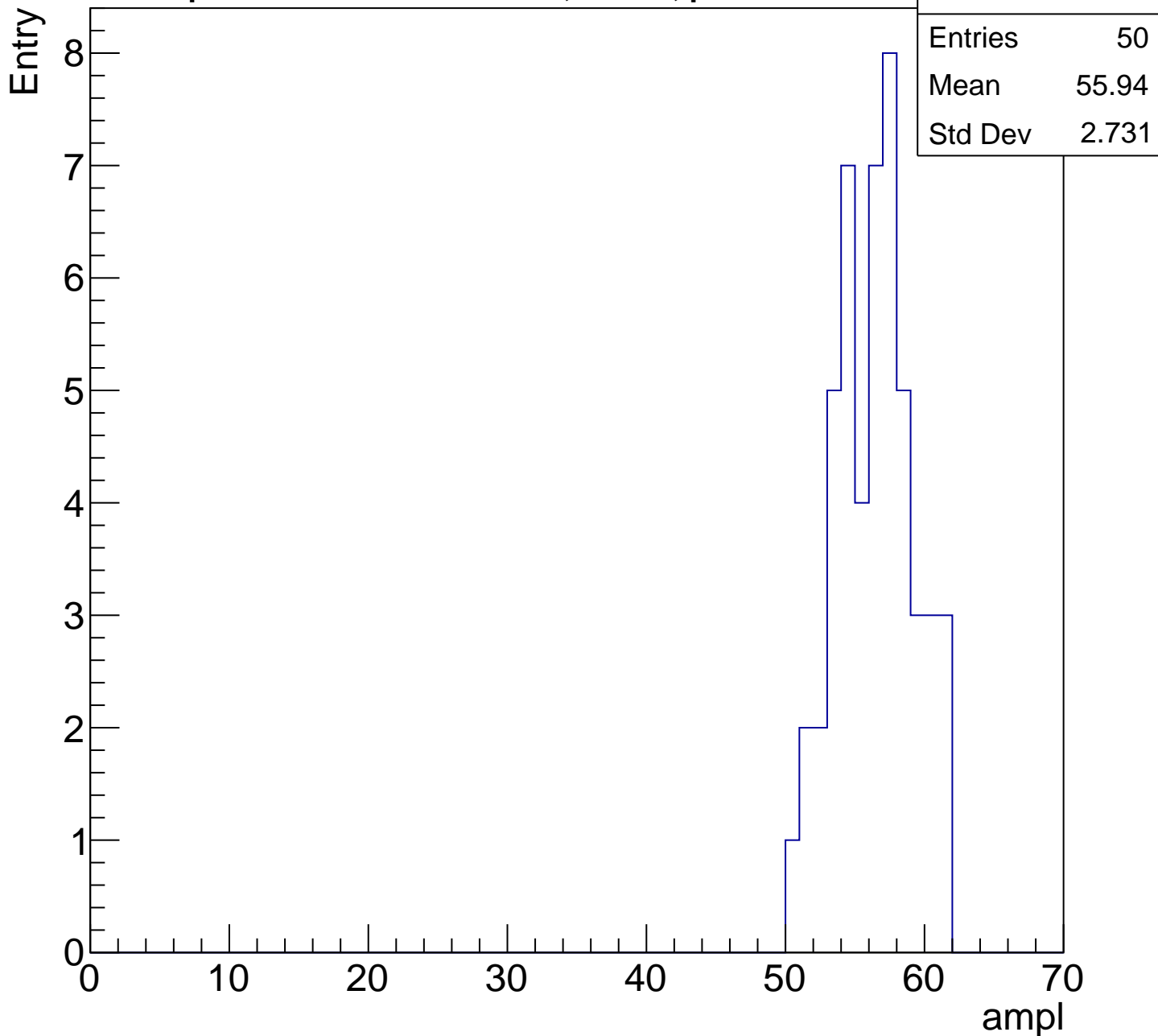
Entry

Entries	52
Mean	51.13
Std Dev	2.876



B1L003S, U19-ch3, adc4

calib_packv5_042523_0143.root, FC#13, port D2

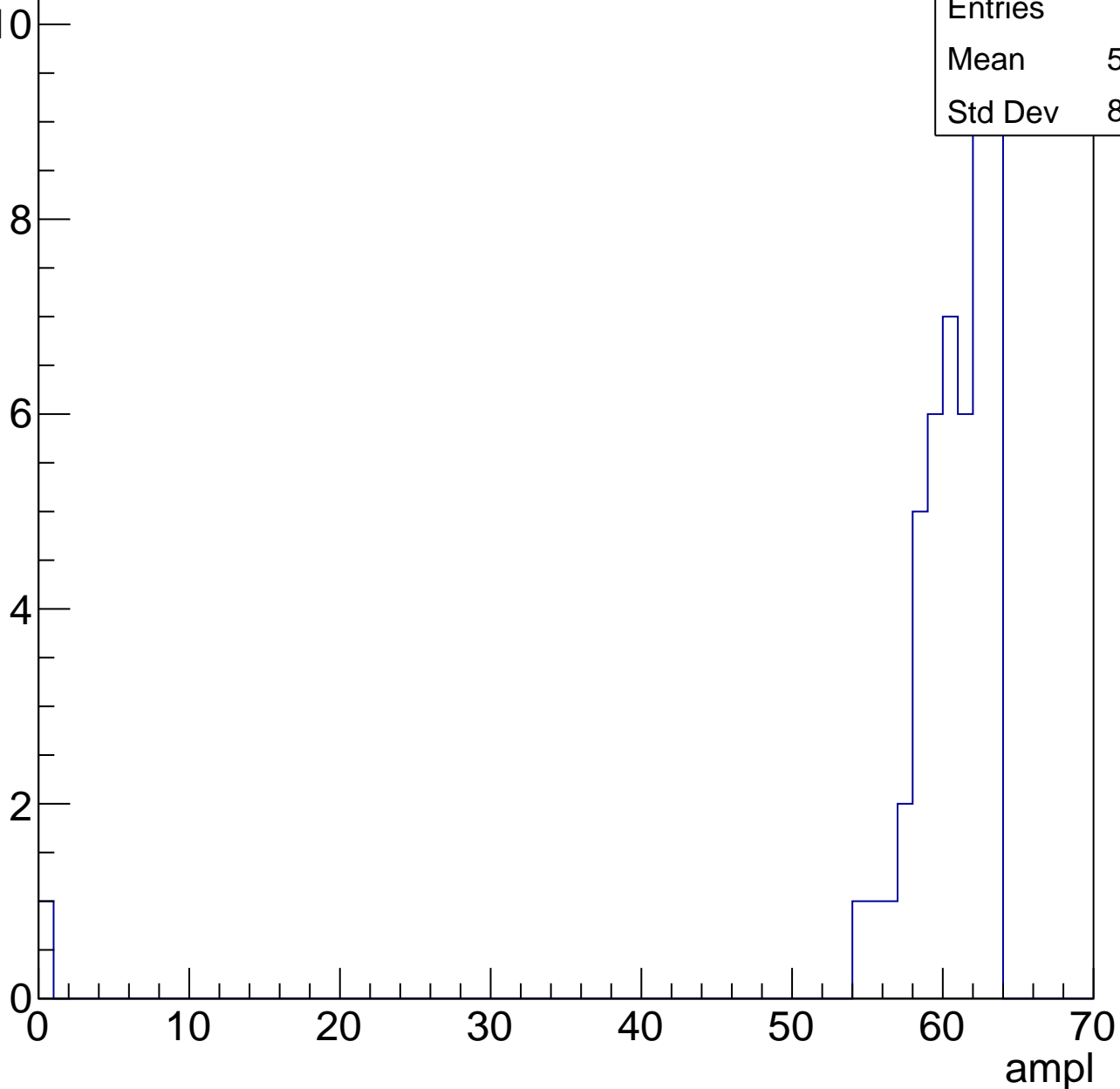


B1L003S, U19-ch3, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.12
Std Dev	8.824



B1L003S, U19-ch3, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch3, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch4, adc0

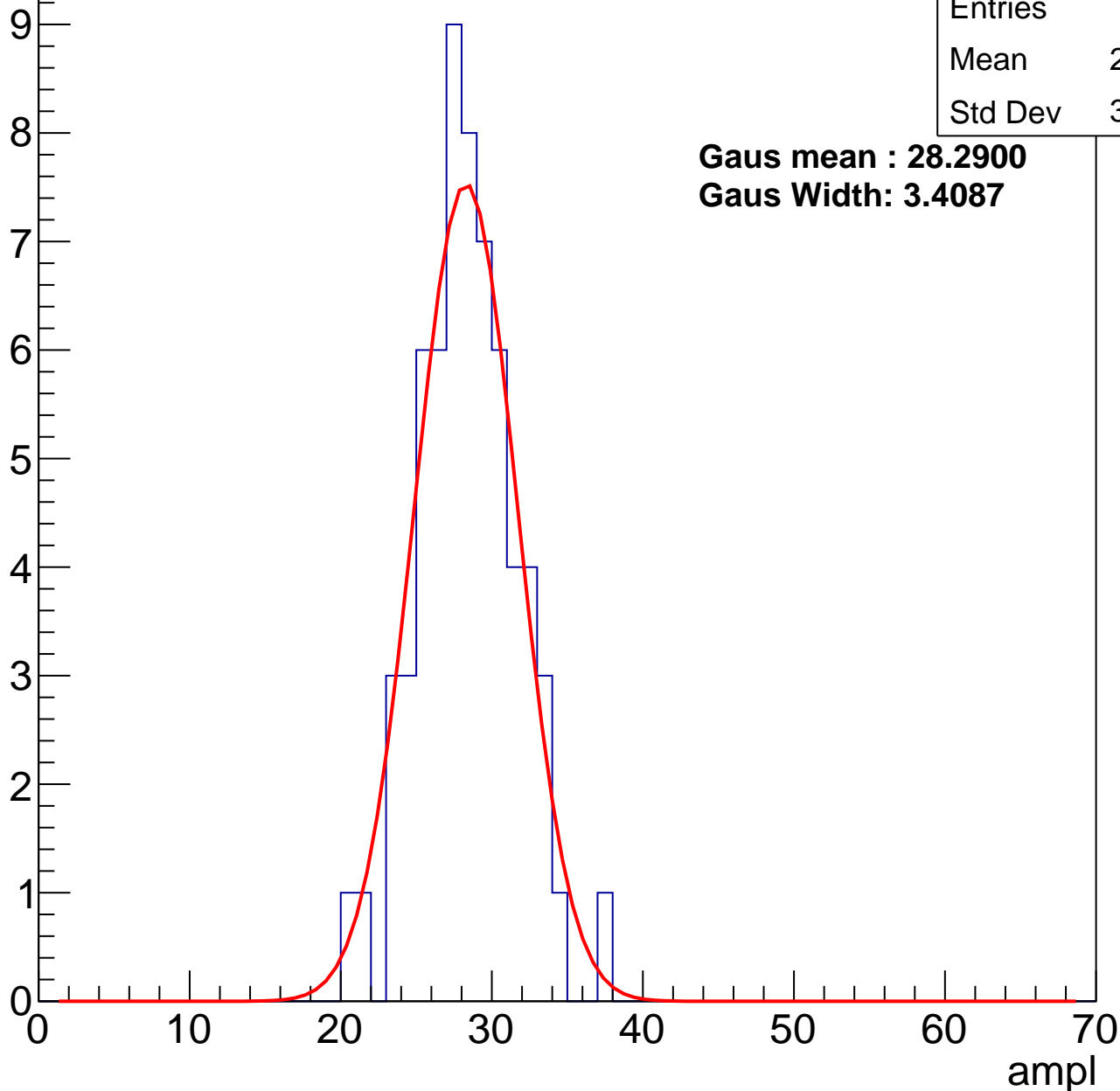
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	27.94
Std Dev	3.202

Gaus mean : 28.2900

Gaus Width: 3.4087



B1L003S, U19-ch4, adc1

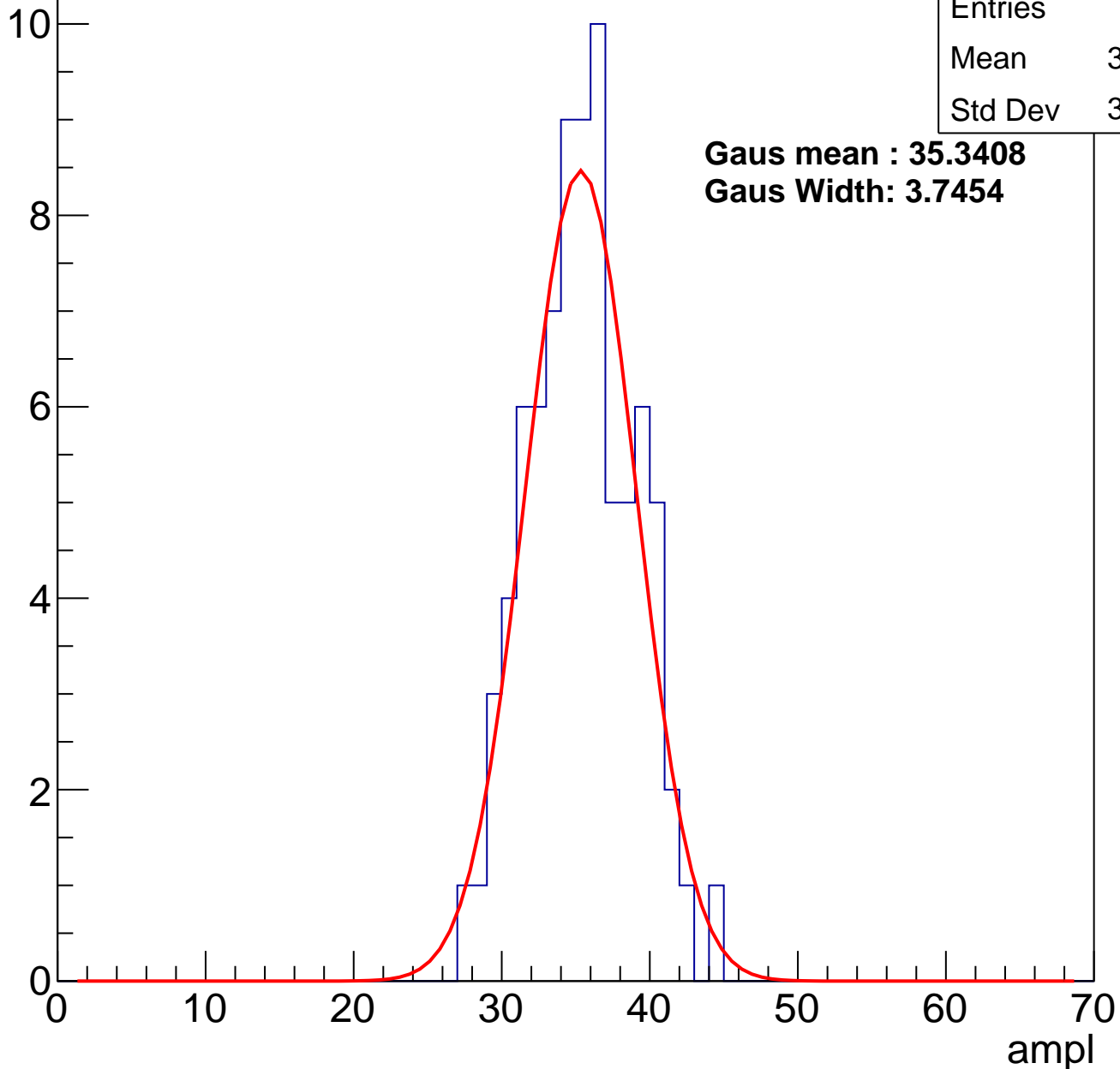
calib_packv5_042523_0143.root, FC#13, port D2

Entries	81
Mean	34.93
Std Dev	3.523

Gaus mean : 35.3408

Gaus Width: 3.7454

Entry



B1L003S, U19-ch4, adc2

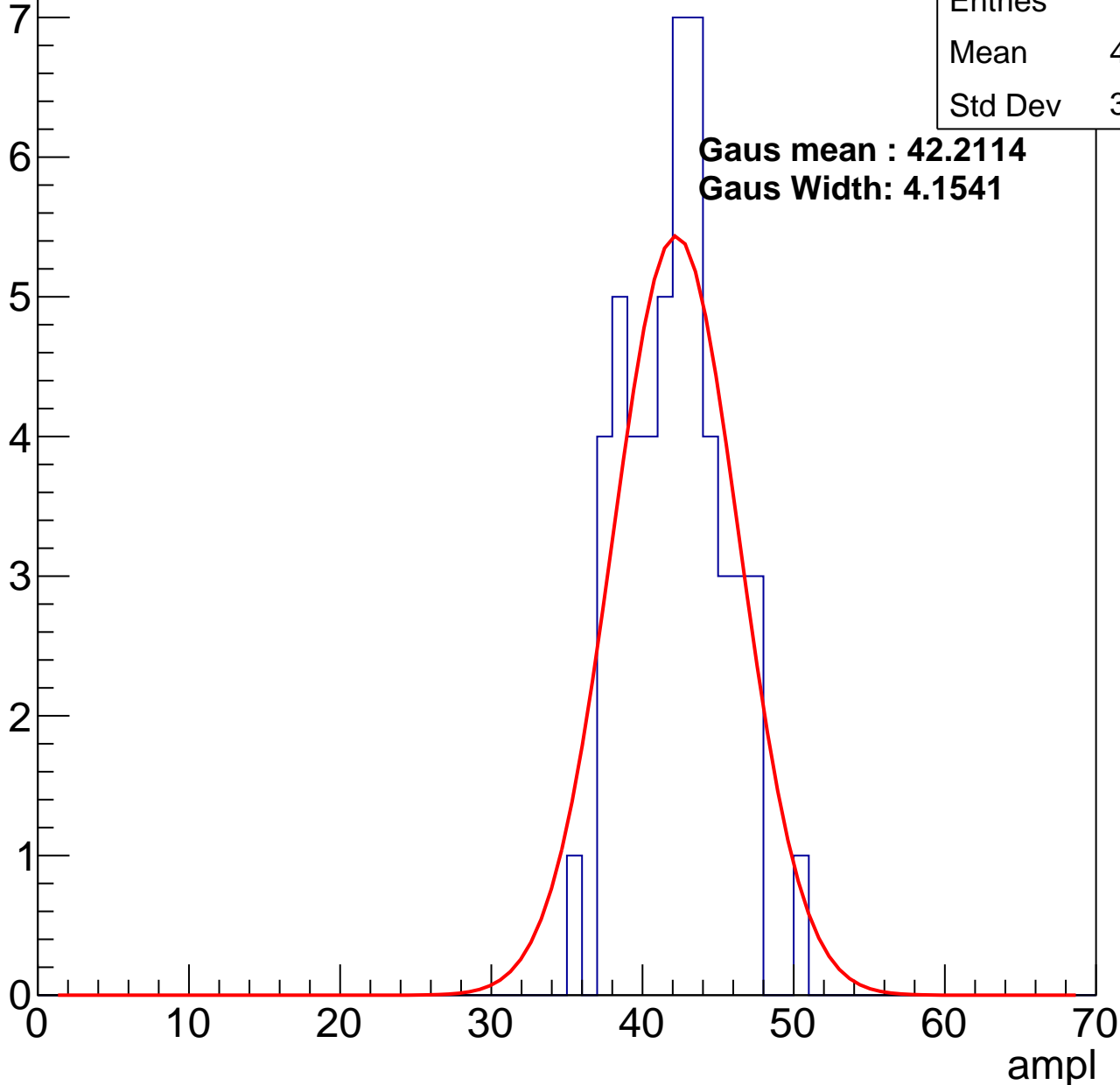
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	41.75
Std Dev	3.192

Gaus mean : 42.2114

Gaus Width: 4.1541

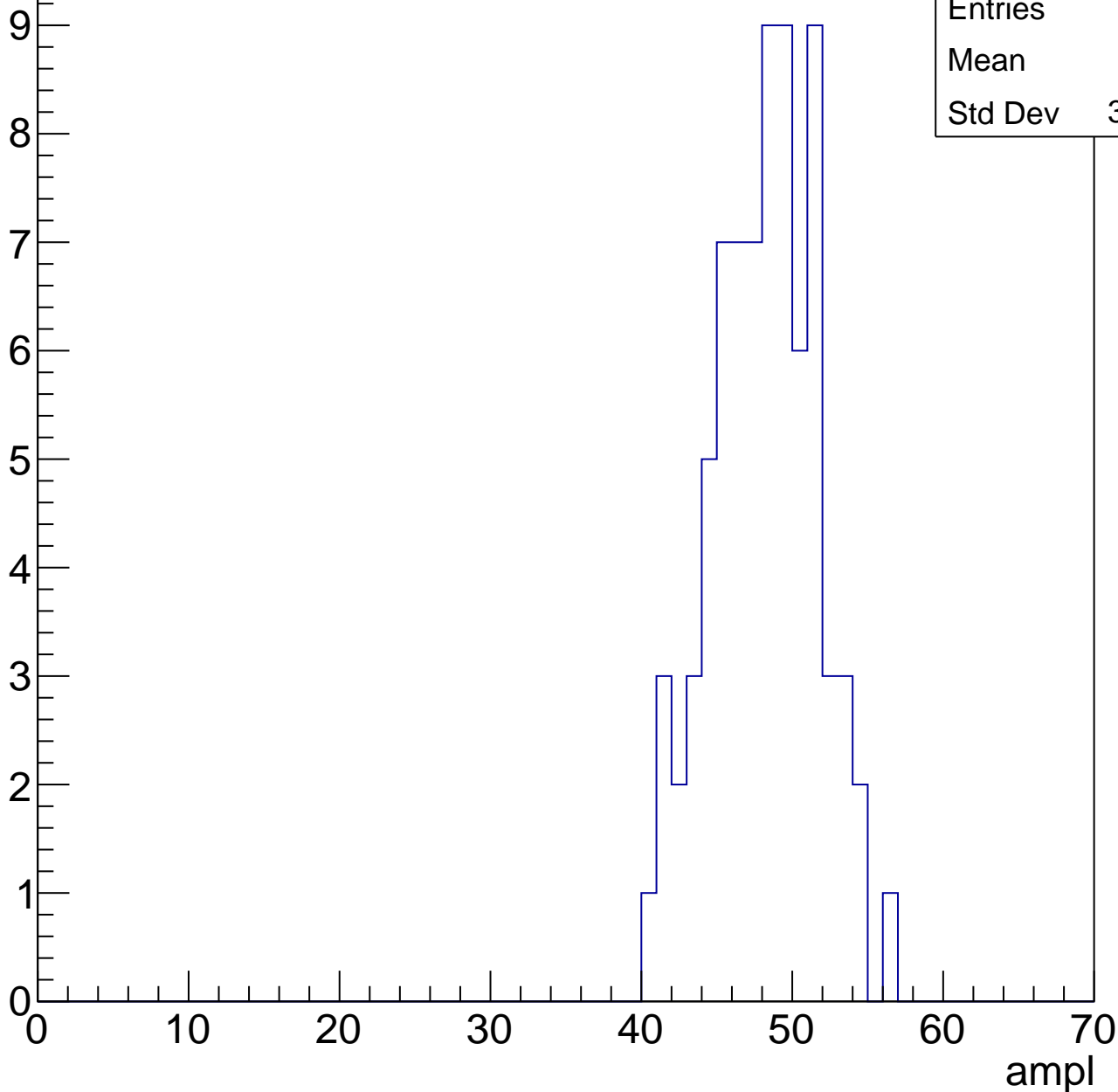


B1L003S, U19-ch4, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	47.7
Std Dev	3.423

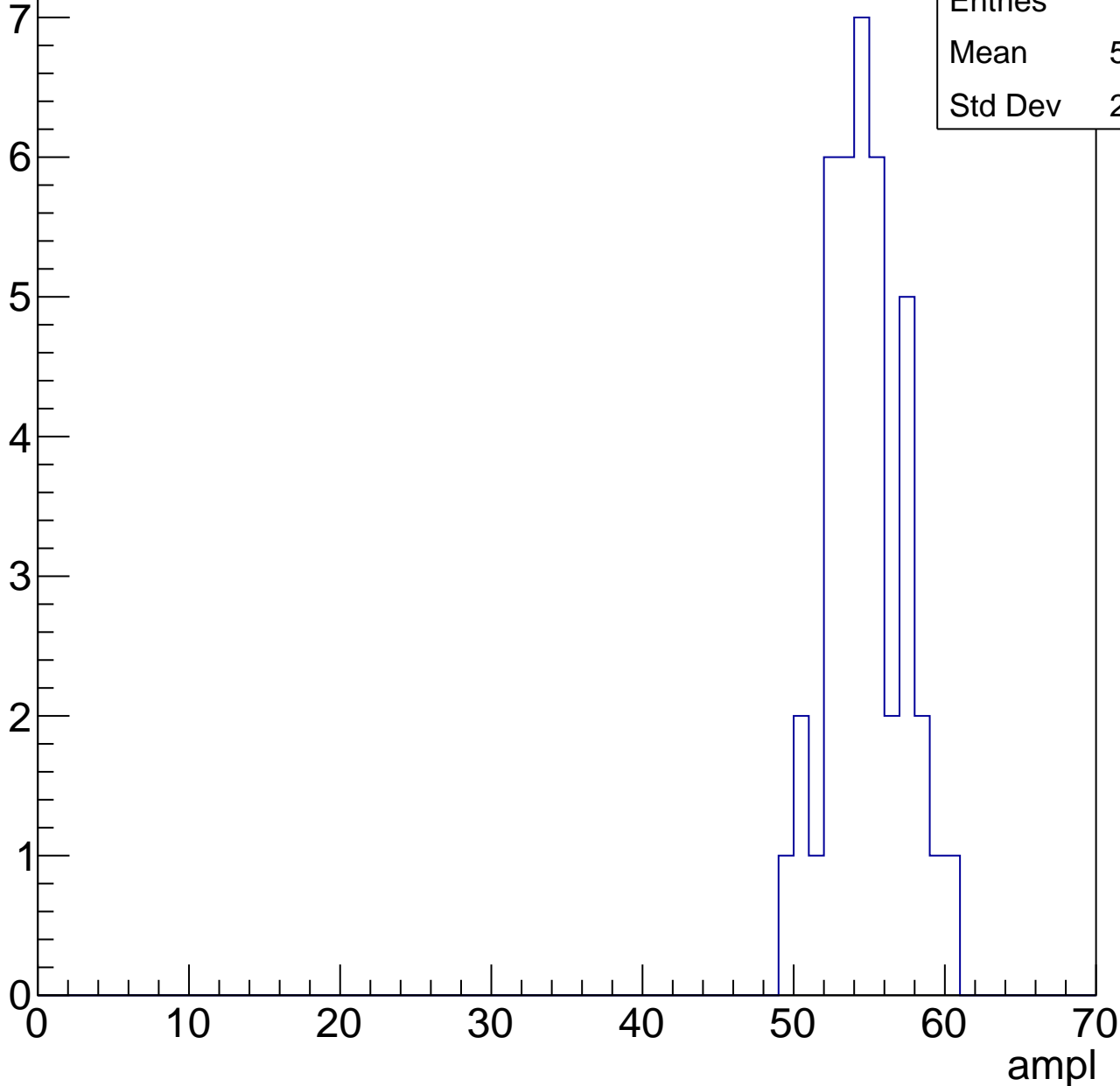


B1L003S, U19-ch4, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	54.25
Std Dev	2.477

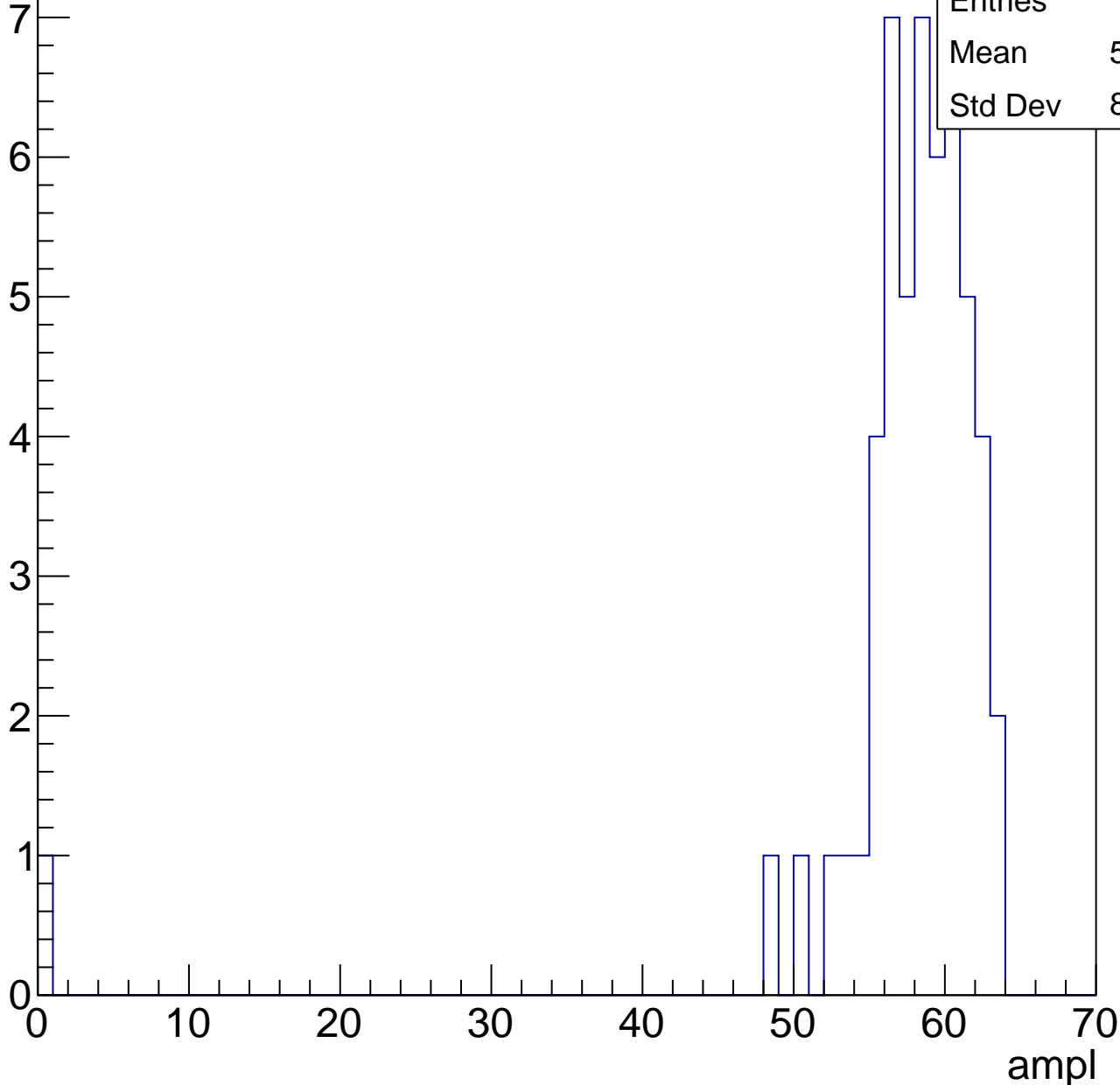


B1L003S, U19-ch4, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	56.85
Std Dev	8.464

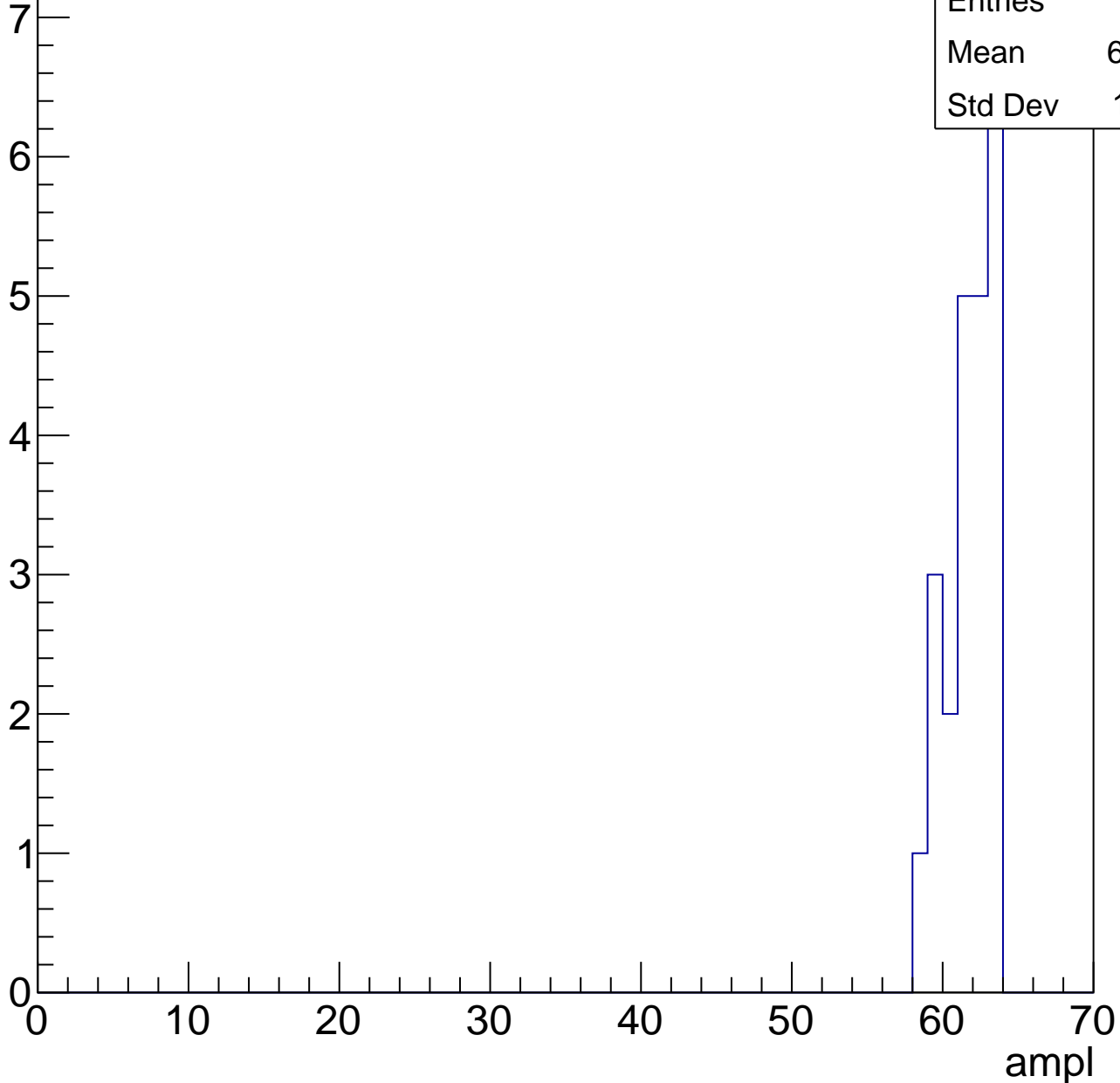


B1L003S, U19-ch4, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

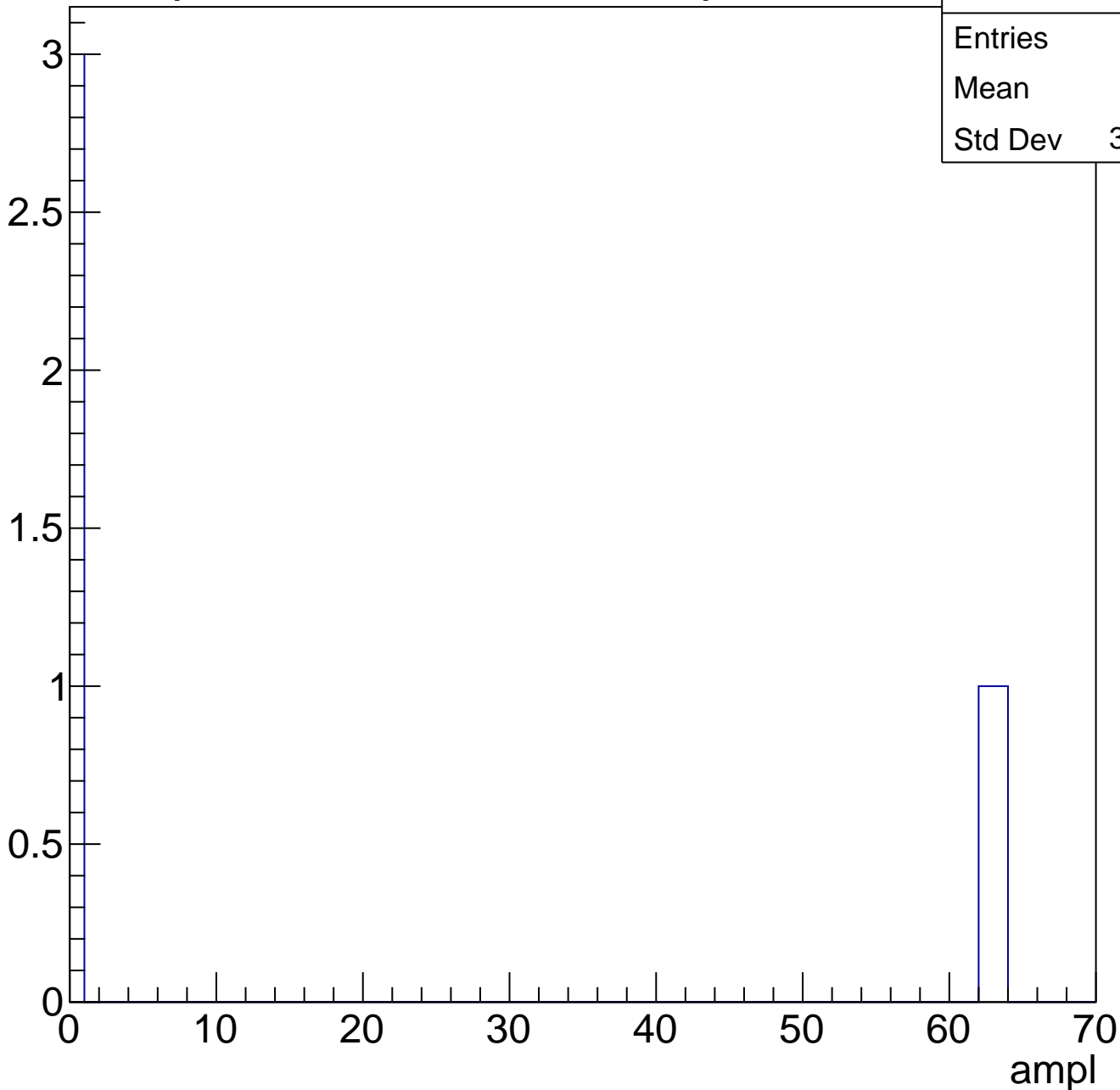
Entries	23
Mean	61.35
Std Dev	1.521



B1L003S, U19-ch4, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch5, adc0

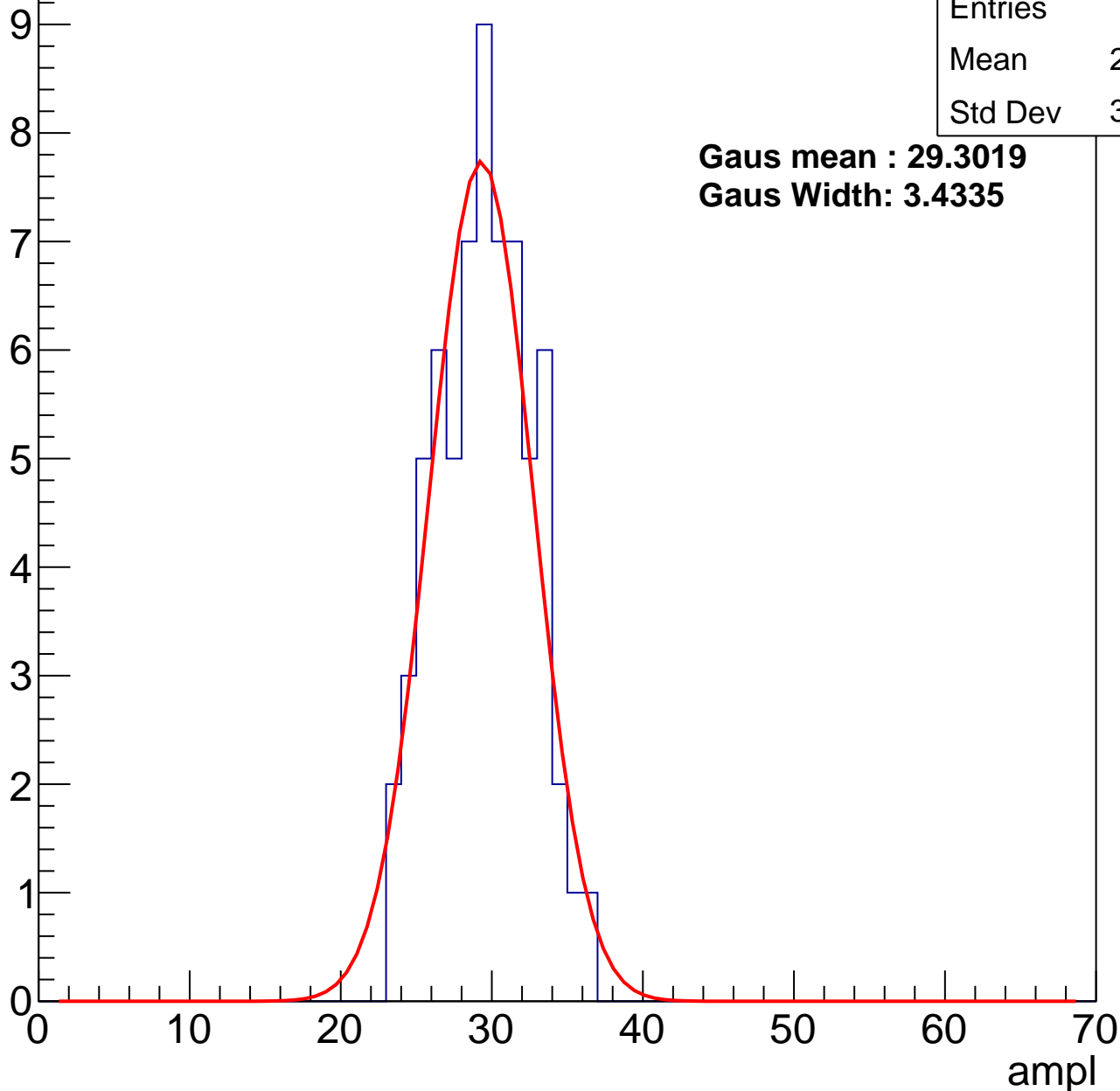
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.02
Std Dev	3.062

Gaus mean : 29.3019

Gaus Width: 3.4335



B1L003S, U19-ch5, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	35.75
Std Dev	3.345

Gaus mean : 36.2840

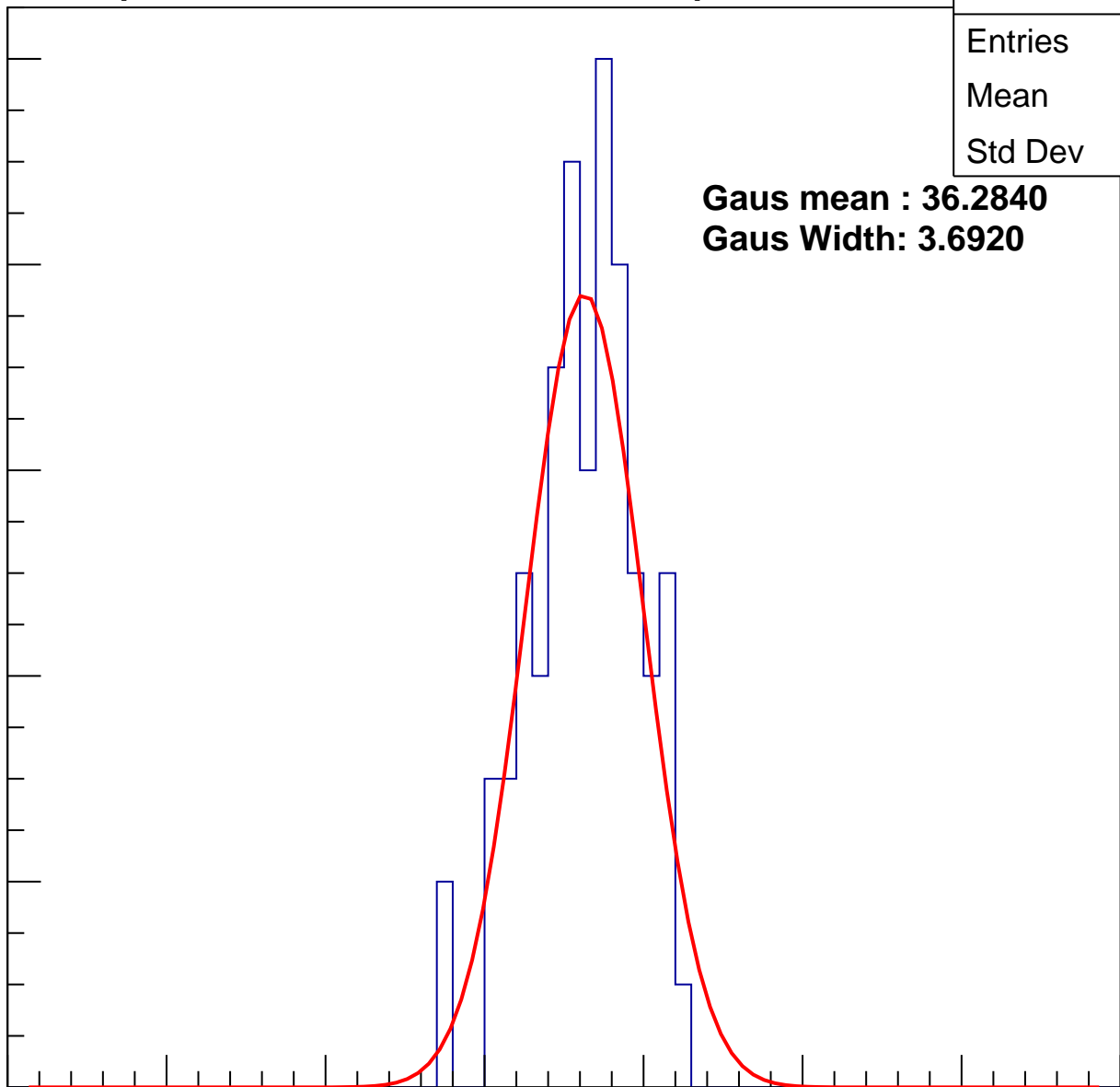
Gaus Width: 3.6920

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch5, adc2

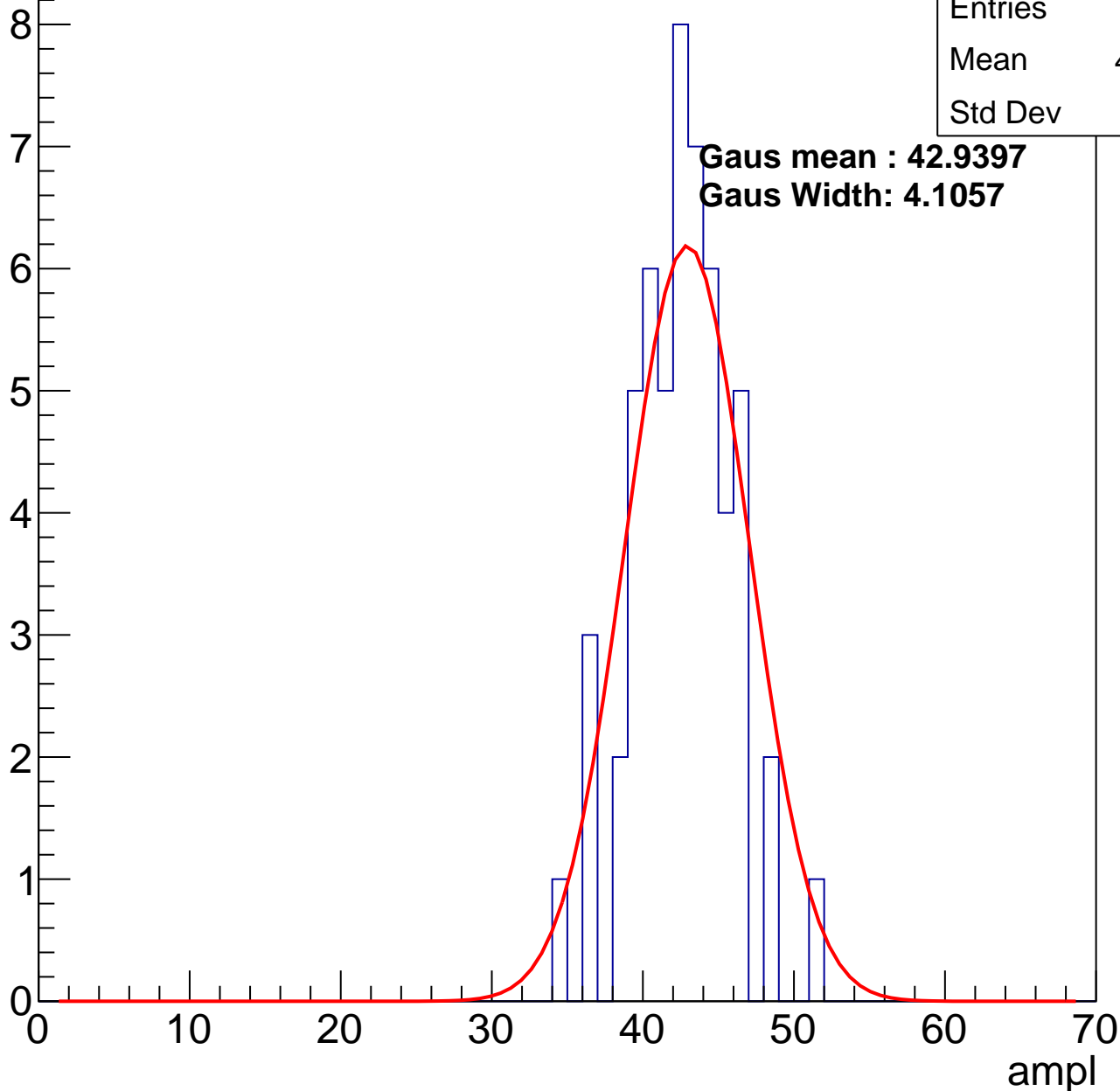
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.11
Std Dev	3.24

Gaus mean : 42.9397

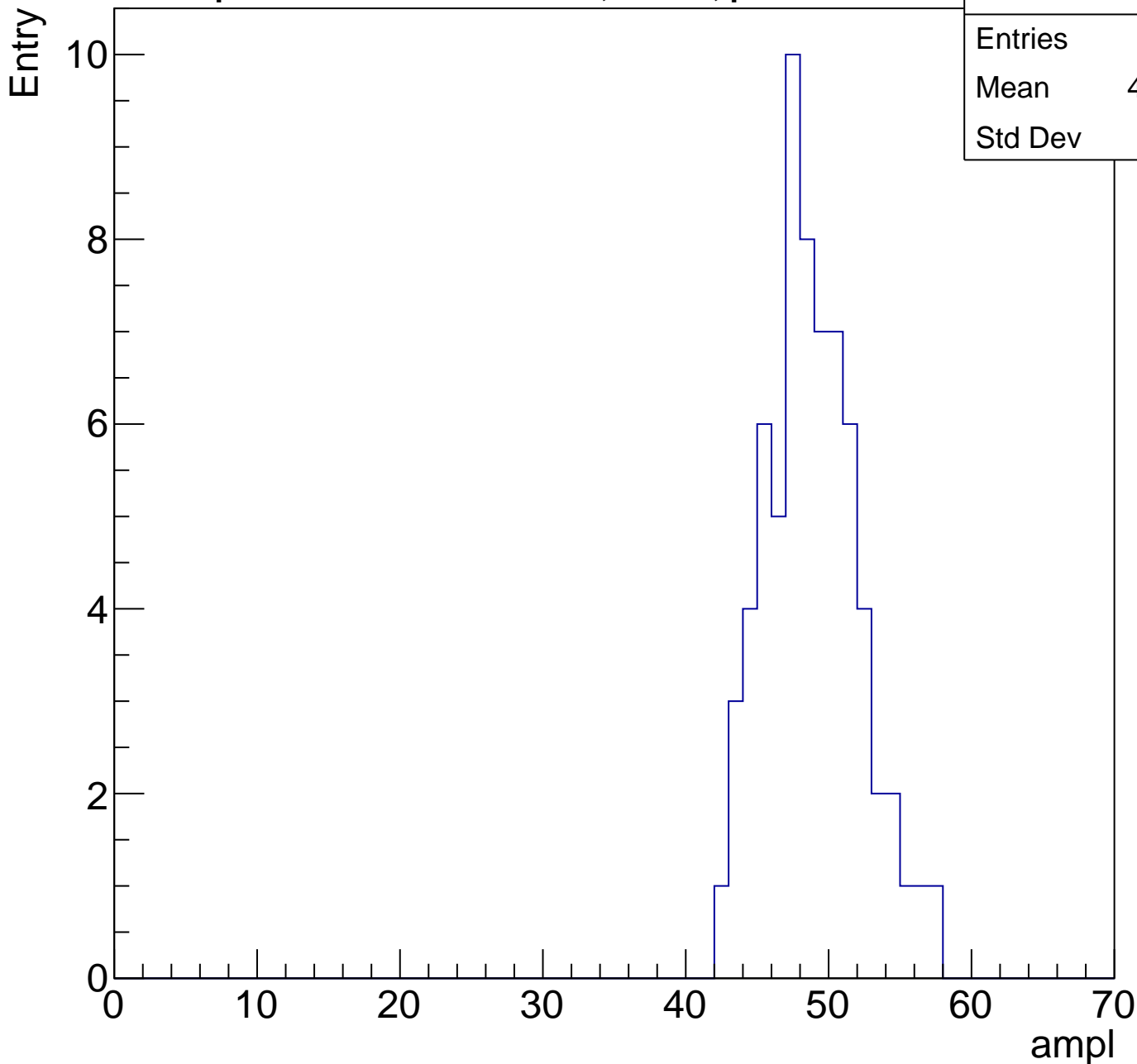
Gaus Width: 4.1057



B1L003S, U19-ch5, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	48.38
Std Dev	3.25

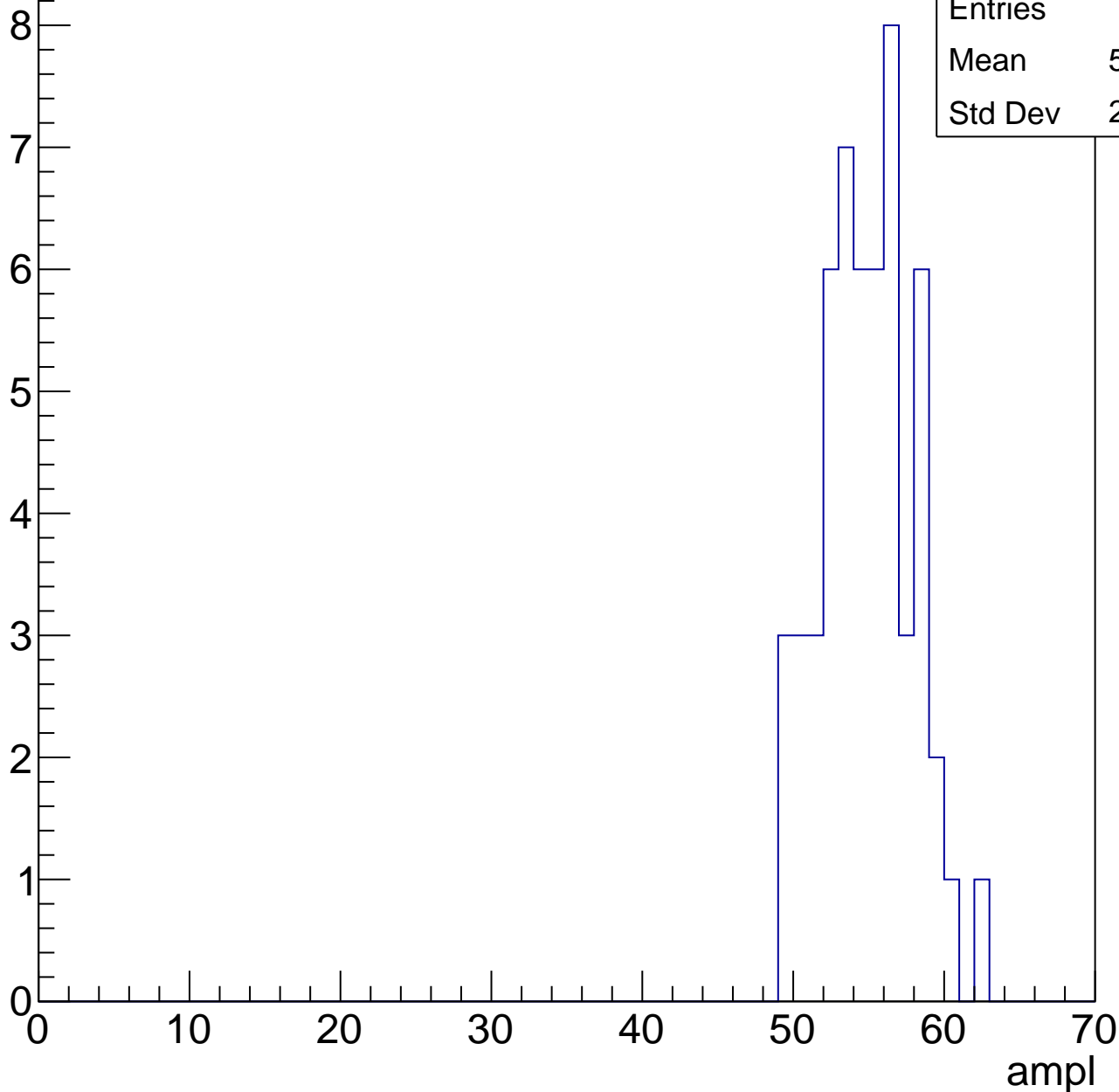


B1L003S, U19-ch5, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.44
Std Dev	2.959

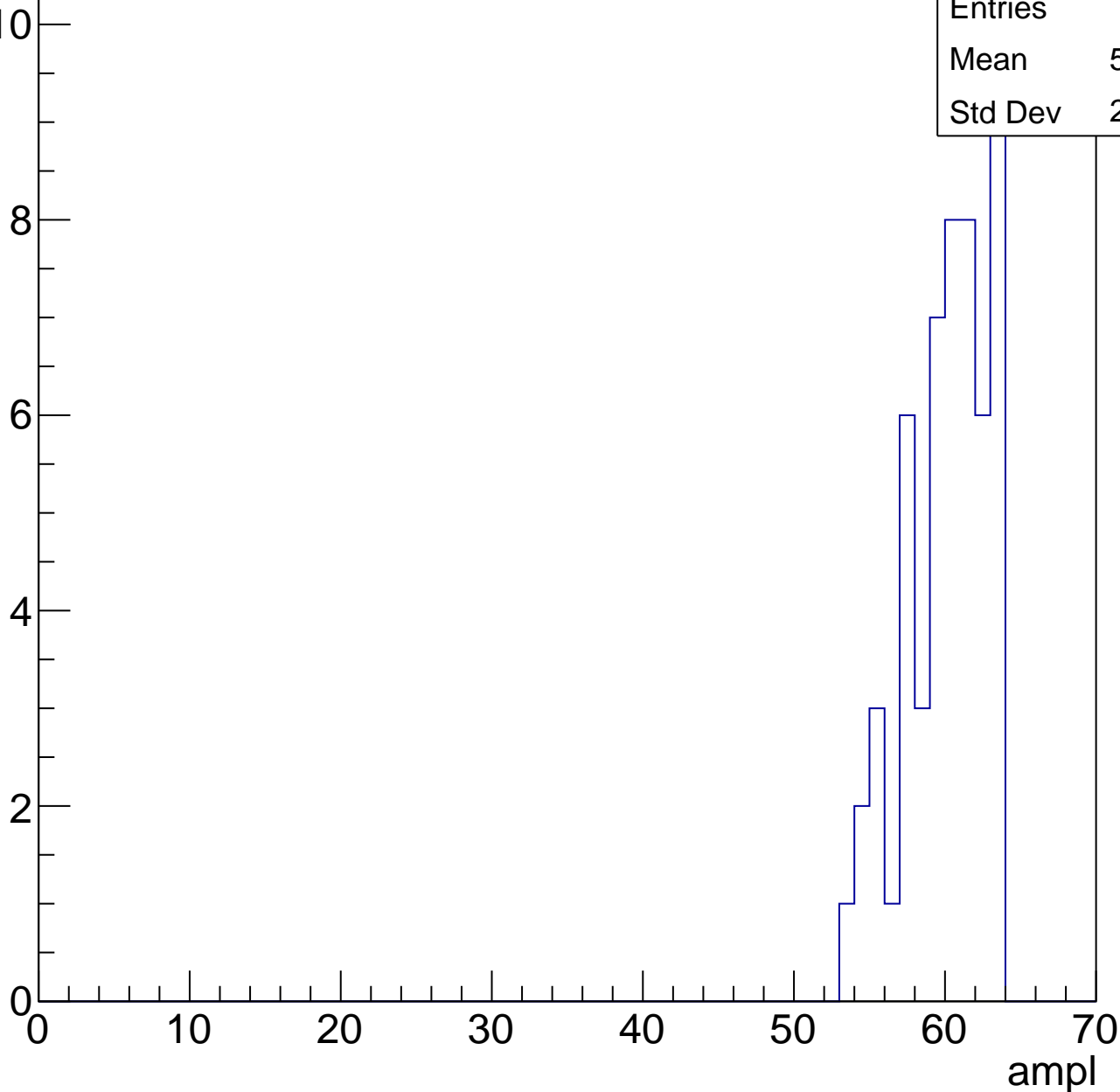


B1L003S, U19-ch5, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	59.65
Std Dev	2.698



B1L003S, U19-ch5, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

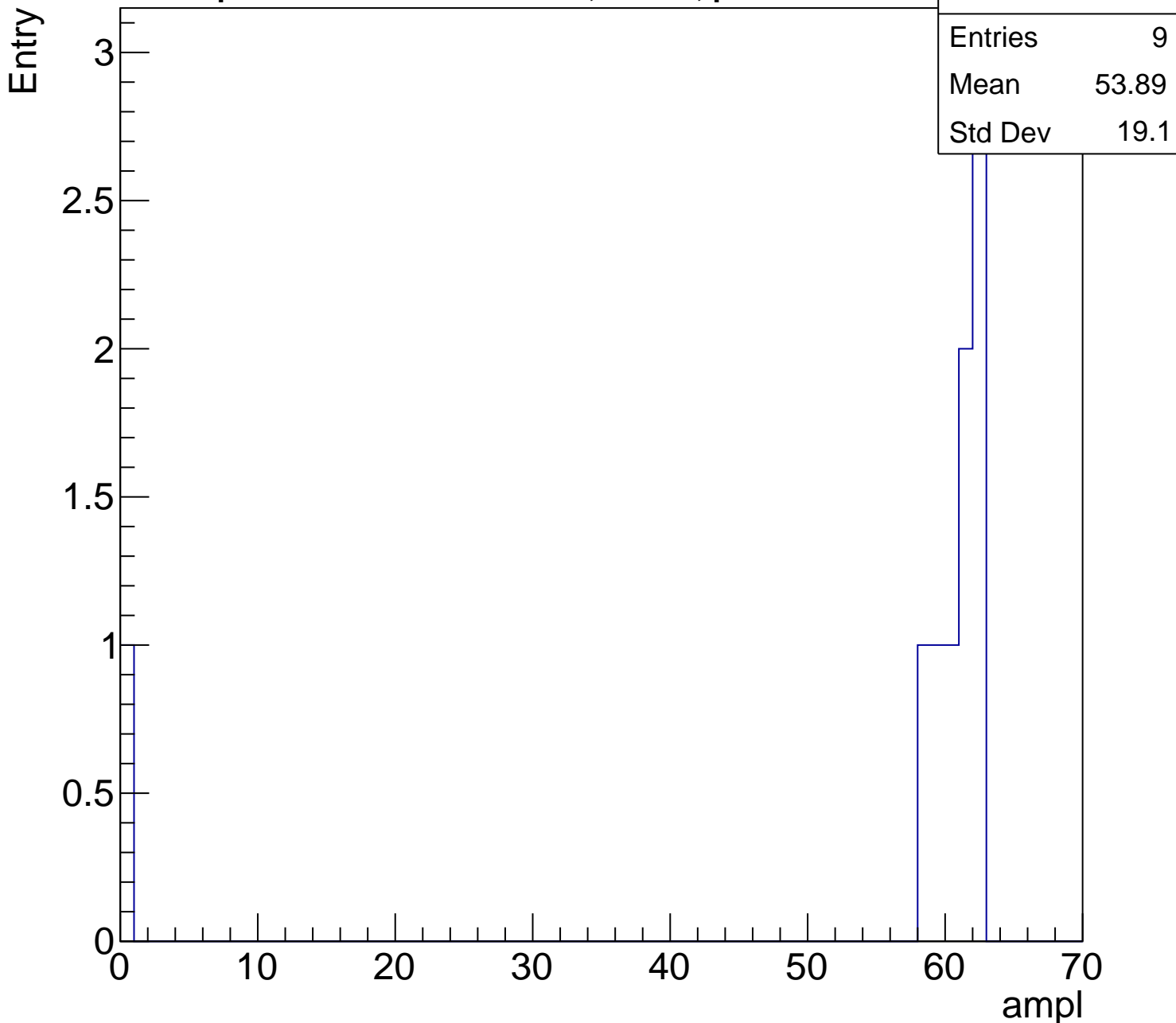
9

Mean

53.89

Std Dev

19.1



B1L003S, U19-ch5, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch6, adc0

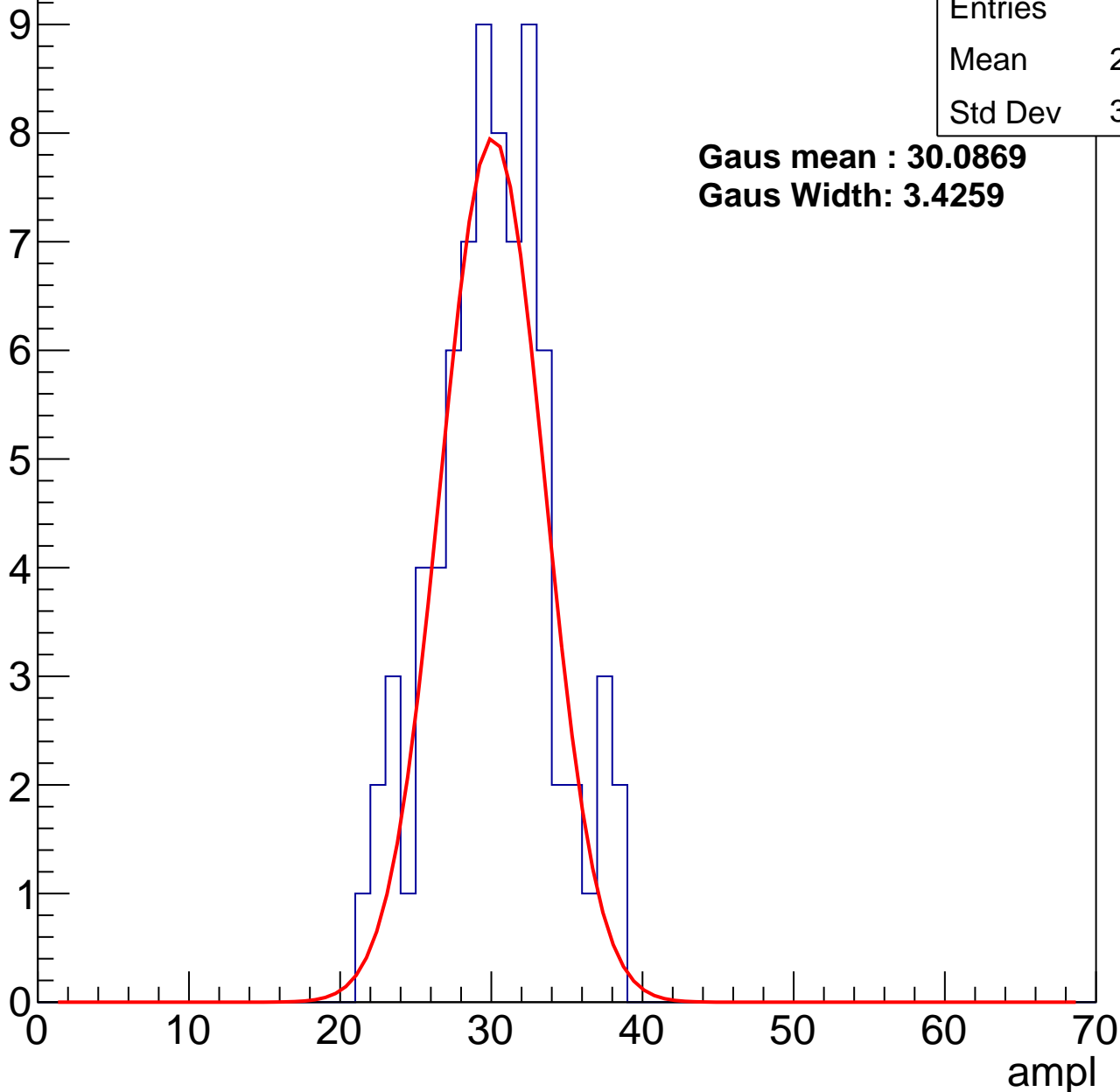
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	29.68
Std Dev	3.829

Gaus mean : 30.0869

Gaus Width: 3.4259



B1L003S, U19-ch6, adc1

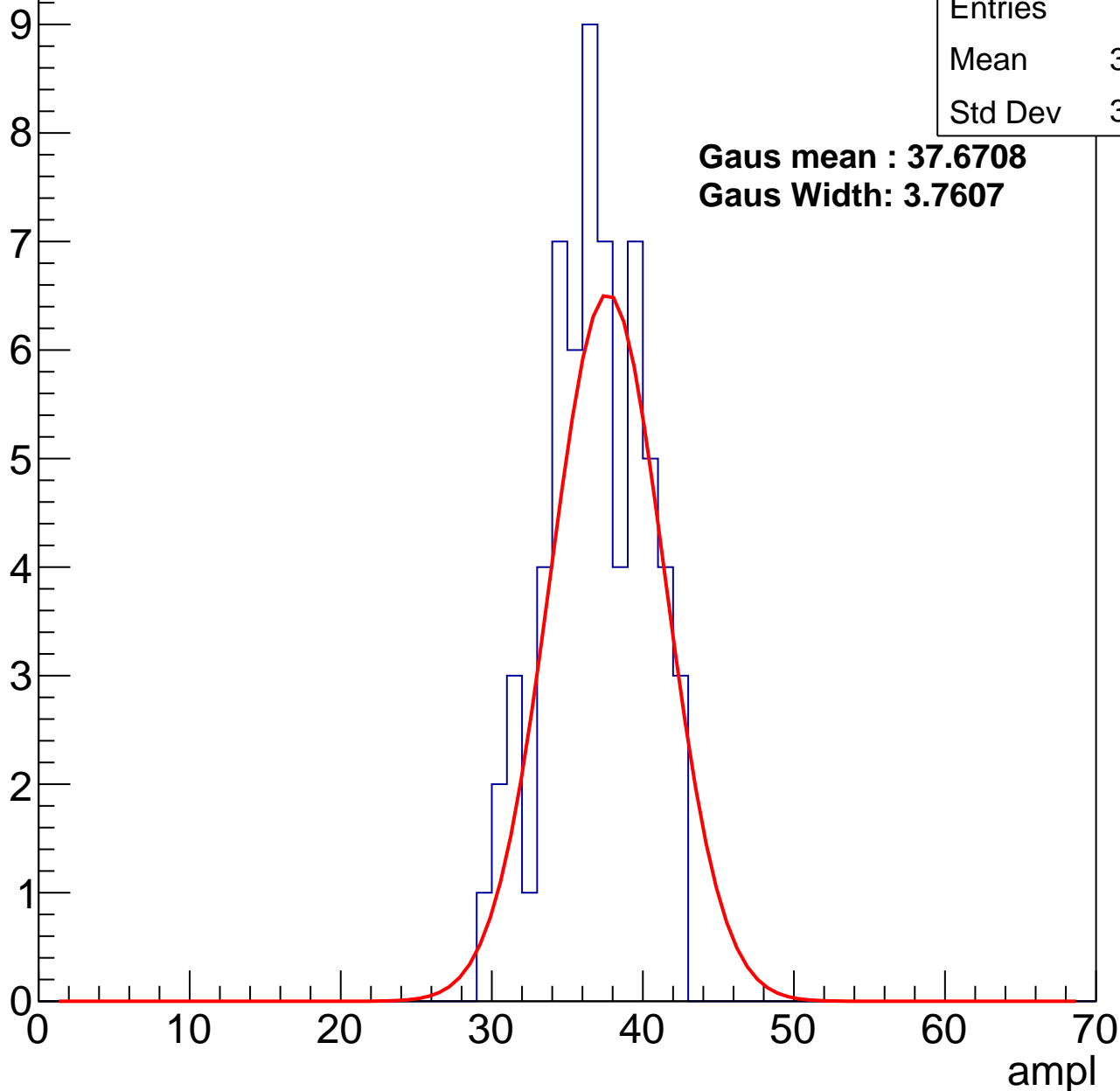
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	36.38
Std Dev	3.204

Gaus mean : 37.6708

Gaus Width: 3.7607



B1L003S, U19-ch6, adc2

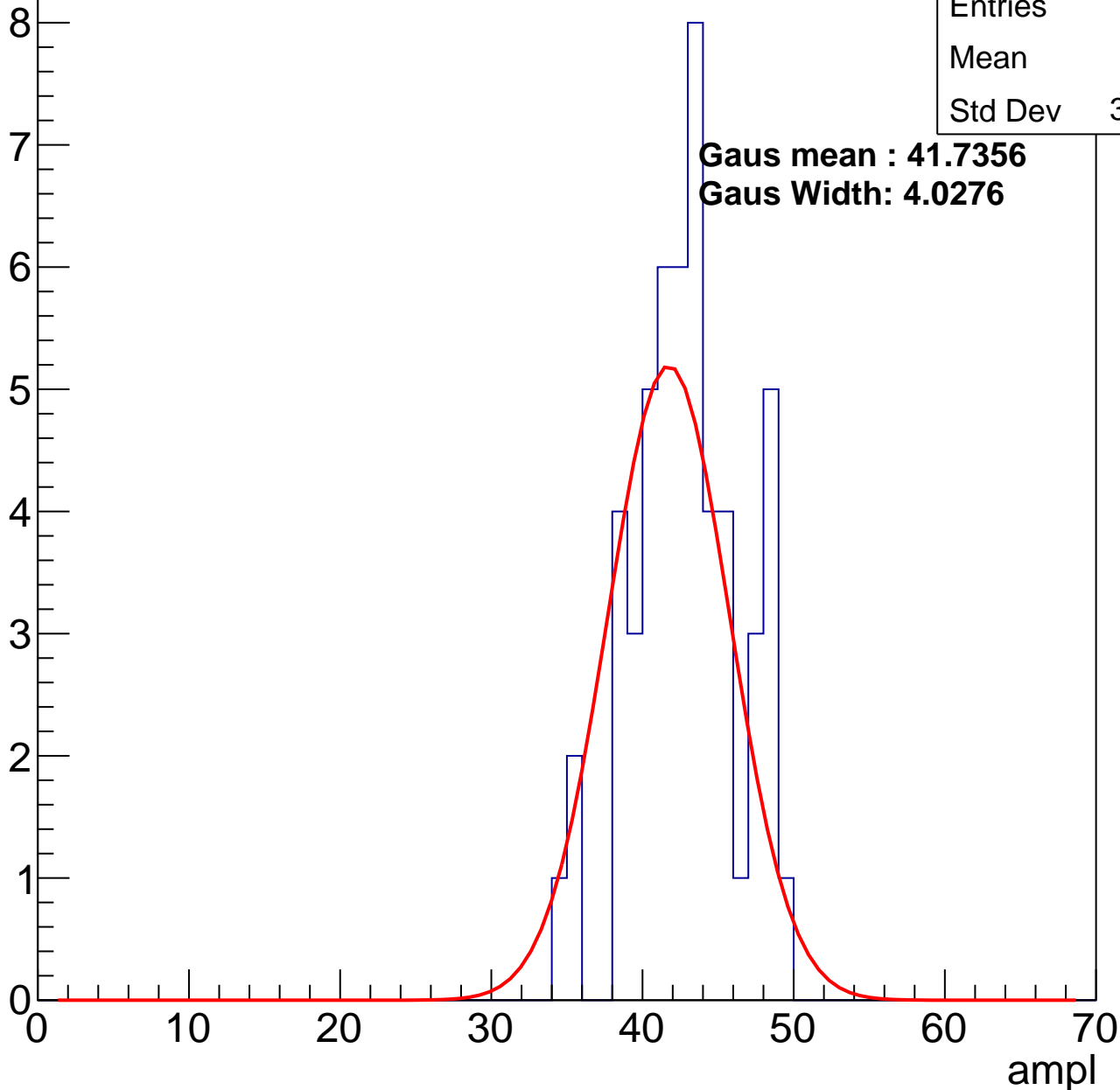
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.4
Std Dev	3.504

Gaus mean : 41.7356

Gaus Width: 4.0276

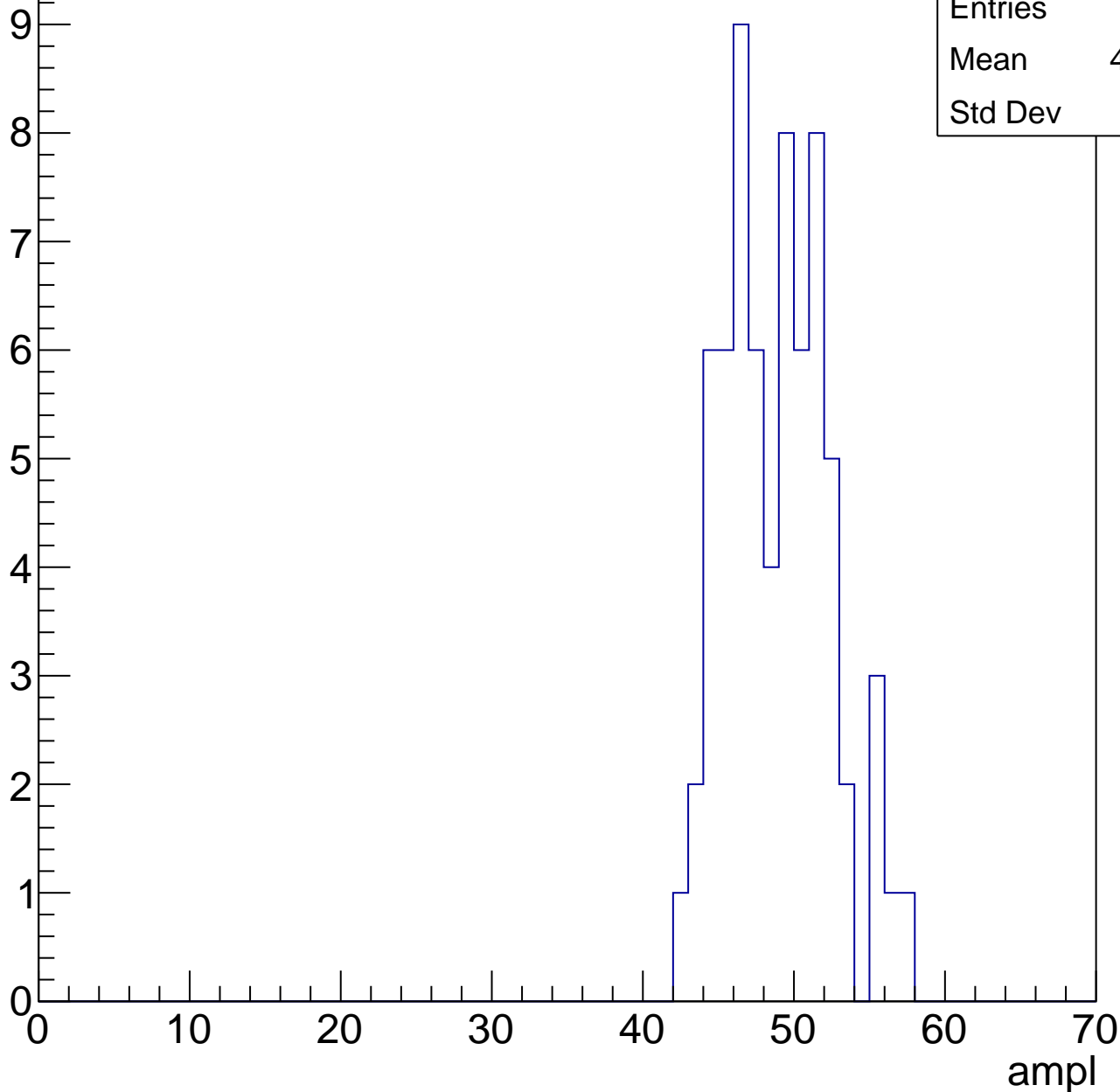


B1L003S, U19-ch6, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	48.44
Std Dev	3.41

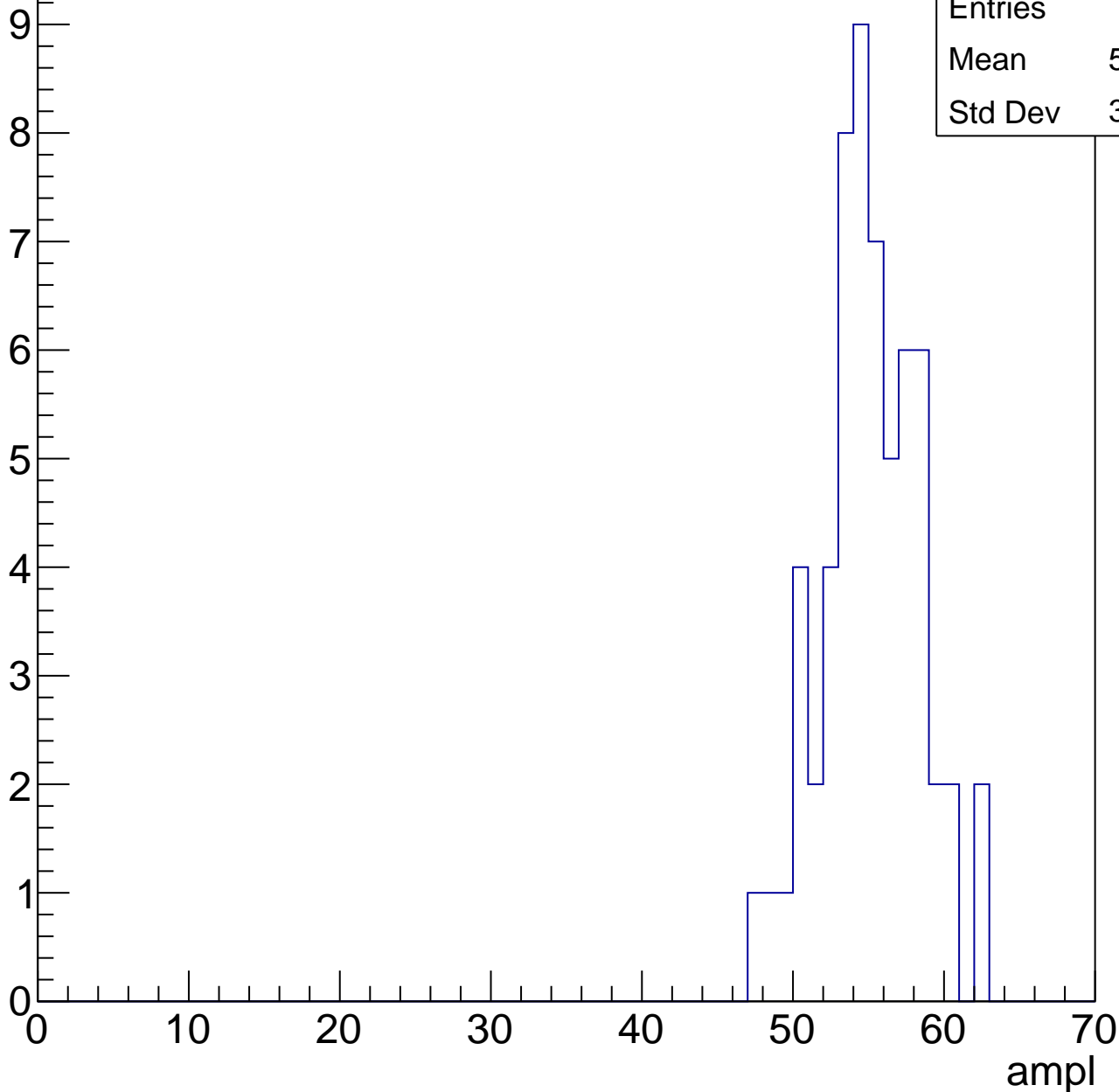


B1L003S, U19-ch6, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.68
Std Dev	3.202

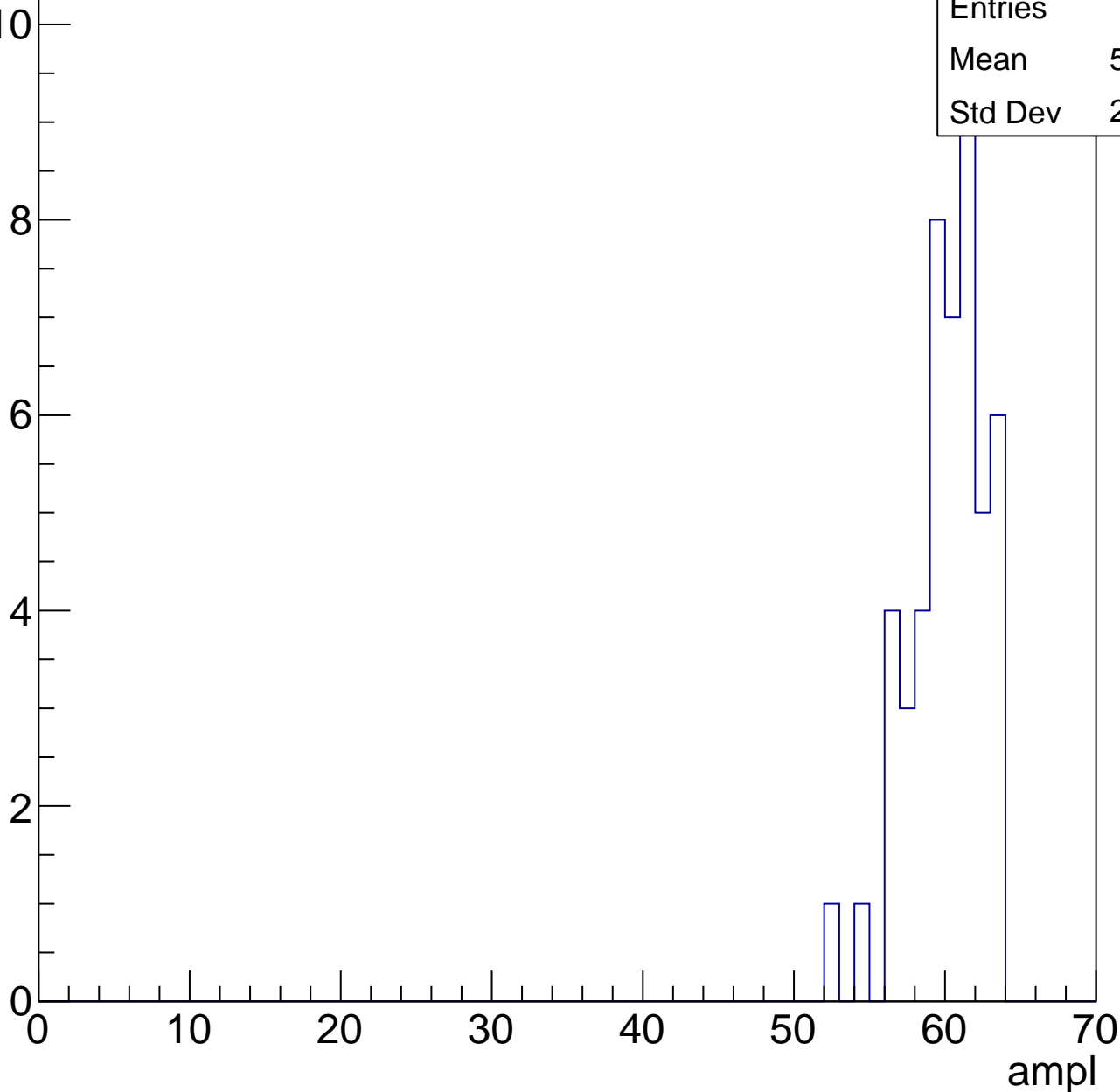


B1L003S, U19-ch6, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

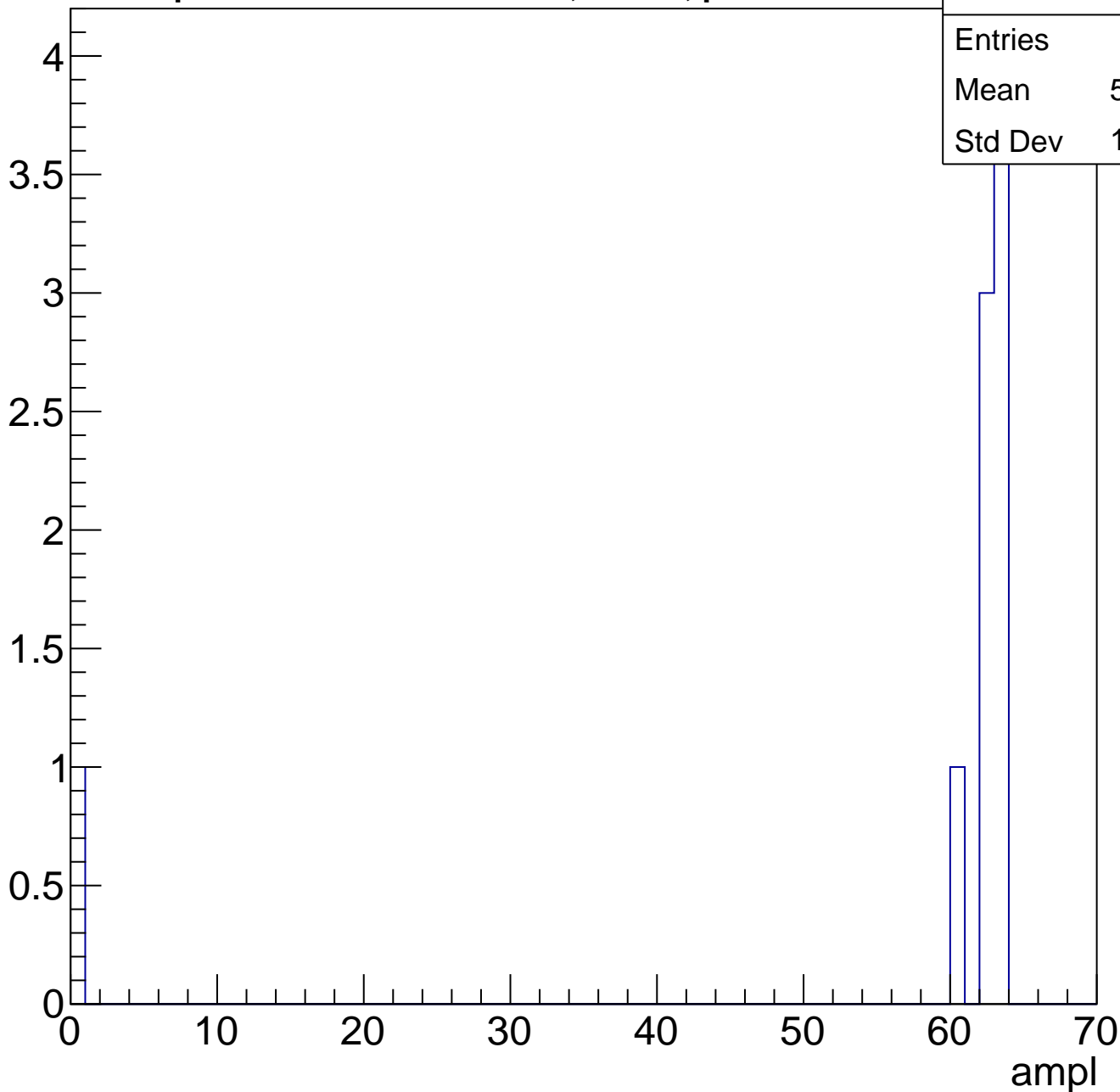
Entries	49
Mean	59.65
Std Dev	2.446



B1L003S, U19-ch6, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch6, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch7, adc0

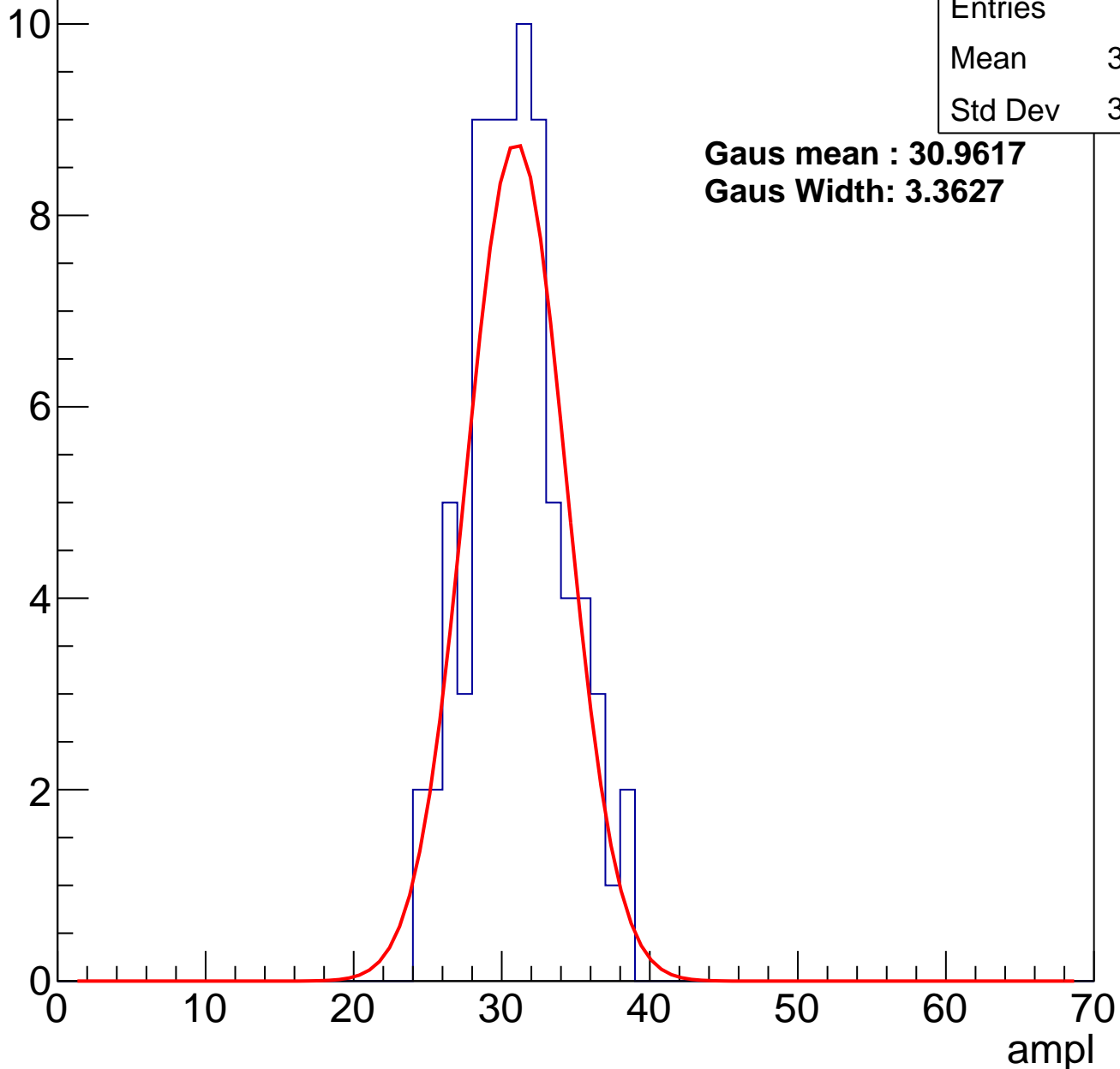
calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	30.55
Std Dev	3.205

Gaus mean : 30.9617

Gaus Width: 3.3627

Entry



B1L003S, U19-ch7, adc1

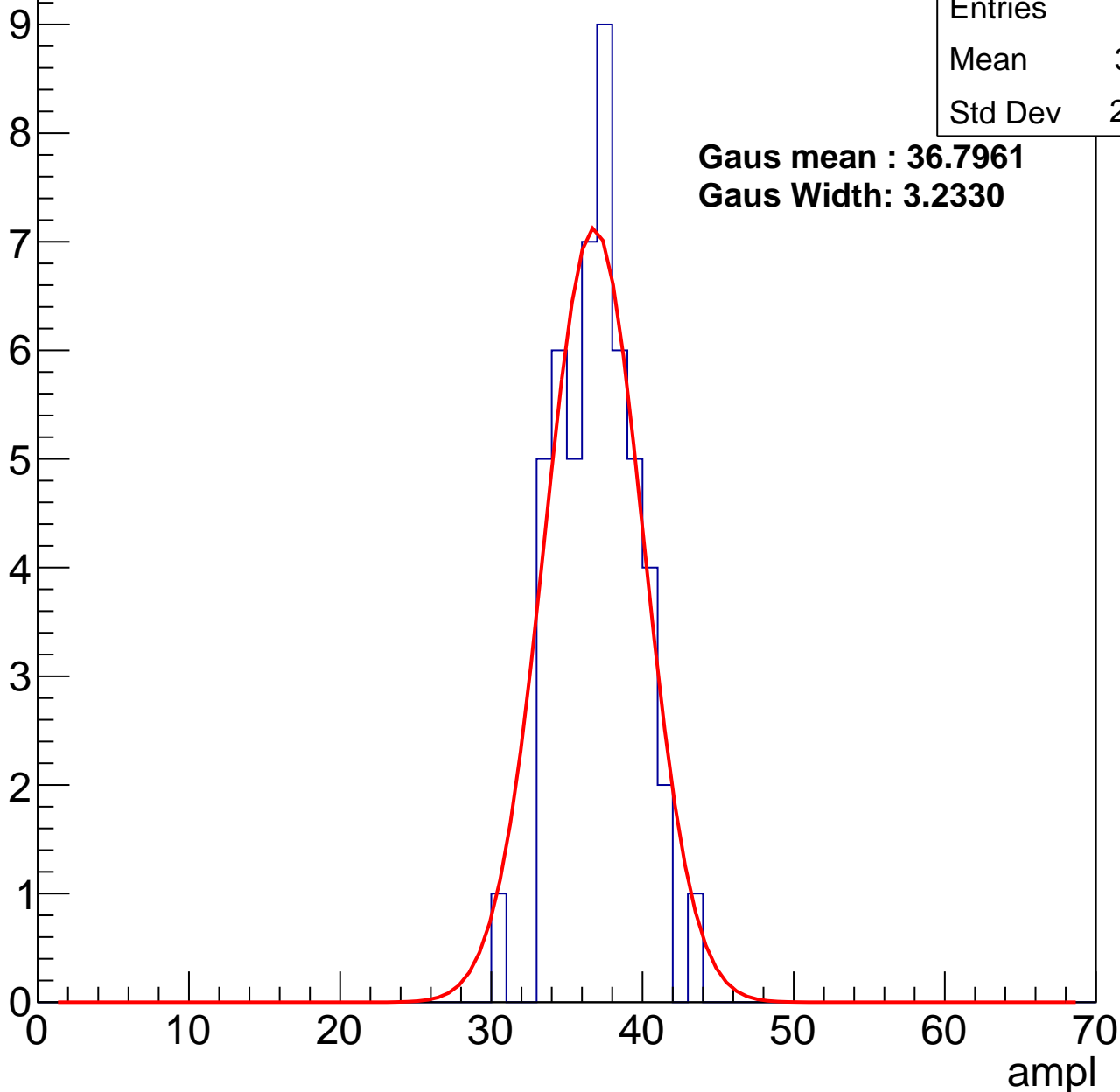
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	36.61
Std Dev	2.552

Gaus mean : 36.7961

Gaus Width: 3.2330



B1L003S, U19-ch7, adc2

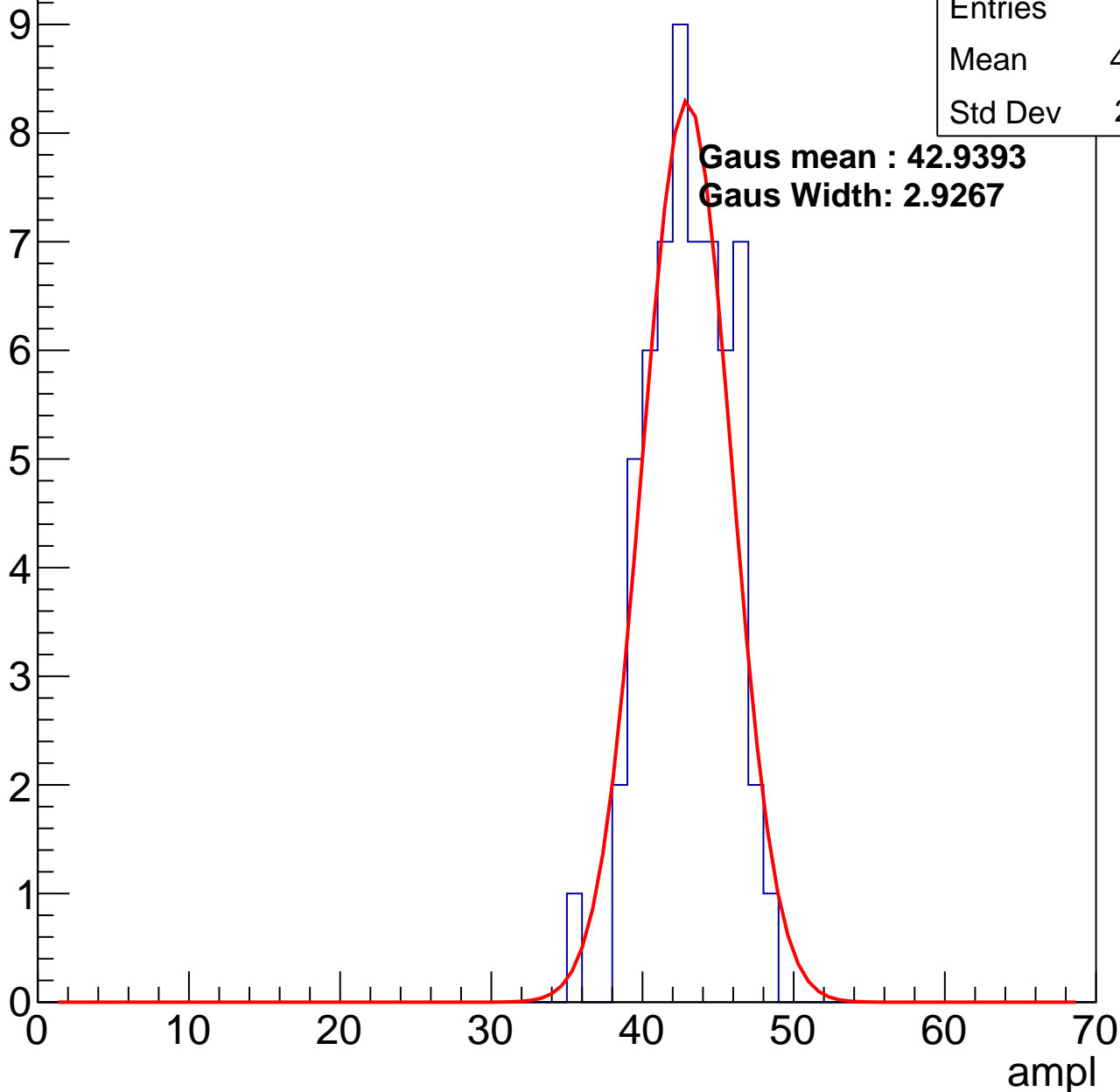
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.57
Std Dev	2.661

Gaus mean : 42.9393

Gaus Width: 2.9267

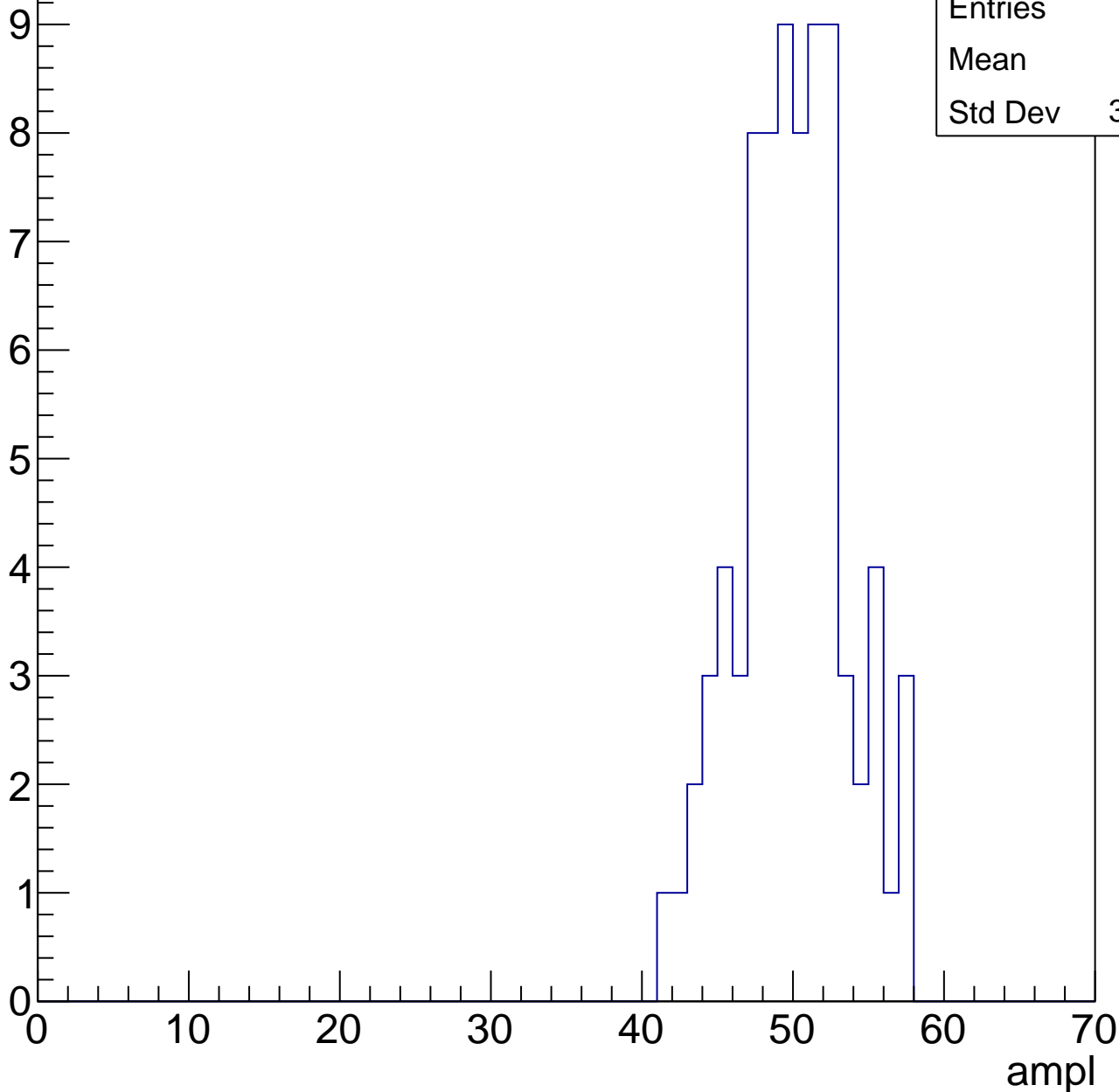


B1L003S, U19-ch7, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	49.5
Std Dev	3.544

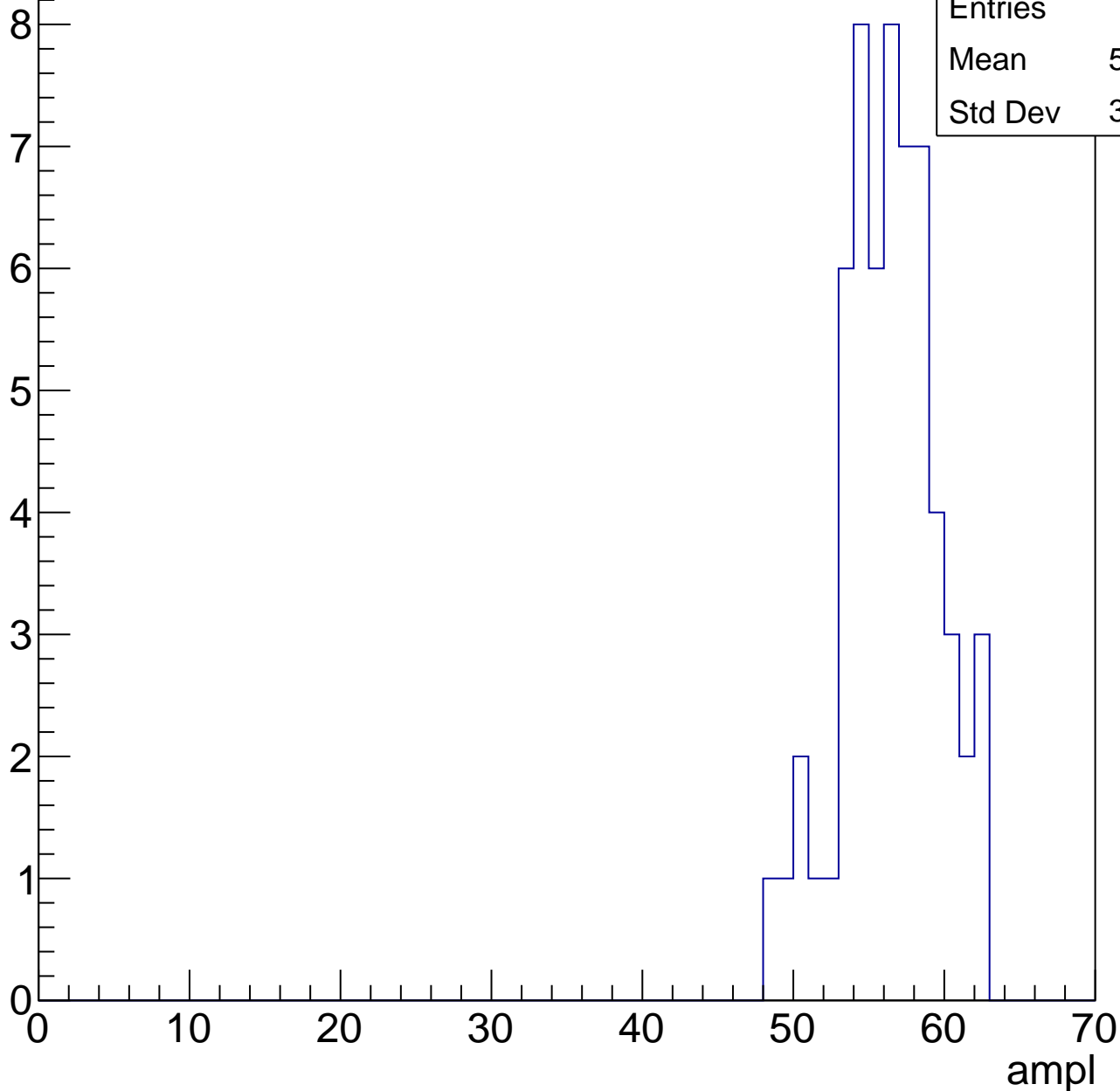


B1L003S, U19-ch7, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	55.95
Std Dev	3.149

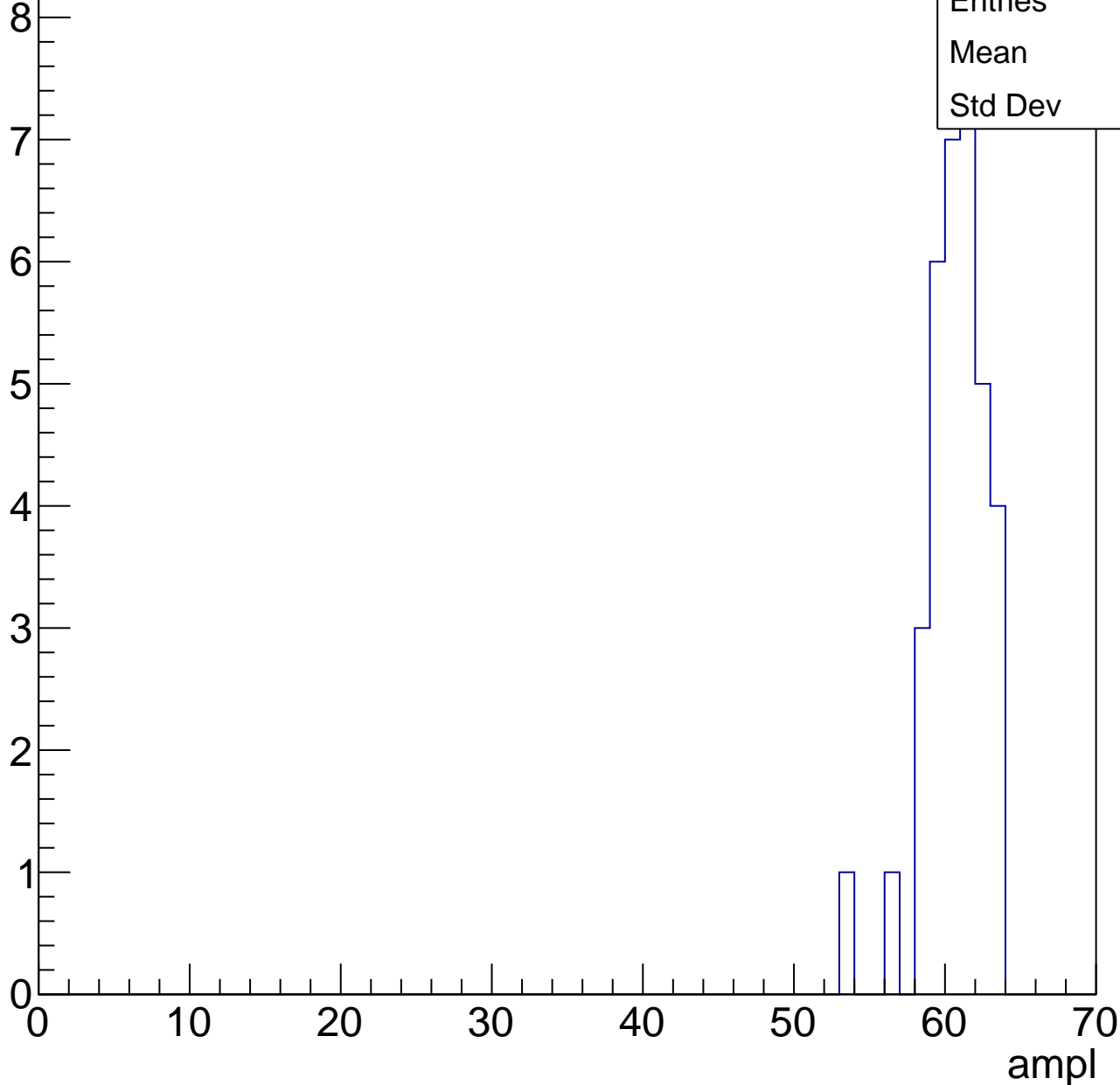


B1L003S, U19-ch7, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	60.2
Std Dev	2.04

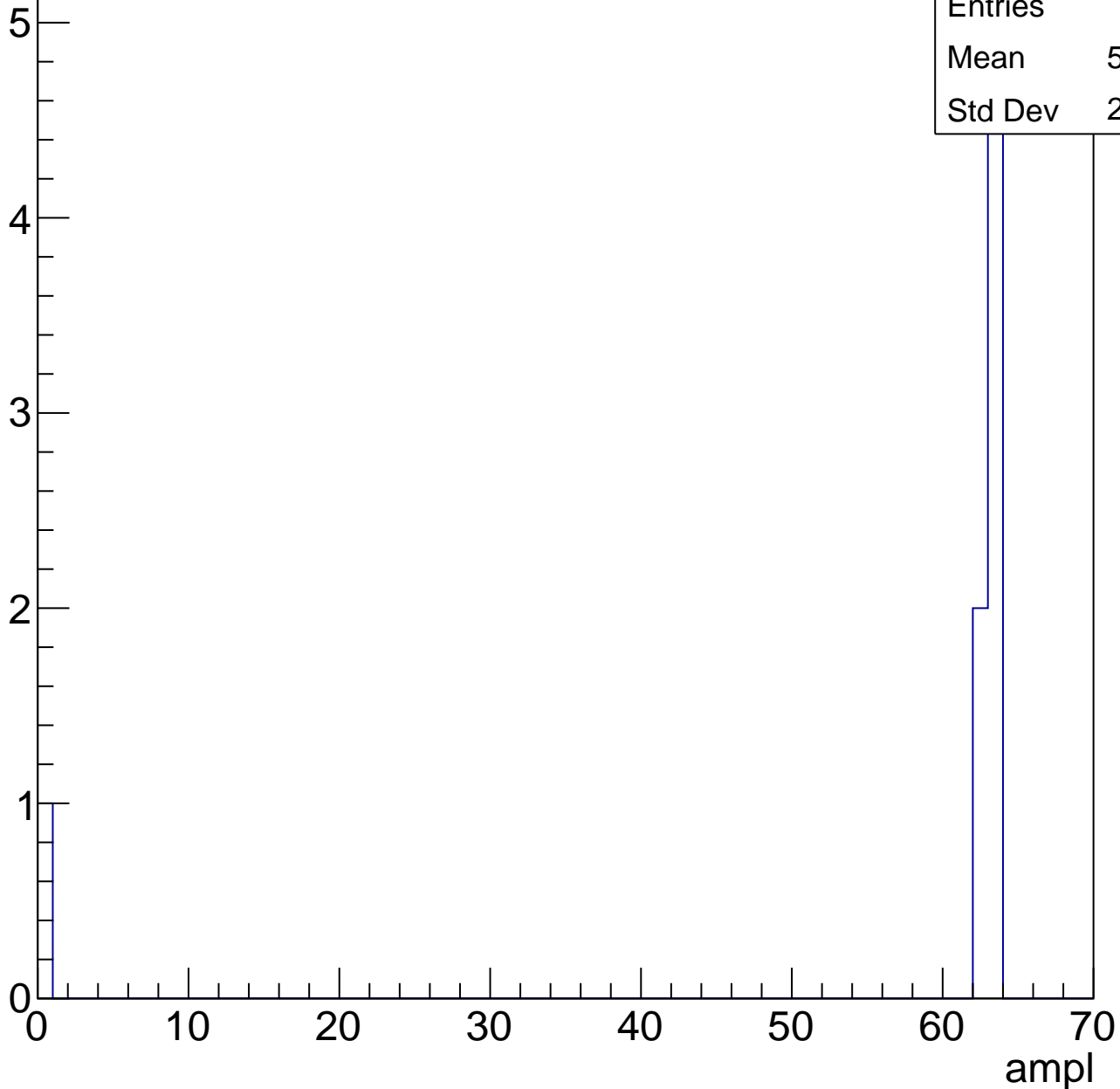


B1L003S, U19-ch7, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

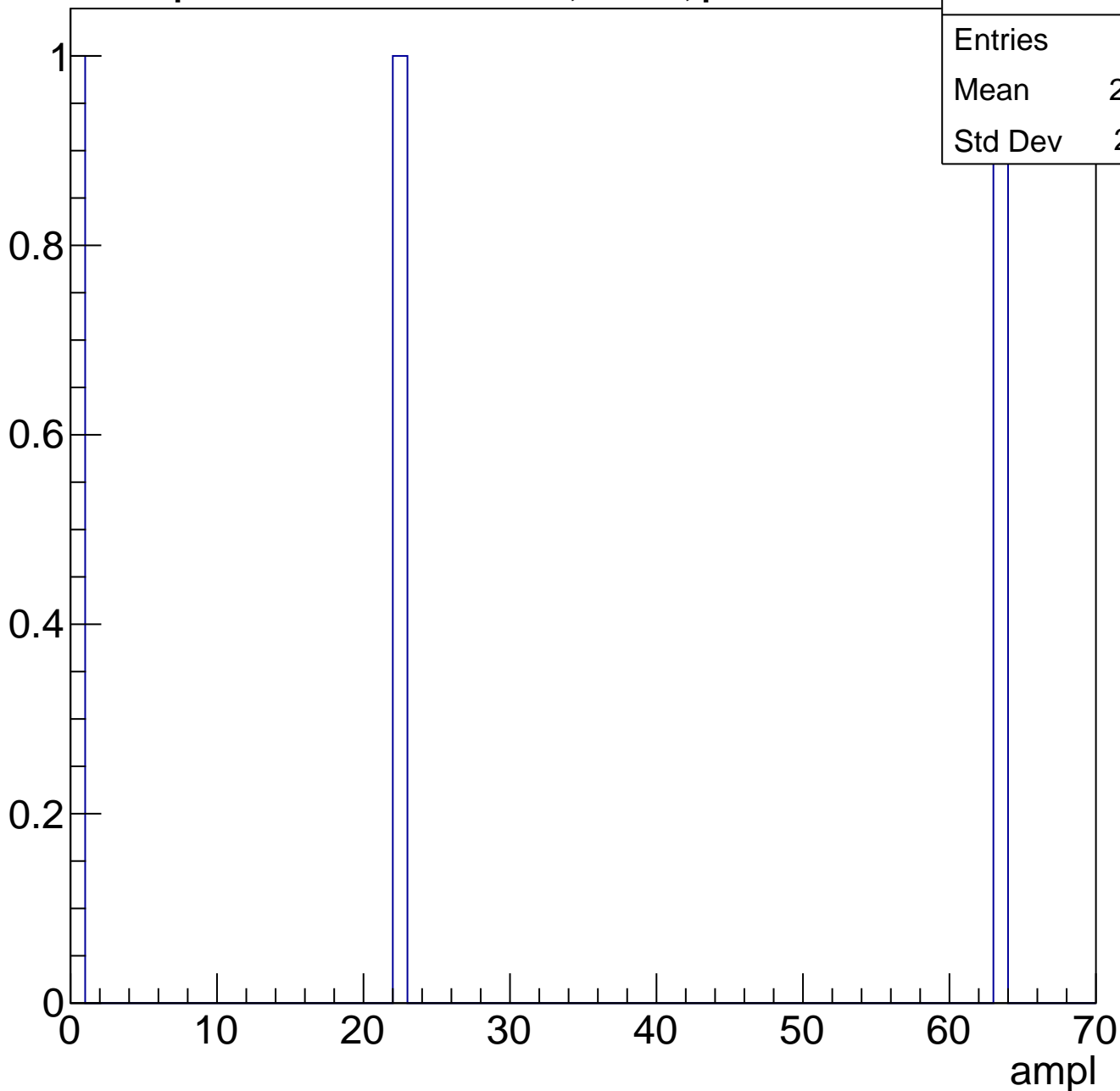
Entries	8
Mean	54.88
Std Dev	20.75



B1L003S, U19-ch7, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B1L003S, U19-ch8, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	29.79
Std Dev	4.8

Gaus mean : 30.7314

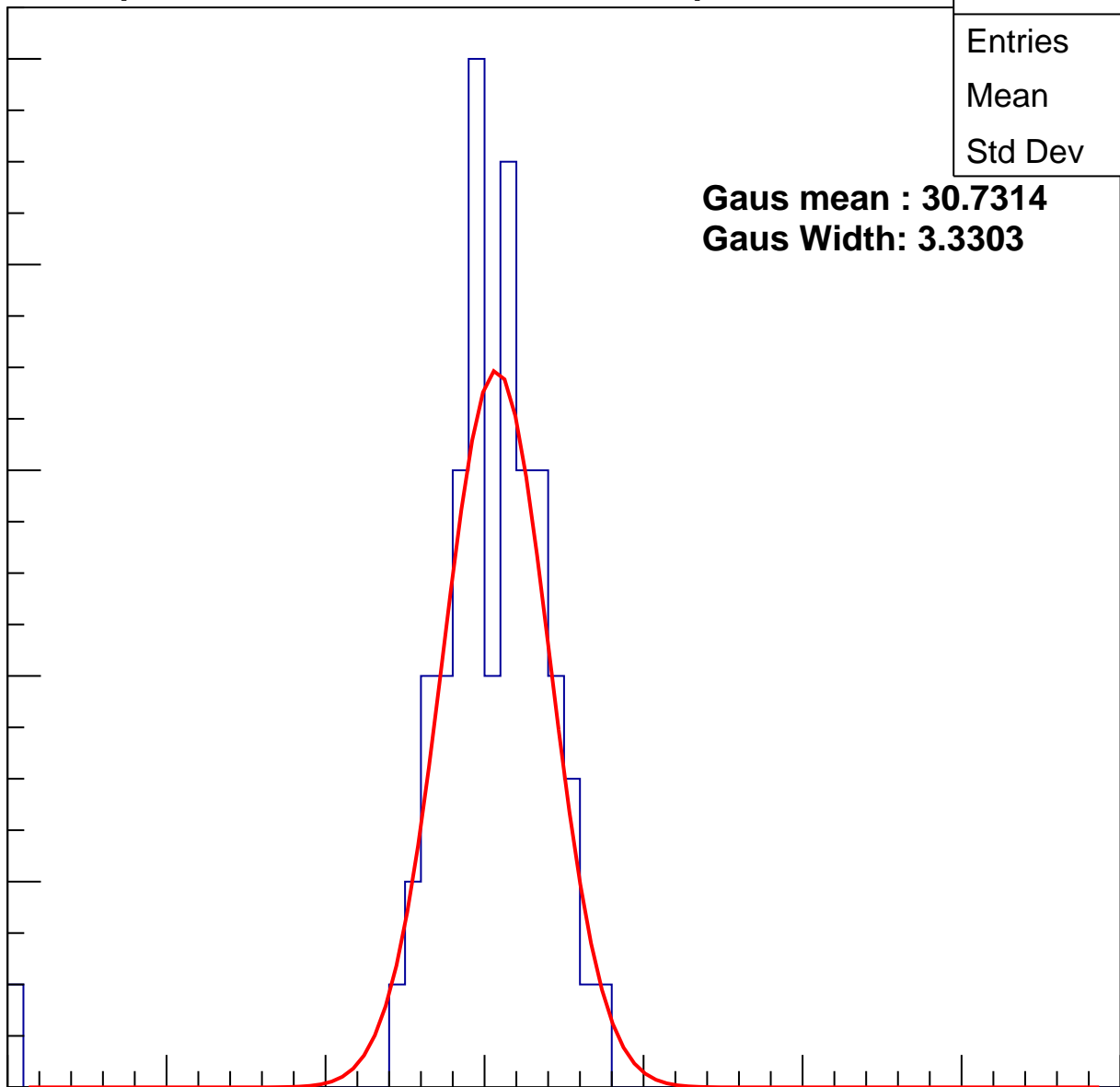
Gaus Width: 3.3303

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch8, adc1

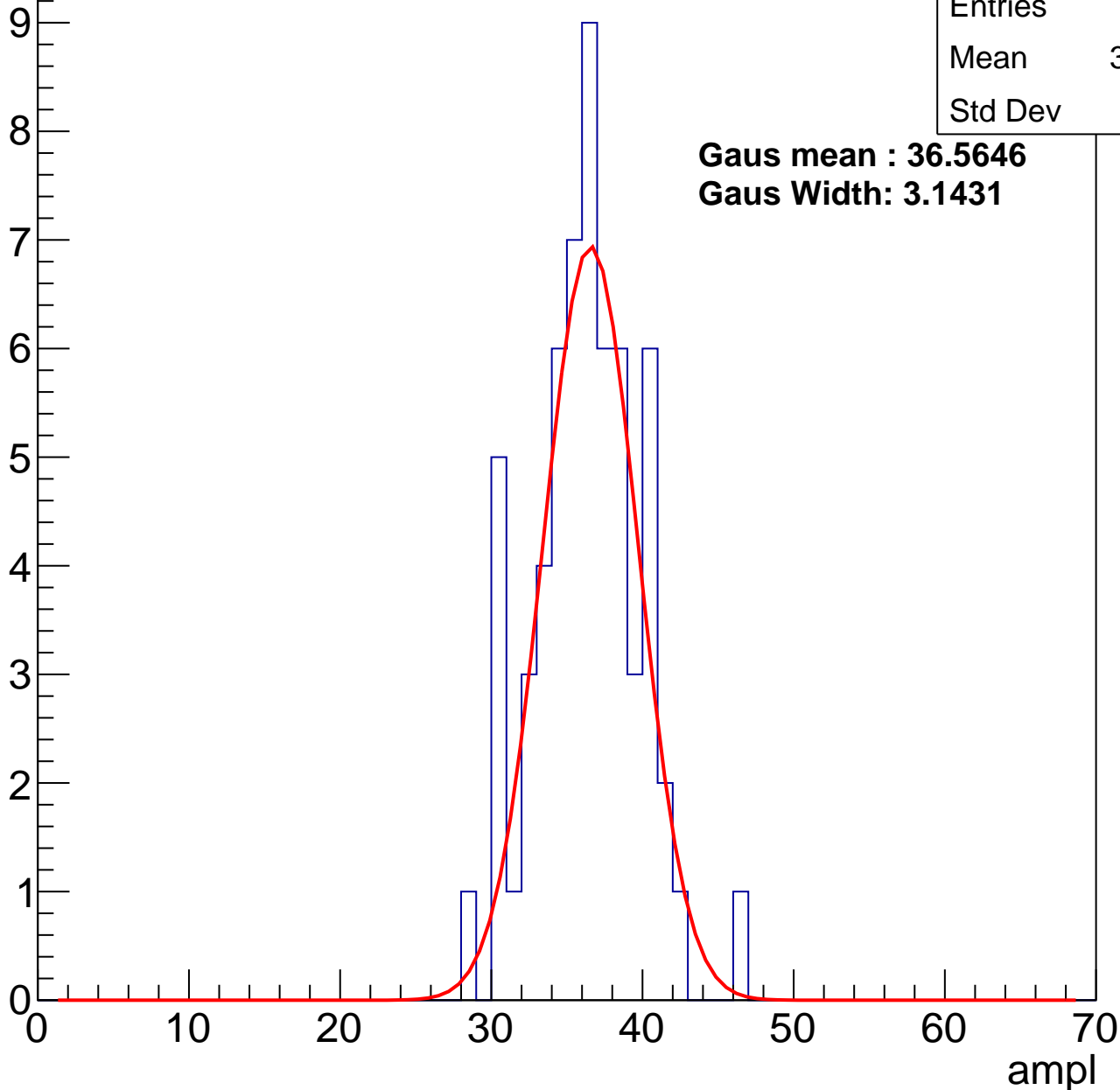
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.85
Std Dev	3.44

Gaus mean : 36.5646

Gaus Width: 3.1431



B1L003S, U19-ch8, adc2

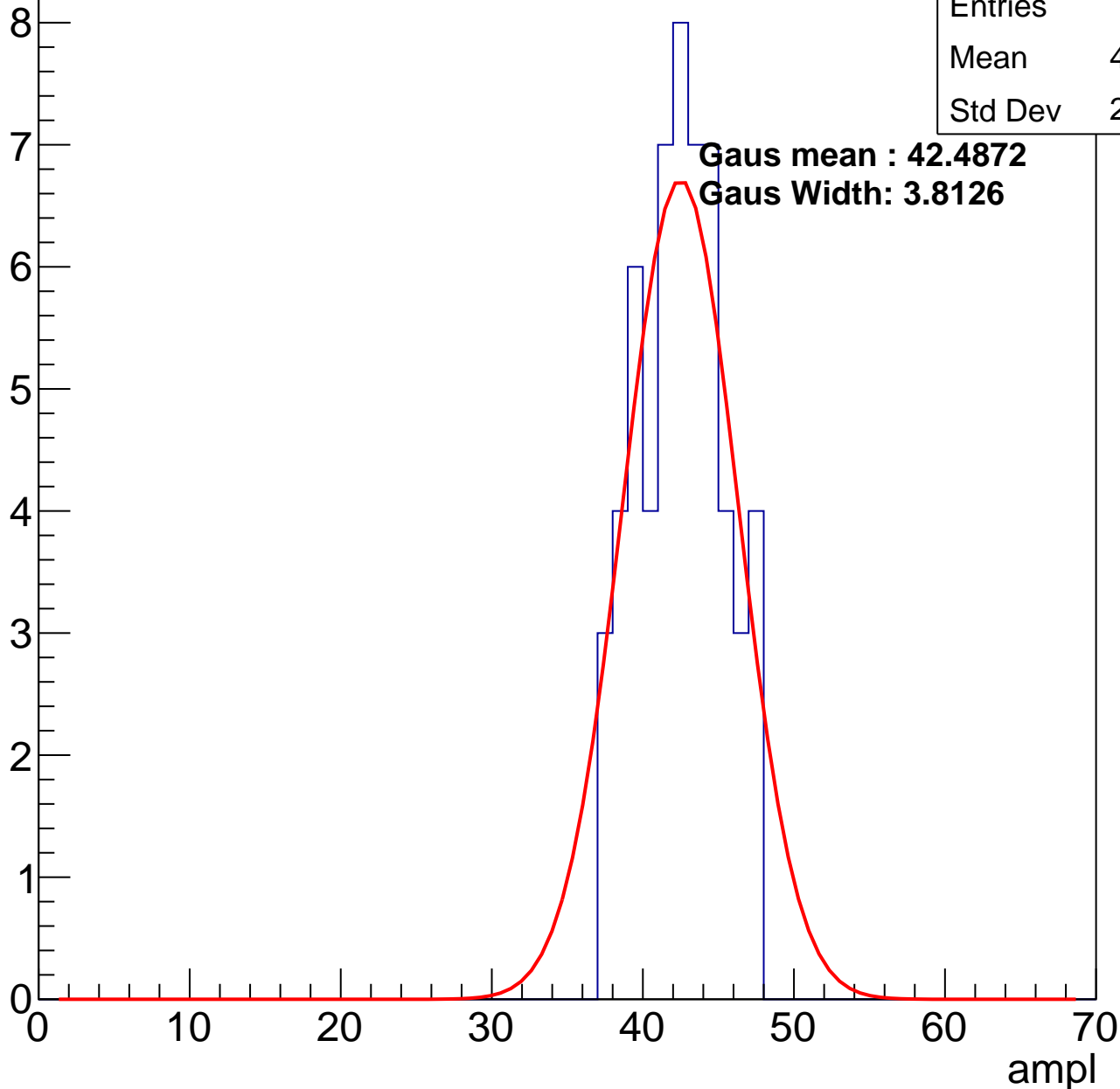
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.02
Std Dev	2.762

Gaus mean : 42.4872

Gaus Width: 3.8126

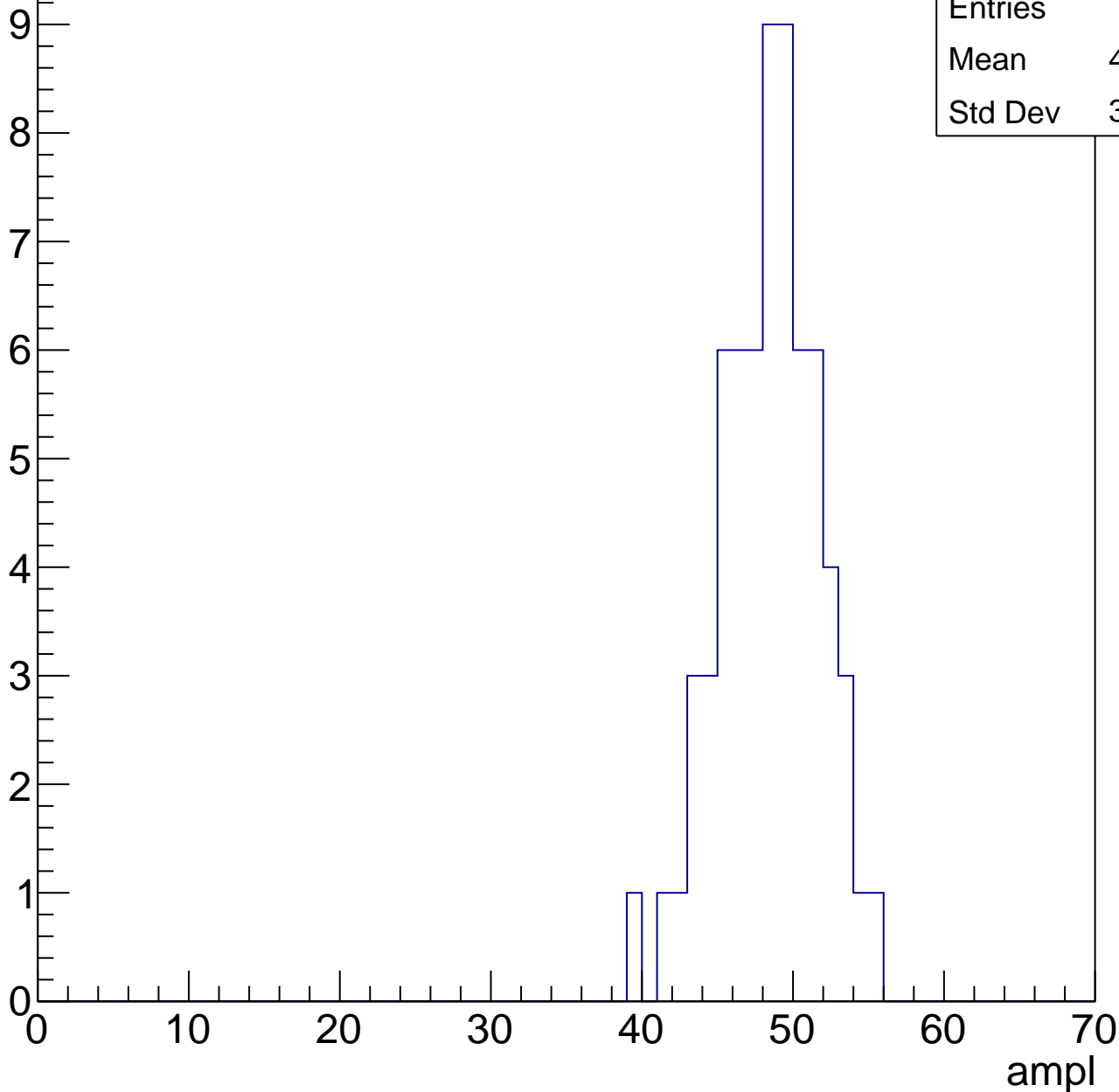


B1L003S, U19-ch8, adc3

calib_packv5_042523_0143.root, FC#13, port D2

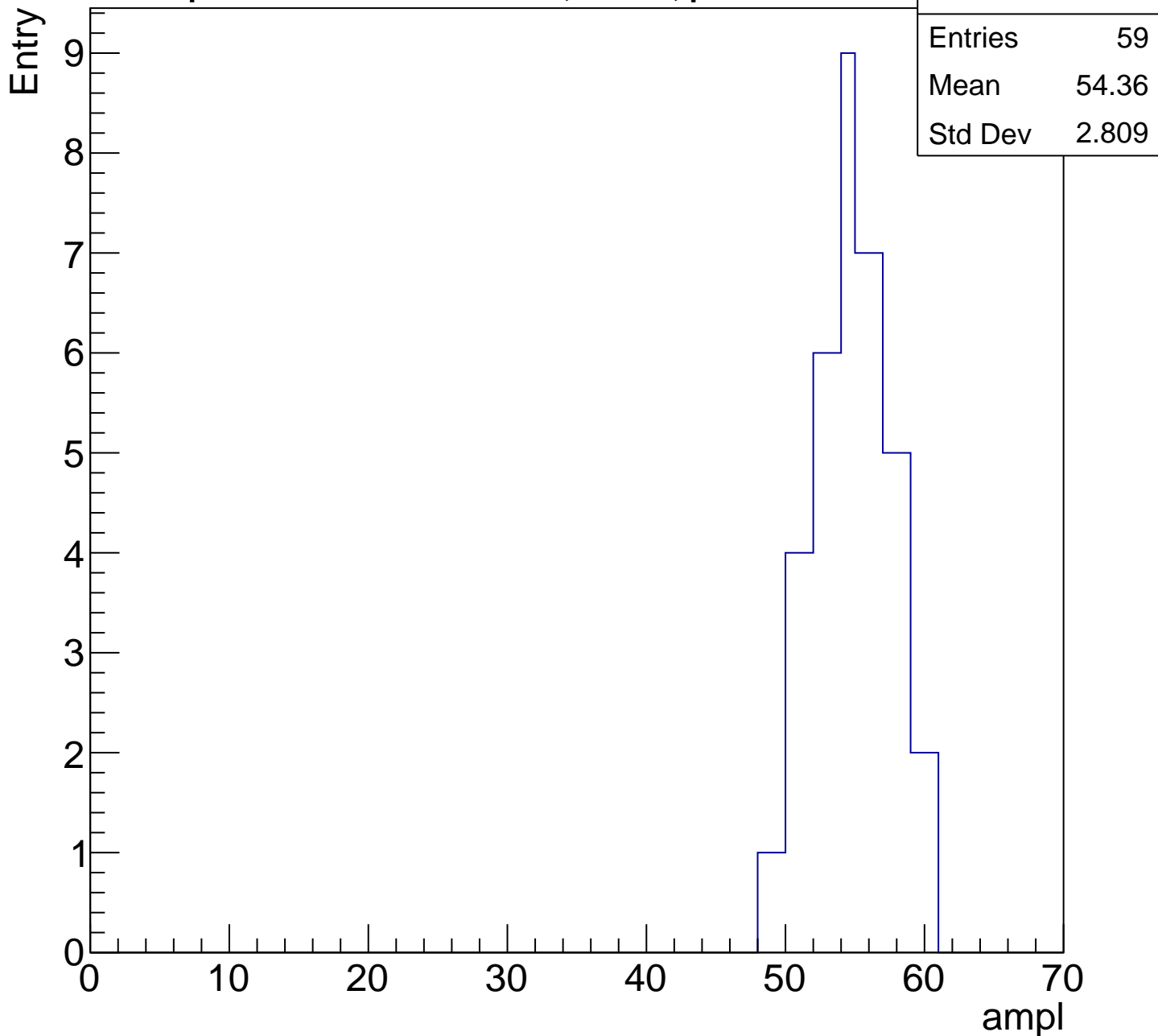
Entry

Entries	66
Mean	47.97
Std Dev	3.219



B1L003S, U19-ch8, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch8, adc5

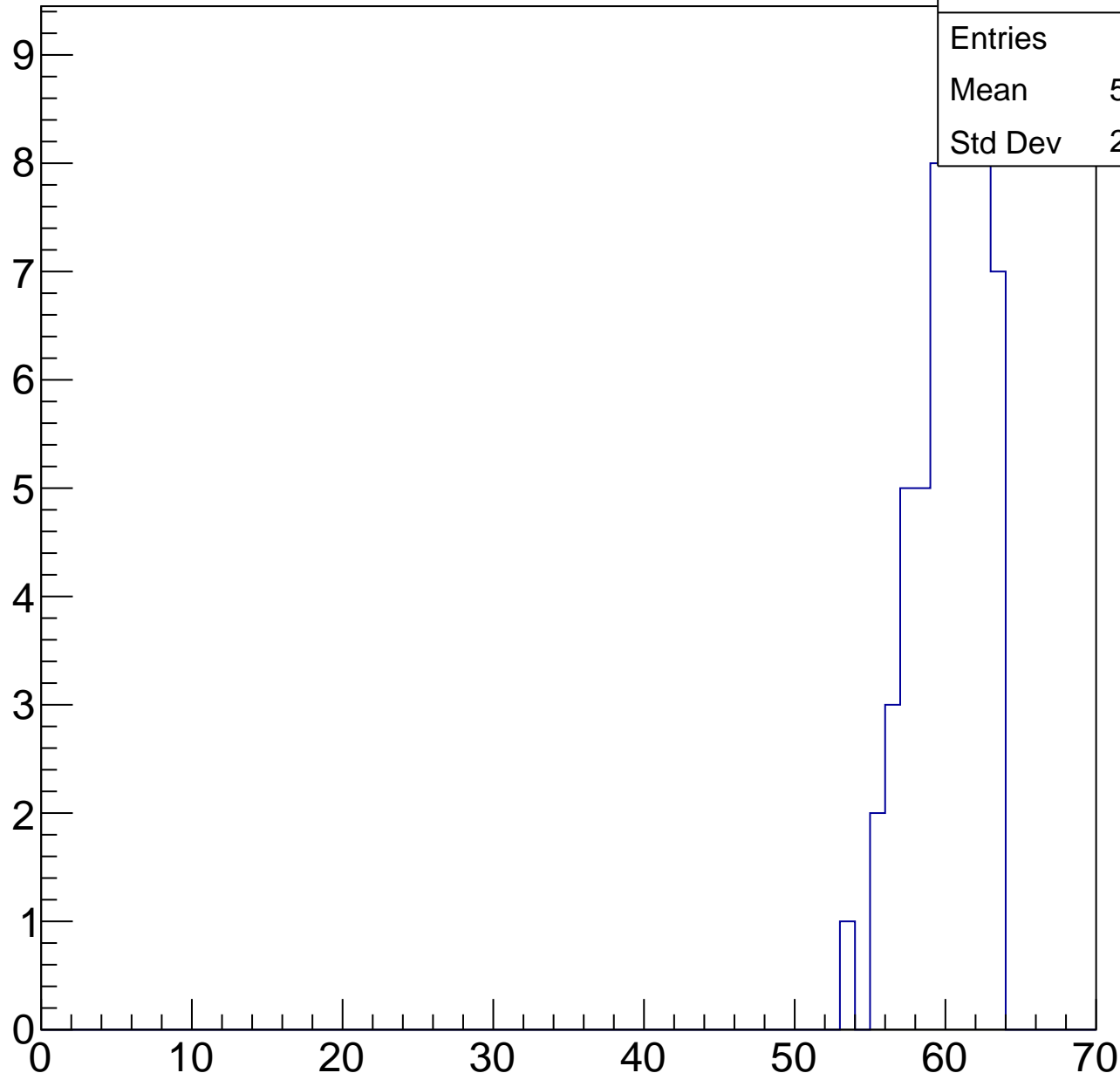
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	59.73
Std Dev	2.402

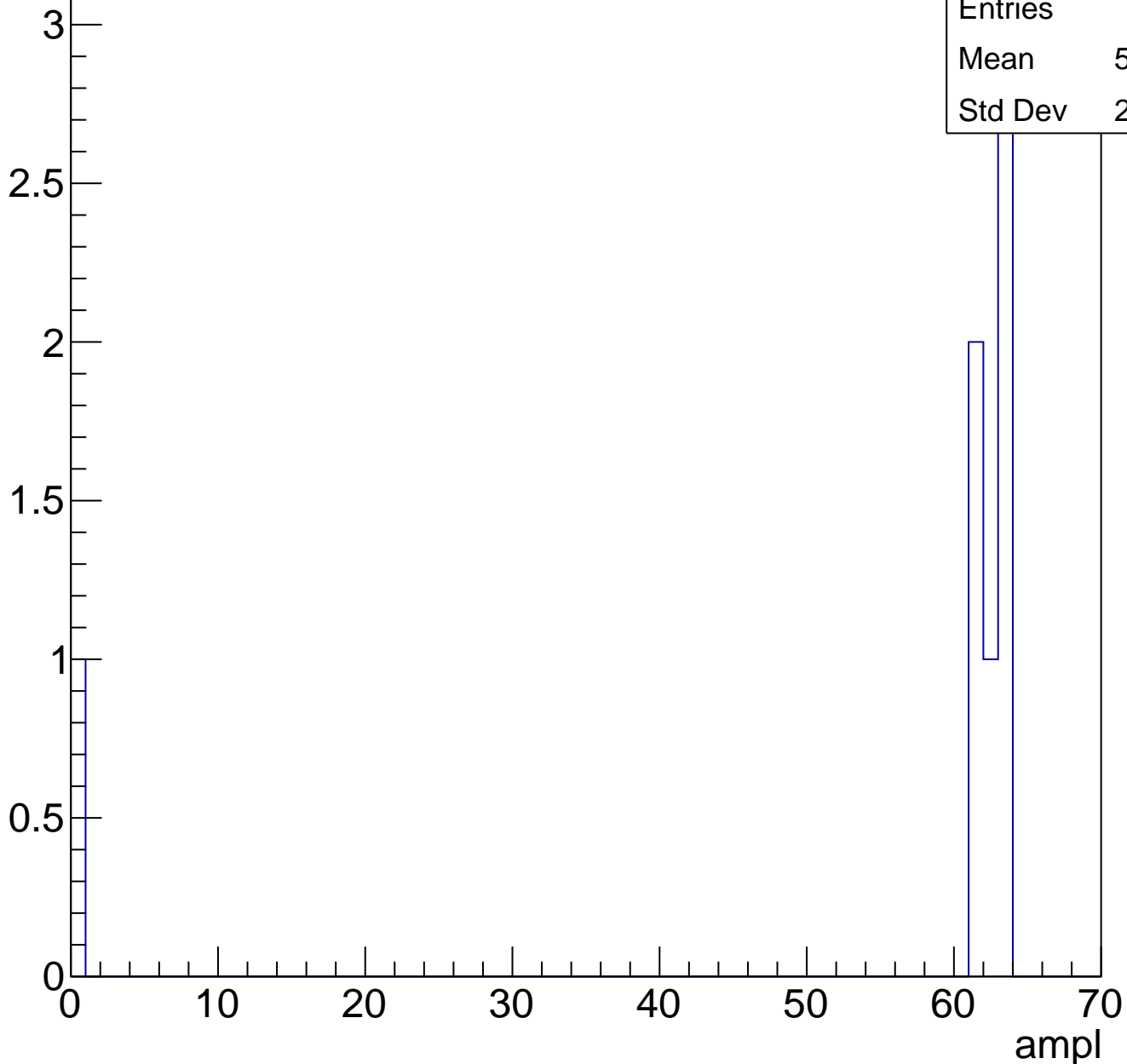
ampl



B1L003S, U19-ch8, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch8, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch9, adc0

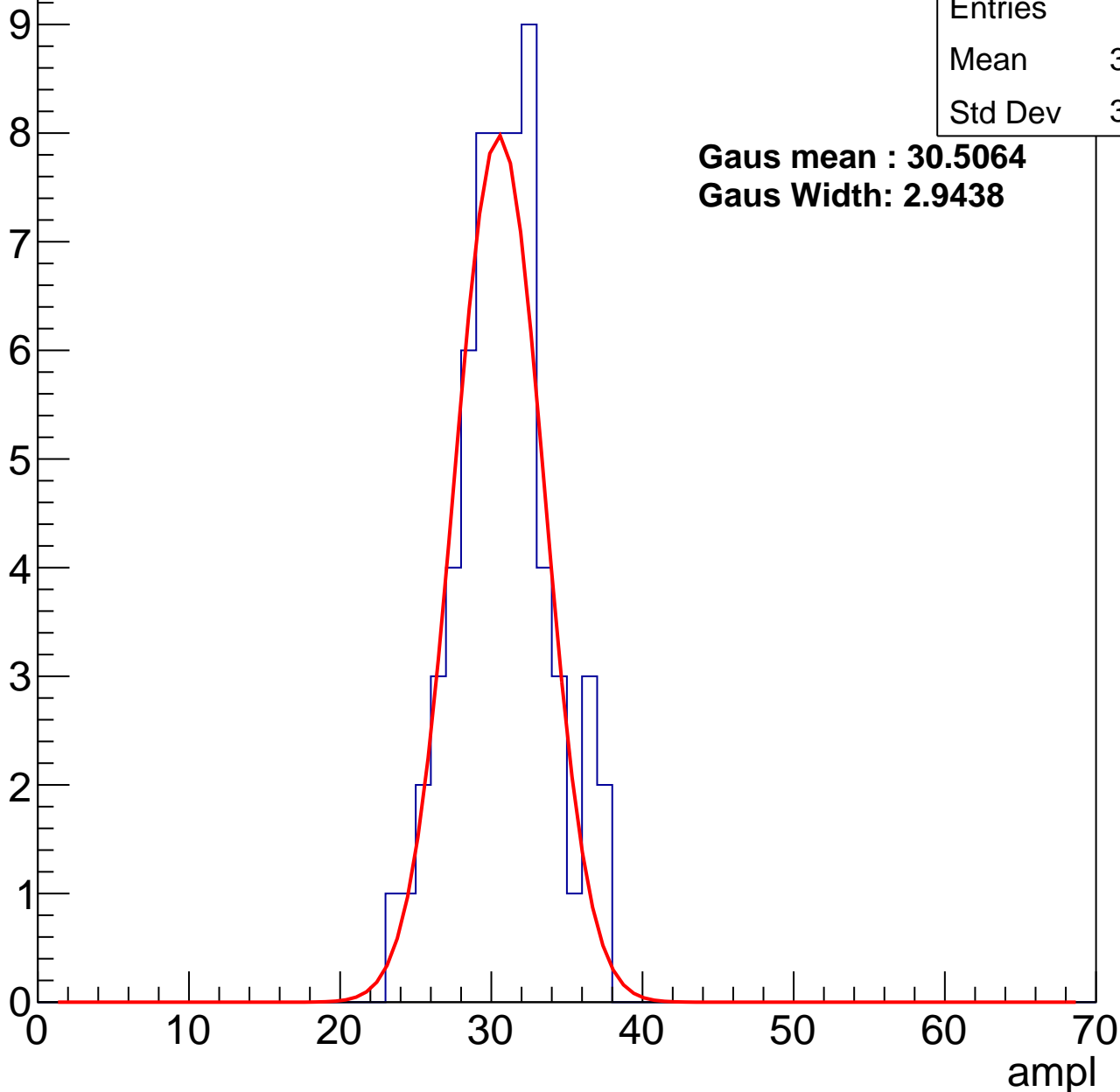
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30.32
Std Dev	3.095

Gaus mean : 30.5064

Gaus Width: 2.9438



B1L003S, U19-ch9, adc1

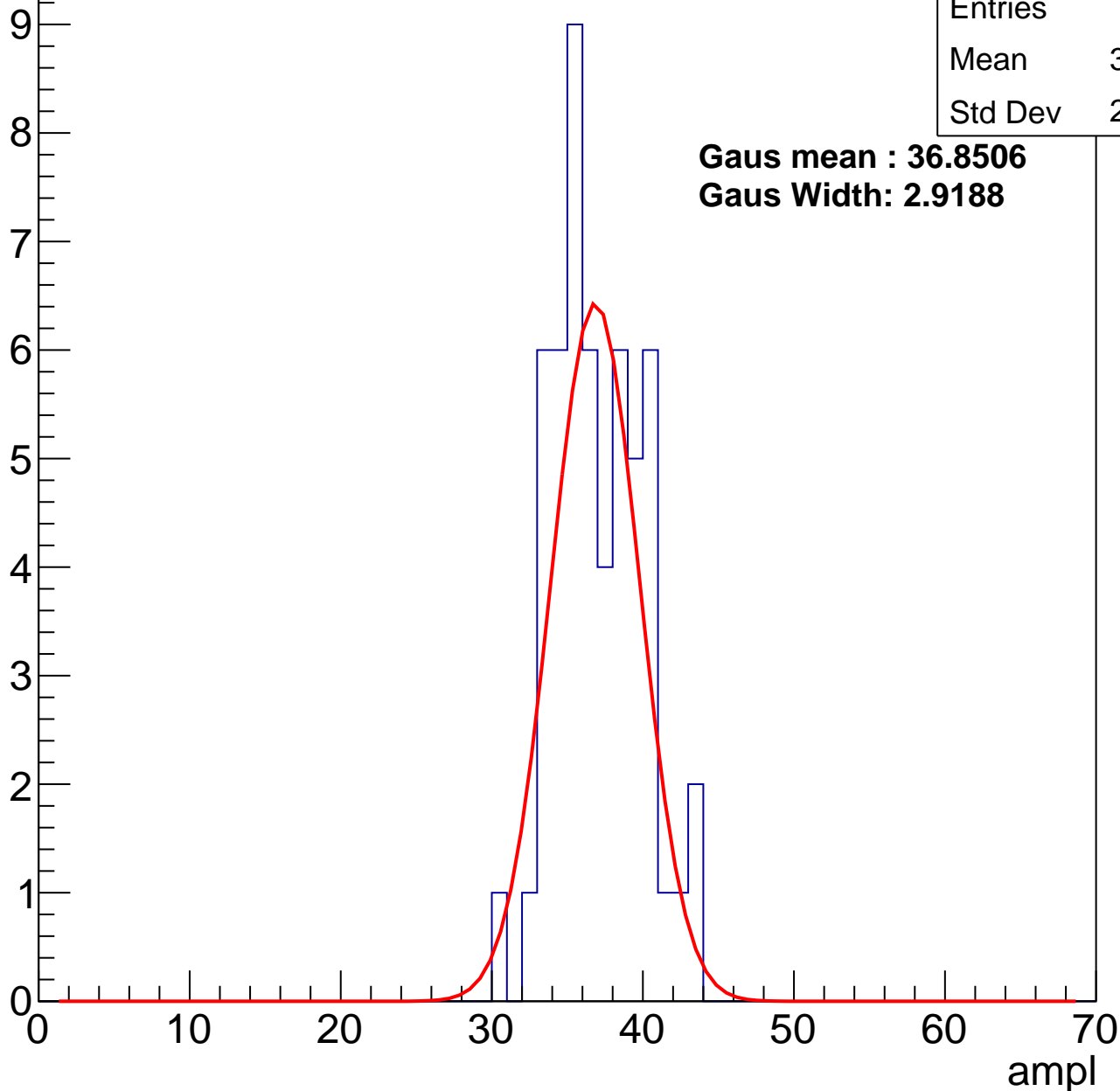
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.57
Std Dev	2.884

Gaus mean : 36.8506

Gaus Width: 2.9188



B1L003S, U19-ch9, adc2

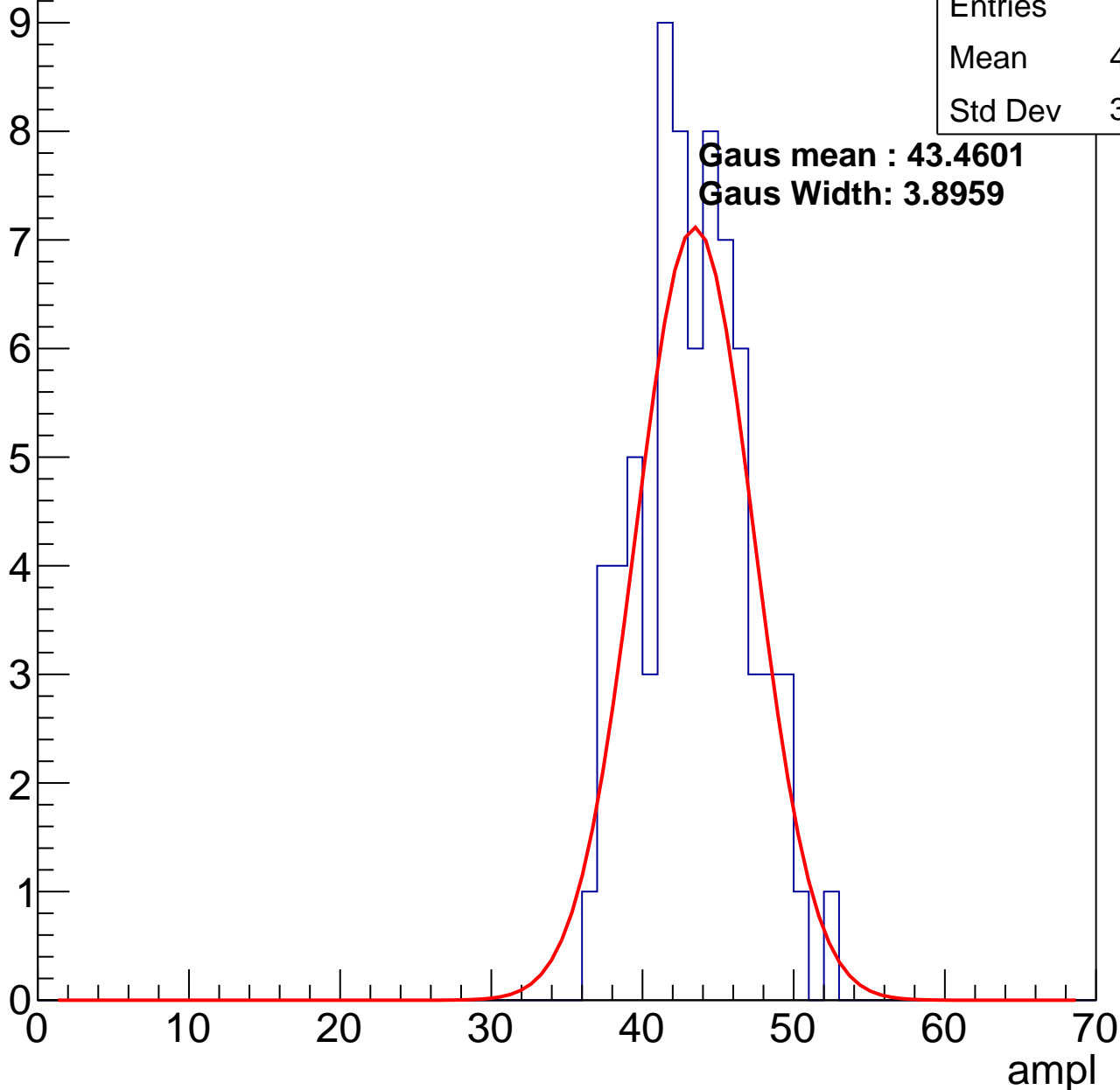
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.93
Std Dev	3.525

Gaus mean : 43.4601

Gaus Width: 3.8959

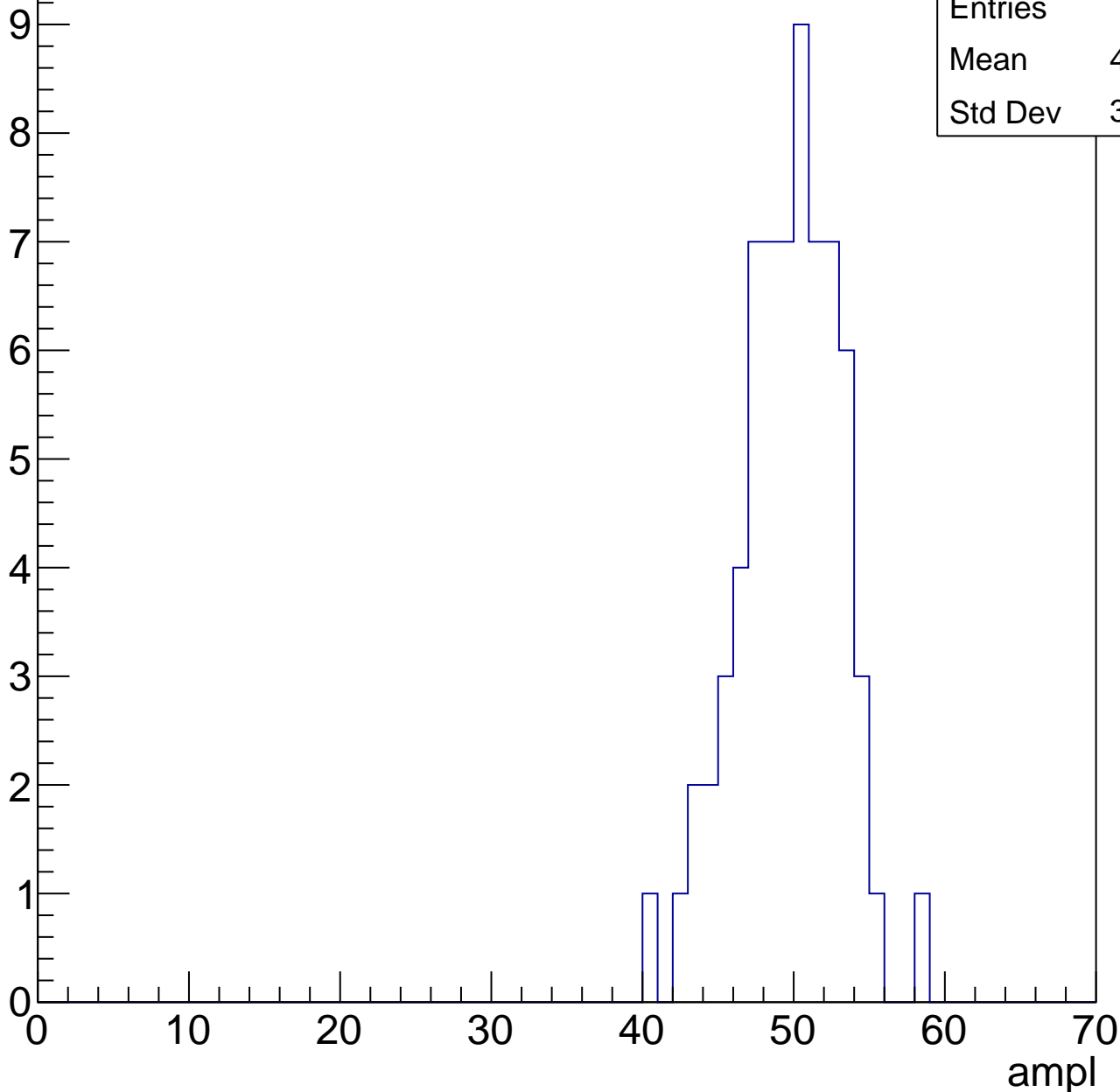


B1L003S, U19-ch9, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.22
Std Dev	3.334



B1L003S, U19-ch9, adc4

calib_packv5_042523_0143.root, FC#13, port D2

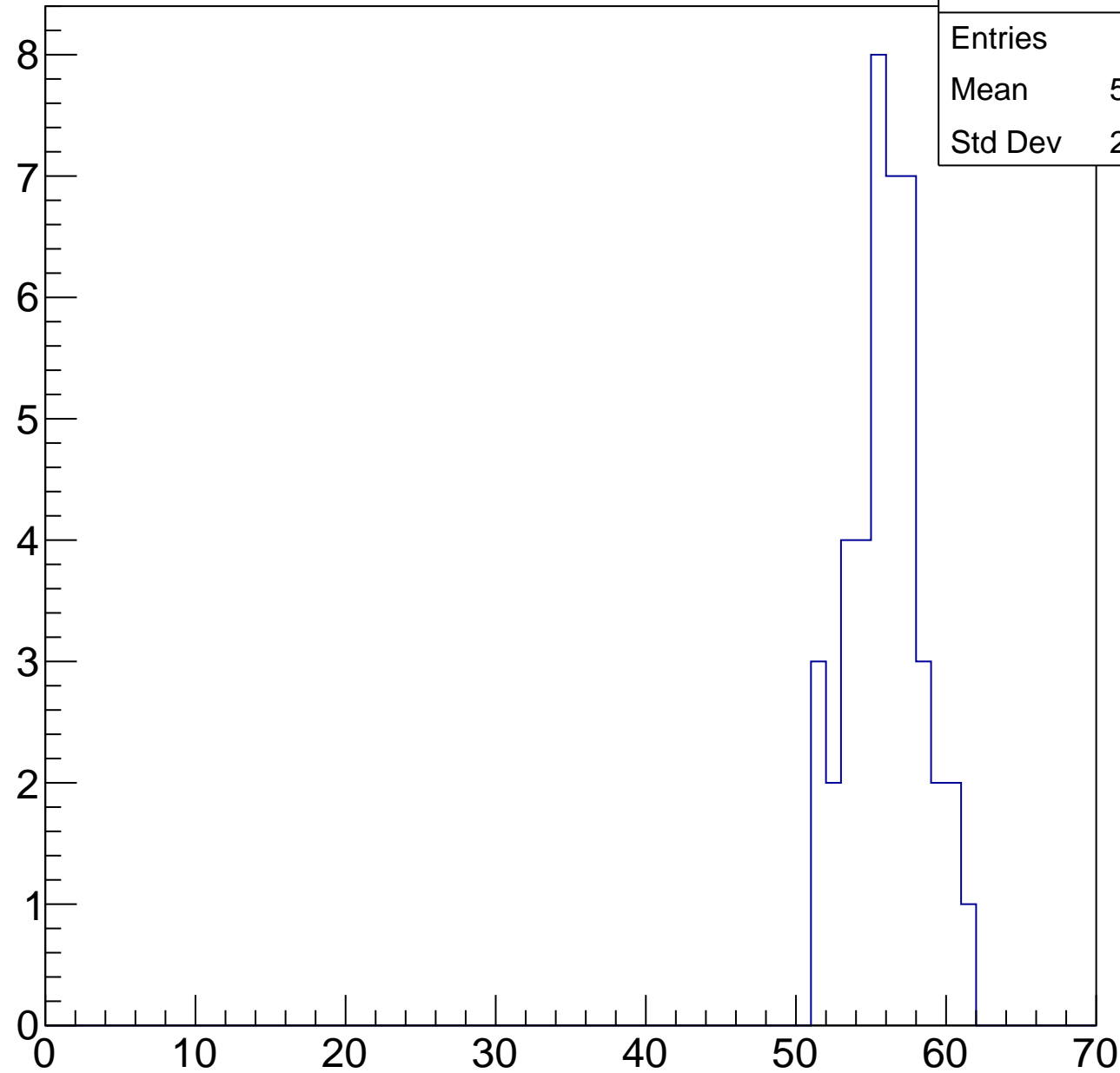
Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	55.56
Std Dev	2.424

ampl

0 10 20 30 40 50 60 70

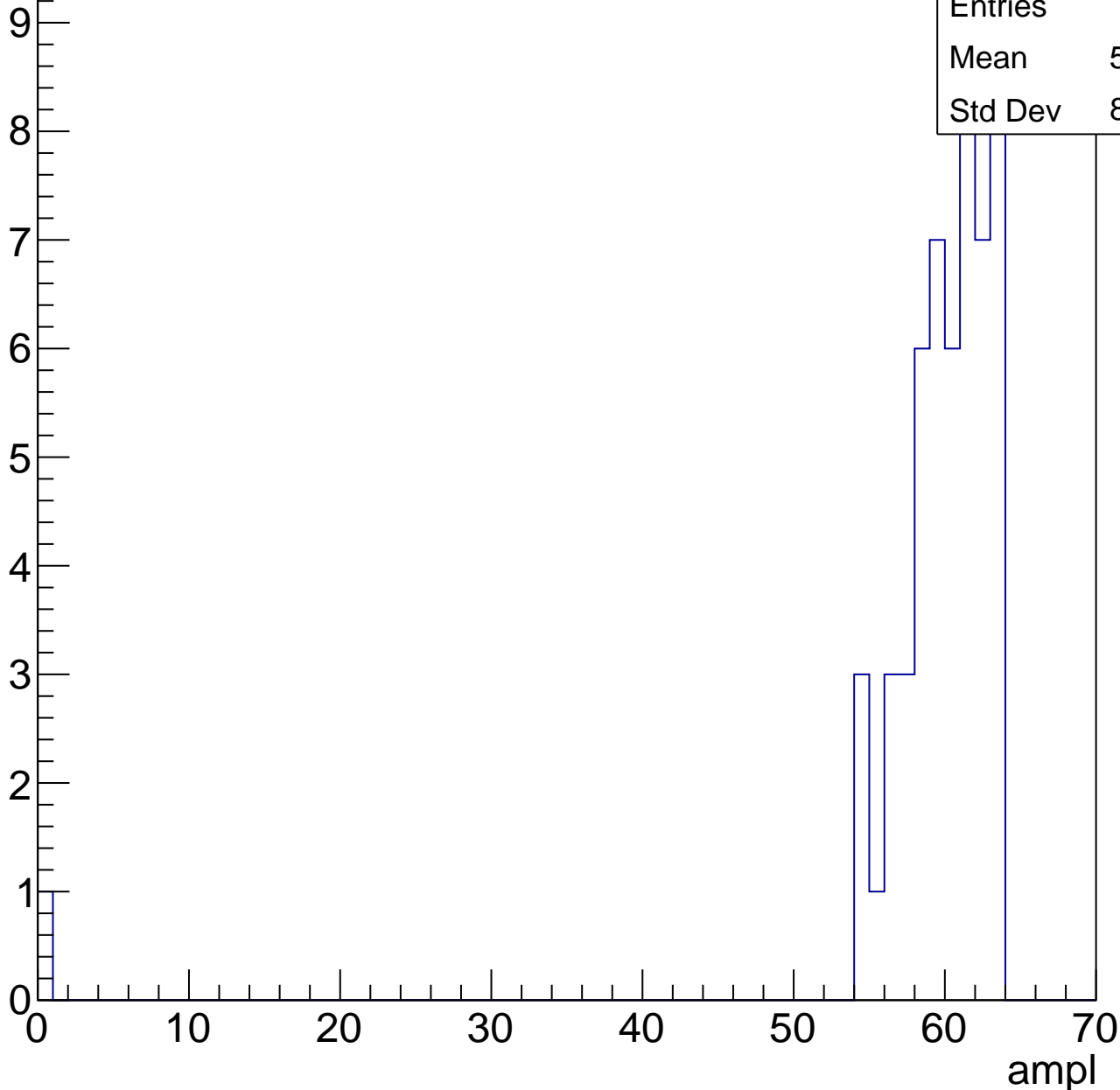


B1L003S, U19-ch9, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

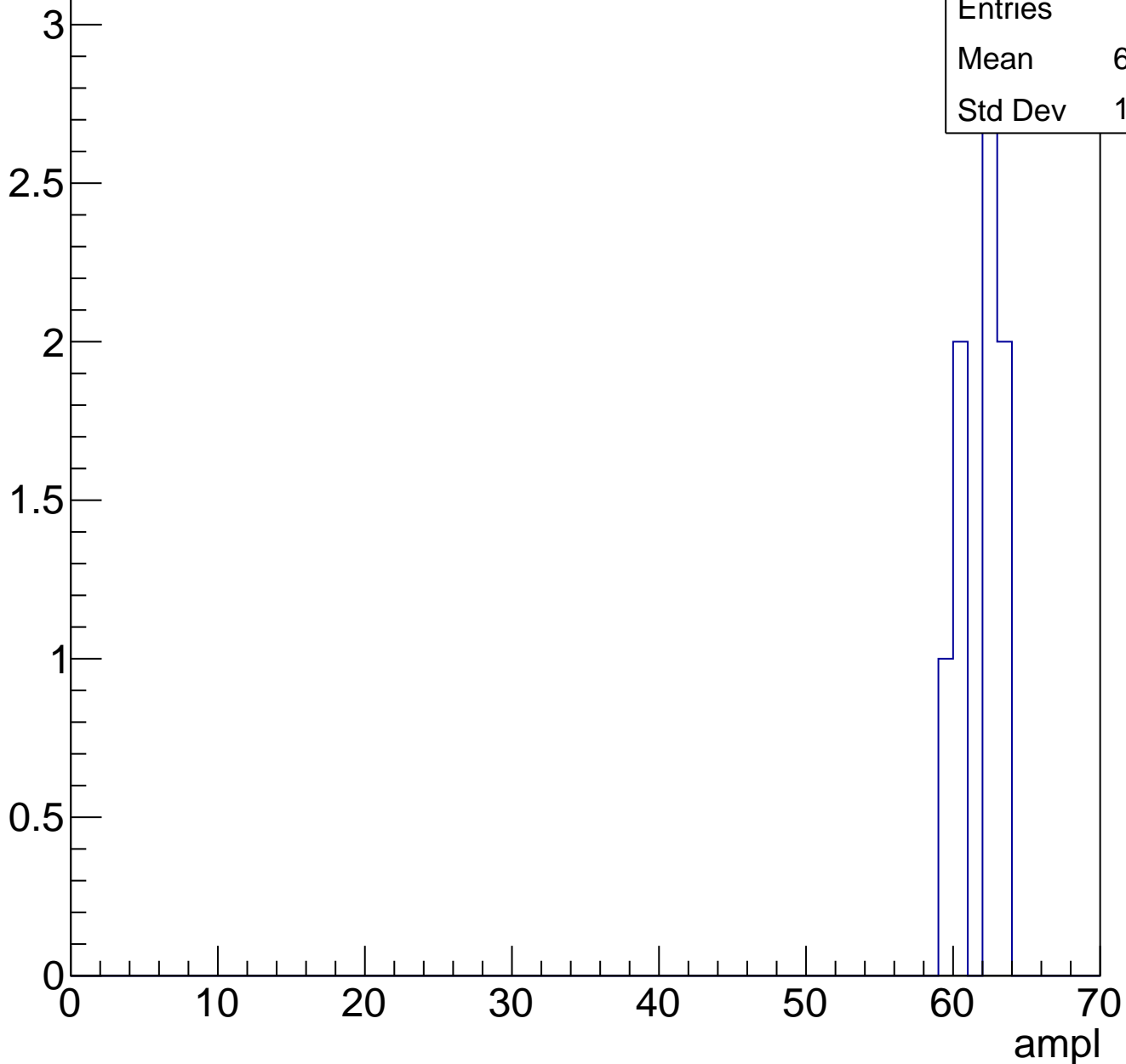
Entries	54
Mean	58.59
Std Dev	8.434



B1L003S, U19-ch9, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch9, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch10, adc0

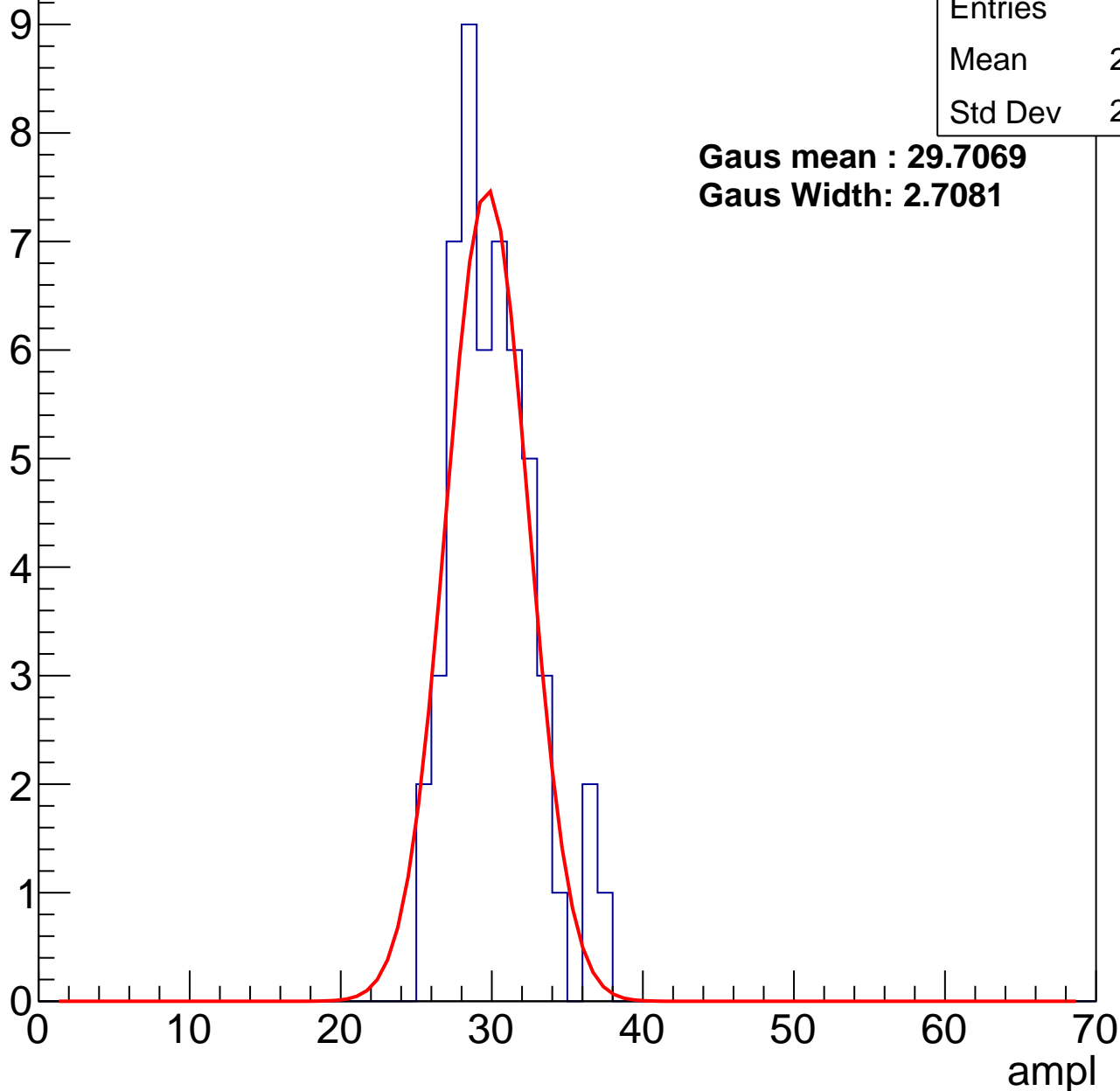
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	29.63
Std Dev	2.725

Gaus mean : 29.7069

Gaus Width: 2.7081



B1L003S, U19-ch10, adc1

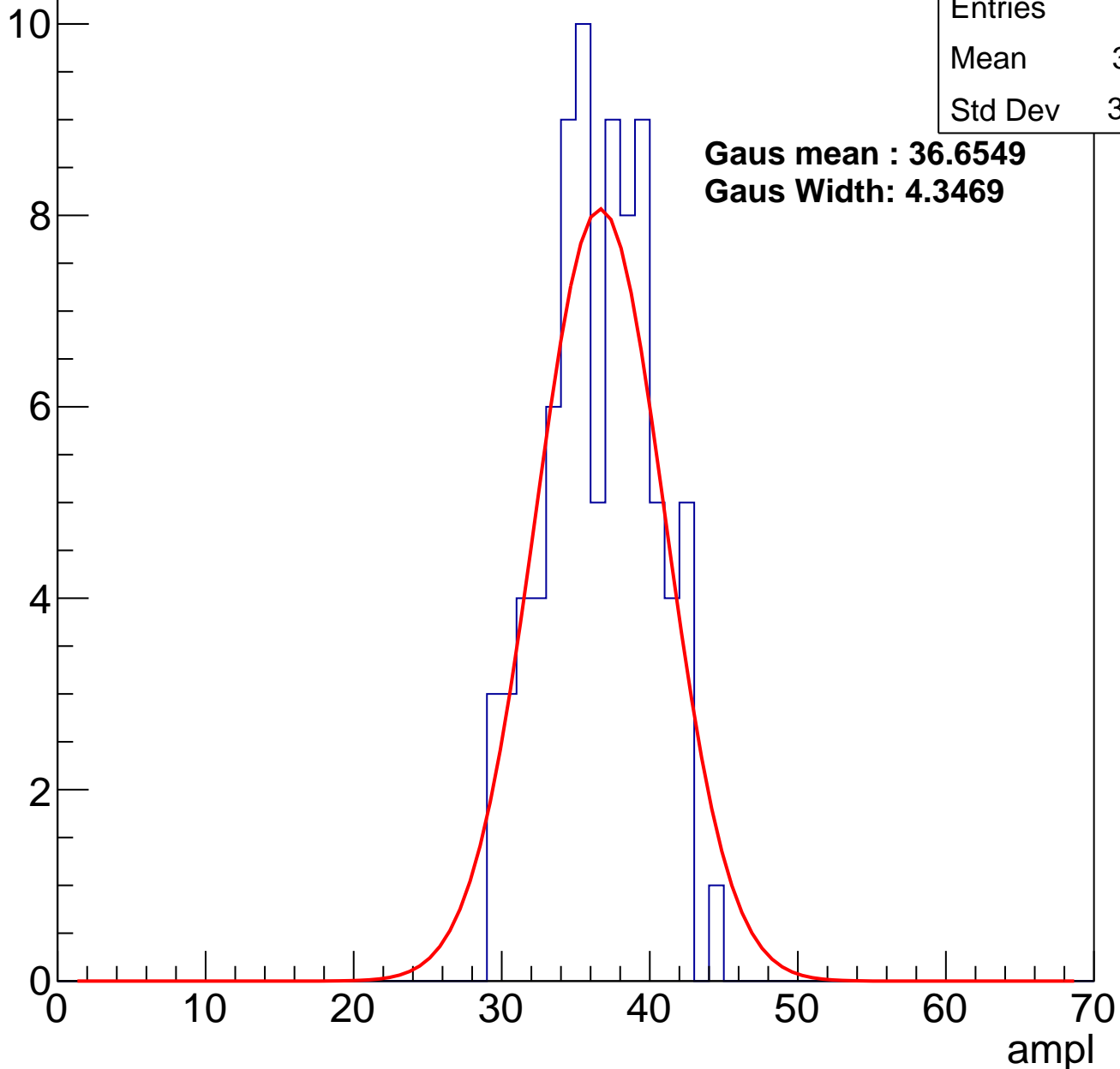
calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	36.11
Std Dev	3.538

Gaus mean : 36.6549

Gaus Width: 4.3469

Entry



B1L003S, U19-ch10, adc2

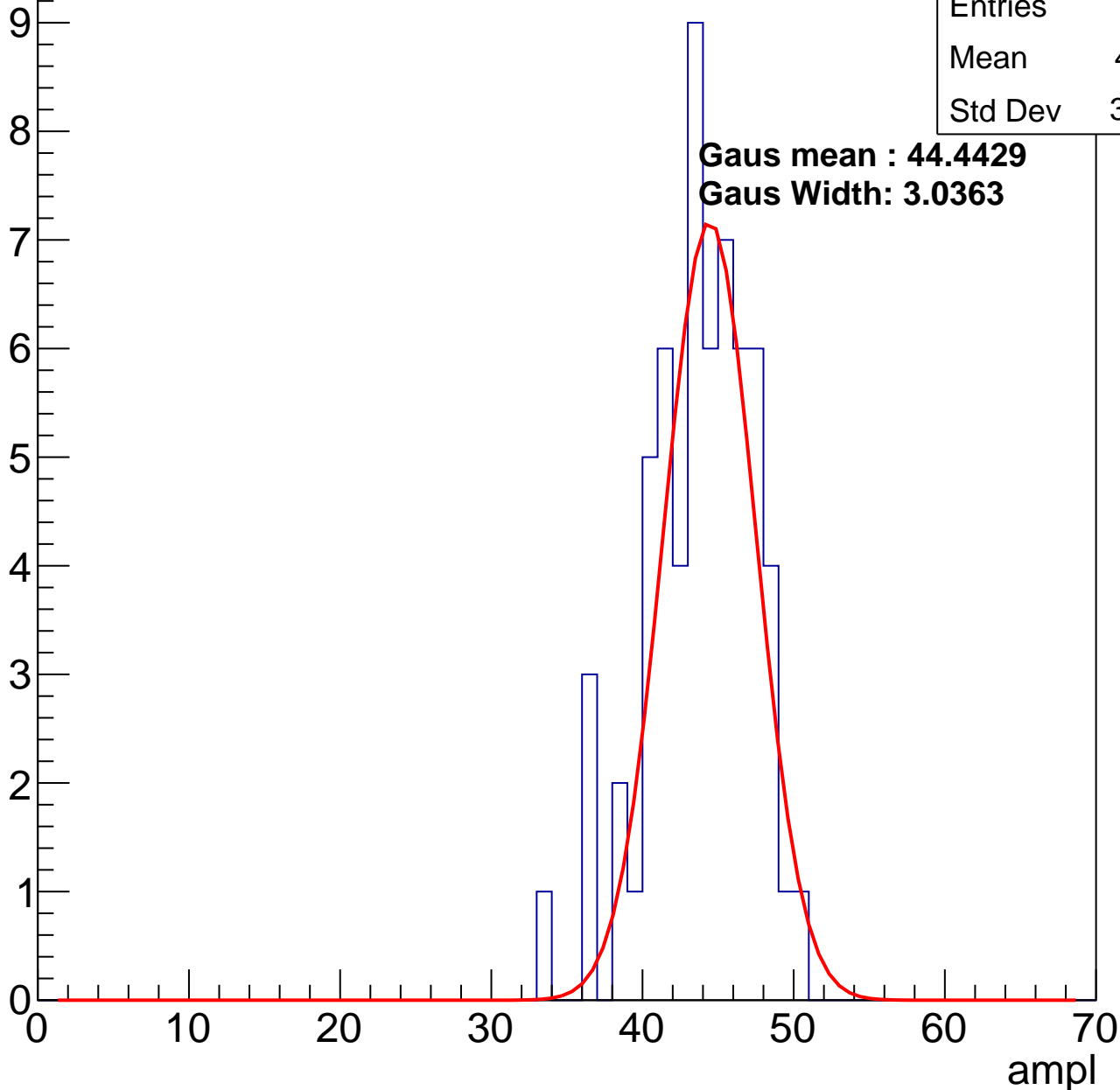
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.31
Std Dev	3.476

Gaus mean : 44.4429

Gaus Width: 3.0363

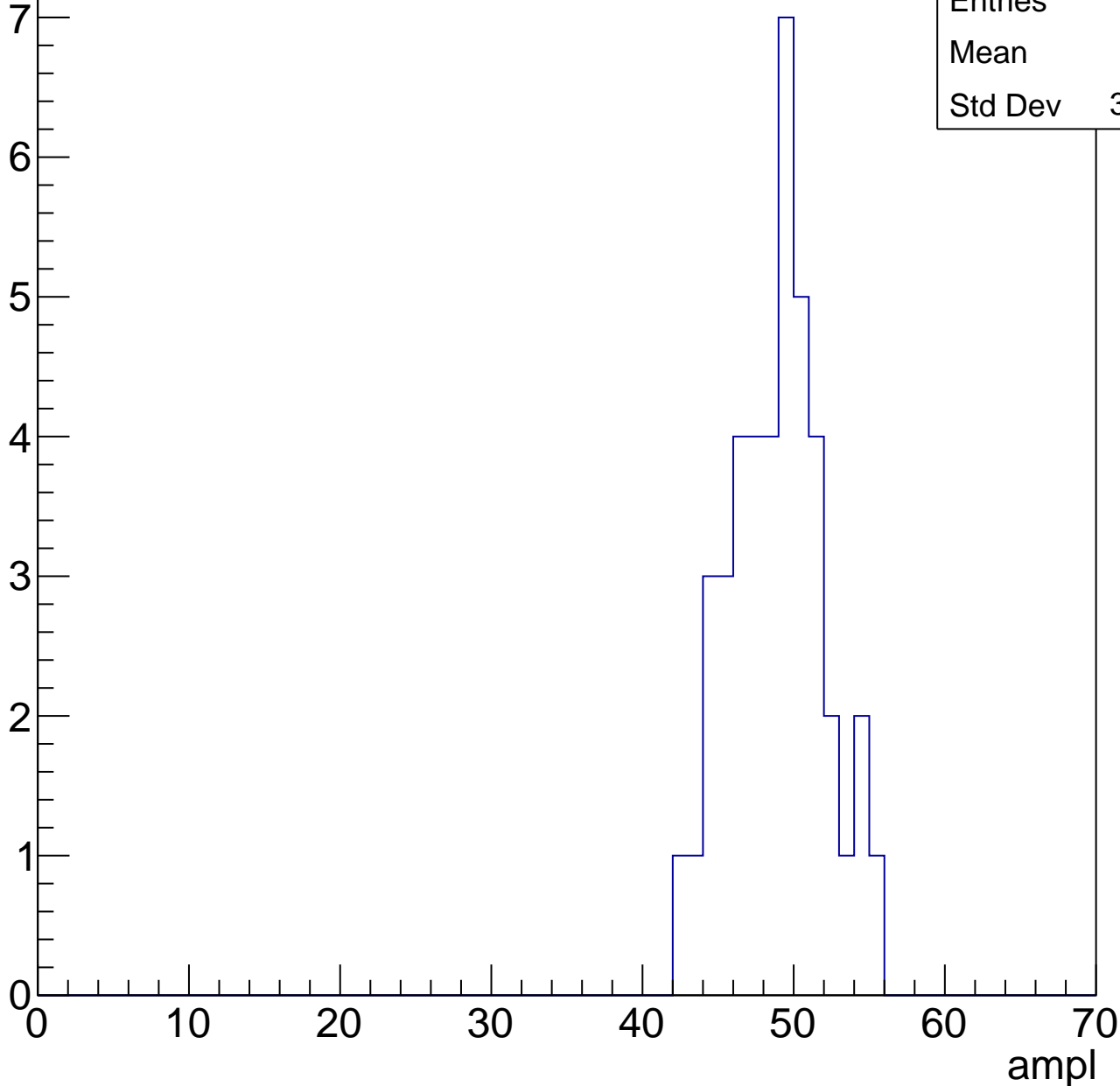


B1L003S, U19-ch10, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	48.4
Std Dev	3.048

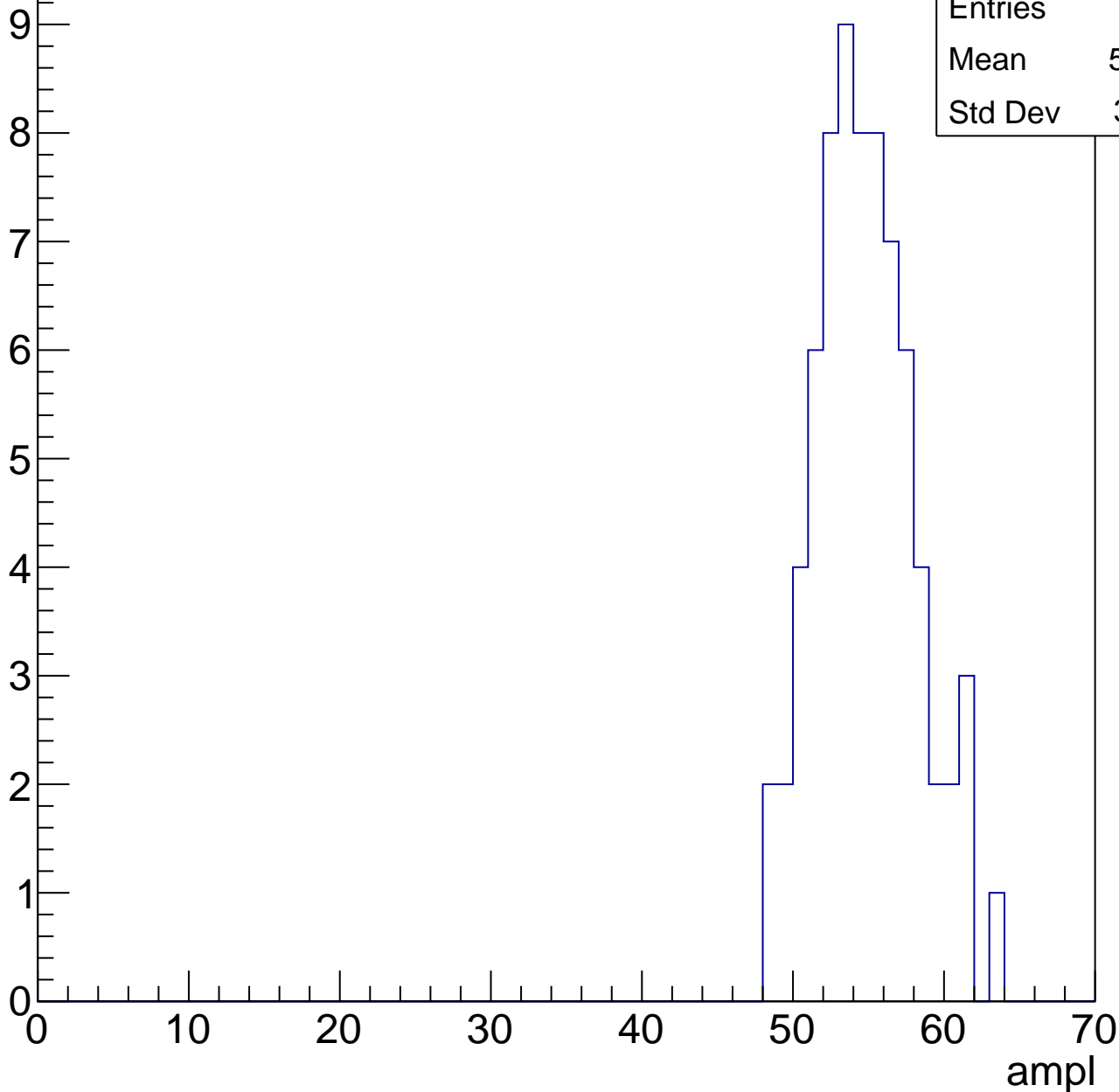


B1L003S, U19-ch10, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	54.38
Std Dev	3.281

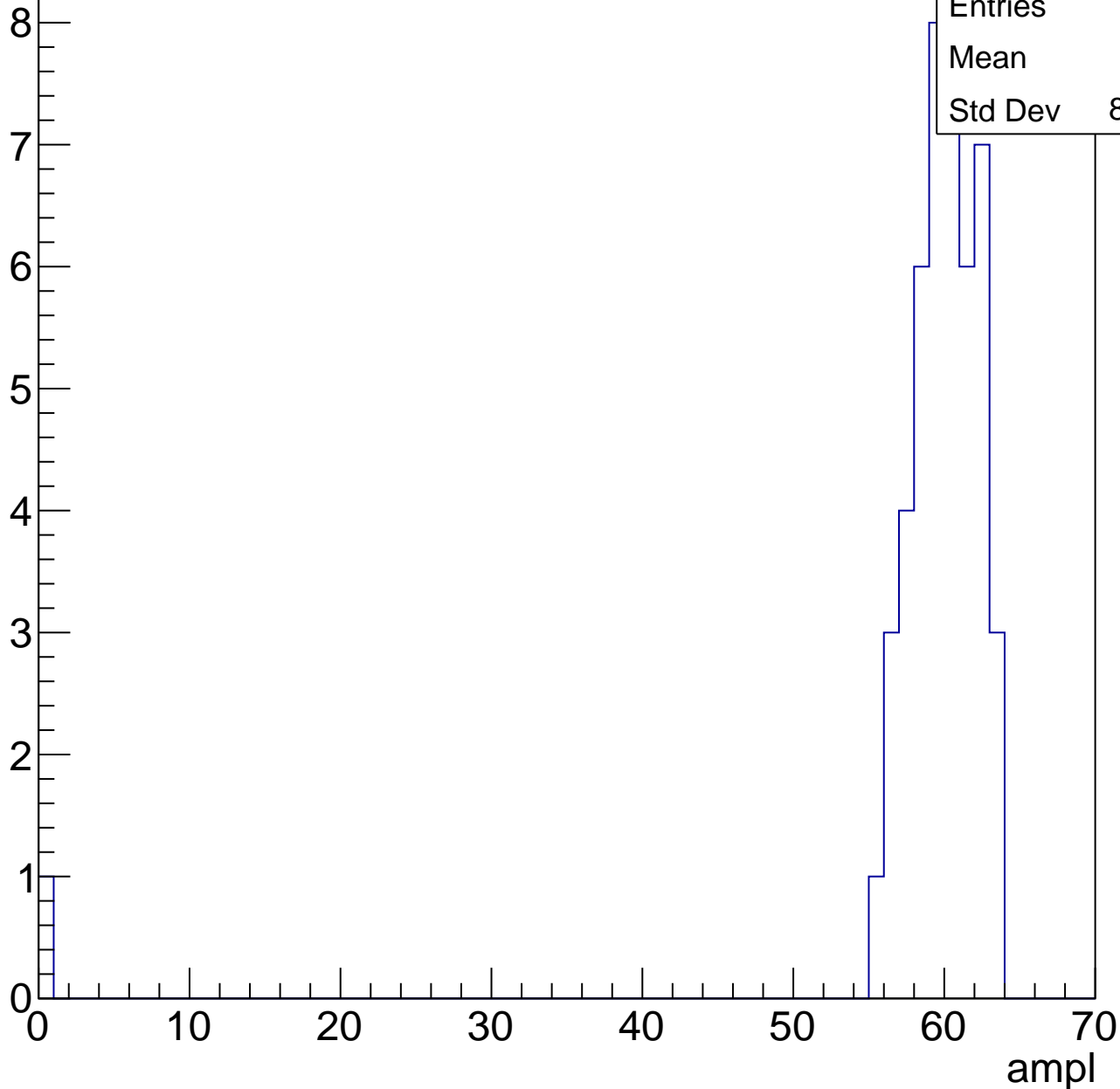


B1L003S, U19-ch10, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	58.3
Std Dev	8.832

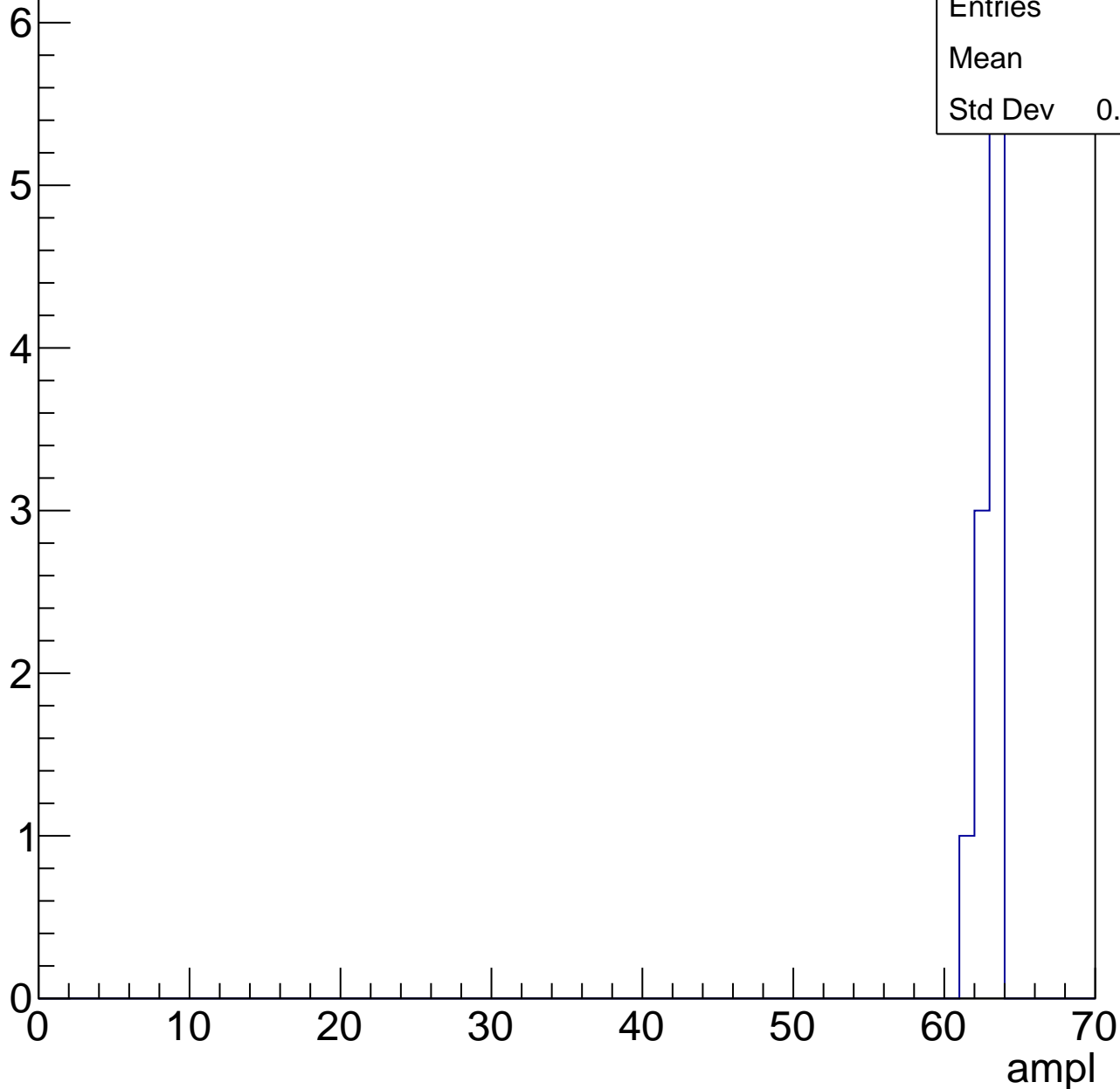


B1L003S, U19-ch10, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

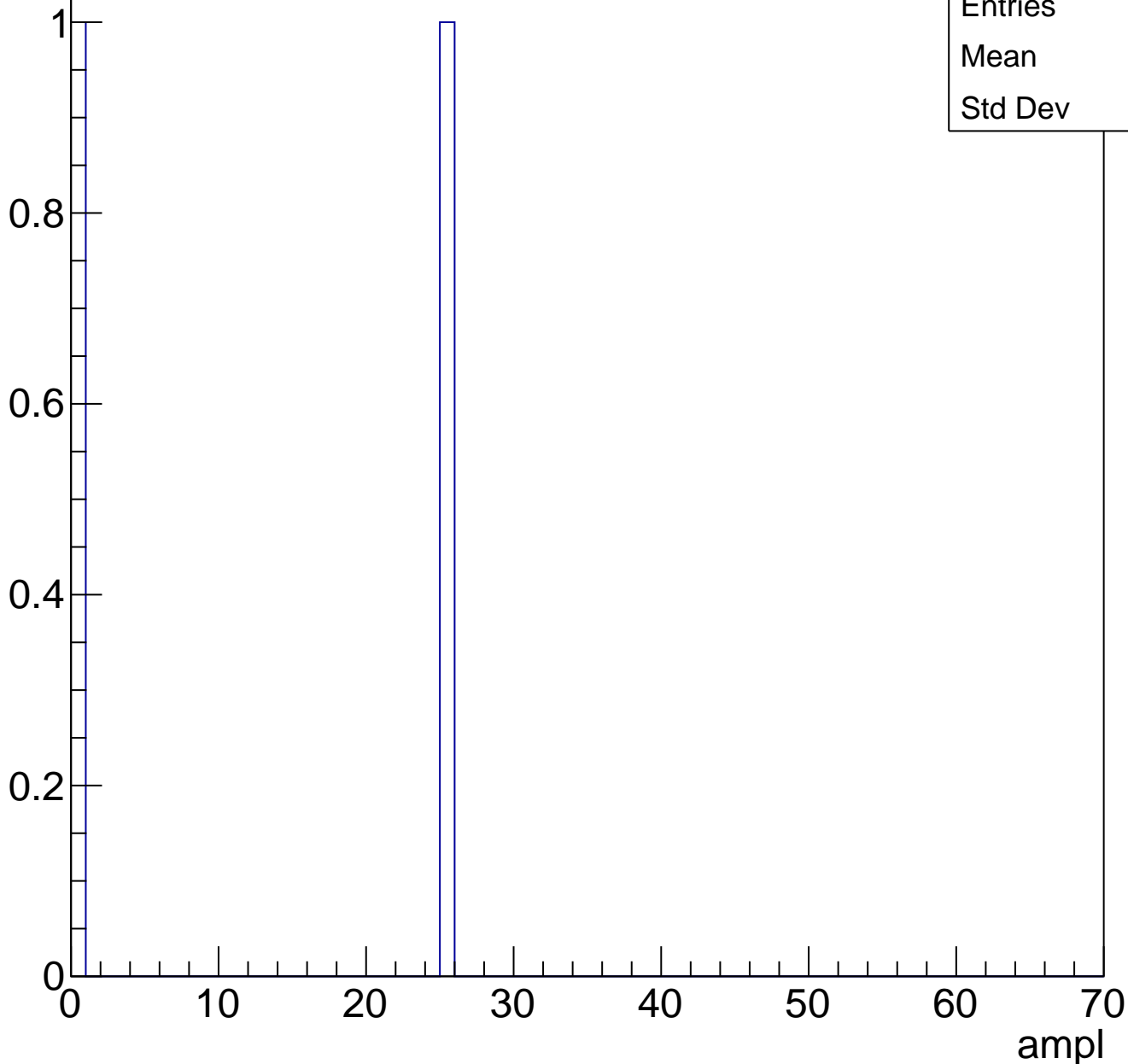
Entries	10
Mean	62.5
Std Dev	0.6708



B1L003S, U19-ch10, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch11, adc0

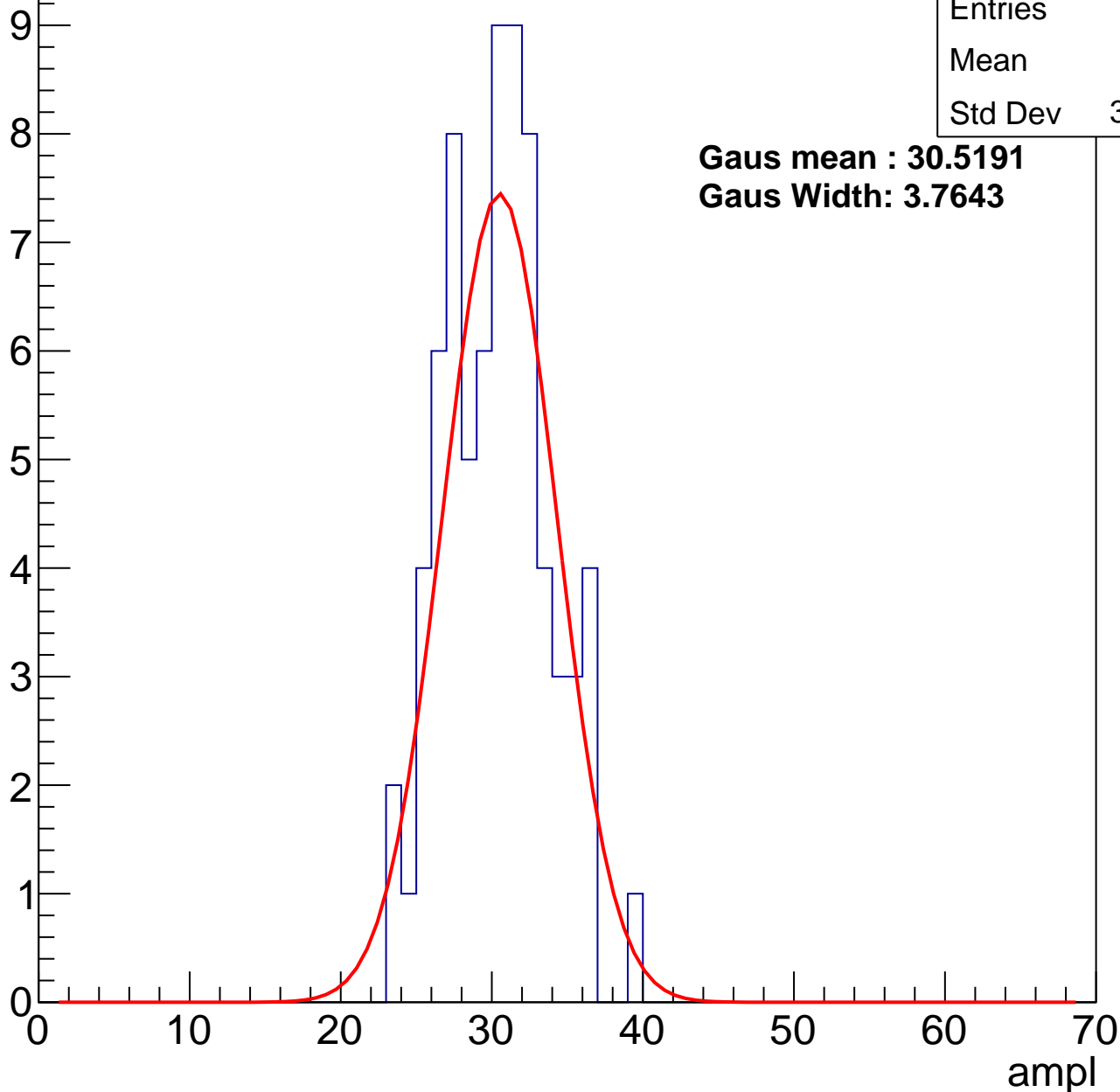
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.9
Std Dev	3.417

Gaus mean : 30.5191

Gaus Width: 3.7643

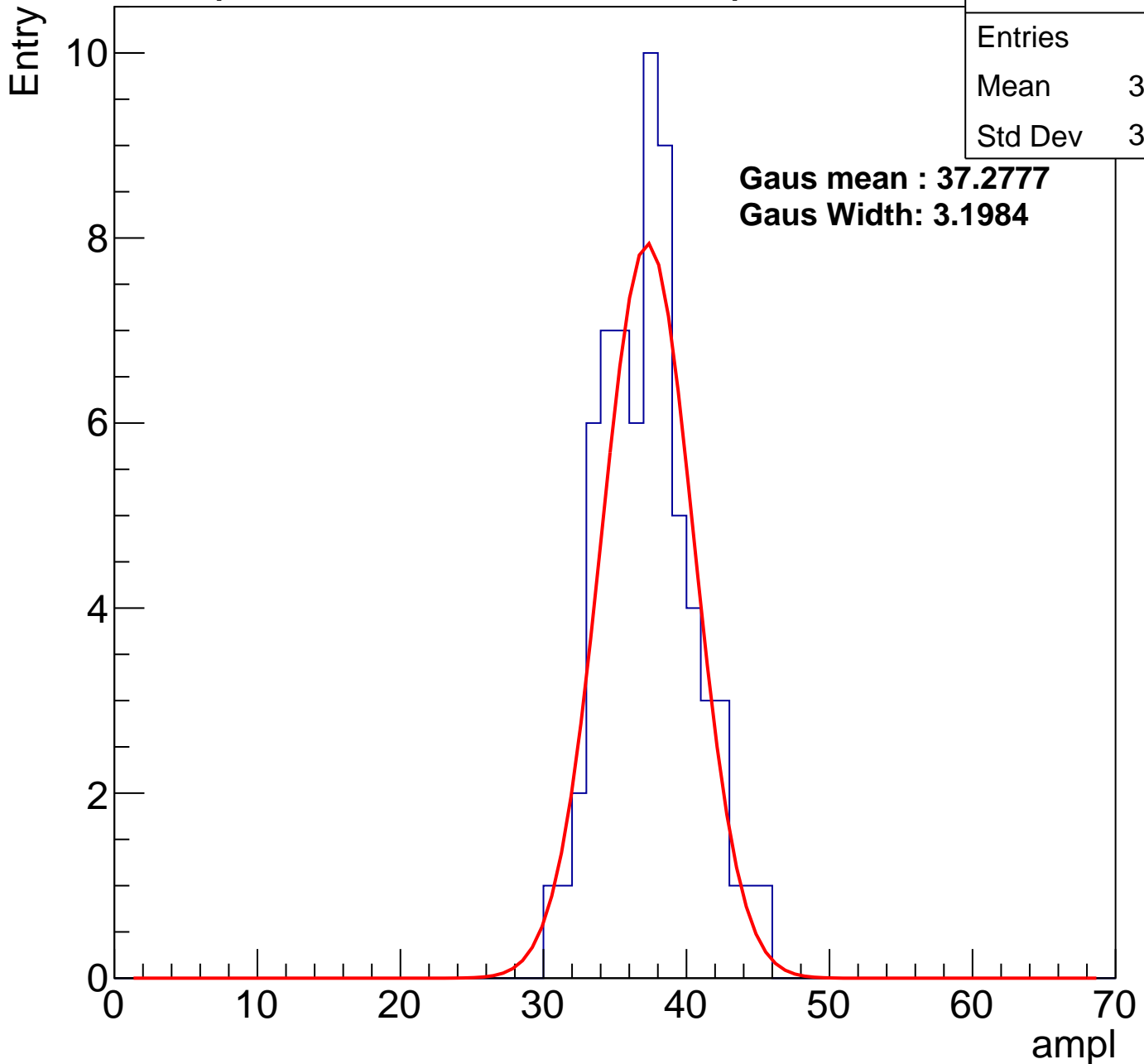


B1L003S, U19-ch11, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	36.87
Std Dev	3.148

Gaus mean : 37.2777
Gaus Width: 3.1984



B1L003S, U19-ch11, adc2

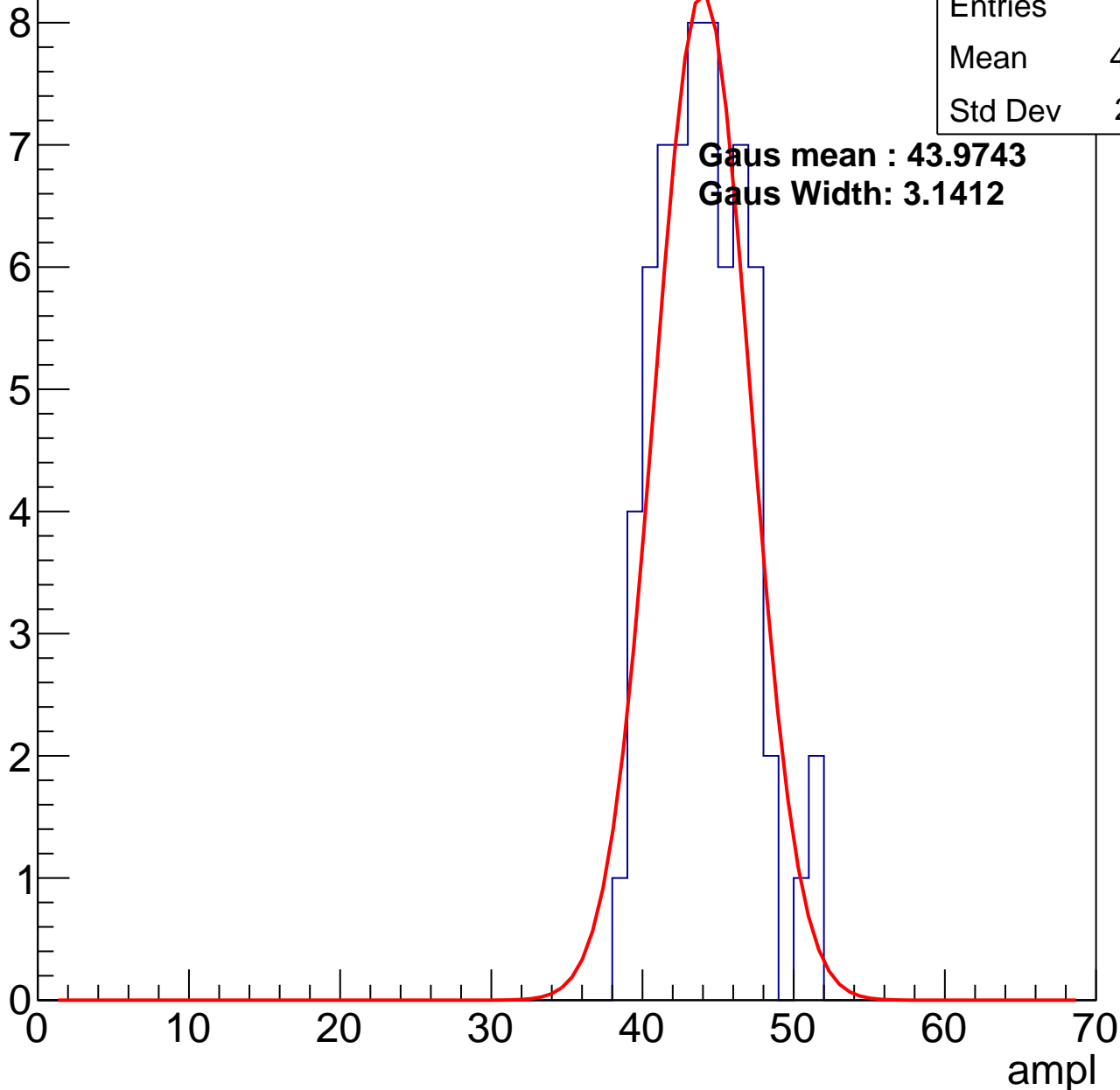
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	43.58
Std Dev	2.971

Gaus mean : 43.9743

Gaus Width: 3.1412

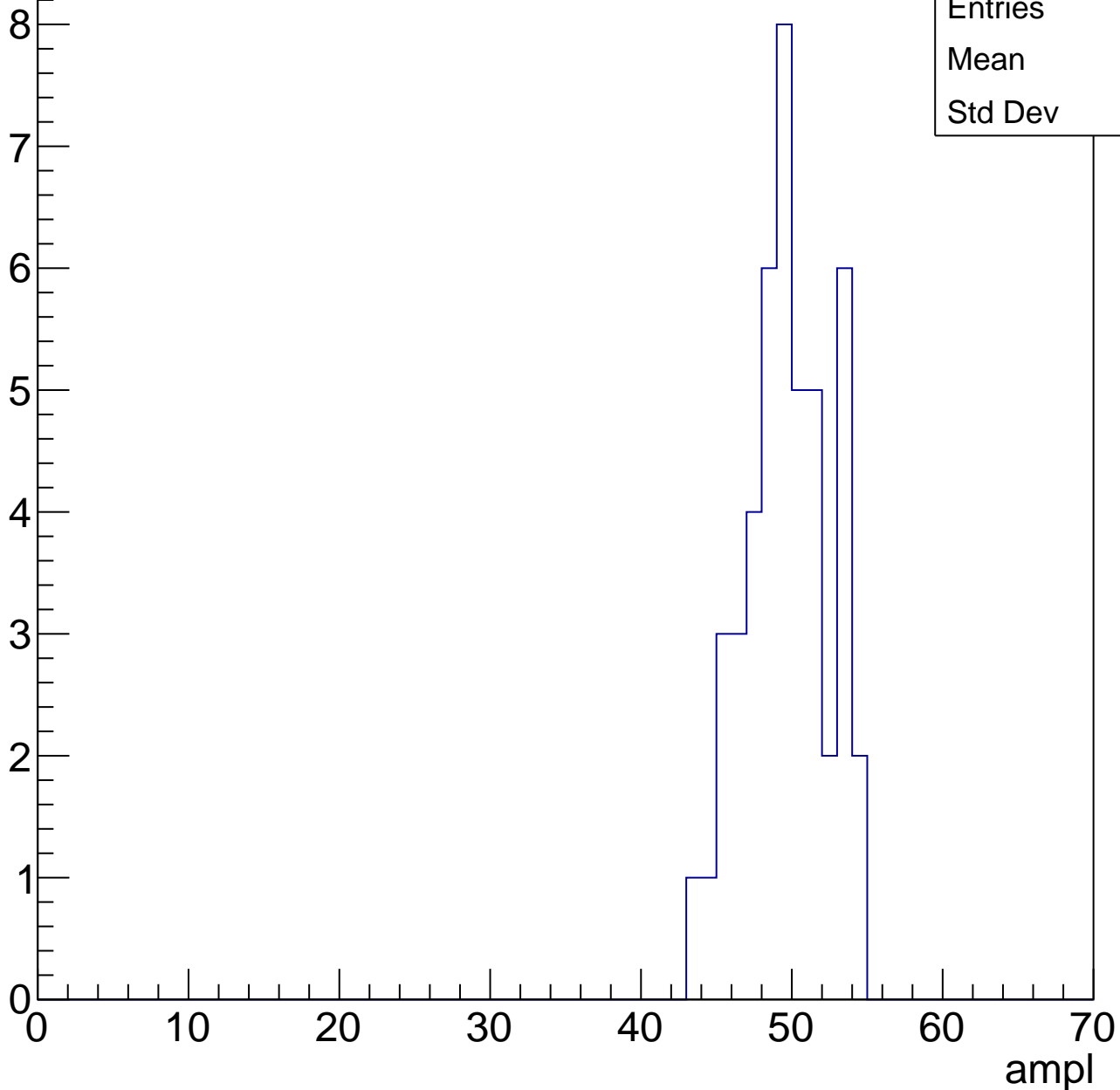


B1L003S, U19-ch11, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

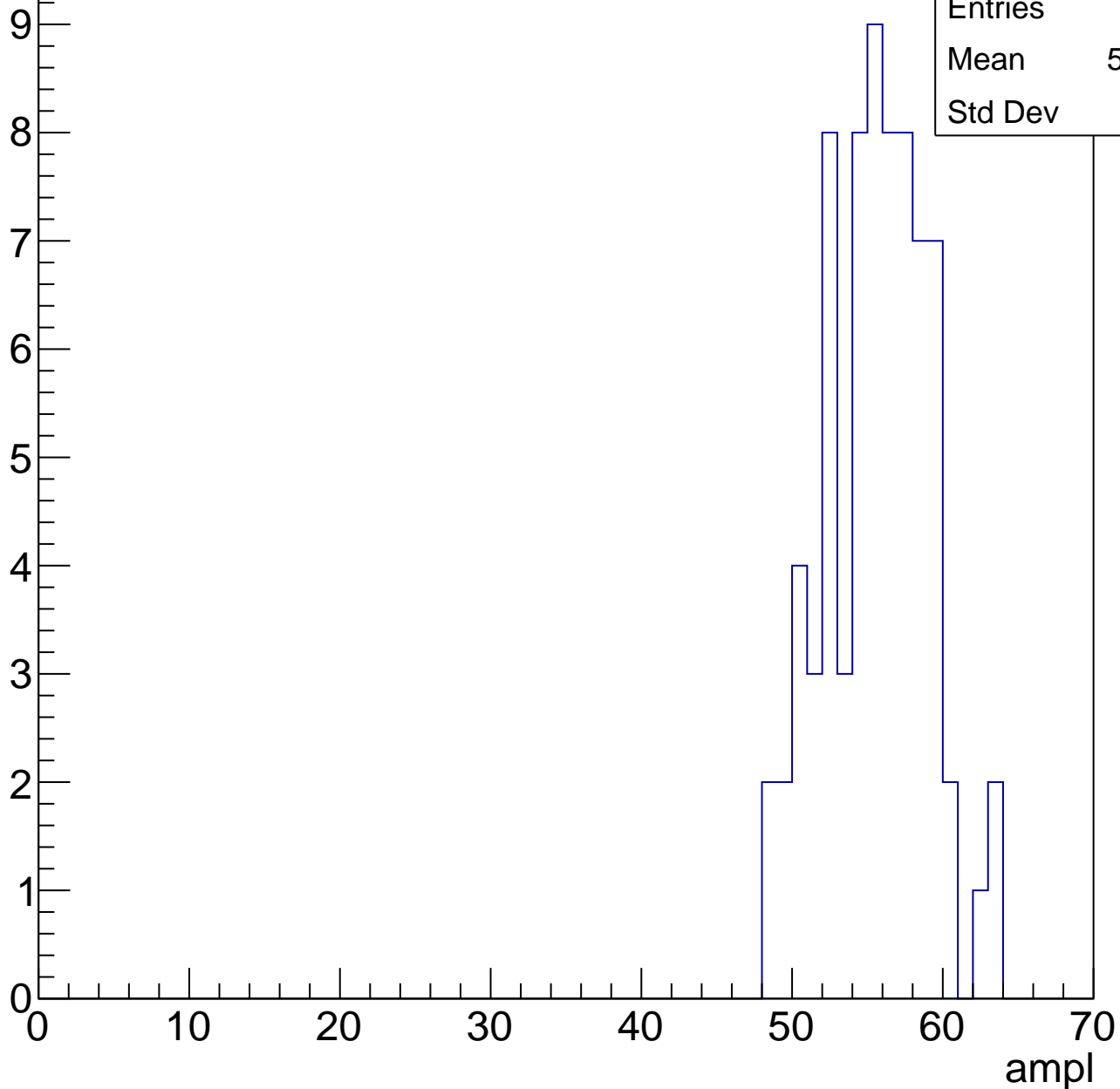
Entries	46
Mean	49.2
Std Dev	2.74



B1L003S, U19-ch11, adc4

calib_packv5_042523_0143.root, FC#13, port D2

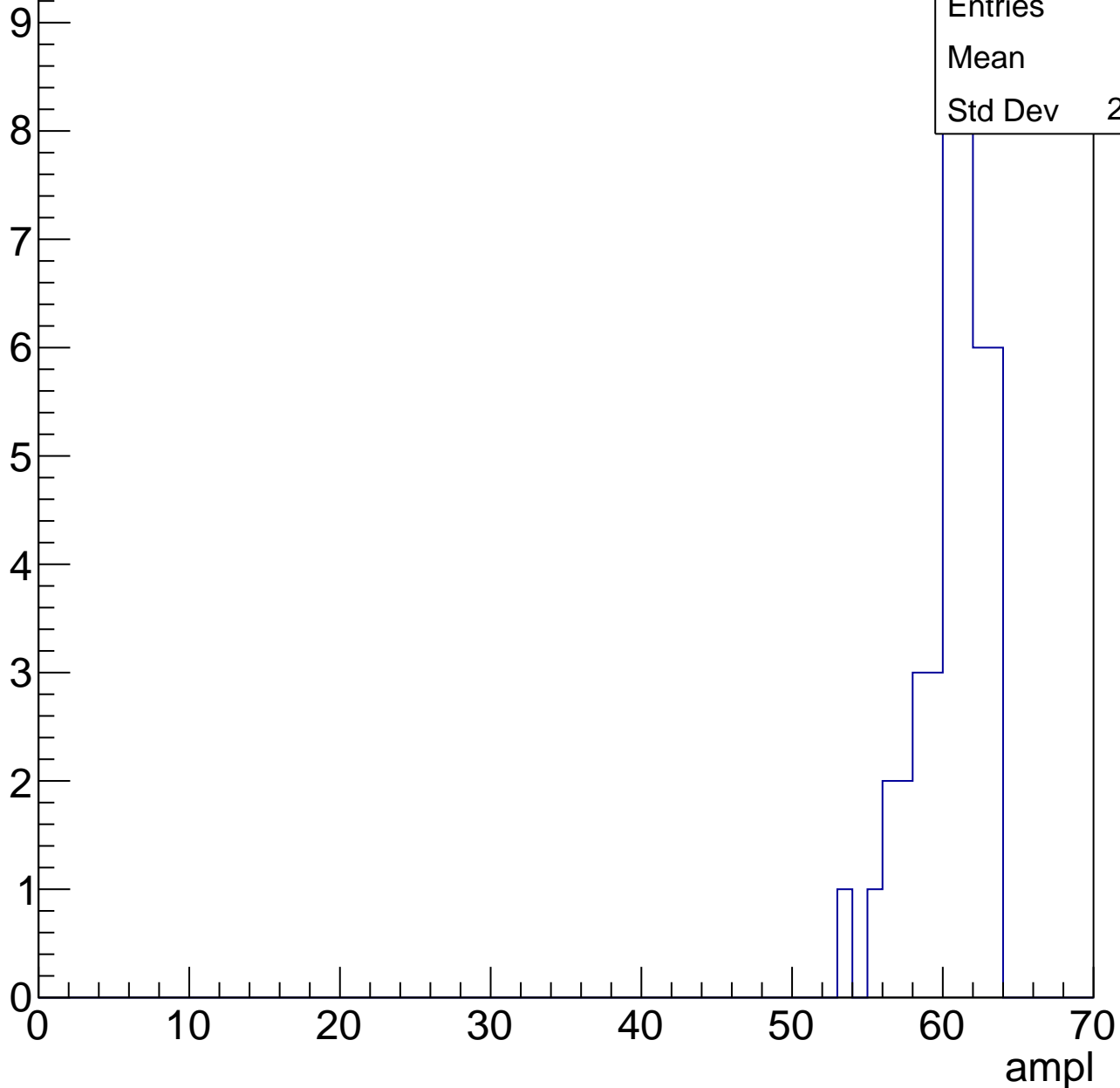
Entry



B1L003S, U19-ch11, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	41
Mean	60.1
Std Dev	2.346

B1L003S, U19-ch11, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch11, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch12, adc0

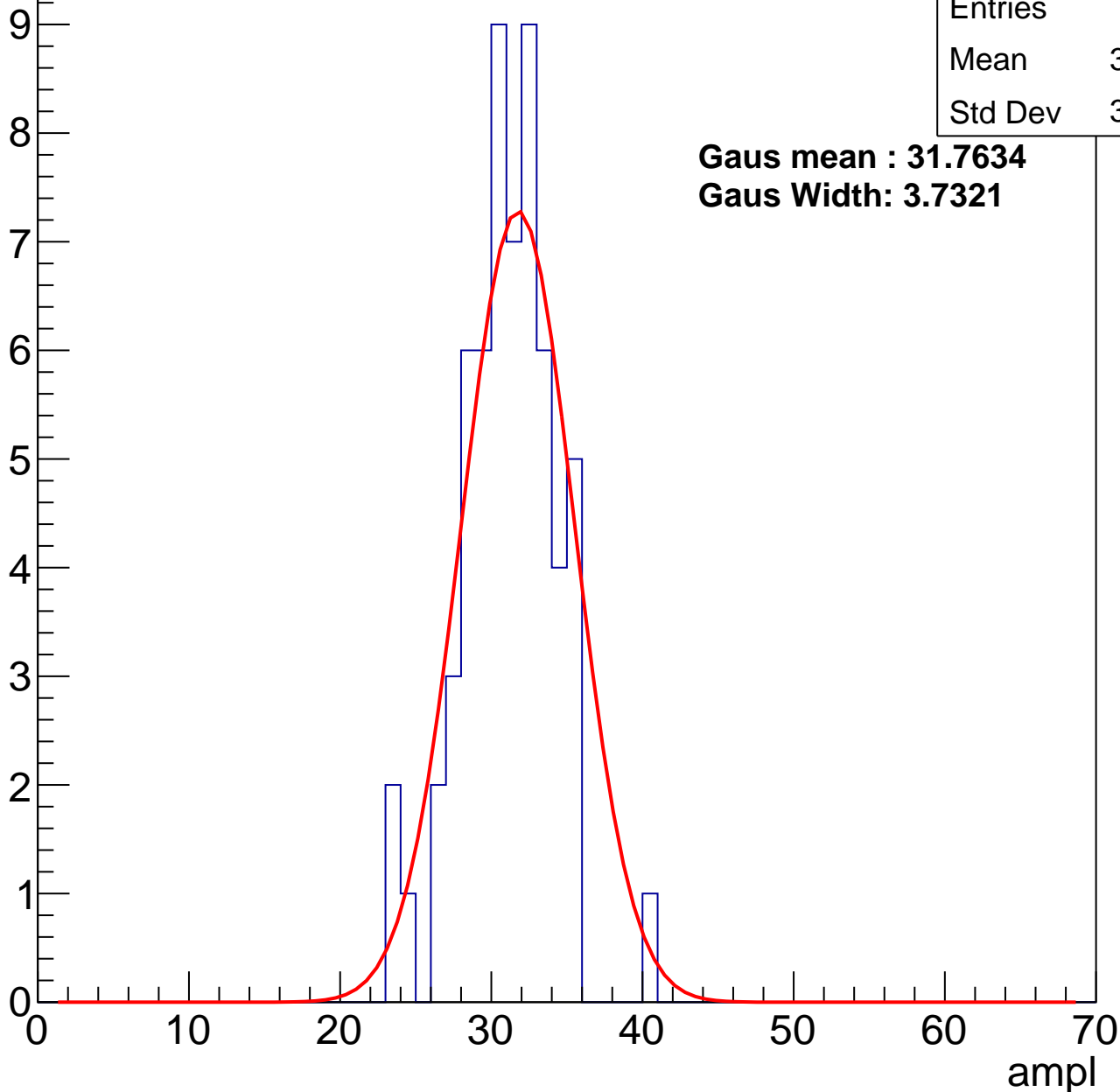
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	30.64
Std Dev	3.094

Gaus mean : 31.7634

Gaus Width: 3.7321



B1L003S, U19-ch12, adc1

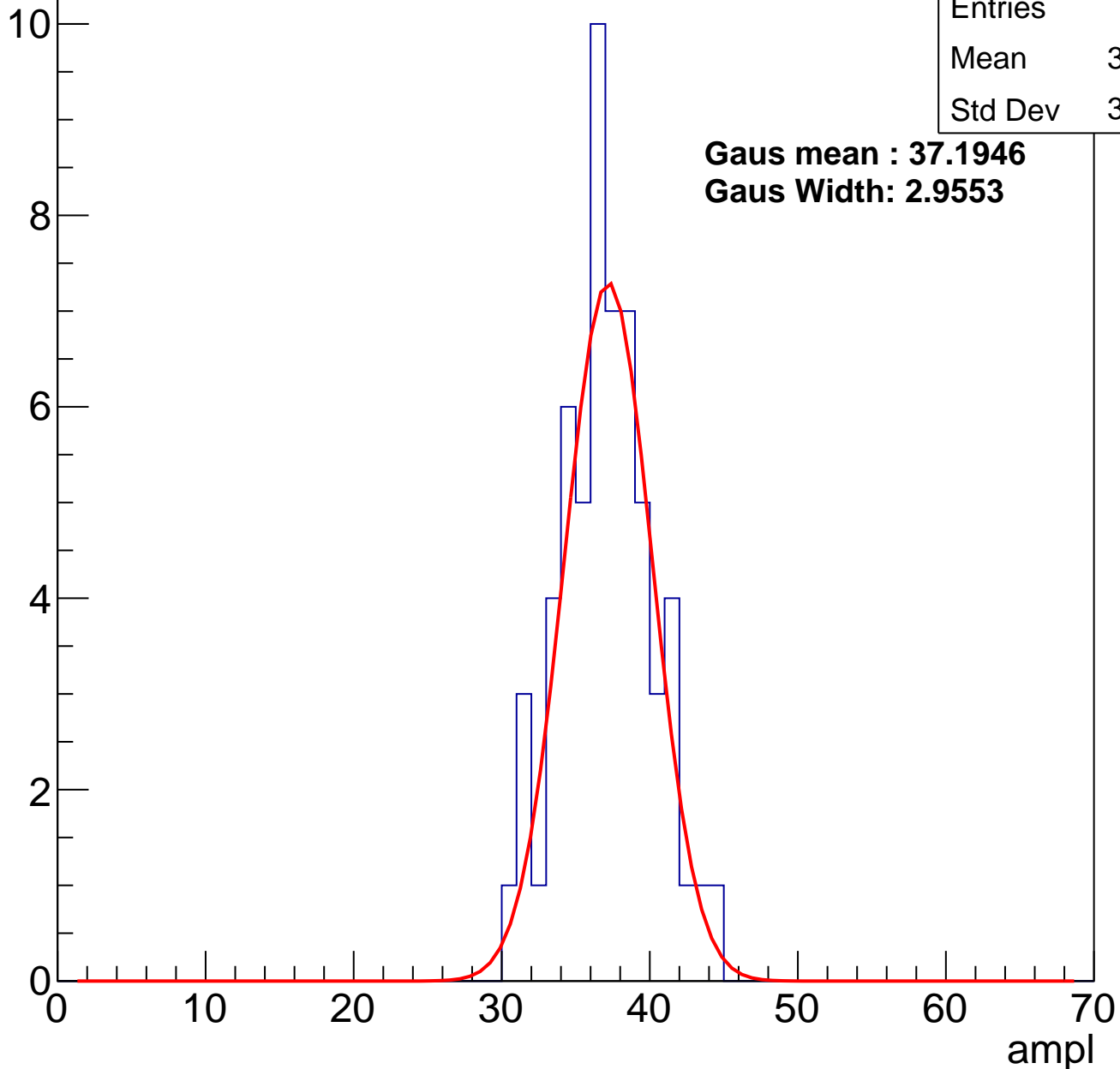
calib_packv5_042523_0143.root, FC#13, port D2

Entries	59
Mean	36.59
Std Dev	3.048

Gaus mean : 37.1946

Gaus Width: 2.9553

Entry



B1L003S, U19-ch12, adc2

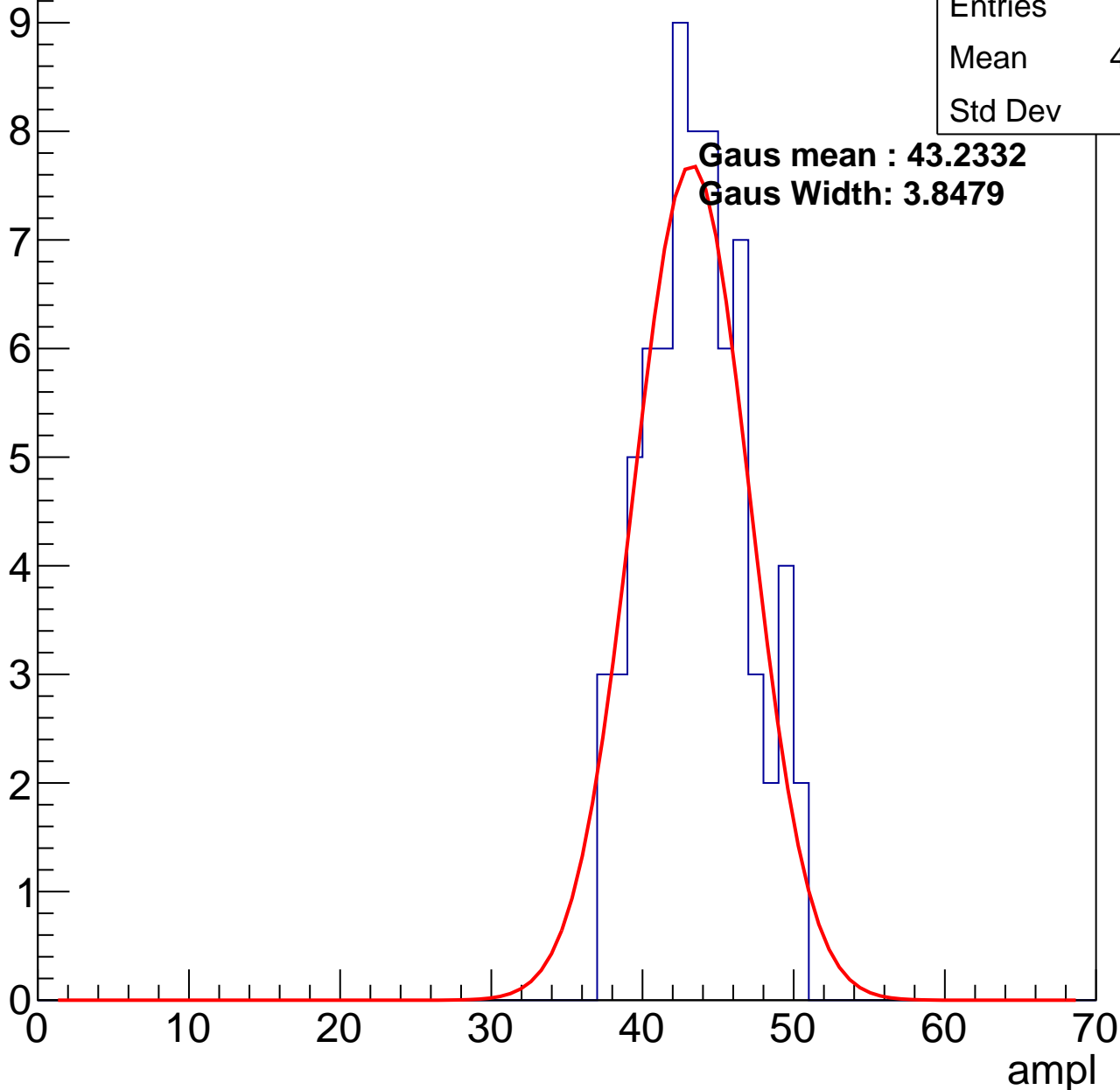
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	43.12
Std Dev	3.3

Gaus mean : 43.2332

Gaus Width: 3.8479

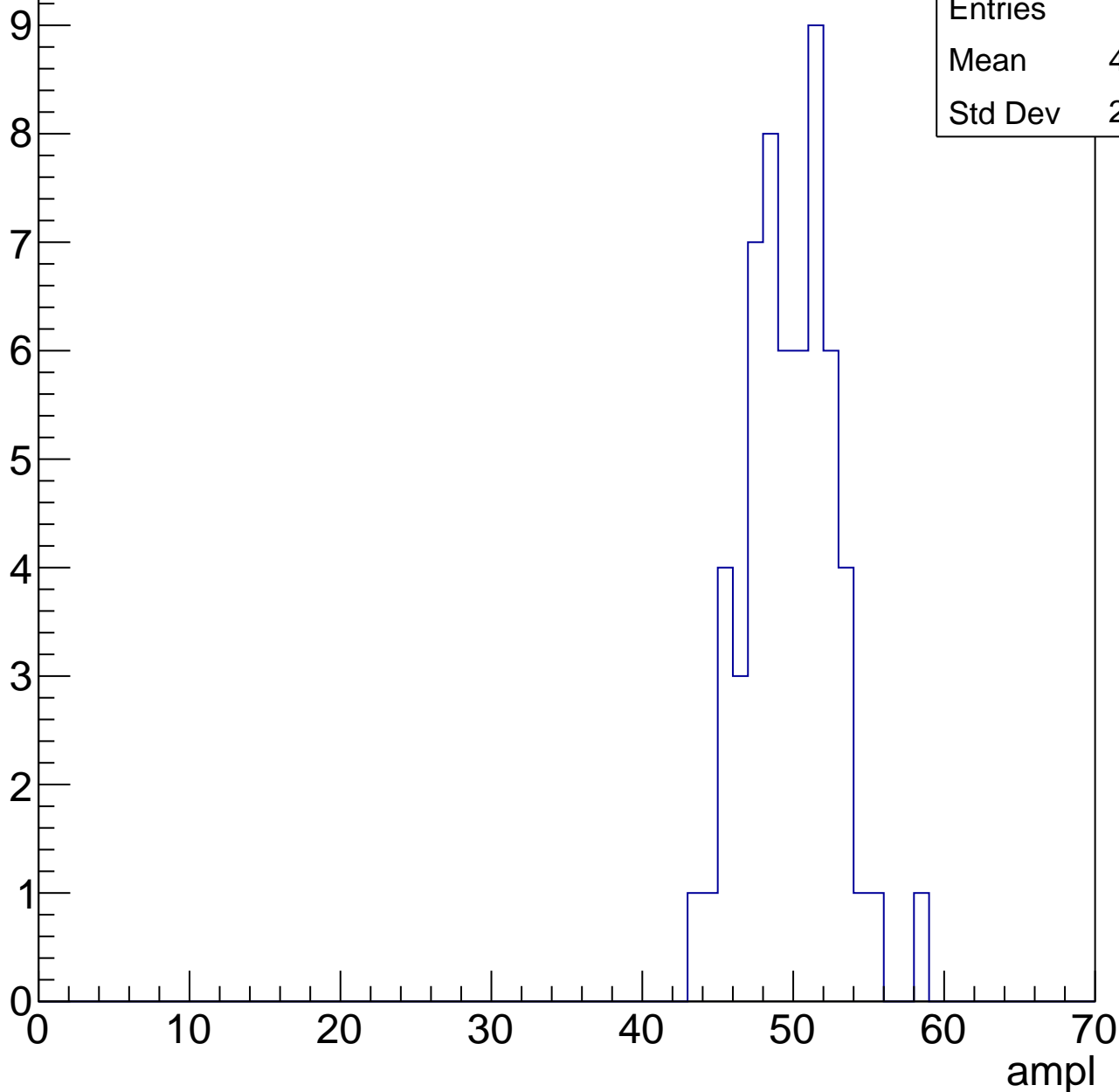


B1L003S, U19-ch12, adc3

calib_packv5_042523_0143.root, FC#13, port D2

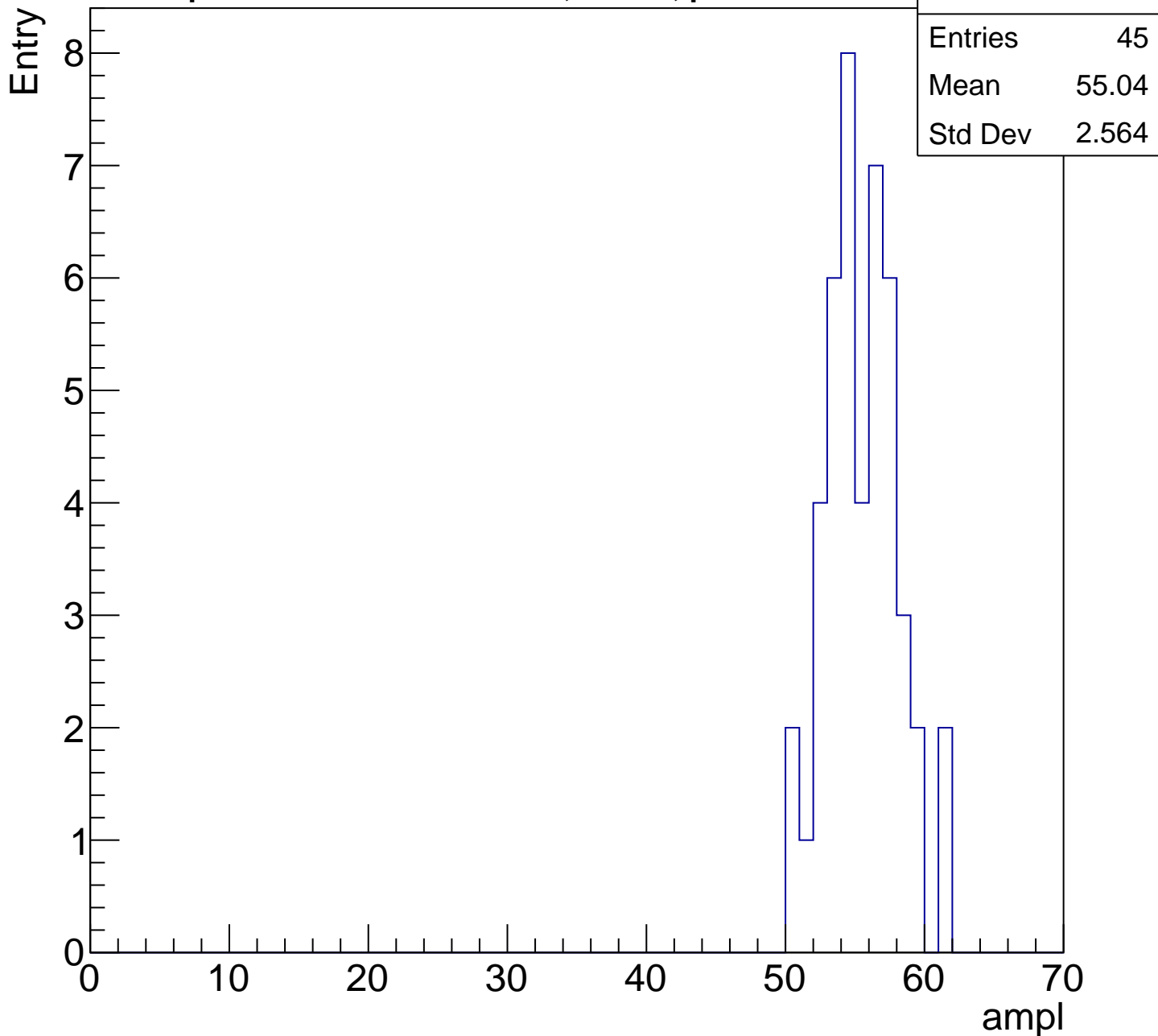
Entry

Entries	58
Mean	49.34
Std Dev	2.886



B1L003S, U19-ch12, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch12, adc5

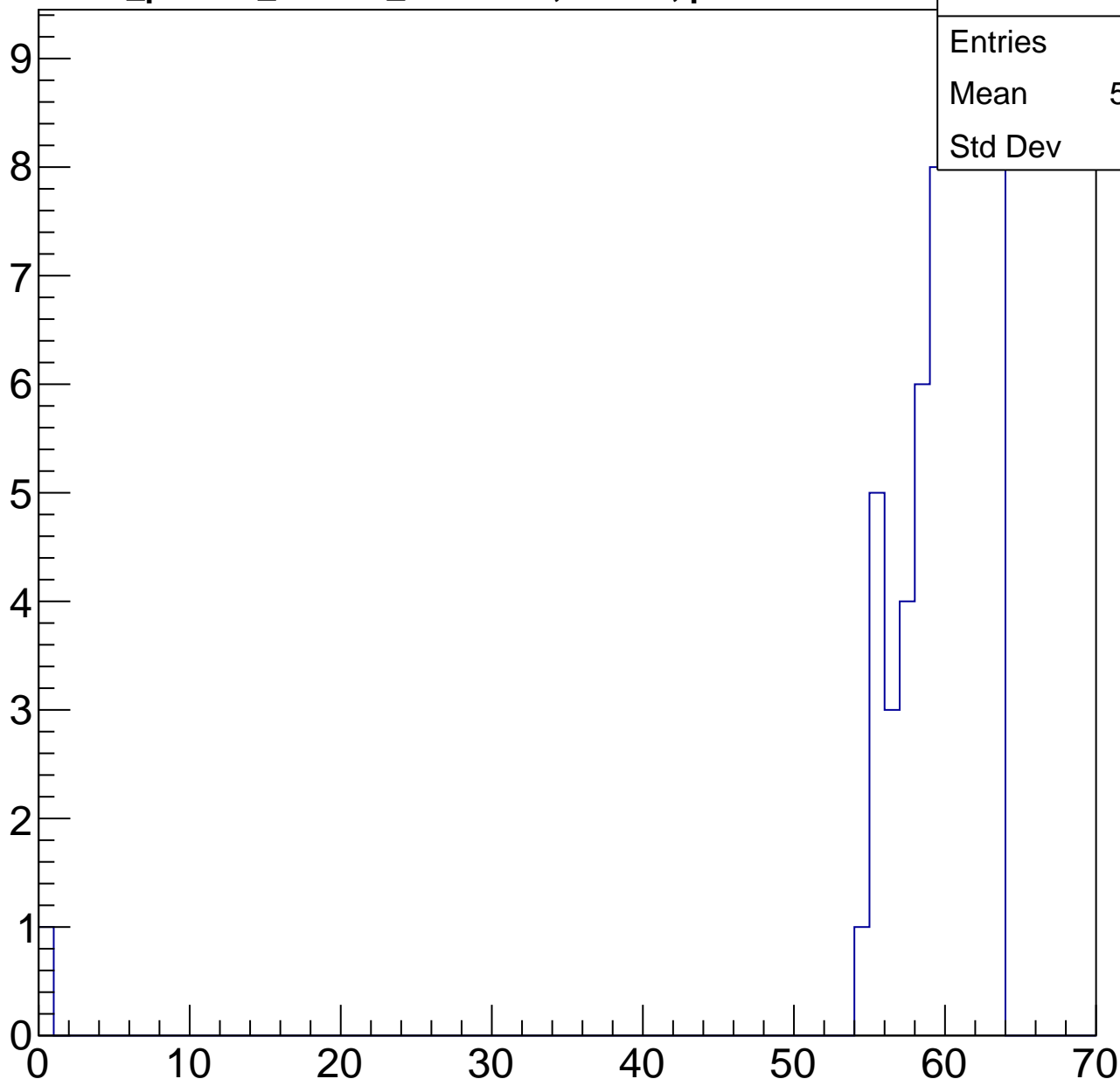
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	58.57
Std Dev	7.96

ampl



B1L003S, U19-ch12, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch12, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch13, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	28.81
Std Dev	5.993

Gaus mean : 29.8810

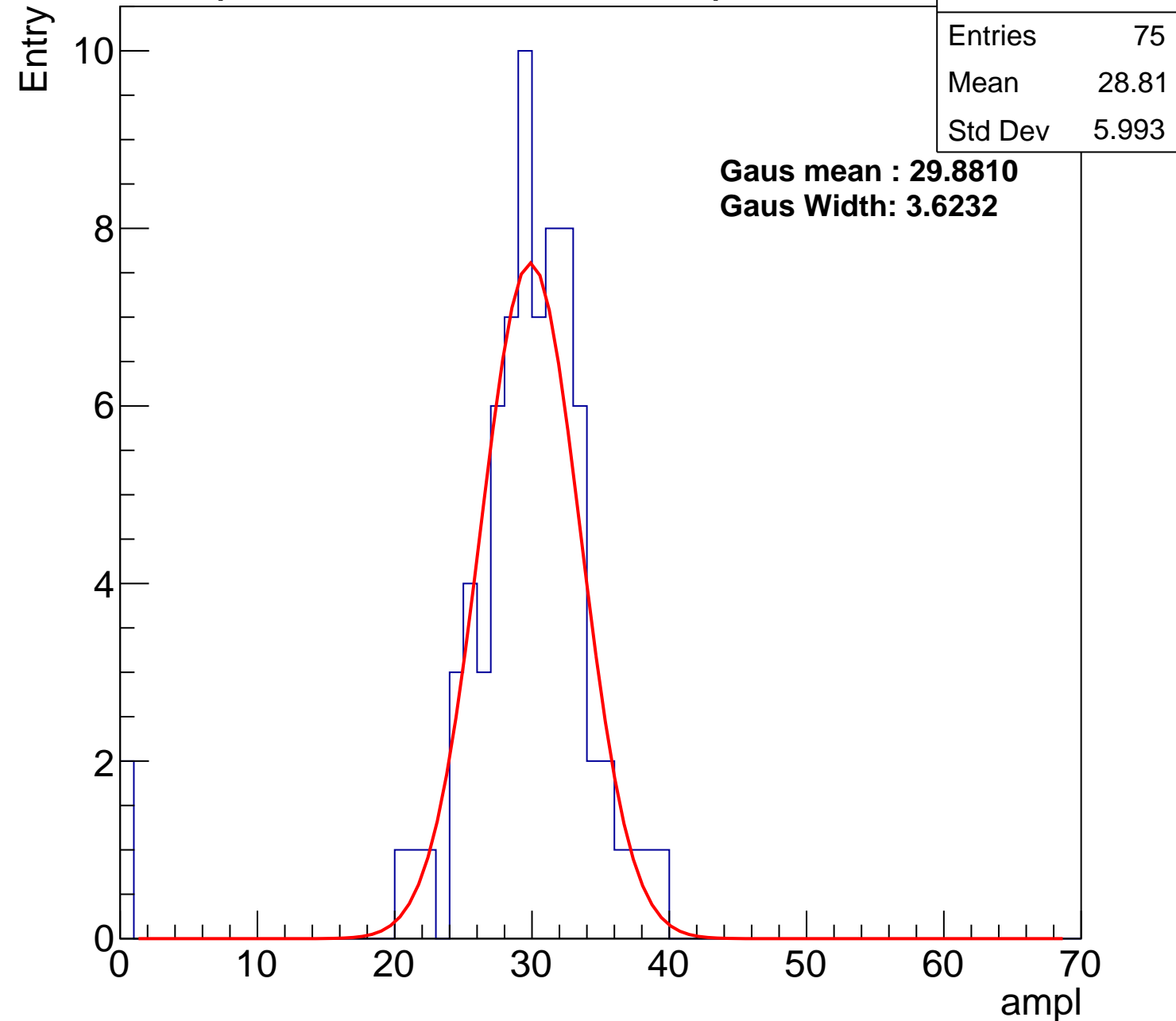
Gaus Width: 3.6232

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



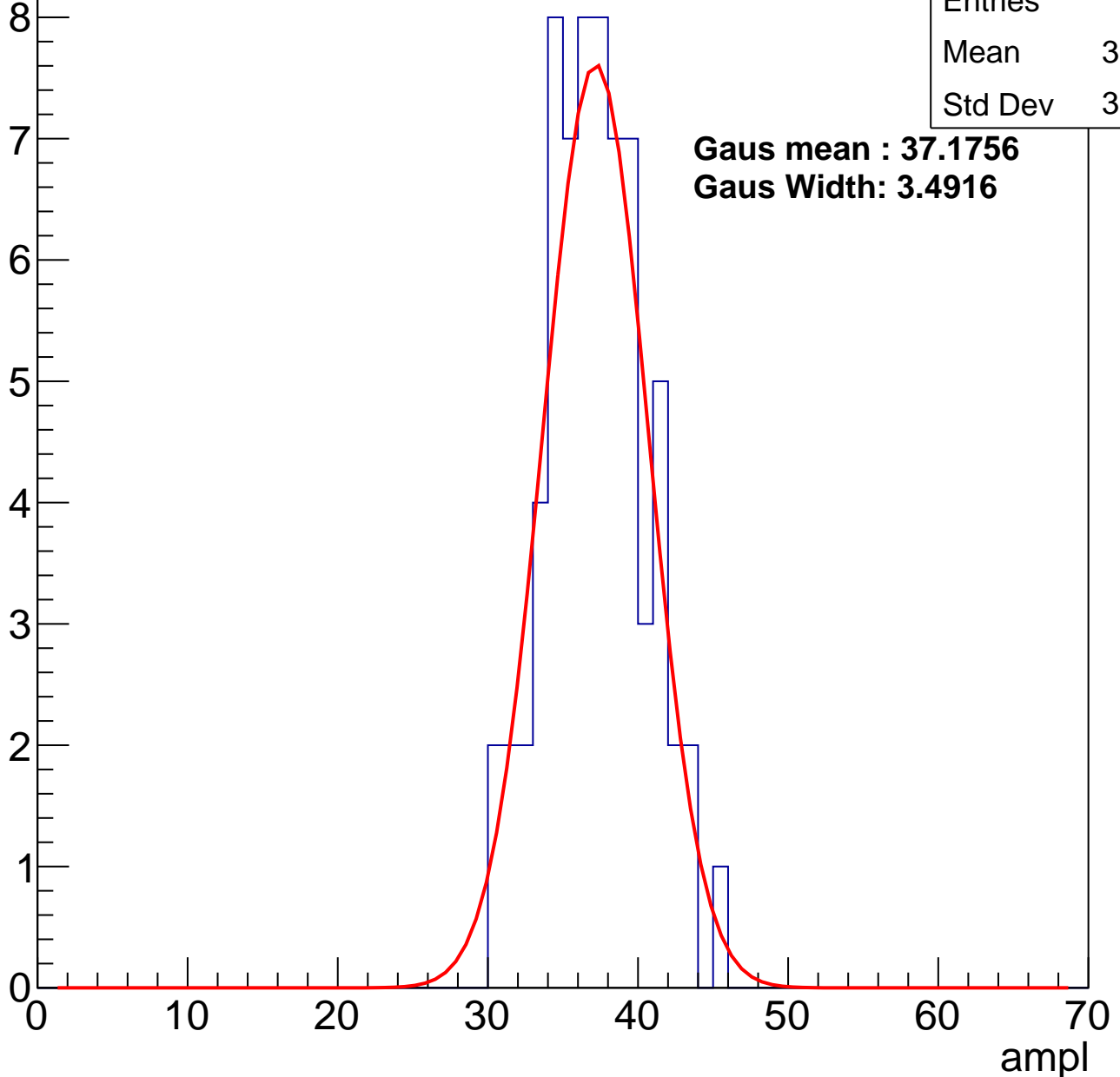
B1L003S, U19-ch13, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.74
Std Dev	3.243

Gaus mean : 37.1756
Gaus Width: 3.4916



B1L003S, U19-ch13, adc2

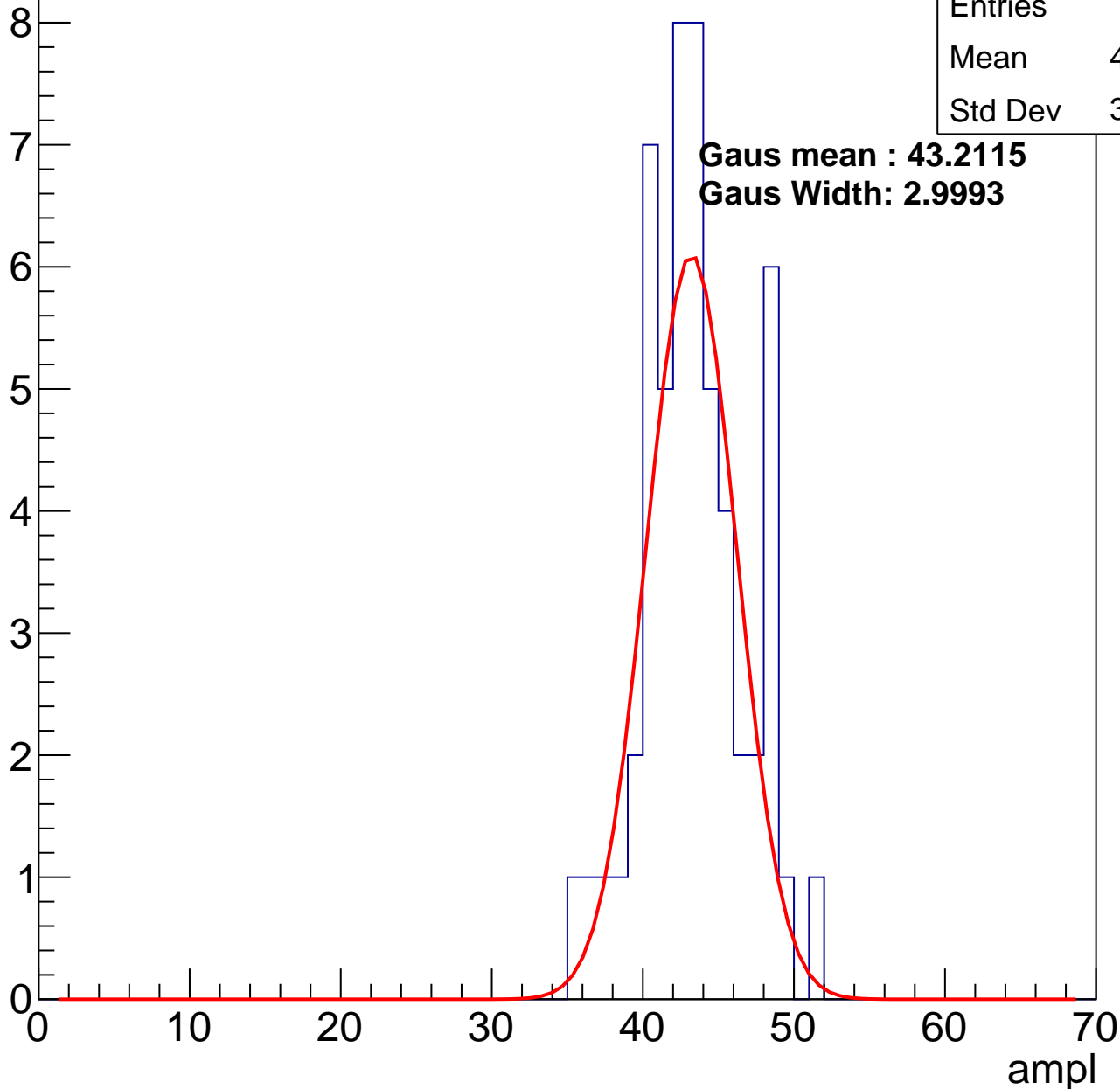
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.96
Std Dev	3.352

Gaus mean : 43.2115

Gaus Width: 2.9993

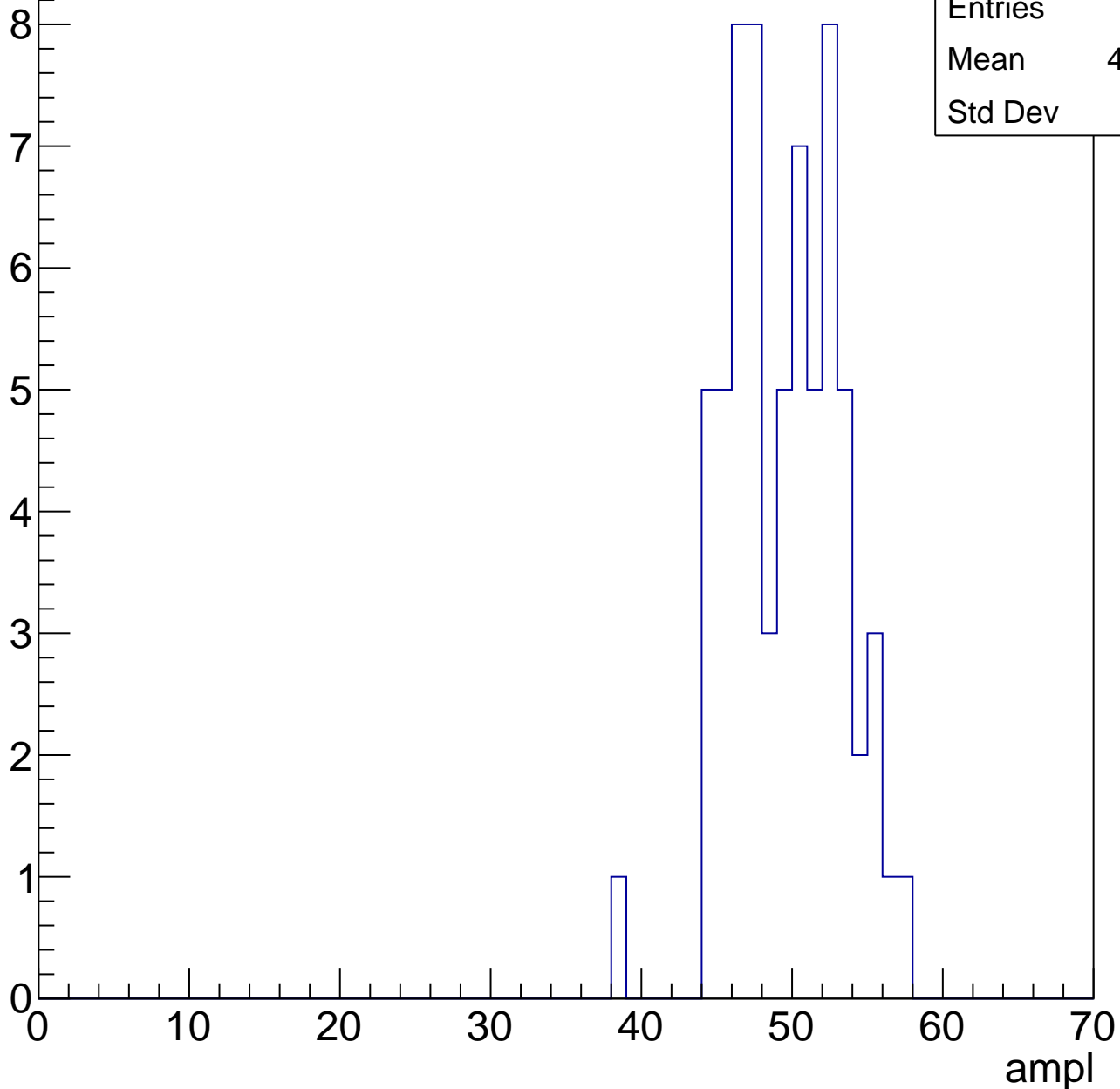


B1L003S, U19-ch13, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	49.07
Std Dev	3.63

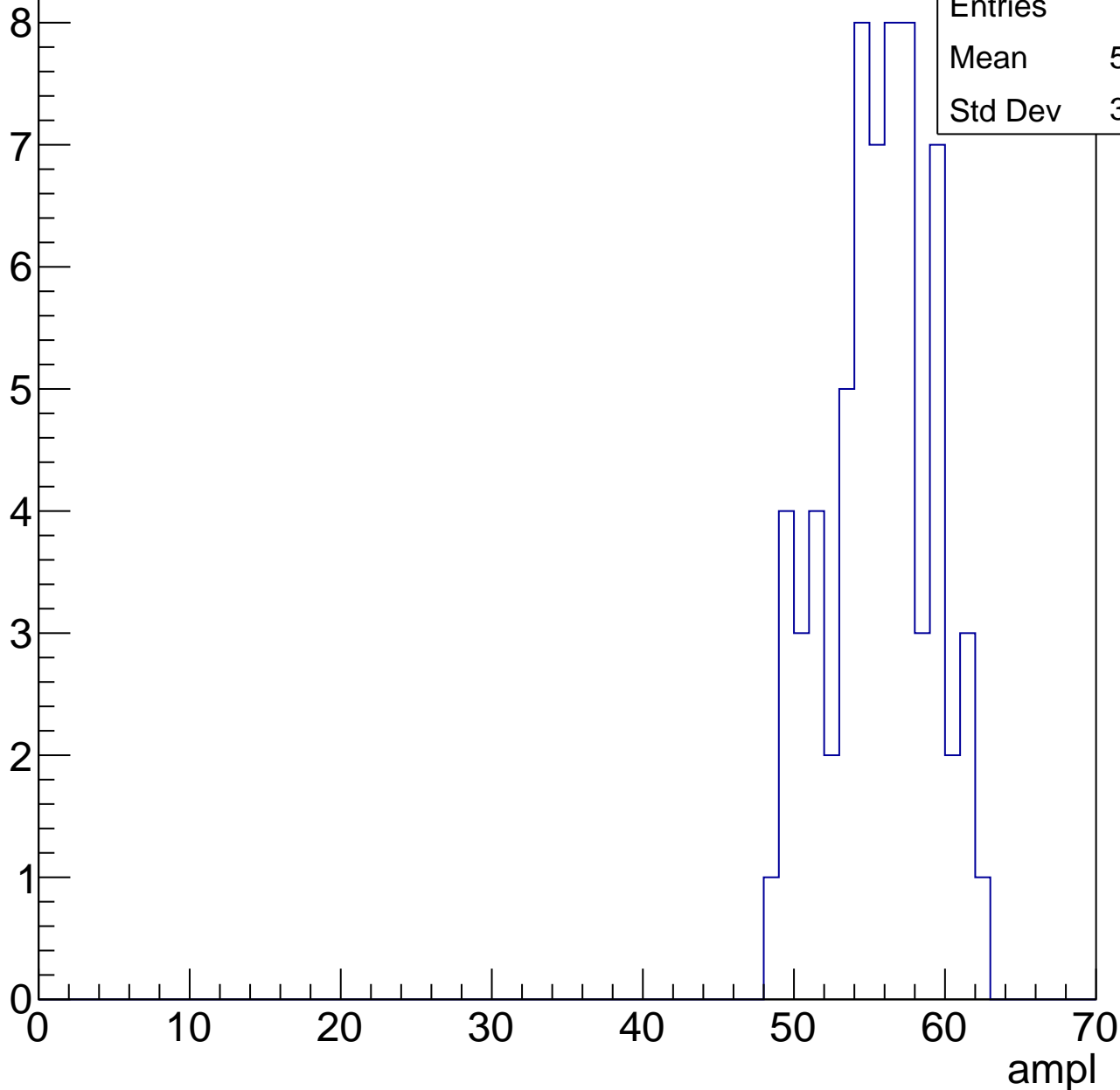


B1L003S, U19-ch13, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

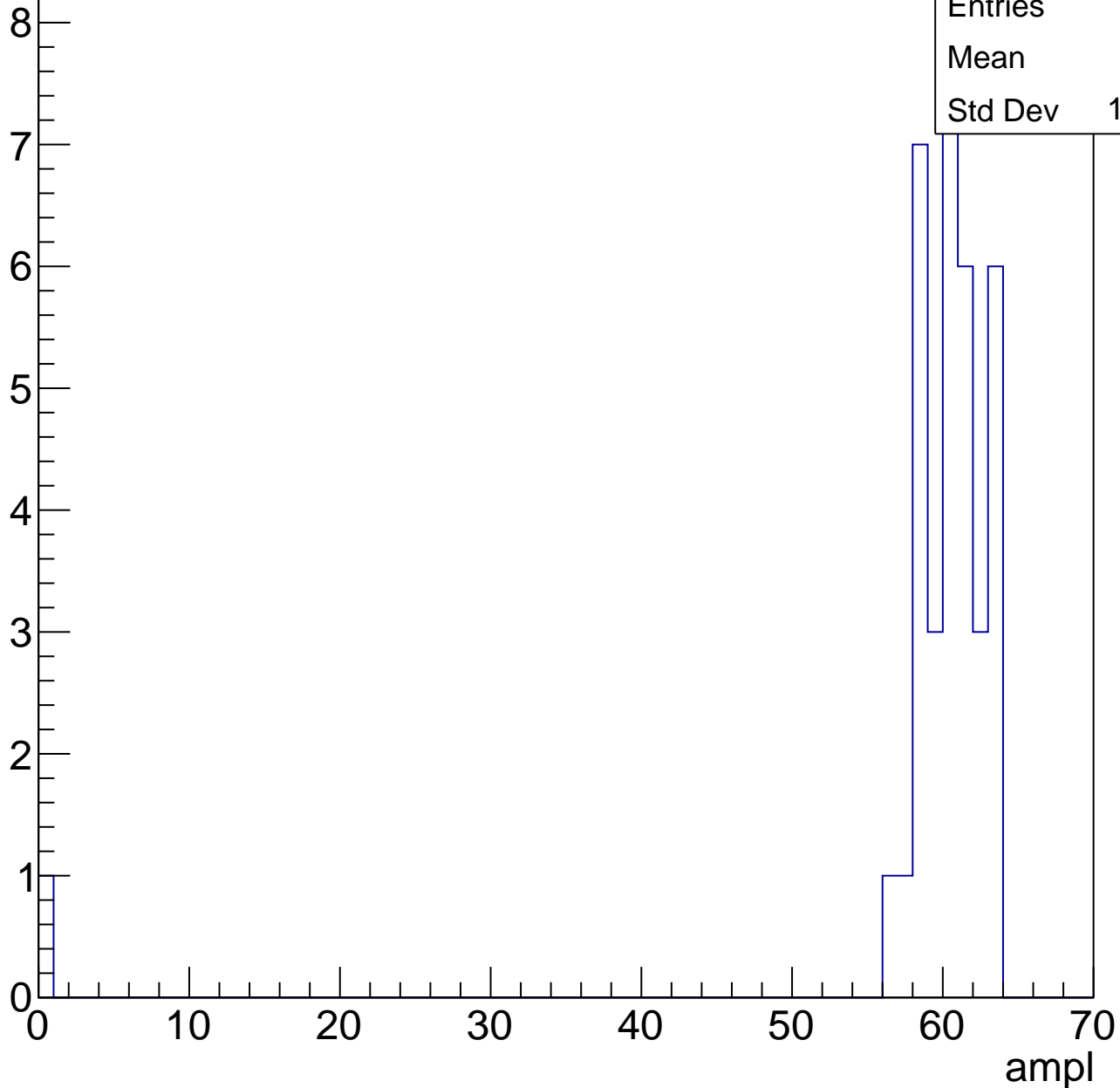
Entries	66
Mean	55.15
Std Dev	3.399



B1L003S, U19-ch13, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

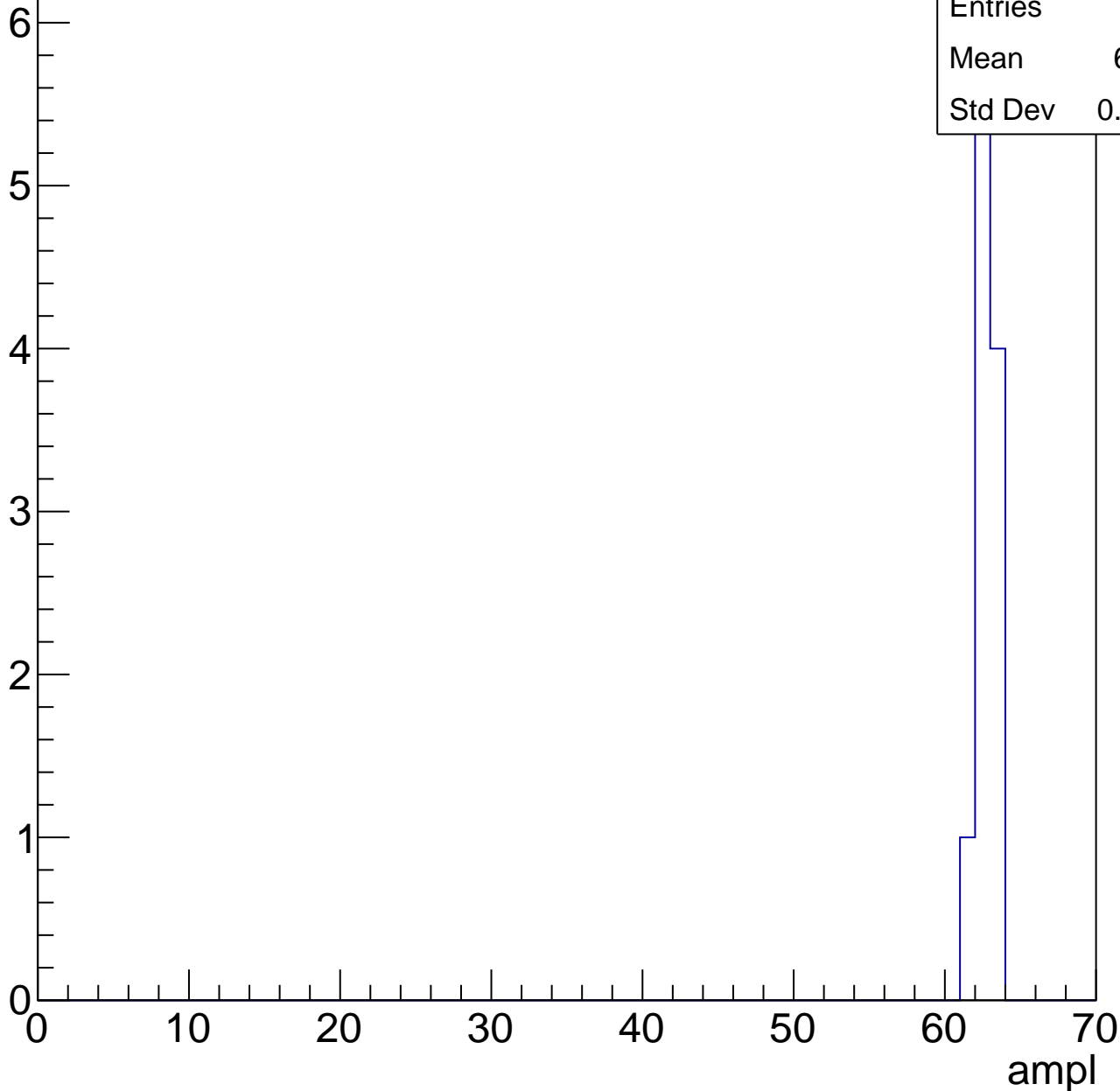


B1L003S, U19-ch13, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.27
Std Dev	0.6166



B1L003S, U19-ch13, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch14, adc0

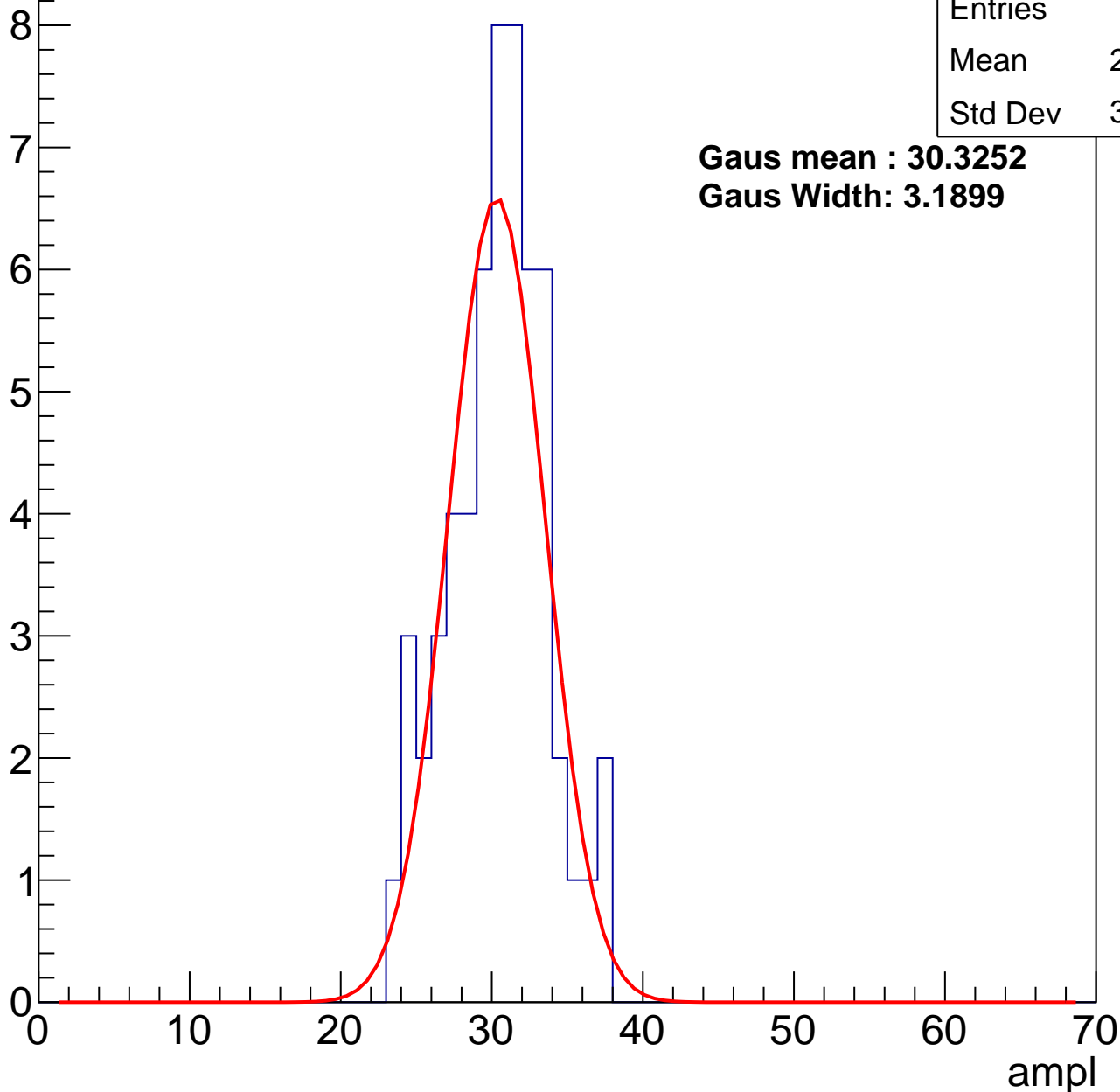
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.96
Std Dev	3.217

Gaus mean : 30.3252

Gaus Width: 3.1899



B1L003S, U19-ch14, adc1

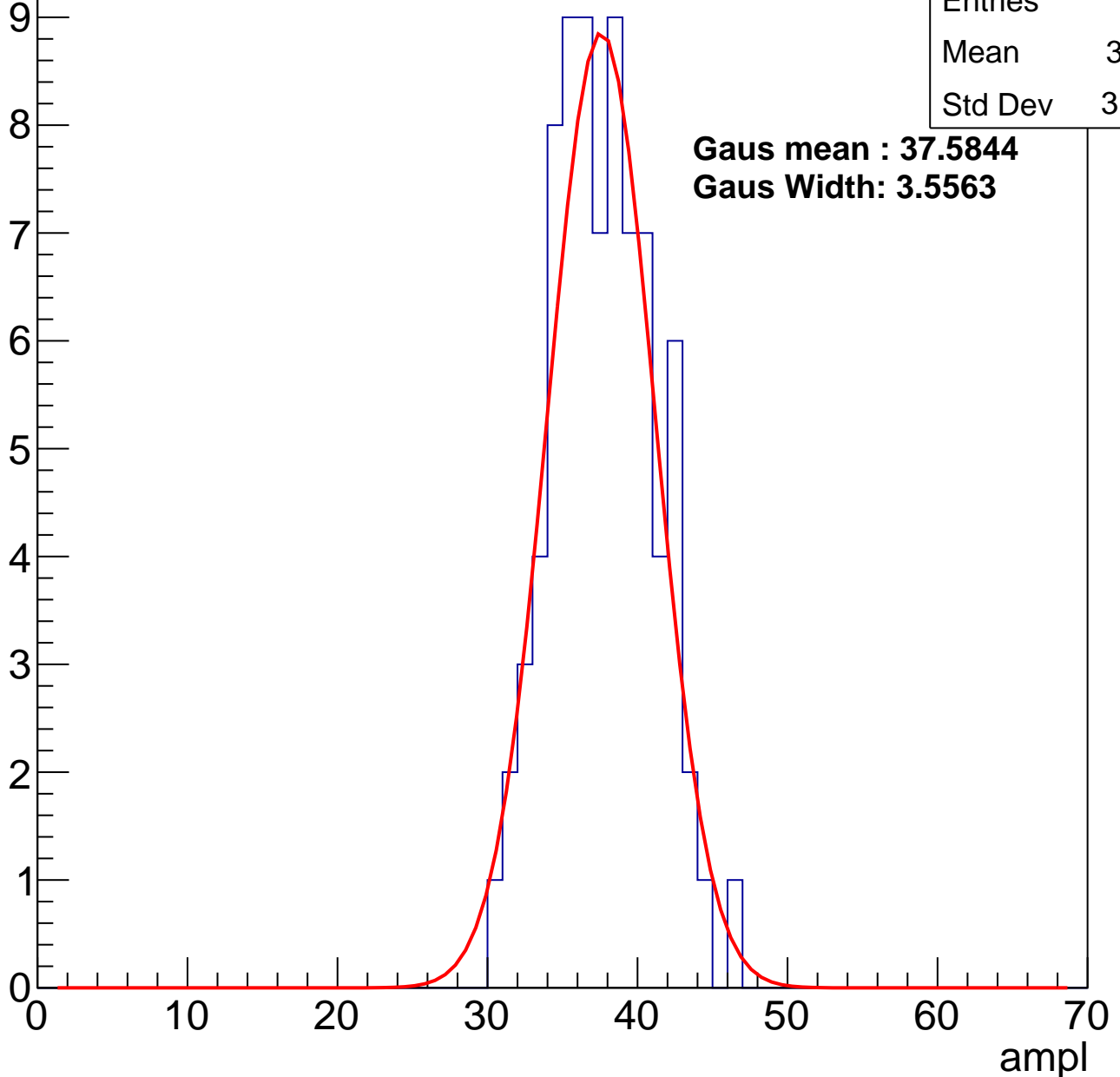
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	37.21
Std Dev	3.334

Gaus mean : 37.5844

Gaus Width: 3.5563



B1L003S, U19-ch14, adc2

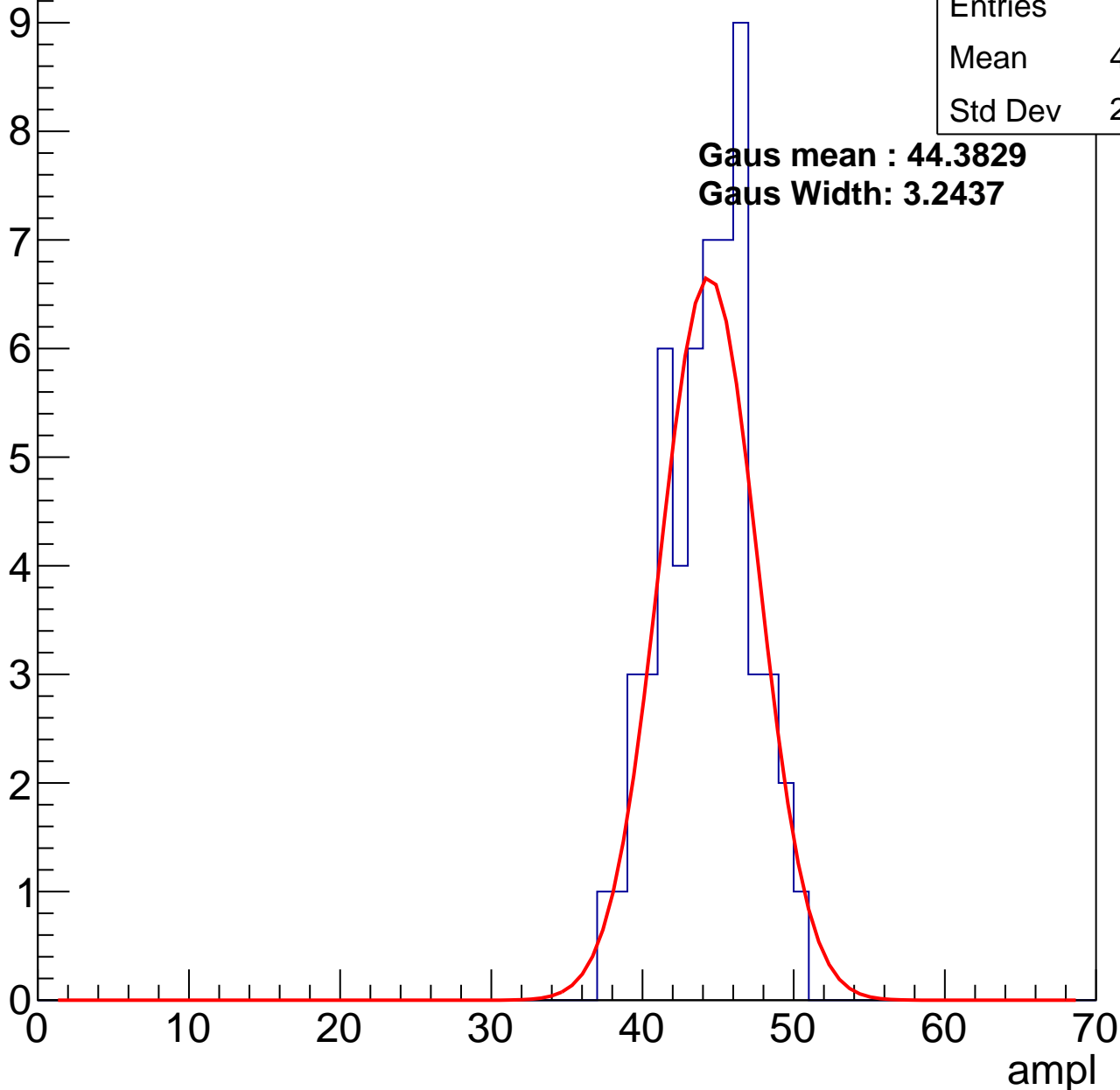
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.82
Std Dev	2.947

Gaus mean : 44.3829

Gaus Width: 3.2437

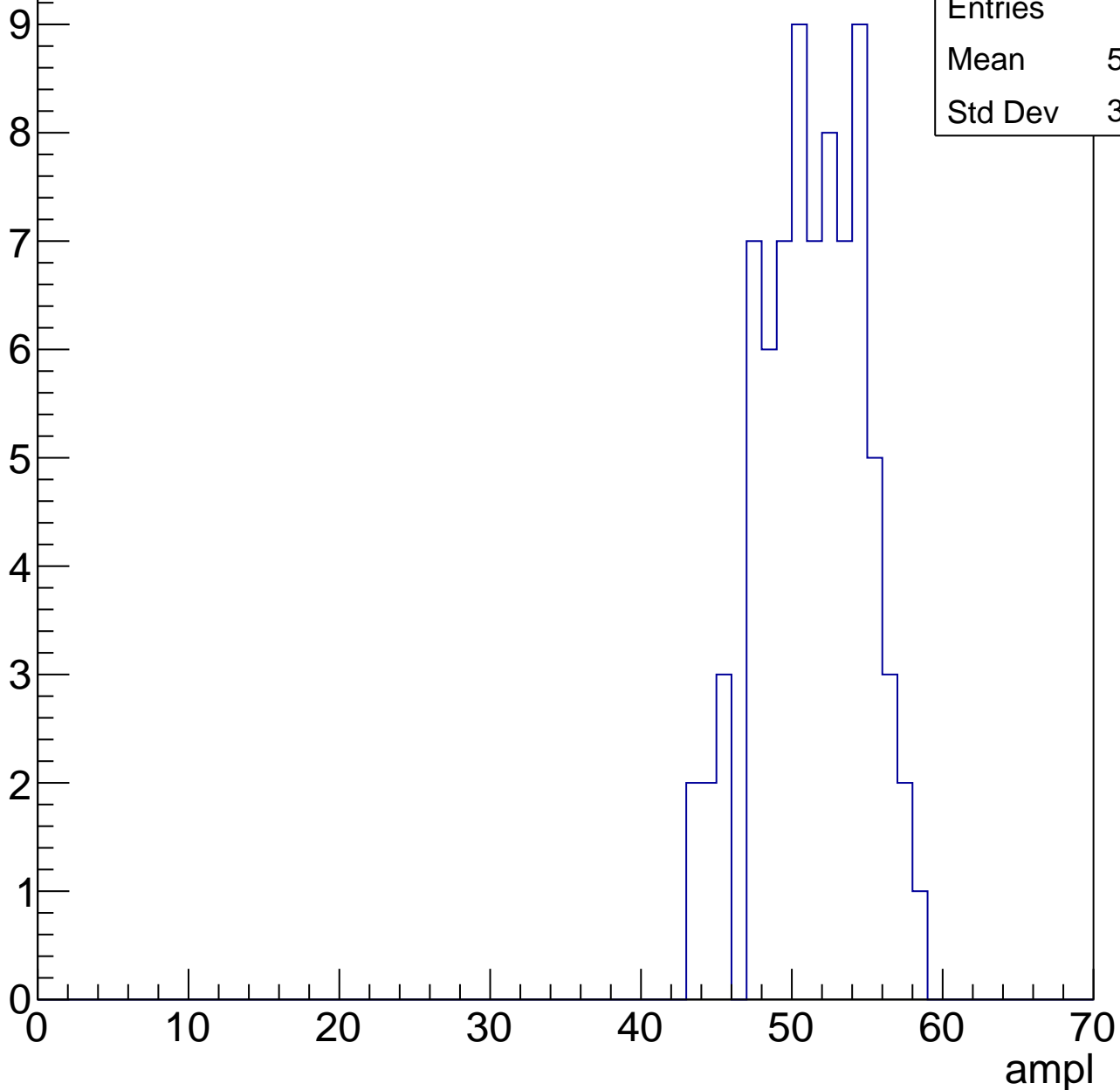


B1L003S, U19-ch14, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	50.82
Std Dev	3.448

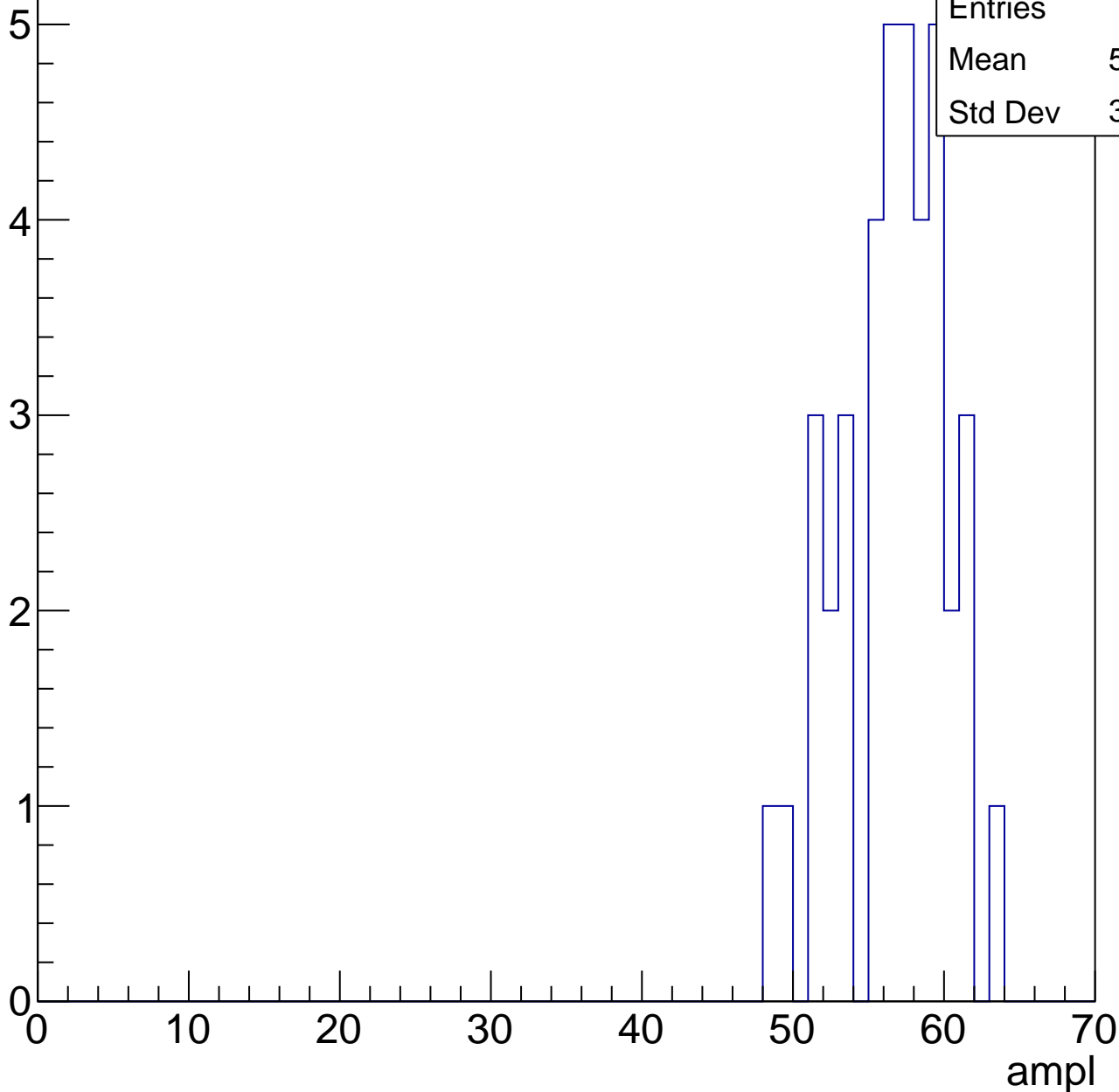


B1L003S, U19-ch14, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

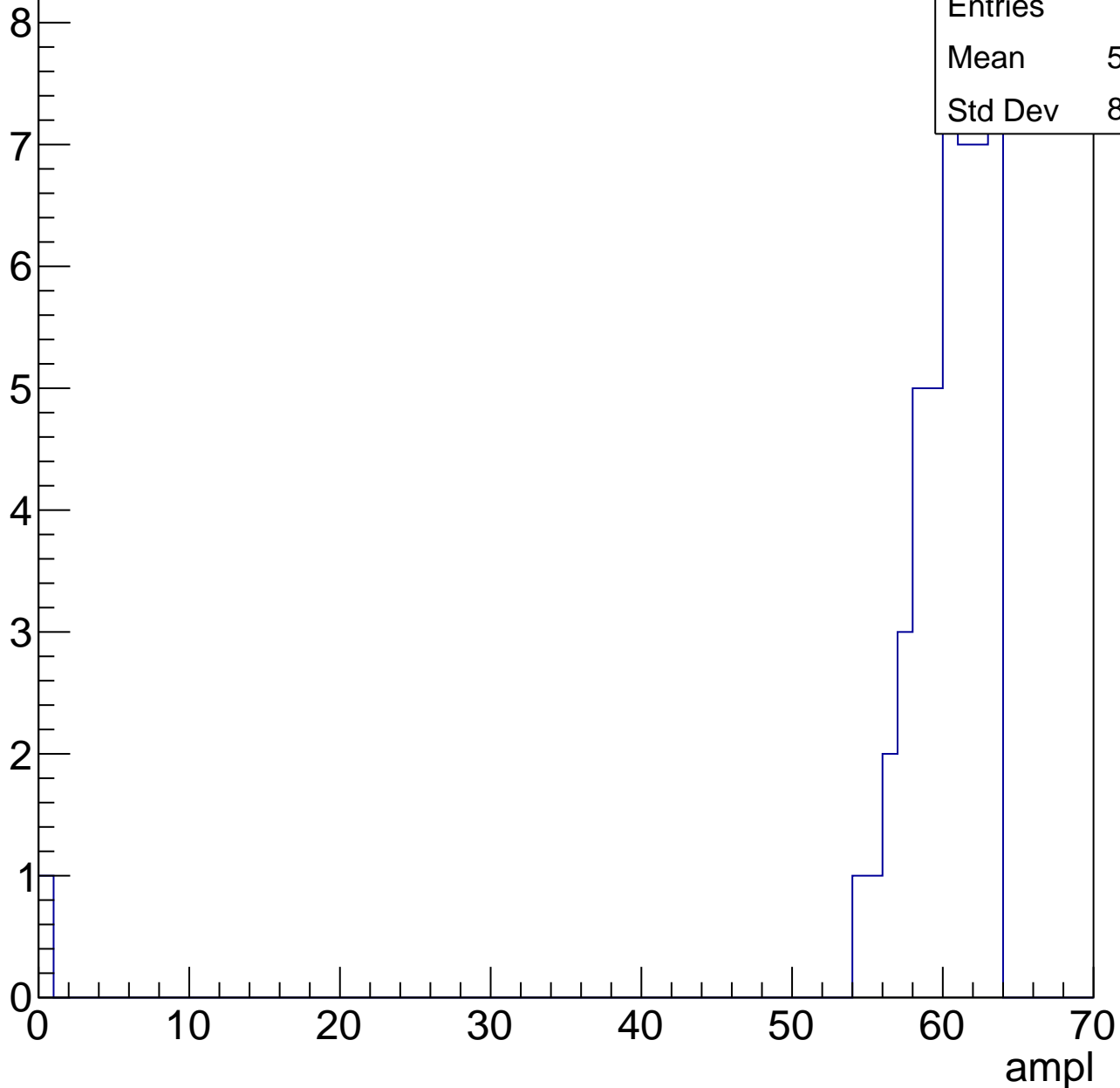
Entries	39
Mean	56.18
Std Dev	3.478



B1L003S, U19-ch14, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	48
Mean	58.79
Std Dev	8.876

B1L003S, U19-ch14, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	62.25
Std Dev	0.433

B1L003S, U19-ch14, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch15, adc0

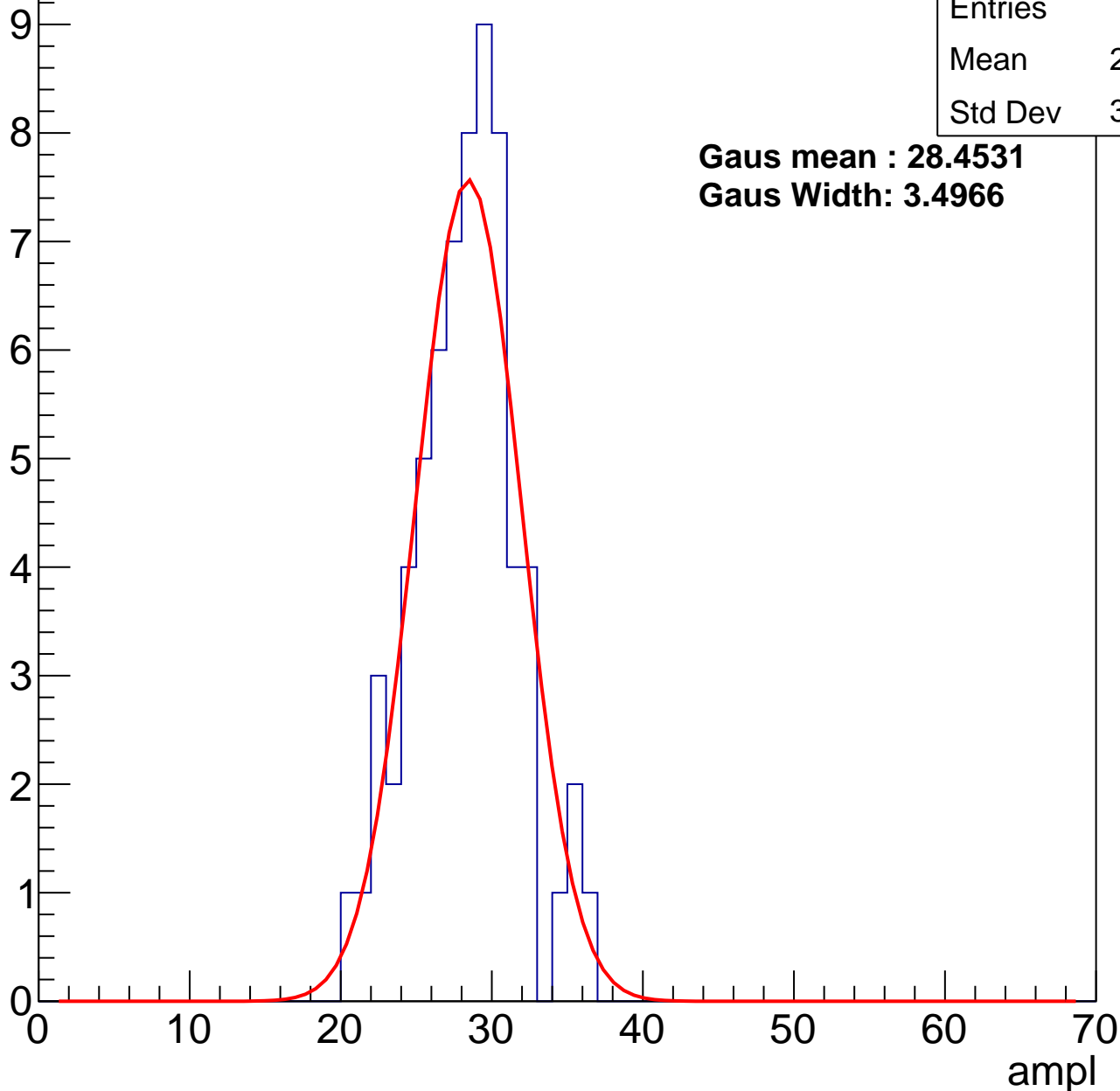
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	27.82
Std Dev	3.366

Gaus mean : 28.4531

Gaus Width: 3.4966



B1L003S, U19-ch15, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	82
Mean	35.21
Std Dev	3.523

Gaus mean : 35.9321

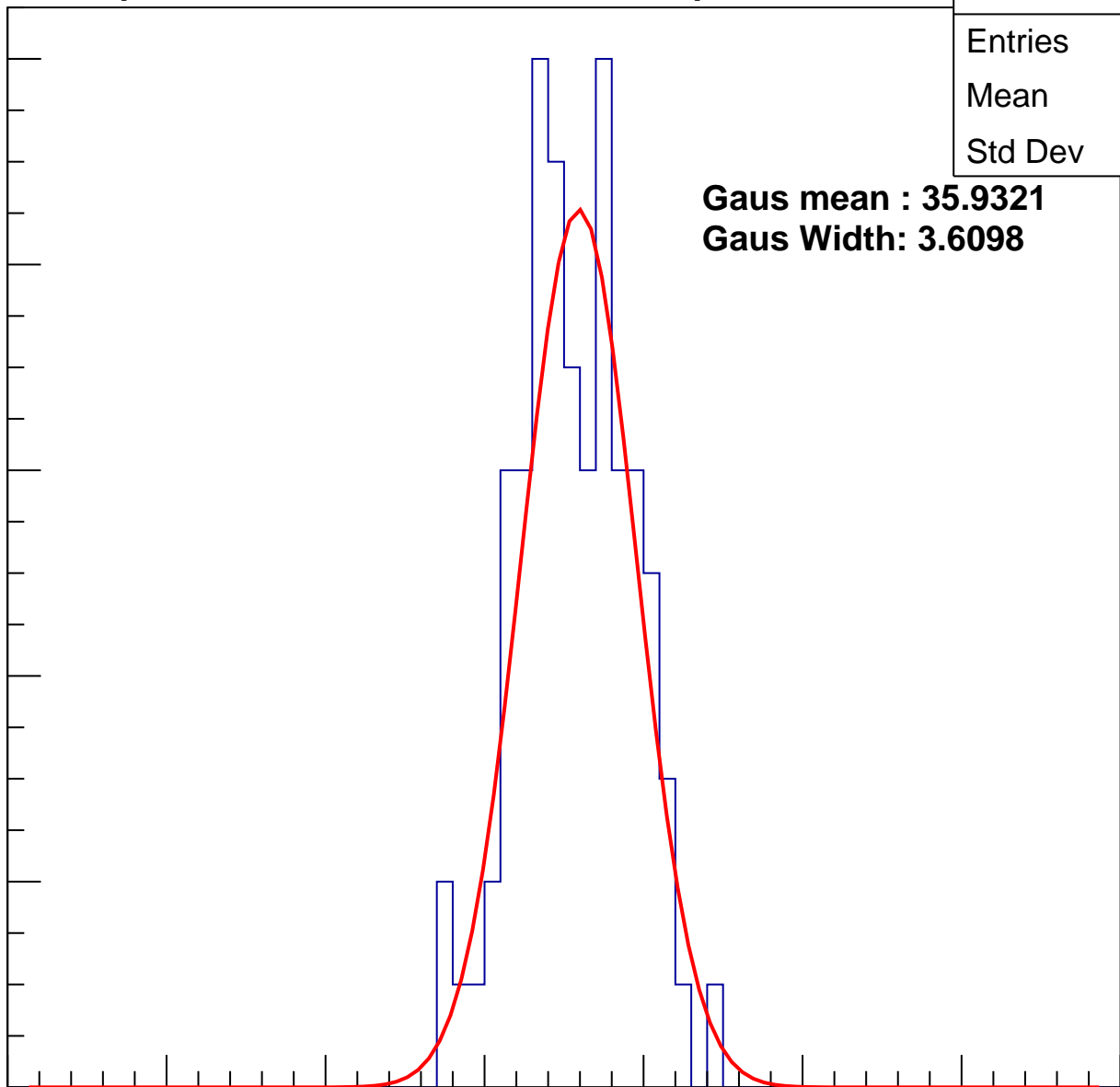
Gaus Width: 3.6098

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch15, adc2

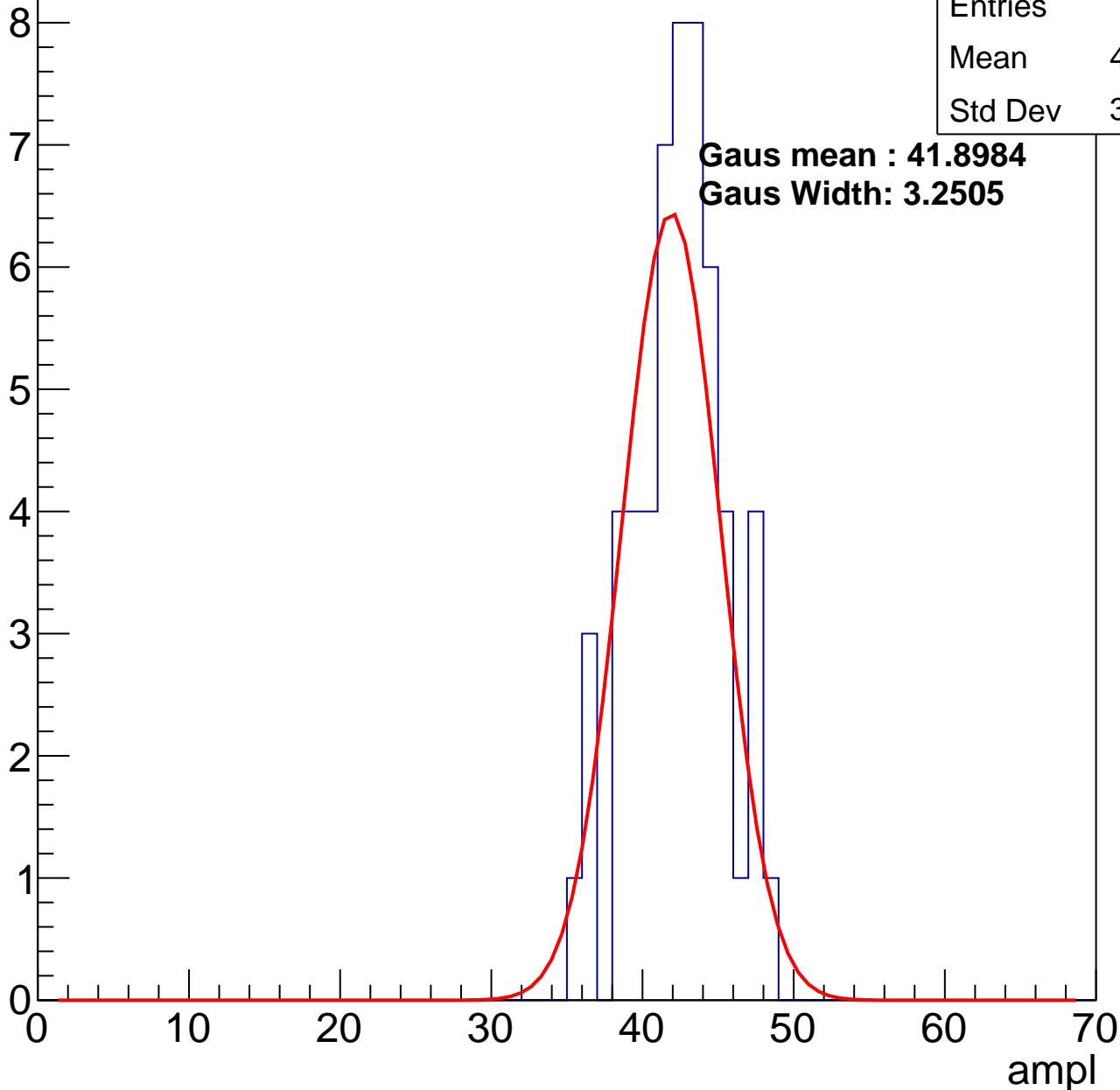
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	41.89
Std Dev	3.013

Gaus mean : 41.8984

Gaus Width: 3.2505

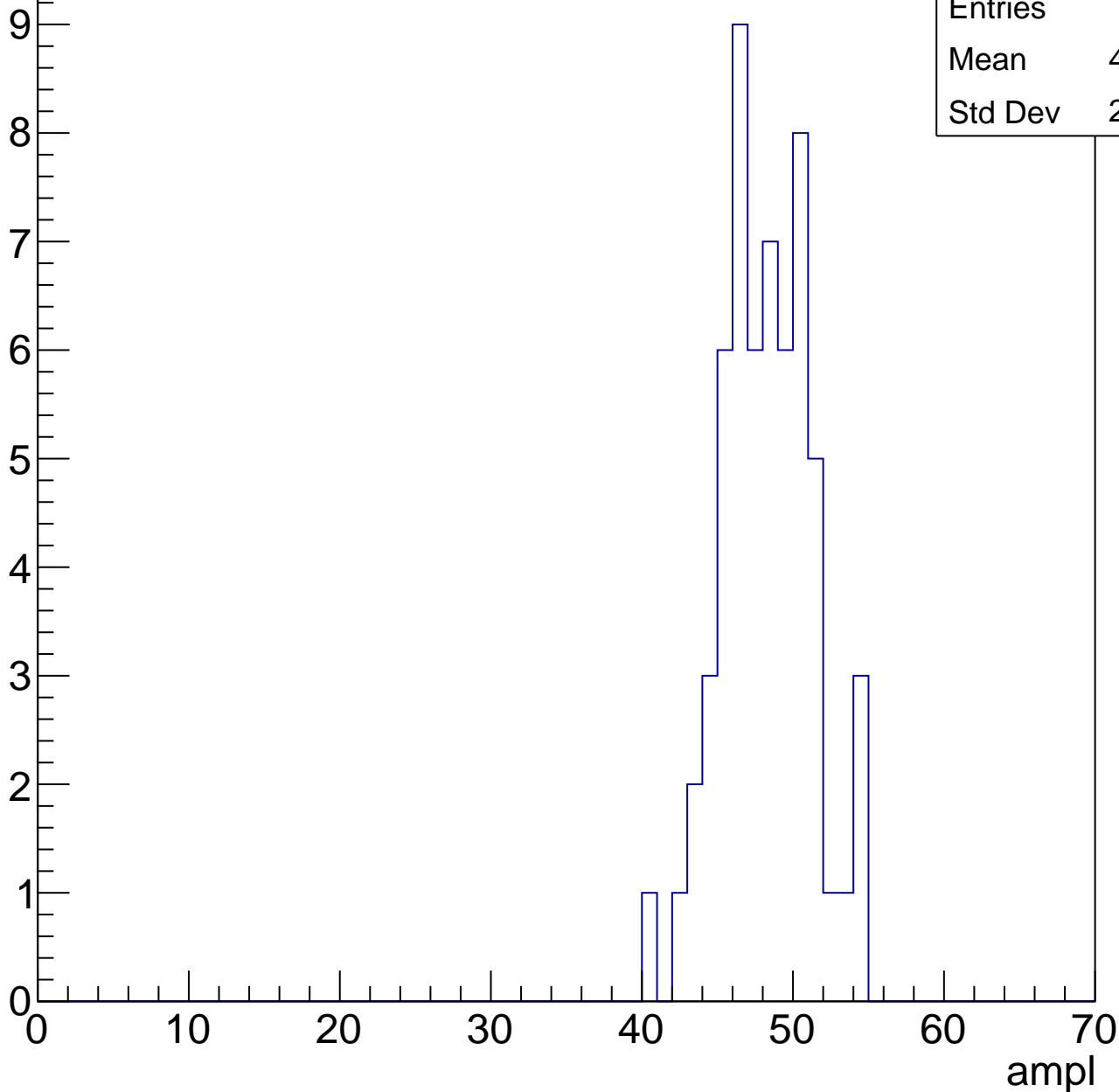


B1L003S, U19-ch15, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

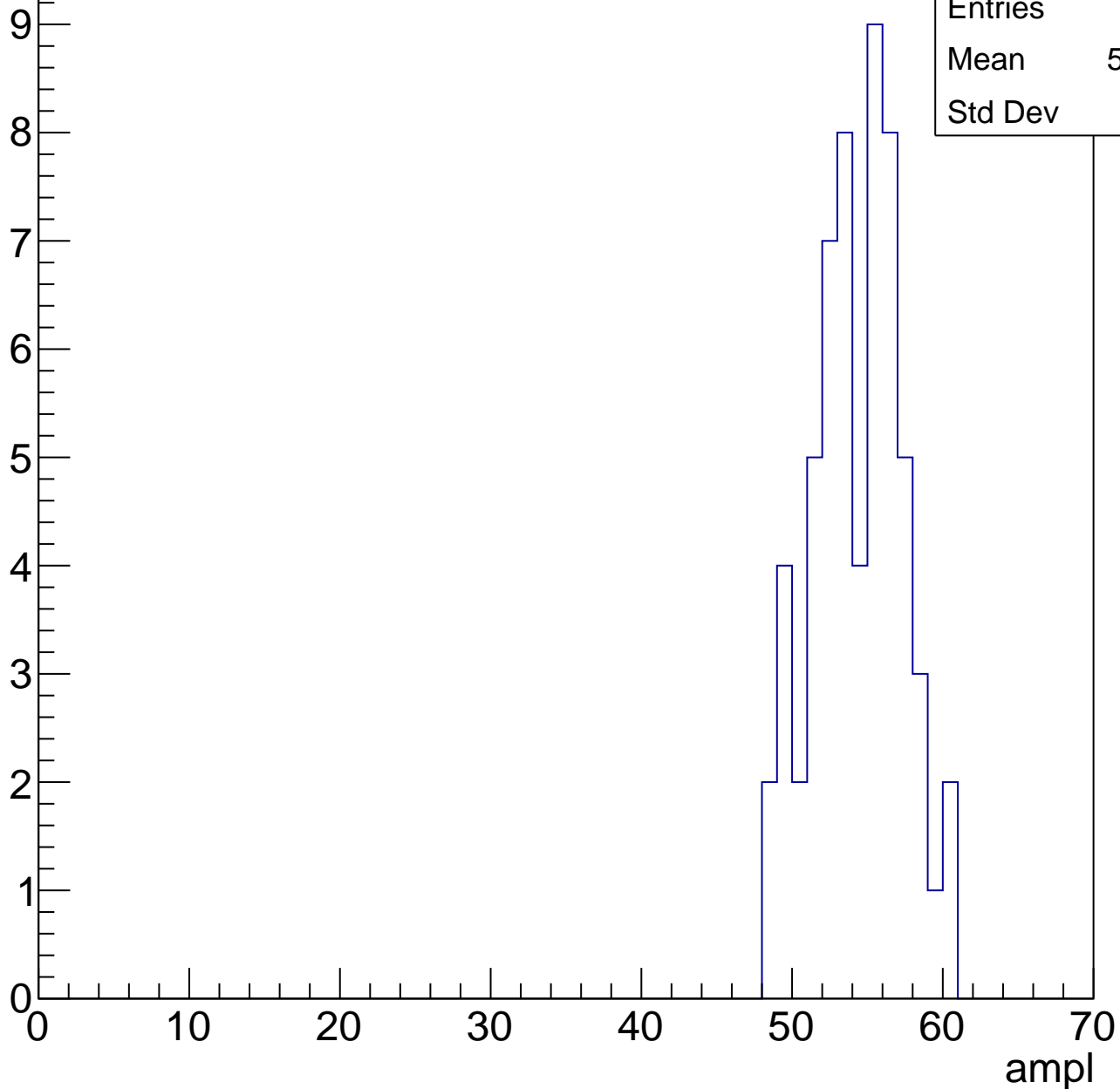
Entries	59
Mean	47.76
Std Dev	2.976



B1L003S, U19-ch15, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

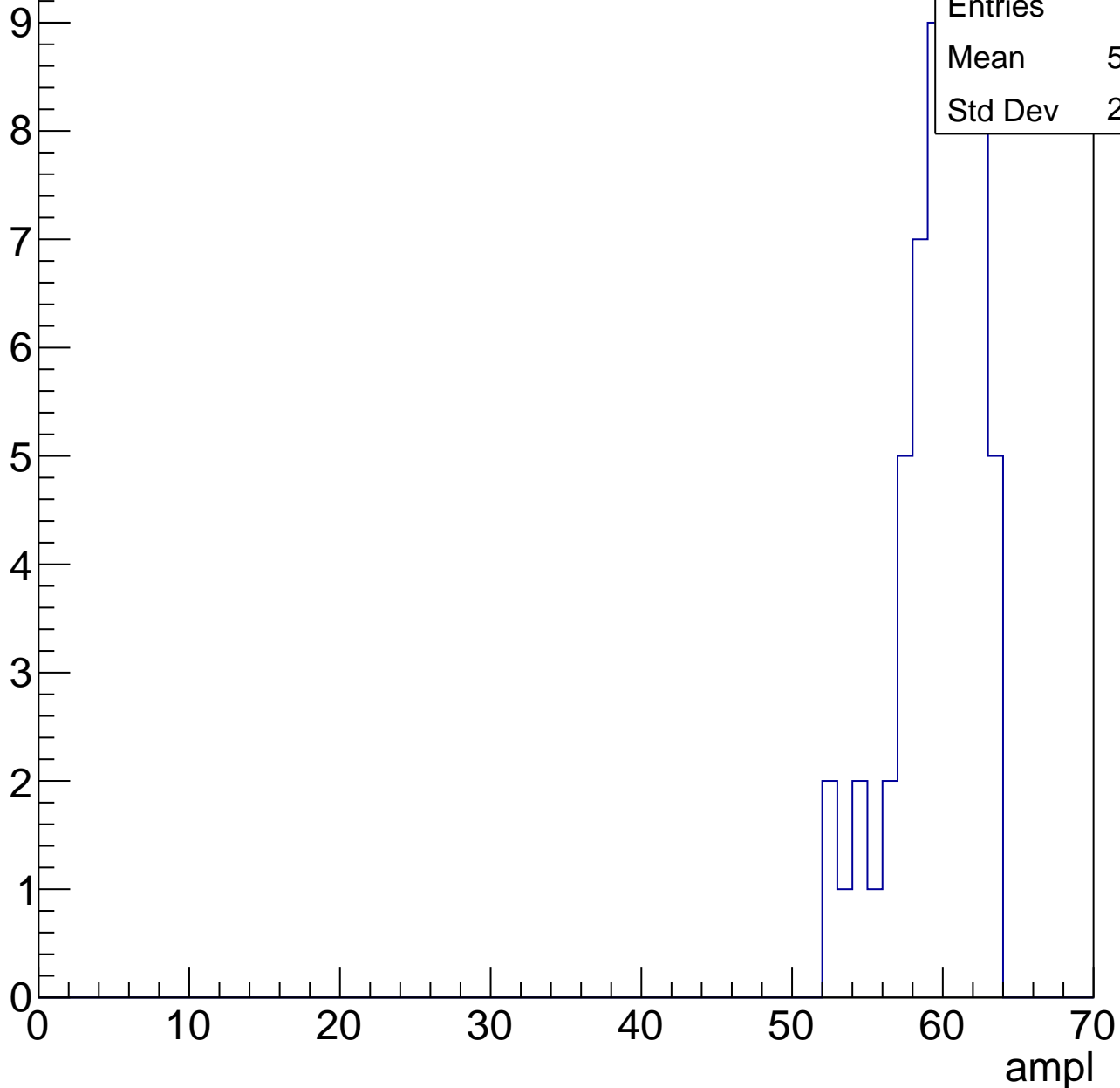


Entries	60
Mean	53.87
Std Dev	2.93

B1L003S, U19-ch15, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

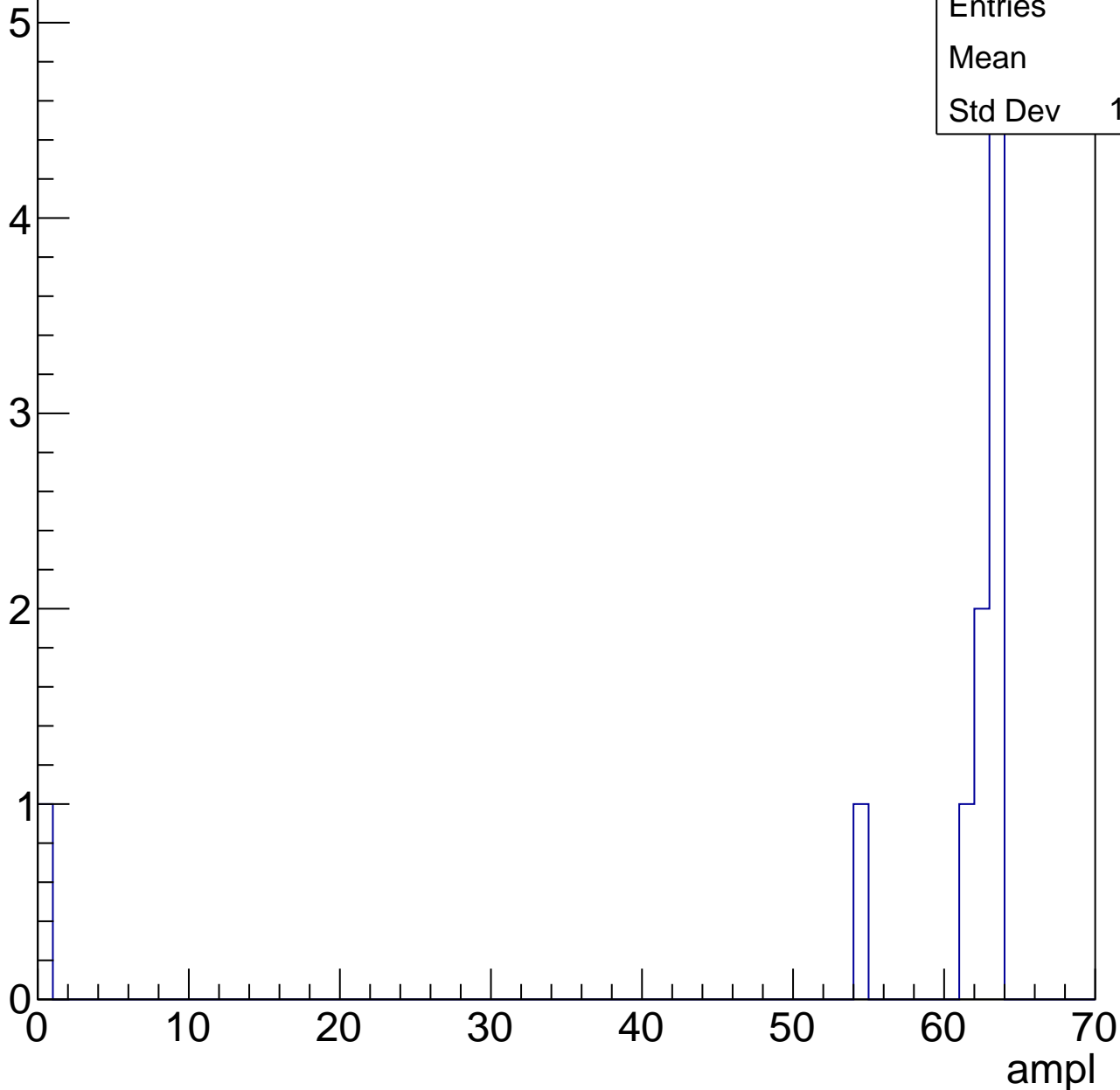


B1L003S, U19-ch15, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	55.4
Std Dev	18.65



B1L003S, U19-ch15, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch16, adc0

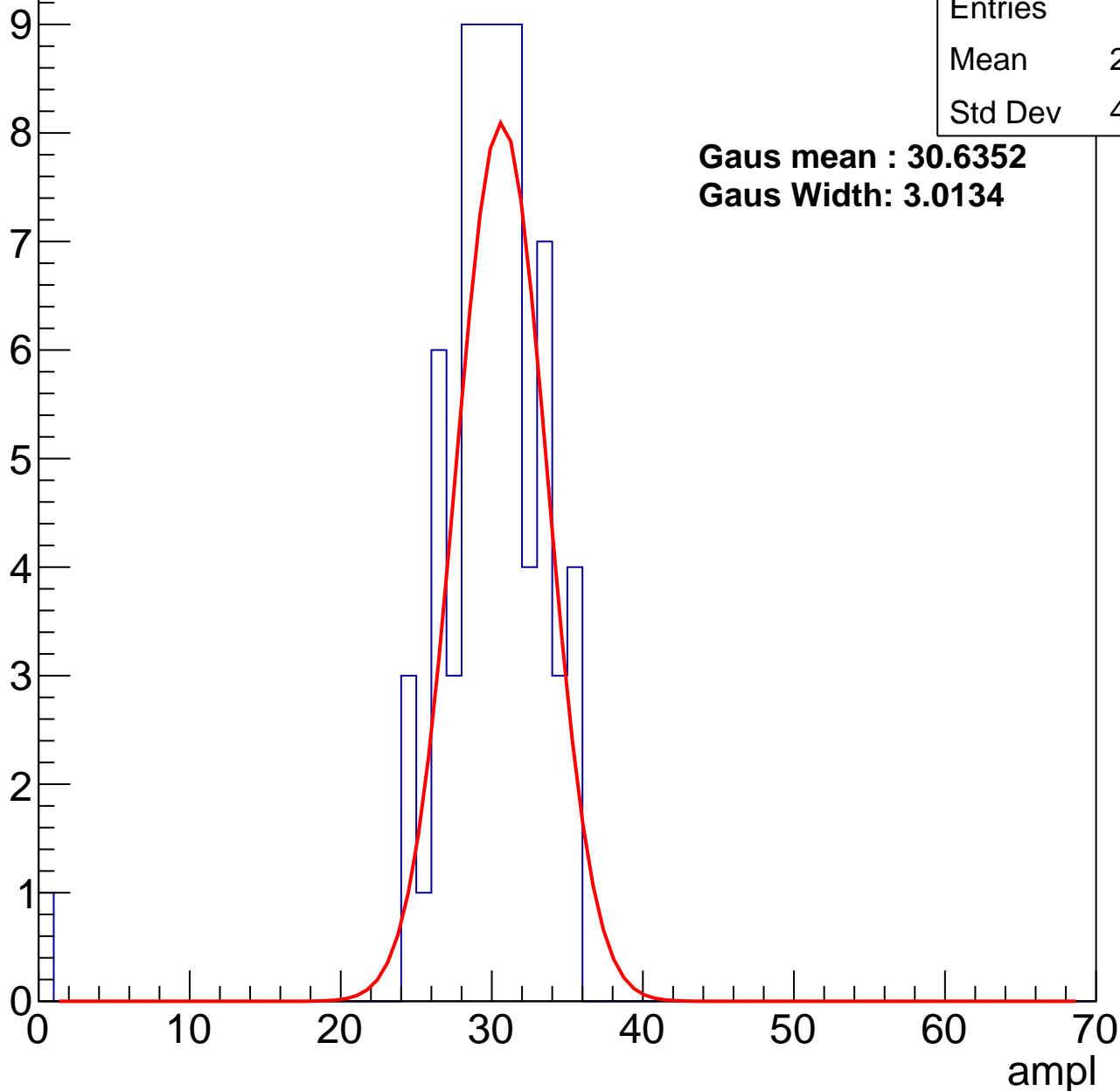
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.37
Std Dev	4.553

Gaus mean : 30.6352

Gaus Width: 3.0134

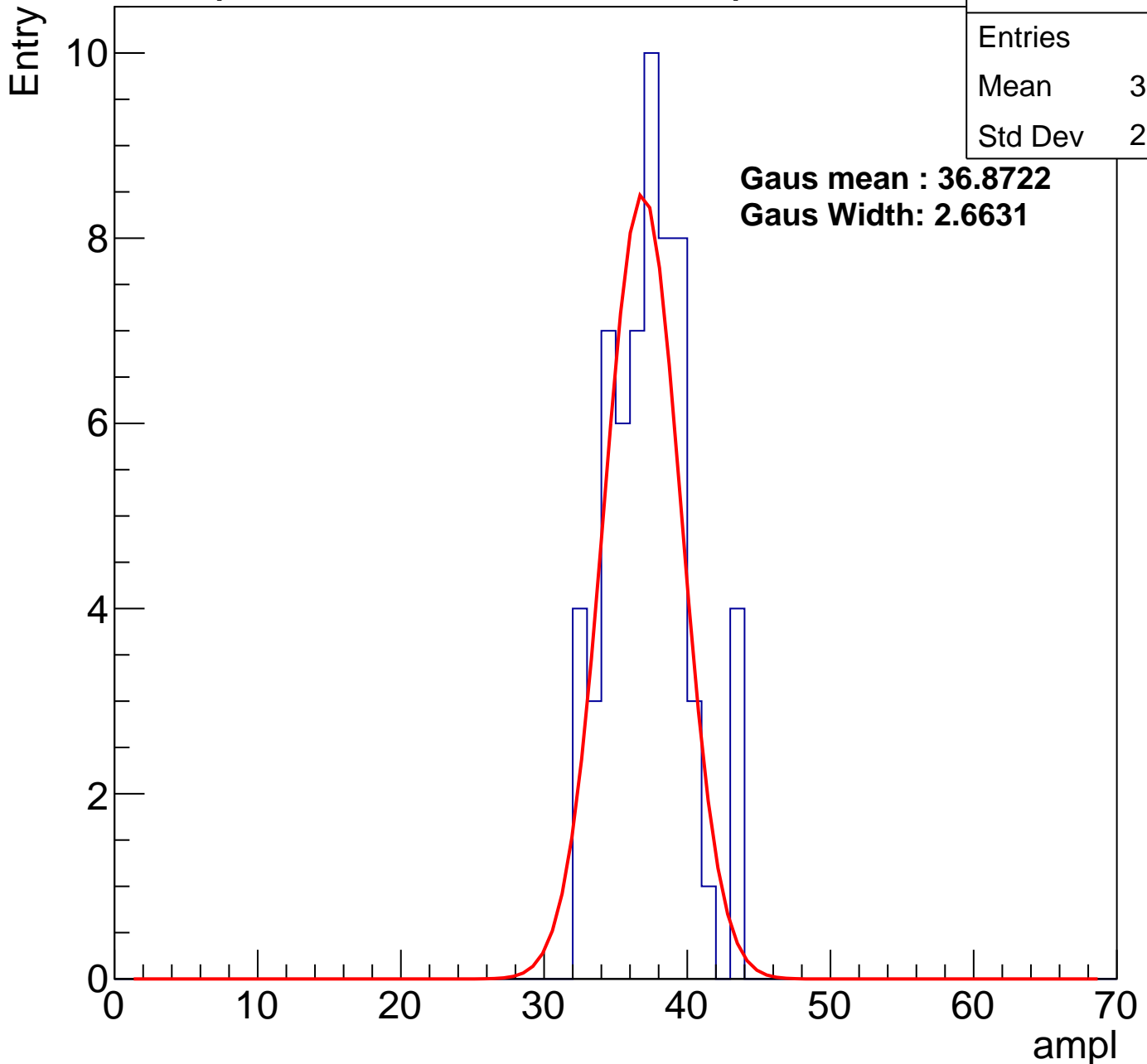


B1L003S, U19-ch16, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	36.82
Std Dev	2.767

Gaus mean : 36.8722
Gaus Width: 2.6631



B1L003S, U19-ch16, adc2

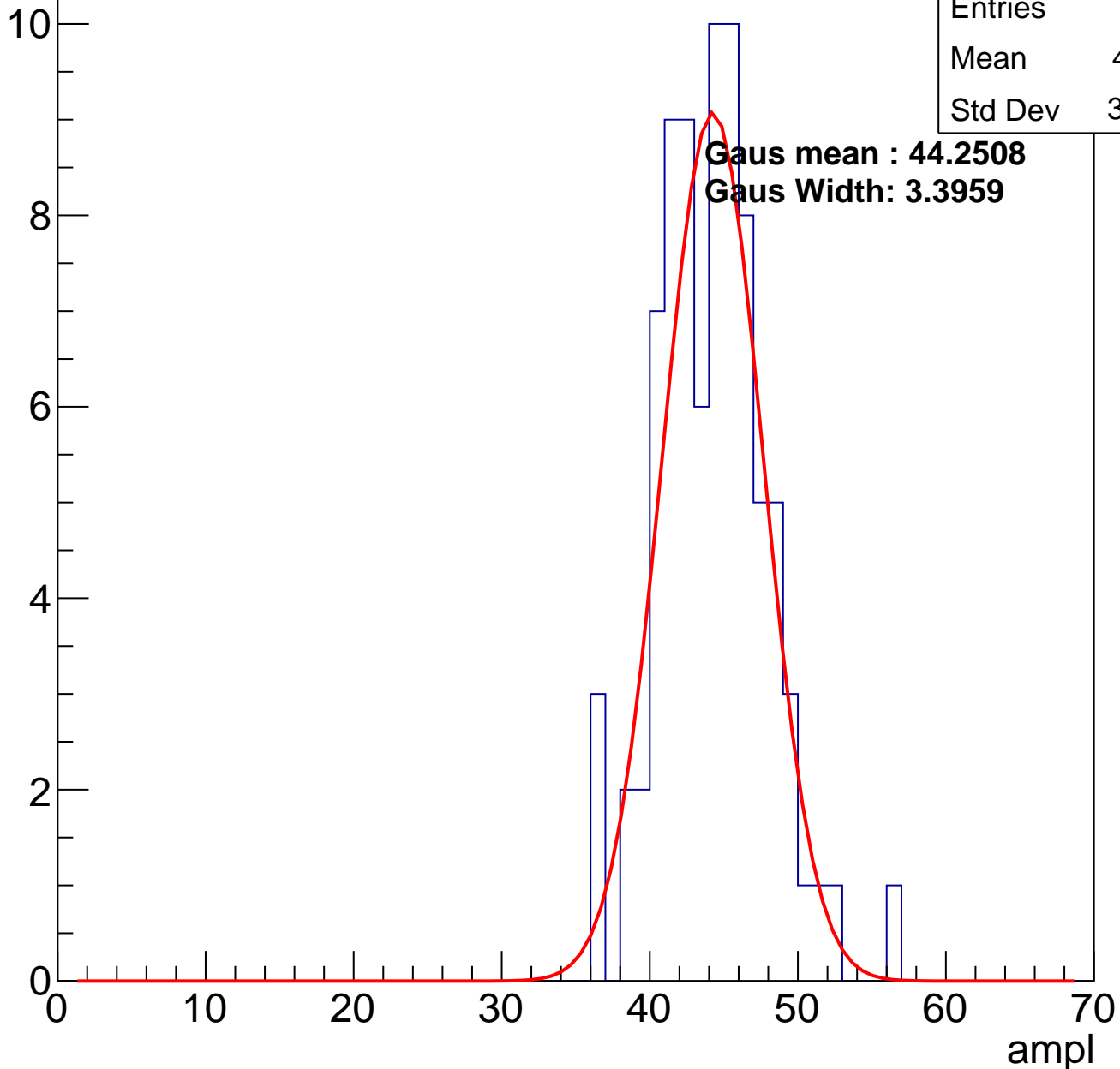
calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	43.81
Std Dev	3.609

Gaus mean : 44.2508

Gaus Width: 3.3959

Entry

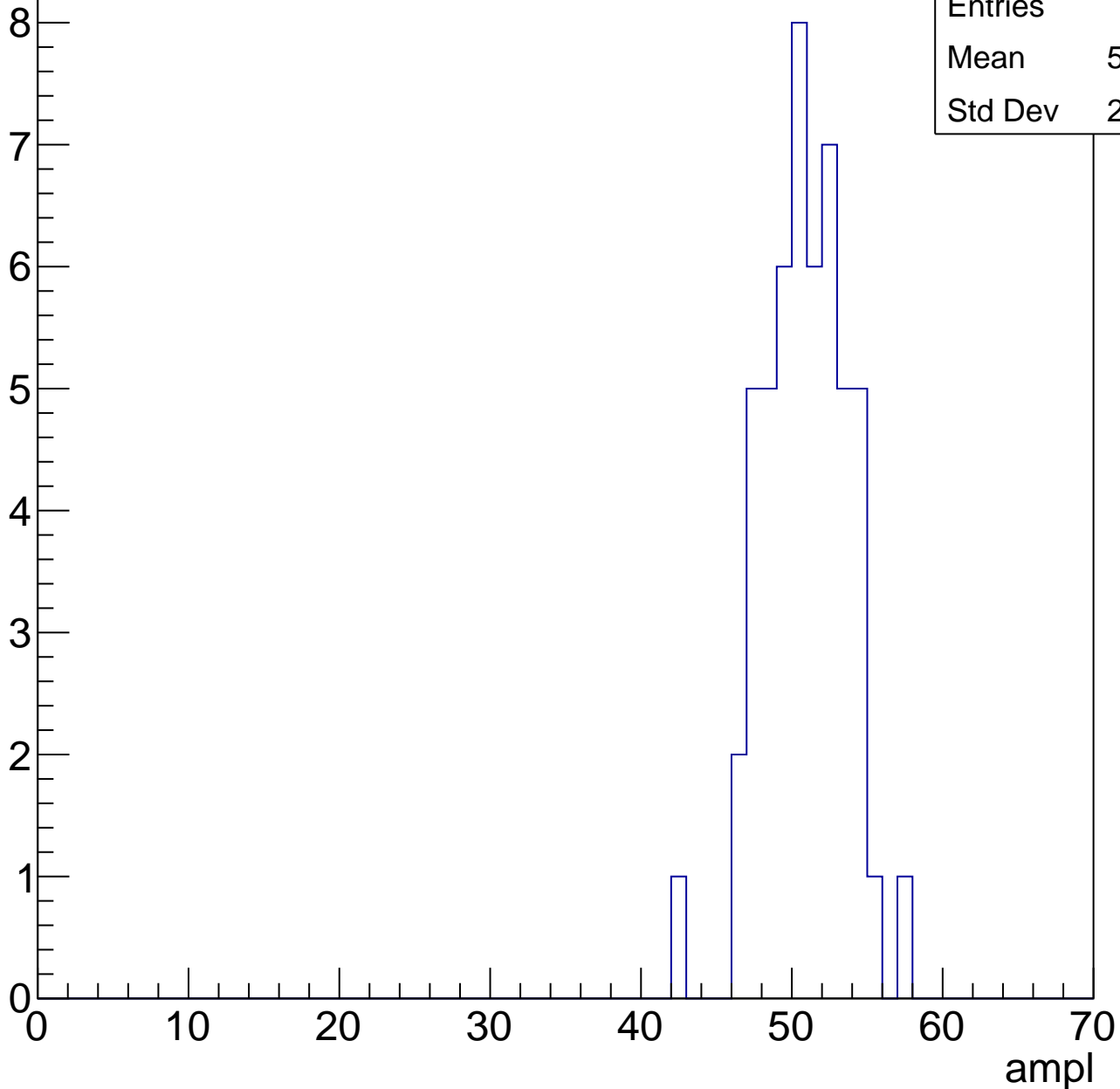


B1L003S, U19-ch16, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	50.38
Std Dev	2.747

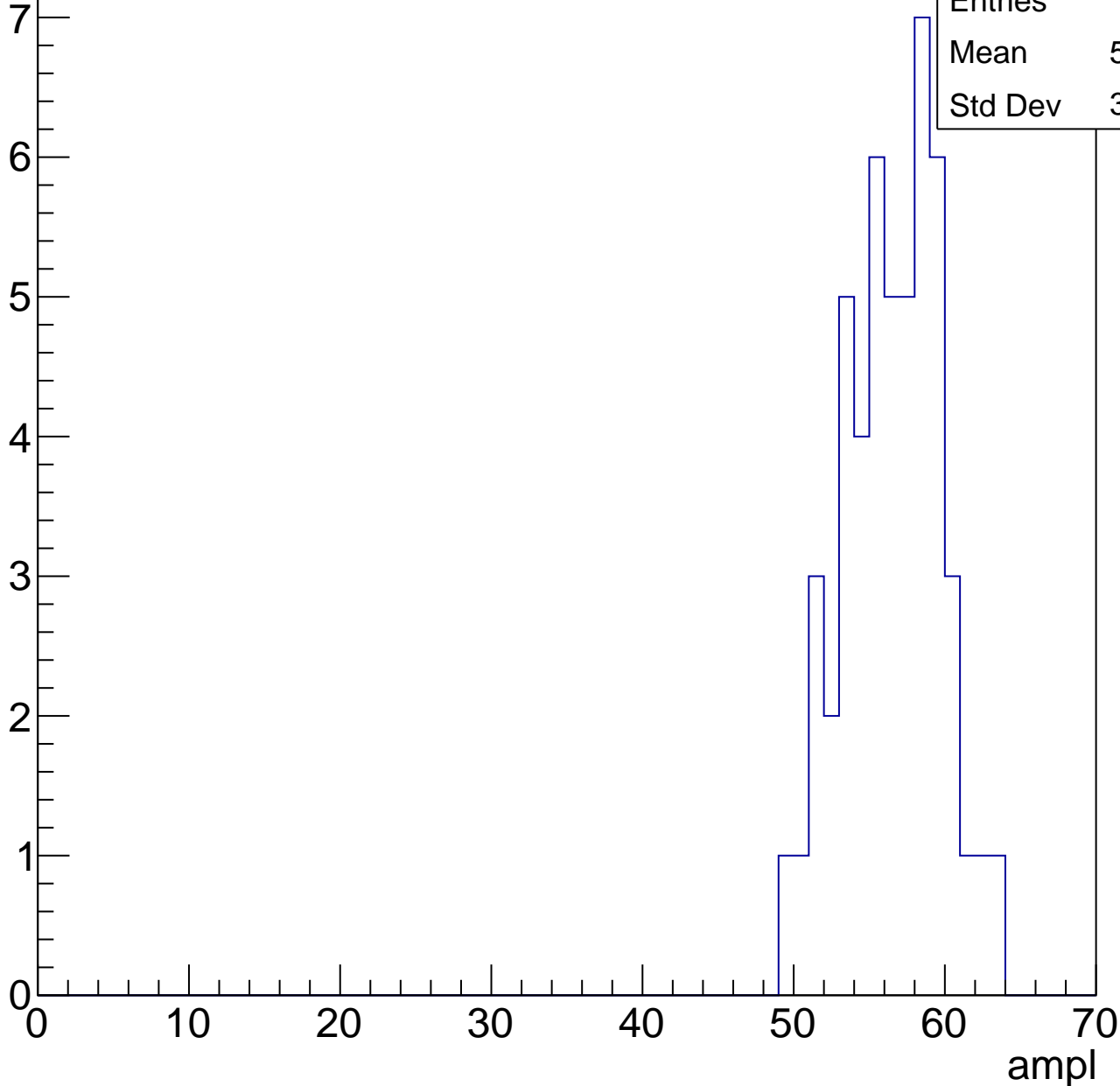


B1L003S, U19-ch16, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.04
Std Dev	3.143



B1L003S, U19-ch16, adc5

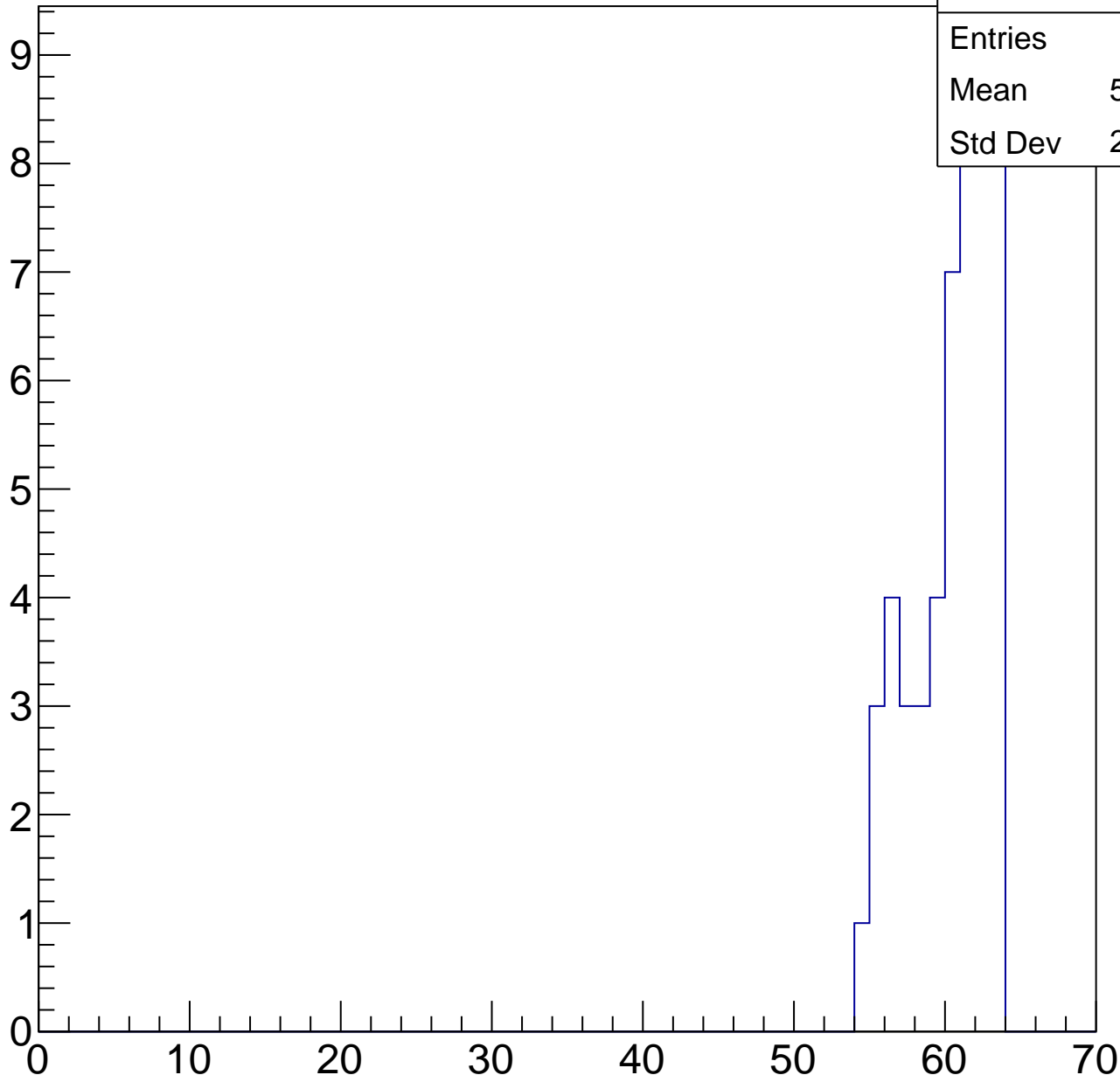
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.88
Std Dev	2.582

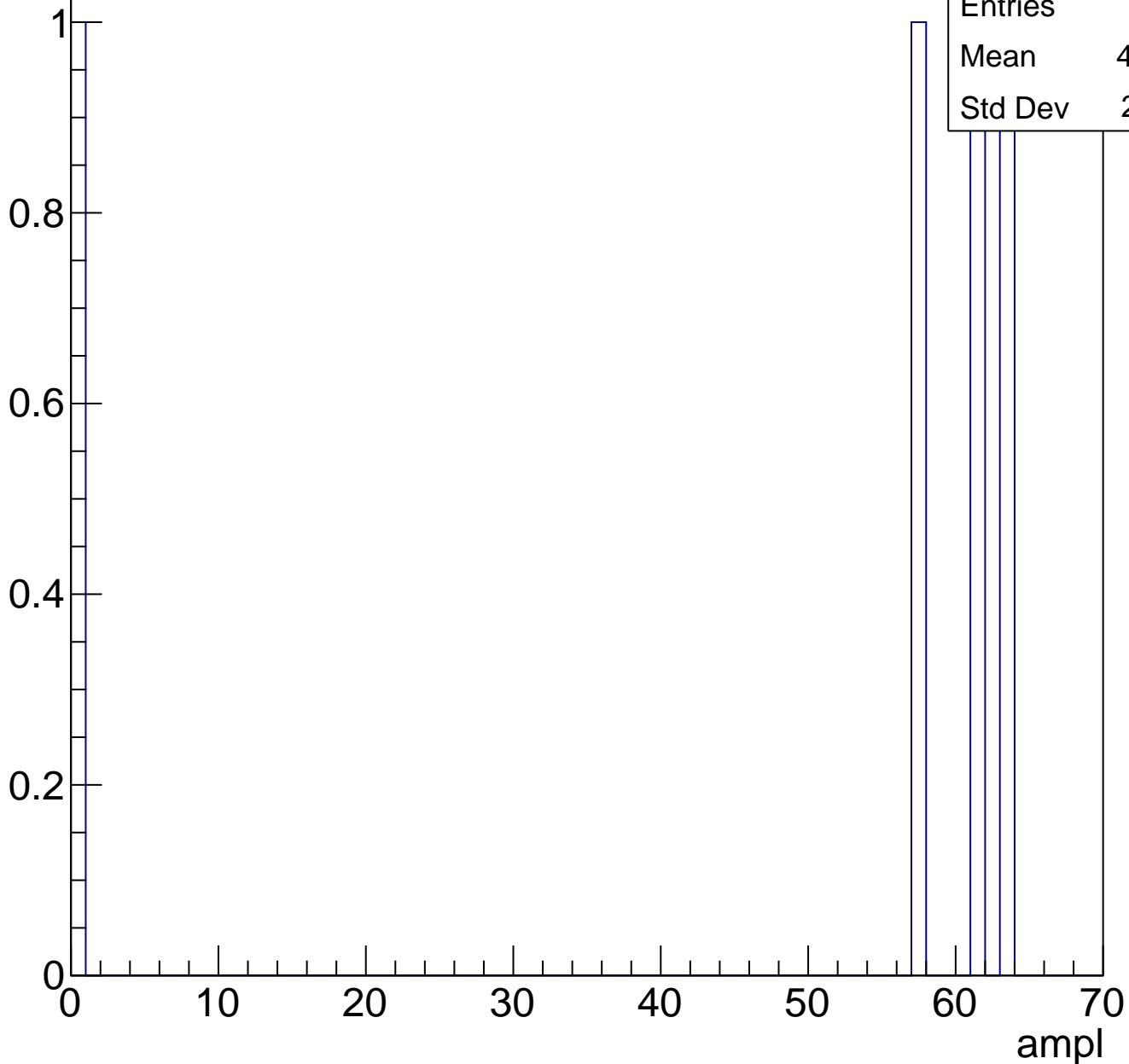
ampl



B1L003S, U19-ch16, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch16, adc7

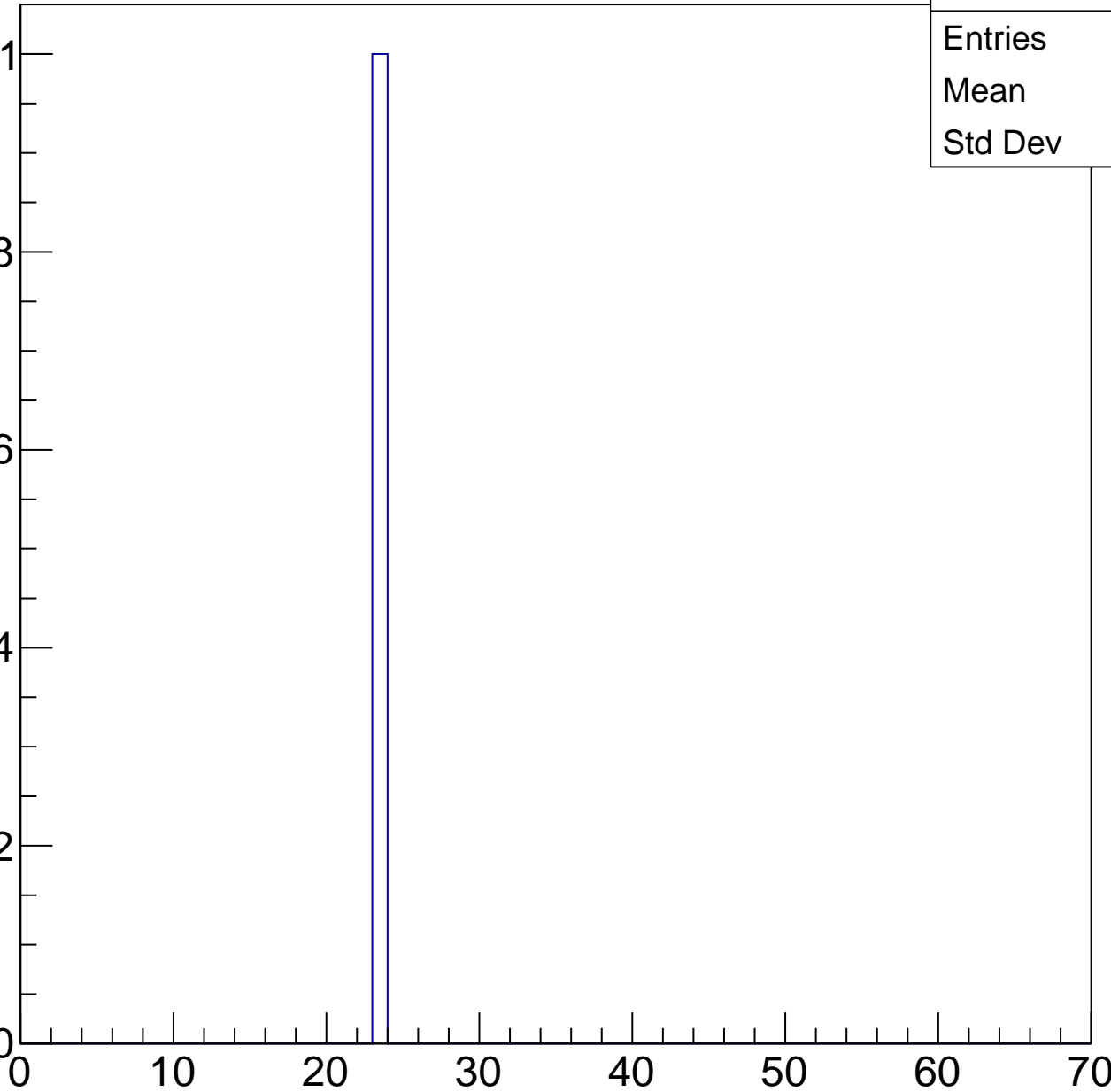
calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B1L003S, U19-ch17, adc0

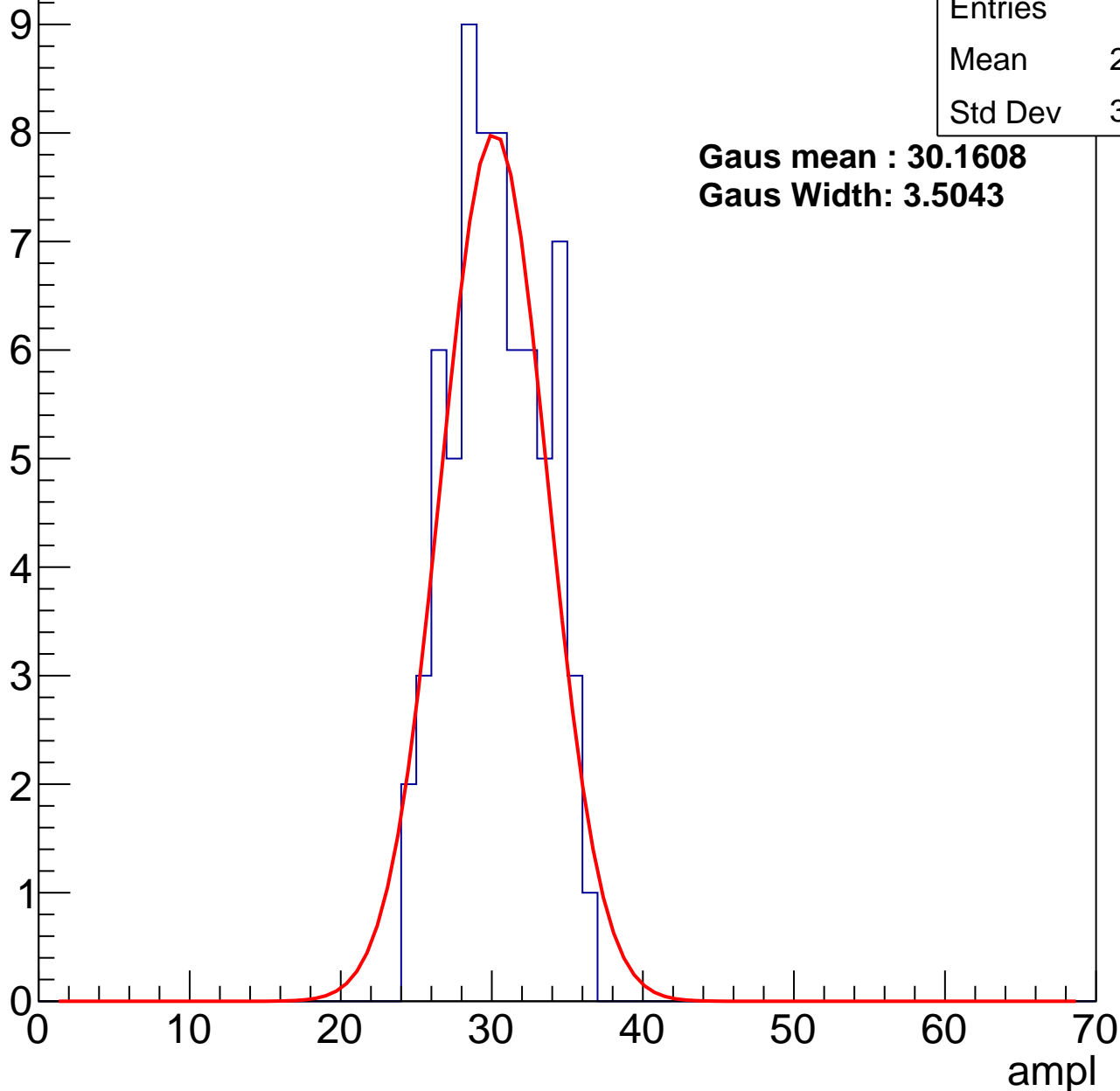
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.86
Std Dev	3.018

Gaus mean : 30.1608

Gaus Width: 3.5043



B1L003S, U19-ch17, adc1

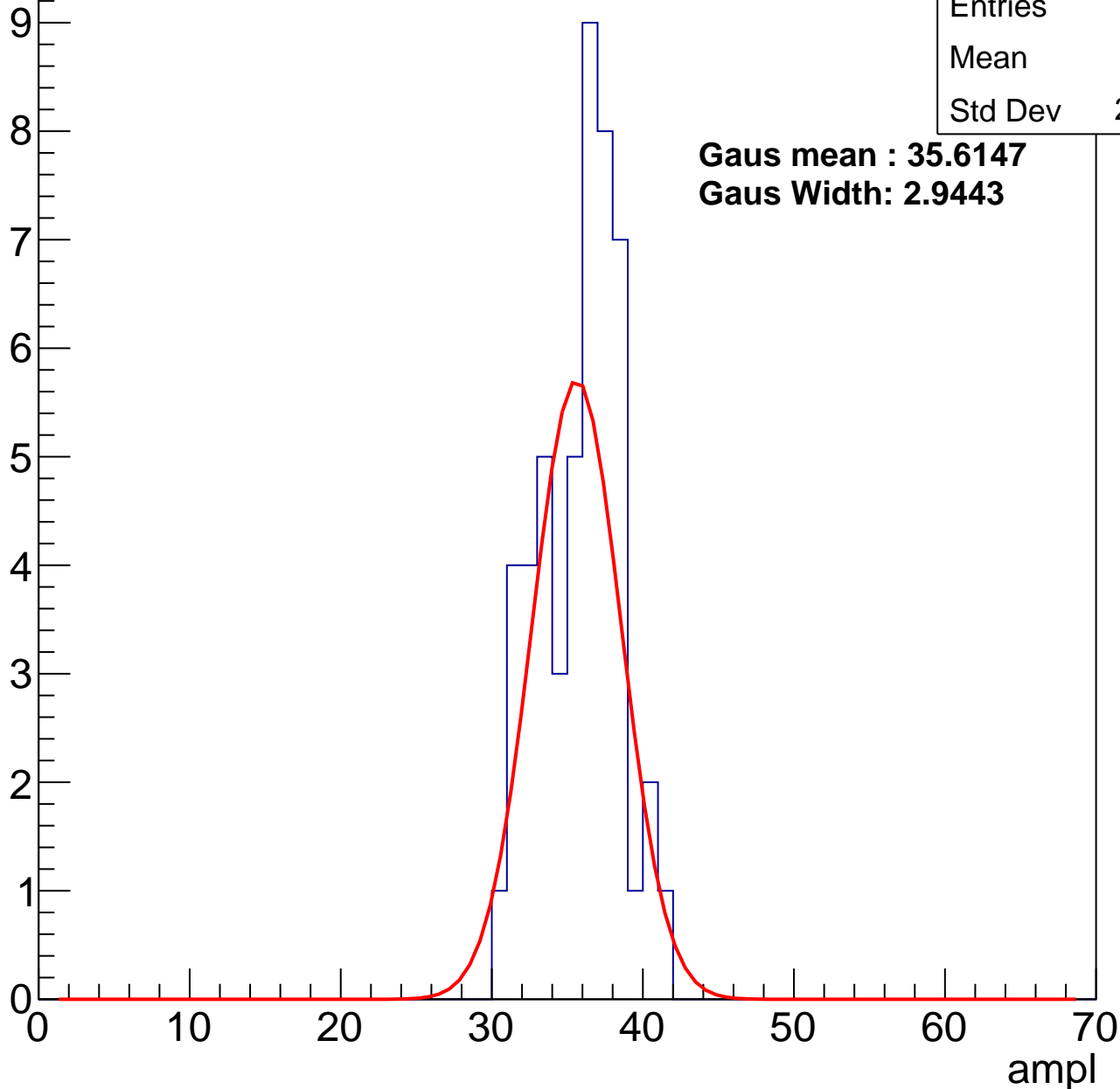
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	35.4
Std Dev	2.631

Gaus mean : 35.6147

Gaus Width: 2.9443



B1L003S, U19-ch17, adc2

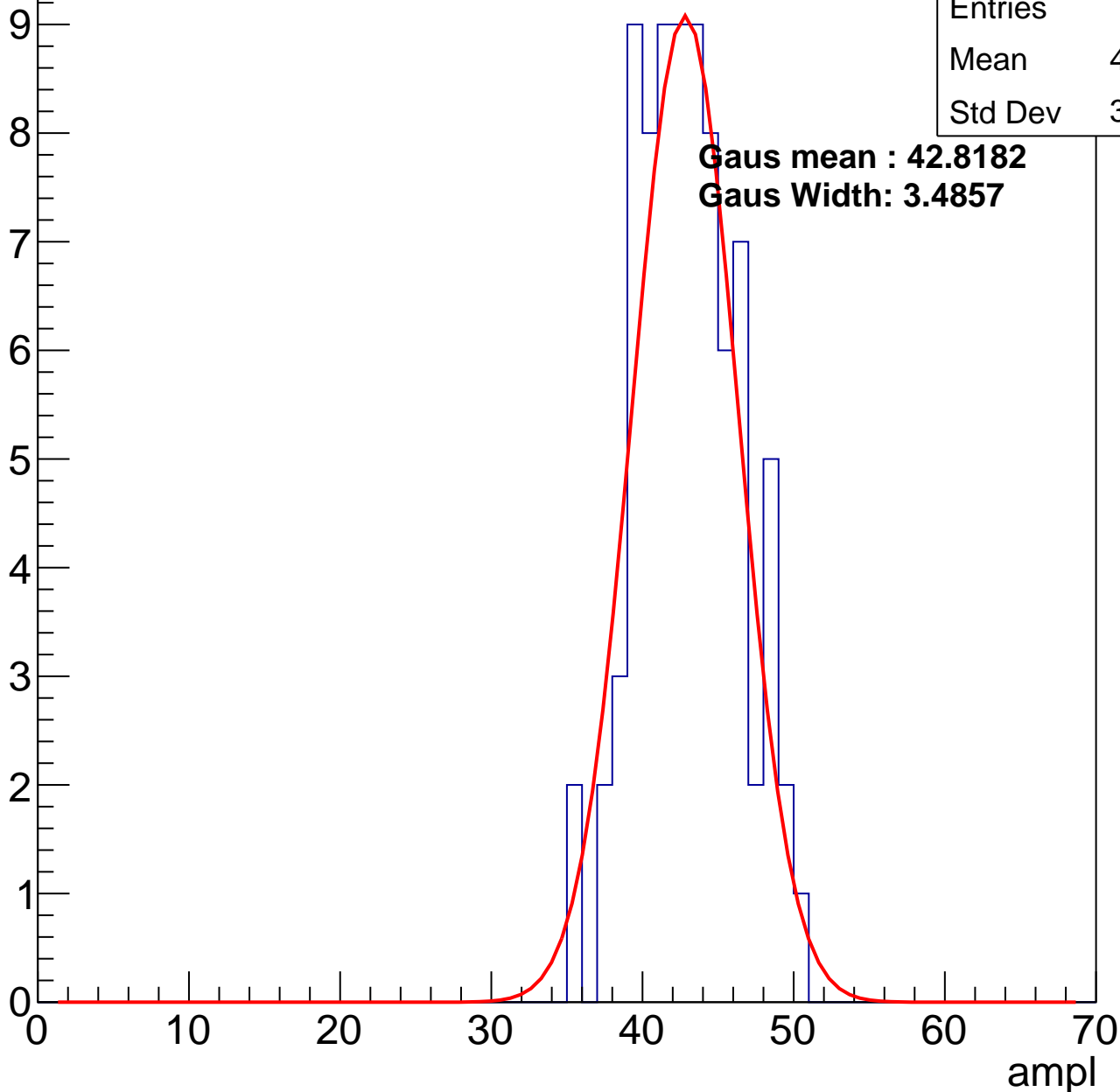
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	42.55
Std Dev	3.299

Gaus mean : 42.8182

Gaus Width: 3.4857

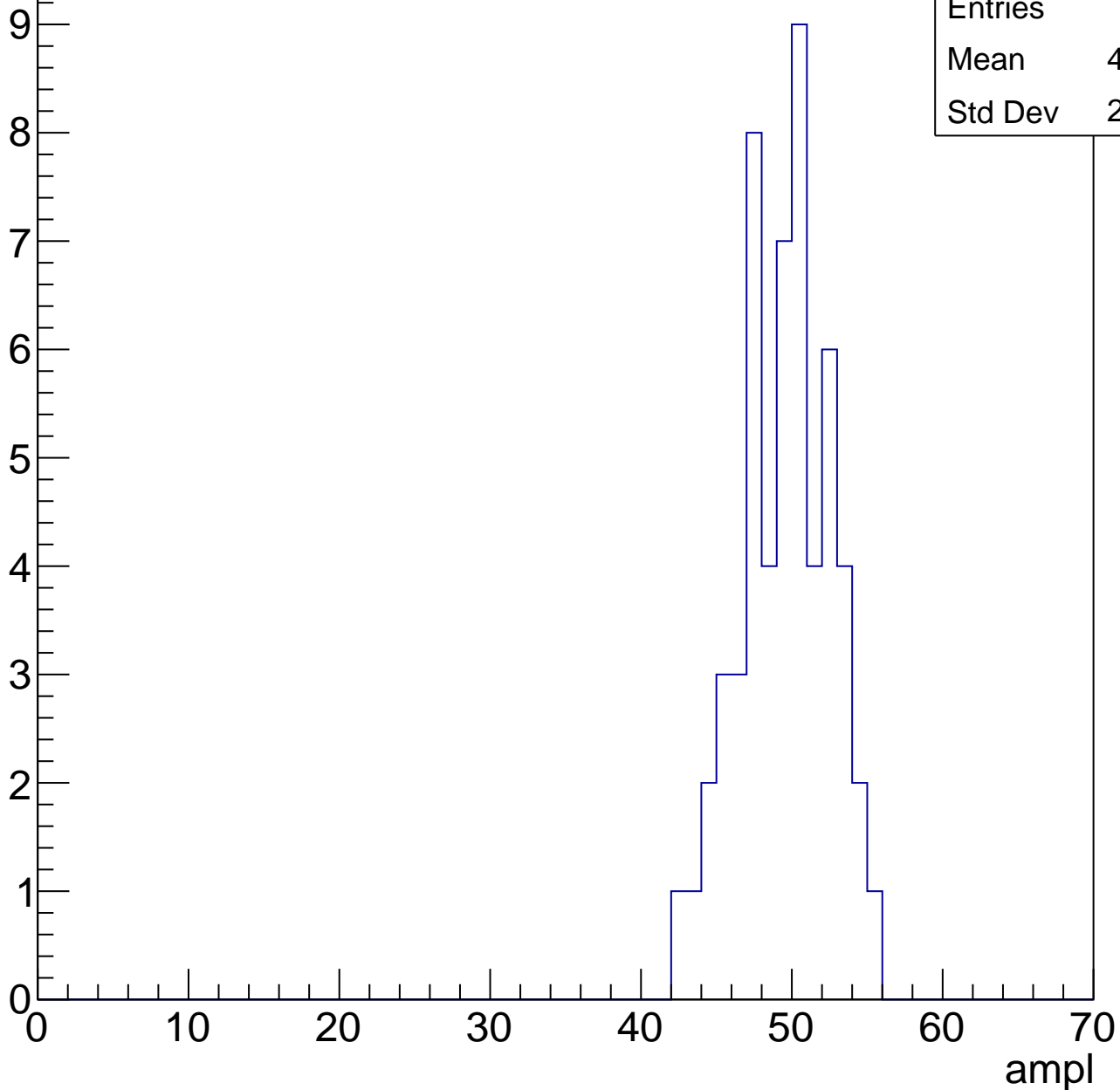


B1L003S, U19-ch17, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

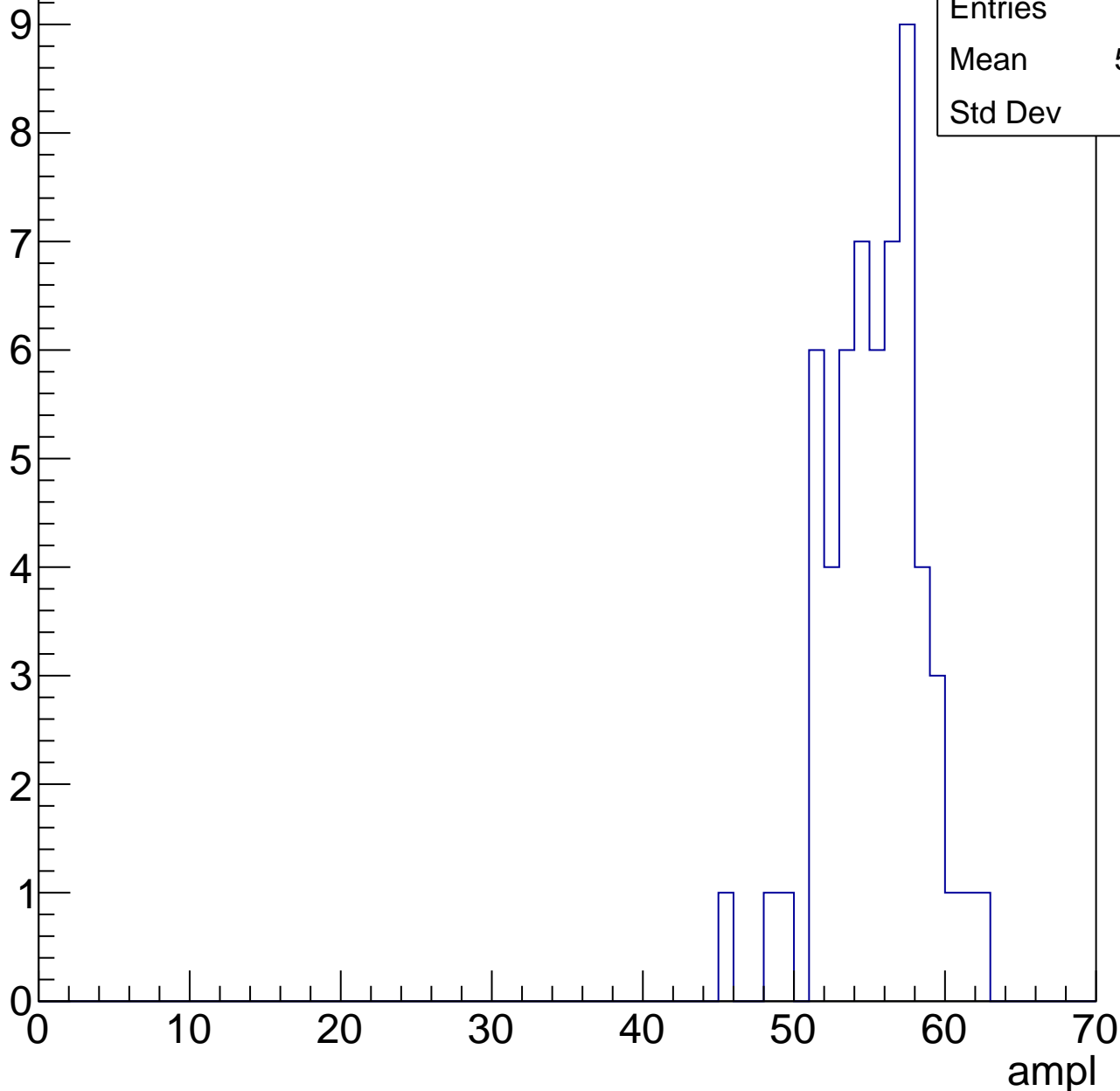
Entries	55
Mean	49.05
Std Dev	2.938



B1L003S, U19-ch17, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch17, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.88
Std Dev	8.292

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L003S, U19-ch17, adc6

calib_packv5_042523_0143.root, FC#13, port D2

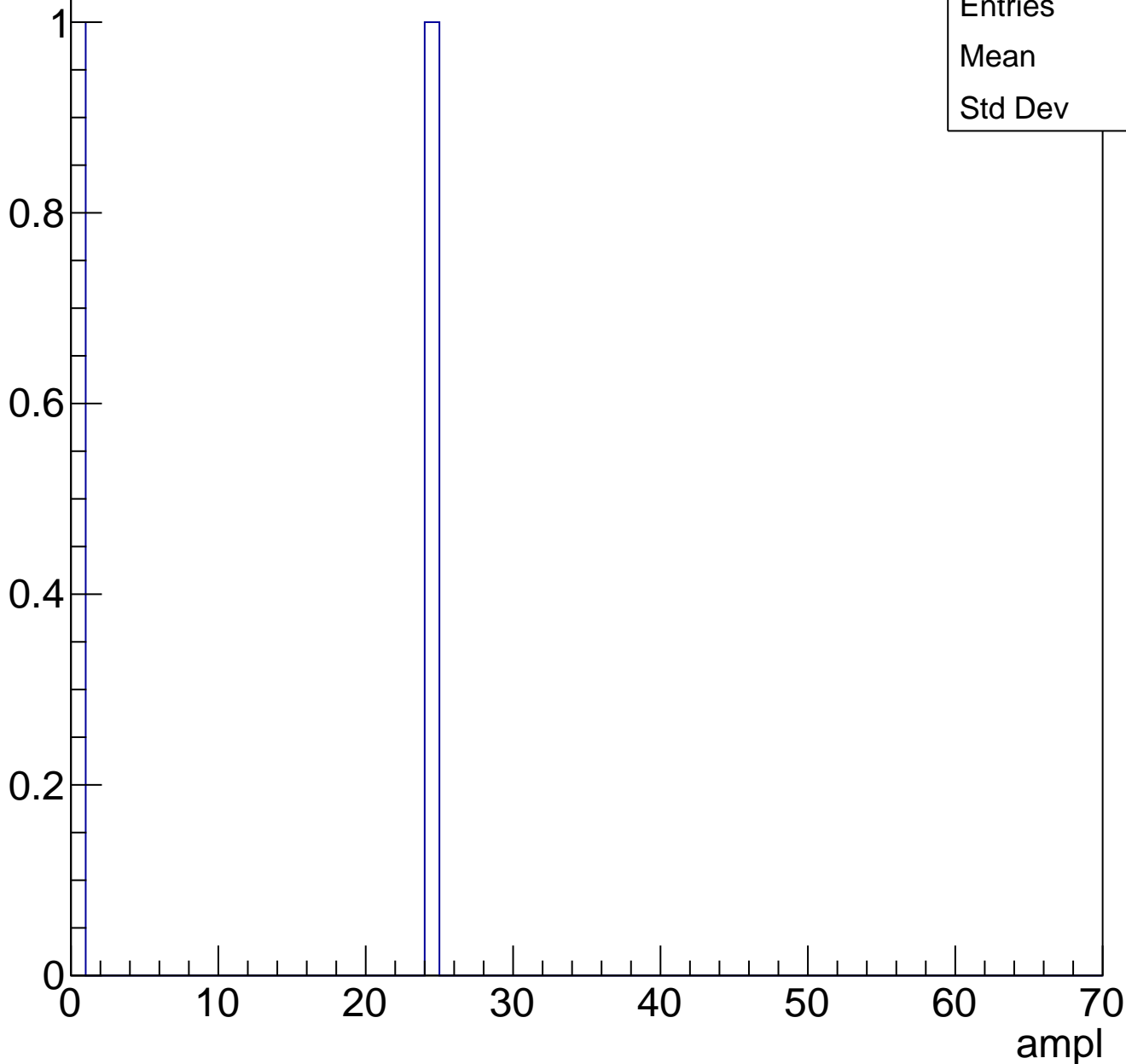
Entry



B1L003S, U19-ch17, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch18, adc0

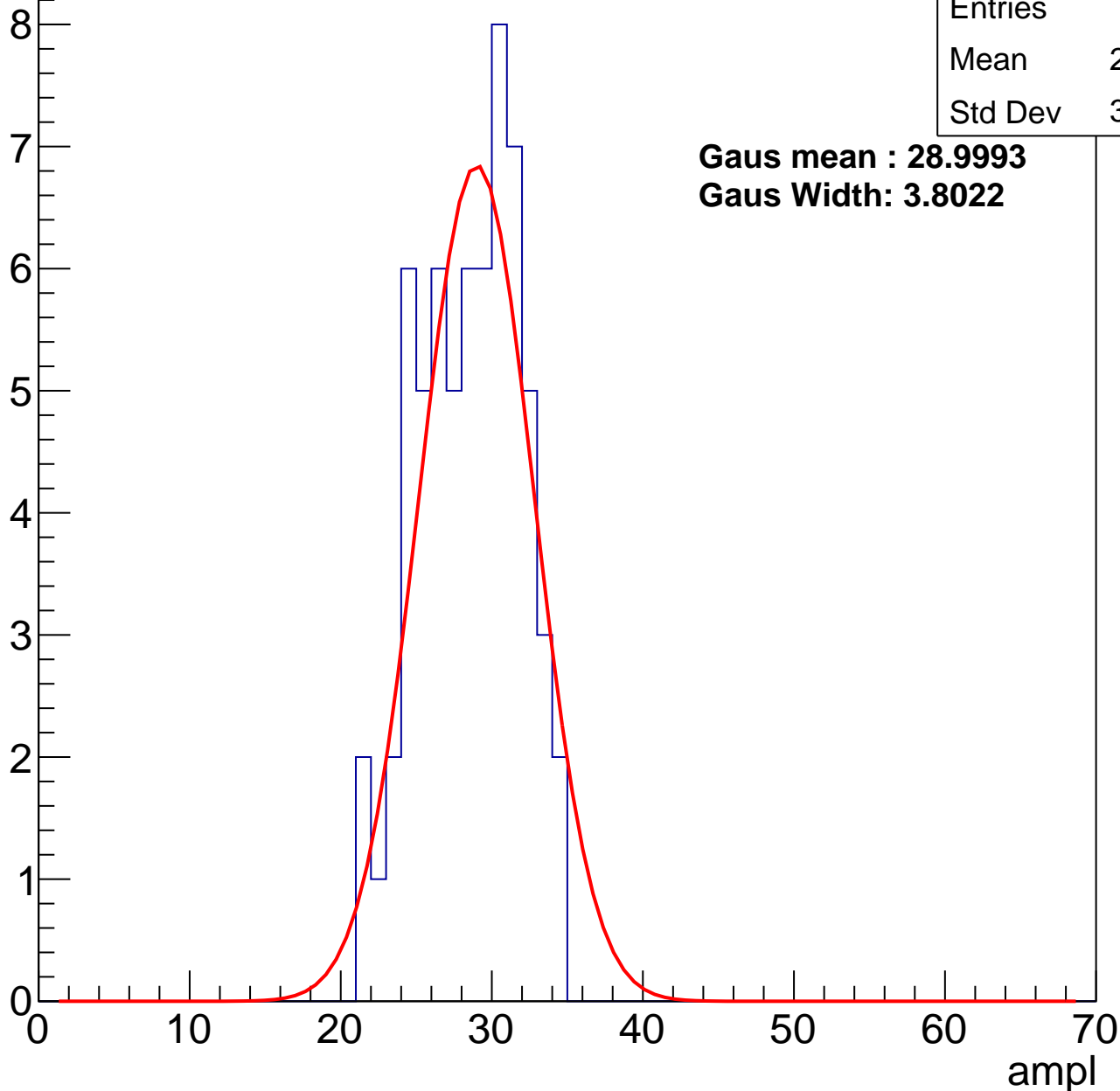
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.06
Std Dev	3.264

Gaus mean : 28.9993

Gaus Width: 3.8022



B1L003S, U19-ch18, adc1

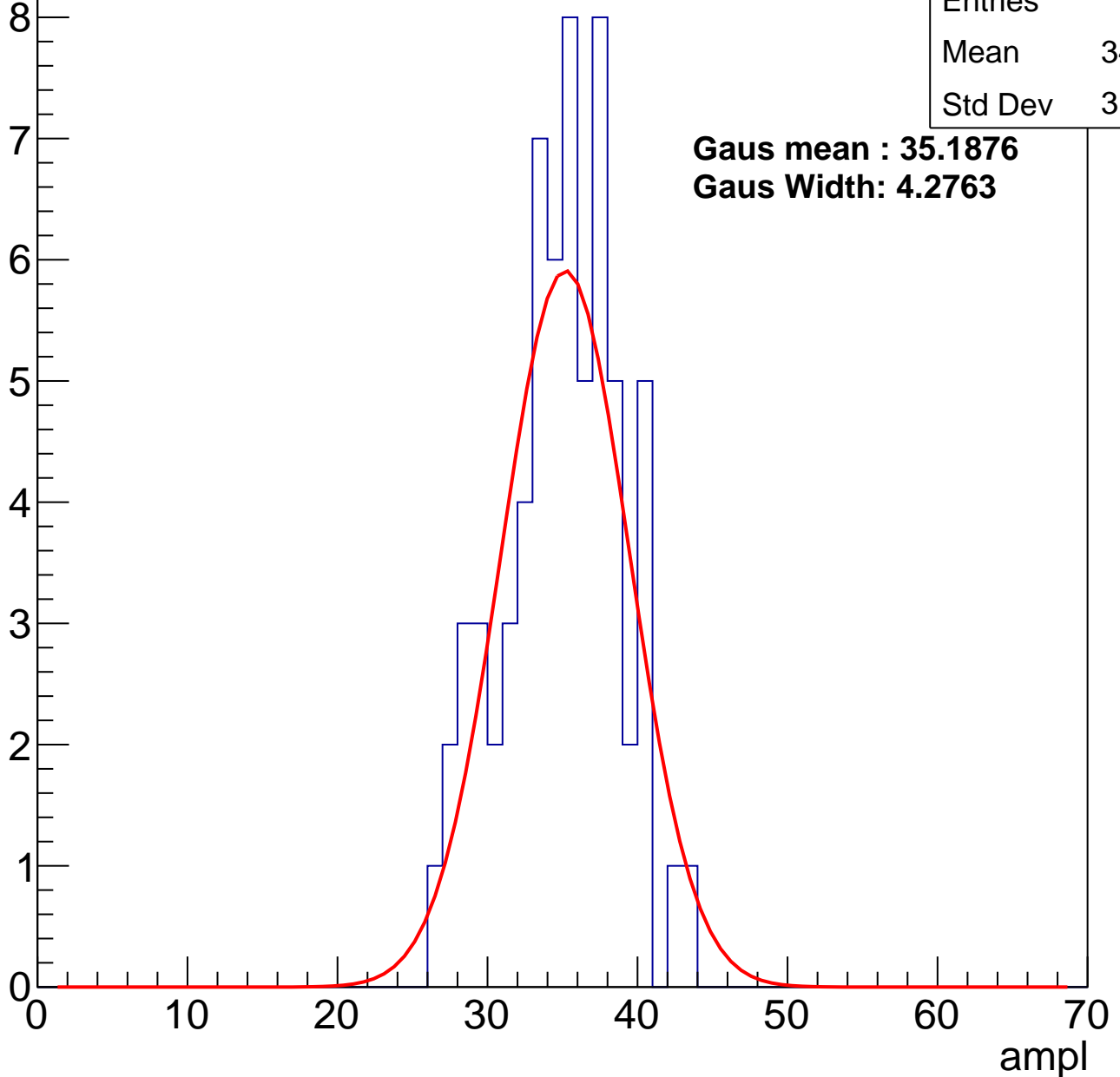
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	34.48
Std Dev	3.827

Gaus mean : 35.1876

Gaus Width: 4.2763



B1L003S, U19-ch18, adc2

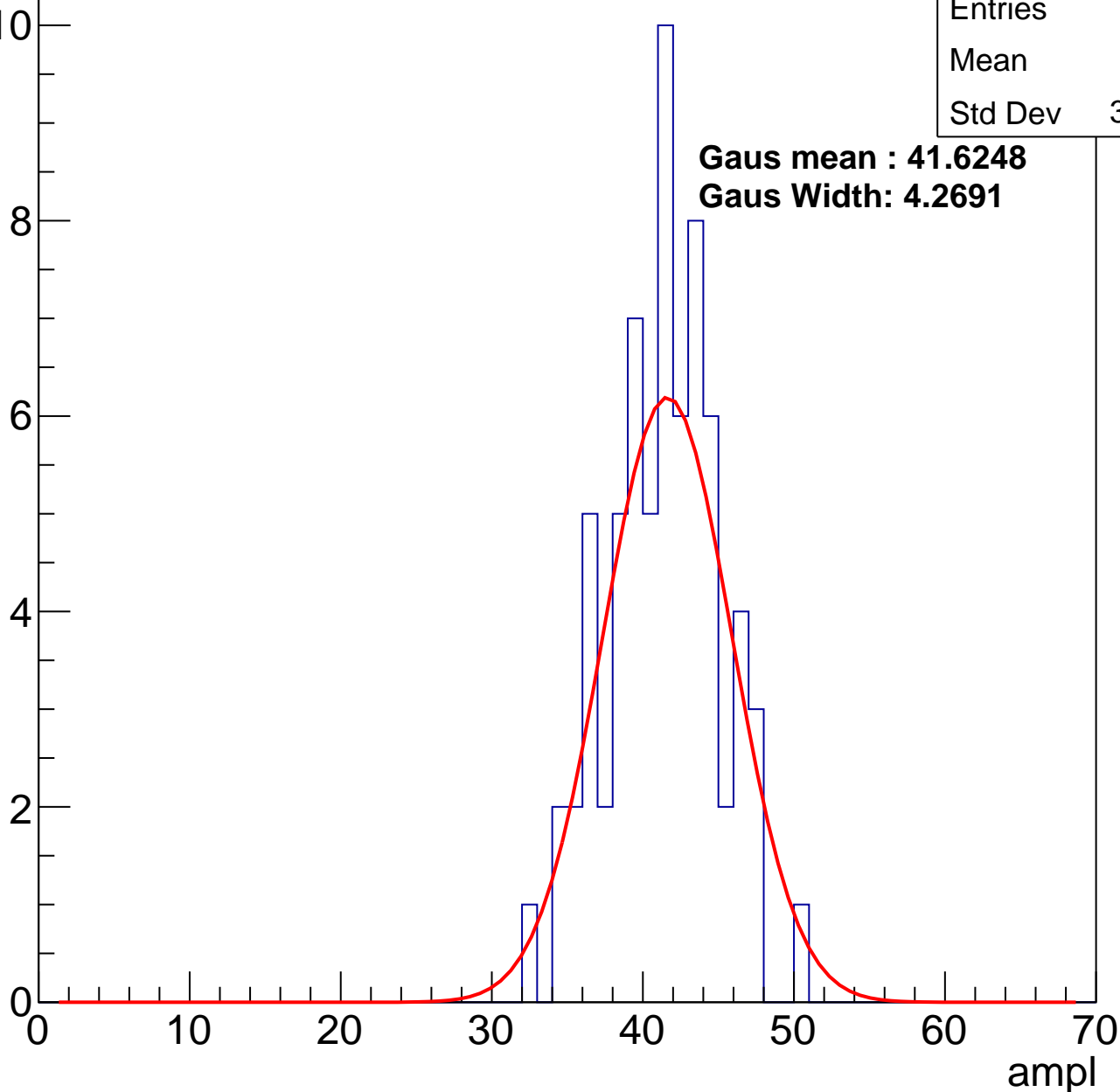
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	40.9
Std Dev	3.608

Gaus mean : 41.6248

Gaus Width: 4.2691

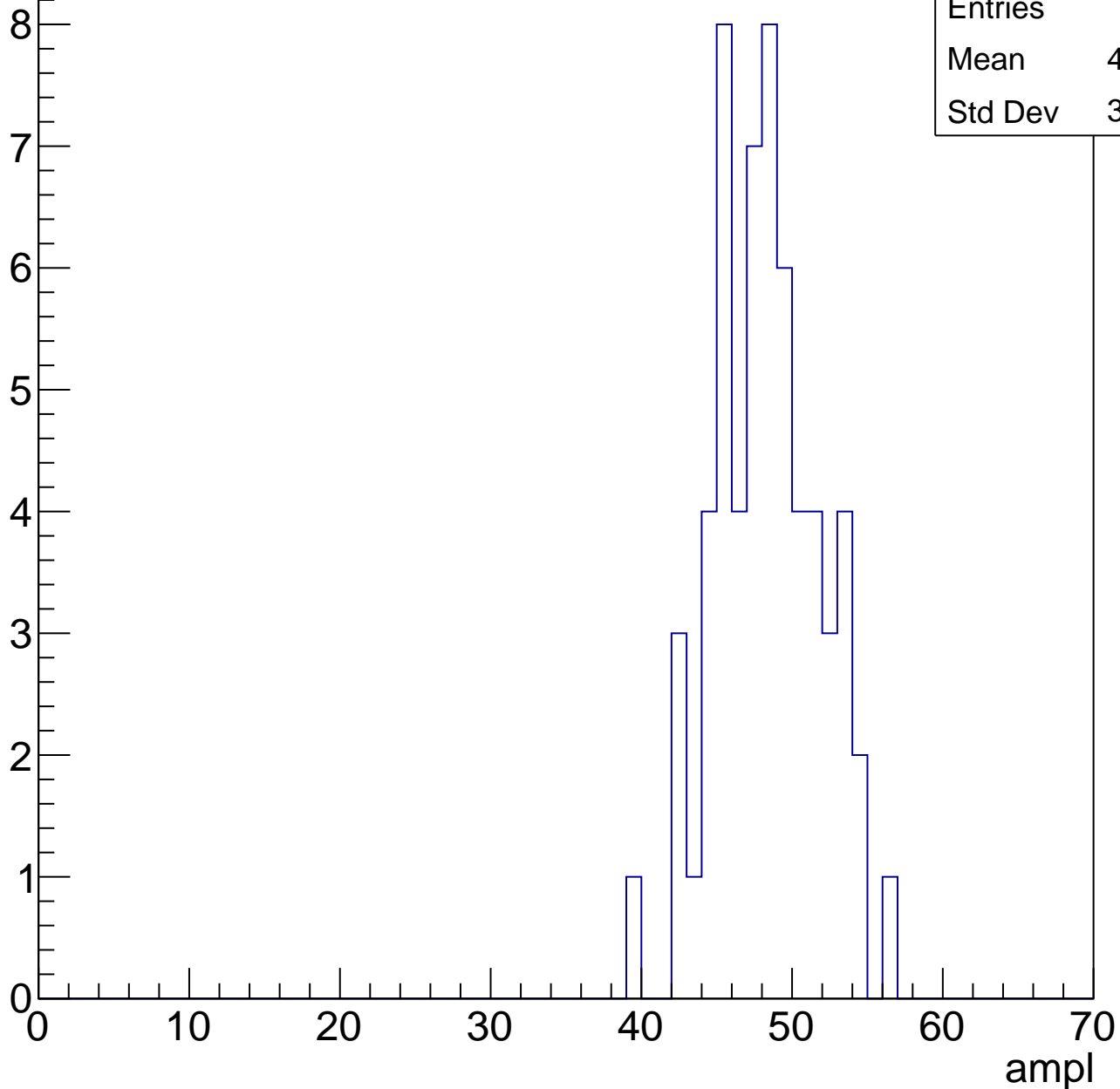


B1L003S, U19-ch18, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	47.85
Std Dev	3.449

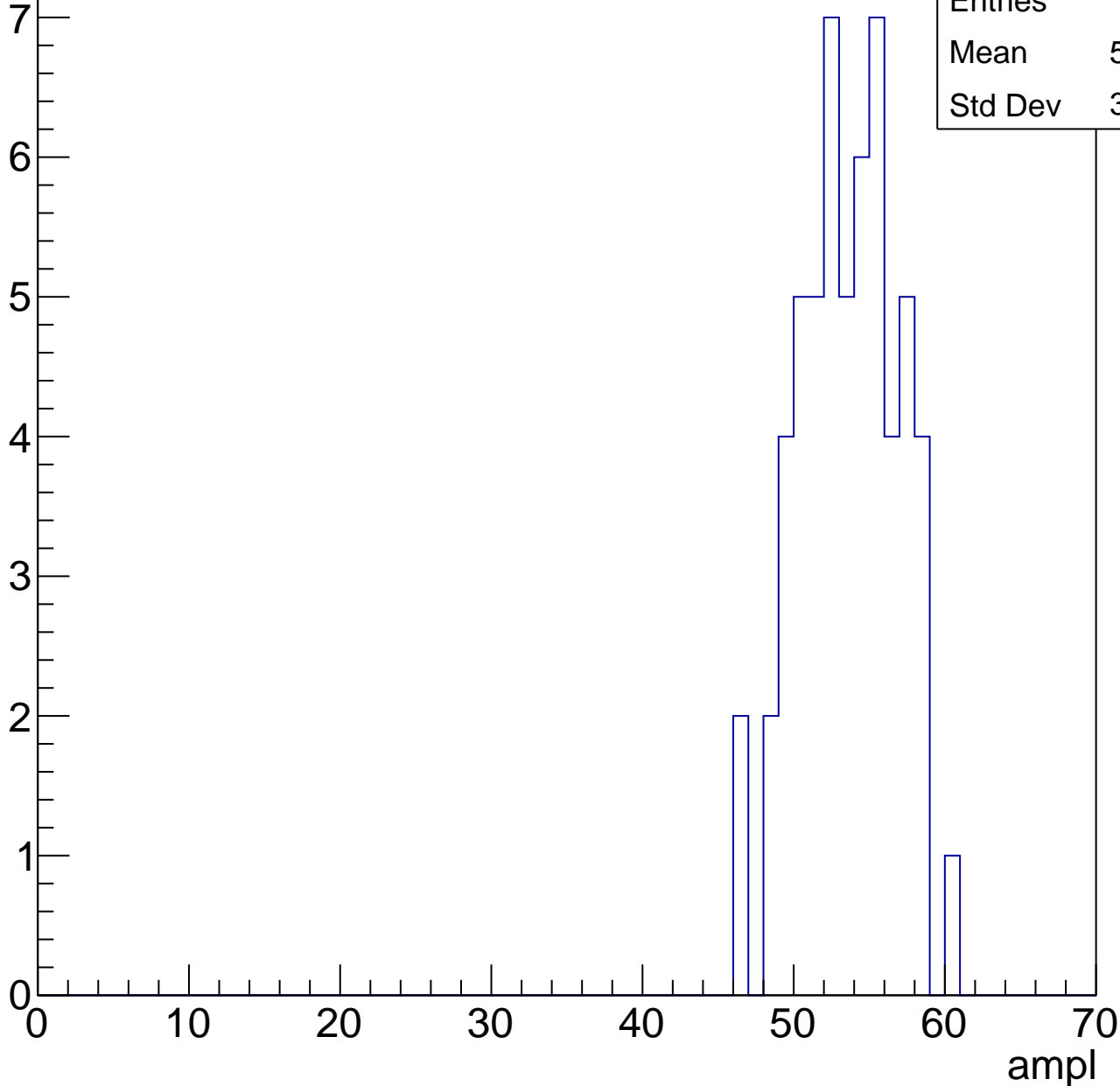


B1L003S, U19-ch18, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	53.12
Std Dev	3.196

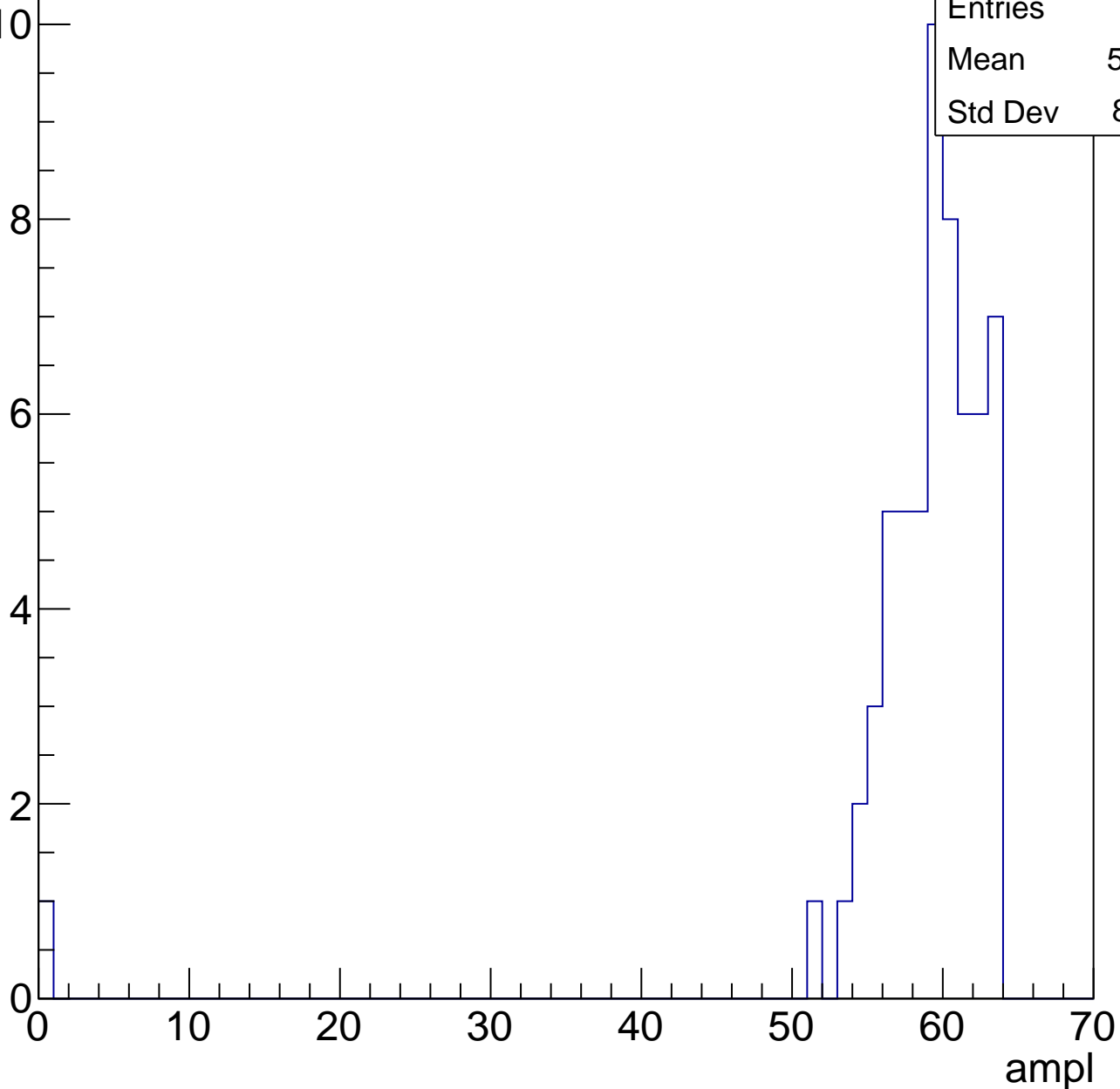


B1L003S, U19-ch18, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

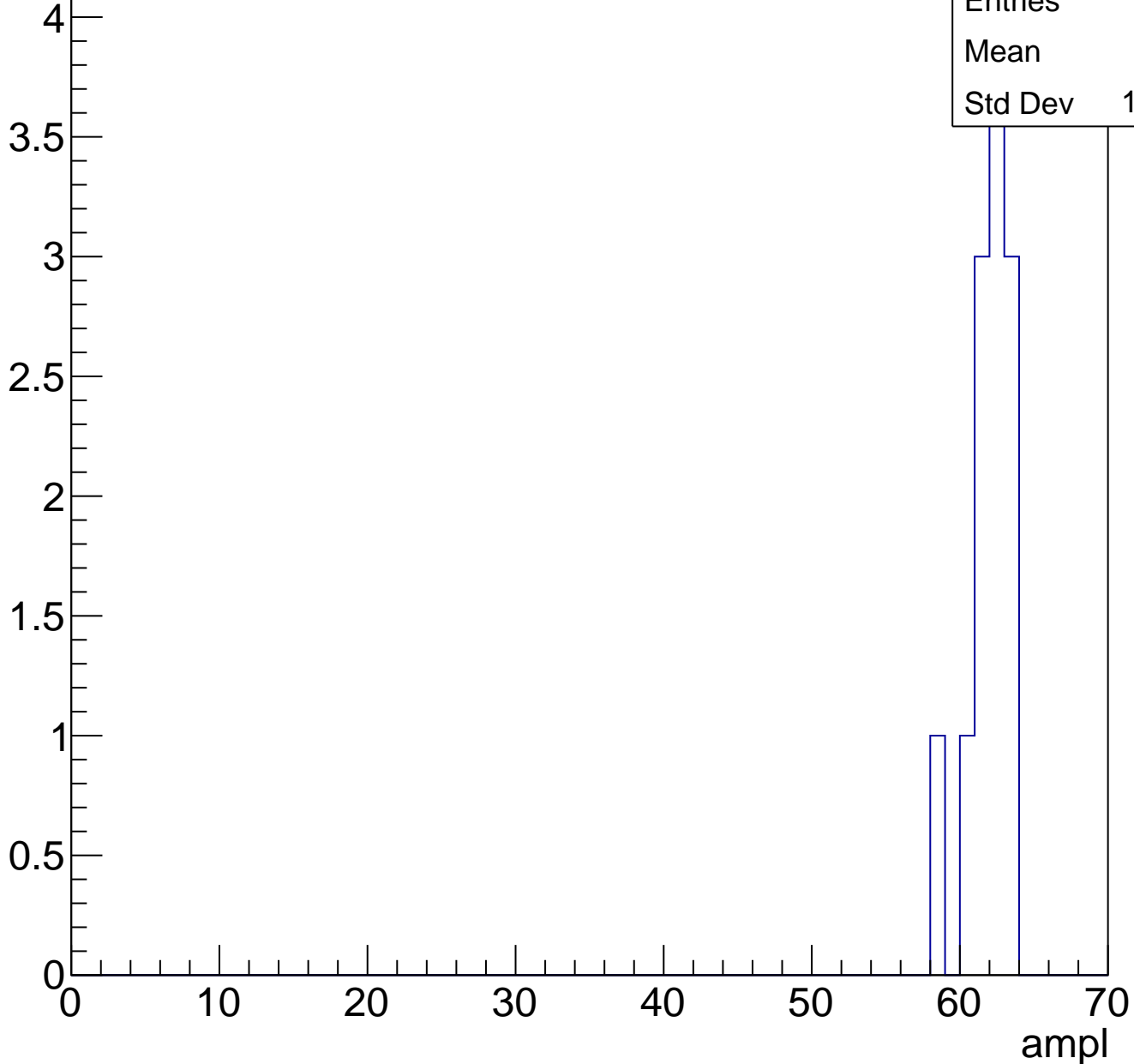
Entries	60
Mean	58.02
Std Dev	8.051



B1L003S, U19-ch18, adc6

calib_packv5_042523_0143.root, FC#13, port D2

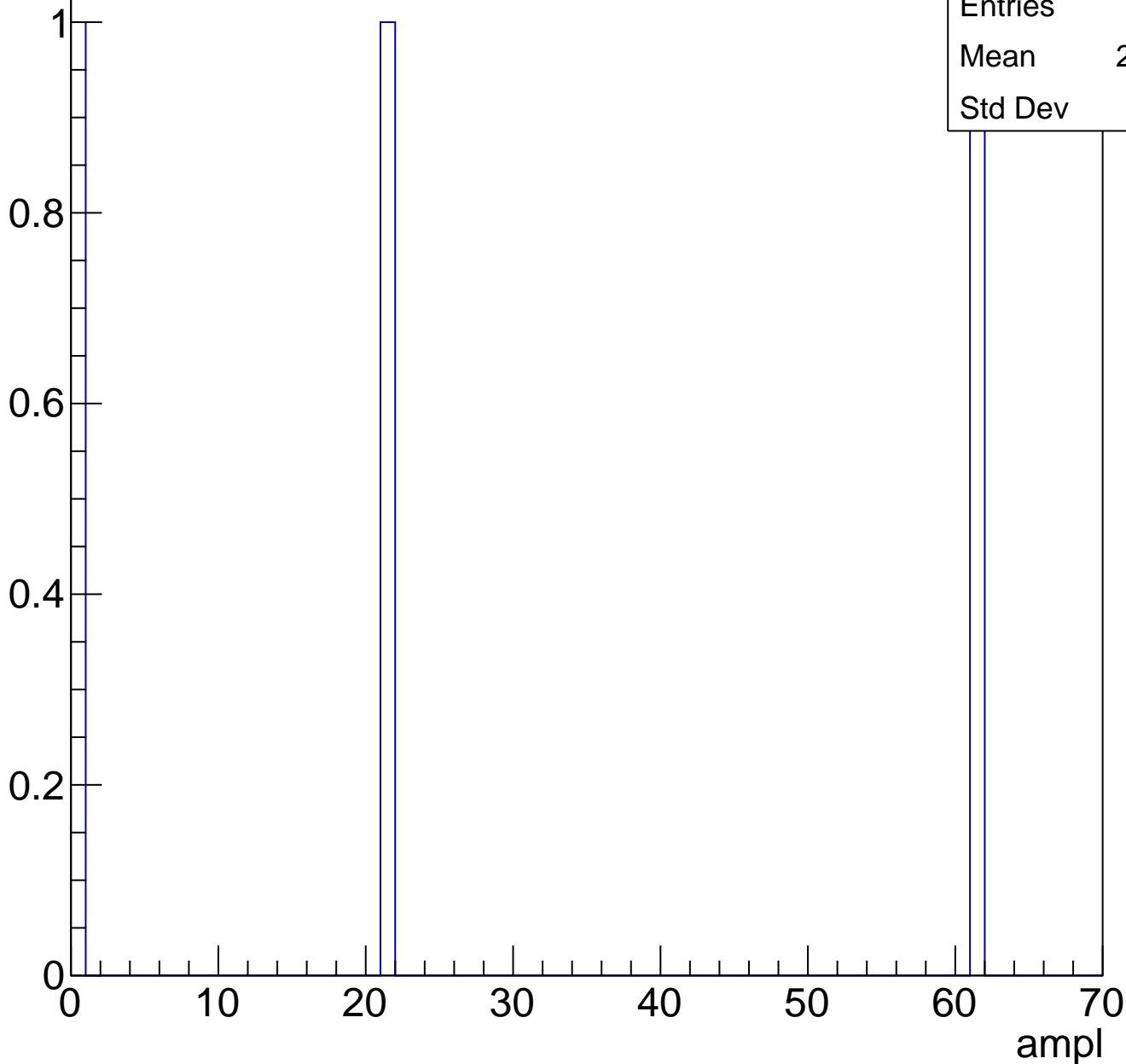
Entry



B1L003S, U19-ch18, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	27.33
Std Dev	25.3

B1L003S, U19-ch19, adc0

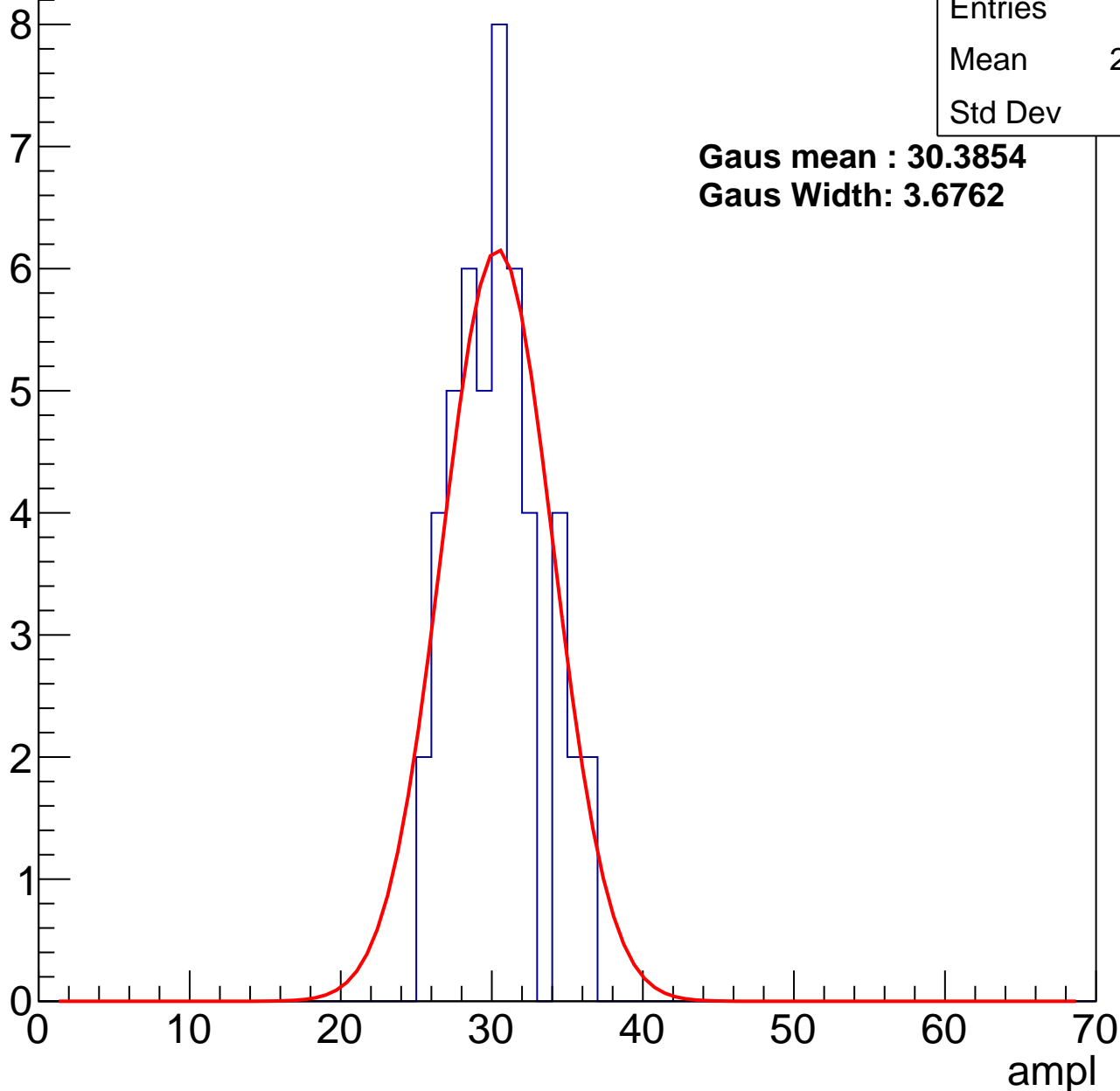
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	29.88
Std Dev	2.87

Gaus mean : 30.3854

Gaus Width: 3.6762



B1L003S, U19-ch19, adc1

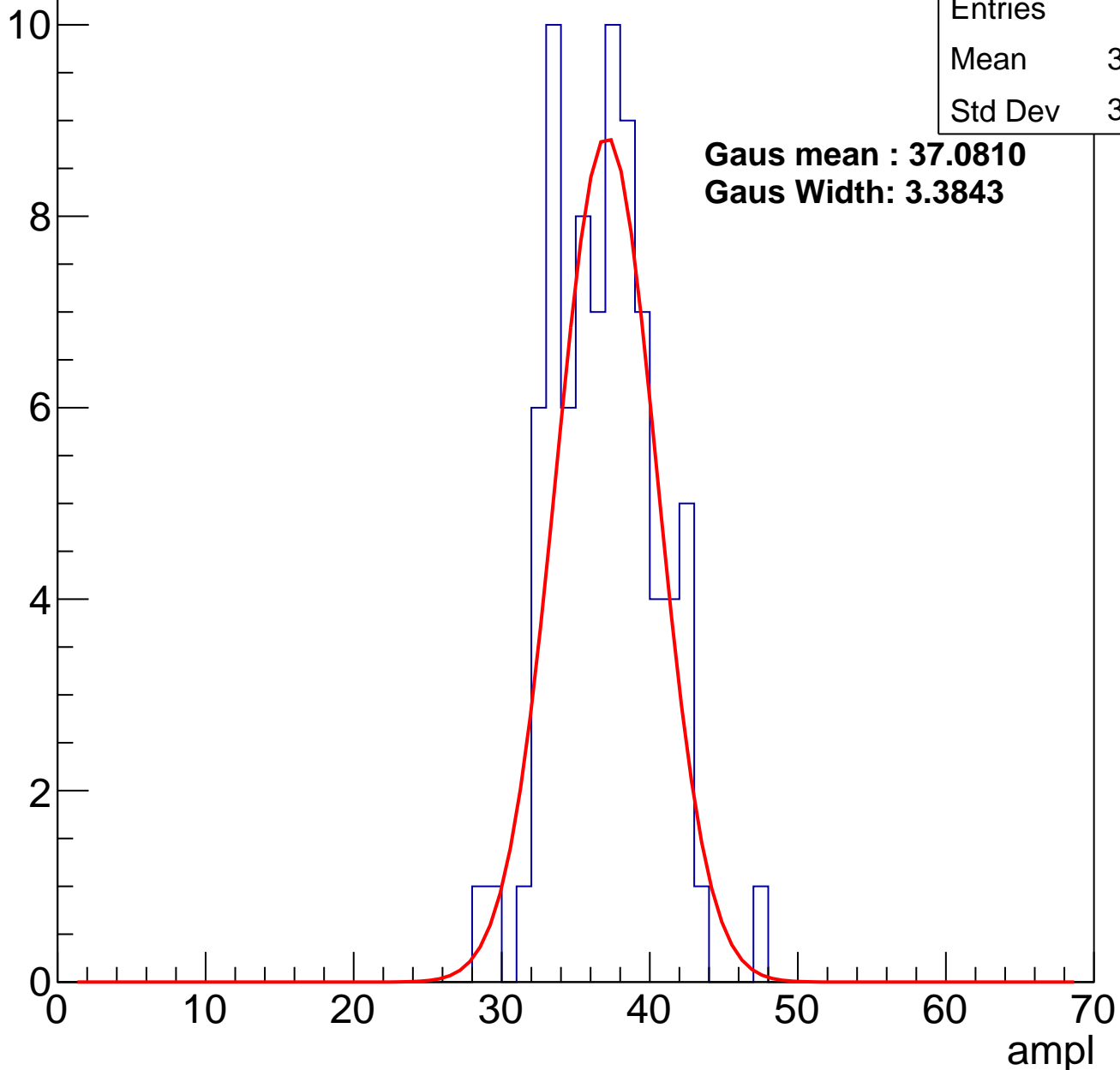
calib_packv5_042523_0143.root, FC#13, port D2

Entries	81
Mean	36.48
Std Dev	3.443

Gaus mean : 37.0810

Gaus Width: 3.3843

Entry



B1L003S, U19-ch19, adc2

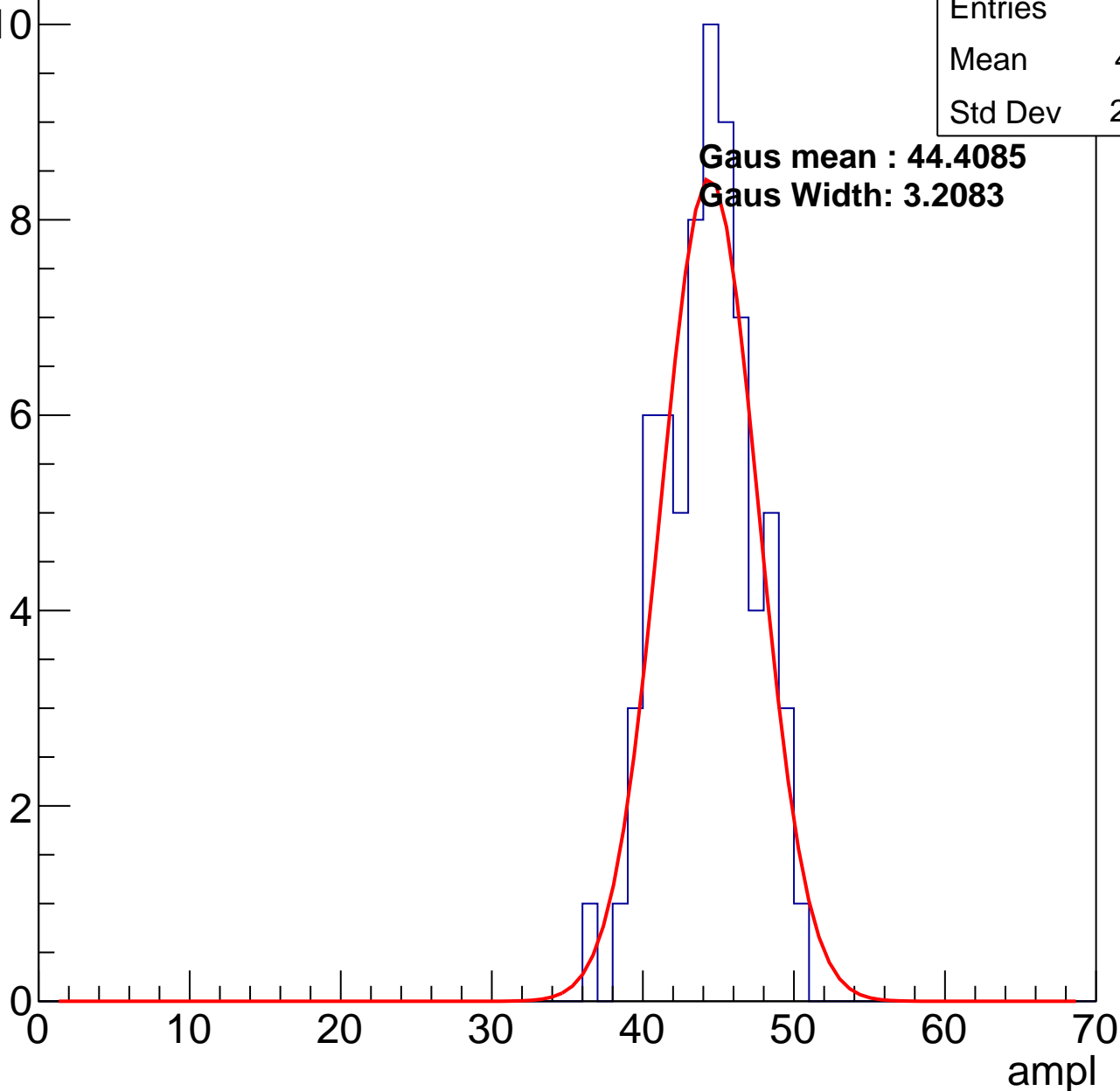
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.81
Std Dev	2.984

Gaus mean : 44.4085

Gaus Width: 3.2083

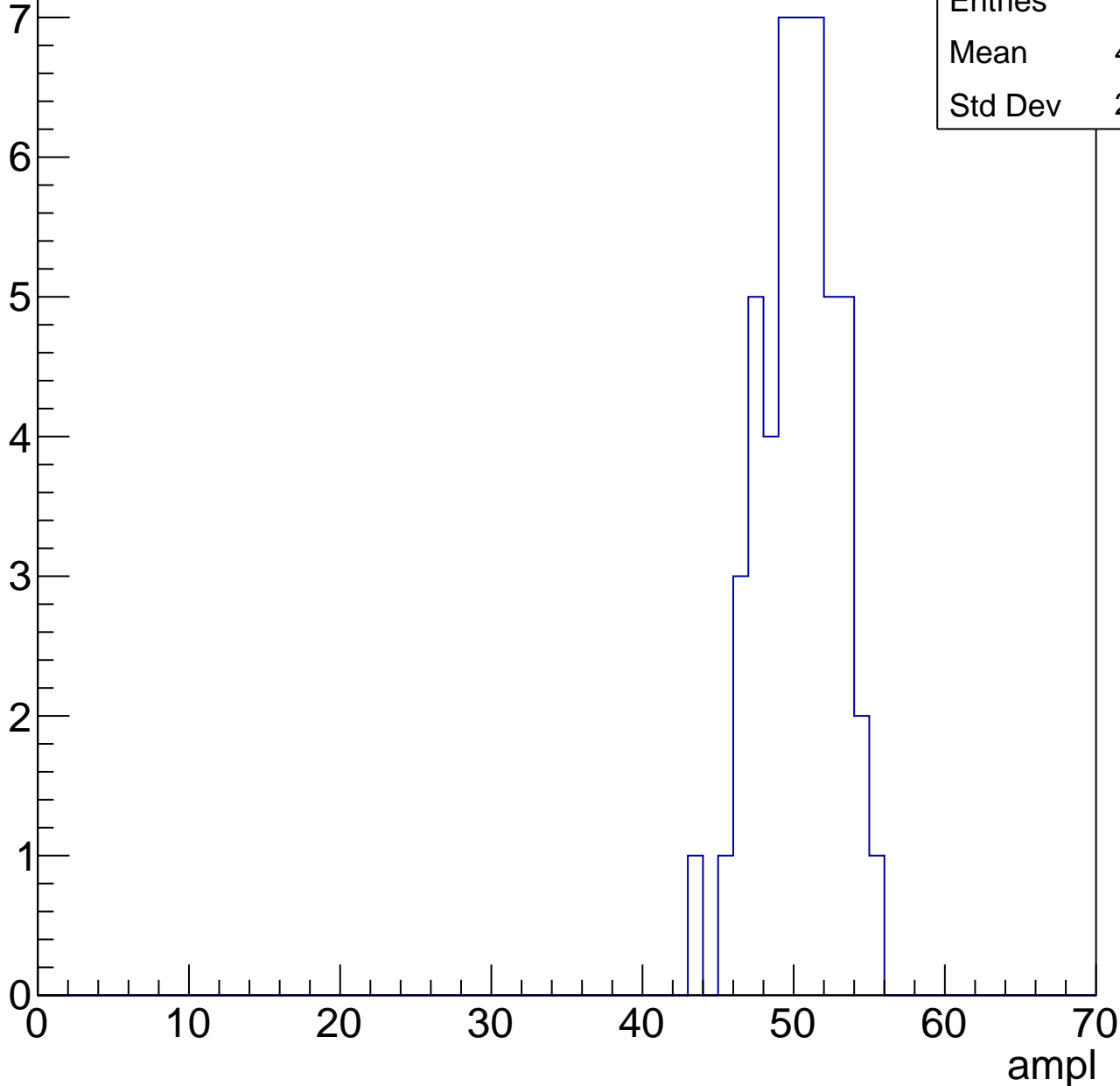


B1L003S, U19-ch19, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	49.81
Std Dev	2.571



B1L003S, U19-ch19, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	59
Mean	55.42
Std Dev	3.049

Entry

10

8

6

4

2

0

0

10

20

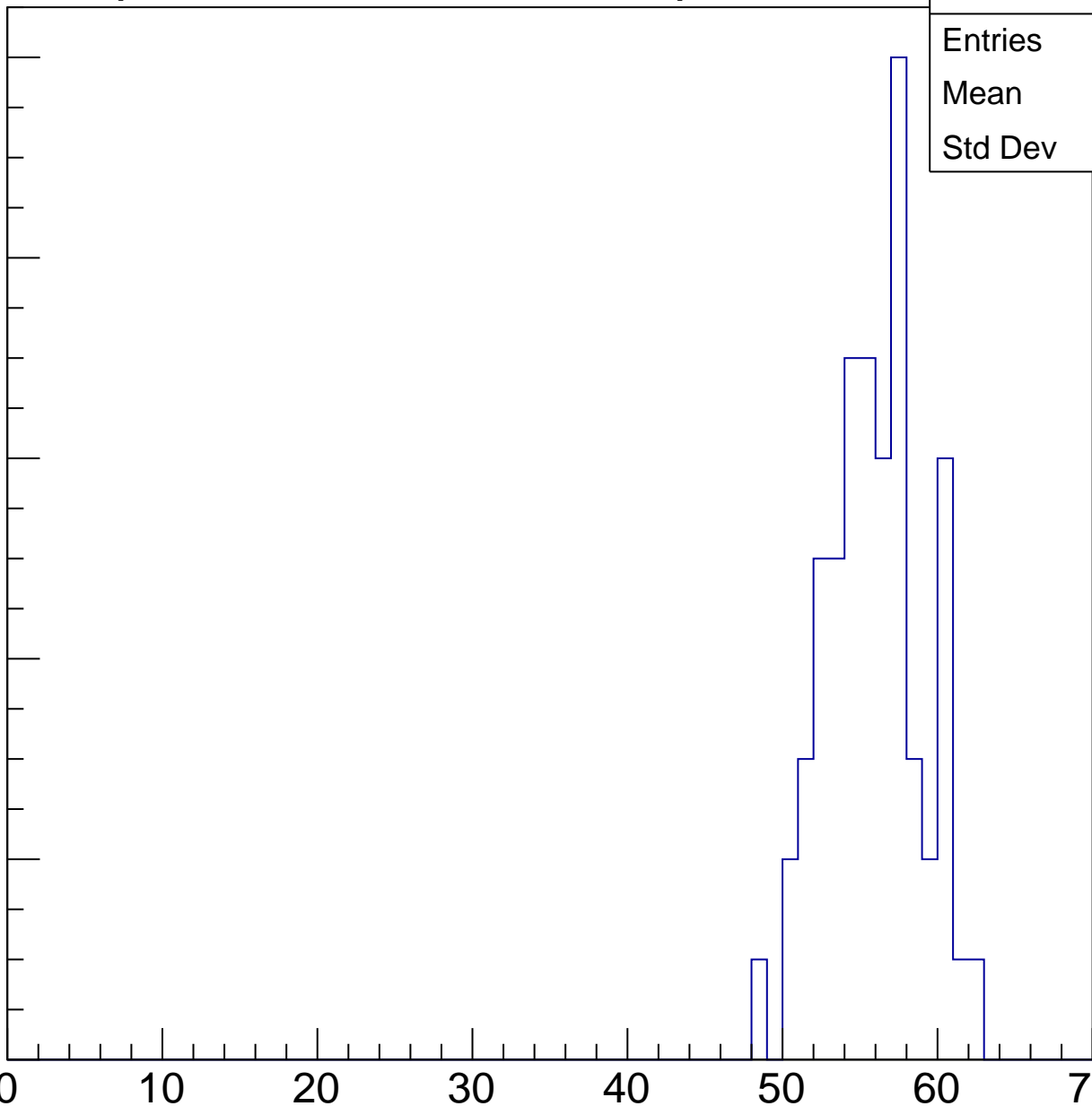
30

40

50

60

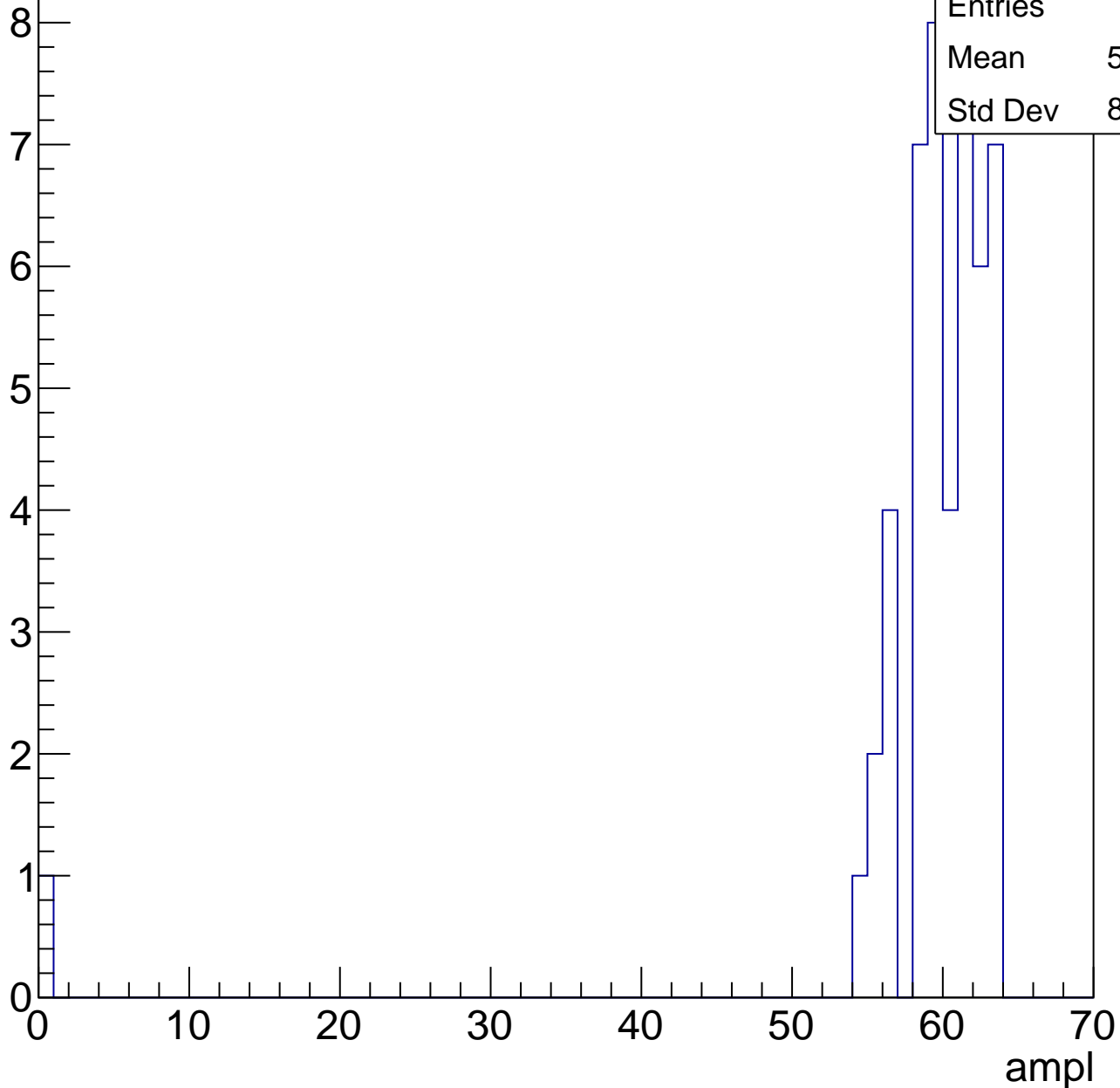
ampl



B1L003S, U19-ch19, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch19, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch19, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U19-ch20, adc0

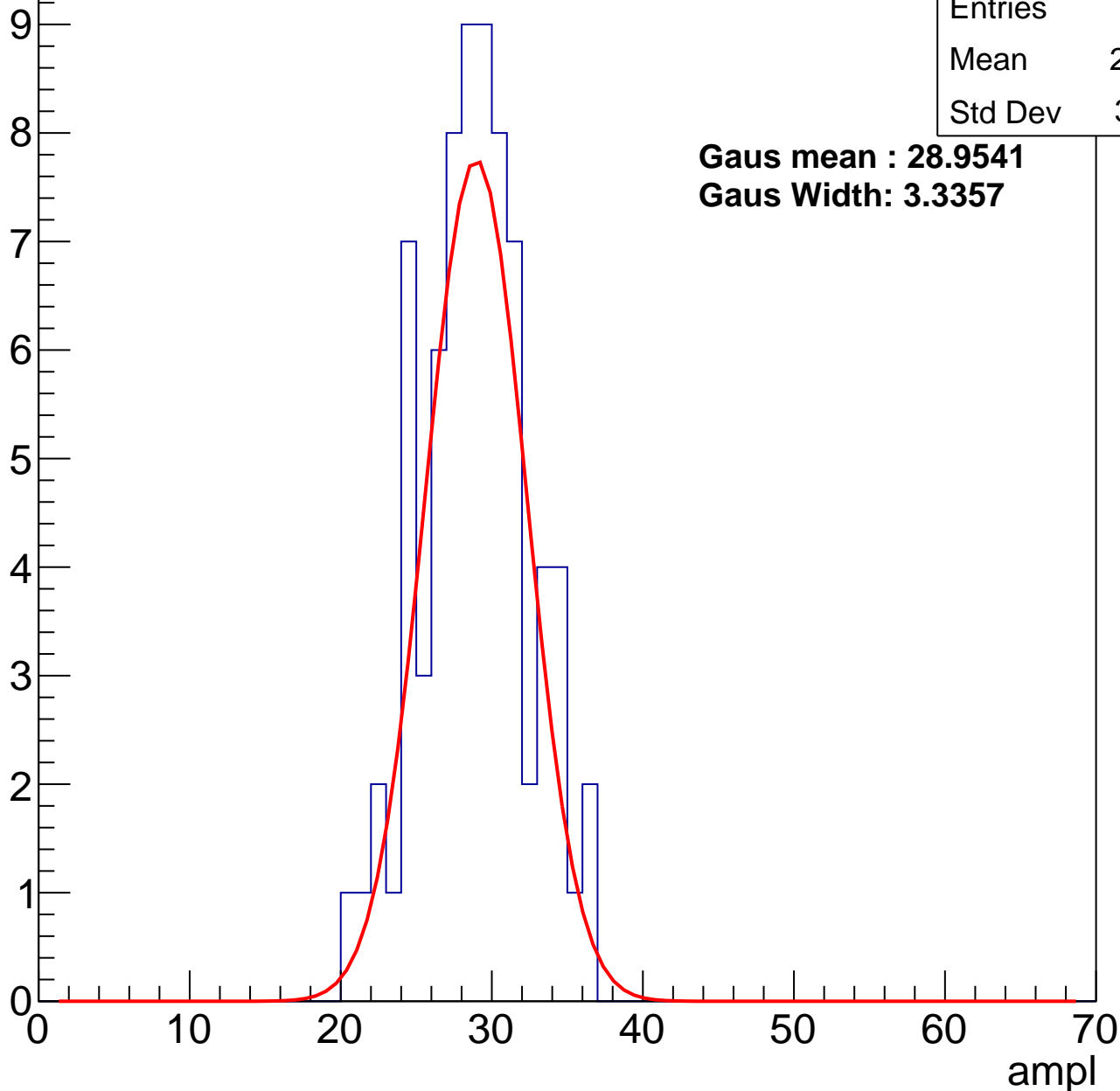
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	28.43
Std Dev	3.511

Gaus mean : 28.9541

Gaus Width: 3.3357



B1L003S, U19-ch20, adc1

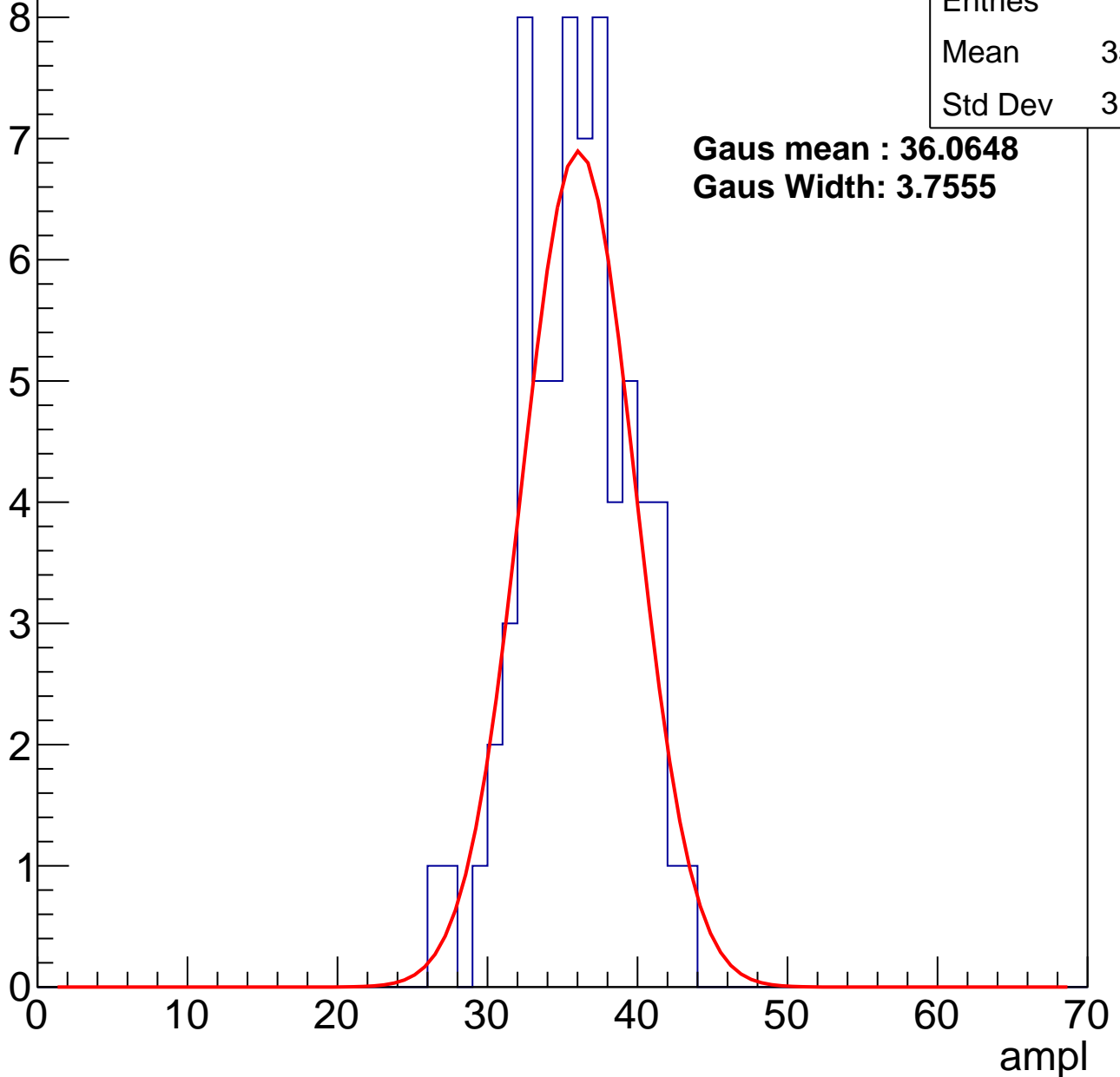
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.44
Std Dev	3.587

Gaus mean : 36.0648

Gaus Width: 3.7555



B1L003S, U19-ch20, adc2

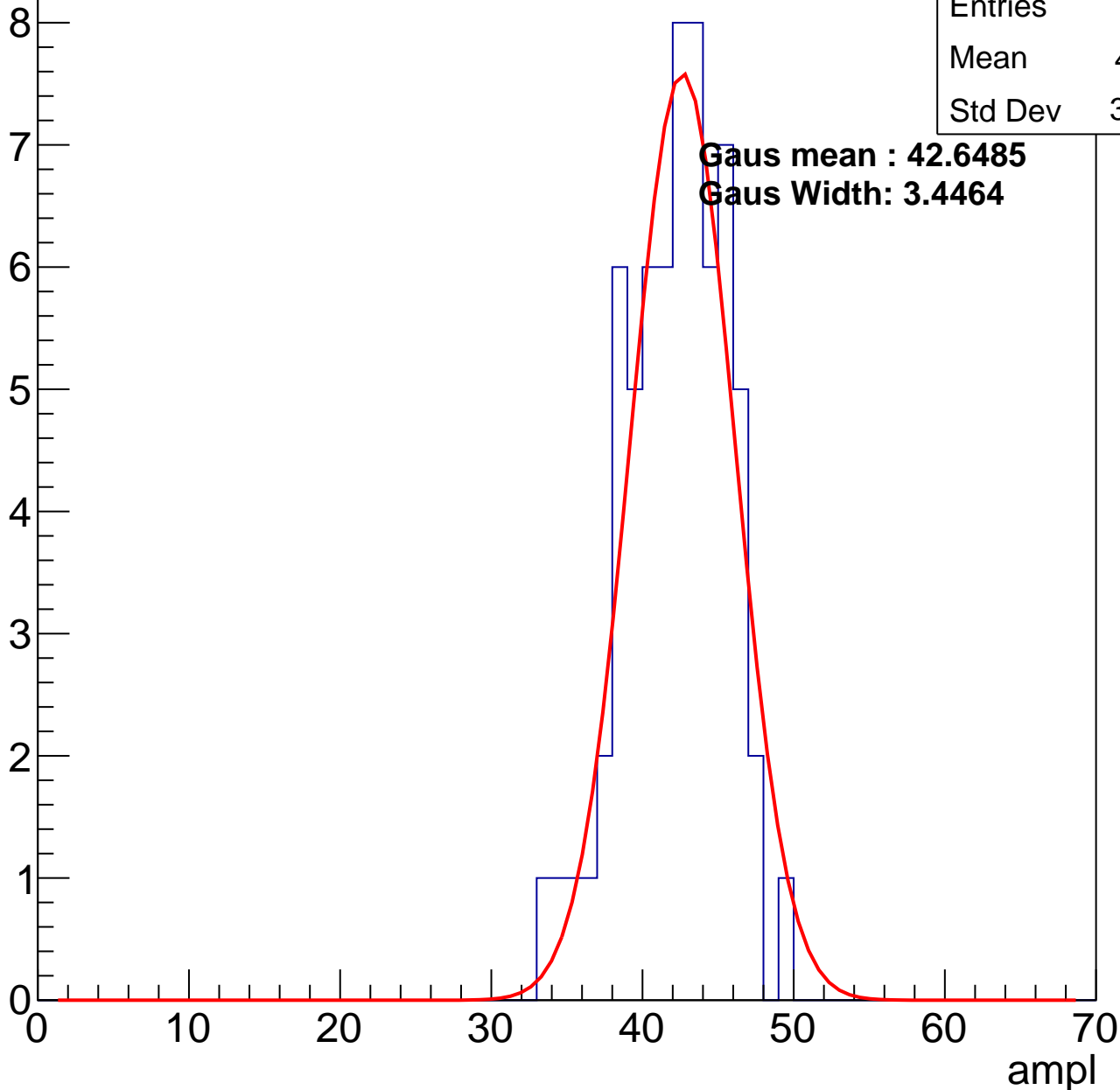
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.71
Std Dev	3.302

Gaus mean : 42.6485

Gaus Width: 3.4464

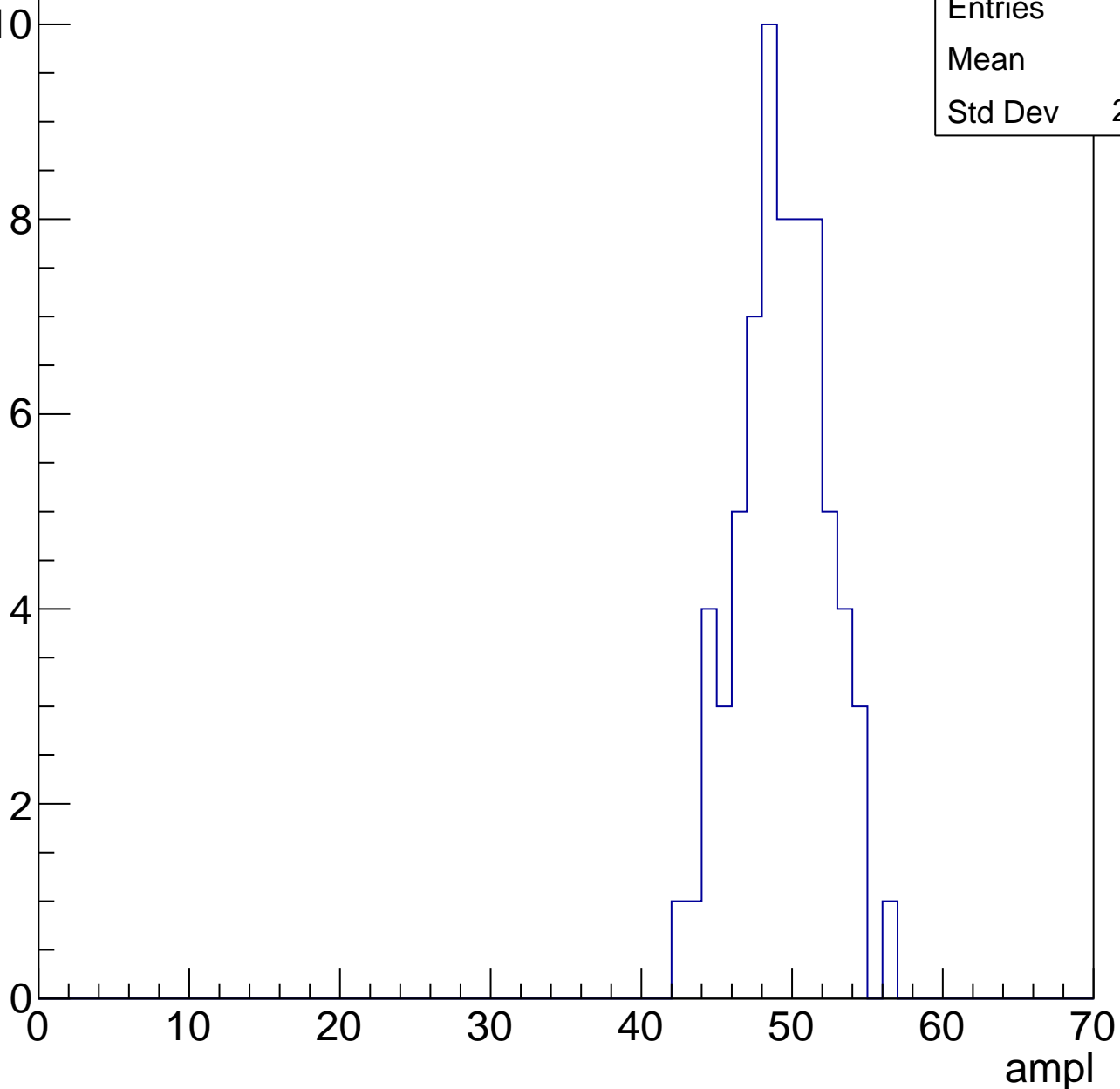


B1L003S, U19-ch20, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

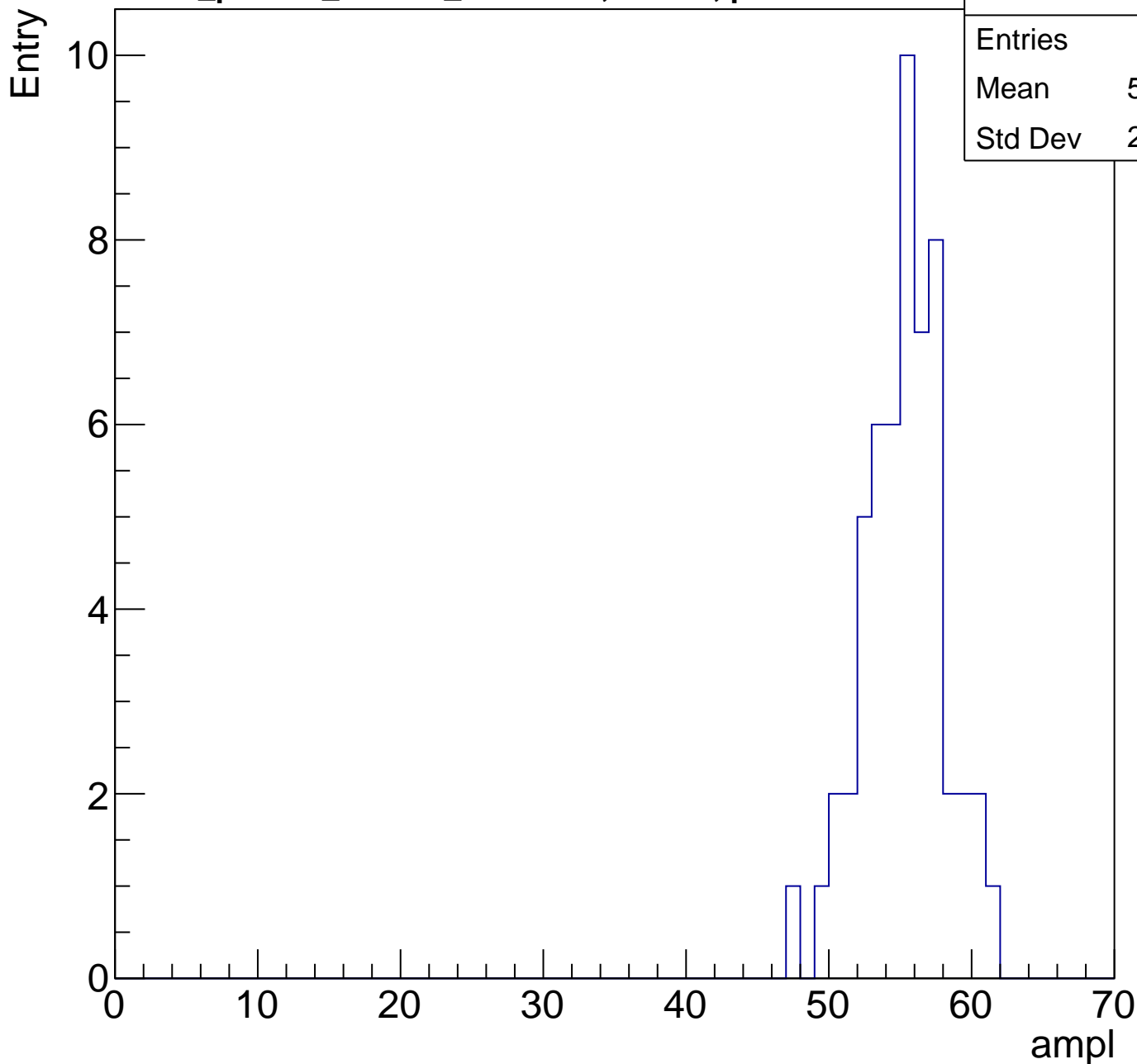
Entries	68
Mean	48.9
Std Dev	2.941



B1L003S, U19-ch20, adc4

calib_packv5_042523_0143.root, FC#13, port D2

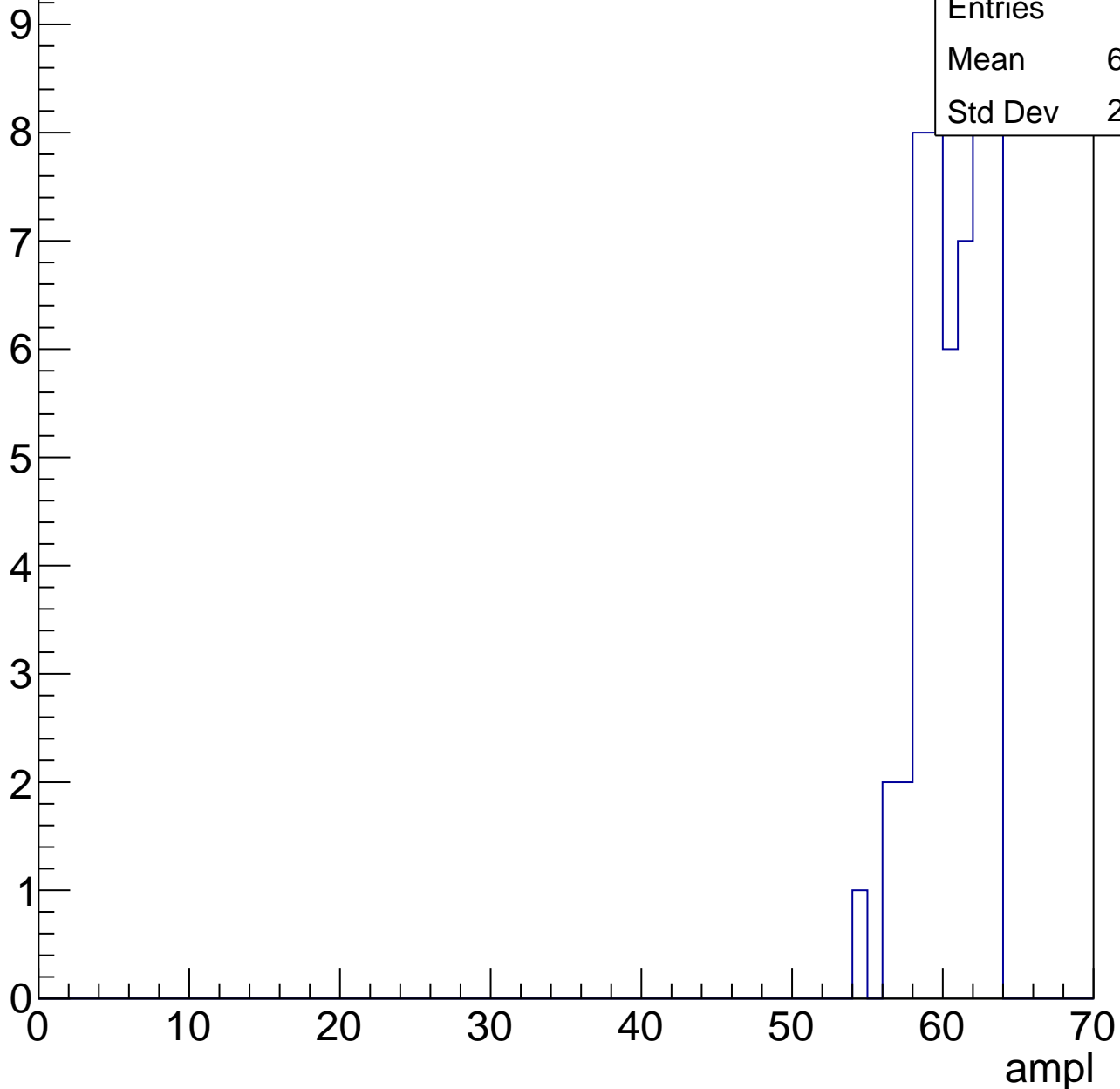
Entries	55
Mean	54.78
Std Dev	2.794



B1L003S, U19-ch20, adc5

calib_packv5_042523_0143.root, FC#13, port D2

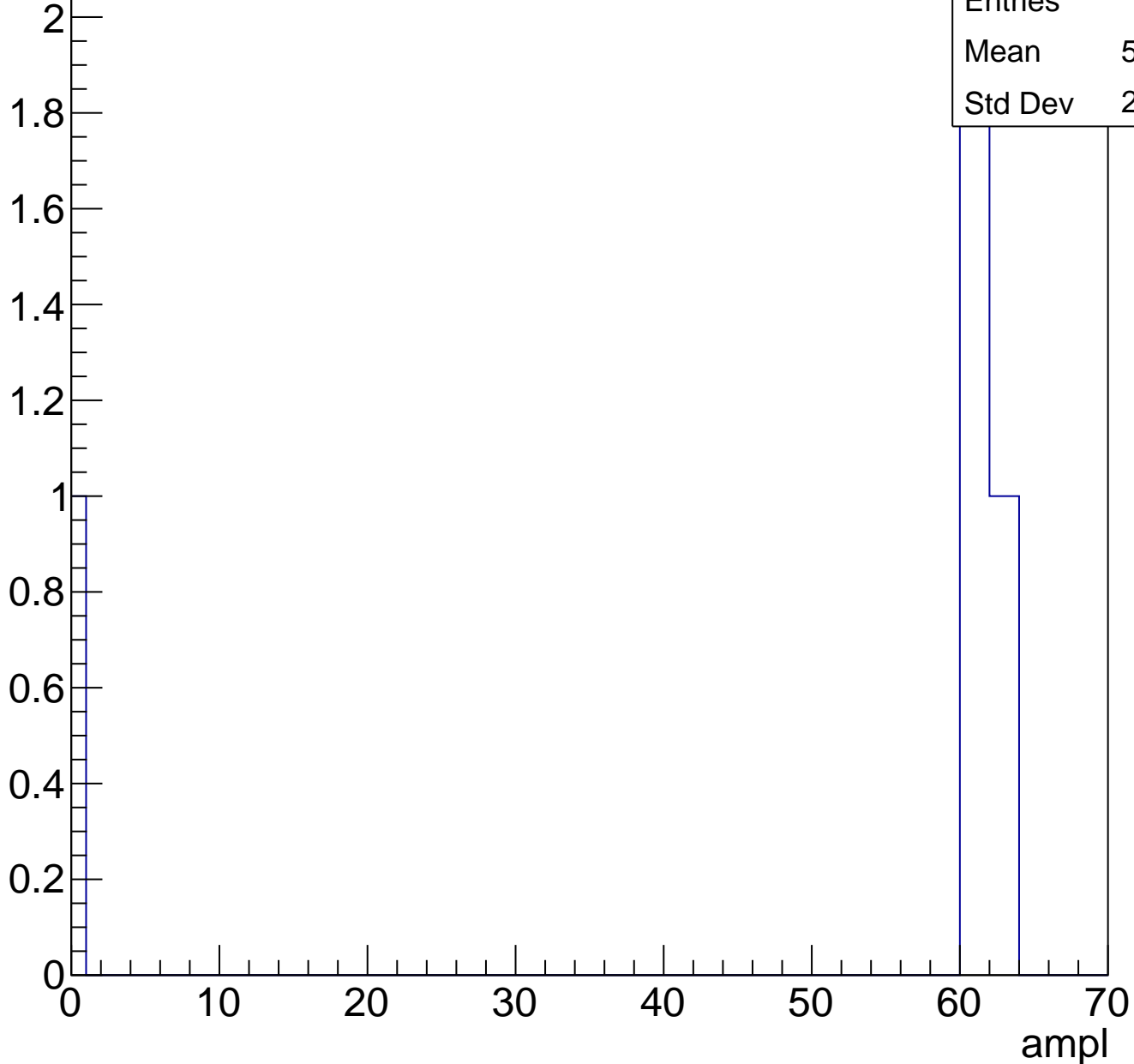
Entry



B1L003S, U19-ch20, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch20, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch21, adc0

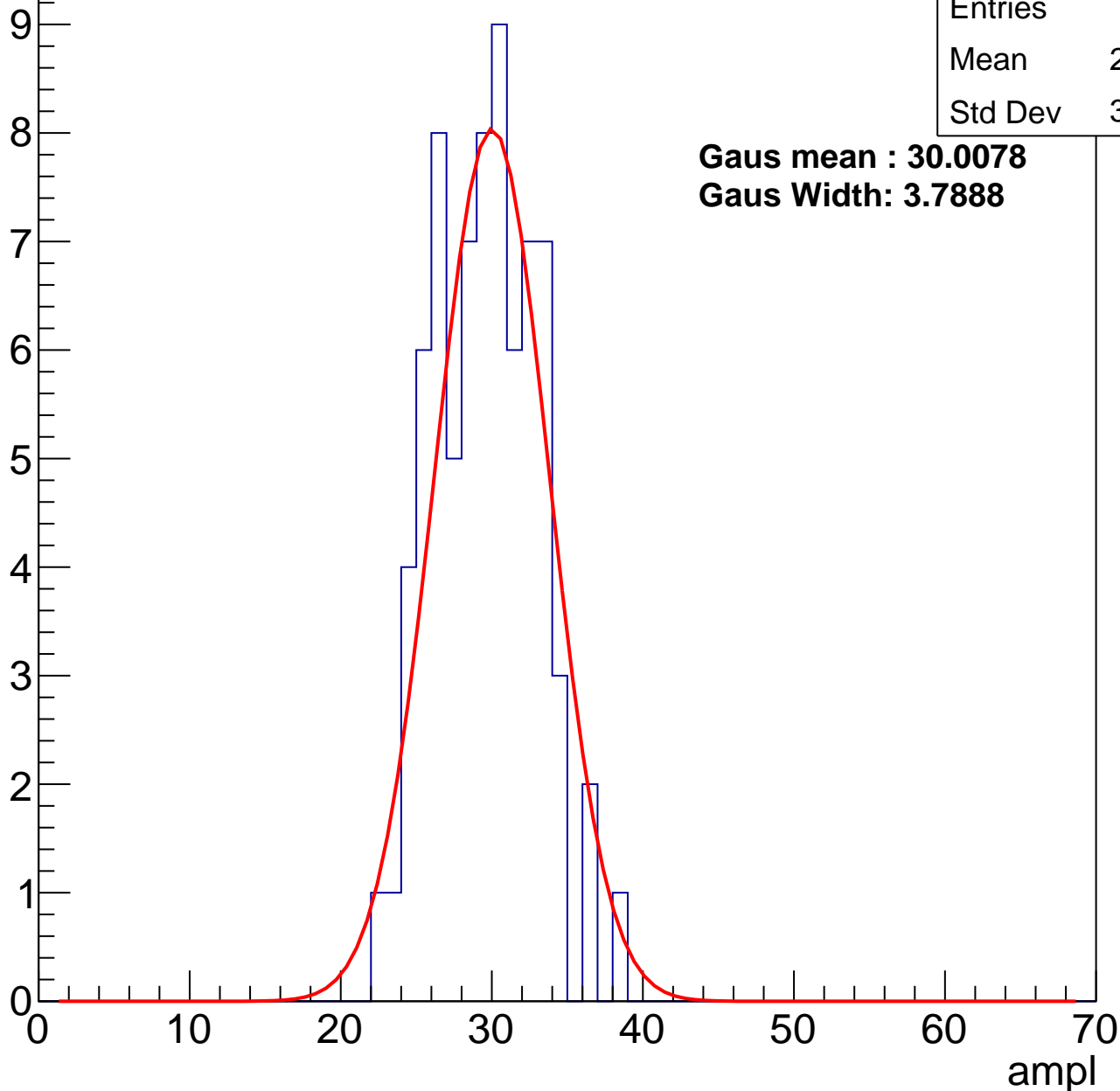
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	29.13
Std Dev	3.348

Gaus mean : 30.0078

Gaus Width: 3.7888



B1L003S, U19-ch21, adc1

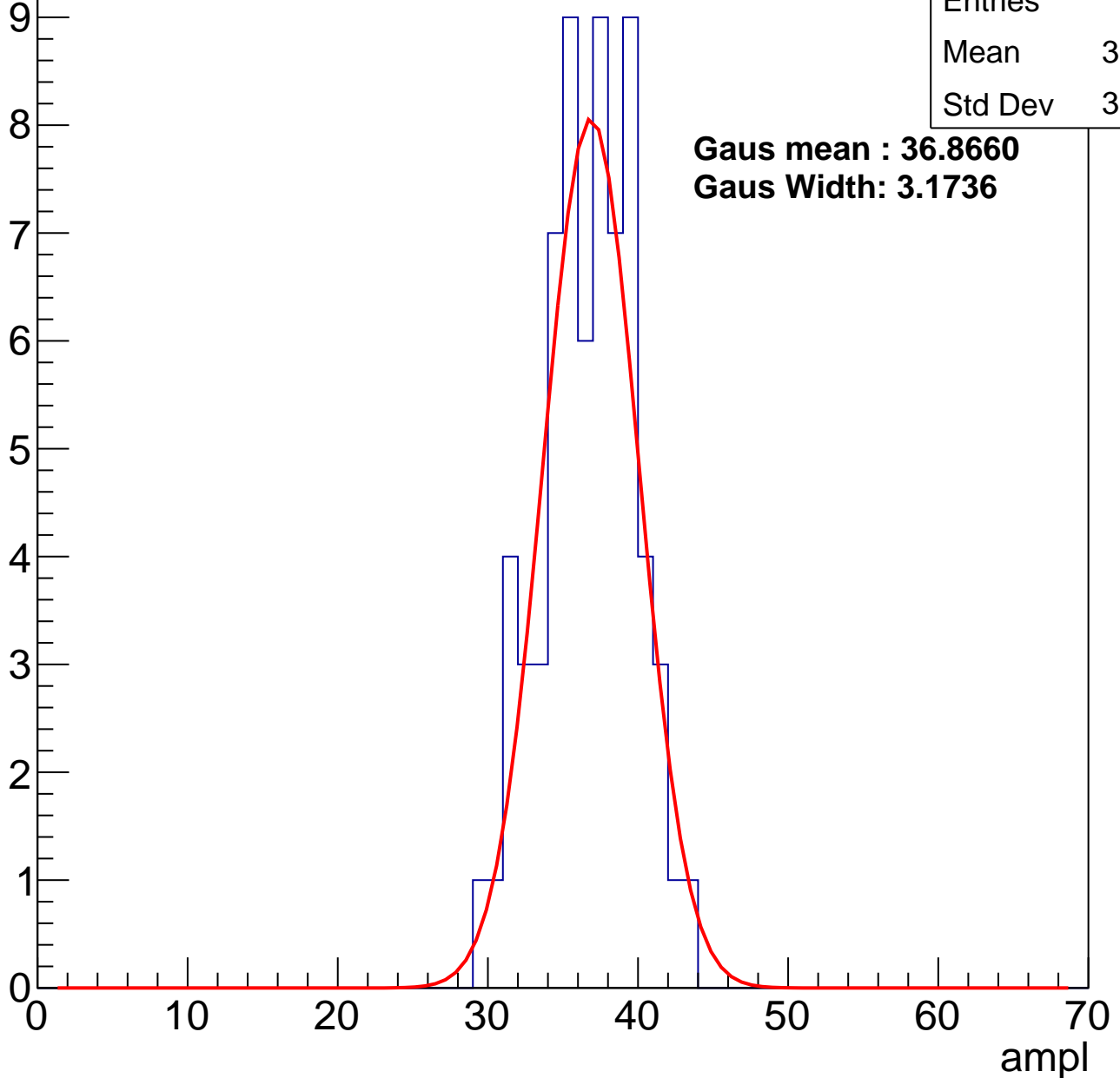
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.25
Std Dev	3.055

Gaus mean : 36.8660

Gaus Width: 3.1736



B1L003S, U19-ch21, adc2

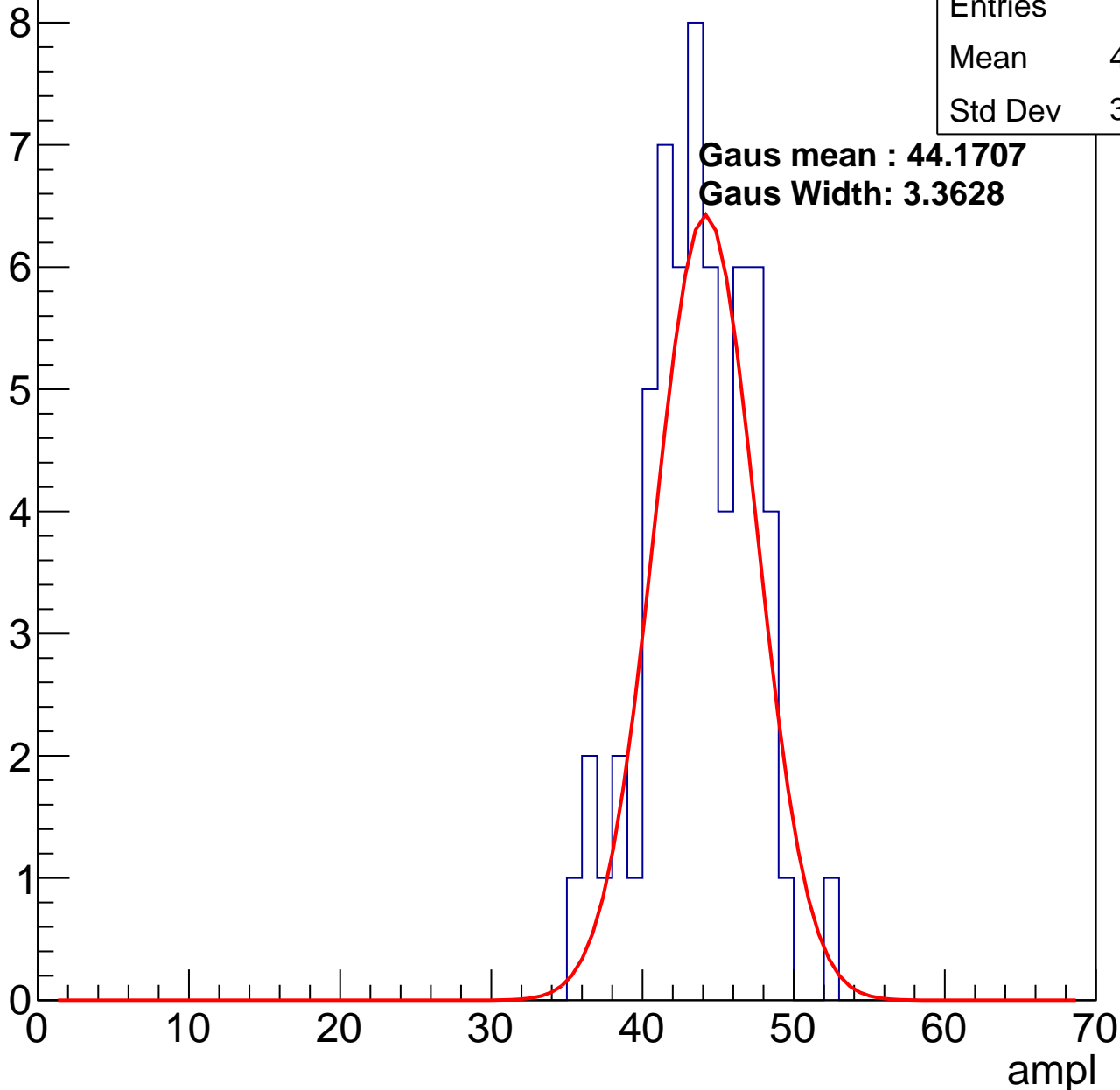
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.23
Std Dev	3.456

Gaus mean : 44.1707

Gaus Width: 3.3628

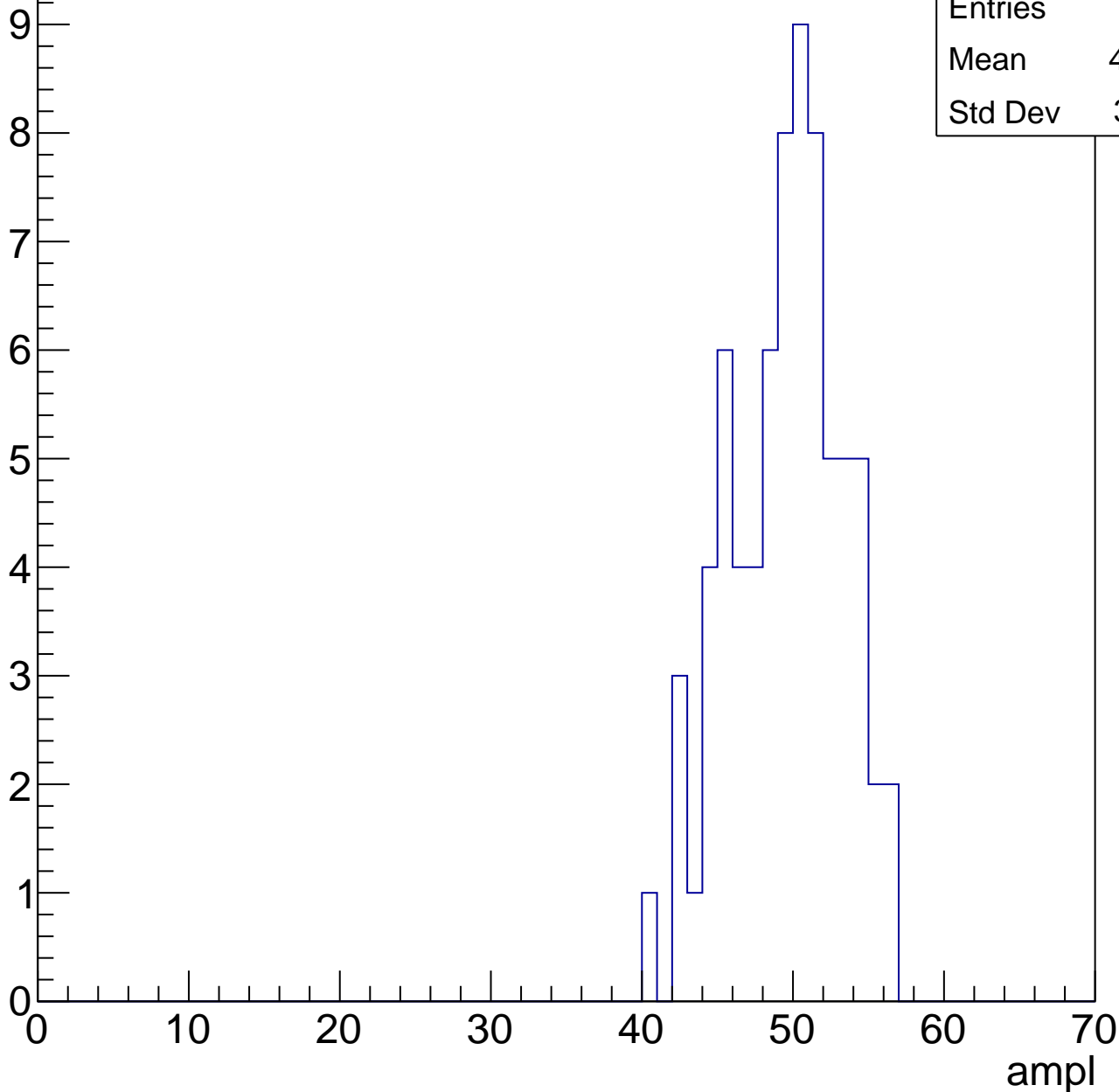


B1L003S, U19-ch21, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

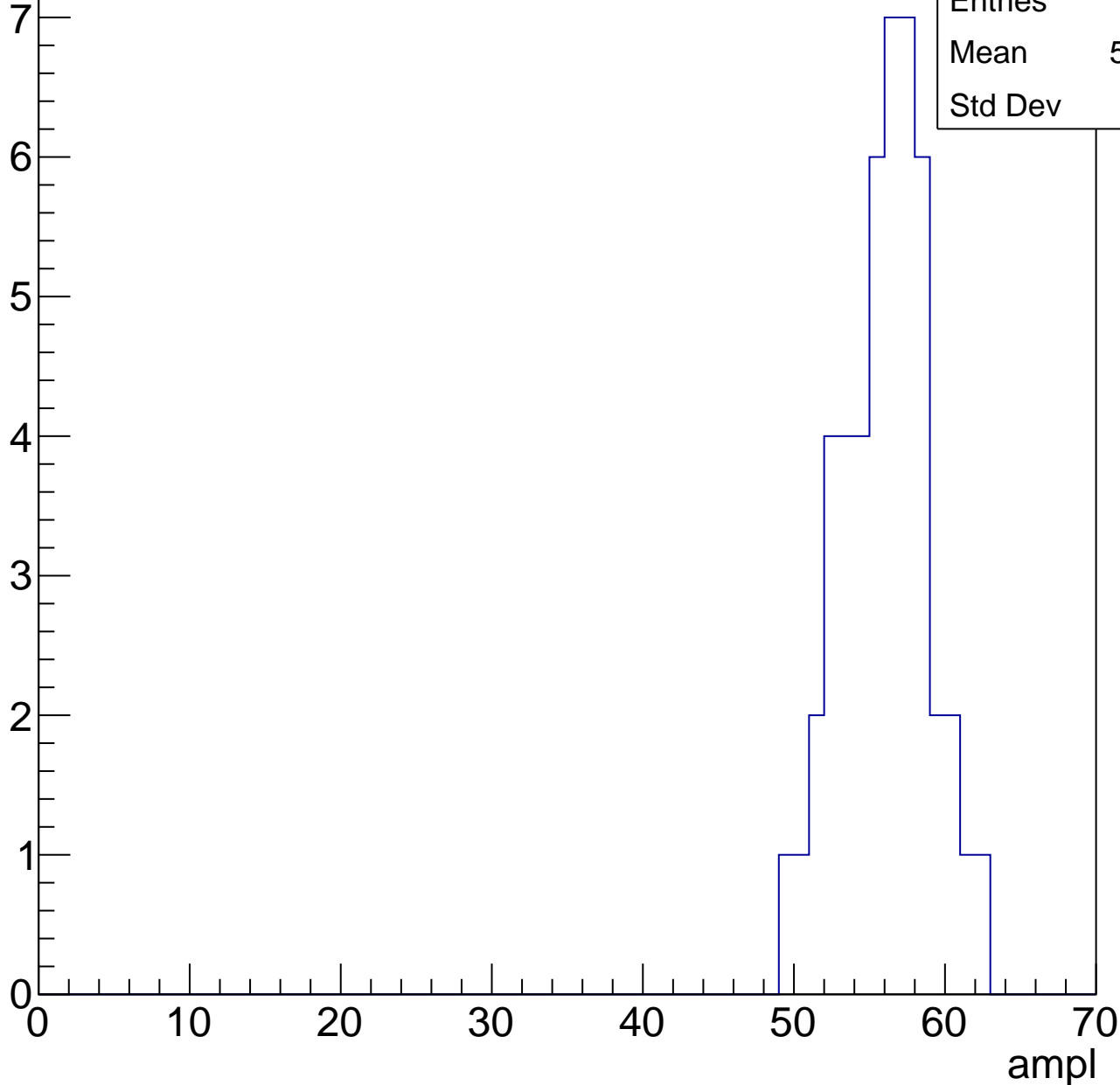
Entries	73
Mean	49.07
Std Dev	3.661



B1L003S, U19-ch21, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

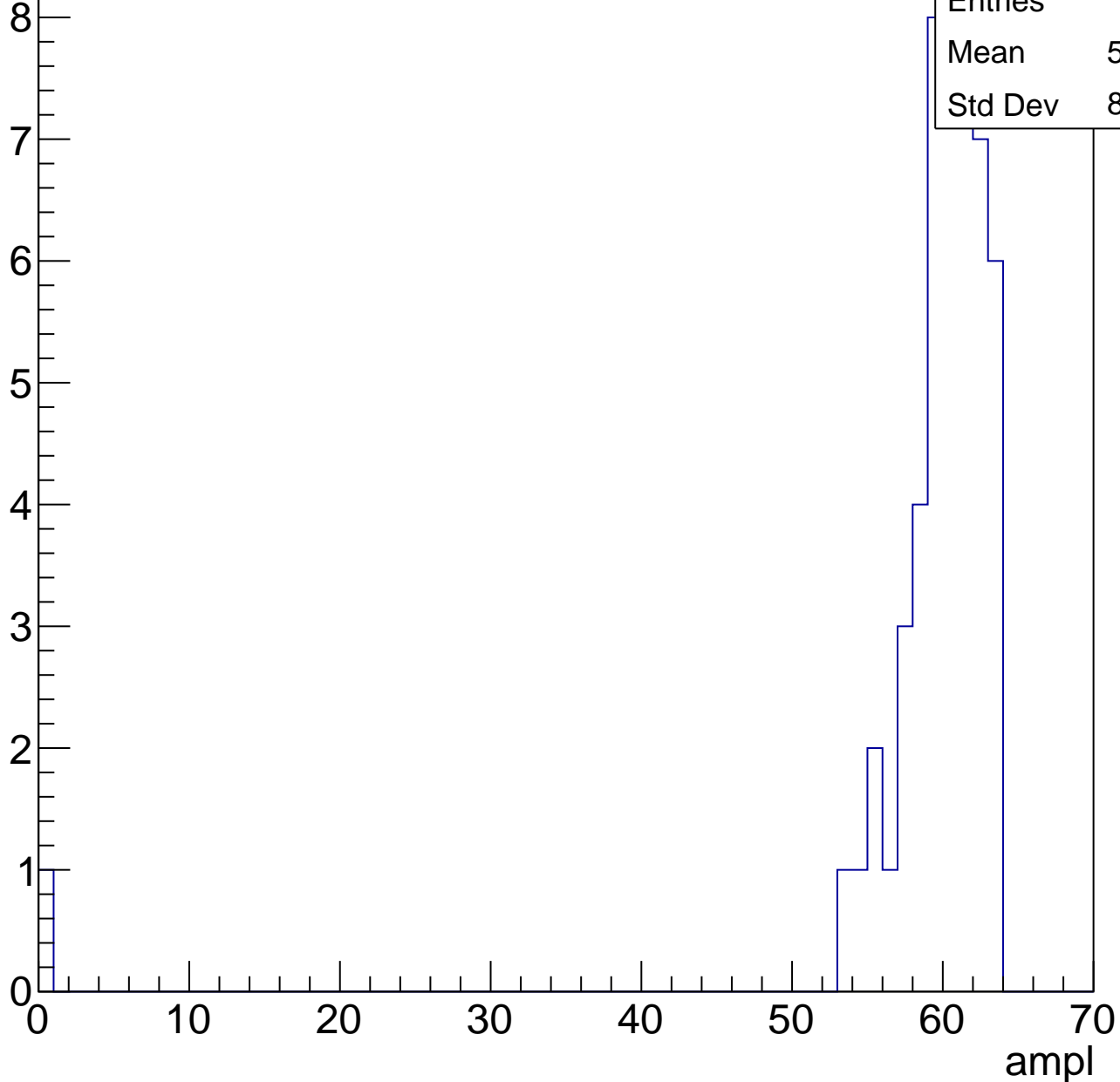


Entries	48
Mean	55.56
Std Dev	2.85

B1L003S, U19-ch21, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	50
Mean	58.56
Std Dev	8.704

B1L003S, U19-ch21, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch21, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U19-ch22, adc0

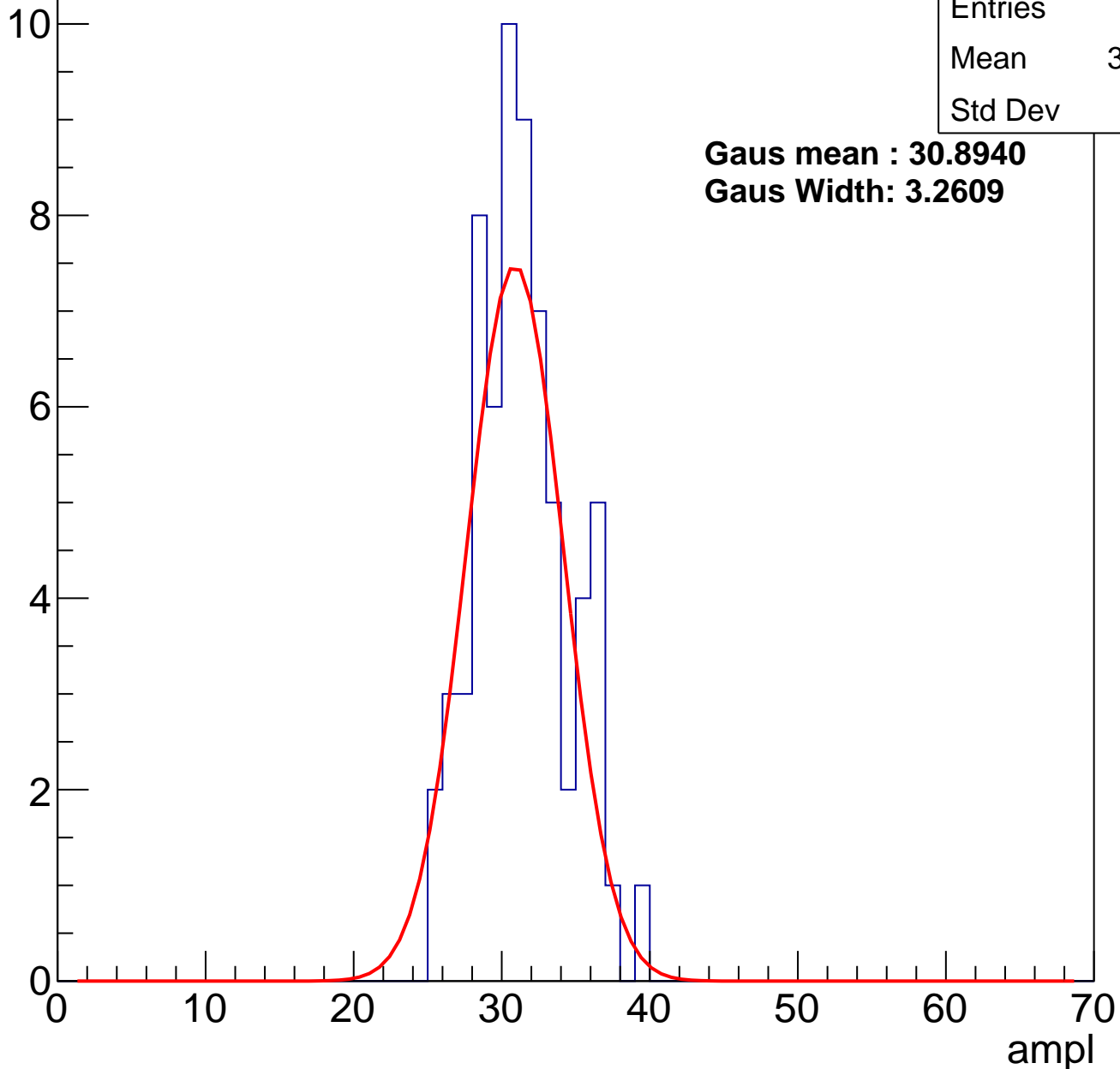
calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	30.89
Std Dev	3.1

Gaus mean : 30.8940

Gaus Width: 3.2609

Entry



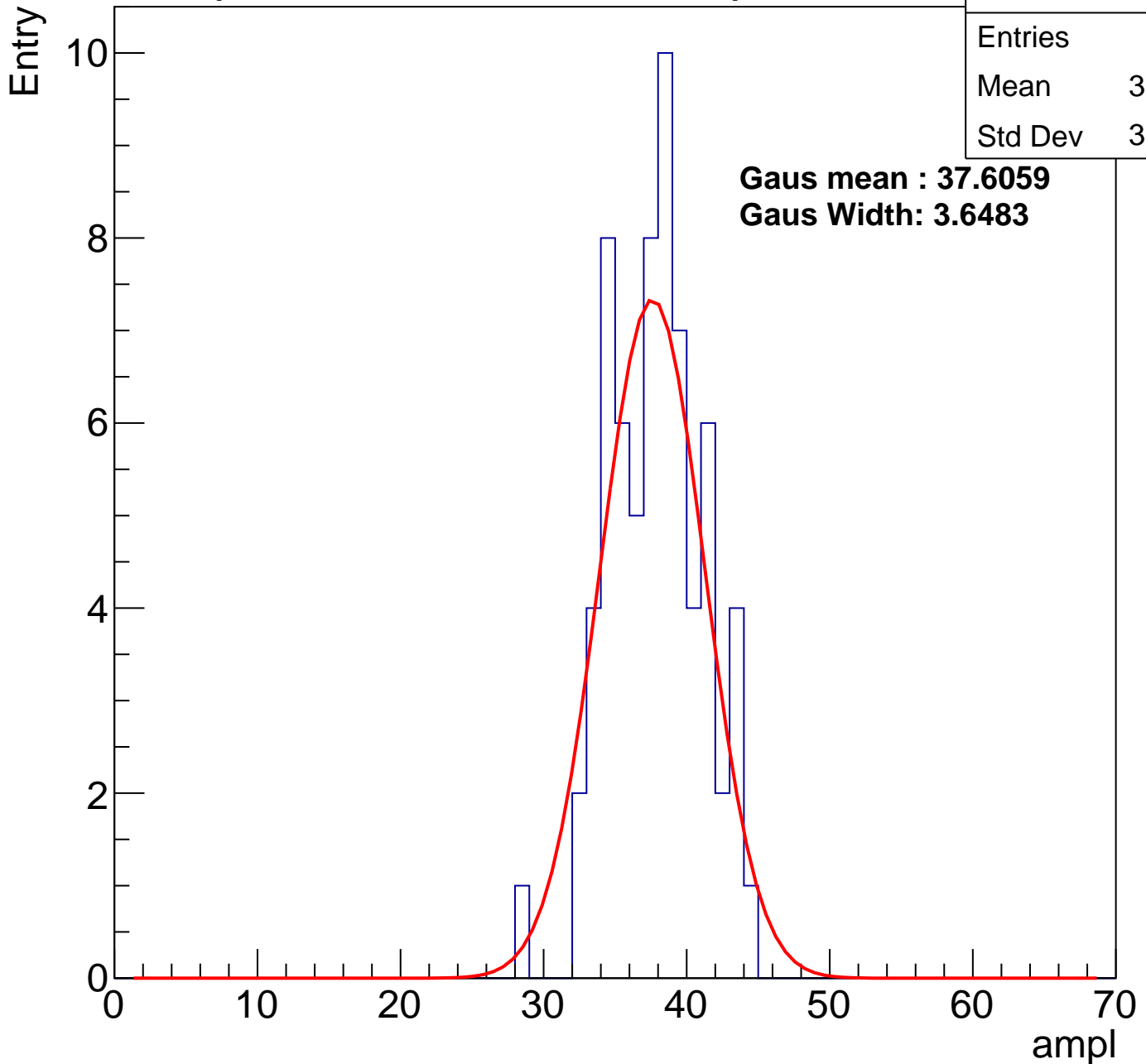
B1L003S, U19-ch22, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	37.37
Std Dev	3.208

Gaus mean : 37.6059

Gaus Width: 3.6483

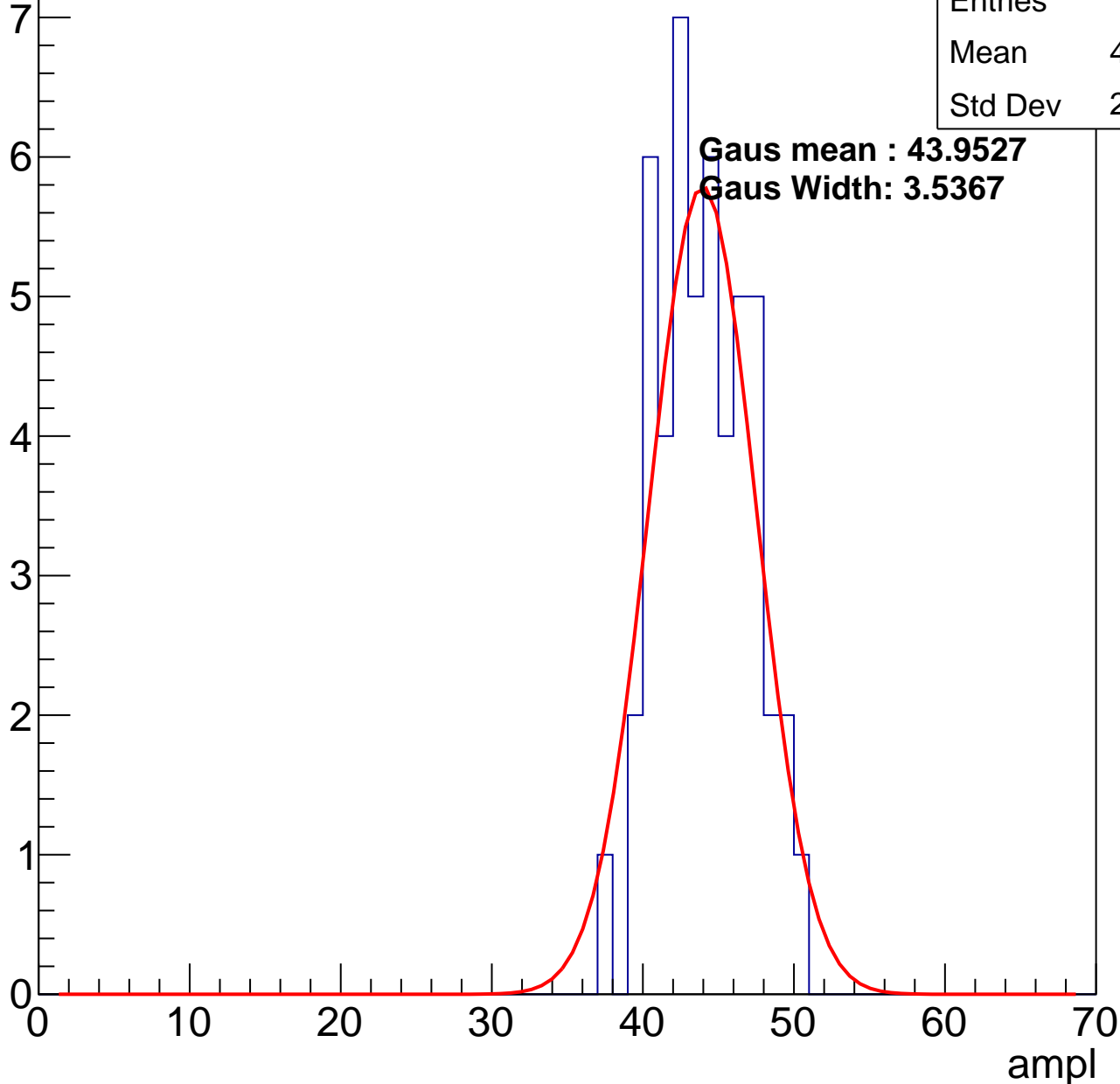


B1L003S, U19-ch22, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.62
Std Dev	2.979

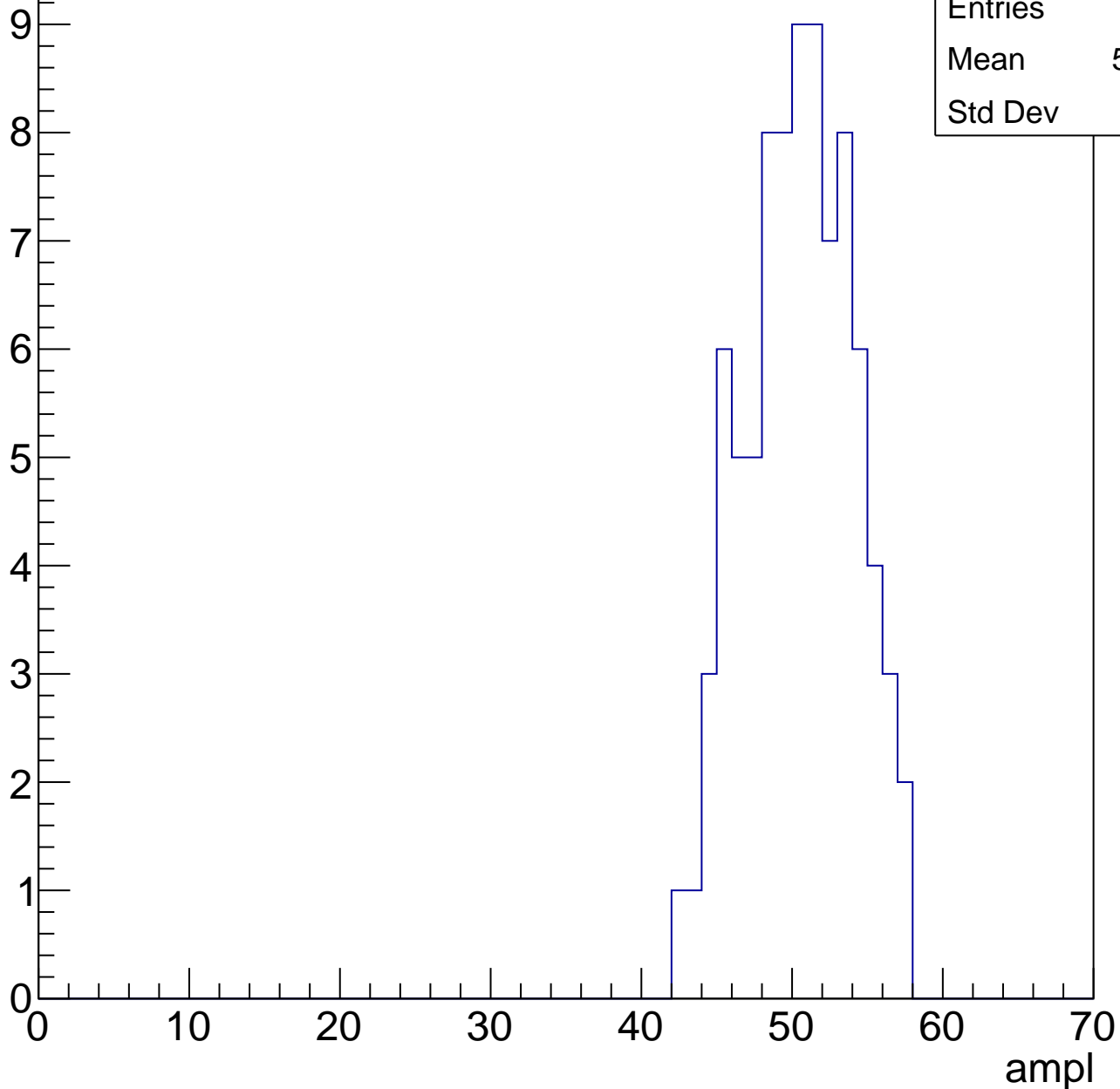


B1L003S, U19-ch22, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	50.01
Std Dev	3.51

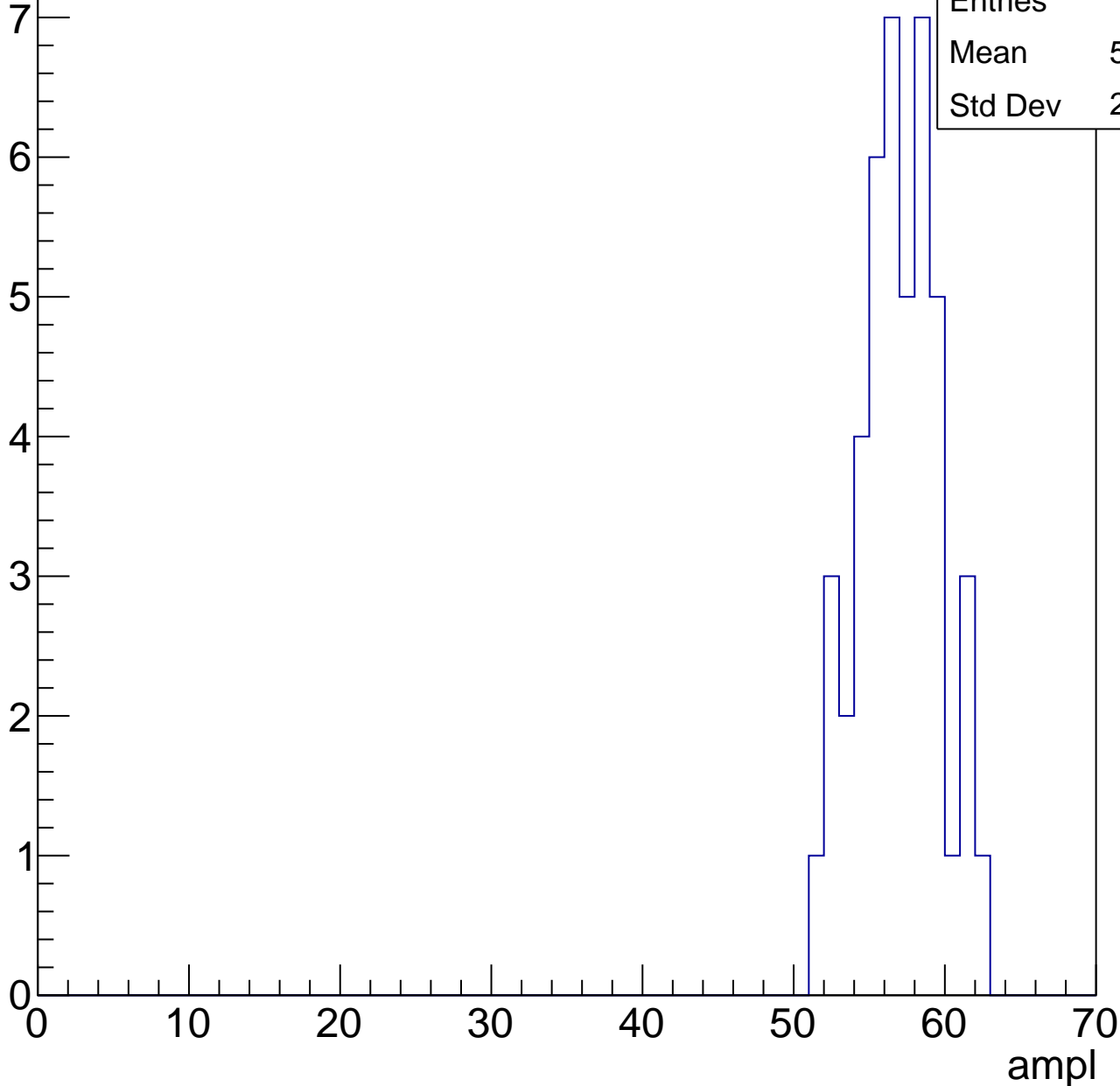


B1L003S, U19-ch22, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	56.49
Std Dev	2.613



B1L003S, U19-ch22, adc5

calib_packv5_042523_0143.root, FC#13, port D2

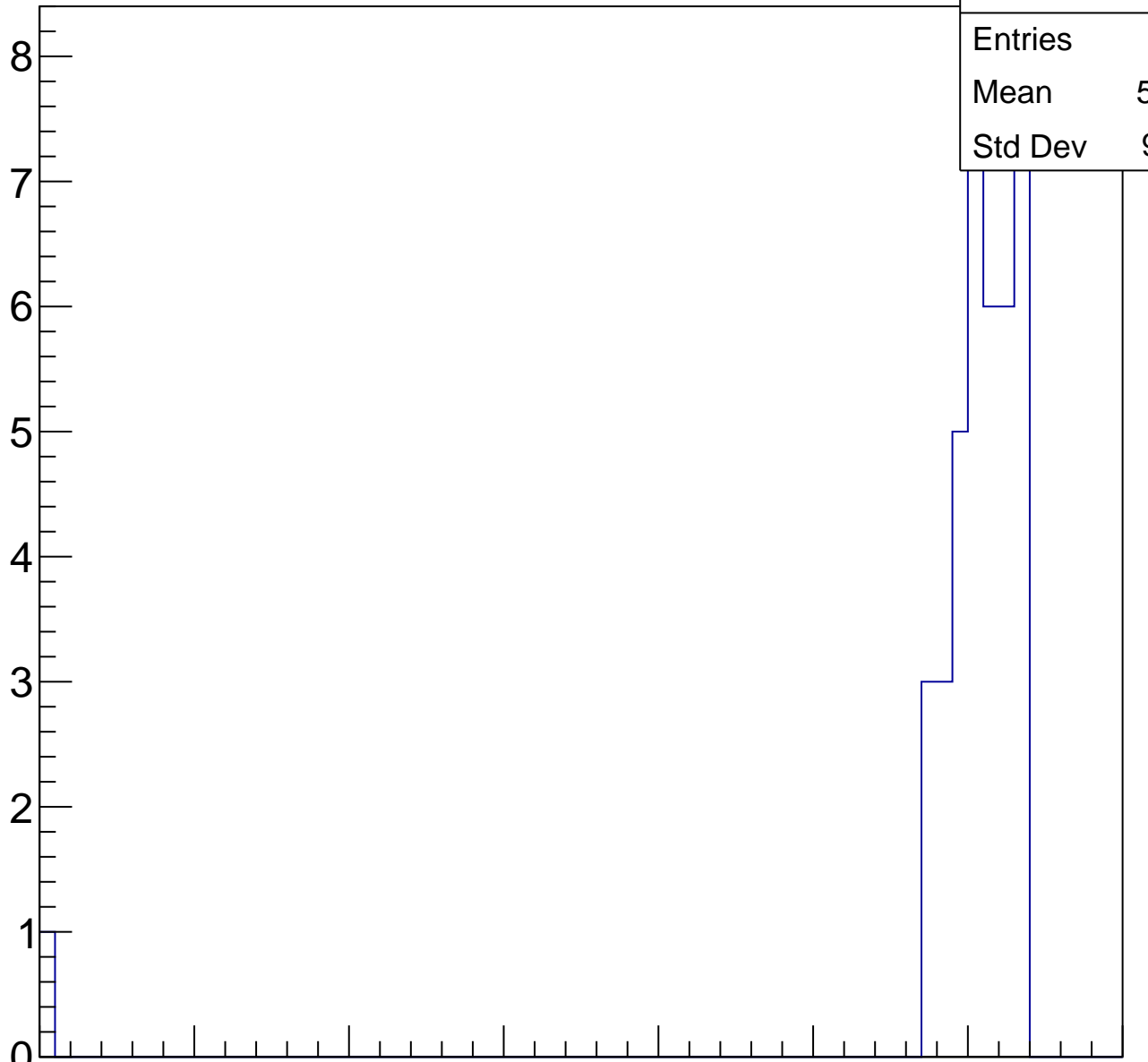
Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.05
Std Dev	9.631

ampl

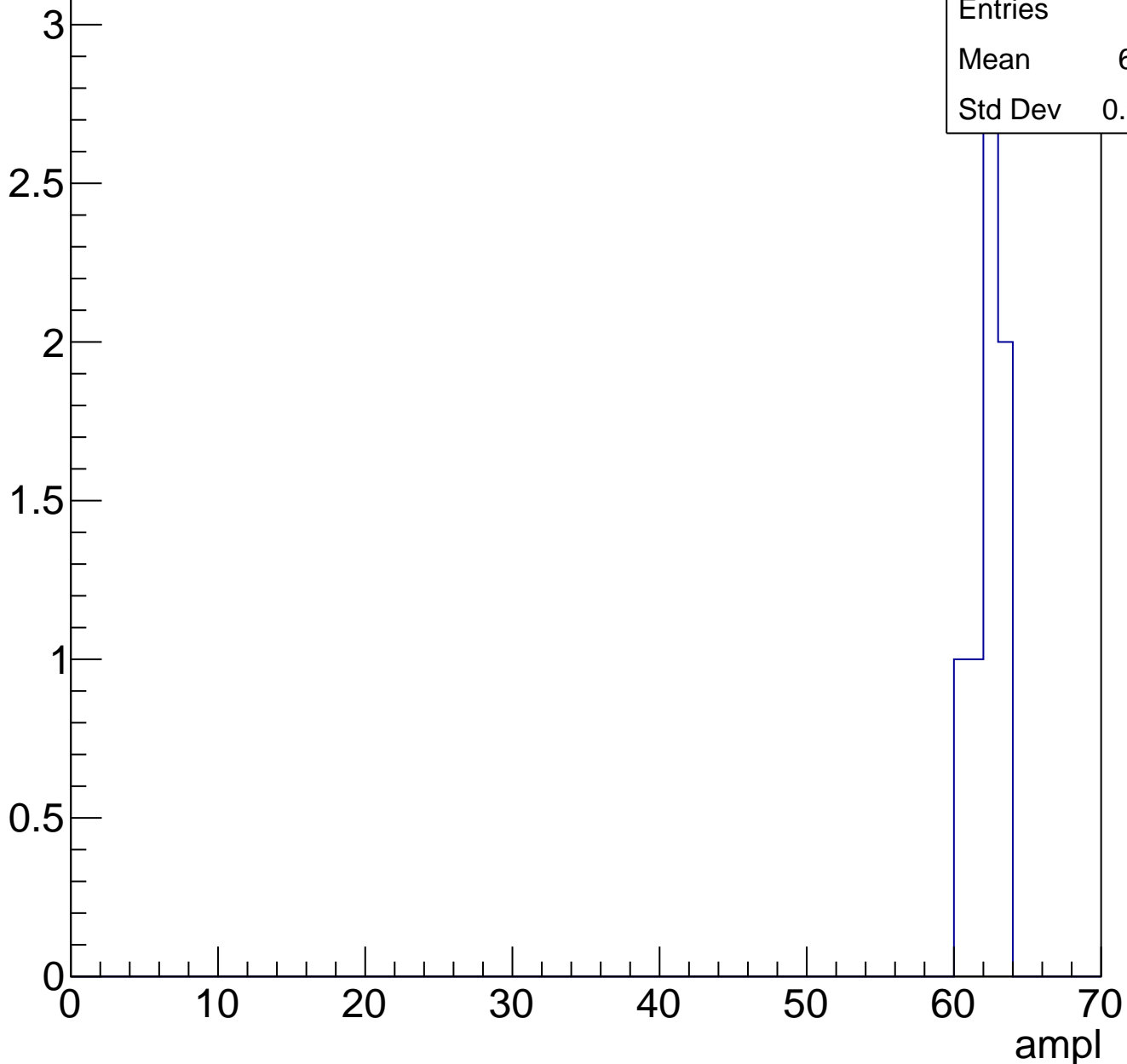
0 10 20 30 40 50 60 70



B1L003S, U19-ch22, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch22, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L003S, U19-ch23, adc0

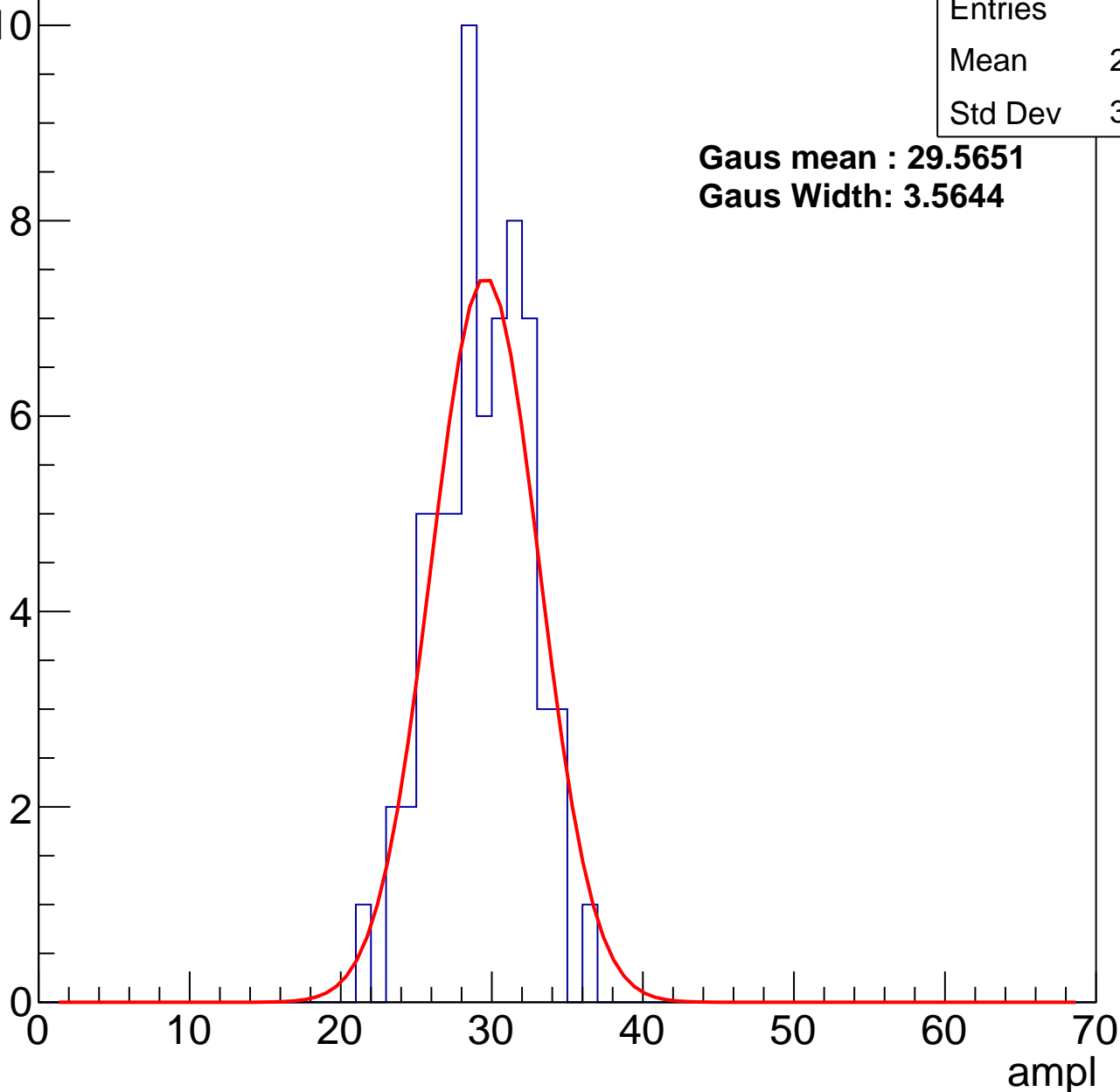
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.89
Std Dev	3.074

Gaus mean : 29.5651

Gaus Width: 3.5644



B1L003S, U19-ch23, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	57
Mean	35.28
Std Dev	2.693

Gaus mean : 35.7959

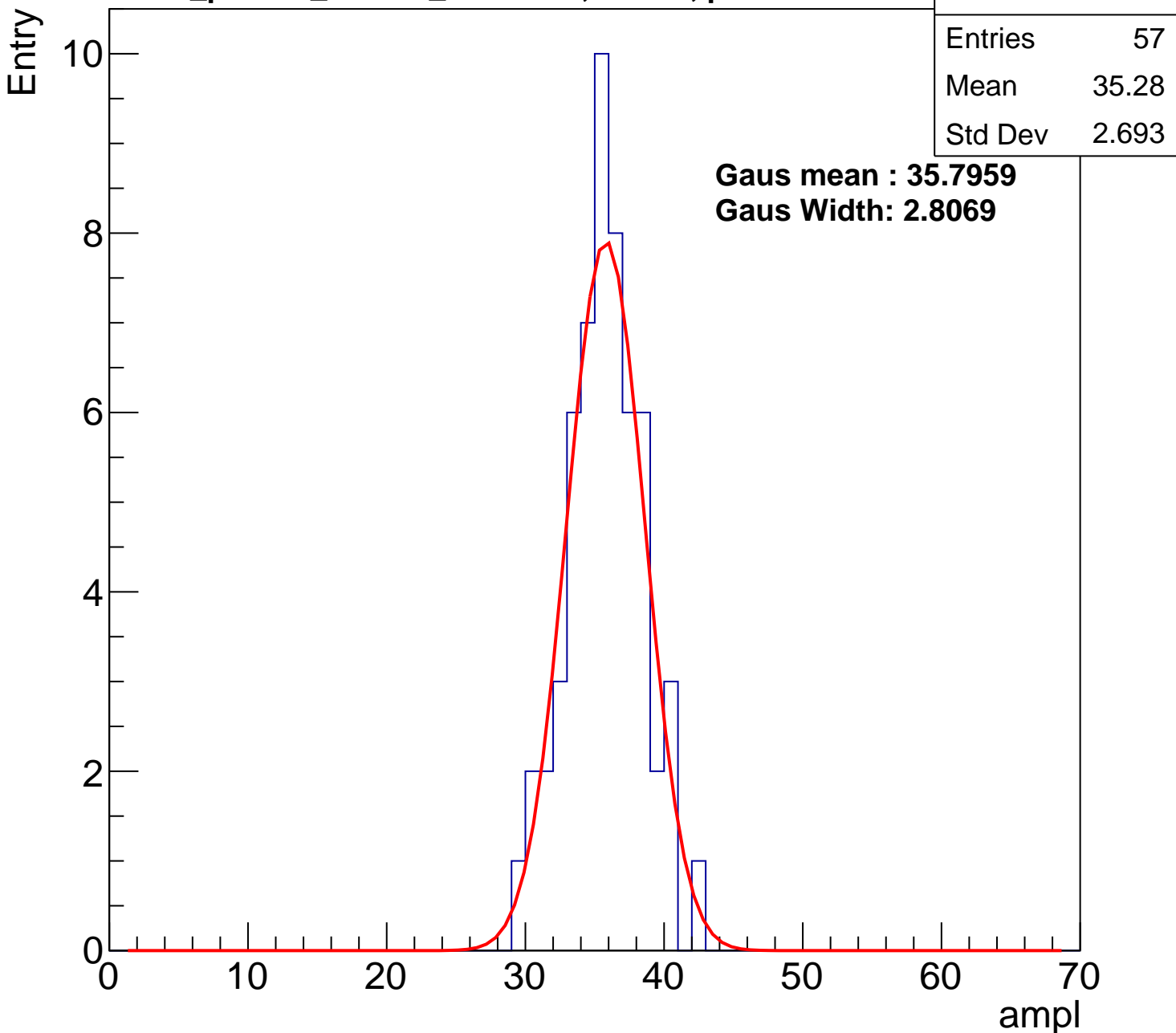
Gaus Width: 2.8069

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U19-ch23, adc2

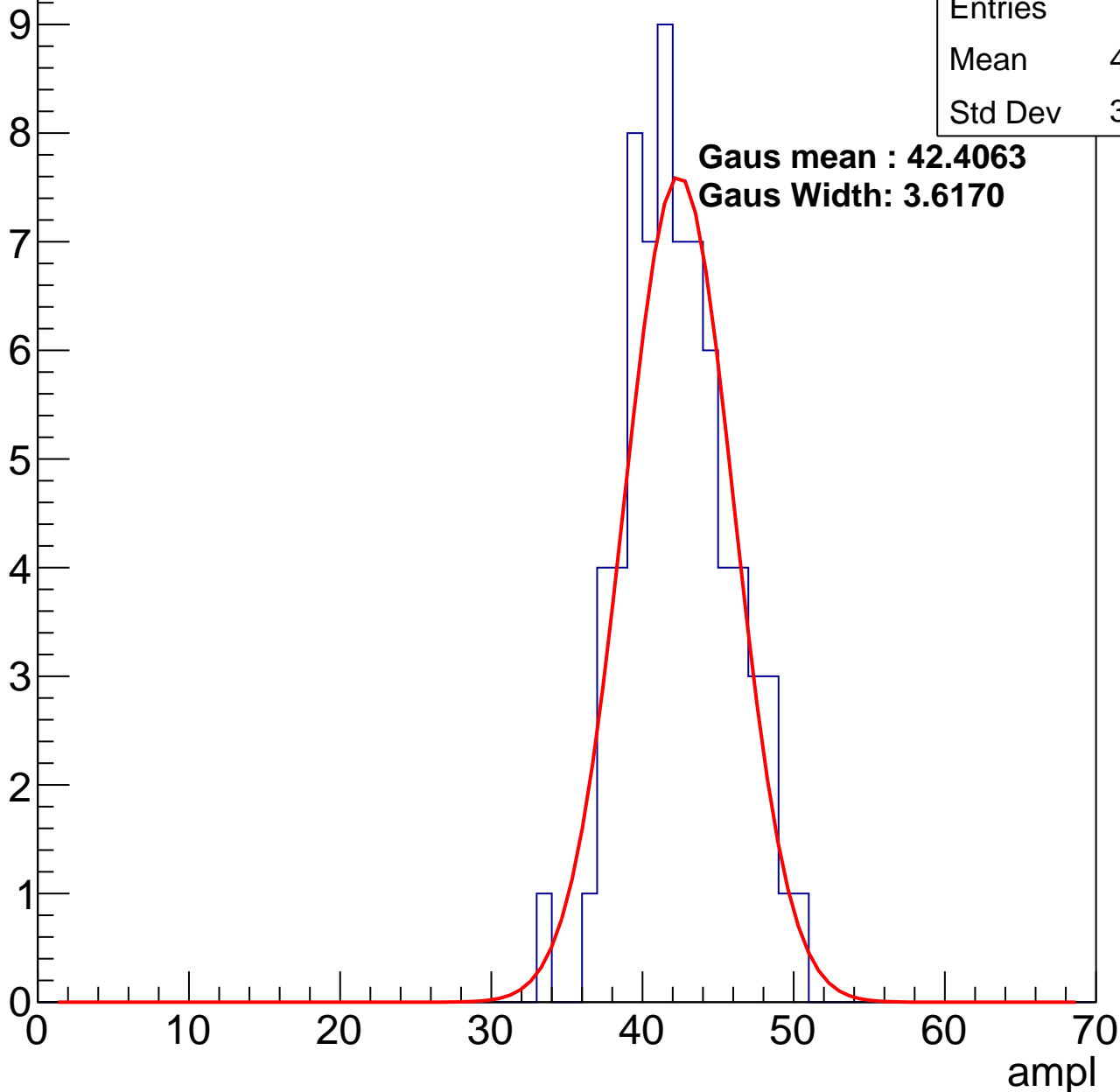
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	41.96
Std Dev	3.416

Gaus mean : 42.4063

Gaus Width: 3.6170

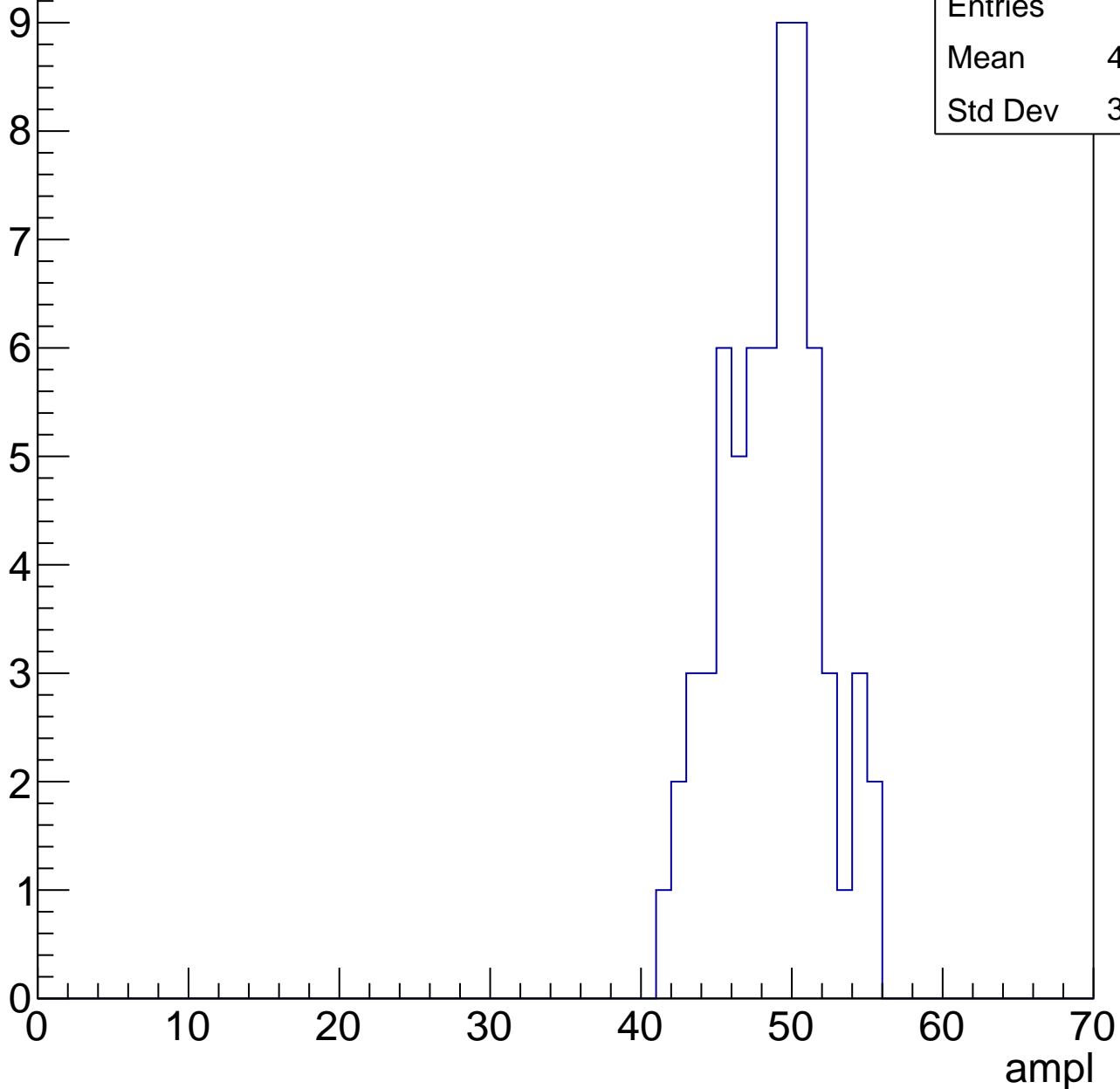


B1L003S, U19-ch23, adc3

calib_packv5_042523_0143.root, FC#13, port D2

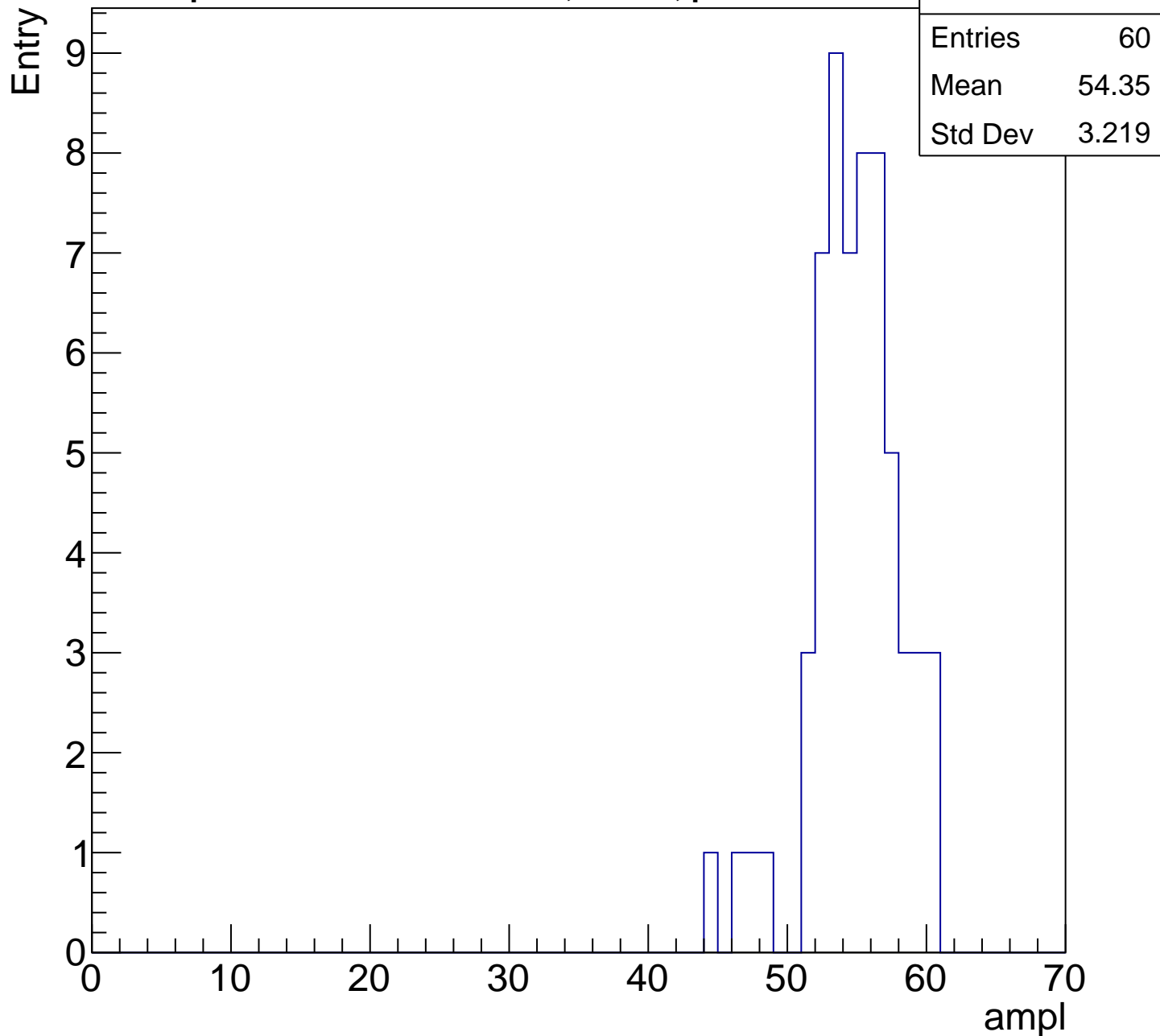
Entry

Entries	65
Mean	48.22
Std Dev	3.279



B1L003S, U19-ch23, adc4

calib_packv5_042523_0143.root, FC#13, port D2

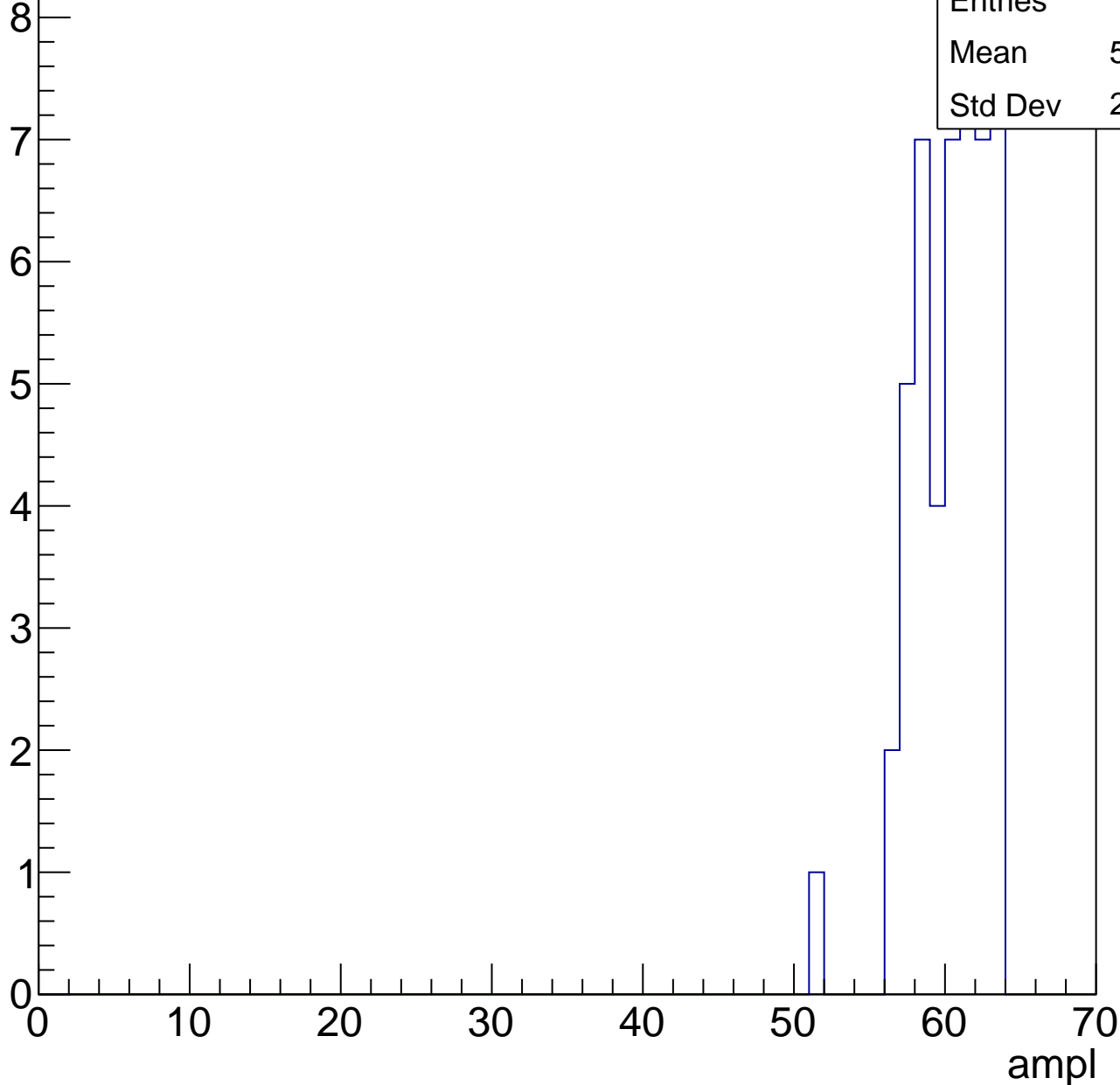


B1L003S, U19-ch23, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.92
Std Dev	2.465



B1L003S, U19-ch23, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	55.55
Std Dev	17.62

B1L003S, U19-ch23, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch24, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	28.07
Std Dev	4.657

Gaus mean : 28.6328

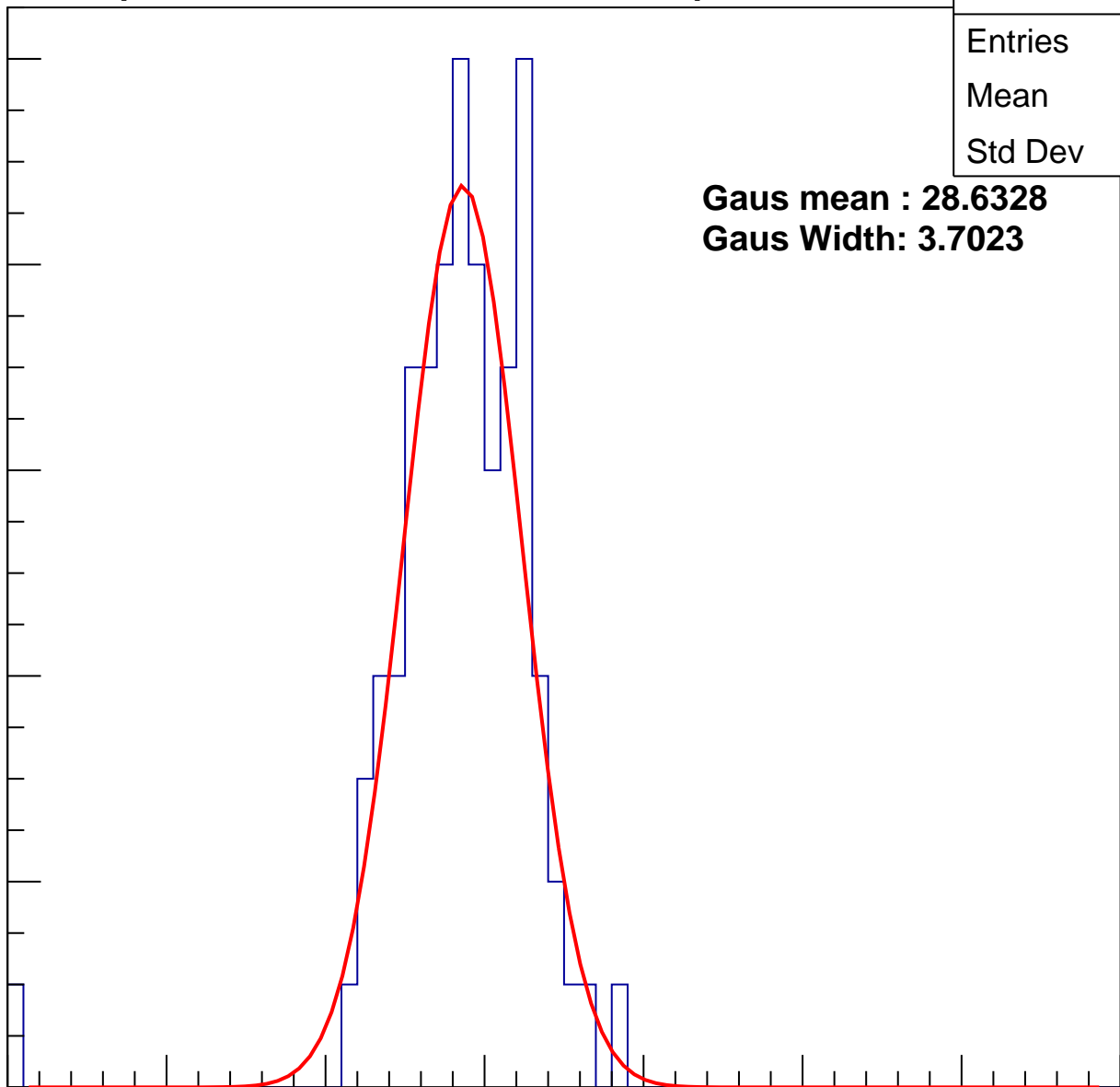
Gaus Width: 3.7023

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch24, adc1

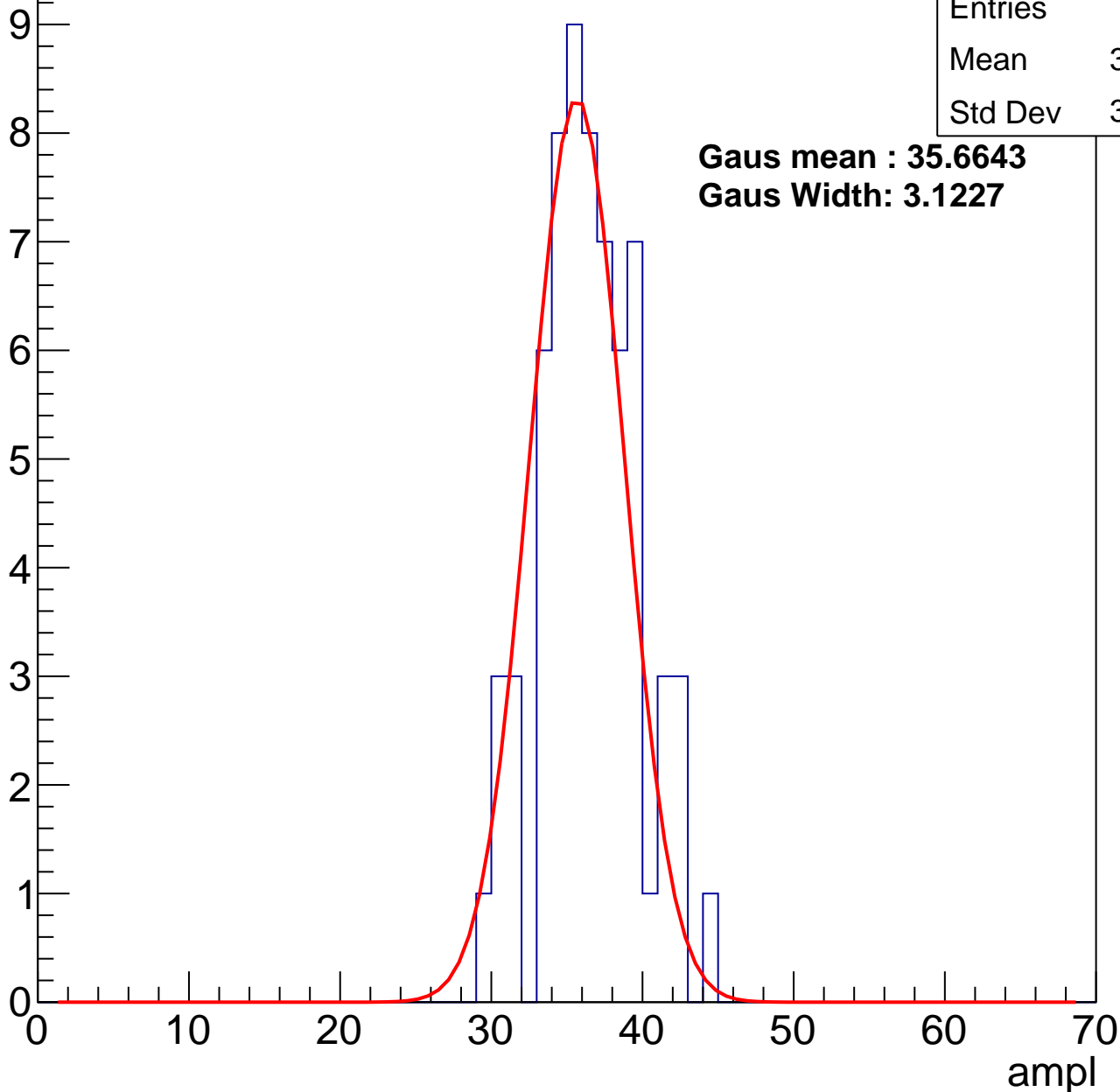
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.03
Std Dev	3.219

Gaus mean : 35.6643

Gaus Width: 3.1227



B1L003S, U19-ch24, adc2

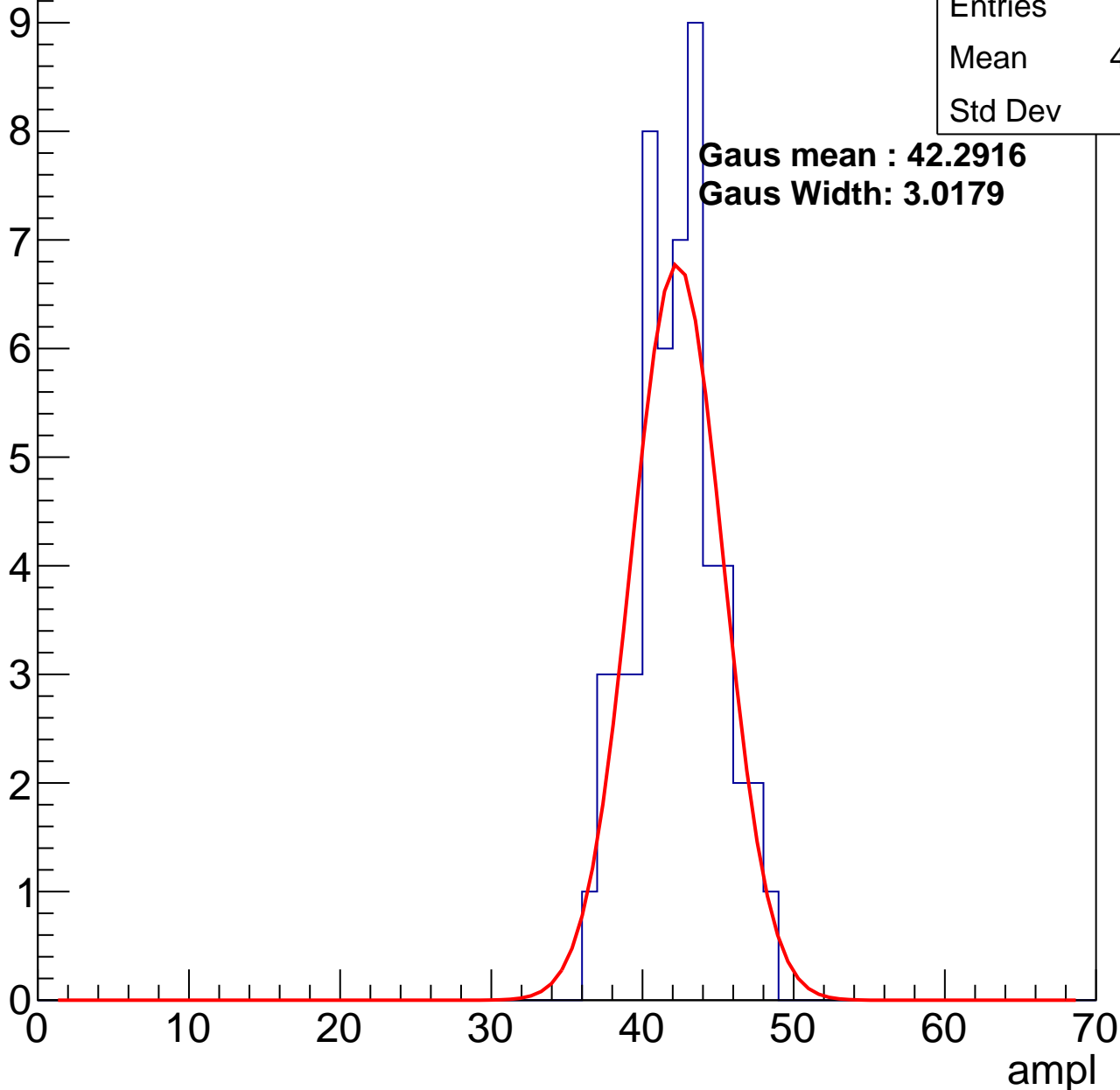
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	41.79
Std Dev	2.75

Gaus mean : 42.2916

Gaus Width: 3.0179

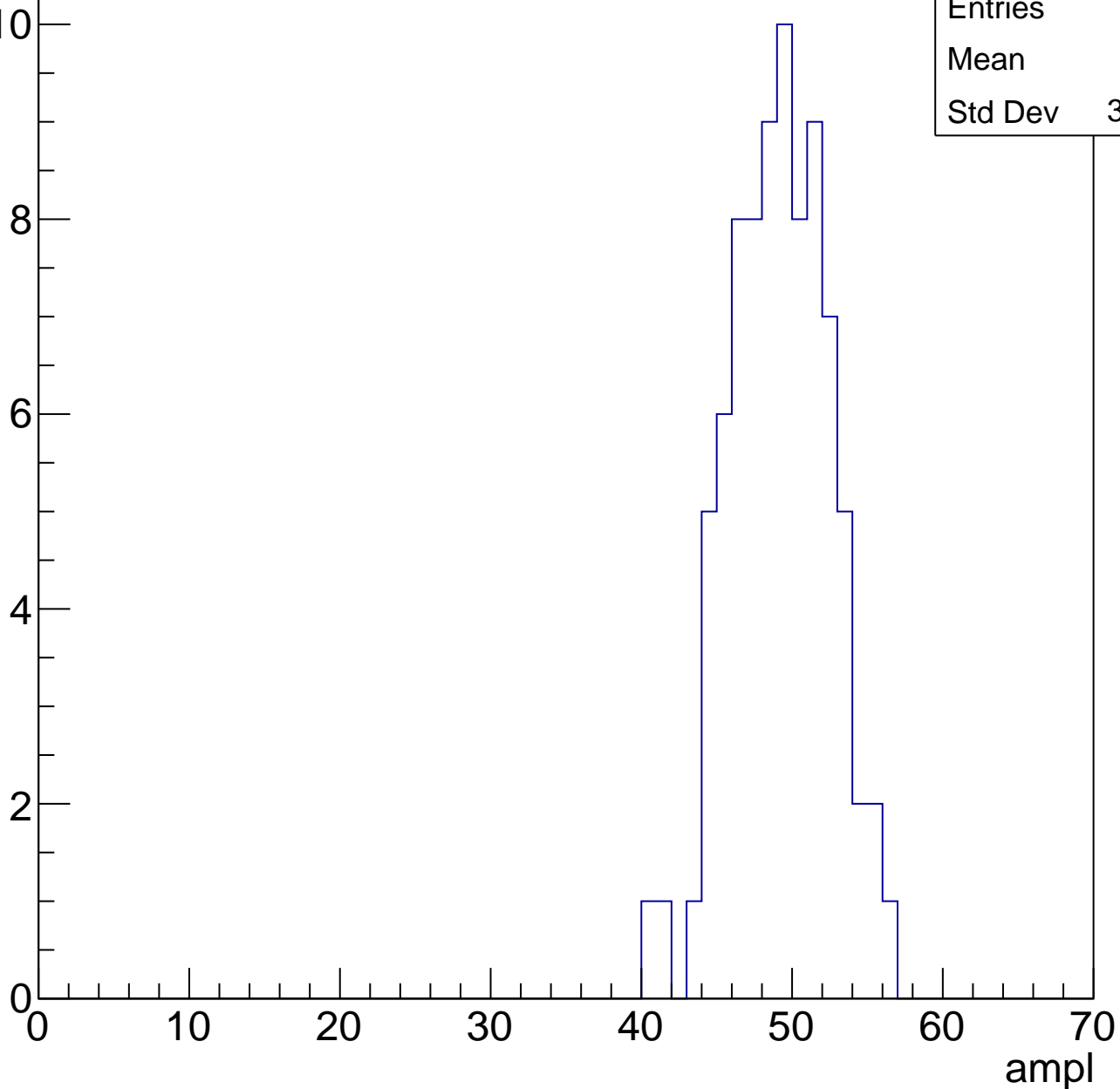


B1L003S, U19-ch24, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

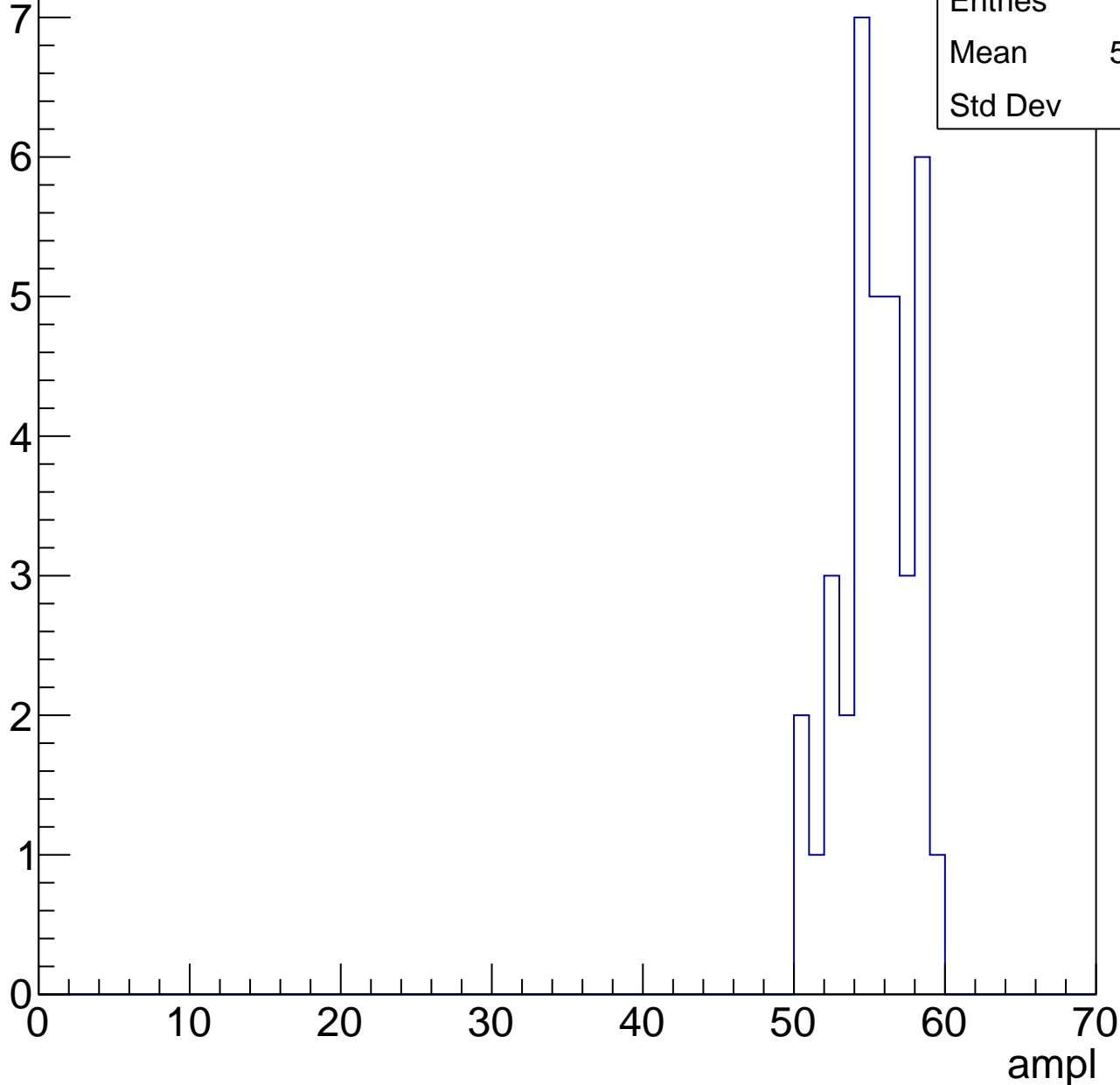
Entries	83
Mean	48.7
Std Dev	3.229



B1L003S, U19-ch24, adc4

calib_packv5_042523_0143.root, FC#13, port D2

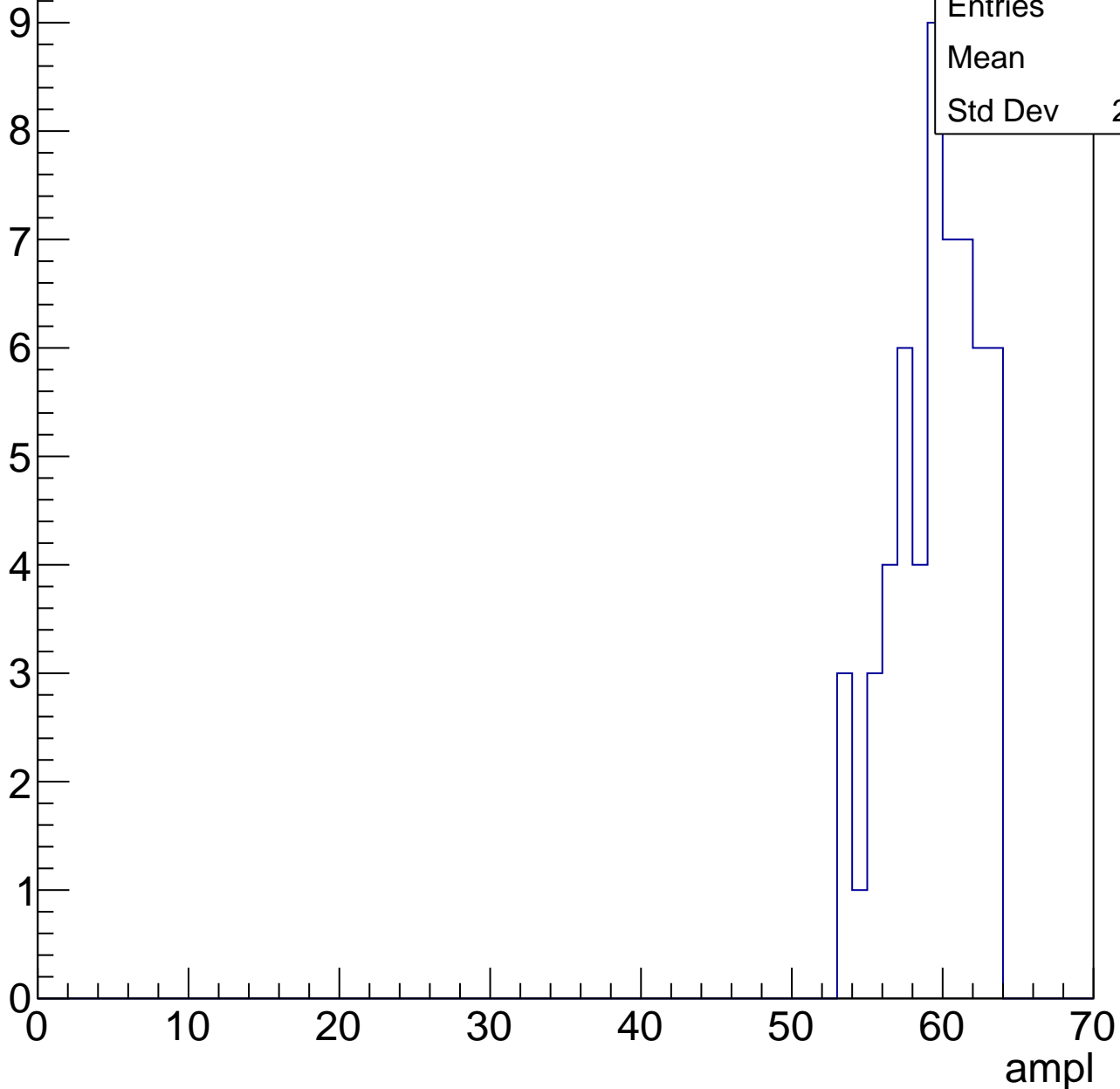
Entry



B1L003S, U19-ch24, adc5

calib_packv5_042523_0143.root, FC#13, port D2

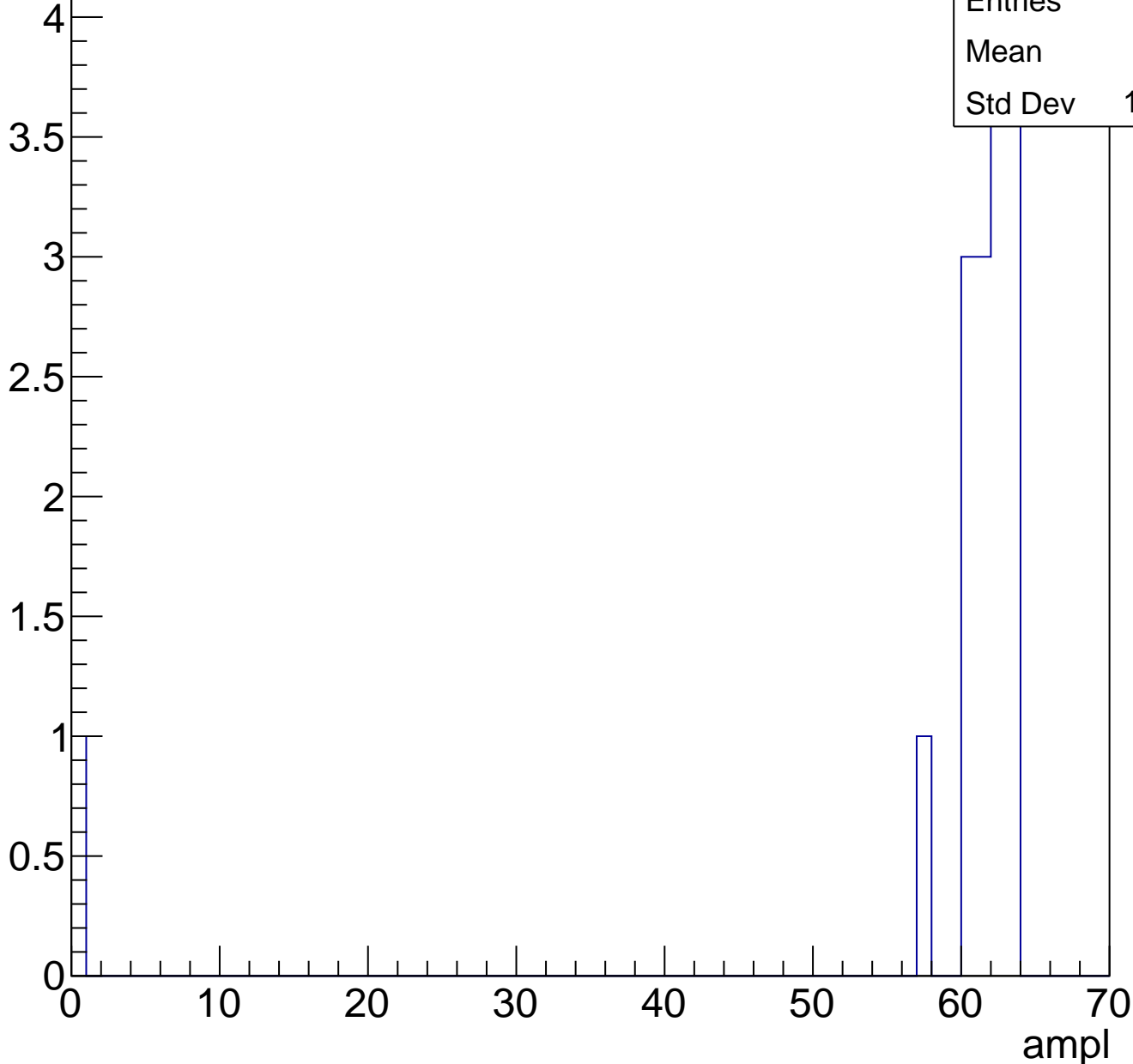
Entry



B1L003S, U19-ch24, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch24, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch25, adc0

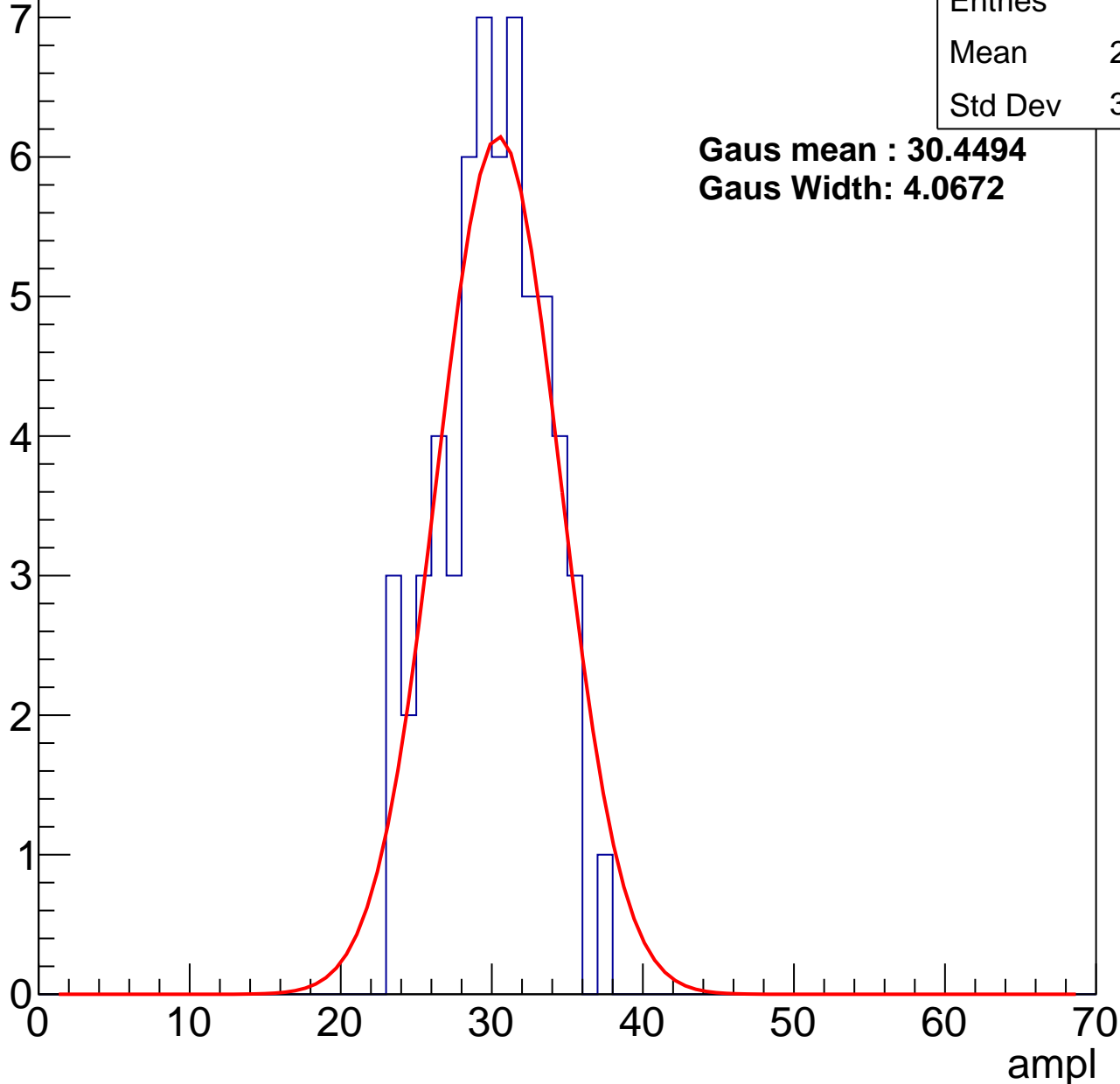
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.63
Std Dev	3.364

Gaus mean : 30.4494

Gaus Width: 4.0672



B1L003S, U19-ch25, adc1

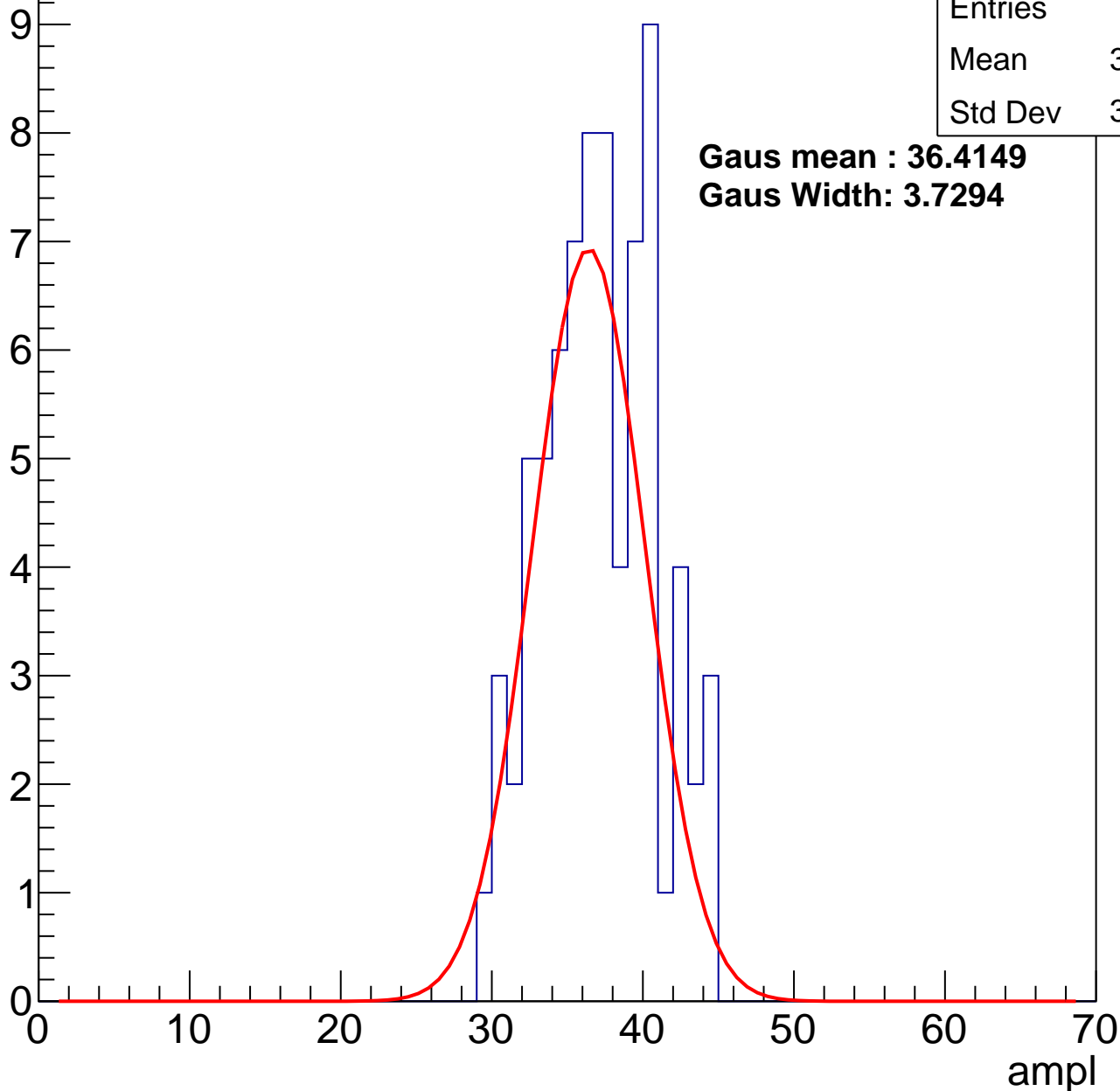
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	36.68
Std Dev	3.685

Gaus mean : 36.4149

Gaus Width: 3.7294



B1L003S, U19-ch25, adc2

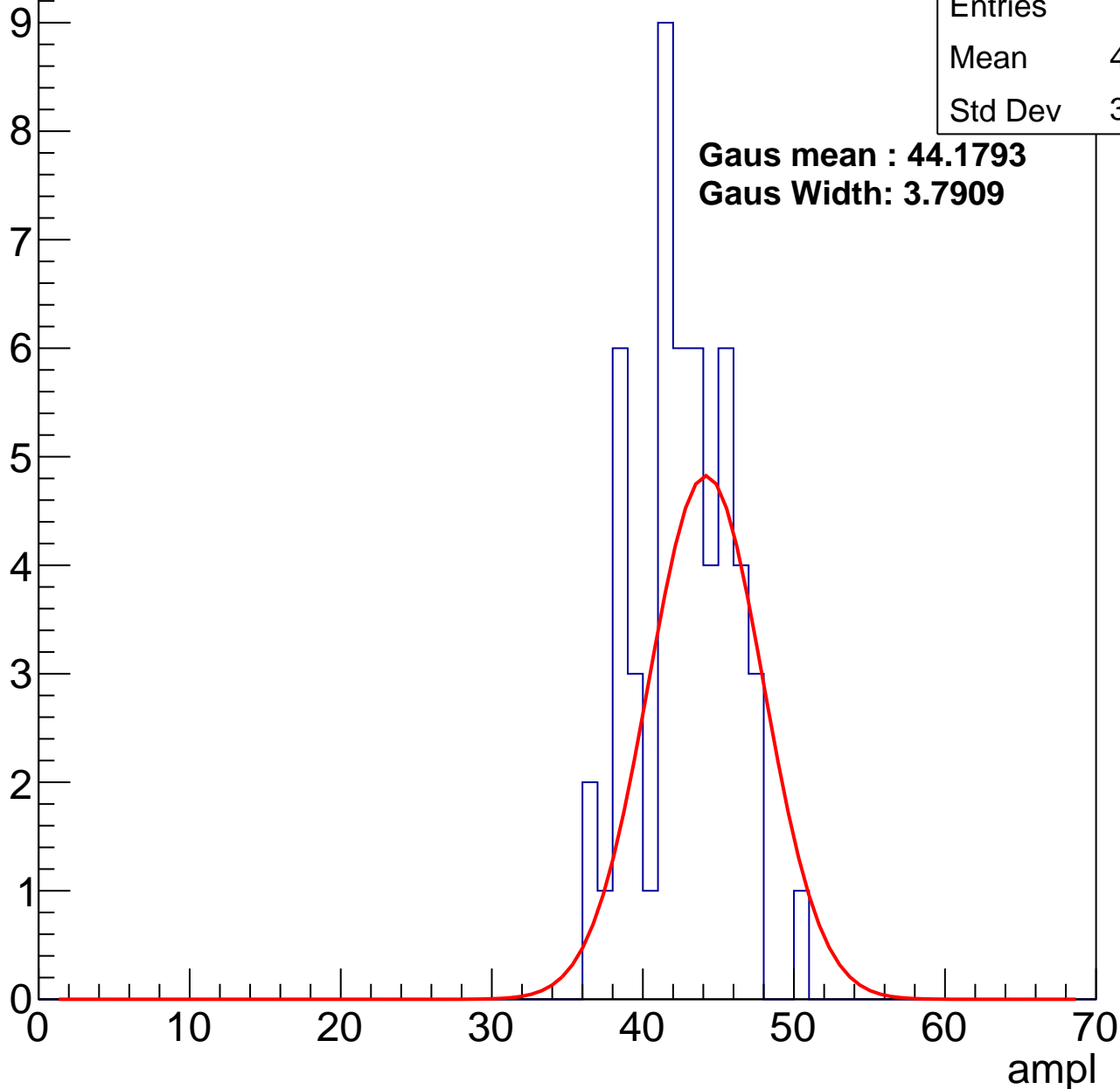
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.19
Std Dev	3.132

Gaus mean : 44.1793

Gaus Width: 3.7909

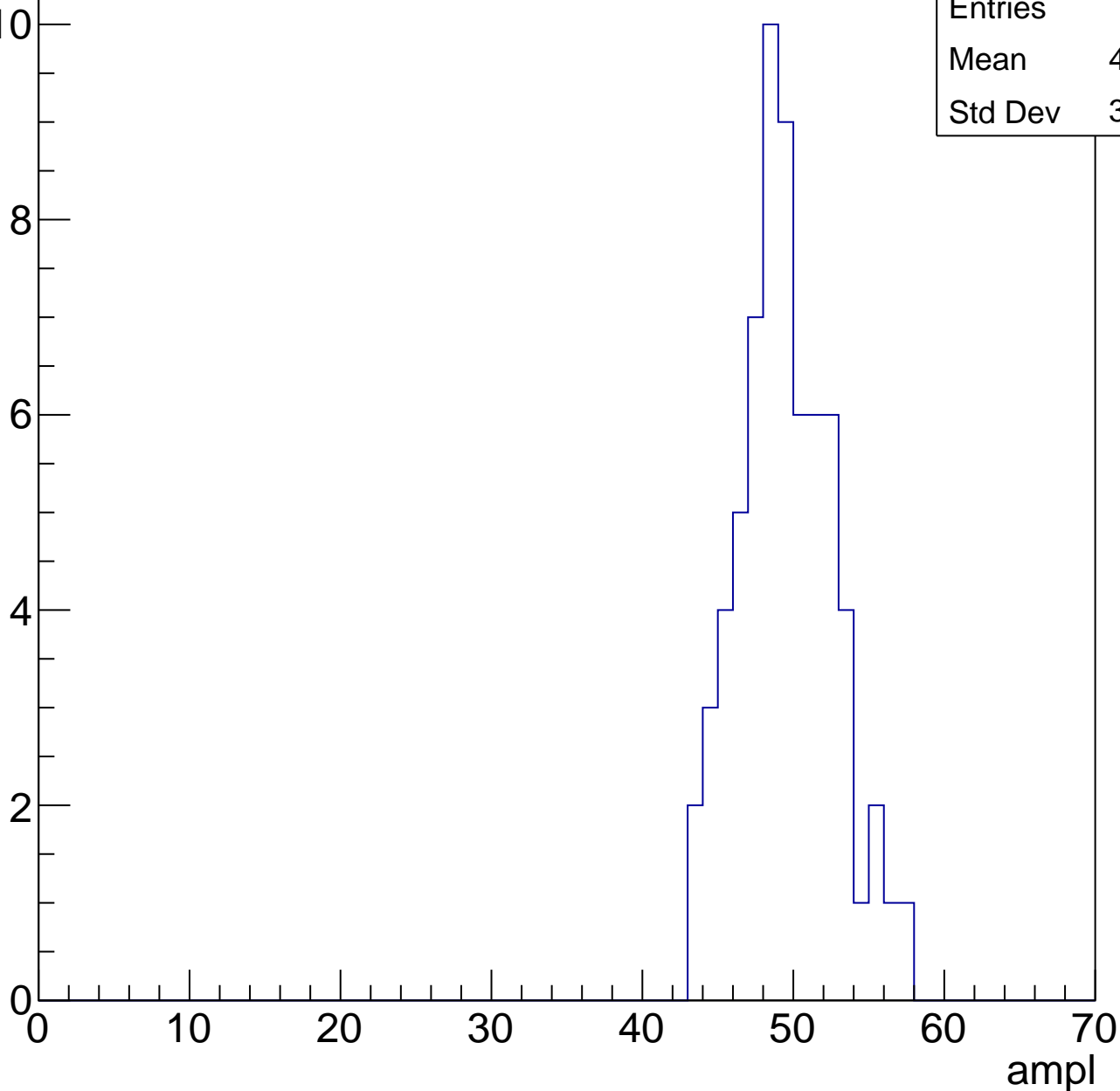


B1L003S, U19-ch25, adc3

calib_packv5_042523_0143.root, FC#13, port D2

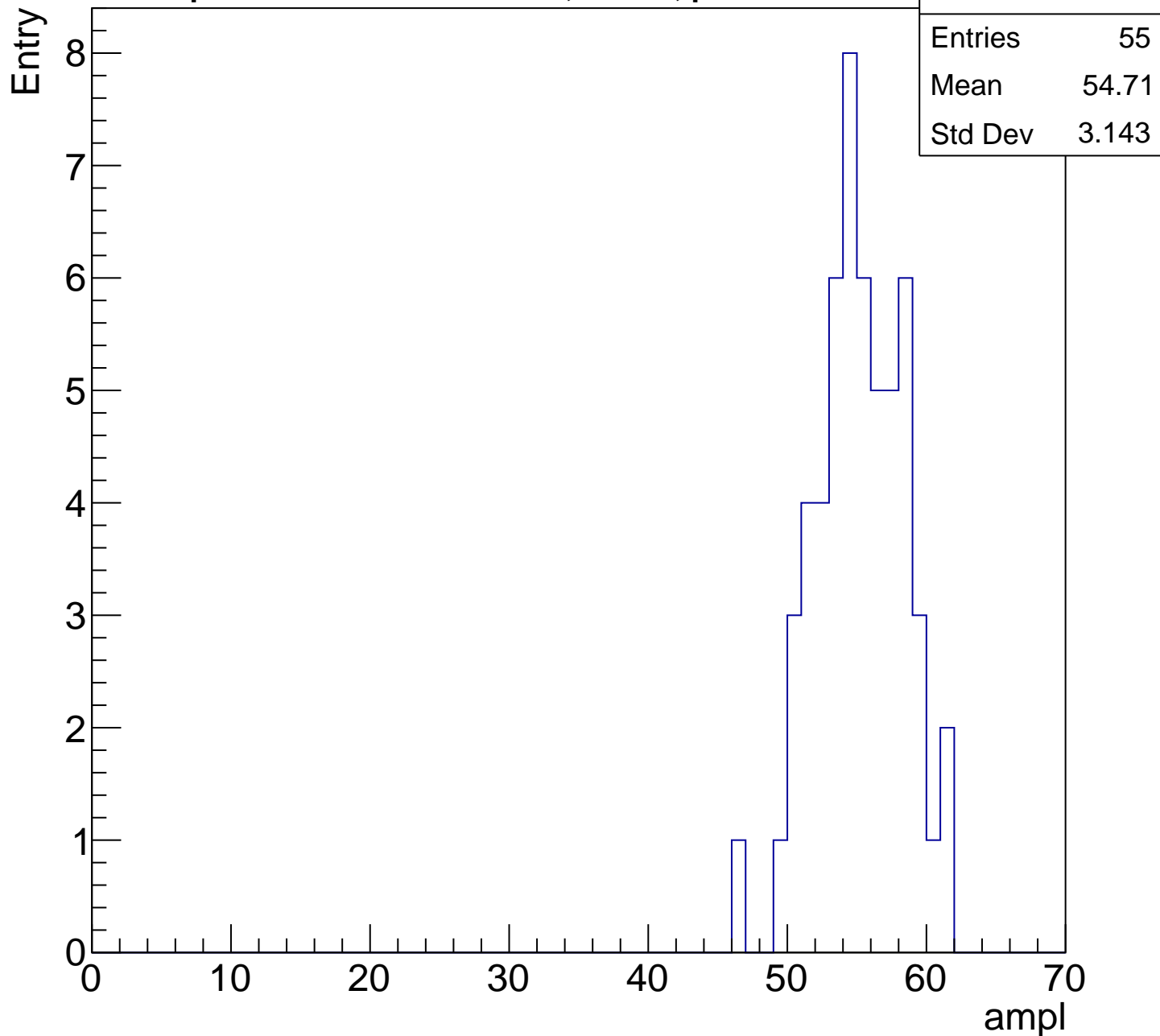
Entry

Entries	67
Mean	49.03
Std Dev	3.119



B1L003S, U19-ch25, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch25, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.73
Std Dev	8.271

ampl

0

10

20

30

40

50

60

70

B1L003S, U19-ch25, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch25, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch26, adc0

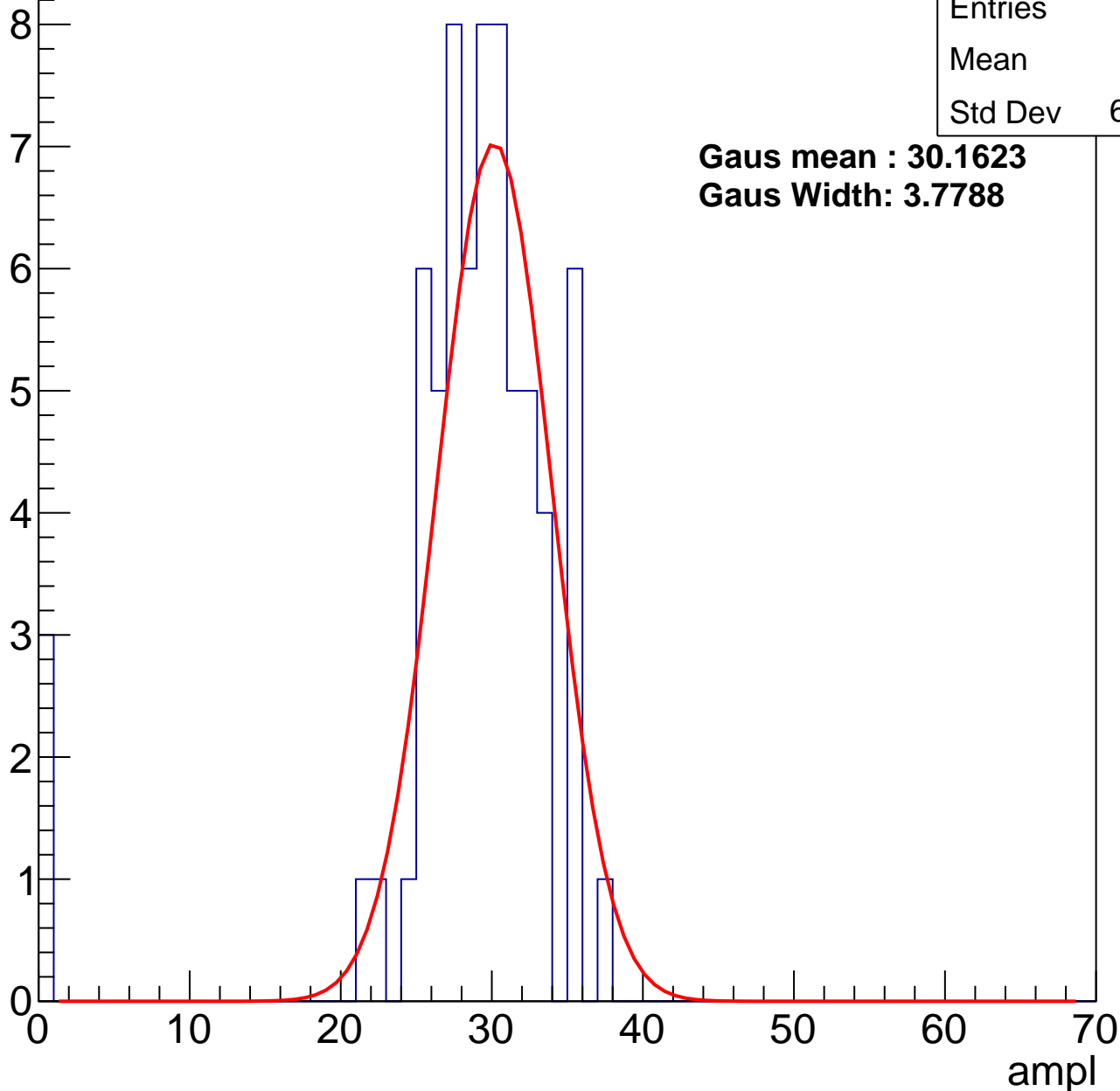
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	27.9
Std Dev	6.832

Gaus mean : 30.1623

Gaus Width: 3.7788



B1L003S, U19-ch26, adc1

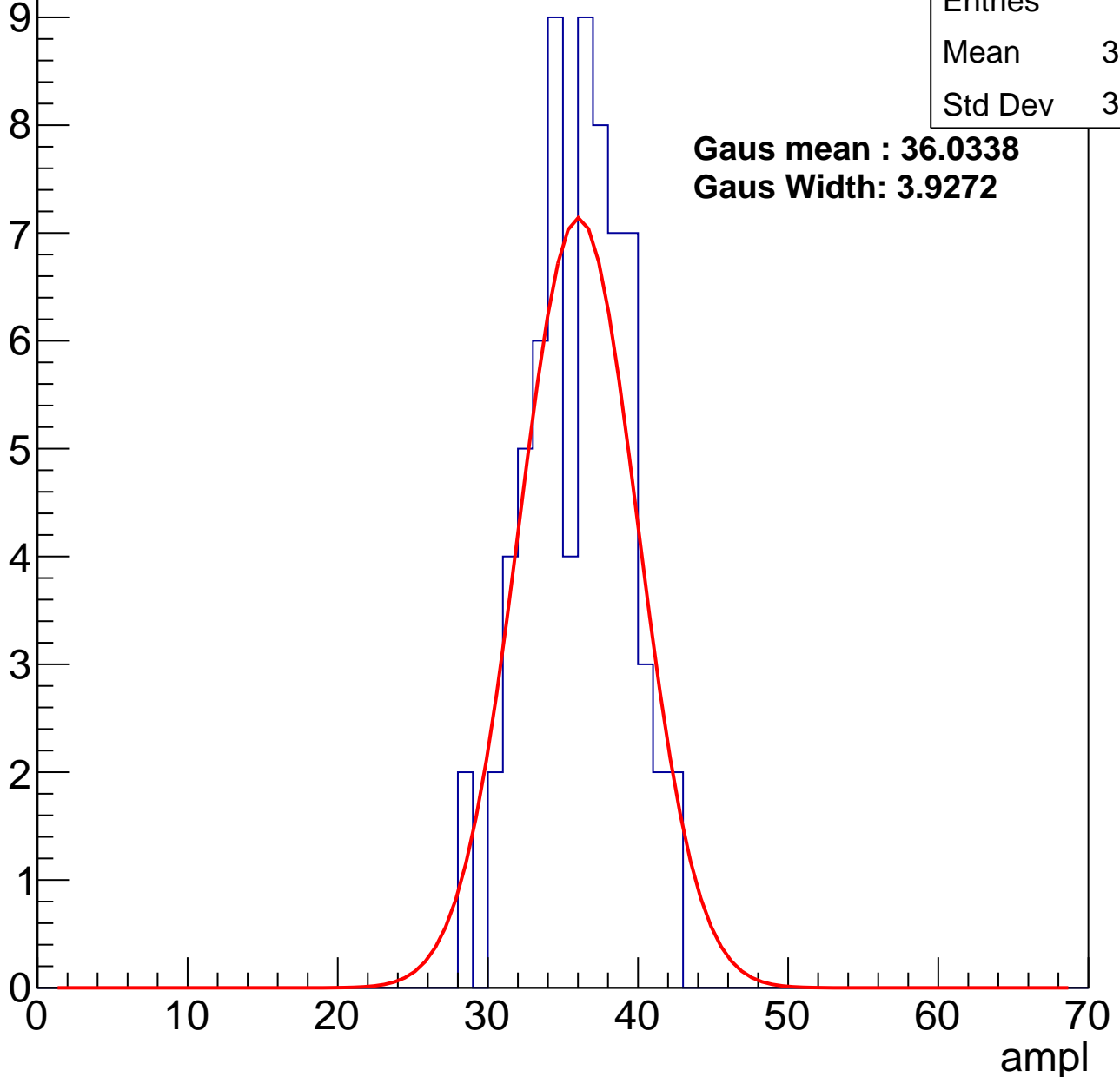
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	35.56
Std Dev	3.228

Gaus mean : 36.0338

Gaus Width: 3.9272



B1L003S, U19-ch26, adc2

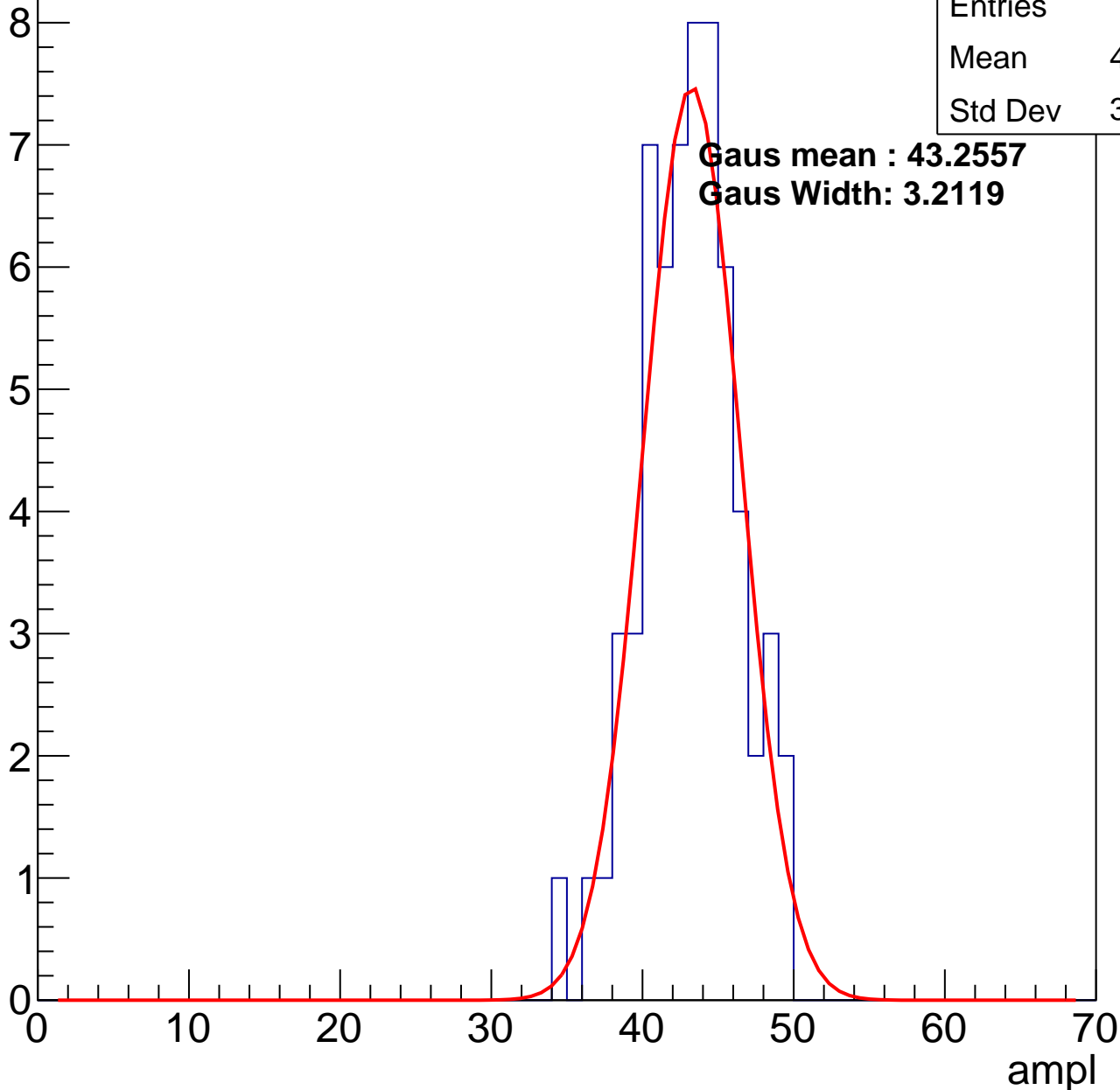
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.65
Std Dev	3.168

Gaus mean : 43.2557

Gaus Width: 3.2119

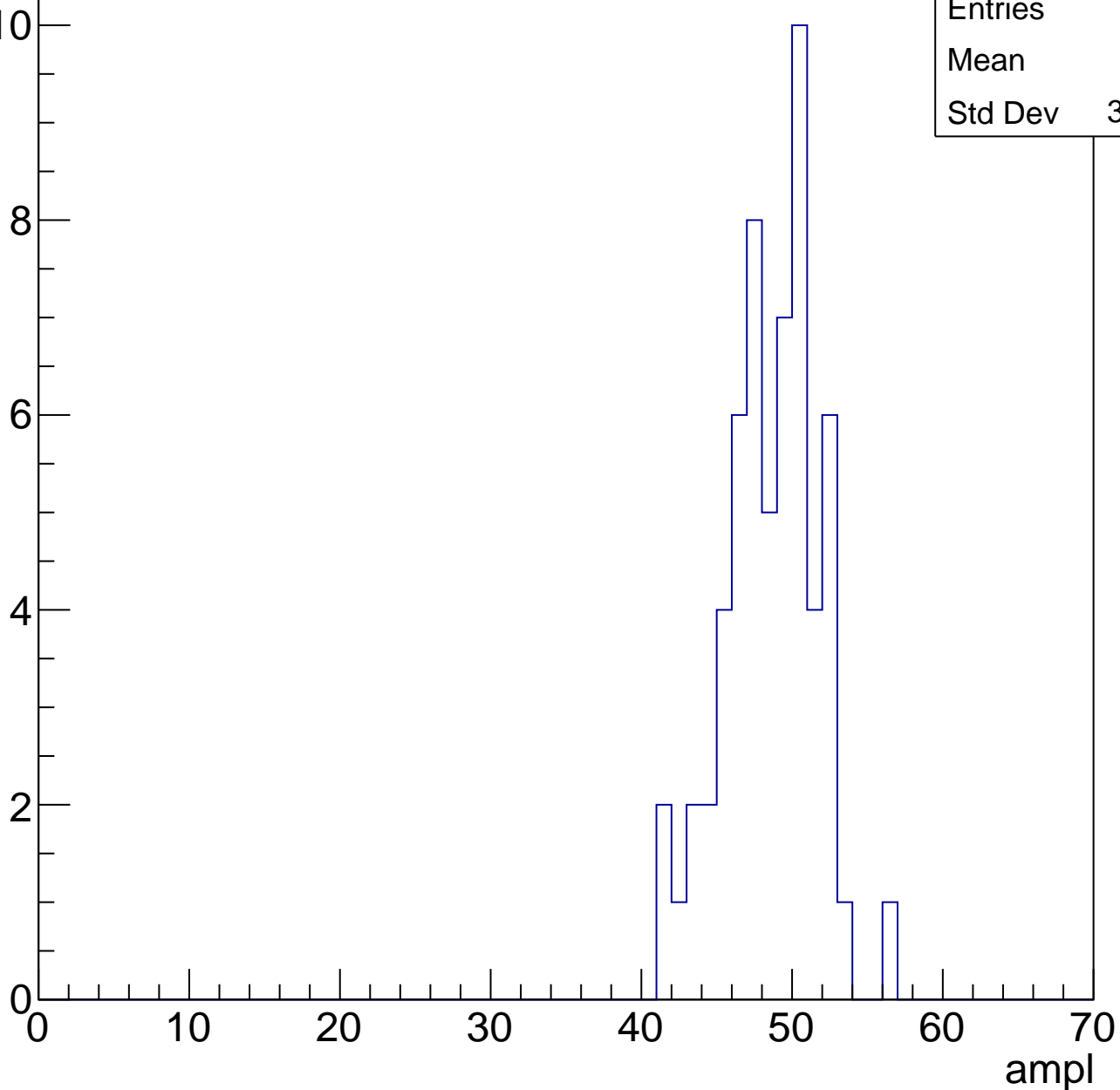


B1L003S, U19-ch26, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	48.1
Std Dev	3.057

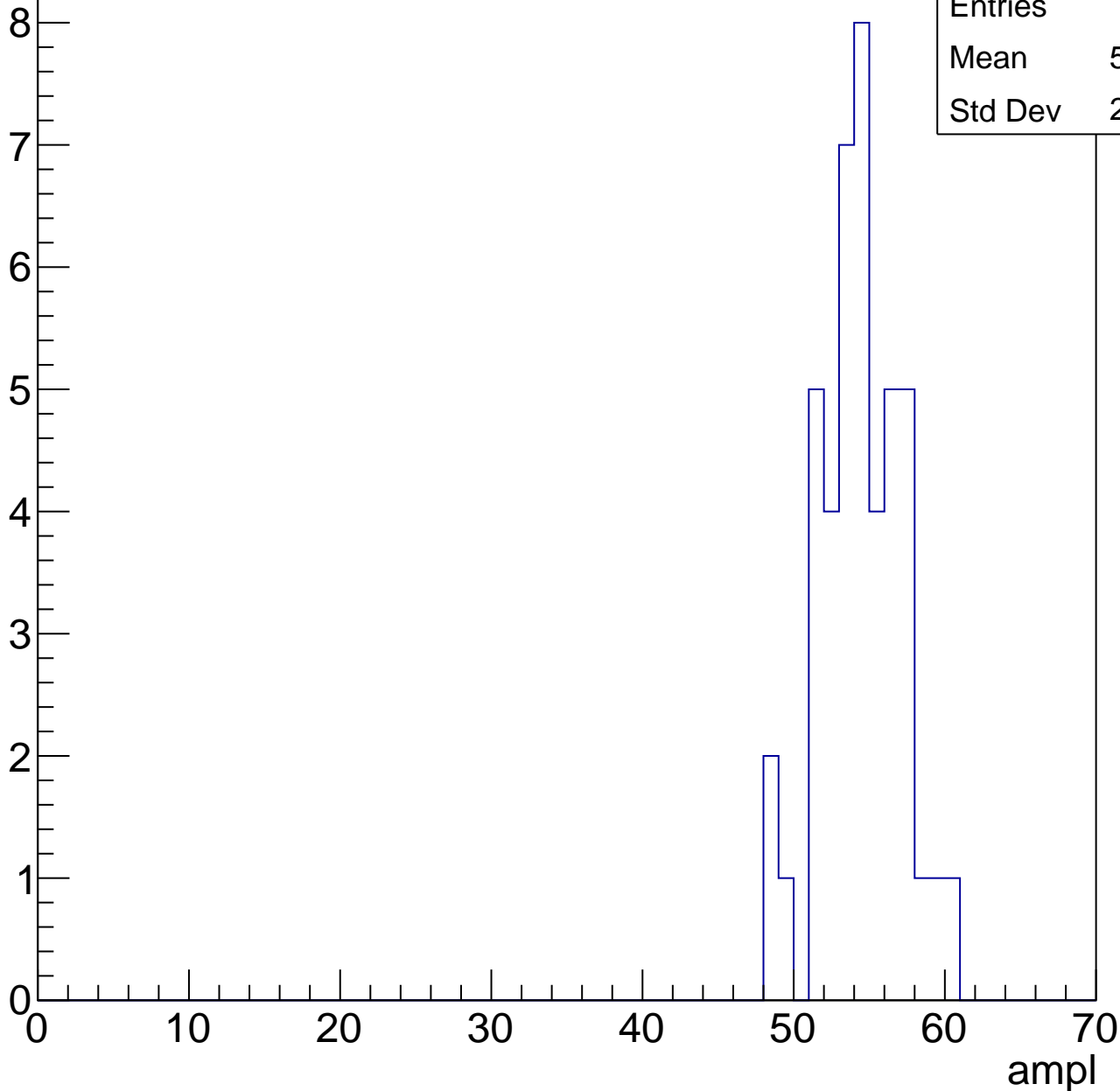


B1L003S, U19-ch26, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

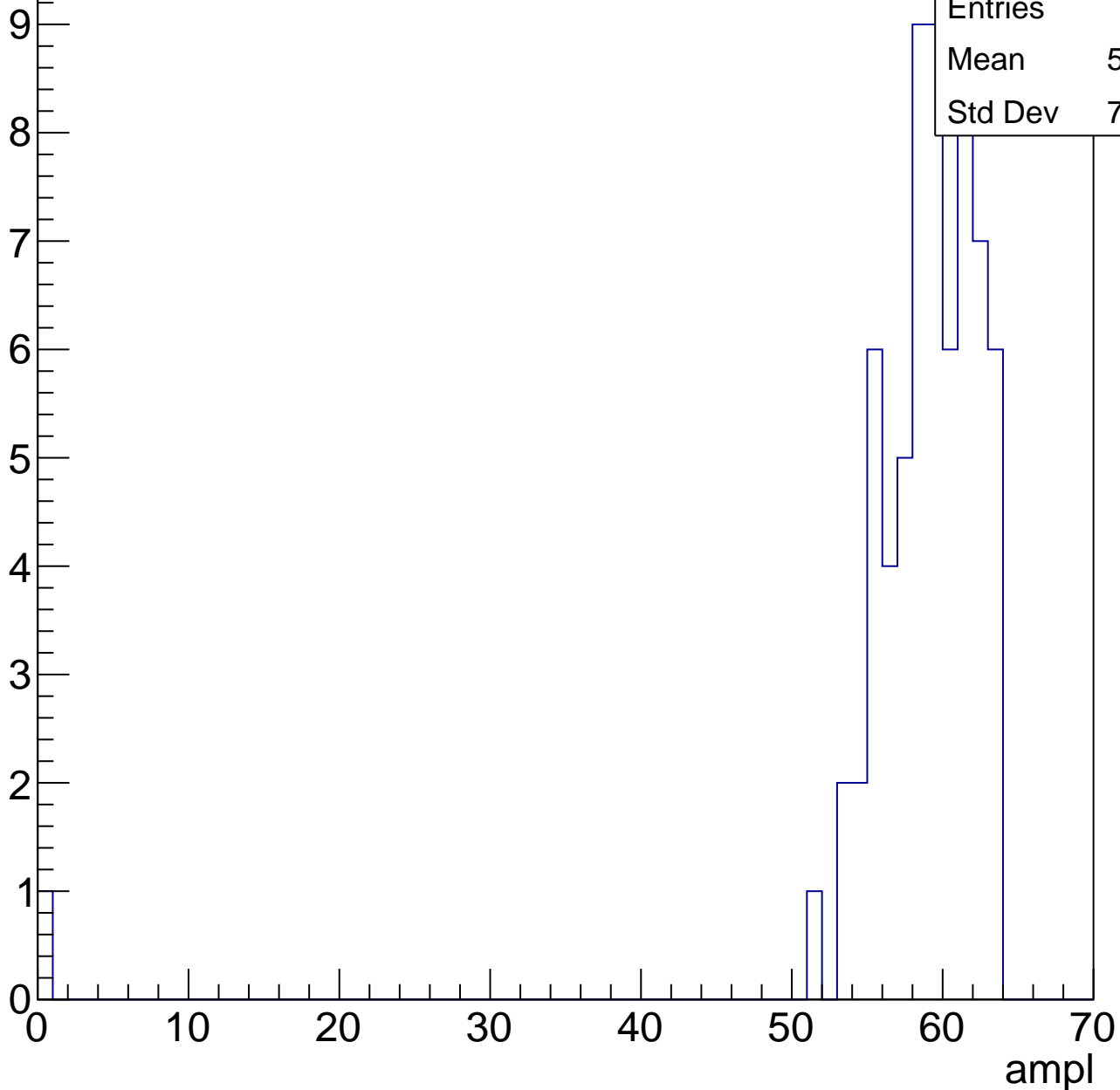
Entries	44
Mean	53.93
Std Dev	2.658



B1L003S, U19-ch26, adc5

calib_packv5_042523_0143.root, FC#13, port D2

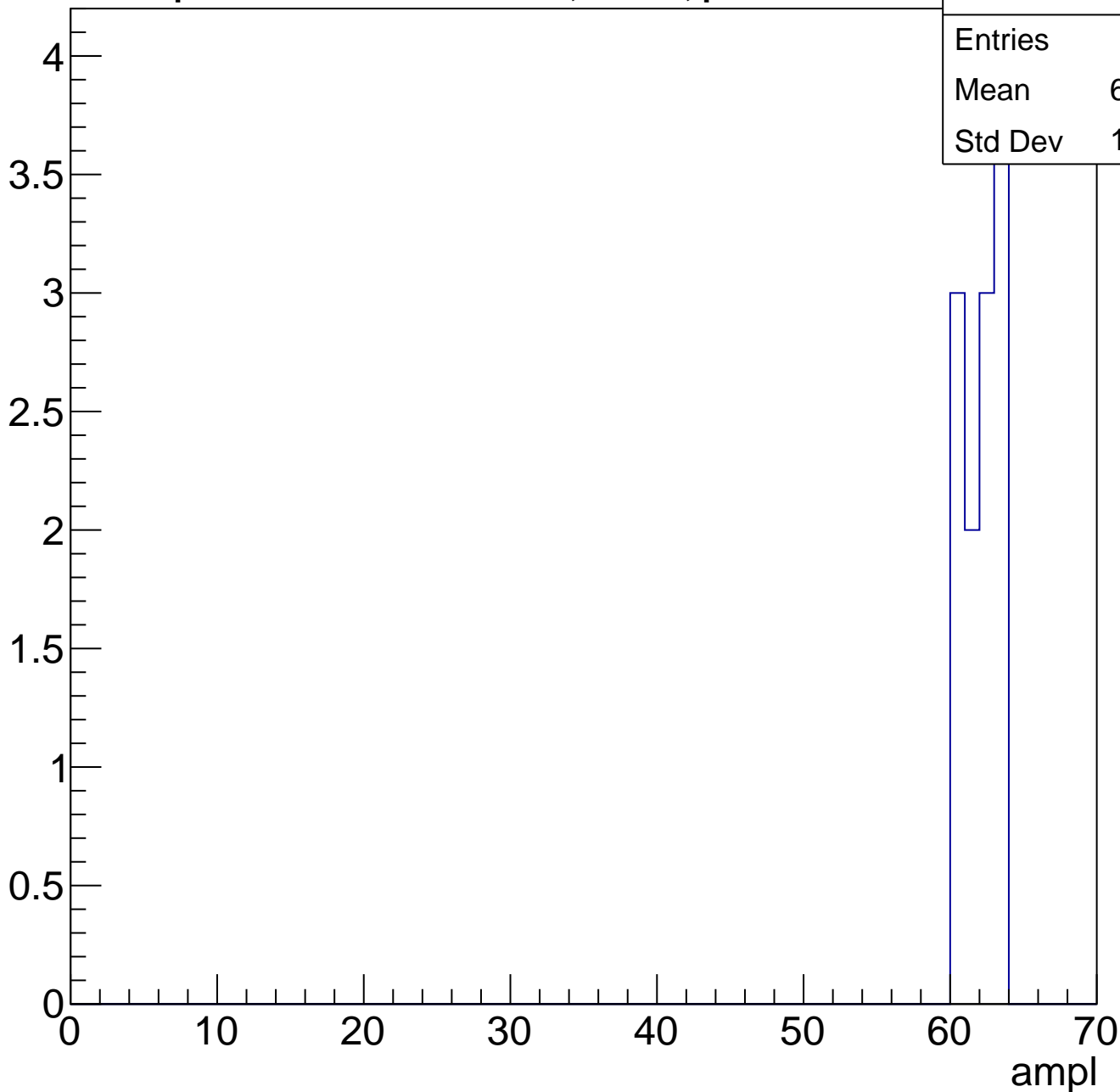
Entry



B1L003S, U19-ch26, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch26, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch27, adc0

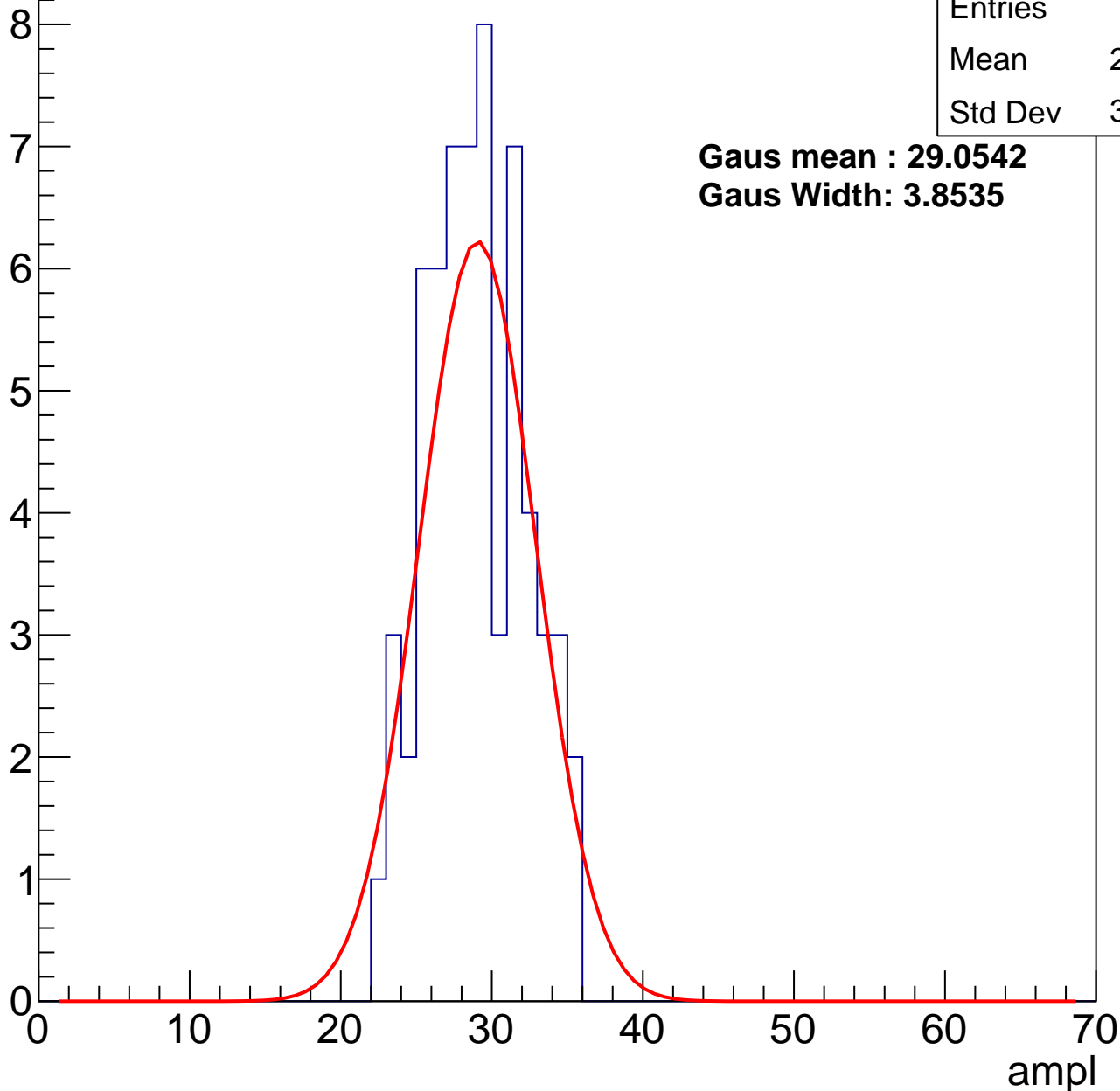
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	28.52
Std Dev	3.212

Gaus mean : 29.0542

Gaus Width: 3.8535

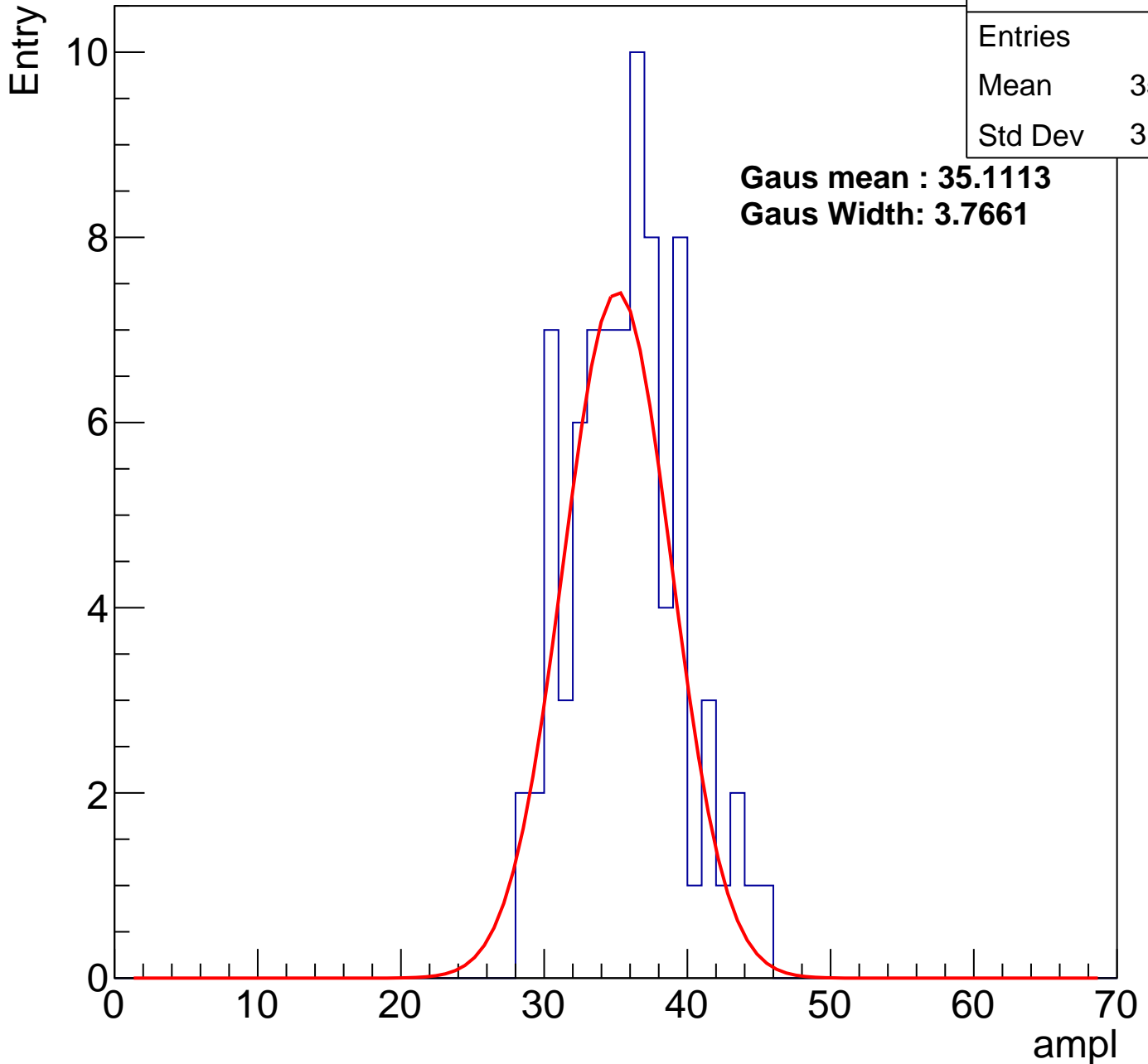


B1L003S, U19-ch27, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	35.29
Std Dev	3.828

Gaus mean : 35.1113
Gaus Width: 3.7661



B1L003S, U19-ch27, adc2

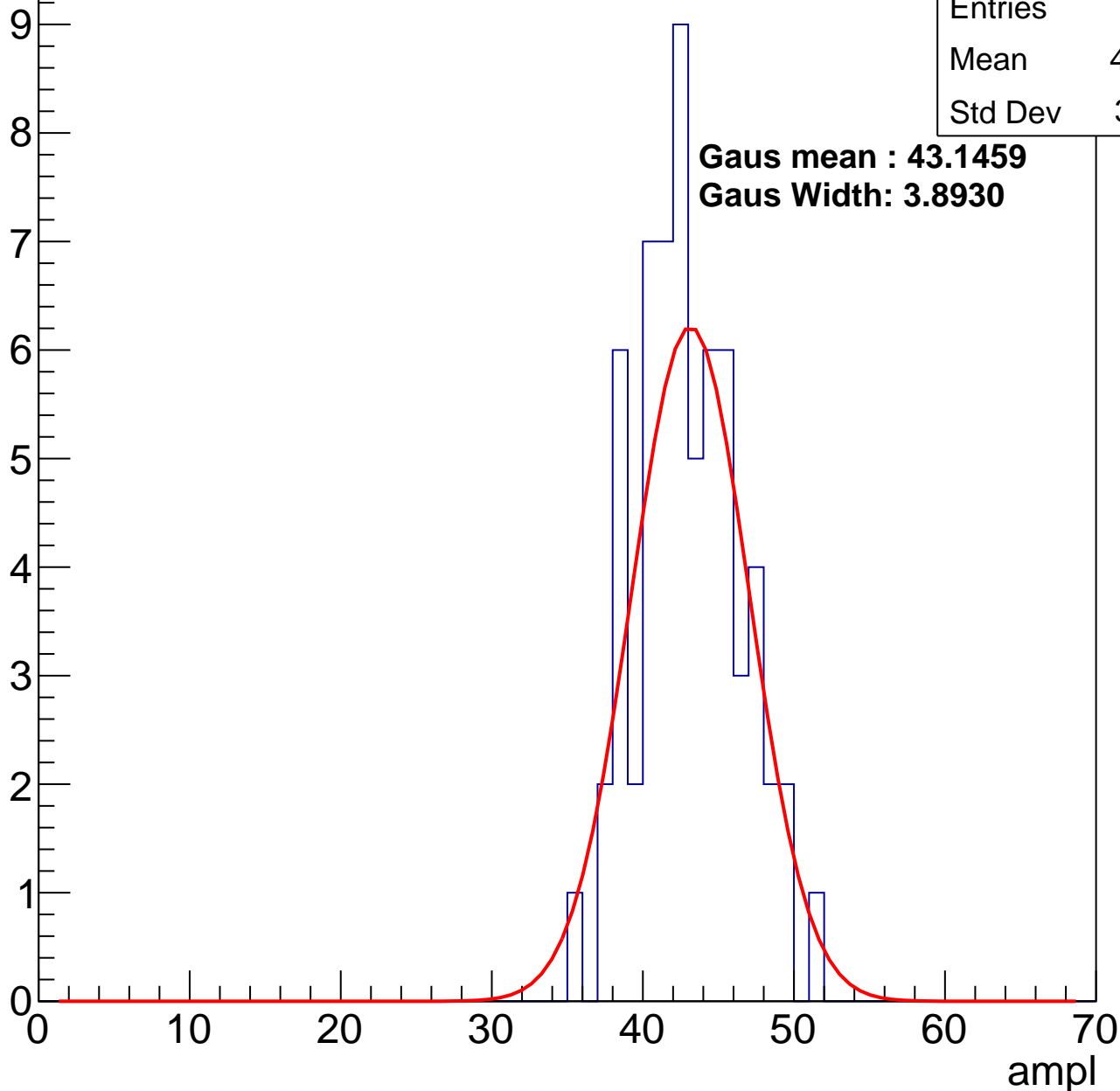
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.54
Std Dev	3.361

Gaus mean : 43.1459

Gaus Width: 3.8930

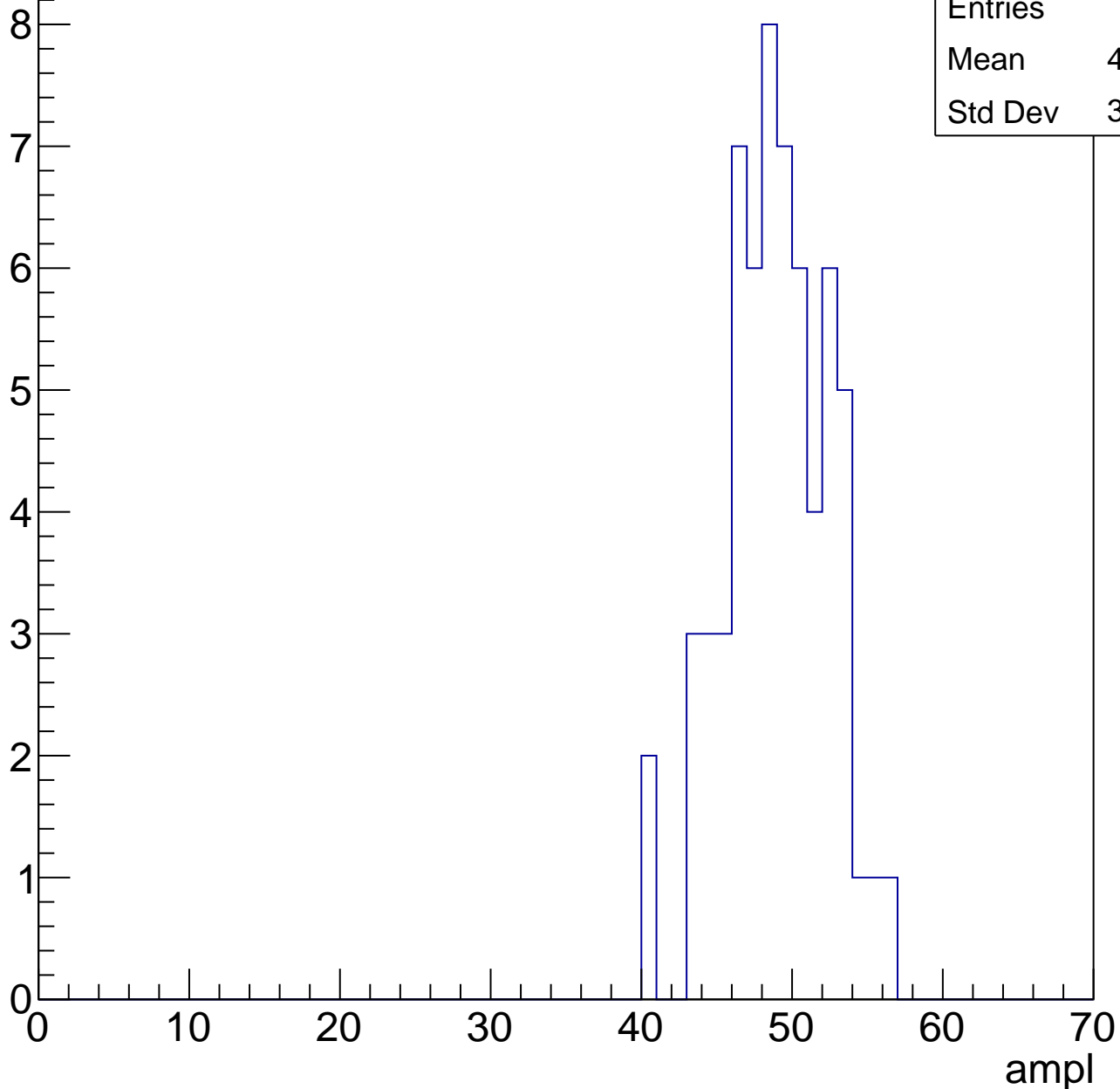


B1L003S, U19-ch27, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

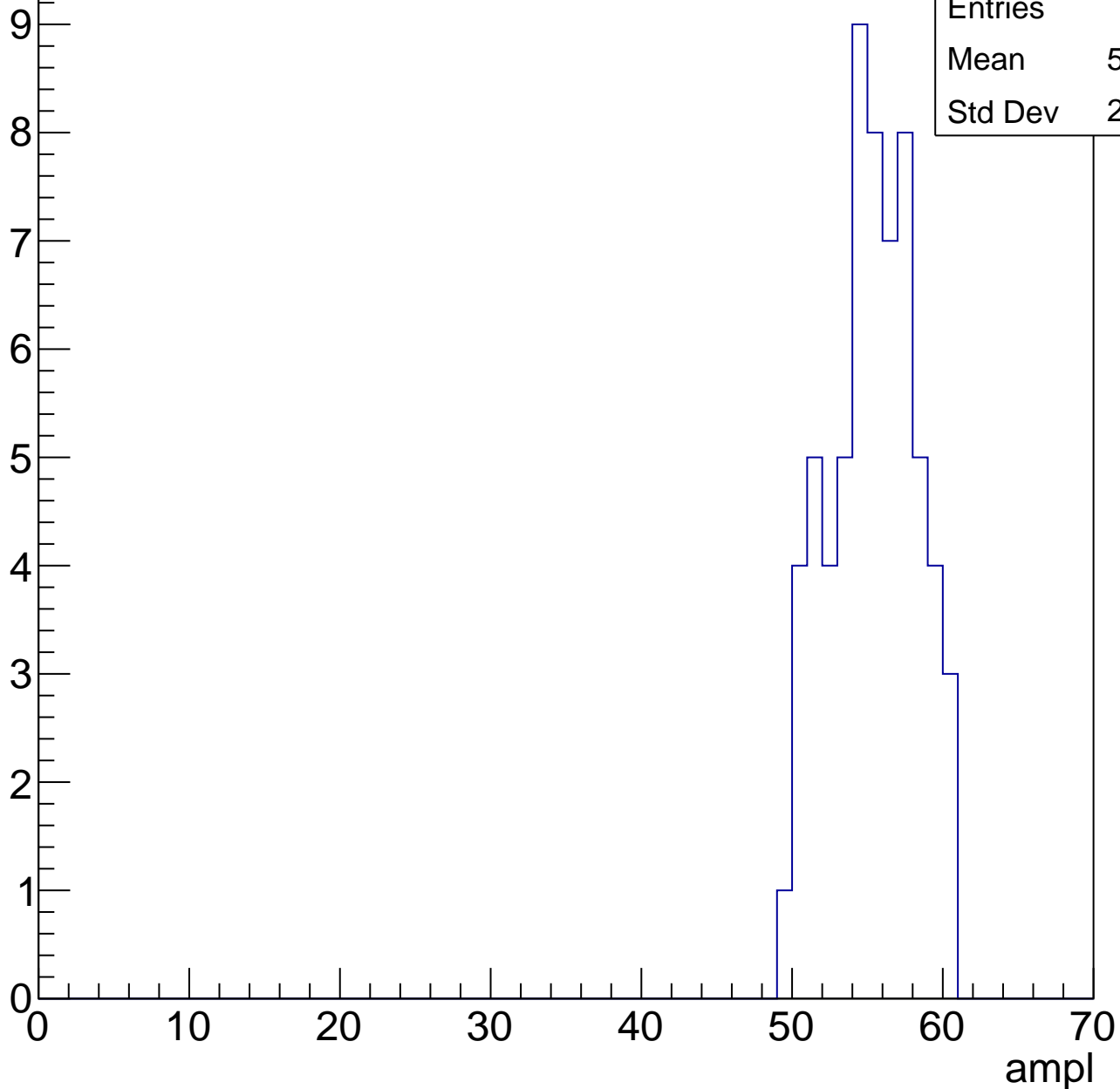
Entries	63
Mean	48.46
Std Dev	3.417



B1L003S, U19-ch27, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



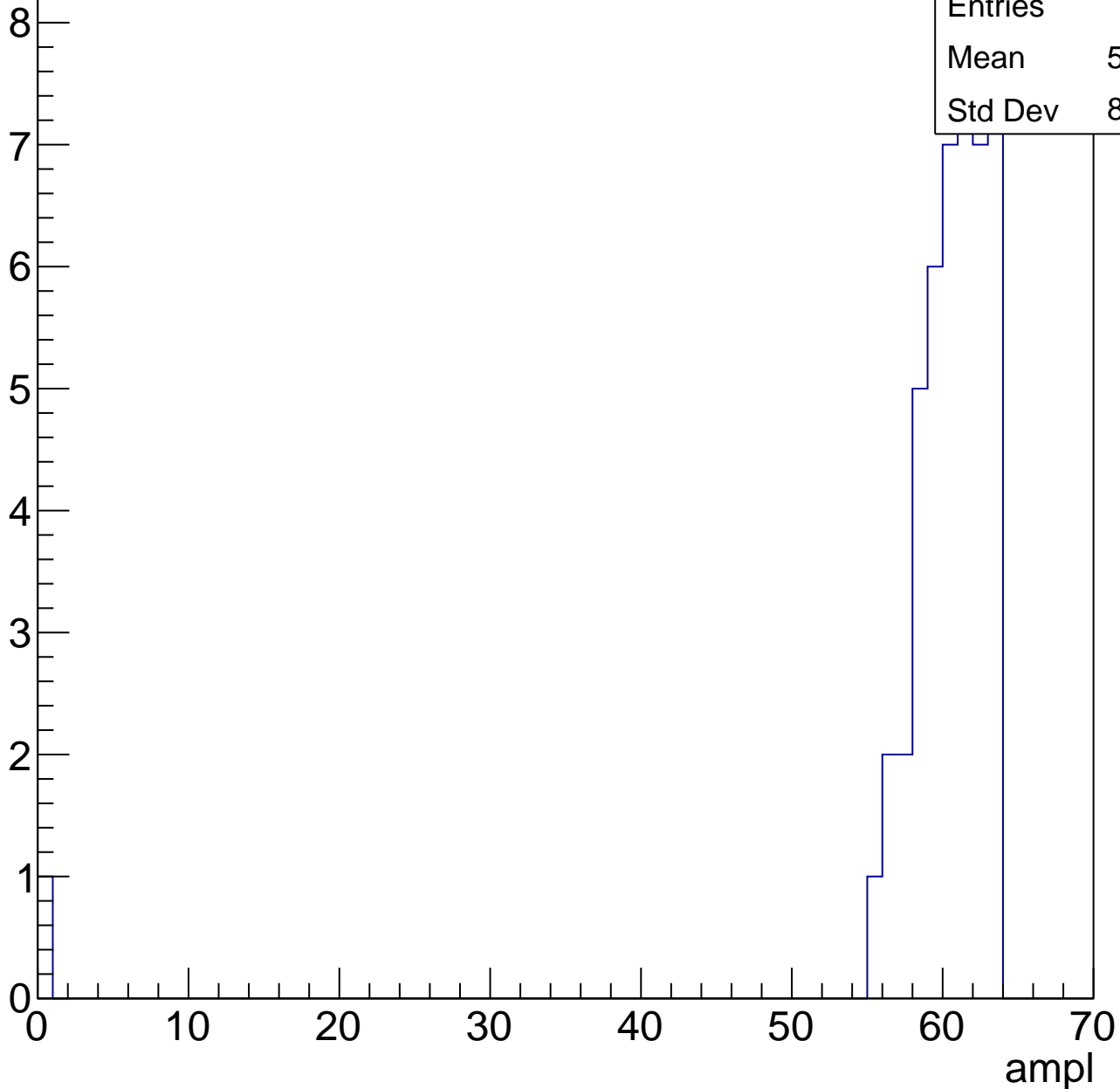
Entries	63
Mean	54.87
Std Dev	2.826

B1L003S, U19-ch27, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

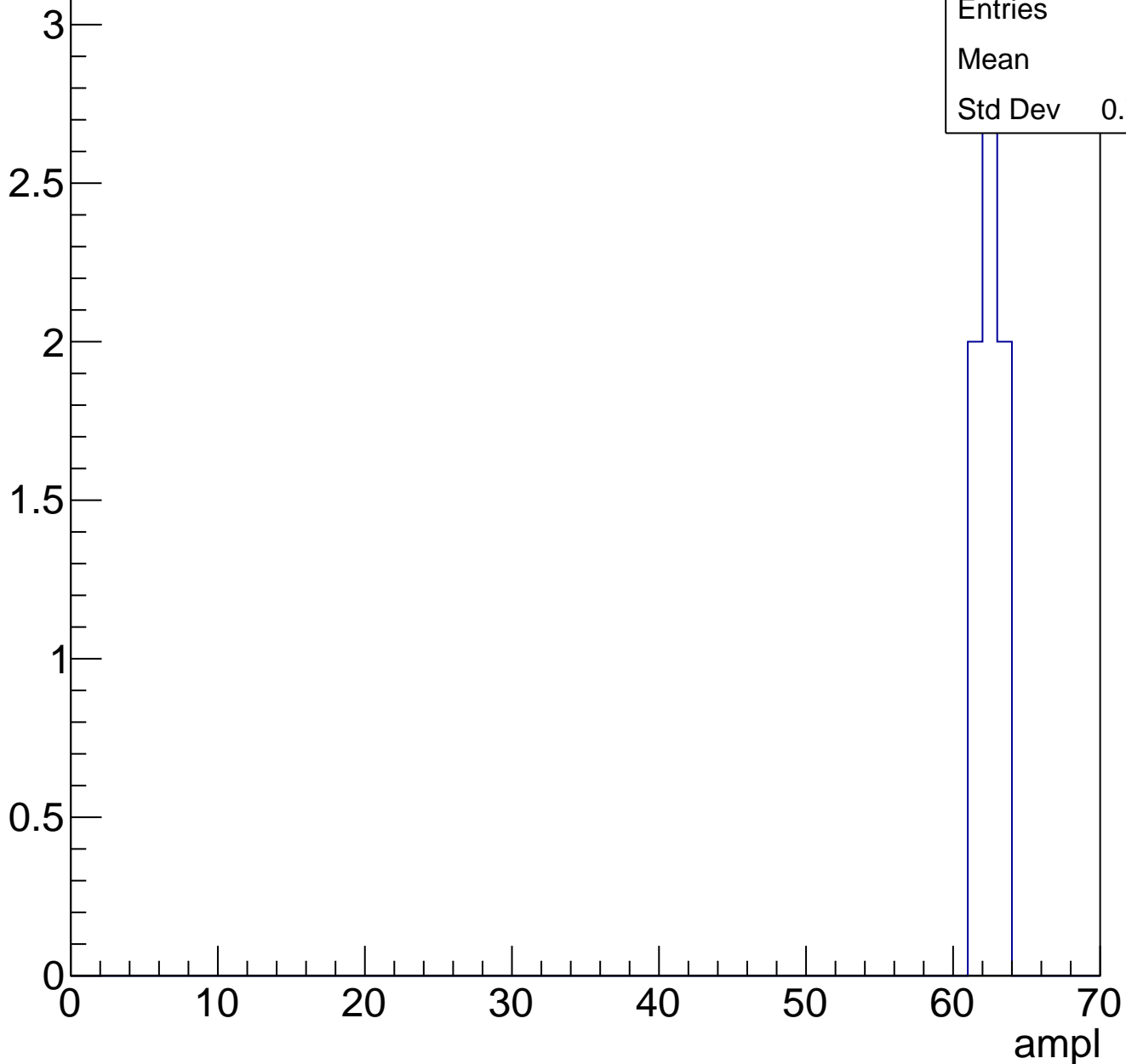
Entries	47
Mean	58.96
Std Dev	8.942



B1L003S, U19-ch27, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch27, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10
Std Dev	10

B1L003S, U19-ch28, adc0

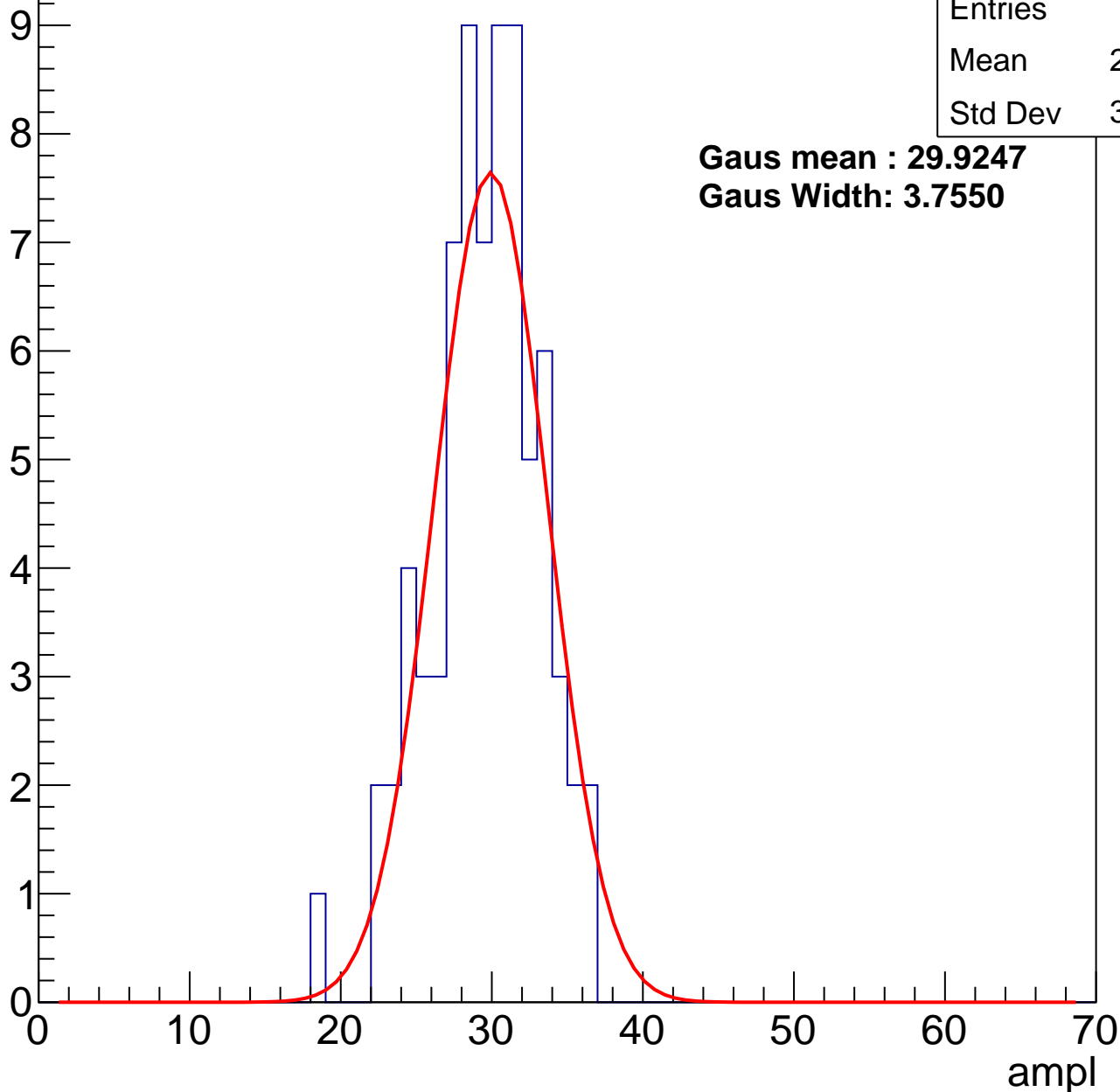
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	29.08
Std Dev	3.552

Gaus mean : 29.9247

Gaus Width: 3.7550



B1L003S, U19-ch28, adc1

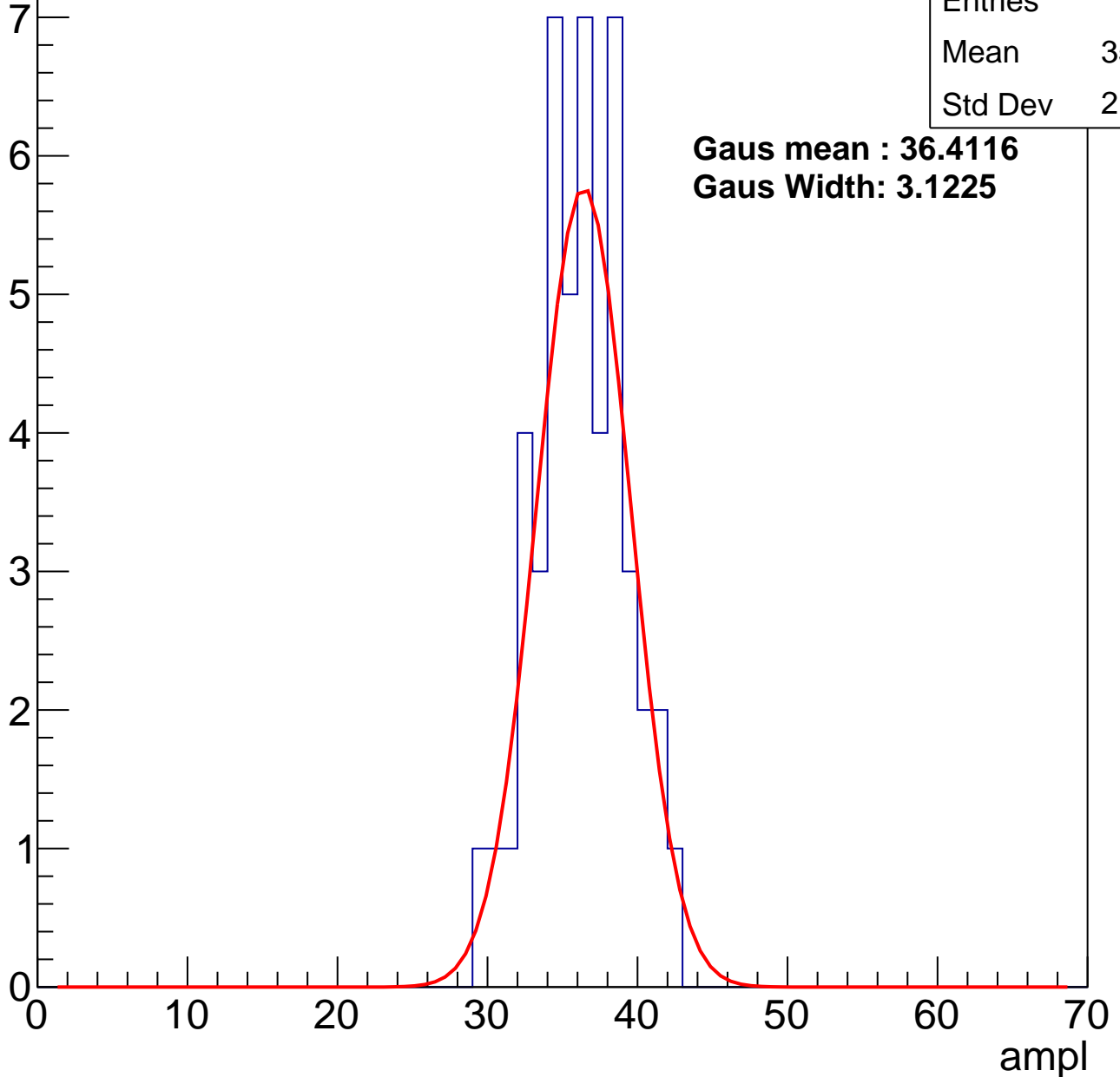
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	35.77
Std Dev	2.917

Gaus mean : 36.4116

Gaus Width: 3.1225



B1L003S, U19-ch28, adc2

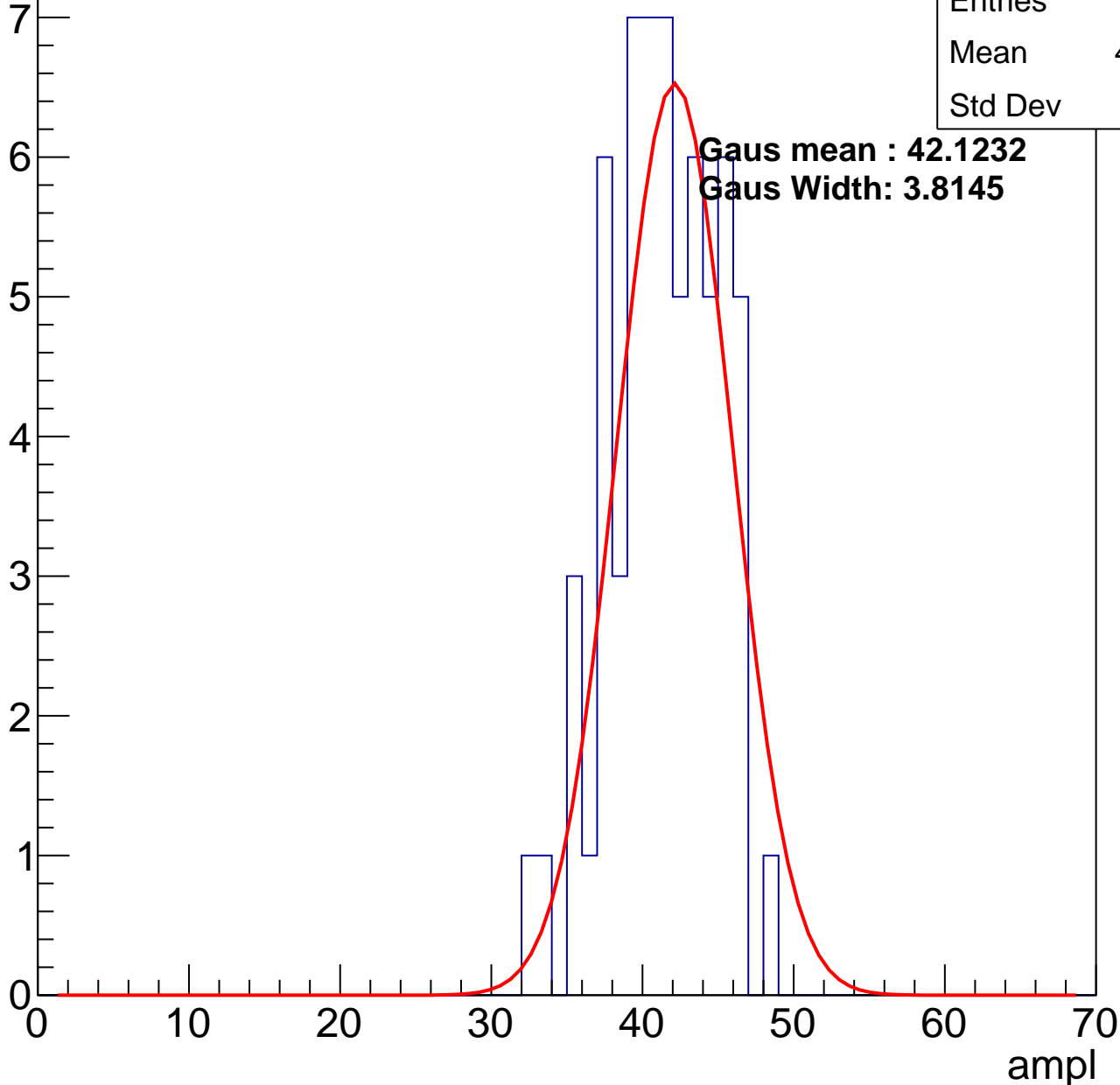
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	40.91
Std Dev	3.49

Gaus mean : 42.1232

Gaus Width: 3.8145

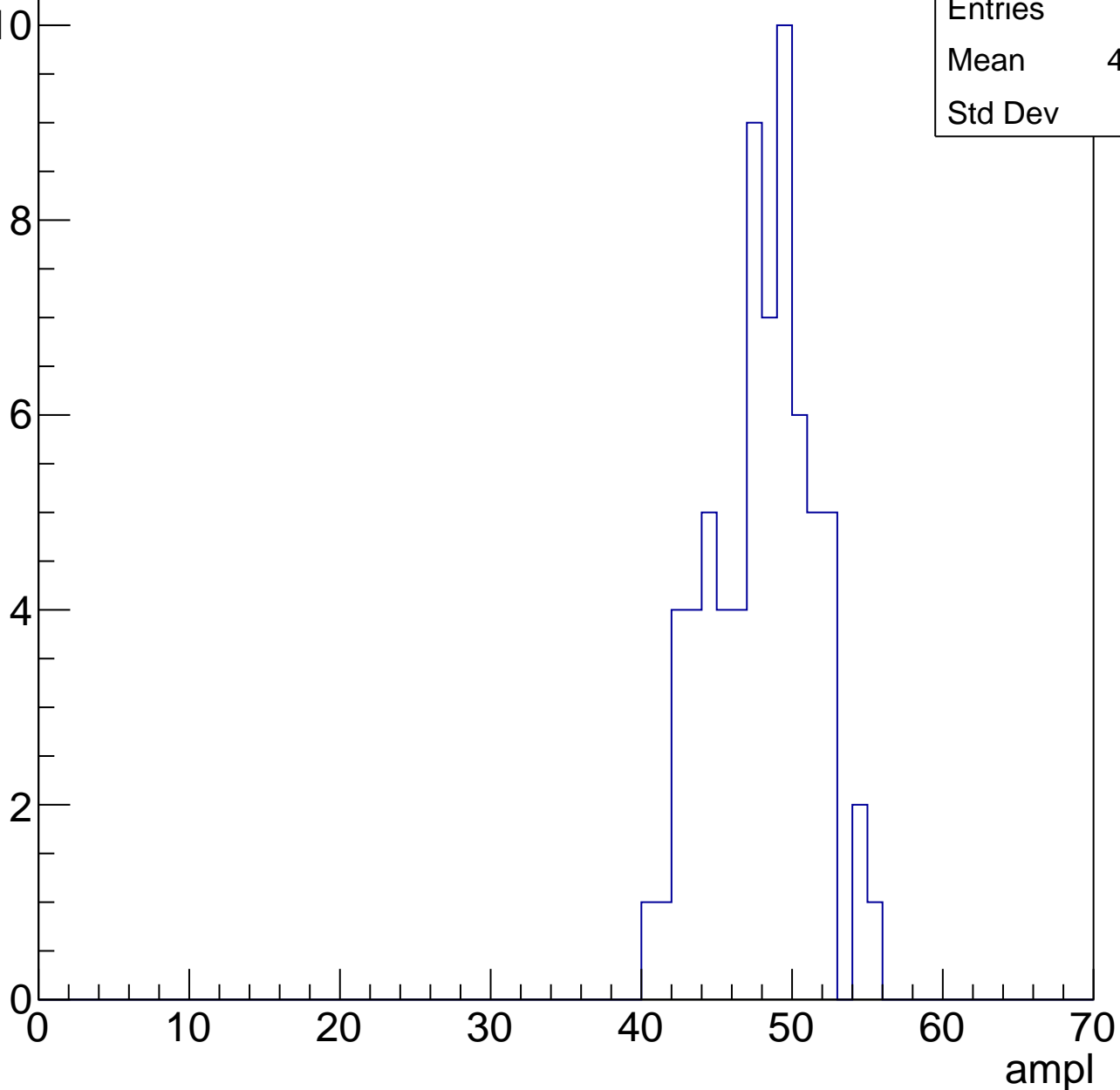


B1L003S, U19-ch28, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	47.53
Std Dev	3.35



B1L003S, U19-ch28, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	59
Mean	53.85
Std Dev	3.085

Entry

10

8

6

4

2

0

0

10

20

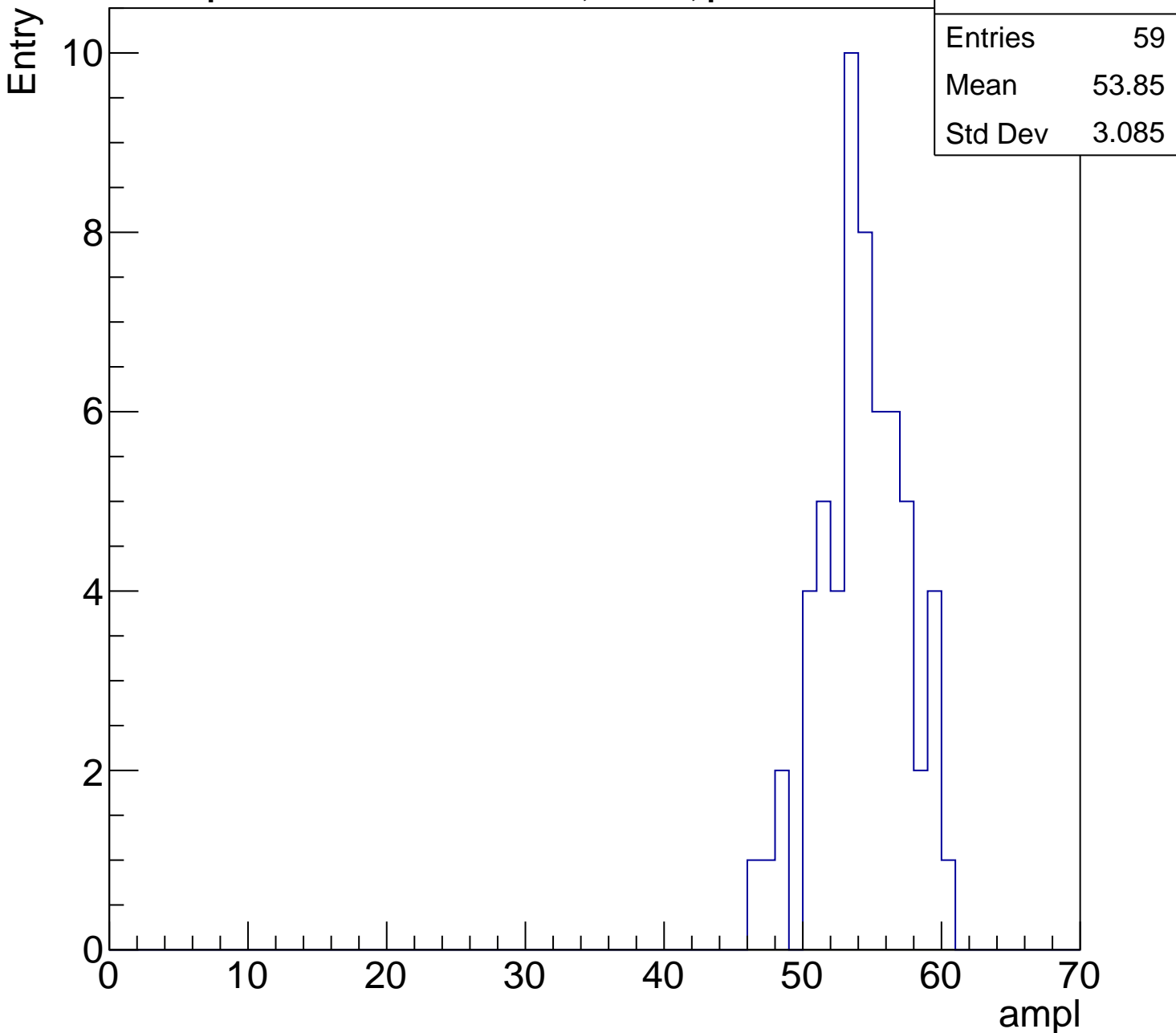
30

40

50

60

ampl

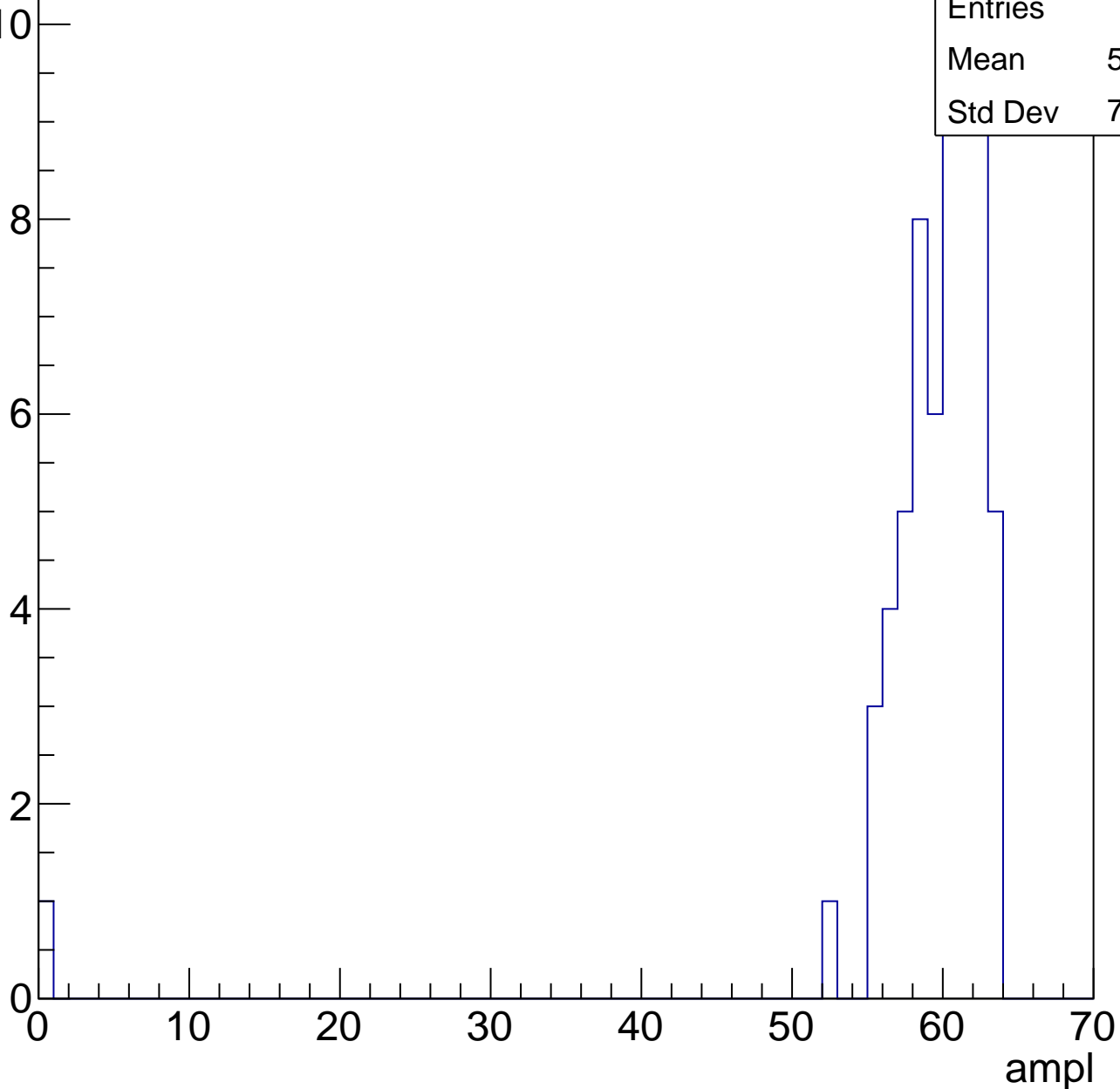


B1L003S, U19-ch28, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	58.48
Std Dev	7.929

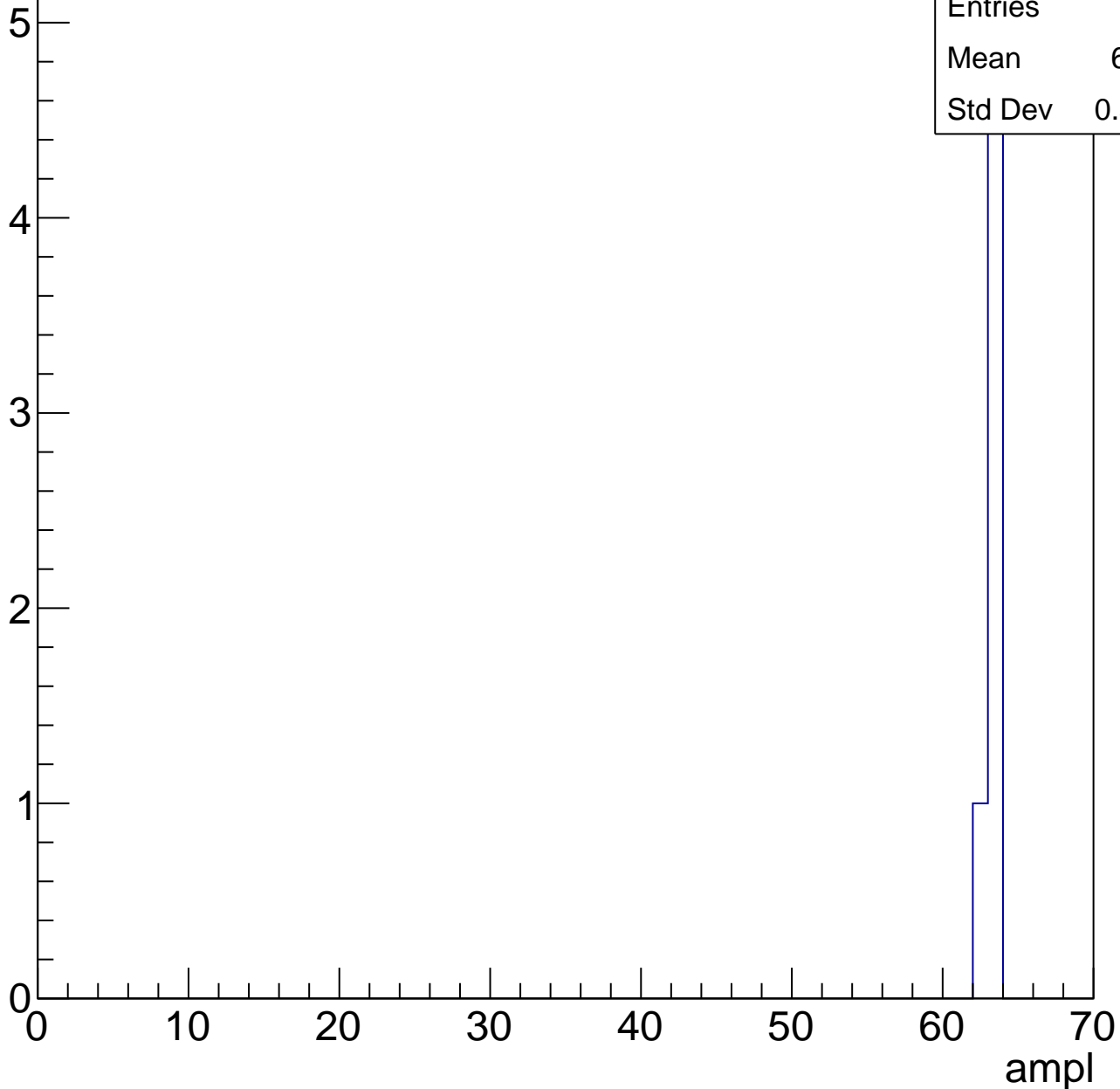


B1L003S, U19-ch28, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B1L003S, U19-ch28, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch29, adc0

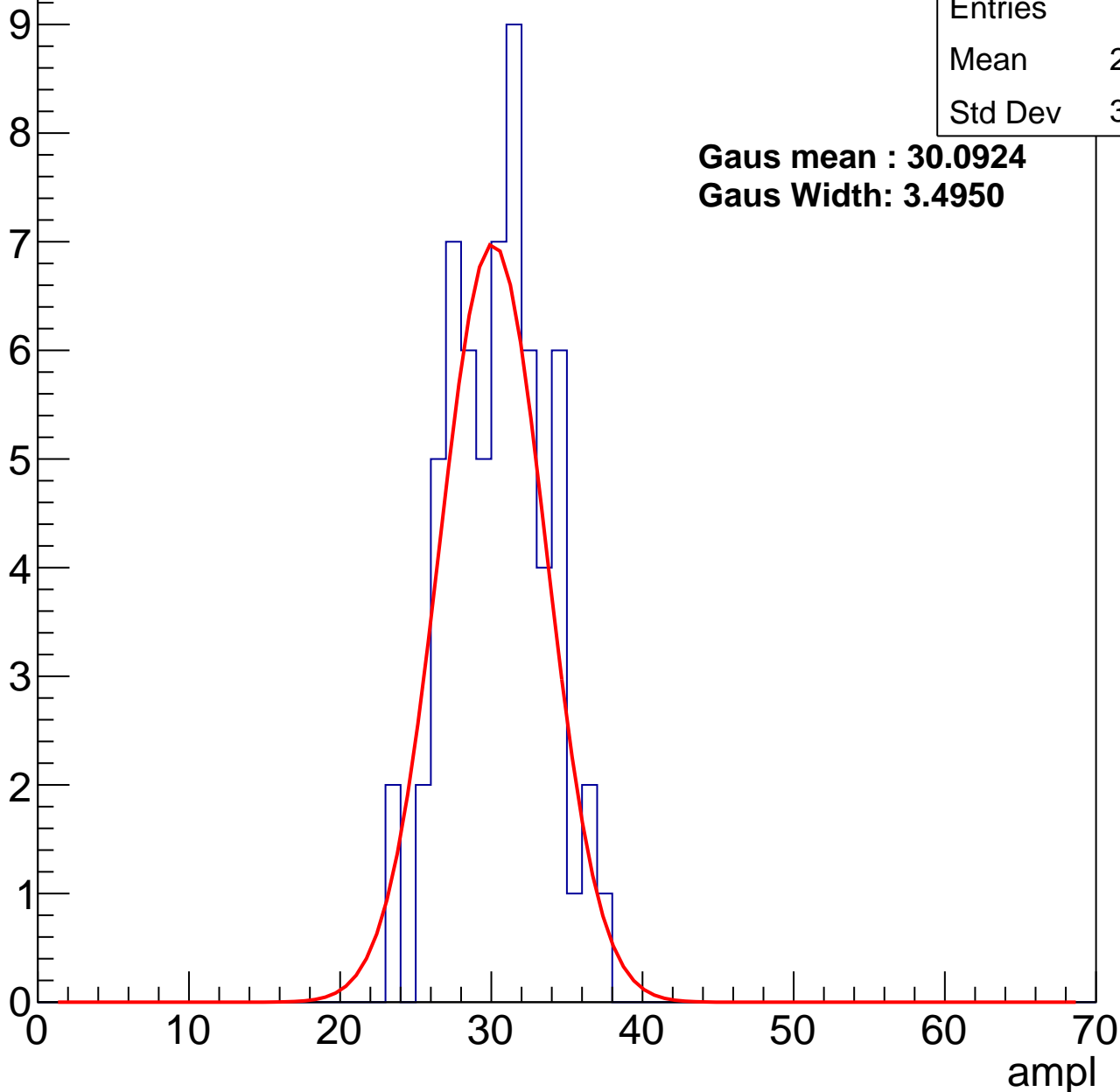
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.98
Std Dev	3.165

Gaus mean : 30.0924

Gaus Width: 3.4950



B1L003S, U19-ch29, adc1

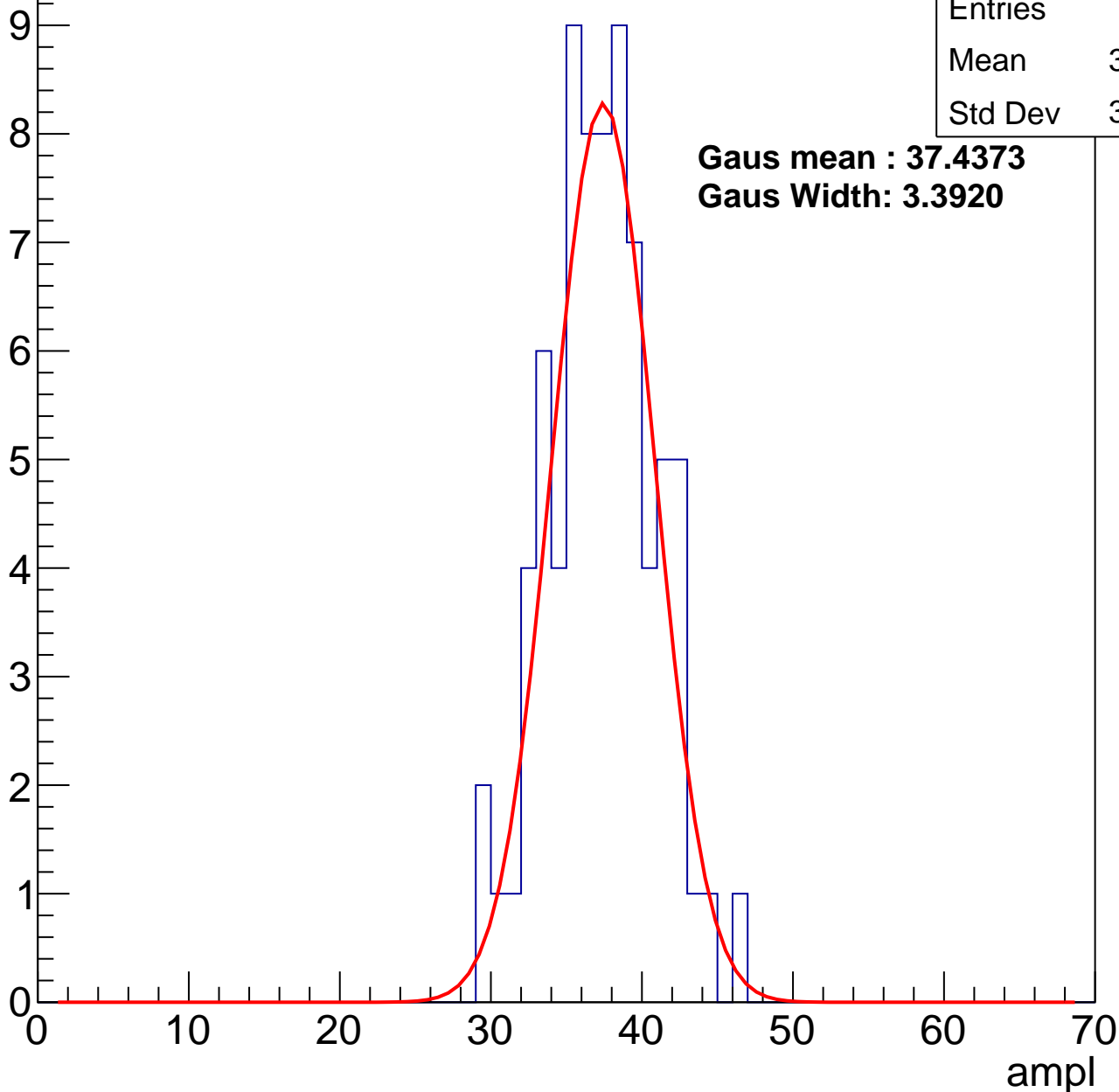
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	36.88
Std Dev	3.502

Gaus mean : 37.4373

Gaus Width: 3.3920



B1L003S, U19-ch29, adc2

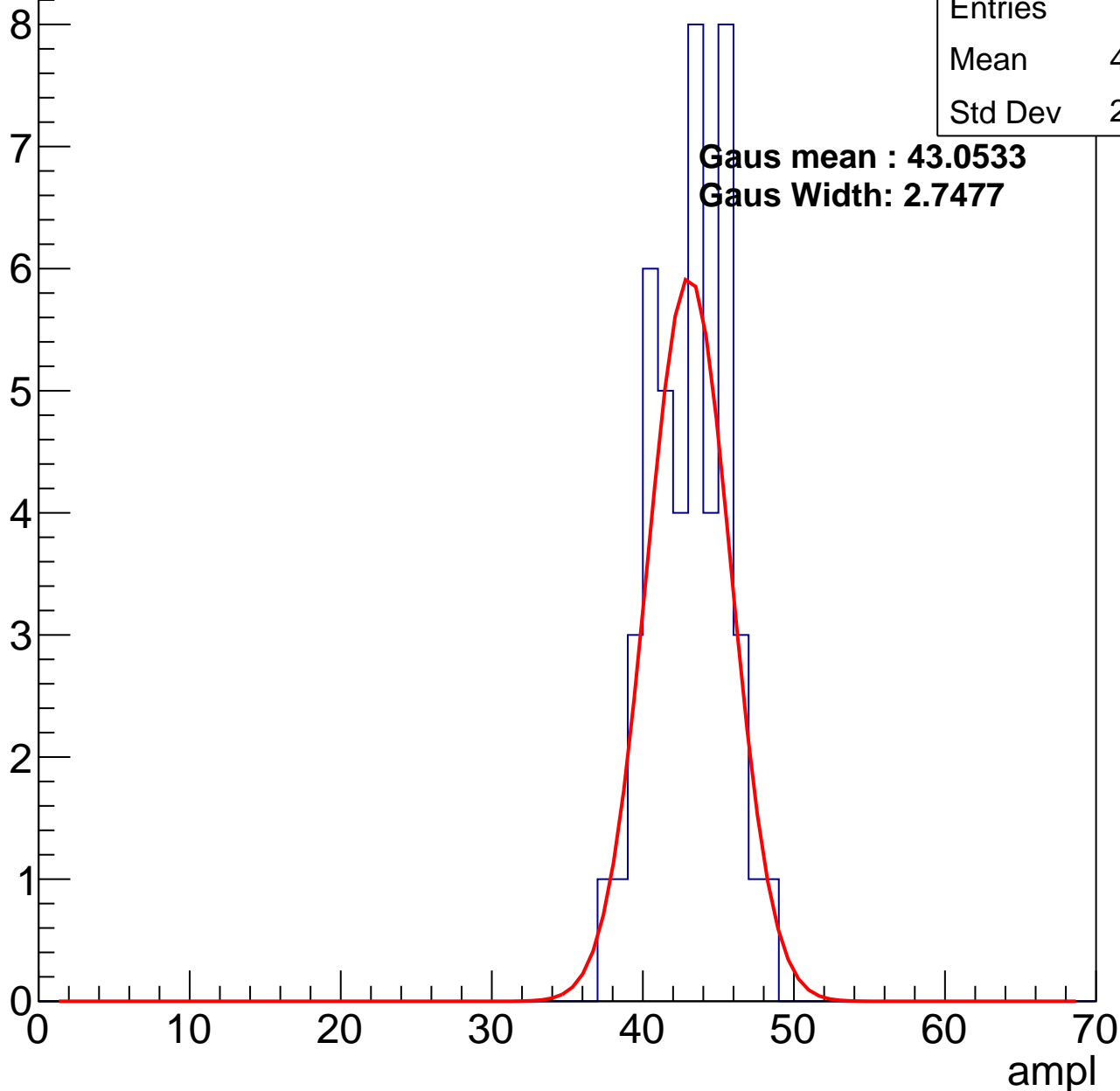
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	42.62
Std Dev	2.515

Gaus mean : 43.0533

Gaus Width: 2.7477

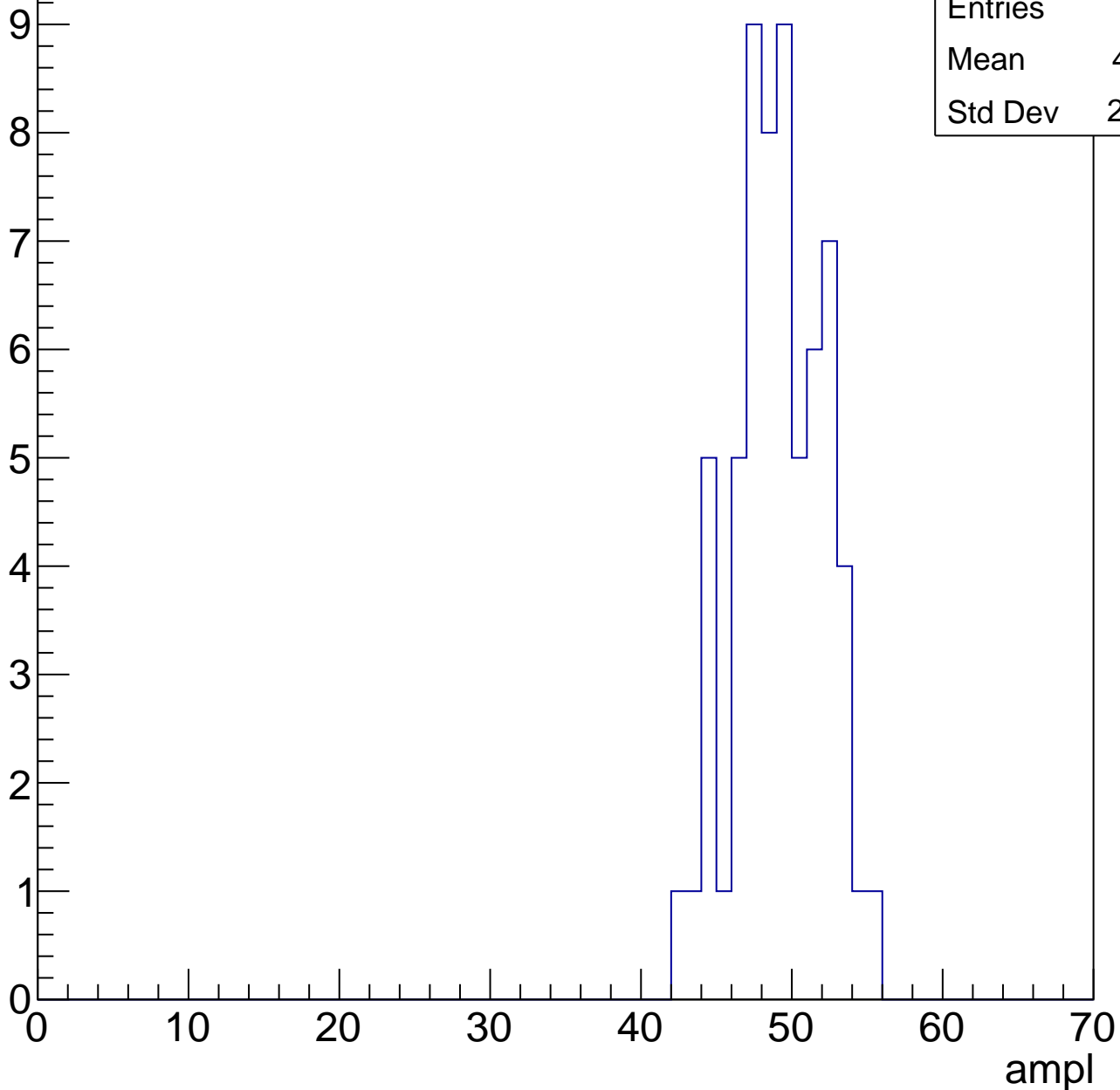


B1L003S, U19-ch29, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	48.71
Std Dev	2.892

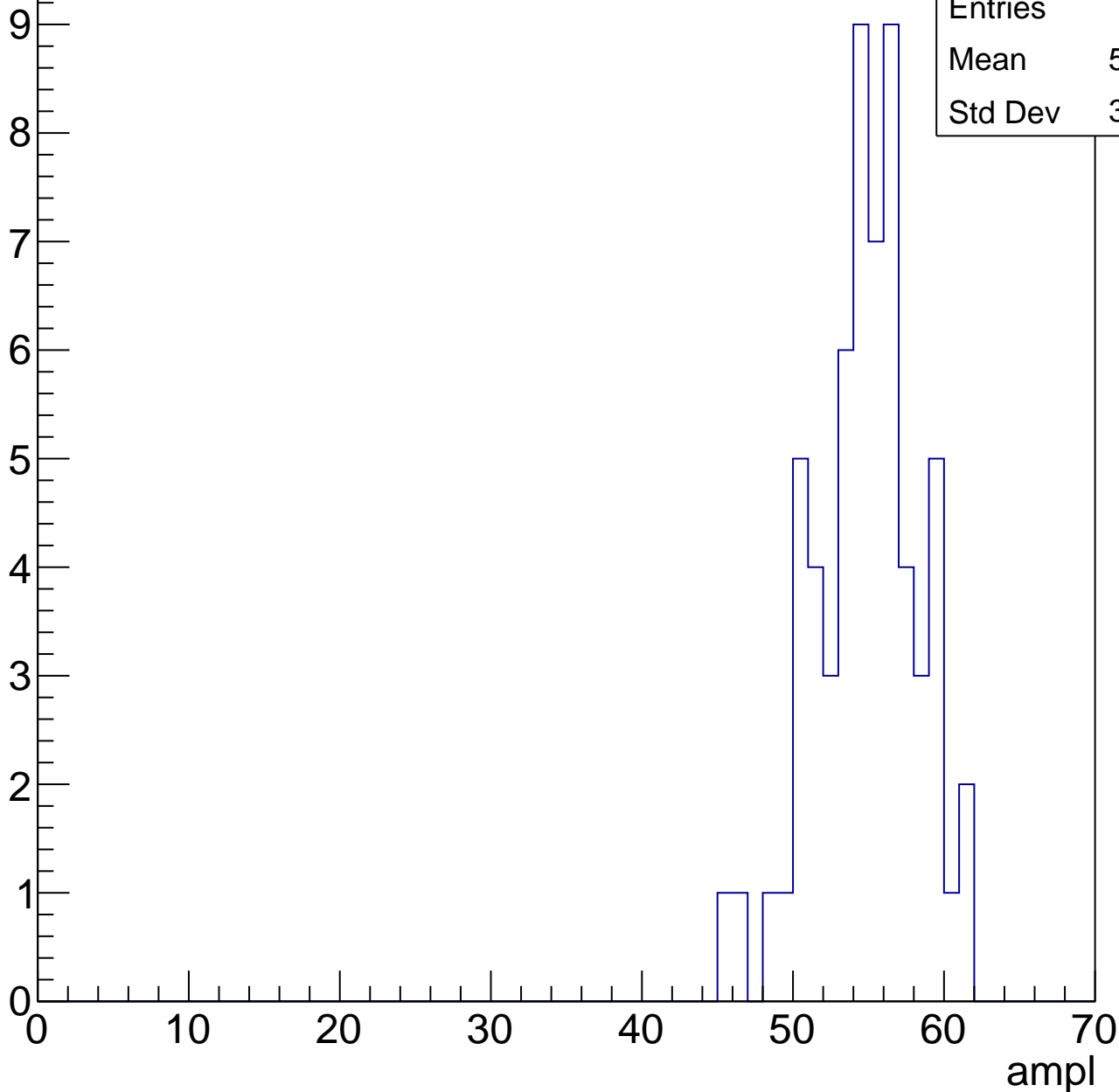


B1L003S, U19-ch29, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	54.35
Std Dev	3.403

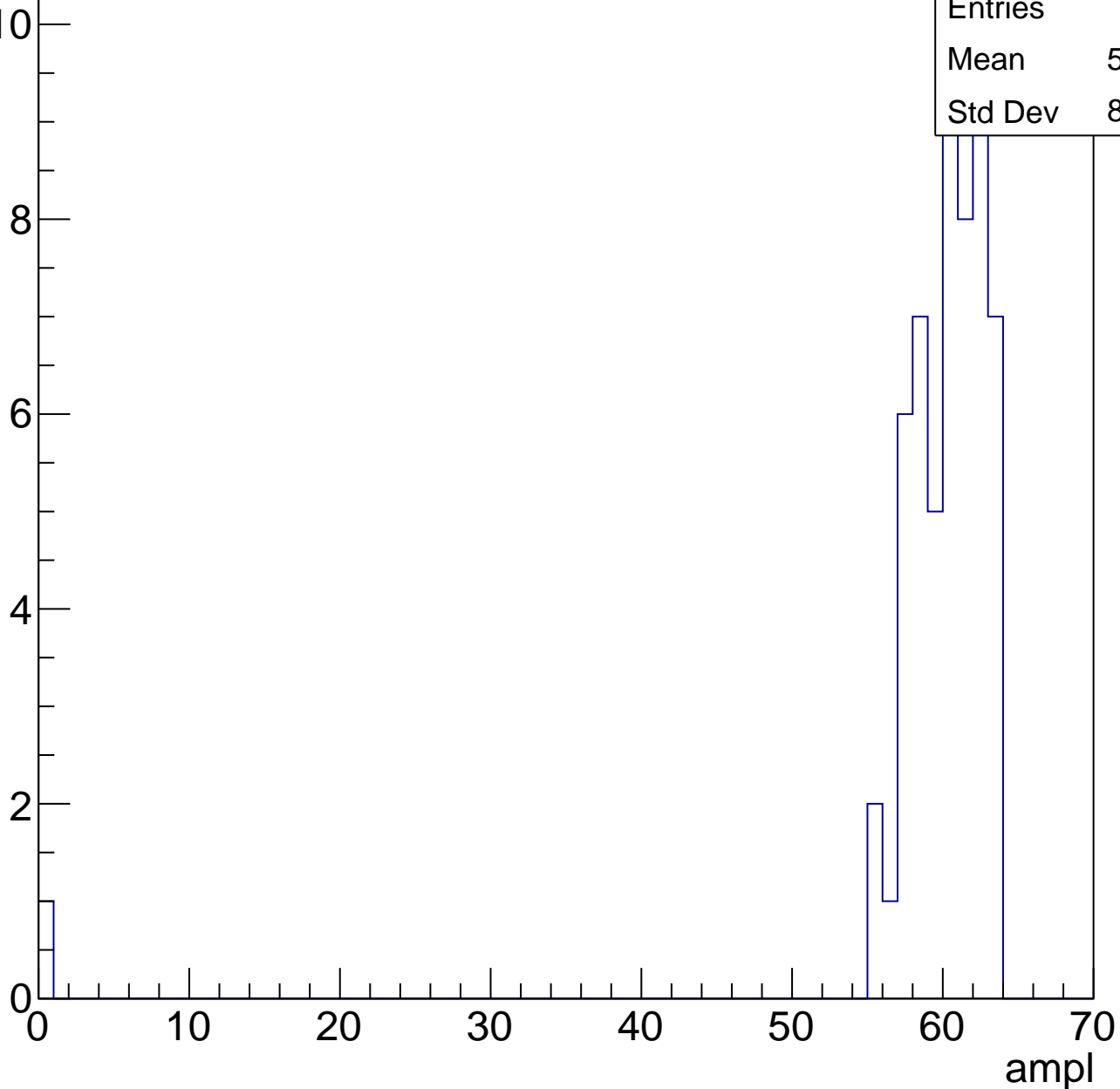


B1L003S, U19-ch29, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.89
Std Dev	8.233



B1L003S, U19-ch29, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch29, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L003S, U19-ch30, adc0

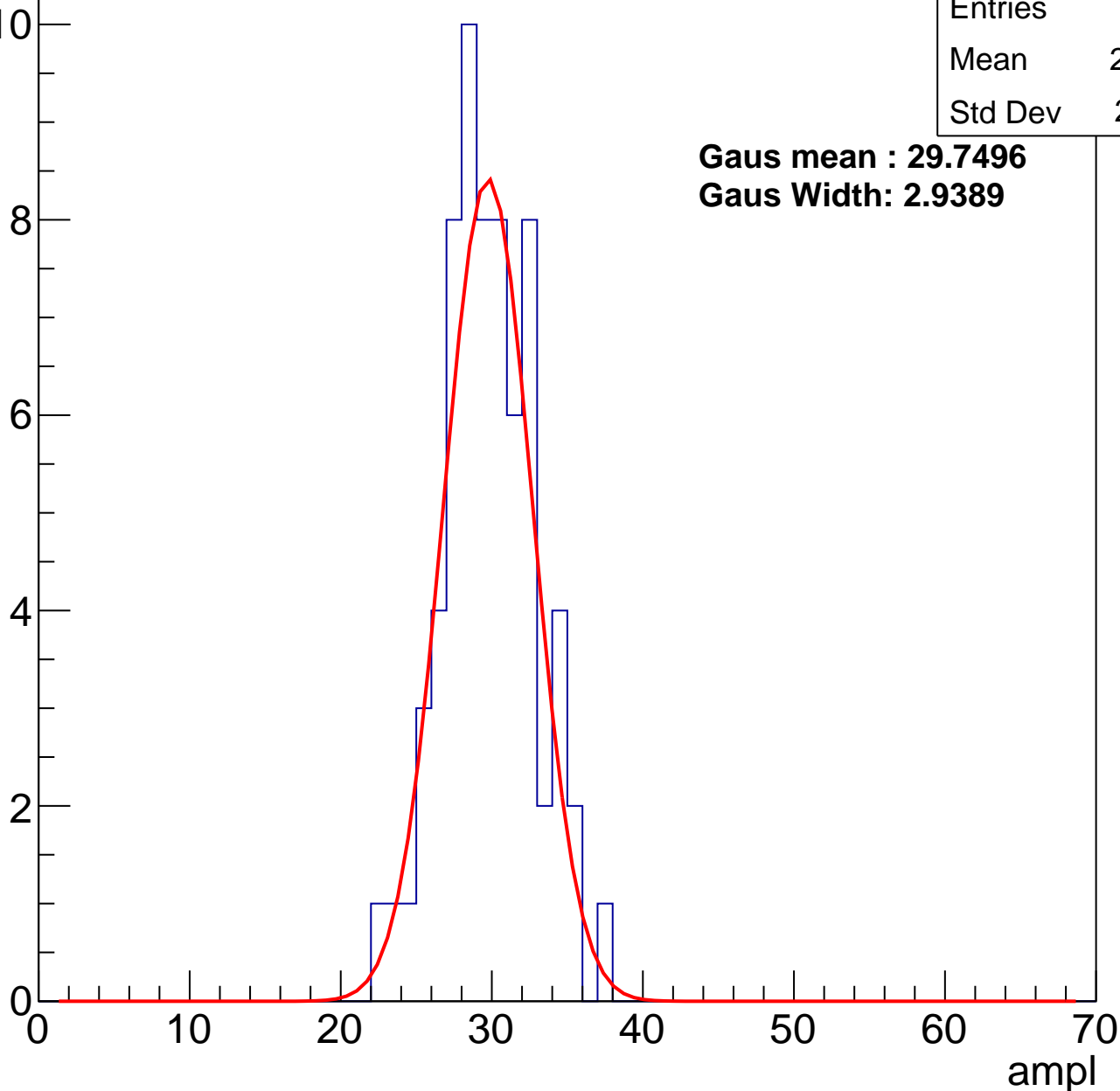
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	29.36
Std Dev	2.991

Gaus mean : 29.7496

Gaus Width: 2.9389



B1L003S, U19-ch30, adc1

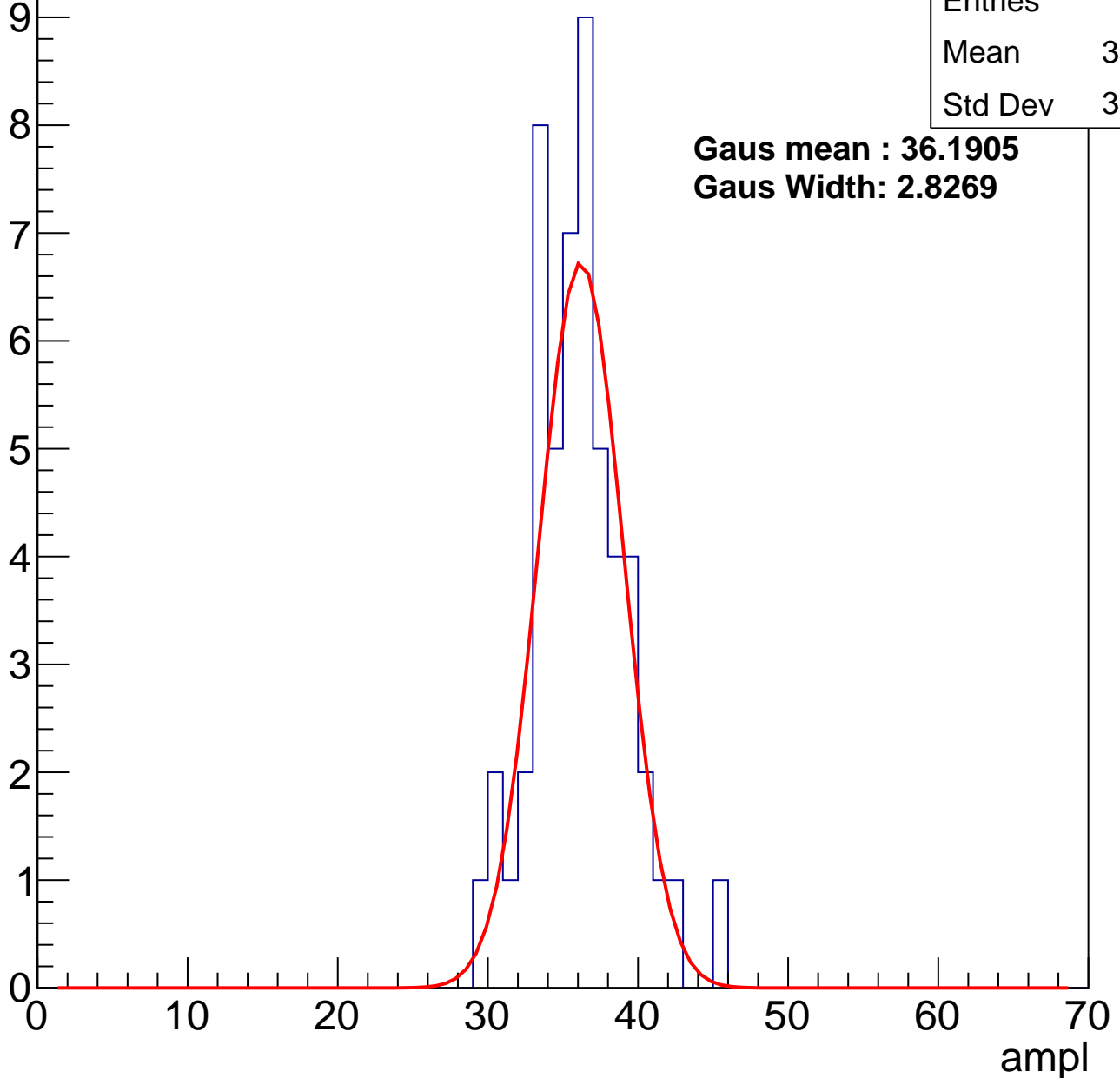
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.62
Std Dev	3.073

Gaus mean : 36.1905

Gaus Width: 2.8269



B1L003S, U19-ch30, adc2

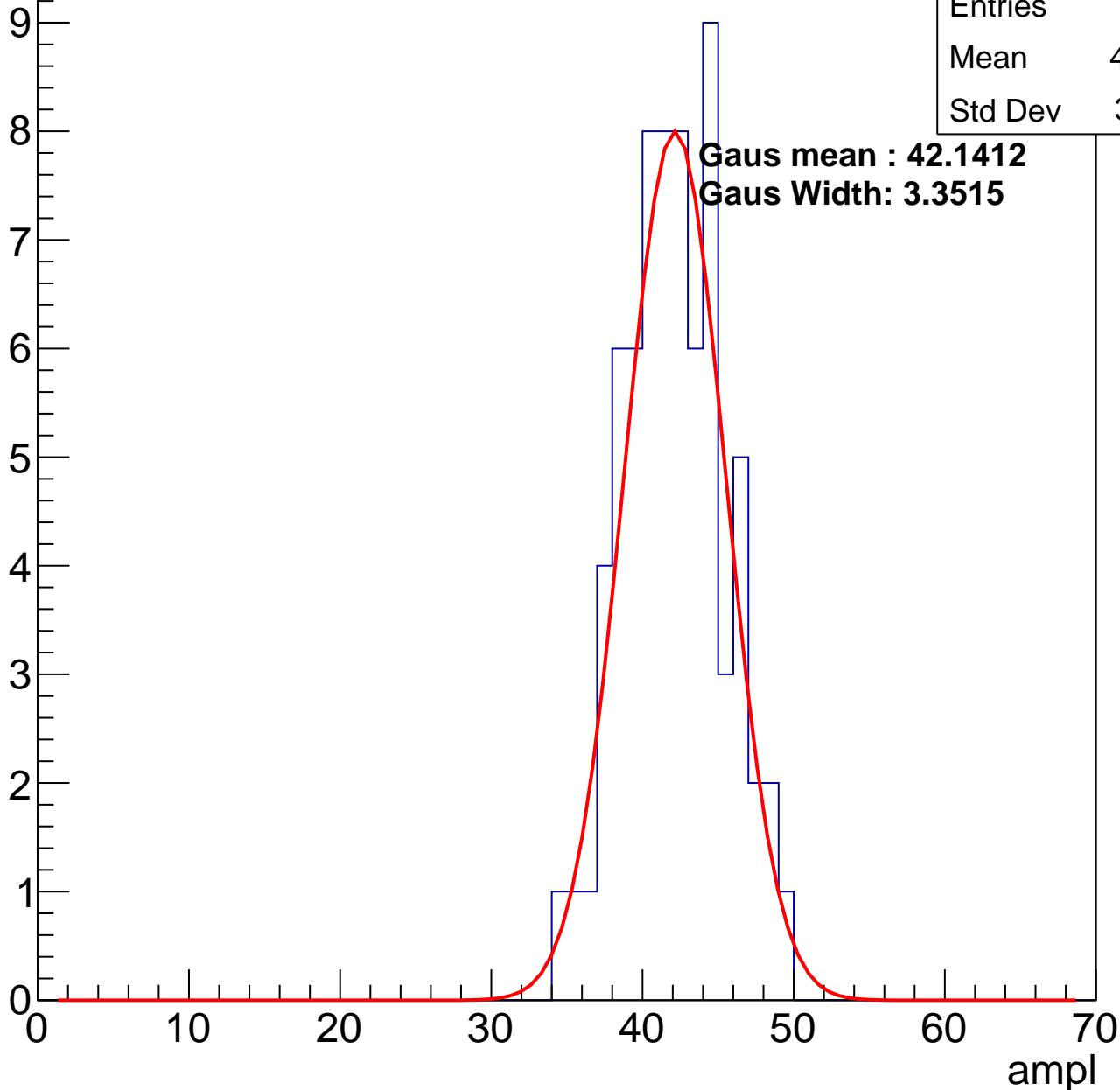
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	41.65
Std Dev	3.251

Gaus mean : 42.1412

Gaus Width: 3.3515

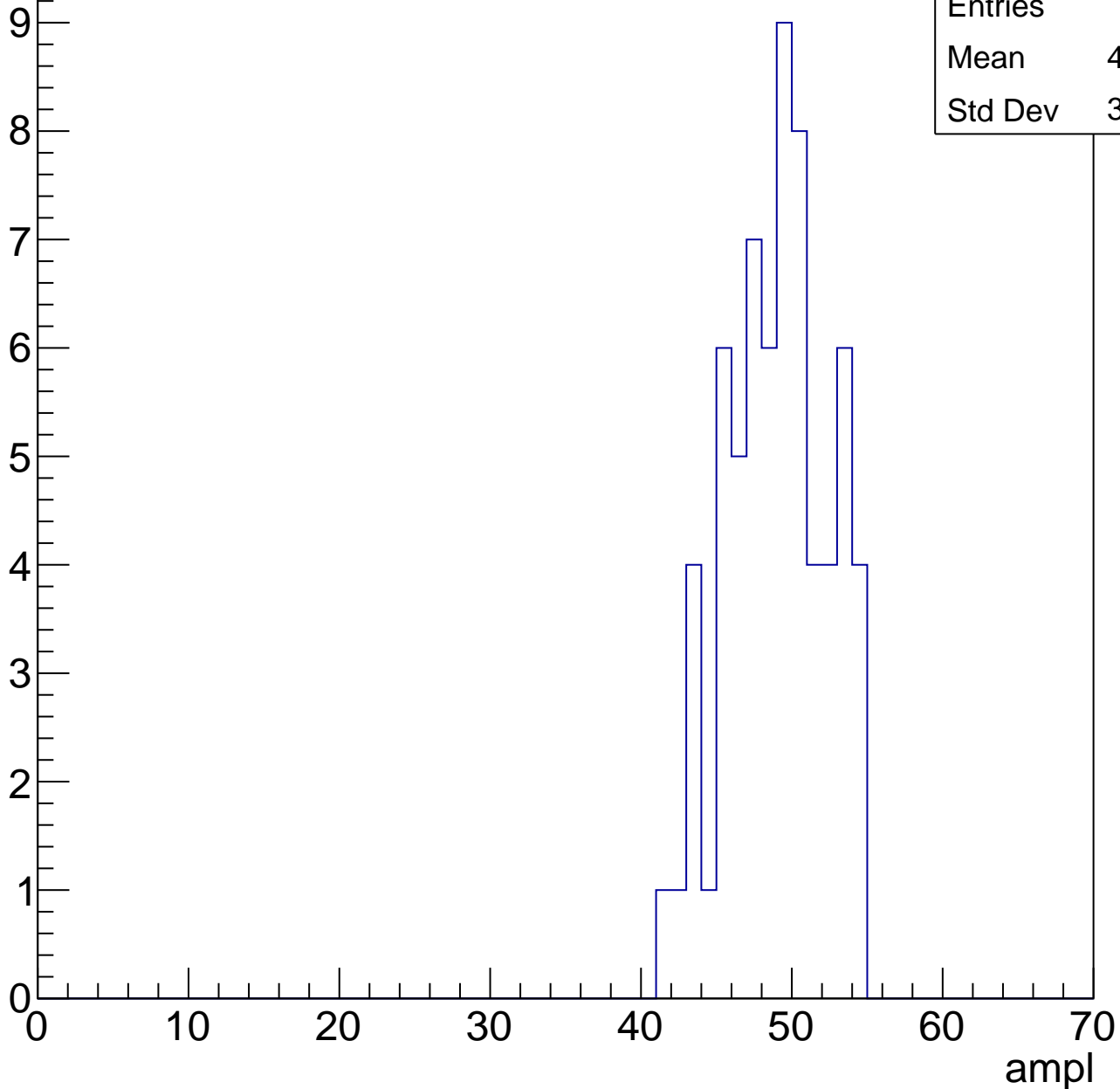


B1L003S, U19-ch30, adc3

calib_packv5_042523_0143.root, FC#13, port D2

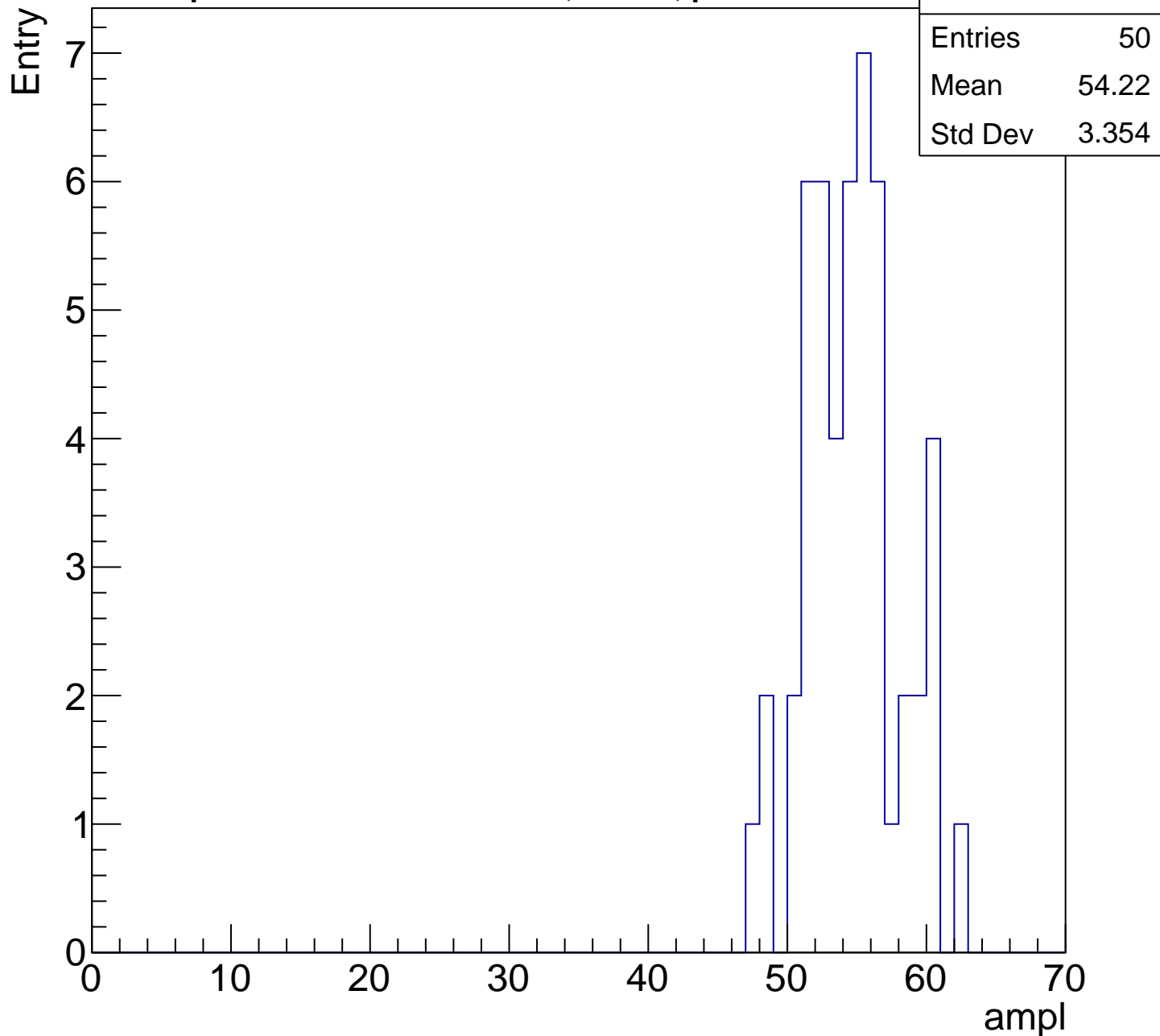
Entry

Entries	66
Mean	48.53
Std Dev	3.253



B1L003S, U19-ch30, adc4

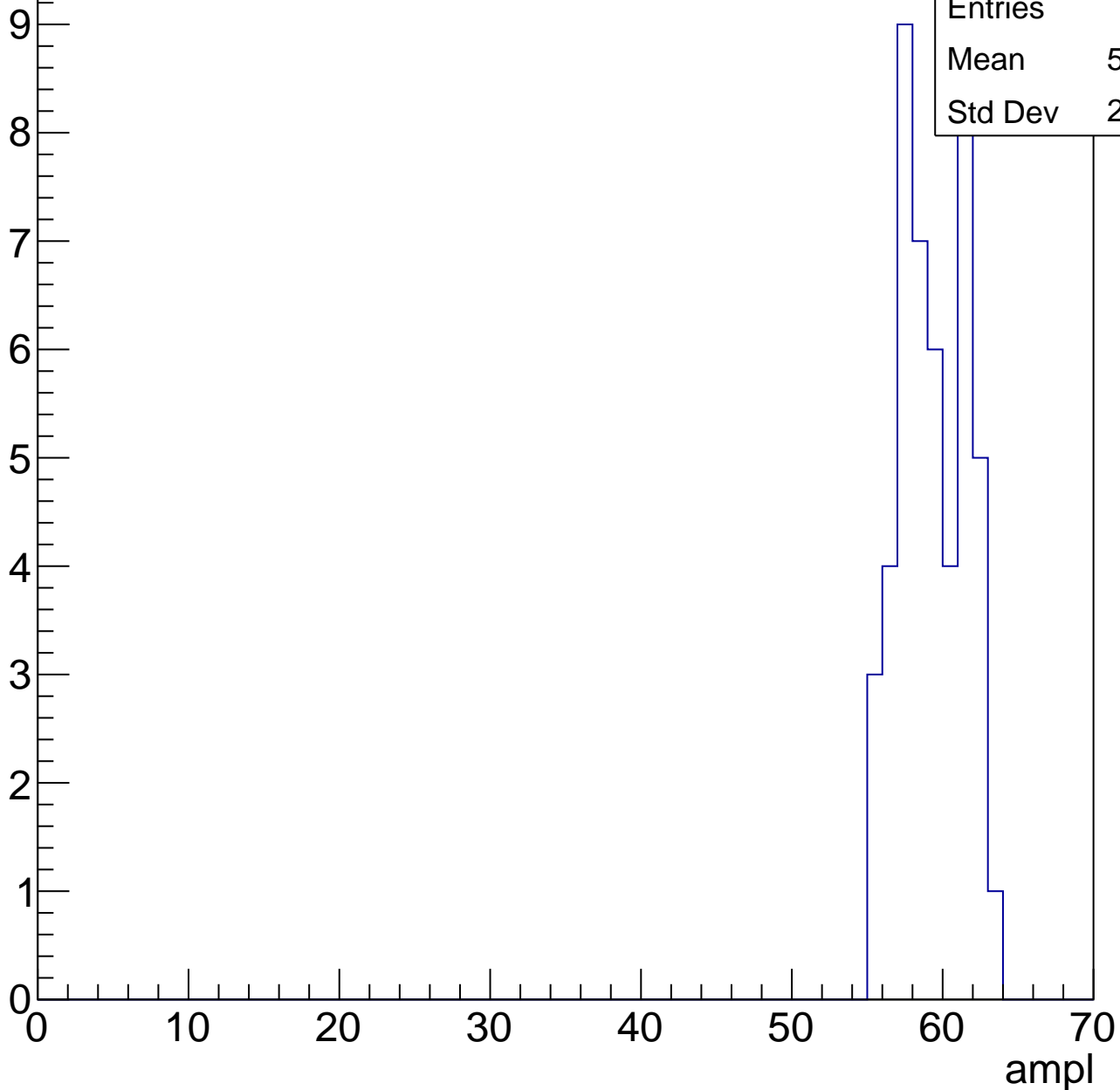
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch30, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

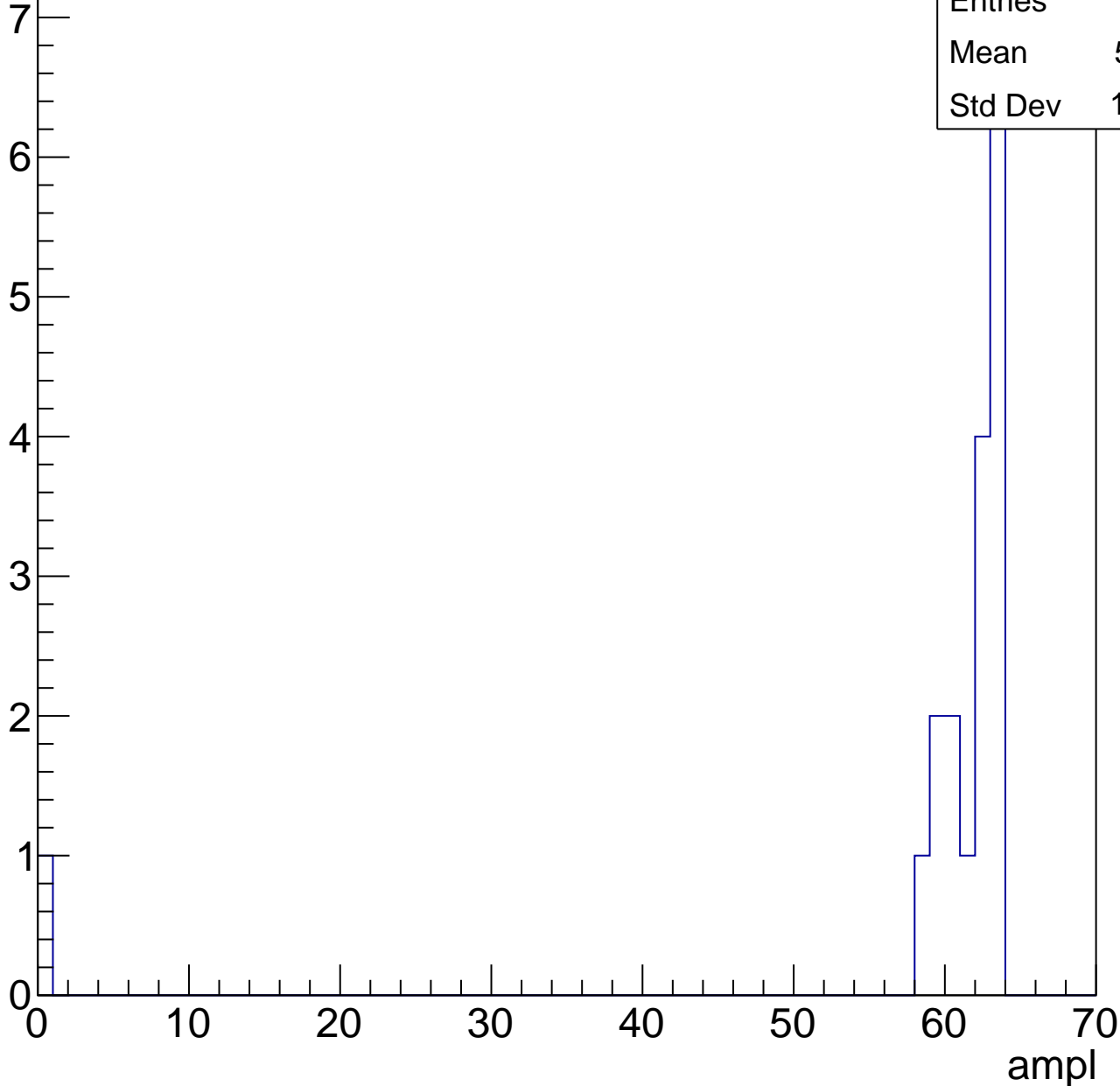


B1L003S, U19-ch30, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	18
Mean	58.11
Std Dev	14.18



B1L003S, U19-ch30, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch31, adc0

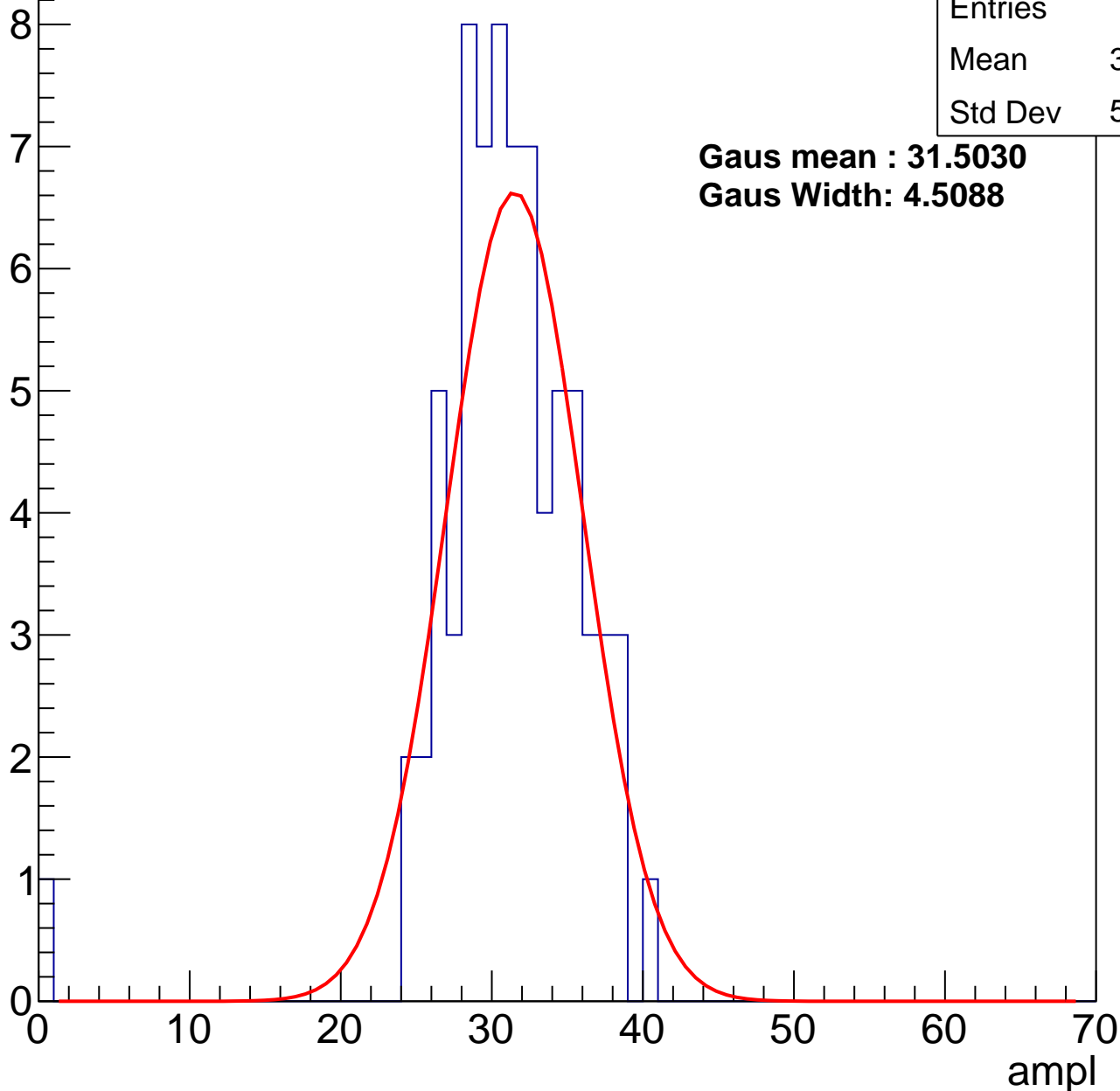
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	30.64
Std Dev	5.148

Gaus mean : 31.5030

Gaus Width: 4.5088



B1L003S, U19-ch31, adc1

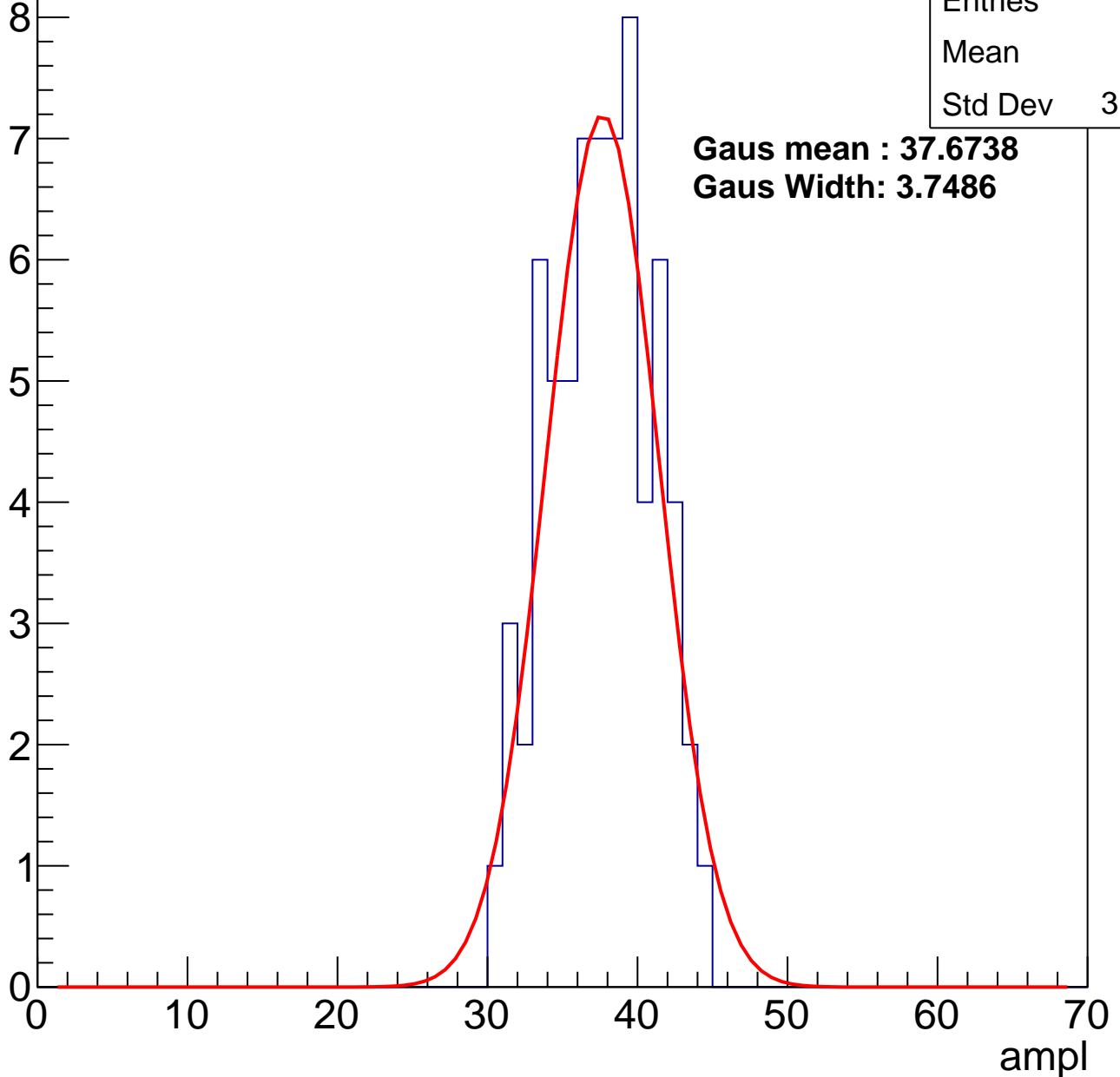
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.1
Std Dev	3.357

Gaus mean : 37.6738

Gaus Width: 3.7486



B1L003S, U19-ch31, adc2

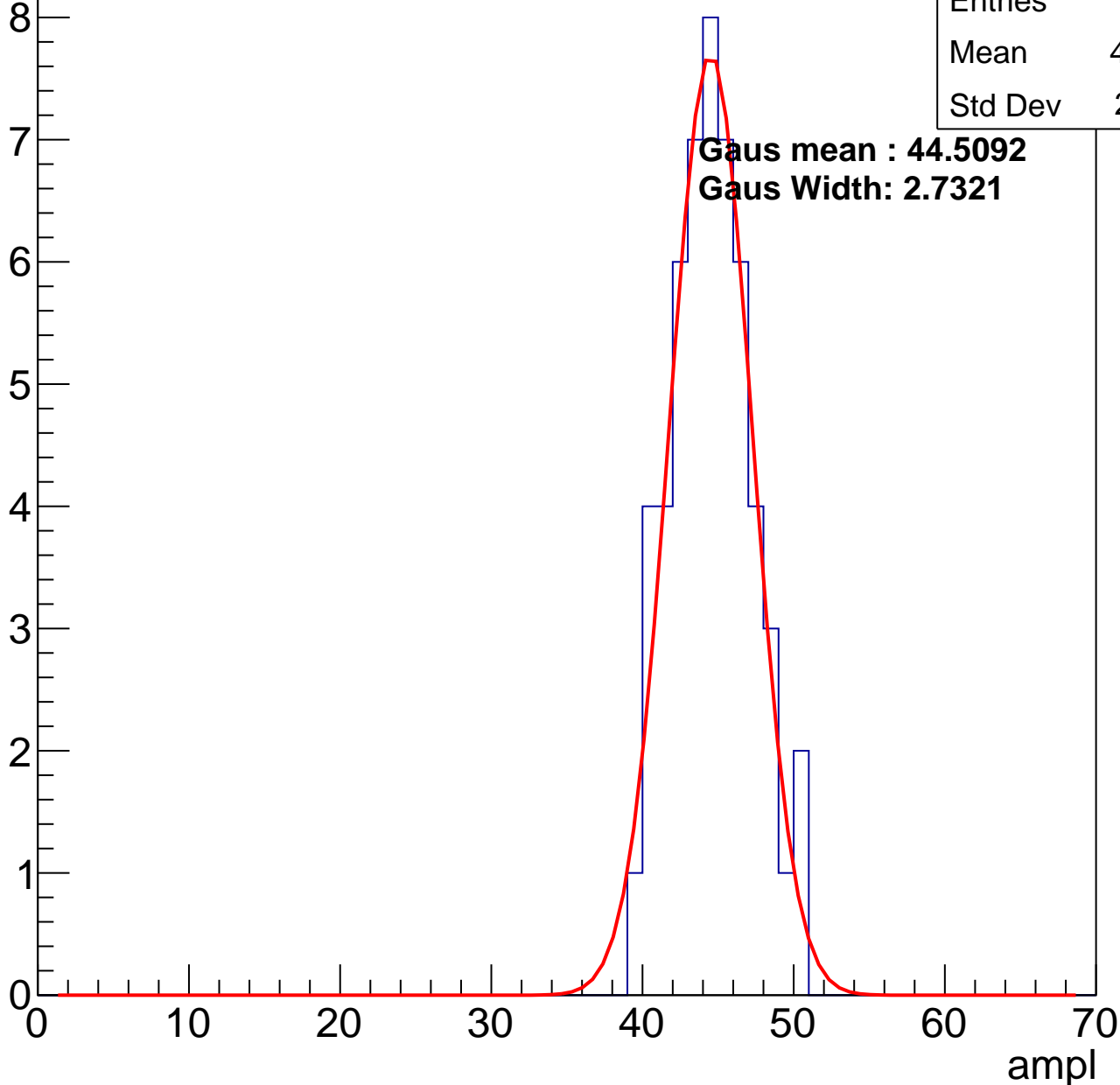
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	44.15
Std Dev	2.631

Gaus mean : 44.5092

Gaus Width: 2.7321

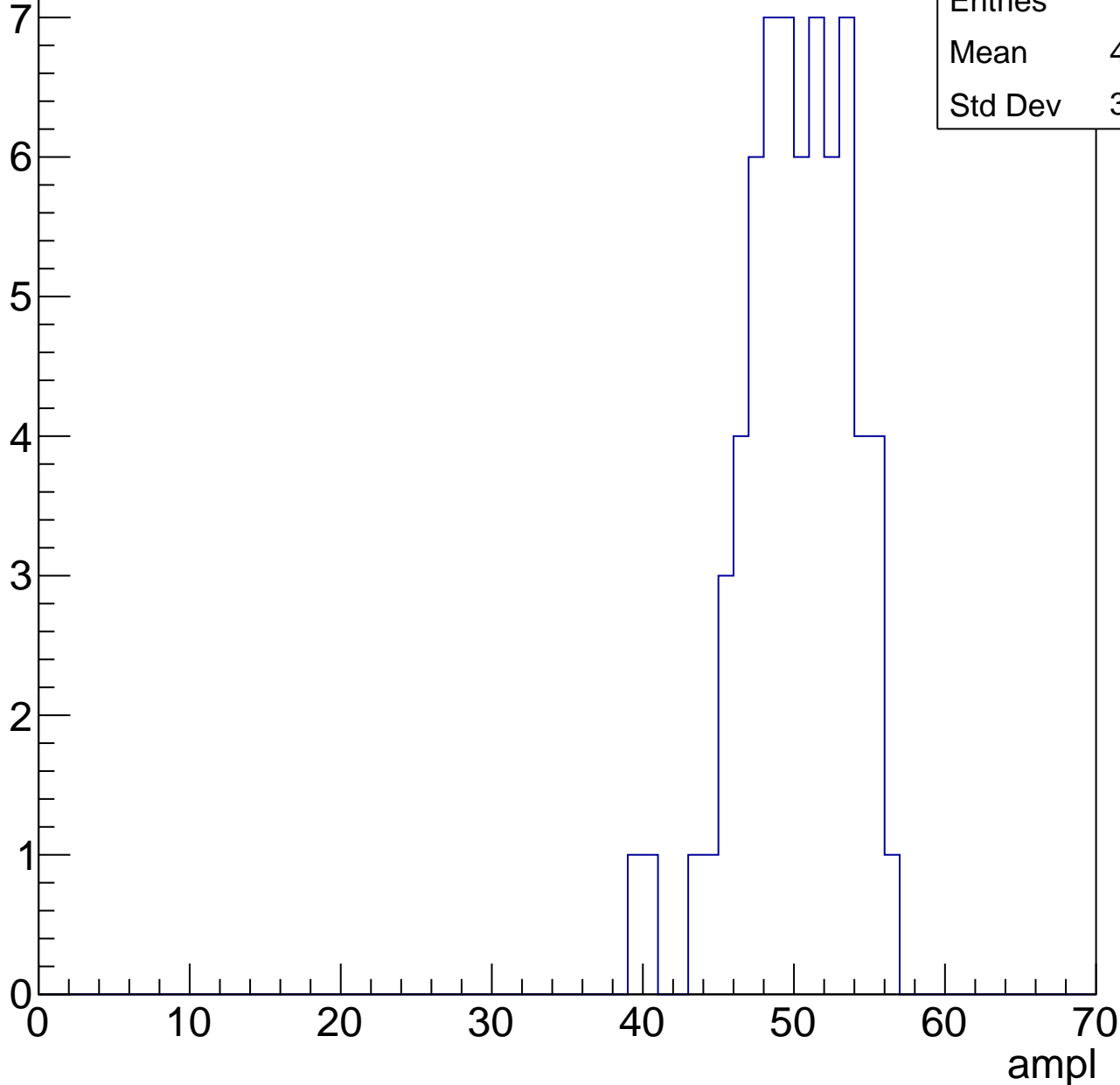


B1L003S, U19-ch31, adc3

calib_packv5_042523_0143.root, FC#13, port D2

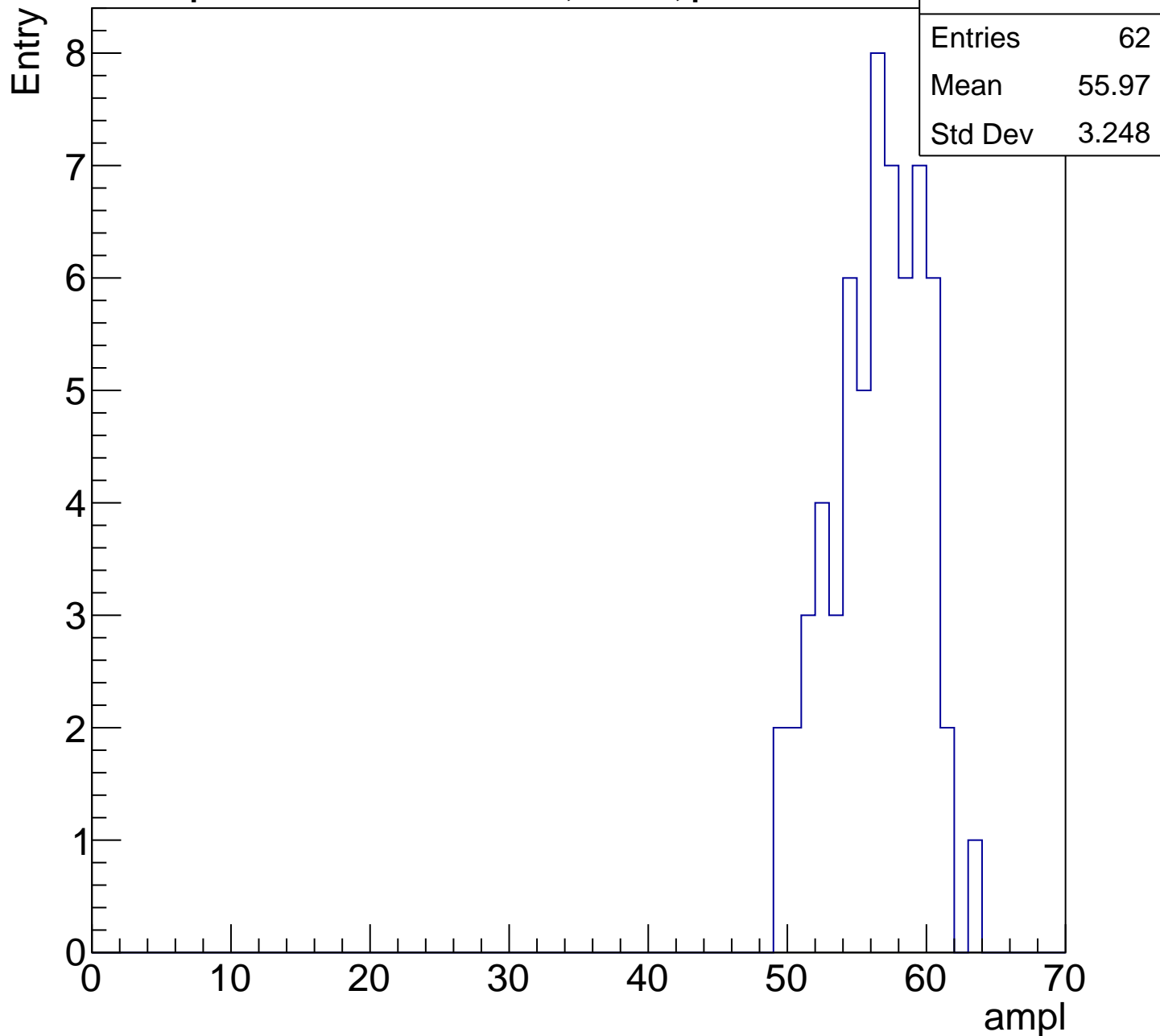
Entry

Entries	66
Mean	49.67
Std Dev	3.526



B1L003S, U19-ch31, adc4

calib_packv5_042523_0143.root, FC#13, port D2

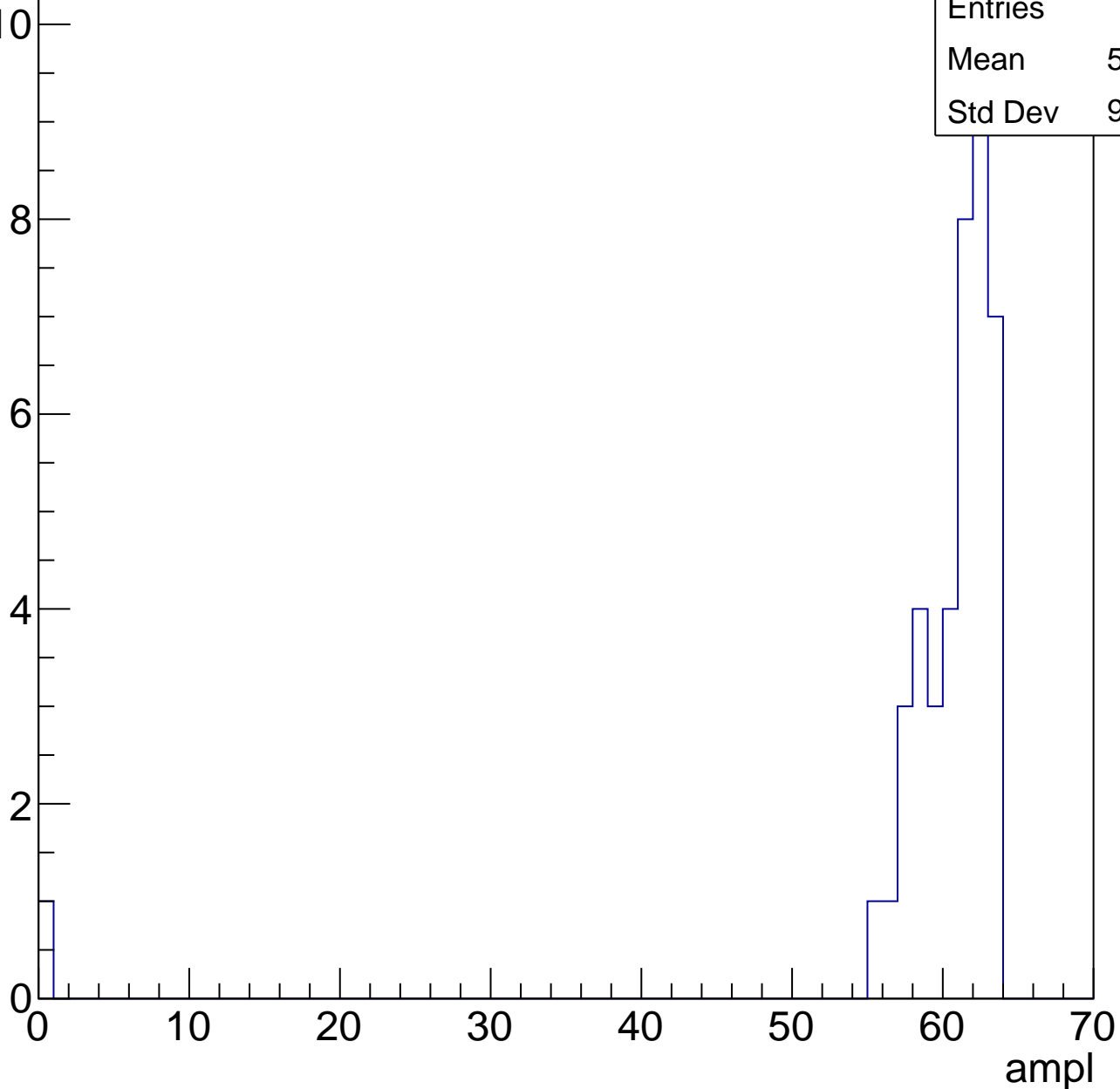


B1L003S, U19-ch31, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	59.05
Std Dev	9.462



B1L003S, U19-ch31, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L003S, U19-ch31, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch32, adc0

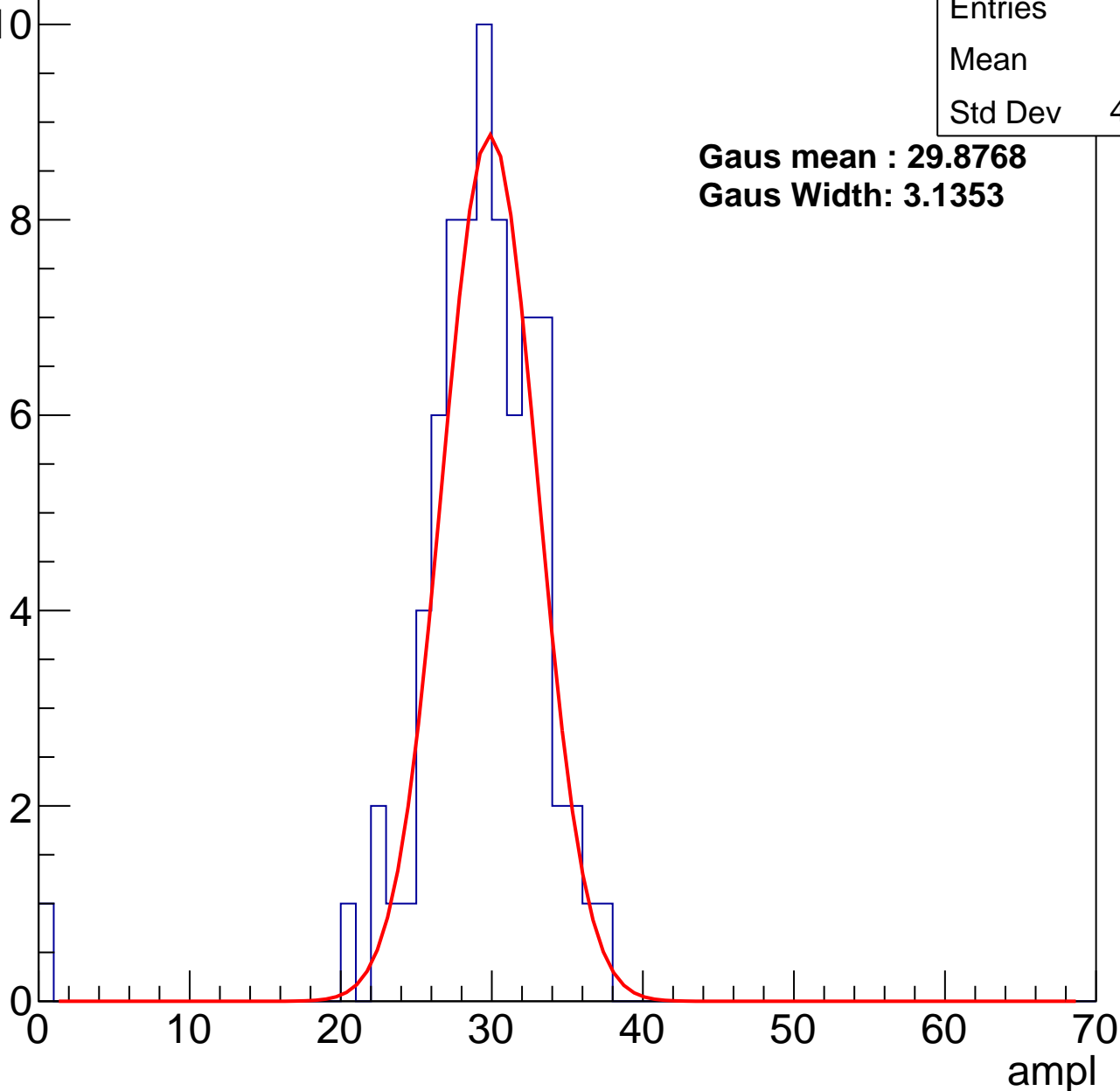
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	28.8
Std Dev	4.699

Gaus mean : 29.8768

Gaus Width: 3.1353



B1L003S, U19-ch32, adc1

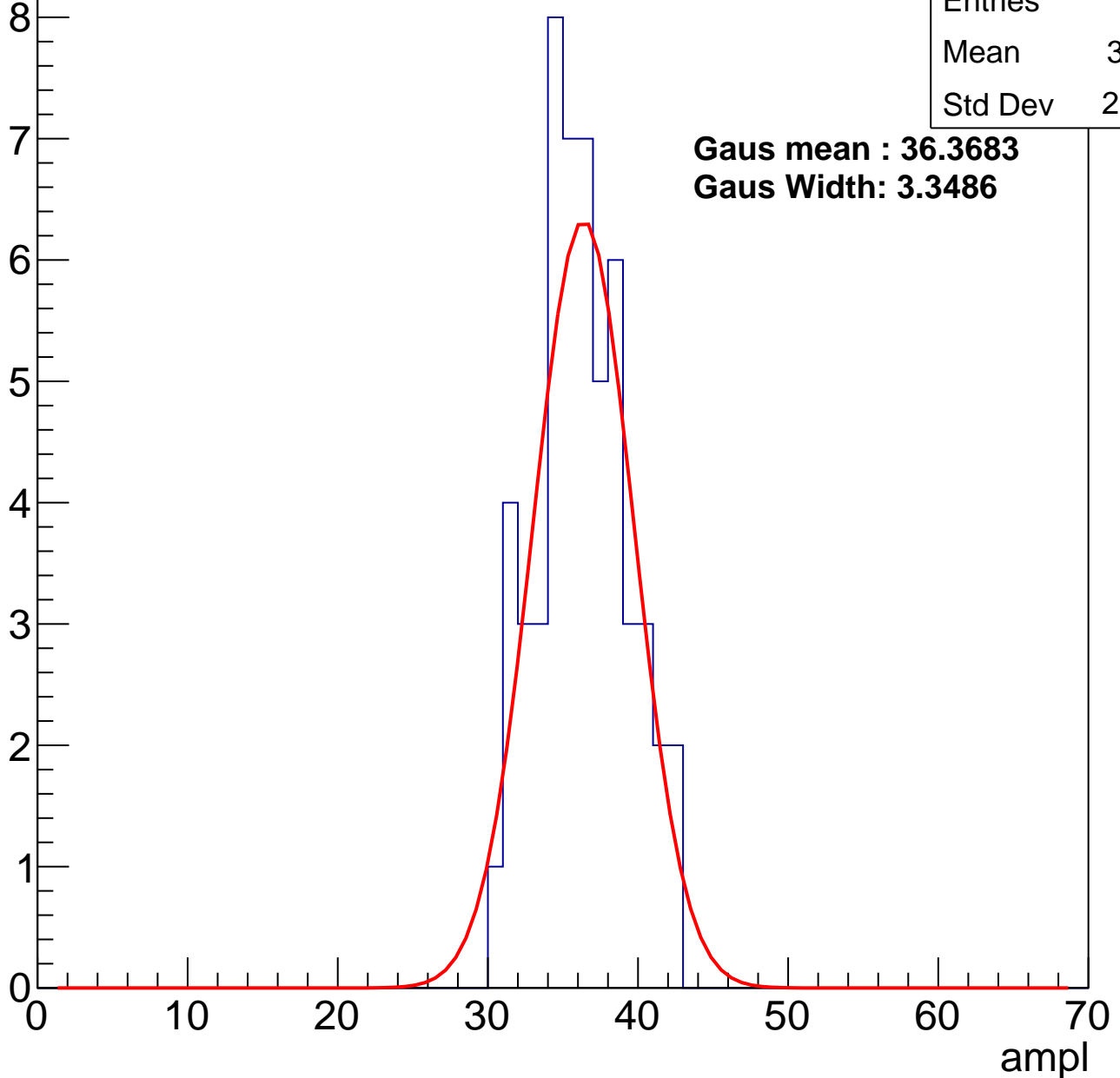
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	35.81
Std Dev	2.963

Gaus mean : 36.3683

Gaus Width: 3.3486



B1L003S, U19-ch32, adc2

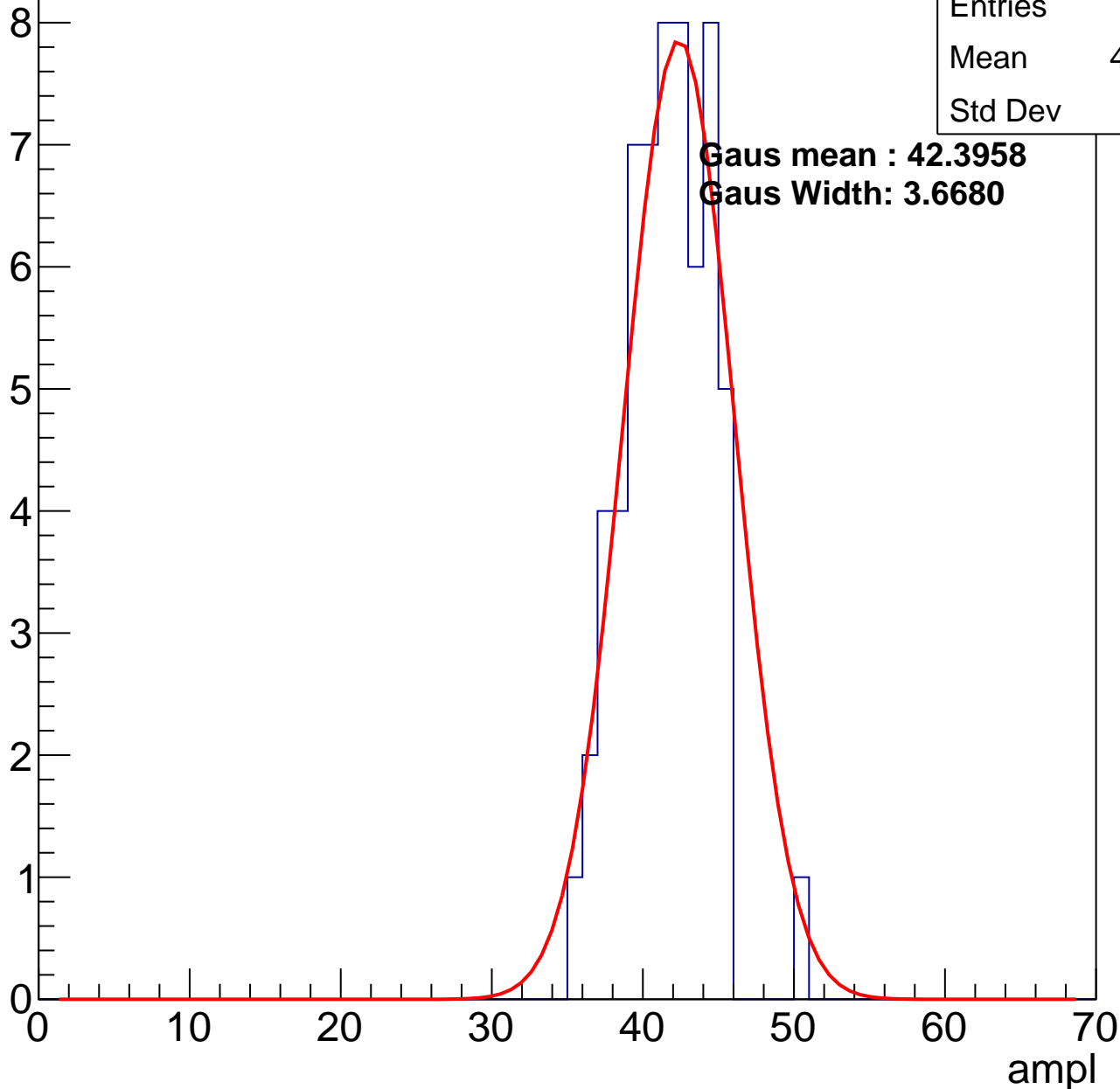
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	41.13
Std Dev	2.82

Gaus mean : 42.3958

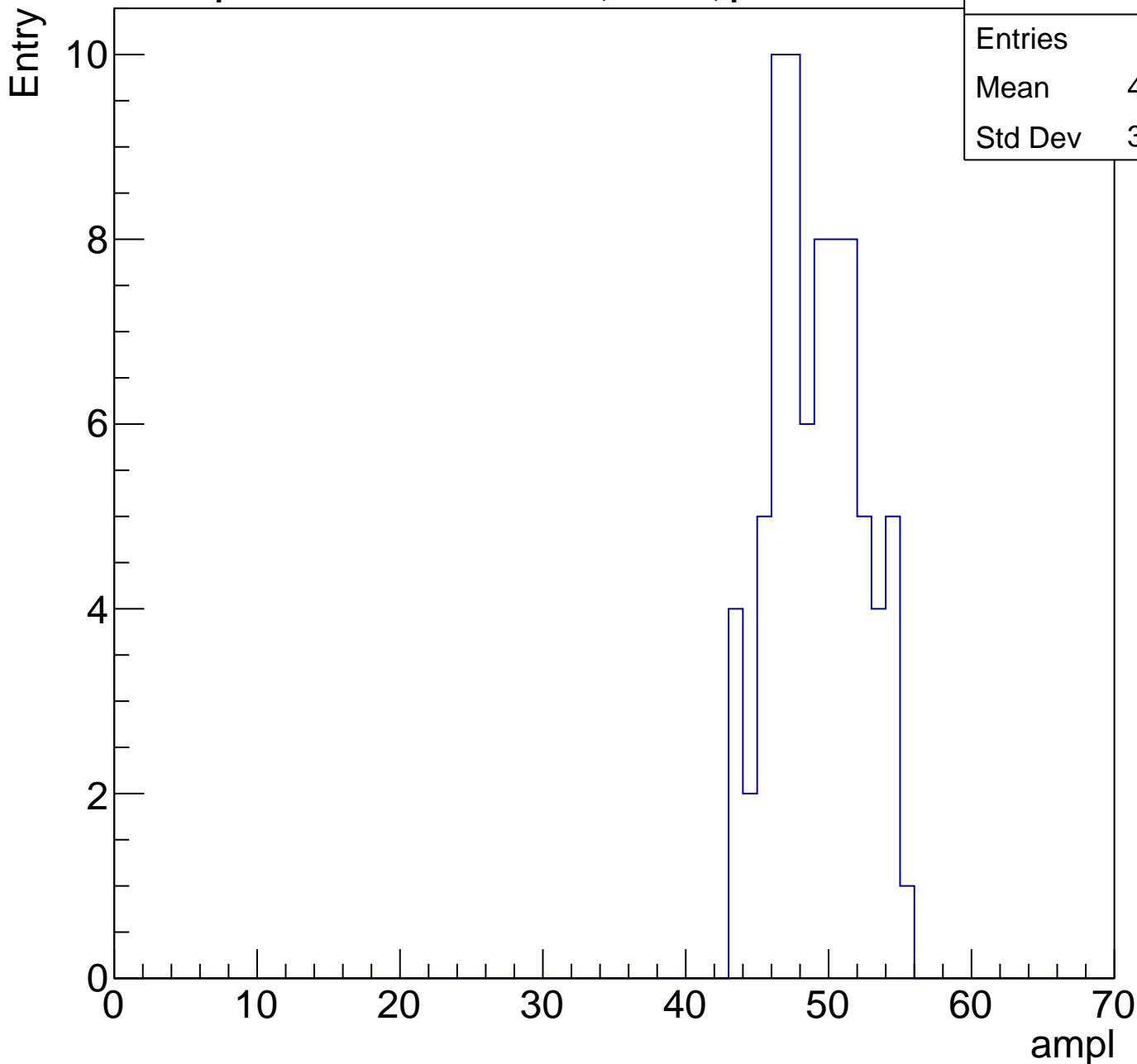
Gaus Width: 3.6680



B1L003S, U19-ch32, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	48.68
Std Dev	3.062

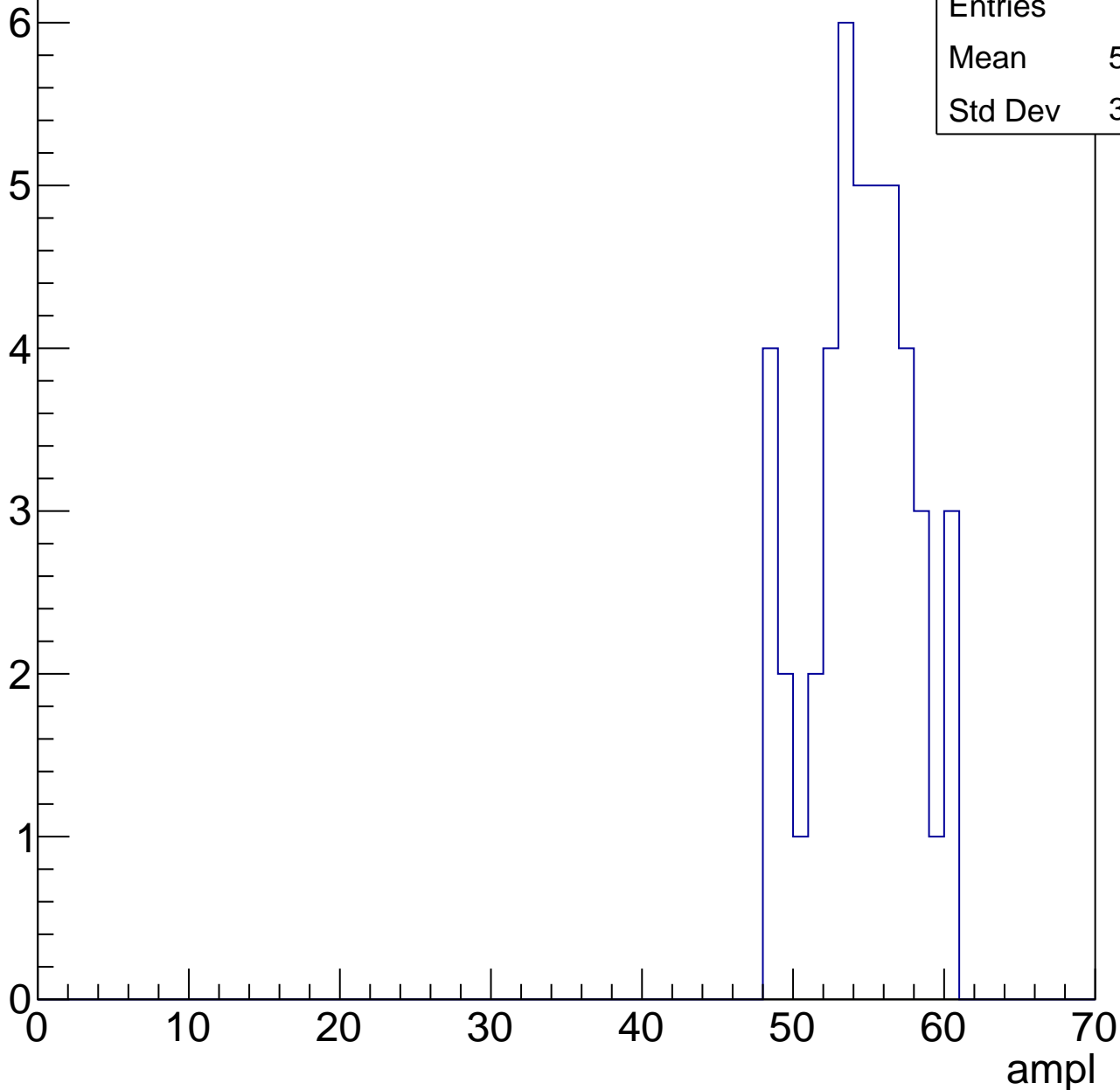


B1L003S, U19-ch32, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

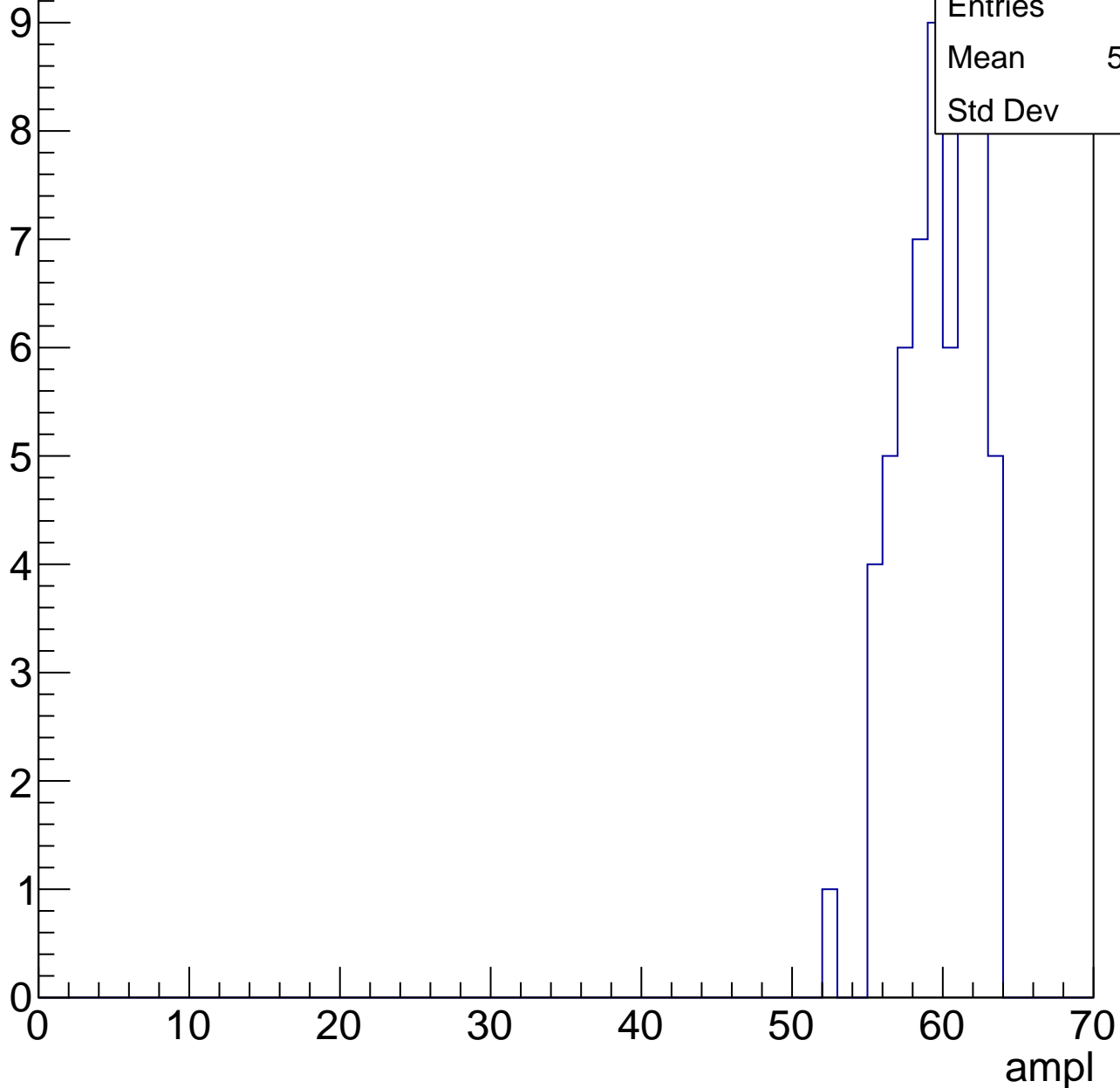
Entries	45
Mean	54.09
Std Dev	3.305



B1L003S, U19-ch32, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



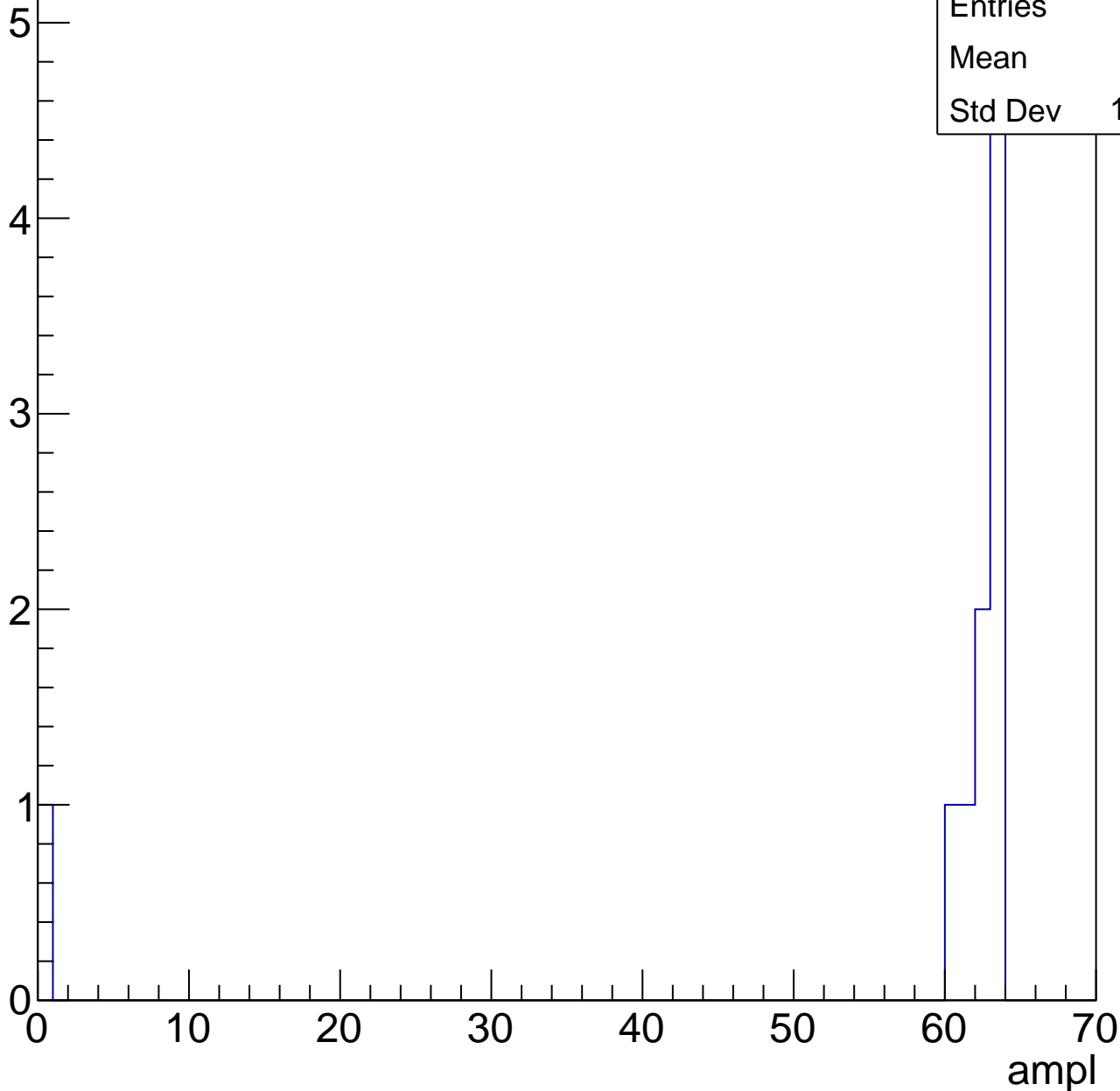
Entries	60
Mean	59.18
Std Dev	2.52

B1L003S, U19-ch32, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	56
Std Dev	18.69



B1L003S, U19-ch32, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch33, adc0

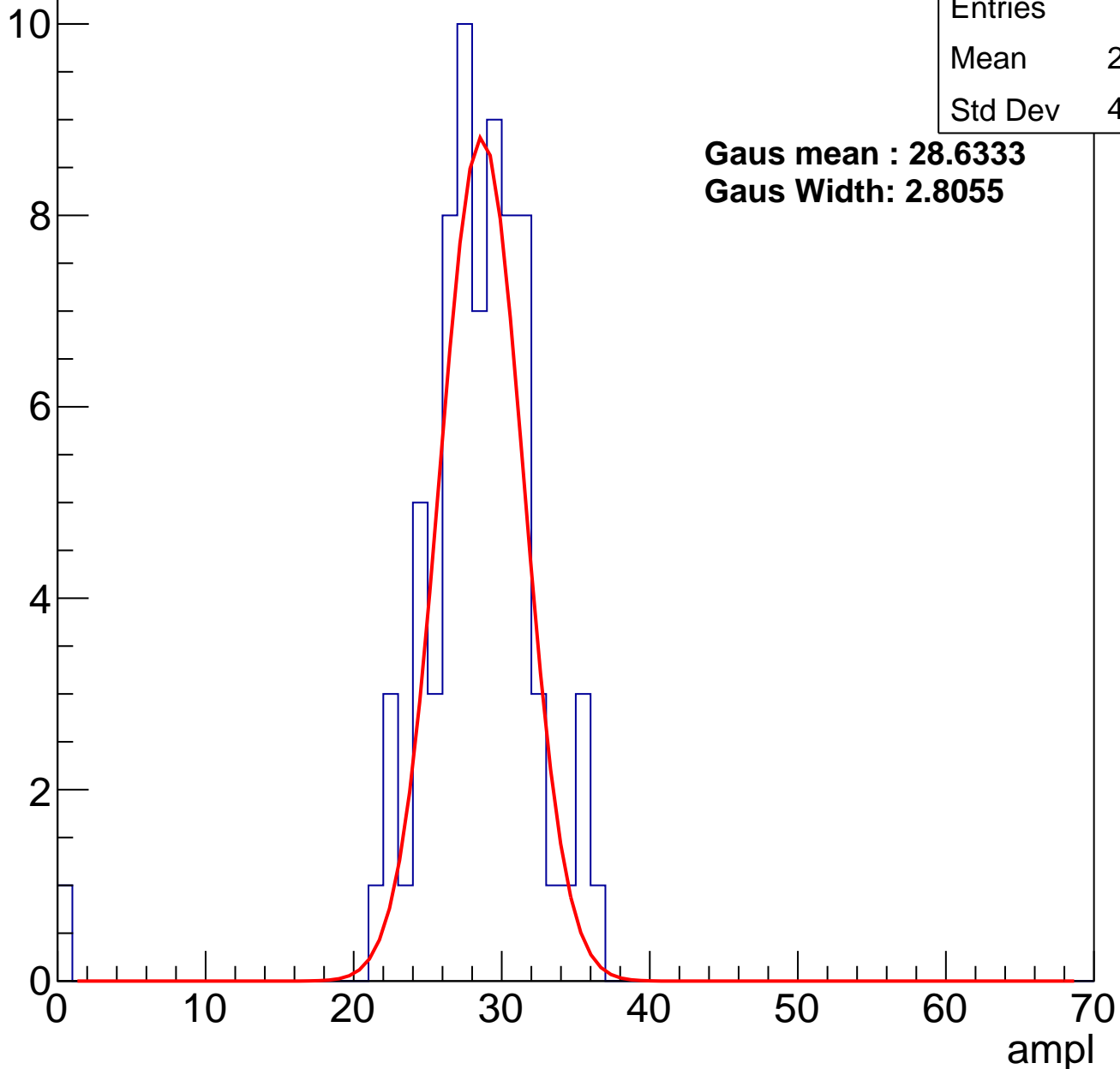
calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	27.84
Std Dev	4.602

Gaus mean : 28.6333

Gaus Width: 2.8055

Entry



B1L003S, U19-ch33, adc1

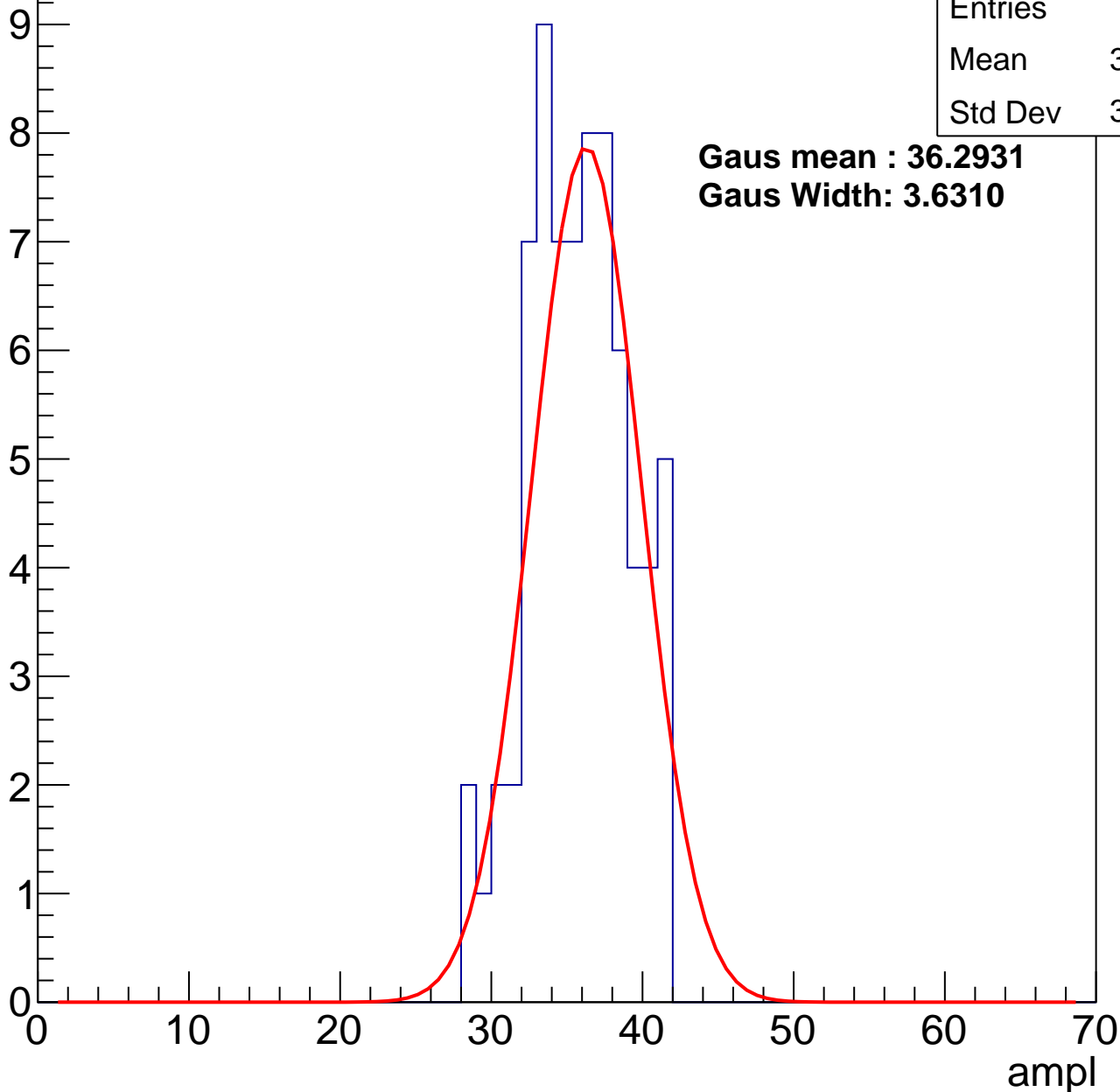
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	35.33
Std Dev	3.232

Gaus mean : 36.2931

Gaus Width: 3.6310



B1L003S, U19-ch33, adc2

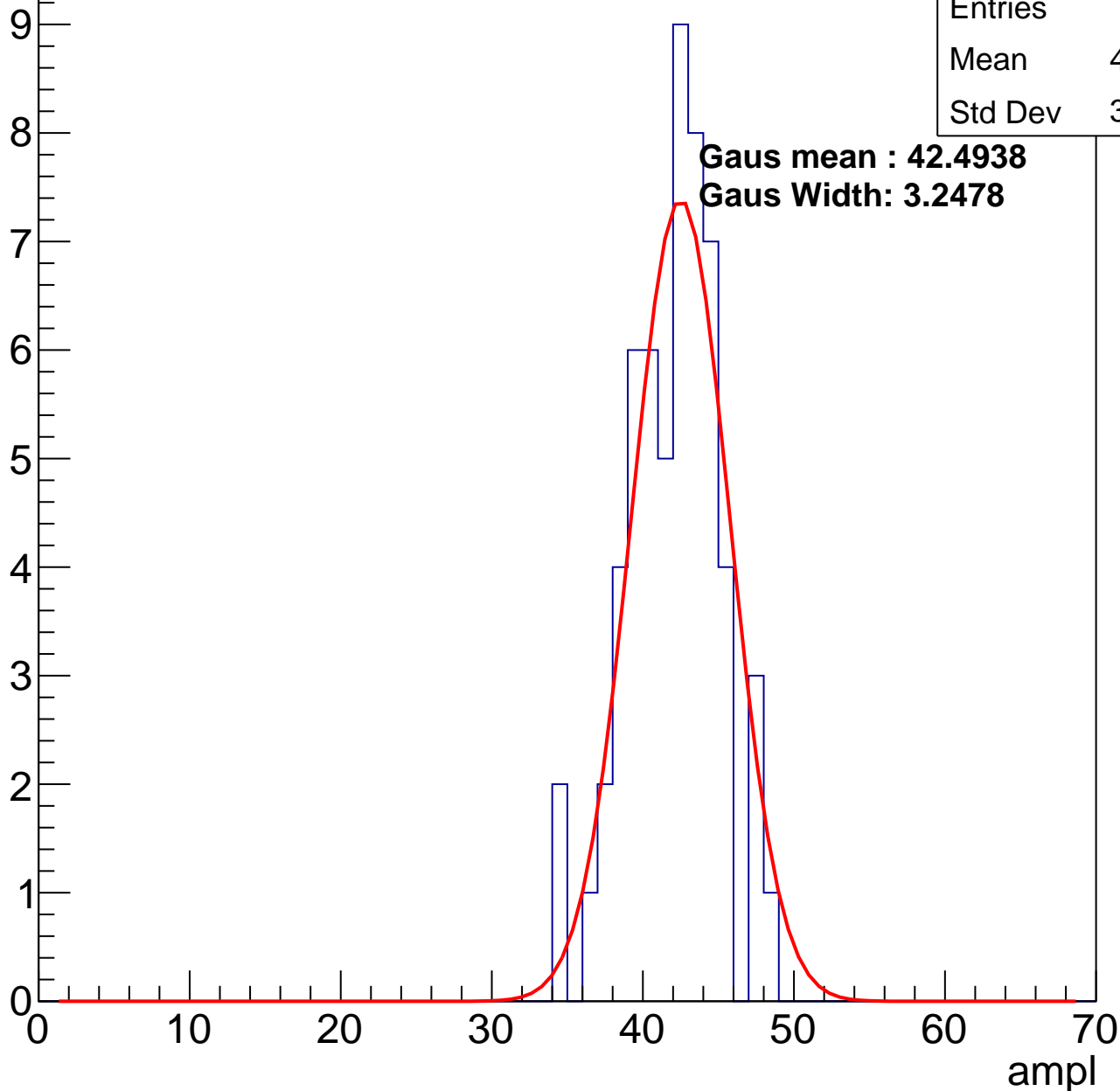
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	41.52
Std Dev	3.024

Gaus mean : 42.4938

Gaus Width: 3.2478



B1L003S, U19-ch33, adc3

calib_packv5_042523_0143.root, FC#13, port D2

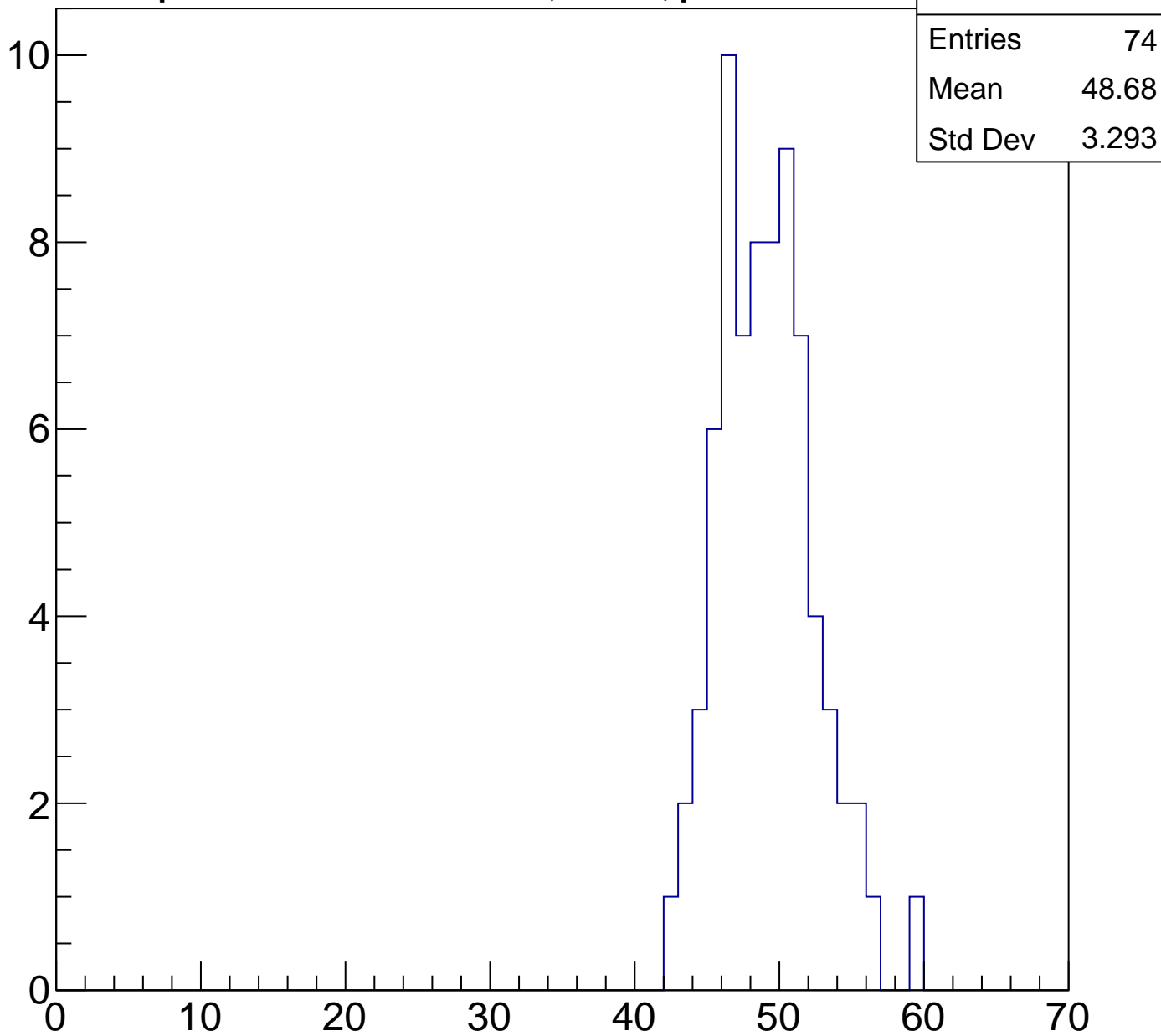
Entries	74
Mean	48.68
Std Dev	3.293

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

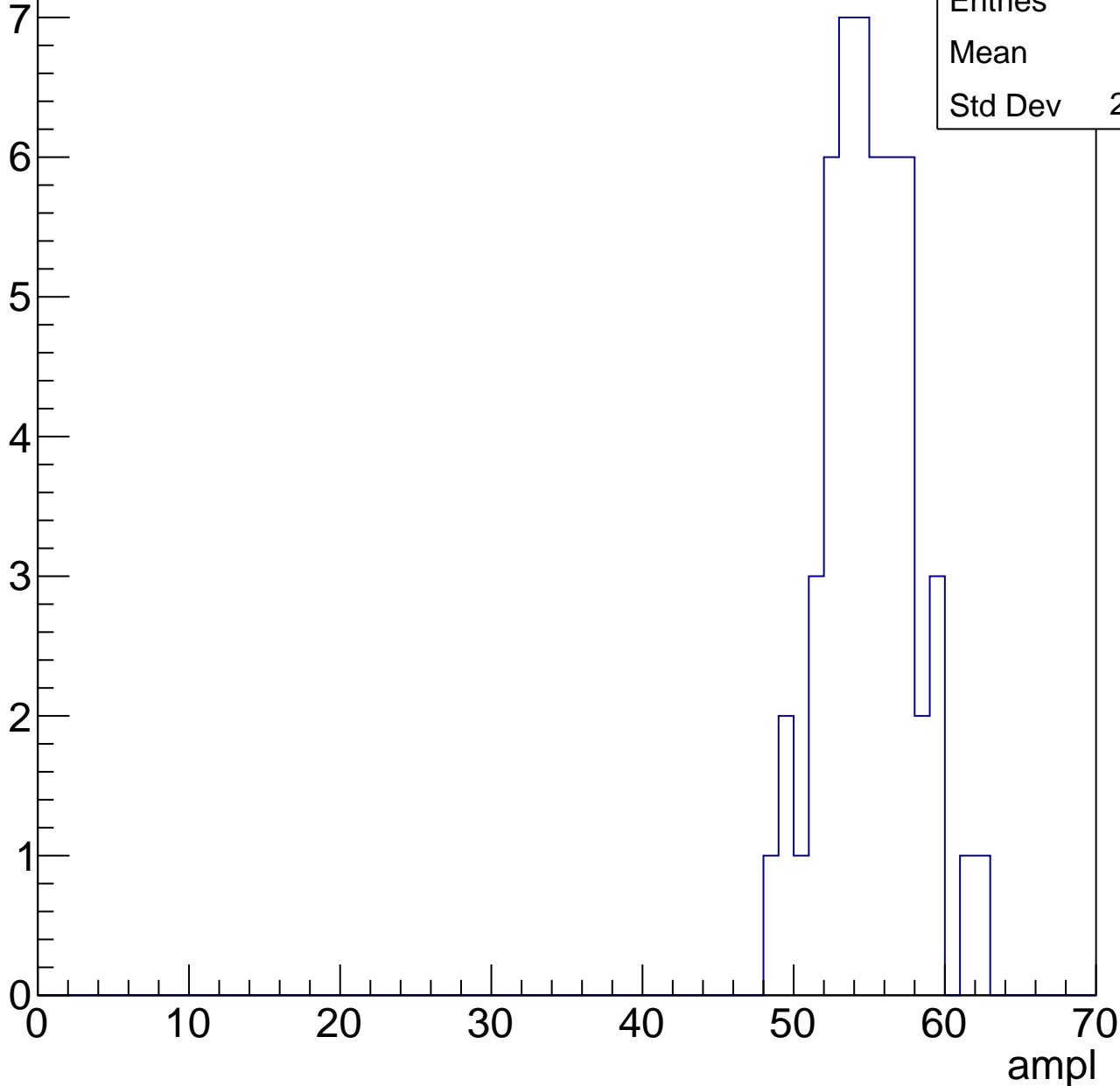


B1L003S, U19-ch33, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

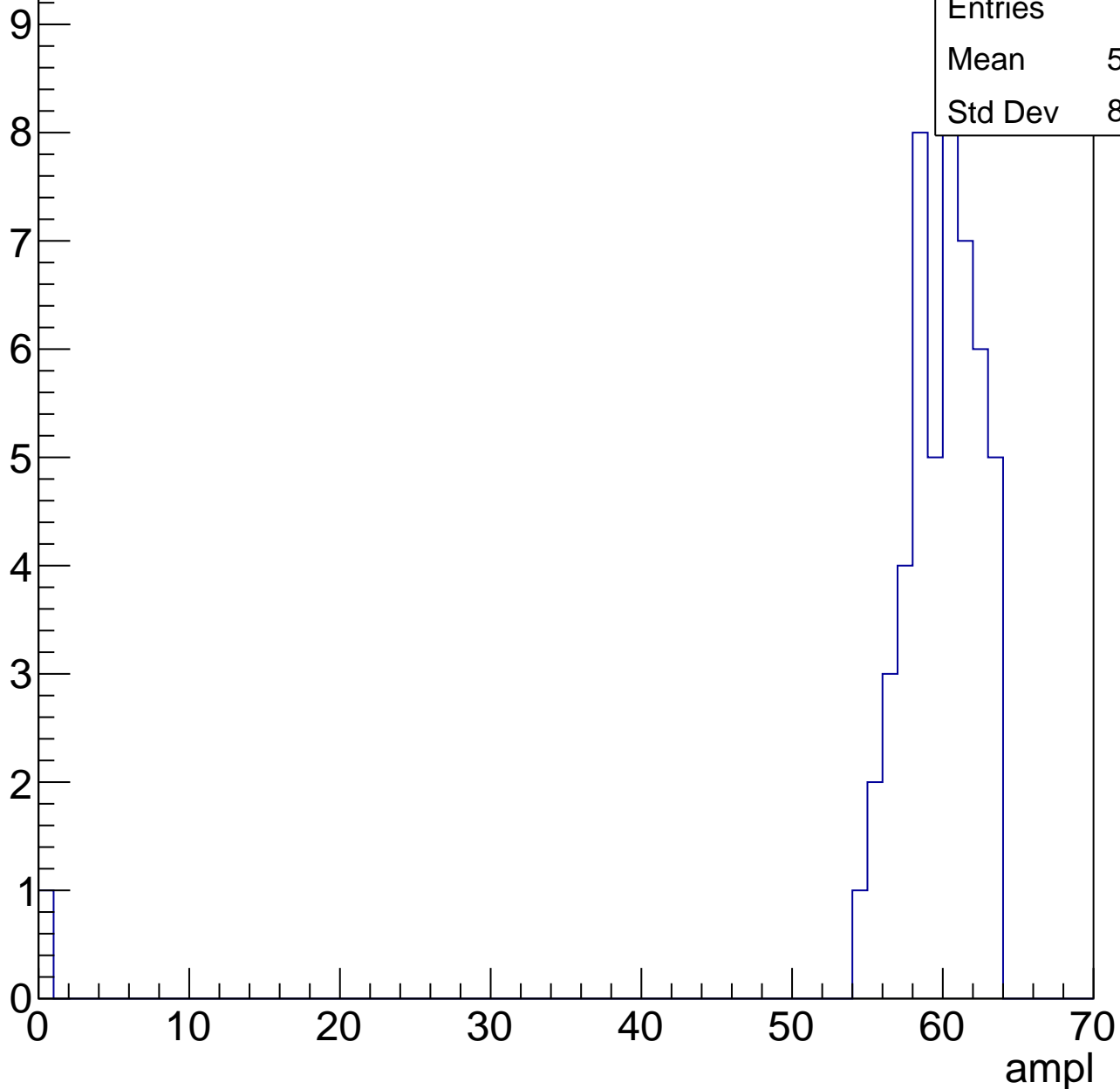
Entries	52
Mean	54.5
Std Dev	2.945



B1L003S, U19-ch33, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



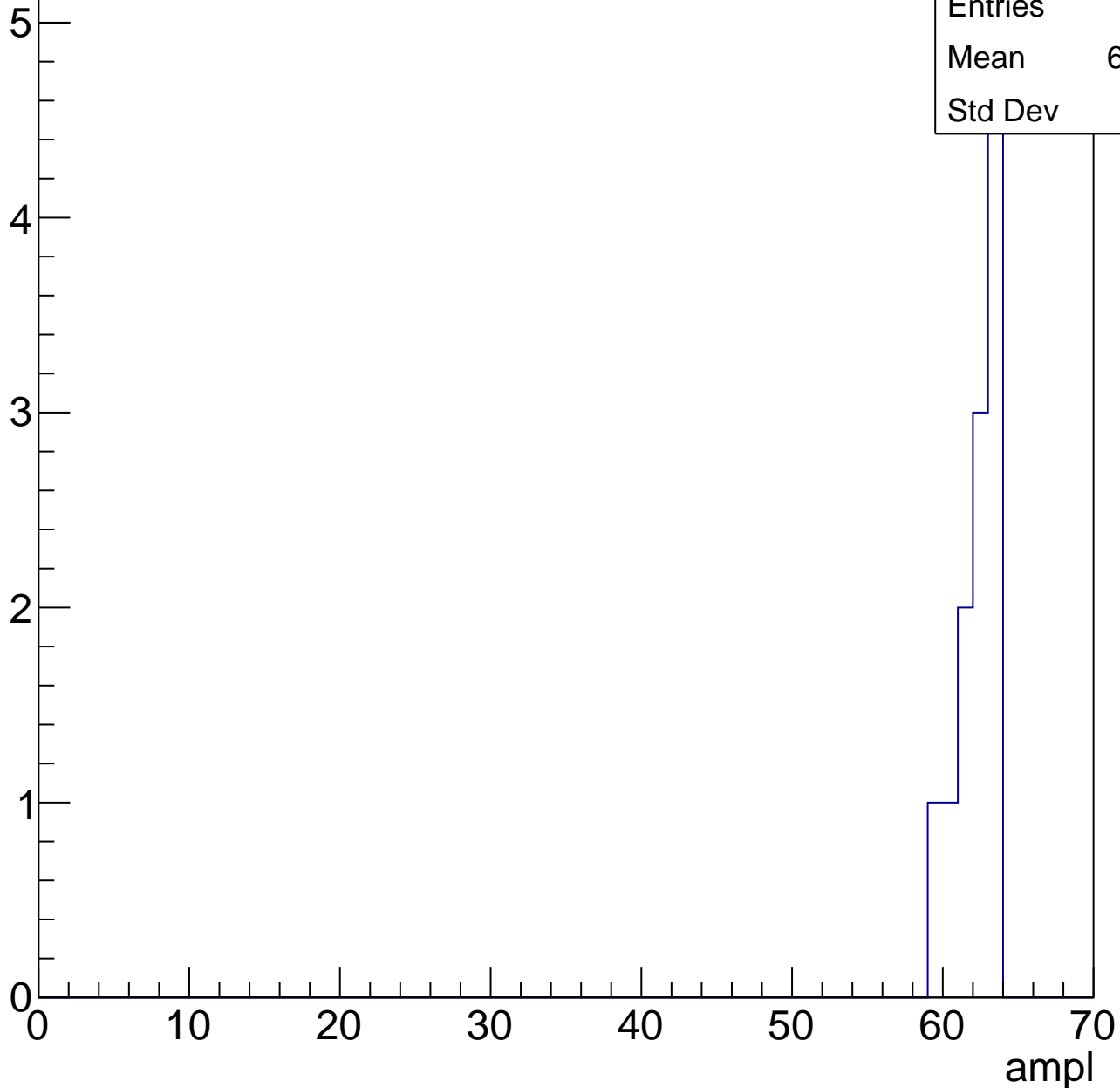
Entries	51
Mean	58.29
Std Dev	8.557

B1L003S, U19-ch33, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	12
Mean	61.83
Std Dev	1.28



B1L003S, U19-ch33, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch34, adc0

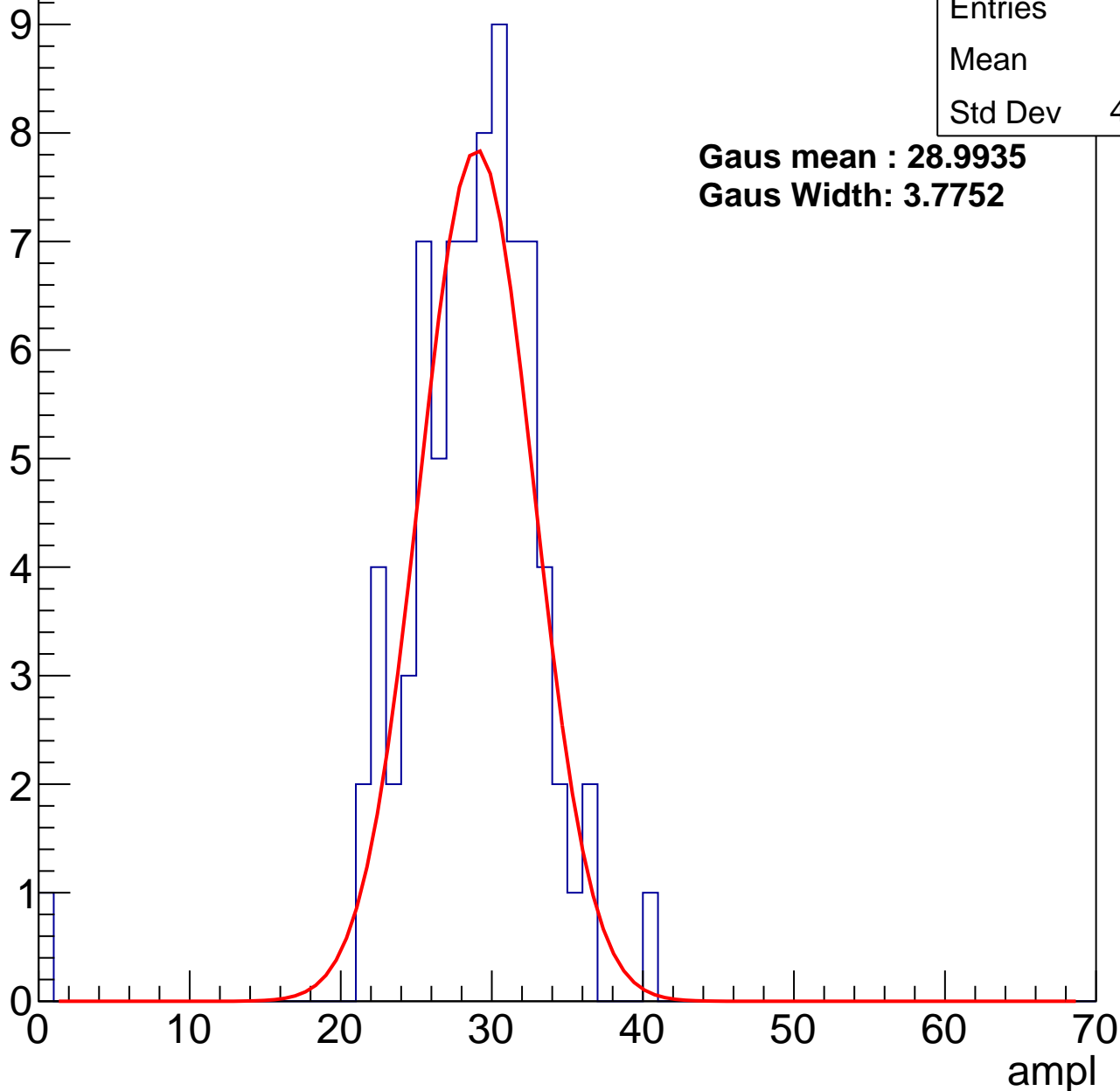
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	28.2
Std Dev	4.933

Gaus mean : 28.9935

Gaus Width: 3.7752



B1L003S, U19-ch34, adc1

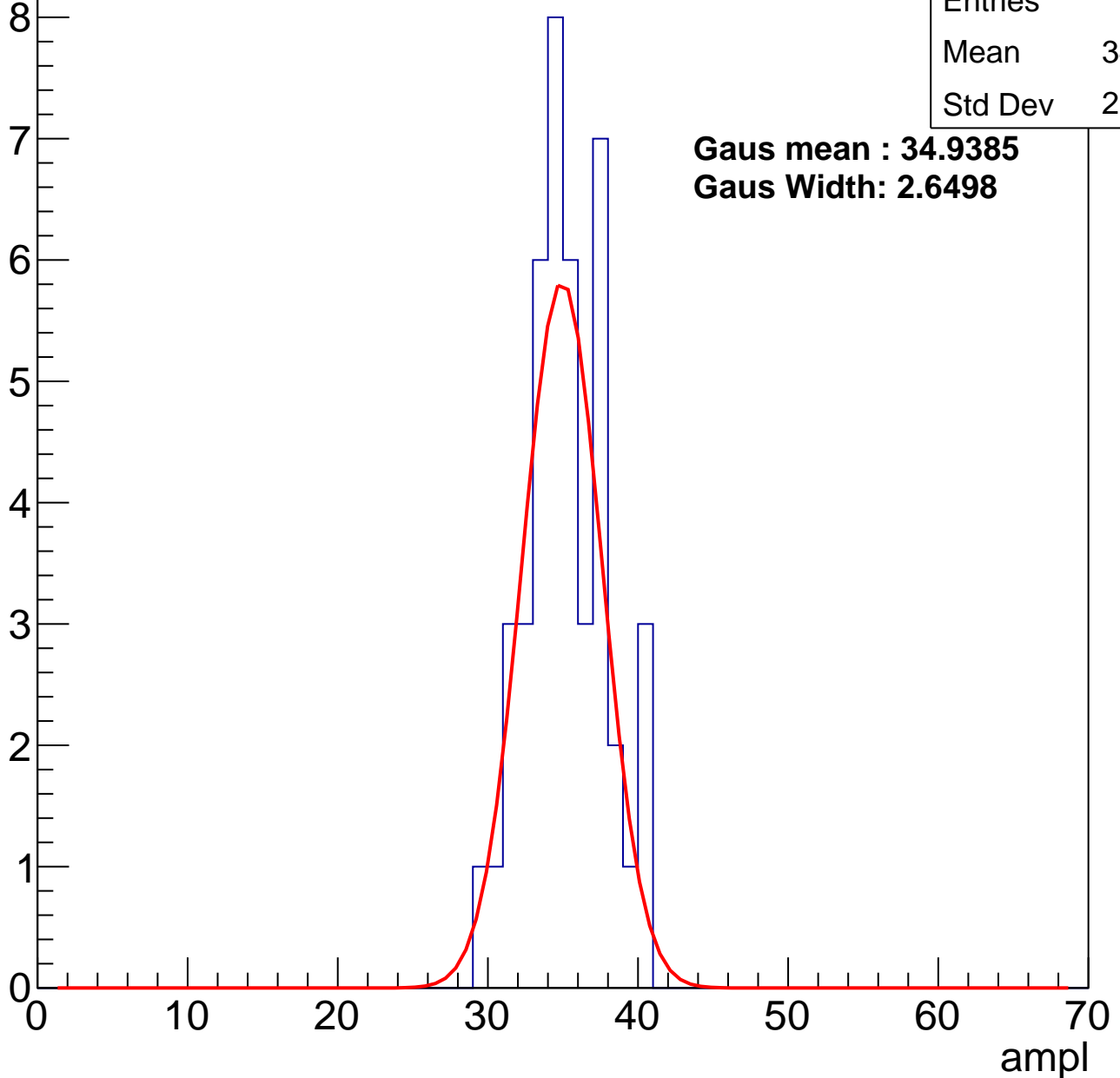
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	34.77
Std Dev	2.636

Gaus mean : 34.9385

Gaus Width: 2.6498



B1L003S, U19-ch34, adc2

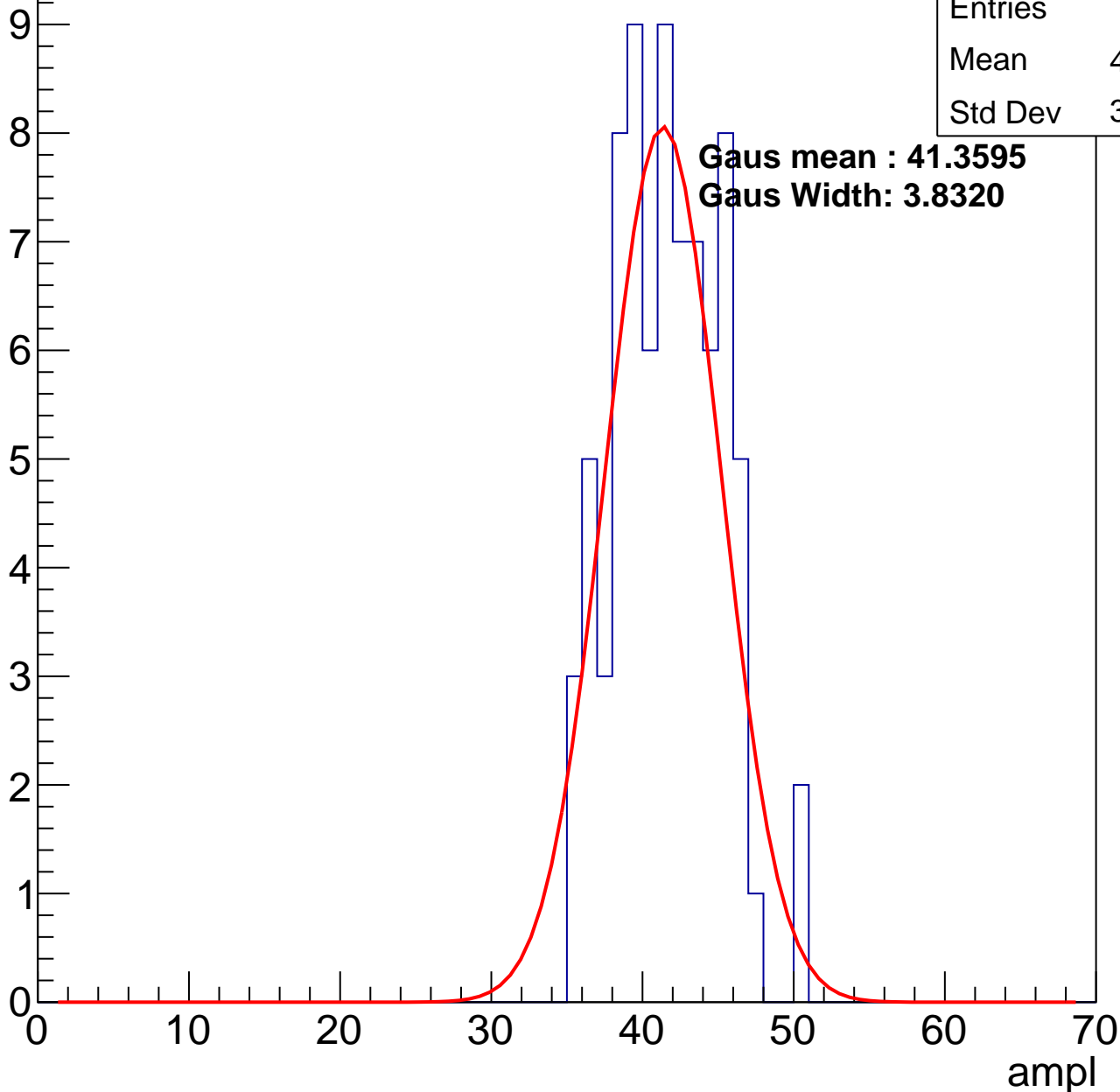
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	41.22
Std Dev	3.434

Gaus mean : 41.3595

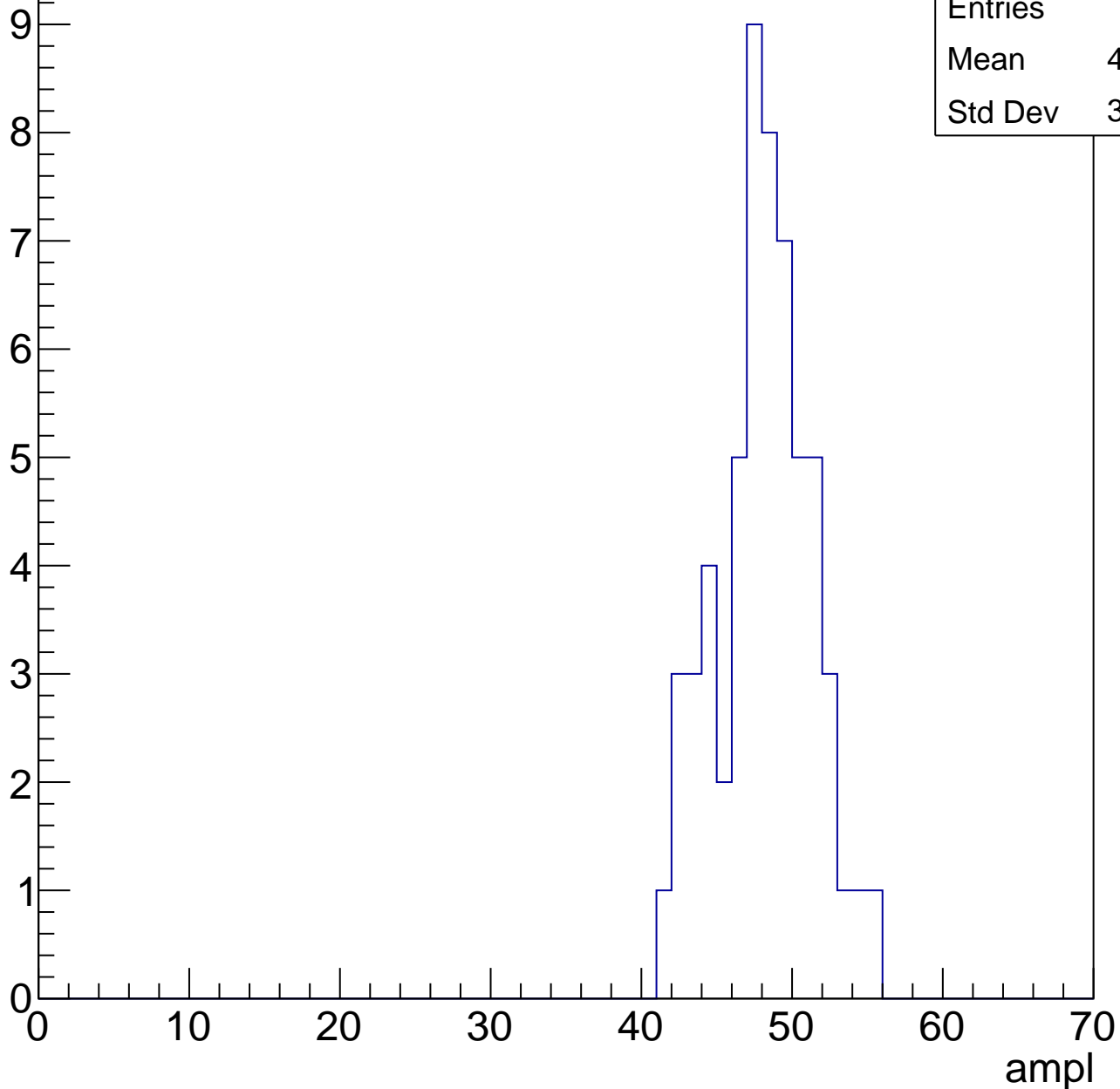
Gaus Width: 3.8320



B1L003S, U19-ch34, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



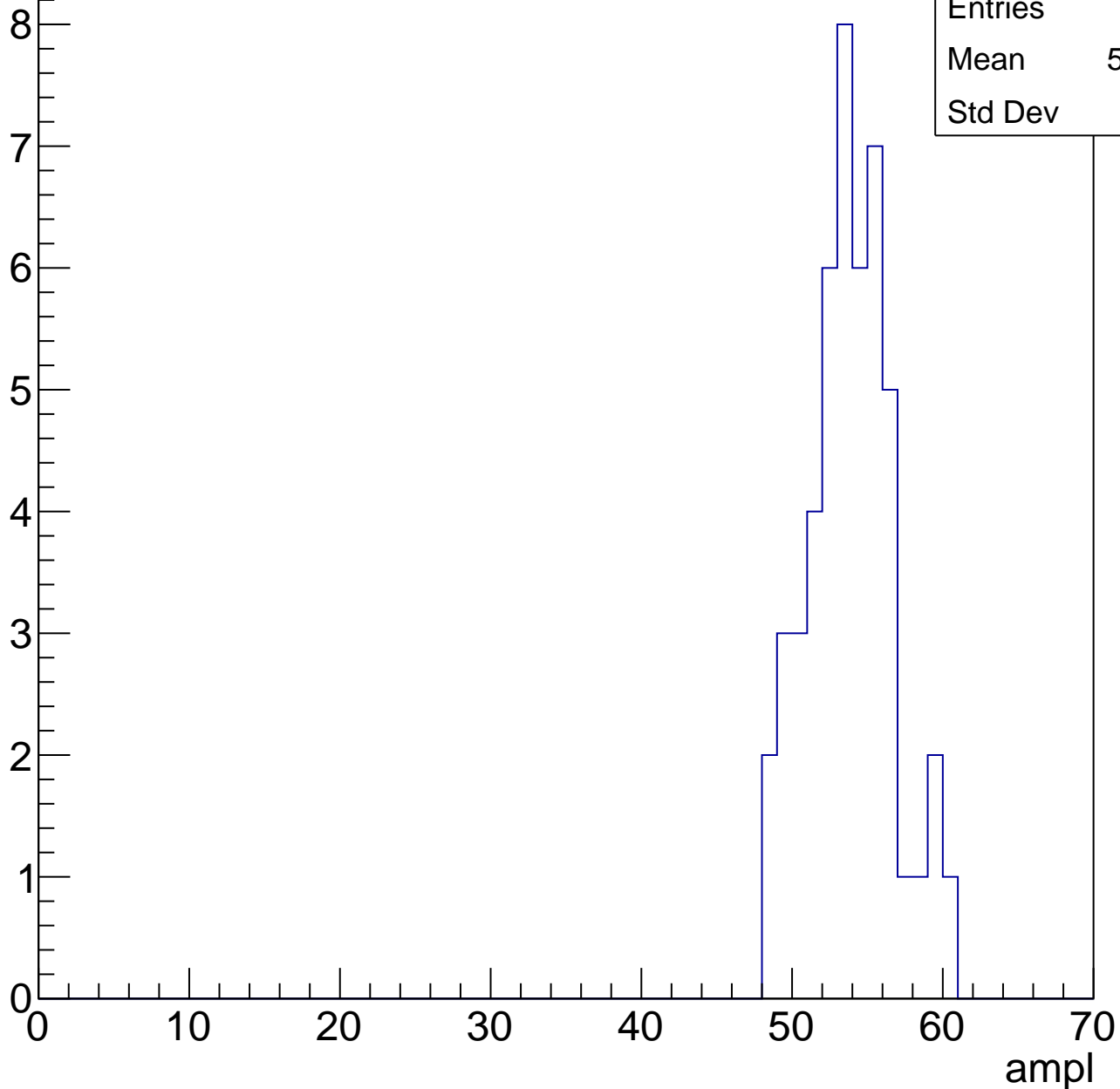
Entries	58
Mean	47.67
Std Dev	3.126

B1L003S, U19-ch34, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

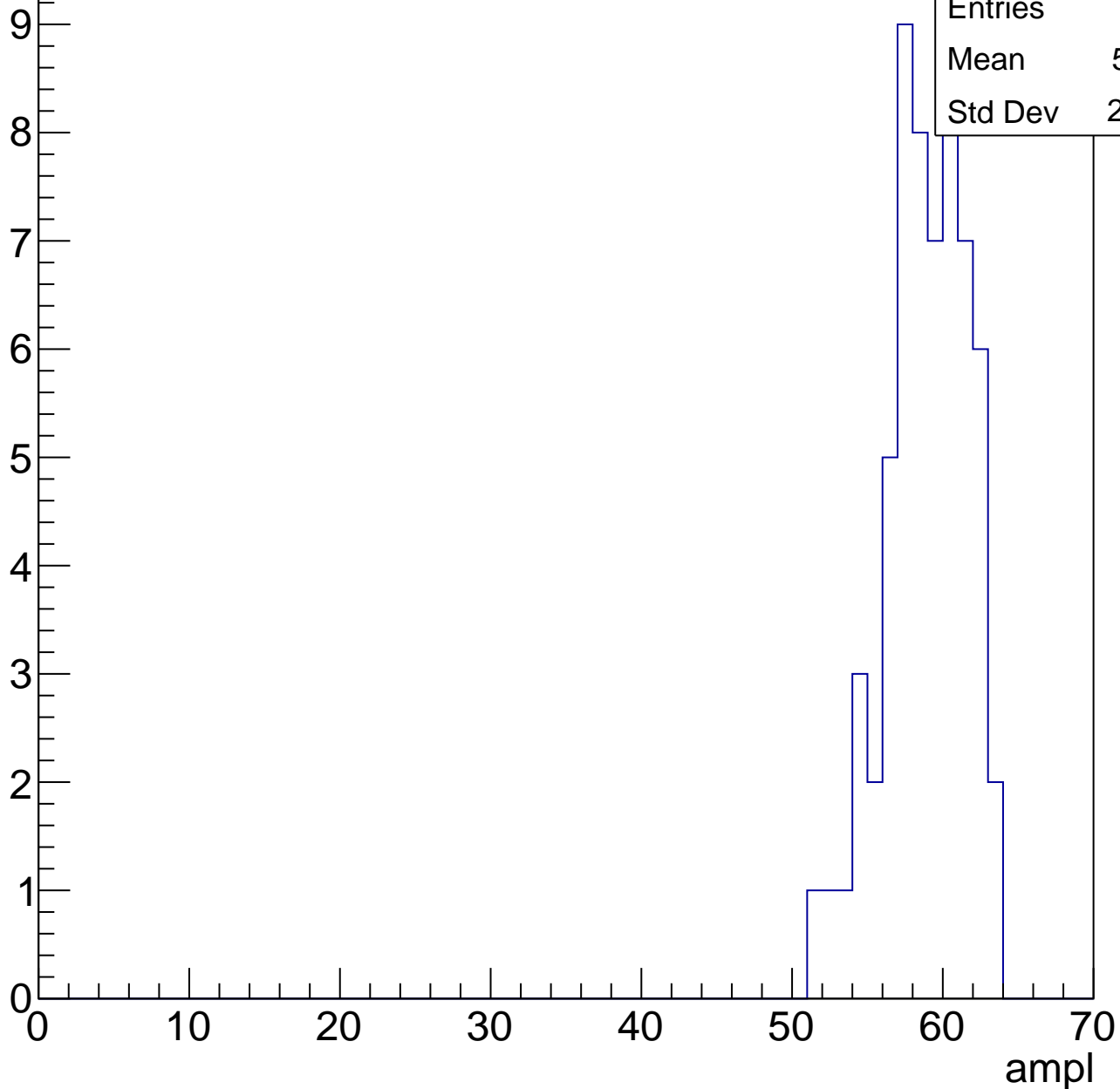
Entries	49
Mean	53.37
Std Dev	2.79



B1L003S, U19-ch34, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	61
Mean	58.41
Std Dev	2.706

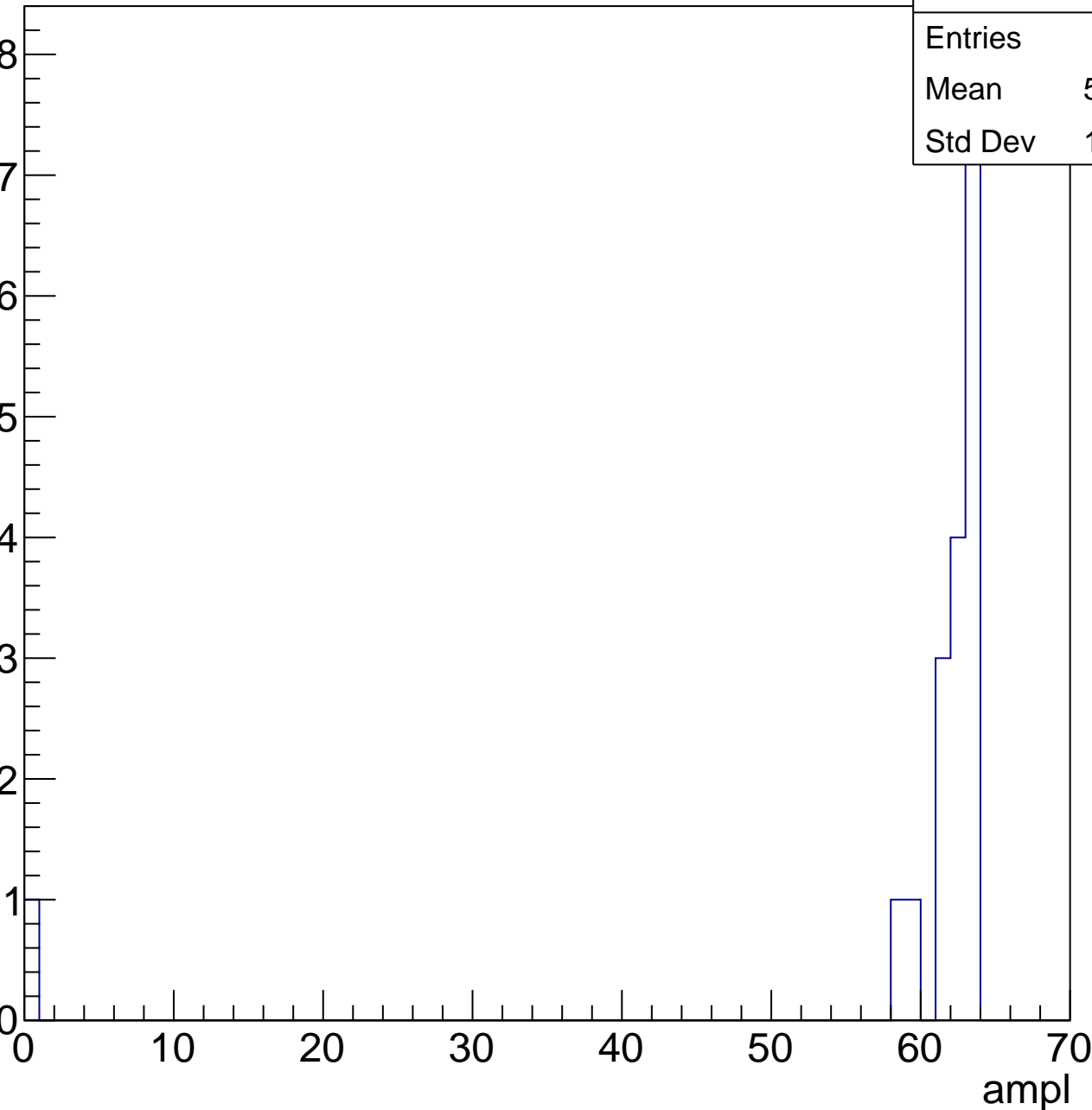
B1L003S, U19-ch34, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	18
Mean	58.44
Std Dev	14.24

8
7
6
5
4
3
2
1
0



B1L003S, U19-ch34, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch35, adc0

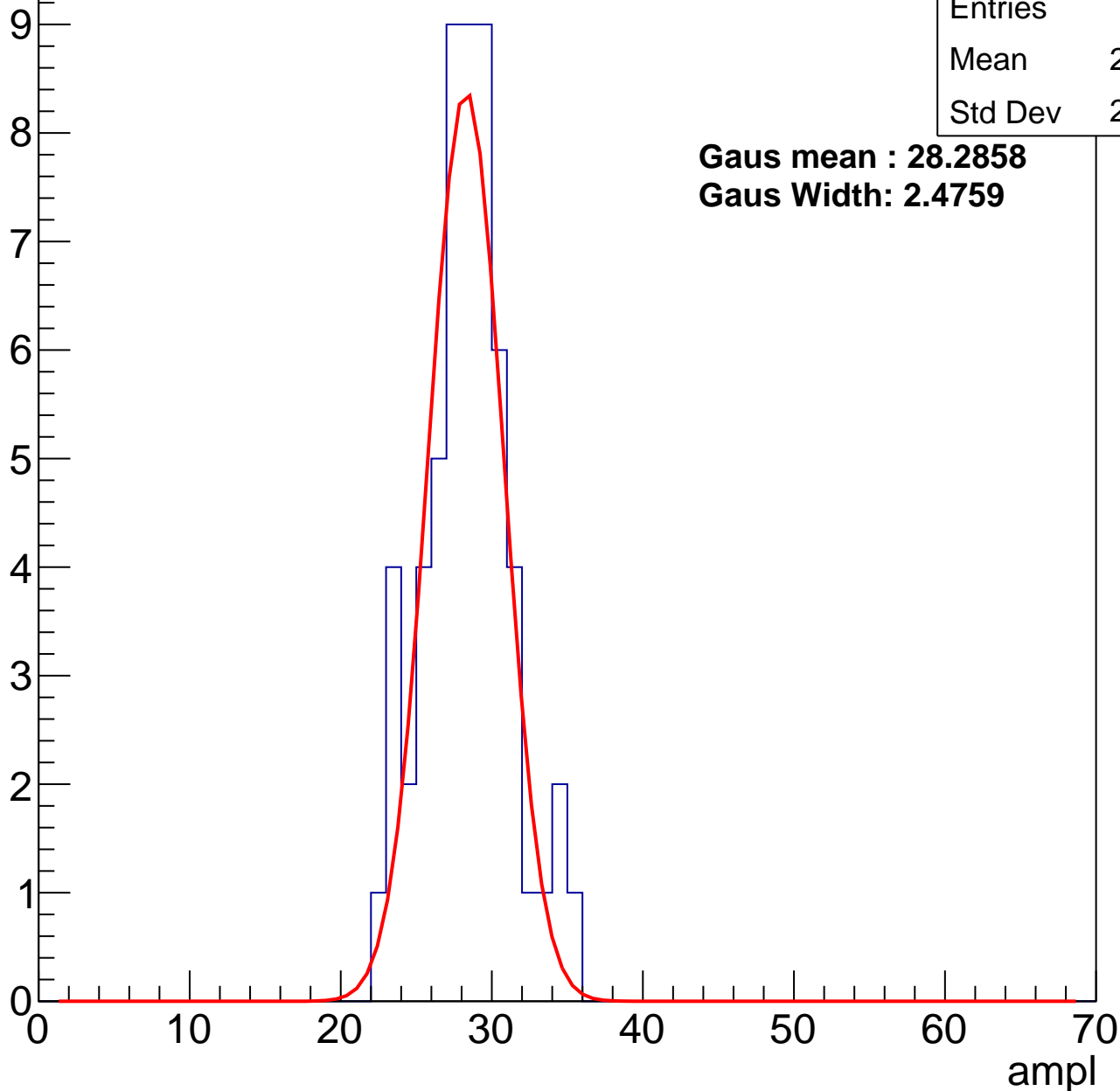
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	27.93
Std Dev	2.828

Gaus mean : 28.2858

Gaus Width: 2.4759



B1L003S, U19-ch35, adc1

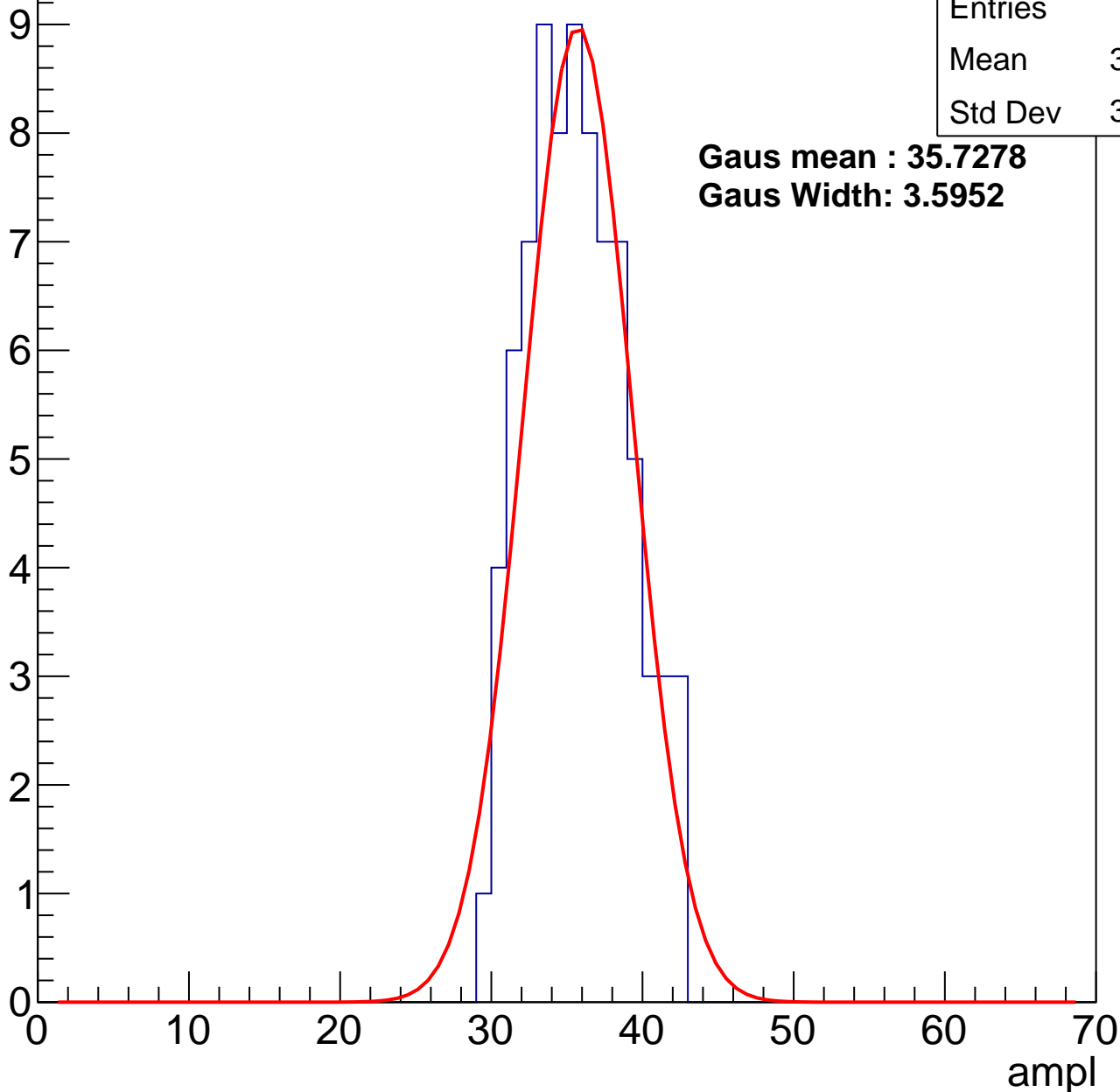
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	35.25
Std Dev	3.246

Gaus mean : 35.7278

Gaus Width: 3.5952



B1L003S, U19-ch35, adc2

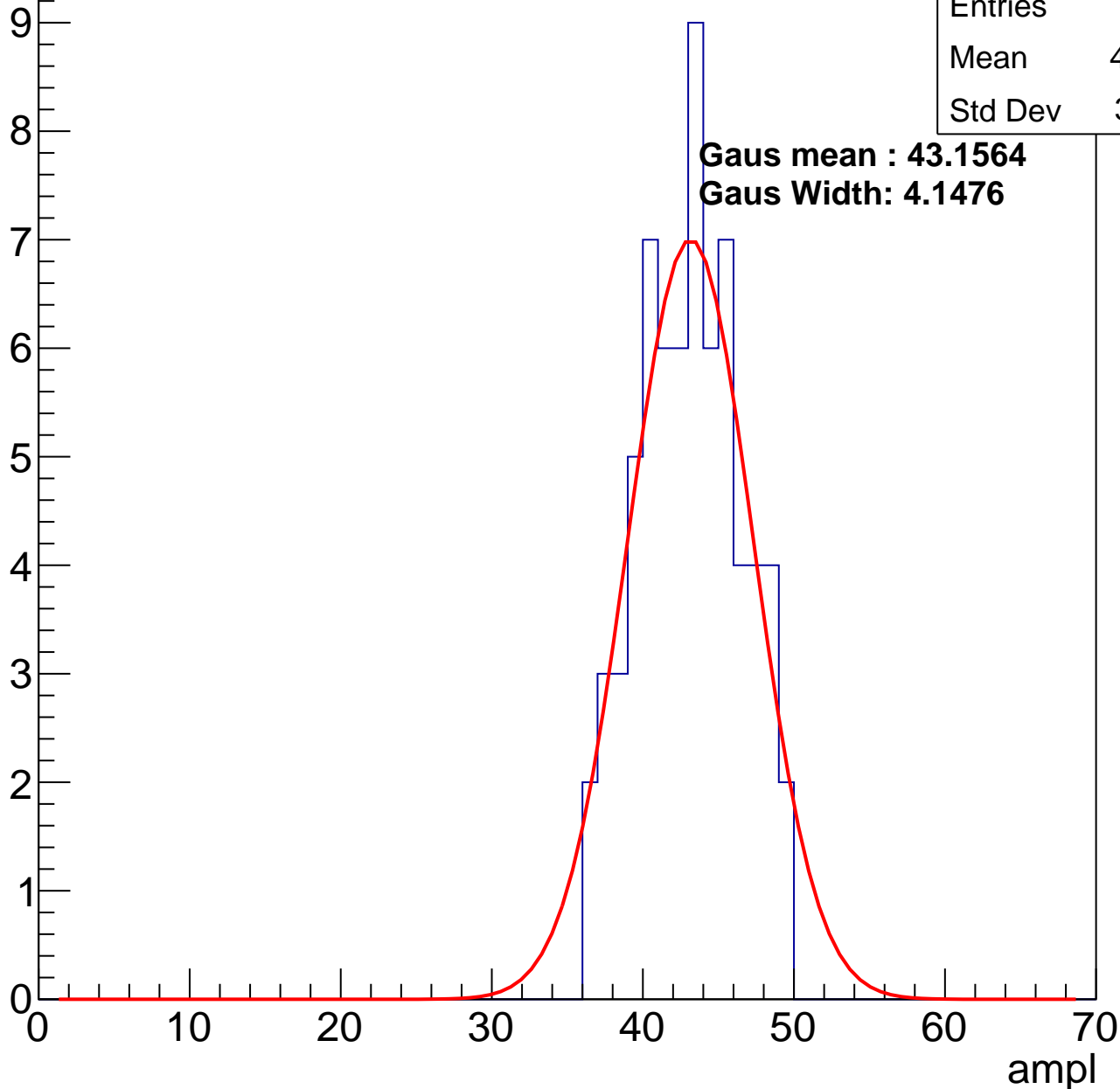
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.62
Std Dev	3.321

Gaus mean : 43.1564

Gaus Width: 4.1476

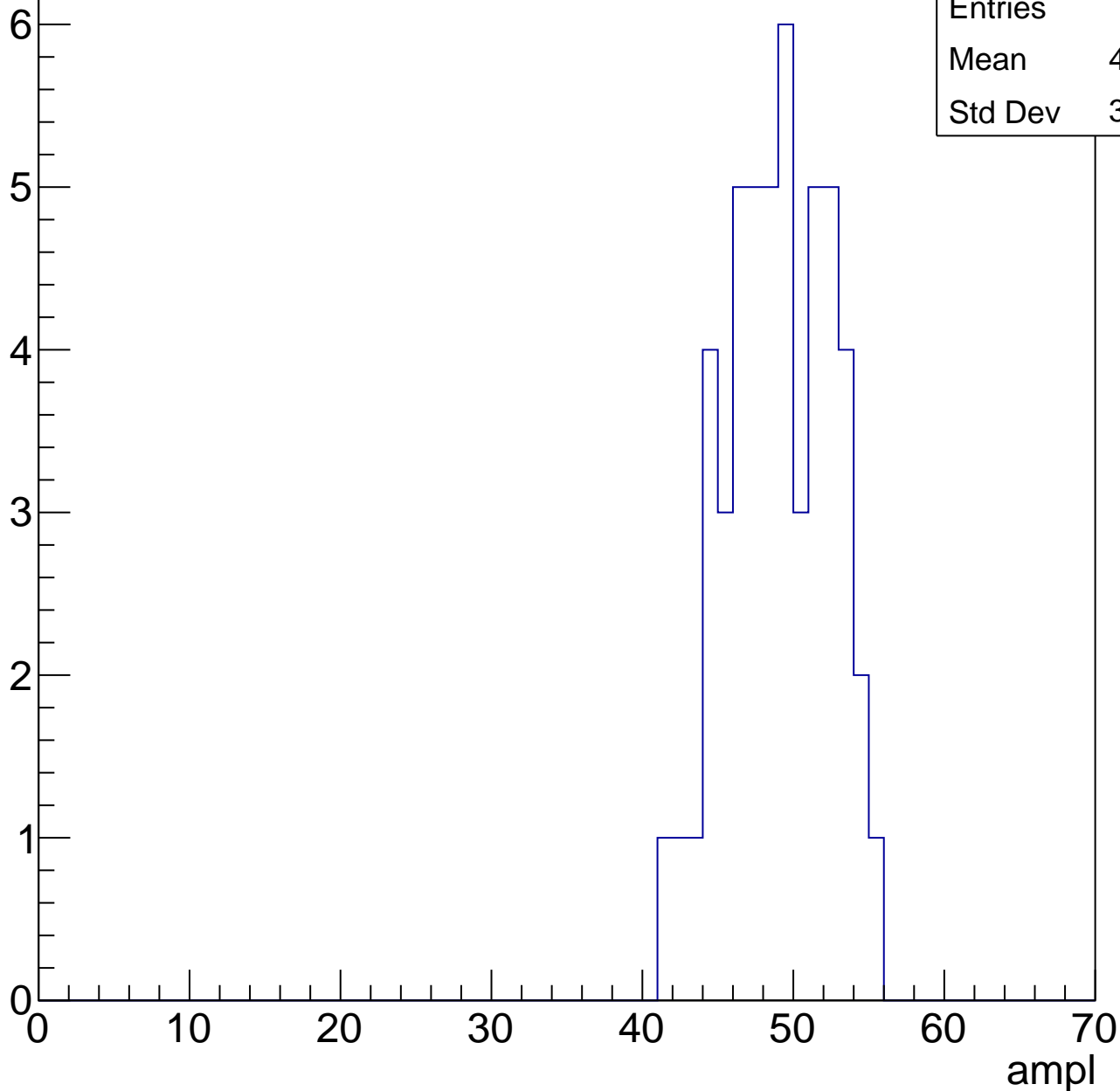


B1L003S, U19-ch35, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	48.55
Std Dev	3.357

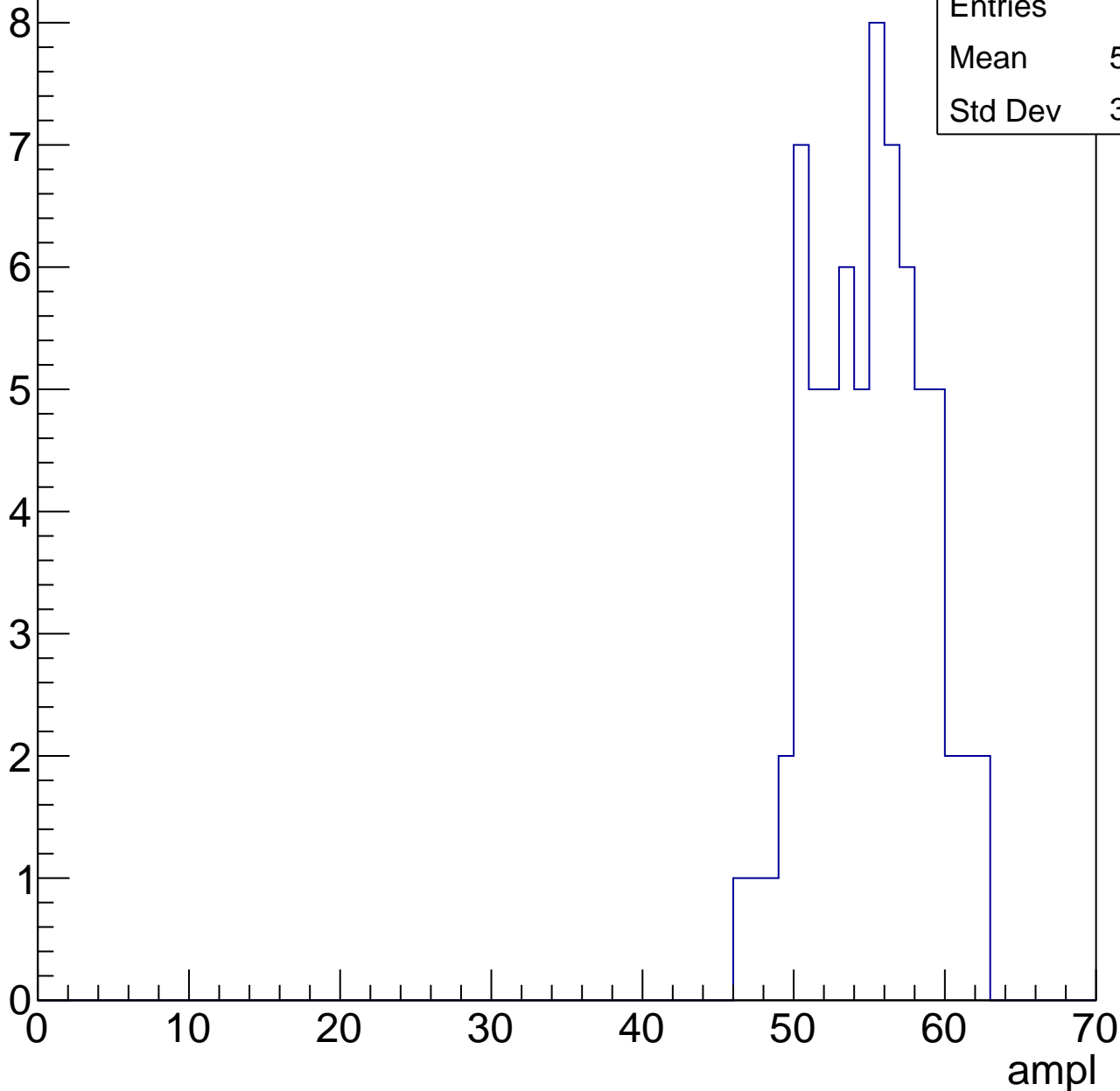


B1L003S, U19-ch35, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	54.53
Std Dev	3.694



B1L003S, U19-ch35, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	46
Mean	59.46
Std Dev	2.627

ampl

10

20

30

40

50

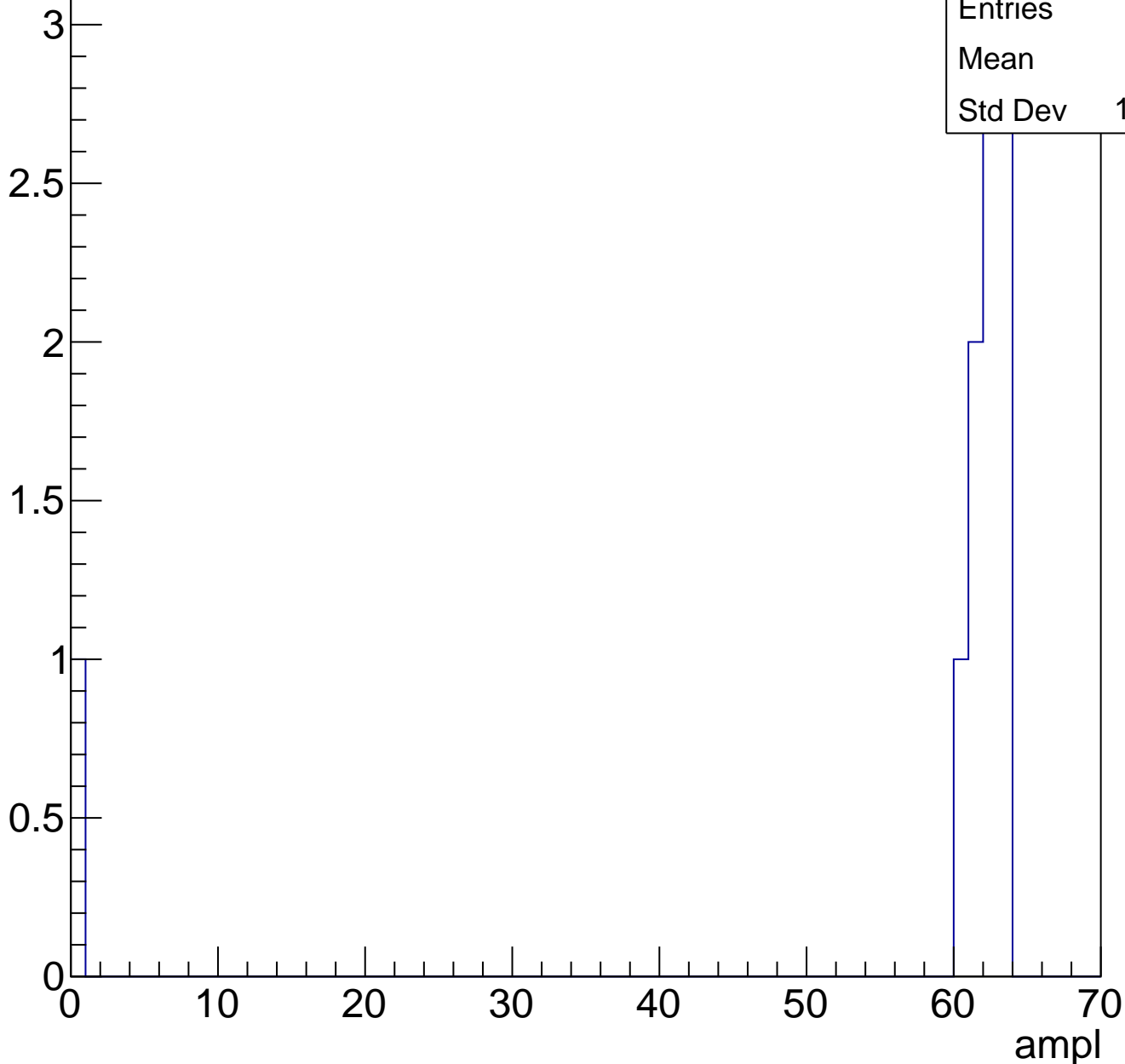
60

70

B1L003S, U19-ch35, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch35, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch36, adc0

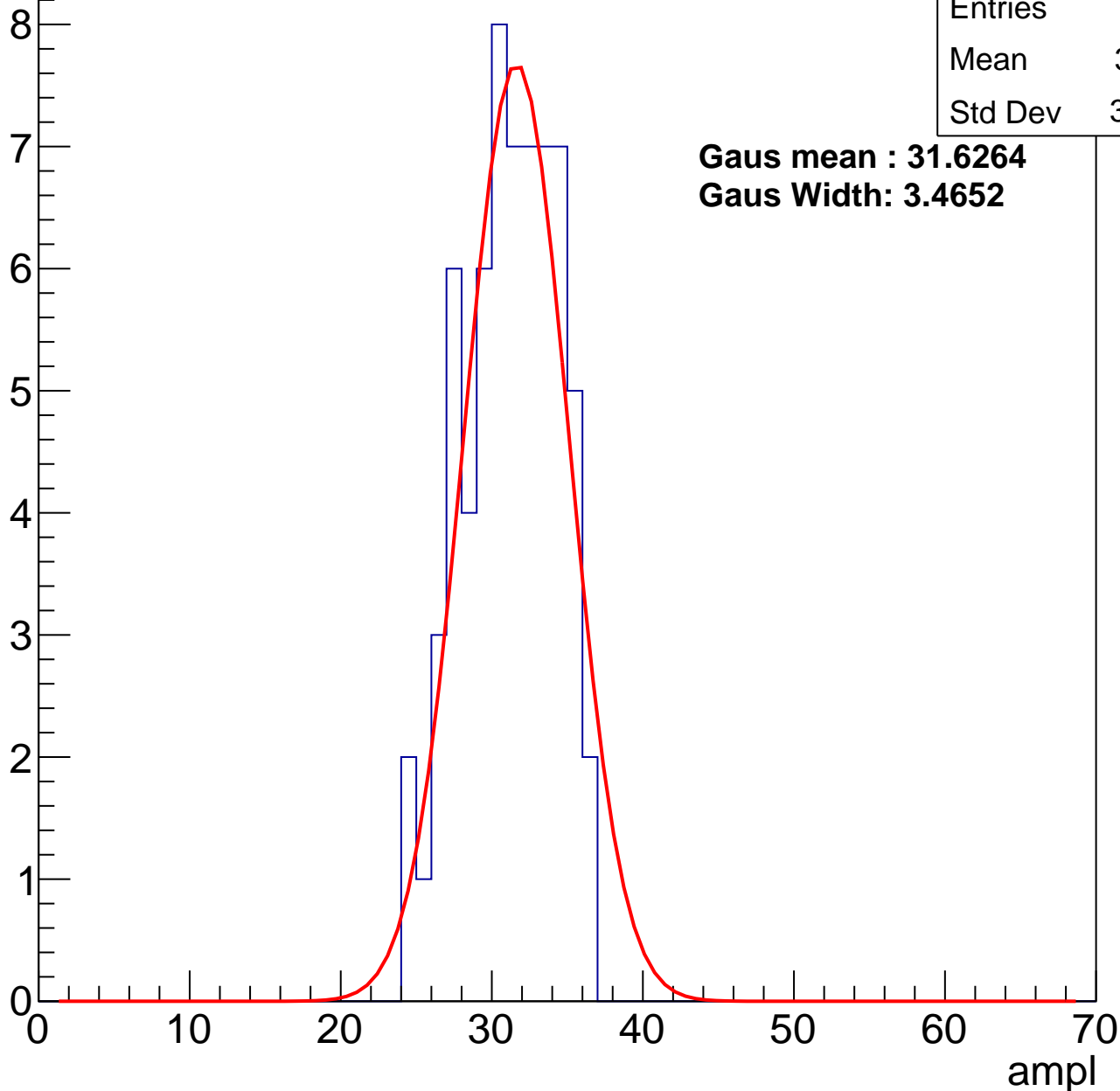
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.71
Std Dev	3.027

Gaus mean : 31.6264

Gaus Width: 3.4652



B1L003S, U19-ch36, adc1

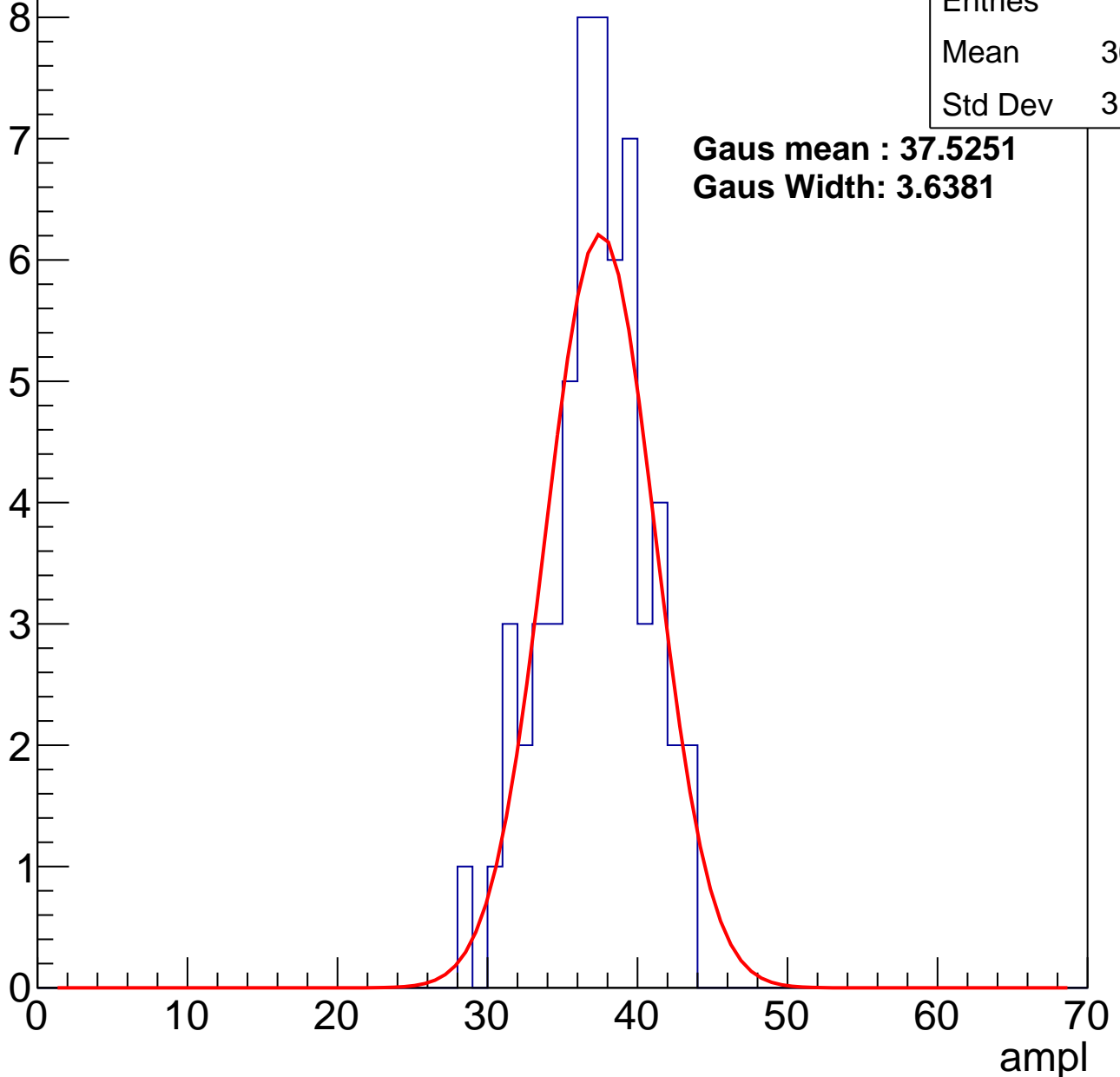
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	36.72
Std Dev	3.305

Gaus mean : 37.5251

Gaus Width: 3.6381



B1L003S, U19-ch36, adc2

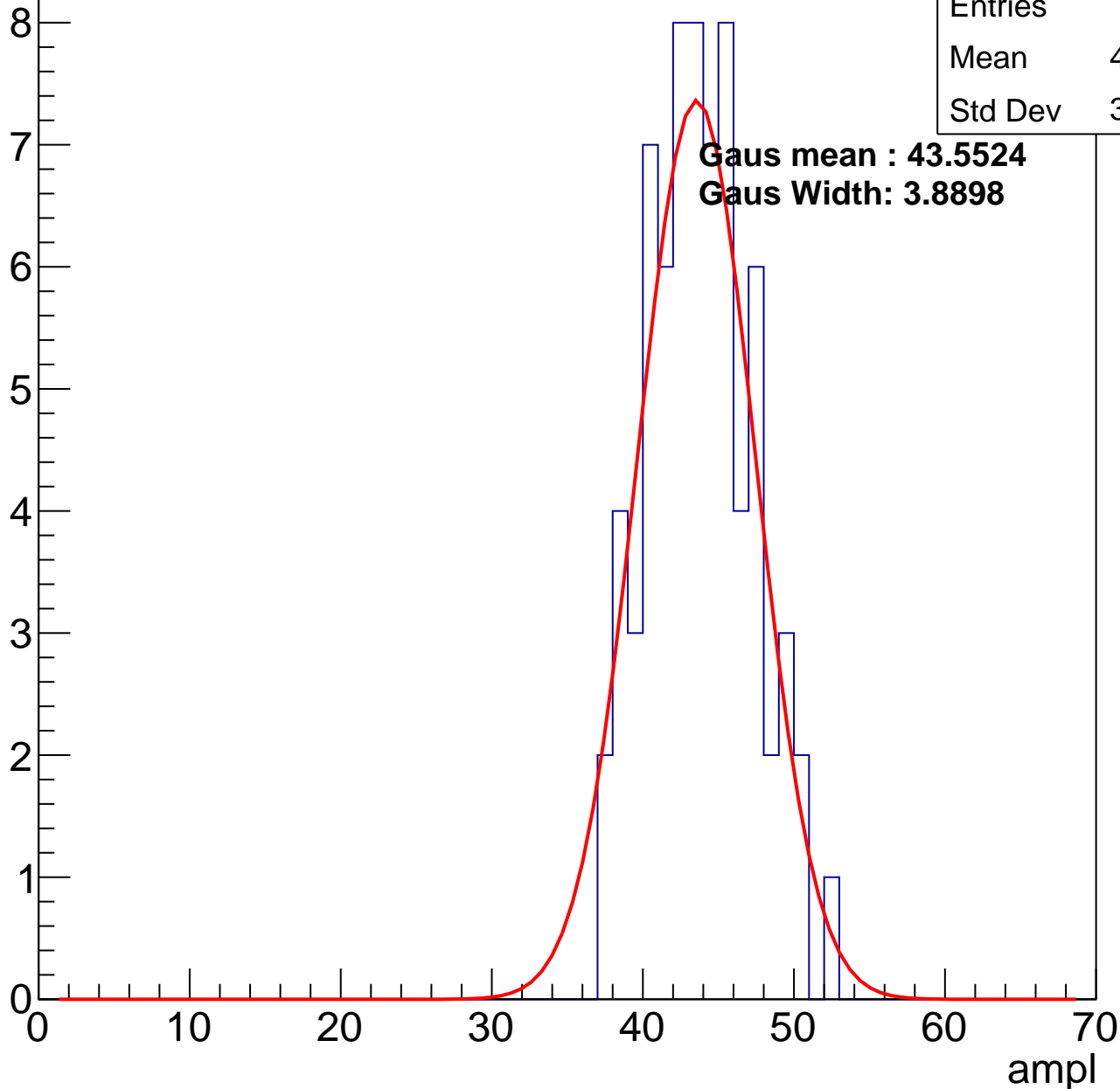
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	43.35
Std Dev	3.386

Gaus mean : 43.5524

Gaus Width: 3.8898

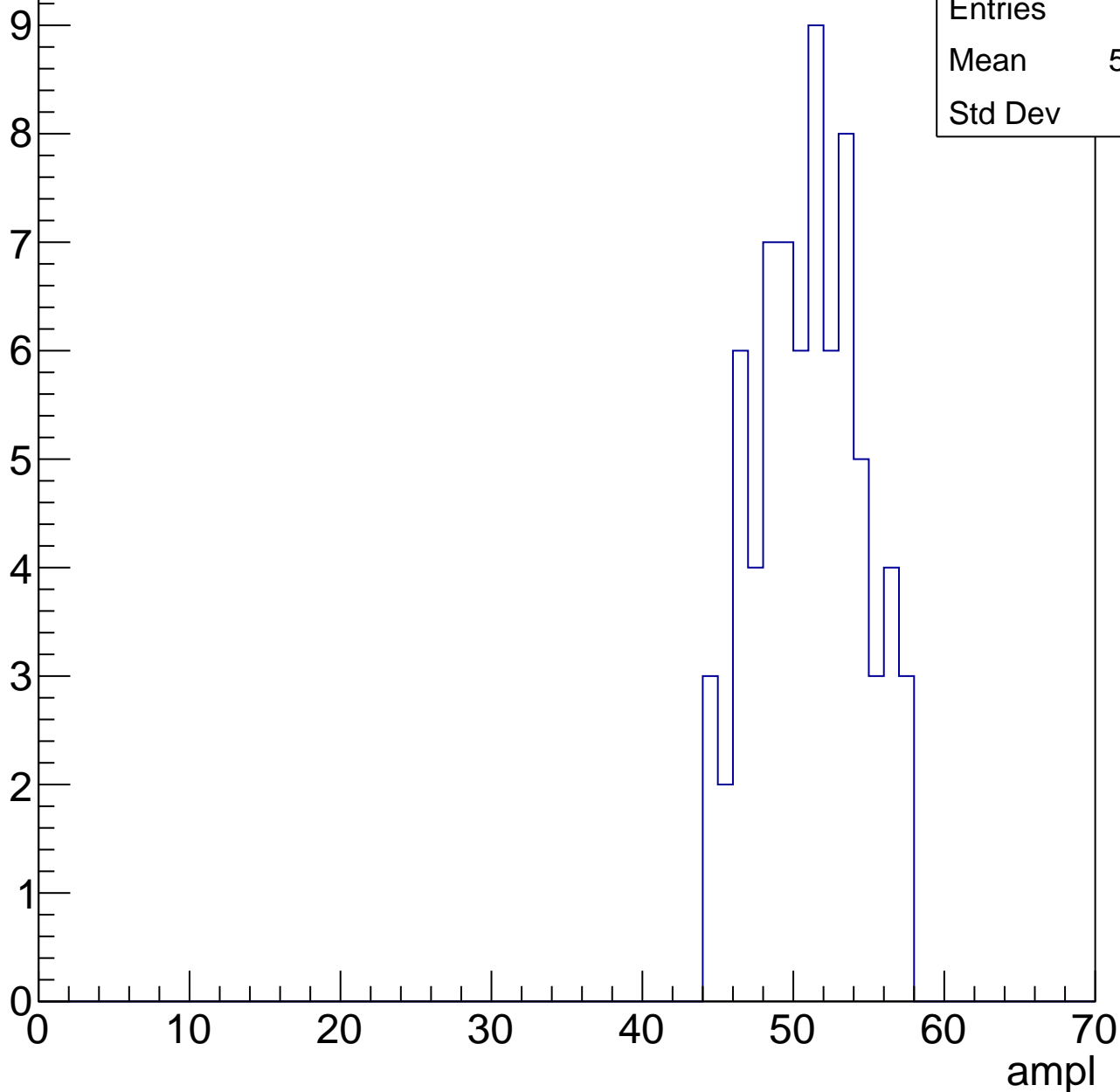


B1L003S, U19-ch36, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	50.55
Std Dev	3.42

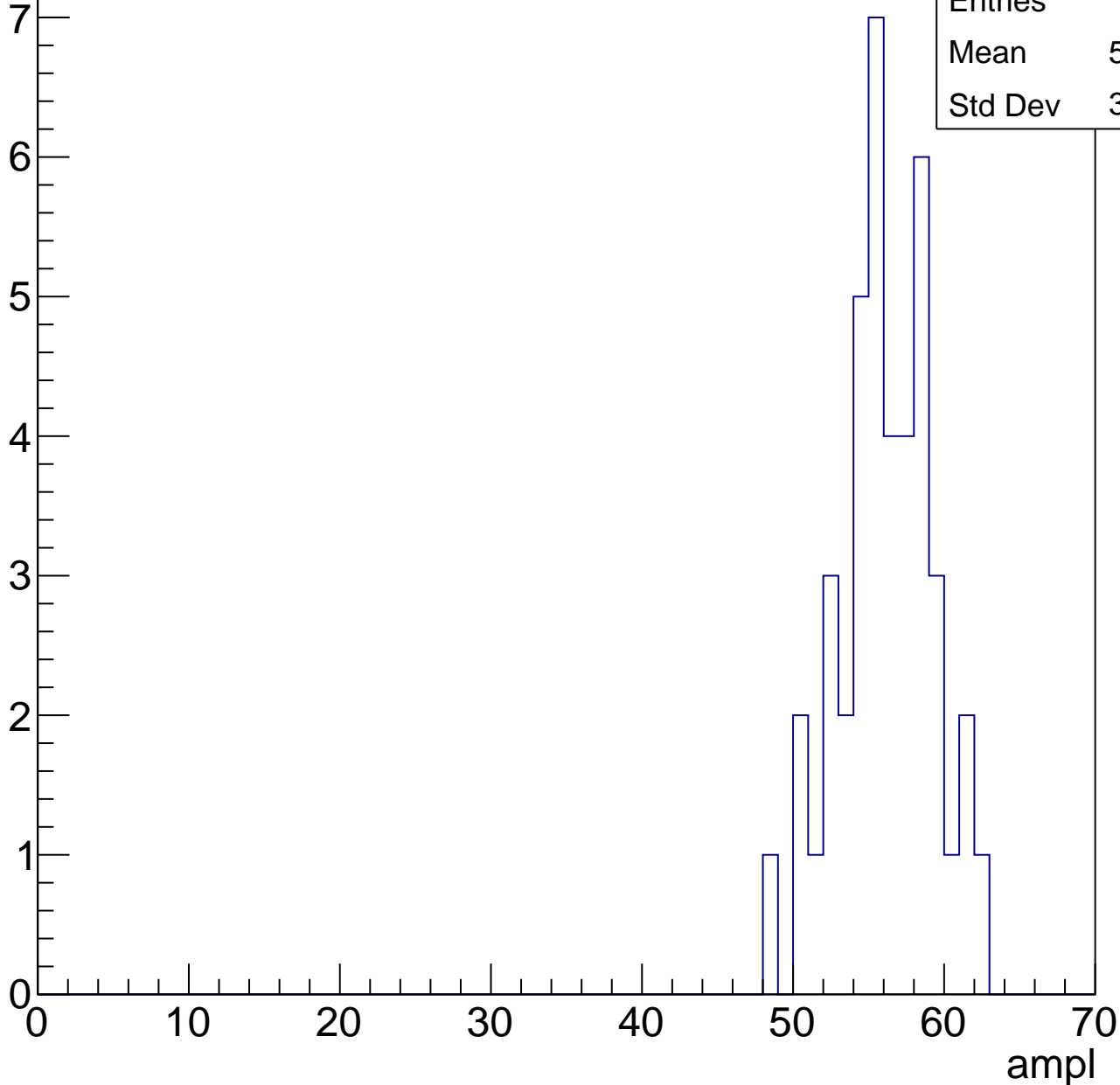


B1L003S, U19-ch36, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	55.64
Std Dev	3.108

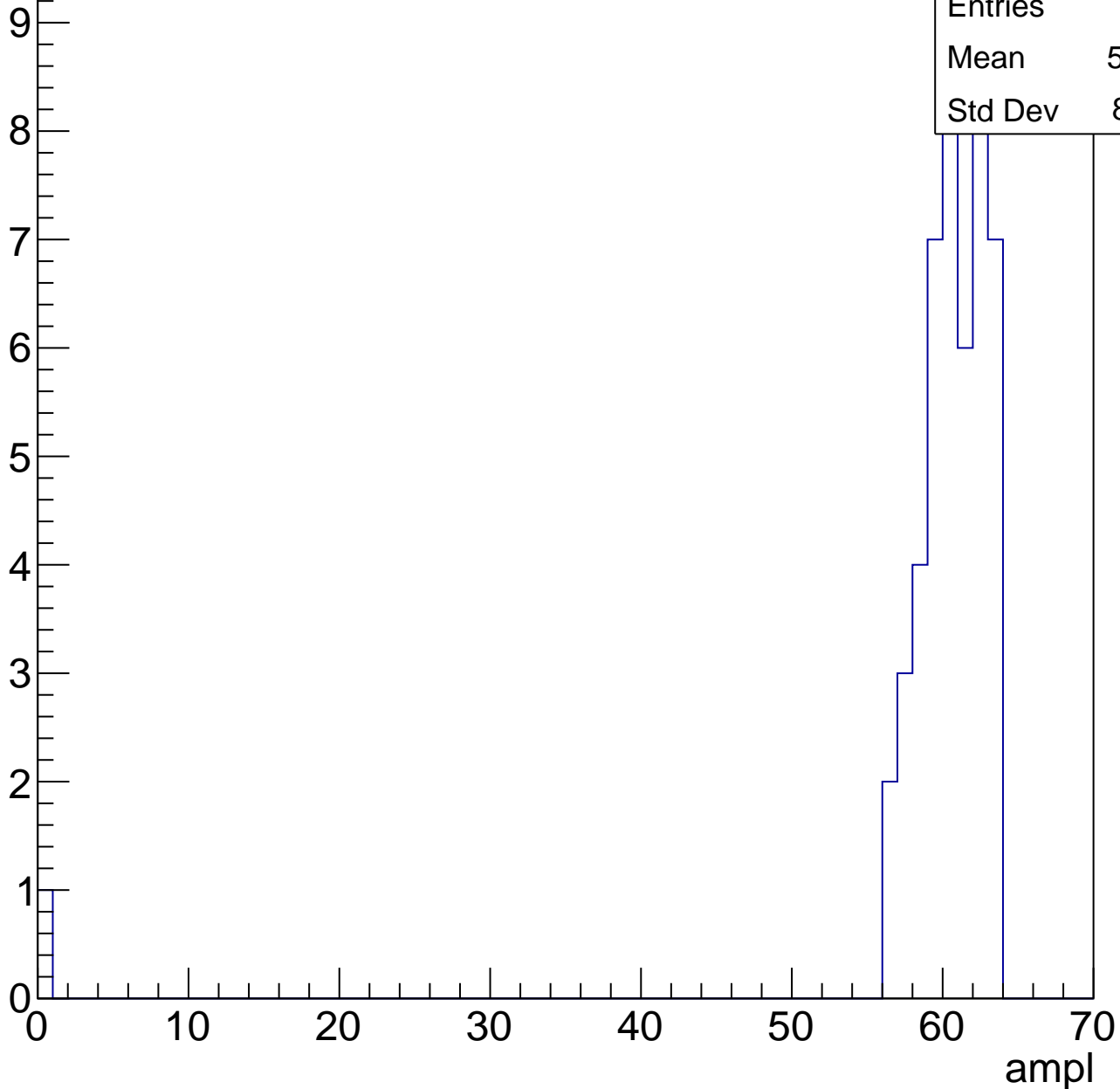


B1L003S, U19-ch36, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	58.96
Std Dev	8.911



B1L003S, U19-ch36, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch36, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch37, adc0

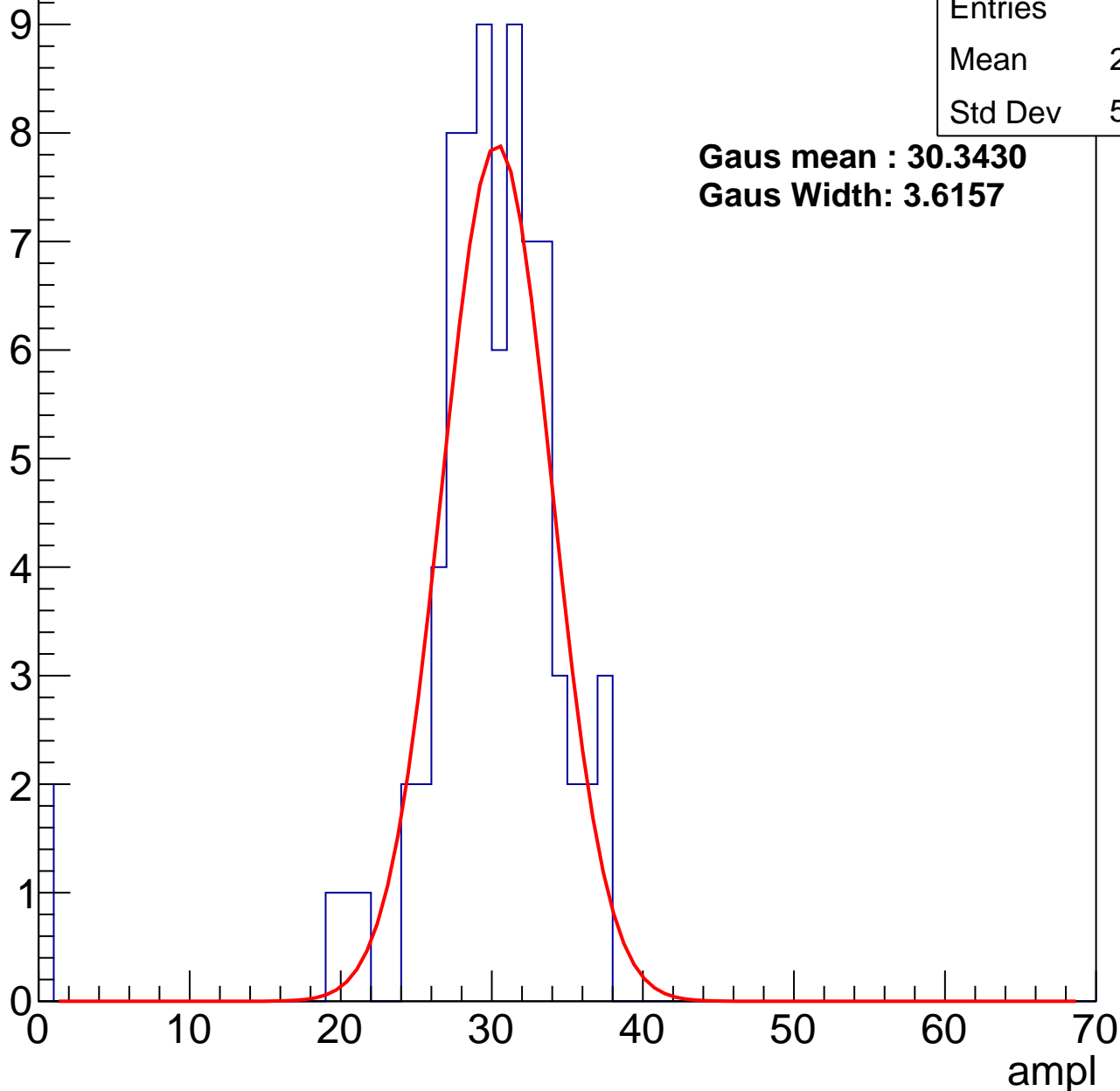
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	28.99
Std Dev	5.964

Gaus mean : 30.3430

Gaus Width: 3.6157



B1L003S, U19-ch37, adc1

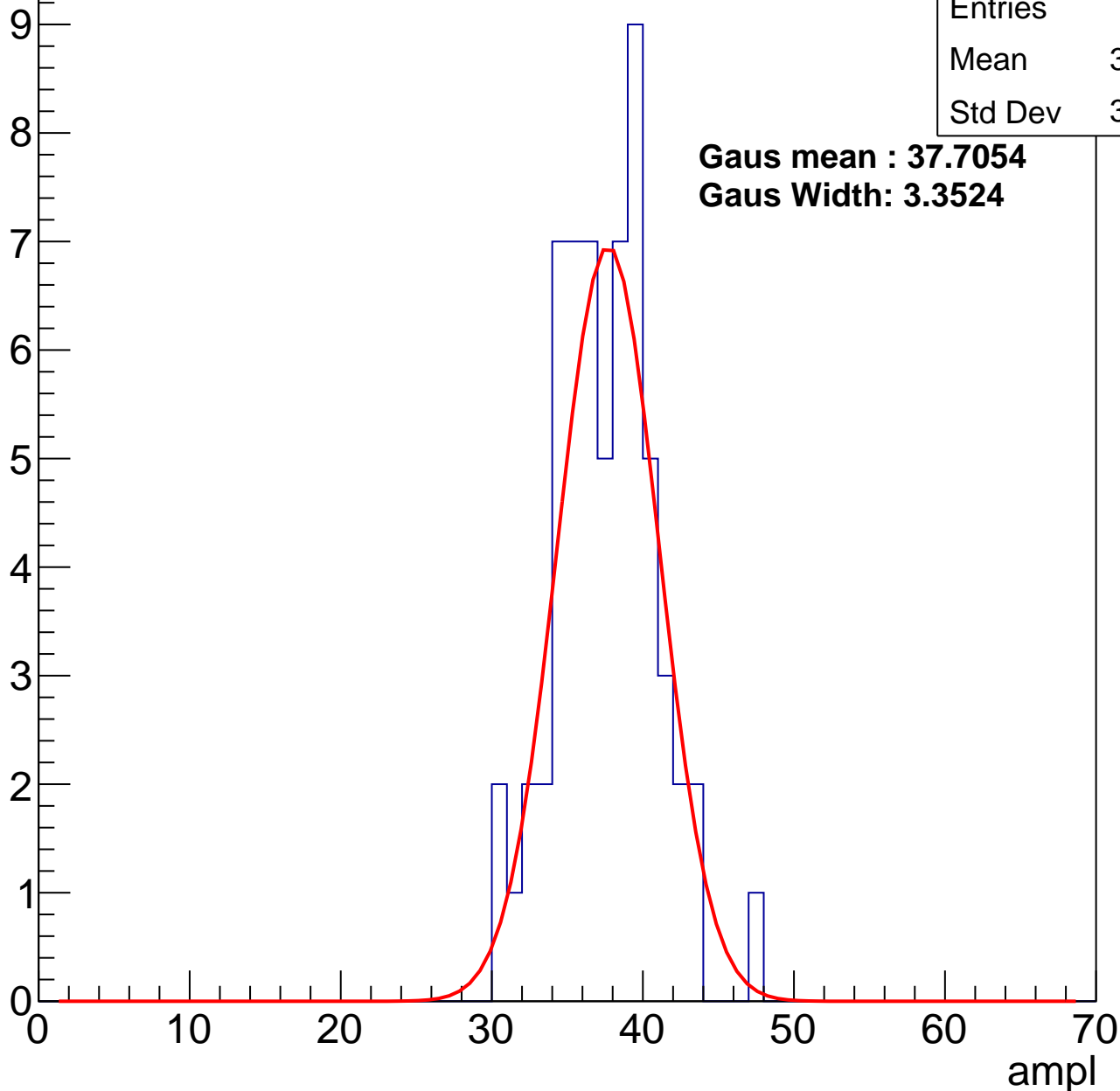
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.06
Std Dev	3.292

Gaus mean : 37.7054

Gaus Width: 3.3524



B1L003S, U19-ch37, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.92
Std Dev	3.555

Gaus mean : 43.9185

Gaus Width: 3.0290

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

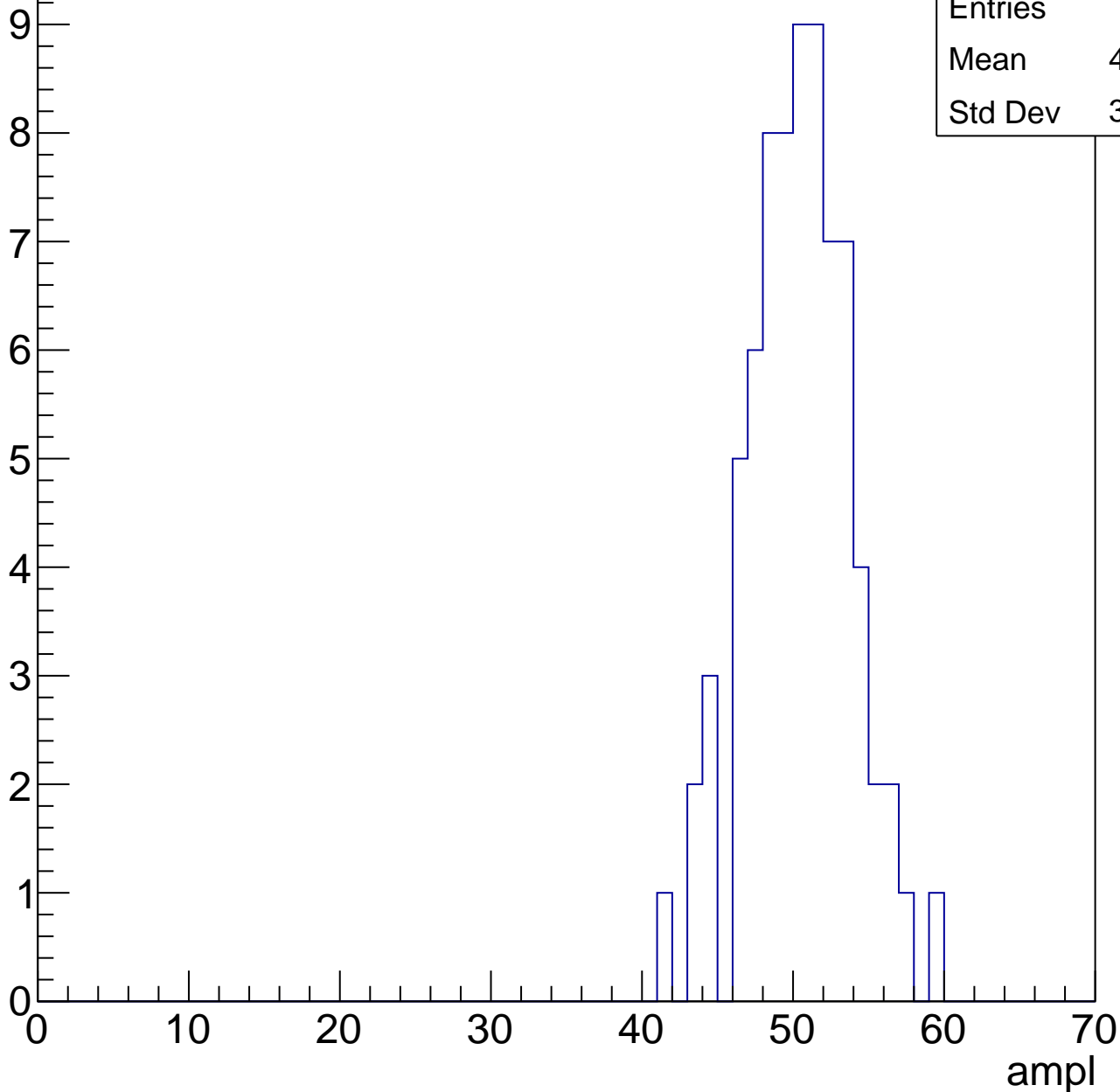
10

B1L003S, U19-ch37, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	49.93
Std Dev	3.419

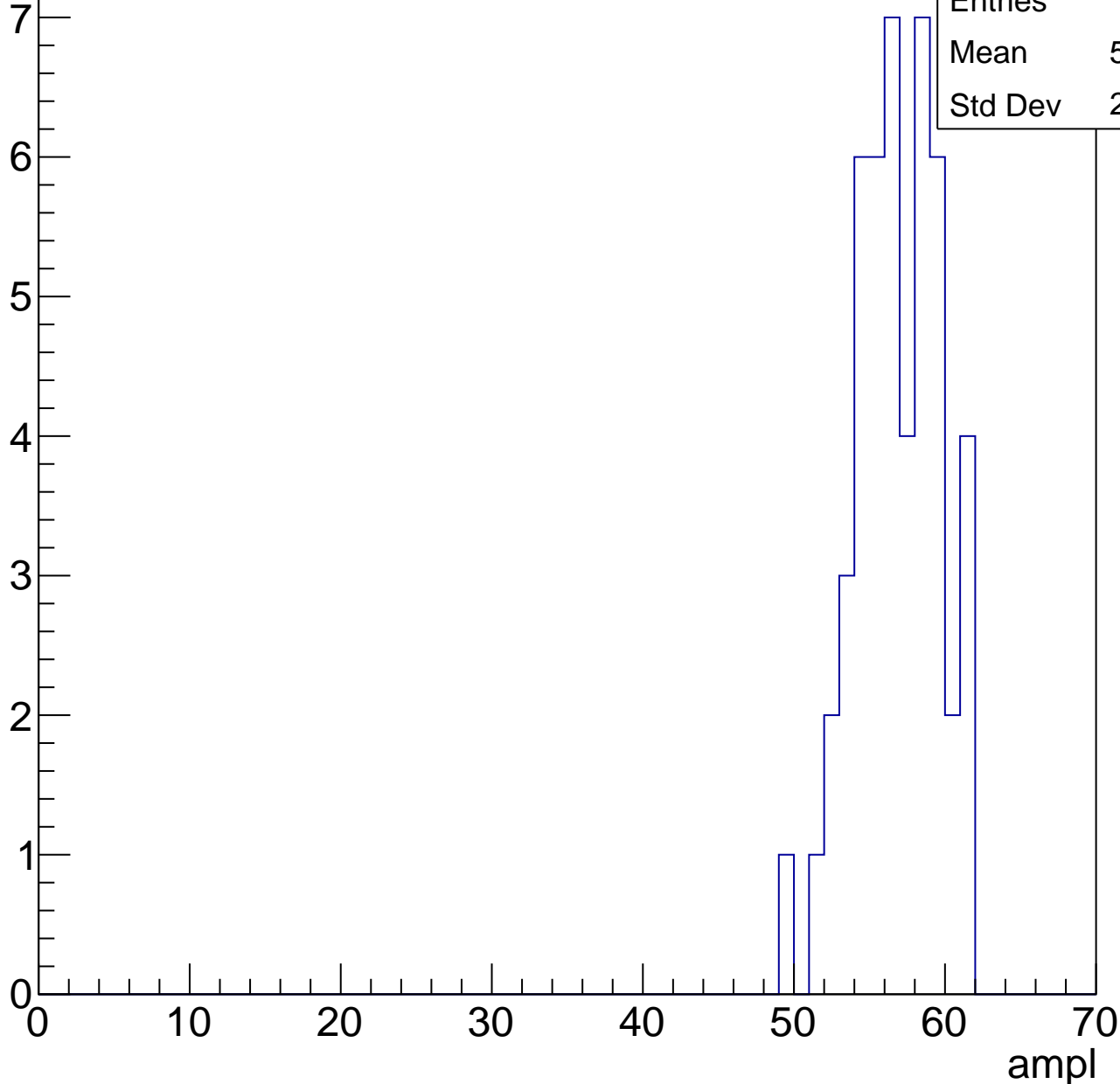


B1L003S, U19-ch37, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

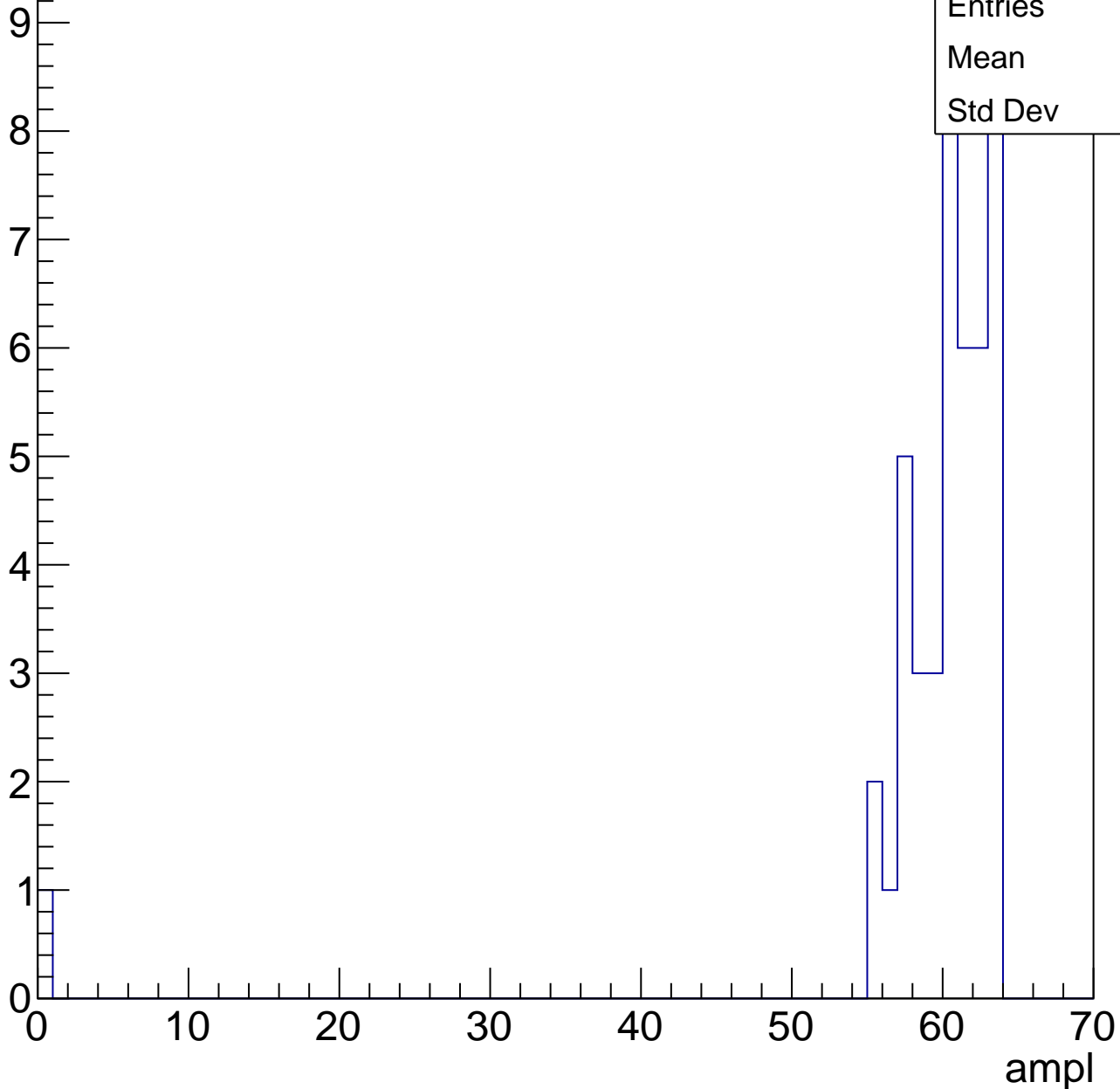
Entries	49
Mean	56.35
Std Dev	2.767



B1L003S, U19-ch37, adc5

calib_packv5_042523_0143.root, FC#13, port D2

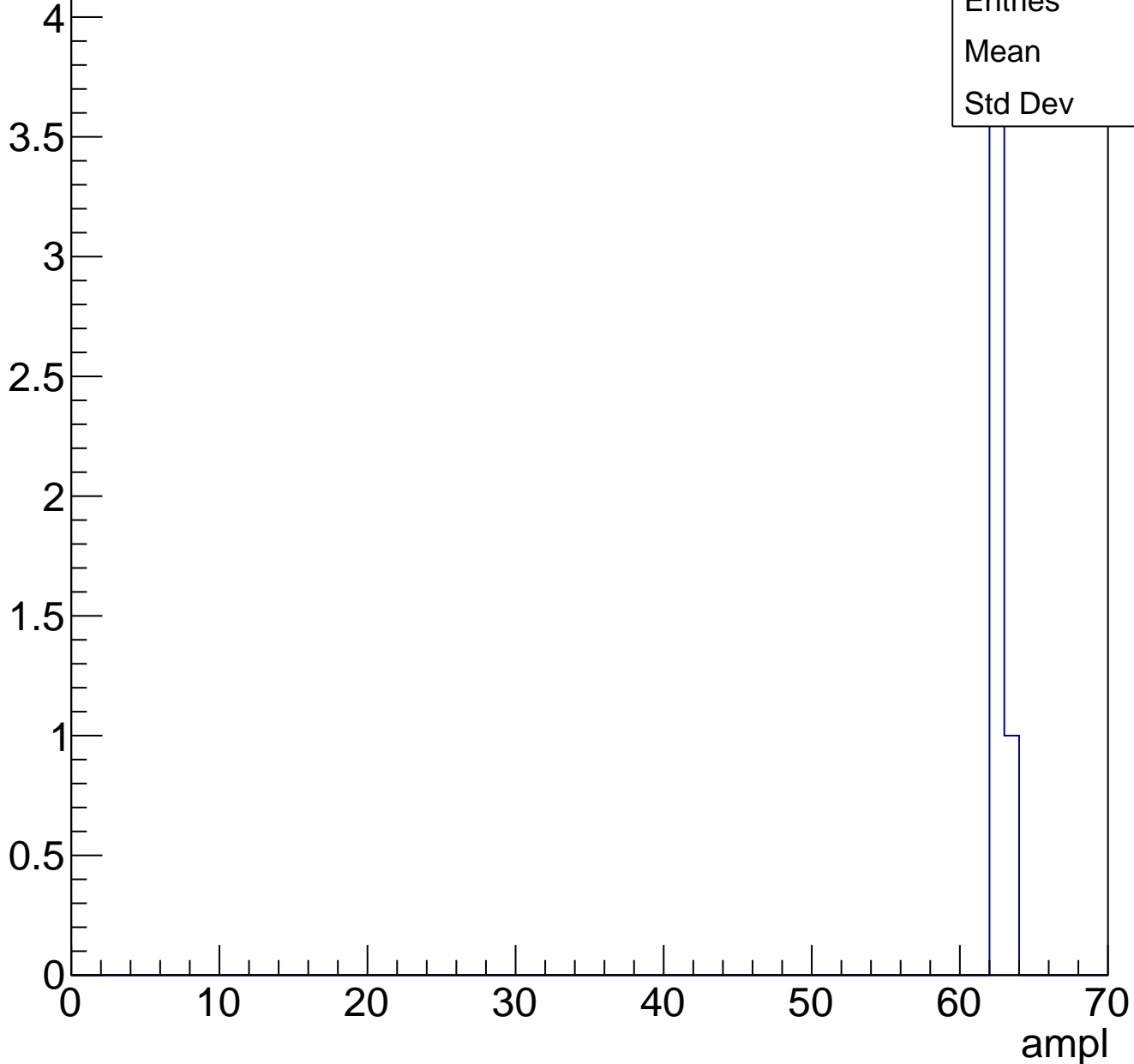
Entry



B1L003S, U19-ch37, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	62.2
Std Dev	0.4

B1L003S, U19-ch37, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch38, adc0

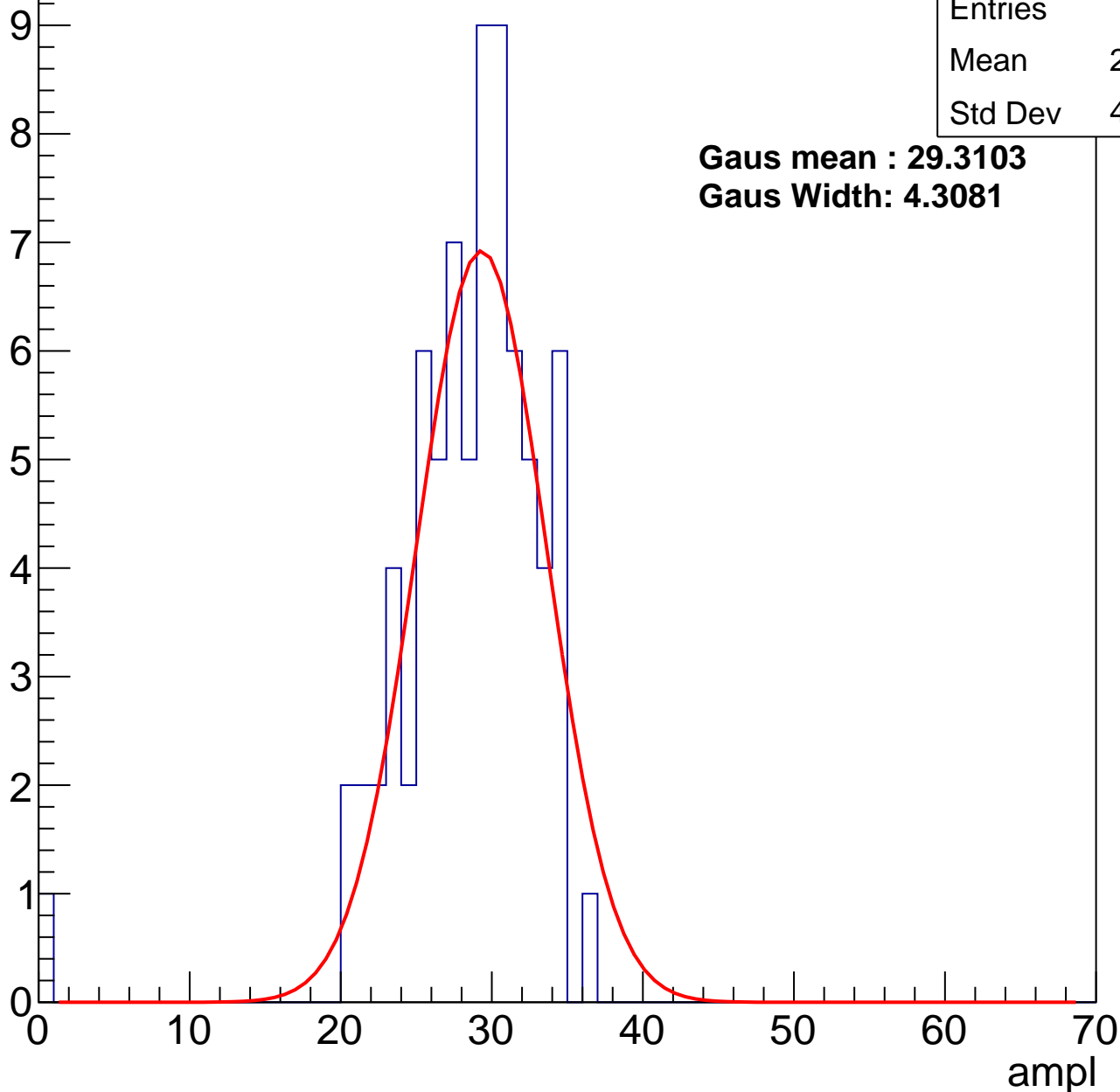
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	27.95
Std Dev	4.939

Gaus mean : 29.3103

Gaus Width: 4.3081



B1L003S, U19-ch38, adc1

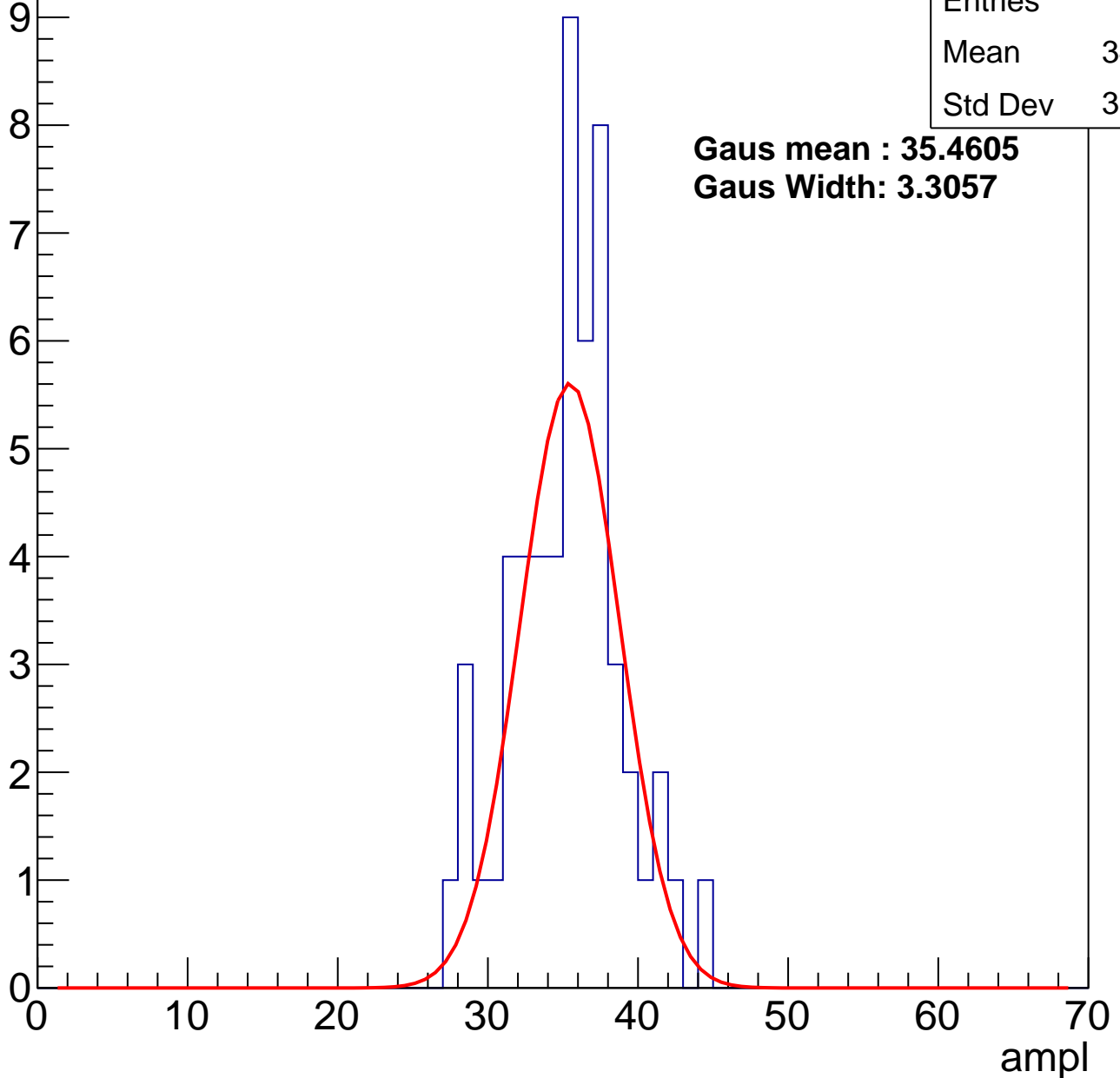
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	34.85
Std Dev	3.605

Gaus mean : 35.4605

Gaus Width: 3.3057



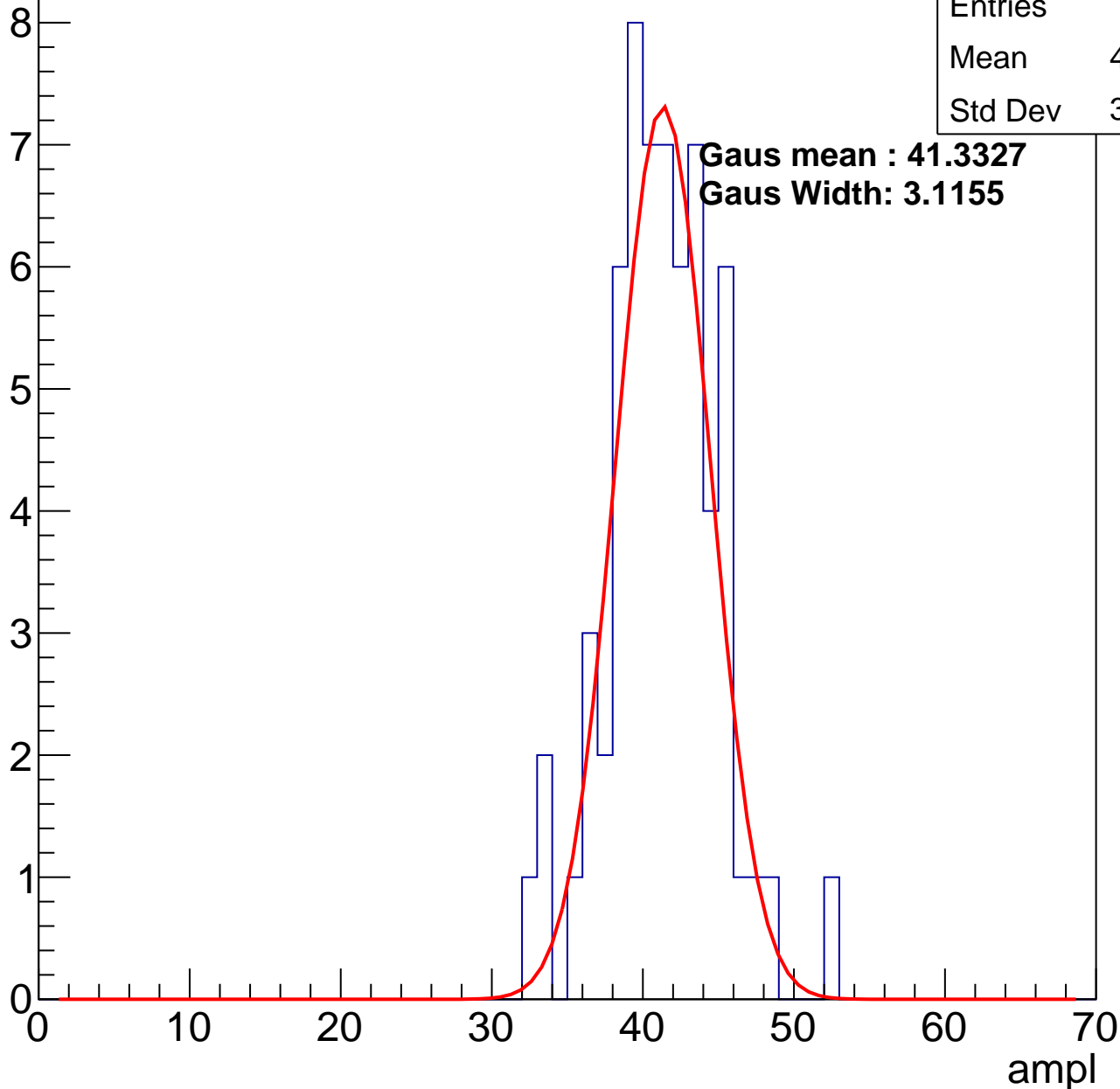
B1L003S, U19-ch38, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	40.84
Std Dev	3.624

Gaus mean : 41.3327
Gaus Width: 3.1155

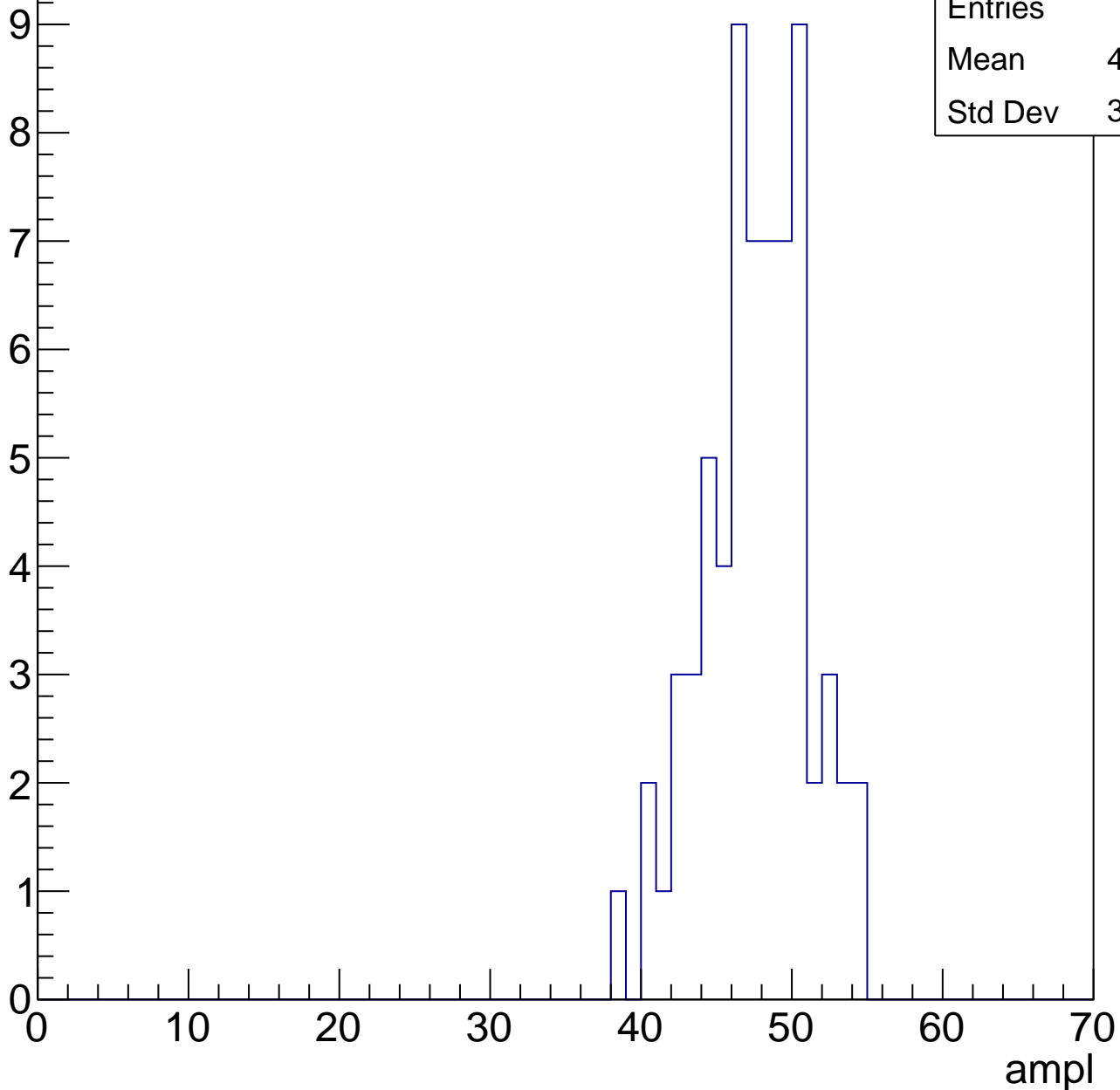


B1L003S, U19-ch38, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	47.13
Std Dev	3.455

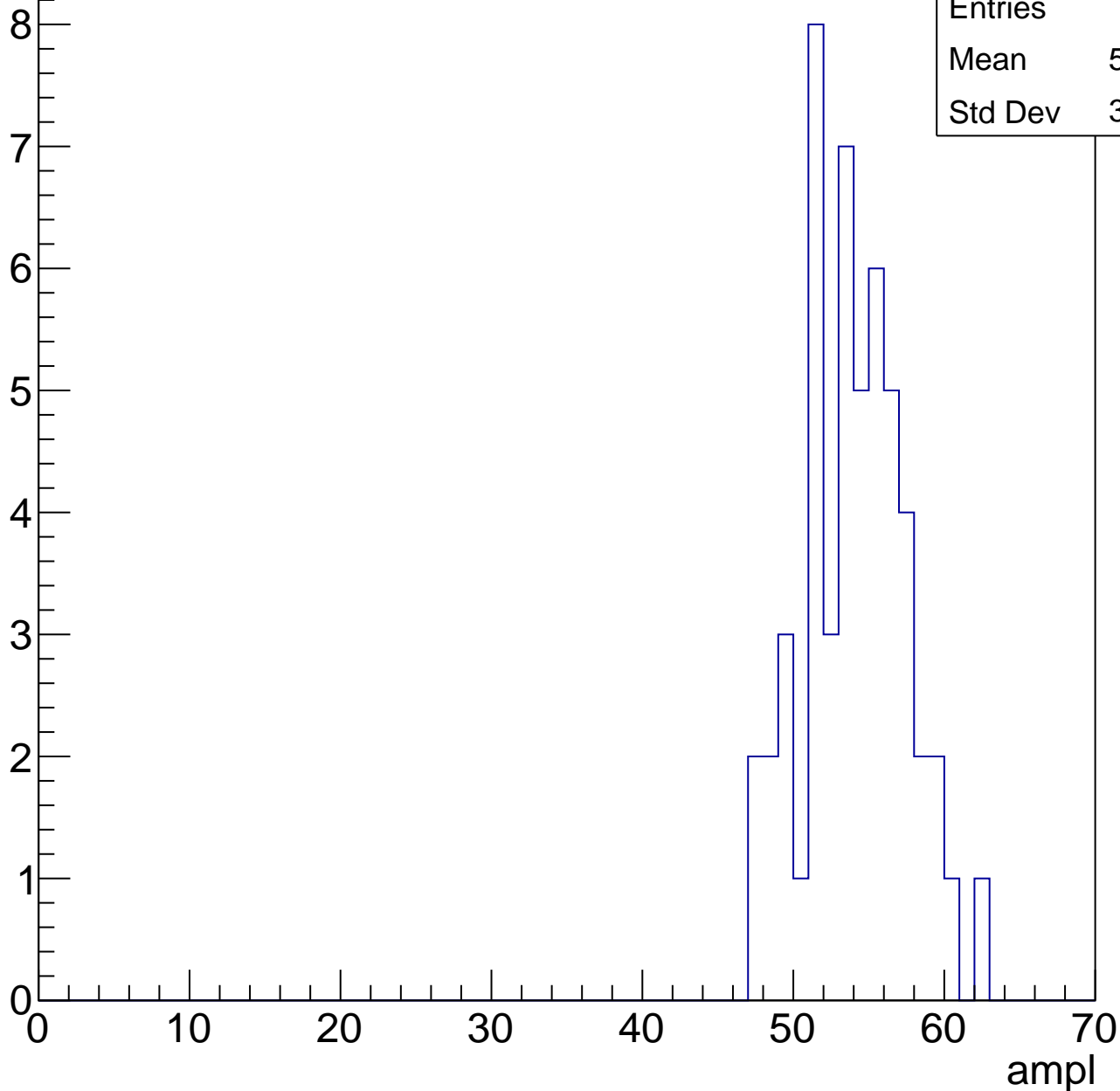


B1L003S, U19-ch38, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

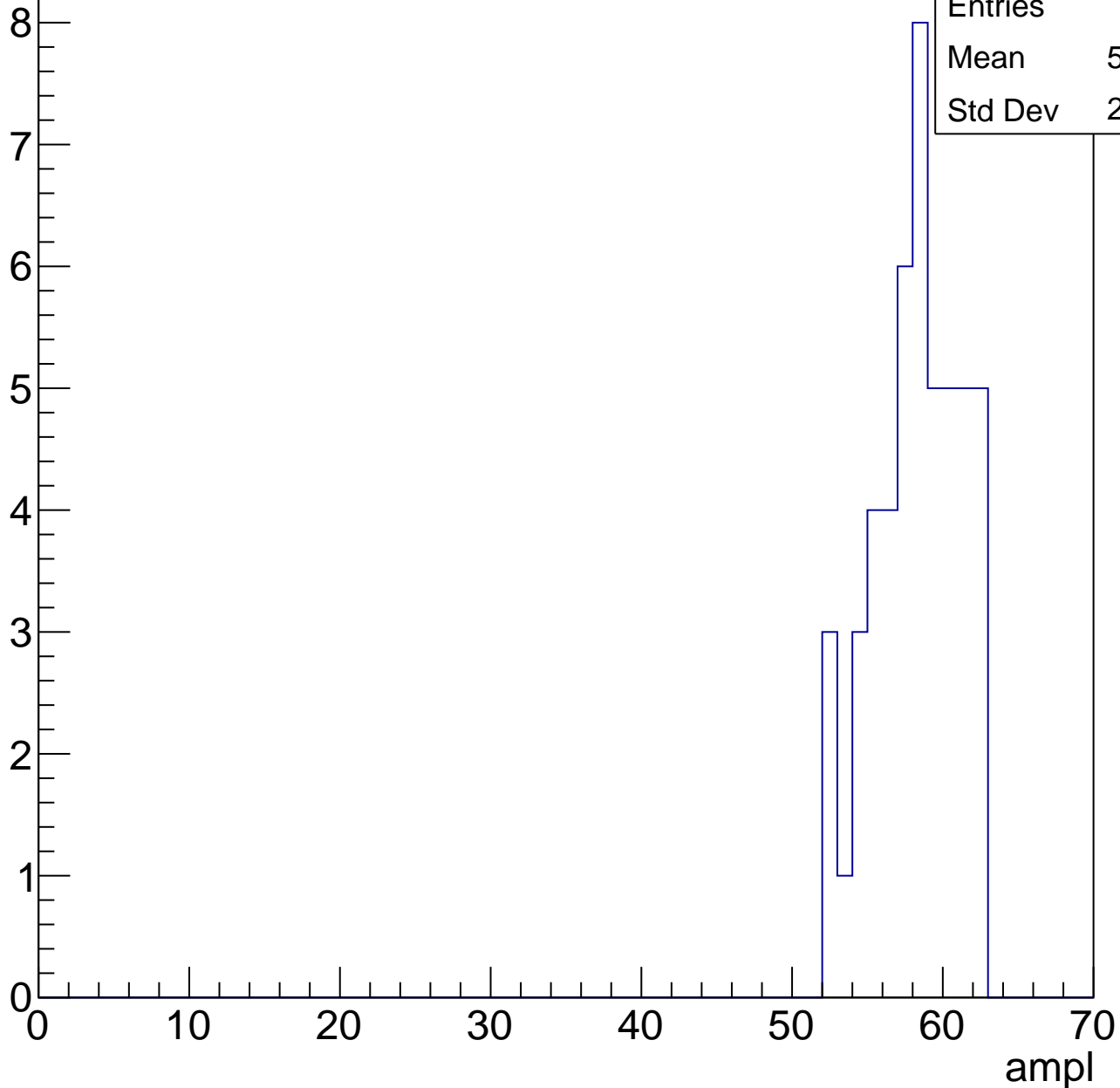
Entries	52
Mean	53.58
Std Dev	3.359



B1L003S, U19-ch38, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

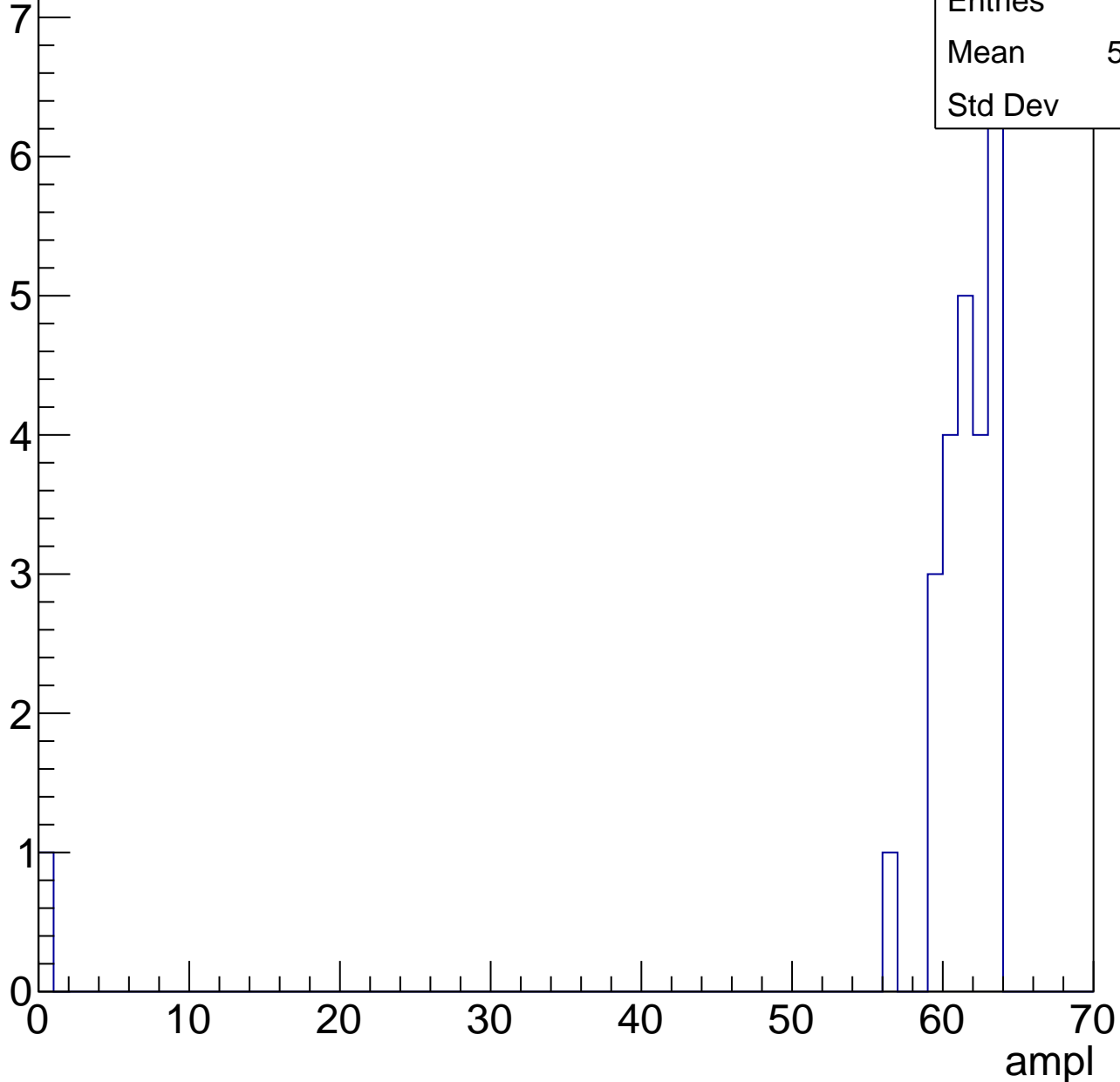


B1L003S, U19-ch38, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

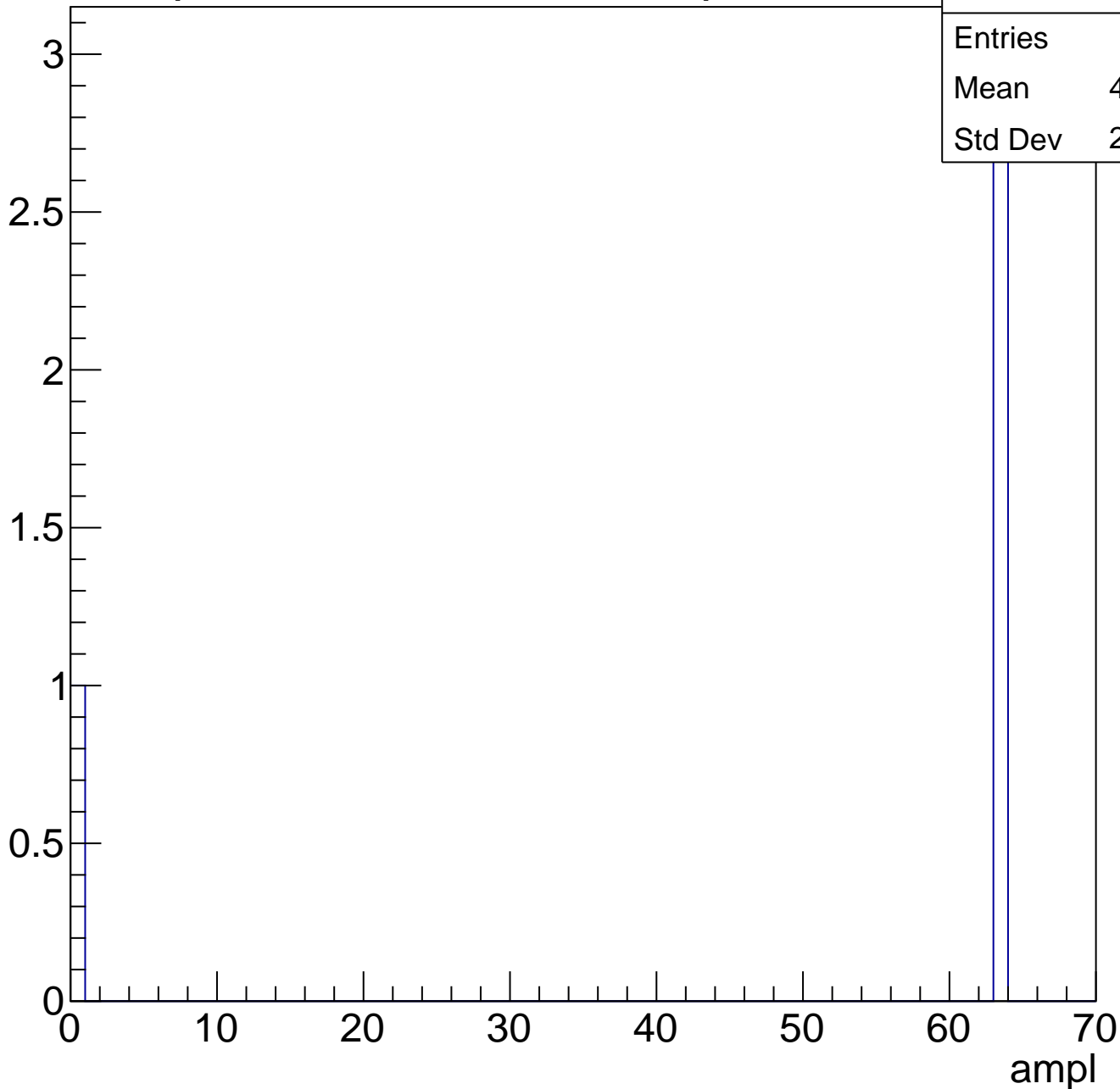
Entries	25
Mean	58.68
Std Dev	12.1



B1L003S, U19-ch38, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch39, adc0

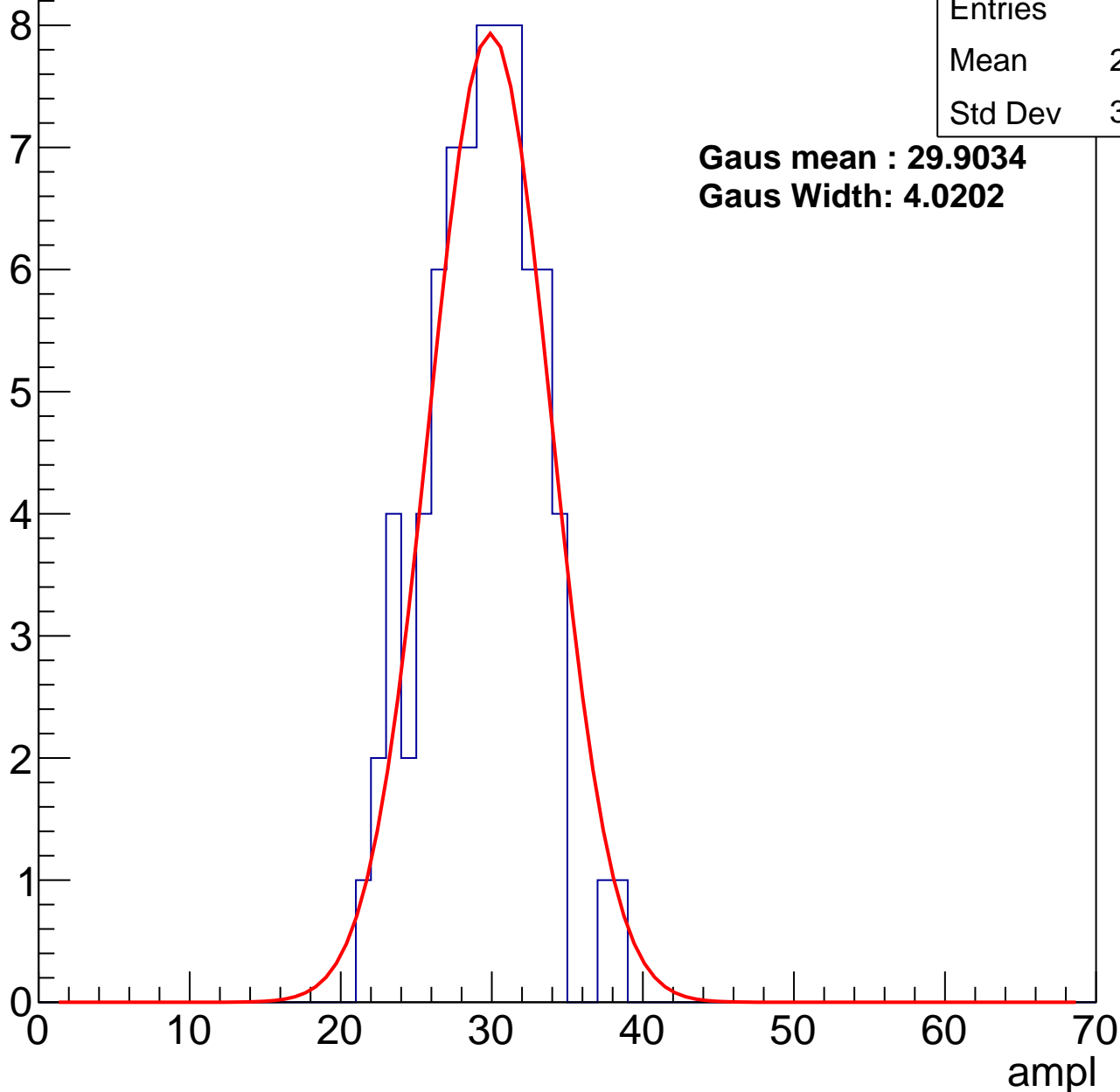
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	28.89
Std Dev	3.546

Gaus mean : 29.9034

Gaus Width: 4.0202



B1L003S, U19-ch39, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	35.89
Std Dev	2.983

Gaus mean : 36.2969

Gaus Width: 2.9828

Entry

10

8

6

4

2

0

0

10

20

30

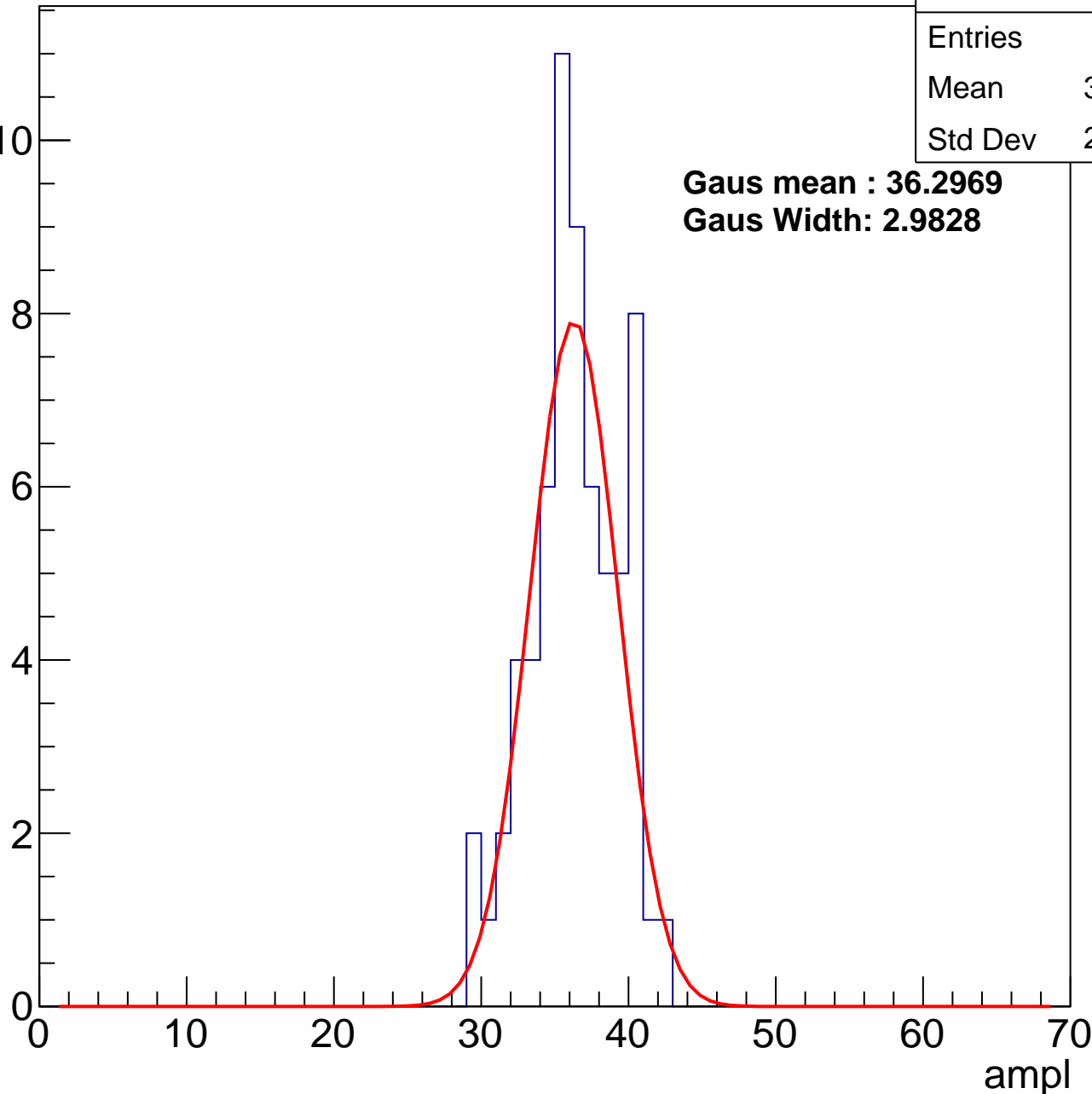
40

50

60

70

ampl



B1L003S, U19-ch39, adc2

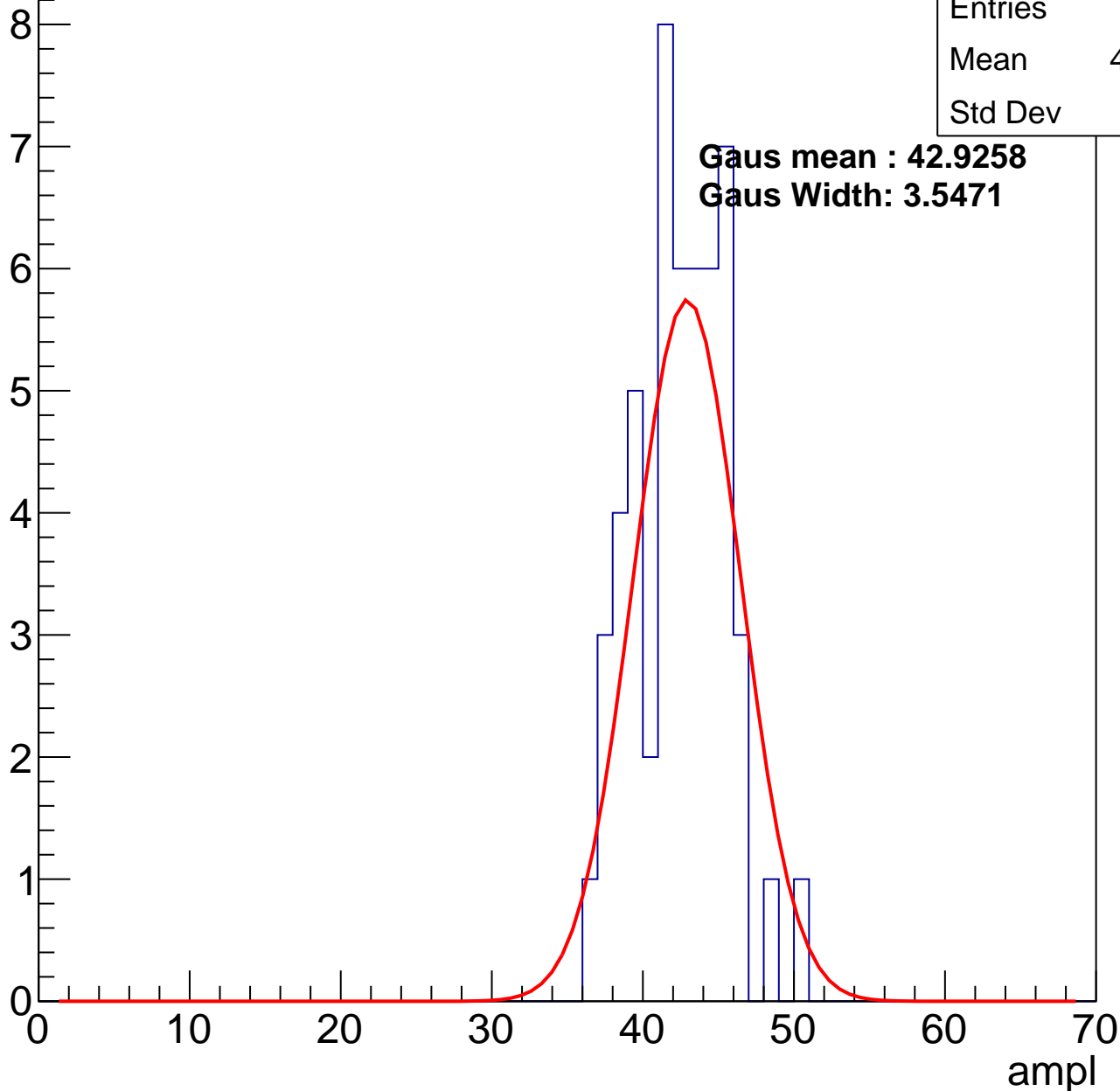
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.02
Std Dev	3

Gaus mean : 42.9258

Gaus Width: 3.5471



B1L003S, U19-ch39, adc3

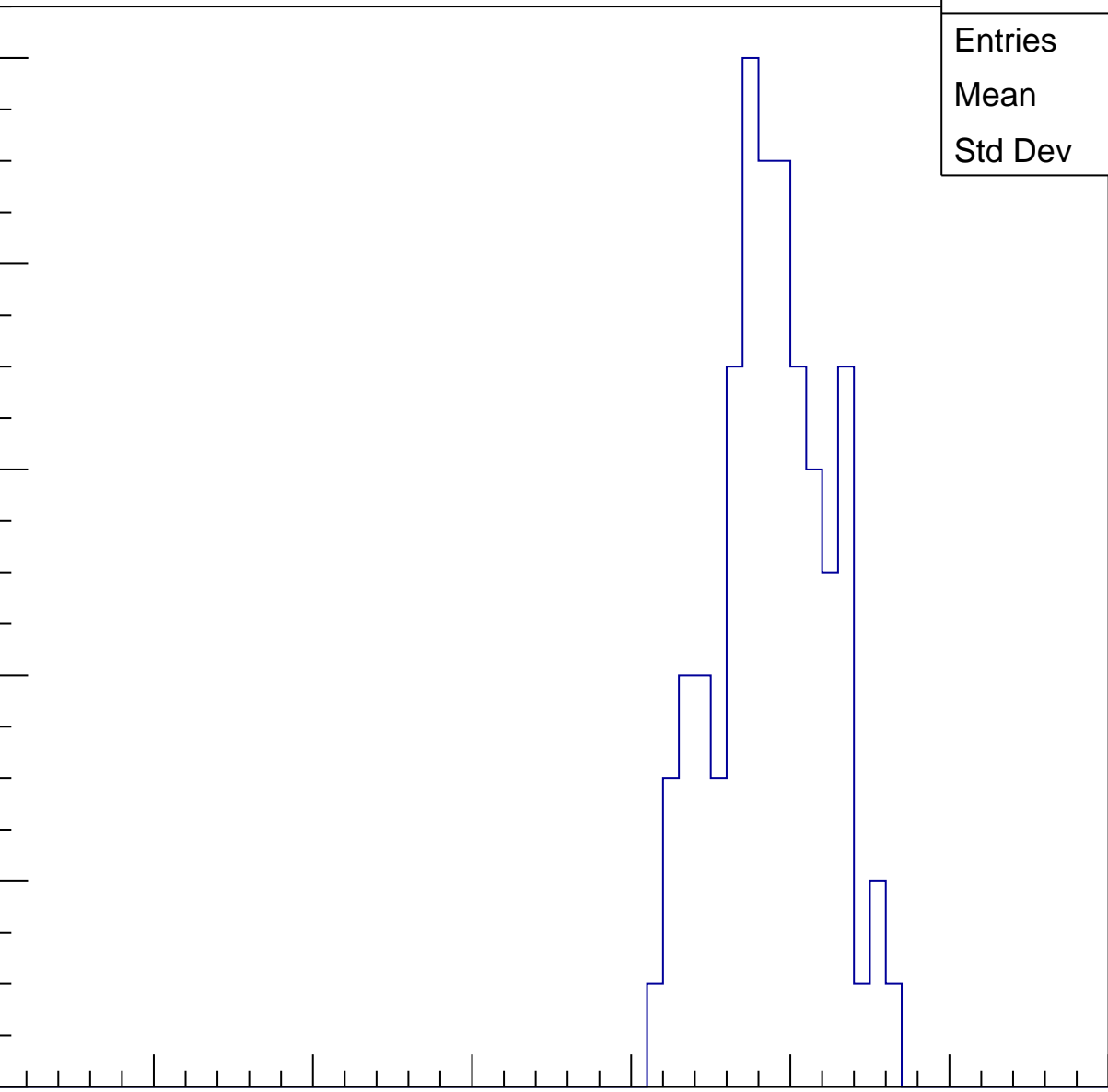
calib_packv5_042523_0143.root, FC#13, port D2

Entries	79
Mean	48.38
Std Dev	3.41

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

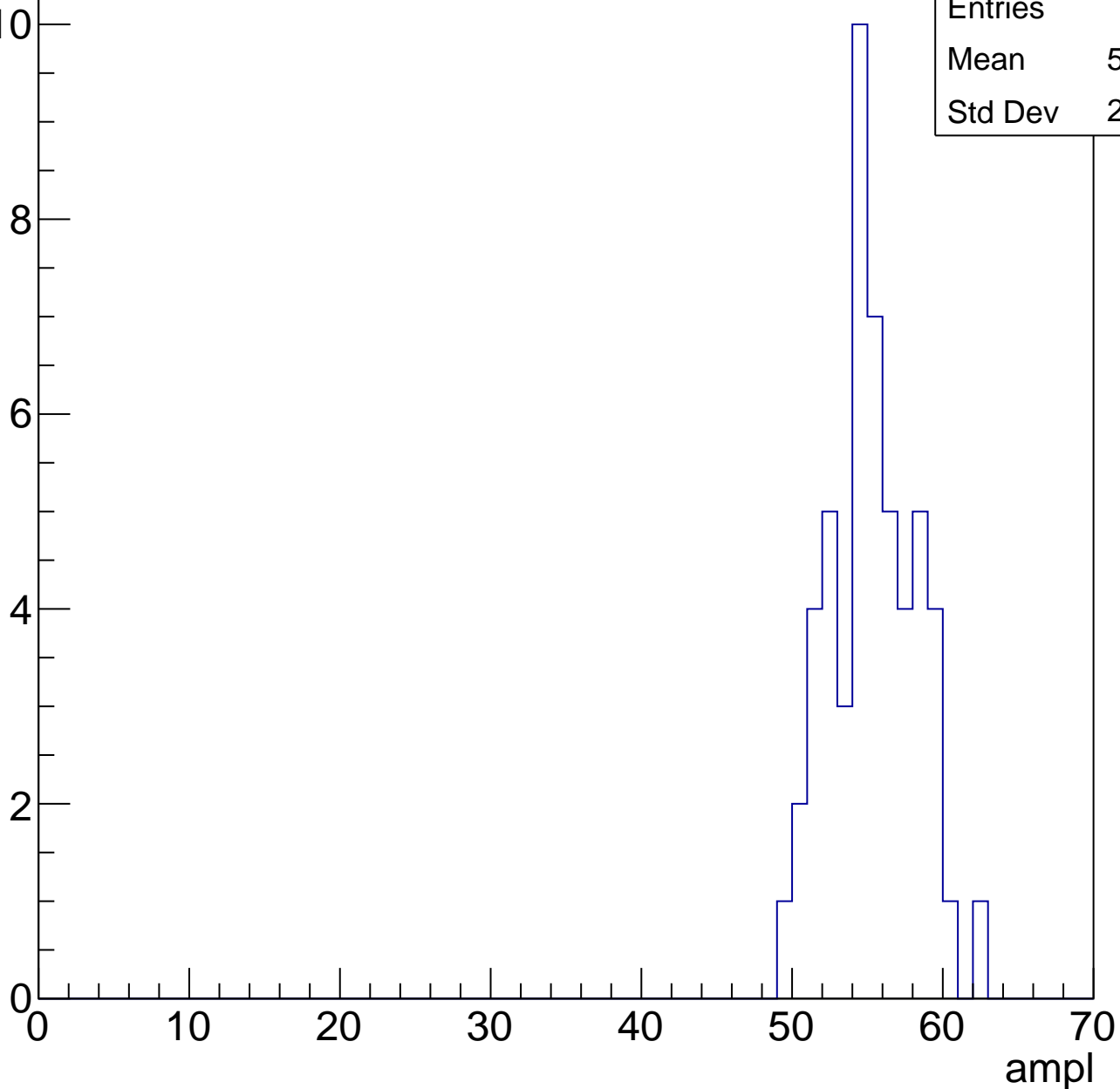


B1L003S, U19-ch39, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	54.87
Std Dev	2.842

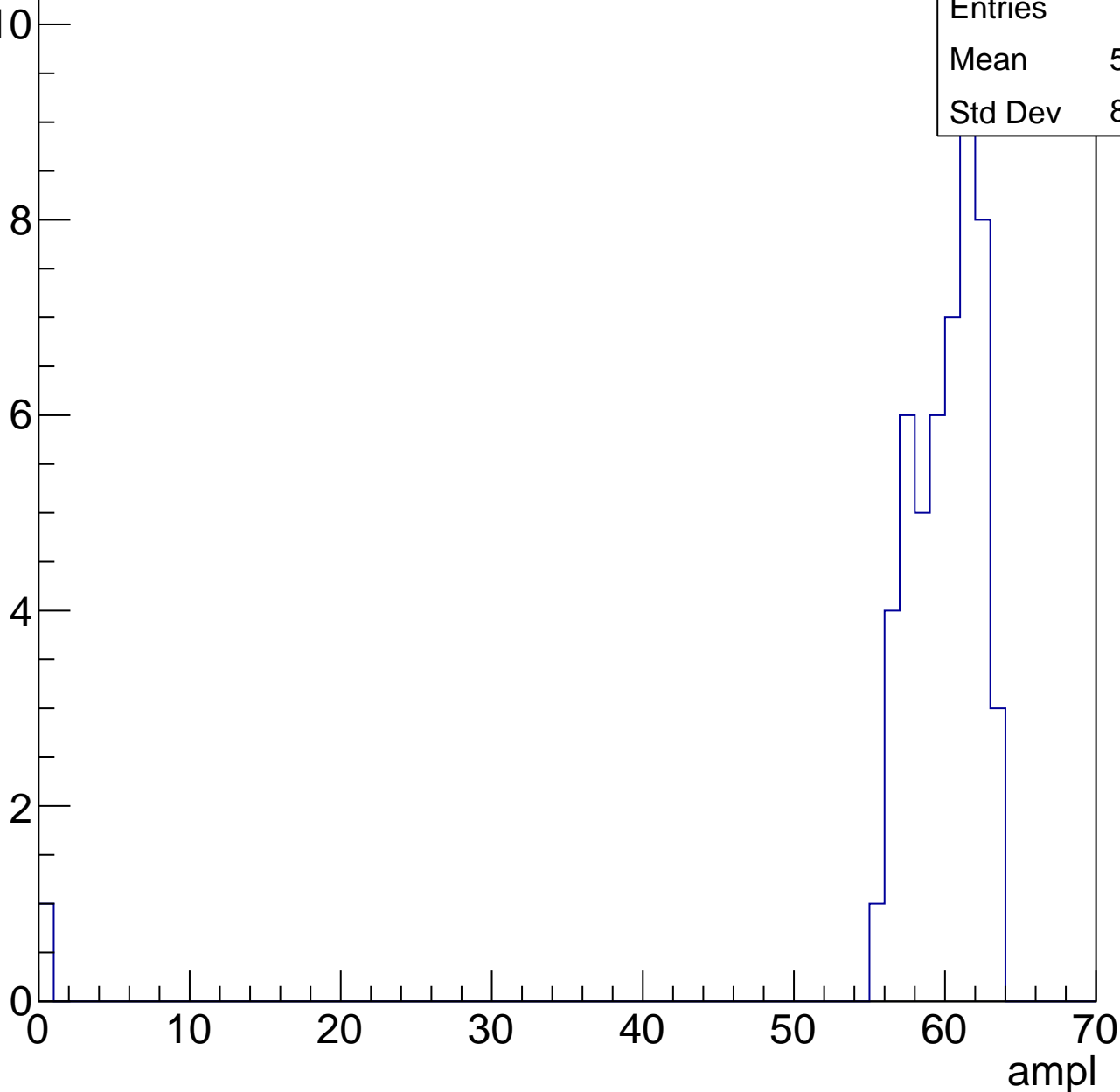


B1L003S, U19-ch39, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.43
Std Dev	8.532

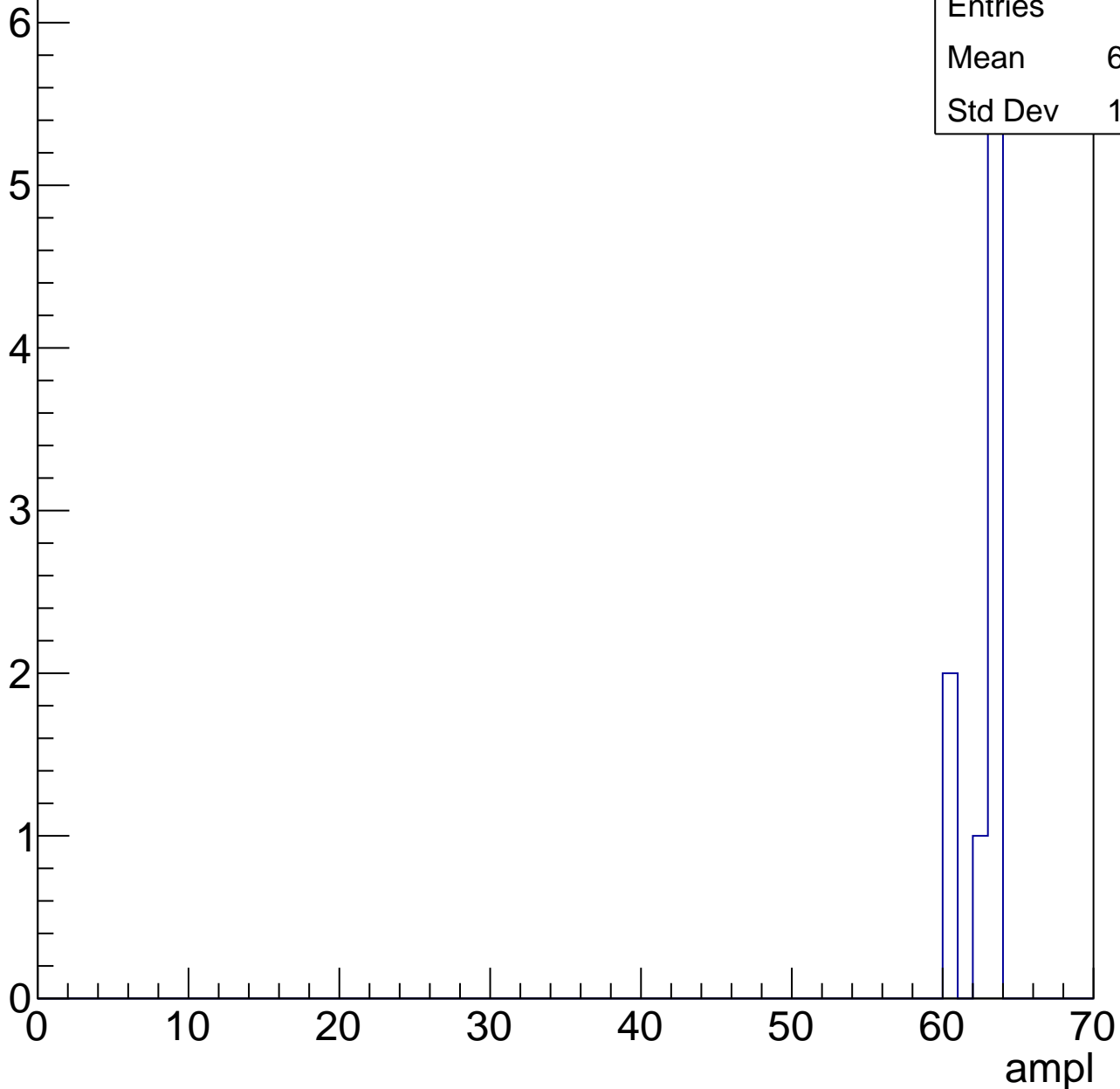


B1L003S, U19-ch39, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	62.22
Std Dev	1.227



B1L003S, U19-ch39, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch40, adc0

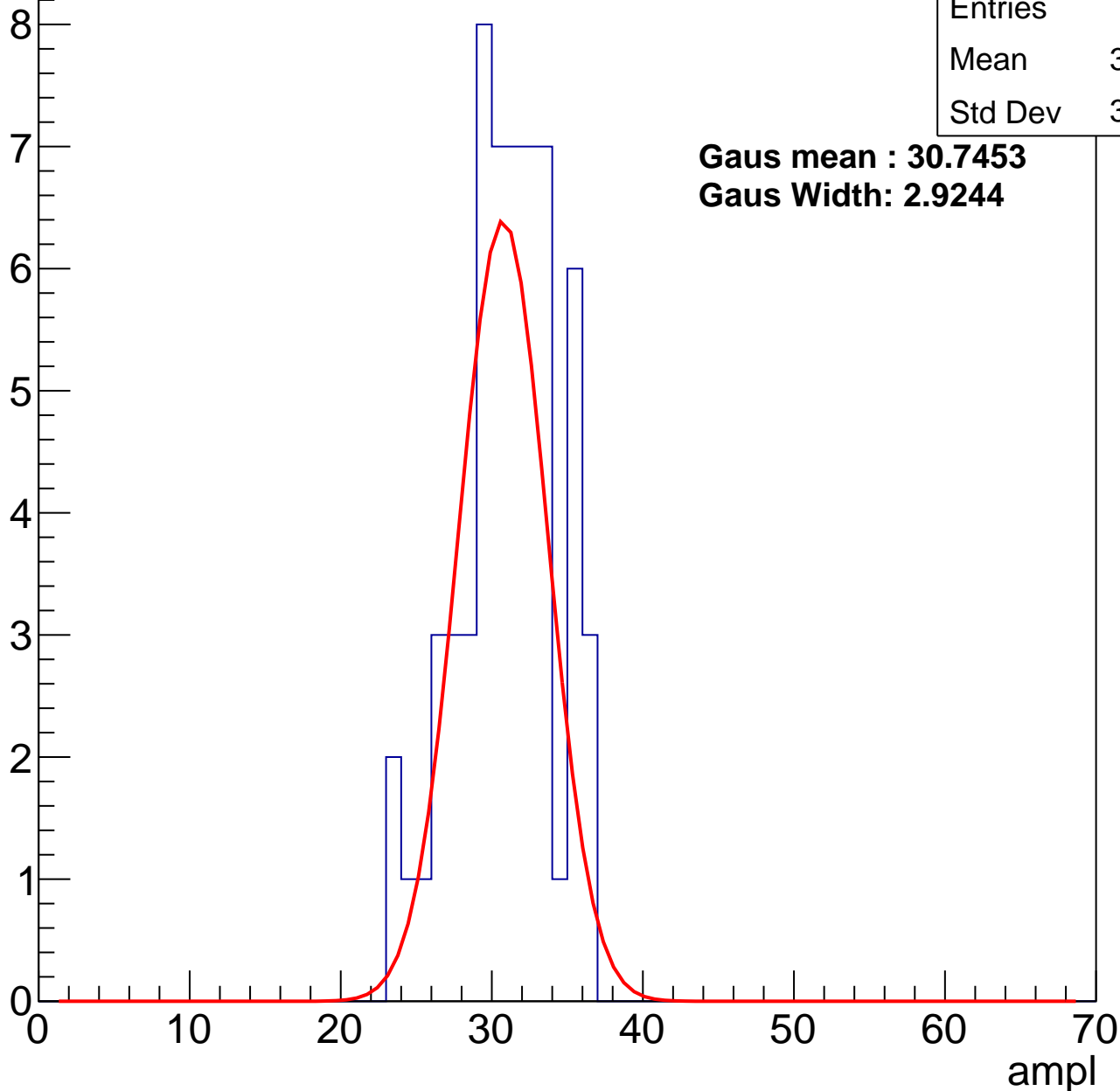
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	30.58
Std Dev	3.206

Gaus mean : 30.7453

Gaus Width: 2.9244



B1L003S, U19-ch40, adc1

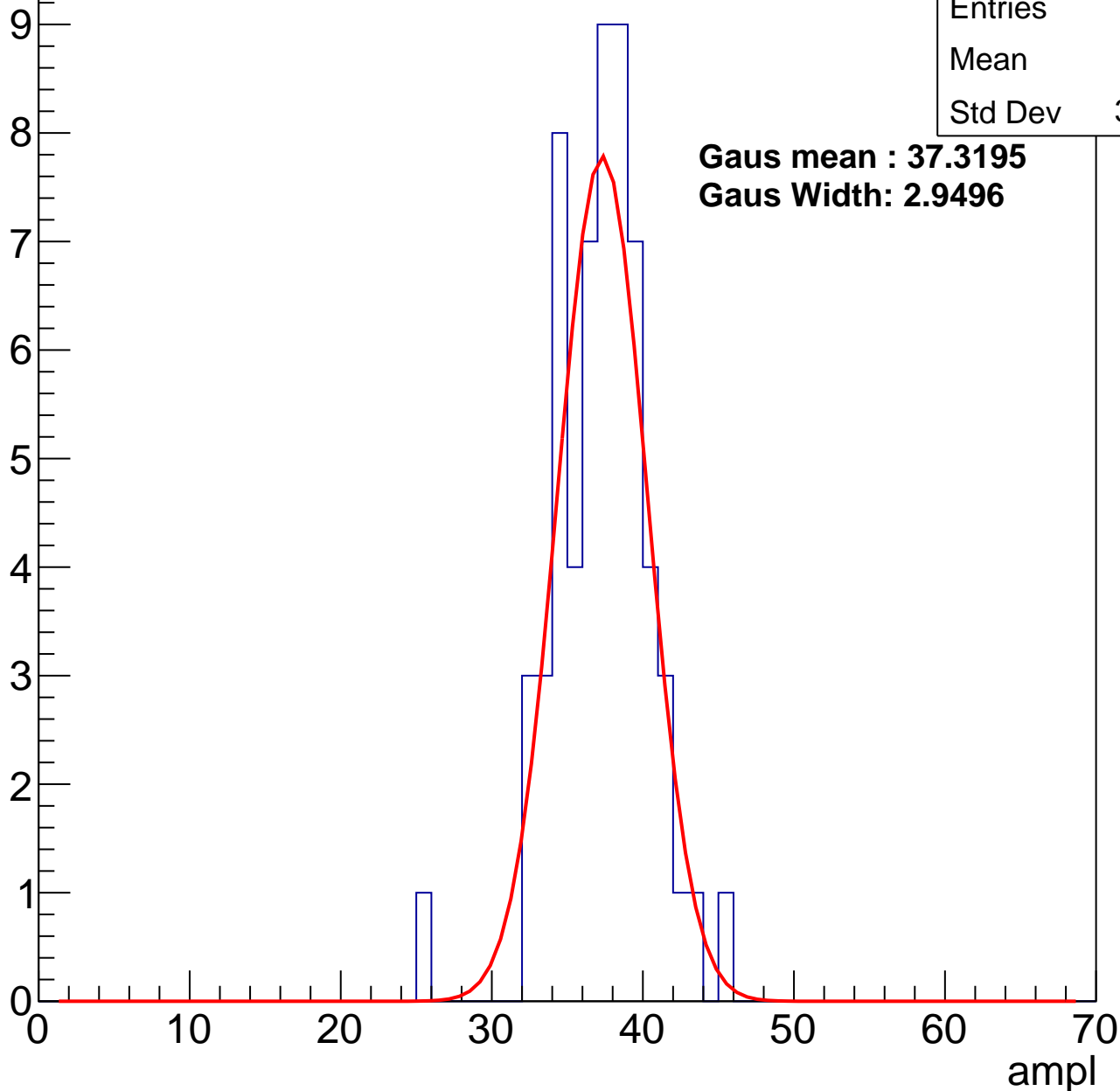
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.8
Std Dev	3.151

Gaus mean : 37.3195

Gaus Width: 2.9496



B1L003S, U19-ch40, adc2

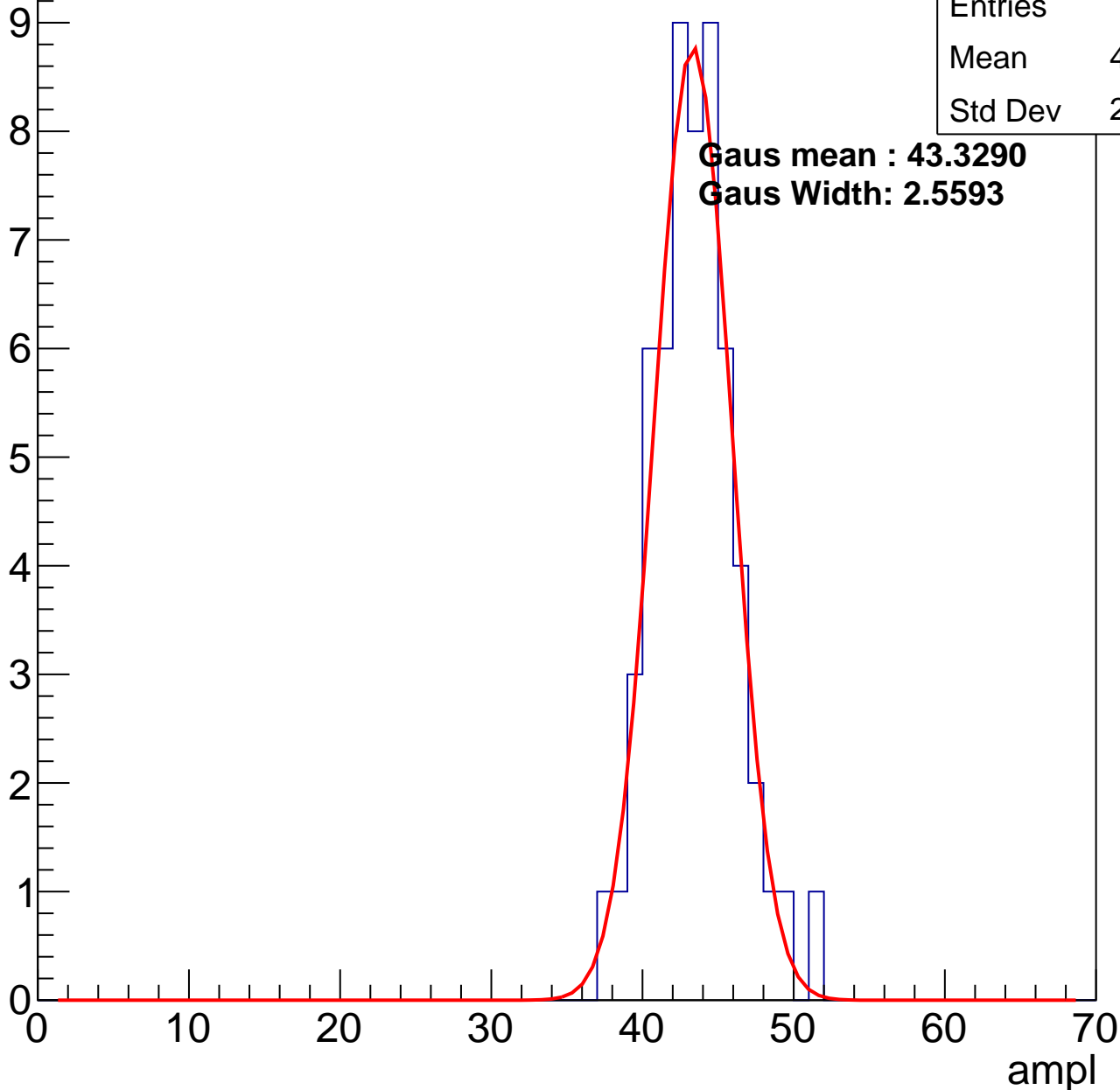
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.97
Std Dev	2.697

Gaus mean : 43.3290

Gaus Width: 2.5593

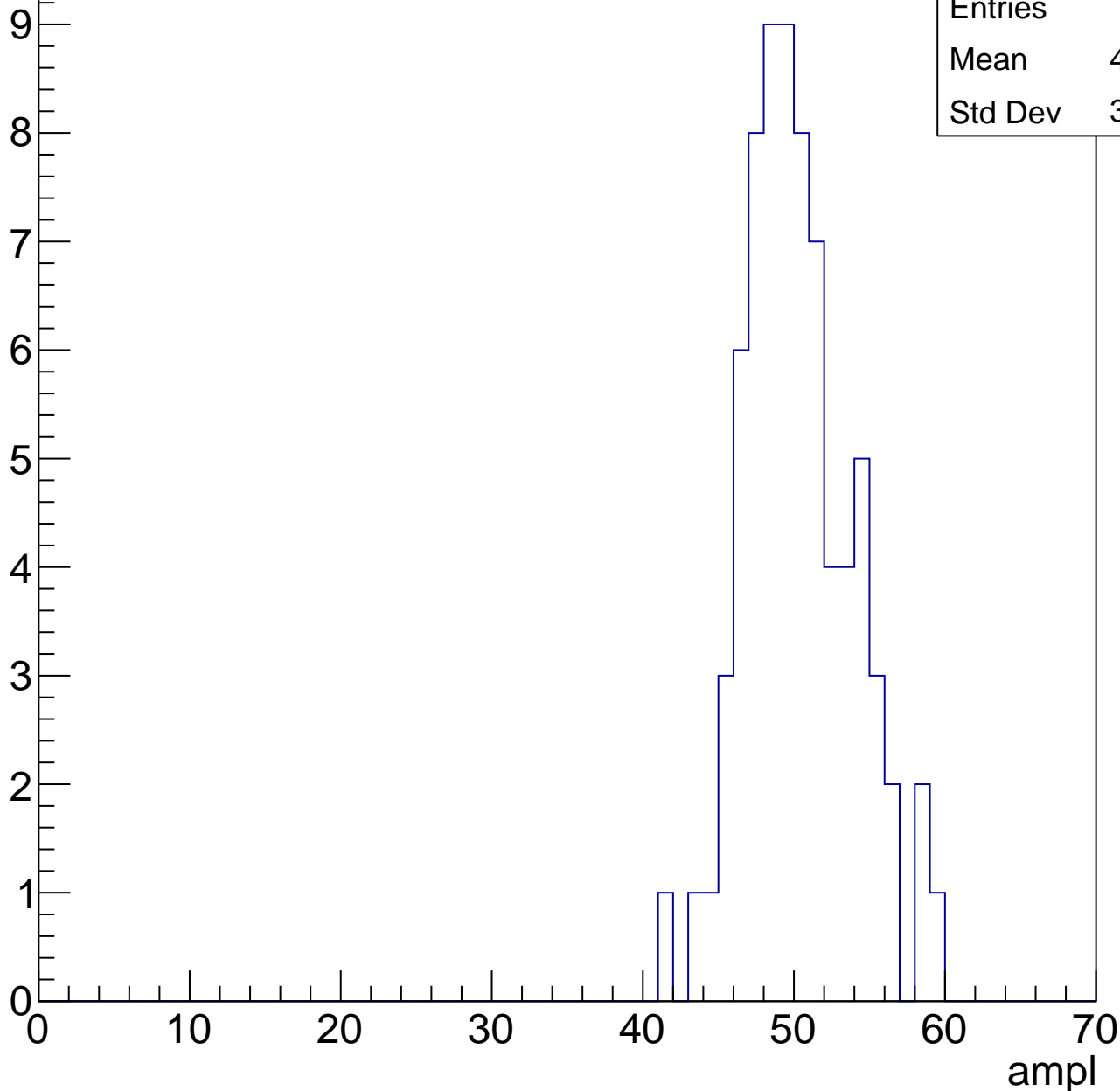


B1L003S, U19-ch40, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

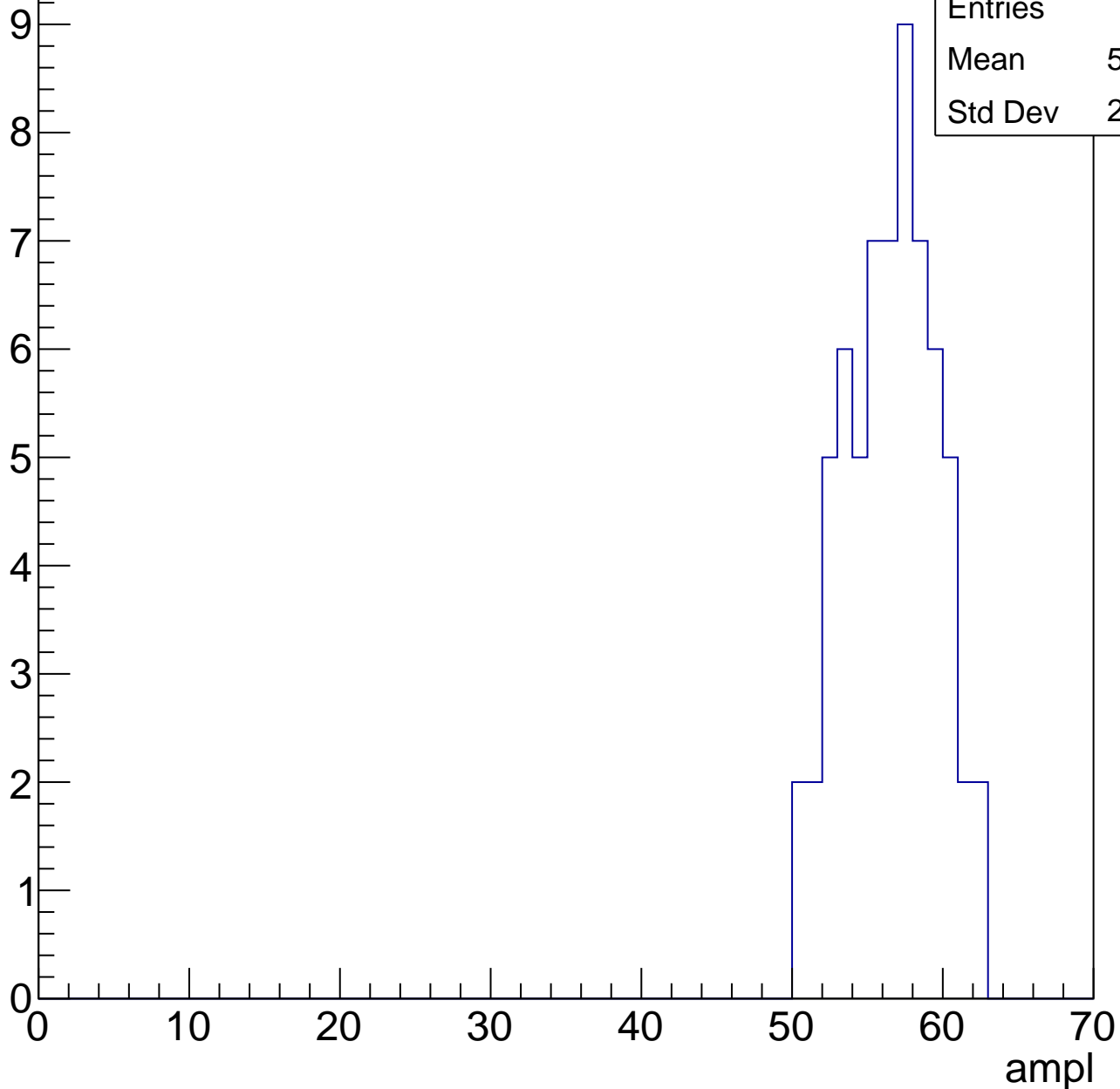
Entries	74
Mean	49.82
Std Dev	3.577



B1L003S, U19-ch40, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

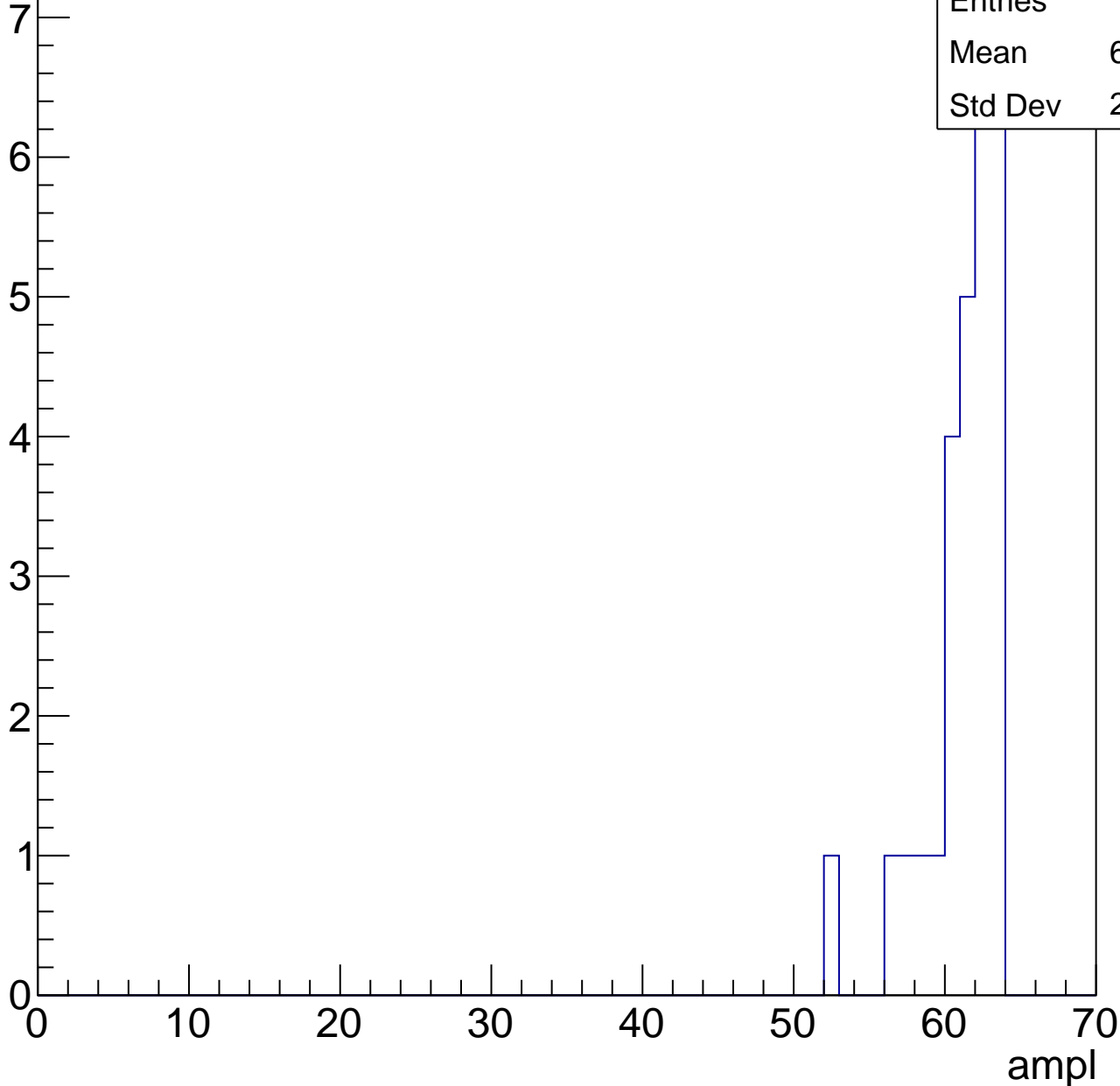


B1L003S, U19-ch40, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	28
Mean	60.79
Std Dev	2.484



B1L003S, U19-ch40, adc6

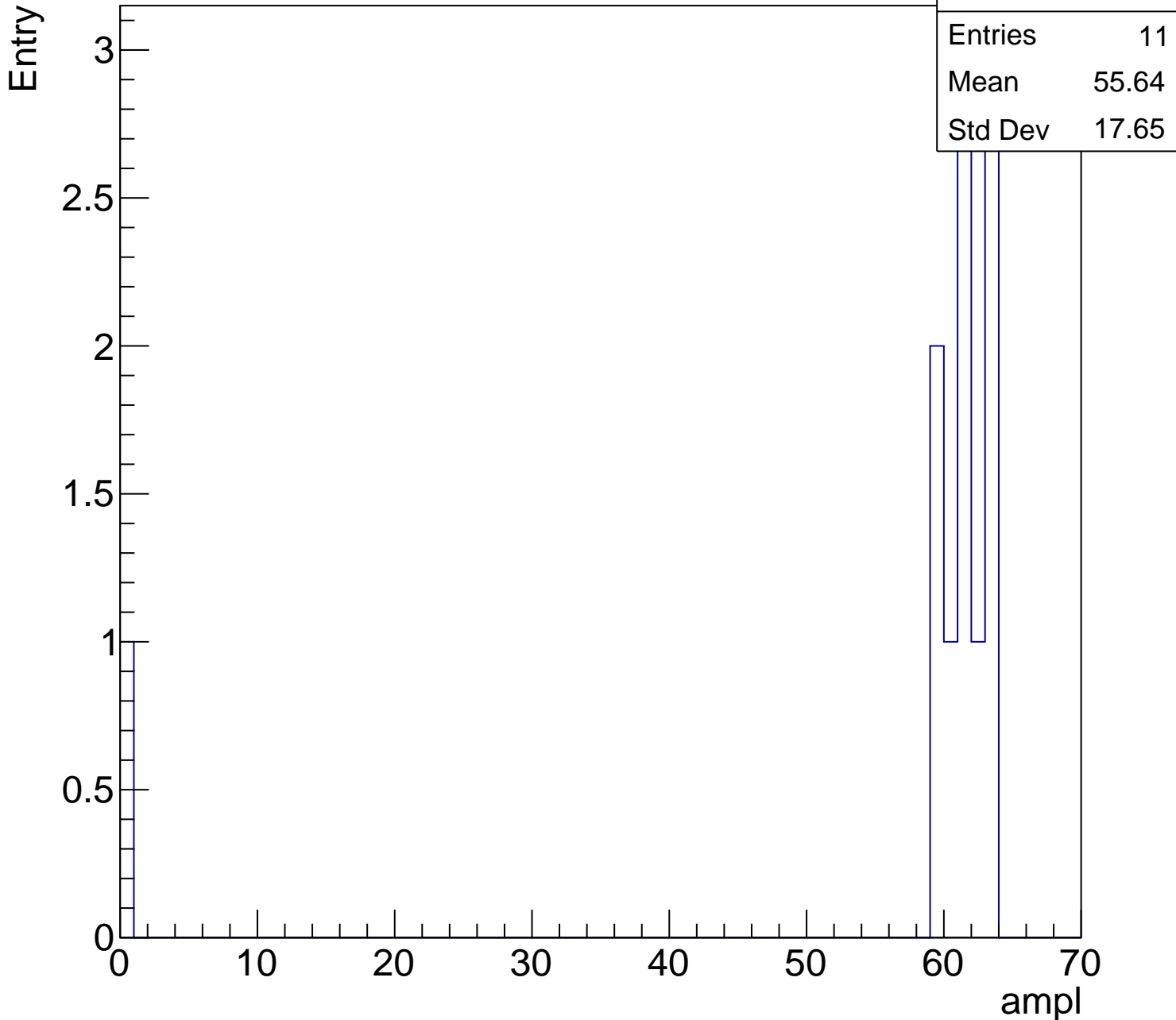
calib_packv5_042523_0143.root, FC#13, port D2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	55.64
Std Dev	17.65

ampl



B1L003S, U19-ch40, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch41, adc0

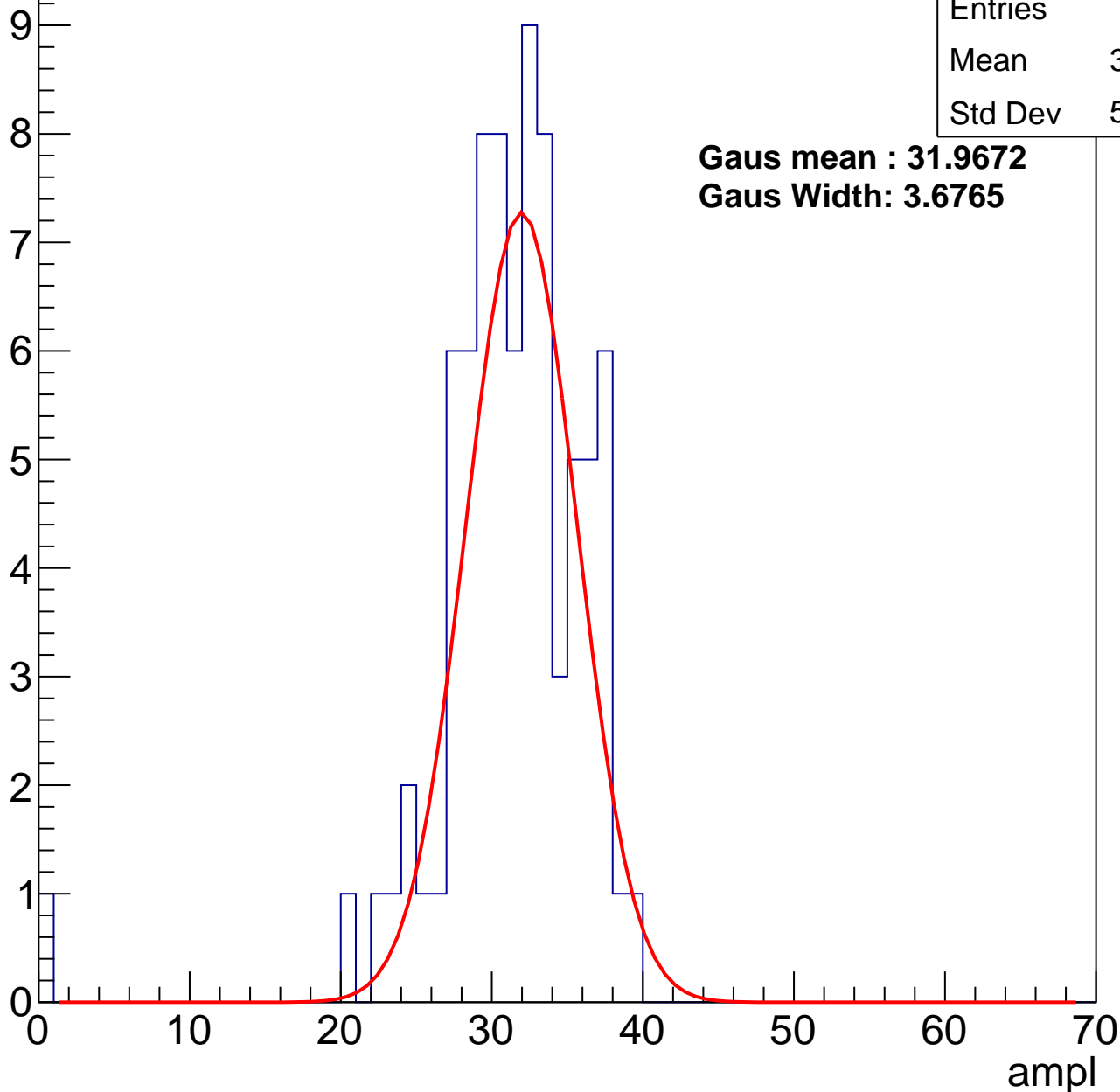
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	30.75
Std Dev	5.212

Gaus mean : 31.9672

Gaus Width: 3.6765



B1L003S, U19-ch41, adc1

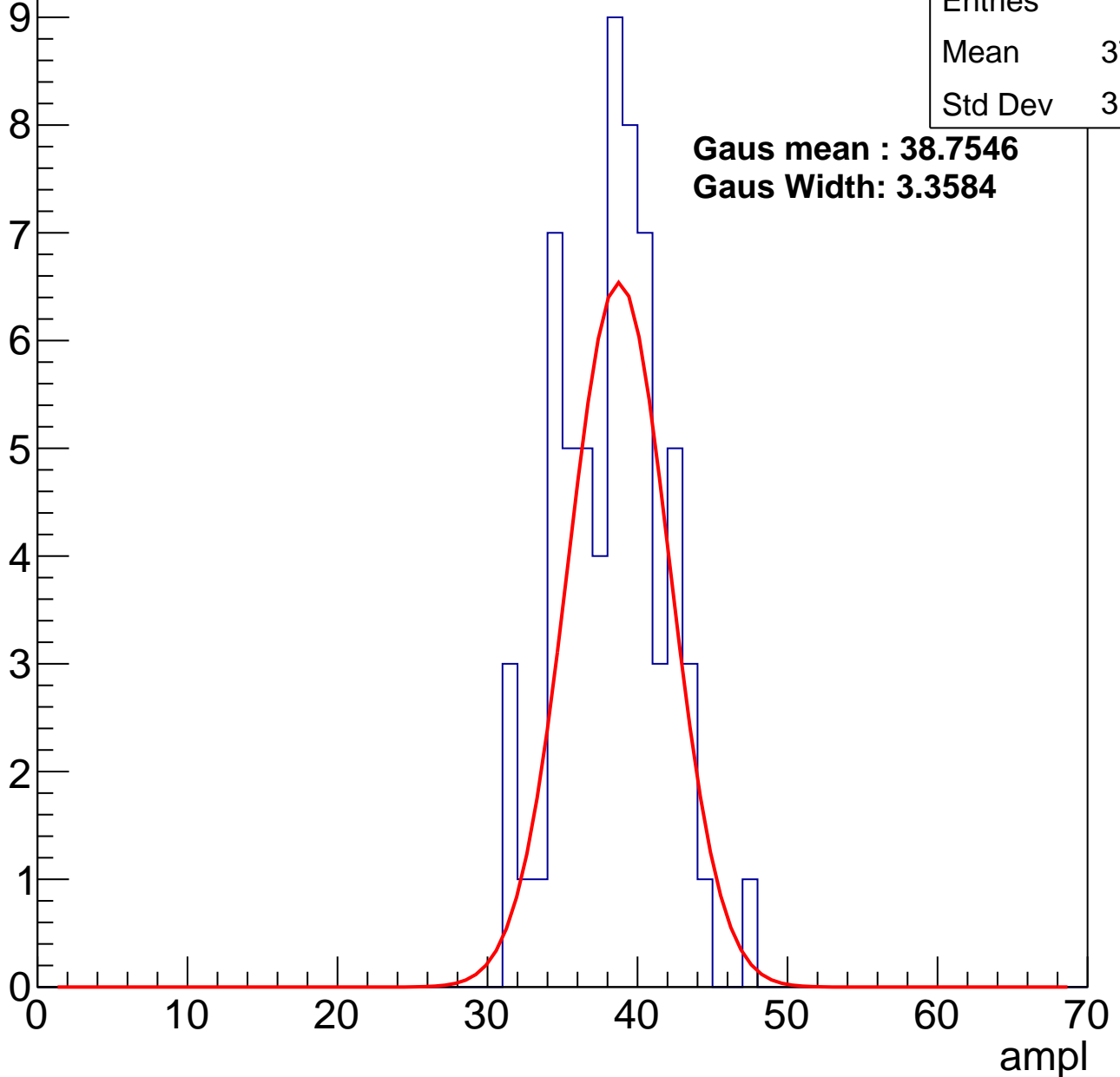
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	37.87
Std Dev	3.388

Gaus mean : 38.7546

Gaus Width: 3.3584



B1L003S, U19-ch41, adc2

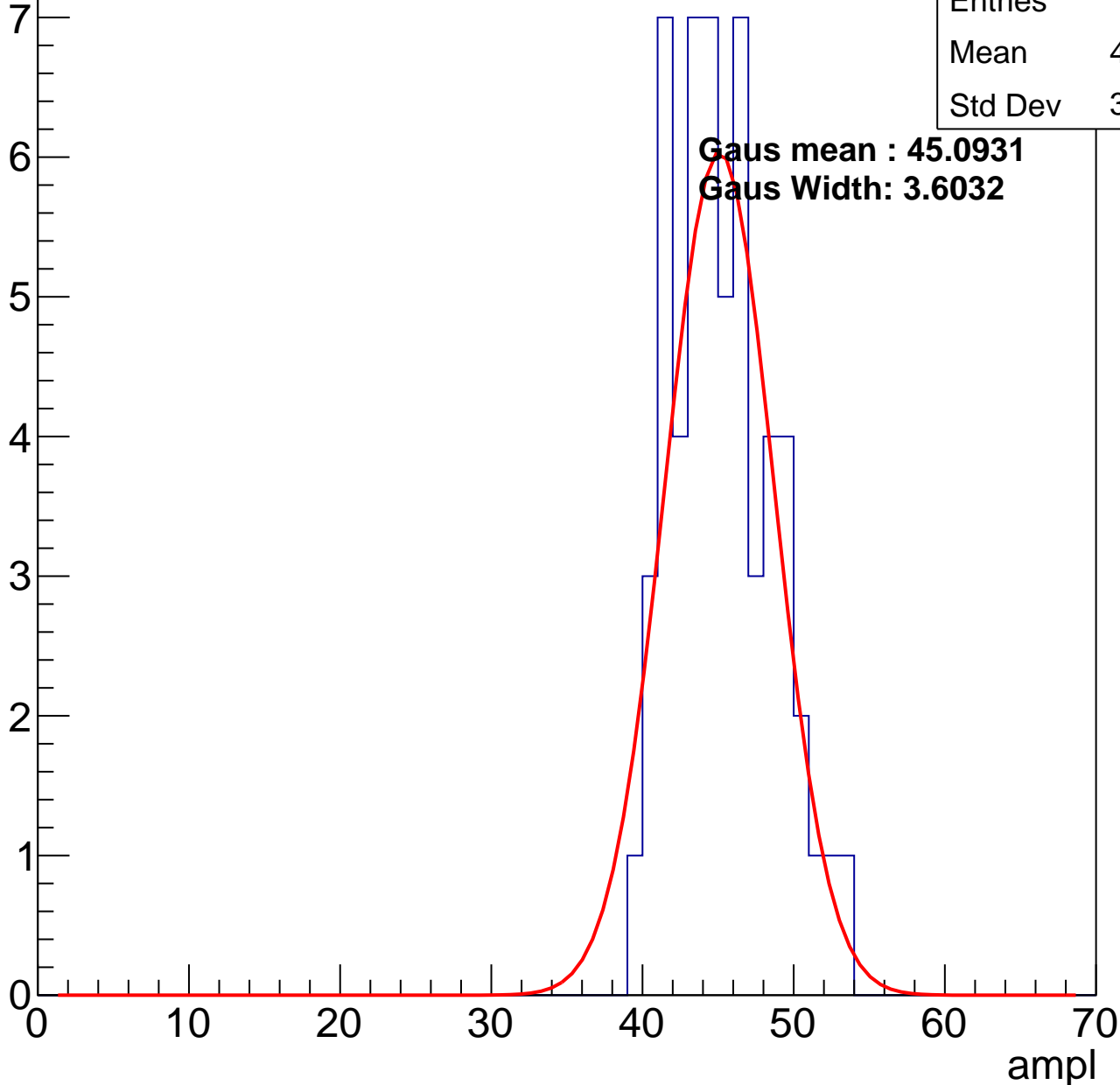
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	44.82
Std Dev	3.272

Gaus mean : 45.0931

Gaus Width: 3.6032

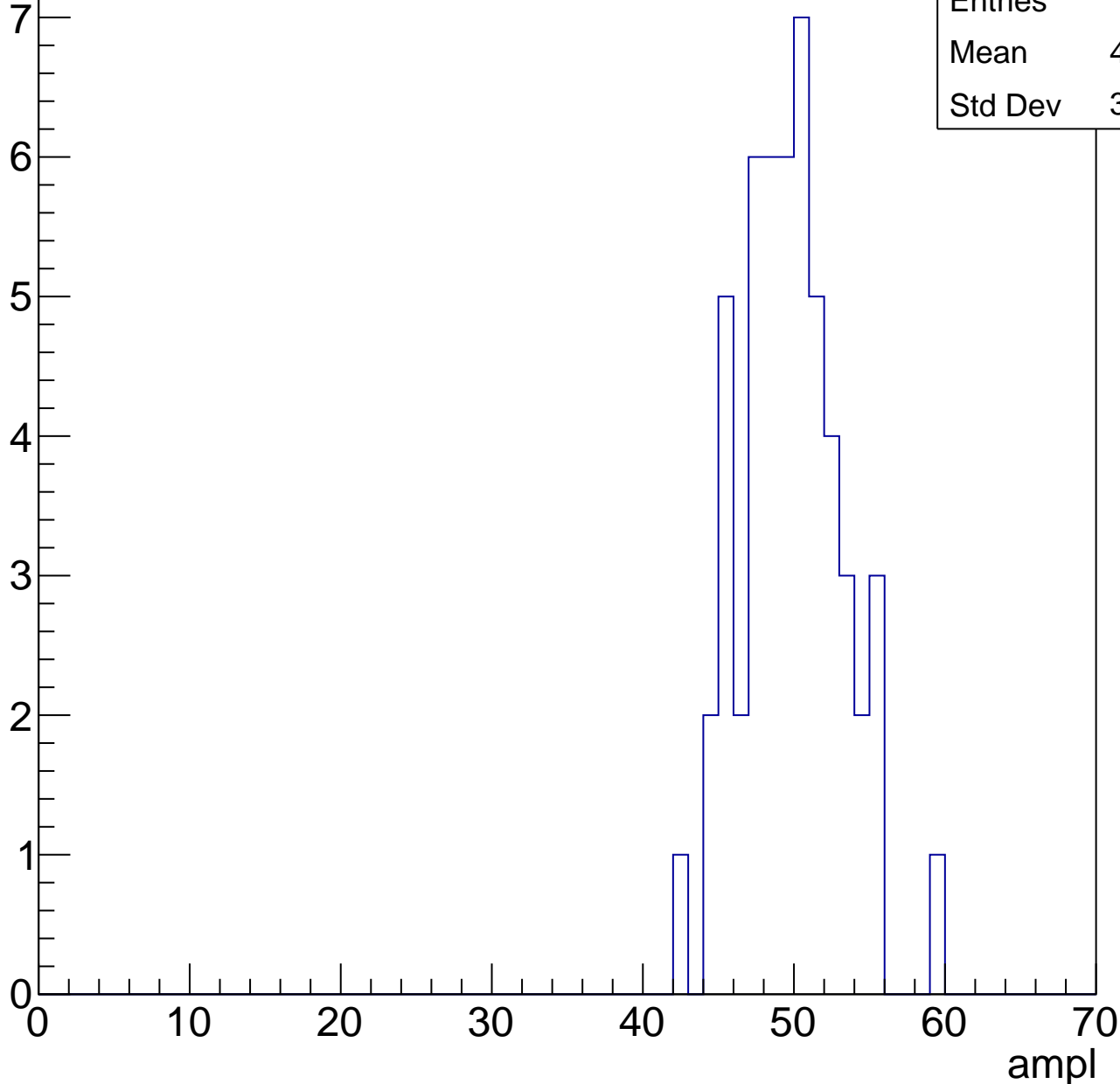


B1L003S, U19-ch41, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

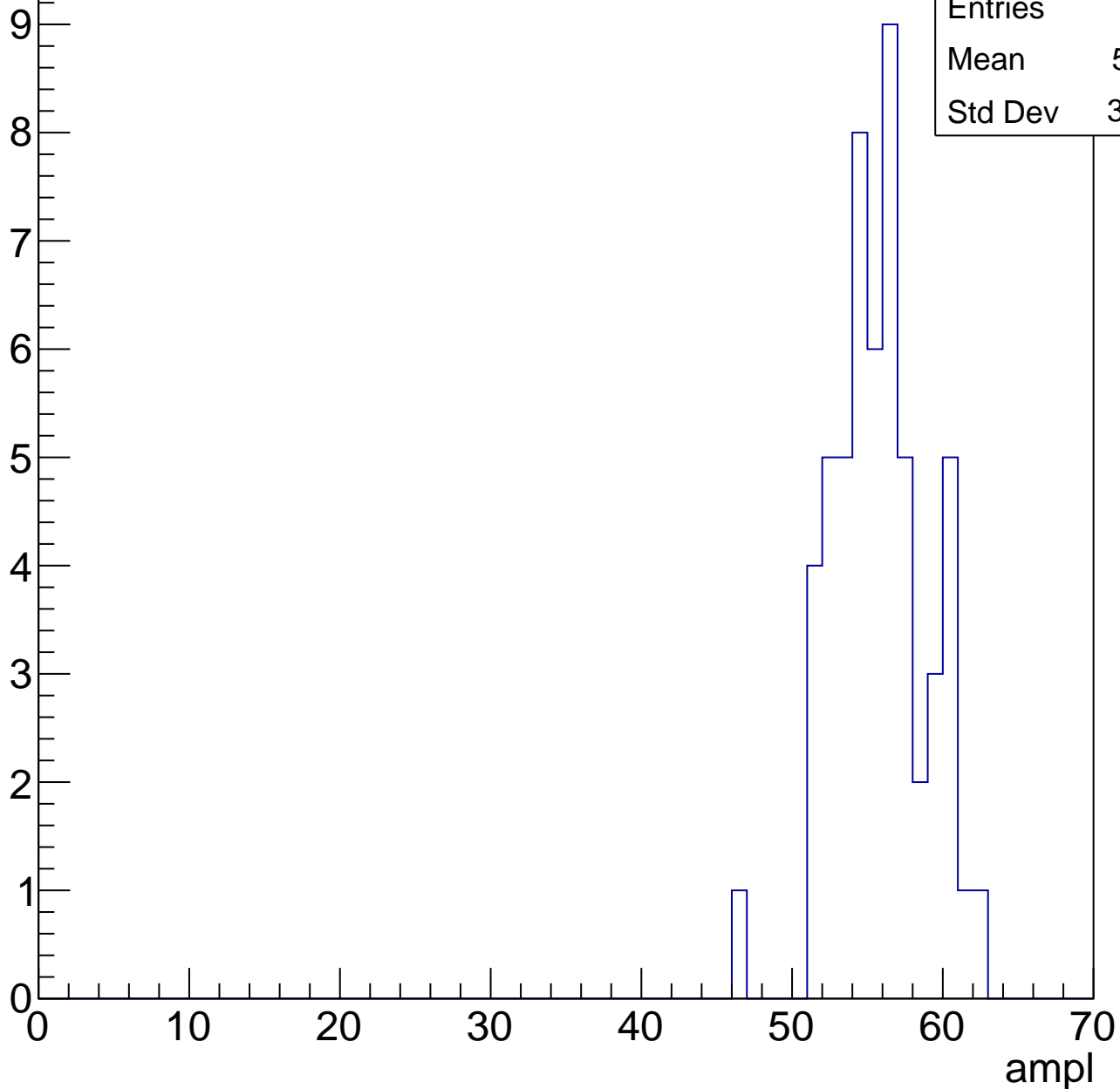
Entries	53
Mean	49.34
Std Dev	3.336



B1L003S, U19-ch41, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



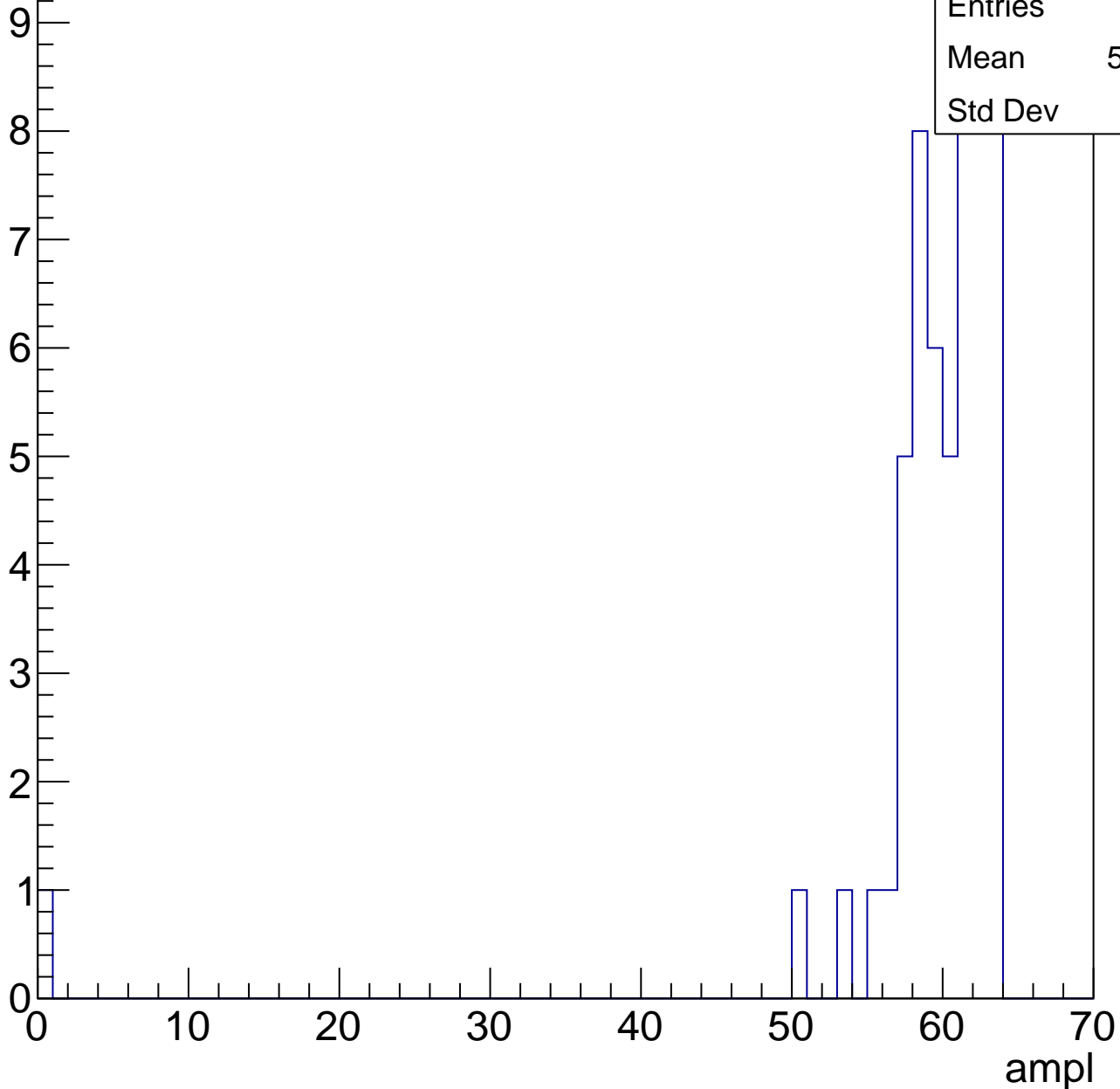
Entries	55
Mean	55.31
Std Dev	3.068

B1L003S, U19-ch41, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.77
Std Dev	8.36



B1L003S, U19-ch41, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U19-ch41, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch42, adc0

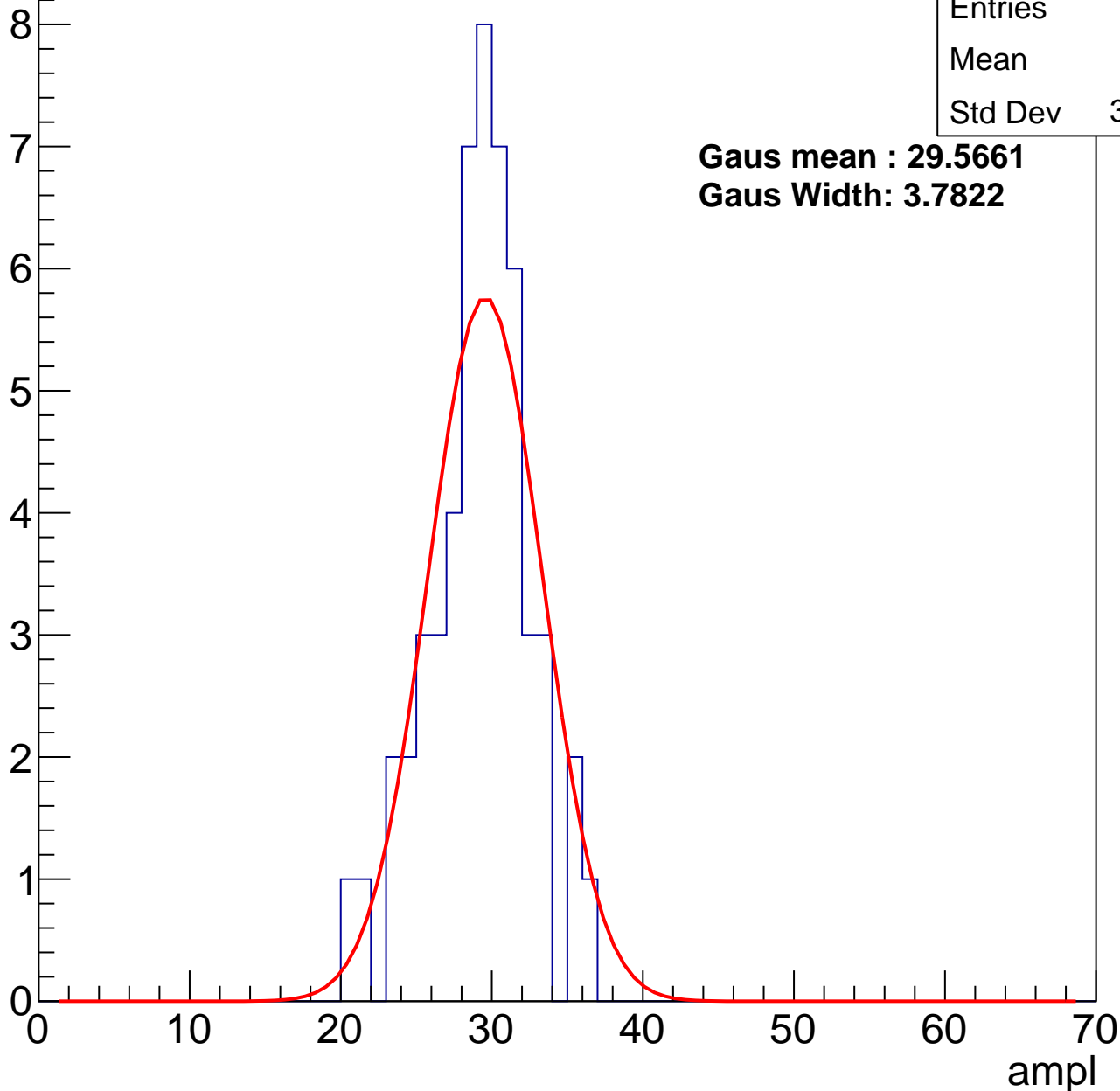
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	28.7
Std Dev	3.328

Gaus mean : 29.5661

Gaus Width: 3.7822

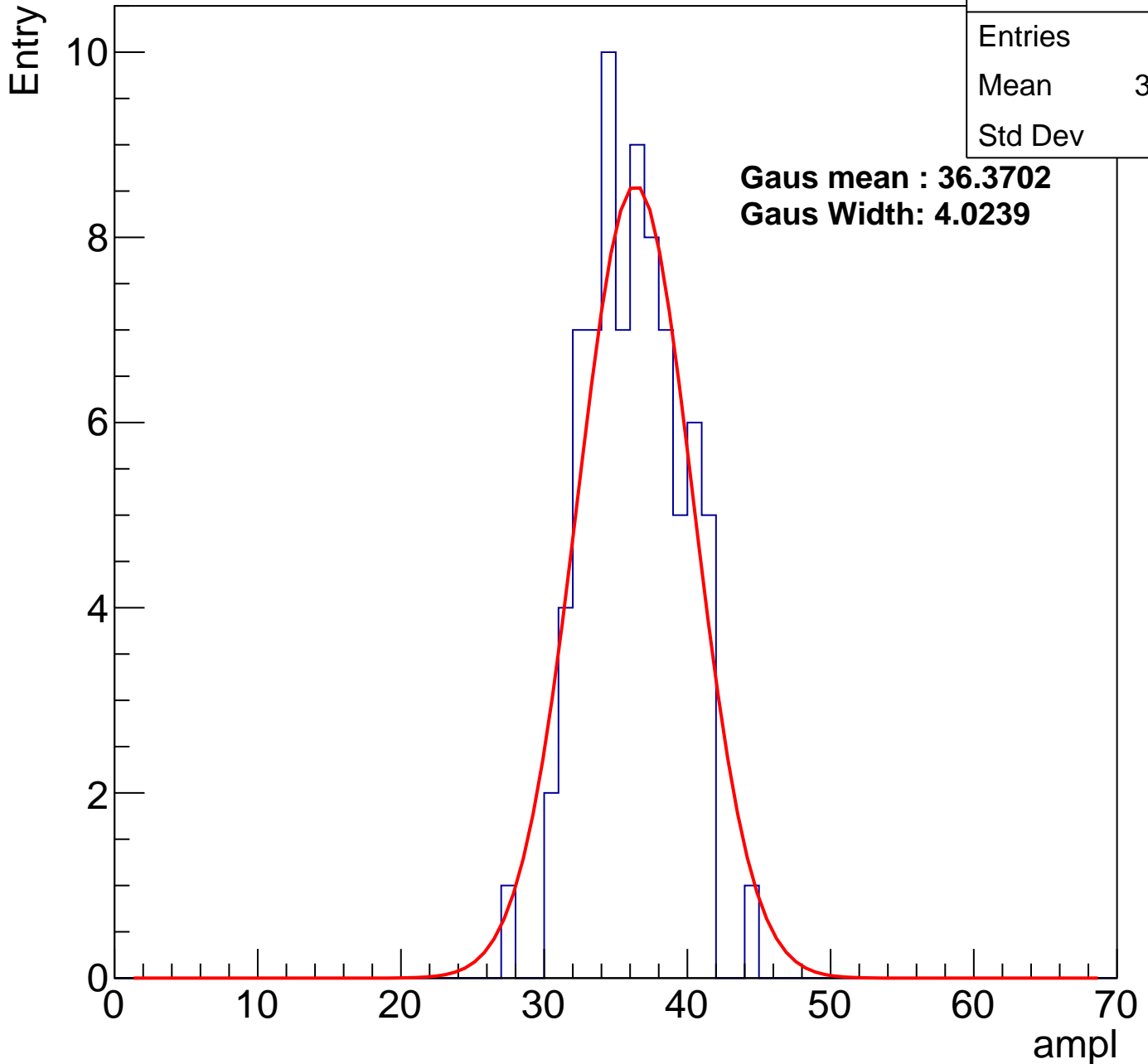


B1L003S, U19-ch42, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	79
Mean	35.71
Std Dev	3.25

Gaus mean : 36.3702
Gaus Width: 4.0239



B1L003S, U19-ch42, adc2

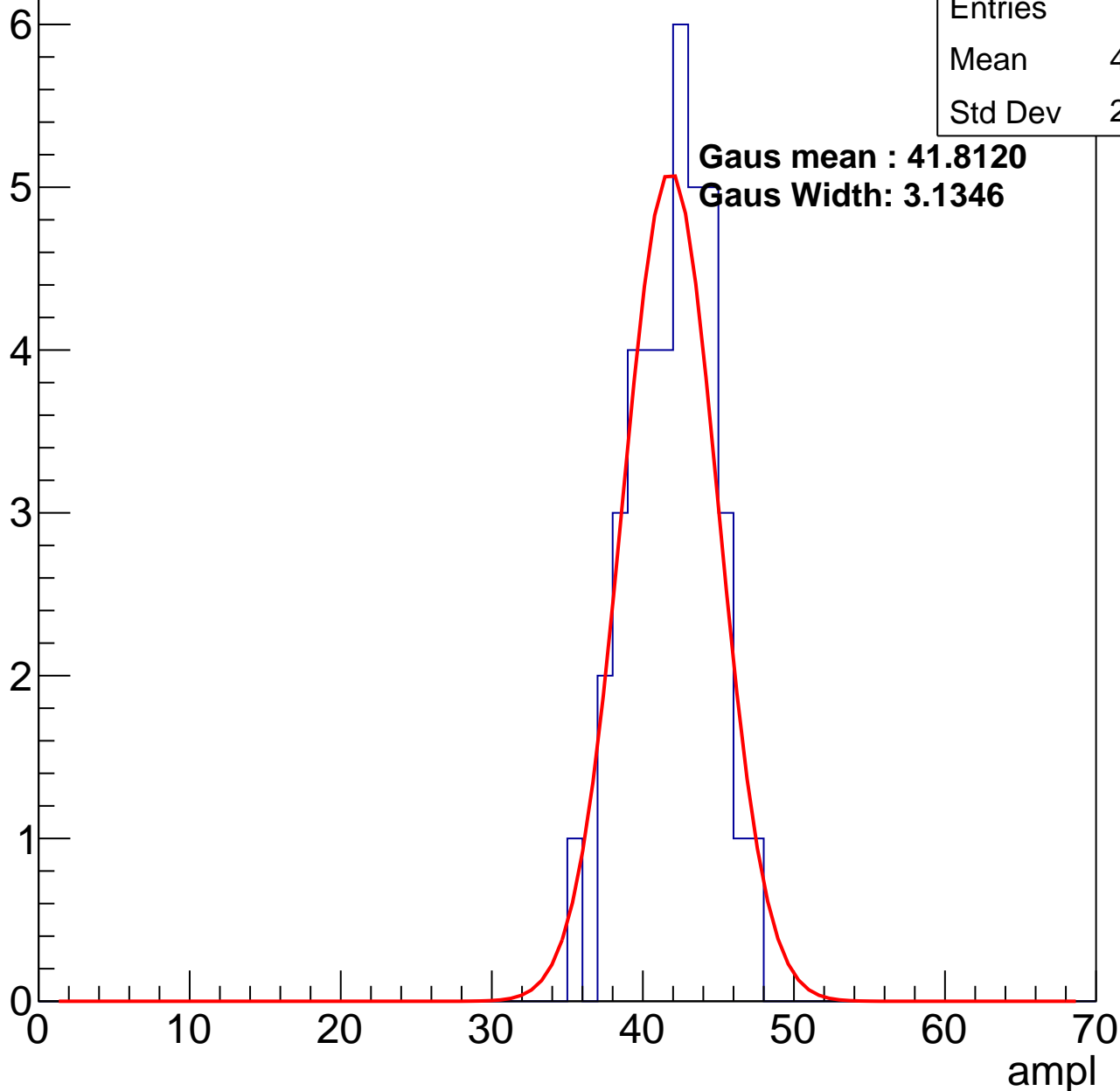
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	41.49
Std Dev	2.707

Gaus mean : 41.8120

Gaus Width: 3.1346

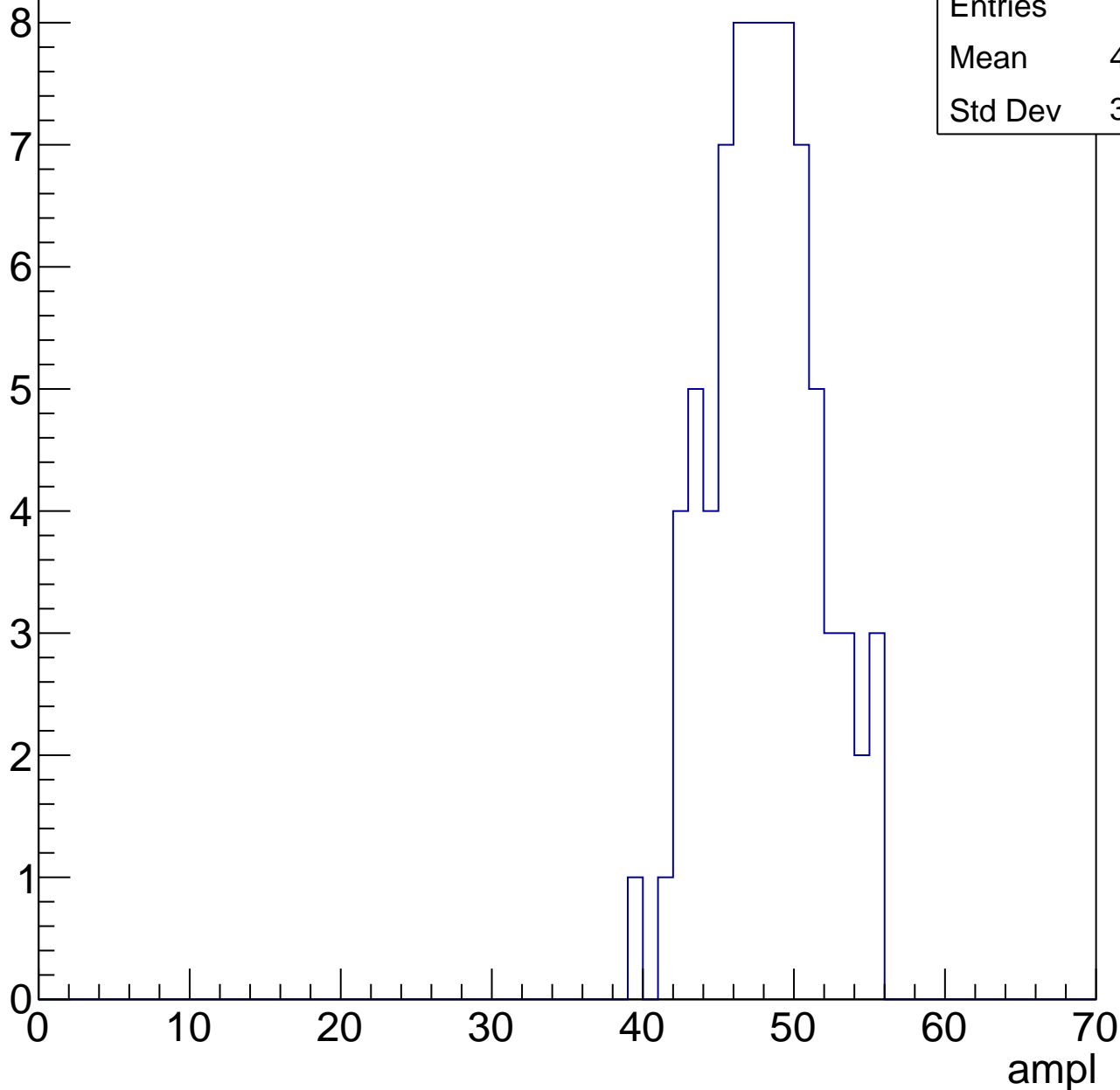


B1L003S, U19-ch42, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

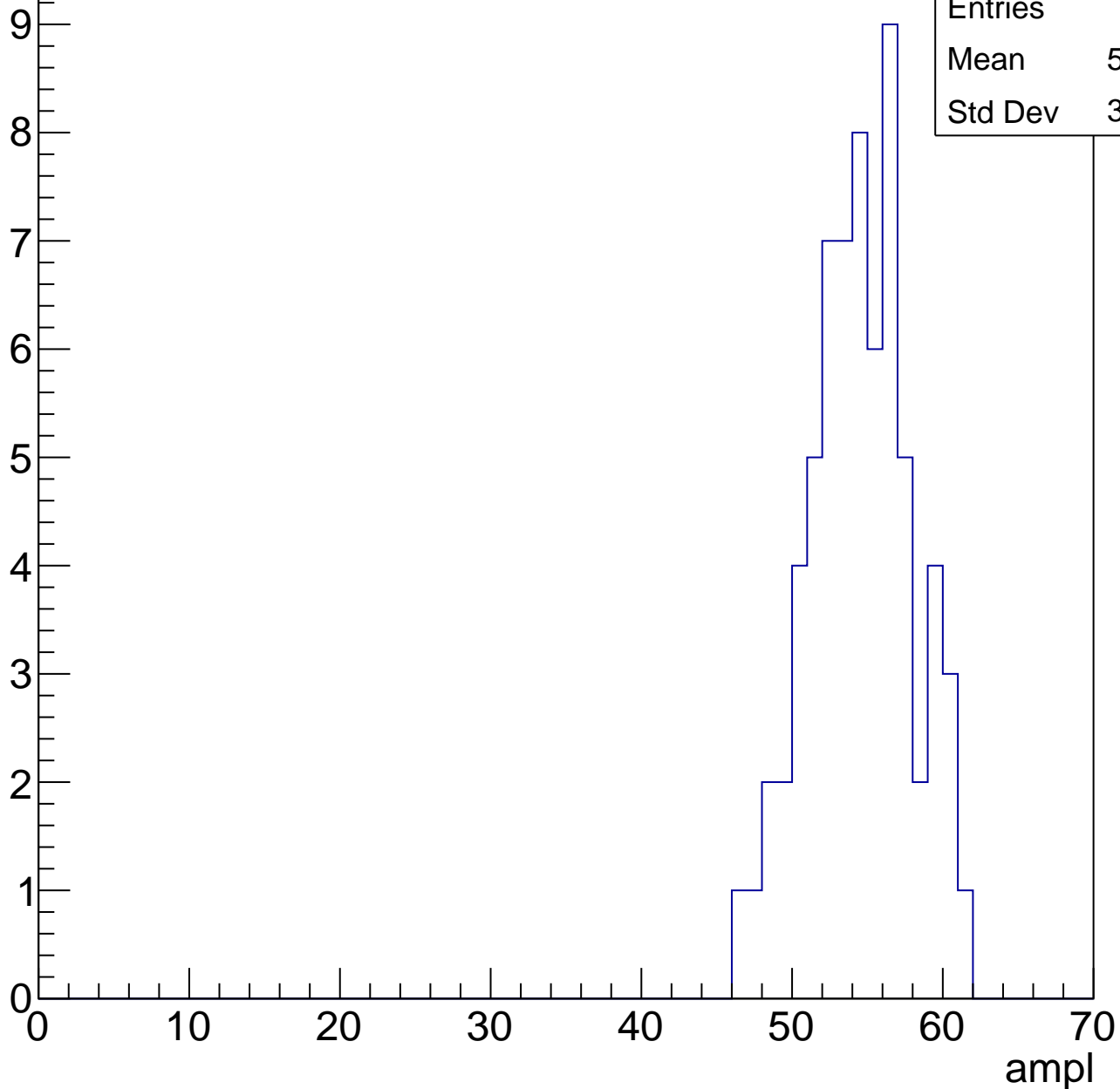
Entries	77
Mean	47.62
Std Dev	3.564



B1L003S, U19-ch42, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	67
Mean	54.04
Std Dev	3.357

B1L003S, U19-ch42, adc5

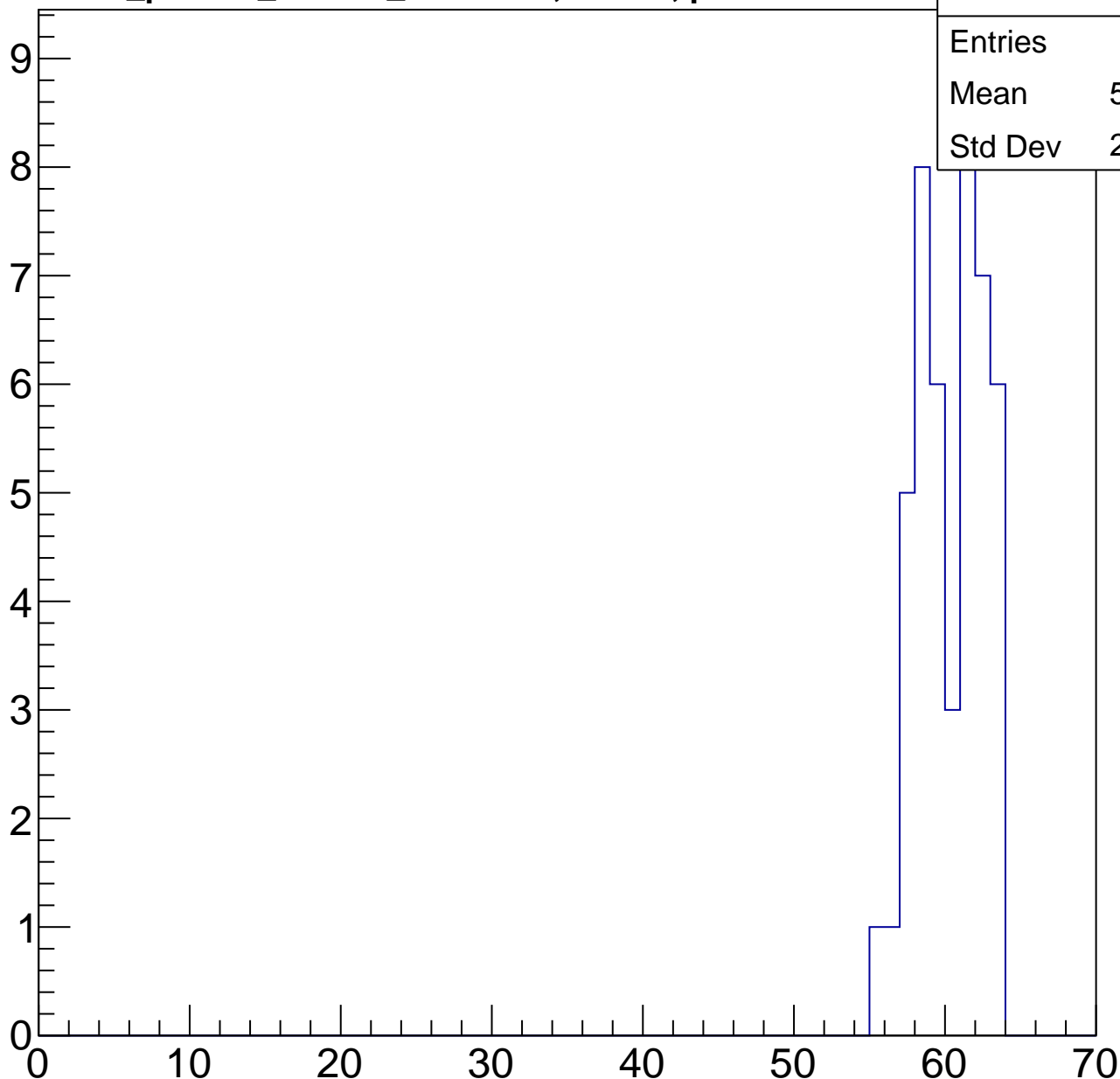
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.89
Std Dev	2.159

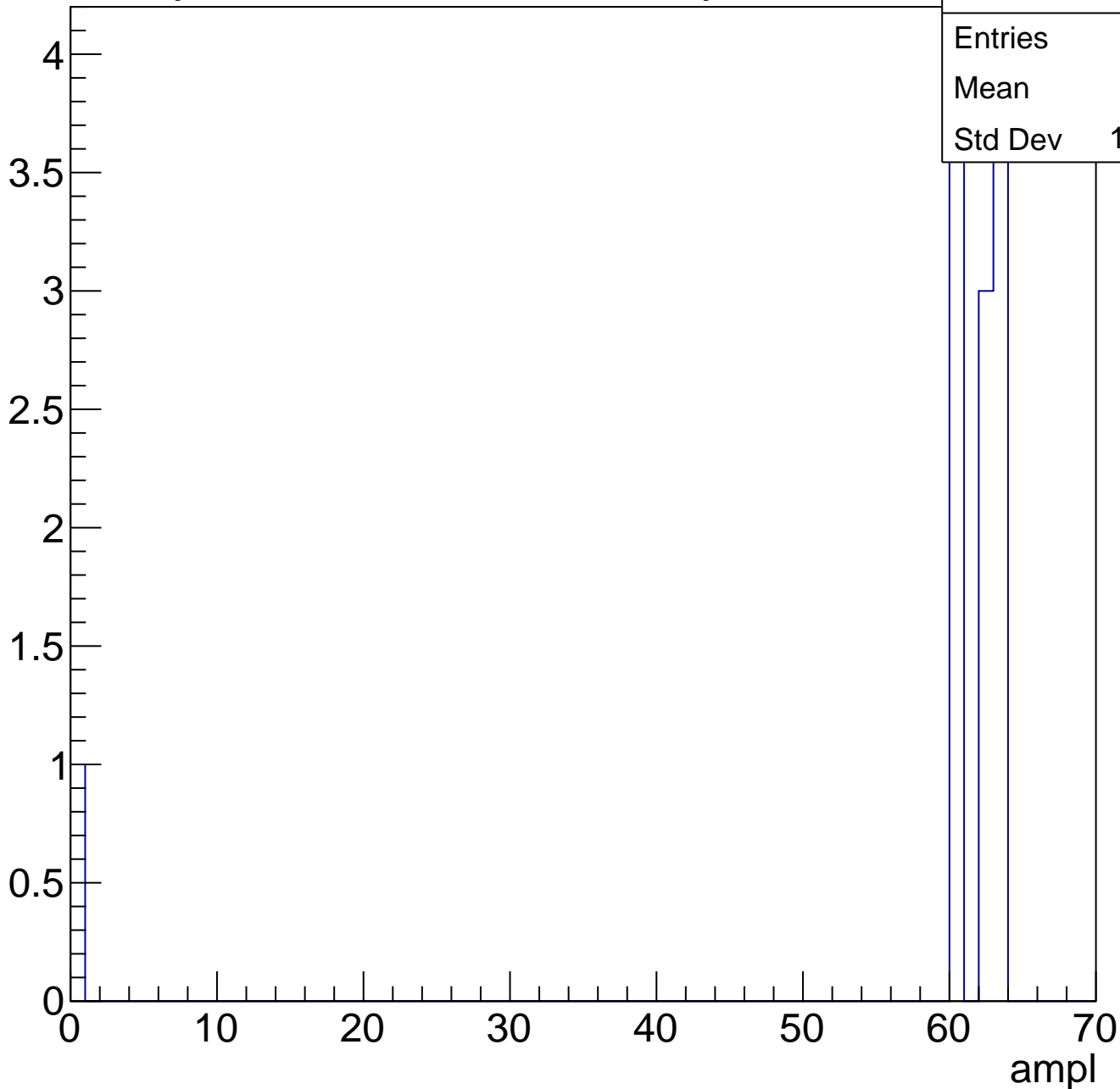
ampl



B1L003S, U19-ch42, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch42, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch43, adc0

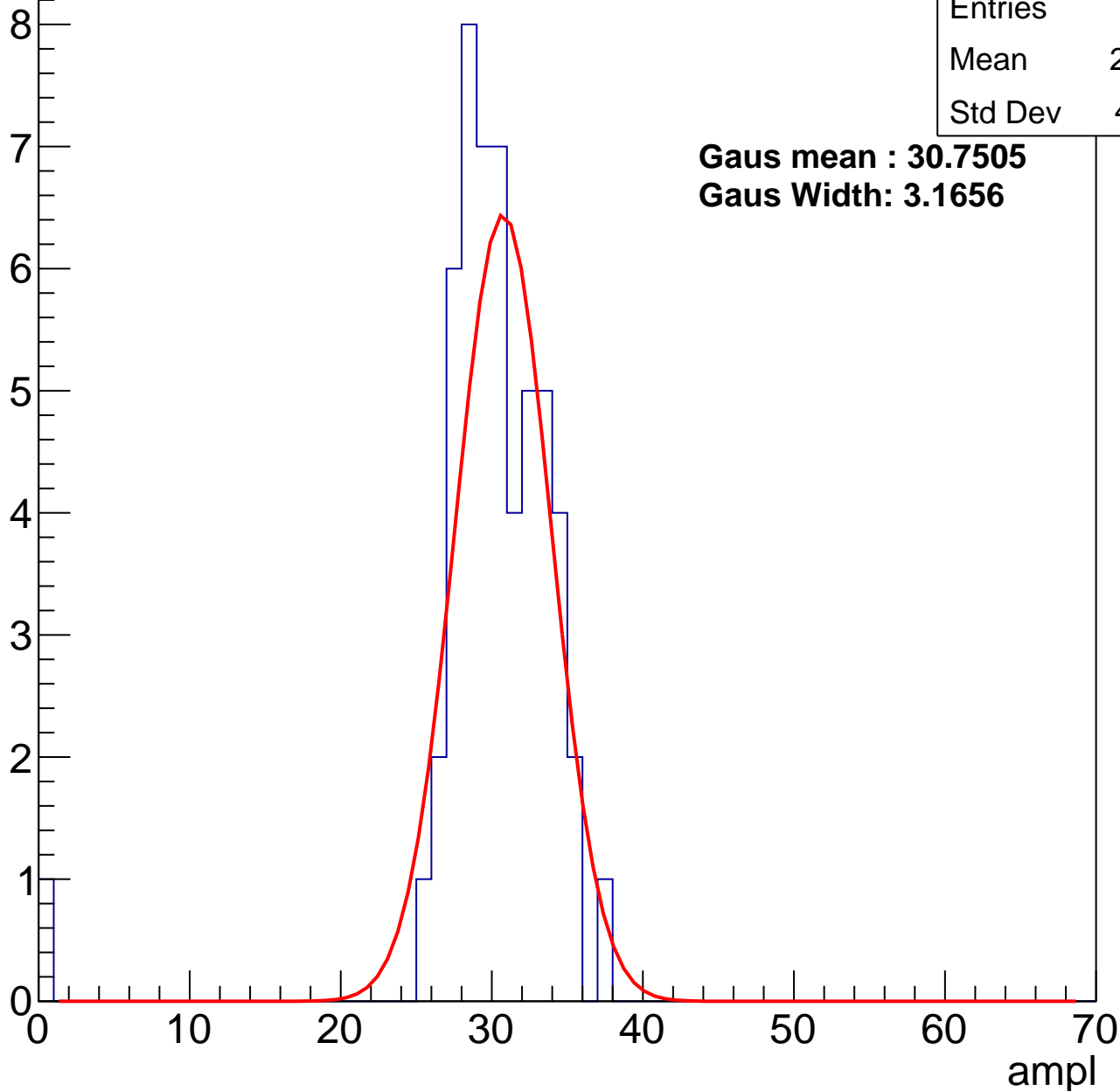
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	29.58
Std Dev	4.901

Gaus mean : 30.7505

Gaus Width: 3.1656



B1L003S, U19-ch43, adc1

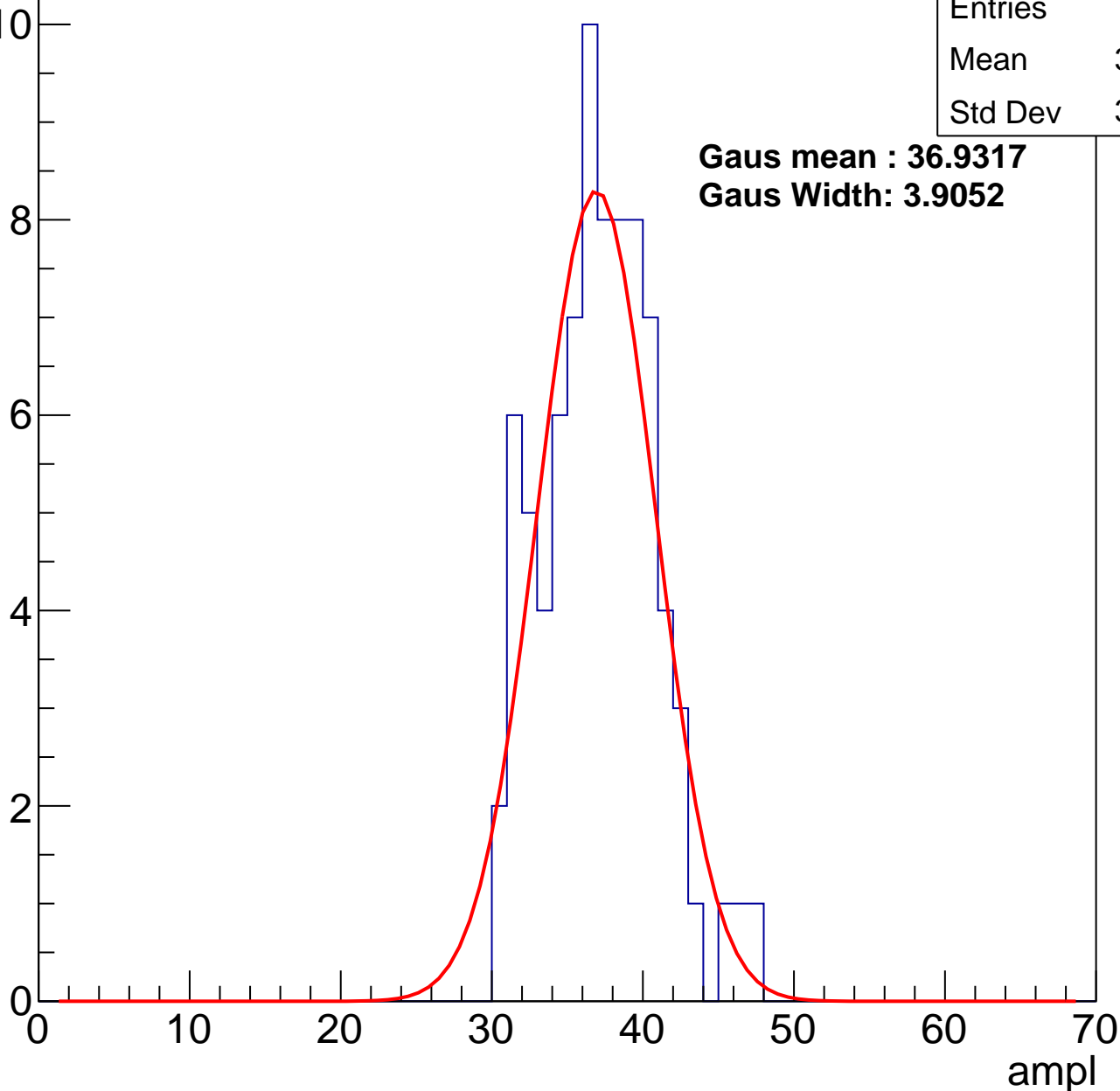
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	36.71
Std Dev	3.681

Gaus mean : 36.9317

Gaus Width: 3.9052



B1L003S, U19-ch43, adc2

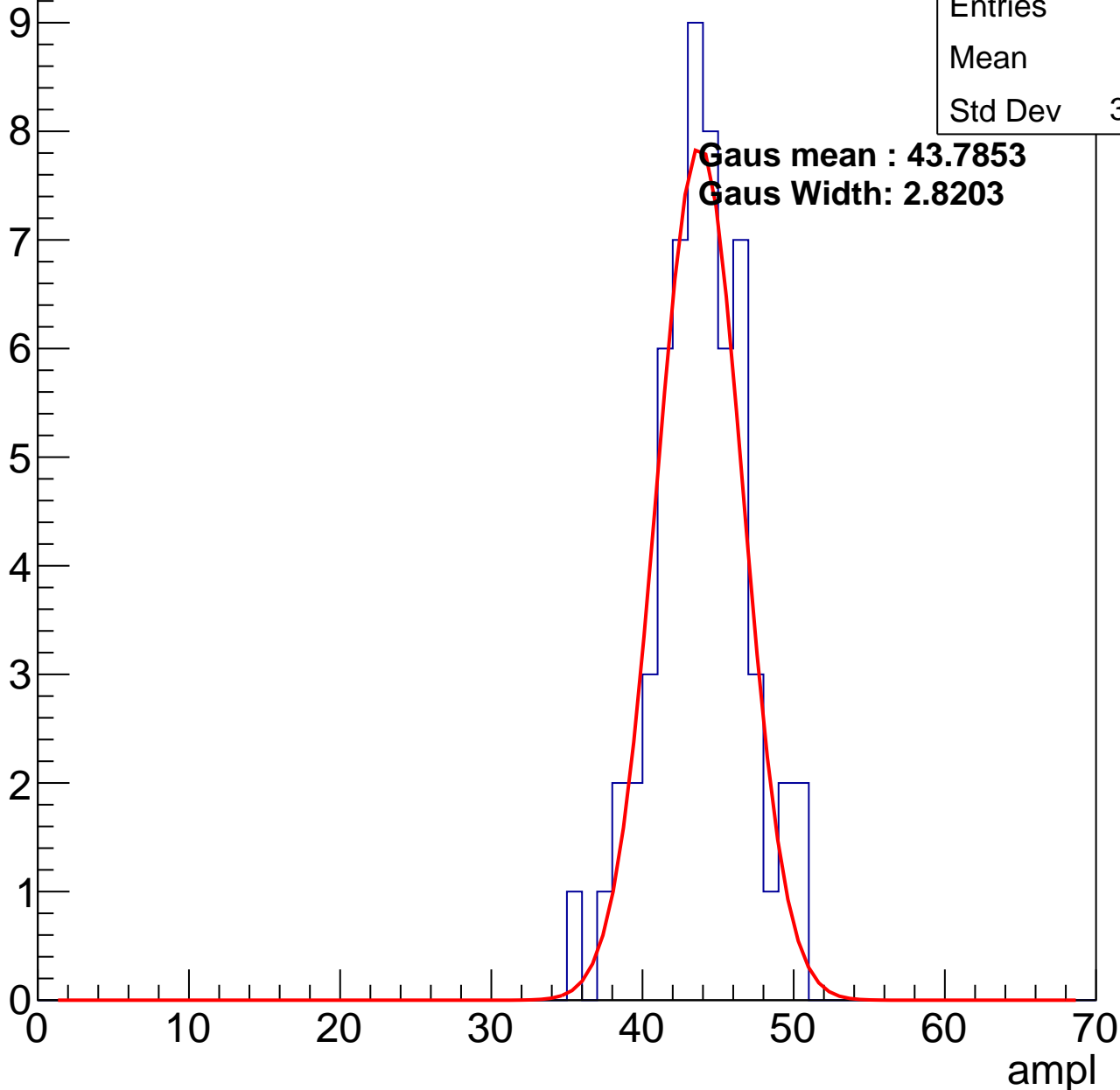
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	43.4
Std Dev	3.078

Gaus mean : 43.7853

Gaus Width: 2.8203

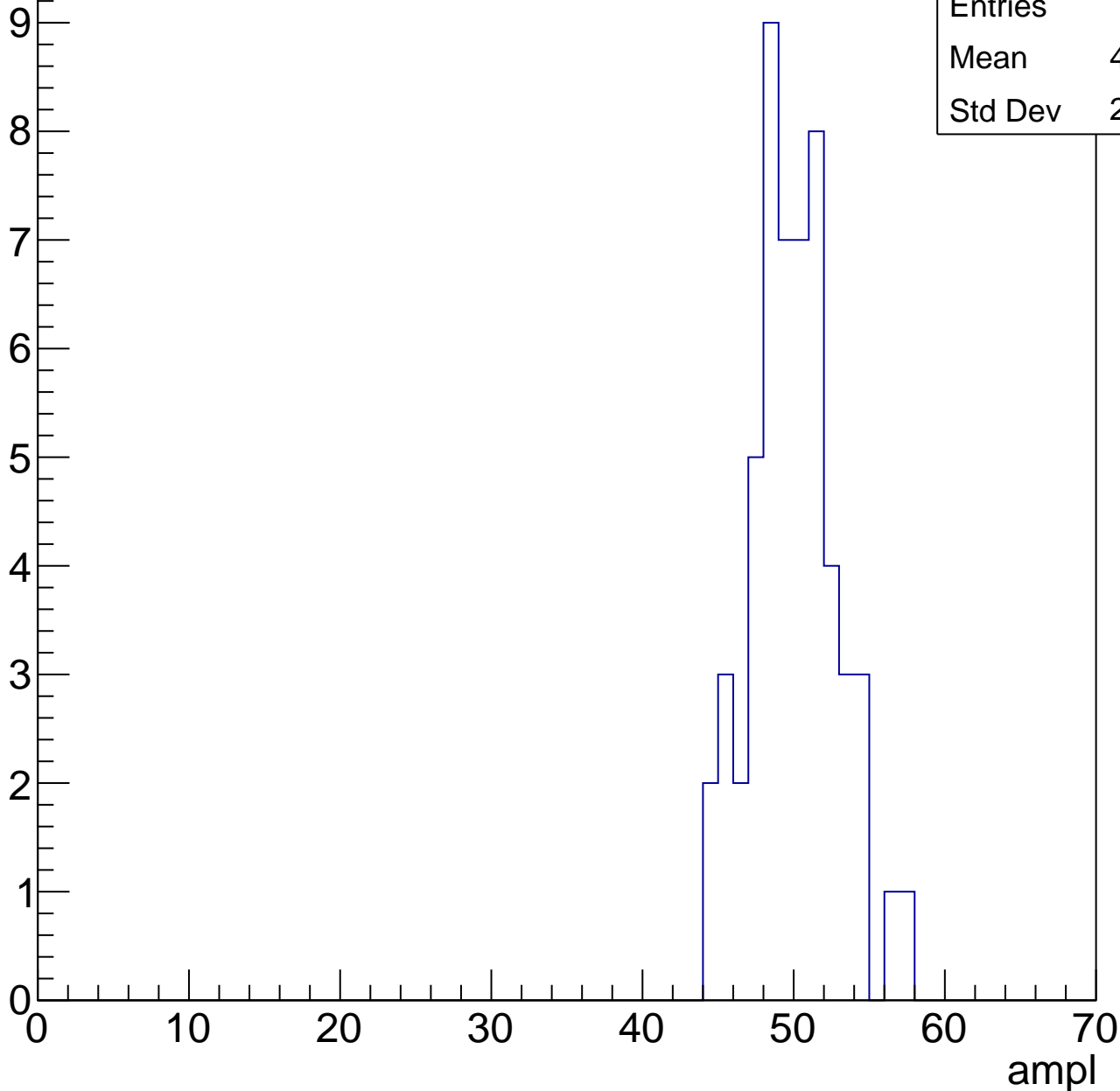


B1L003S, U19-ch43, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	49.55
Std Dev	2.827

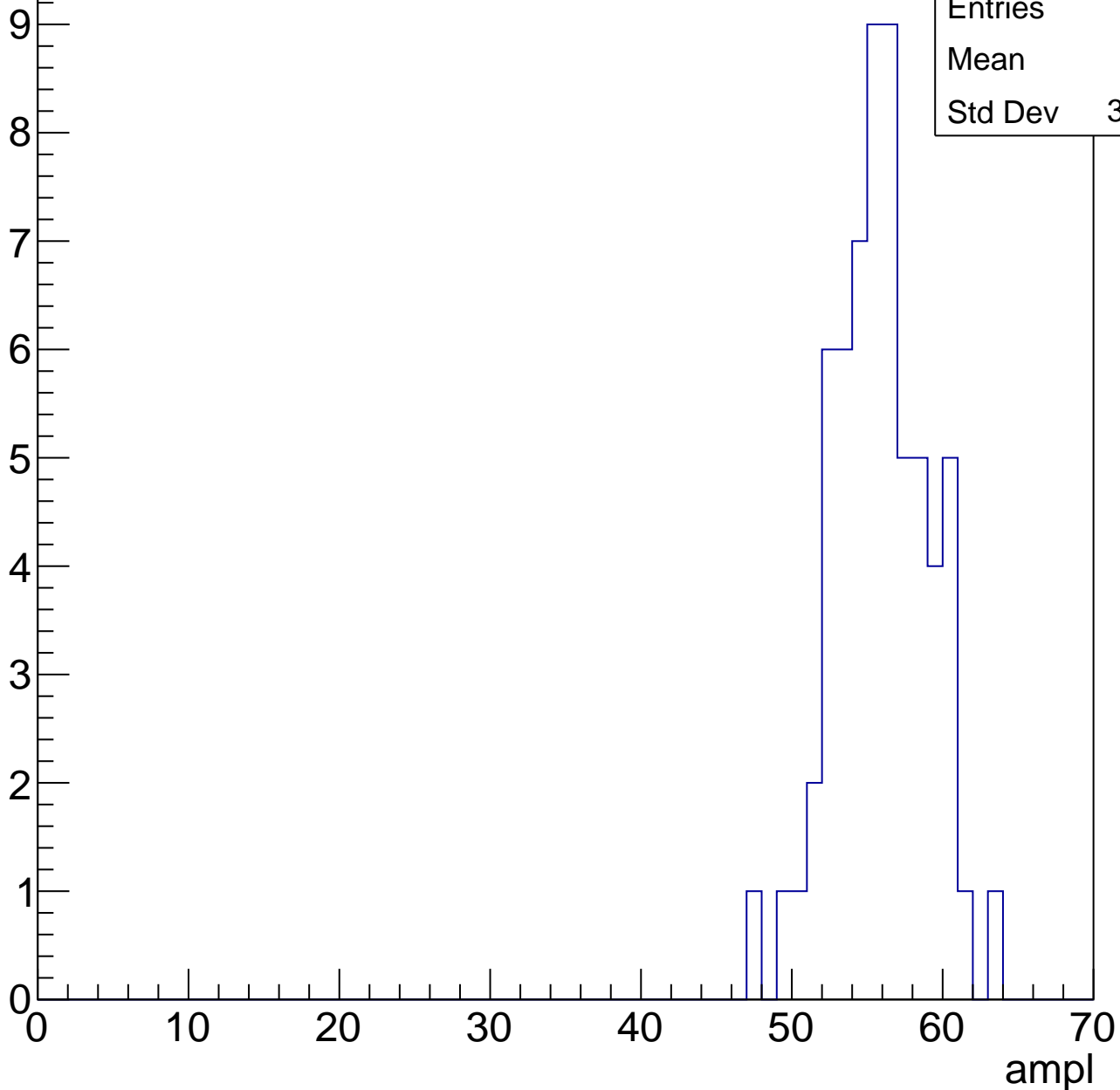


B1L003S, U19-ch43, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	55.4
Std Dev	3.073

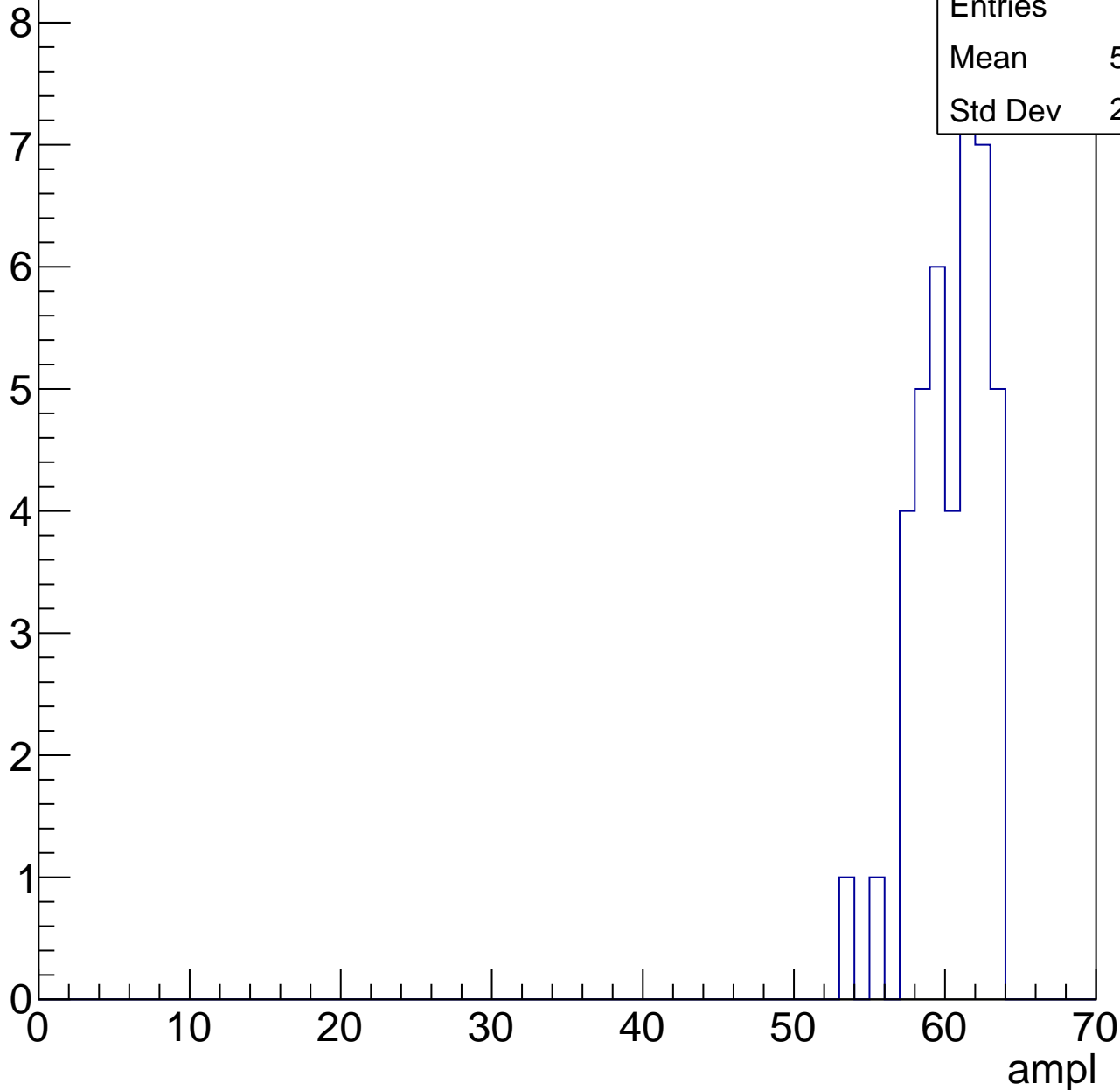


B1L003S, U19-ch43, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

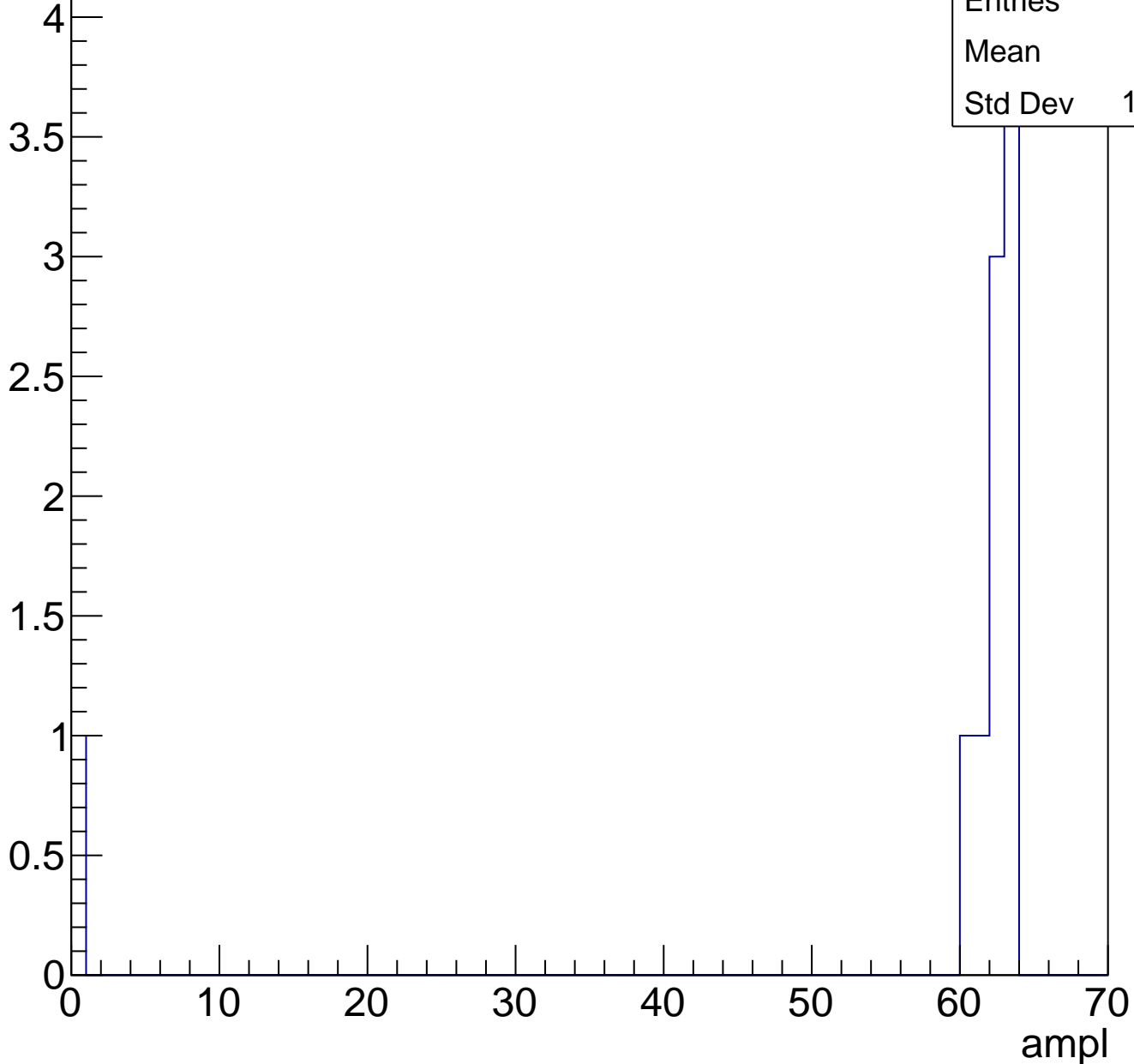
Entries	41
Mean	59.93
Std Dev	2.299



B1L003S, U19-ch43, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch43, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch44, adc0

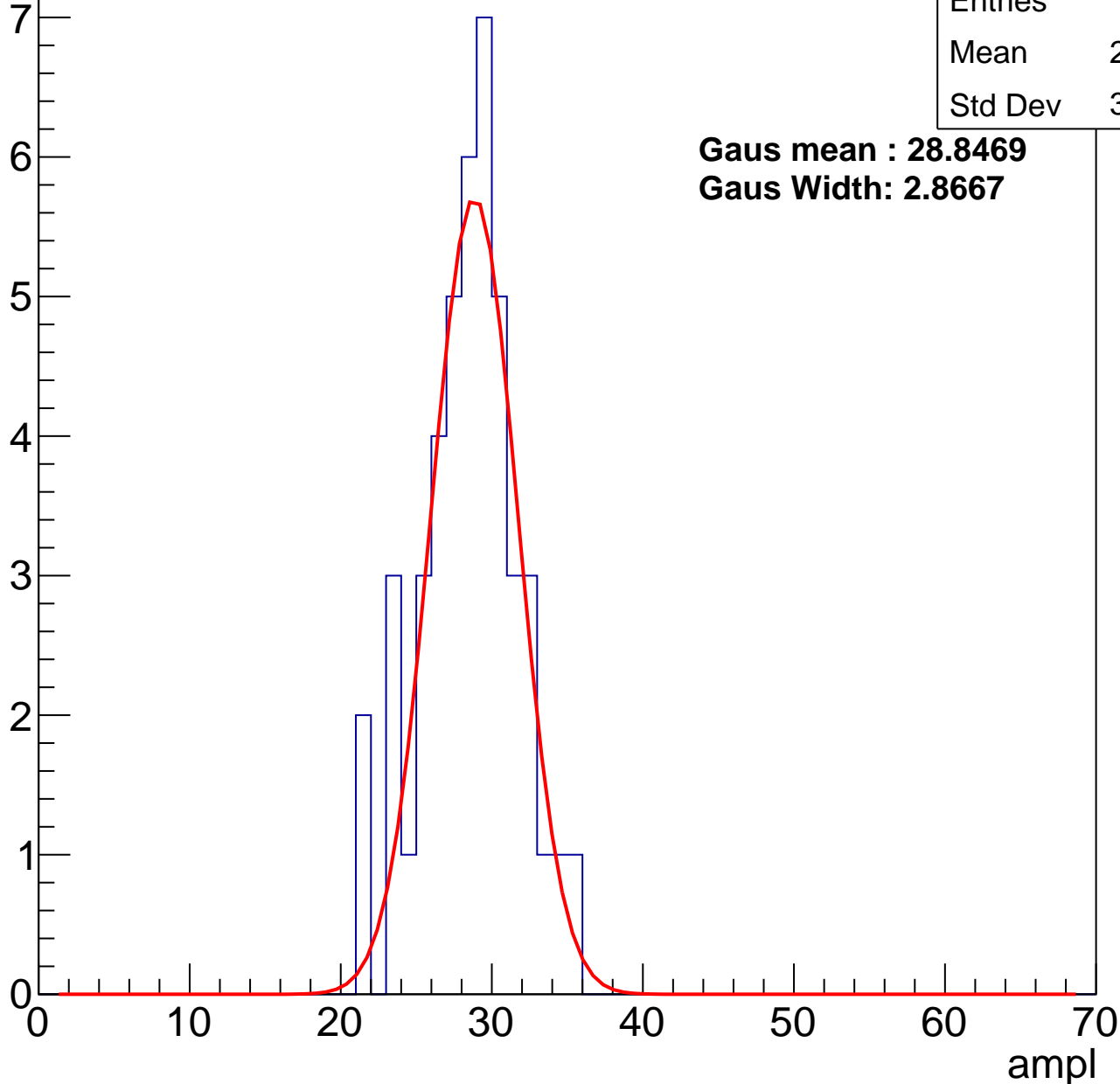
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	28.02
Std Dev	3.159

Gaus mean : 28.8469

Gaus Width: 2.8667



B1L003S, U19-ch44, adc1

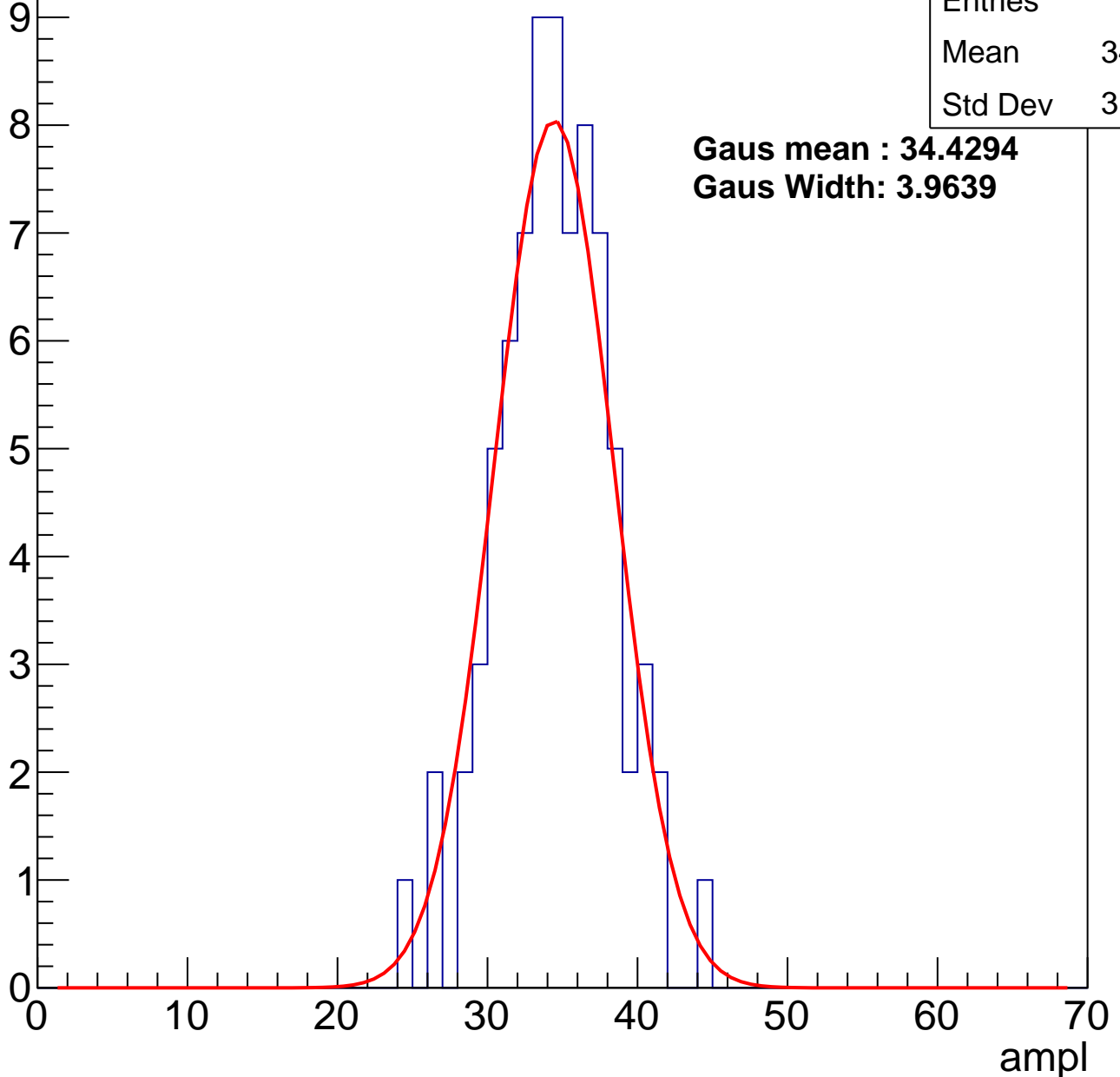
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	34.03
Std Dev	3.704

Gaus mean : 34.4294

Gaus Width: 3.9639



B1L003S, U19-ch44, adc2

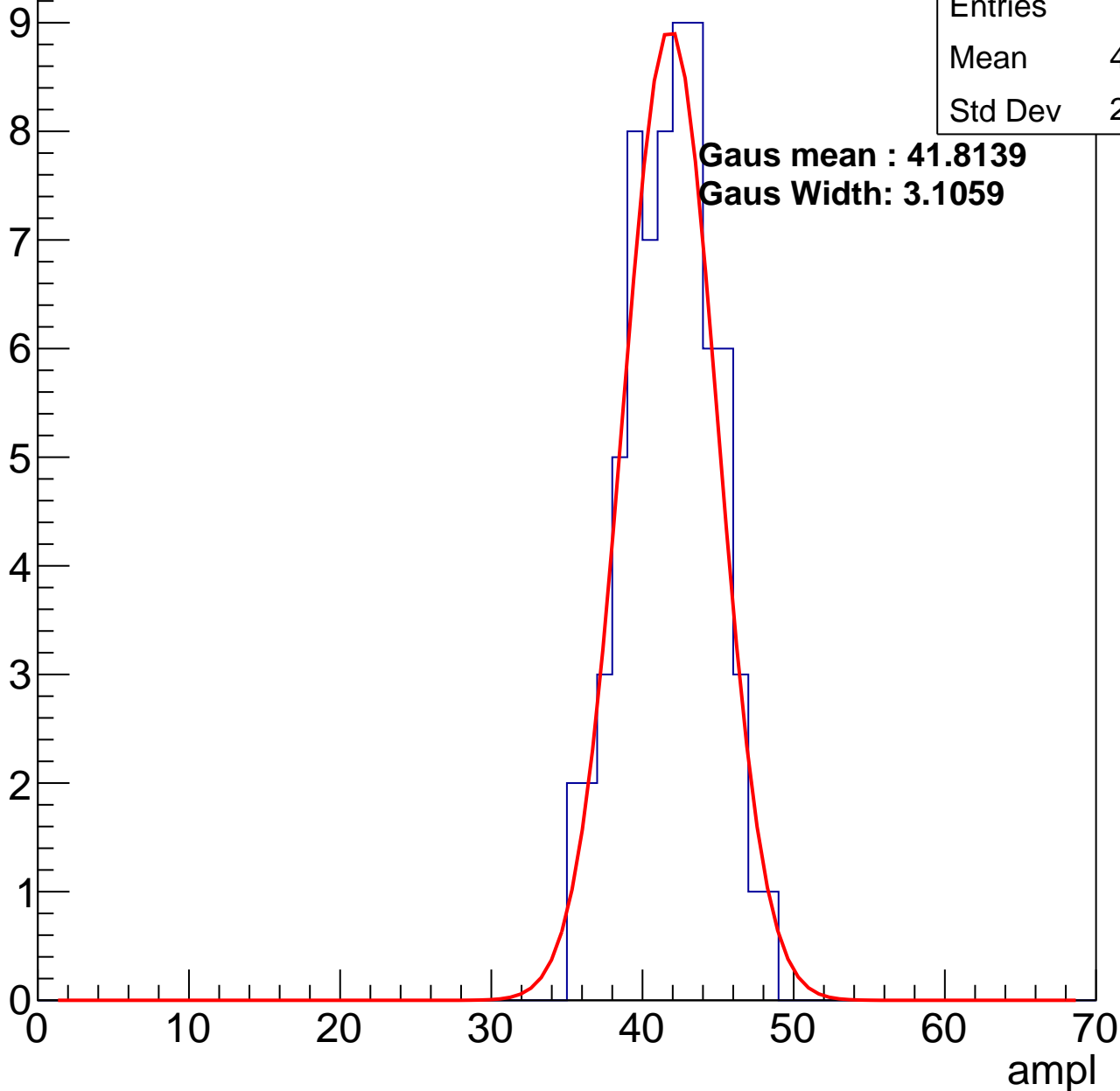
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	41.36
Std Dev	2.928

Gaus mean : 41.8139

Gaus Width: 3.1059

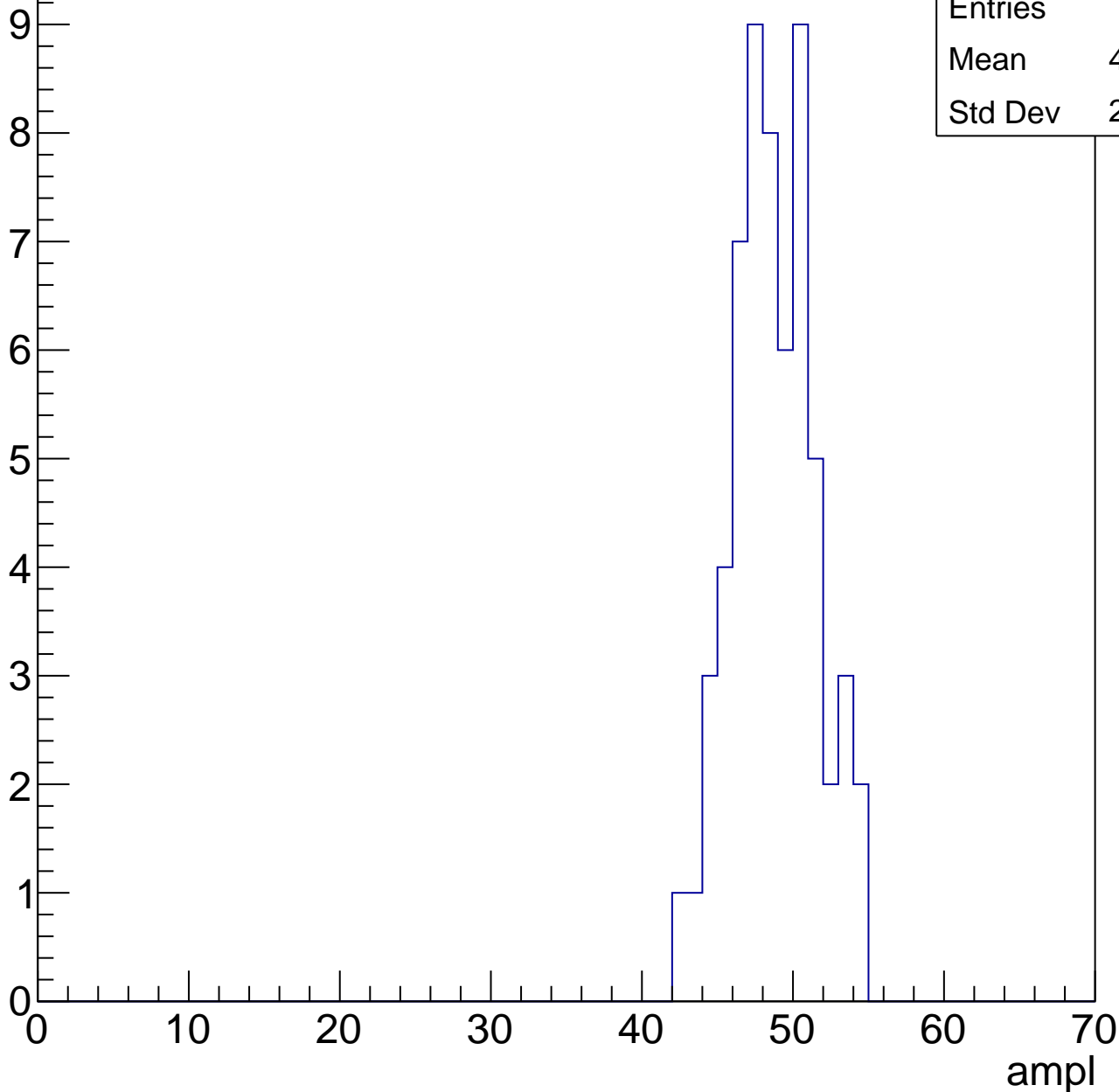


B1L003S, U19-ch44, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.27
Std Dev	2.719

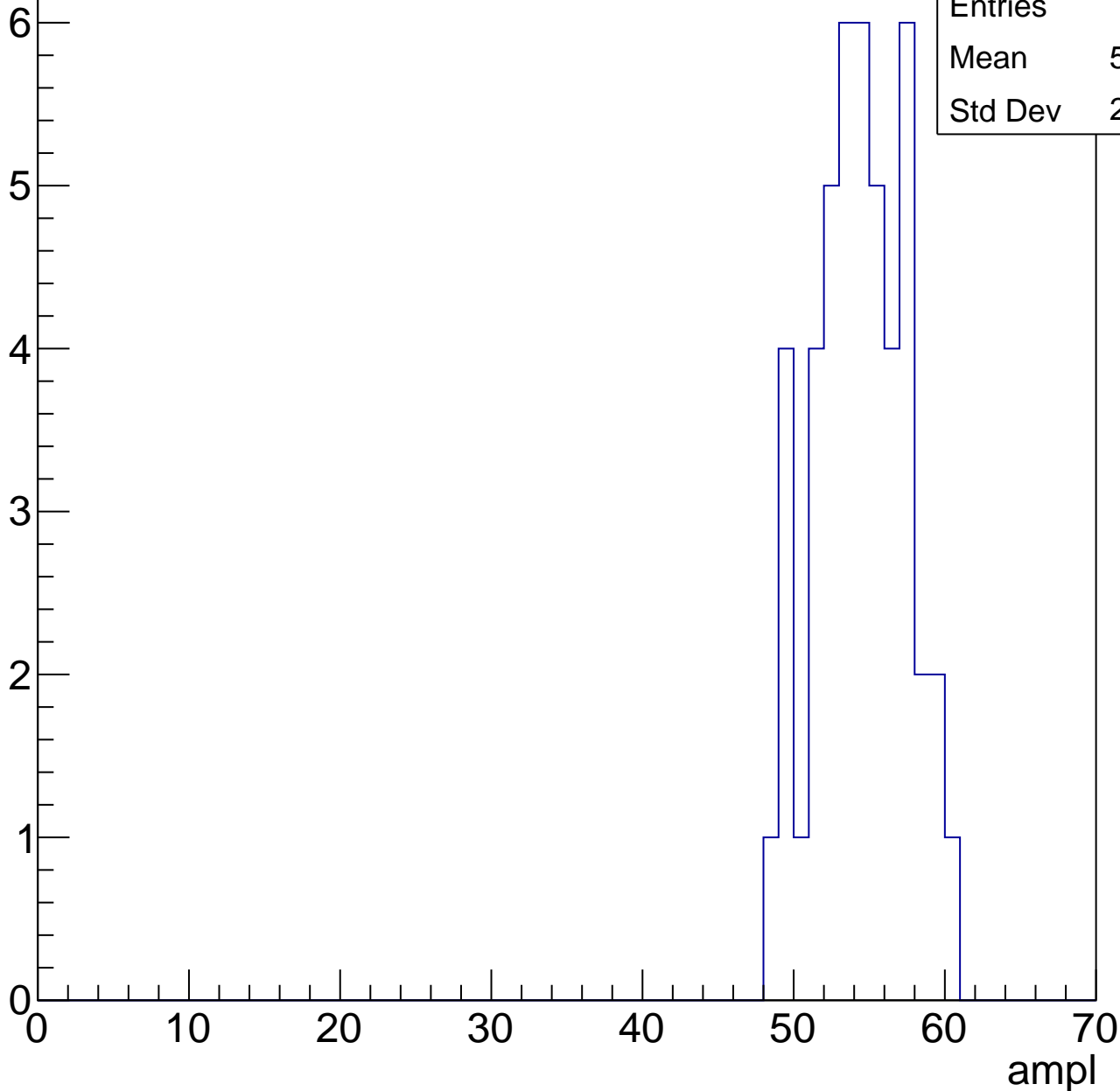


B1L003S, U19-ch44, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	53.94
Std Dev	2.942

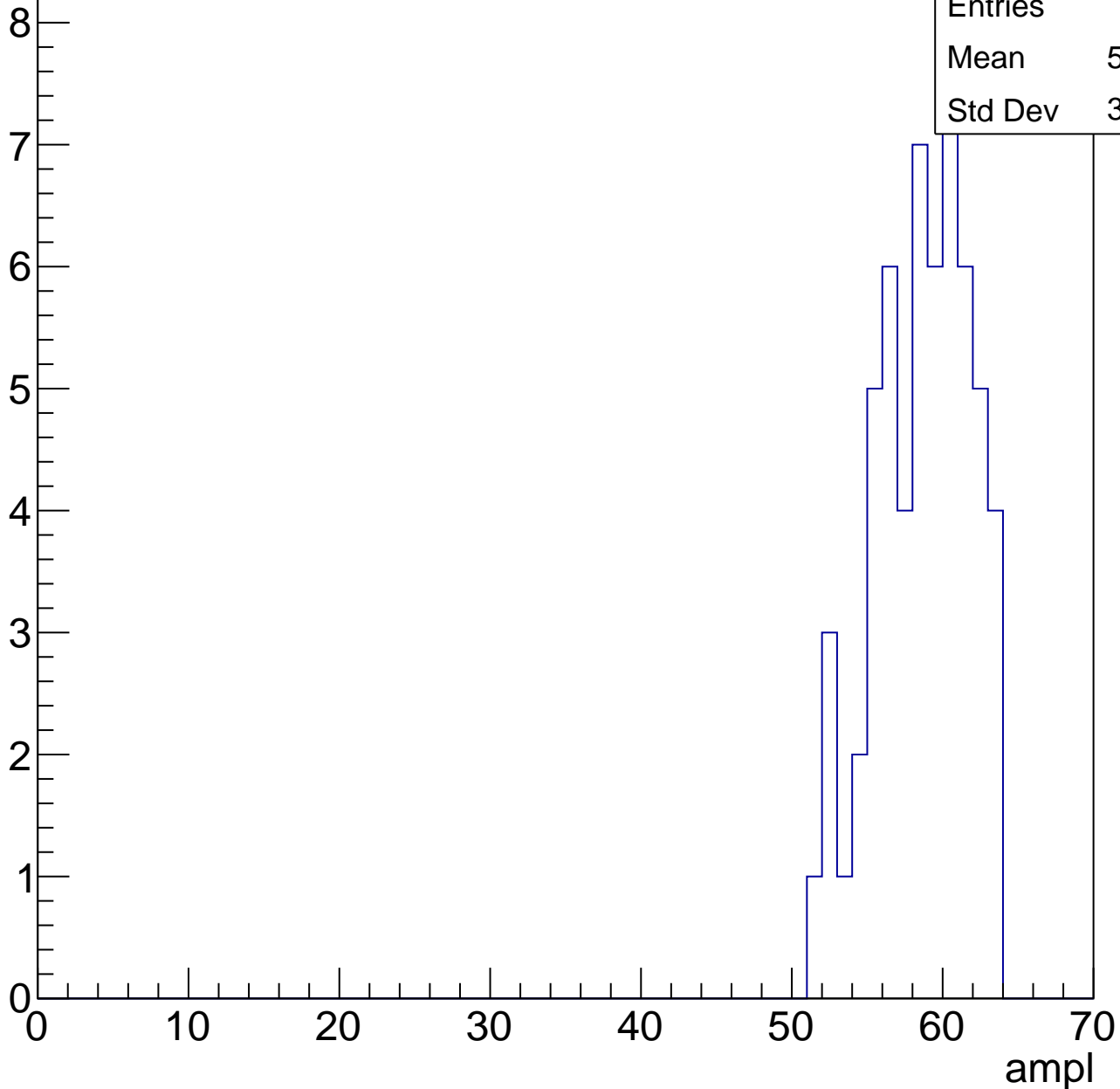


B1L003S, U19-ch44, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

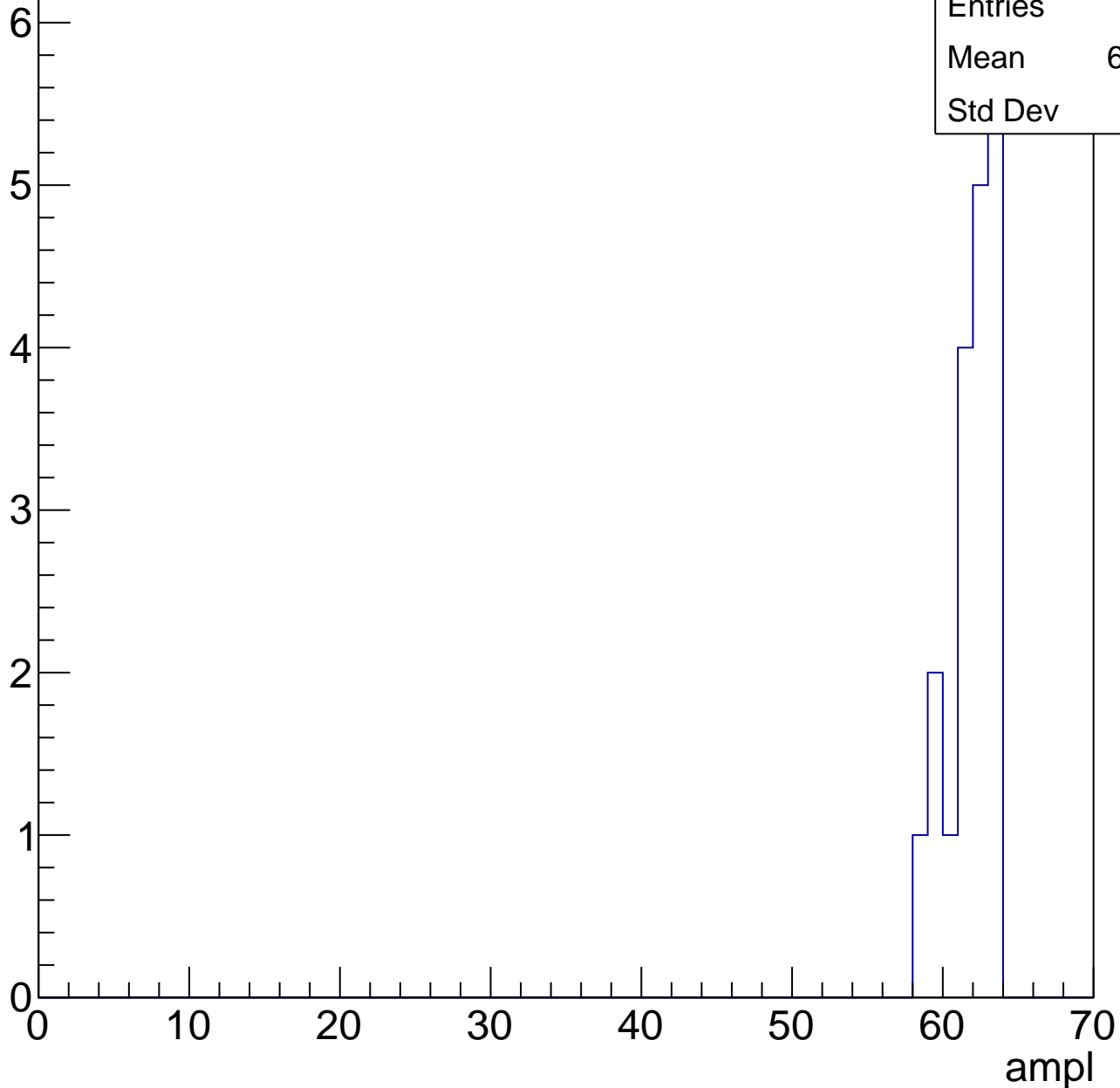
Entries	58
Mean	58.19
Std Dev	3.099



B1L003S, U19-ch44, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch44, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch45, adc0

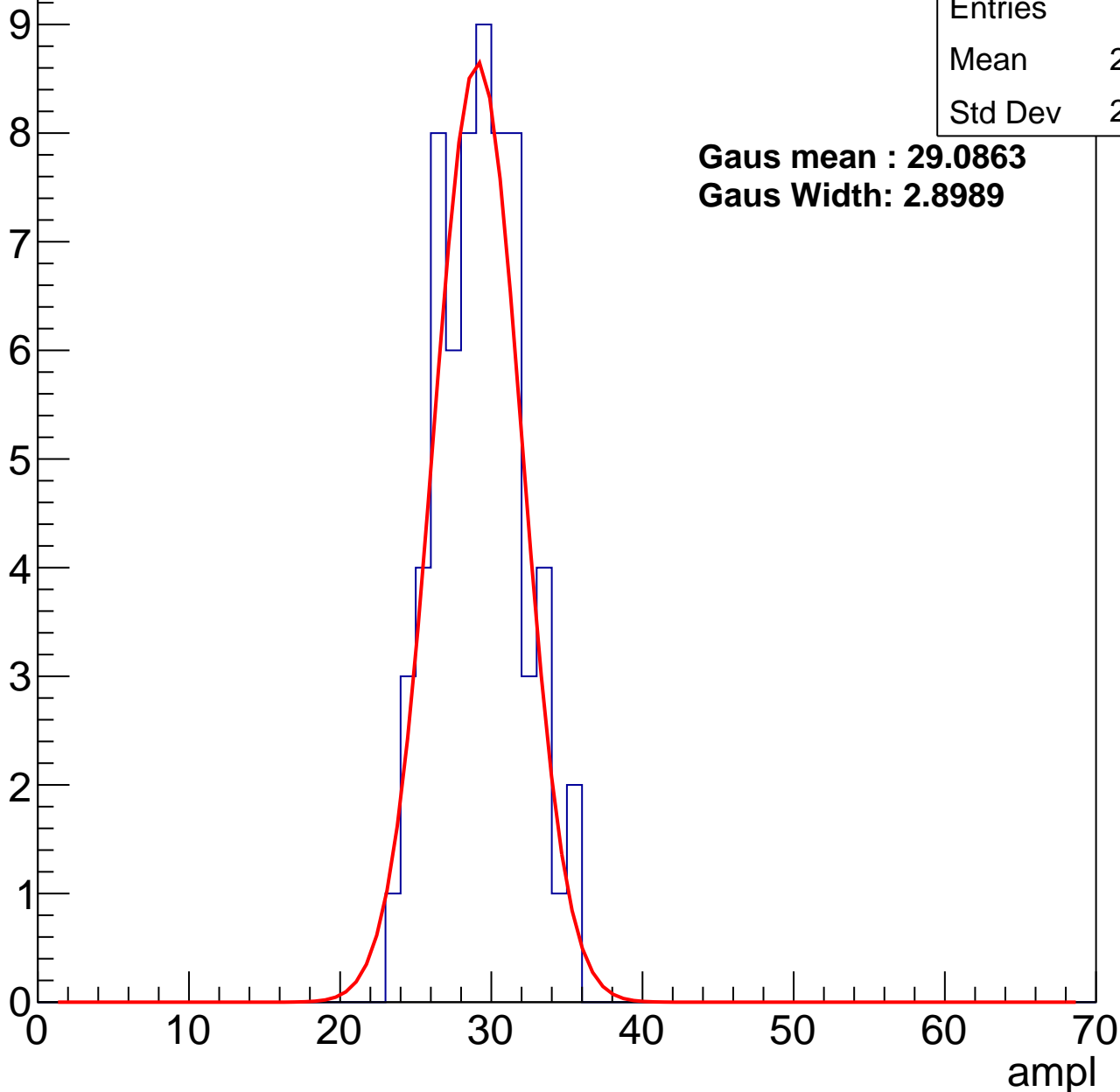
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.77
Std Dev	2.783

Gaus mean : 29.0863

Gaus Width: 2.8989



B1L003S, U19-ch45, adc1

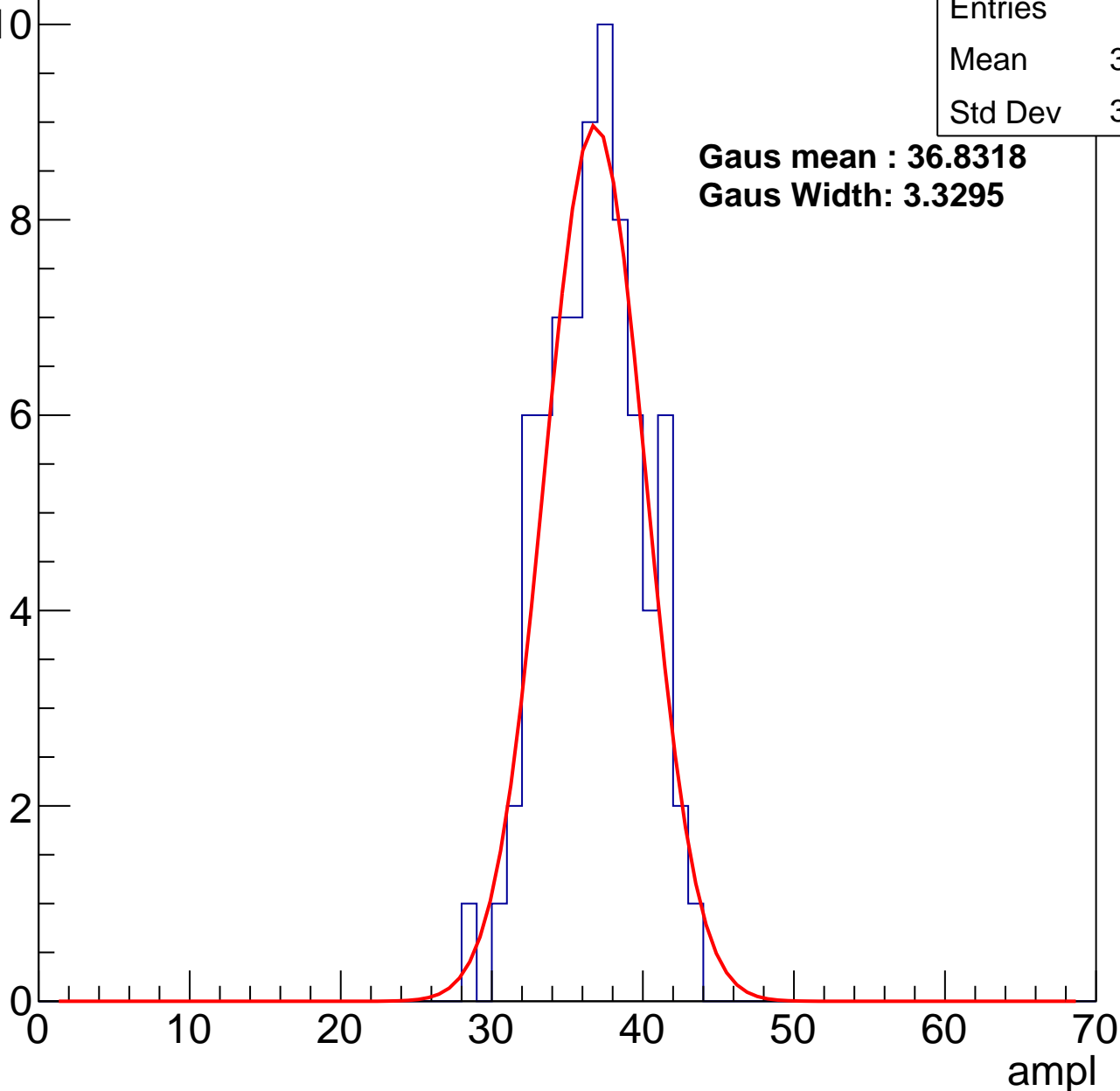
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	36.29
Std Dev	3.162

Gaus mean : 36.8318

Gaus Width: 3.3295



B1L003S, U19-ch45, adc2

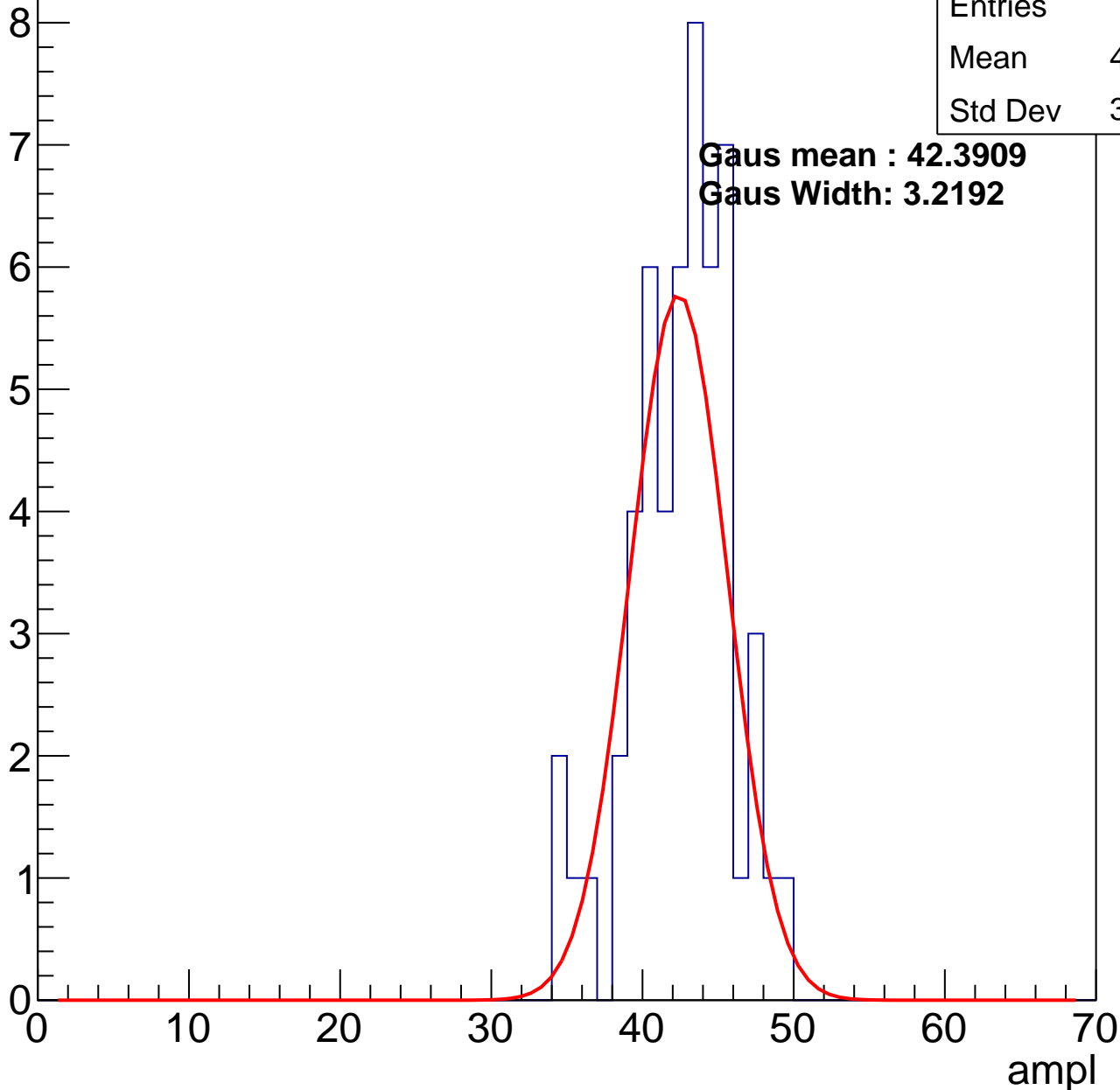
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.15
Std Dev	3.305

Gaus mean : 42.3909

Gaus Width: 3.2192

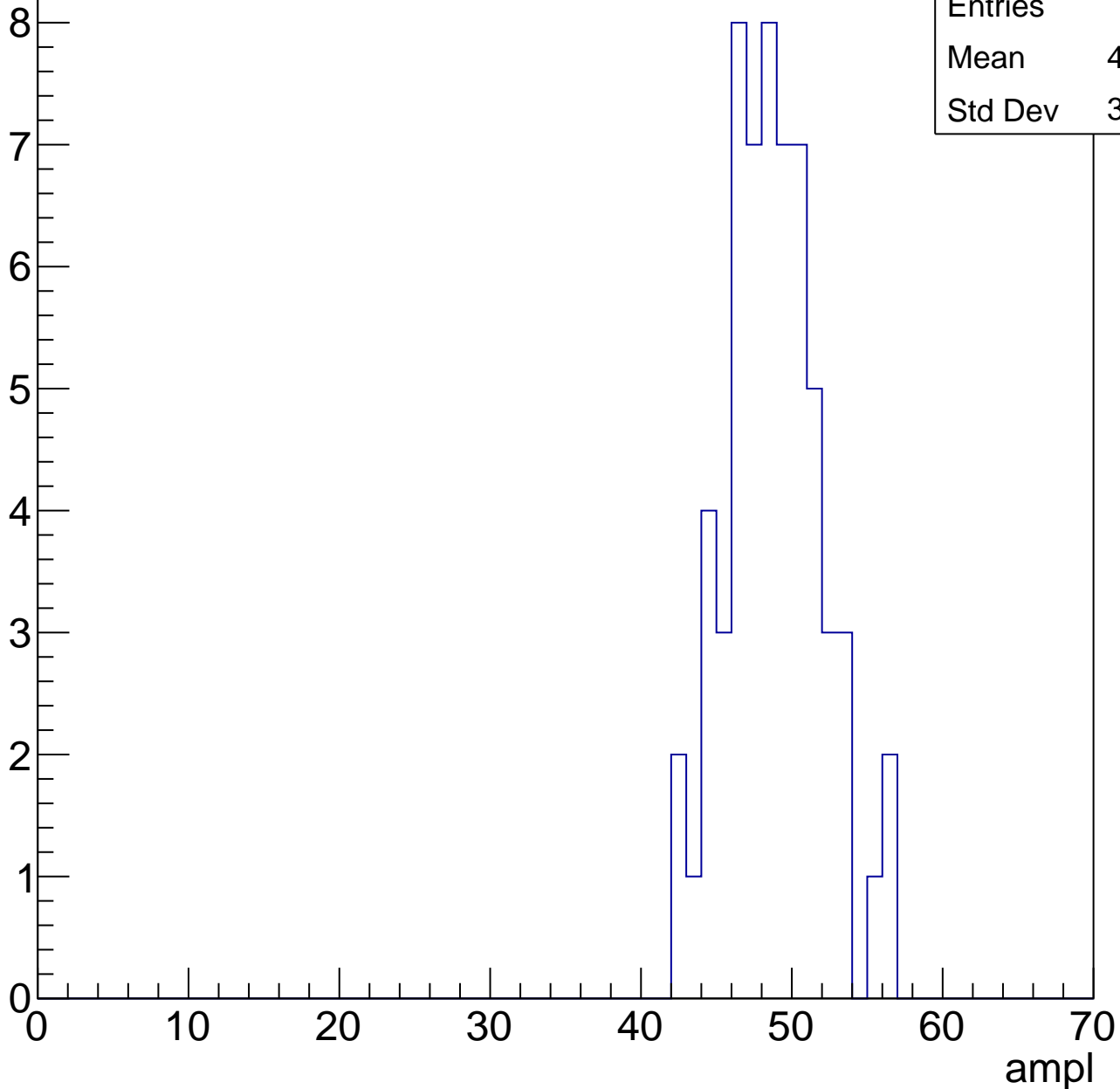


B1L003S, U19-ch45, adc3

calib_packv5_042523_0143.root, FC#13, port D2

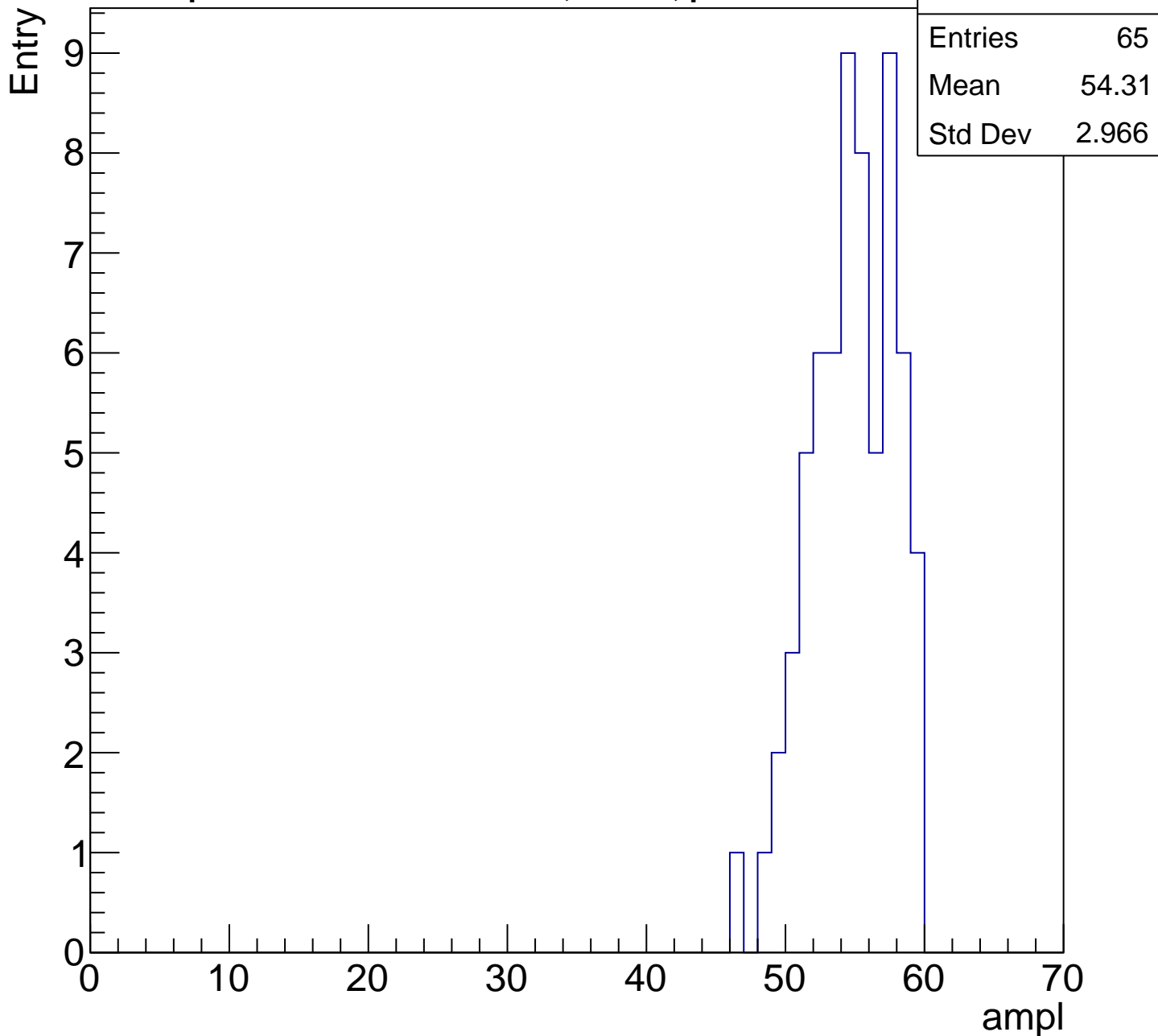
Entry

Entries	61
Mean	48.34
Std Dev	3.136



B1L003S, U19-ch45, adc4

calib_packv5_042523_0143.root, FC#13, port D2

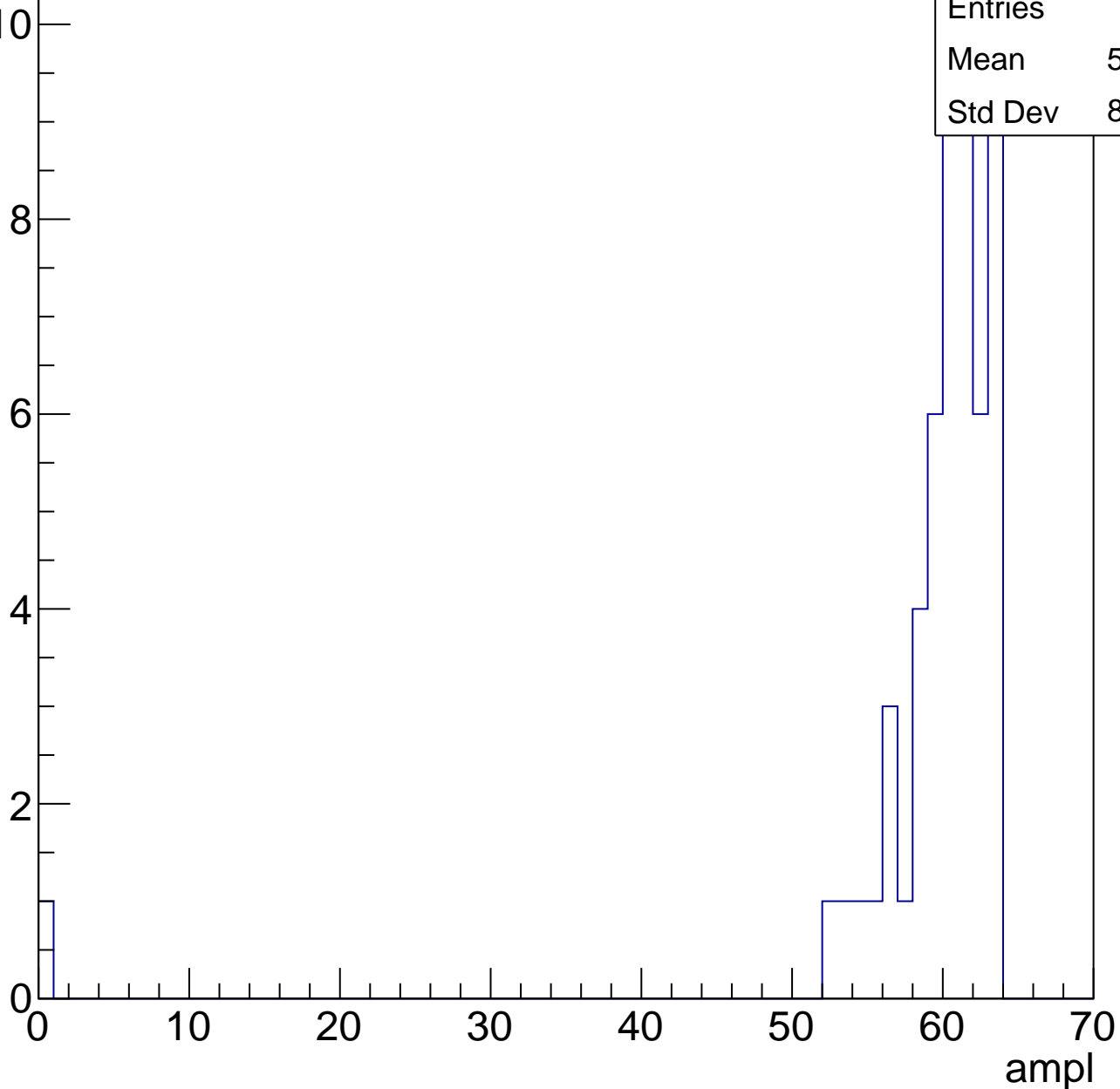


B1L003S, U19-ch45, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

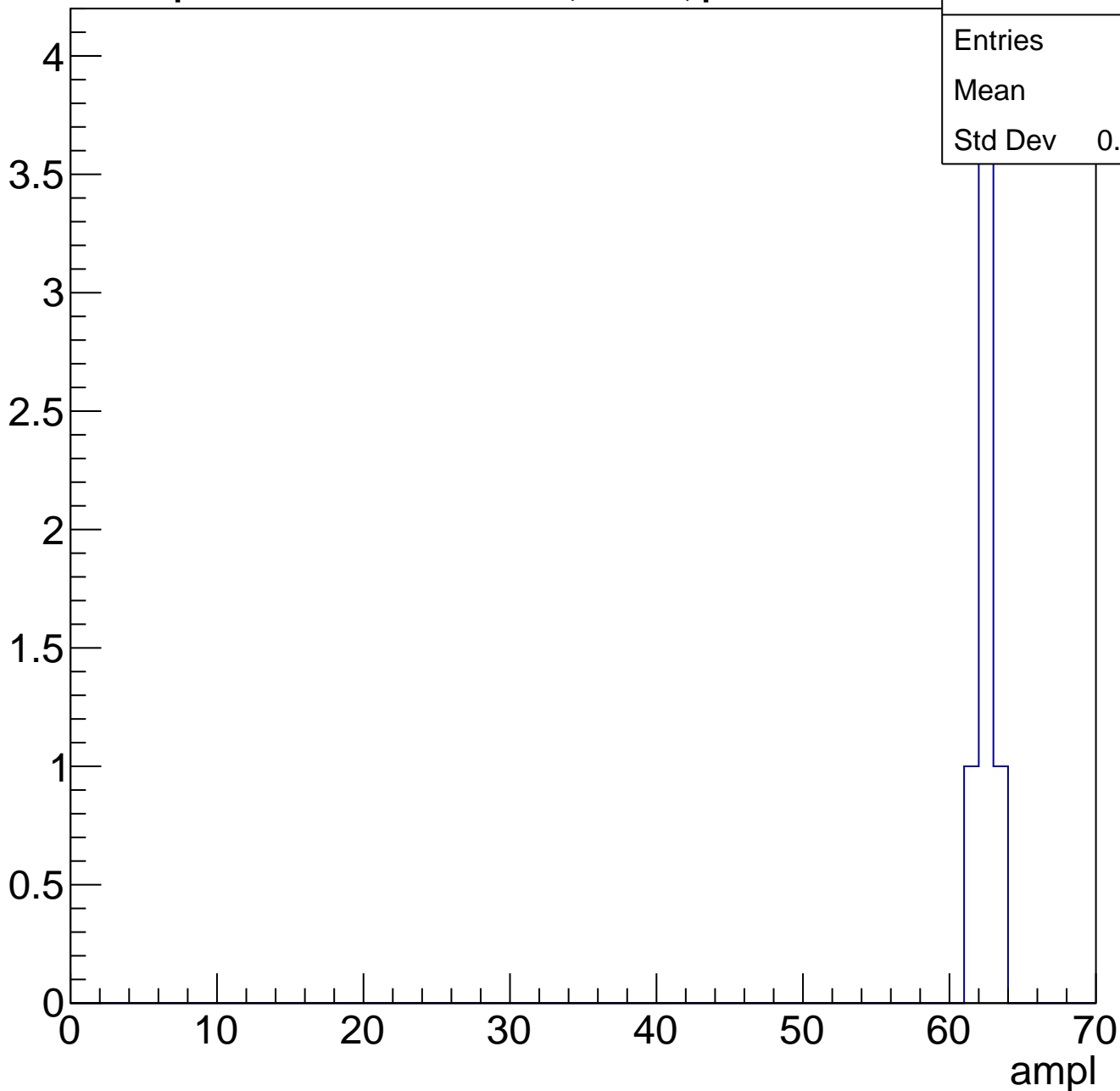
Entries	53
Mean	58.74
Std Dev	8.559



B1L003S, U19-ch45, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch45, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch46, adc0

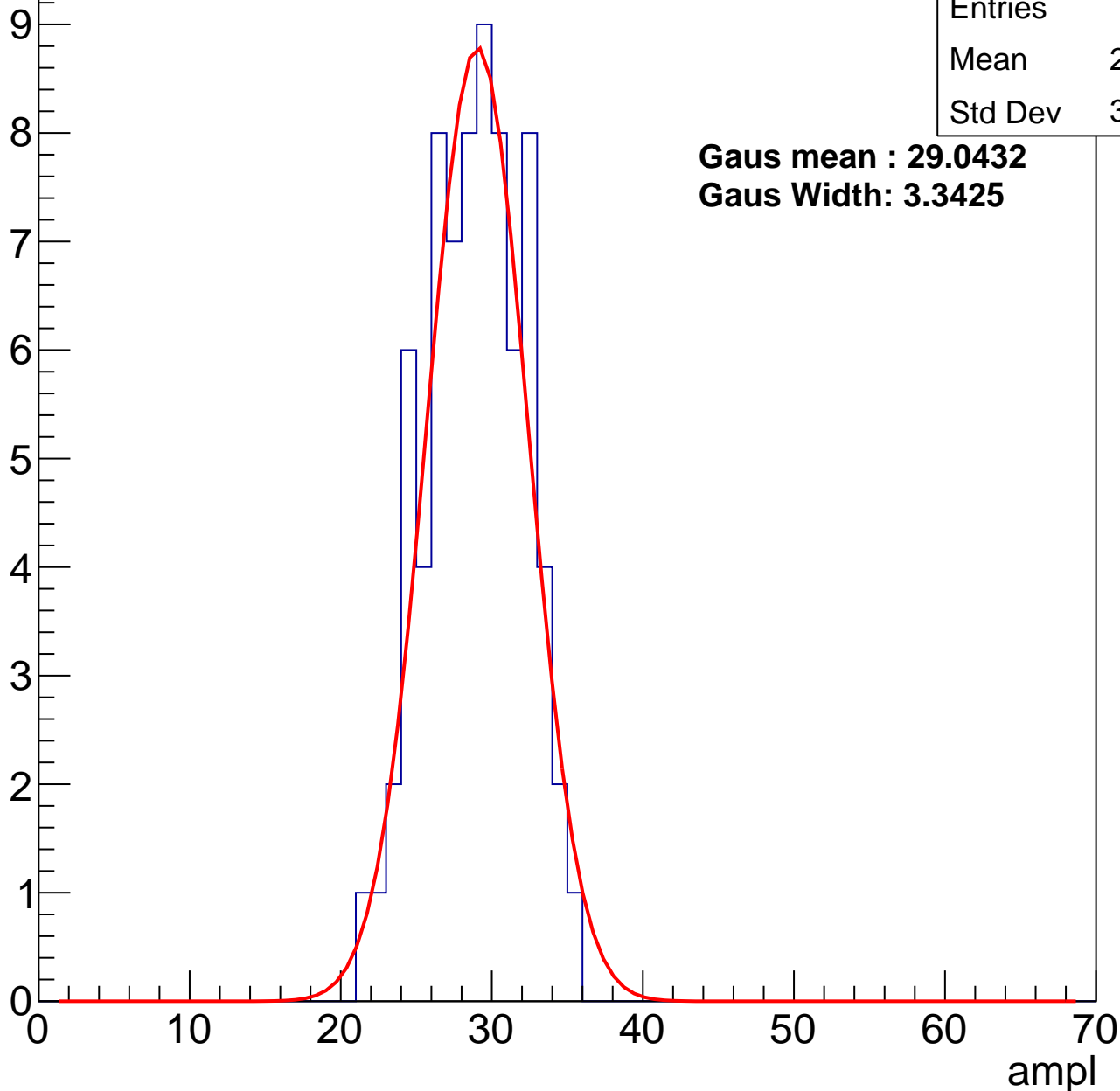
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	28.43
Std Dev	3.133

Gaus mean : 29.0432

Gaus Width: 3.3425



B1L003S, U19-ch46, adc1

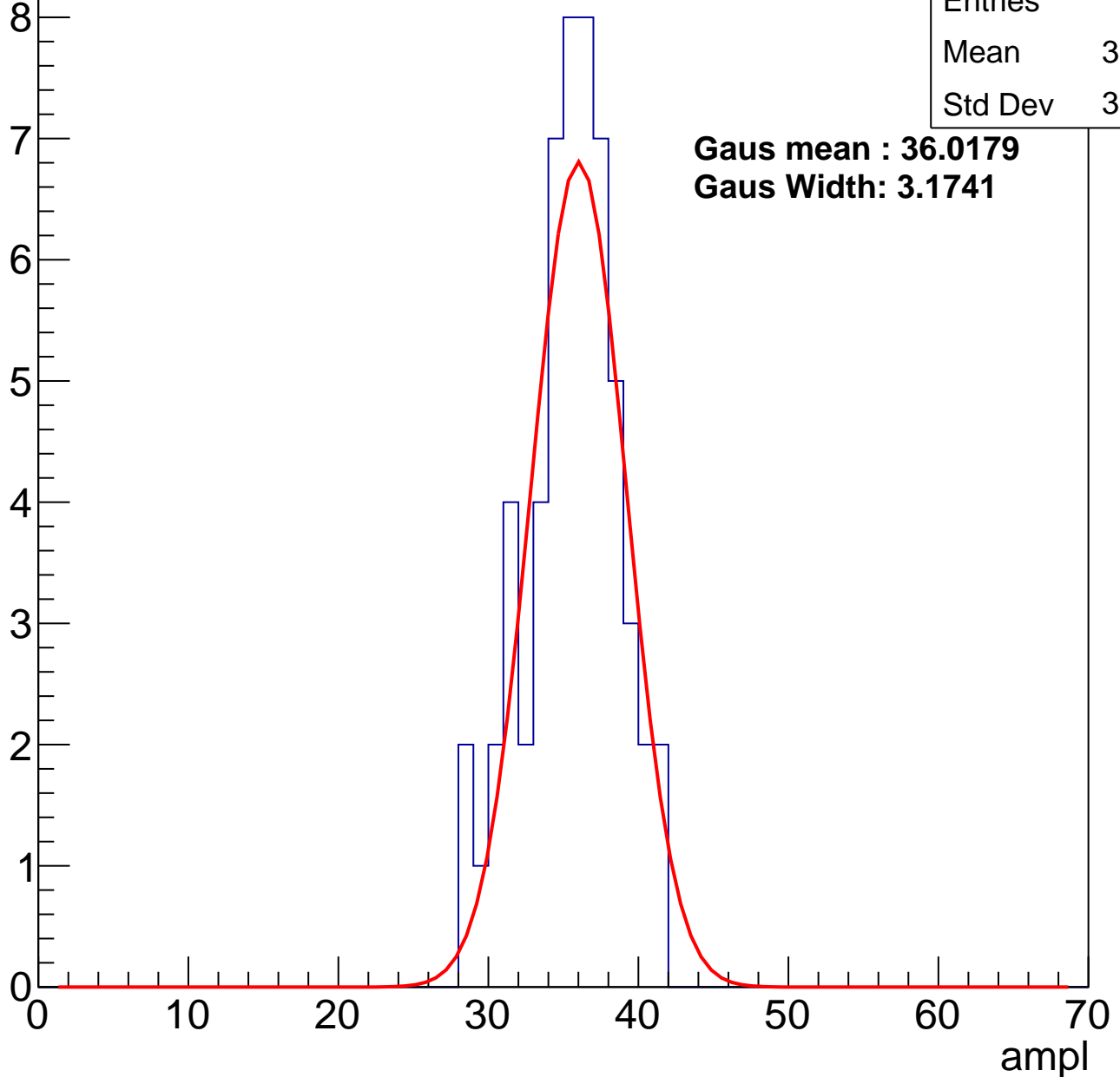
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.07
Std Dev	3.077

Gaus mean : 36.0179

Gaus Width: 3.1741



B1L003S, U19-ch46, adc2

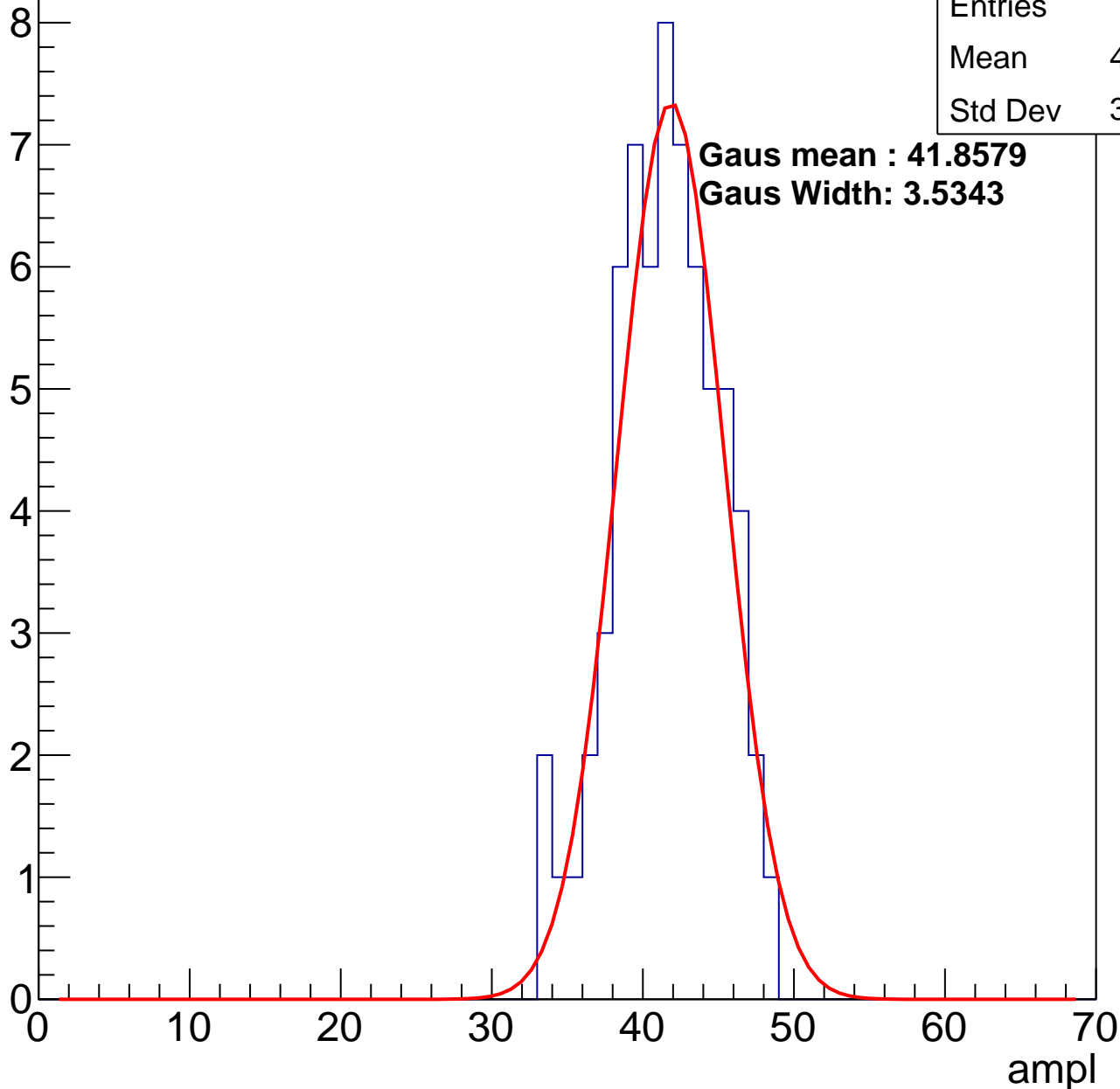
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.06
Std Dev	3.428

Gaus mean : 41.8579

Gaus Width: 3.5343

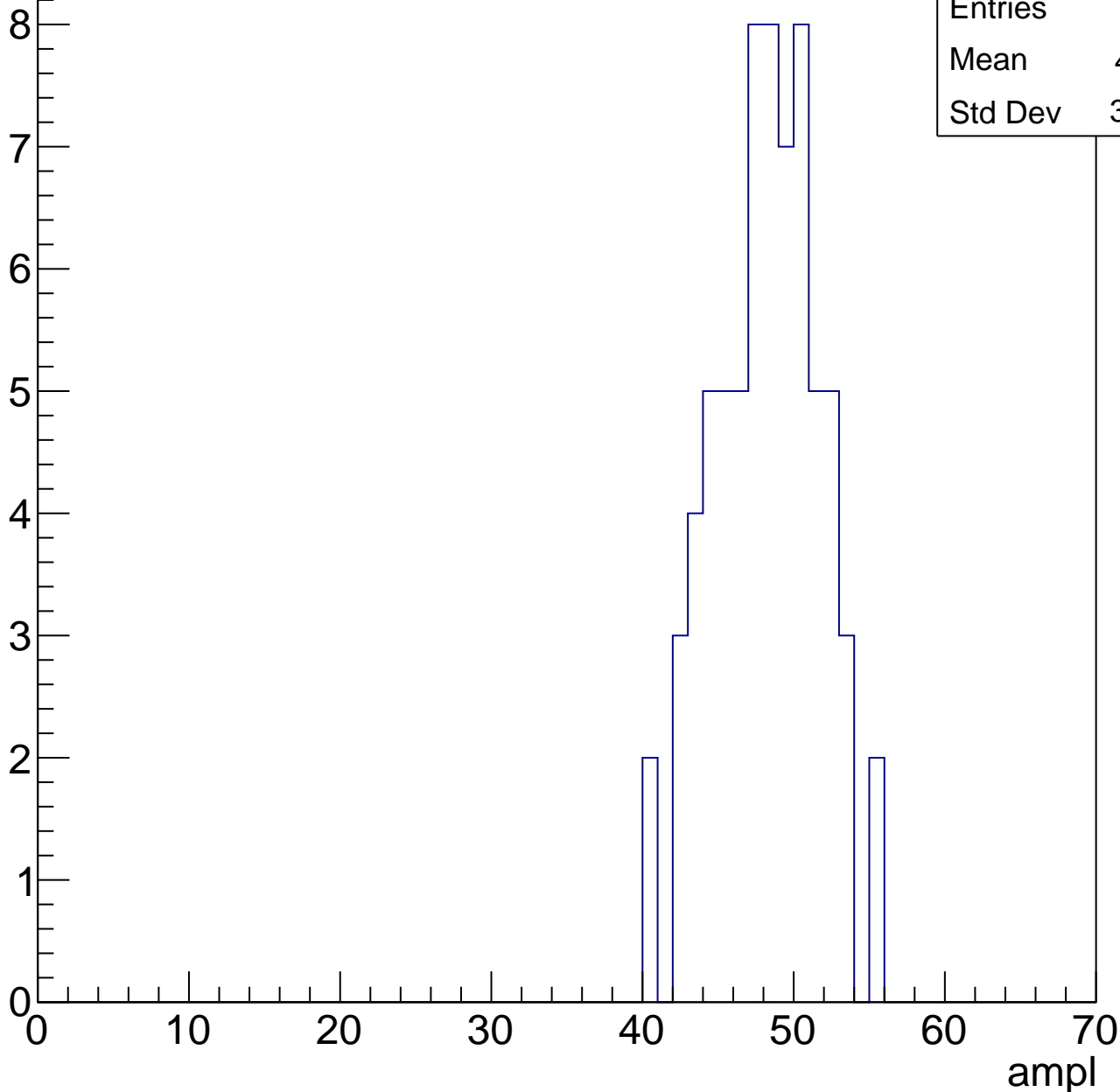


B1L003S, U19-ch46, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	47.71
Std Dev	3.423

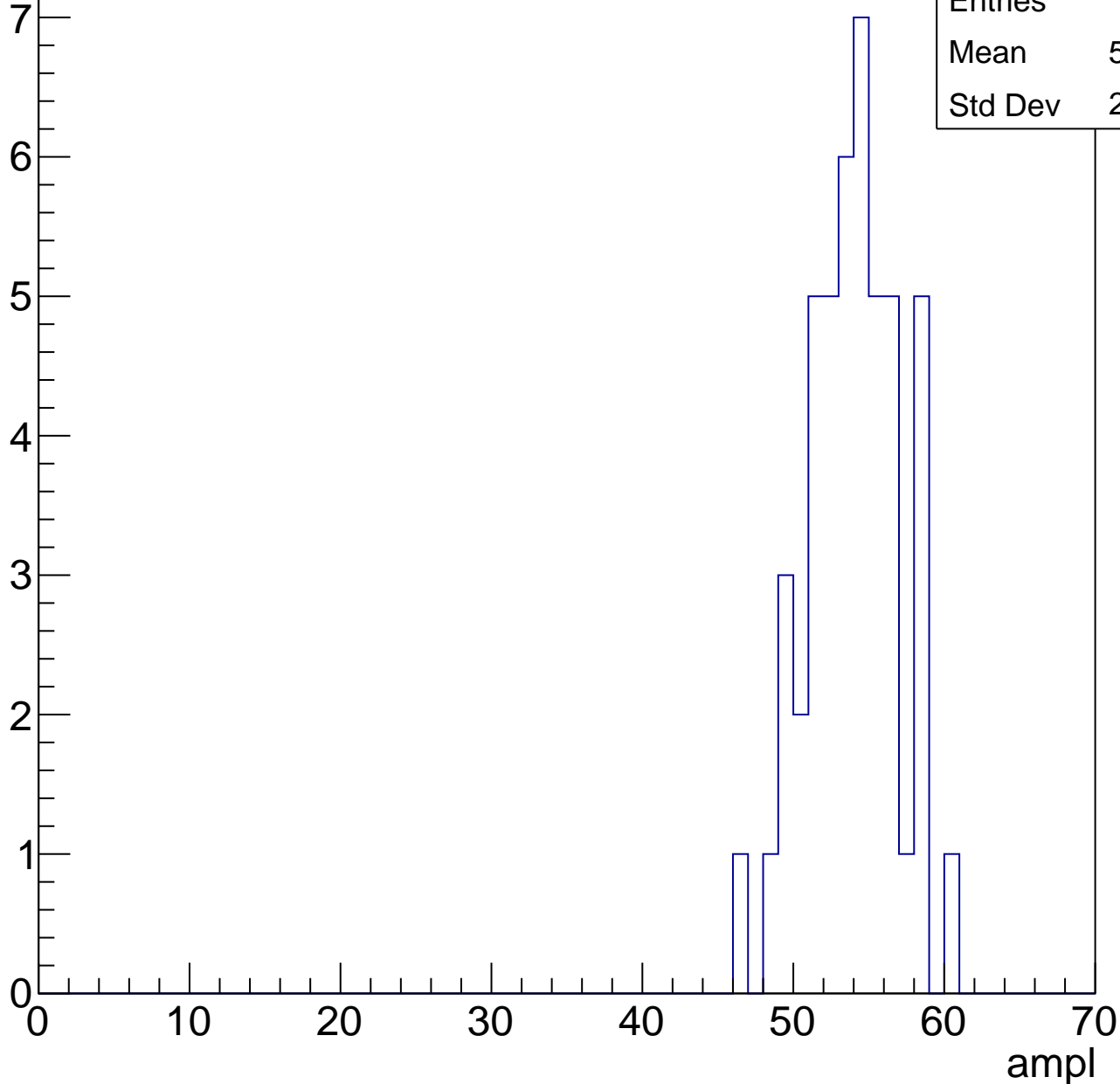


B1L003S, U19-ch46, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

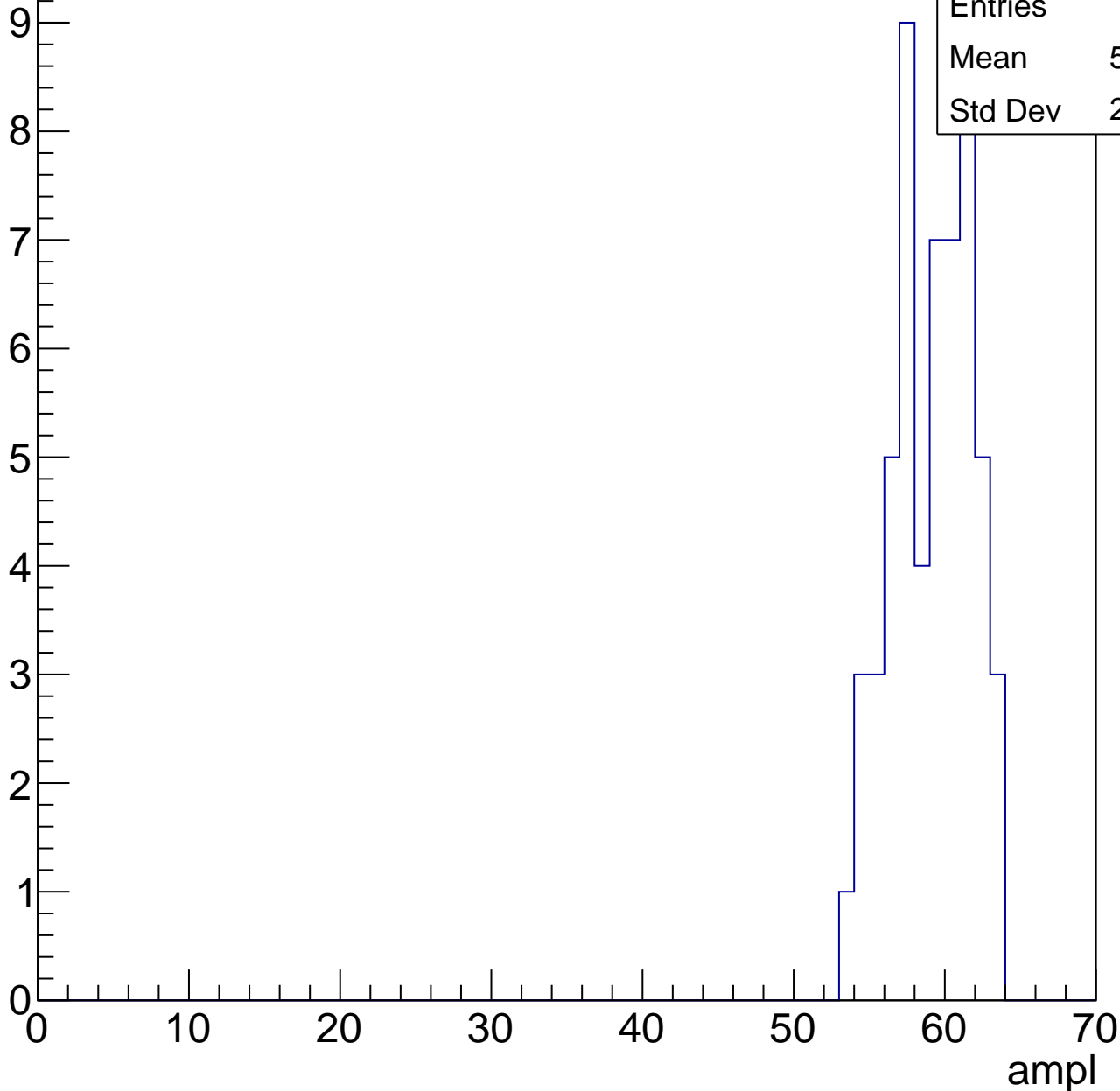
Entries	47
Mean	53.49
Std Dev	2.974



B1L003S, U19-ch46, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

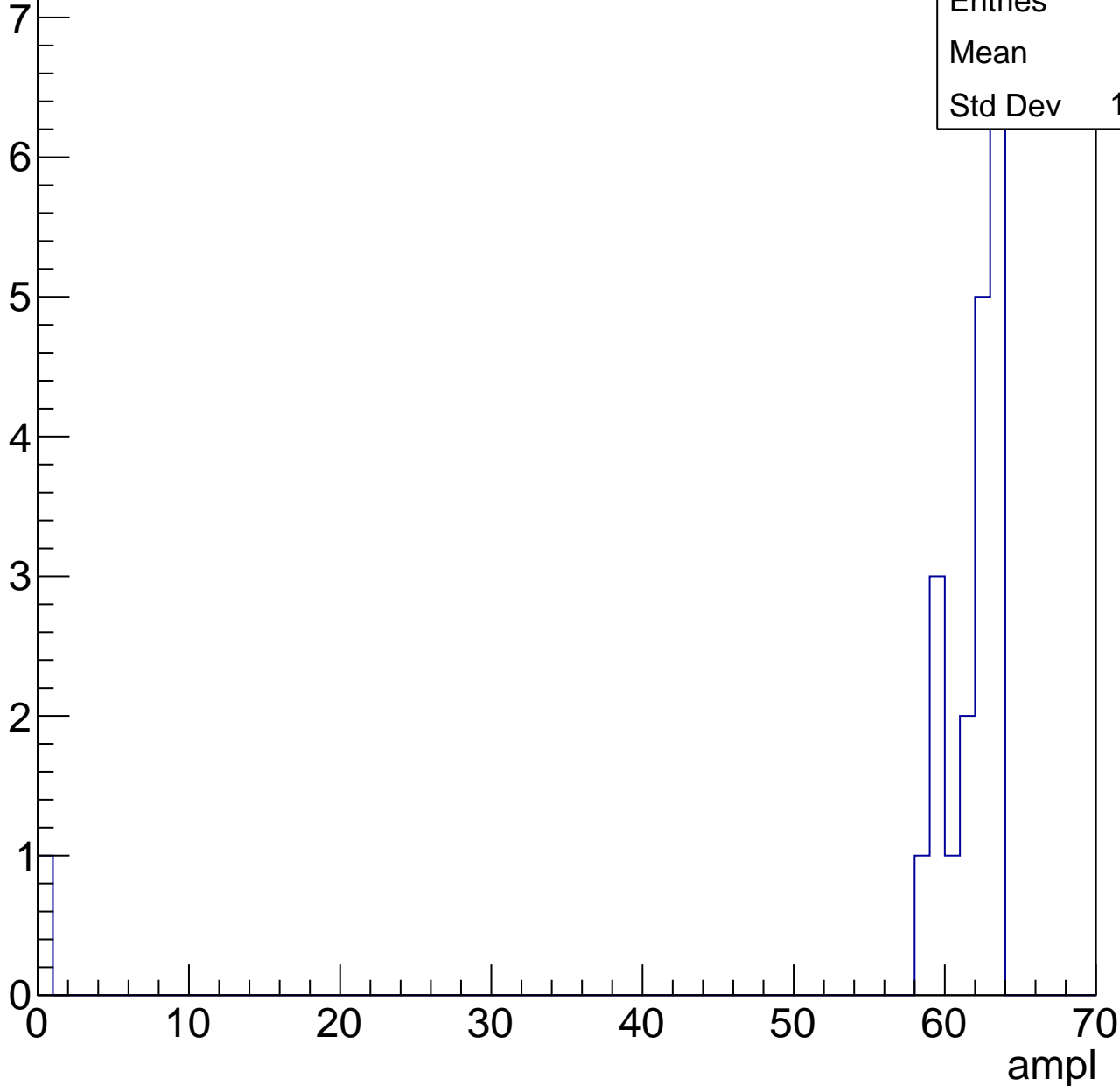


B1L003S, U19-ch46, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	20
Mean	58.4
Std Dev	13.49



B1L003S, U19-ch46, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	60
Std Dev	0

B1L003S, U19-ch47, adc0

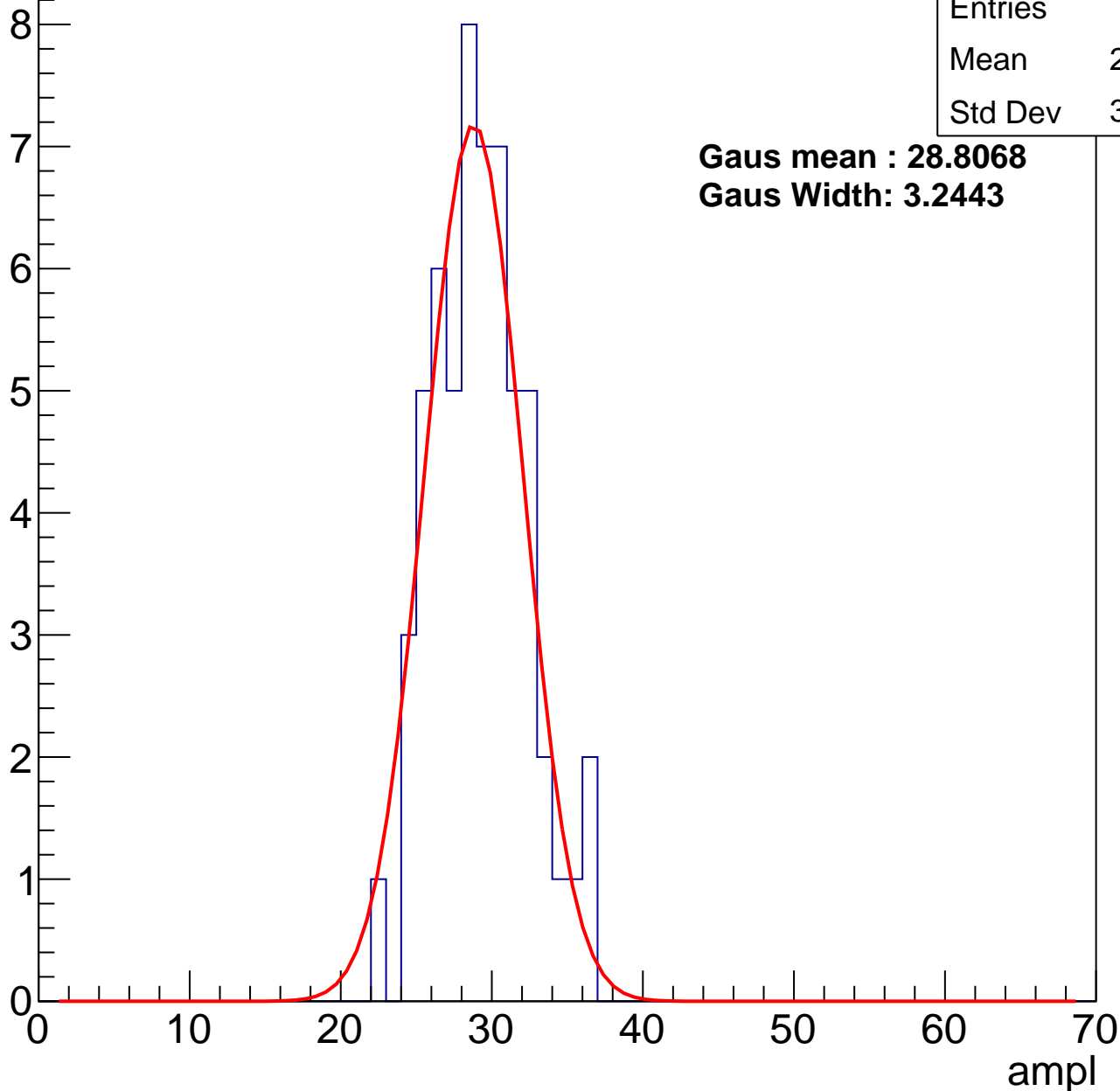
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.78
Std Dev	3.068

Gaus mean : 28.8068

Gaus Width: 3.2443



B1L003S, U19-ch47, adc1

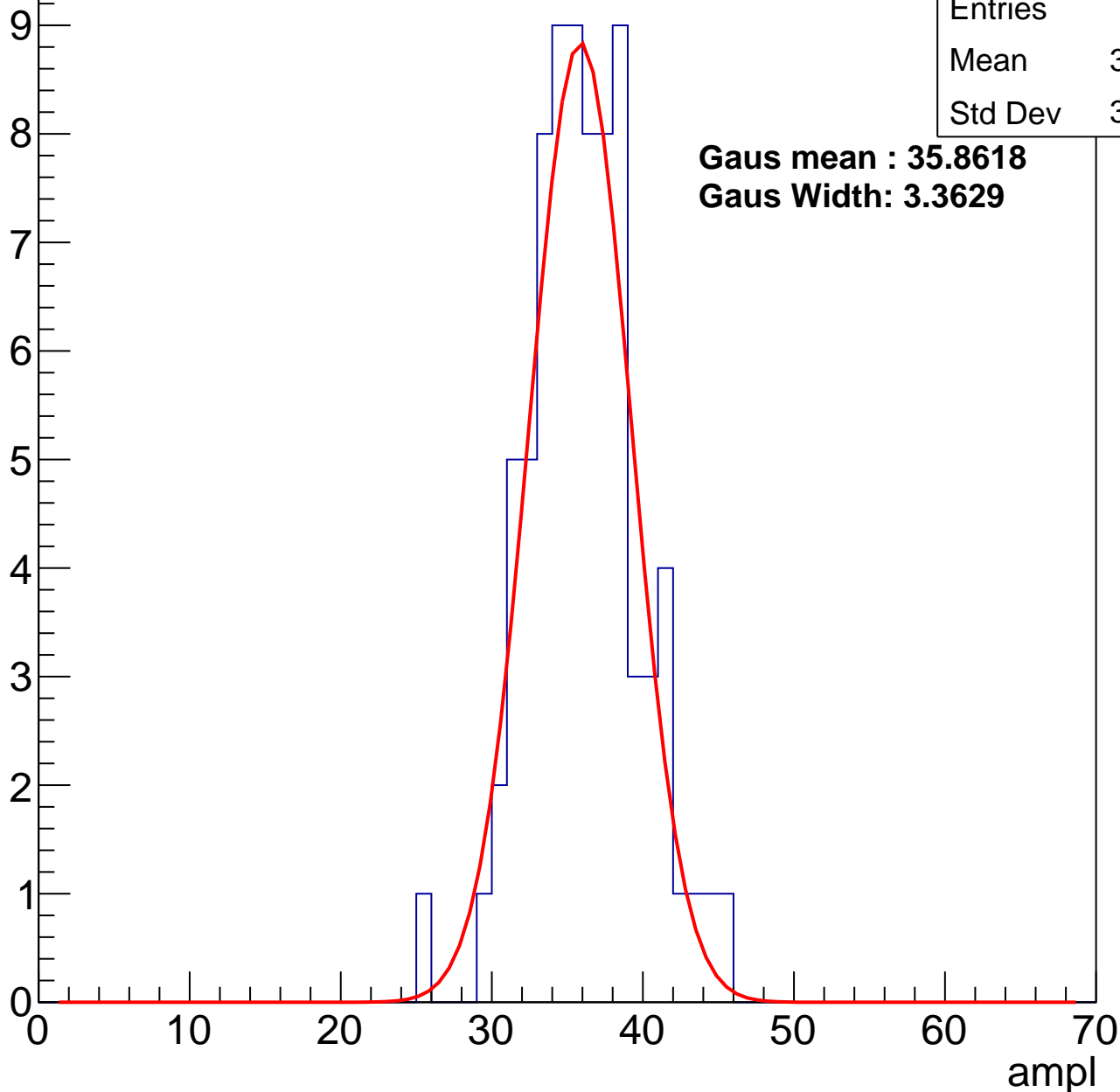
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	35.63
Std Dev	3.562

Gaus mean : 35.8618

Gaus Width: 3.3629



B1L003S, U19-ch47, adc2

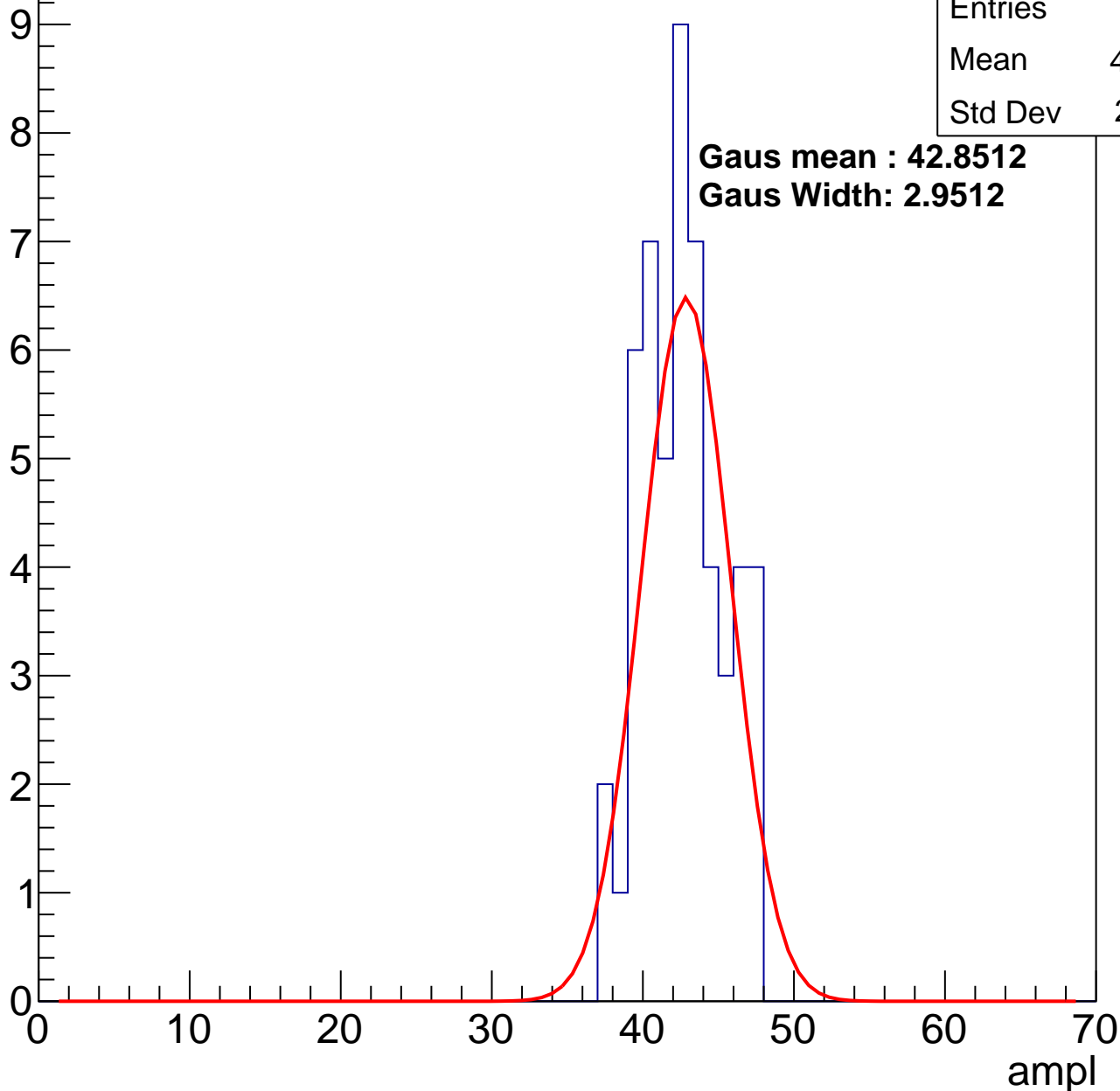
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.17
Std Dev	2.651

Gaus mean : 42.8512

Gaus Width: 2.9512



B1L003S, U19-ch47, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	48.66
Std Dev	3.559

Entry

10

8

6

4

2

0

0

10

20

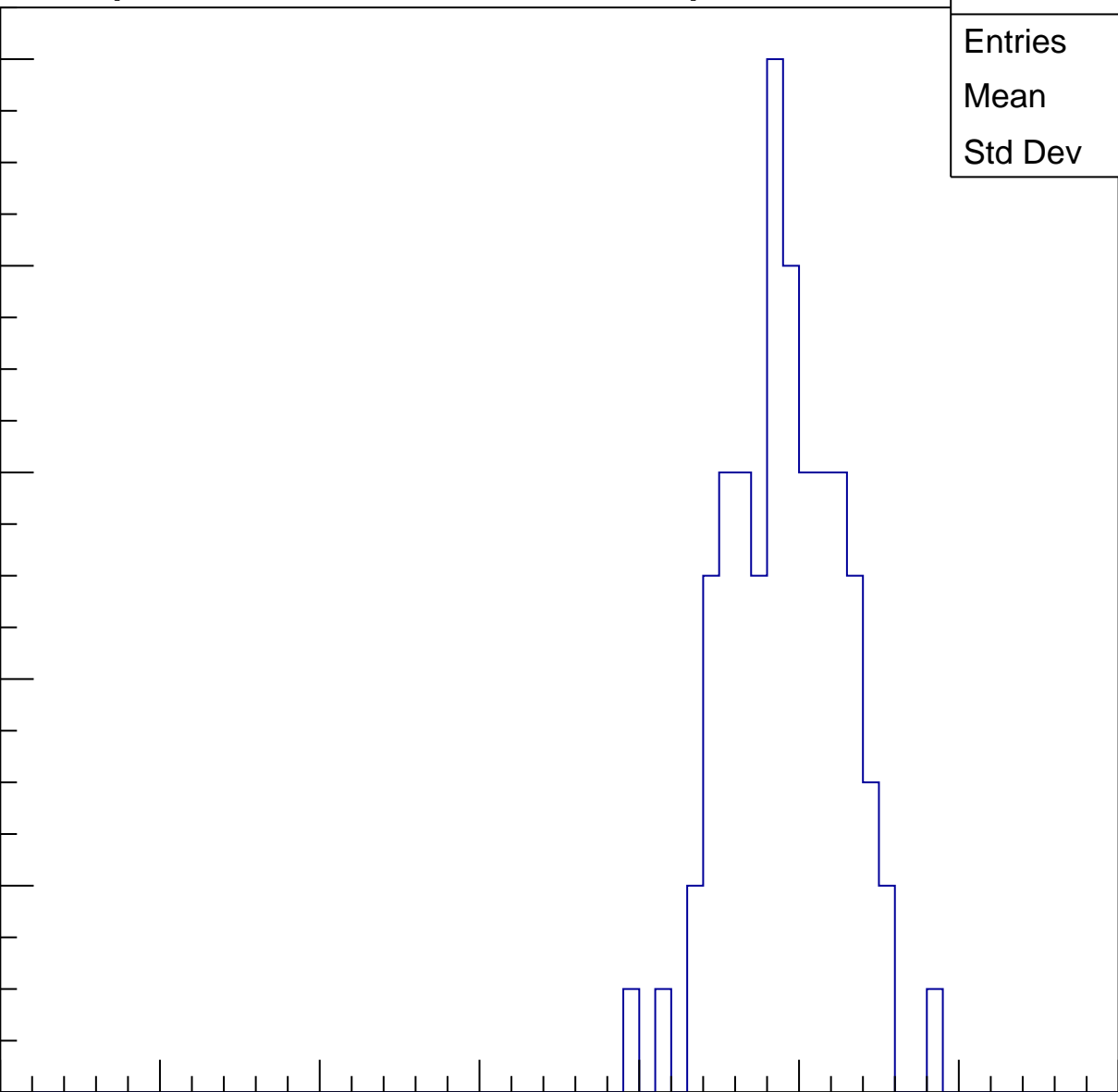
30

40

50

60

ampl

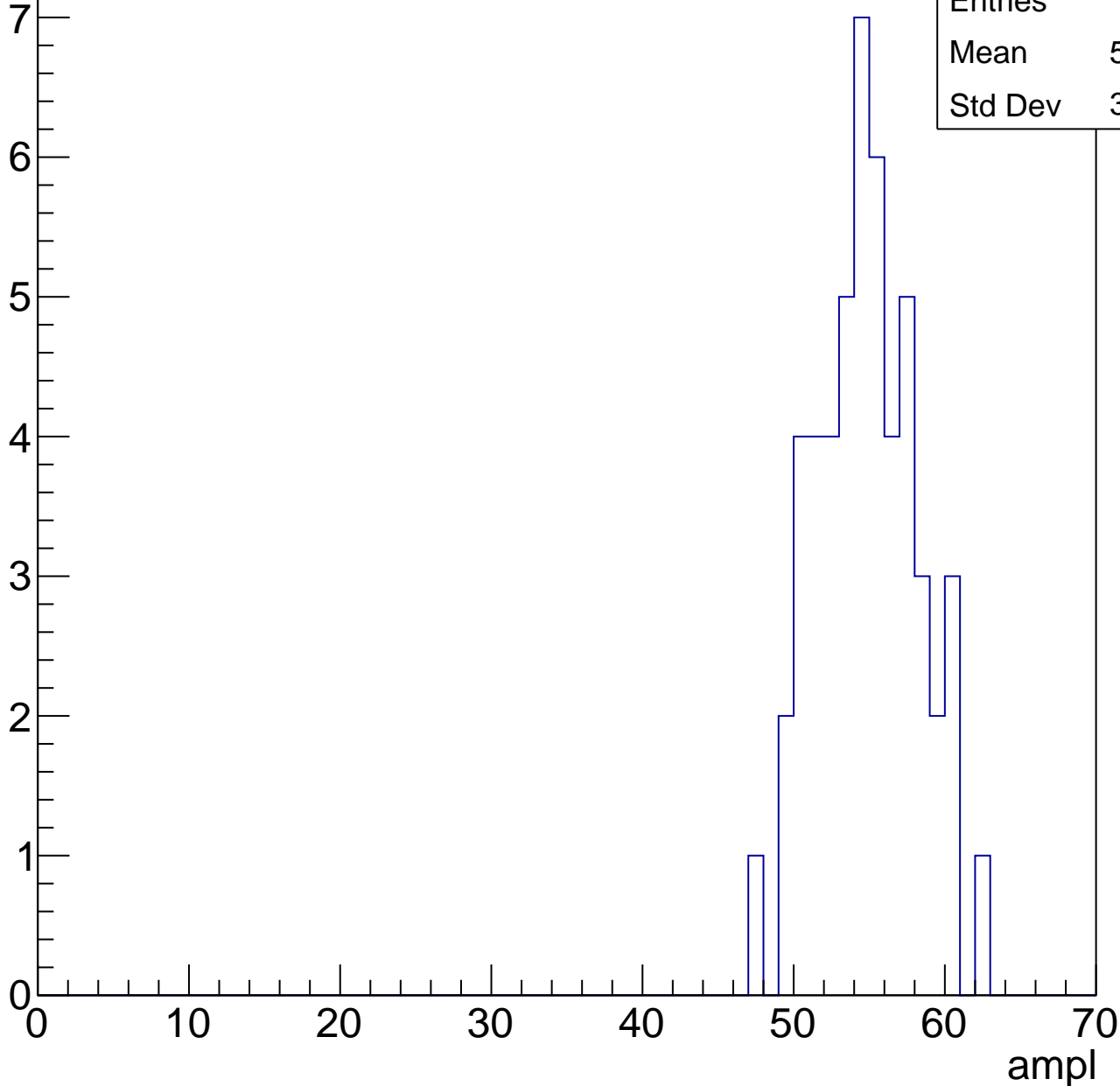


B1L003S, U19-ch47, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

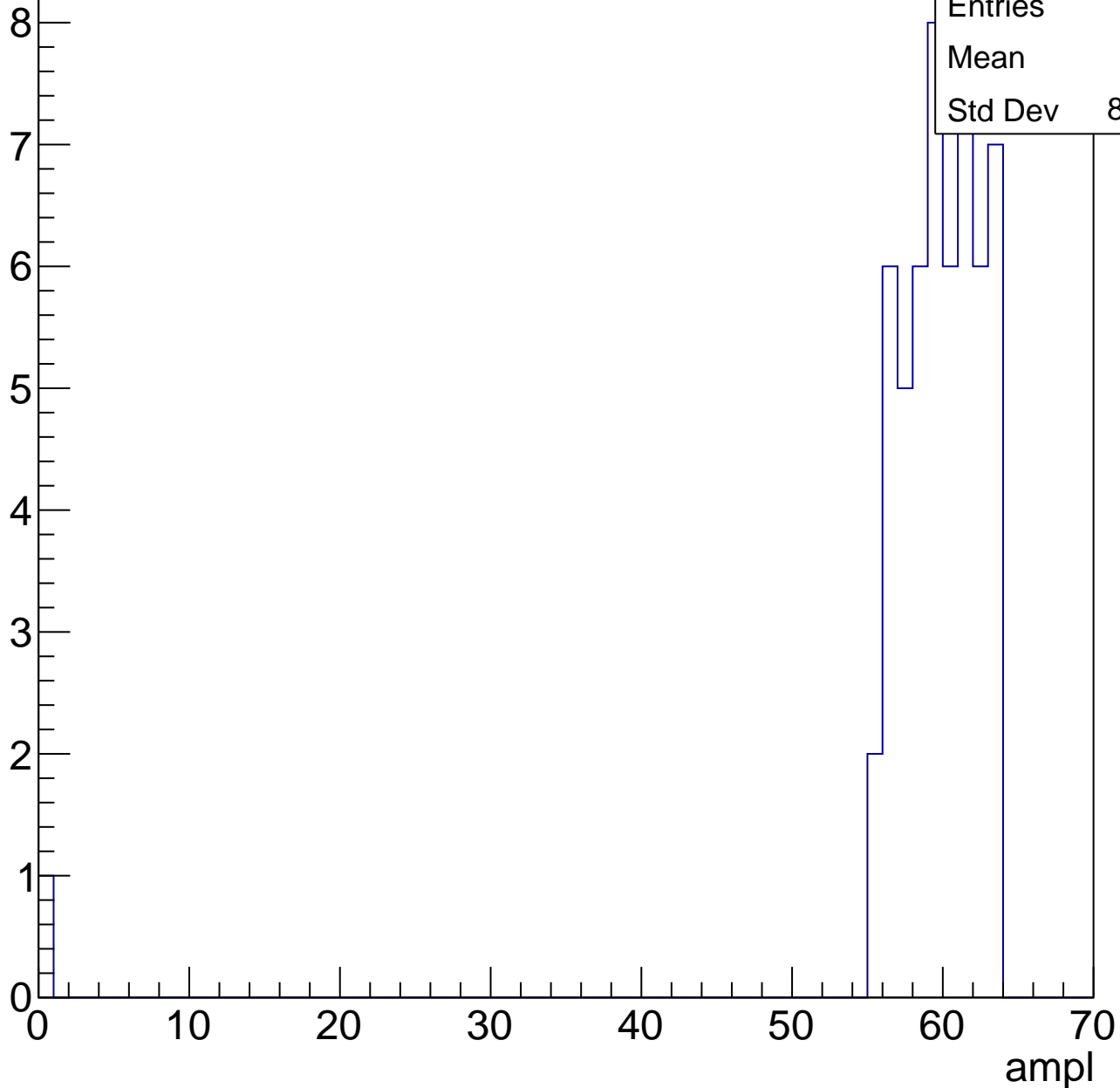
Entries	51
Mean	54.37
Std Dev	3.284



B1L003S, U19-ch47, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch47, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

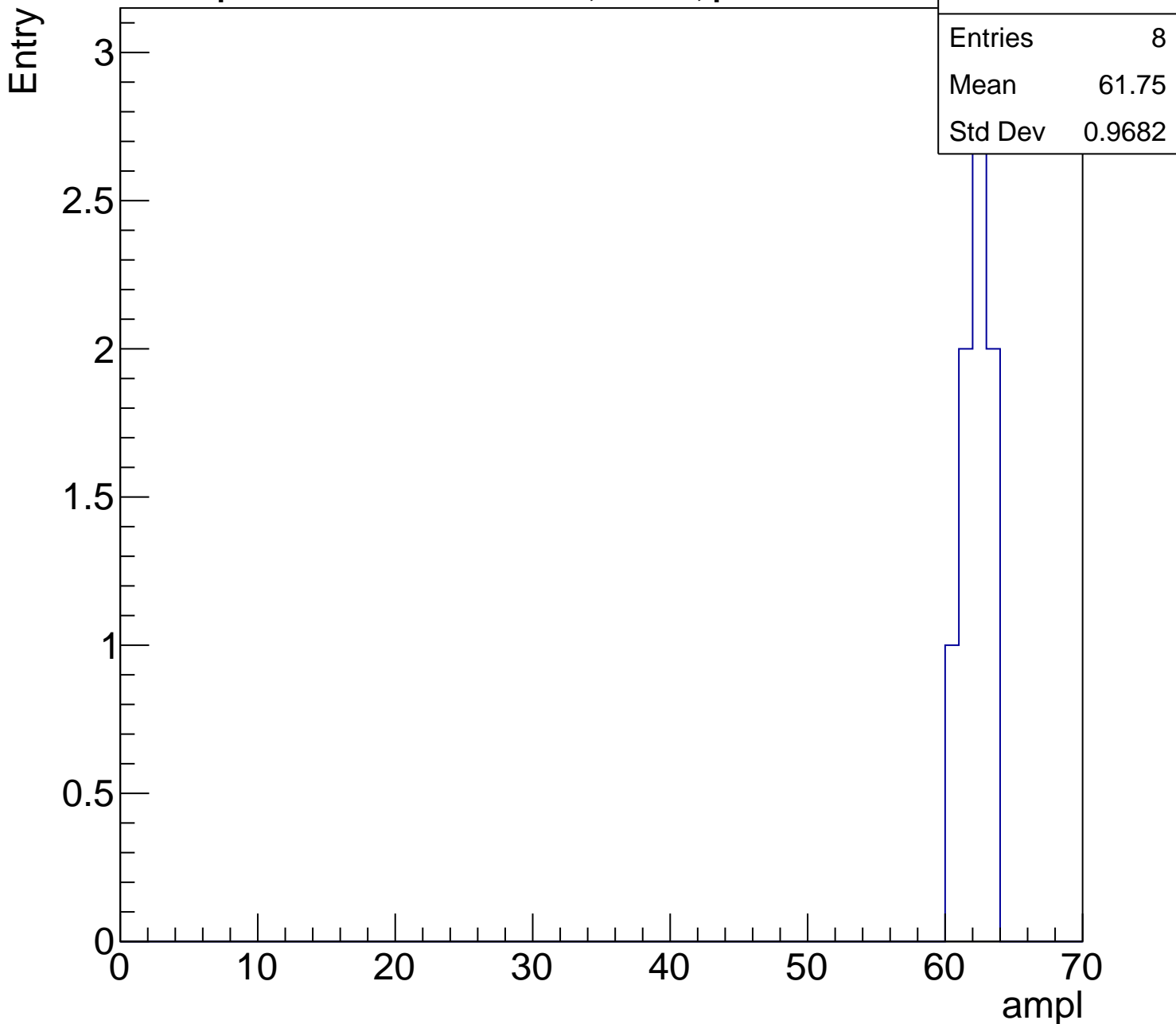
8

Mean

61.75

Std Dev

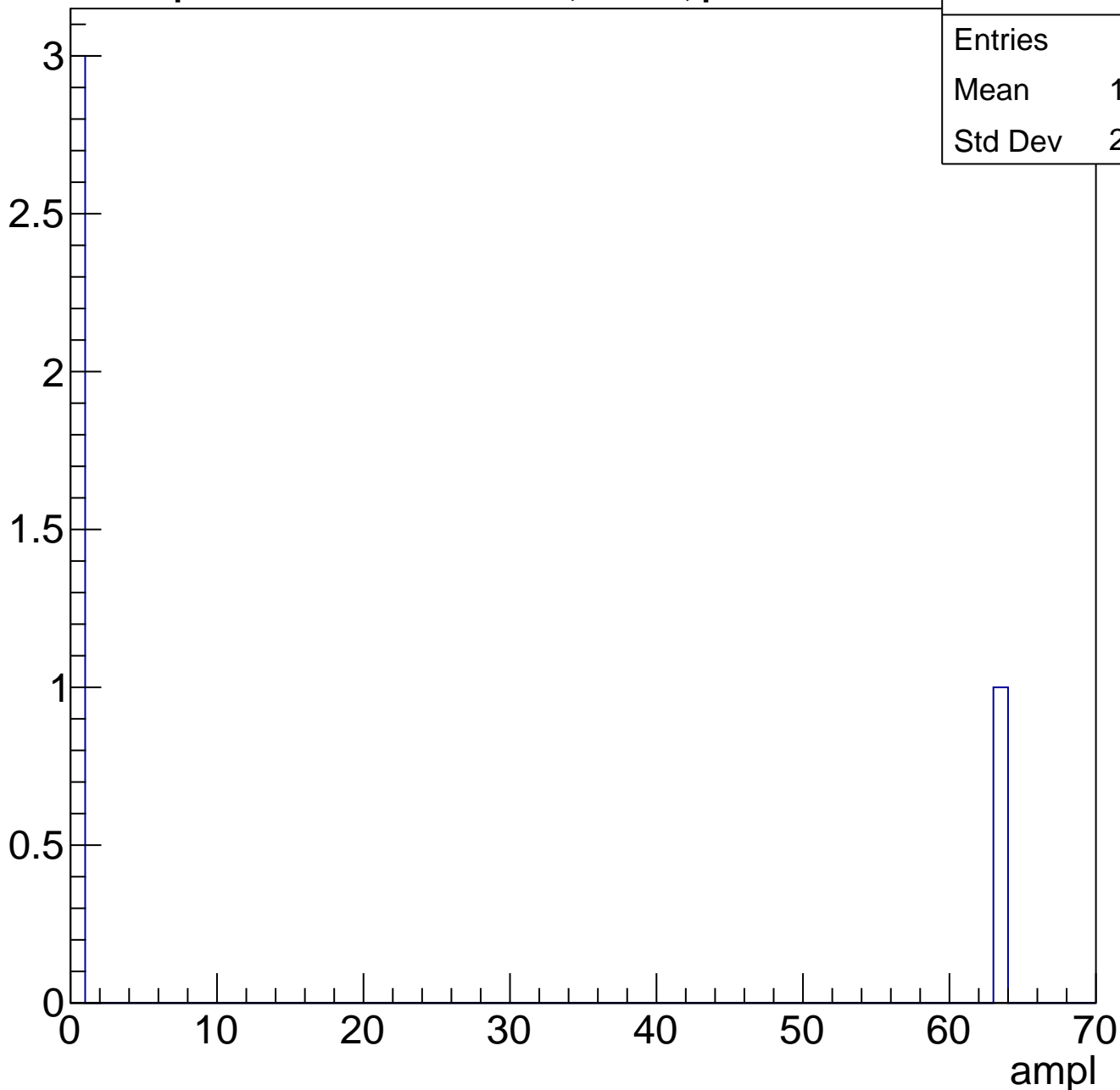
0.9682



B1L003S, U19-ch47, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	15.75
Std Dev	27.28

B1L003S, U19-ch48, adc0

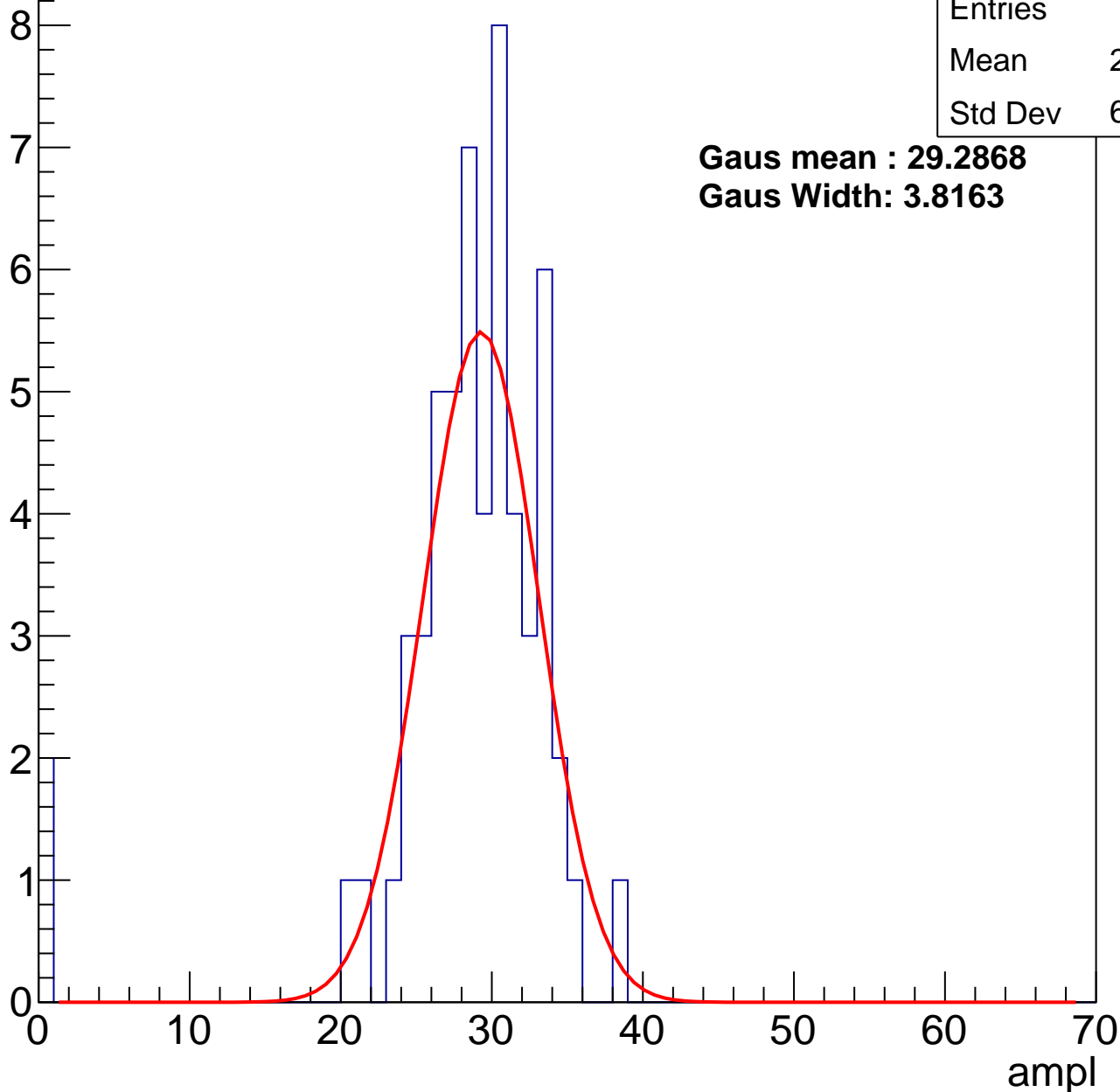
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	27.84
Std Dev	6.346

Gaus mean : 29.2868

Gaus Width: 3.8163



B1L003S, U19-ch48, adc1

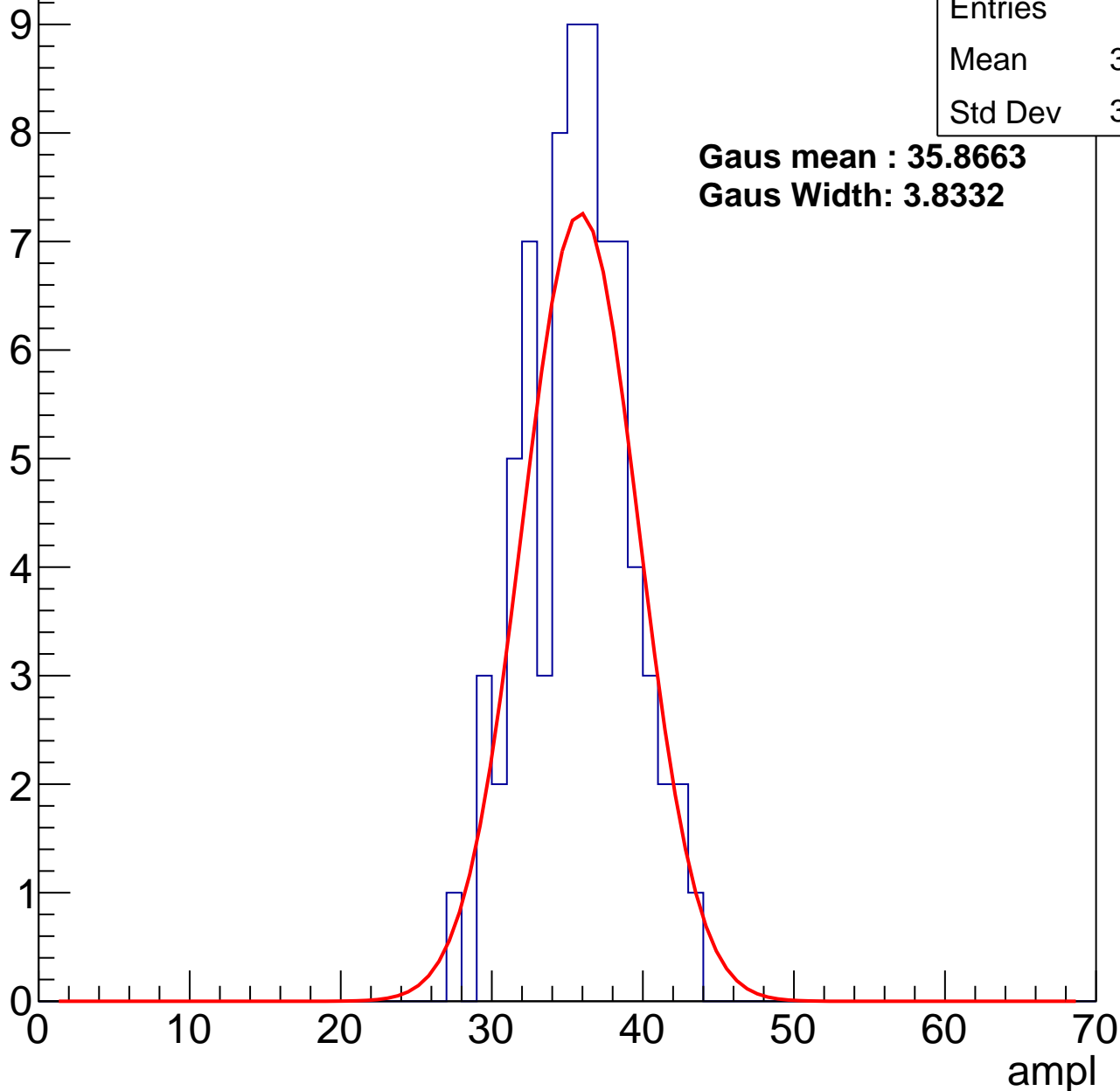
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	35.25
Std Dev	3.419

Gaus mean : 35.8663

Gaus Width: 3.8332



B1L003S, U19-ch48, adc2

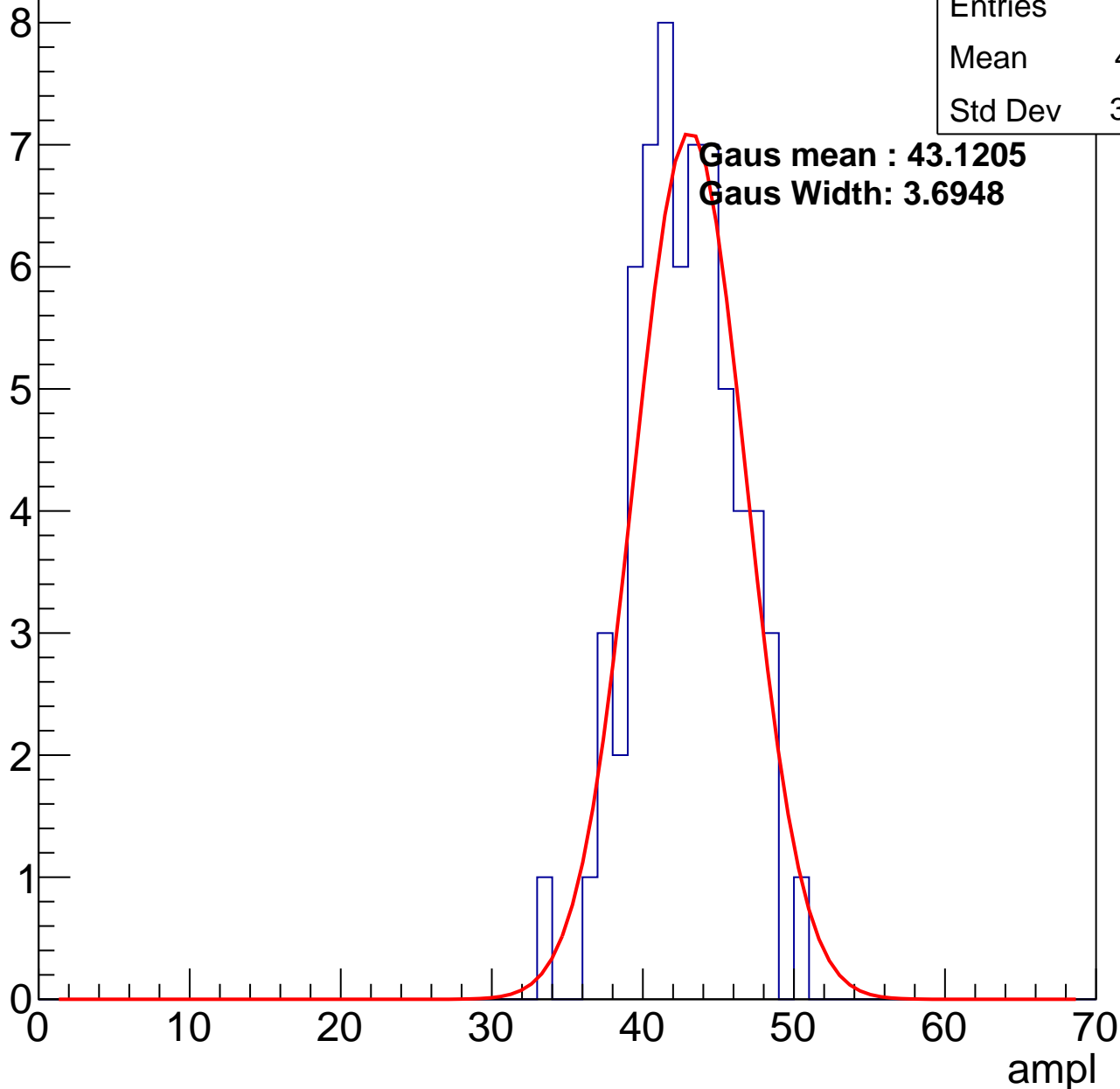
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.31
Std Dev	3.337

Gaus mean : 43.1205

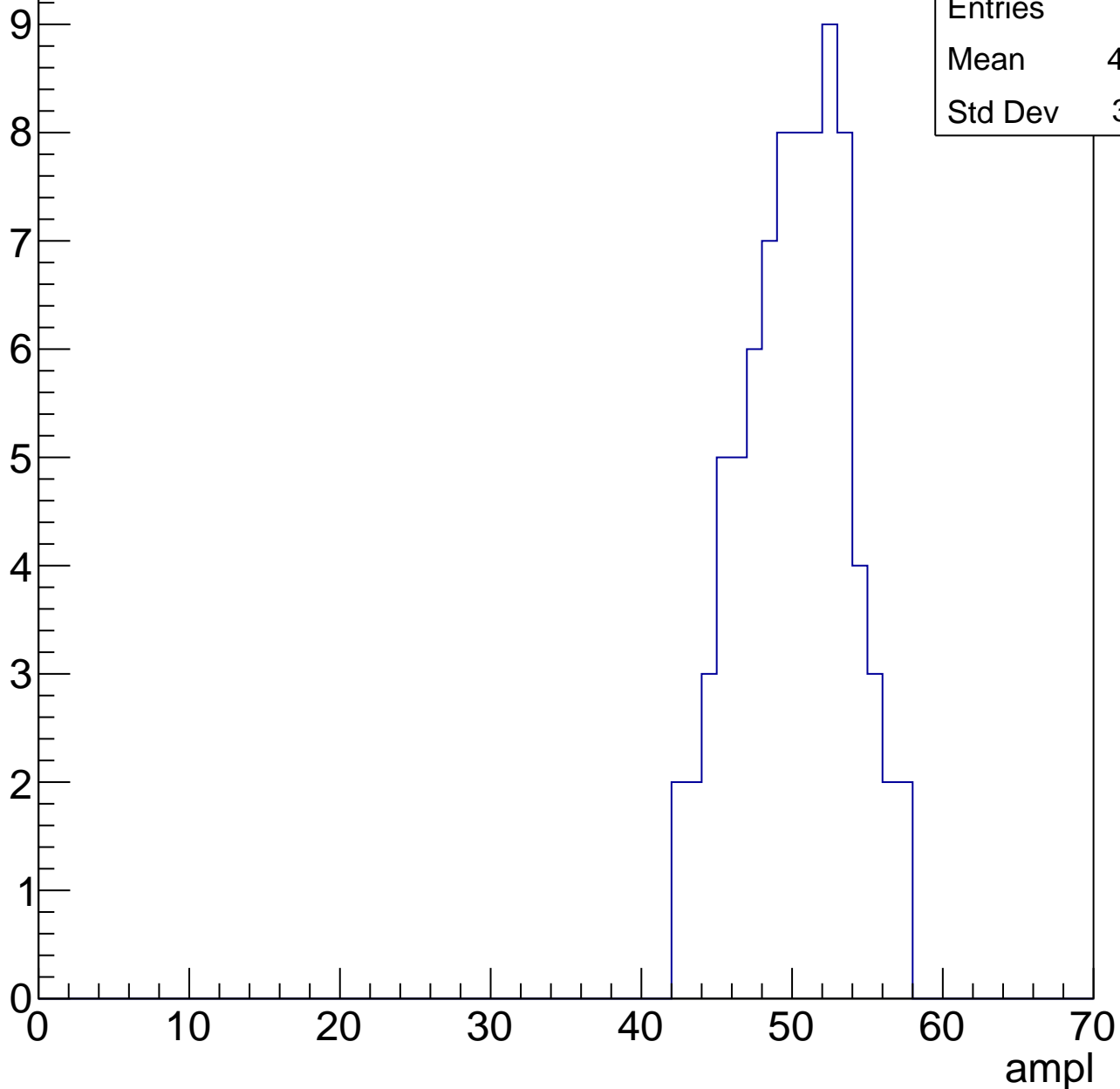
Gaus Width: 3.6948



B1L003S, U19-ch48, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

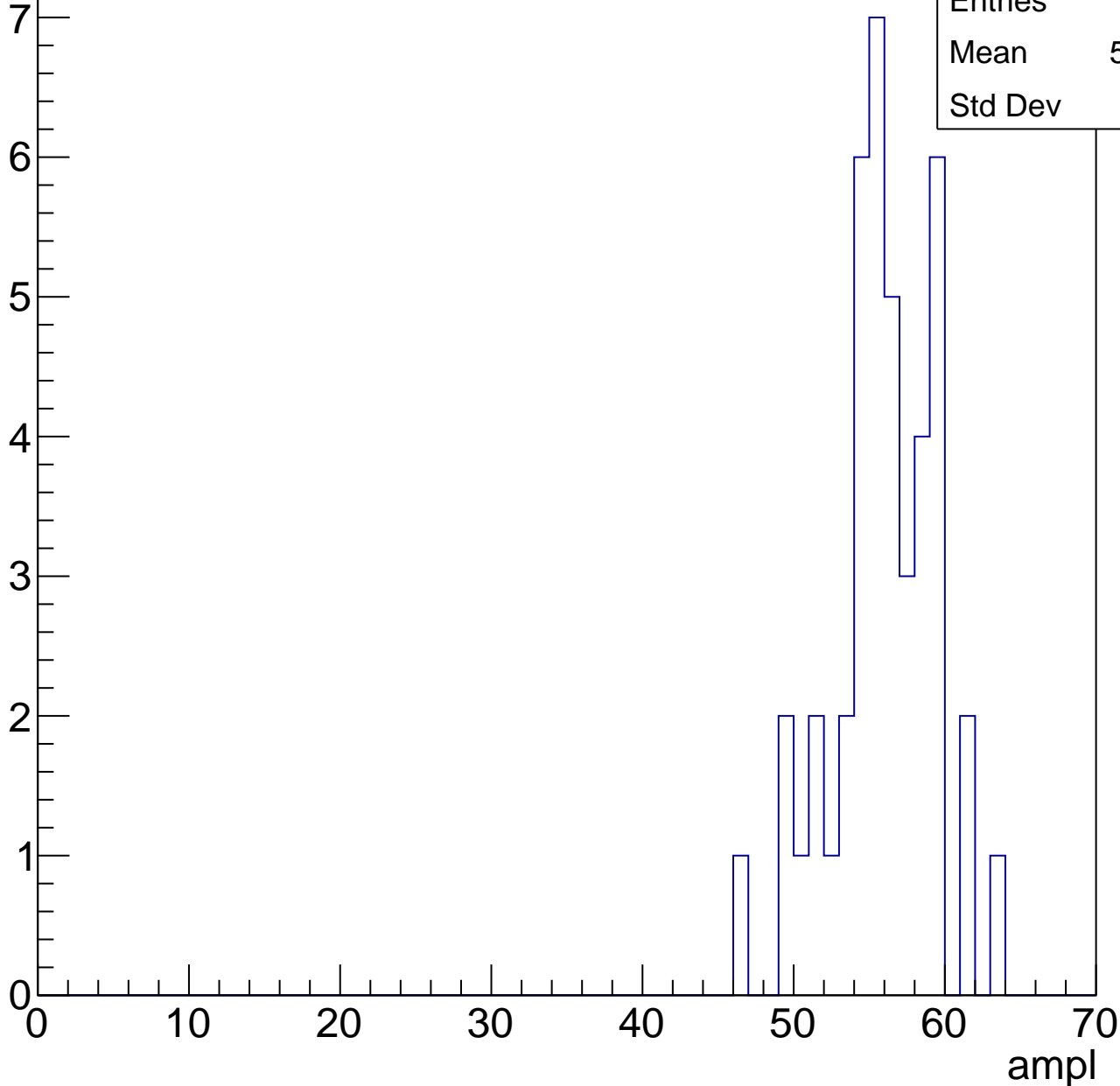


B1L003S, U19-ch48, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	55.47
Std Dev	3.44

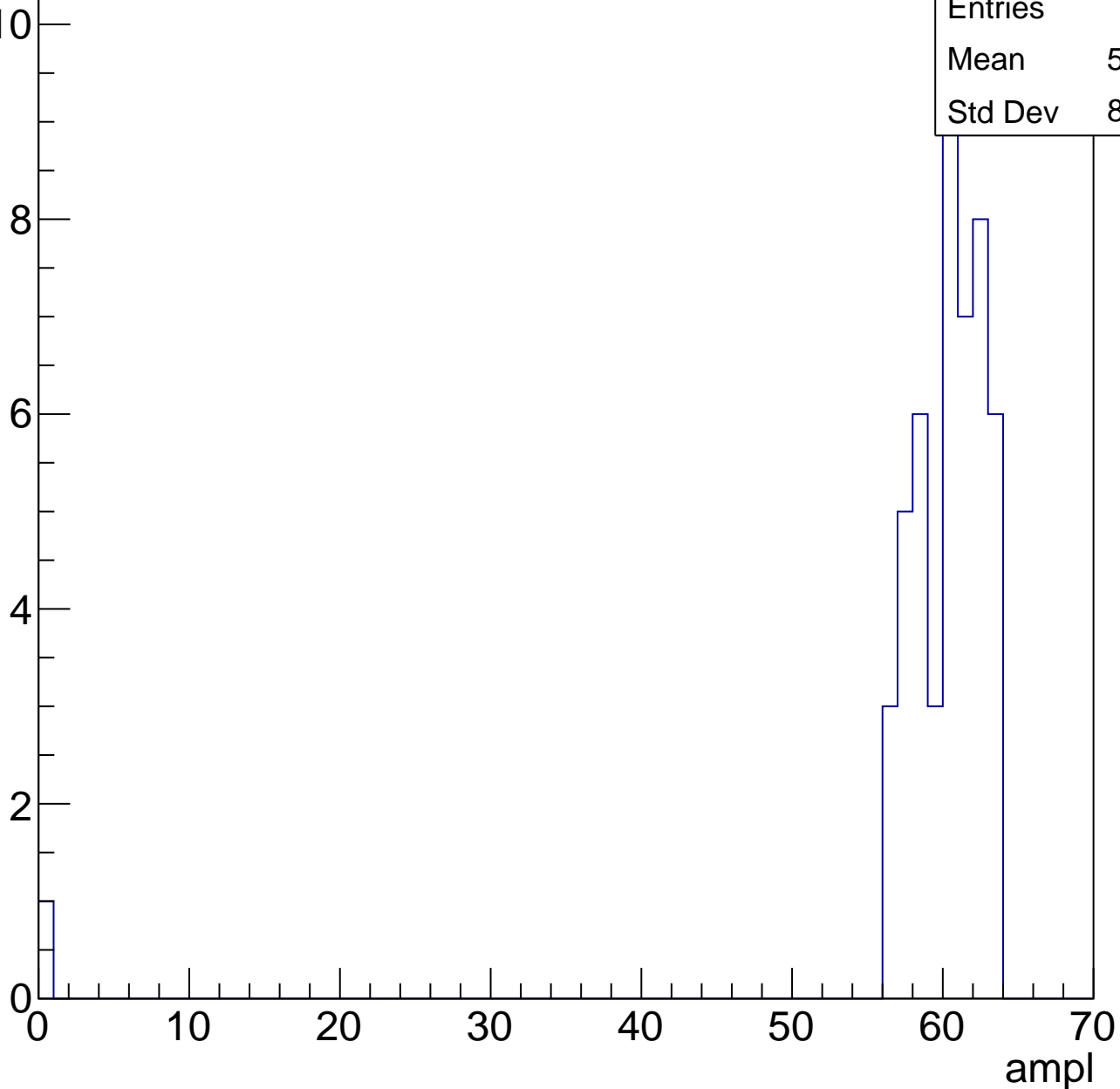


B1L003S, U19-ch48, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

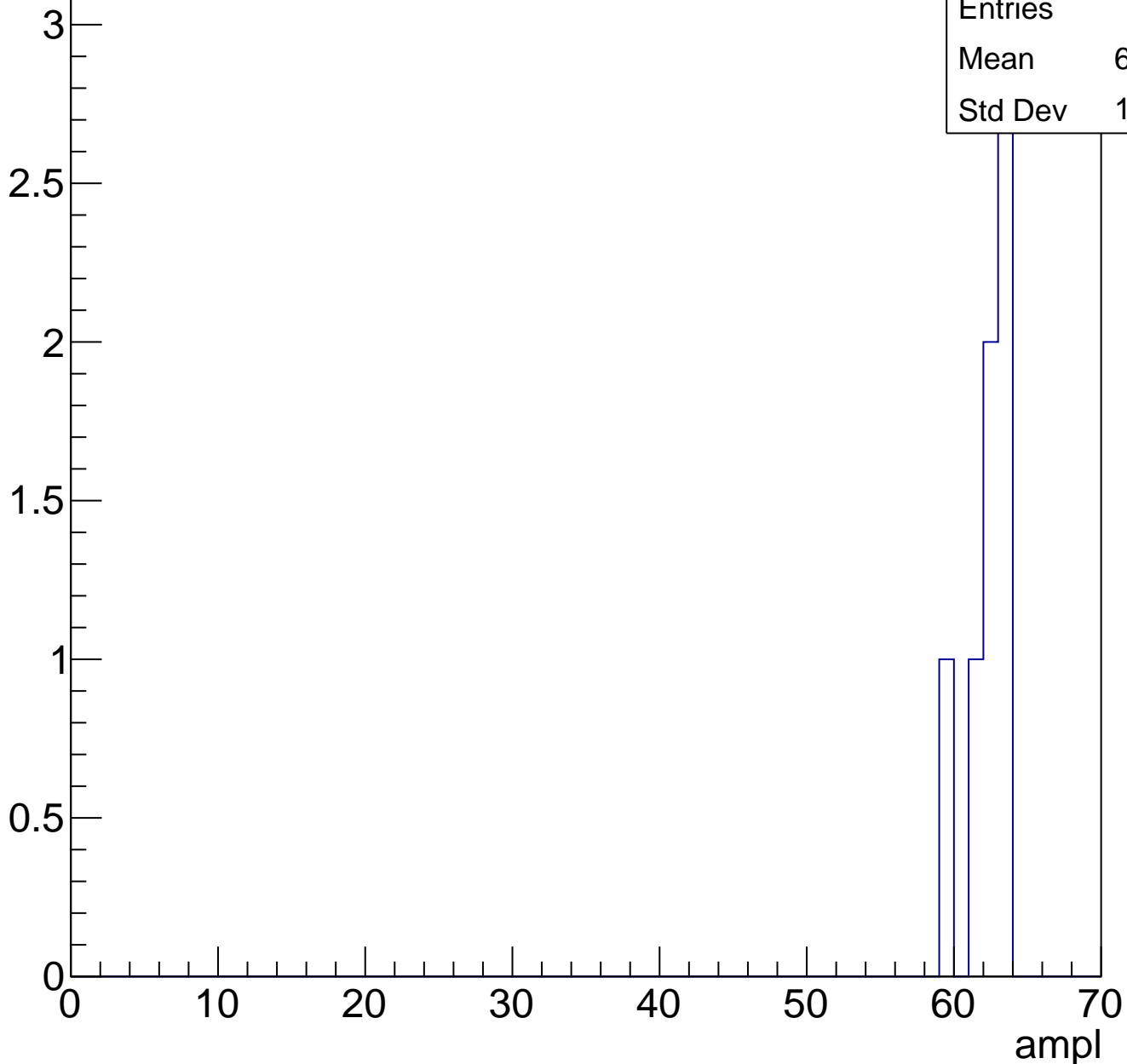
Entries	49
Mean	58.76
Std Dev	8.733



B1L003S, U19-ch48, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch48, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch49, adc0

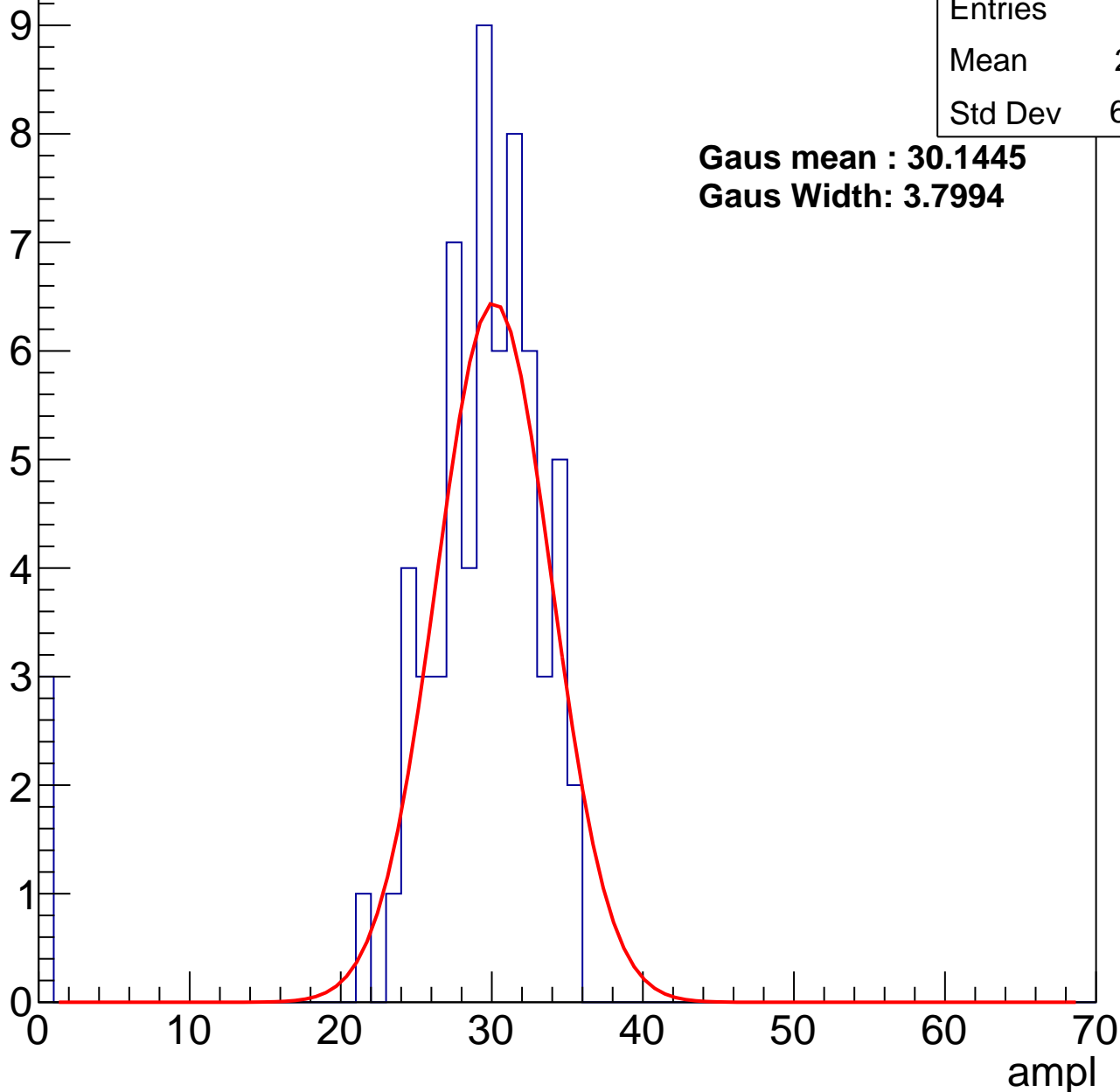
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	27.91
Std Dev	6.894

Gaus mean : 30.1445

Gaus Width: 3.7994



B1L003S, U19-ch49, adc1

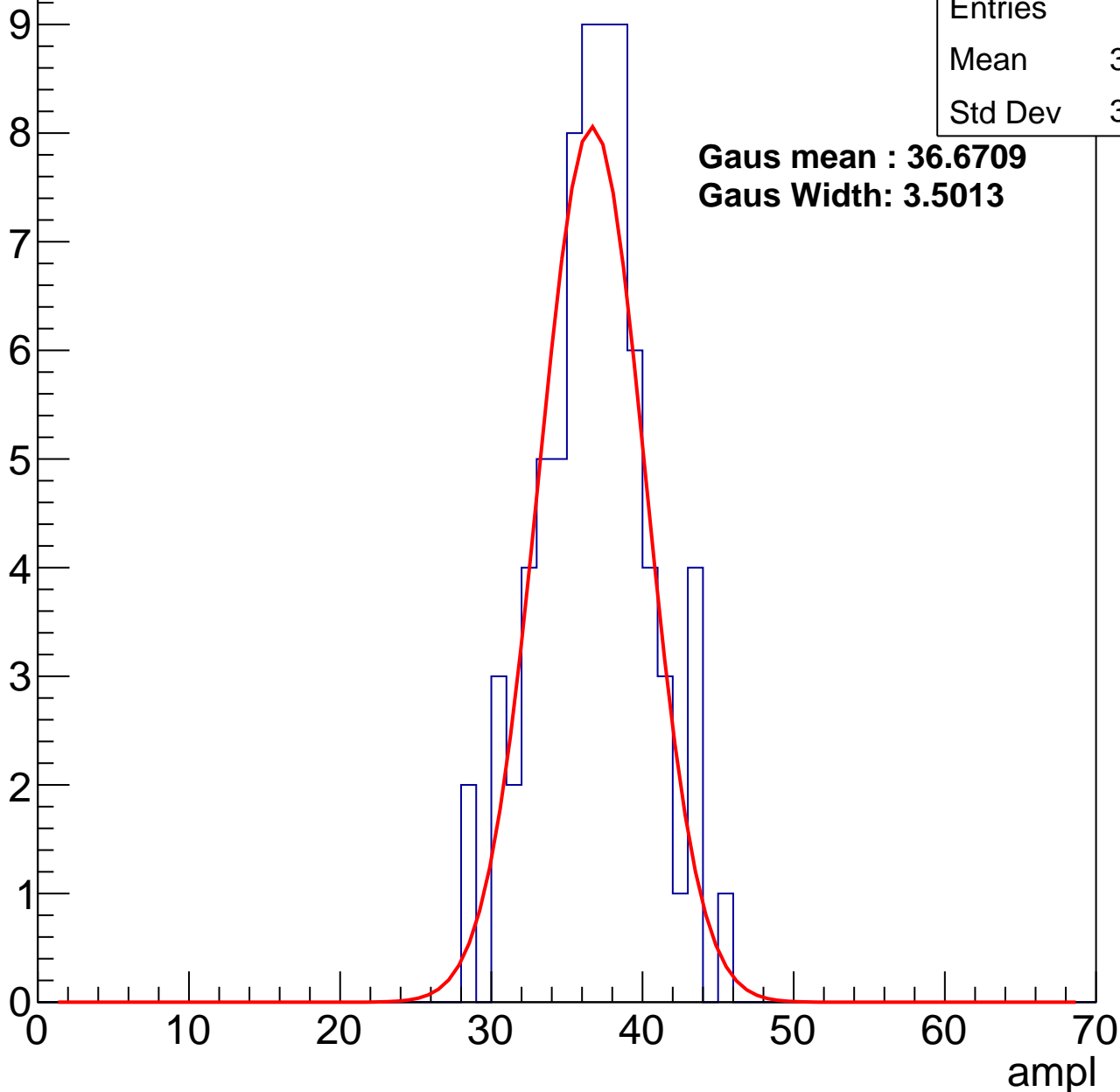
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	36.35
Std Dev	3.587

Gaus mean : 36.6709

Gaus Width: 3.5013



B1L003S, U19-ch49, adc2

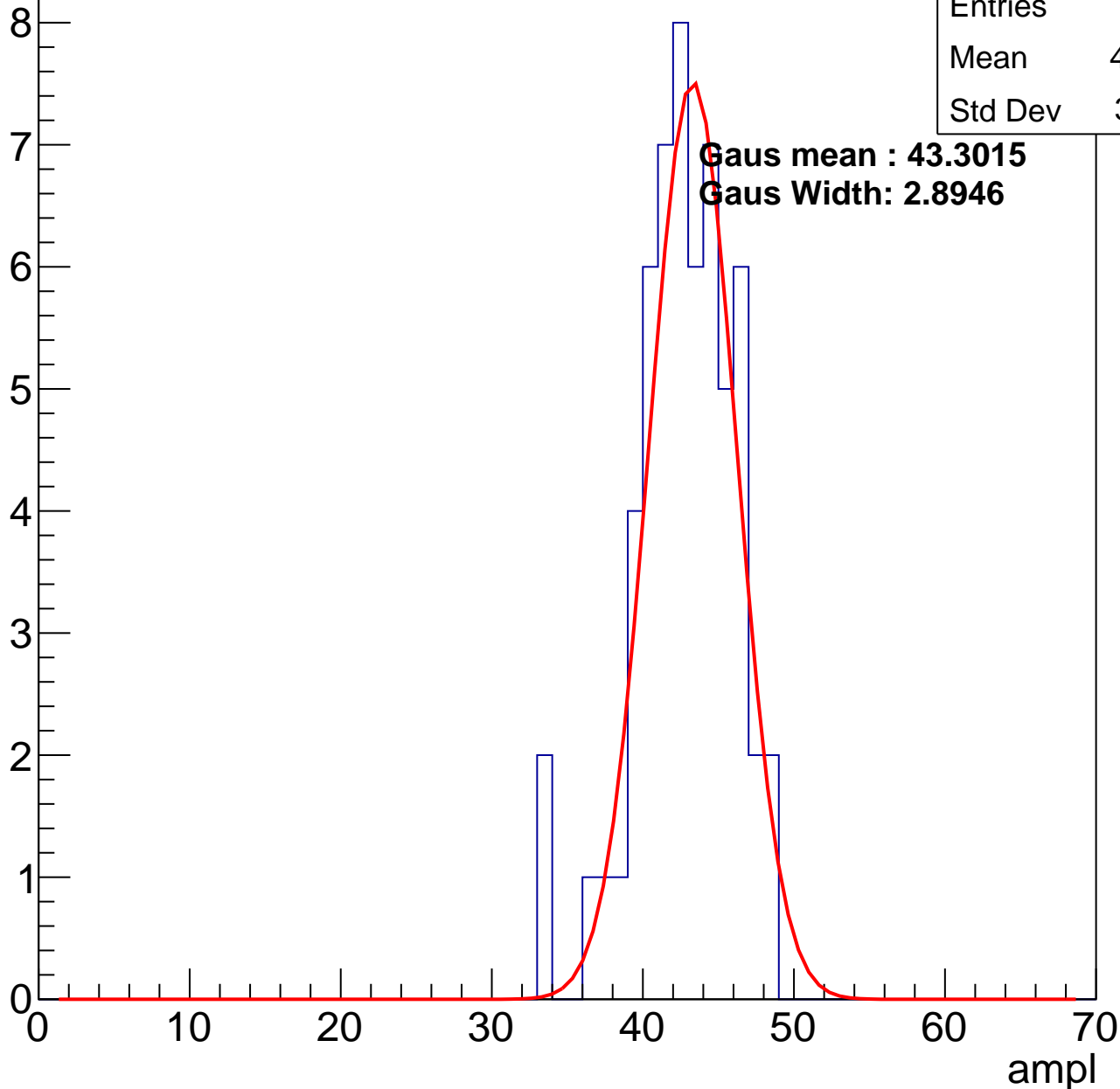
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.29
Std Dev	3.211

Gaus mean : 43.3015

Gaus Width: 2.8946

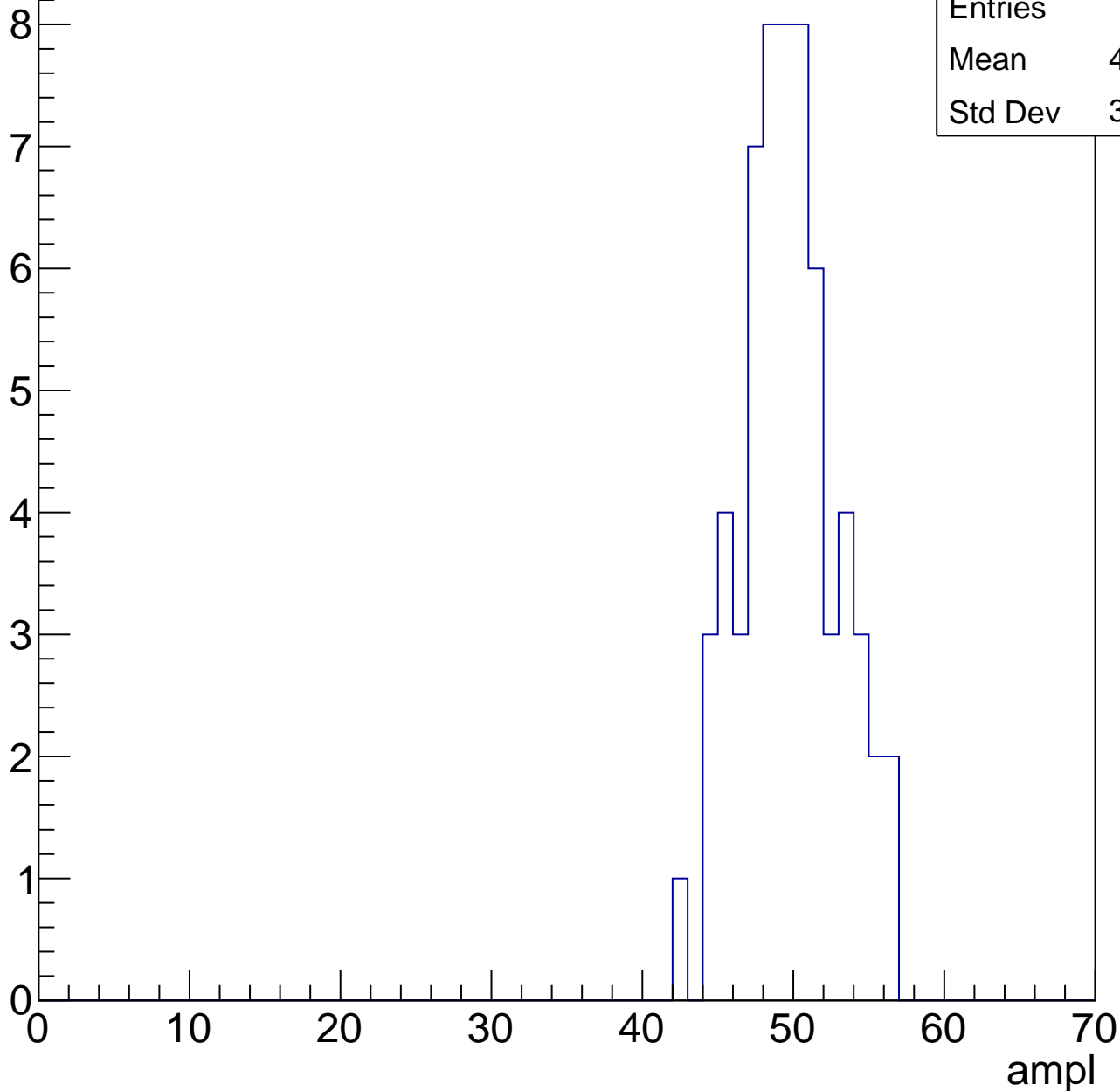


B1L003S, U19-ch49, adc3

calib_packv5_042523_0143.root, FC#13, port D2

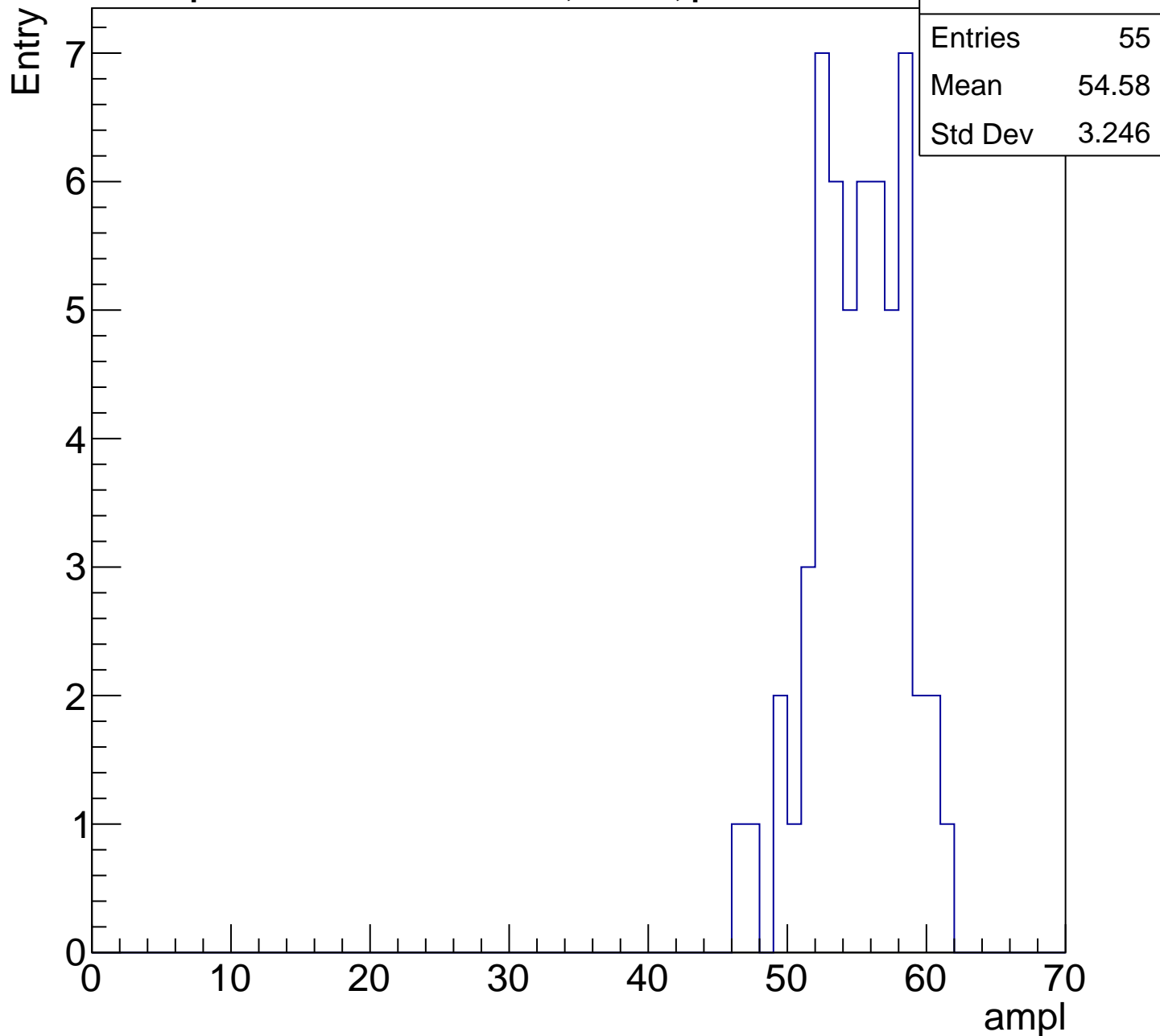
Entry

Entries	62
Mean	49.27
Std Dev	3.148



B1L003S, U19-ch49, adc4

calib_packv5_042523_0143.root, FC#13, port D2

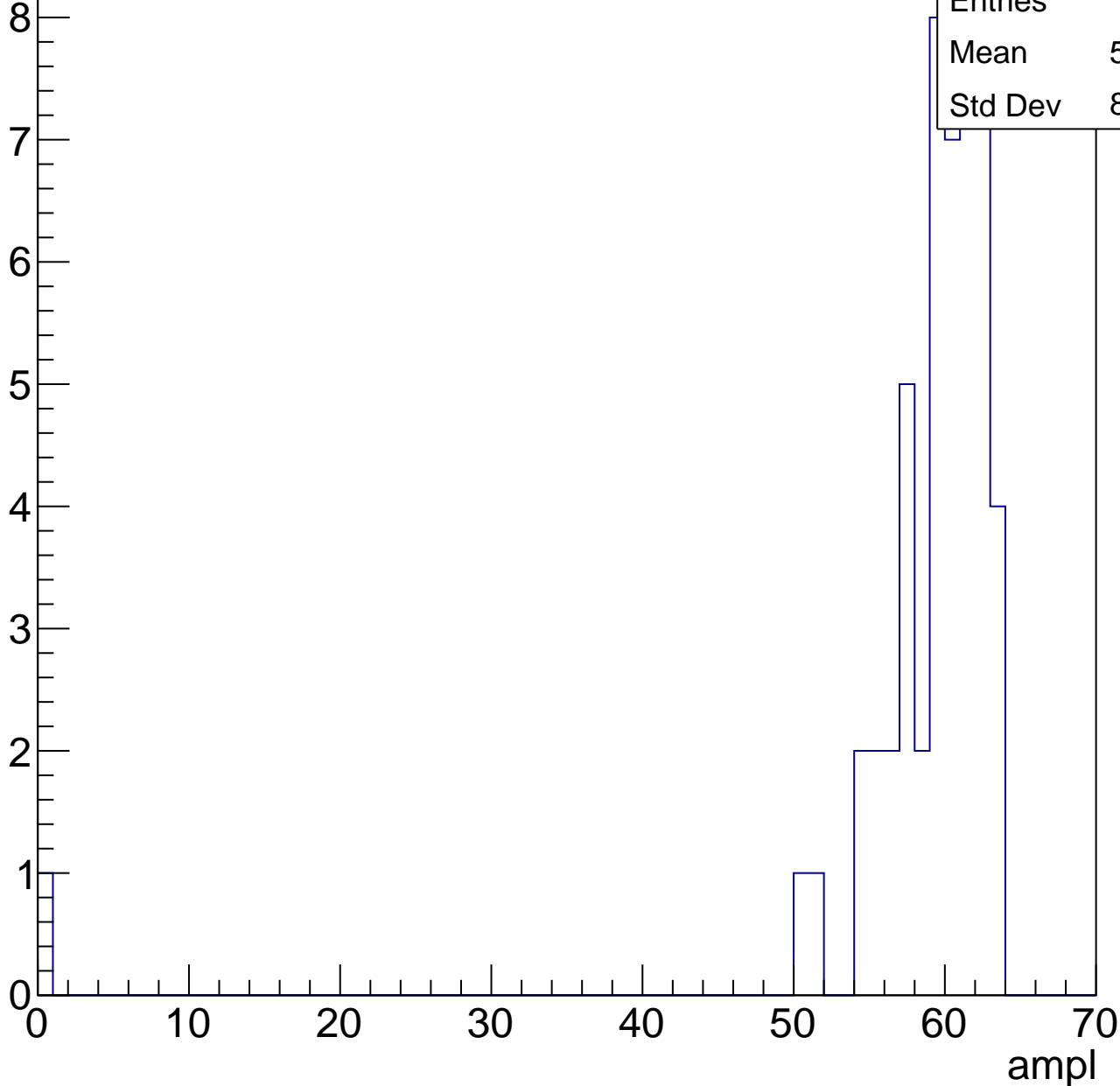


B1L003S, U19-ch49, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.04
Std Dev	8.718

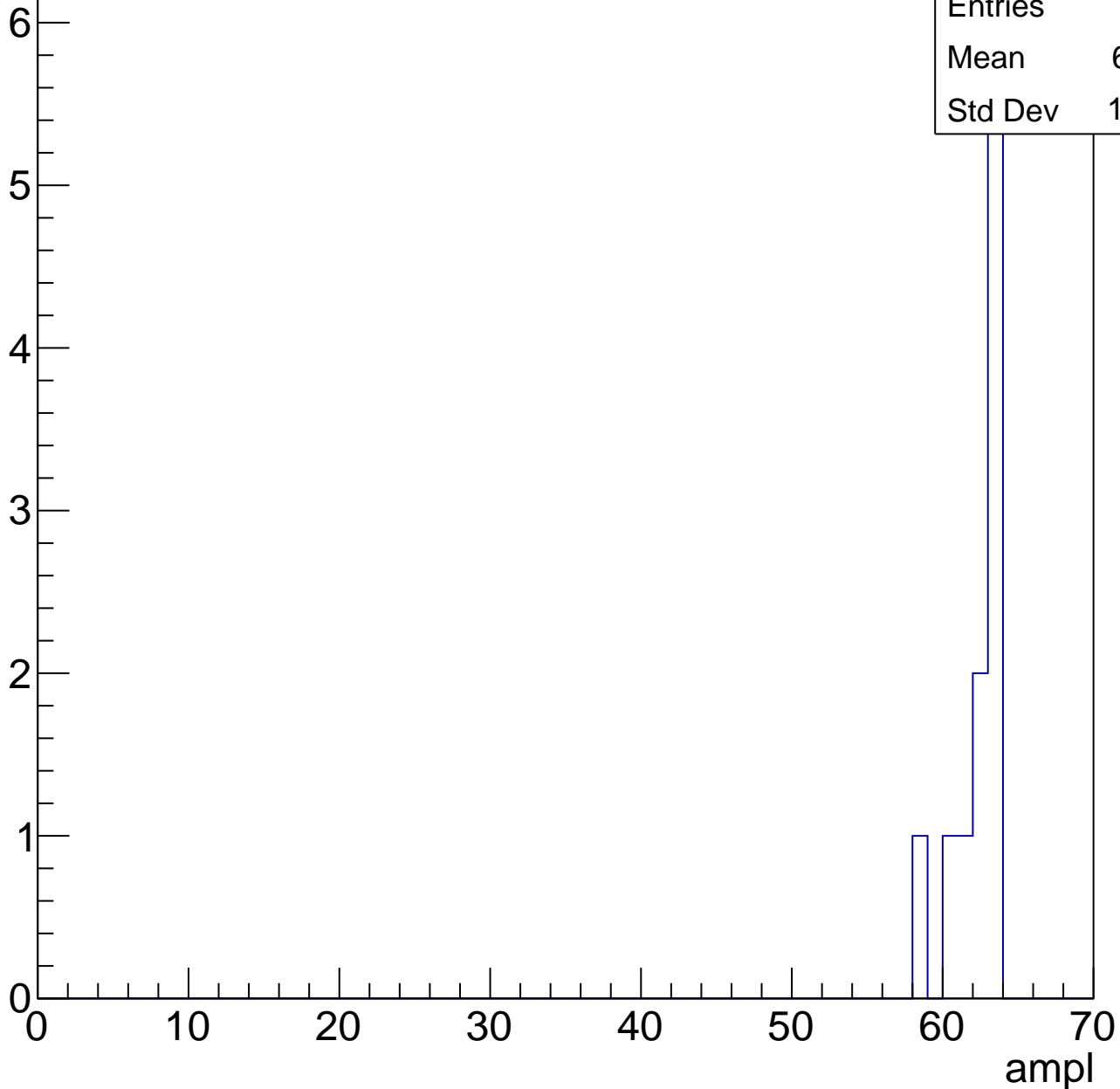


B1L003S, U19-ch49, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	61.91
Std Dev	1.564



B1L003S, U19-ch49, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch50, adc0

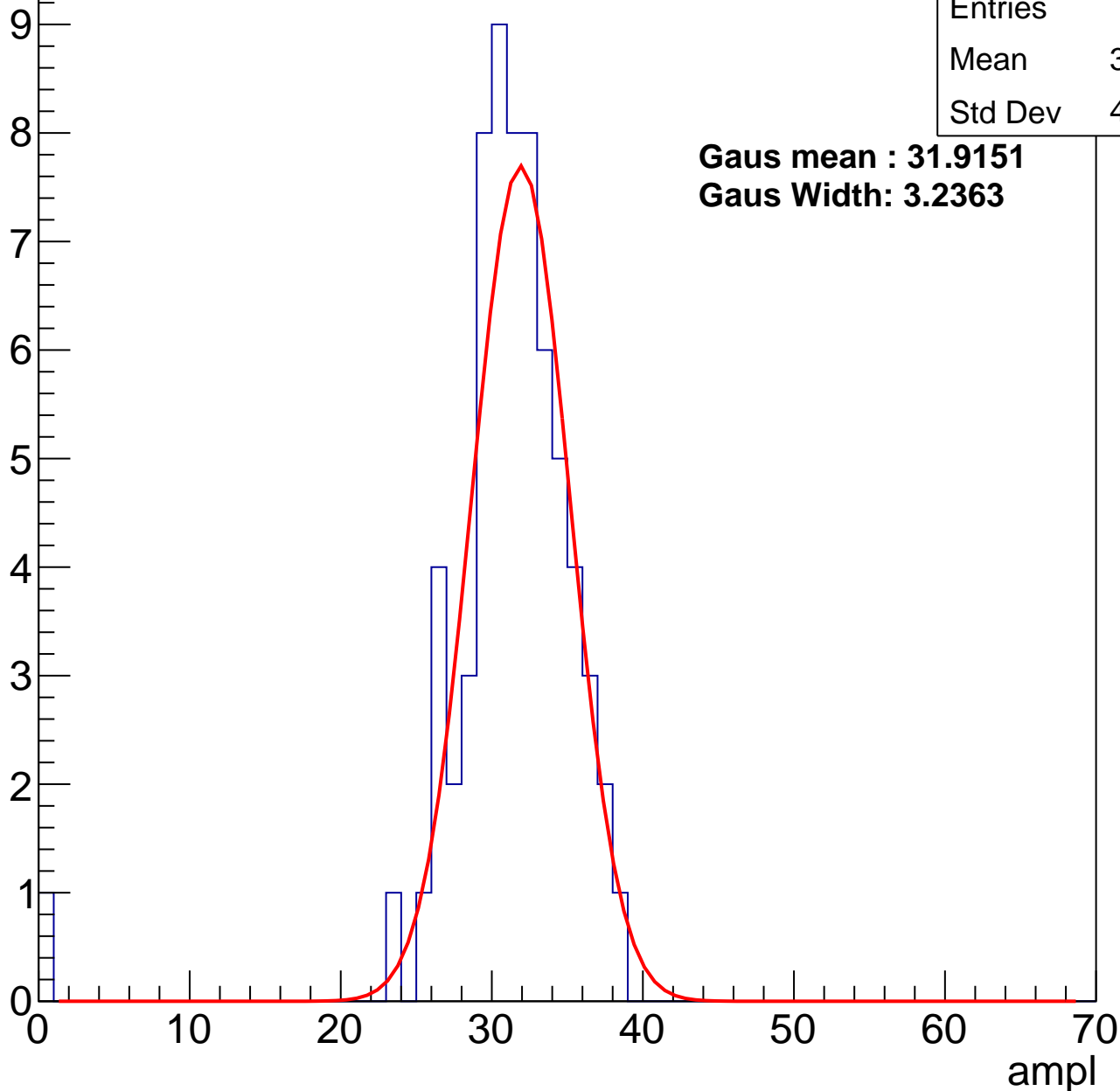
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	30.67
Std Dev	4.909

Gaus mean : 31.9151

Gaus Width: 3.2363



B1L003S, U19-ch50, adc1

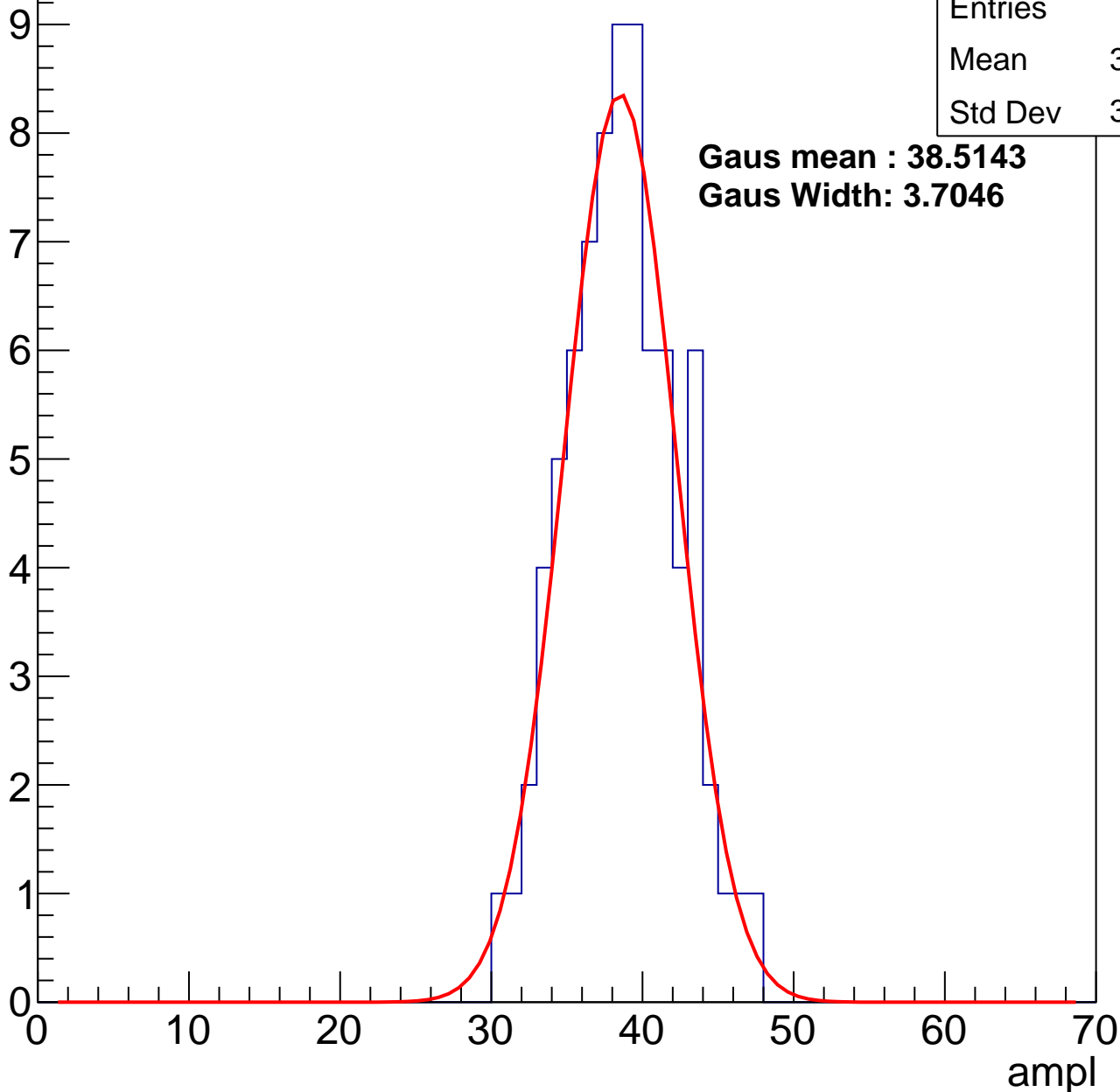
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	38.18
Std Dev	3.592

Gaus mean : 38.5143

Gaus Width: 3.7046



B1L003S, U19-ch50, adc2

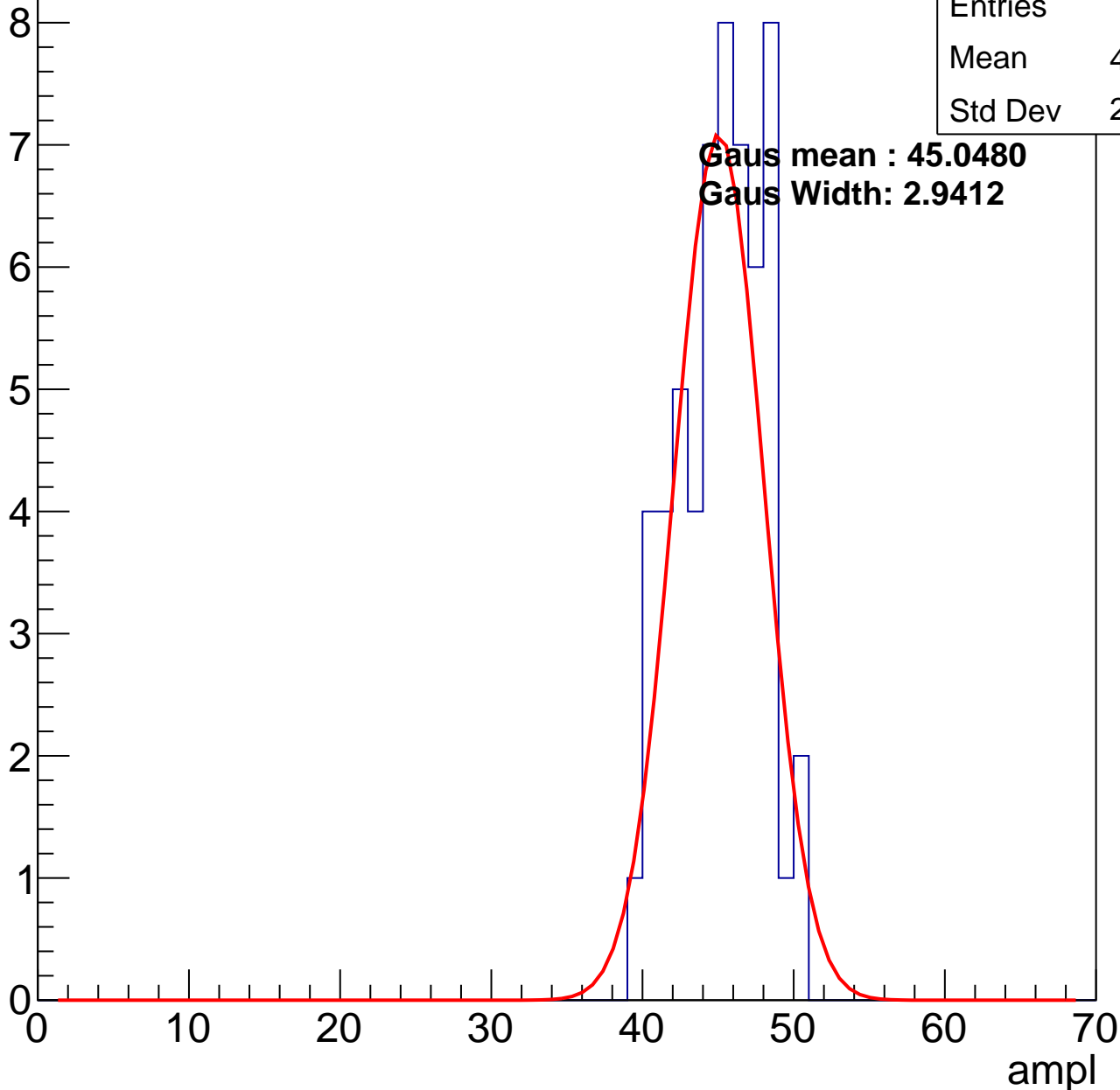
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	44.74
Std Dev	2.756

Gaus mean : 45.0480

Gaus Width: 2.9412

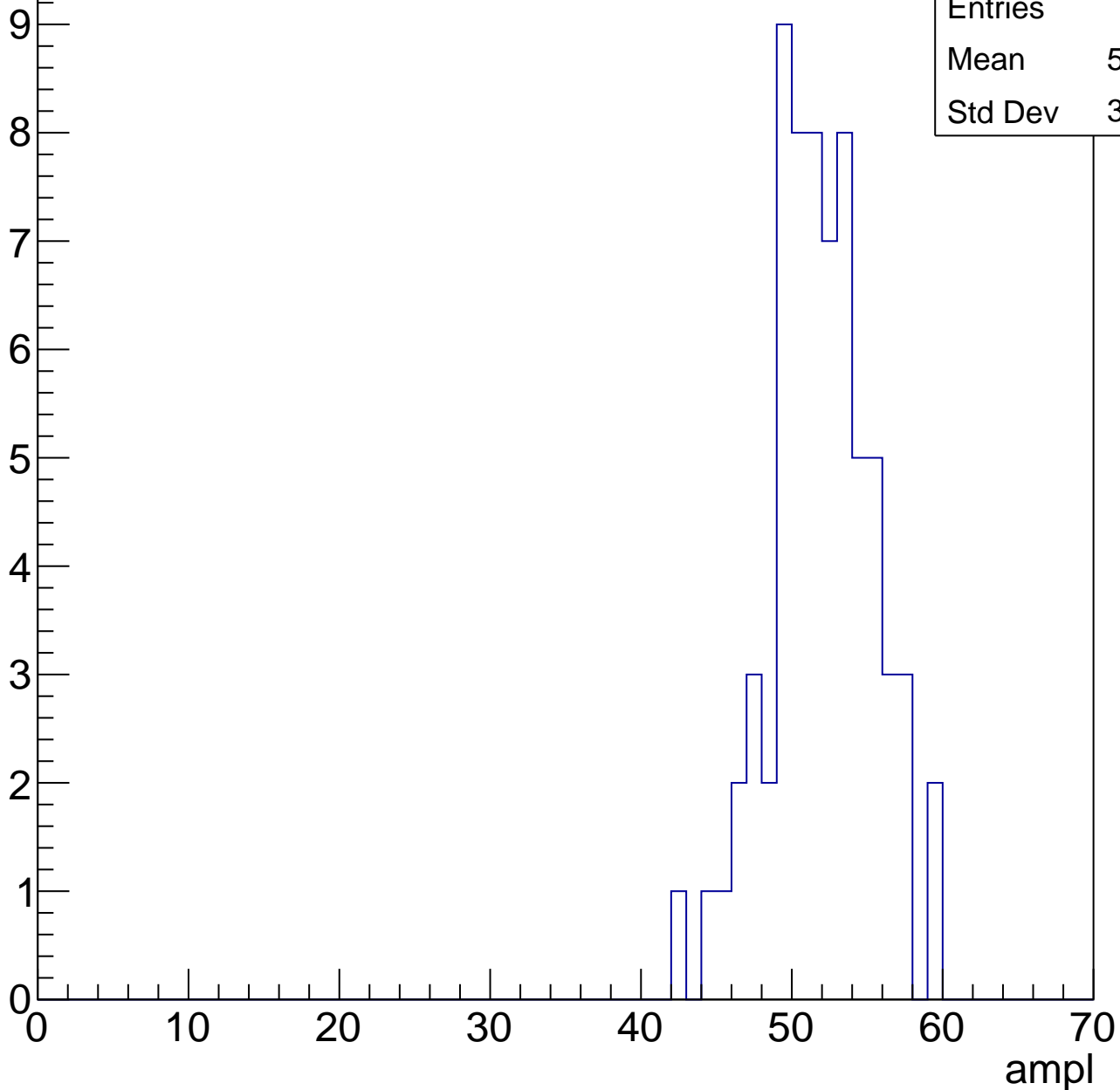


B1L003S, U19-ch50, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	51.46
Std Dev	3.402

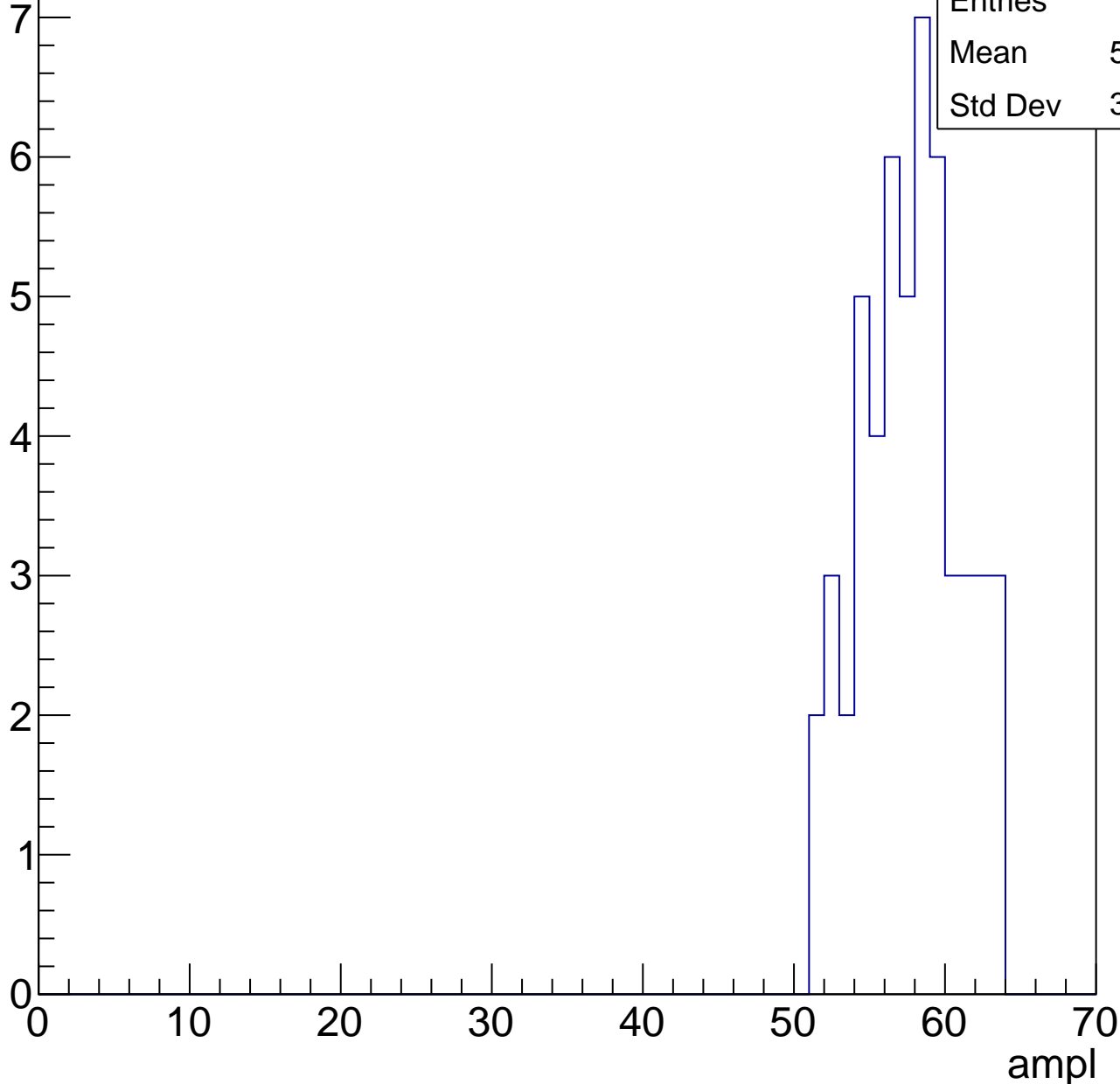


B1L003S, U19-ch50, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

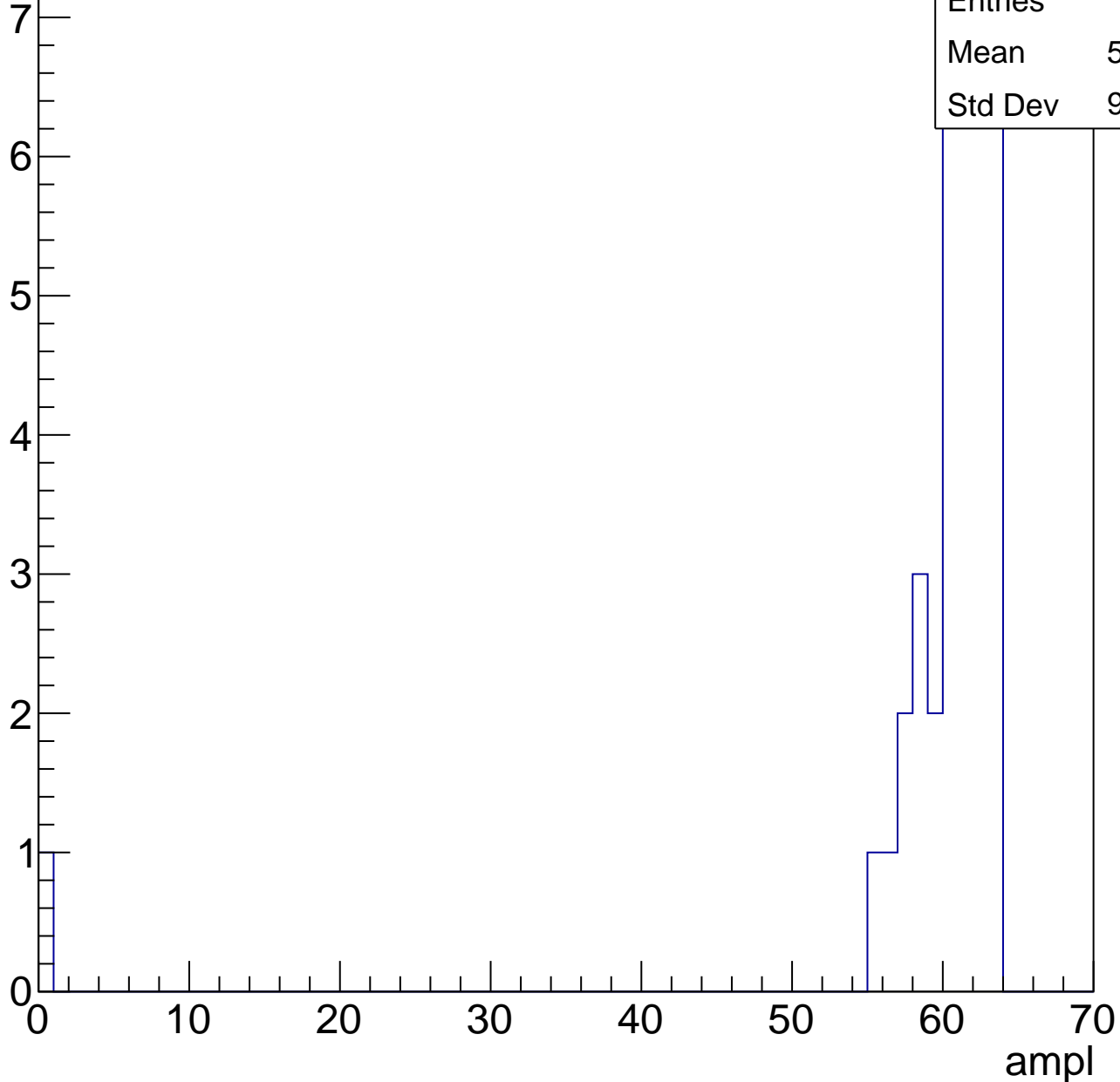
Entries	52
Mean	57.17
Std Dev	3.203



B1L003S, U19-ch50, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch50, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch50, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch51, adc0

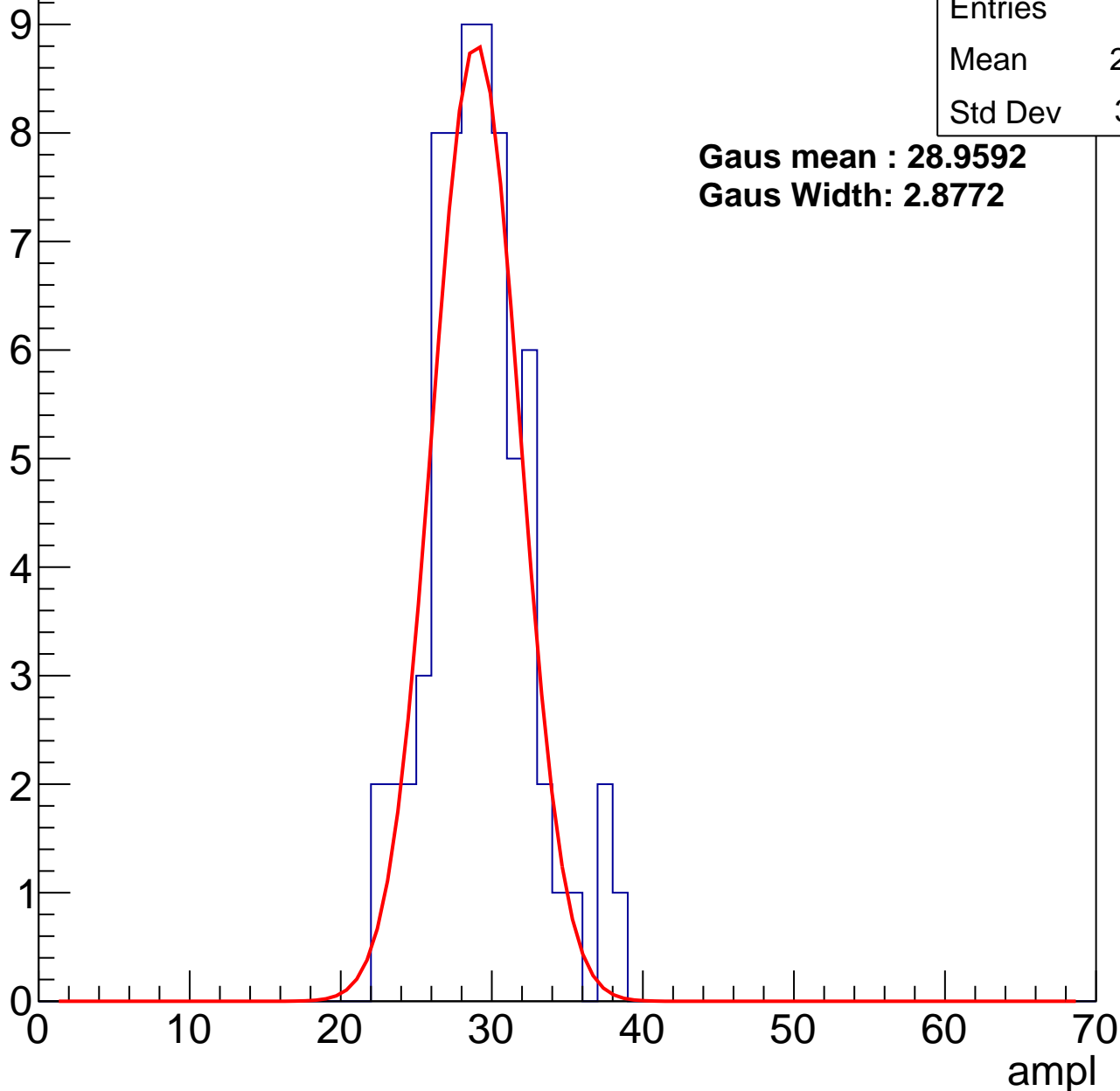
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	28.75
Std Dev	3.321

Gaus mean : 28.9592

Gaus Width: 2.8772



B1L003S, U19-ch51, adc1

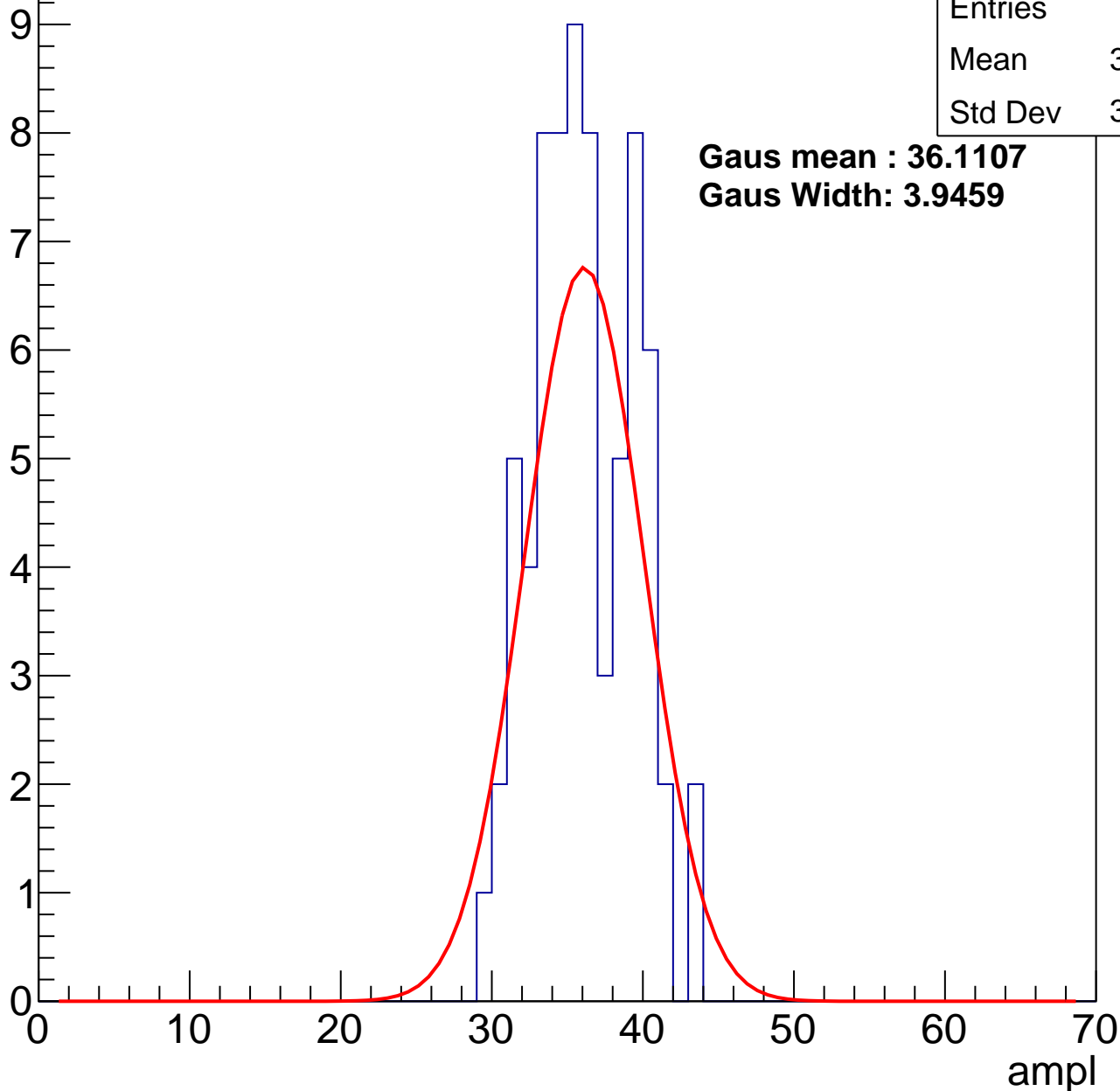
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	35.66
Std Dev	3.259

Gaus mean : 36.1107

Gaus Width: 3.9459



B1L003S, U19-ch51, adc2

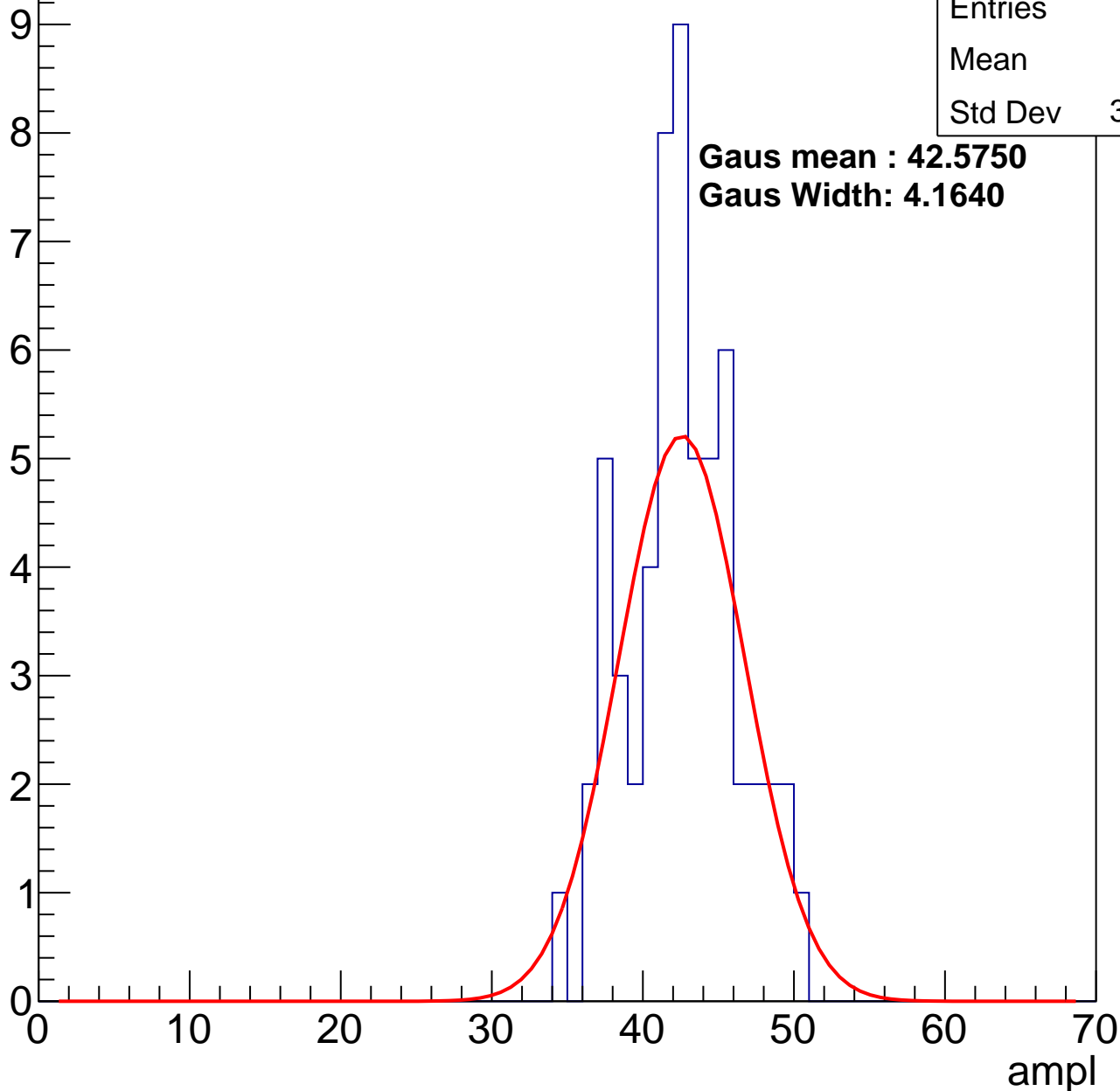
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	42.1
Std Dev	3.554

Gaus mean : 42.5750

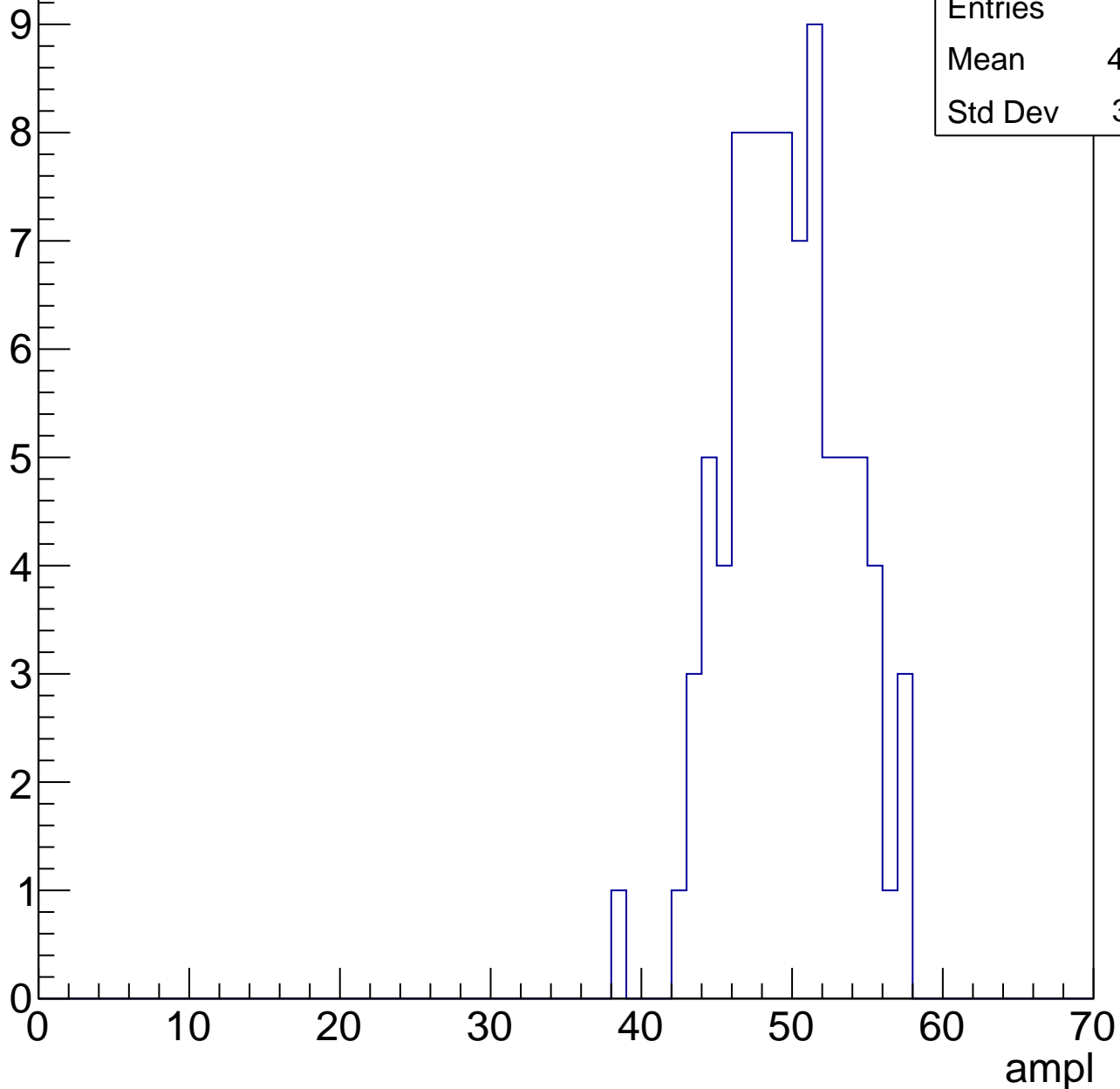
Gaus Width: 4.1640



B1L003S, U19-ch51, adc3

calib_packv5_042523_0143.root, FC#13, port D2

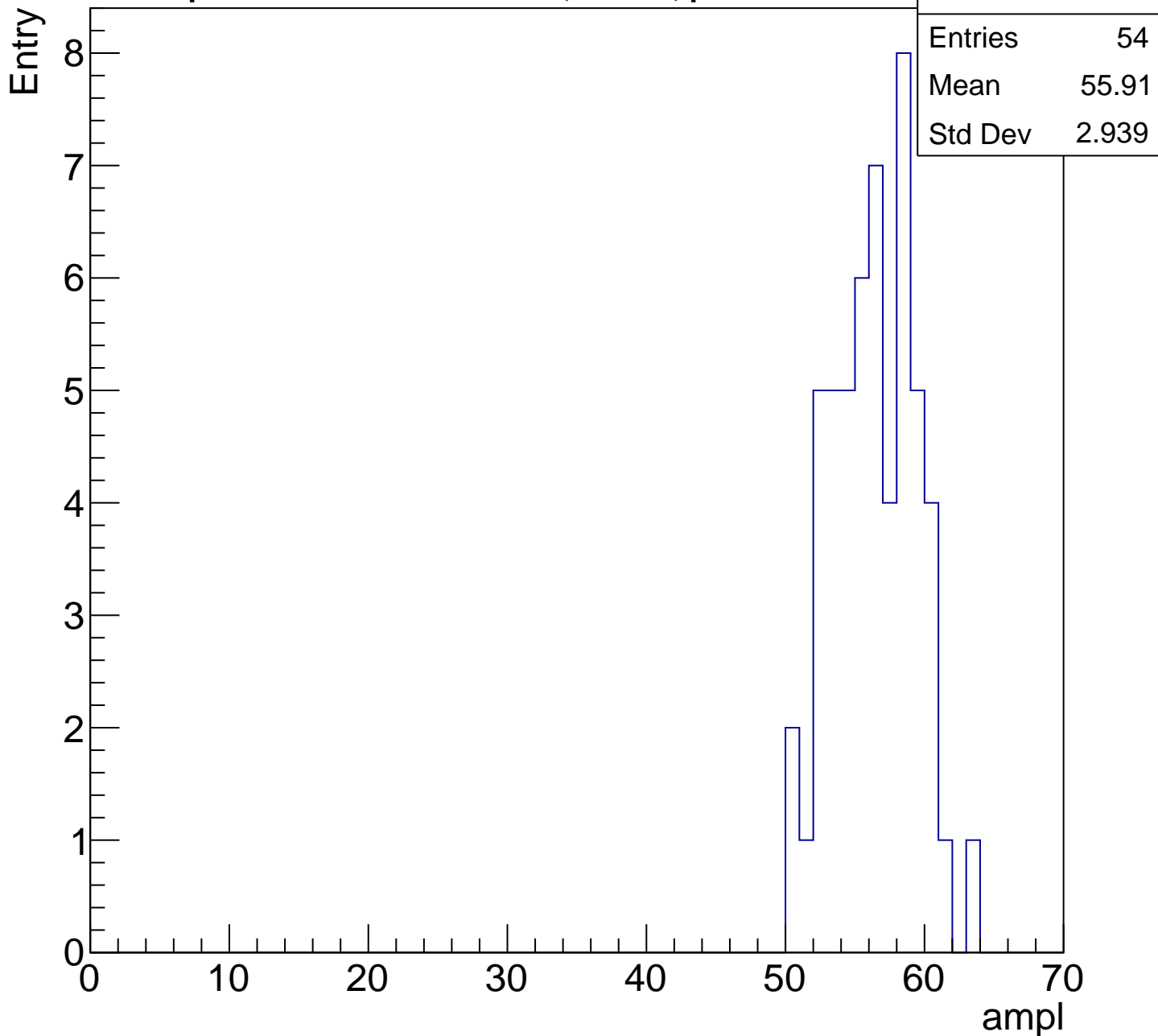
Entry



Entries	85
Mean	49.18
Std Dev	3.841

B1L003S, U19-ch51, adc4

calib_packv5_042523_0143.root, FC#13, port D2

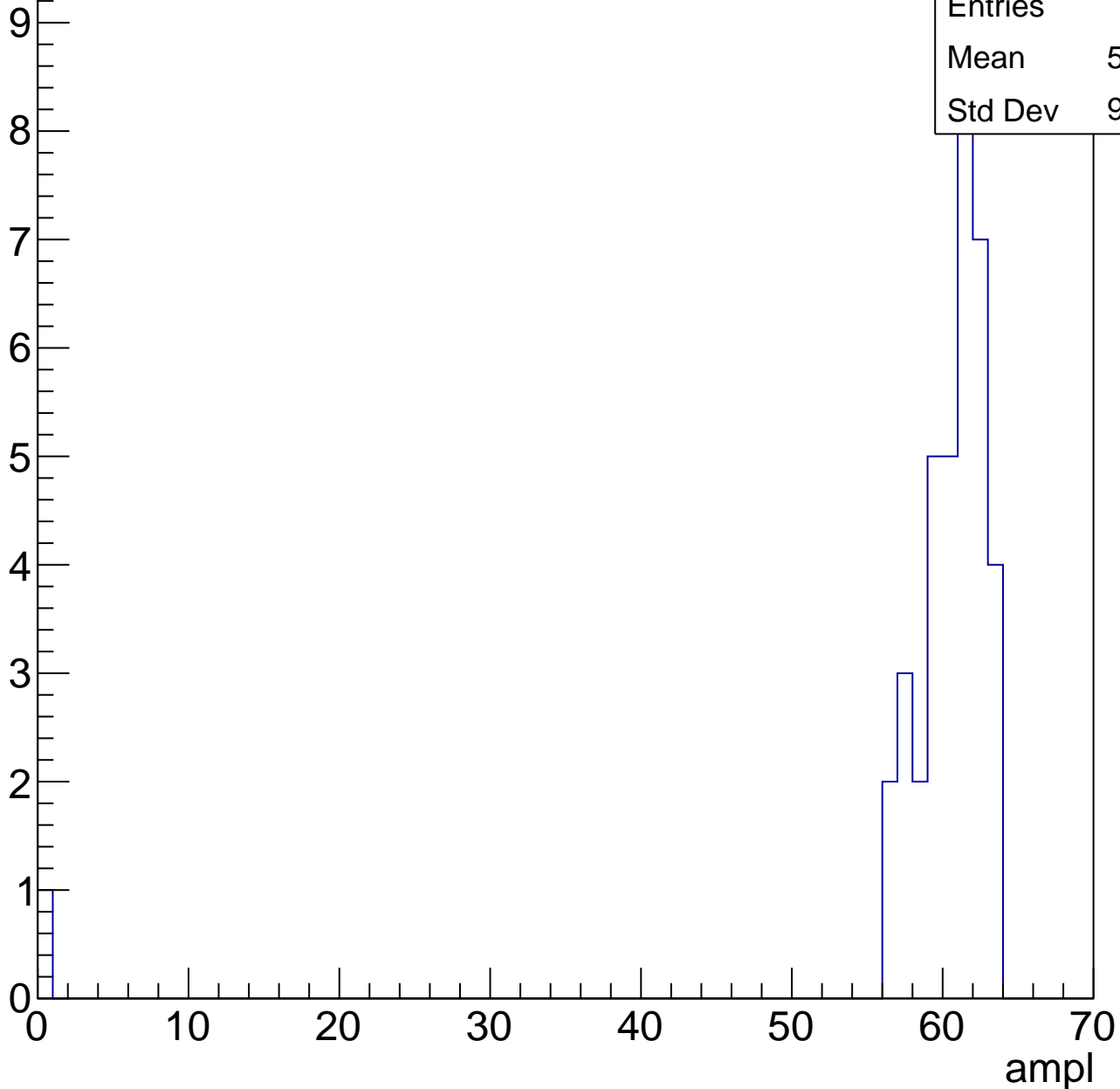


B1L003S, U19-ch51, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	58.66
Std Dev	9.836

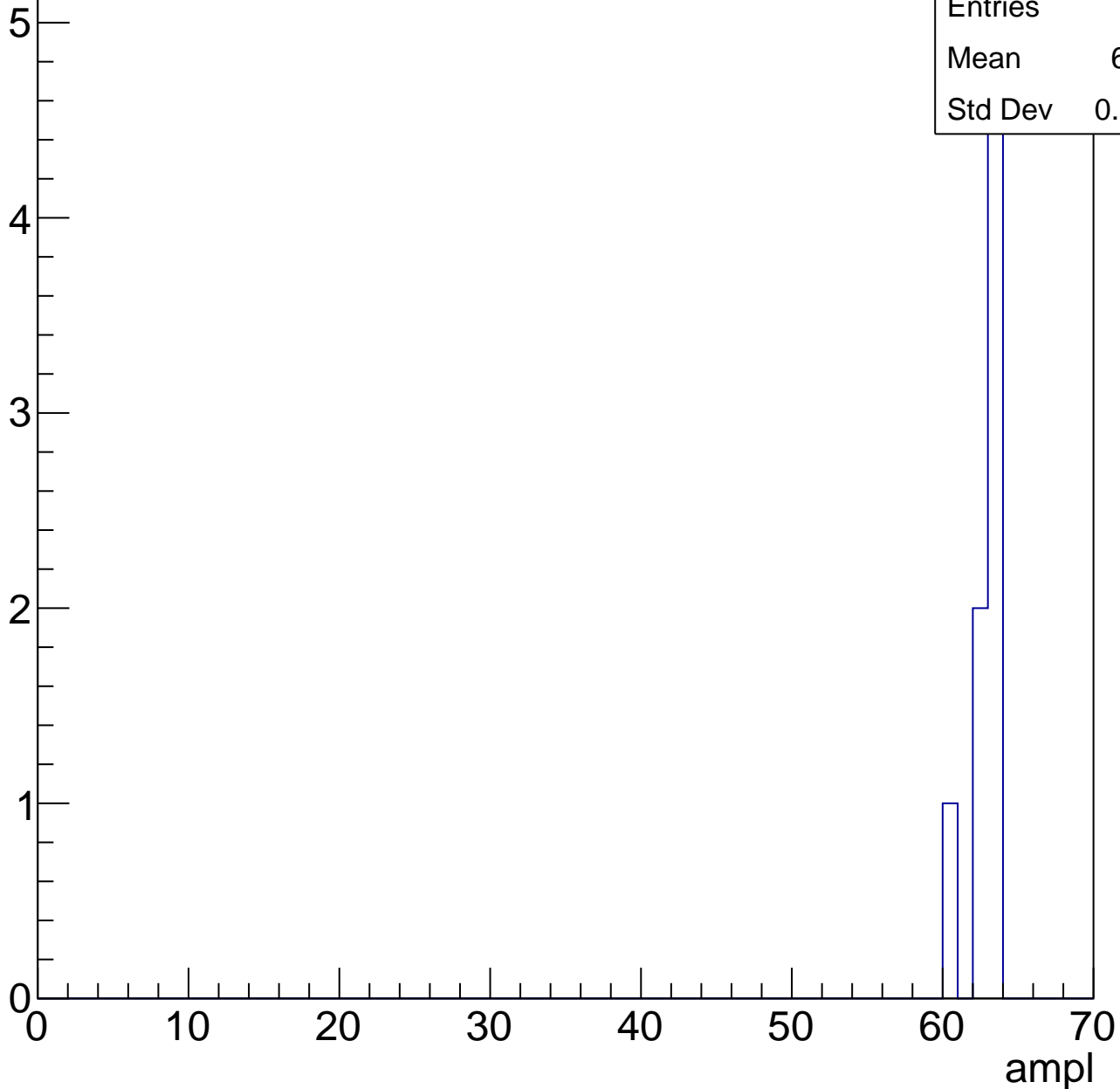


B1L003S, U19-ch51, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

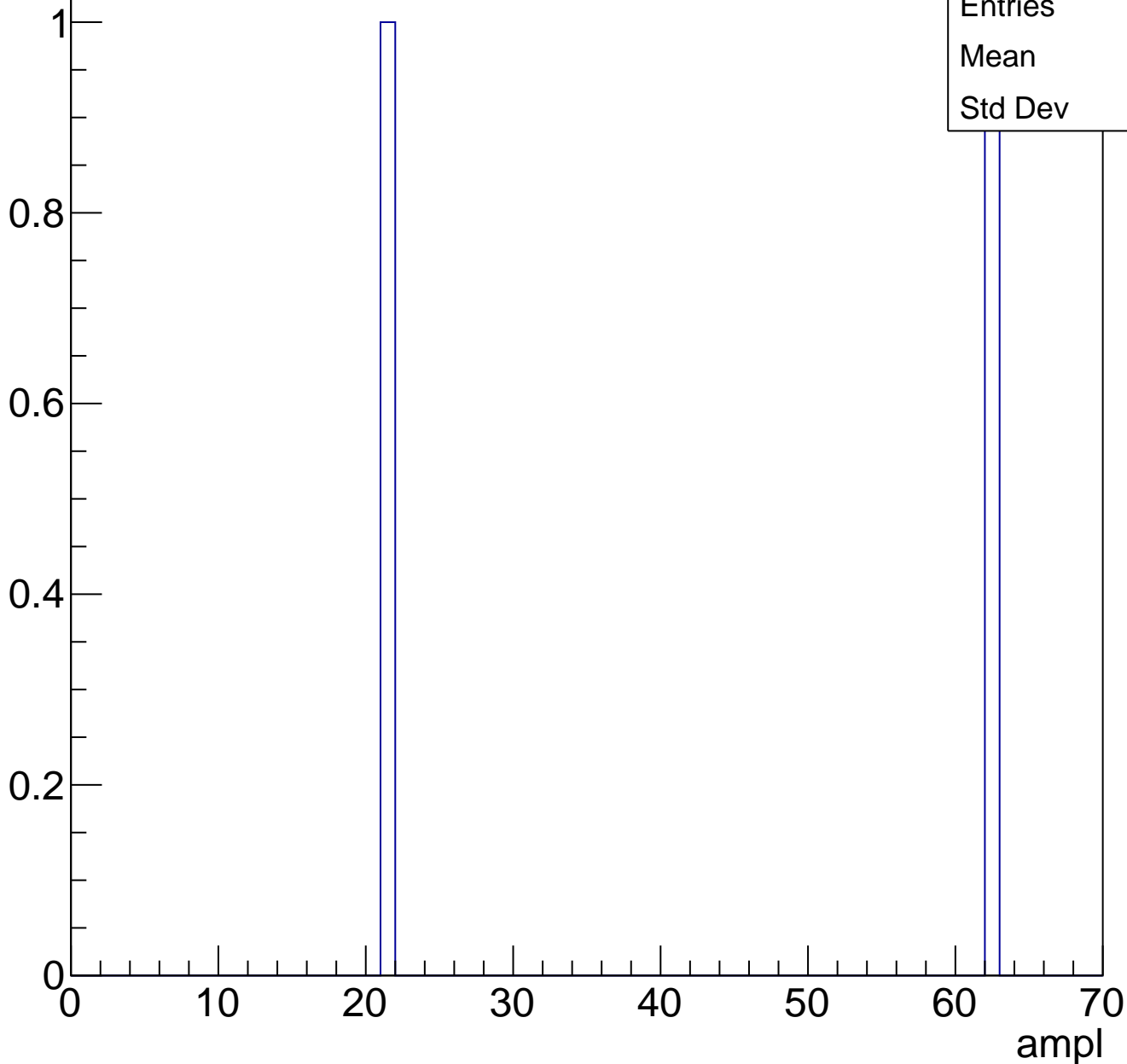
Entries	8
Mean	62.38
Std Dev	0.9922



B1L003S, U19-ch51, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	41.5
Std Dev	20.5

B1L003S, U19-ch52, adc0

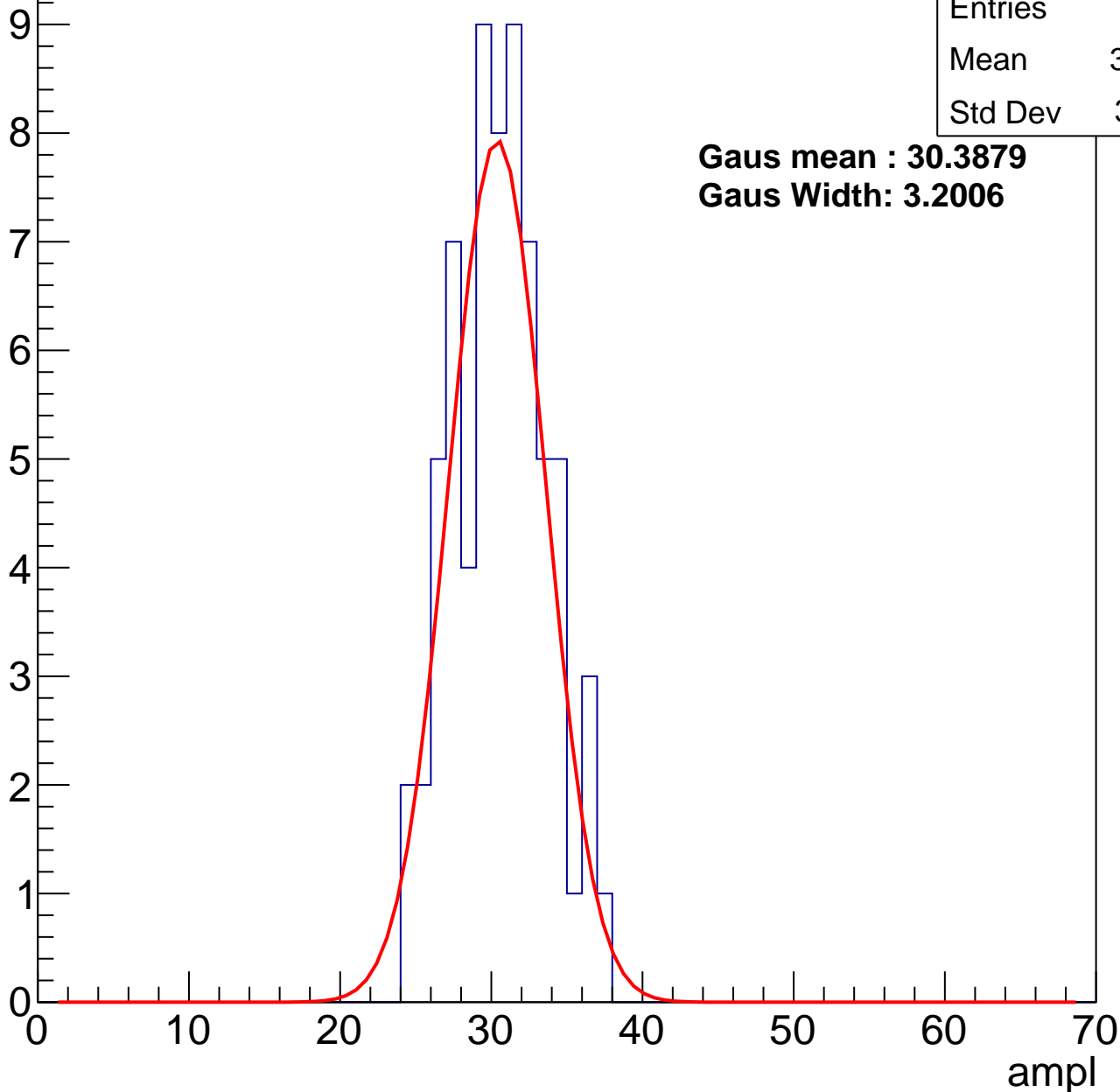
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	30.12
Std Dev	3.051

Gaus mean : 30.3879

Gaus Width: 3.2006



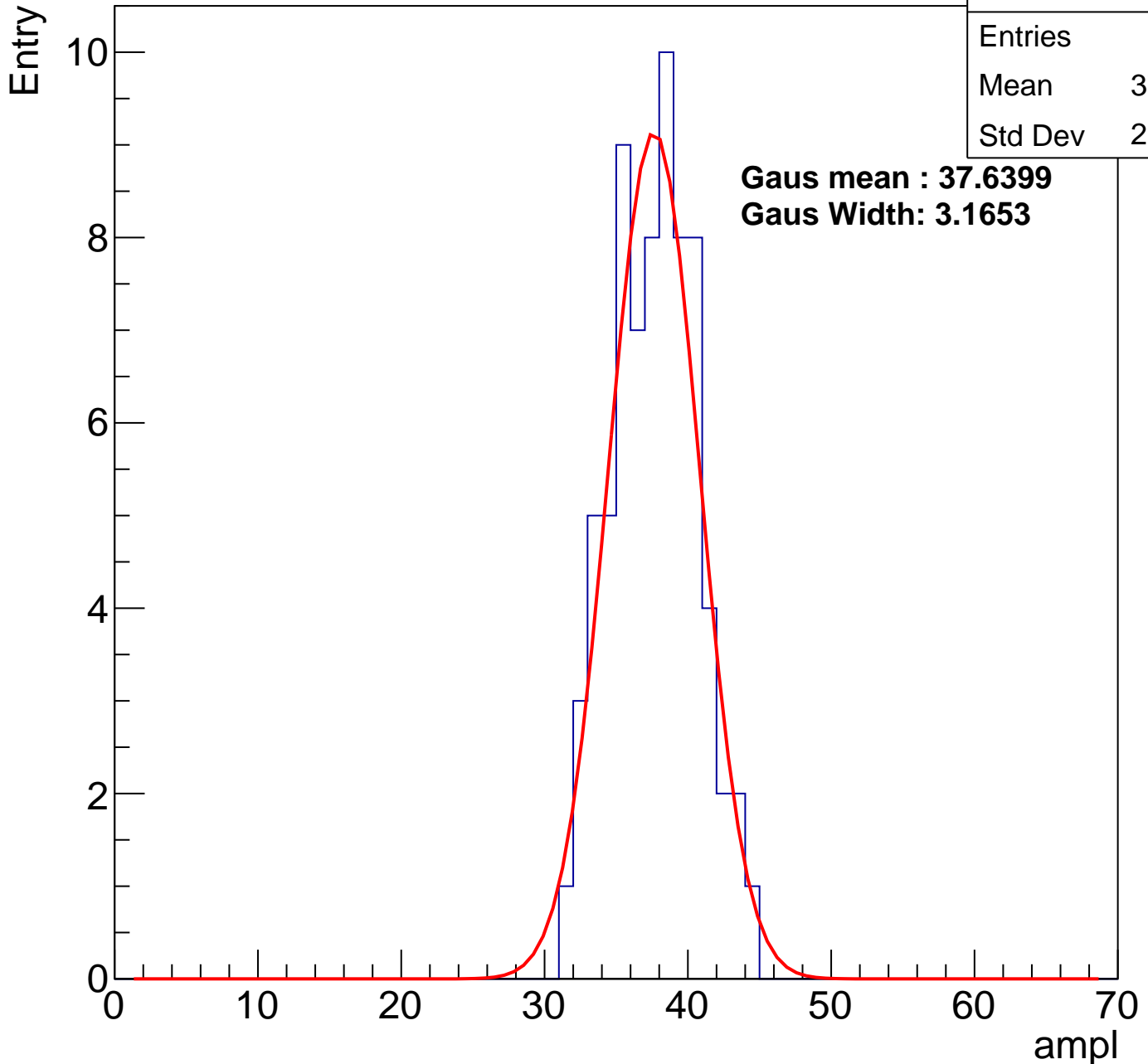
B1L003S, U19-ch52, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	37.19
Std Dev	2.927

Gaus mean : 37.6399

Gaus Width: 3.1653



B1L003S, U19-ch52, adc2

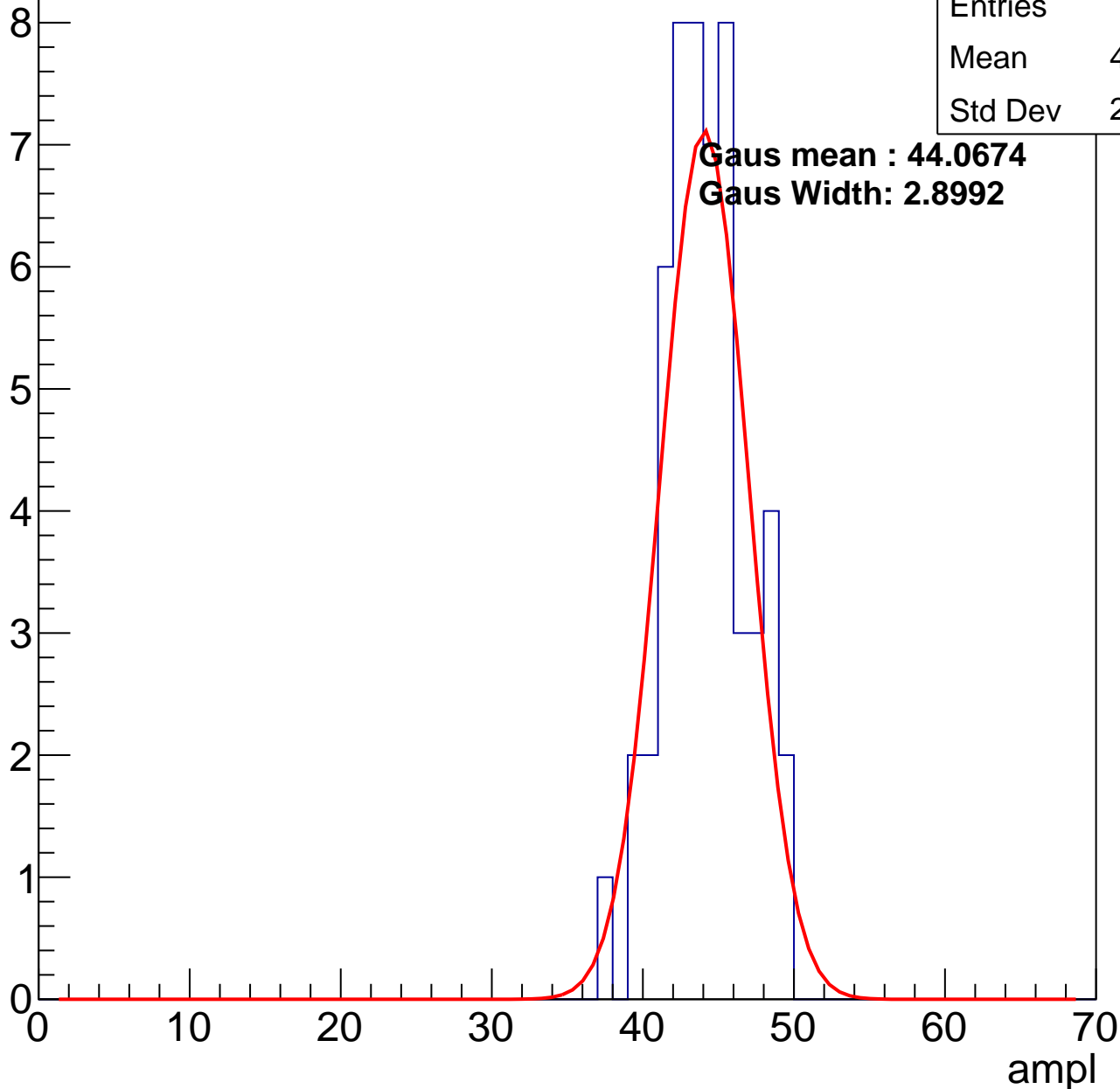
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.67
Std Dev	2.653

Gaus mean : 44.0674

Gaus Width: 2.8992



B1L003S, U19-ch52, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	50.15
Std Dev	3.126

Entry

10

8

6

4

2

0

0

10

20

30

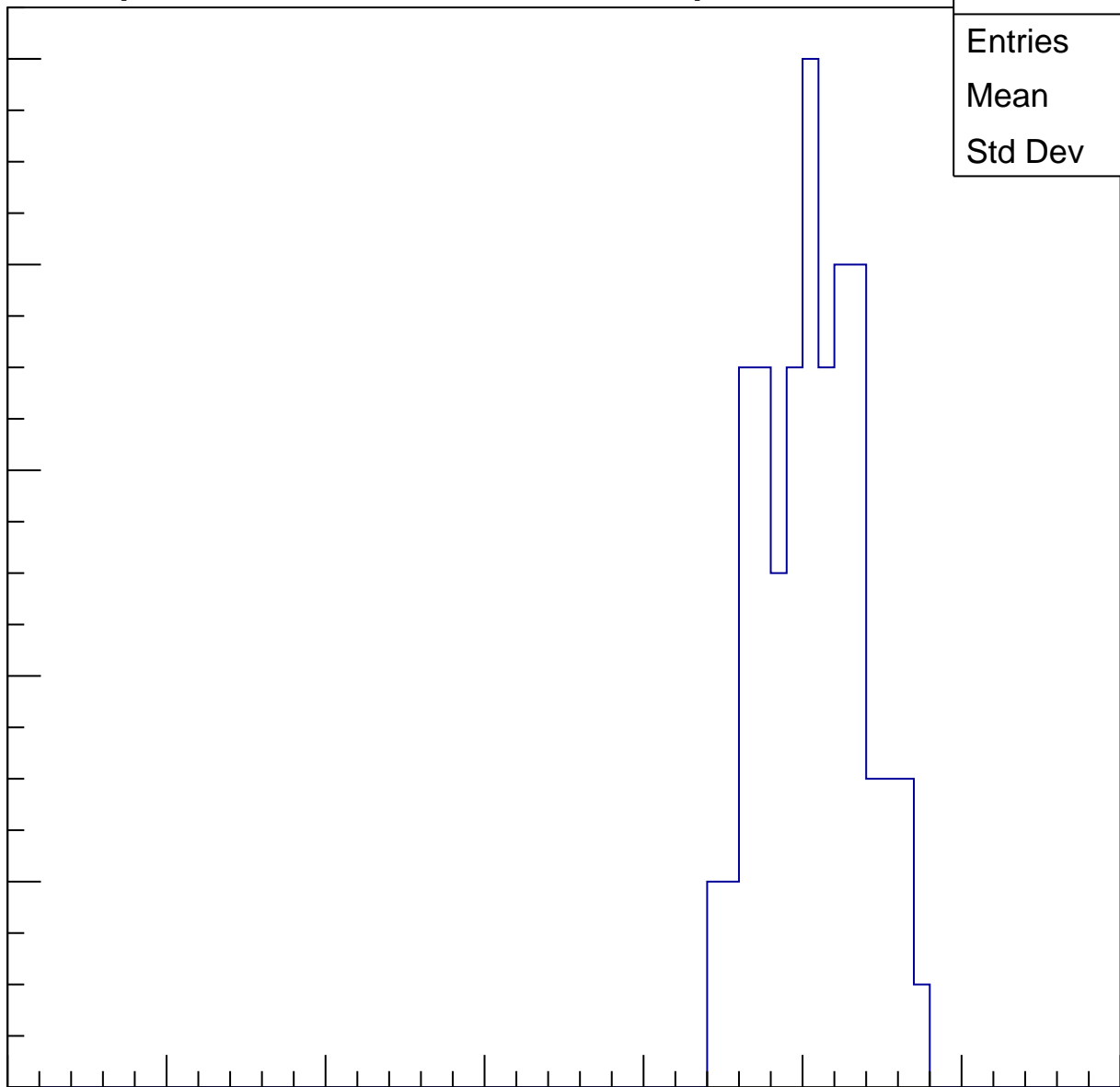
40

50

60

70

ampl

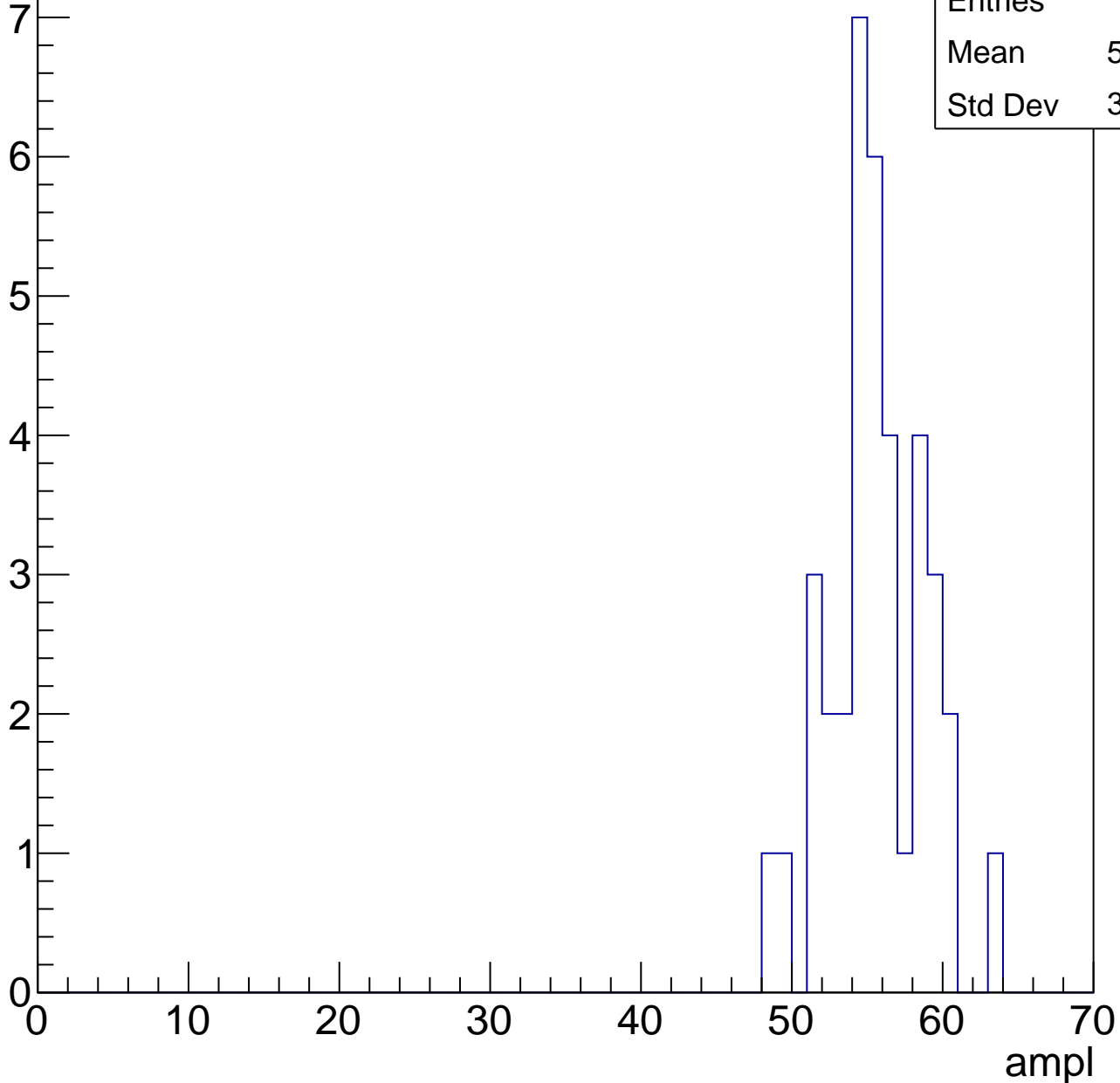


B1L003S, U19-ch52, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	55.16
Std Dev	3.167

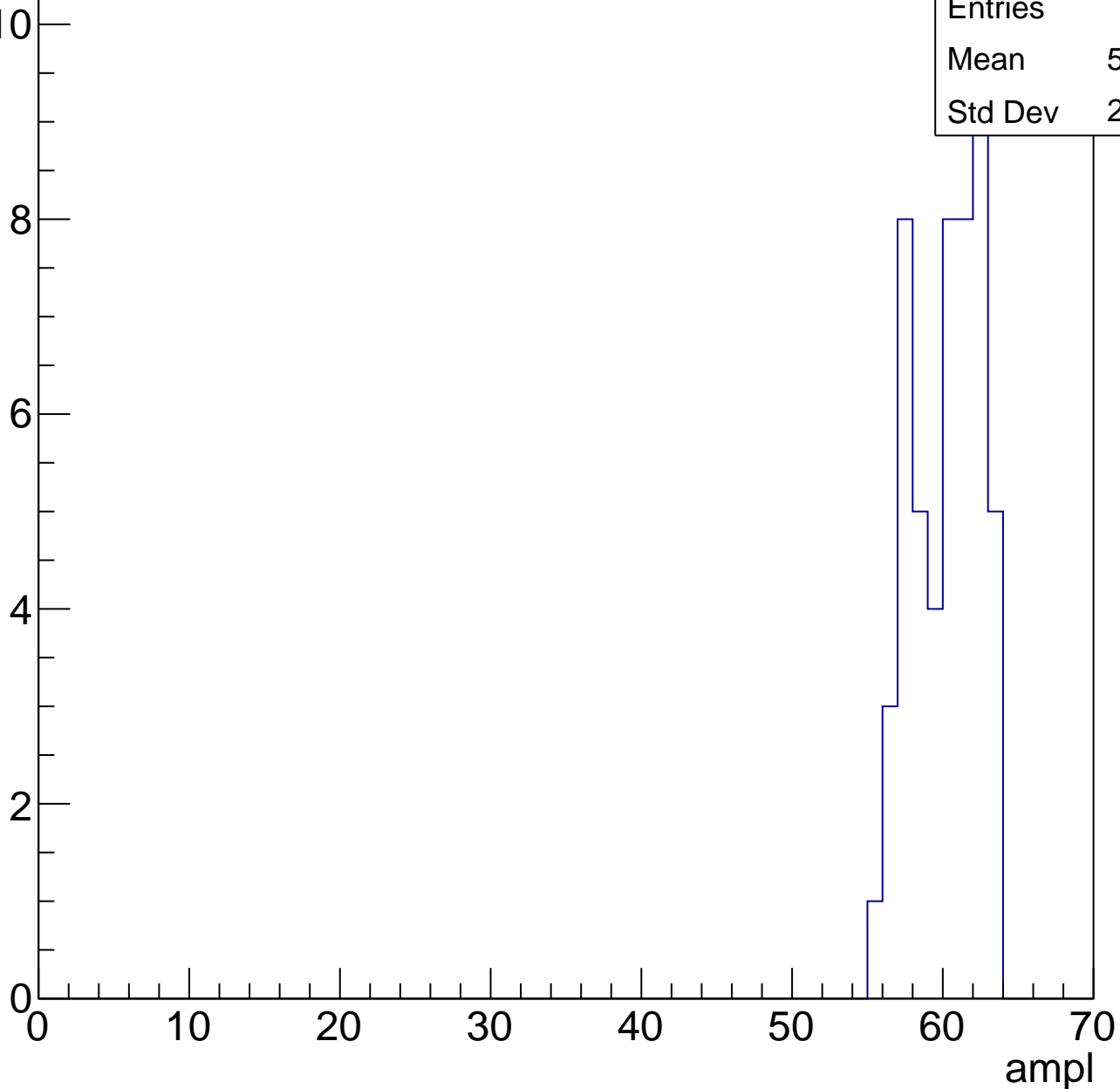


B1L003S, U19-ch52, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

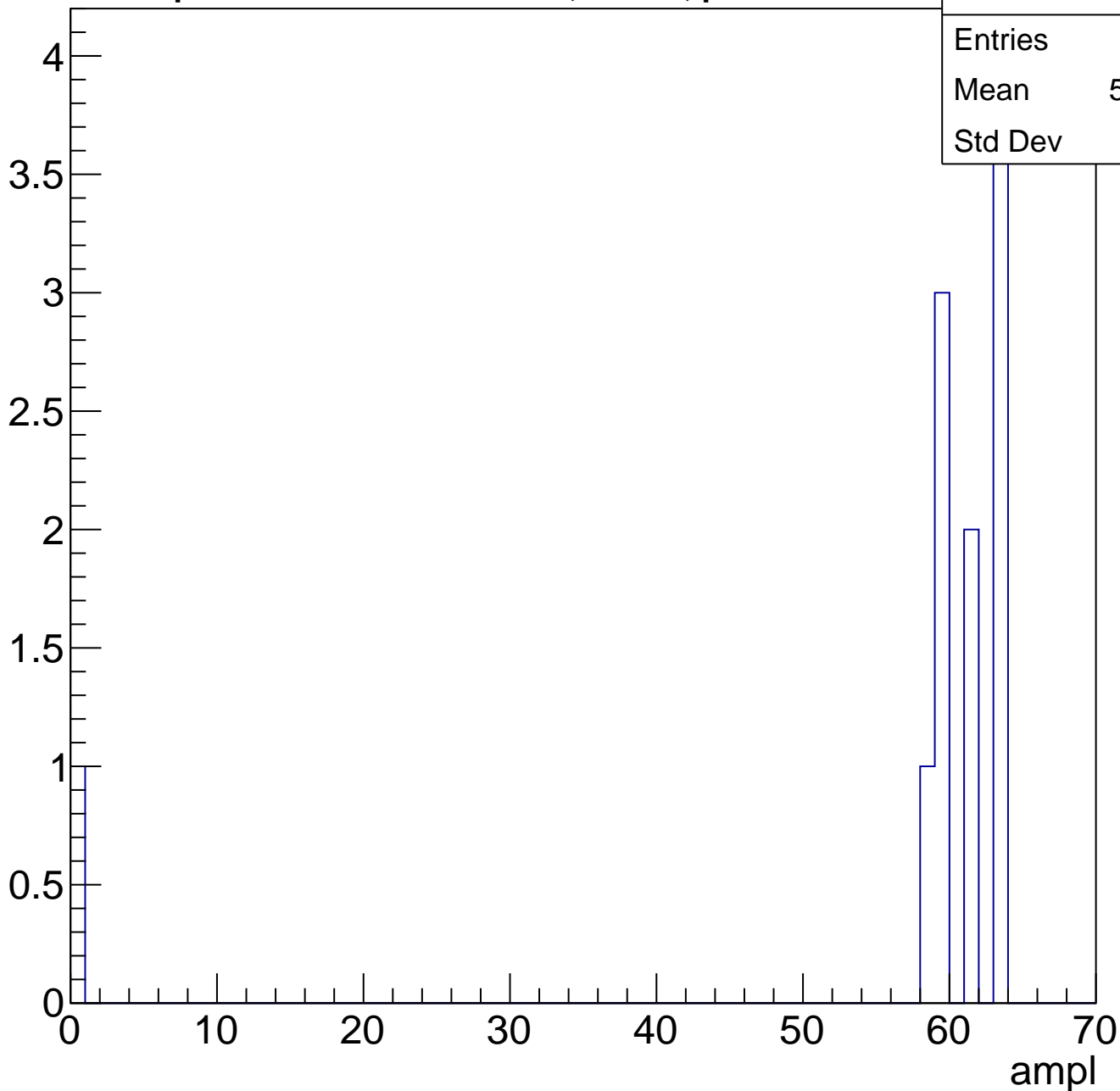
Entries	52
Mean	59.77
Std Dev	2.233



B1L003S, U19-ch52, adc6

calib_packv5_042523_0143.root, FC#13, port D2

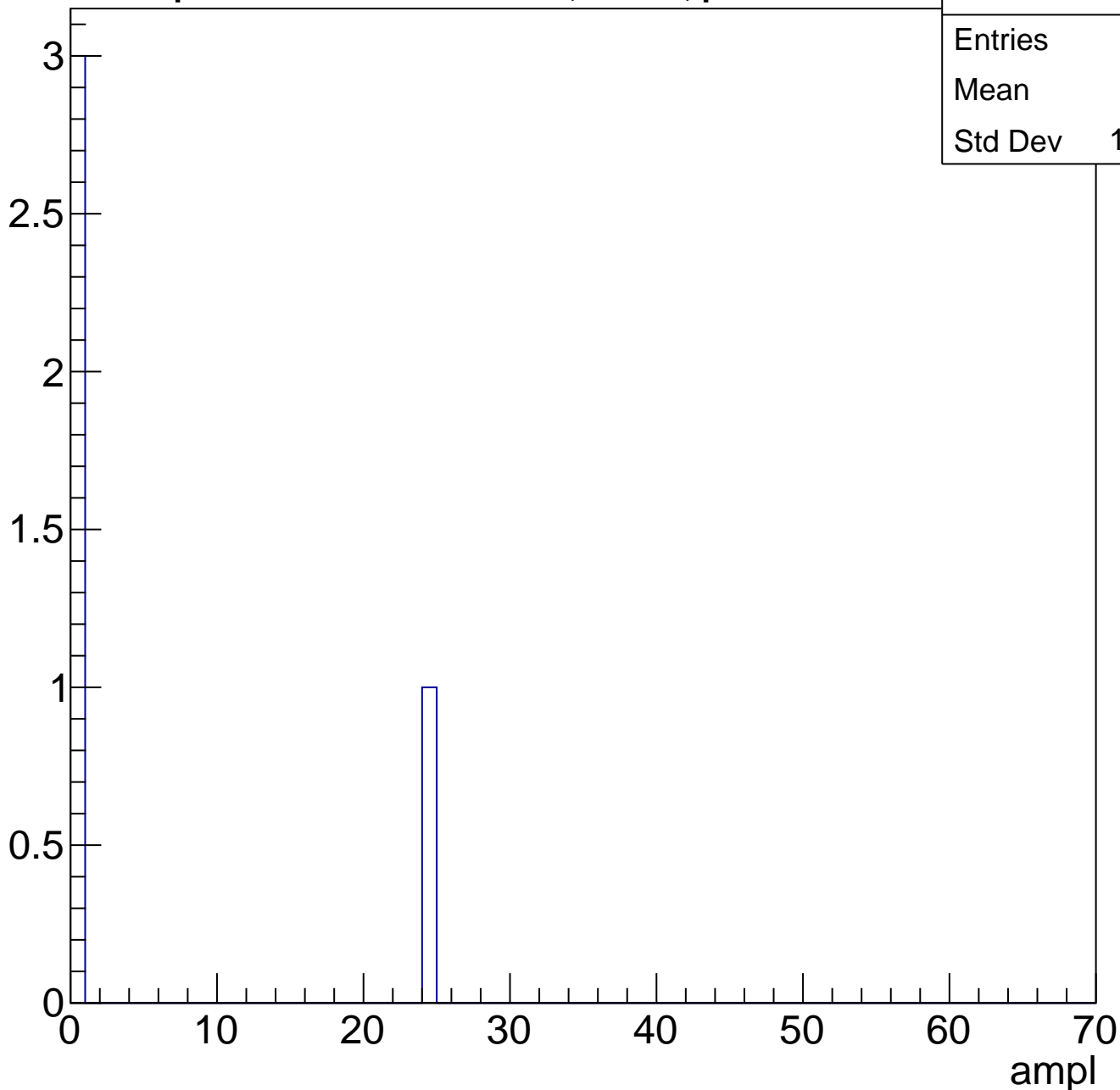
Entry



B1L003S, U19-ch52, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	6
Std Dev	10.39

B1L003S, U19-ch53, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	29.13
Std Dev	4.943

Gaus mean : 30.2794

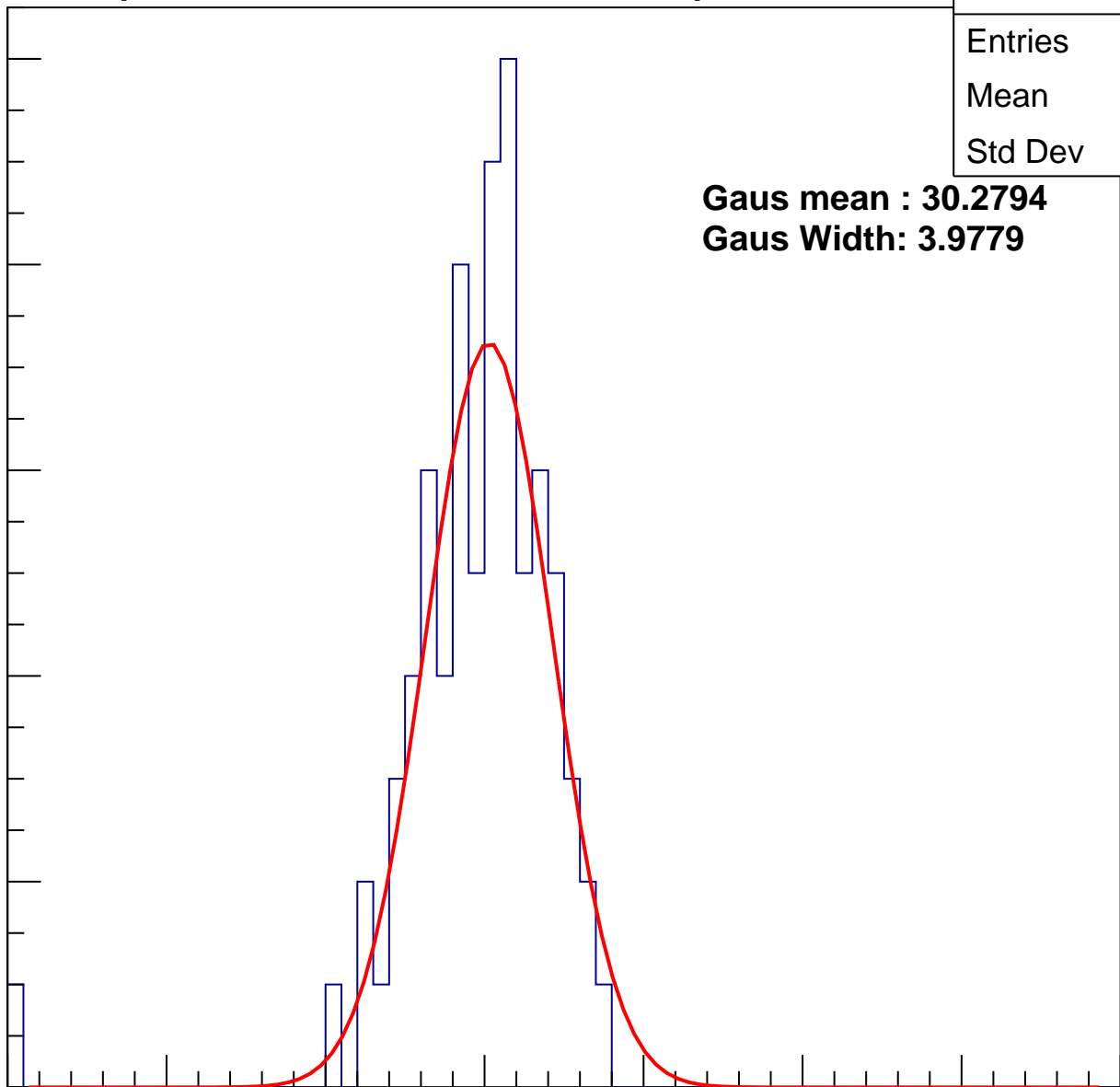
Gaus Width: 3.9779

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



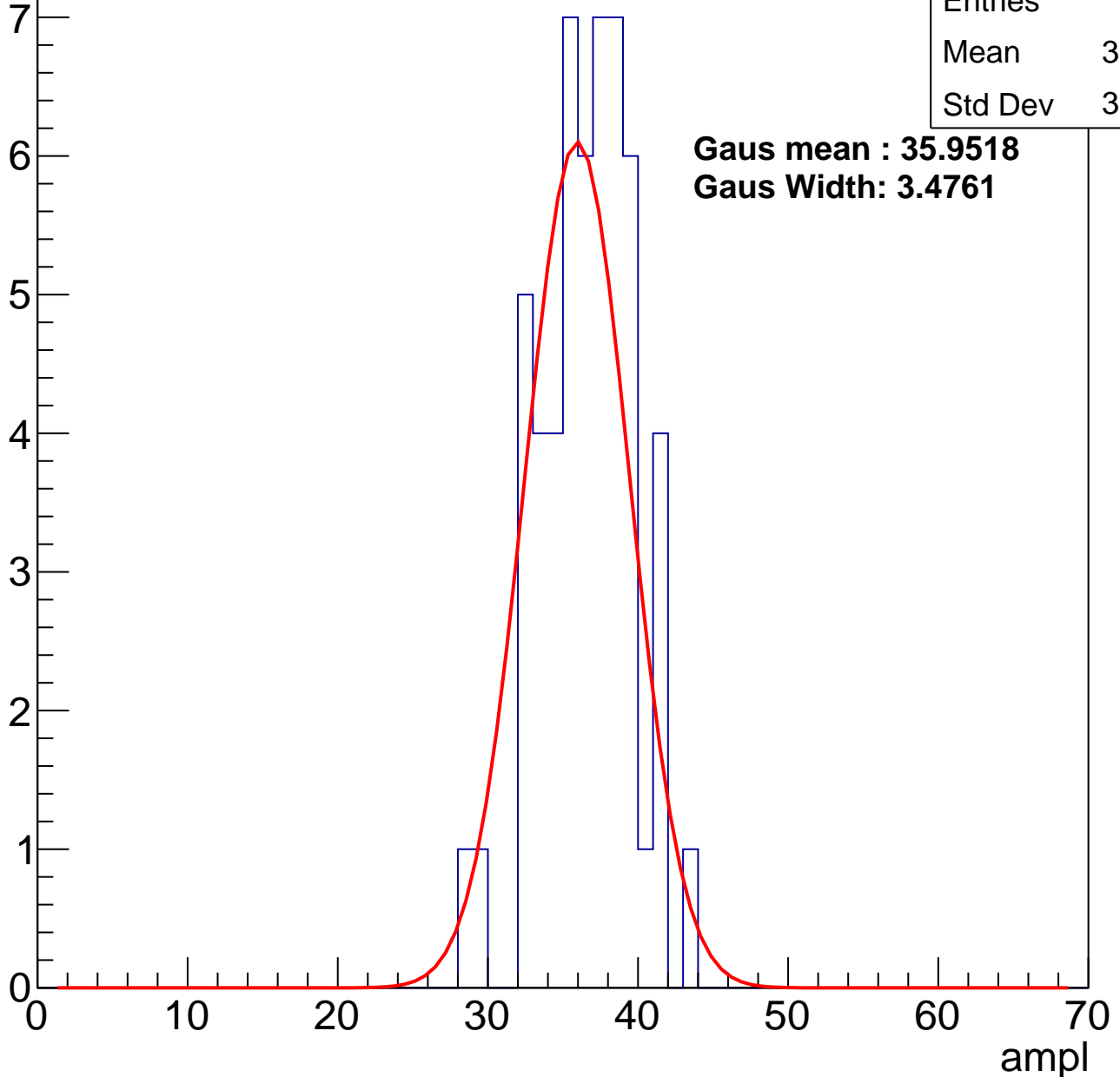
B1L003S, U19-ch53, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.15
Std Dev	3.058

Gaus mean : 35.9518
Gaus Width: 3.4761



B1L003S, U19-ch53, adc2

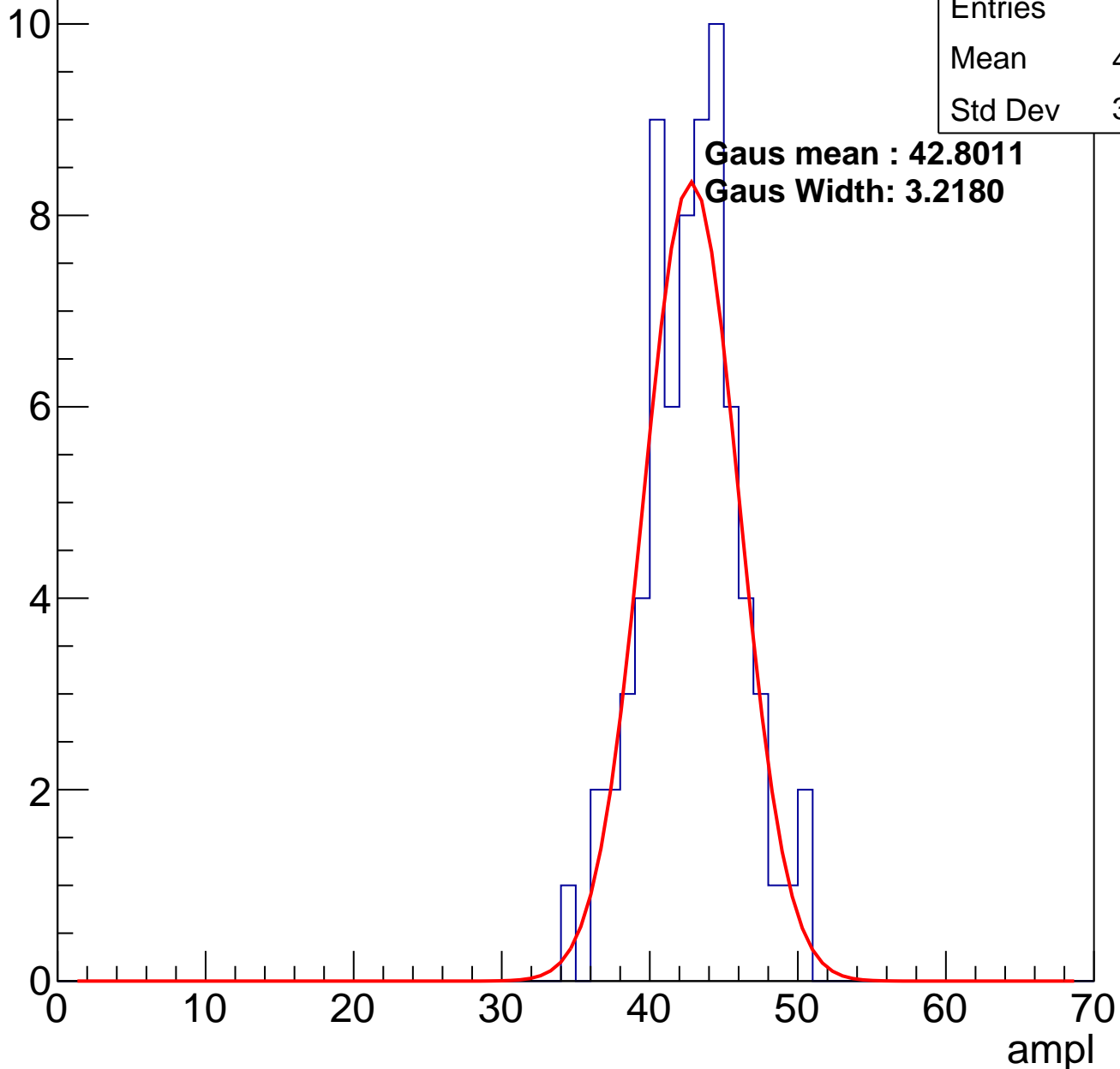
calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	42.41
Std Dev	3.261

Gaus mean : 42.8011

Gaus Width: 3.2180

Entry

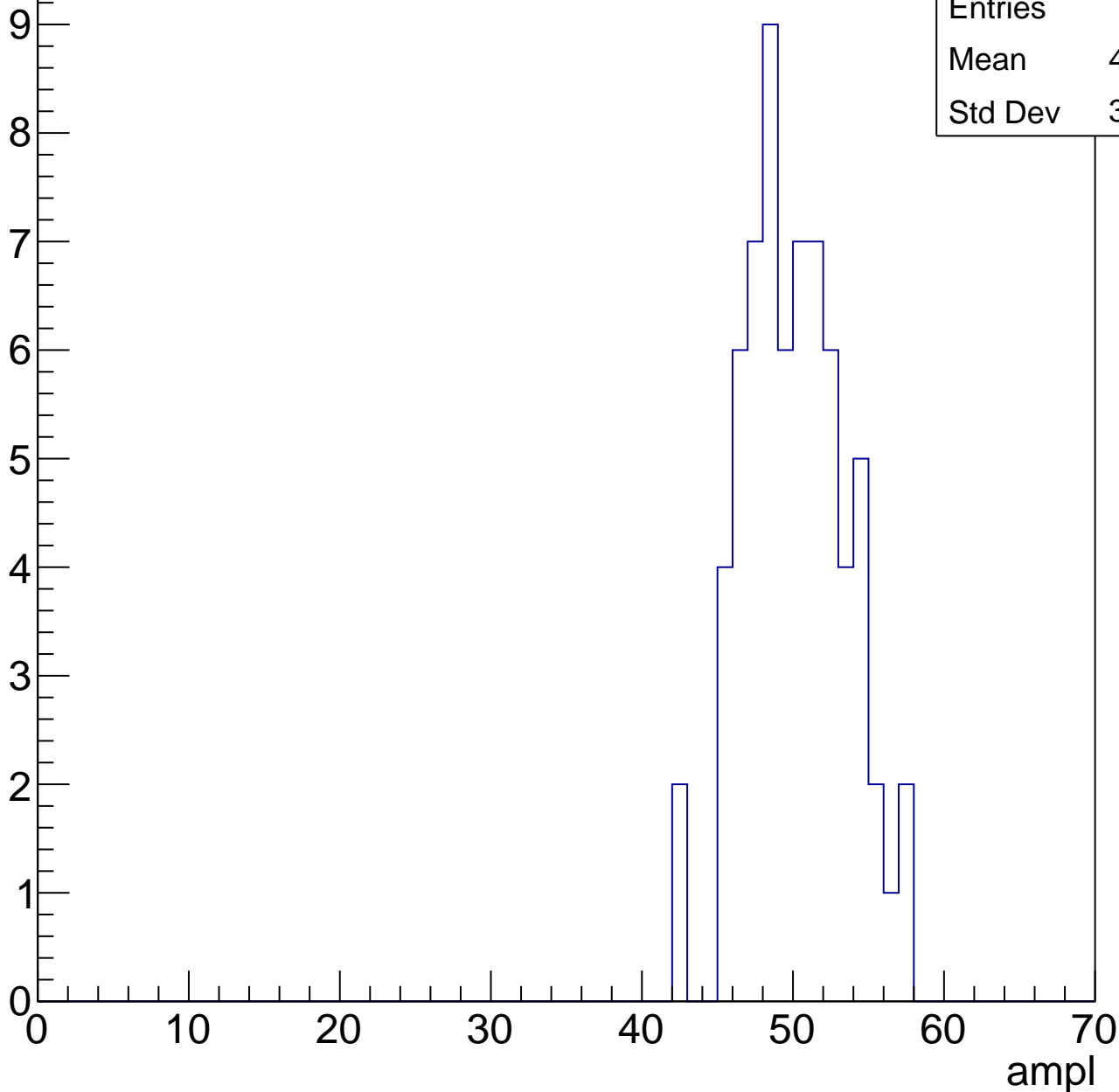


B1L003S, U19-ch53, adc3

calib_packv5_042523_0143.root, FC#13, port D2

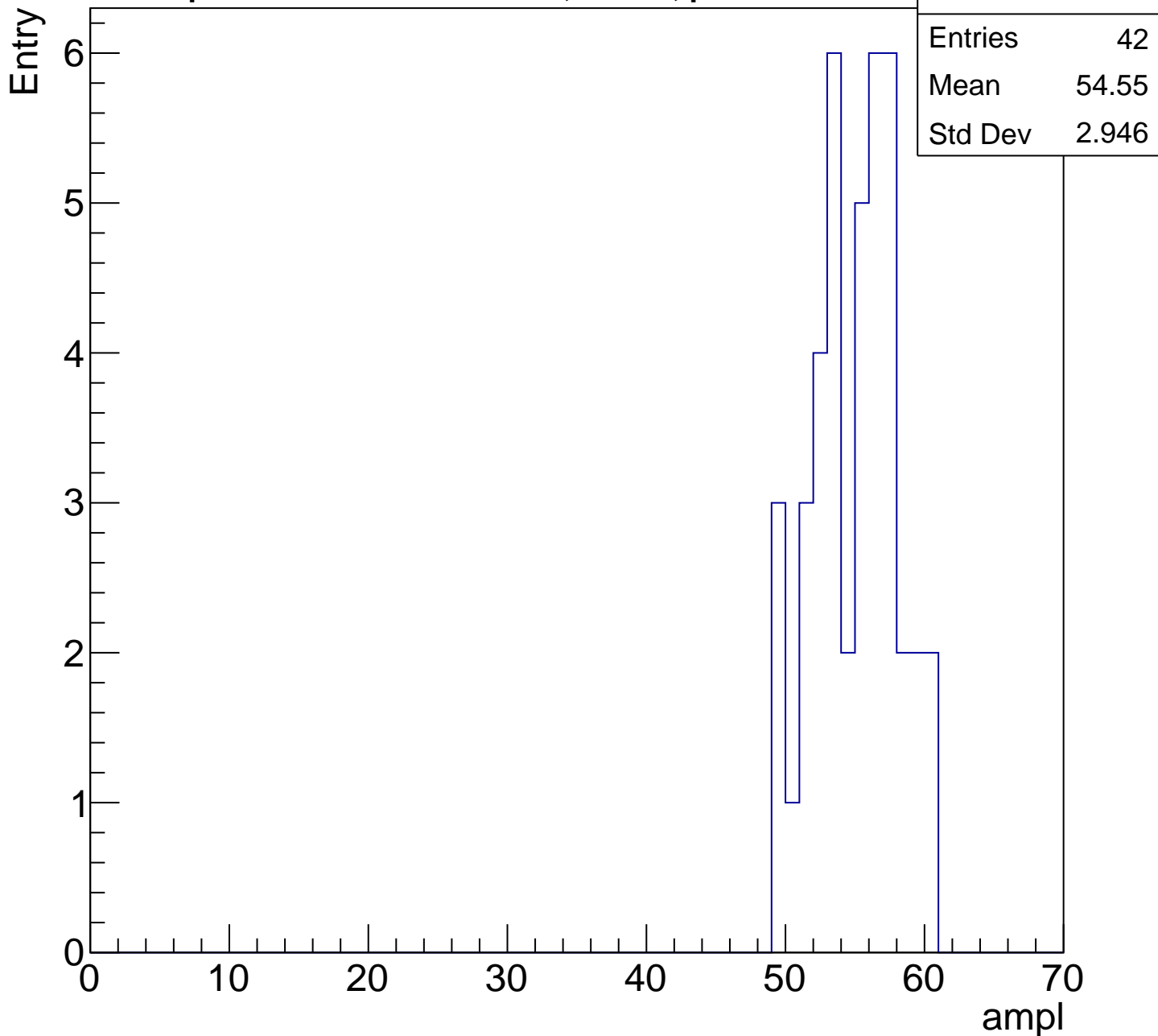
Entry

Entries	68
Mean	49.65
Std Dev	3.324



B1L003S, U19-ch53, adc4

calib_packv5_042523_0143.root, FC#13, port D2

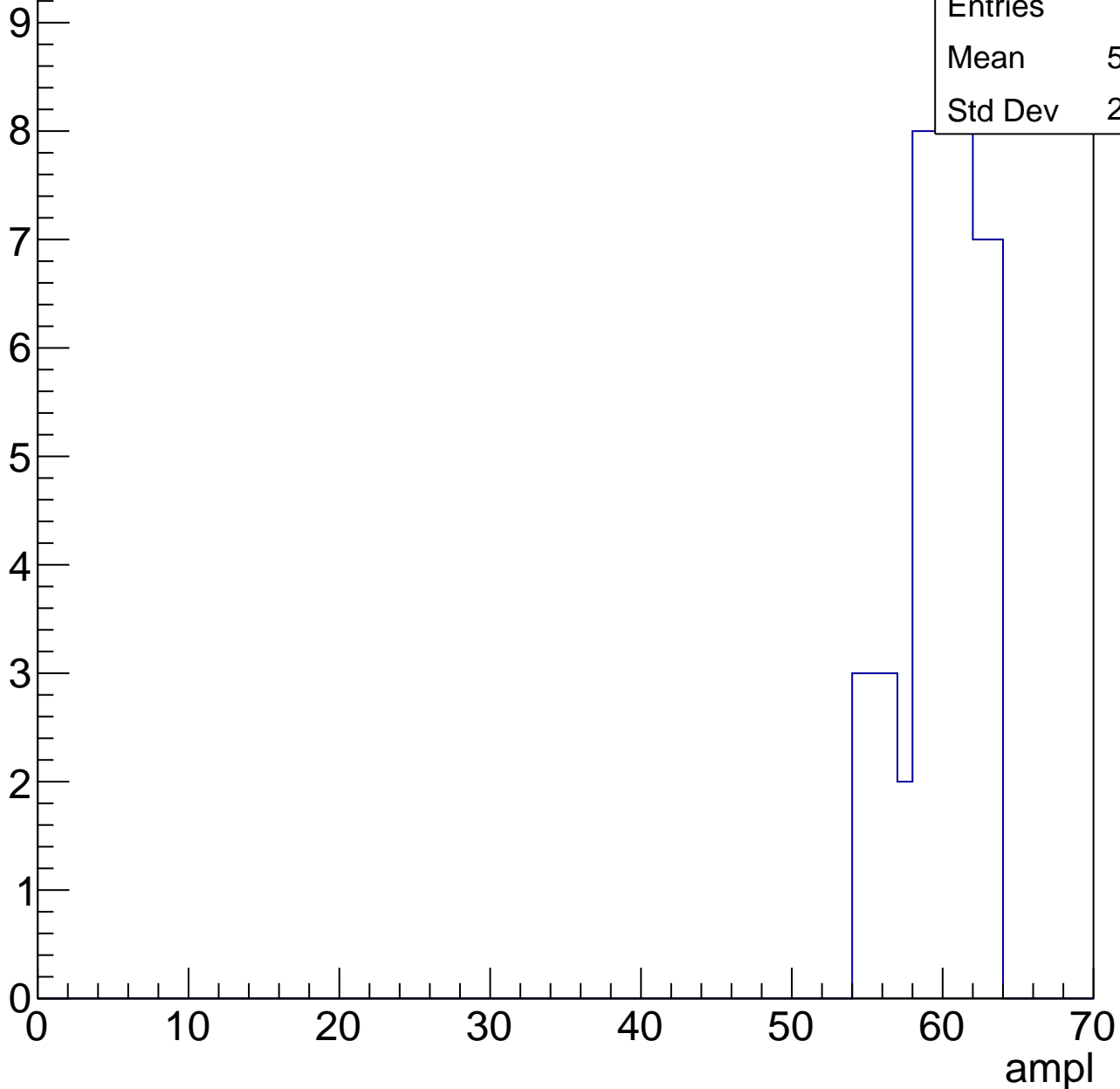


B1L003S, U19-ch53, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

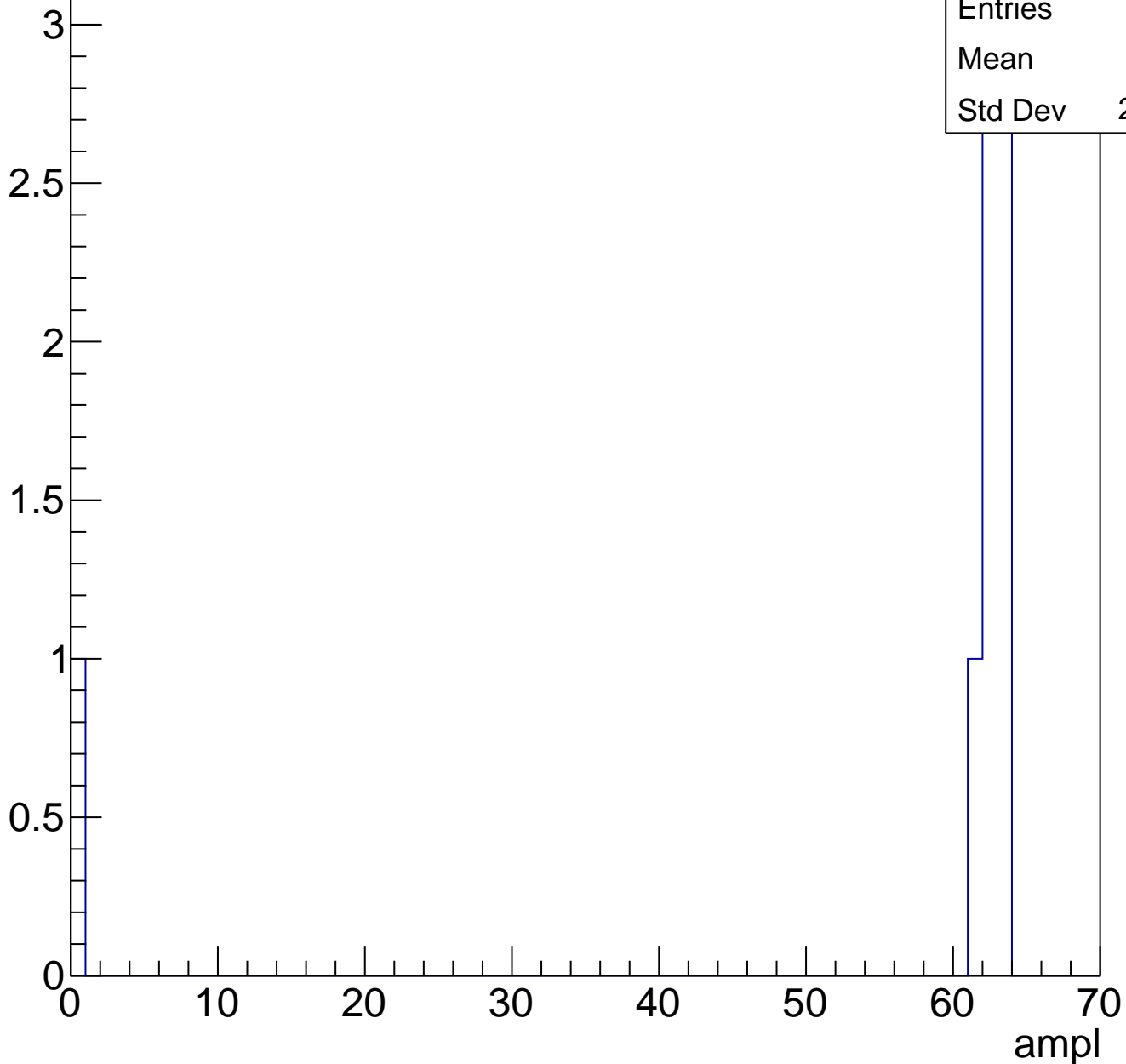
Entries	58
Mean	59.47
Std Dev	2.534



B1L003S, U19-ch53, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch53, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch54, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	87
Mean	27.2
Std Dev	4.492

Gaus mean : 28.2714

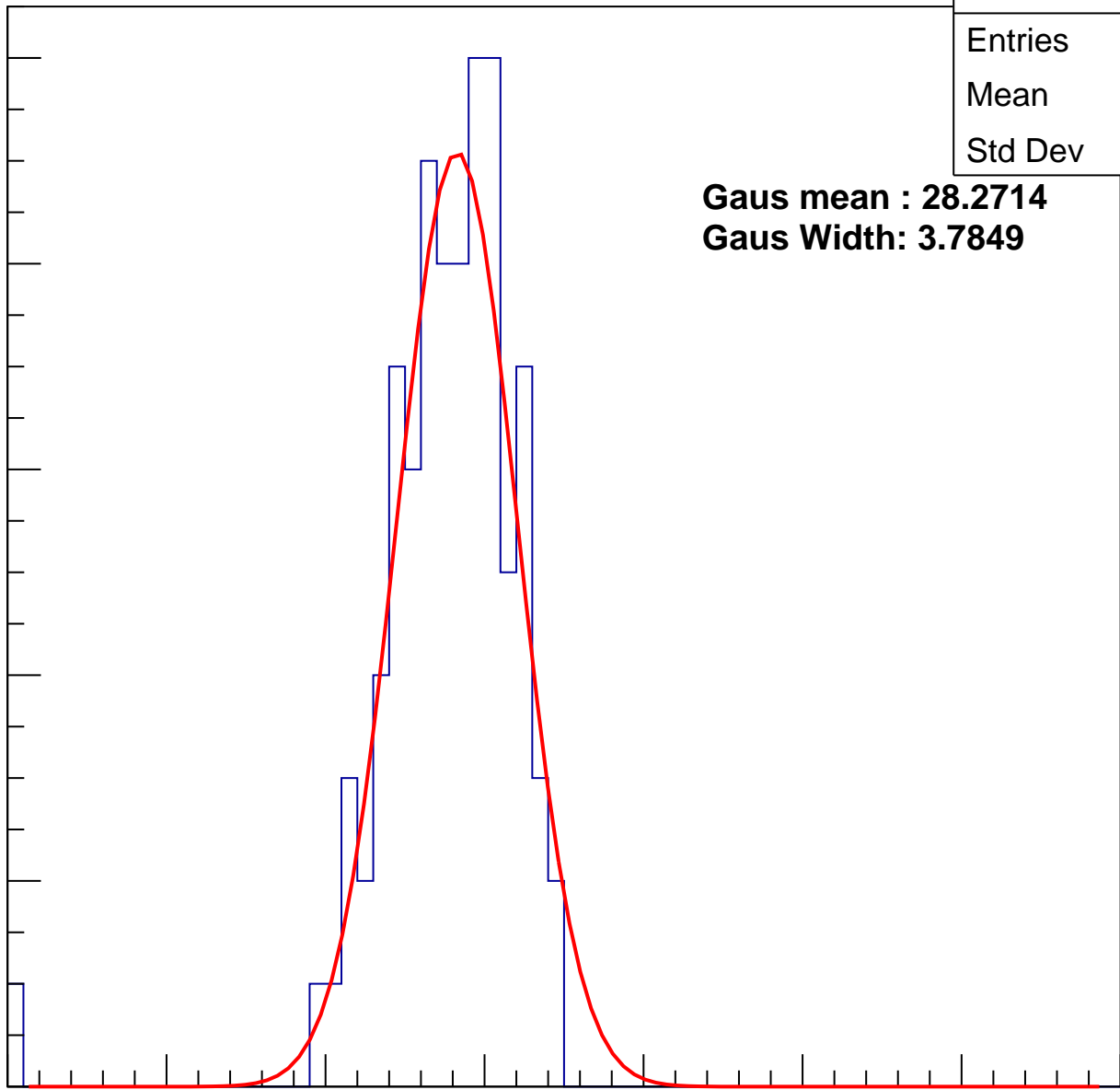
Gaus Width: 3.7849

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch54, adc1

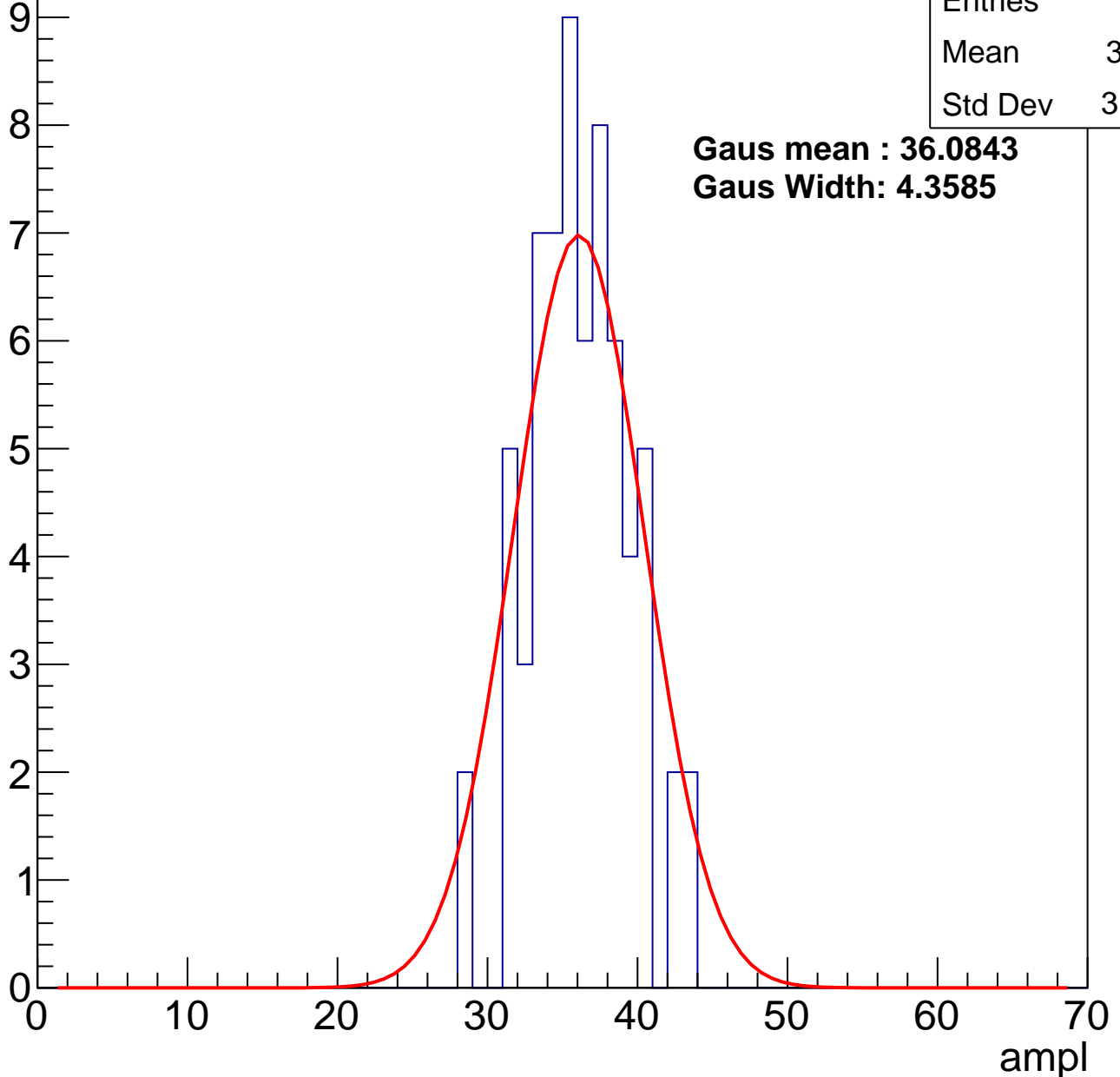
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.71
Std Dev	3.288

Gaus mean : 36.0843

Gaus Width: 4.3585



B1L003S, U19-ch54, adc2

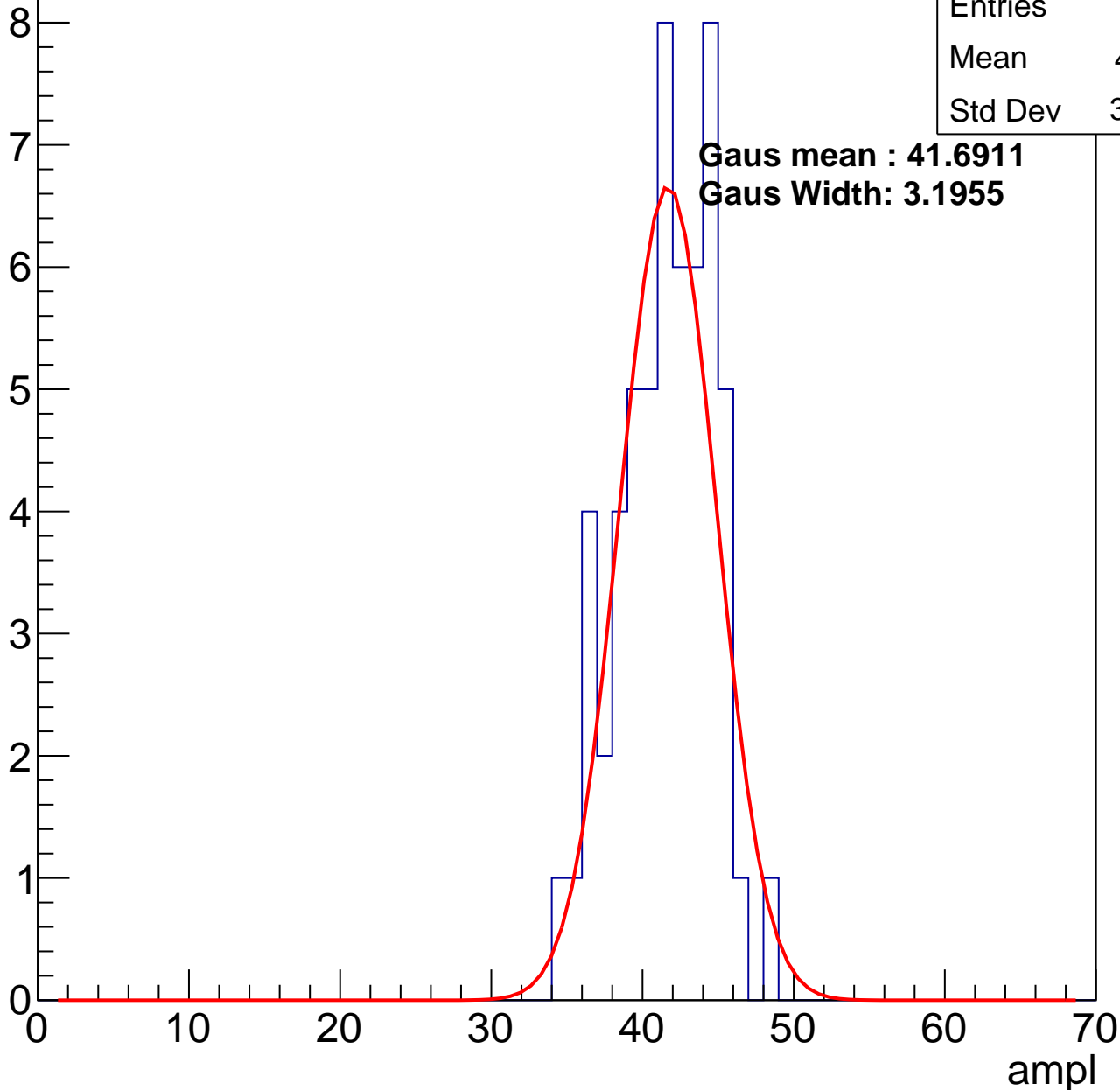
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	41.11
Std Dev	3.059

Gaus mean : 41.6911

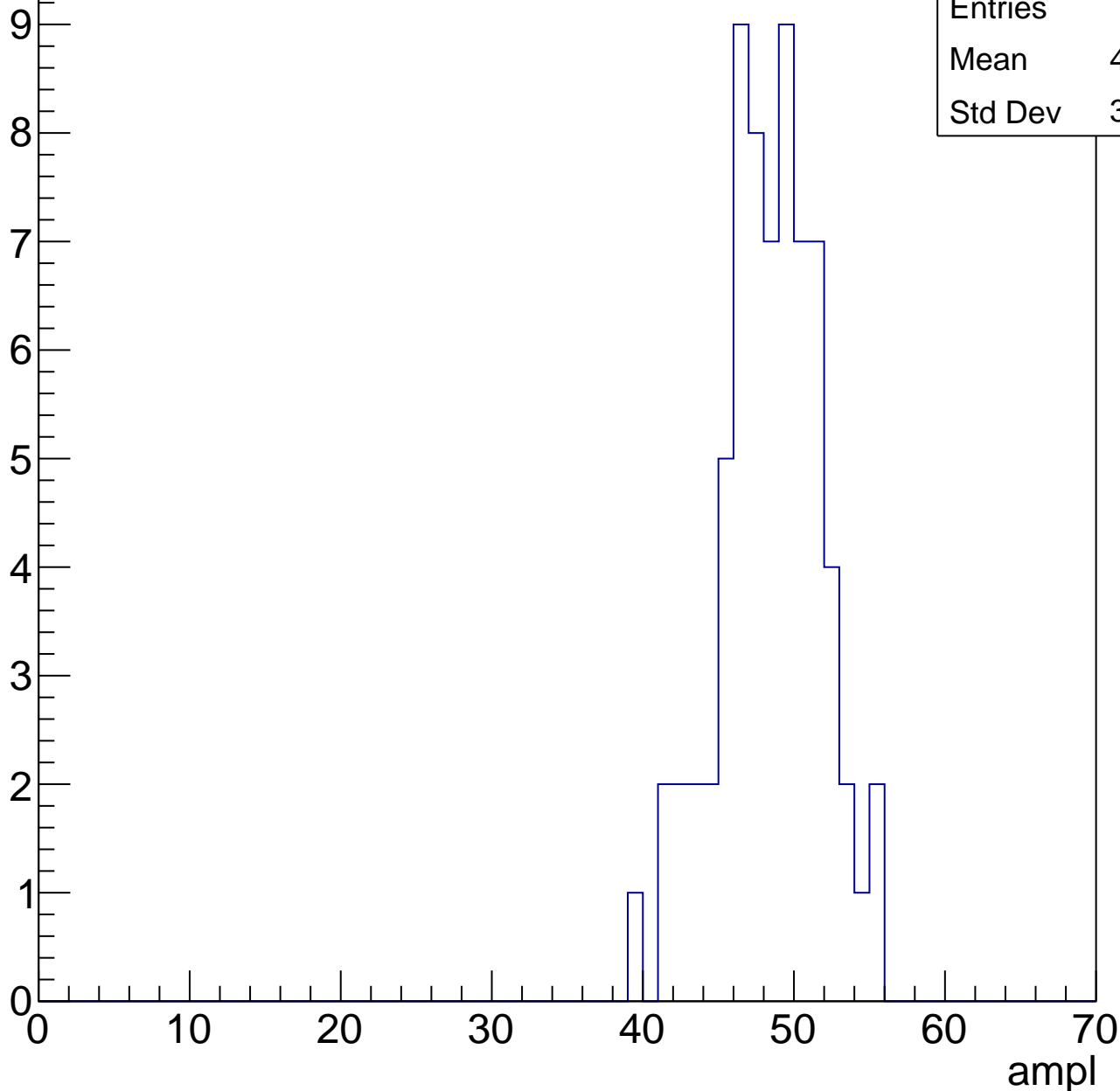
Gaus Width: 3.1955



B1L003S, U19-ch54, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



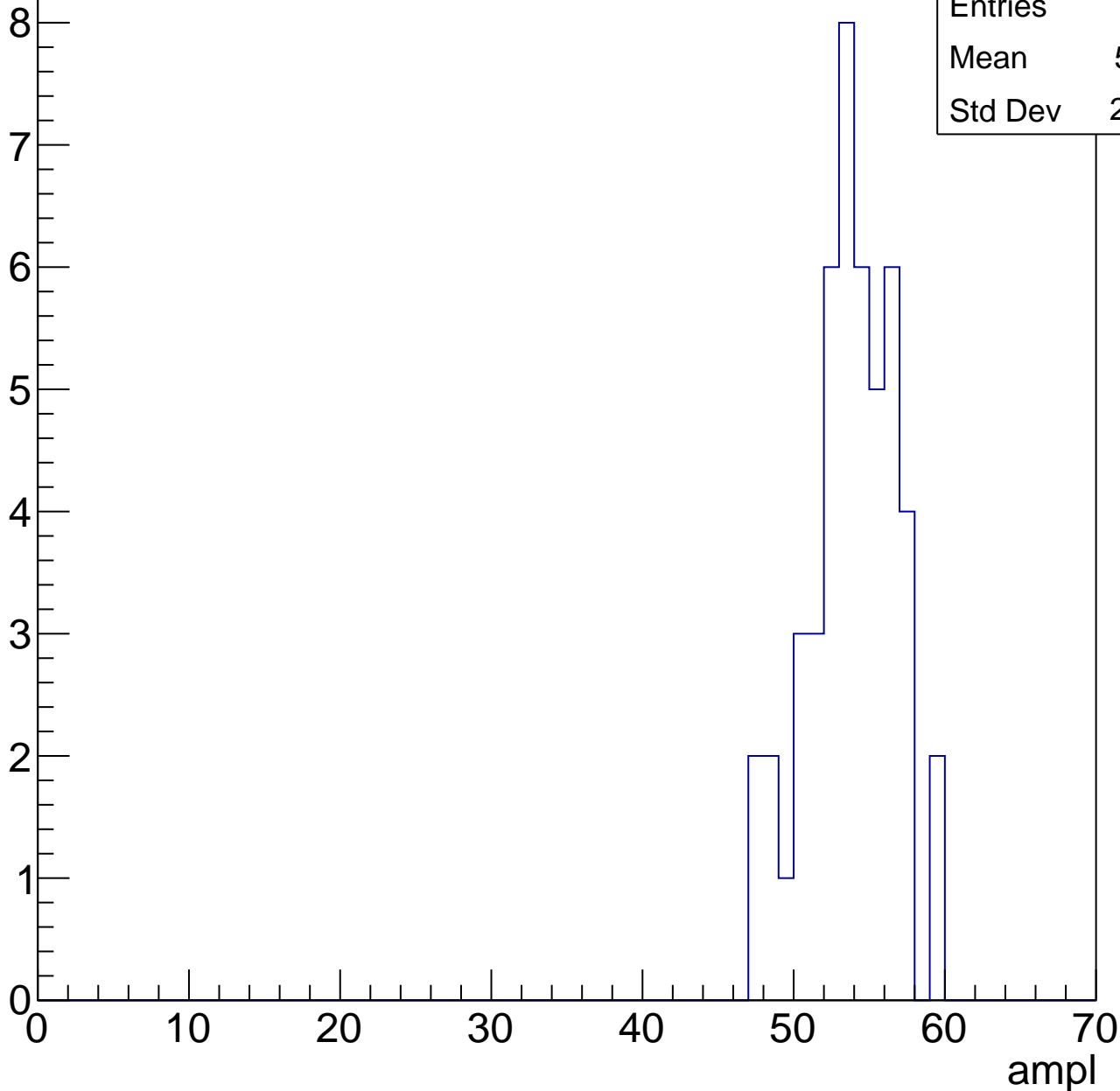
Entries	70
Mean	47.94
Std Dev	3.316

B1L003S, U19-ch54, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

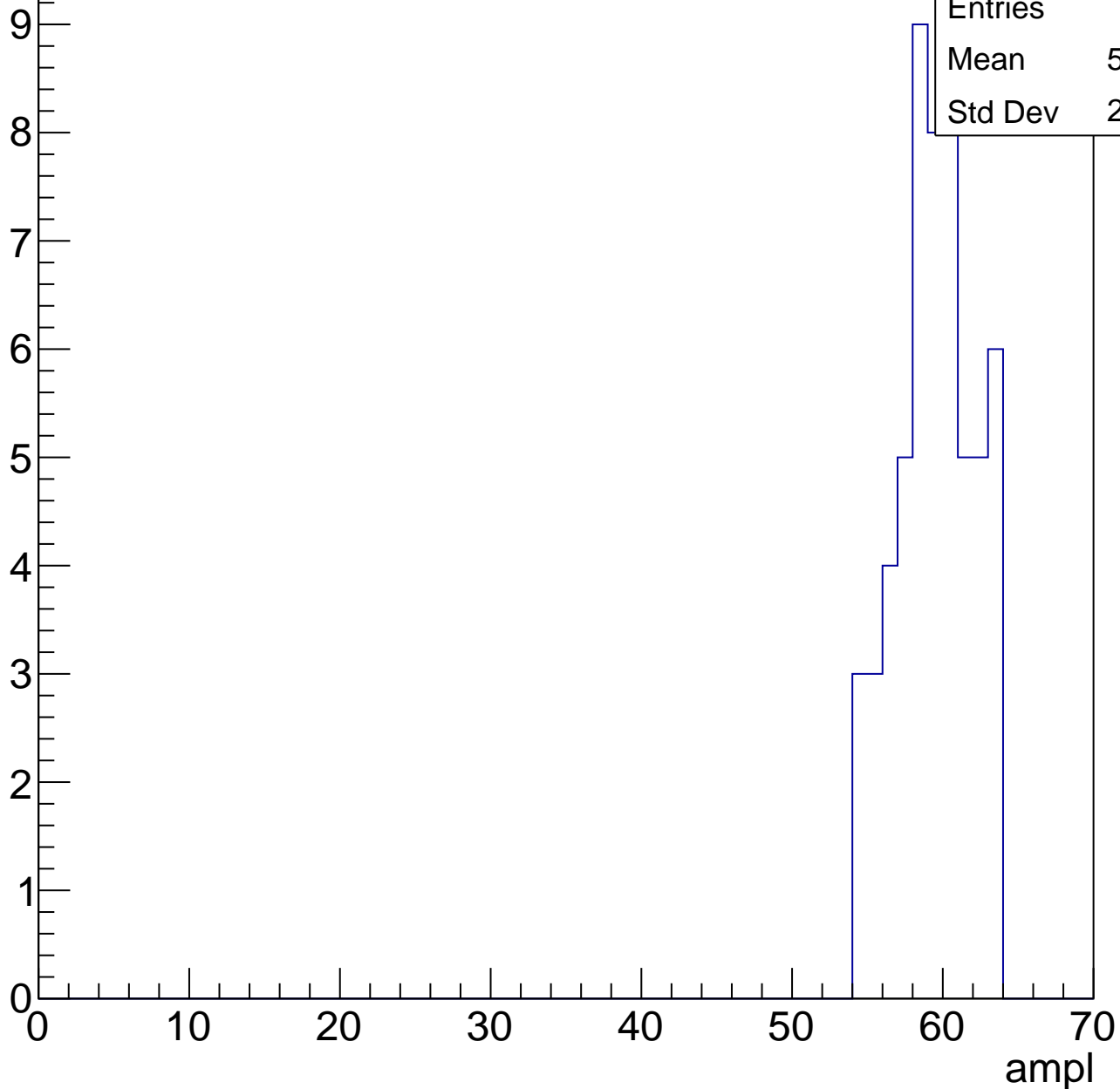
Entries	48
Mean	53.31
Std Dev	2.866



B1L003S, U19-ch54, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

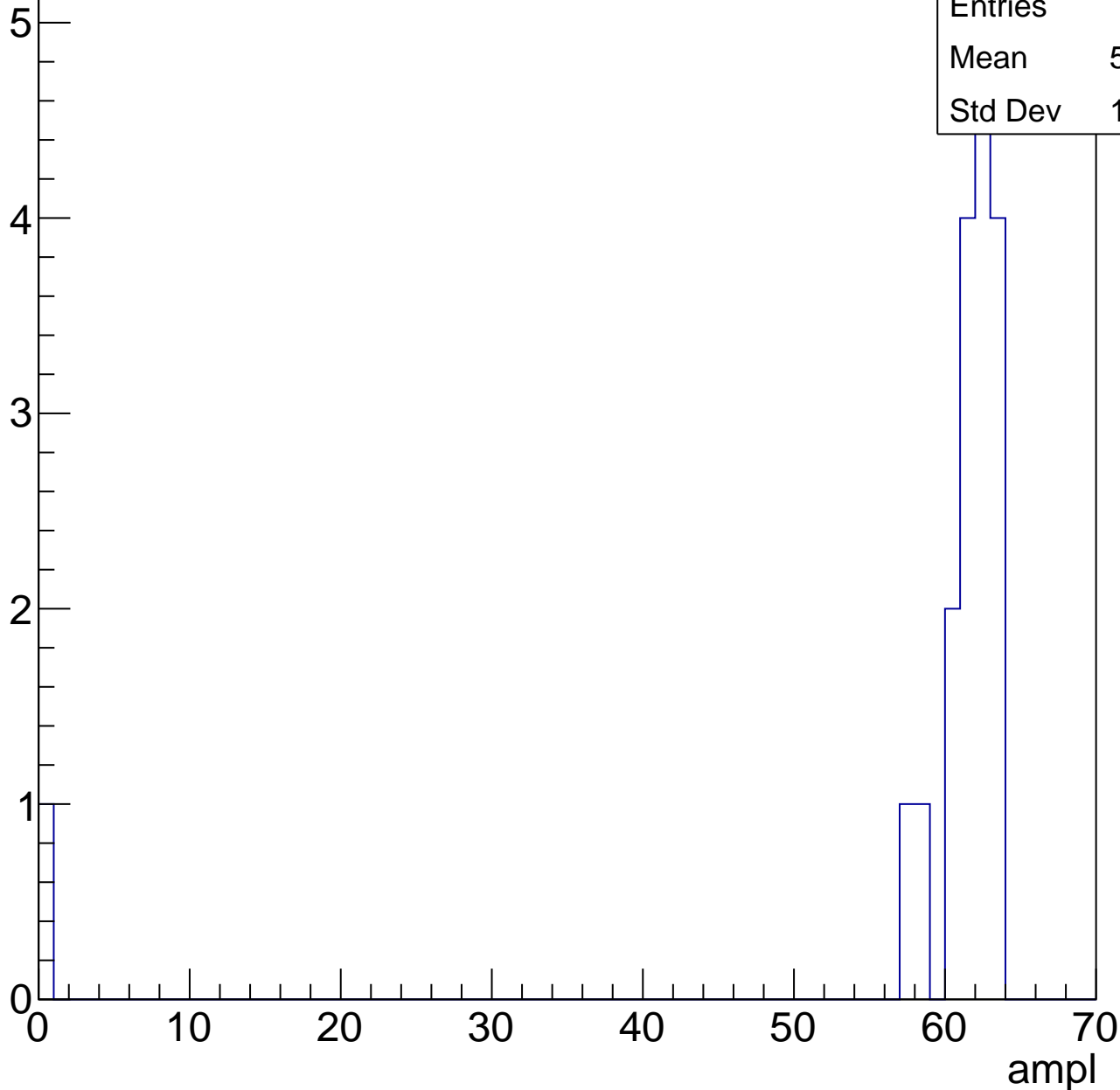


B1L003S, U19-ch54, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	18
Mean	57.83
Std Dev	14.12



B1L003S, U19-ch54, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch55, adc0

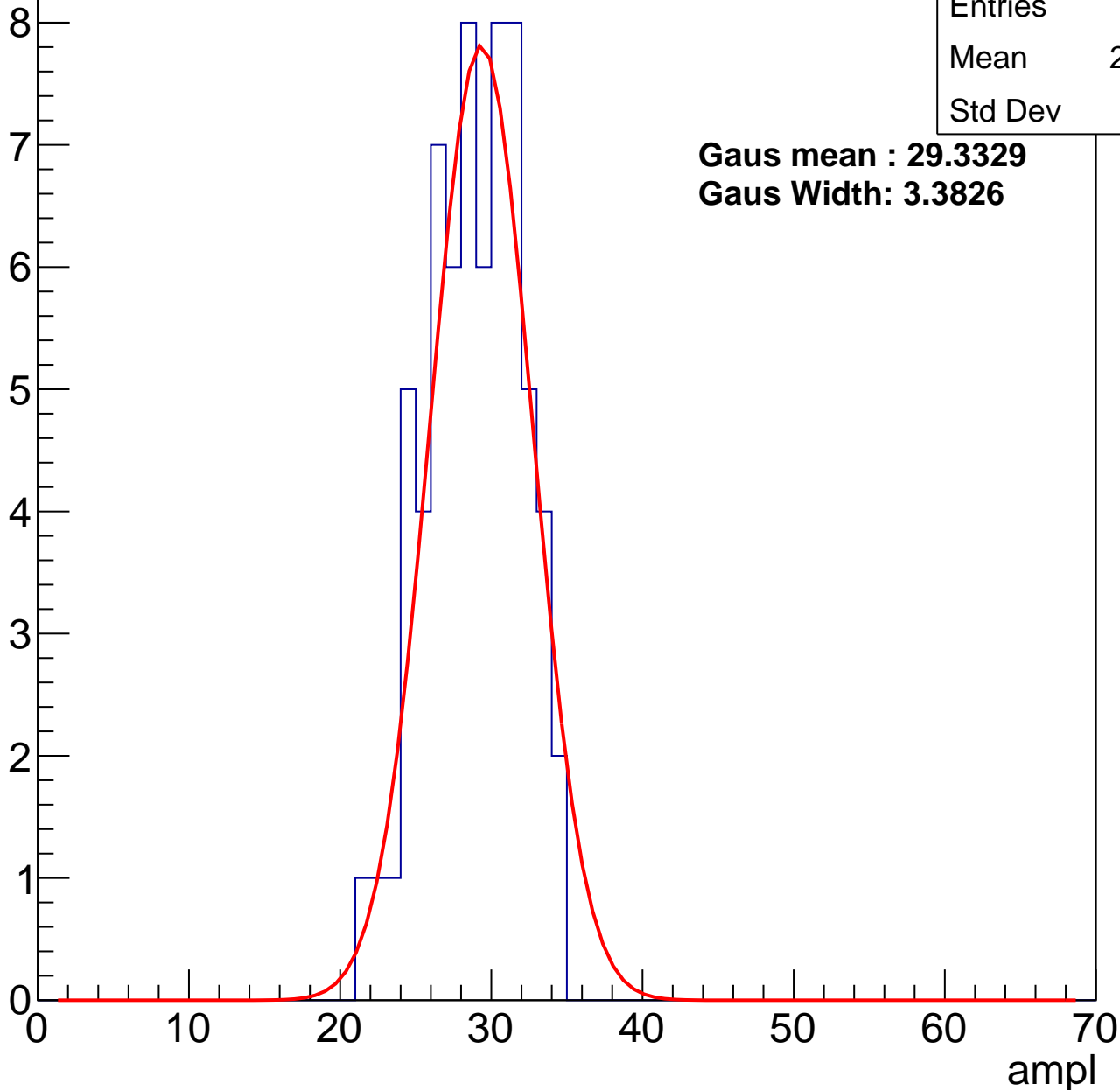
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	28.42
Std Dev	3.04

Gaus mean : 29.3329

Gaus Width: 3.3826



B1L003S, U19-ch55, adc1

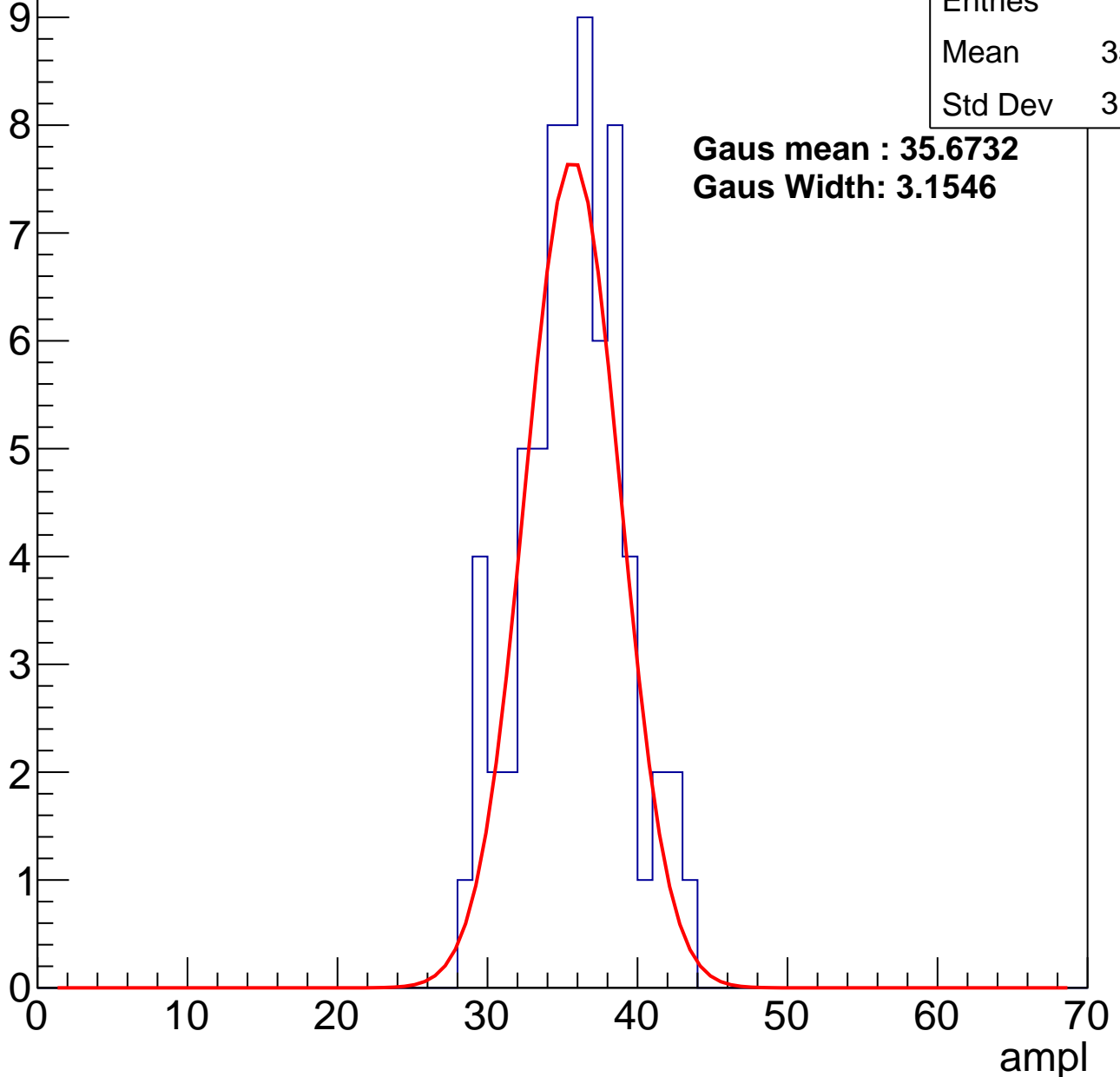
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.26
Std Dev	3.368

Gaus mean : 35.6732

Gaus Width: 3.1546



B1L003S, U19-ch55, adc2

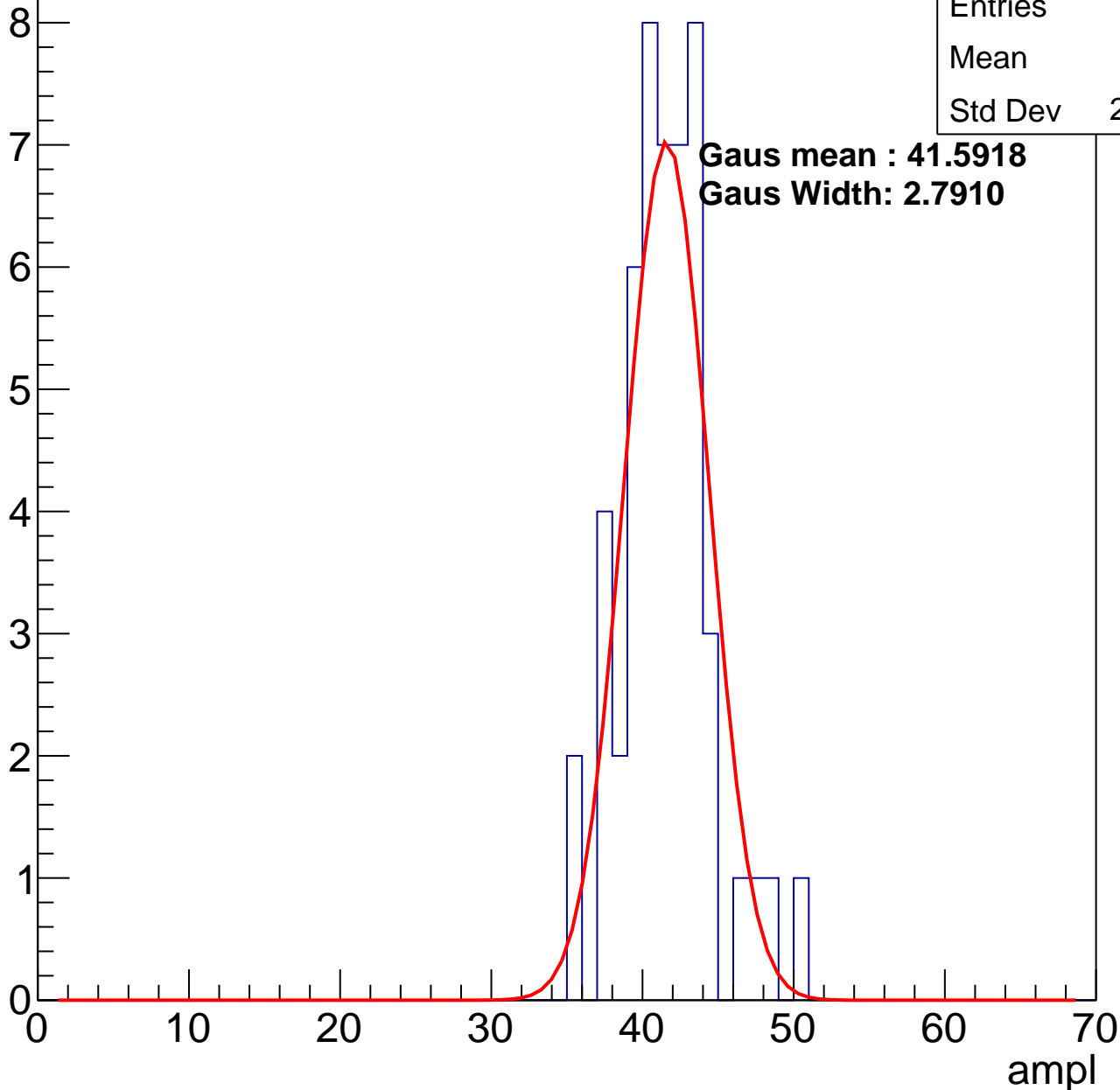
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	41.1
Std Dev	2.946

Gaus mean : 41.5918

Gaus Width: 2.7910



B1L003S, U19-ch55, adc3

calib_packv5_042523_0143.root, FC#13, port D2

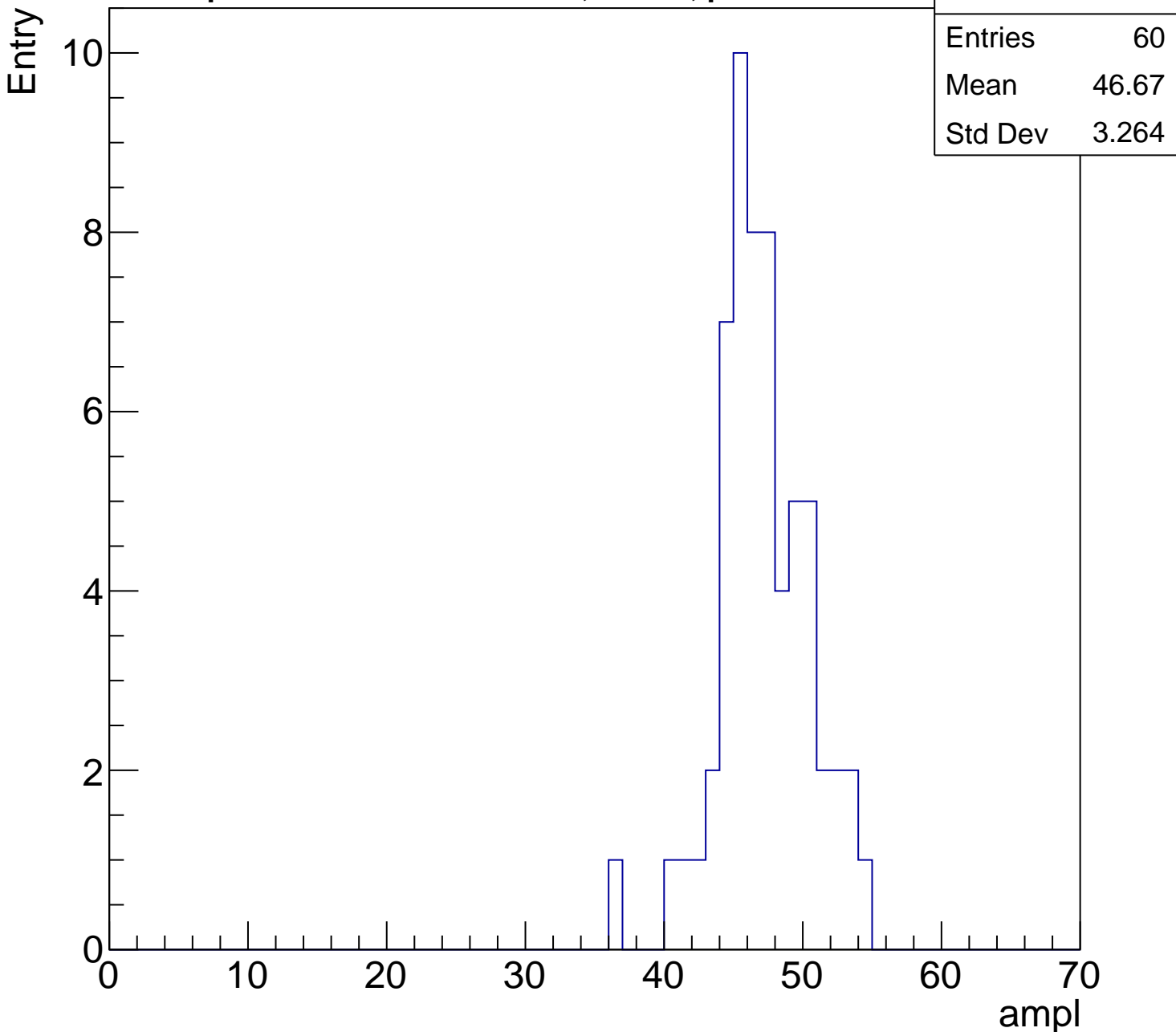
Entries	60
Mean	46.67
Std Dev	3.264

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

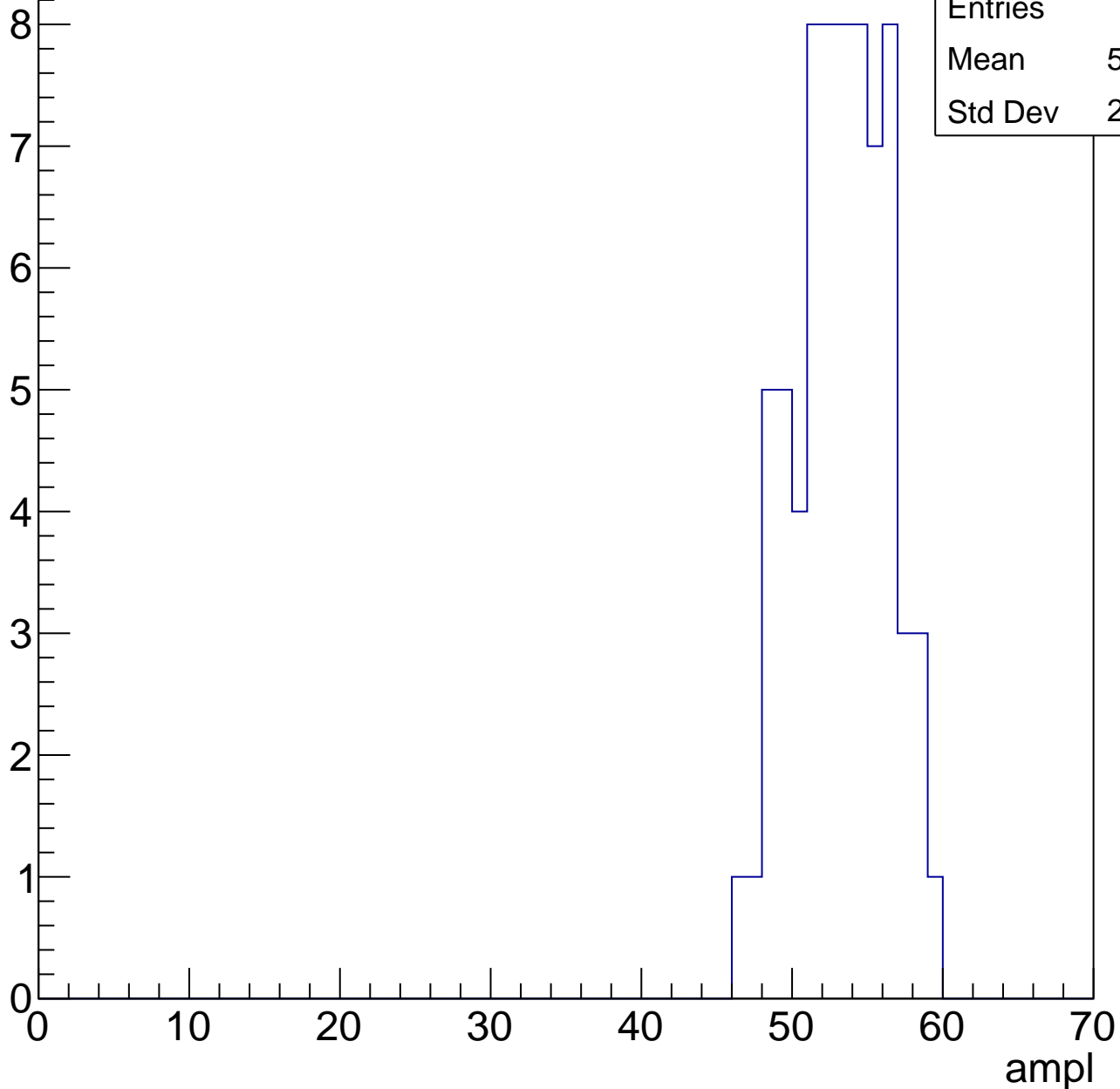
ampl



B1L003S, U19-ch55, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

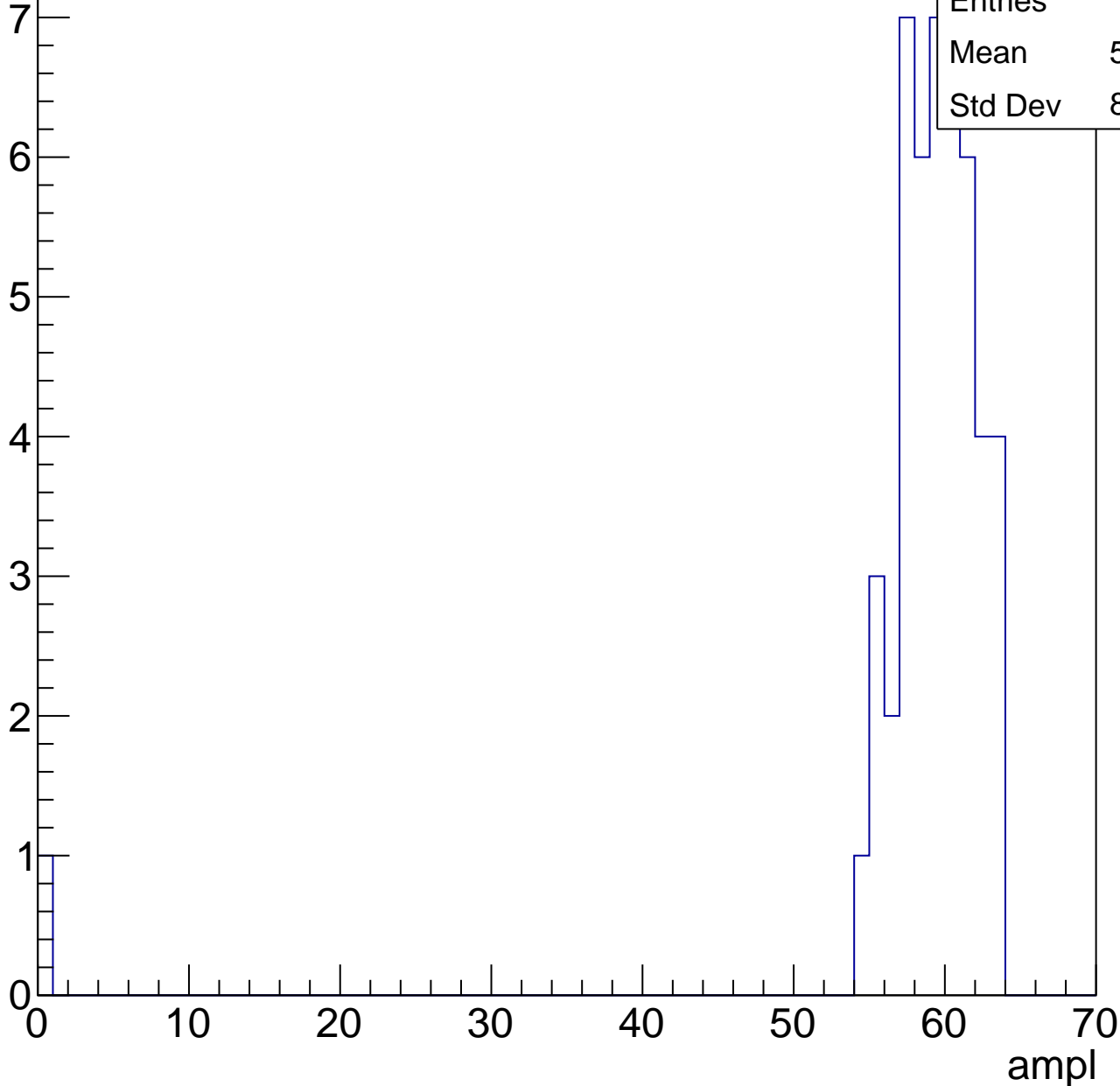


B1L003S, U19-ch55, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	57.85
Std Dev	8.749

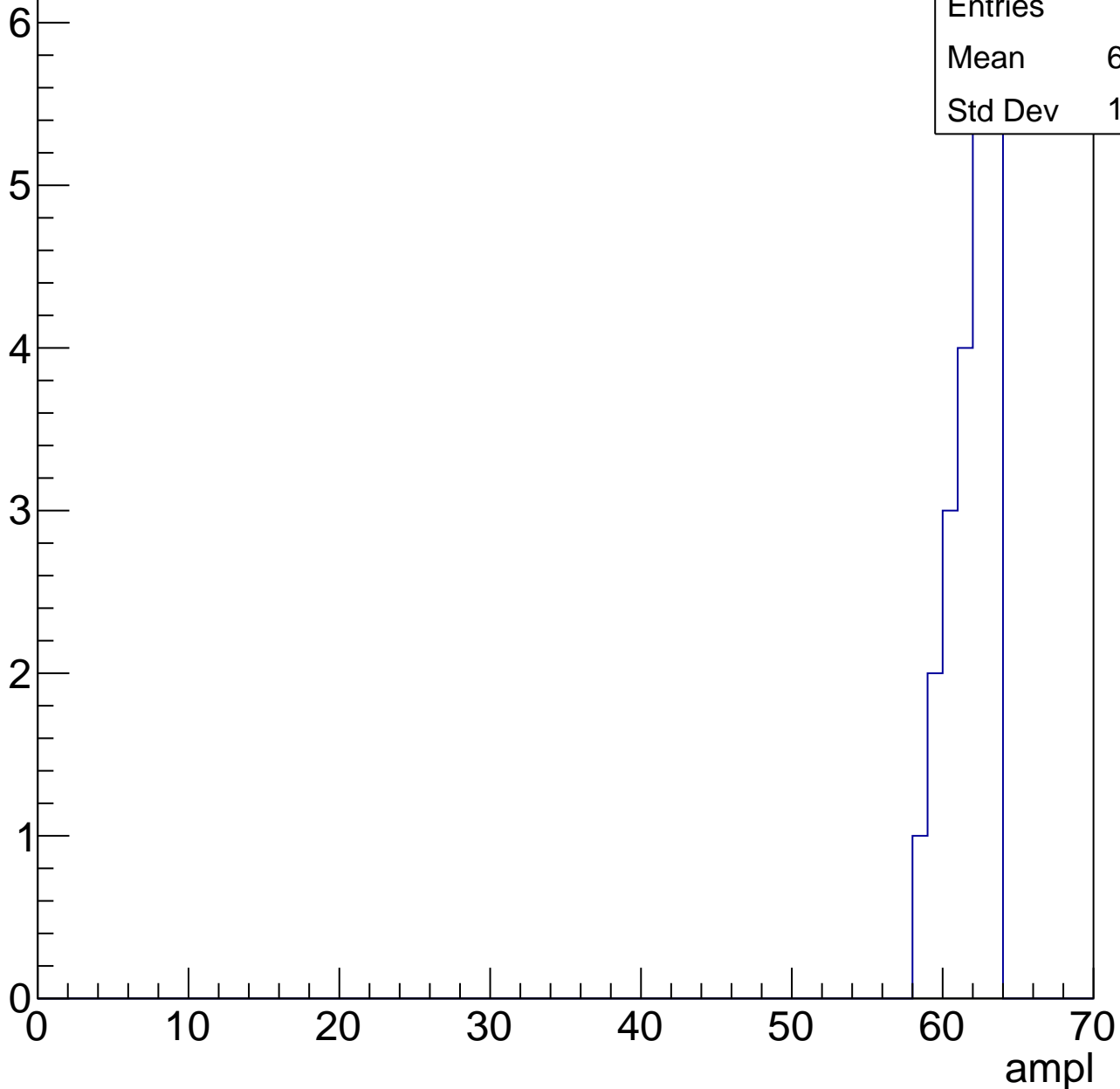


B1L003S, U19-ch55, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	22
Mean	61.36
Std Dev	1.463



B1L003S, U19-ch55, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch56, adc0

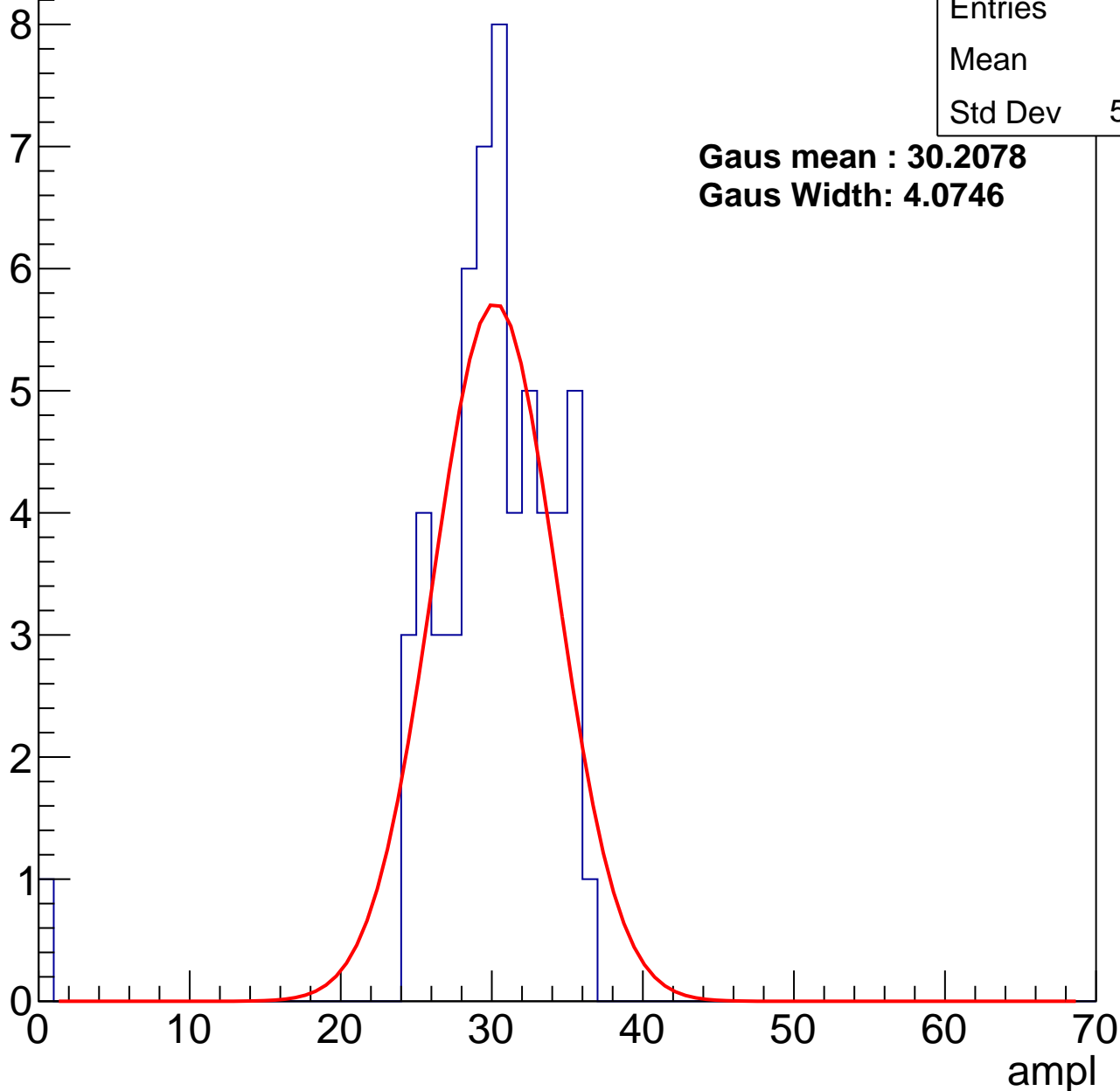
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	29.4
Std Dev	5.048

Gaus mean : 30.2078

Gaus Width: 4.0746



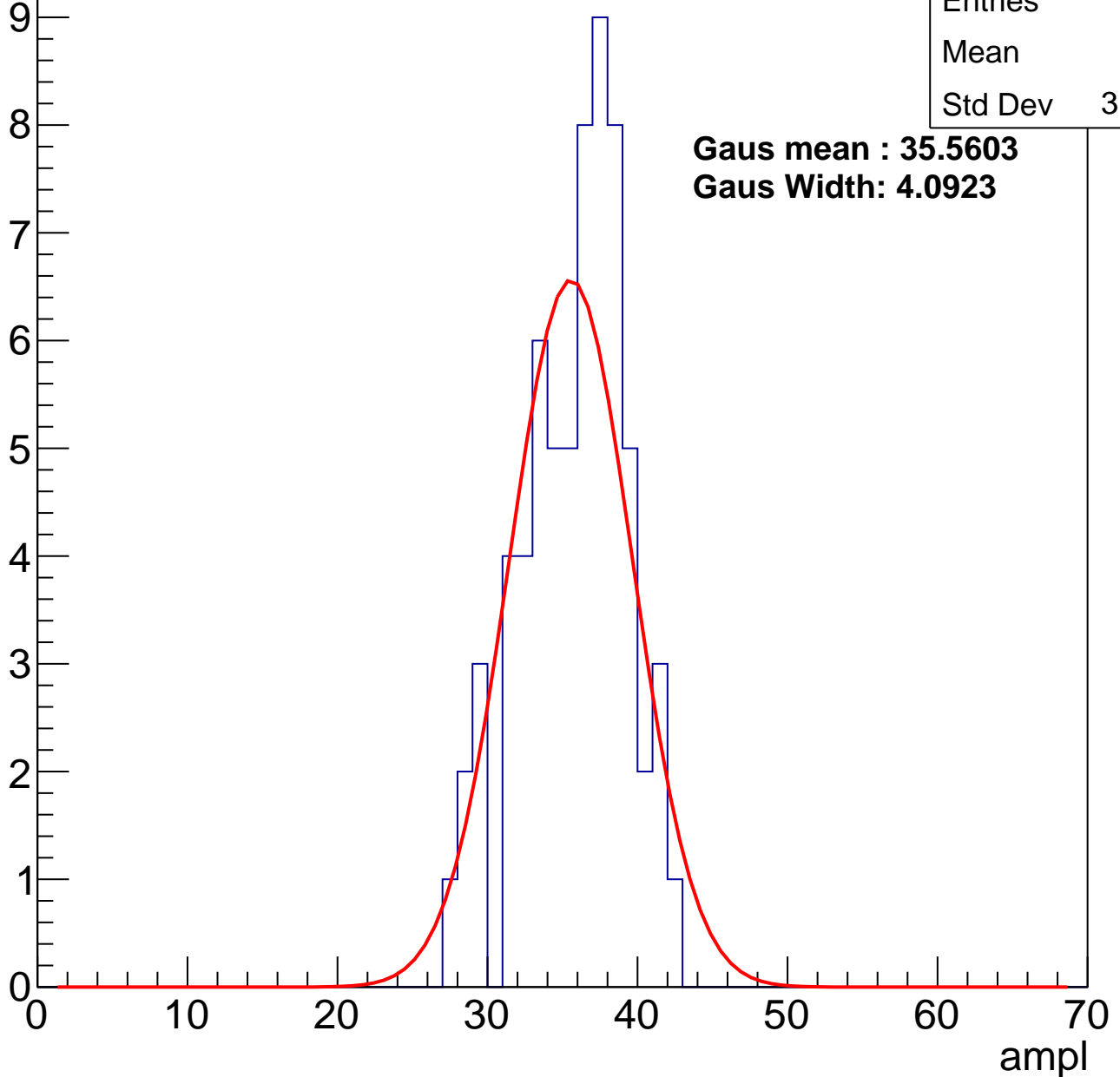
B1L003S, U19-ch56, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.3
Std Dev	3.473

Gaus mean : 35.5603
Gaus Width: 4.0923



B1L003S, U19-ch56, adc2

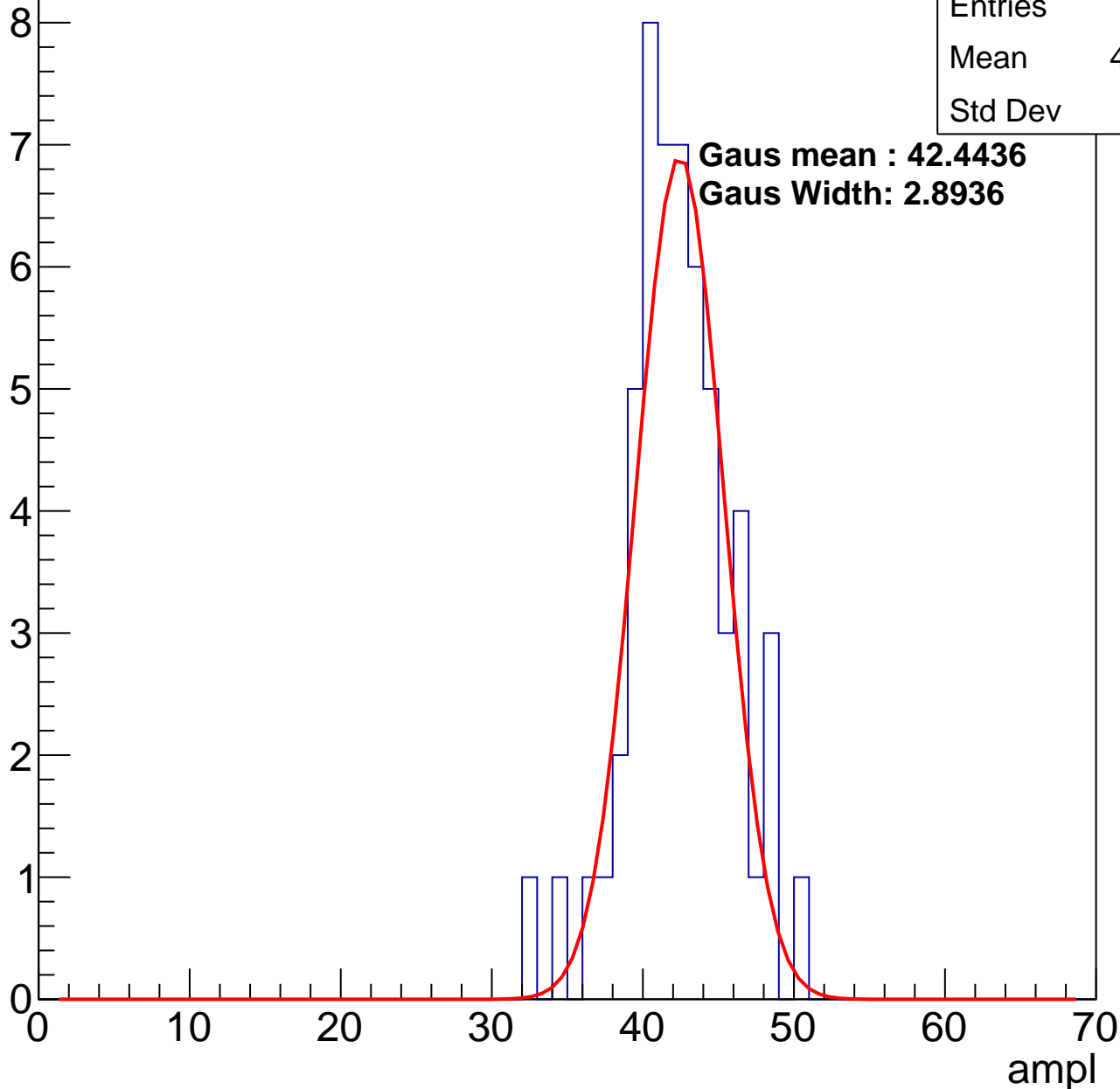
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	41.95
Std Dev	3.42

Gaus mean : 42.4436

Gaus Width: 2.8936

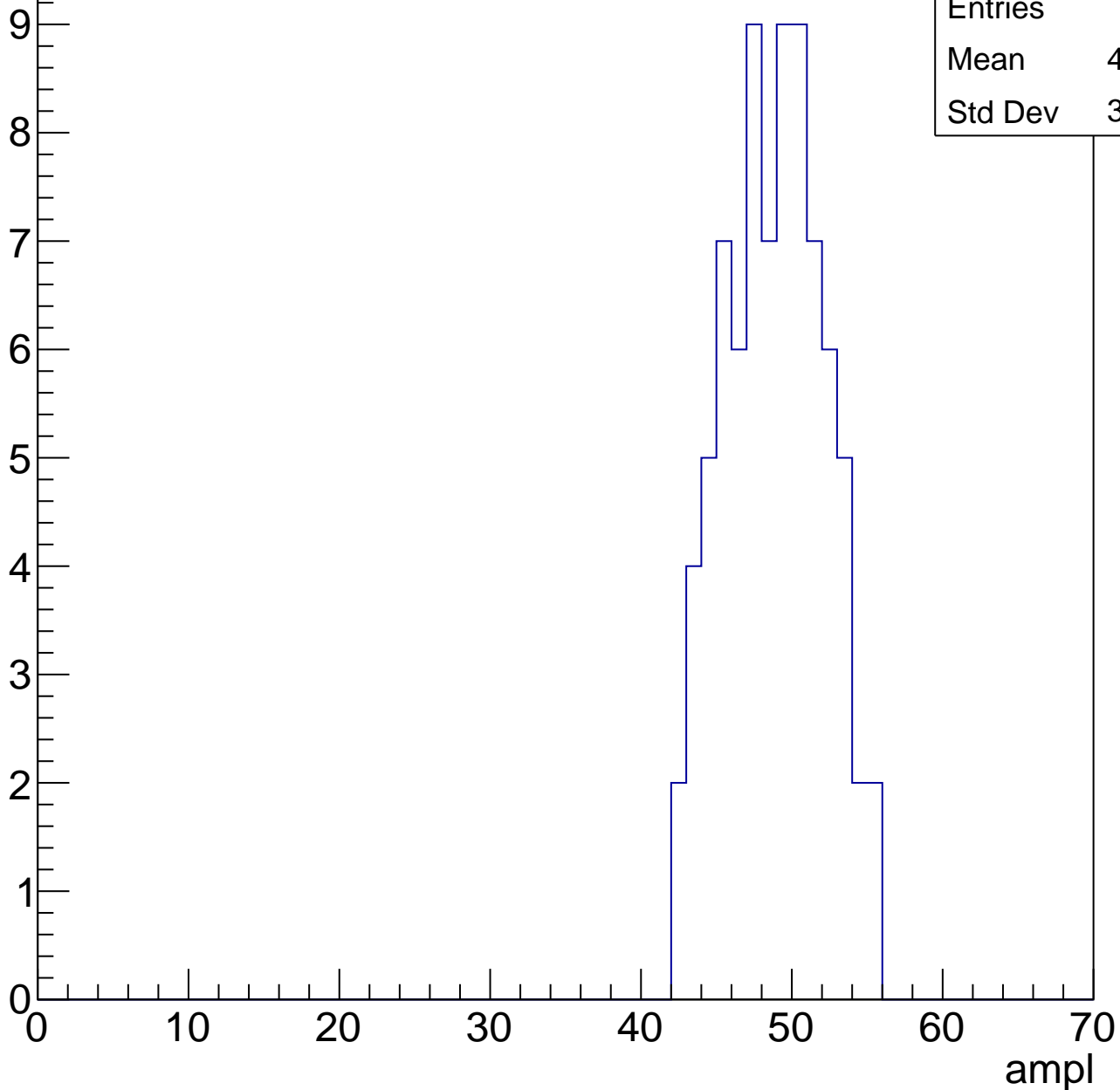


B1L003S, U19-ch56, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	48.36
Std Dev	3.234

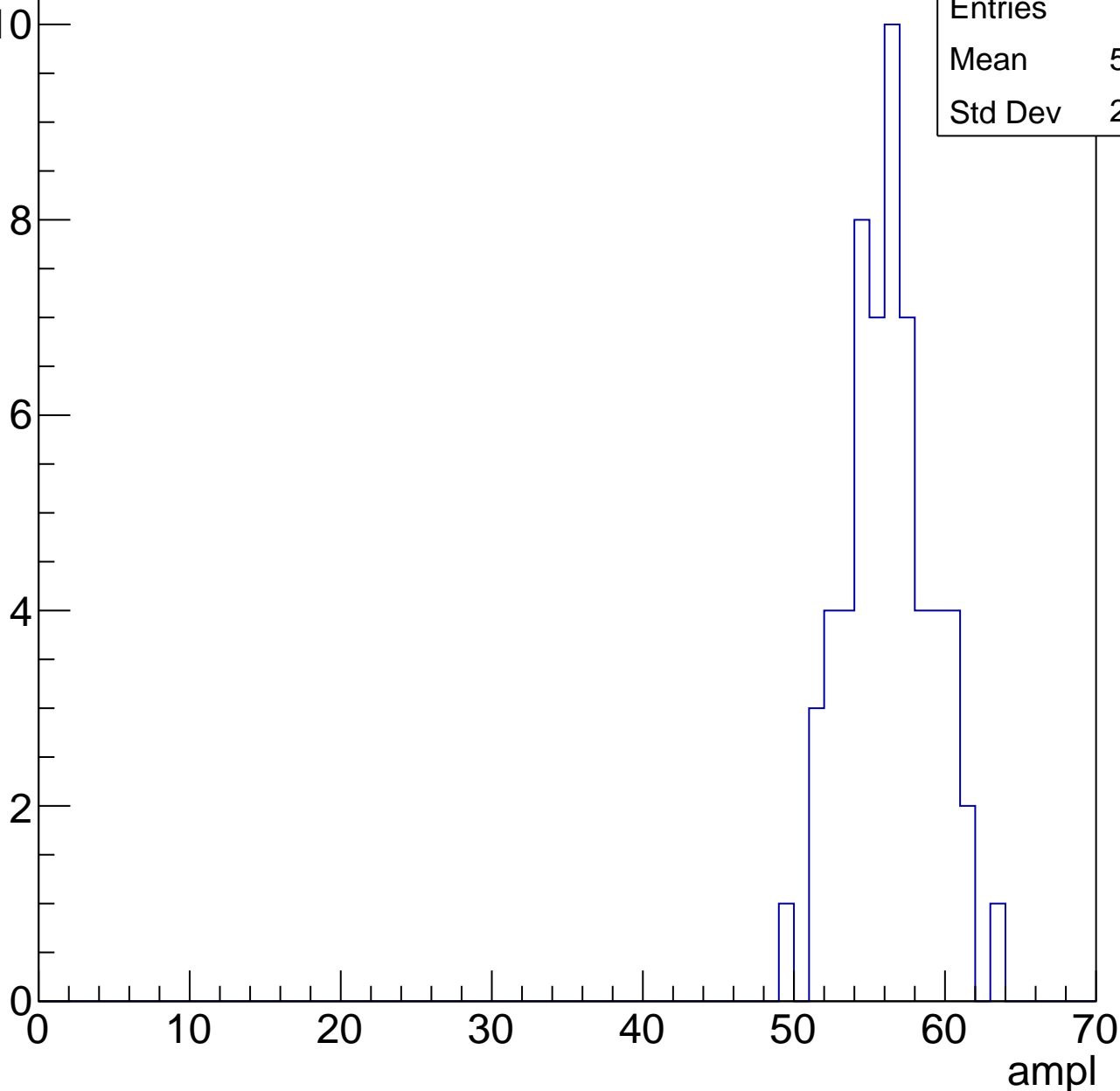


B1L003S, U19-ch56, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.78
Std Dev	2.859

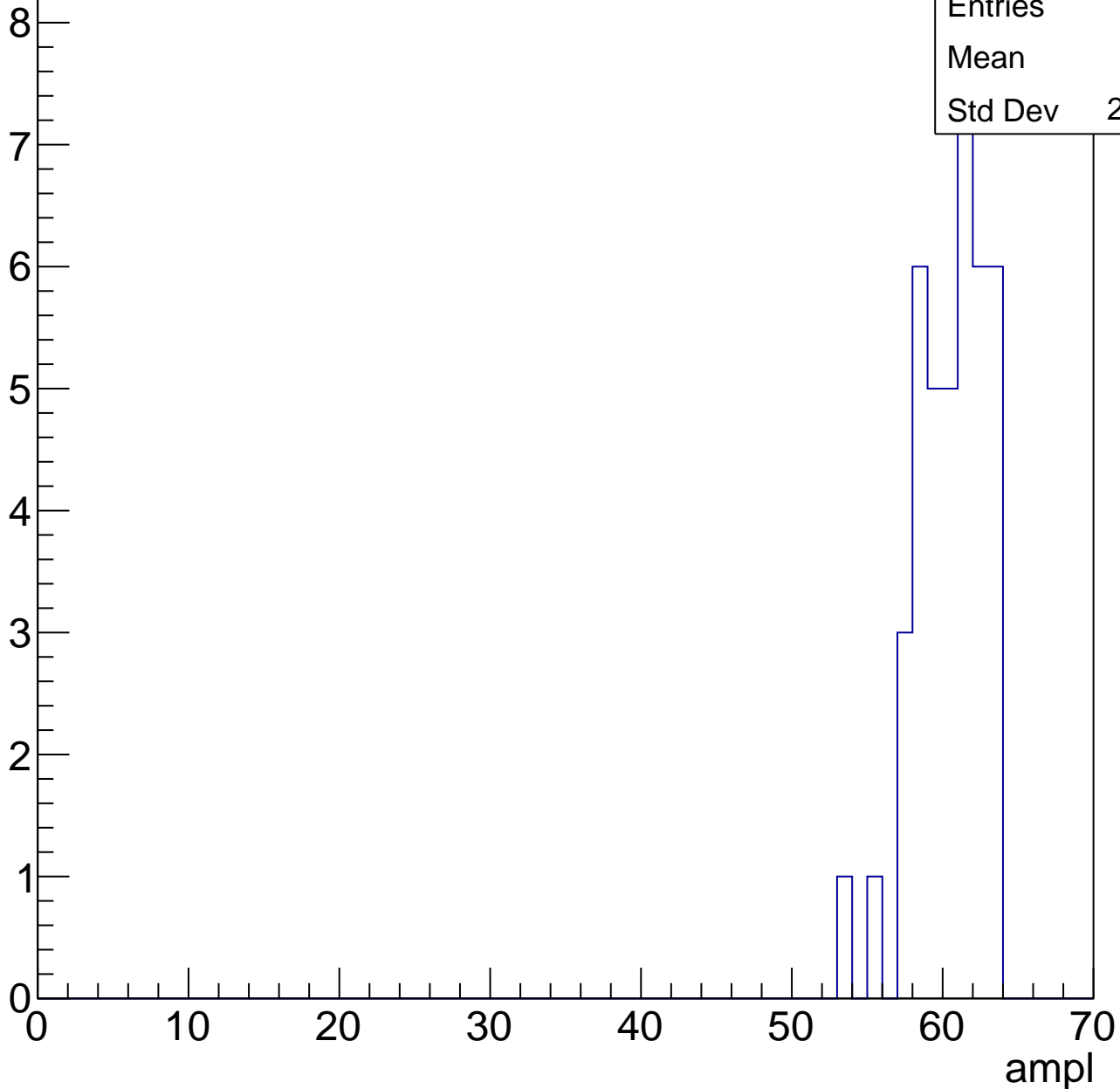


B1L003S, U19-ch56, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

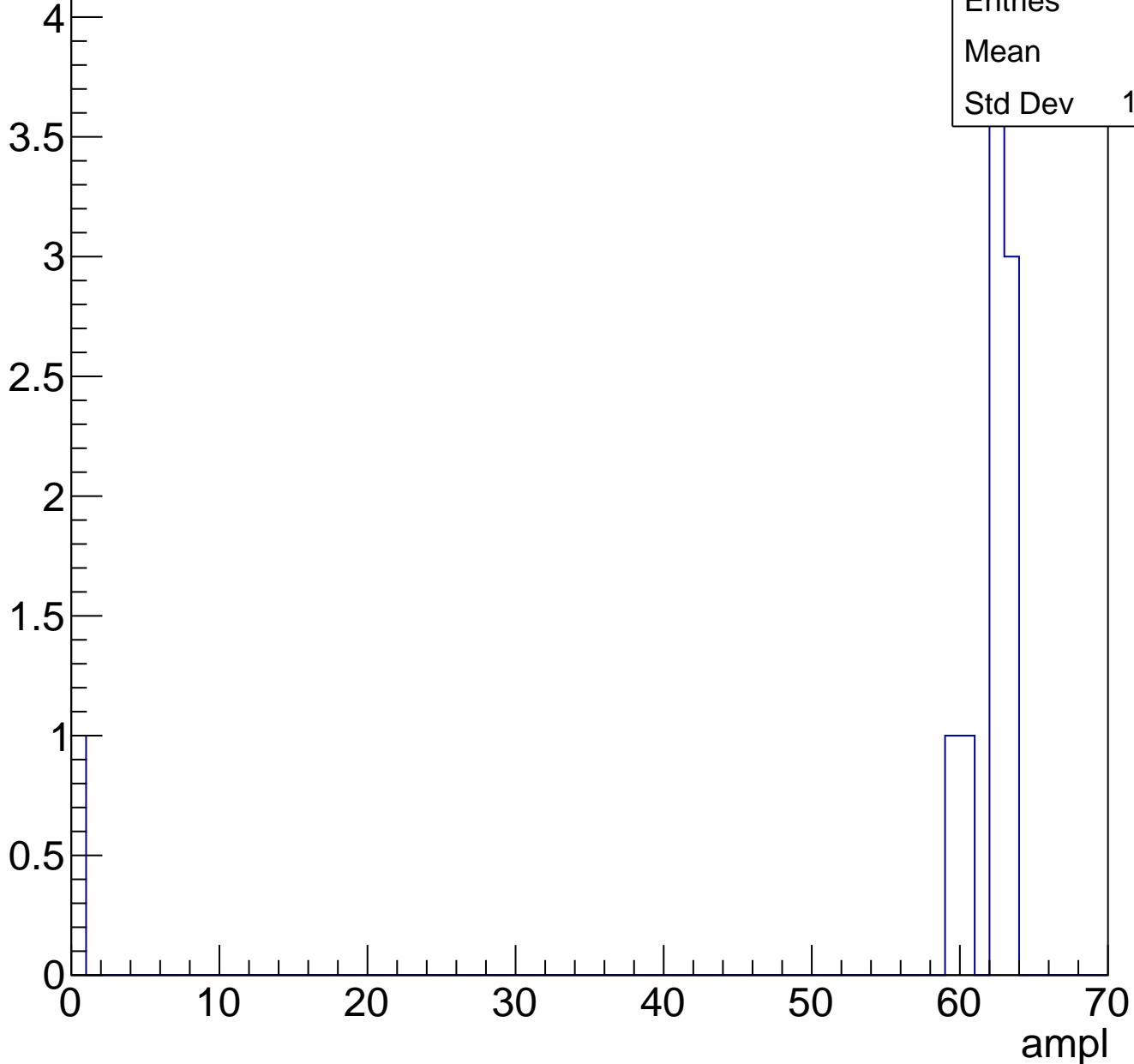
Entries	41
Mean	60
Std Dev	2.295



B1L003S, U19-ch56, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch56, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch57, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	29.76
Std Dev	3.406

Gaus mean : 30.0633

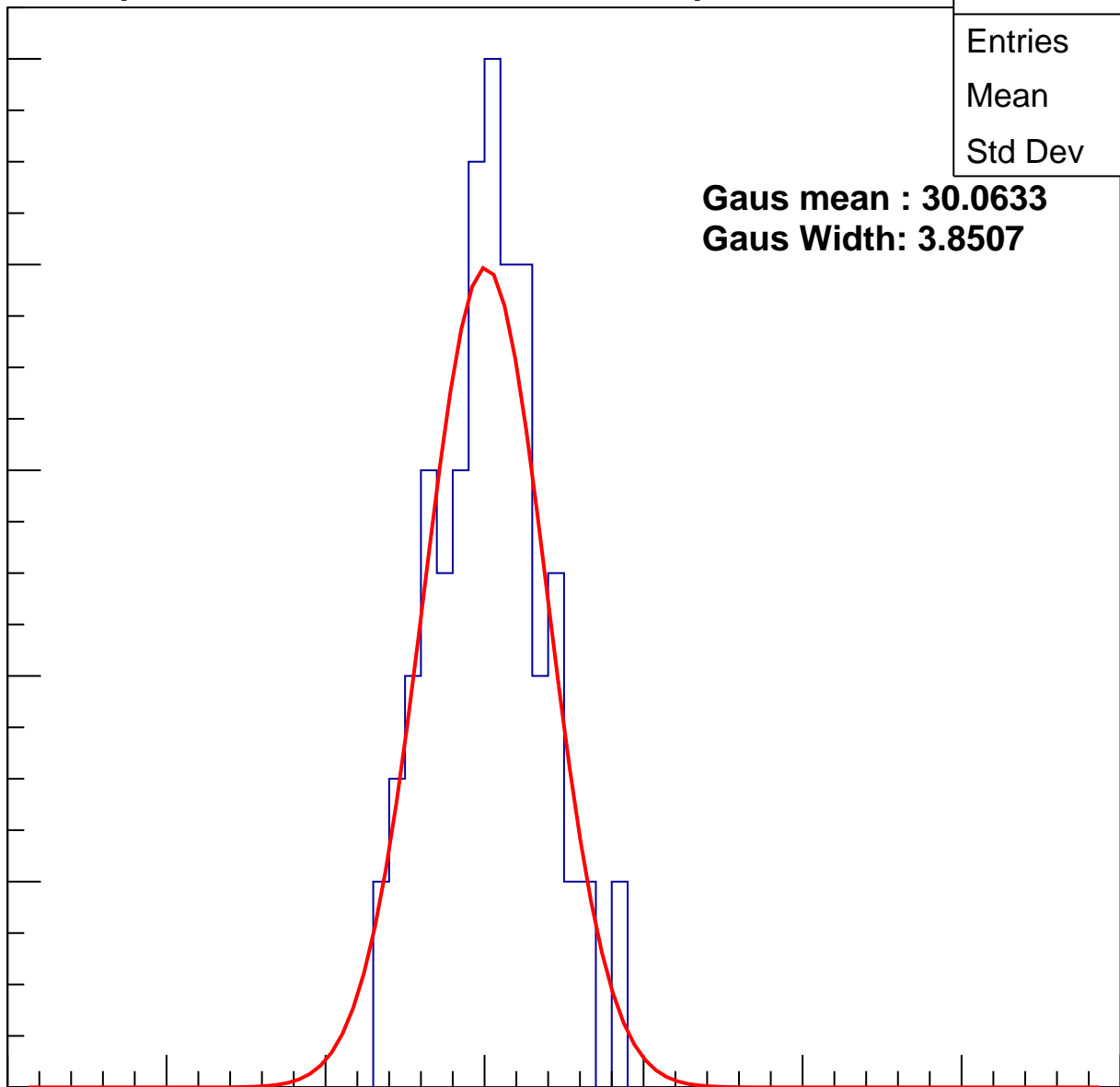
Gaus Width: 3.8507

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



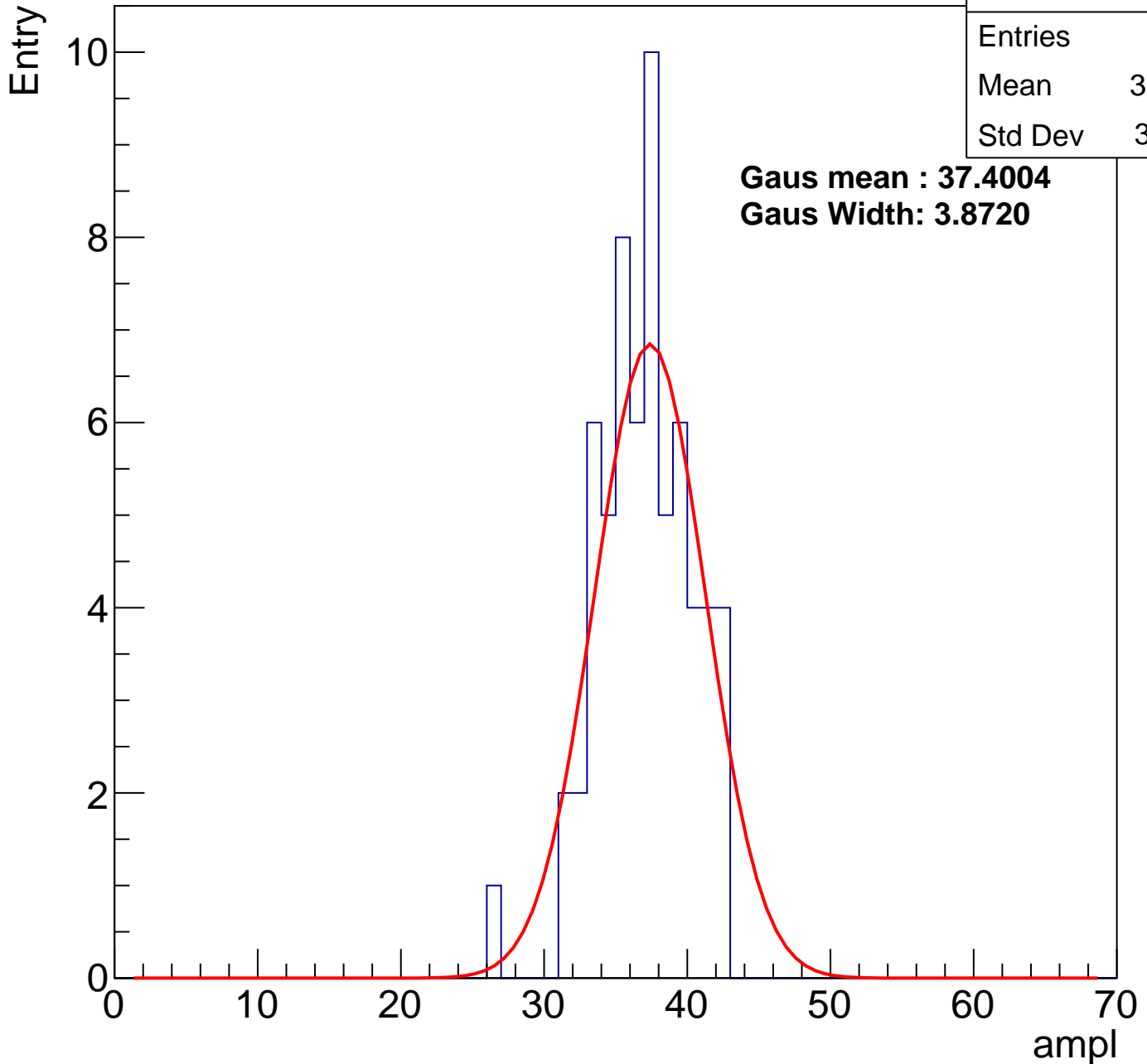
B1L003S, U19-ch57, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	63
Mean	36.54
Std Dev	3.181

Gaus mean : 37.4004

Gaus Width: 3.8720



B1L003S, U19-ch57, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	43.27
Std Dev	3.236

Gaus mean : 43.8539

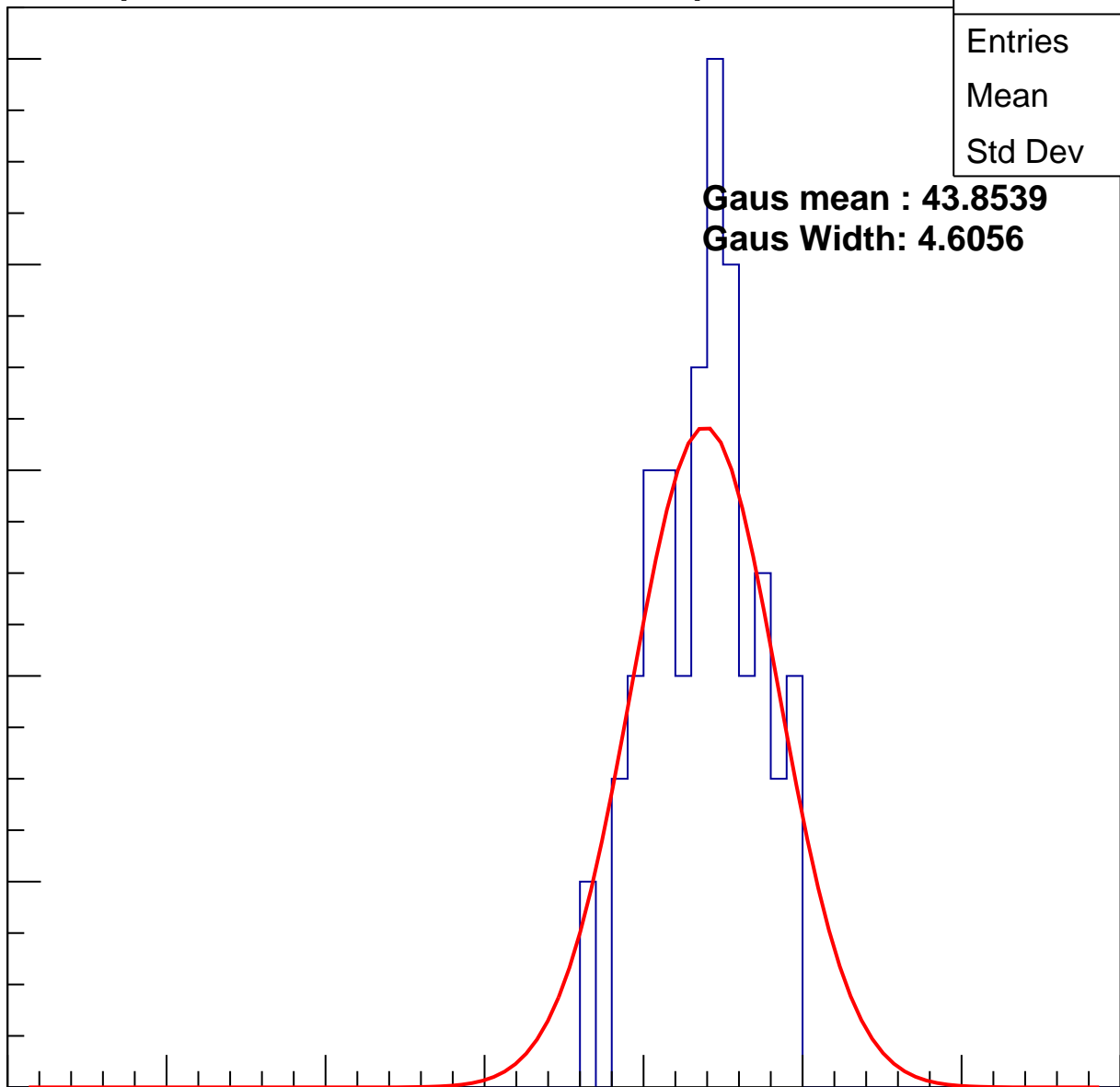
Gaus Width: 4.6056

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

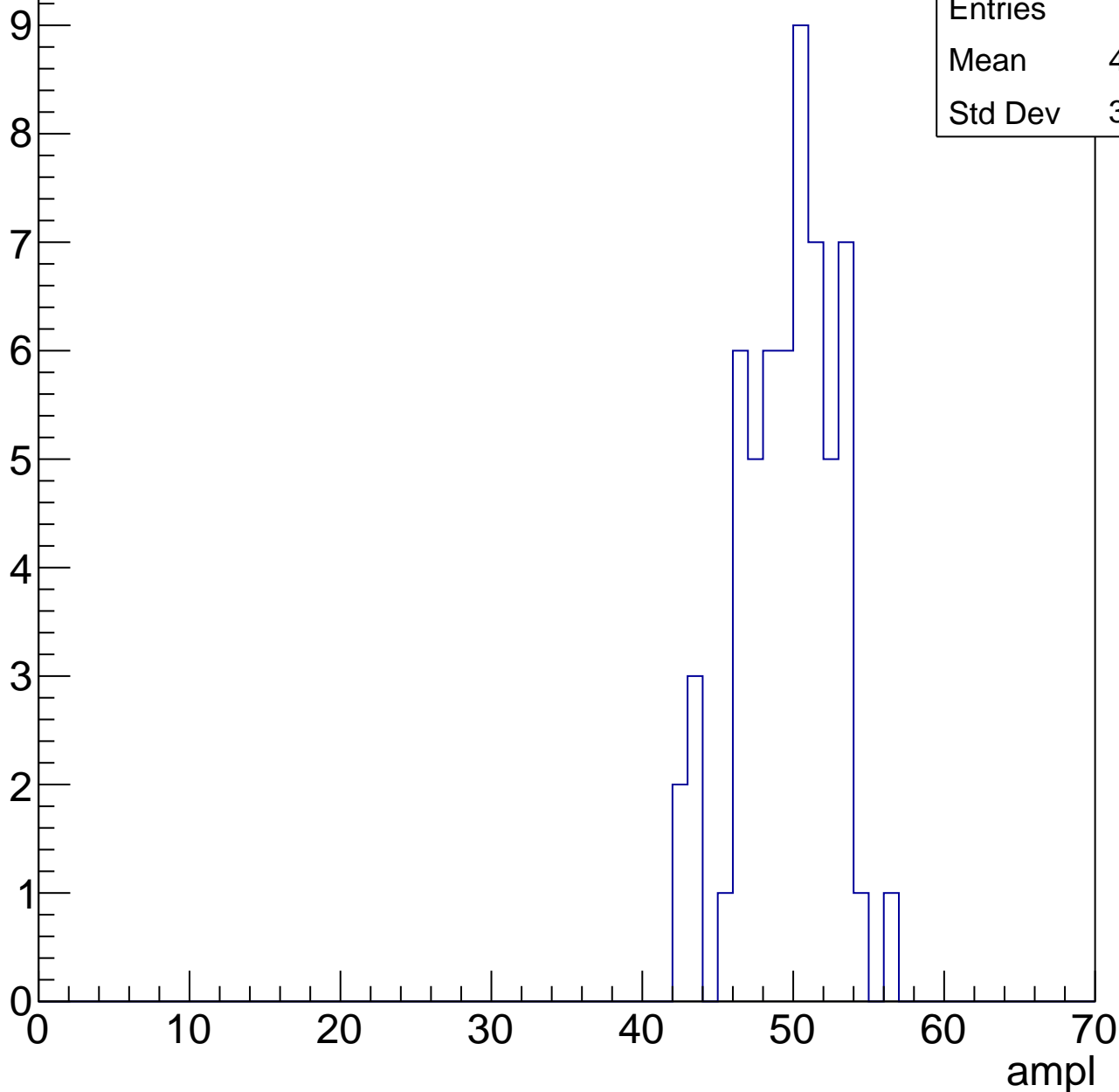


B1L003S, U19-ch57, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

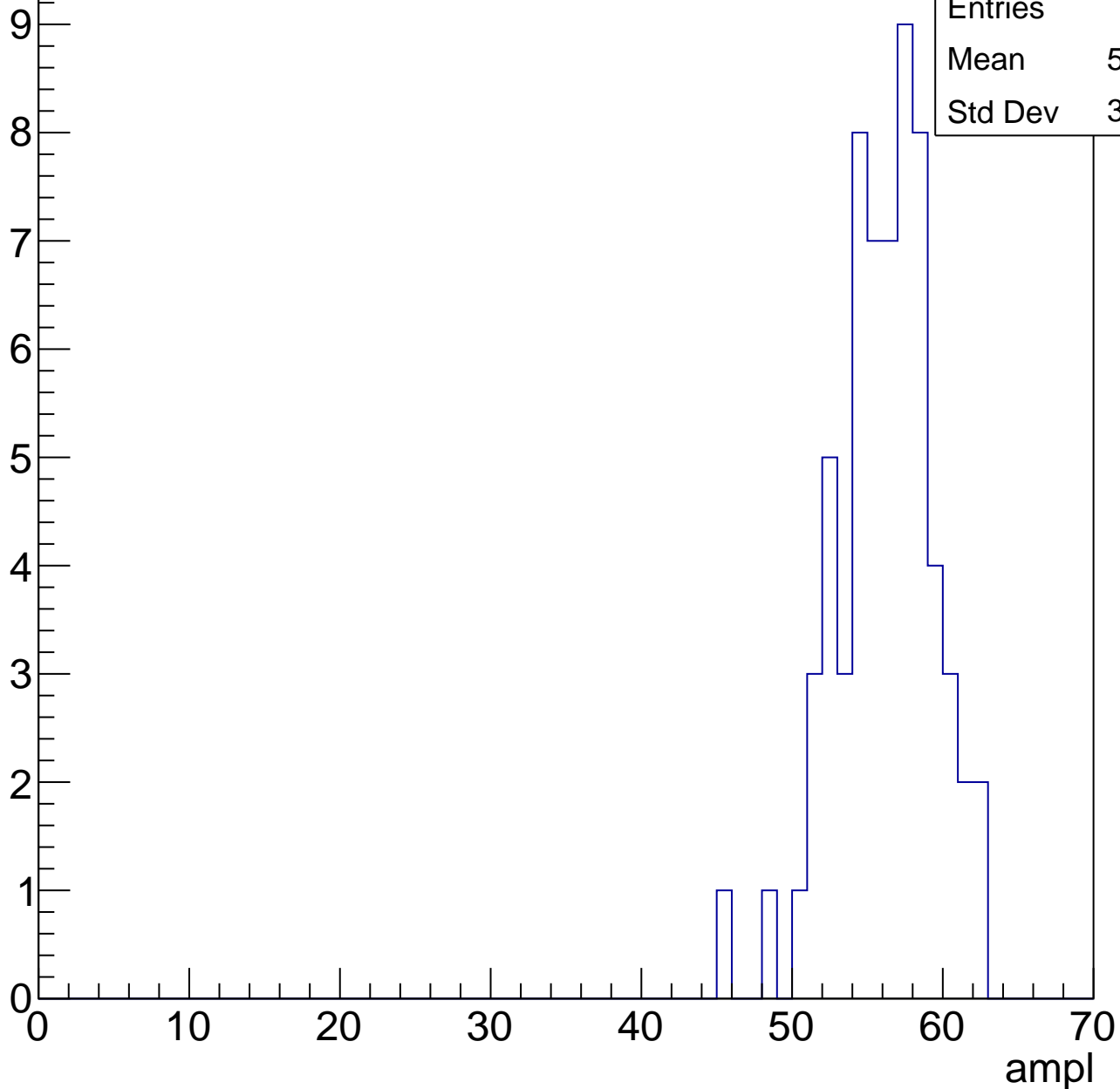
Entries	59
Mean	49.14
Std Dev	3.105



B1L003S, U19-ch57, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

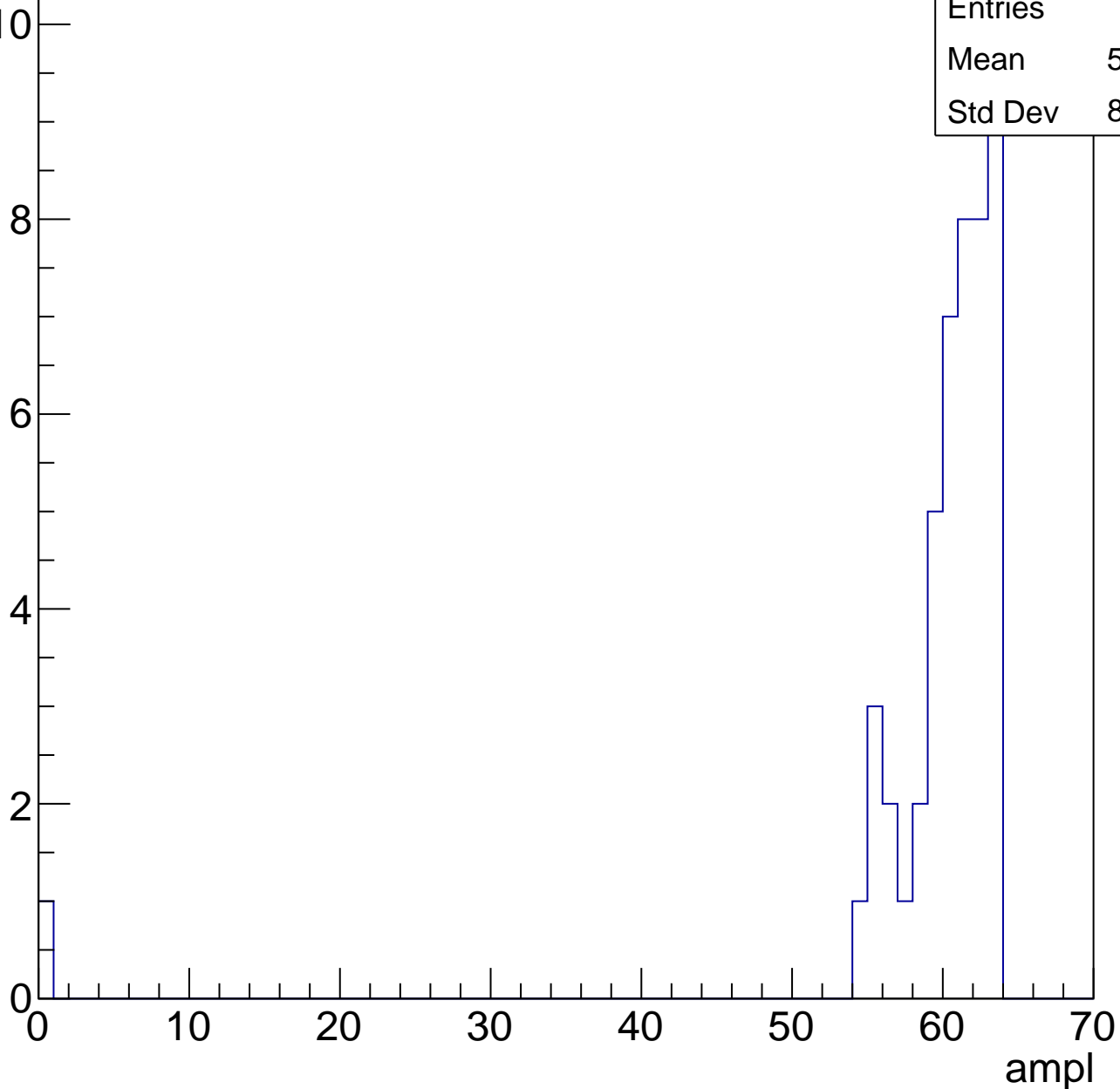


B1L003S, U19-ch57, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

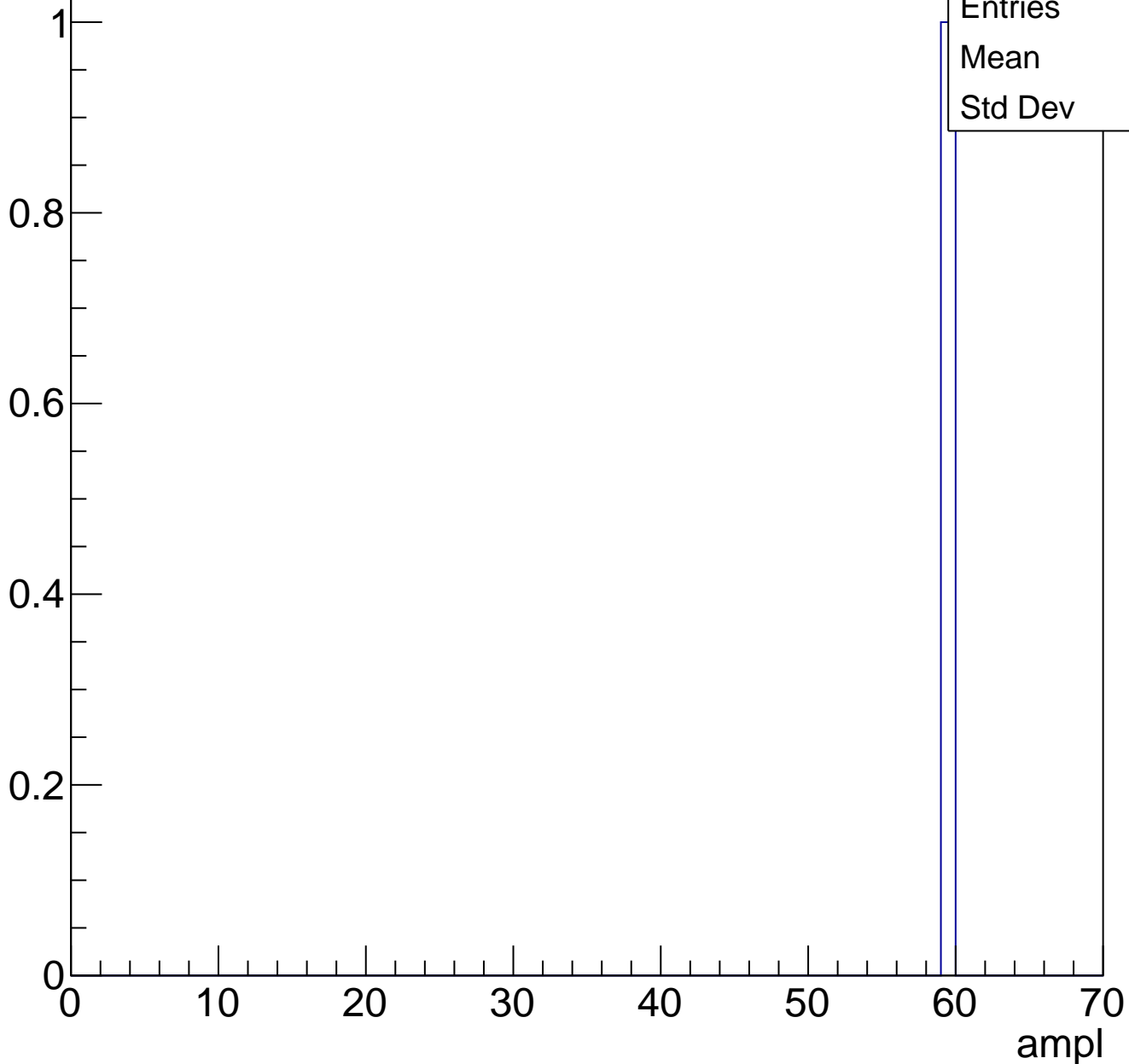
Entries	48
Mean	59.02
Std Dev	8.955



B1L003S, U19-ch57, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	59
Std Dev	0

B1L003S, U19-ch57, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch58, adc0

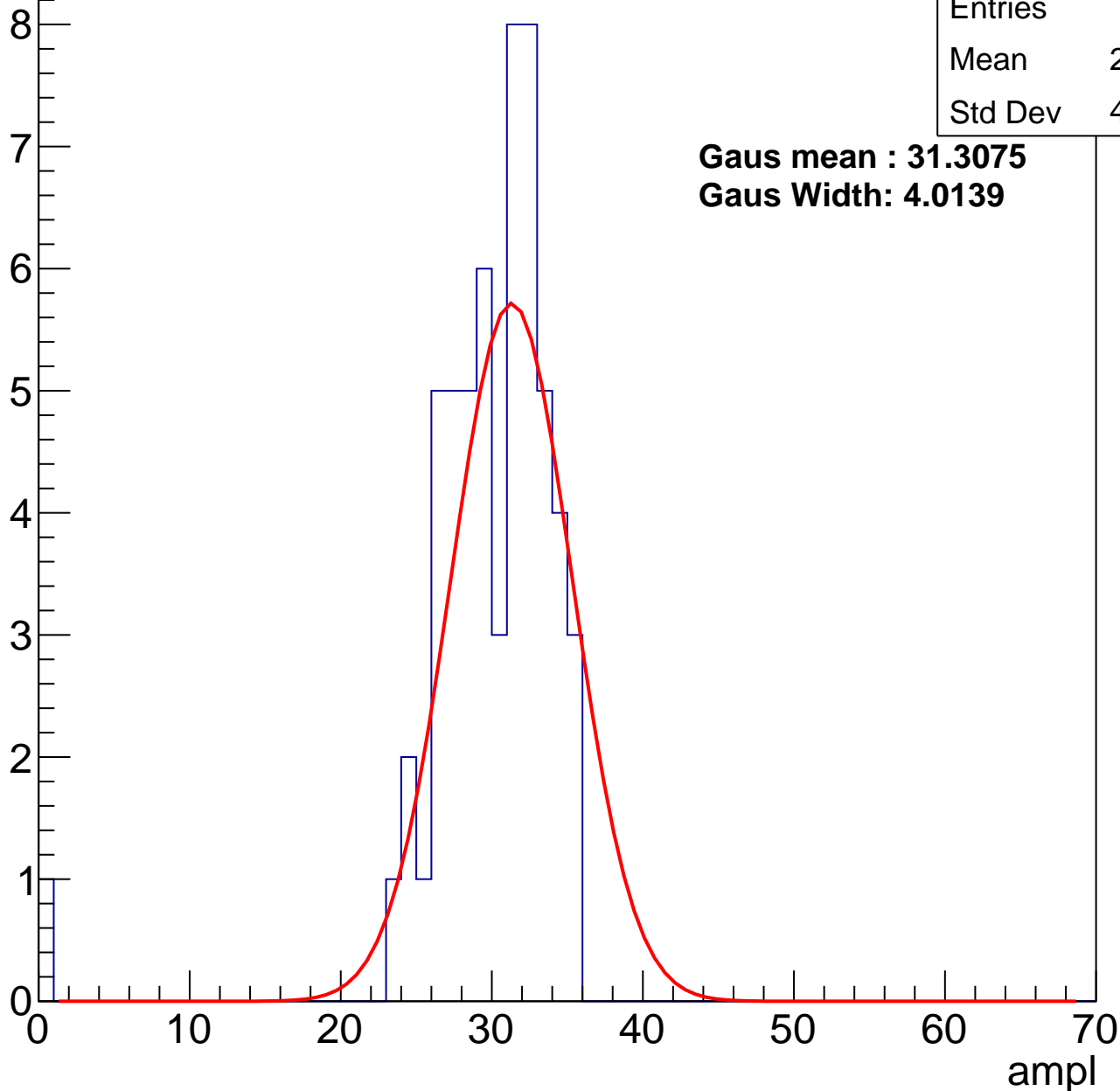
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.39
Std Dev	4.955

Gaus mean : 31.3075

Gaus Width: 4.0139



B1L003S, U19-ch58, adc1

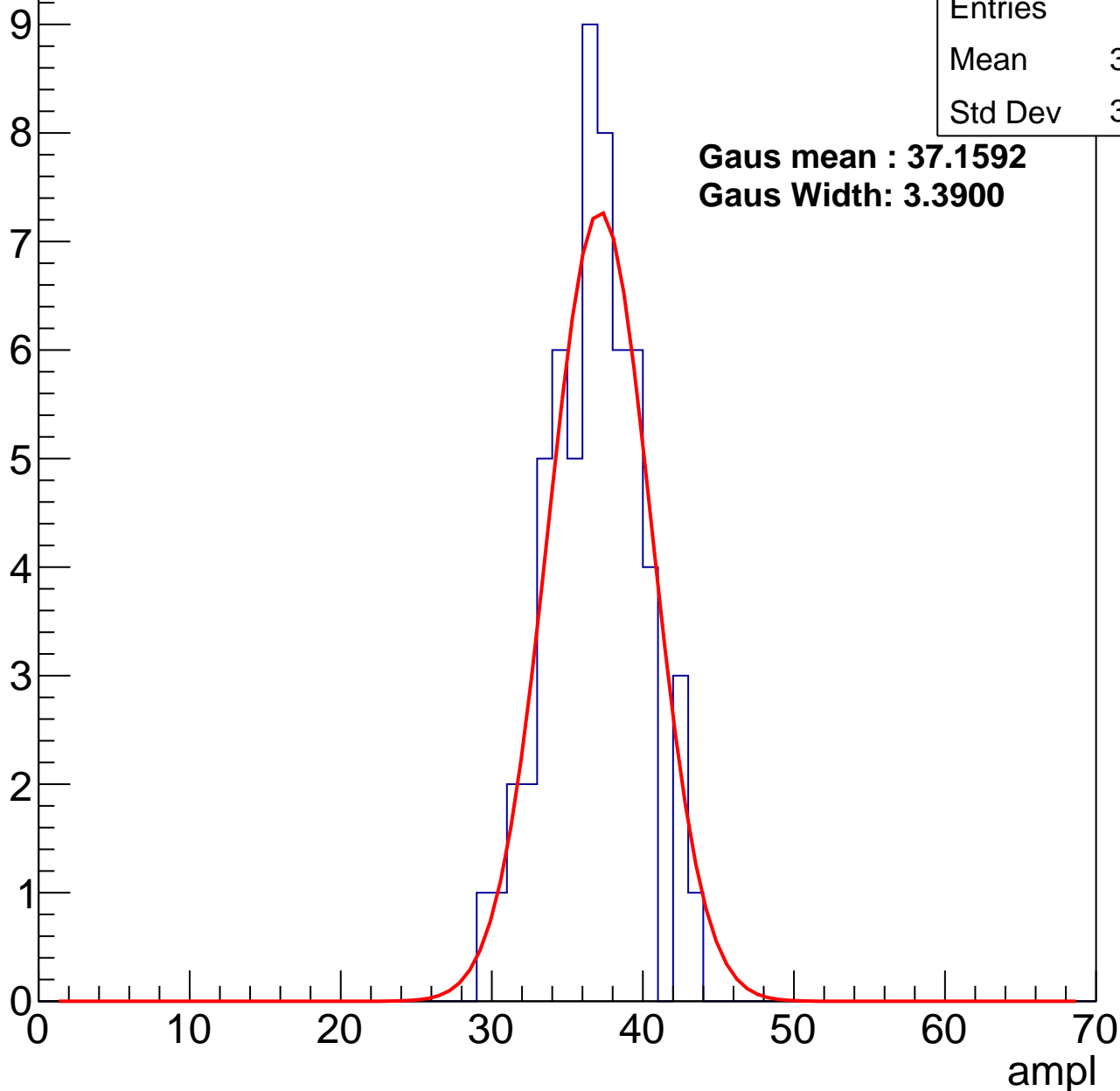
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.27
Std Dev	3.036

Gaus mean : 37.1592

Gaus Width: 3.3900



B1L003S, U19-ch58, adc2

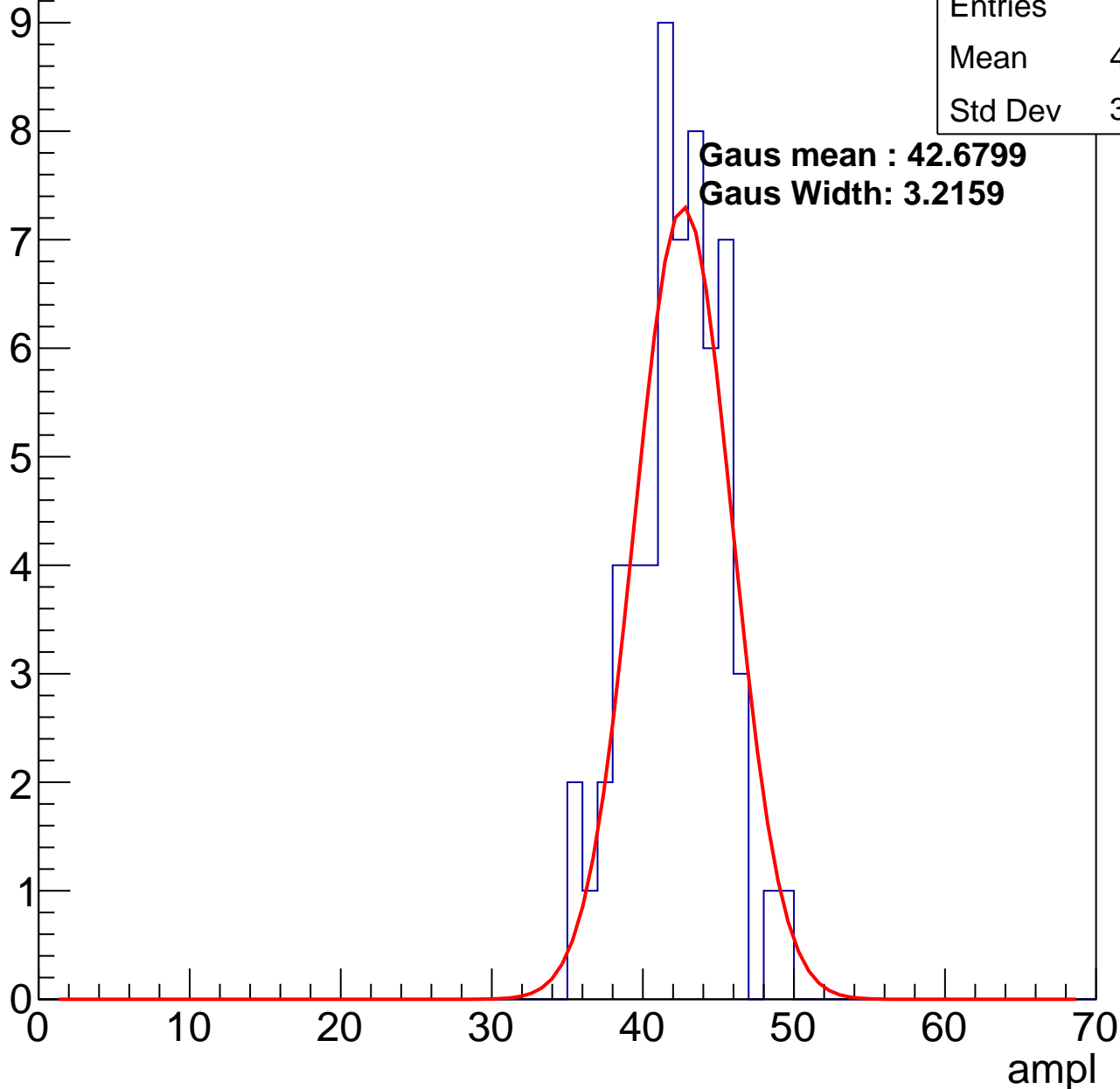
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.85
Std Dev	3.013

Gaus mean : 42.6799

Gaus Width: 3.2159

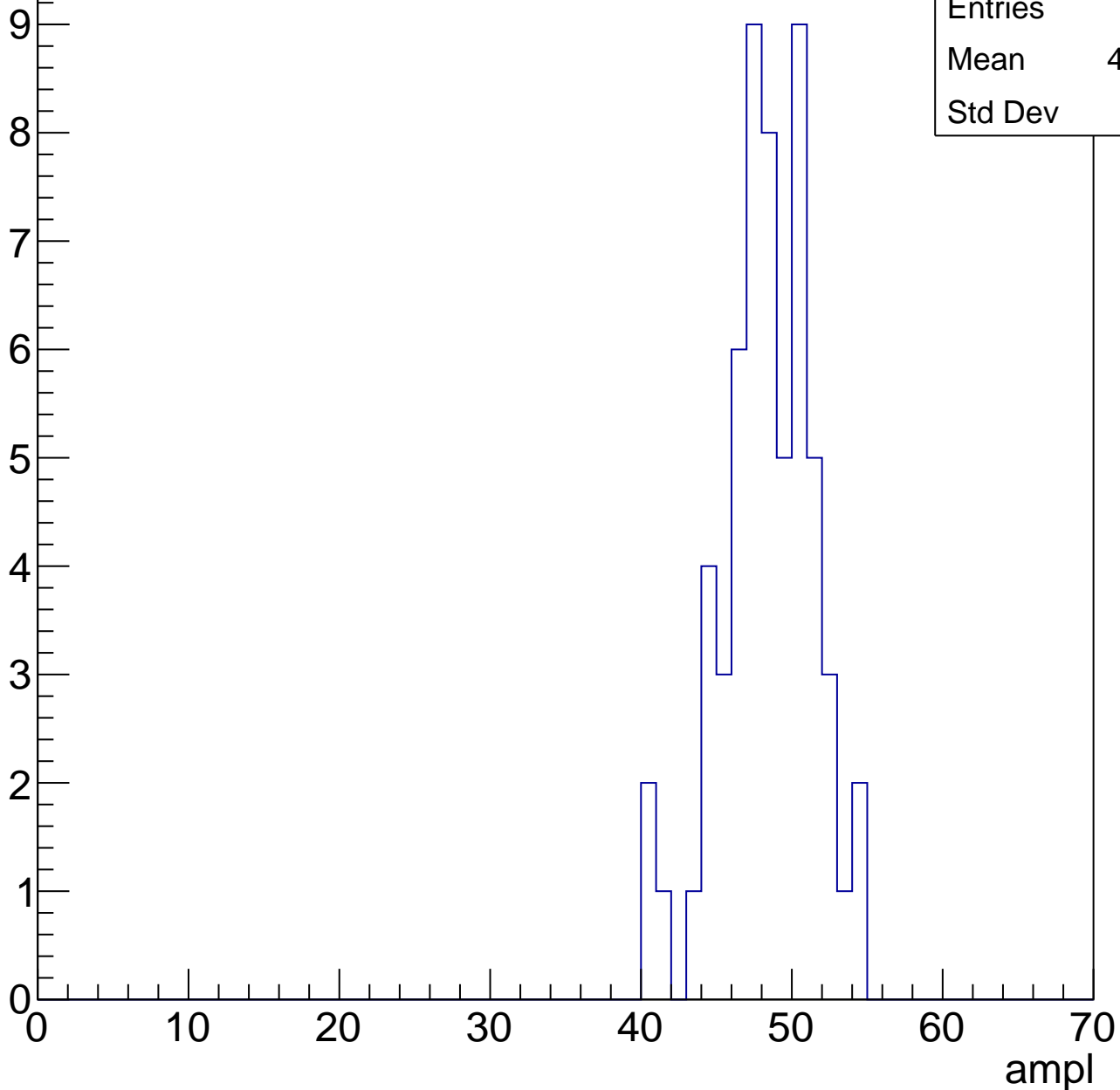


B1L003S, U19-ch58, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	47.88
Std Dev	3.07

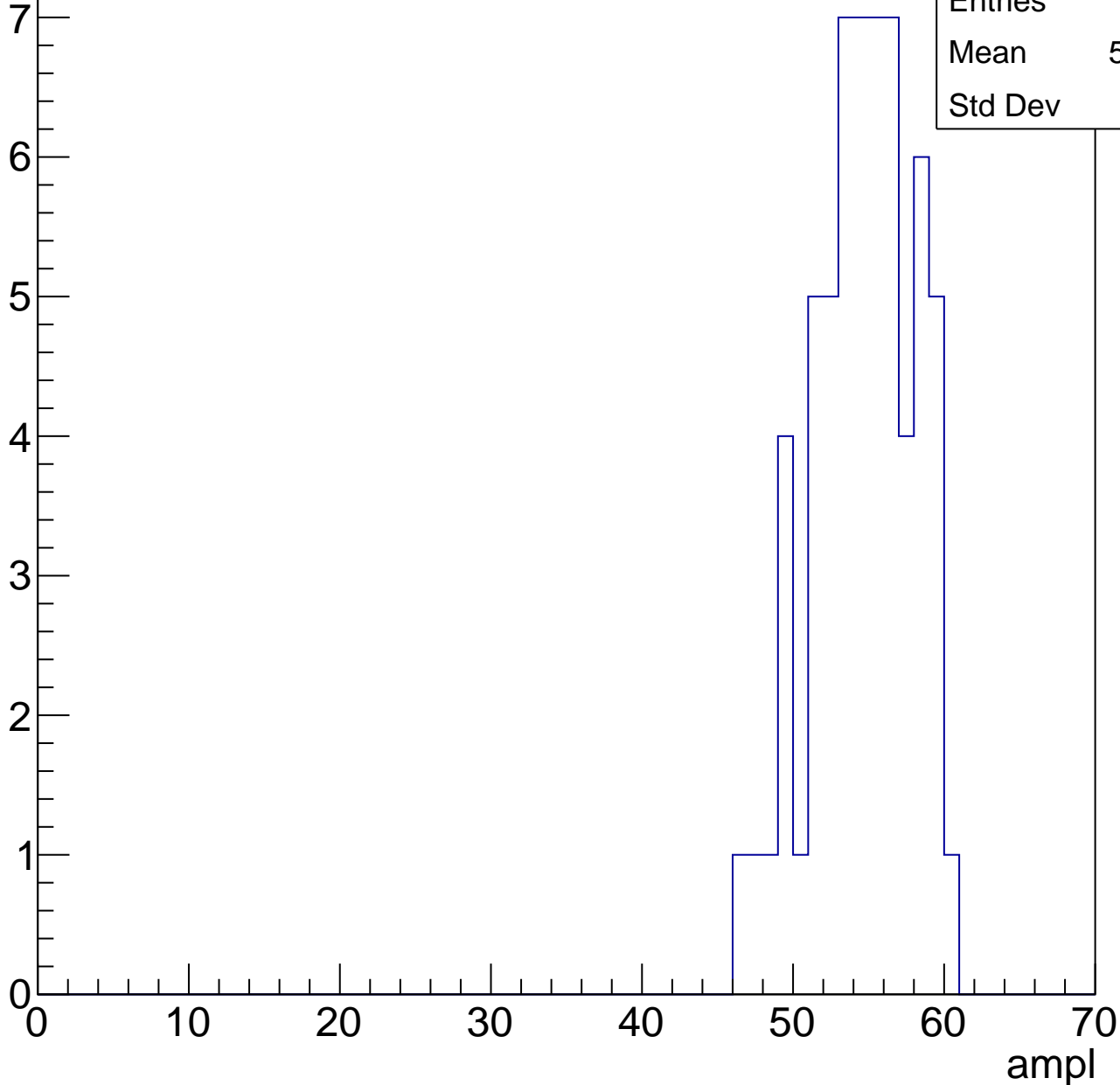


B1L003S, U19-ch58, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	54.18
Std Dev	3.27

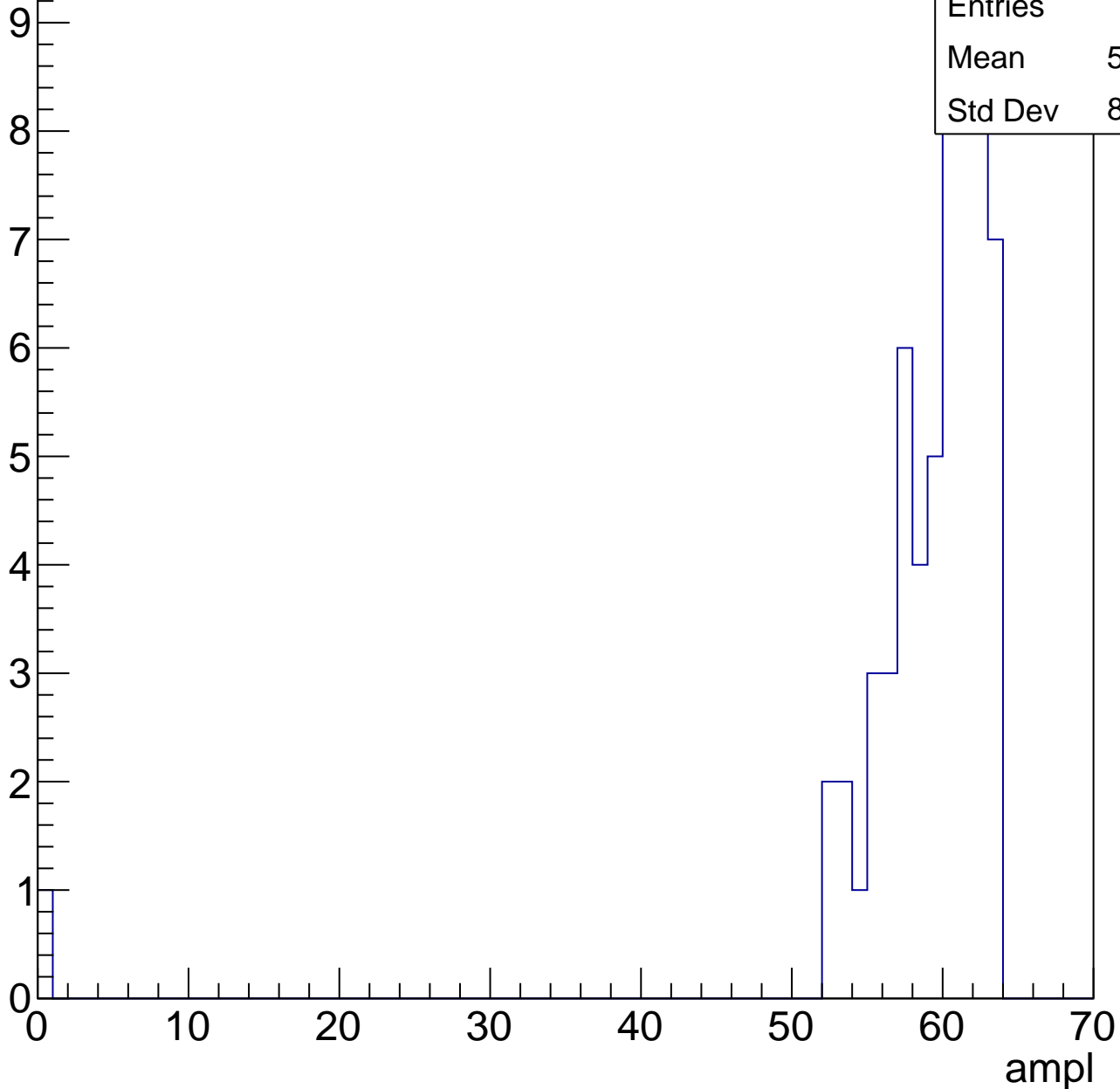


B1L003S, U19-ch58, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	58.19
Std Dev	8.206



B1L003S, U19-ch58, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	7
Mean	61.86
Std Dev	1.125

B1L003S, U19-ch58, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L003S, U19-ch59, adc0

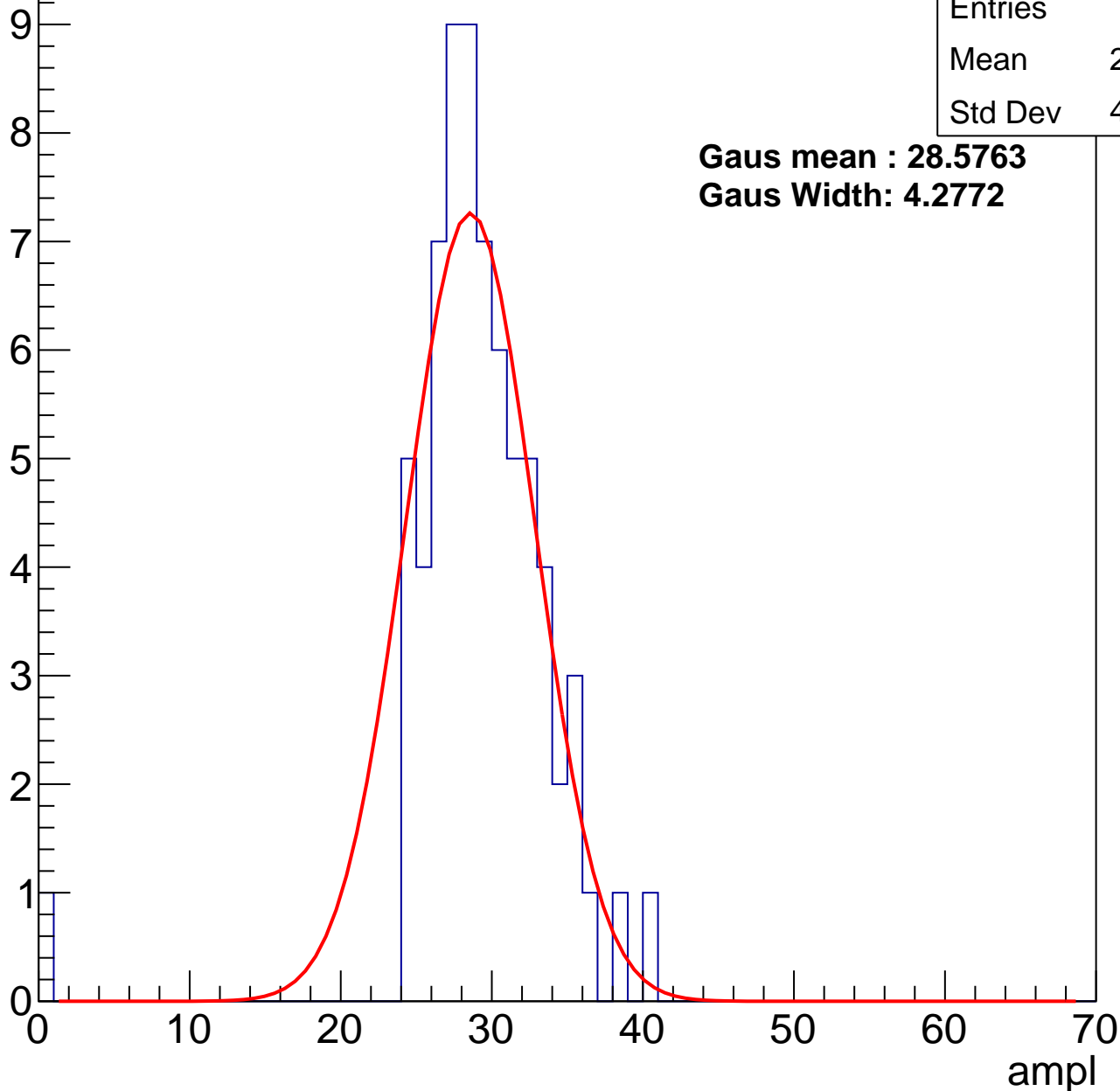
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.77
Std Dev	4.897

Gaus mean : 28.5763

Gaus Width: 4.2772



B1L003S, U19-ch59, adc1

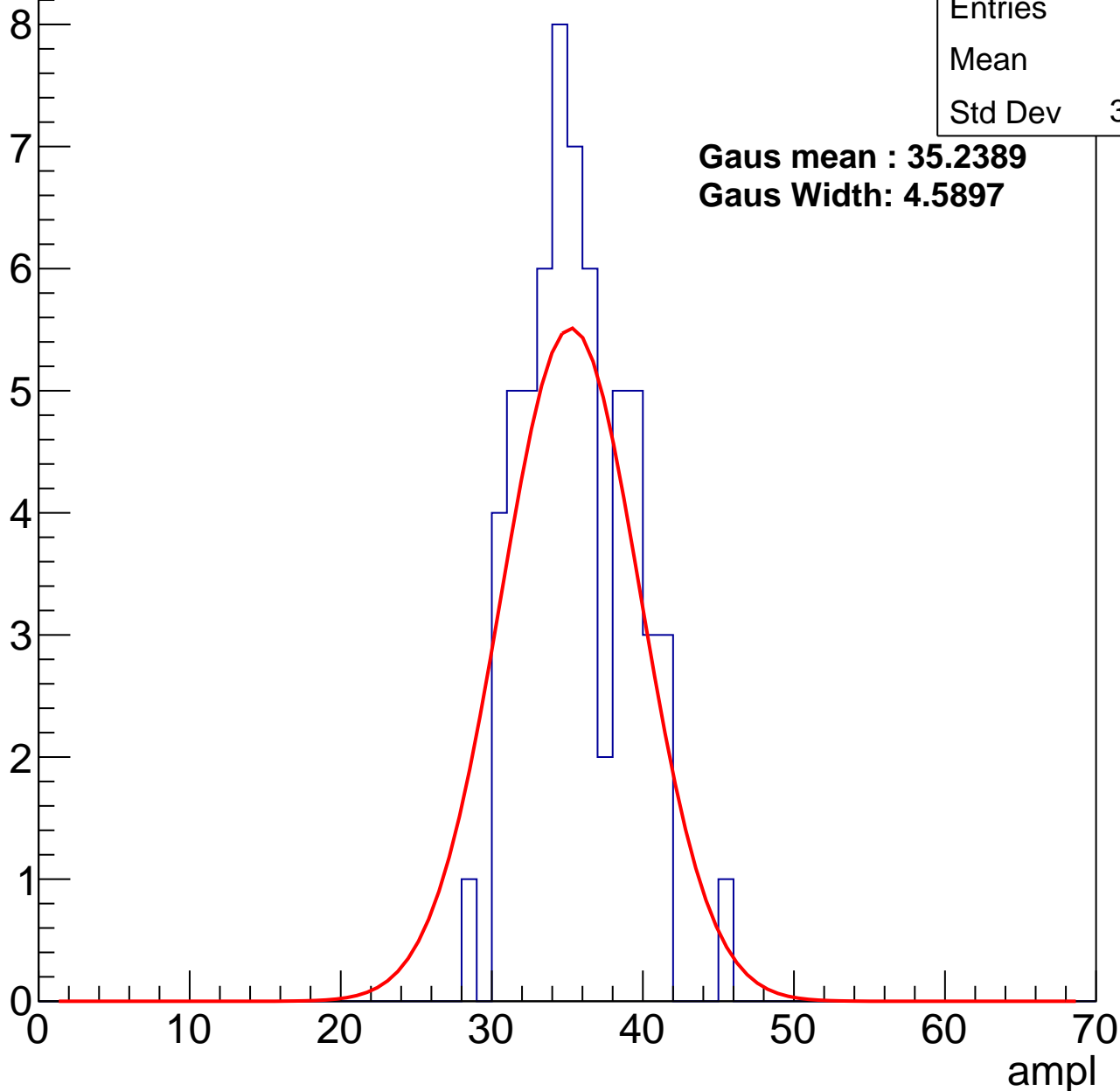
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.1
Std Dev	3.453

Gaus mean : 35.2389

Gaus Width: 4.5897



B1L003S, U19-ch59, adc2

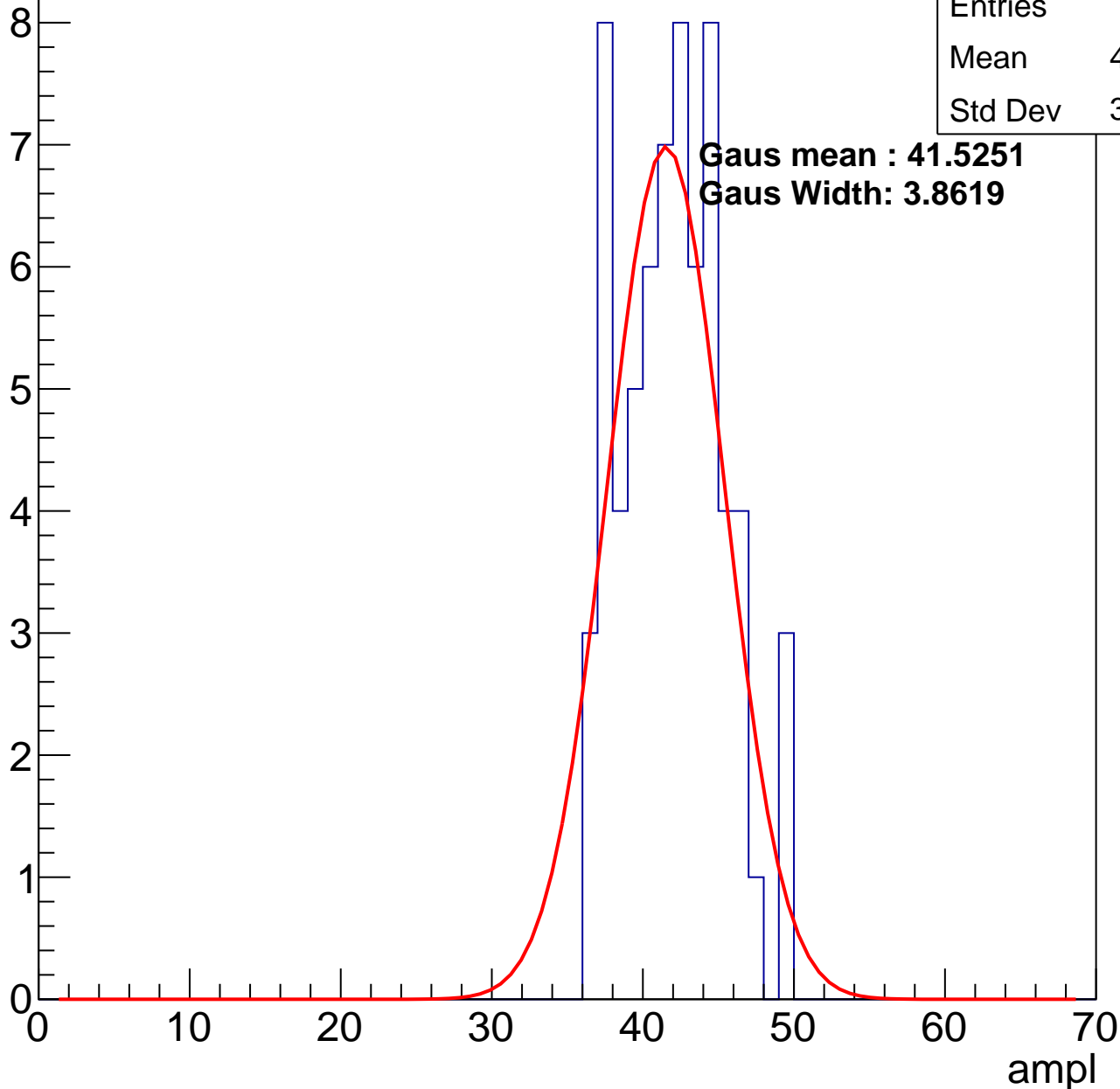
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.52
Std Dev	3.329

Gaus mean : 41.5251

Gaus Width: 3.8619



B1L003S, U19-ch59, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	48.68
Std Dev	3.301

Entry

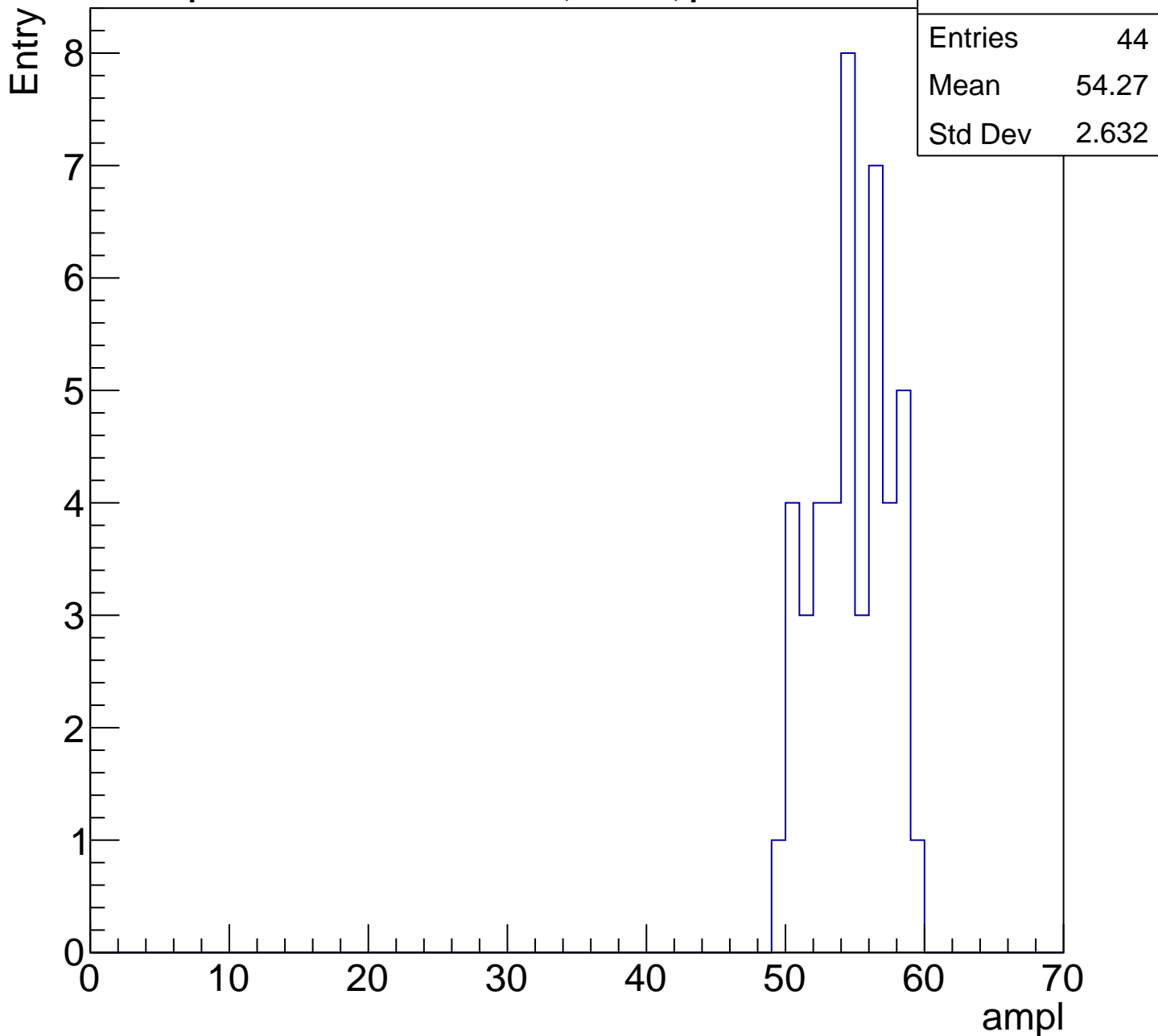
10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl



B1L003S, U19-ch59, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch59, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	64
Mean	59.73
Std Dev	2.32

B1L003S, U19-ch59, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch59, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch60, adc0

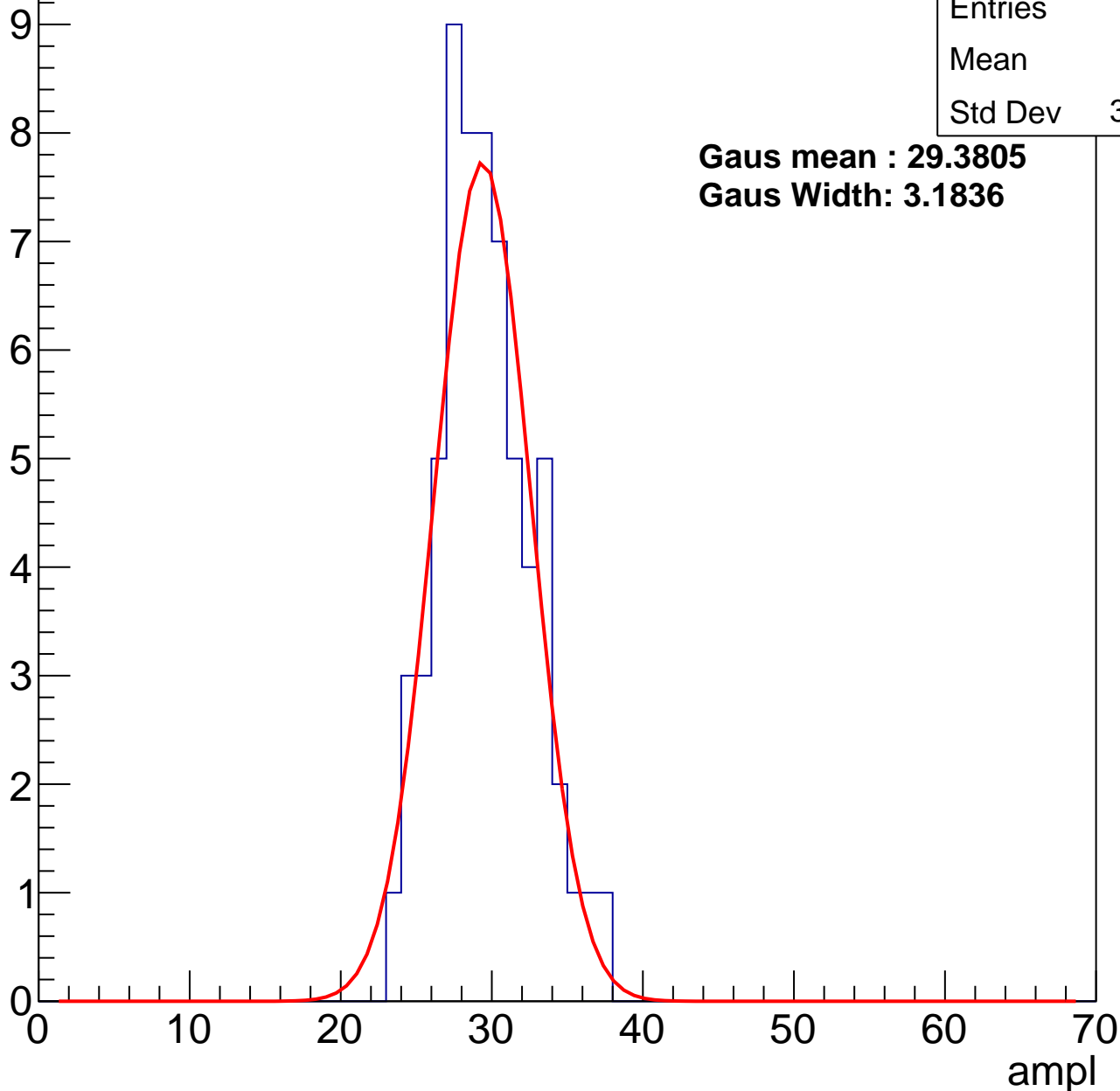
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.1
Std Dev	3.059

Gaus mean : 29.3805

Gaus Width: 3.1836



B1L003S, U19-ch60, adc1

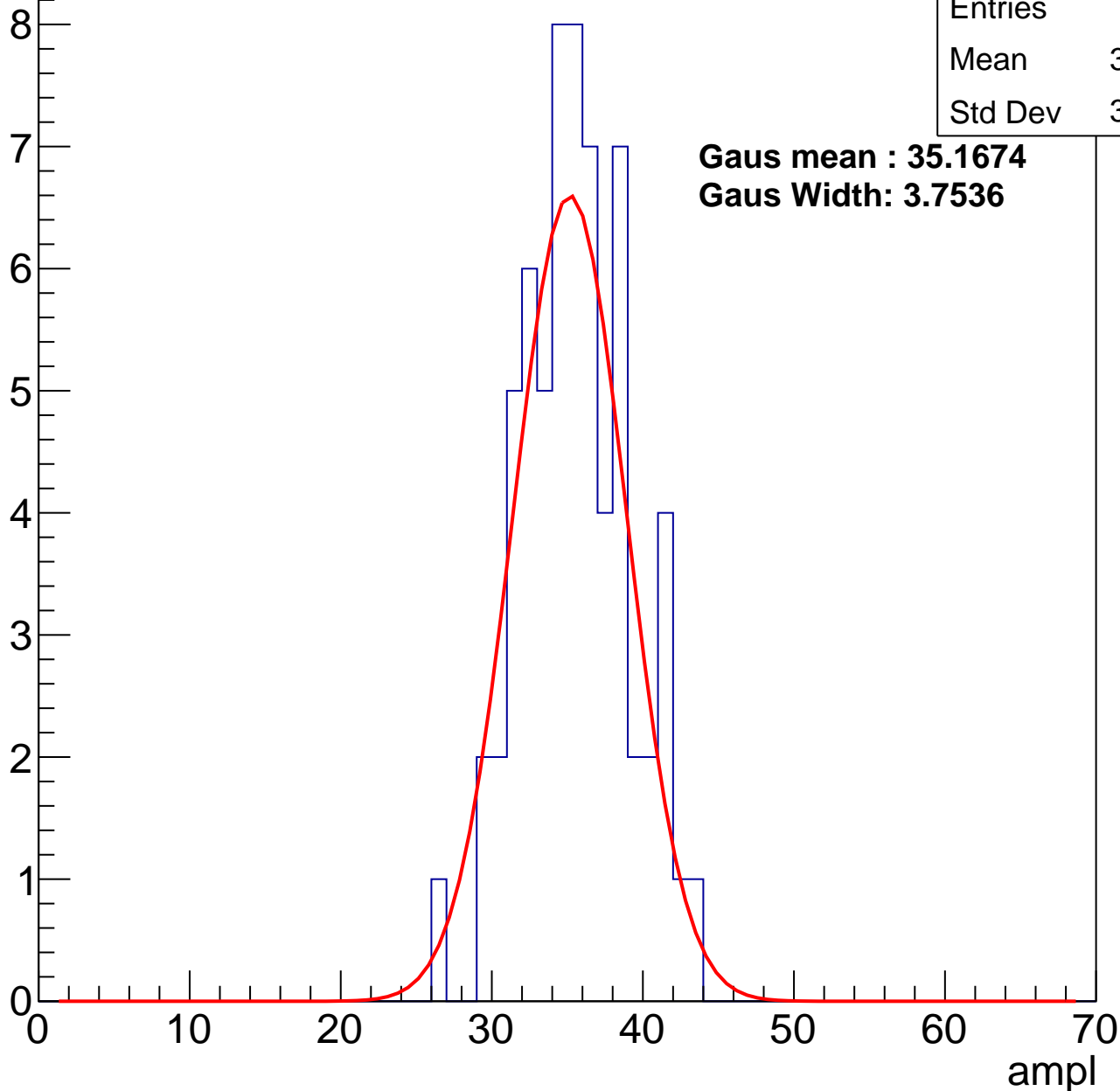
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.09
Std Dev	3.485

Gaus mean : 35.1674

Gaus Width: 3.7536



B1L003S, U19-ch60, adc2

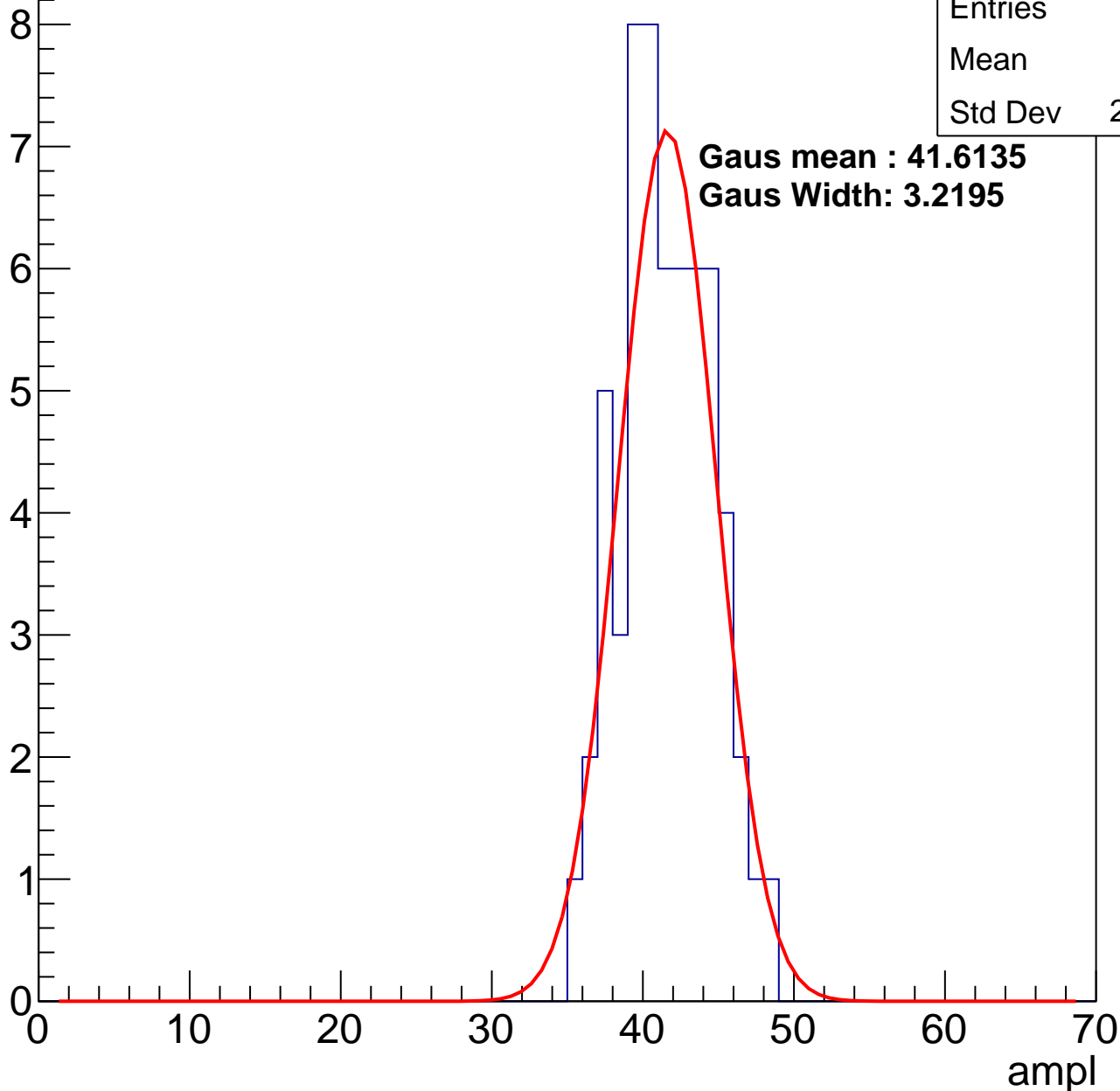
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.1
Std Dev	2.956

Gaus mean : 41.6135

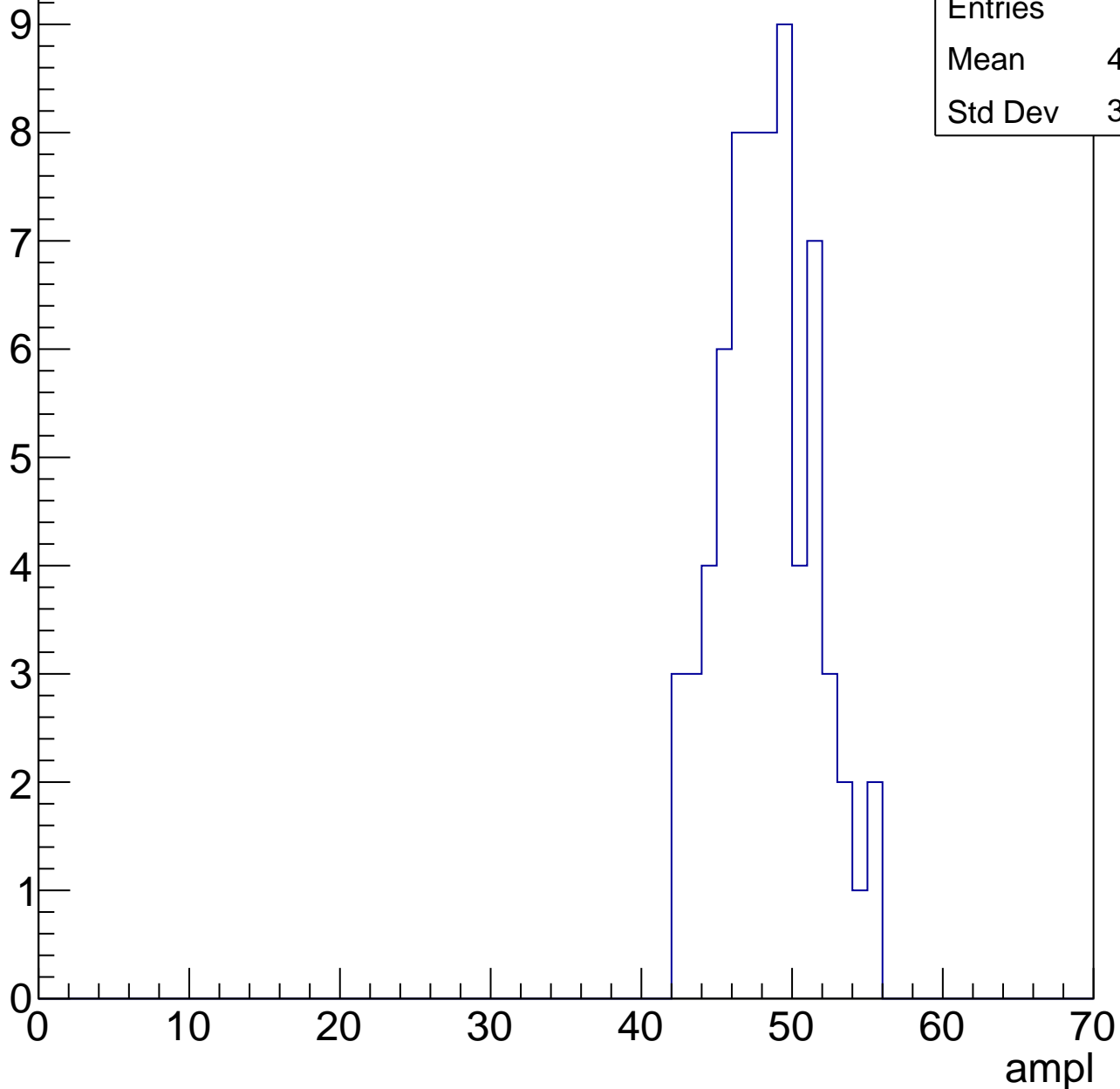
Gaus Width: 3.2195



B1L003S, U19-ch60, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



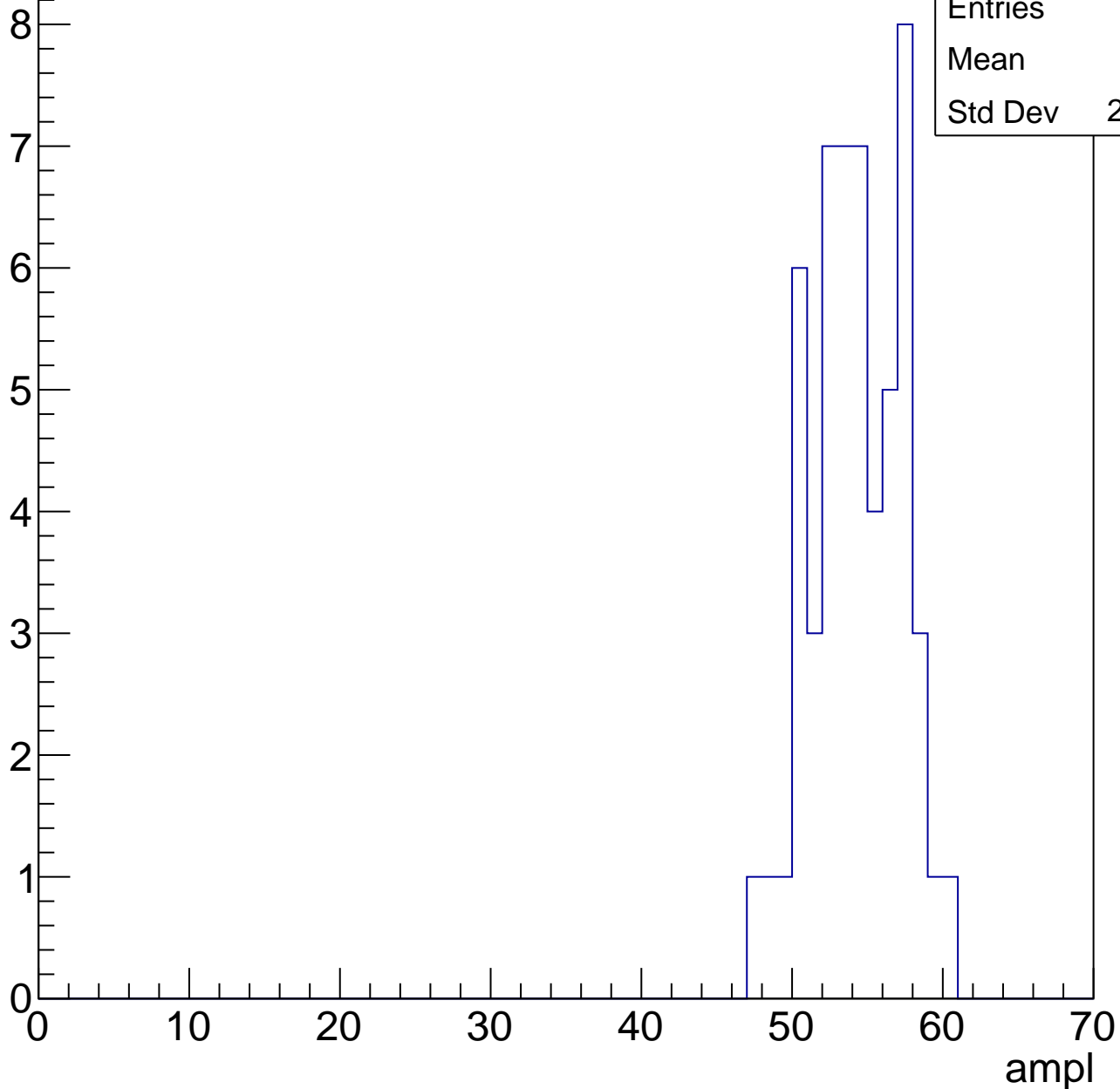
Entries	68
Mean	47.84
Std Dev	3.114

B1L003S, U19-ch60, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	53.8
Std Dev	2.926

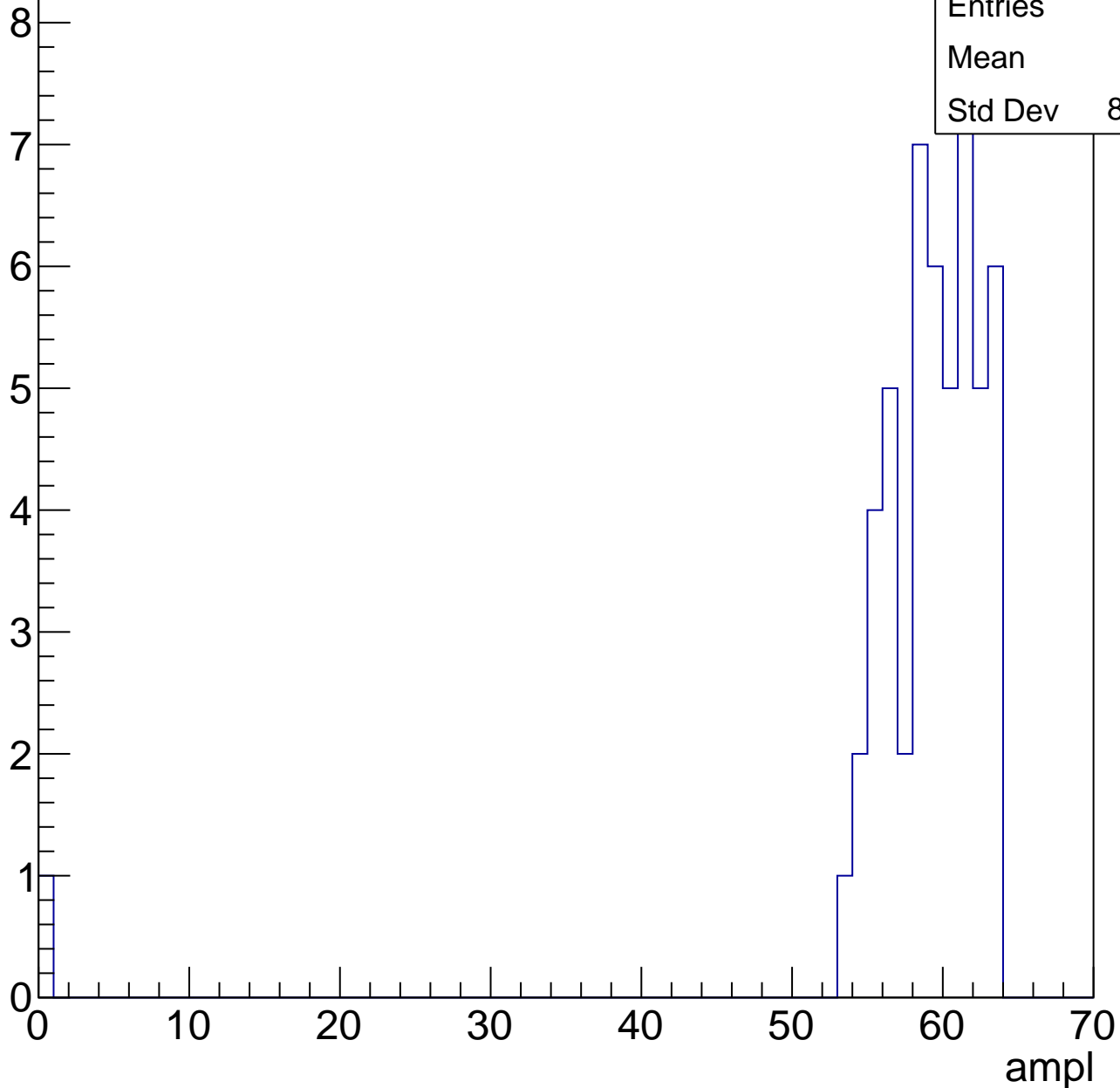


B1L003S, U19-ch60, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	57.9
Std Dev	8.556

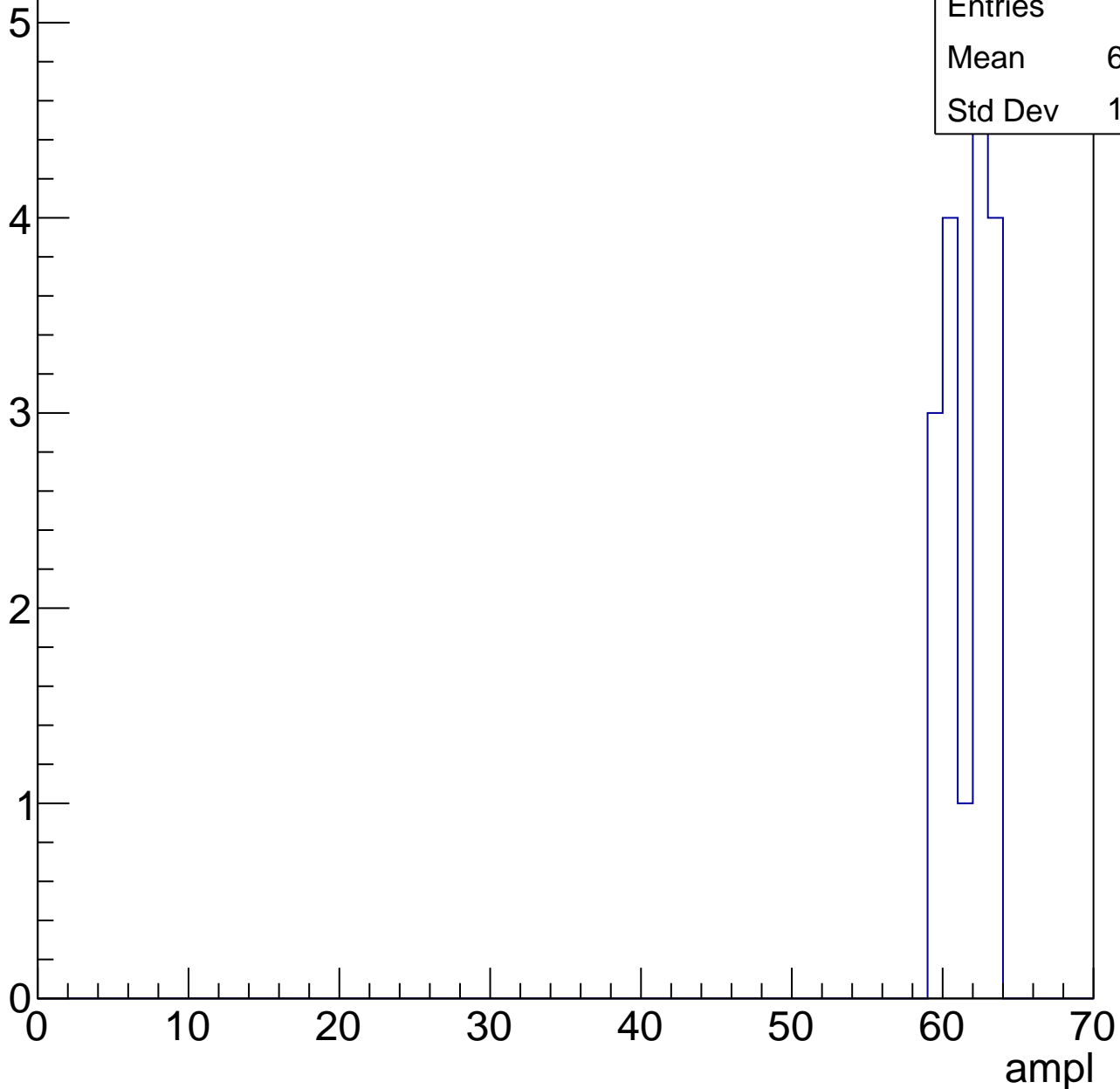


B1L003S, U19-ch60, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

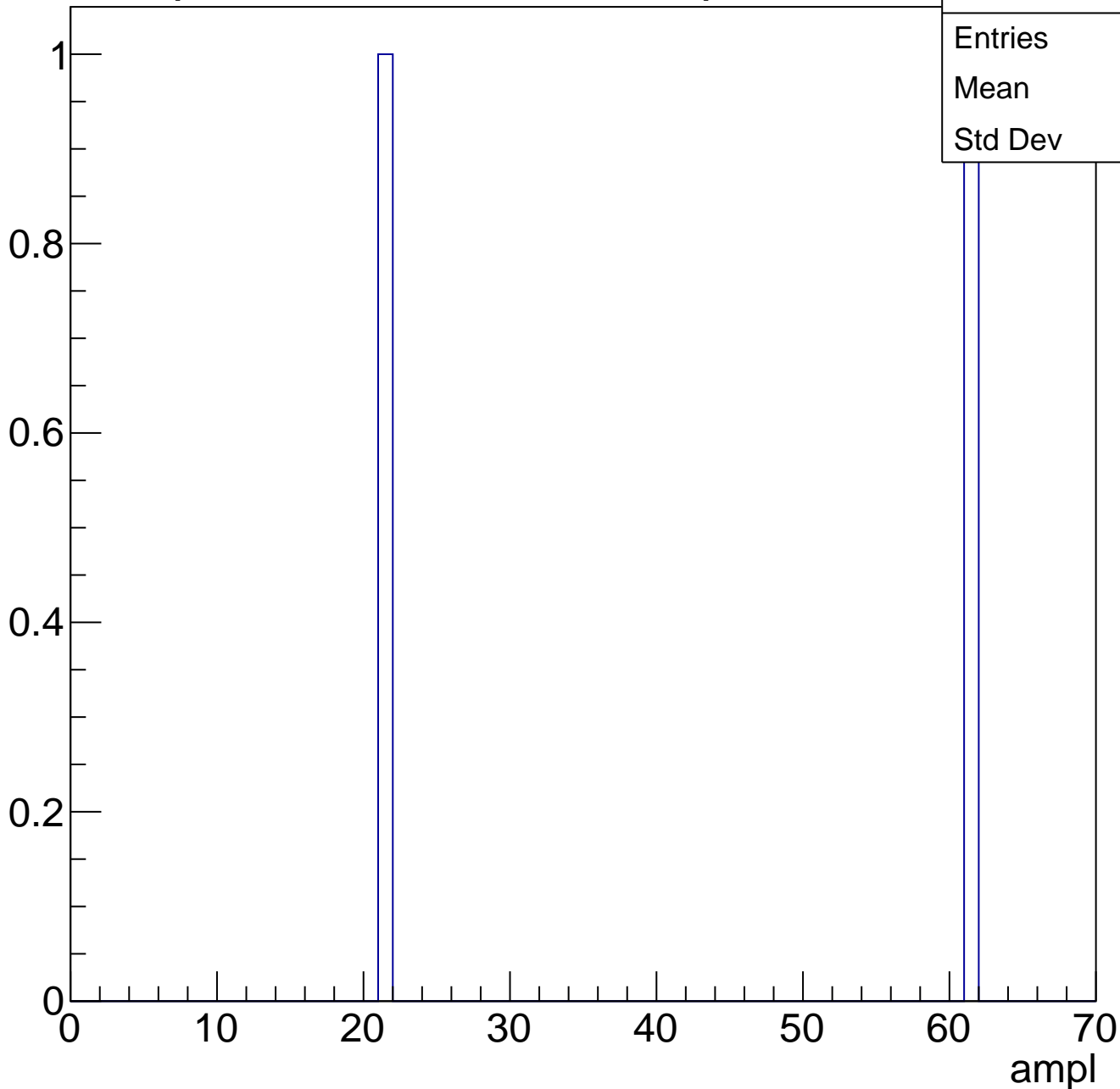
Entries	17
Mean	61.18
Std Dev	1.465



B1L003S, U19-ch60, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch61, adc0

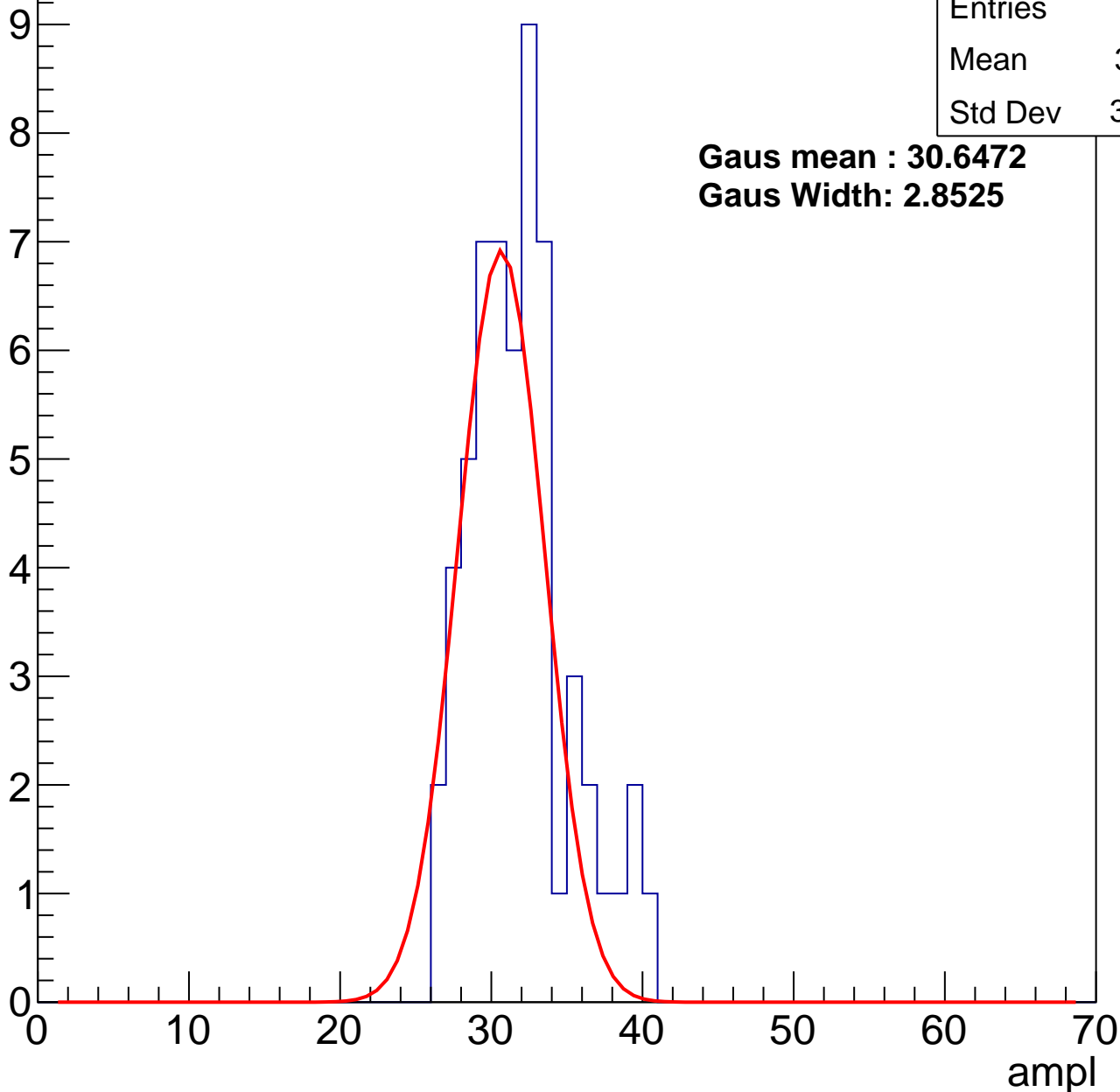
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	31.41
Std Dev	3.275

Gaus mean : 30.6472

Gaus Width: 2.8525



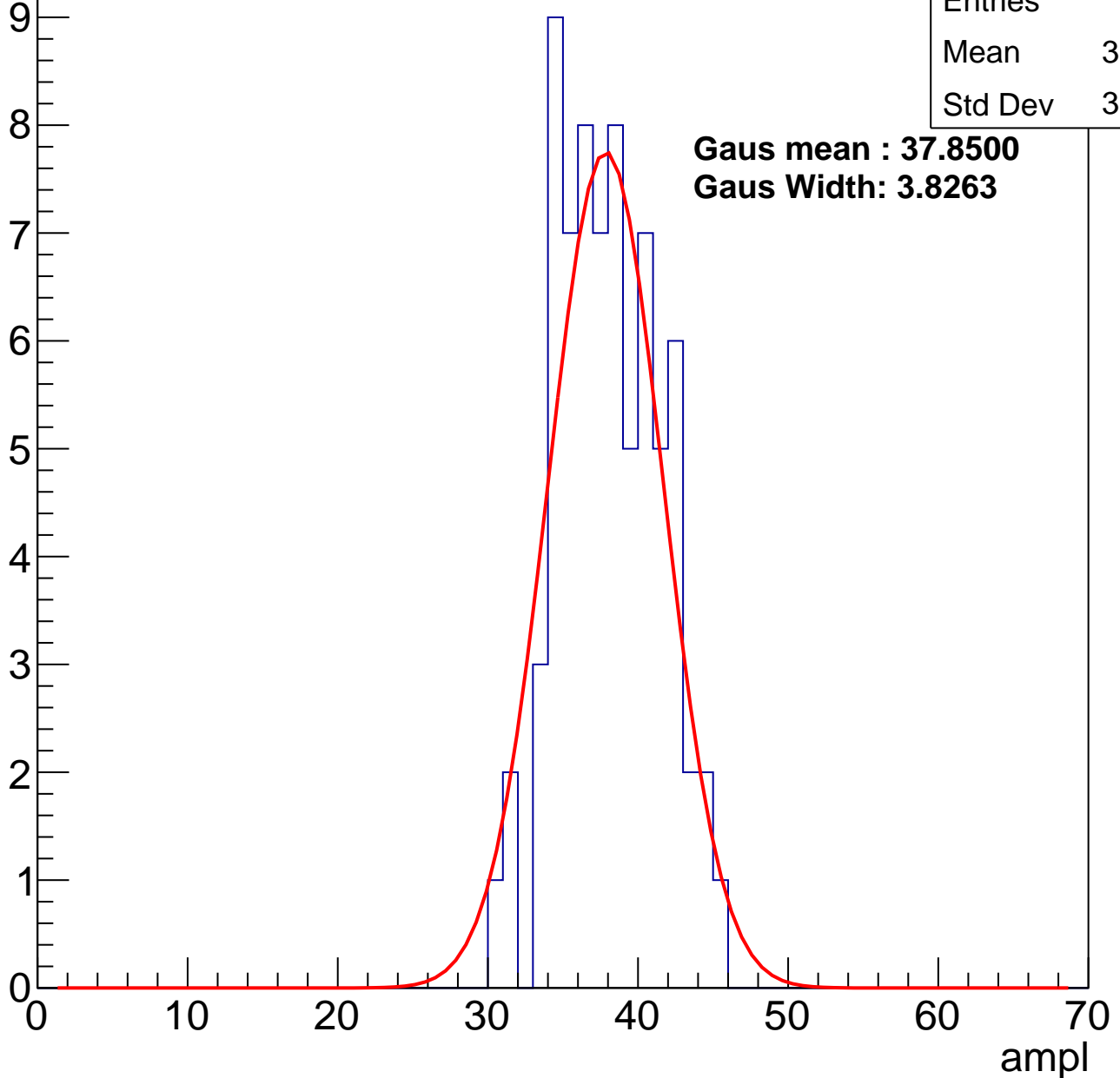
B1L003S, U19-ch61, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37.59
Std Dev	3.343

Gaus mean : 37.8500
Gaus Width: 3.8263



B1L003S, U19-ch61, adc2

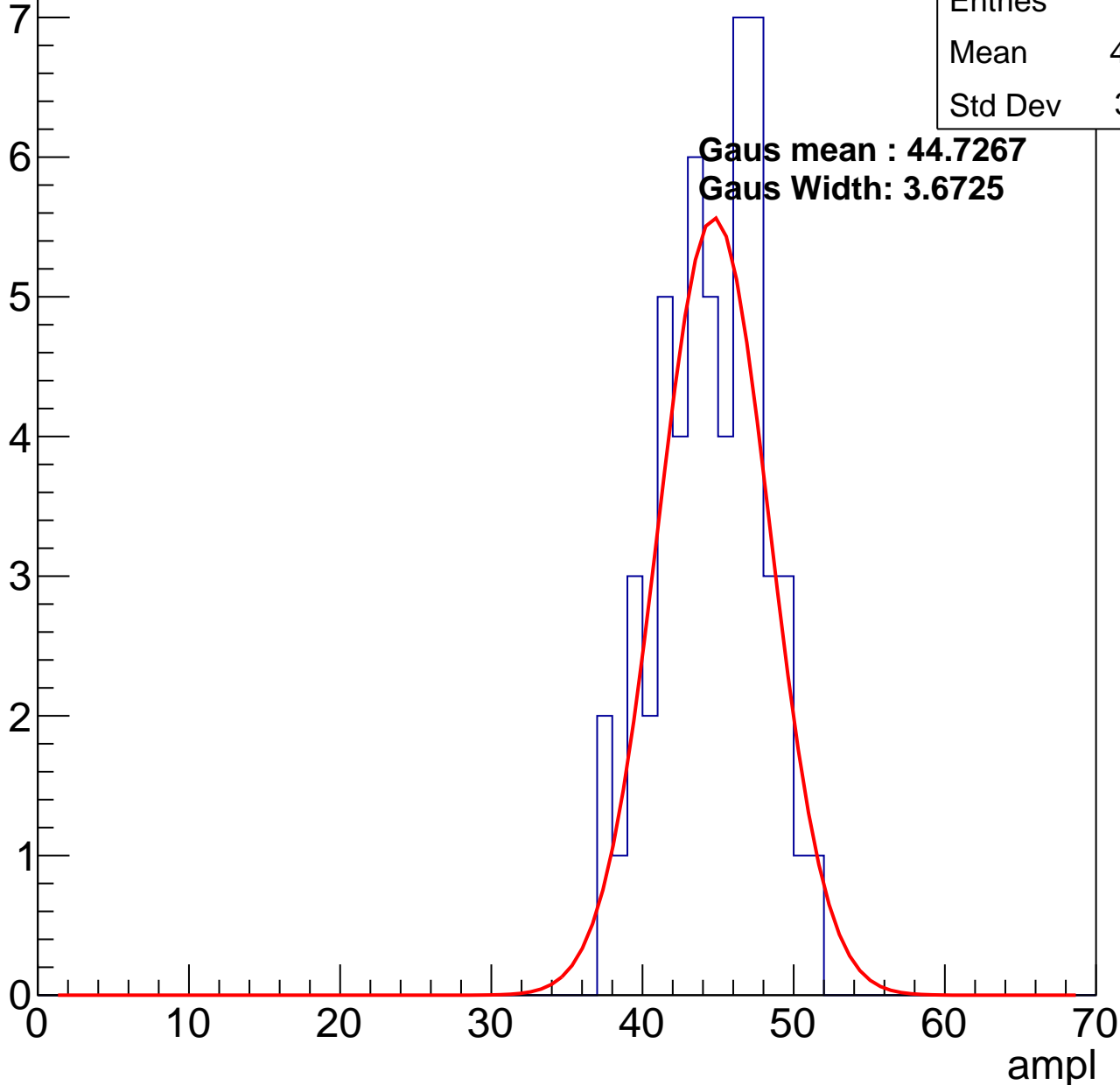
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	44.13
Std Dev	3.361

Gaus mean : 44.7267

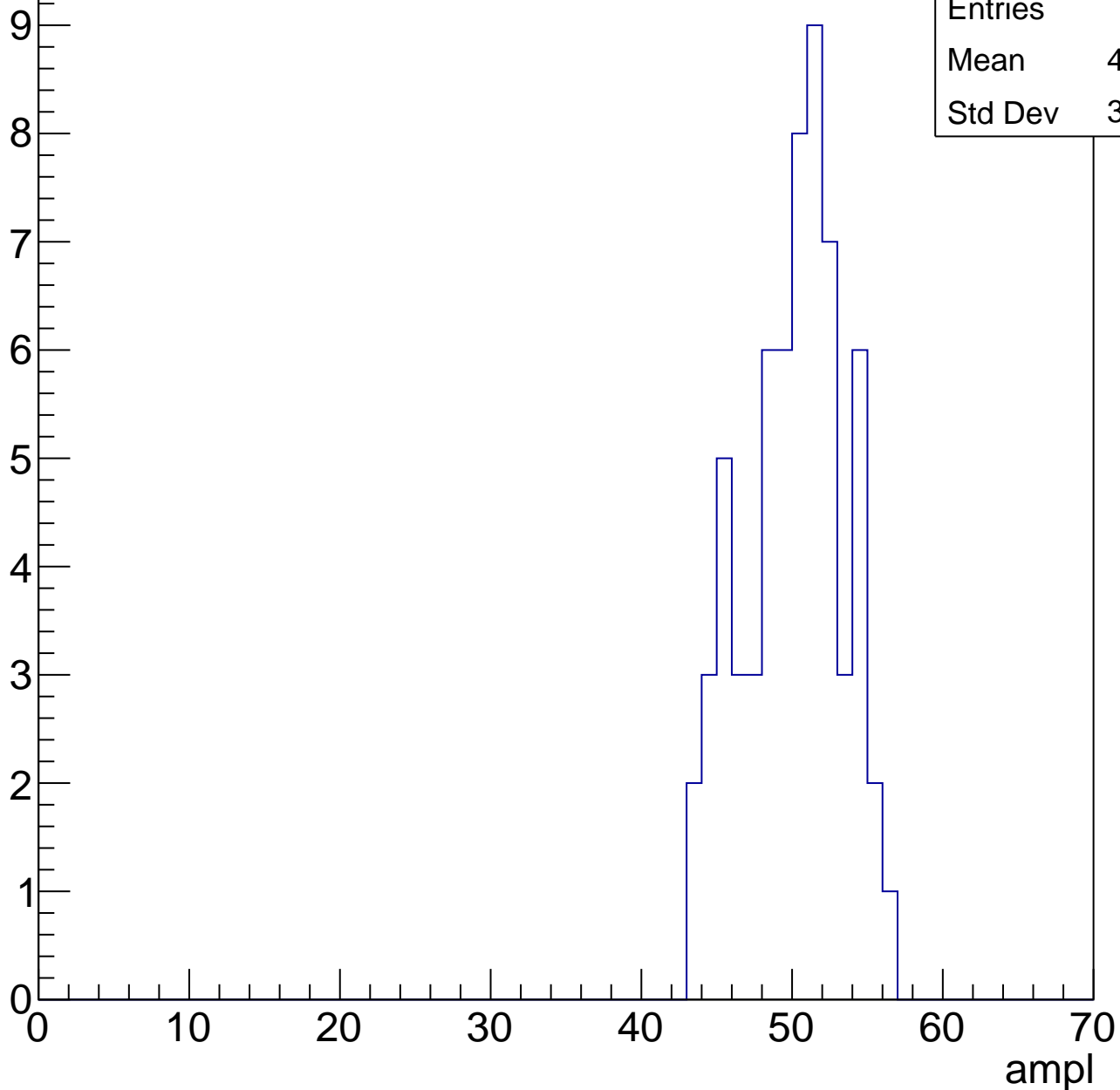
Gaus Width: 3.6725



B1L003S, U19-ch61, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



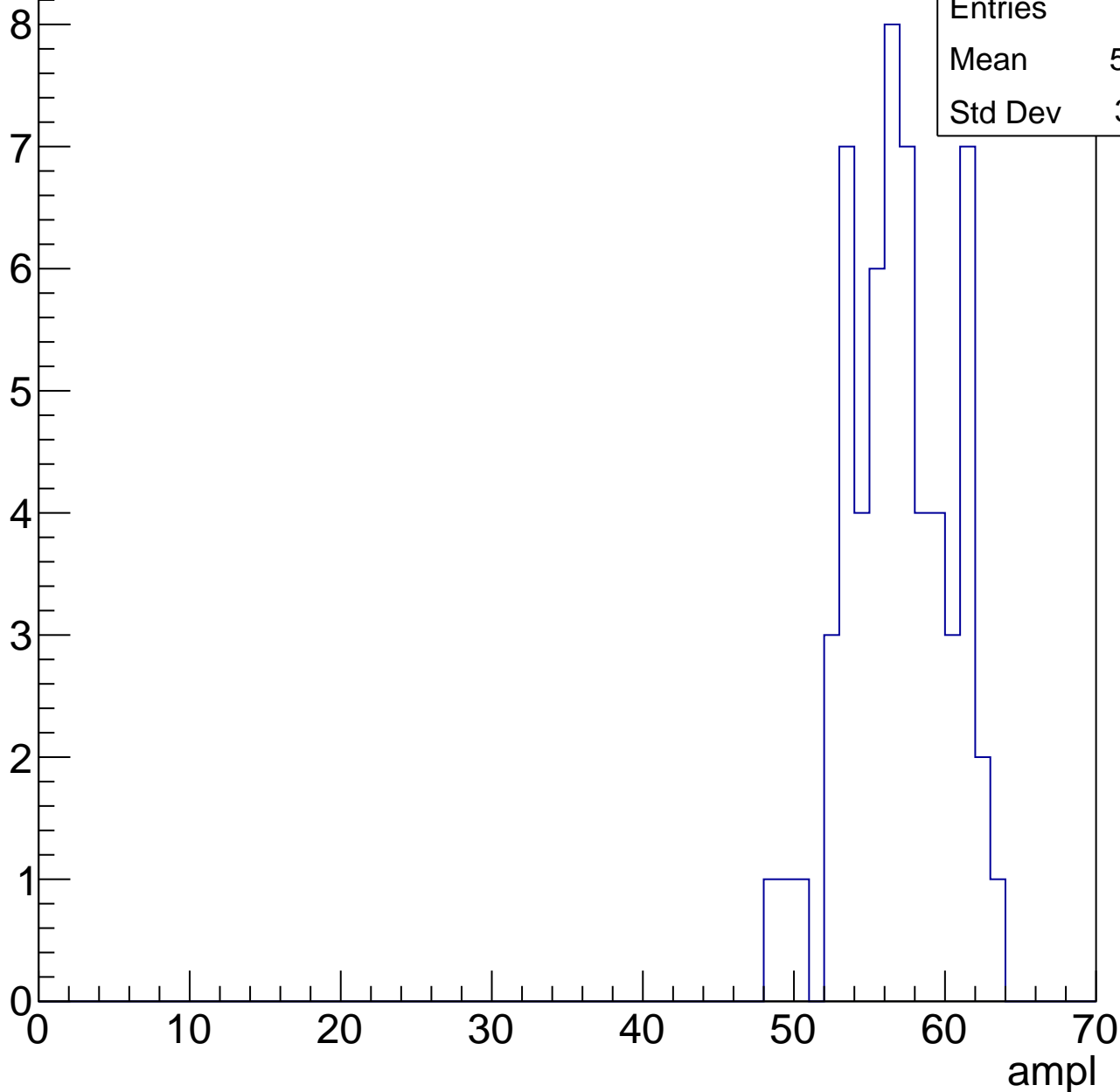
Entries	64
Mean	49.62
Std Dev	3.243

B1L003S, U19-ch61, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.42
Std Dev	3.381



B1L003S, U19-ch61, adc5

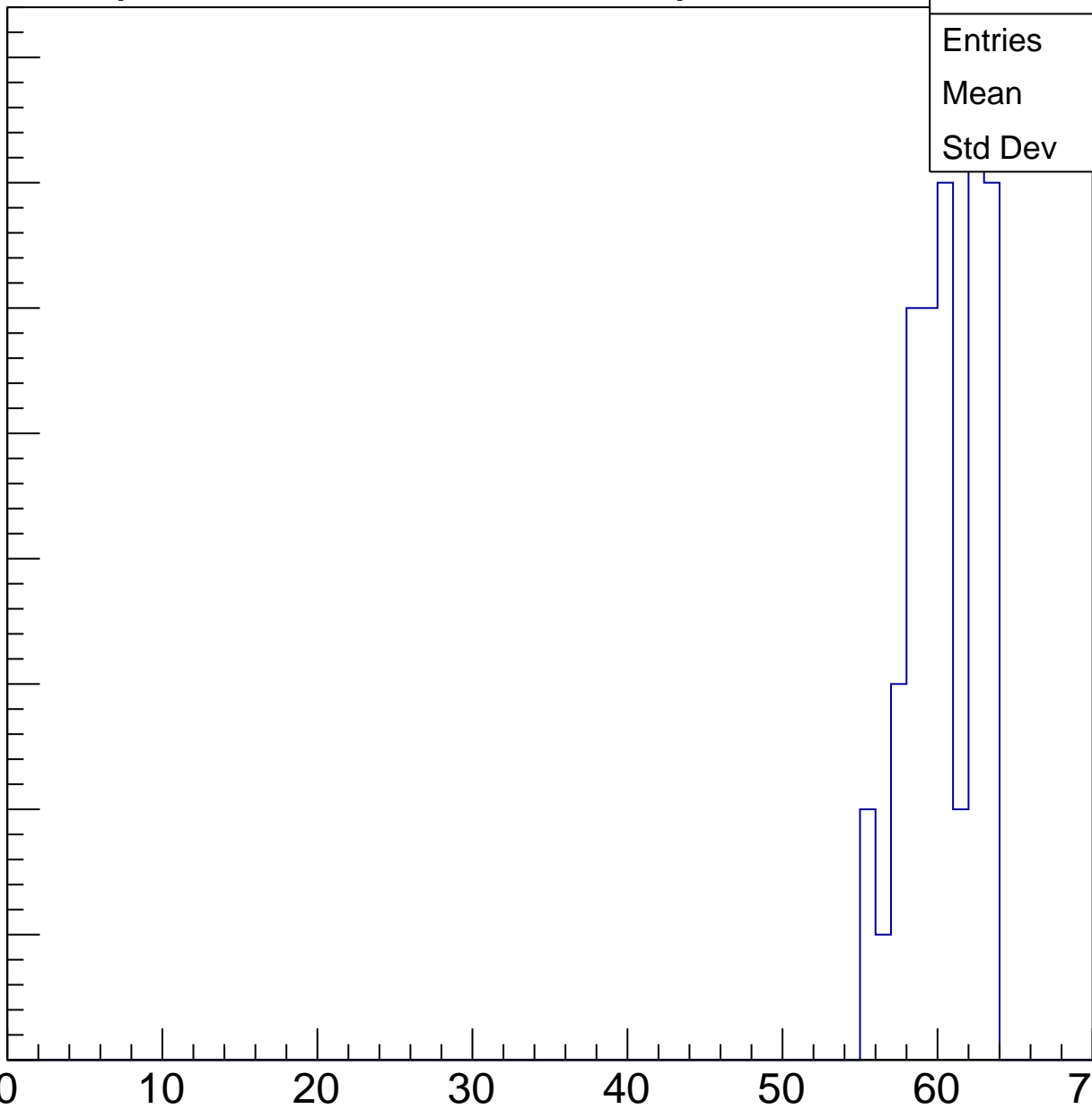
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.95
Std Dev	2.288

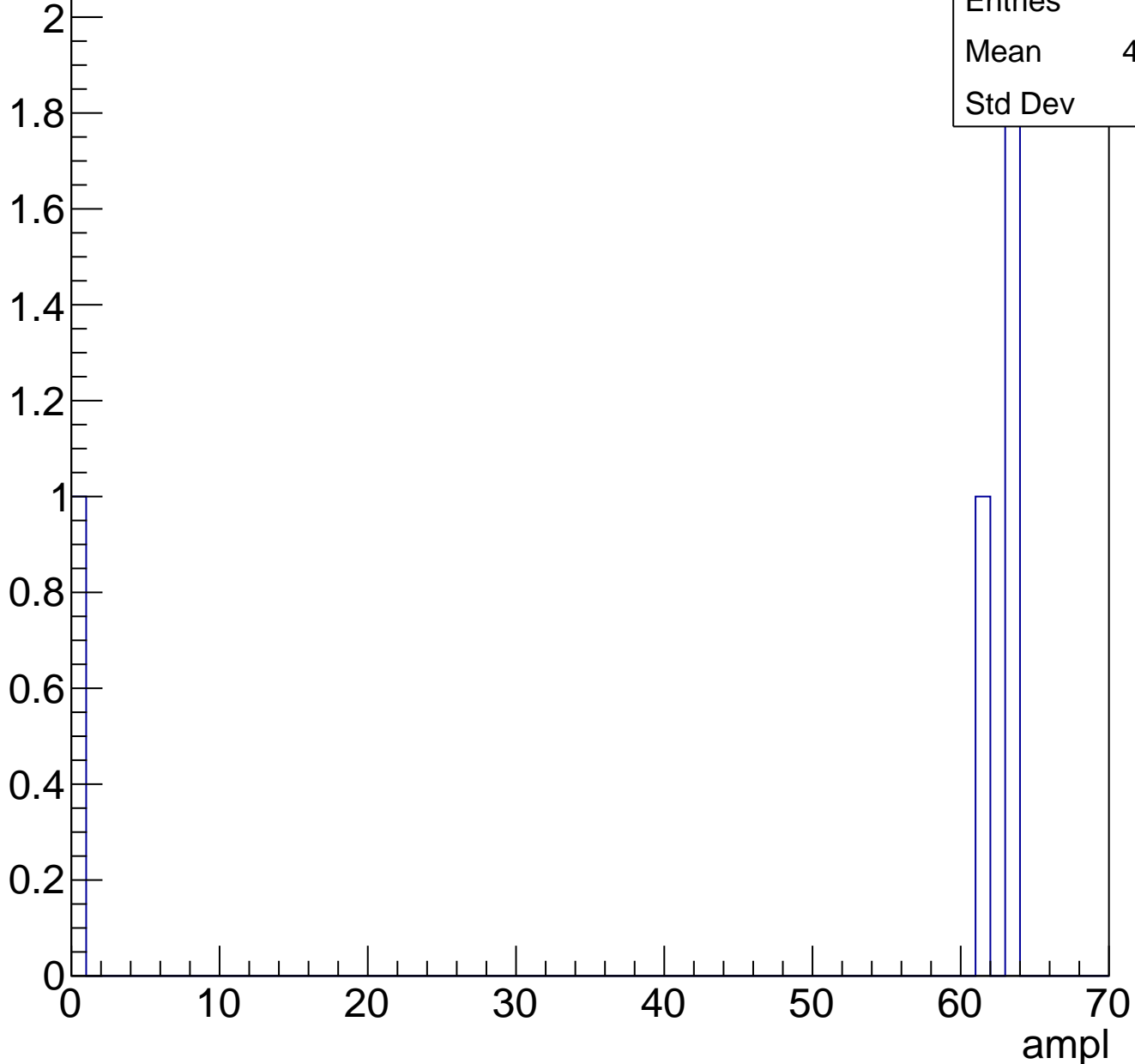
ampl



B1L003S, U19-ch61, adc6

calib_packv5_042523_0143.root, FC#13, port D2

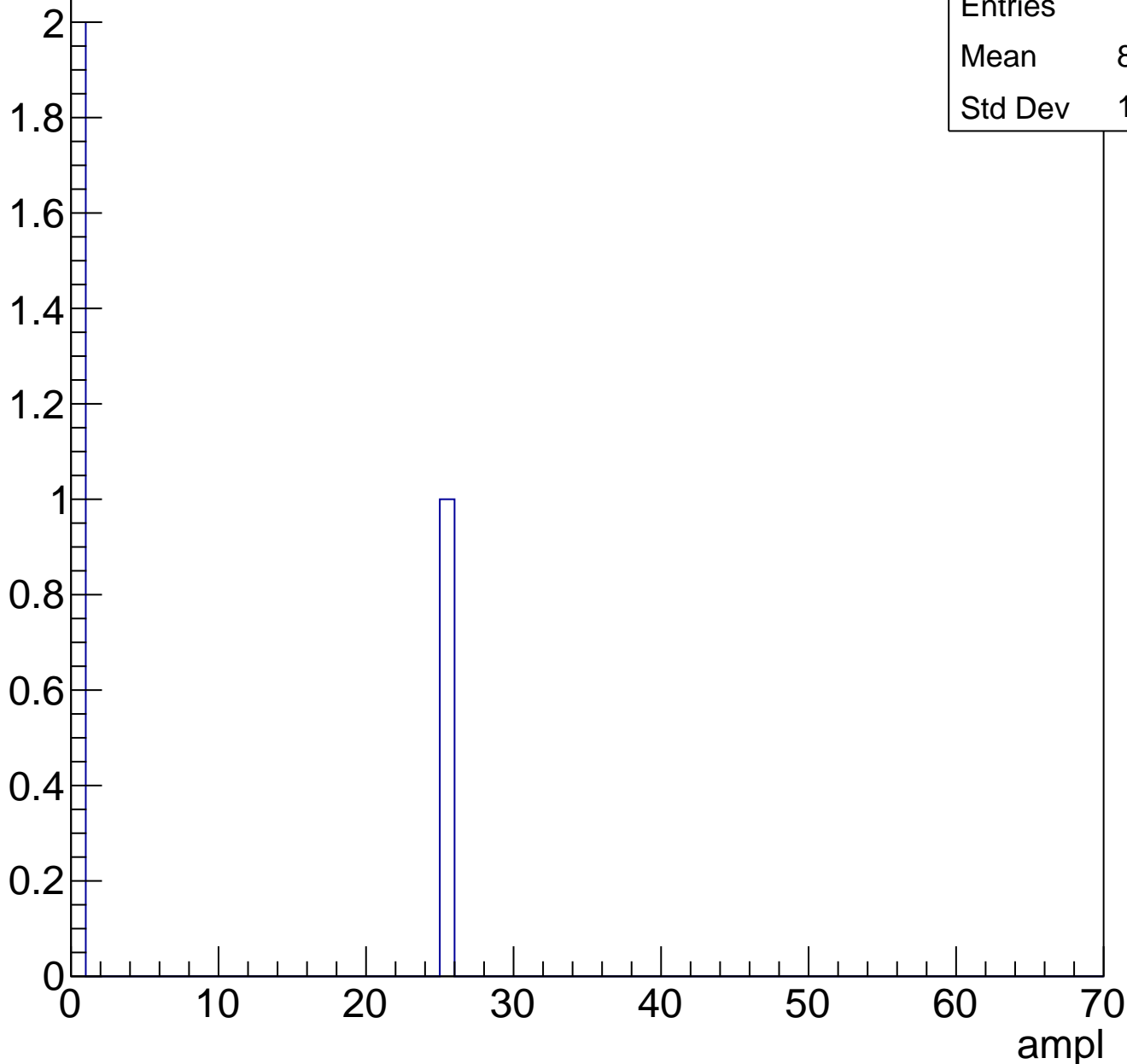
Entry



B1L003S, U19-ch61, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B1L003S, U19-ch62, adc0

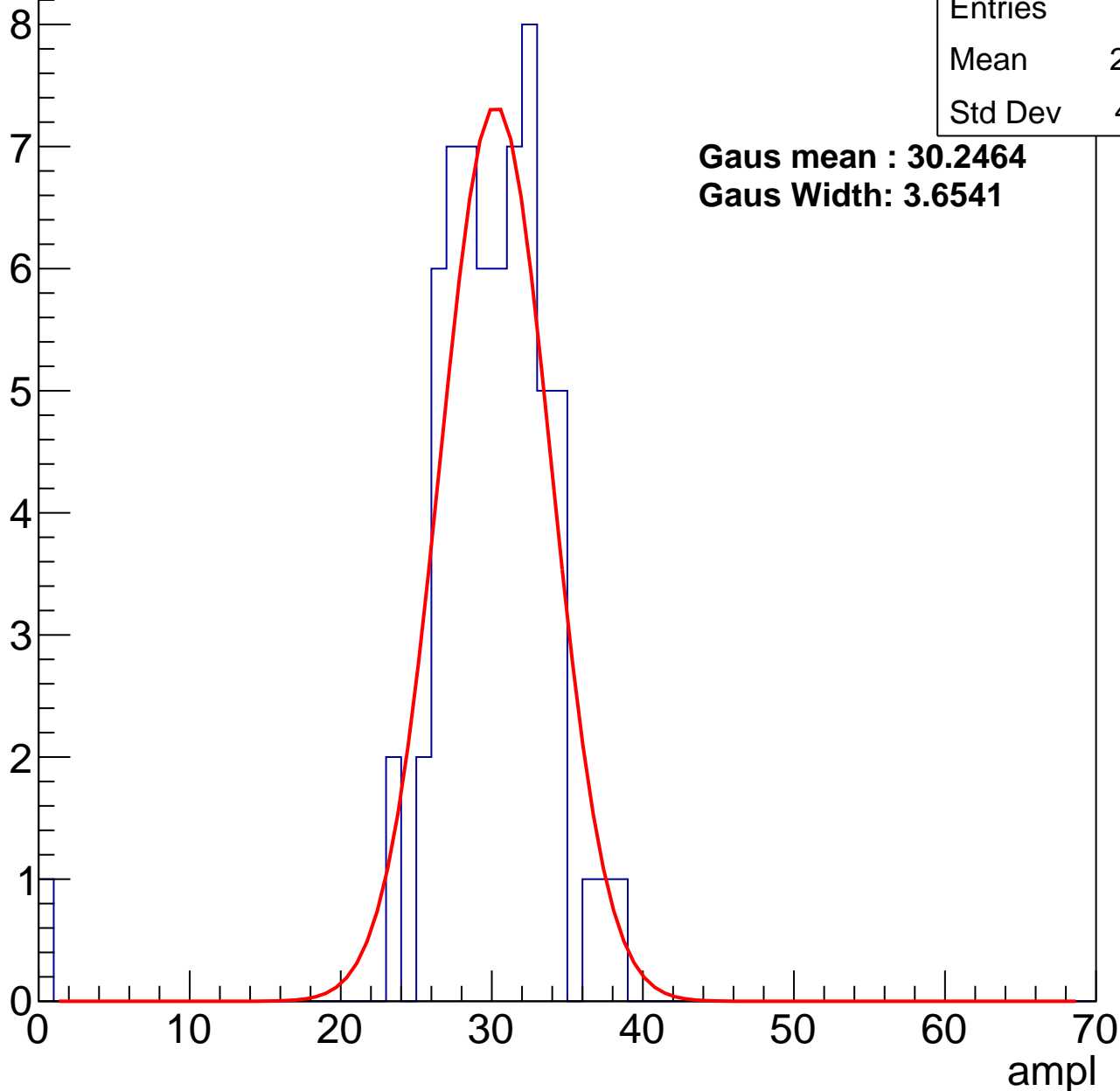
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.38
Std Dev	4.851

Gaus mean : 30.2464

Gaus Width: 3.6541



B1L003S, U19-ch62, adc1

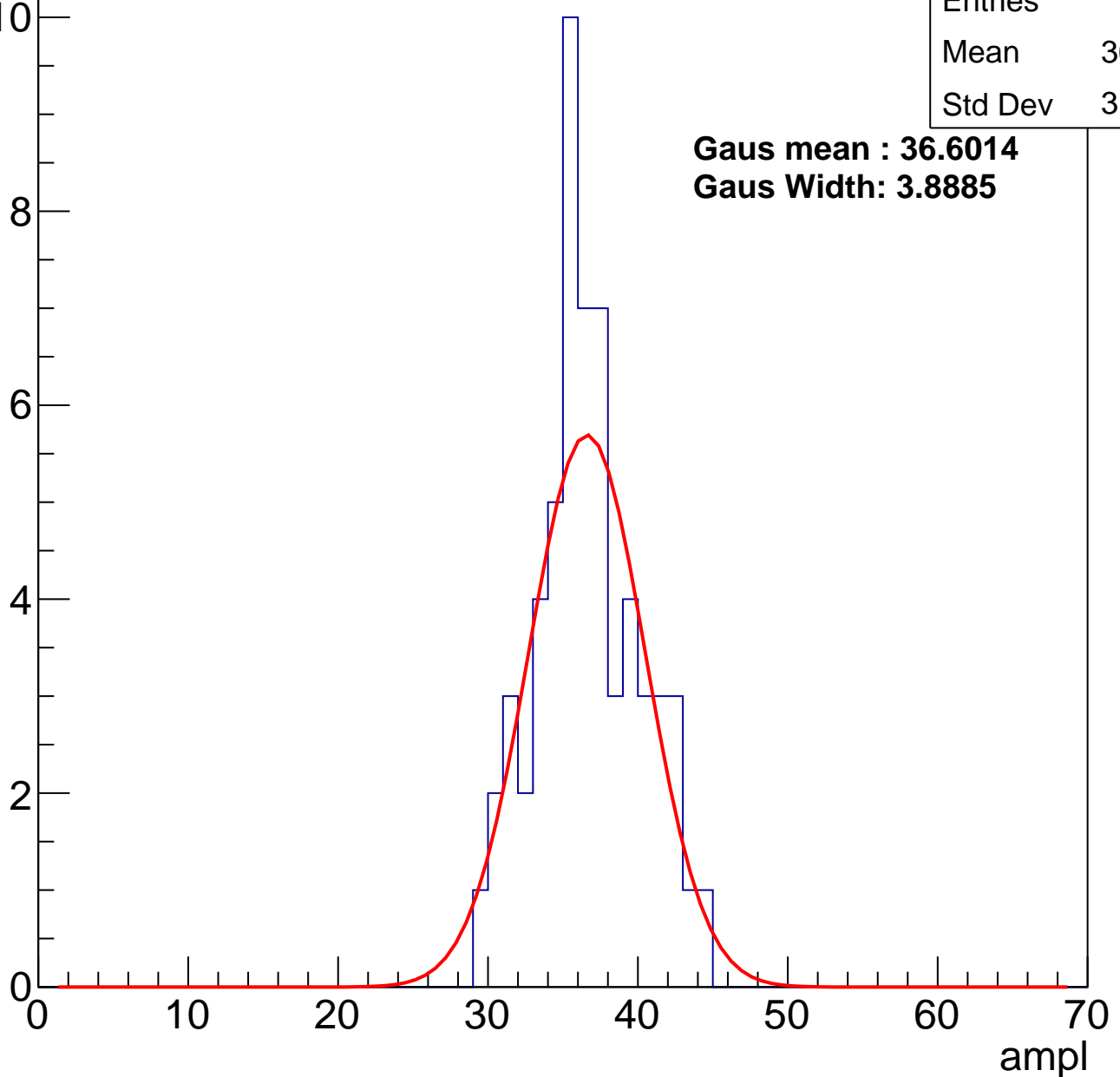
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.19
Std Dev	3.422

Gaus mean : 36.6014

Gaus Width: 3.8885



B1L003S, U19-ch62, adc2

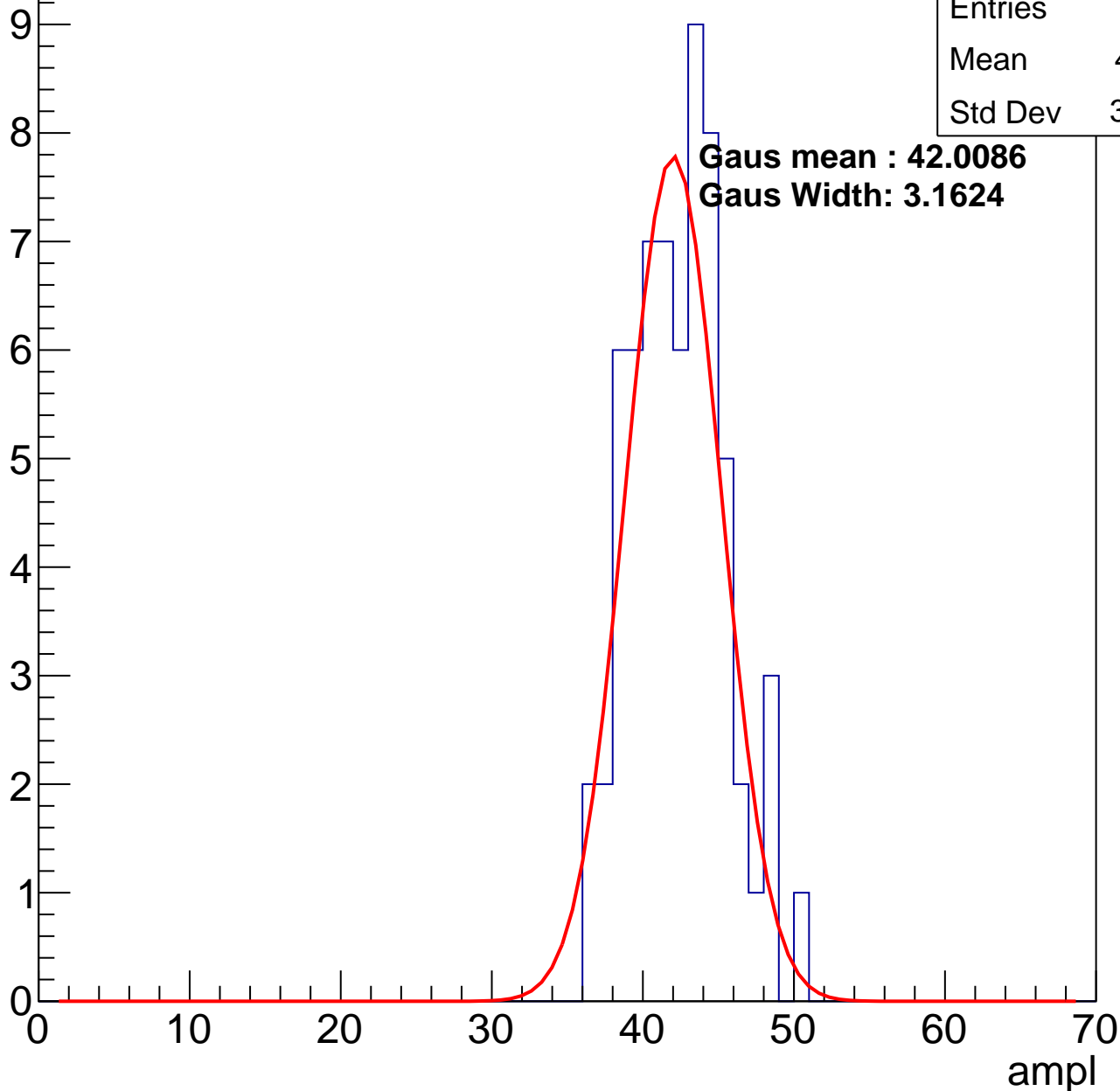
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	41.91
Std Dev	3.092

Gaus mean : 42.0086

Gaus Width: 3.1624

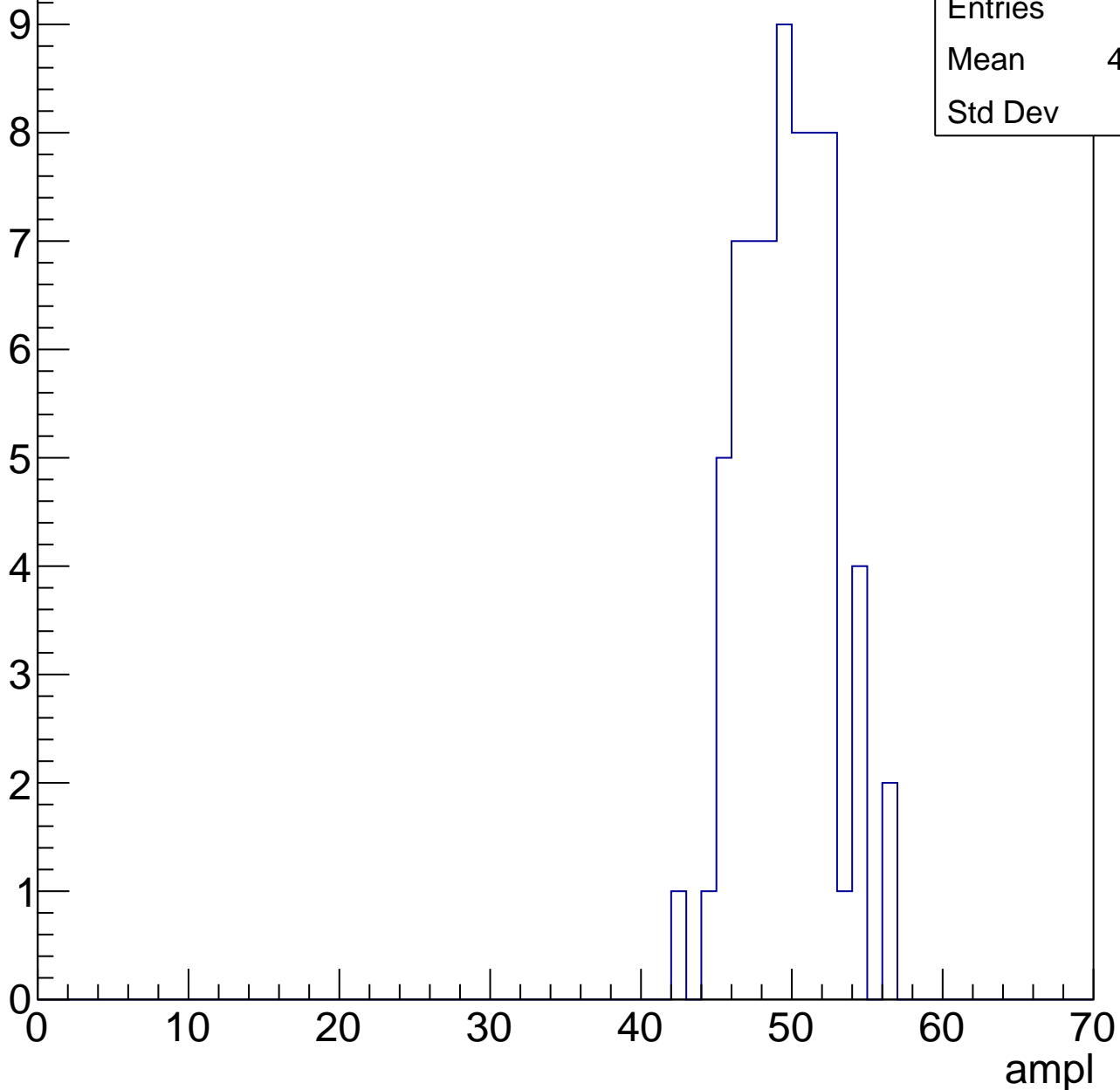


B1L003S, U19-ch62, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

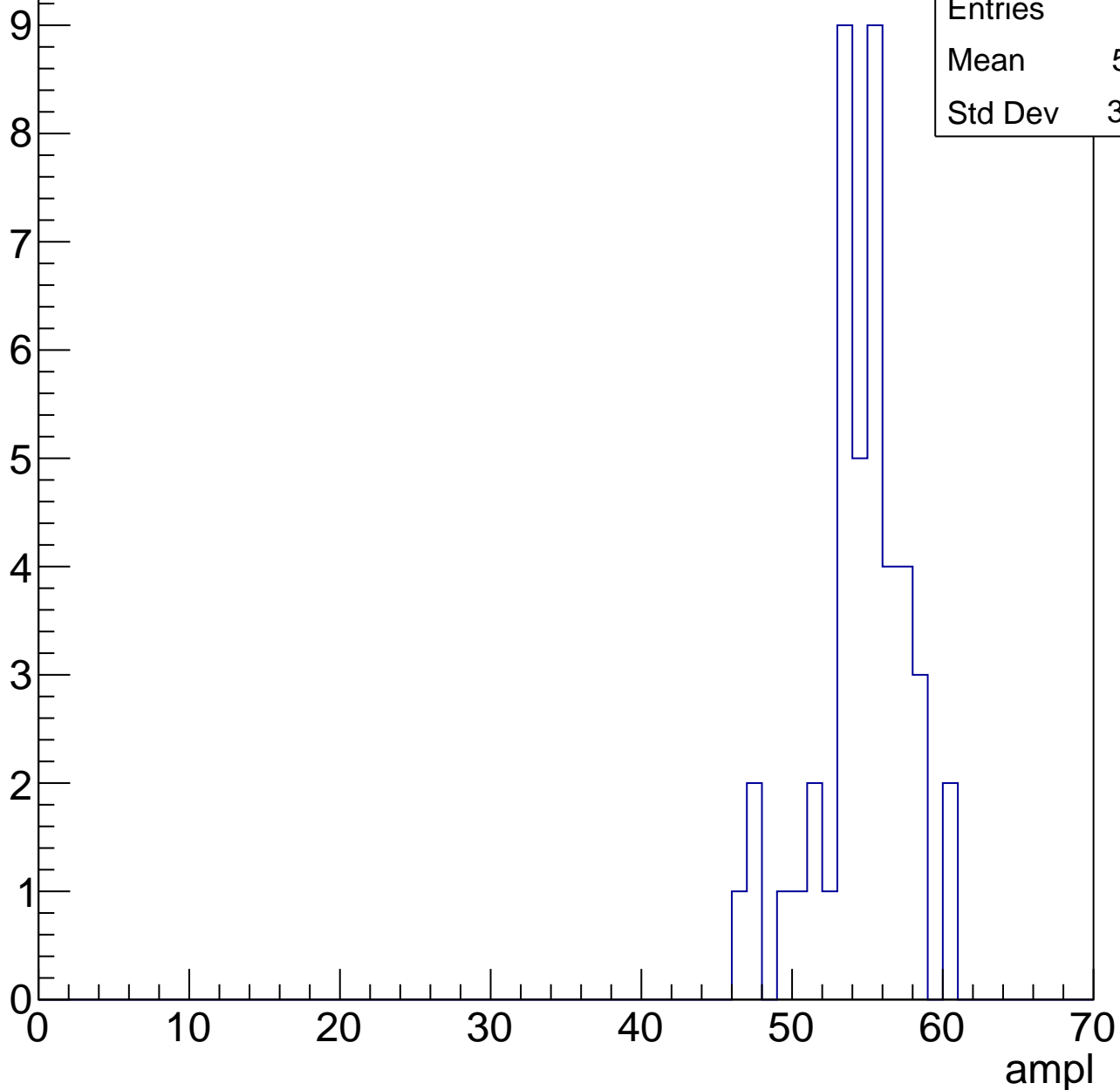
Entries	68
Mean	49.18
Std Dev	2.91



B1L003S, U19-ch62, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	44
Mean	54.11
Std Dev	3.069

B1L003S, U19-ch62, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

Entries 60

Mean 59.35

Std Dev 2.358

8

6

4

2

0

0

10

20

30

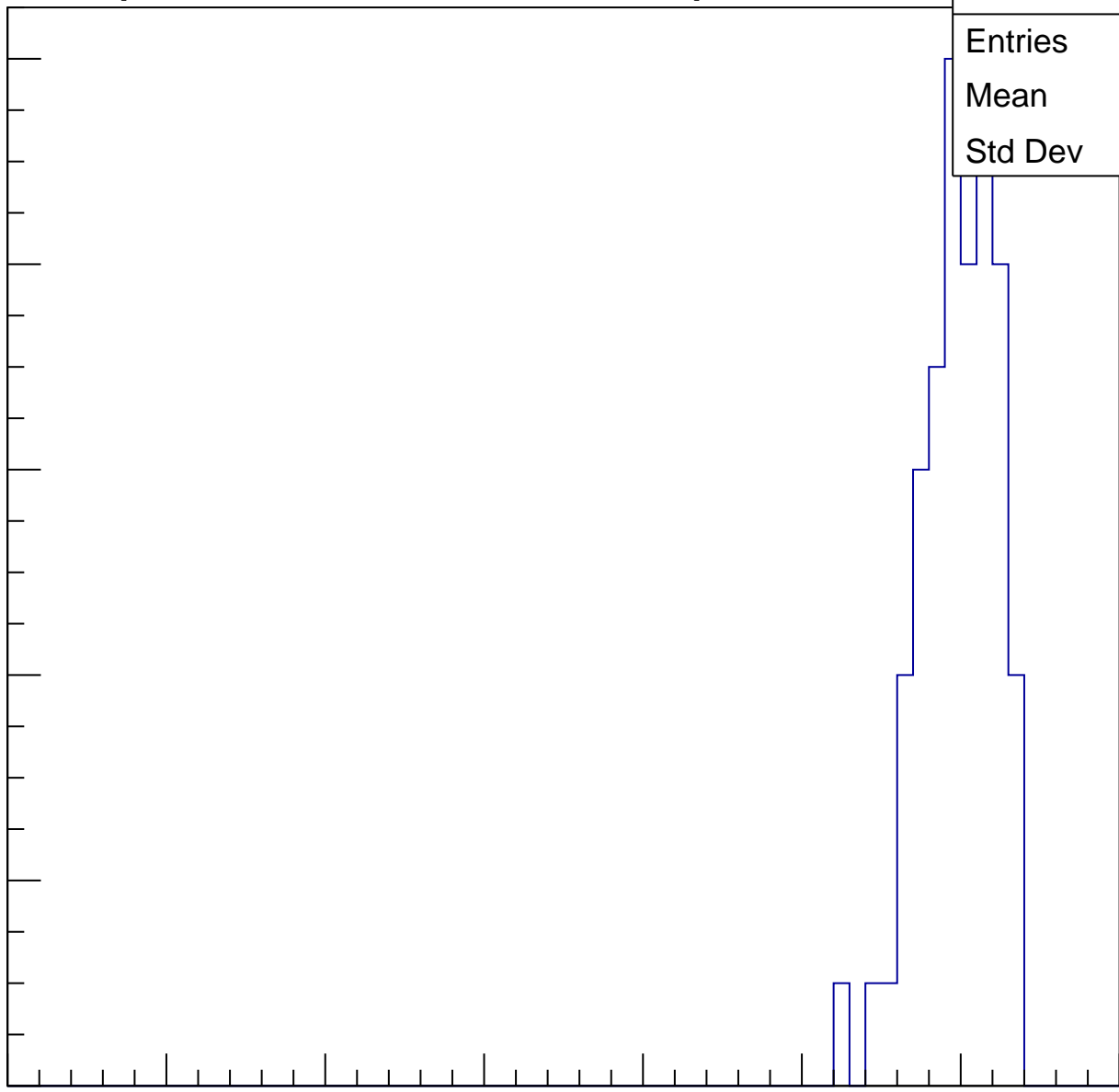
40

50

60

70

ampl

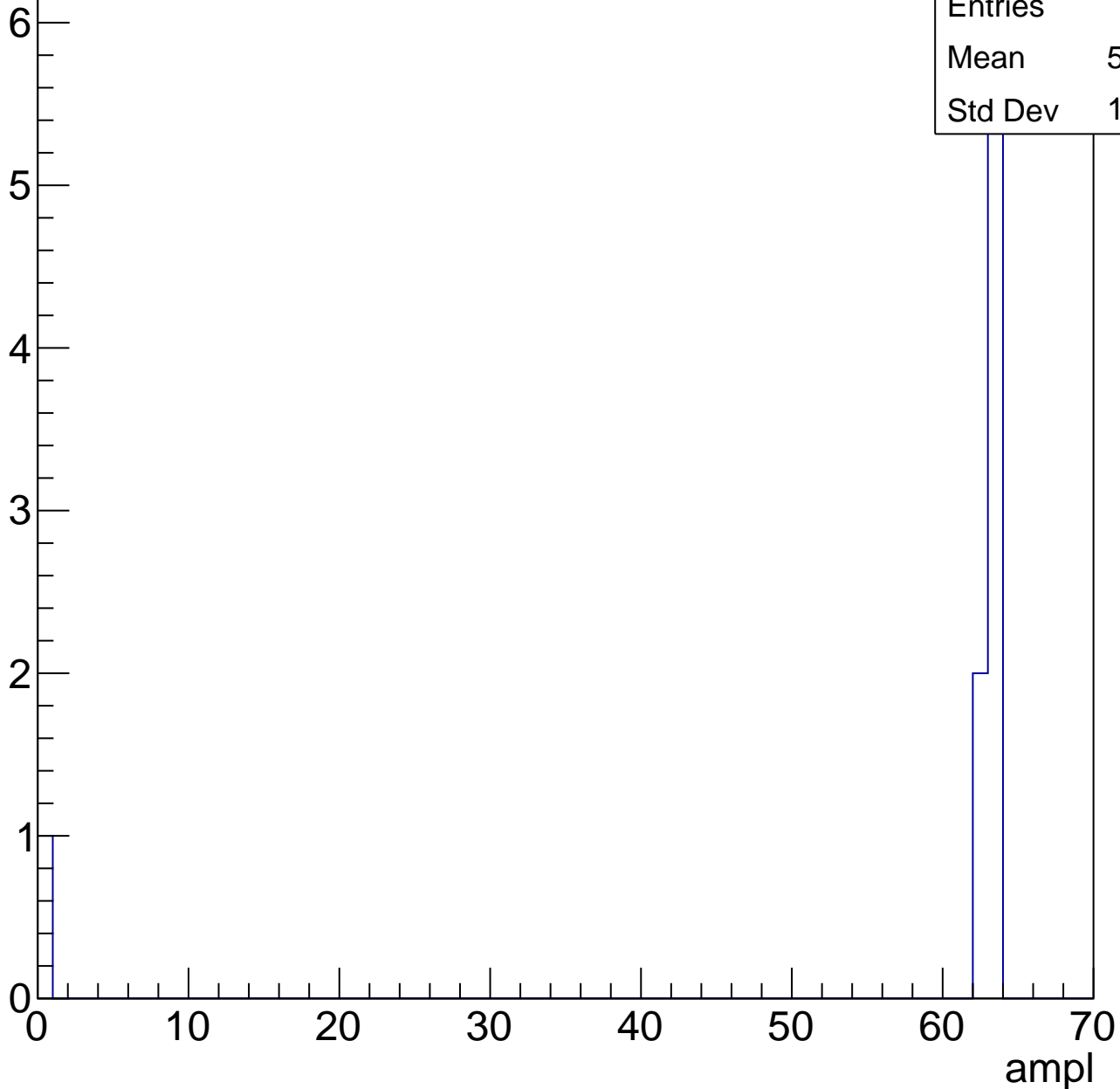


B1L003S, U19-ch62, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	55.78
Std Dev	19.72



B1L003S, U19-ch62, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch63, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	29.62
Std Dev	3.798

Gaus mean : 30.0118

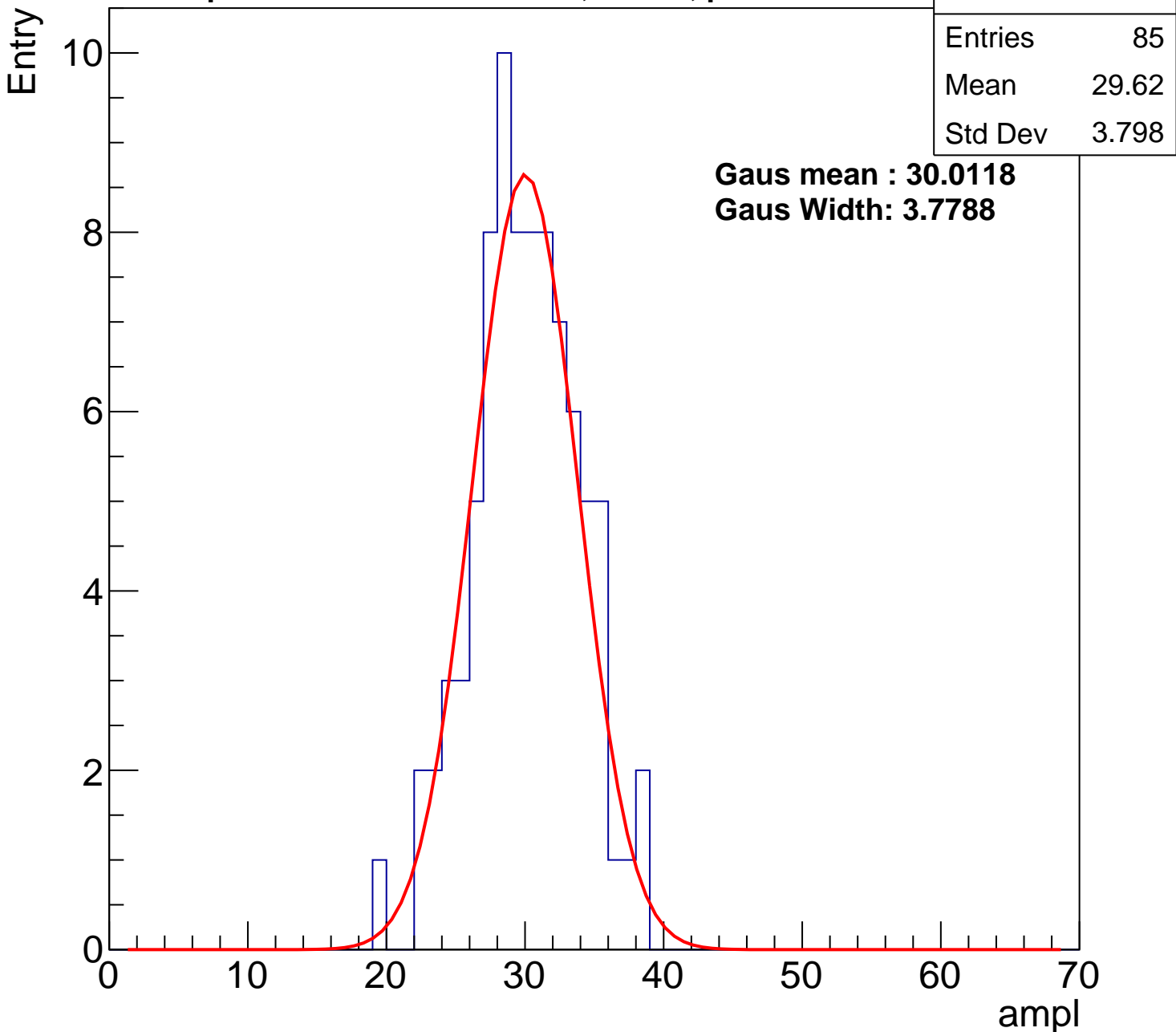
Gaus Width: 3.7788

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch63, adc1

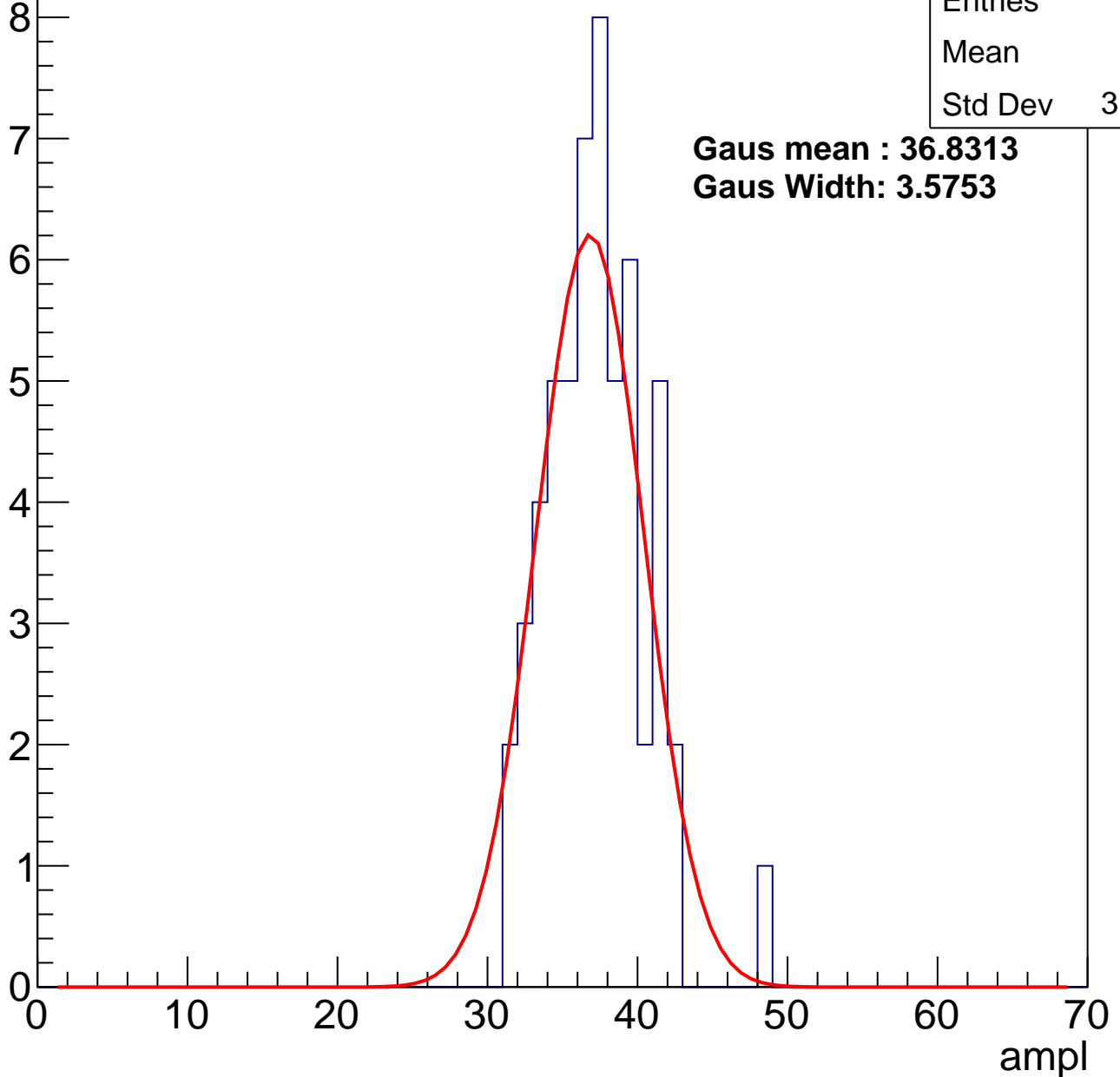
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	36.8
Std Dev	3.244

Gaus mean : 36.8313

Gaus Width: 3.5753



B1L003S, U19-ch63, adc2

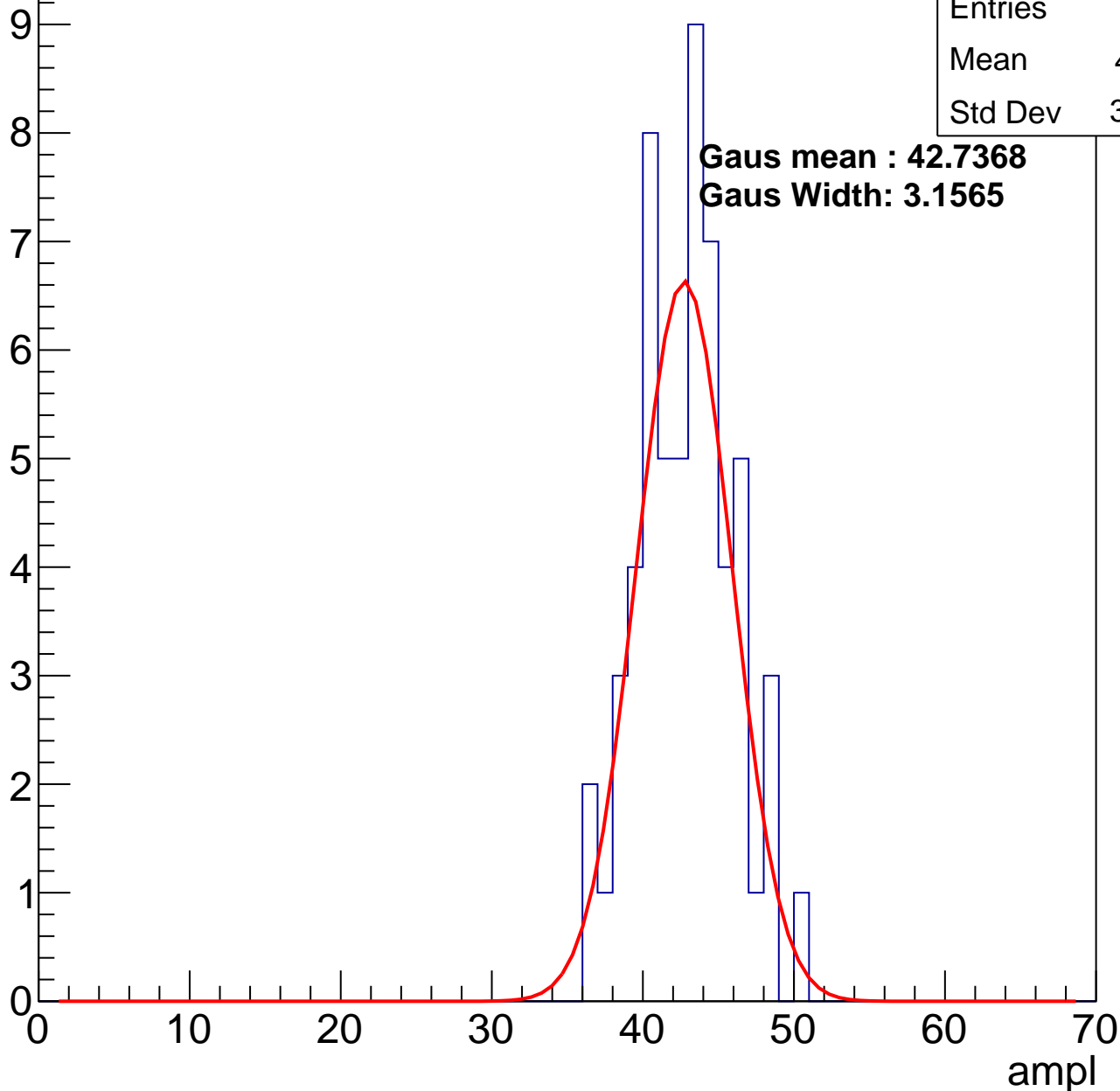
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.41
Std Dev	3.102

Gaus mean : 42.7368

Gaus Width: 3.1565

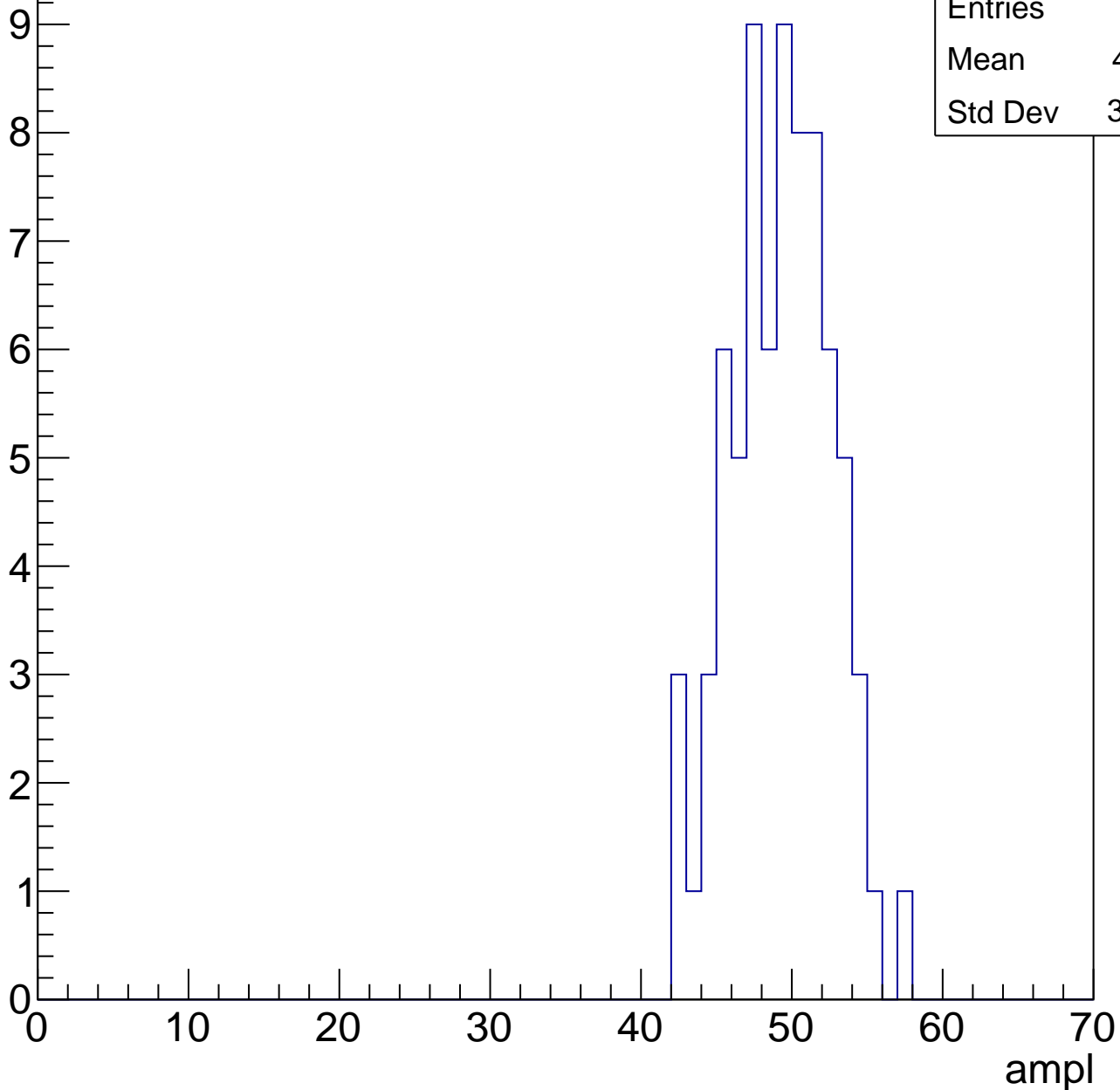


B1L003S, U19-ch63, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	48.81
Std Dev	3.262

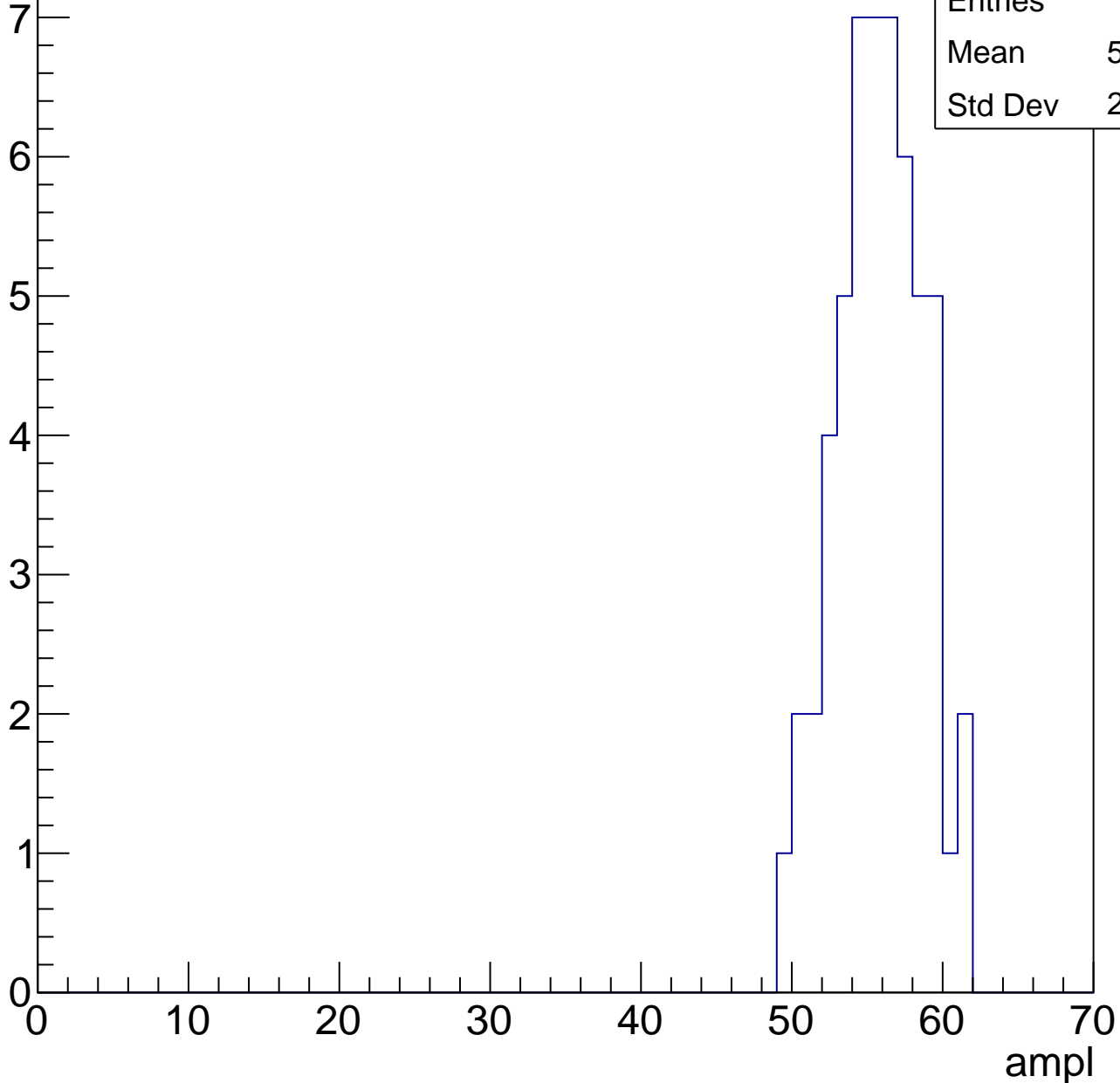


B1L003S, U19-ch63, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	55.33
Std Dev	2.815



B1L003S, U19-ch63, adc5

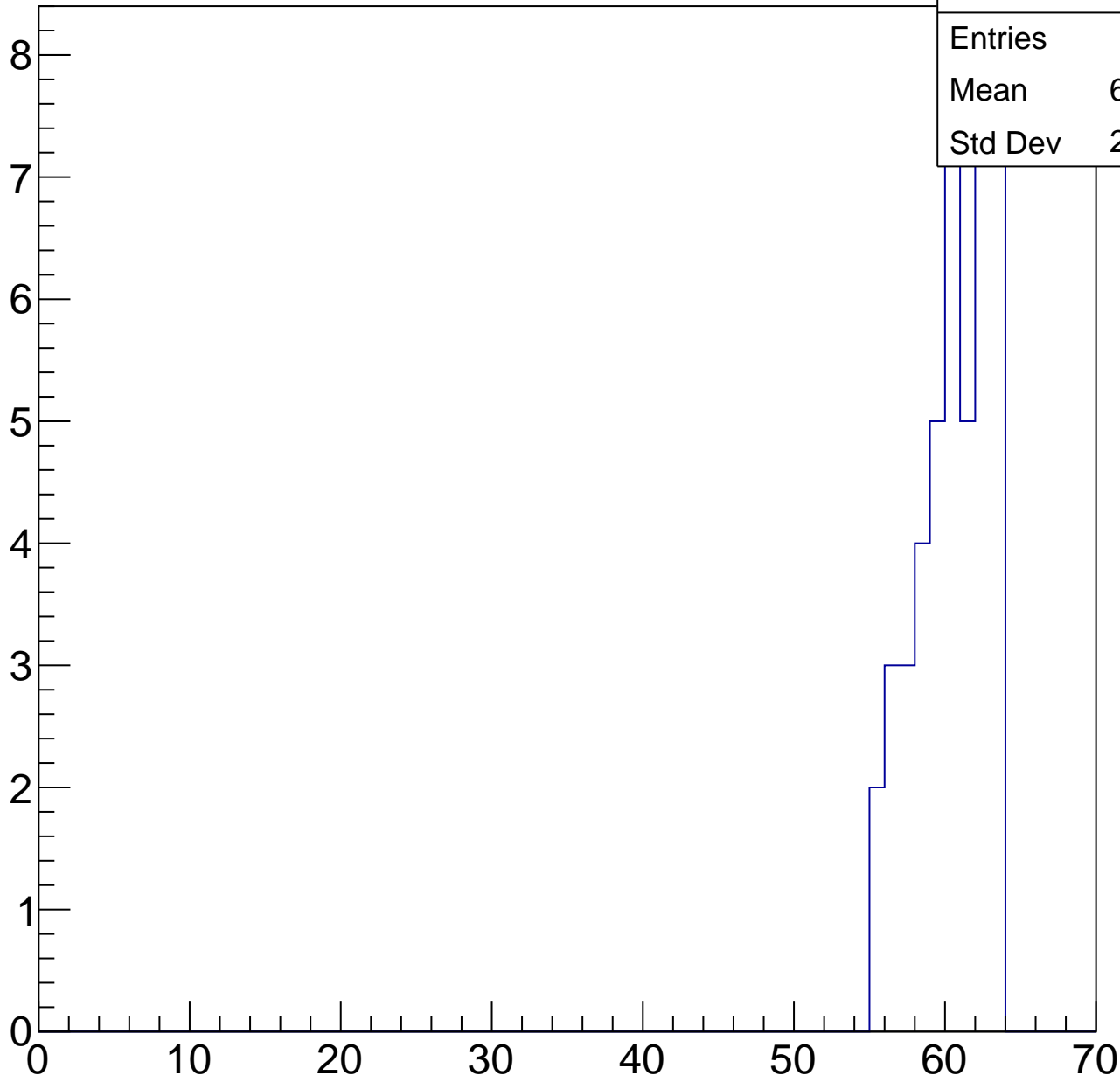
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	60.02
Std Dev	2.354

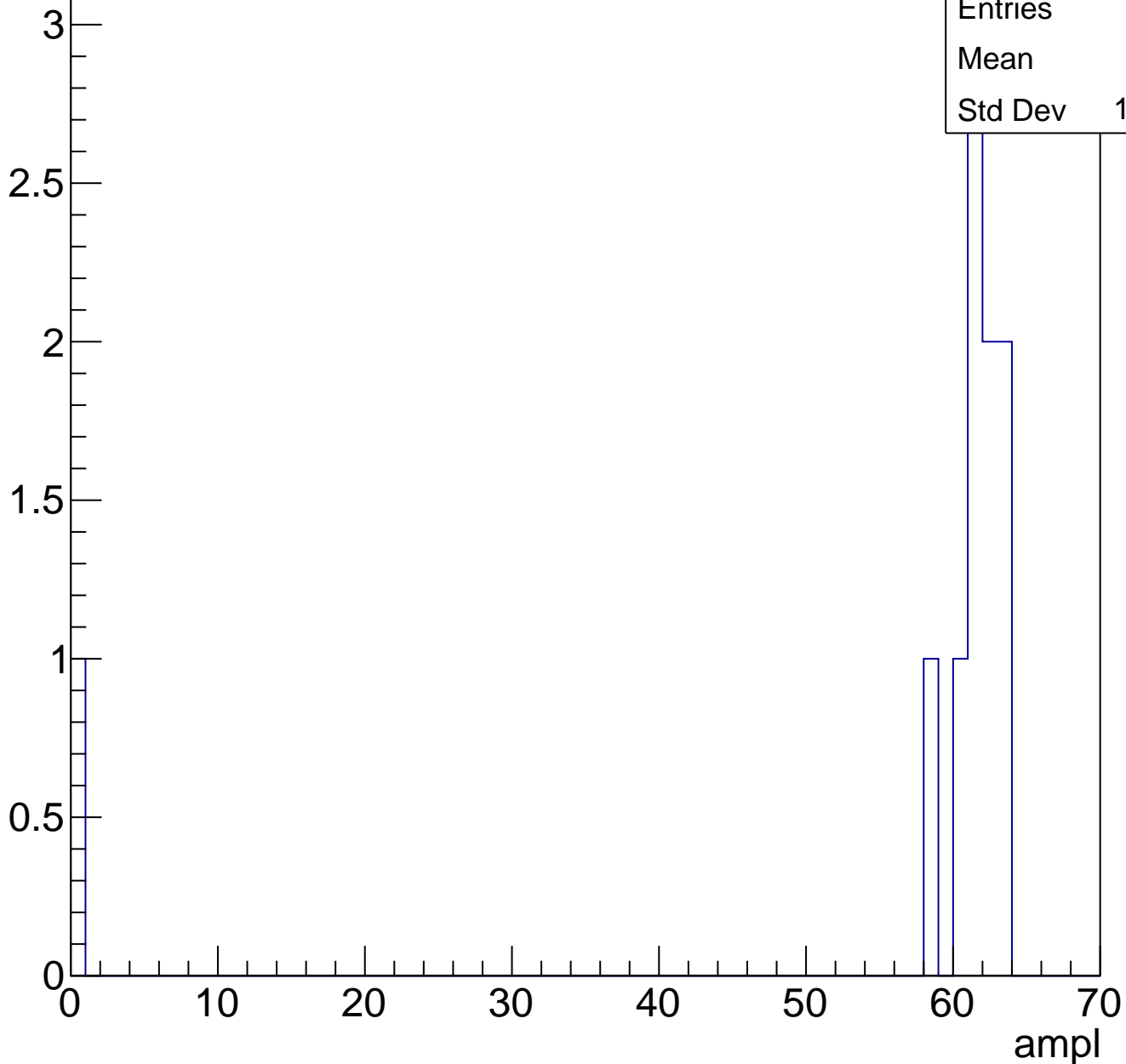
ampl



B1L003S, U19-ch63, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch63, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch64, adc0

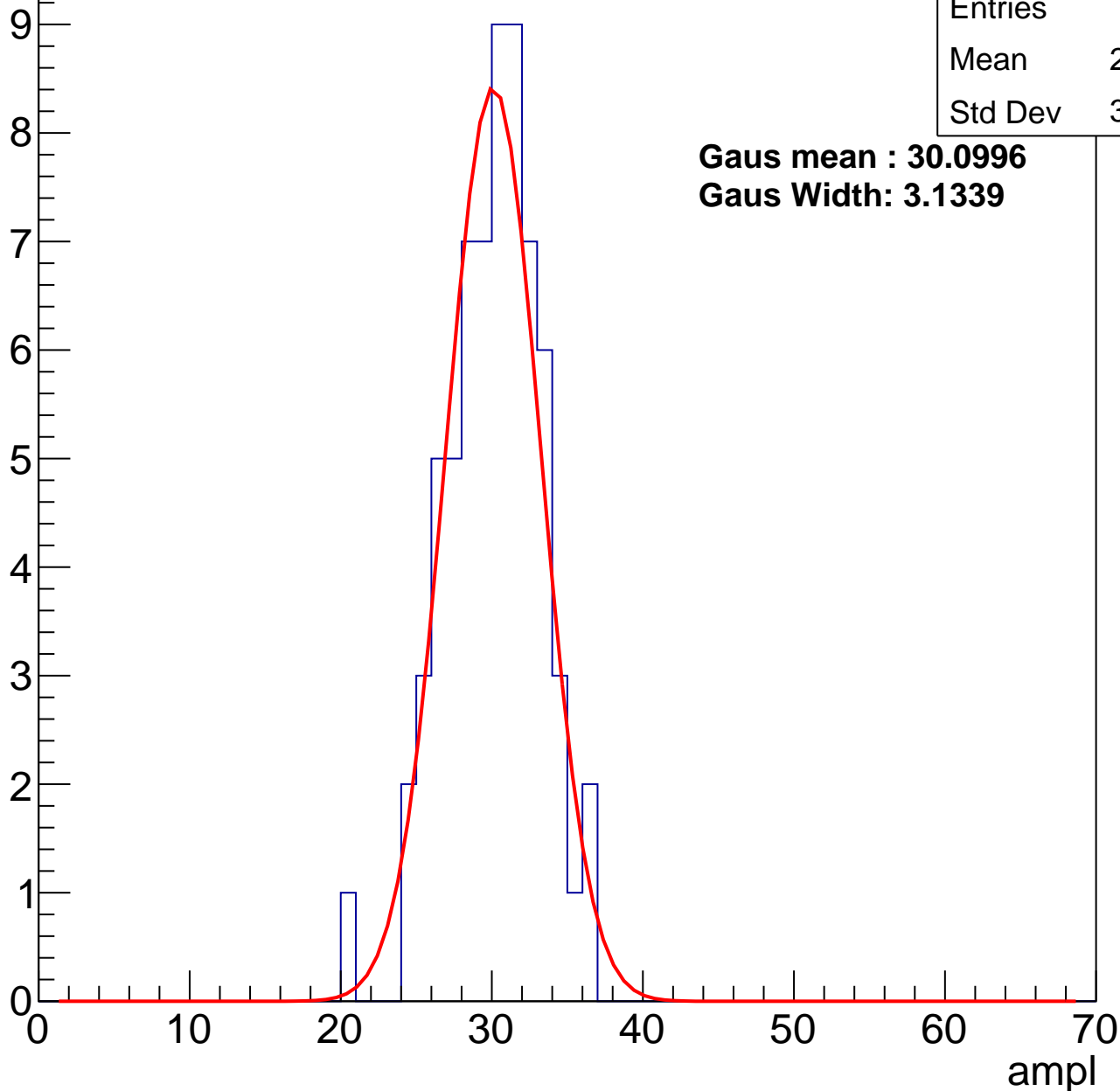
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	29.66
Std Dev	3.079

Gaus mean : 30.0996

Gaus Width: 3.1339



B1L003S, U19-ch64, adc1

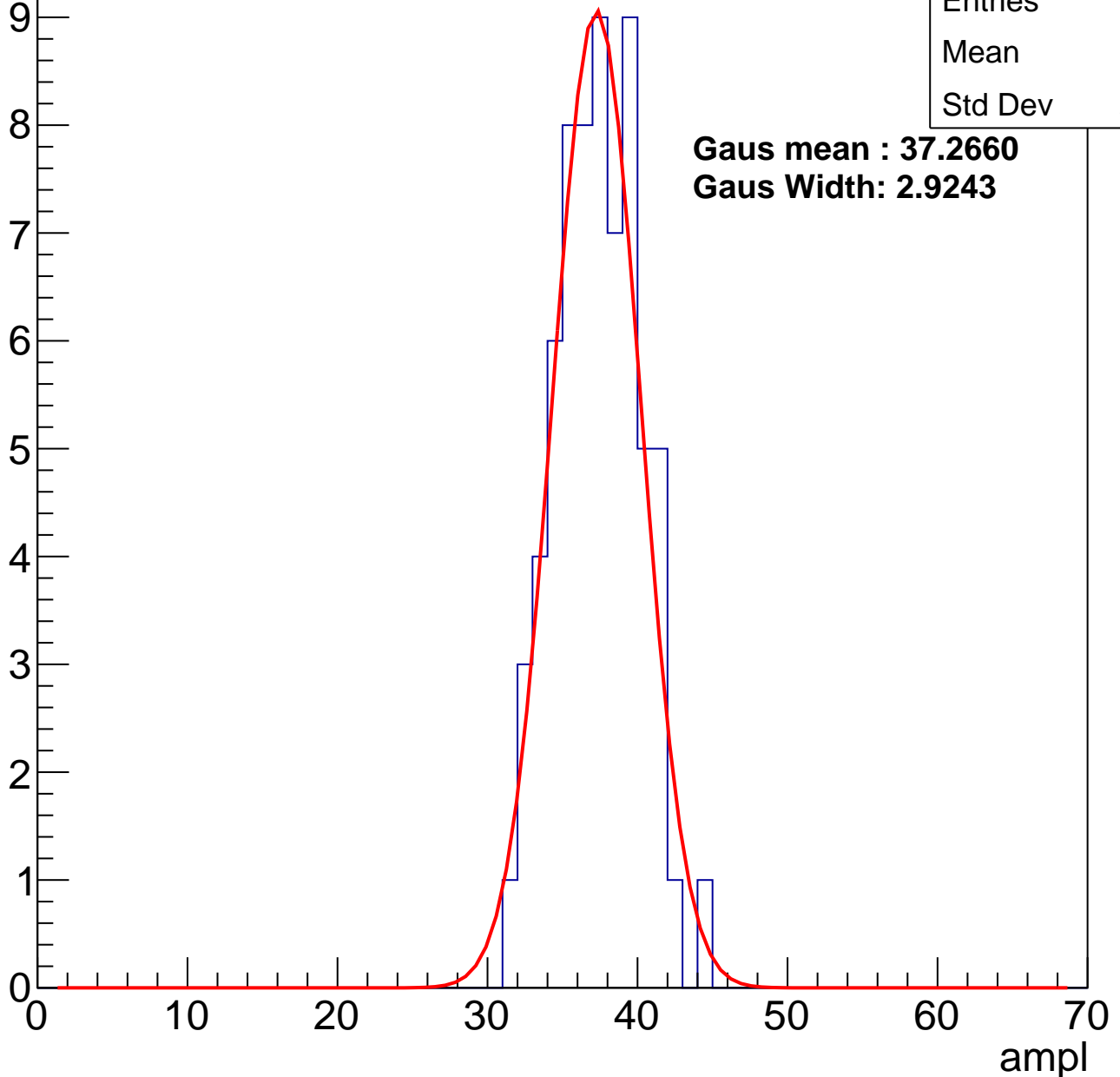
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.9
Std Dev	2.76

Gaus mean : 37.2660

Gaus Width: 2.9243



B1L003S, U19-ch64, adc2

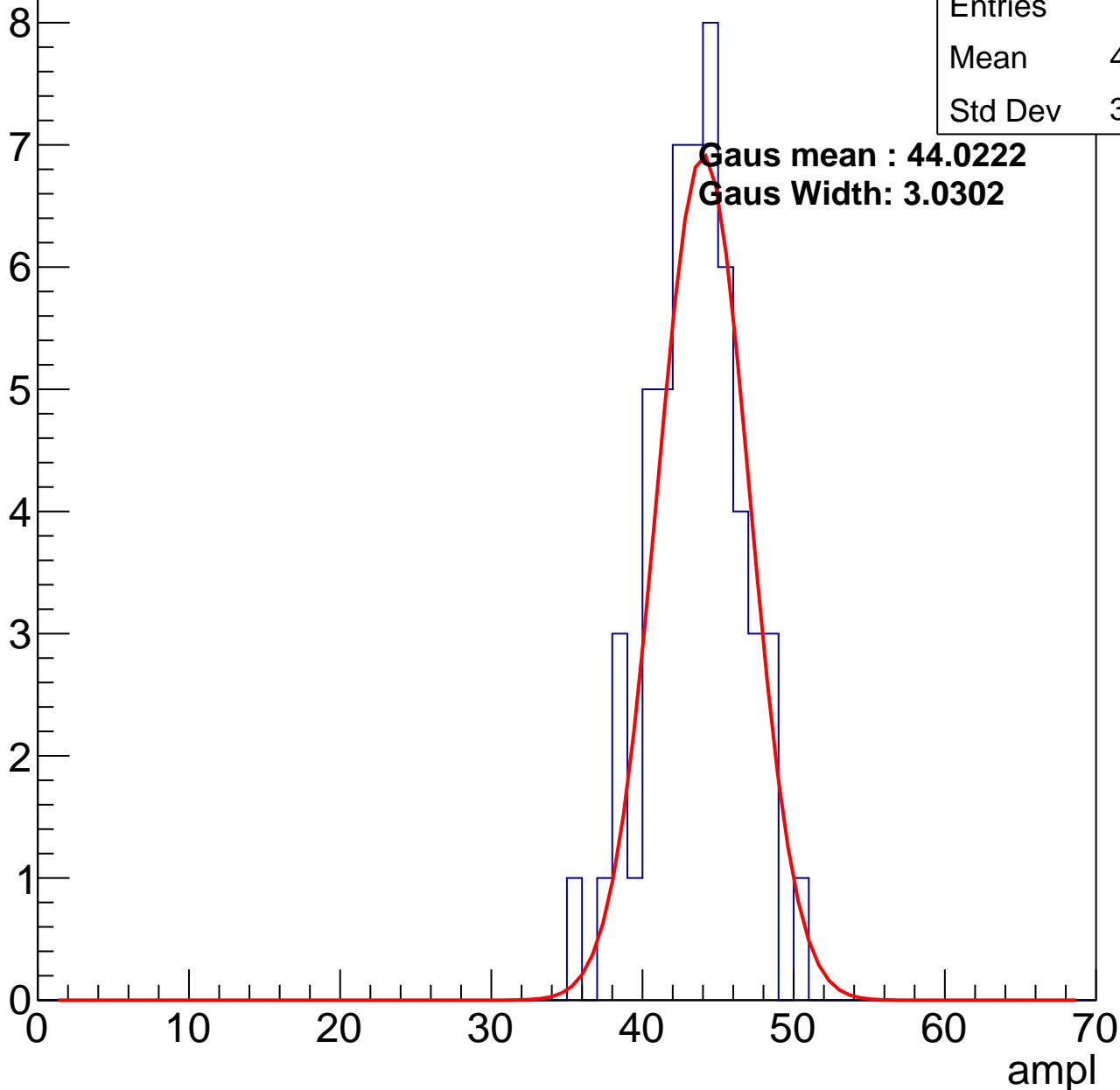
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	43.02
Std Dev	3.024

Gaus mean : 44.0222

Gaus Width: 3.0302

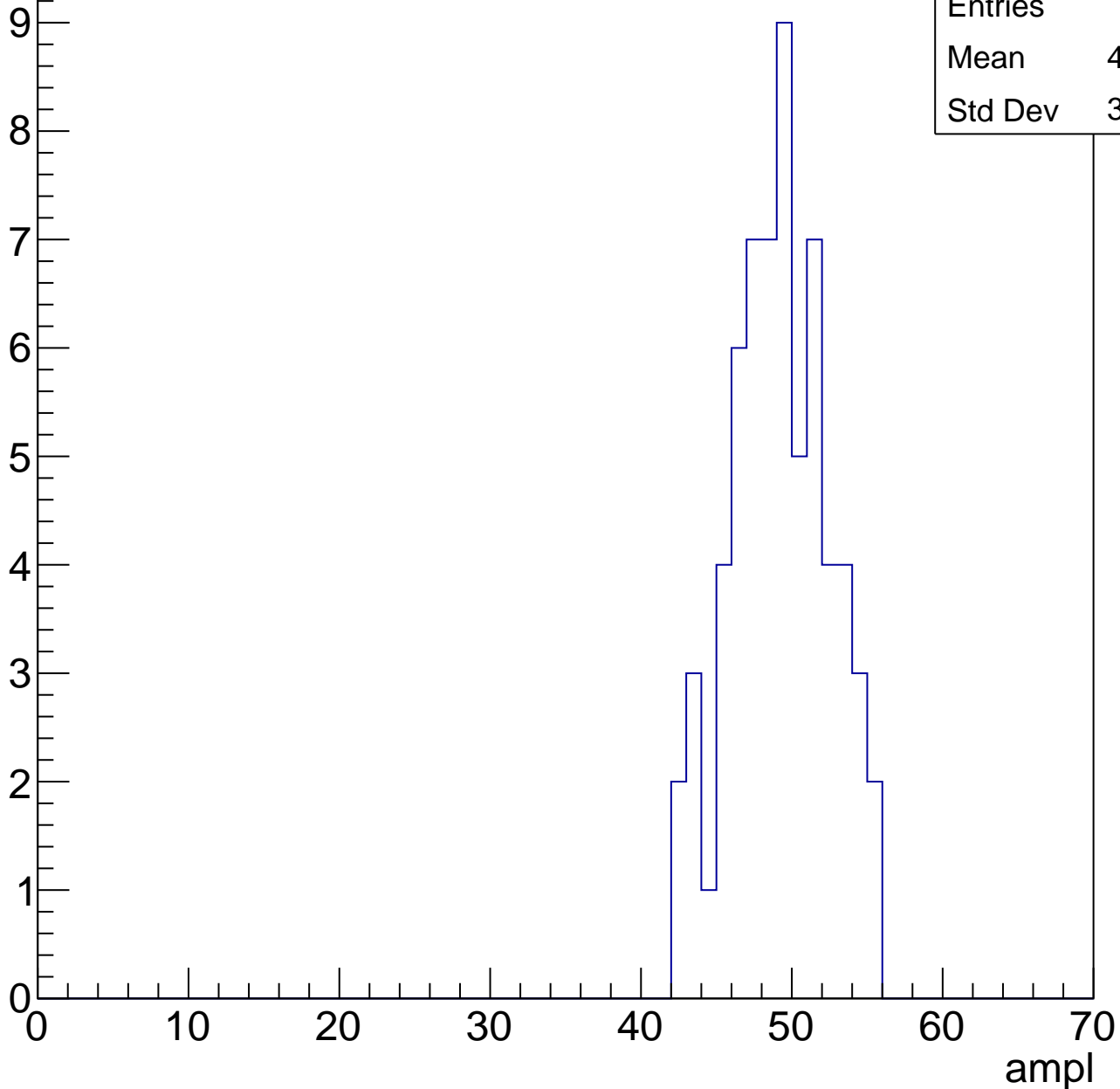


B1L003S, U19-ch64, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

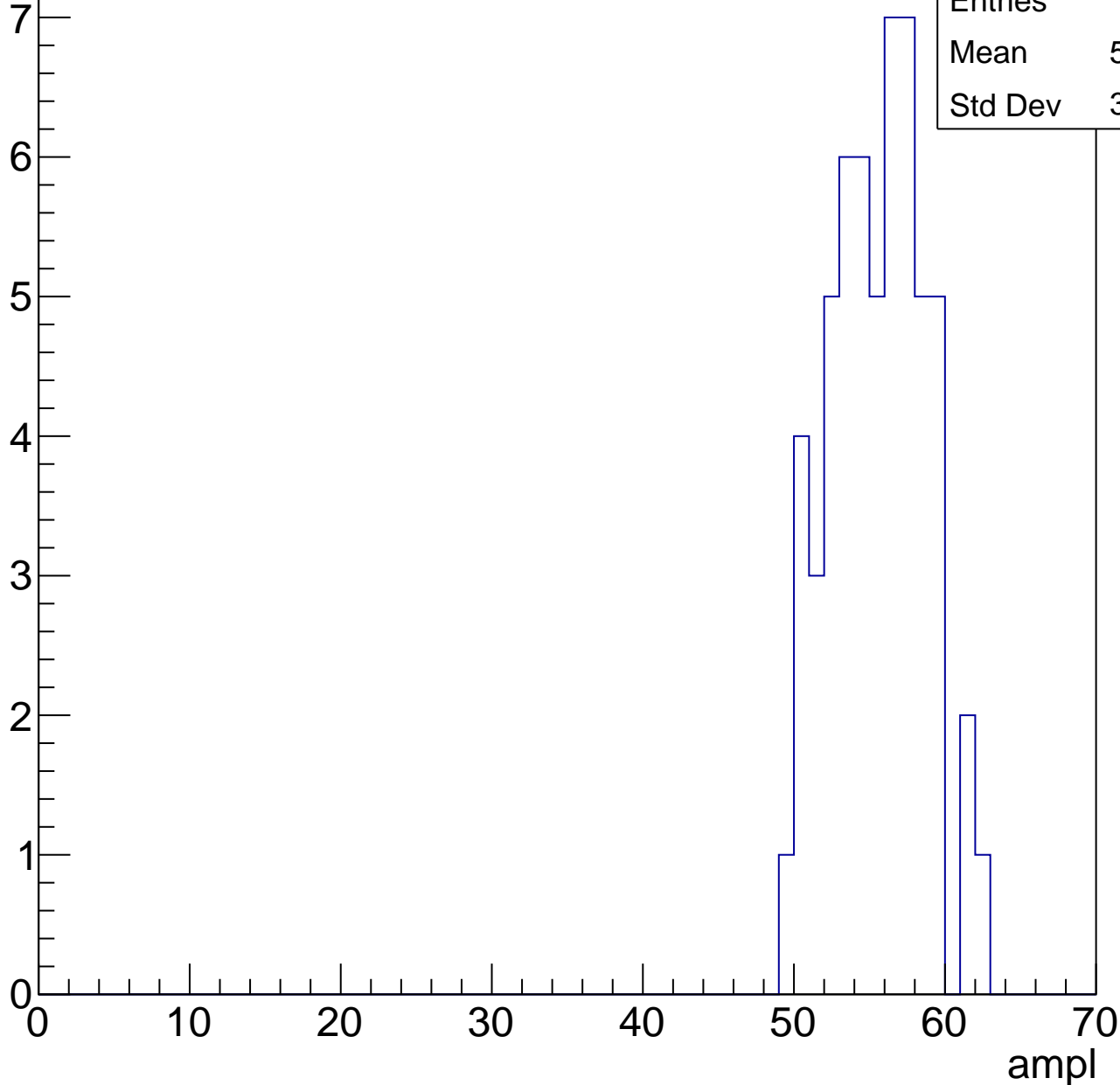
Entries	64
Mean	48.72
Std Dev	3.209



B1L003S, U19-ch64, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch64, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

8

6

4

2

0

0

10

20

30

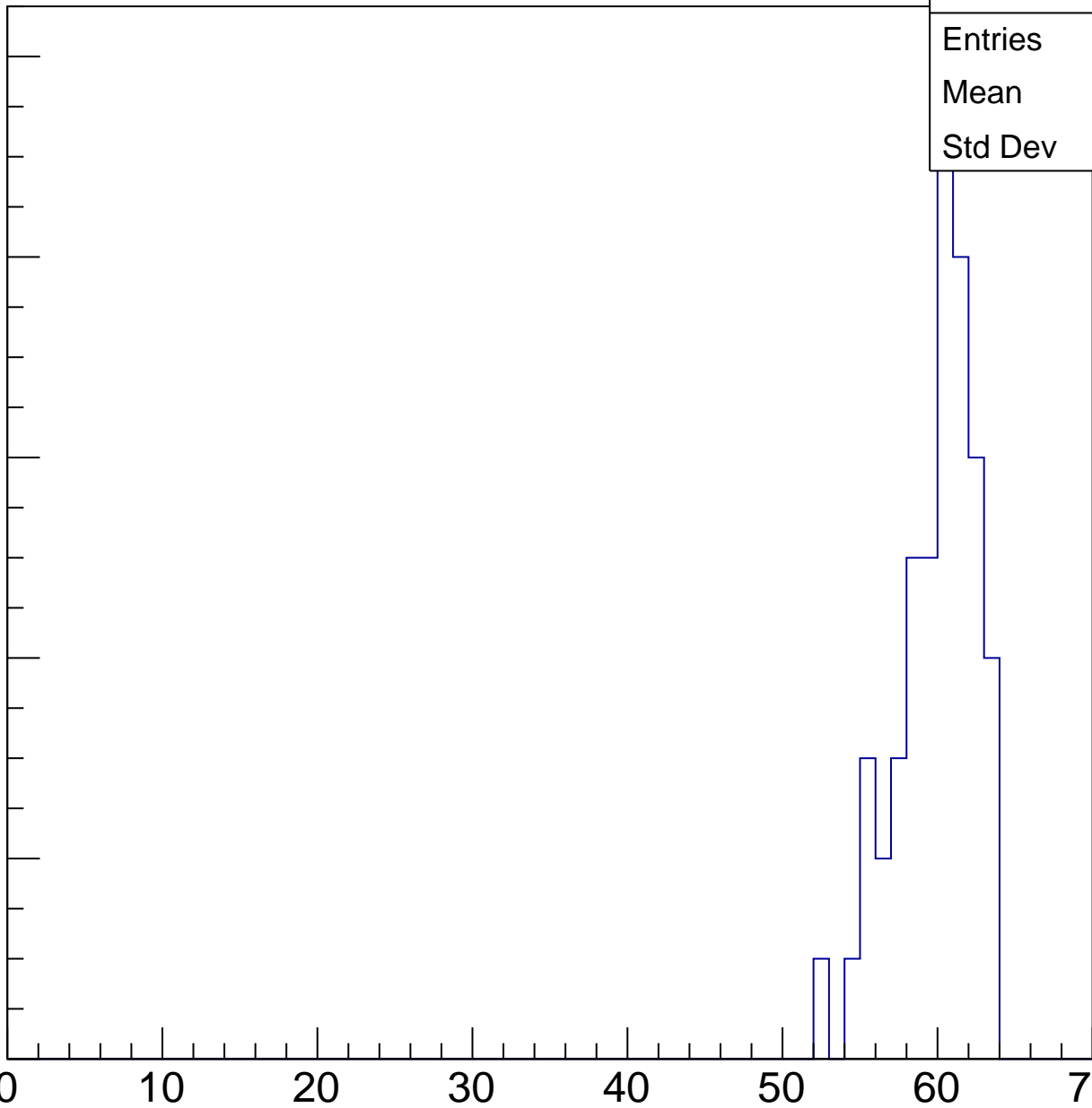
40

50

60

ampl

Entries	48
Mean	59.4
Std Dev	2.539

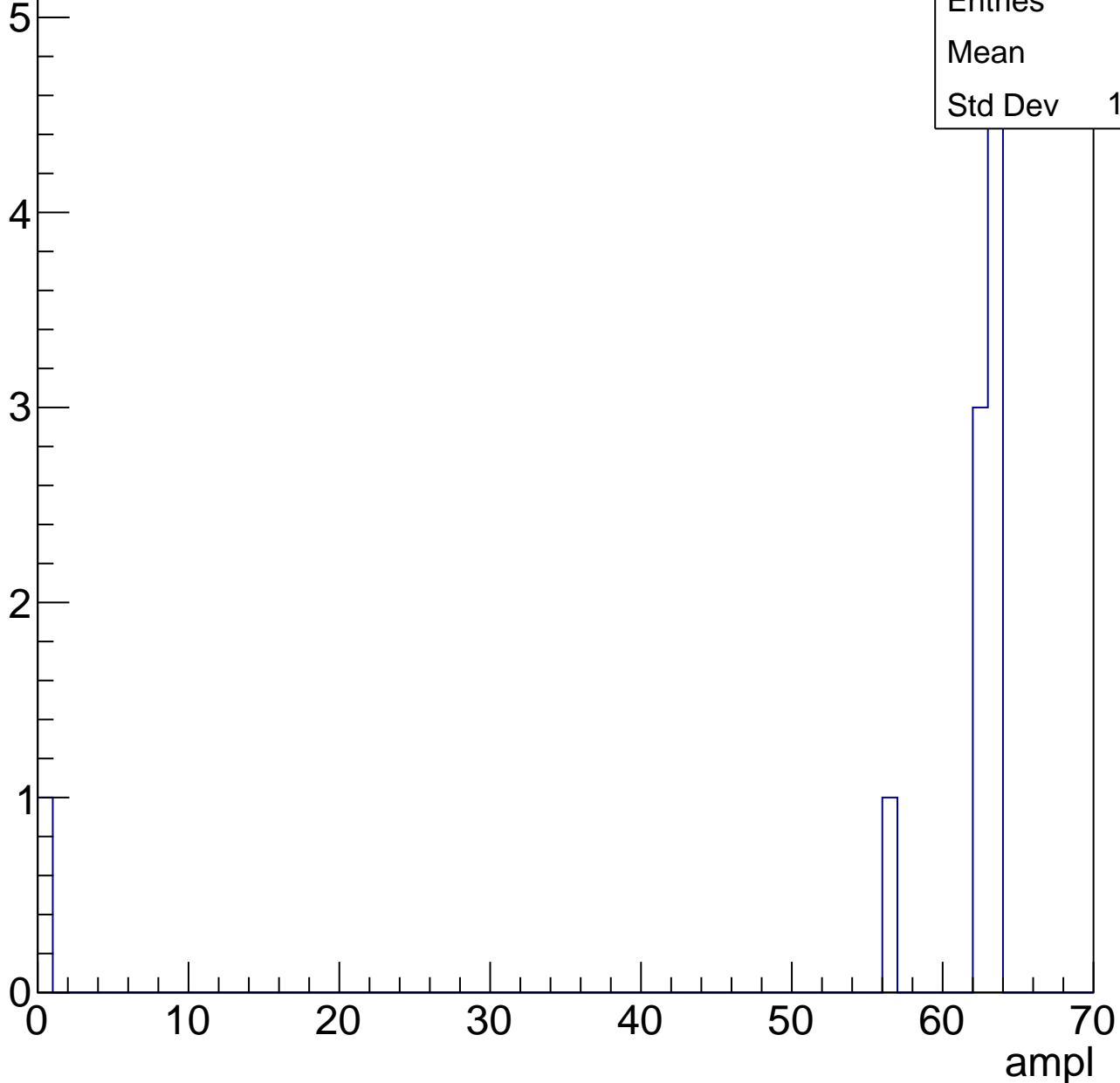


B1L003S, U19-ch64, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	55.7
Std Dev	18.68



B1L003S, U19-ch64, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L003S, U19-ch65, adc0

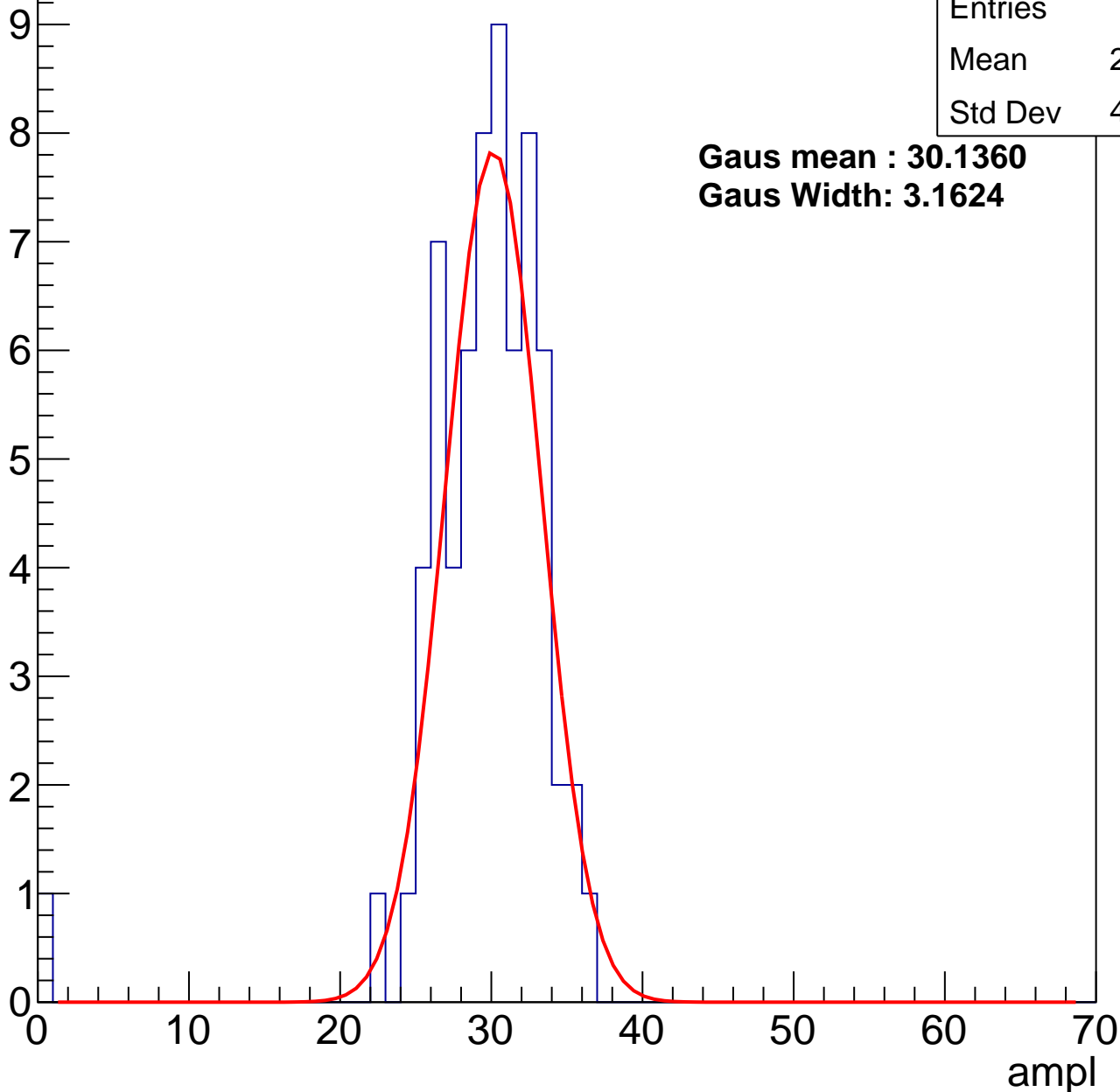
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.09
Std Dev	4.664

Gaus mean : 30.1360

Gaus Width: 3.1624

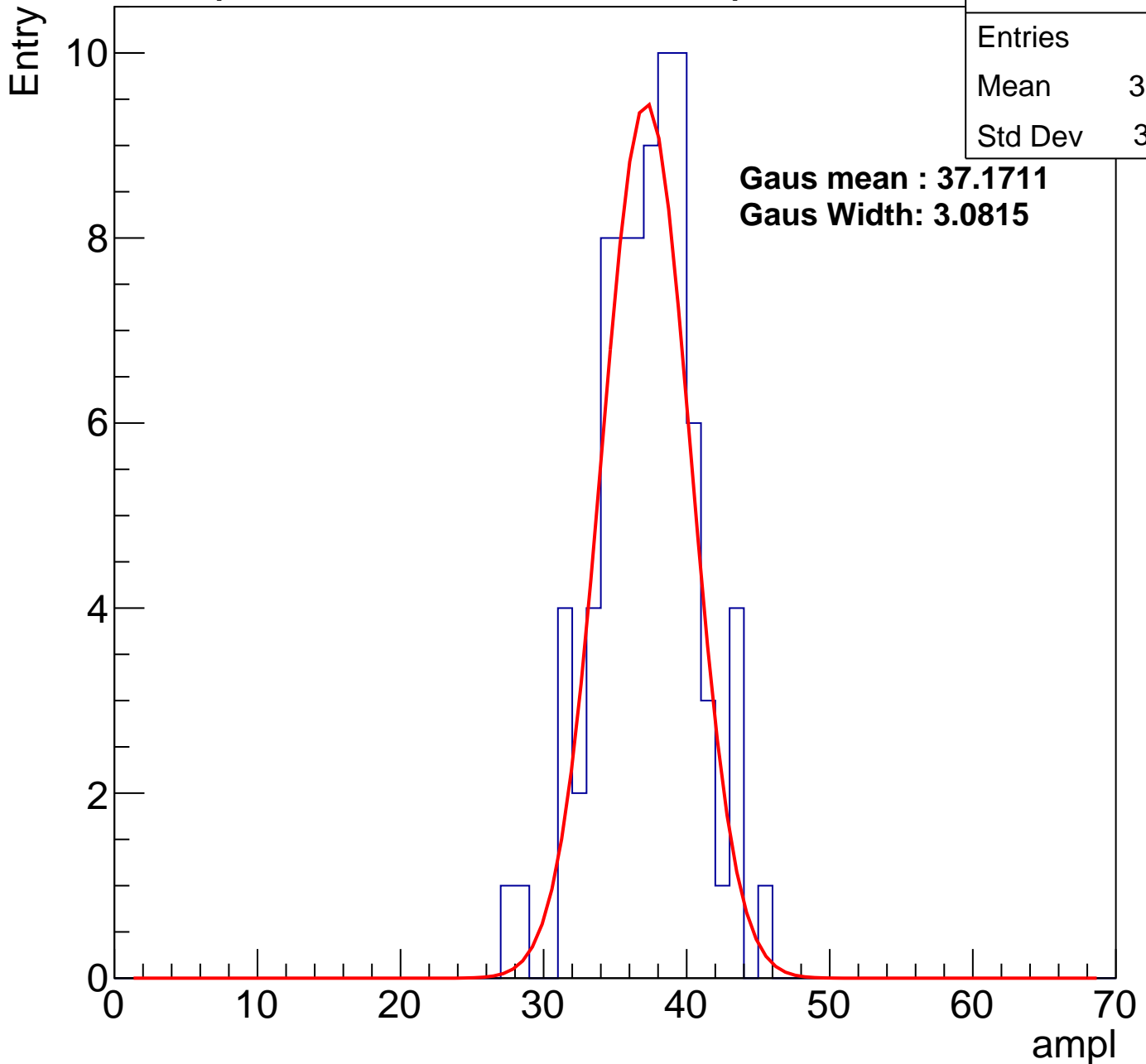


B1L003S, U19-ch65, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	36.75
Std Dev	3.411

Gaus mean : 37.1711
Gaus Width: 3.0815



B1L003S, U19-ch65, adc2

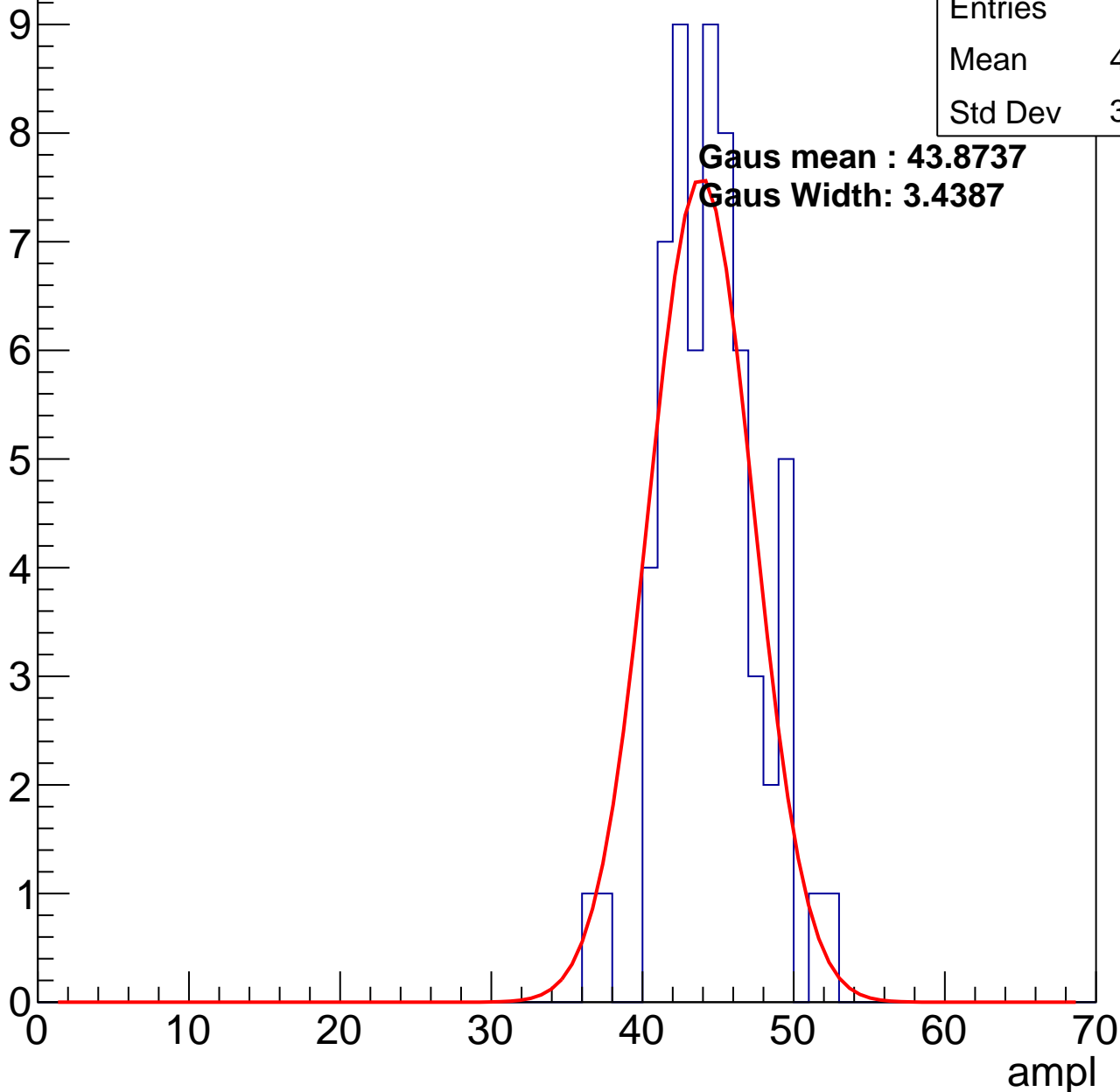
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	44.02
Std Dev	3.114

Gaus mean : 43.8737

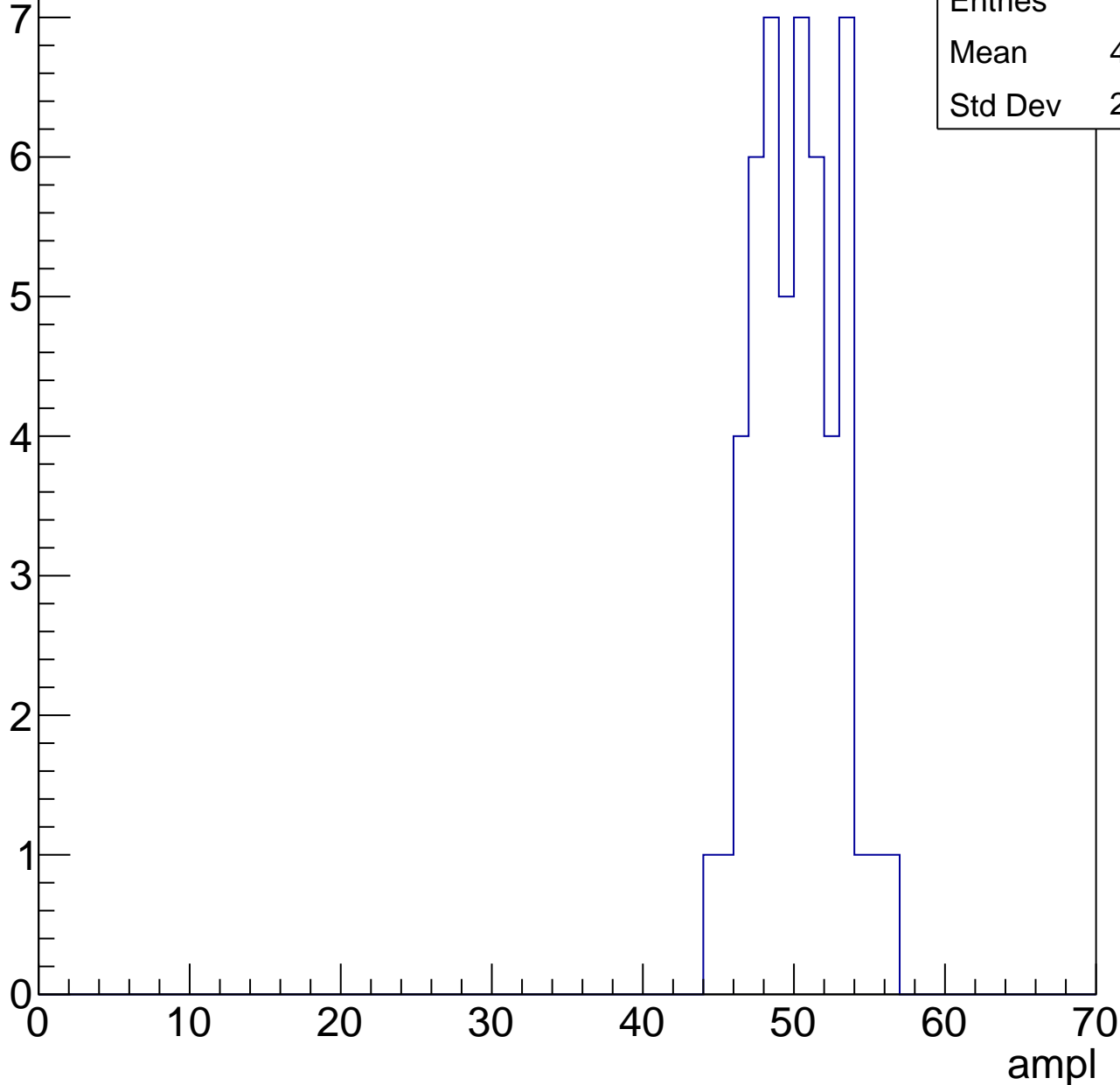
Gaus Width: 3.4387



B1L003S, U19-ch65, adc3

calib_packv5_042523_0143.root, FC#13, port D2

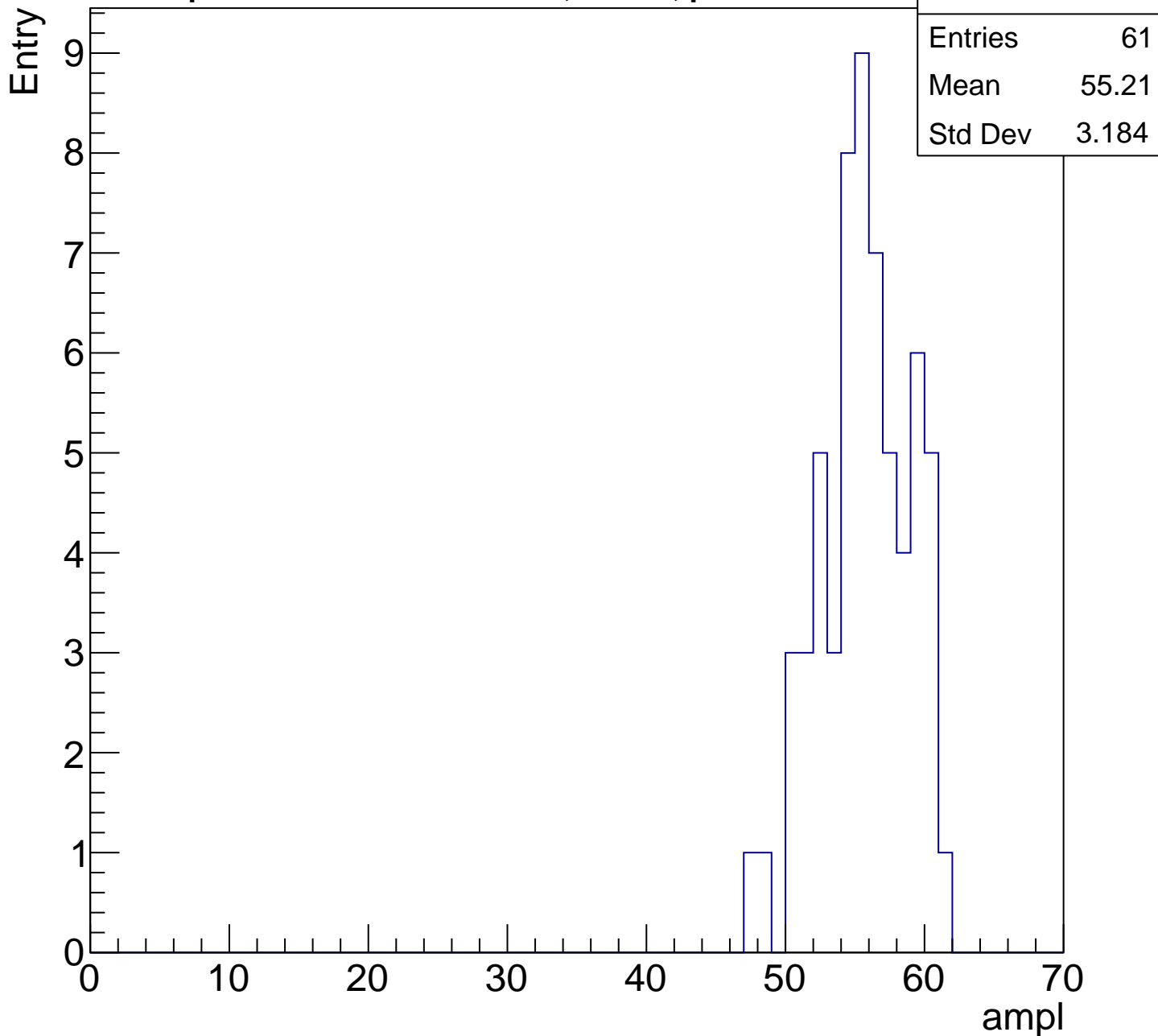
Entry



Entries	51
Mean	49.73
Std Dev	2.694

B1L003S, U19-ch65, adc4

calib_packv5_042523_0143.root, FC#13, port D2

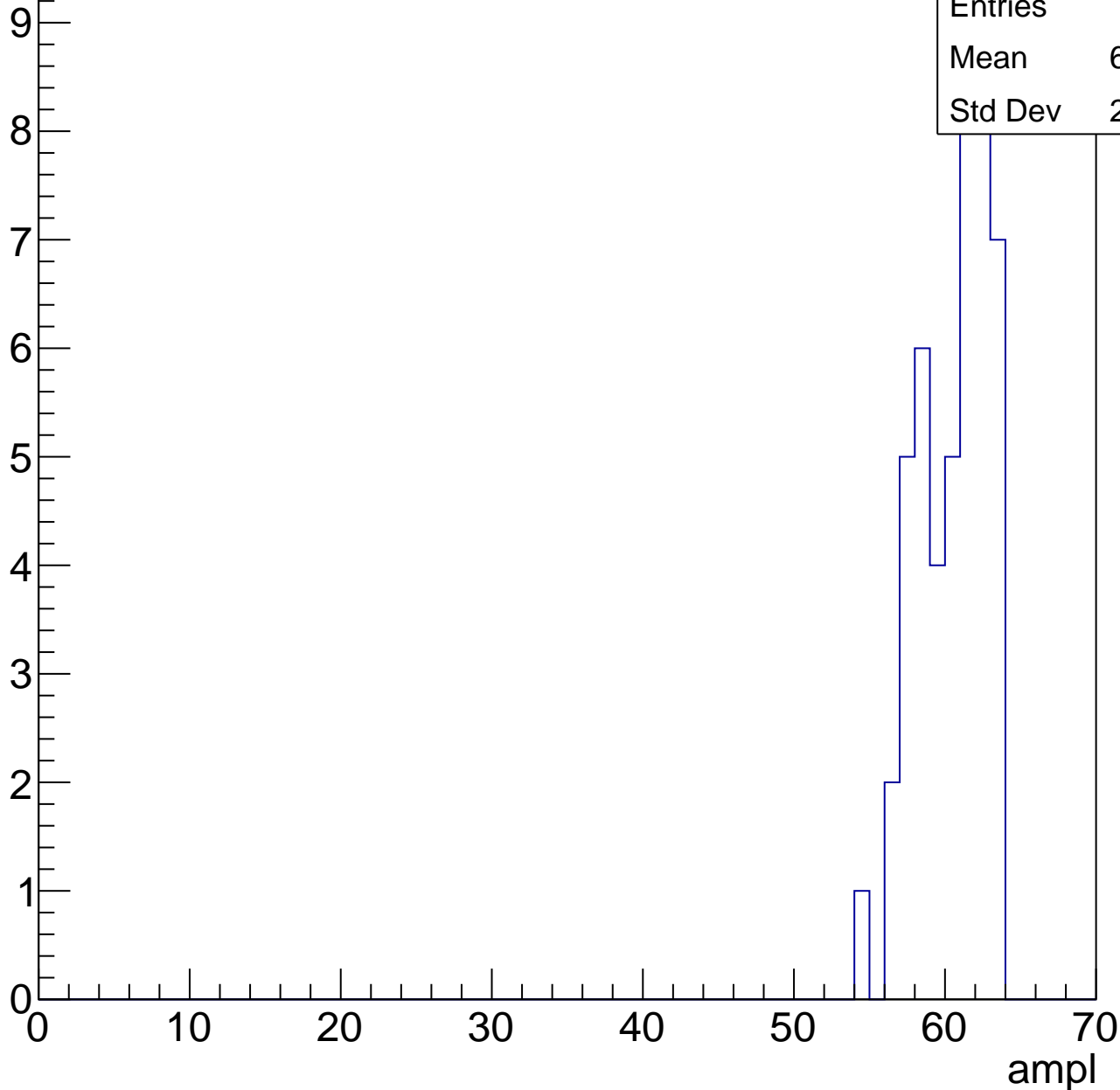


B1L003S, U19-ch65, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	60.06
Std Dev	2.277



B1L003S, U19-ch65, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch65, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L003S, U19-ch66, adc0

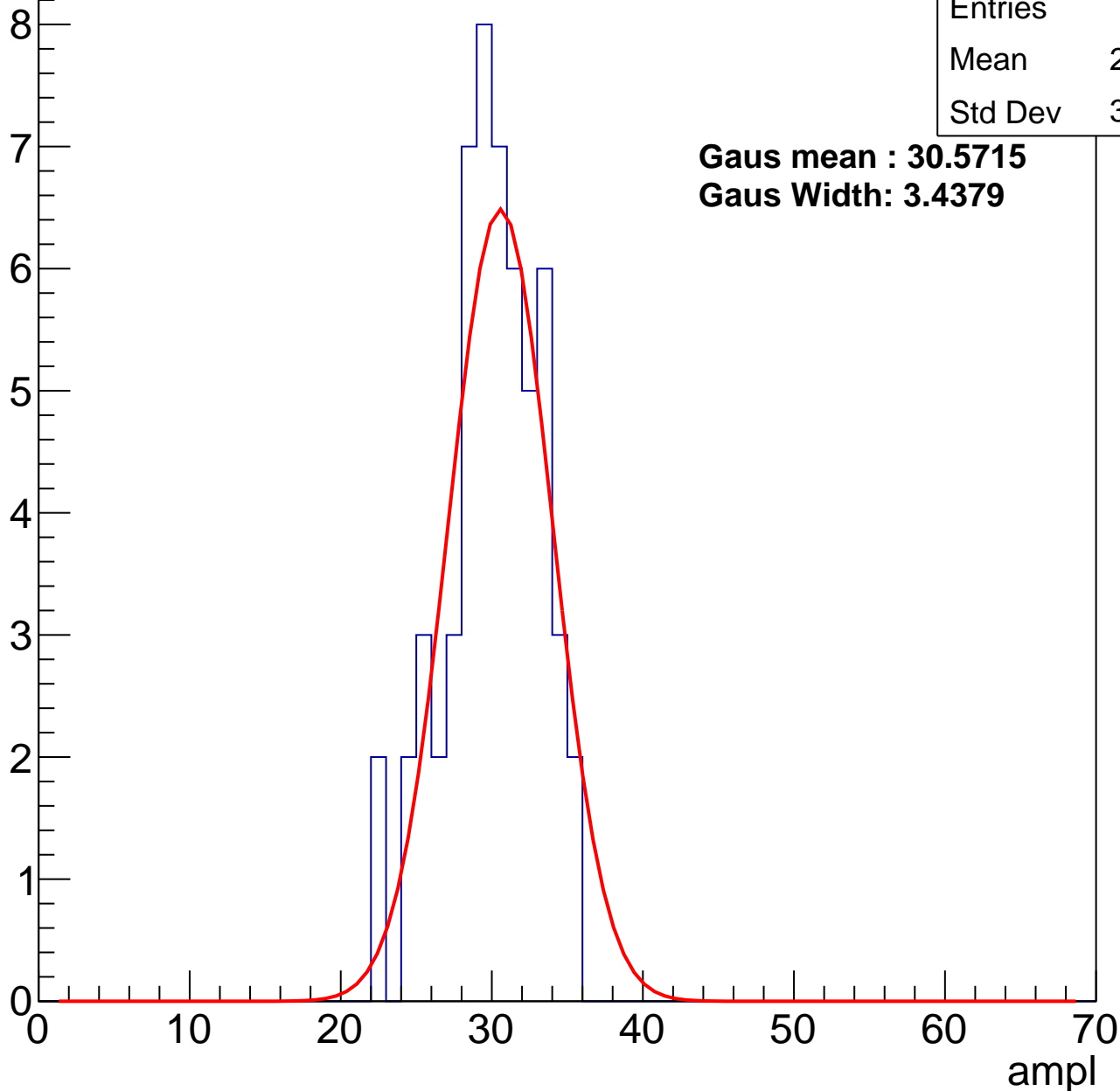
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	29.54
Std Dev	3.094

Gaus mean : 30.5715

Gaus Width: 3.4379



B1L003S, U19-ch66, adc1

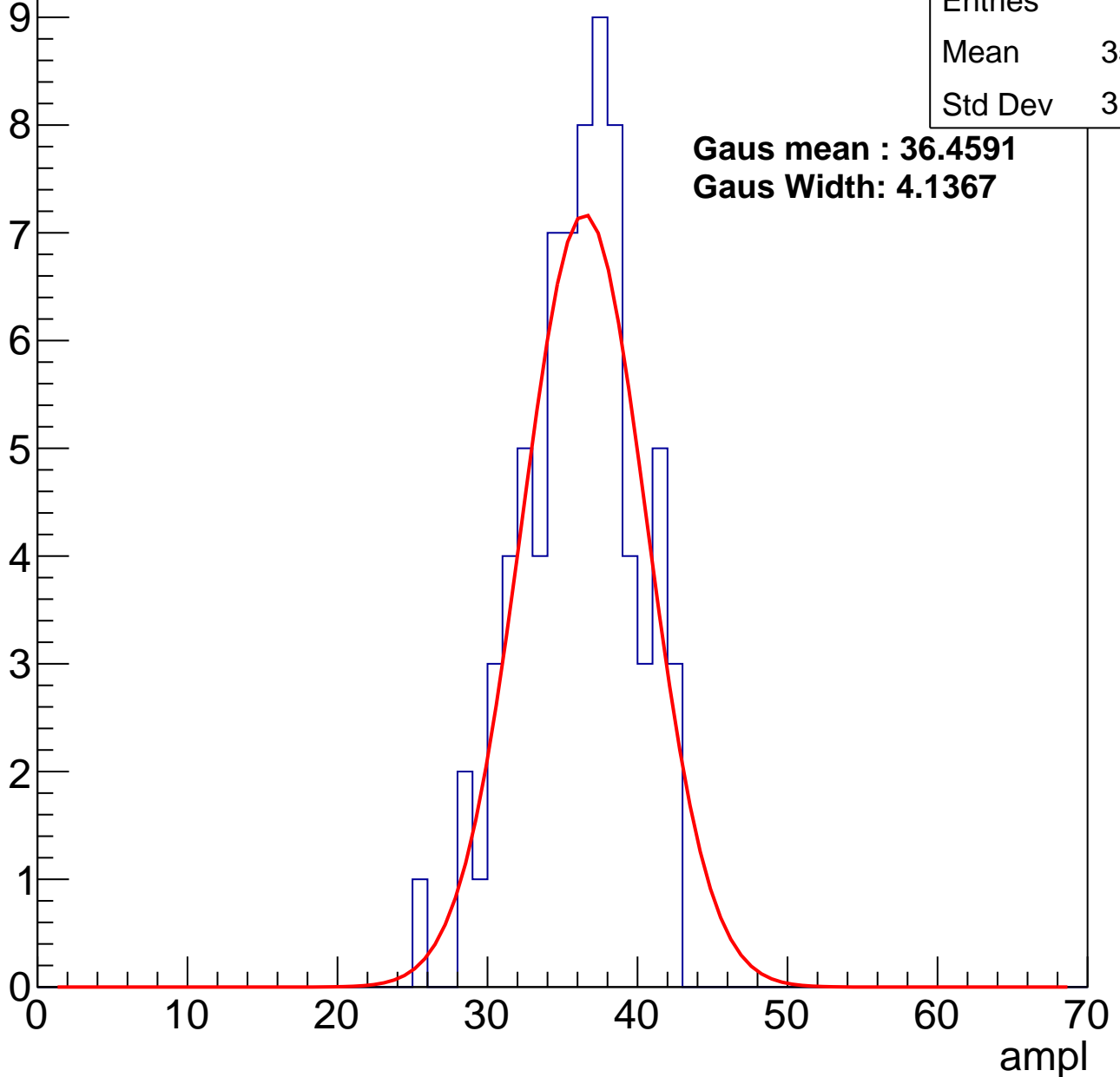
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	35.55
Std Dev	3.677

Gaus mean : 36.4591

Gaus Width: 4.1367



B1L003S, U19-ch66, adc2

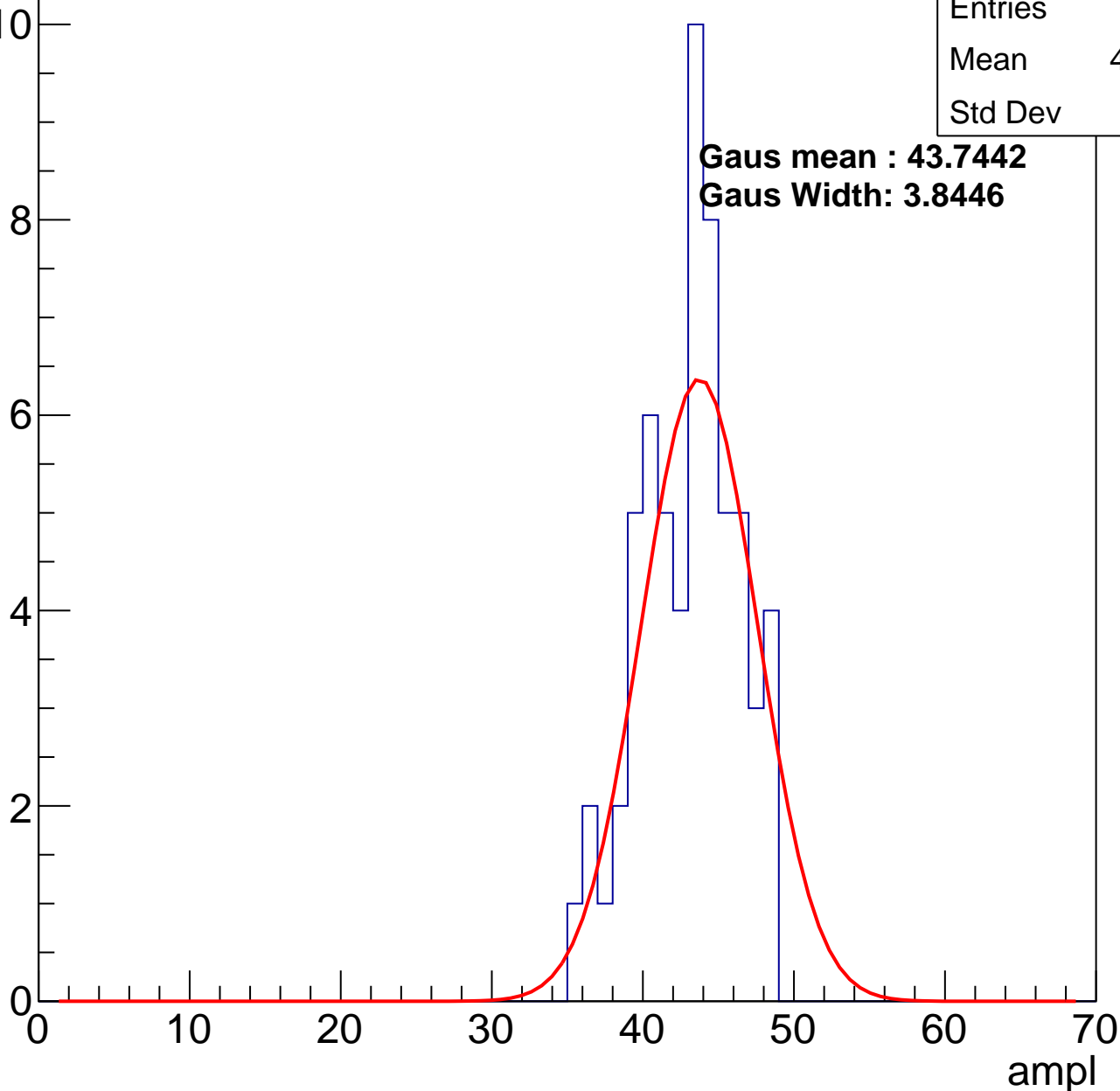
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.59
Std Dev	3.18

Gaus mean : 43.7442

Gaus Width: 3.8446

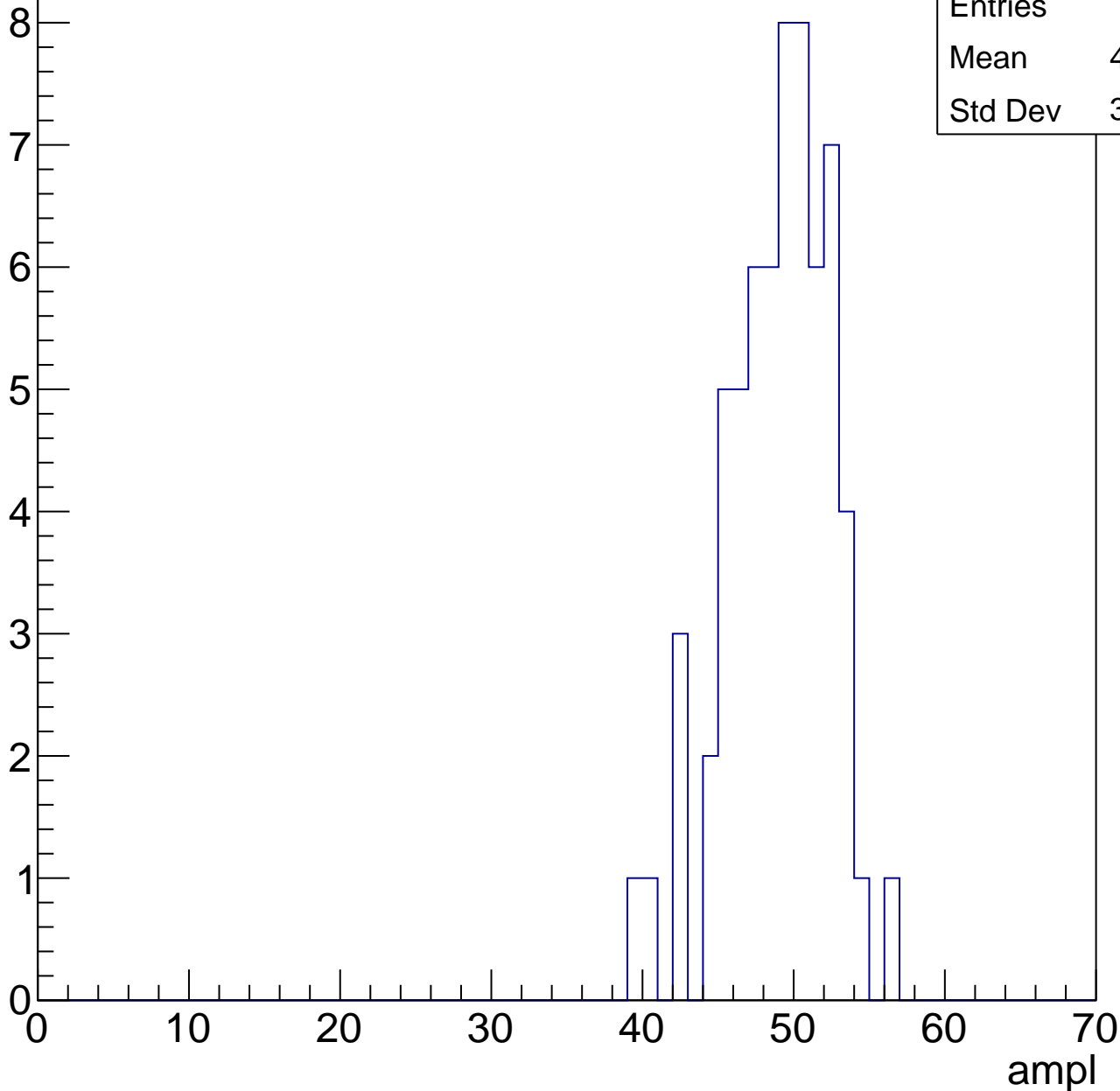


B1L003S, U19-ch66, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.47
Std Dev	3.419

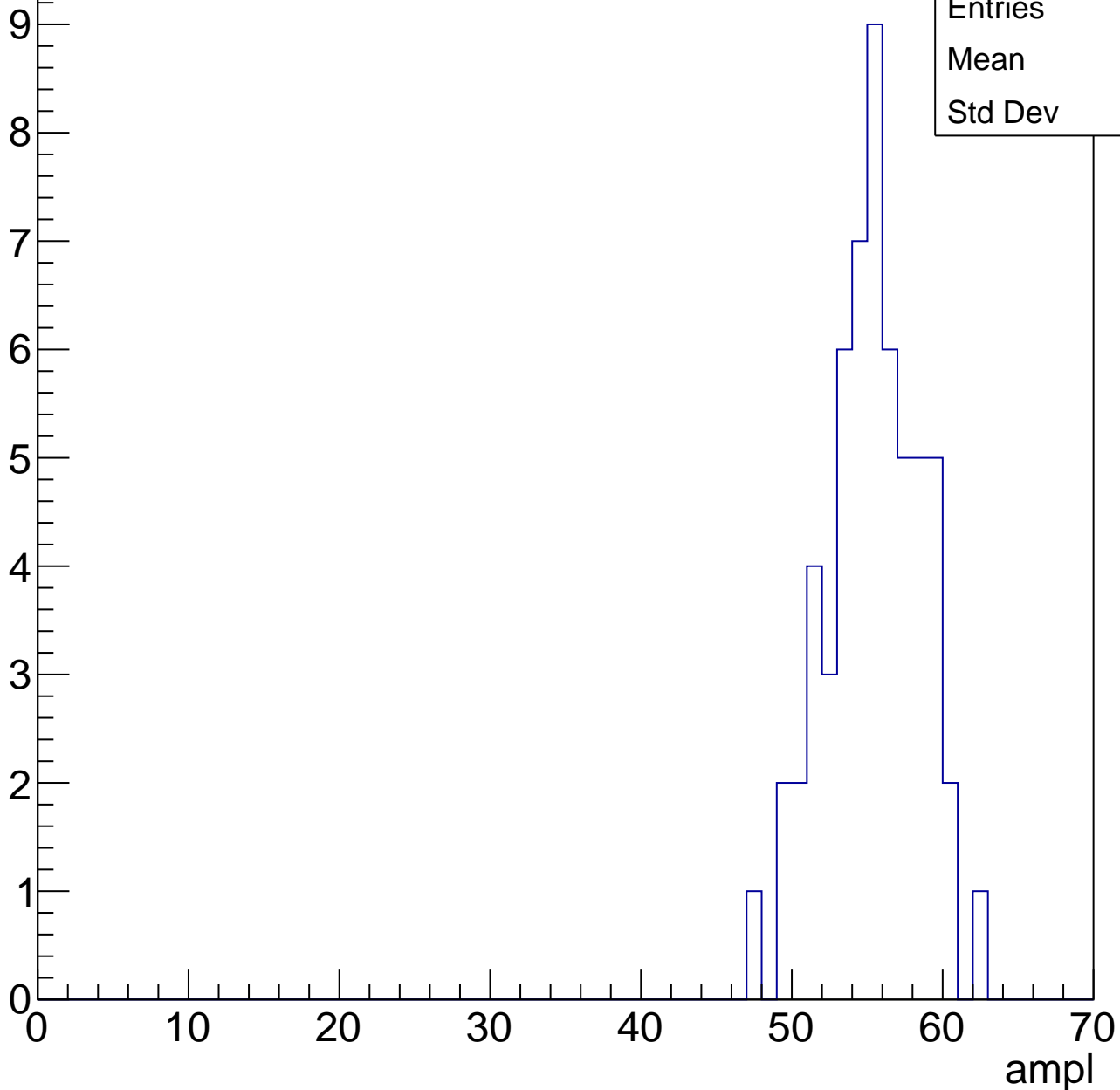


B1L003S, U19-ch66, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.9
Std Dev	3.1

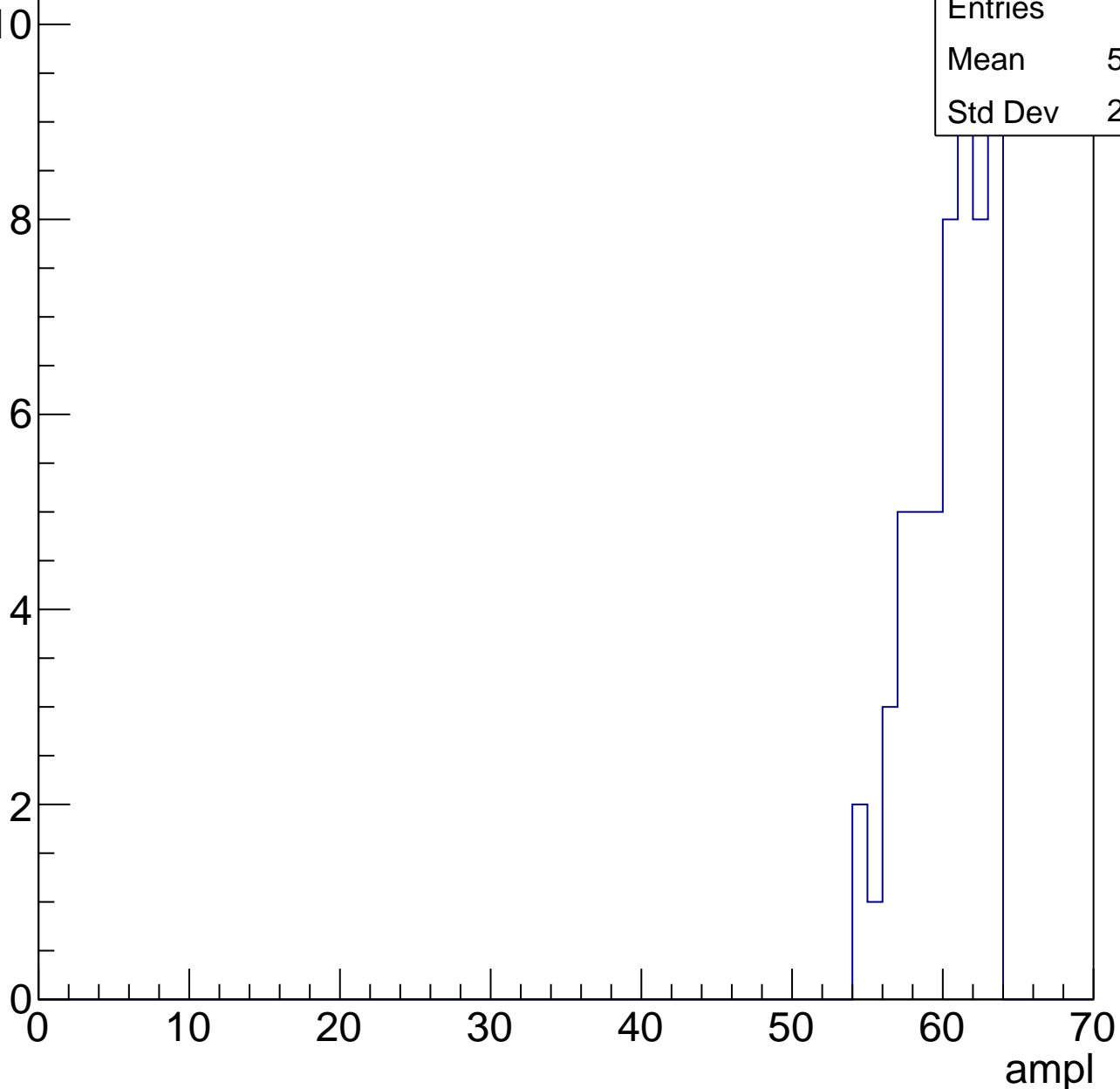


B1L003S, U19-ch66, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	59.89
Std Dev	2.454



B1L003S, U19-ch66, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch66, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch67, adc0

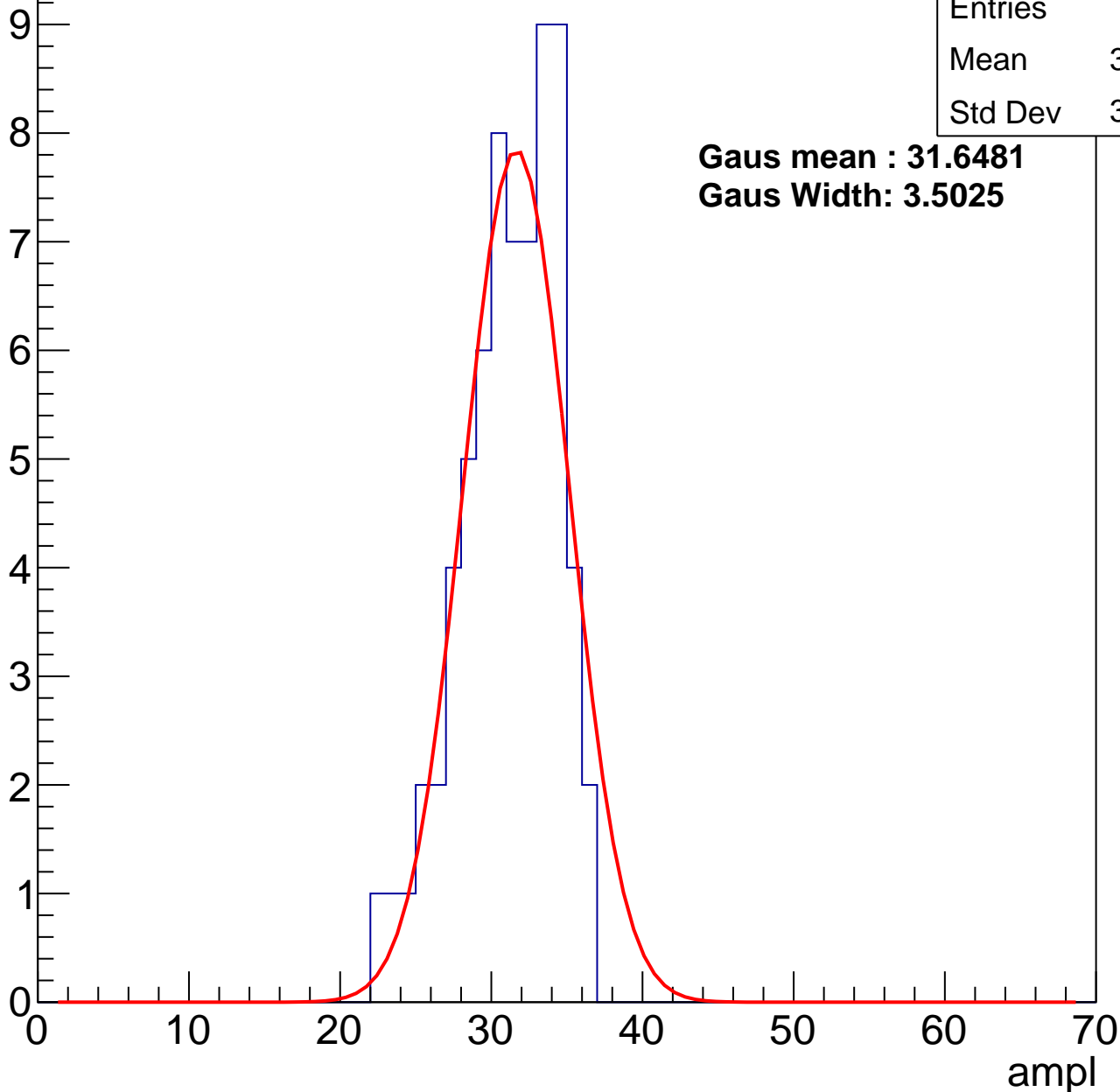
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	30.72
Std Dev	3.185

Gaus mean : 31.6481

Gaus Width: 3.5025



B1L003S, U19-ch67, adc1

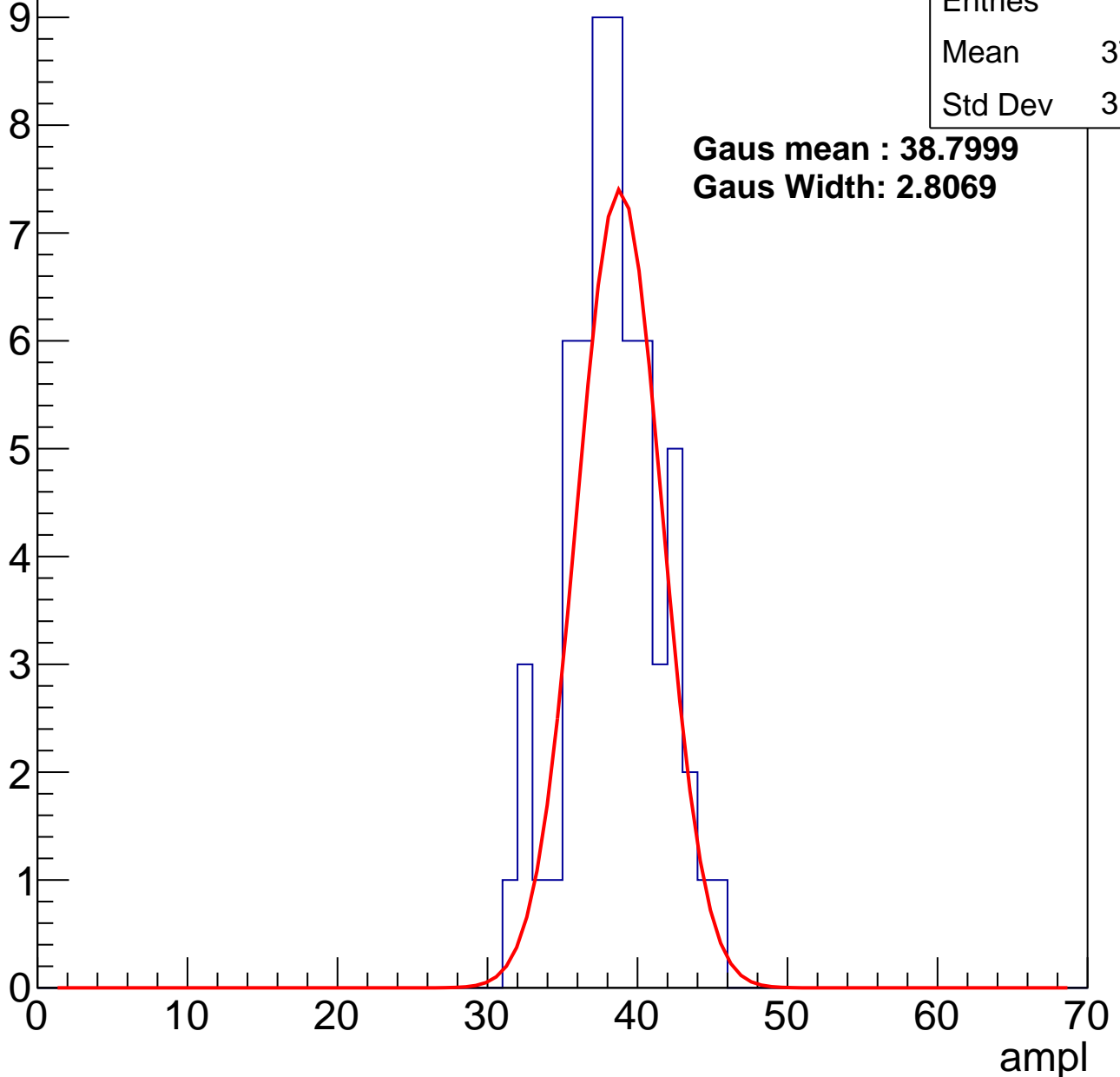
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	37.95
Std Dev	3.046

Gaus mean : 38.7999

Gaus Width: 2.8069



B1L003S, U19-ch67, adc2

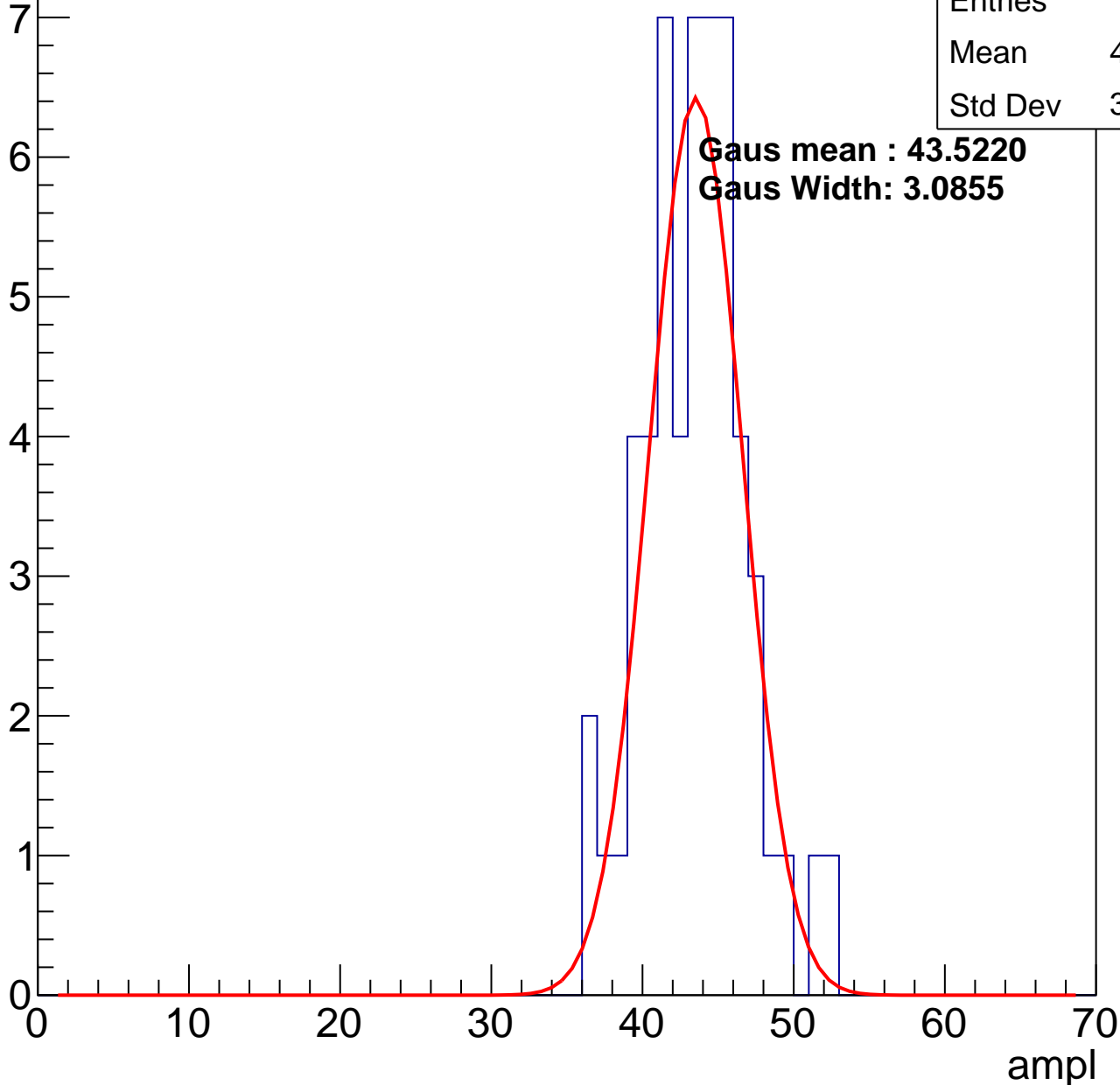
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	43.04
Std Dev	3.346

Gaus mean : 43.5220

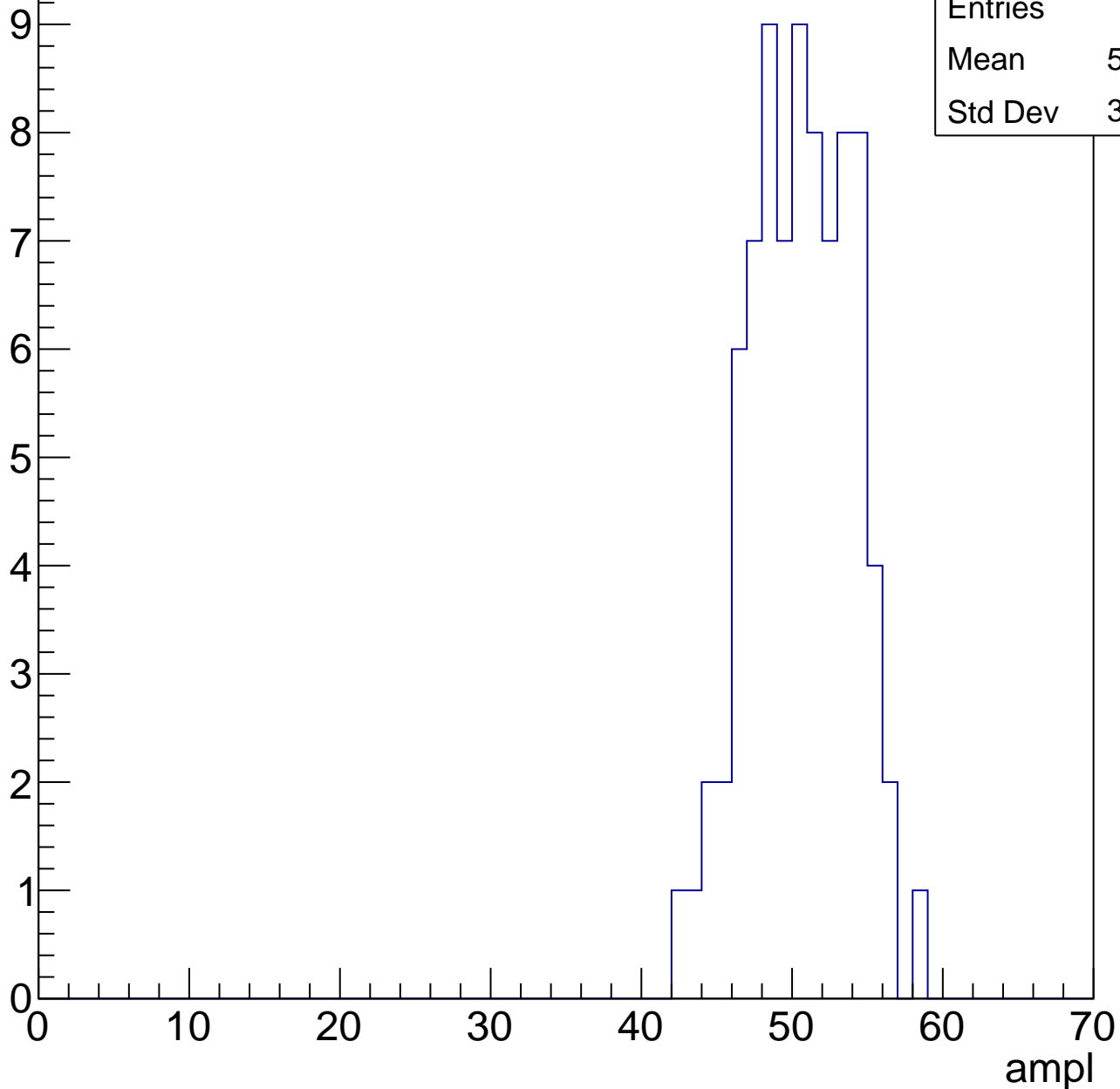
Gaus Width: 3.0855



B1L003S, U19-ch67, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

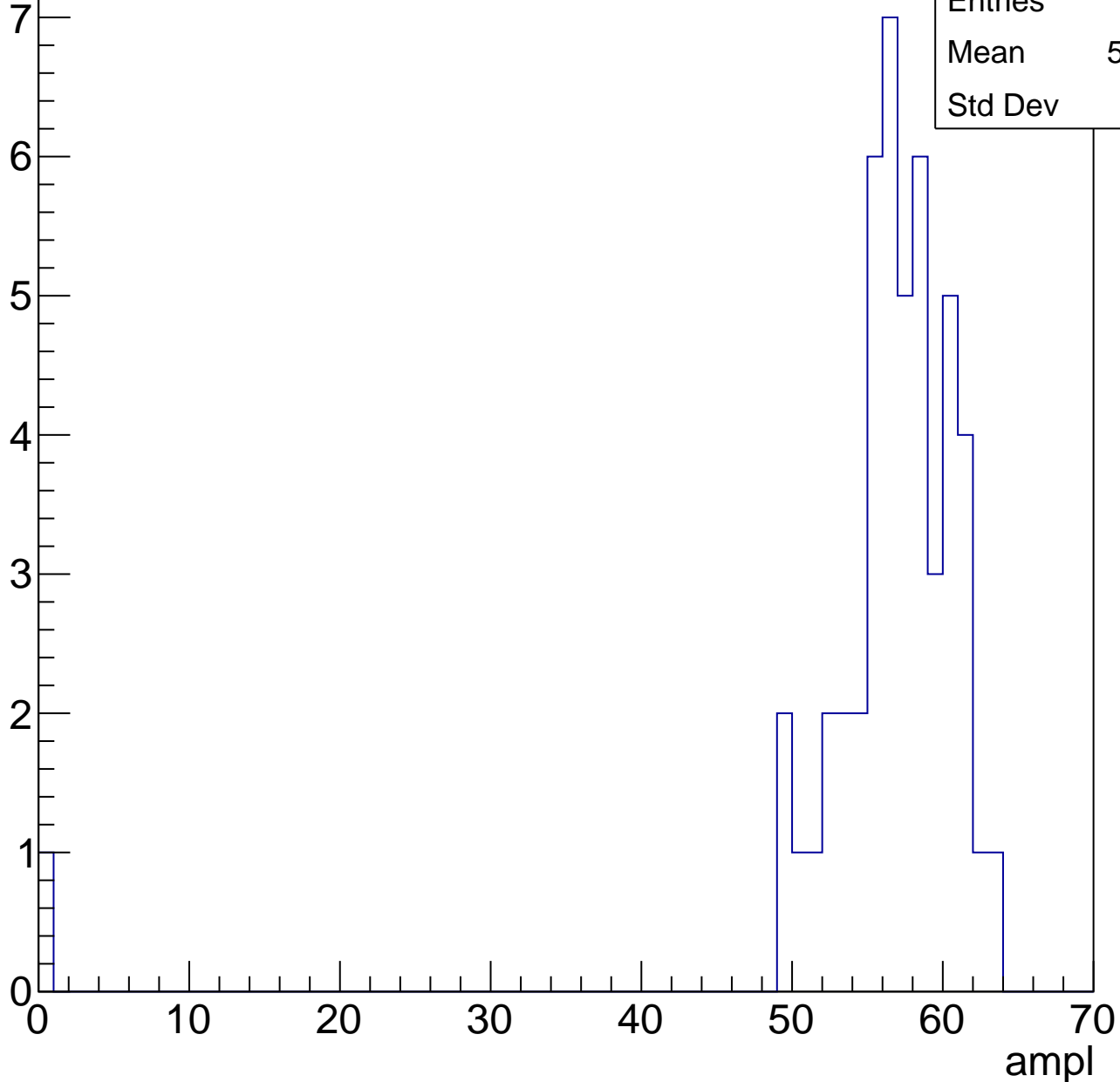


B1L003S, U19-ch67, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55.47
Std Dev	8.65

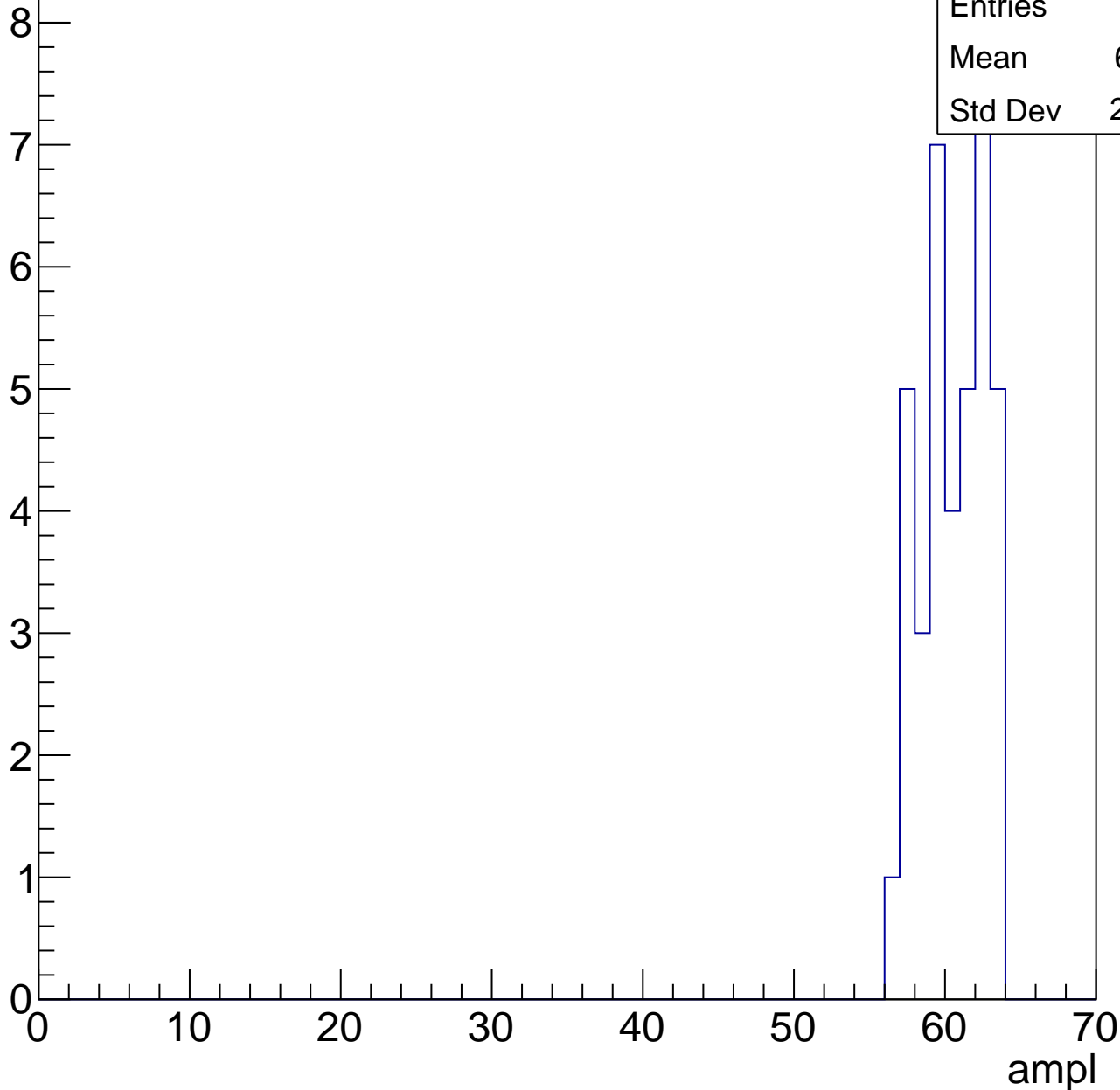


B1L003S, U19-ch67, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

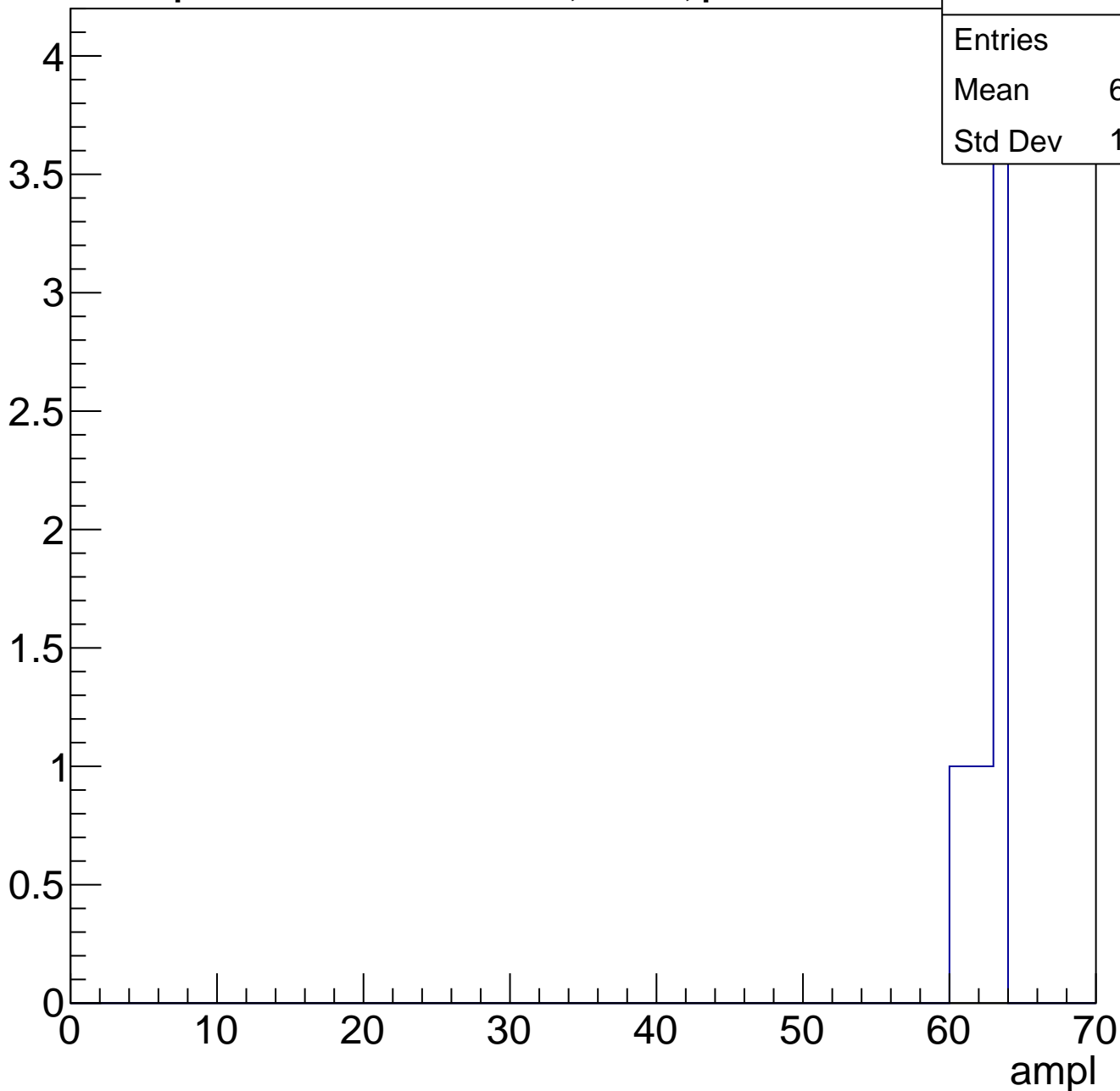
Entries	38
Mean	60.11
Std Dev	2.062



B1L003S, U19-ch67, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch67, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch68, adc0

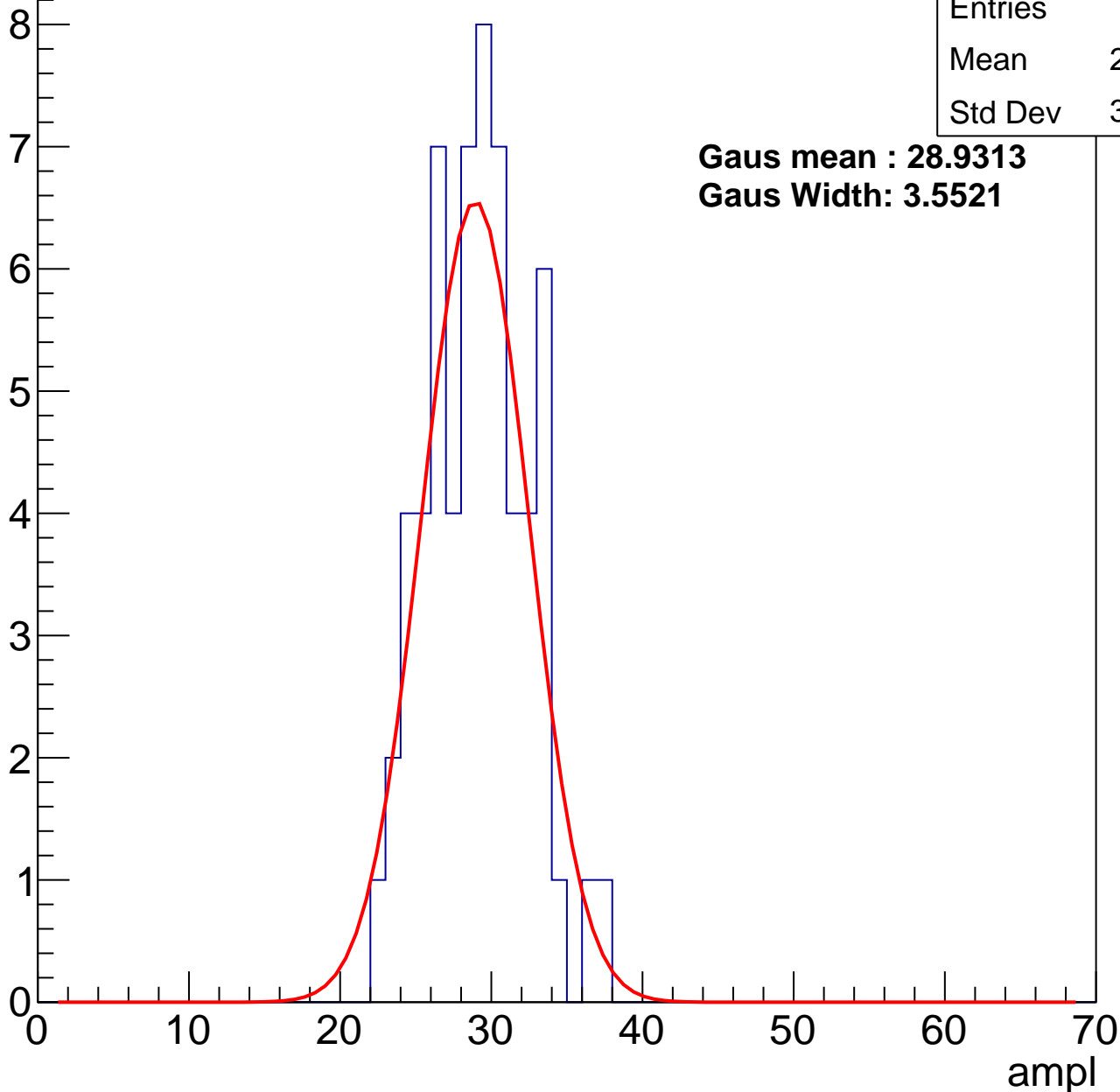
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.67
Std Dev	3.283

Gaus mean : 28.9313

Gaus Width: 3.5521



B1L003S, U19-ch68, adc1

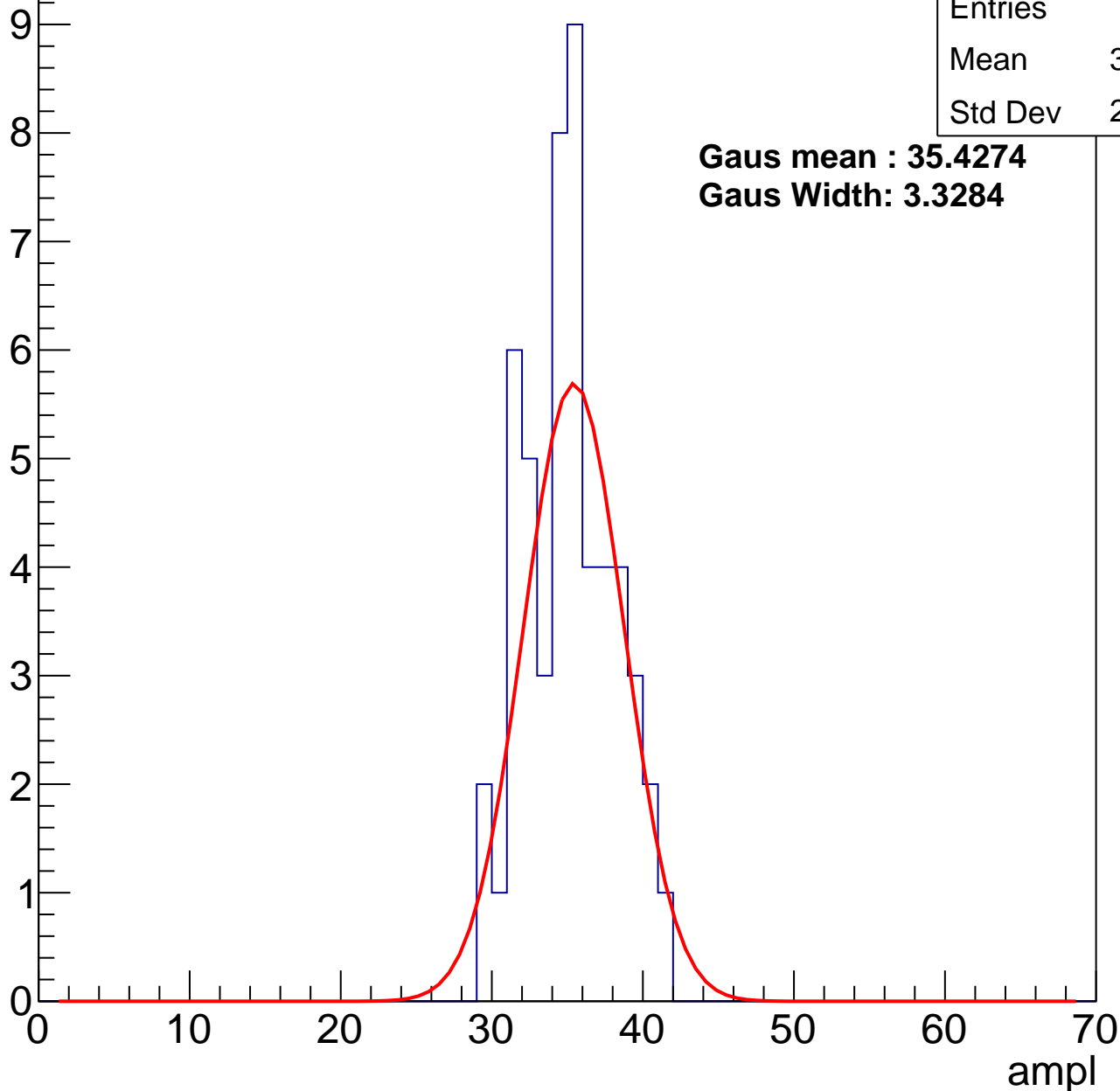
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	34.65
Std Dev	2.915

Gaus mean : 35.4274

Gaus Width: 3.3284



B1L003S, U19-ch68, adc2

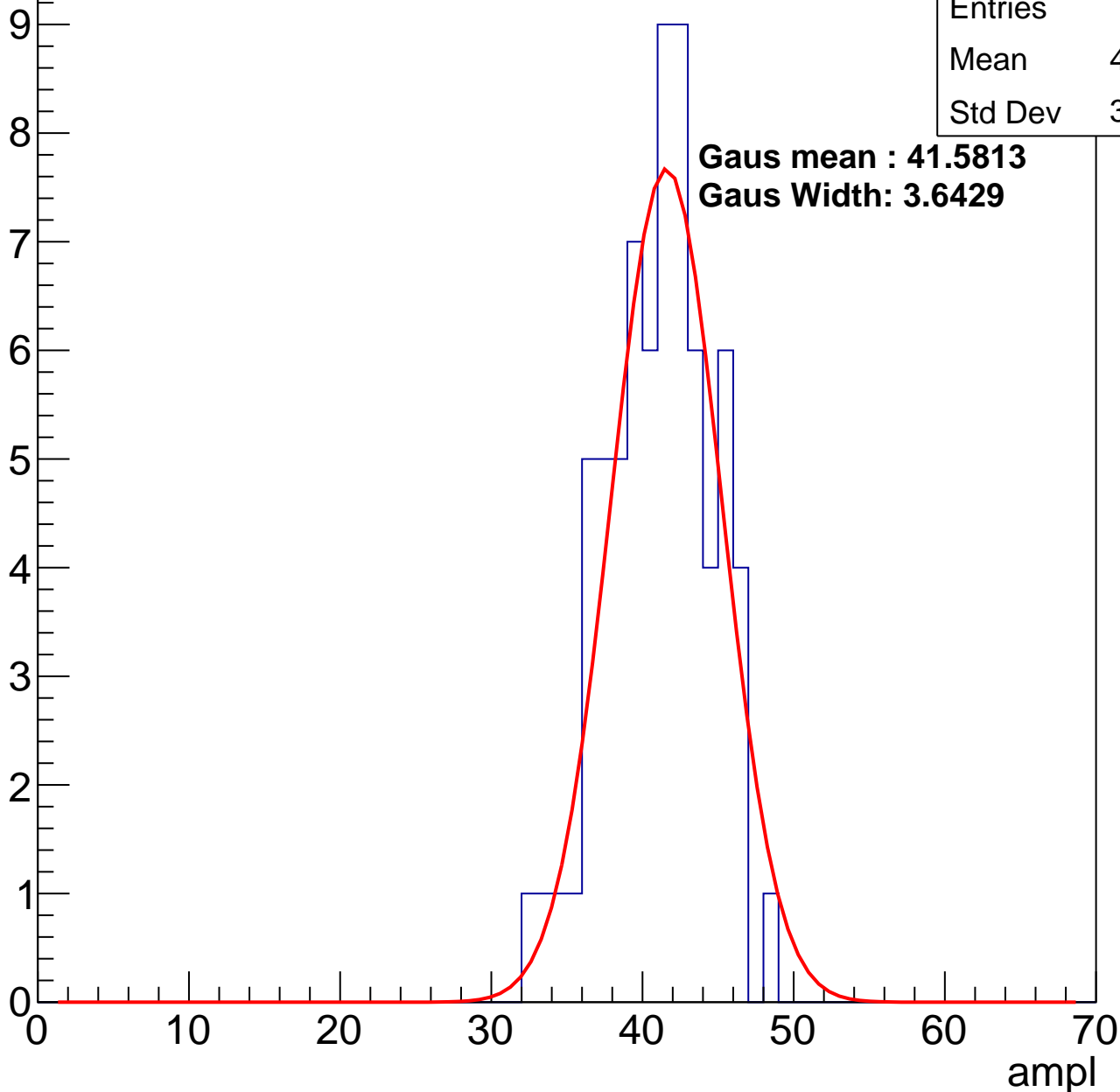
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	40.63
Std Dev	3.395

Gaus mean : 41.5813

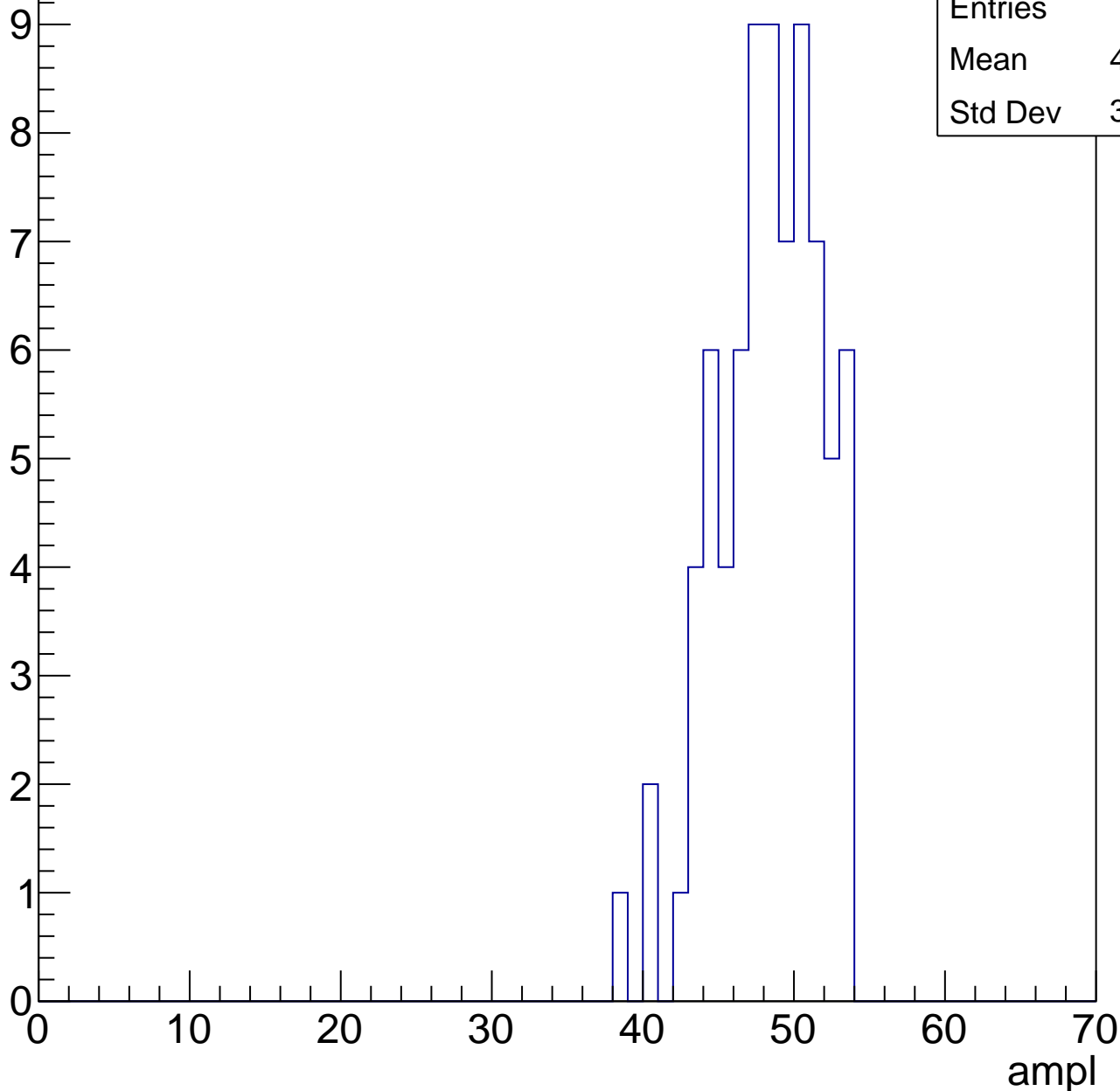
Gaus Width: 3.6429



B1L003S, U19-ch68, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

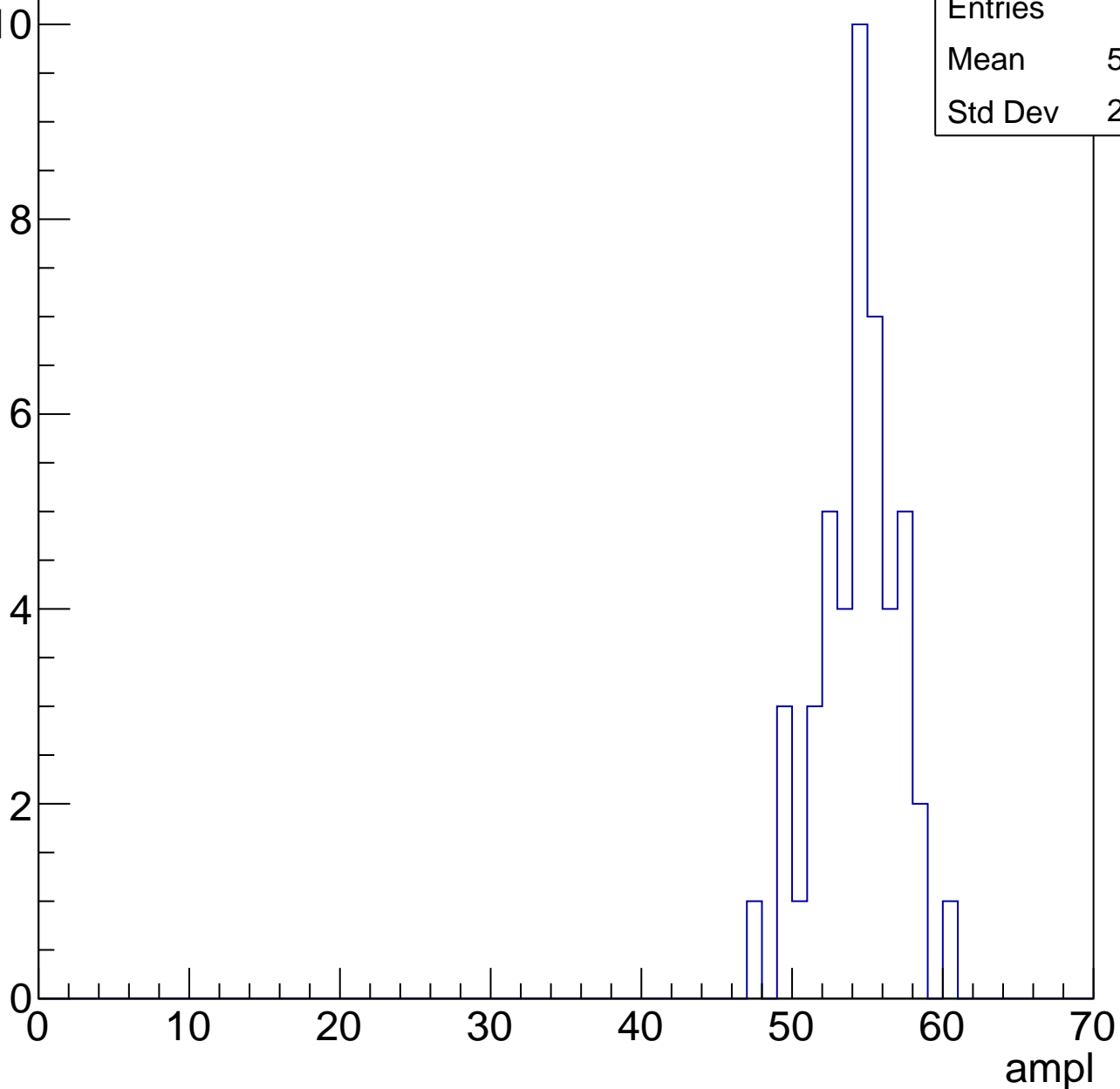


B1L003S, U19-ch68, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	53.89
Std Dev	2.664



B1L003S, U19-ch68, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

Entries 59

Mean 59.31

Std Dev 2.219

8

6

4

2

0

0

10

20

30

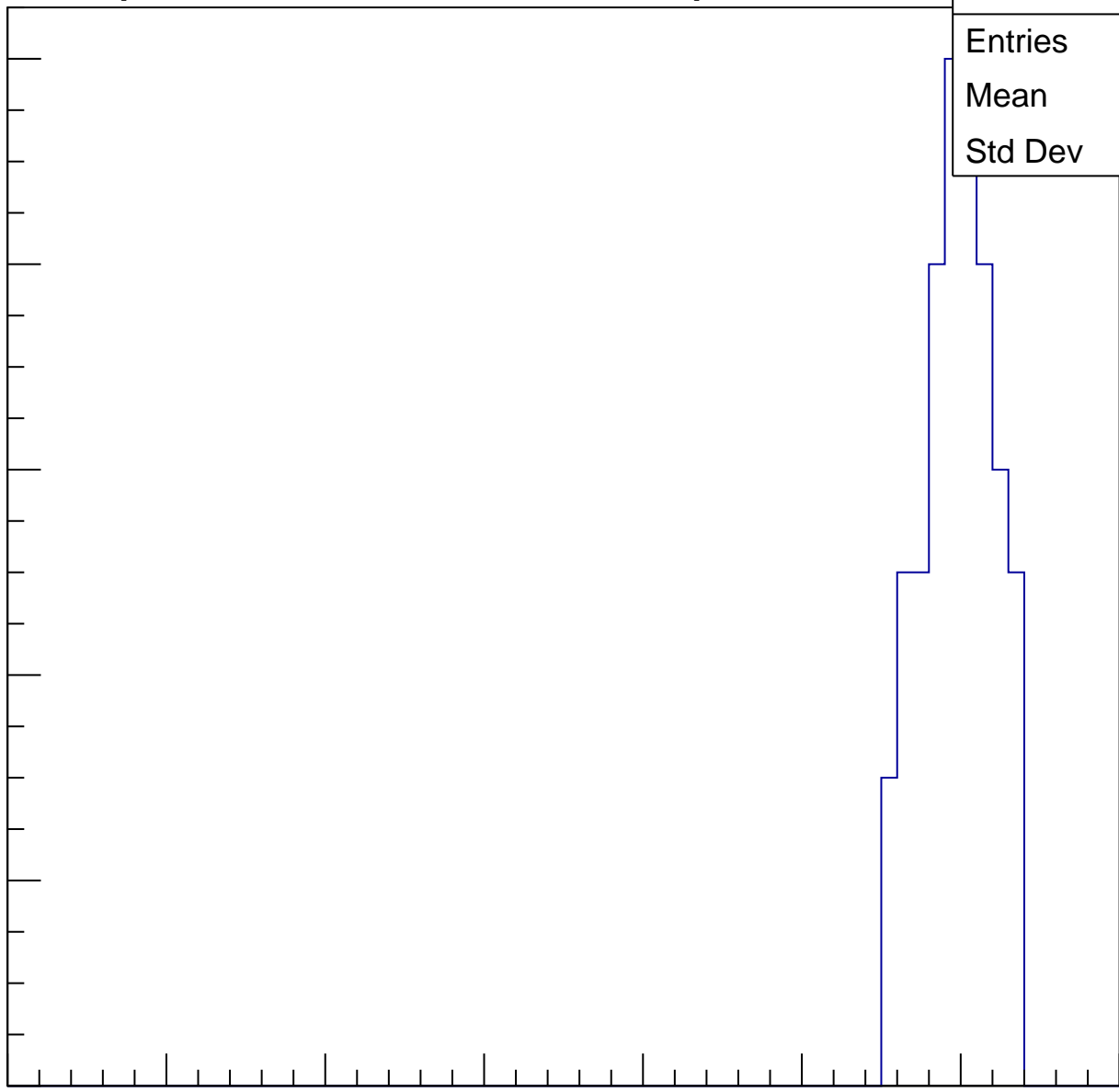
40

50

60

70

ampl

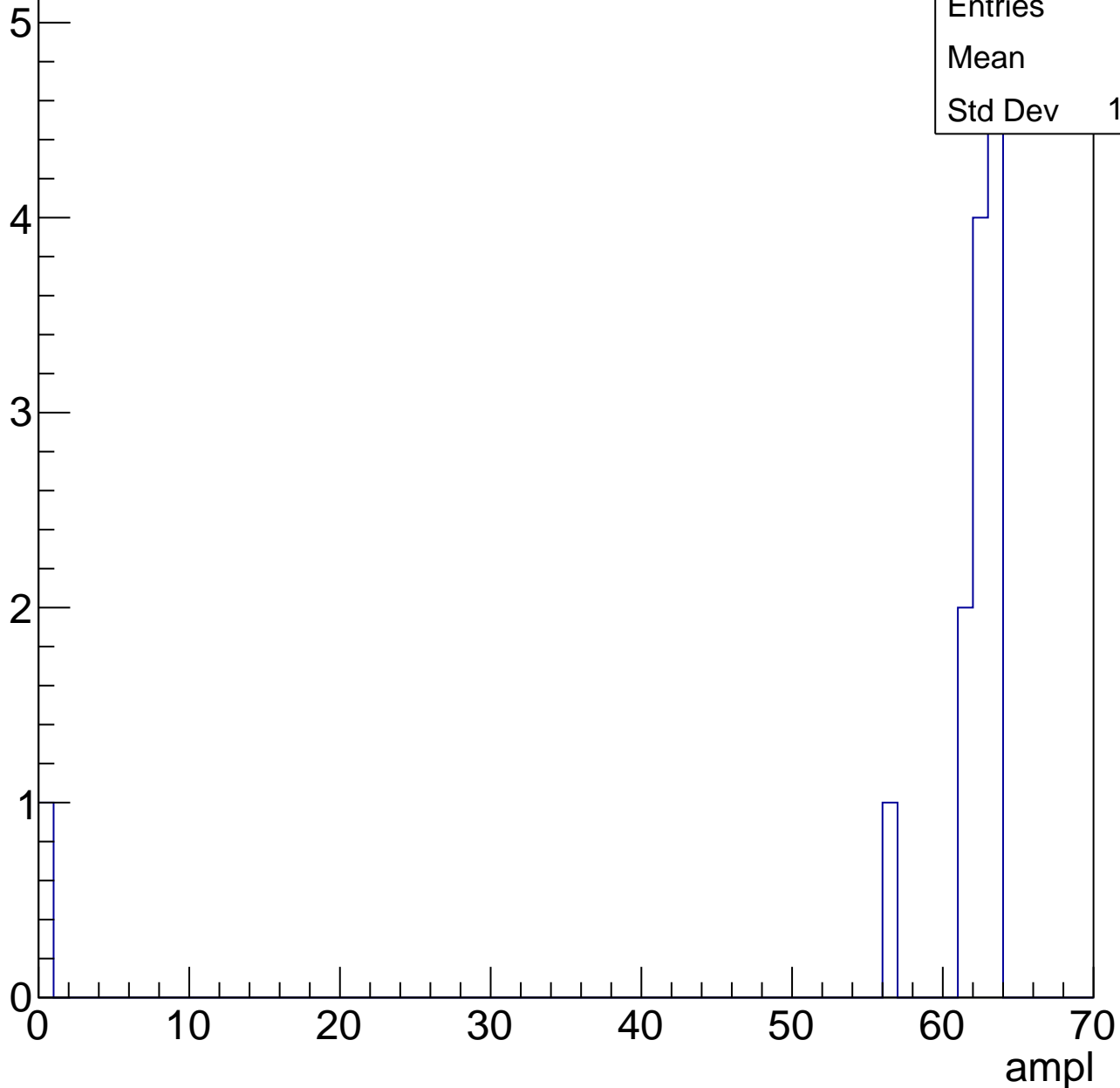


B1L003S, U19-ch68, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	57
Std Dev	16.55



B1L003S, U19-ch68, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L003S, U19-ch69, adc0

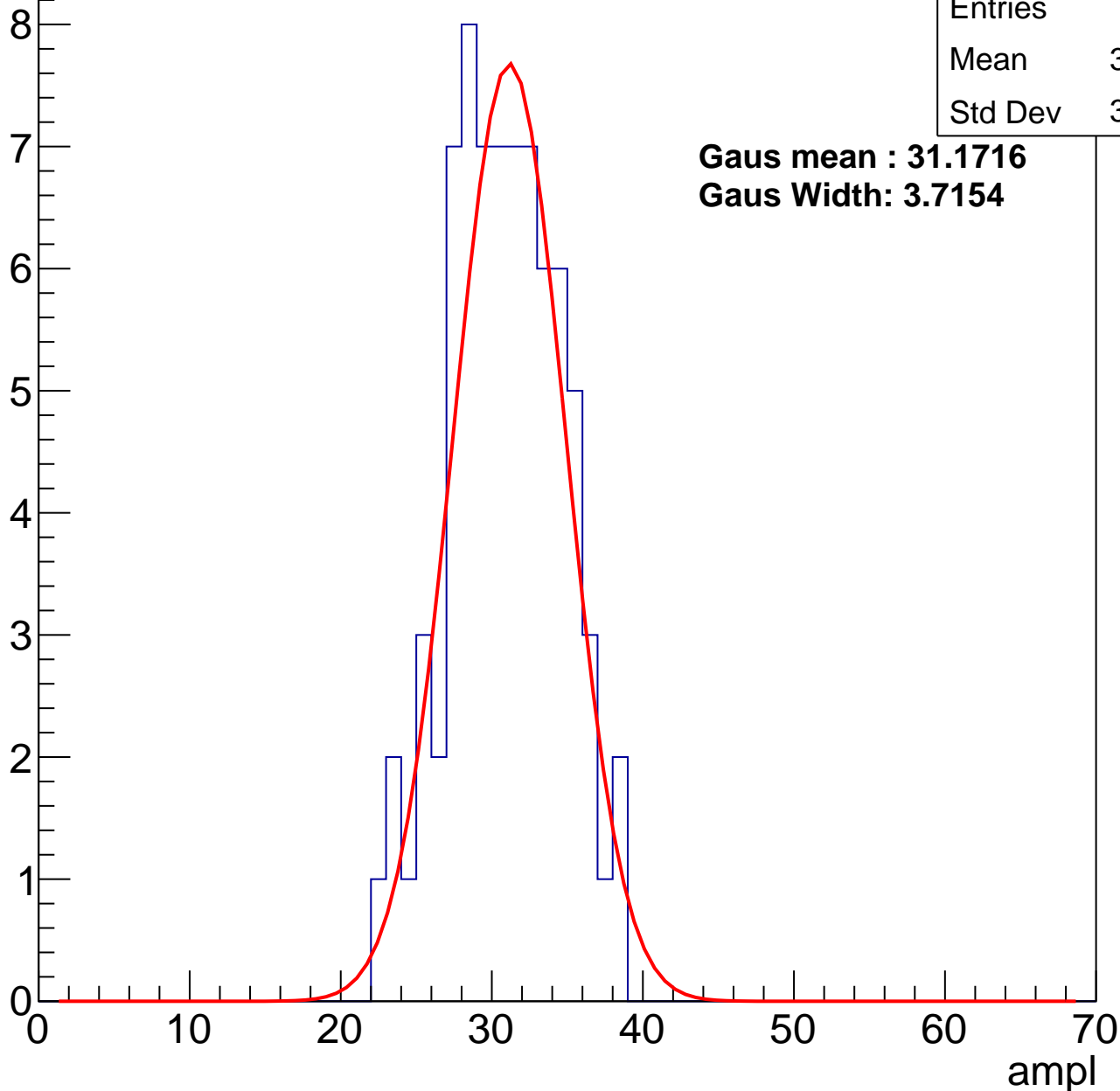
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	30.45
Std Dev	3.627

Gaus mean : 31.1716

Gaus Width: 3.7154

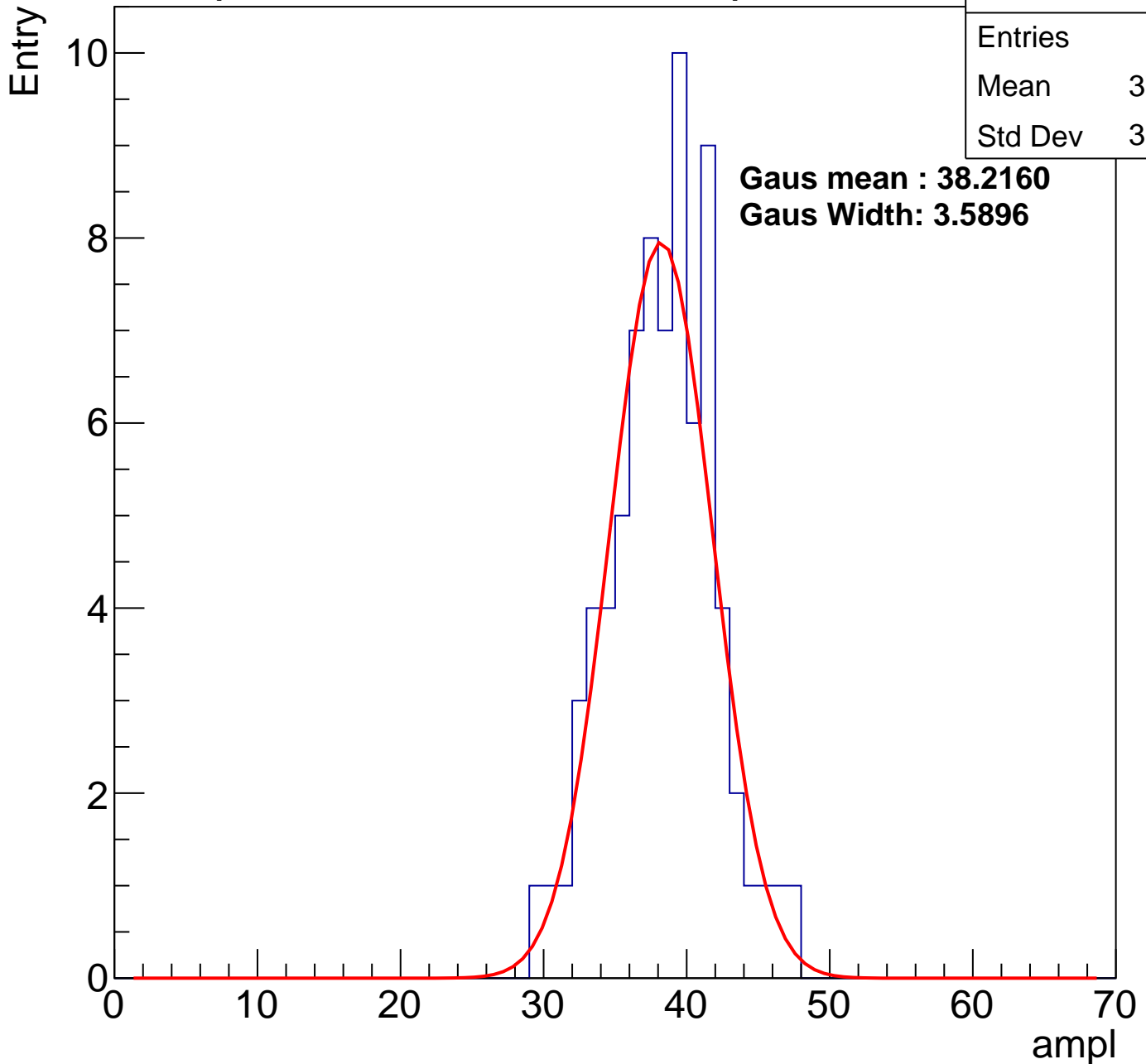


B1L003S, U19-ch69, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	37.87
Std Dev	3.636

Gaus mean : 38.2160
Gaus Width: 3.5896

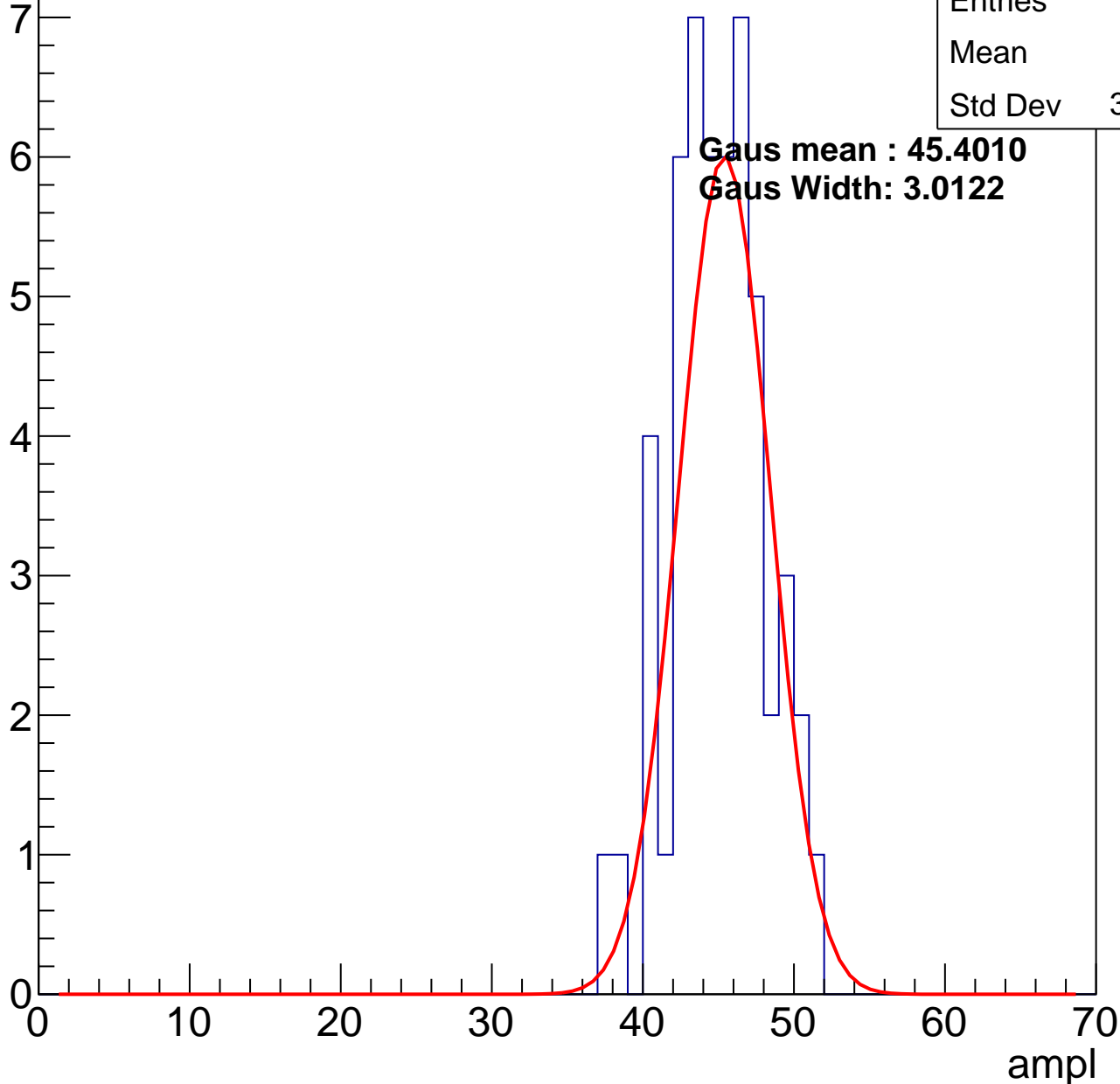


B1L003S, U19-ch69, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	44.5
Std Dev	3.048

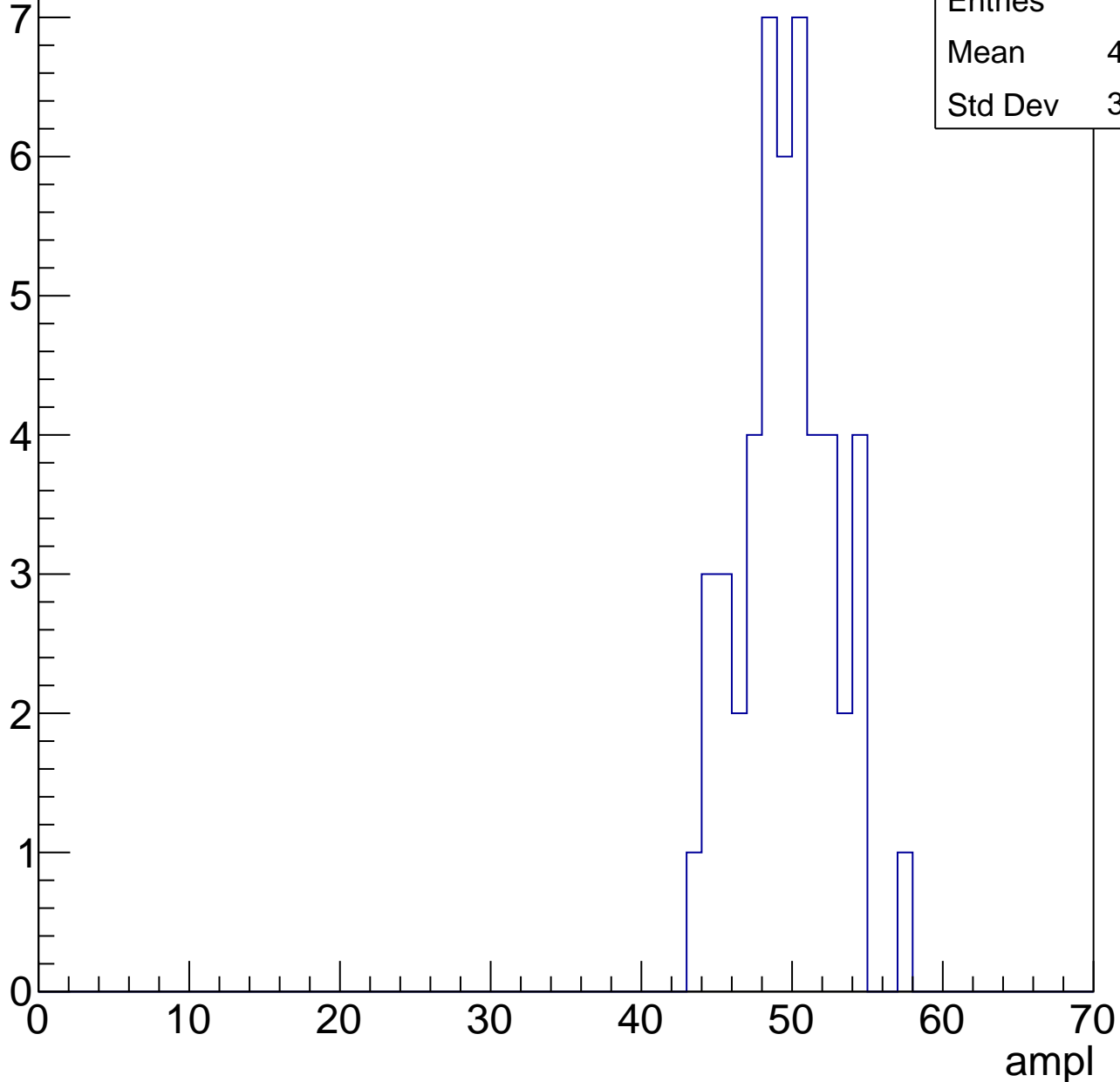


B1L003S, U19-ch69, adc3

calib_packv5_042523_0143.root, FC#13, port D2

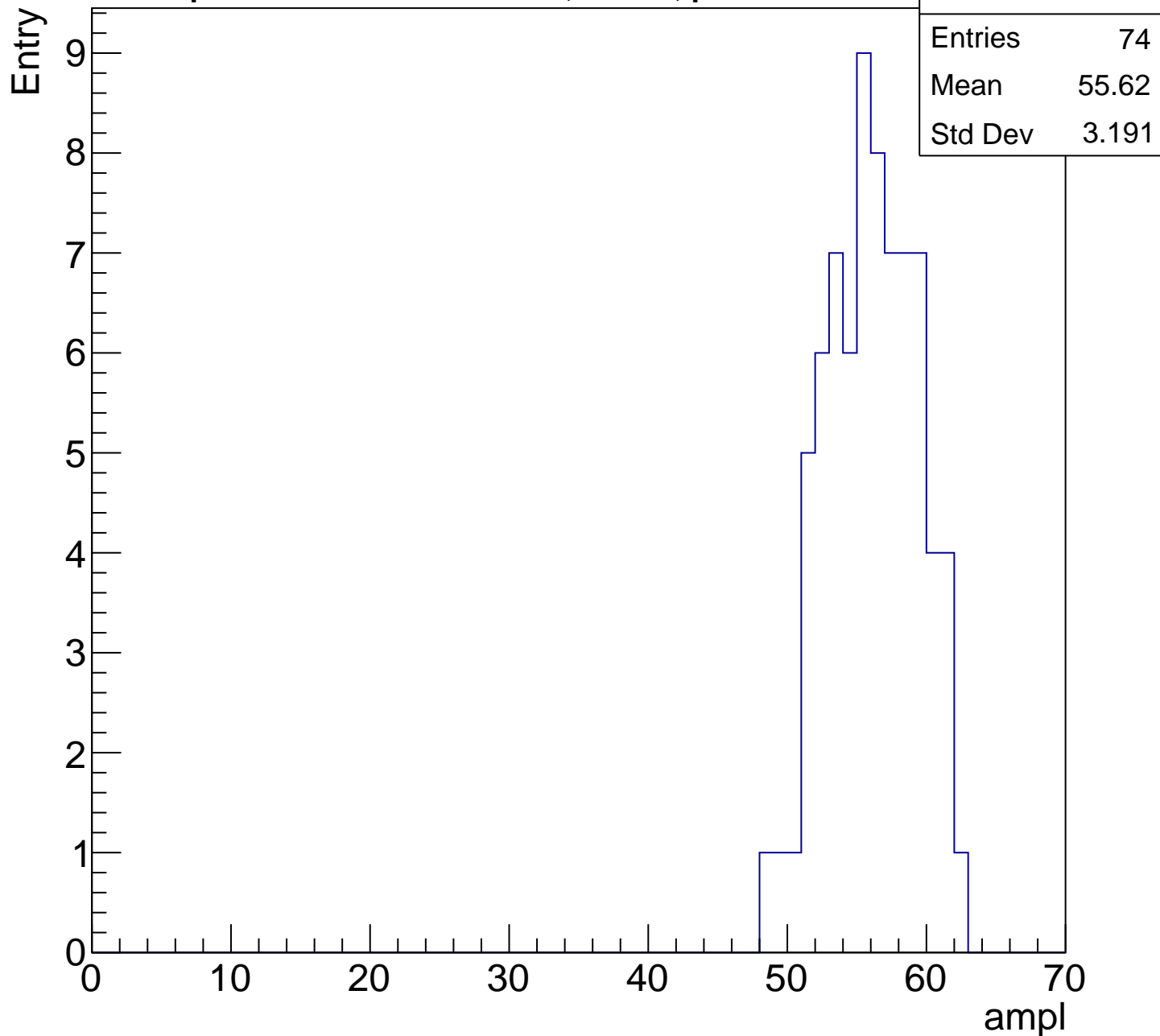
Entry

Entries	48
Mean	49.19
Std Dev	3.073



B1L003S, U19-ch69, adc4

calib_packv5_042523_0143.root, FC#13, port D2

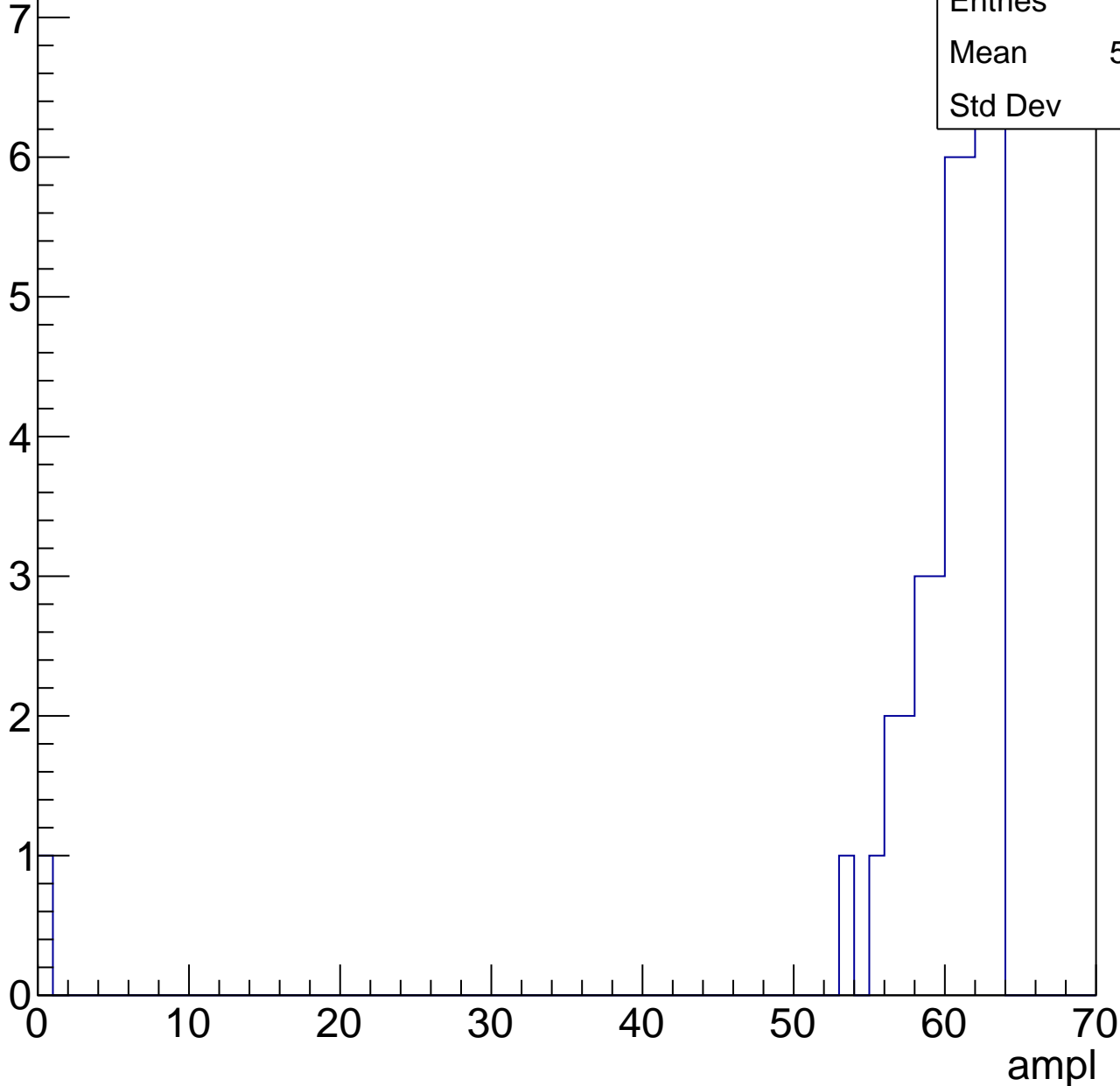


B1L003S, U19-ch69, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.62
Std Dev	9.82



B1L003S, U19-ch69, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch69, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch70, adc0

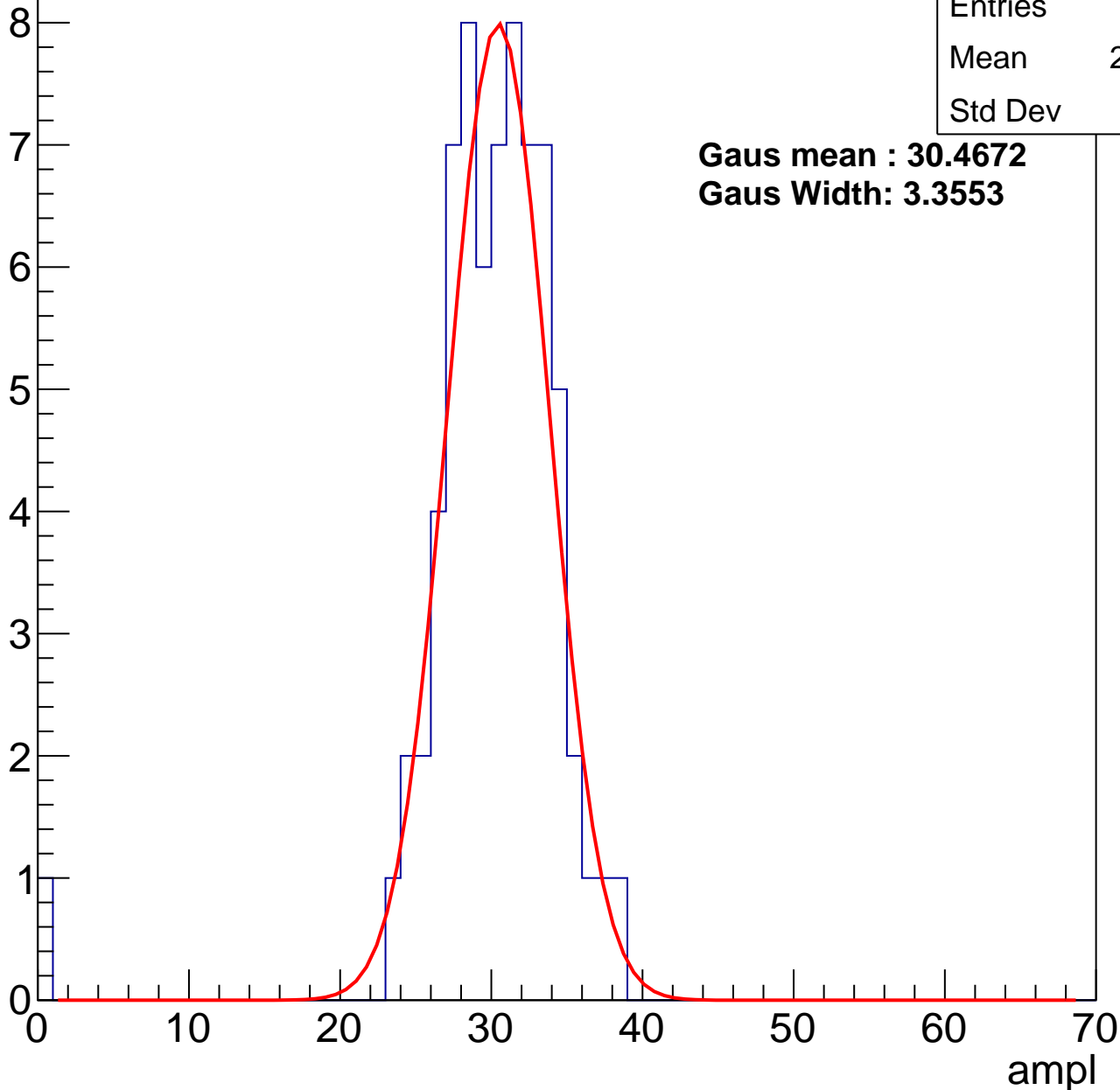
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.66
Std Dev	4.79

Gaus mean : 30.4672

Gaus Width: 3.3553



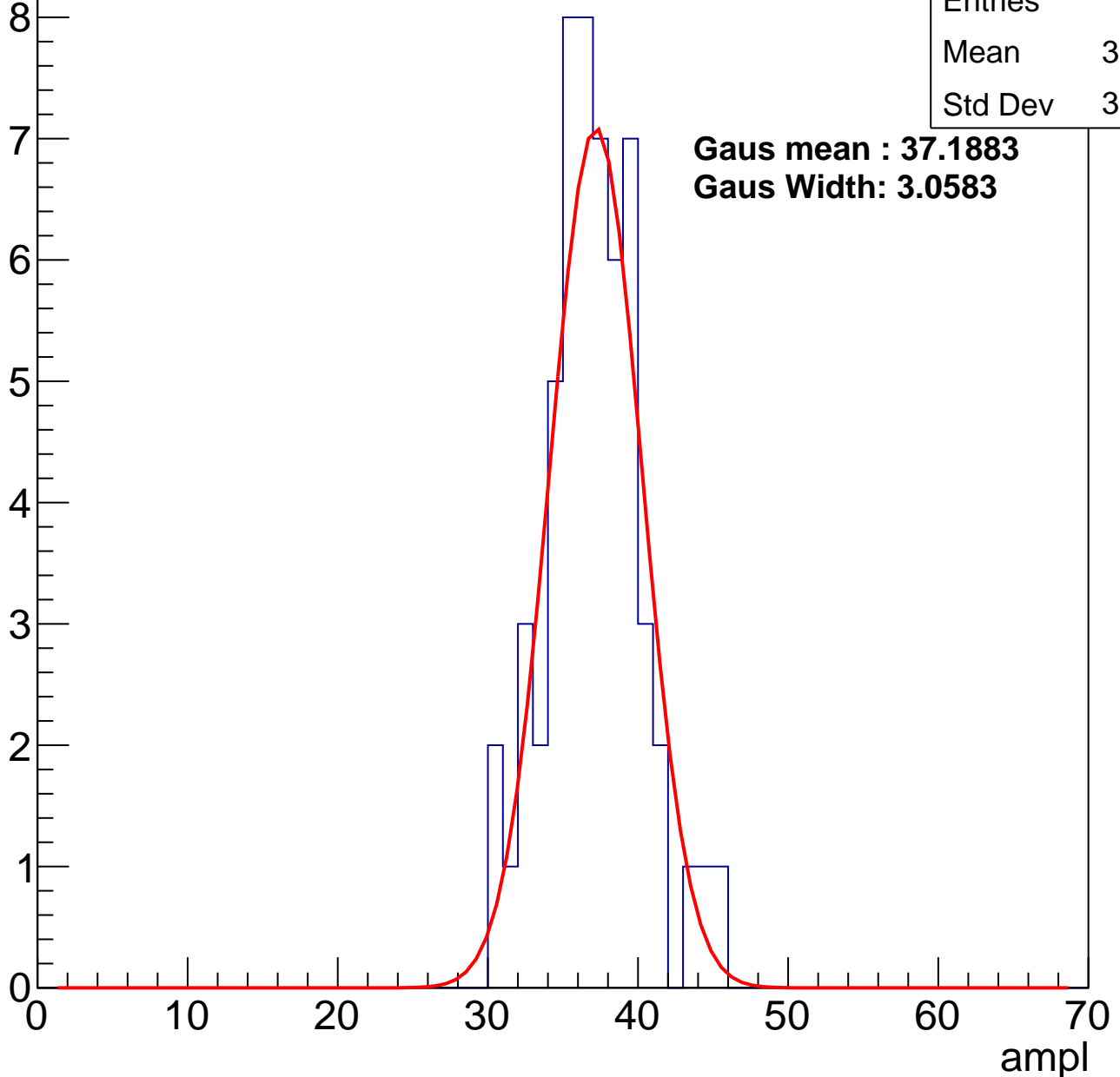
B1L003S, U19-ch70, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.58
Std Dev	3.129

Gaus mean : 37.1883
Gaus Width: 3.0583



B1L003S, U19-ch70, adc2

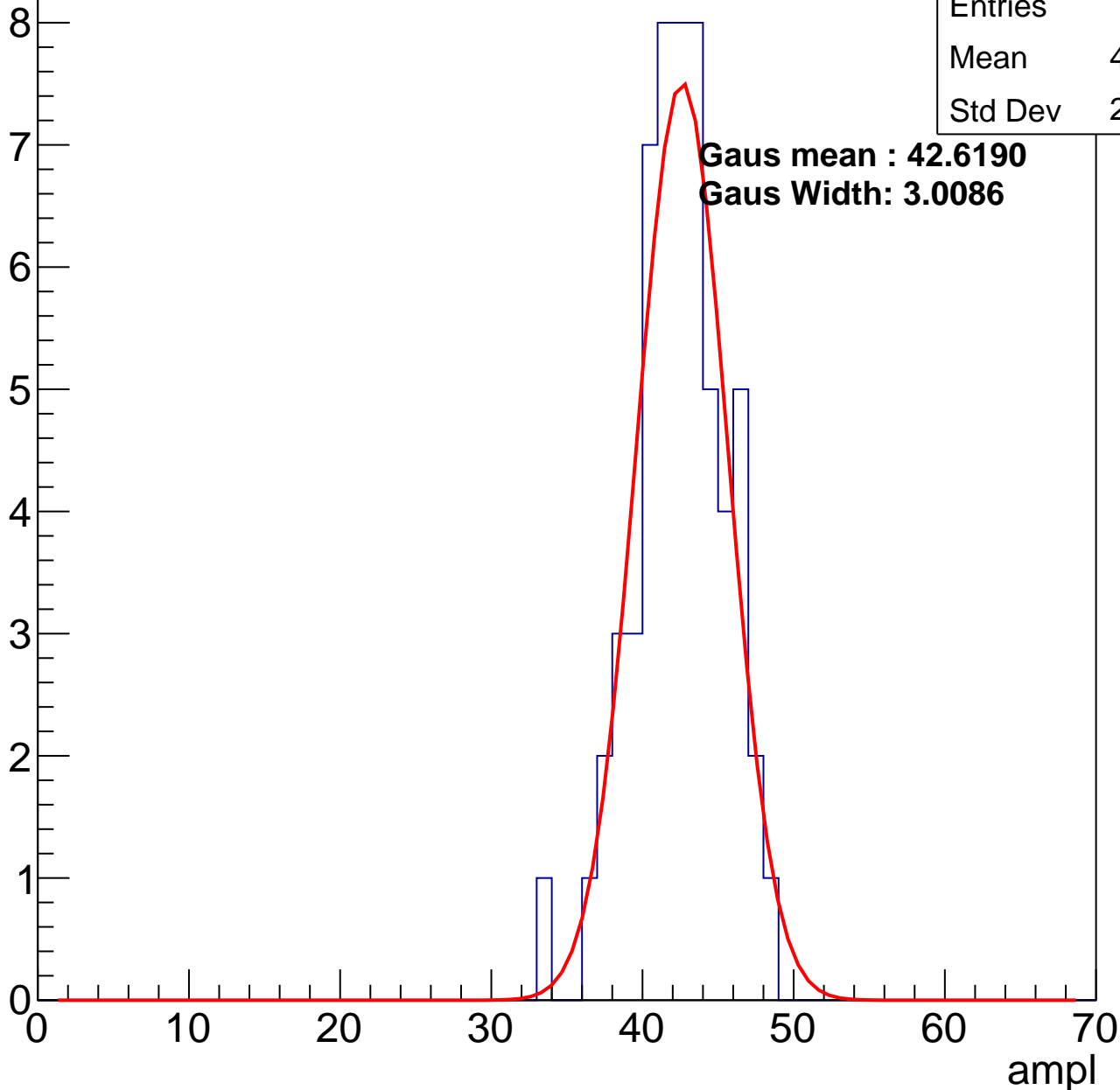
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	41.97
Std Dev	2.959

Gaus mean : 42.6190

Gaus Width: 3.0086

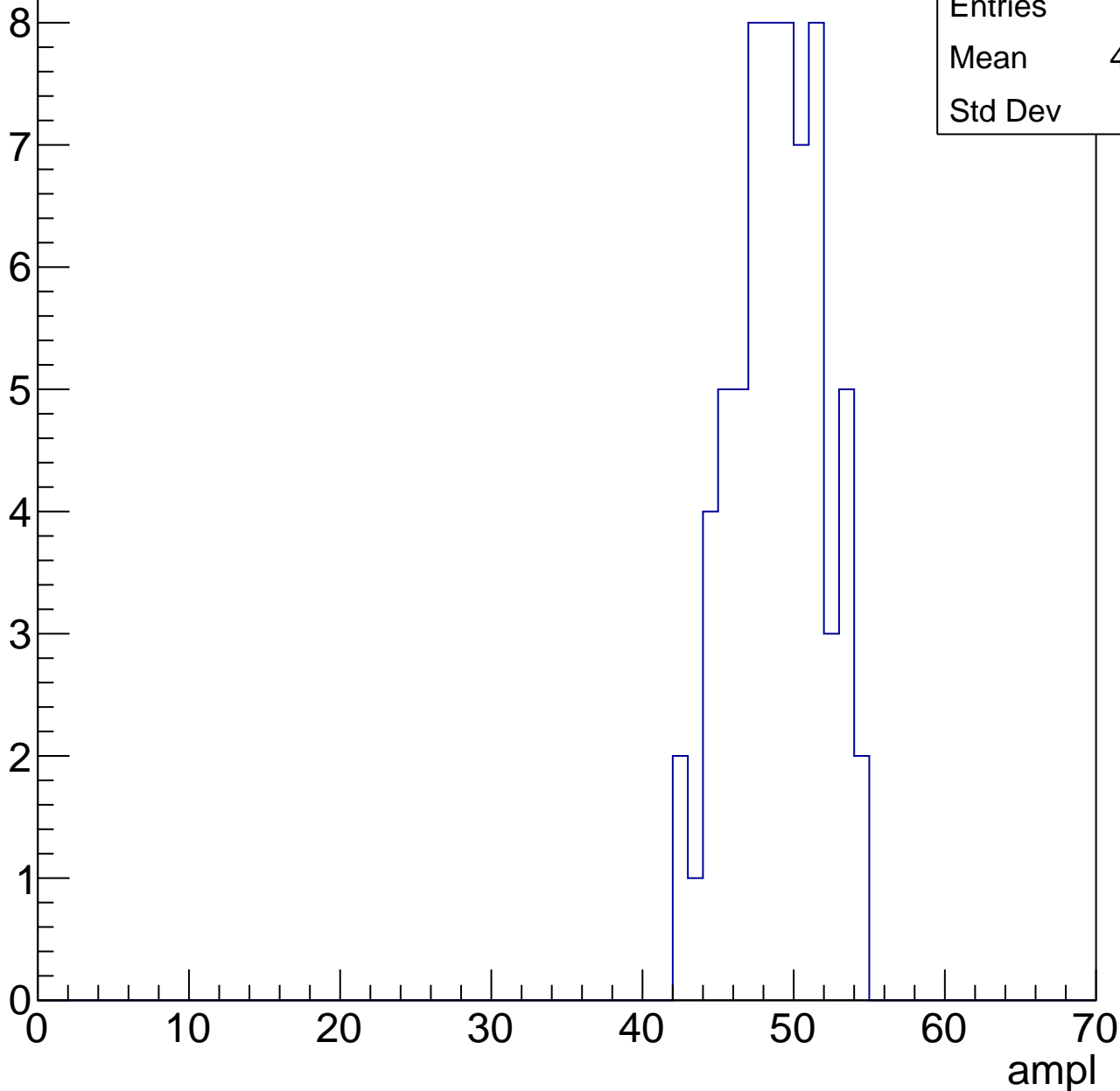


B1L003S, U19-ch70, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	48.44
Std Dev	2.95

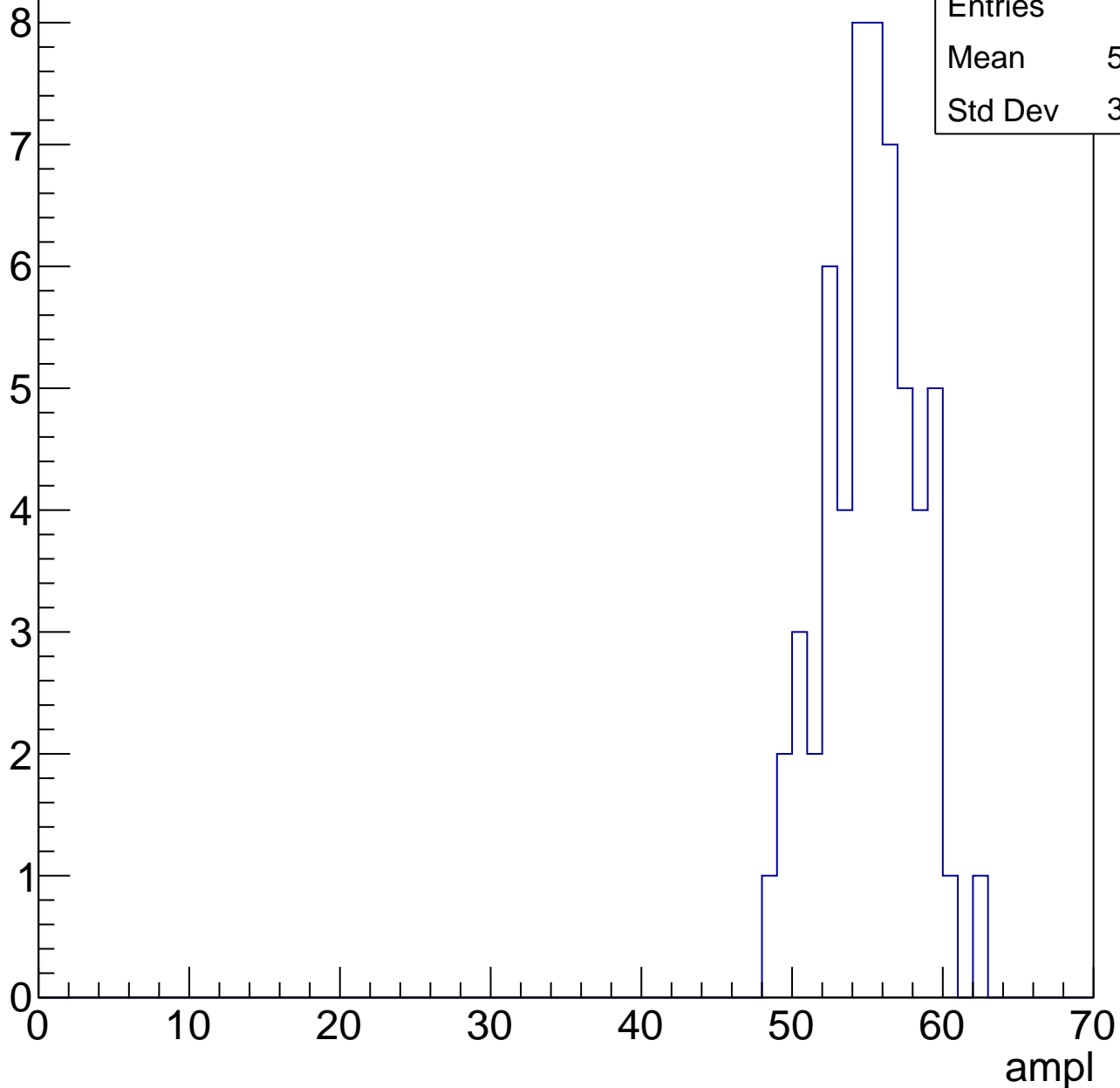


B1L003S, U19-ch70, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	54.74
Std Dev	3.018

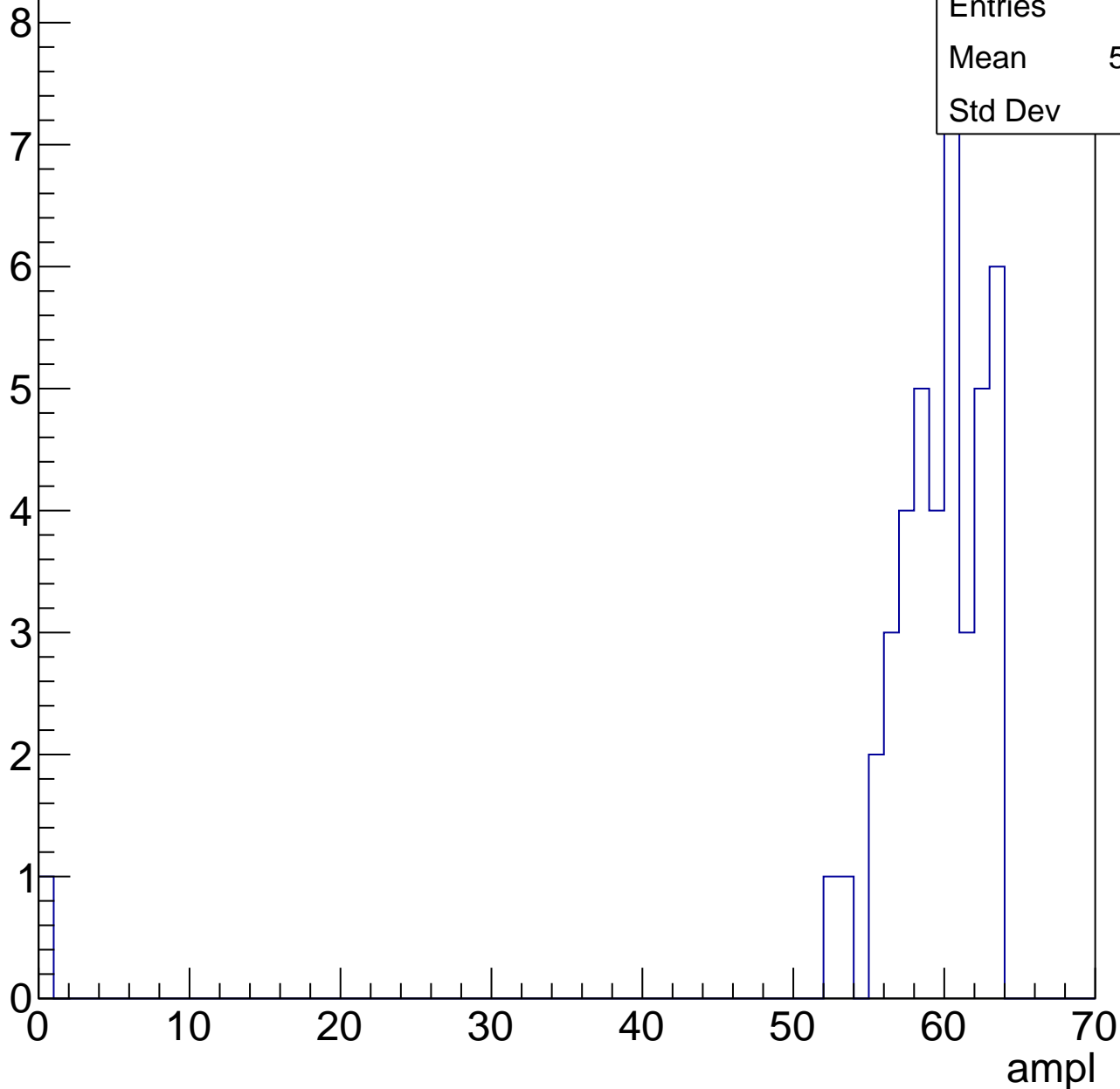


B1L003S, U19-ch70, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

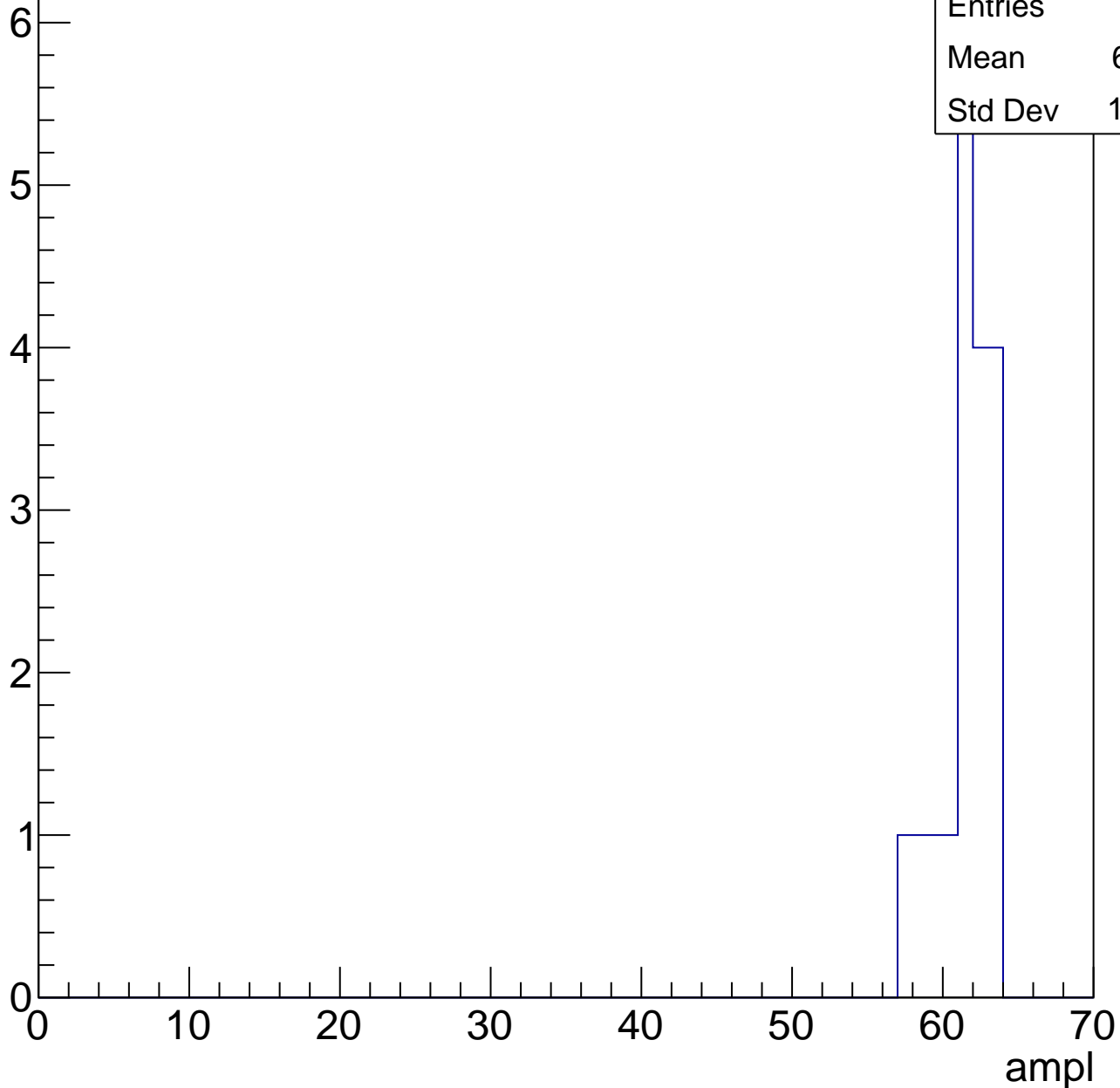
Entries	43
Mean	57.86
Std Dev	9.34



B1L003S, U19-ch70, adc6

calib_packv5_042523_0143.root, FC#13, port D2

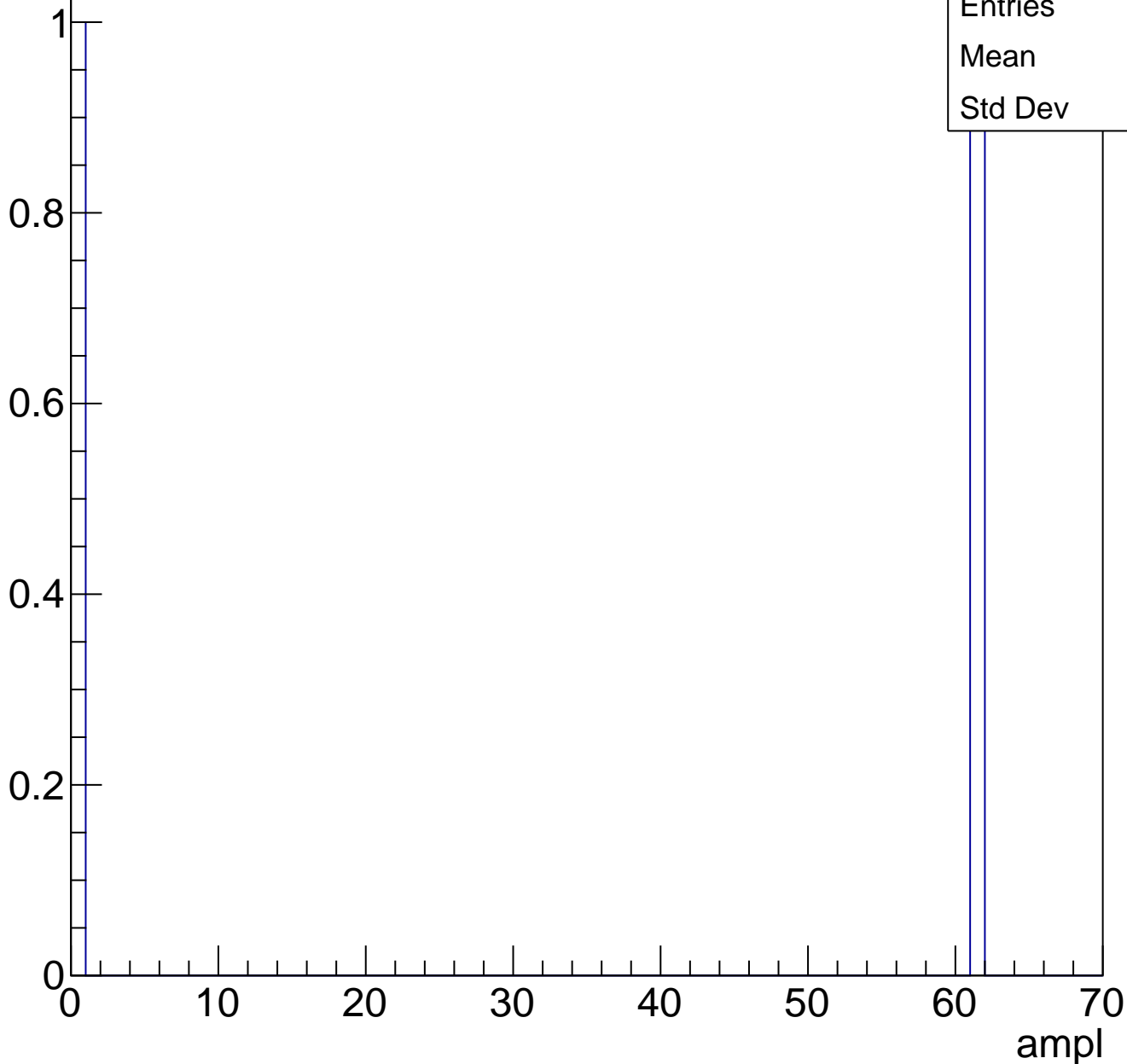
Entry



B1L003S, U19-ch70, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch71, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	59
Mean	28.81
Std Dev	6.22

Gaus mean : 29.9441

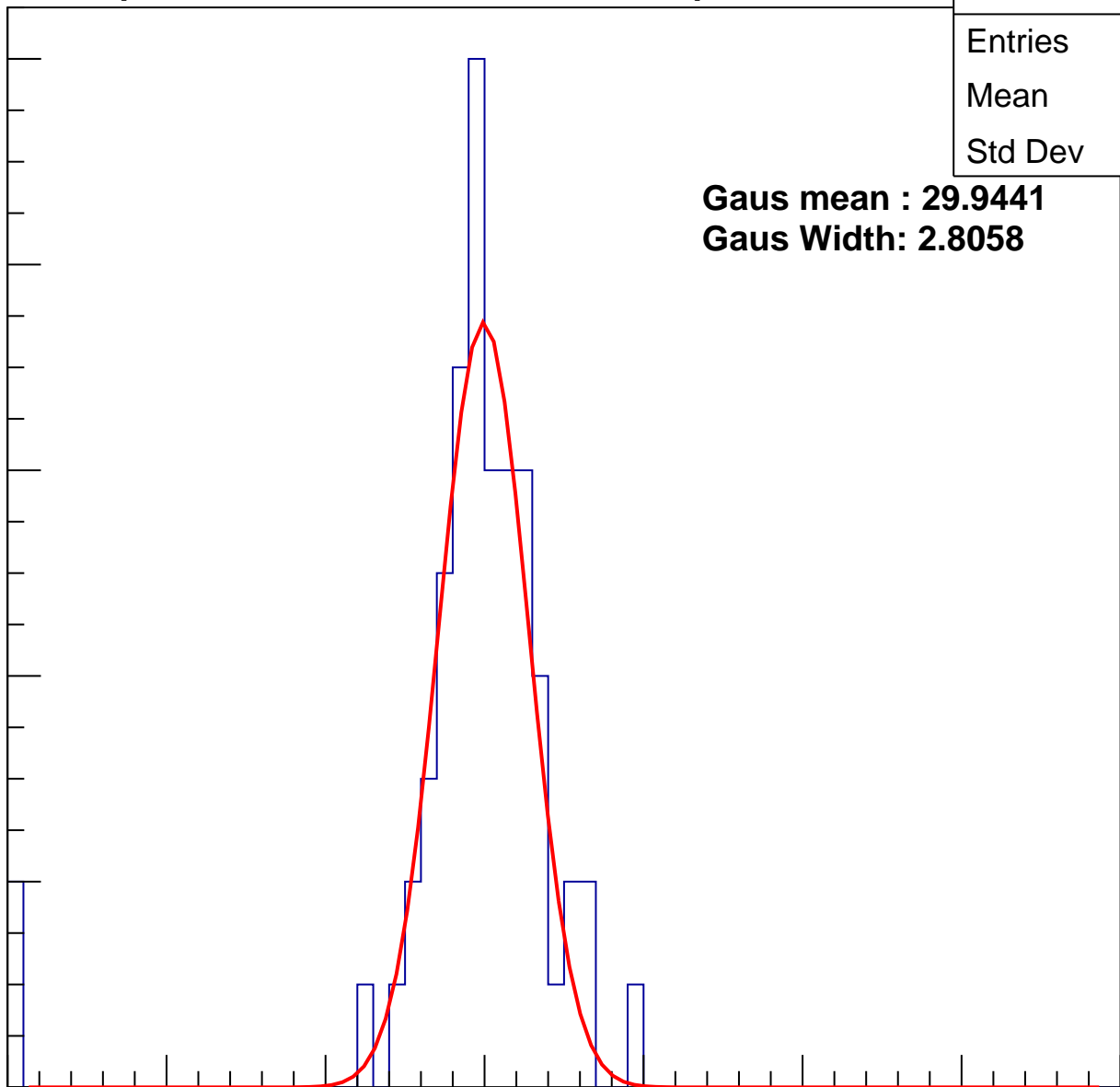
Gaus Width: 2.8058

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



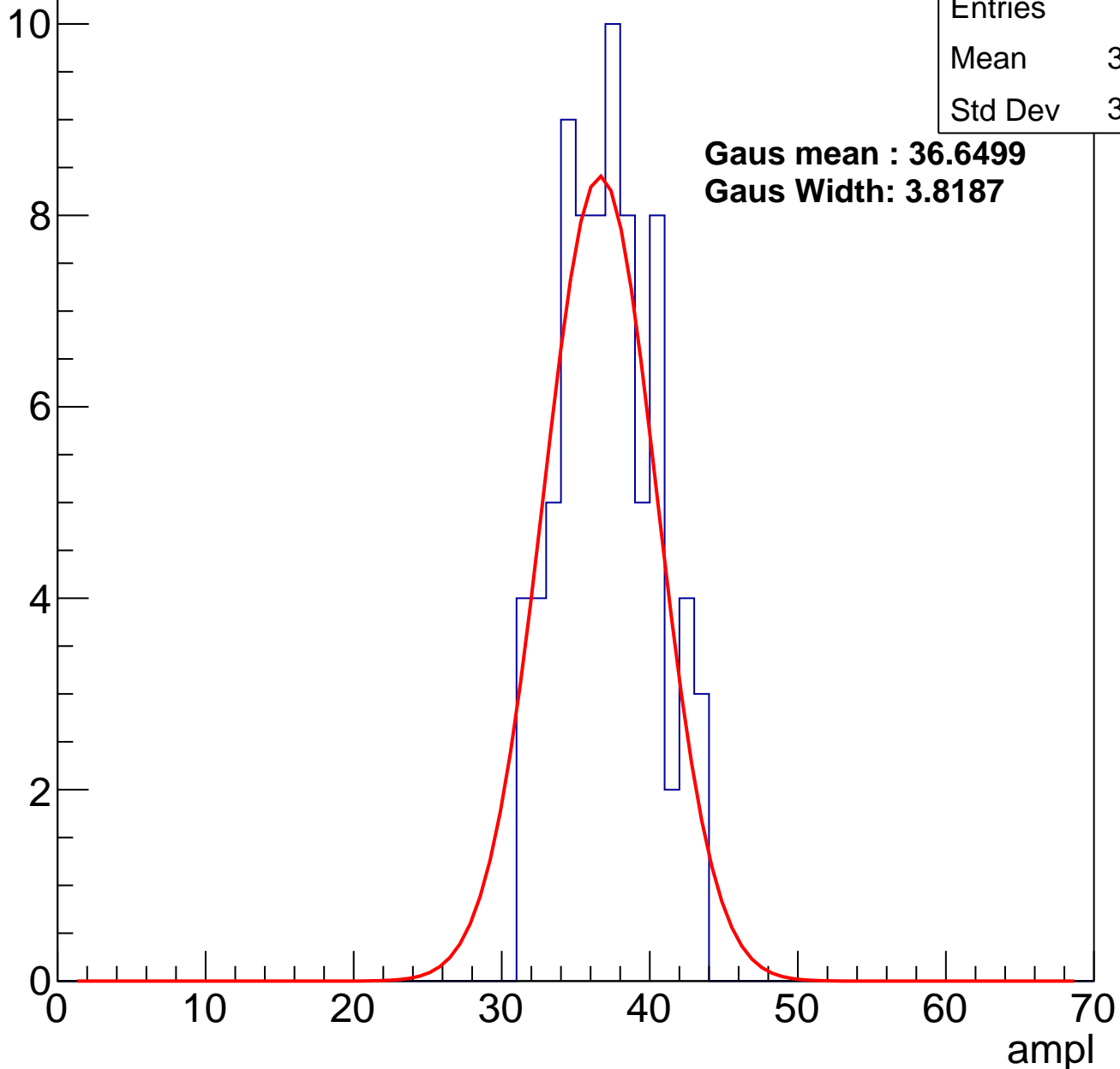
B1L003S, U19-ch71, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	36.65
Std Dev	3.153

Gaus mean : 36.6499
Gaus Width: 3.8187

Entry



B1L003S, U19-ch71, adc2

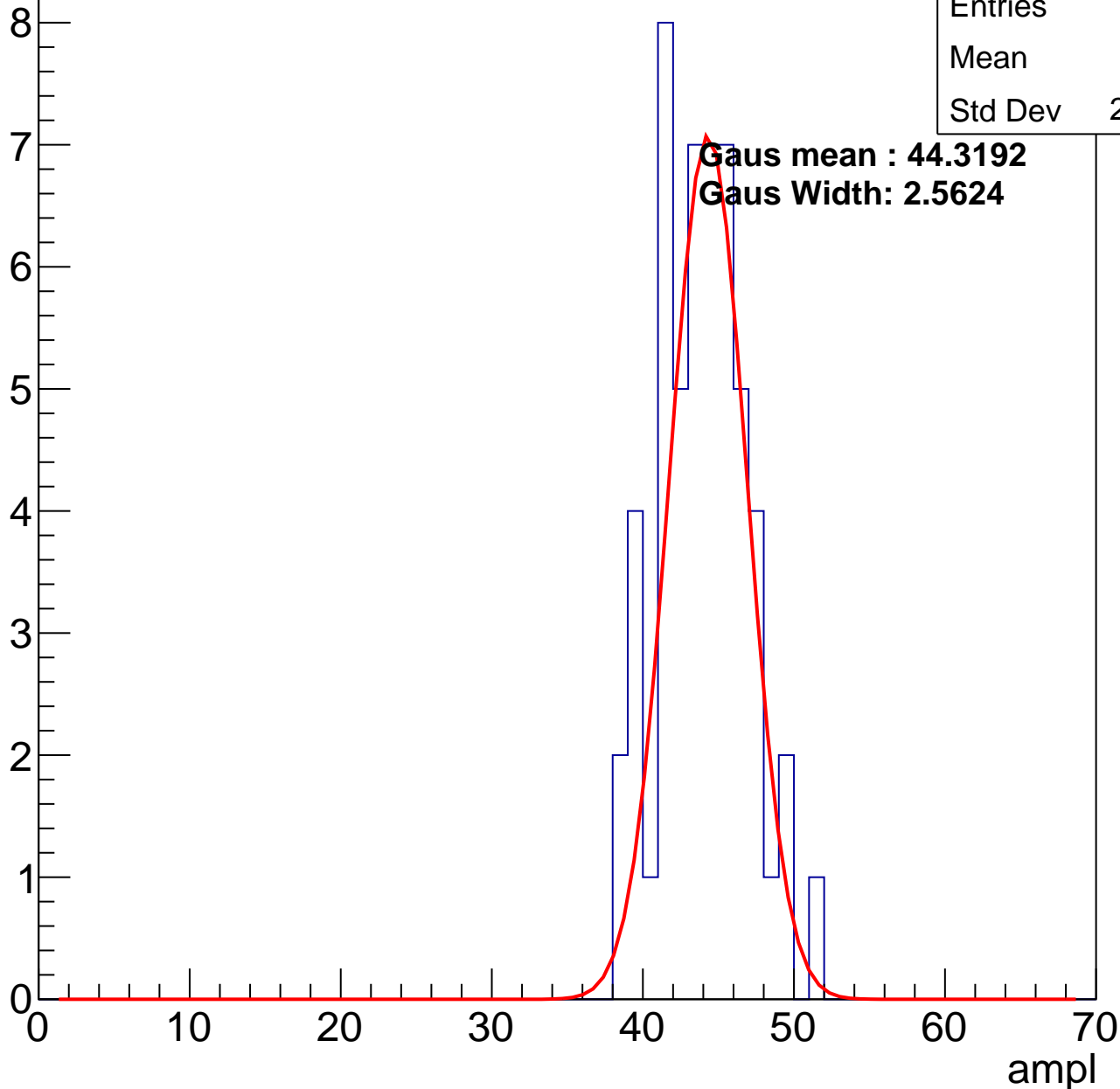
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.5
Std Dev	2.892

Gaus mean : 44.3192

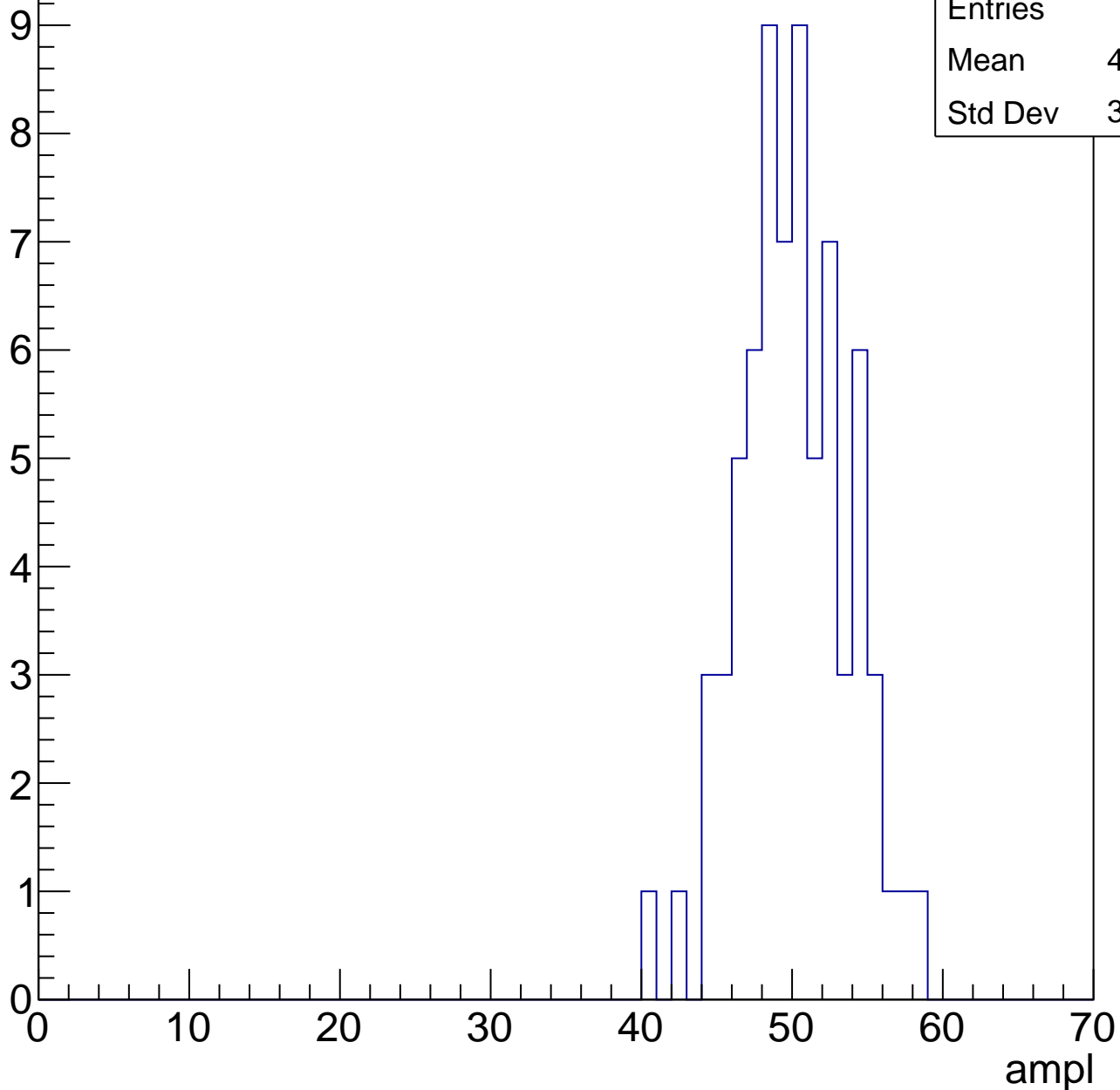
Gaus Width: 2.5624



B1L003S, U19-ch71, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch71, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

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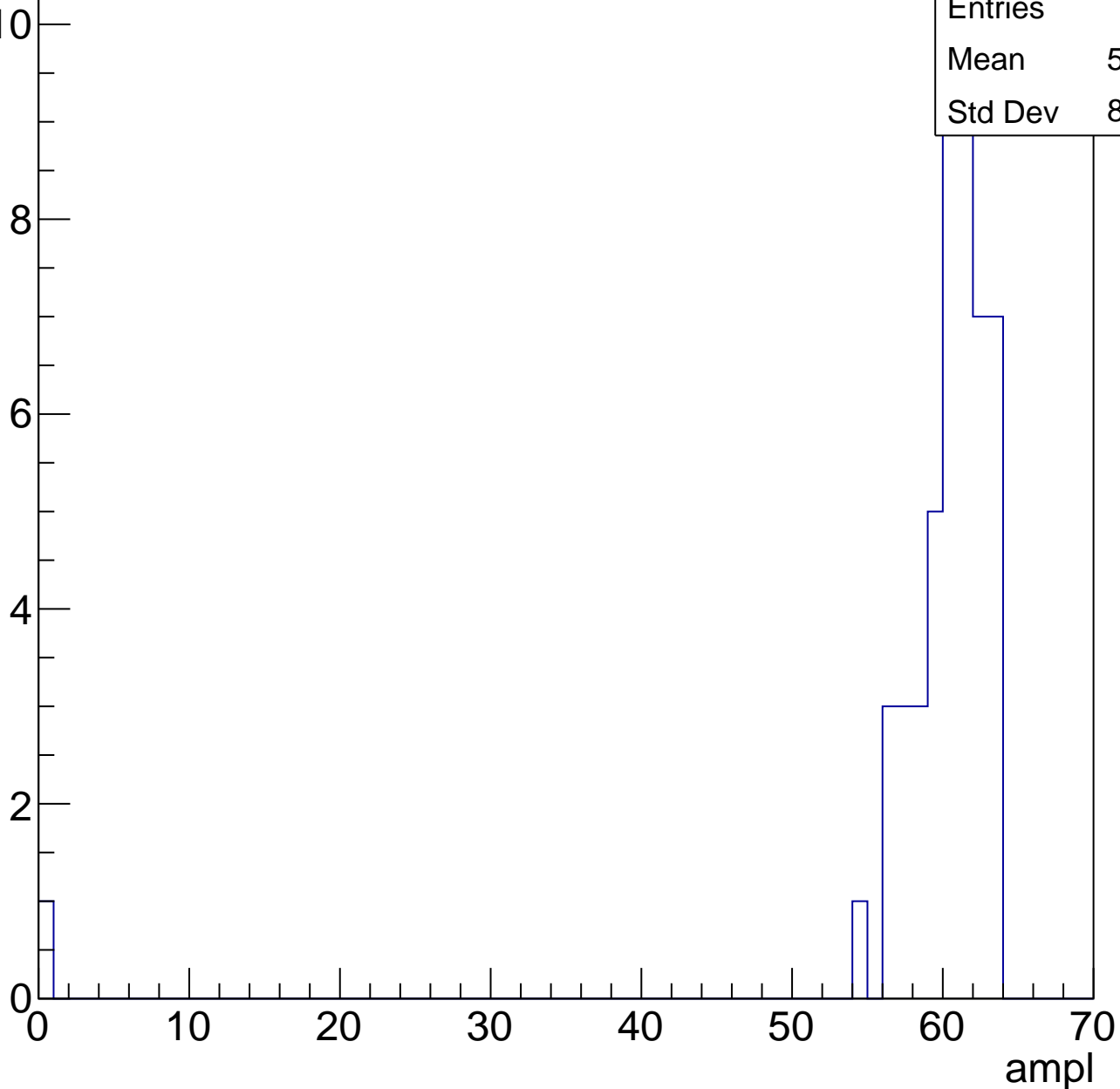
336

B1L003S, U19-ch71, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.92
Std Dev	8.773



B1L003S, U19-ch71, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch71, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch72, adc0

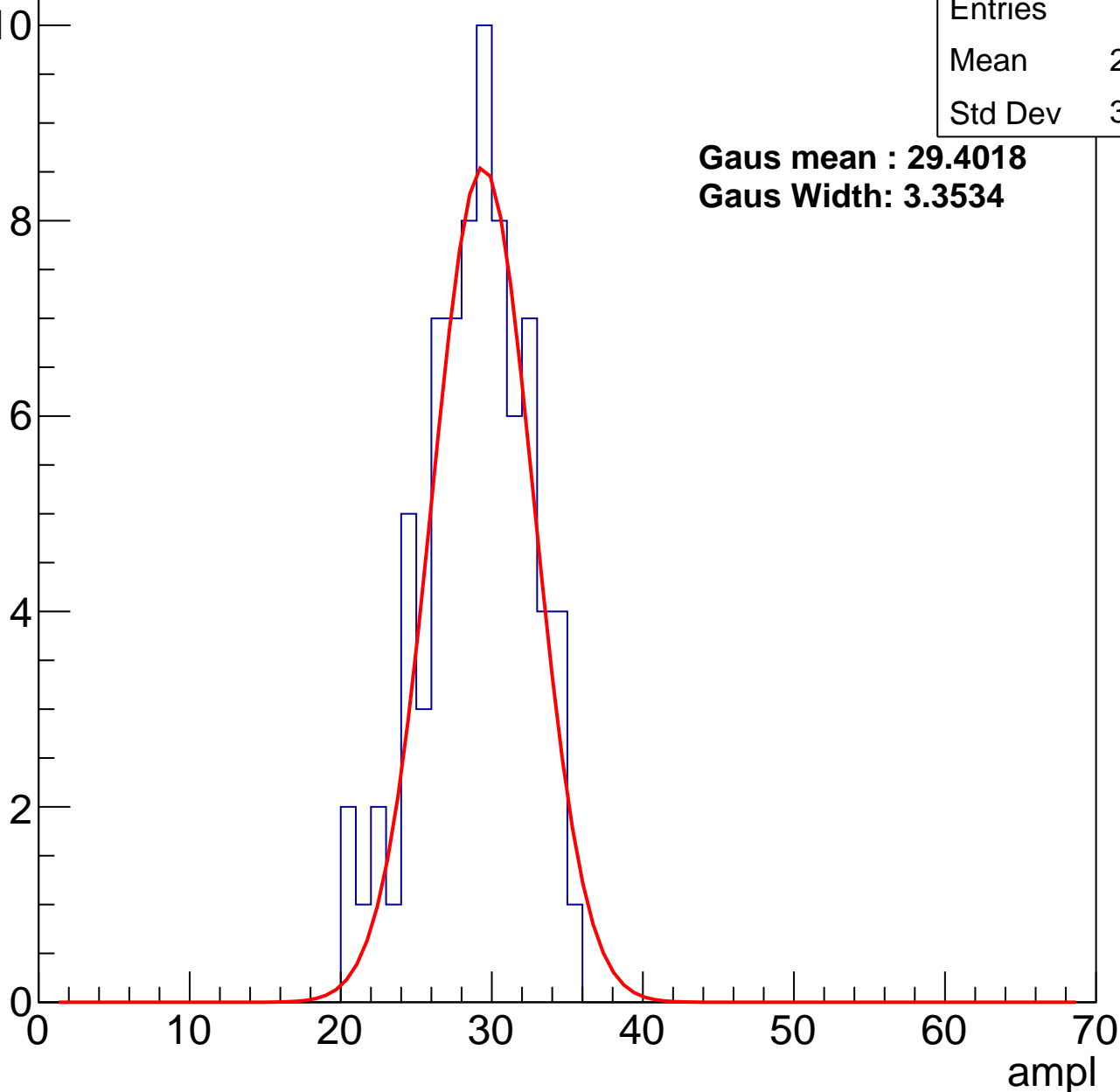
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	28.43
Std Dev	3.446

Gaus mean : 29.4018

Gaus Width: 3.3534



B1L003S, U19-ch72, adc1

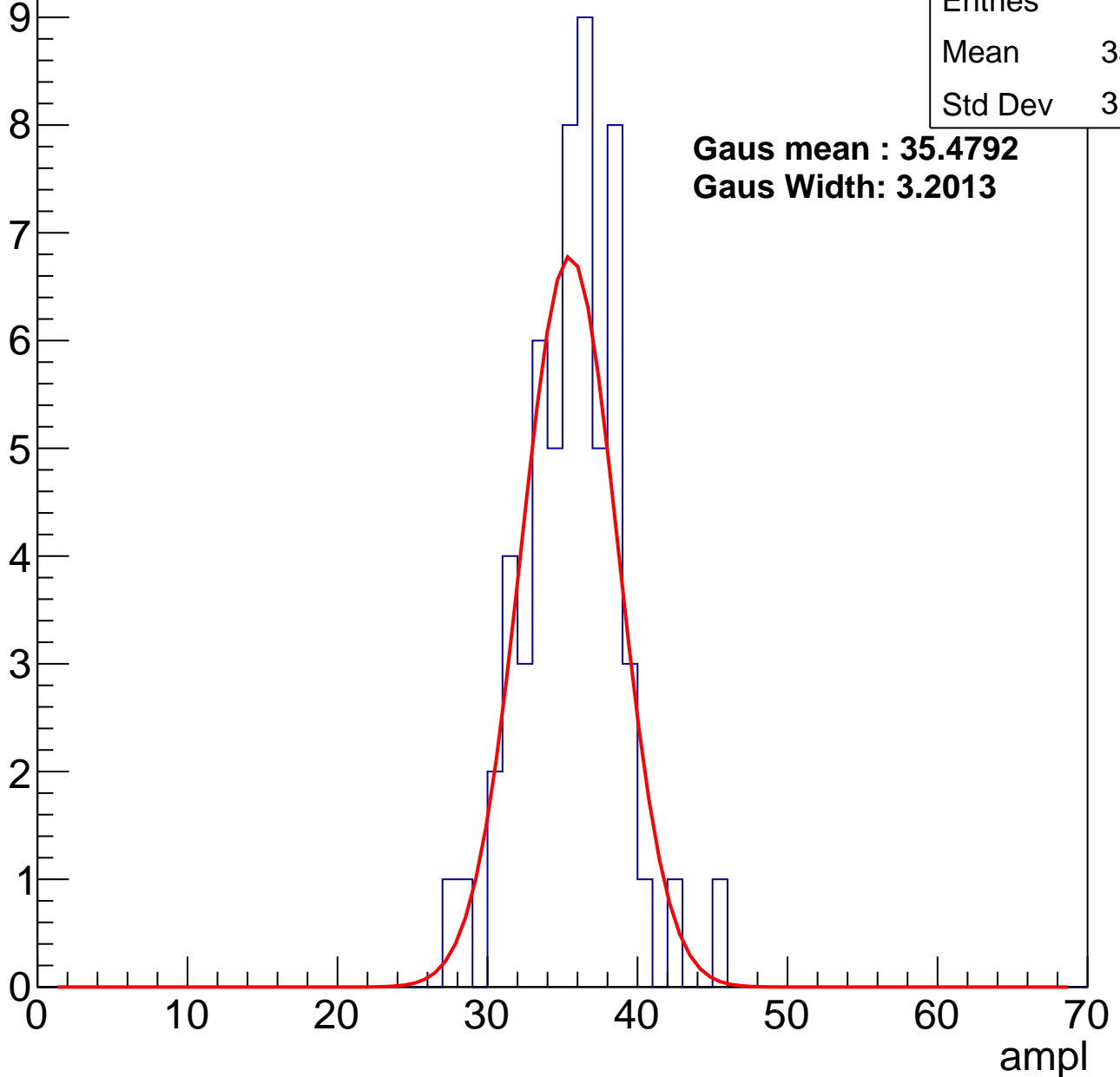
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	35.17
Std Dev	3.228

Gaus mean : 35.4792

Gaus Width: 3.2013



B1L003S, U19-ch72, adc2

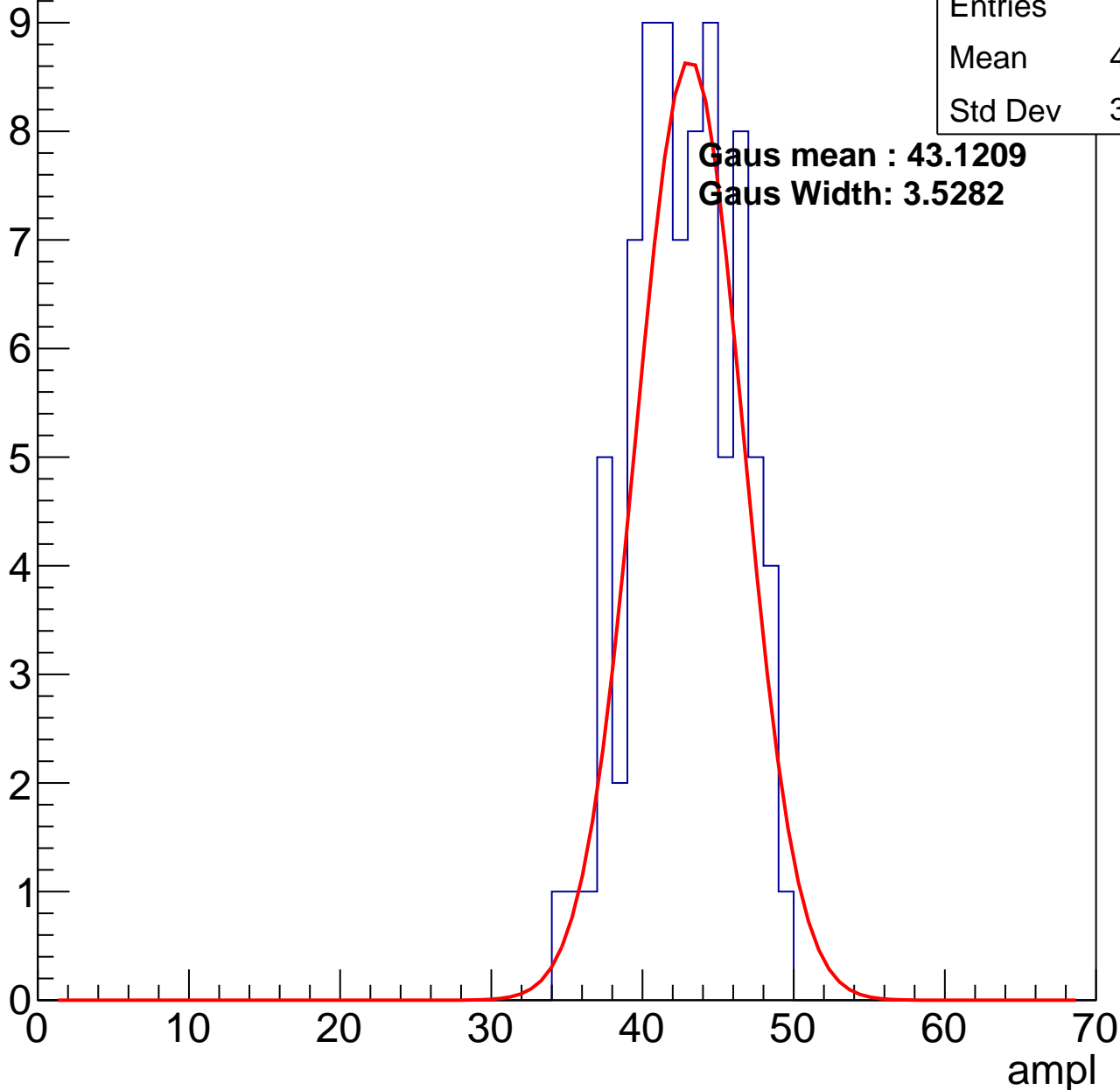
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	42.33
Std Dev	3.386

Gaus mean : 43.1209

Gaus Width: 3.5282

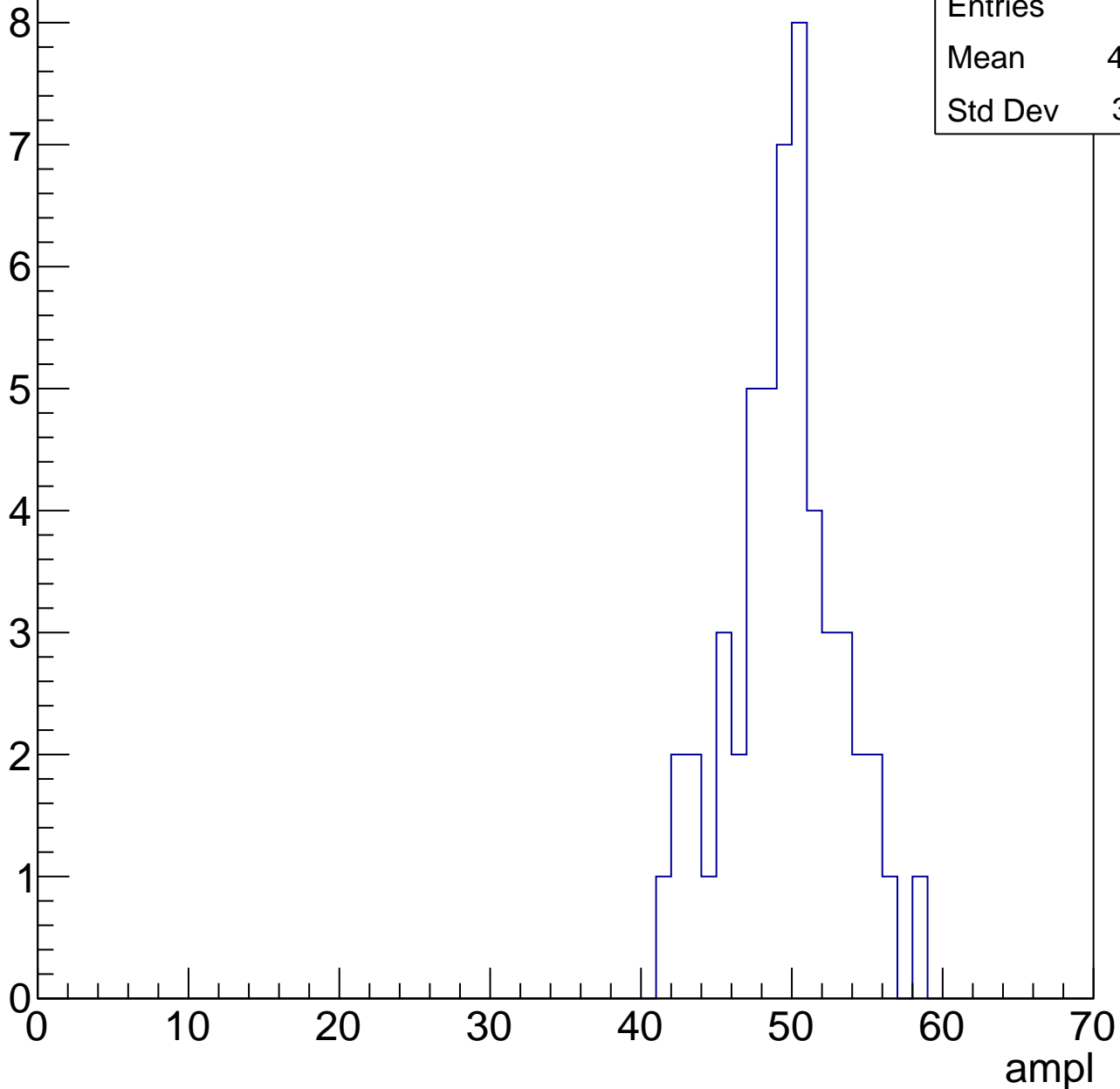


B1L003S, U19-ch72, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	49.06
Std Dev	3.671

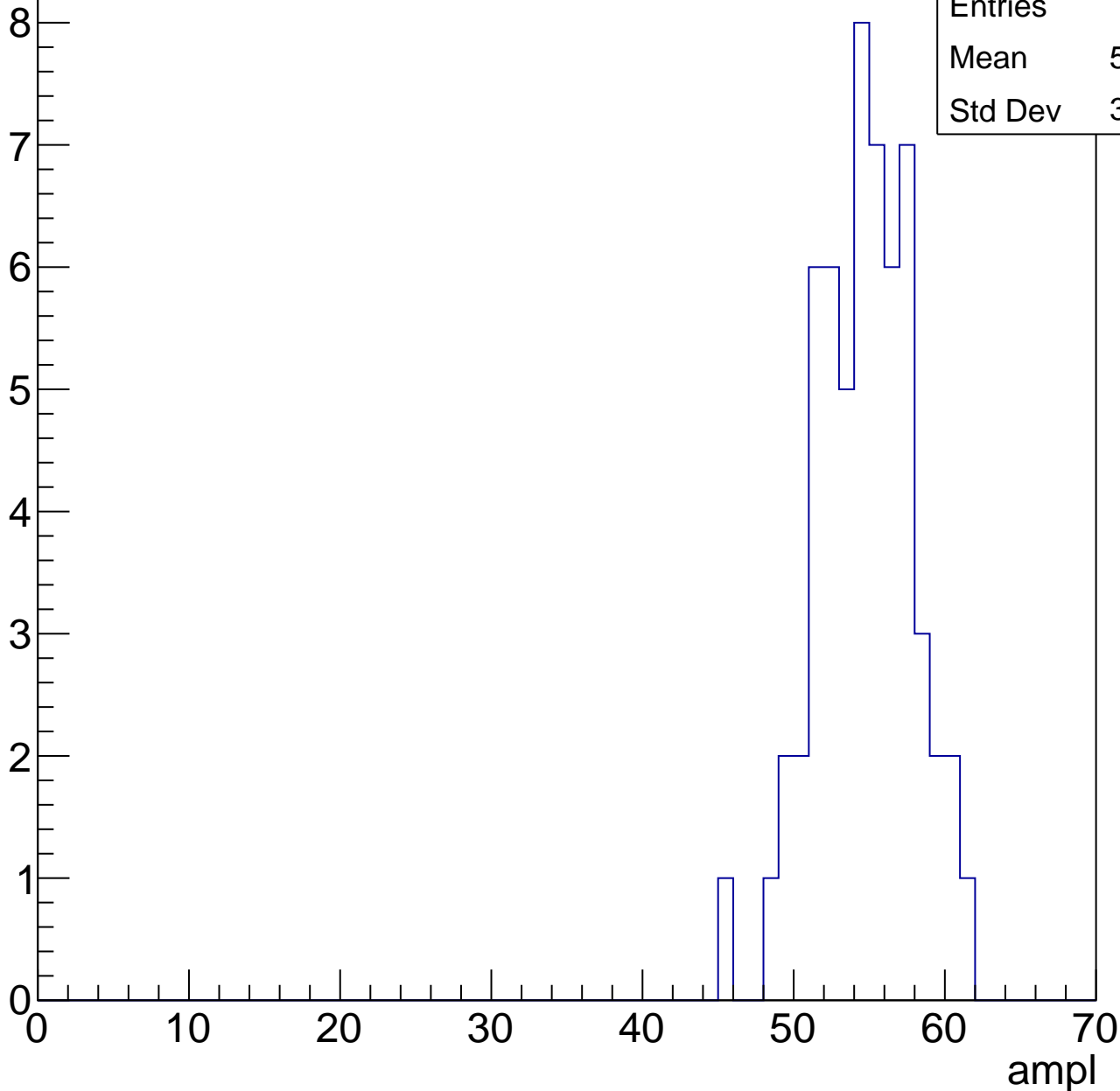


B1L003S, U19-ch72, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.22
Std Dev	3.168

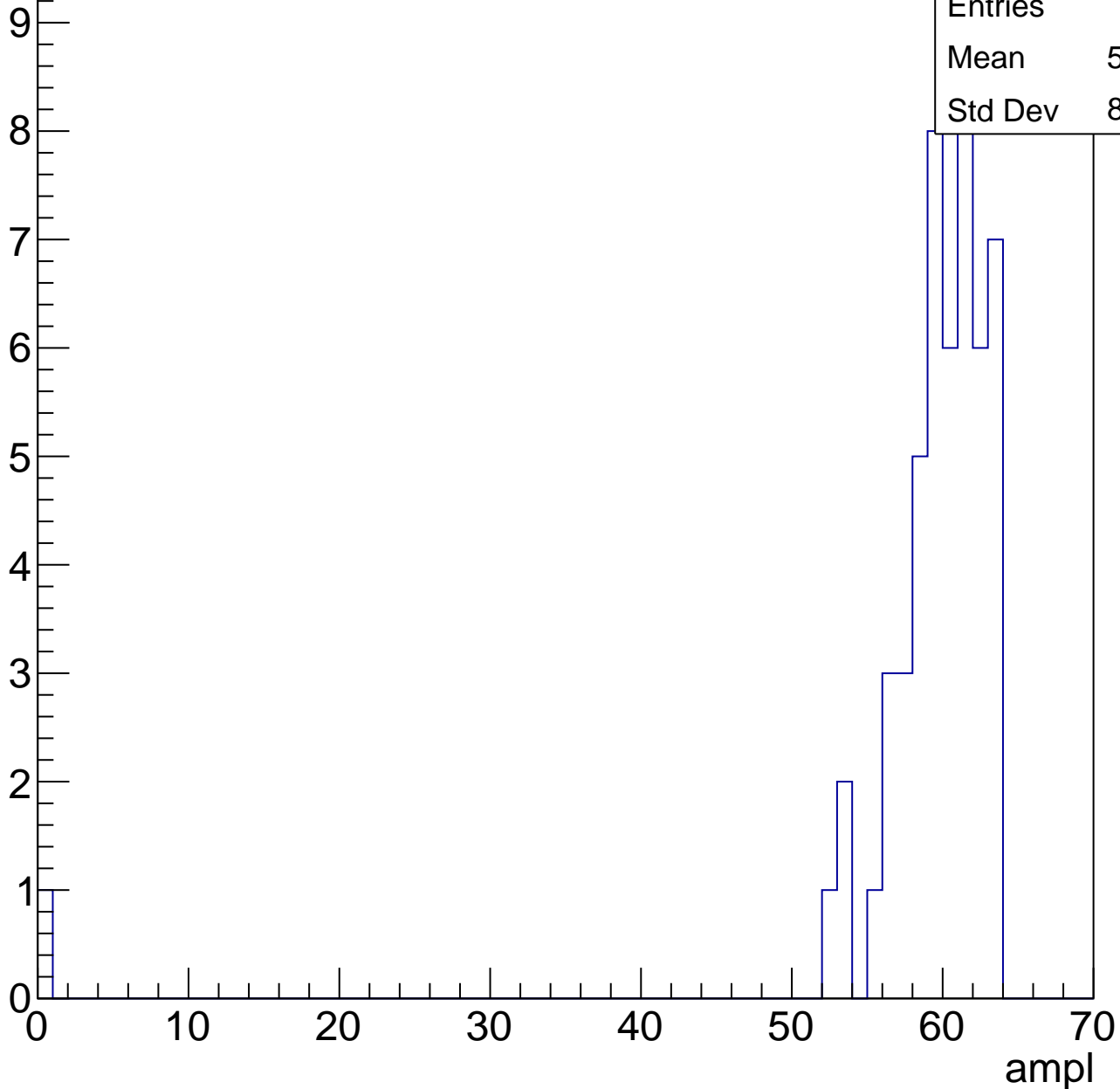


B1L003S, U19-ch72, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

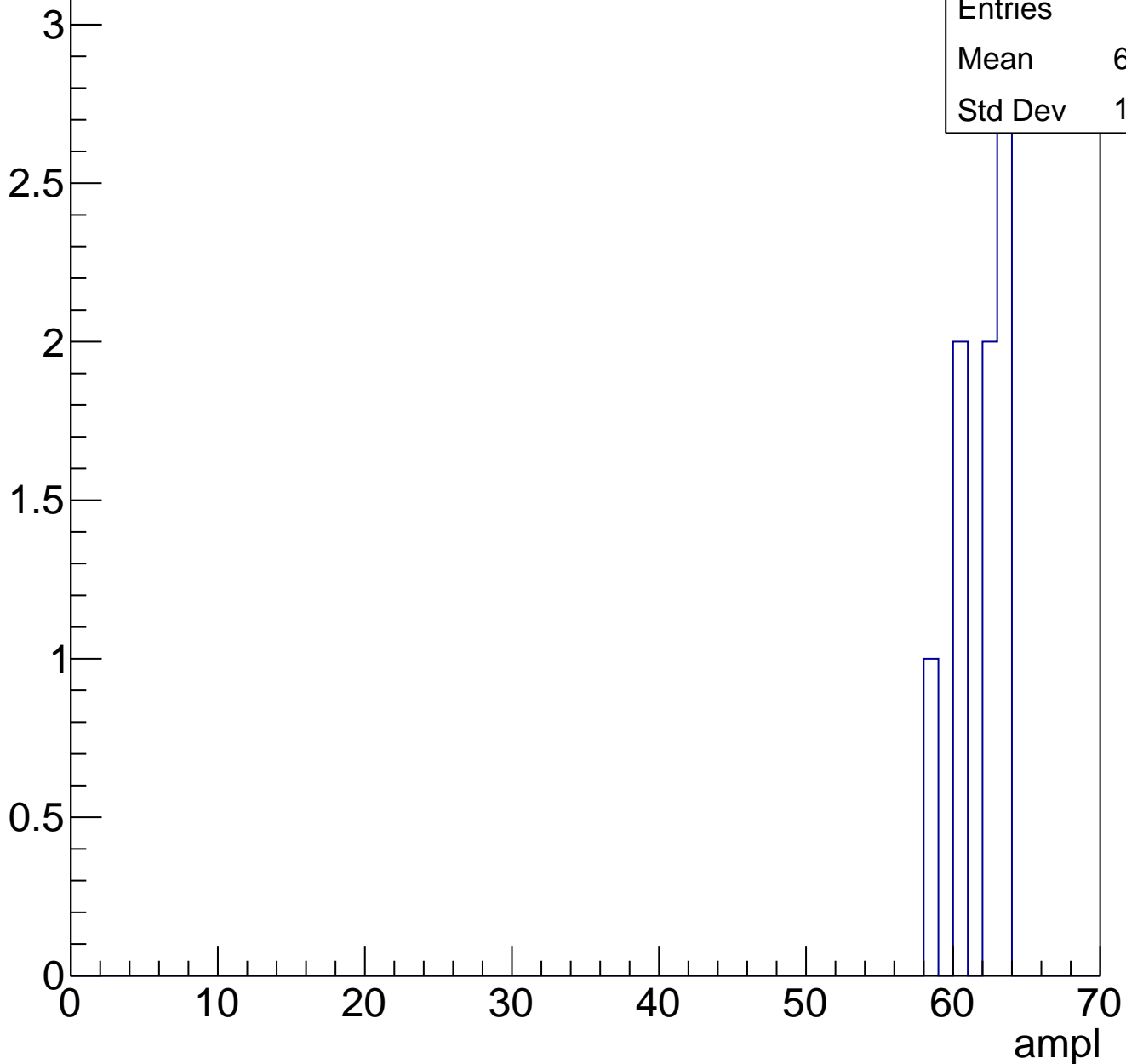
Entries	52
Mean	58.38
Std Dev	8.605



B1L003S, U19-ch72, adc6

calib_packv5_042523_0143.root, FC#13, port D2

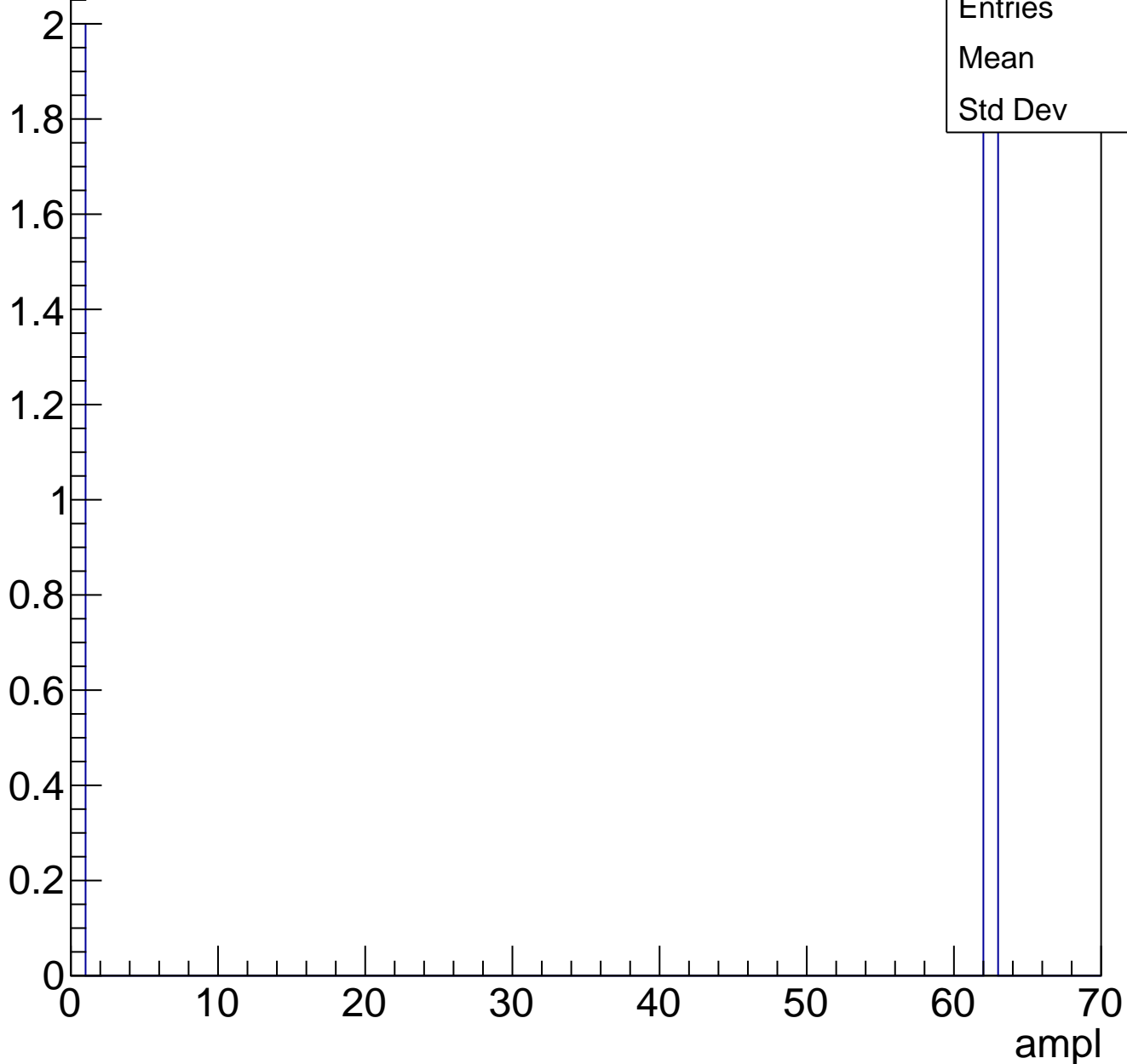
Entry



B1L003S, U19-ch72, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch73, adc0

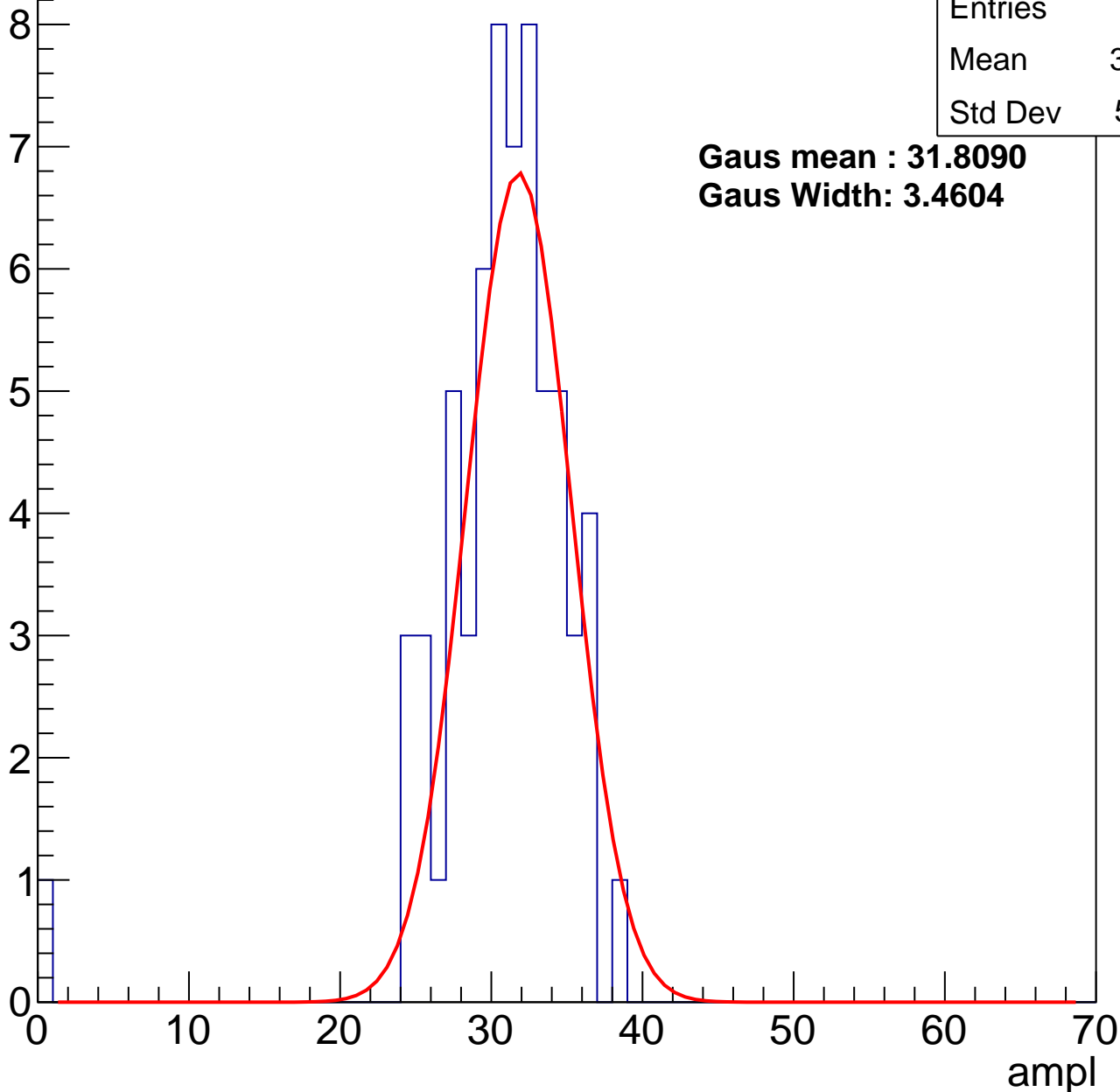
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30.17
Std Dev	5.051

Gaus mean : 31.8090

Gaus Width: 3.4604



B1L003S, U19-ch73, adc1

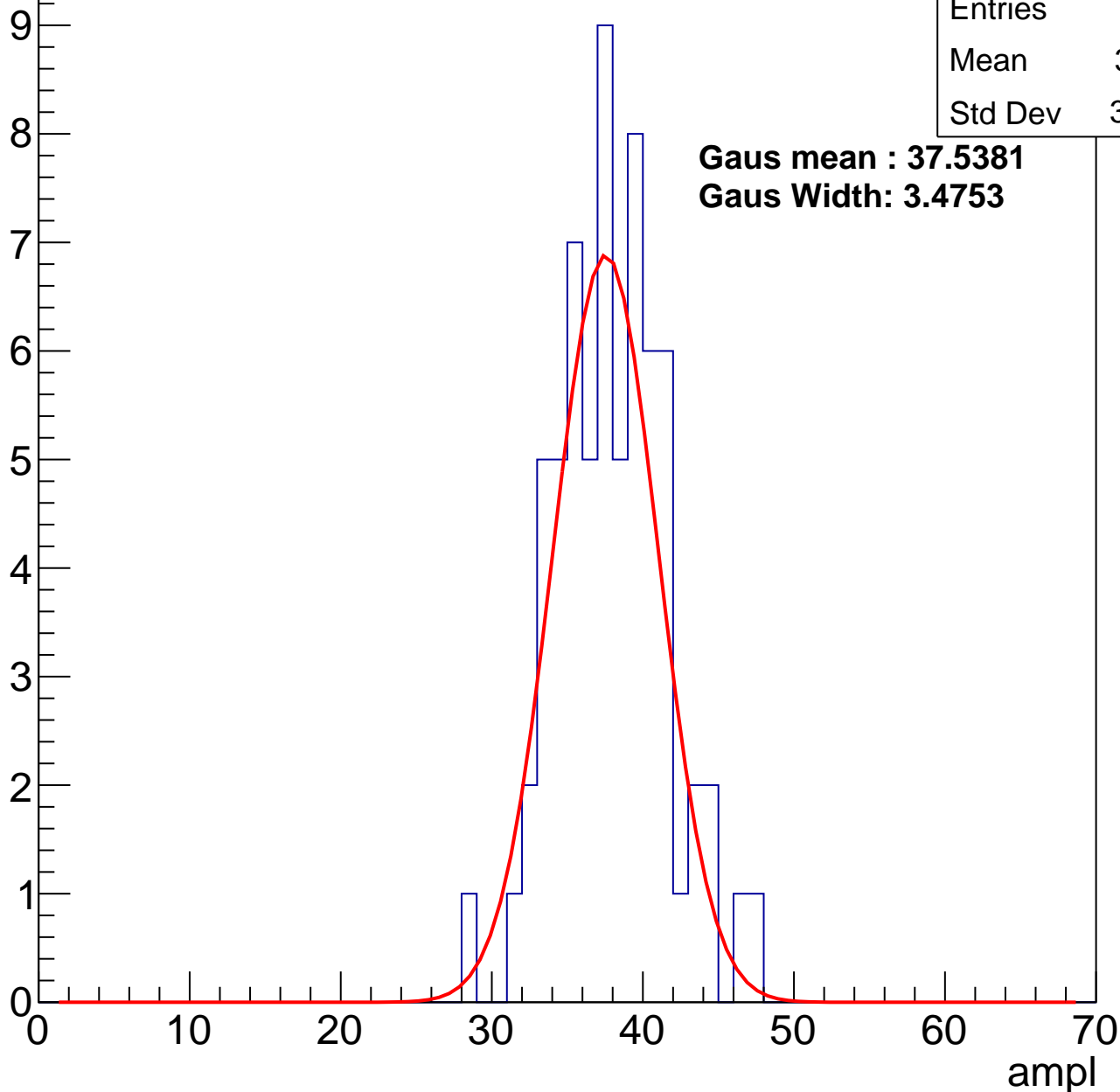
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.51
Std Dev	3.605

Gaus mean : 37.5381

Gaus Width: 3.4753



B1L003S, U19-ch73, adc2

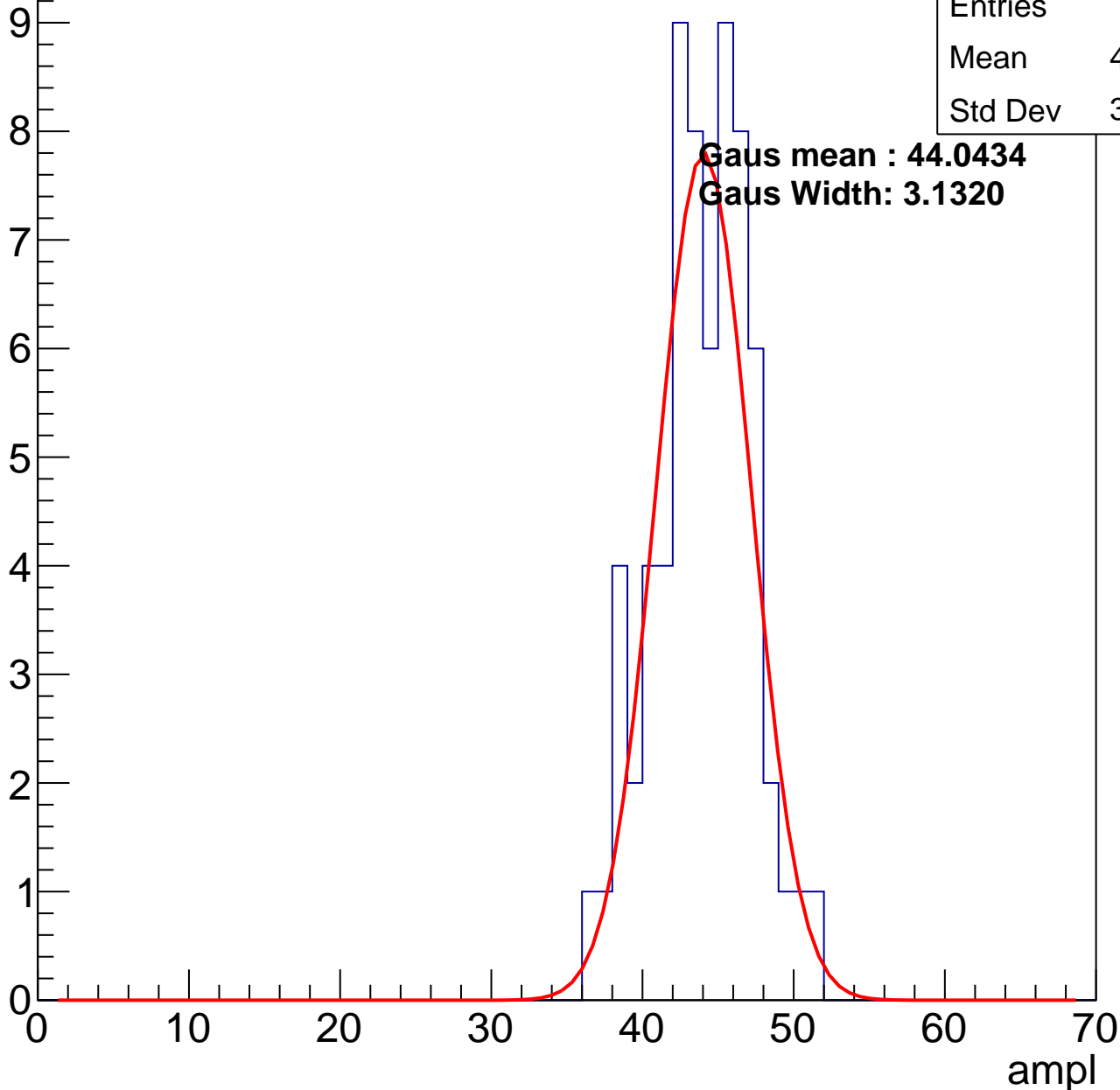
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	43.49
Std Dev	3.164

Gaus mean : 44.0434

Gaus Width: 3.1320

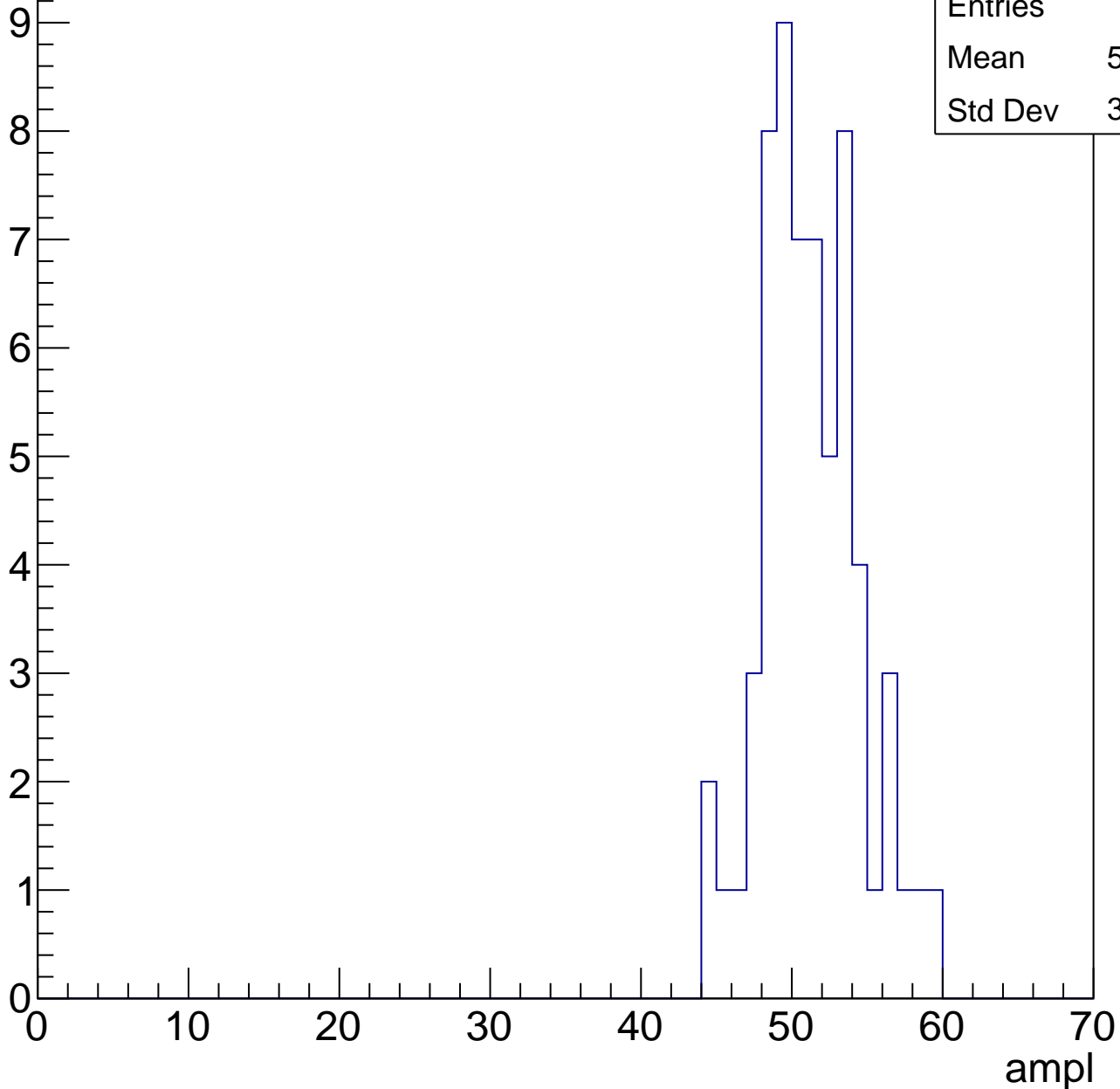


B1L003S, U19-ch73, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

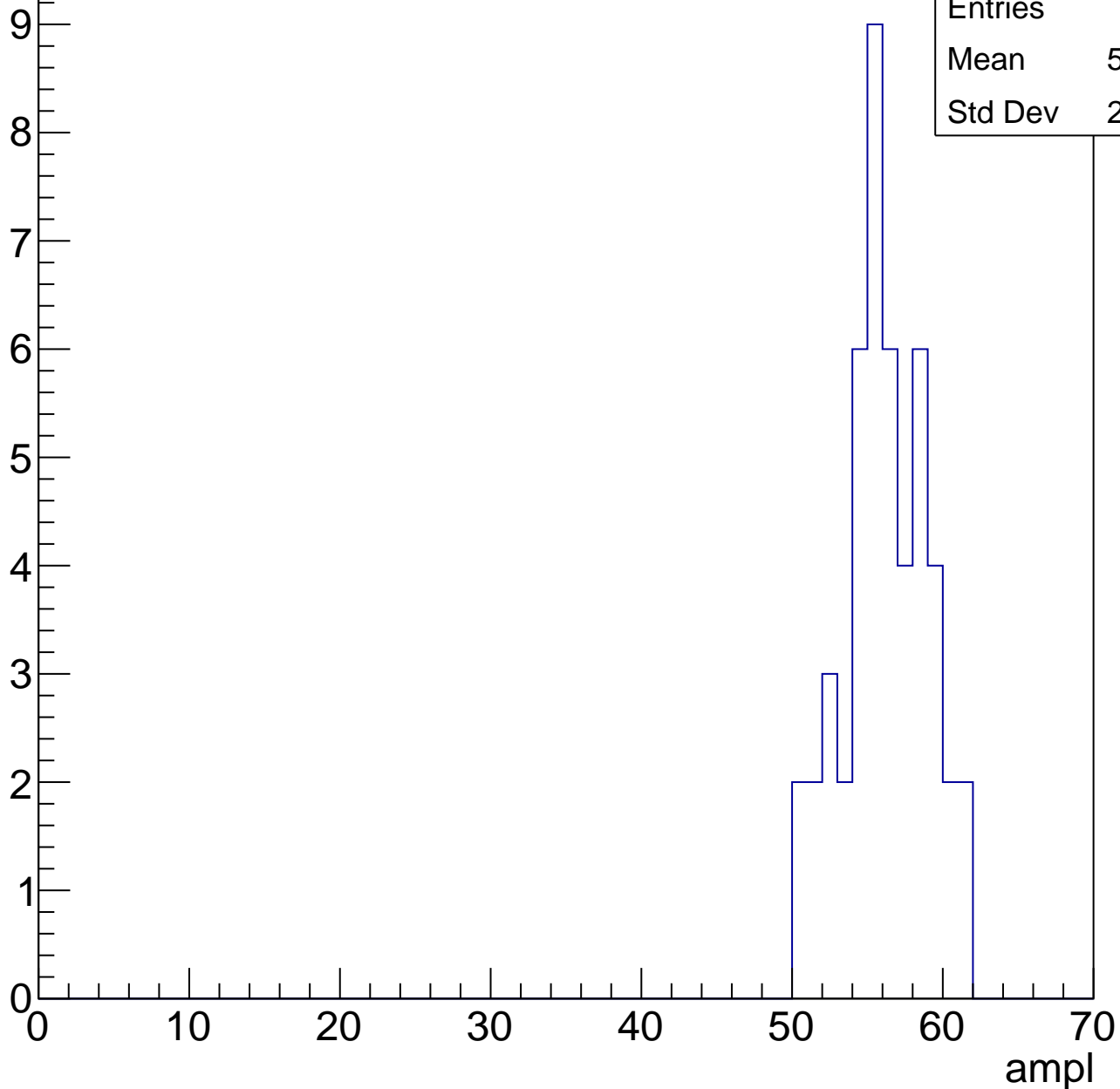
Entries	62
Mean	50.79
Std Dev	3.193



B1L003S, U19-ch73, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

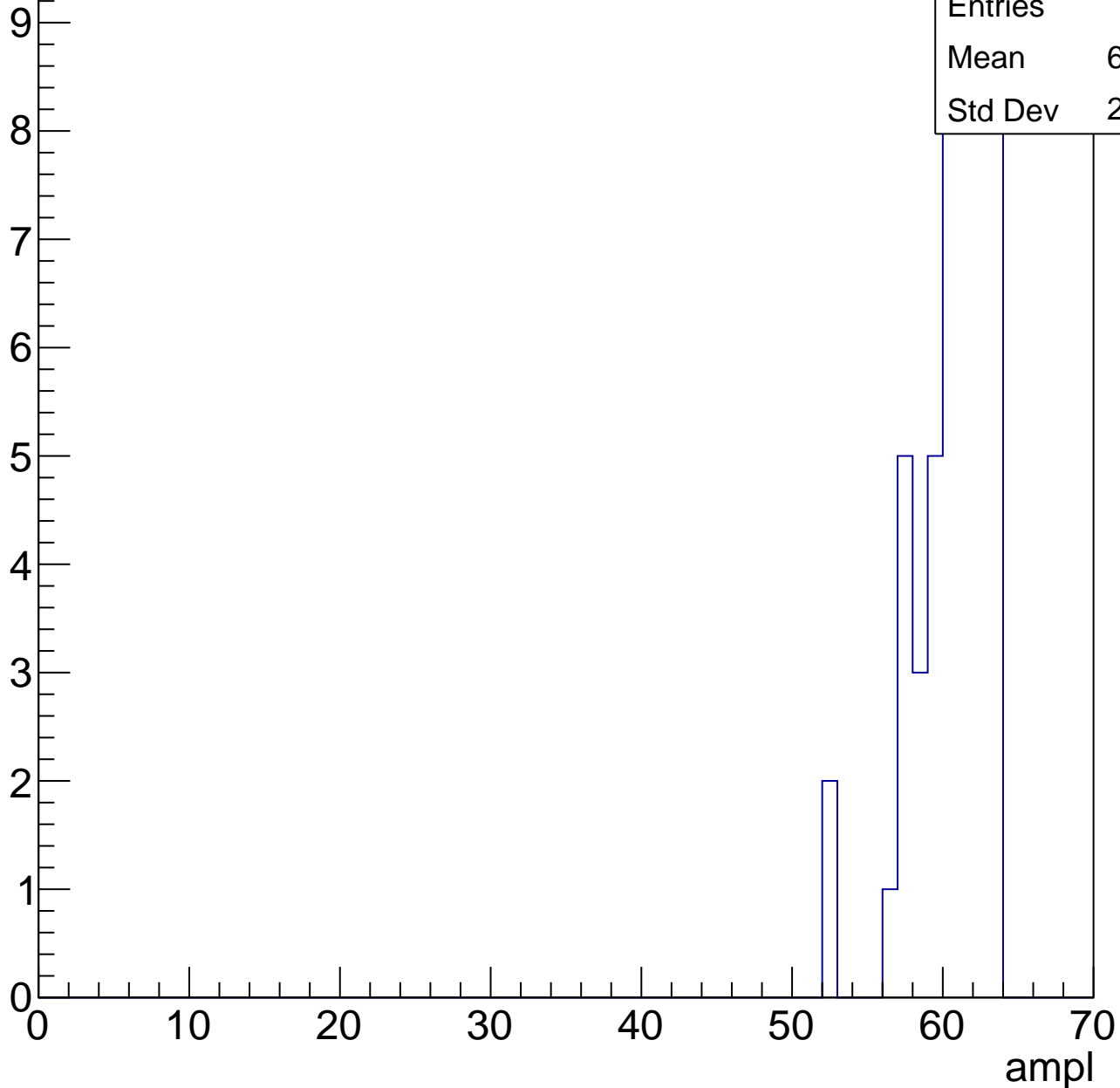


Entries	48
Mean	55.69
Std Dev	2.747

B1L003S, U19-ch73, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch73, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch73, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch74, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	28.62
Std Dev	3.534

Gaus mean : 29.2253

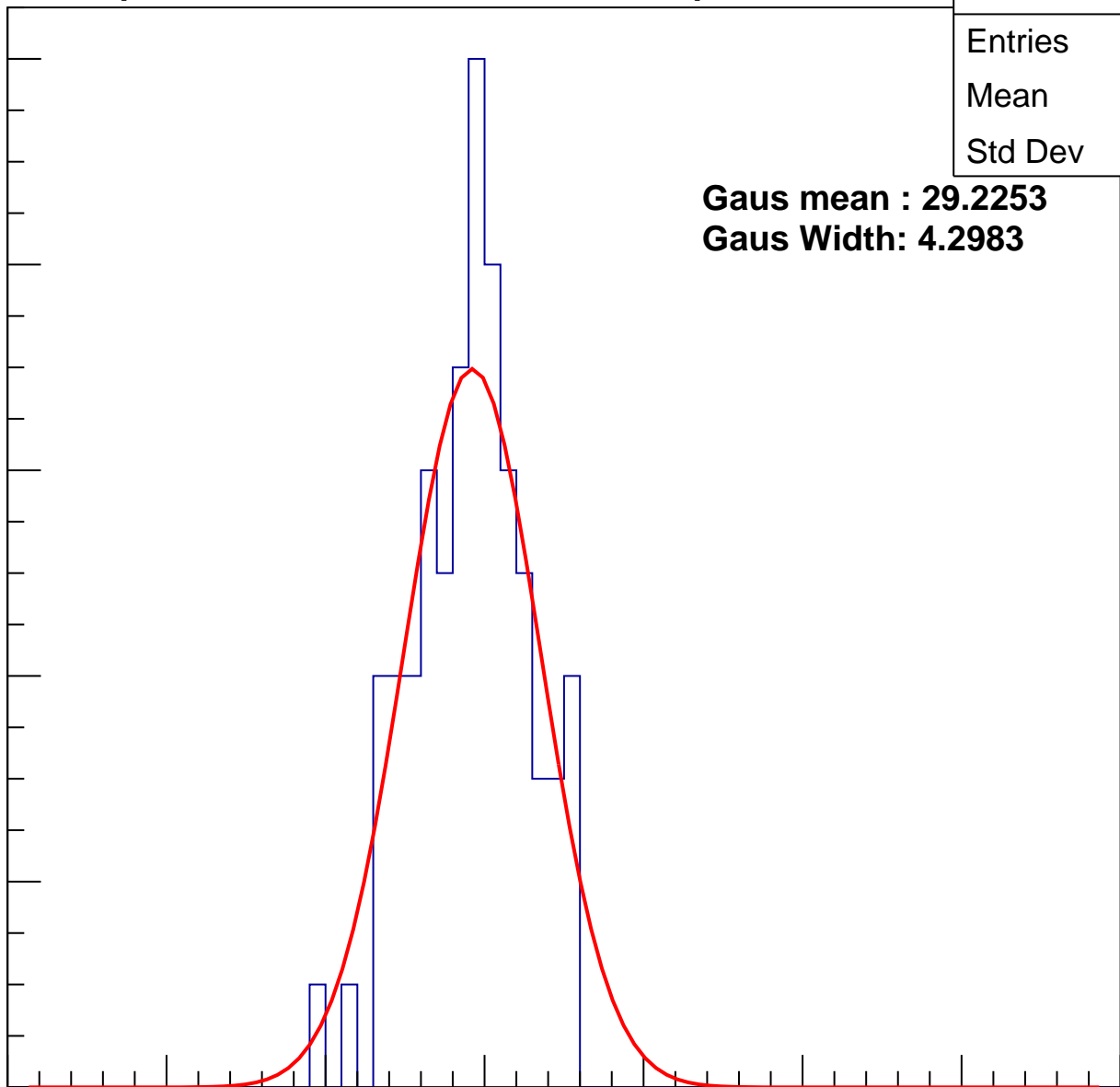
Gaus Width: 4.2983

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch74, adc1

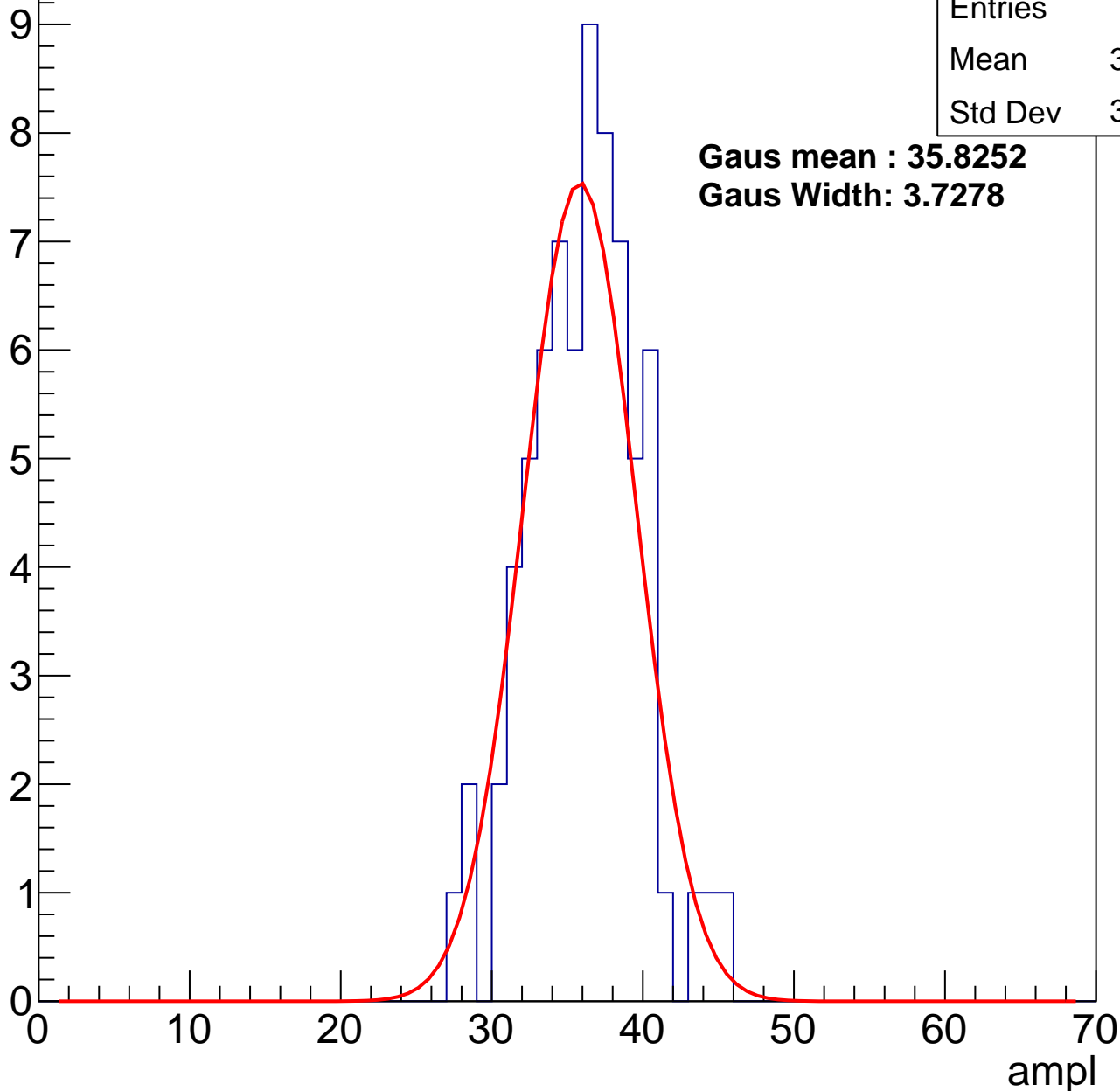
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	35.65
Std Dev	3.606

Gaus mean : 35.8252

Gaus Width: 3.7278



B1L003S, U19-ch74, adc2

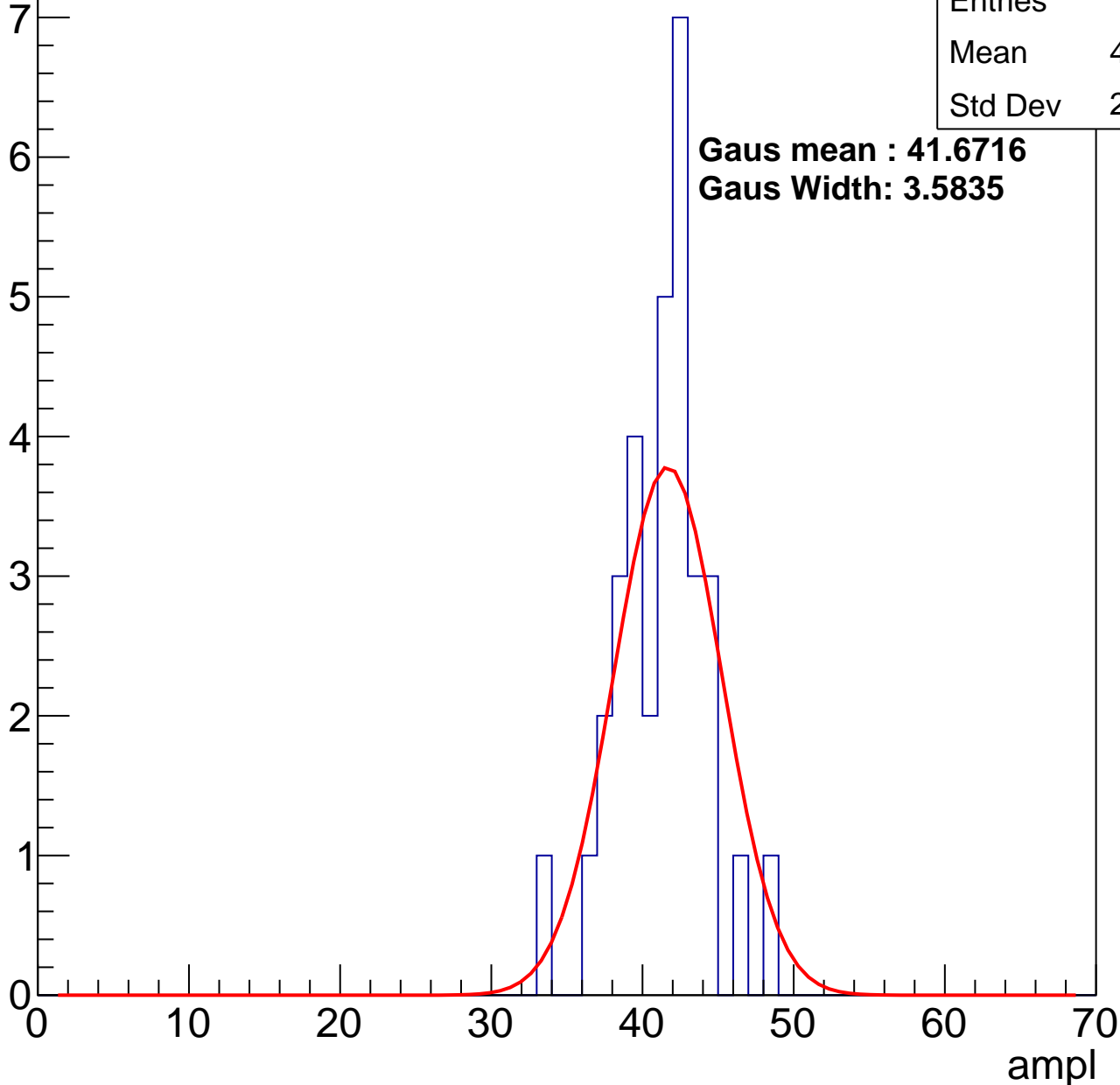
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	40.82
Std Dev	2.938

Gaus mean : 41.6716

Gaus Width: 3.5835



B1L003S, U19-ch74, adc3

calib_packv5_042523_0143.root, FC#13, port D2

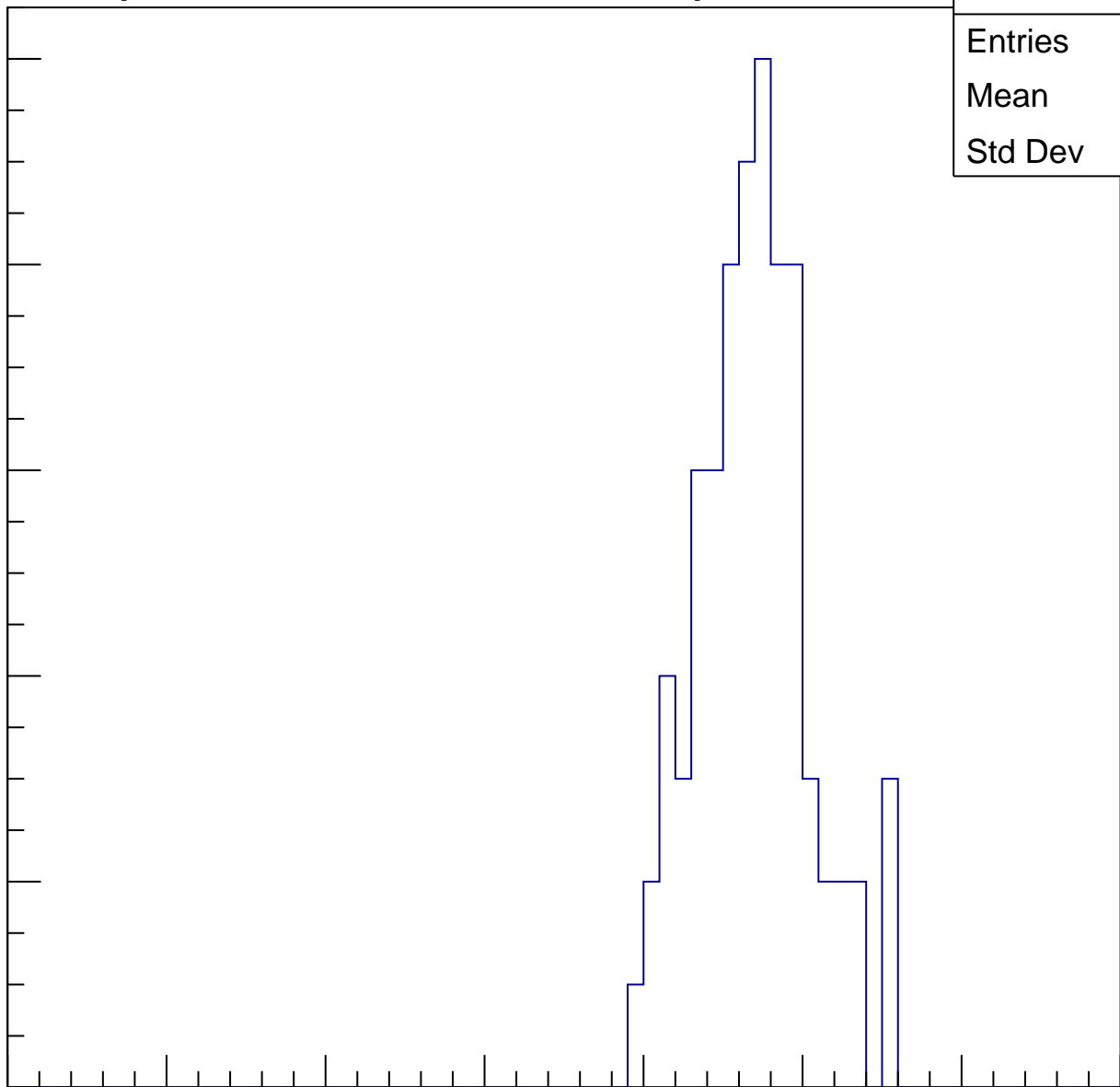
Entries	77
Mean	46.47
Std Dev	3.537

Entry

10
8
6
4
2
0

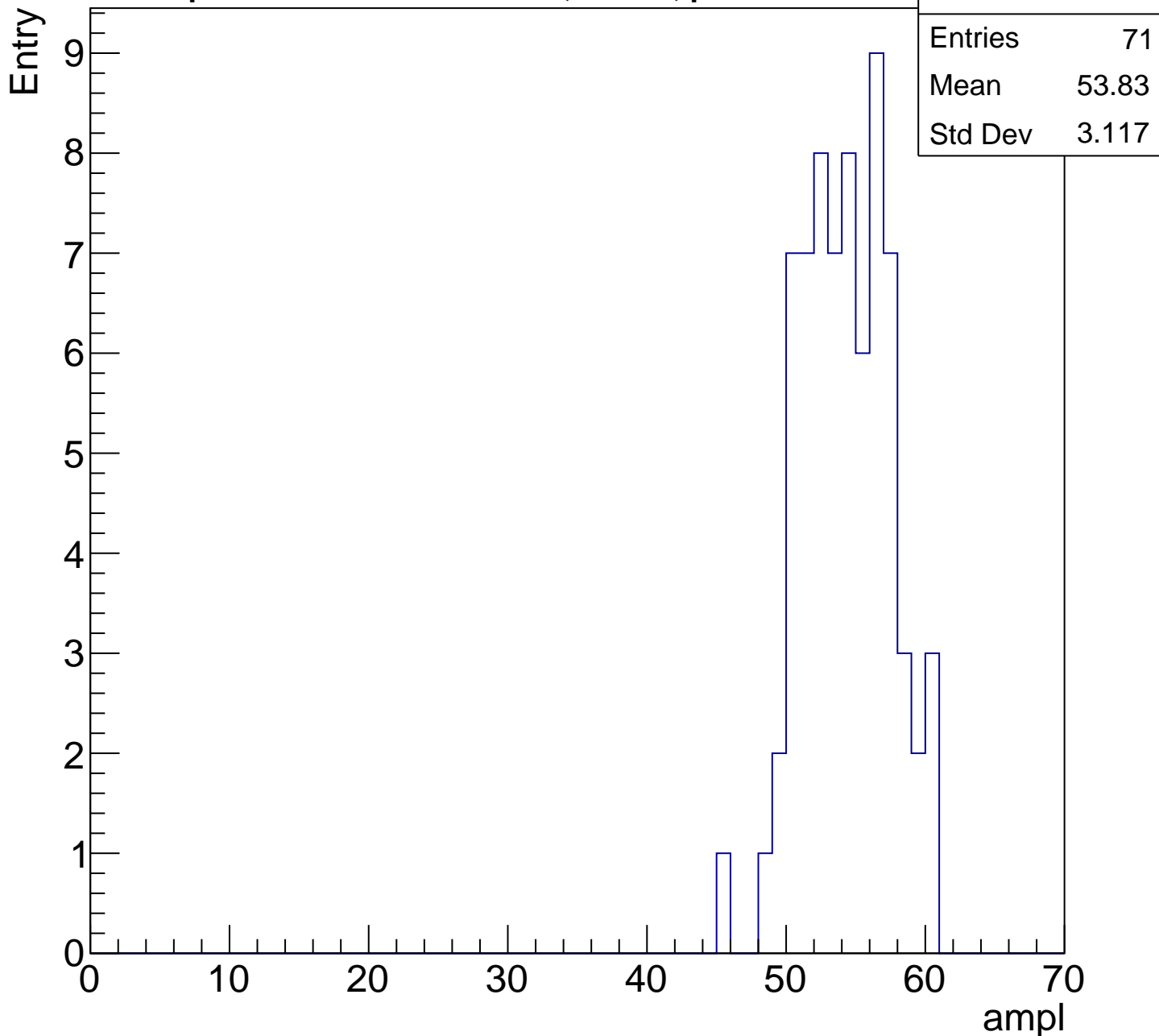
0 10 20 30 40 50 60 70

ampl



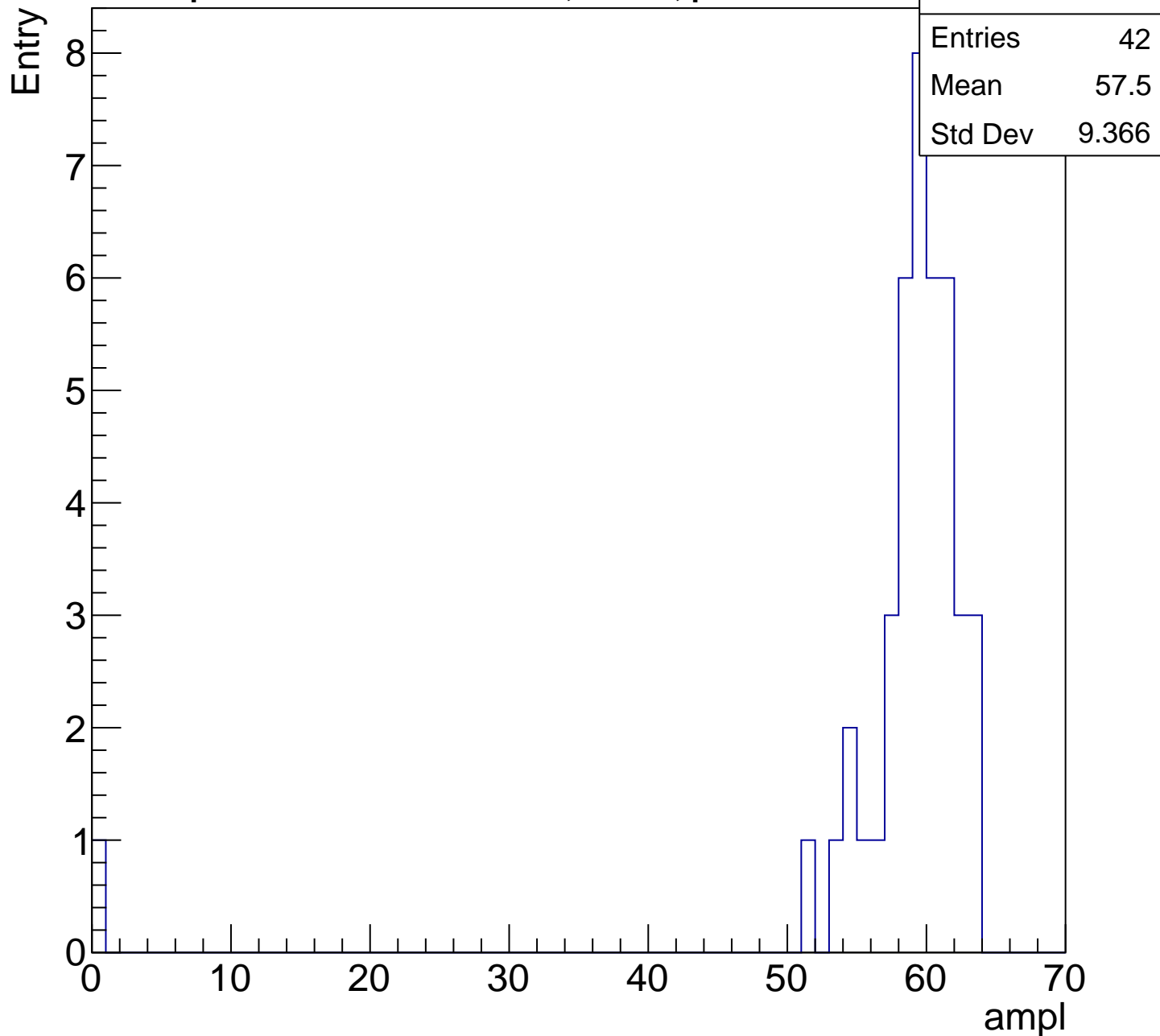
B1L003S, U19-ch74, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch74, adc5

calib_packv5_042523_0143.root, FC#13, port D2

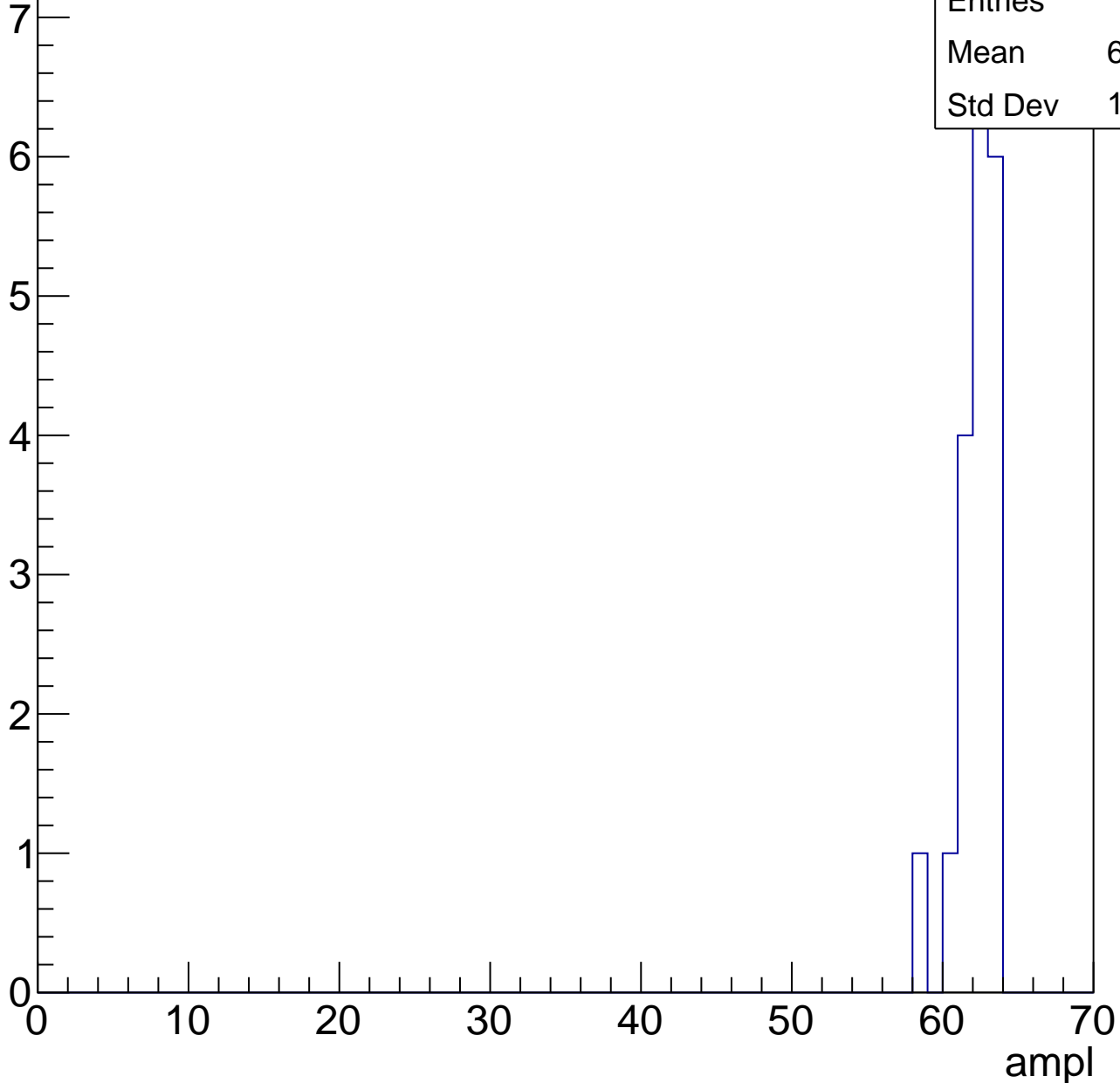


B1L003S, U19-ch74, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	19
Mean	61.79
Std Dev	1.239



B1L003S, U19-ch74, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L003S, U19-ch75, adc0

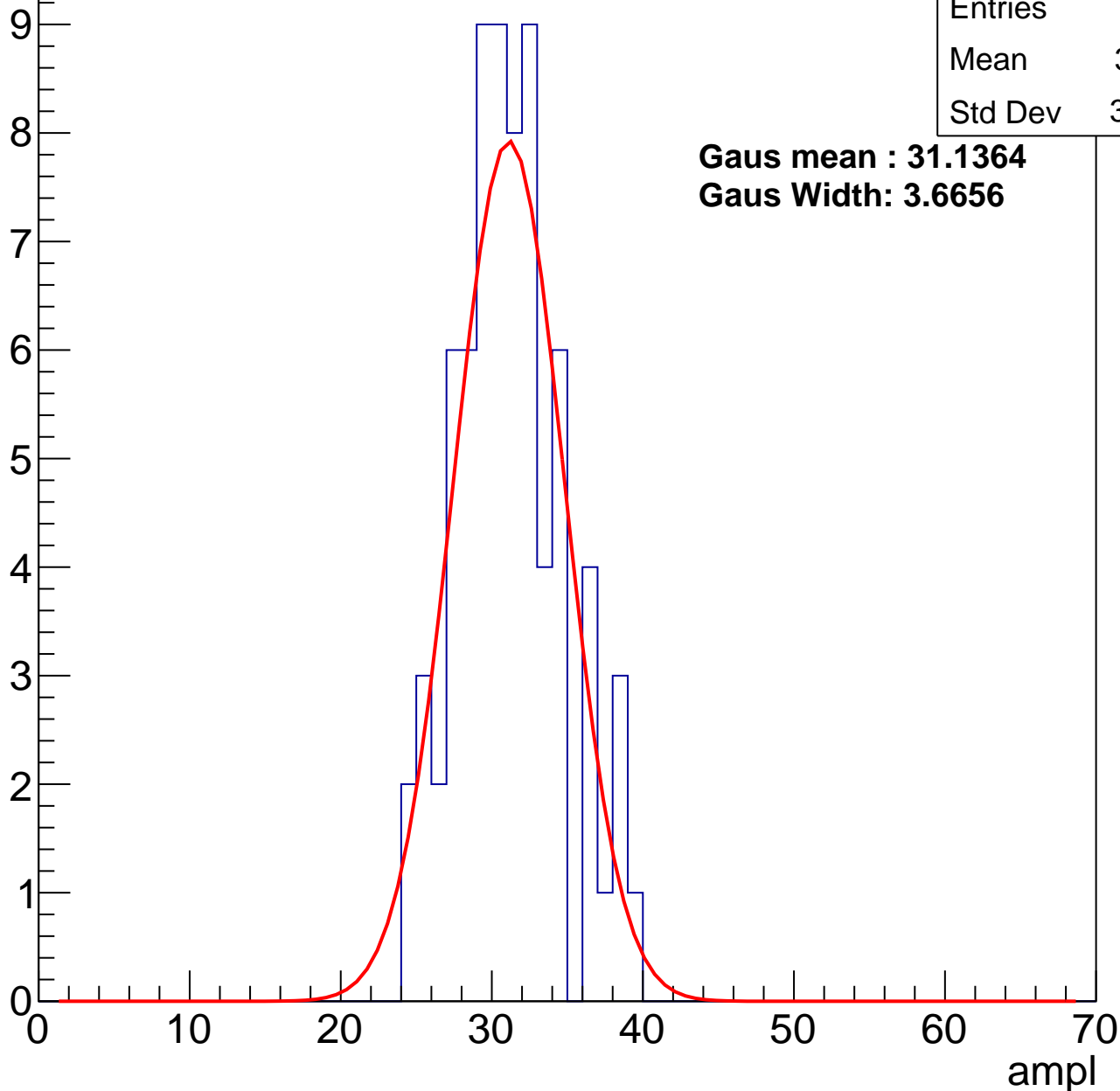
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	30.71
Std Dev	3.454

Gaus mean : 31.1364

Gaus Width: 3.6656



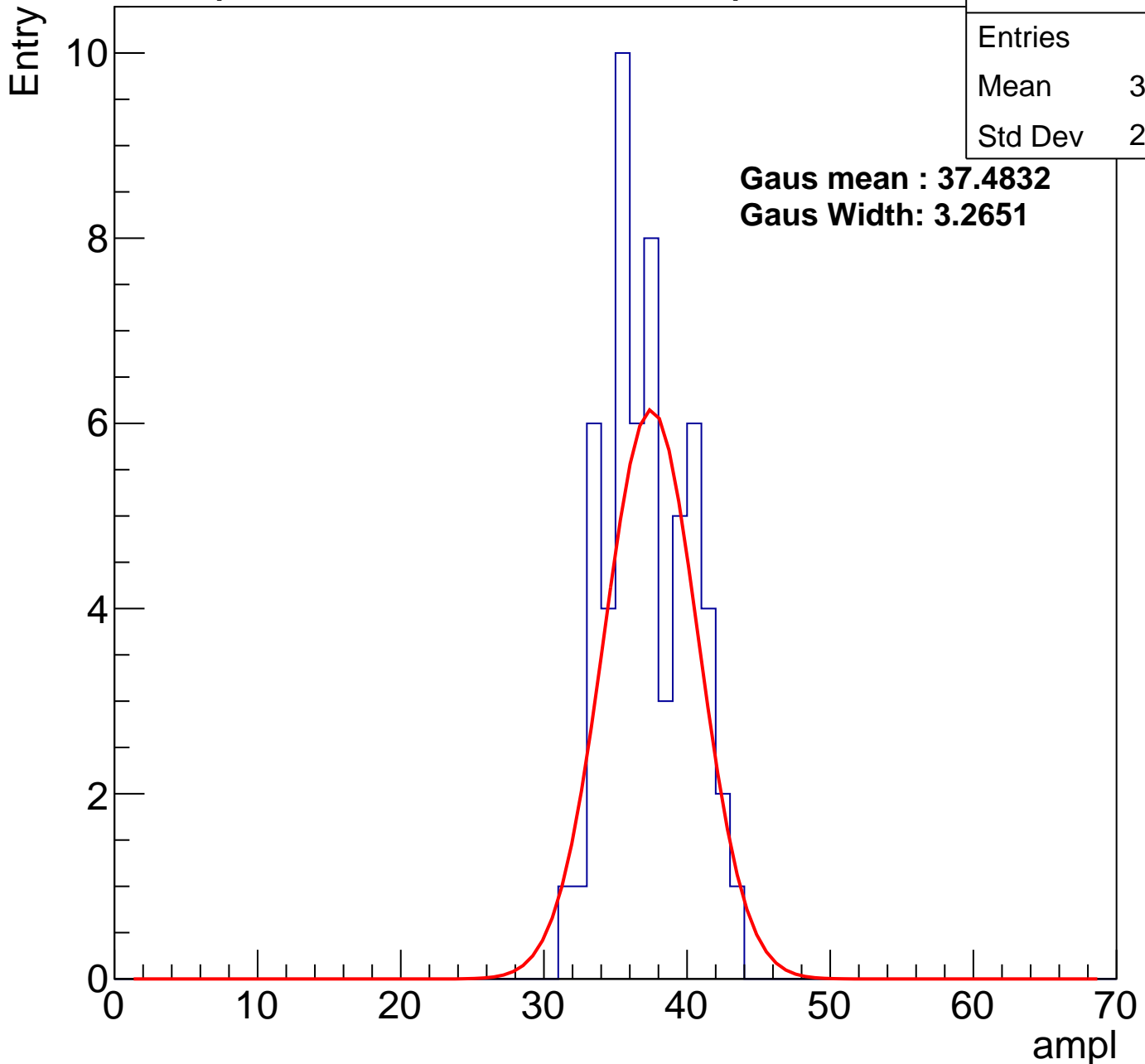
B1L003S, U19-ch75, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	57
Mean	36.82
Std Dev	2.854

Gaus mean : 37.4832

Gaus Width: 3.2651



B1L003S, U19-ch75, adc2

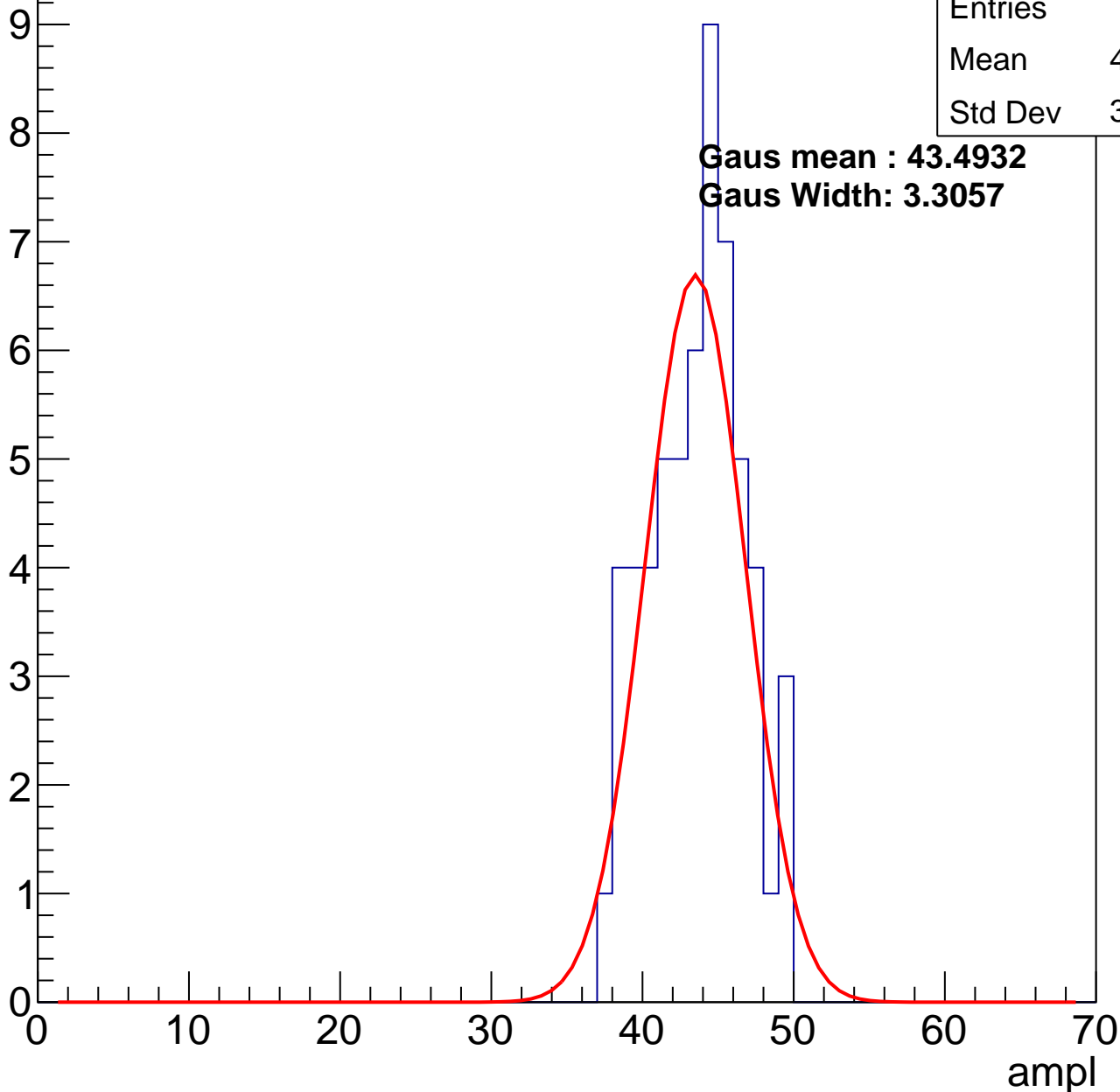
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.14
Std Dev	3.048

Gaus mean : 43.4932

Gaus Width: 3.3057

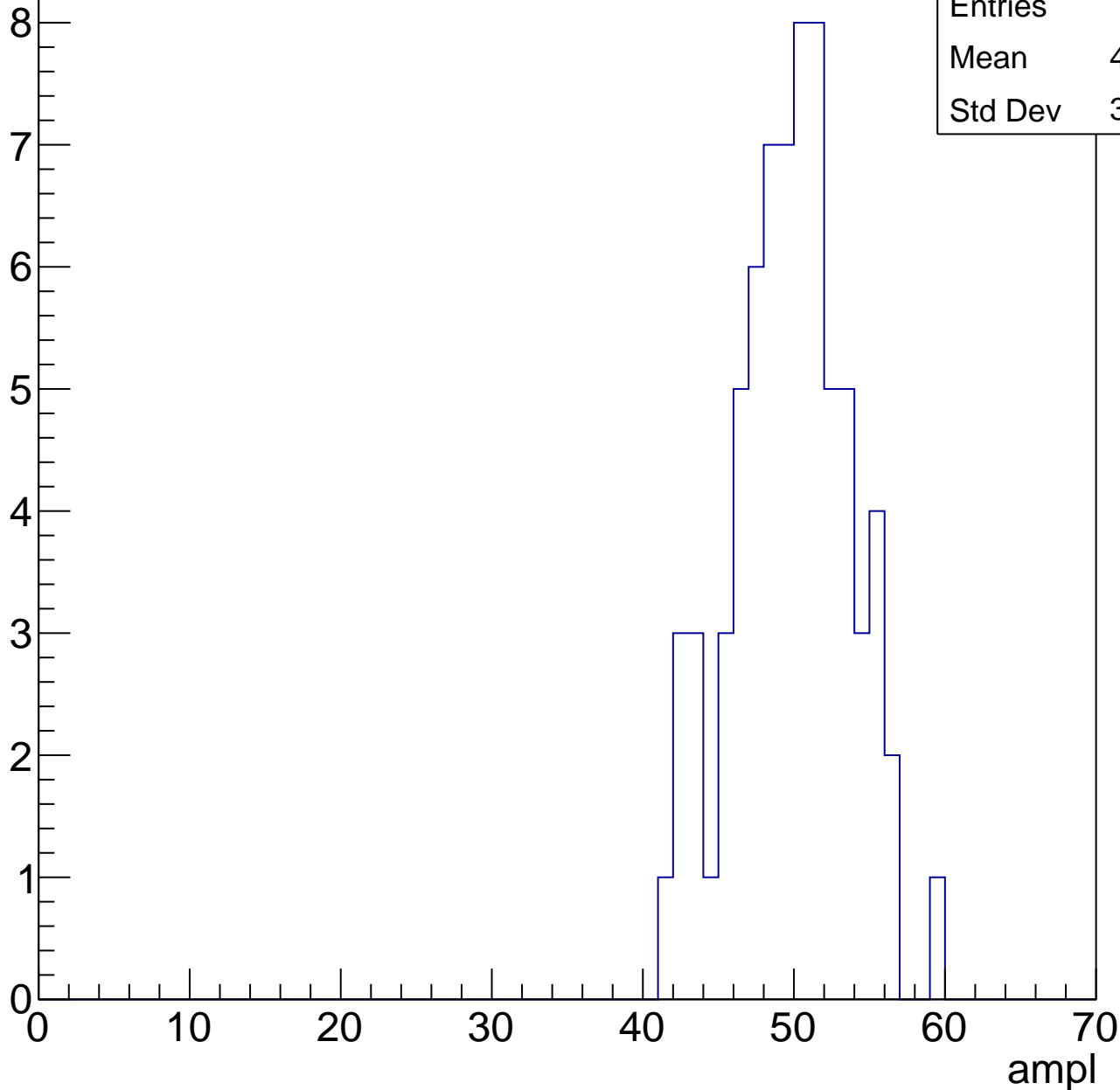


B1L003S, U19-ch75, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	49.33
Std Dev	3.804

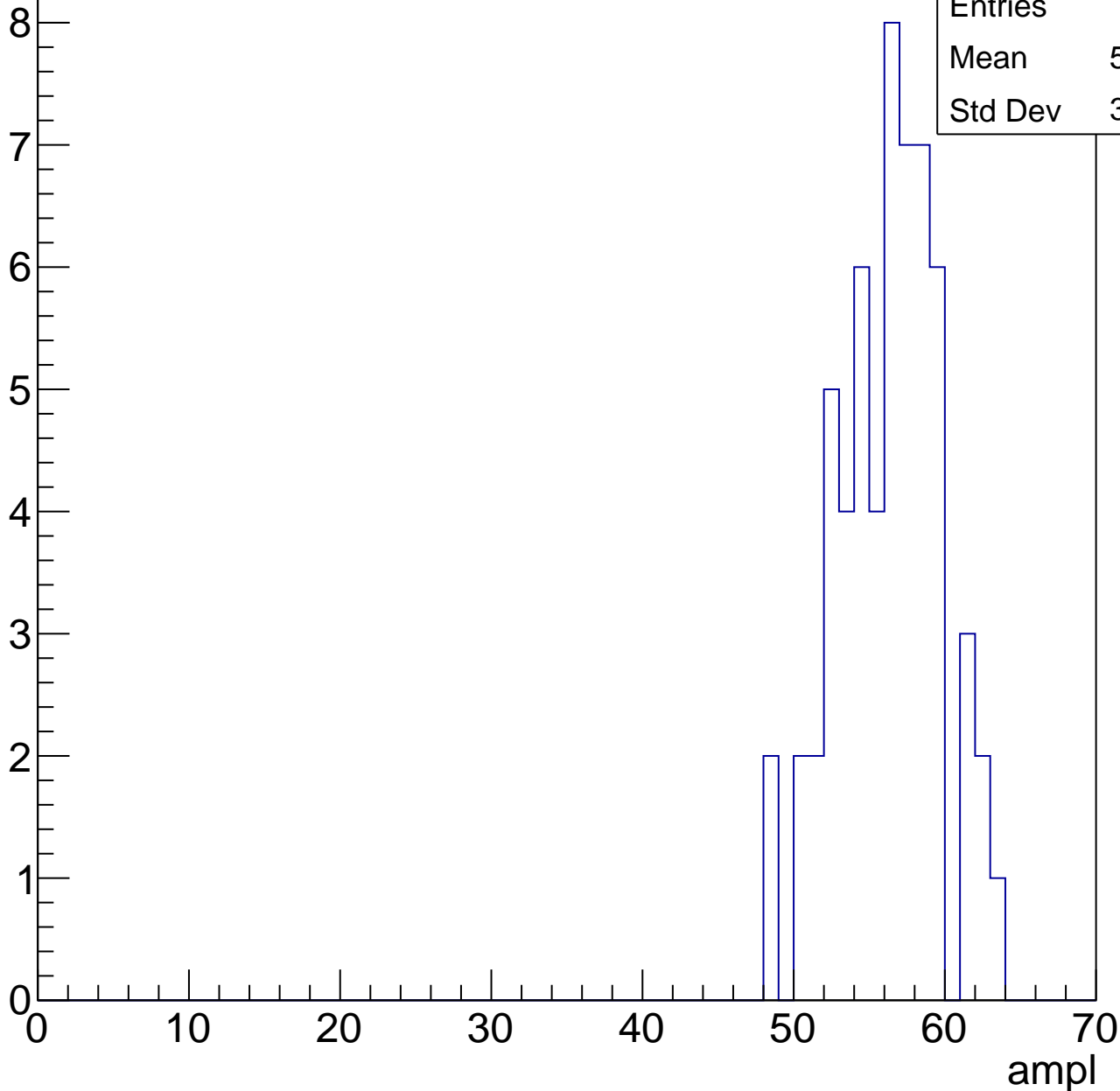


B1L003S, U19-ch75, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.78
Std Dev	3.385

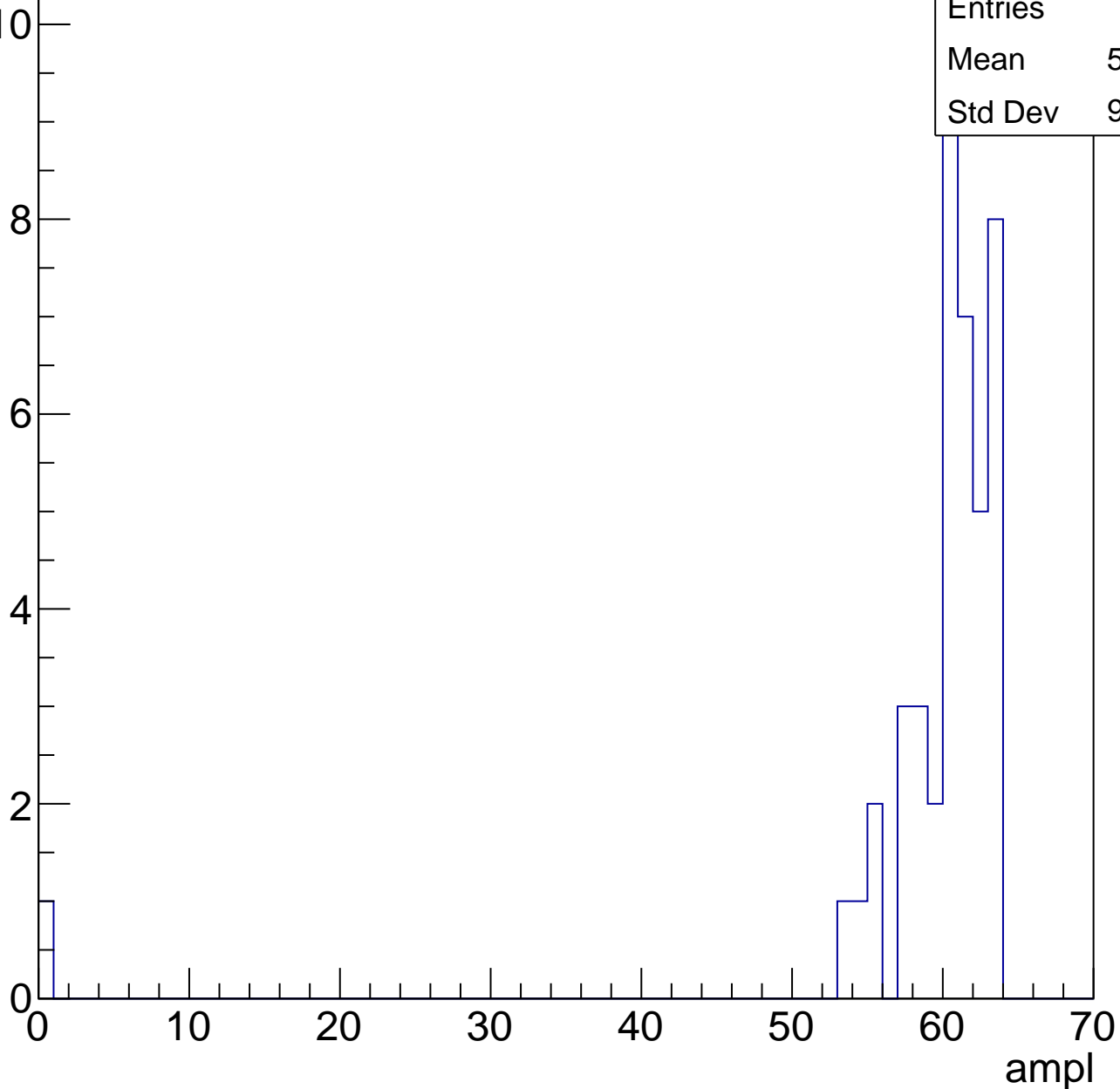


B1L003S, U19-ch75, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

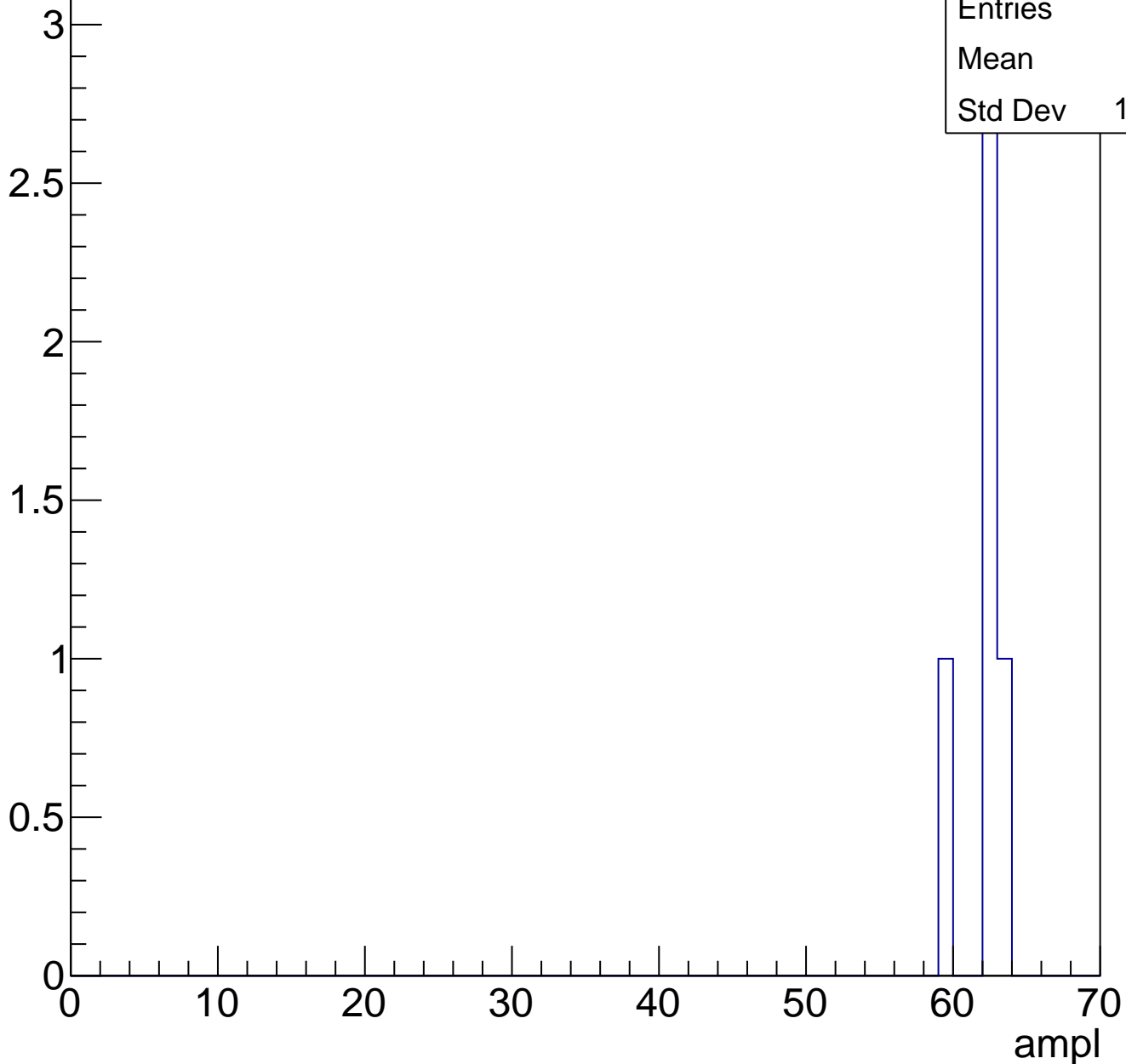
Entries	43
Mean	58.63
Std Dev	9.393



B1L003S, U19-ch75, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch75, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch76, adc0

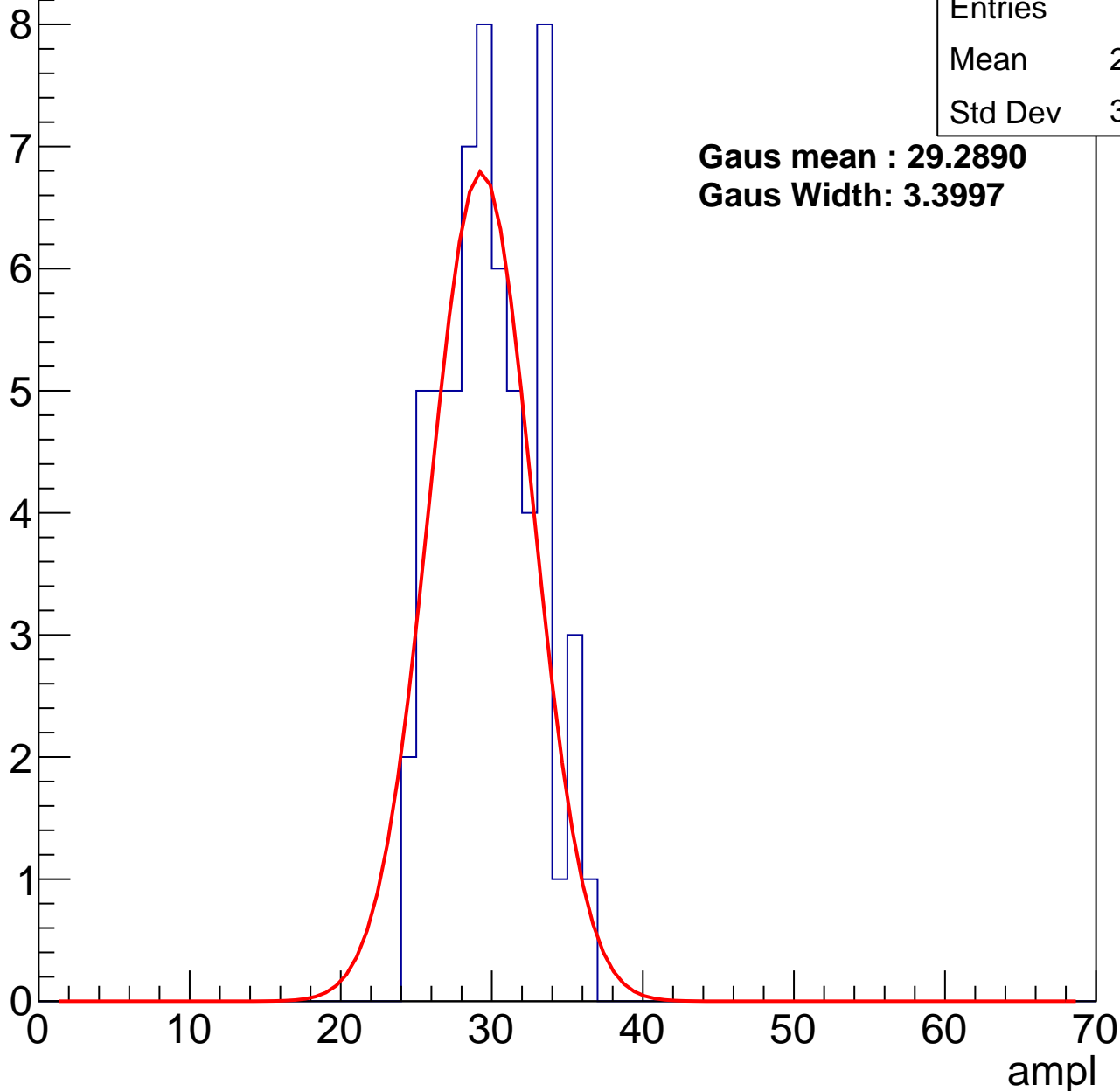
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29.47
Std Dev	3.058

Gaus mean : 29.2890

Gaus Width: 3.3997



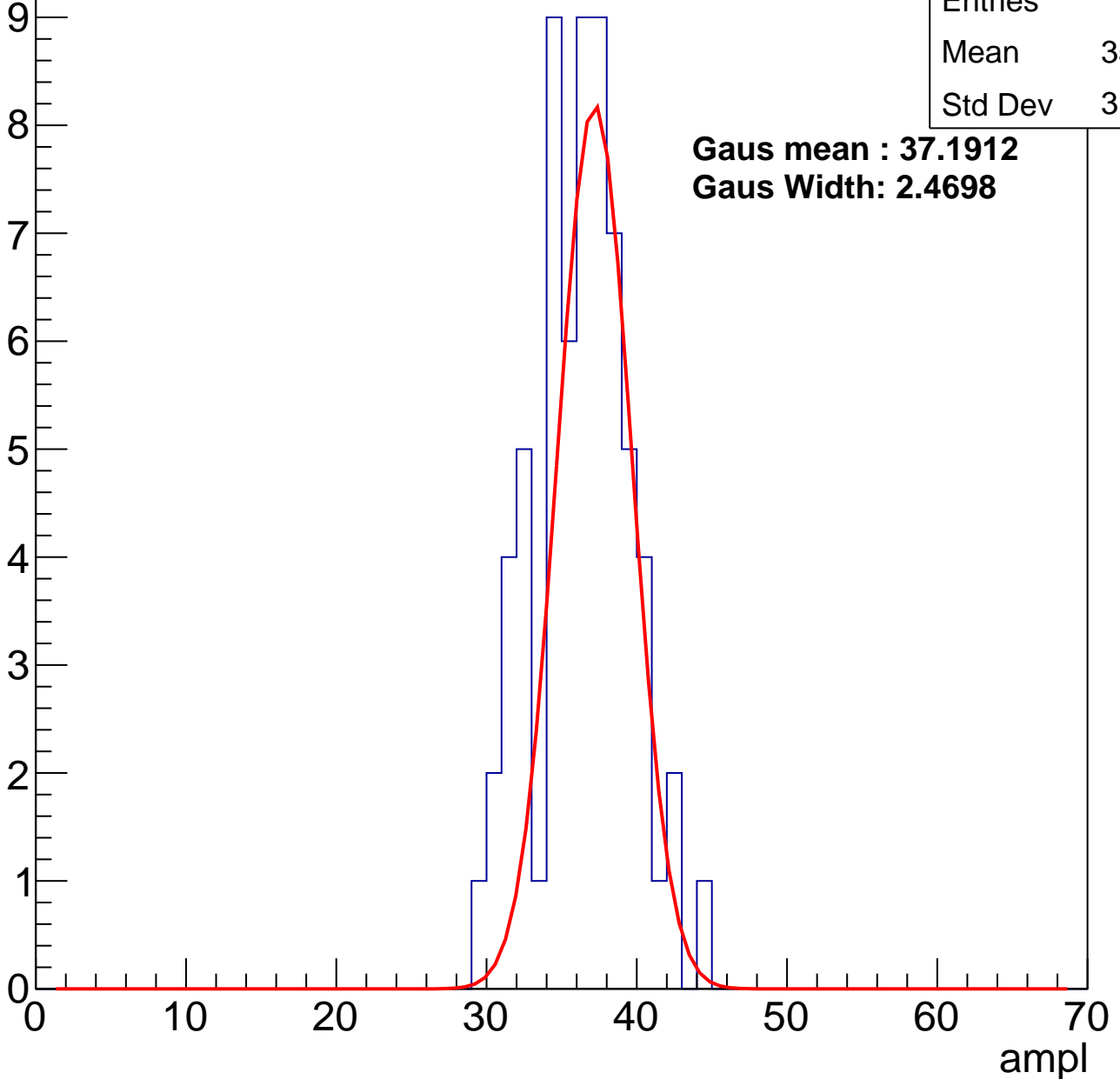
B1L003S, U19-ch76, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.89
Std Dev	3.158

Gaus mean : 37.1912
Gaus Width: 2.4698



B1L003S, U19-ch76, adc2

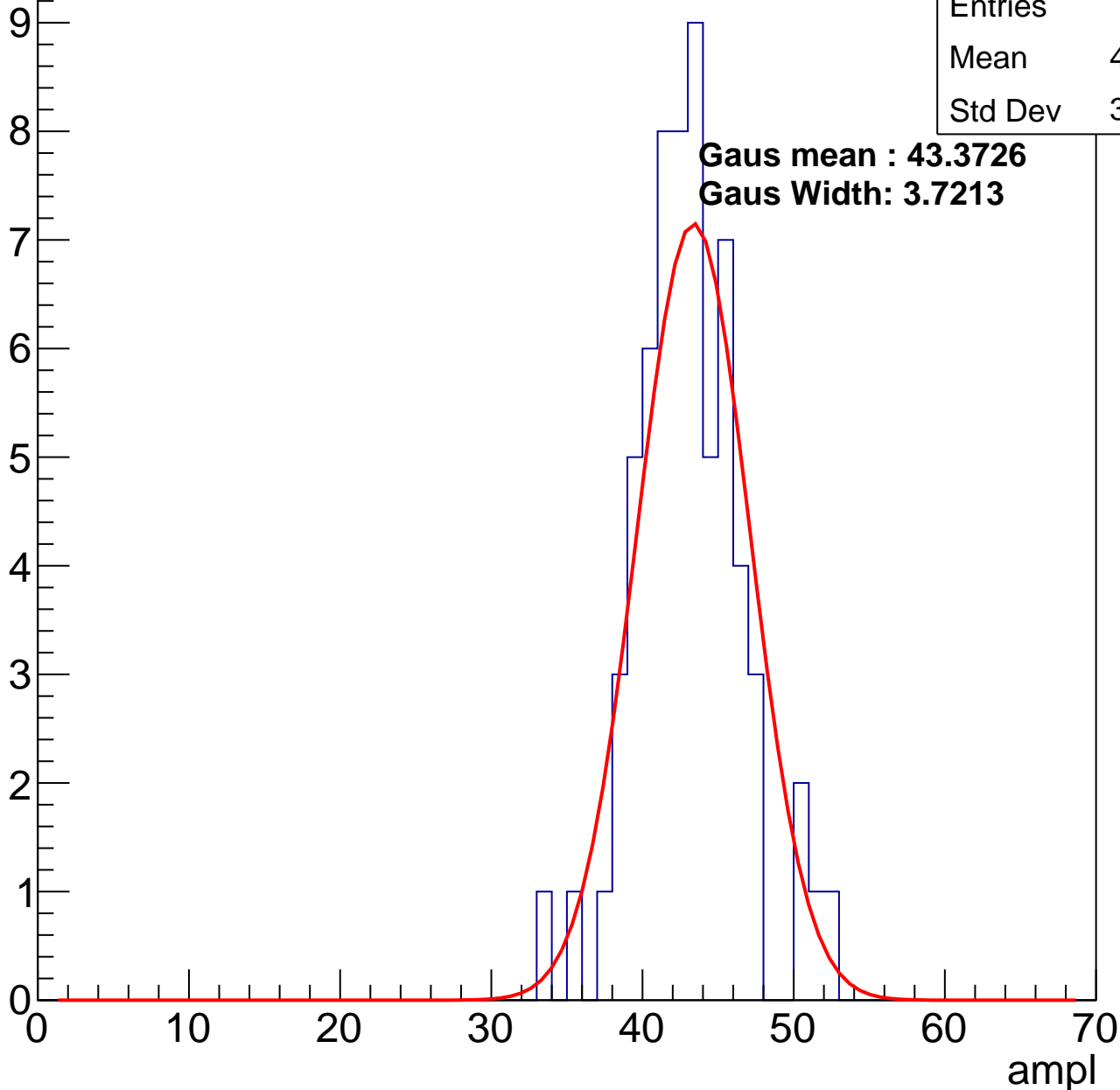
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.58
Std Dev	3.503

Gaus mean : 43.3726

Gaus Width: 3.7213

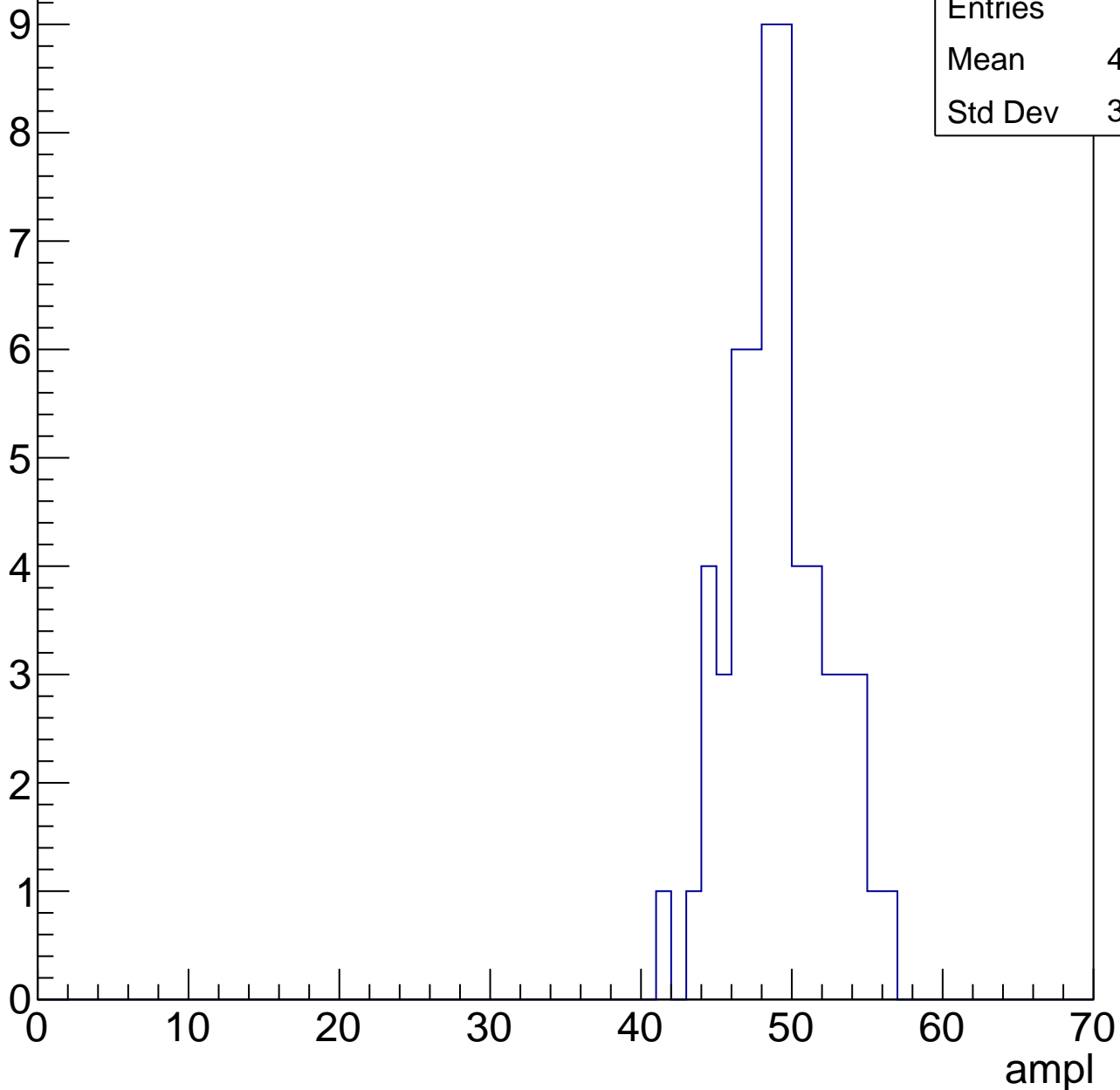


B1L003S, U19-ch76, adc3

calib_packv5_042523_0143.root, FC#13, port D2

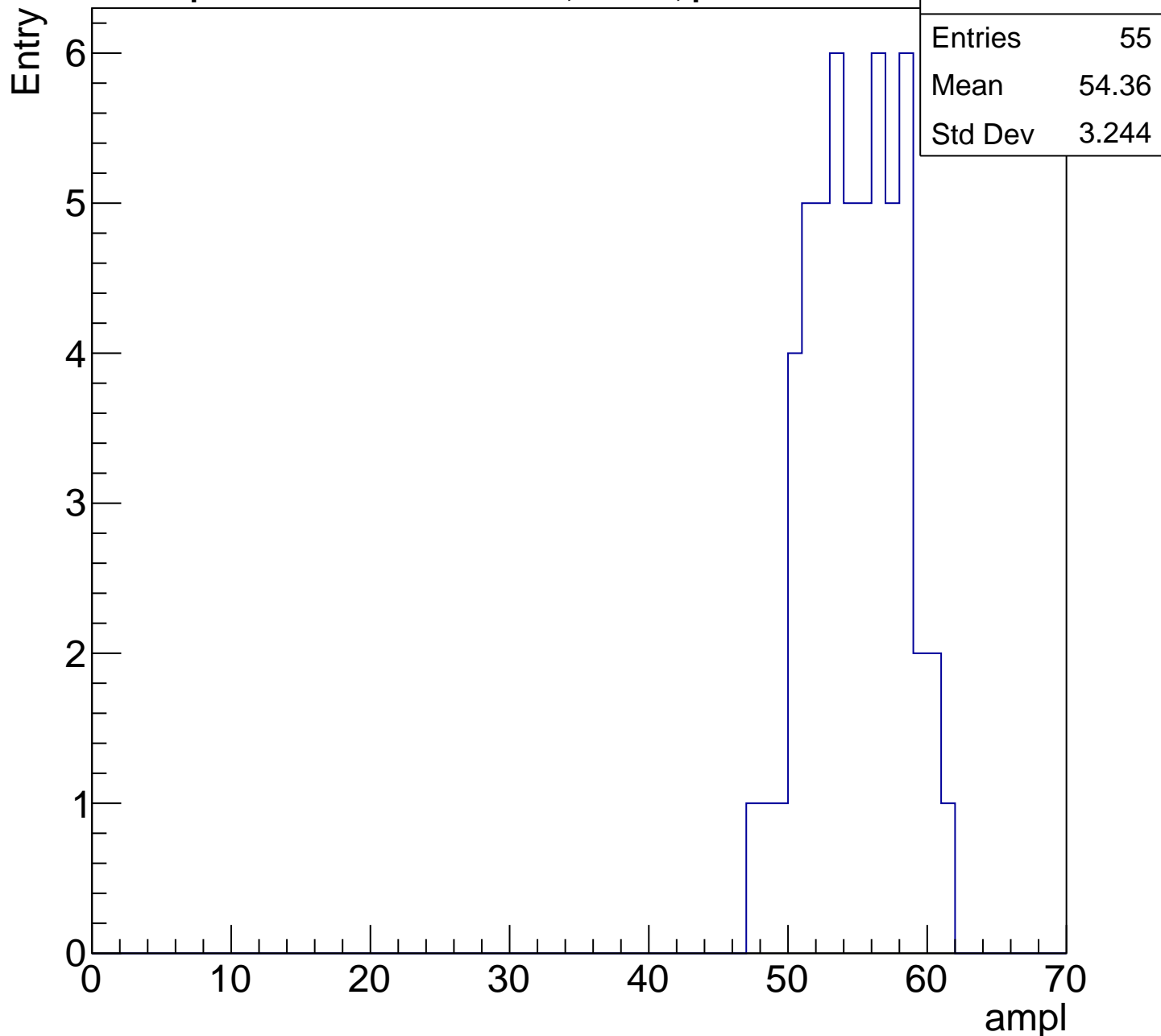
Entry

Entries	58
Mean	48.59
Std Dev	3.162



B1L003S, U19-ch76, adc4

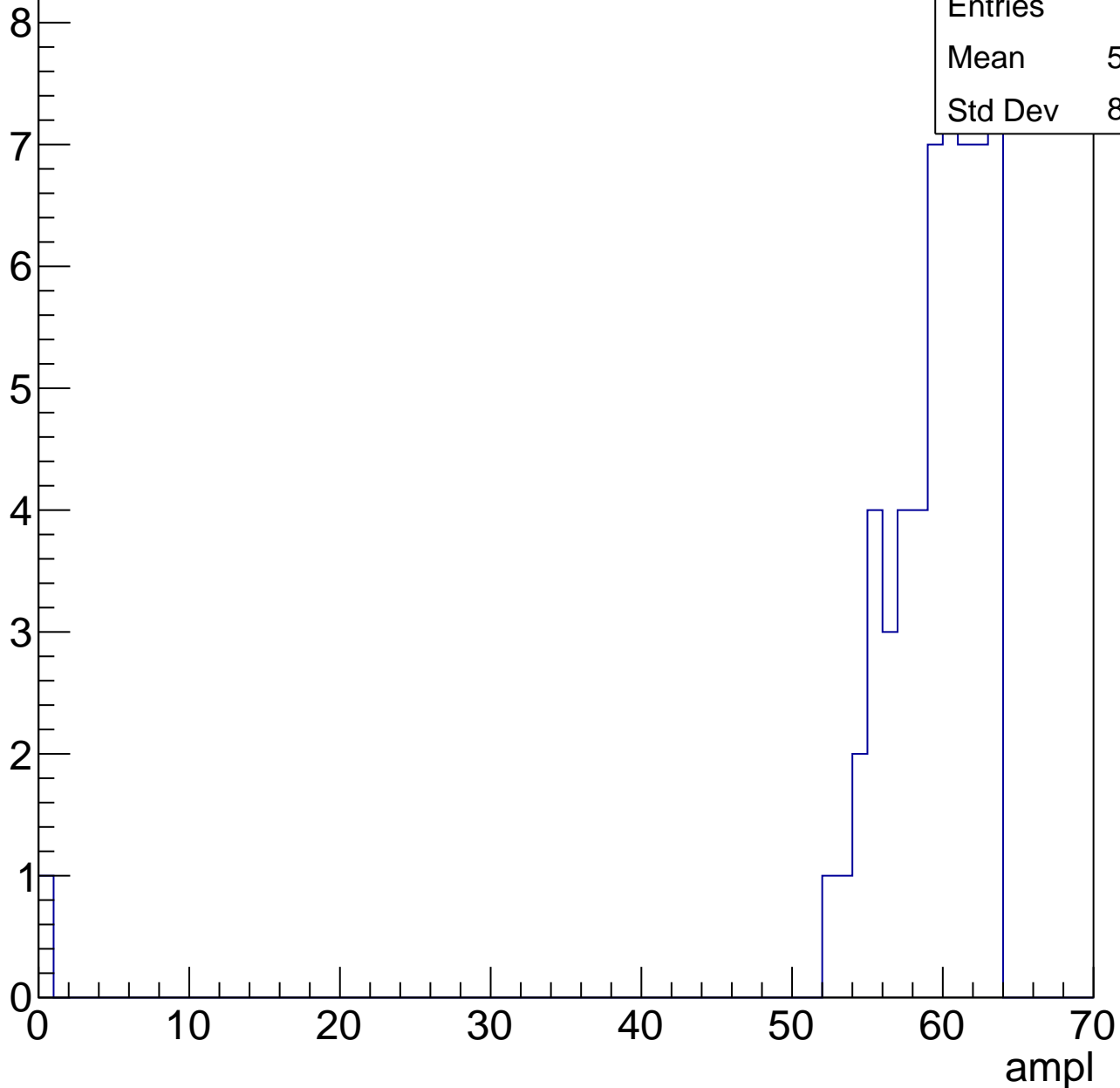
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch76, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch76, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

8

Mean

60.88

Std Dev

1.833

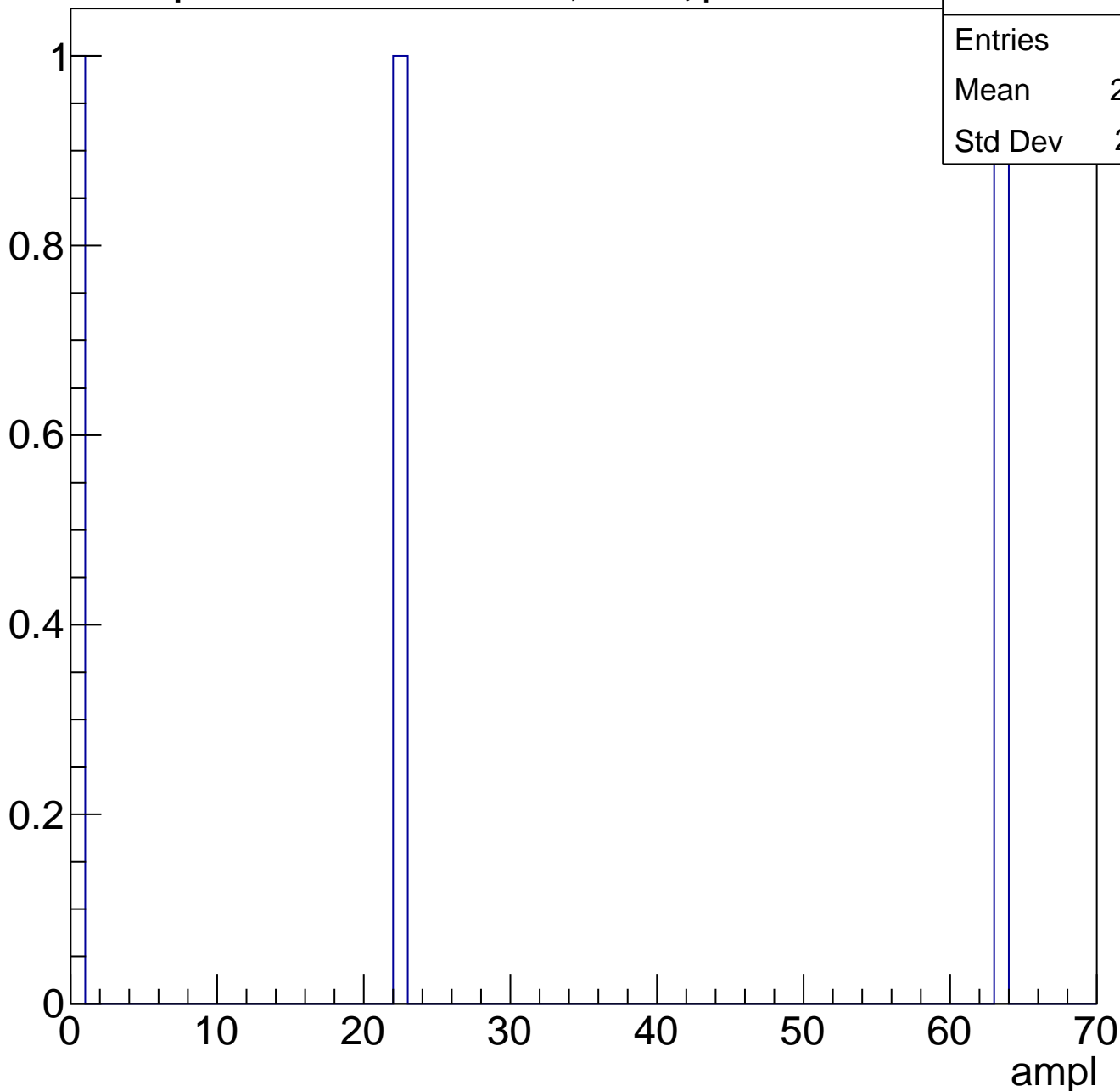
ampl

0 10 20 30 40 50 60 70

B1L003S, U19-ch76, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B1L003S, U19-ch77, adc0

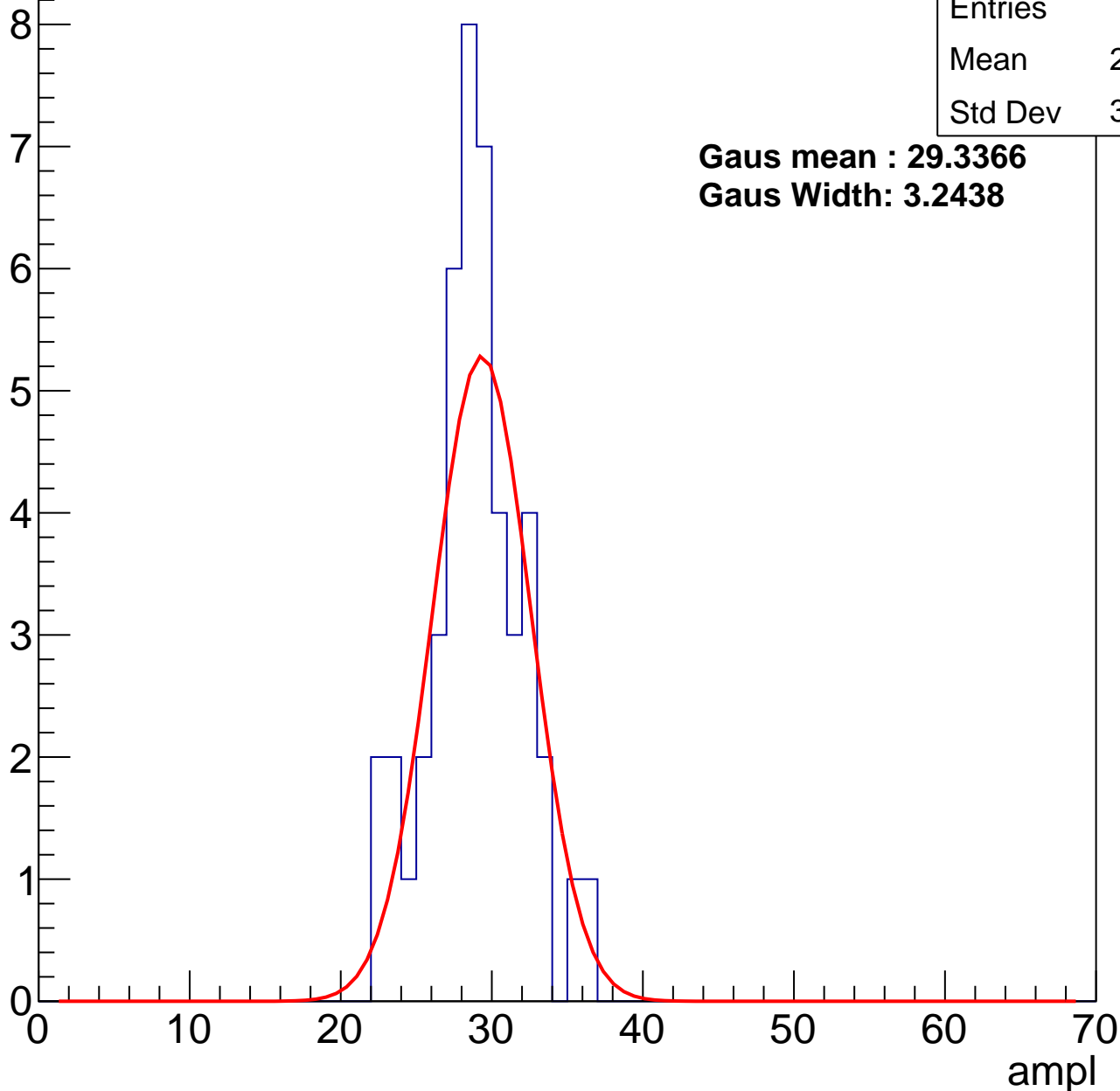
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	28.46
Std Dev	3.098

Gaus mean : 29.3366

Gaus Width: 3.2438



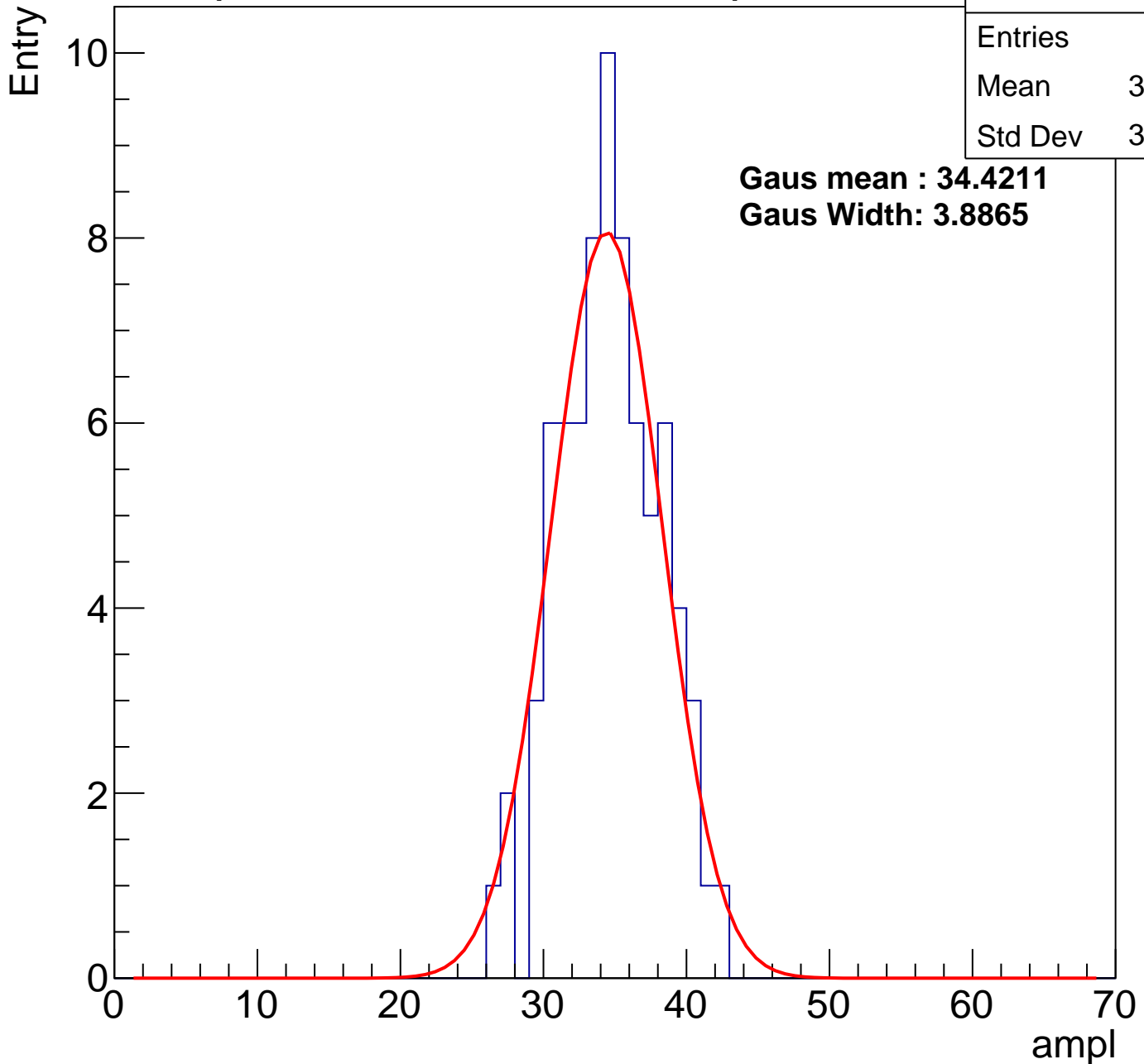
B1L003S, U19-ch77, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	34.17
Std Dev	3.462

Gaus mean : 34.4211

Gaus Width: 3.8865



B1L003S, U19-ch77, adc2

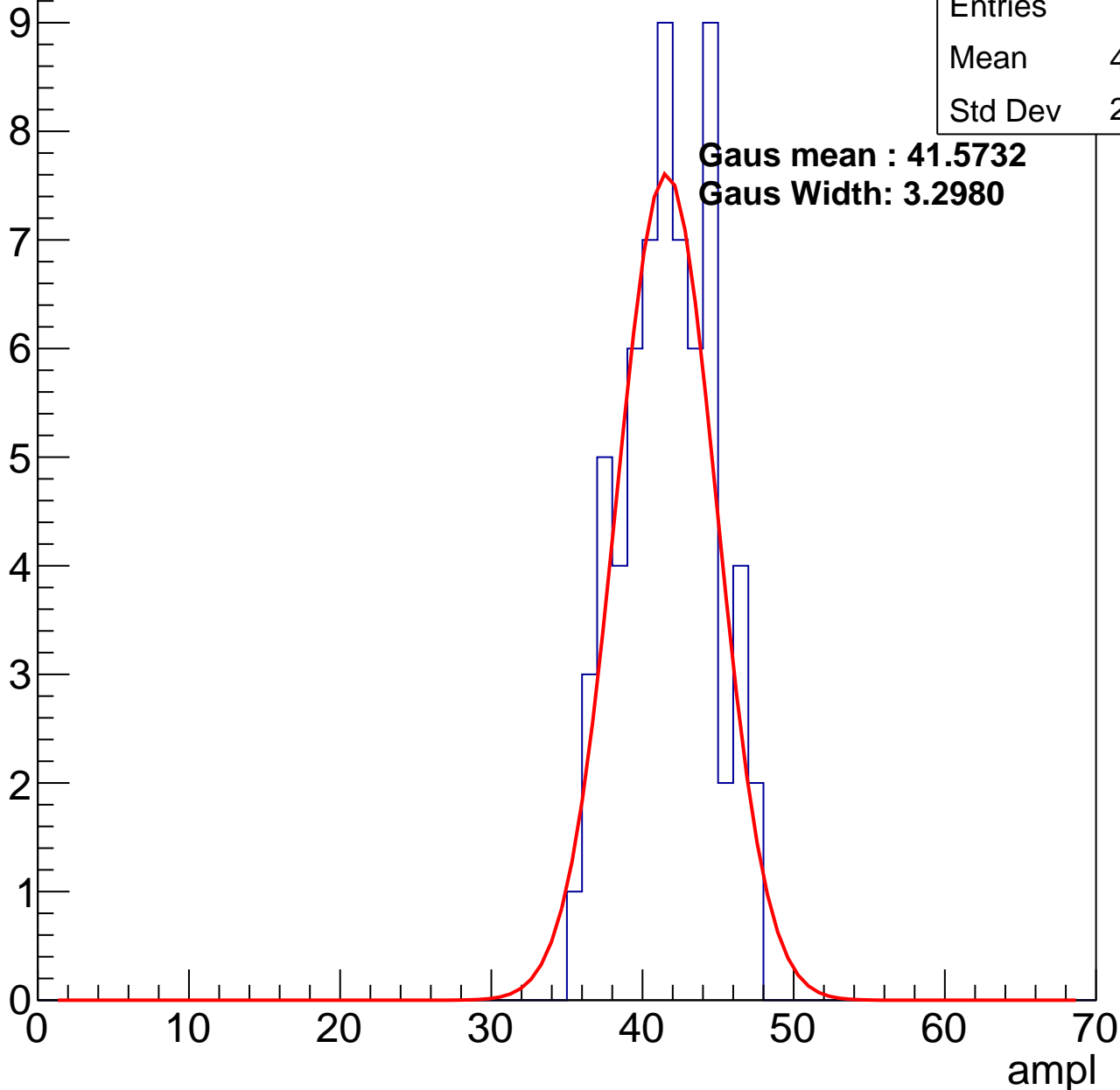
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	41.22
Std Dev	2.964

Gaus mean : 41.5732

Gaus Width: 3.2980



B1L003S, U19-ch77, adc3

calib_packv5_042523_0143.root, FC#13, port D2

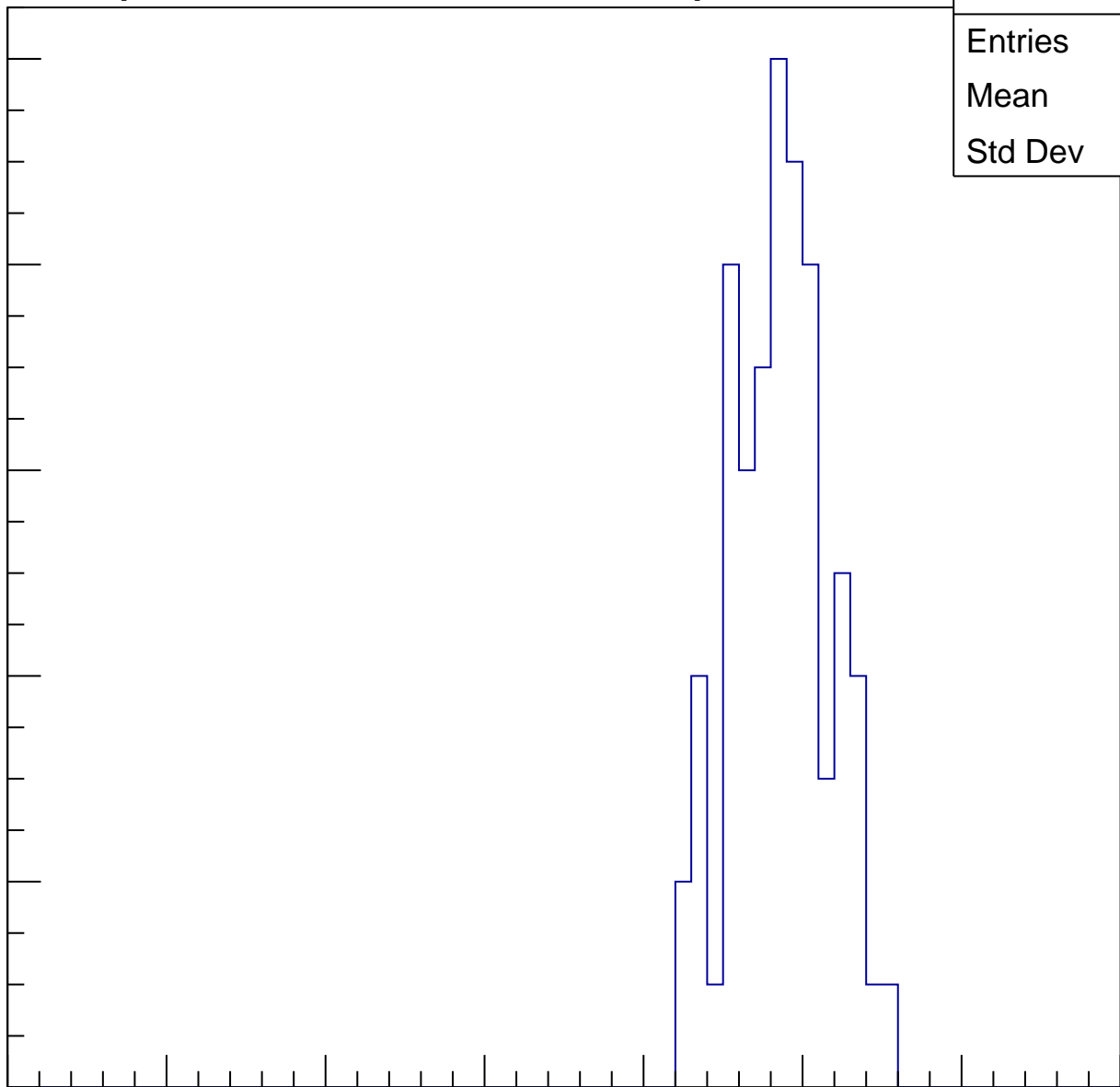
Entries	69
Mean	48.12
Std Dev	3.005

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

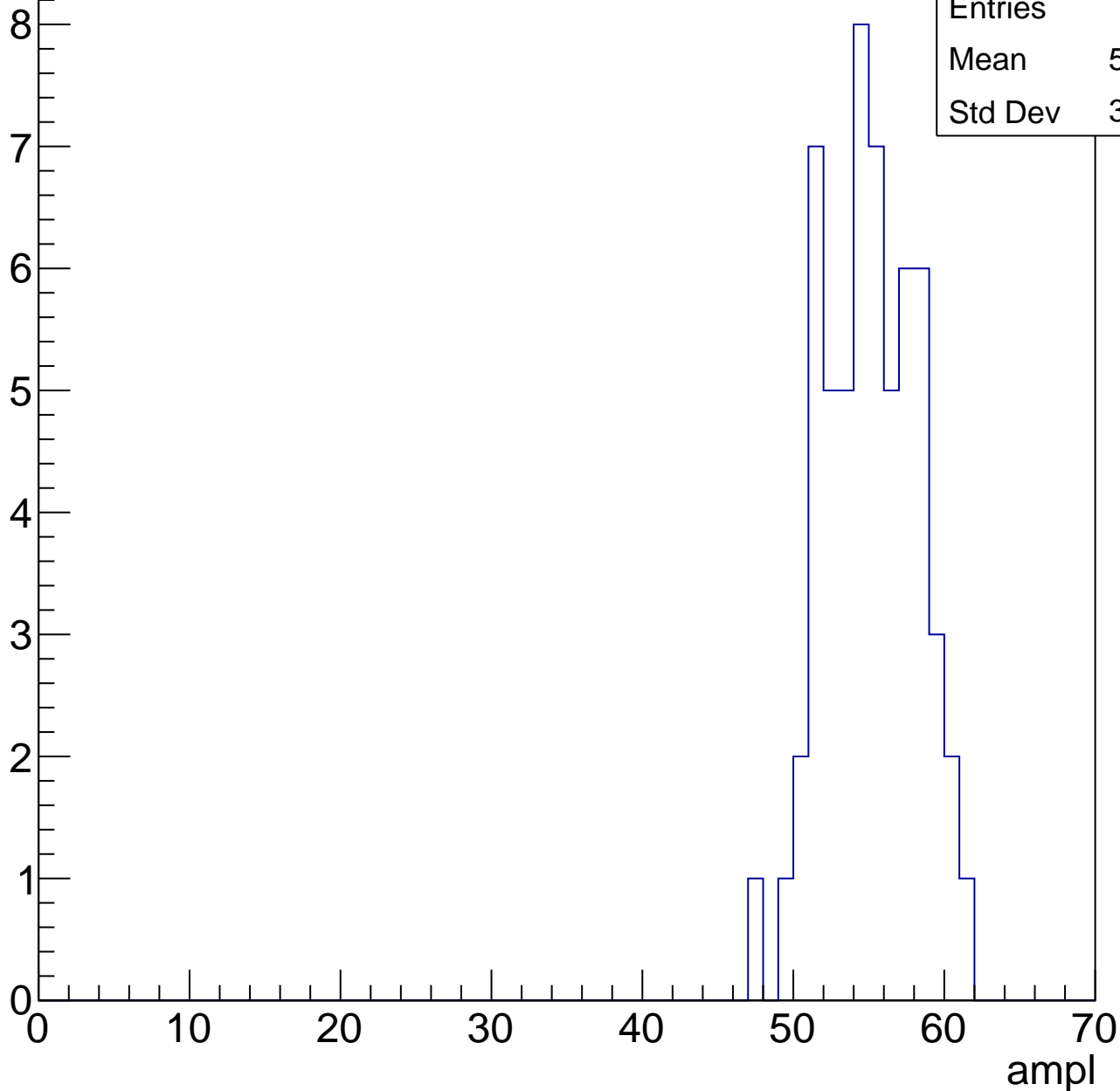


B1L003S, U19-ch77, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.63
Std Dev	3.036



B1L003S, U19-ch77, adc5

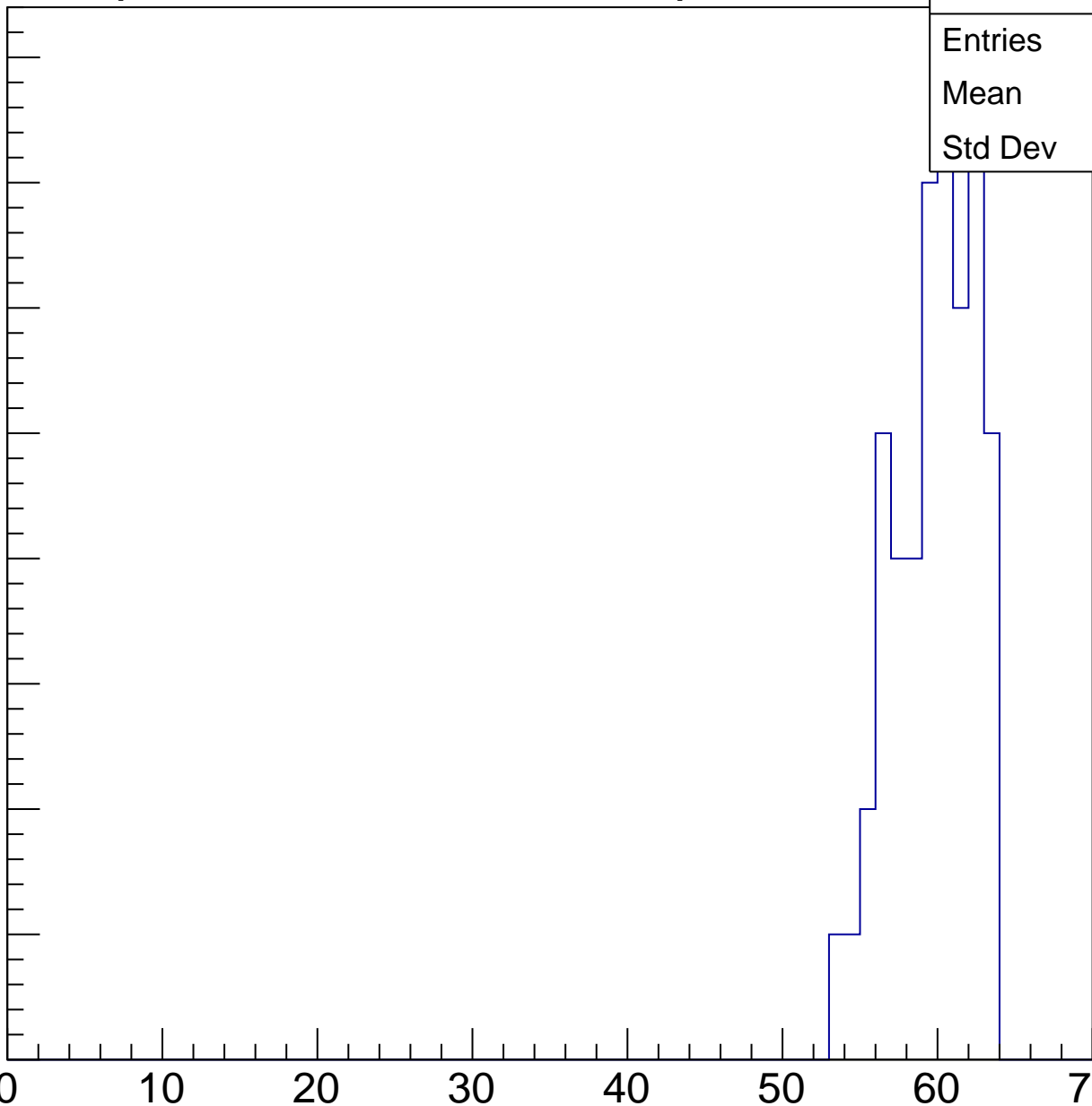
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.35
Std Dev	2.565

ampl

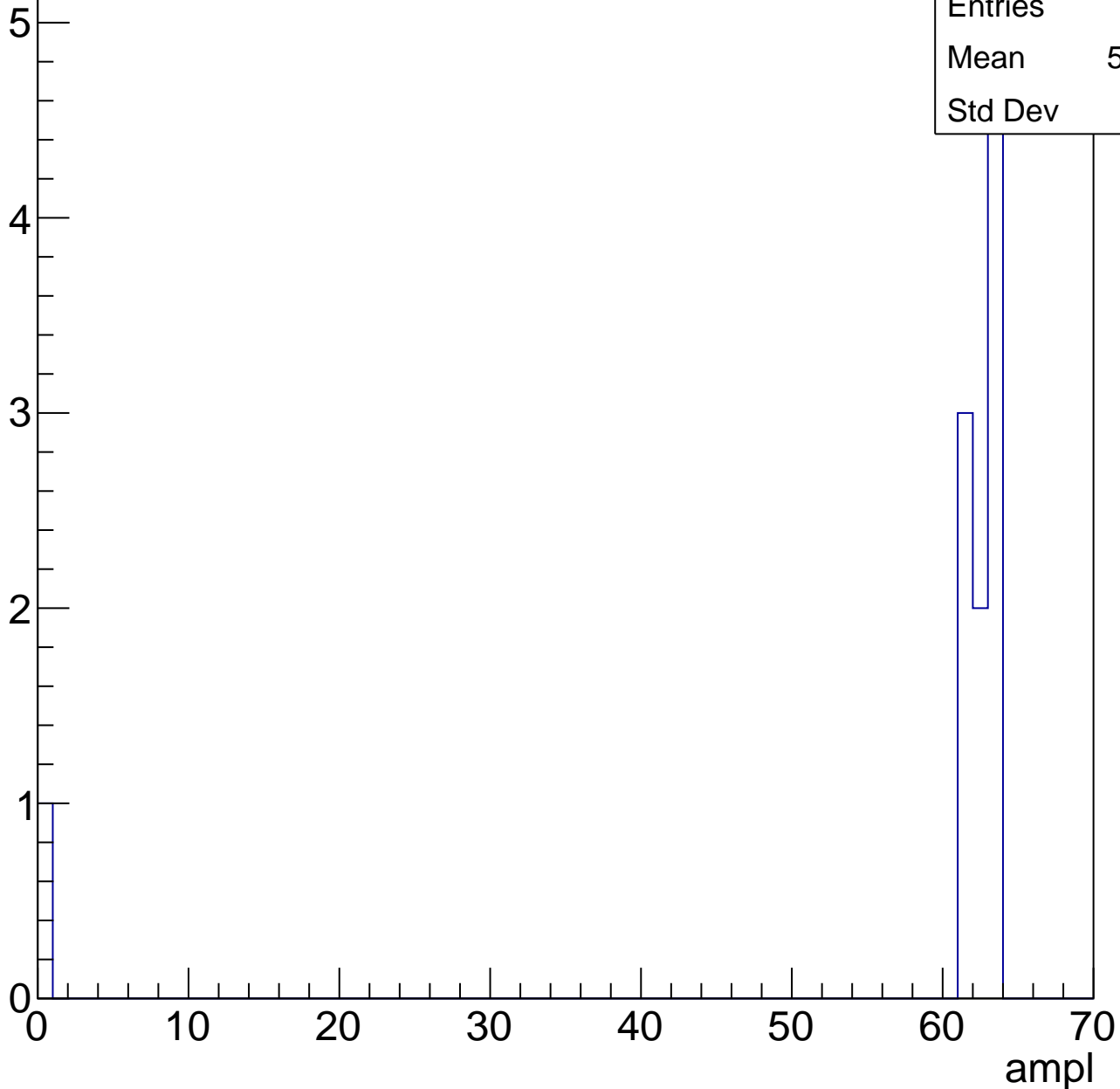


B1L003S, U19-ch77, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	56.55
Std Dev	17.9



B1L003S, U19-ch77, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch78, adc0

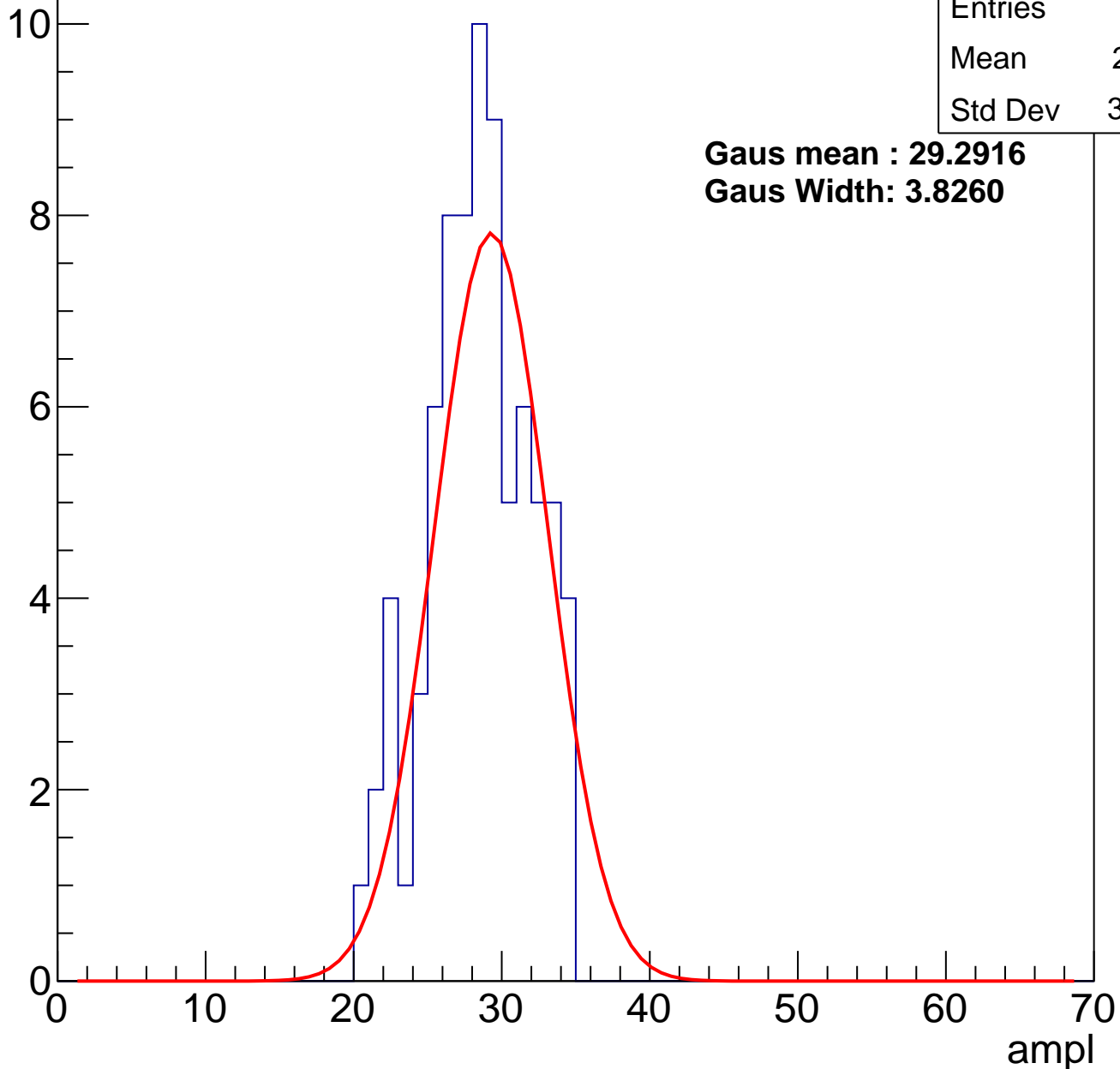
calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	28.01
Std Dev	3.428

Gaus mean : 29.2916

Gaus Width: 3.8260

Entry



B1L003S, U19-ch78, adc1

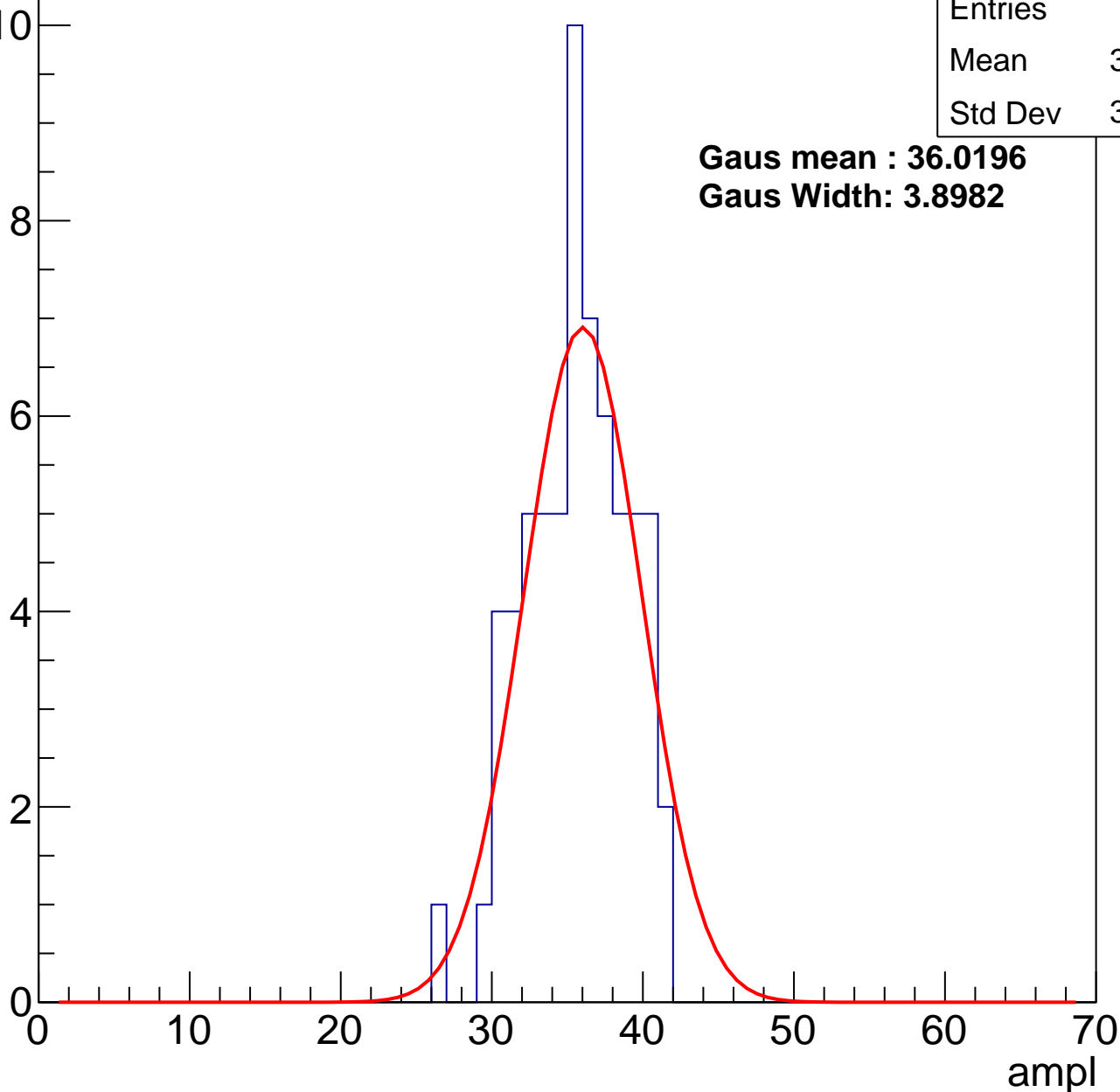
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.15
Std Dev	3.287

Gaus mean : 36.0196

Gaus Width: 3.8982



B1L003S, U19-ch78, adc2

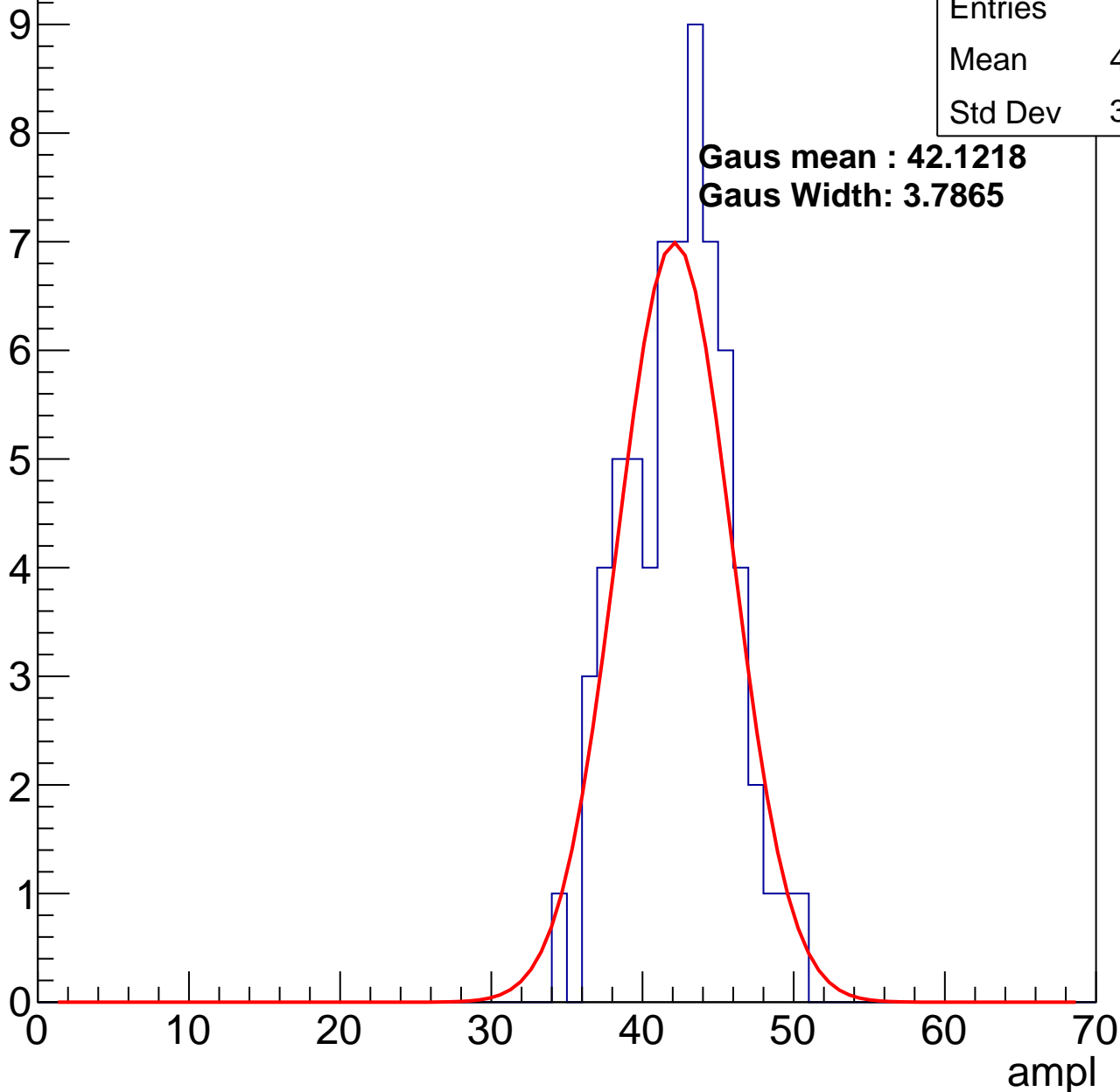
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.88
Std Dev	3.397

Gaus mean : 42.1218

Gaus Width: 3.7865

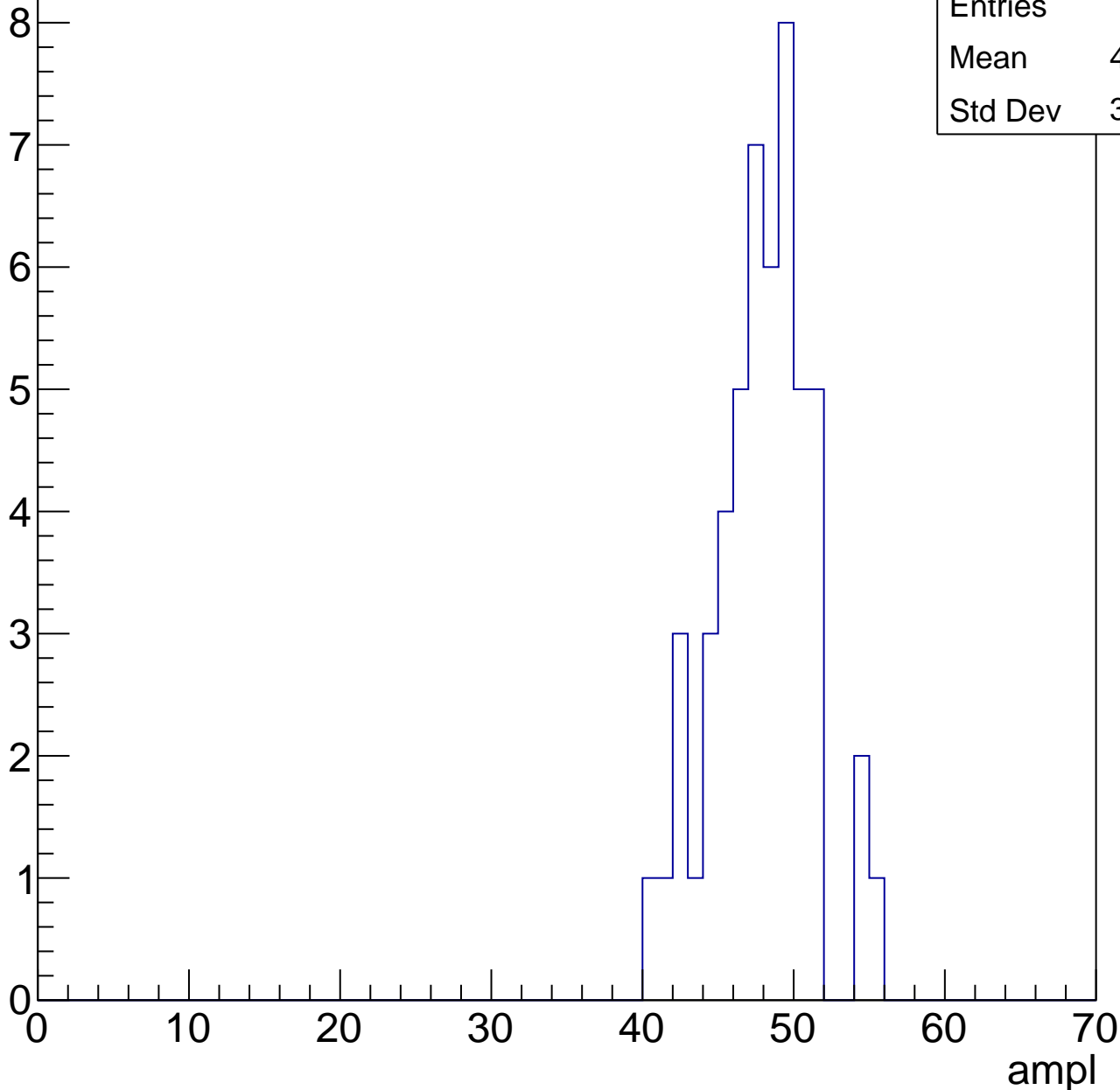


B1L003S, U19-ch78, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	47.48
Std Dev	3.213

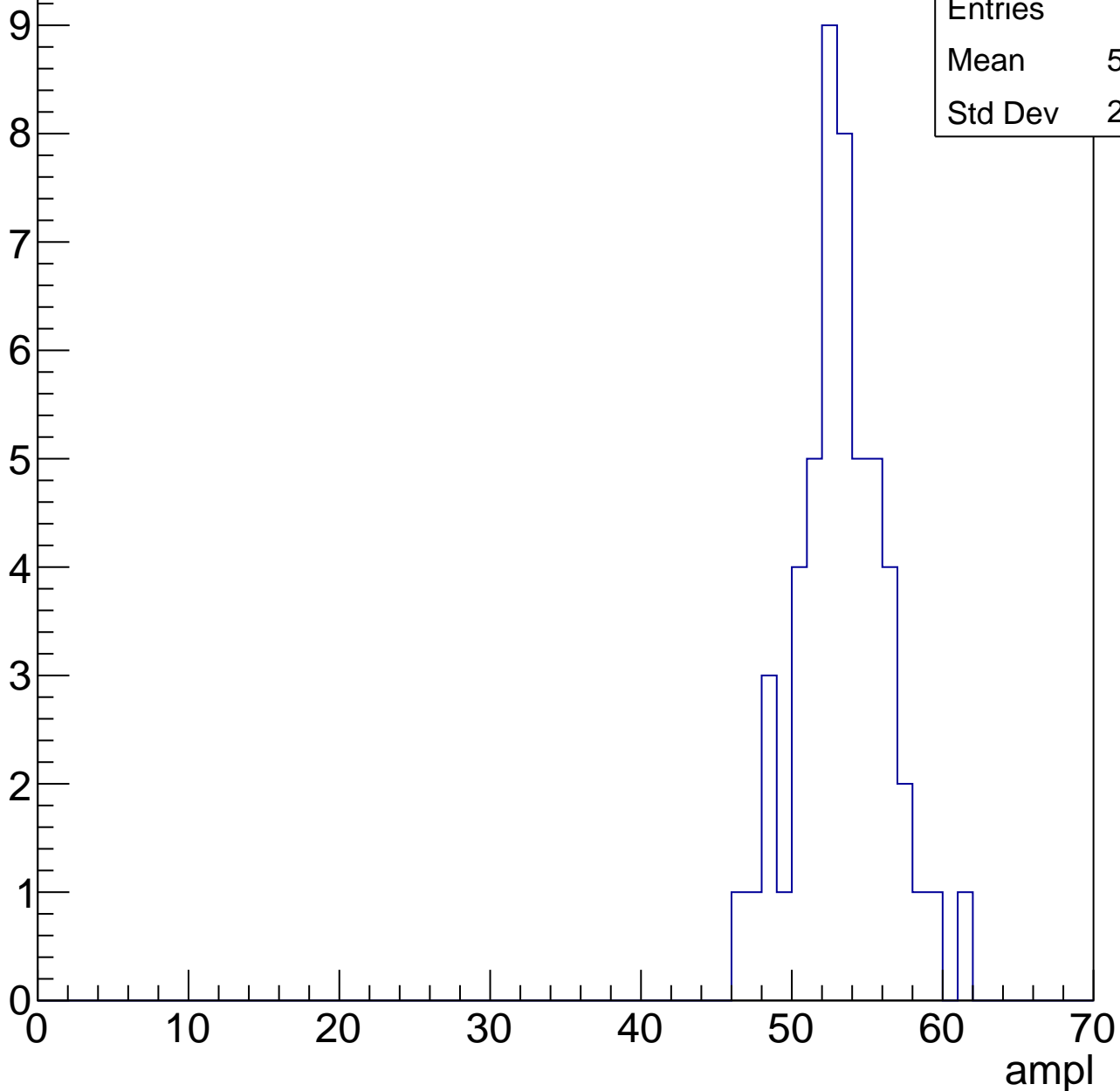


B1L003S, U19-ch78, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	52.82
Std Dev	2.995

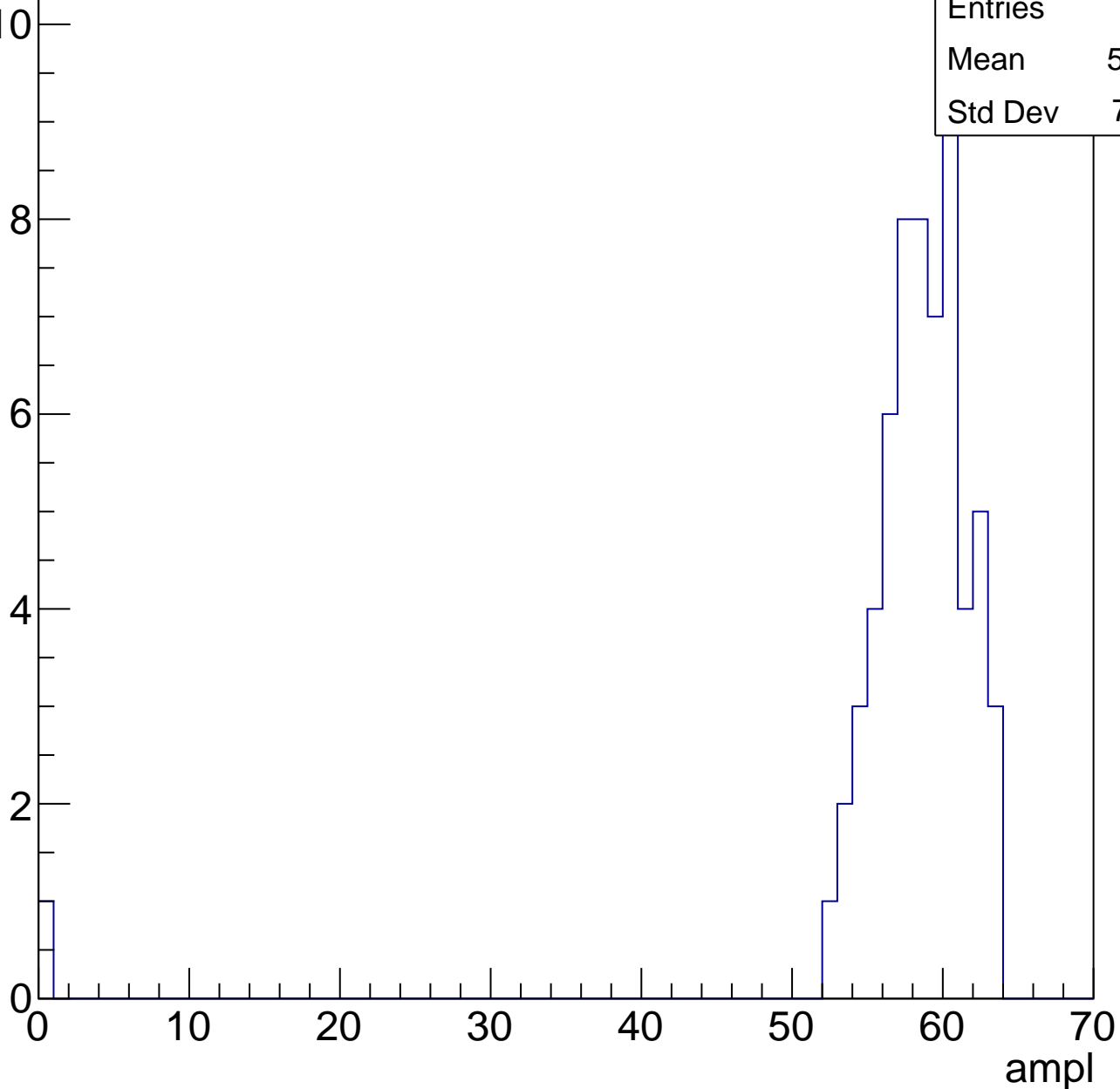


B1L003S, U19-ch78, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	57.29
Std Dev	7.801

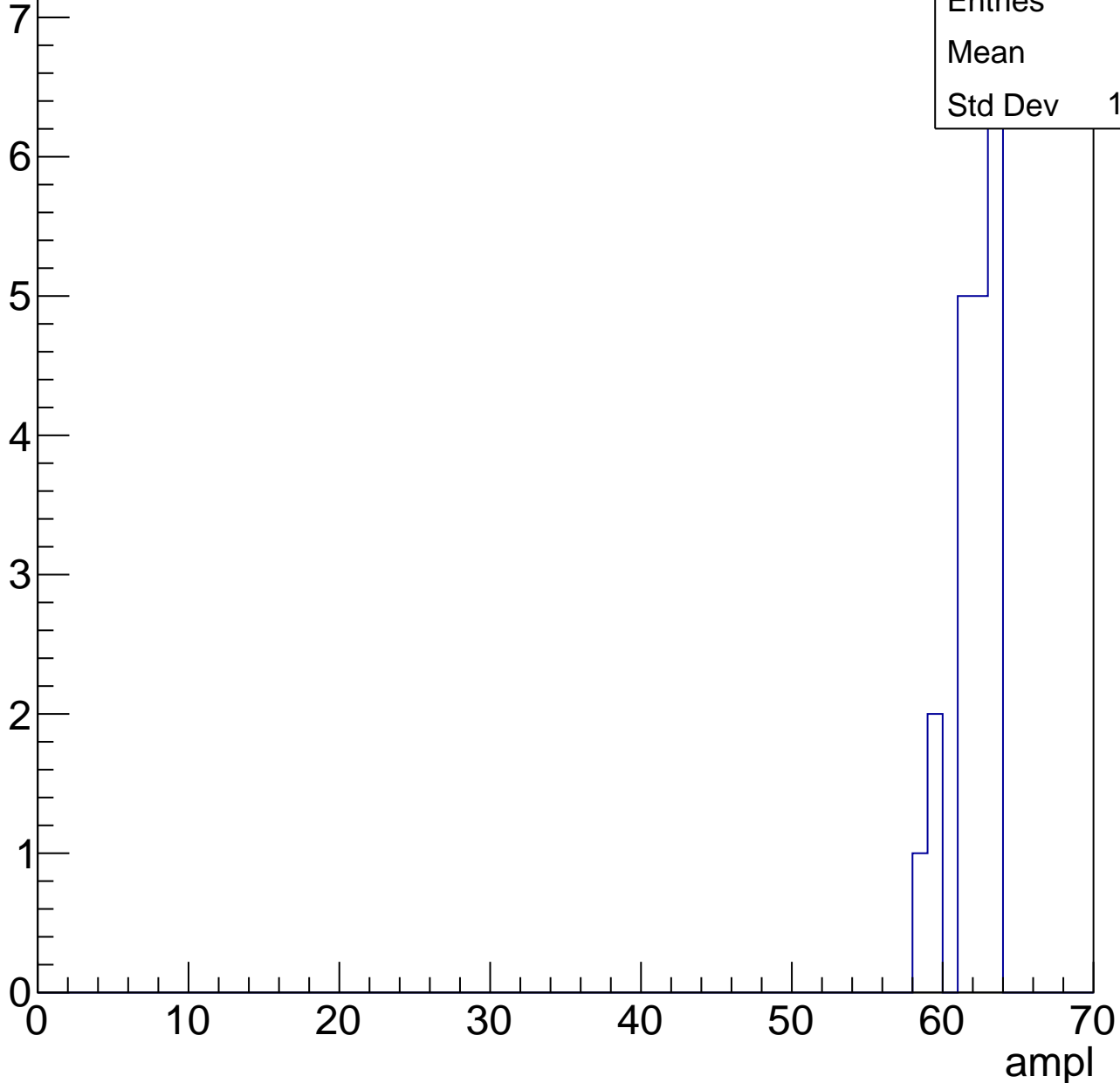


B1L003S, U19-ch78, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	20
Mean	61.6
Std Dev	1.463



B1L003S, U19-ch78, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch79, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	63
Mean	29.05
Std Dev	4.61

Gaus mean : 29.8499

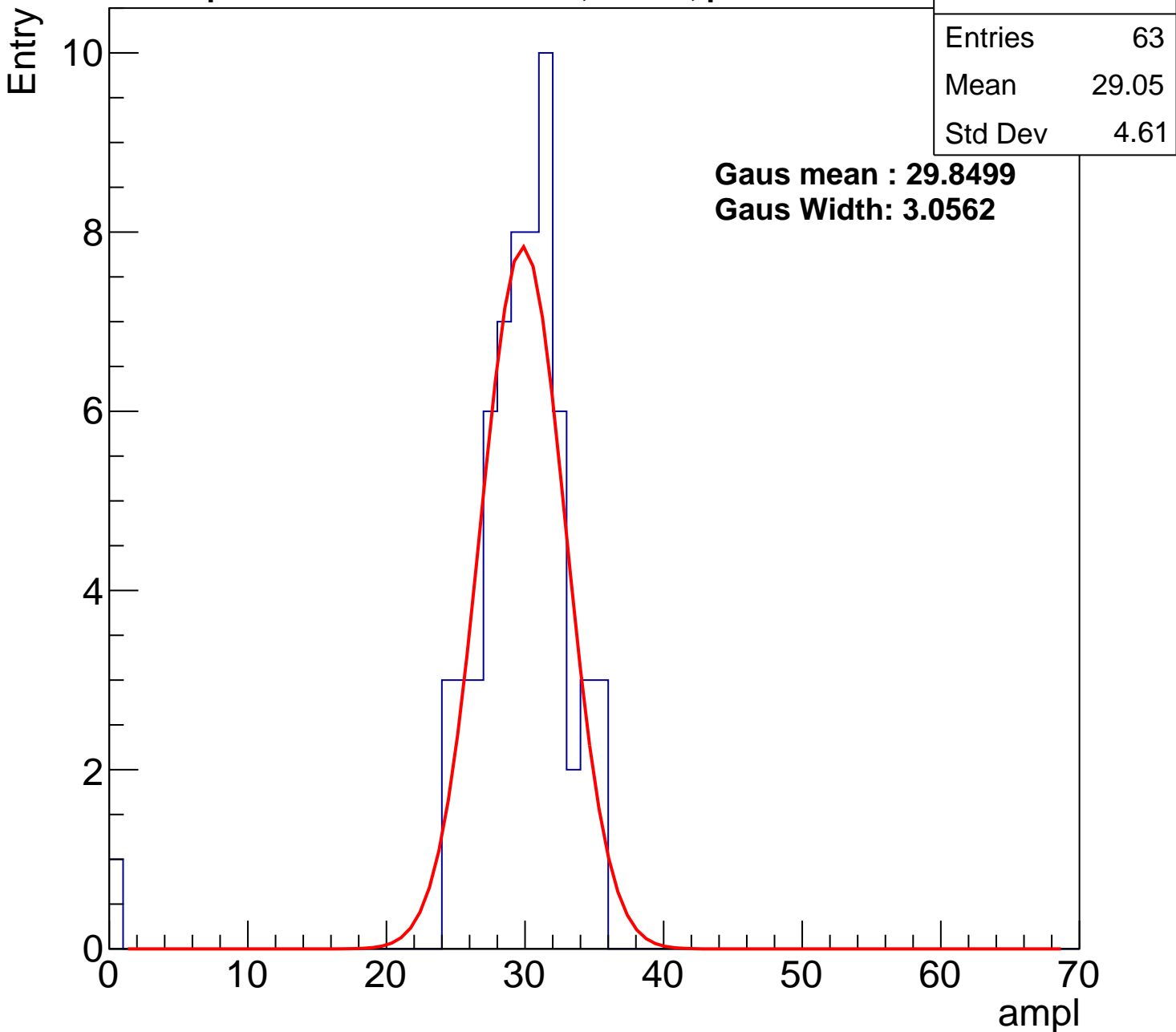
Gaus Width: 3.0562

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



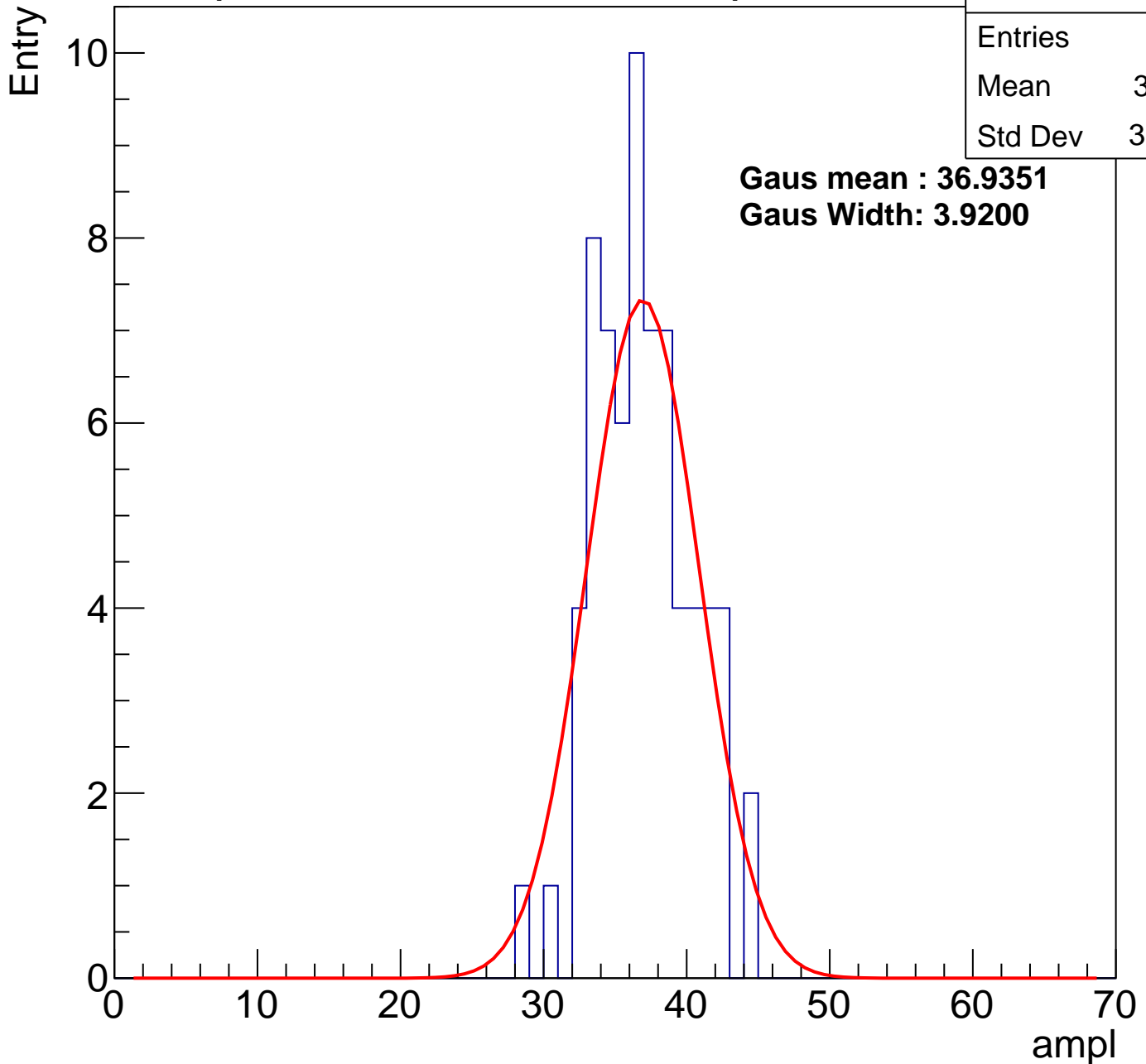
B1L003S, U19-ch79, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	36.51
Std Dev	3.317

Gaus mean : 36.9351

Gaus Width: 3.9200



B1L003S, U19-ch79, adc2

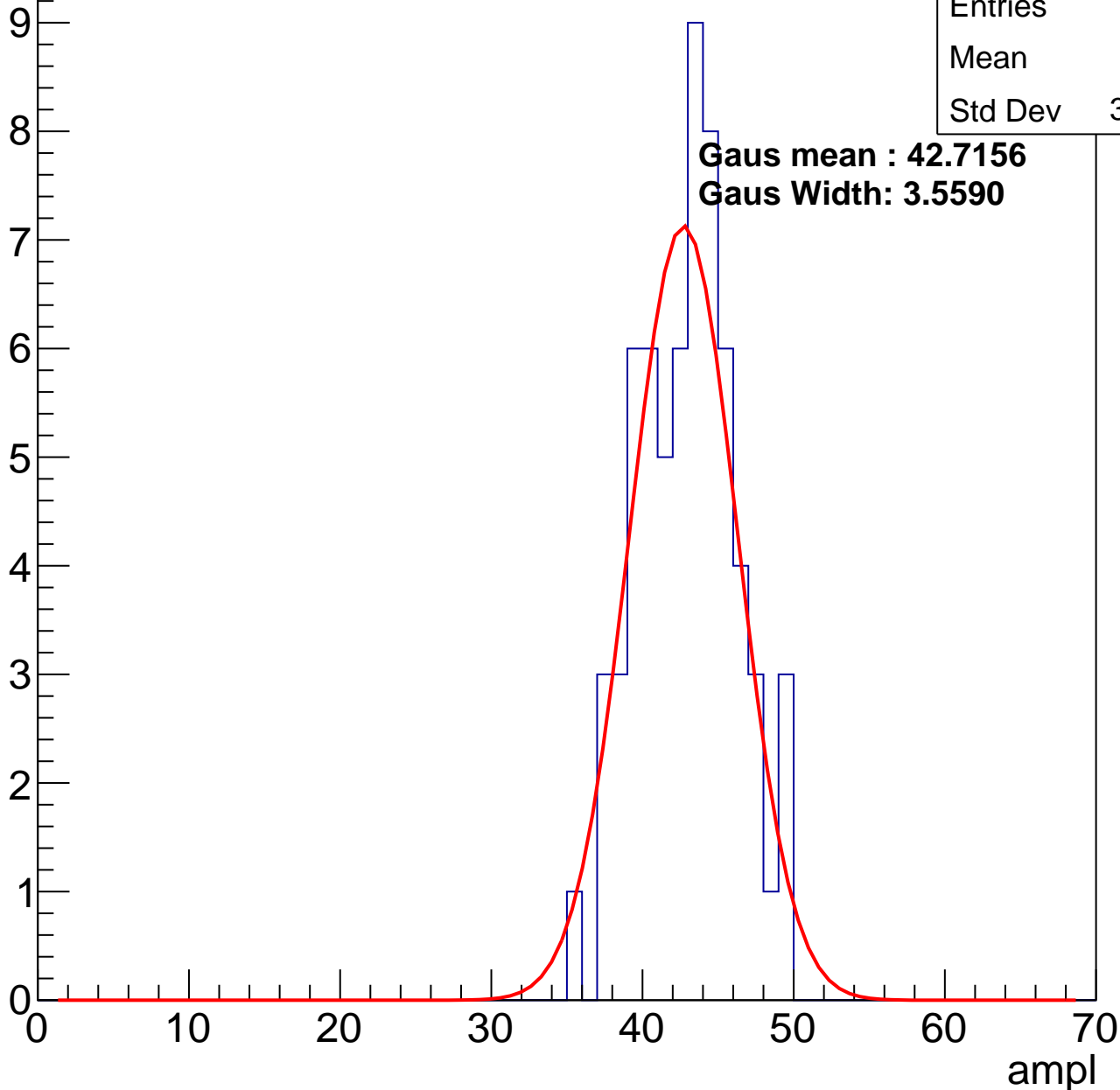
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.5
Std Dev	3.202

Gaus mean : 42.7156

Gaus Width: 3.5590

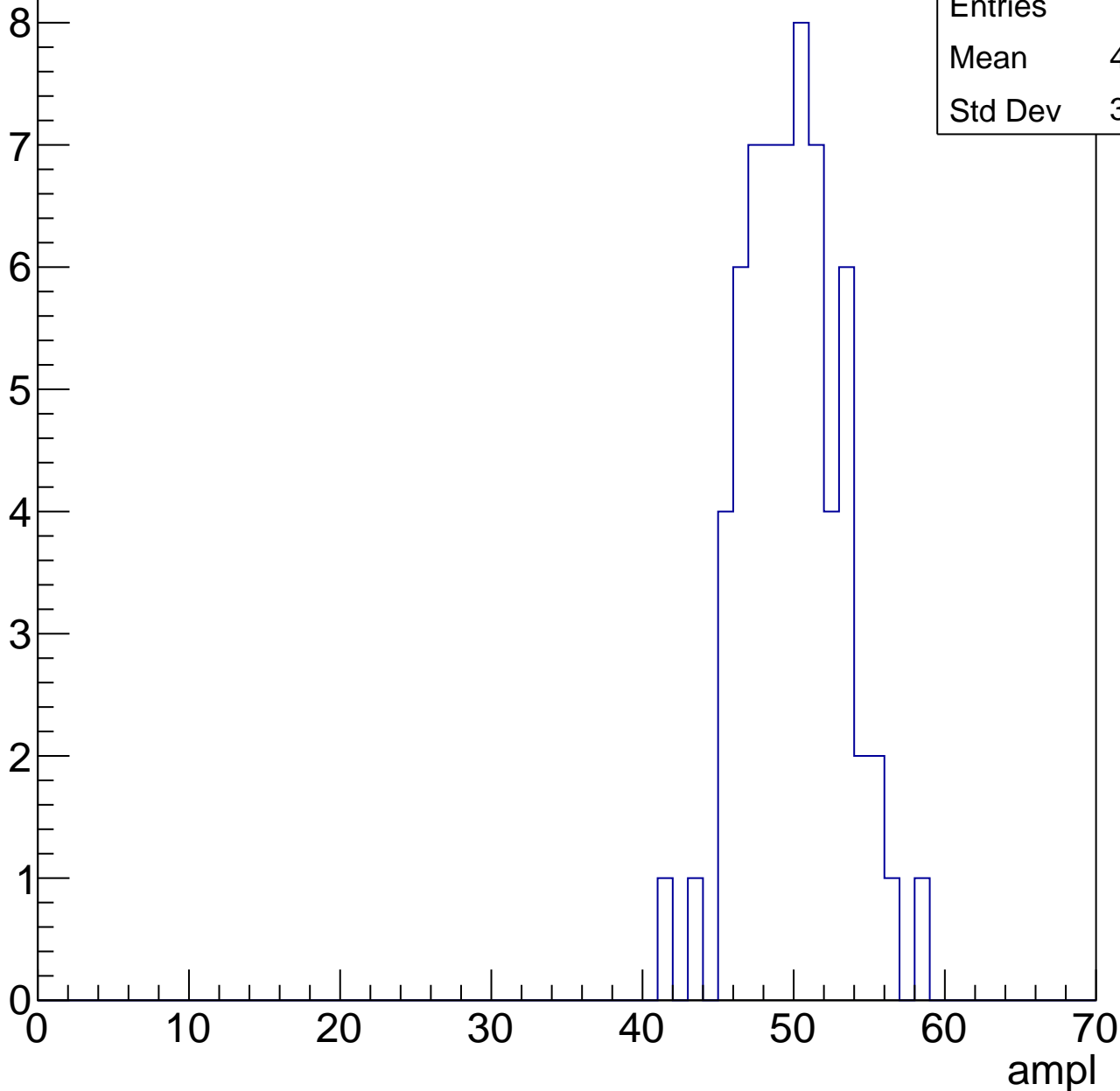


B1L003S, U19-ch79, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.42
Std Dev	3.215

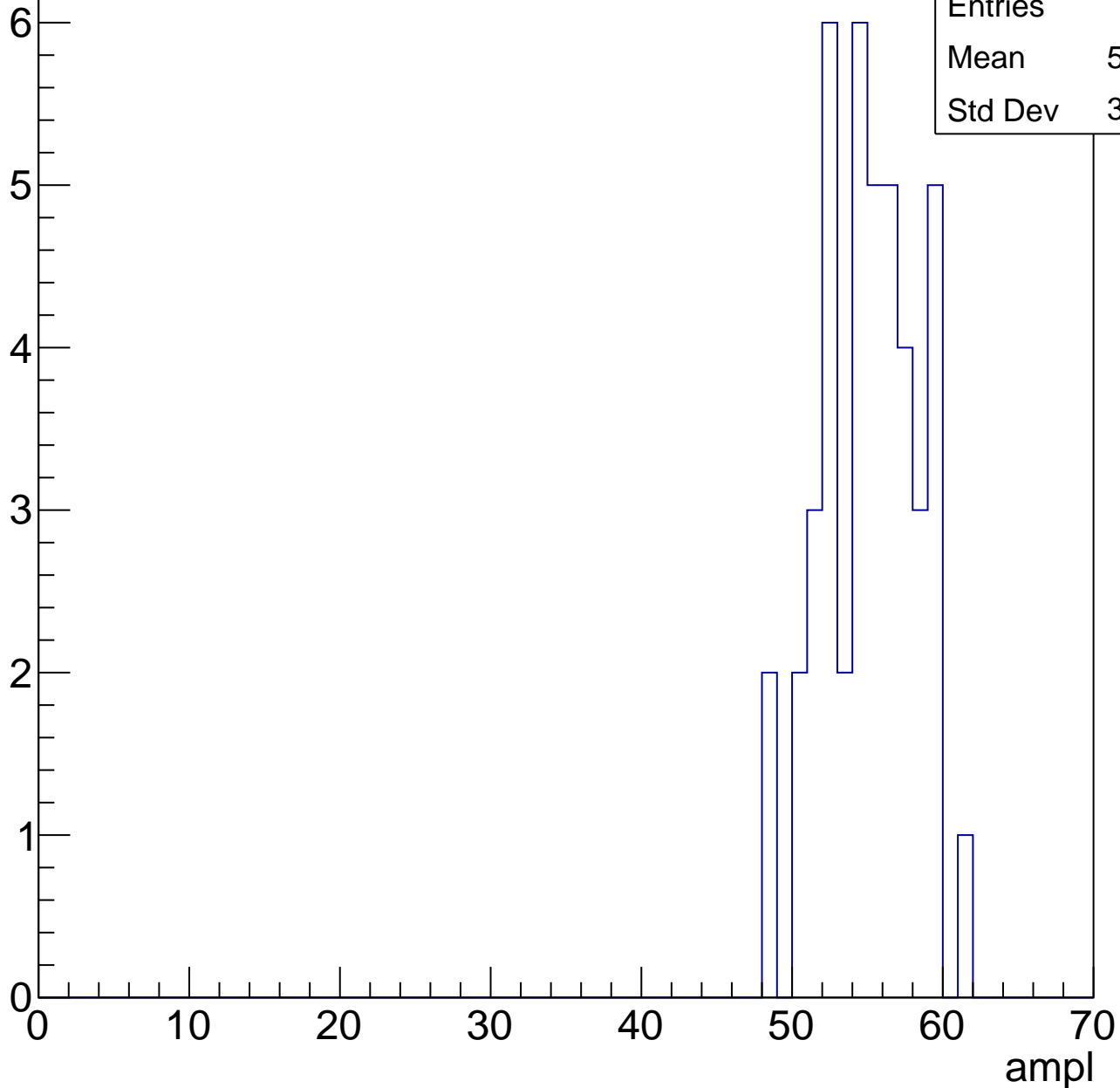


B1L003S, U19-ch79, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	54.64
Std Dev	3.098



B1L003S, U19-ch79, adc5

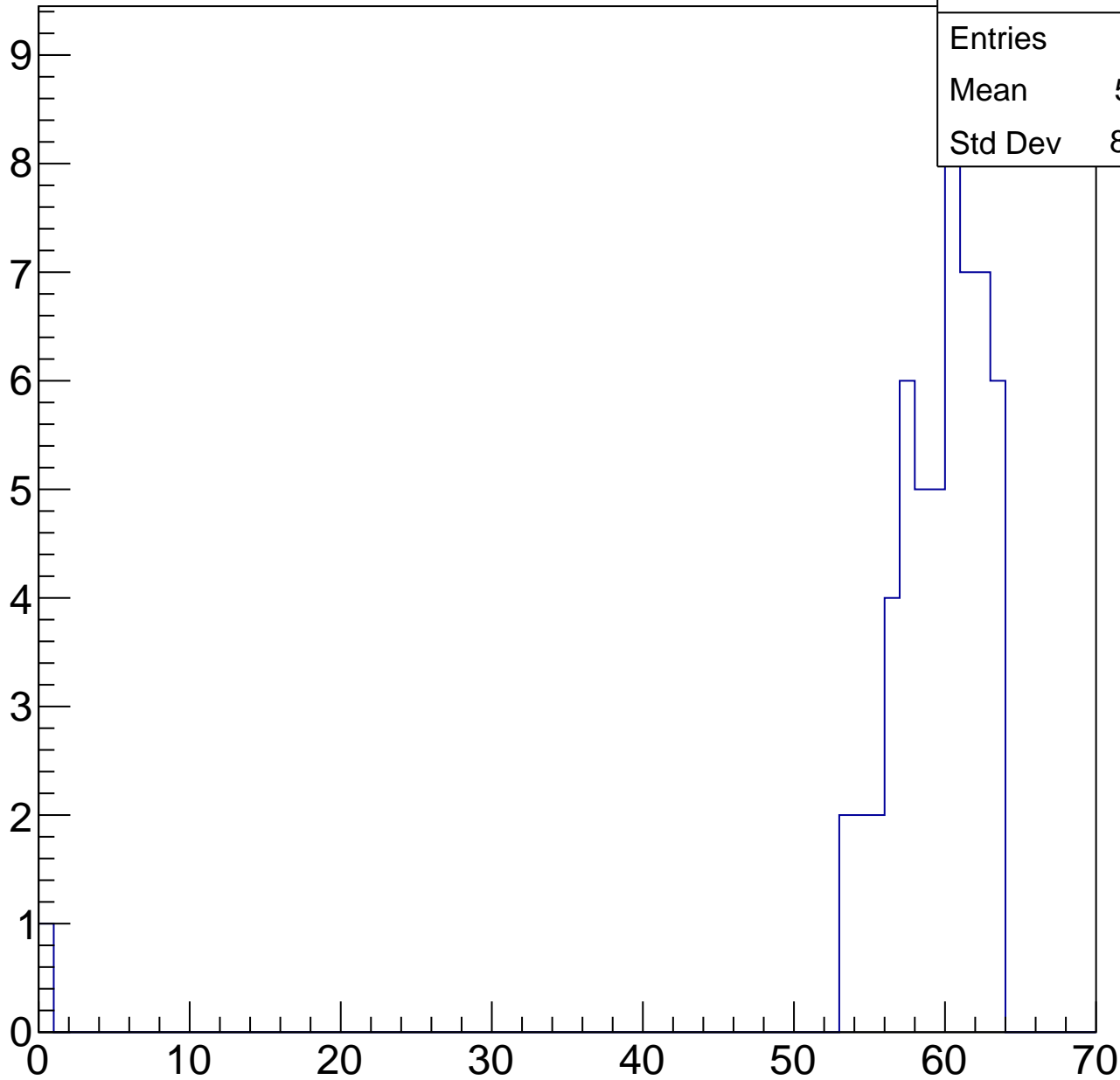
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.11
Std Dev	8.293

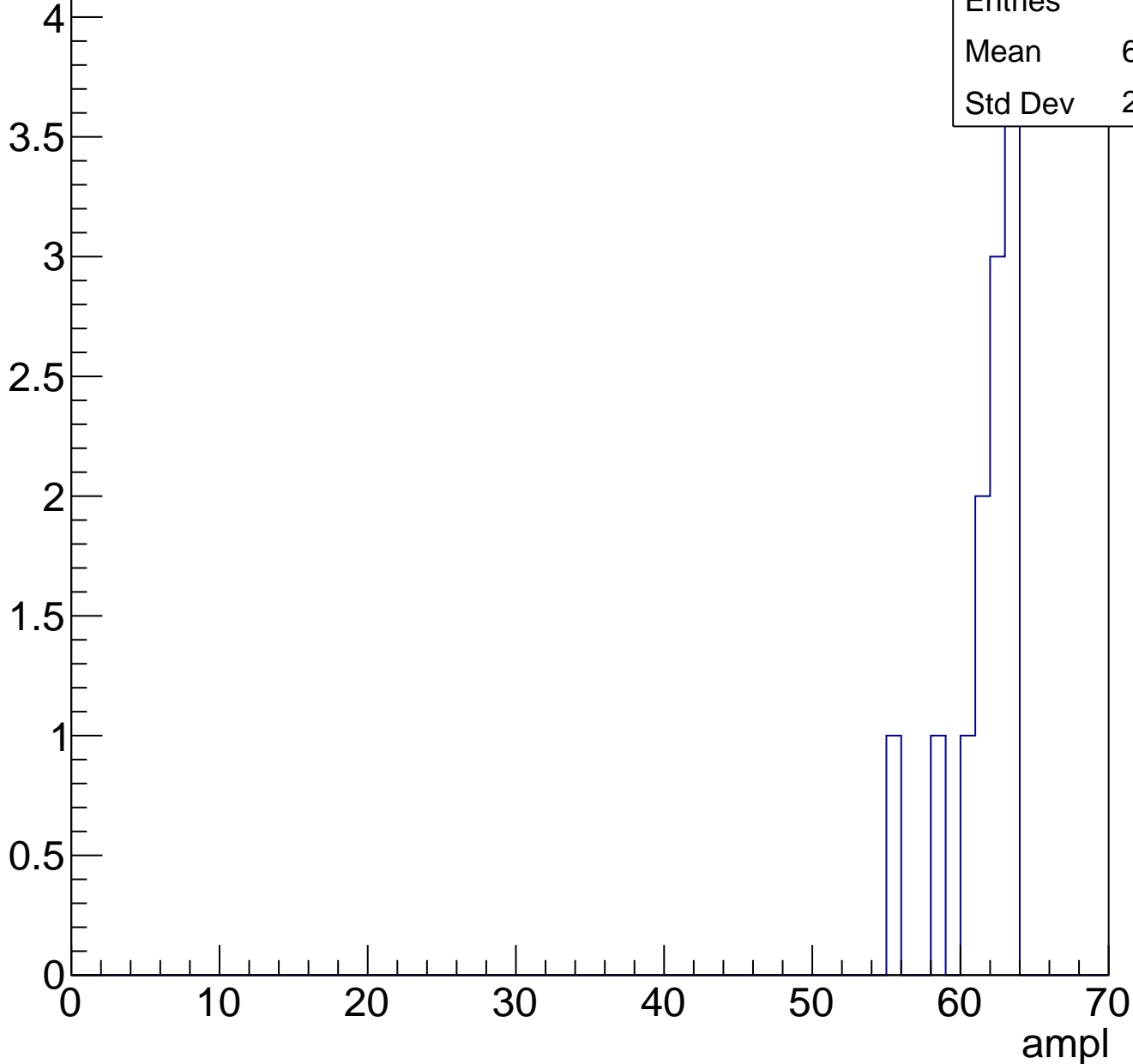
ampl



B1L003S, U19-ch79, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch79, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch80, adc0

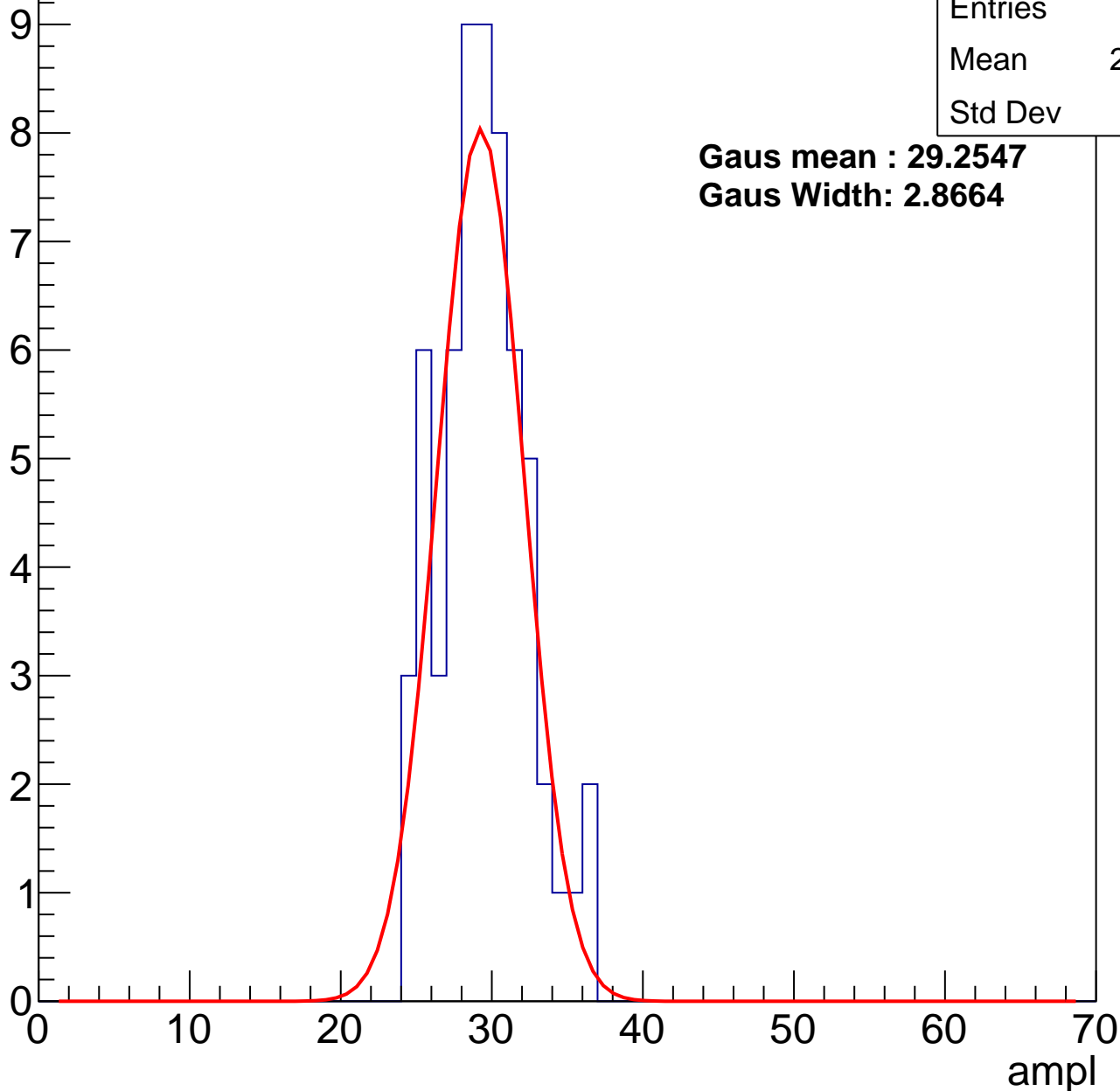
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.98
Std Dev	2.86

Gaus mean : 29.2547

Gaus Width: 2.8664



B1L003S, U19-ch80, adc1

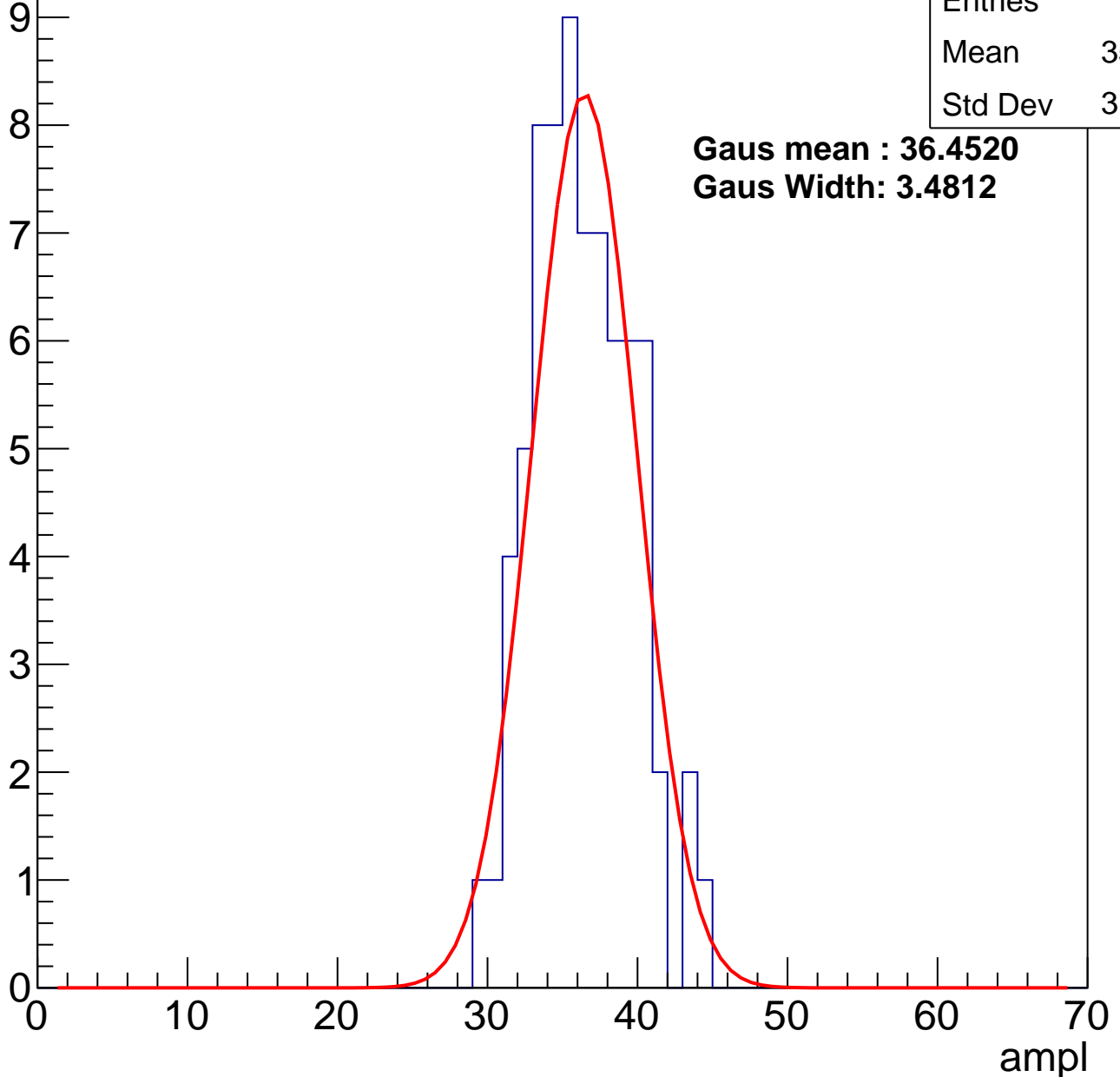
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	35.88
Std Dev	3.248

Gaus mean : 36.4520

Gaus Width: 3.4812



B1L003S, U19-ch80, adc2

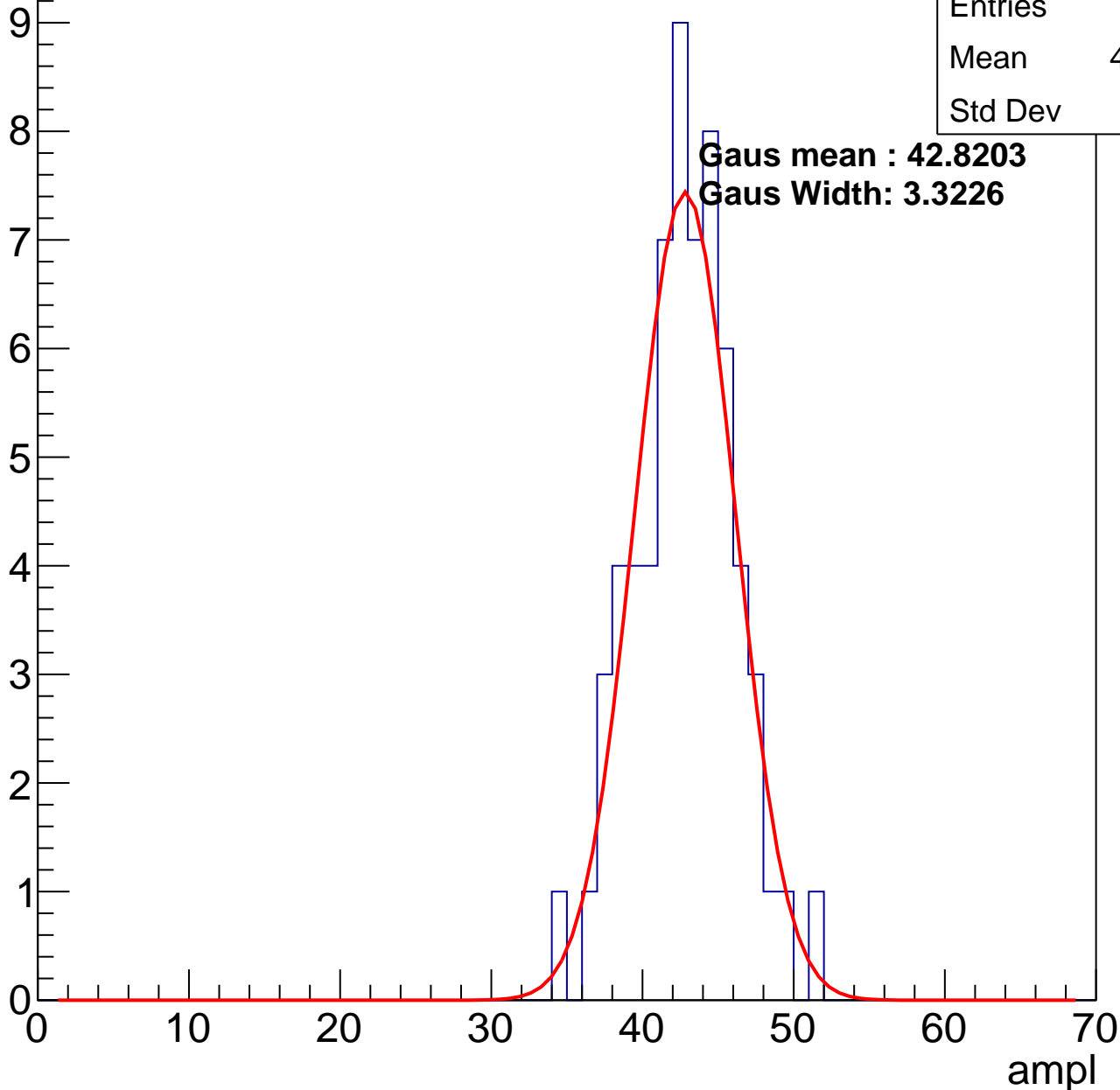
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.34
Std Dev	3.28

Gaus mean : 42.8203

Gaus Width: 3.3226

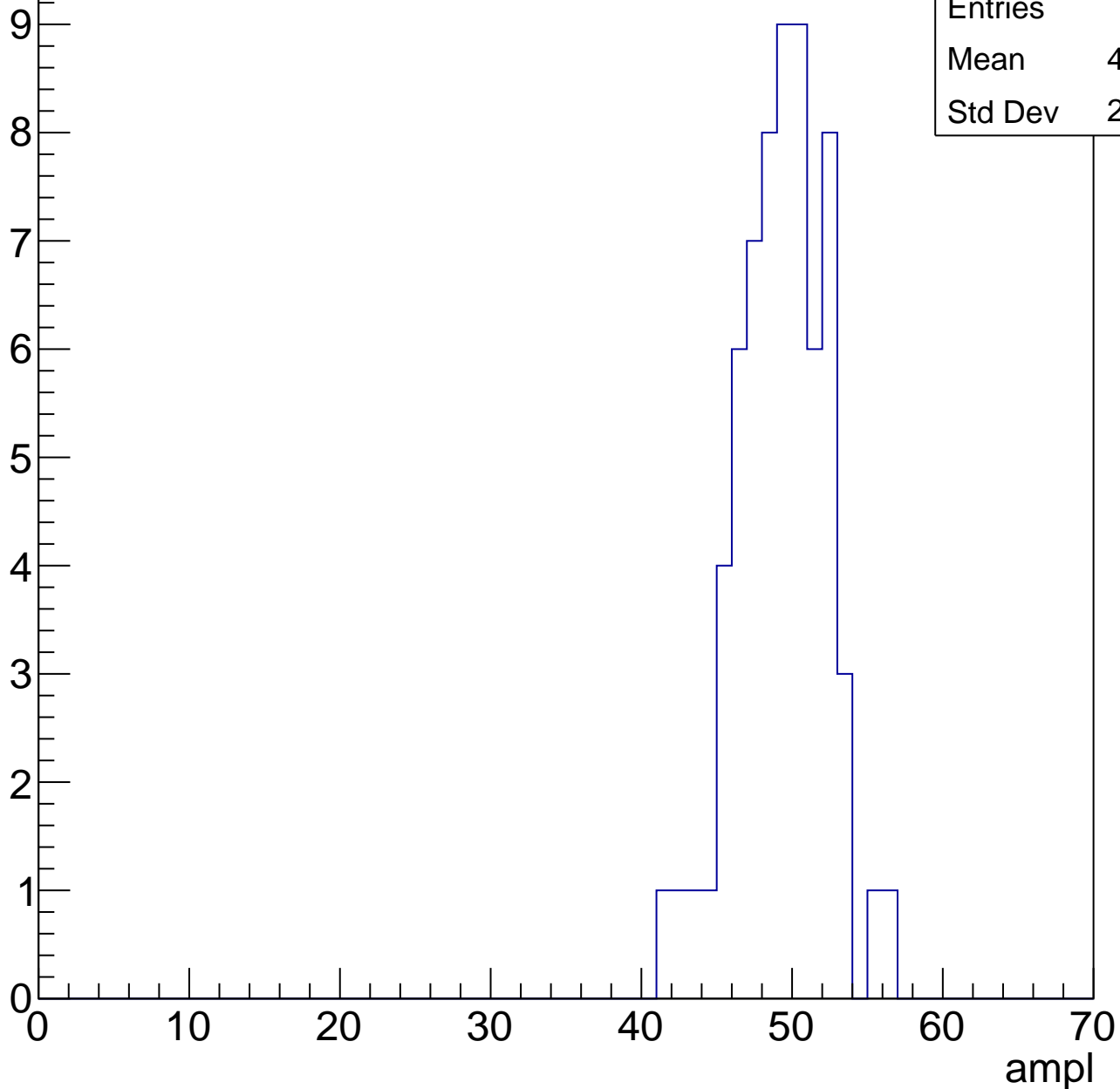


B1L003S, U19-ch80, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	48.82
Std Dev	2.923

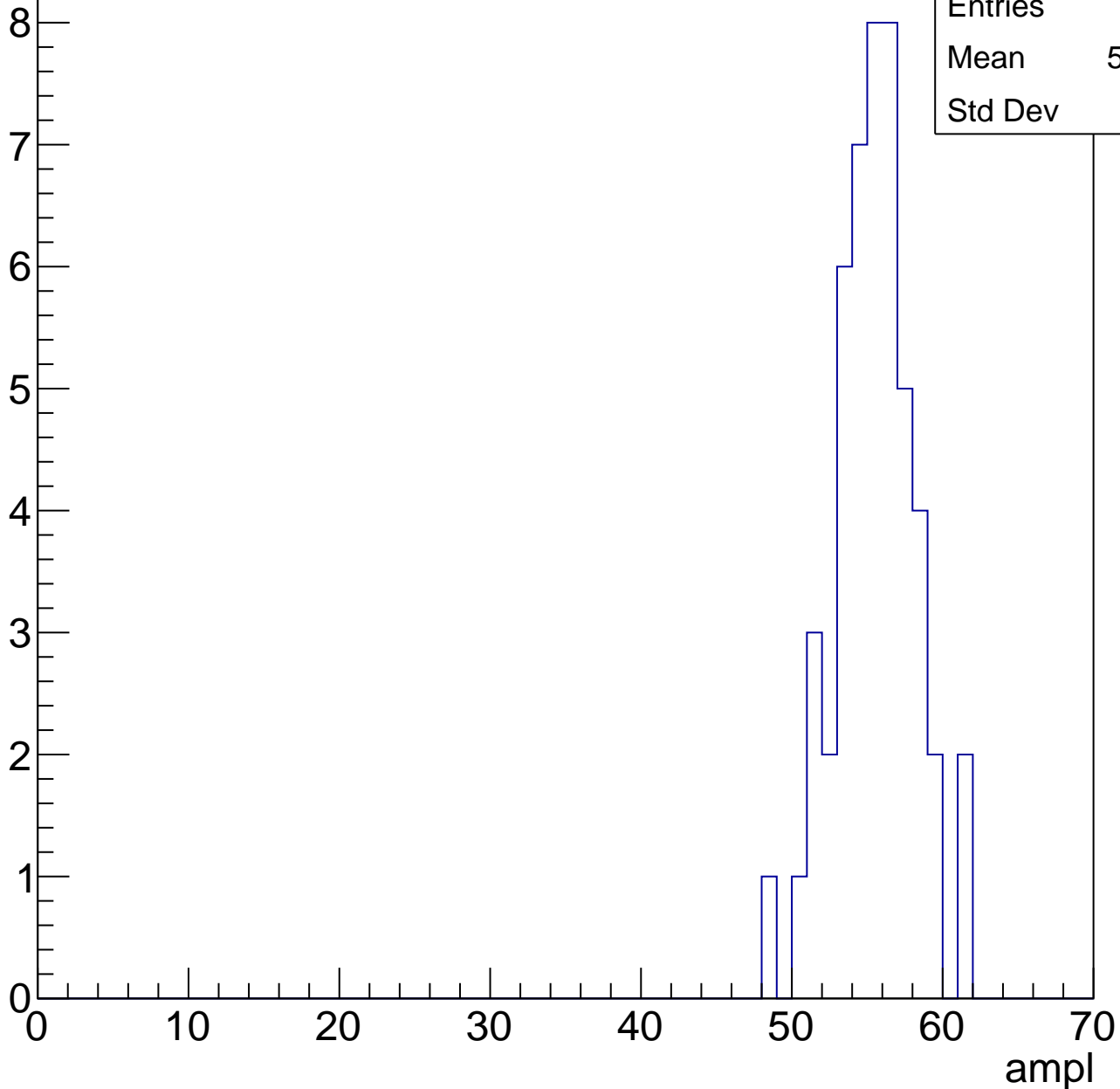


B1L003S, U19-ch80, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55.02
Std Dev	2.63

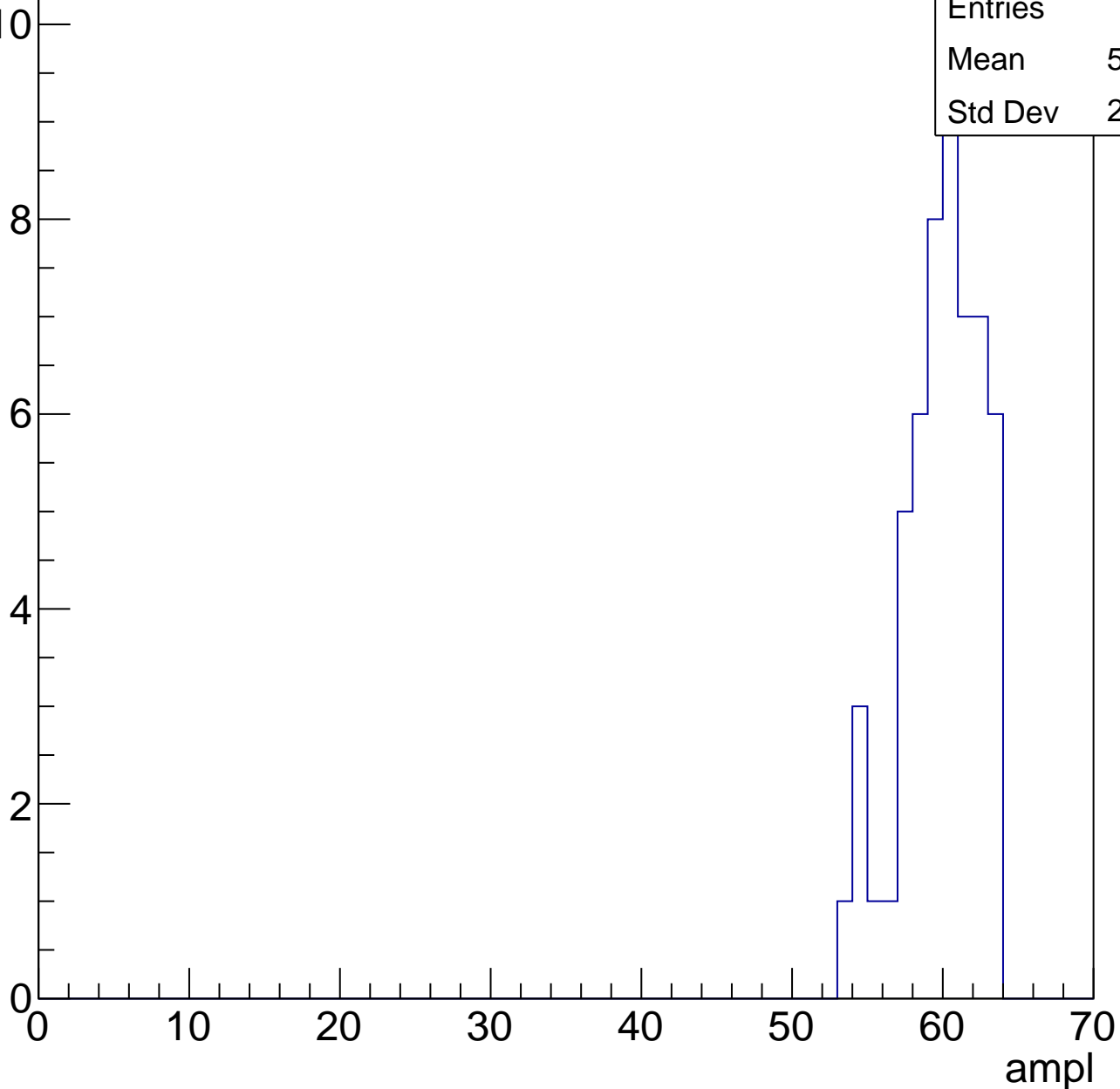


B1L003S, U19-ch80, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

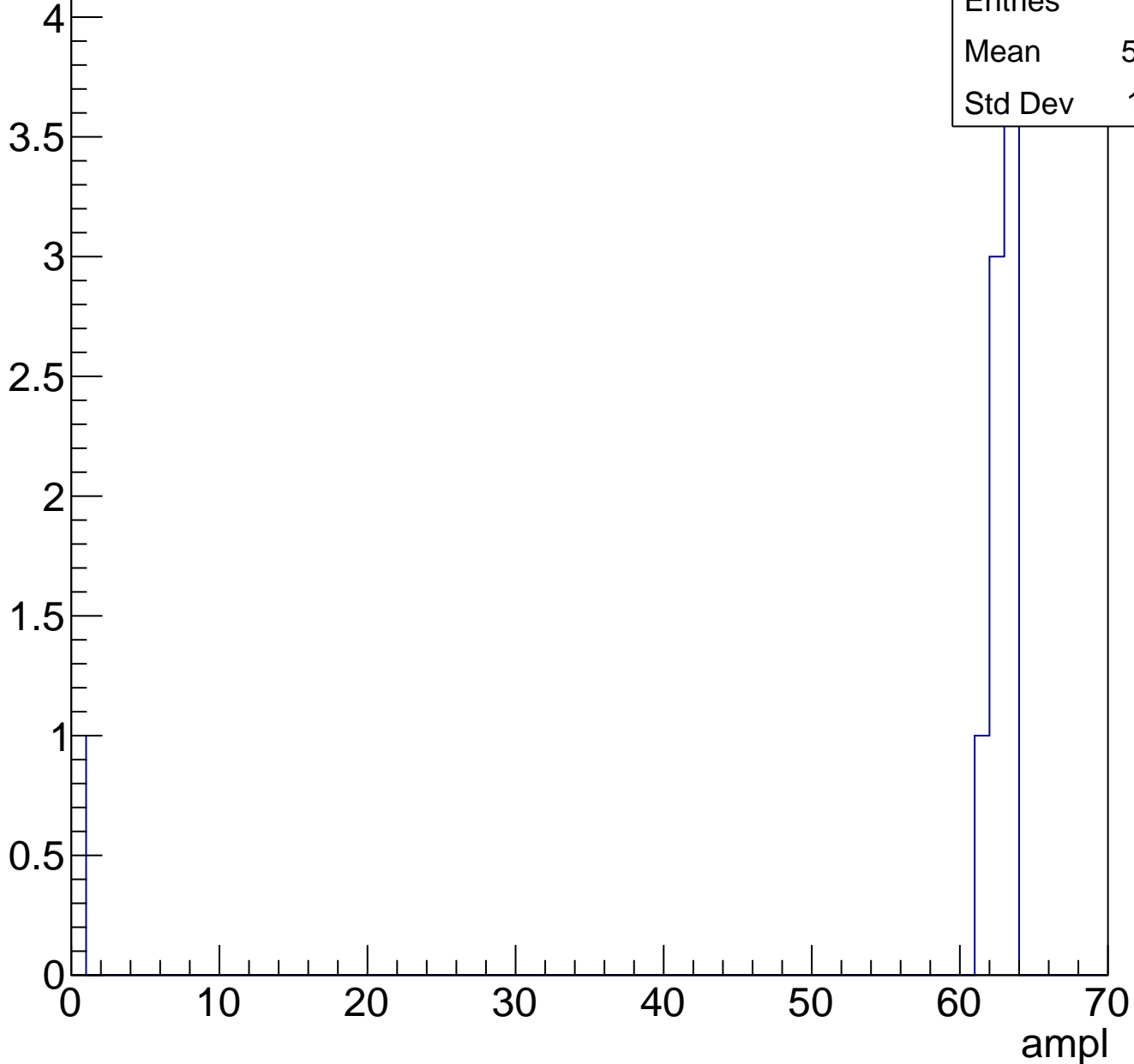
Entries	55
Mean	59.45
Std Dev	2.514



B1L003S, U19-ch80, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch80, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L003S, U19-ch81, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	80
Mean	29.89
Std Dev	3.65

Gaus mean : 30.7013

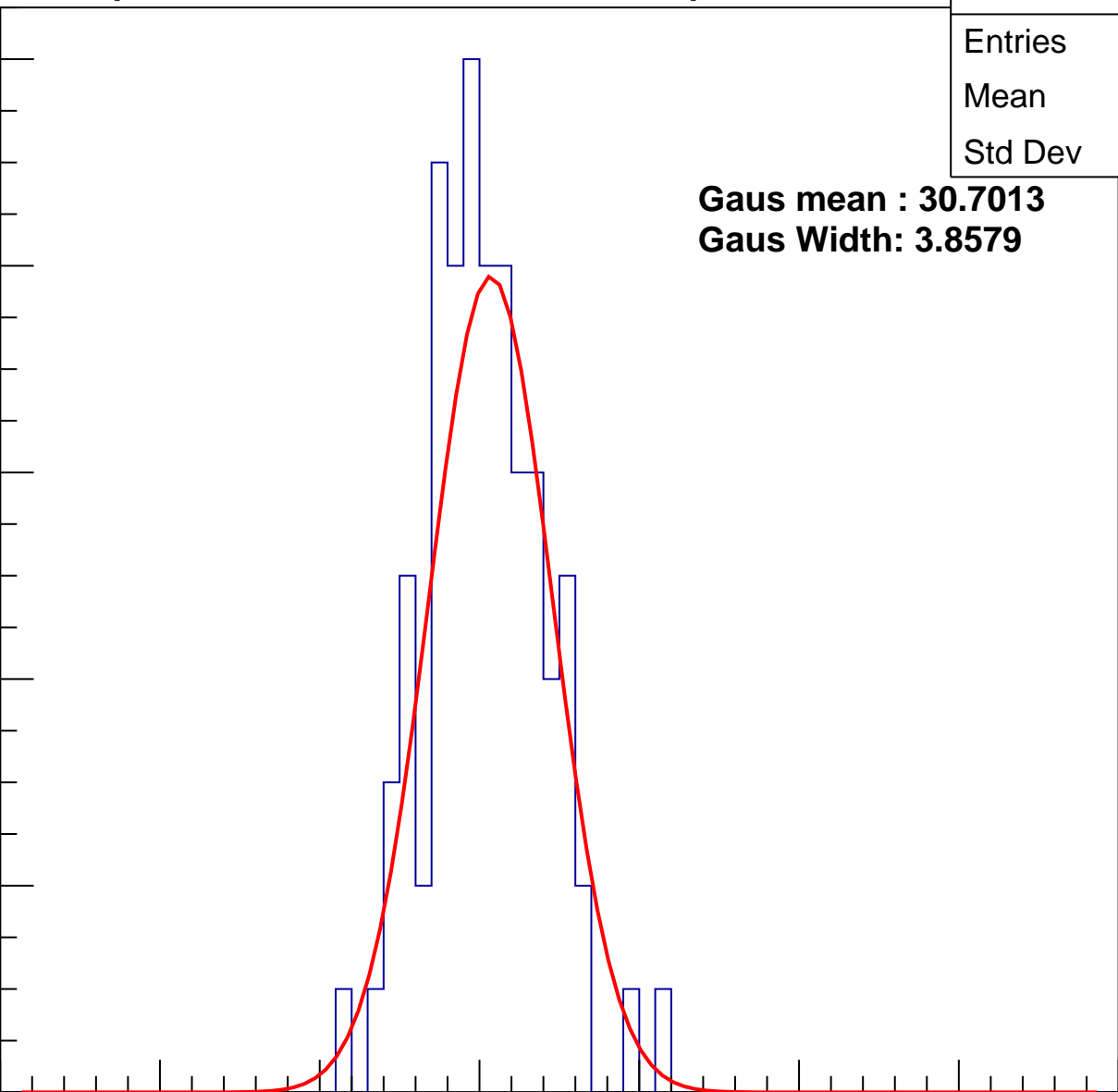
Gaus Width: 3.8579

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



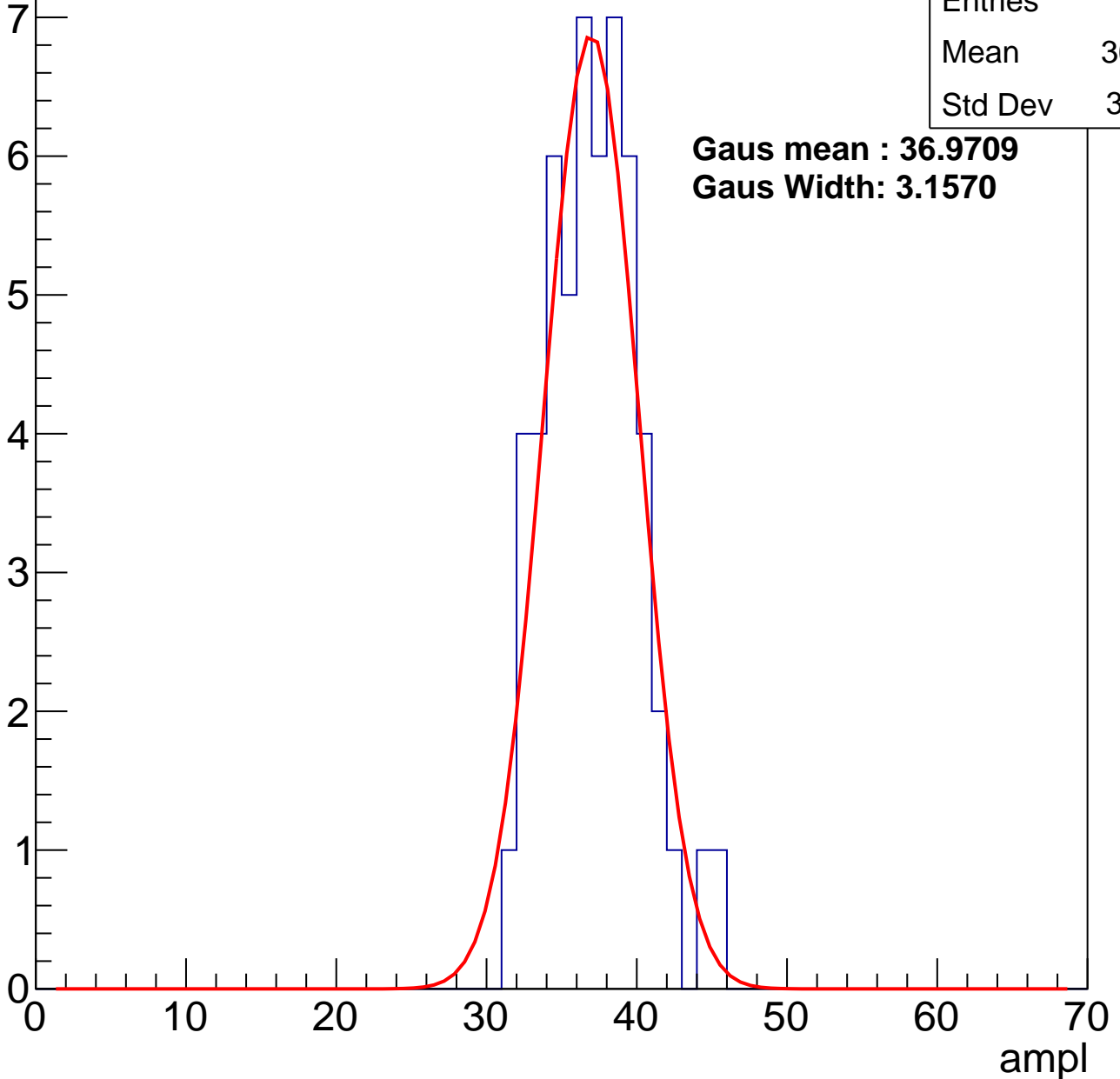
B1L003S, U19-ch81, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	36.67
Std Dev	3.051

Gaus mean : 36.9709
Gaus Width: 3.1570

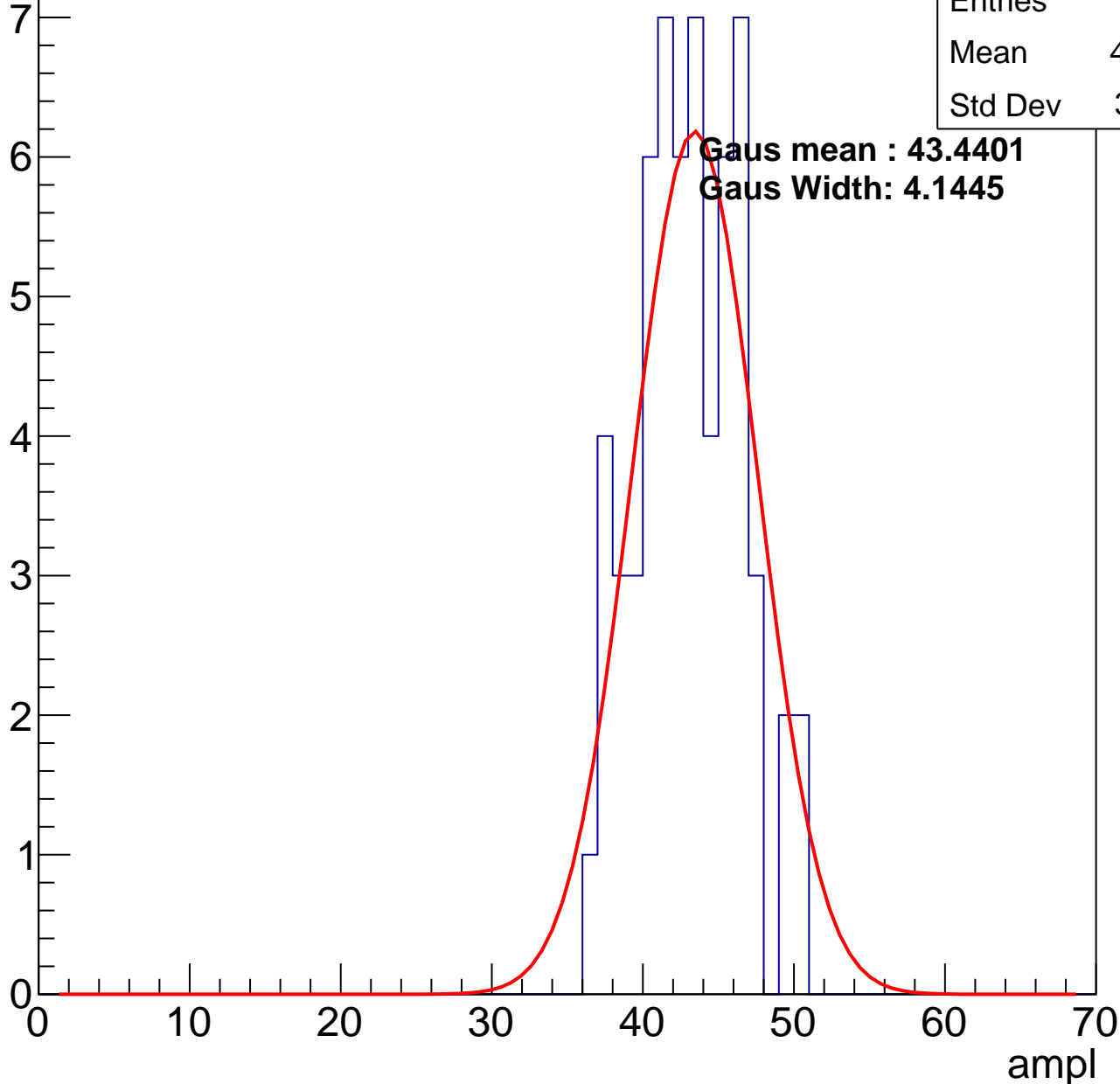


B1L003S, U19-ch81, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.66
Std Dev	3.401

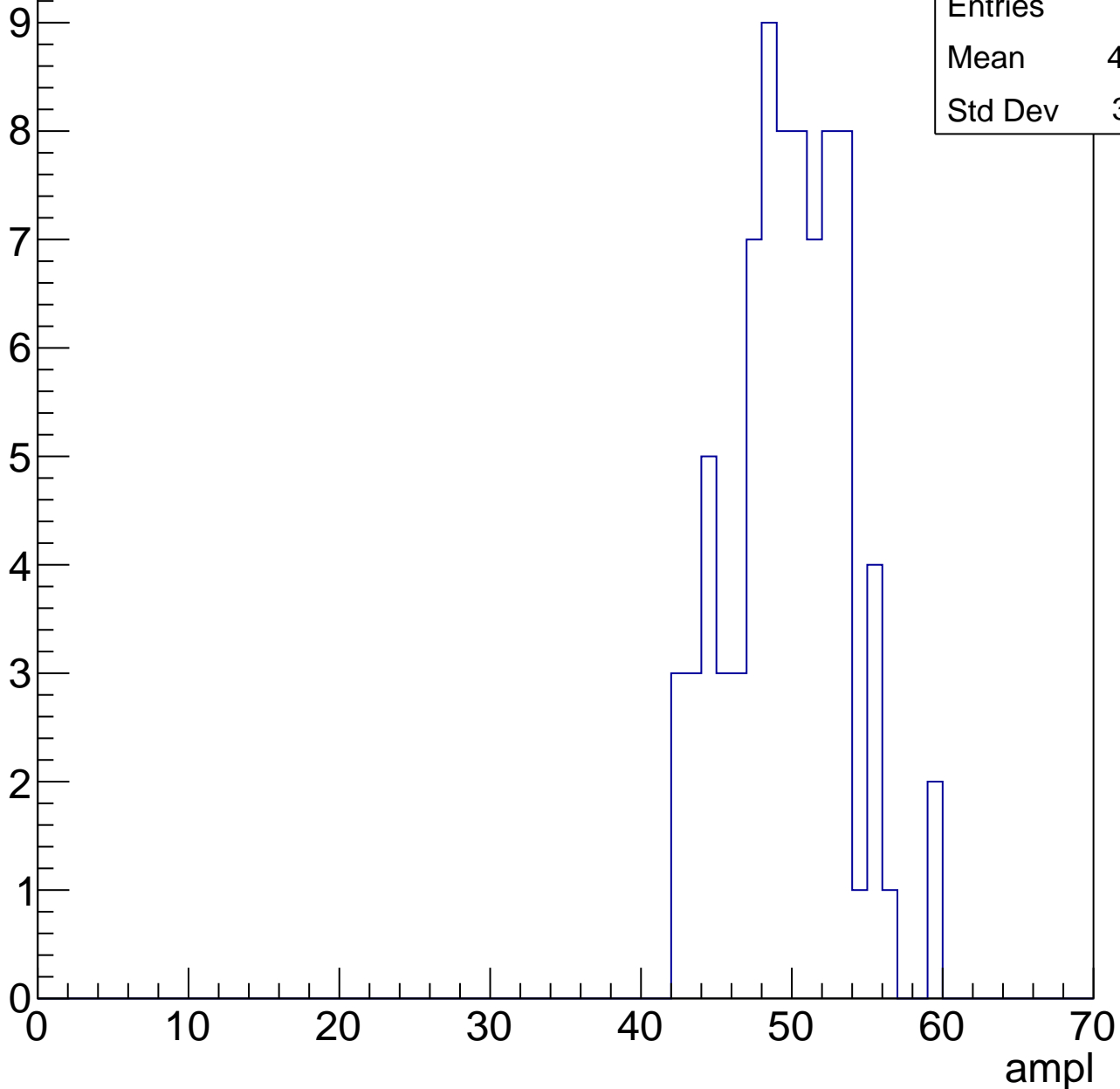


B1L003S, U19-ch81, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	49.33
Std Dev	3.771

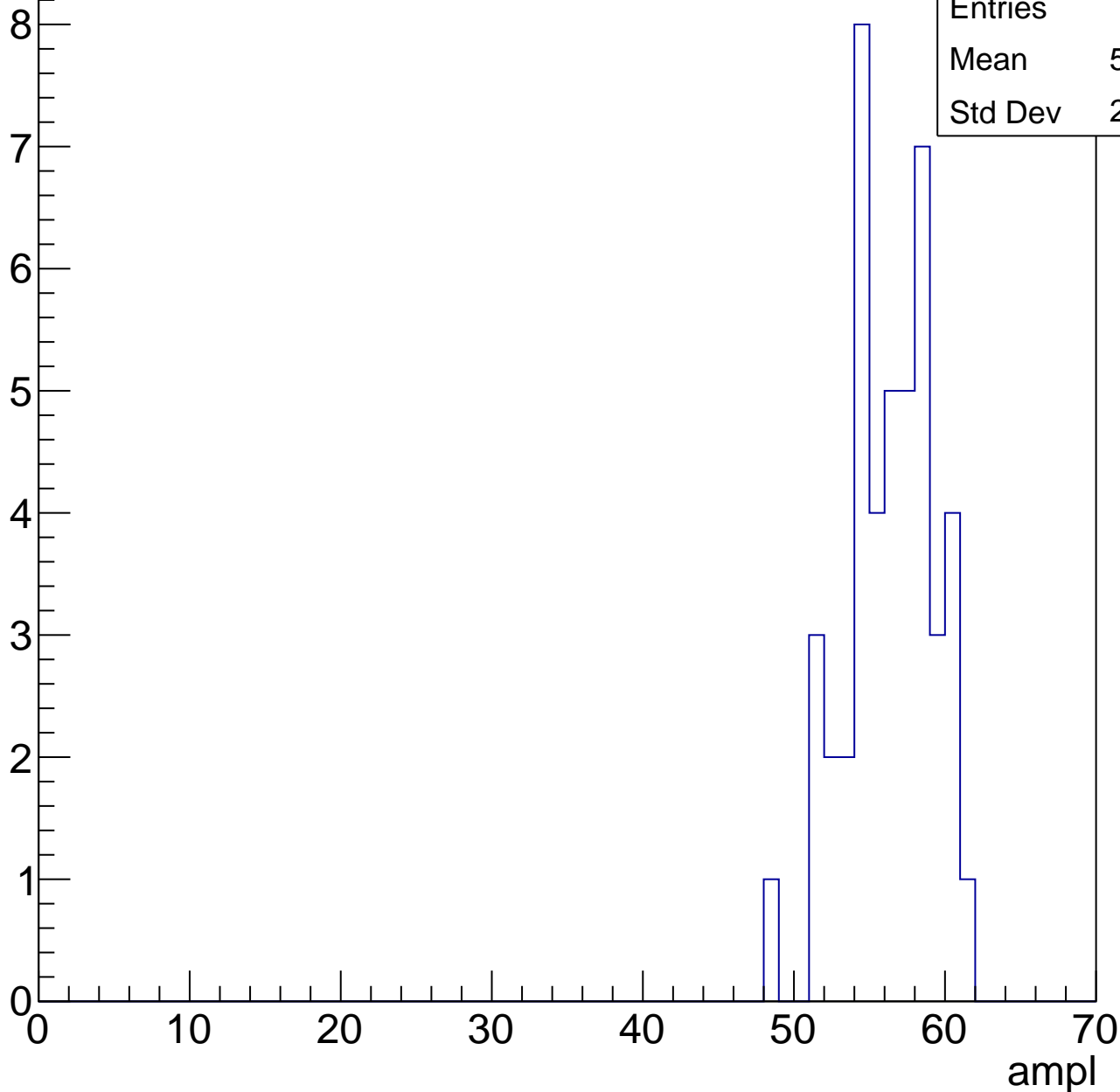


B1L003S, U19-ch81, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	55.82
Std Dev	2.877

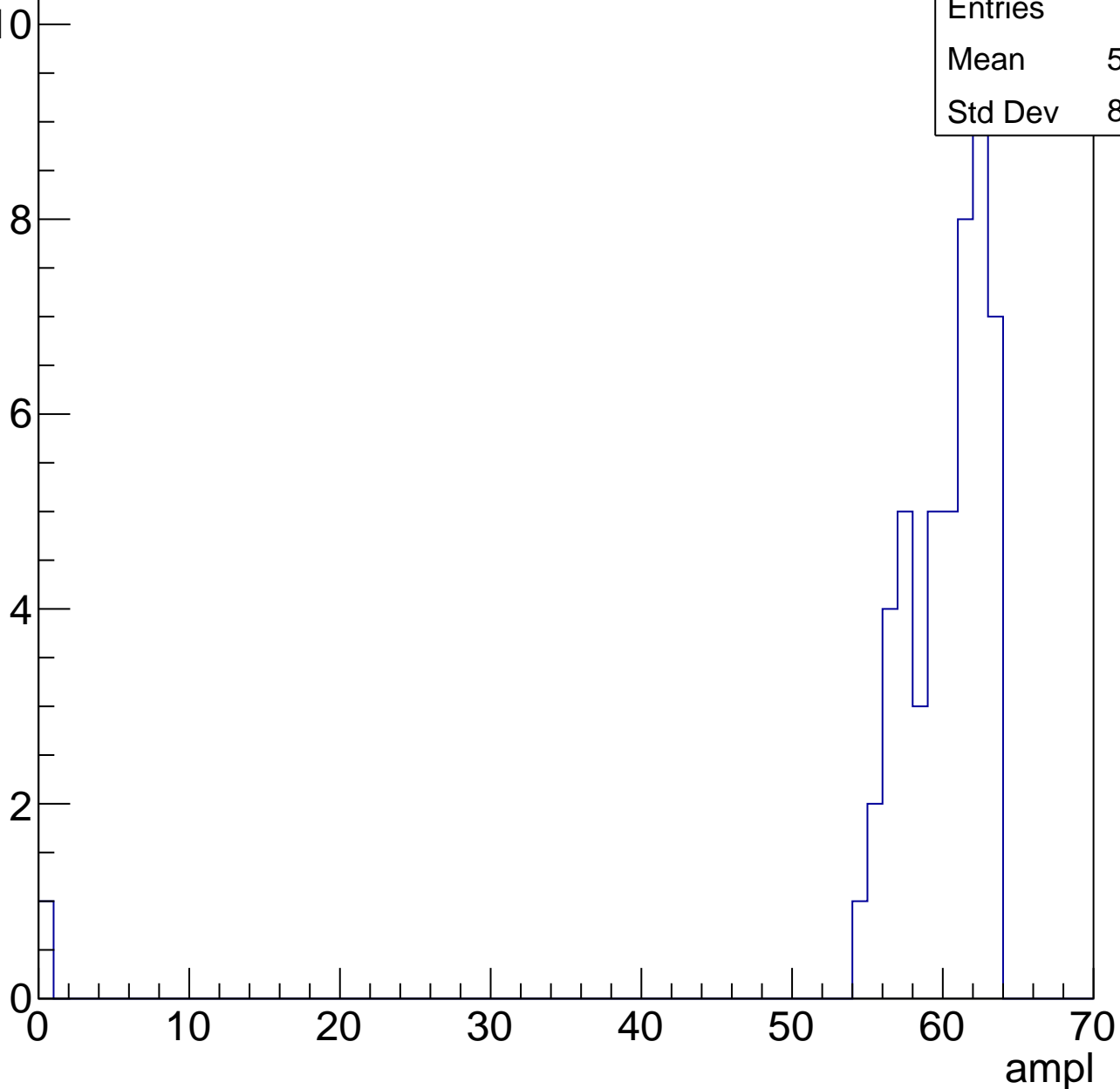


B1L003S, U19-ch81, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.65
Std Dev	8.666



B1L003S, U19-ch81, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch81, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch82, adc0

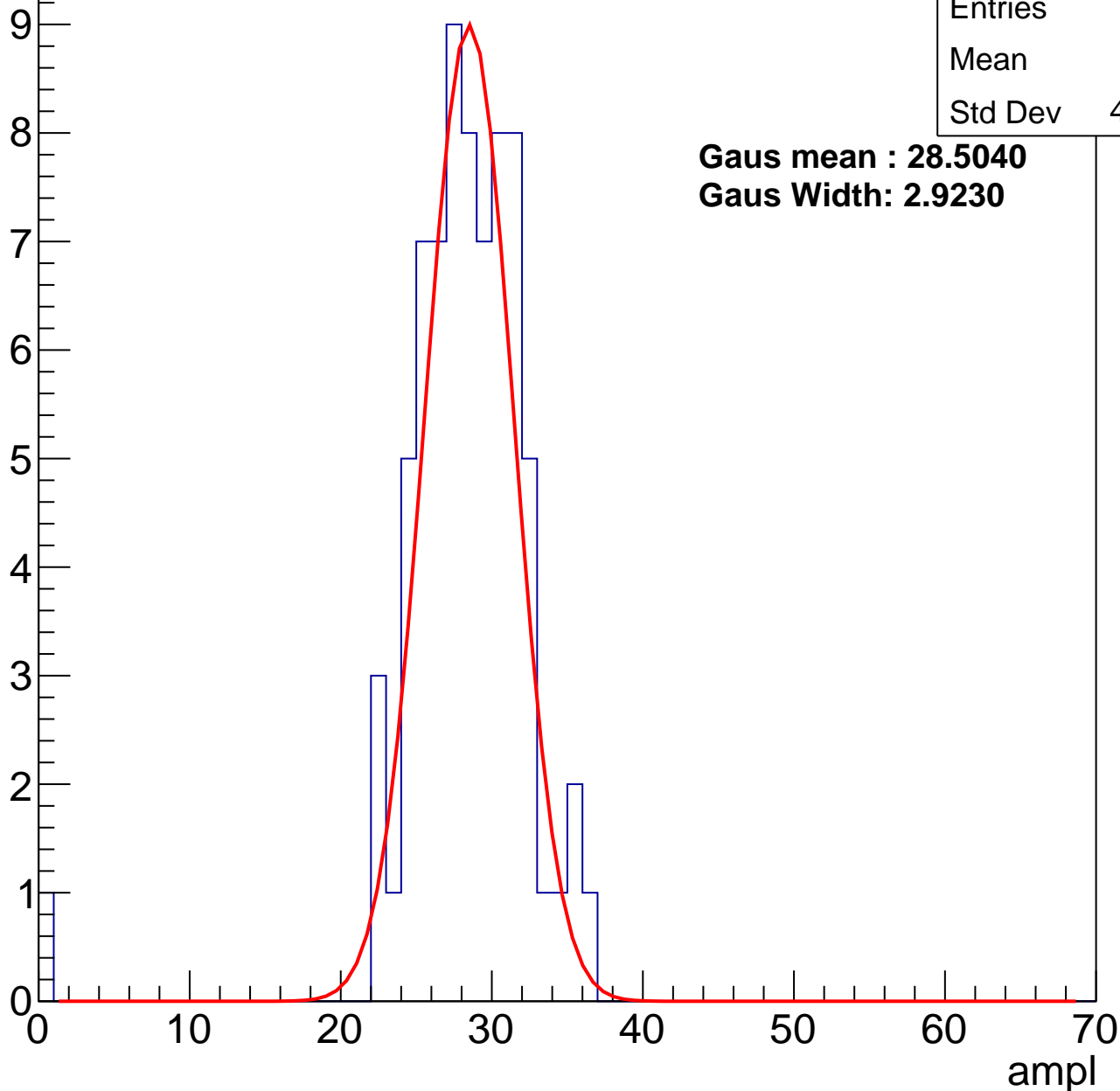
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	27.8
Std Dev	4.514

Gaus mean : 28.5040

Gaus Width: 2.9230



B1L003S, U19-ch82, adc1

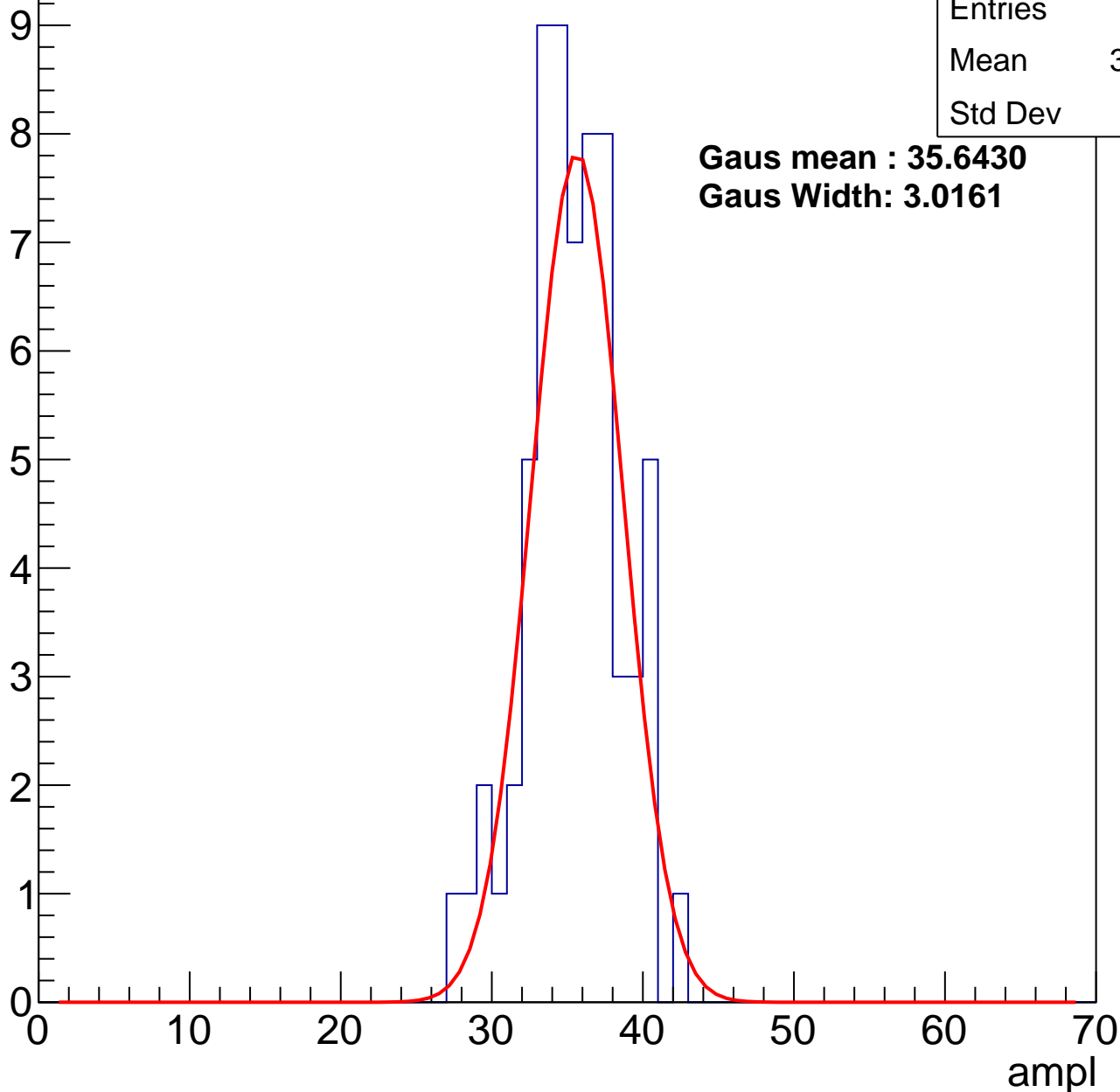
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	34.92
Std Dev	3.09

Gaus mean : 35.6430

Gaus Width: 3.0161



B1L003S, U19-ch82, adc2

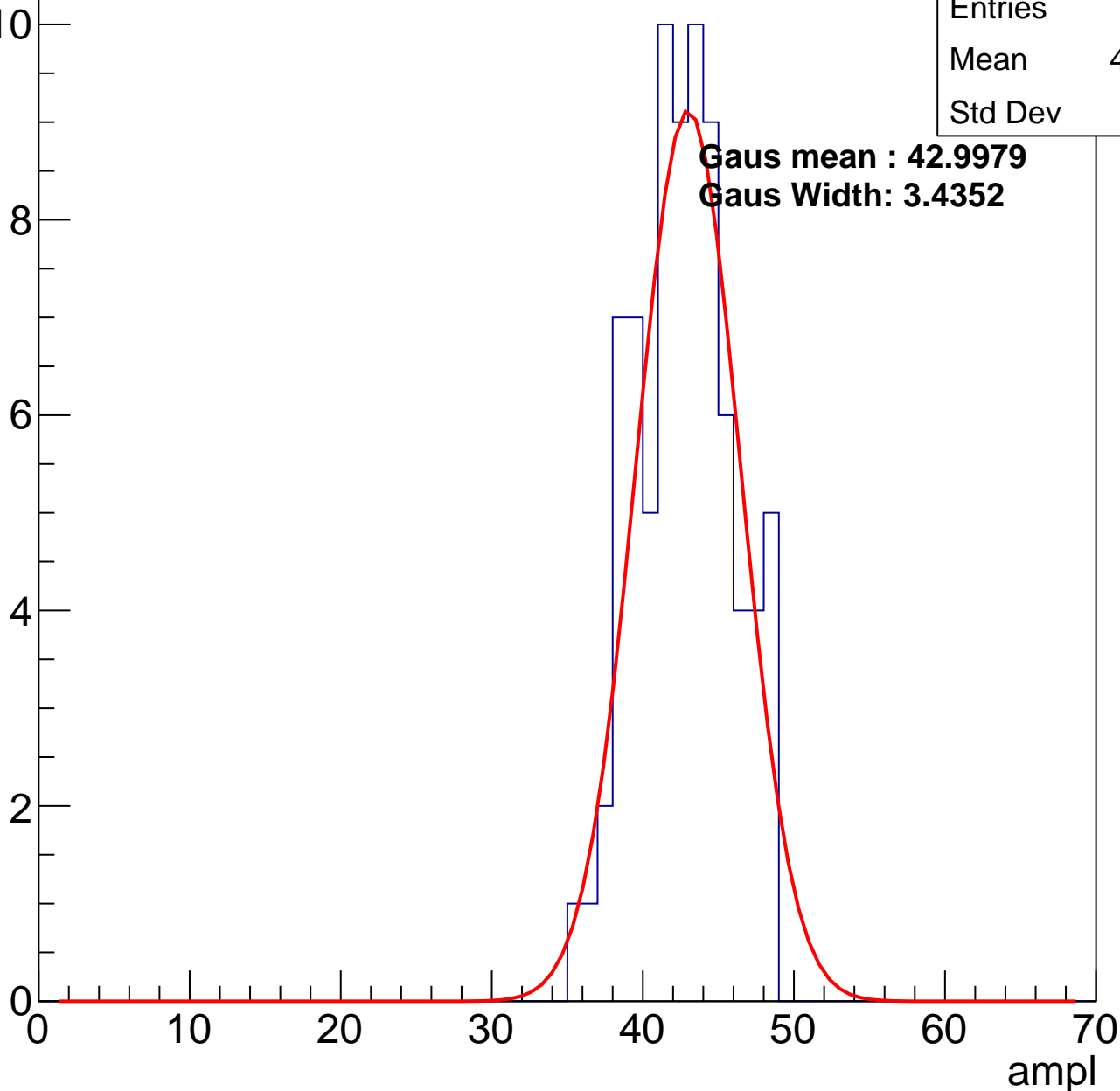
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	42.25
Std Dev	3.12

Gaus mean : 42.9979

Gaus Width: 3.4352

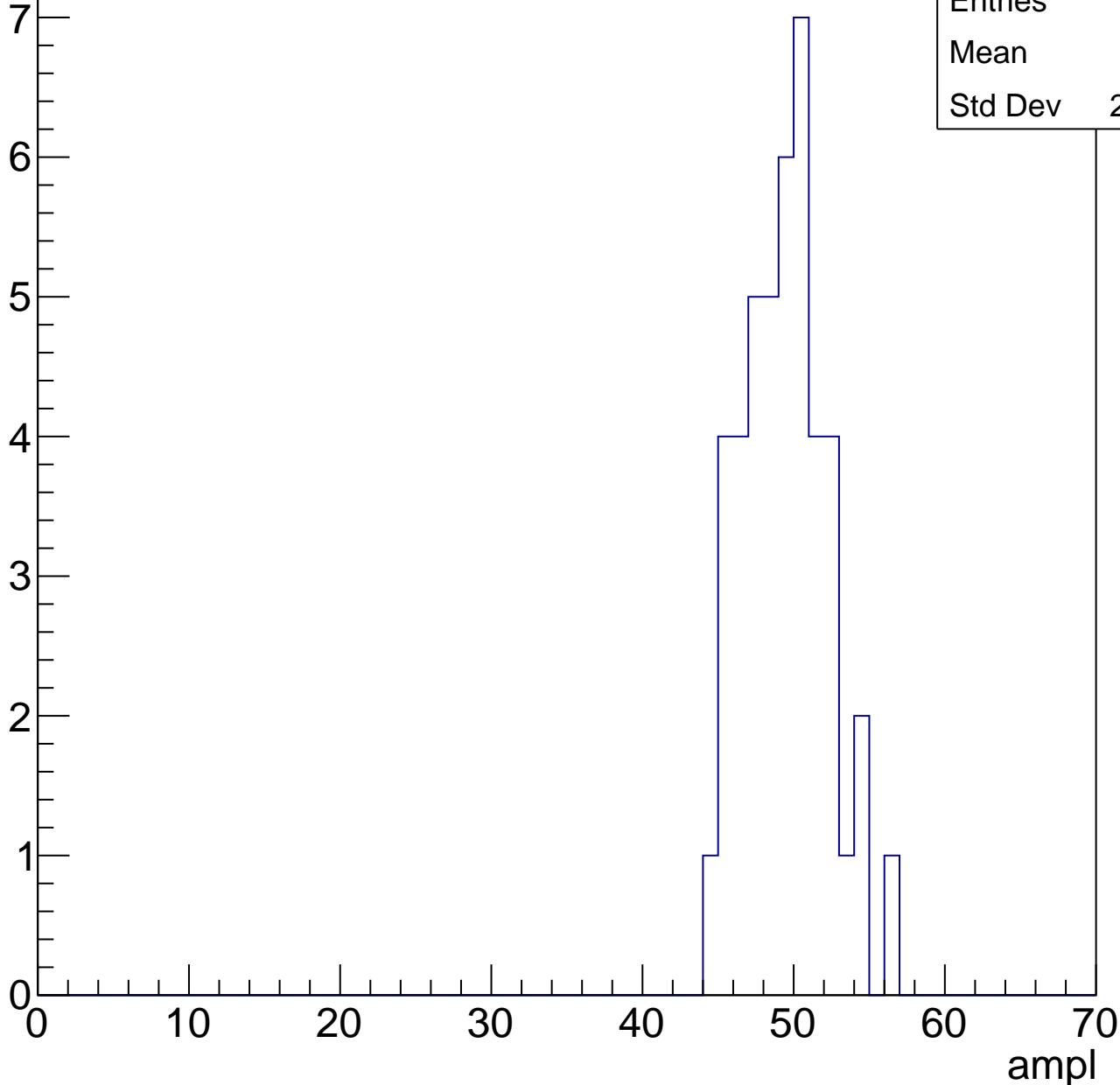


B1L003S, U19-ch82, adc3

calib_packv5_042523_0143.root, FC#13, port D2

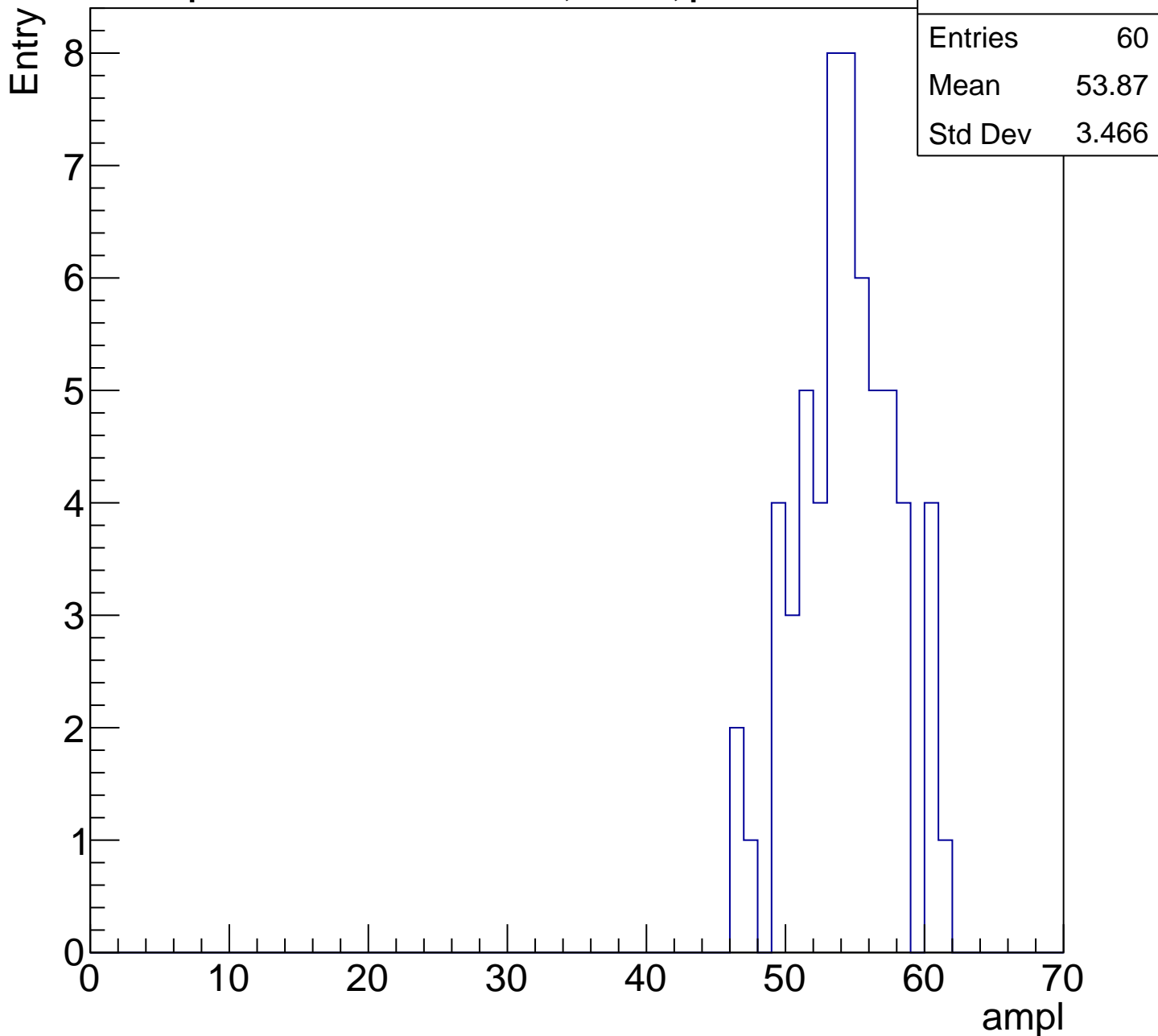
Entry

Entries	44
Mean	49
Std Dev	2.714



B1L003S, U19-ch82, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch82, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	57
Mean	58.77
Std Dev	2.938

Entry

10

8

6

4

2

0

0

10

20

30

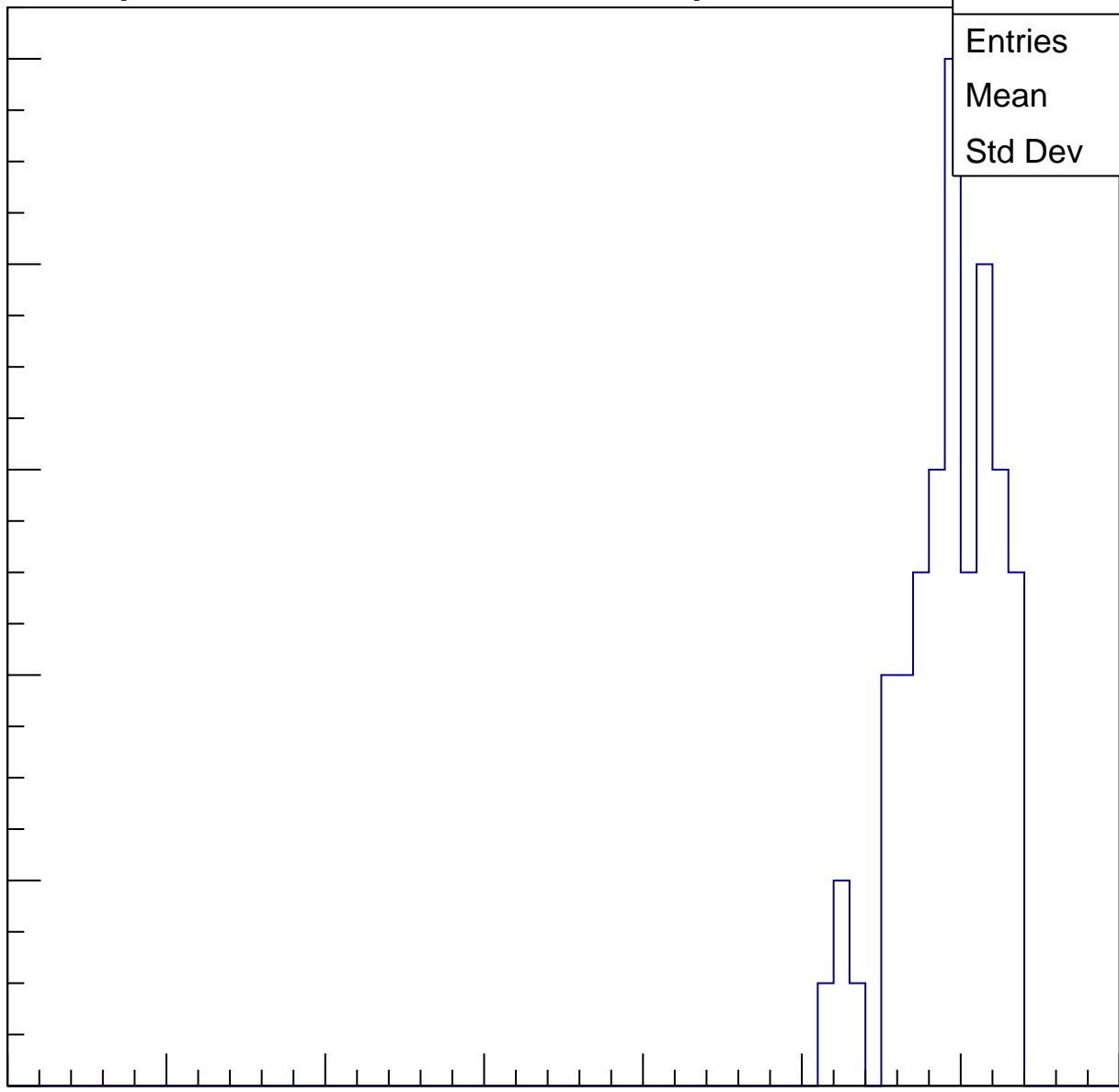
40

50

60

70

ampl

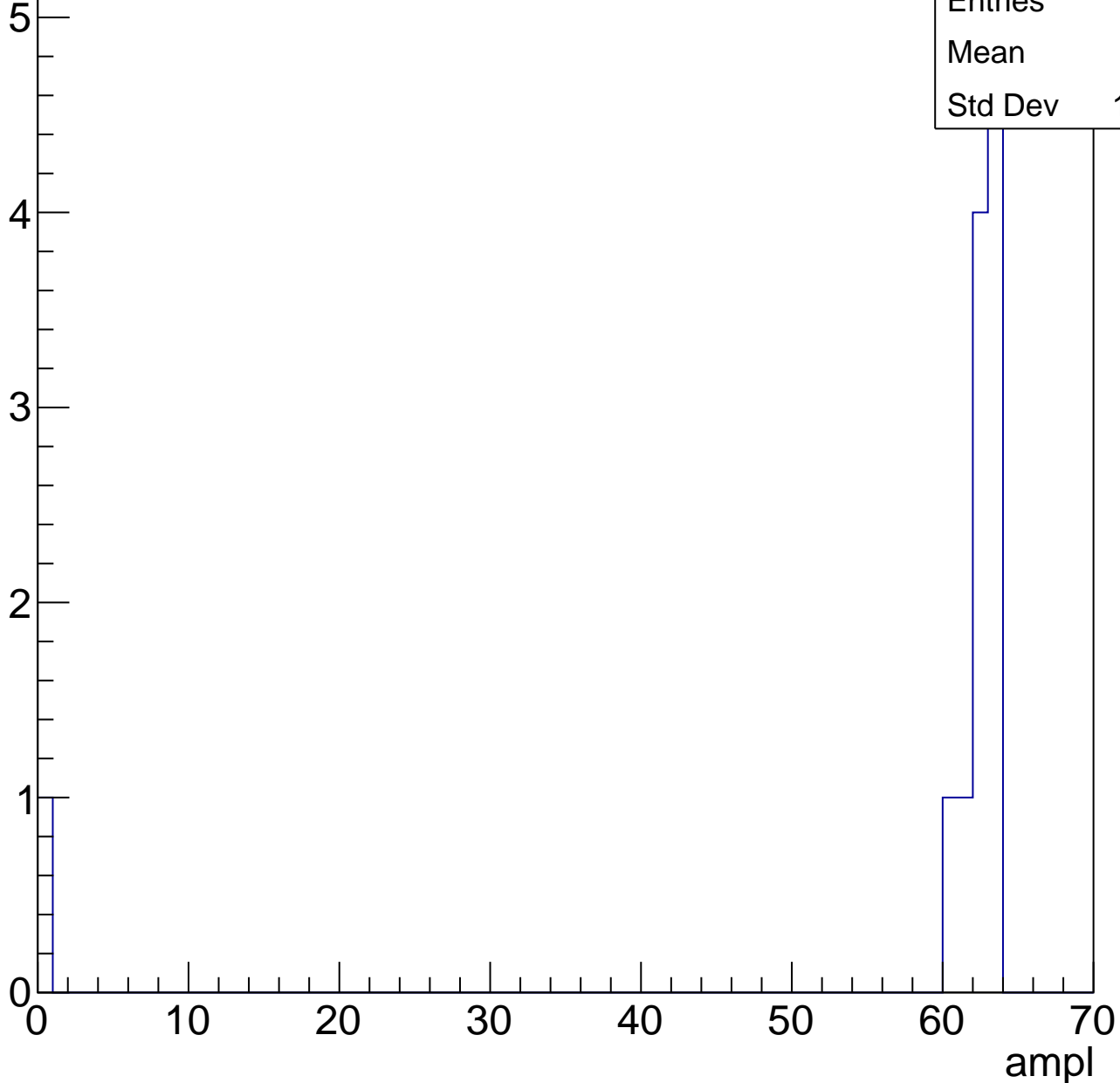


B1L003S, U19-ch82, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

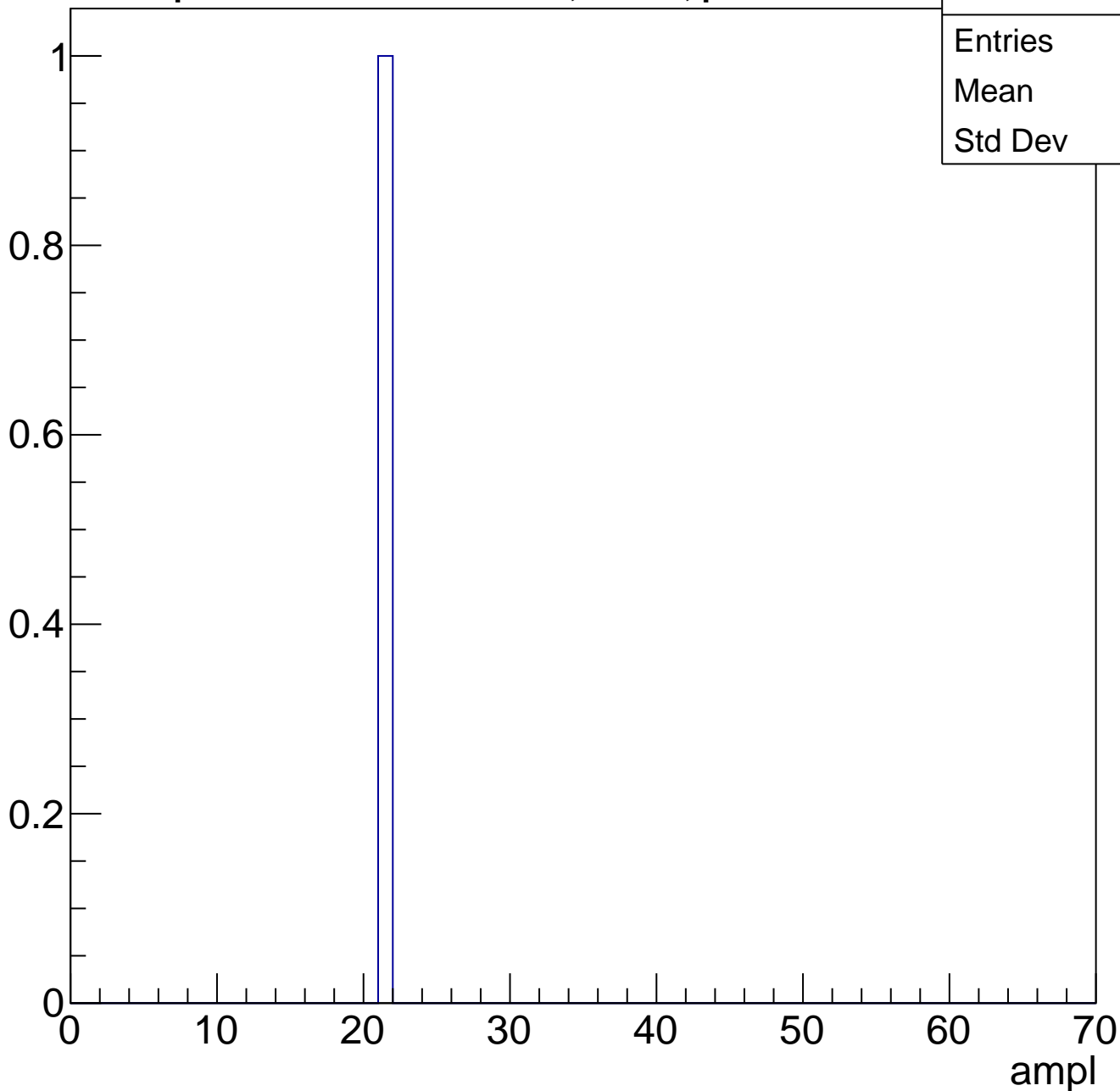
Entries	12
Mean	57
Std Dev	17.21



B1L003S, U19-ch82, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	21
Std Dev	0

B1L003S, U19-ch83, adc0

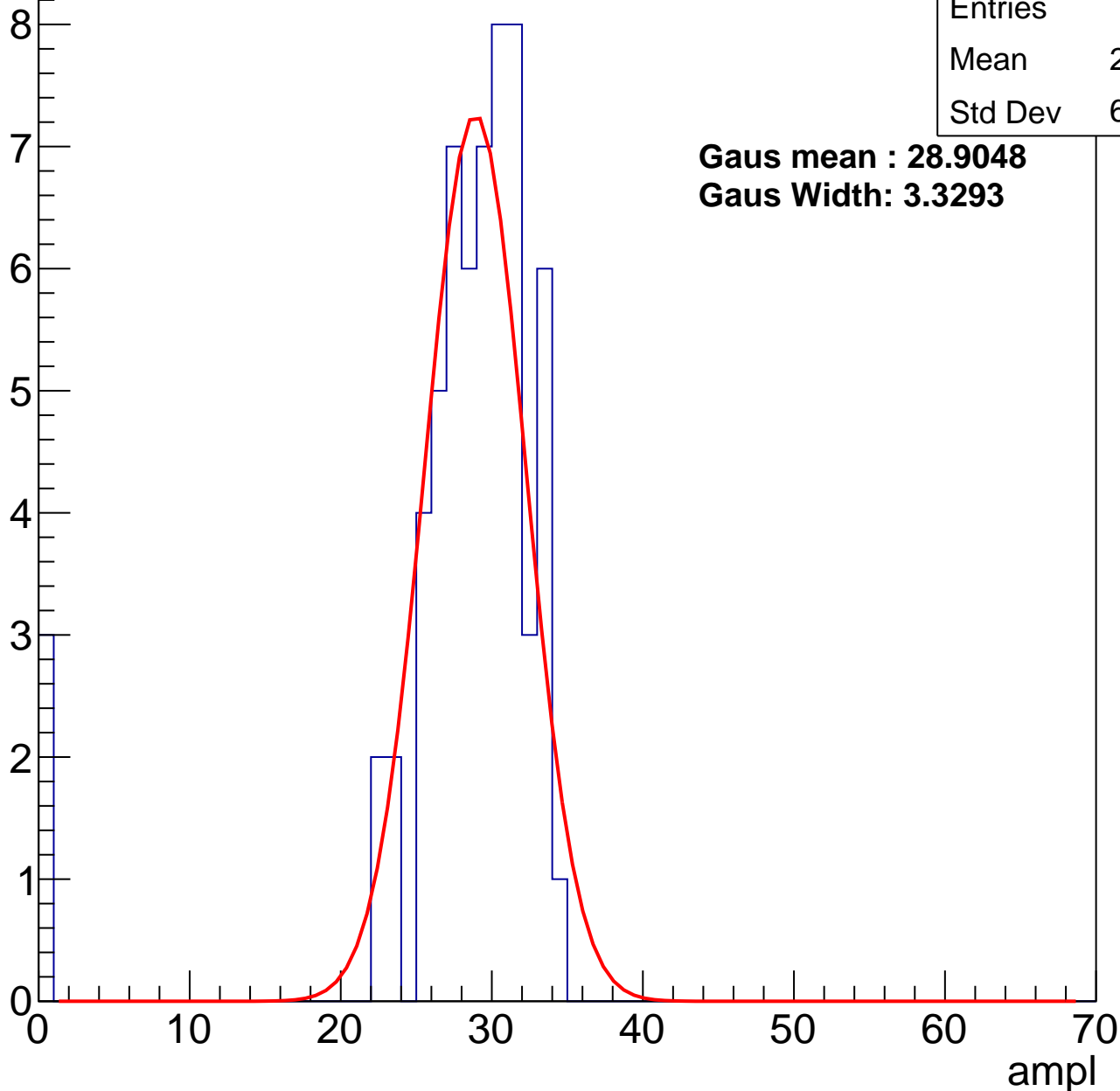
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	27.35
Std Dev	6.787

Gaus mean : 28.9048

Gaus Width: 3.3293



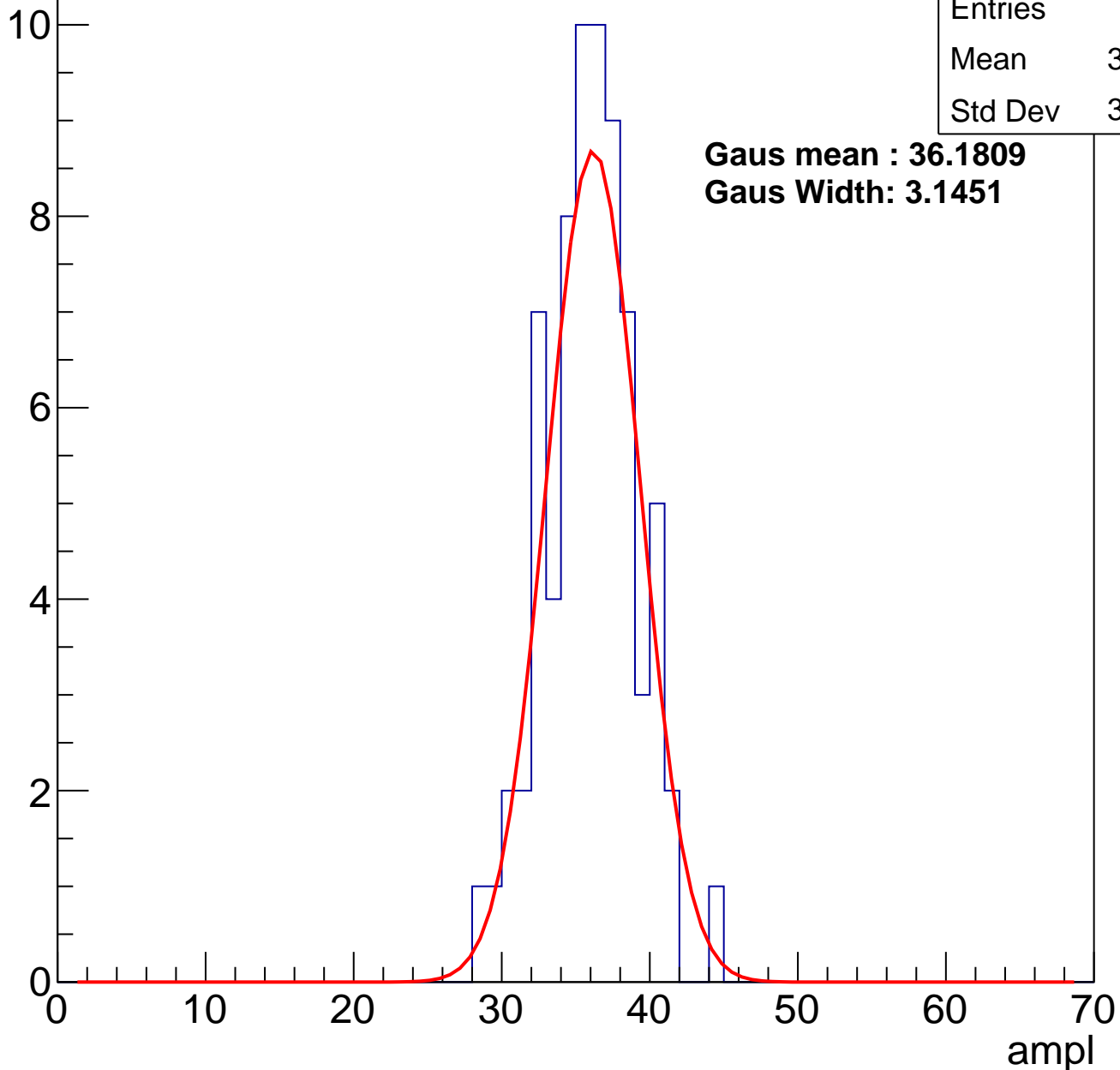
B1L003S, U19-ch83, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	35.54
Std Dev	3.046

Gaus mean : 36.1809
Gaus Width: 3.1451

Entry



B1L003S, U19-ch83, adc2

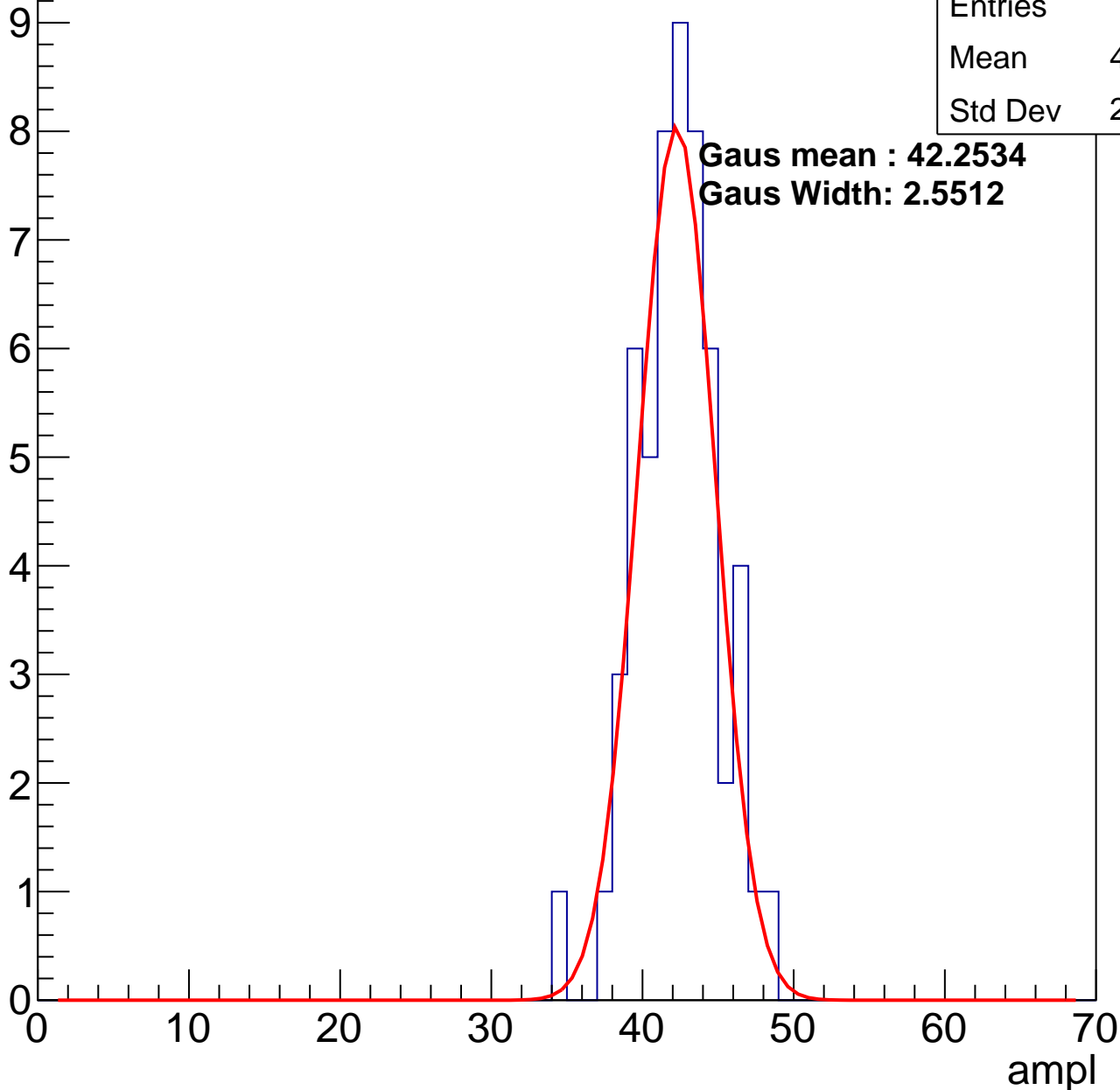
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	41.85
Std Dev	2.673

Gaus mean : 42.2534

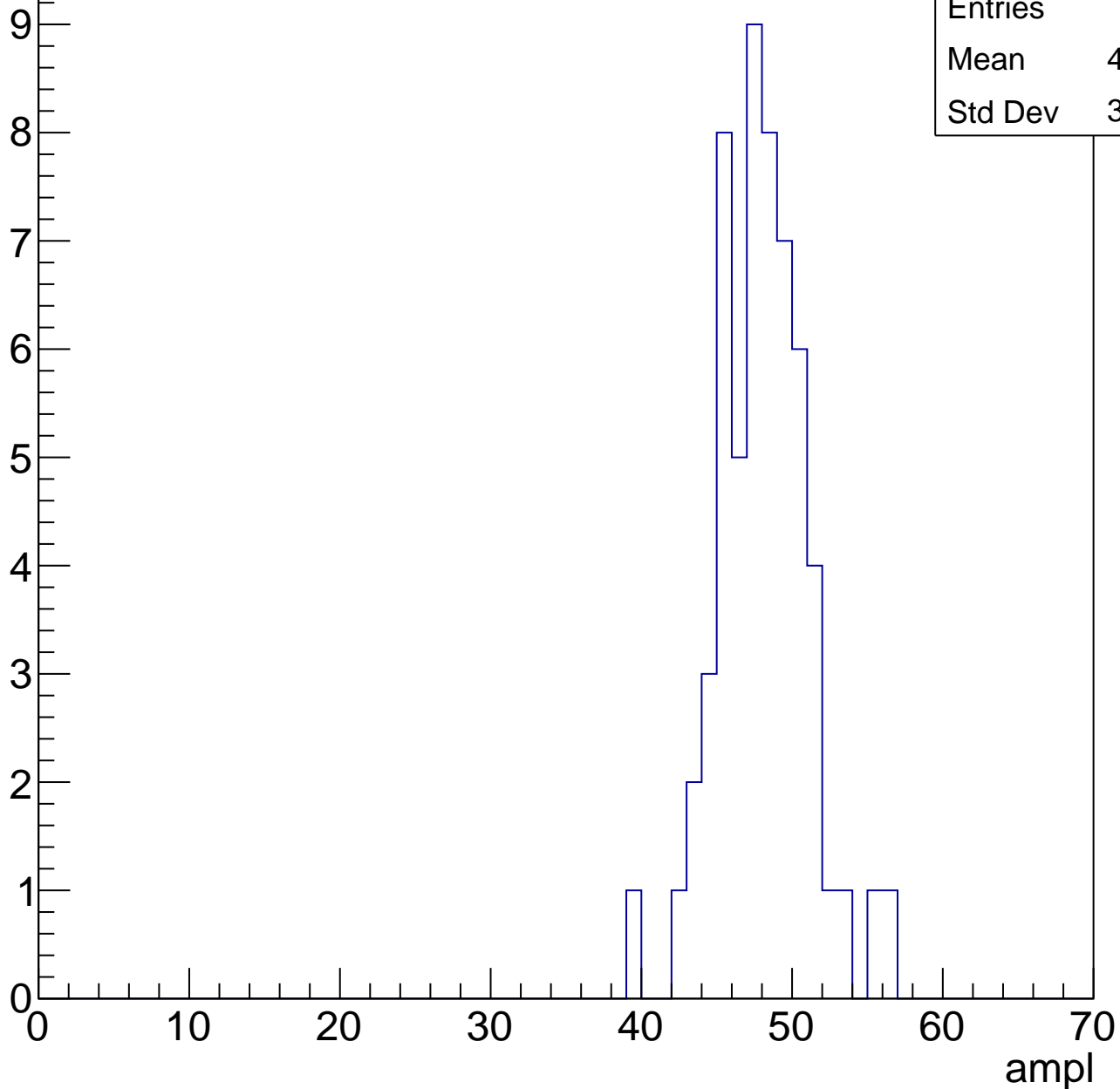
Gaus Width: 2.5512



B1L003S, U19-ch83, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



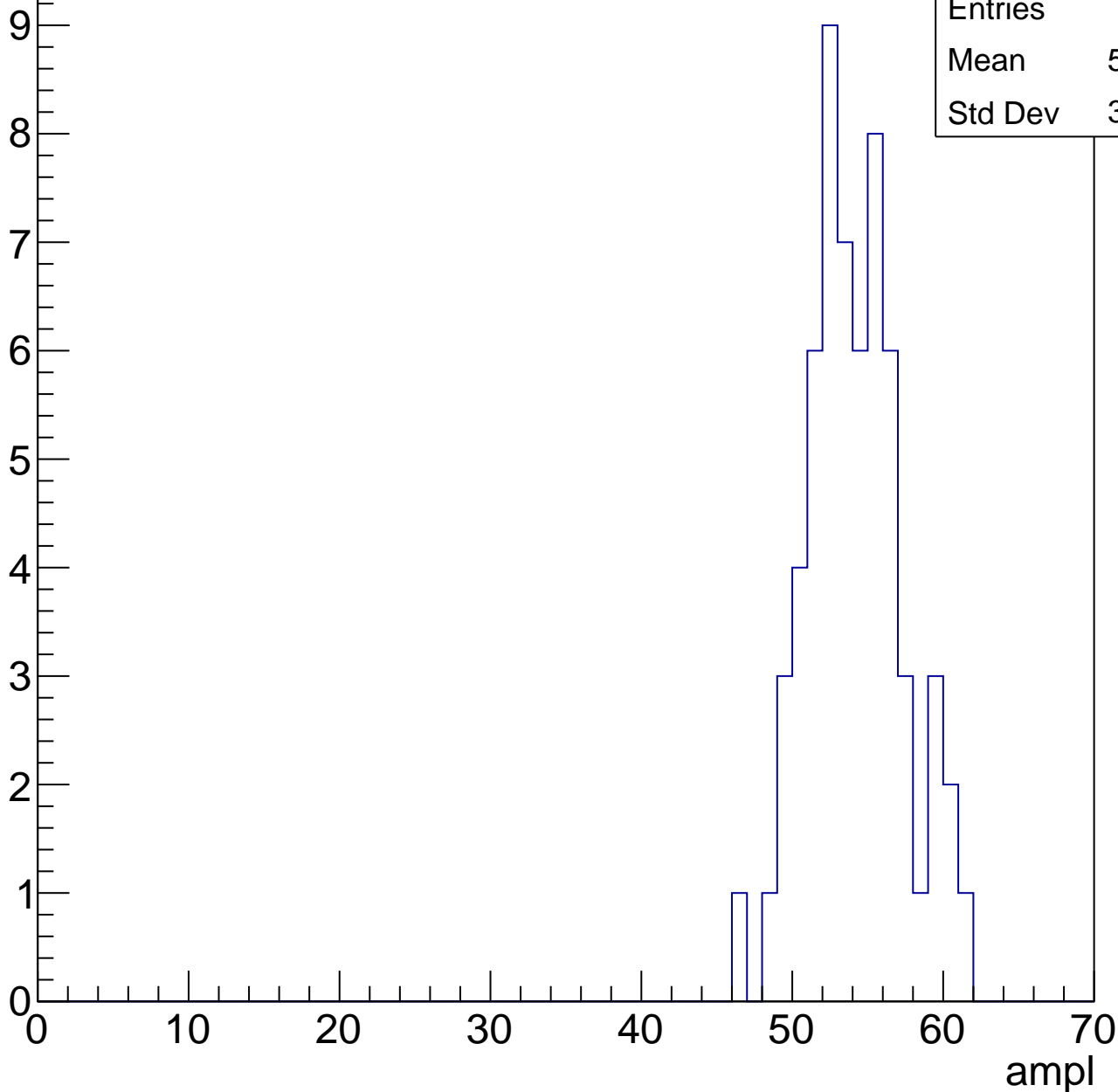
Entries	58
Mean	47.57
Std Dev	3.012

B1L003S, U19-ch83, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	53.66
Std Dev	3.136



B1L003S, U19-ch83, adc5

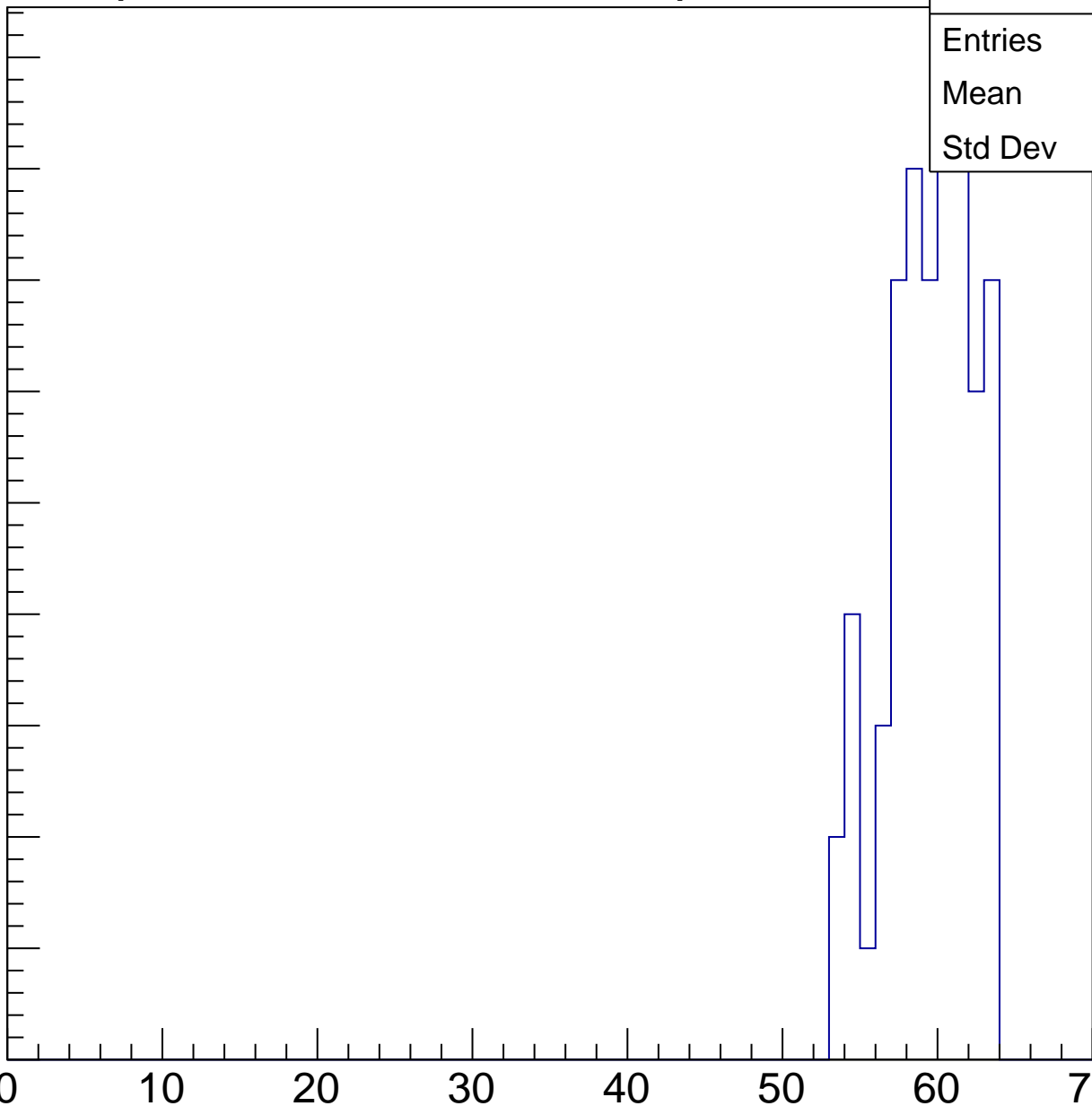
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	59.08
Std Dev	2.726

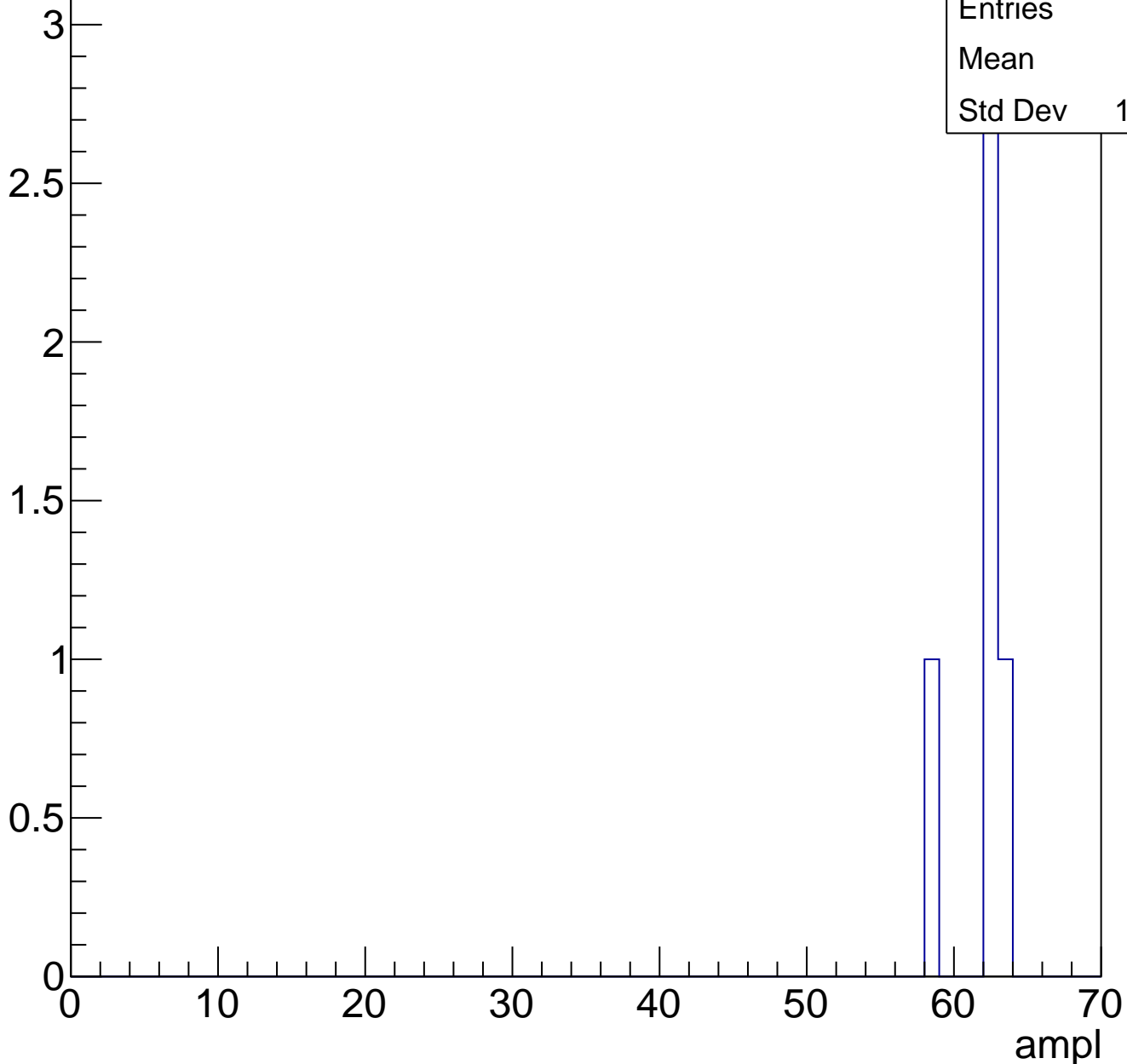
ampl



B1L003S, U19-ch83, adc6

calib_packv5_042523_0143.root, FC#13, port D2

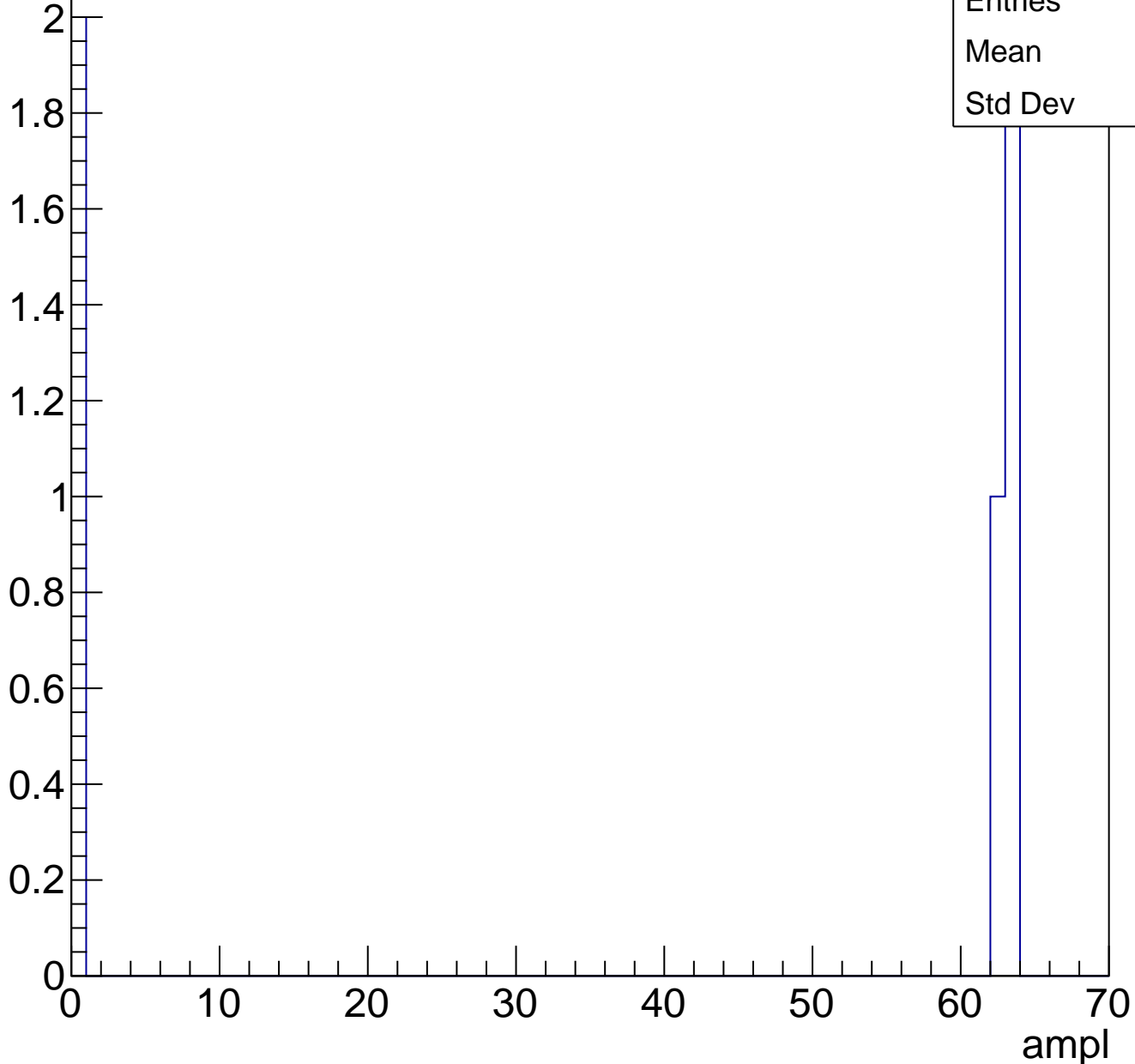
Entry



B1L003S, U19-ch83, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch84, adc0

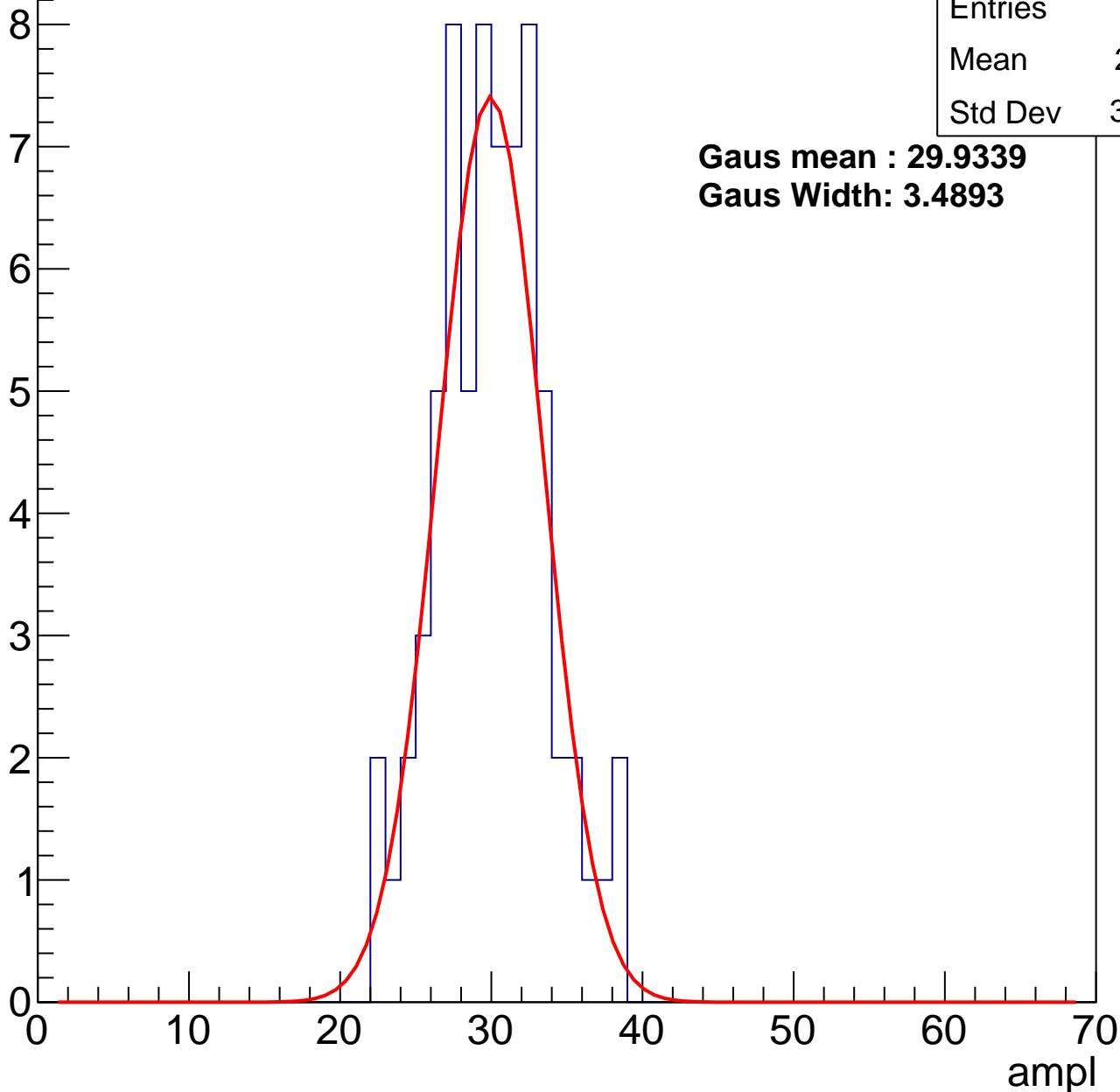
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.61
Std Dev	3.556

Gaus mean : 29.9339

Gaus Width: 3.4893



B1L003S, U19-ch84, adc1

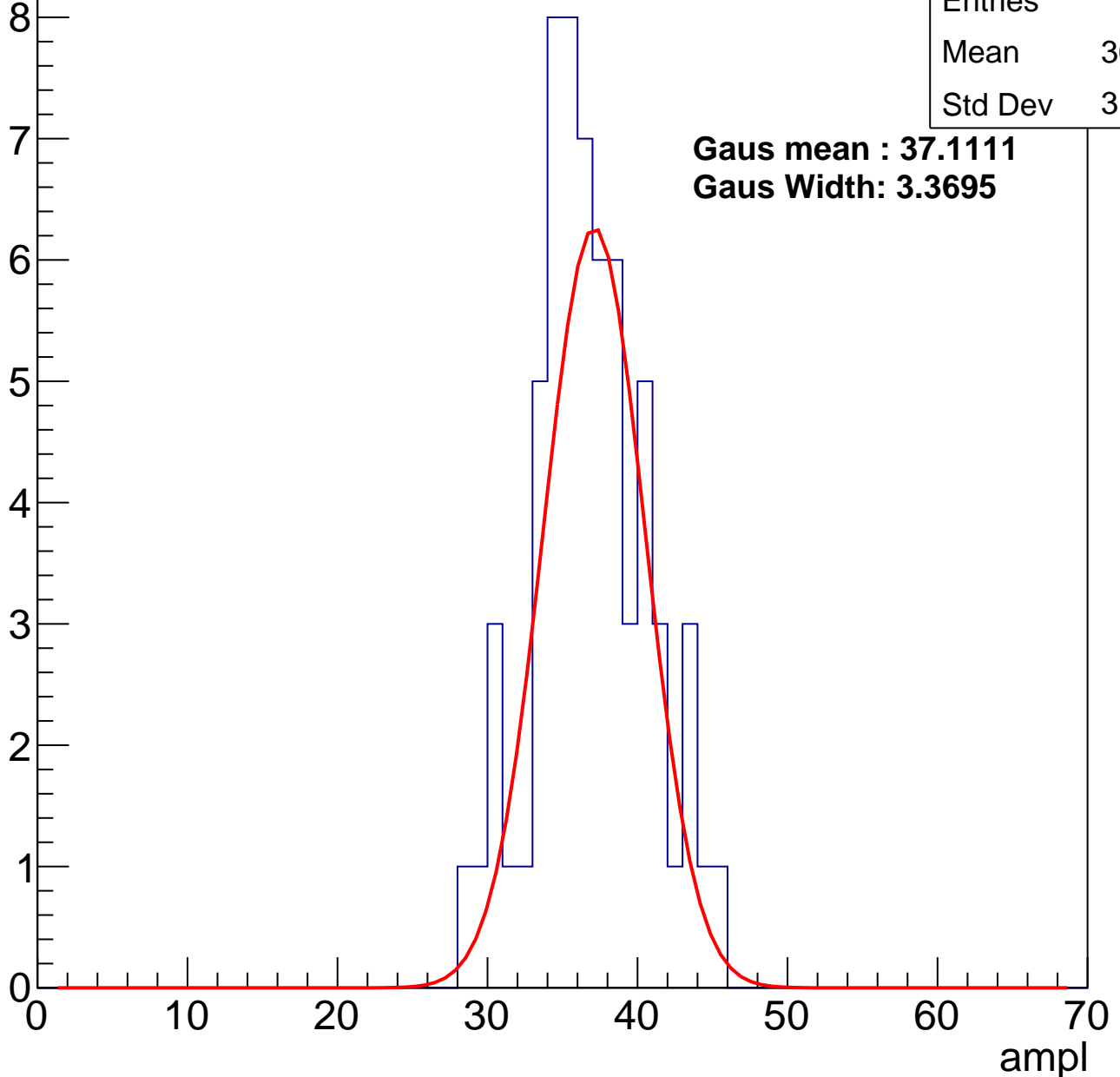
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	36.39
Std Dev	3.702

Gaus mean : 37.1111

Gaus Width: 3.3695



B1L003S, U19-ch84, adc2

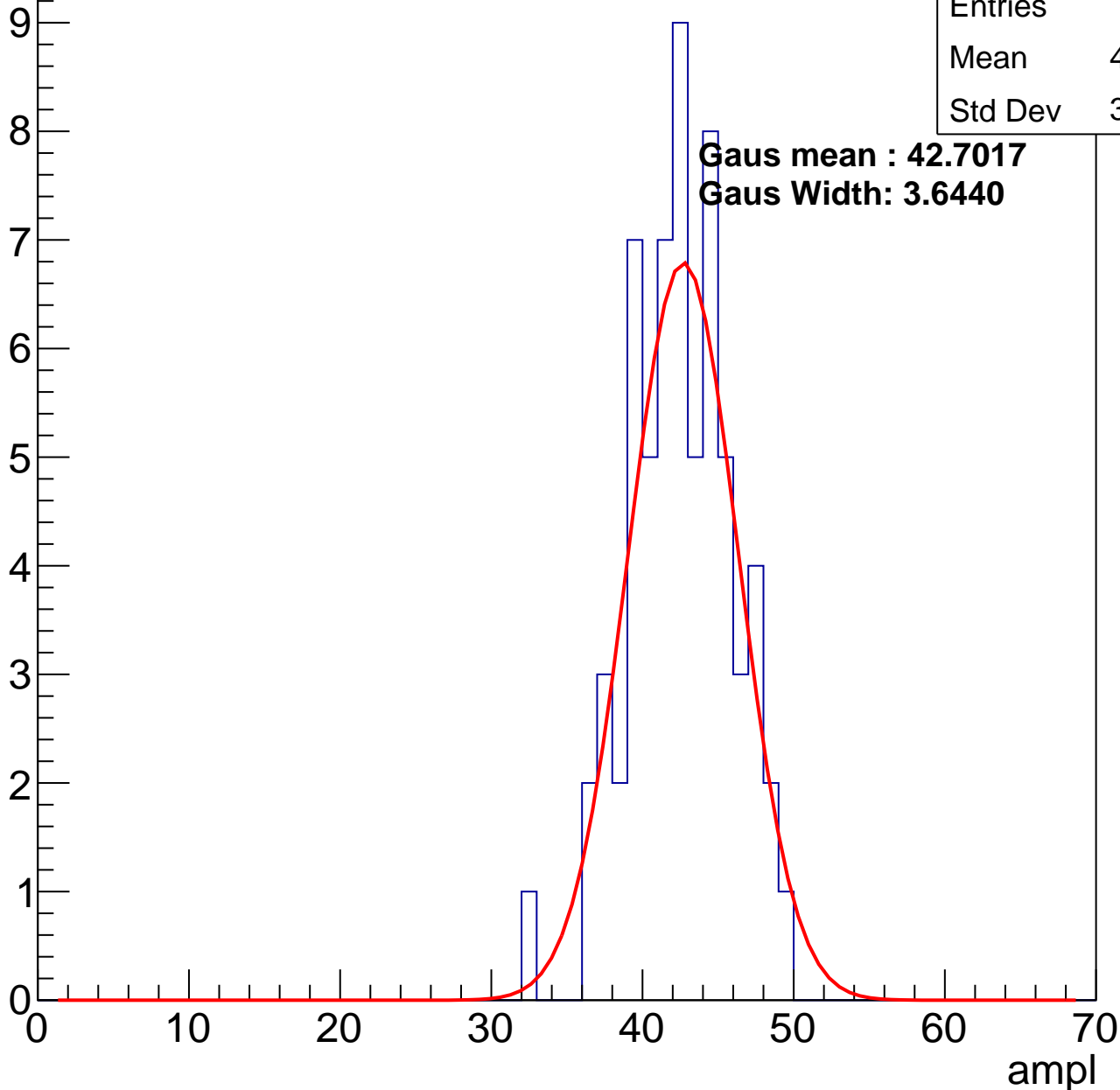
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.06
Std Dev	3.354

Gaus mean : 42.7017

Gaus Width: 3.6440

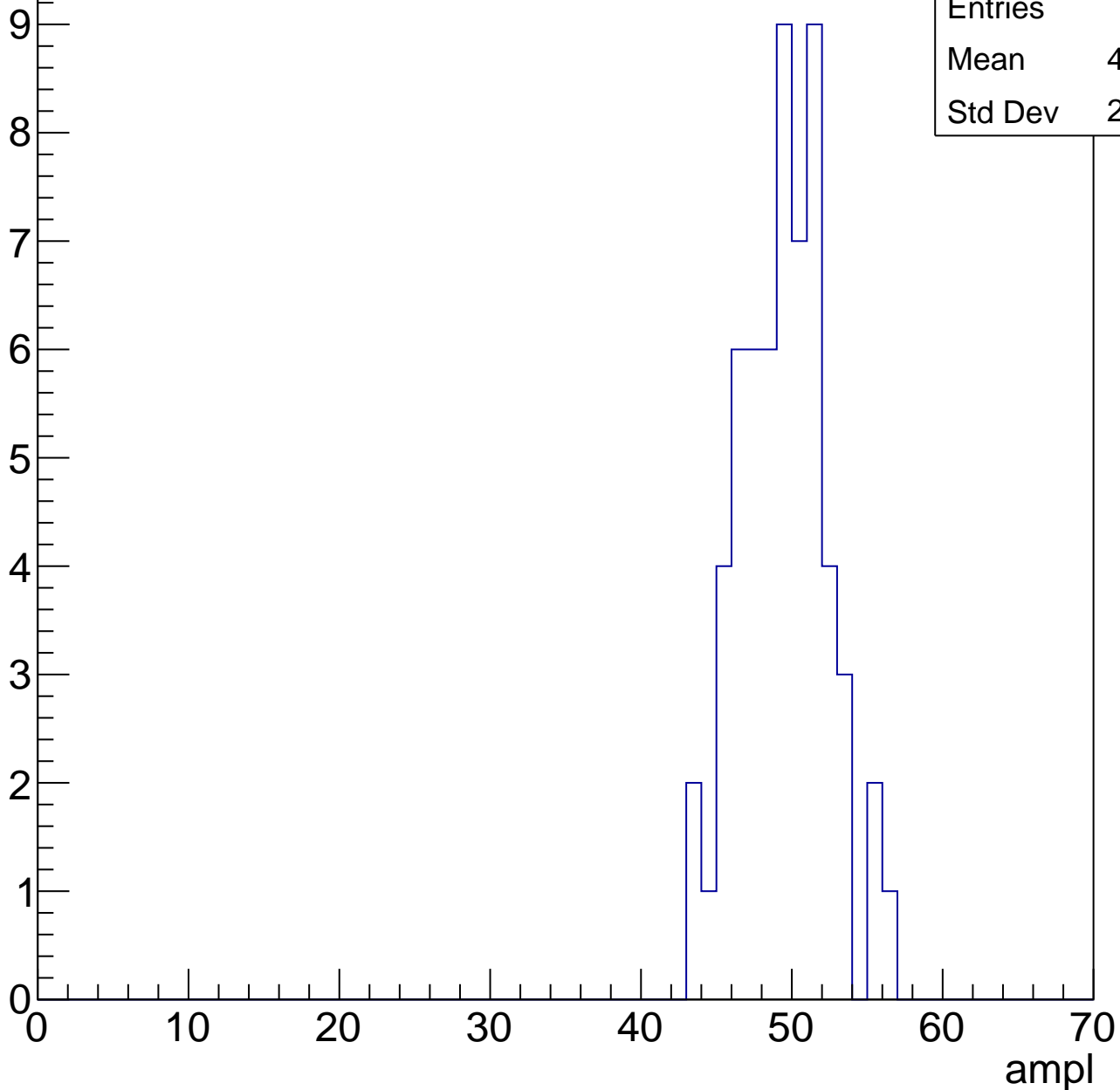


B1L003S, U19-ch84, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.98
Std Dev	2.866

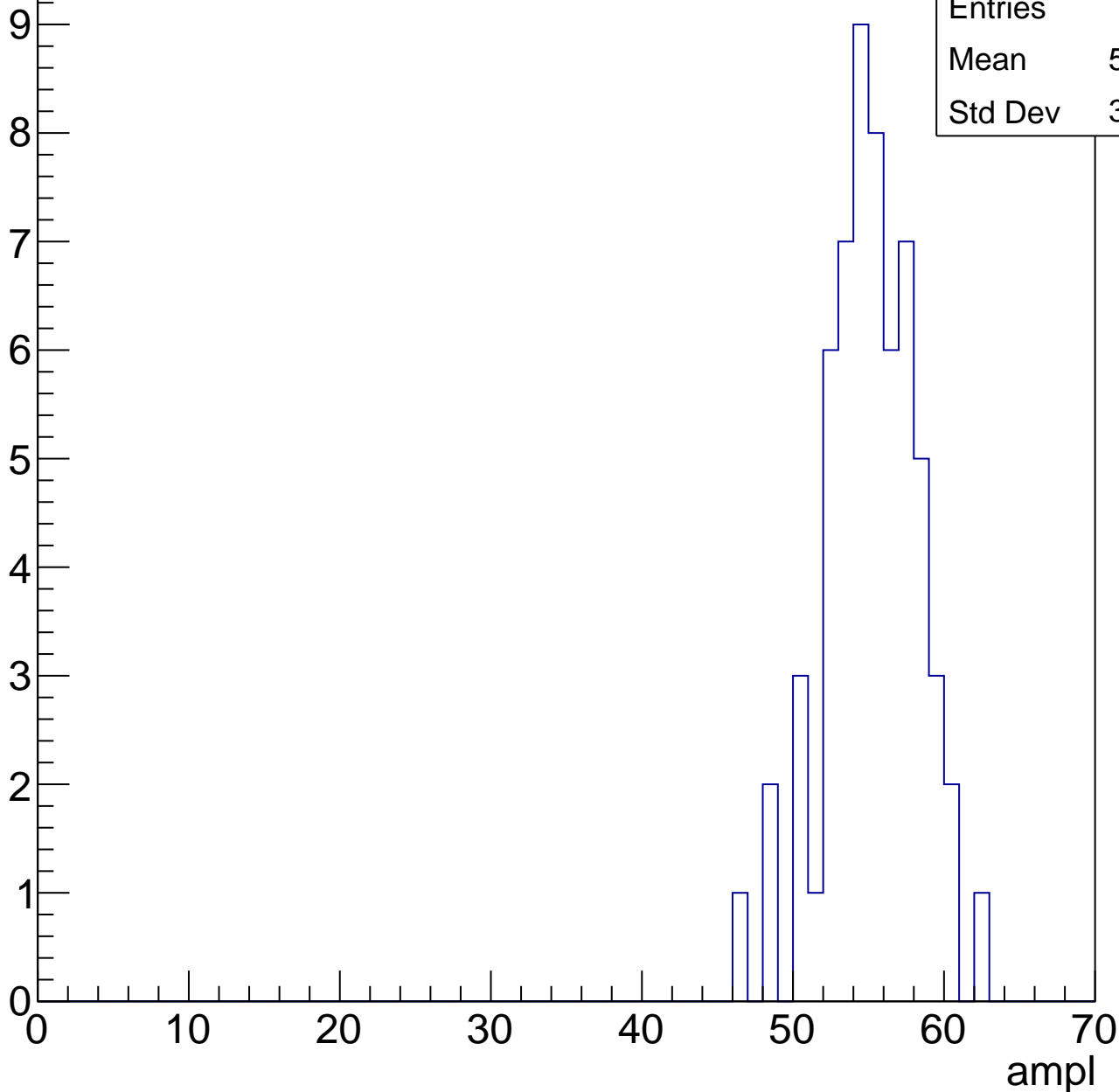


B1L003S, U19-ch84, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	54.69
Std Dev	3.086

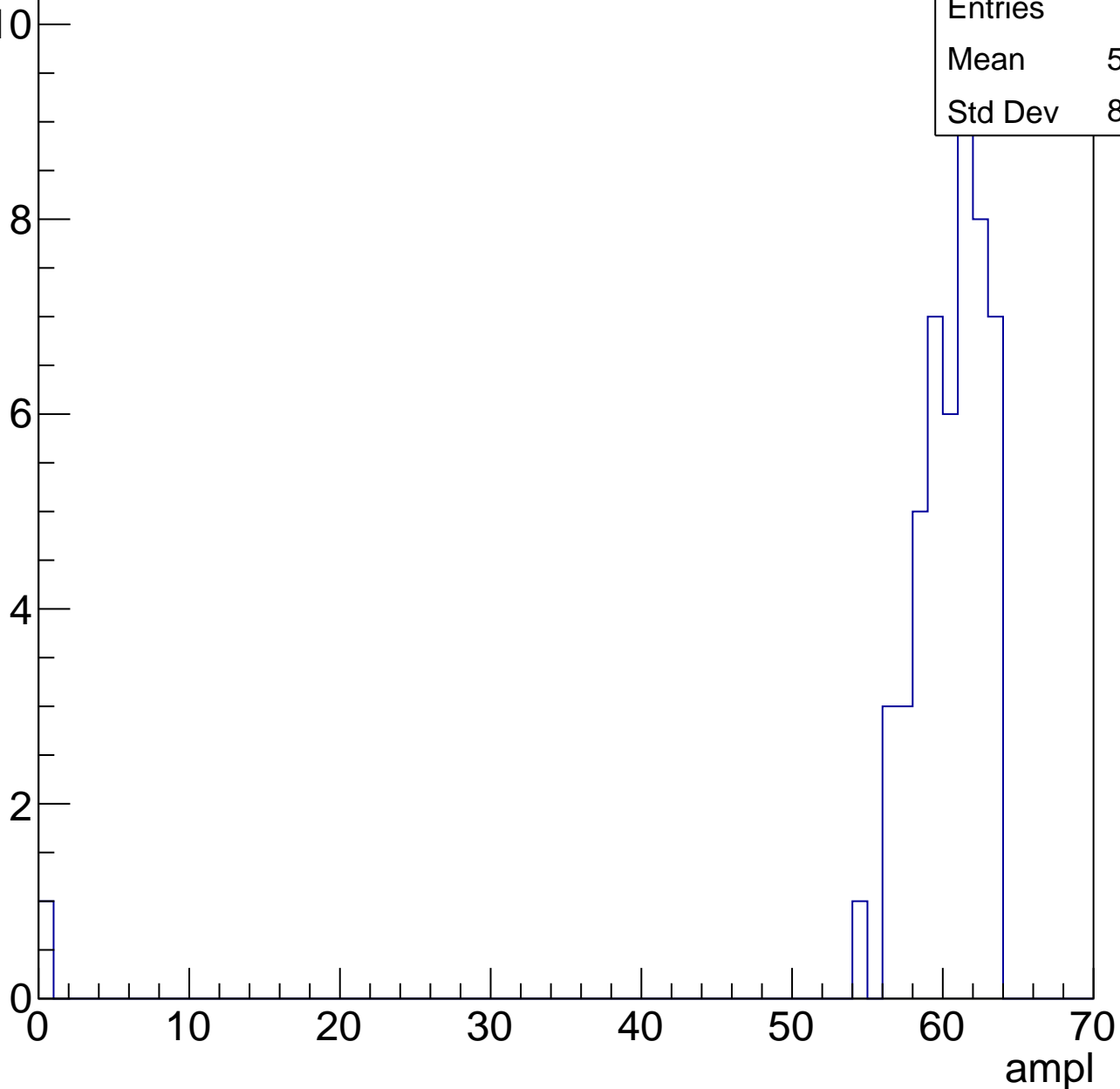


B1L003S, U19-ch84, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

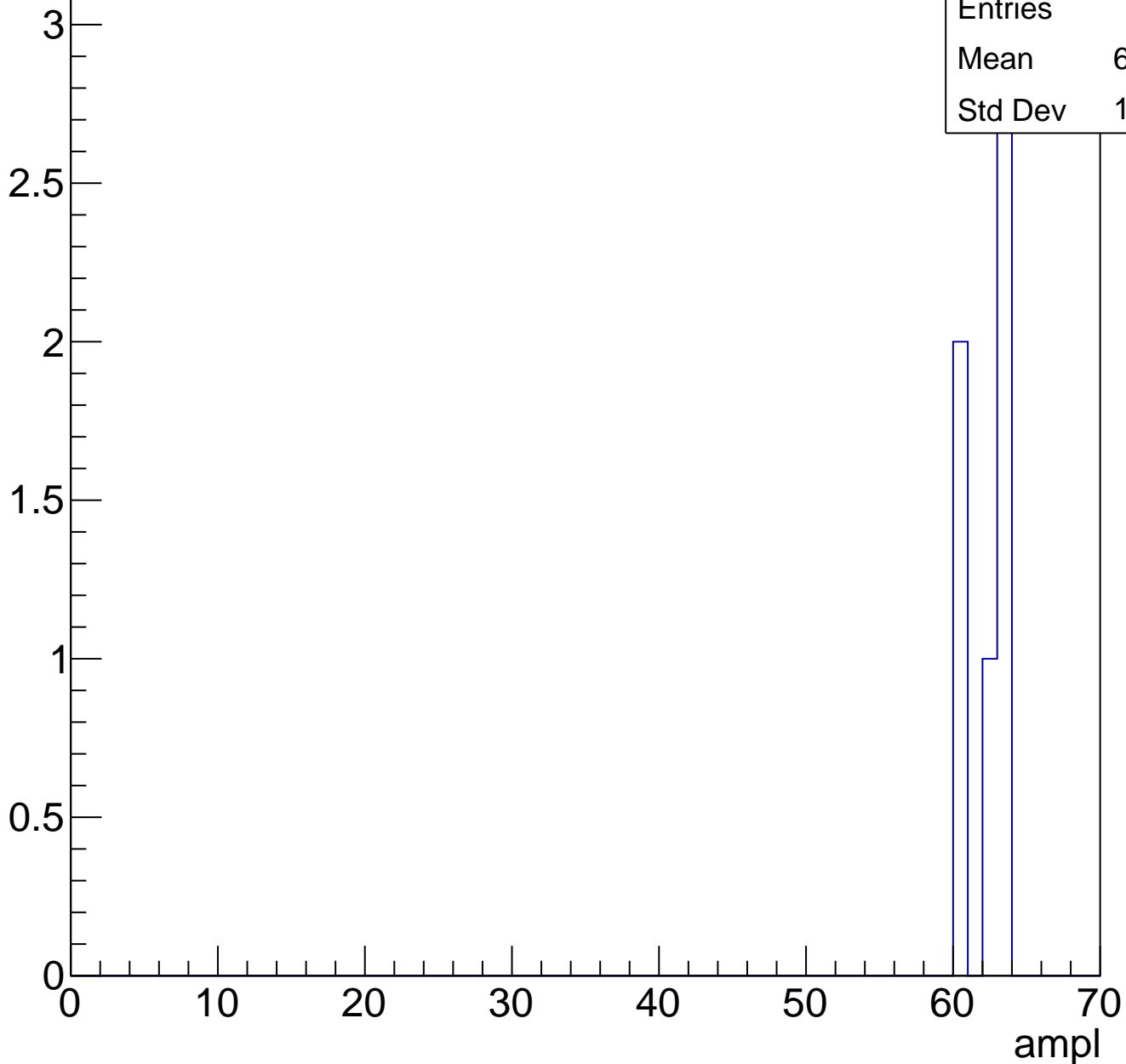
Entries	51
Mean	58.88
Std Dev	8.608



B1L003S, U19-ch84, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch84, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch85, adc0

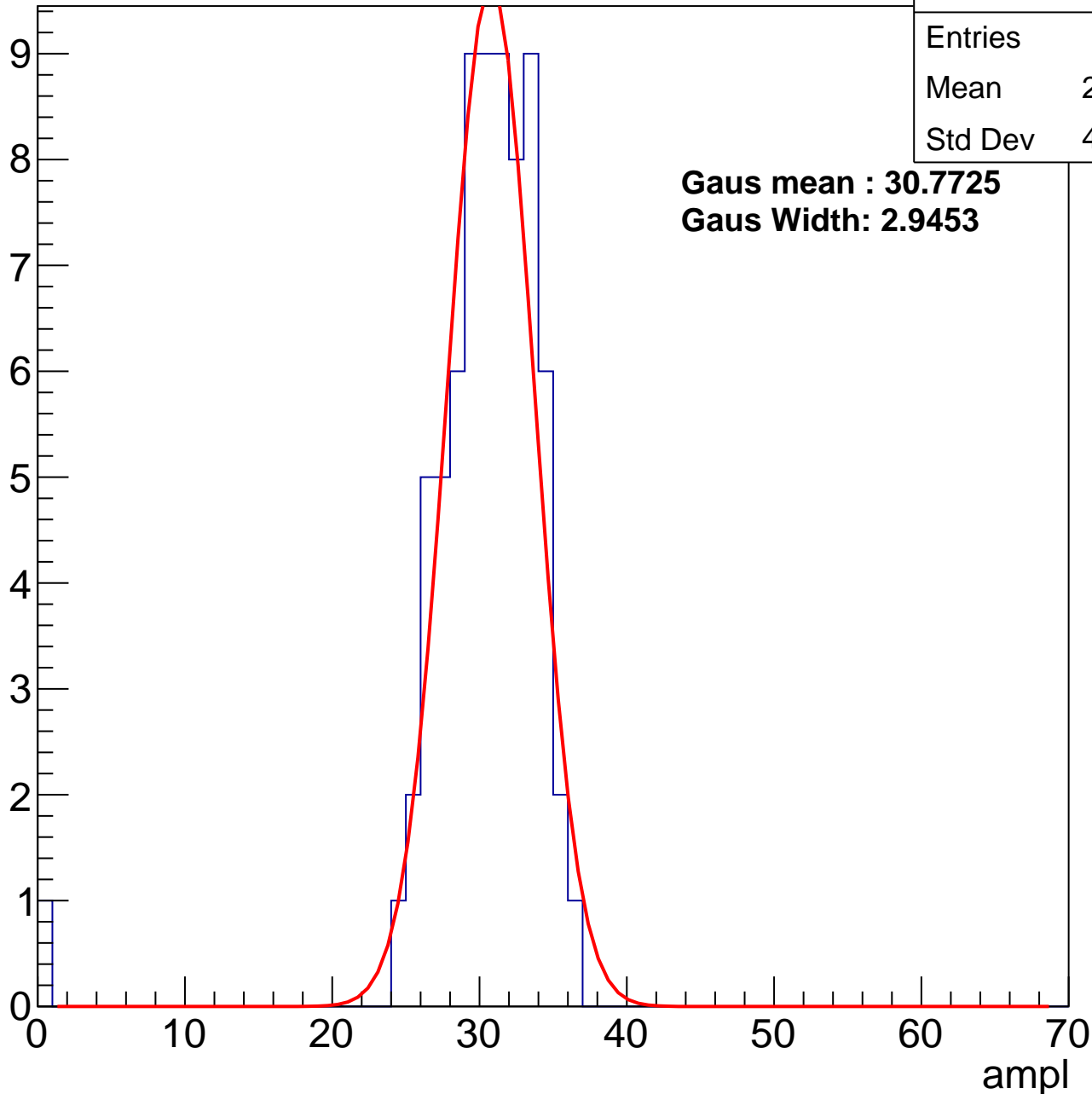
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.86
Std Dev	4.452

Gaus mean : 30.7725

Gaus Width: 2.9453



B1L003S, U19-ch85, adc1

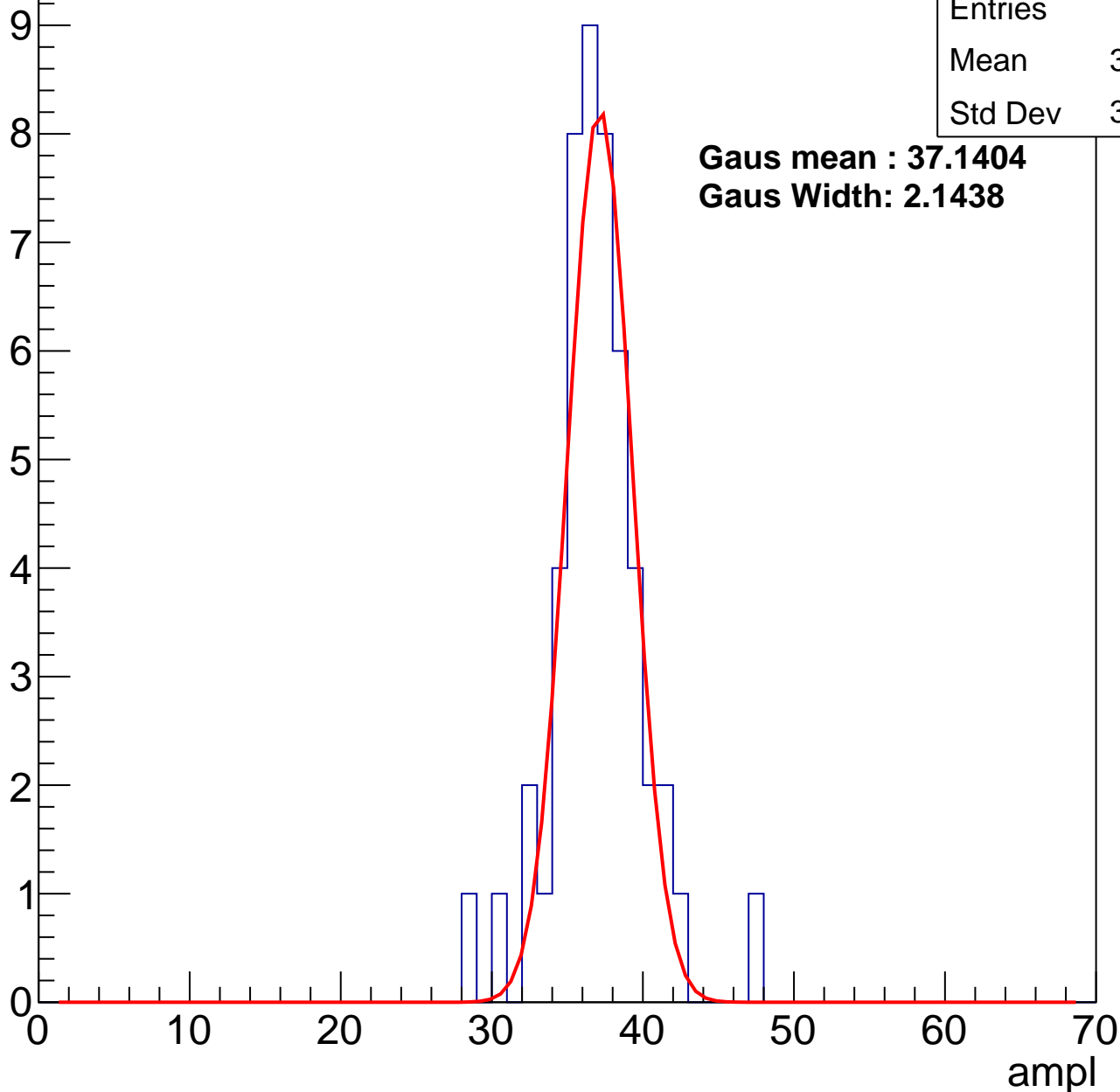
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	36.52
Std Dev	3.028

Gaus mean : 37.1404

Gaus Width: 2.1438



B1L003S, U19-ch85, adc2

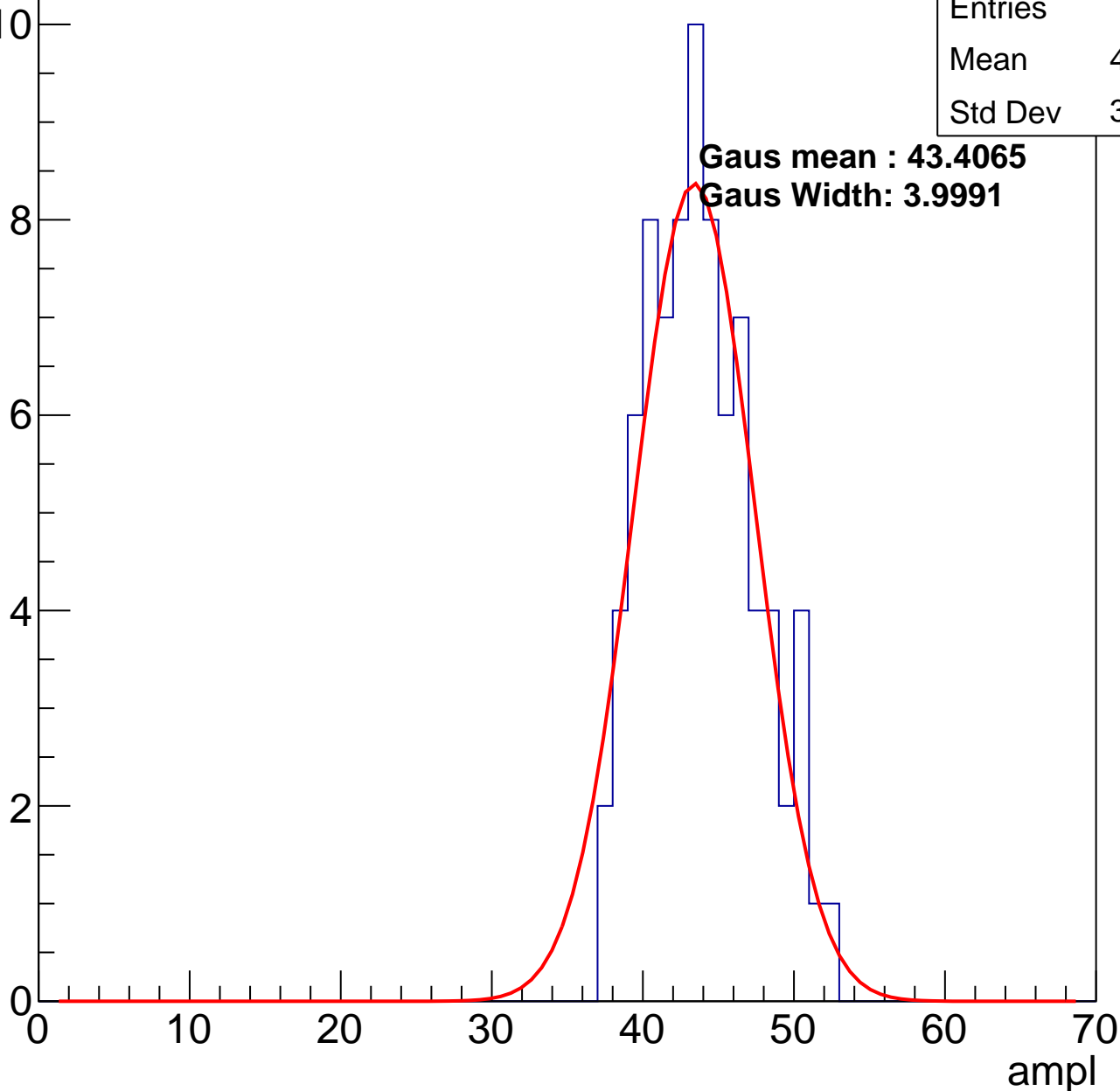
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	43.39
Std Dev	3.554

Gaus mean : 43.4065

Gaus Width: 3.9991

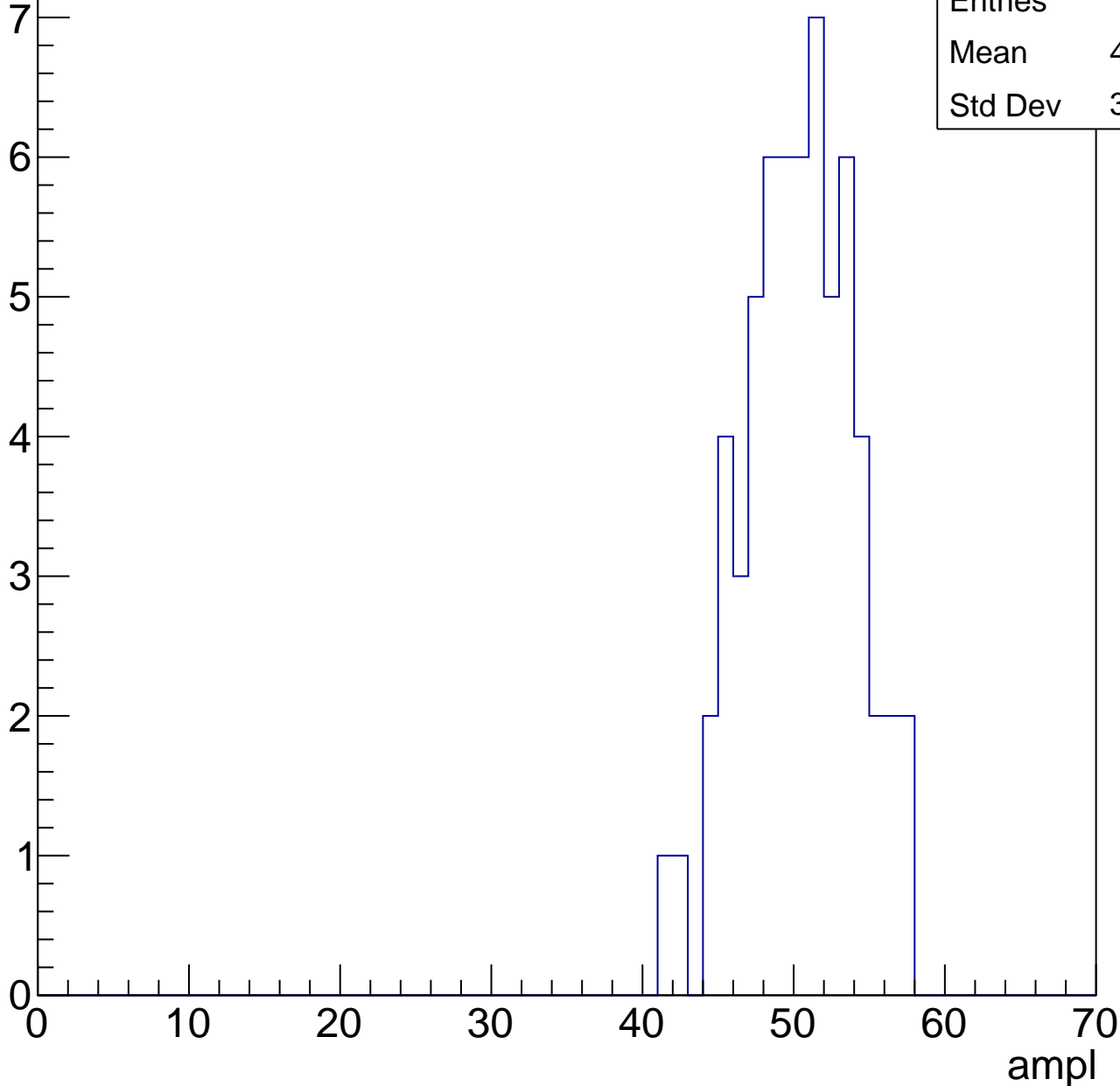


B1L003S, U19-ch85, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49.89
Std Dev	3.602

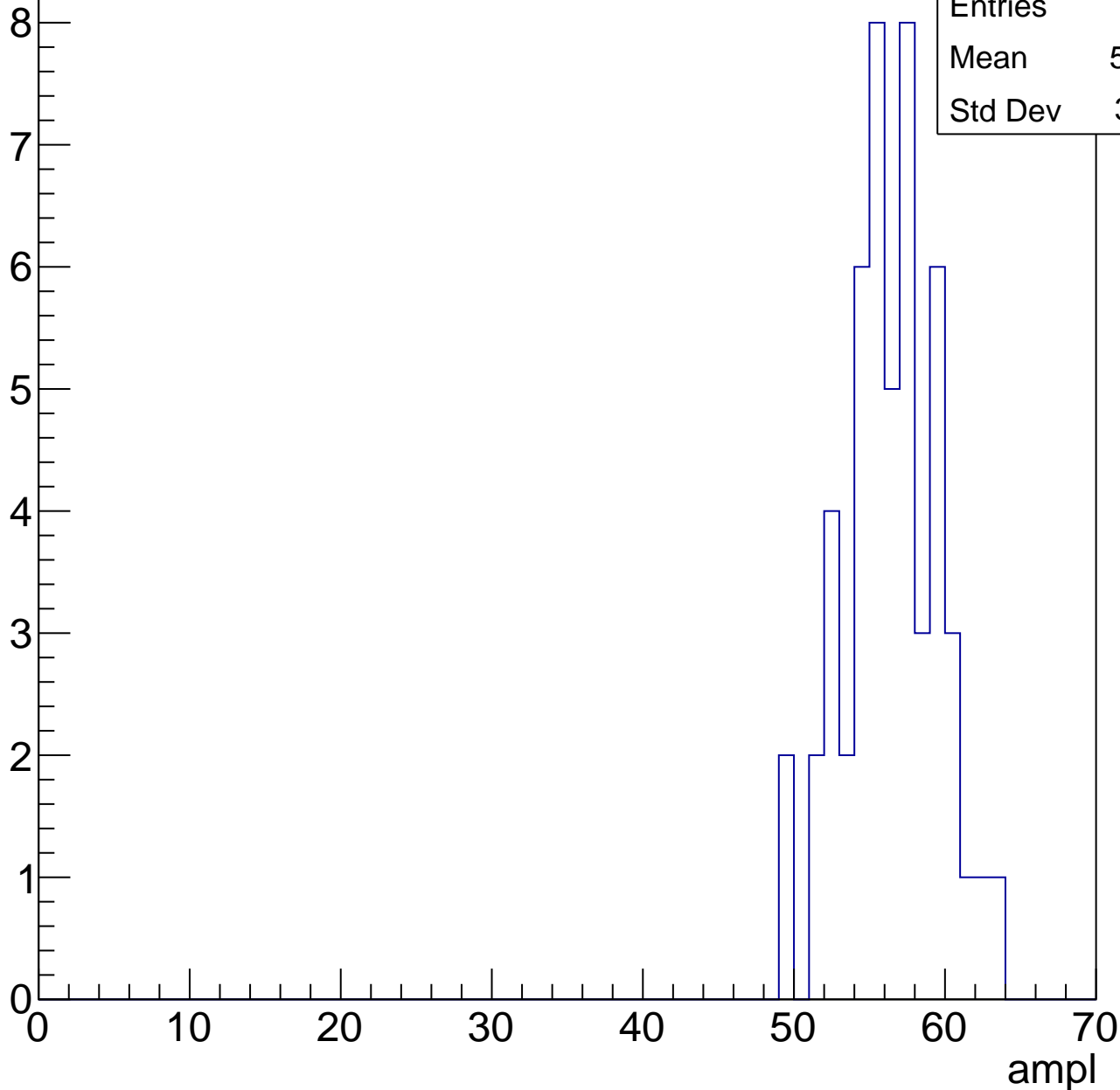


B1L003S, U19-ch85, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.92
Std Dev	3.081

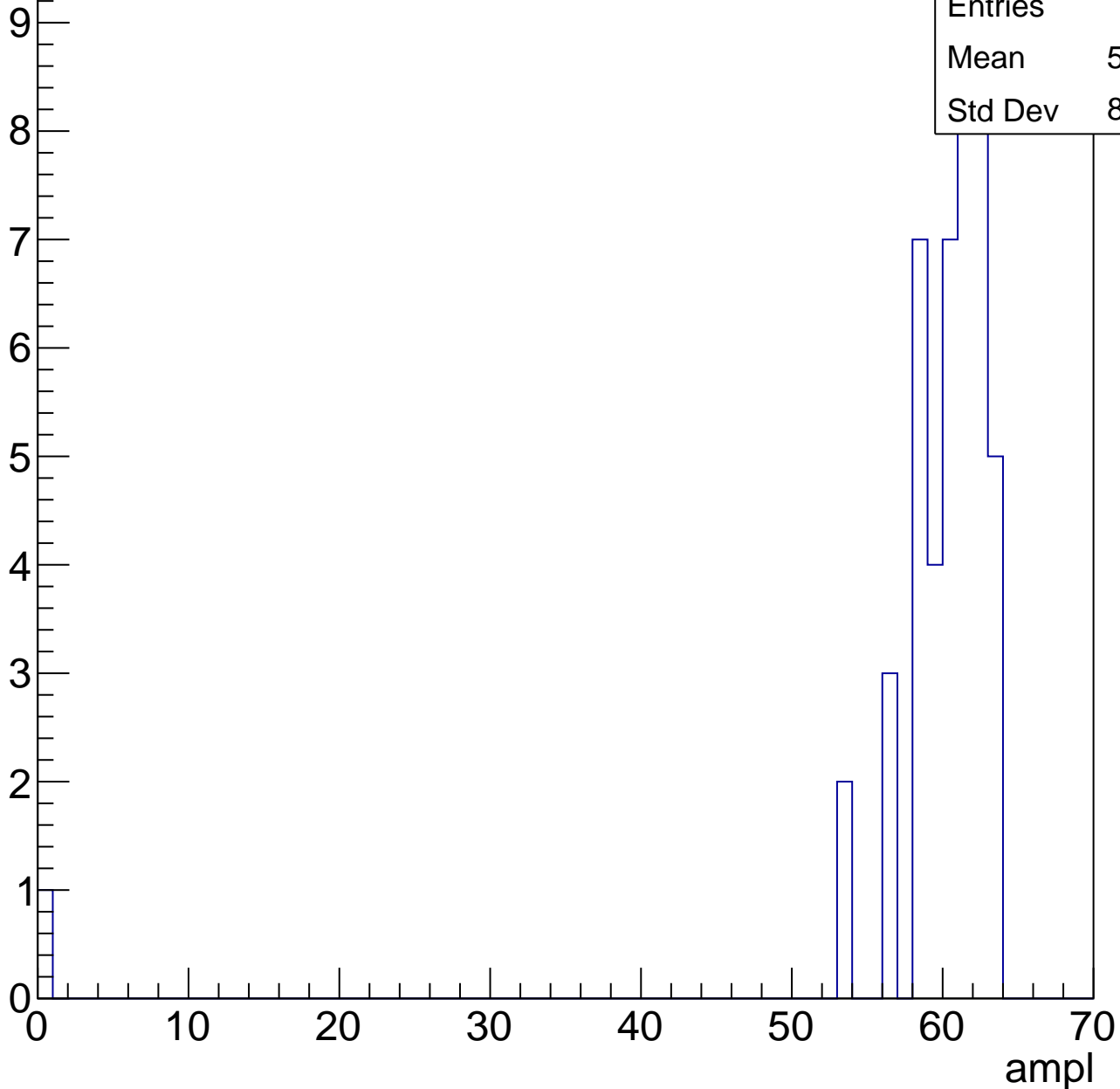


B1L003S, U19-ch85, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	58.68
Std Dev	8.975



B1L003S, U19-ch85, adc6

calib_packv5_042523_0143.root, FC#13, port D2

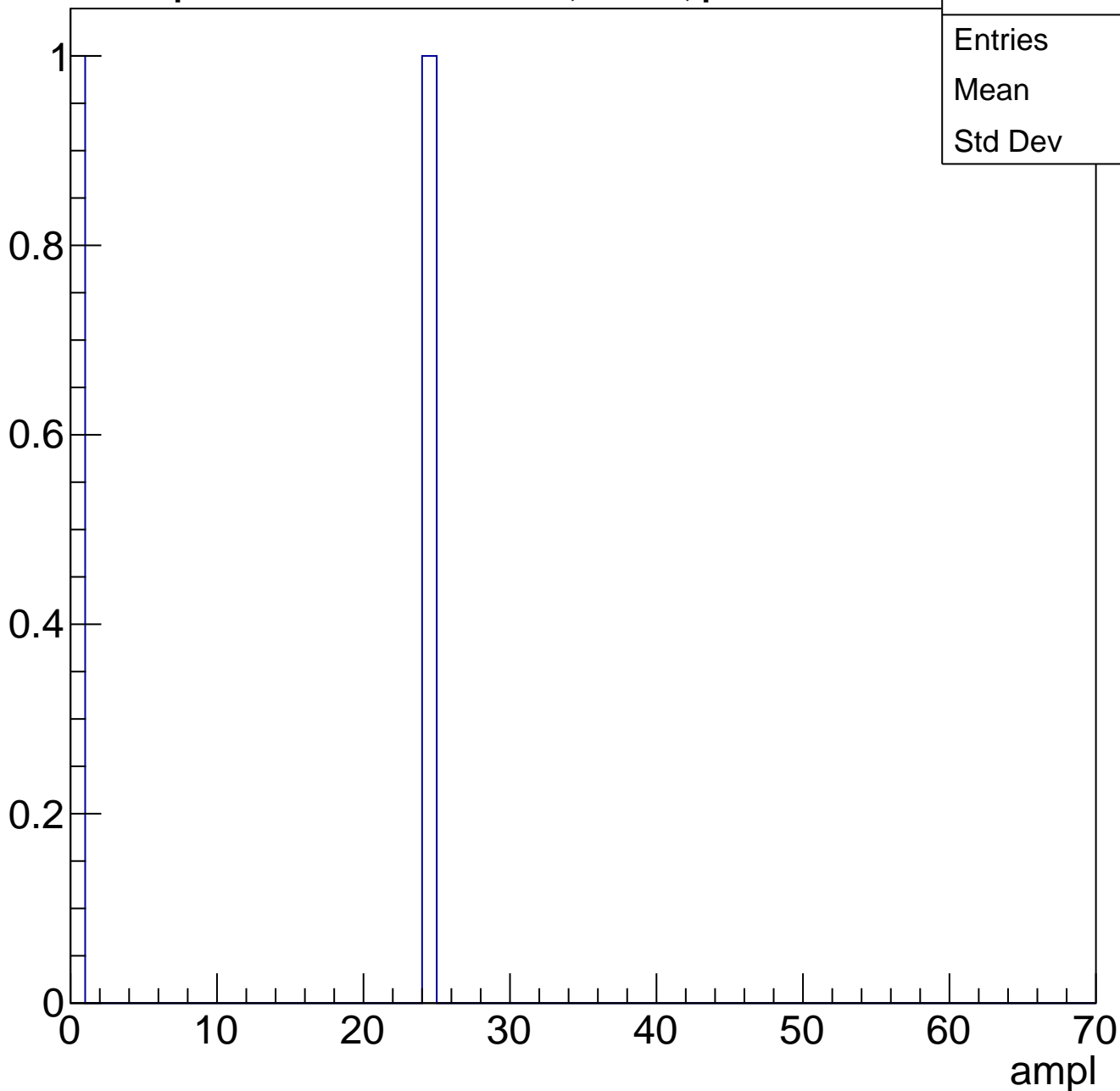
Entry



B1L003S, U19-ch85, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	12
Std Dev	12

B1L003S, U19-ch86, adc0

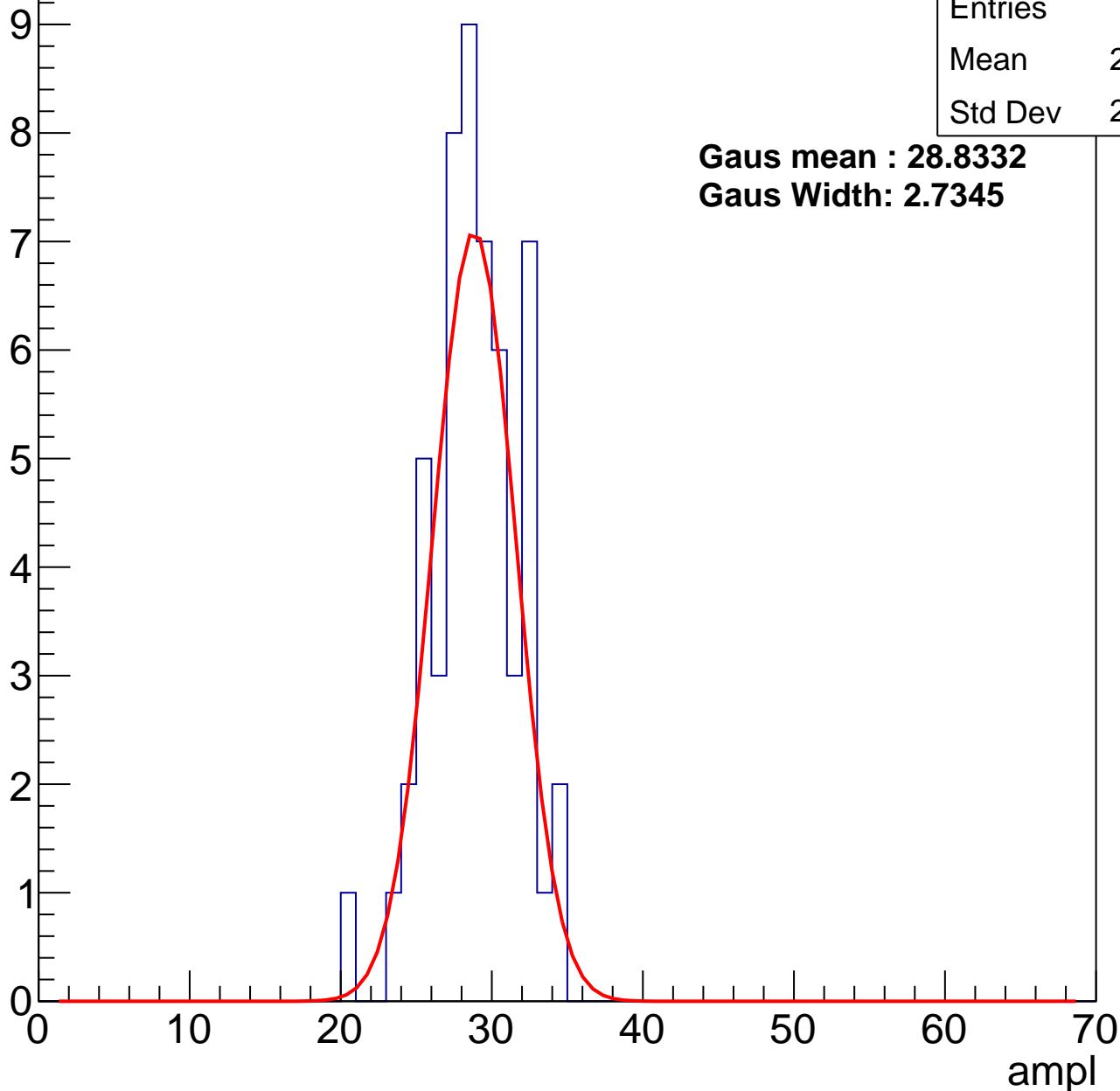
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	28.42
Std Dev	2.839

Gaus mean : 28.8332

Gaus Width: 2.7345



B1L003S, U19-ch86, adc1

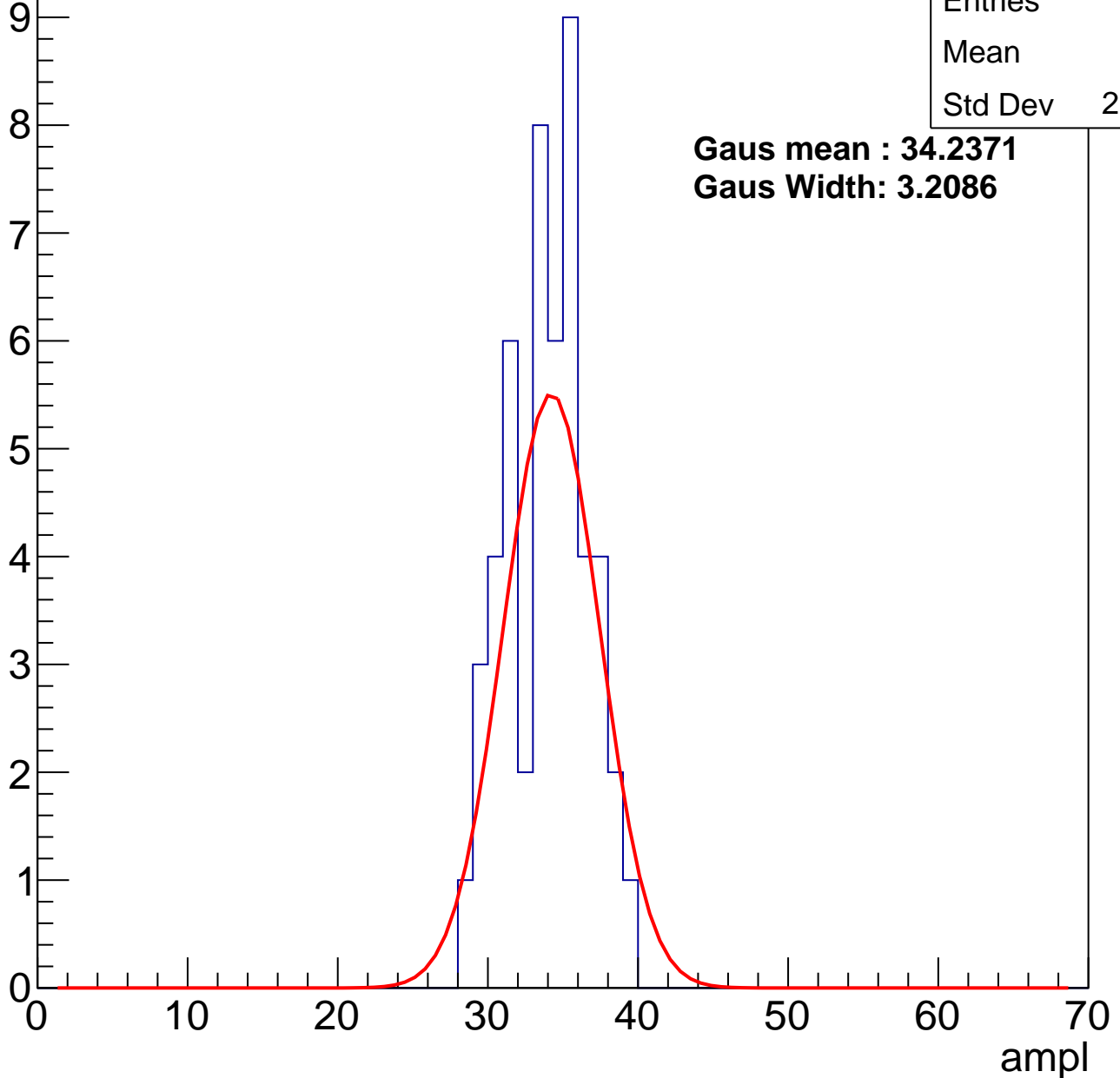
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	33.5
Std Dev	2.648

Gaus mean : 34.2371

Gaus Width: 3.2086



B1L003S, U19-ch86, adc2

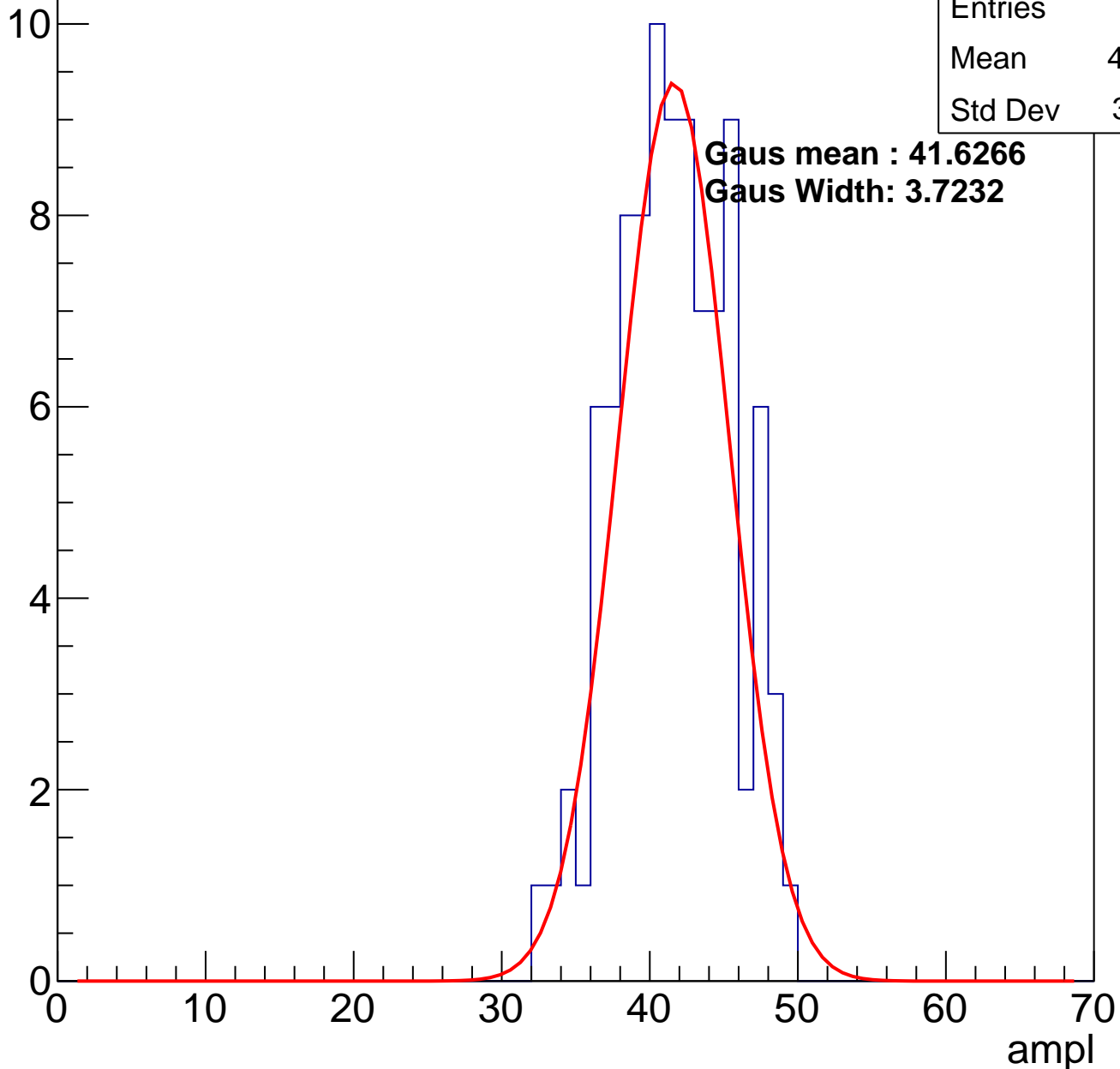
calib_packv5_042523_0143.root, FC#13, port D2

Entries	96
Mean	41.15
Std Dev	3.761

Gaus mean : 41.6266

Gaus Width: 3.7232

Entry

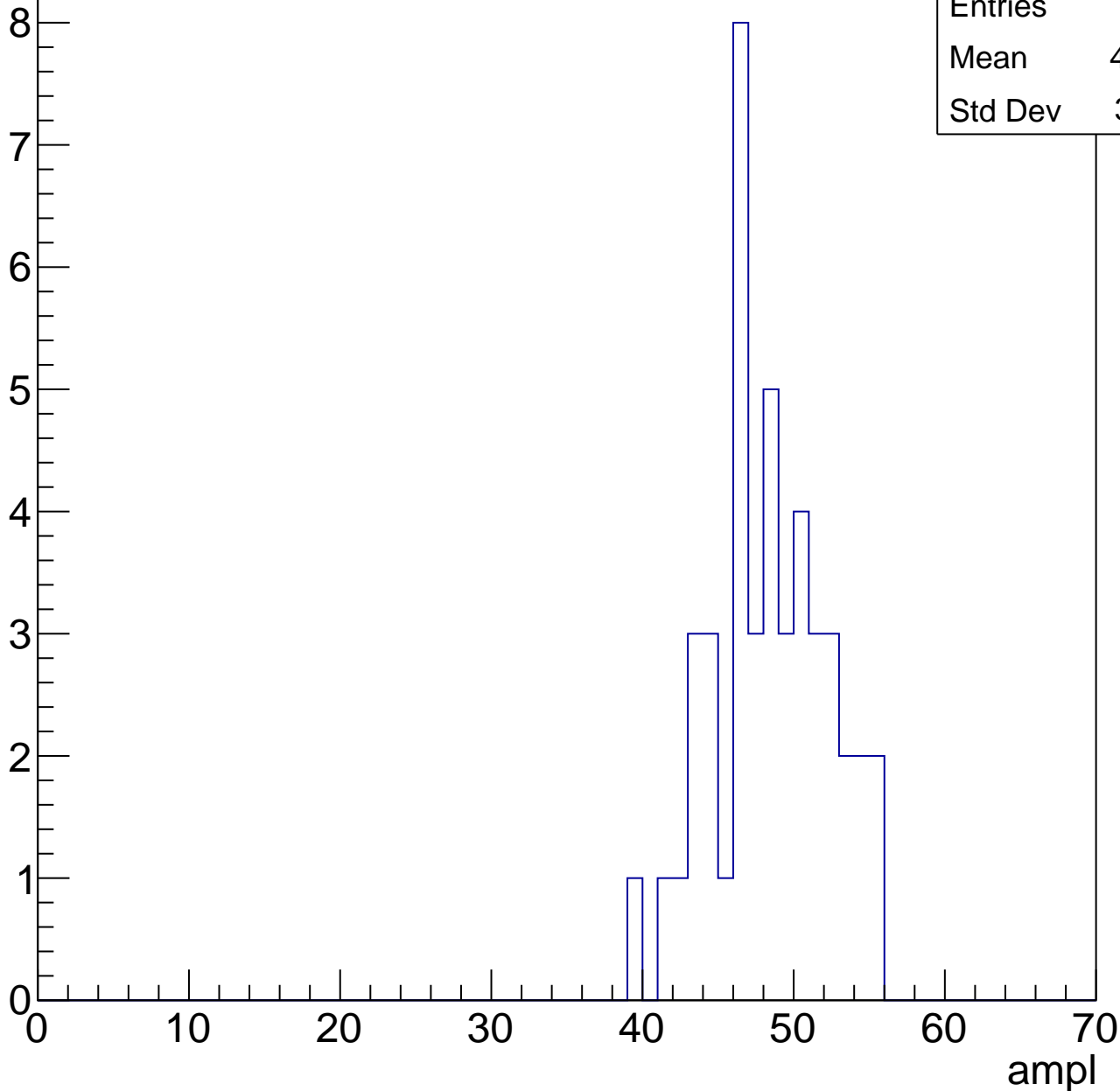


B1L003S, U19-ch86, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	47.93
Std Dev	3.791

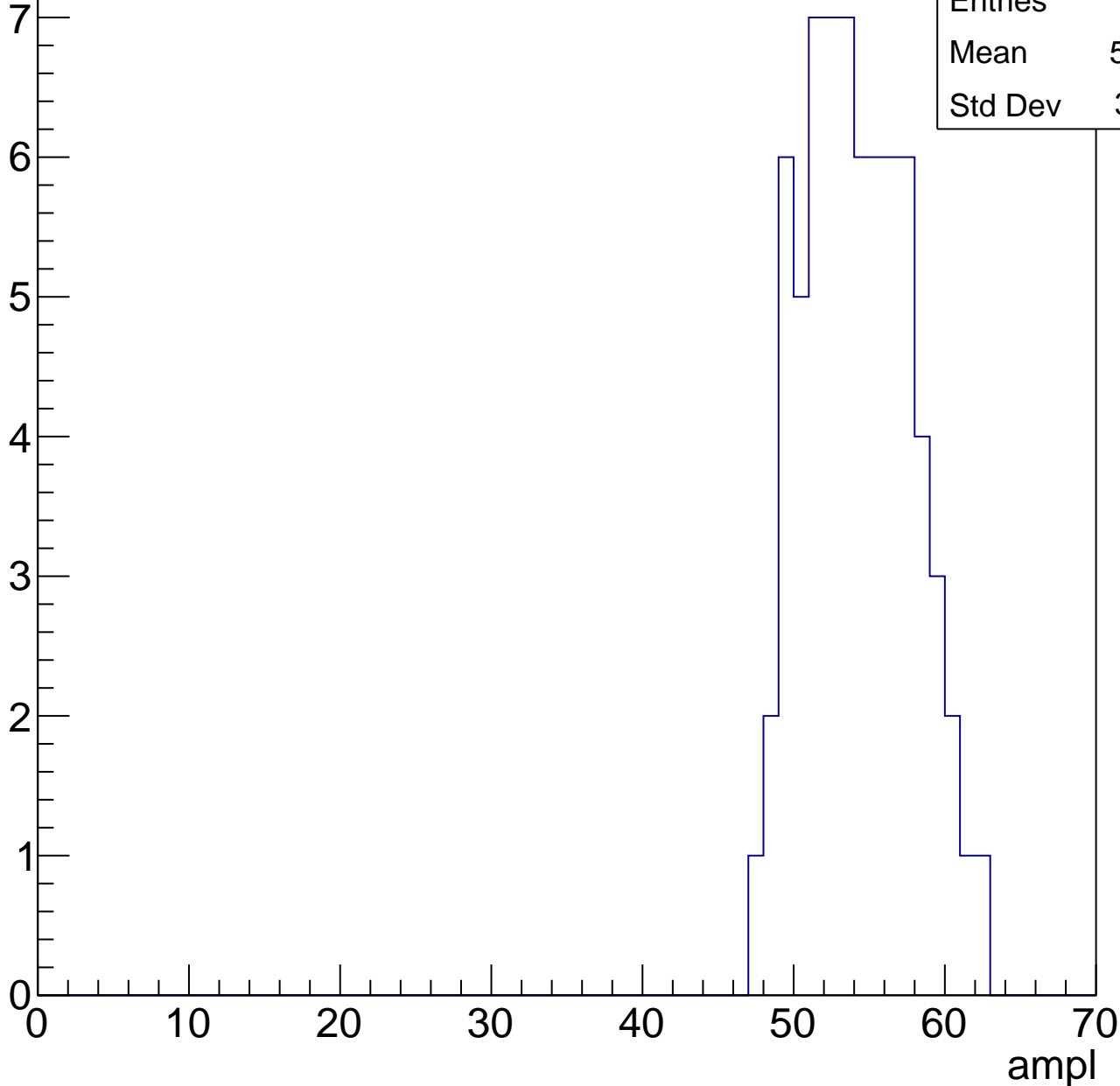


B1L003S, U19-ch86, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	53.76
Std Dev	3.491



B1L003S, U19-ch86, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	50
Mean	59.08
Std Dev	2.965

ampl

B1L003S, U19-ch86, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

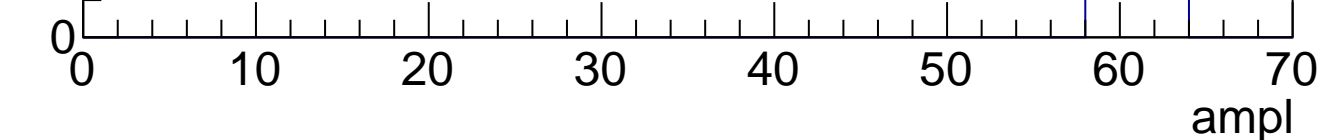
50

60

70

ampl

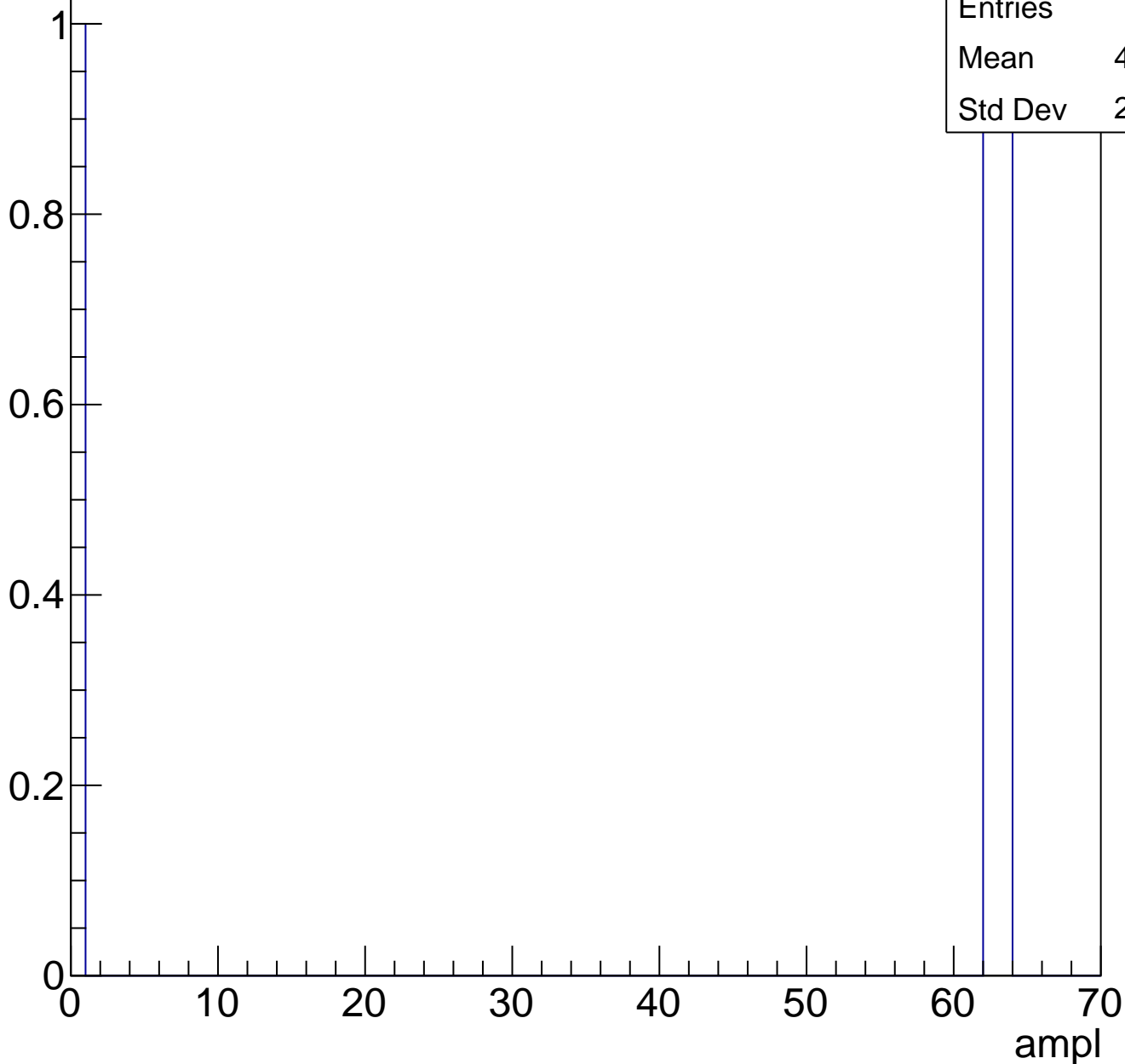
Entries	11
Mean	61.09
Std Dev	1.621



B1L003S, U19-ch86, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch87, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	87
Mean	28.75
Std Dev	6.564

Gaus mean : 30.3156

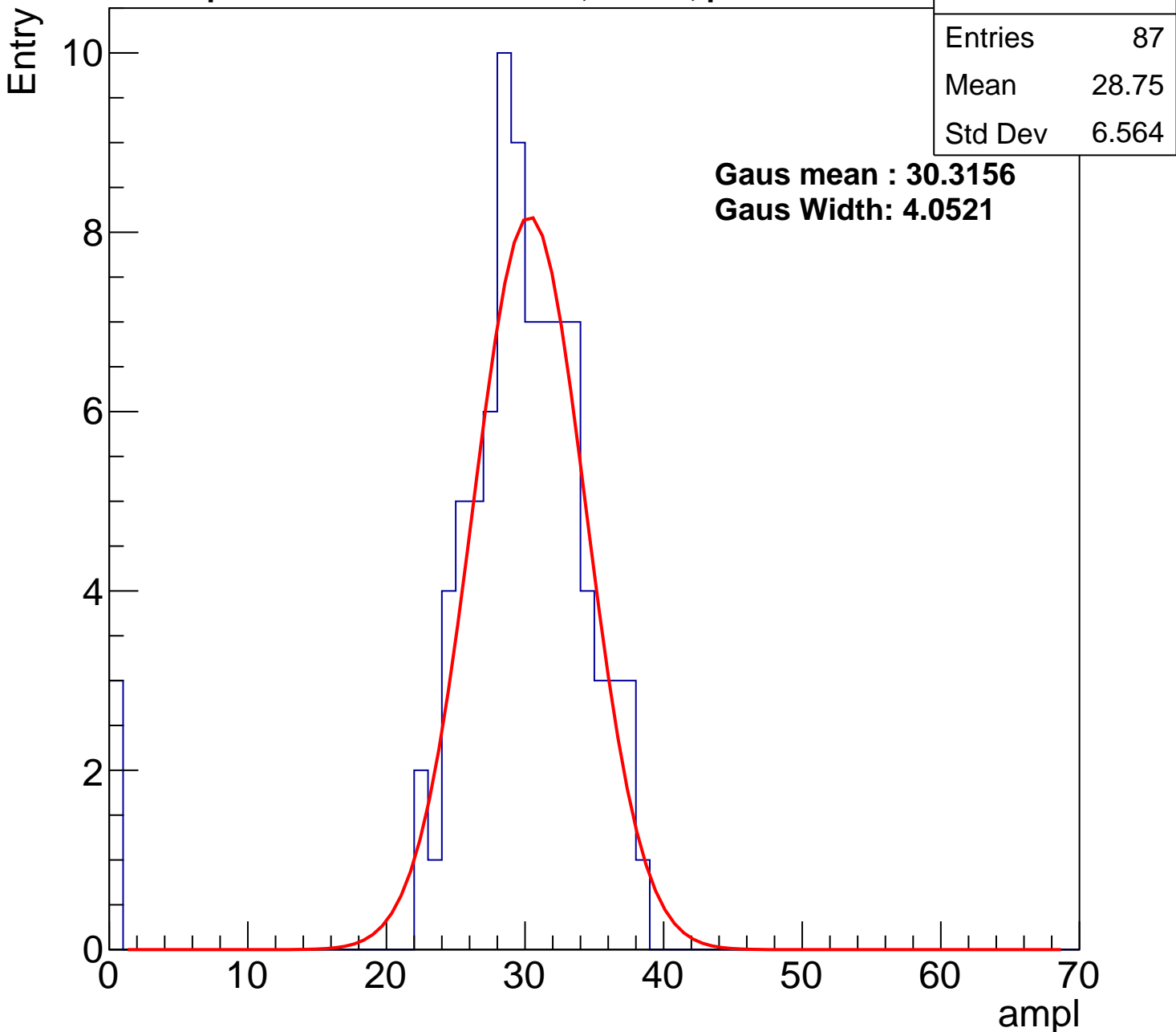
Gaus Width: 4.0521

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch87, adc1

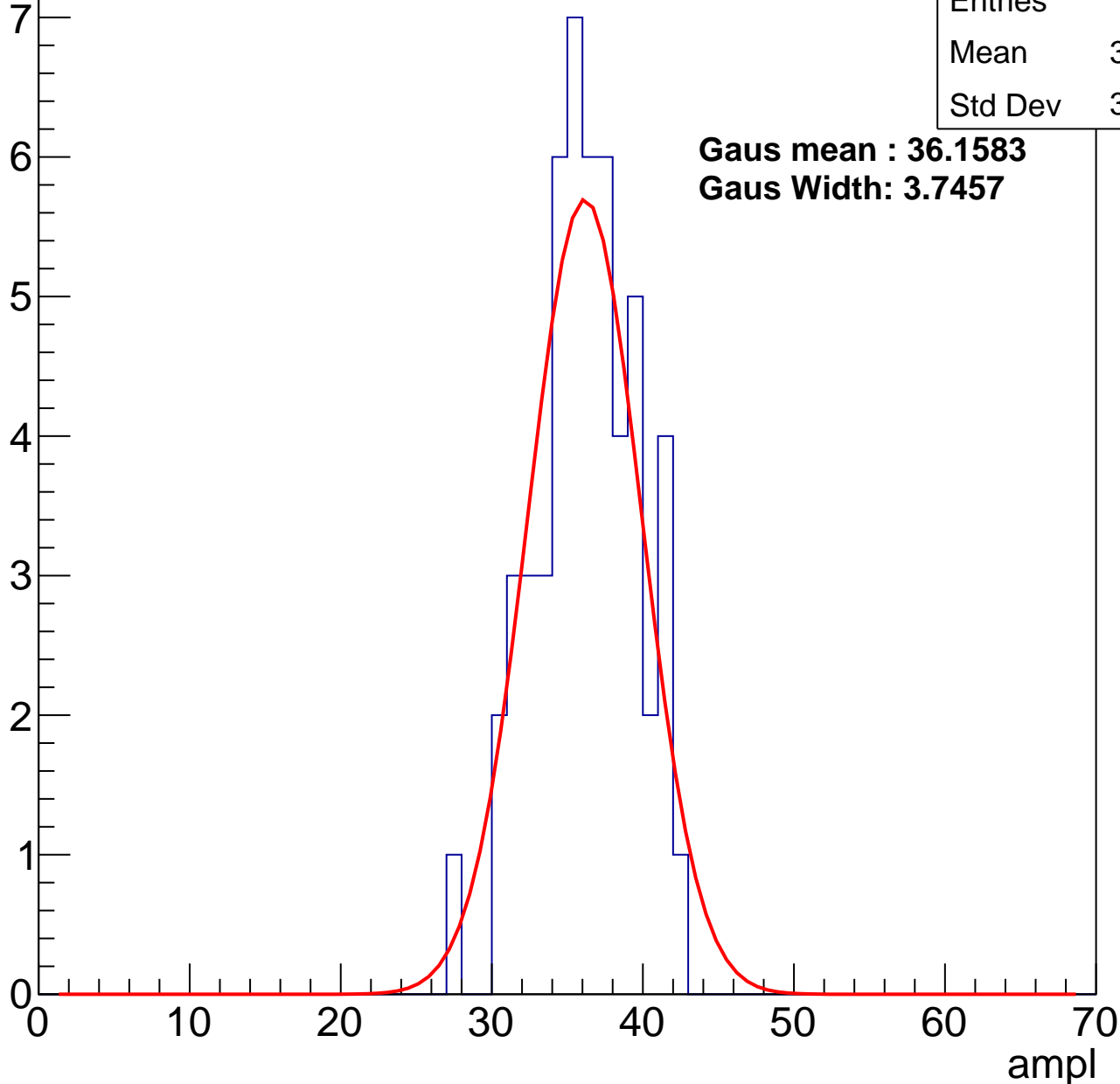
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.75
Std Dev	3.267

Gaus mean : 36.1583

Gaus Width: 3.7457



B1L003S, U19-ch87, adc2

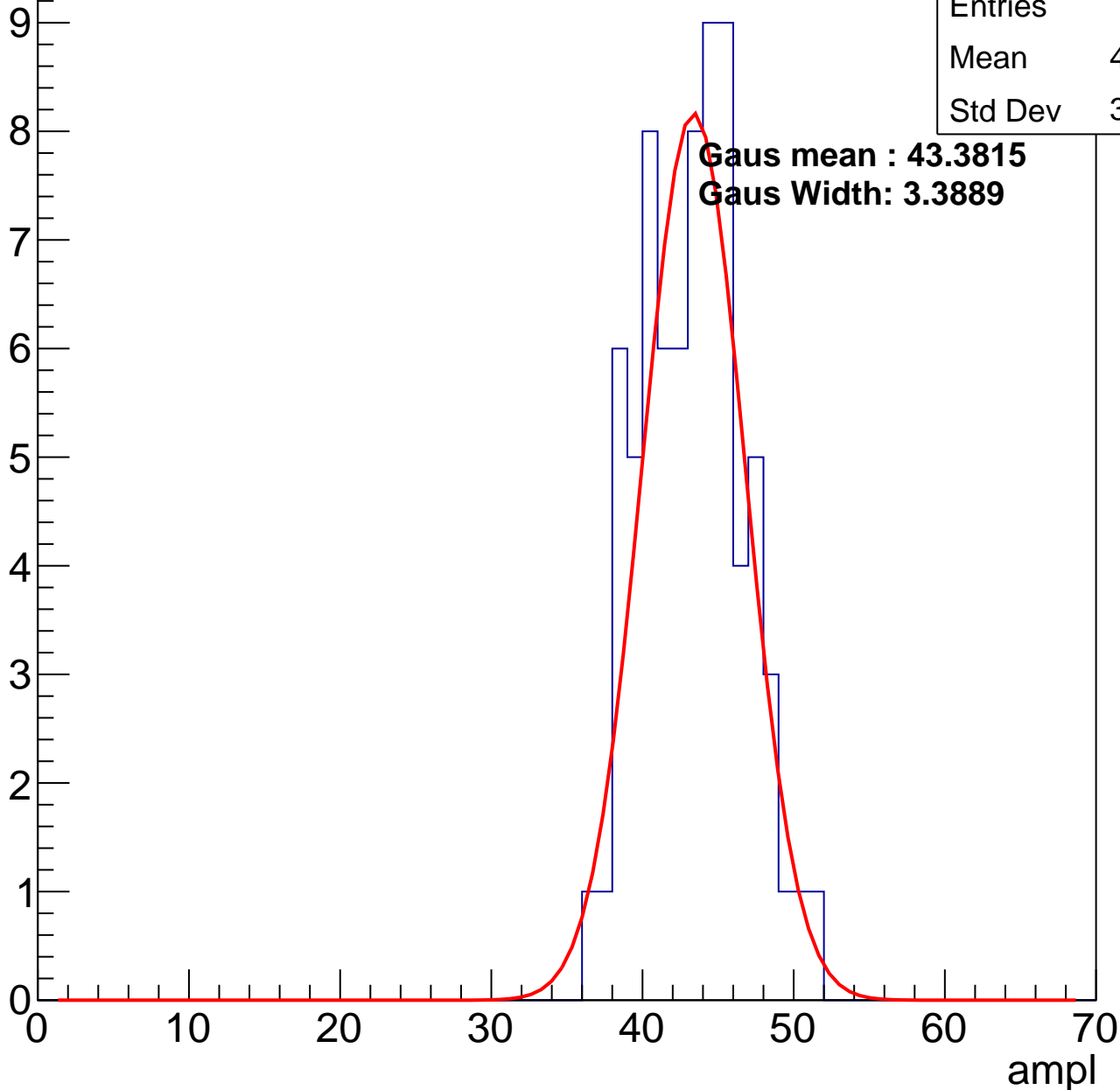
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	42.86
Std Dev	3.289

Gaus mean : 43.3815

Gaus Width: 3.3889

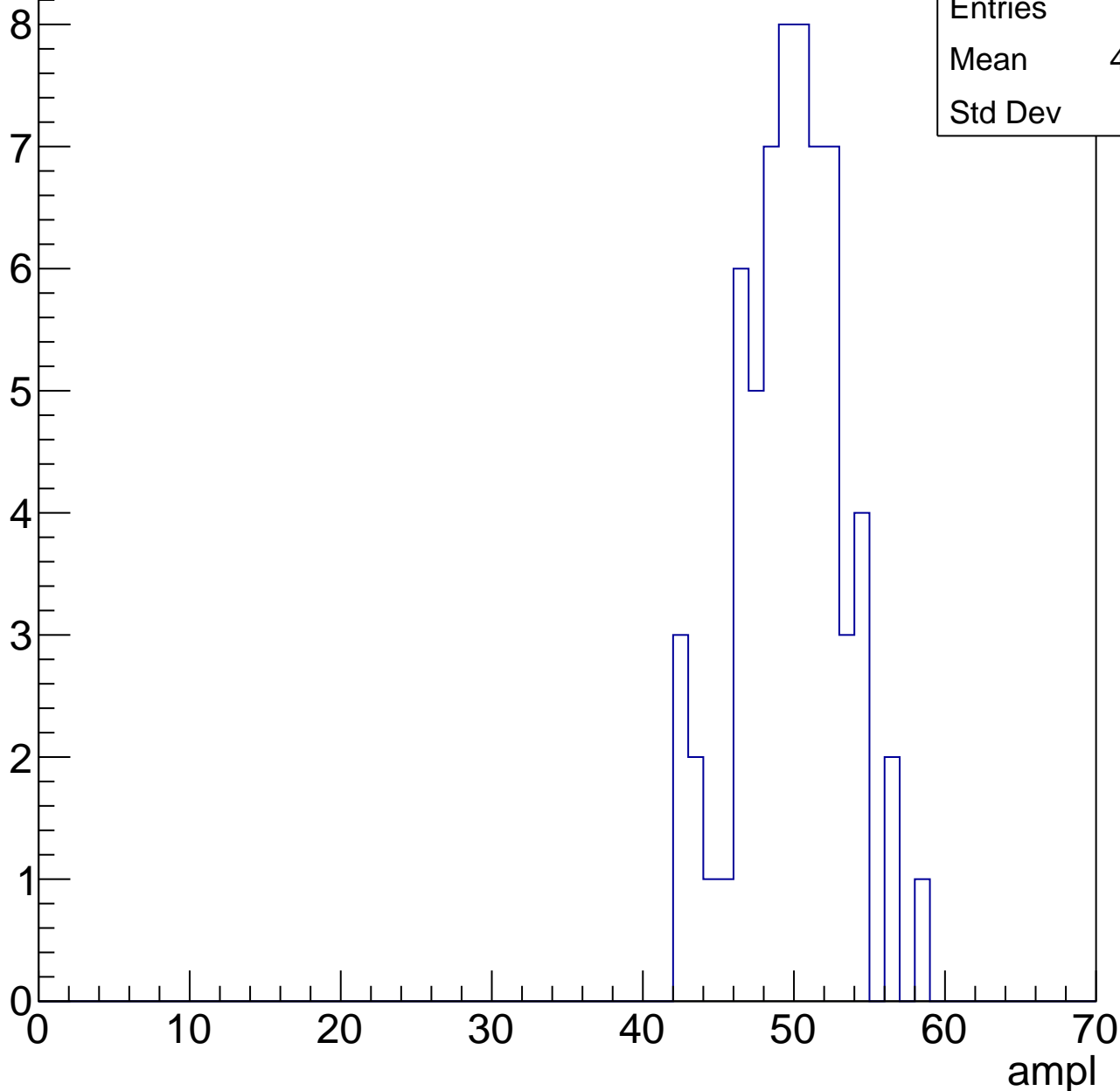


B1L003S, U19-ch87, adc3

calib_packv5_042523_0143.root, FC#13, port D2

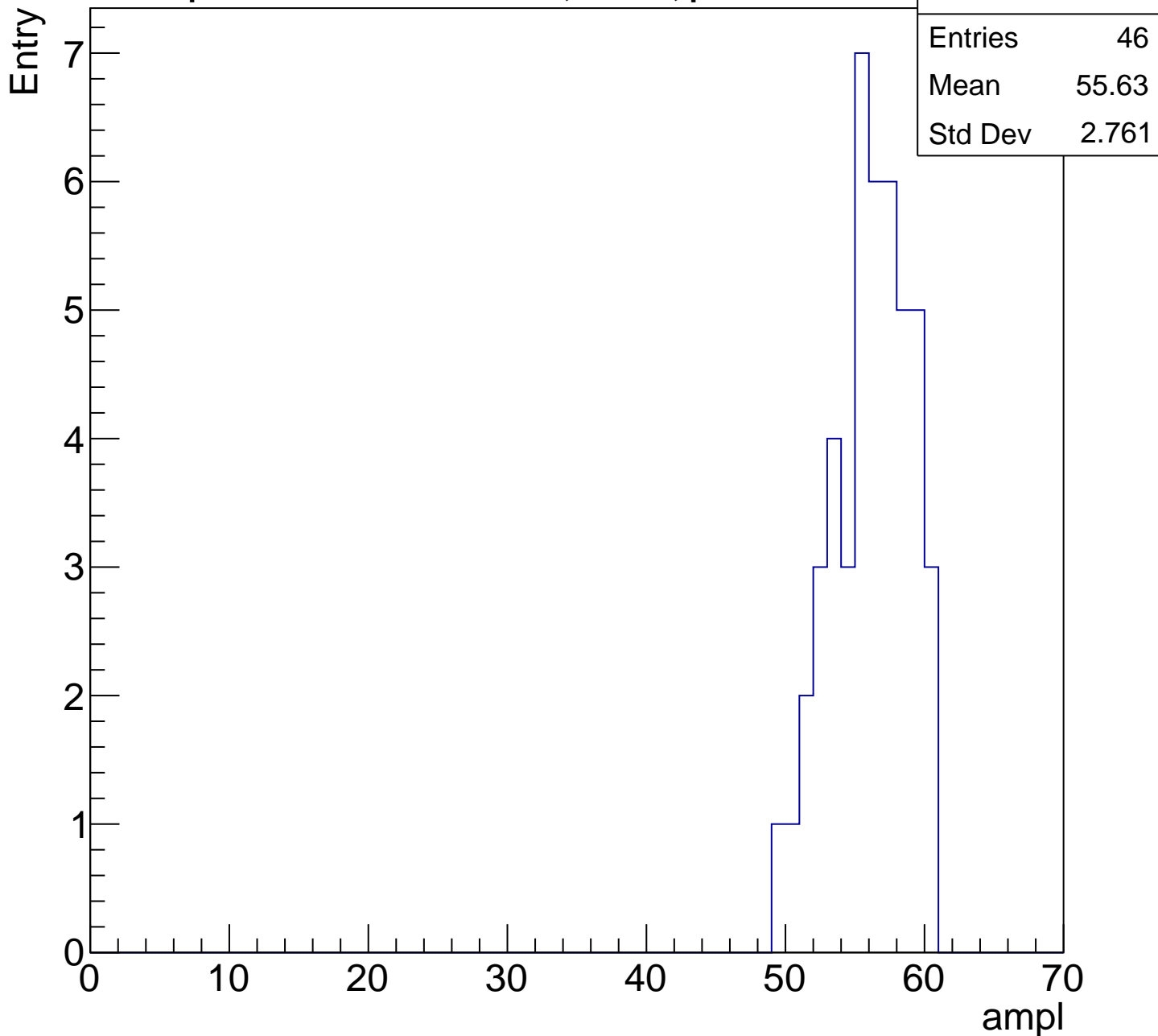
Entry

Entries	65
Mean	49.32
Std Dev	3.42



B1L003S, U19-ch87, adc4

calib_packv5_042523_0143.root, FC#13, port D2

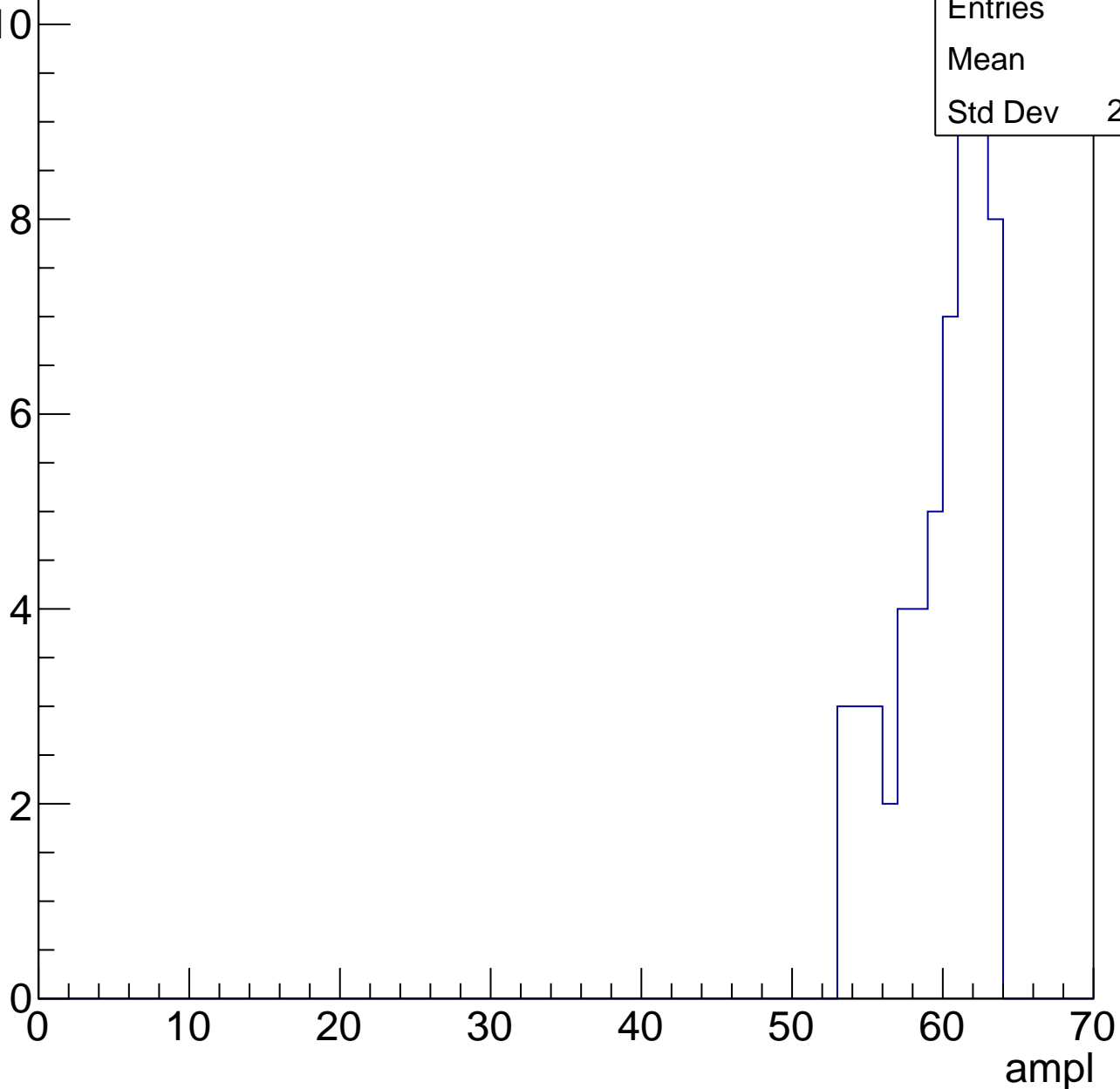


B1L003S, U19-ch87, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	59.4
Std Dev	2.982



B1L003S, U19-ch87, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch87, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch88, adc0

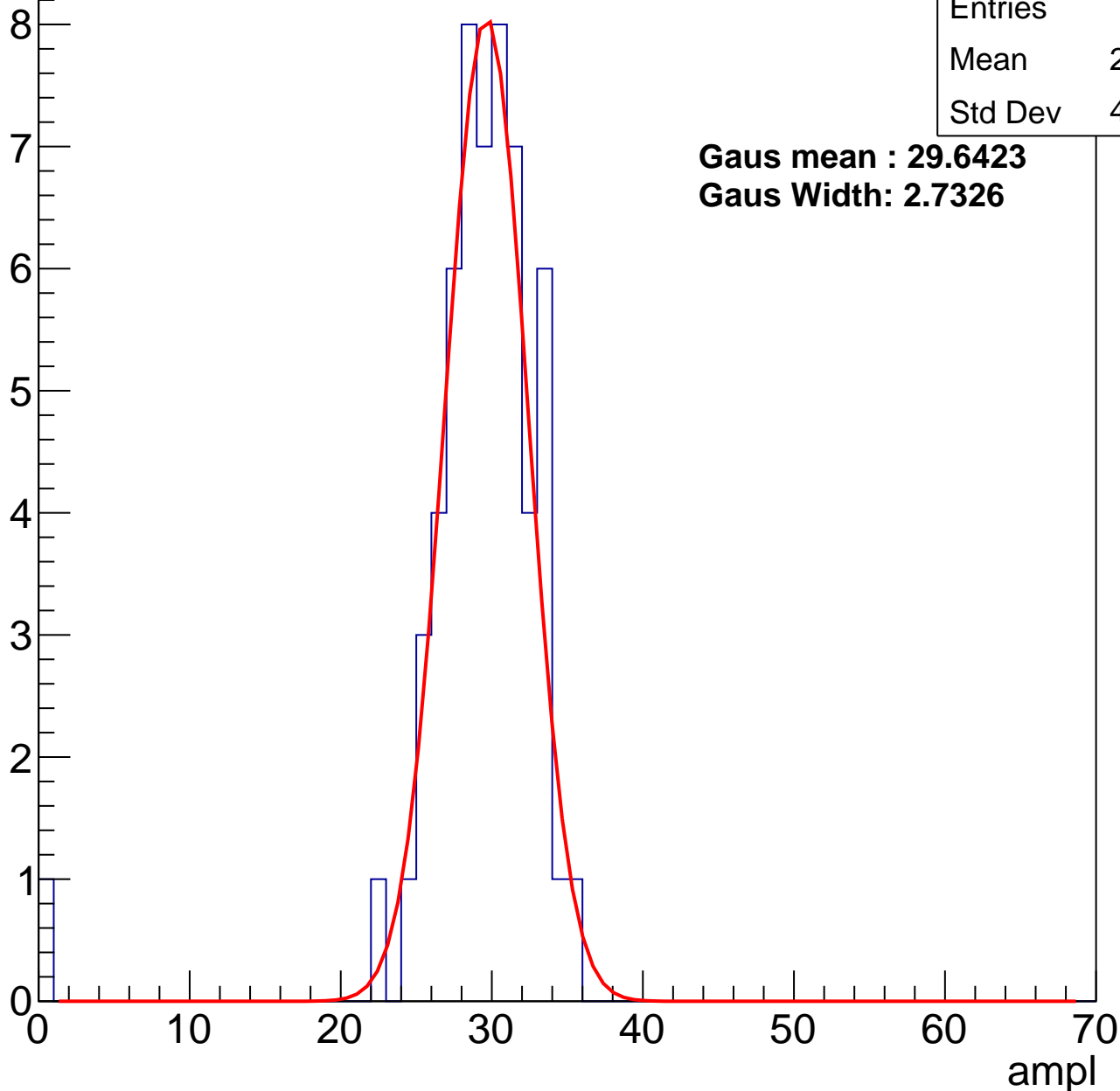
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.72
Std Dev	4.653

Gaus mean : 29.6423

Gaus Width: 2.7326



B1L003S, U19-ch88, adc1

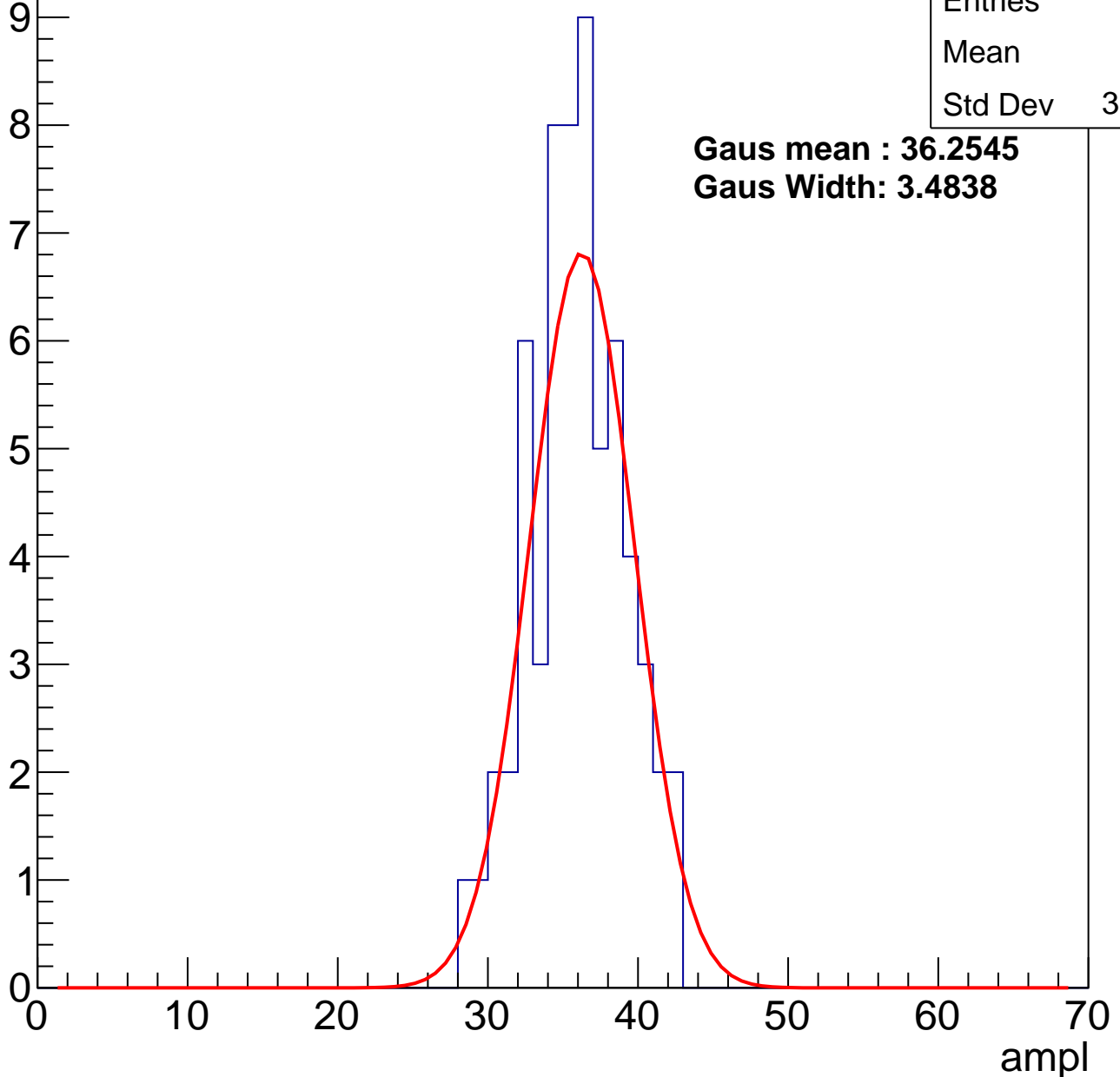
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	35.5
Std Dev	3.156

Gaus mean : 36.2545

Gaus Width: 3.4838



B1L003S, U19-ch88, adc2

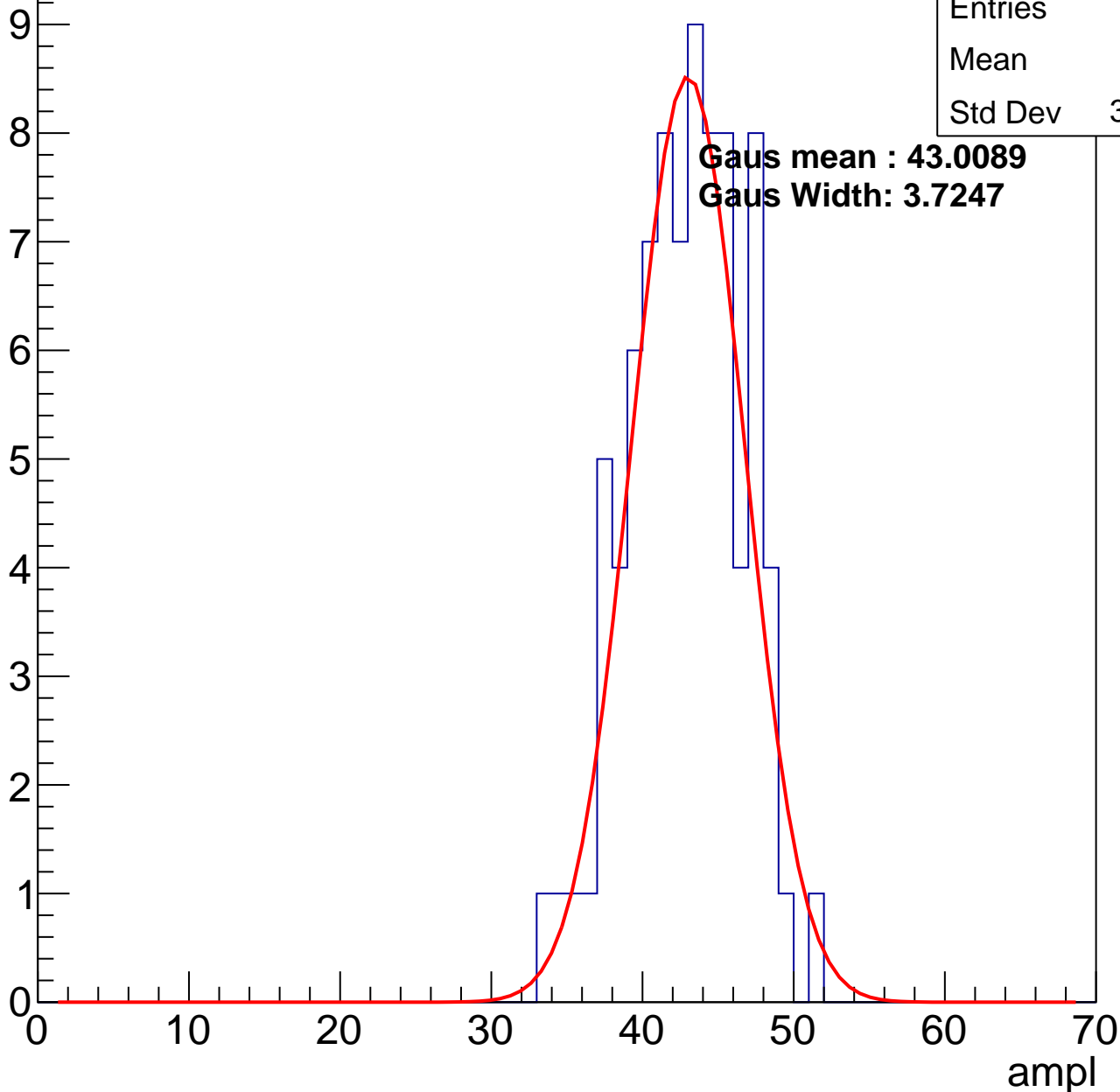
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	42.4
Std Dev	3.694

Gaus mean : 43.0089

Gaus Width: 3.7247

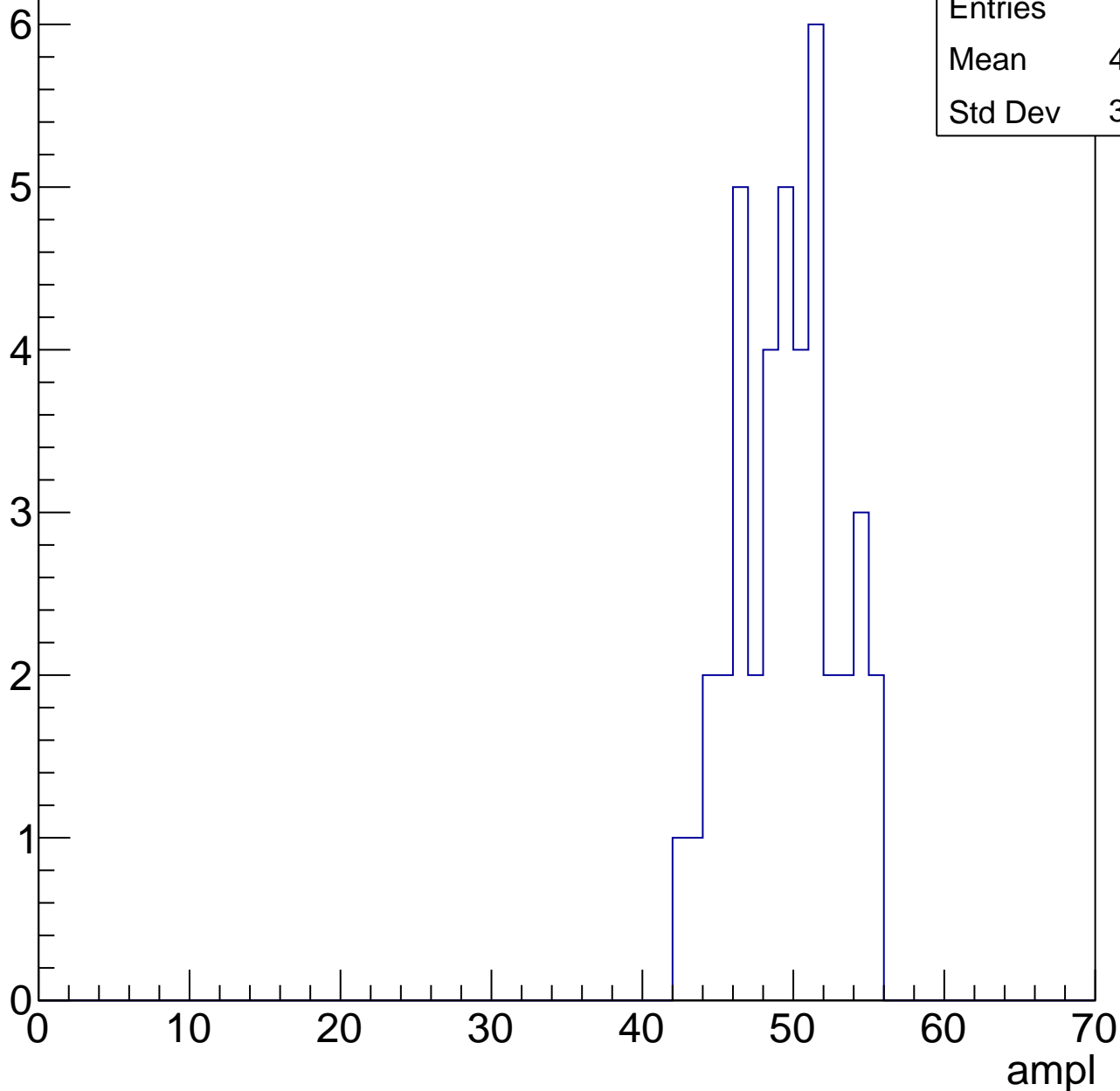


B1L003S, U19-ch88, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	49.07
Std Dev	3.308

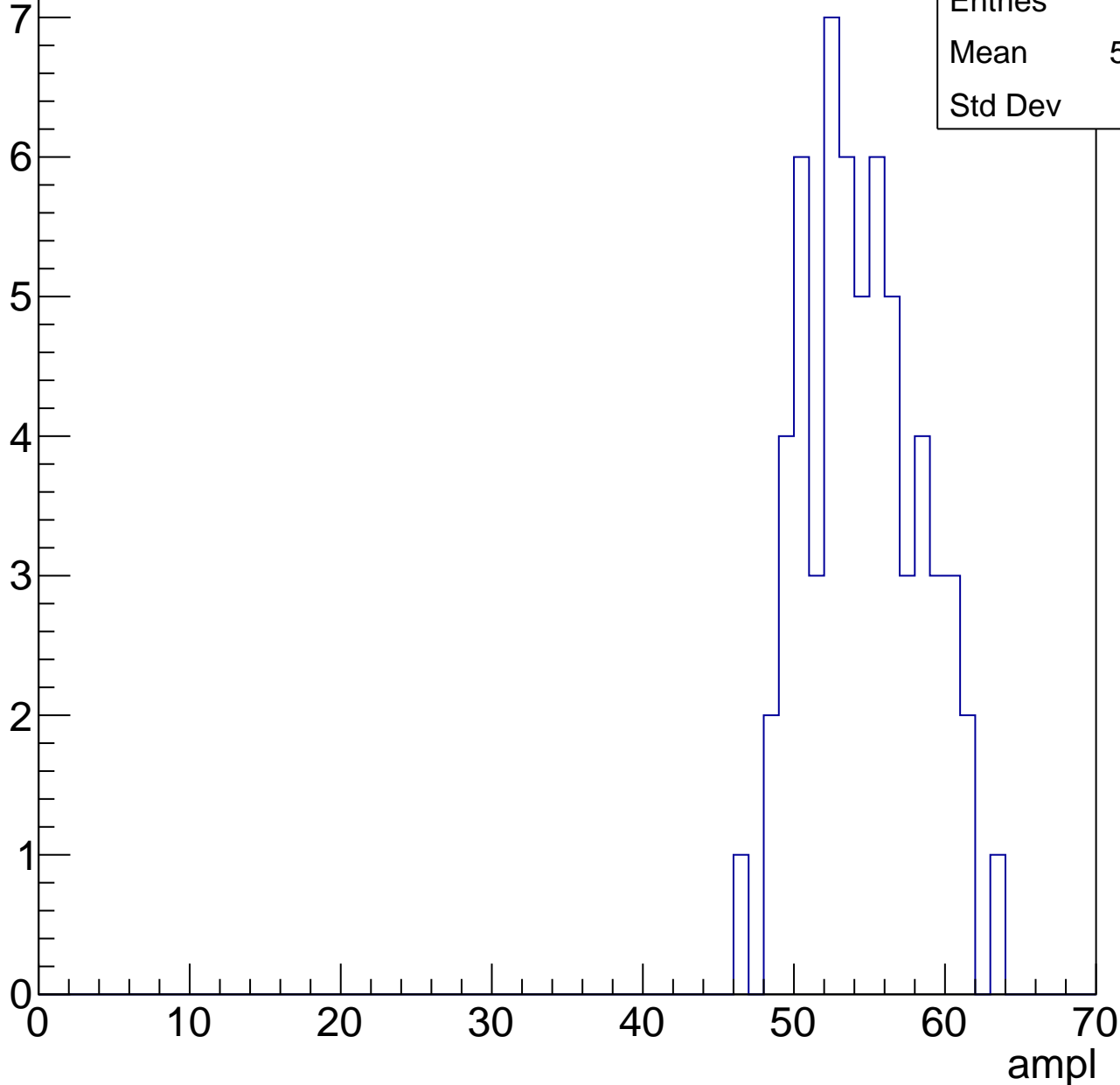


B1L003S, U19-ch88, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

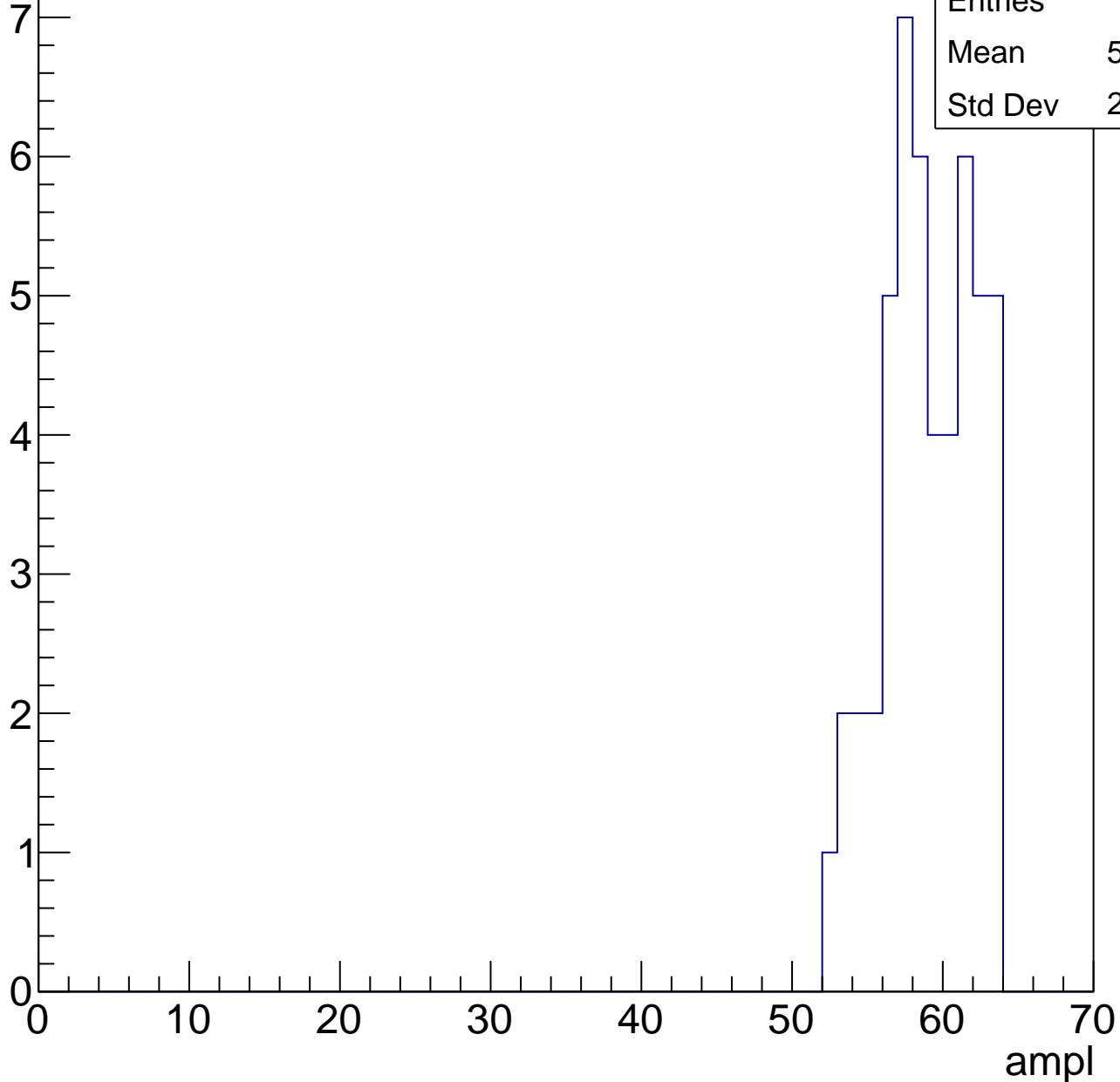
Entries	61
Mean	54.07
Std Dev	3.78



B1L003S, U19-ch88, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



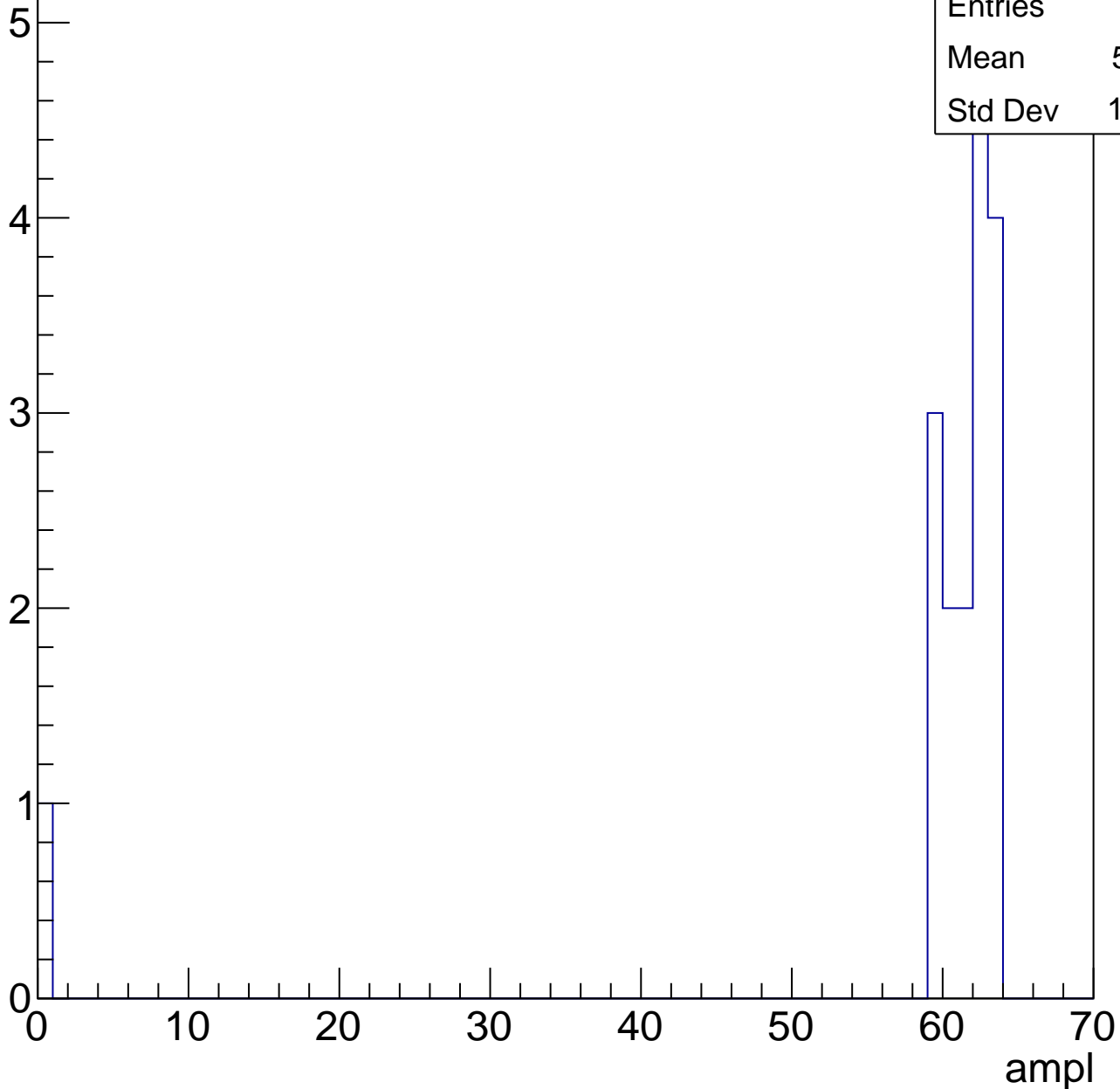
Entries	49
Mean	58.57
Std Dev	2.949

B1L003S, U19-ch88, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

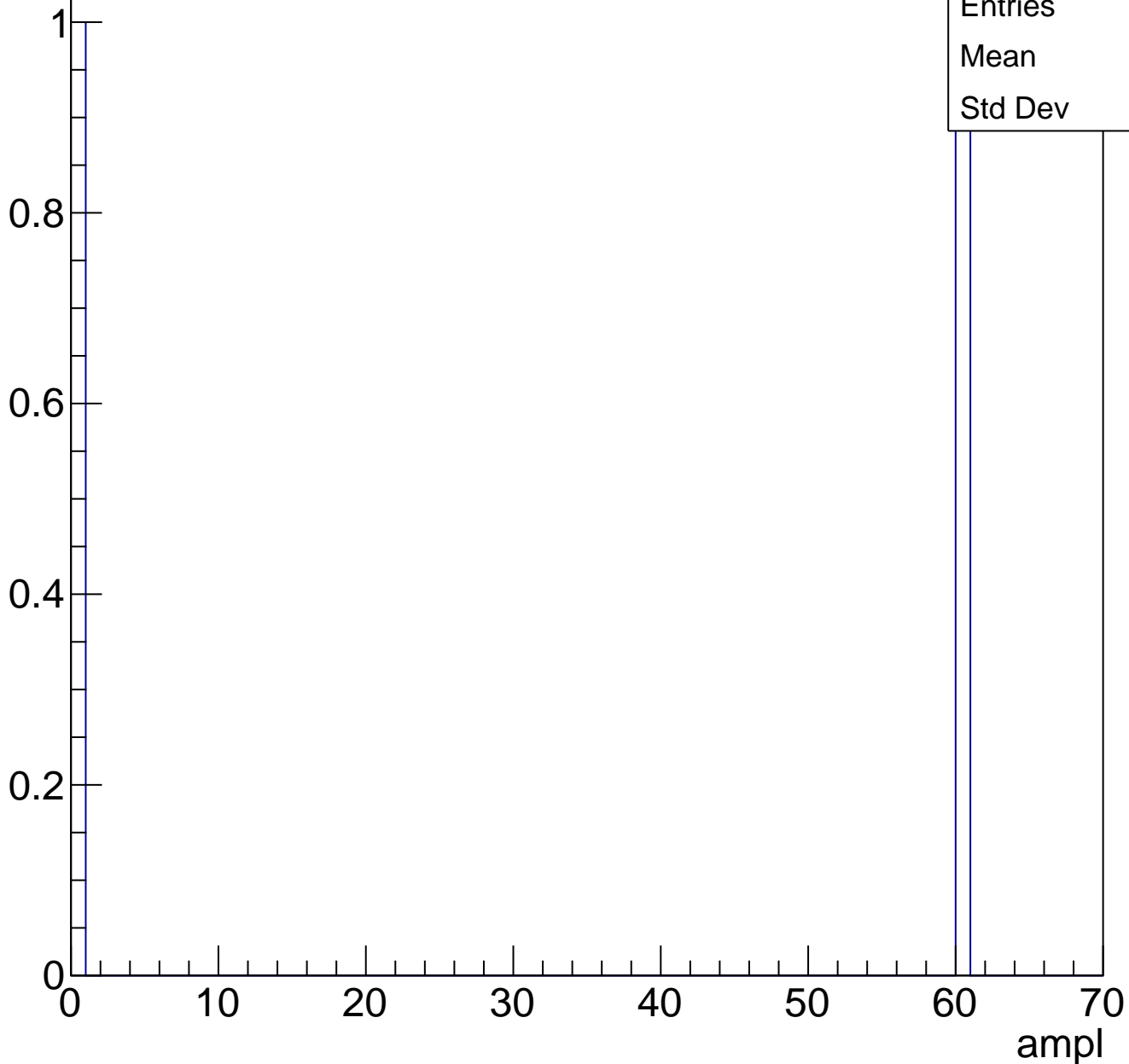
Entries	17
Mean	57.71
Std Dev	14.49



B1L003S, U19-ch88, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch89, adc0

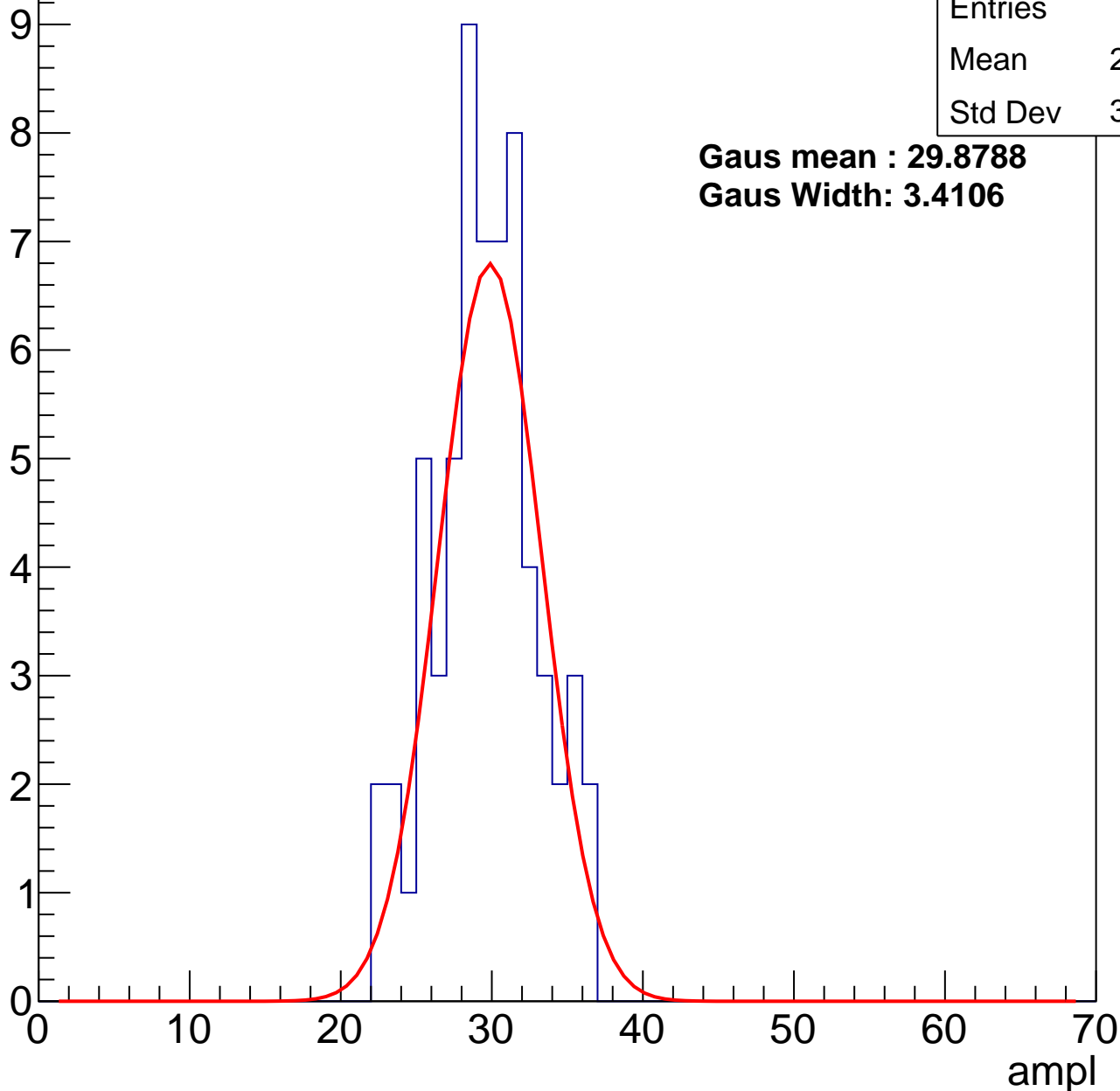
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.16
Std Dev	3.353

Gaus mean : 29.8788

Gaus Width: 3.4106



B1L003S, U19-ch89, adc1

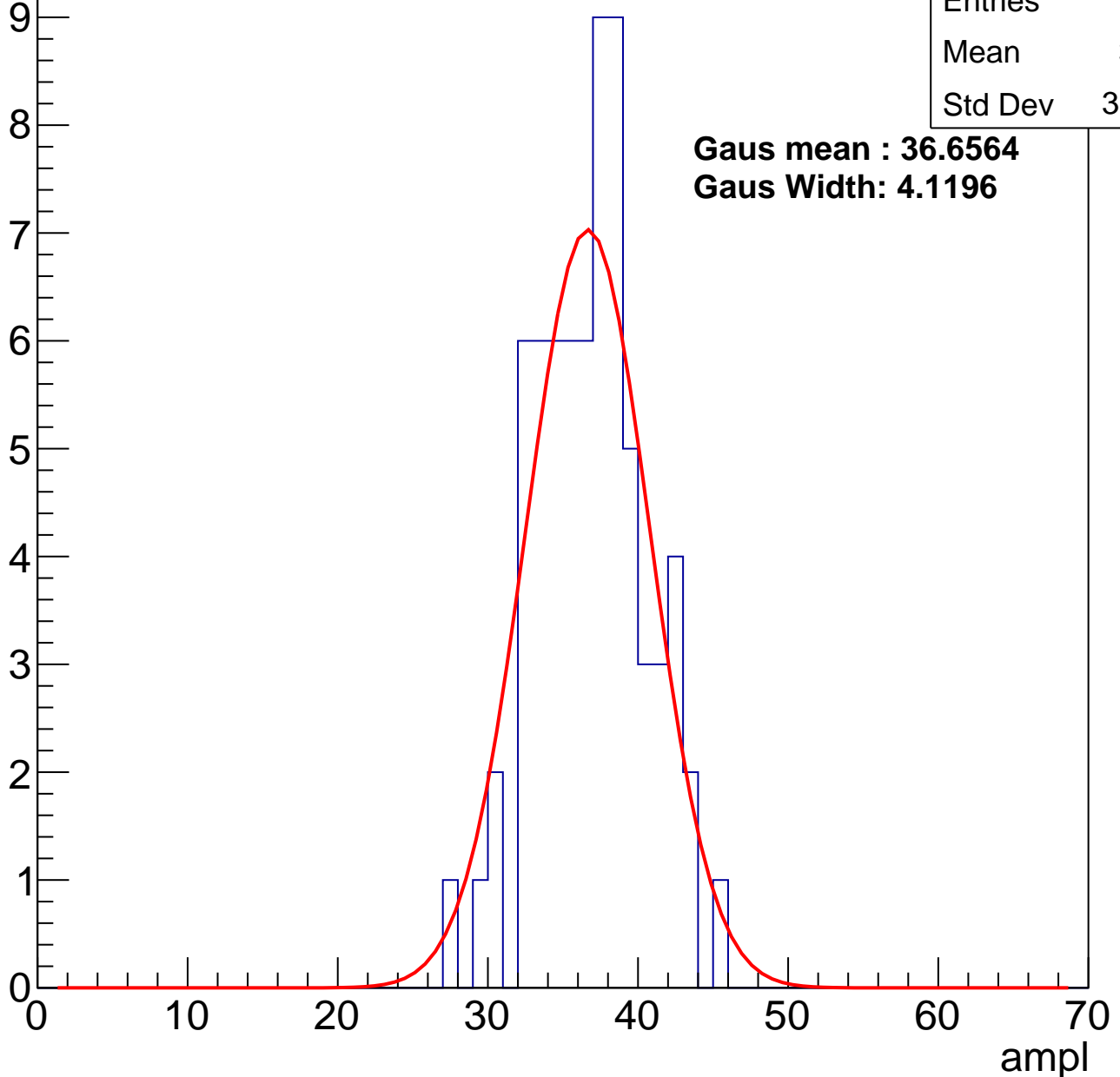
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.4
Std Dev	3.599

Gaus mean : 36.6564

Gaus Width: 4.1196



B1L003S, U19-ch89, adc2

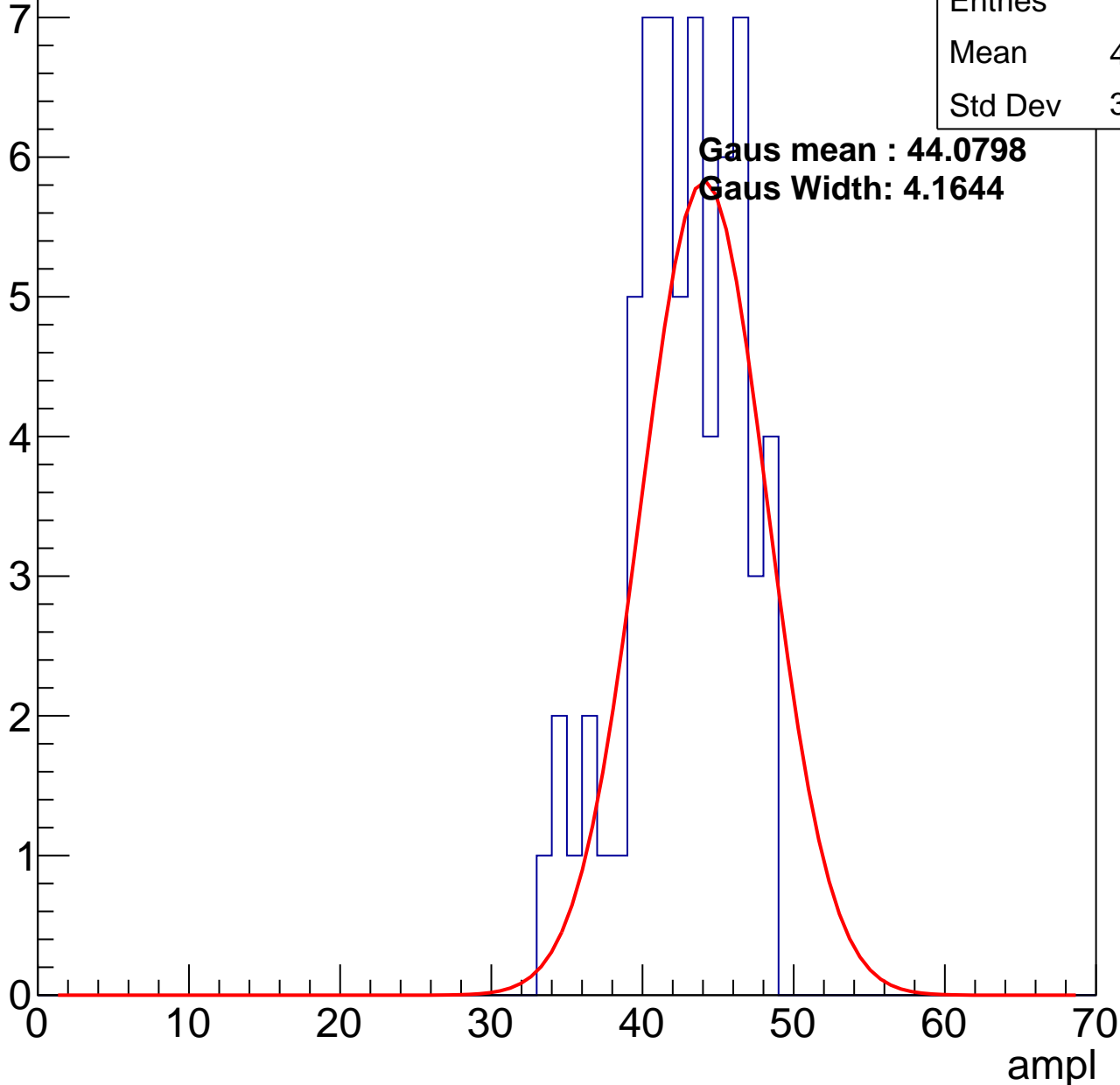
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.17
Std Dev	3.688

Gaus mean : 44.0798

Gaus Width: 4.1644

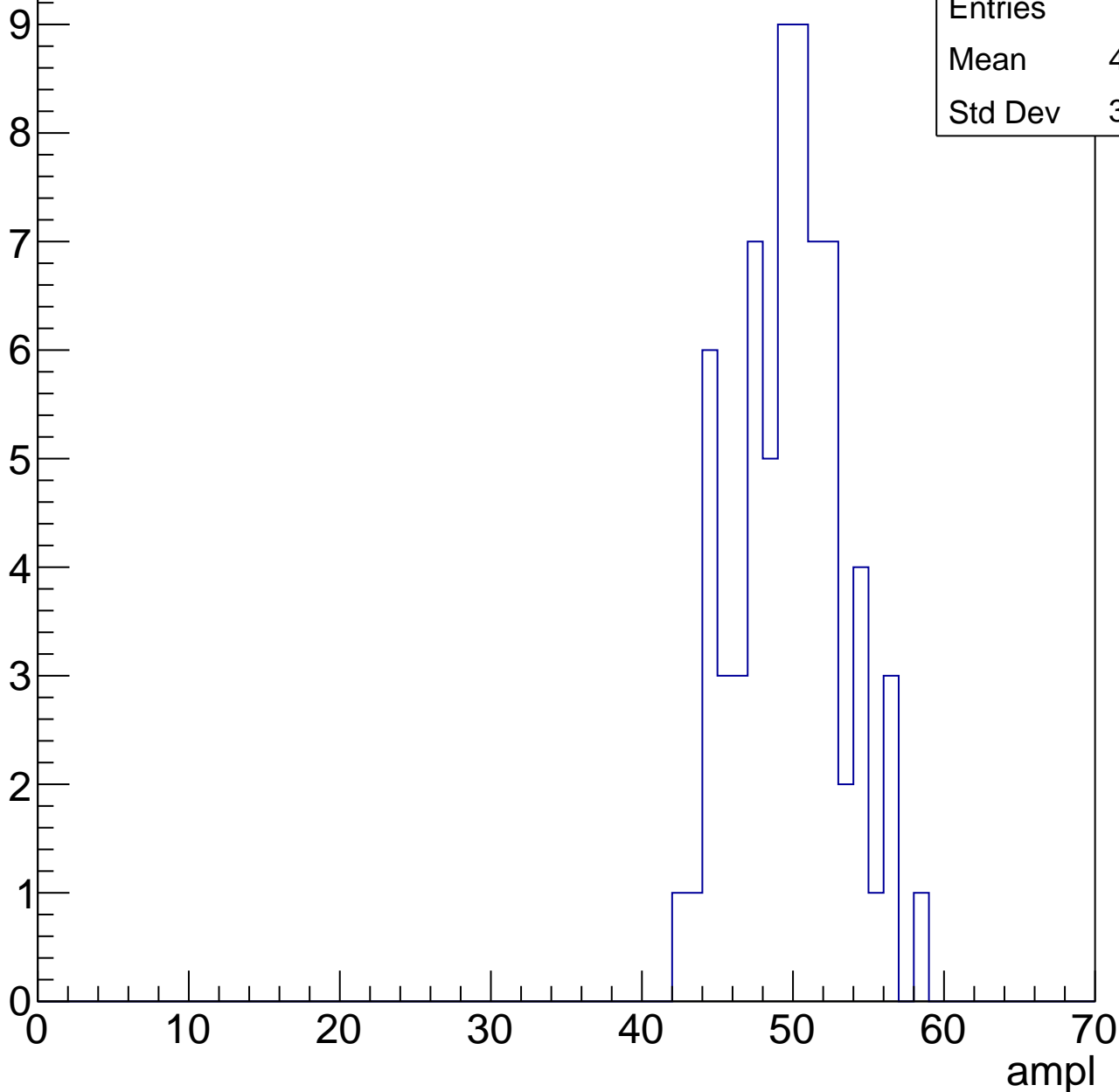


B1L003S, U19-ch89, adc3

calib_packv5_042523_0143.root, FC#13, port D2

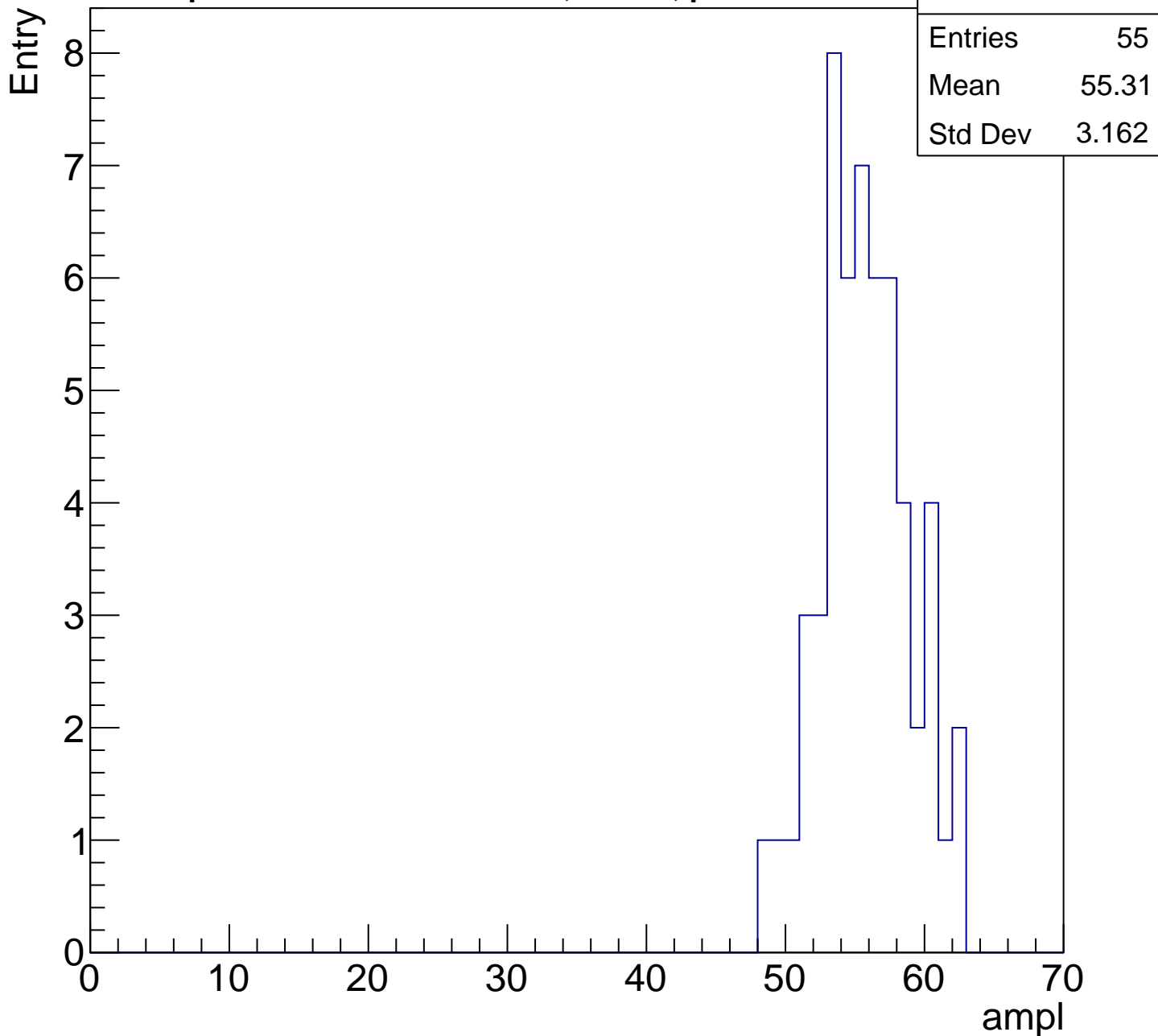
Entry

Entries	69
Mean	49.36
Std Dev	3.468



B1L003S, U19-ch89, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U19-ch89, adc5

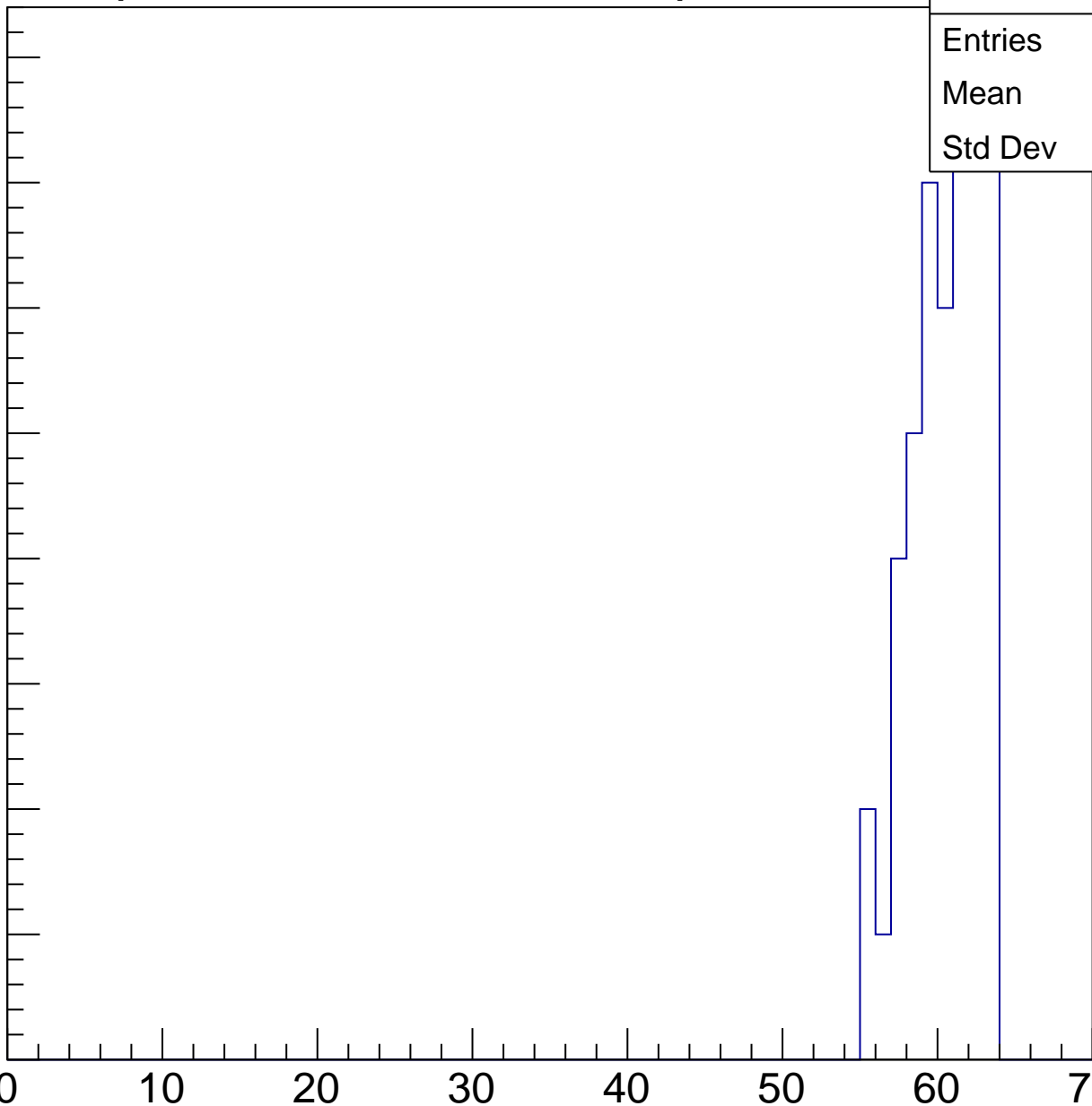
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	60.1
Std Dev	2.215

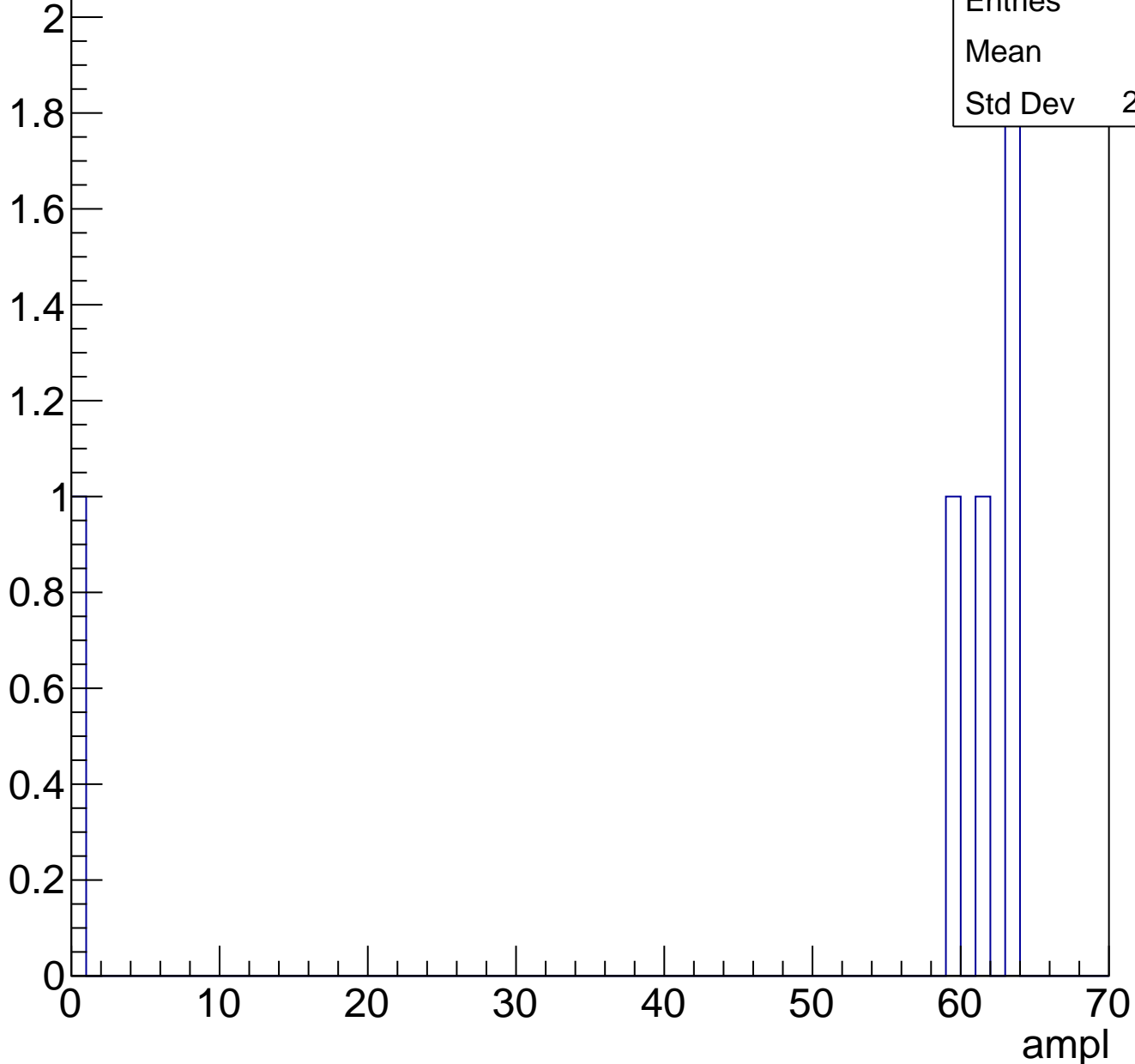
ampl



B1L003S, U19-ch89, adc6

calib_packv5_042523_0143.root, FC#13, port D2

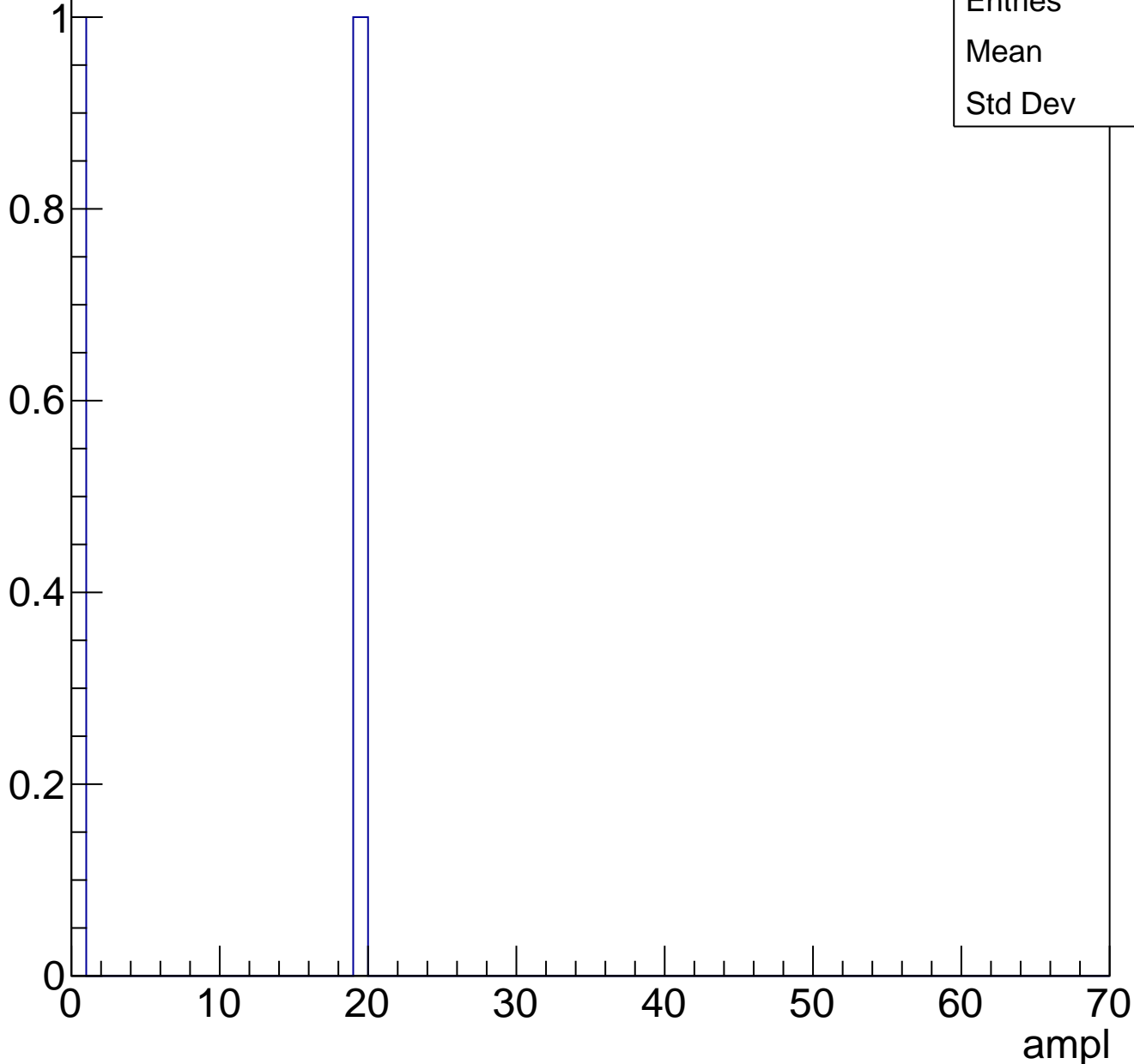
Entry



B1L003S, U19-ch89, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L003S, U19-ch90, adc0

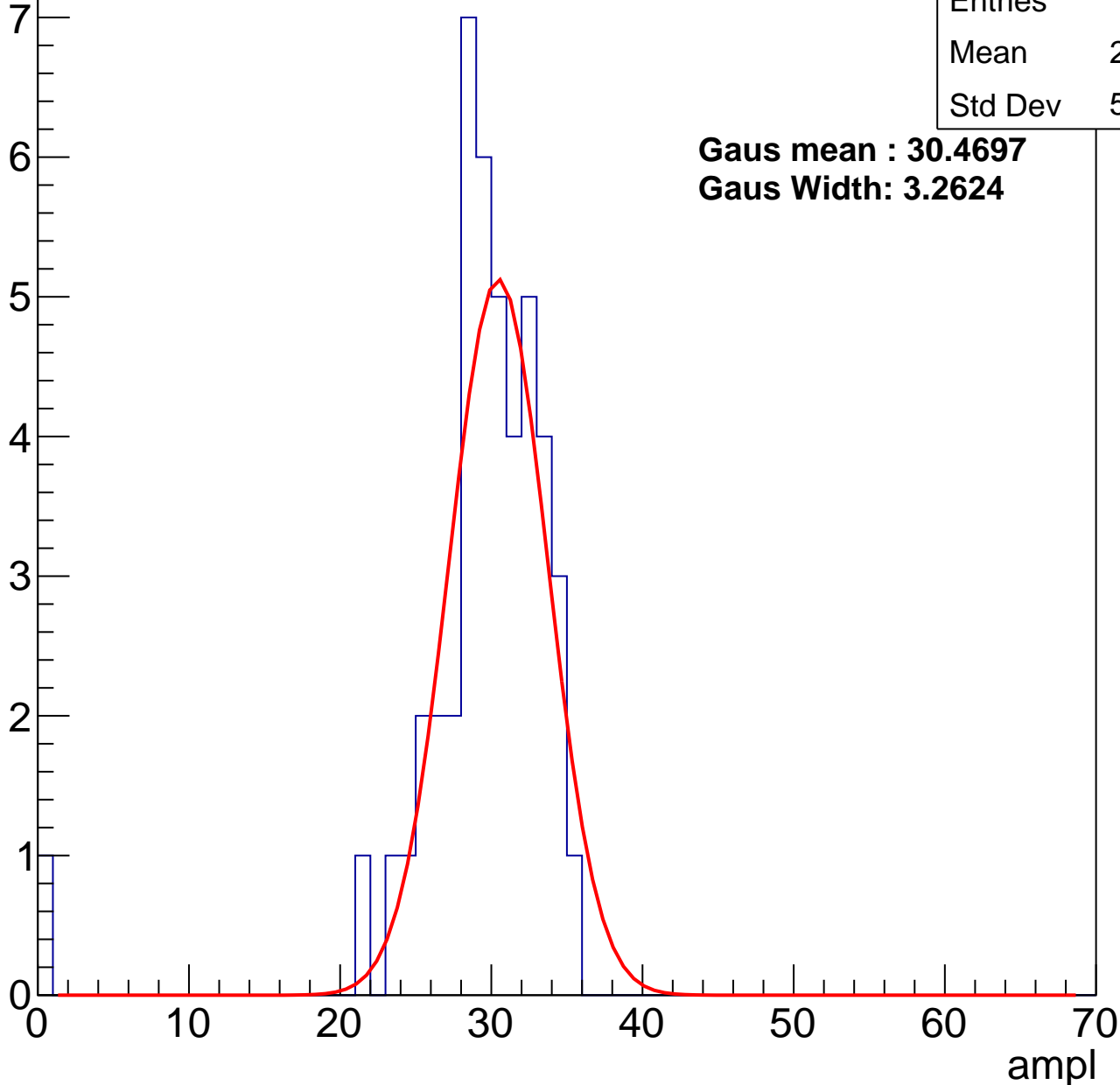
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	28.82
Std Dev	5.314

Gaus mean : 30.4697

Gaus Width: 3.2624



B1L003S, U19-ch90, adc1

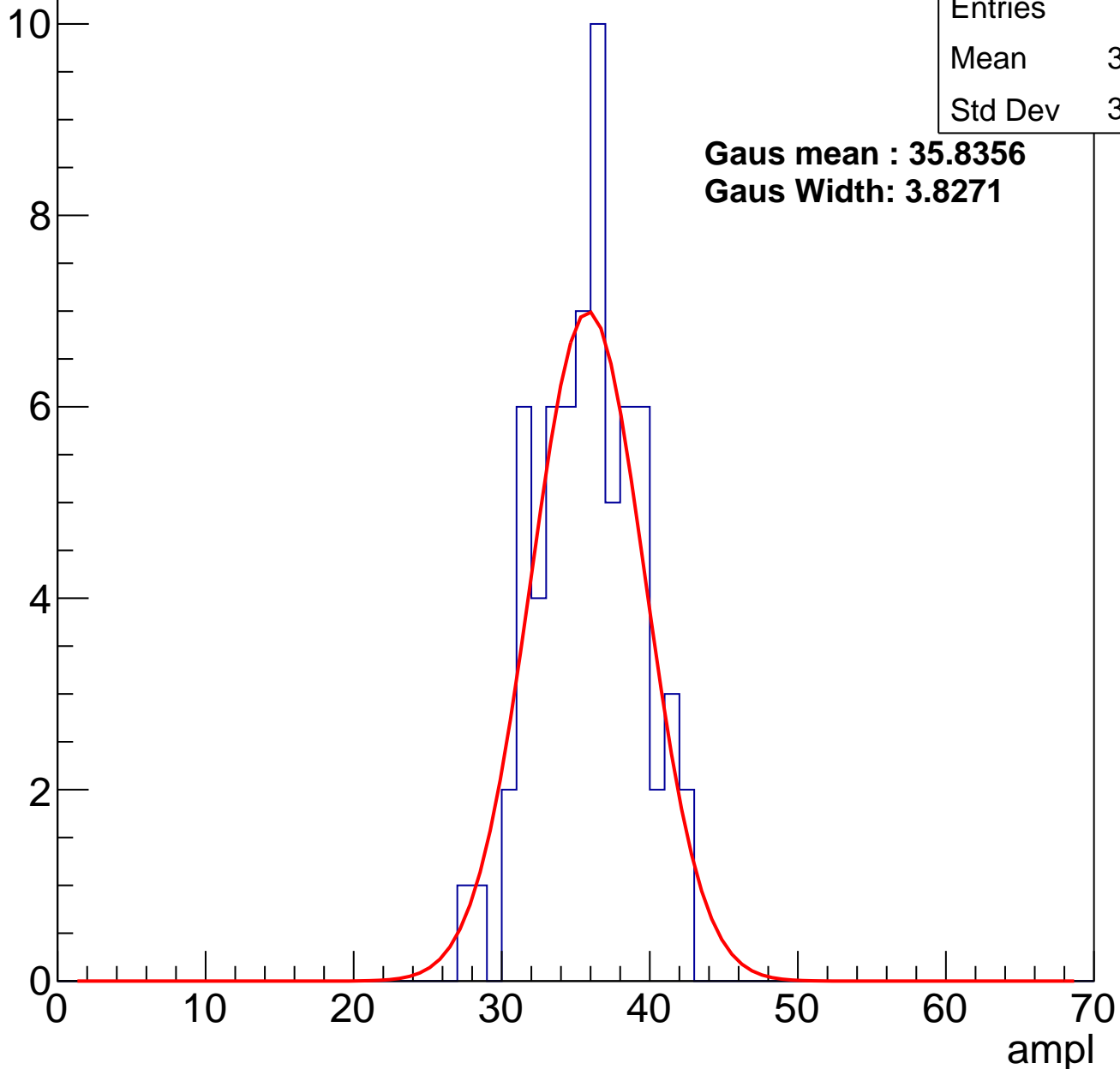
calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	35.37
Std Dev	3.349

Gaus mean : 35.8356

Gaus Width: 3.8271

Entry



B1L003S, U19-ch90, adc2

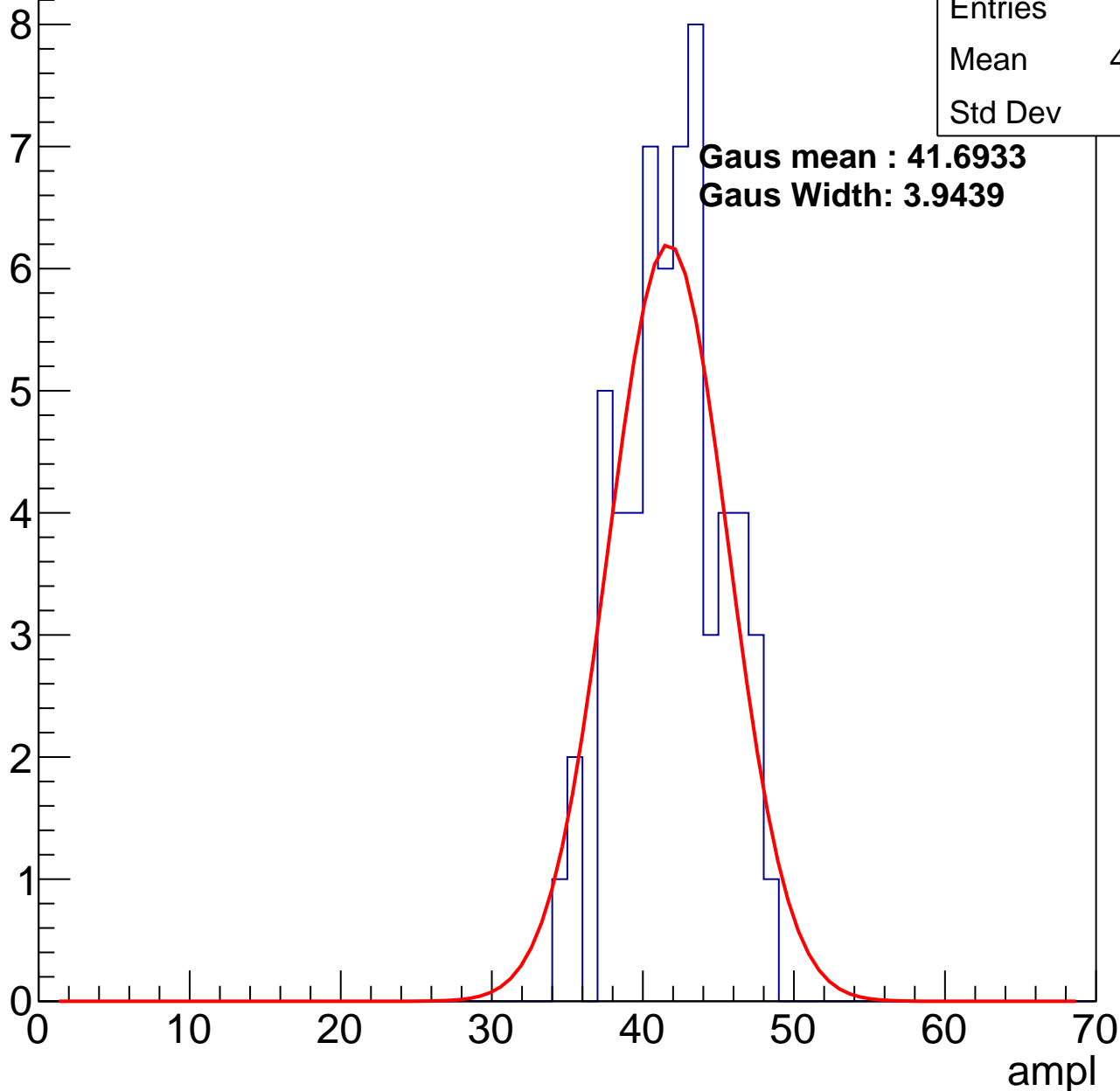
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.46
Std Dev	3.28

Gaus mean : 41.6933

Gaus Width: 3.9439

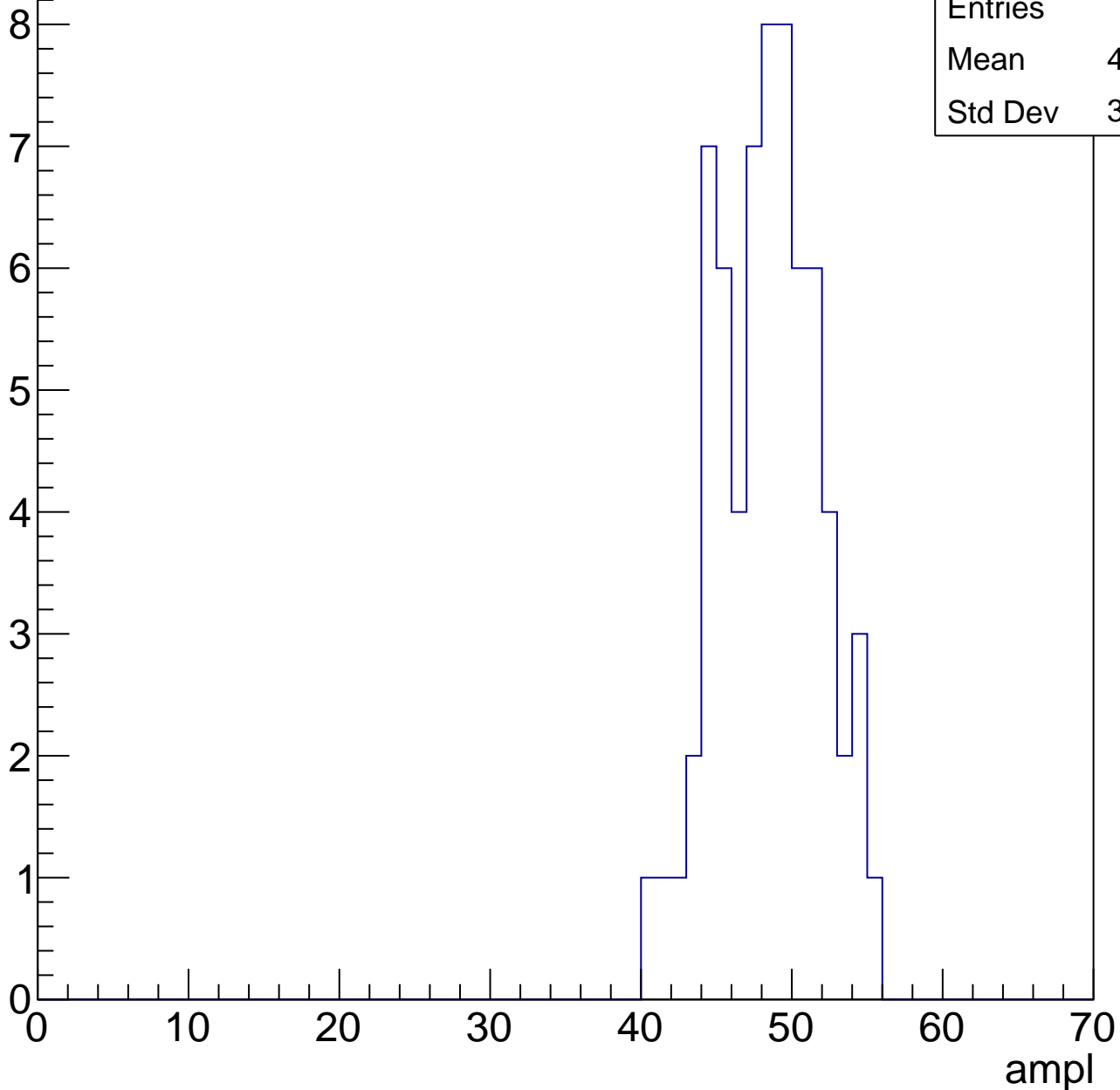


B1L003S, U19-ch90, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	47.96
Std Dev	3.334

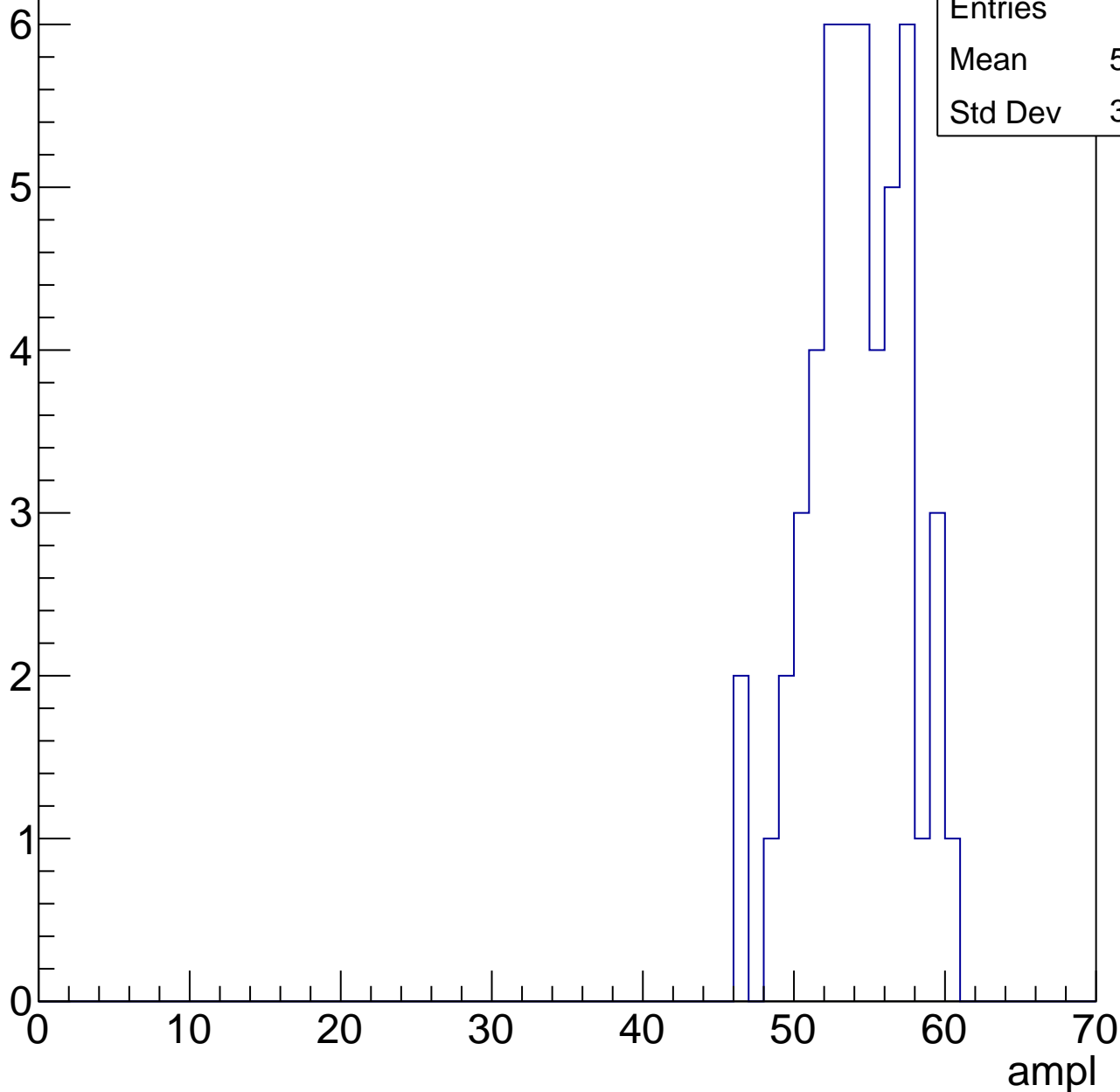


B1L003S, U19-ch90, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	53.66
Std Dev	3.247

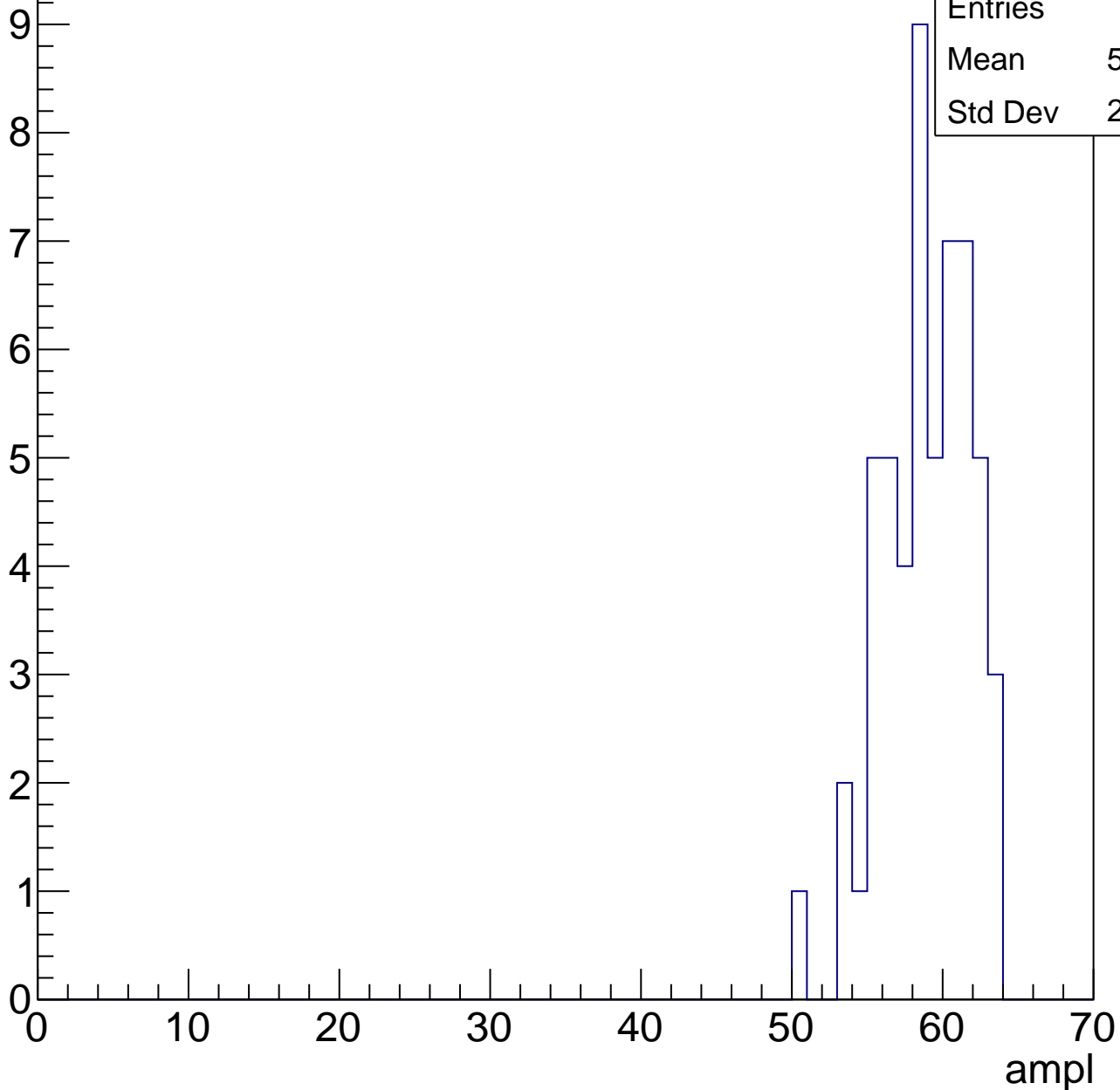


B1L003S, U19-ch90, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.44
Std Dev	2.852

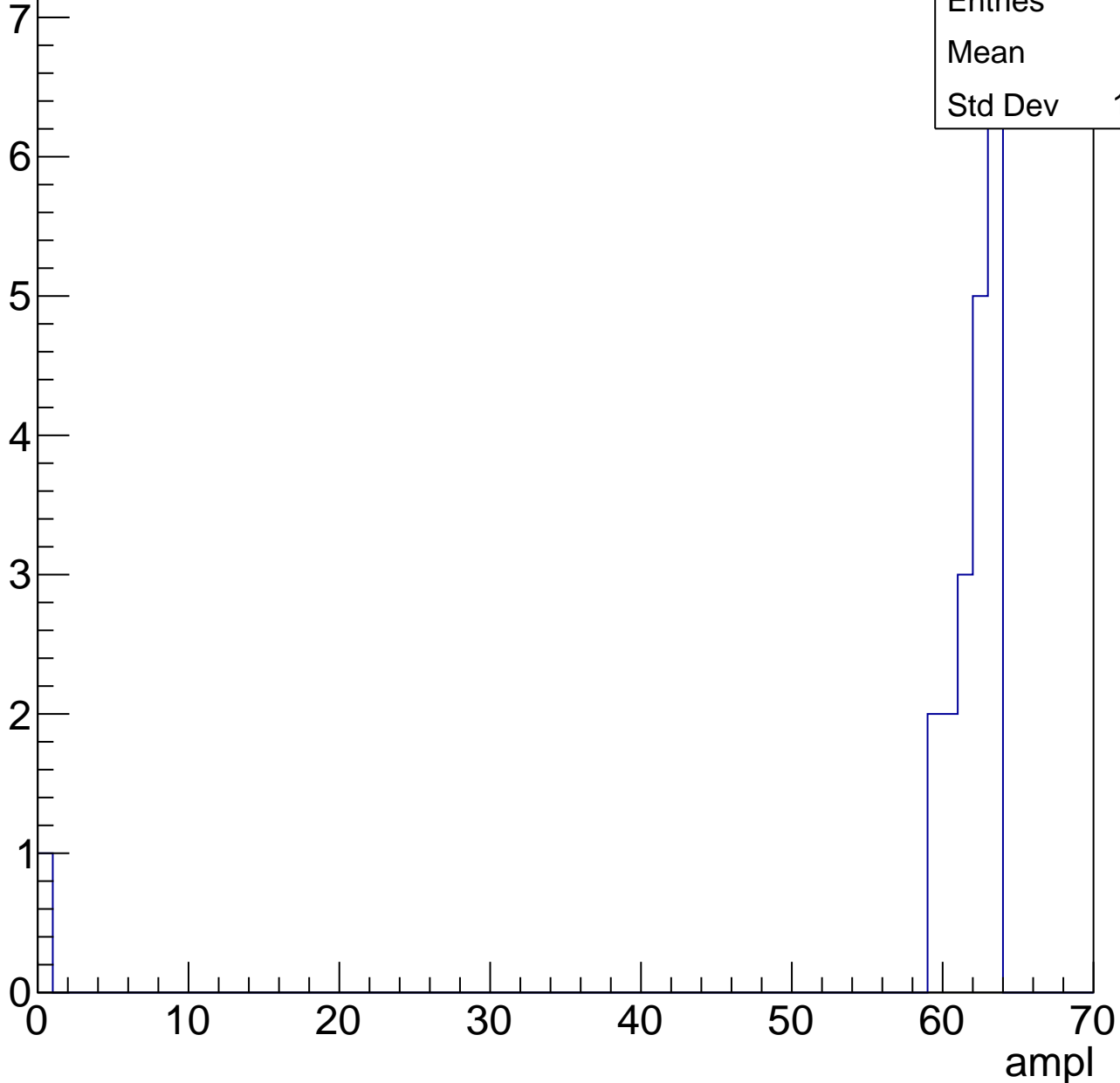


B1L003S, U19-ch90, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	20
Mean	58.6
Std Dev	13.51



B1L003S, U19-ch90, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch91, adc0

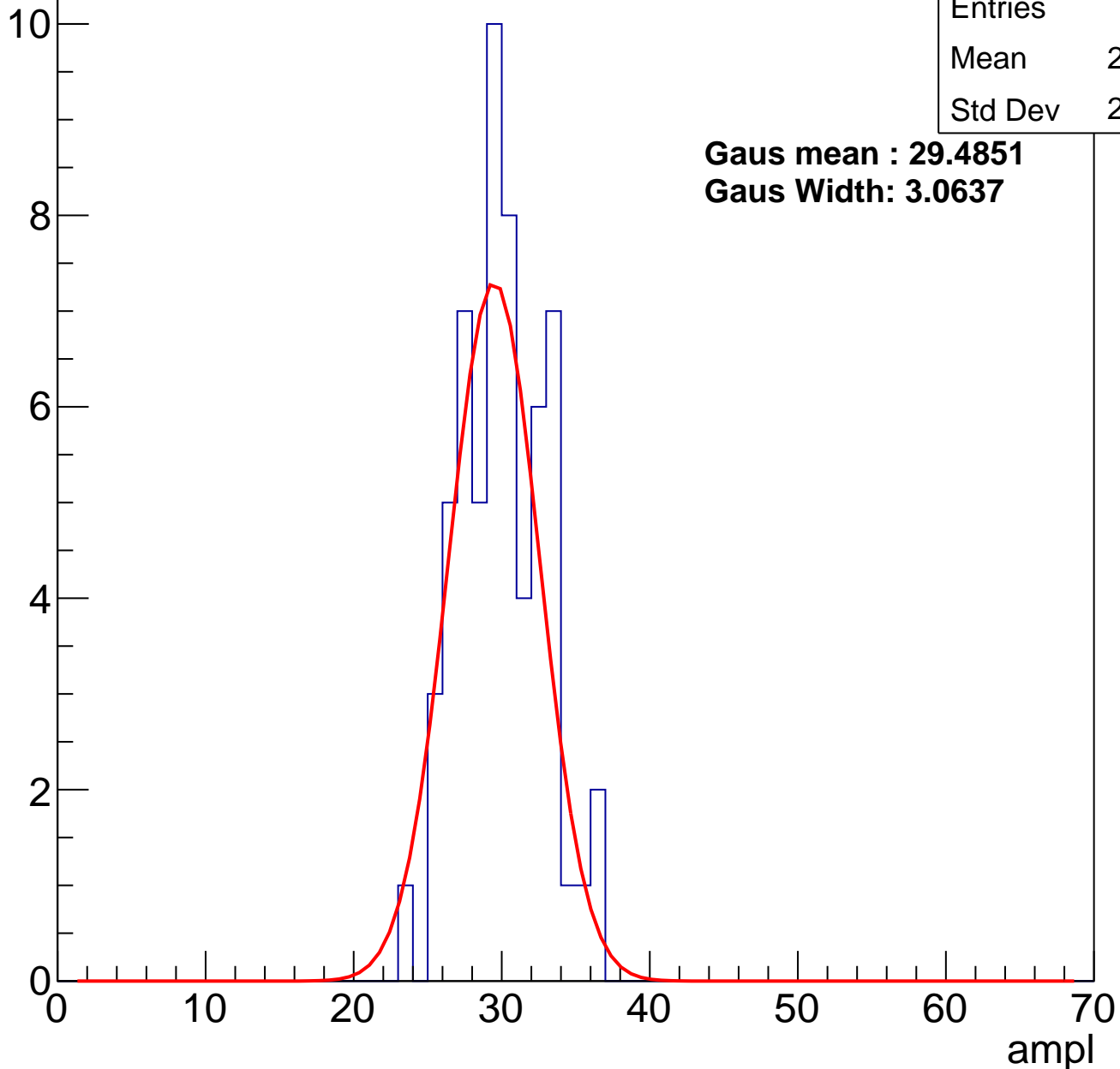
calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	29.58
Std Dev	2.859

Gaus mean : 29.4851

Gaus Width: 3.0637

Entry



B1L003S, U19-ch91, adc1

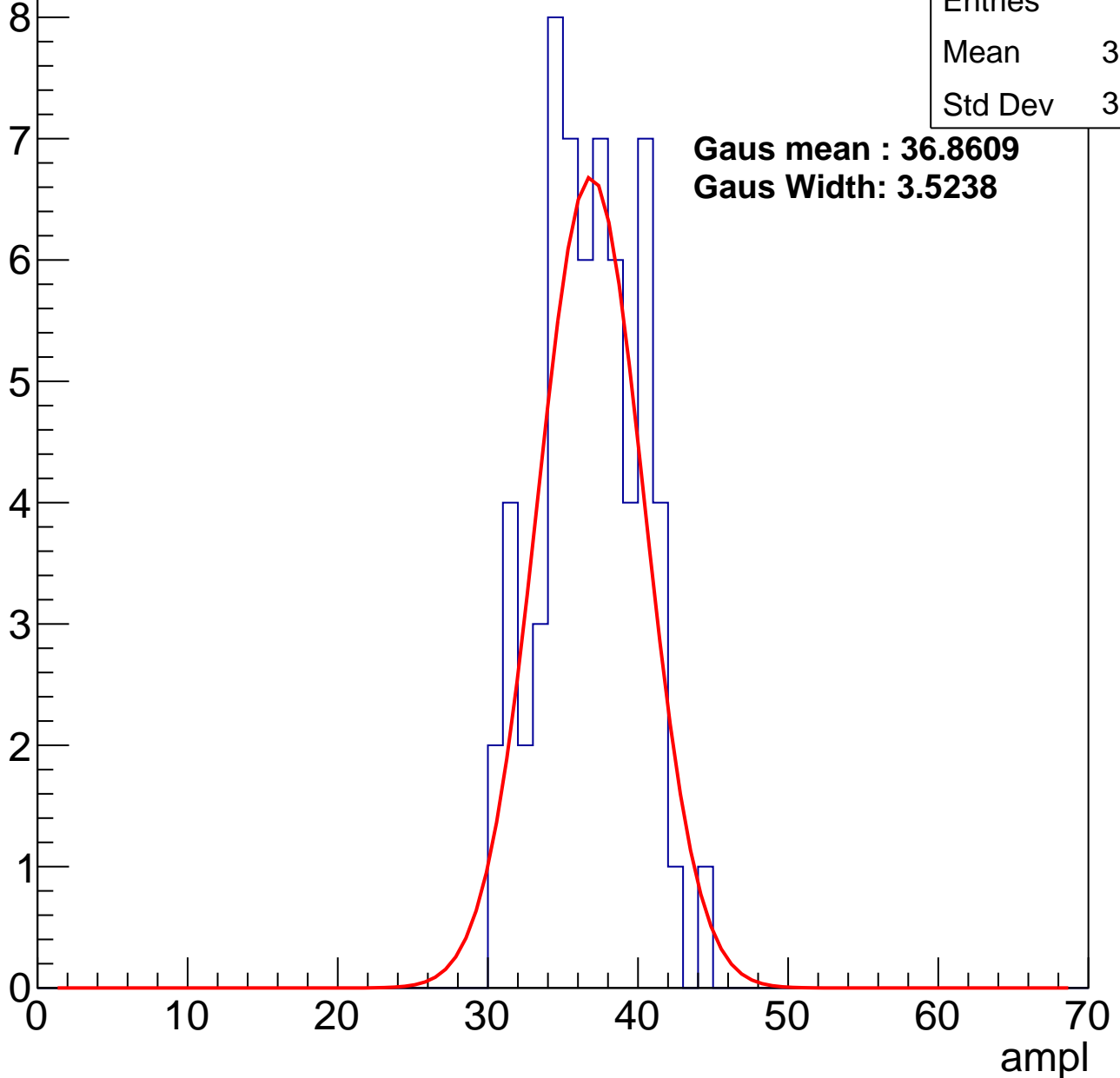
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.34
Std Dev	3.218

Gaus mean : 36.8609

Gaus Width: 3.5238



B1L003S, U19-ch91, adc2

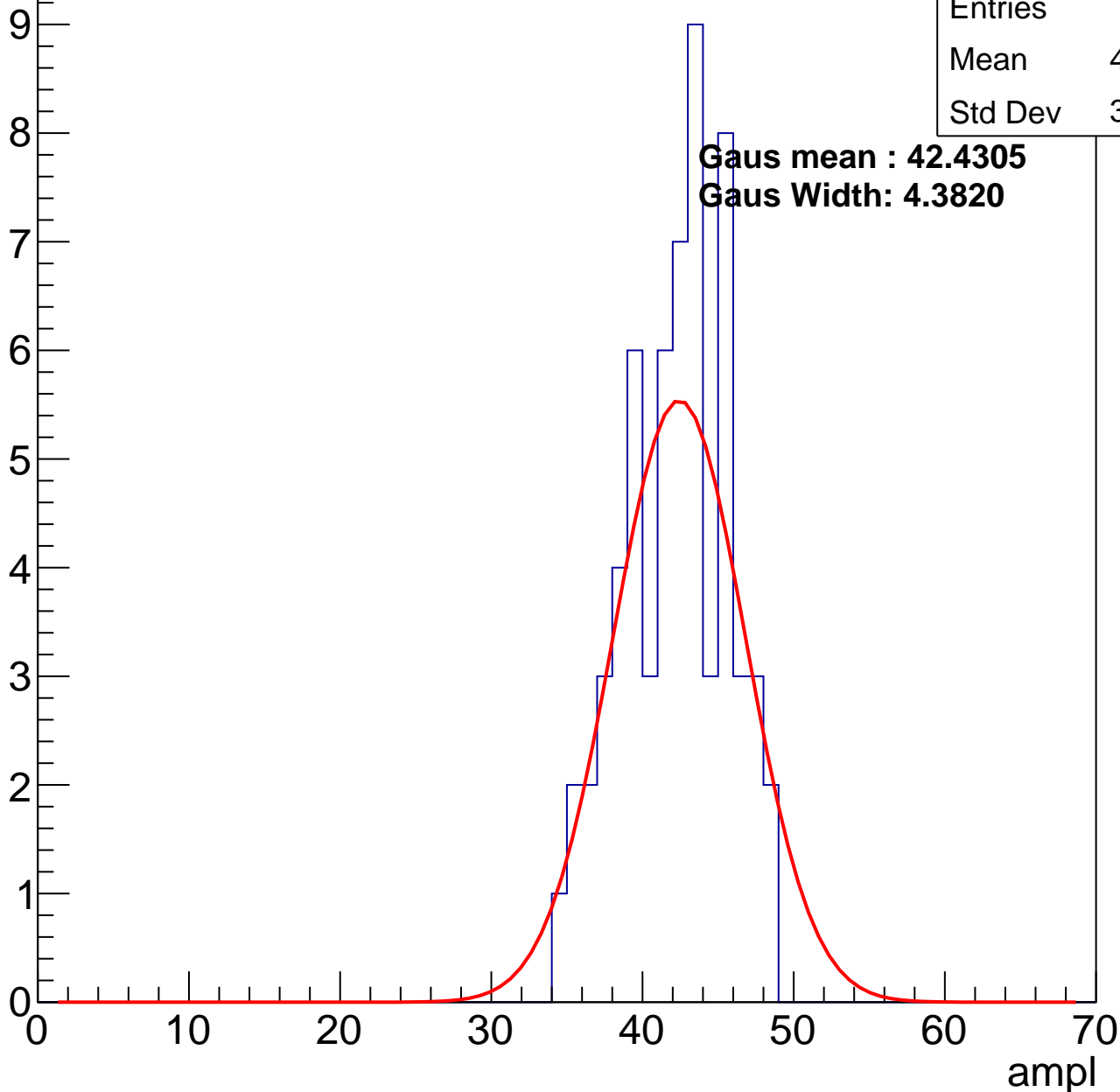
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	41.73
Std Dev	3.427

Gaus mean : 42.4305

Gaus Width: 4.3820

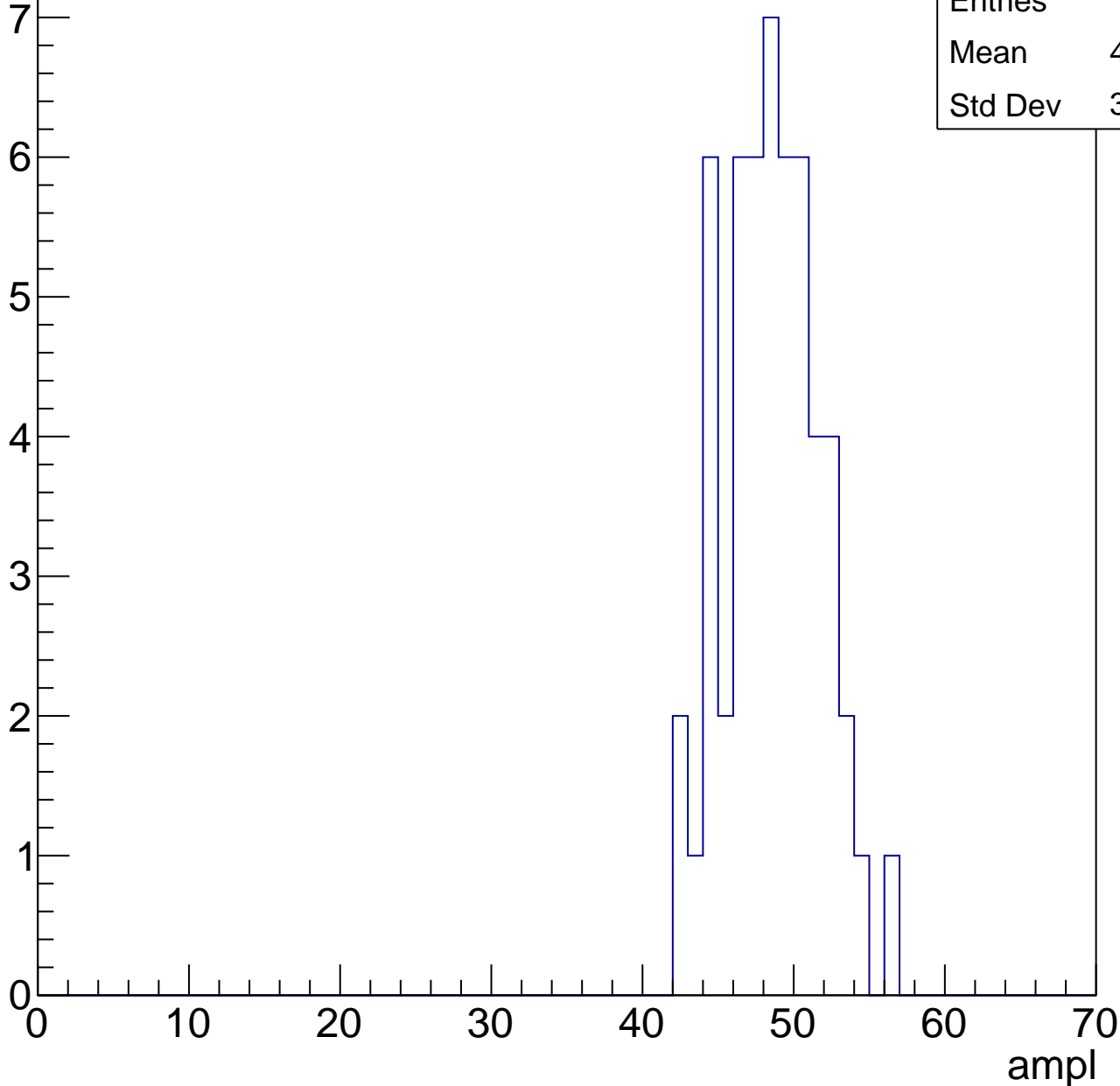


B1L003S, U19-ch91, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

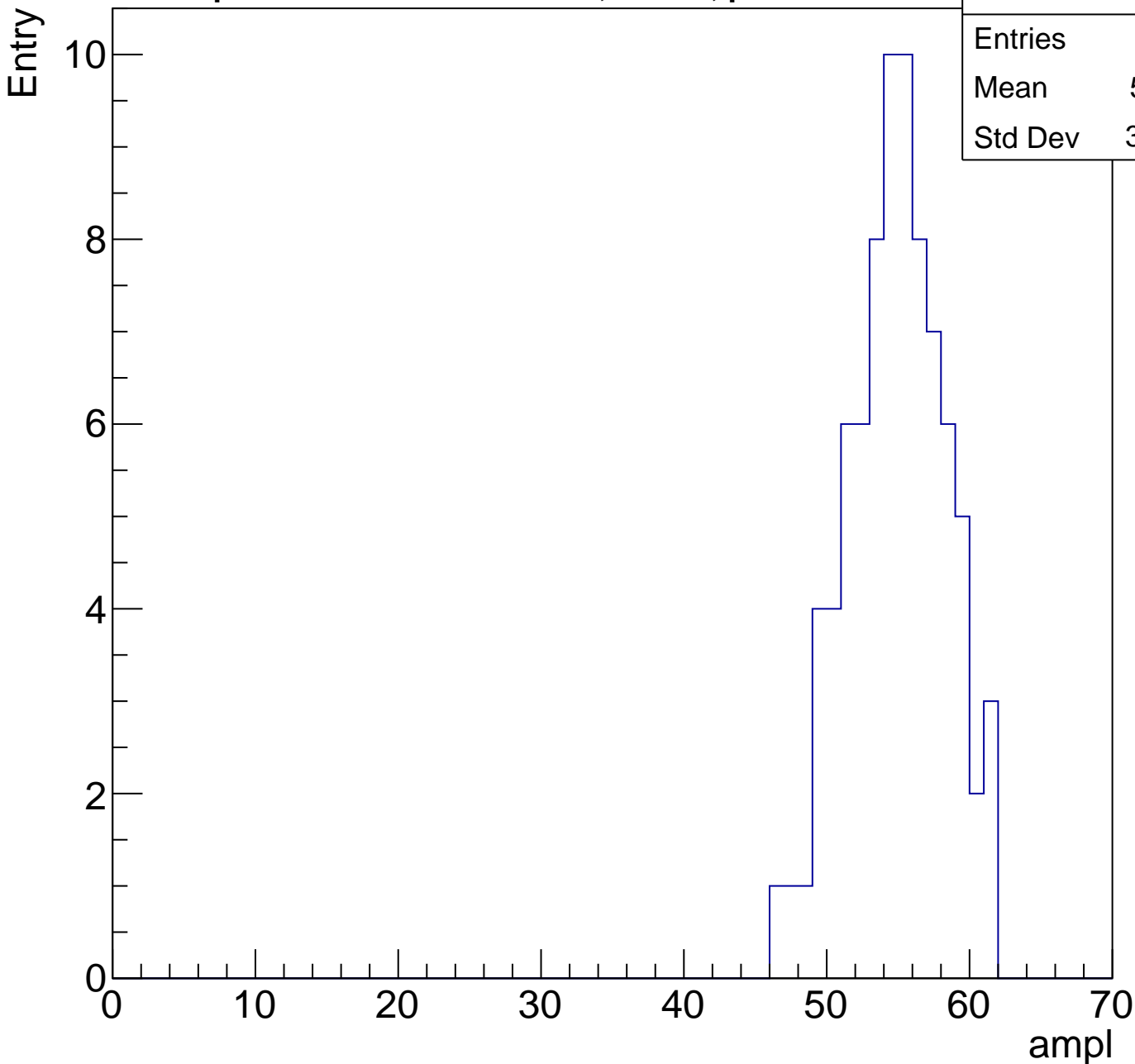
Entries	54
Mean	48.09
Std Dev	3.105



B1L003S, U19-ch91, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	82
Mean	54.41
Std Dev	3.368



B1L003S, U19-ch91, adc5

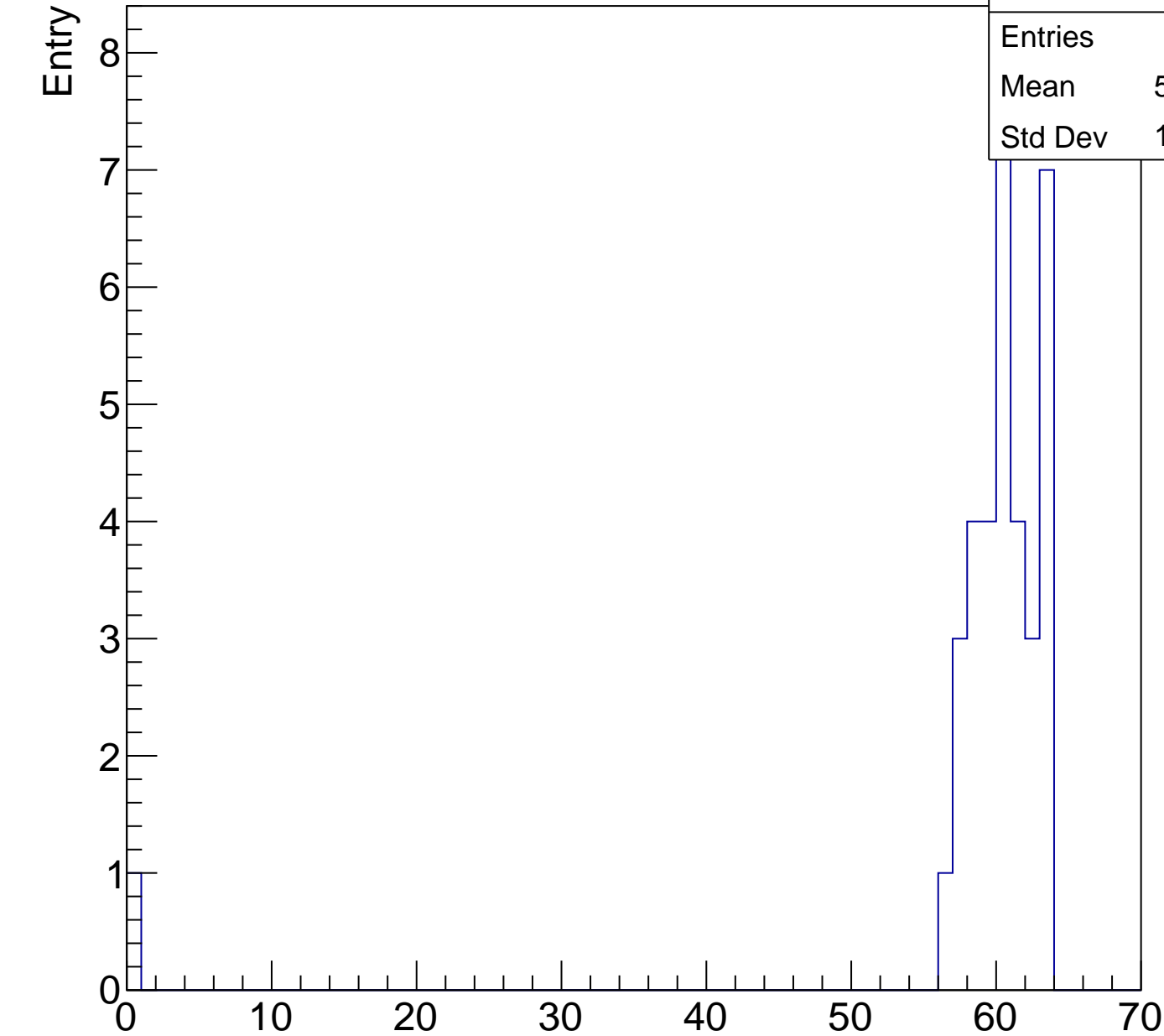
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	58.46
Std Dev	10.22

ampl

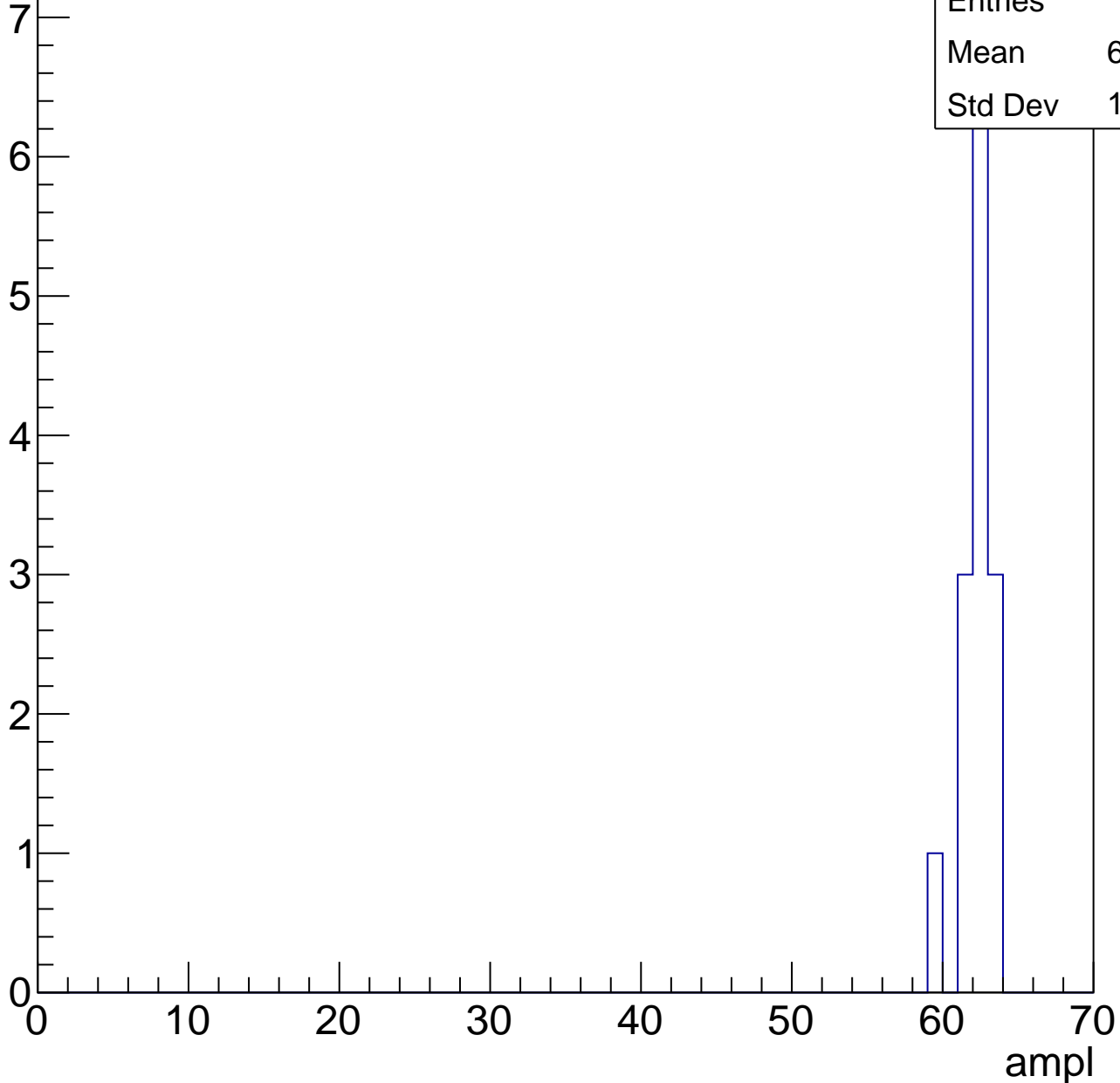


B1L003S, U19-ch91, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	61.79
Std Dev	1.013



B1L003S, U19-ch91, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch92, adc0

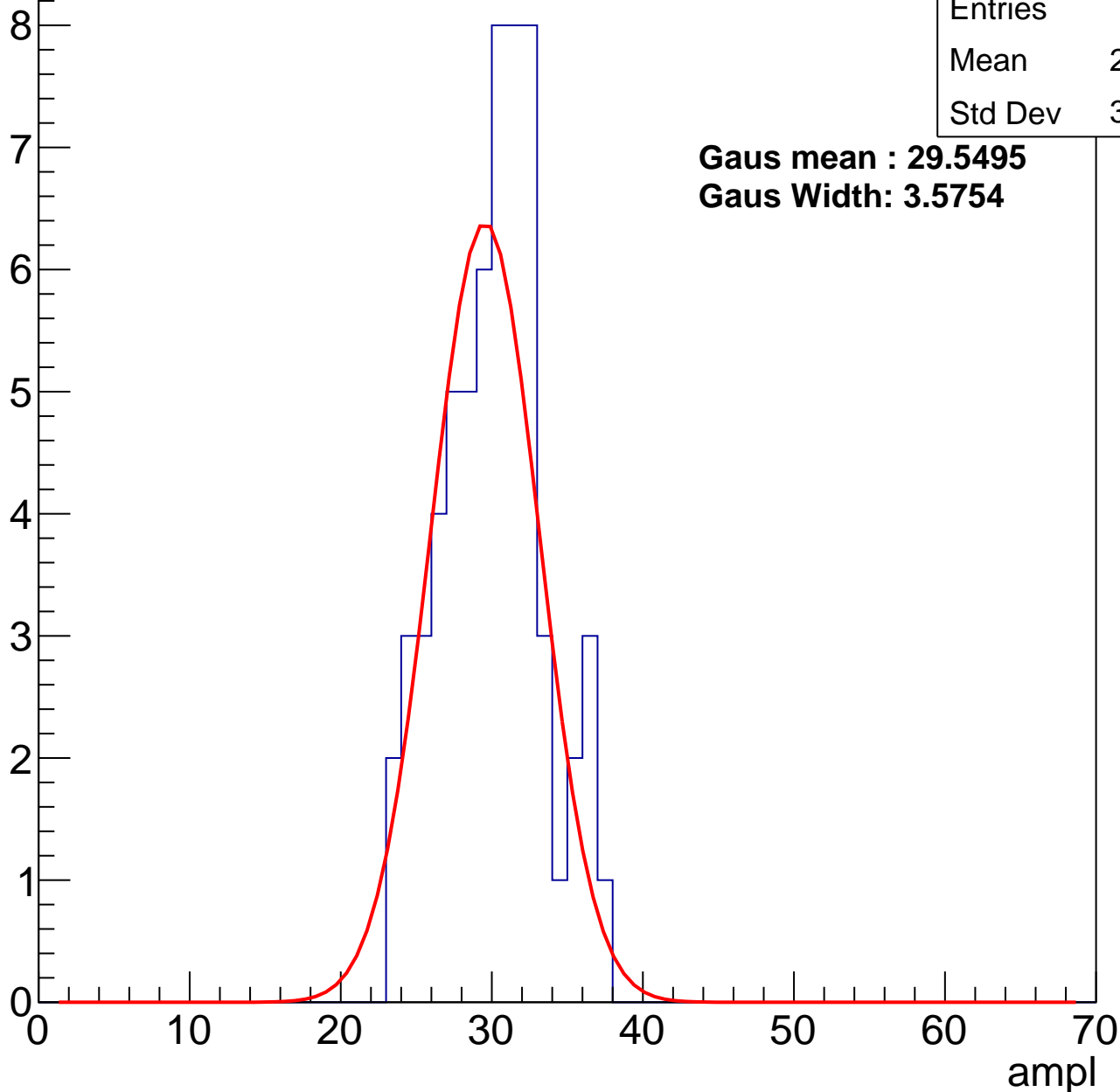
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.65
Std Dev	3.356

Gaus mean : 29.5495

Gaus Width: 3.5754



B1L003S, U19-ch92, adc1

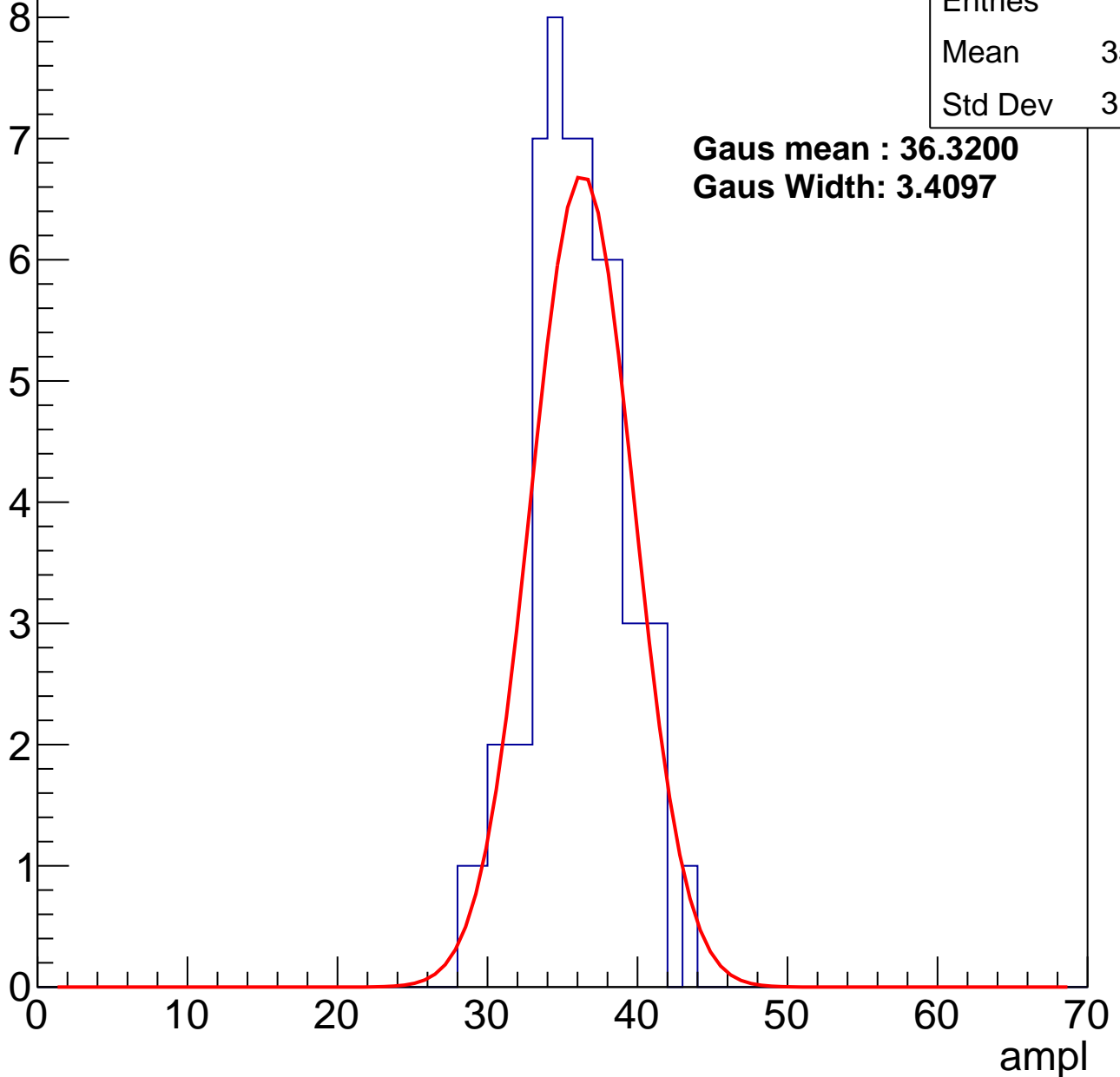
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.53
Std Dev	3.148

Gaus mean : 36.3200

Gaus Width: 3.4097



B1L003S, U19-ch92, adc2

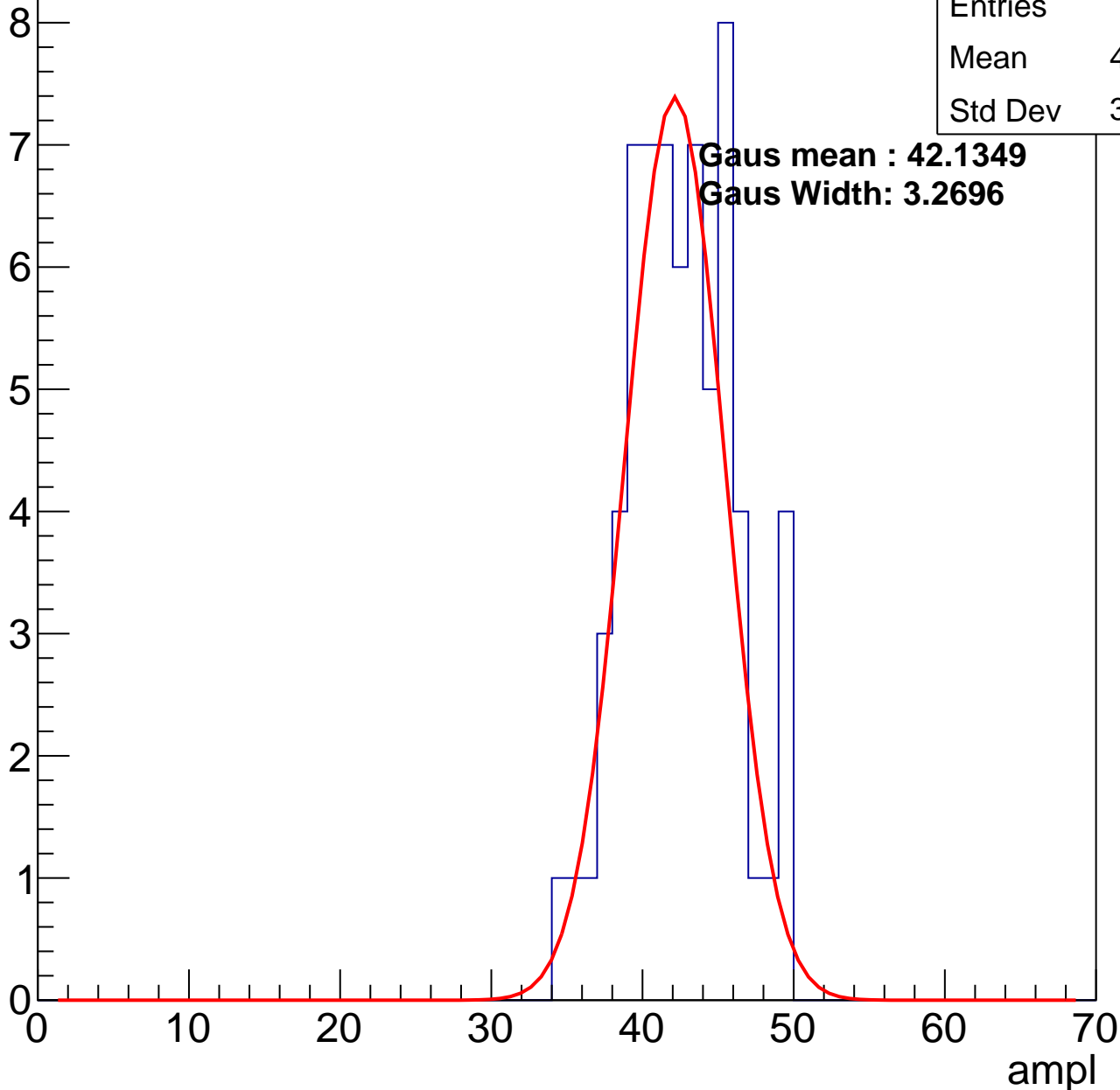
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.03
Std Dev	3.468

Gaus mean : 42.1349

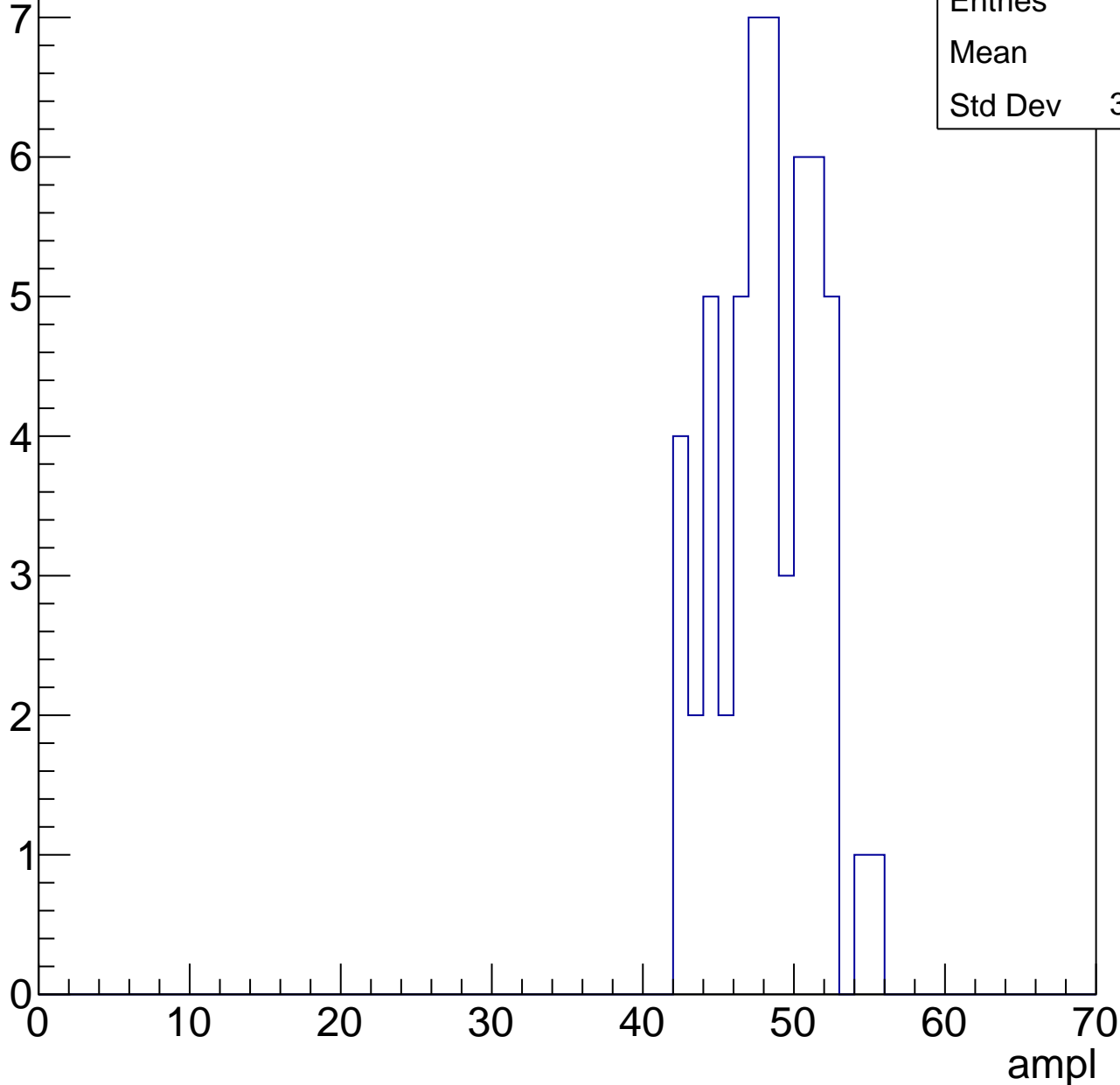
Gaus Width: 3.2696



B1L003S, U19-ch92, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

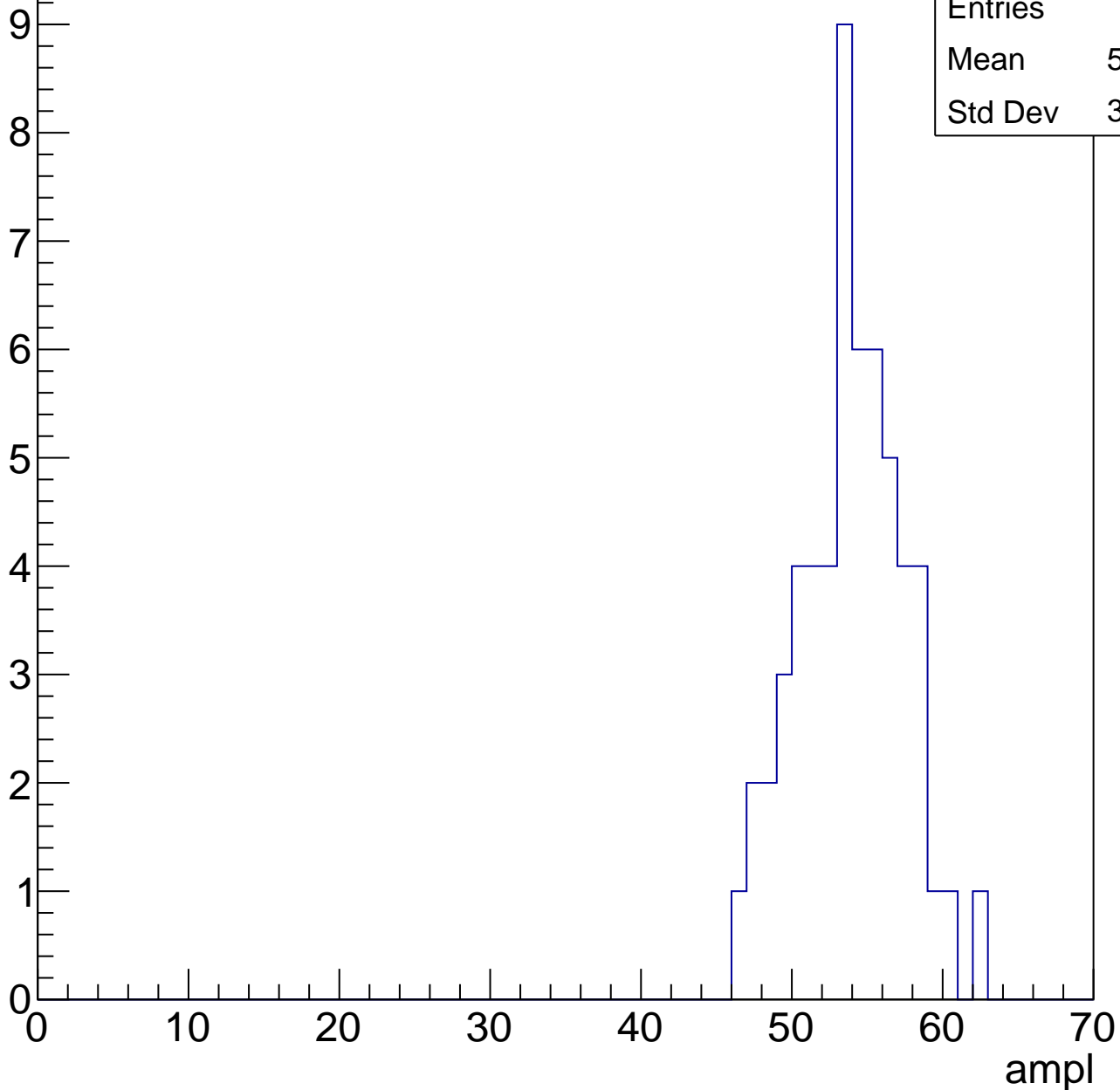


B1L003S, U19-ch92, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	53.46
Std Dev	3.424



B1L003S, U19-ch92, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	58.24
Std Dev	7.686

Entry

10

8

6

4

2

0

0

10

20

30

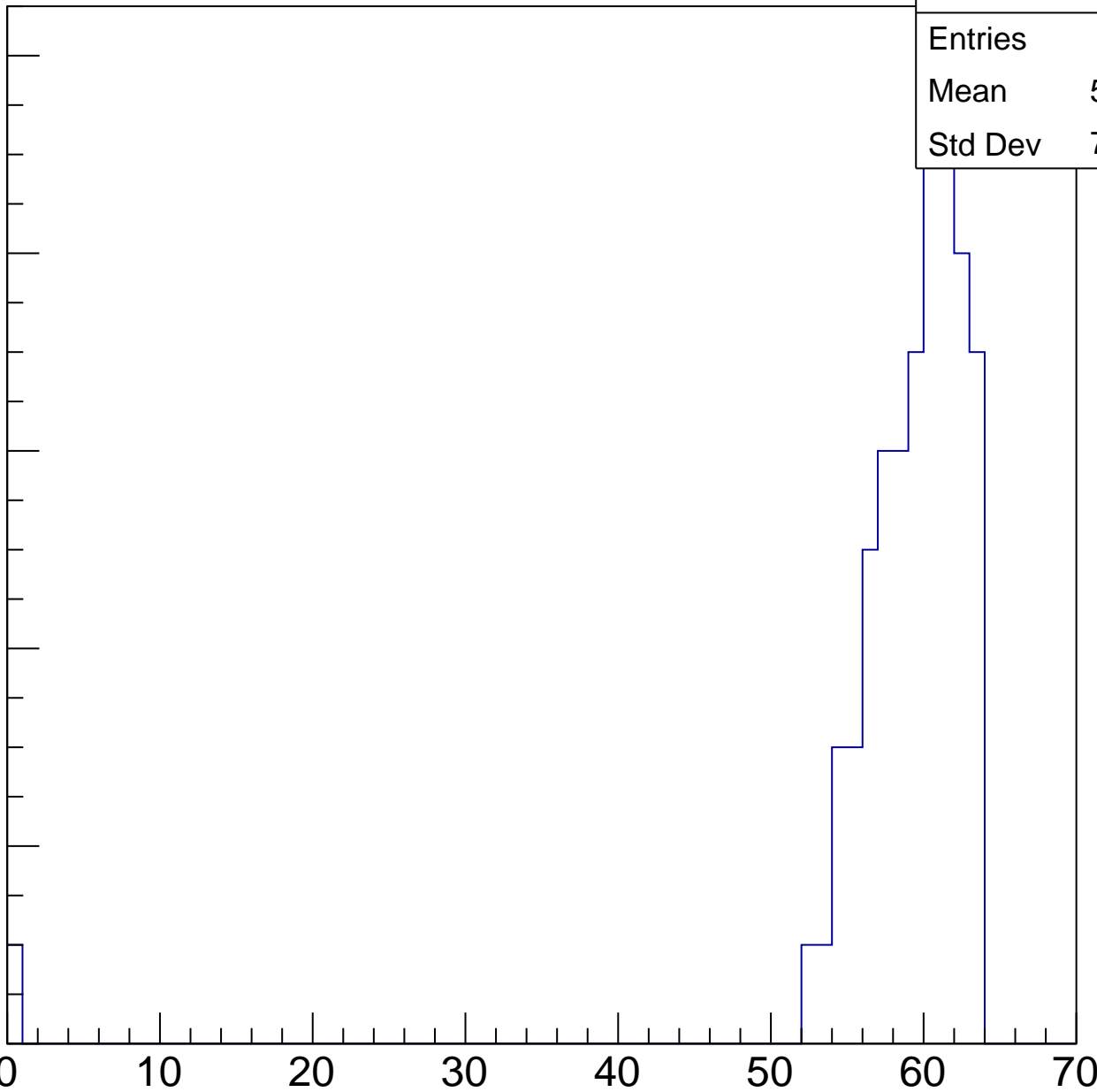
40

50

60

70

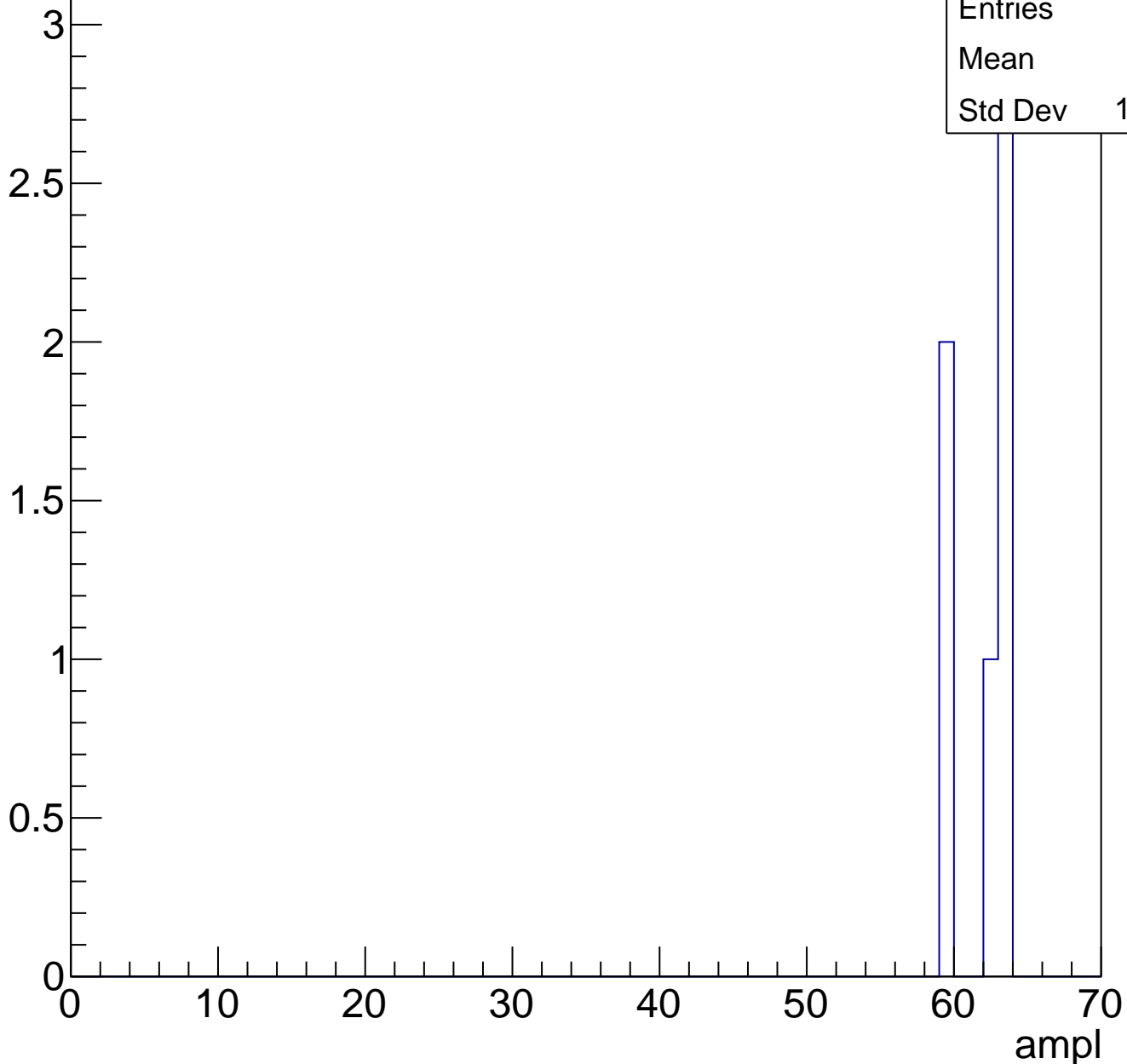
ampl



B1L003S, U19-ch92, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch92, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch93, adc0

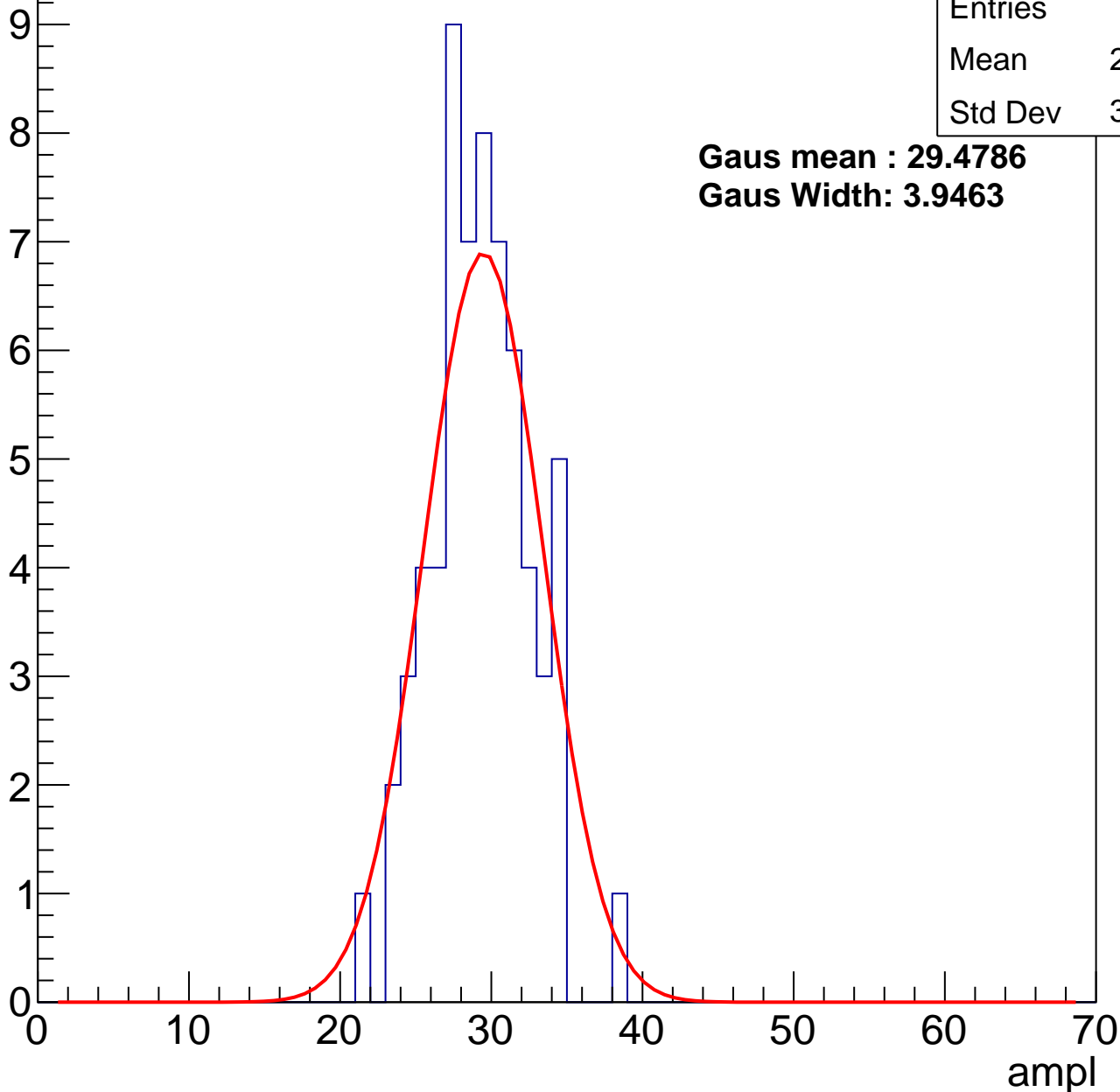
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.83
Std Dev	3.243

Gaus mean : 29.4786

Gaus Width: 3.9463



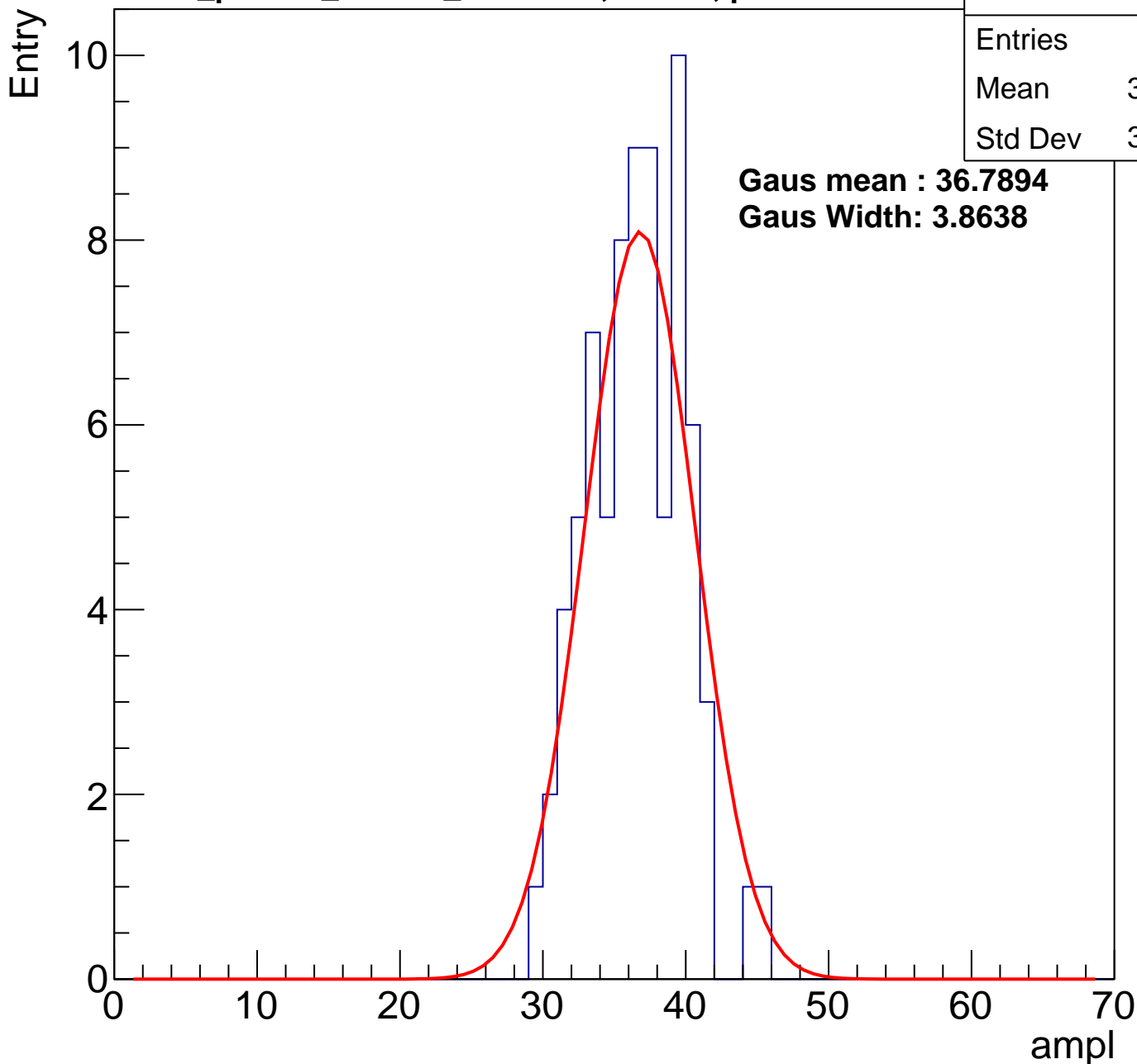
B1L003S, U19-ch93, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	36.09
Std Dev	3.297

Gaus mean : 36.7894

Gaus Width: 3.8638



B1L003S, U19-ch93, adc2

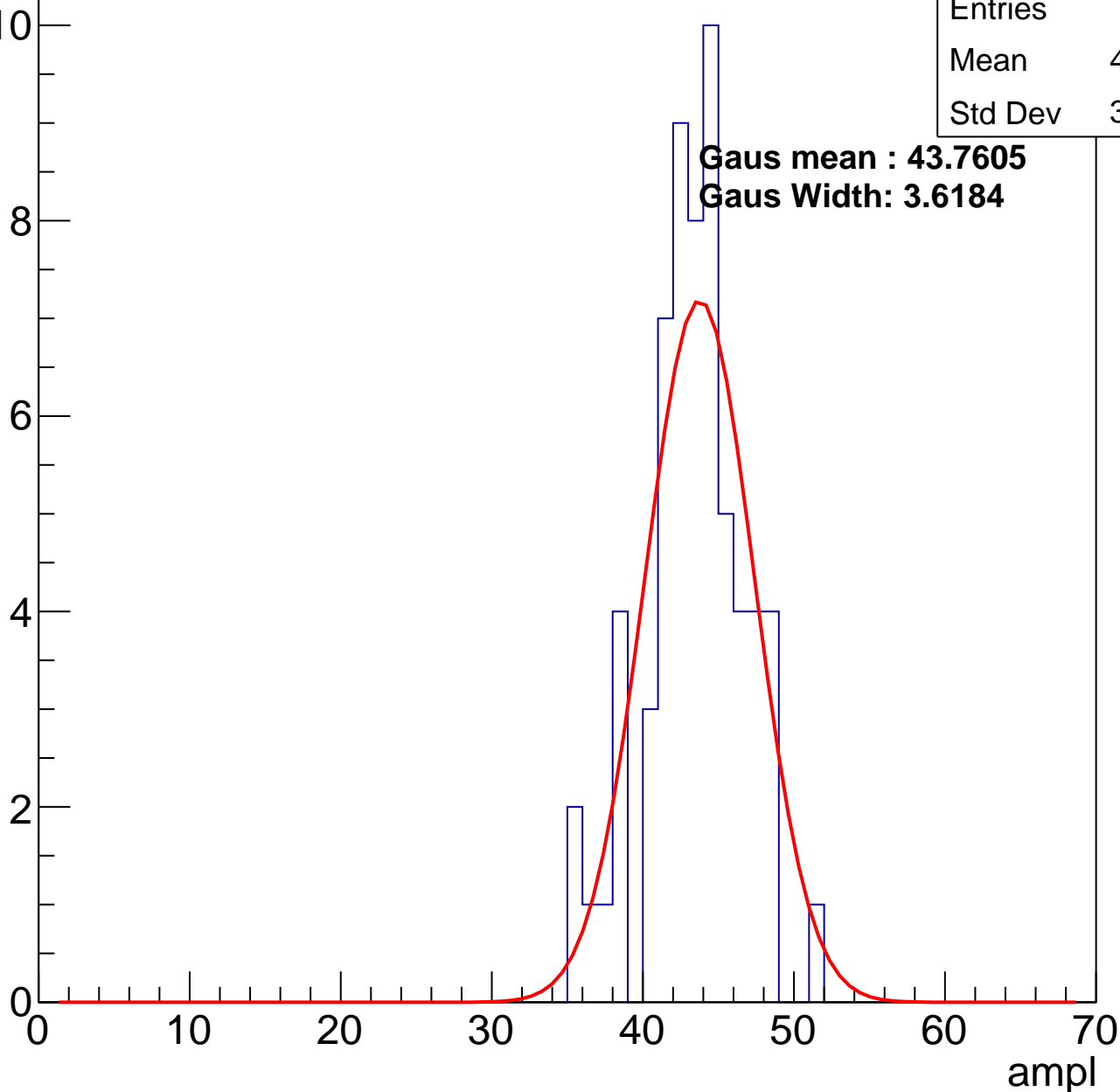
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.92
Std Dev	3.263

Gaus mean : 43.7605

Gaus Width: 3.6184



B1L003S, U19-ch93, adc3

calib_packv5_042523_0143.root, FC#13, port D2

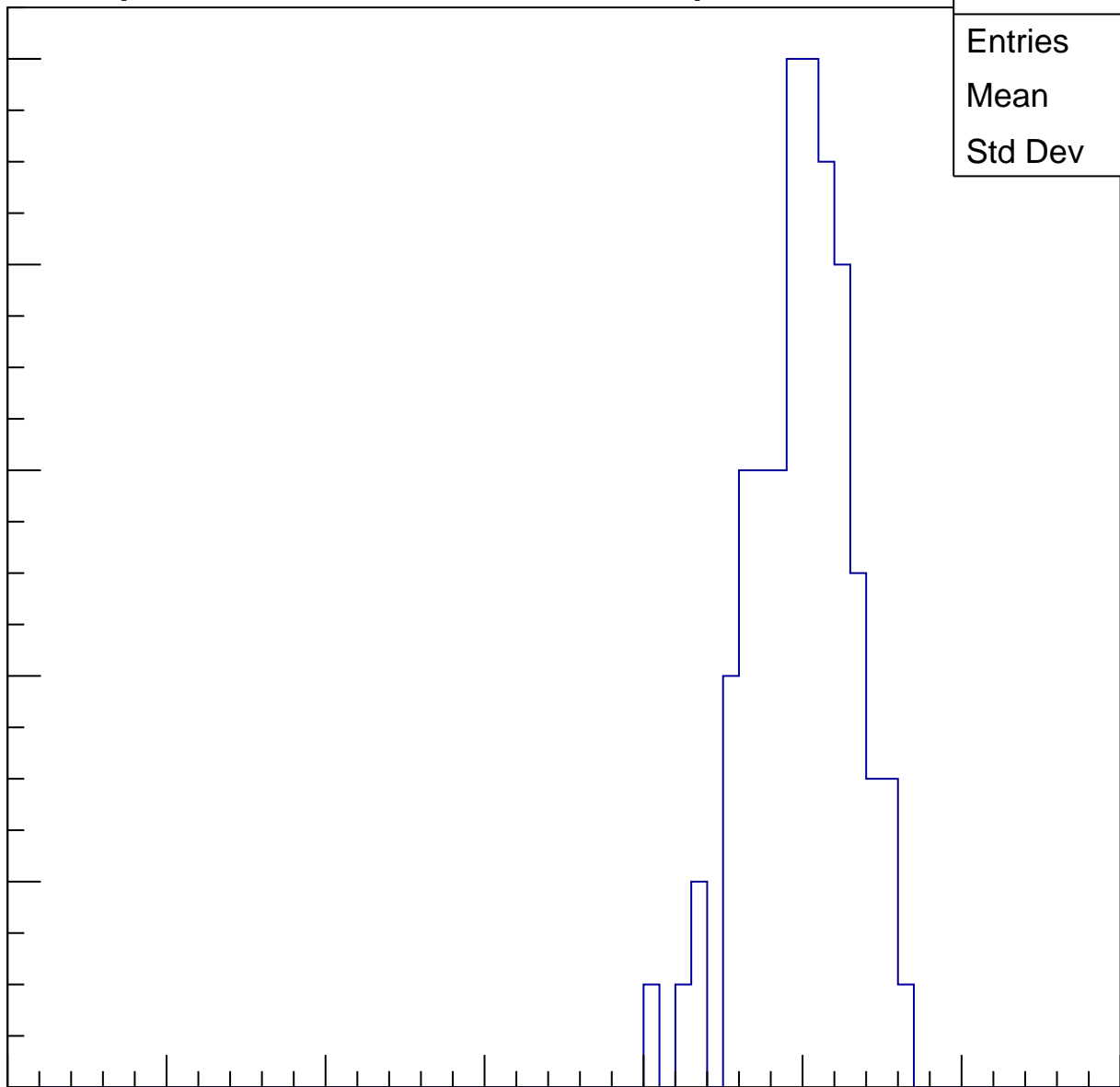
Entries	75
Mean	49.43
Std Dev	3.197

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

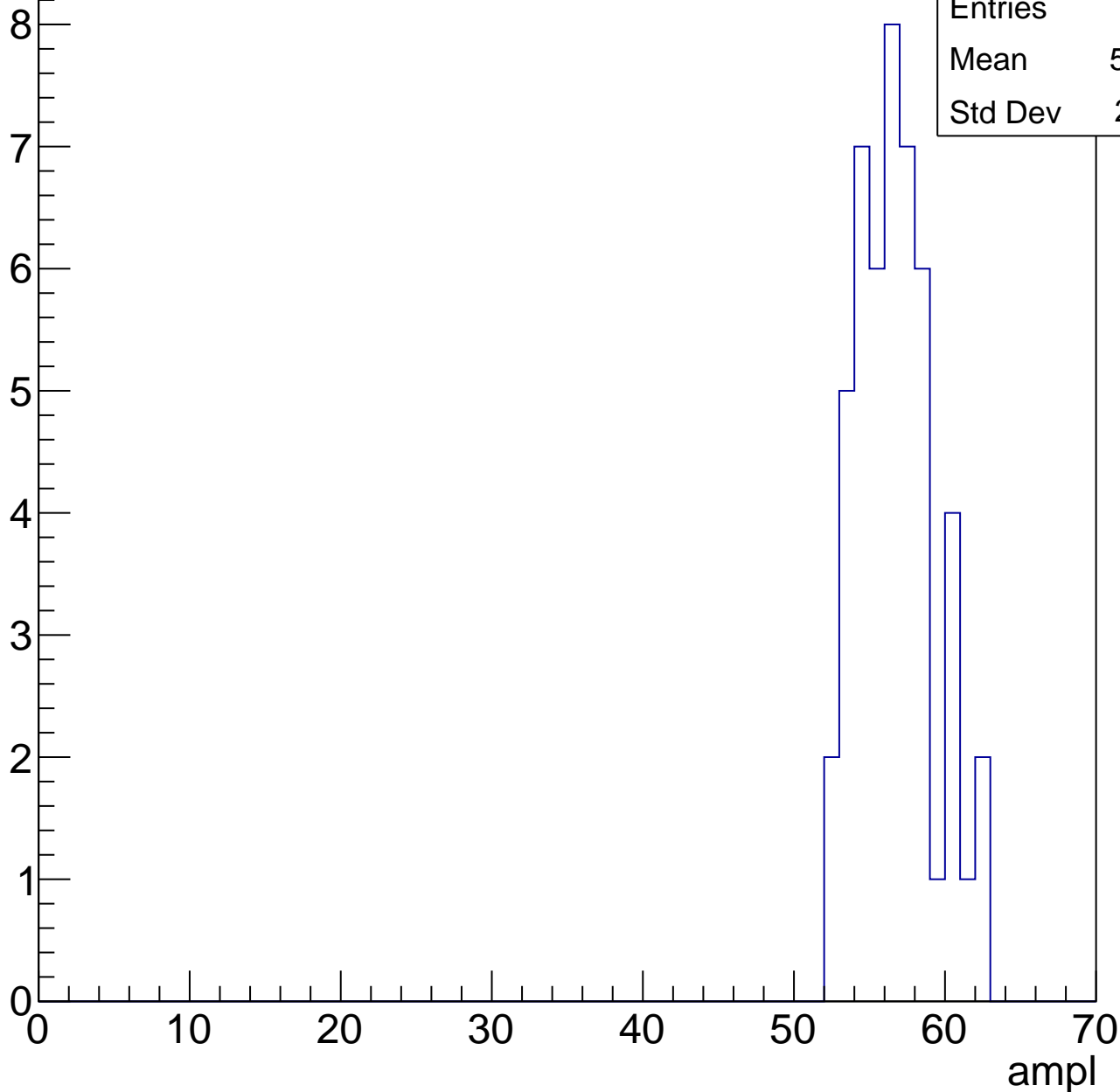


B1L003S, U19-ch93, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	56.24
Std Dev	2.511



B1L003S, U19-ch93, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries

40

Mean

60.1

Std Dev

2.119

0

ampl

0

10

20

30

40

50

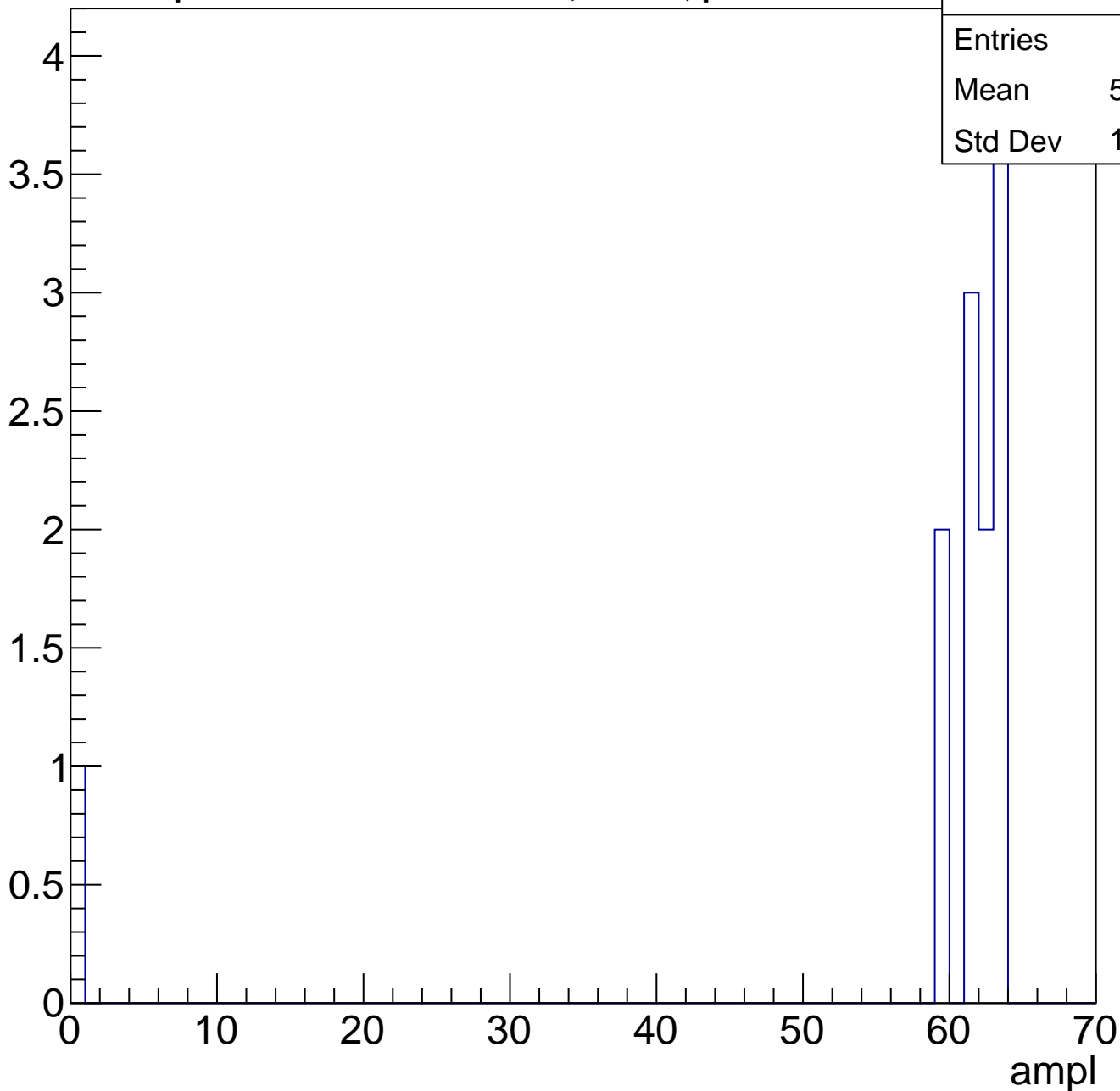
60

70

B1L003S, U19-ch93, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch93, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch94, adc0

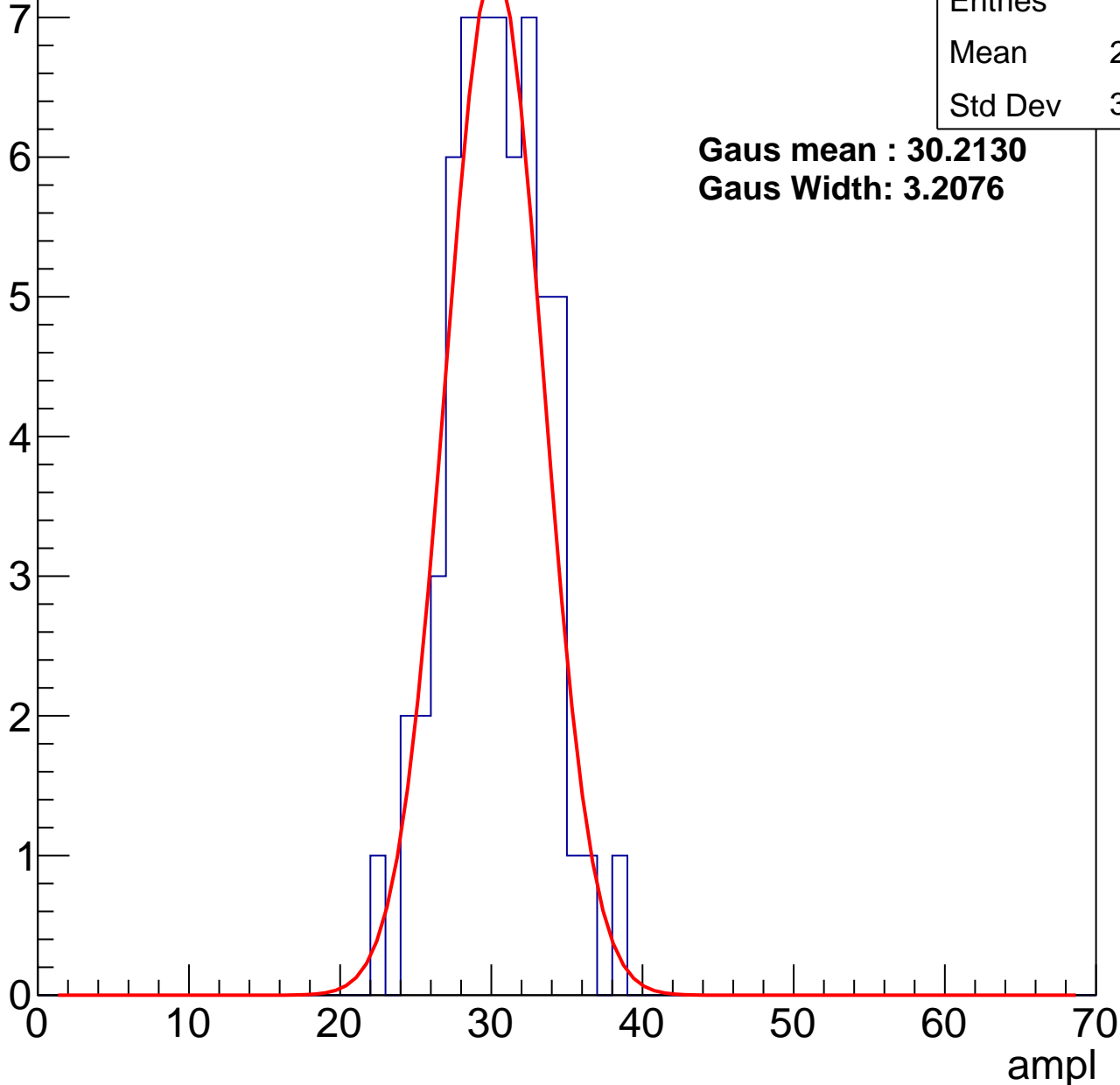
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.89
Std Dev	3.152

Gaus mean : 30.2130

Gaus Width: 3.2076



B1L003S, U19-ch94, adc1

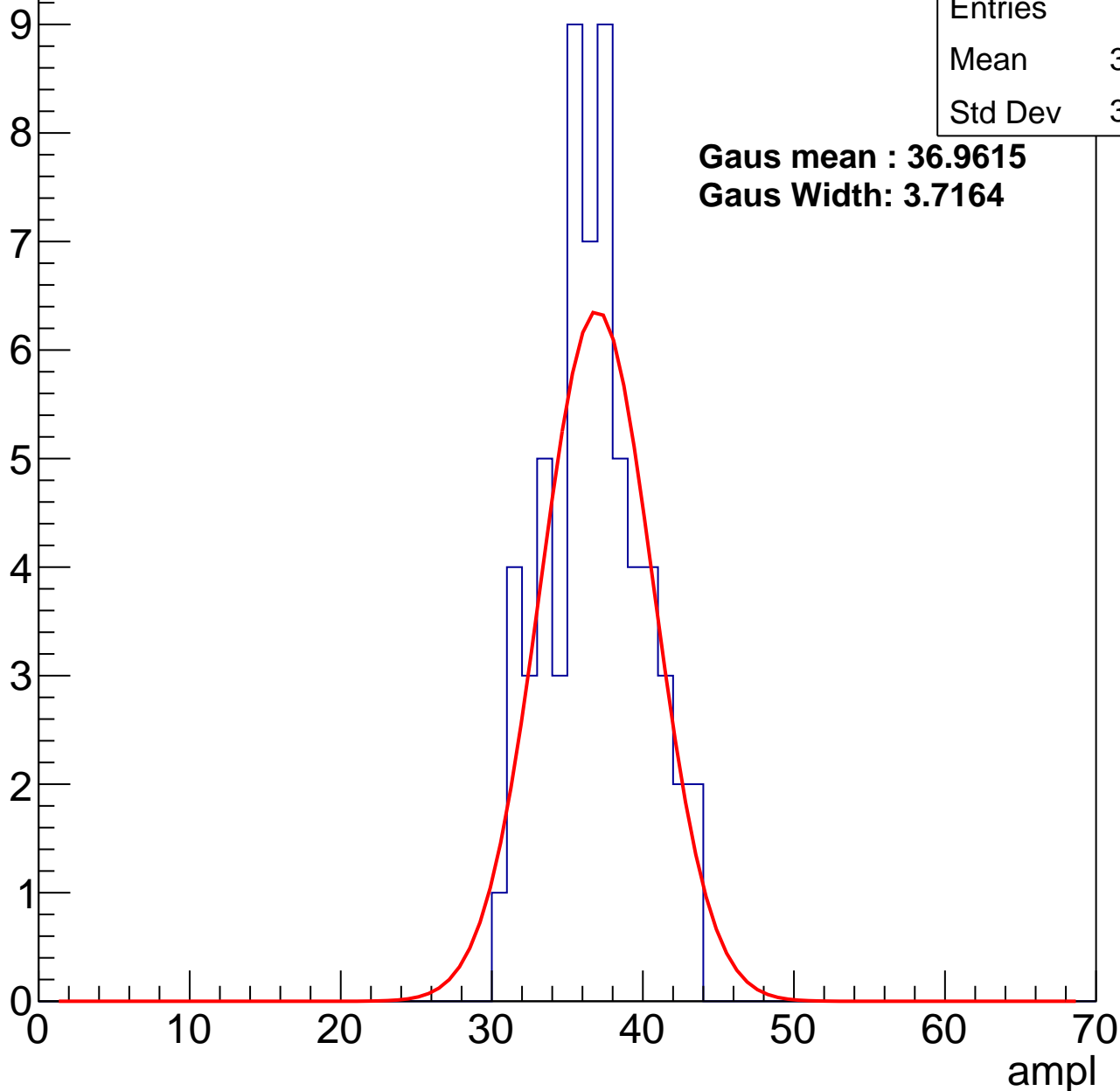
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.33
Std Dev	3.182

Gaus mean : 36.9615

Gaus Width: 3.7164



B1L003S, U19-ch94, adc2

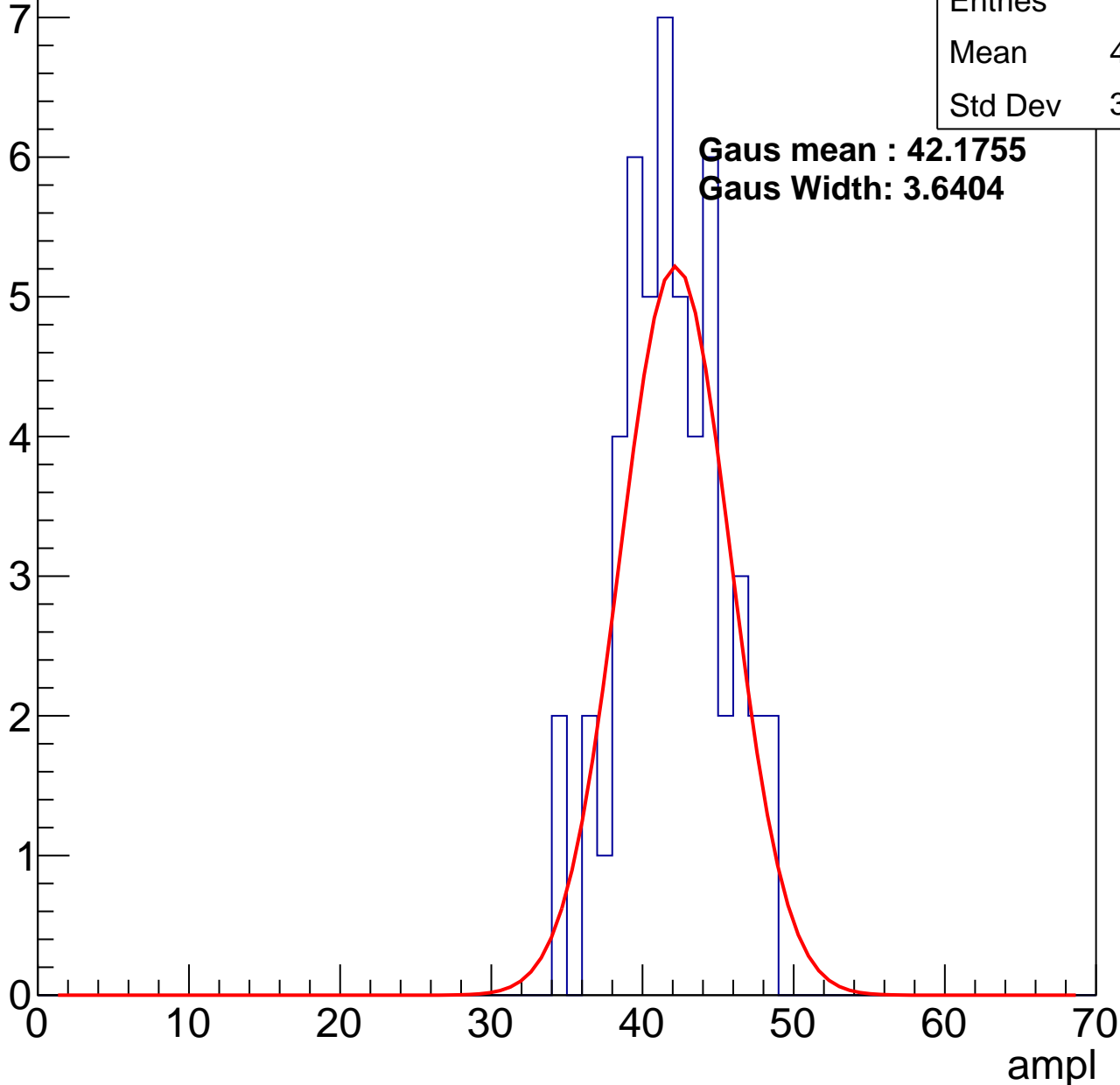
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	41.45
Std Dev	3.345

Gaus mean : 42.1755

Gaus Width: 3.6404

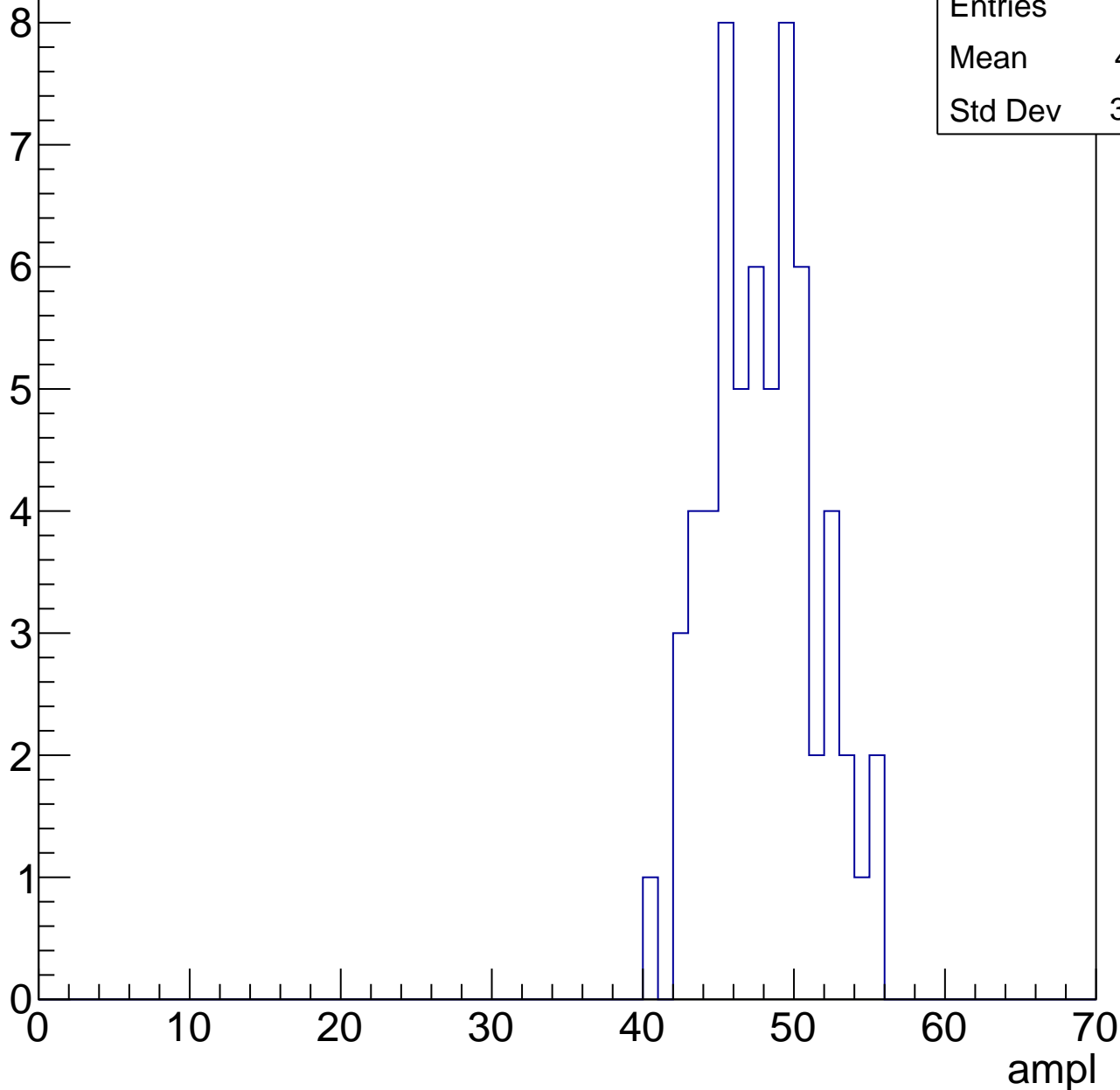


B1L003S, U19-ch94, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	47.51
Std Dev	3.429

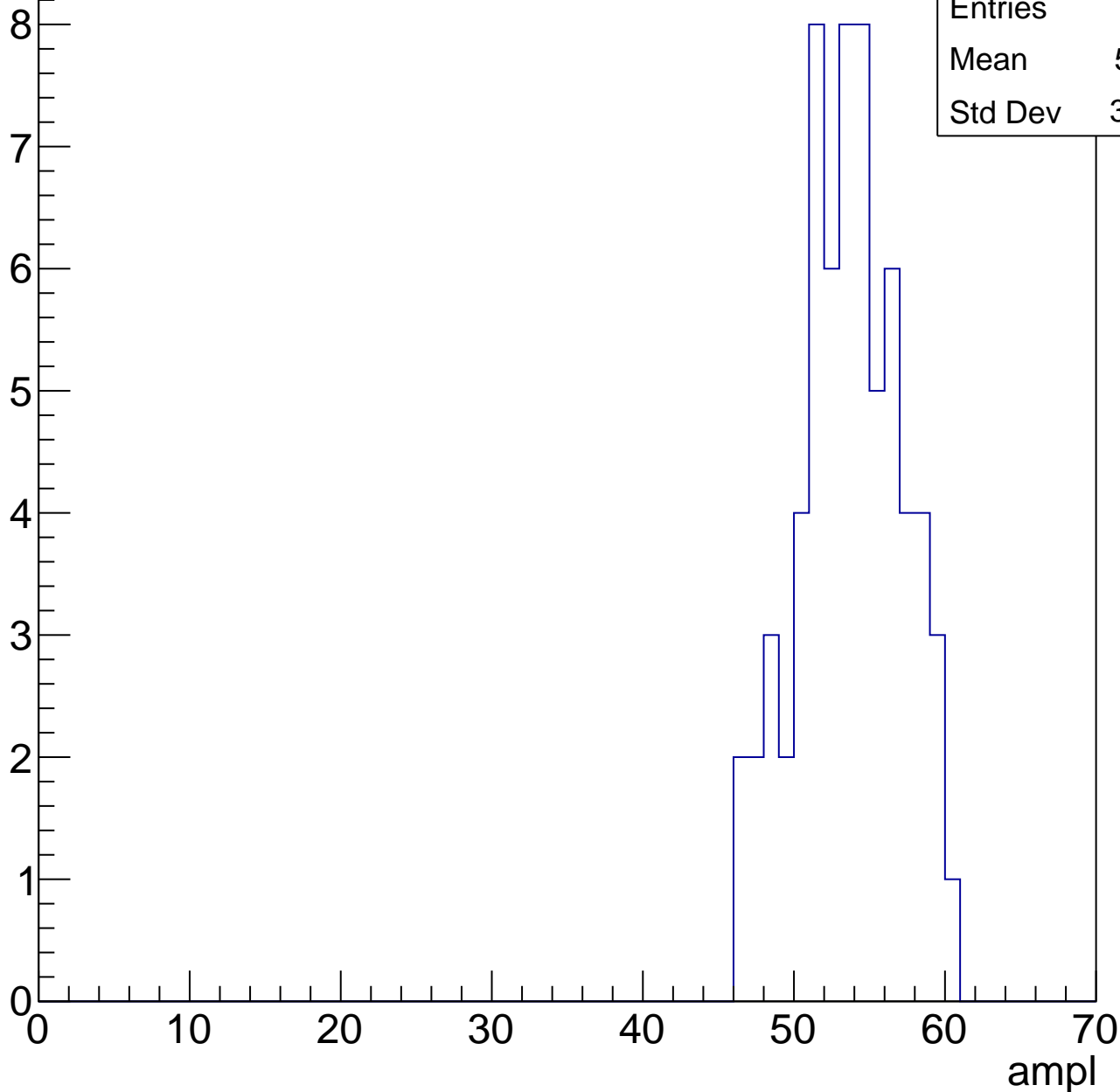


B1L003S, U19-ch94, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	53.21
Std Dev	3.373

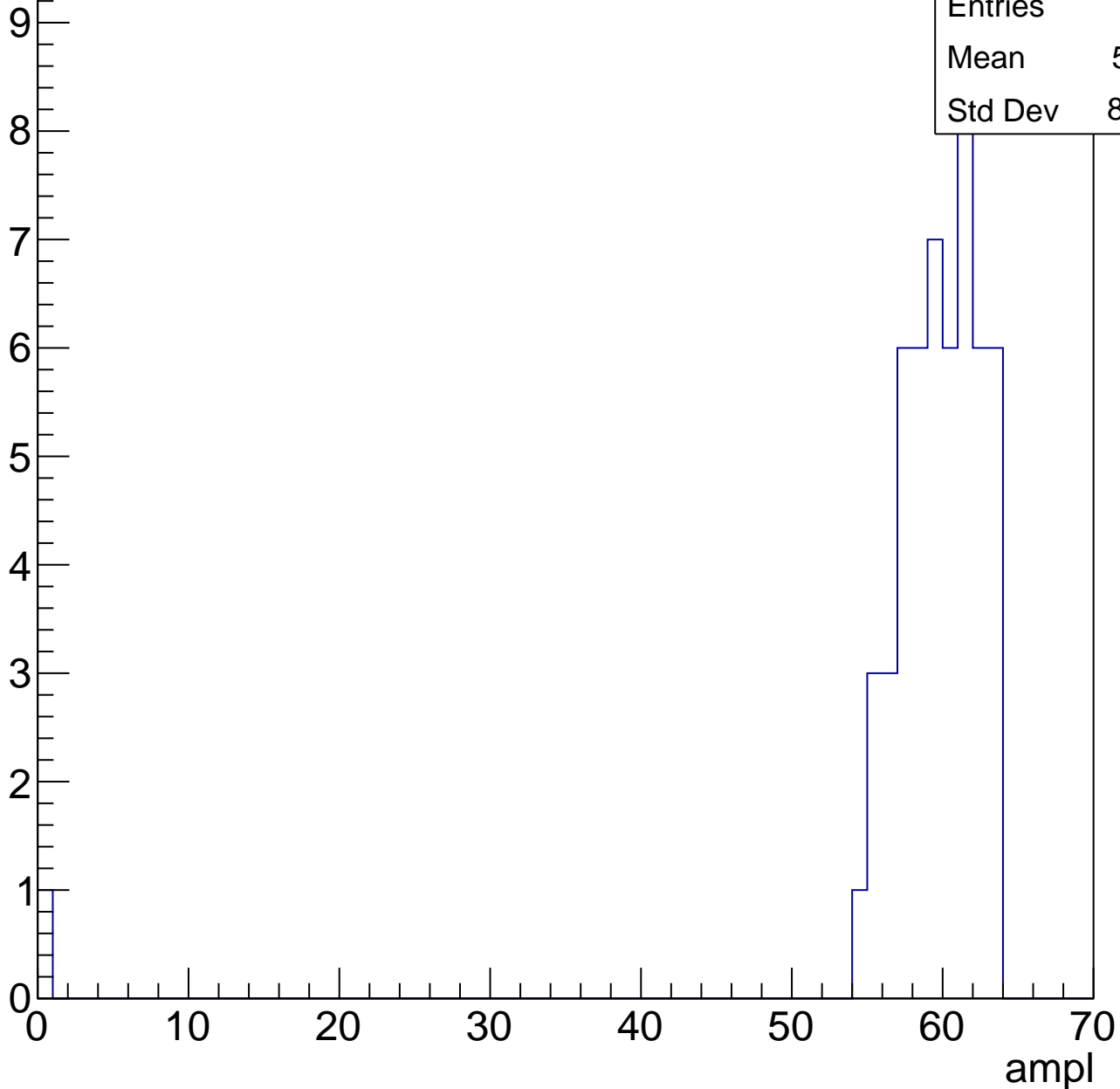


B1L003S, U19-ch94, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

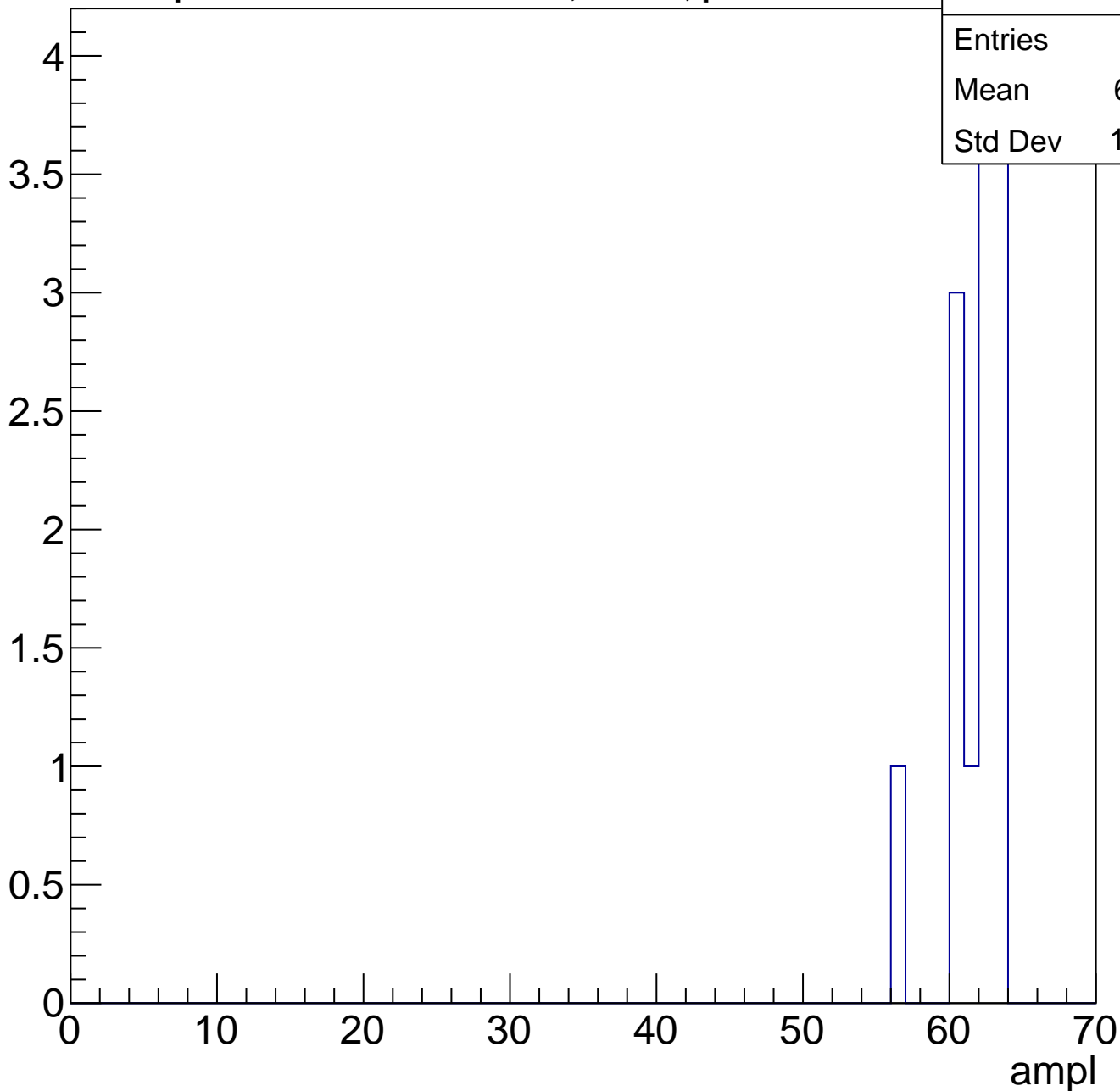
Entries	54
Mean	58.31
Std Dev	8.364



B1L003S, U19-ch94, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	13
Mean	61.31
Std Dev	1.897

B1L003S, U19-ch94, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch95, adc0

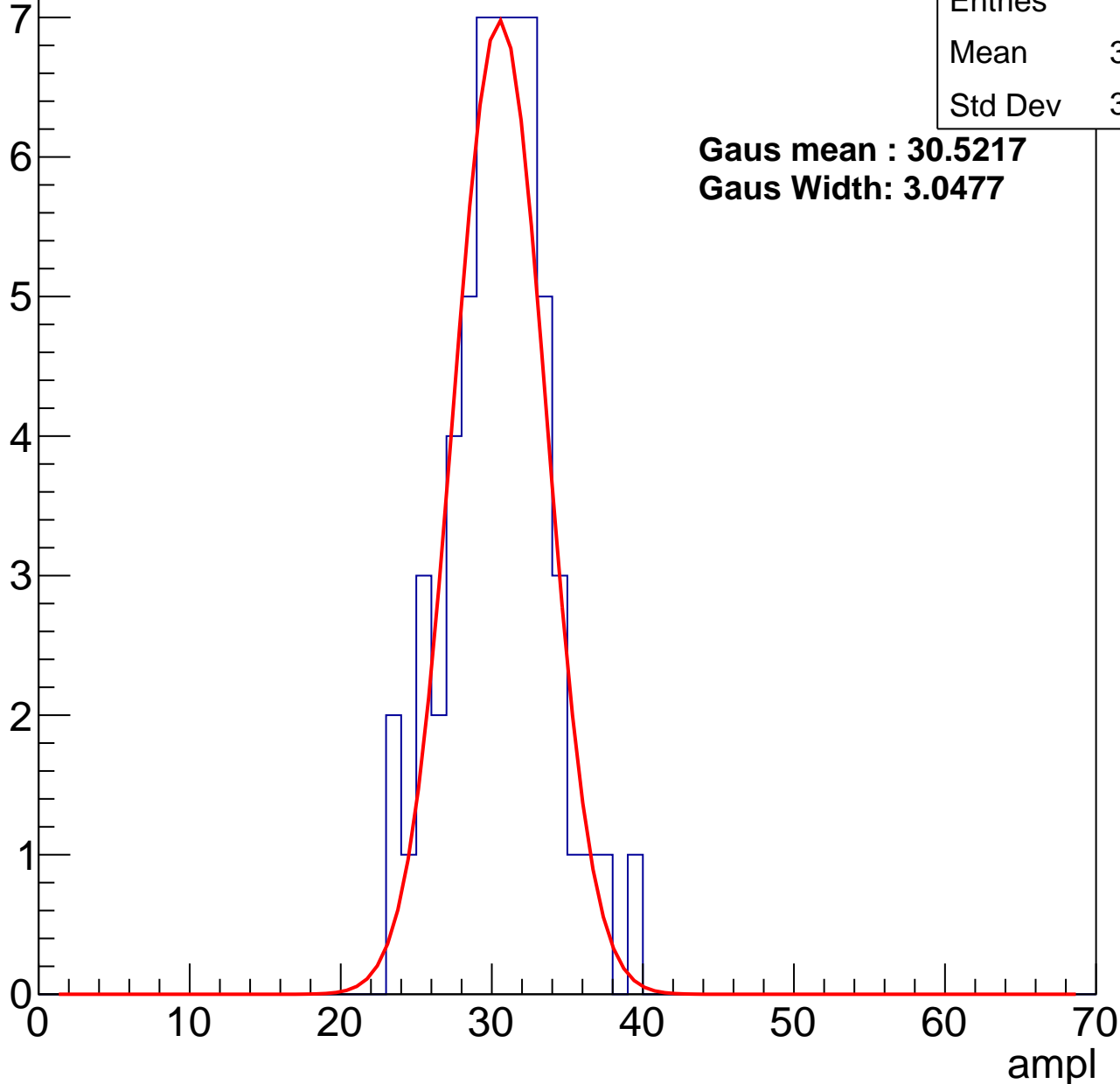
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	30.05
Std Dev	3.306

Gaus mean : 30.5217

Gaus Width: 3.0477



B1L003S, U19-ch95, adc1

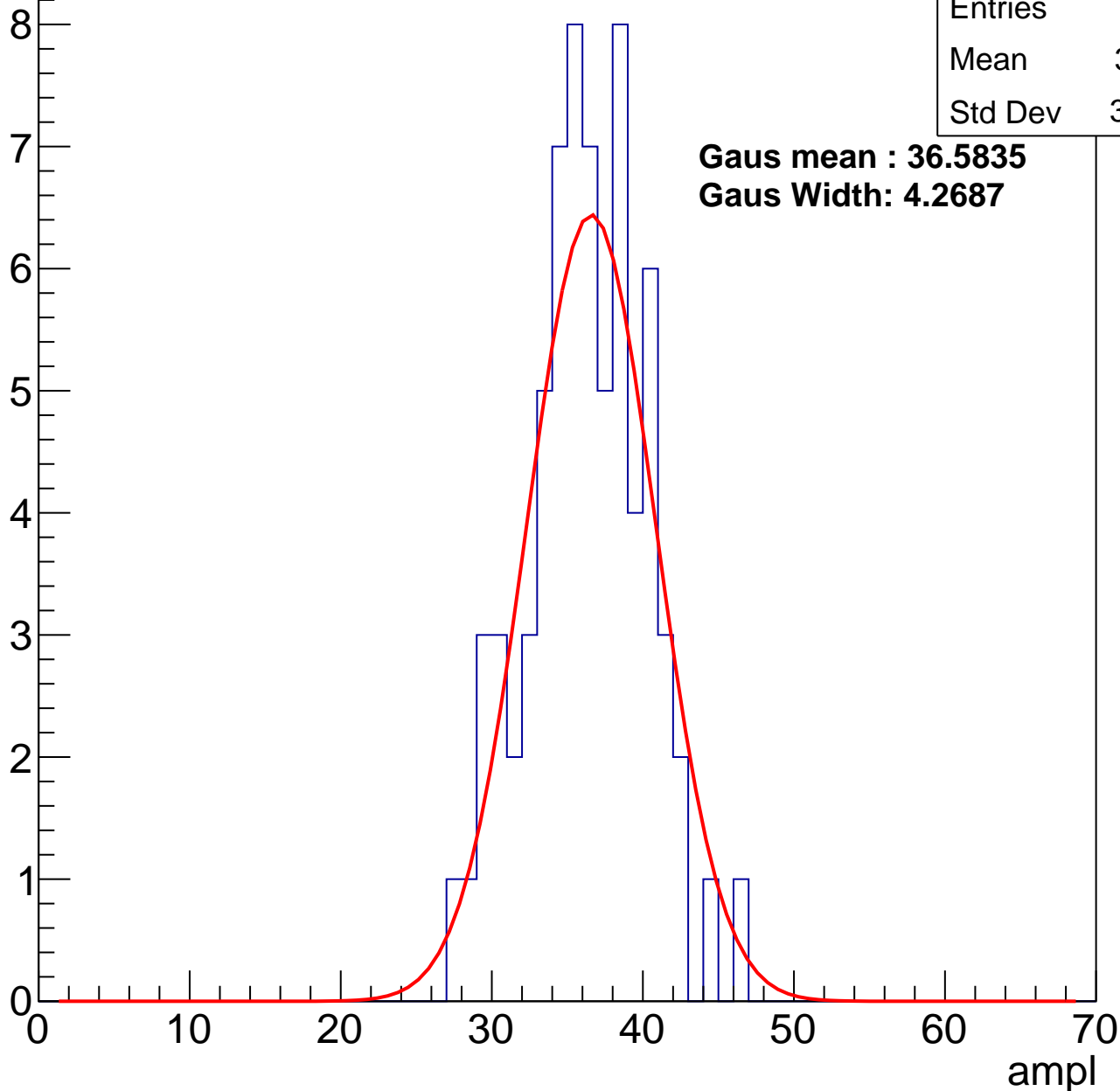
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	35.81
Std Dev	3.885

Gaus mean : 36.5835

Gaus Width: 4.2687



B1L003S, U19-ch95, adc2

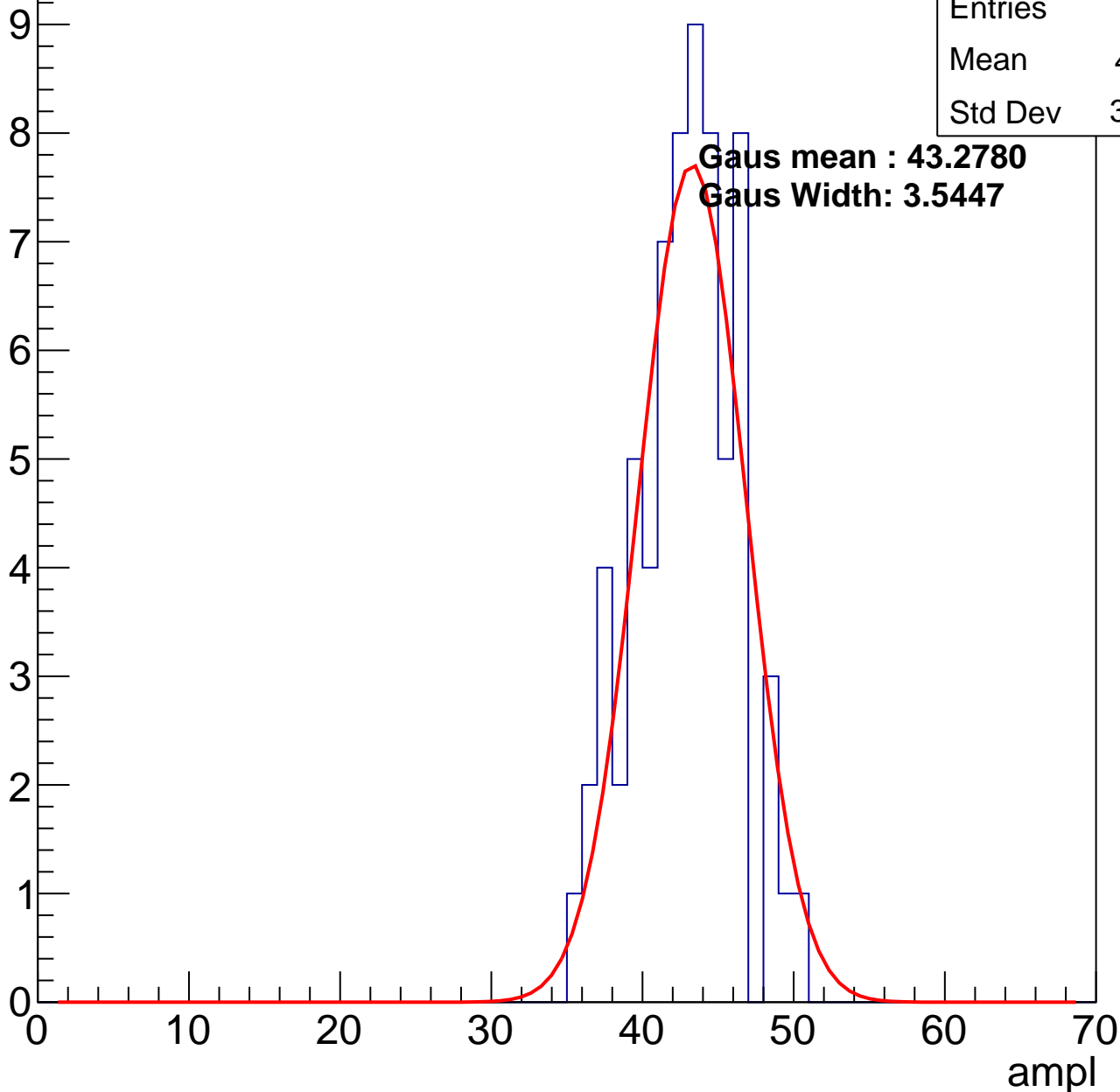
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.41
Std Dev	3.309

Gaus mean : 43.2780

Gaus Width: 3.5447



B1L003S, U19-ch95, adc3

calib_packv5_042523_0143.root, FC#13, port D2

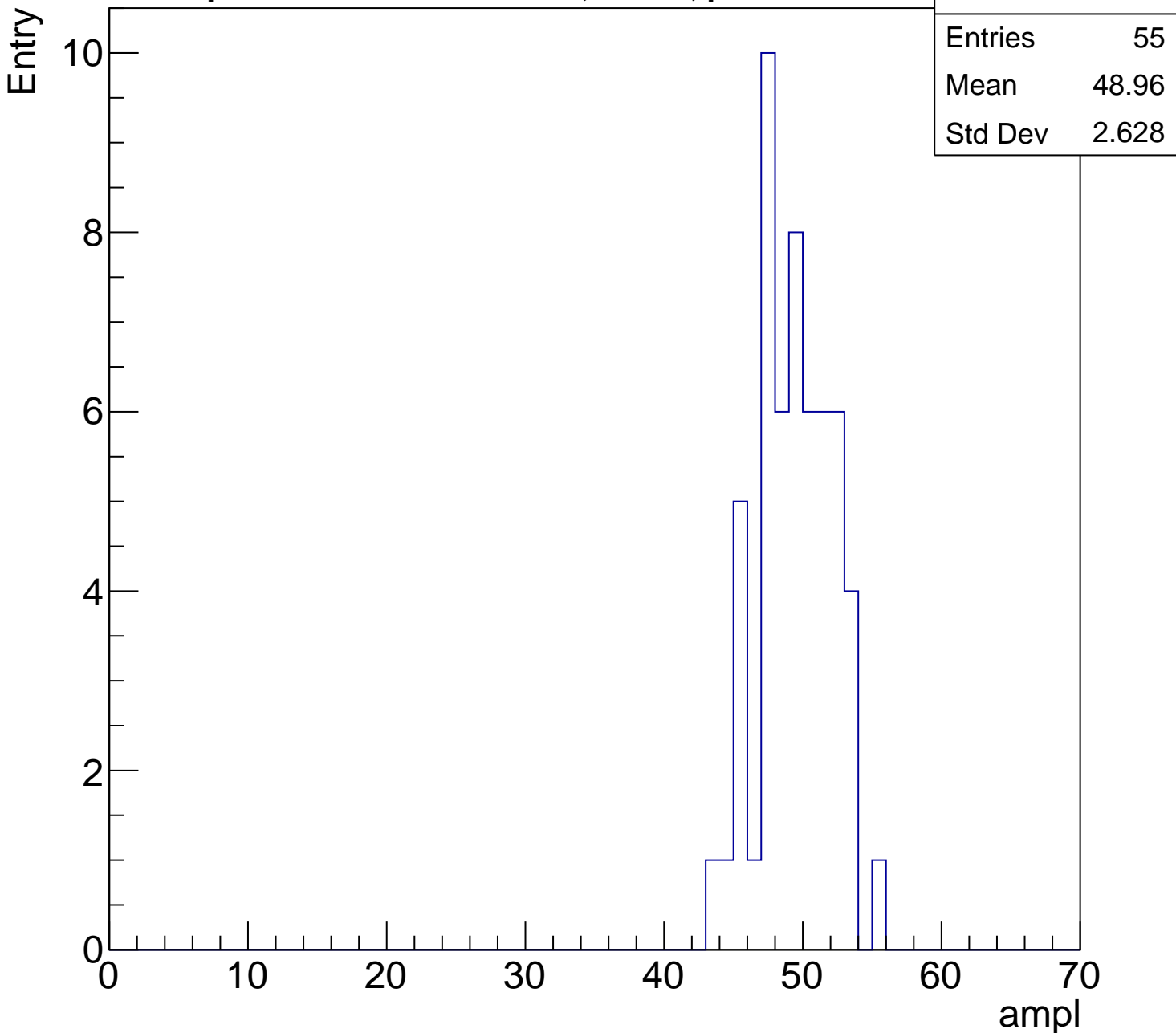
Entries	55
Mean	48.96
Std Dev	2.628

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

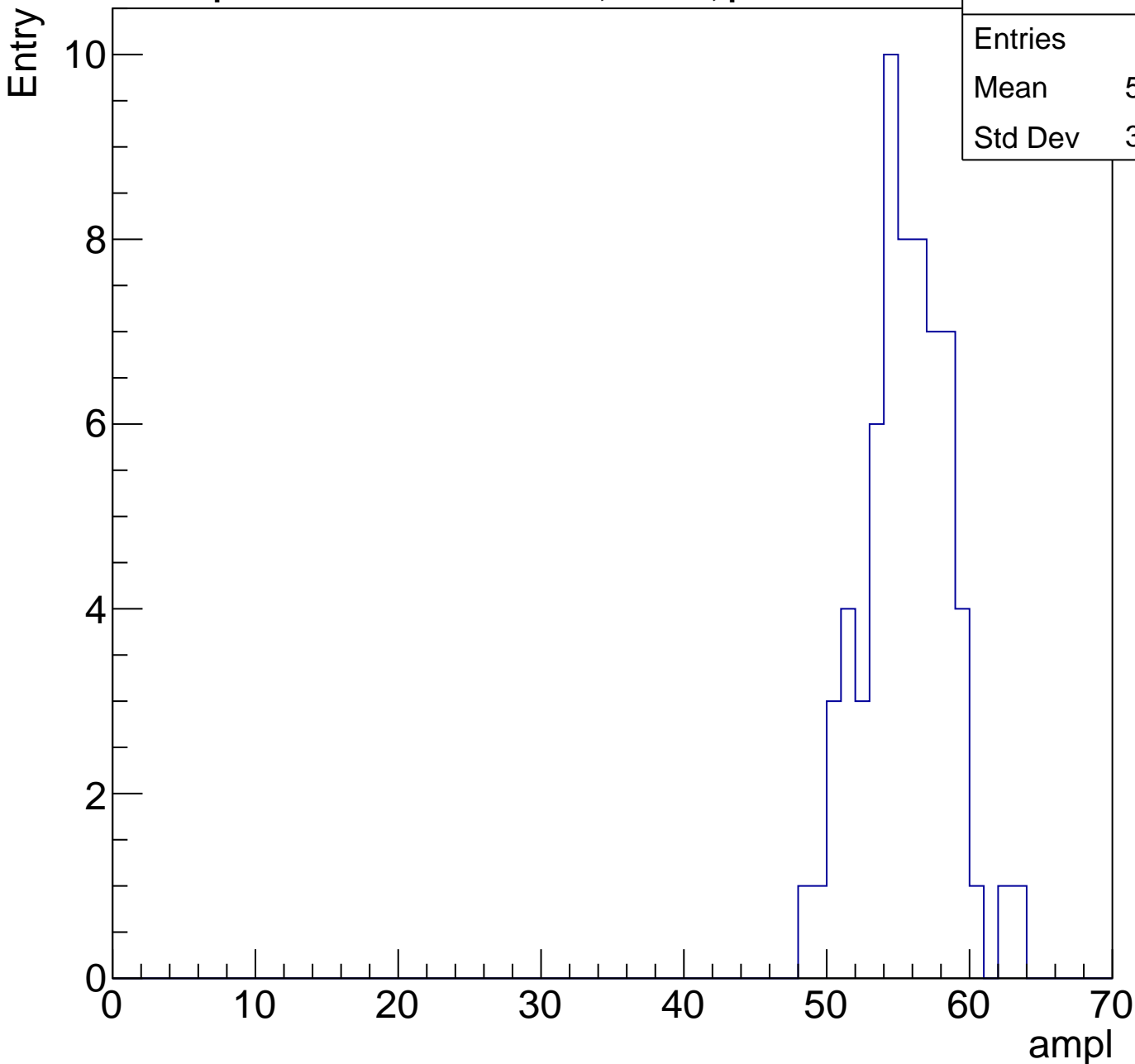
ampl



B1L003S, U19-ch95, adc4

calib_packv5_042523_0143.root, FC#13, port D2

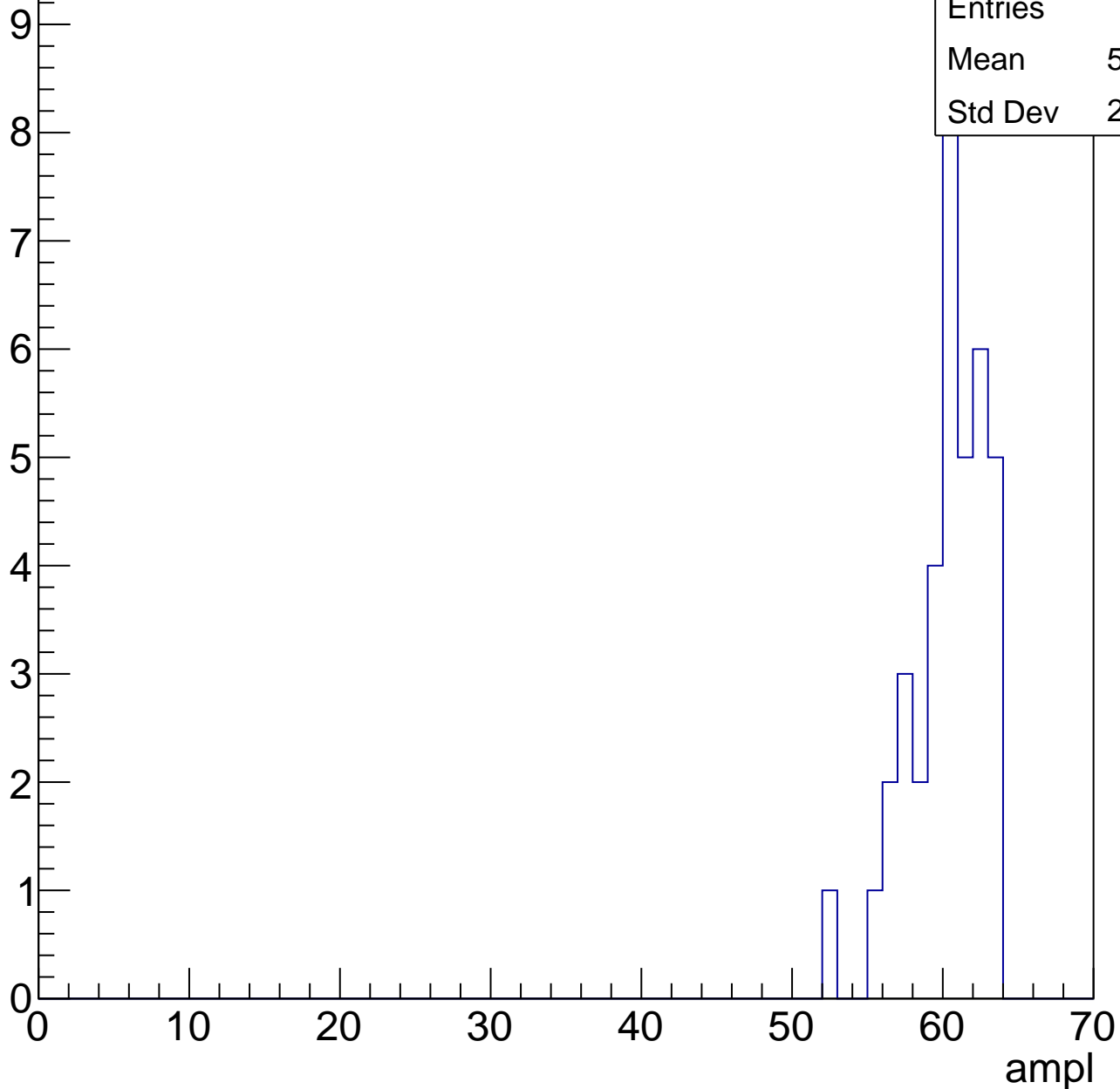
Entries	65
Mean	55.06
Std Dev	3.002



B1L003S, U19-ch95, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

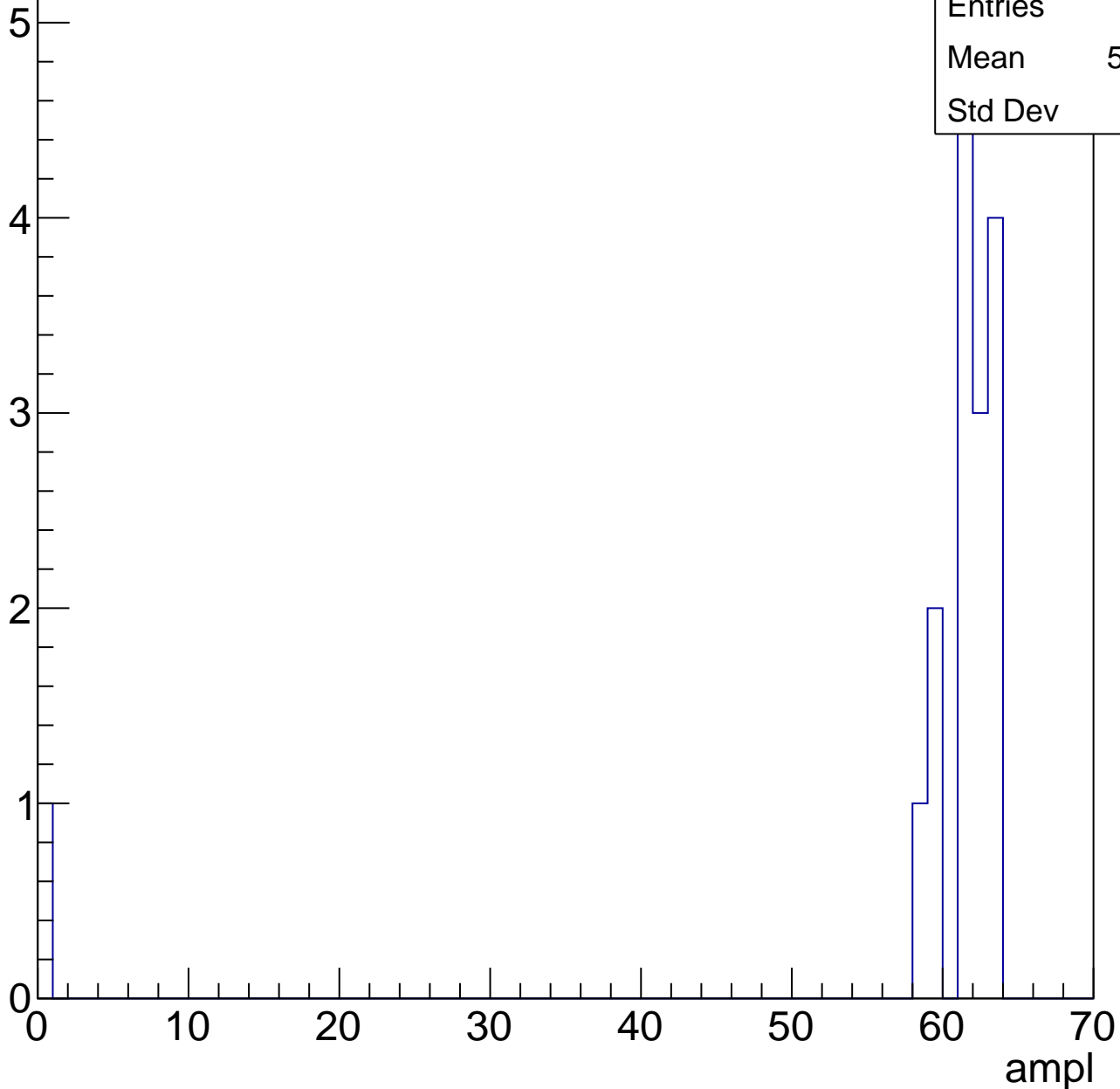


B1L003S, U19-ch95, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	57.44
Std Dev	14.9



B1L003S, U19-ch95, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch96, adc0

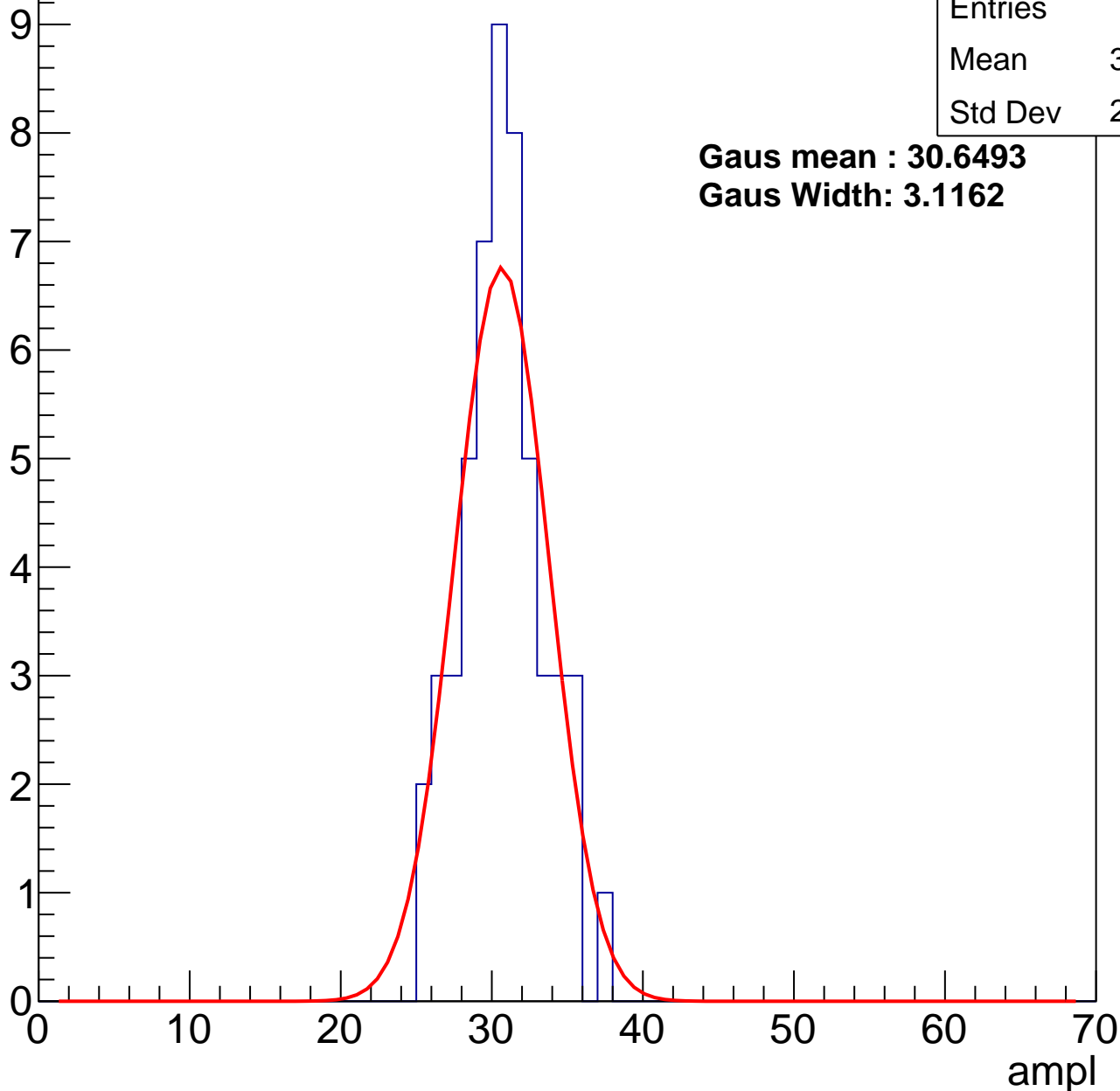
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	30.25
Std Dev	2.688

Gaus mean : 30.6493

Gaus Width: 3.1162



B1L003S, U19-ch96, adc1

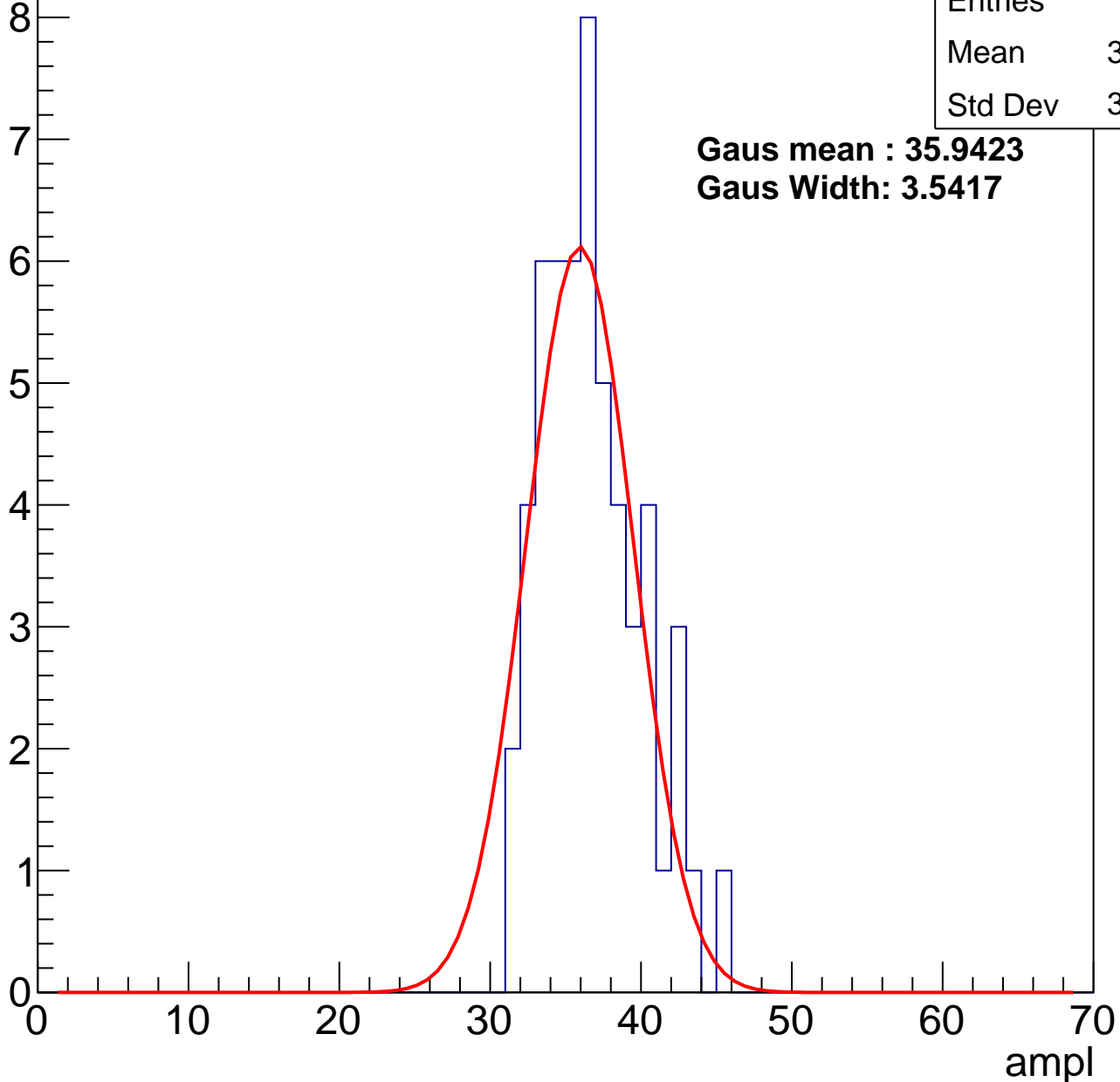
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.28
Std Dev	3.246

Gaus mean : 35.9423

Gaus Width: 3.5417



B1L003S, U19-ch96, adc2

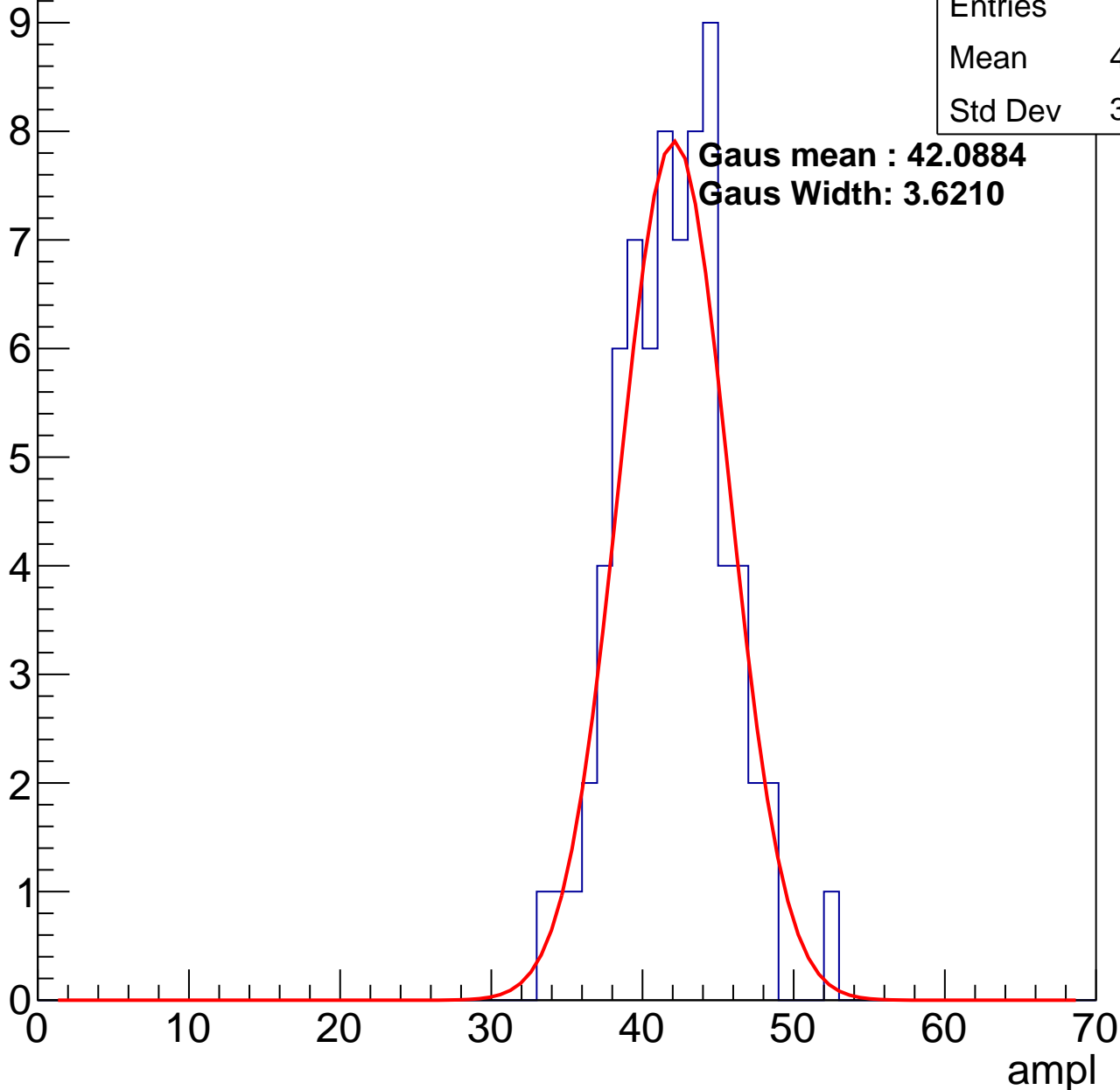
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	41.52
Std Dev	3.523

Gaus mean : 42.0884

Gaus Width: 3.6210

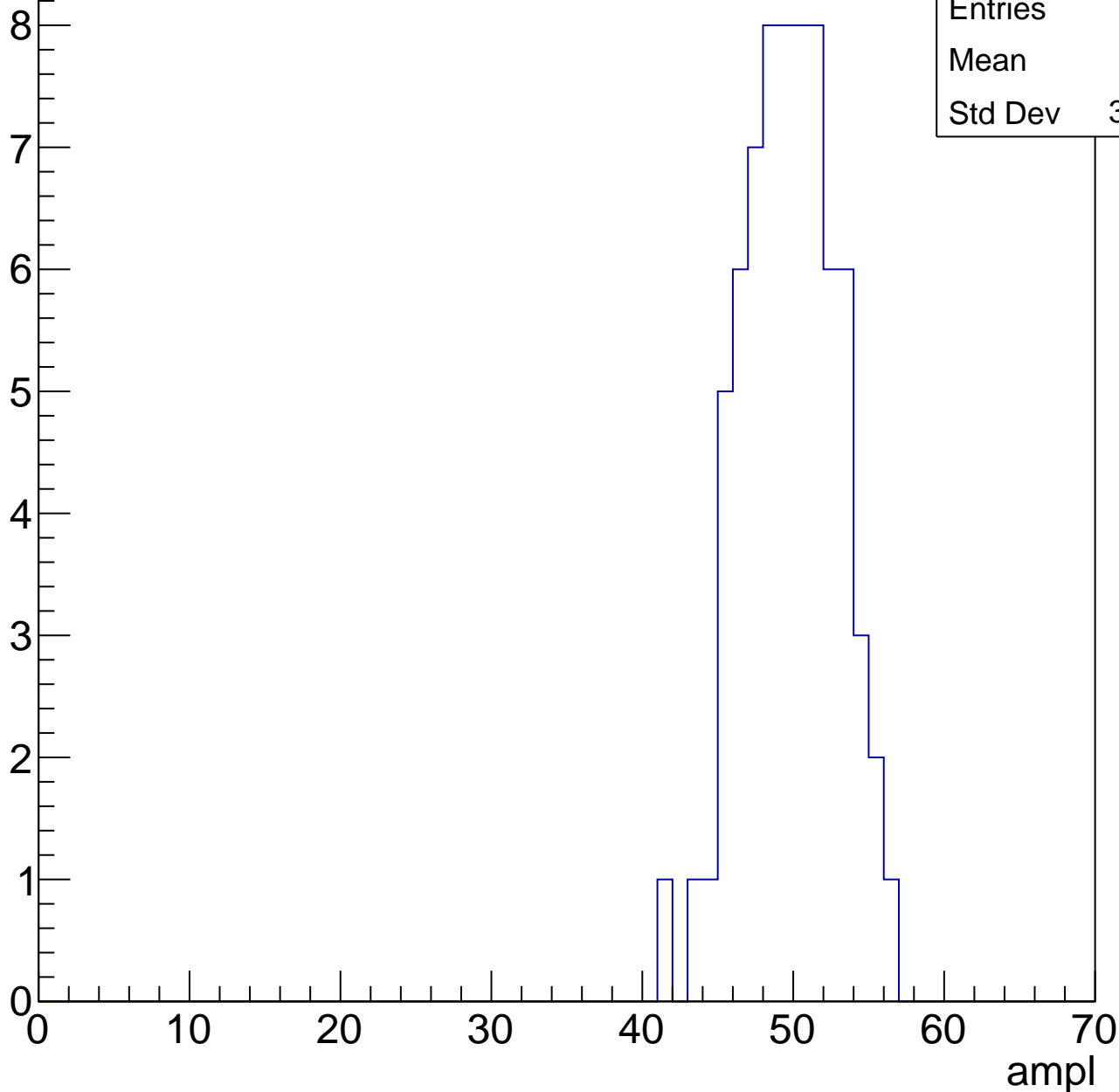


B1L003S, U19-ch96, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	49.3
Std Dev	3.083

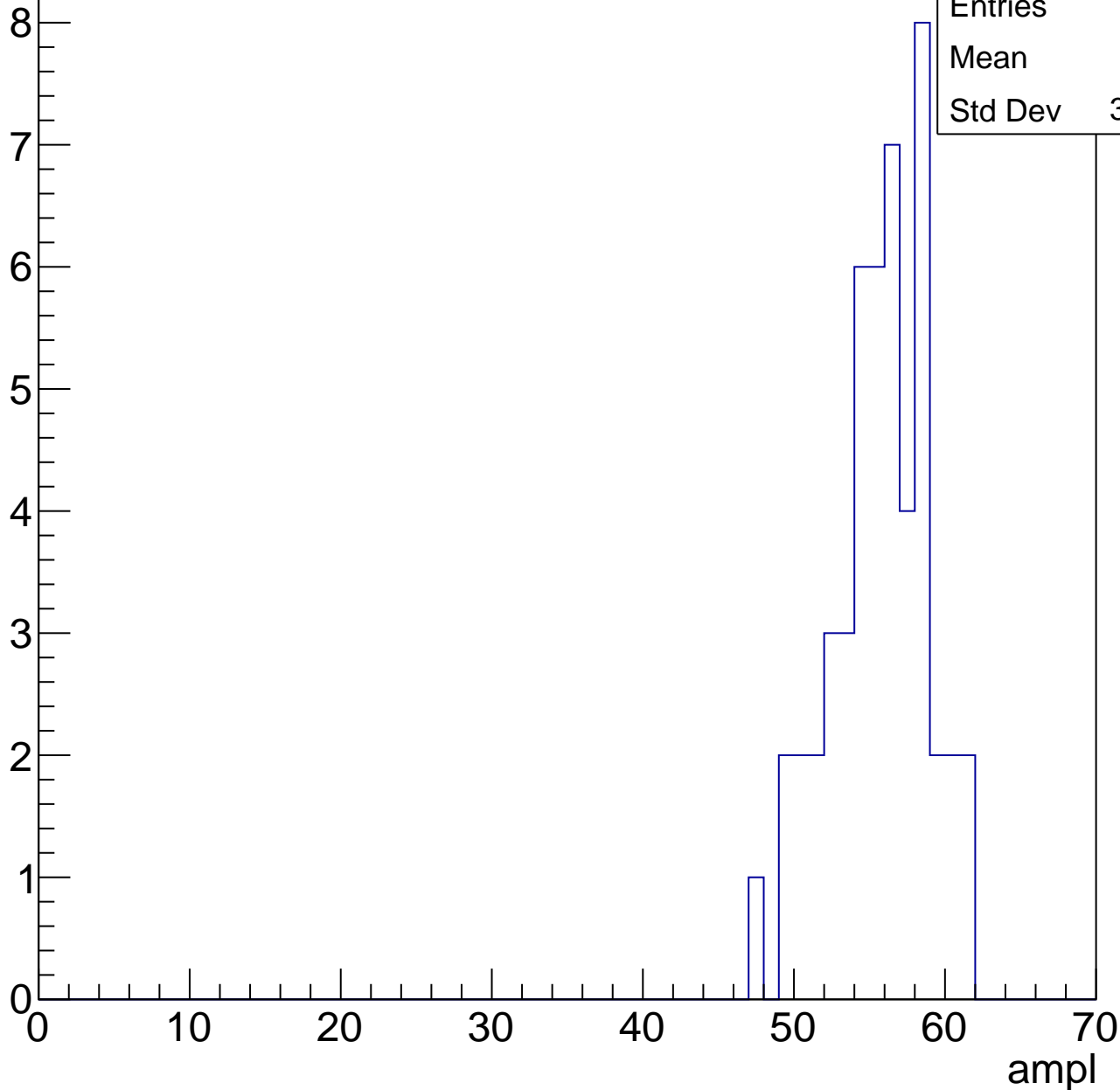


B1L003S, U19-ch96, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

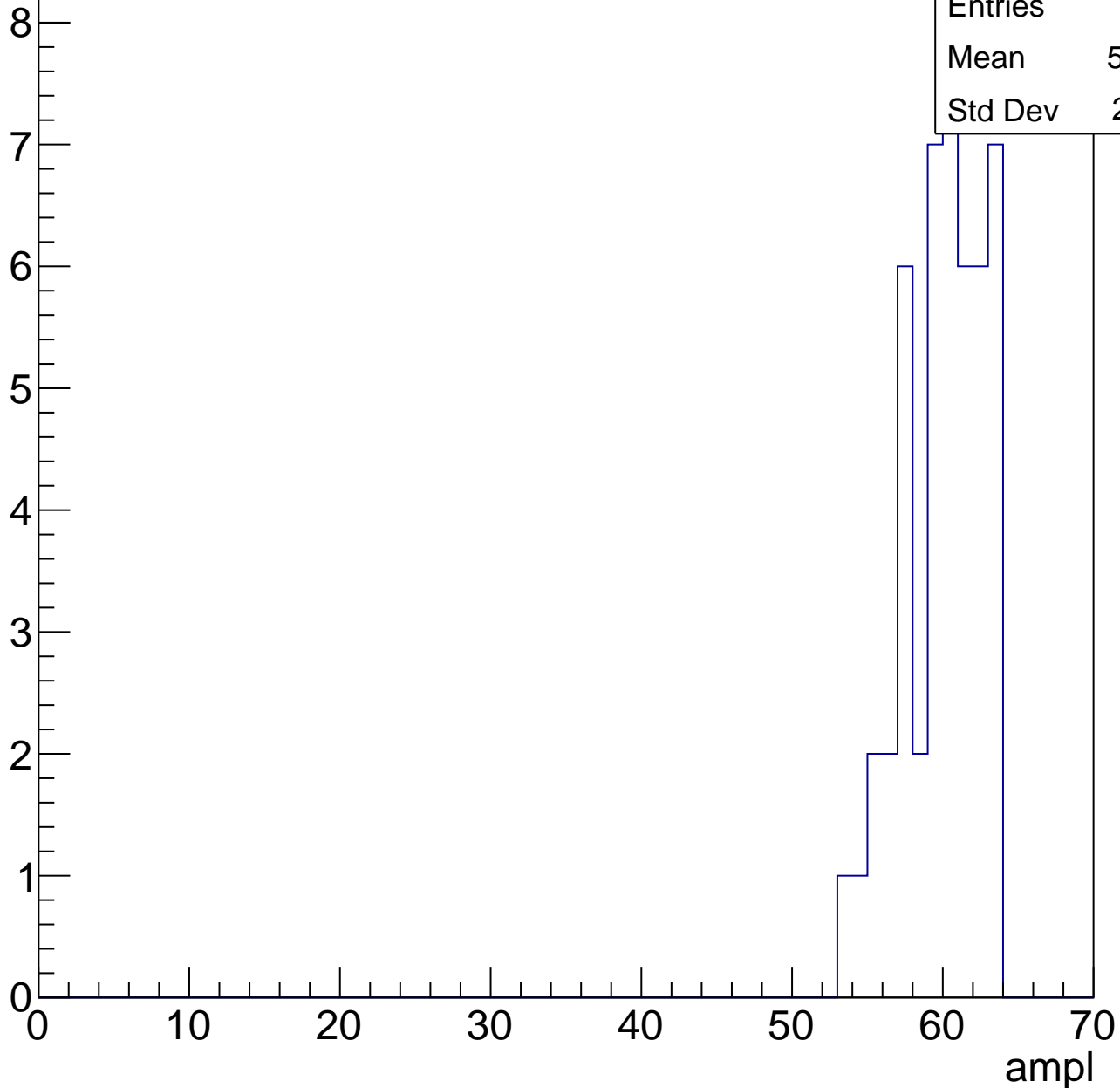
Entries	50
Mean	55.2
Std Dev	3.194



B1L003S, U19-ch96, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

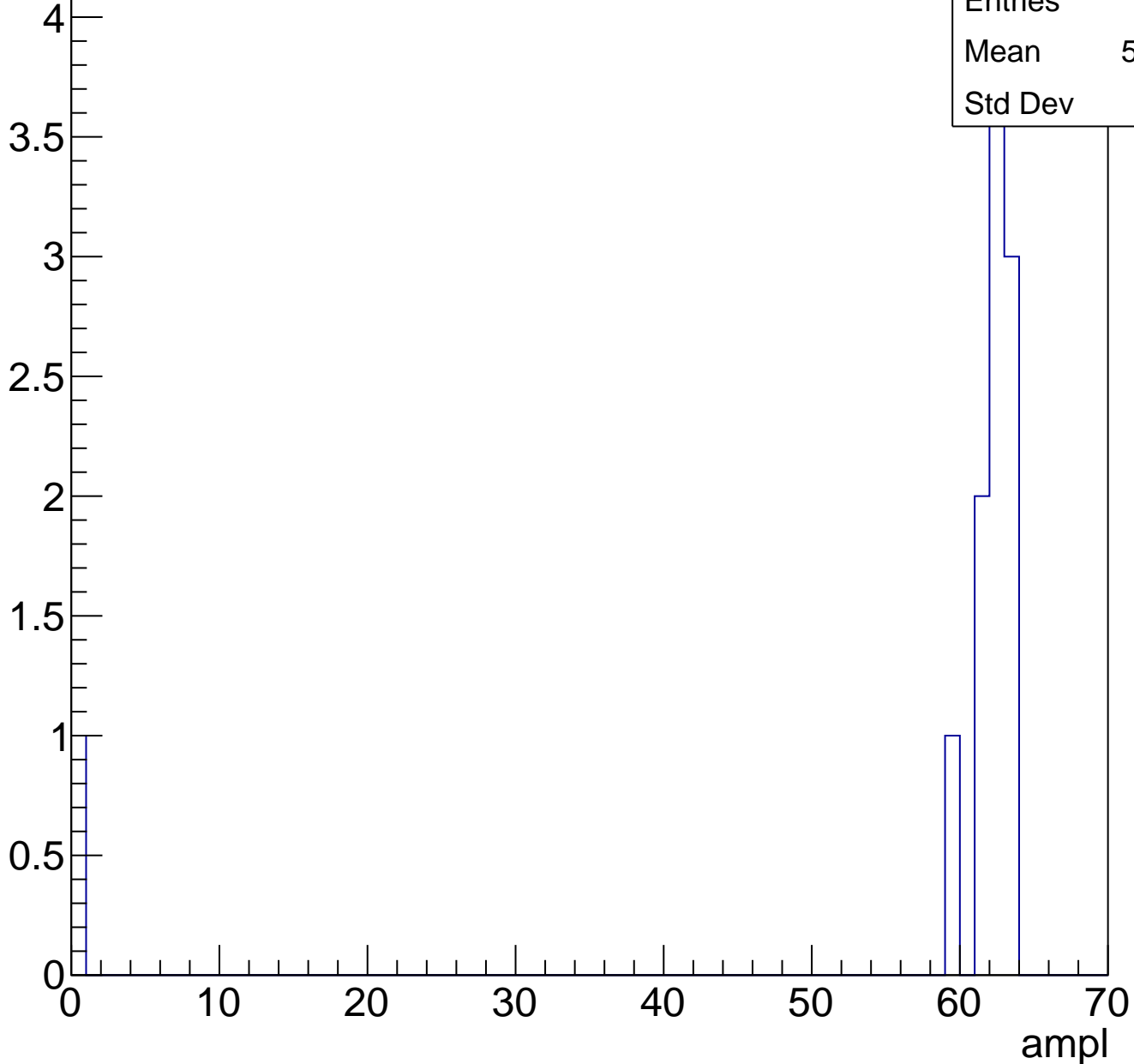


Entries	48
Mean	59.56
Std Dev	2.581

B1L003S, U19-ch96, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	11
Mean	56.18
Std Dev	17.8

B1L003S, U19-ch96, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch97, adc0

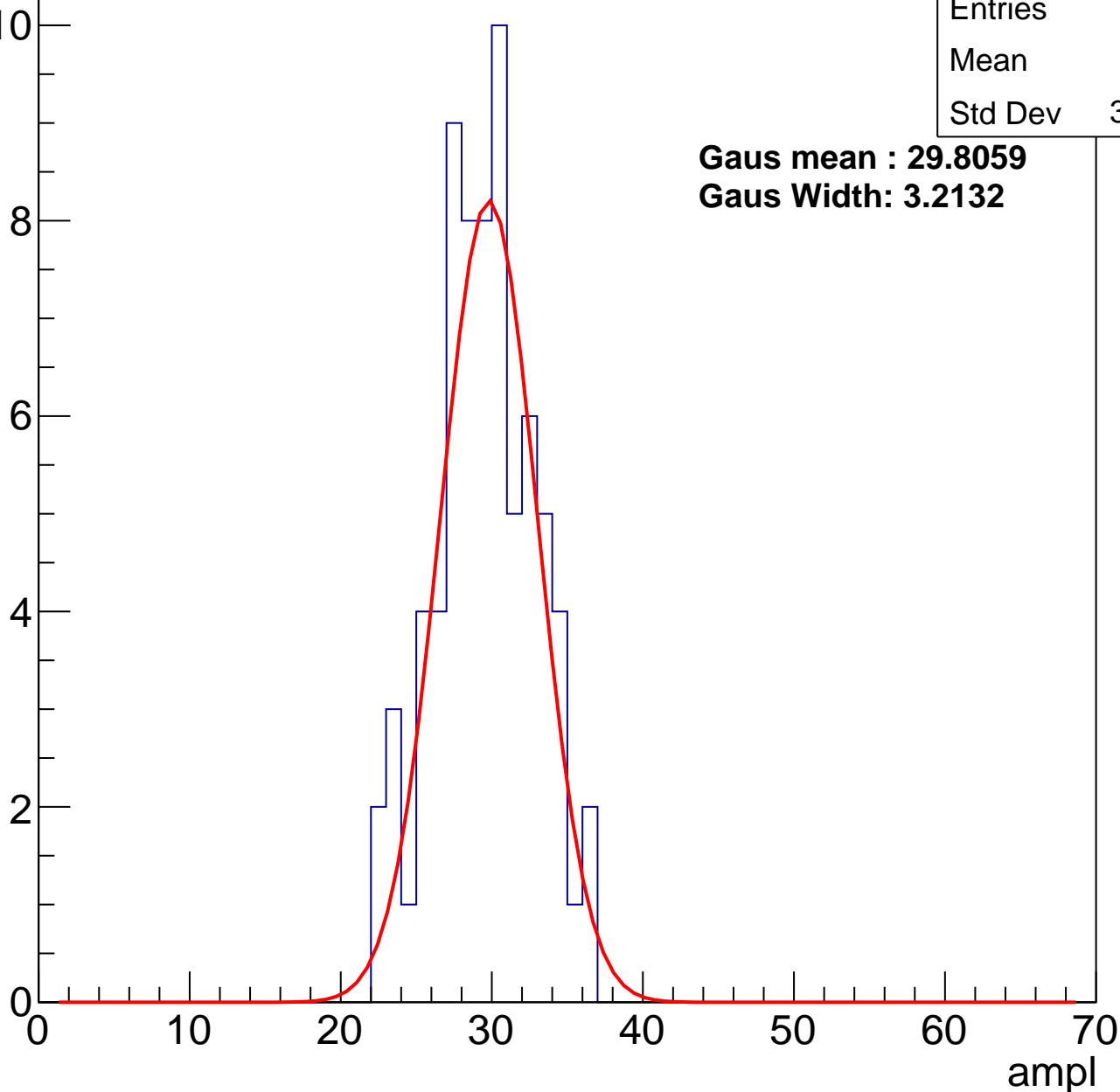
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.1
Std Dev	3.275

Gaus mean : 29.8059

Gaus Width: 3.2132



B1L003S, U19-ch97, adc1

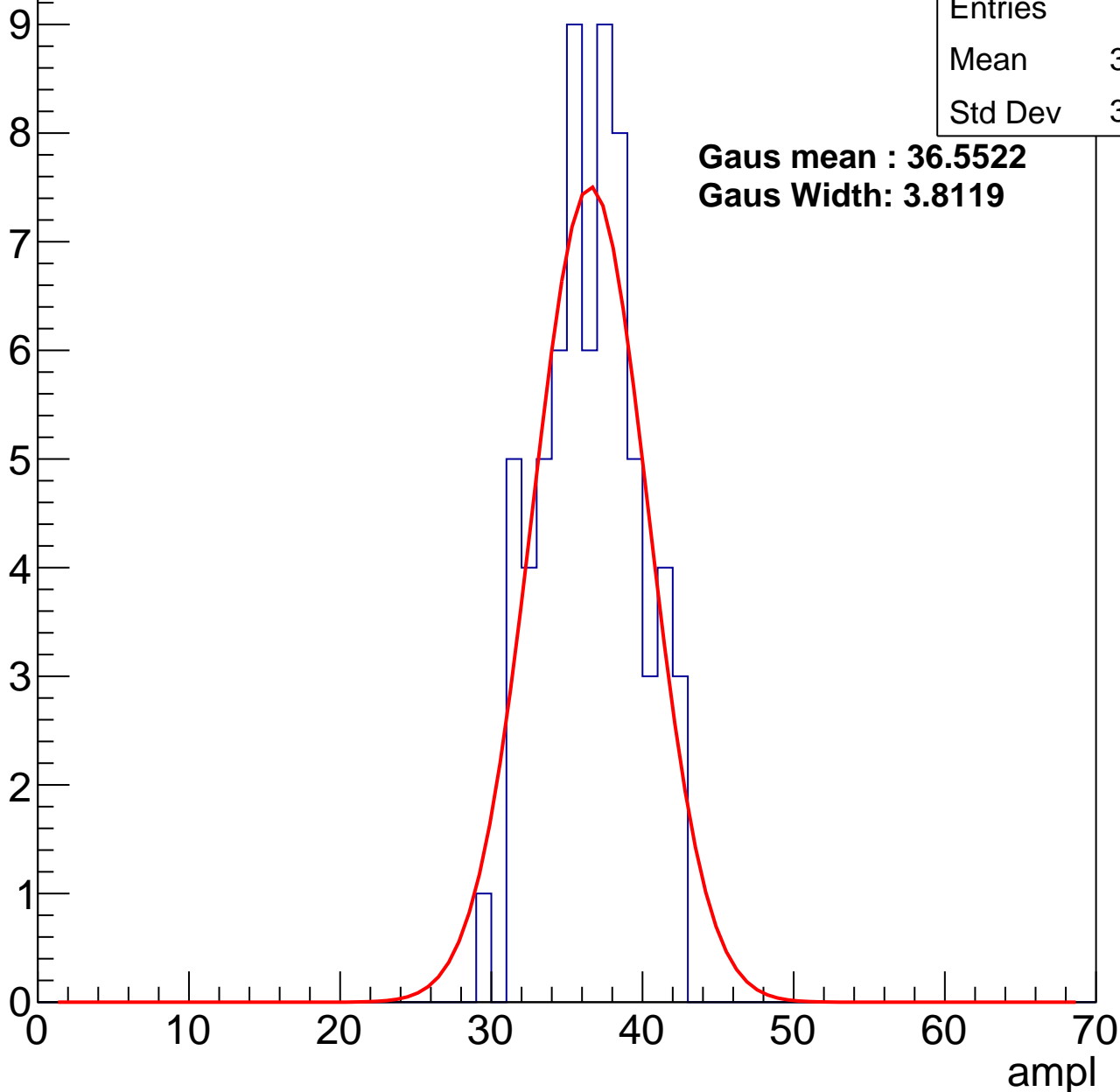
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.09
Std Dev	3.109

Gaus mean : 36.5522

Gaus Width: 3.8119



B1L003S, U19-ch97, adc2

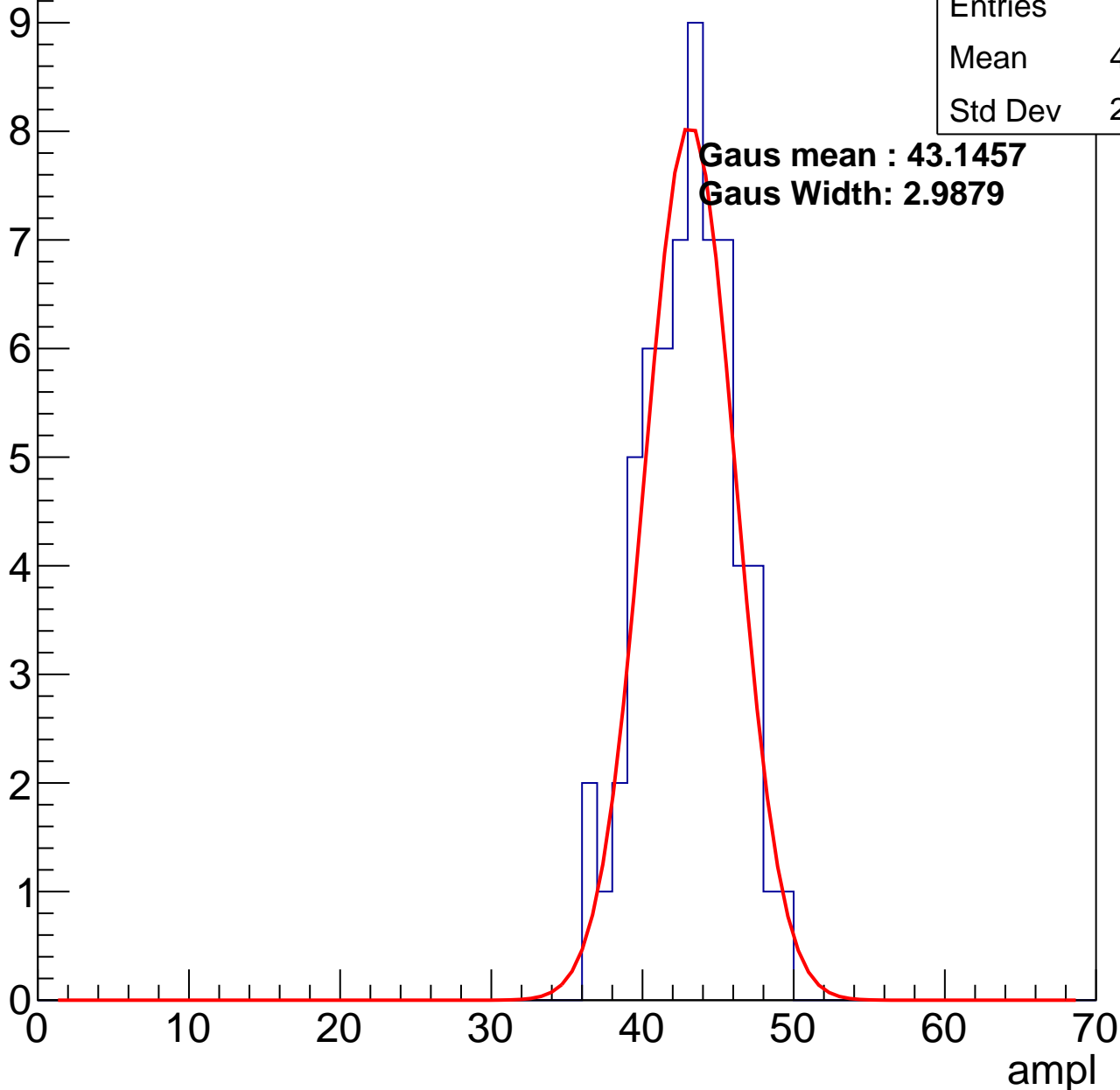
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.56
Std Dev	2.933

Gaus mean : 43.1457

Gaus Width: 2.9879

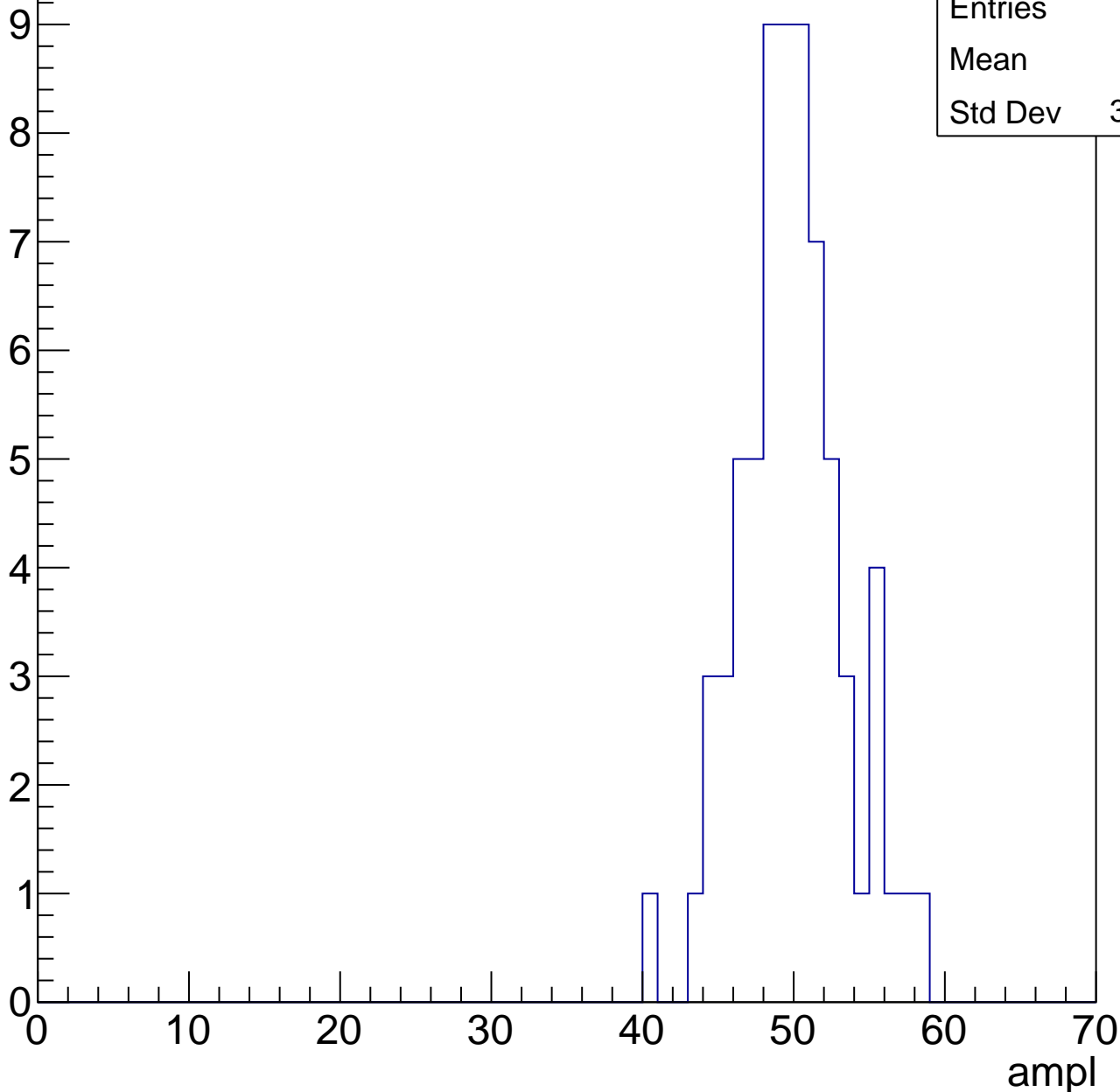


B1L003S, U19-ch97, adc3

calib_packv5_042523_0143.root, FC#13, port D2

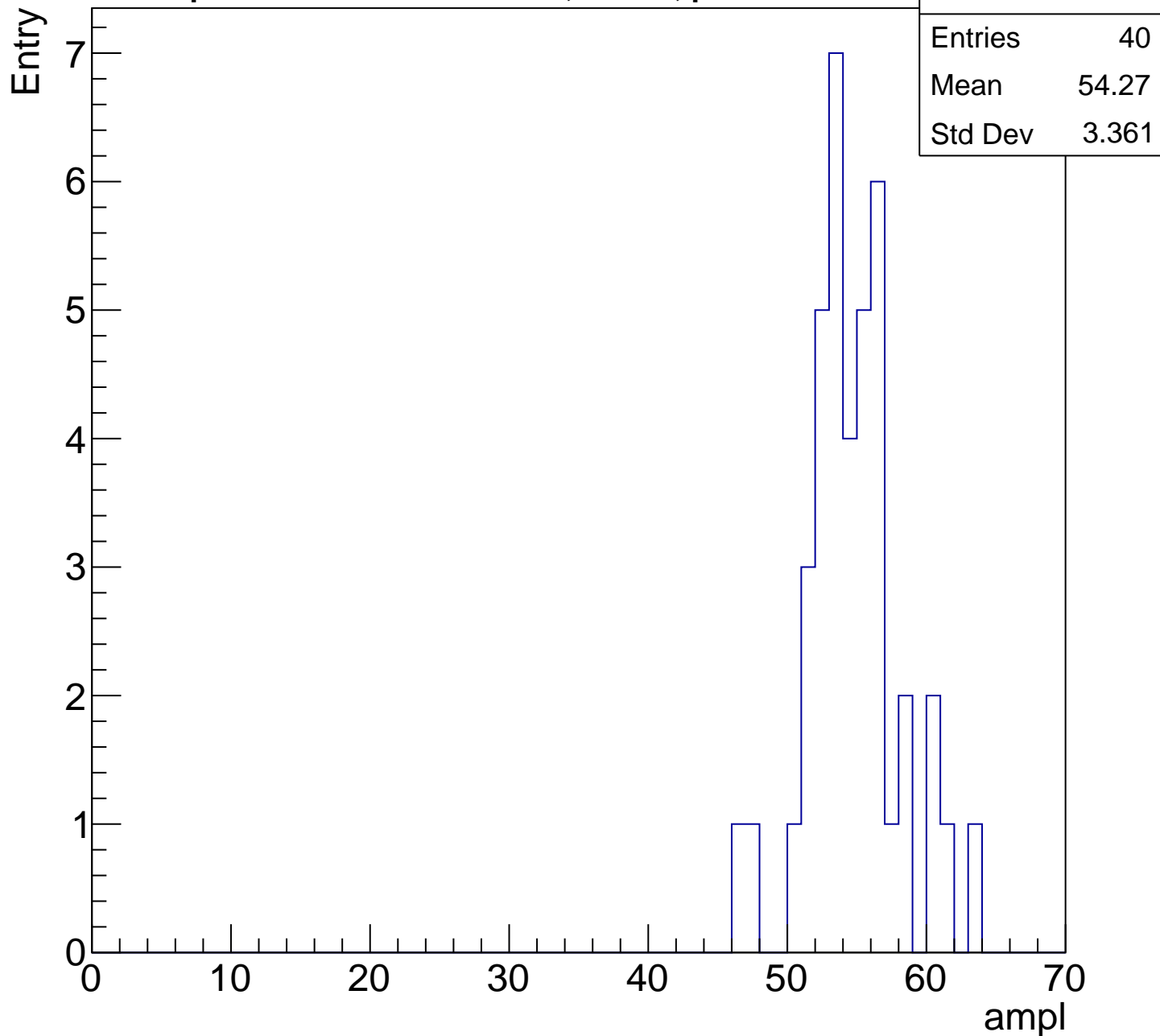
Entry

Entries	68
Mean	49.4
Std Dev	3.422



B1L003S, U19-ch97, adc4

calib_packv5_042523_0143.root, FC#13, port D2

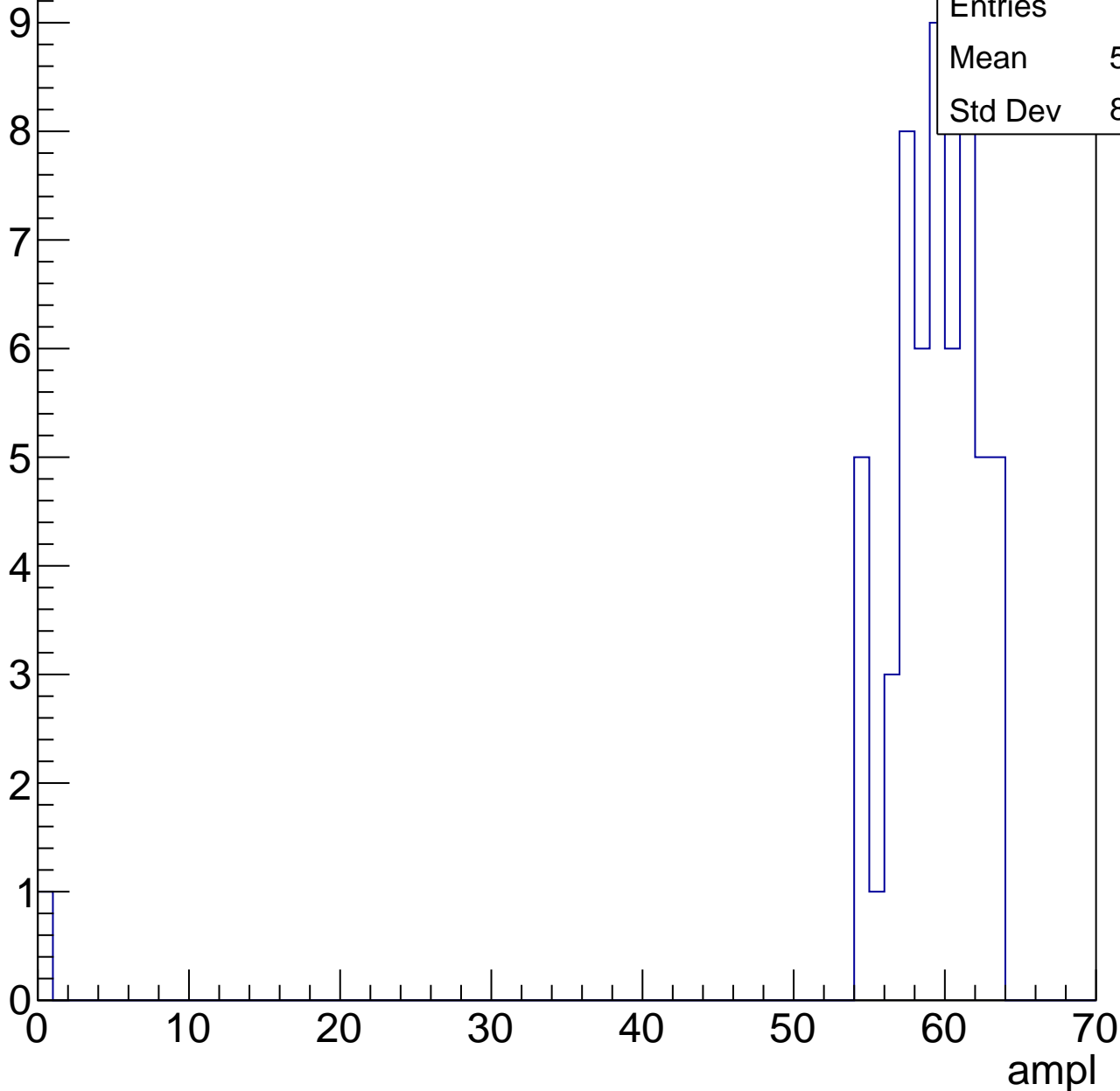


B1L003S, U19-ch97, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	57.97
Std Dev	8.086

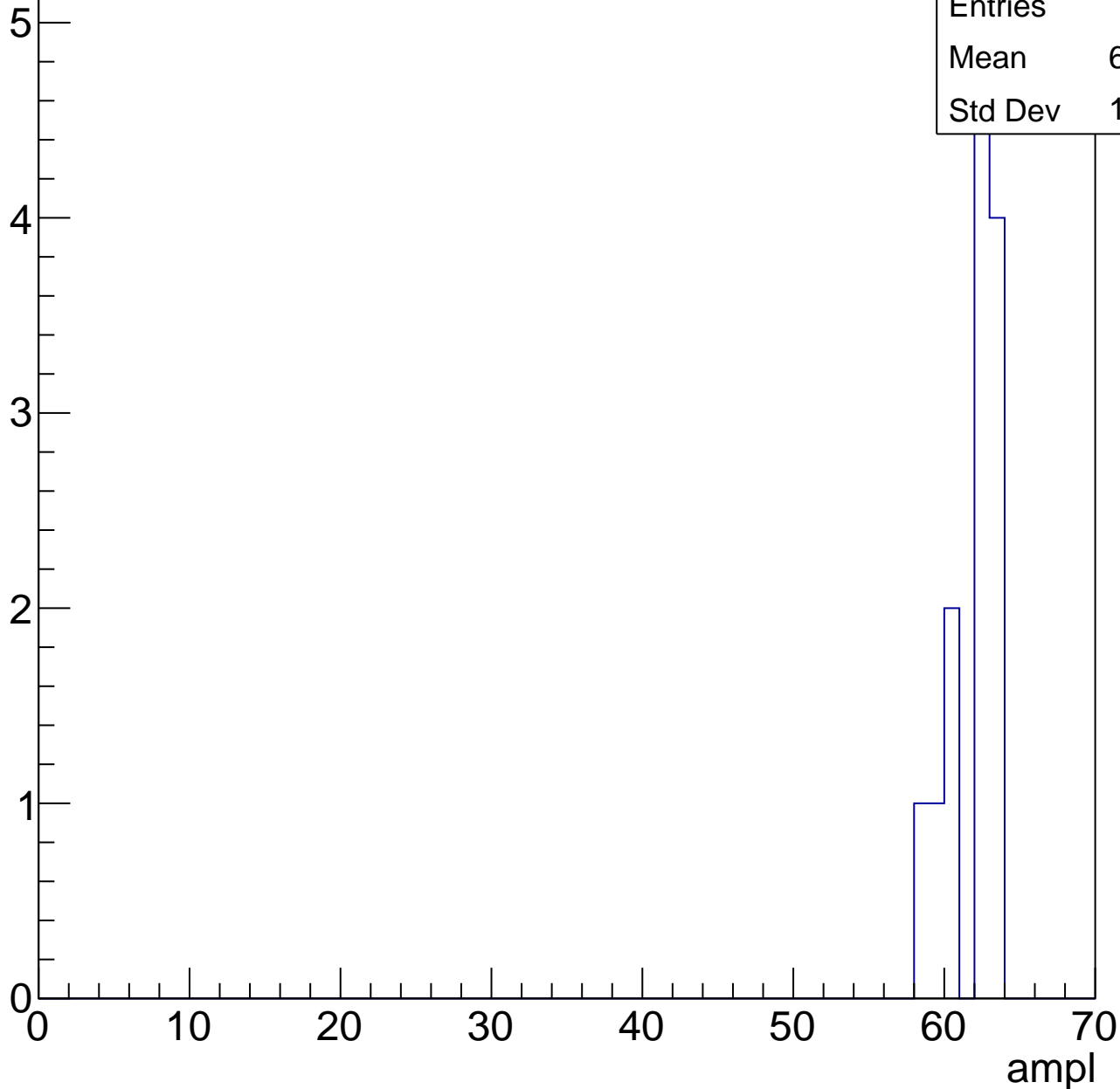


B1L003S, U19-ch97, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.46
Std Dev	1.599



B1L003S, U19-ch97, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch98, adc0

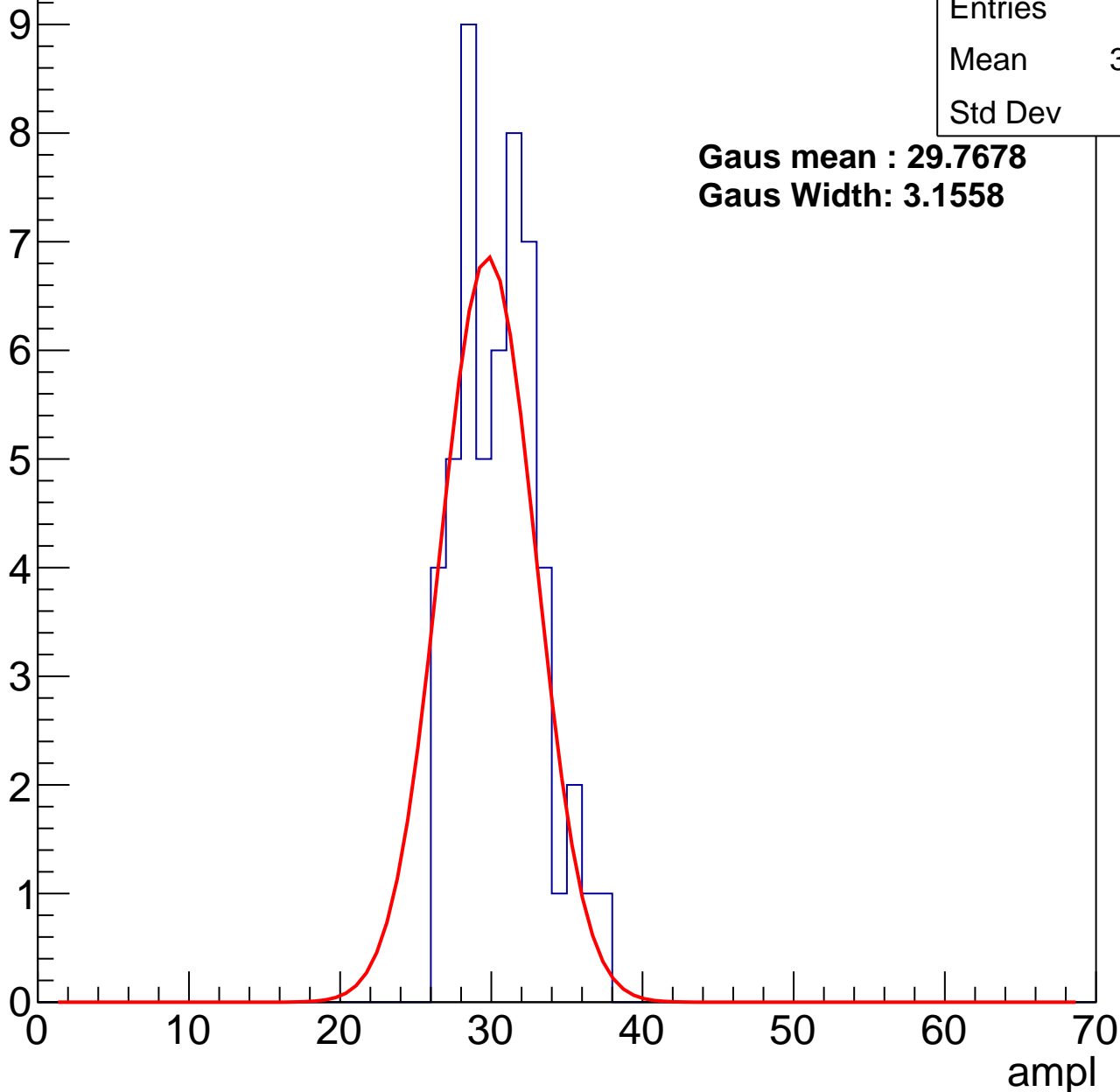
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	30.13
Std Dev	2.65

Gaus mean : 29.7678

Gaus Width: 3.1558



B1L003S, U19-ch98, adc1

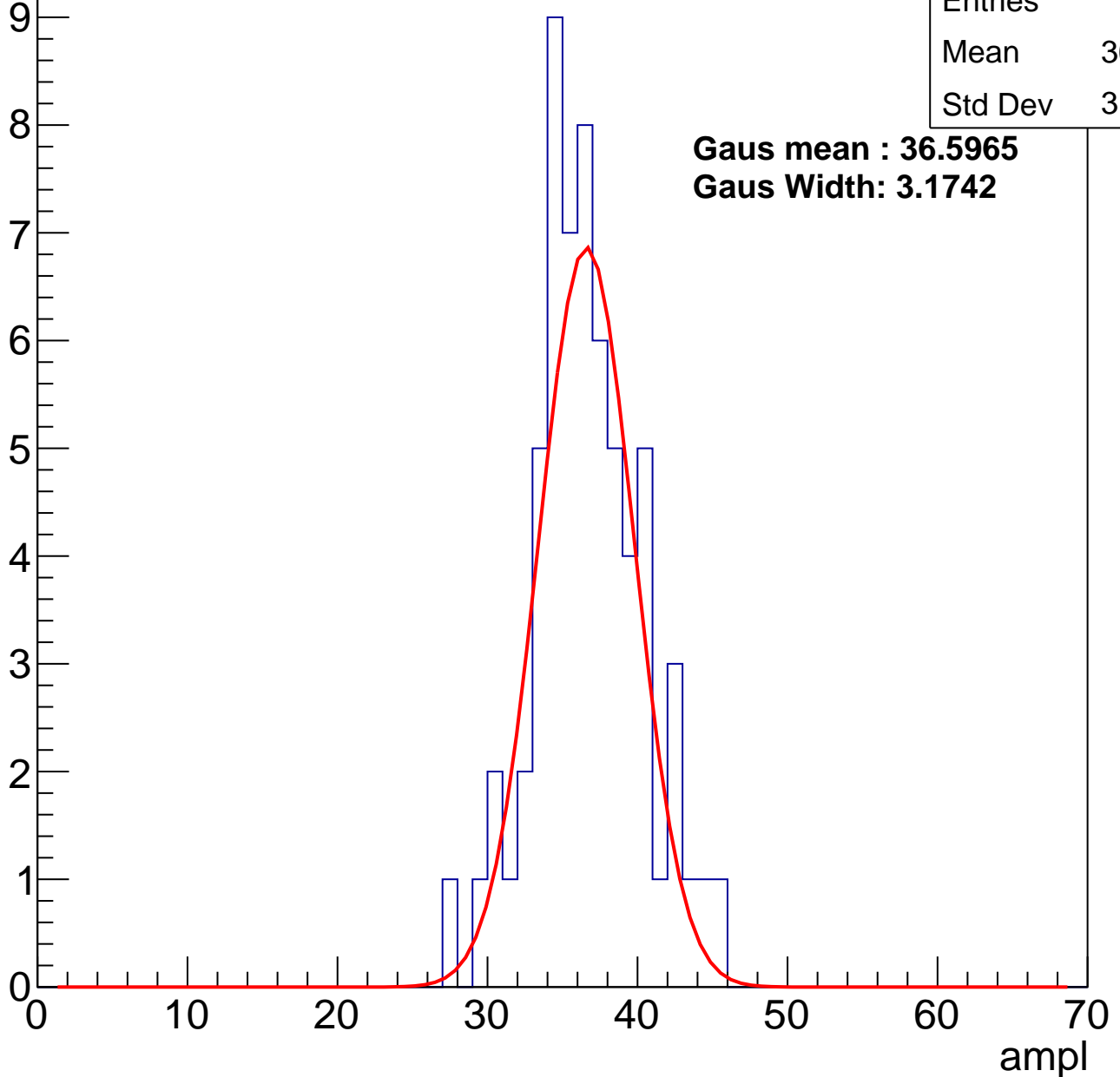
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	36.22
Std Dev	3.592

Gaus mean : 36.5965

Gaus Width: 3.1742



B1L003S, U19-ch98, adc2

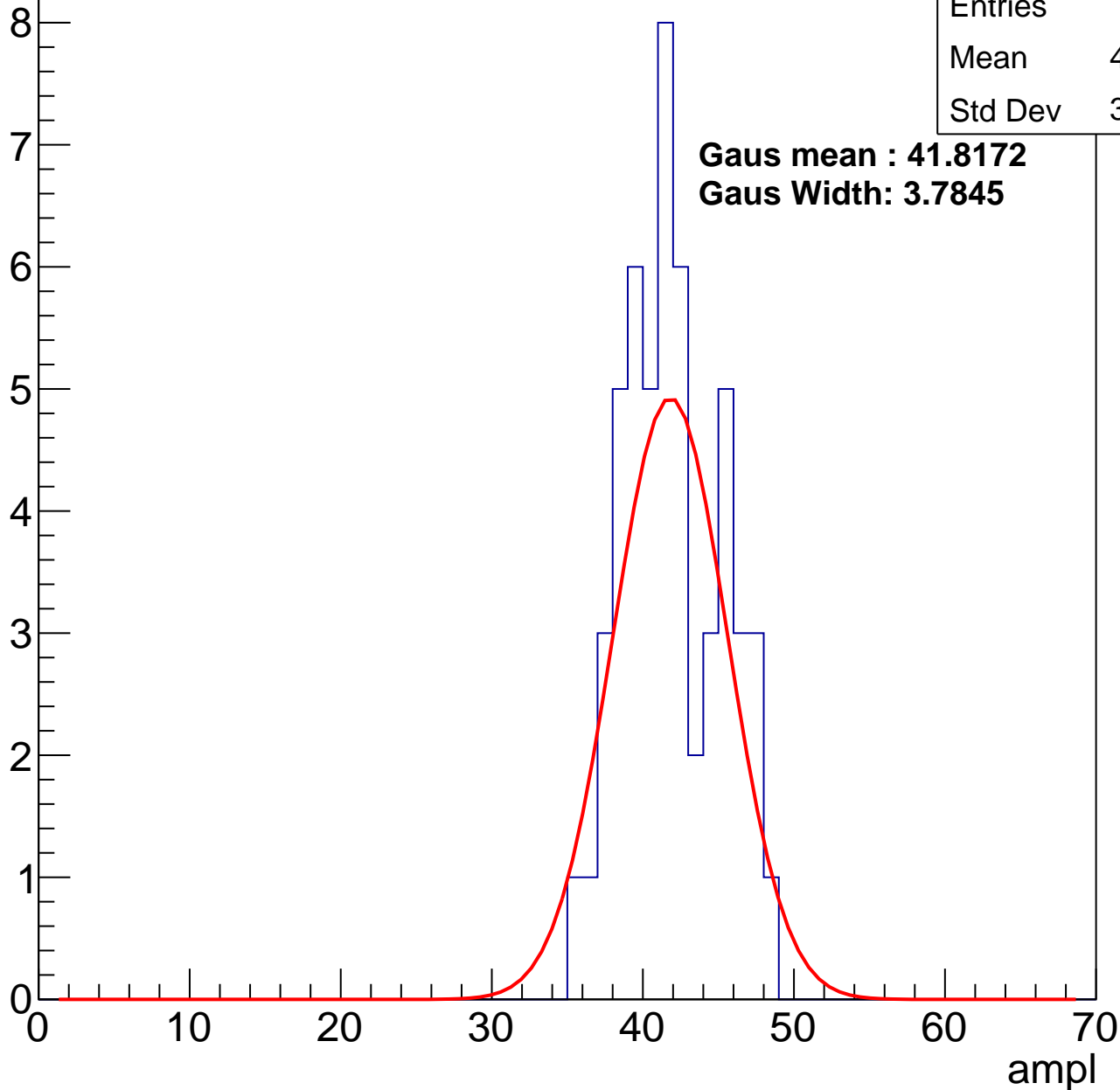
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	41.46
Std Dev	3.177

Gaus mean : 41.8172

Gaus Width: 3.7845

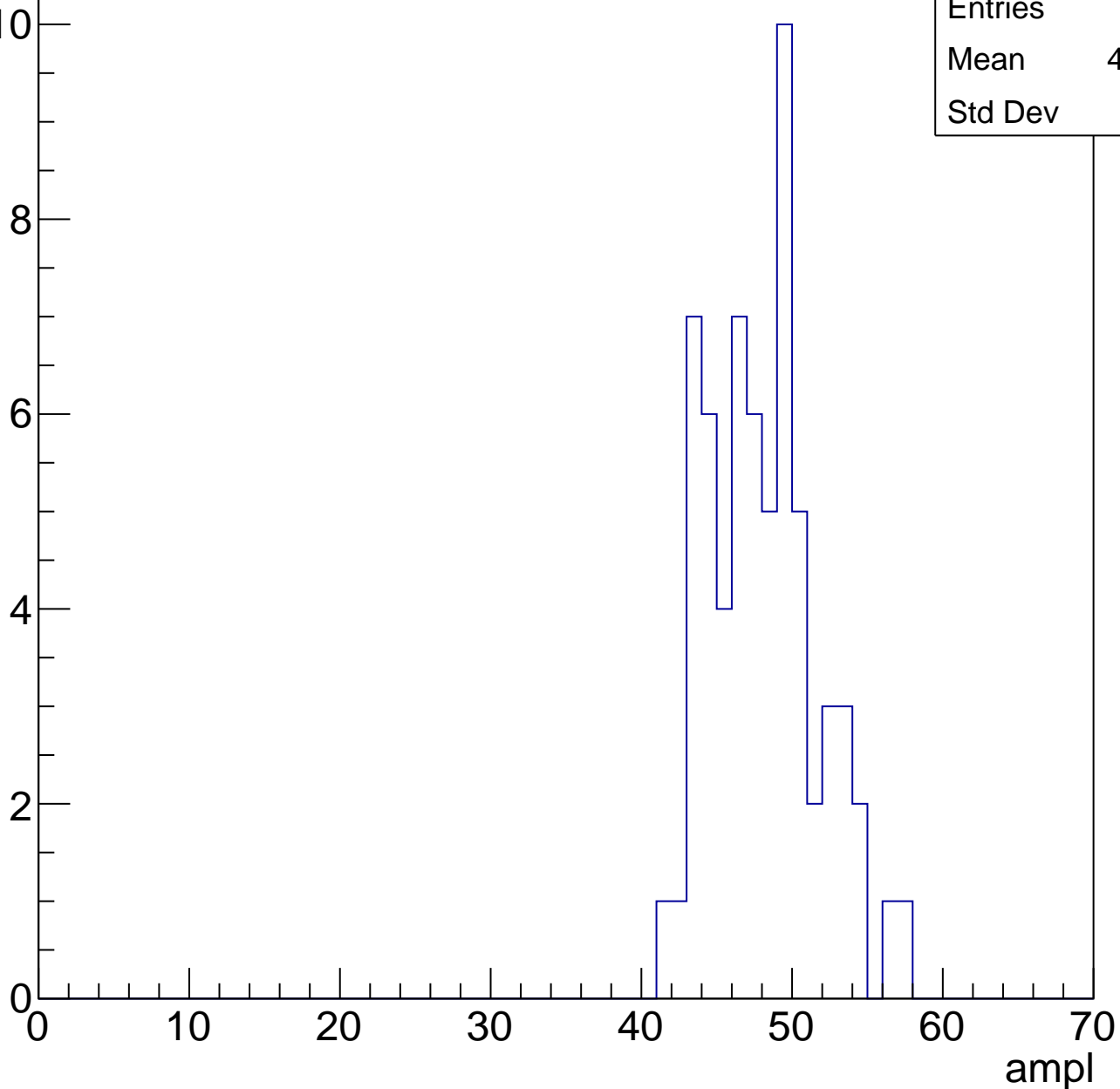


B1L003S, U19-ch98, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	47.66
Std Dev	3.55

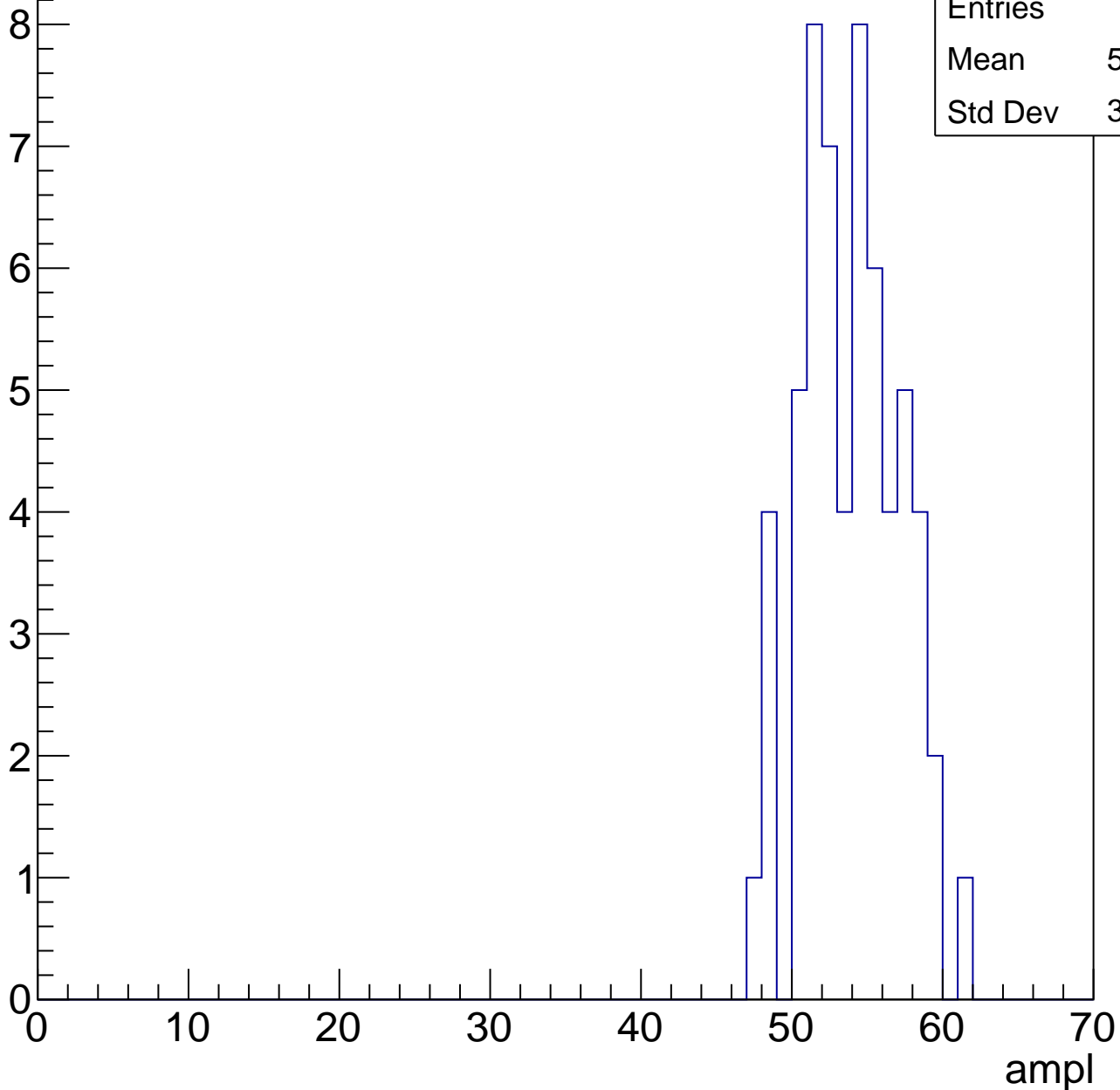


B1L003S, U19-ch98, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

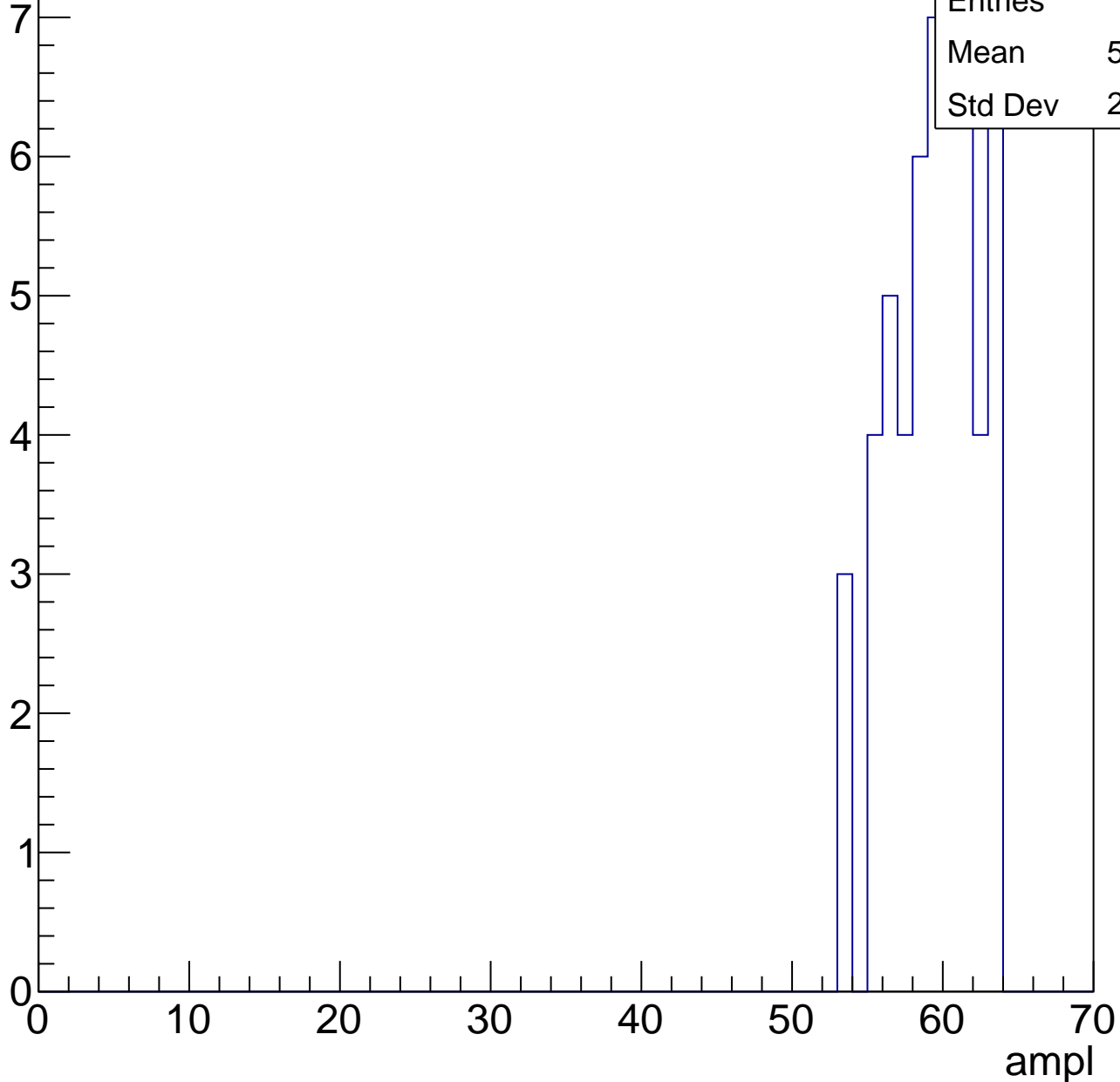
Entries	59
Mean	53.47
Std Dev	3.164



B1L003S, U19-ch98, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

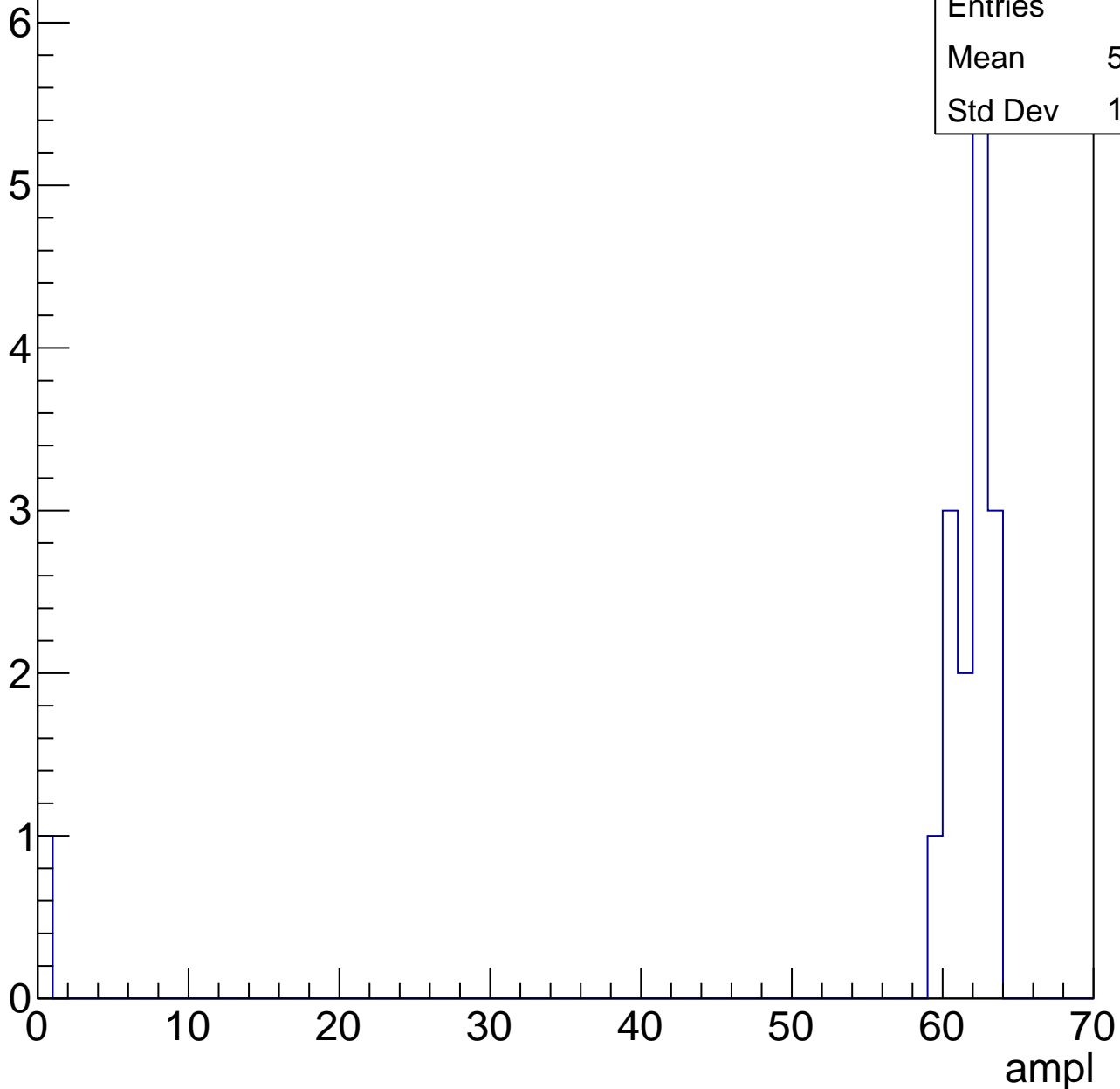


B1L003S, U19-ch98, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	57.62
Std Dev	14.92



B1L003S, U19-ch98, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch99, adc0

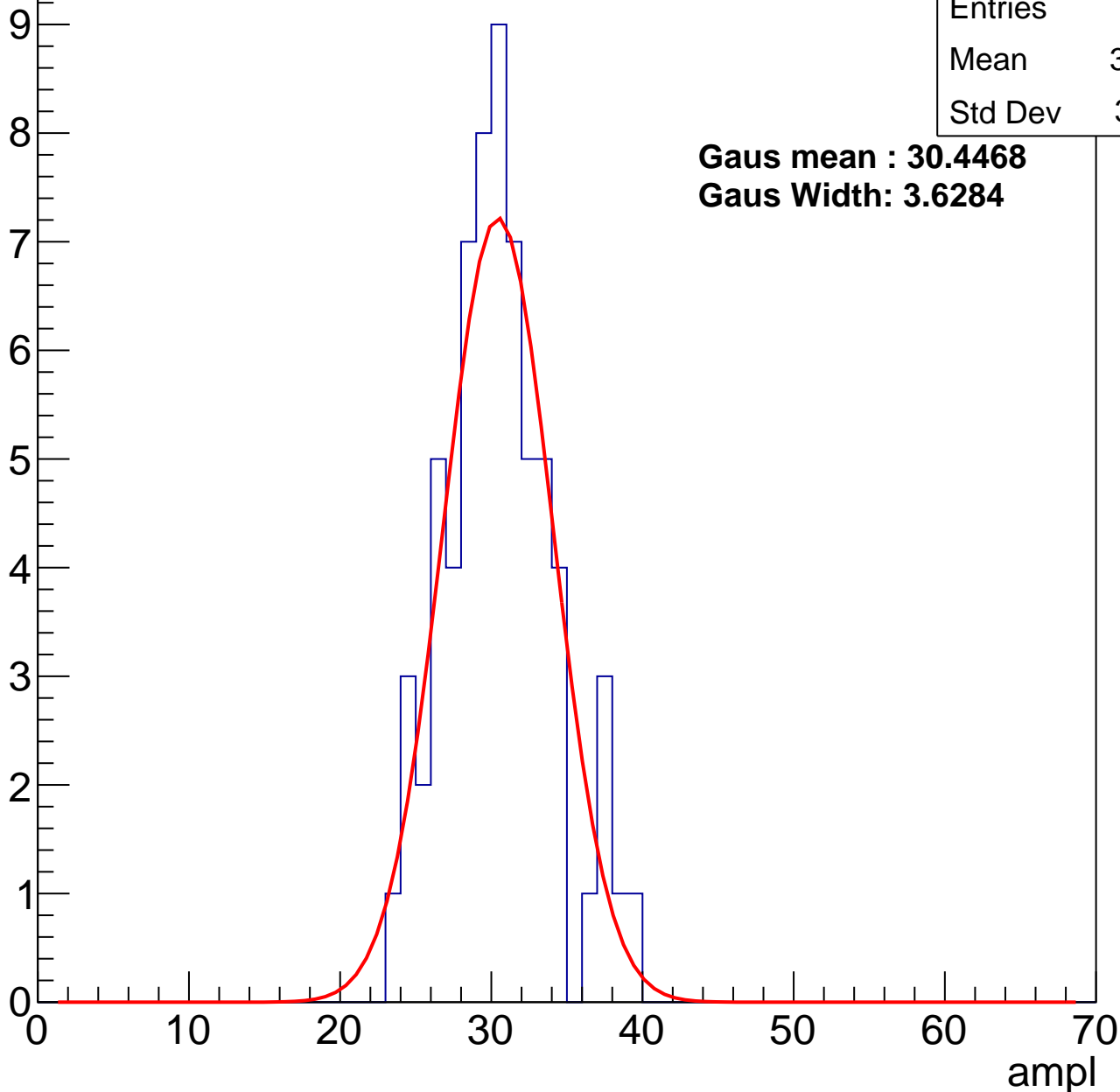
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	30.05
Std Dev	3.531

Gaus mean : 30.4468

Gaus Width: 3.6284



B1L003S, U19-ch99, adc1

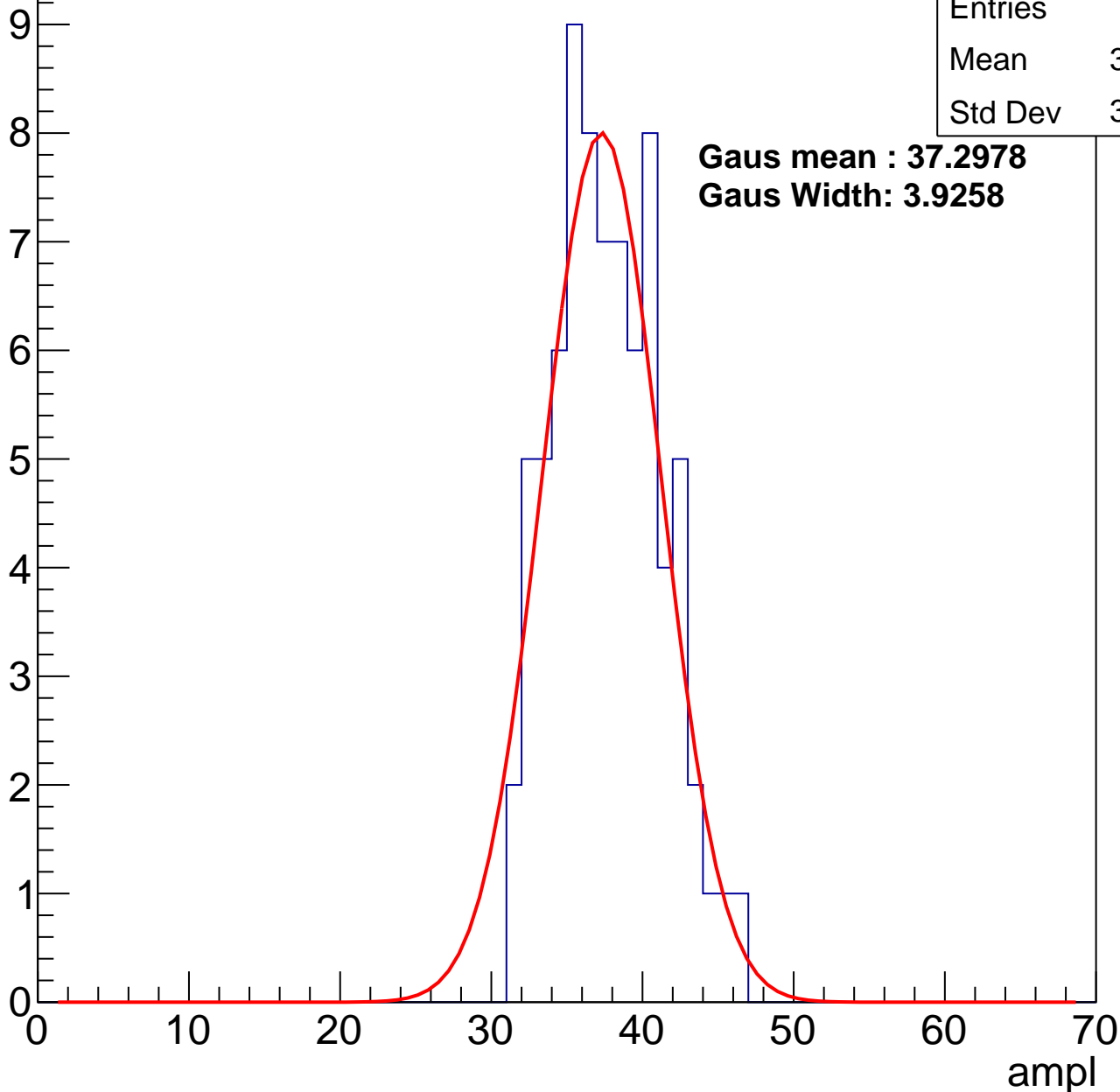
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	37.25
Std Dev	3.472

Gaus mean : 37.2978

Gaus Width: 3.9258



B1L003S, U19-ch99, adc2

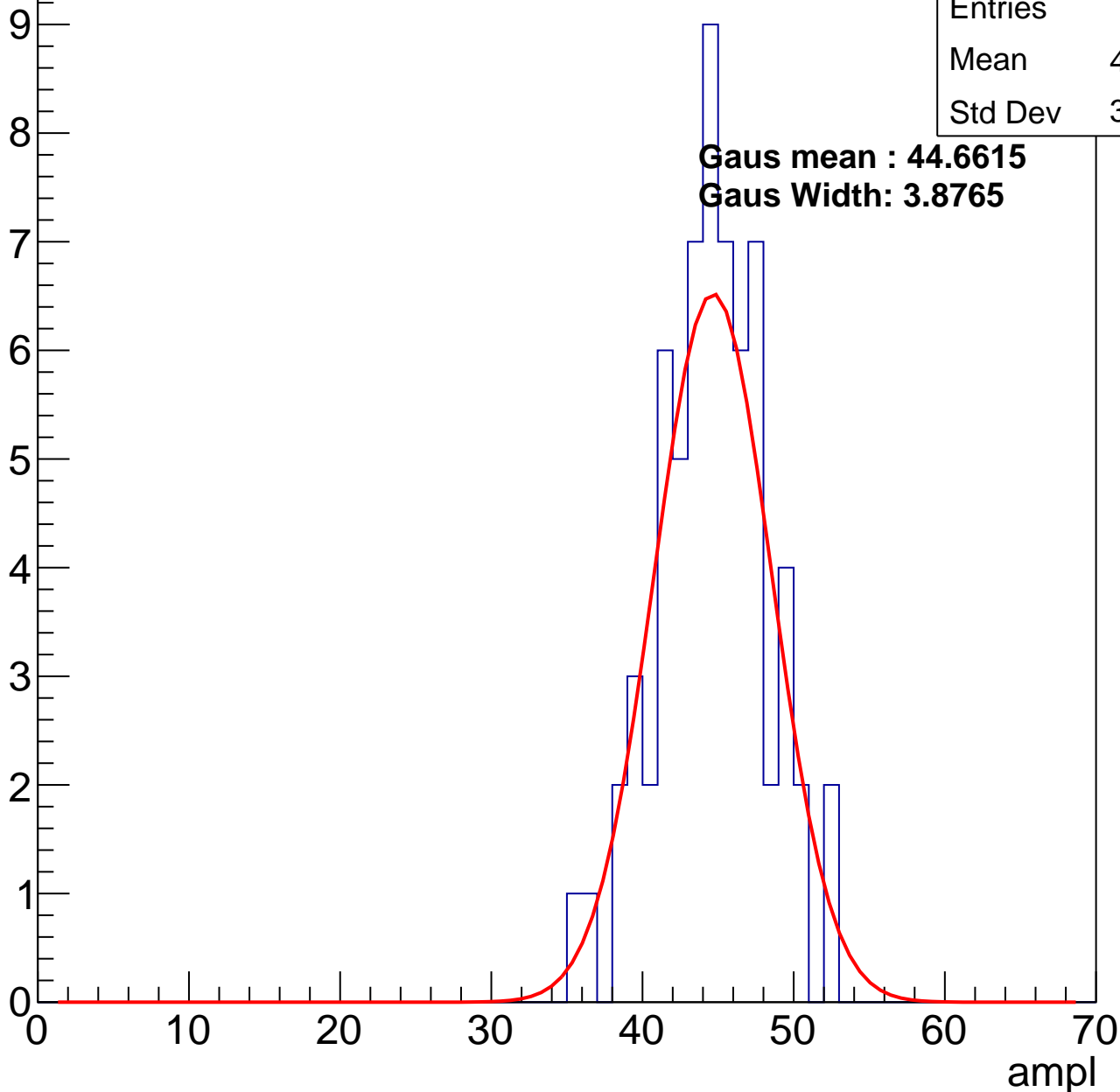
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	44.14
Std Dev	3.546

Gaus mean : 44.6615

Gaus Width: 3.8765

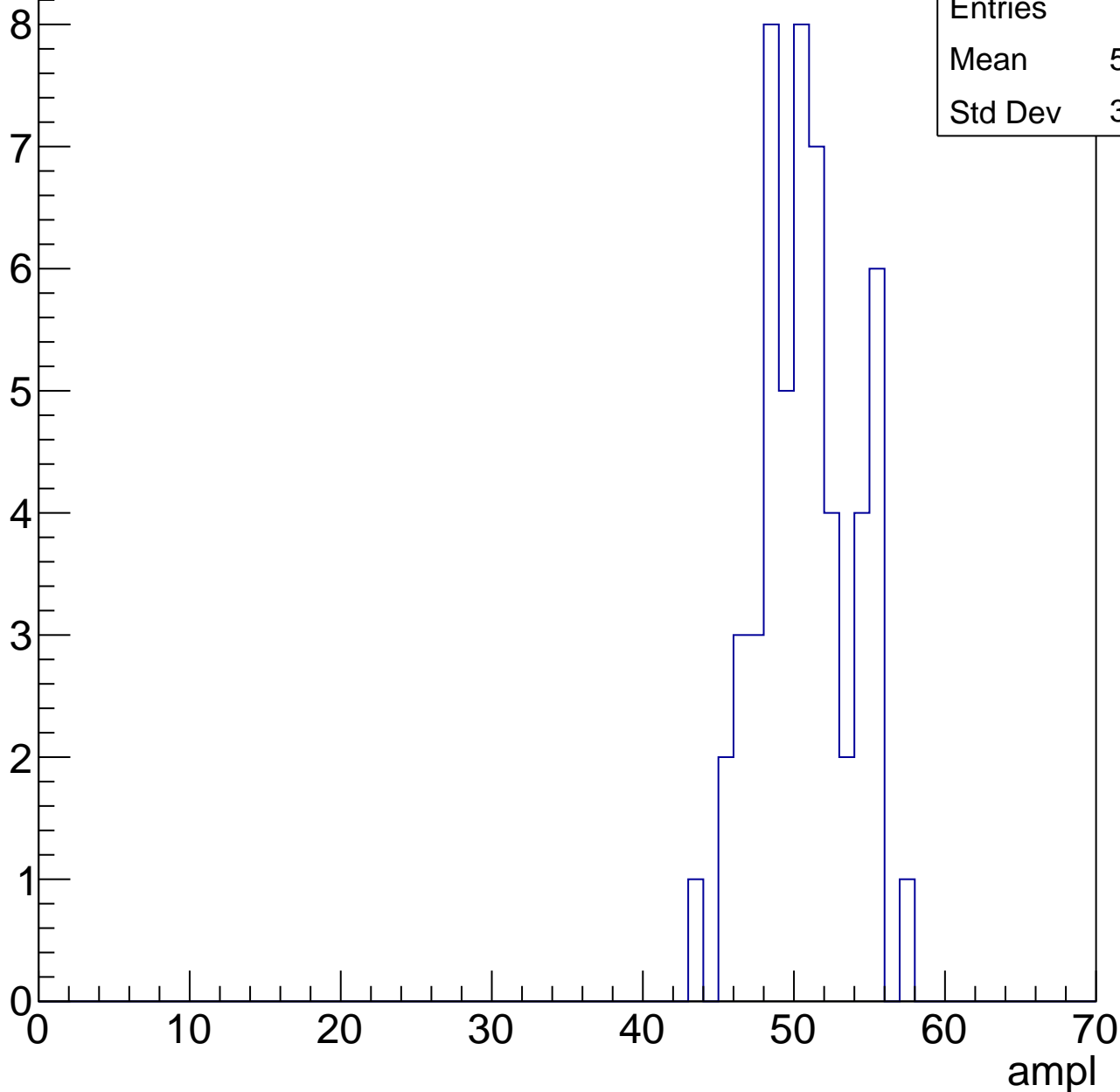


B1L003S, U19-ch99, adc3

calib_packv5_042523_0143.root, FC#13, port D2

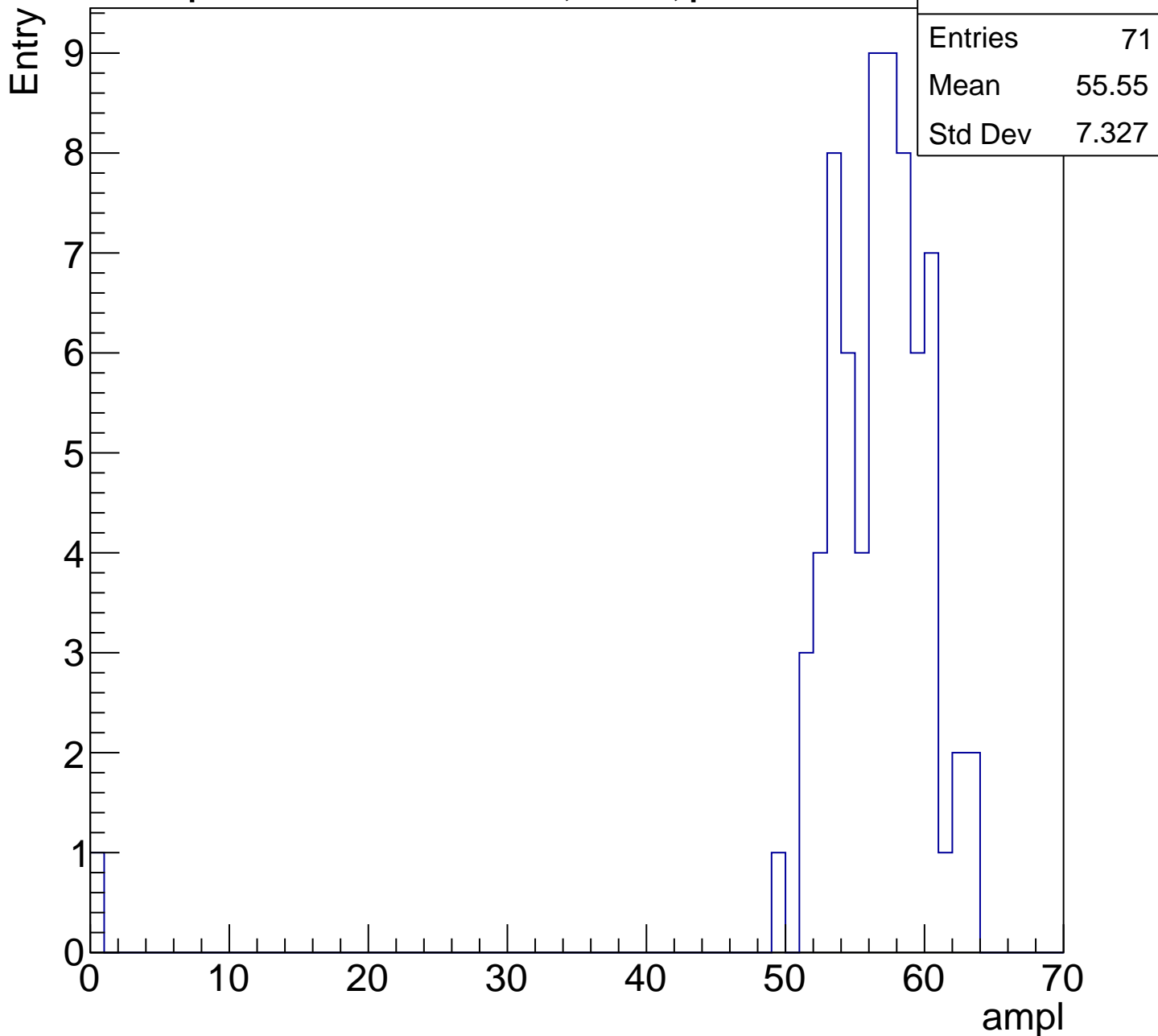
Entry

Entries	54
Mean	50.28
Std Dev	3.076



B1L003S, U19-ch99, adc4

calib_packv5_042523_0143.root, FC#13, port D2

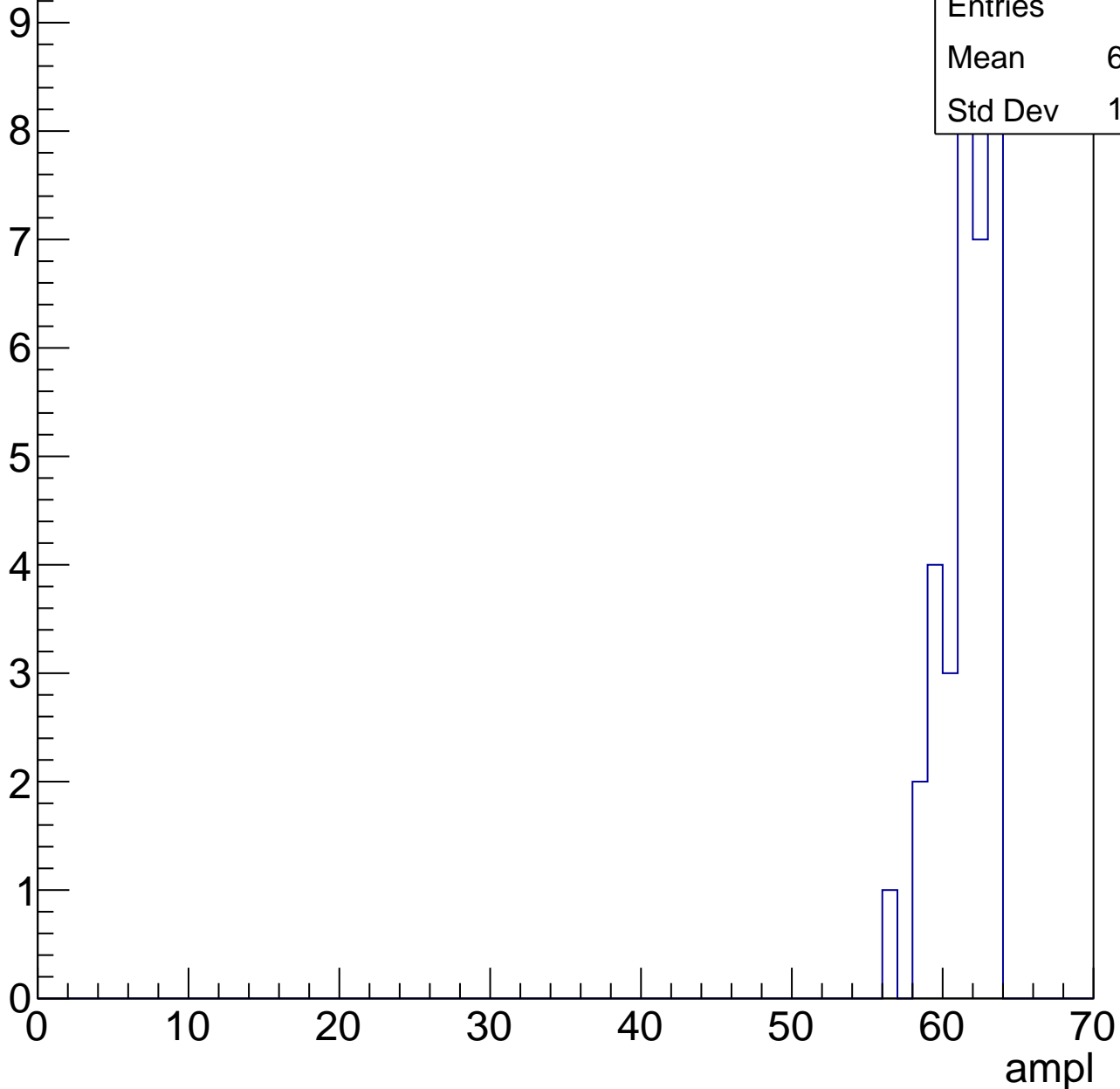


B1L003S, U19-ch99, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	61.03
Std Dev	1.723



B1L003S, U19-ch99, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch99, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch100, adc0

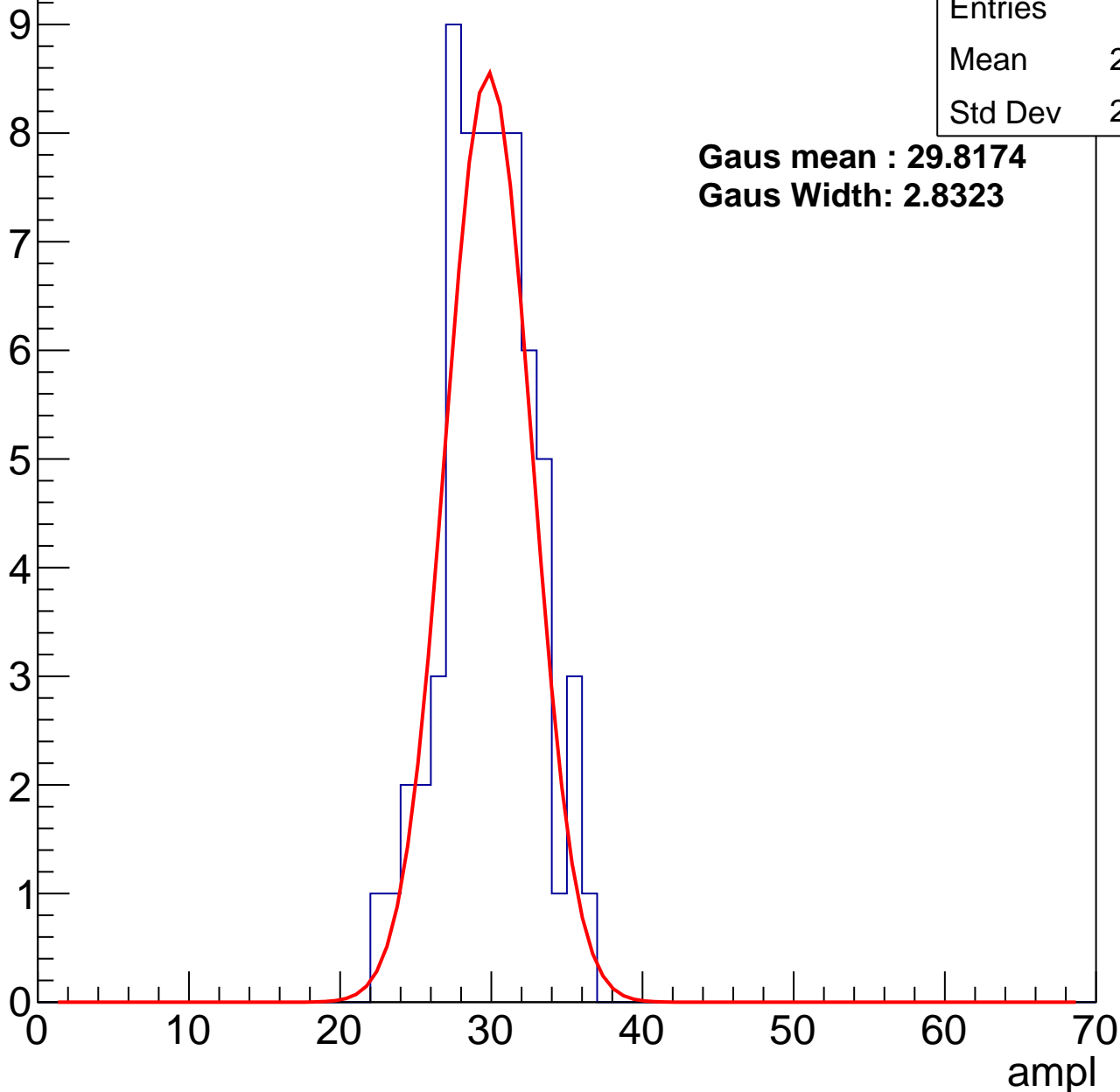
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.39
Std Dev	2.974

Gaus mean : 29.8174

Gaus Width: 2.8323



B1L003S, U19-ch100, adc1

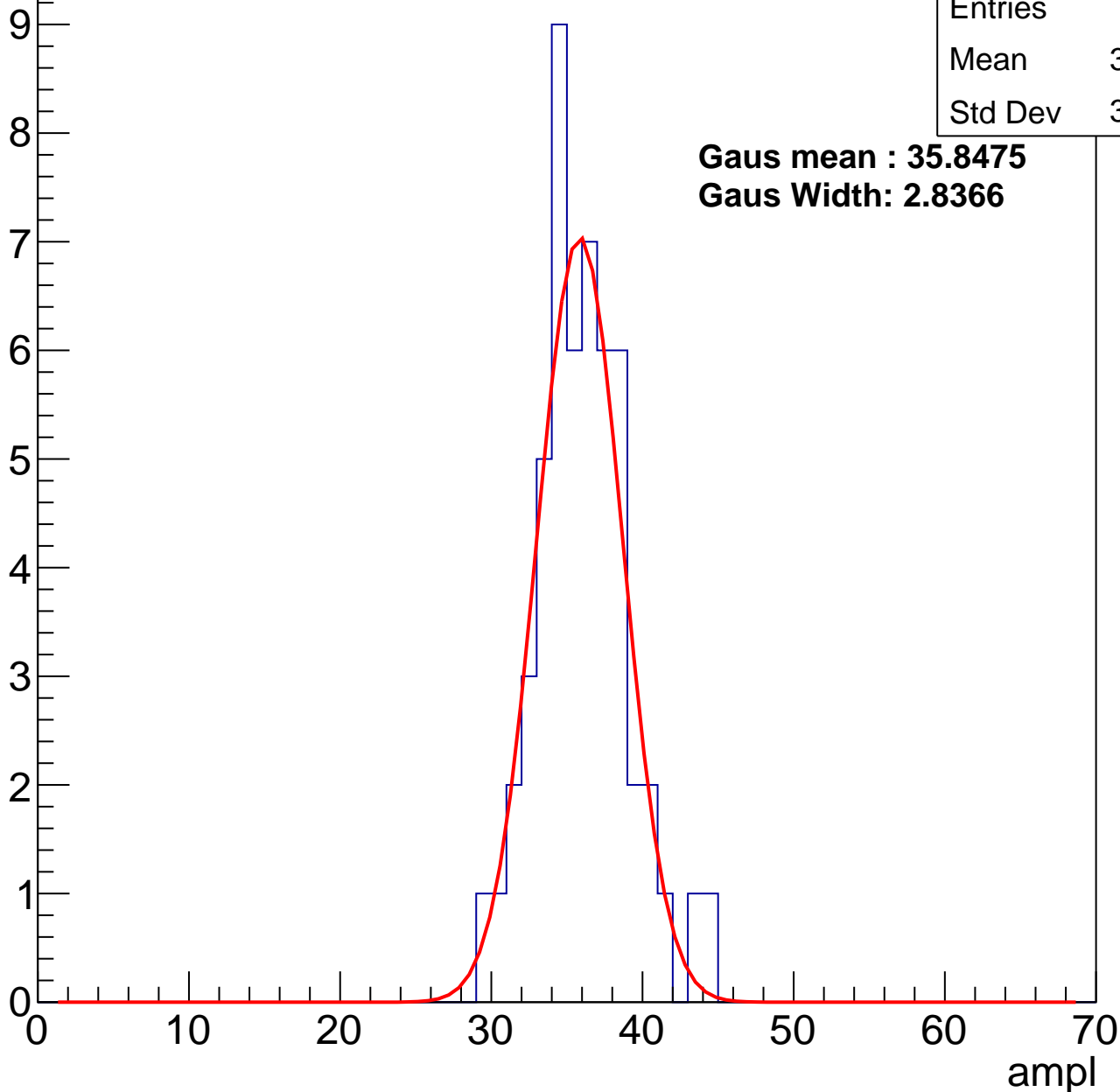
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.58
Std Dev	3.006

Gaus mean : 35.8475

Gaus Width: 2.8366



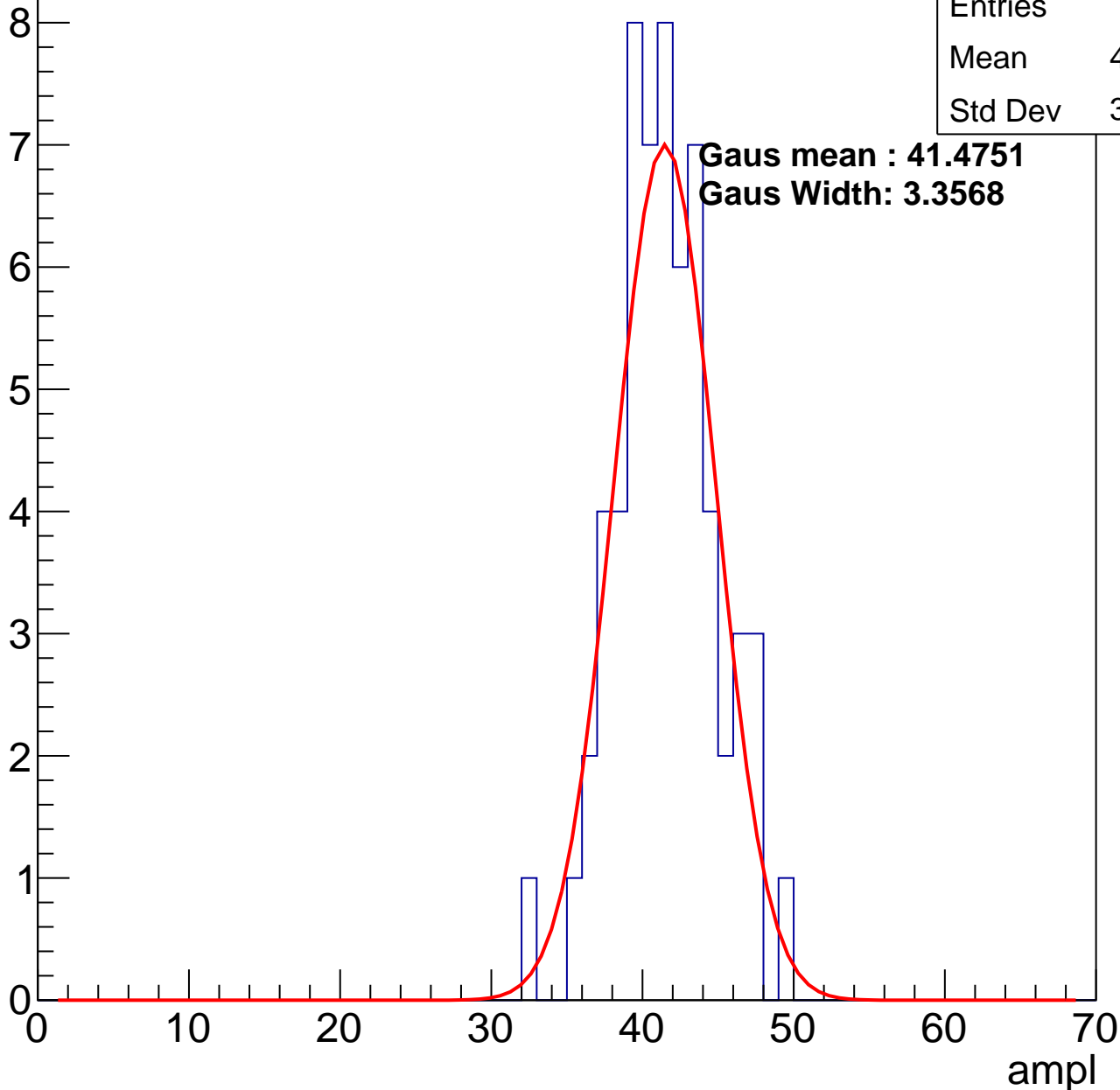
B1L003S, U19-ch100, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	41.08
Std Dev	3.276

Gaus mean : 41.4751
Gaus Width: 3.3568

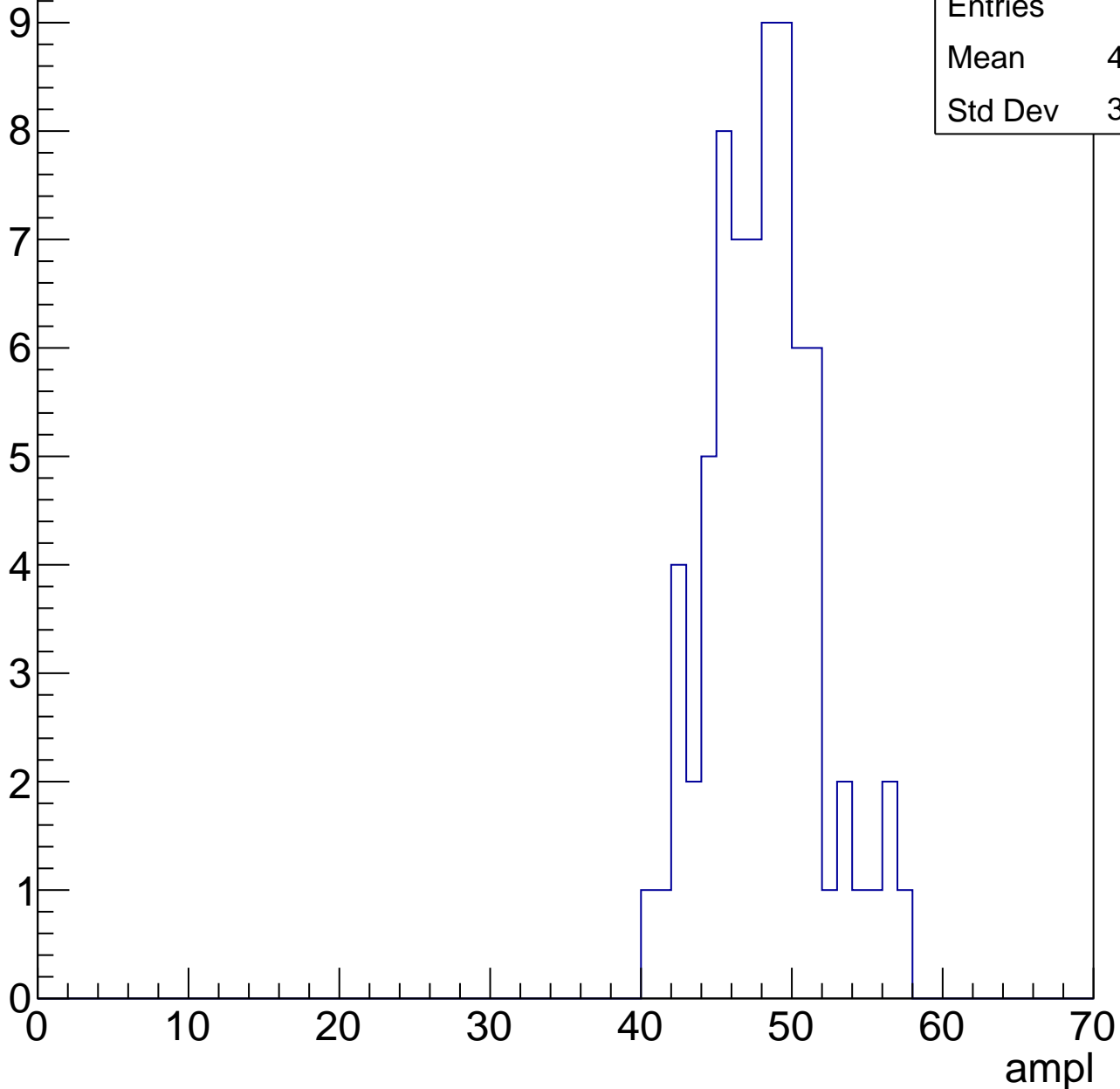


B1L003S, U19-ch100, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	47.68
Std Dev	3.576

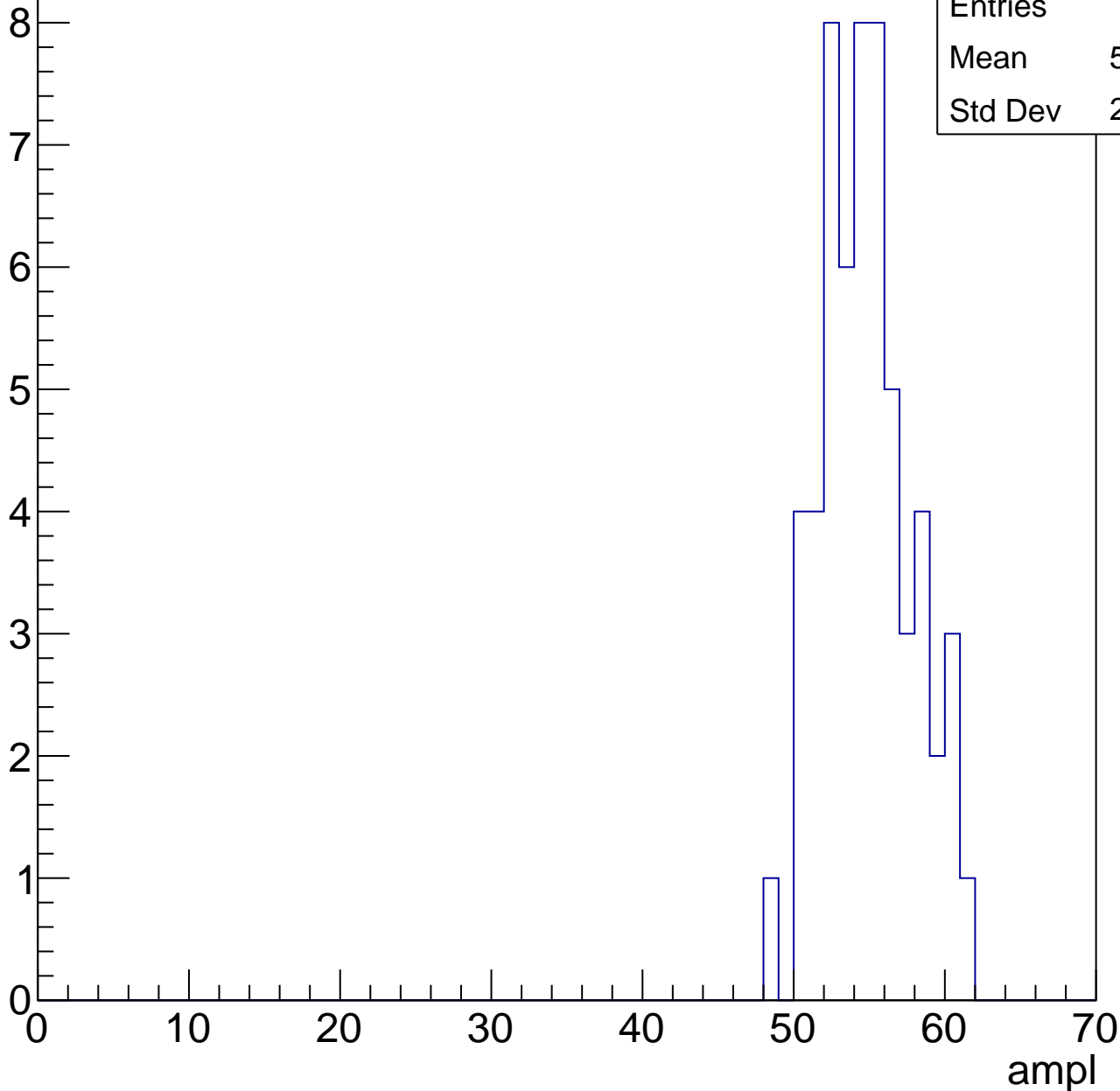


B1L003S, U19-ch100, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	54.39
Std Dev	2.936

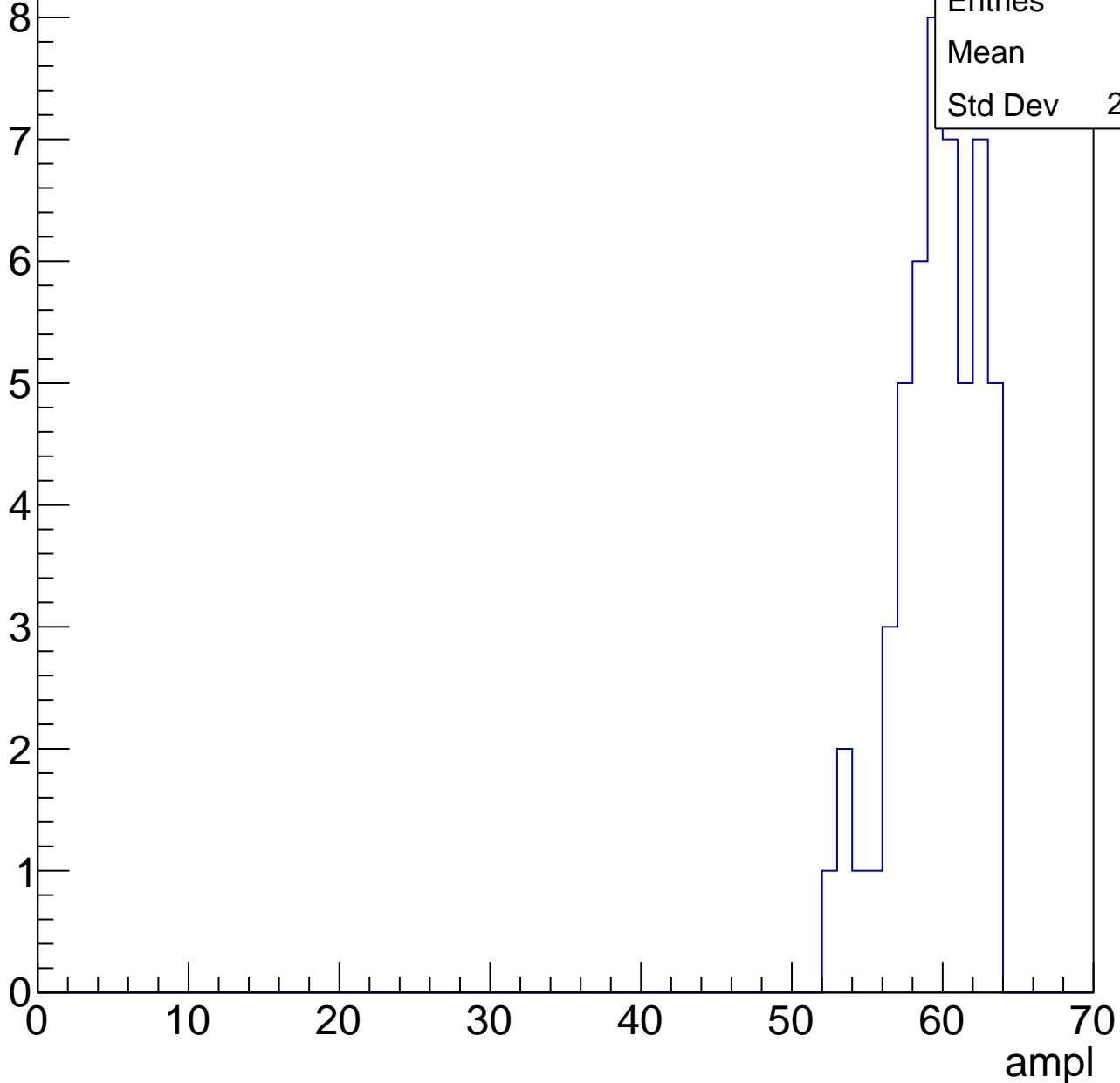


B1L003S, U19-ch100, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

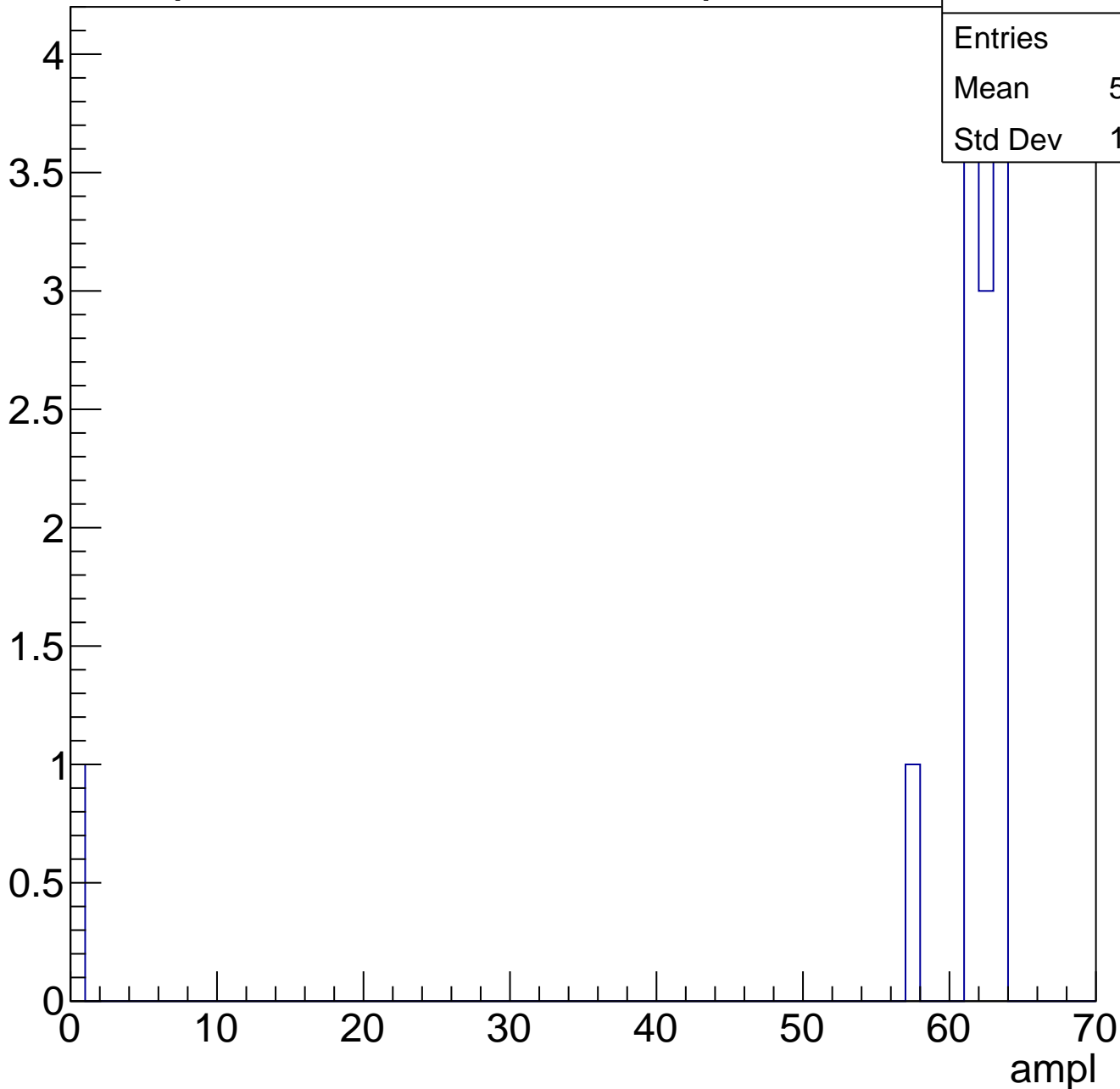
Entries	51
Mean	59.1
Std Dev	2.746



B1L003S, U19-ch100, adc6

calib_packv5_042523_0143.root, FC#13, port D2

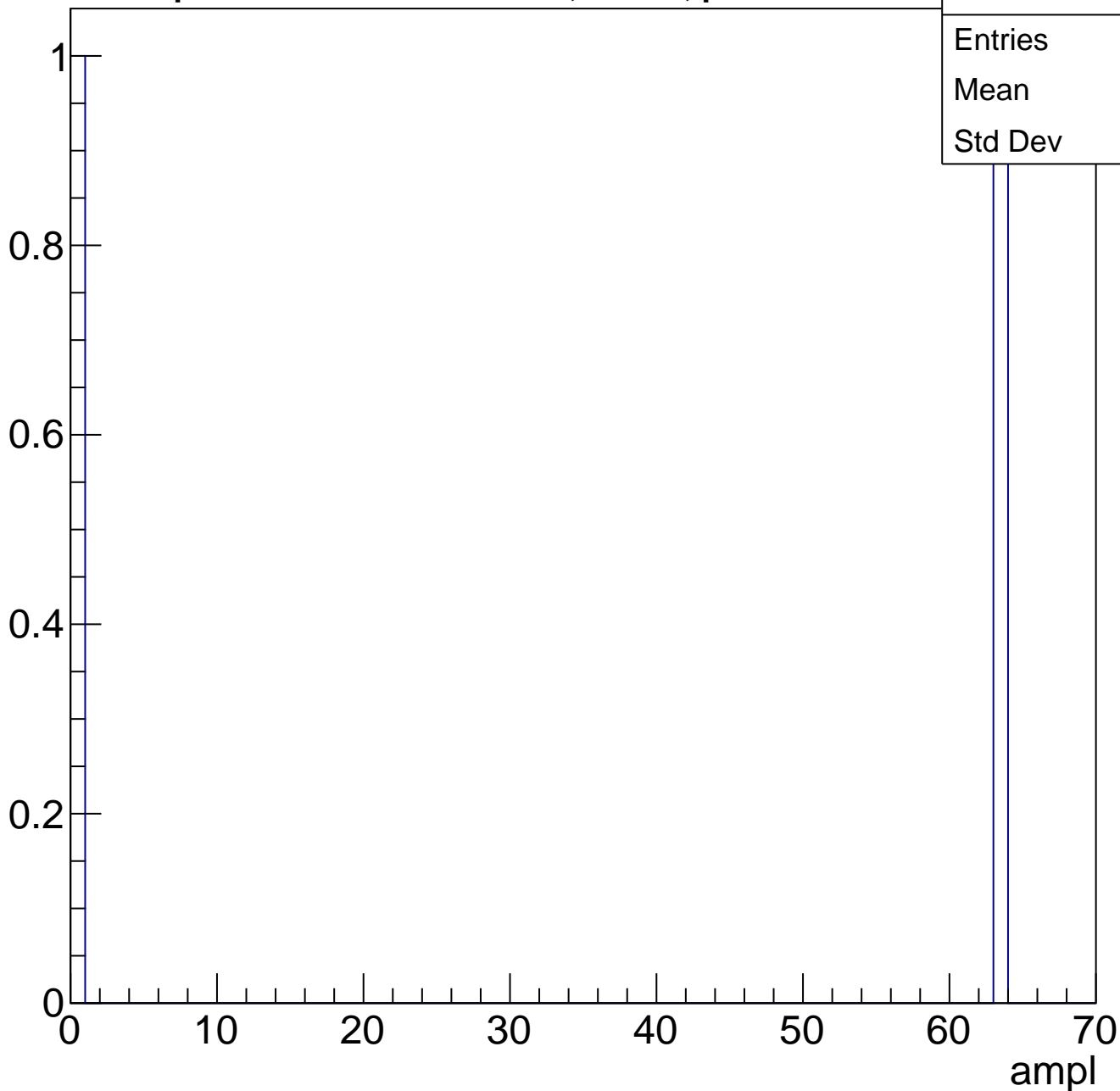
Entry



B1L003S, U19-ch100, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch101, adc0

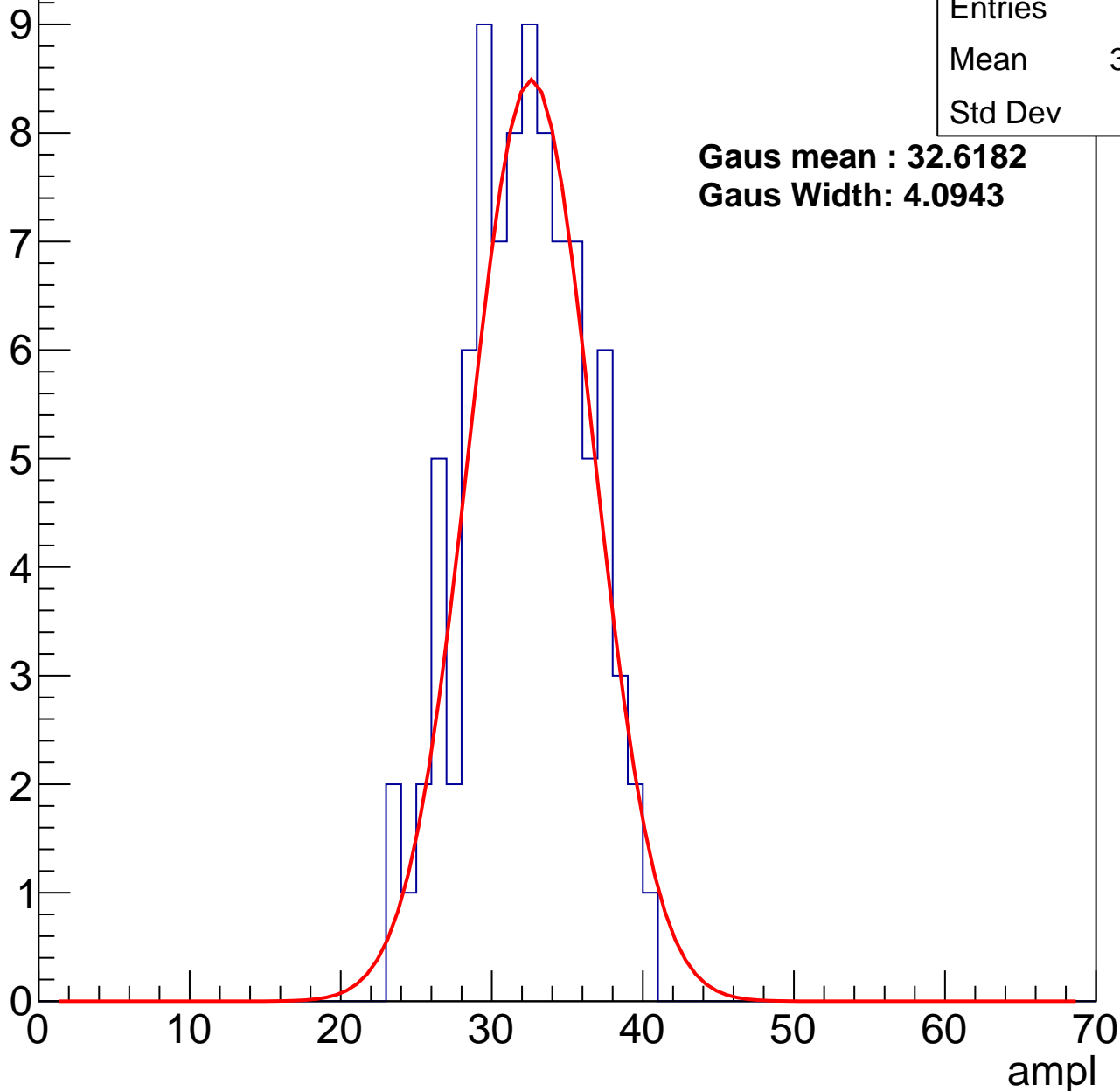
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	90
Mean	31.78
Std Dev	3.89

Gaus mean : 32.6182

Gaus Width: 4.0943



B1L003S, U19-ch101, adc1

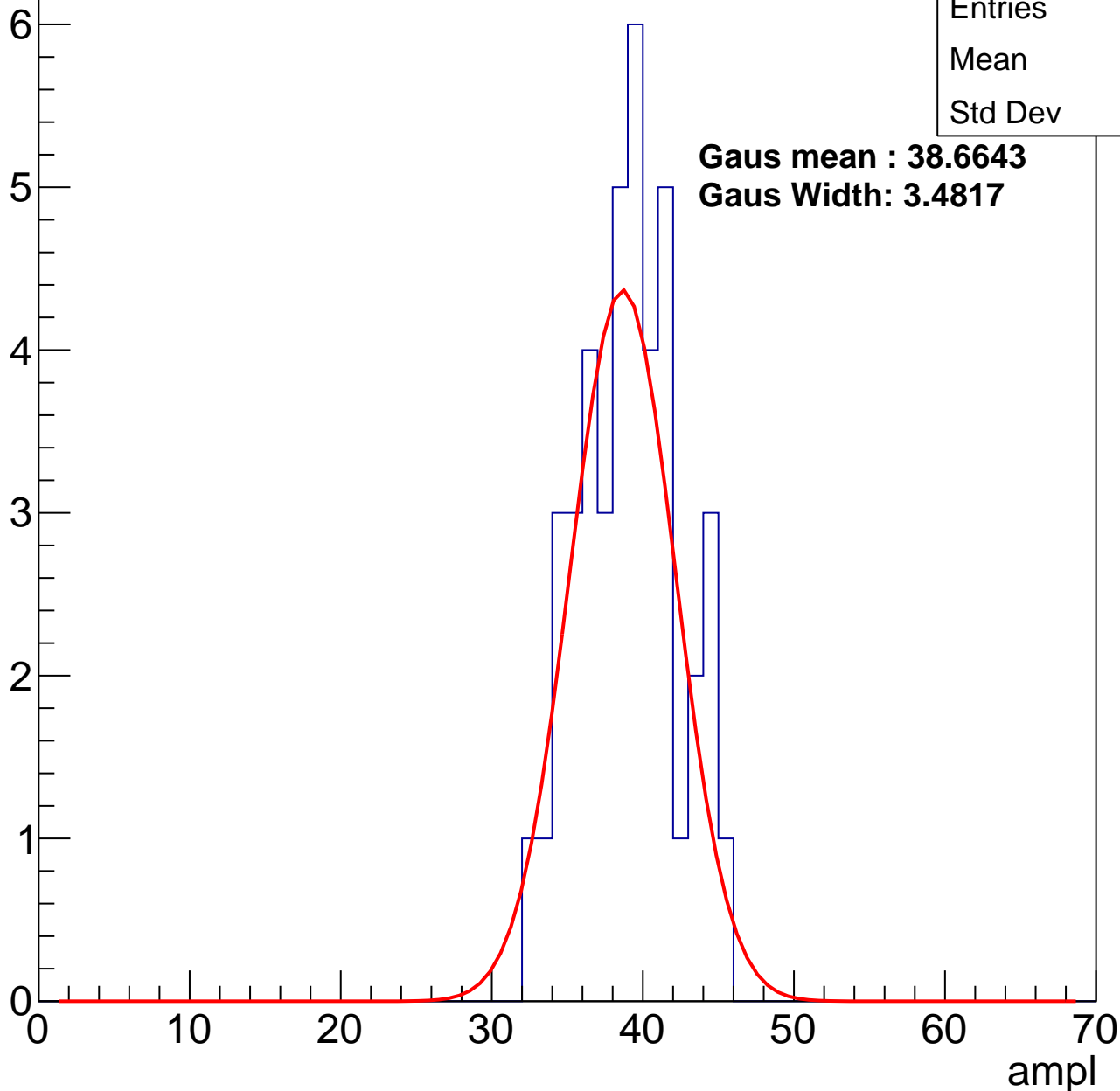
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	38.6
Std Dev	3.2

Gaus mean : 38.6643

Gaus Width: 3.4817



B1L003S, U19-ch101, adc2

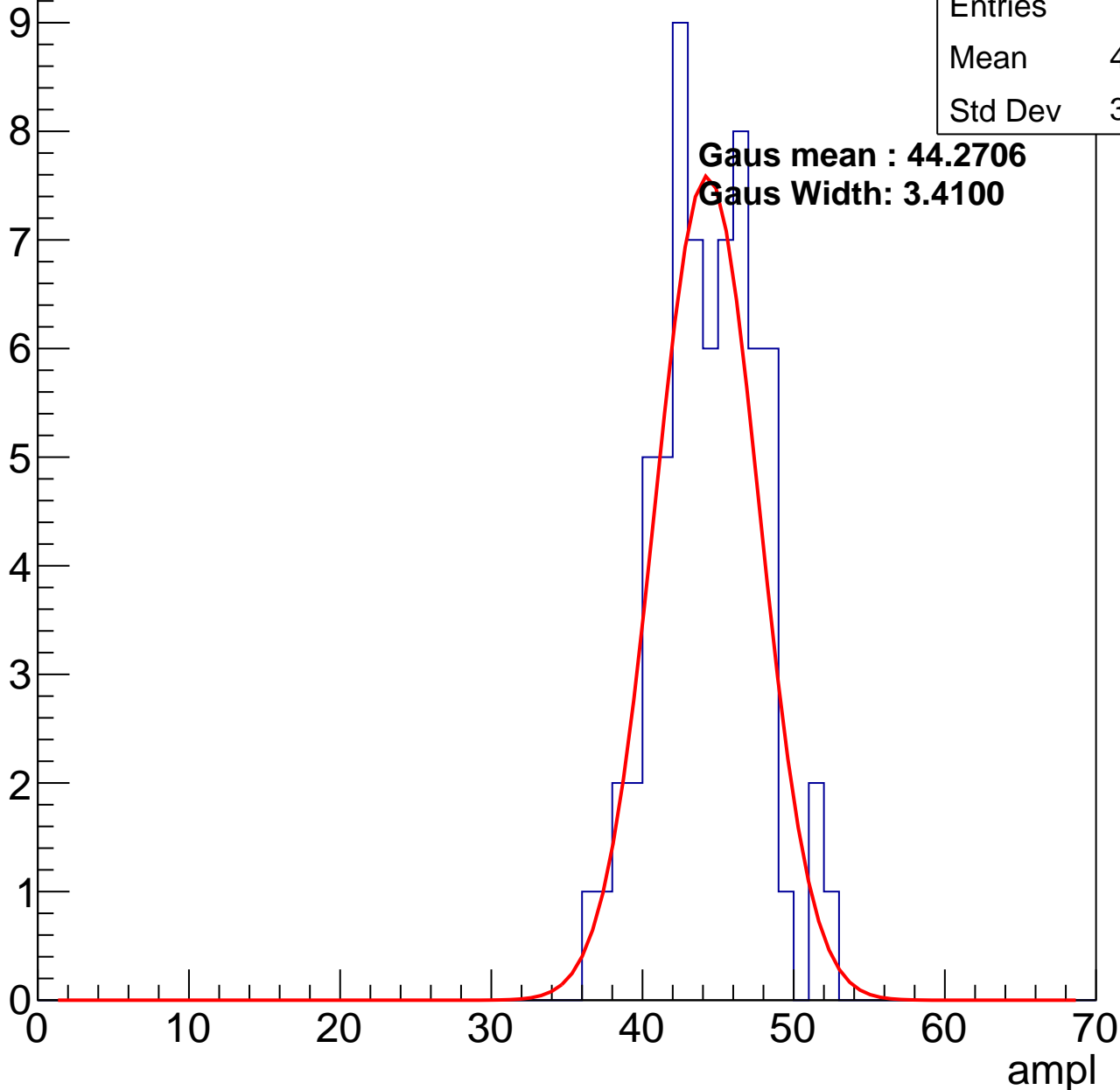
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.93
Std Dev	3.359

Gaus mean : 44.2706

Gaus Width: 3.4100

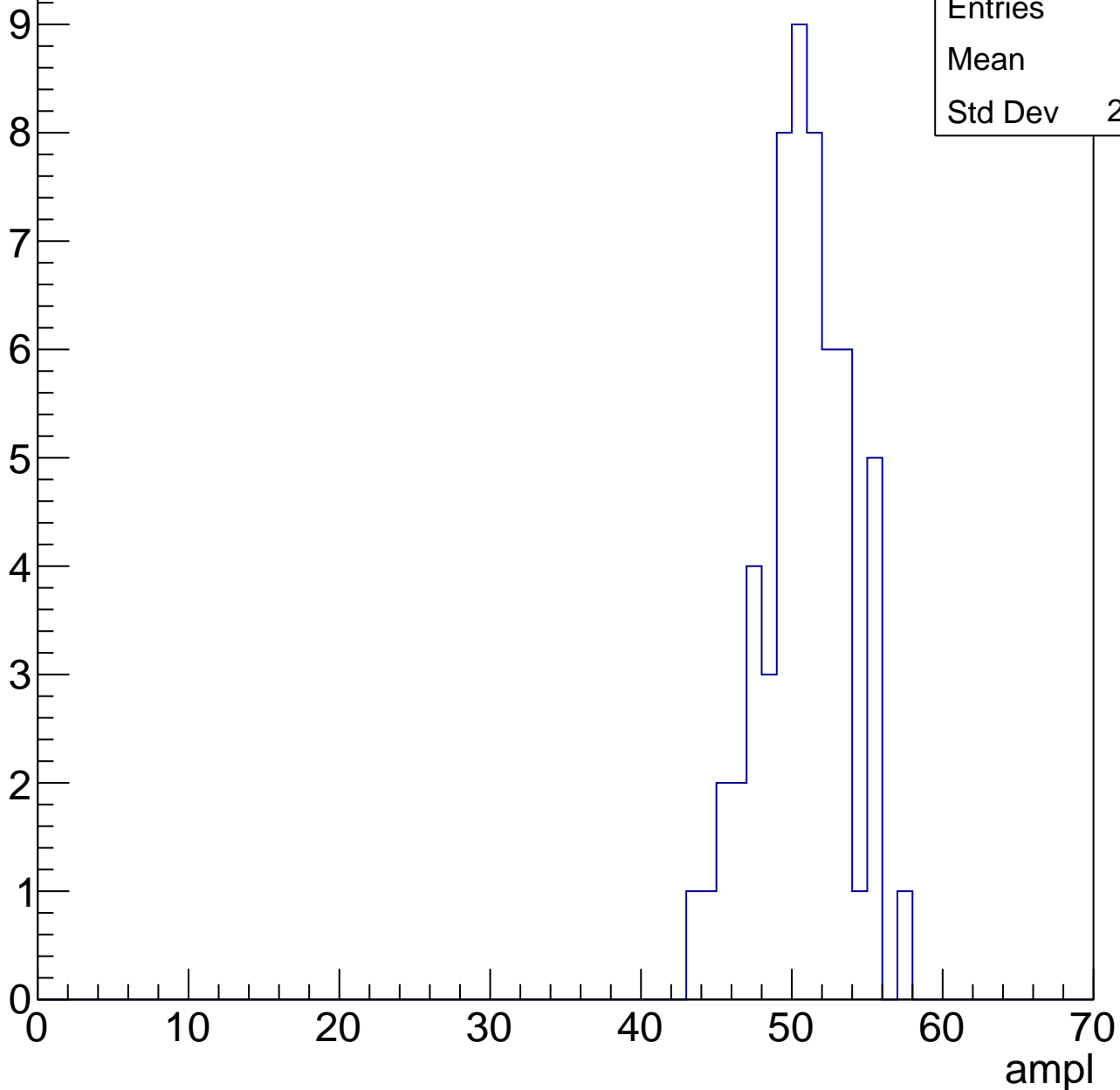


B1L003S, U19-ch101, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	50.3
Std Dev	2.944

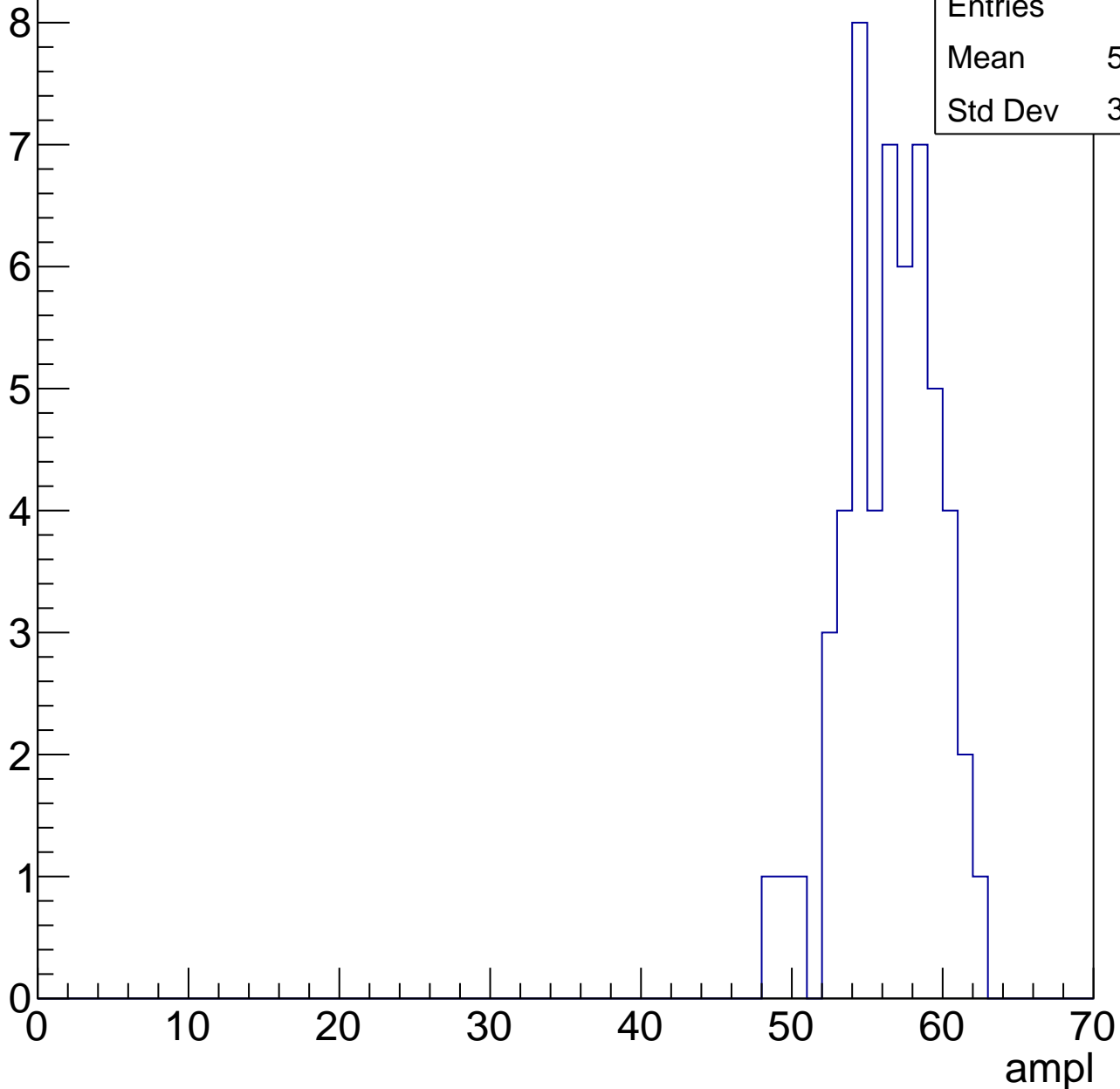


B1L003S, U19-ch101, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	56.04
Std Dev	3.037

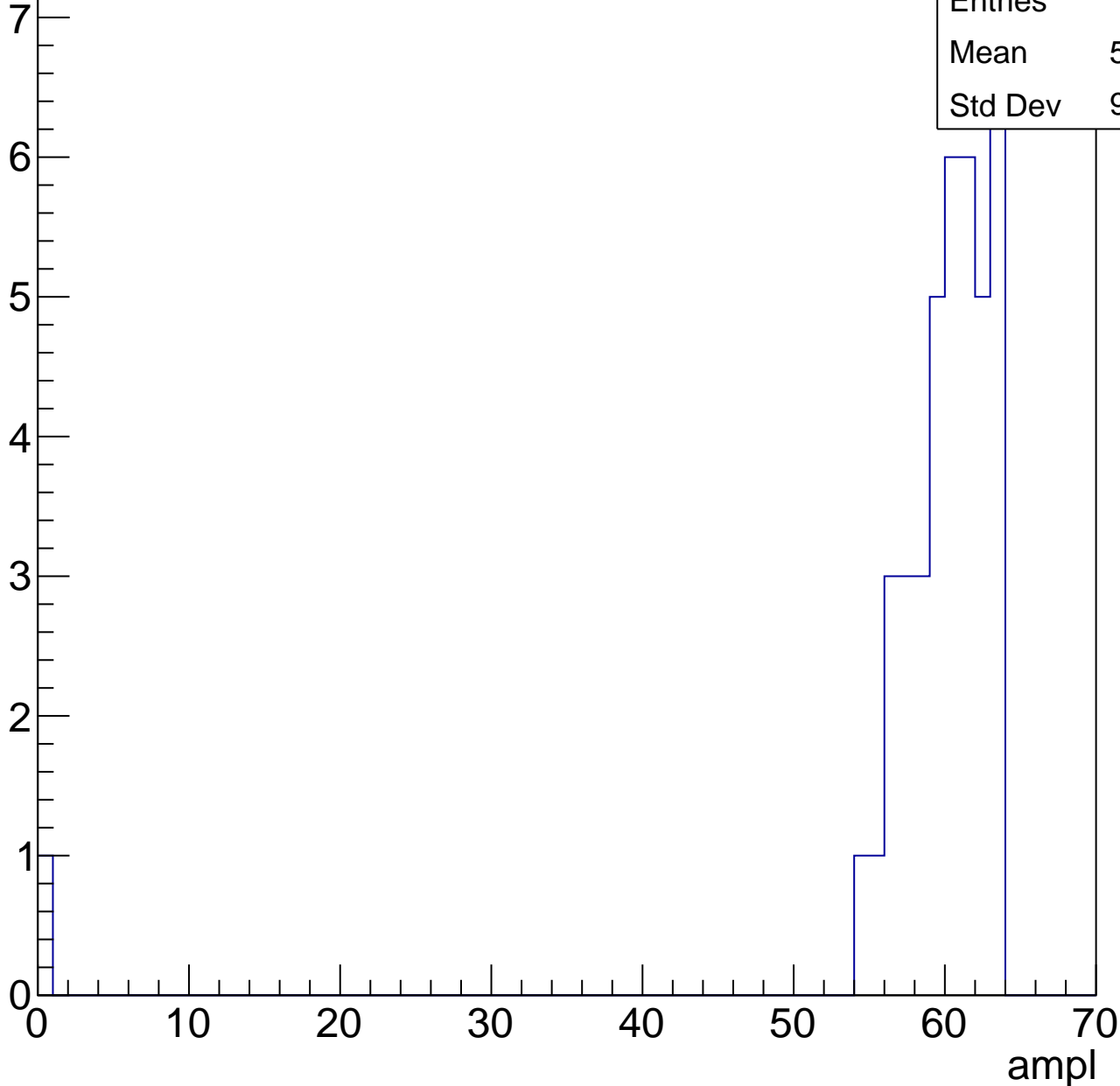


B1L003S, U19-ch101, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

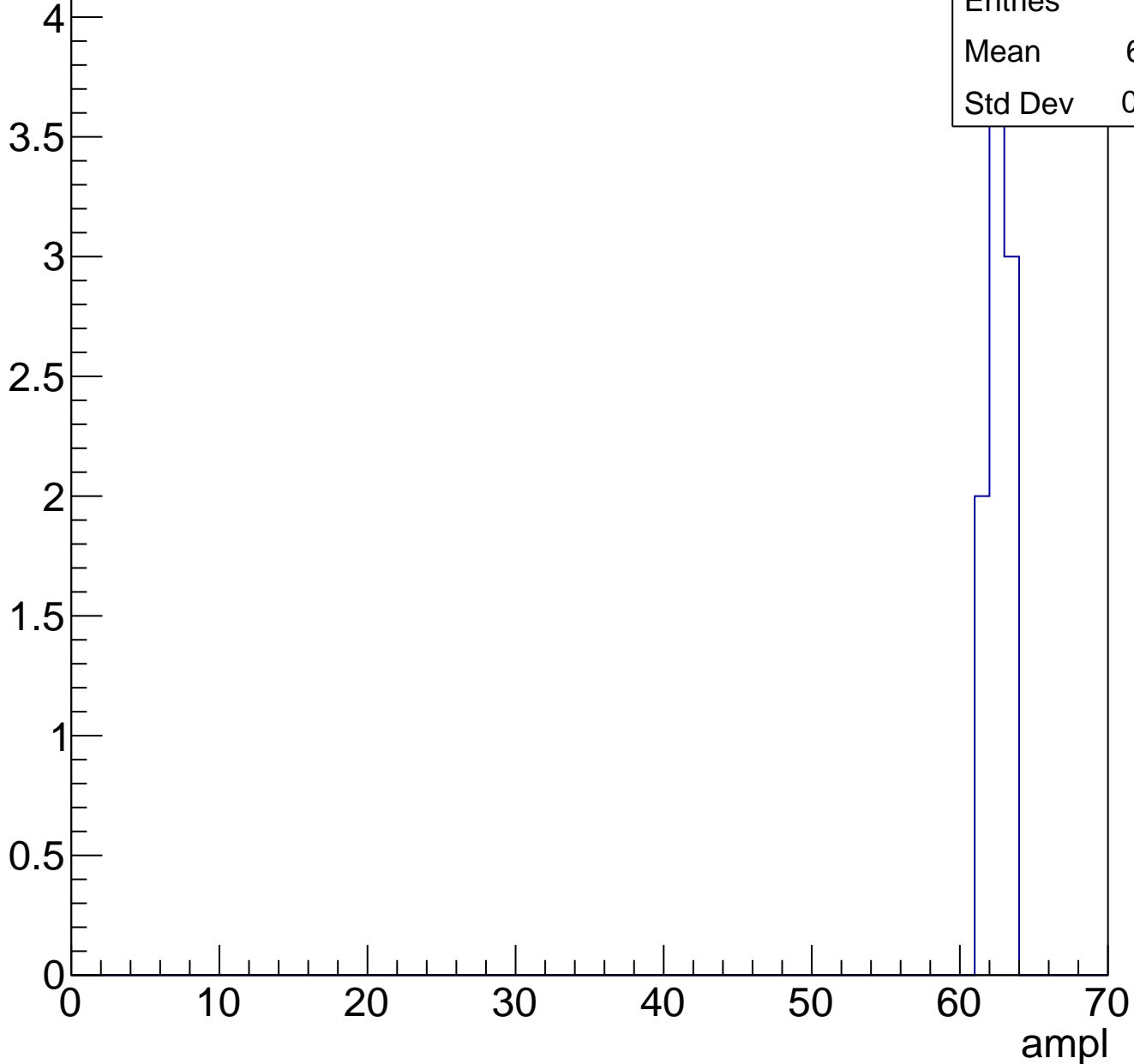
Entries	41
Mean	58.39
Std Dev	9.545



B1L003S, U19-ch101, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	9
Mean	62.11
Std Dev	0.737

B1L003S, U19-ch101, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch102, adc0

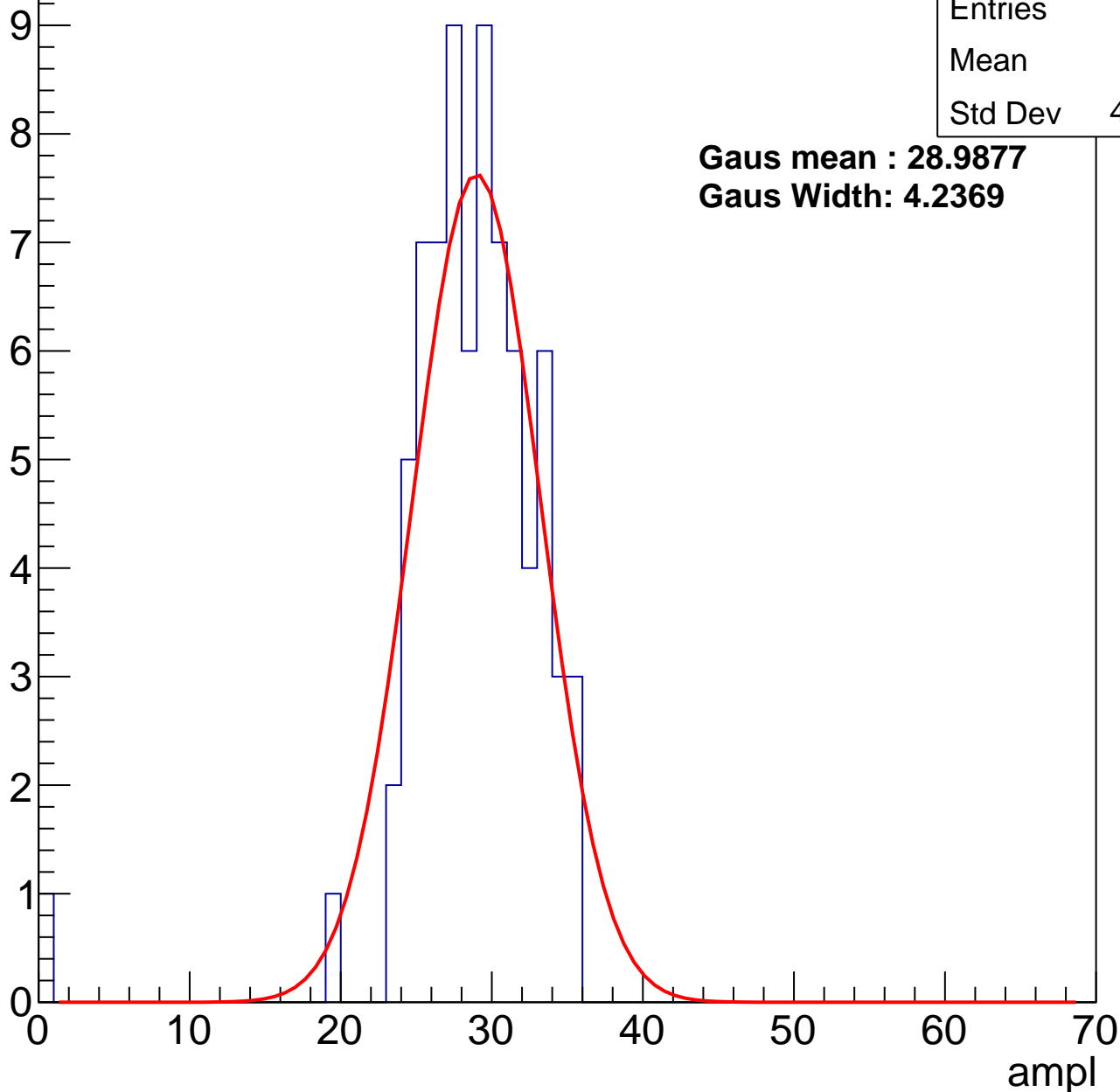
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	28.2
Std Dev	4.662

Gaus mean : 28.9877

Gaus Width: 4.2369



B1L003S, U19-ch102, adc1

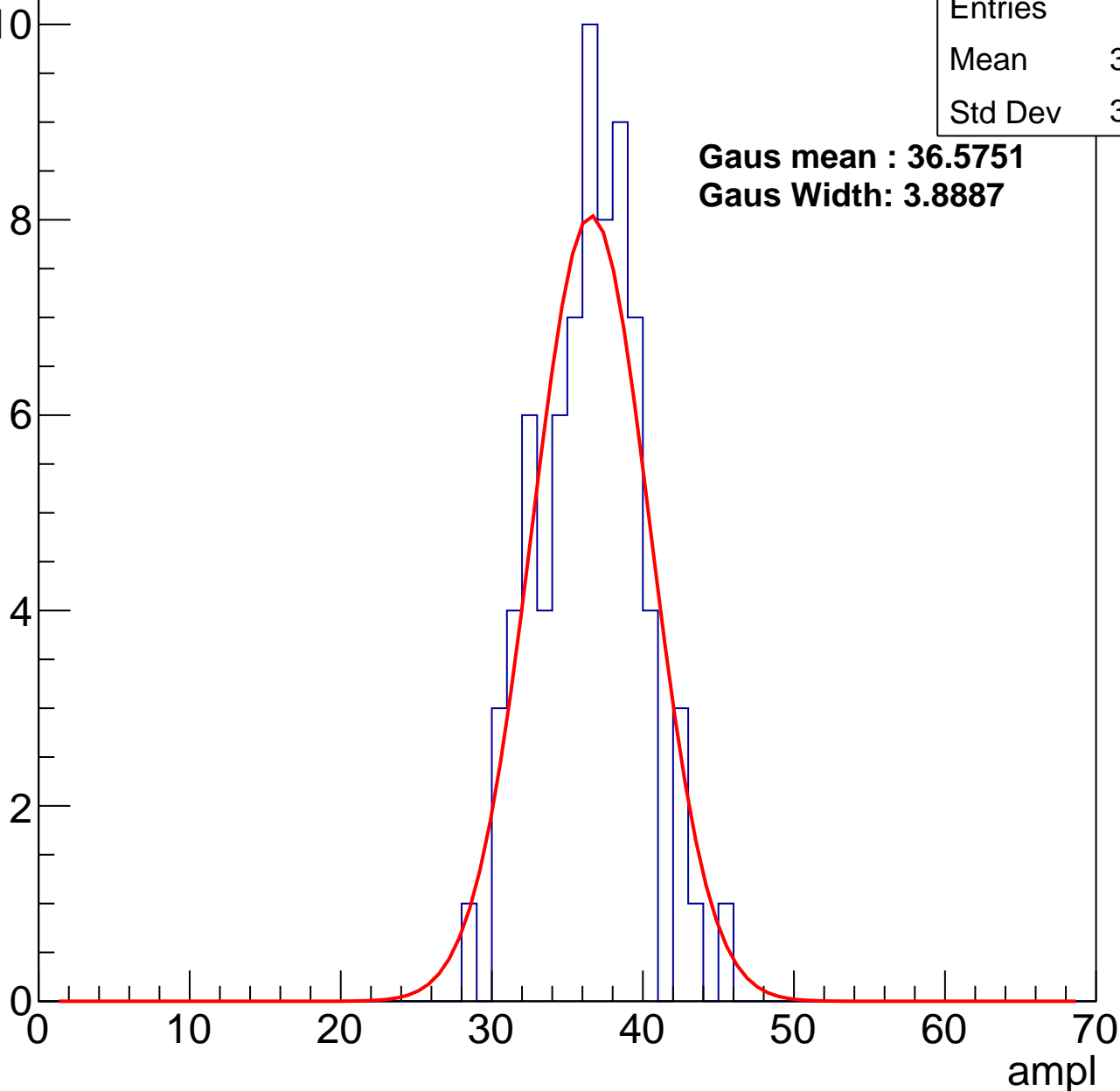
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	35.95
Std Dev	3.377

Gaus mean : 36.5751

Gaus Width: 3.8887



B1L003S, U19-ch102, adc2

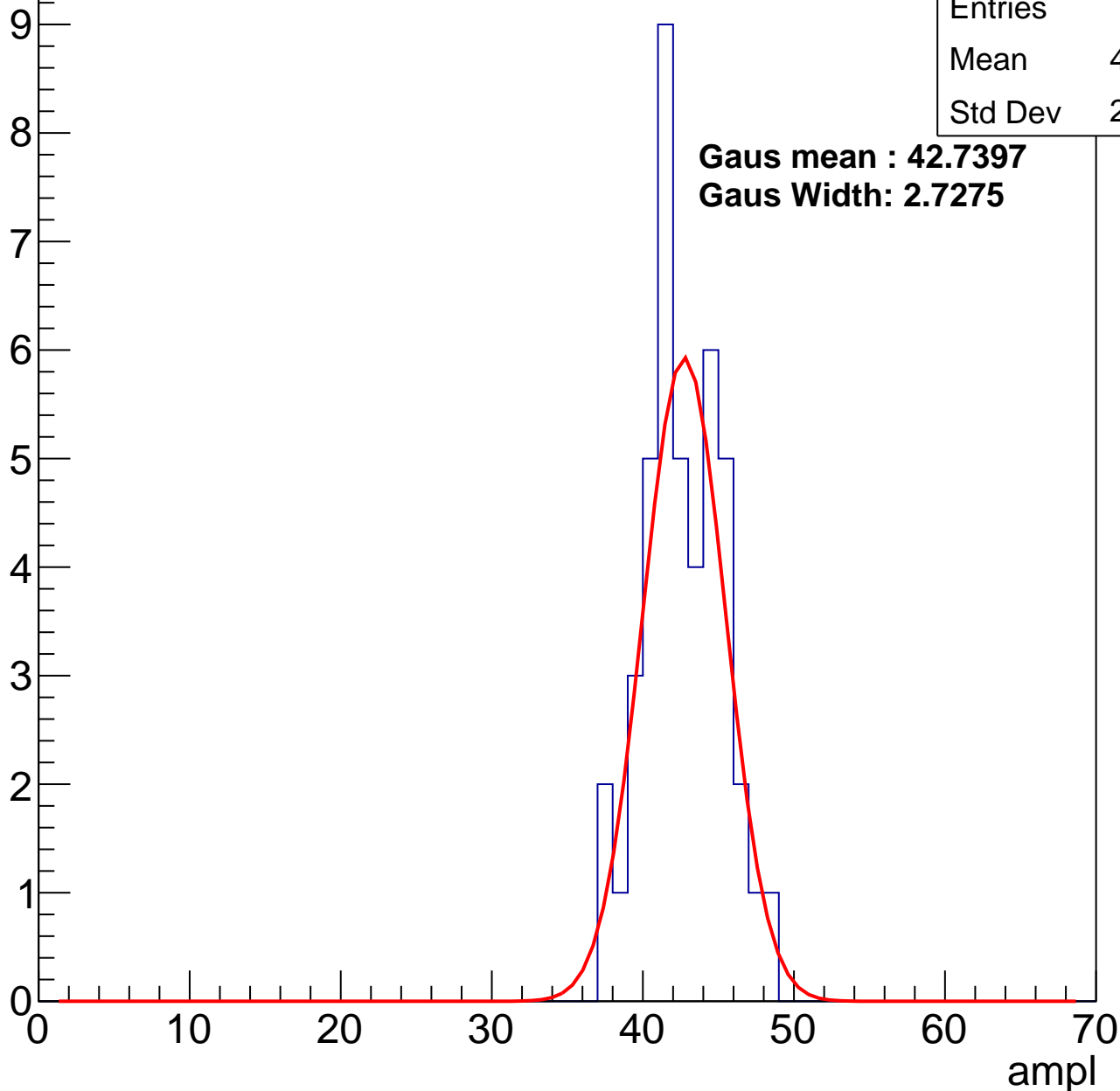
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	42.18
Std Dev	2.552

Gaus mean : 42.7397

Gaus Width: 2.7275

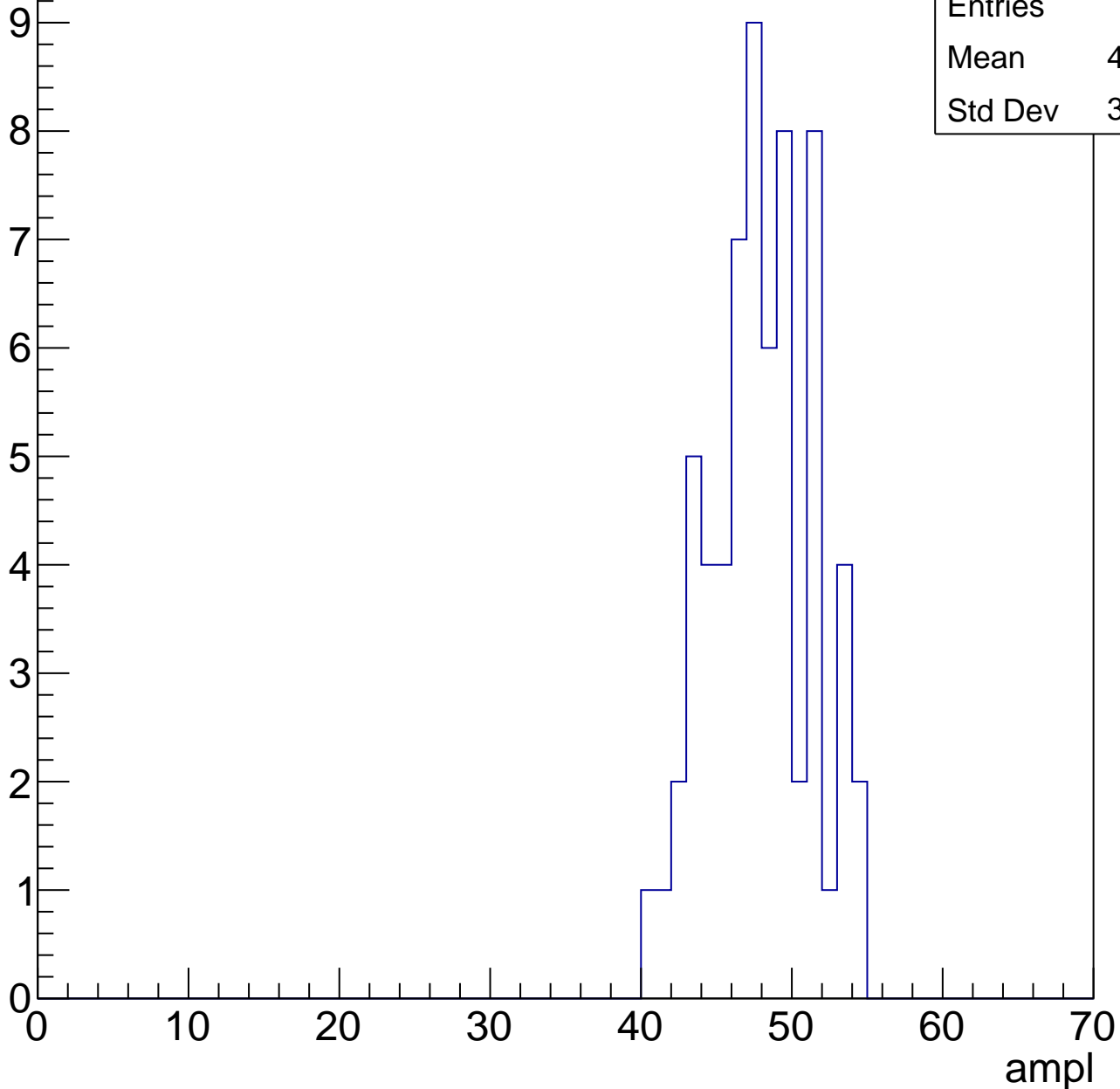


B1L003S, U19-ch102, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	47.52
Std Dev	3.326

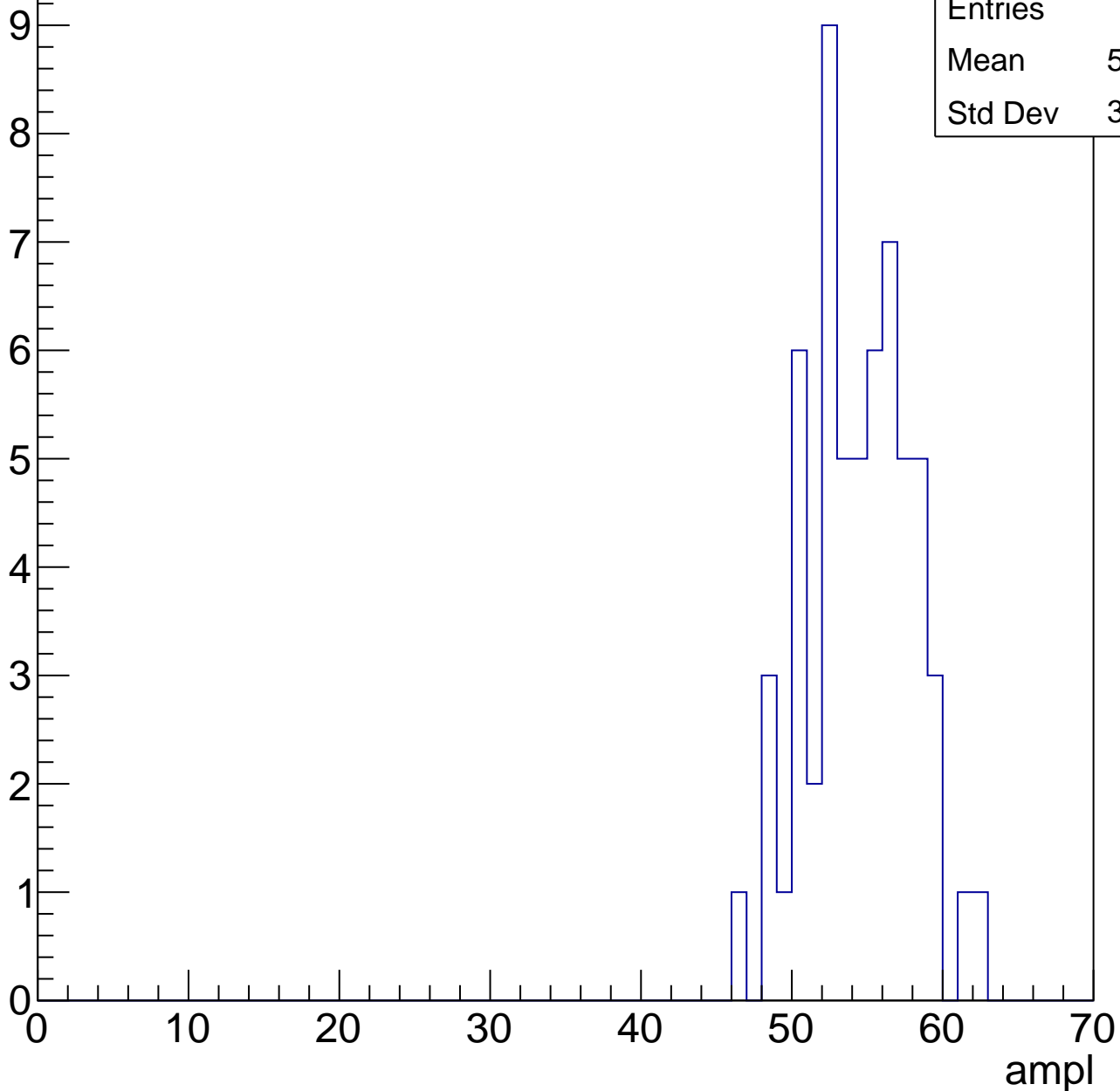


B1L003S, U19-ch102, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.02
Std Dev	3.413

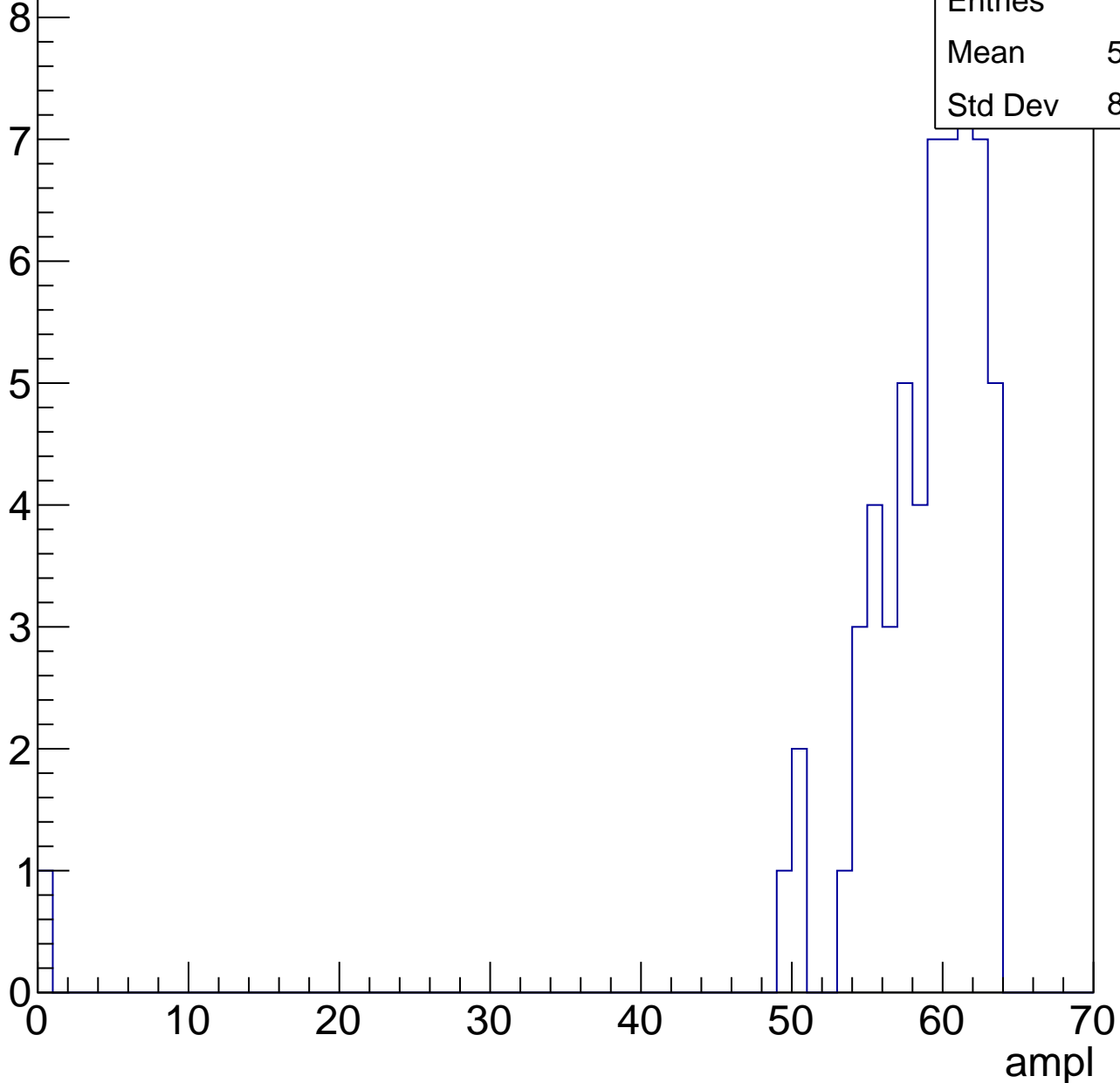


B1L003S, U19-ch102, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	57.57
Std Dev	8.338

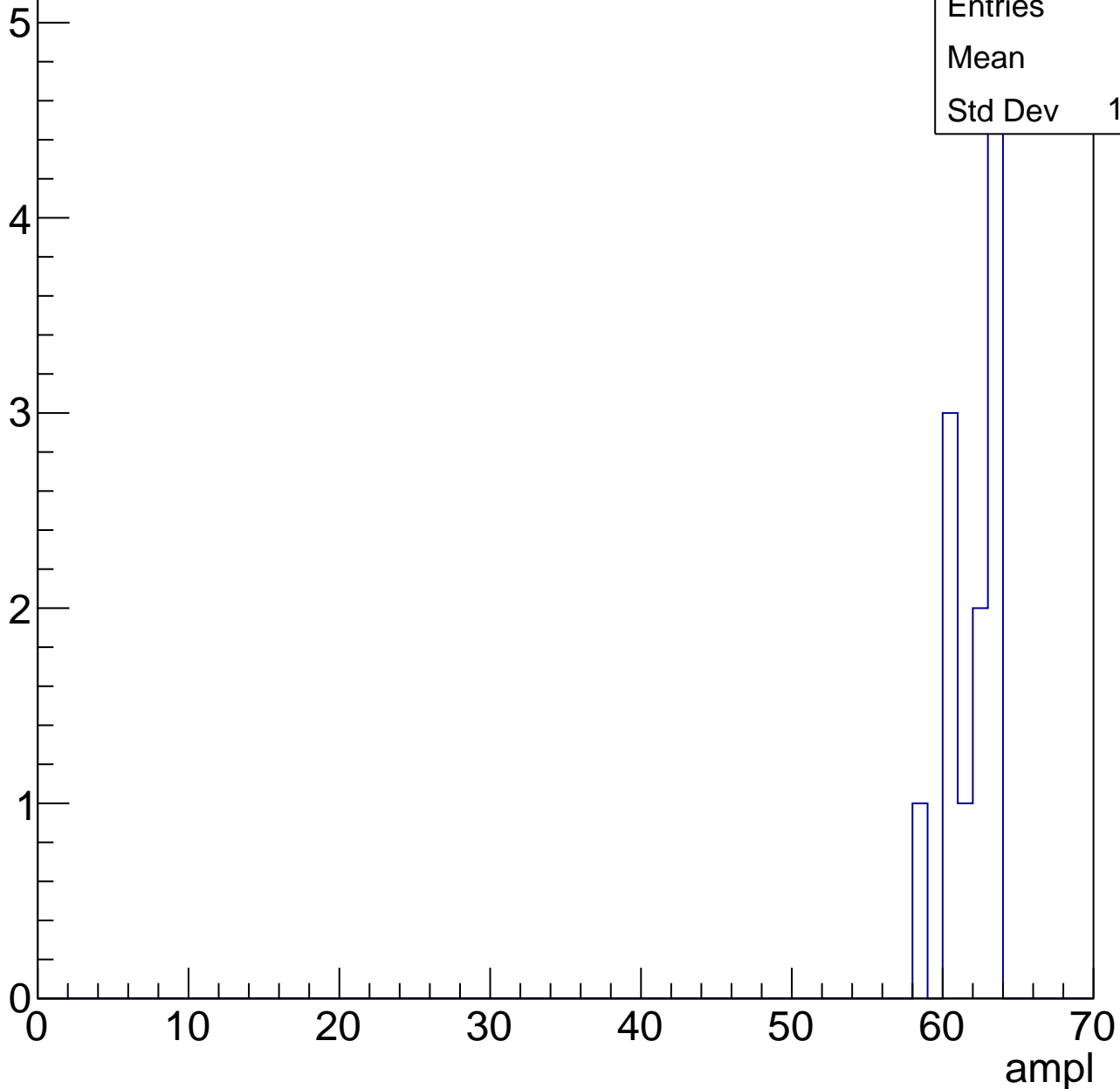


B1L003S, U19-ch102, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	12
Mean	61.5
Std Dev	1.607



B1L003S, U19-ch102, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch103, adc0

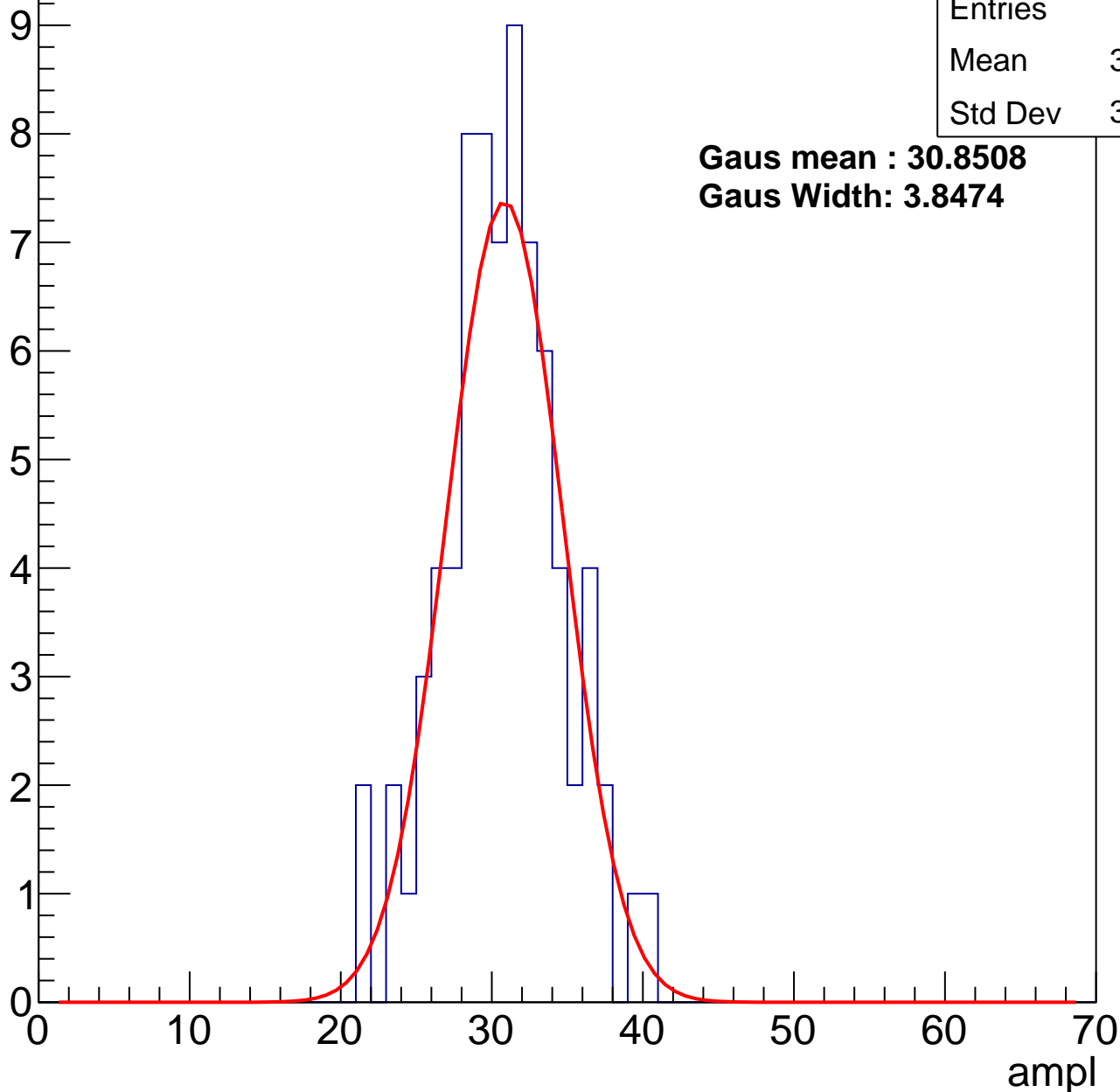
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	30.25
Std Dev	3.889

Gaus mean : 30.8508

Gaus Width: 3.8474



B1L003S, U19-ch103, adc1

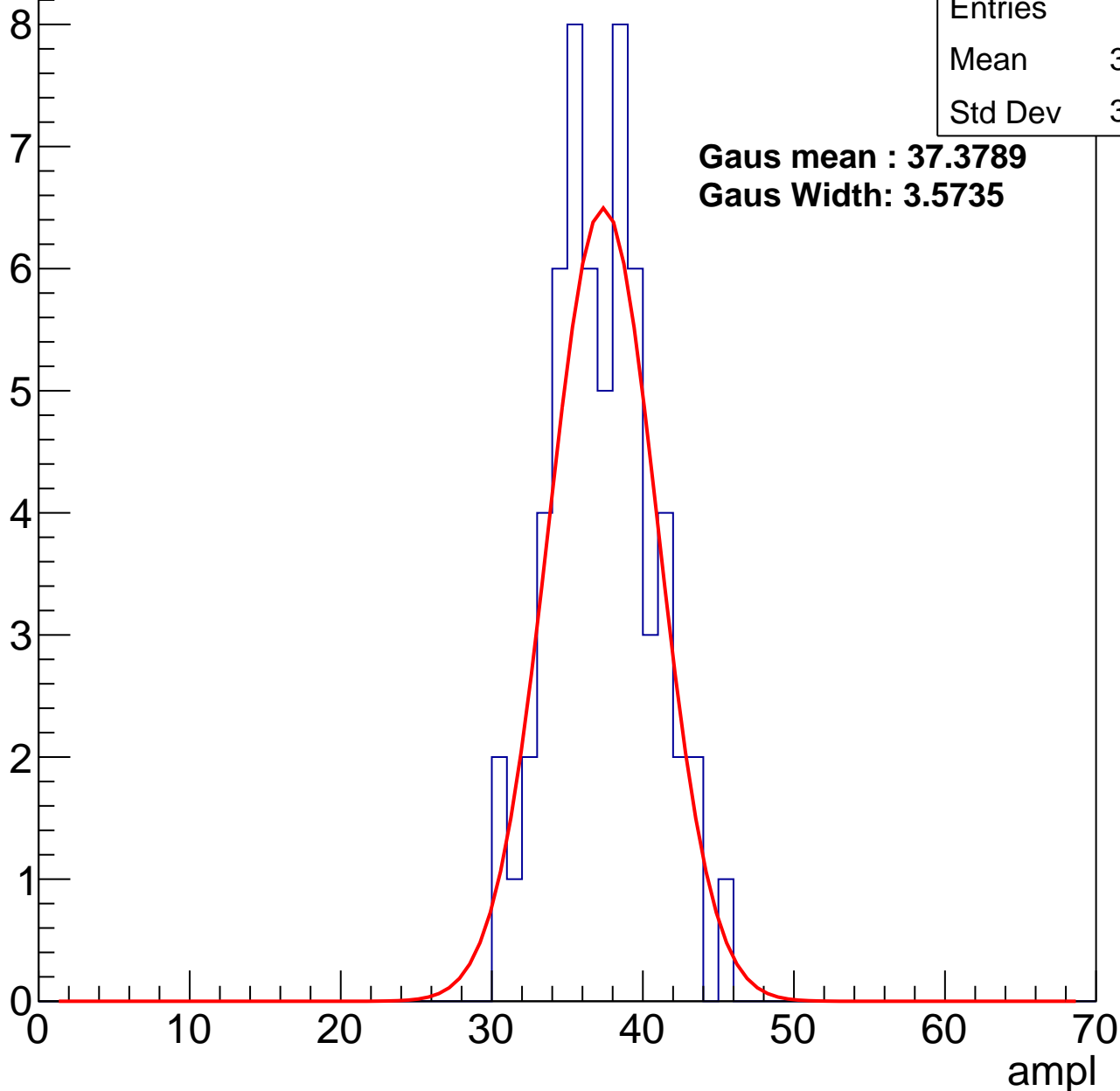
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.82
Std Dev	3.284

Gaus mean : 37.3789

Gaus Width: 3.5735



B1L003S, U19-ch103, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	43.79
Std Dev	3.204

Gaus mean : 44.0712

Gaus Width: 3.2561

10

8

6

4

2

0

0

10

20

30

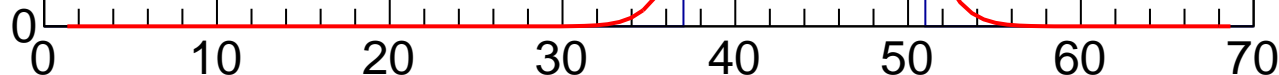
40

50

60

70

ampl

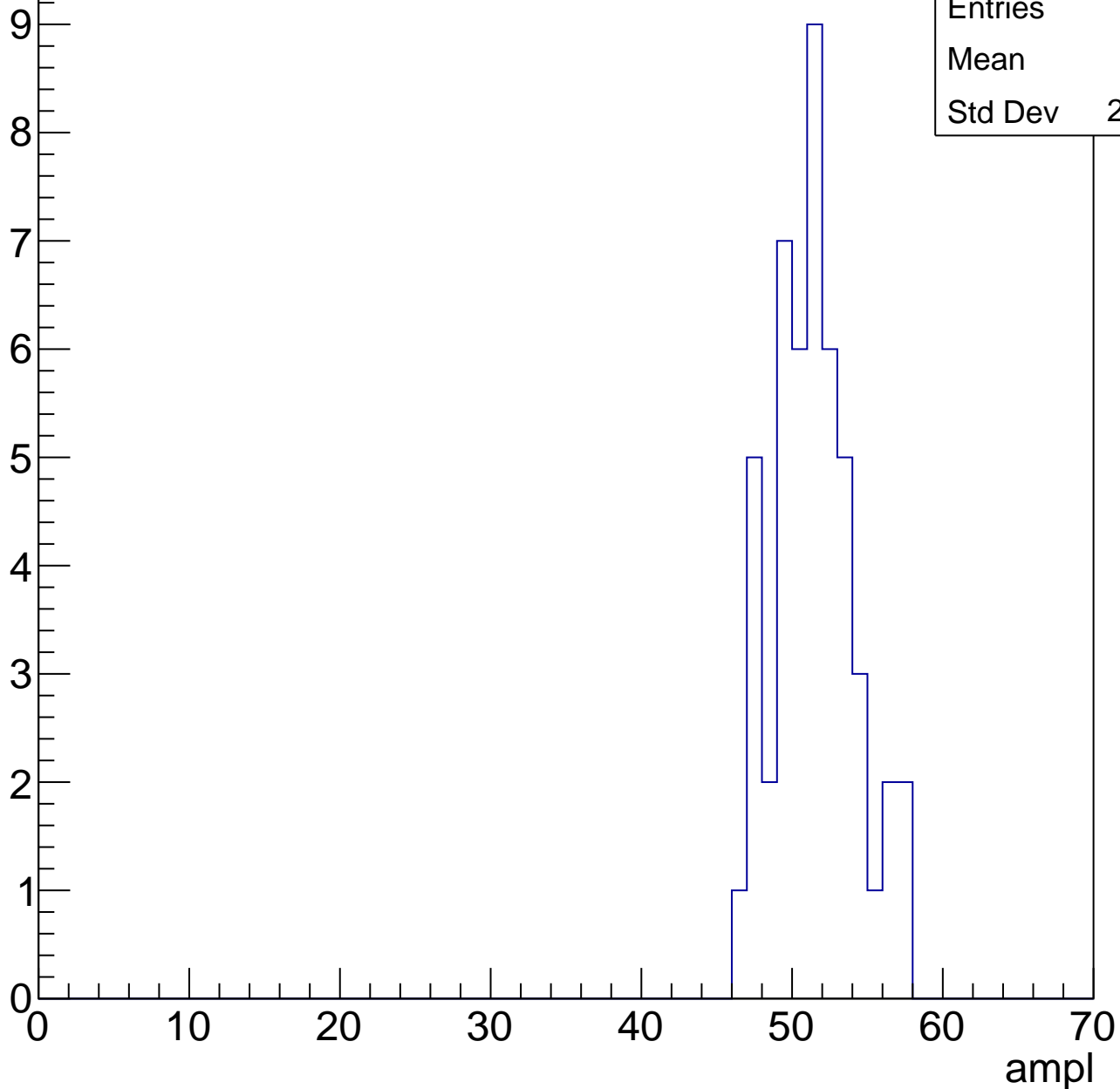


B1L003S, U19-ch103, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

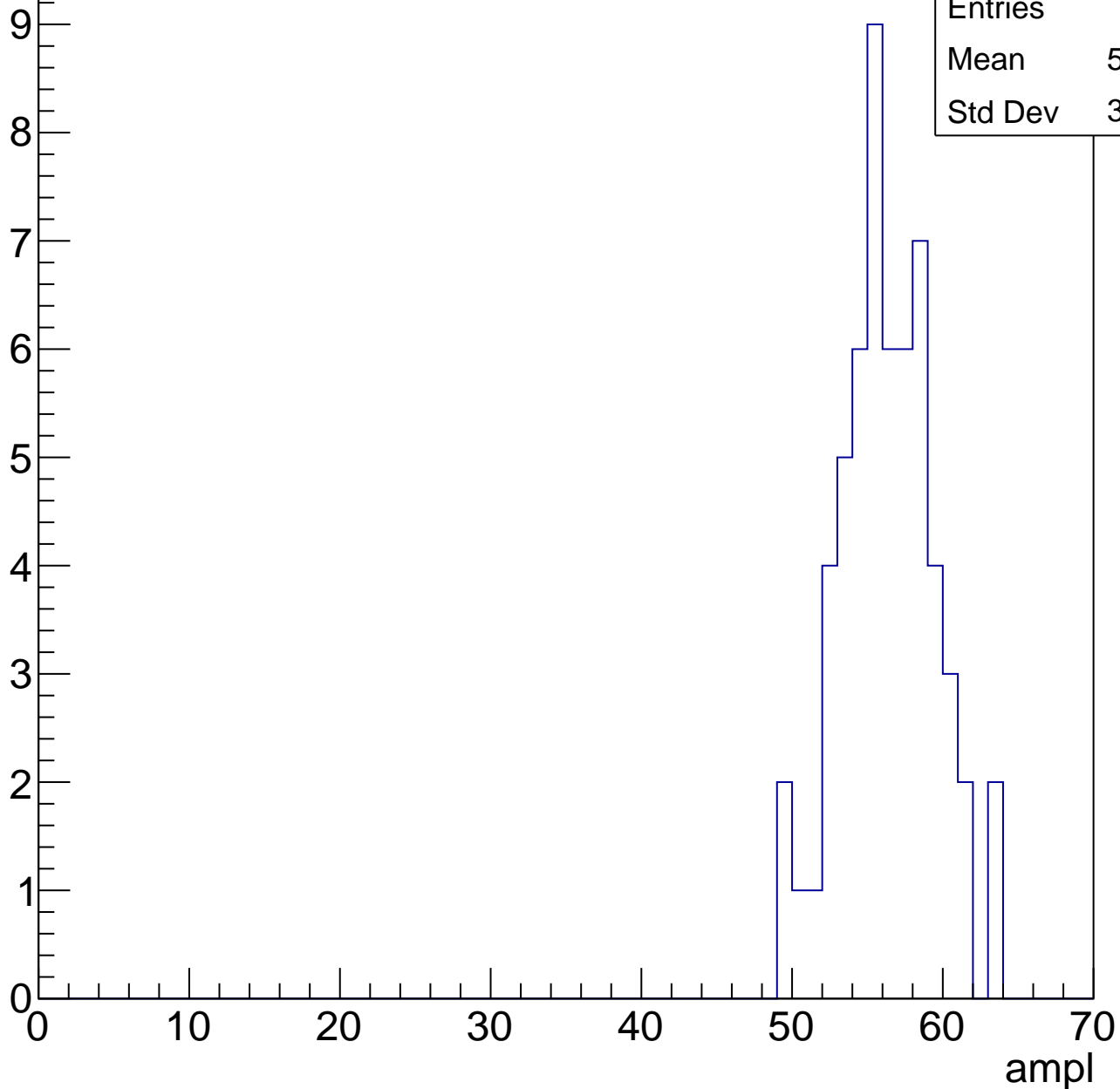
Entries	49
Mean	51
Std Dev	2.665



B1L003S, U19-ch103, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



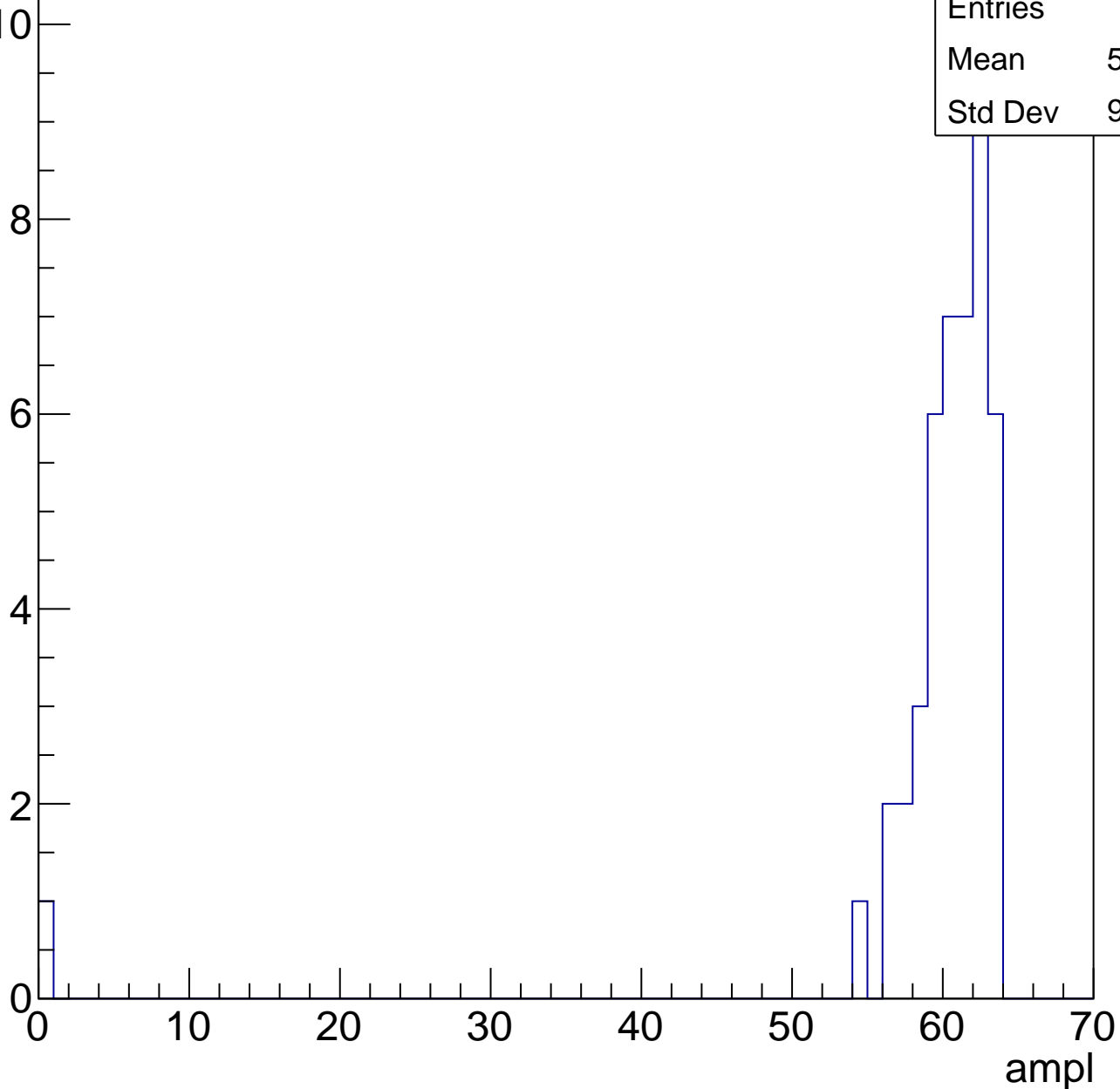
Entries	58
Mean	55.84
Std Dev	3.123

B1L003S, U19-ch103, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.96
Std Dev	9.136



B1L003S, U19-ch103, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch103, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch104, adc0

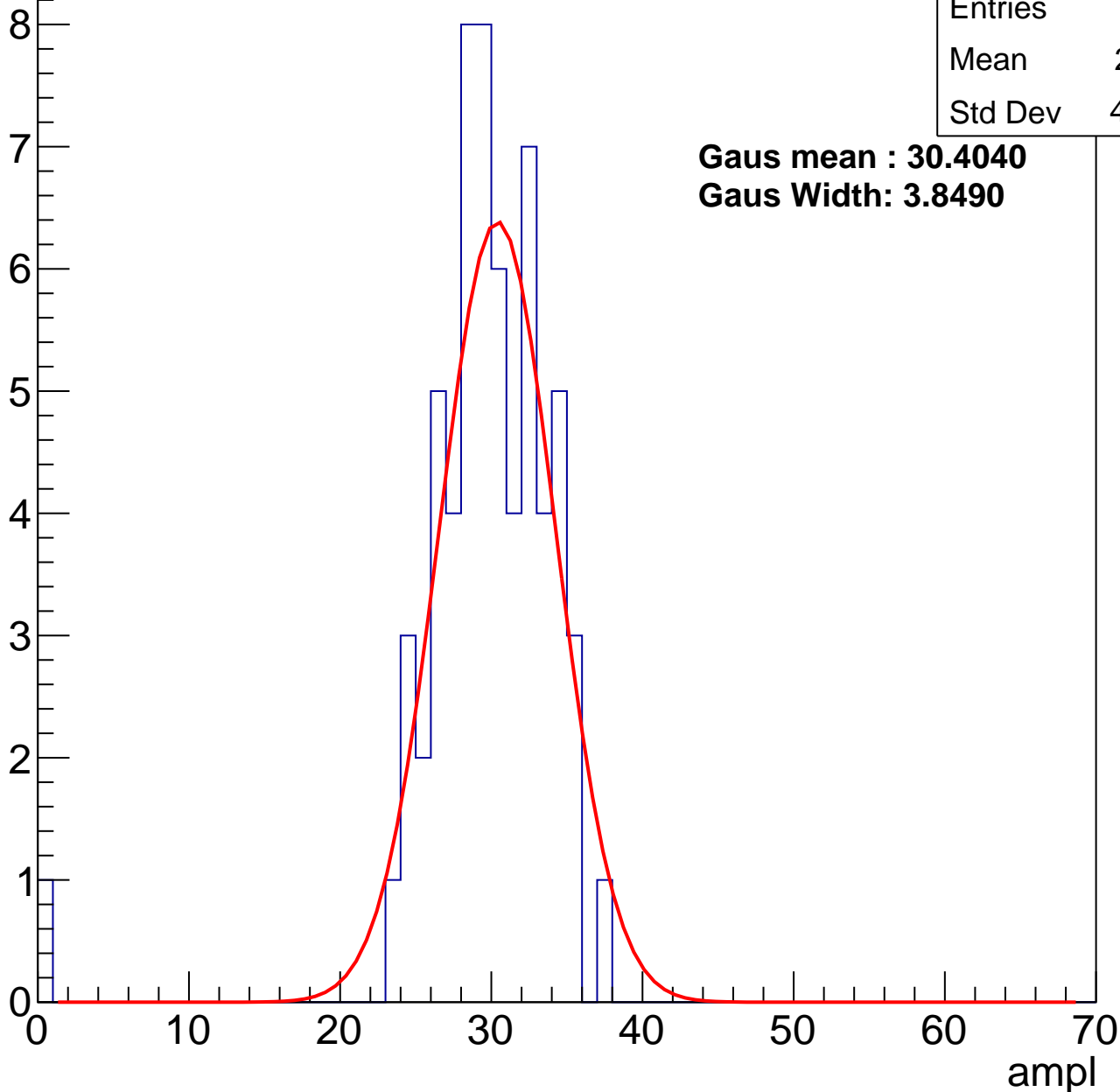
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.21
Std Dev	4.913

Gaus mean : 30.4040

Gaus Width: 3.8490



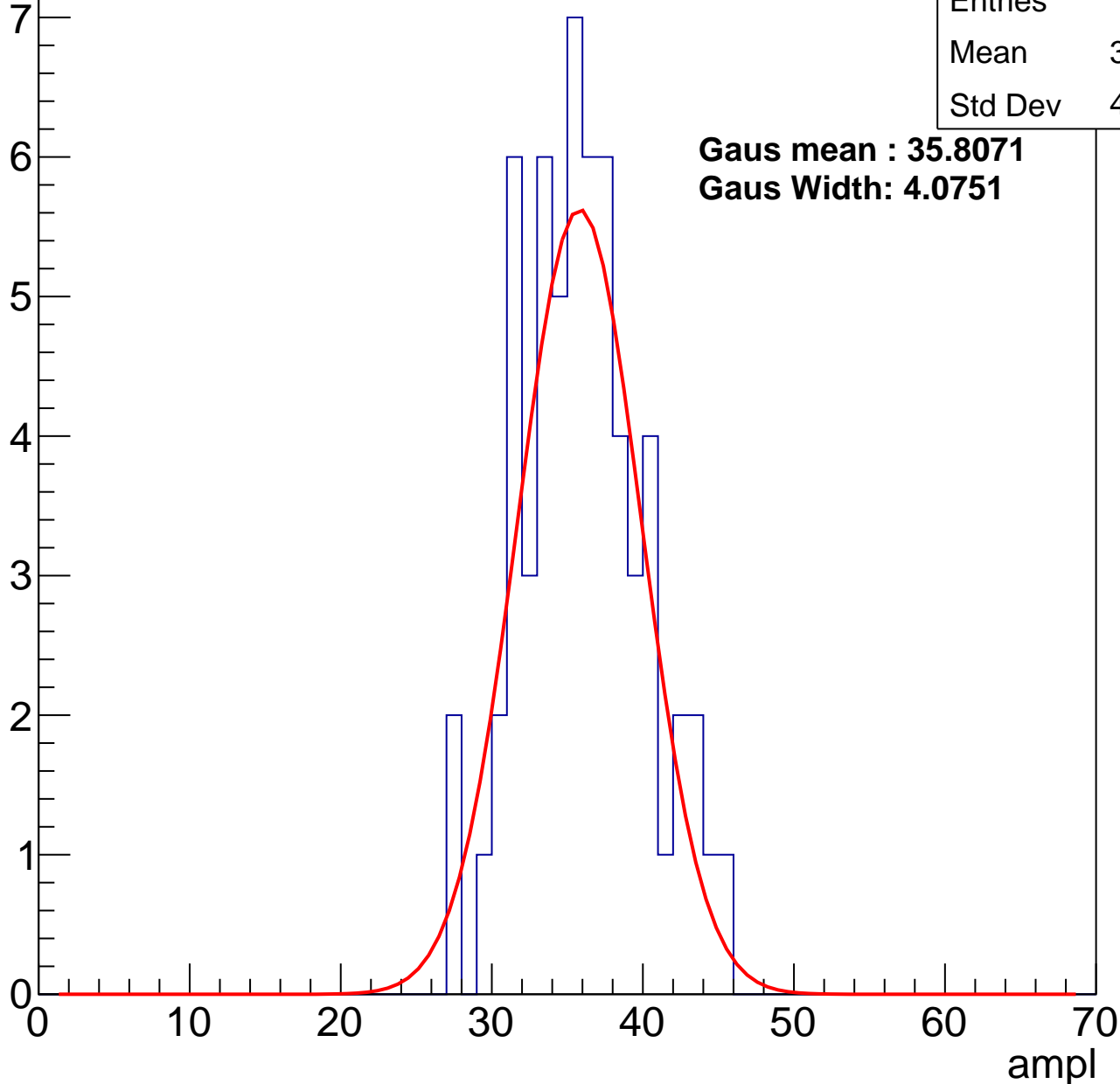
B1L003S, U19-ch104, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	35.56
Std Dev	4.023

Gaus mean : 35.8071
Gaus Width: 4.0751



B1L003S, U19-ch104, adc2

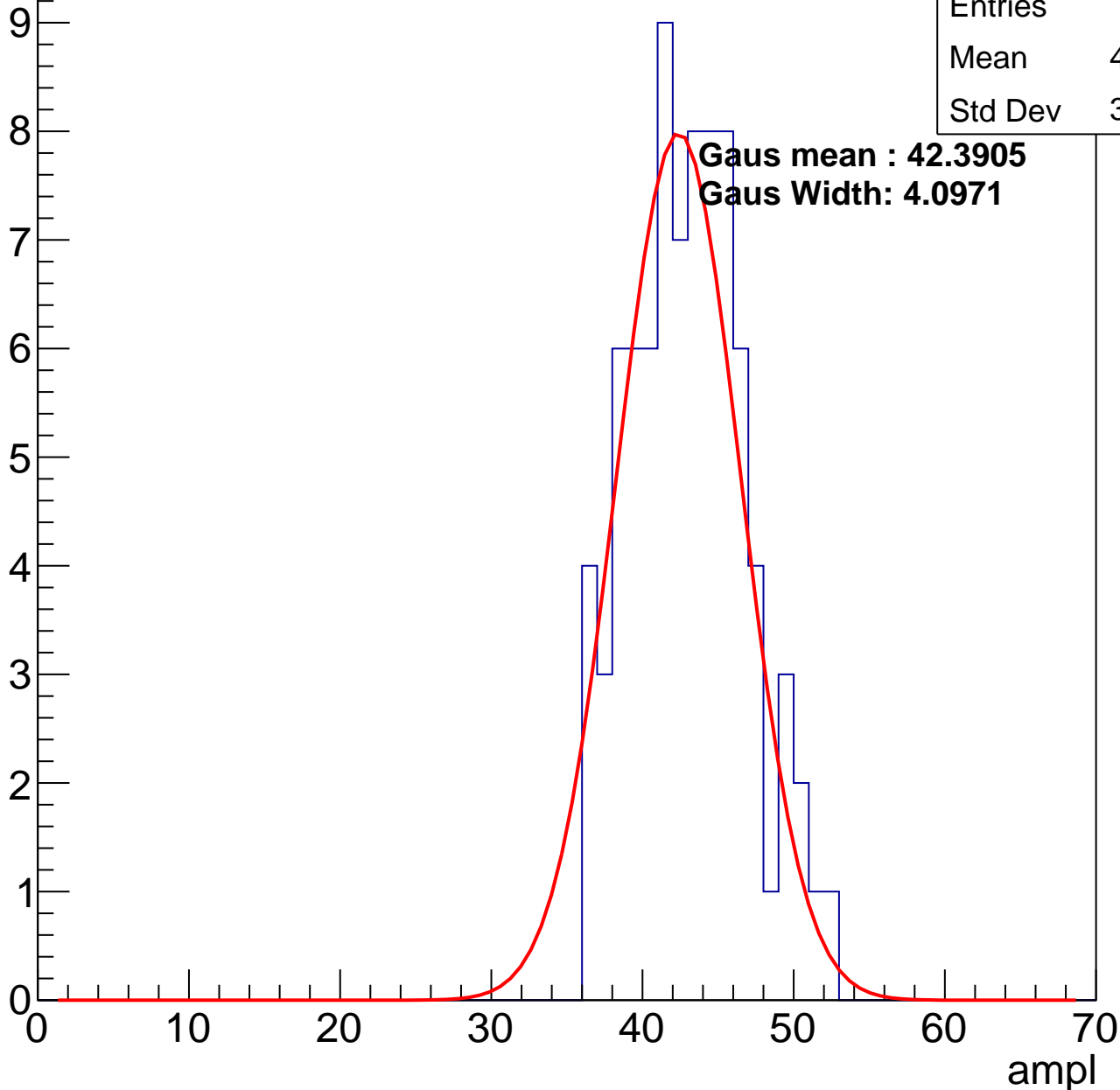
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	42.63
Std Dev	3.754

Gaus mean : 42.3905

Gaus Width: 4.0971

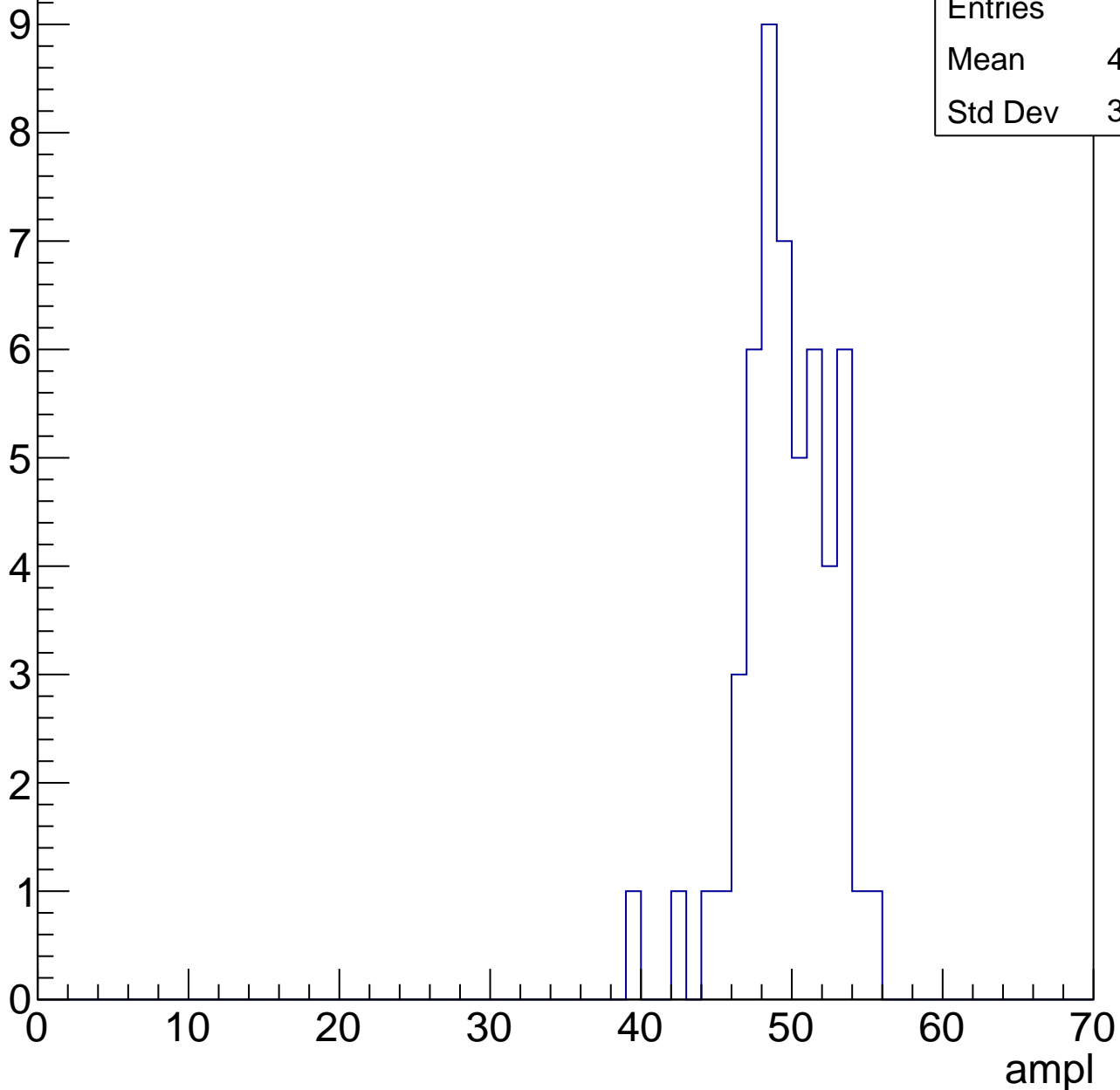


B1L003S, U19-ch104, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	49.15
Std Dev	3.009

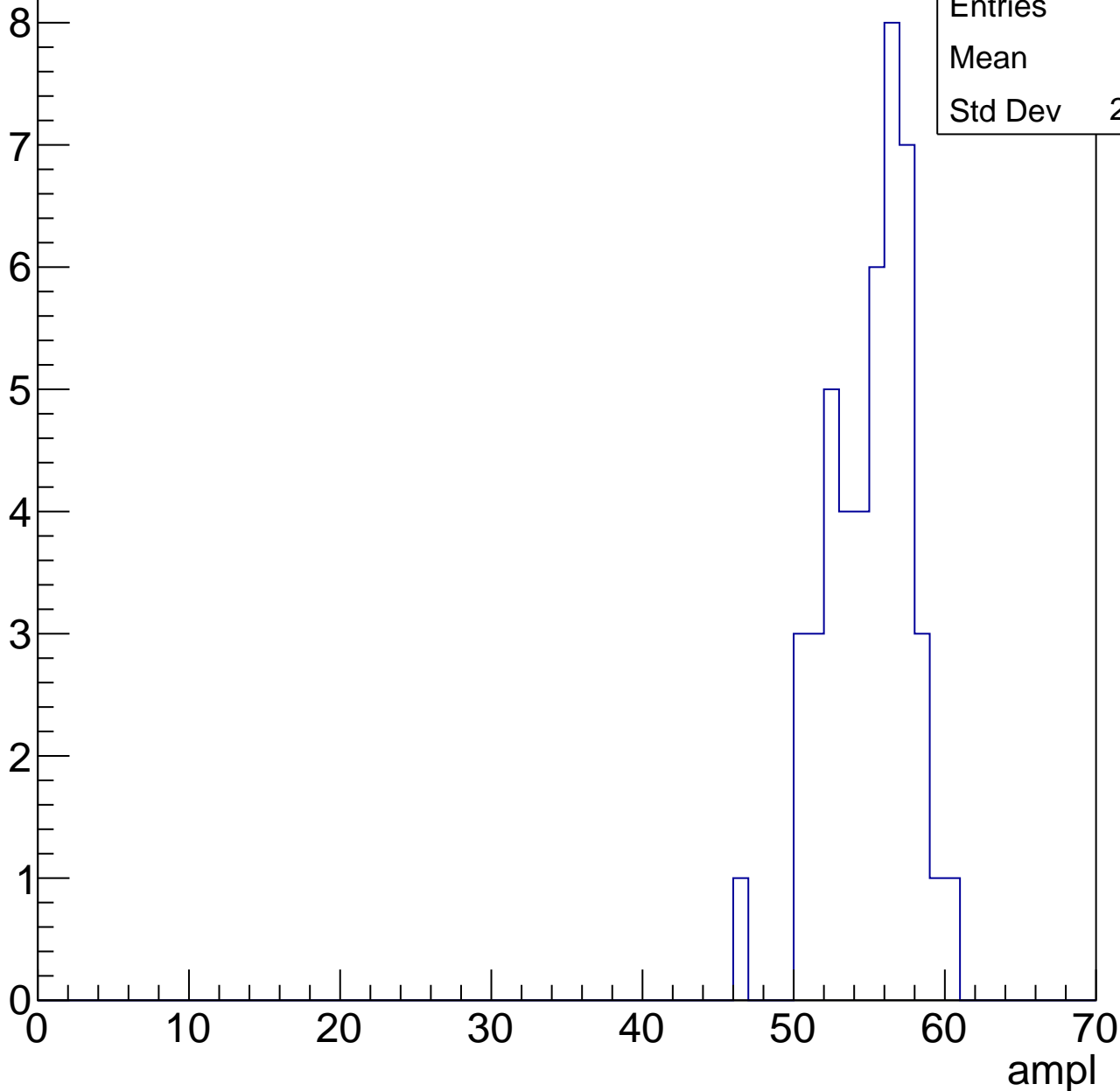


B1L003S, U19-ch104, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	54.5
Std Dev	2.803



B1L003S, U19-ch104, adc5

calib_packv5_042523_0143.root, FC#13, port D2

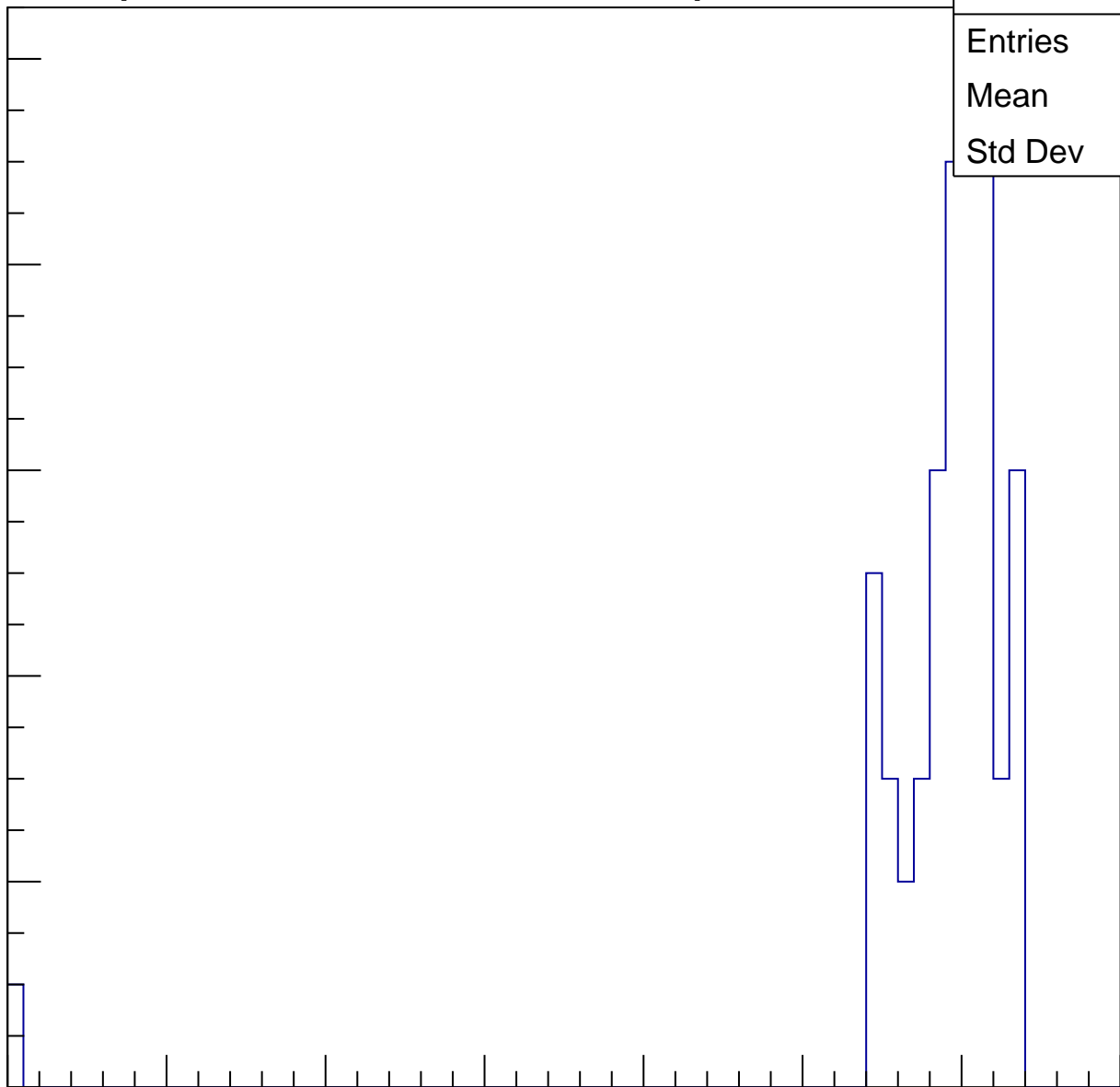
Entries	57
Mean	58.09
Std Dev	8.181

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

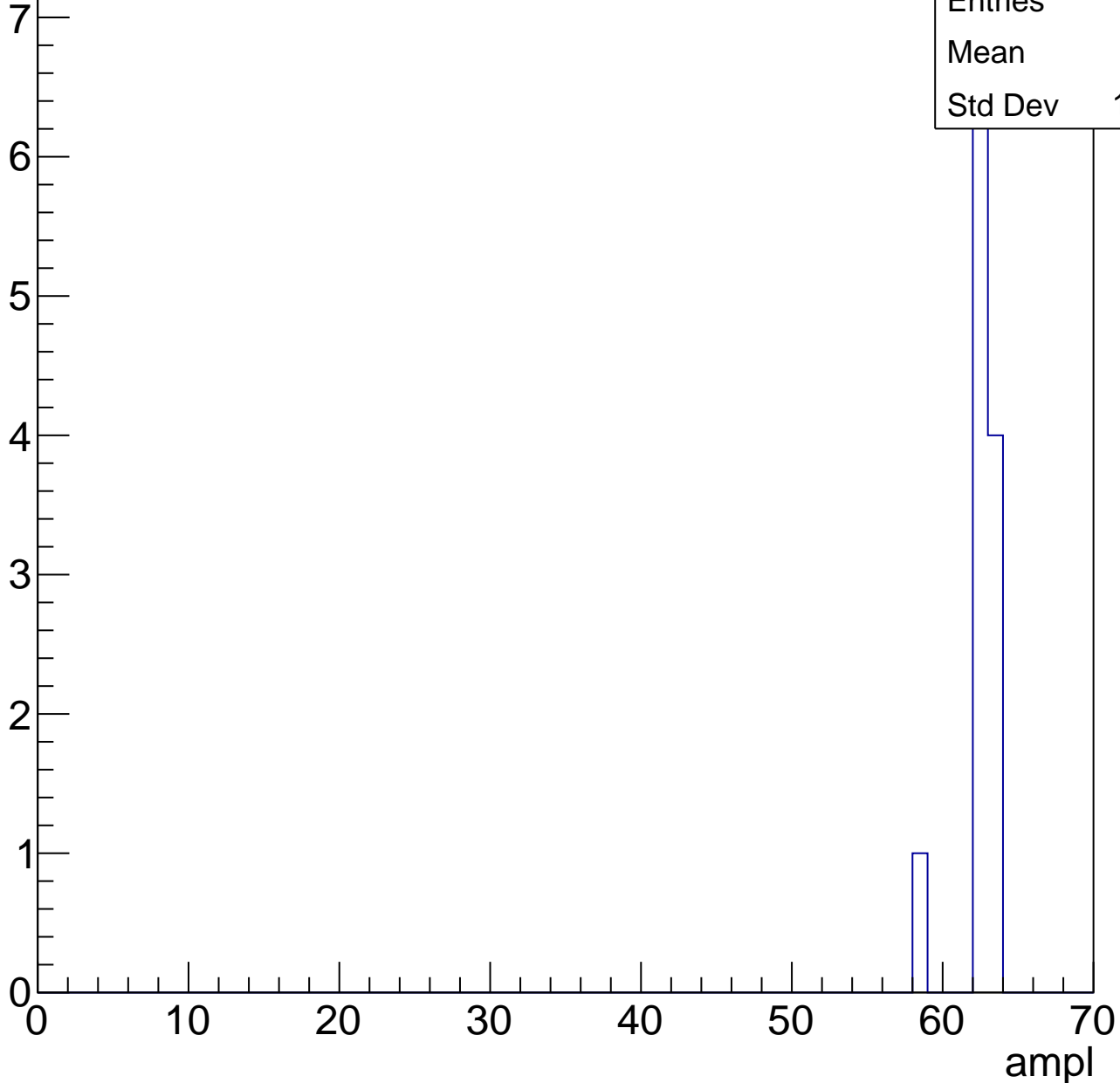


B1L003S, U19-ch104, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	12
Mean	62
Std Dev	1.291



B1L003S, U19-ch104, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch105, adc0

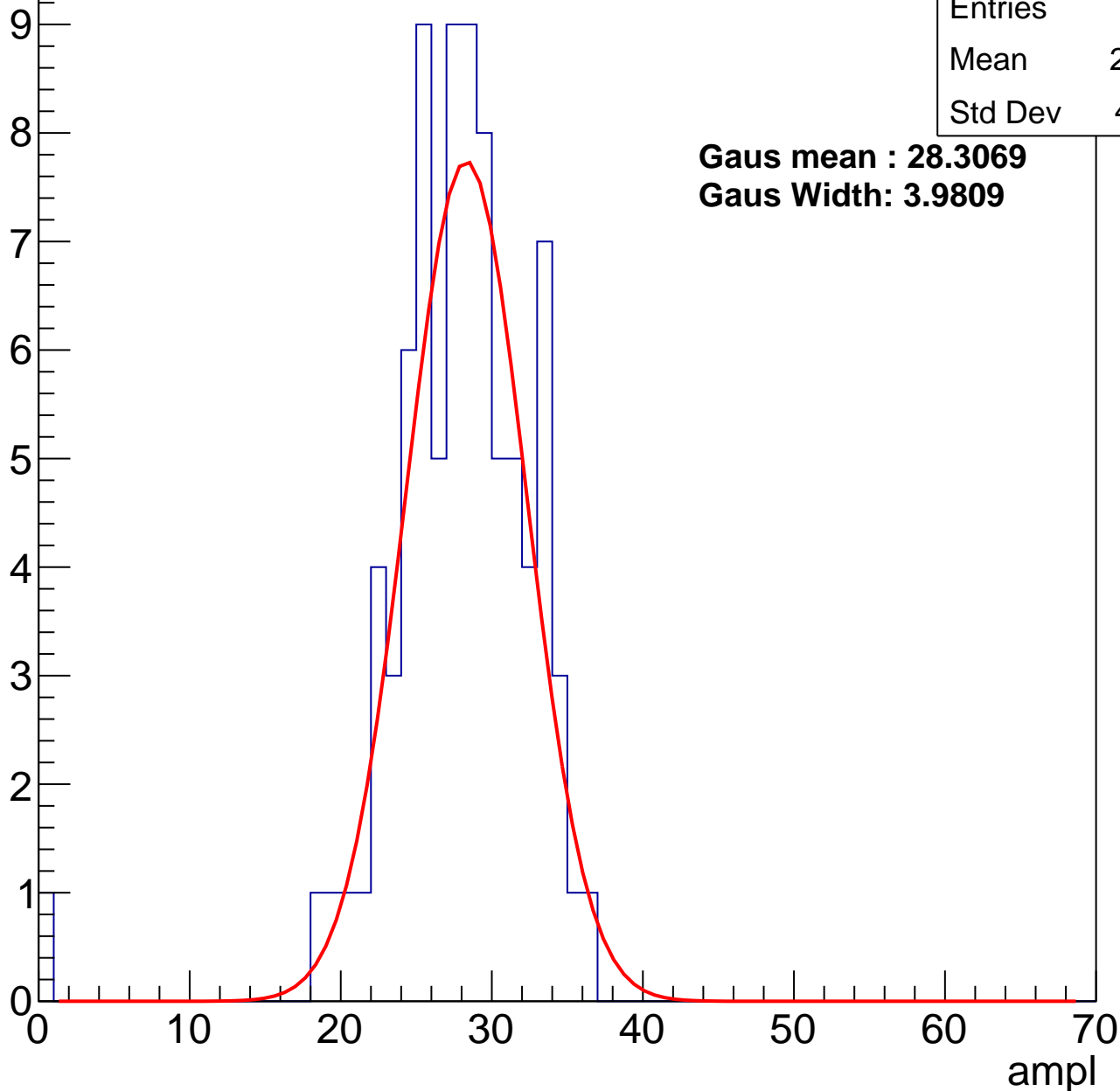
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	27.36
Std Dev	4.881

Gaus mean : 28.3069

Gaus Width: 3.9809



B1L003S, U19-ch105, adc1

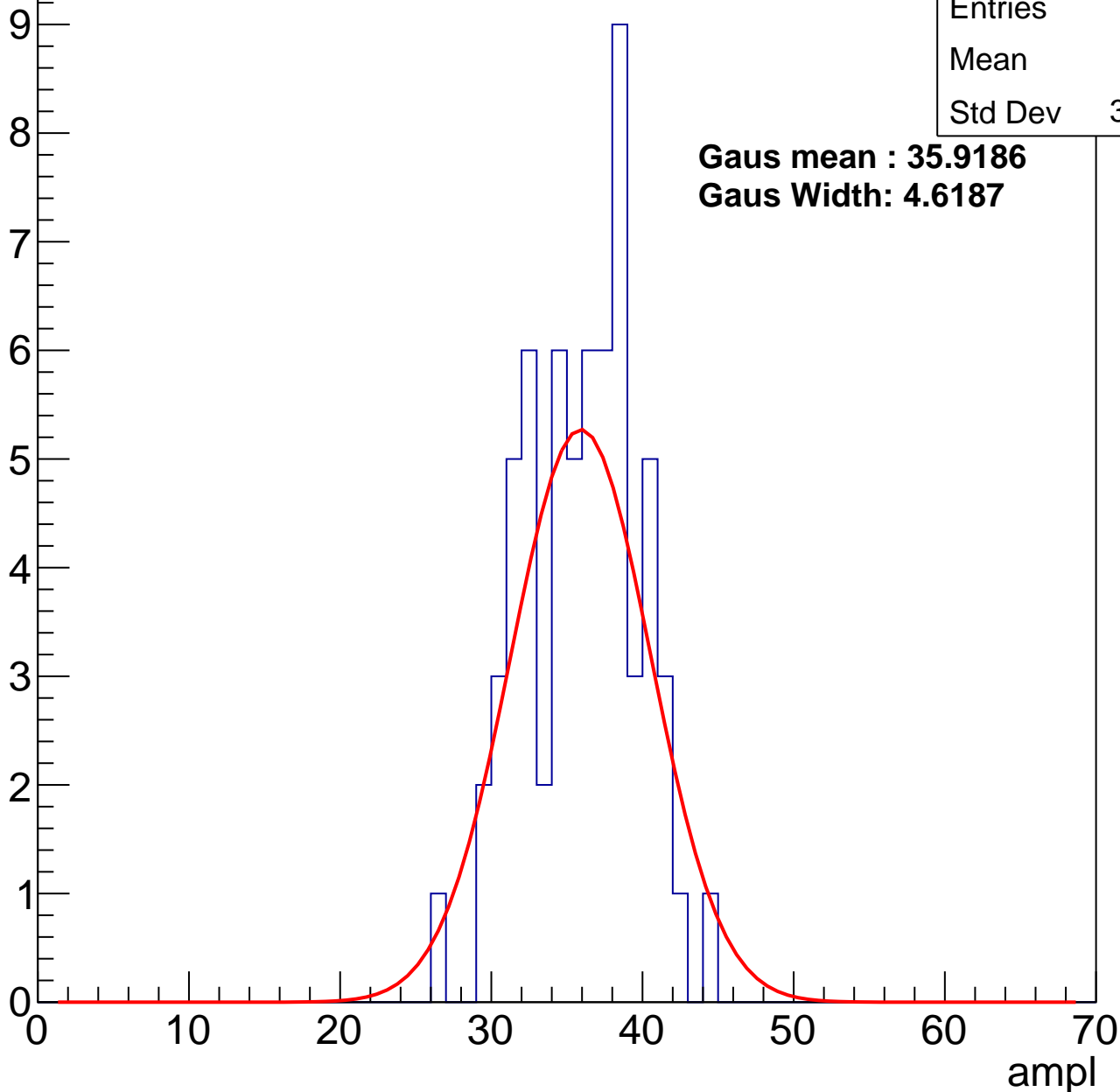
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	35.5
Std Dev	3.712

Gaus mean : 35.9186

Gaus Width: 4.6187



B1L003S, U19-ch105, adc2

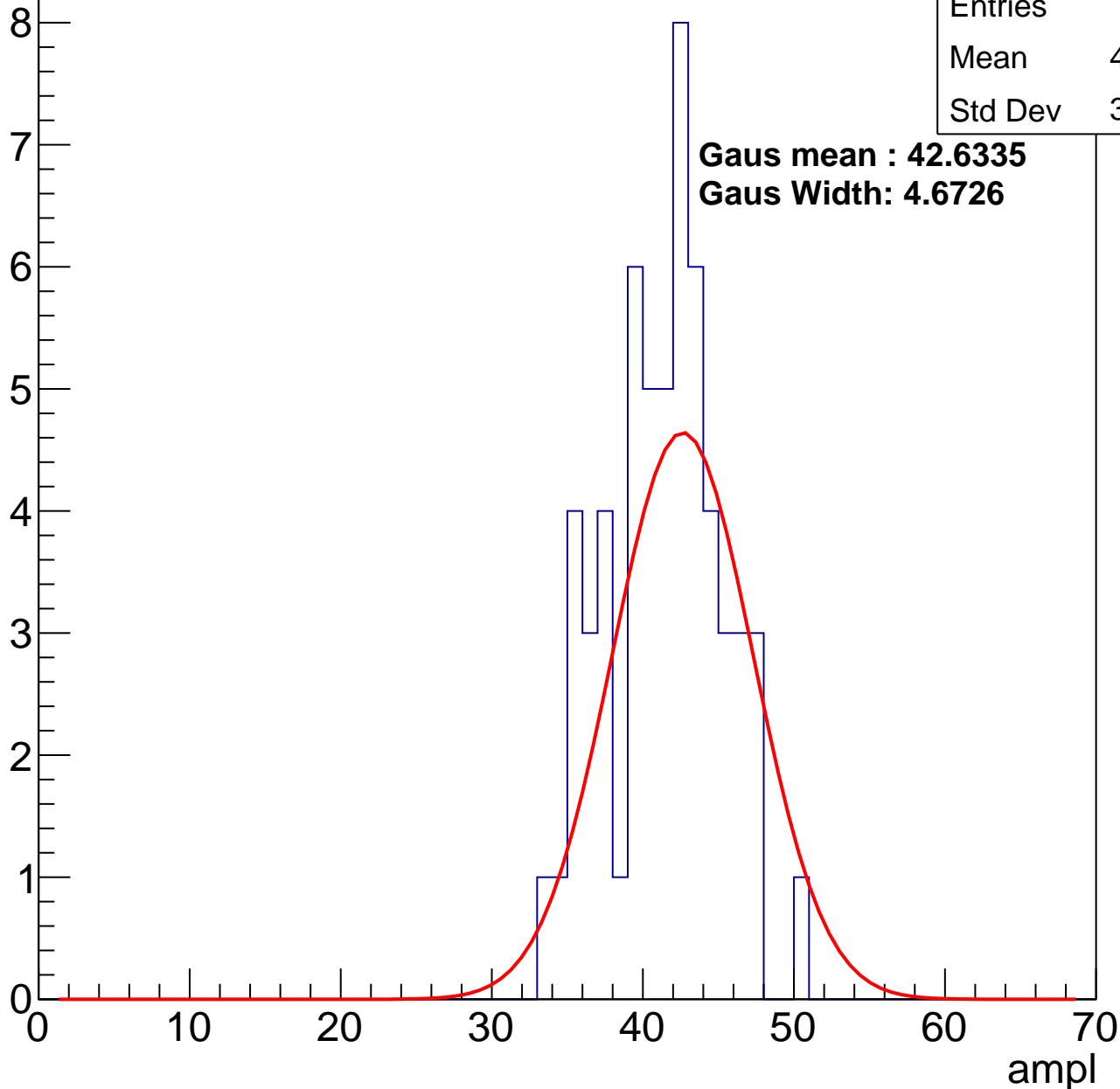
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	40.93
Std Dev	3.746

Gaus mean : 42.6335

Gaus Width: 4.6726

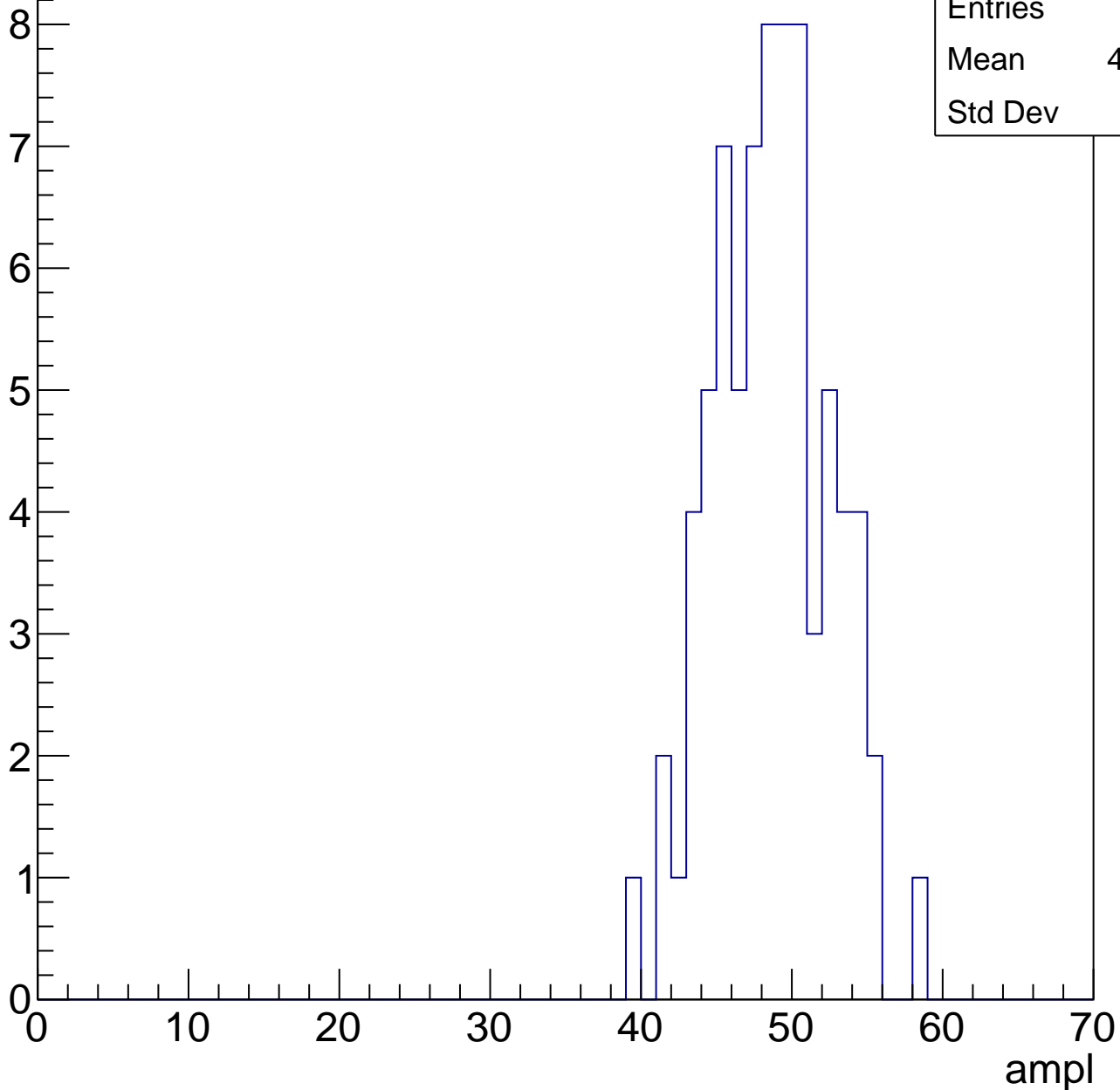


B1L003S, U19-ch105, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	48.19
Std Dev	3.78

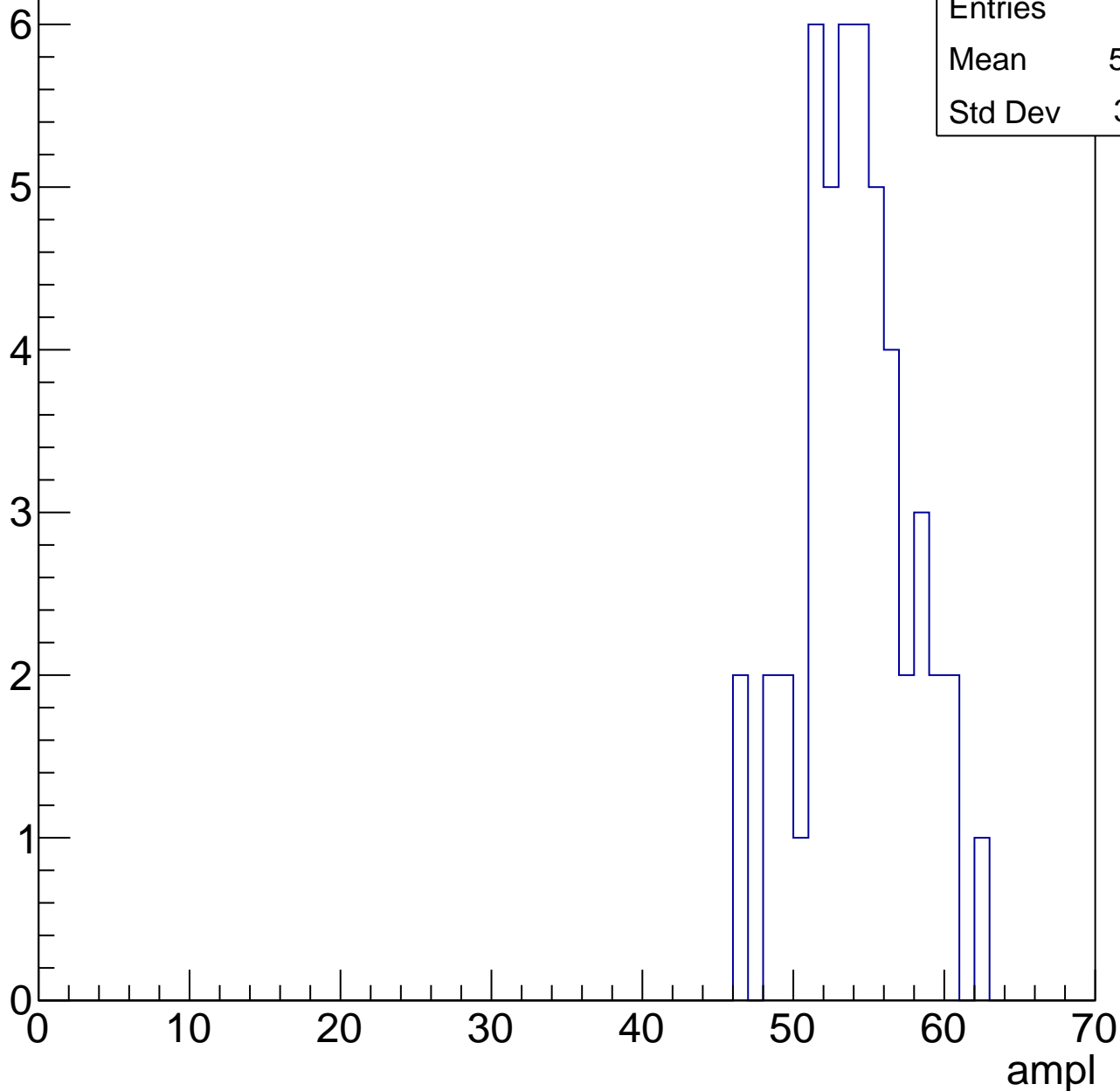


B1L003S, U19-ch105, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	53.69
Std Dev	3.541

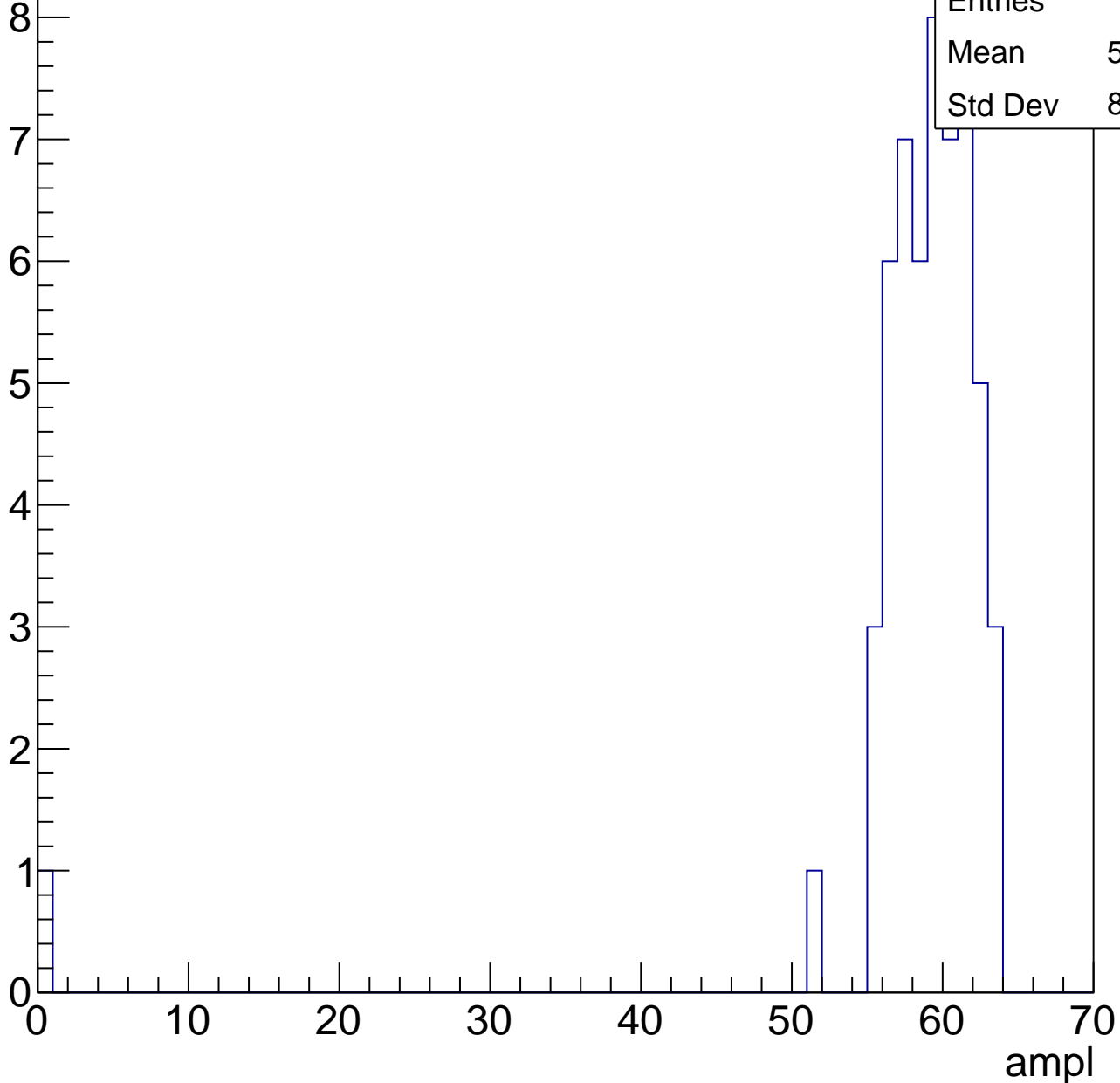


B1L003S, U19-ch105, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	57.78
Std Dev	8.237

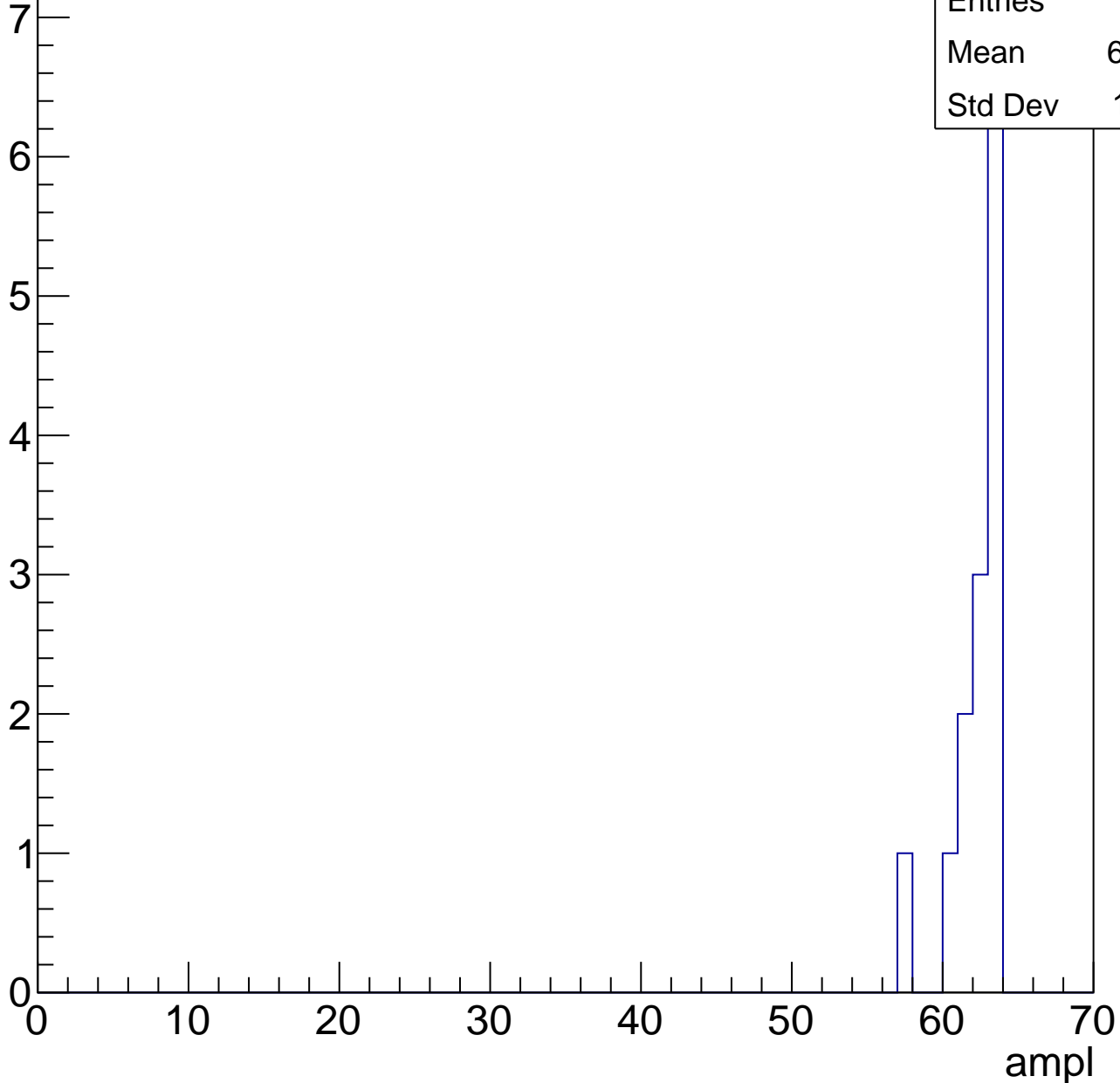


B1L003S, U19-ch105, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	61.86
Std Dev	1.641



B1L003S, U19-ch105, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L003S, U19-ch106, adc0

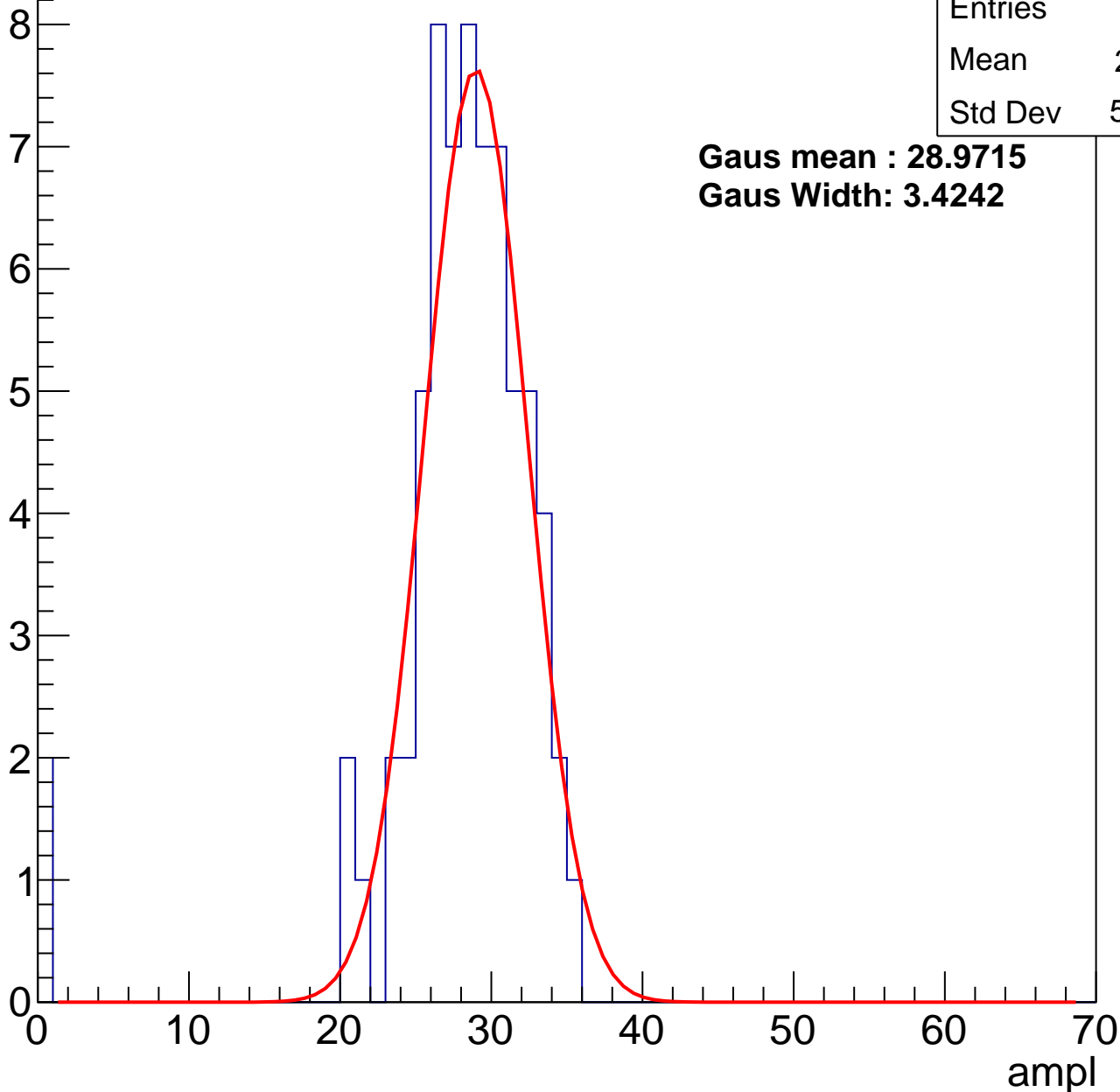
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	27.41
Std Dev	5.776

Gaus mean : 28.9715

Gaus Width: 3.4242



B1L003S, U19-ch106, adc1

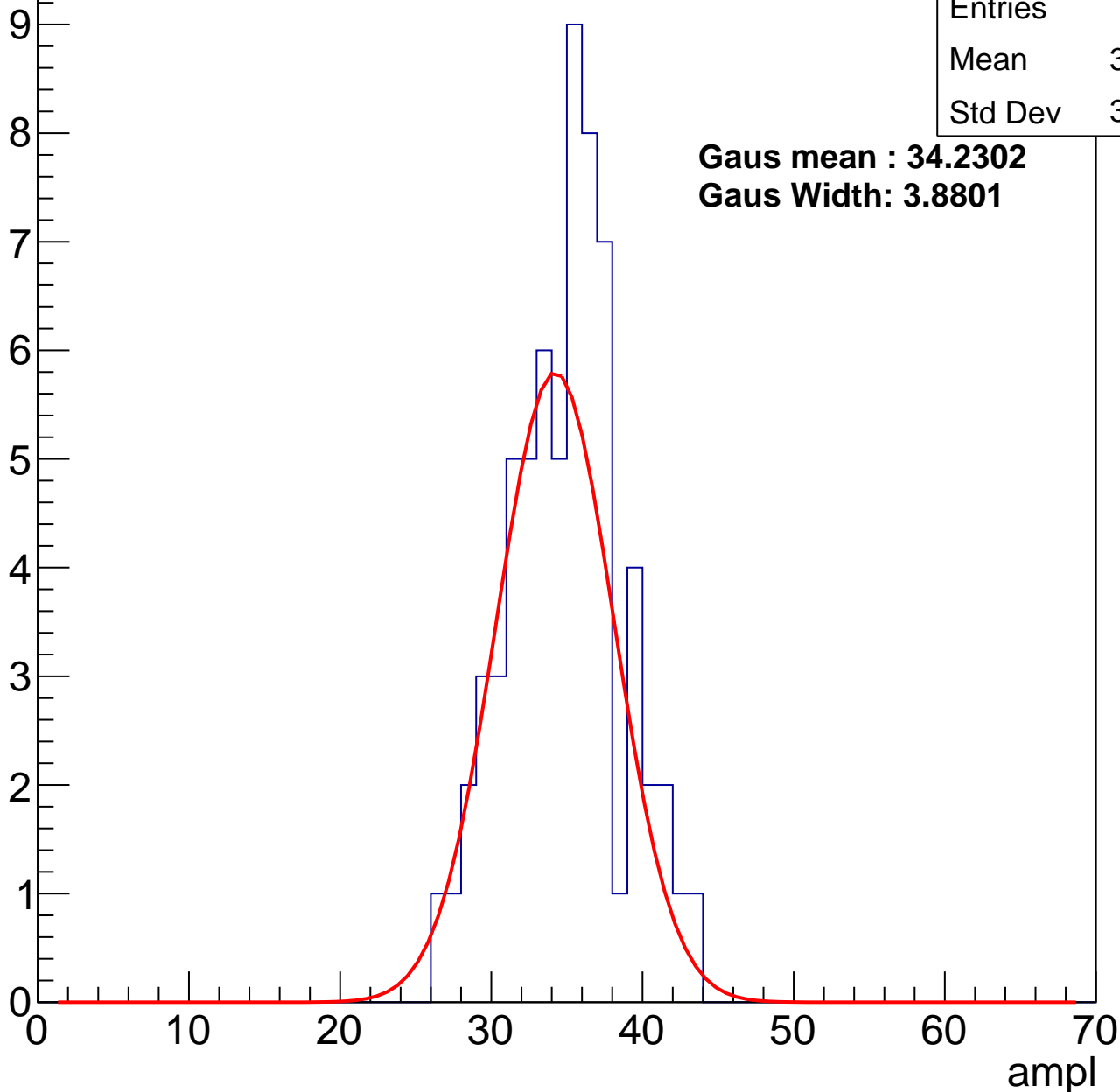
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	34.42
Std Dev	3.697

Gaus mean : 34.2302

Gaus Width: 3.8801



B1L003S, U19-ch106, adc2

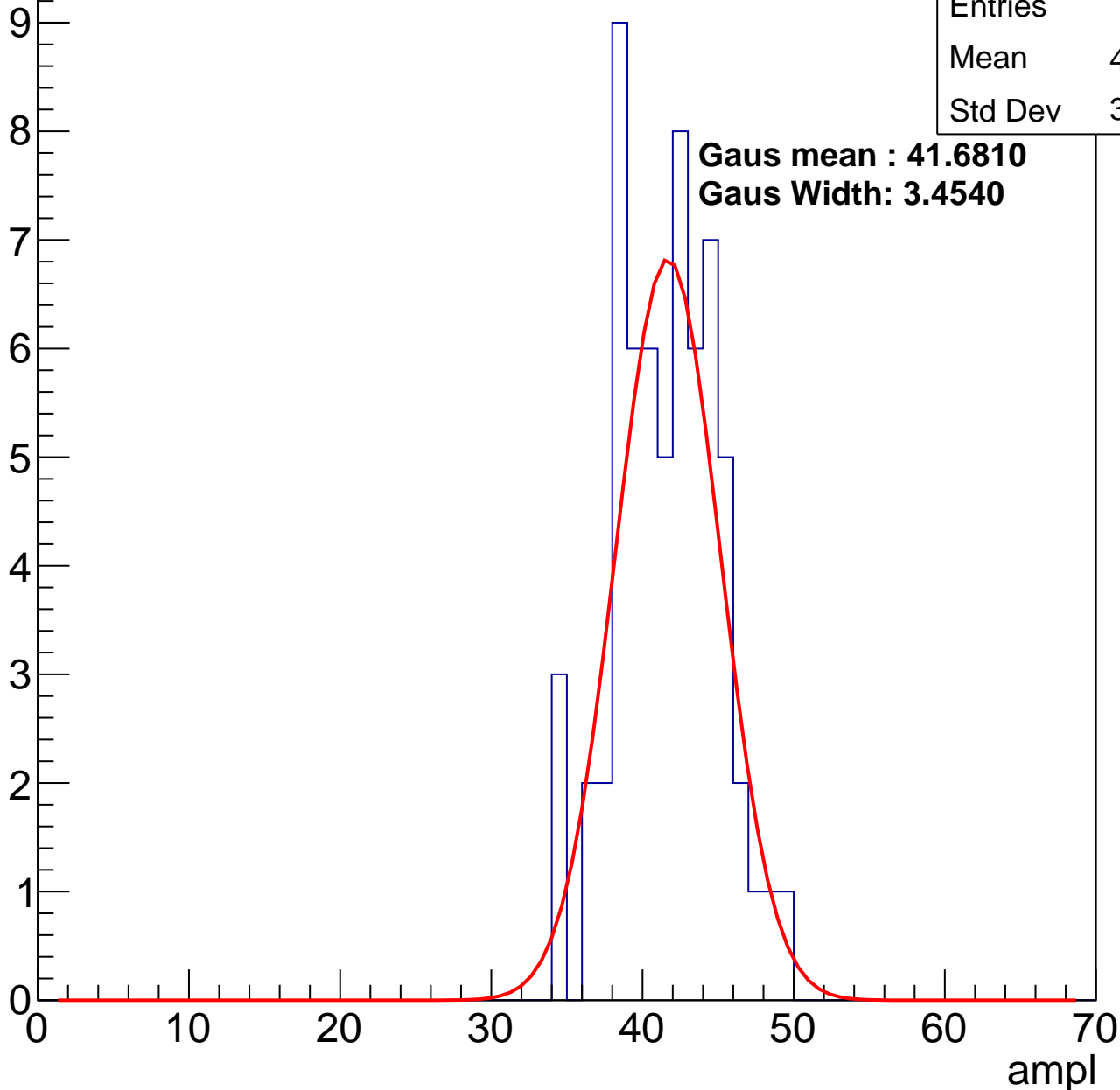
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	41.12
Std Dev	3.338

Gaus mean : 41.6810

Gaus Width: 3.4540

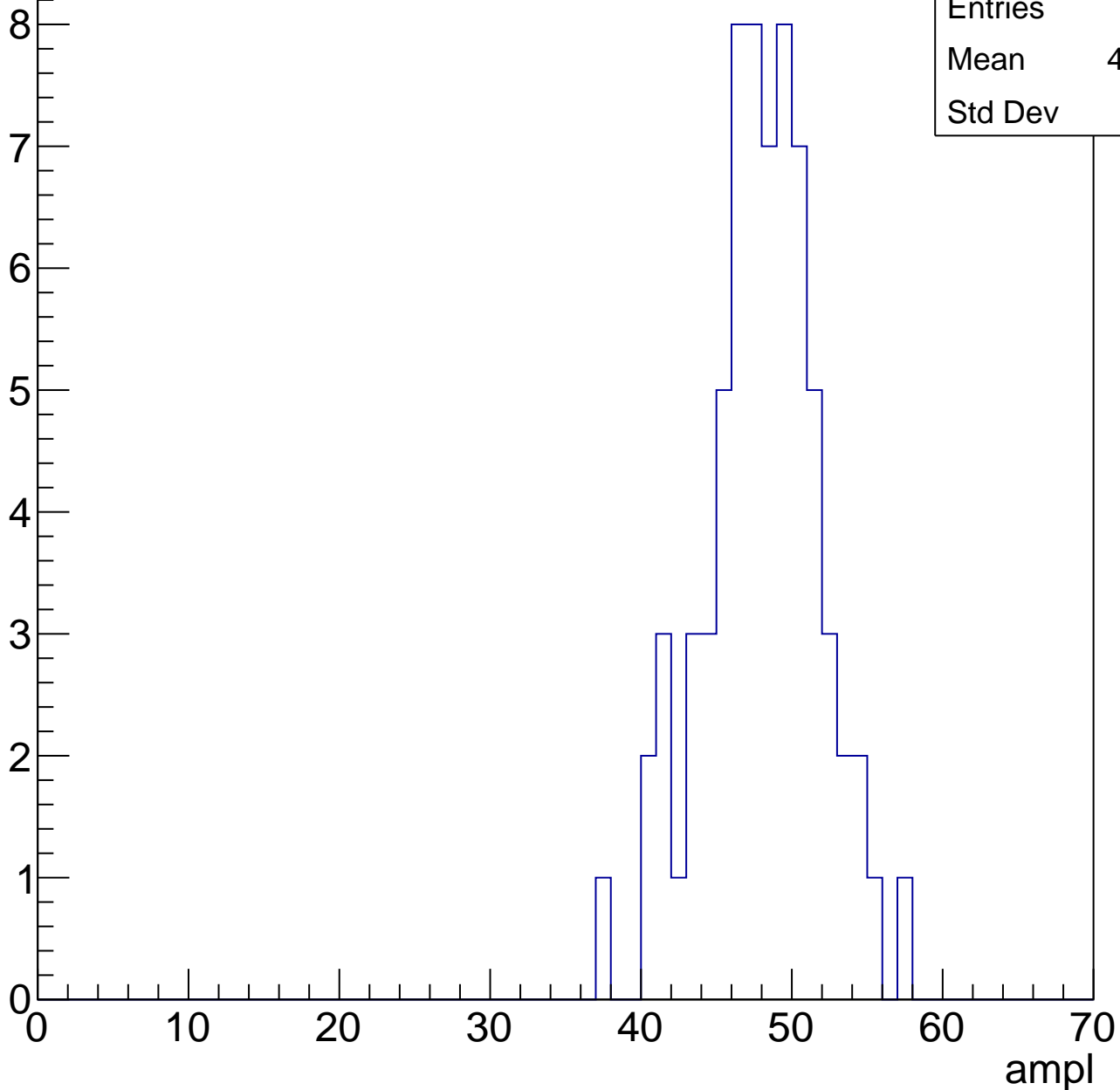


B1L003S, U19-ch106, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	47.53
Std Dev	3.79

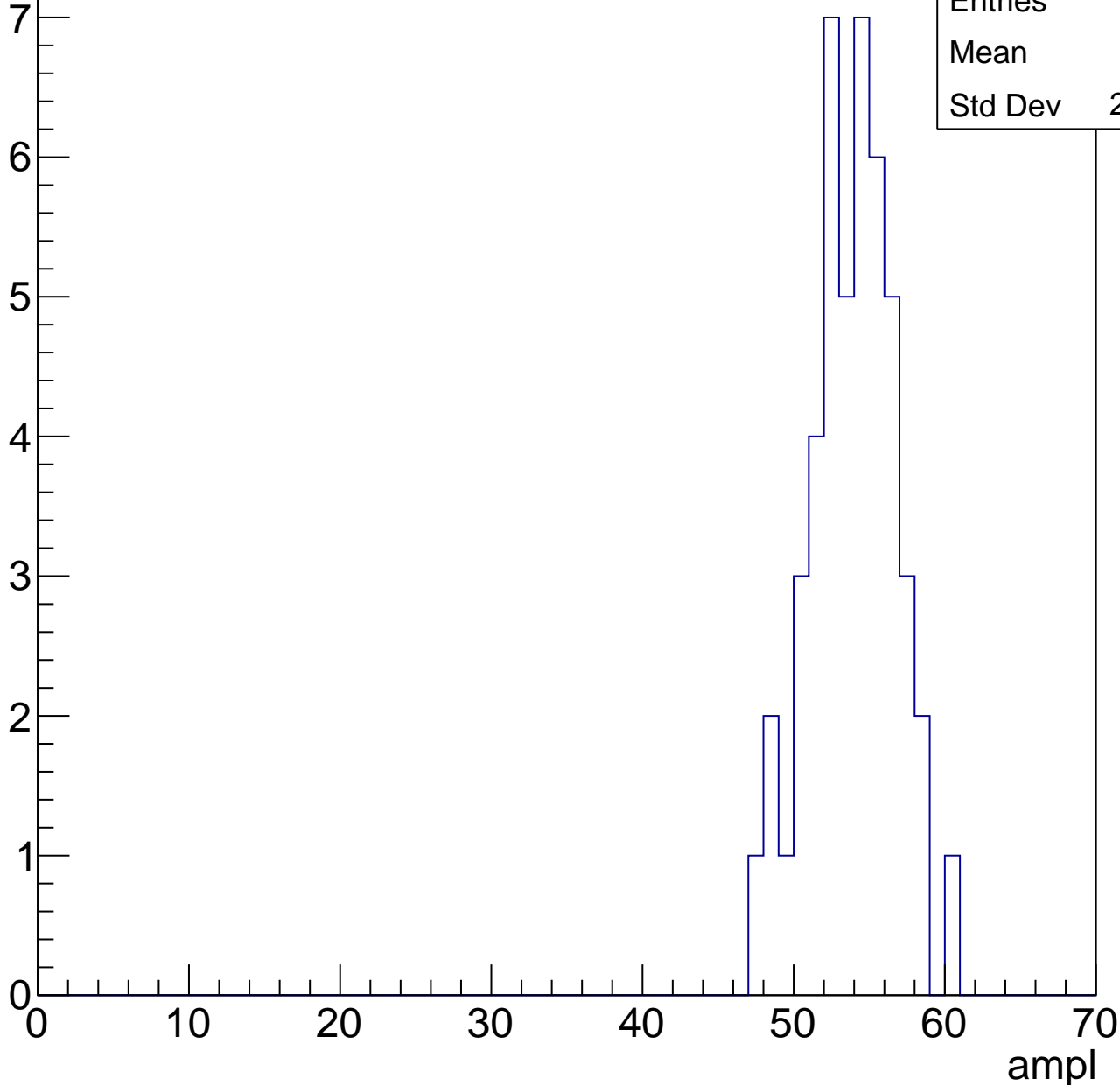


B1L003S, U19-ch106, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	53.4
Std Dev	2.796

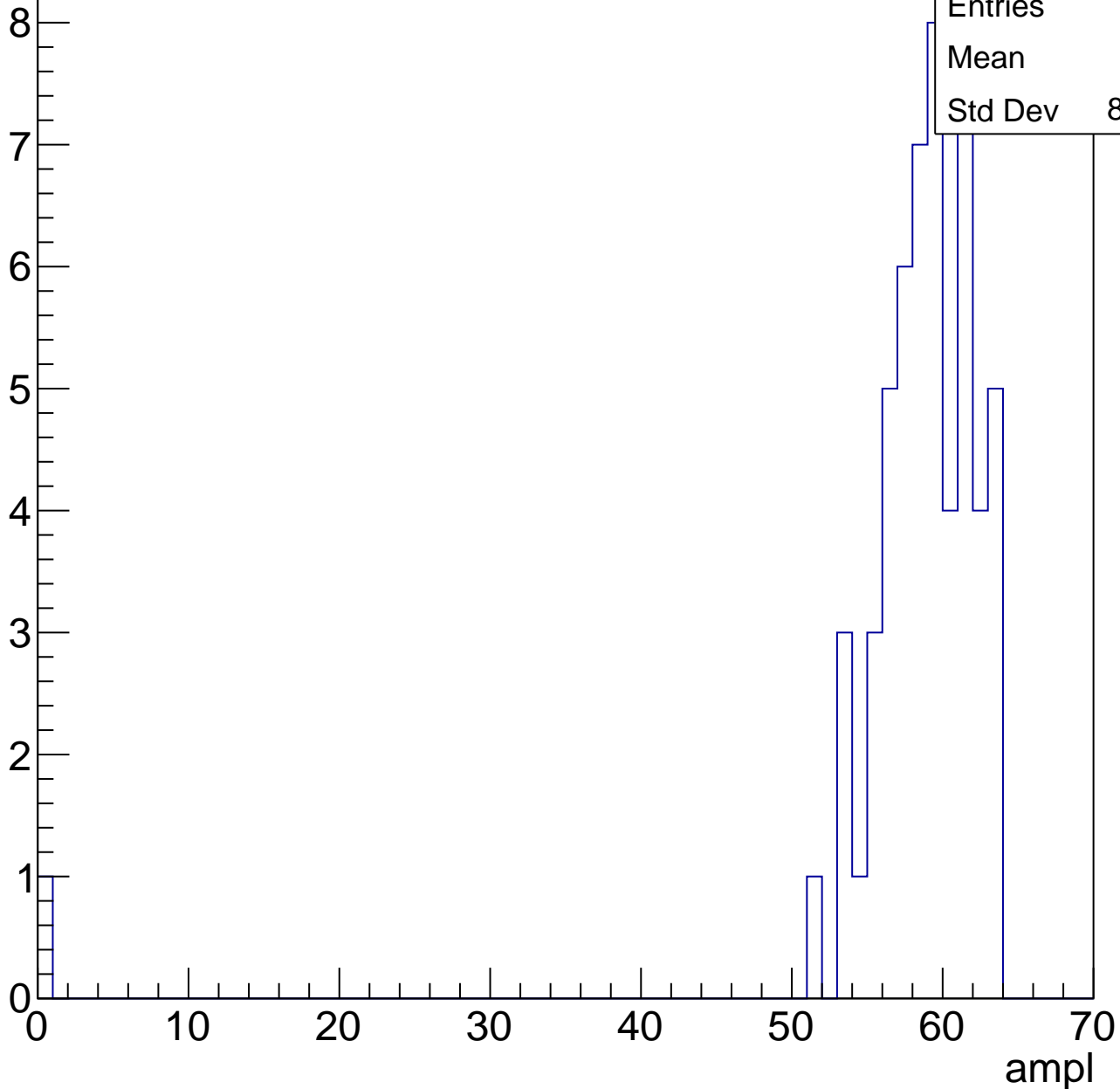


B1L003S, U19-ch106, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	57.5
Std Dev	8.268

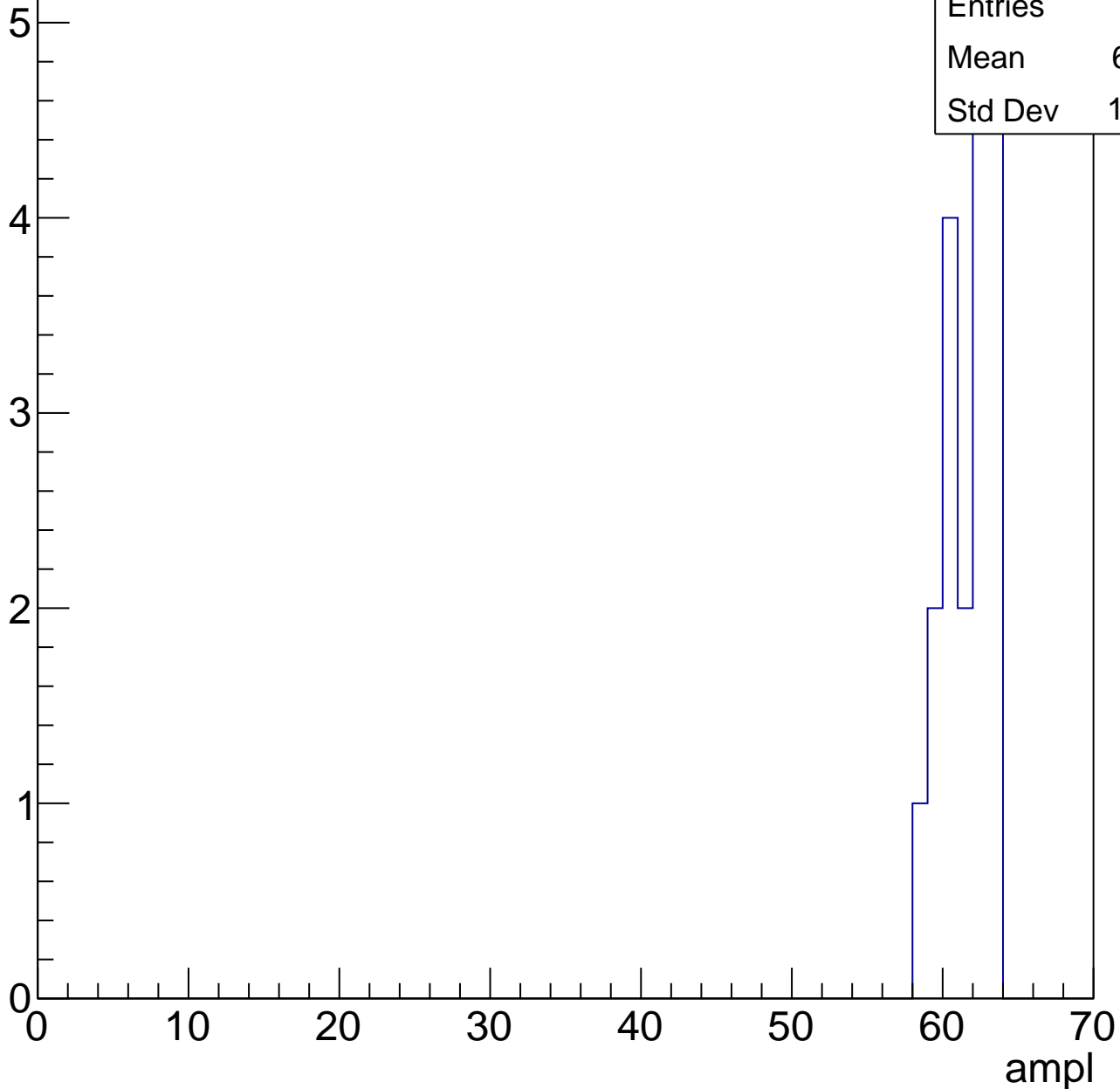


B1L003S, U19-ch106, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

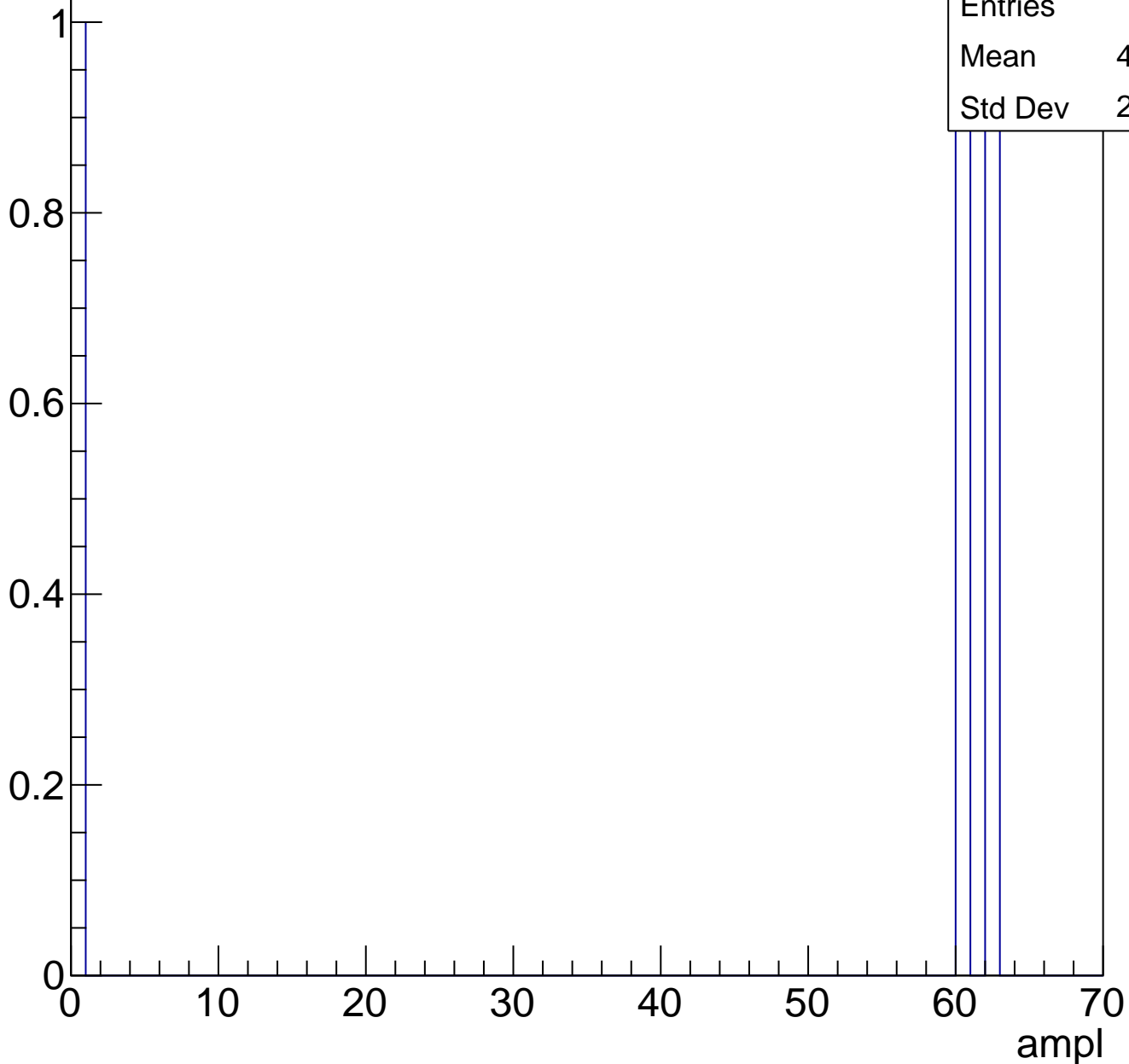
Entries	19
Mean	61.21
Std Dev	1.542



B1L003S, U19-ch106, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch107, adc0

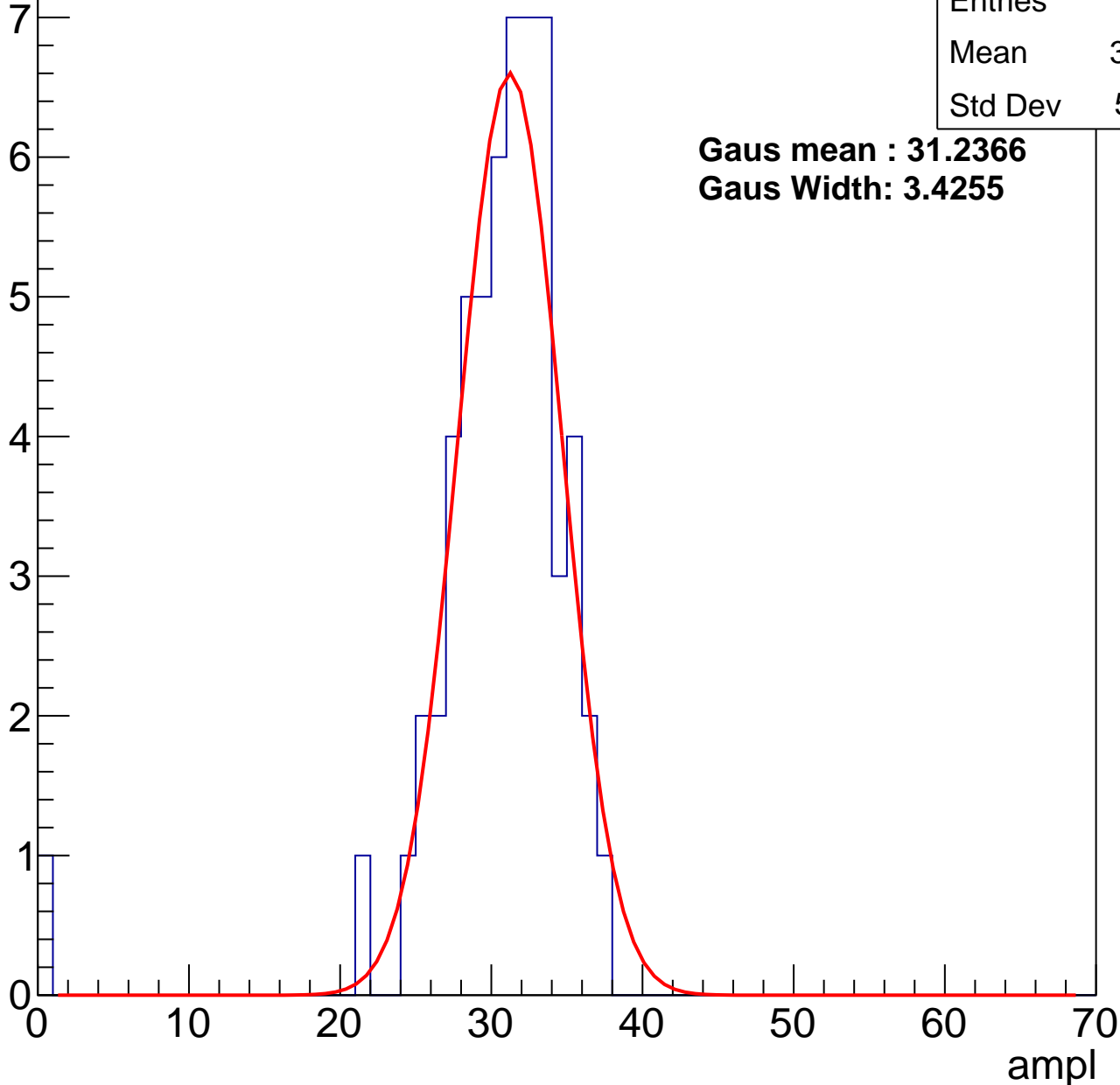
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	30.05
Std Dev	5.131

Gaus mean : 31.2366

Gaus Width: 3.4255



B1L003S, U19-ch107, adc1

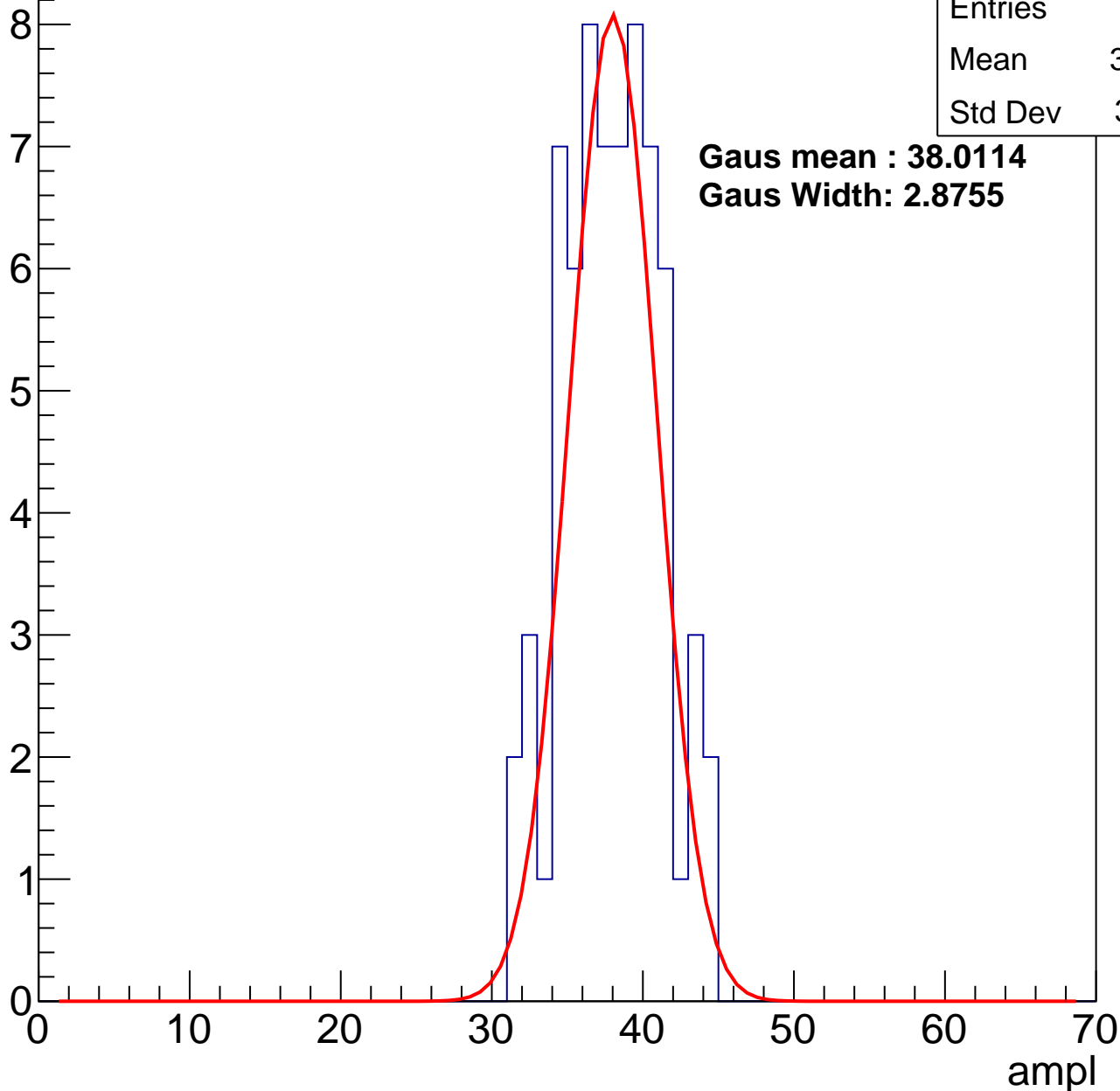
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.49
Std Dev	3.141

Gaus mean : 38.0114

Gaus Width: 2.8755



B1L003S, U19-ch107, adc2

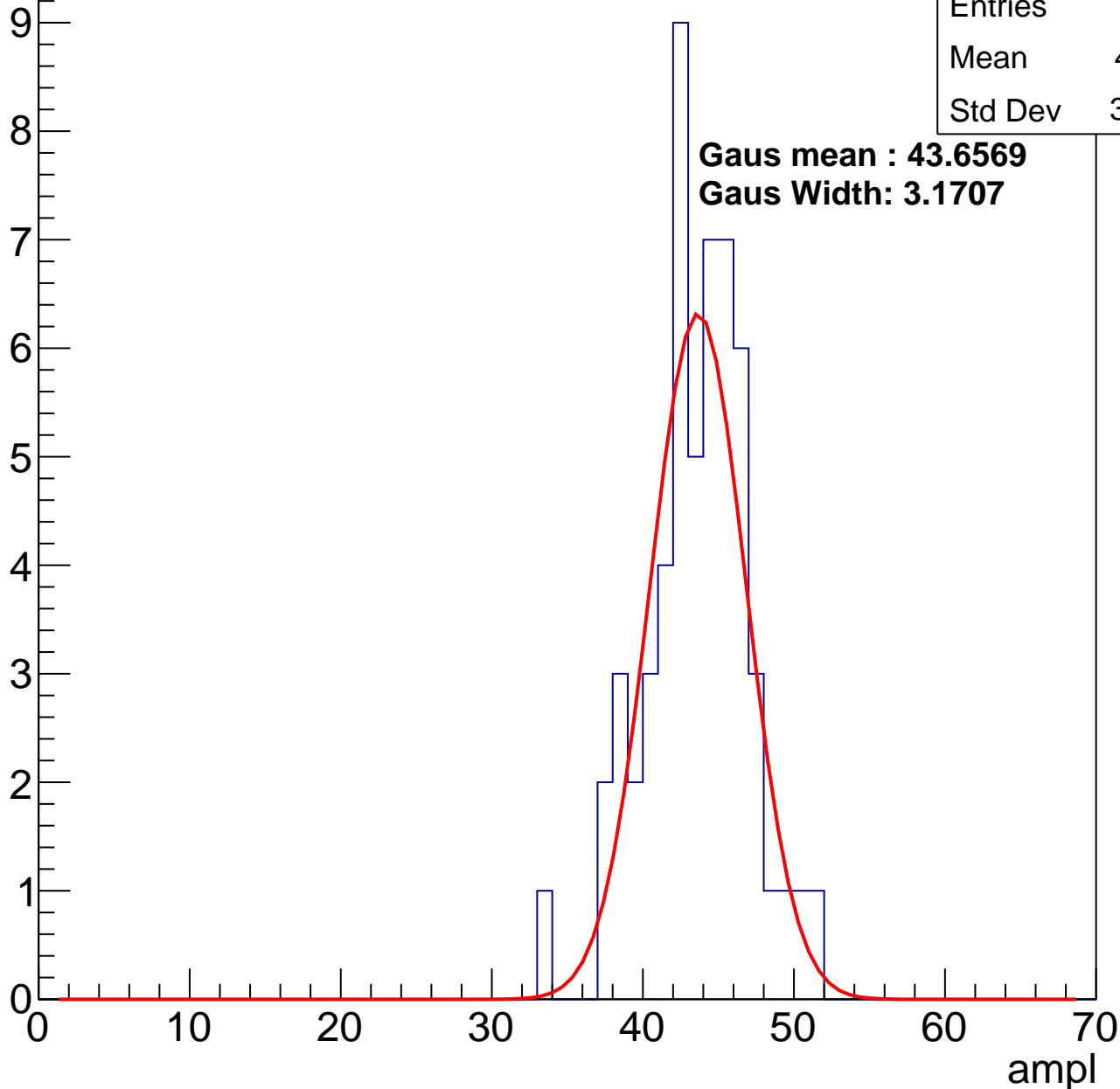
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.11
Std Dev	3.384

Gaus mean : 43.6569

Gaus Width: 3.1707

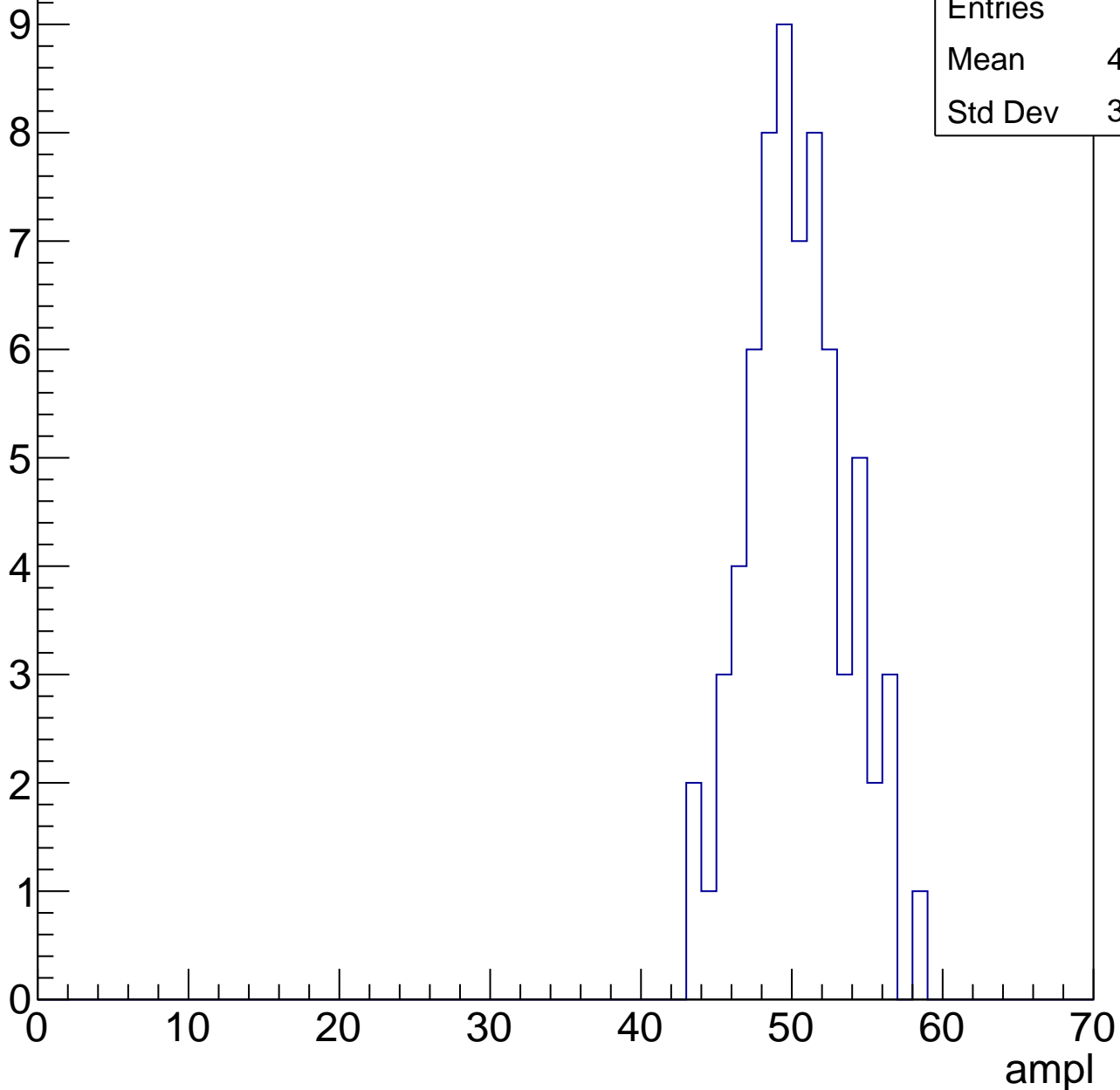


B1L003S, U19-ch107, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.87
Std Dev	3.272

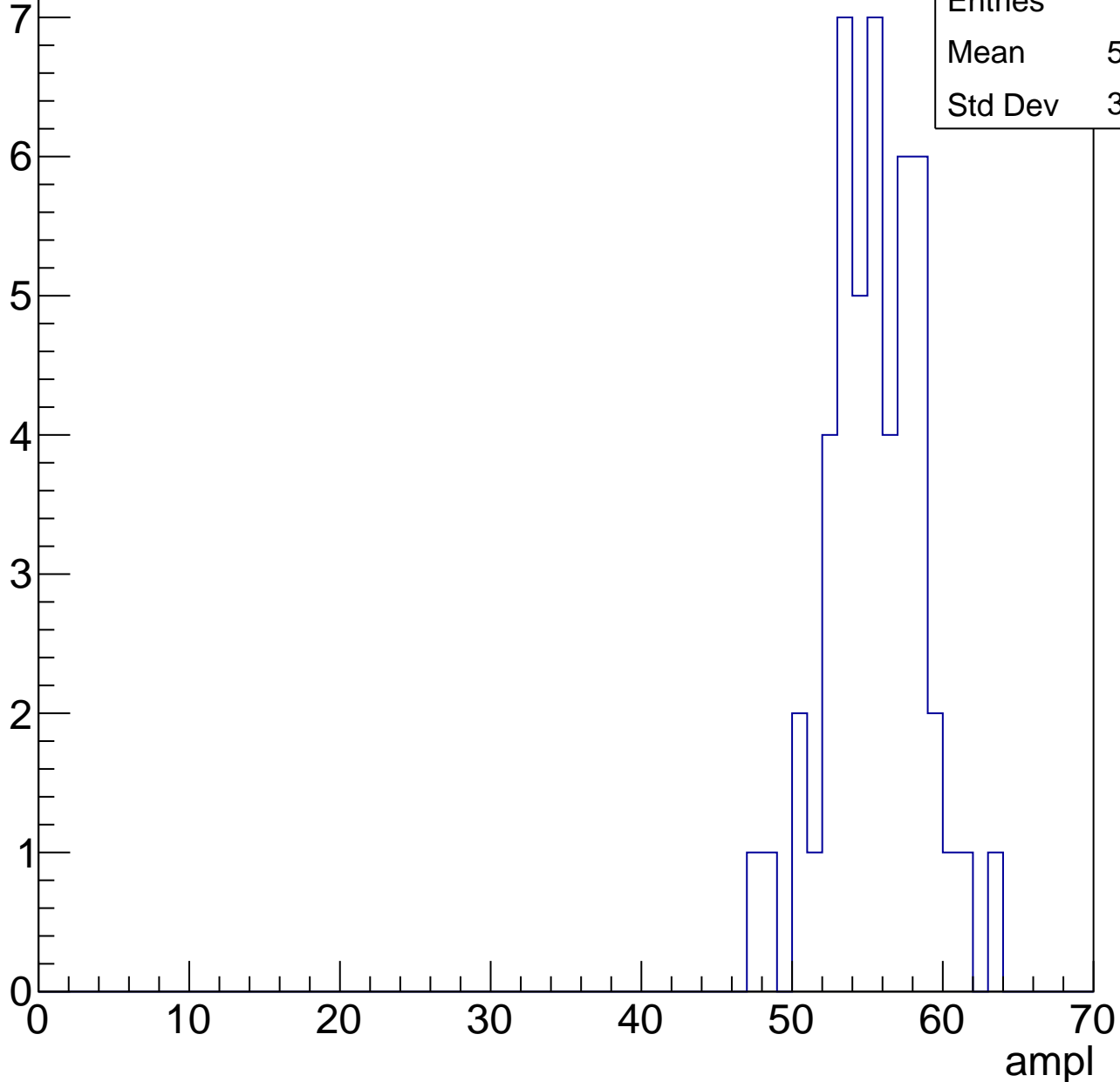


B1L003S, U19-ch107, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55.02
Std Dev	3.153

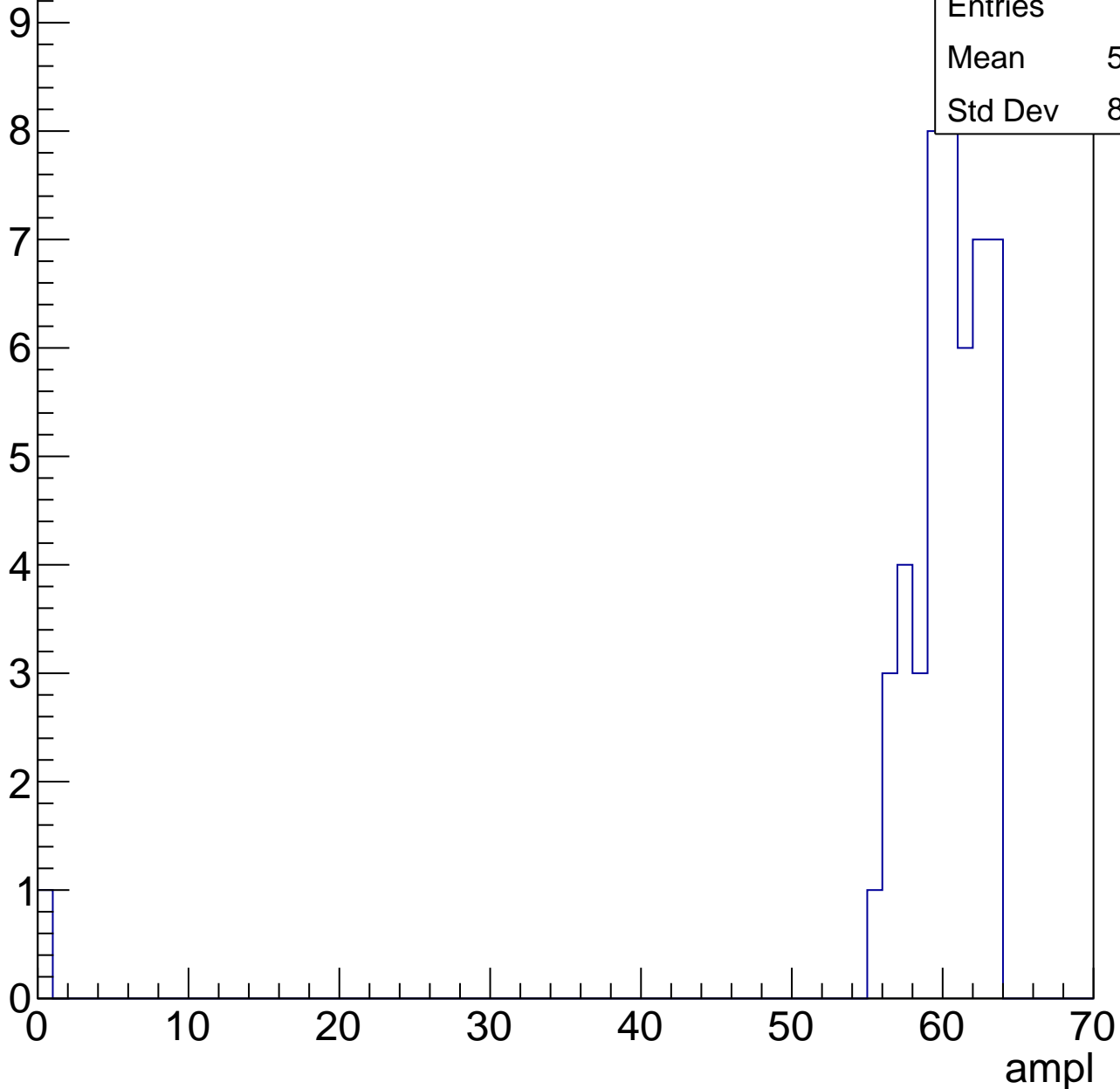


B1L003S, U19-ch107, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

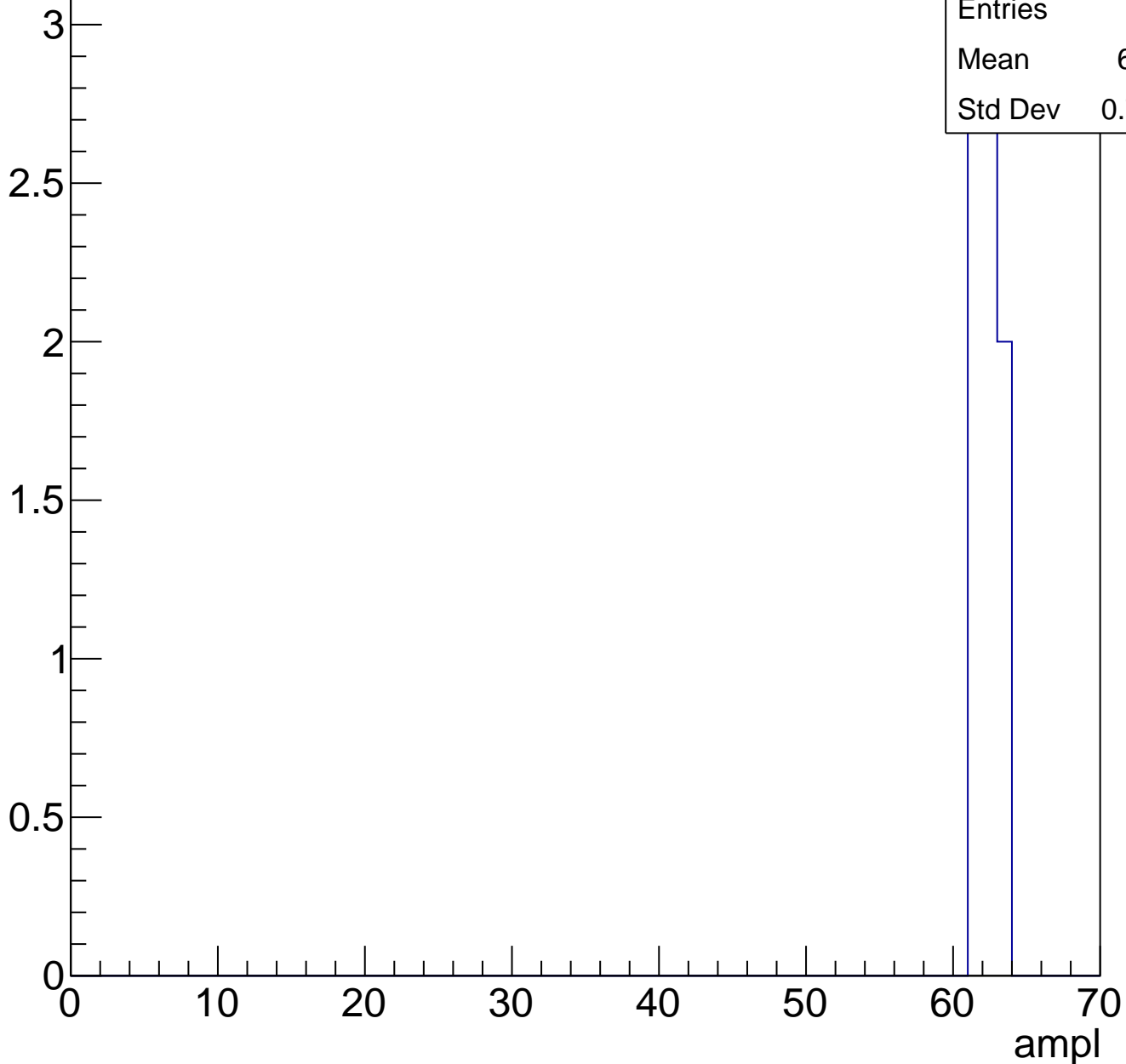
Entries	49
Mean	58.73
Std Dev	8.745



B1L003S, U19-ch107, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch107, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch108, adc0

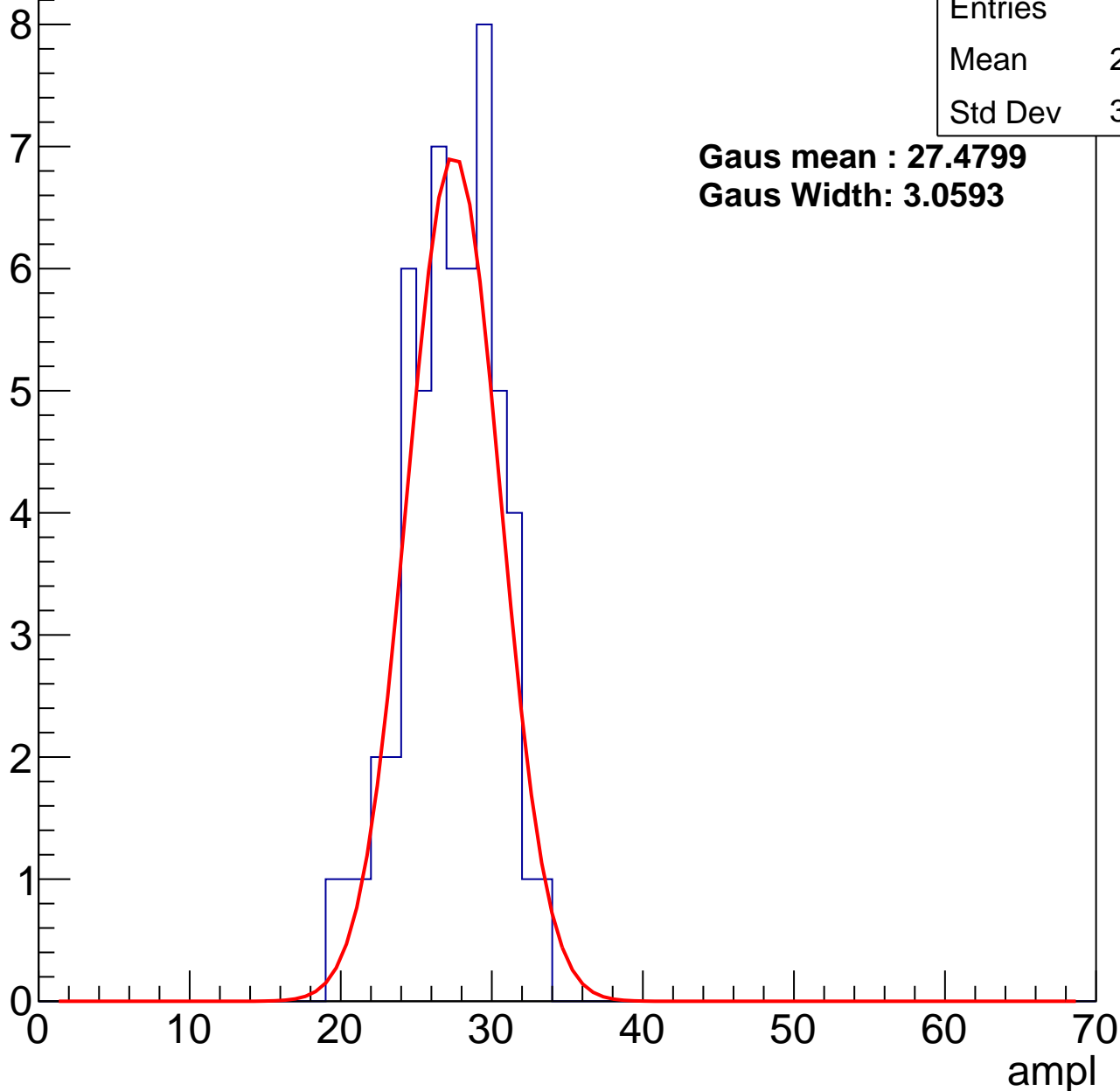
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	26.82
Std Dev	3.042

Gaus mean : 27.4799

Gaus Width: 3.0593



B1L003S, U19-ch108, adc1

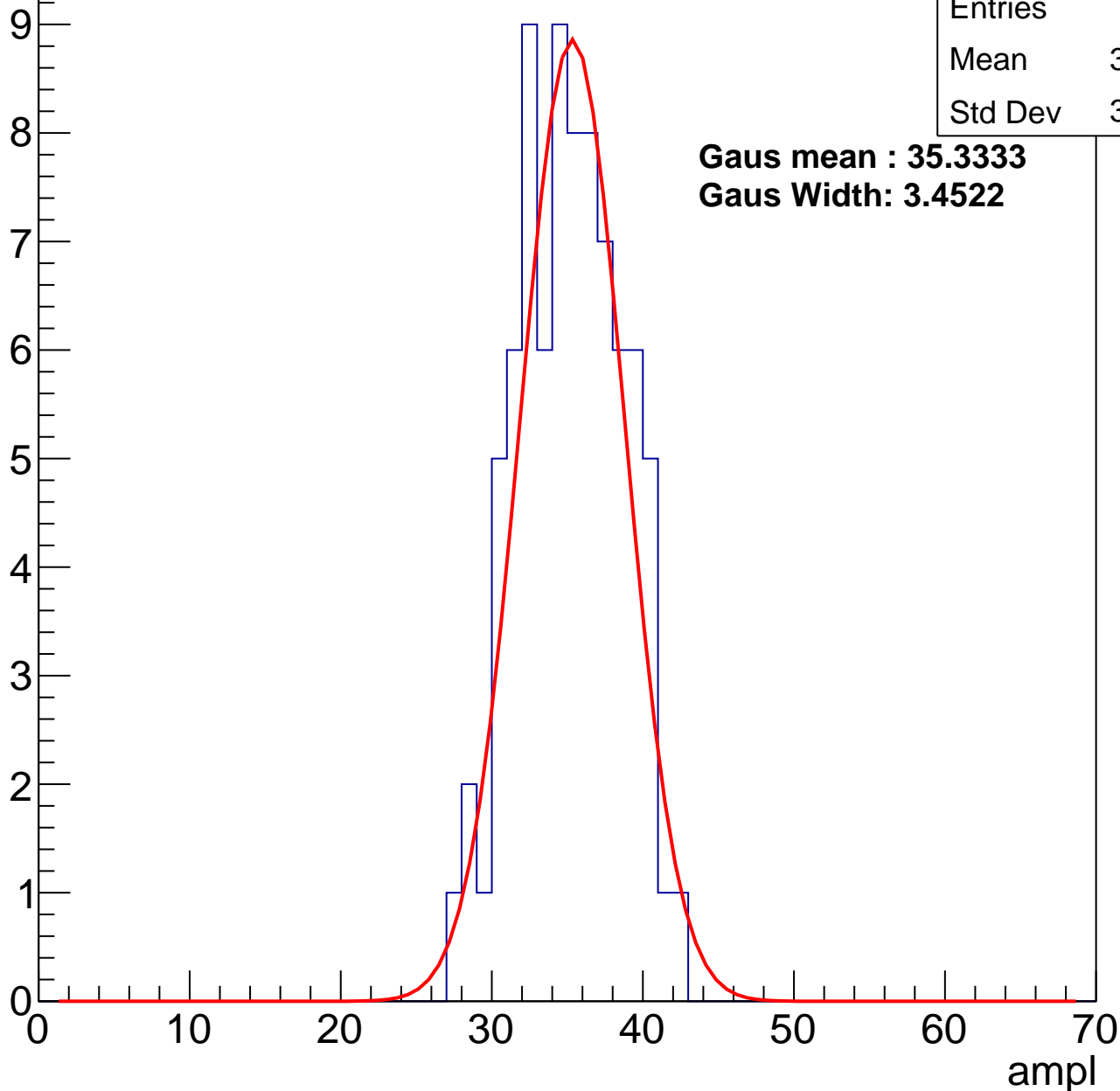
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	34.72
Std Dev	3.375

Gaus mean : 35.3333

Gaus Width: 3.4522



B1L003S, U19-ch108, adc2

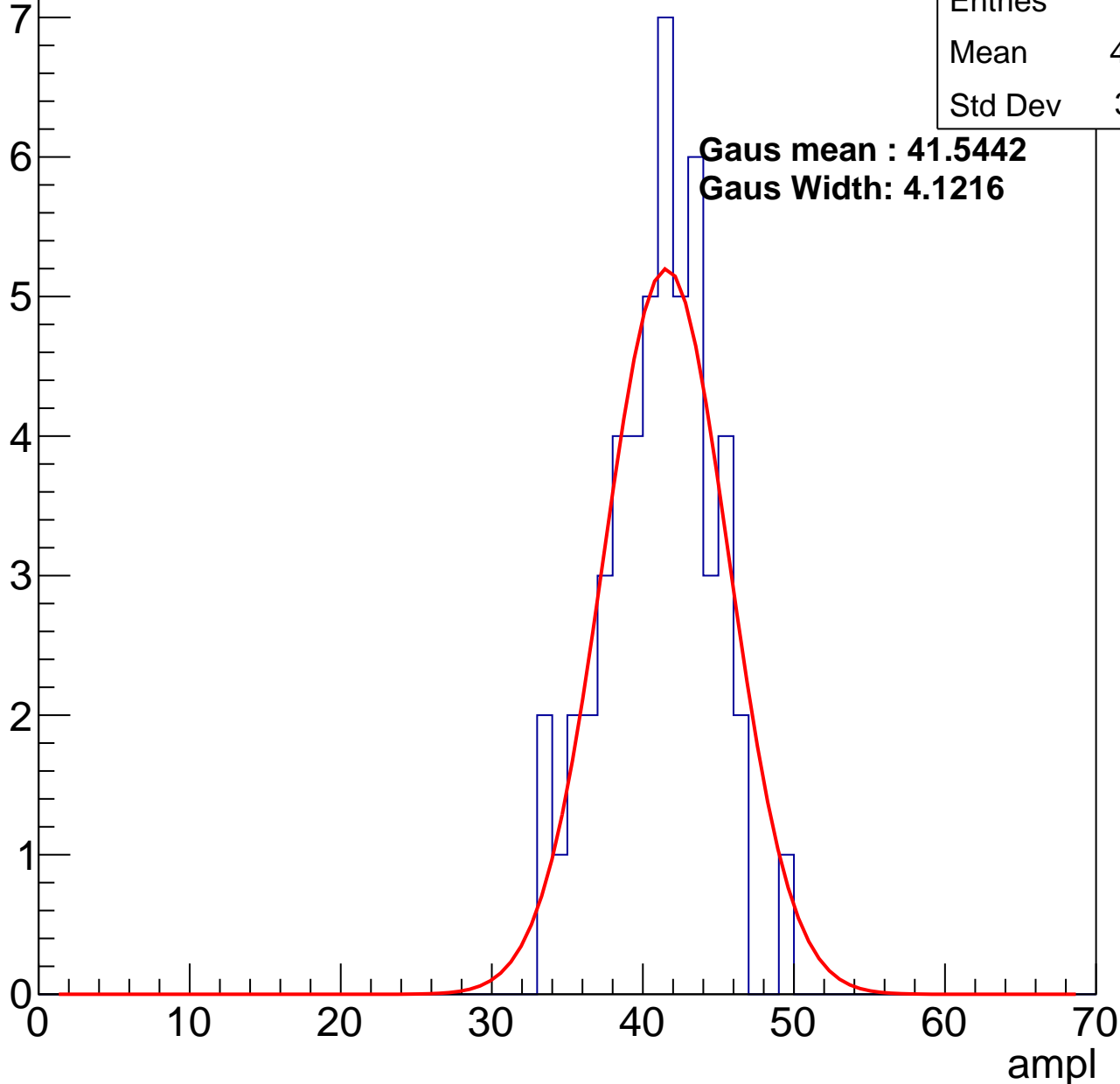
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	40.57
Std Dev	3.511

Gaus mean : 41.5442

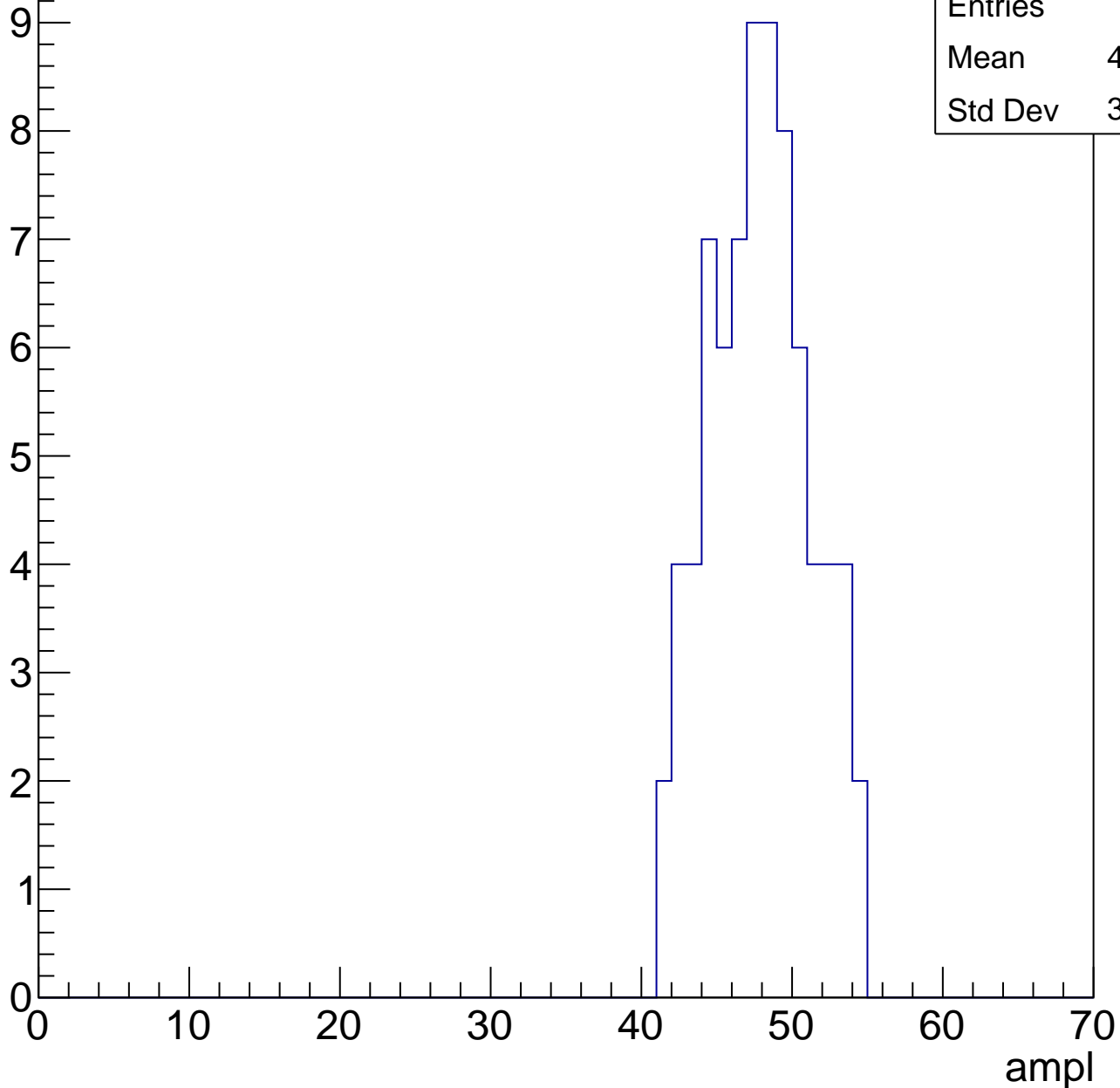
Gaus Width: 4.1216



B1L003S, U19-ch108, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



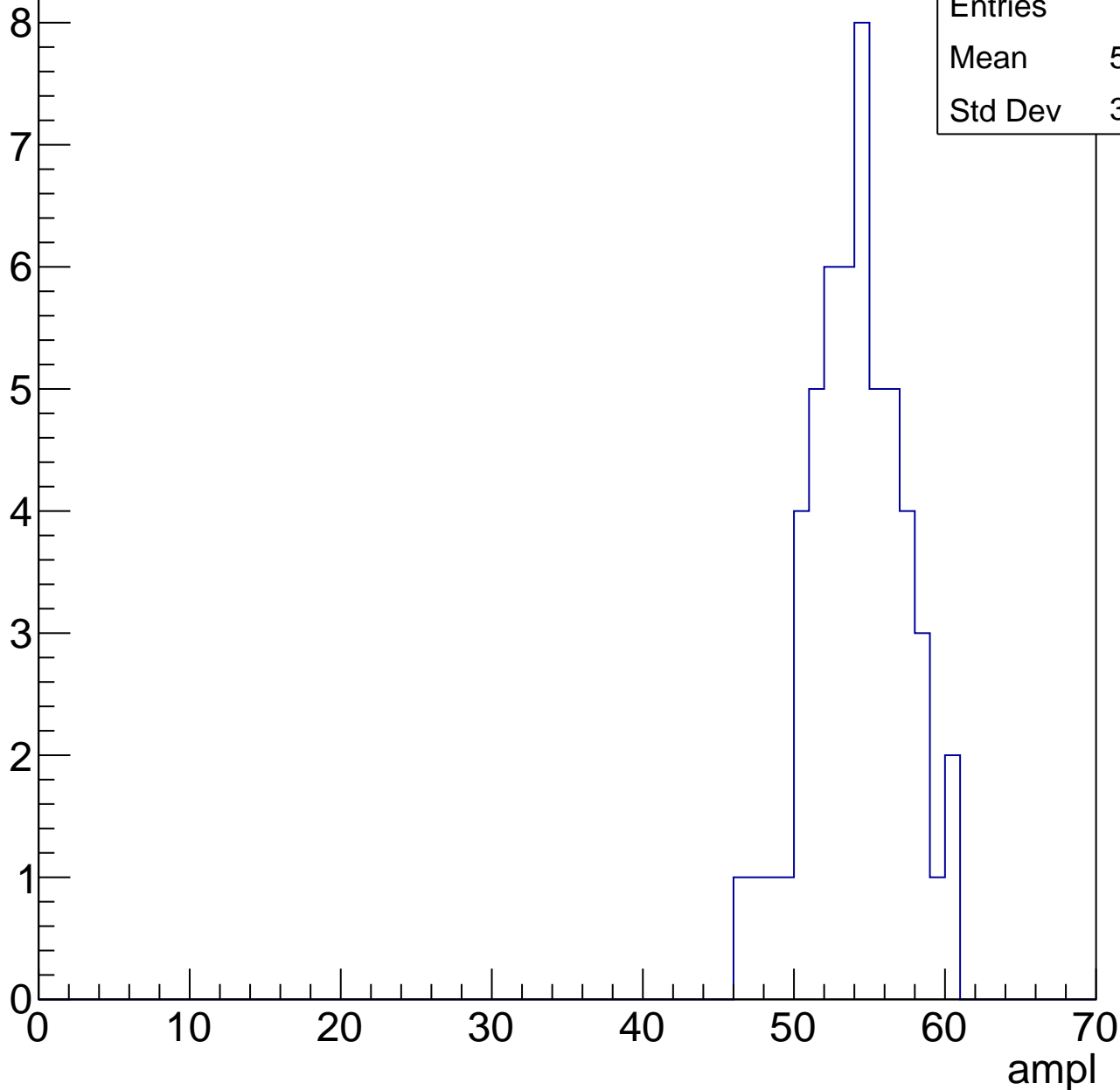
Entries	76
Mean	47.38
Std Dev	3.285

B1L003S, U19-ch108, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	53.64
Std Dev	3.109

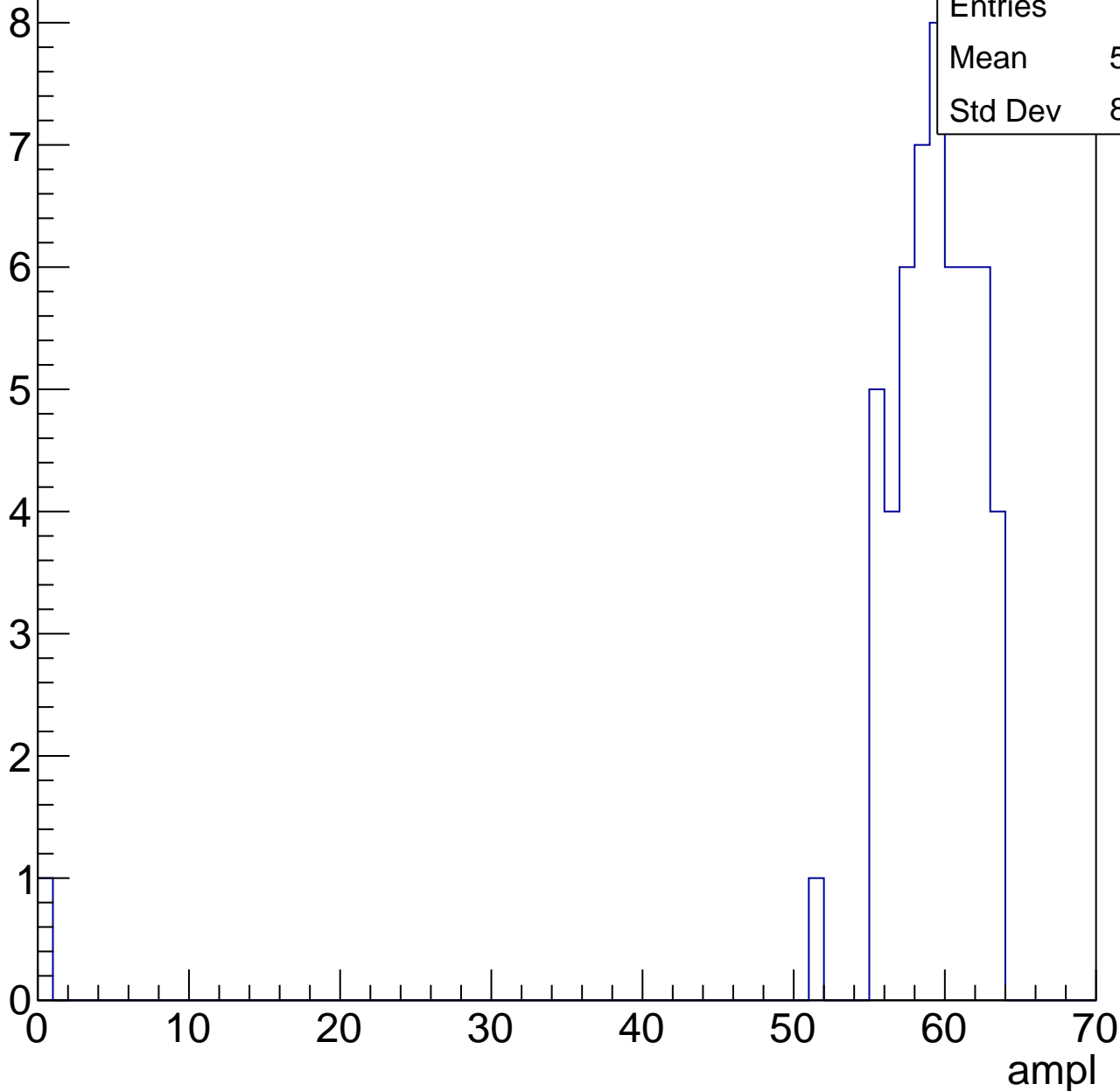


B1L003S, U19-ch108, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	57.78
Std Dev	8.344

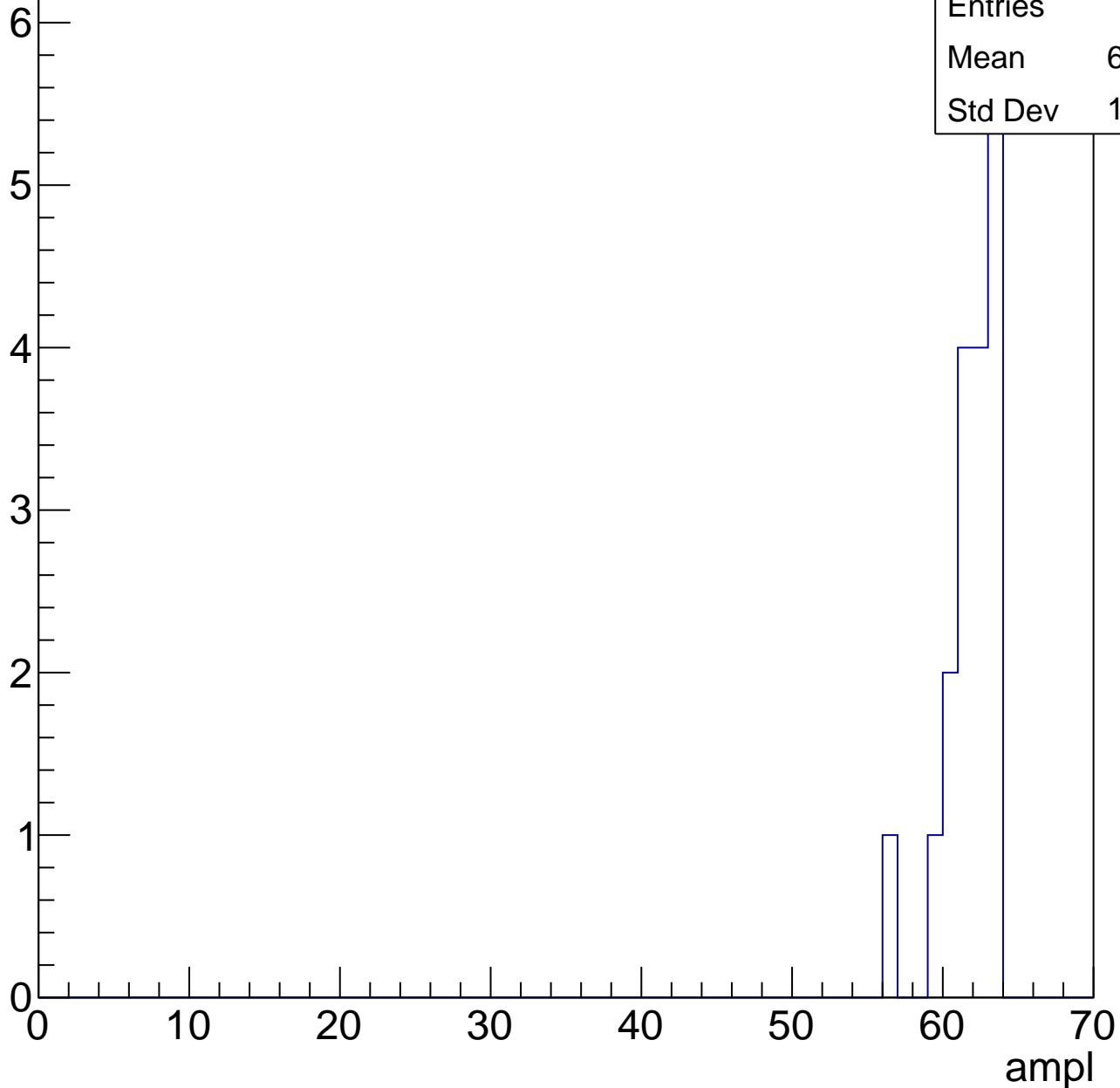


B1L003S, U19-ch108, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	18
Mean	61.39
Std Dev	1.768



B1L003S, U19-ch108, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch109, adc0

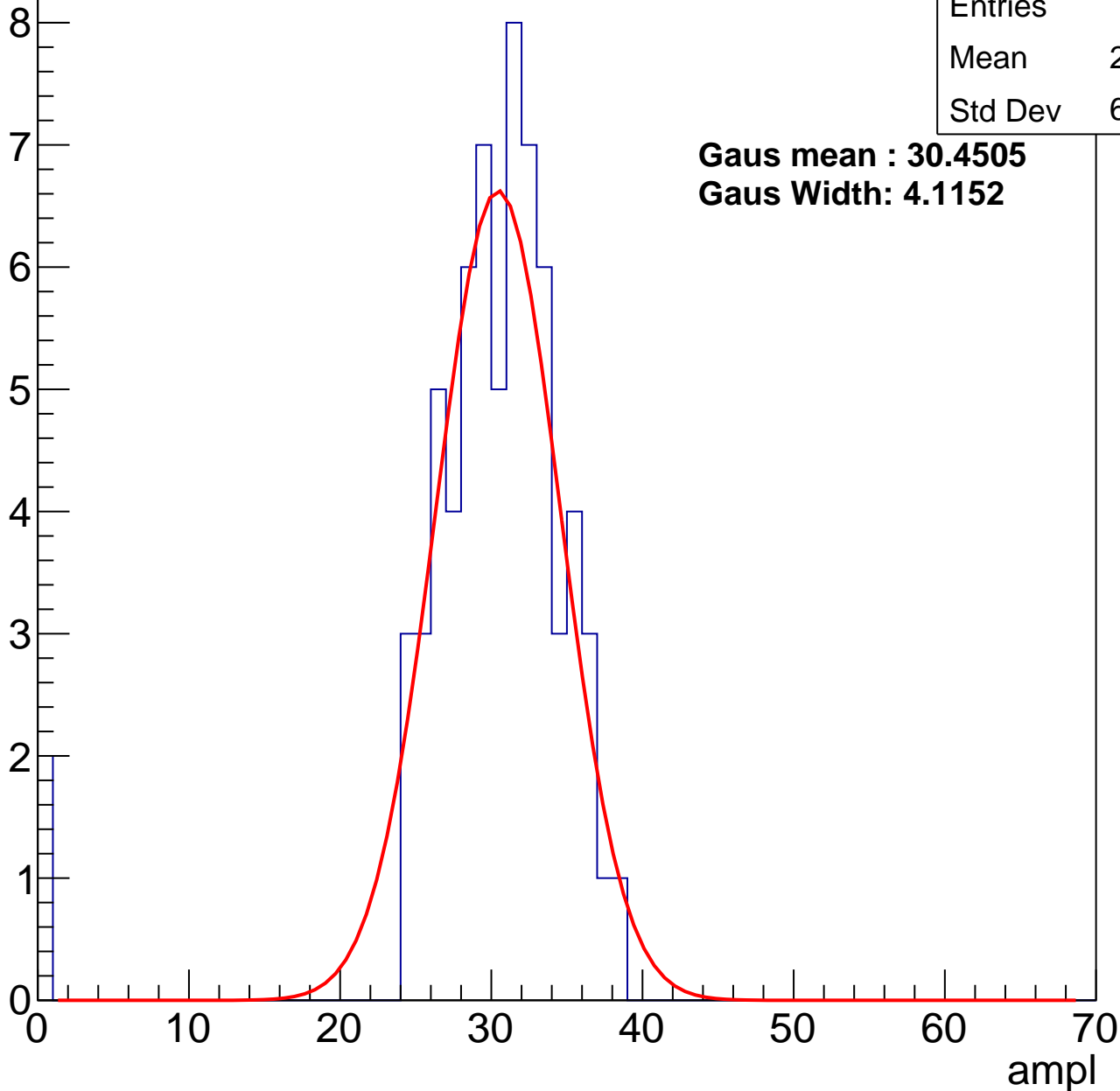
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.43
Std Dev	6.144

Gaus mean : 30.4505

Gaus Width: 4.1152



B1L003S, U19-ch109, adc1

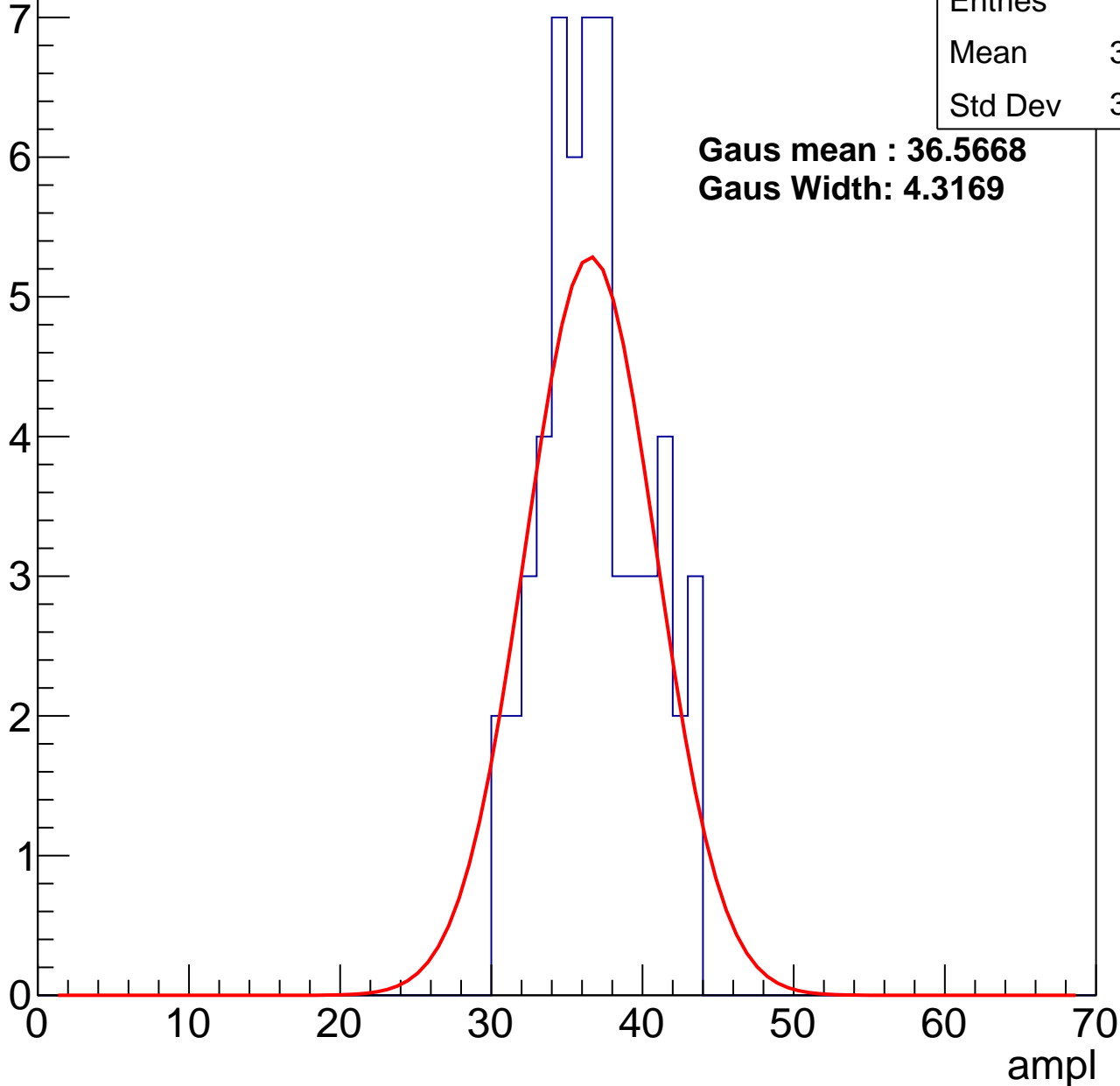
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	36.38
Std Dev	3.394

Gaus mean : 36.5668

Gaus Width: 4.3169



B1L003S, U19-ch109, adc2

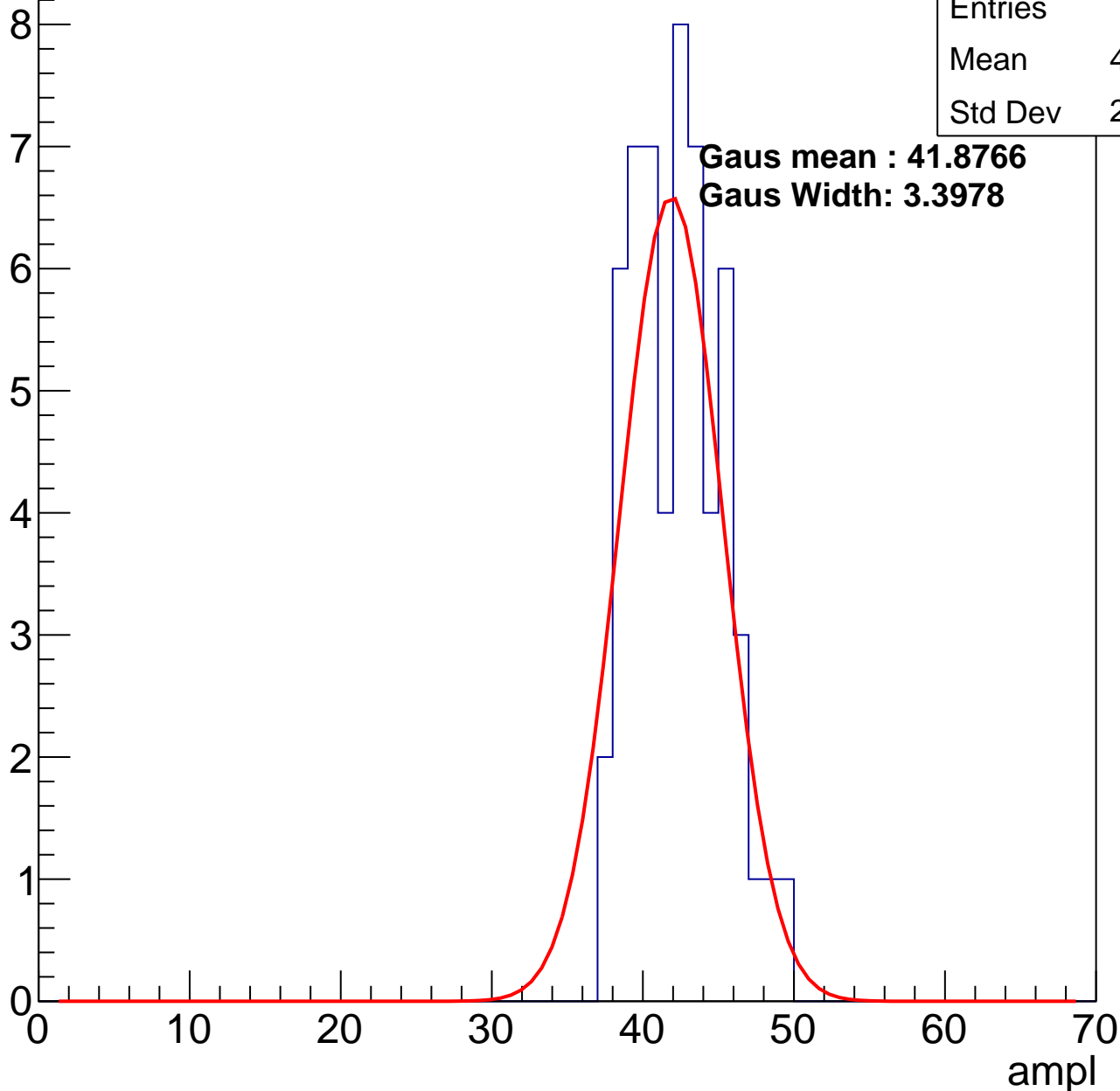
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	41.82
Std Dev	2.884

Gaus mean : 41.8766

Gaus Width: 3.3978

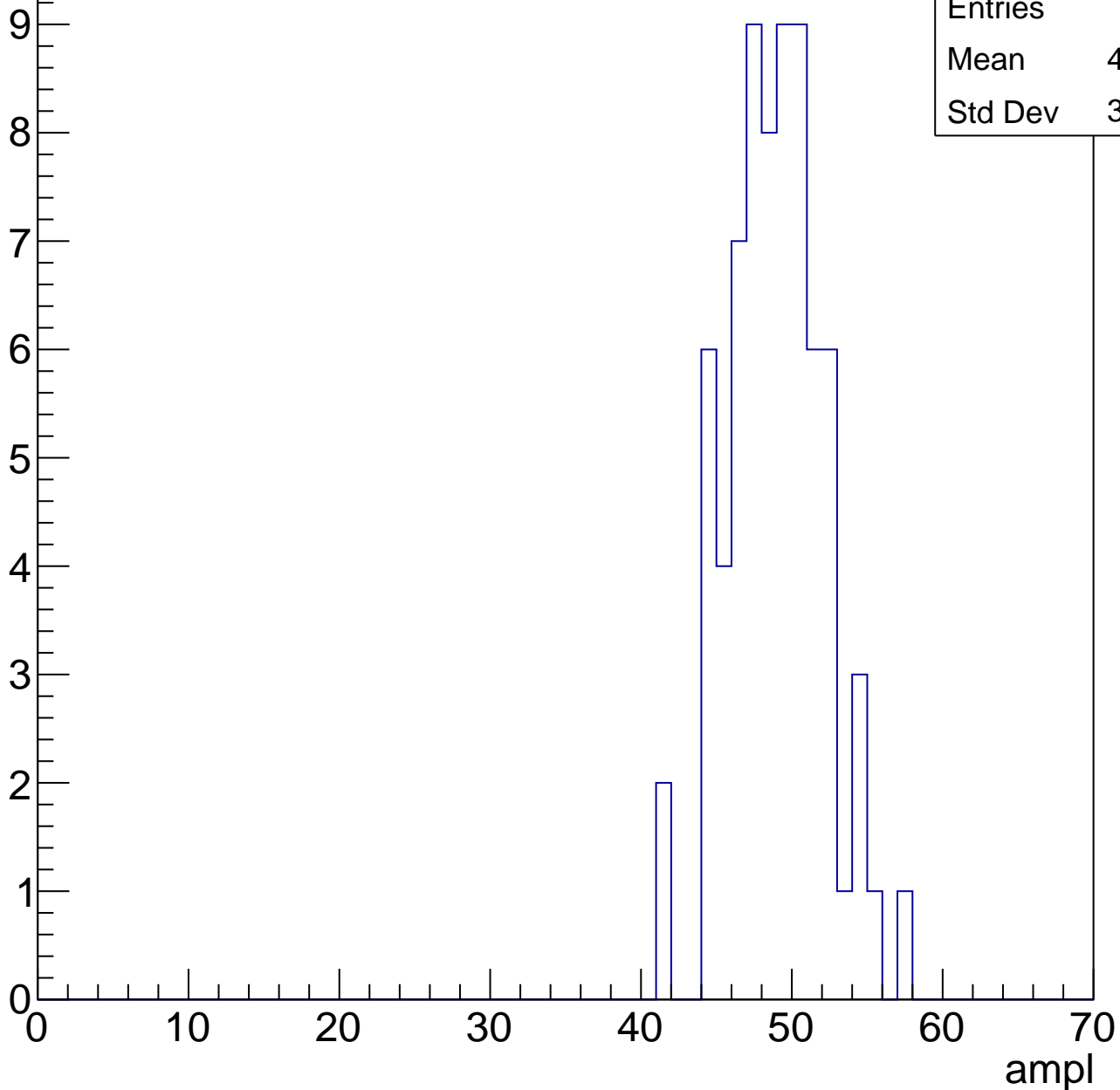


B1L003S, U19-ch109, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	48.49
Std Dev	3.136

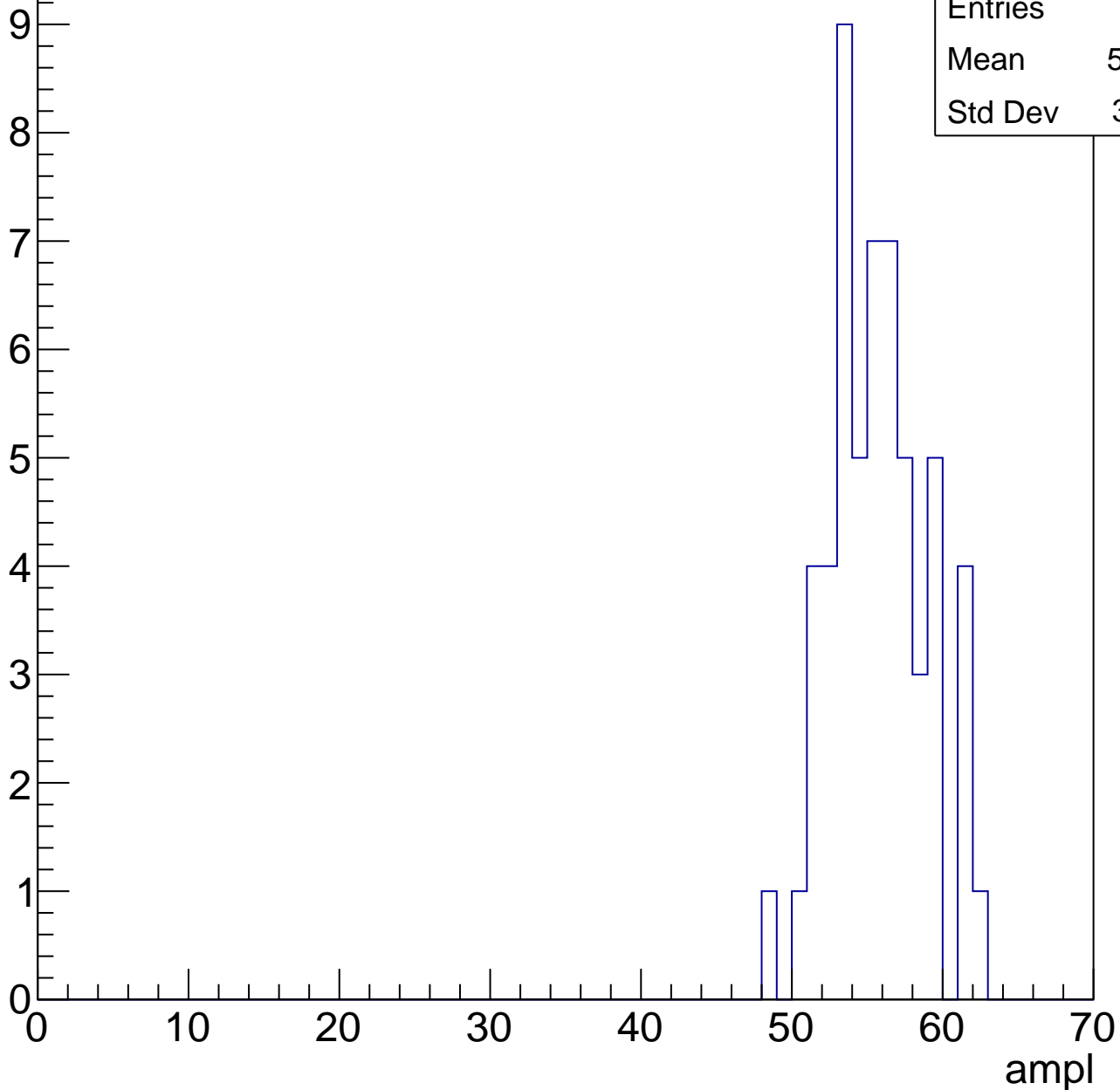


B1L003S, U19-ch109, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	55.25
Std Dev	3.101

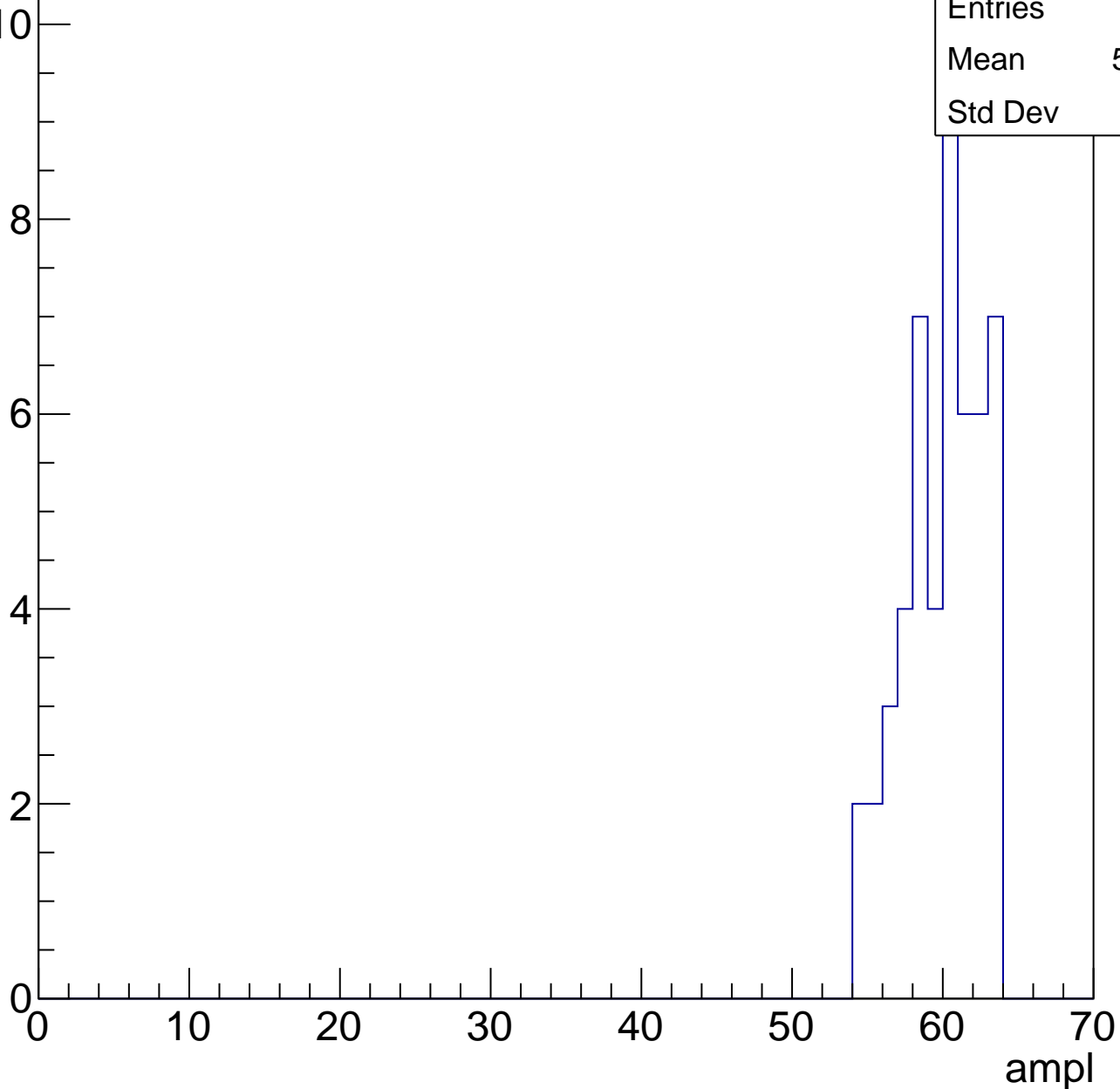


B1L003S, U19-ch109, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	59.51
Std Dev	2.5



B1L003S, U19-ch109, adc6

calib_packv5_042523_0143.root, FC#13, port D2

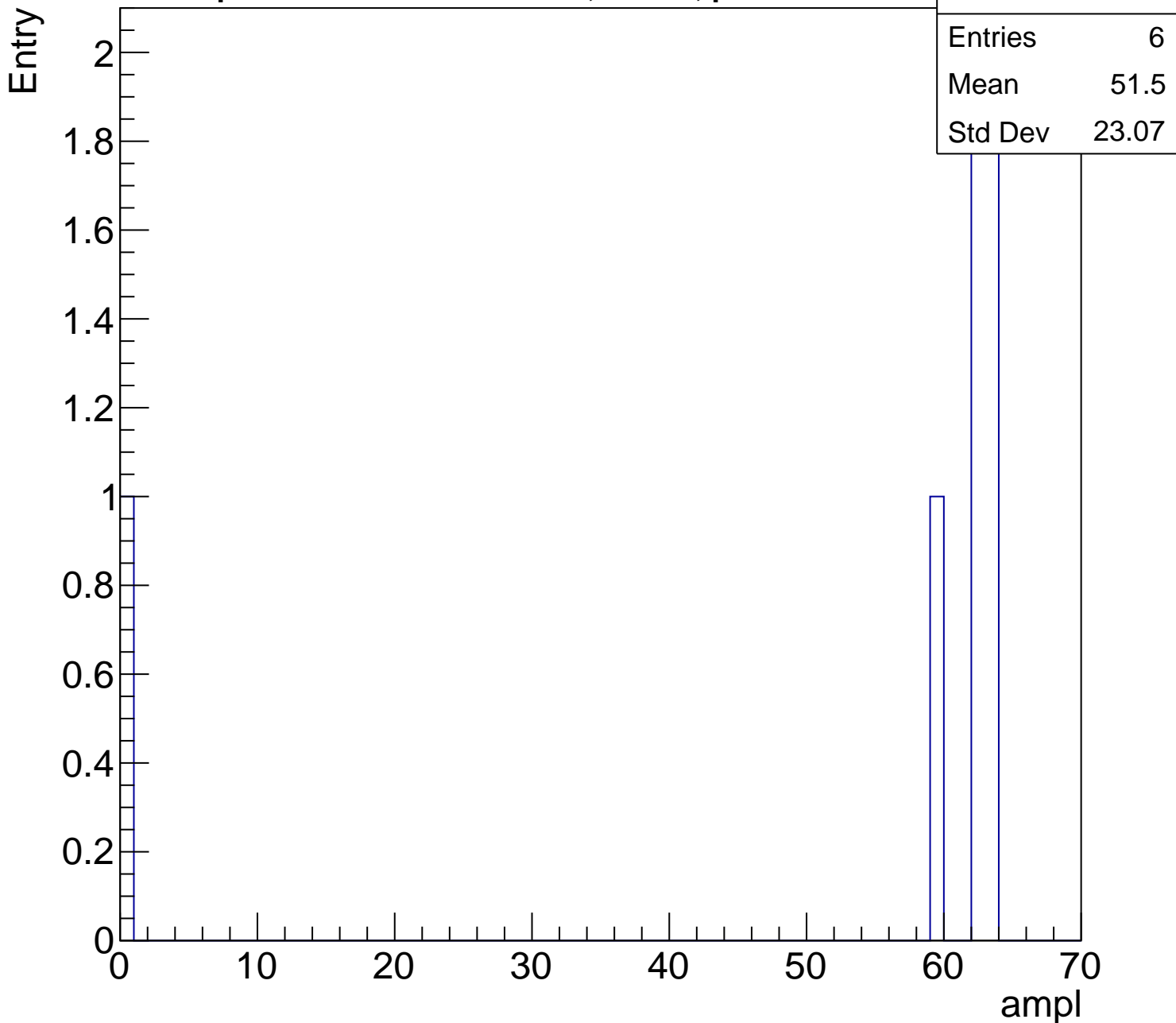
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.5
Std Dev	23.07

ampl

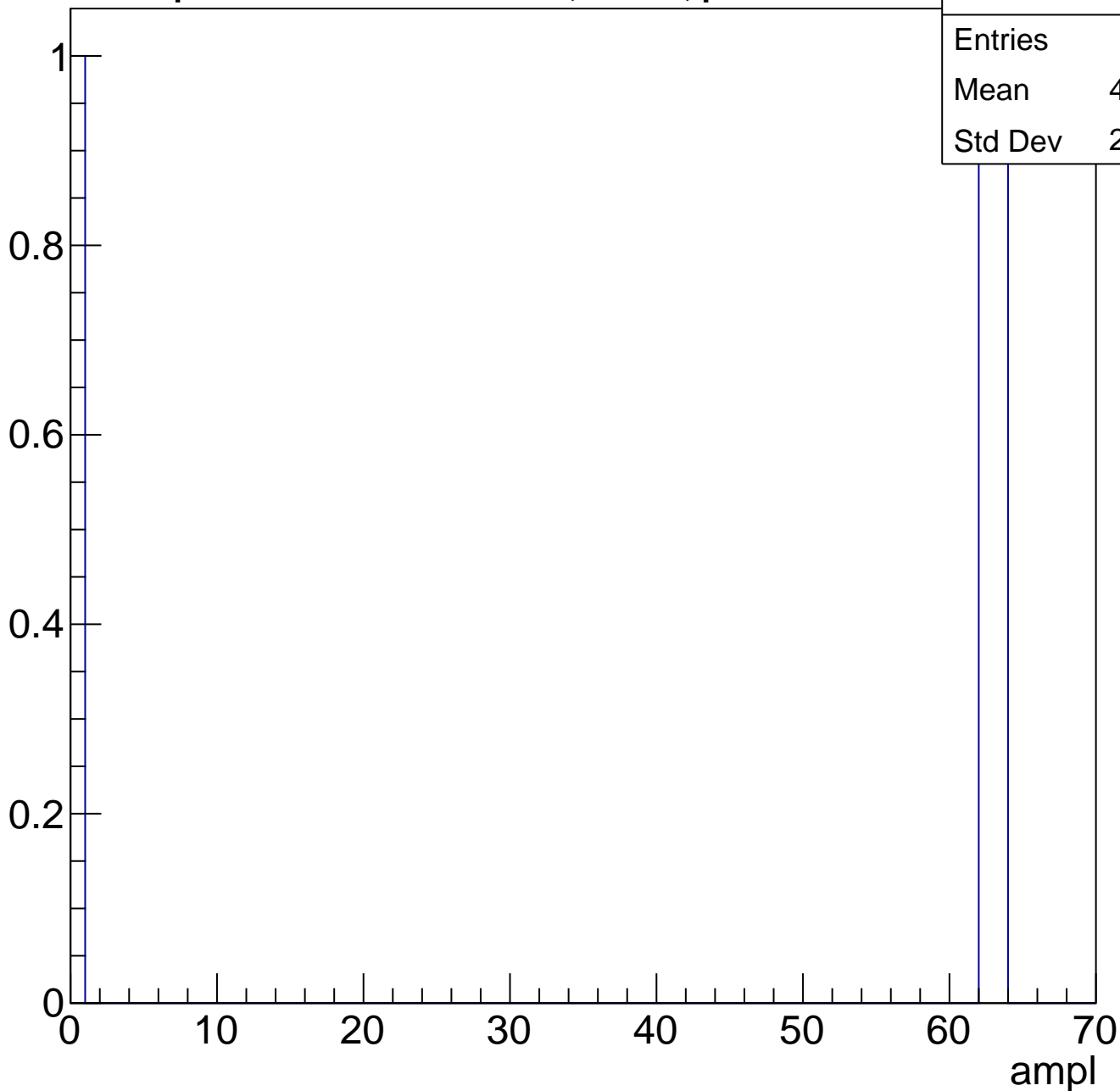
0 10 20 30 40 50 60 70



B1L003S, U19-ch109, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch110, adc0

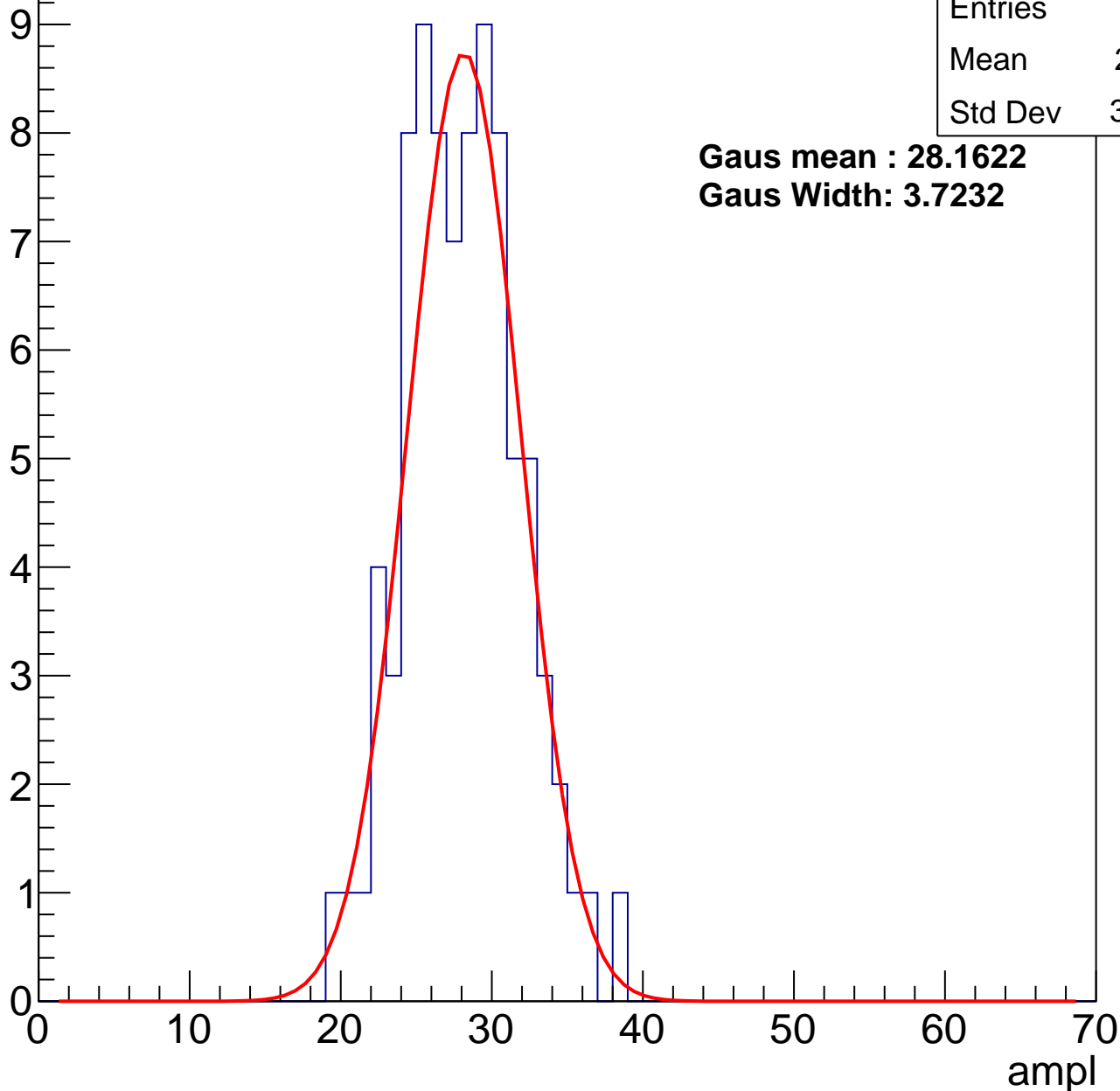
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	27.61
Std Dev	3.717

Gaus mean : 28.1622

Gaus Width: 3.7232



B1L003S, U19-ch110, adc1

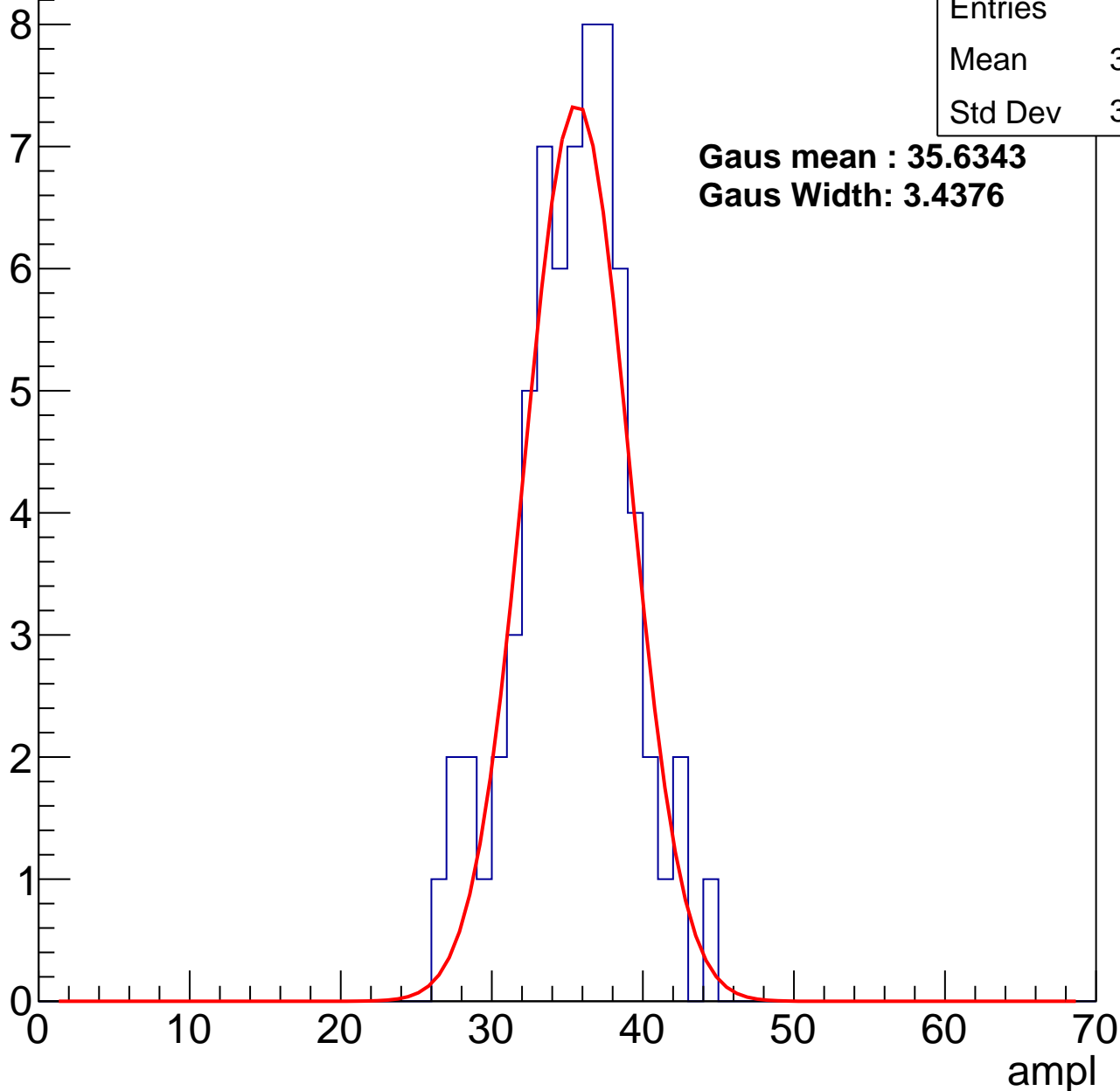
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	34.93
Std Dev	3.723

Gaus mean : 35.6343

Gaus Width: 3.4376



B1L003S, U19-ch110, adc2

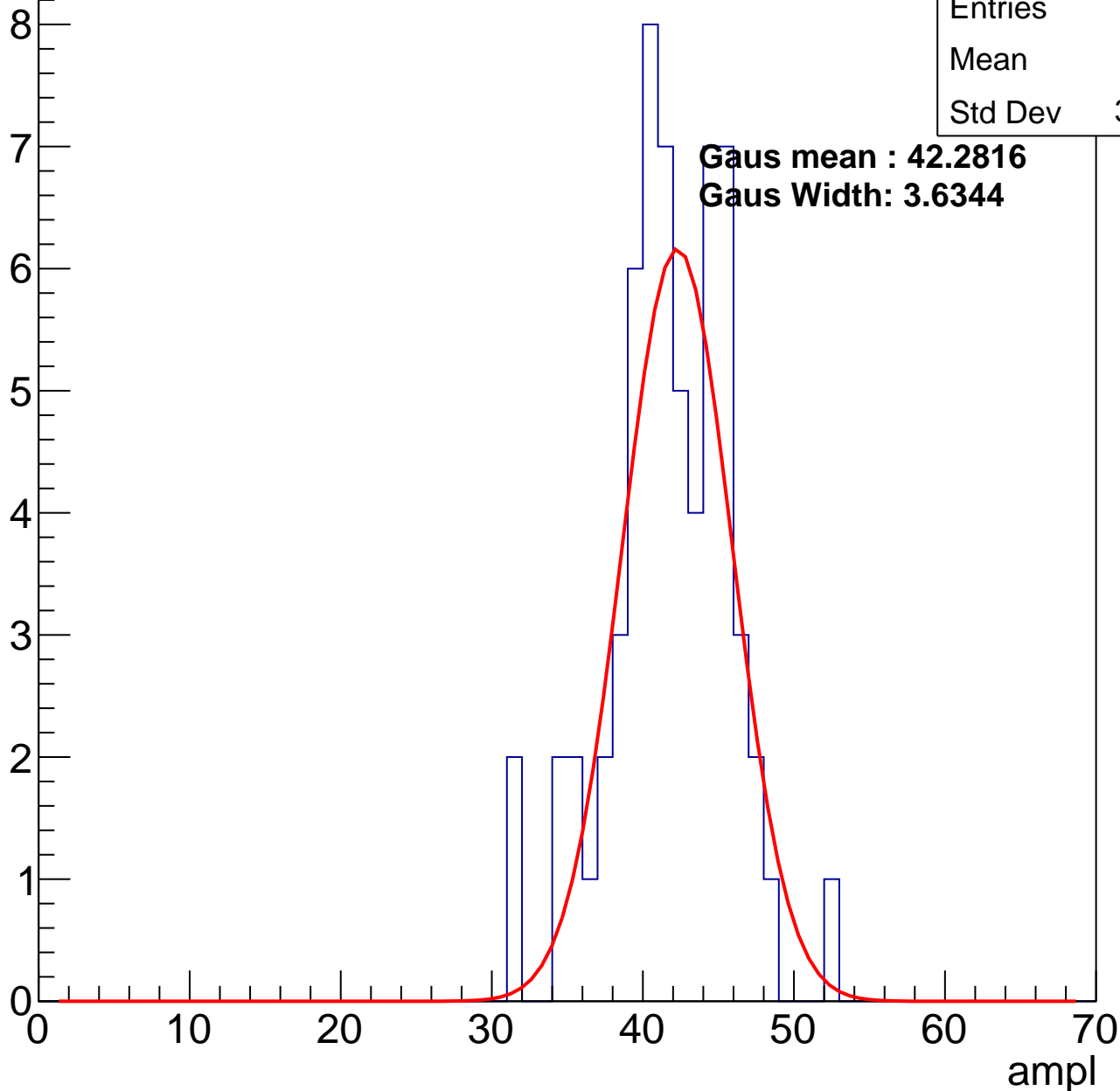
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	41.3
Std Dev	3.971

Gaus mean : 42.2816

Gaus Width: 3.6344

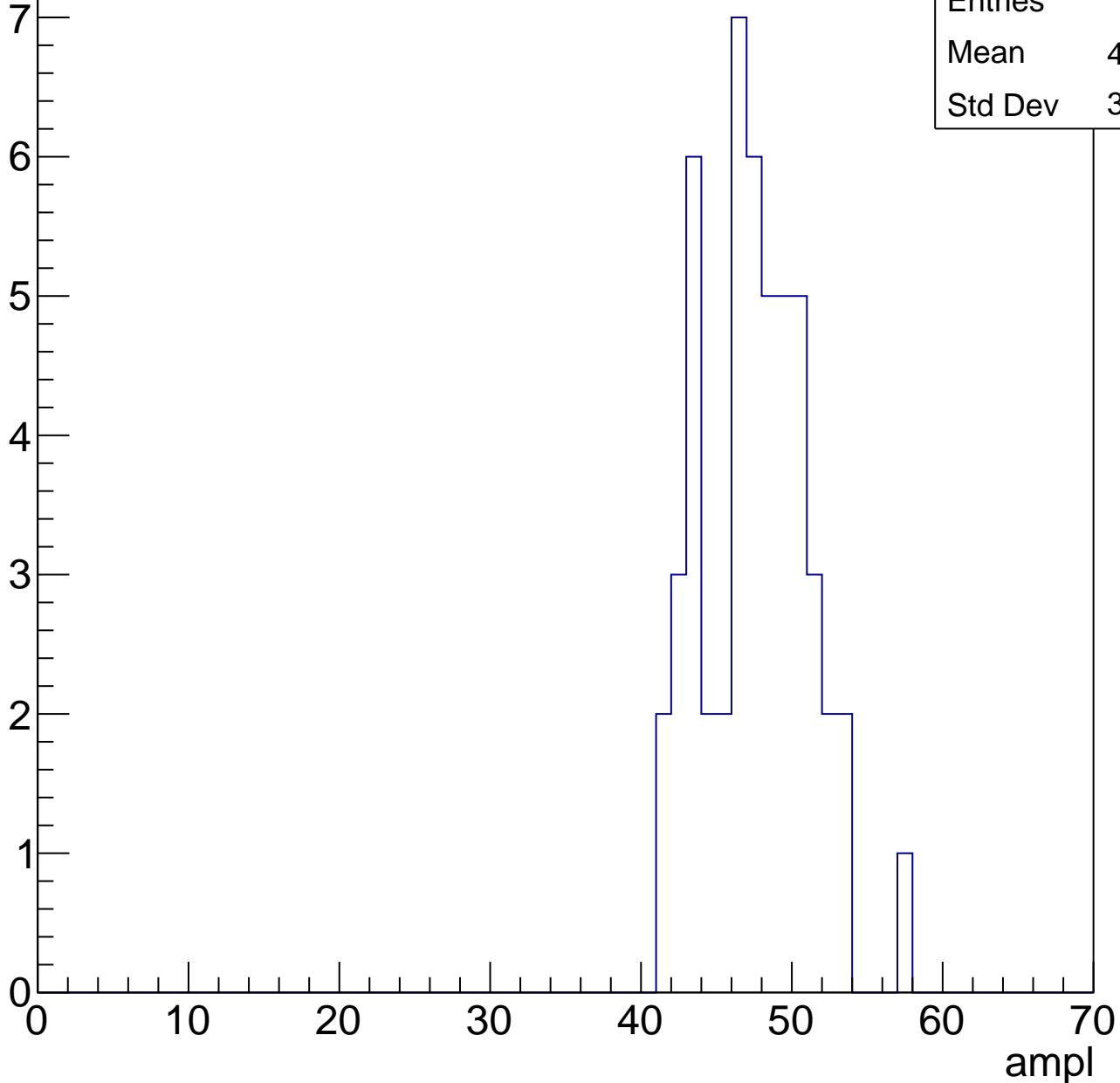


B1L003S, U19-ch110, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	47.12
Std Dev	3.473

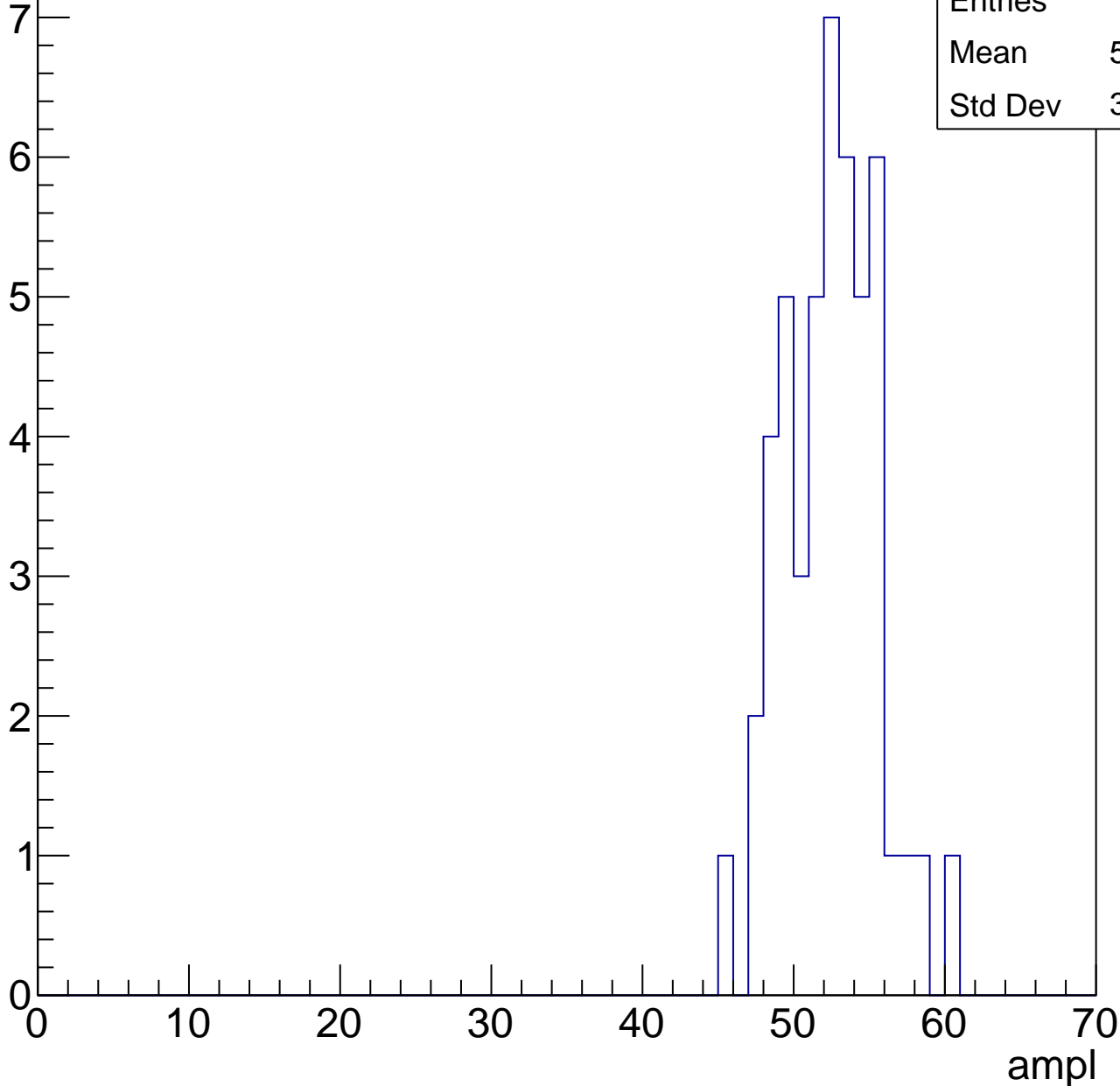


B1L003S, U19-ch110, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	51.96
Std Dev	3.048

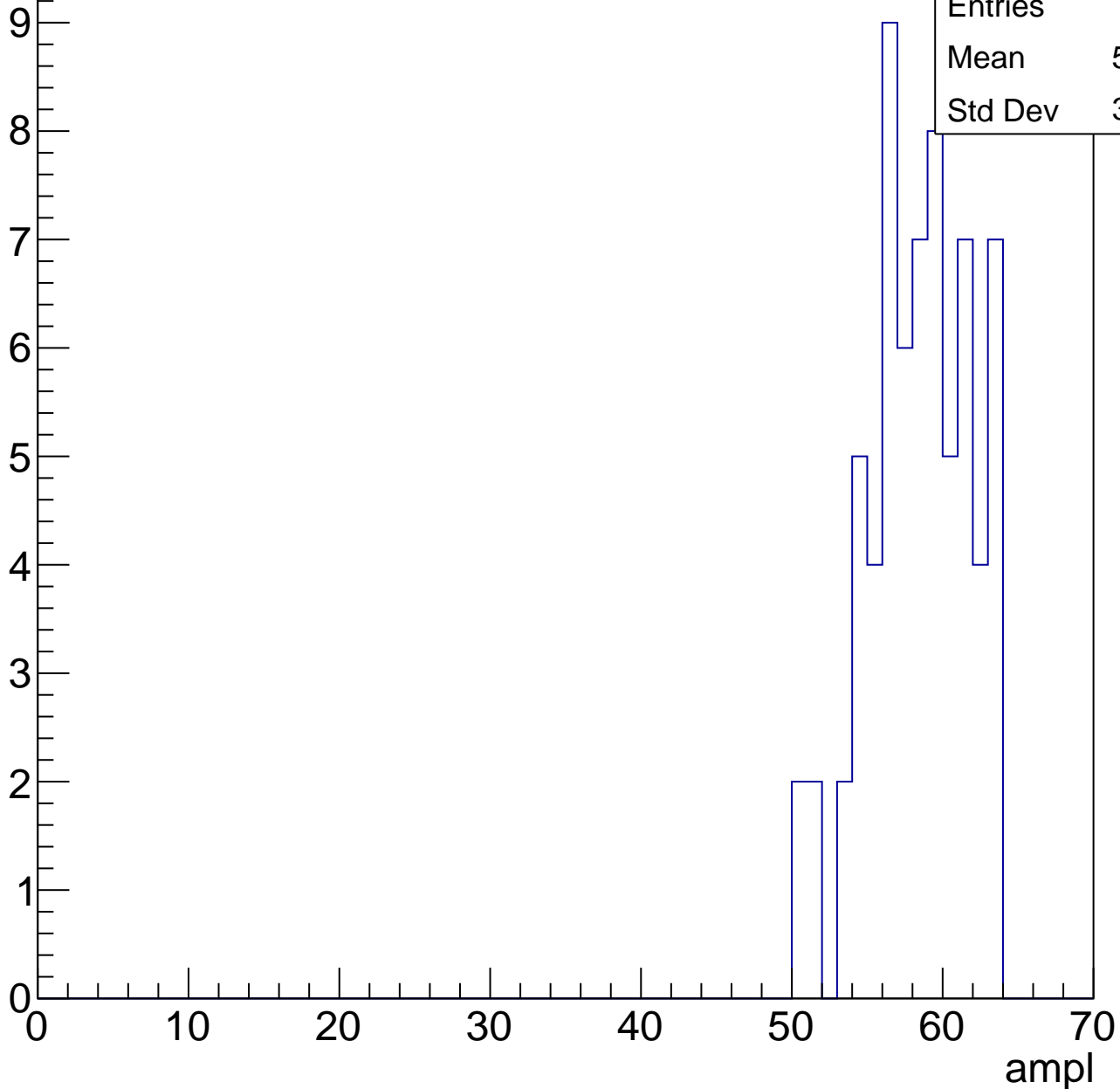


B1L003S, U19-ch110, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	57.91
Std Dev	3.351

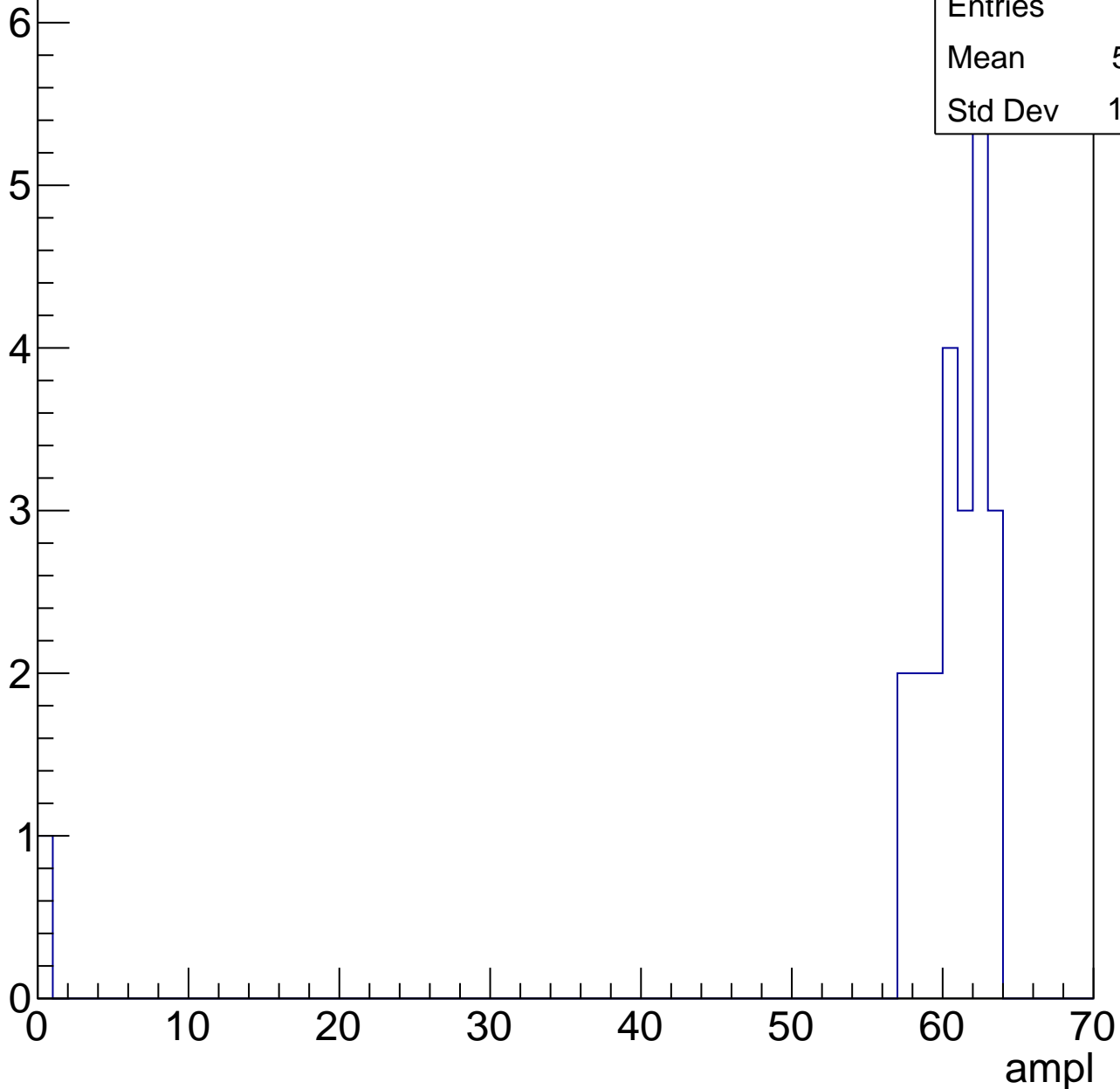


B1L003S, U19-ch110, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	23
Mean	57.91
Std Dev	12.48



B1L003S, U19-ch110, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch111, adc0

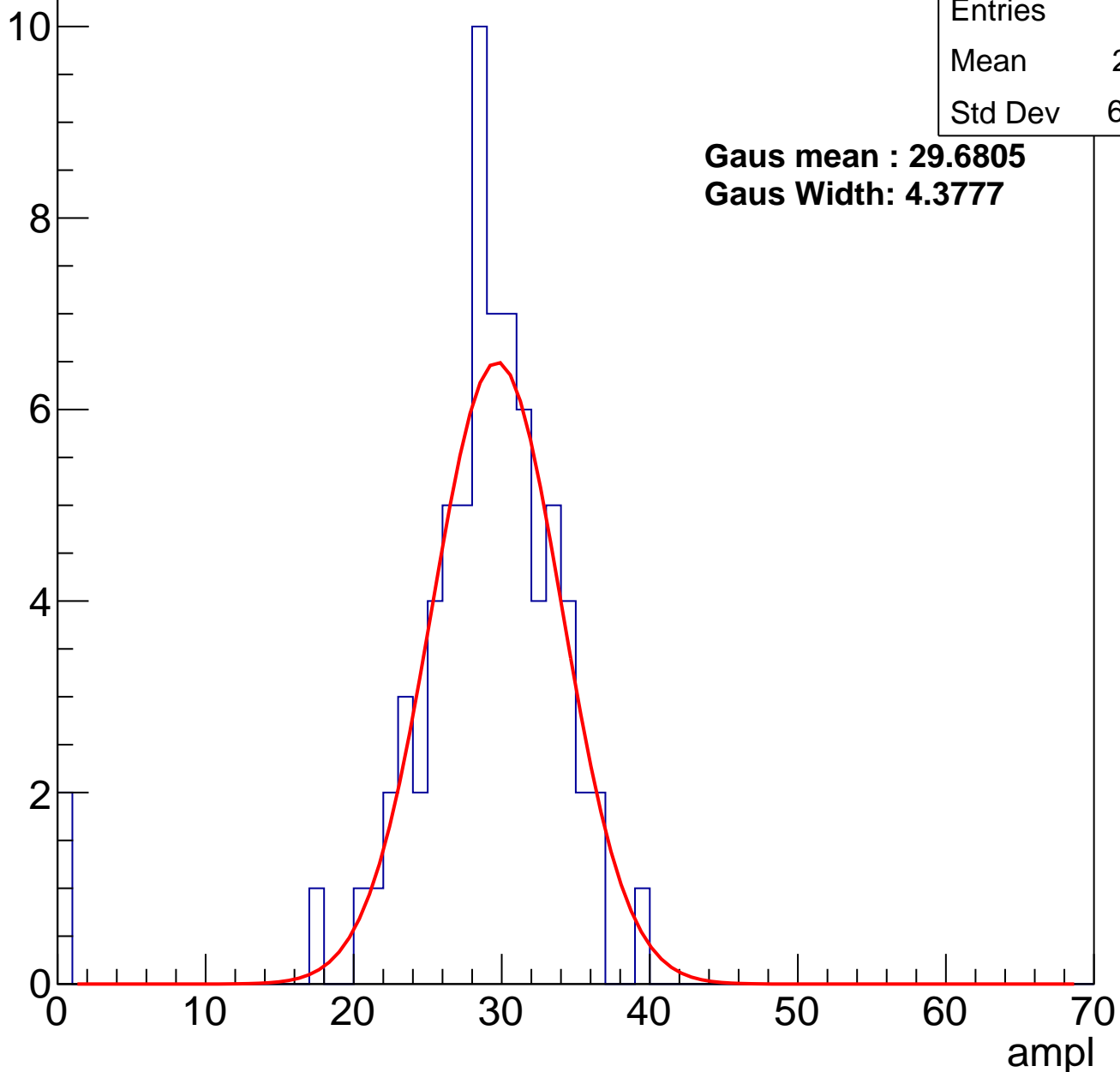
calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	28.01
Std Dev	6.163

Gaus mean : 29.6805

Gaus Width: 4.3777

Entry



B1L003S, U19-ch111, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	35.03
Std Dev	3.569

Gaus mean : 36.5219

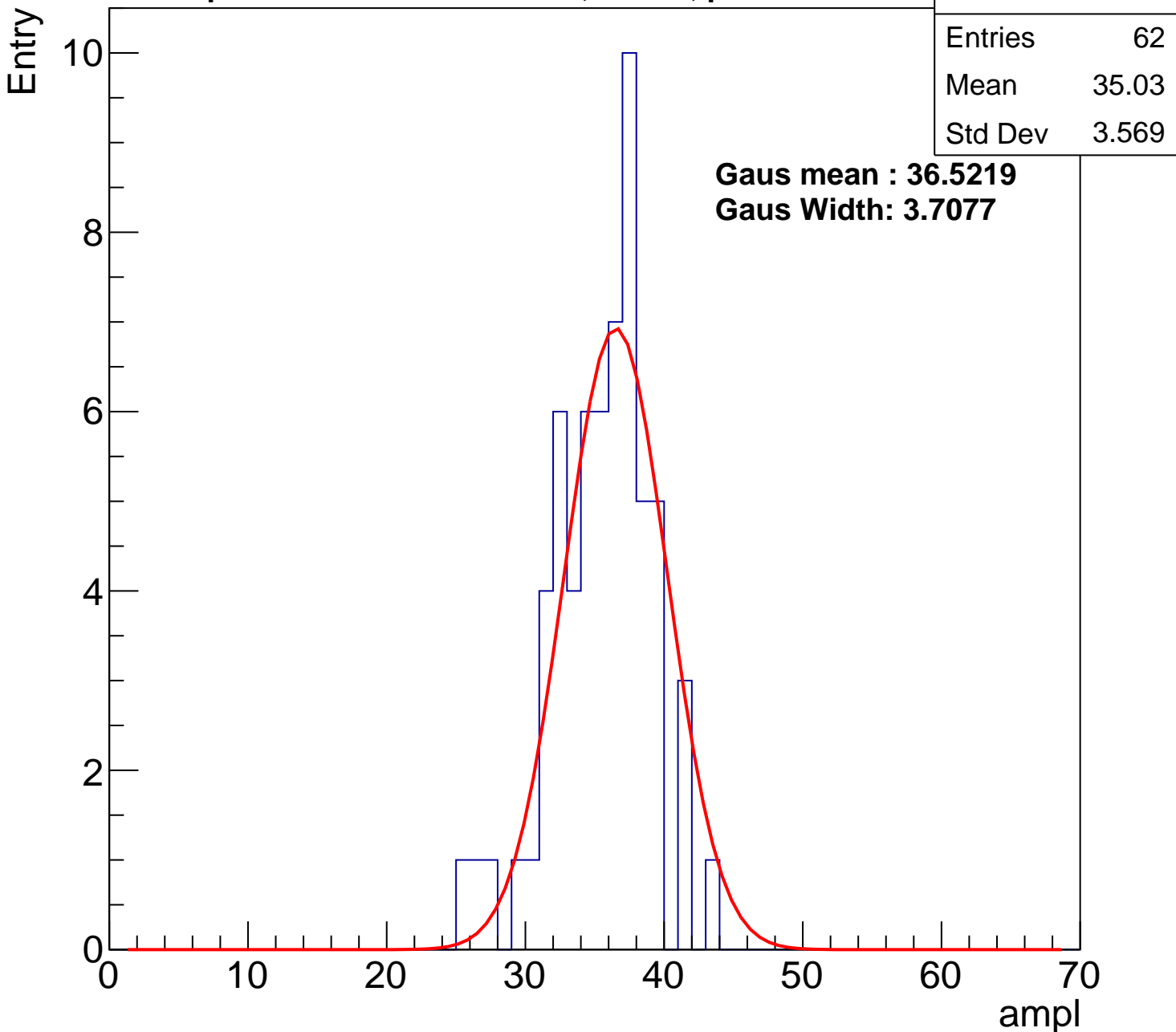
Gaus Width: 3.7077

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U19-ch111, adc2

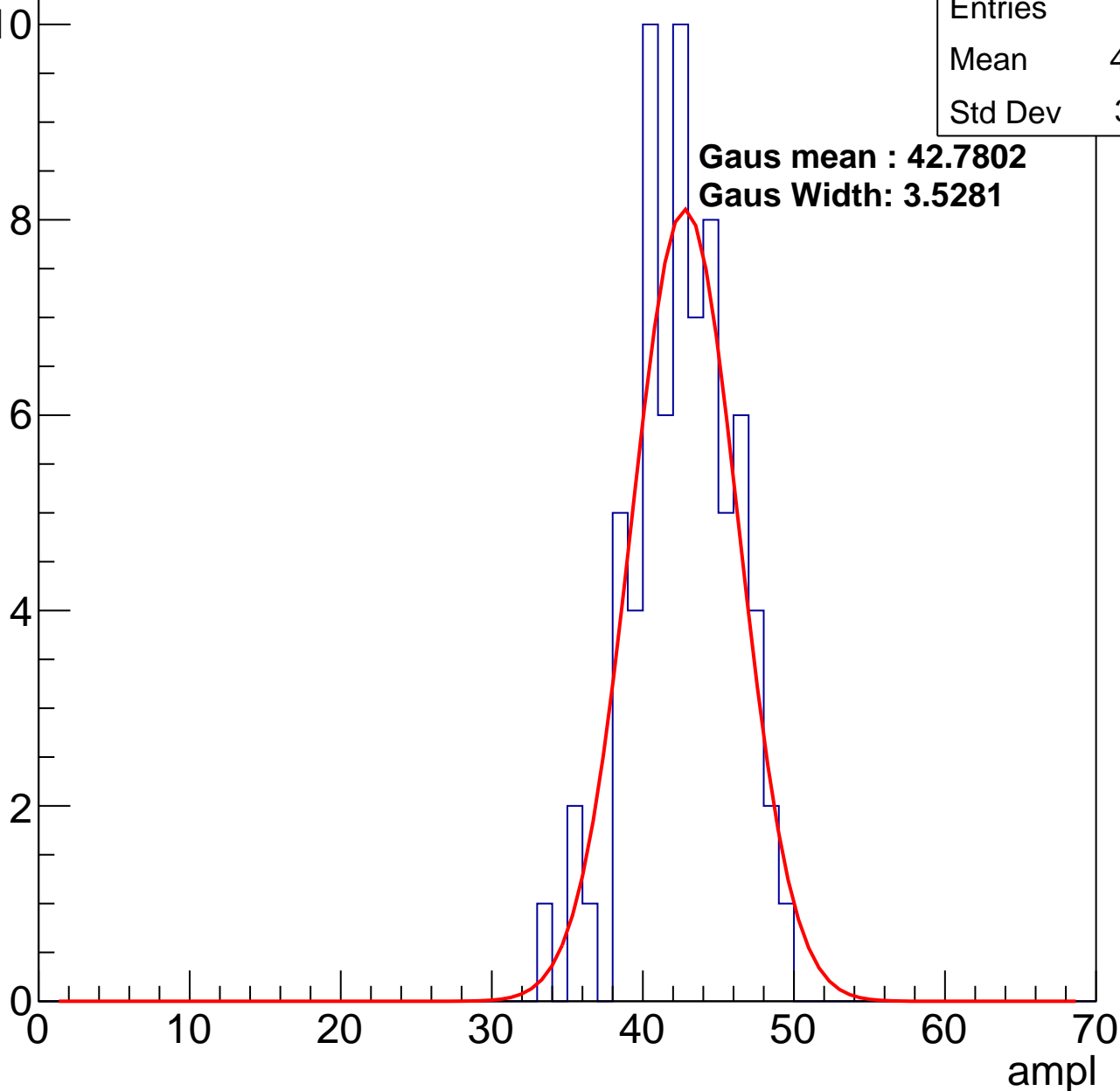
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.19
Std Dev	3.281

Gaus mean : 42.7802

Gaus Width: 3.5281

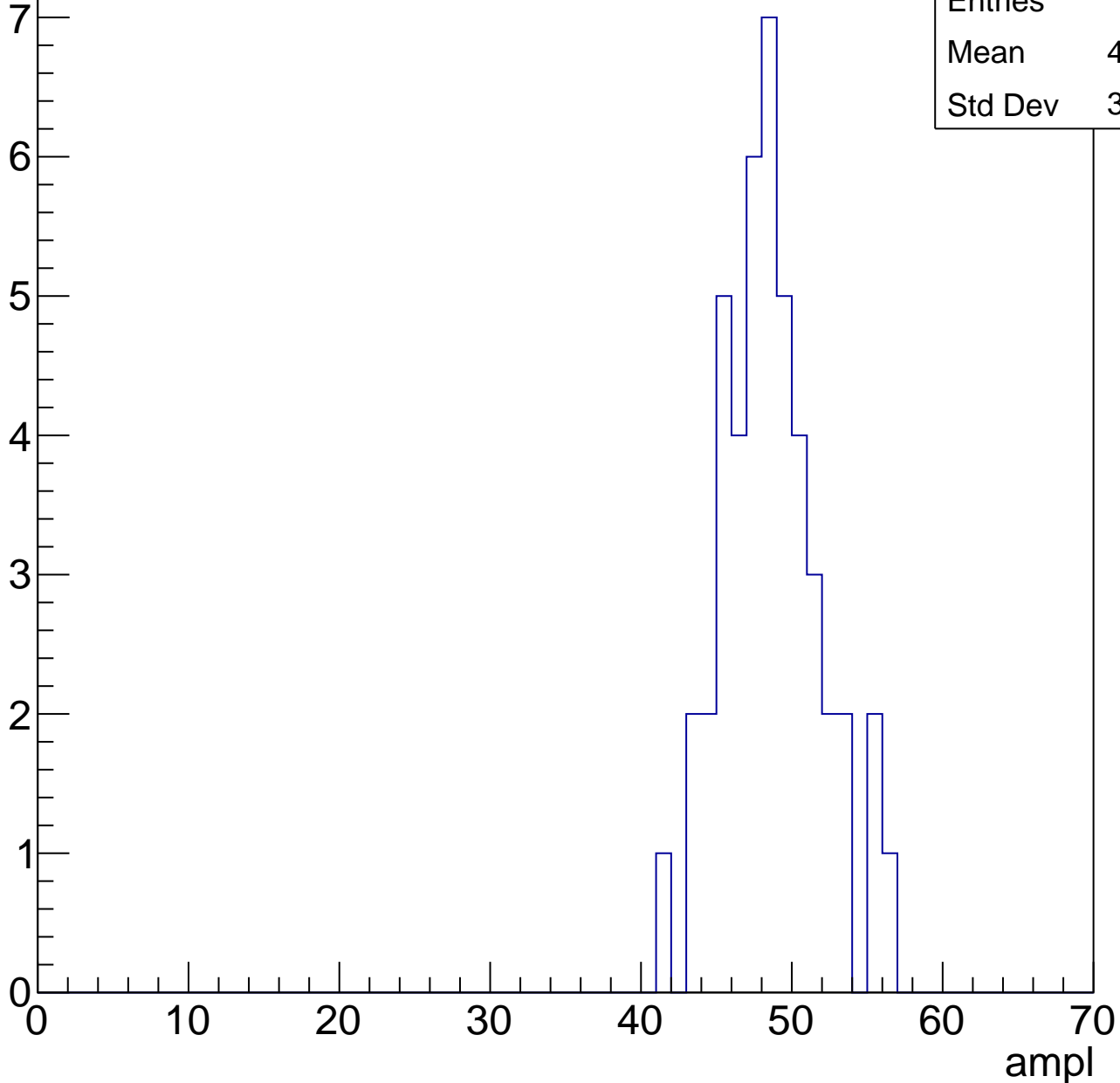


B1L003S, U19-ch111, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	48.17
Std Dev	3.259

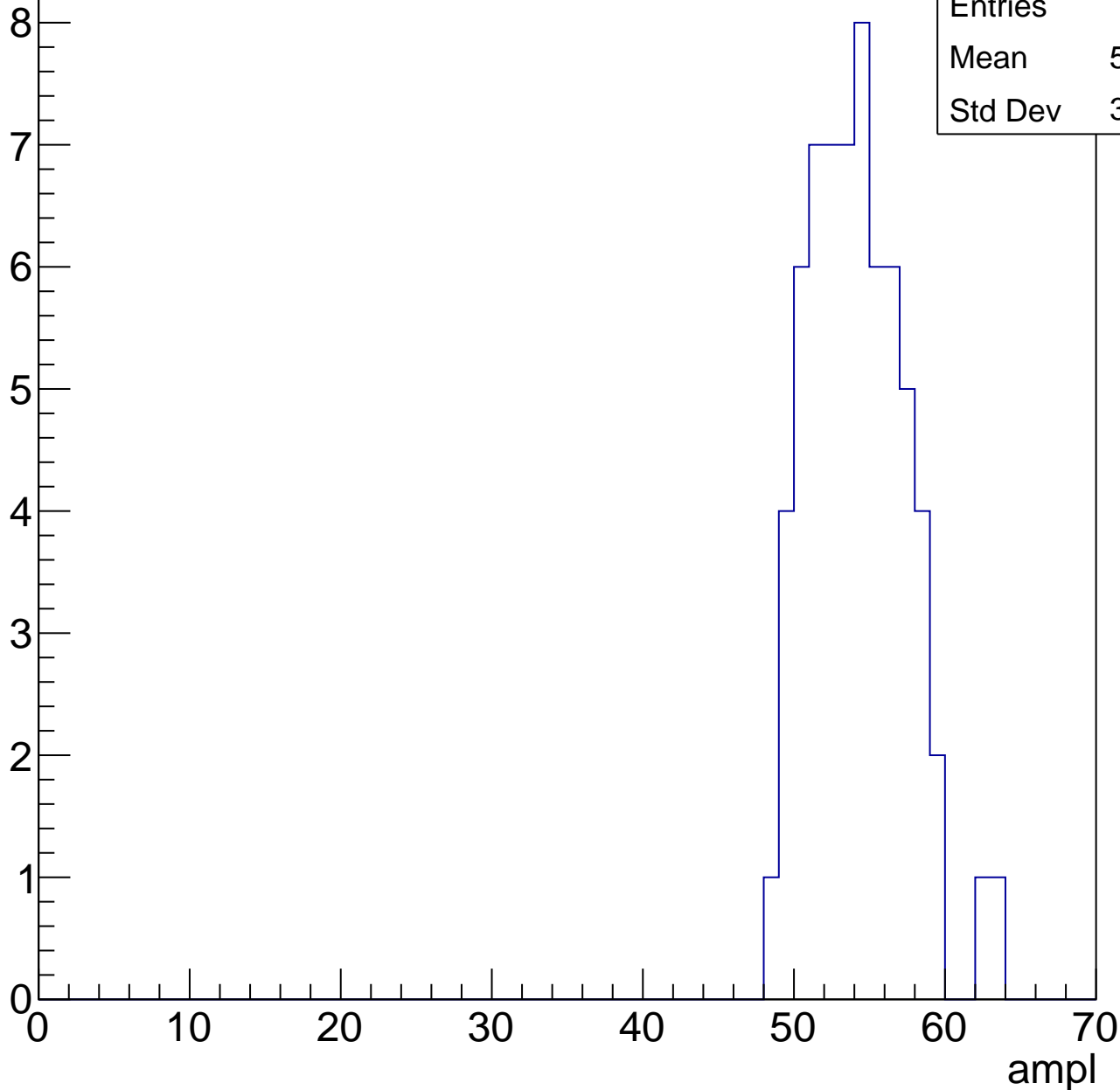


B1L003S, U19-ch111, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	53.75
Std Dev	3.187

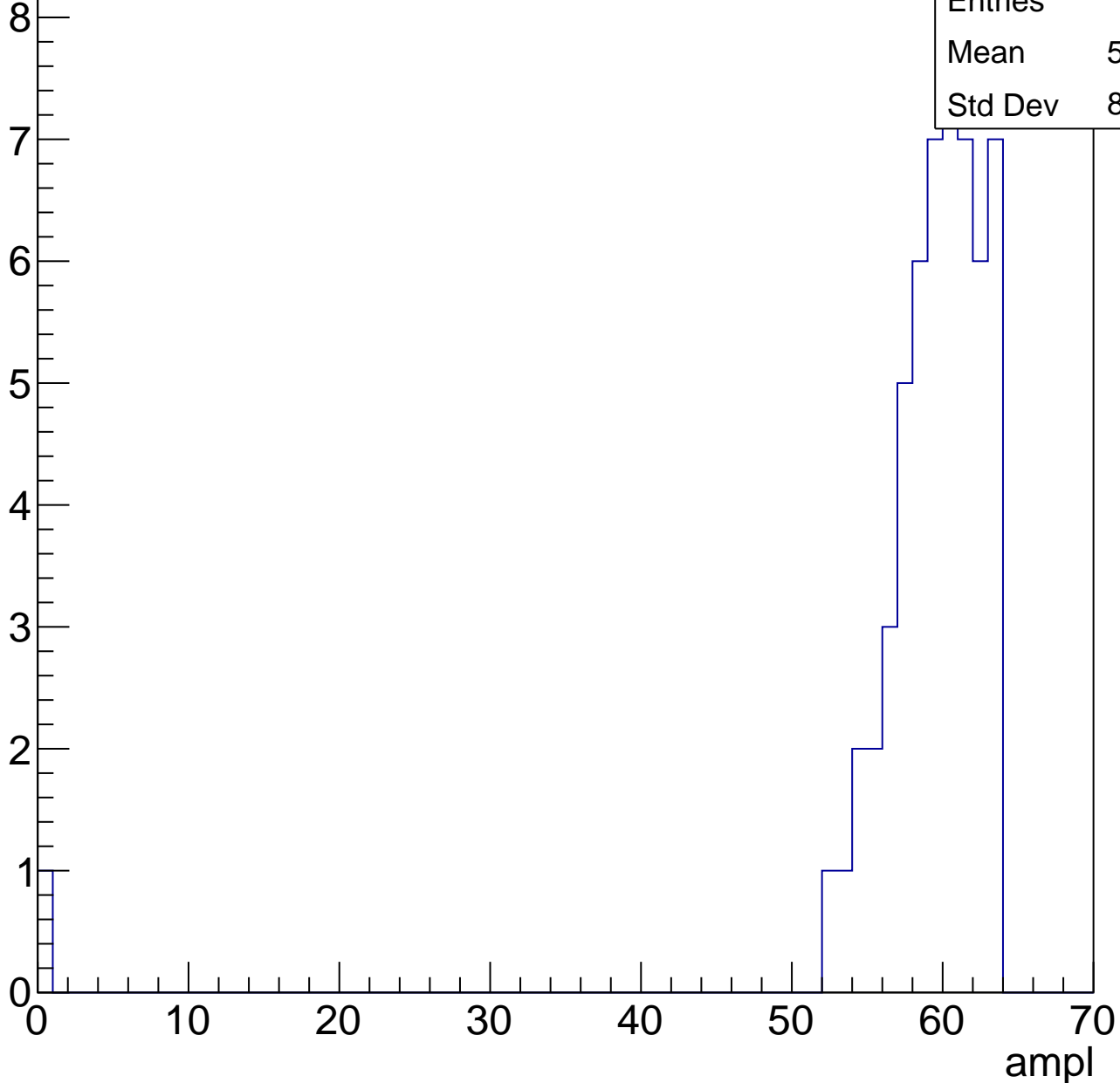


B1L003S, U19-ch111, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

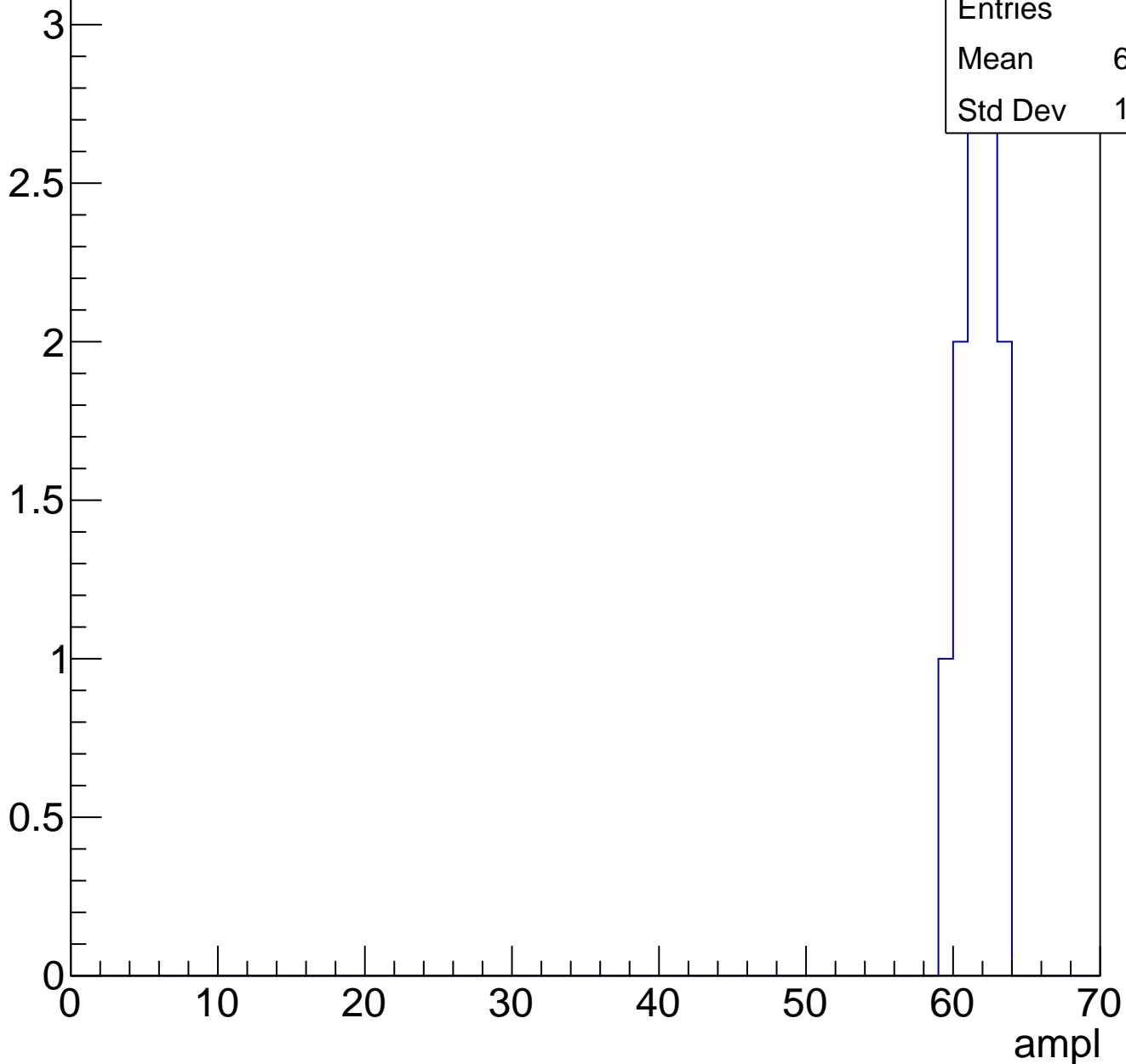
Entries	56
Mean	58.16
Std Dev	8.306



B1L003S, U19-ch111, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch111, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch112, adc0

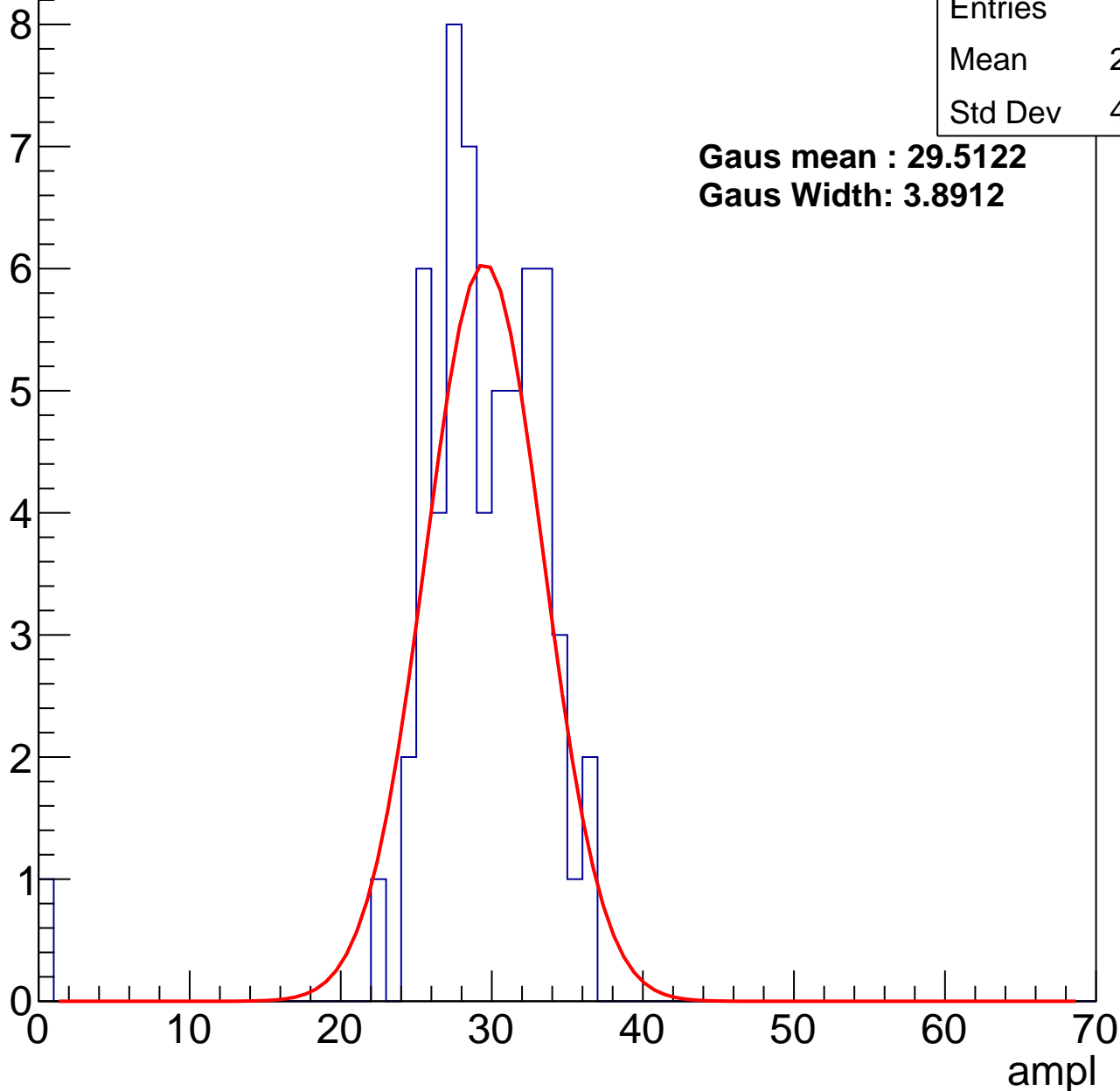
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.79
Std Dev	4.946

Gaus mean : 29.5122

Gaus Width: 3.8912



B1L003S, U19-ch112, adc1

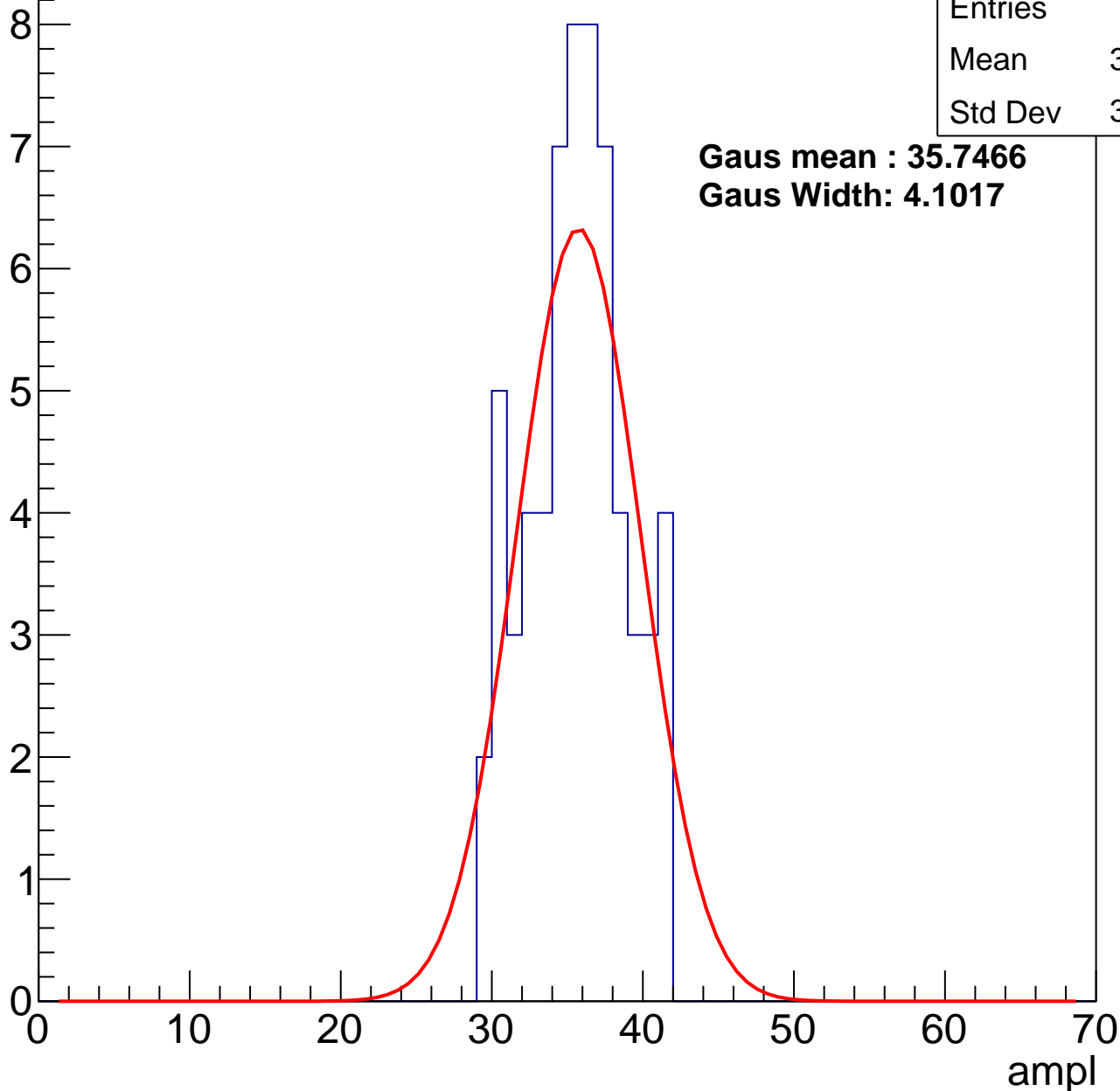
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	35.15
Std Dev	3.217

Gaus mean : 35.7466

Gaus Width: 4.1017



B1L003S, U19-ch112, adc2

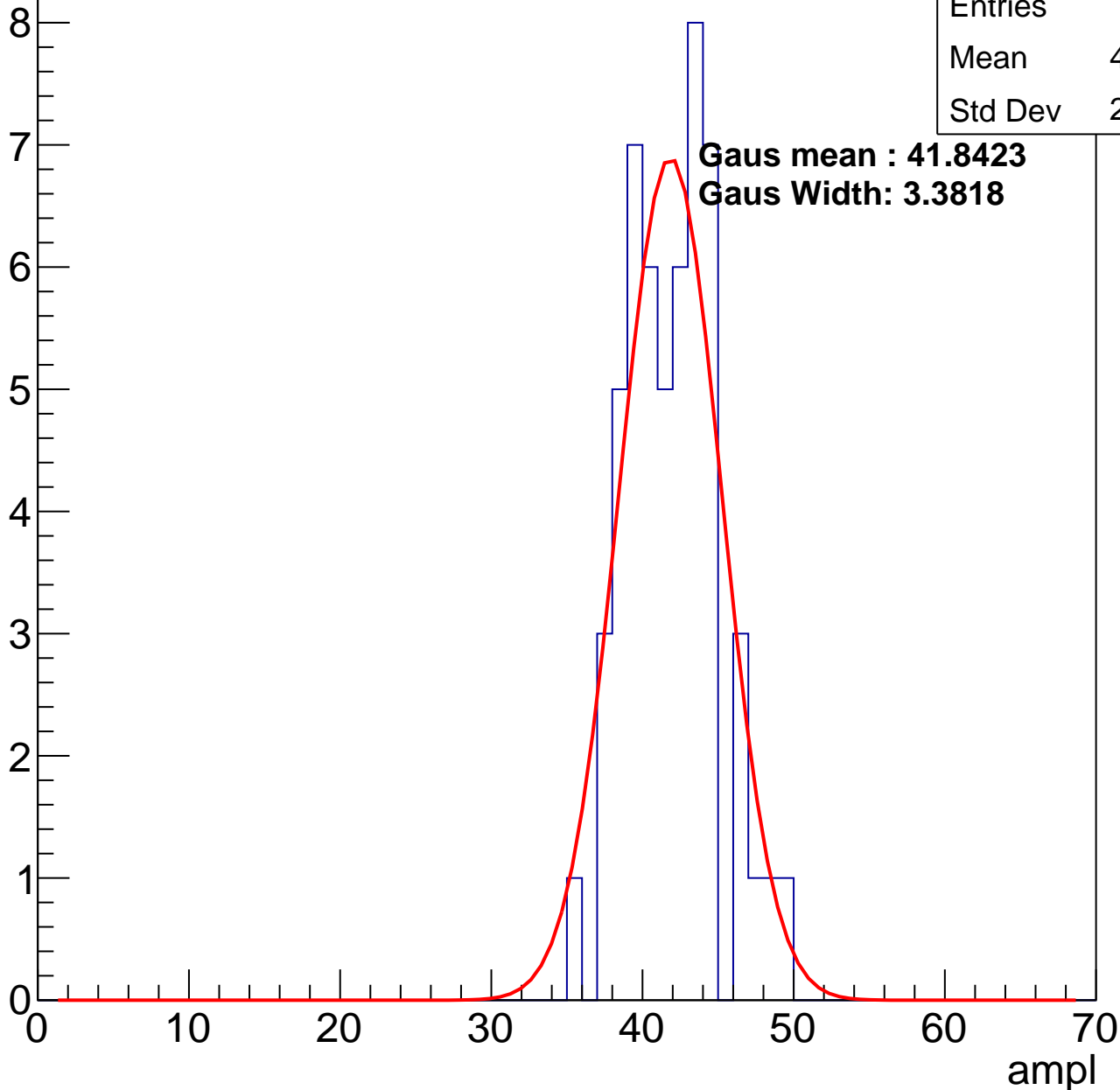
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	41.48
Std Dev	2.967

Gaus mean : 41.8423

Gaus Width: 3.3818

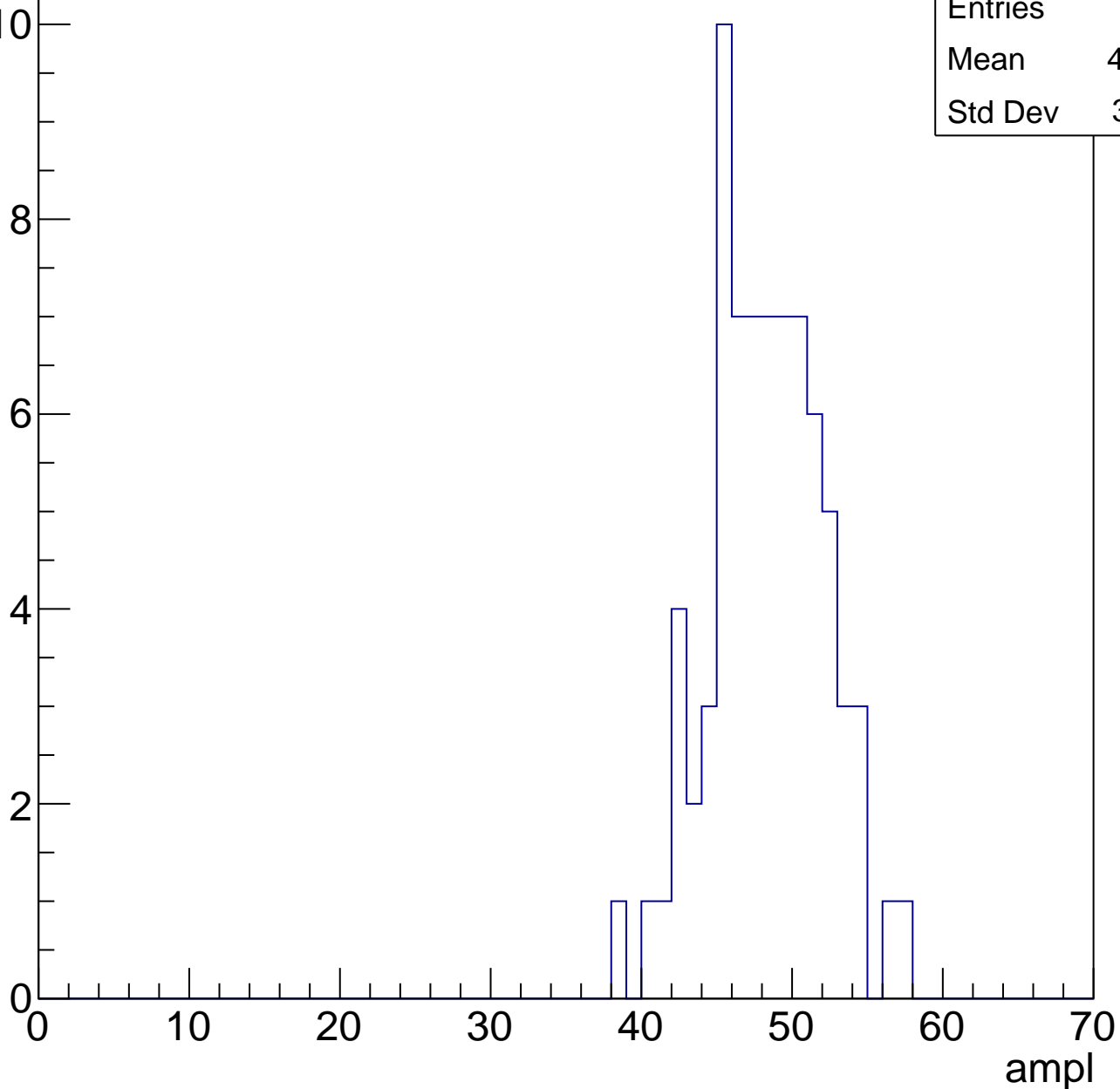


B1L003S, U19-ch112, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

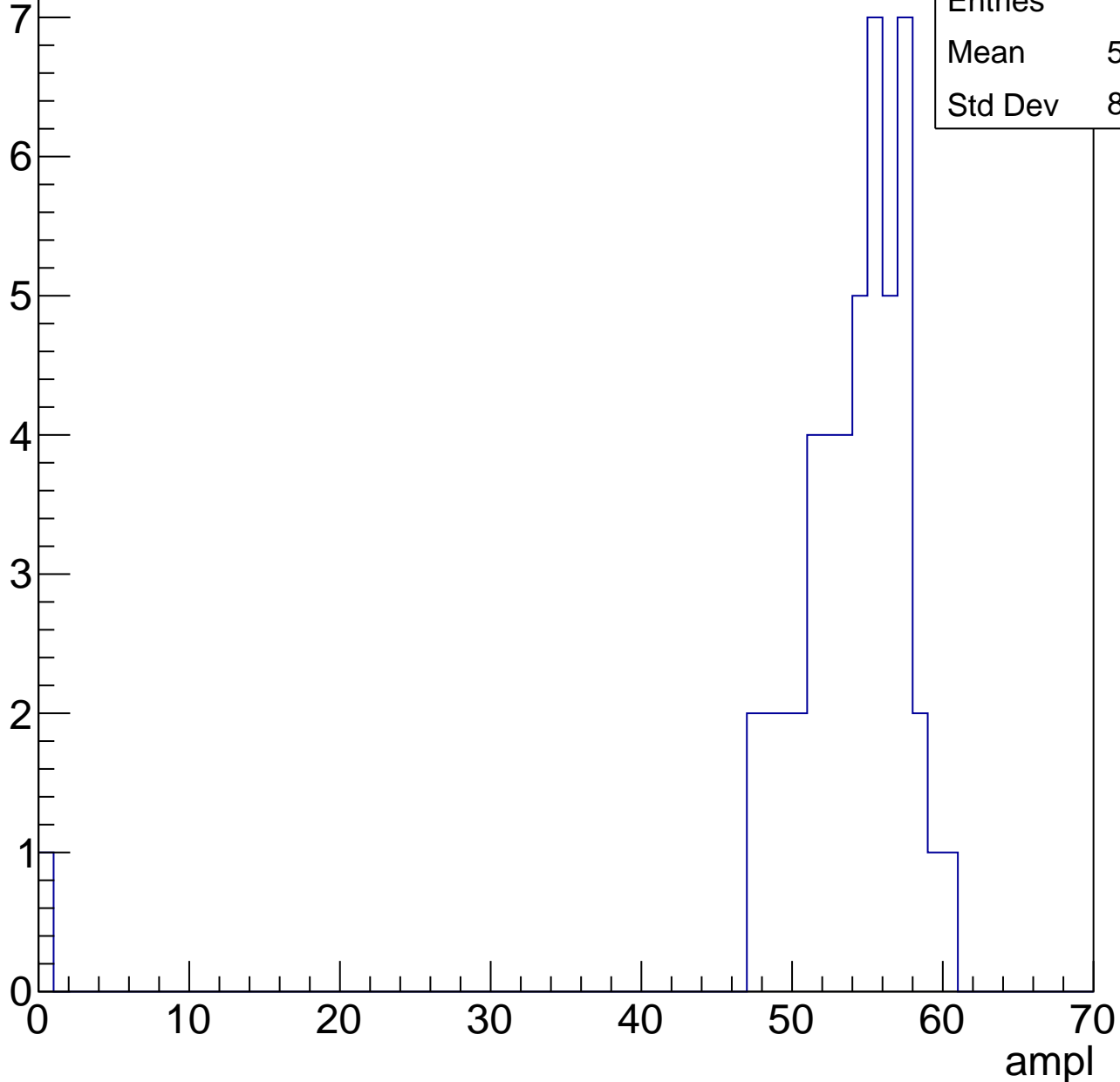
Entries	76
Mean	47.83
Std Dev	3.761



B1L003S, U19-ch112, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

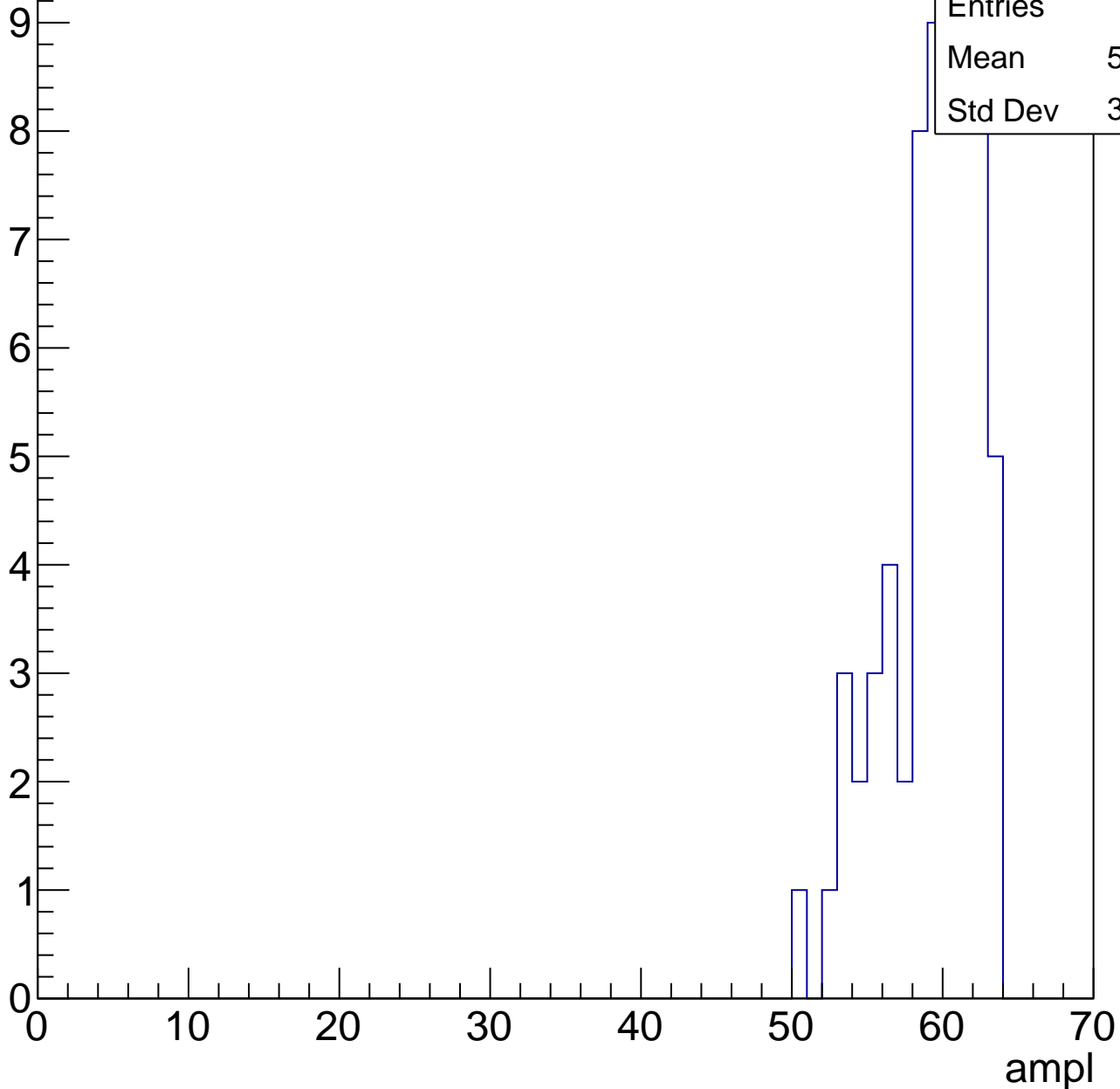


B1L003S, U19-ch112, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	58.83
Std Dev	3.032

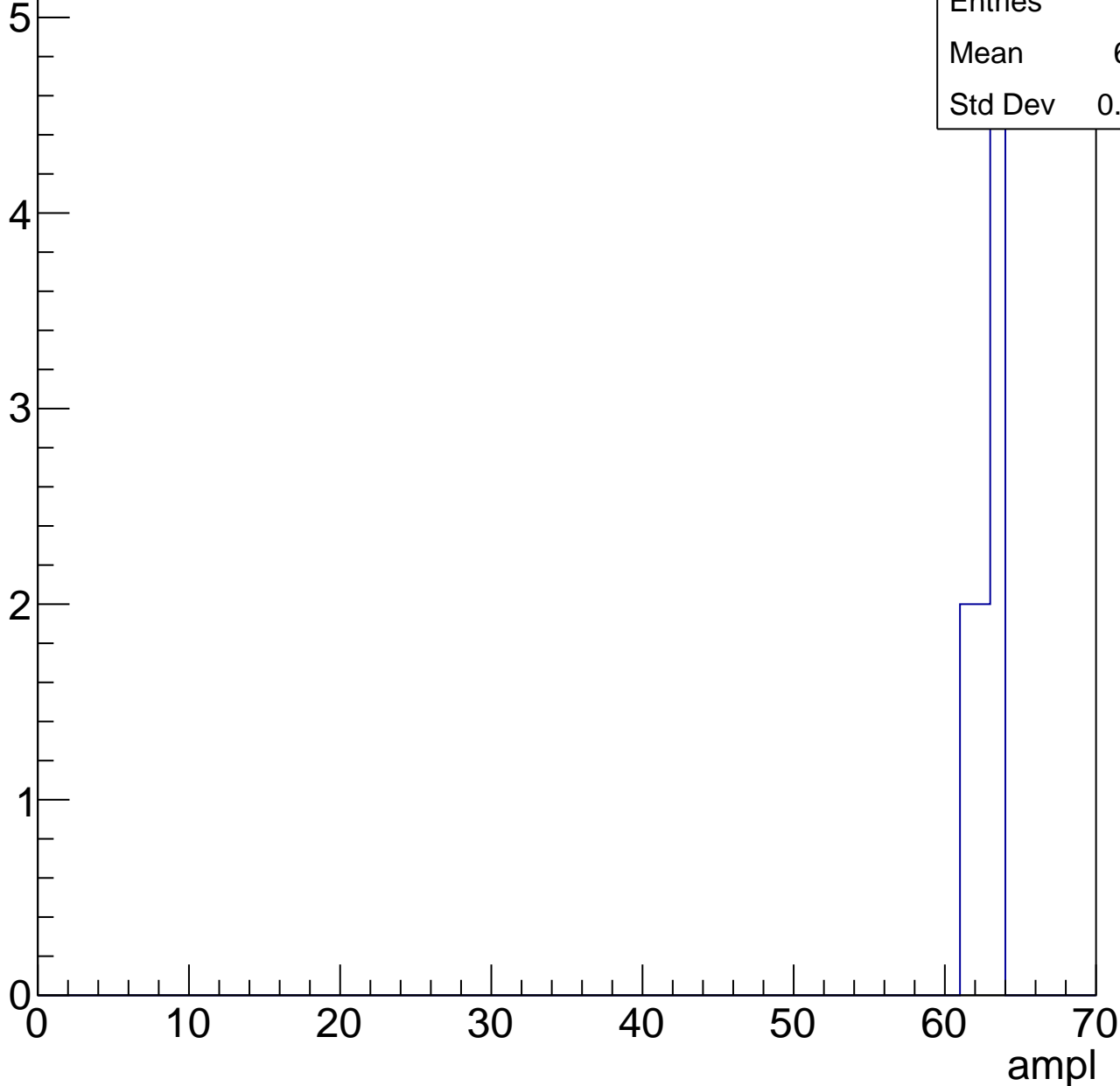


B1L003S, U19-ch112, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	62.33
Std Dev	0.8165



B1L003S, U19-ch112, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch113, adc0

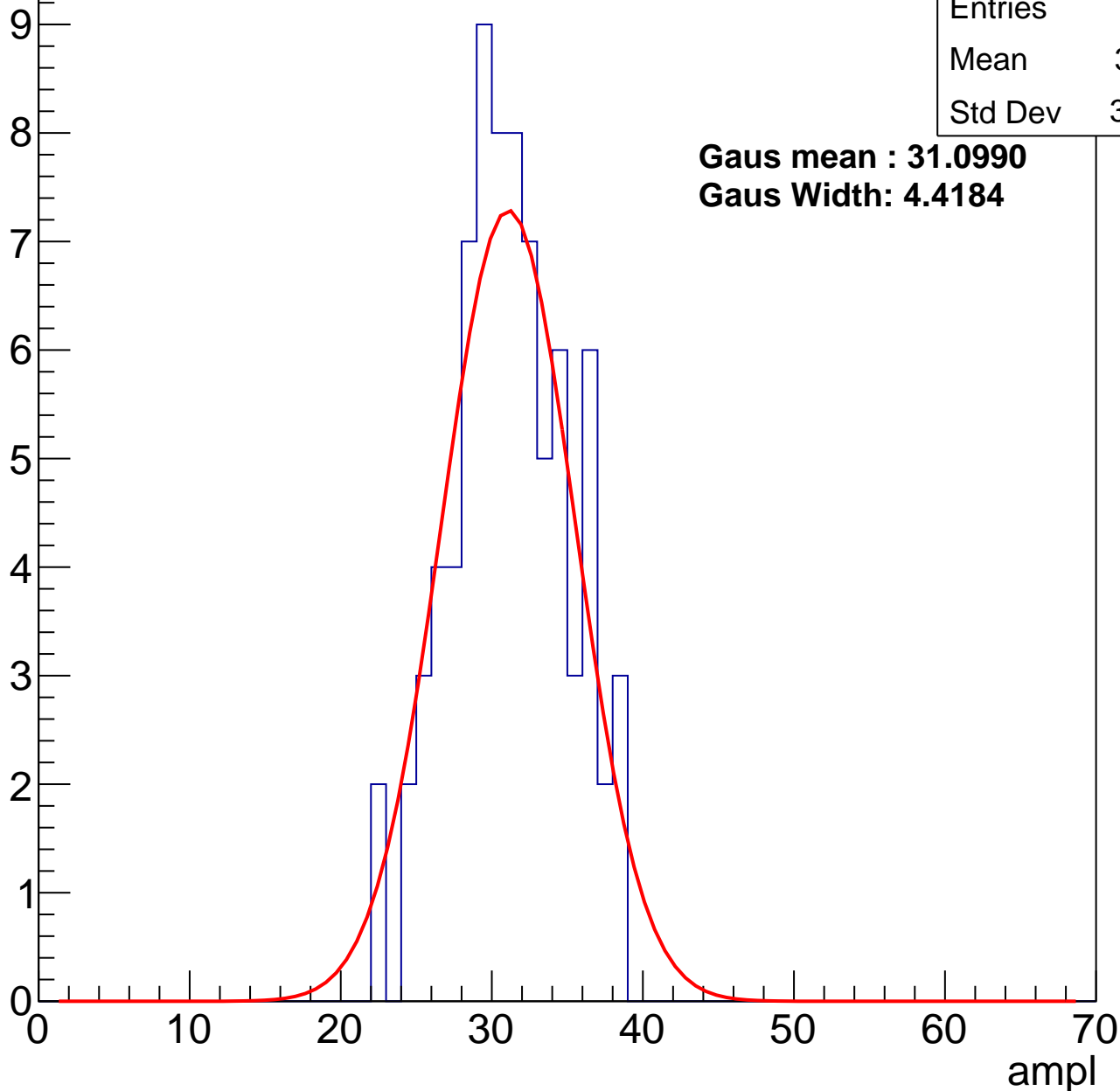
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	30.71
Std Dev	3.783

Gaus mean : 31.0990

Gaus Width: 4.4184



B1L003S, U19-ch113, adc1

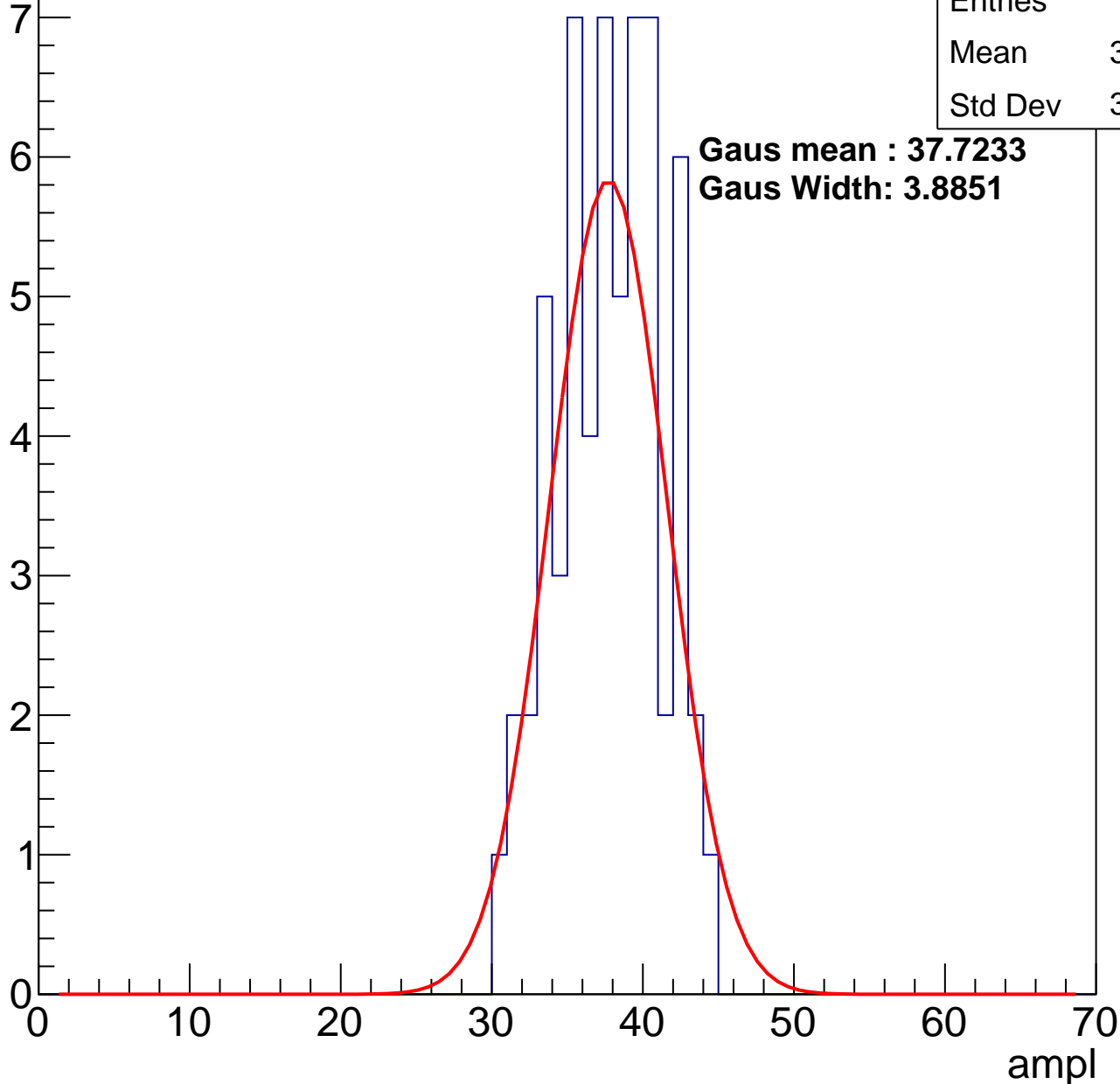
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	37.34
Std Dev	3.392

Gaus mean : 37.7233

Gaus Width: 3.8851



B1L003S, U19-ch113, adc2

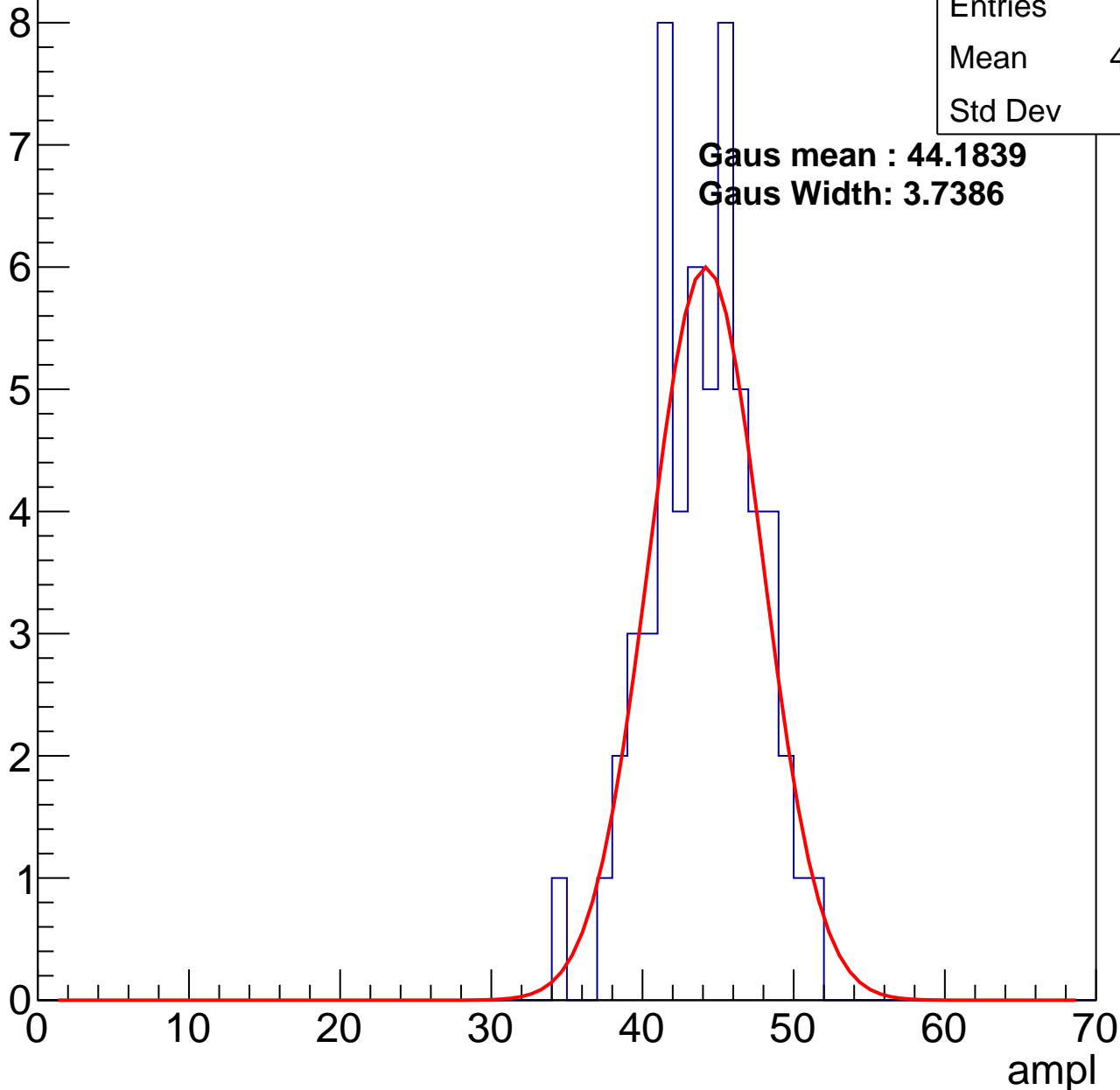
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.57
Std Dev	3.45

Gaus mean : 44.1839

Gaus Width: 3.7386

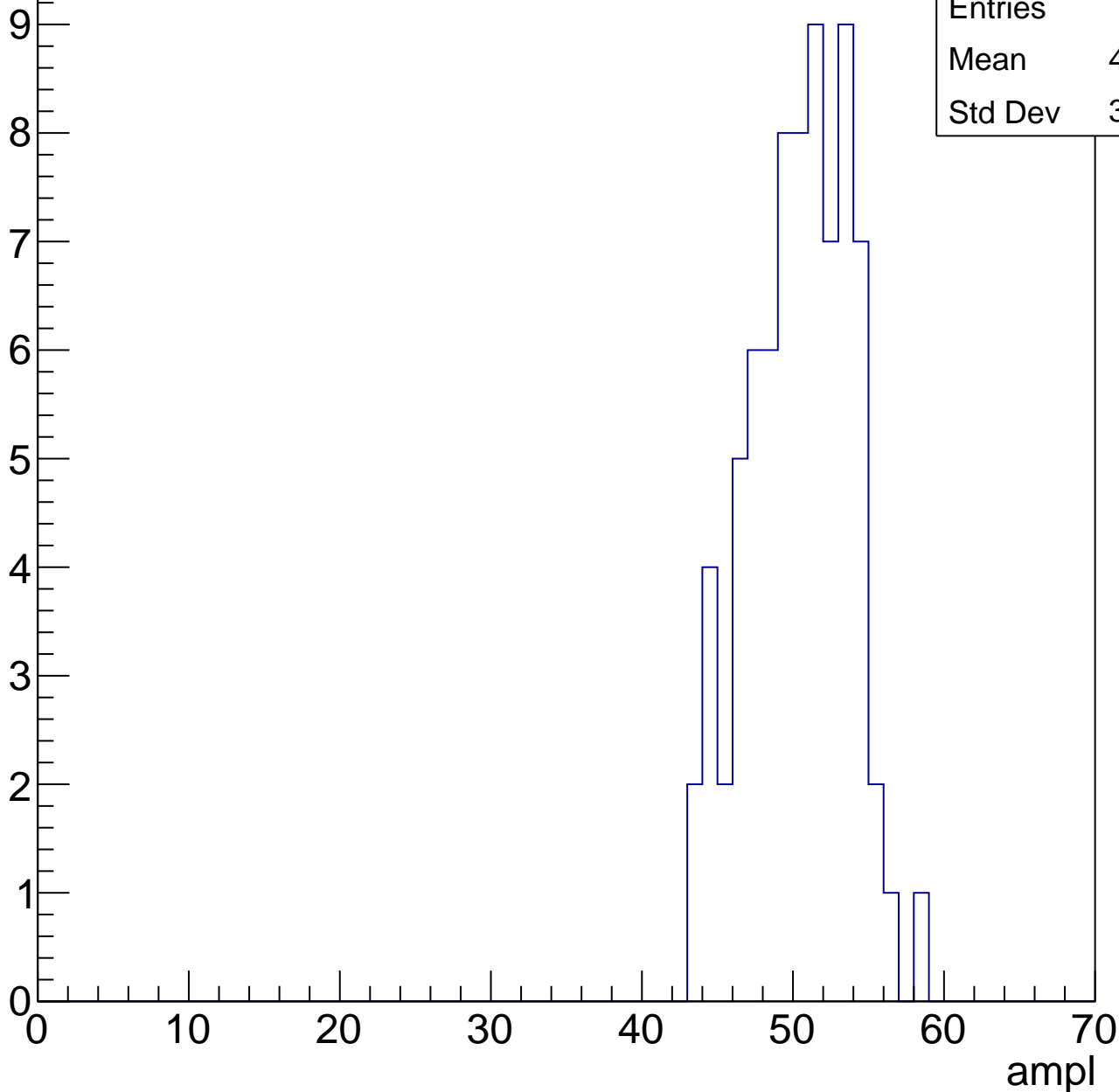


B1L003S, U19-ch113, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	49.95
Std Dev	3.299

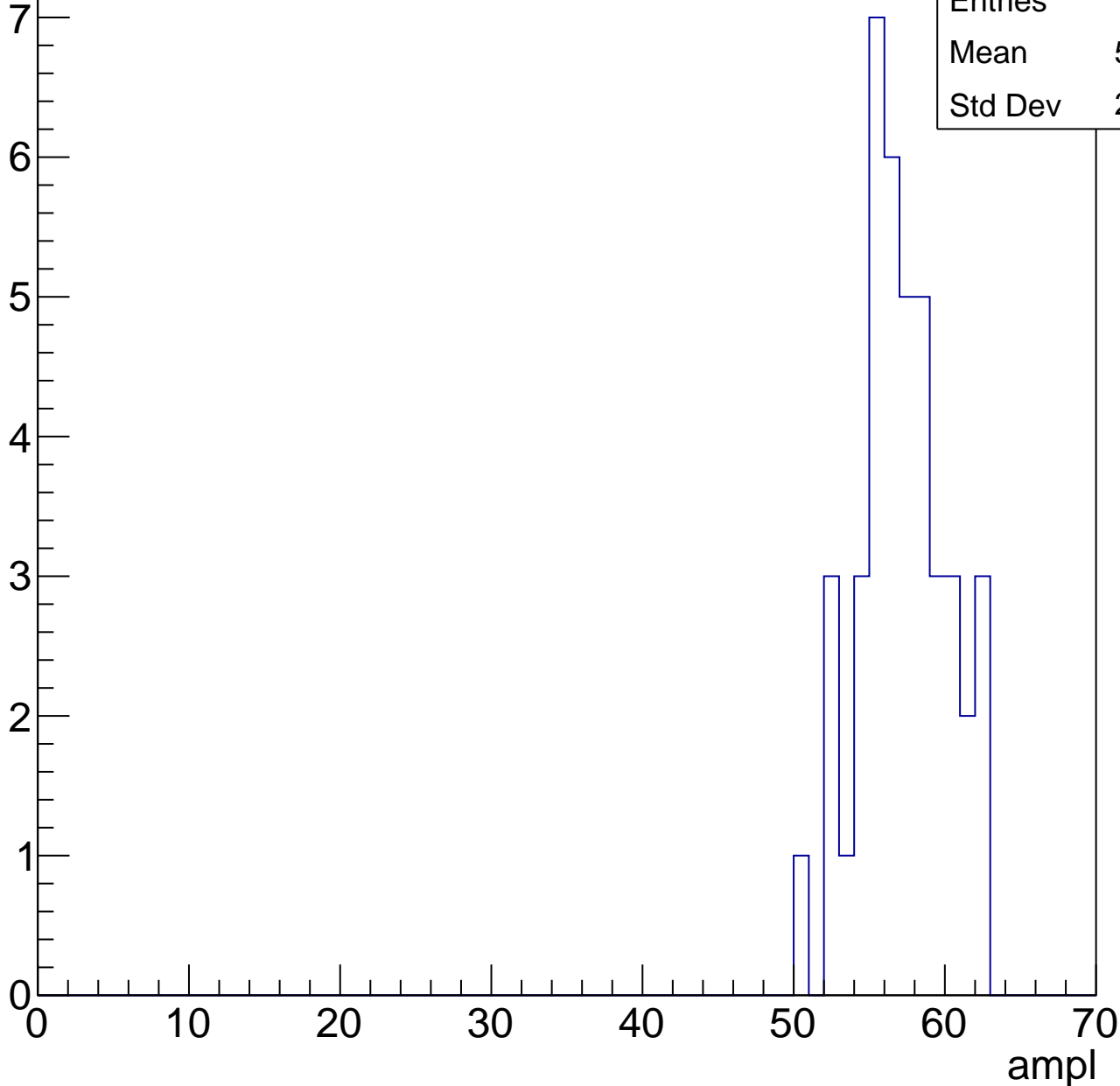


B1L003S, U19-ch113, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	56.71
Std Dev	2.881

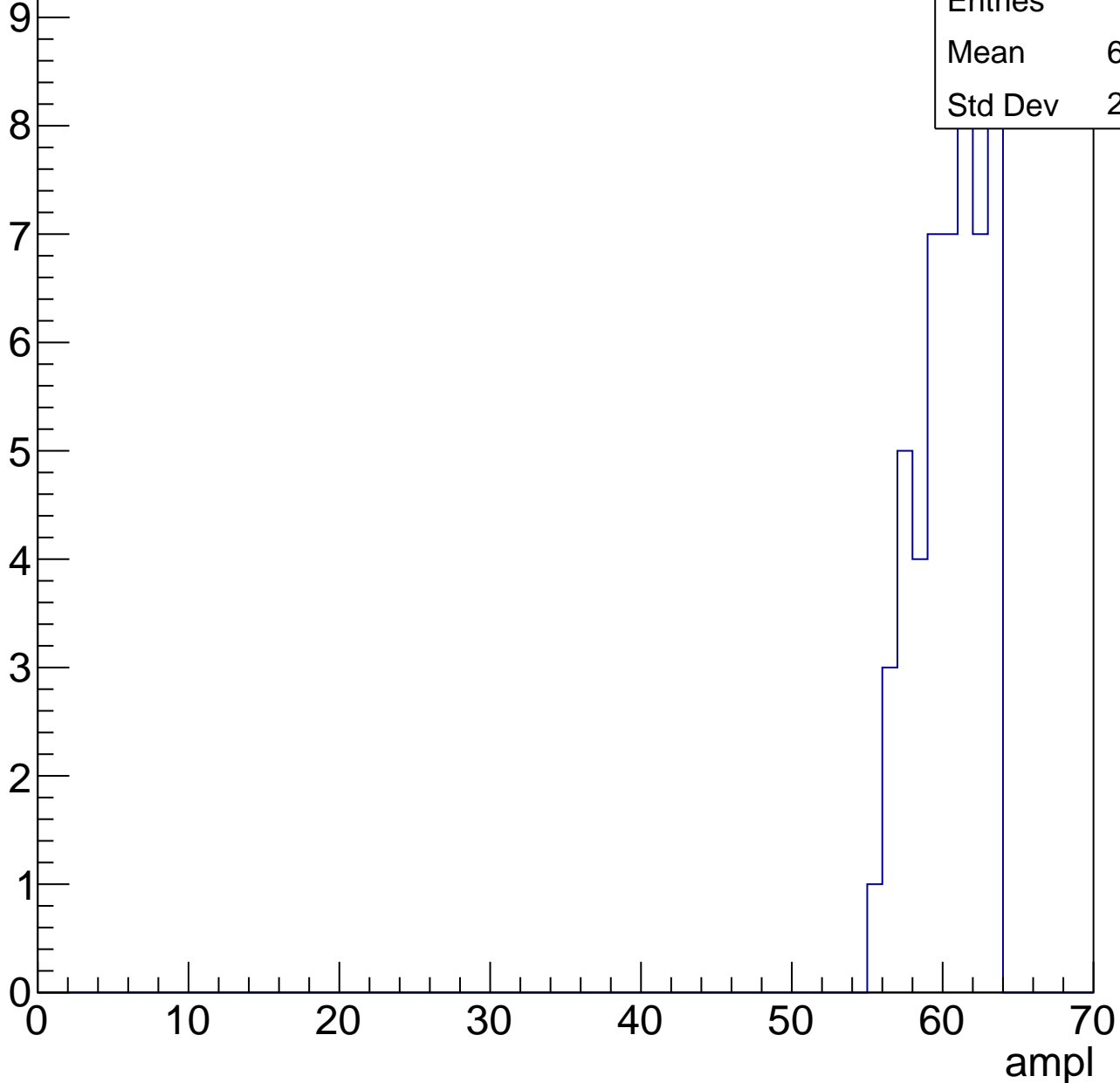


B1L003S, U19-ch113, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	60.04
Std Dev	2.249



B1L003S, U19-ch113, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	31.5
Std Dev	31.5

B1L003S, U19-ch113, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch114, adc0

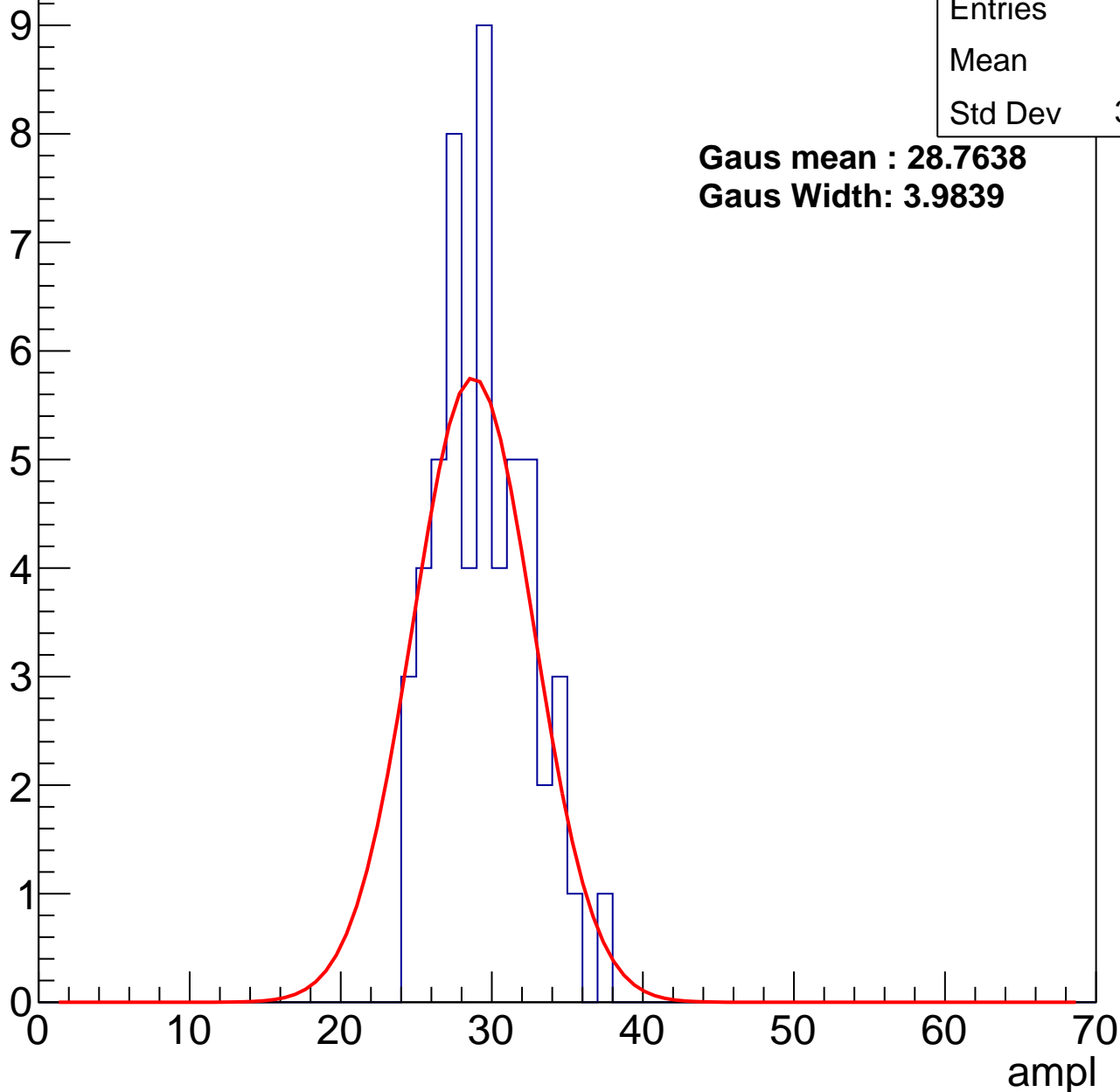
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	29
Std Dev	3.031

Gaus mean : 28.7638

Gaus Width: 3.9839



B1L003S, U19-ch114, adc1

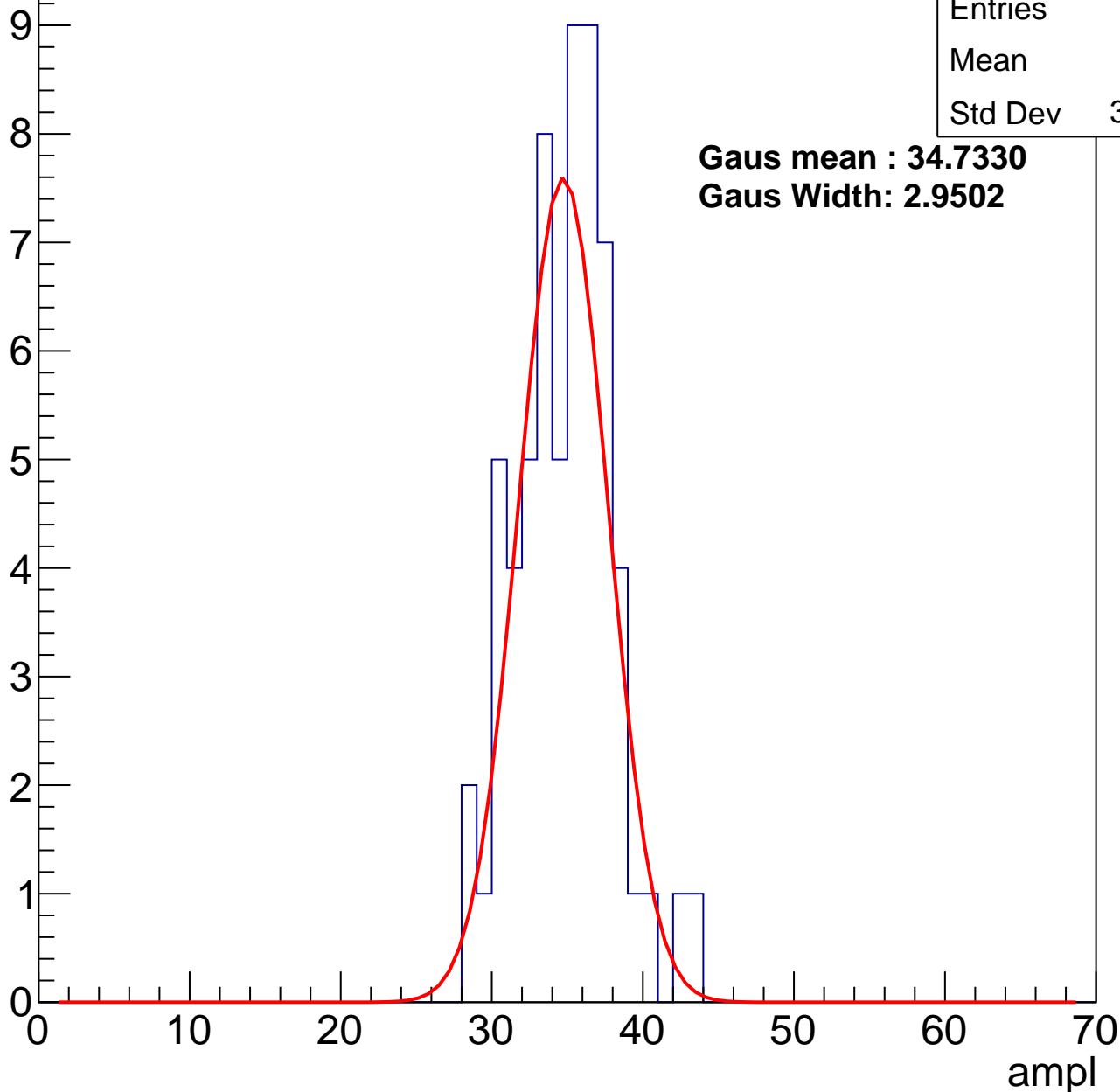
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	34.4
Std Dev	3.109

Gaus mean : 34.7330

Gaus Width: 2.9502



B1L003S, U19-ch114, adc2

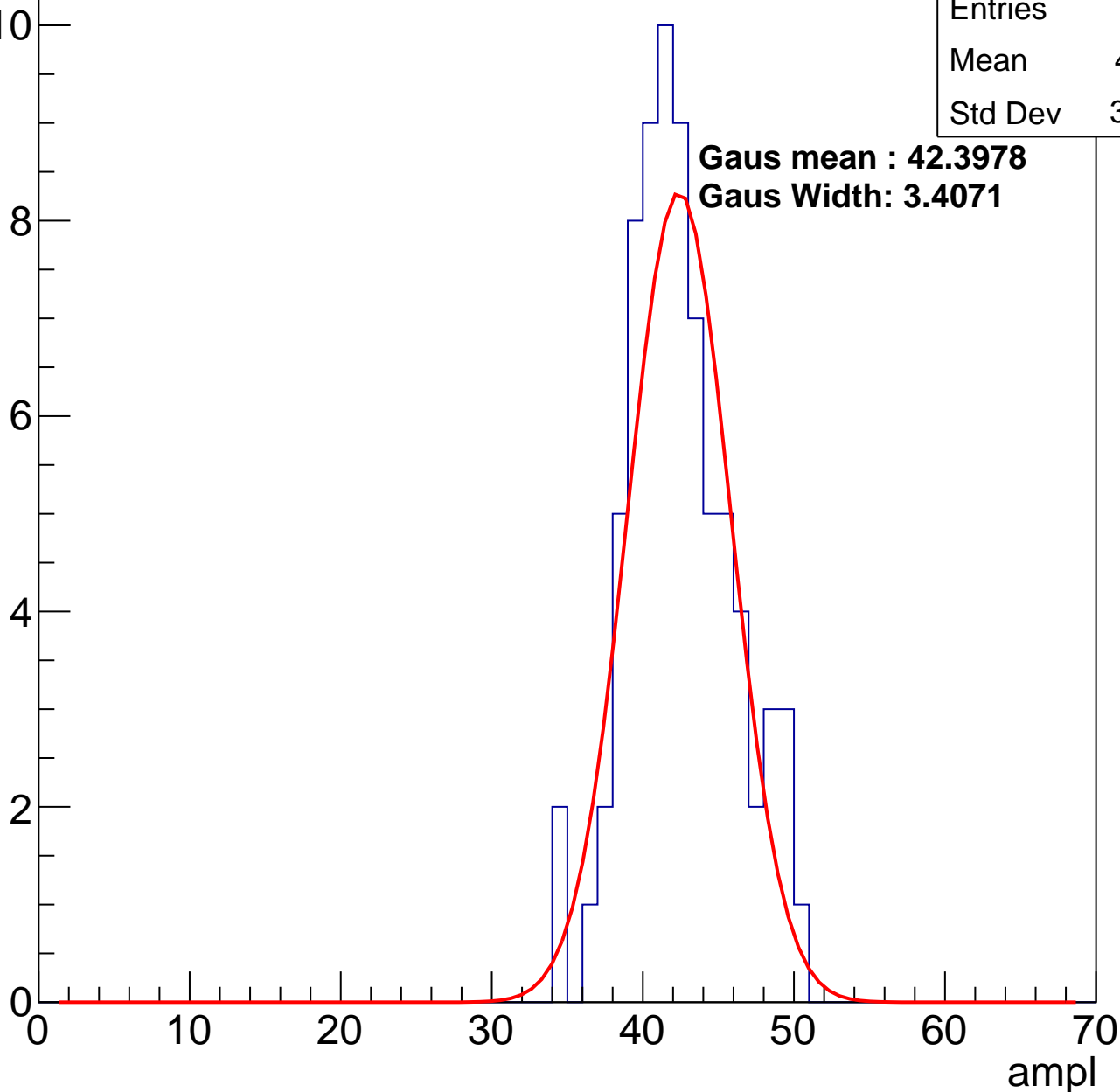
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	42.01
Std Dev	3.474

Gaus mean : 42.3978

Gaus Width: 3.4071

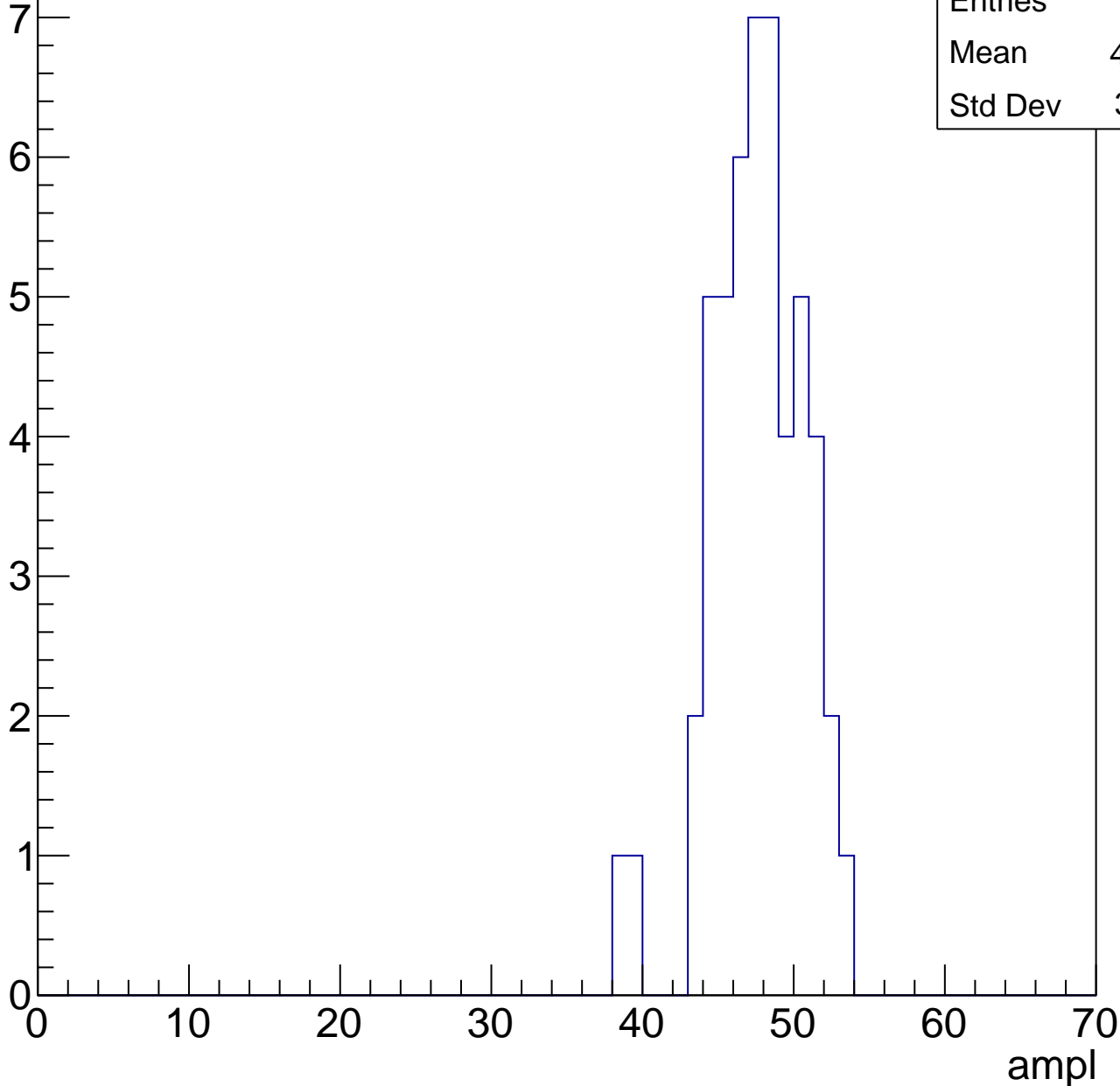


B1L003S, U19-ch114, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	47.12
Std Dev	3.051



B1L003S, U19-ch114, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	54
Mean	53.63
Std Dev	2.591

Entry

10

8

6

4

2

0

0

10

20

30

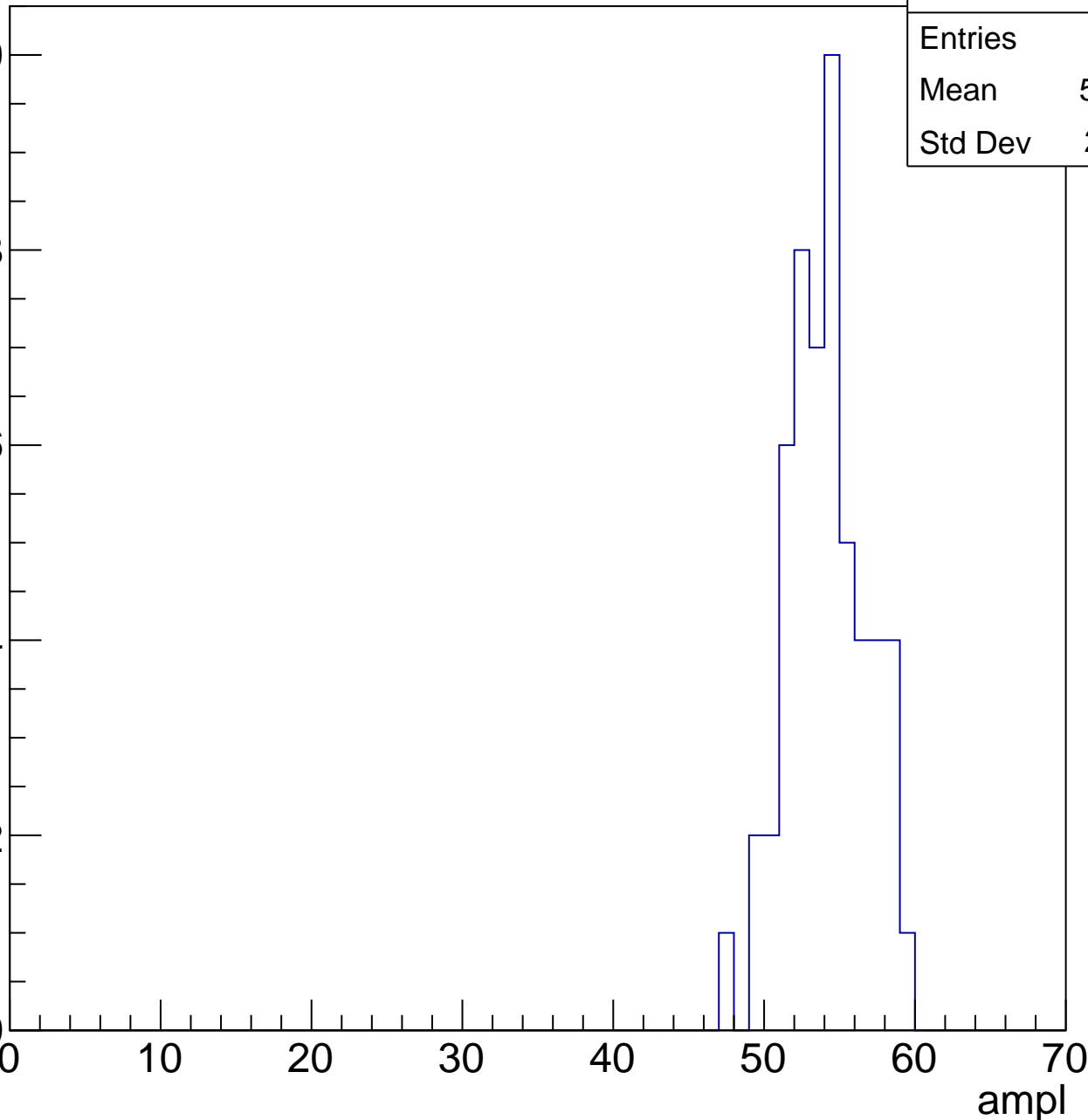
40

50

60

70

ampl

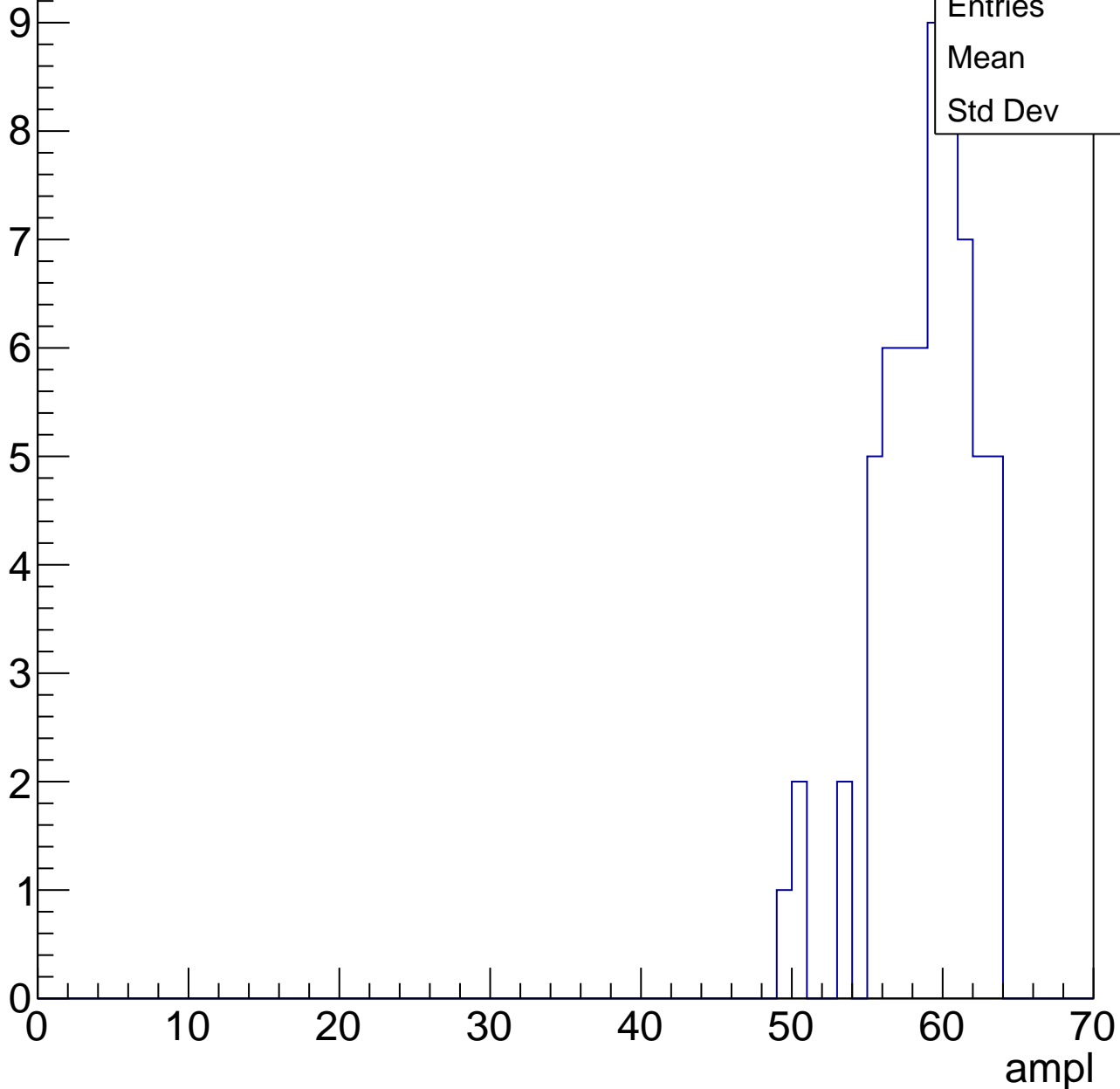


B1L003S, U19-ch114, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	58.4
Std Dev	3.18

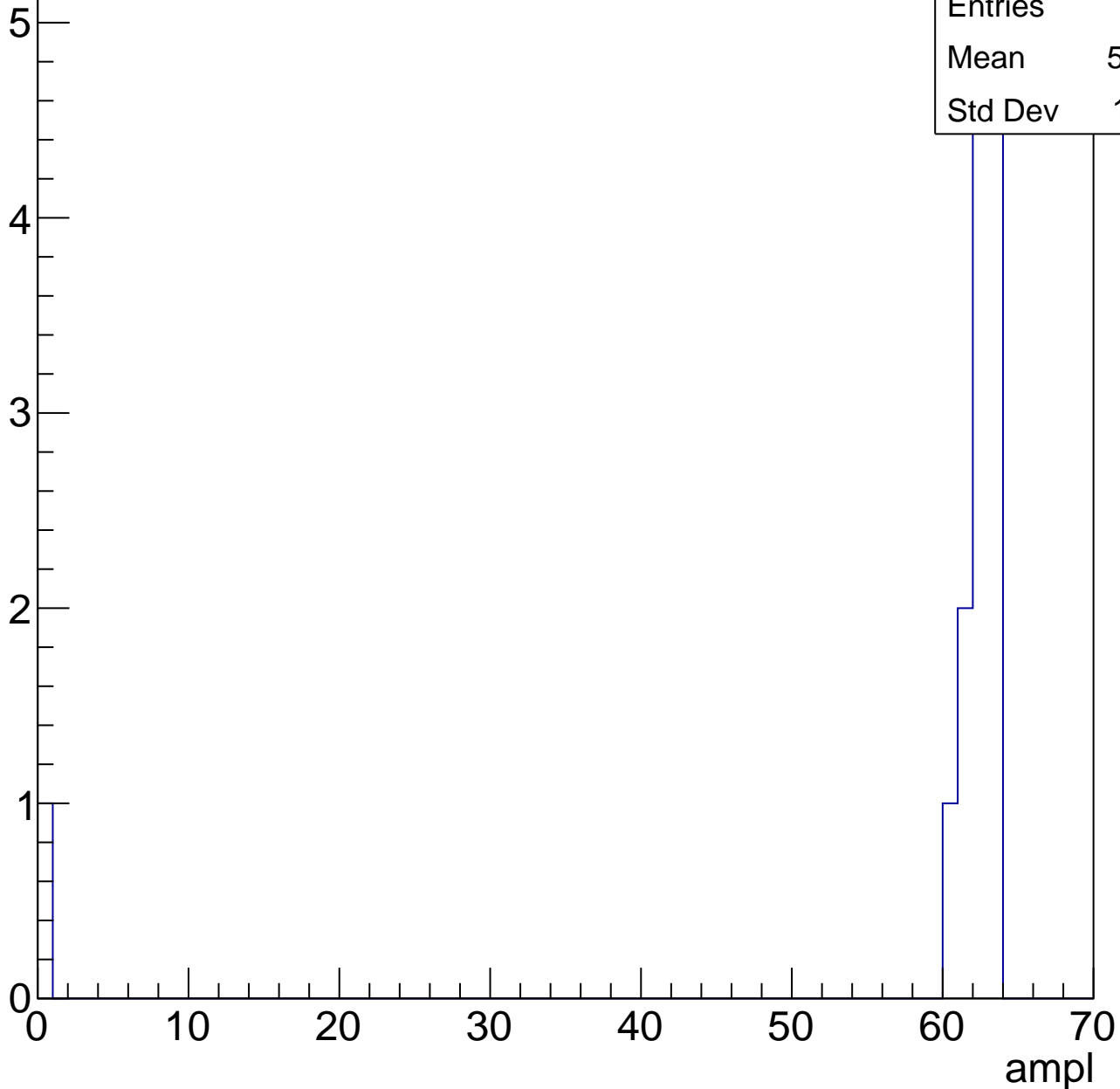


B1L003S, U19-ch114, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

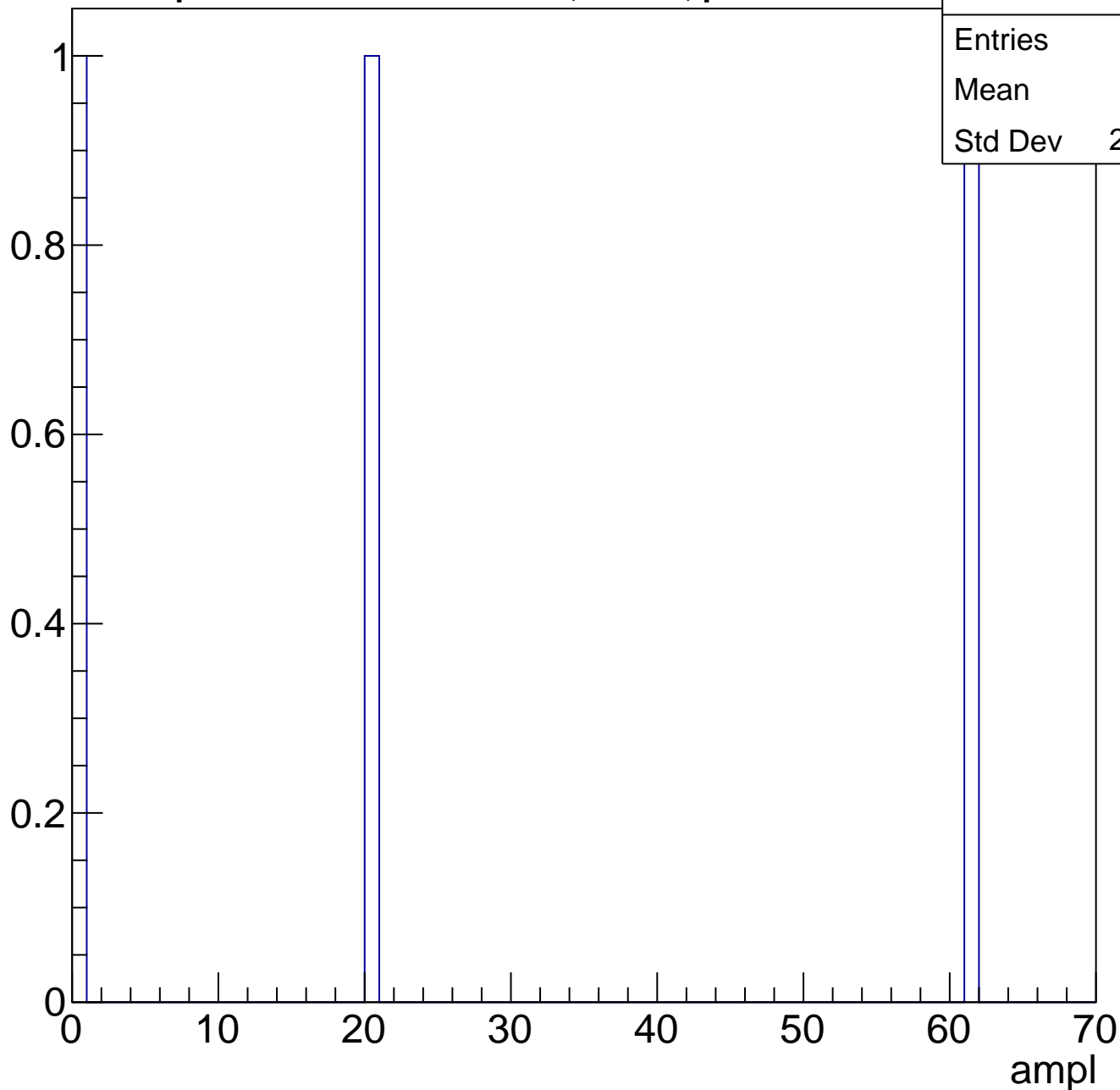
Entries	14
Mean	57.64
Std Dev	16.01



B1L003S, U19-ch114, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	27
Std Dev	25.39

B1L003S, U19-ch115, adc0

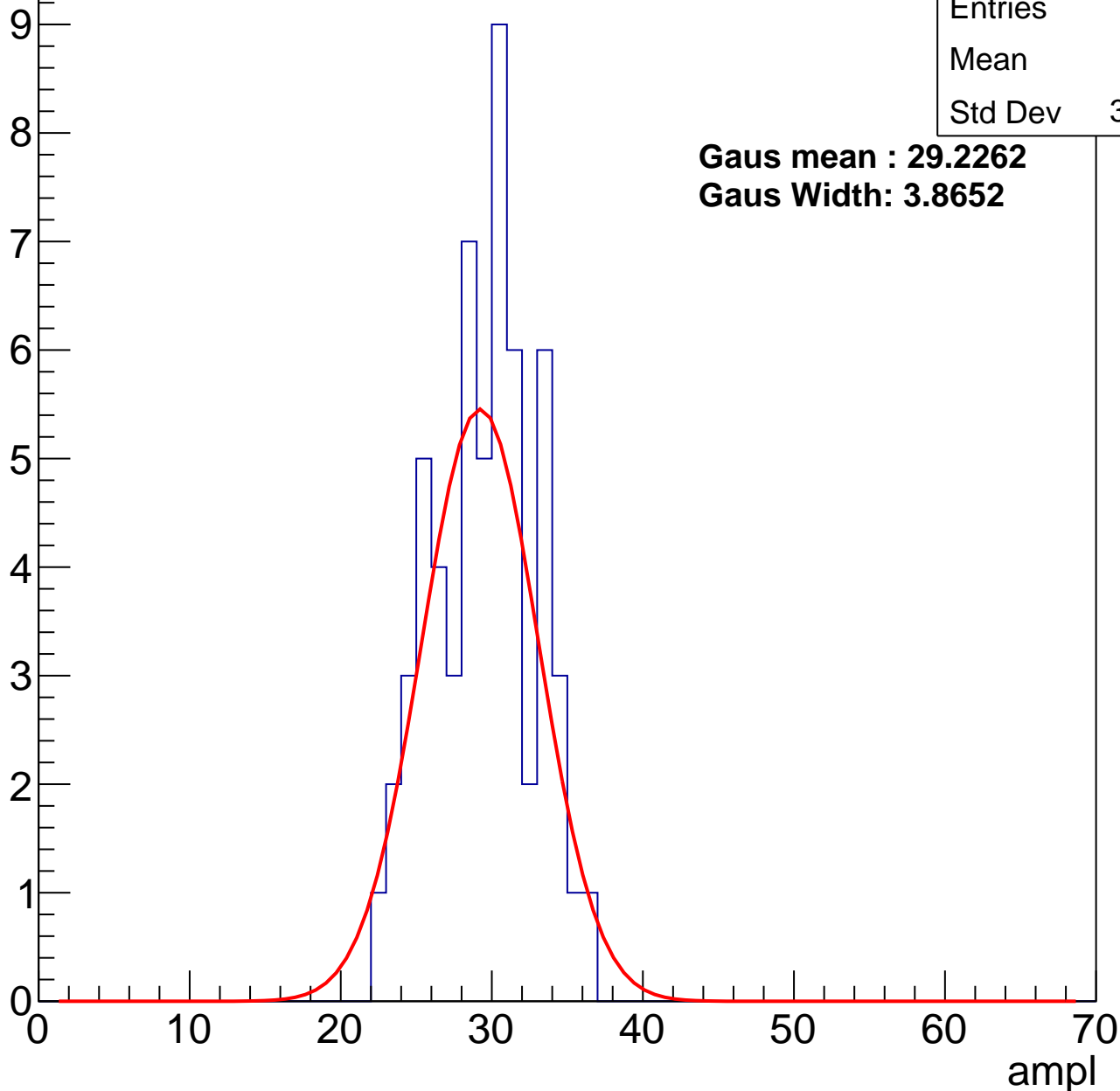
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	29
Std Dev	3.317

Gaus mean : 29.2262

Gaus Width: 3.8652



B1L003S, U19-ch115, adc1

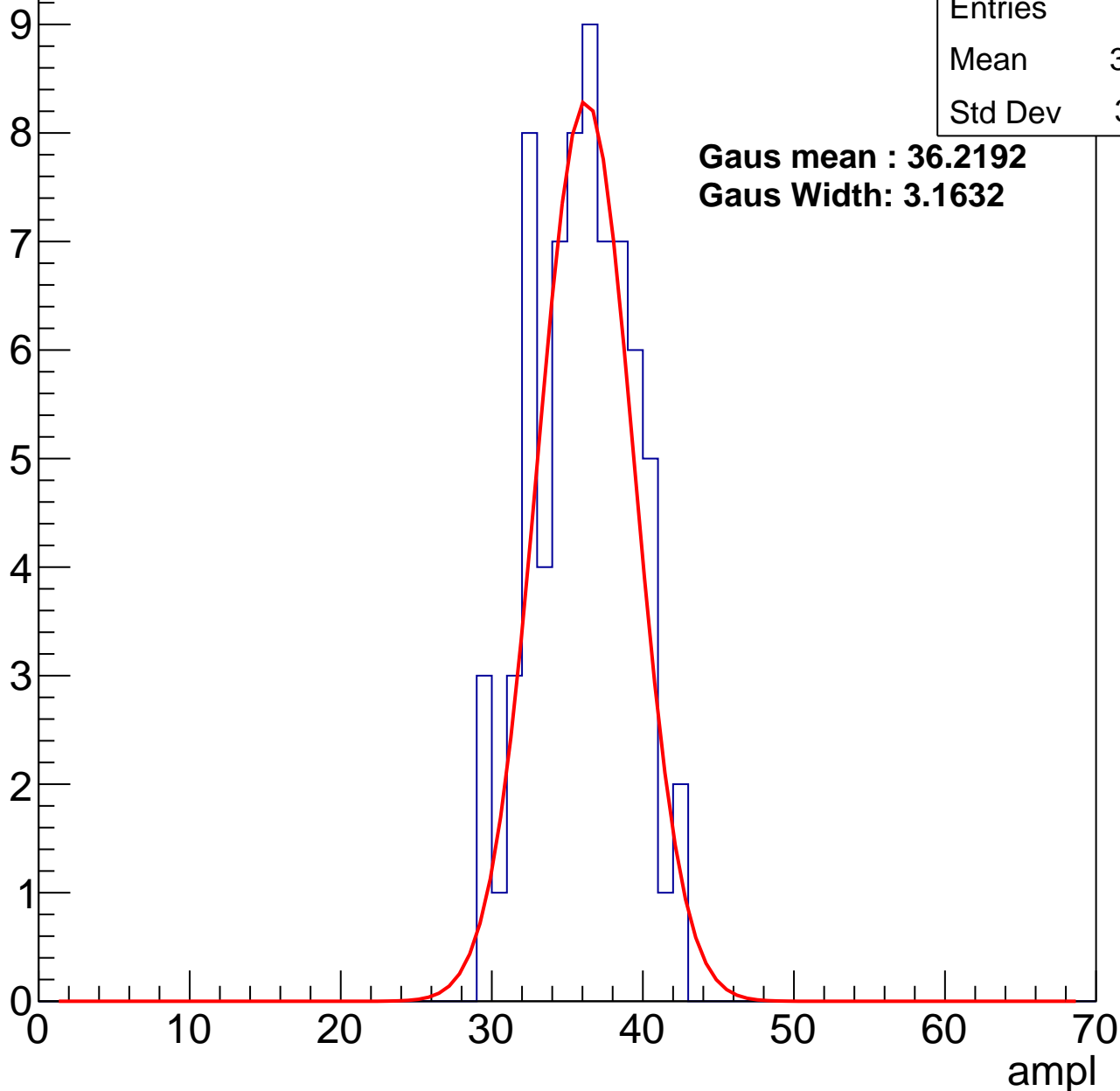
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	35.55
Std Dev	3.161

Gaus mean : 36.2192

Gaus Width: 3.1632



B1L003S, U19-ch115, adc2

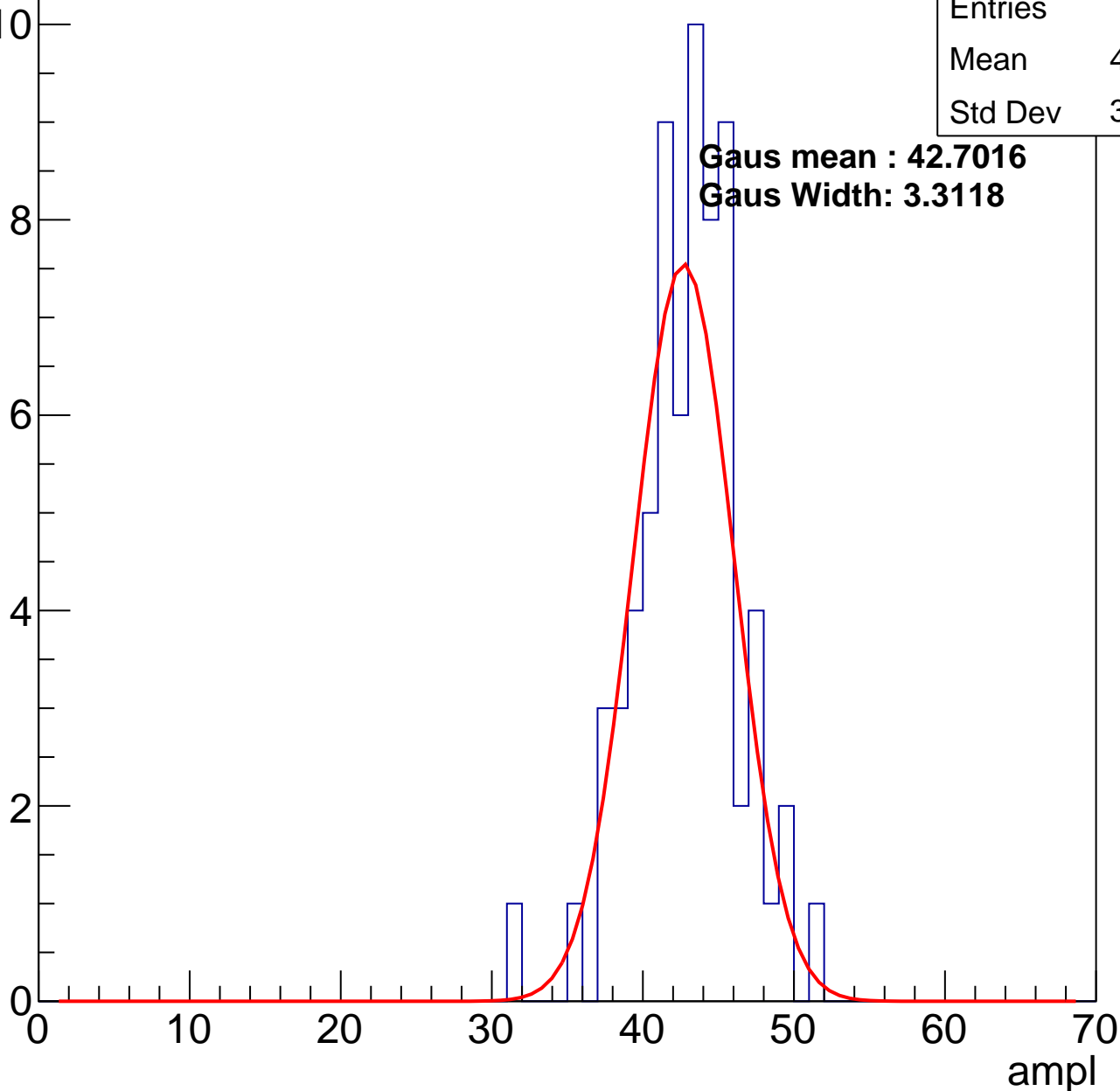
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.49
Std Dev	3.429

Gaus mean : 42.7016

Gaus Width: 3.3118

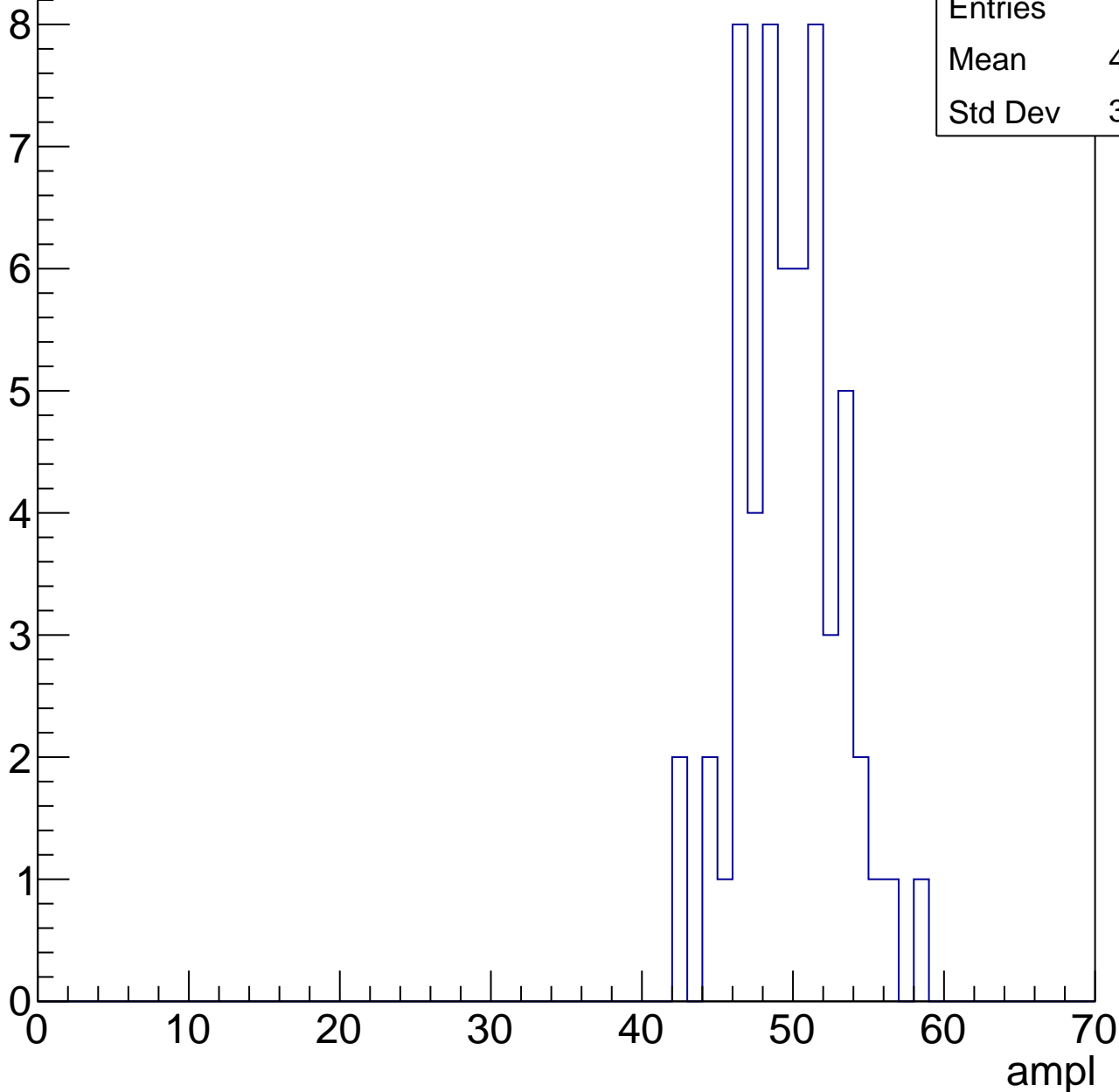


B1L003S, U19-ch115, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

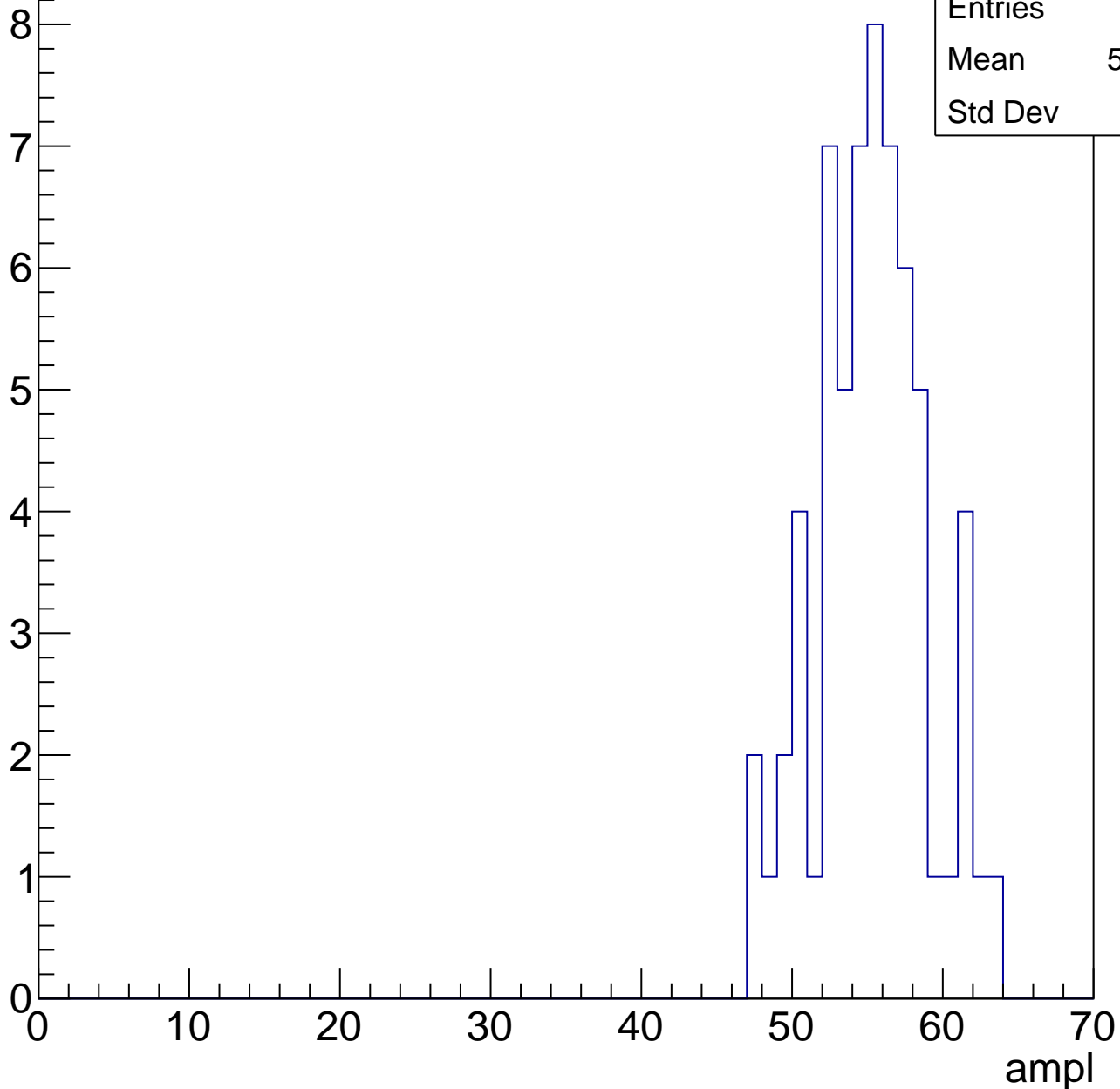
Entries	58
Mean	49.26
Std Dev	3.262



B1L003S, U19-ch115, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

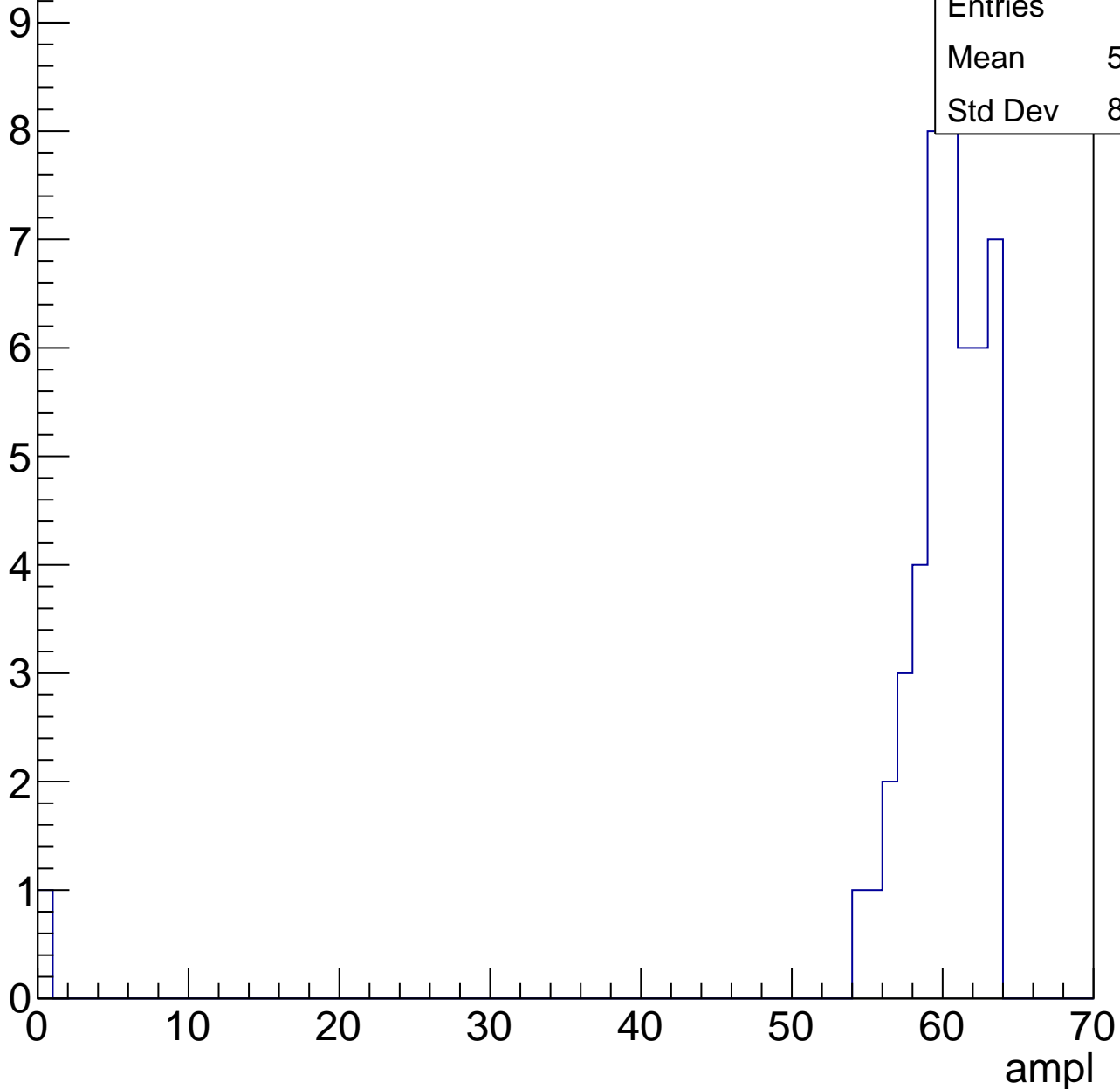


B1L003S, U19-ch115, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

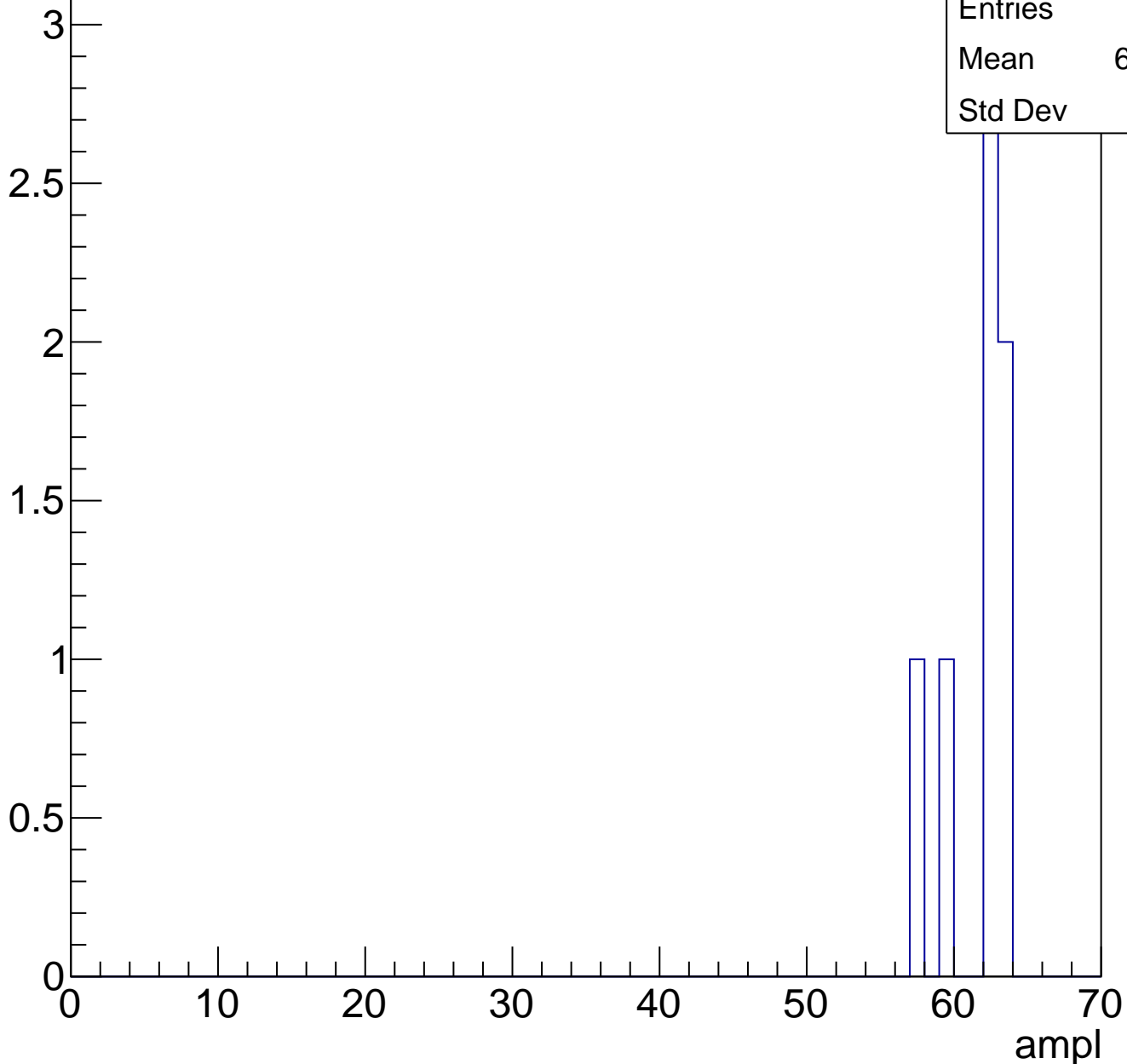
Entries	48
Mean	58.65
Std Dev	8.838



B1L003S, U19-ch115, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	7
Mean	61.14
Std Dev	2.1

B1L003S, U19-ch115, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch116, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	28.21
Std Dev	3.712

Gaus mean : 29.2677

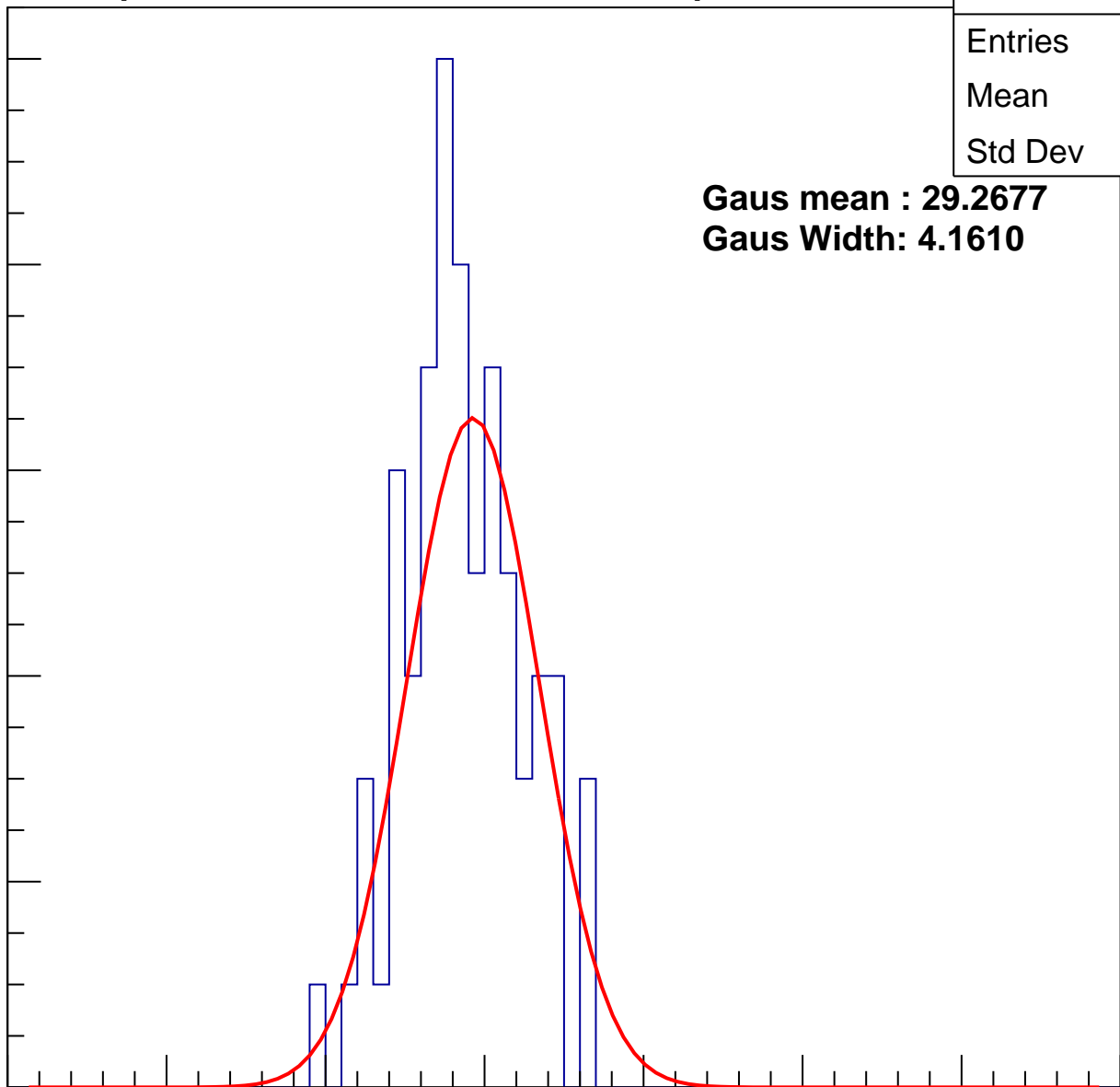
Gaus Width: 4.1610

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch116, adc1

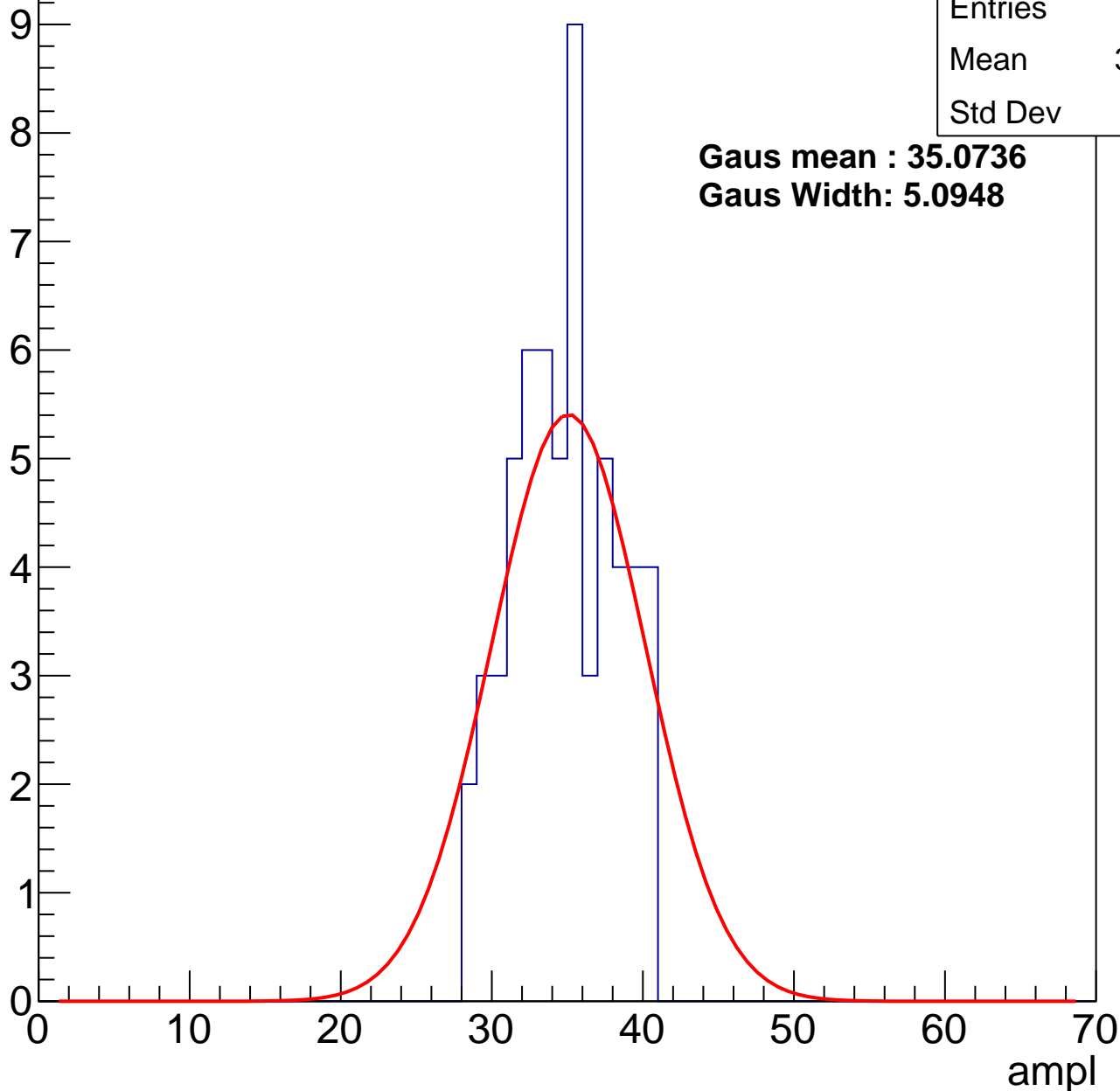
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	34.31
Std Dev	3.29

Gaus mean : 35.0736

Gaus Width: 5.0948



B1L003S, U19-ch116, adc2

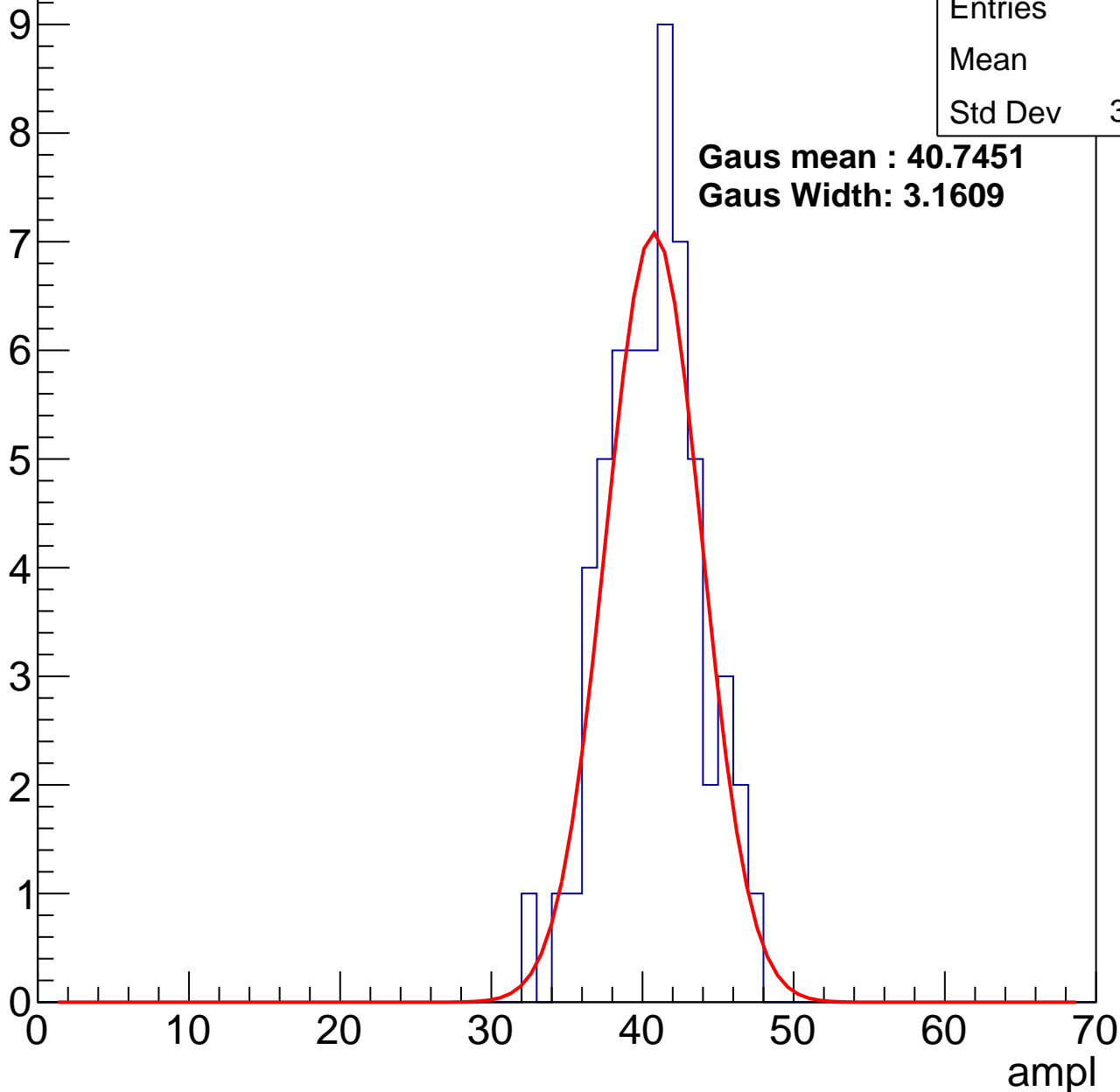
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	40.2
Std Dev	3.112

Gaus mean : 40.7451

Gaus Width: 3.1609

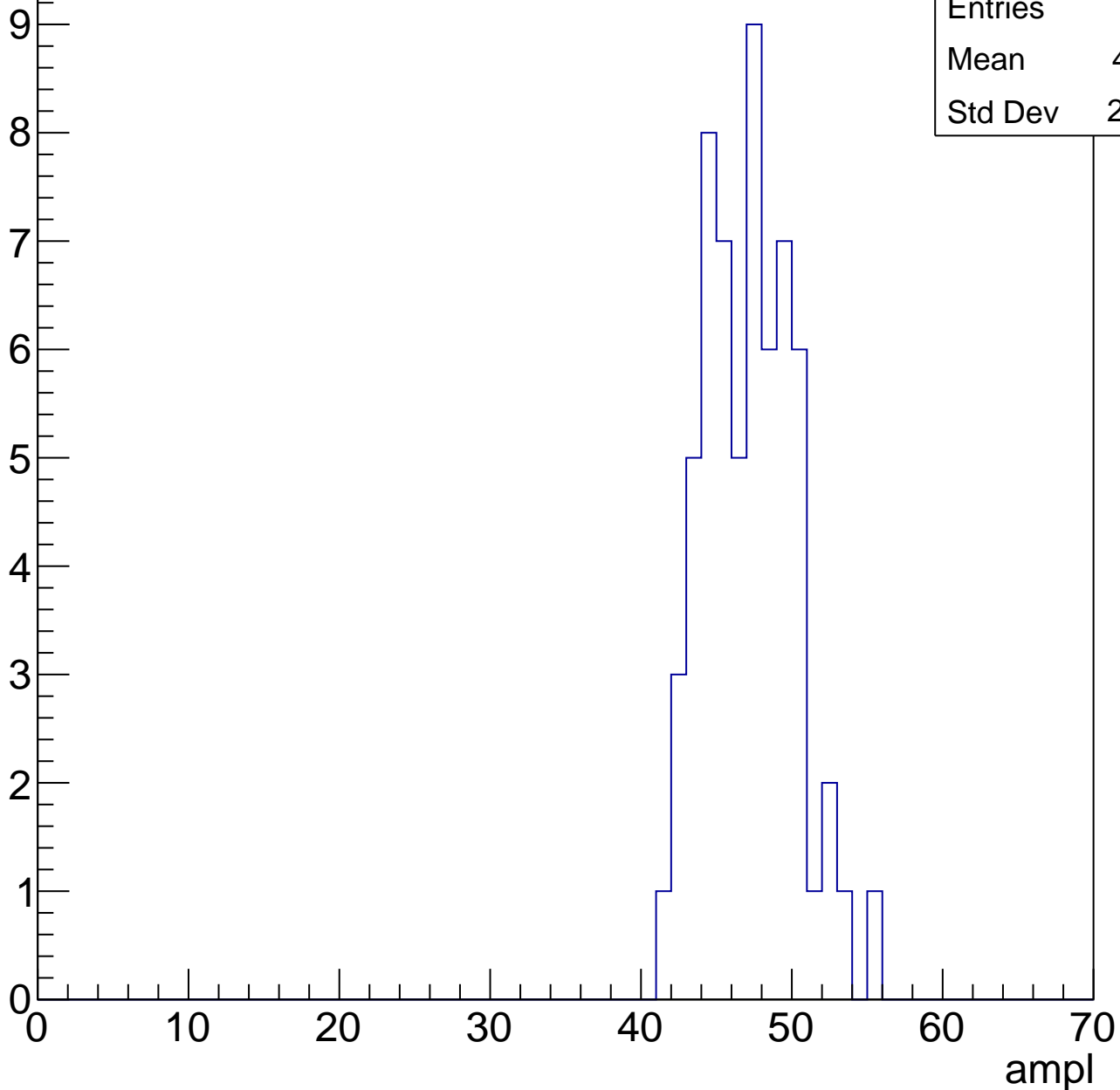


B1L003S, U19-ch116, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	46.71
Std Dev	2.964

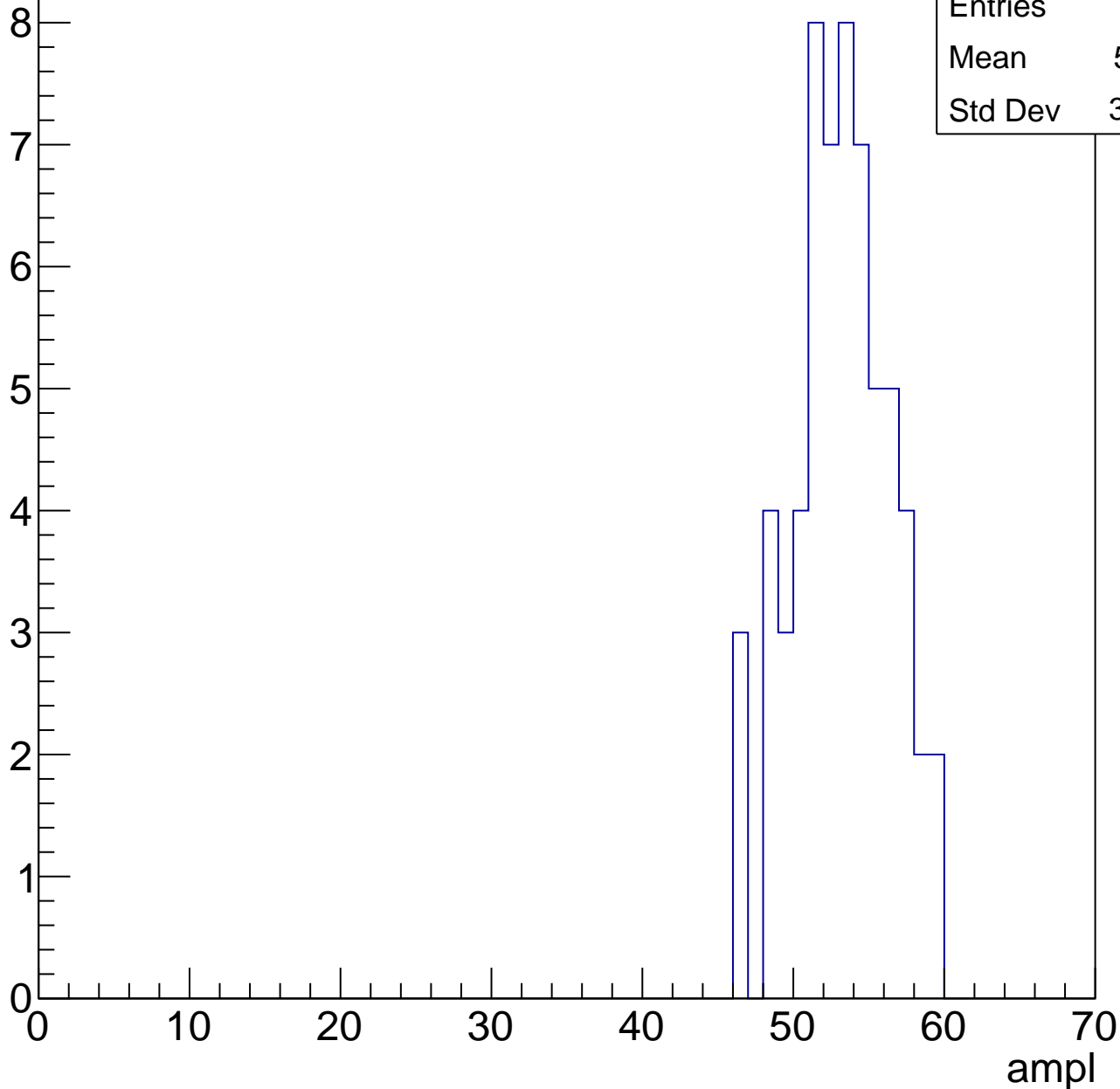


B1L003S, U19-ch116, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	52.71
Std Dev	3.169

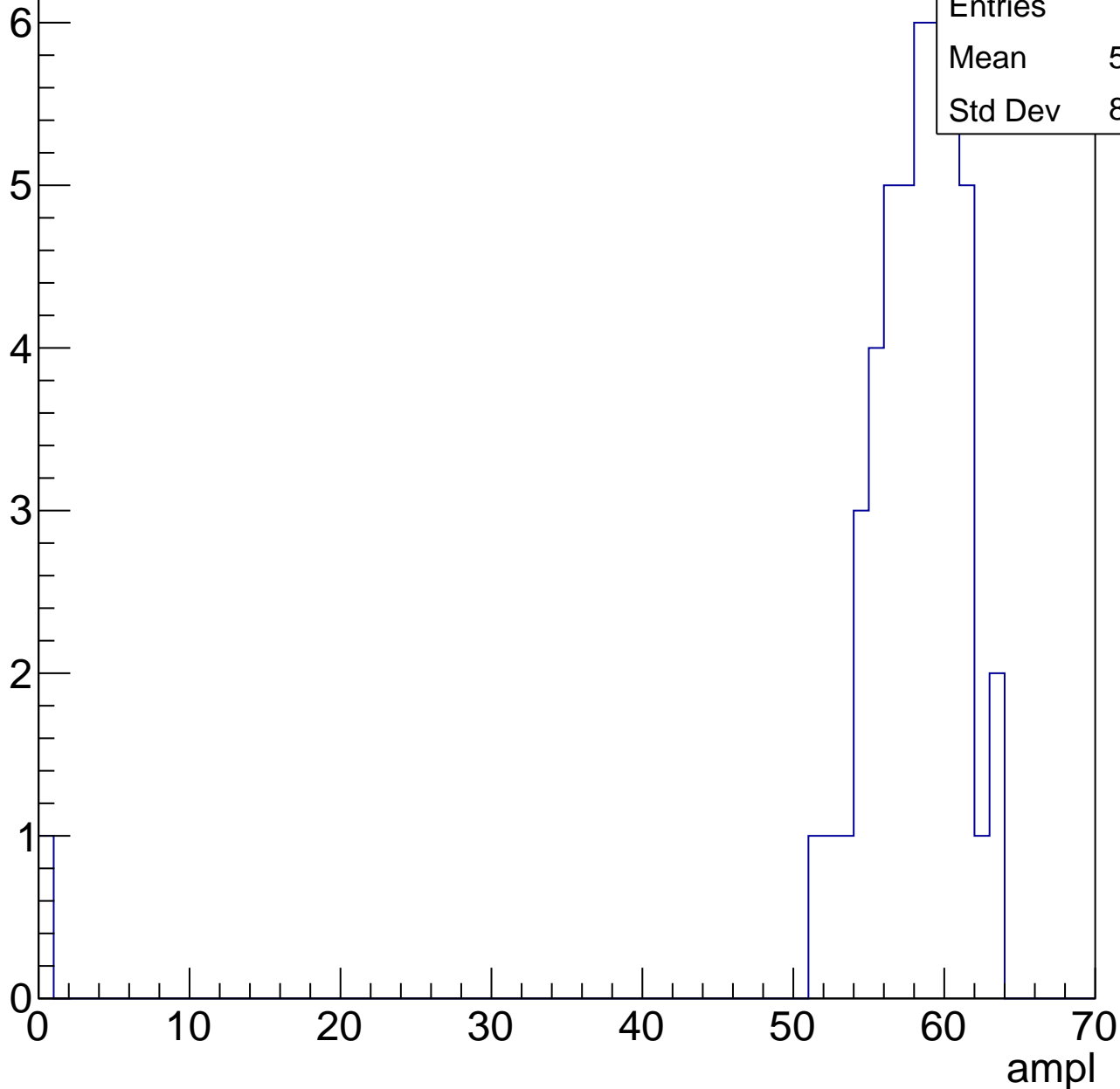


B1L003S, U19-ch116, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	56.55
Std Dev	8.783

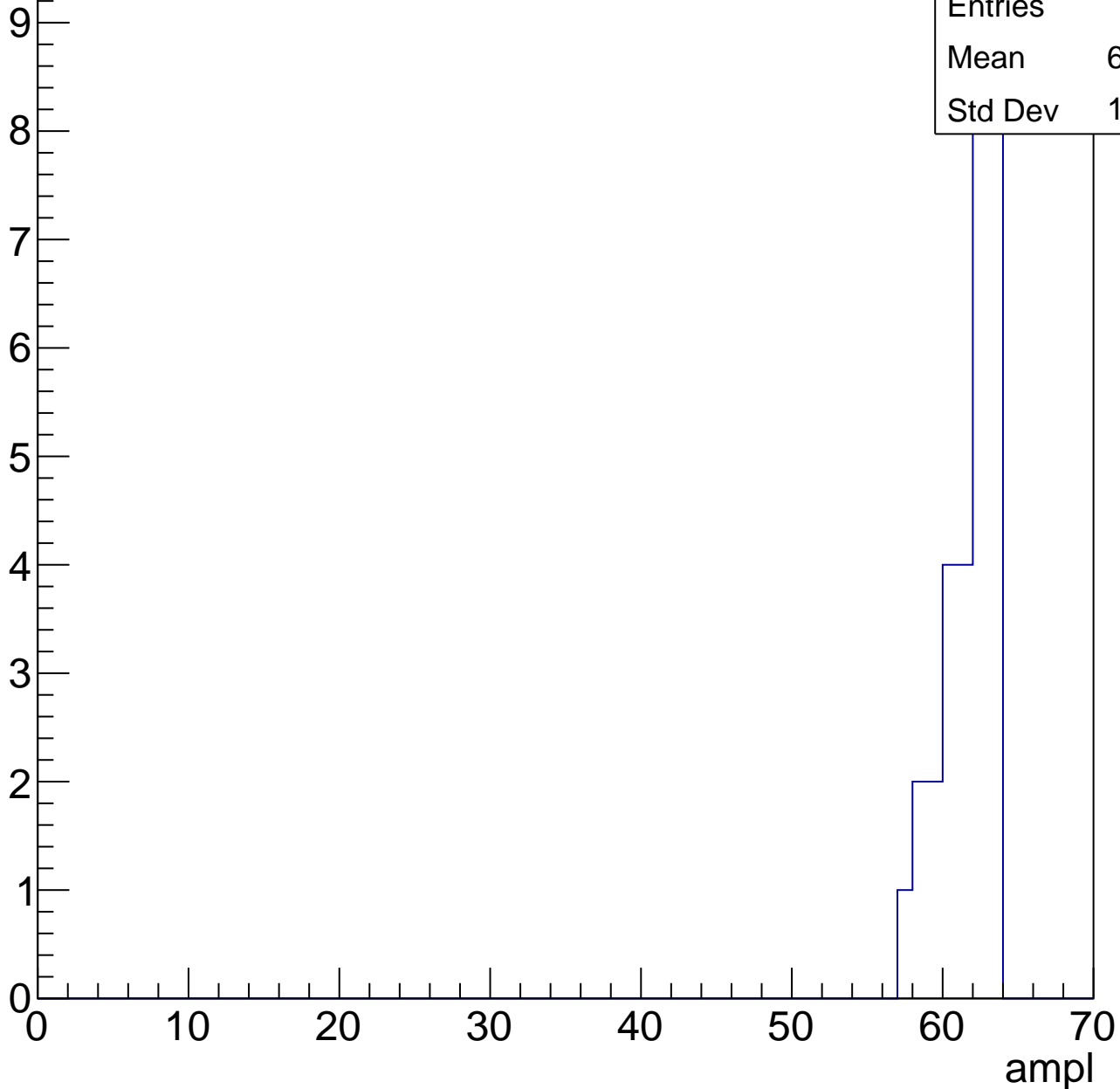


B1L003S, U19-ch116, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	30
Mean	61.23
Std Dev	1.687



B1L003S, U19-ch116, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch117, adc0

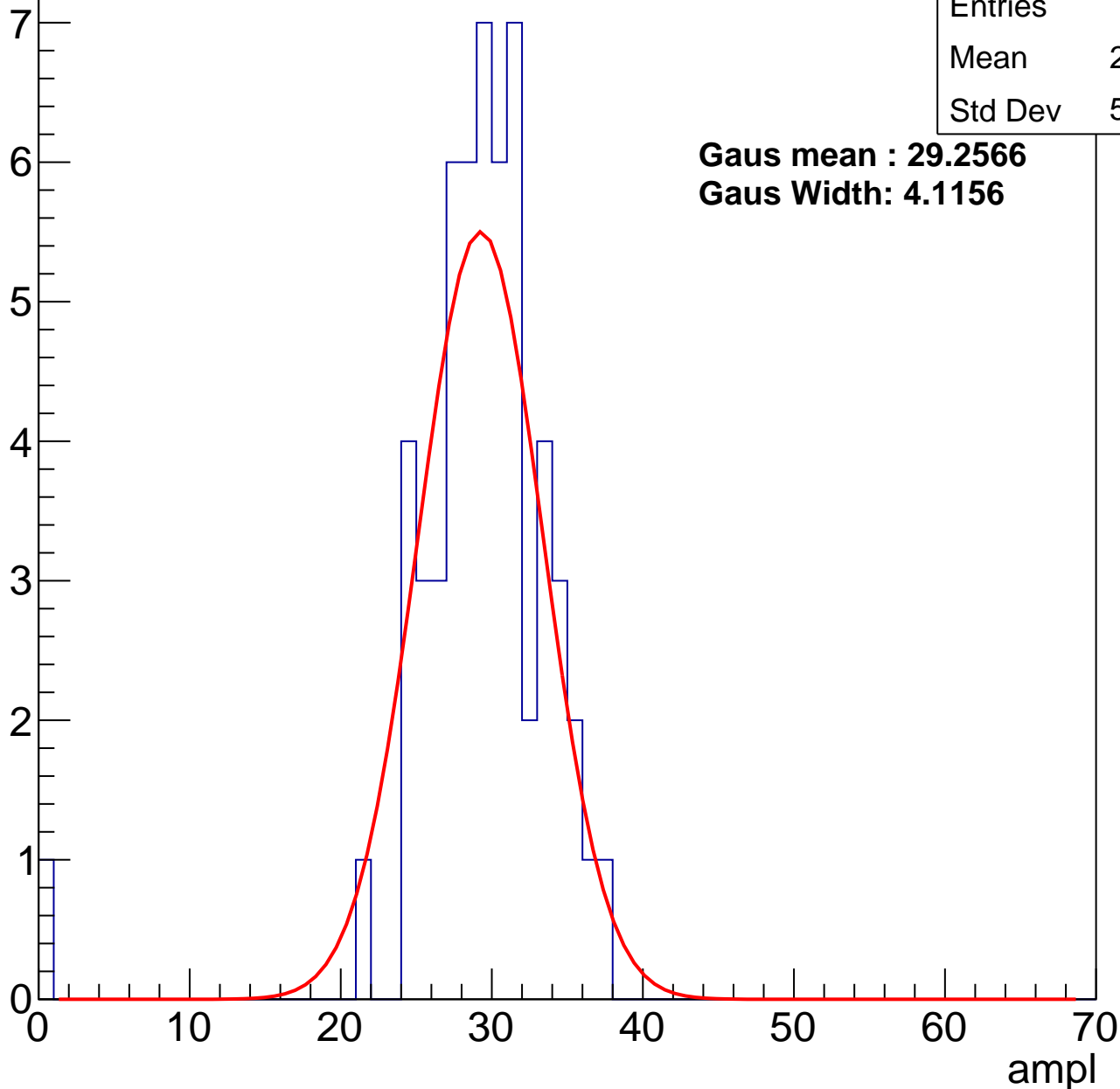
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	28.79
Std Dev	5.102

Gaus mean : 29.2566

Gaus Width: 4.1156



B1L003S, U19-ch117, adc1

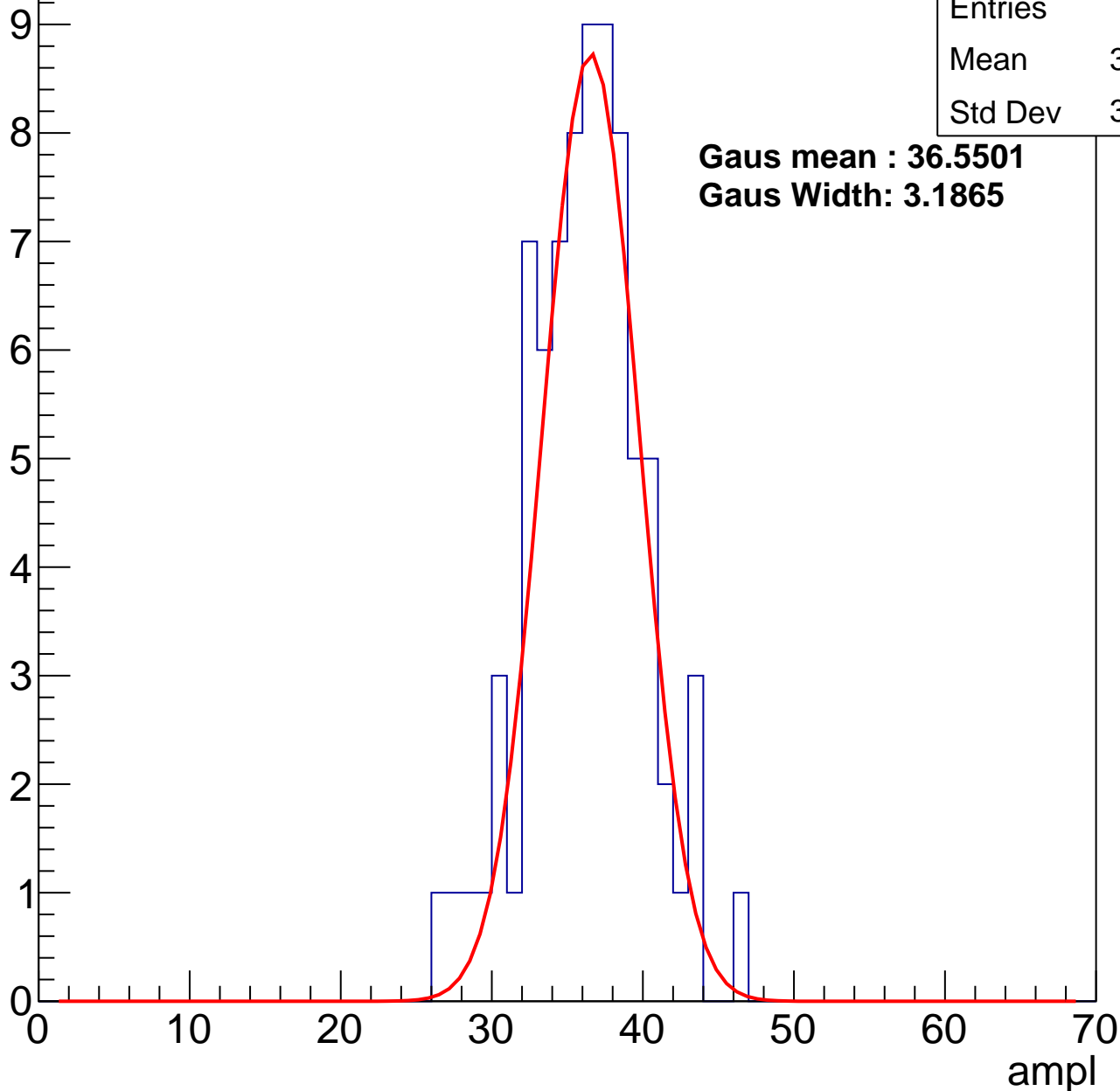
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	35.77
Std Dev	3.772

Gaus mean : 36.5501

Gaus Width: 3.1865



B1L003S, U19-ch117, adc2

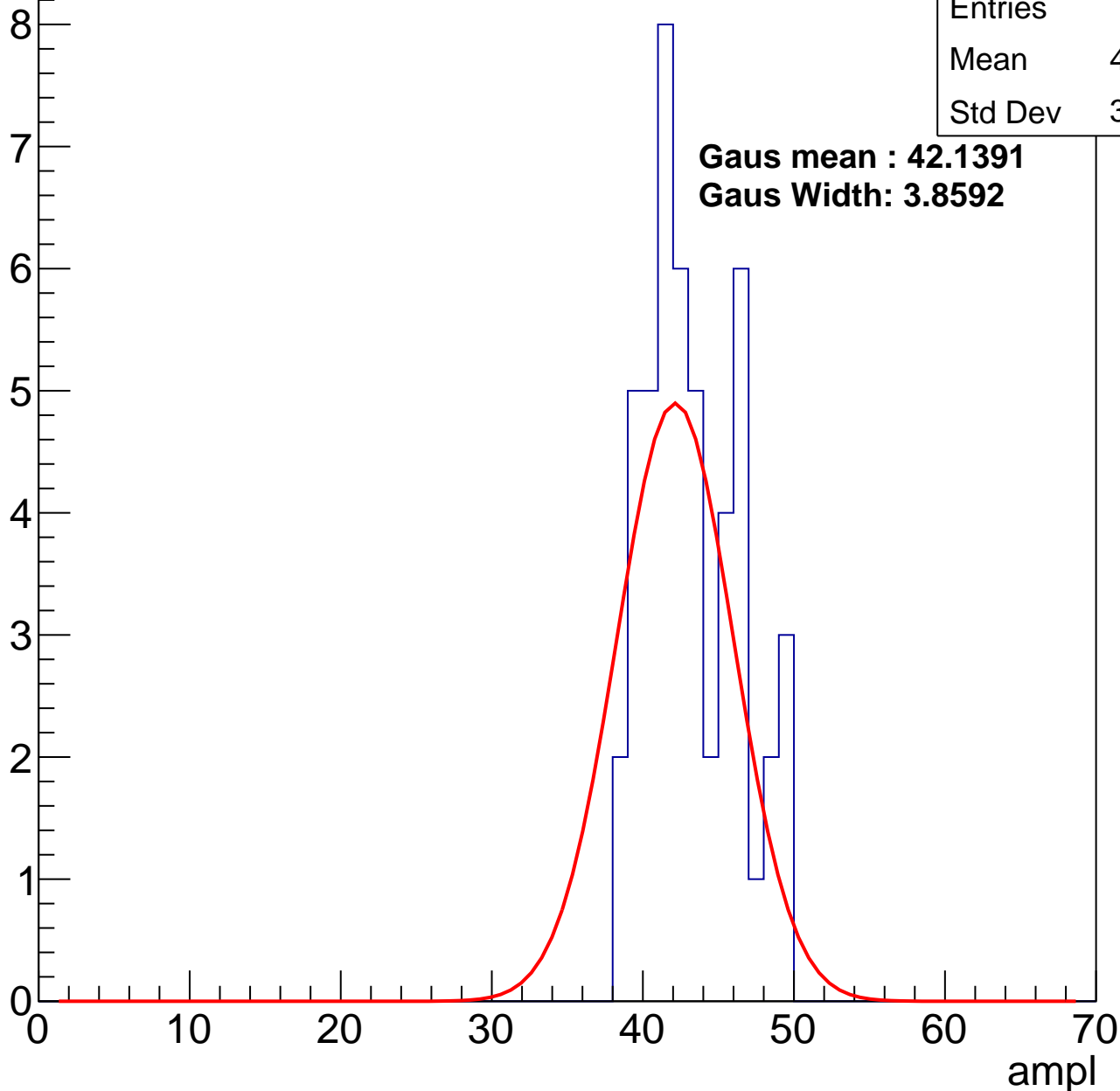
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	42.86
Std Dev	3.057

Gaus mean : 42.1391

Gaus Width: 3.8592

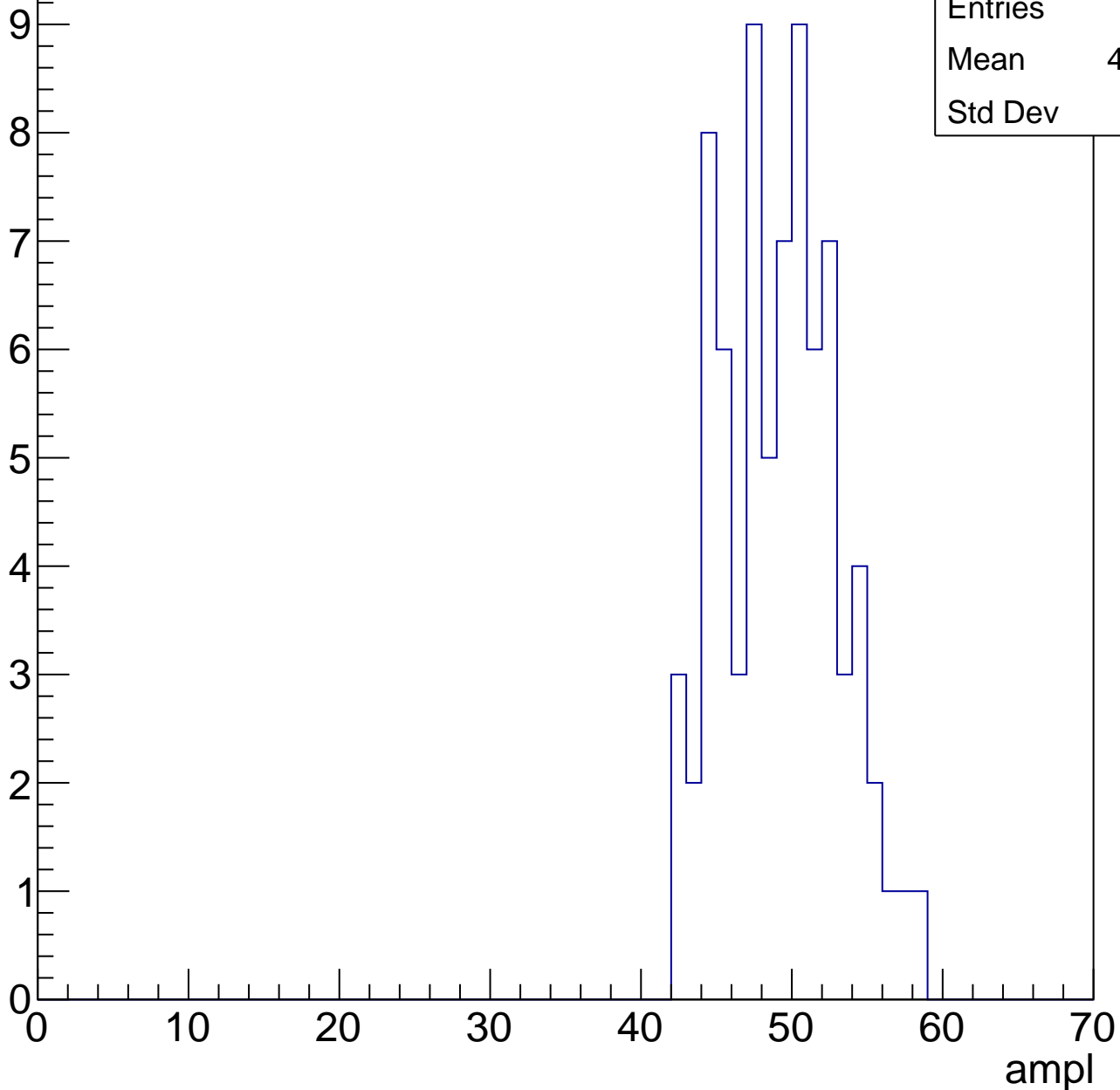


B1L003S, U19-ch117, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	48.75
Std Dev	3.76

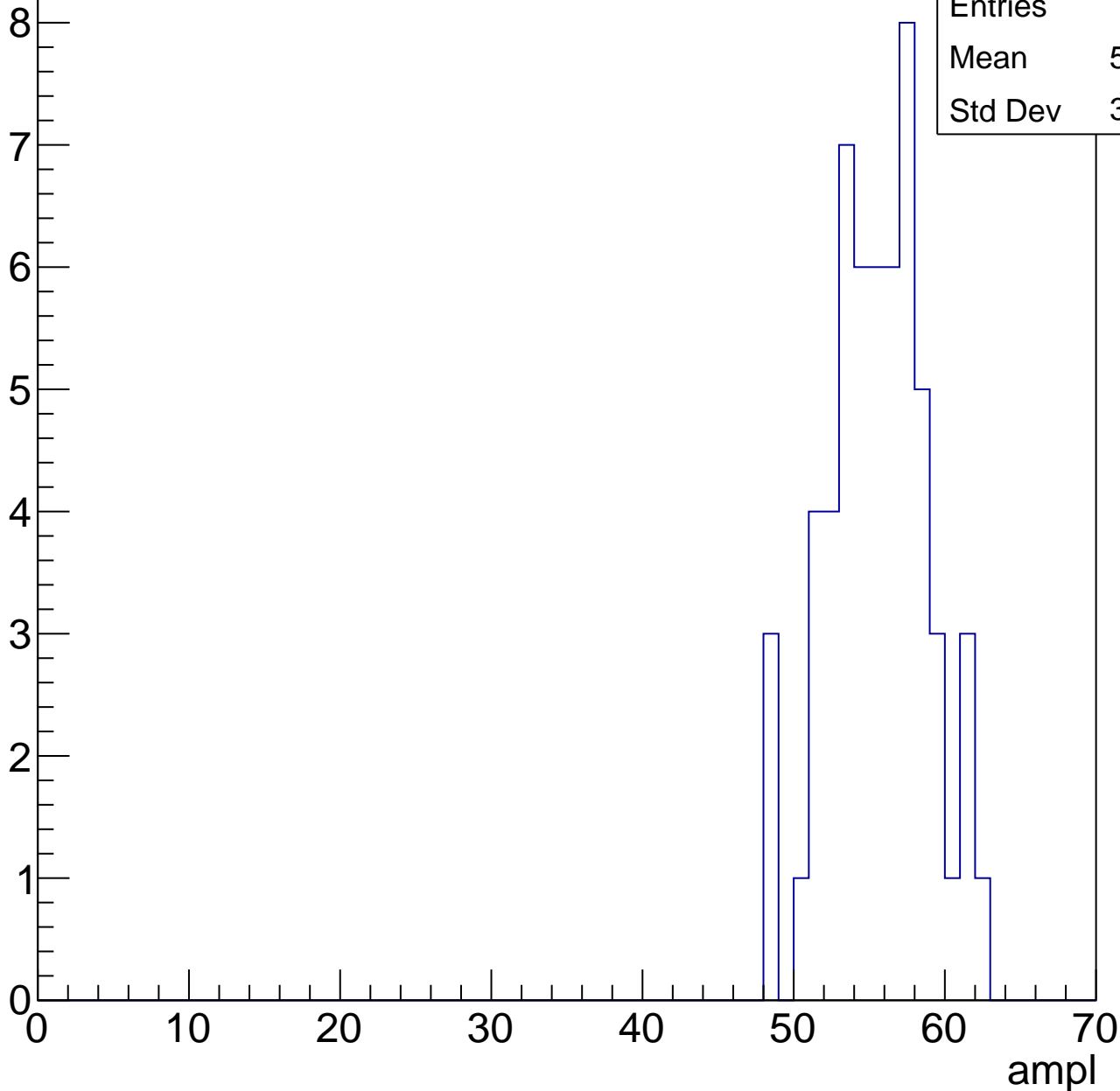


B1L003S, U19-ch117, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.09
Std Dev	3.266

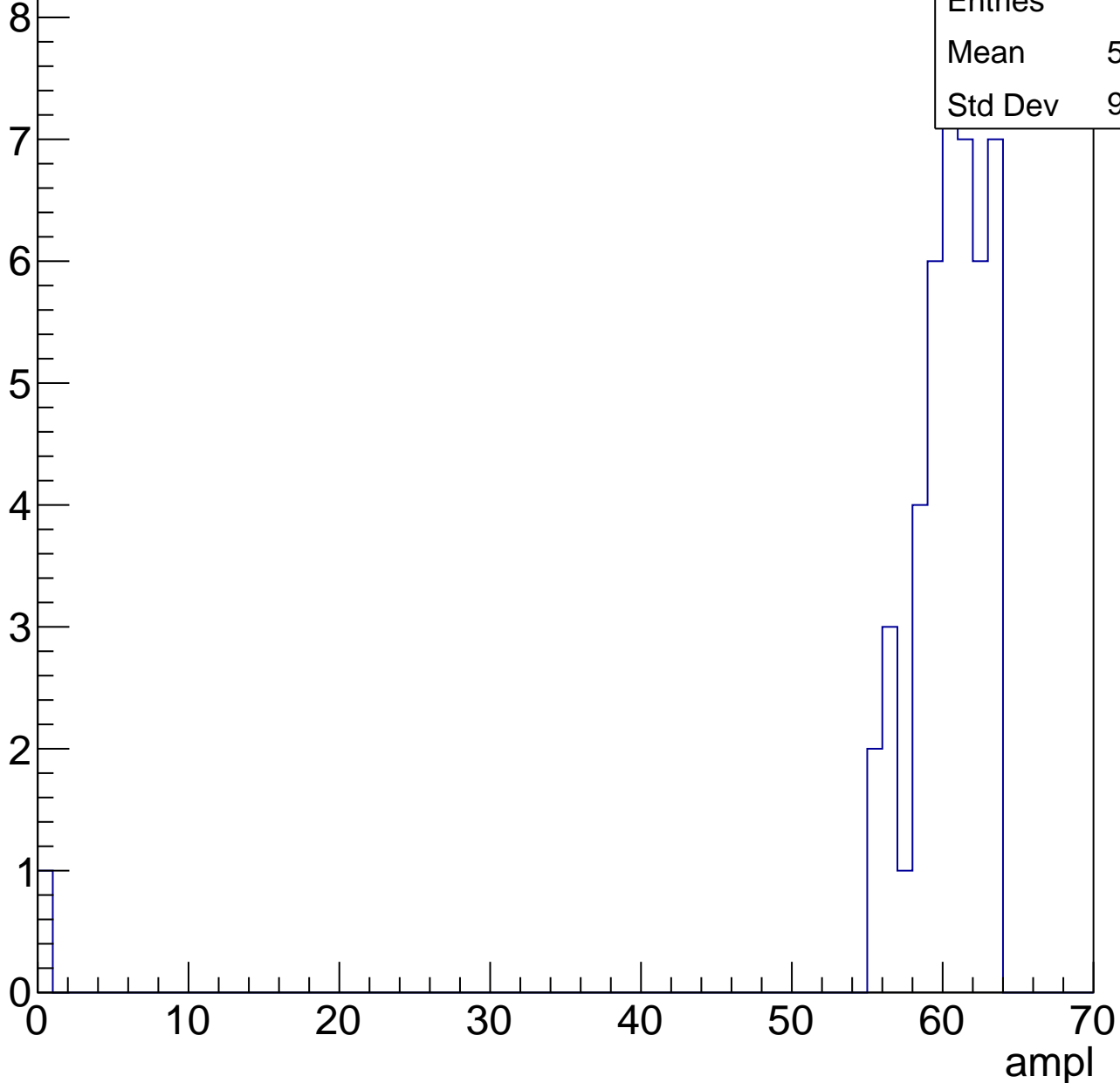


B1L003S, U19-ch117, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

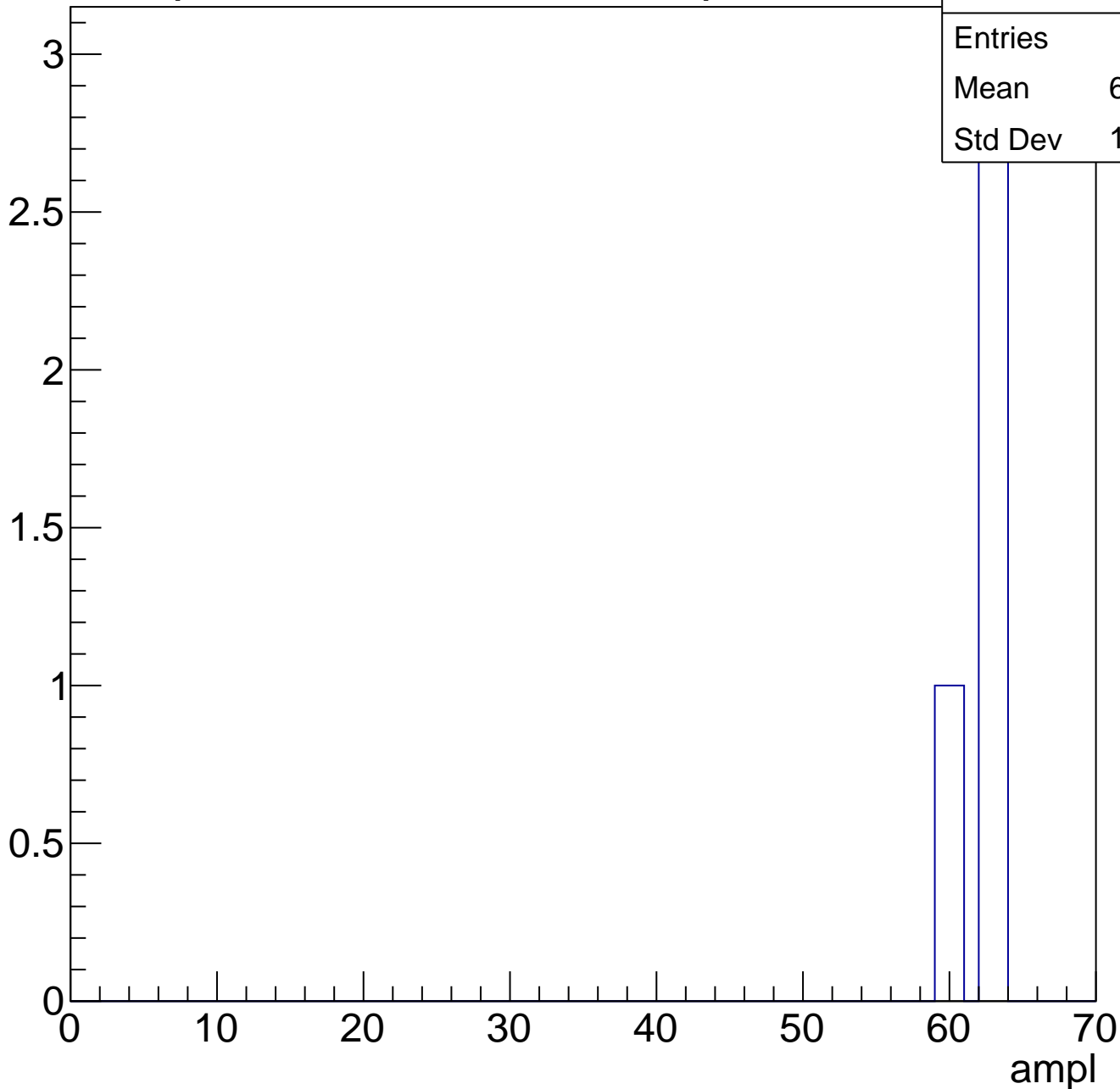
Entries	45
Mean	58.69
Std Dev	9.123



B1L003S, U19-ch117, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch117, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch118, adc0

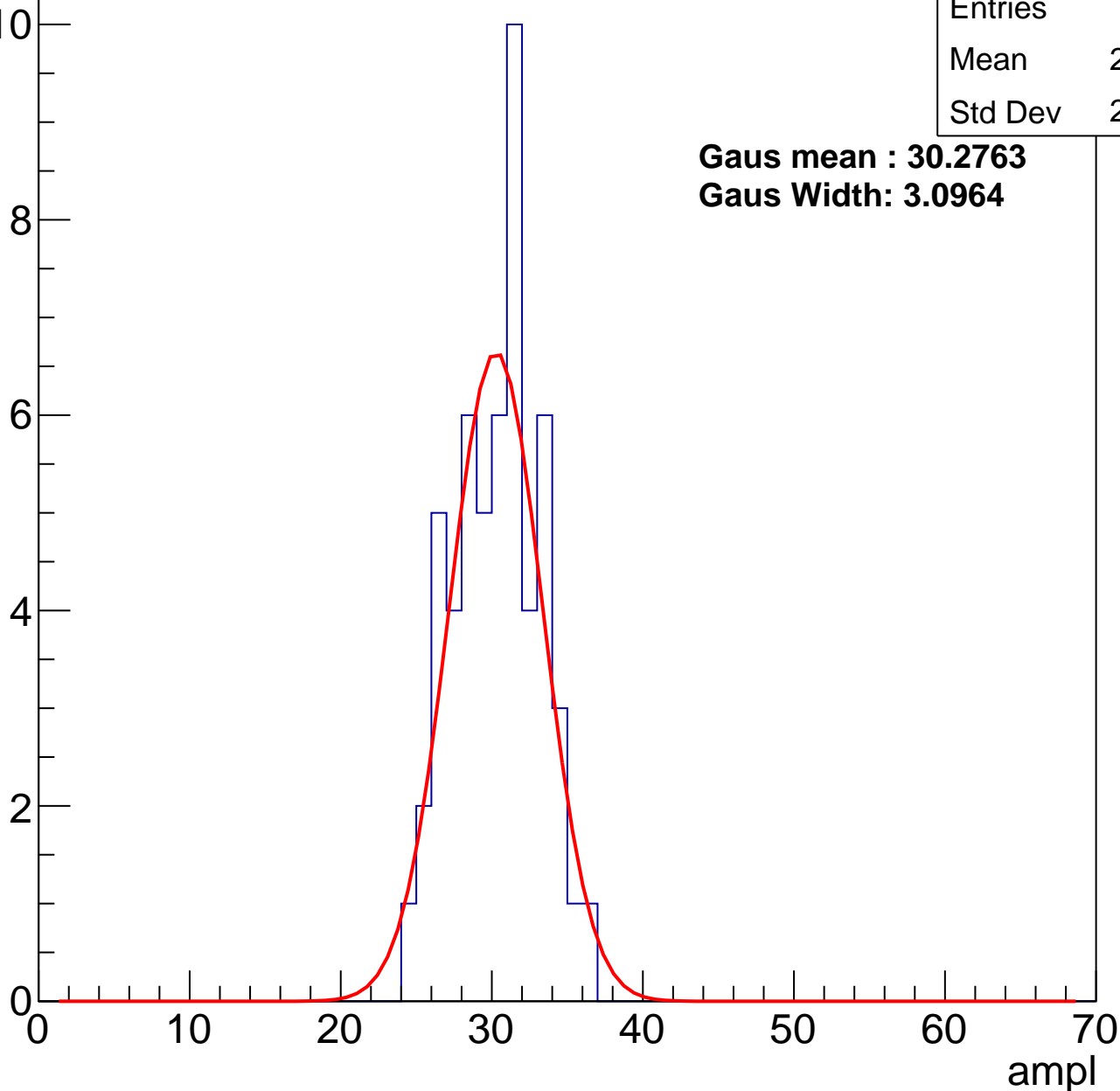
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	29.89
Std Dev	2.787

Gaus mean : 30.2763

Gaus Width: 3.0964



B1L003S, U19-ch118, adc1

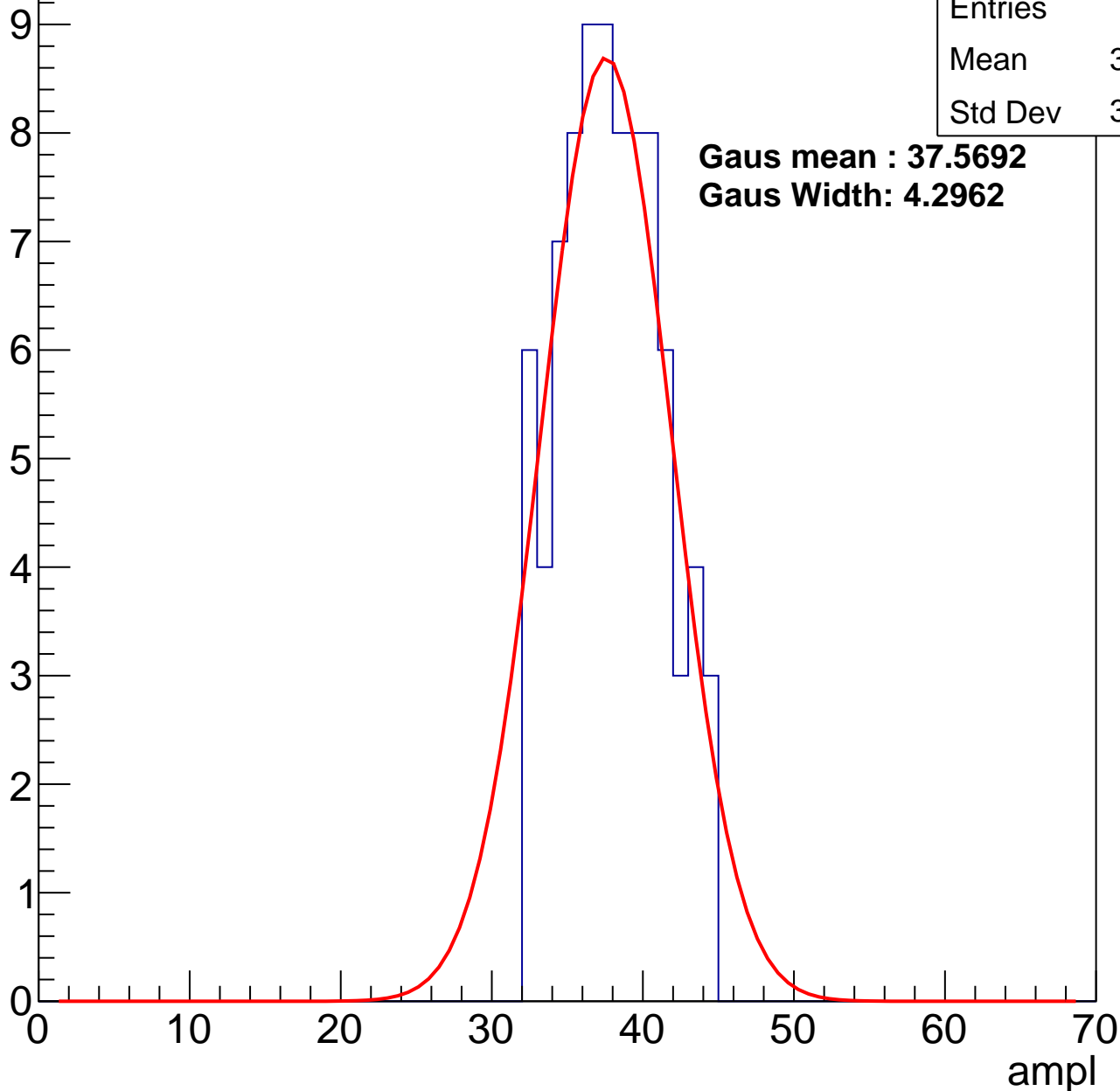
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	37.48
Std Dev	3.243

Gaus mean : 37.5692

Gaus Width: 4.2962



B1L003S, U19-ch118, adc2

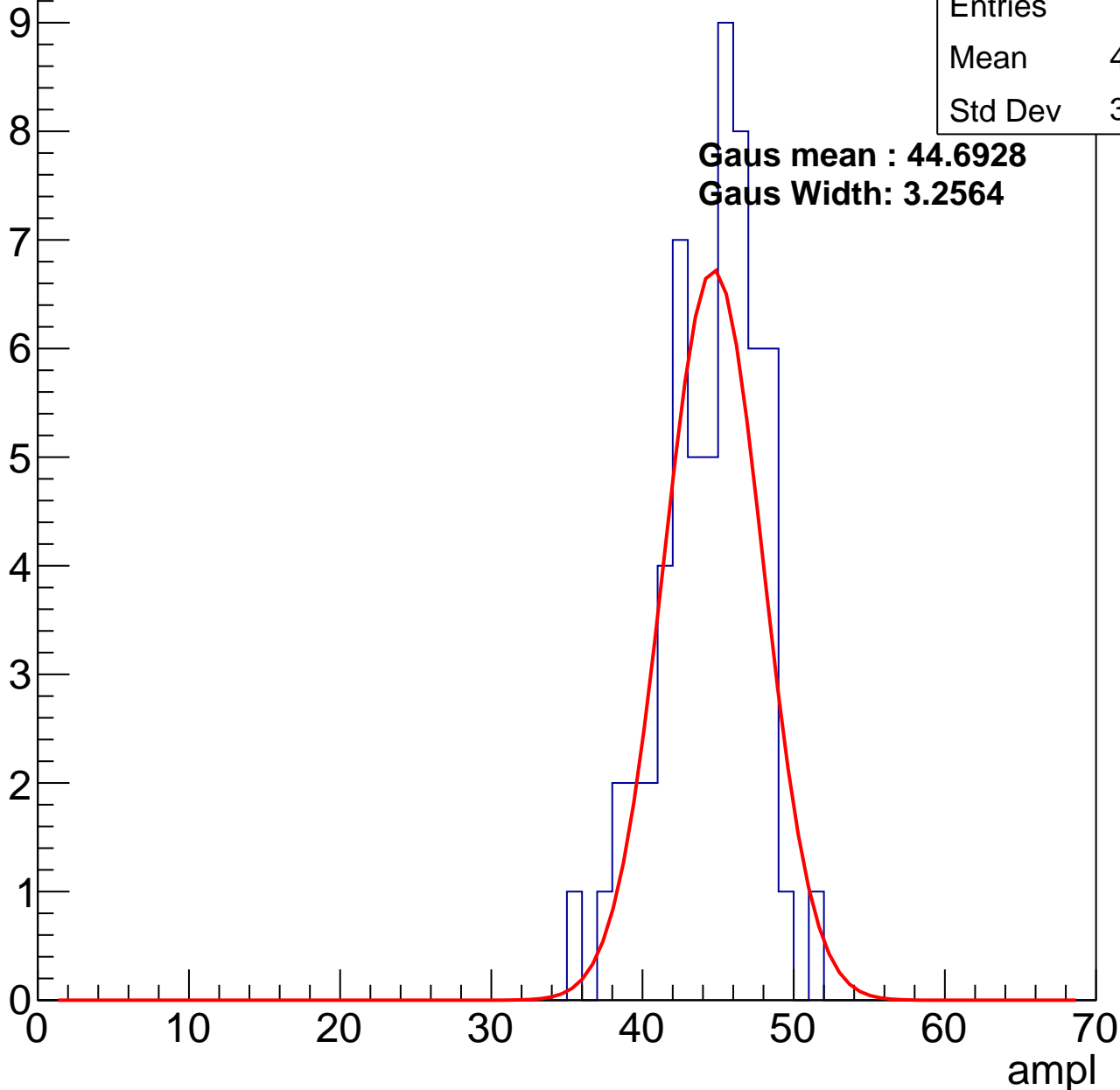
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	44.03
Std Dev	3.209

Gaus mean : 44.6928

Gaus Width: 3.2564

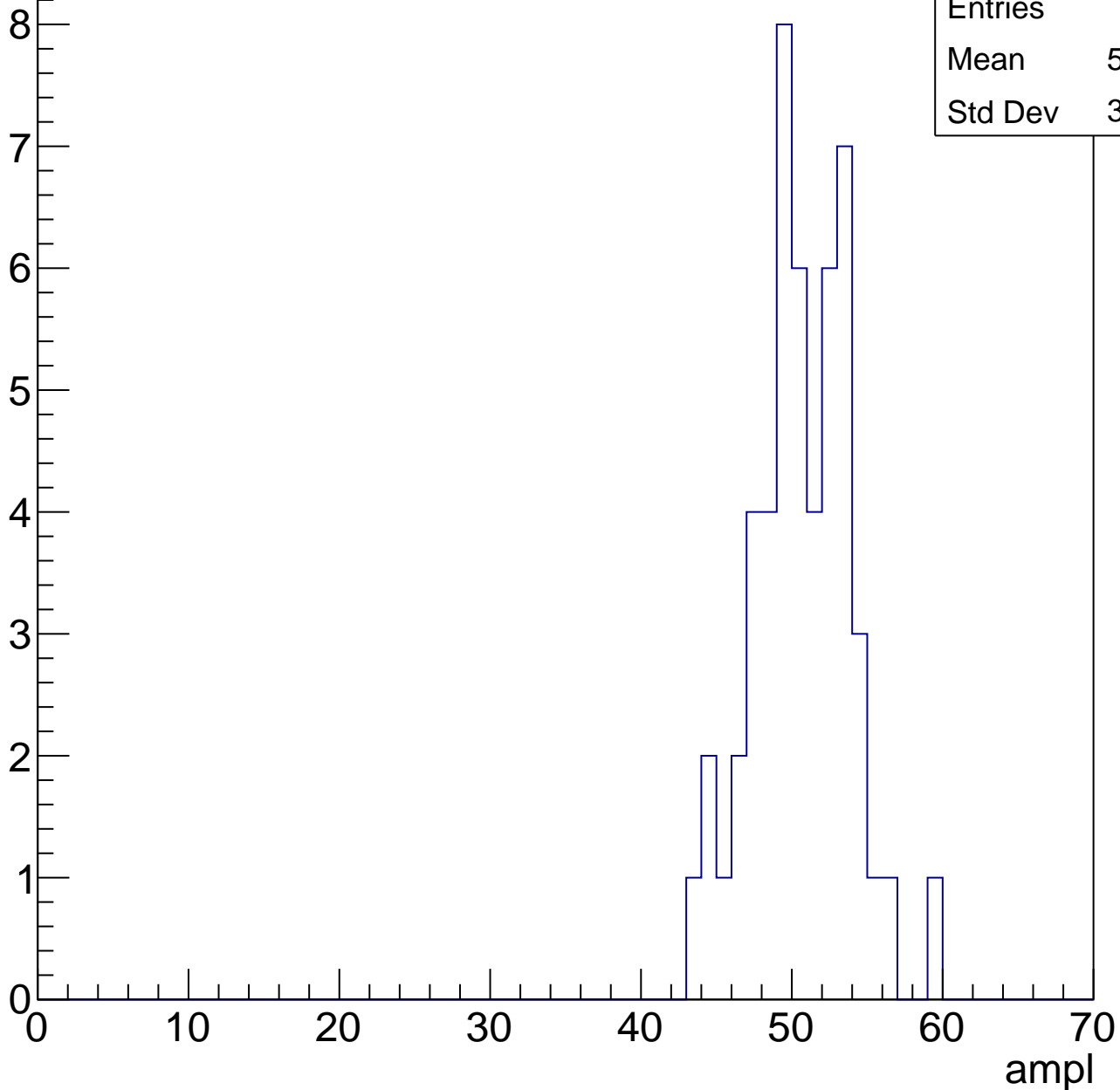


B1L003S, U19-ch118, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	50.18
Std Dev	3.185

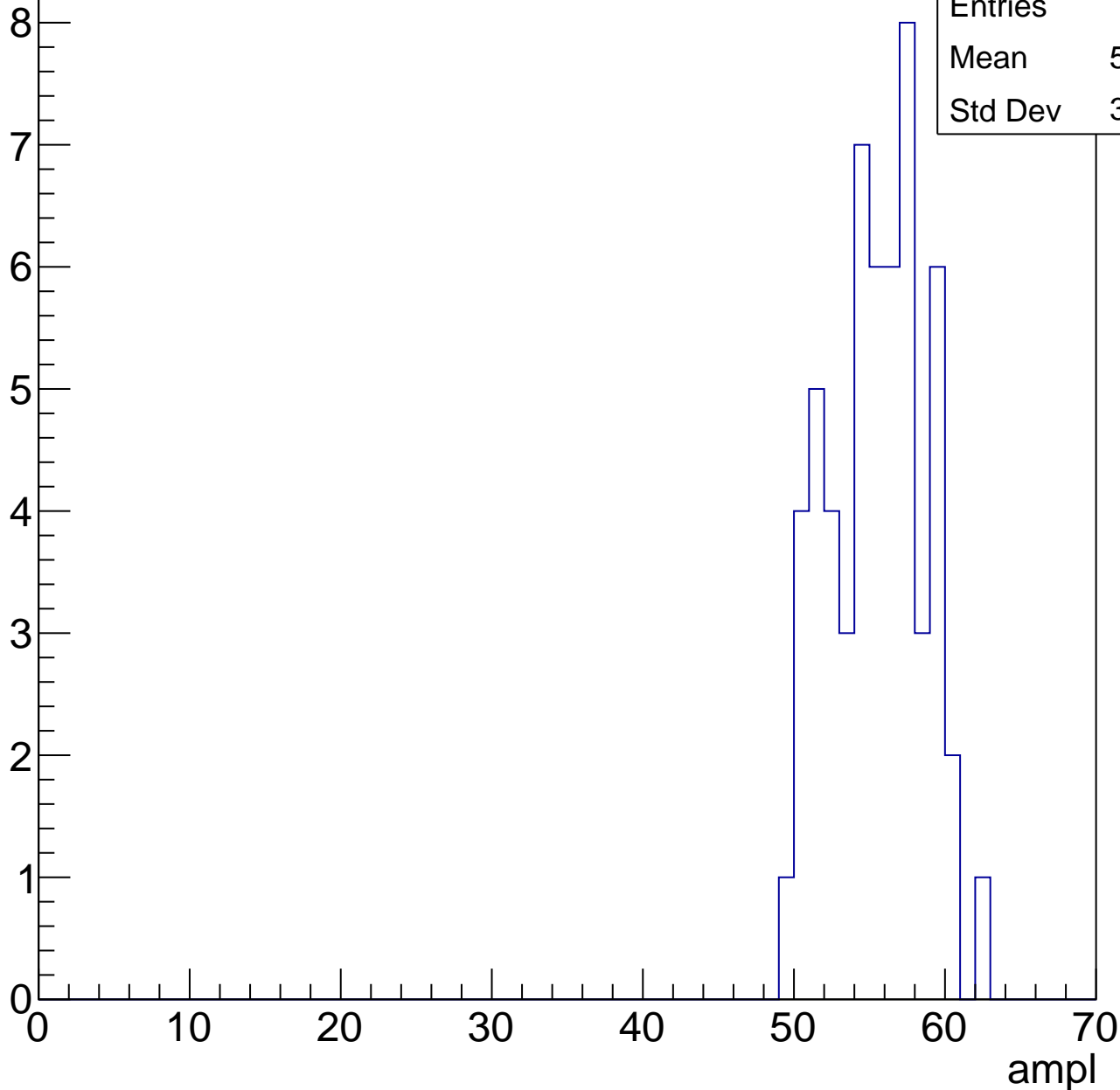


B1L003S, U19-ch118, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	55.02
Std Dev	3.079

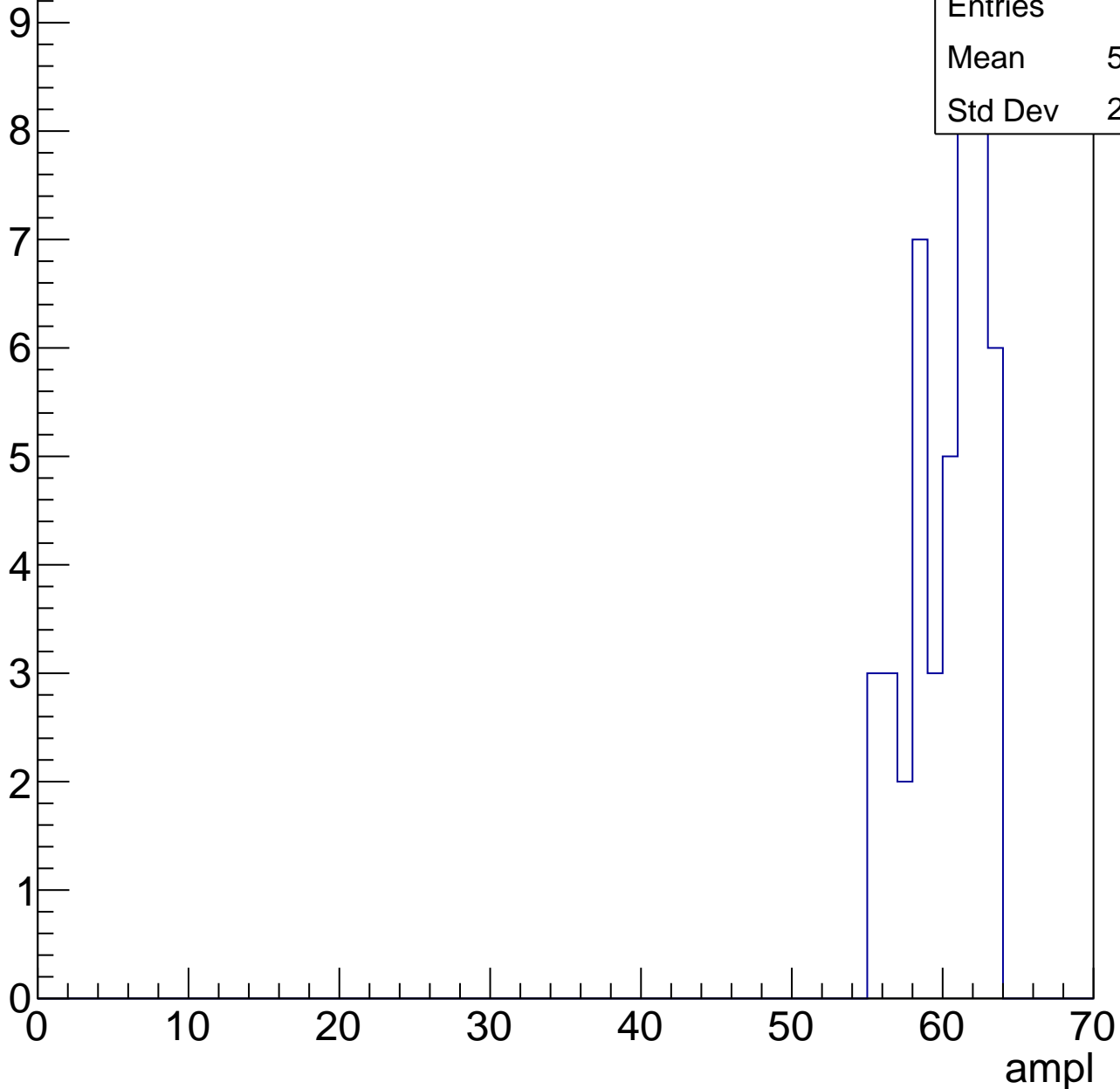


B1L003S, U19-ch118, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

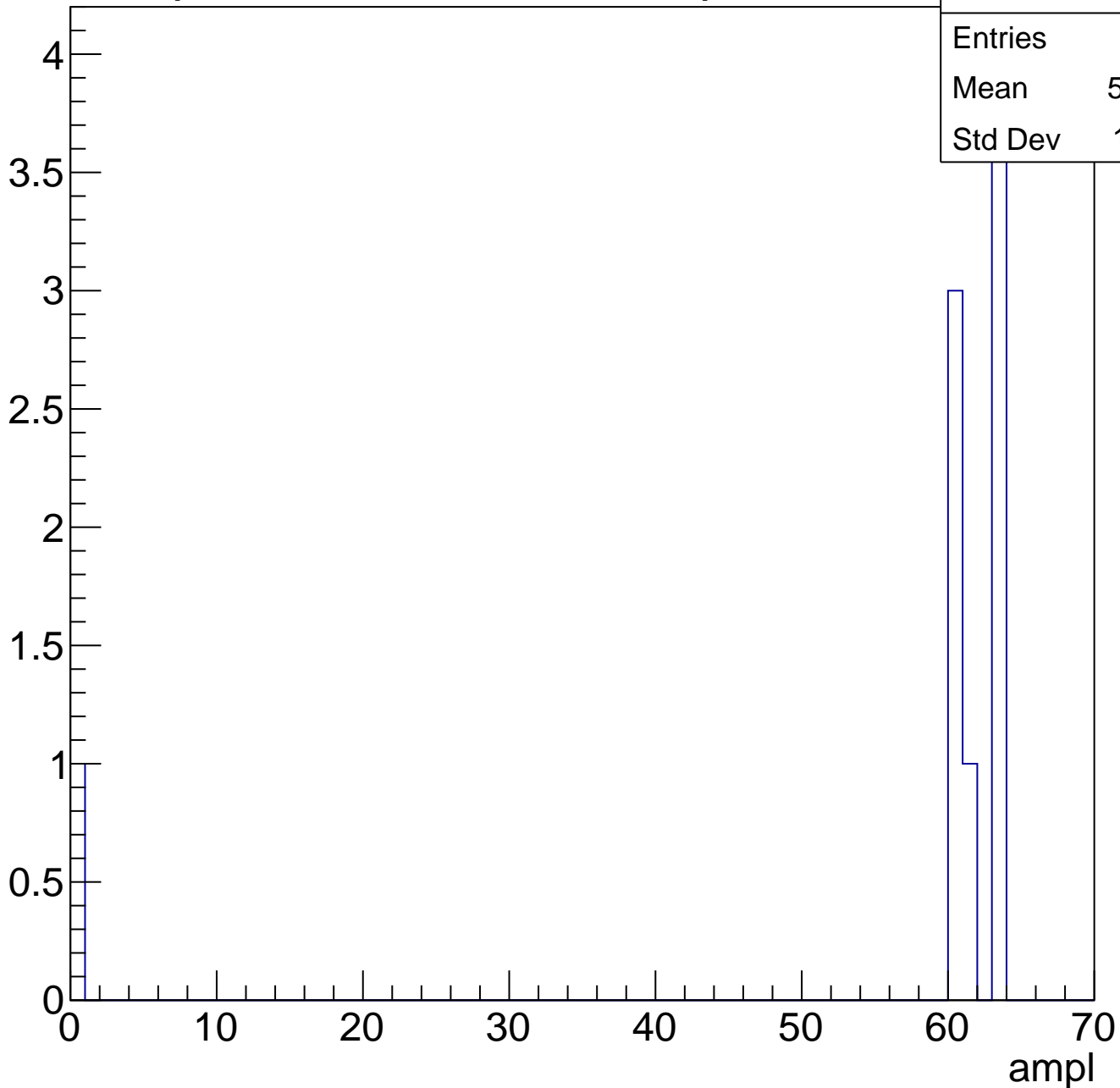
Entries	47
Mean	59.89
Std Dev	2.399



B1L003S, U19-ch118, adc6

calib_packv5_042523_0143.root, FC#13, port D2

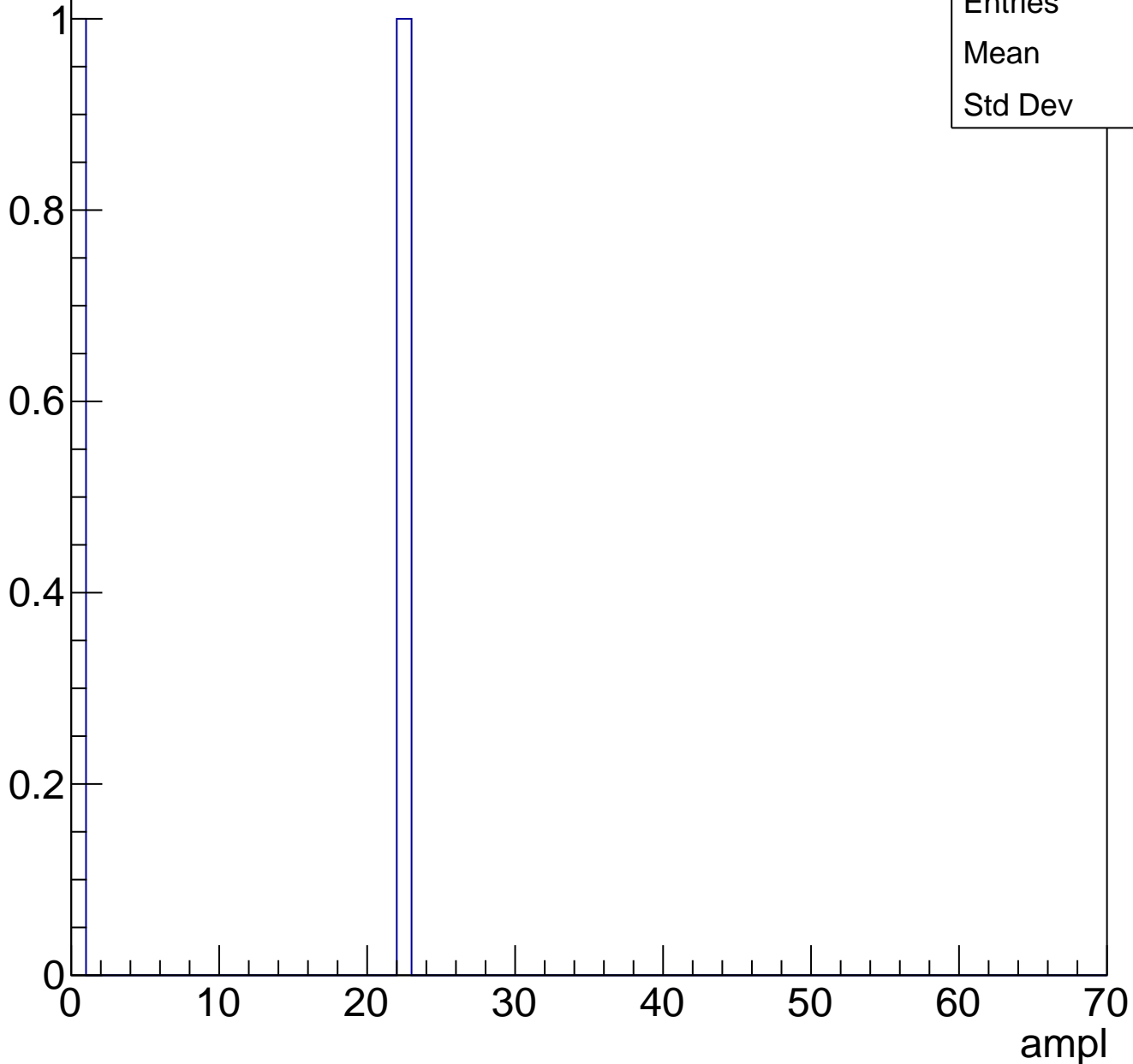
Entry



B1L003S, U19-ch118, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch119, adc0

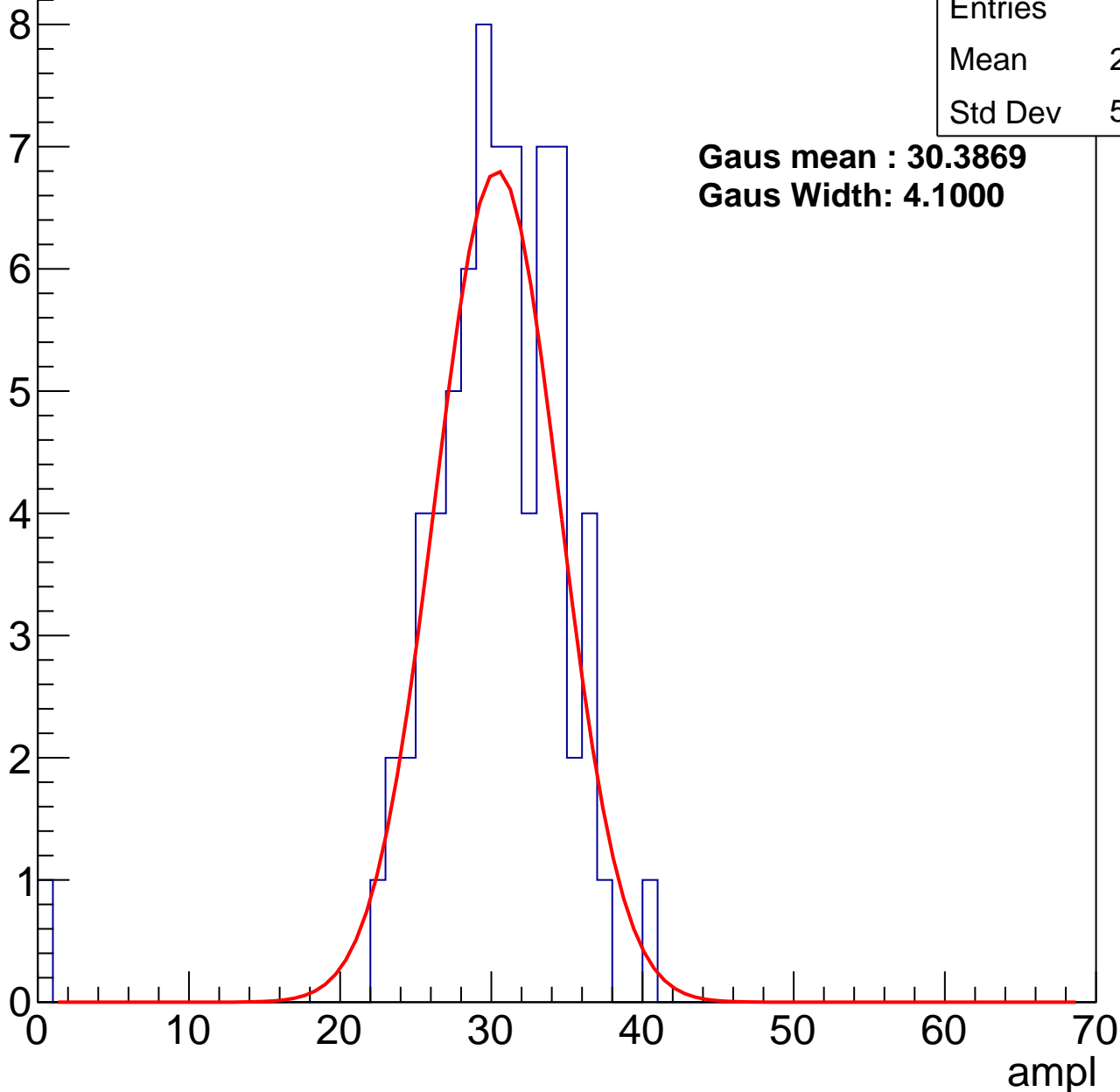
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.73
Std Dev	5.116

Gaus mean : 30.3869

Gaus Width: 4.1000



B1L003S, U19-ch119, adc1

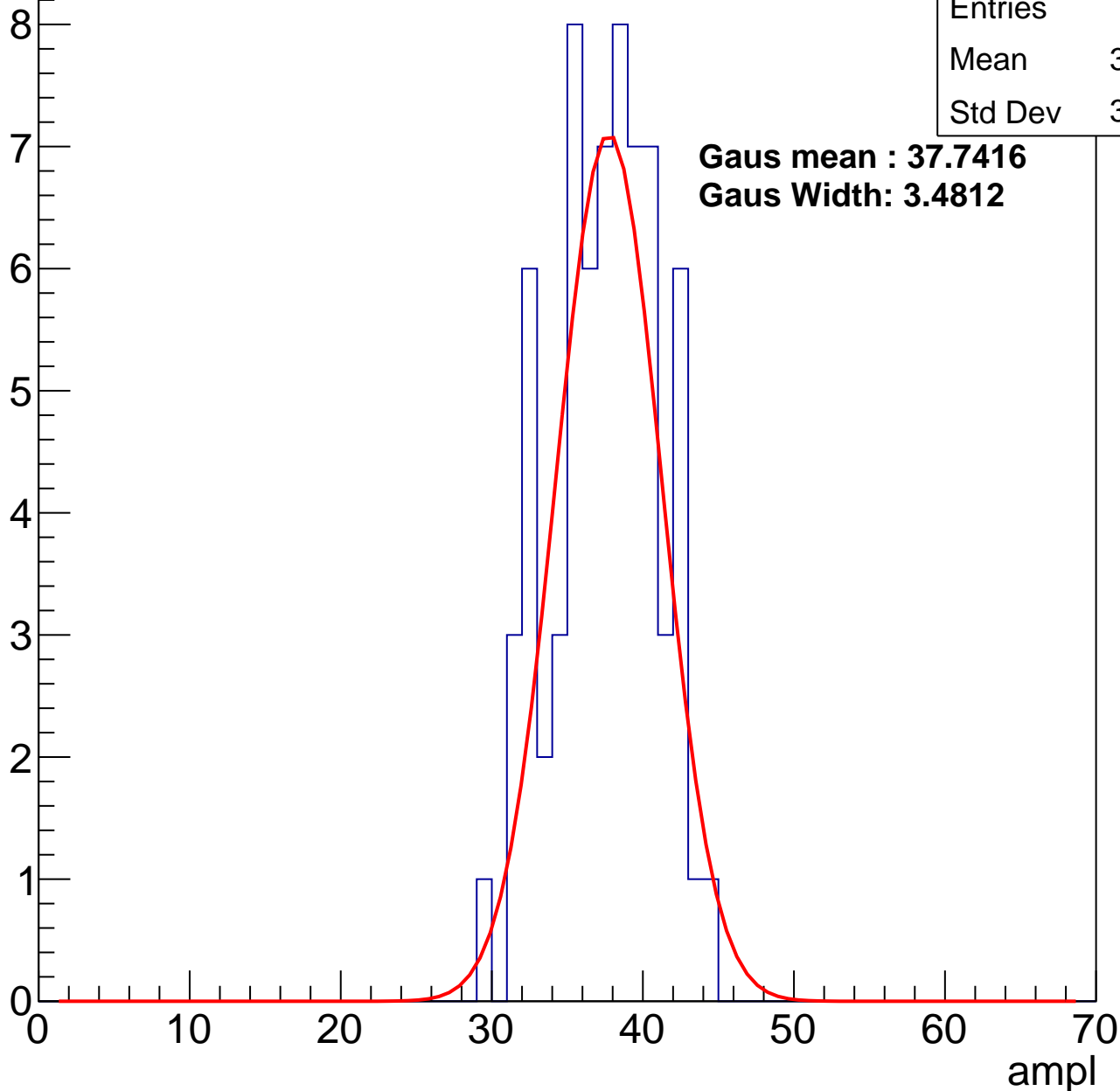
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	37.04
Std Dev	3.407

Gaus mean : 37.7416

Gaus Width: 3.4812

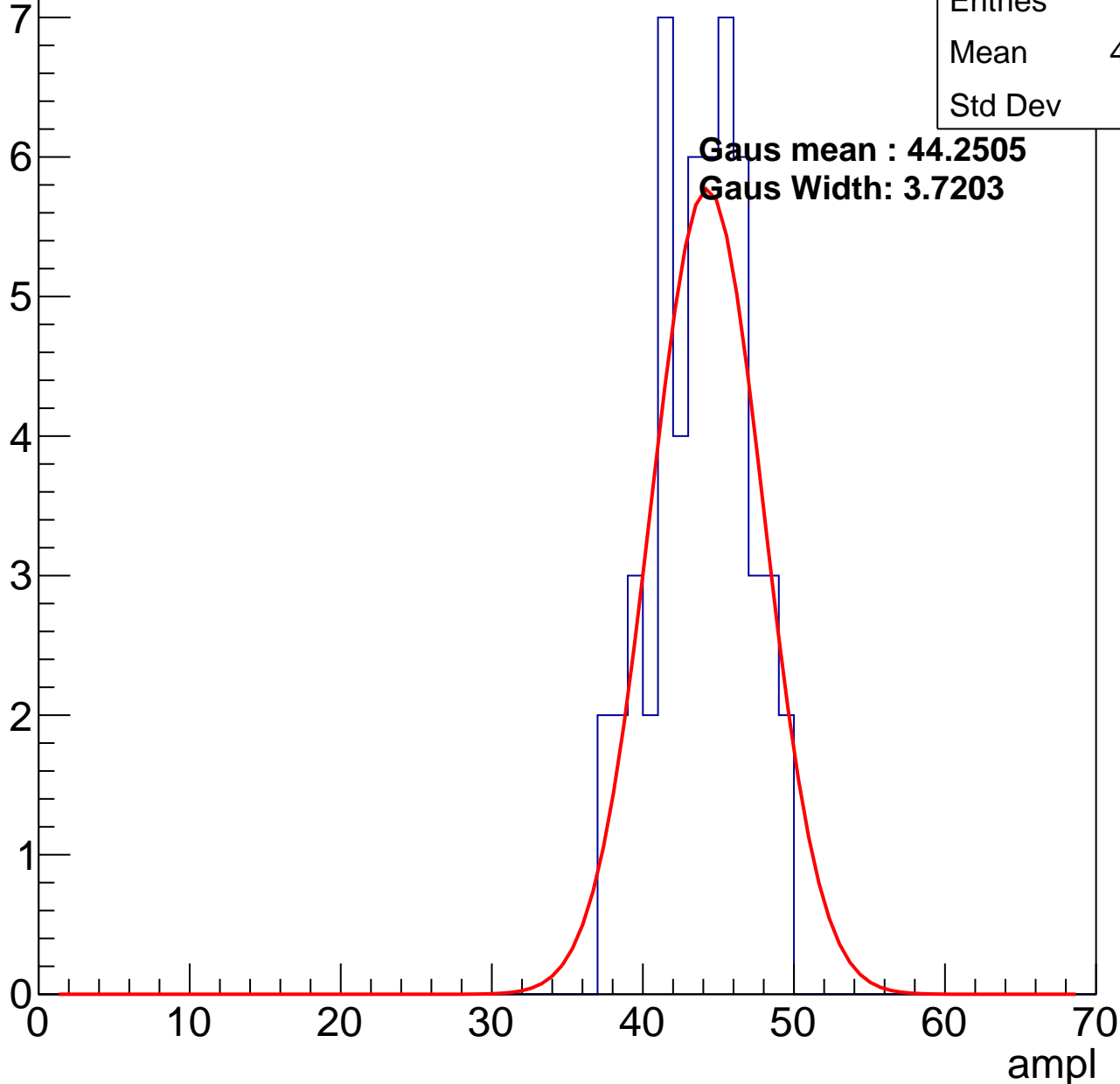


B1L003S, U19-ch119, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.36
Std Dev	3.06

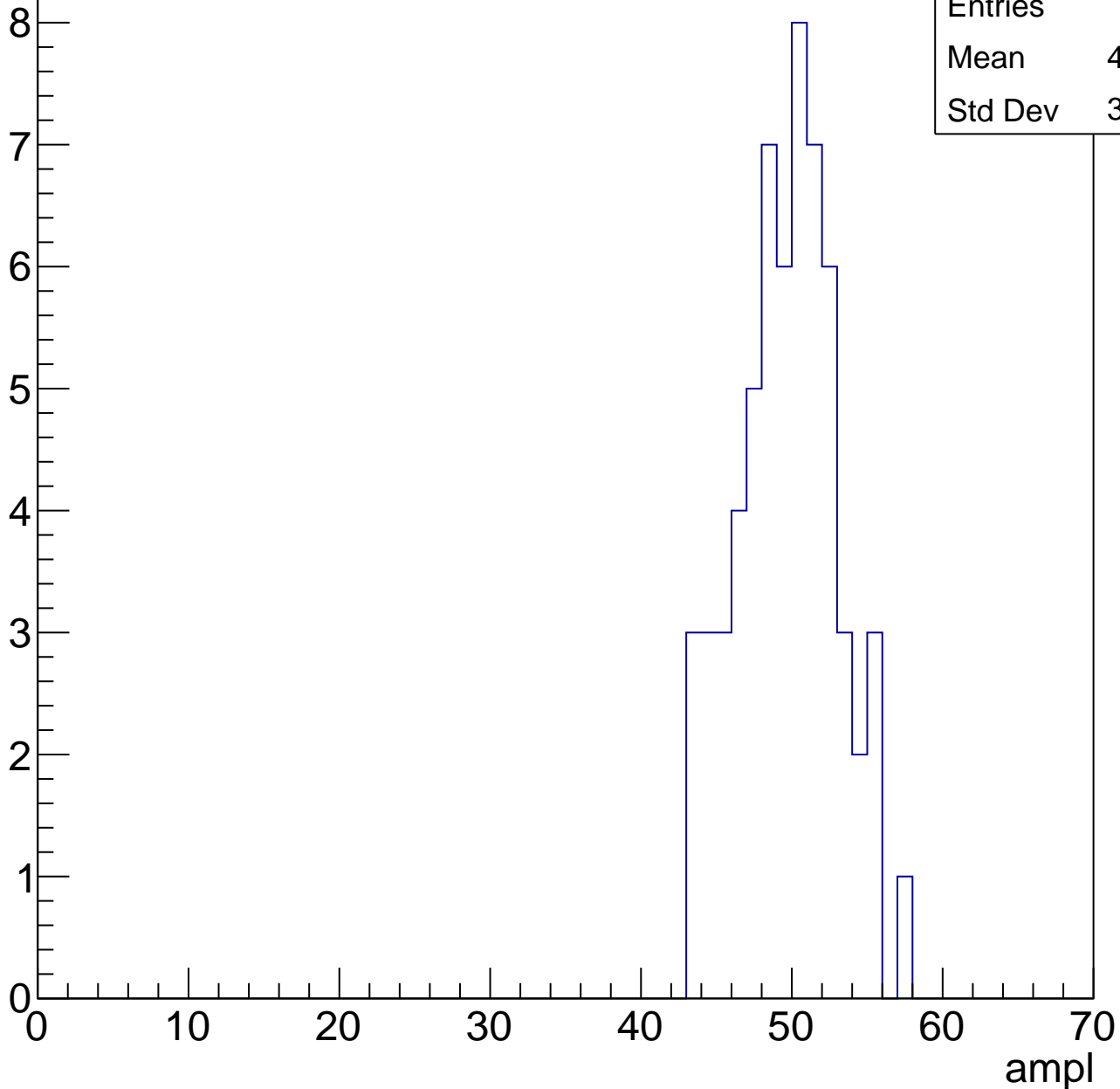


B1L003S, U19-ch119, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

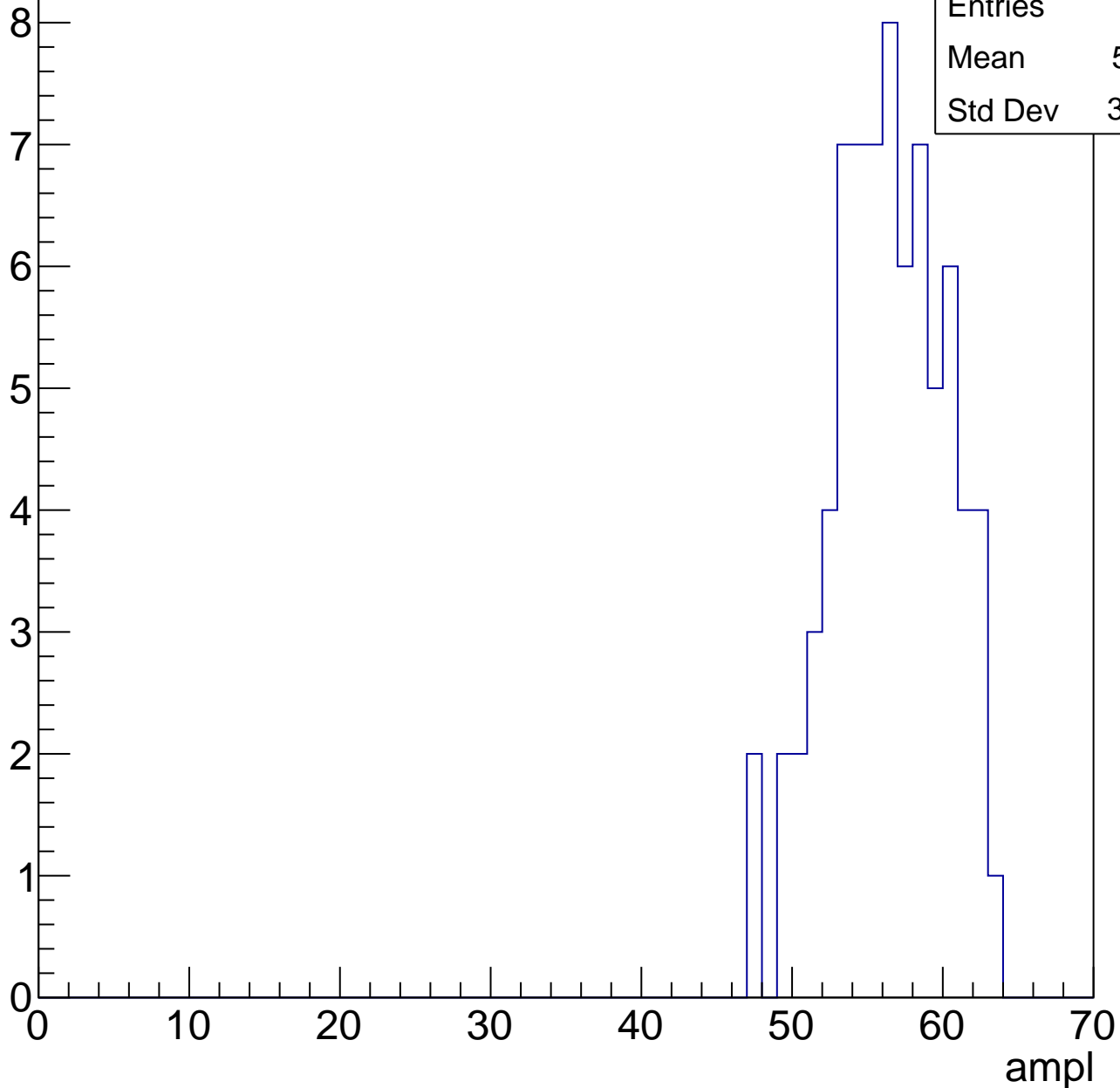
Entries	61
Mean	49.23
Std Dev	3.266



B1L003S, U19-ch119, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

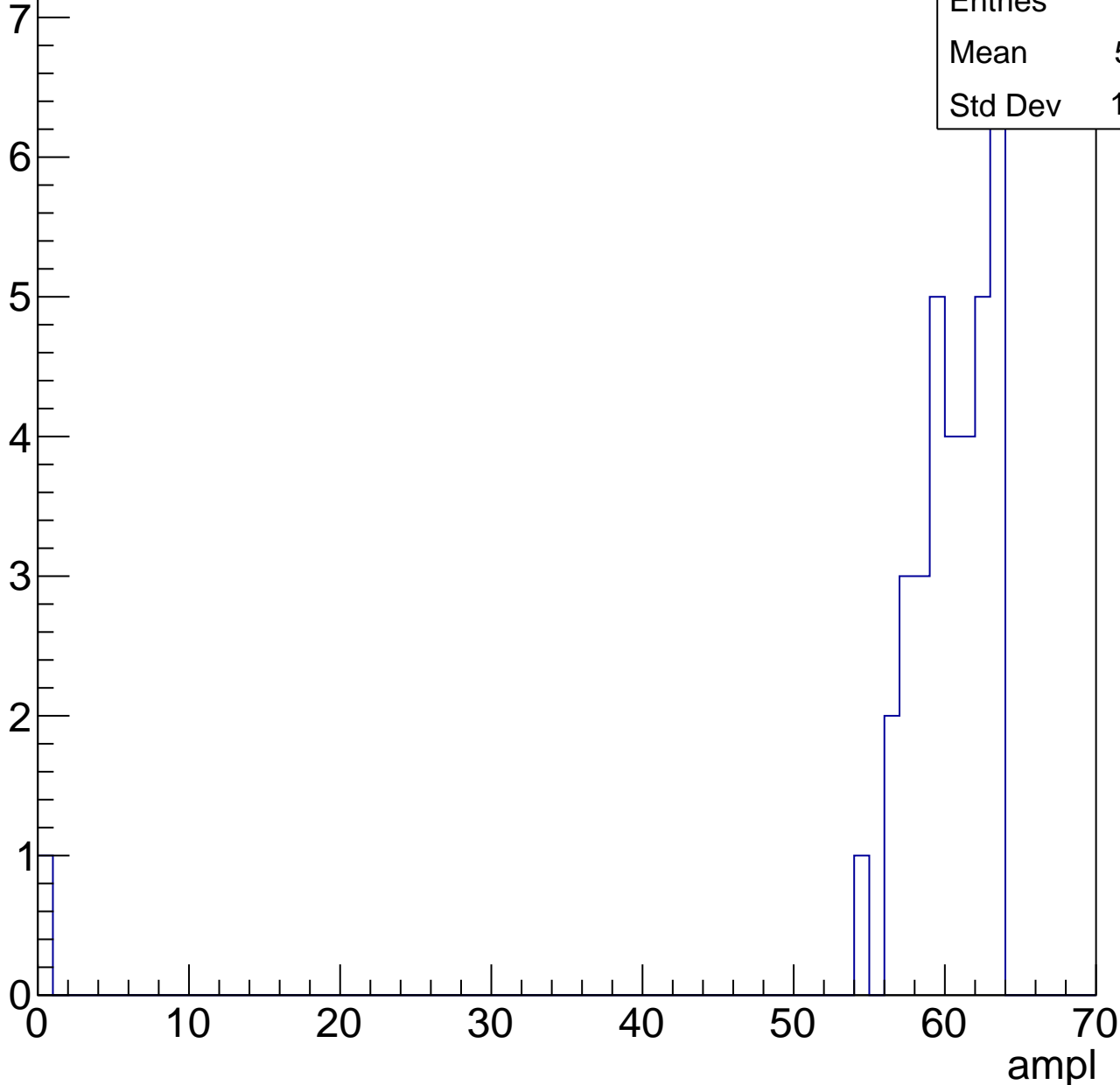


B1L003S, U19-ch119, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

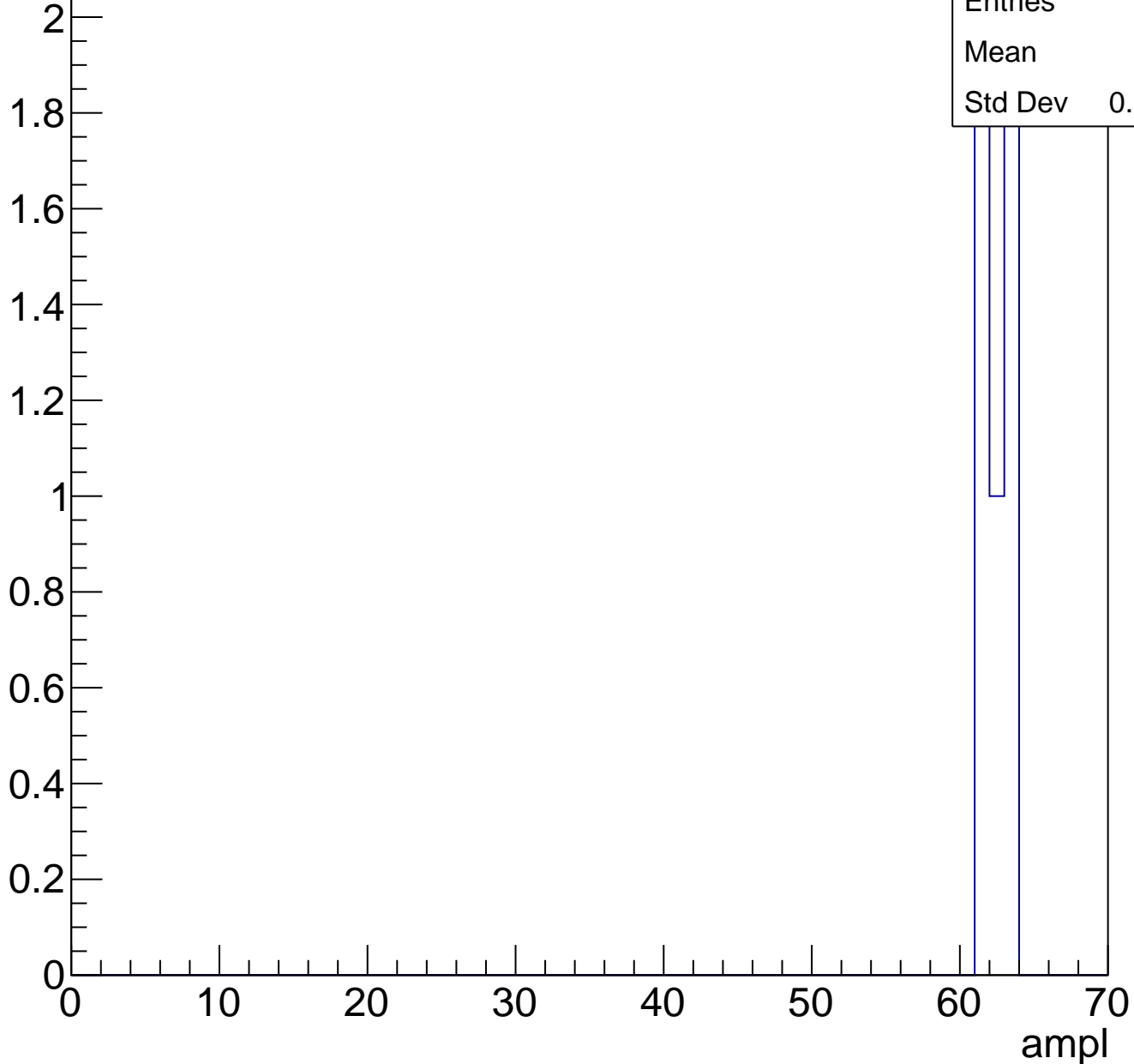
Entries	35
Mean	58.31
Std Dev	10.28



B1L003S, U19-ch119, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch119, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U19-ch120, adc0

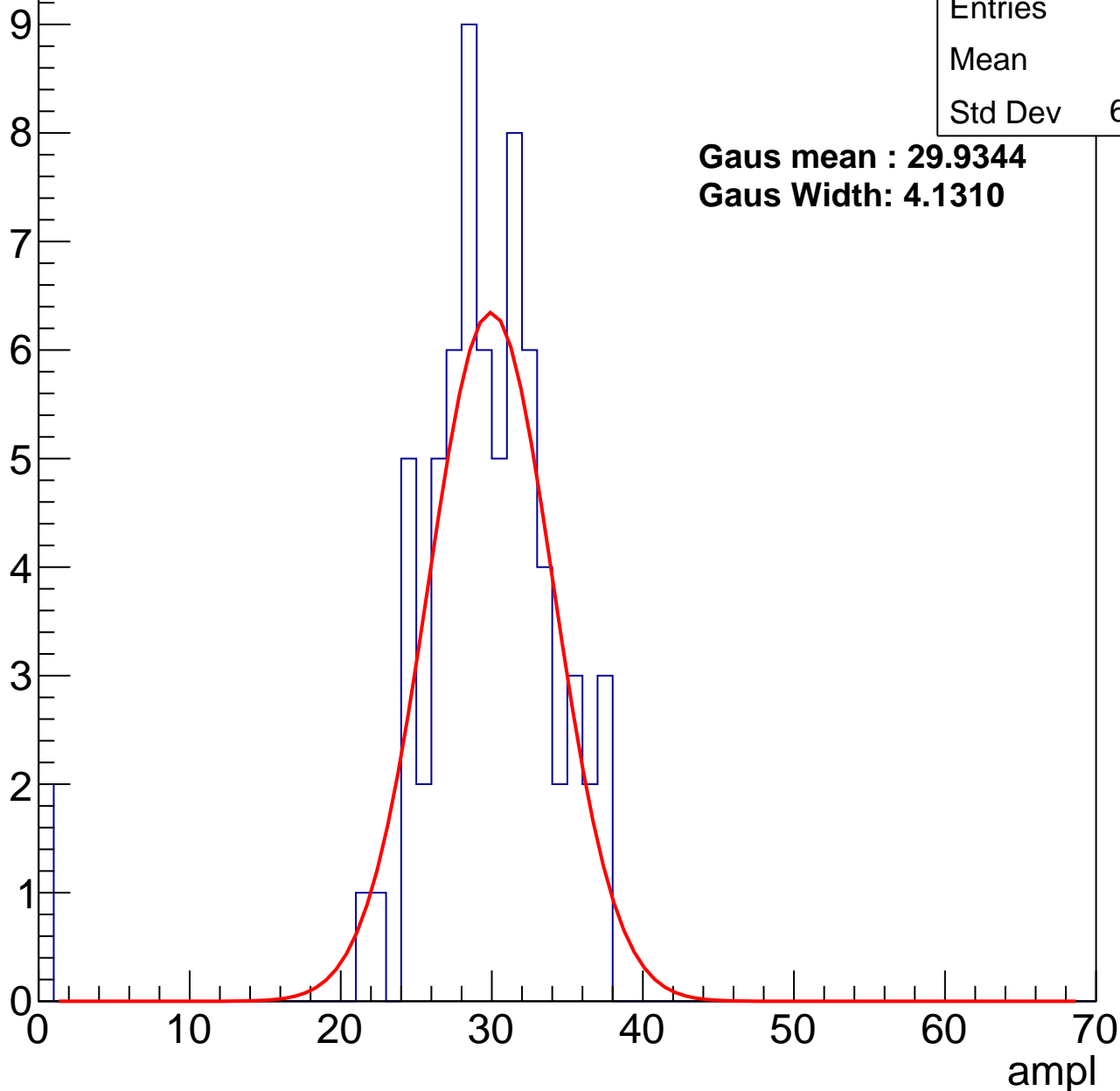
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.7
Std Dev	6.126

Gaus mean : 29.9344

Gaus Width: 4.1310



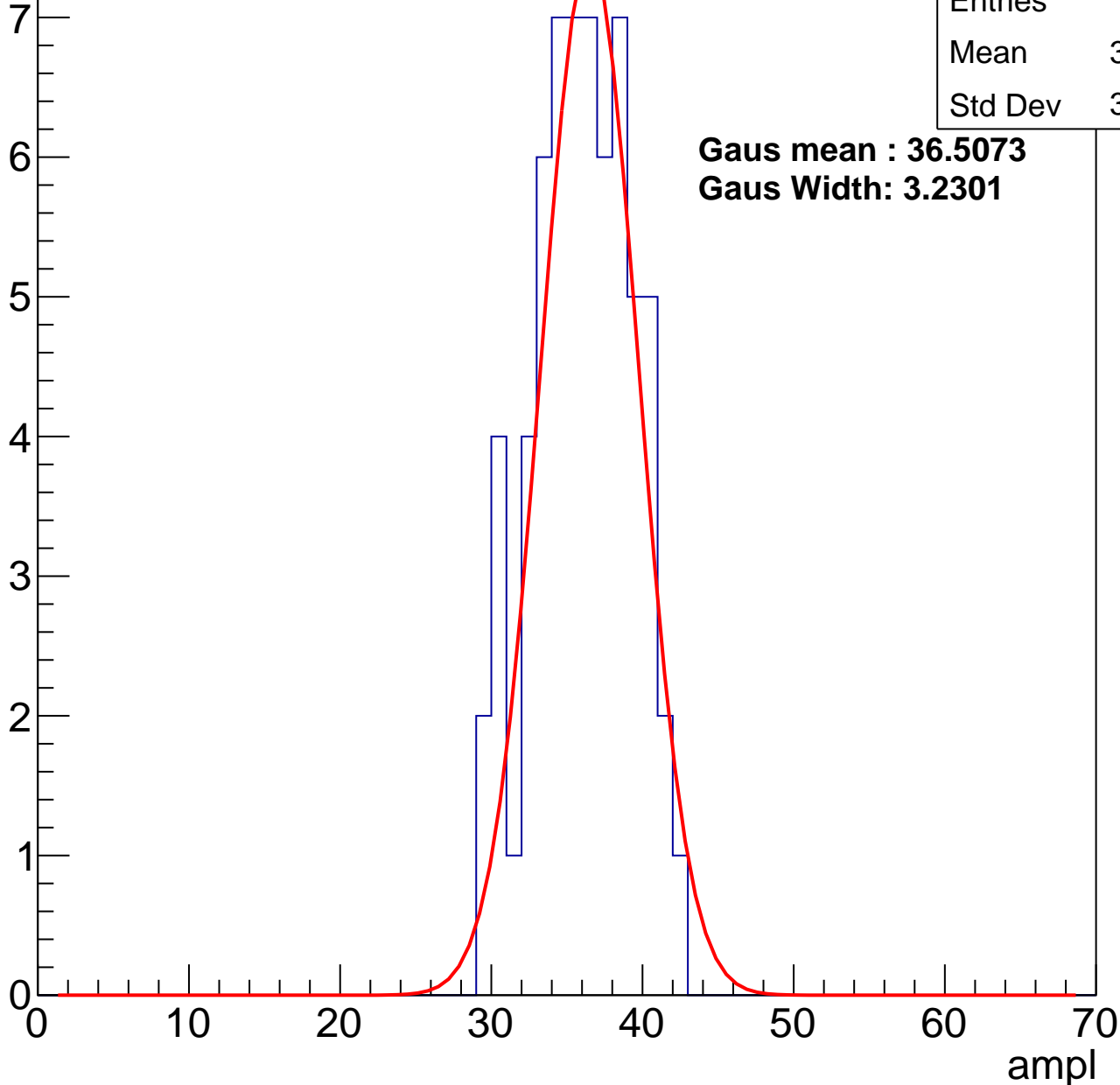
B1L003S, U19-ch120, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	35.58
Std Dev	3.196

Gaus mean : 36.5073
Gaus Width: 3.2301



B1L003S, U19-ch120, adc2

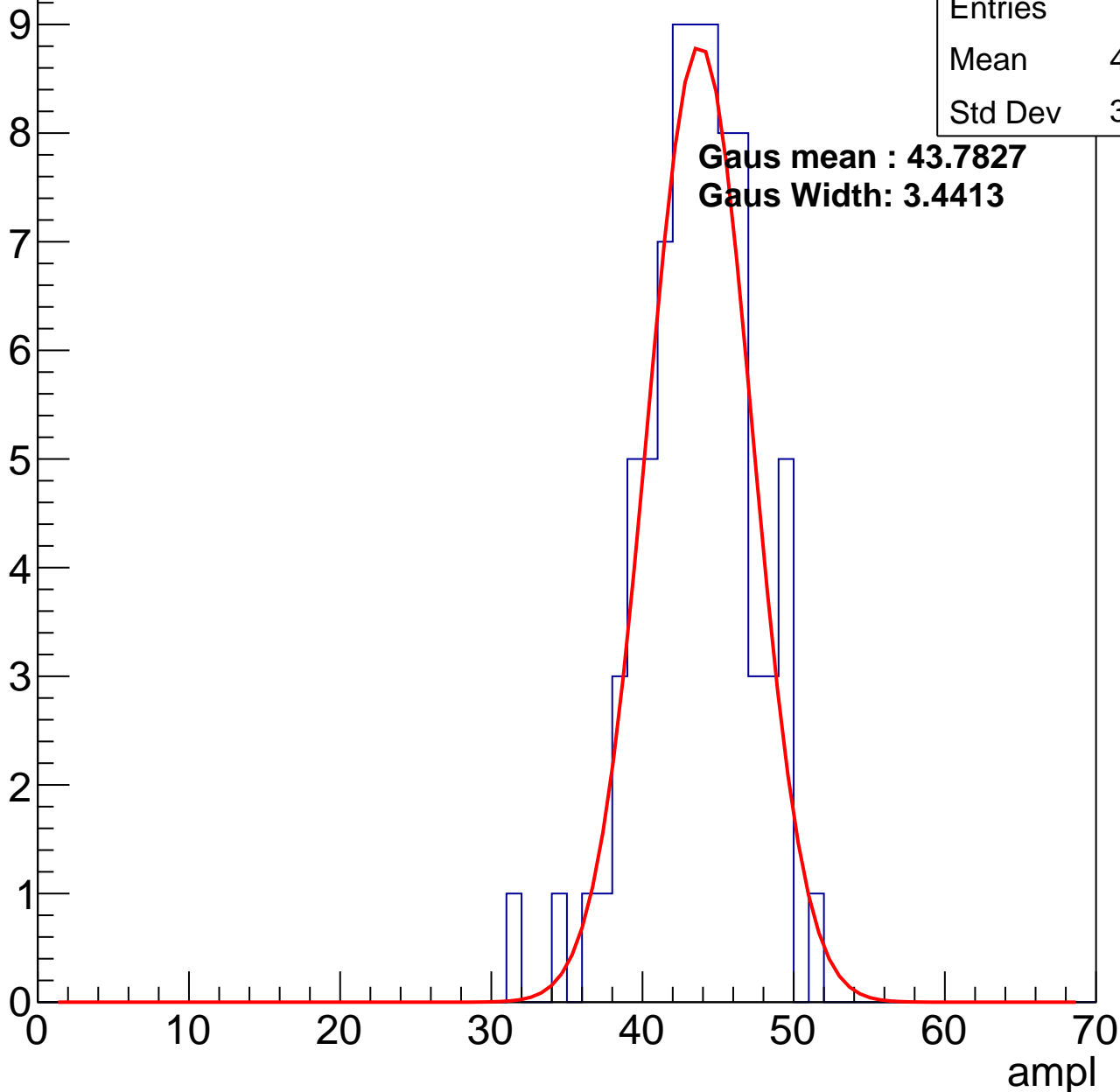
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	43.09
Std Dev	3.604

Gaus mean : 43.7827

Gaus Width: 3.4413

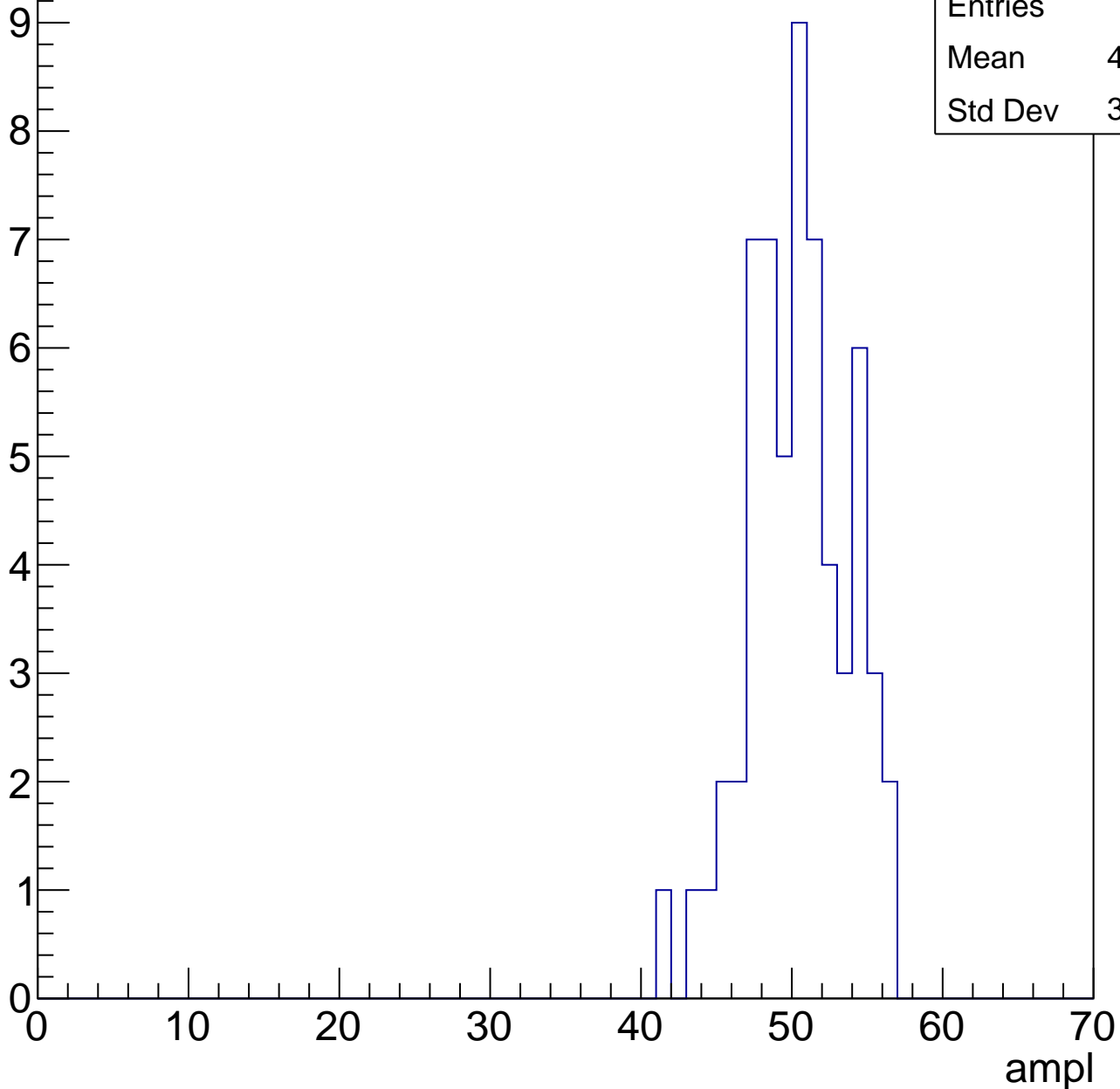


B1L003S, U19-ch120, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	49.92
Std Dev	3.257

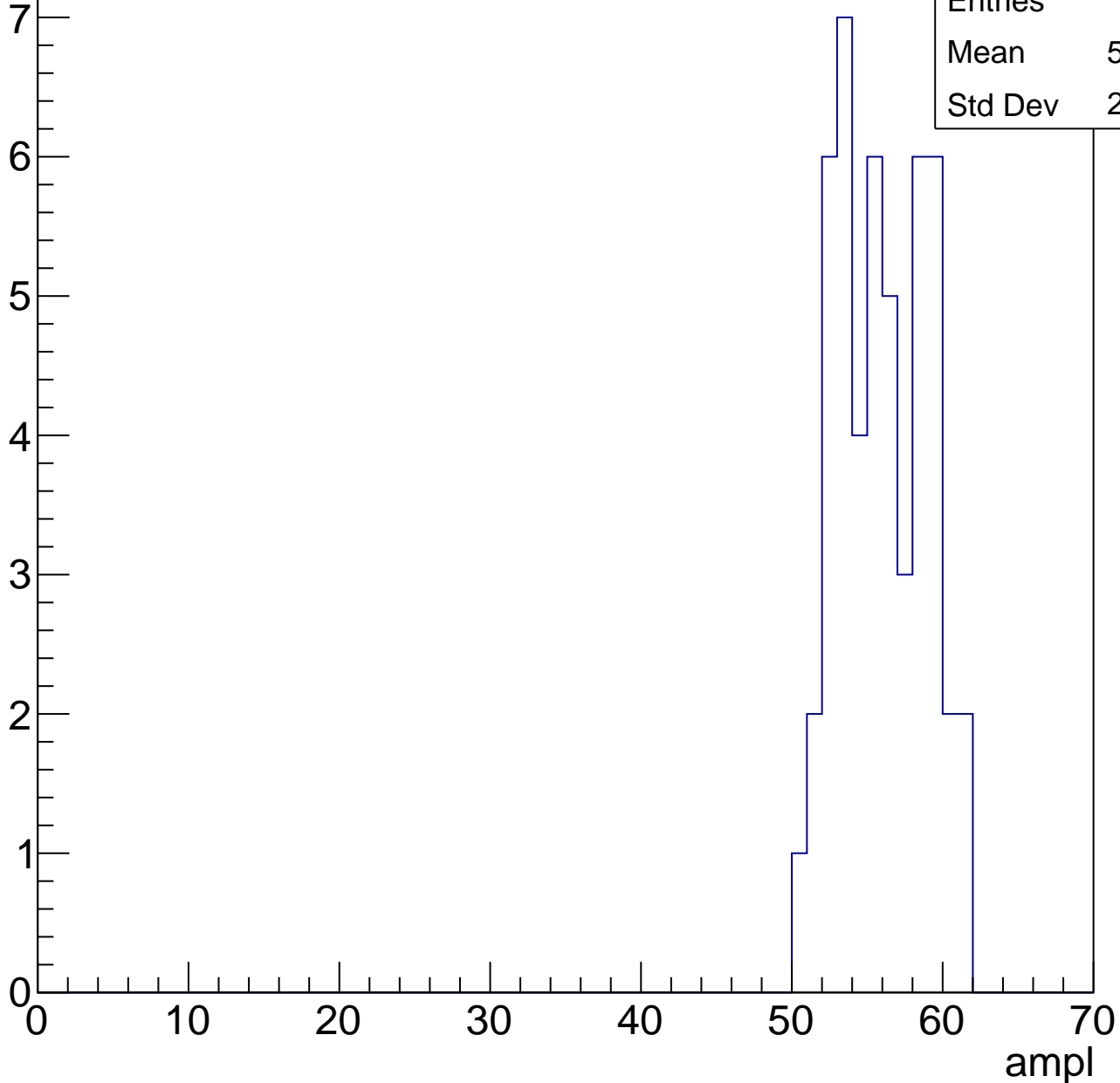


B1L003S, U19-ch120, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	55.52
Std Dev	2.893

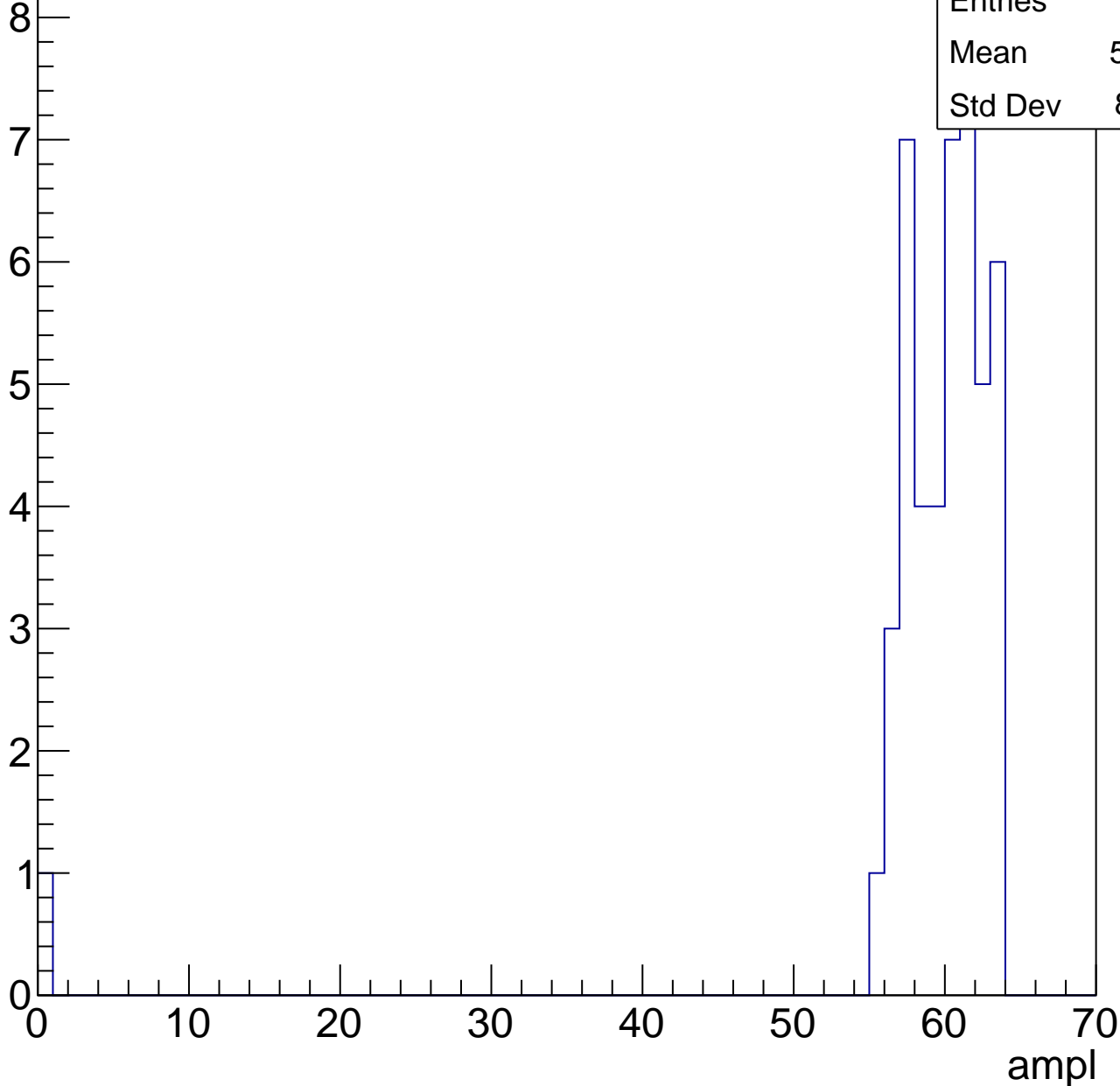


B1L003S, U19-ch120, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.39
Std Dev	8.991

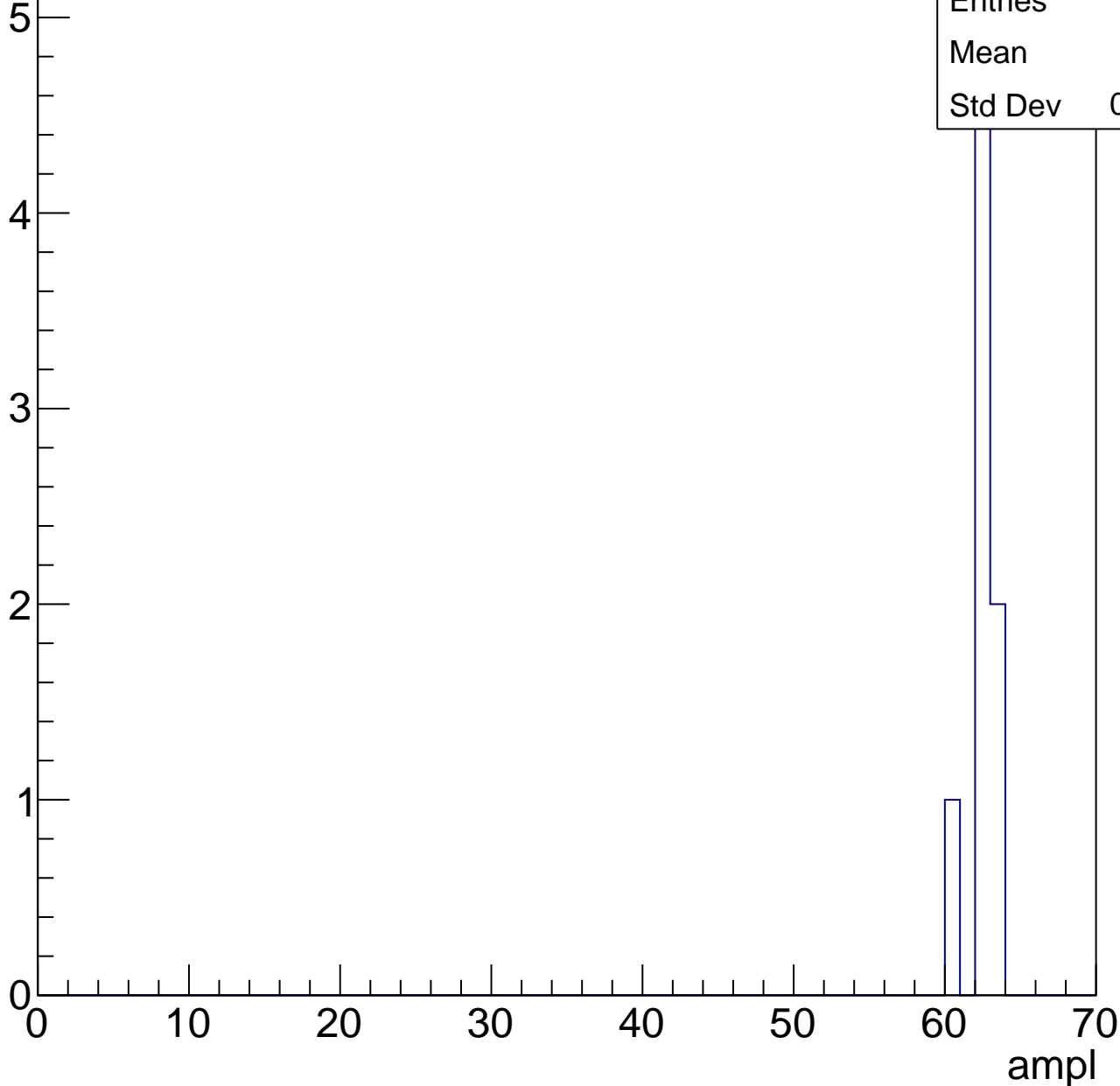


B1L003S, U19-ch120, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	8
Mean	62
Std Dev	0.866



B1L003S, U19-ch120, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch121, adc0

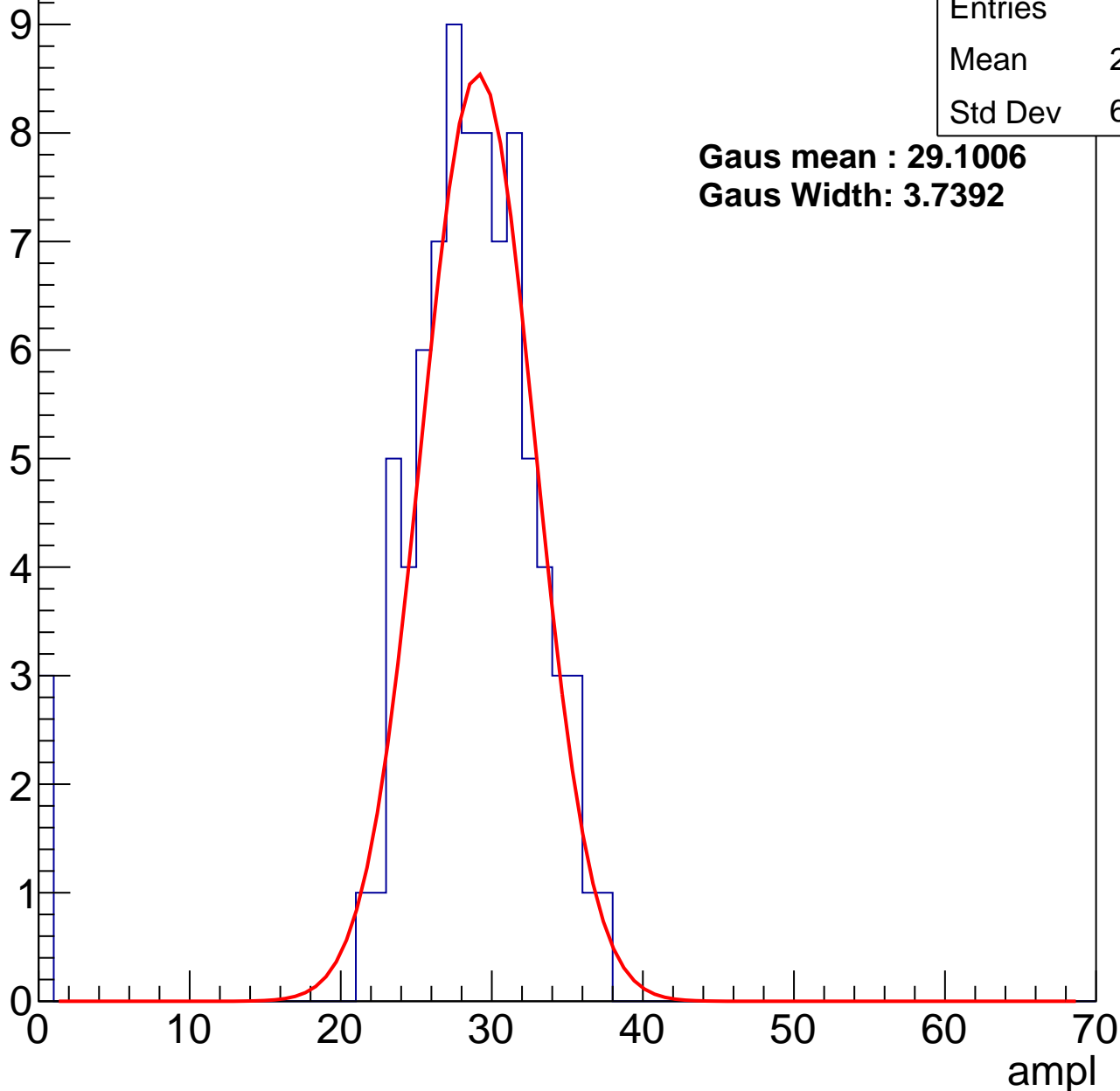
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	27.56
Std Dev	6.352

Gaus mean : 29.1006

Gaus Width: 3.7392



B1L003S, U19-ch121, adc1

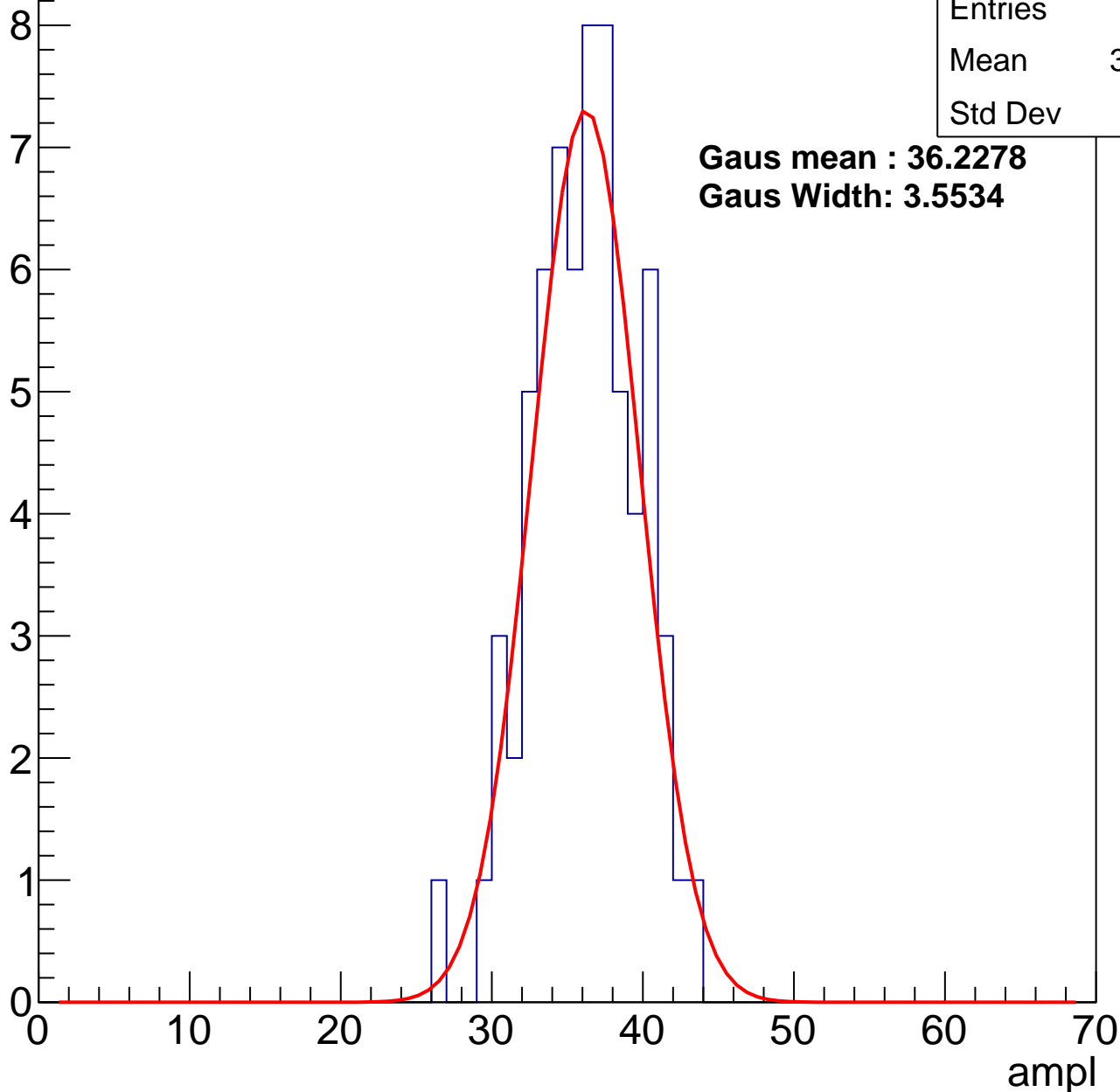
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	35.69
Std Dev	3.43

Gaus mean : 36.2278

Gaus Width: 3.5534



B1L003S, U19-ch121, adc2

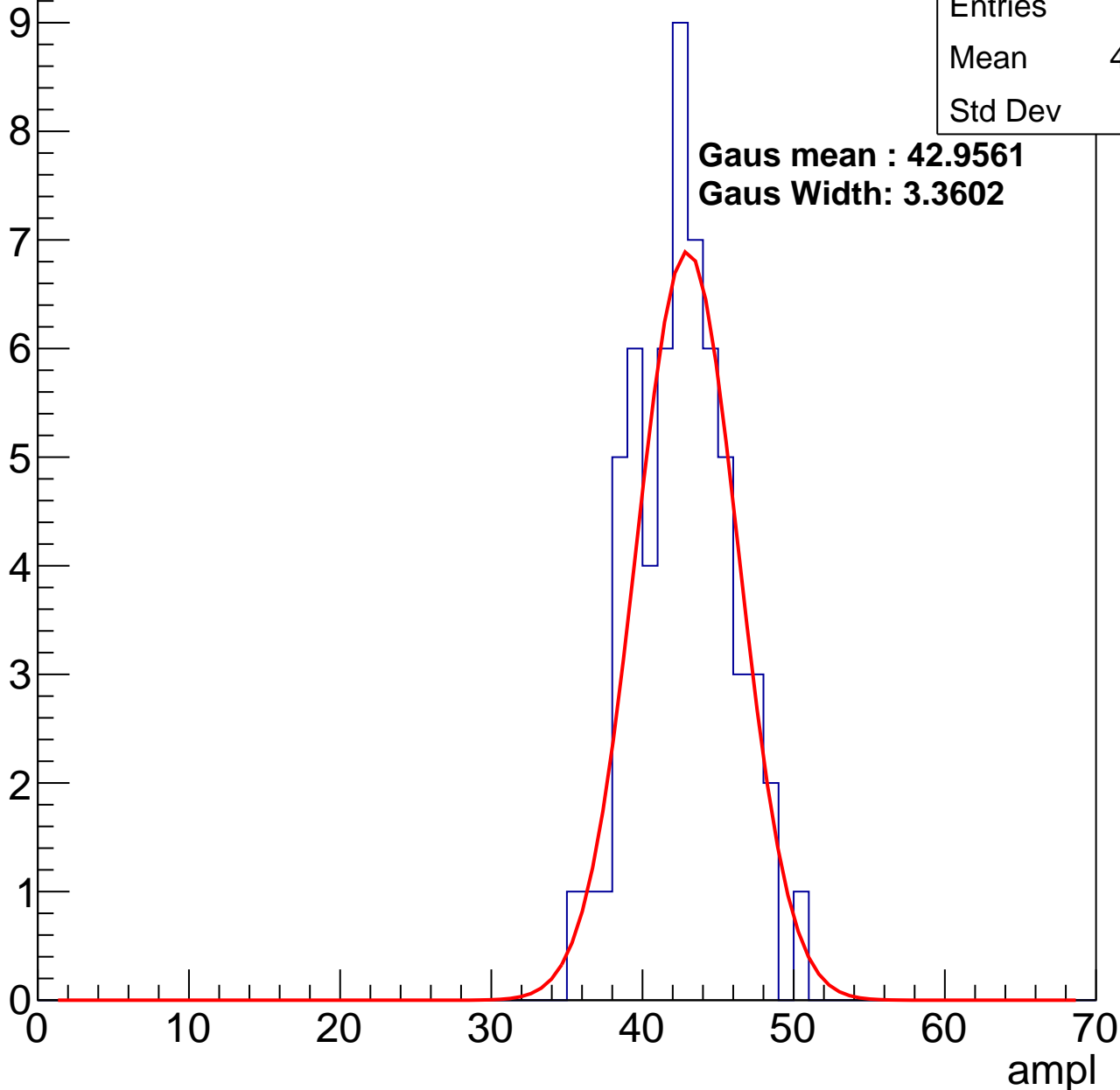
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.18
Std Dev	3.16

Gaus mean : 42.9561

Gaus Width: 3.3602

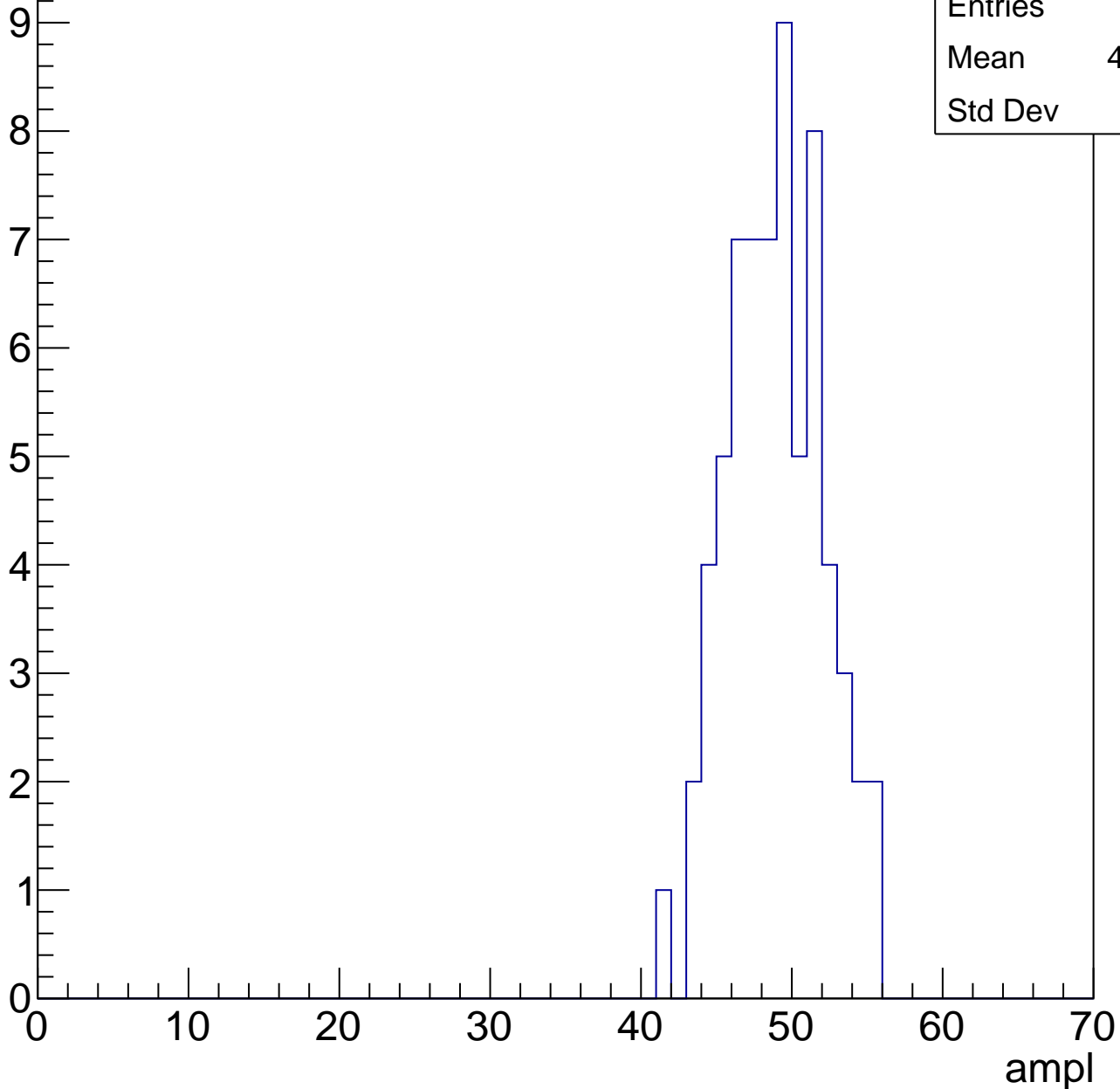


B1L003S, U19-ch121, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	48.47
Std Dev	3.11

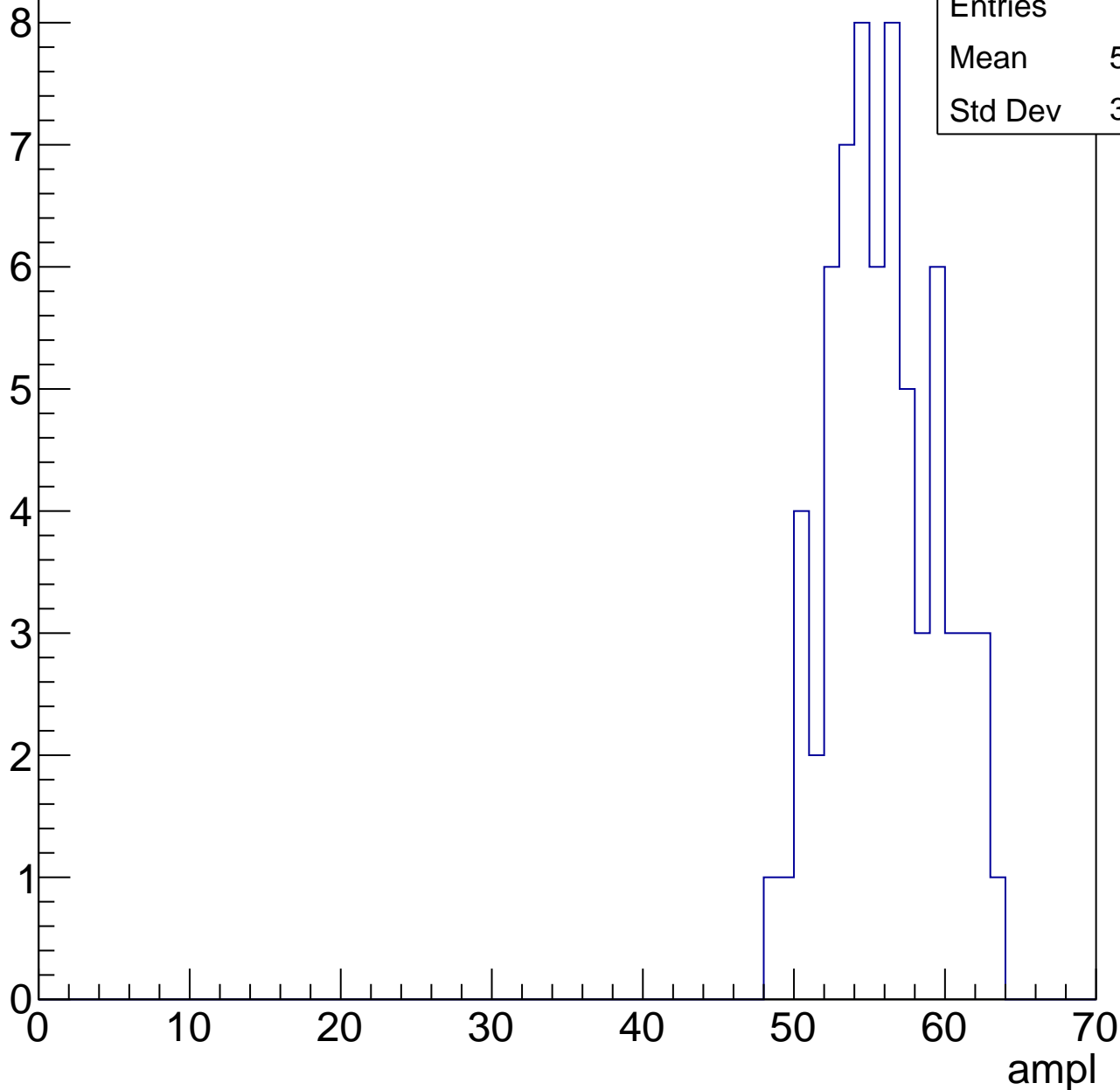


B1L003S, U19-ch121, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.48
Std Dev	3.534

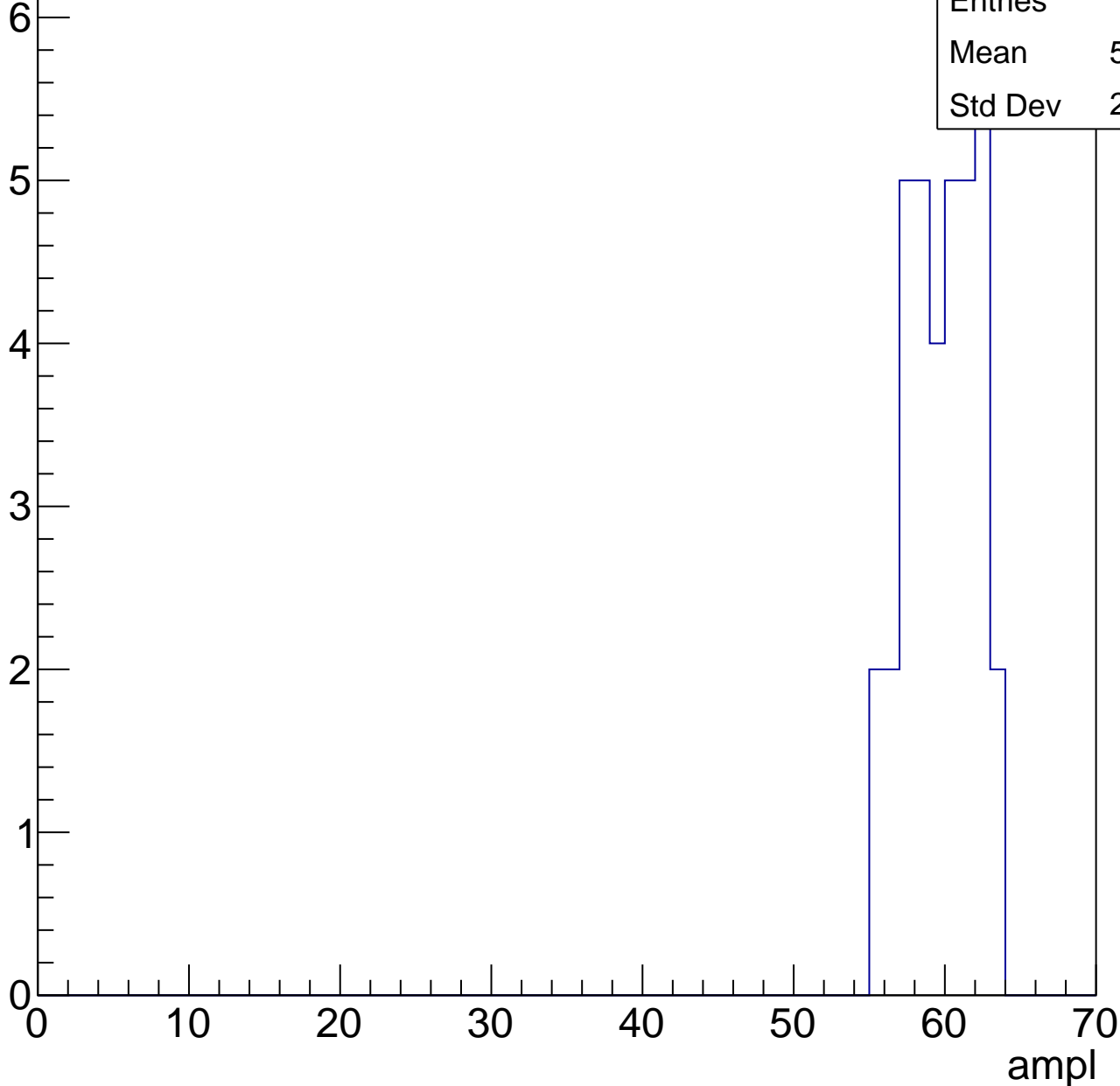


B1L003S, U19-ch121, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.33
Std Dev	2.248

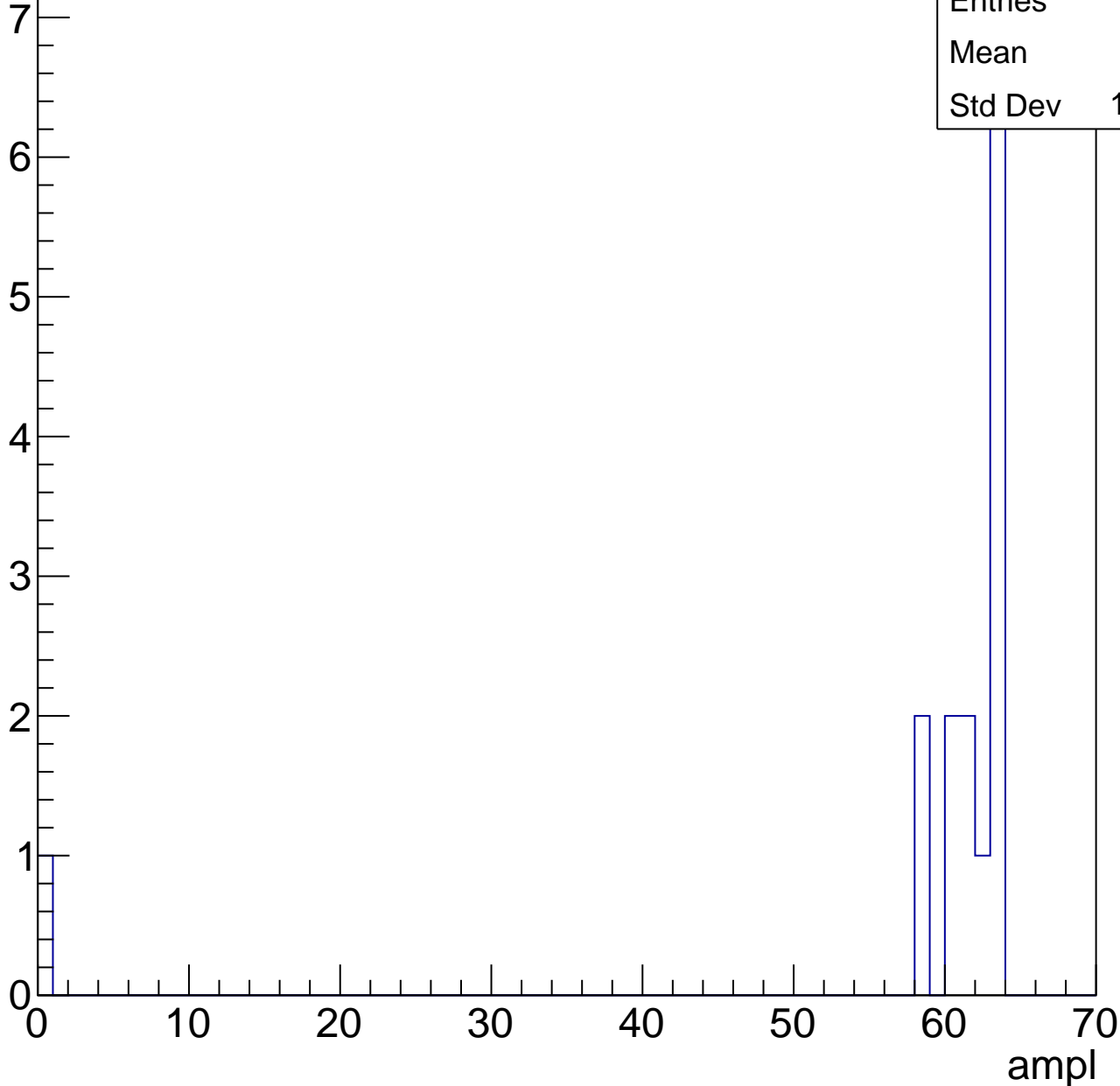


B1L003S, U19-ch121, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	57.4
Std Dev	15.44



B1L003S, U19-ch121, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch122, adc0

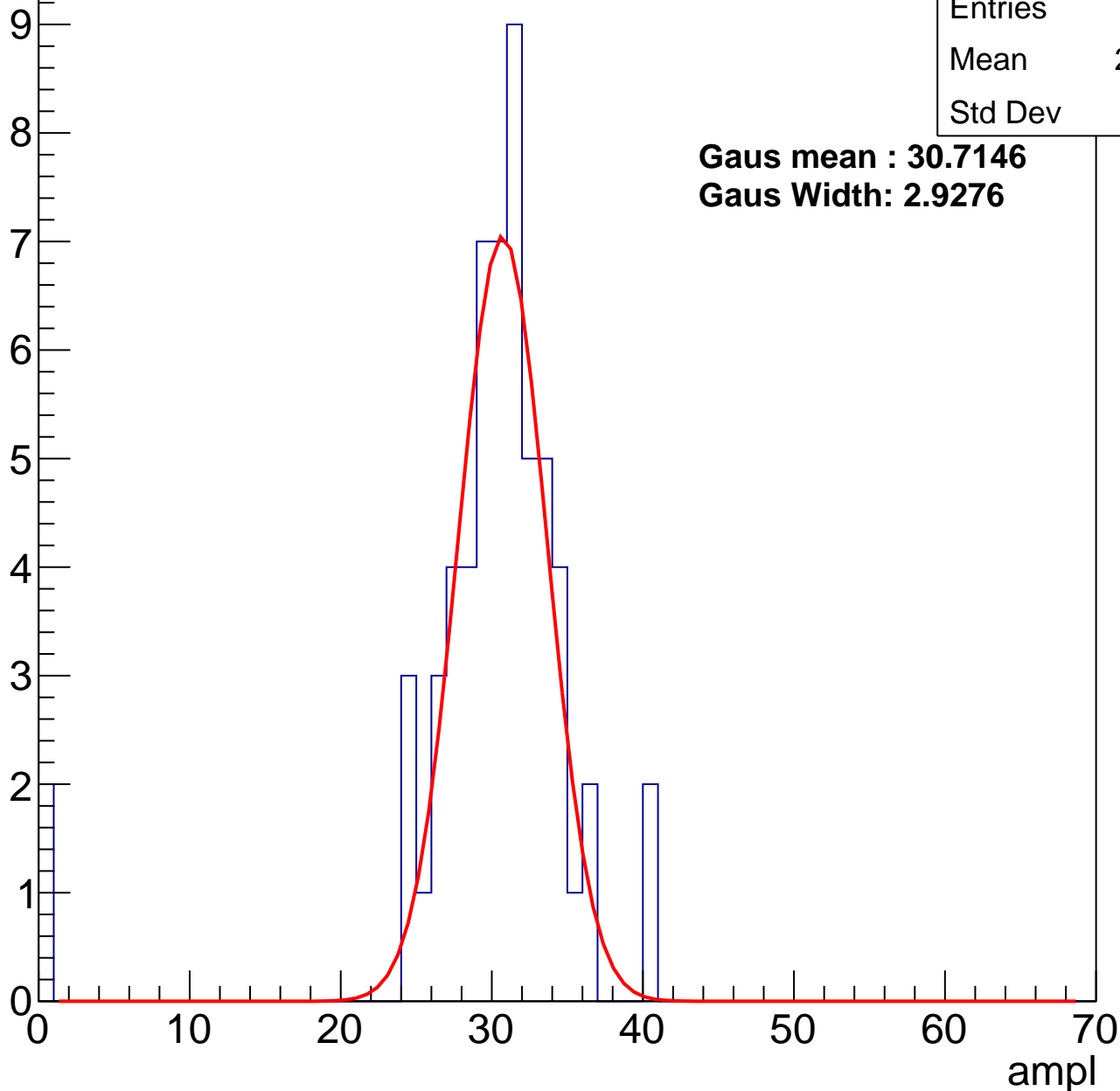
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.41
Std Dev	6.45

Gaus mean : 30.7146

Gaus Width: 2.9276



B1L003S, U19-ch122, adc1

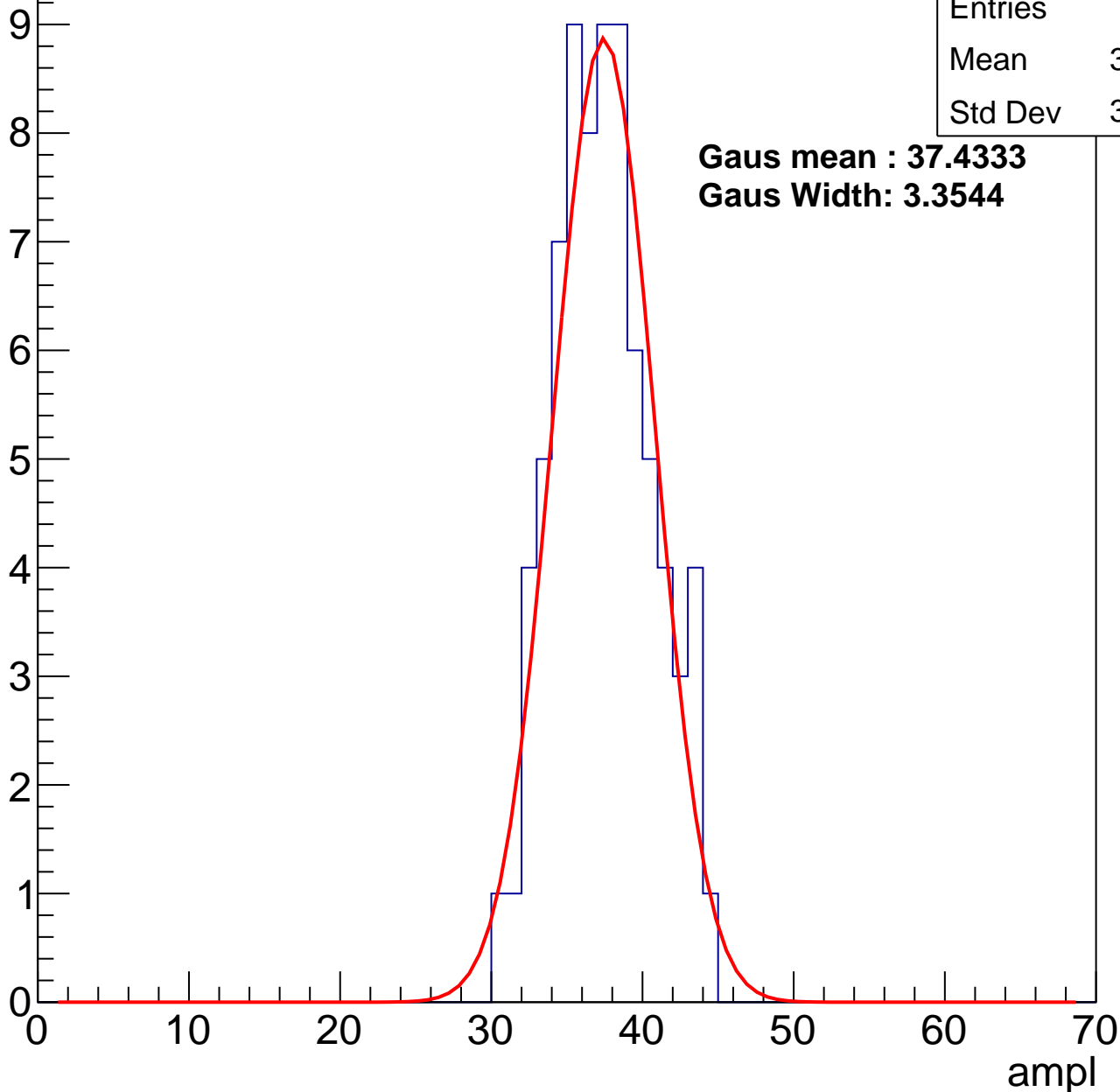
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	36.97
Std Dev	3.208

Gaus mean : 37.4333

Gaus Width: 3.3544



B1L003S, U19-ch122, adc2

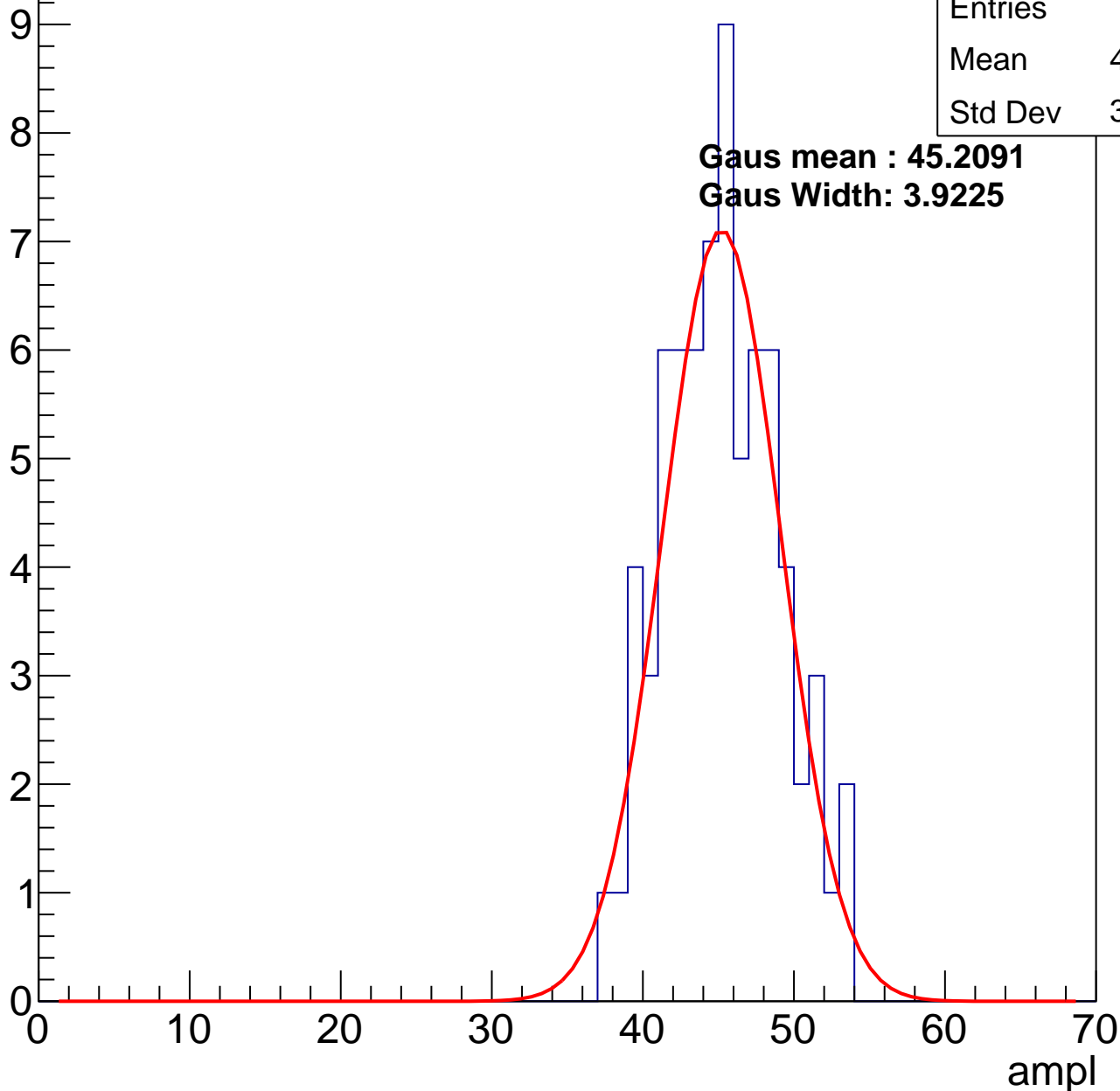
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	44.82
Std Dev	3.713

Gaus mean : 45.2091

Gaus Width: 3.9225

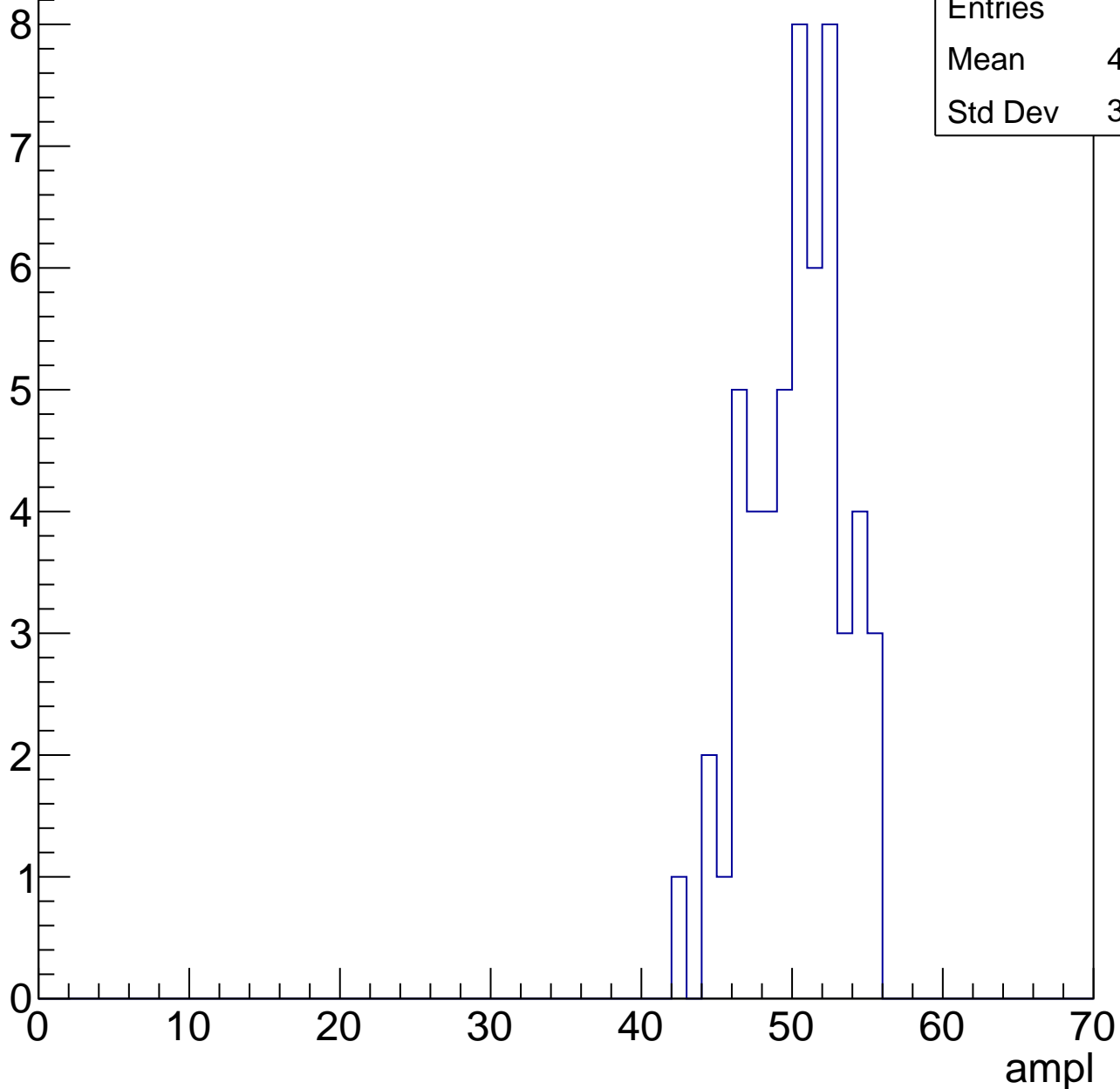


B1L003S, U19-ch122, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	49.85
Std Dev	3.045

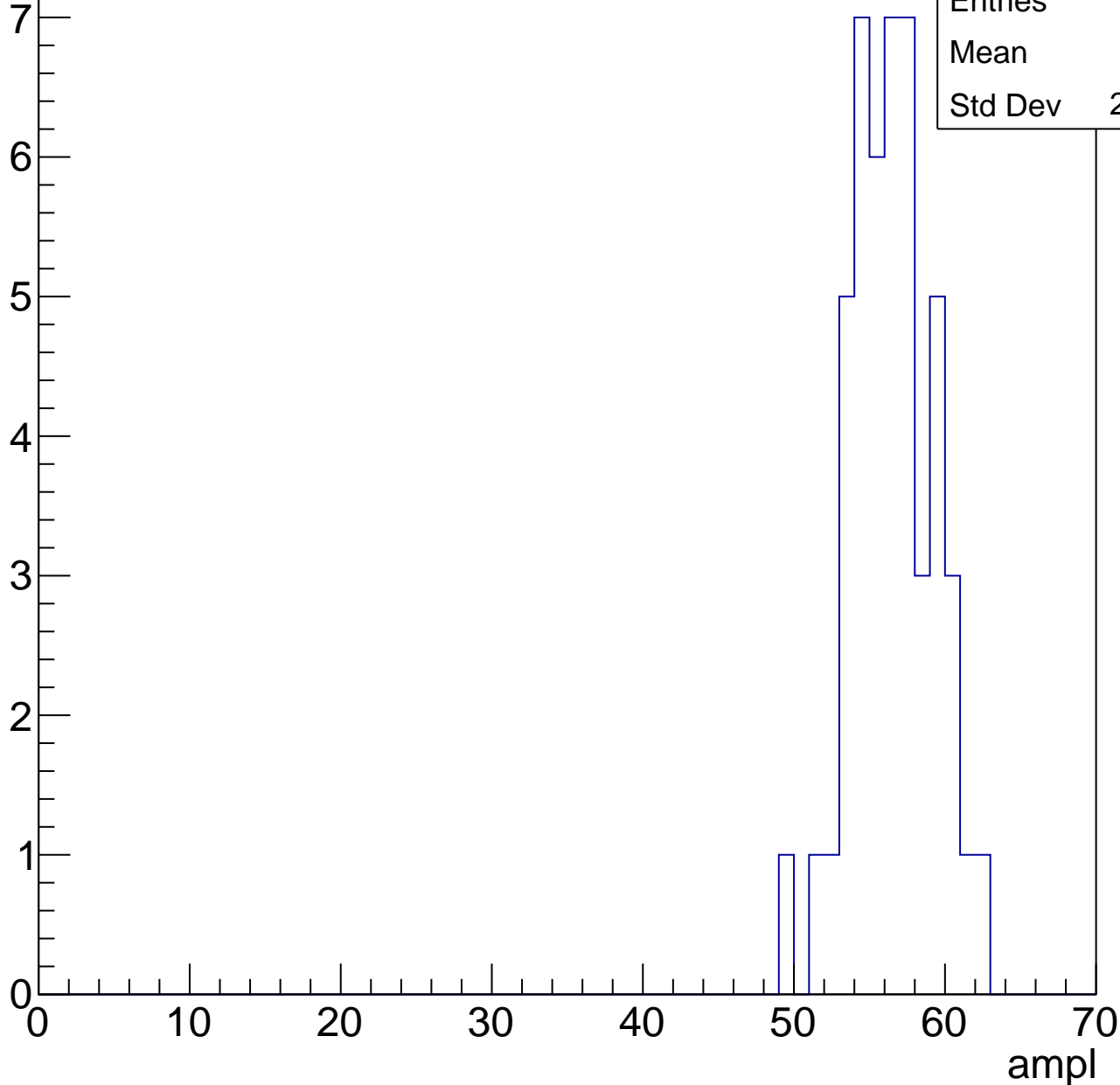


B1L003S, U19-ch122, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	56
Std Dev	2.669

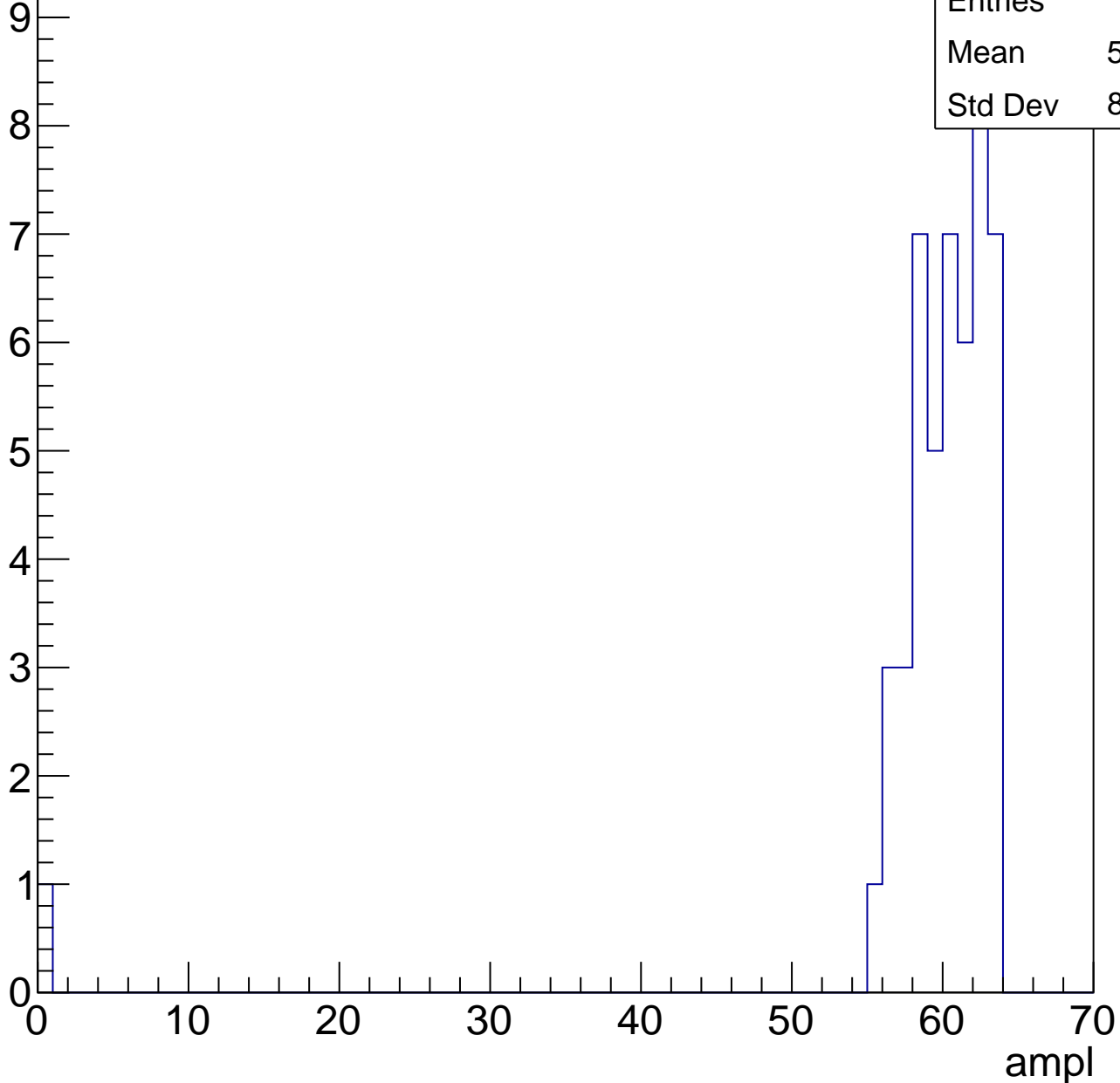


B1L003S, U19-ch122, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

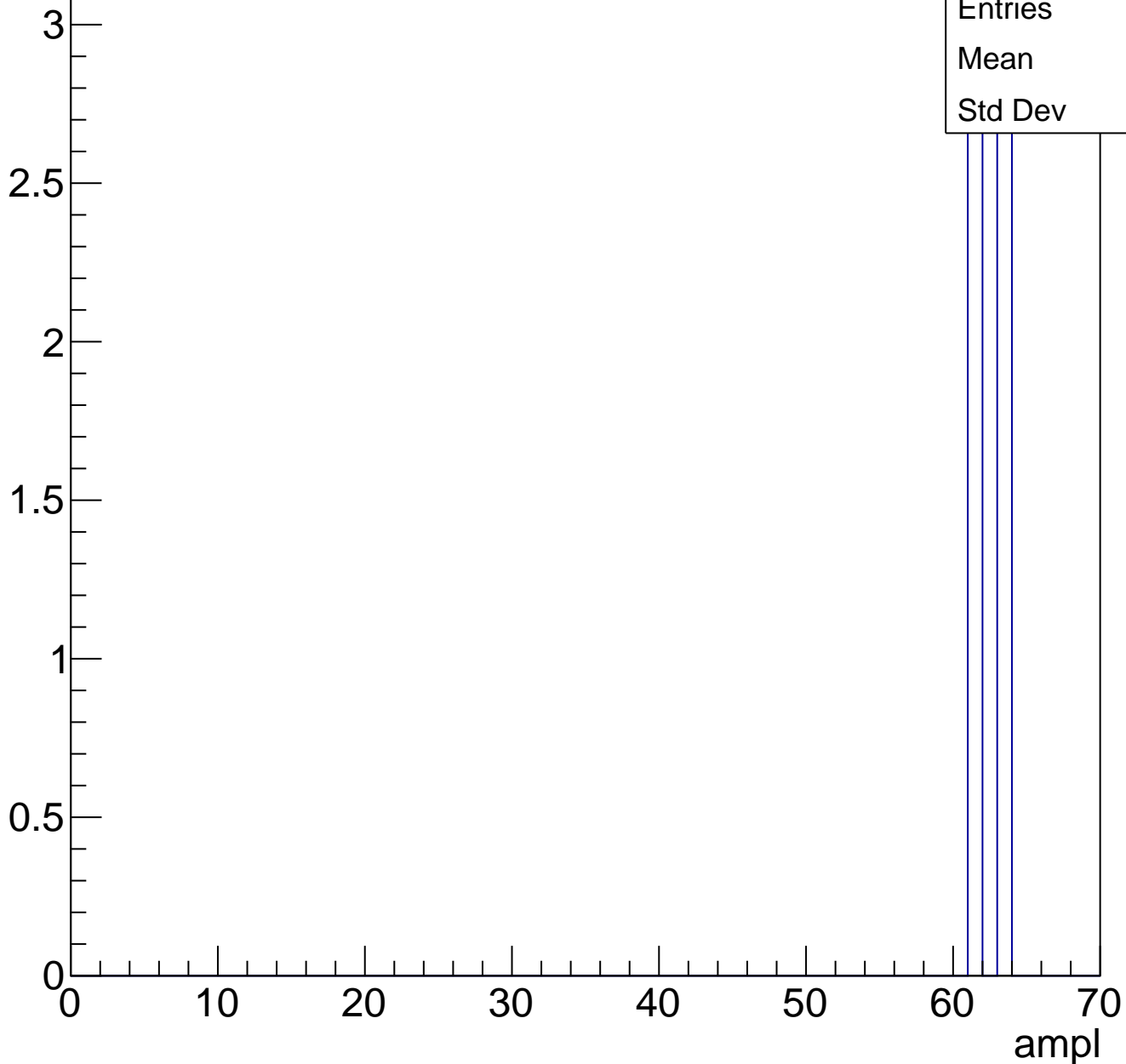
Entries	49
Mean	58.78
Std Dev	8.765



B1L003S, U19-ch122, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch122, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch123, adc0

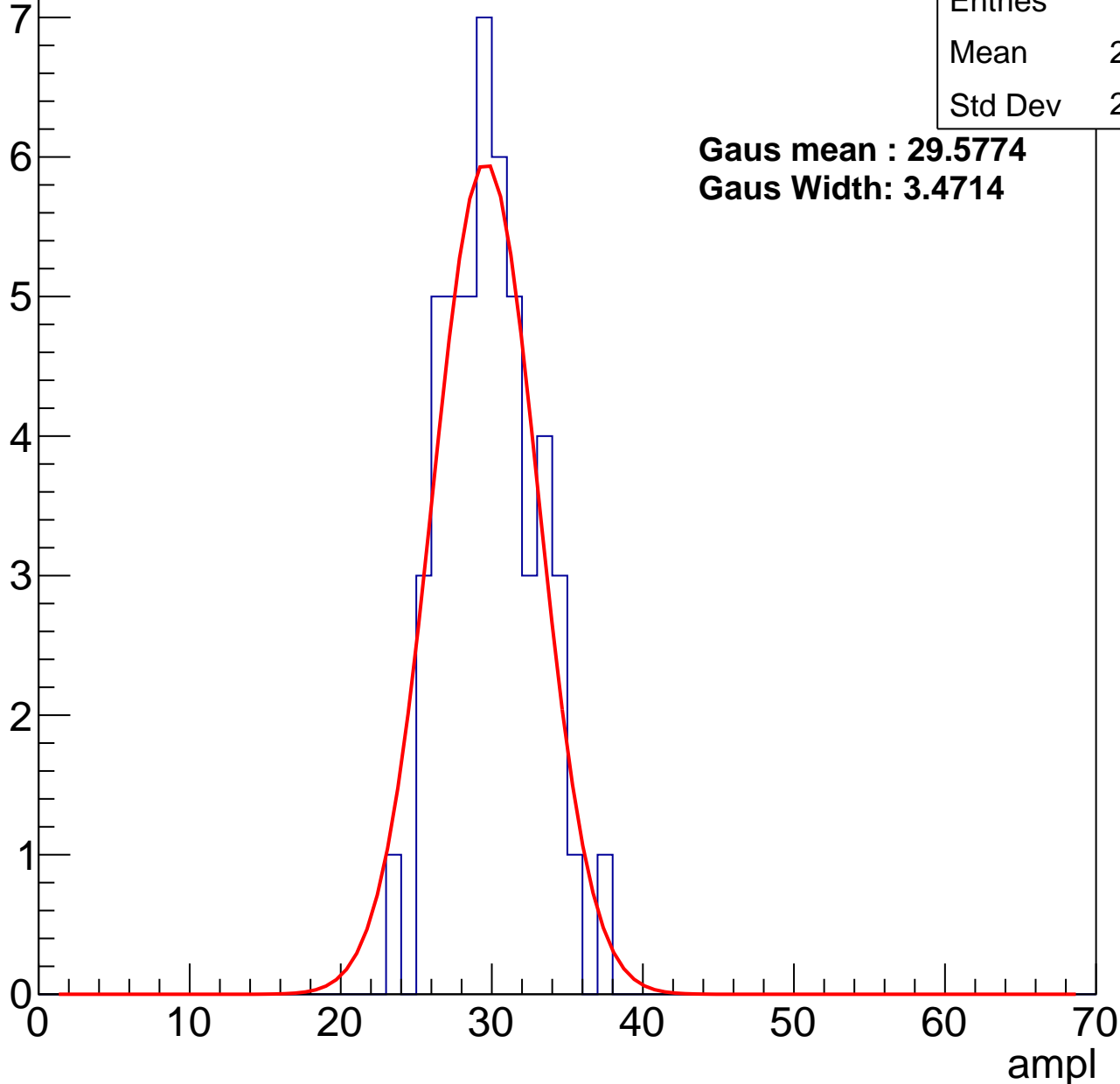
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	29.45
Std Dev	2.983

Gaus mean : 29.5774

Gaus Width: 3.4714



B1L003S, U19-ch123, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	91
Mean	36.03
Std Dev	3.772

Gaus mean : 36.8669

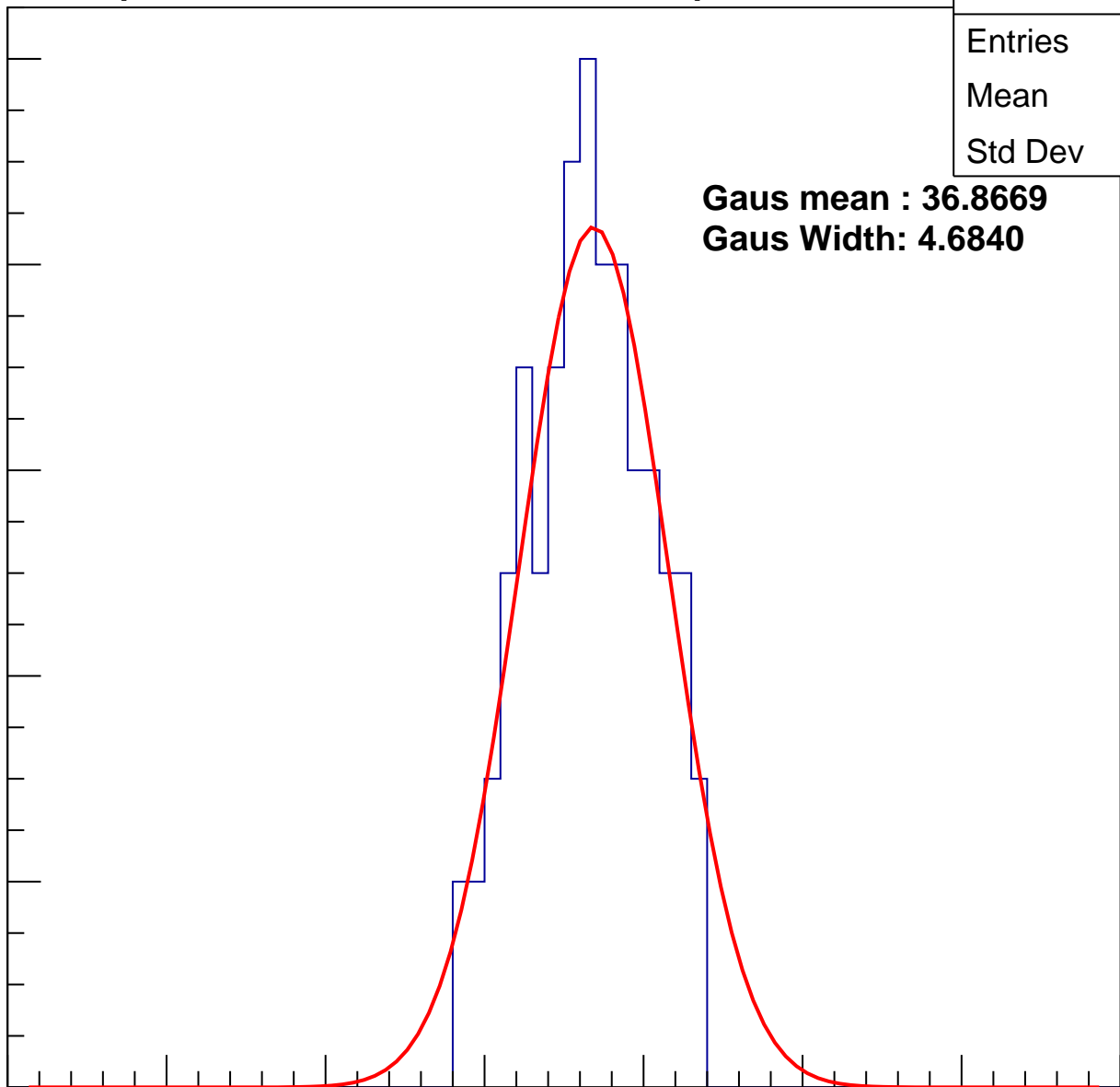
Gaus Width: 4.6840

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

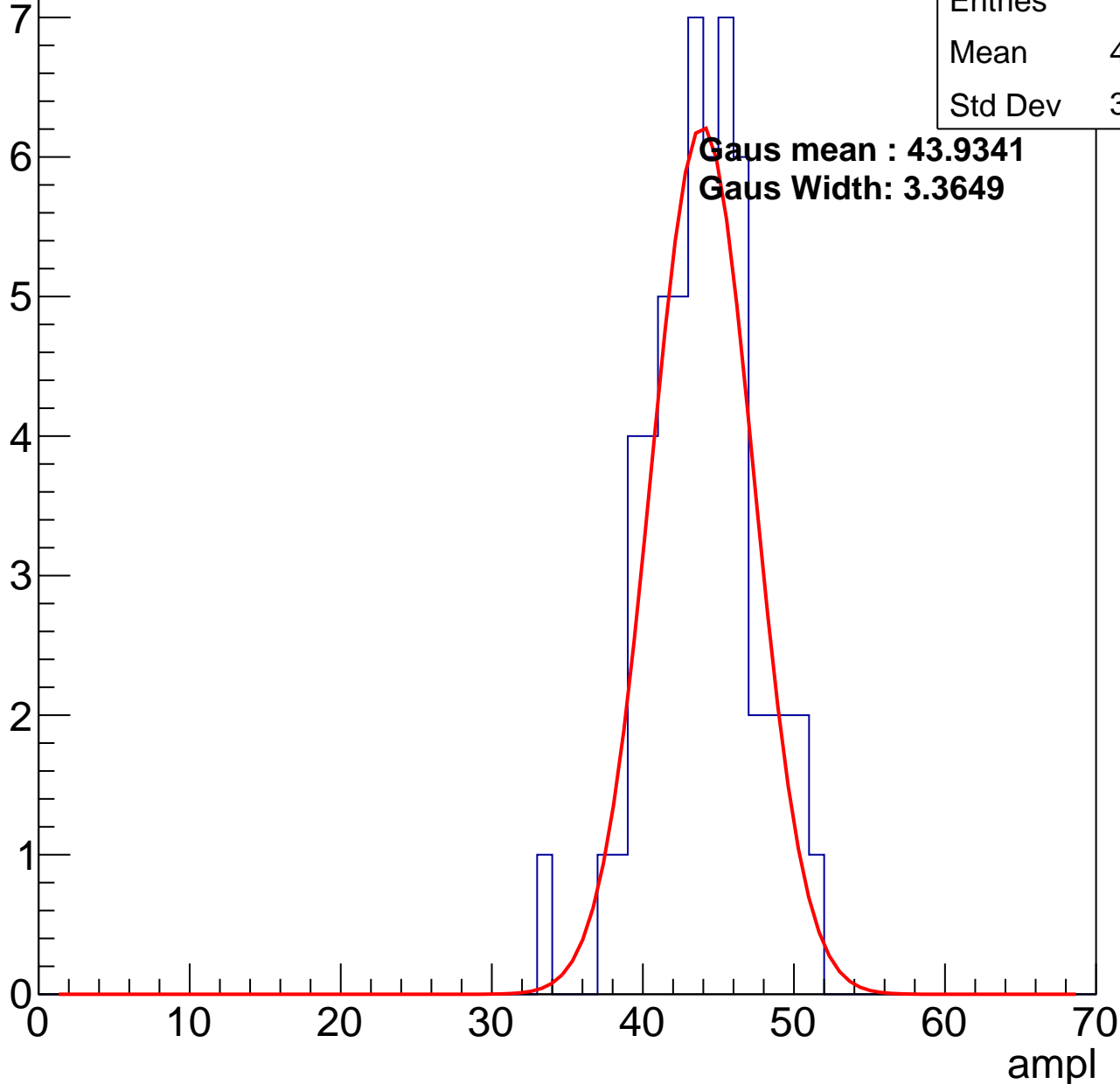


B1L003S, U19-ch123, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.46
Std Dev	3.479

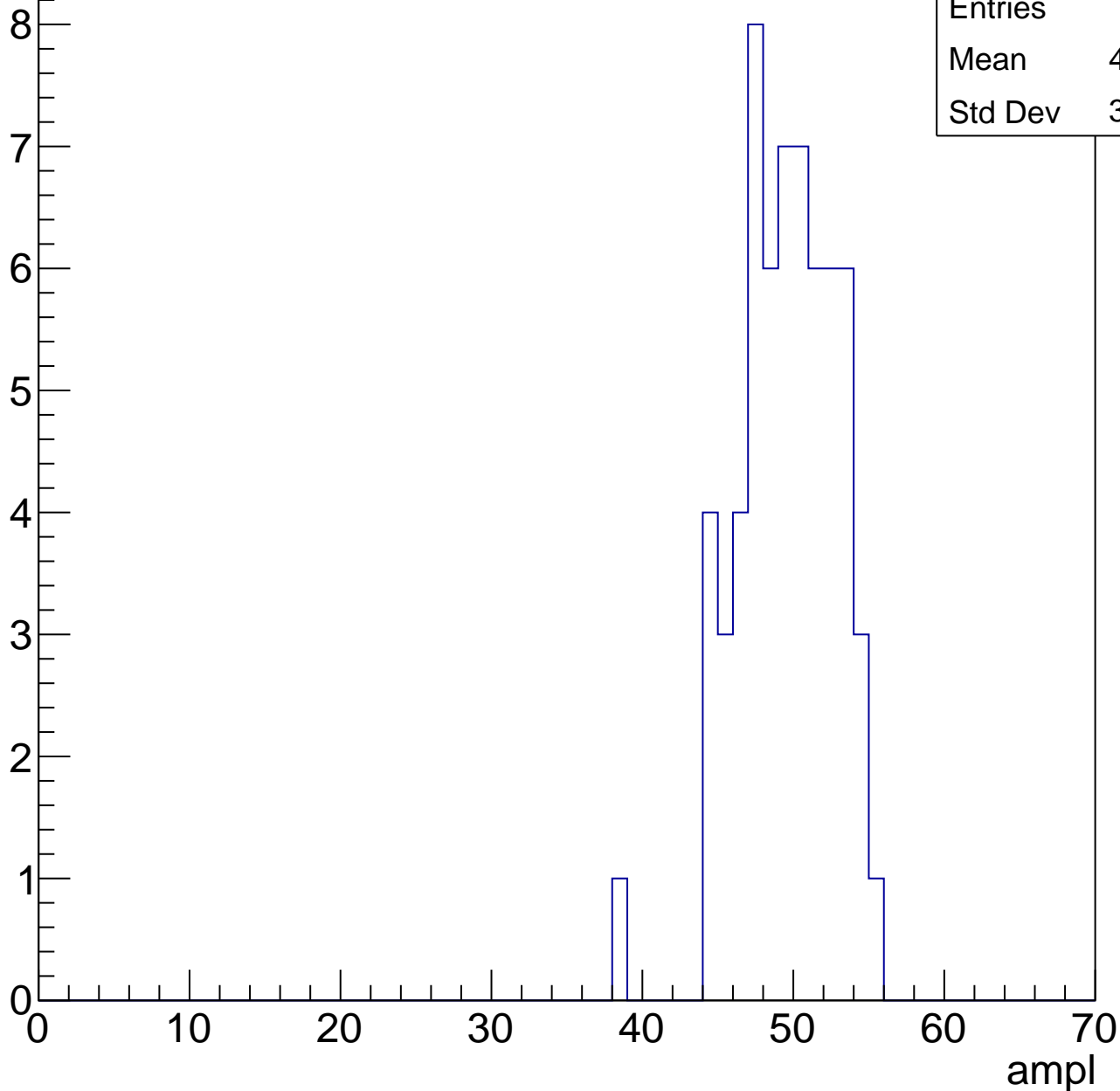


B1L003S, U19-ch123, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

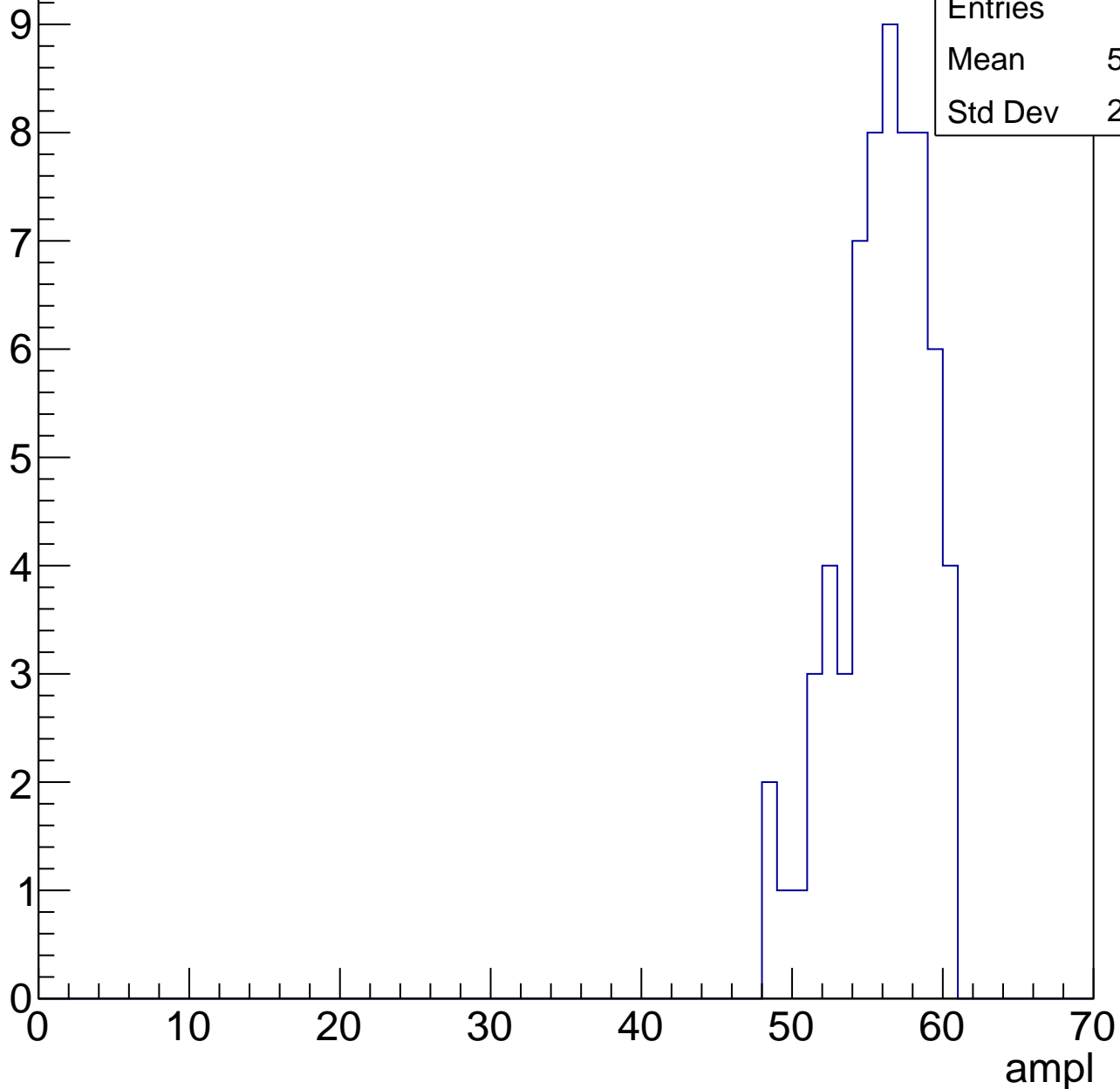
Entries	62
Mean	49.08
Std Dev	3.199



B1L003S, U19-ch123, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

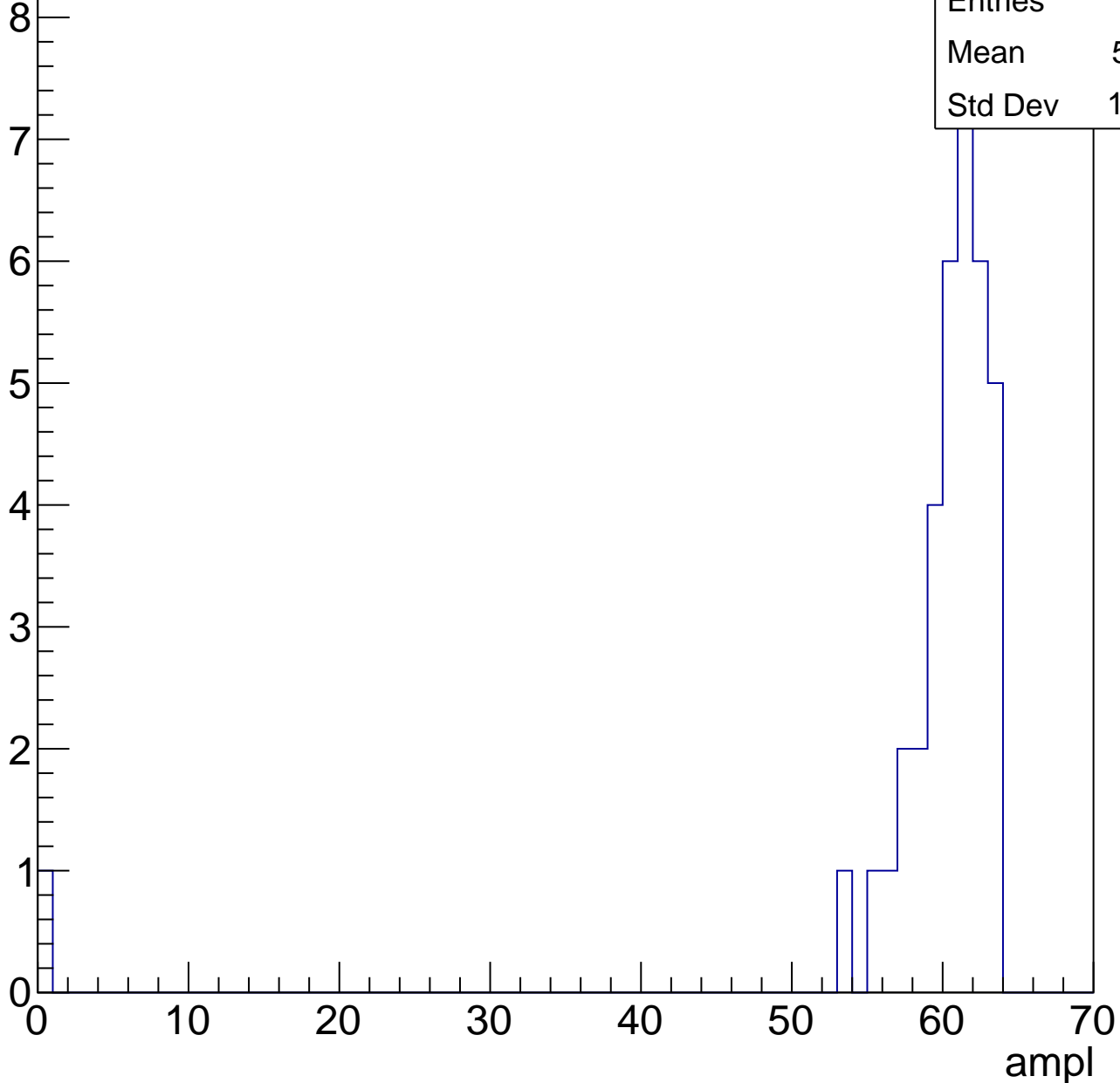


B1L003S, U19-ch123, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	58.51
Std Dev	10.02

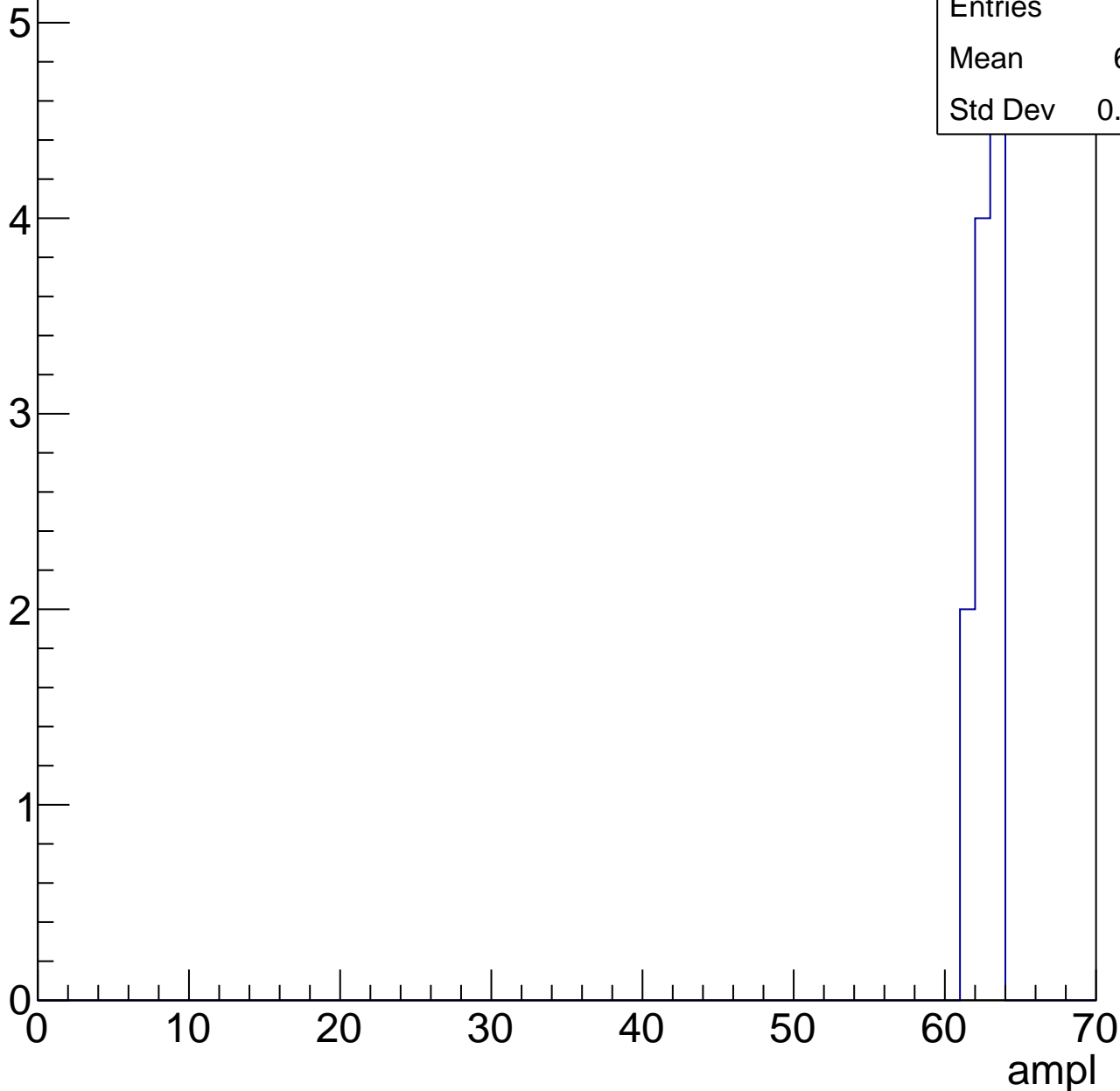


B1L003S, U19-ch123, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.27
Std Dev	0.7497



B1L003S, U19-ch123, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U19-ch124, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	30.18
Std Dev	3.056

Gaus mean : 31.0067

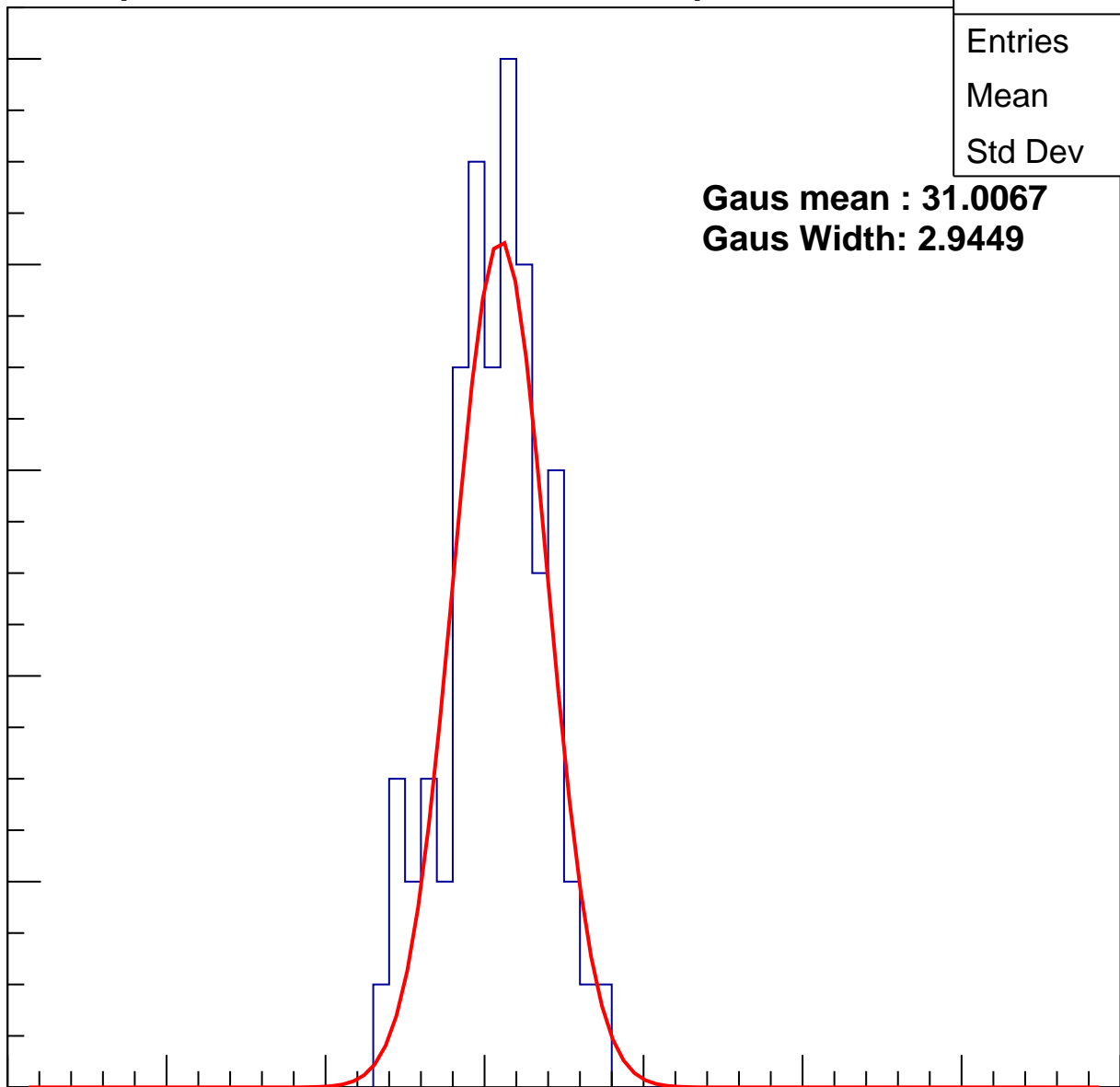
Gaus Width: 2.9449

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch124, adc1

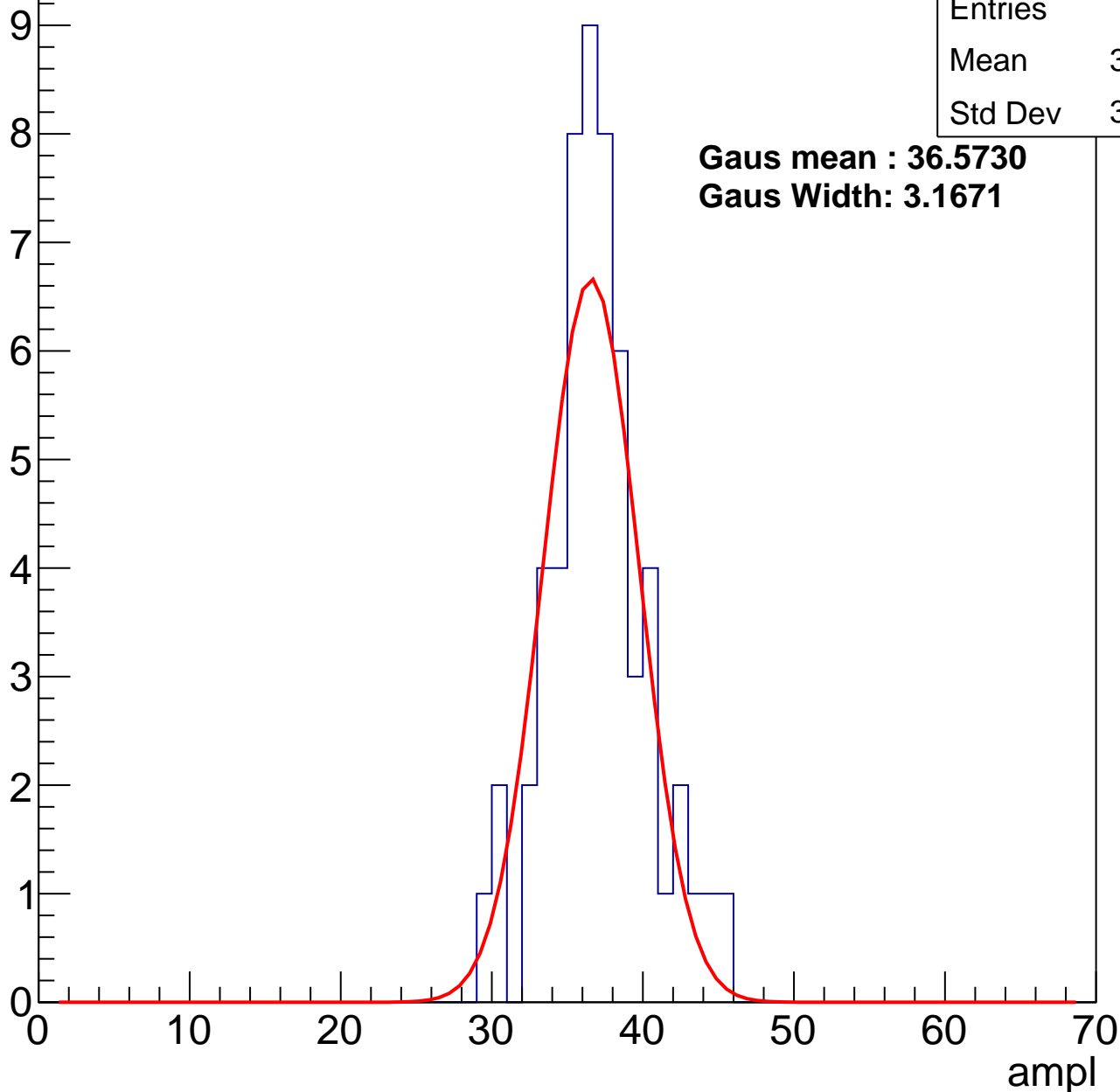
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.54
Std Dev	3.272

Gaus mean : 36.5730

Gaus Width: 3.1671



B1L003S, U19-ch124, adc2

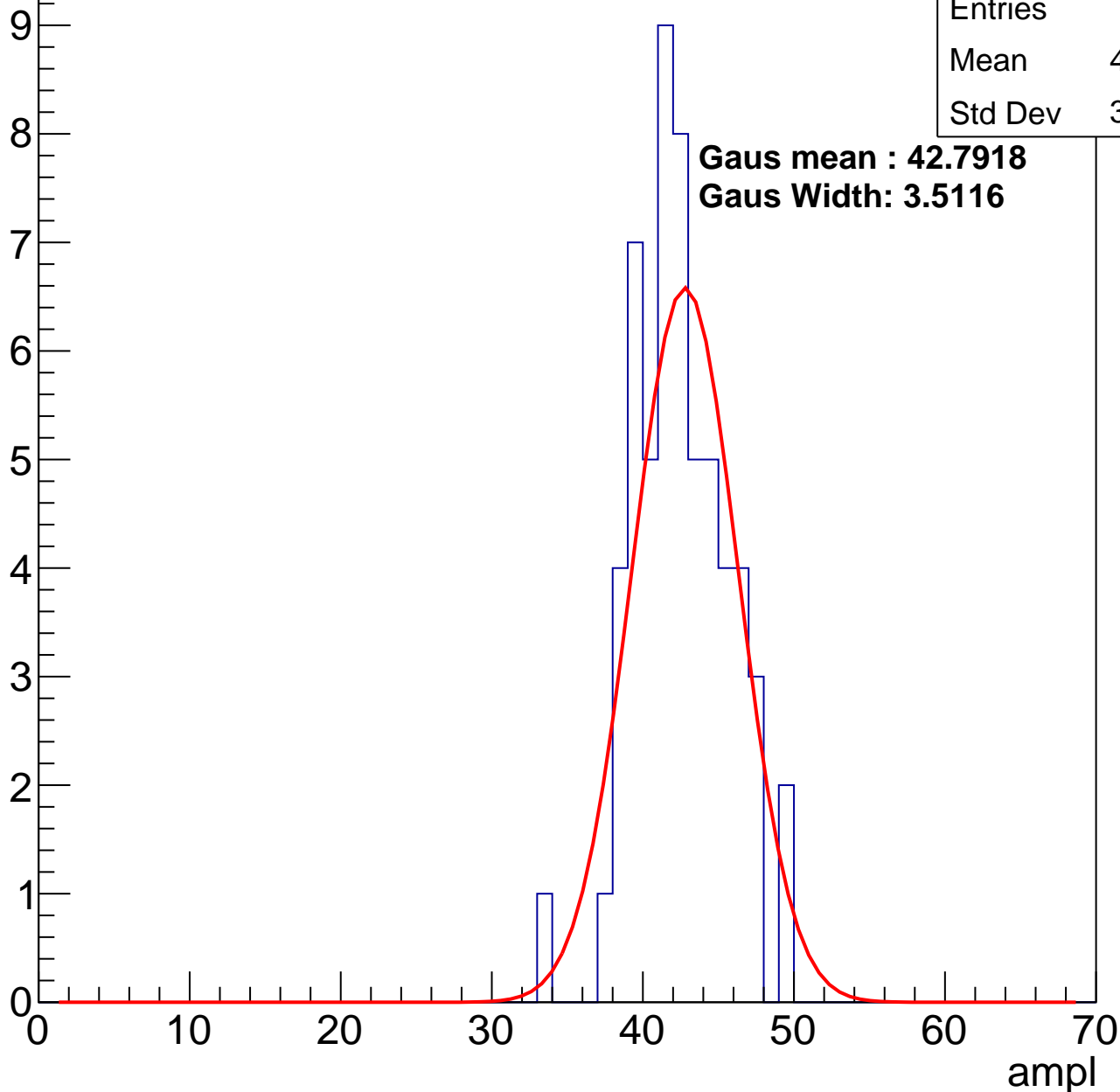
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.03
Std Dev	3.107

Gaus mean : 42.7918

Gaus Width: 3.5116

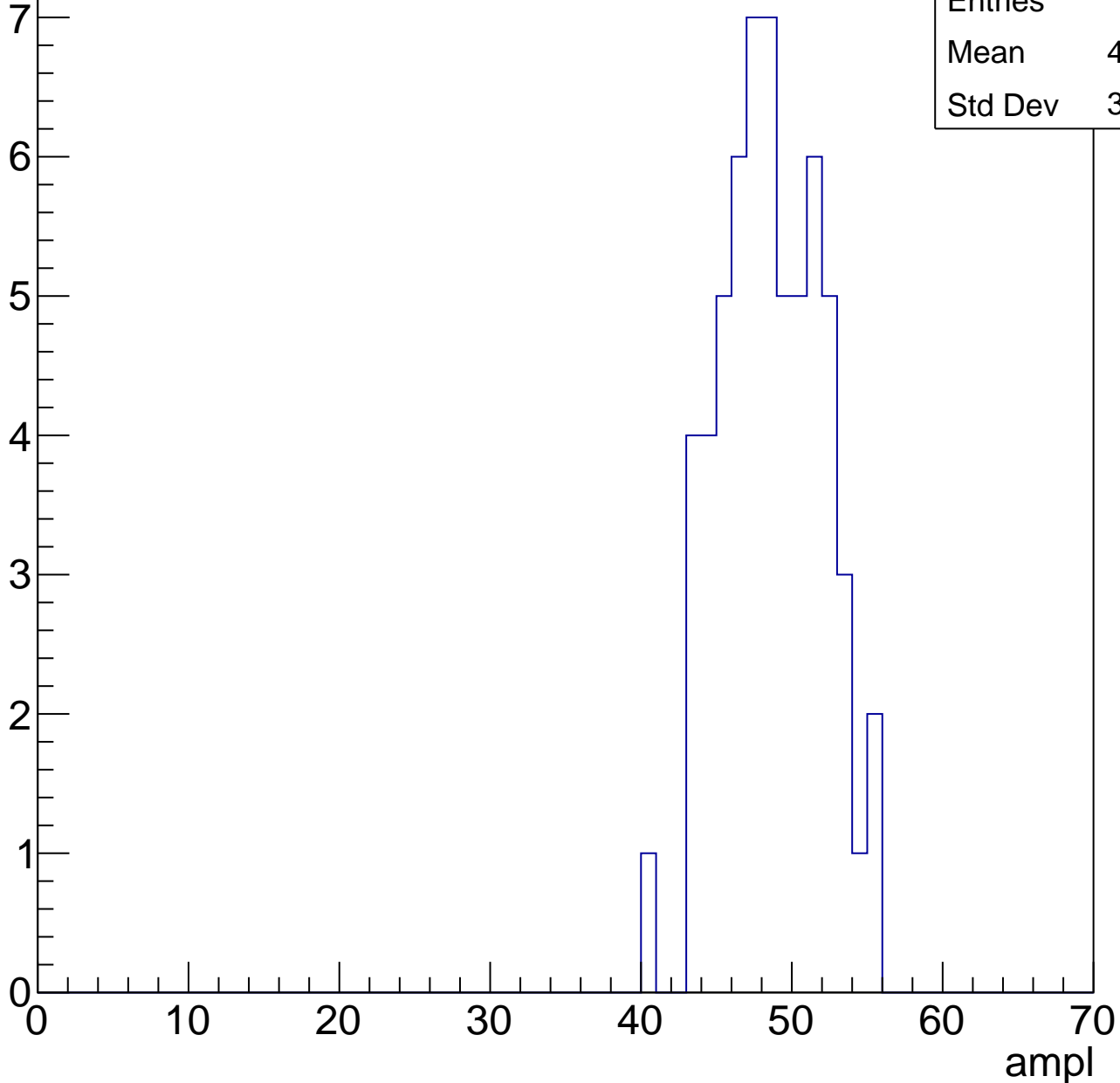


B1L003S, U19-ch124, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.16
Std Dev	3.315

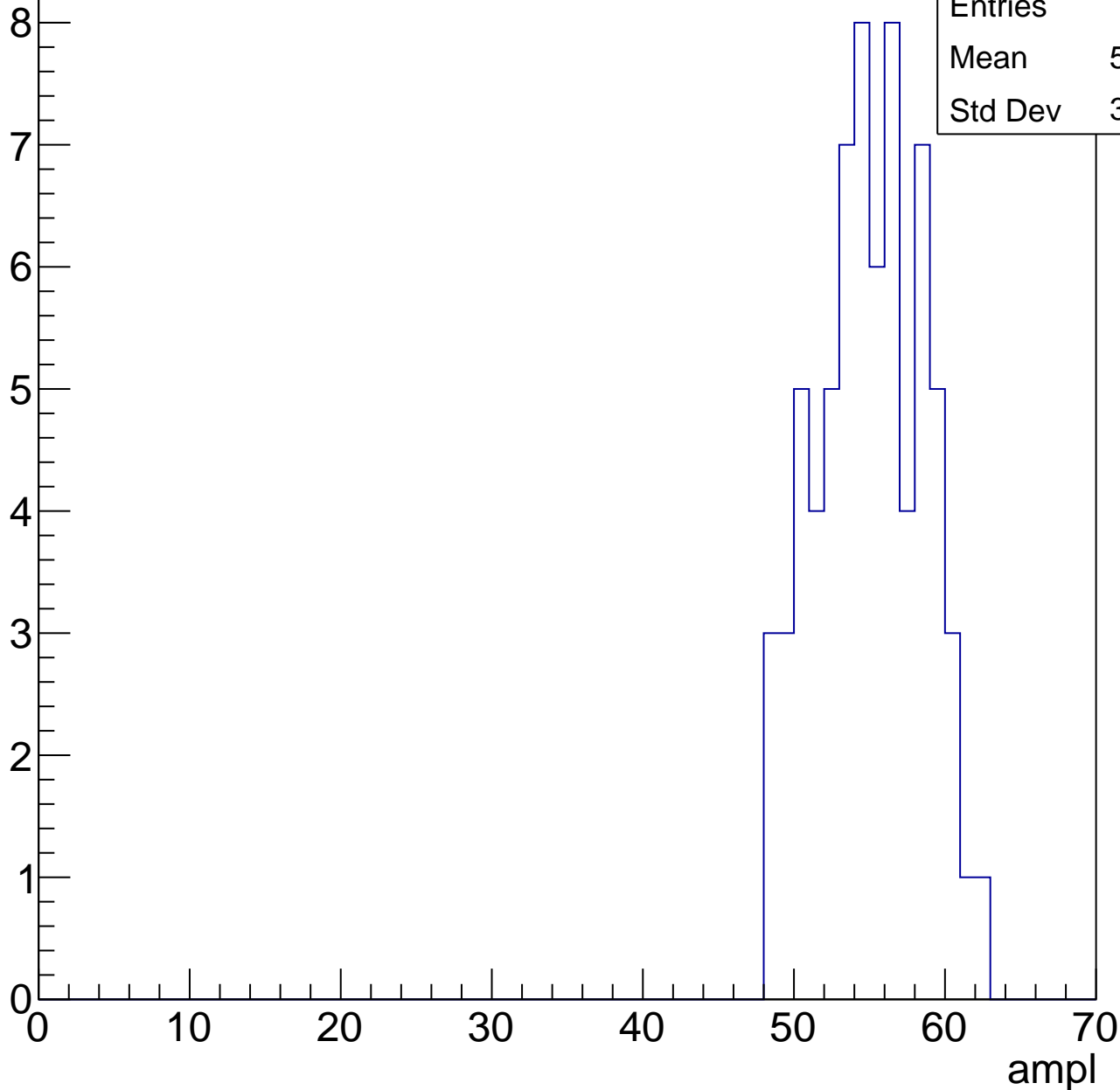


B1L003S, U19-ch124, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	54.54
Std Dev	3.459



B1L003S, U19-ch124, adc5

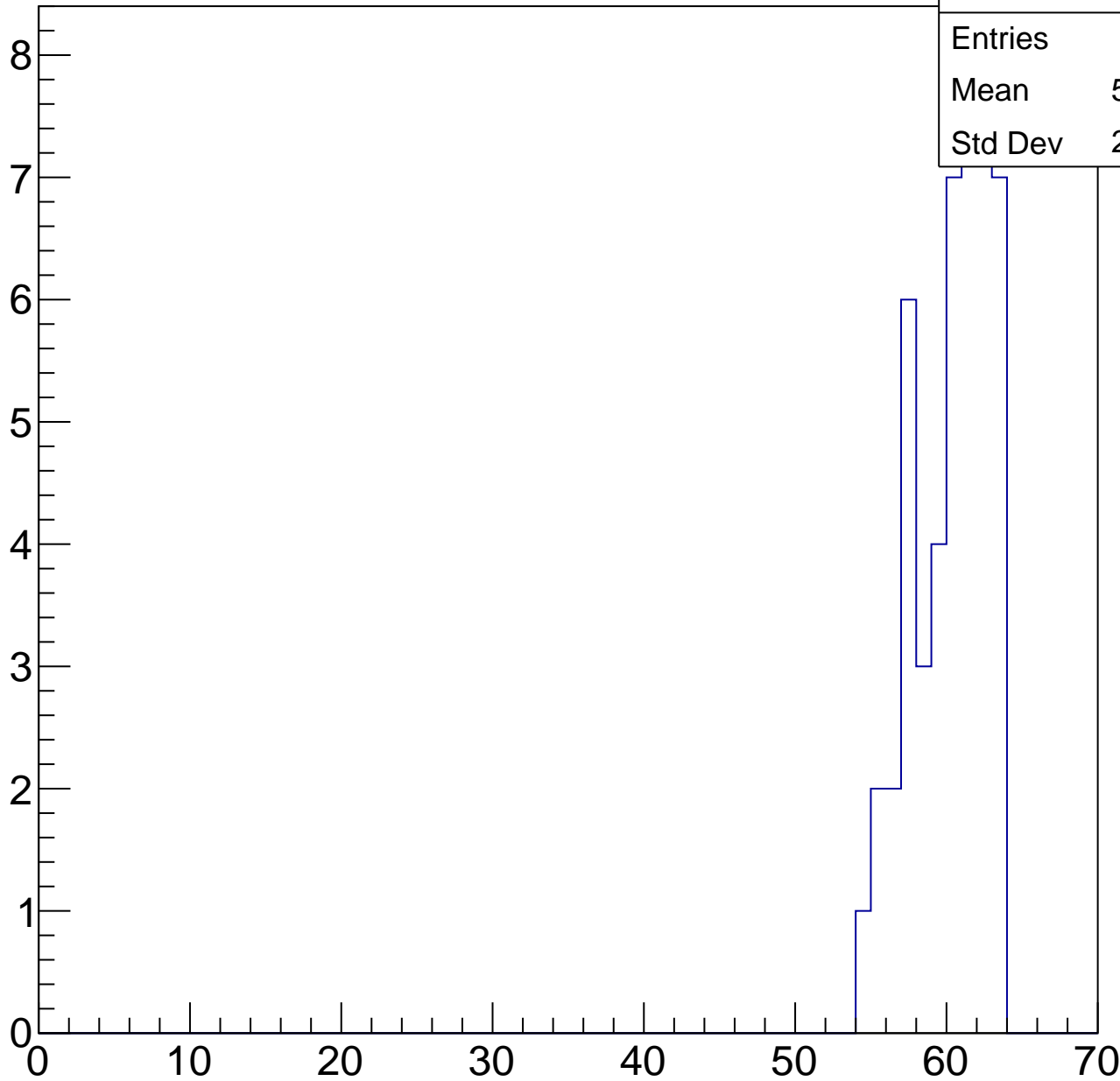
calib_packv5_042523_0143.root, FC#13, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.85
Std Dev	2.458

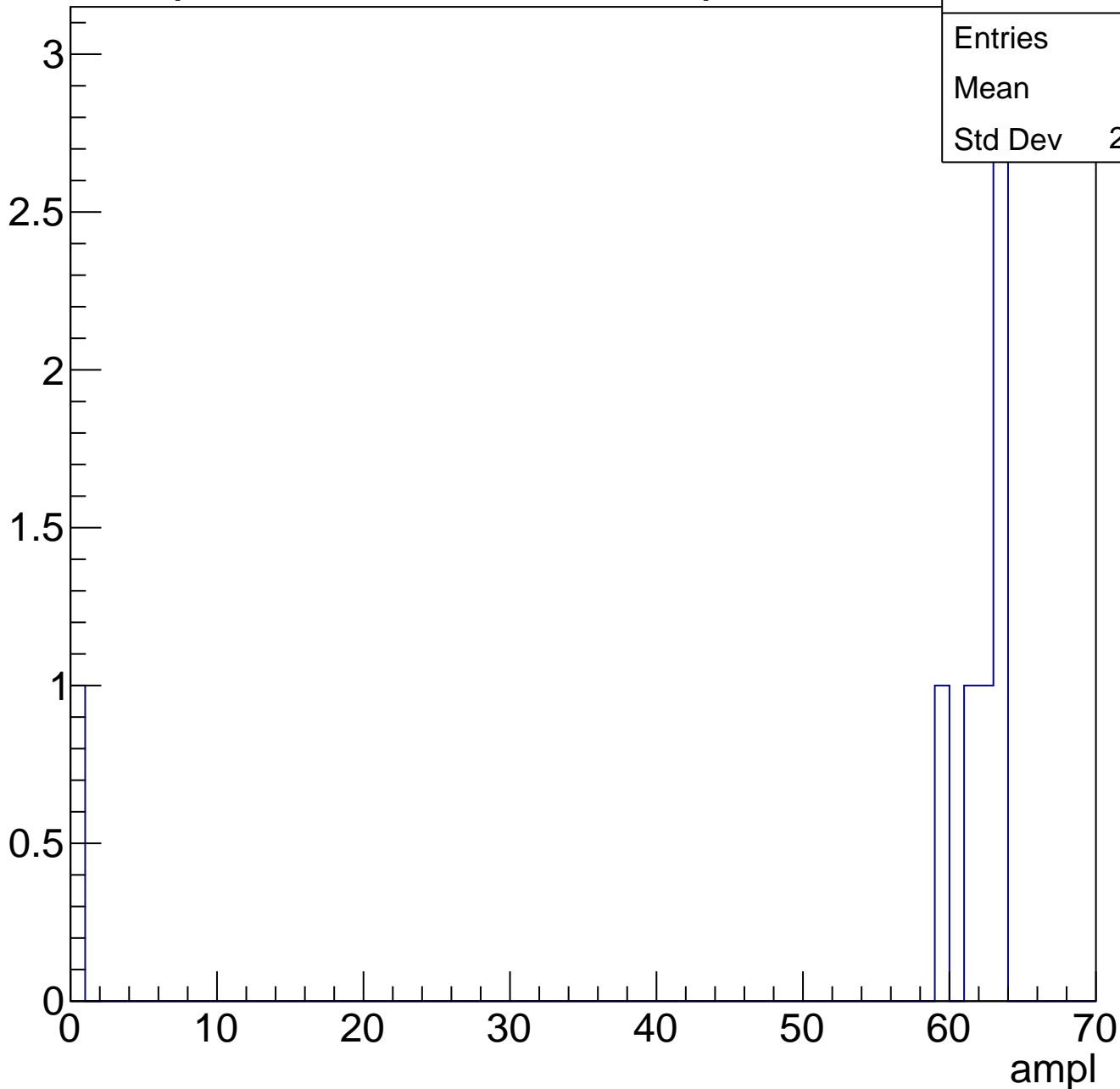
ampl



B1L003S, U19-ch124, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch124, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L003S, U19-ch125, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	27.42
Std Dev	3.687

Gaus mean : 27.5292

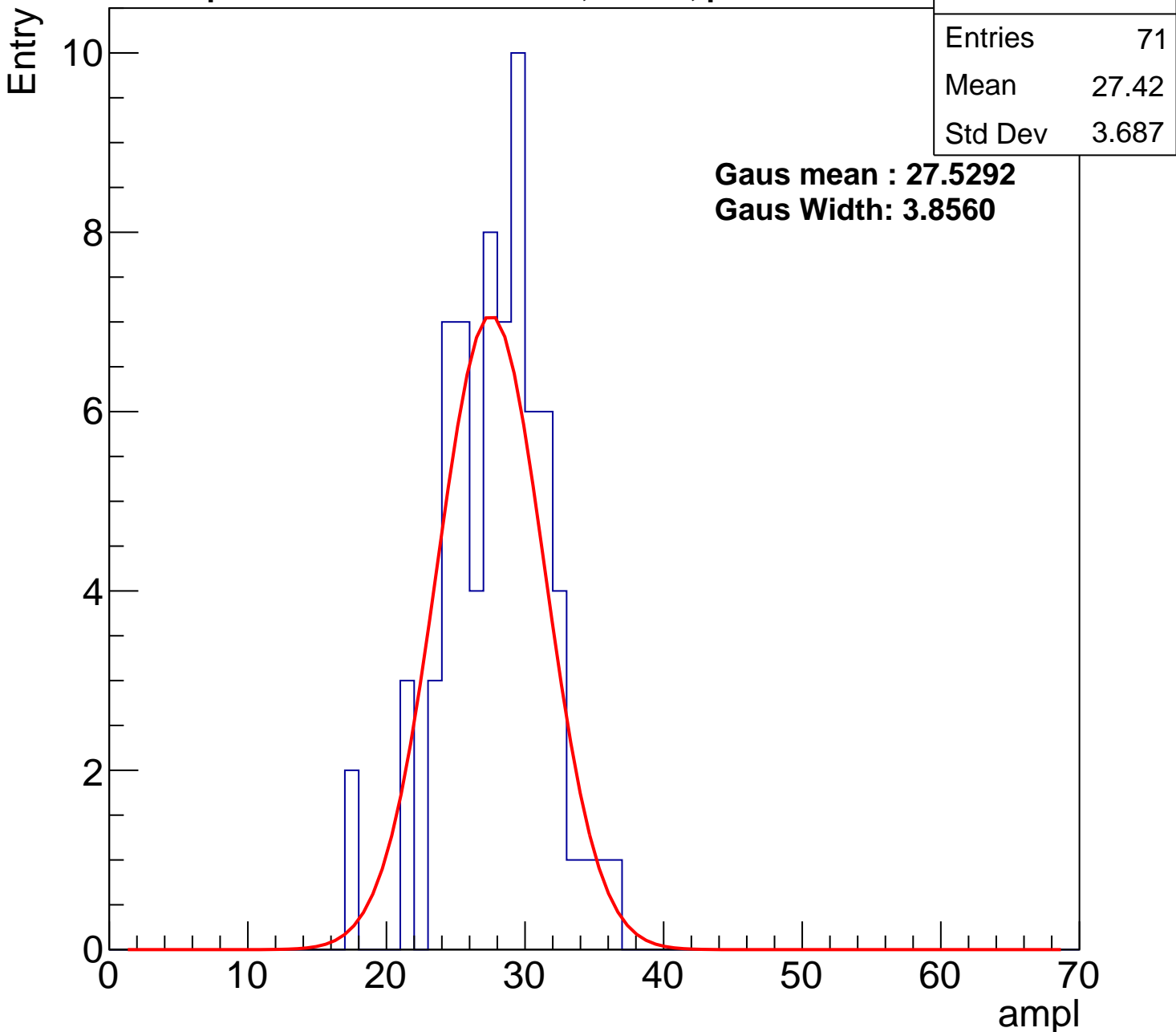
Gaus Width: 3.8560

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch125, adc1

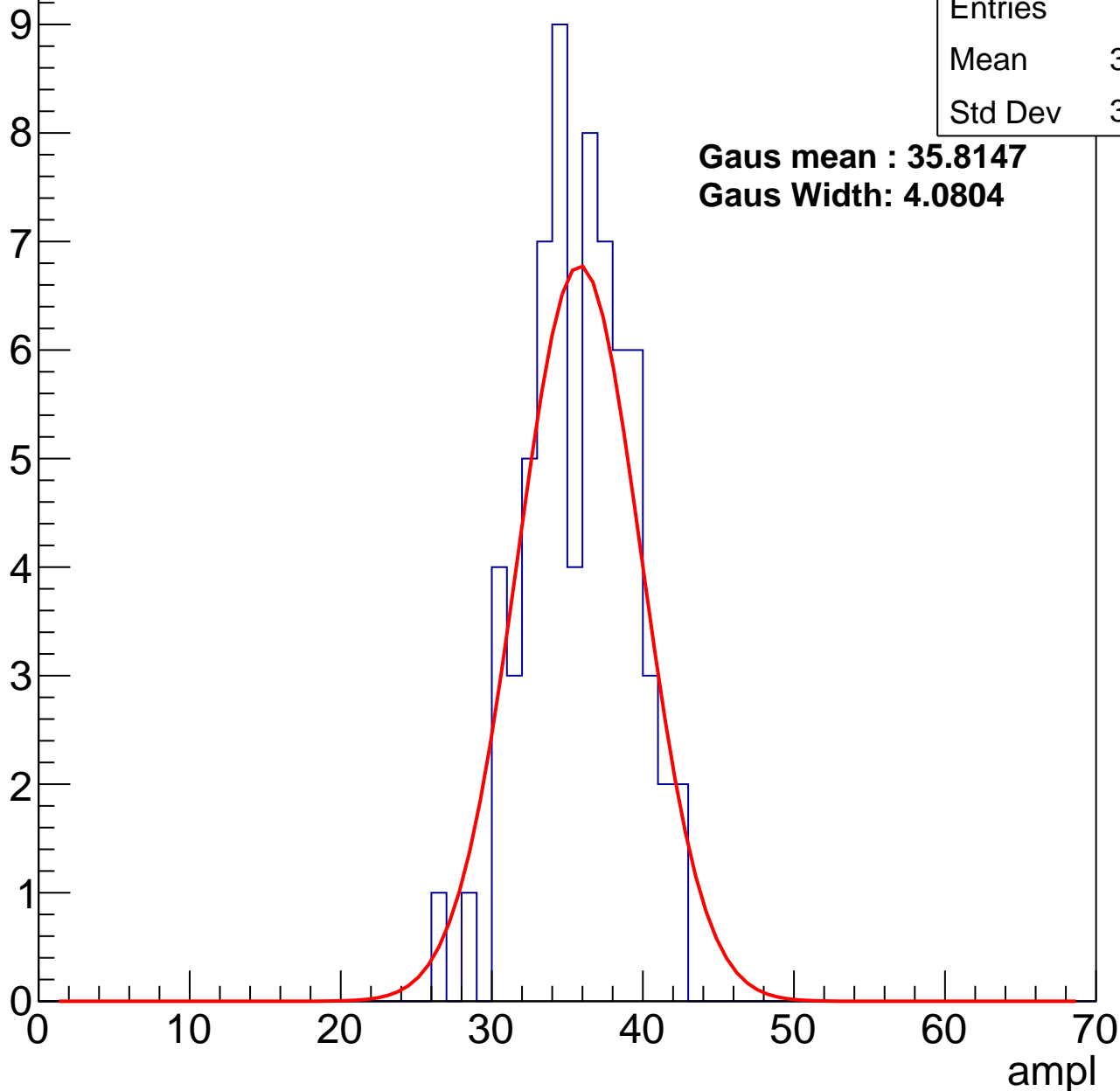
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.28
Std Dev	3.399

Gaus mean : 35.8147

Gaus Width: 4.0804



B1L003S, U19-ch125, adc2

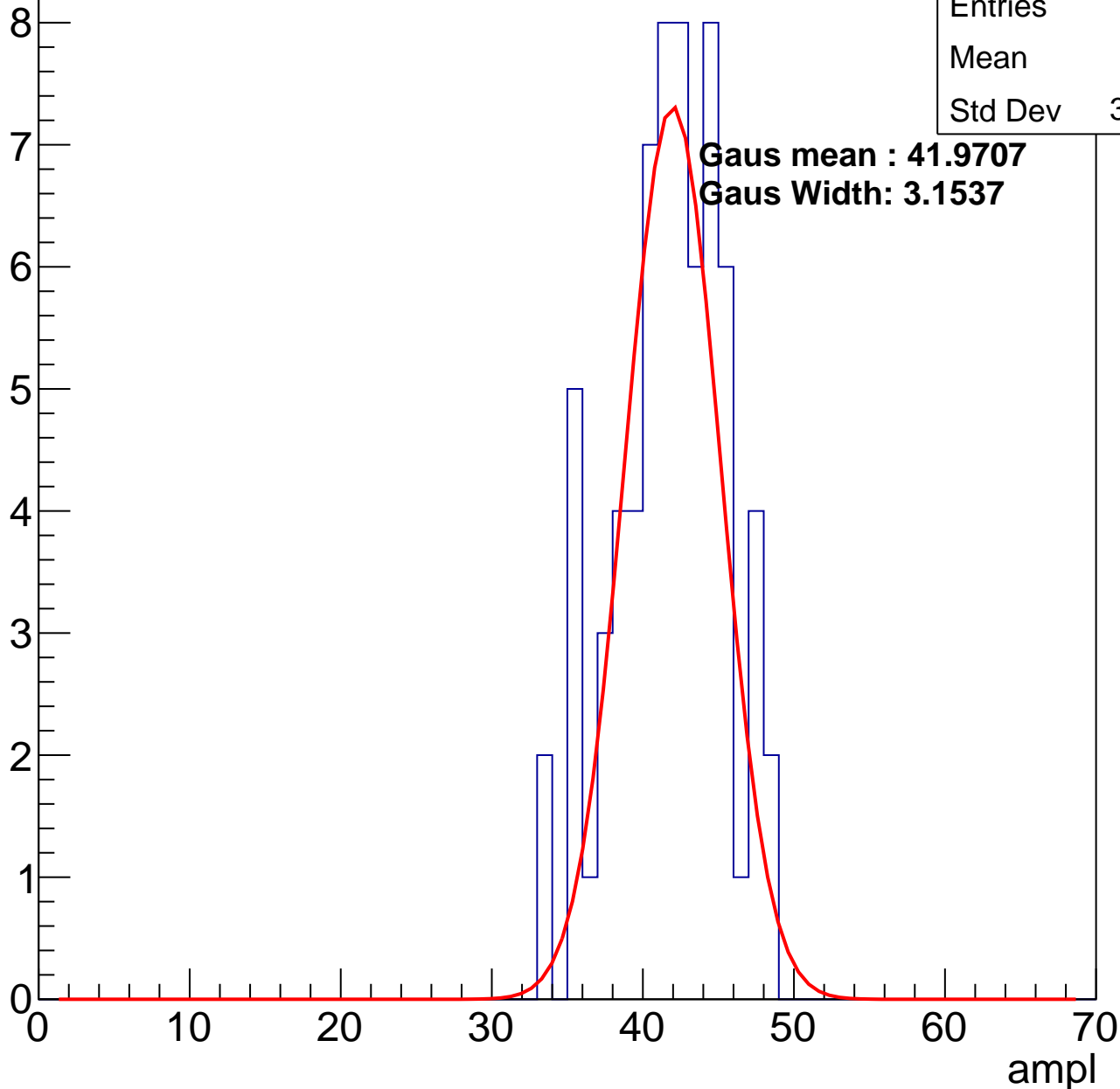
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	41.3
Std Dev	3.613

Gaus mean : 41.9707

Gaus Width: 3.1537

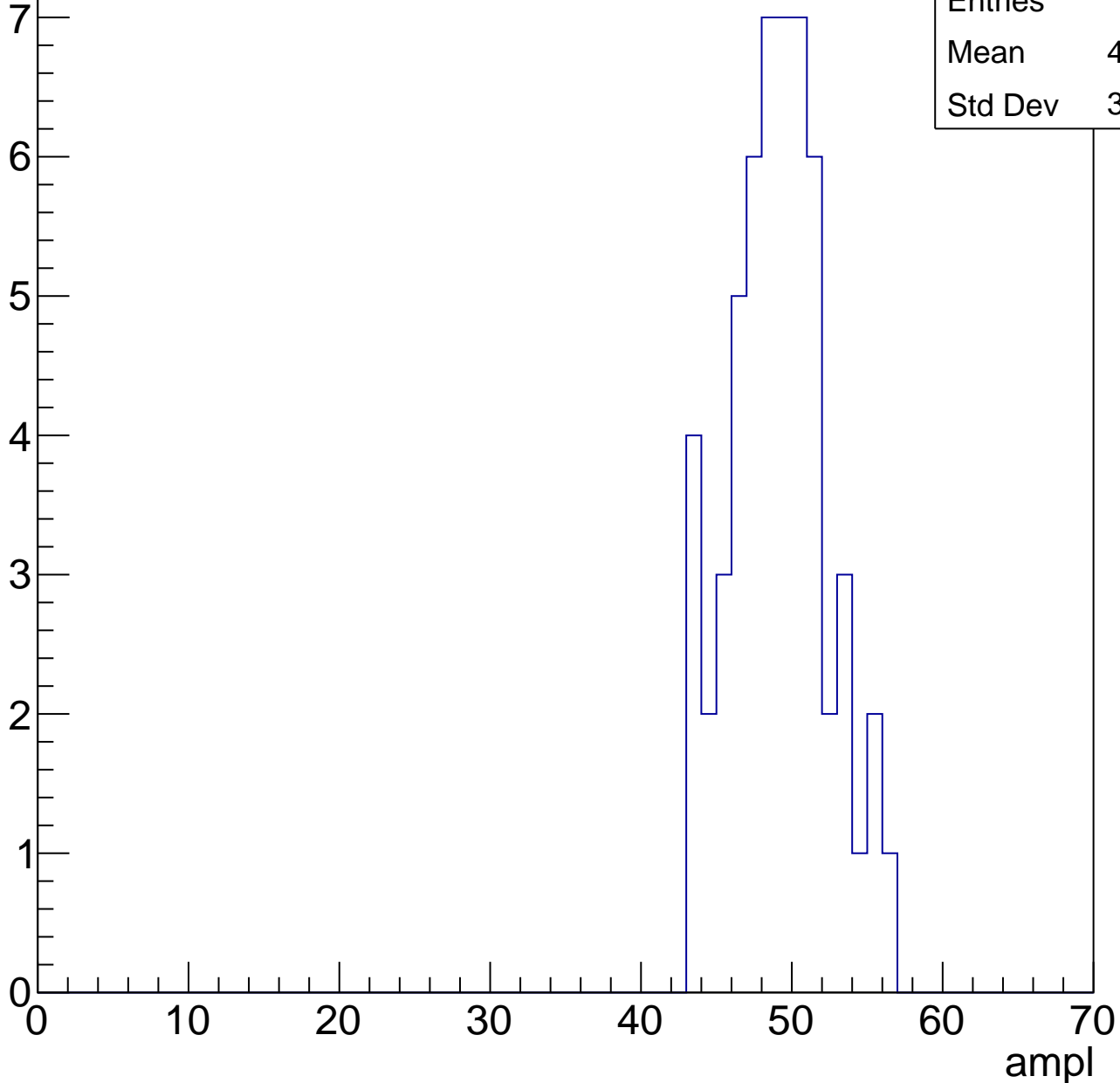


B1L003S, U19-ch125, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	48.66
Std Dev	3.147

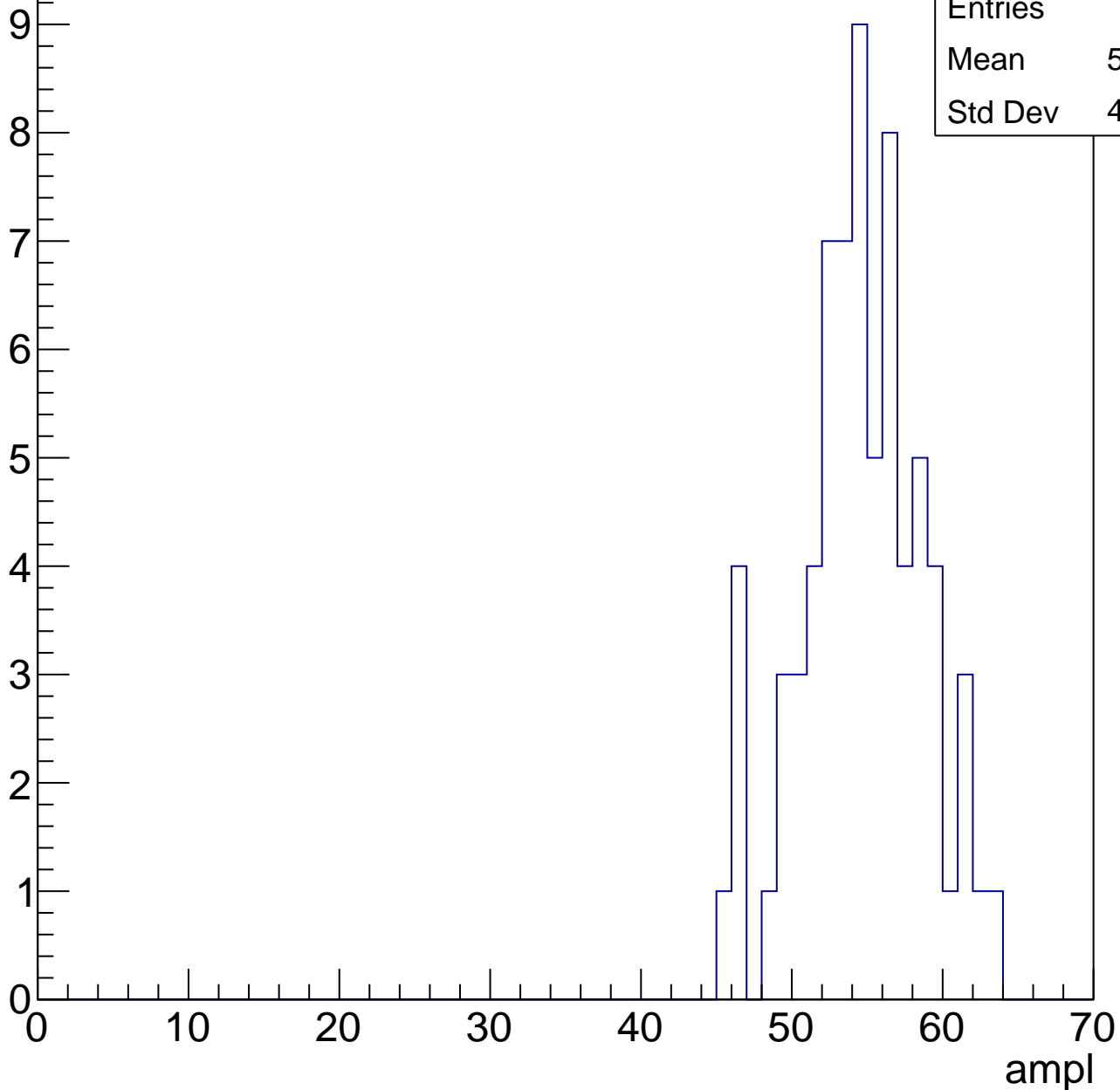


B1L003S, U19-ch125, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	54.14
Std Dev	4.019

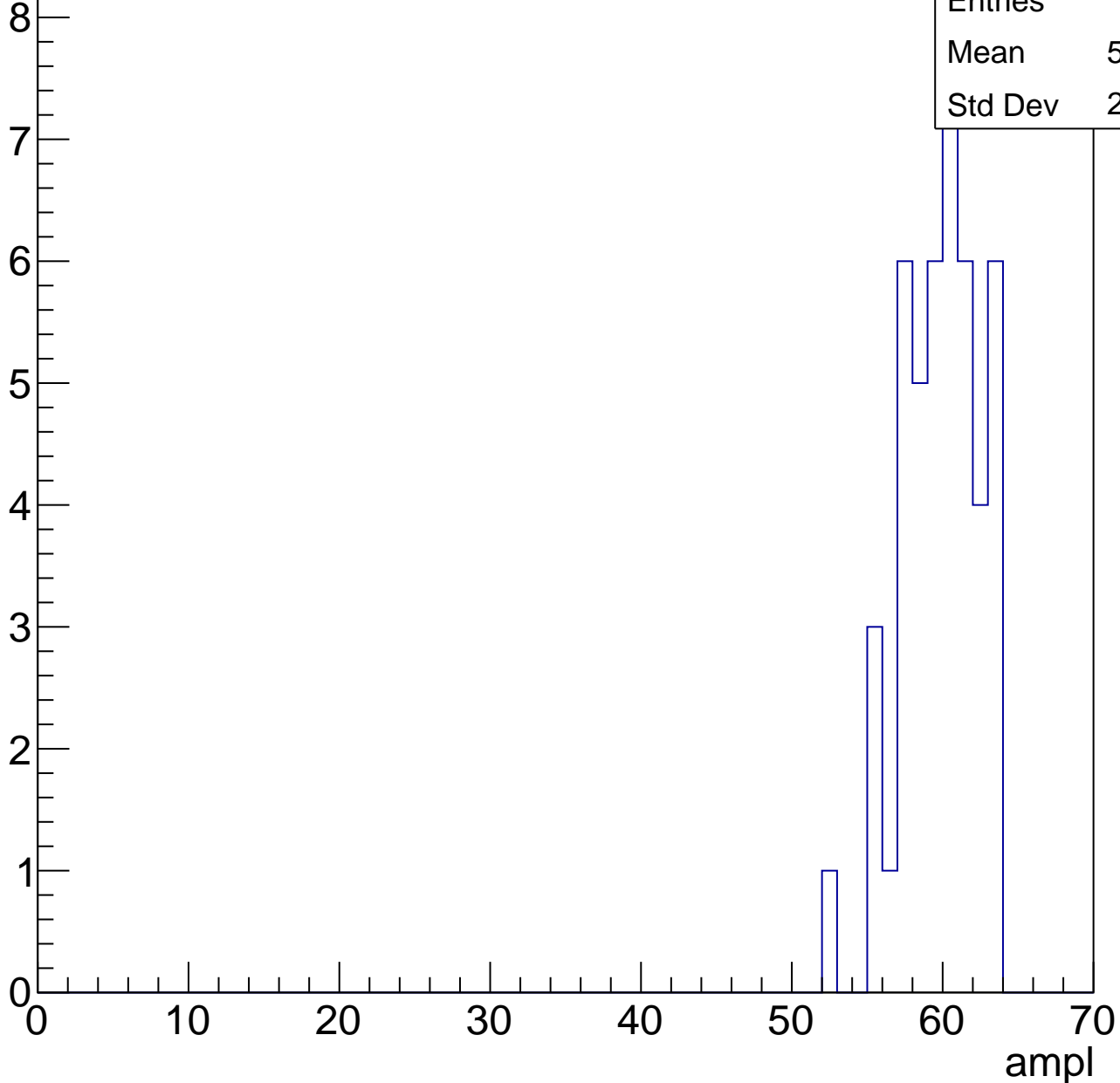


B1L003S, U19-ch125, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	59.37
Std Dev	2.523

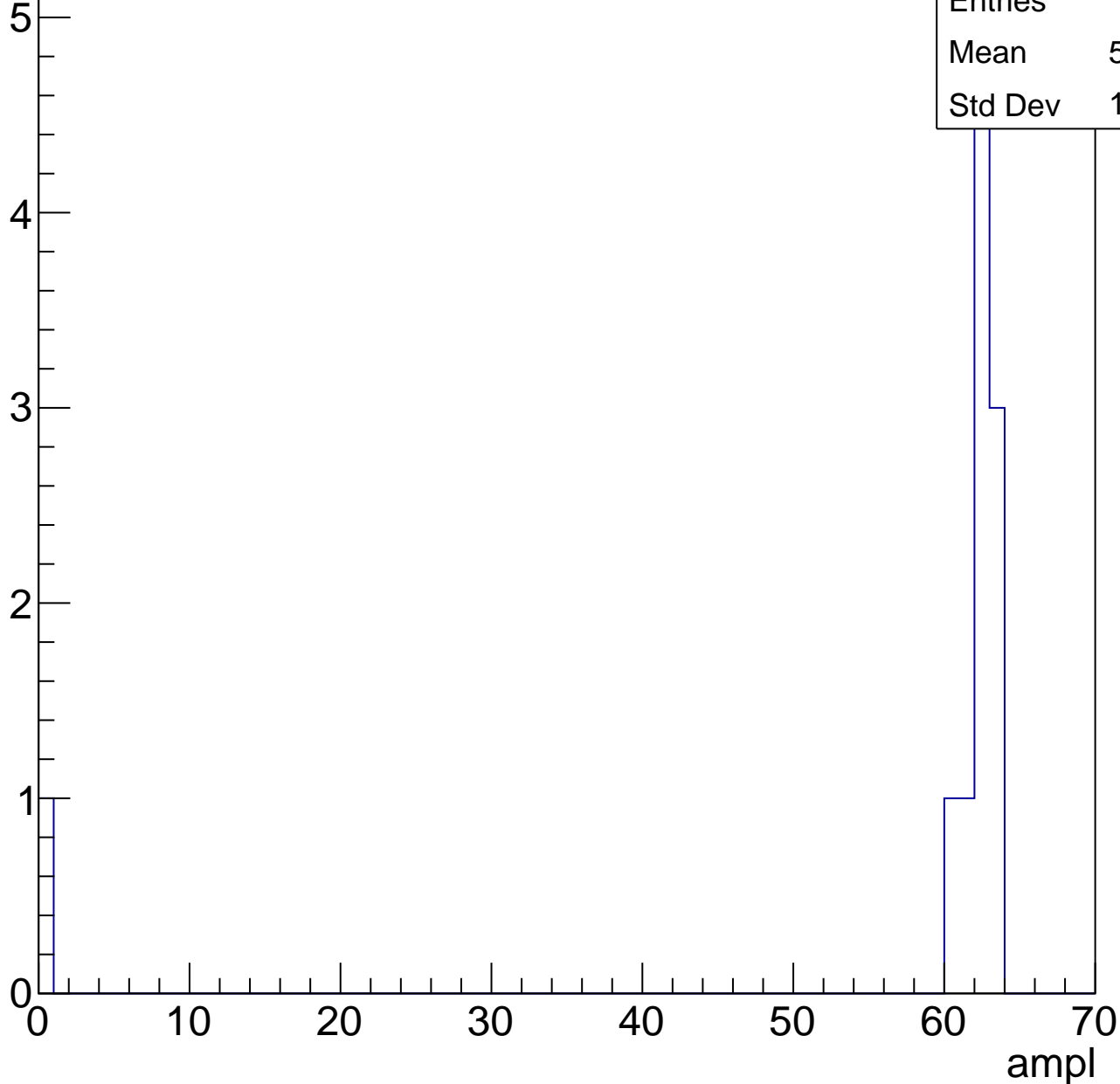


B1L003S, U19-ch125, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	56.36
Std Dev	17.84



B1L003S, U19-ch125, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch126, adc0

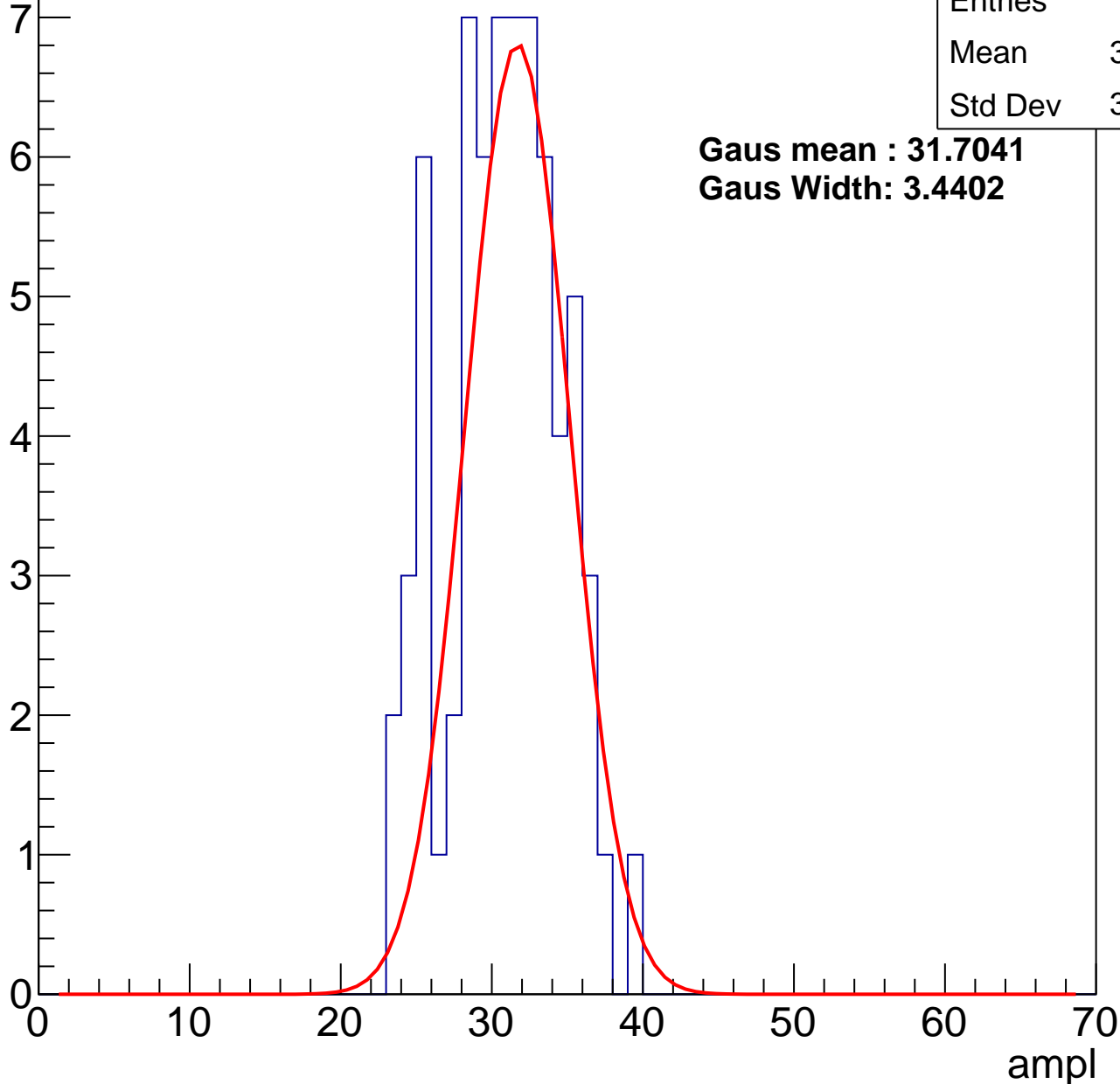
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	30.32
Std Dev	3.704

Gaus mean : 31.7041

Gaus Width: 3.4402

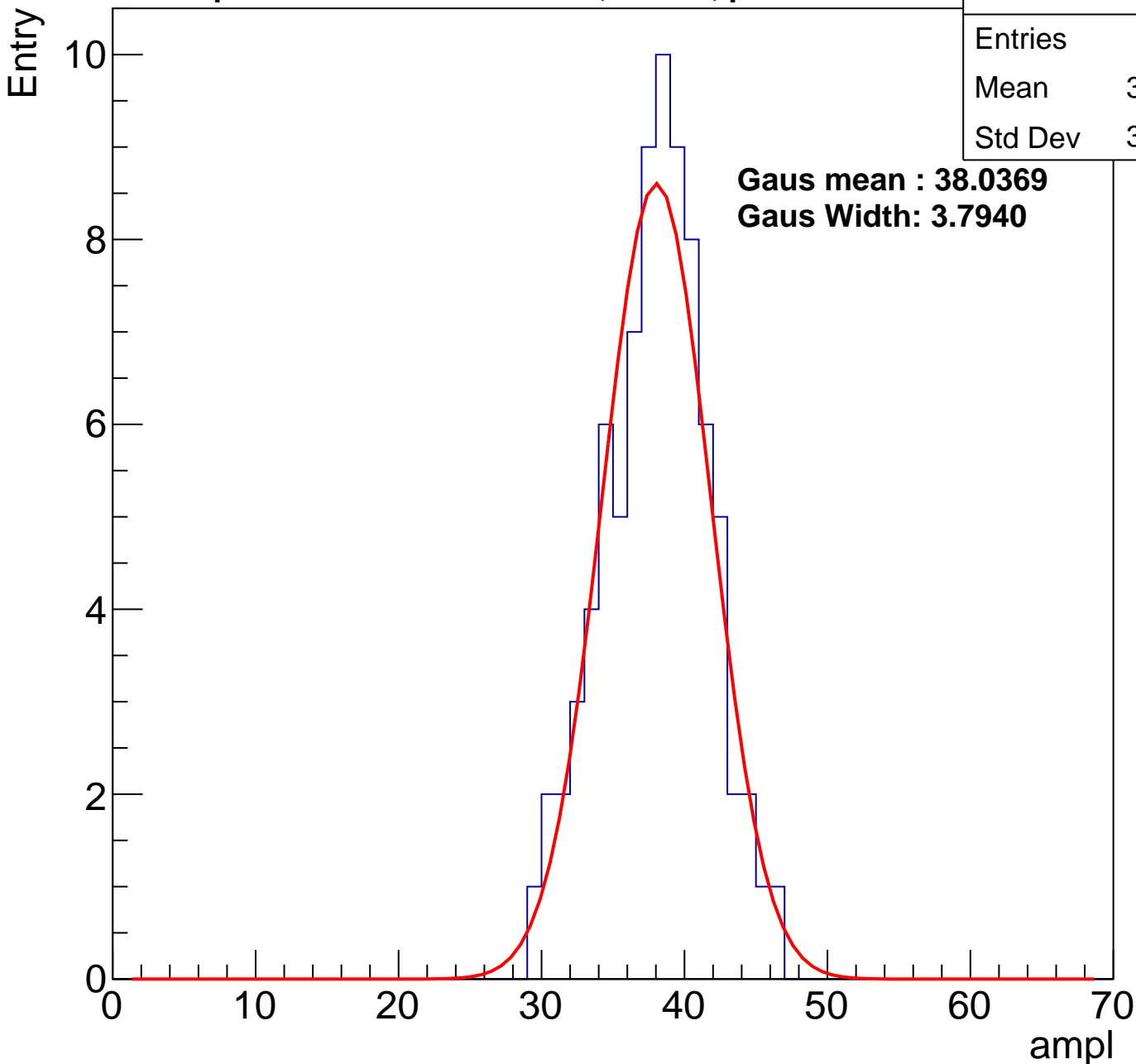


B1L003S, U19-ch126, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	37.53
Std Dev	3.615

Gaus mean : 38.0369
Gaus Width: 3.7940



B1L003S, U19-ch126, adc2

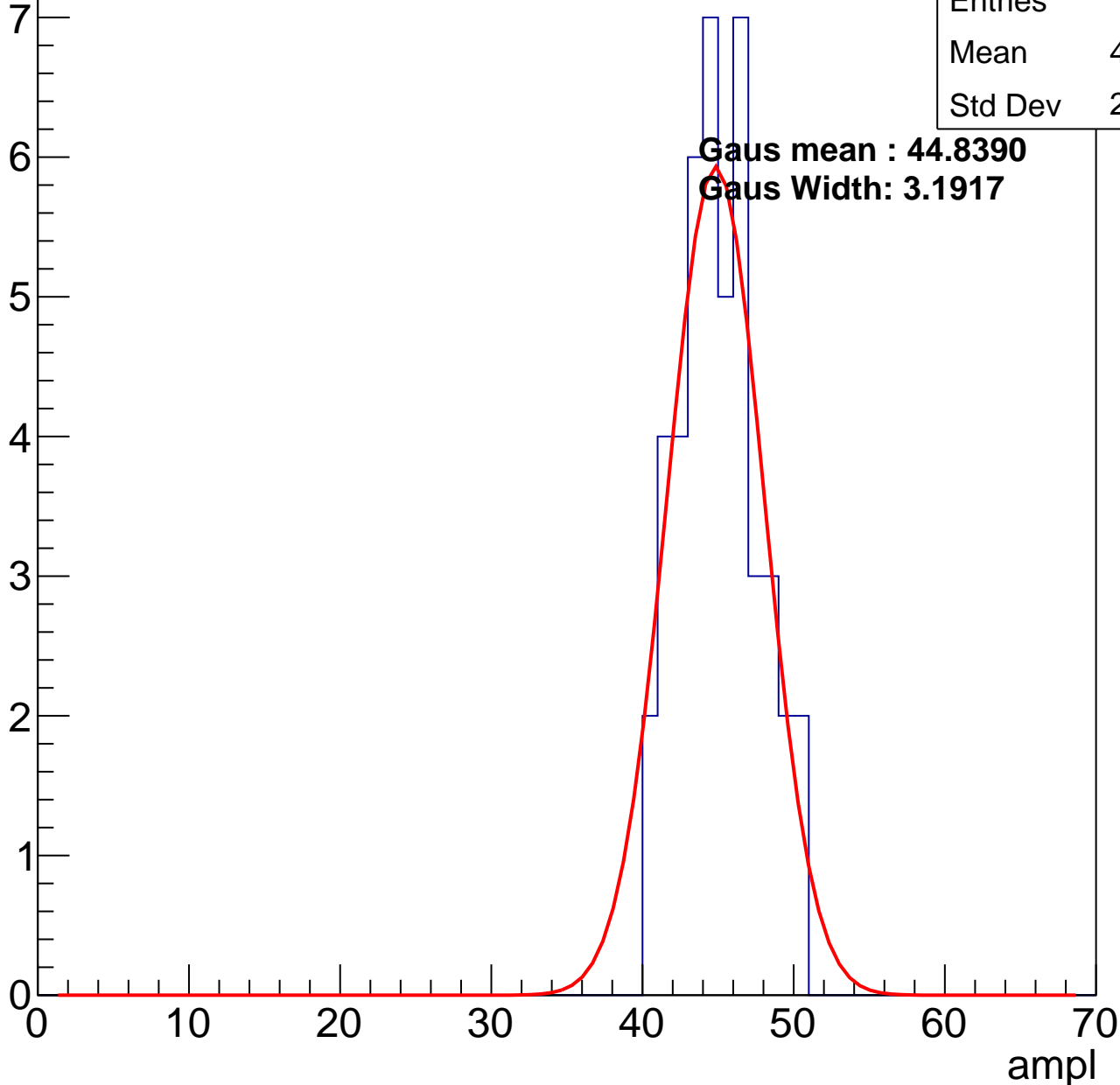
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	44.62
Std Dev	2.593

Gaus mean : 44.8390

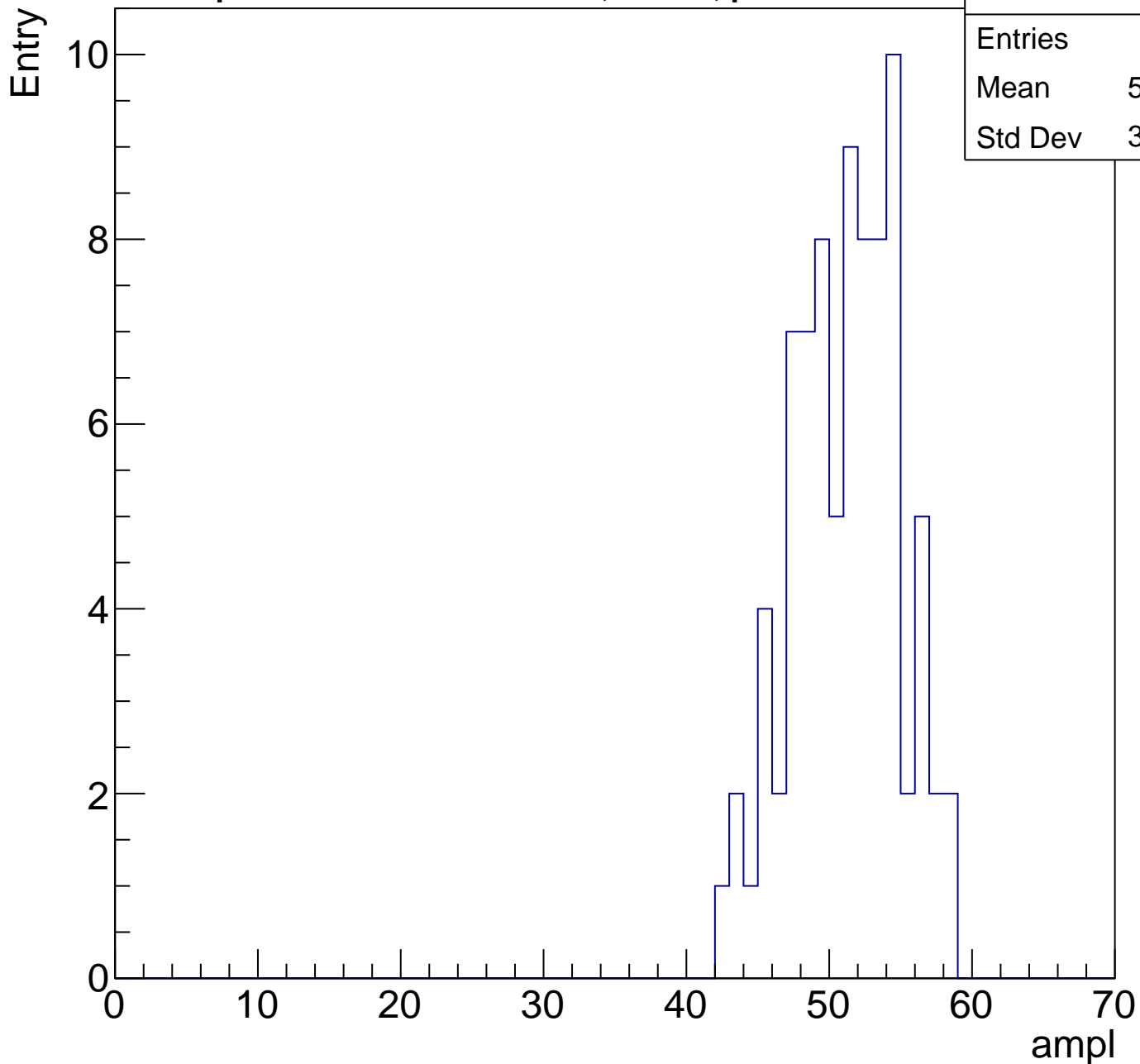
Gaus Width: 3.1917



B1L003S, U19-ch126, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	50.72
Std Dev	3.665

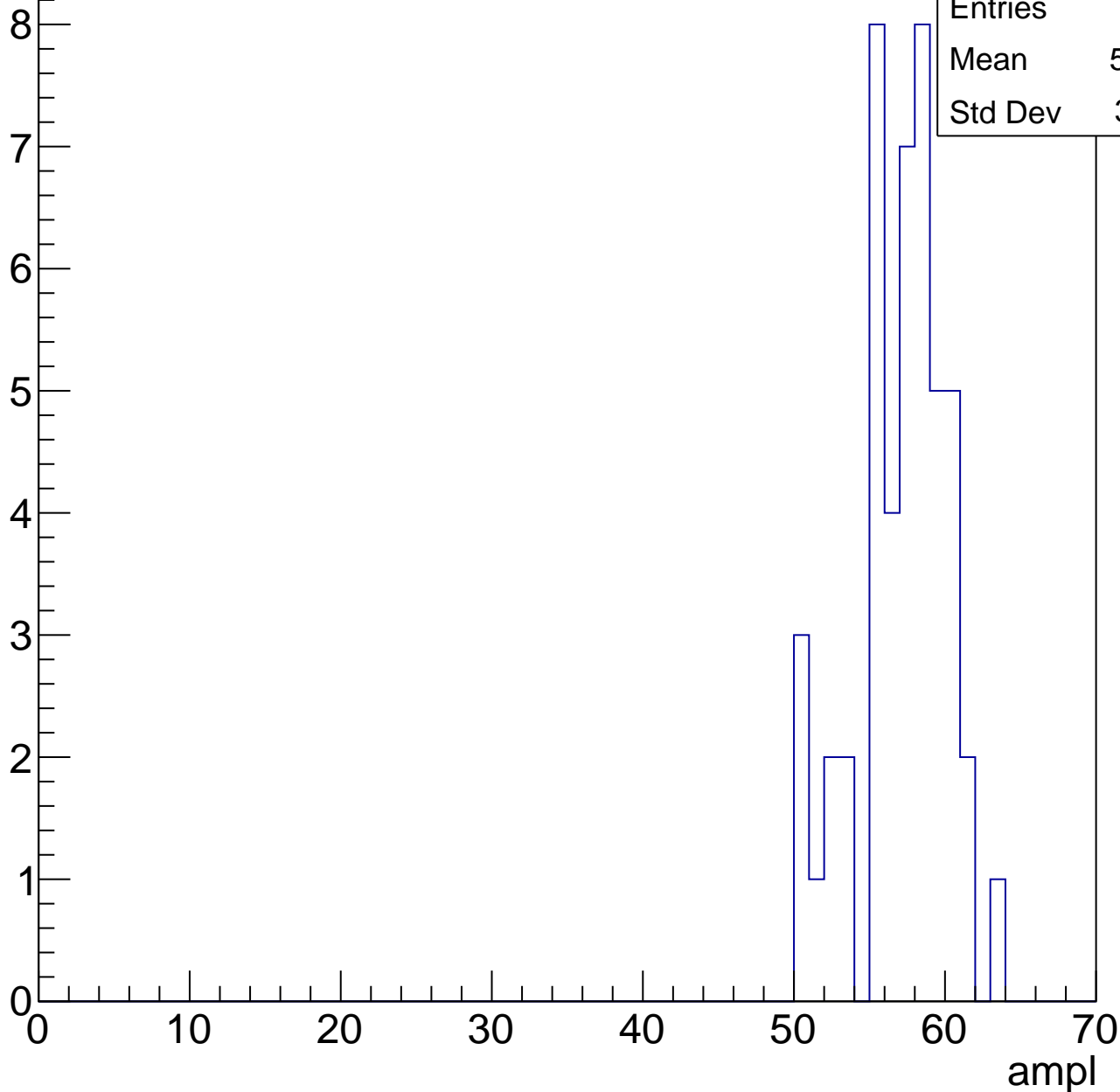


B1L003S, U19-ch126, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	56.62
Std Dev	3.011

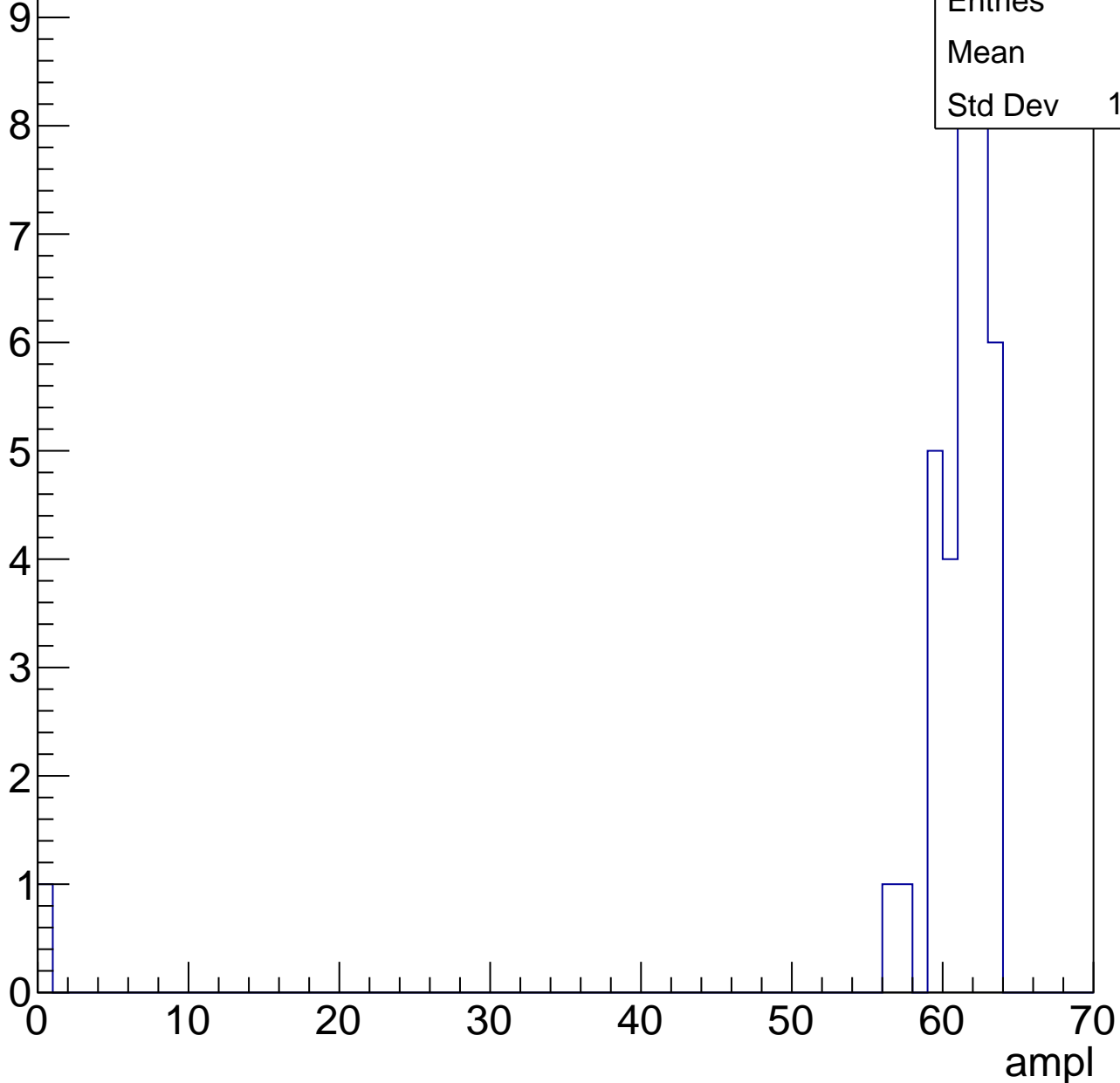


B1L003S, U19-ch126, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	59.2
Std Dev	10.29



B1L003S, U19-ch126, adc6

calib_packv5_042523_0143.root, FC#13, port D2

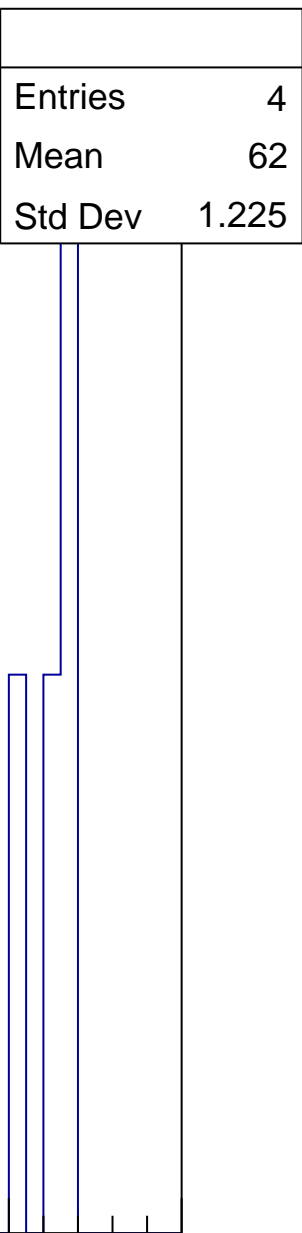
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

ampl



B1L003S, U19-ch126, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch127, adc0

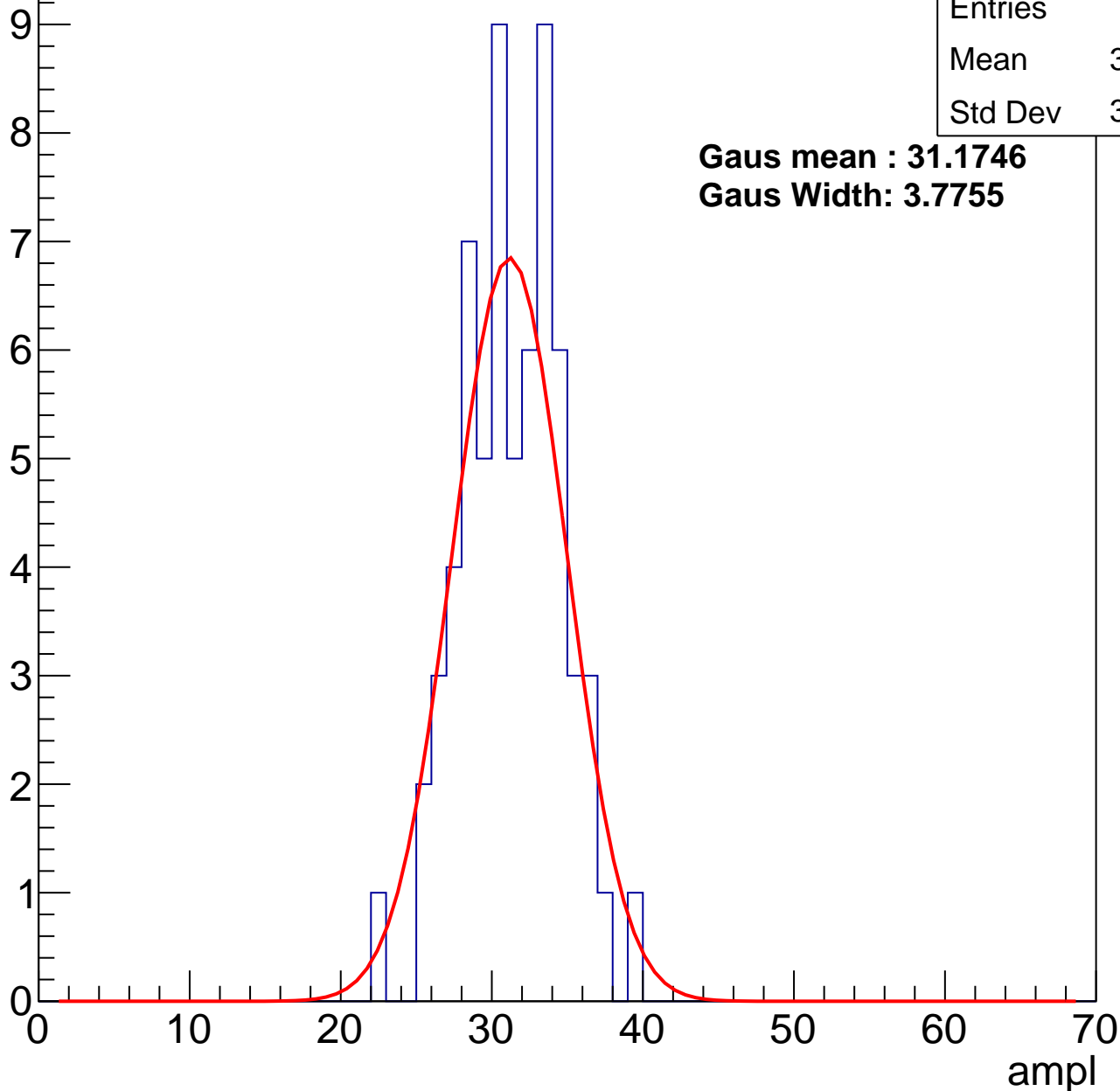
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.86
Std Dev	3.286

Gaus mean : 31.1746

Gaus Width: 3.7755



B1L003S, U19-ch127, adc1

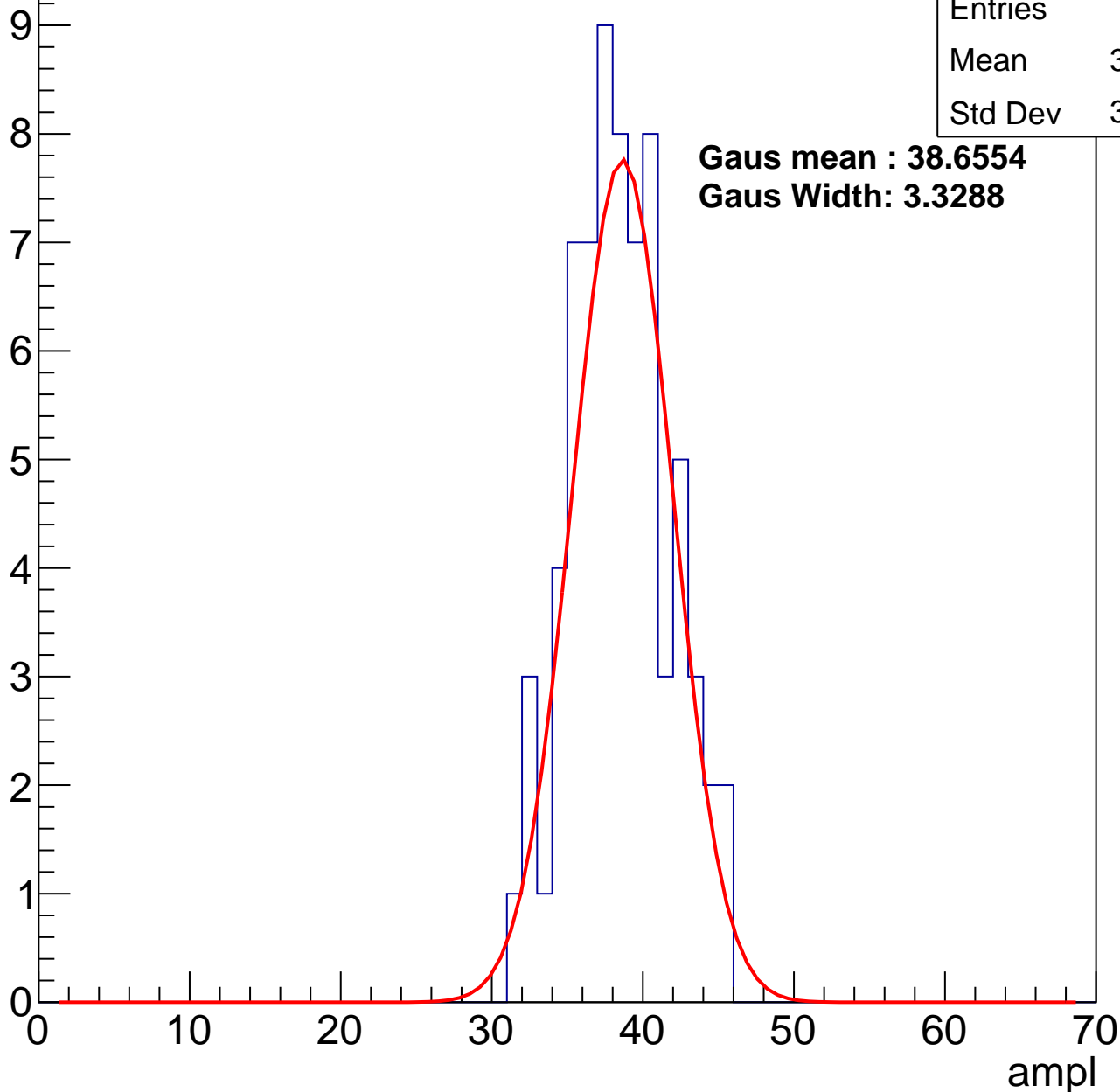
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	38.04
Std Dev	3.244

Gaus mean : 38.6554

Gaus Width: 3.3288



B1L003S, U19-ch127, adc2

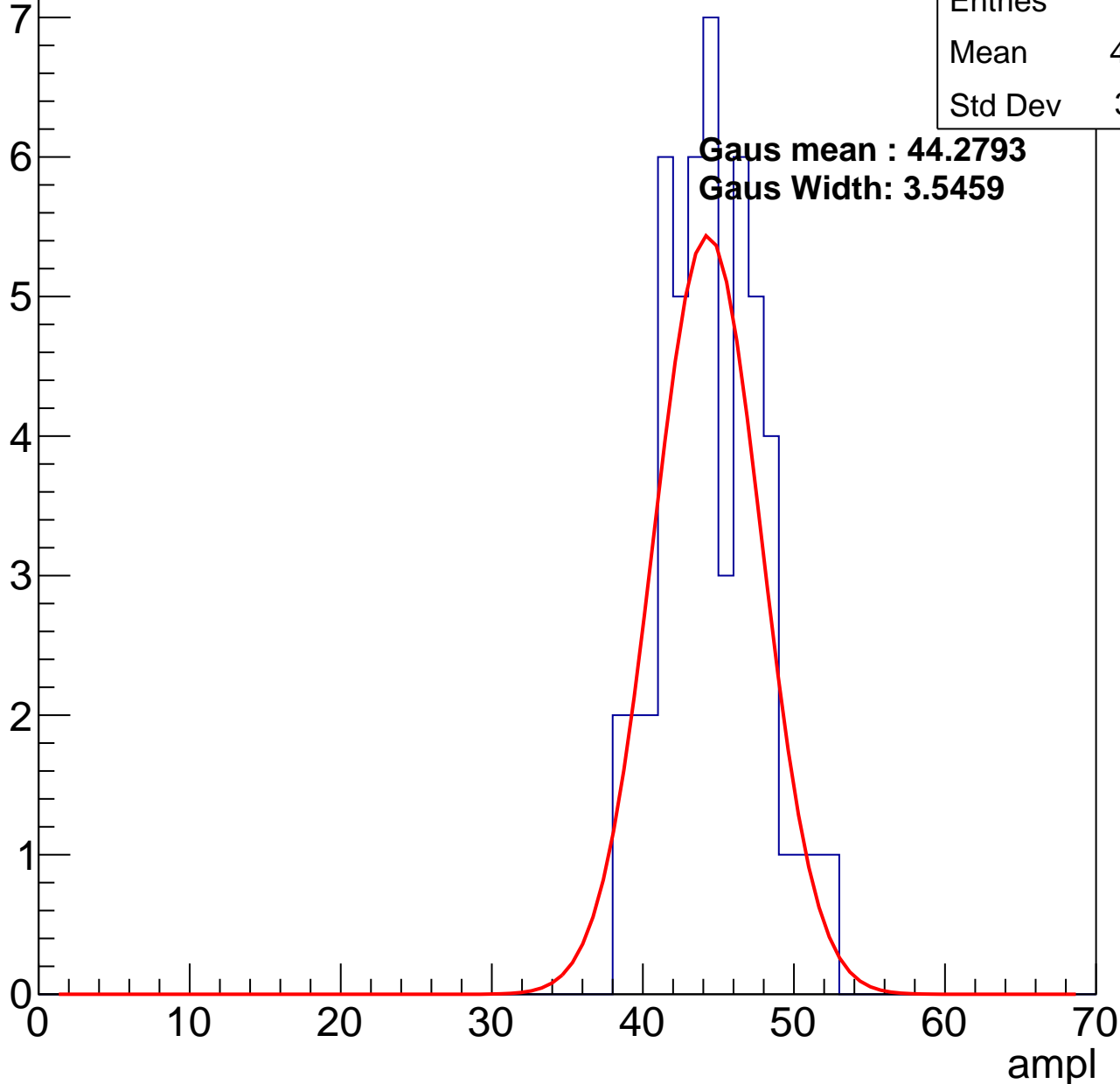
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	44.15
Std Dev	3.231

Gaus mean : 44.2793

Gaus Width: 3.5459

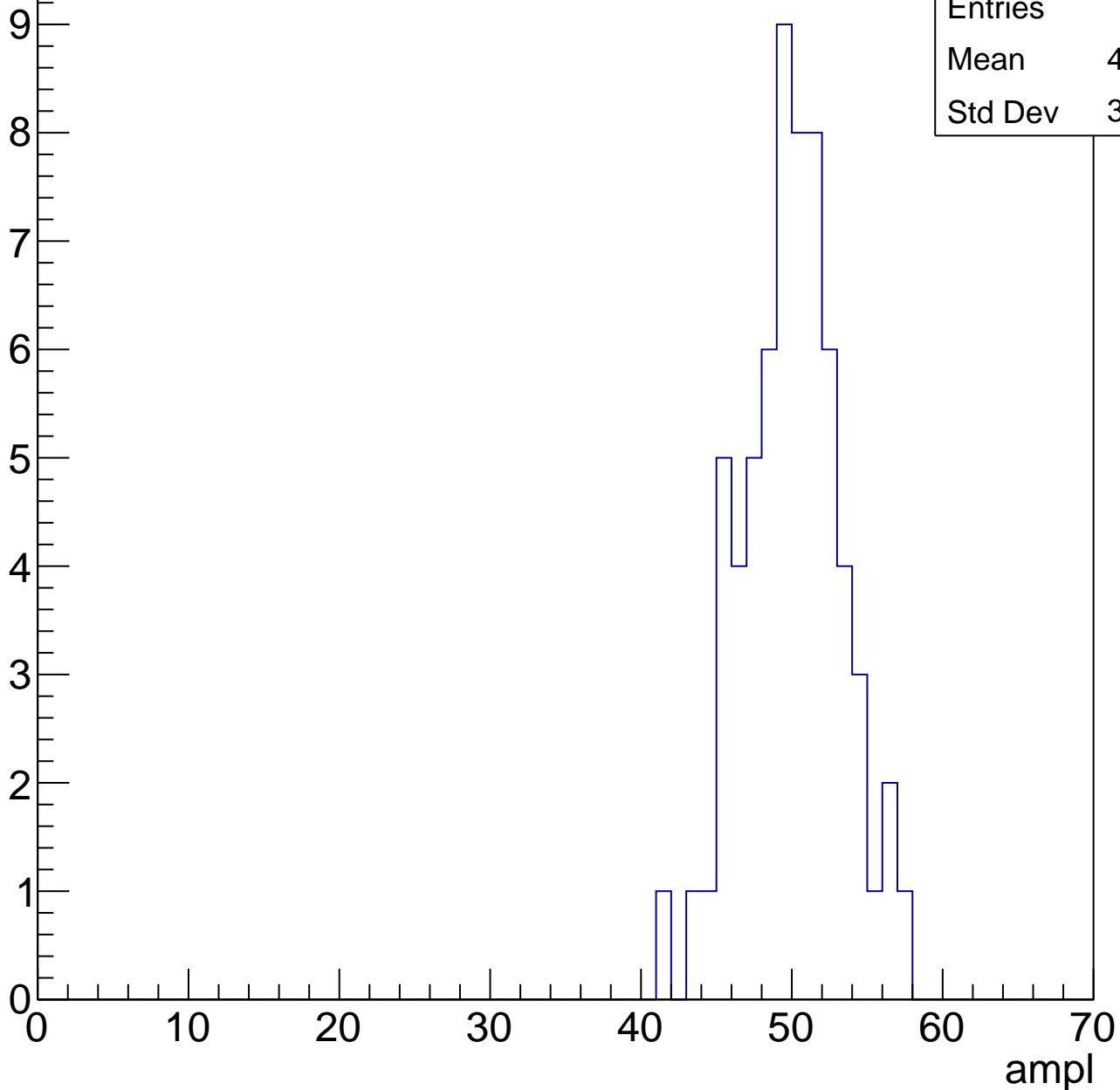


B1L003S, U19-ch127, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	49.52
Std Dev	3.235

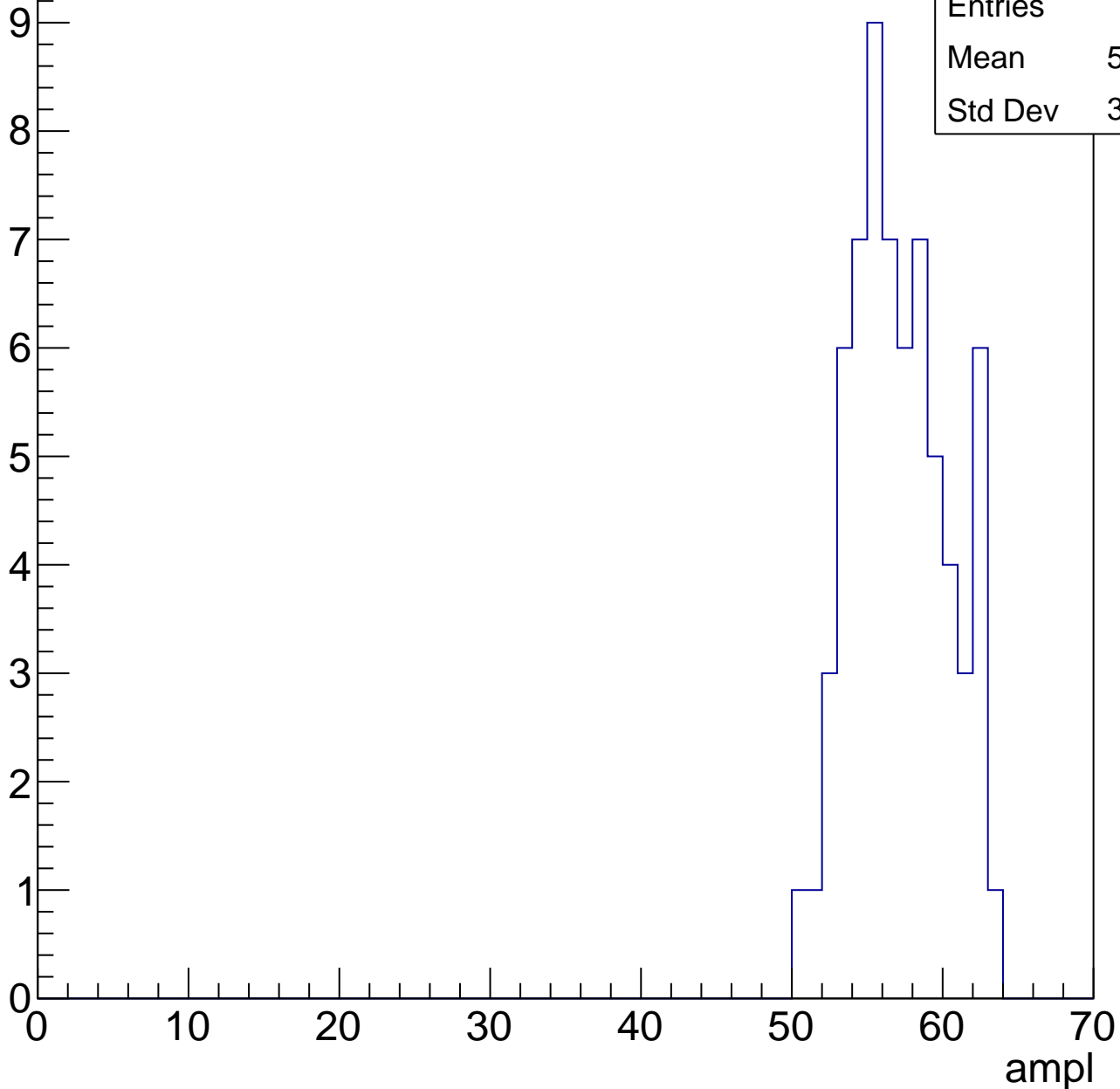


B1L003S, U19-ch127, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	56.68
Std Dev	3.144

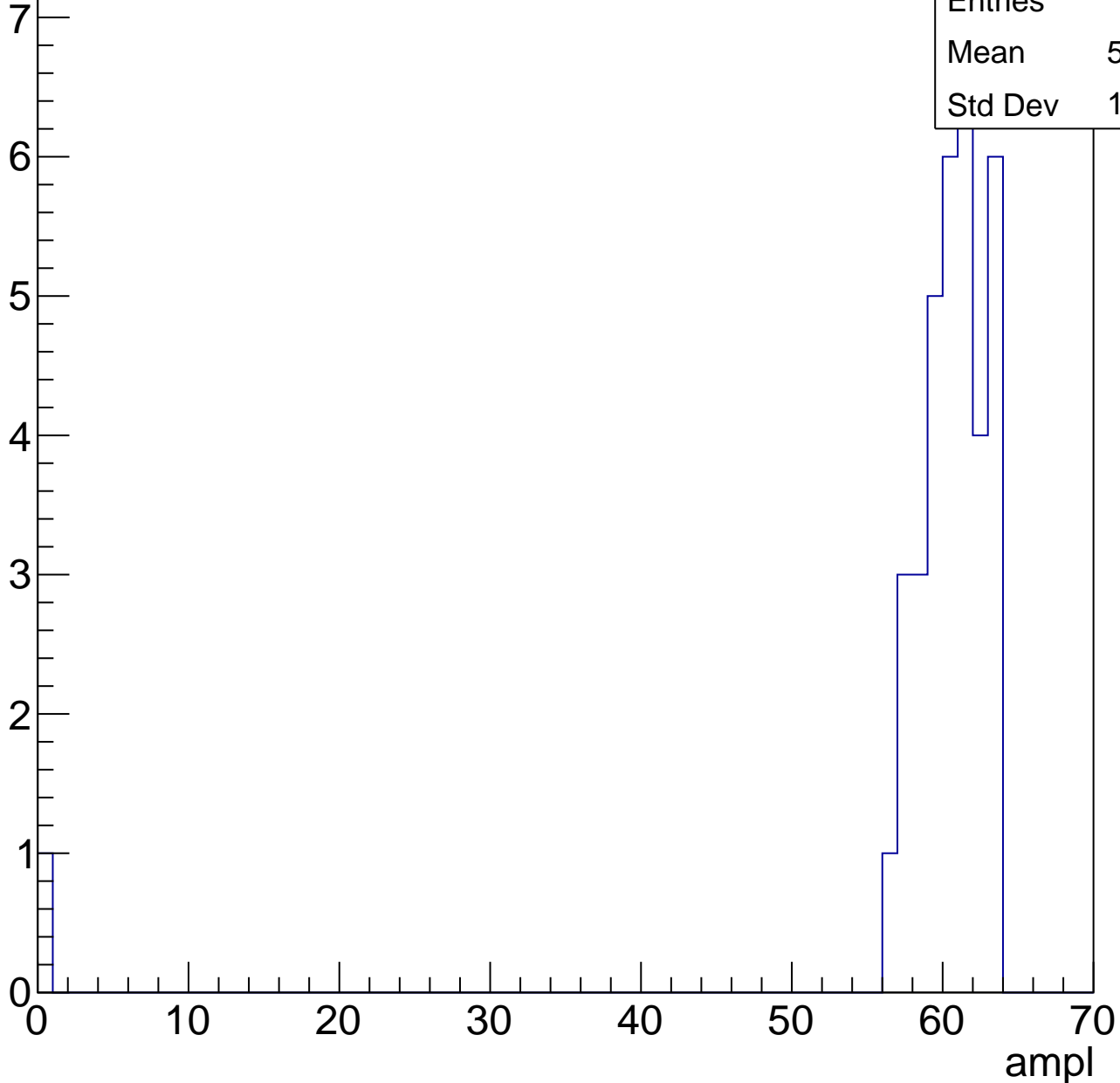


B1L003S, U19-ch127, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	58.58
Std Dev	10.09



B1L003S, U19-ch127, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

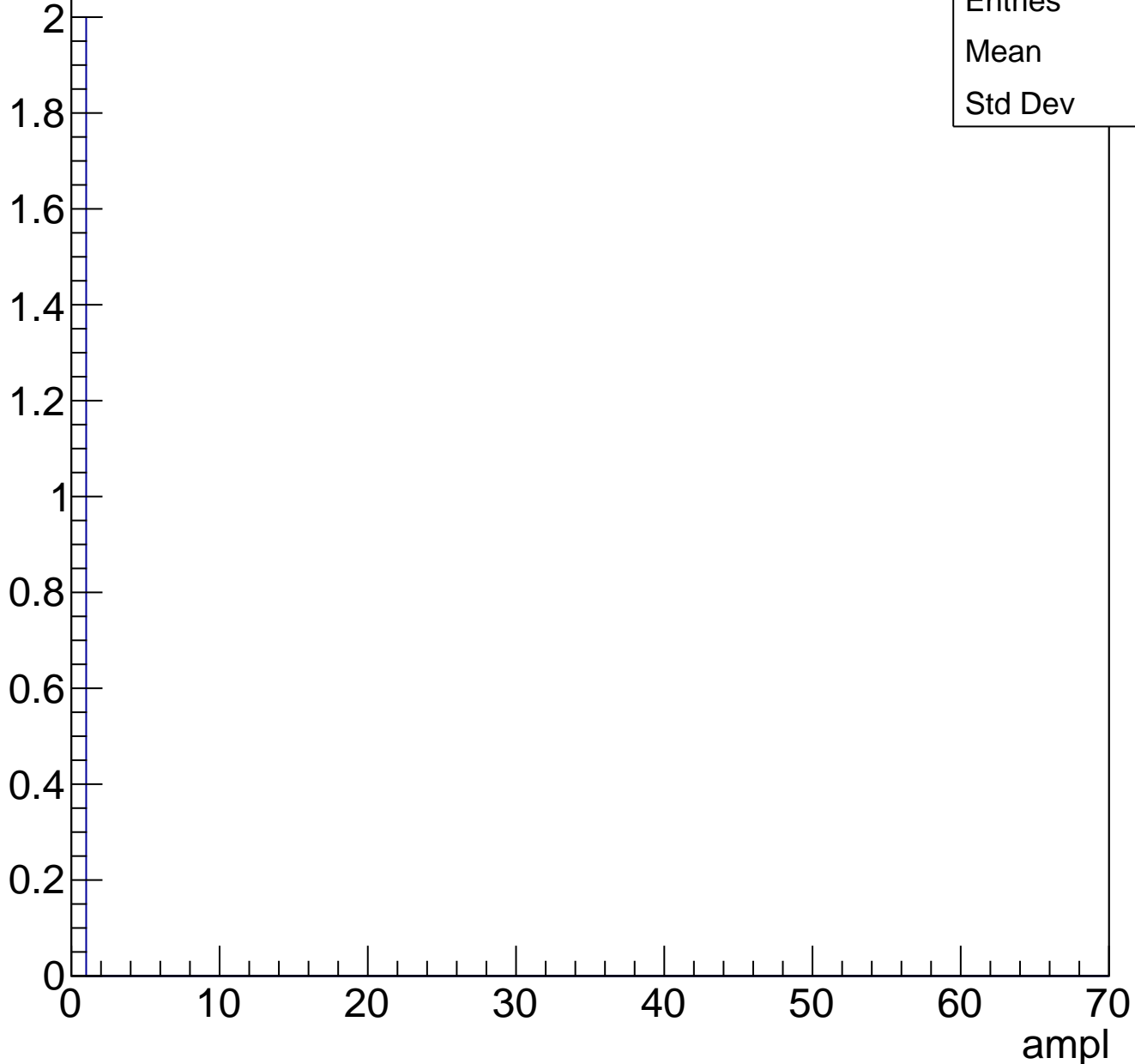


Entries	2
Mean	0
Std Dev	0

B1L003S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0