



# B0L102S, U18-ch0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.14
Std Dev	11.55

**Turn on : 26.1798**

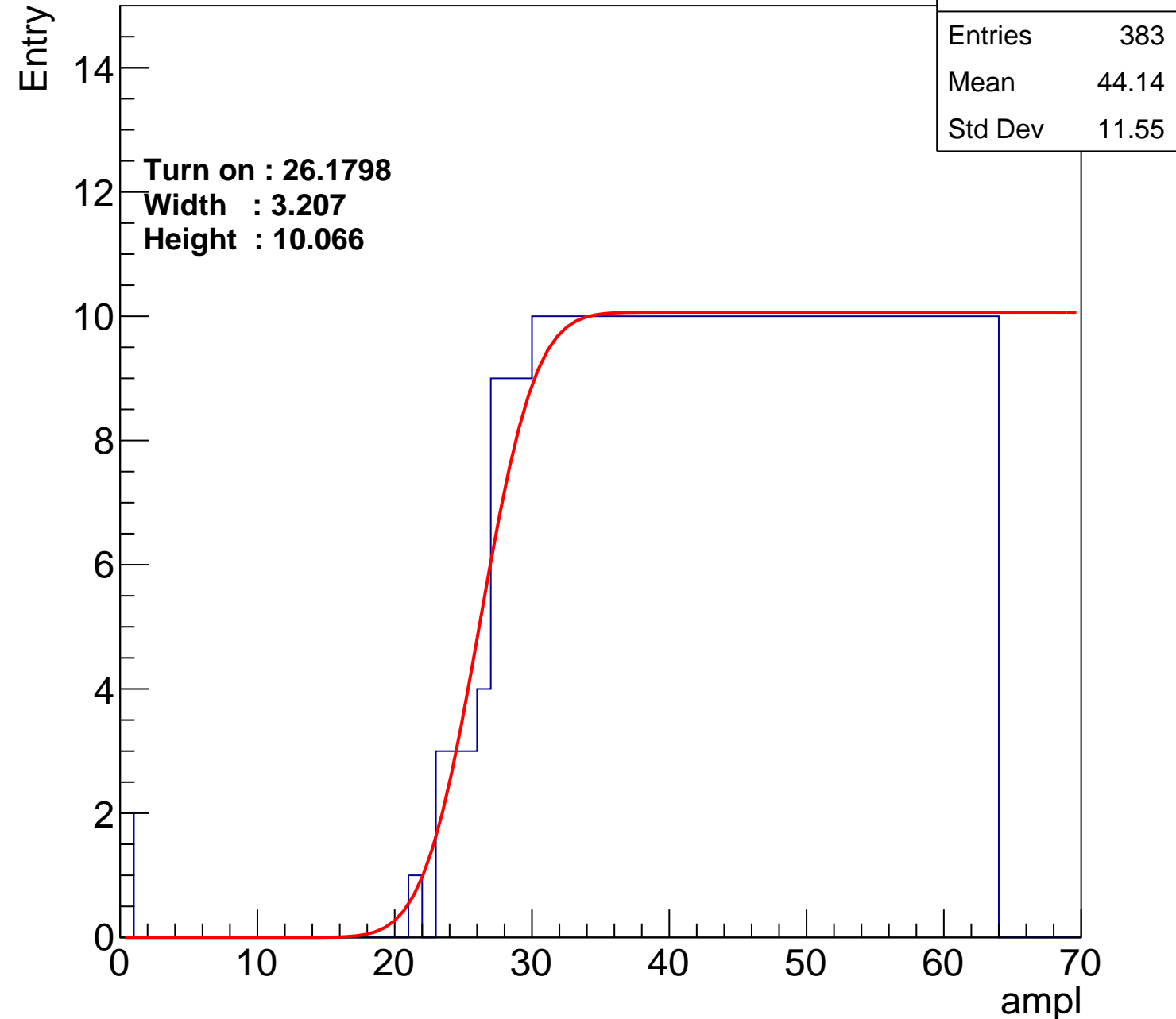
**Width : 3.207**

**Height : 10.066**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 27.0060

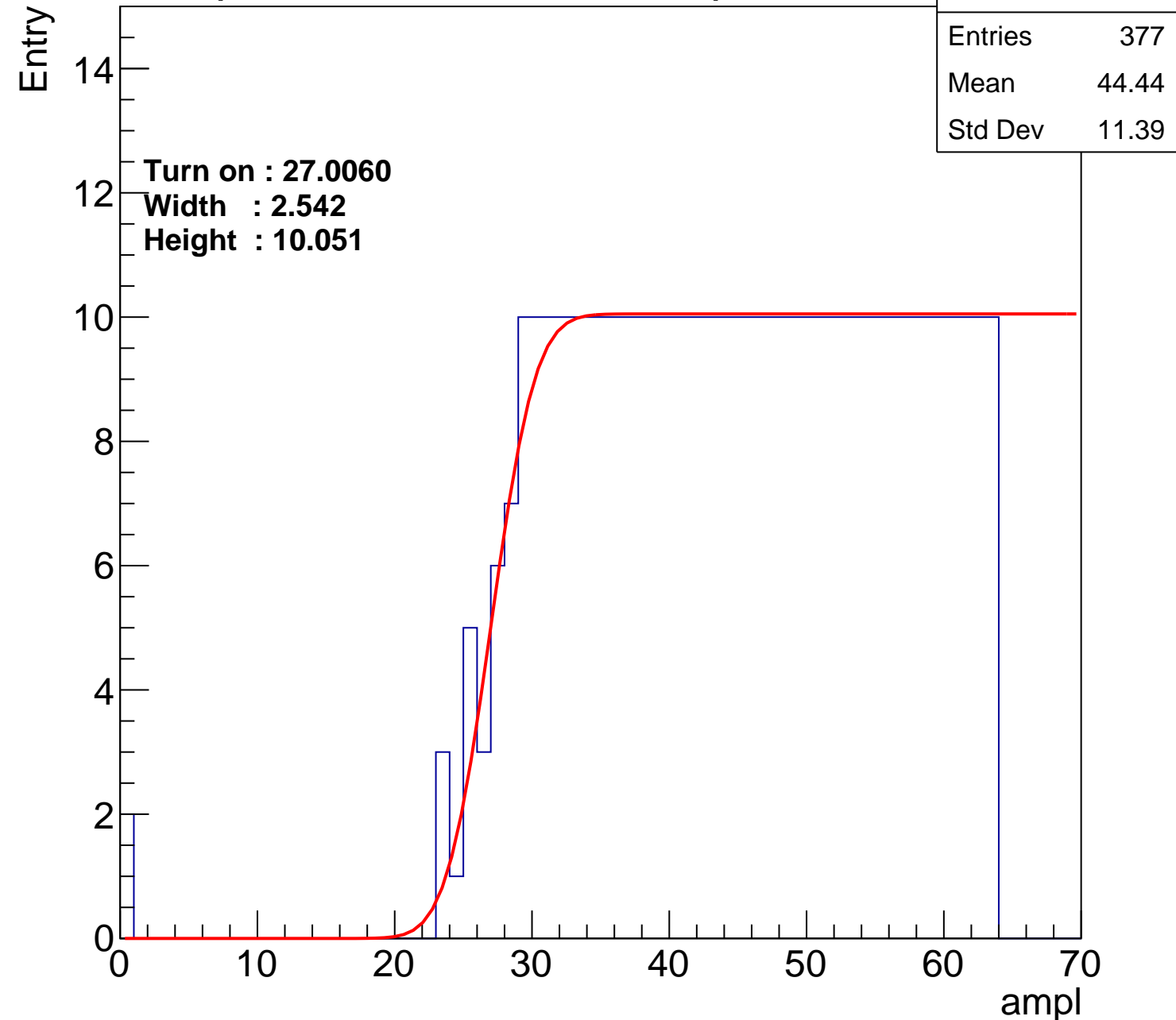
Width : 2.542

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	44.11
Std Dev	11.92

Turn on : 25.7522

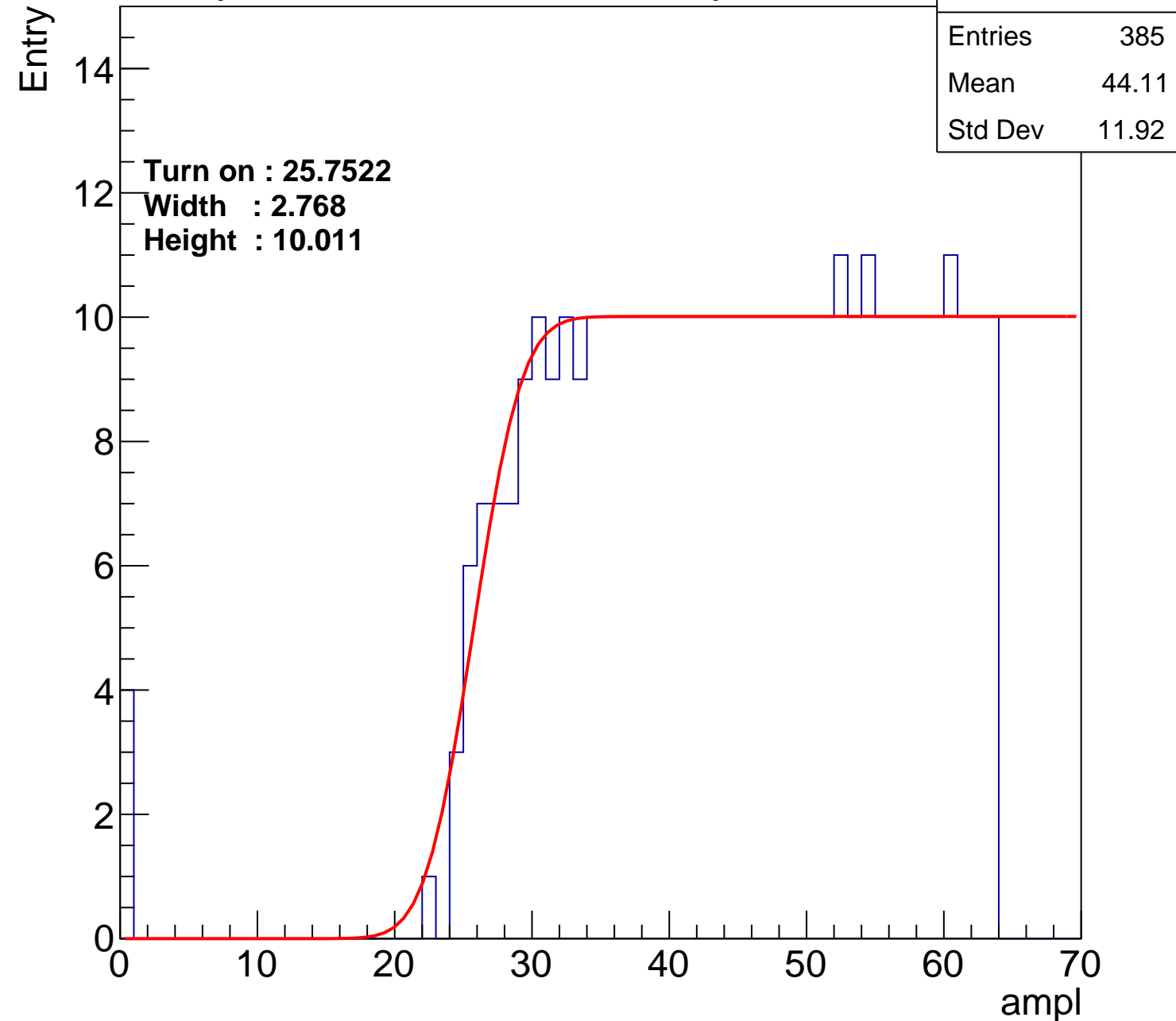
Width : 2.768

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.29
Std Dev	11.71

Turn on : 26.7856

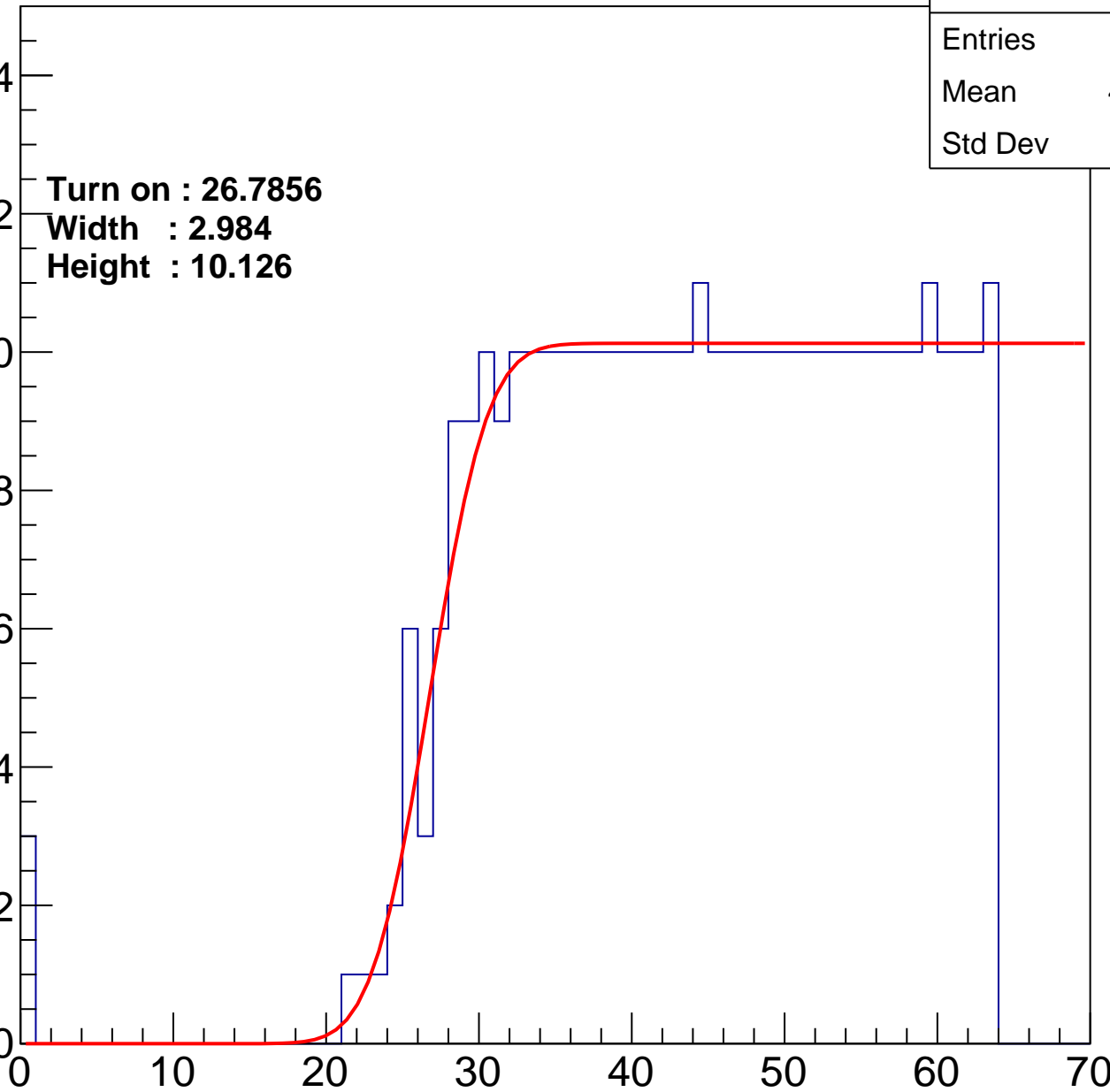
Width : 2.984

Height : 10.126

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.08
Std Dev	11.78

Turn on : 26.3706

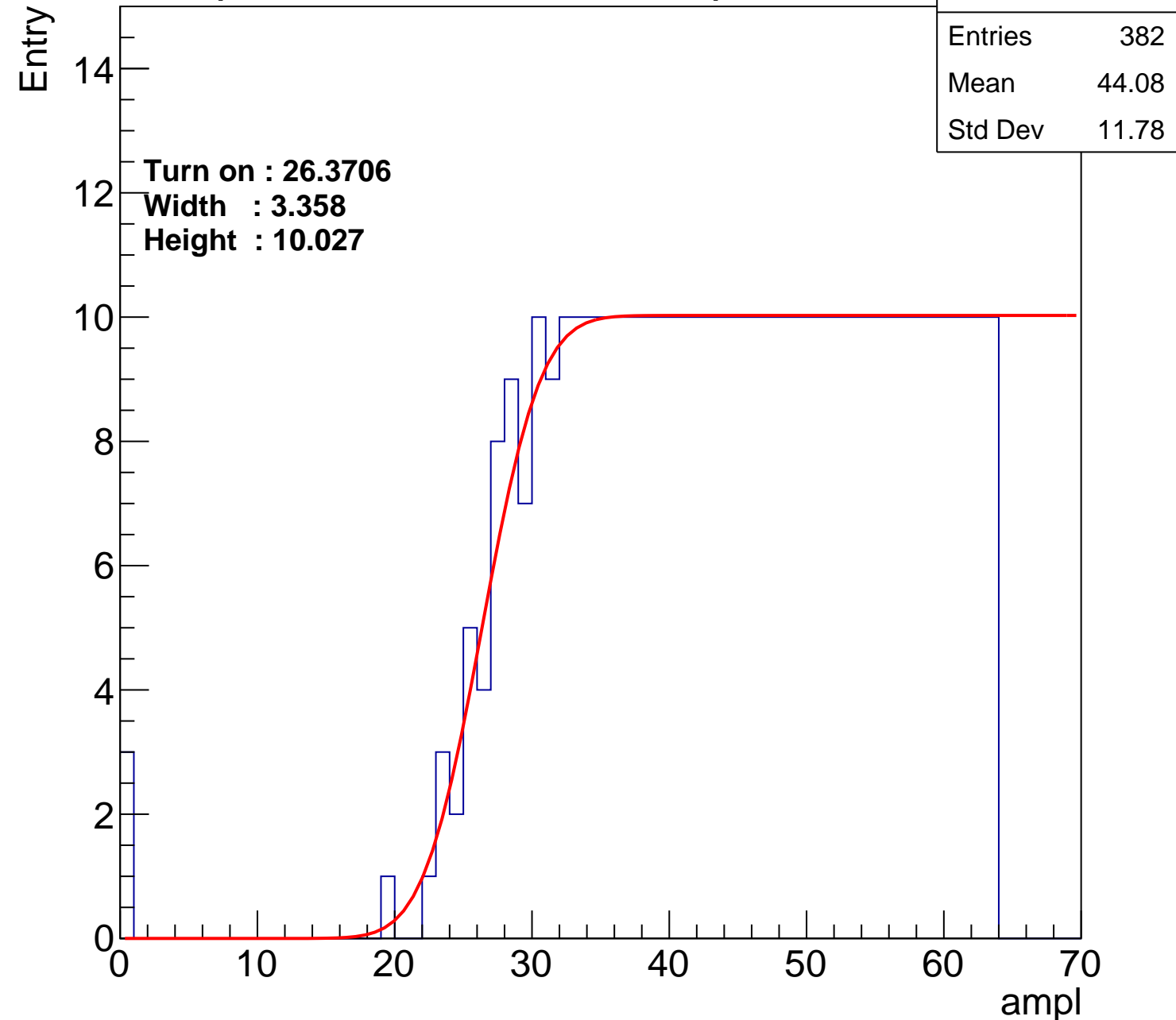
Width : 3.358

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.82
Std Dev	11.21

**Turn on : 27.6268**

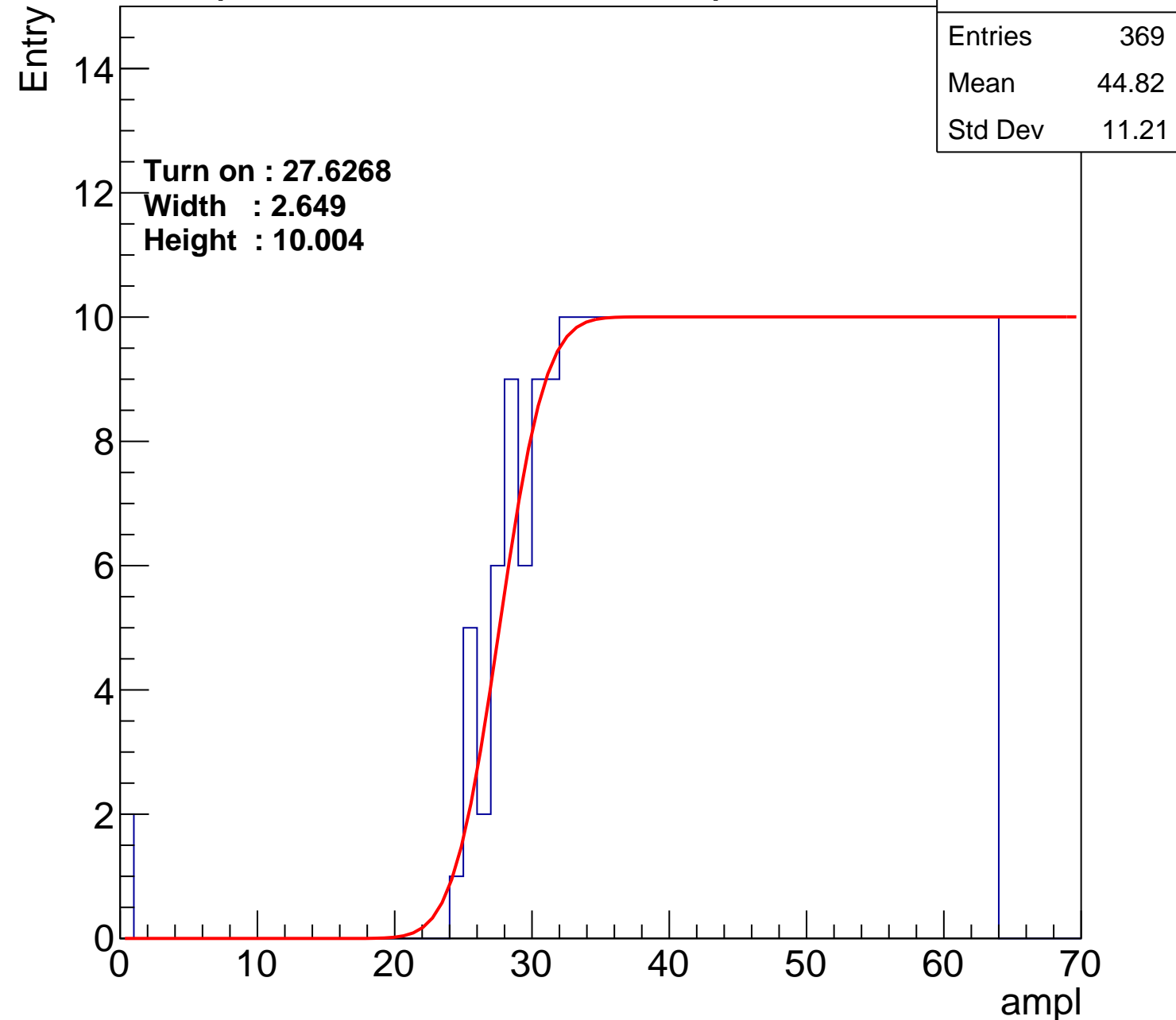
**Width : 2.649**

**Height : 10.004**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.11
Std Dev	11.56

**Turn on : 25.8707**

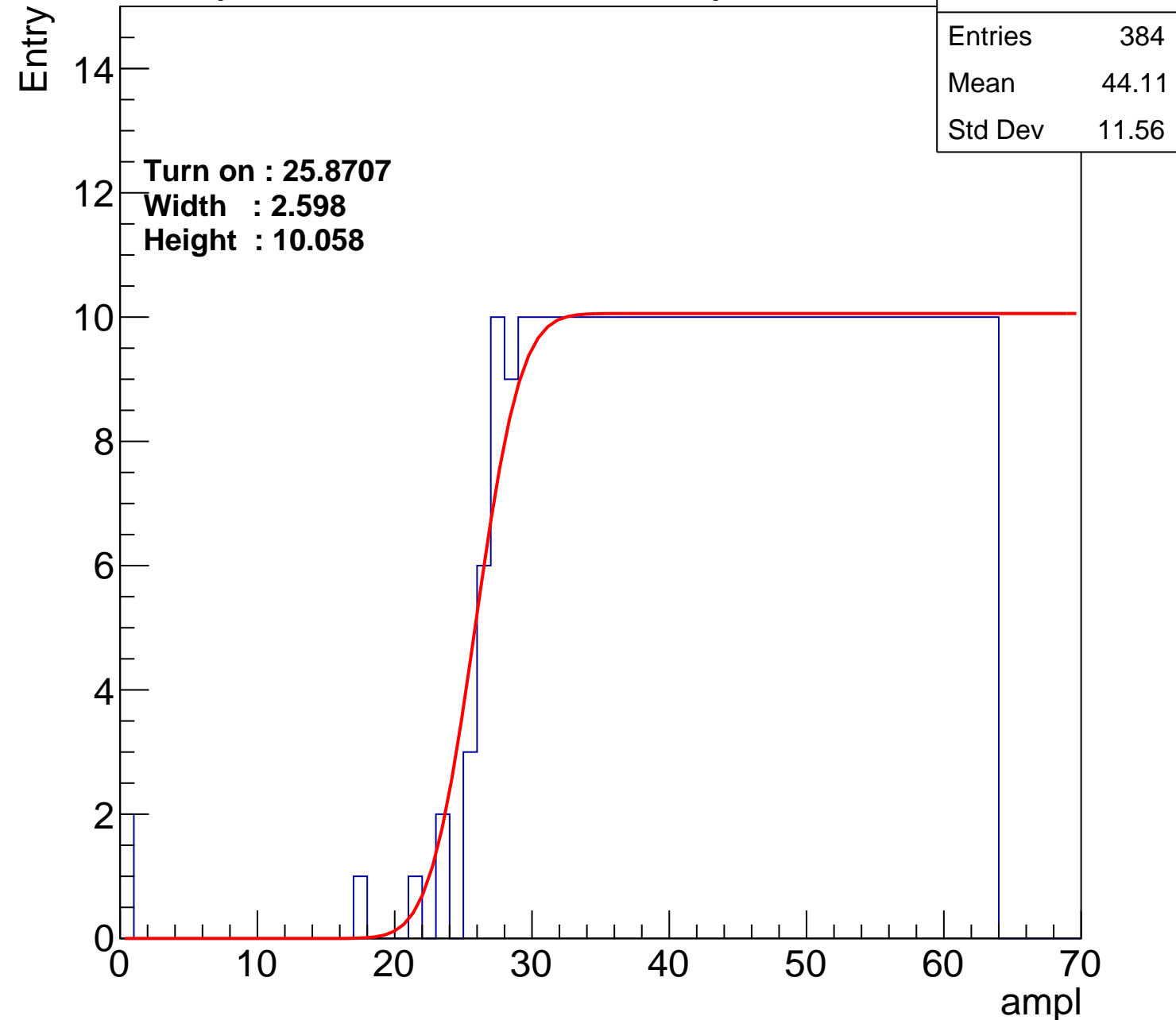
**Width : 2.598**

**Height : 10.058**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch7

calib\_packv5\_042523\_0143.root, FC#12, port B1

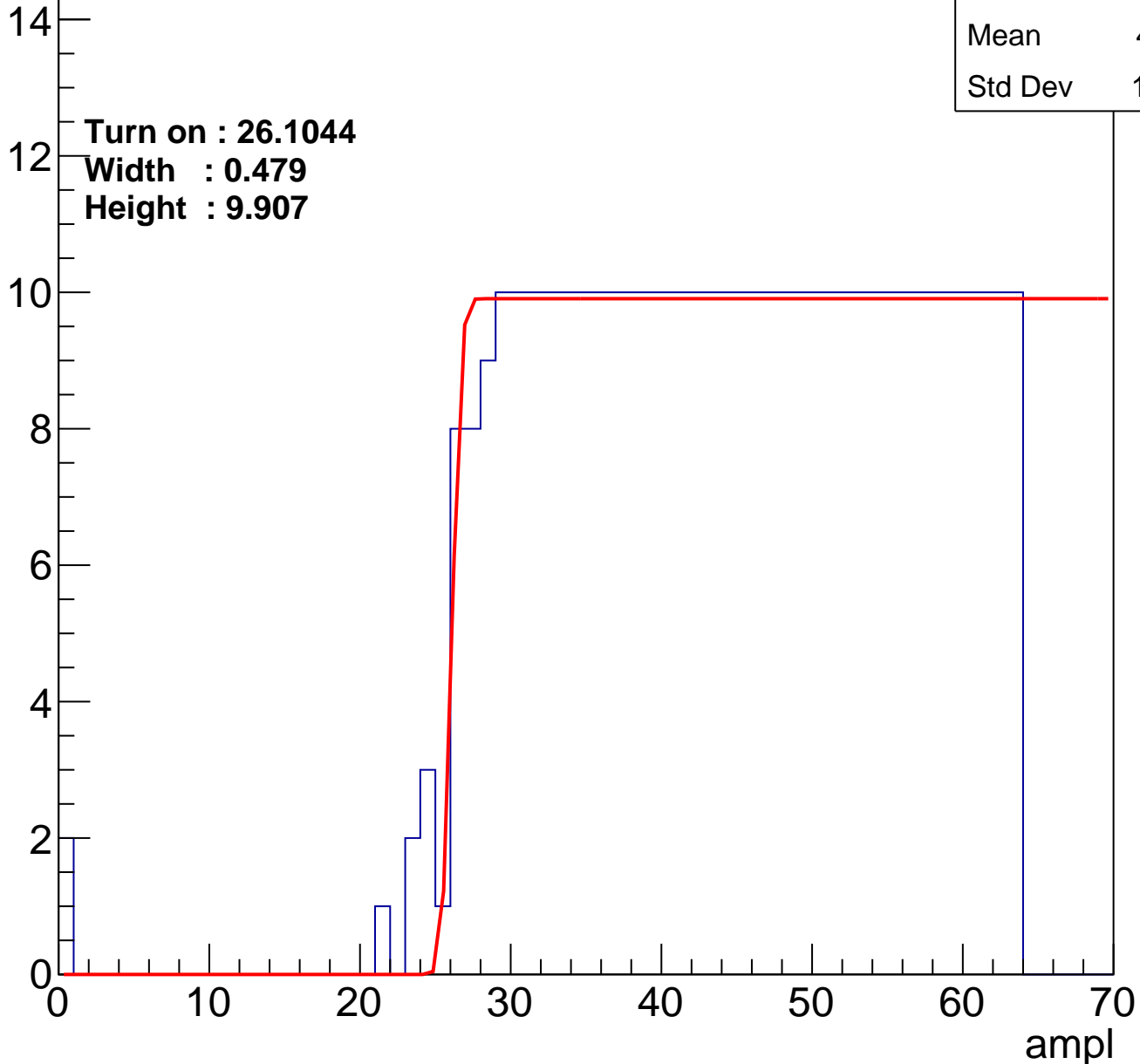
Entry

Entries	384
Mean	44.11
Std Dev	11.54

Turn on : 26.1044

Width : 0.479

Height : 9.907



# B0L102S, U18-ch8

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.1
Std Dev	11.74

**Turn on : 26.4918**

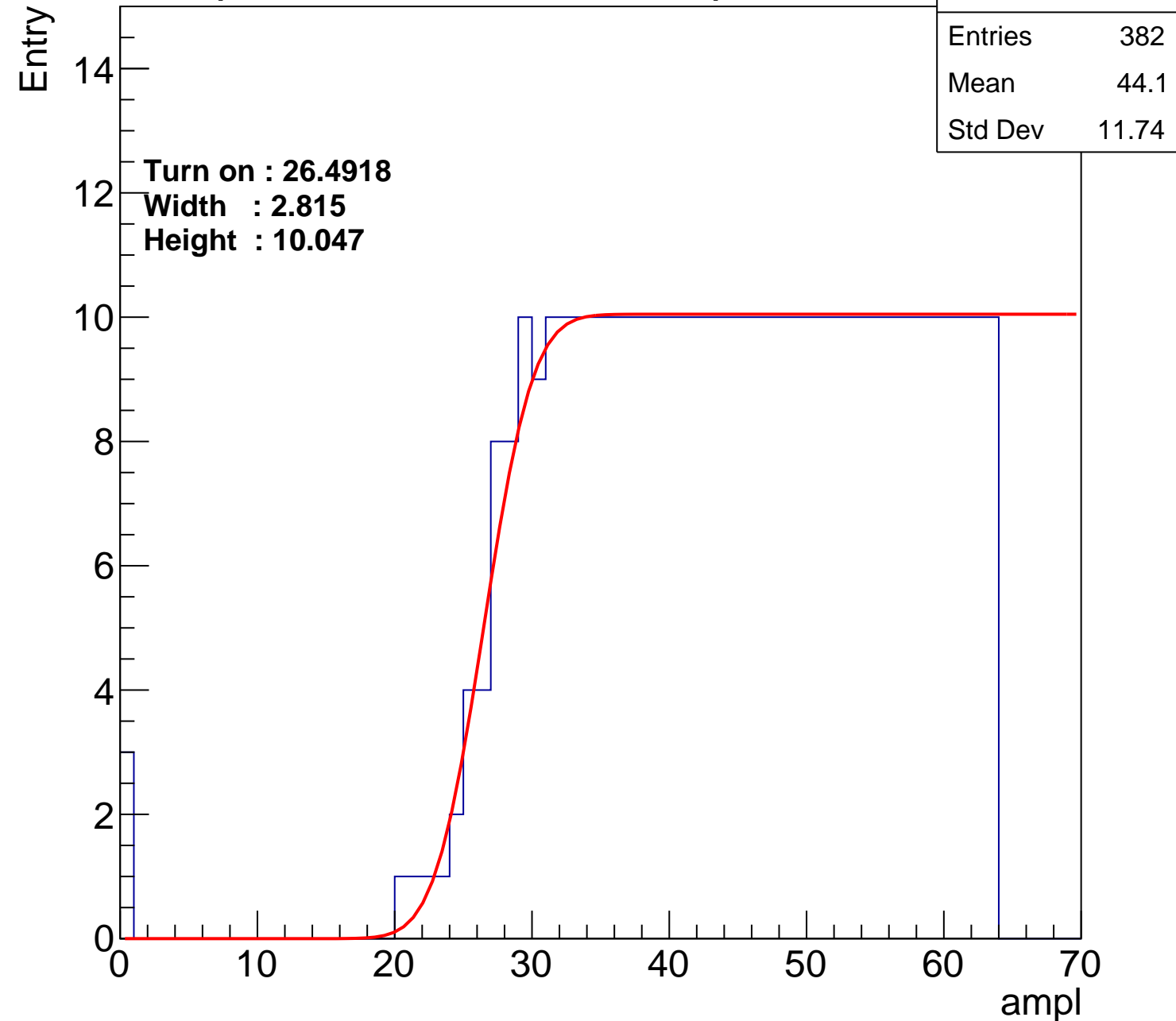
**Width : 2.815**

**Height : 10.047**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch9

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.82
Std Dev	11.4

Turn on : 28.2593

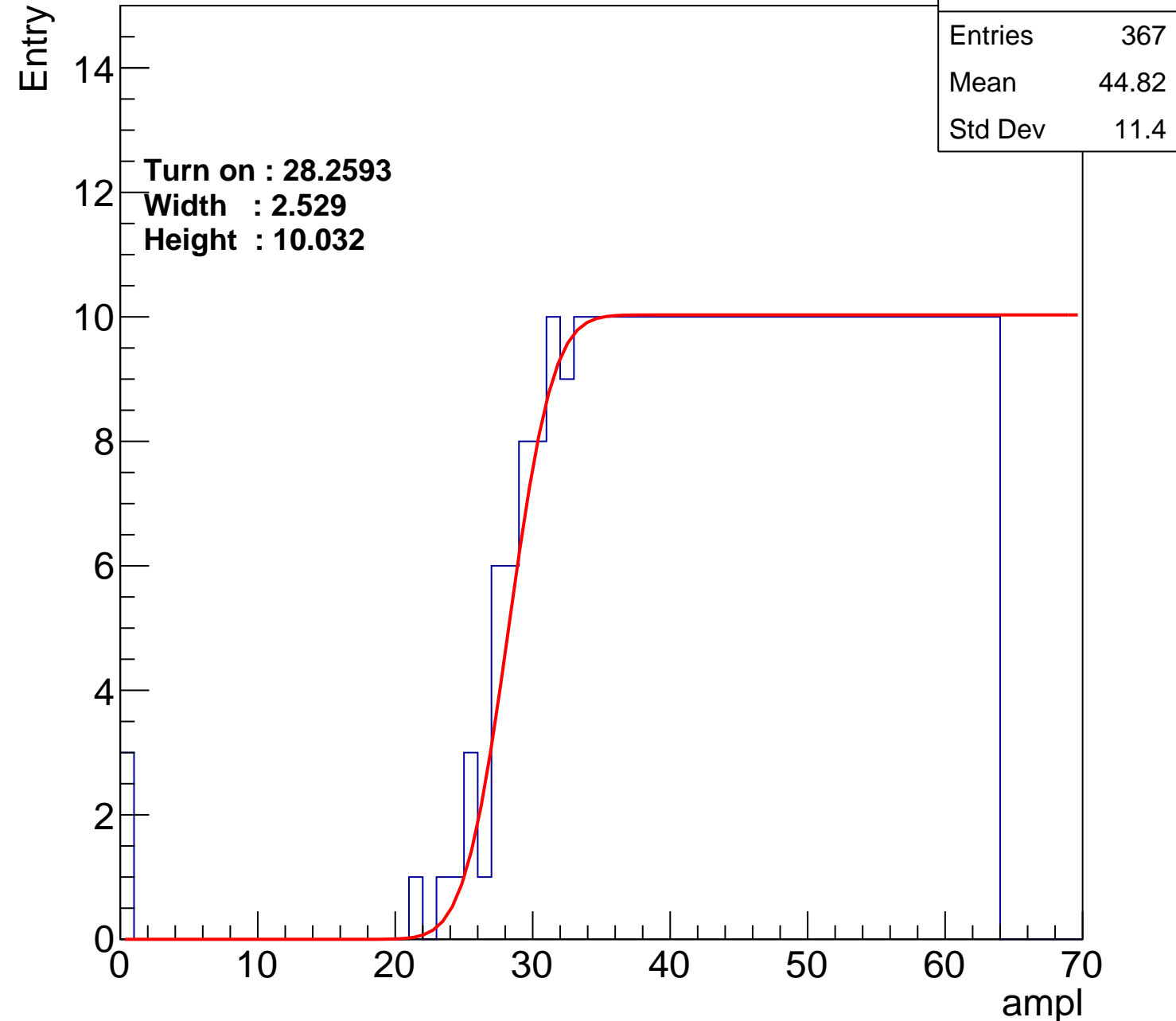
Width : 2.529

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch10

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	398
Mean	43.45
Std Dev	11.8

Turn on : 24.6436

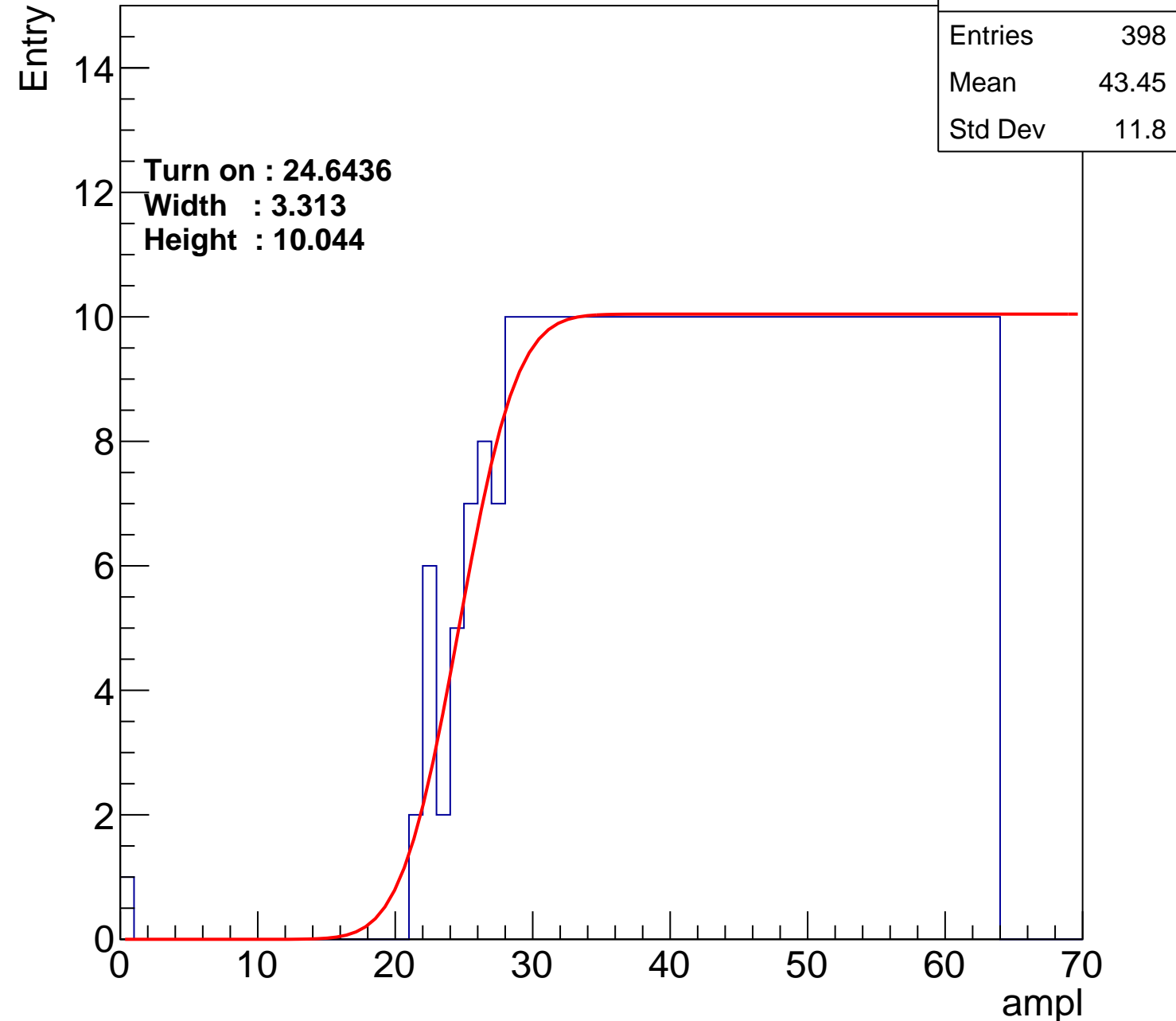
Width : 3.313

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch11

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.9
Std Dev	11.54

Turn on : 25.3893

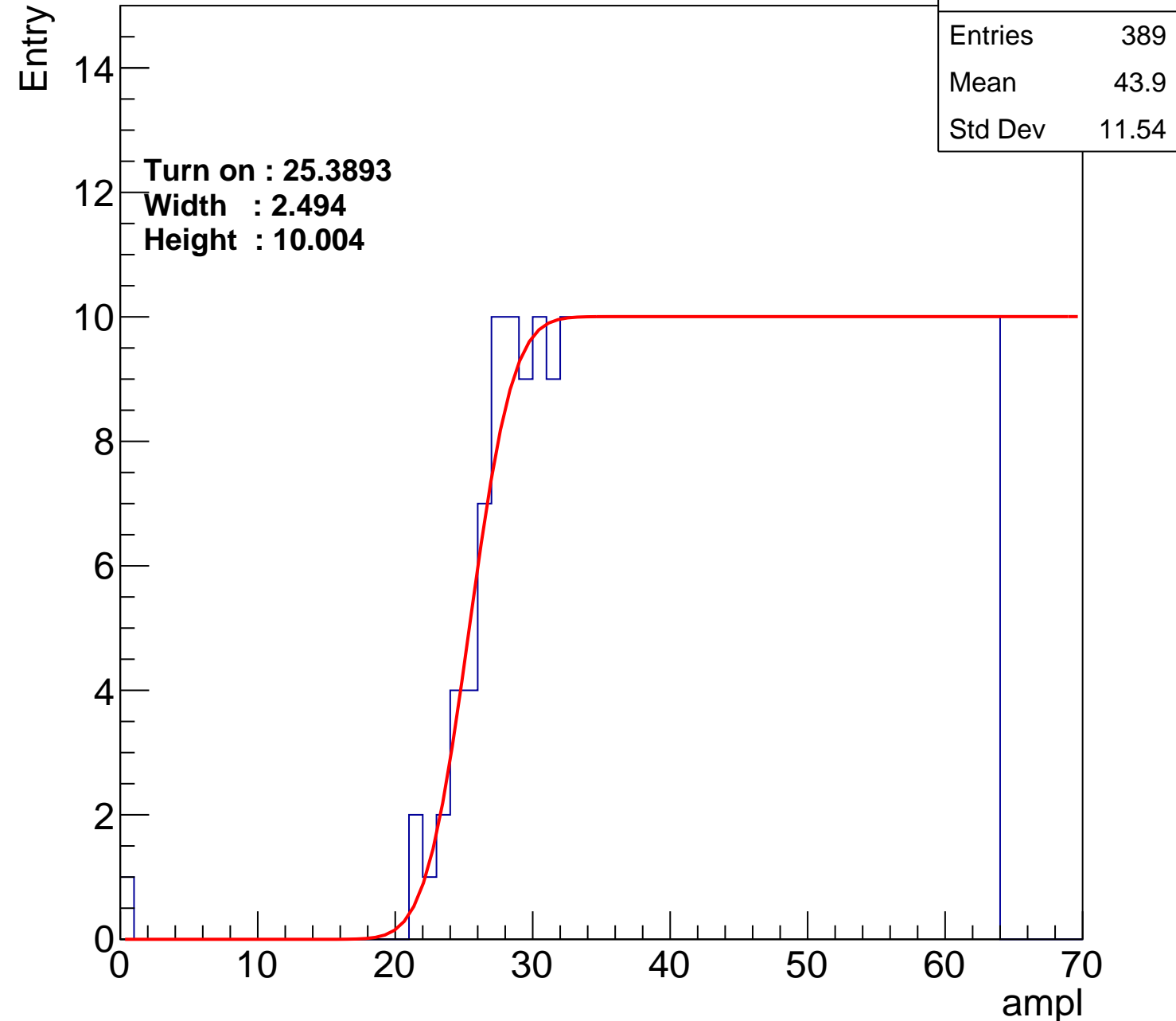
Width : 2.494

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch12

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	361
Mean	45.07
Std Dev	11.63

Turn on : 28.2493

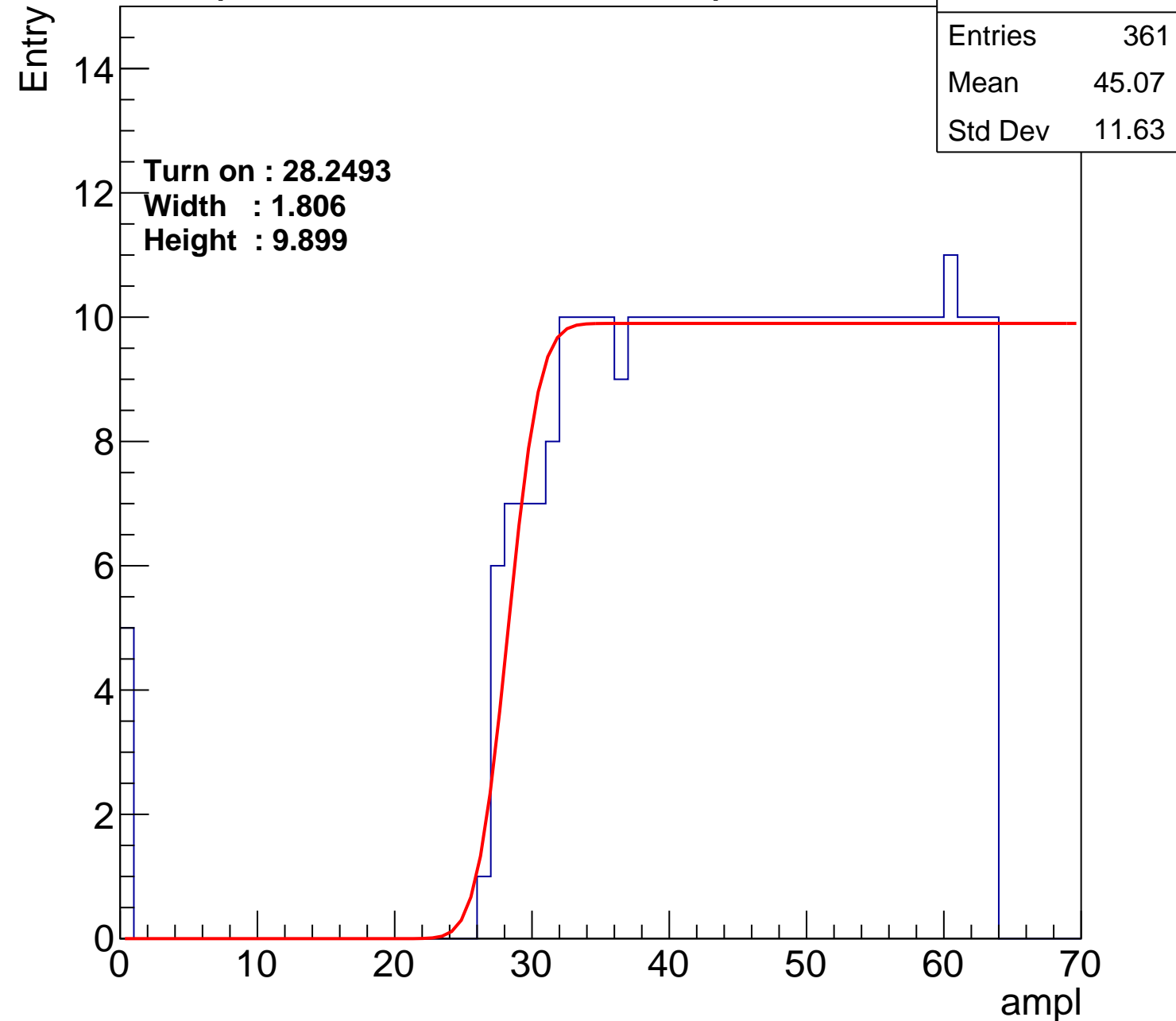
Width : 1.806

Height : 9.899

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch13

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.44
Std Dev	11.27

Turn on : 26.5056

Width : 3.282

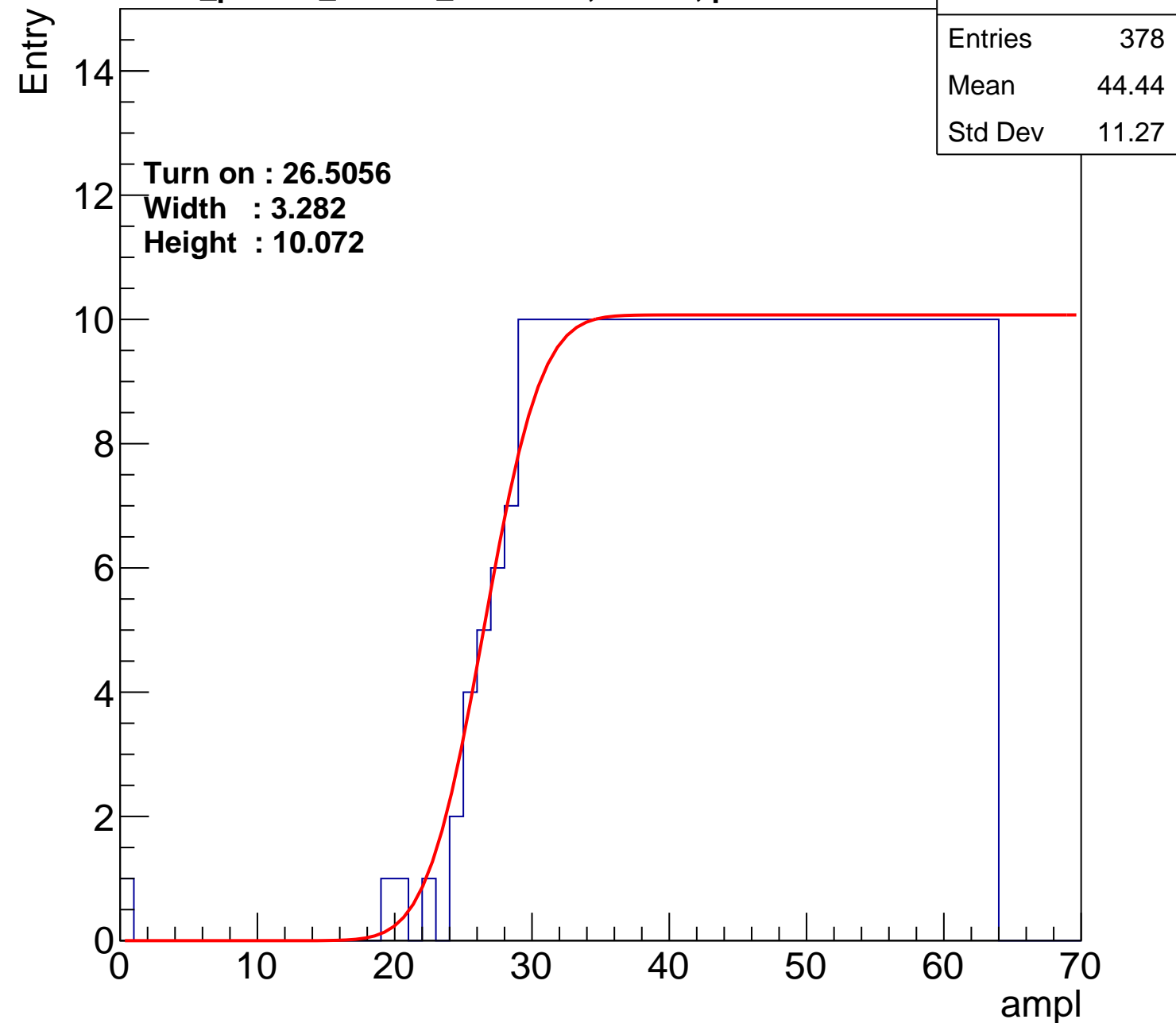
Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U18-ch14

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.28
Std Dev	11.56

Turn on : 26.1214

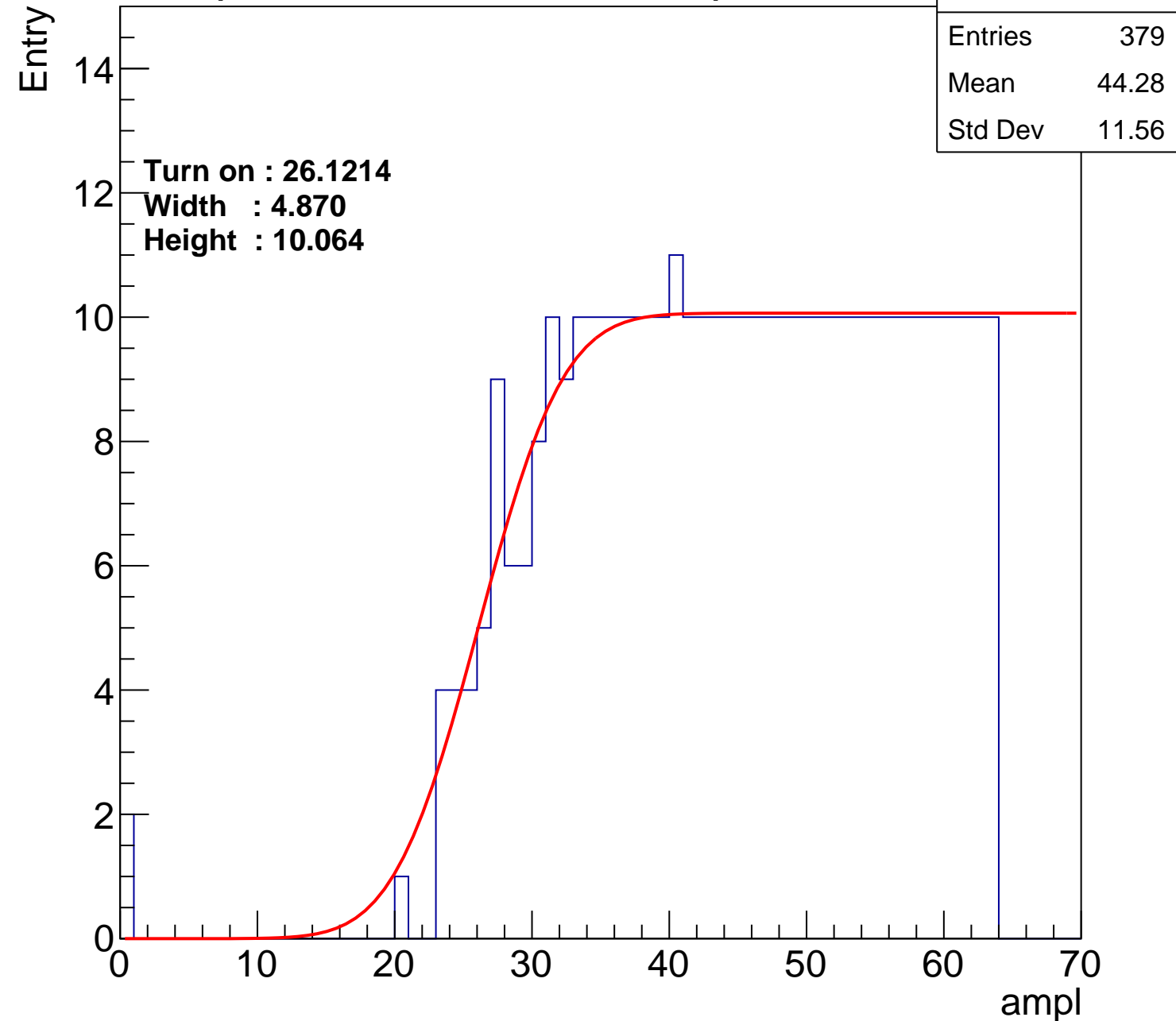
Width : 4.870

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch15

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	362
Mean	45.05
Std Dev	11.29

**Turn on : 27.8604**

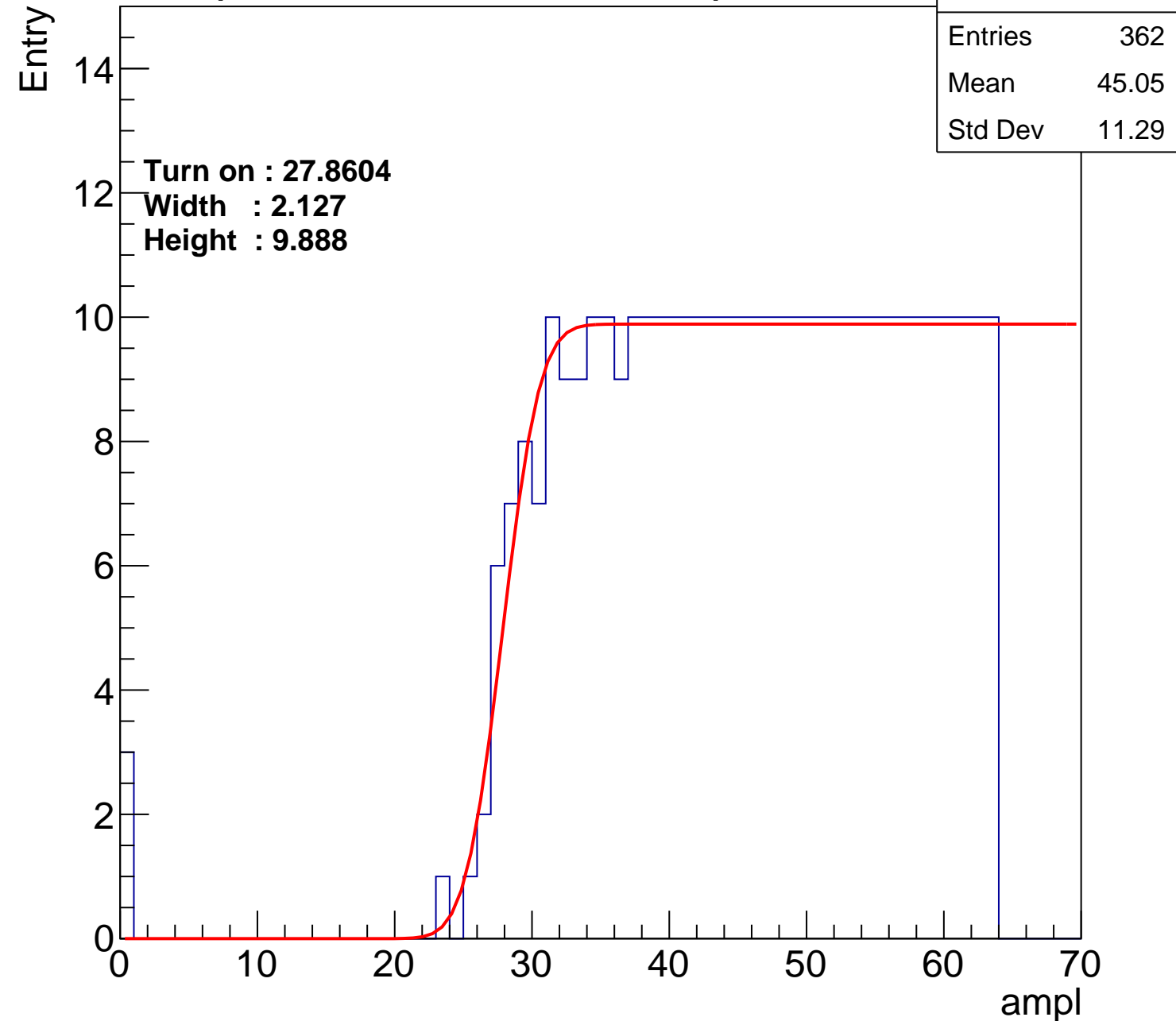
**Width : 2.127**

**Height : 9.888**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch16

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	399
Mean	43.27
Std Dev	12.15

Turn on : 23.5852

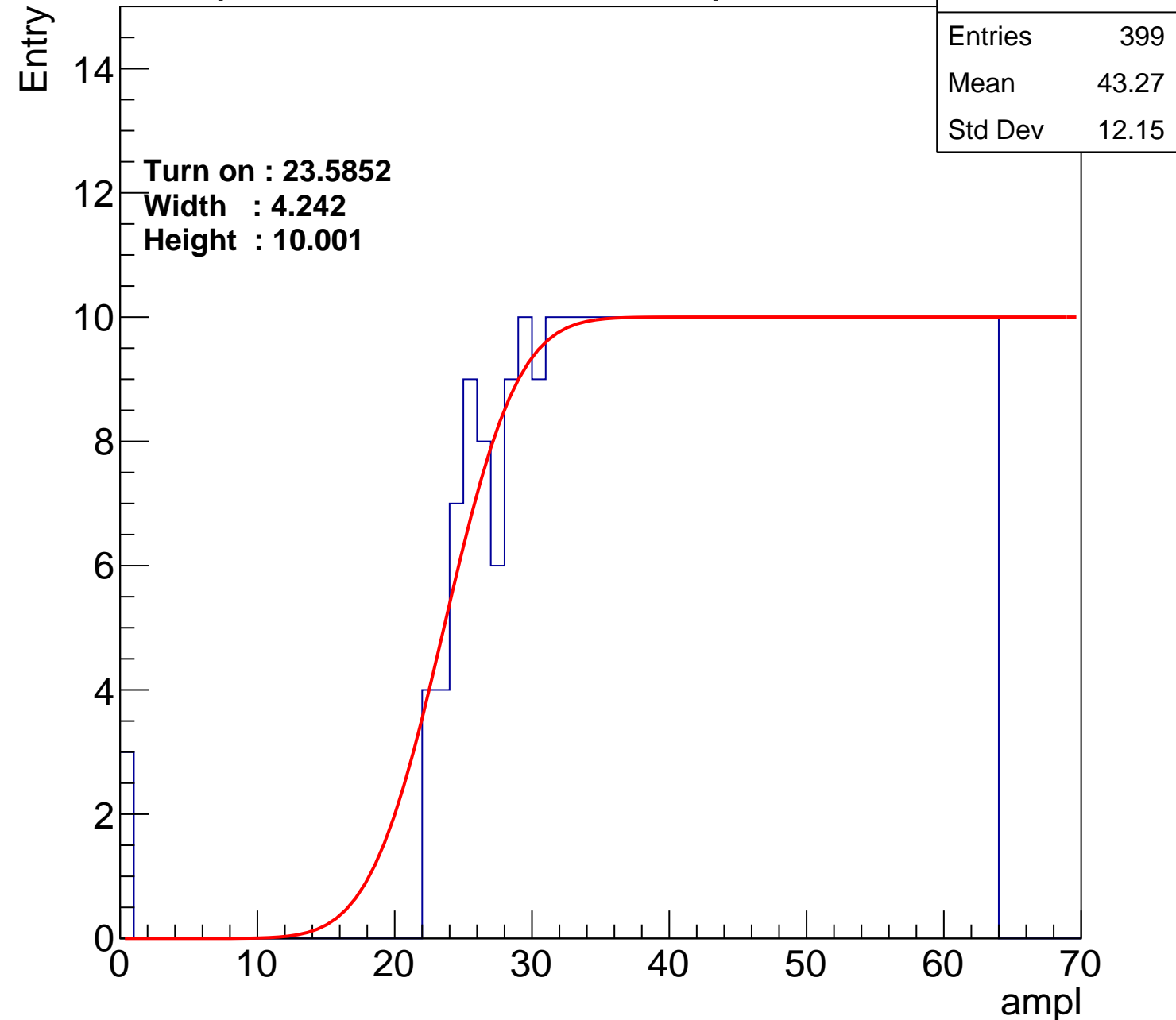
Width : 4.242

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch17

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.6822

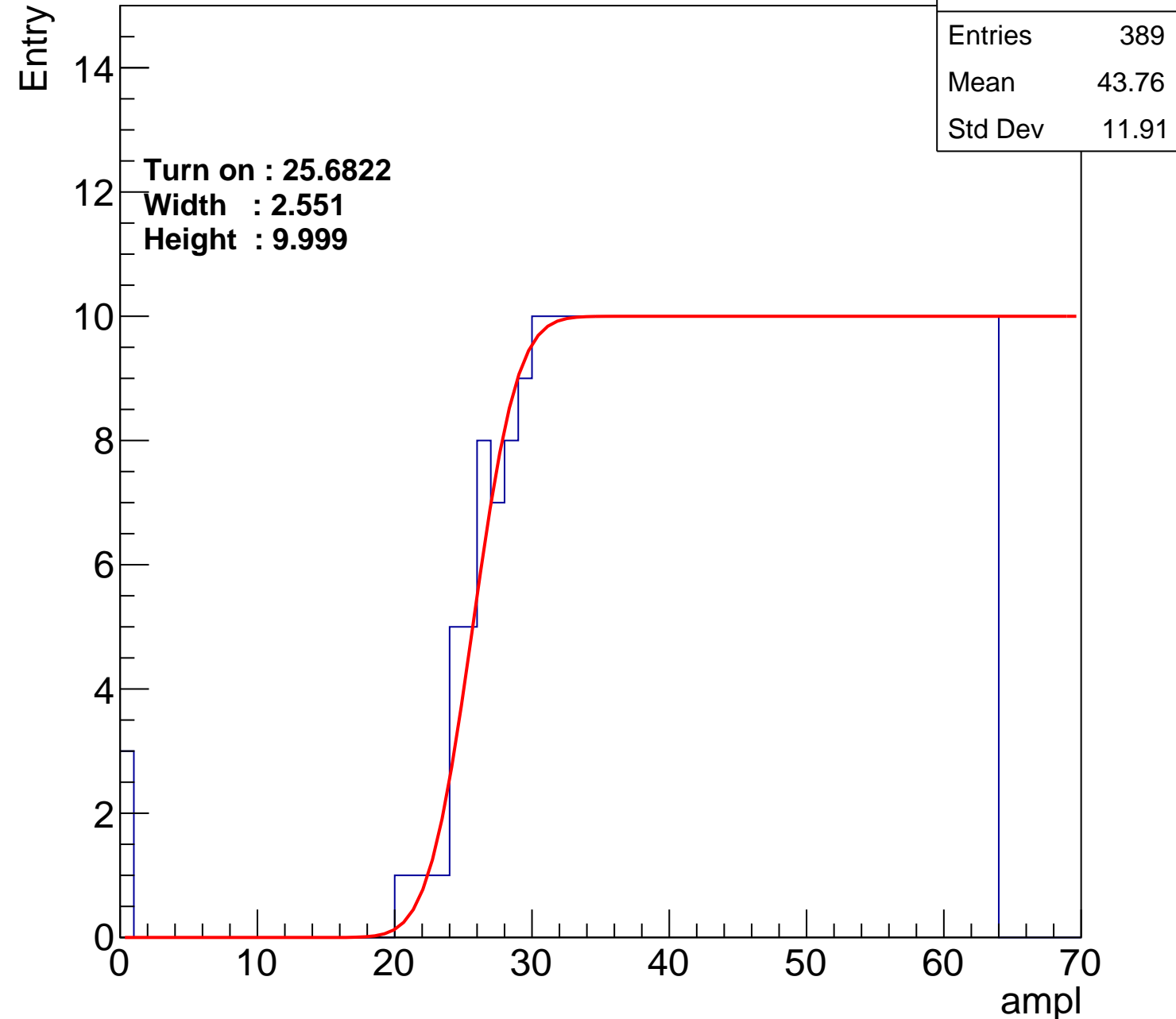
Width : 2.551

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch18

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	353
Mean	45.51
Std Dev	11.06

Turn on : 28.5904

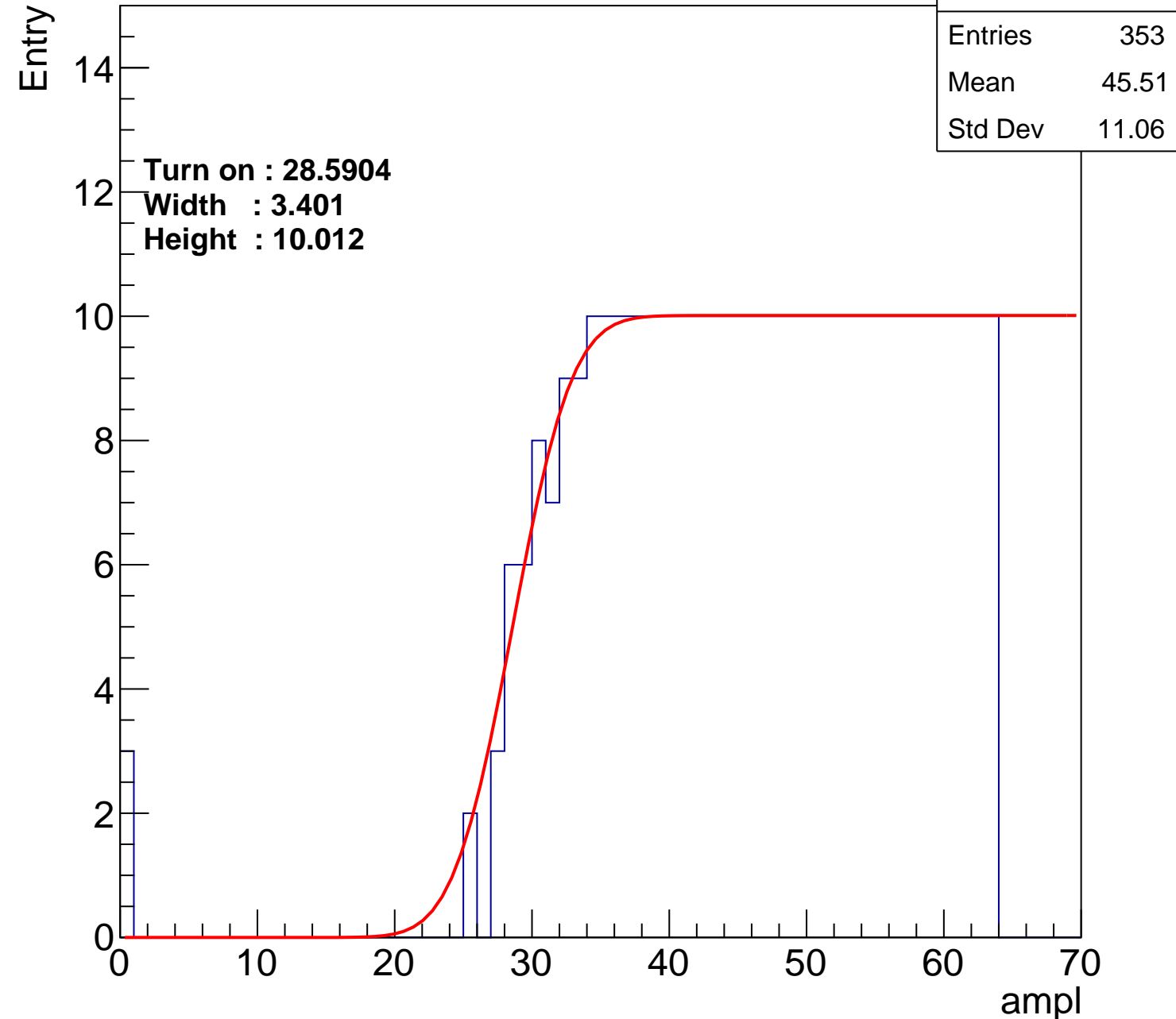
Width : 3.401

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch19

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	358
Mean	45.36
Std Dev	10.94

**Turn on : 28.7674**

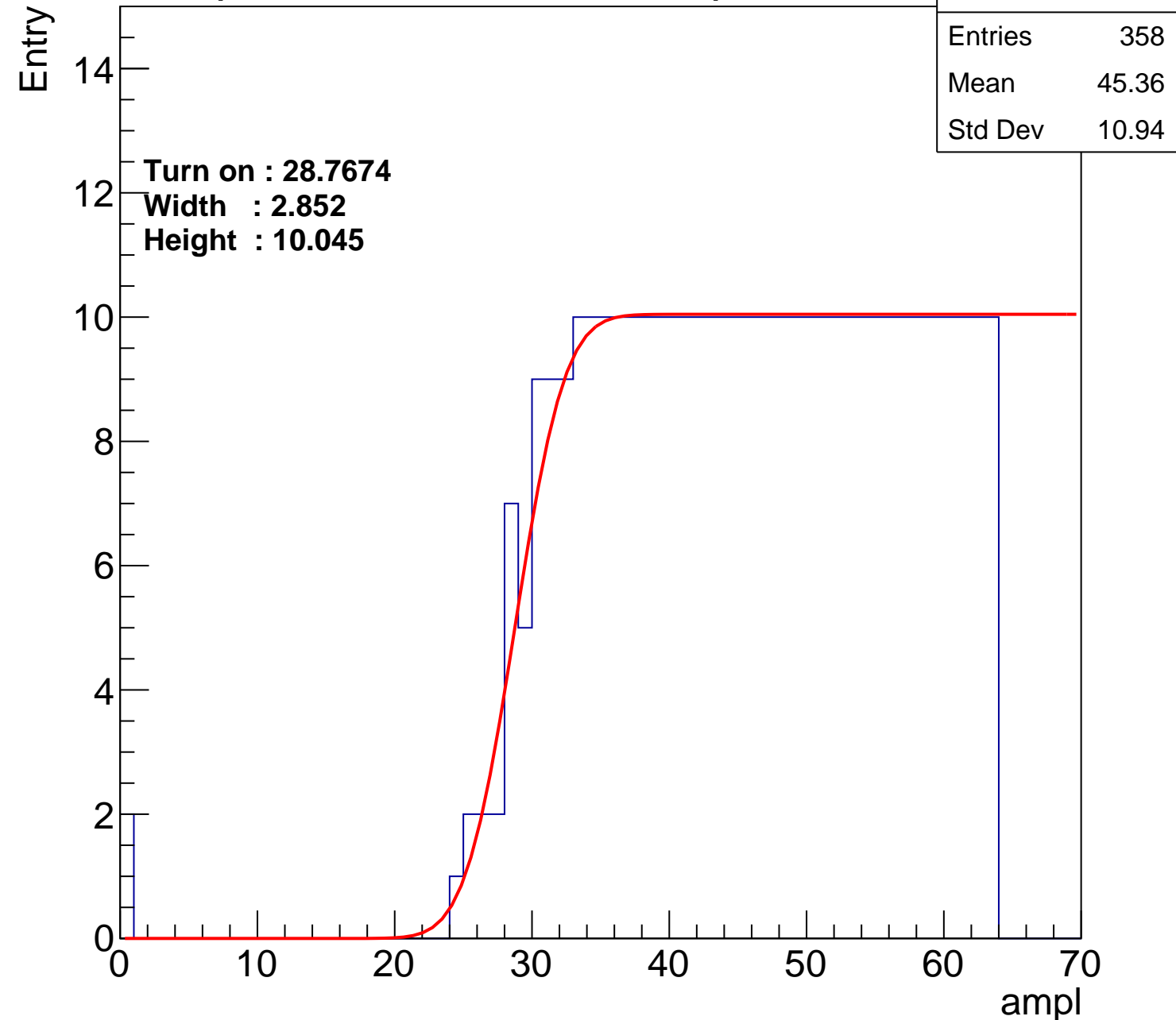
**Width : 2.852**

**Height : 10.045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch20

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.78
Std Dev	12.05

**Turn on : 25.7844**

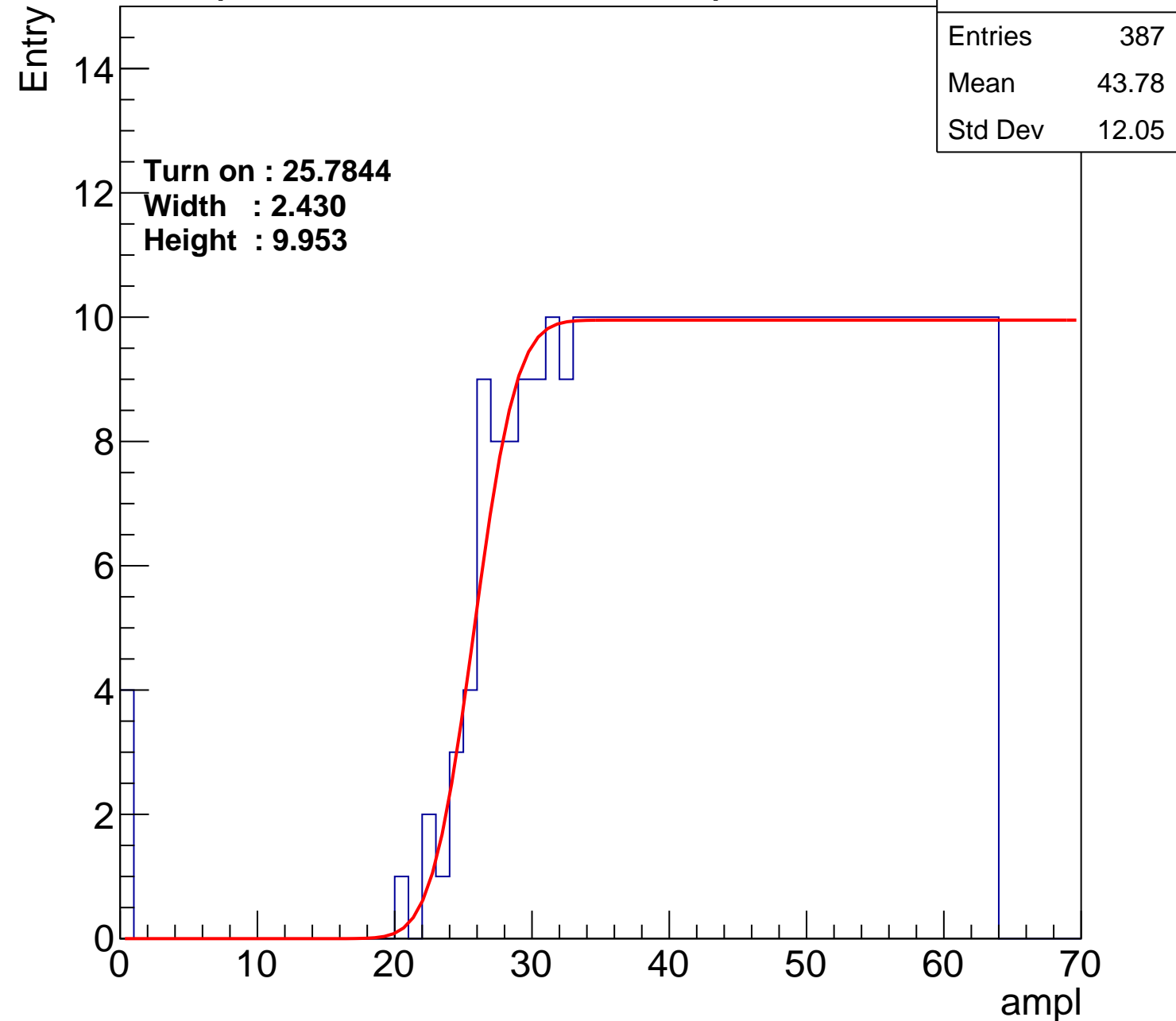
**Width : 2.430**

**Height : 9.953**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch21

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.2
Std Dev	11.52

Turn on : 26.2324

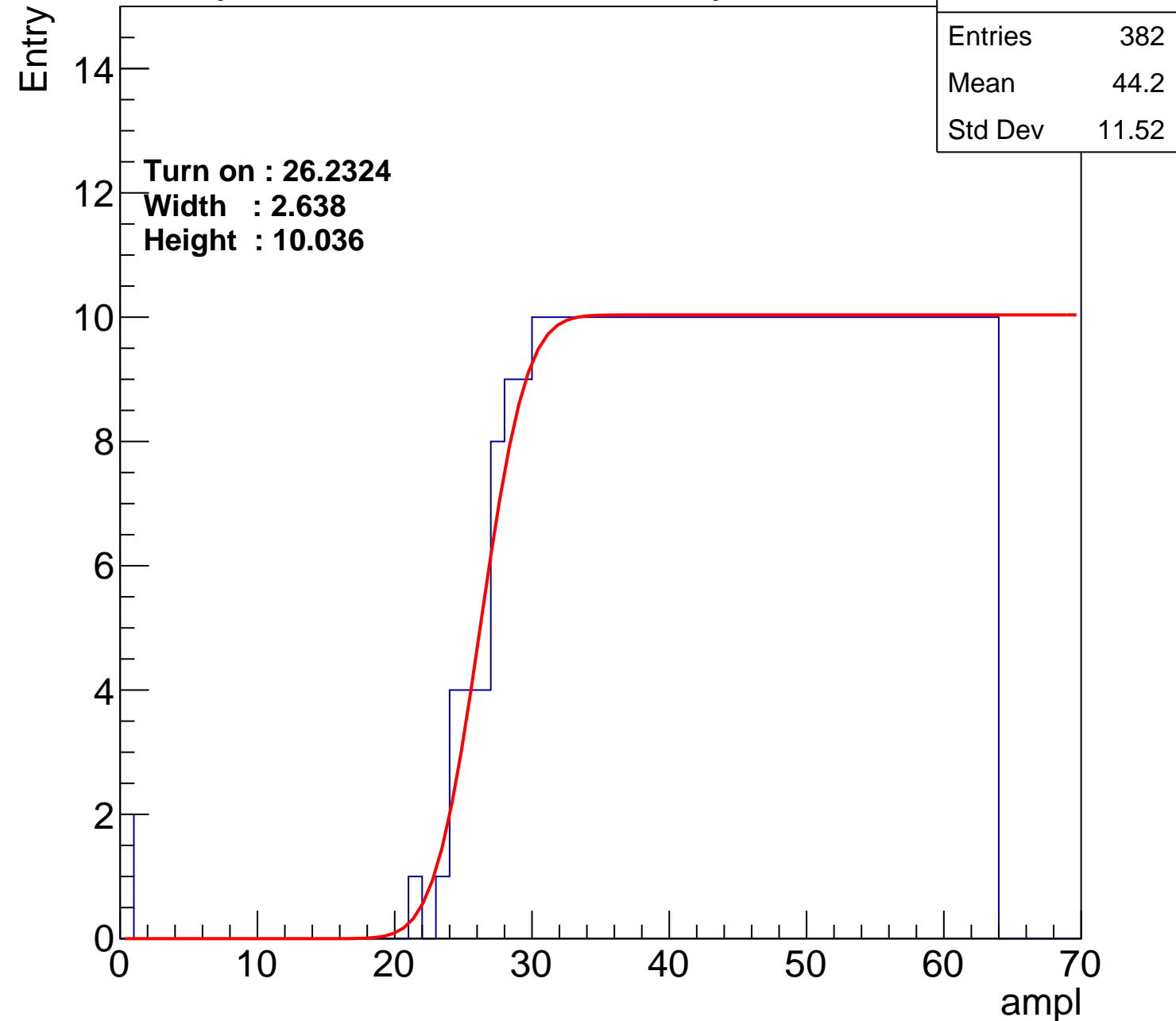
Width : 2.638

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch22

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	353
Mean	45.53
Std Dev	10.93

**Turn on : 29.5685**

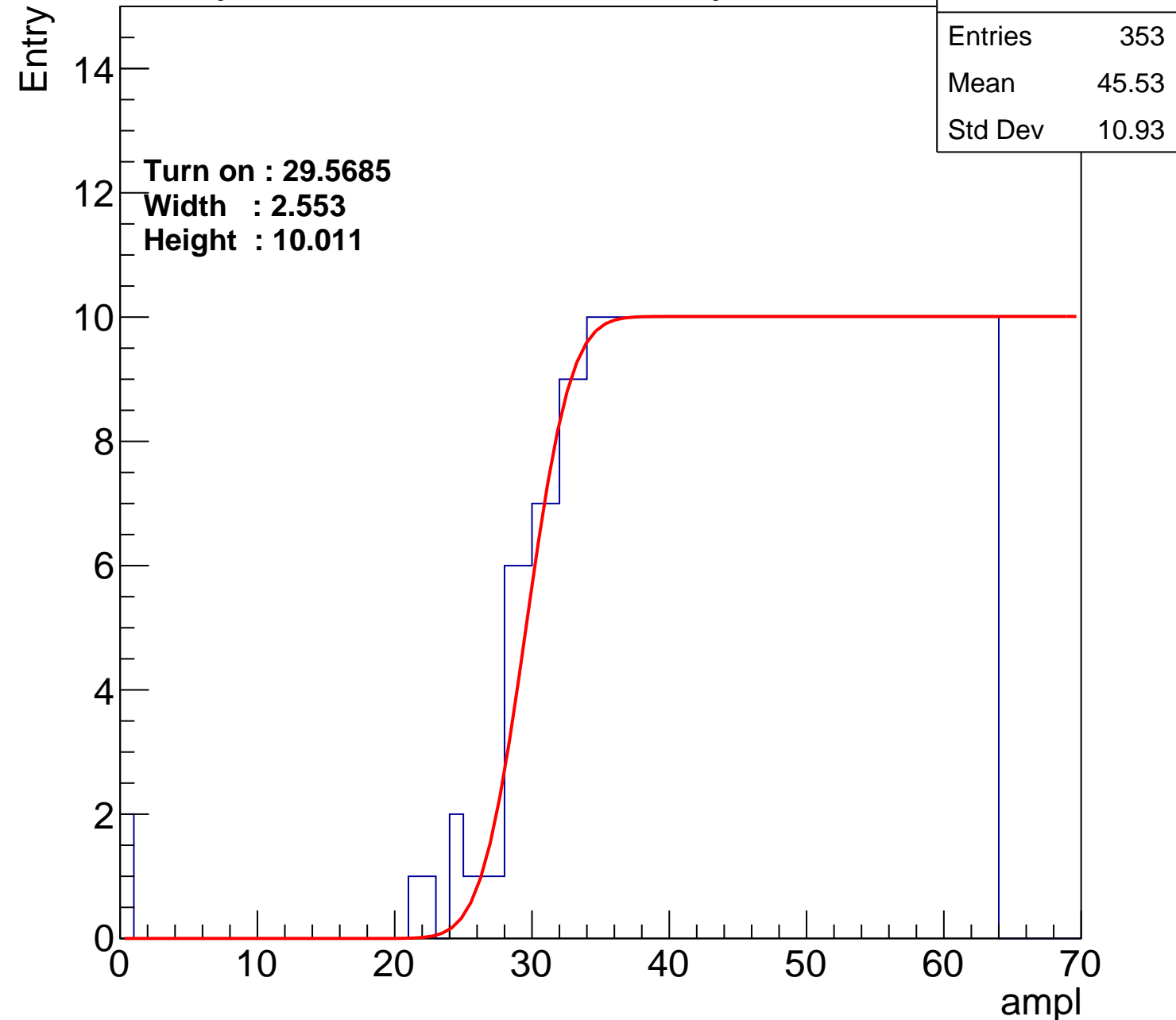
**Width : 2.553**

**Height : 10.011**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch23

calib\_packv5\_042523\_0143.root, FC#12, port B1

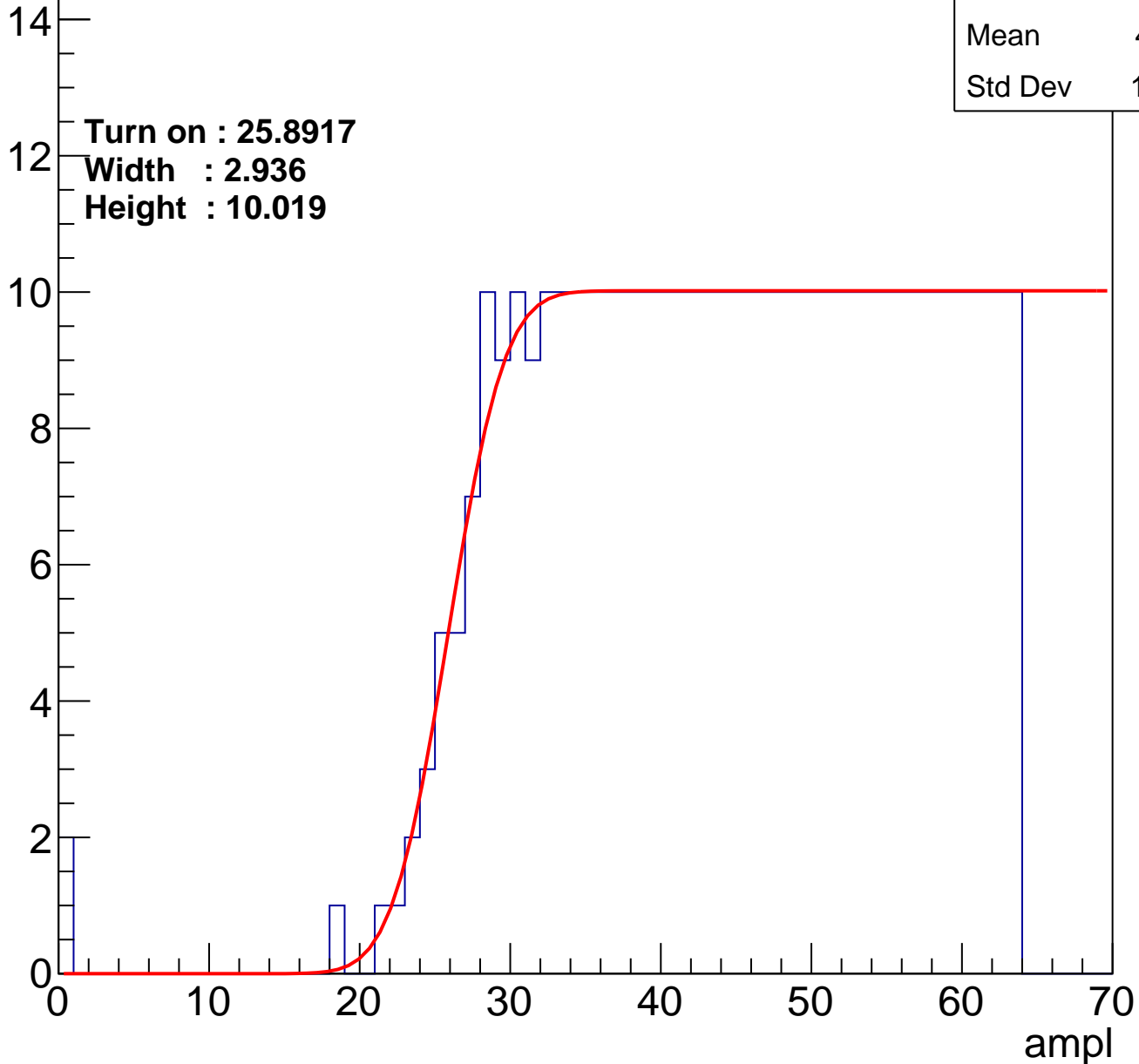
Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 25.8917

Width : 2.936

Height : 10.019

Entry



# B0L102S, U18-ch24

calib\_packv5\_042523\_0143.root, FC#12, port B1

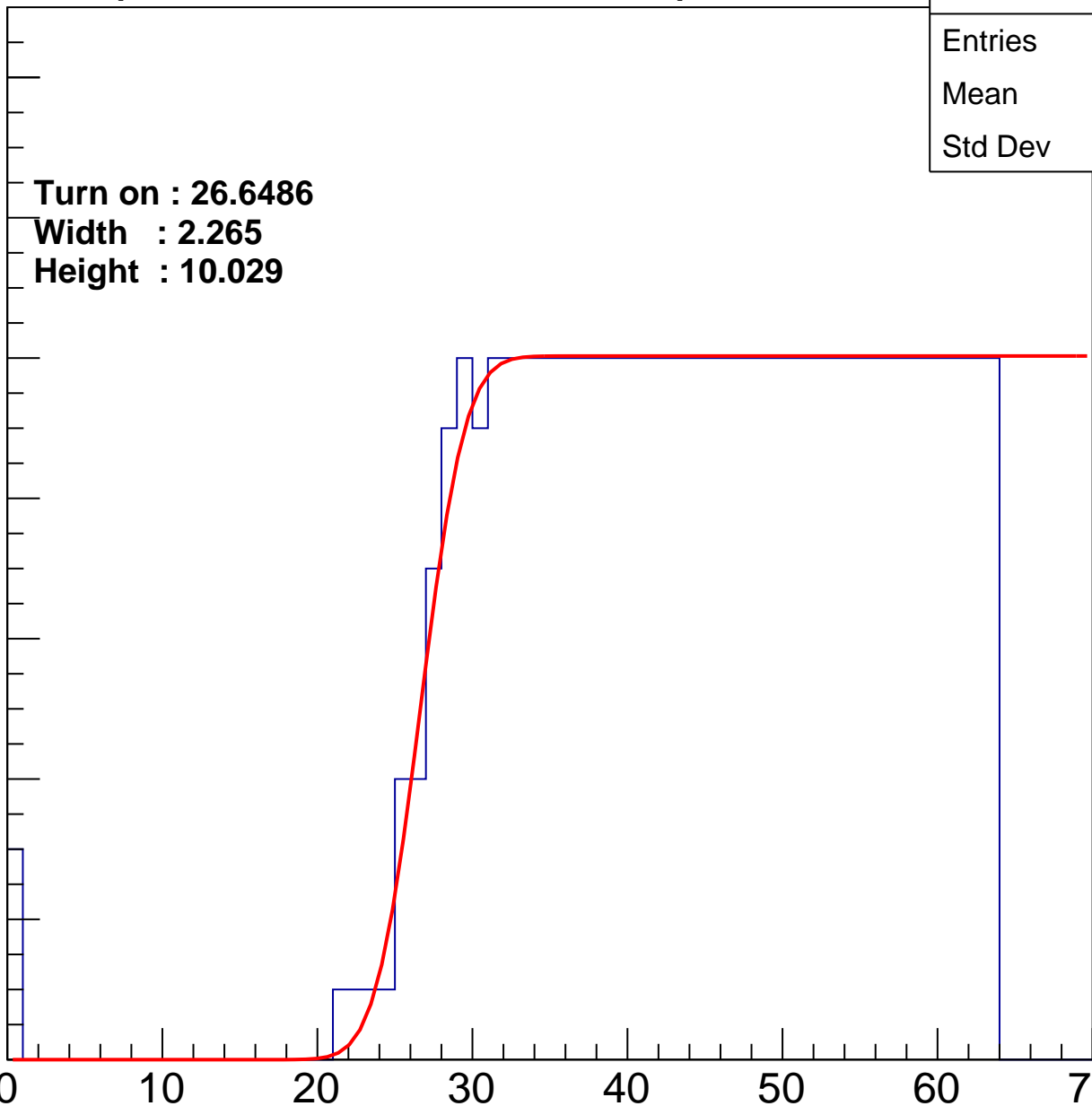
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.6486  
Width : 2.265  
Height : 10.029

Entries	380
Mean	44.22
Std Dev	11.65

ampl



# B0L102S, U18-ch25

calib\_packv5\_042523\_0143.root, FC#12, port B1

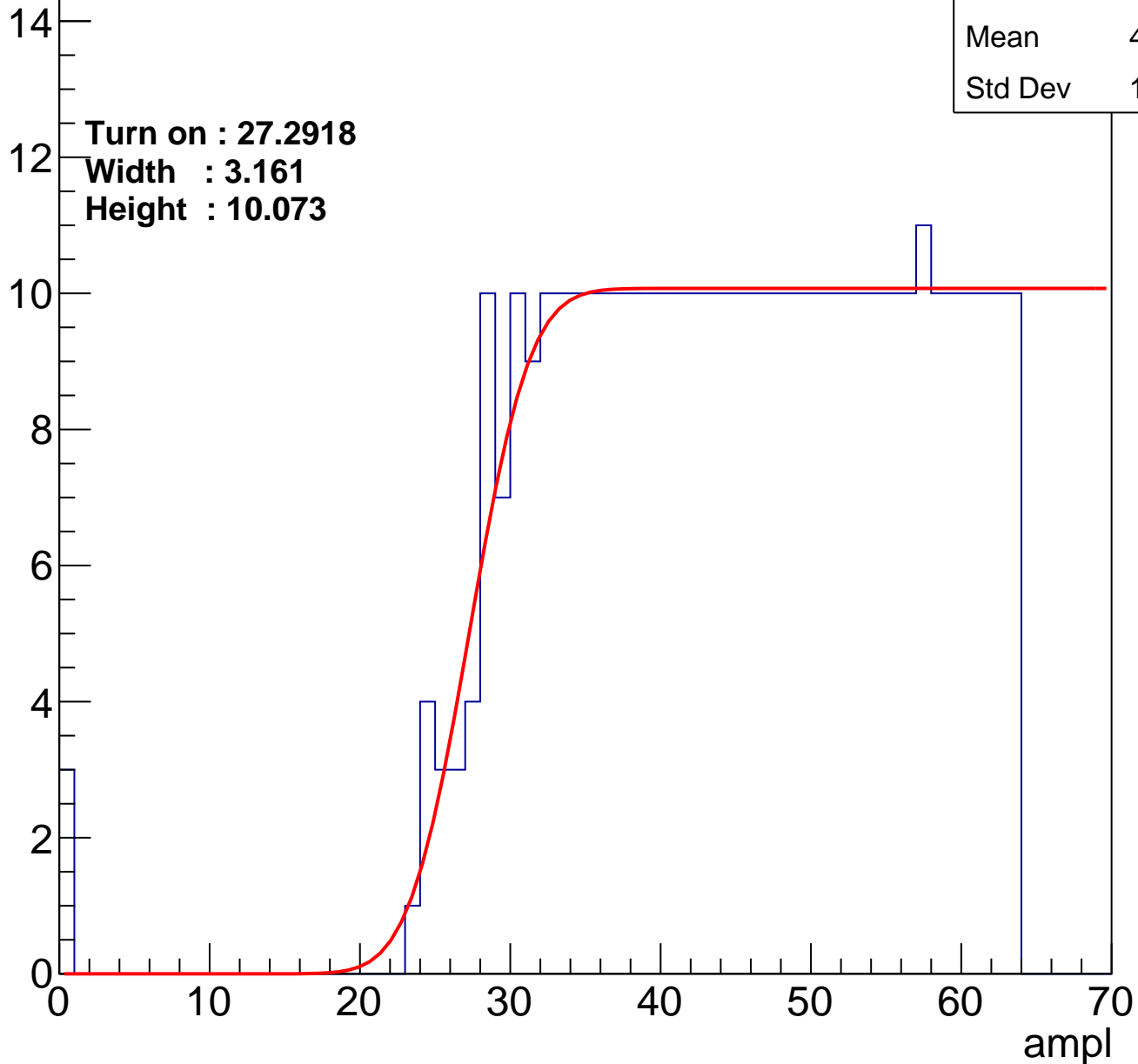
Entries	375
Mean	44.53
Std Dev	11.54

Turn on : 27.2918

Width : 3.161

Height : 10.073

Entry



# B0L102S, U18-ch26

calib\_packv5\_042523\_0143.root, FC#12, port B1

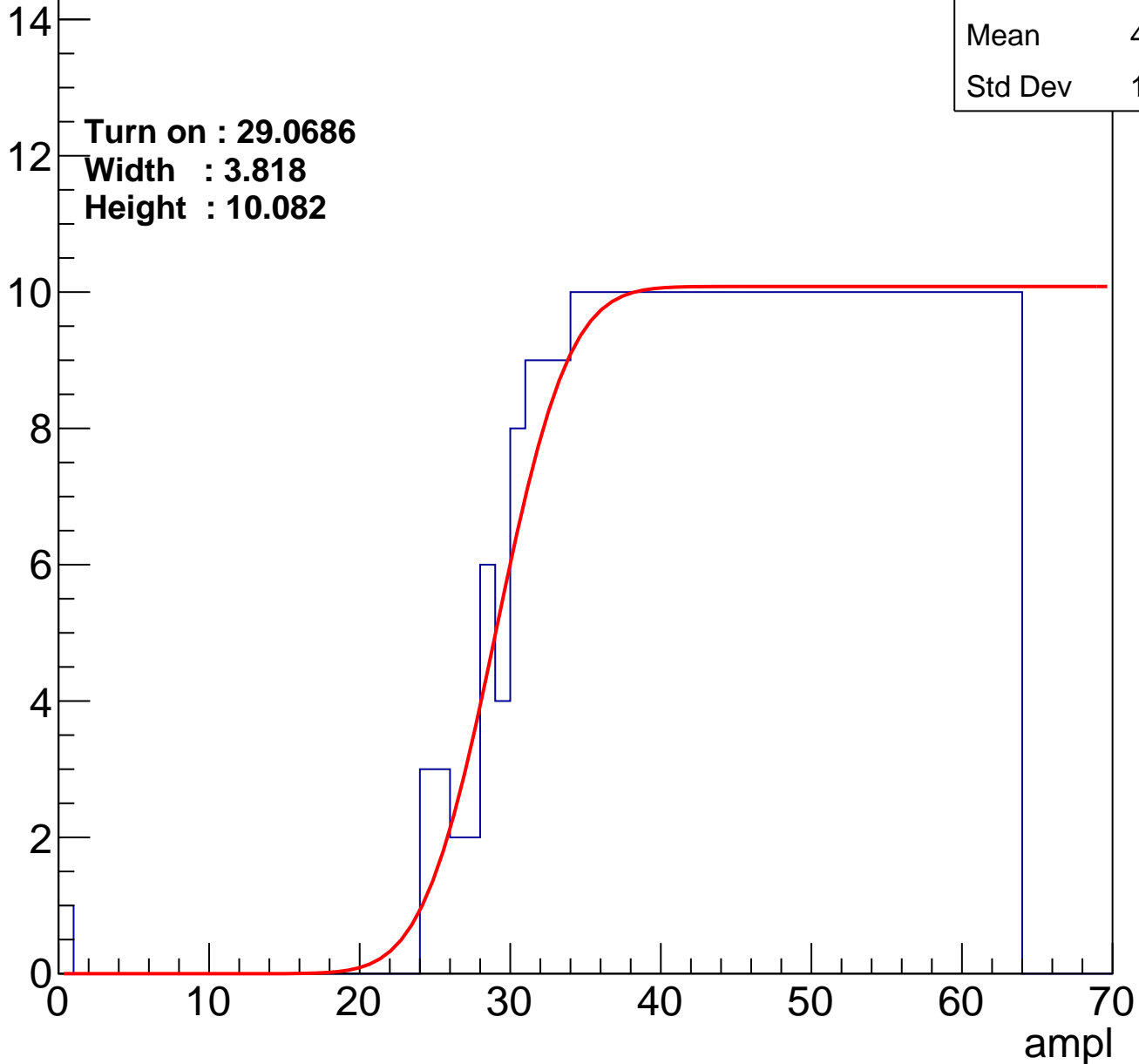
Entries	356
Mean	45.48
Std Dev	10.75

**Turn on : 29.0686**

**Width : 3.818**

**Height : 10.082**

Entry



# B0L102S, U18-ch27

calib\_packv5\_042523\_0143.root, FC#12, port B1

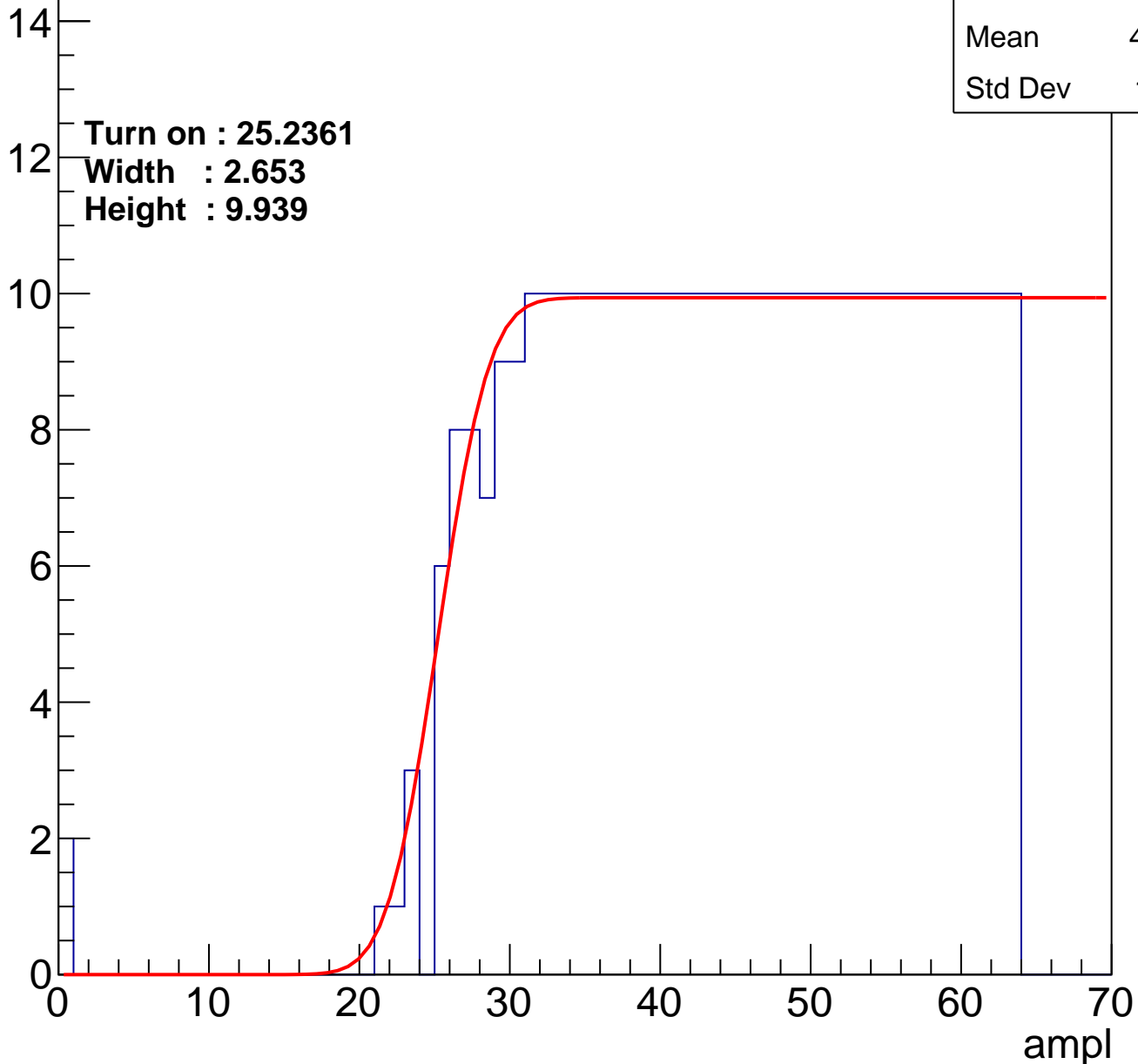
Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 25.2361

Width : 2.653

Height : 9.939

Entry



# B0L102S, U18-ch28

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.7
Std Dev	12.24

Turn on : 26.1796

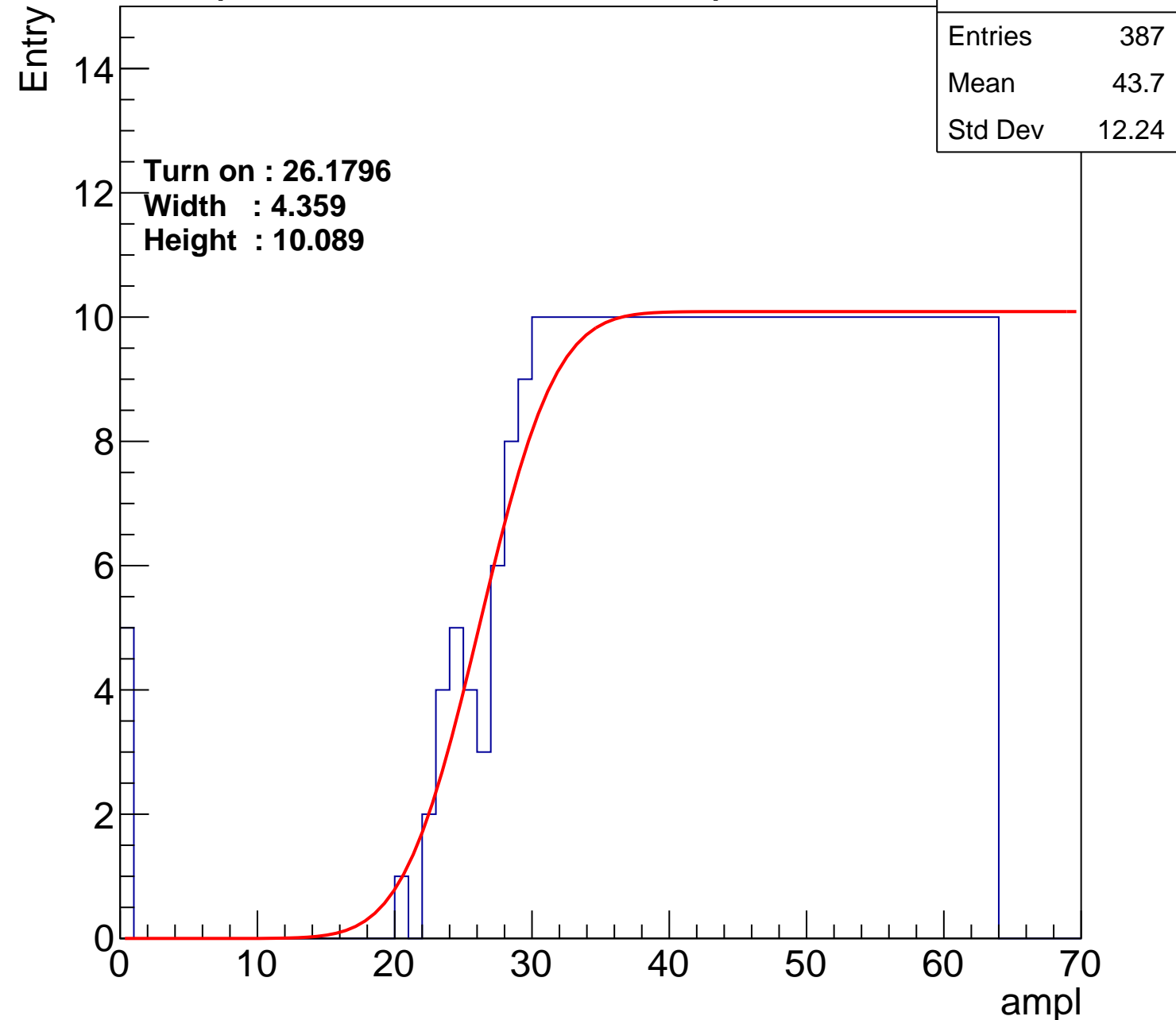
Width : 4.359

Height : 10.089

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch29

calib\_packv5\_042523\_0143.root, FC#12, port B1

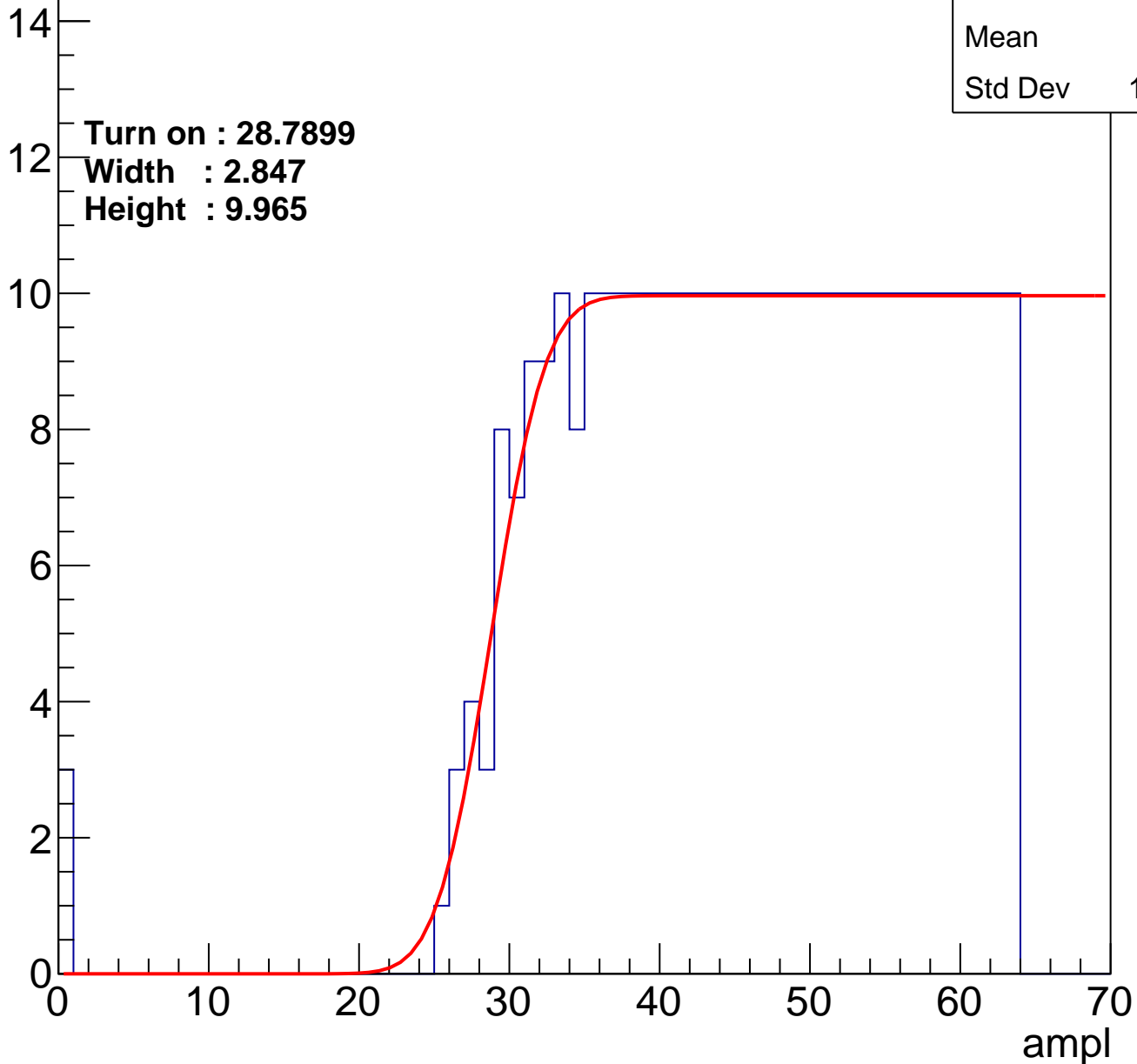
Entries	355
Mean	45.4
Std Dev	11.12

Turn on : 28.7899

Width : 2.847

Height : 9.965

Entry



# B0L102S, U18-ch30

calib\_packv5\_042523\_0143.root, FC#12, port B1

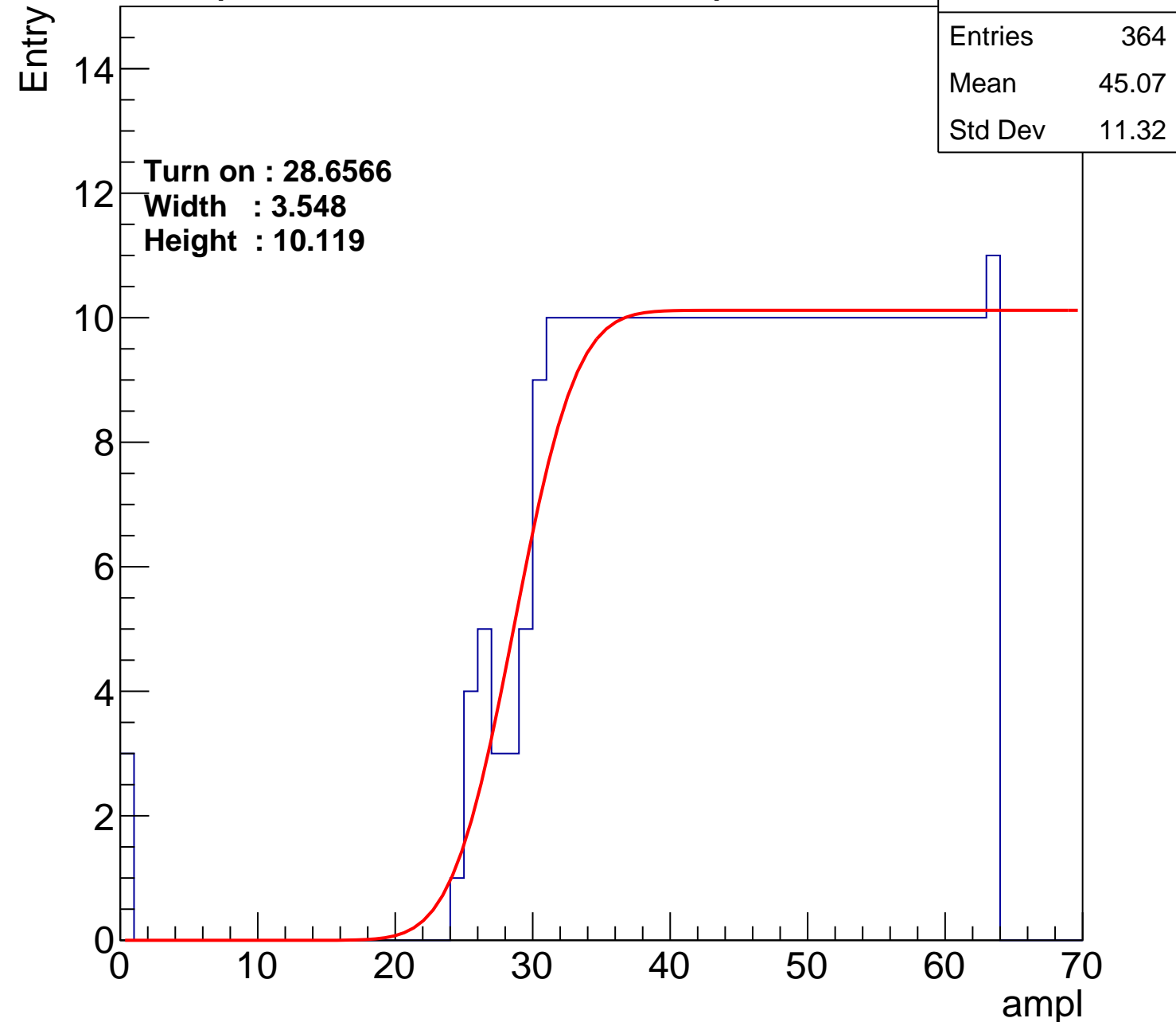
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.6566  
Width : 3.548  
Height : 10.119

Entries	364
Mean	45.07
Std Dev	11.32

ampl





# B0L102S, U18-ch31

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.18
Std Dev	11.77

Turn on : 26.5917

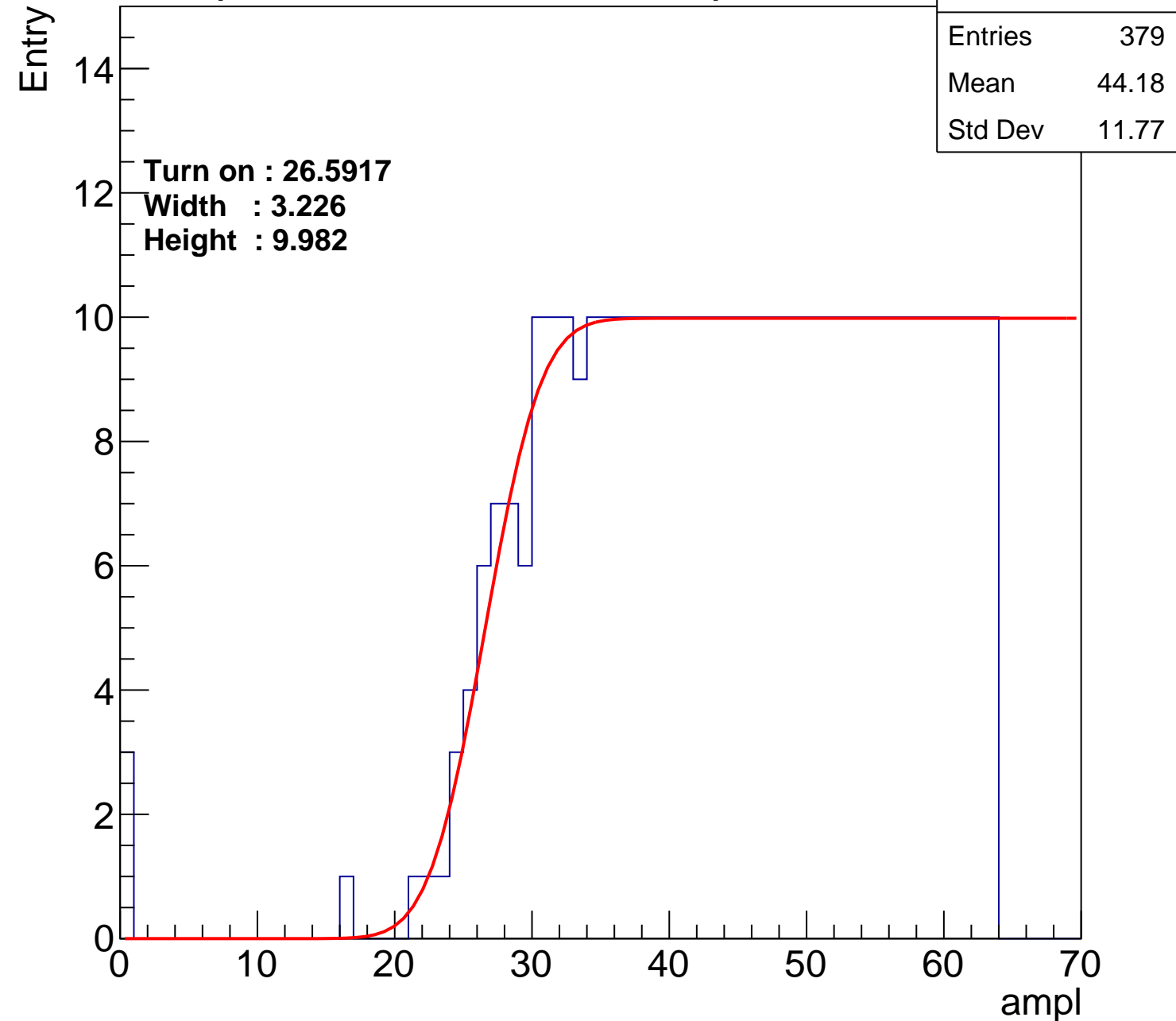
Width : 3.226

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch32

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.61
Std Dev	11.33

Turn on : 27.0867

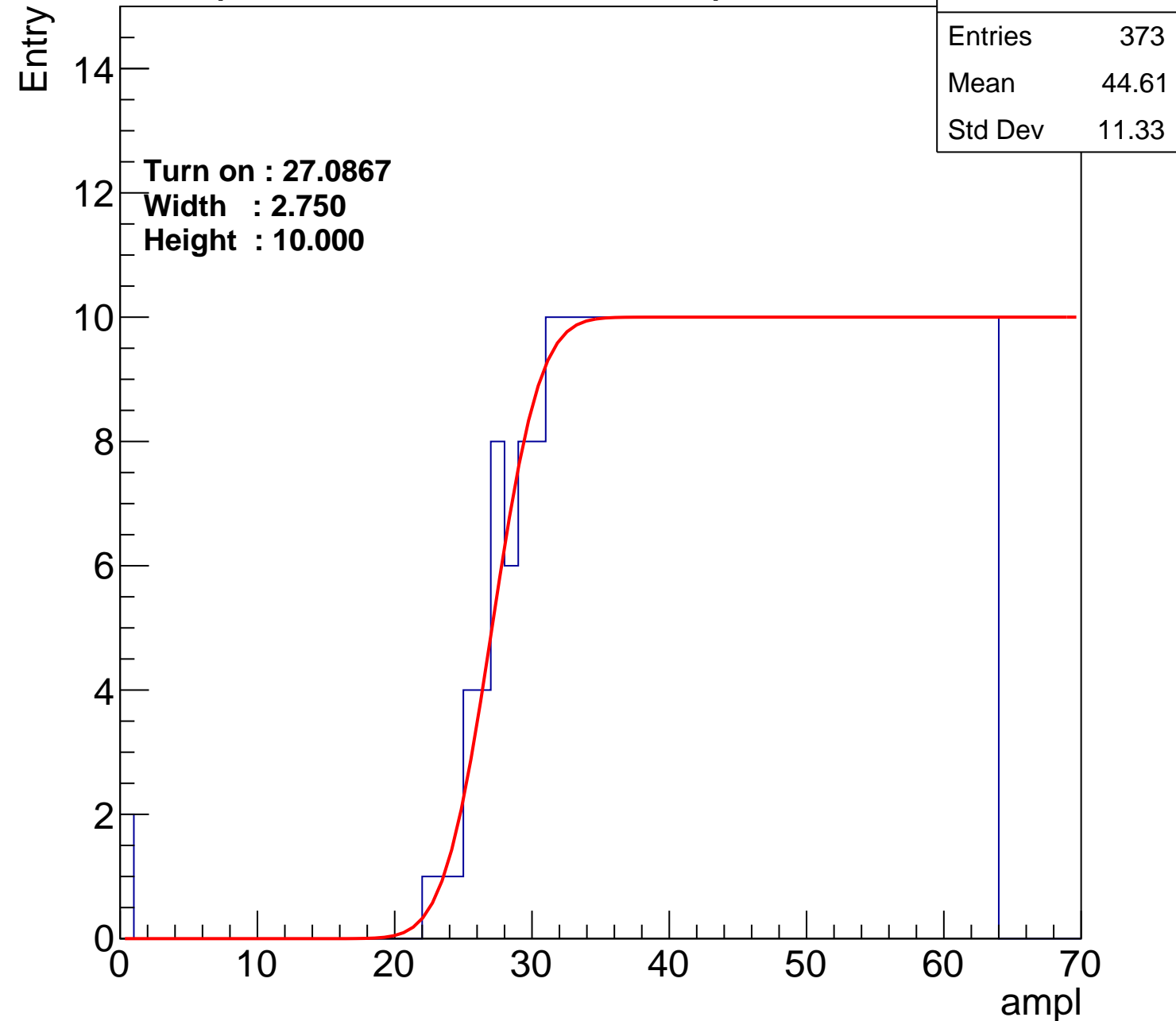
Width : 2.750

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch33

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.11
Std Dev	11.9

Turn on : 26.9271

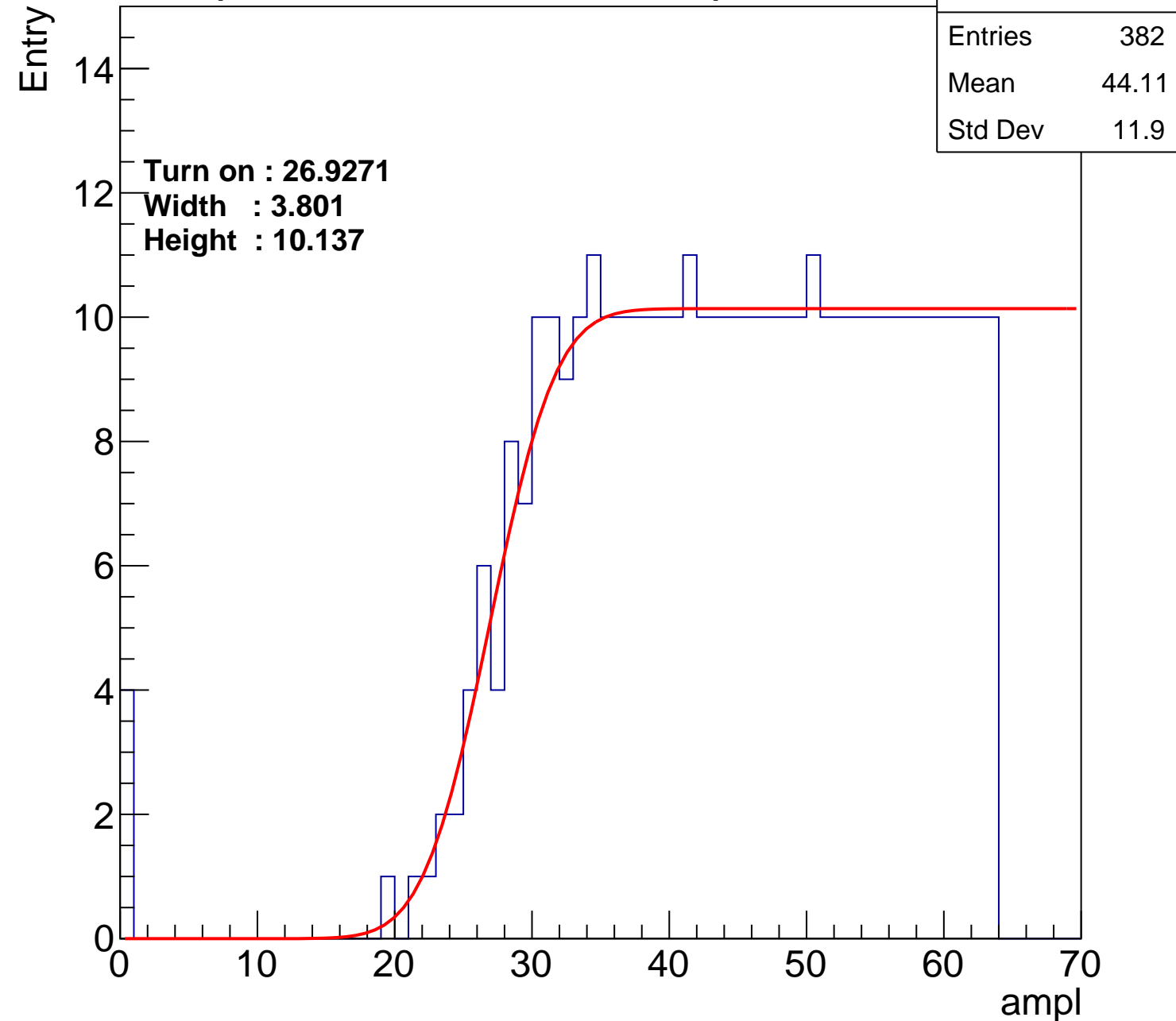
Width : 3.801

Height : 10.137

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch34

calib\_packv5\_042523\_0143.root, FC#12, port B1

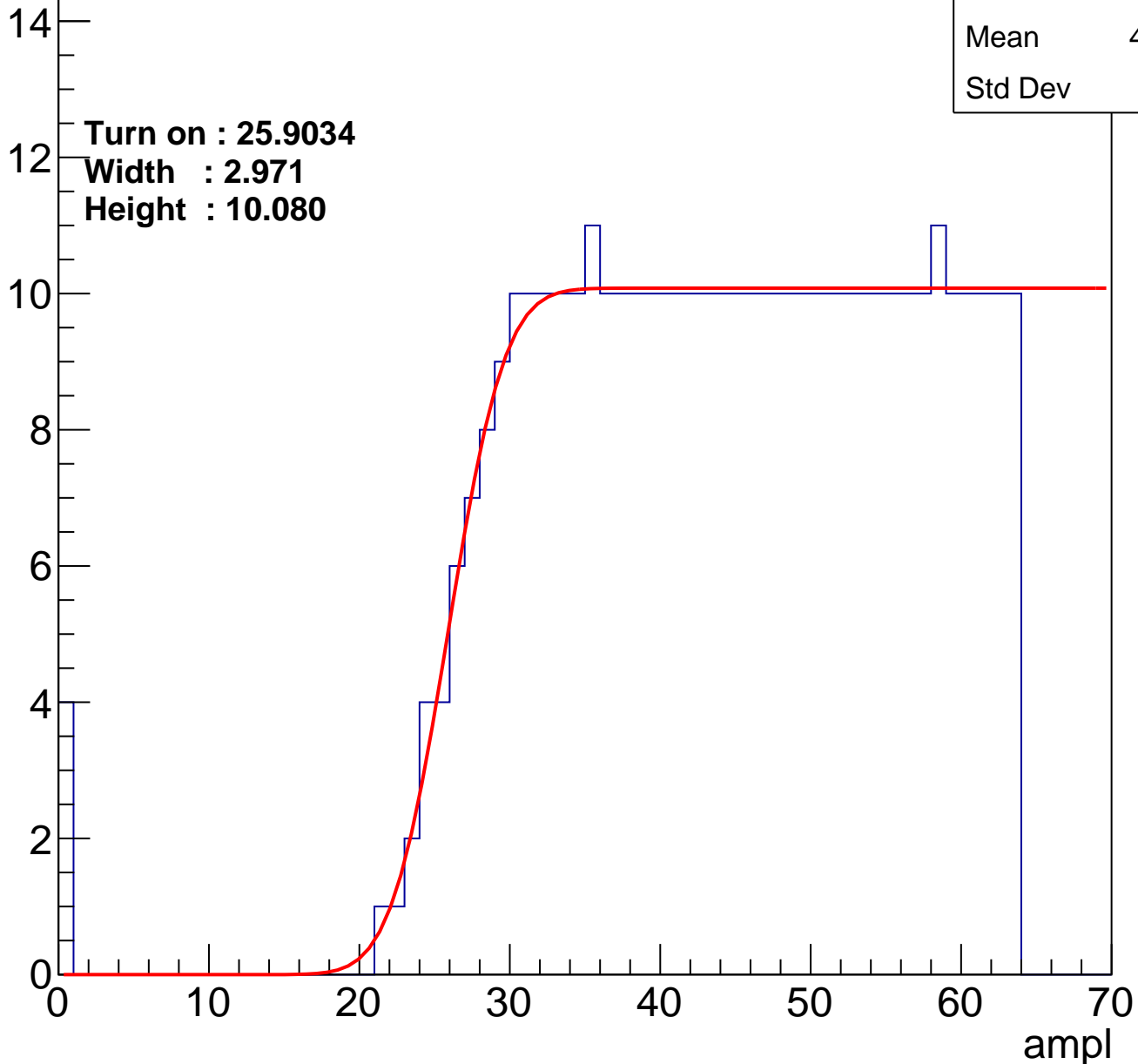
Entries	388
Mean	43.86
Std Dev	12

Turn on : 25.9034

Width : 2.971

Height : 10.080

Entry



# B0L102S, U18-ch35

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.73
Std Dev	11.77

**Turn on : 25.7842**

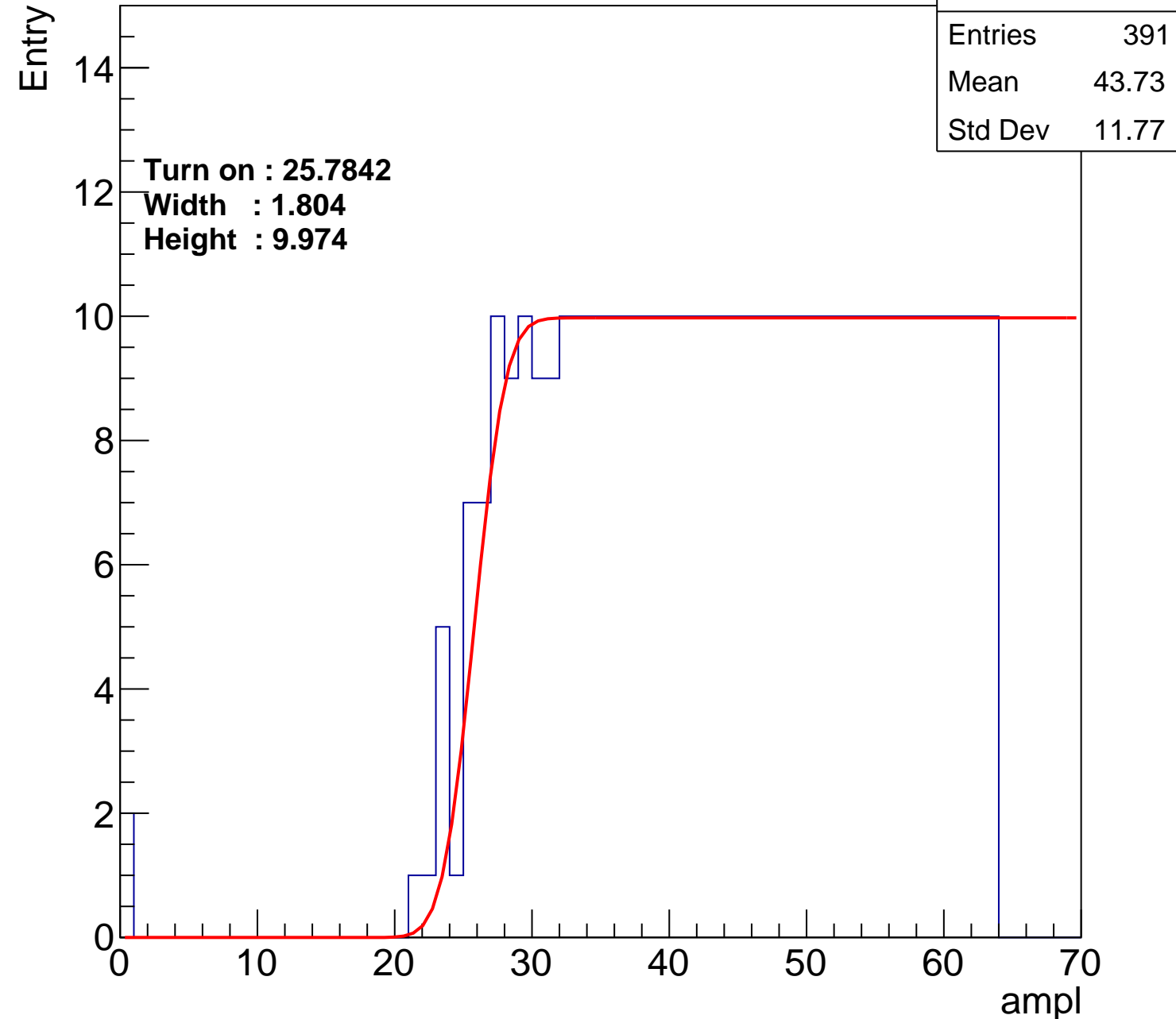
**Width : 1.804**

**Height : 9.974**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch36

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.91
Std Dev	12.02

Turn on : 26.1003

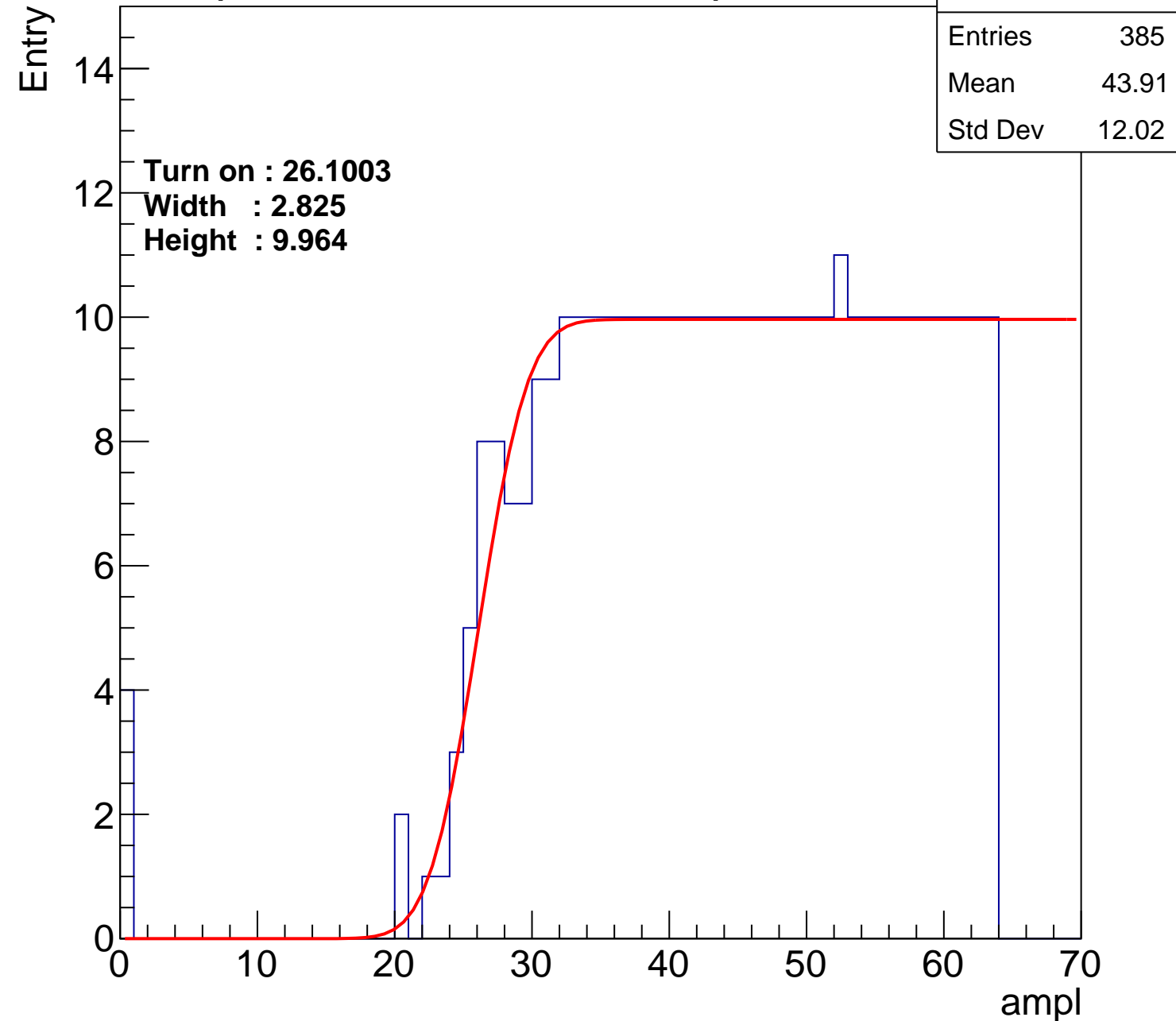
Width : 2.825

Height : 9.964

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch37

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.8
Std Dev	12.17

Turn on : 26.1208

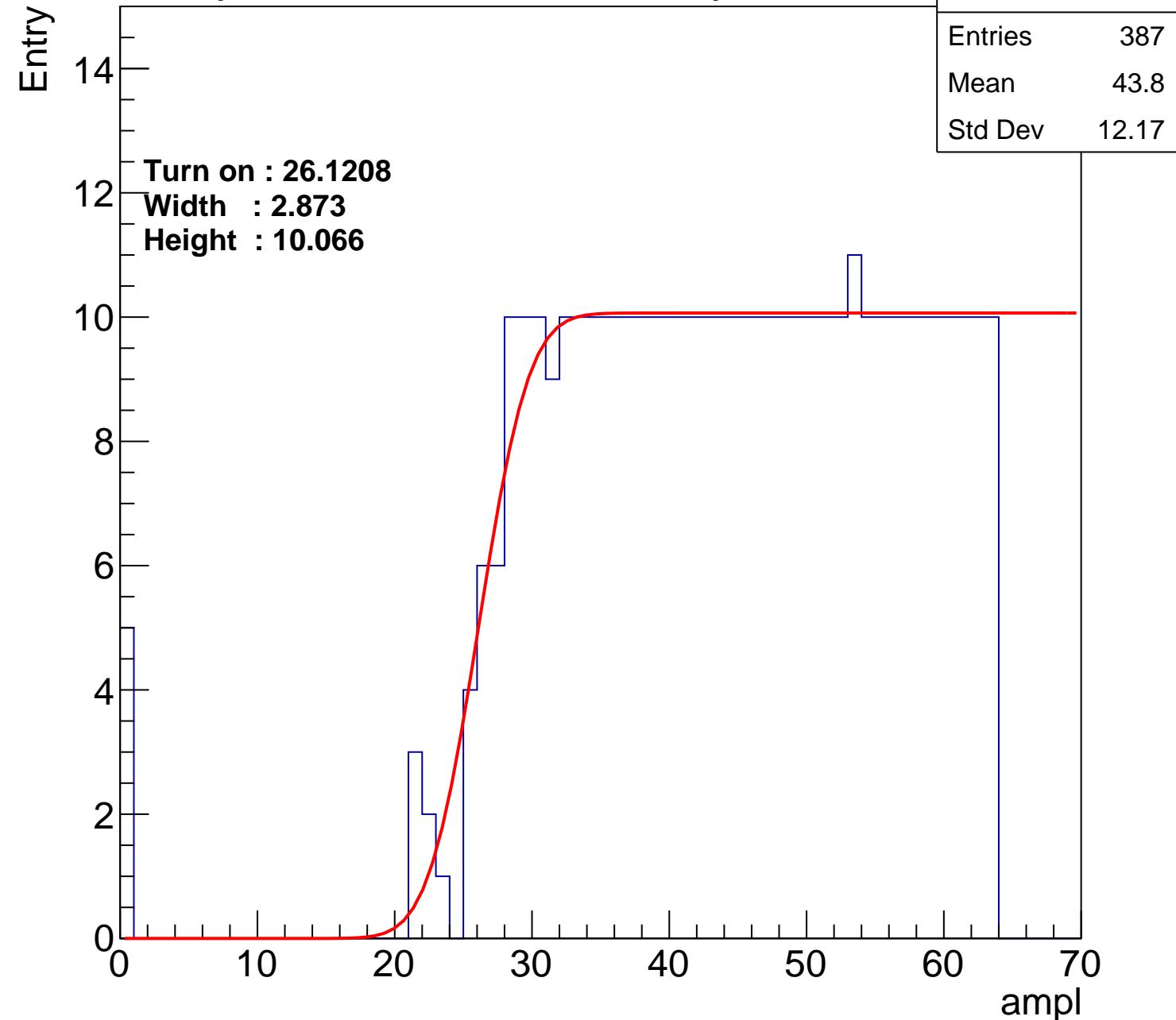
Width : 2.873

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch38

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 26.5417

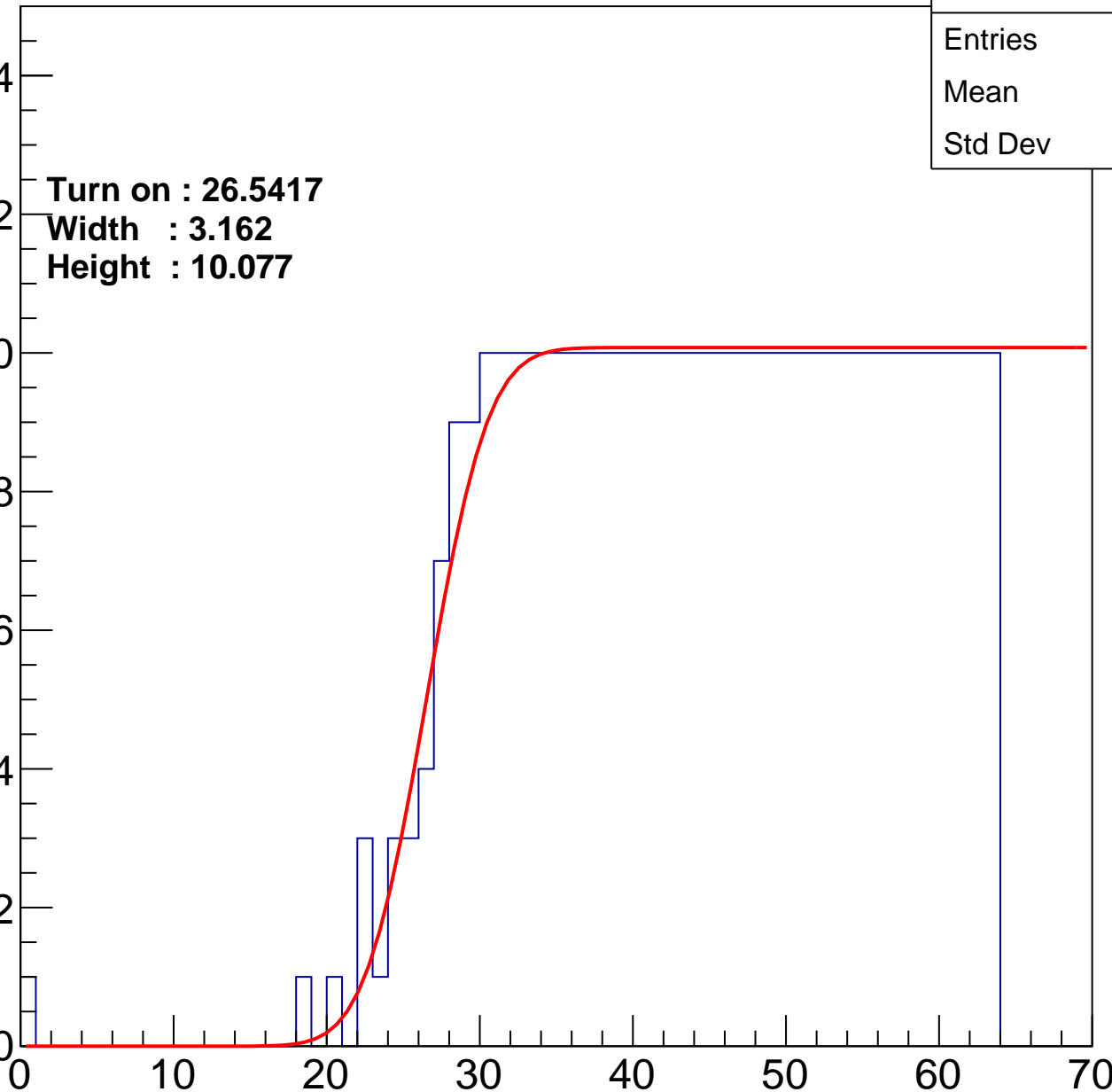
Width : 3.162

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch39

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.36
Std Dev	11.64

**Turn on : 26.8903**

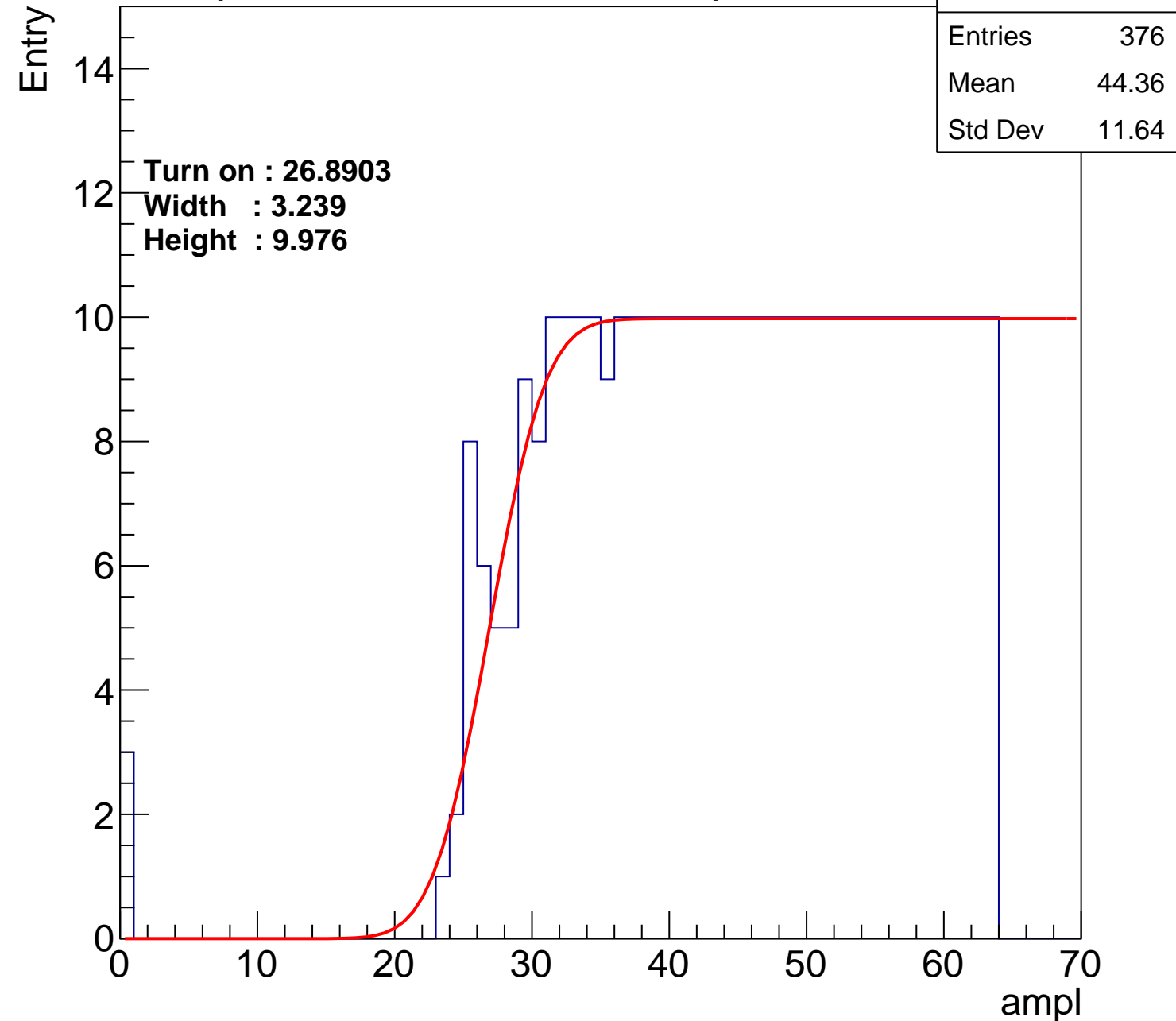
**Width : 3.239**

**Height : 9.976**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch40

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.46
Std Dev	12.18

Turn on : 25.3959

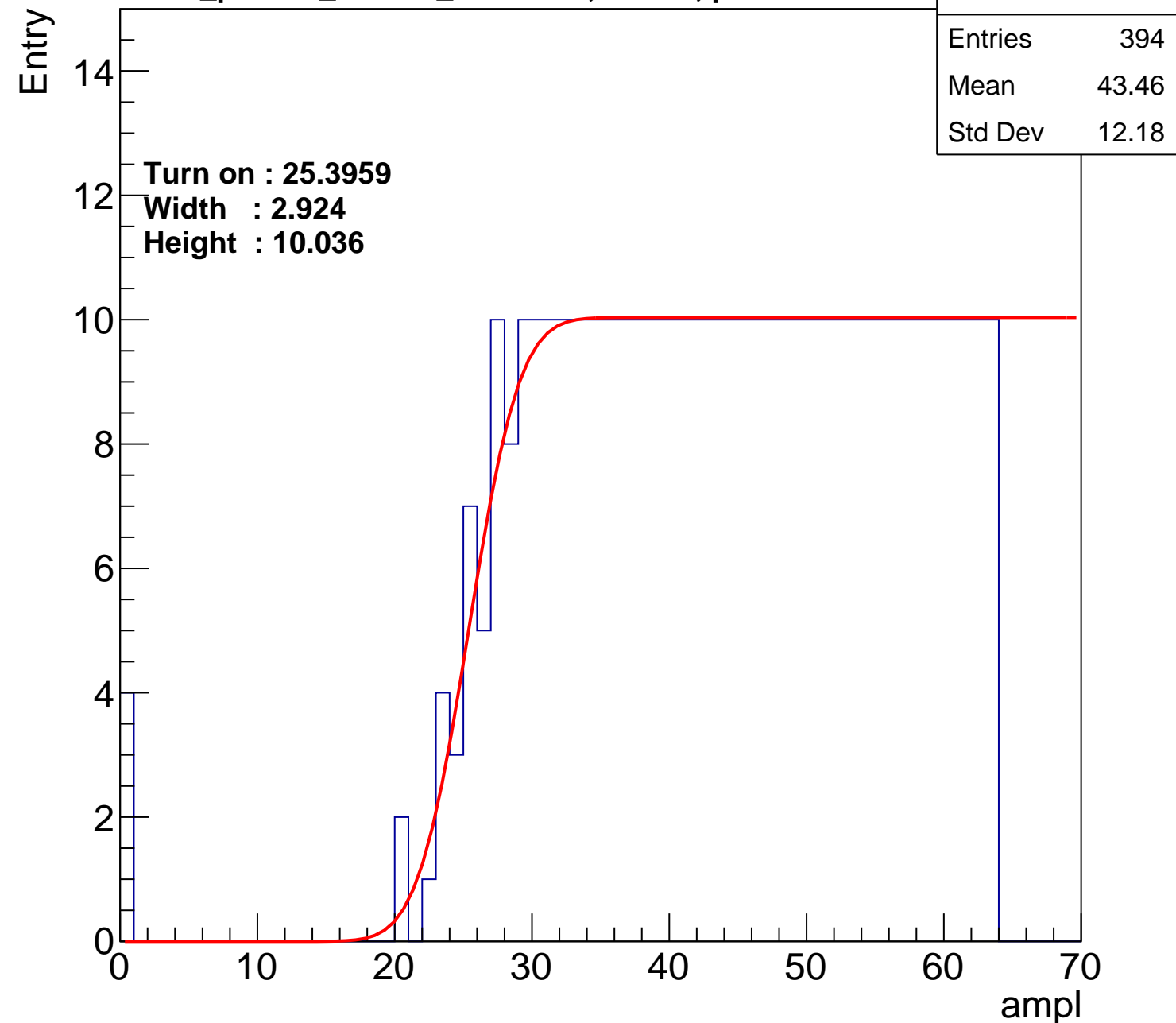
Width : 2.924

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch41

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.1743

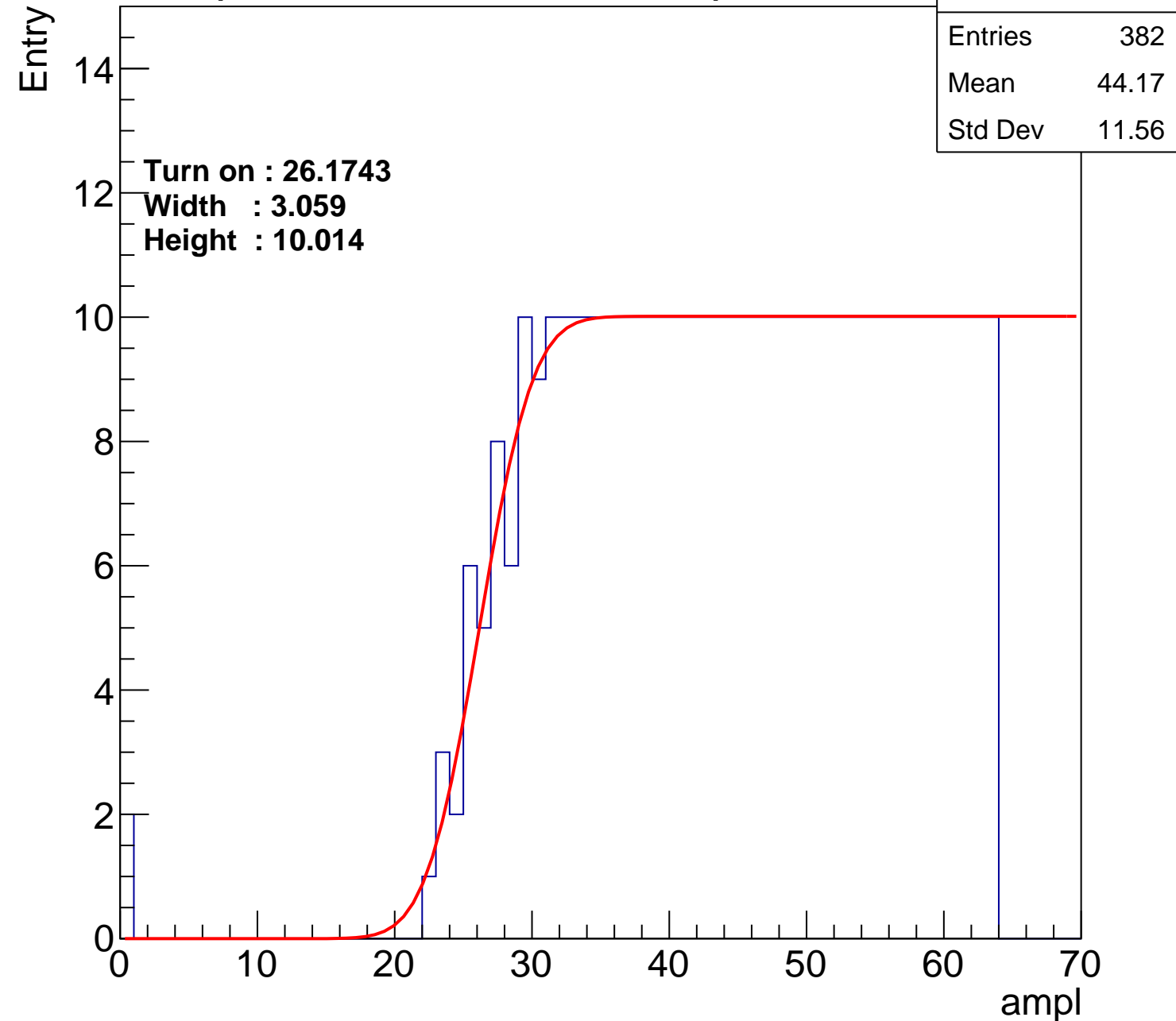
Width : 3.059

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch42

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.79
Std Dev	11.96

Turn on : 25.7133

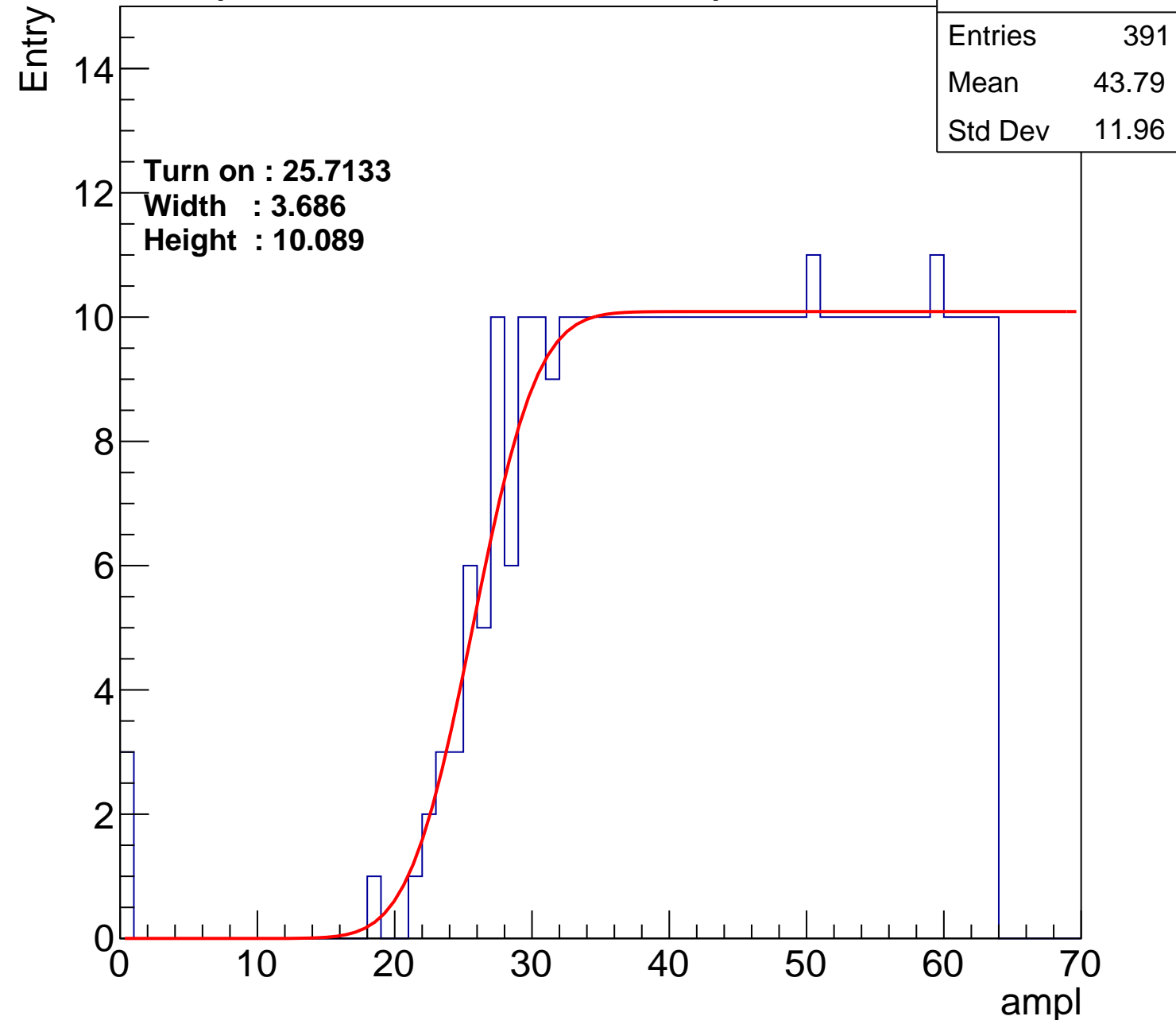
Width : 3.686

Height : 10.089

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch43

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.57
Std Dev	11.28

**Turn on : 26.9485**

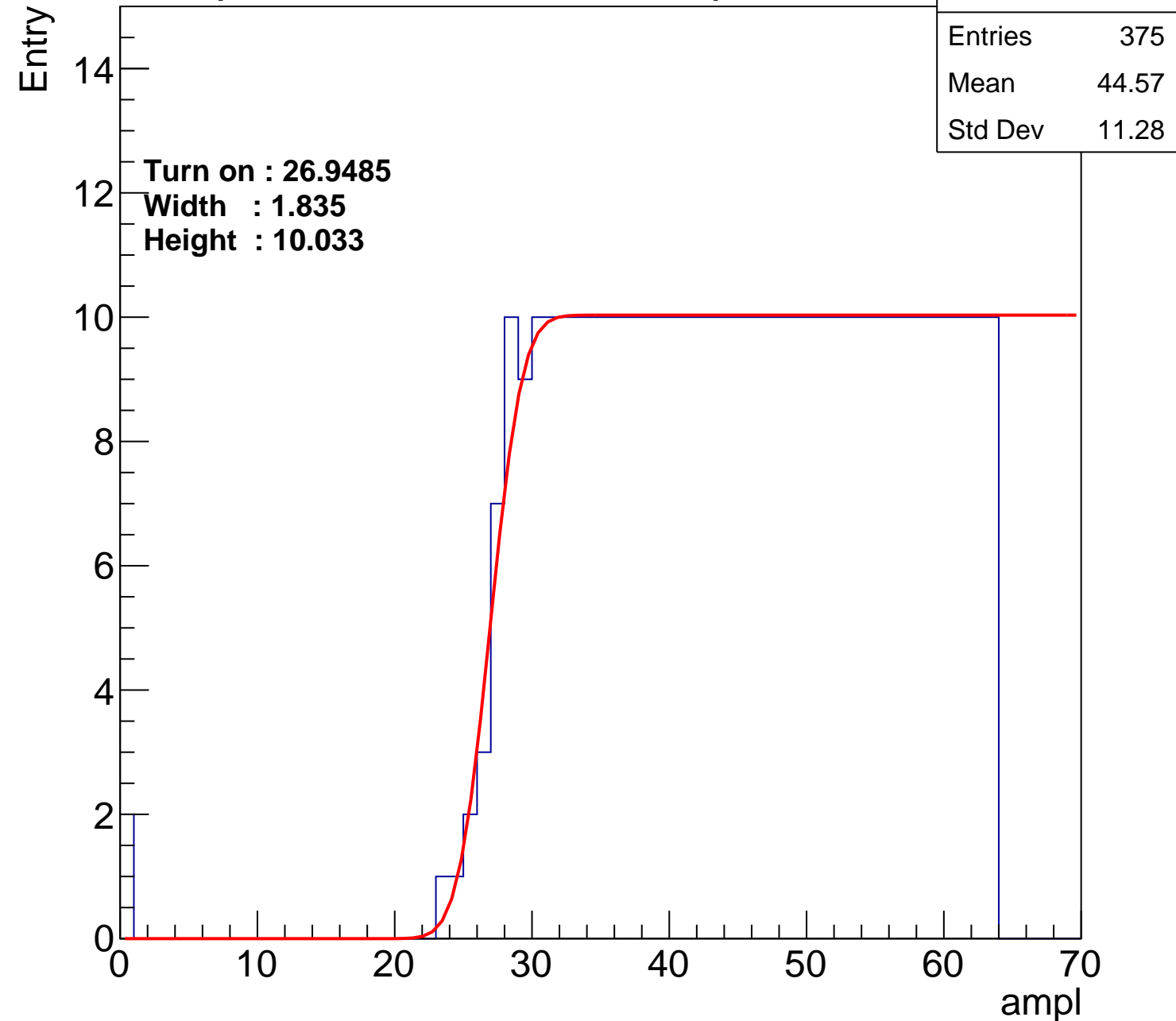
**Width : 1.835**

**Height : 10.033**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch44

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.17
Std Dev	11.72

Turn on : 26.0903

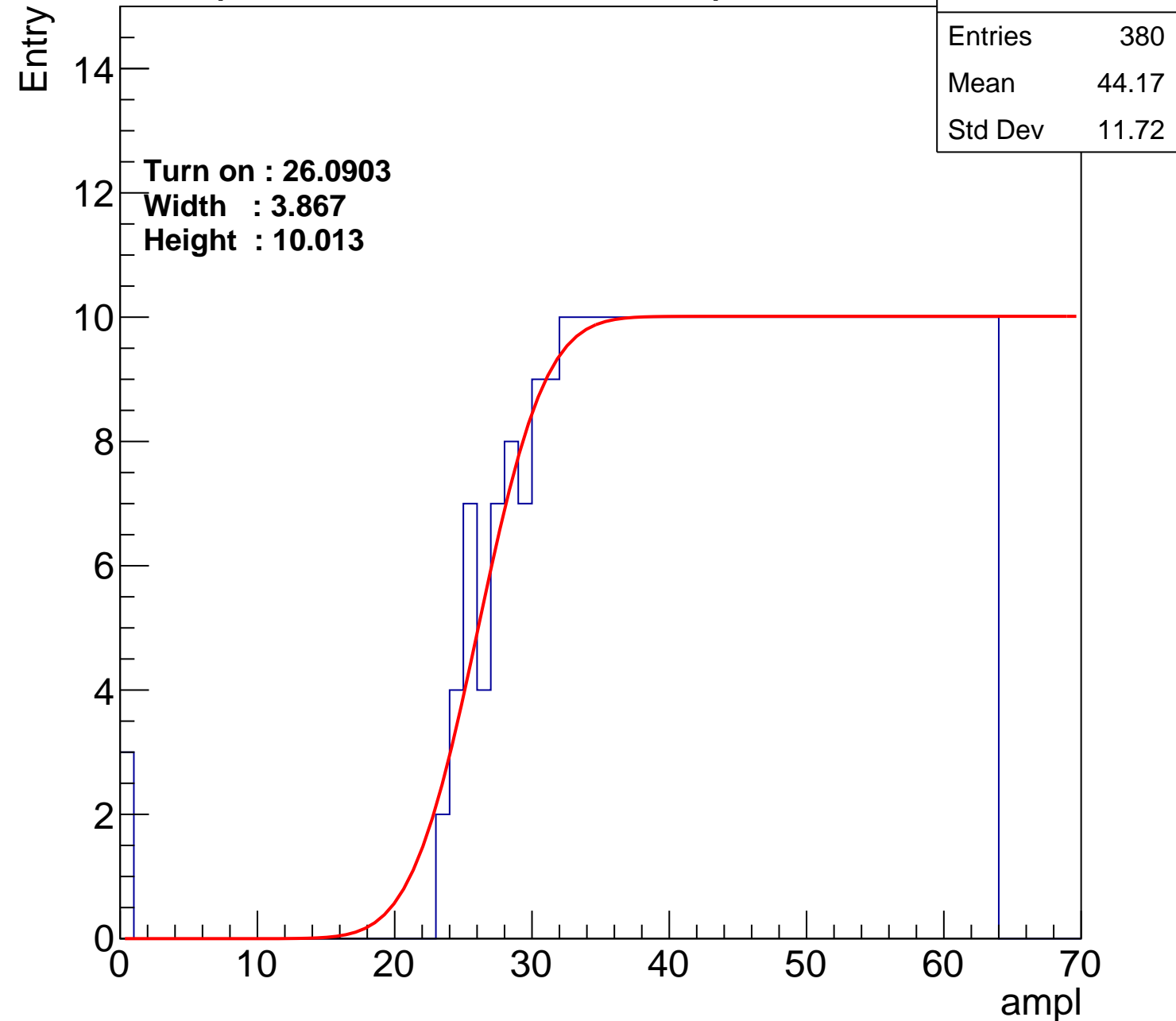
Width : 3.867

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch45

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	43.81
Std Dev	12.34

Turn on : 26.6579

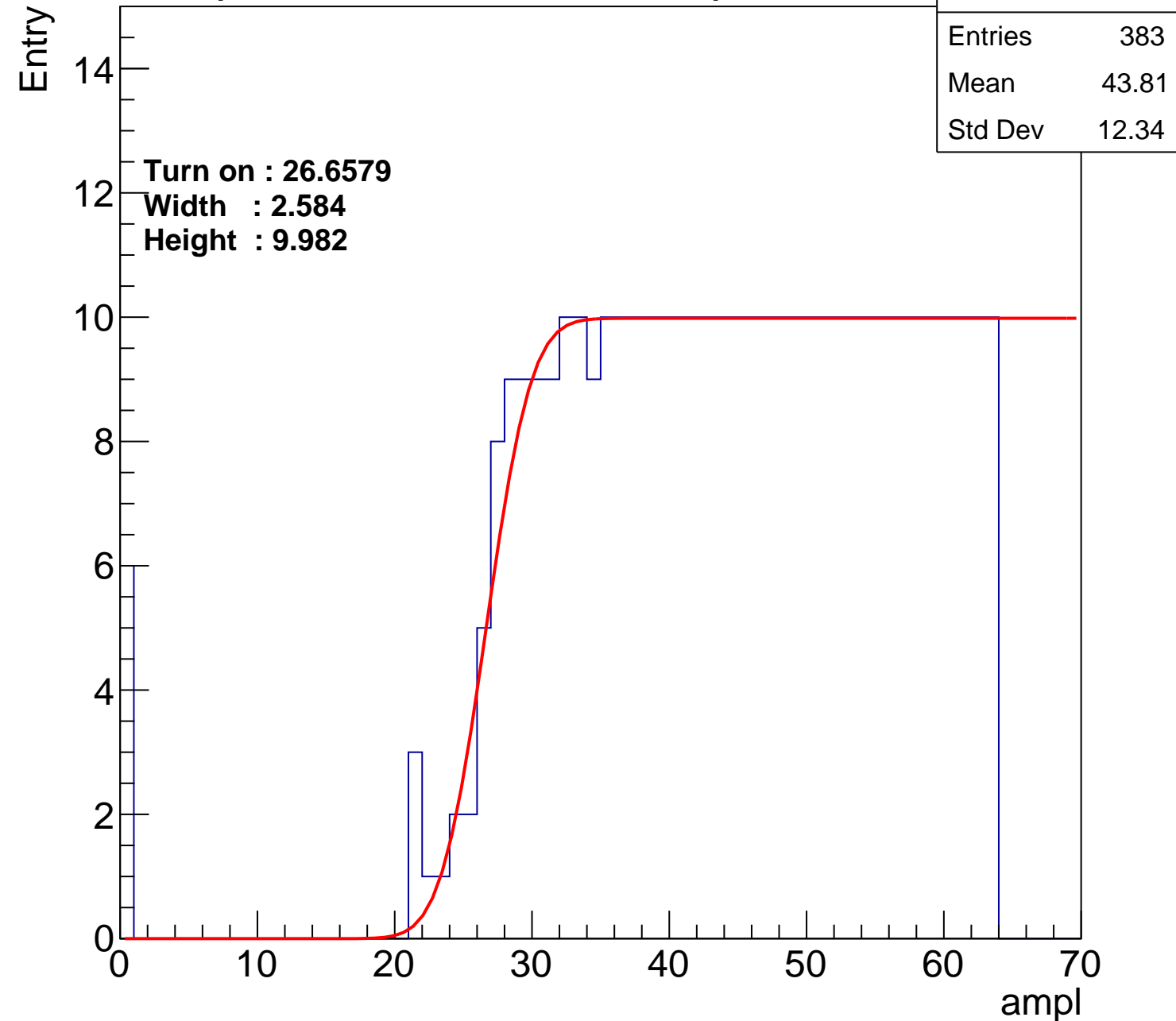
Width : 2.584

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch46

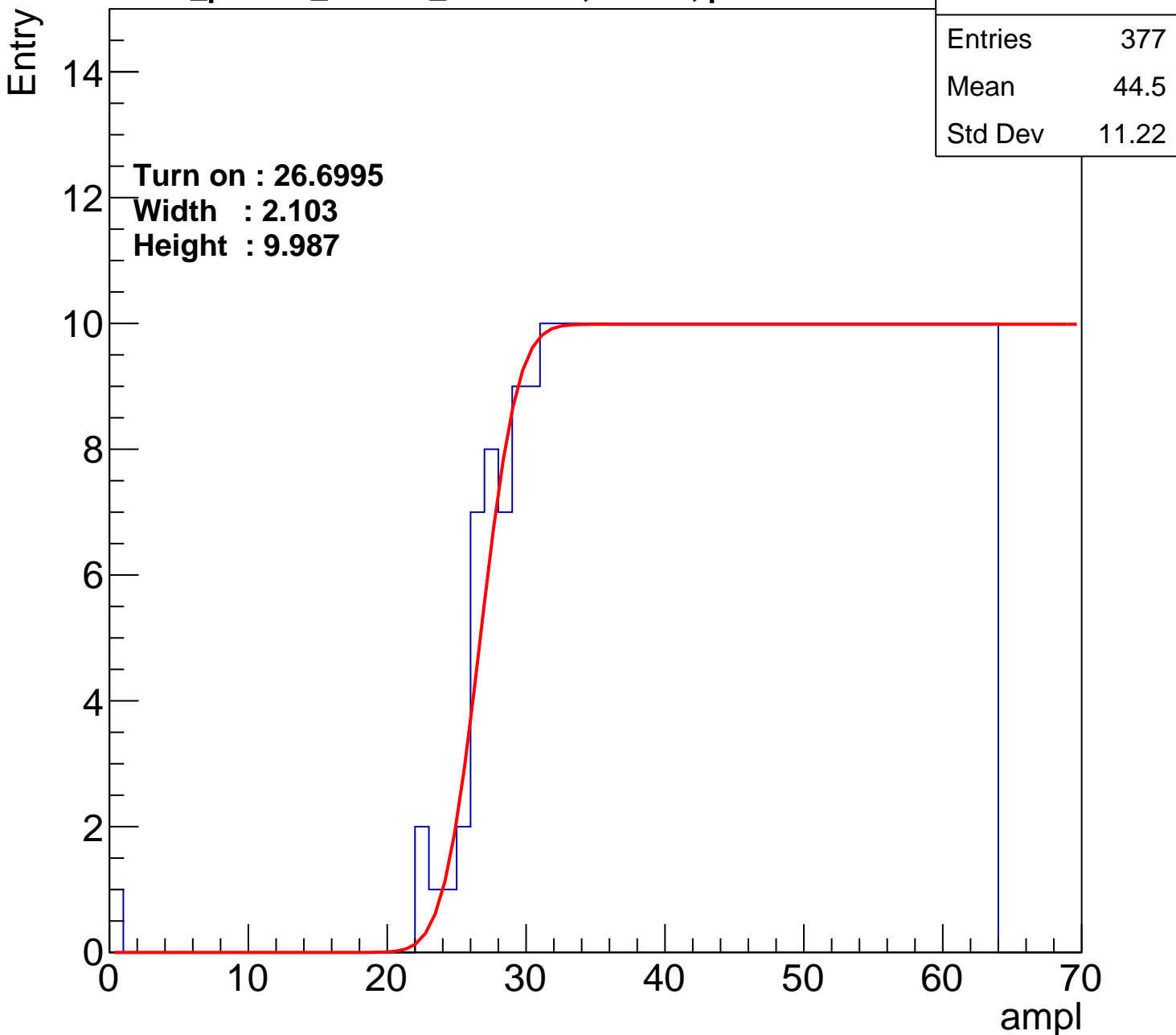
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

**Turn on : 26.6995**

**Width : 2.103**

**Height : 9.987**

Entries	377
Mean	44.5
Std Dev	11.22





# B0L102S, U18-ch47

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 27.2726

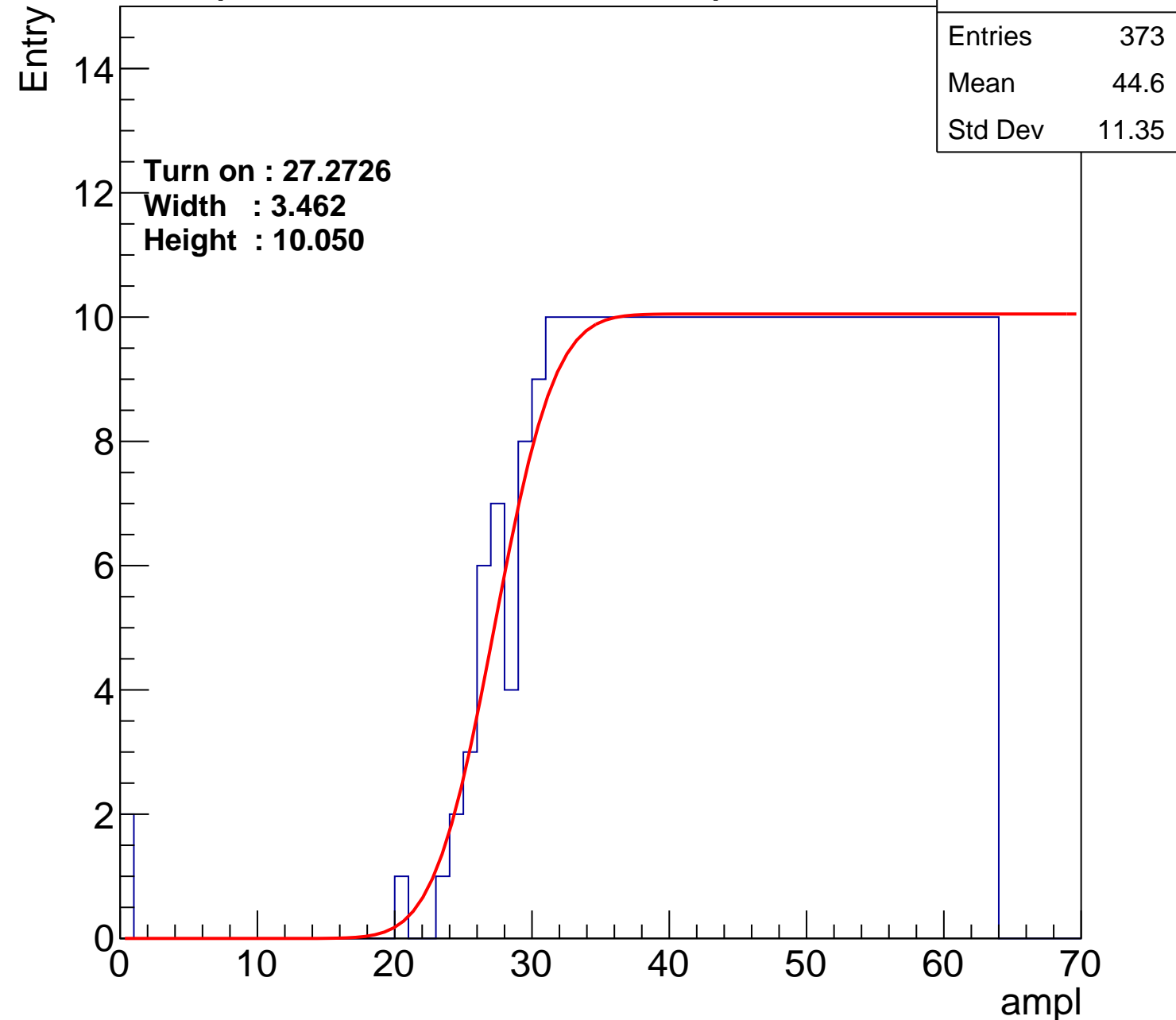
Width : 3.462

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch48

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	43.9
Std Dev	12.25

Turn on : 27.1844

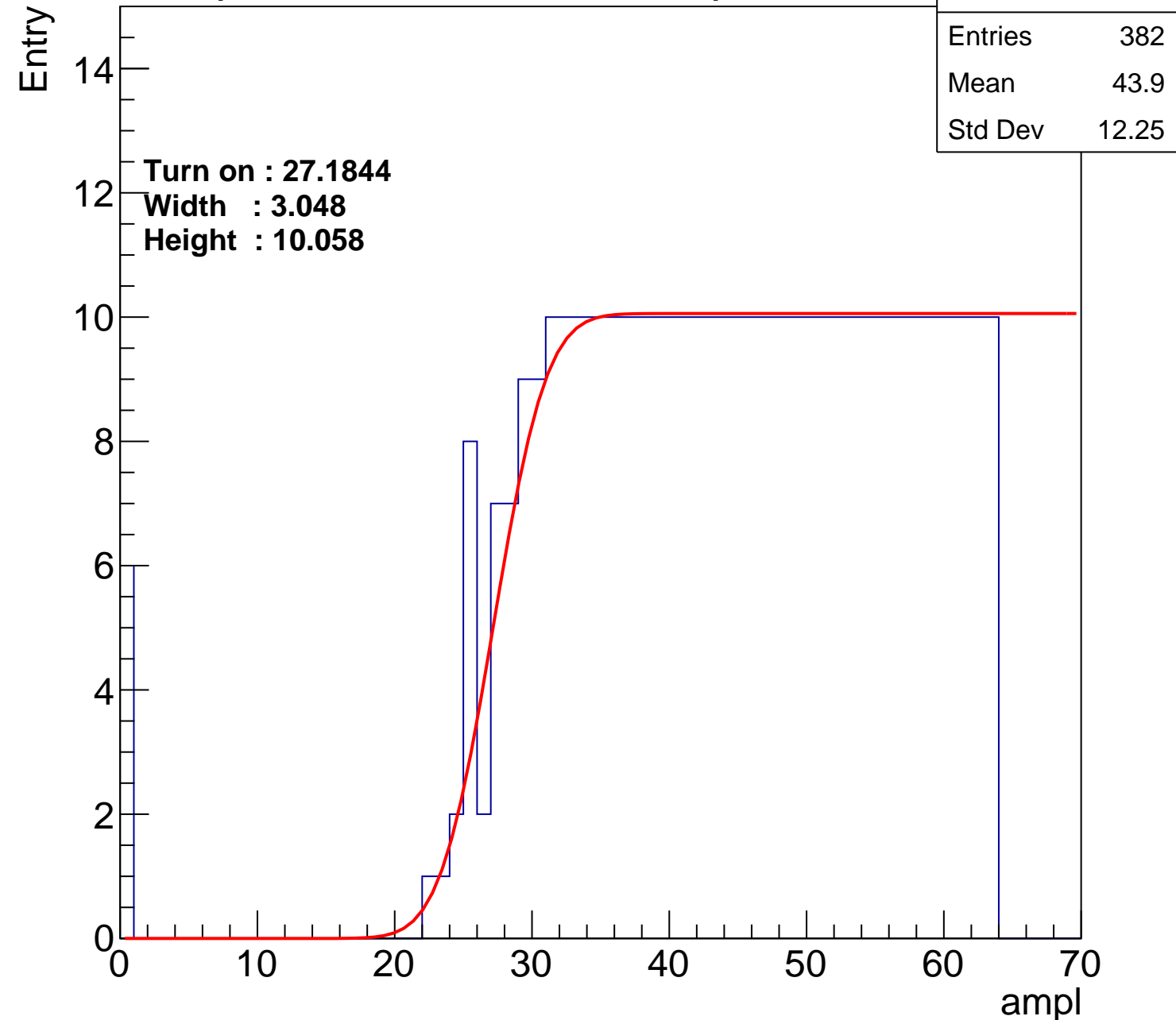
Width : 3.048

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch49

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.07
Std Dev	11.92

**Turn on : 26.9620**

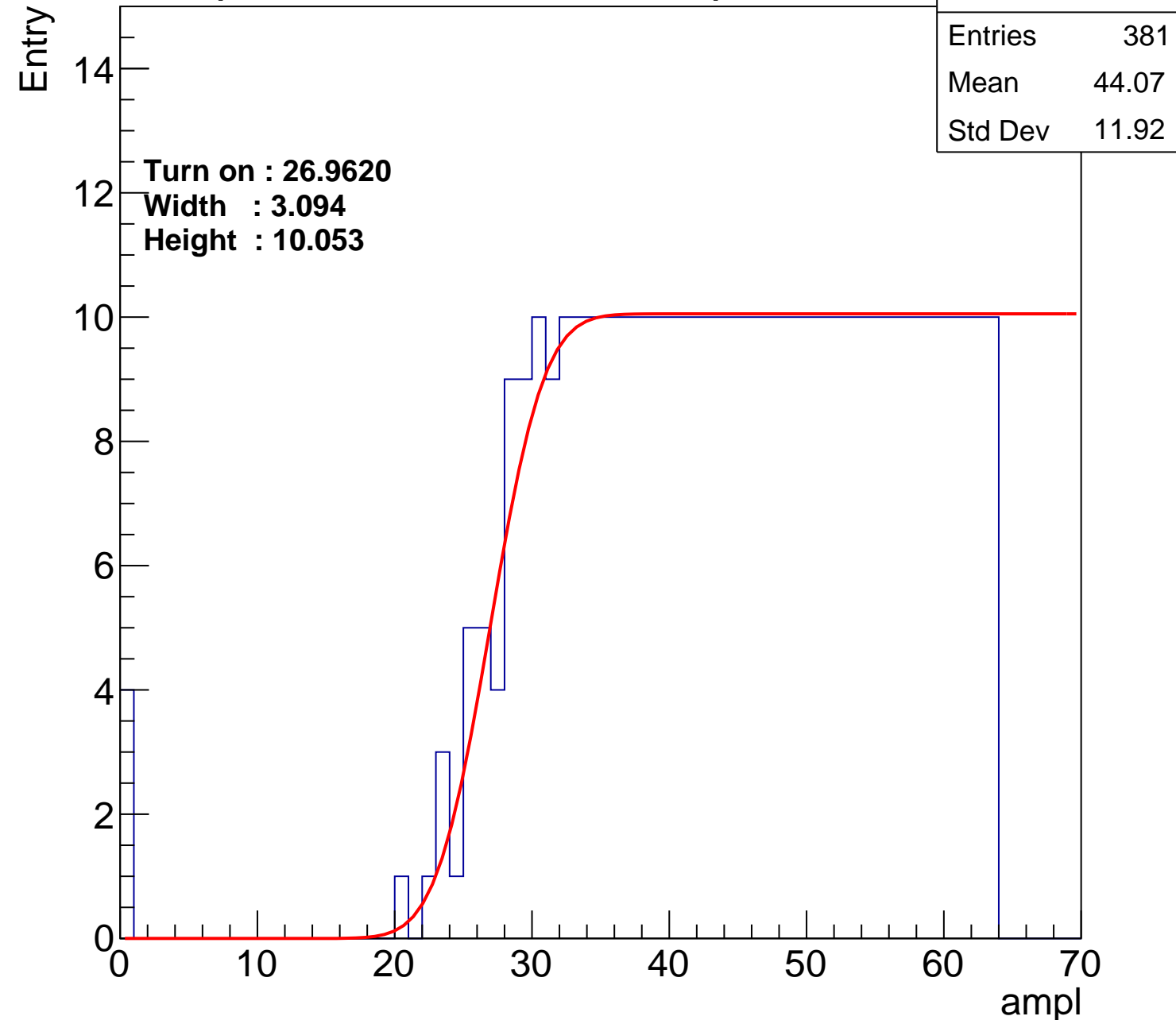
**Width : 3.094**

**Height : 10.053**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch50

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	365
Mean	45.11
Std Dev	10.86

Turn on : 27.4862

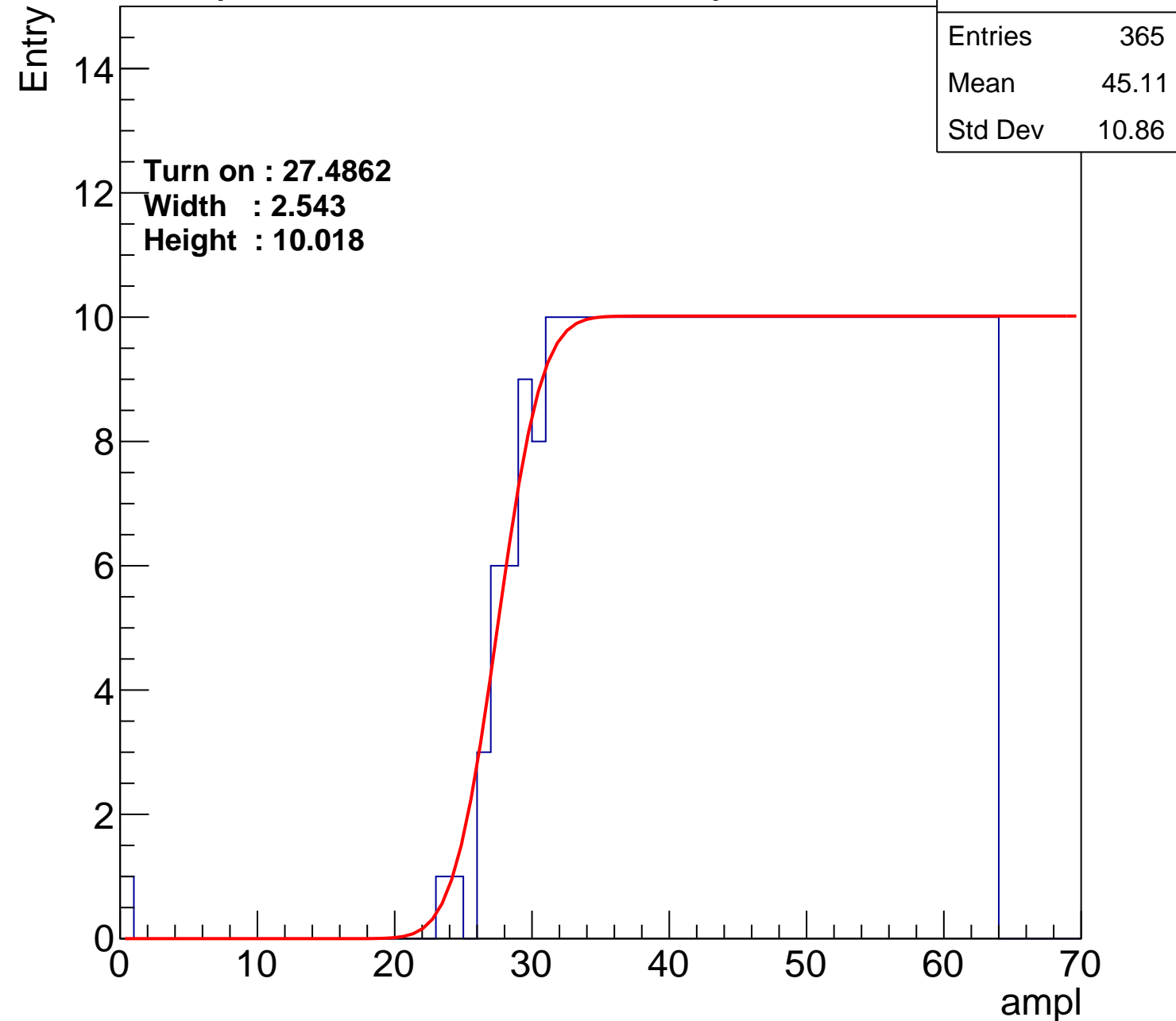
Width : 2.543

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch51

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.44
Std Dev	12.19

Turn on : 24.5011

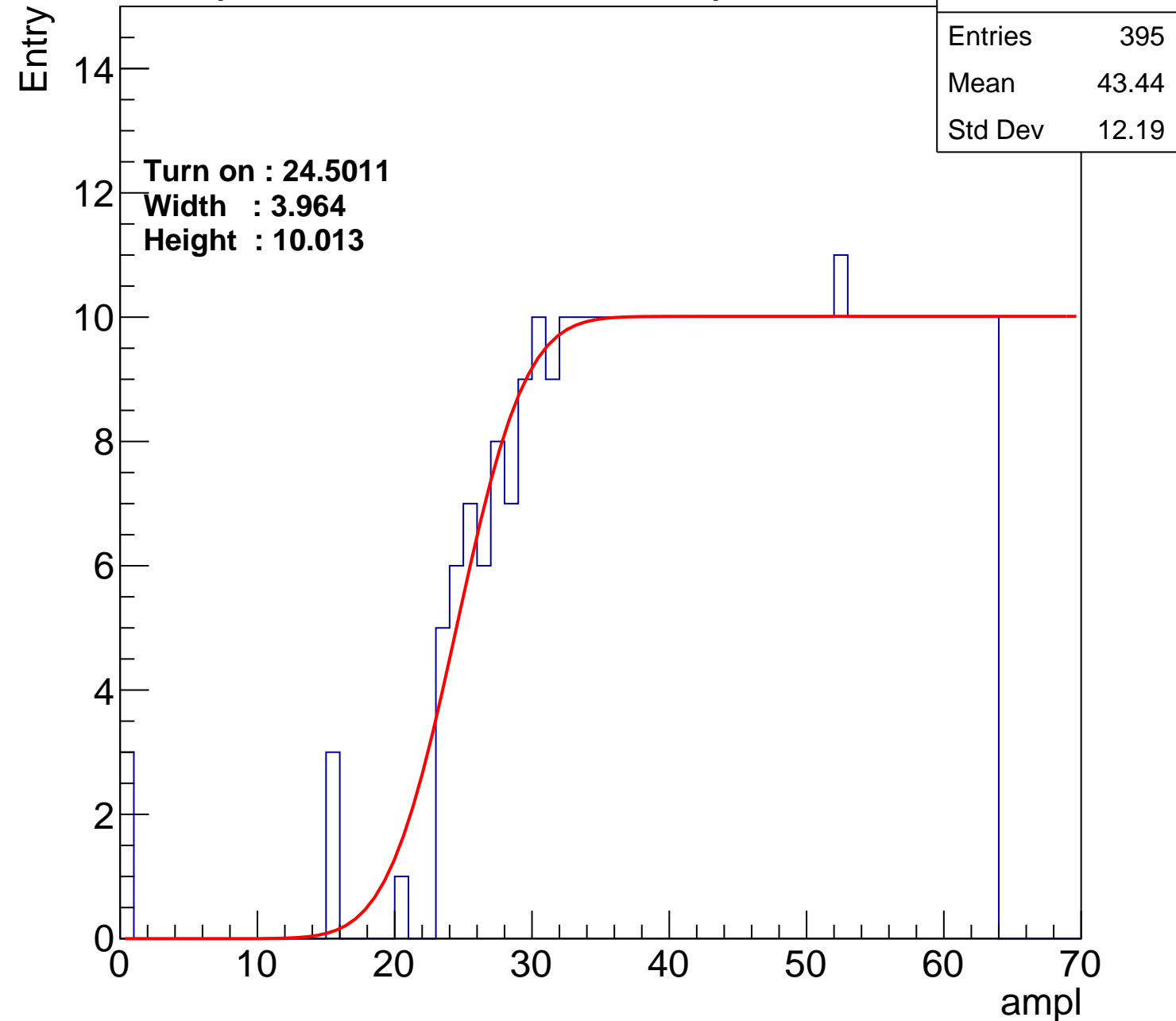
Width : 3.964

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch52

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.81
Std Dev	11.89

Turn on : 25.5063

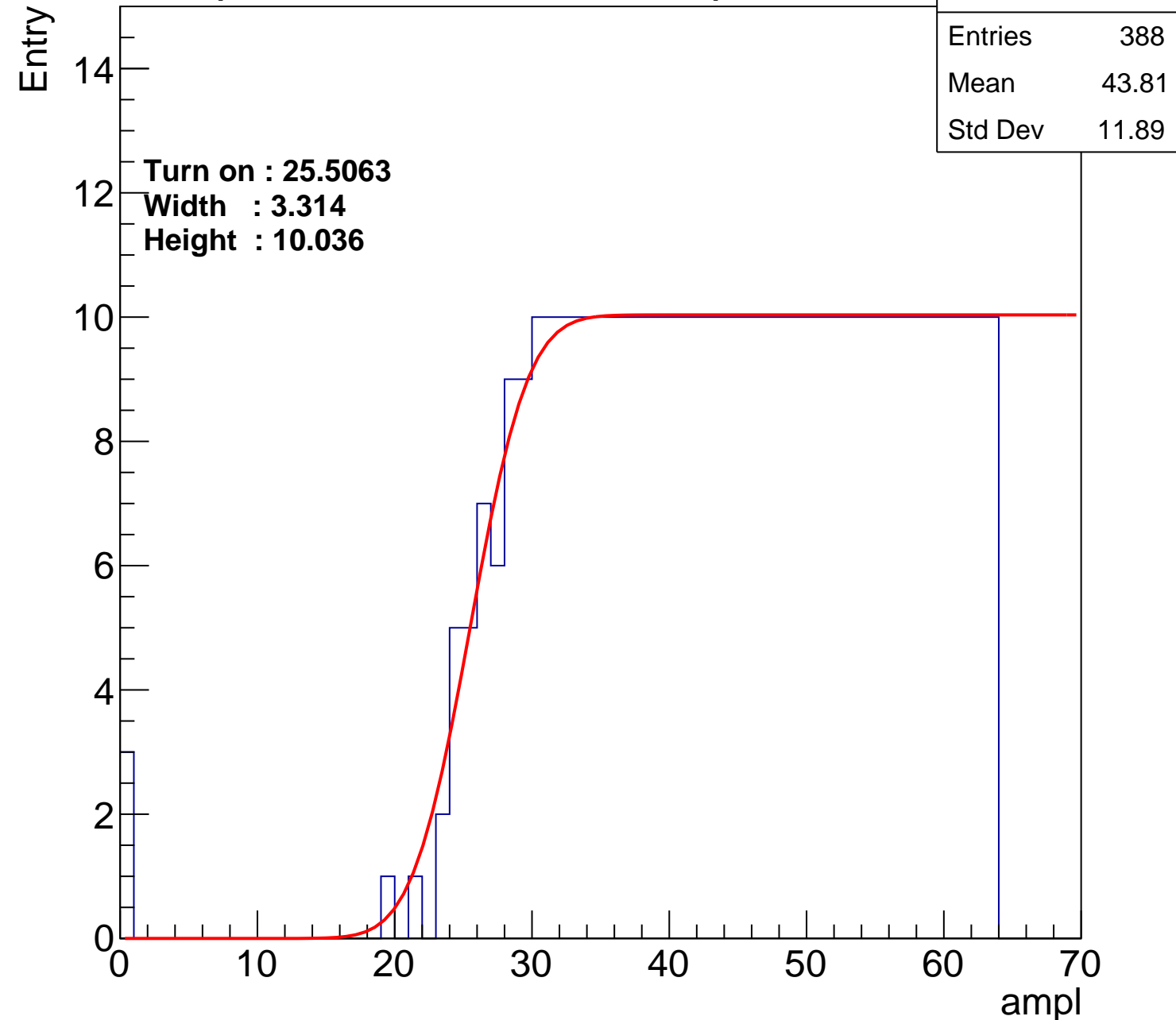
Width : 3.314

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch53

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.65
Std Dev	12.21

**Turn on : 26.2843**

**Width : 2.974**

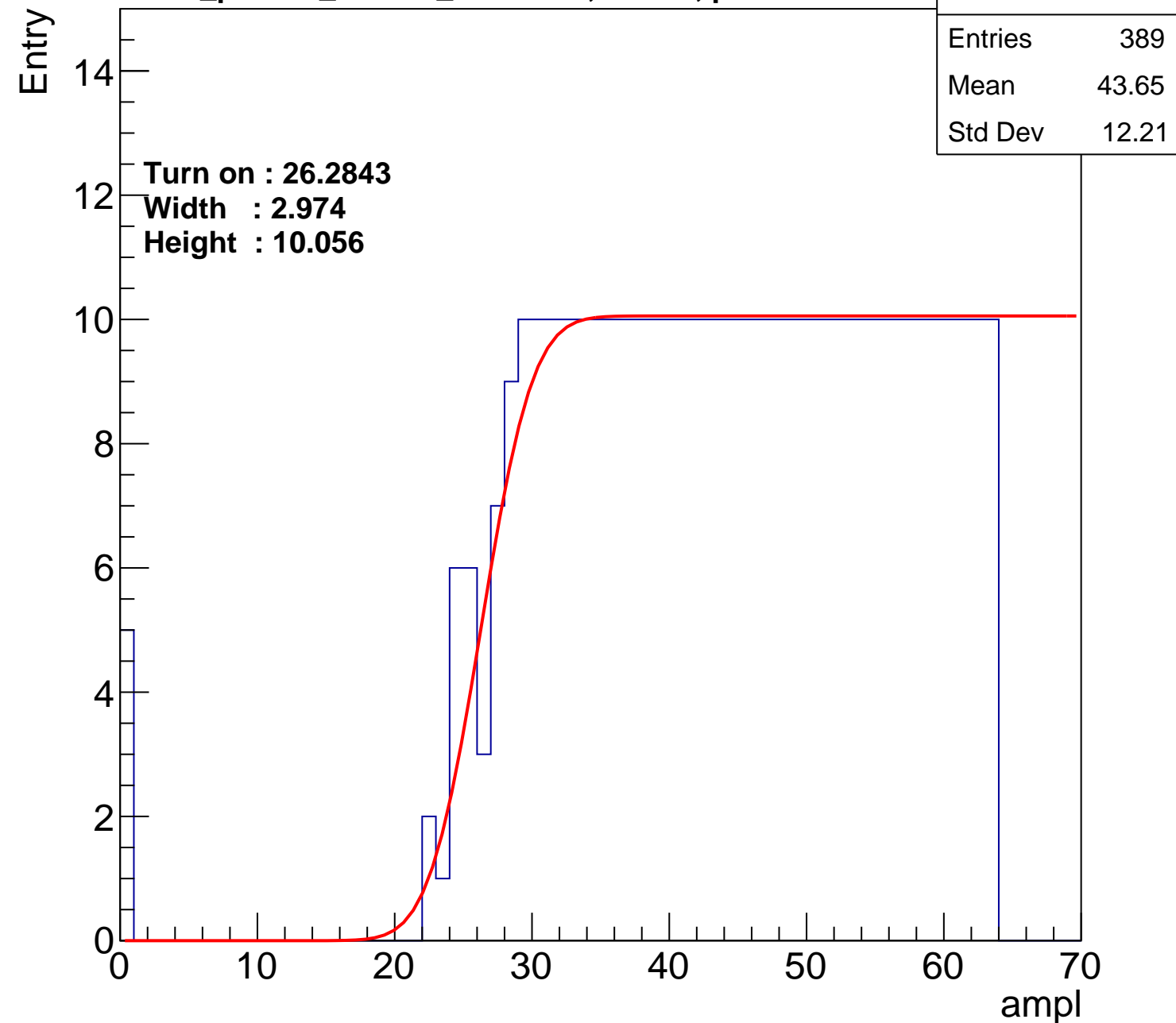
**Height : 10.056**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U18-ch54

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	43.96
Std Dev	12.05

Turn on : 26.4193

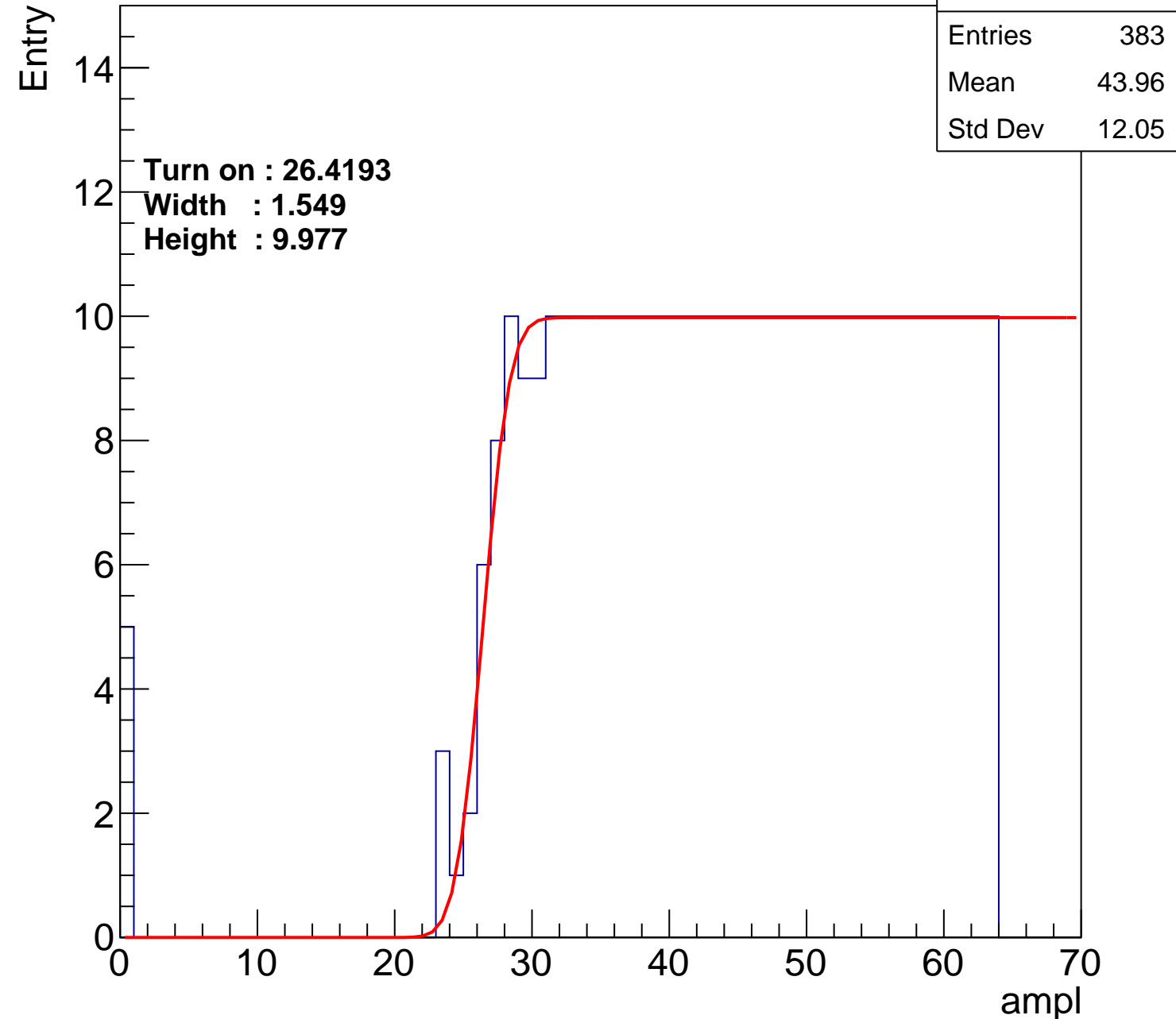
Width : 1.549

Height : 9.977

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch55

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.05
Std Dev	11.72

**Turn on : 26.0160**

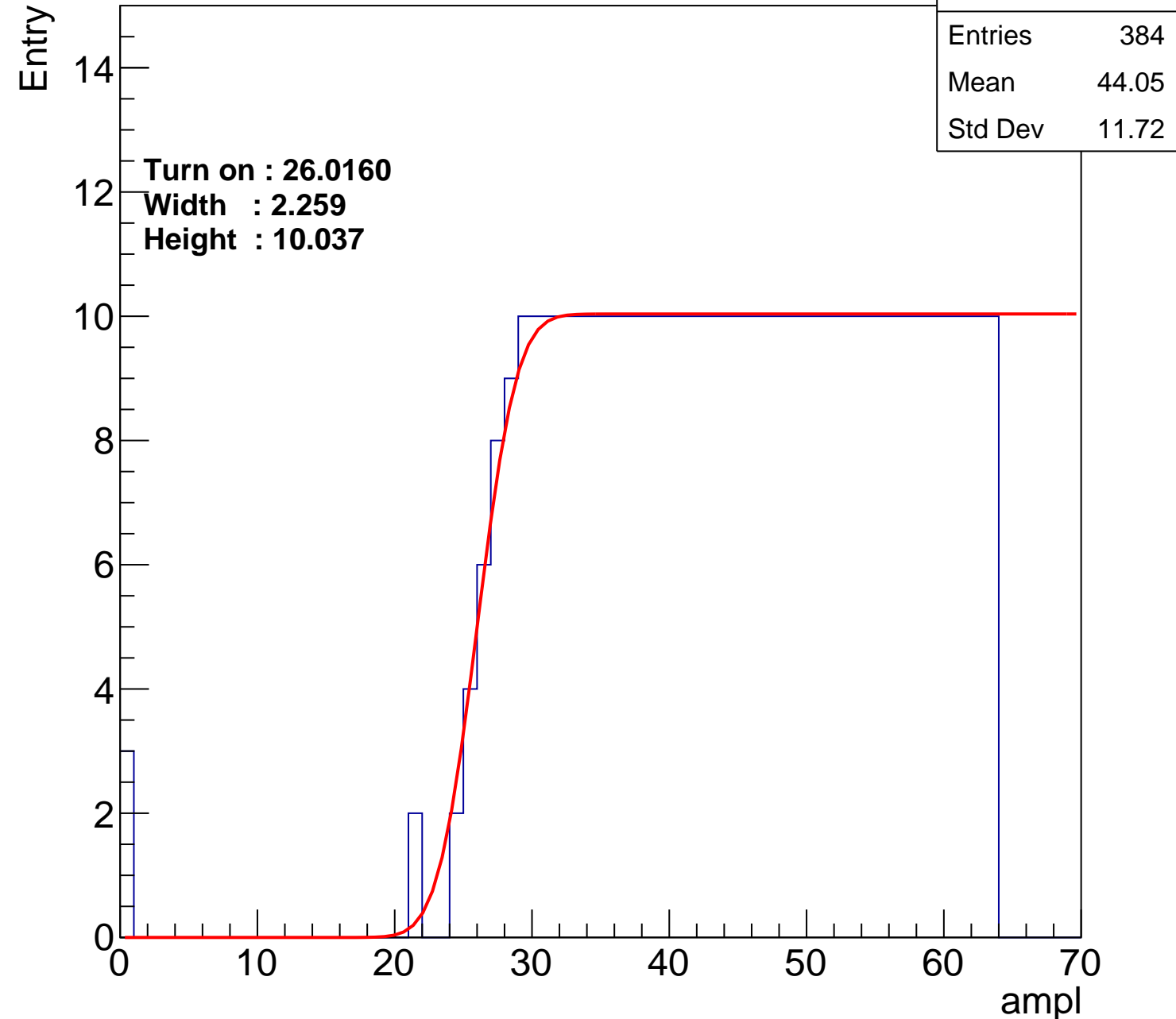
**Width : 2.259**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch56

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	43.94
Std Dev	11.96

Turn on : 25.5441

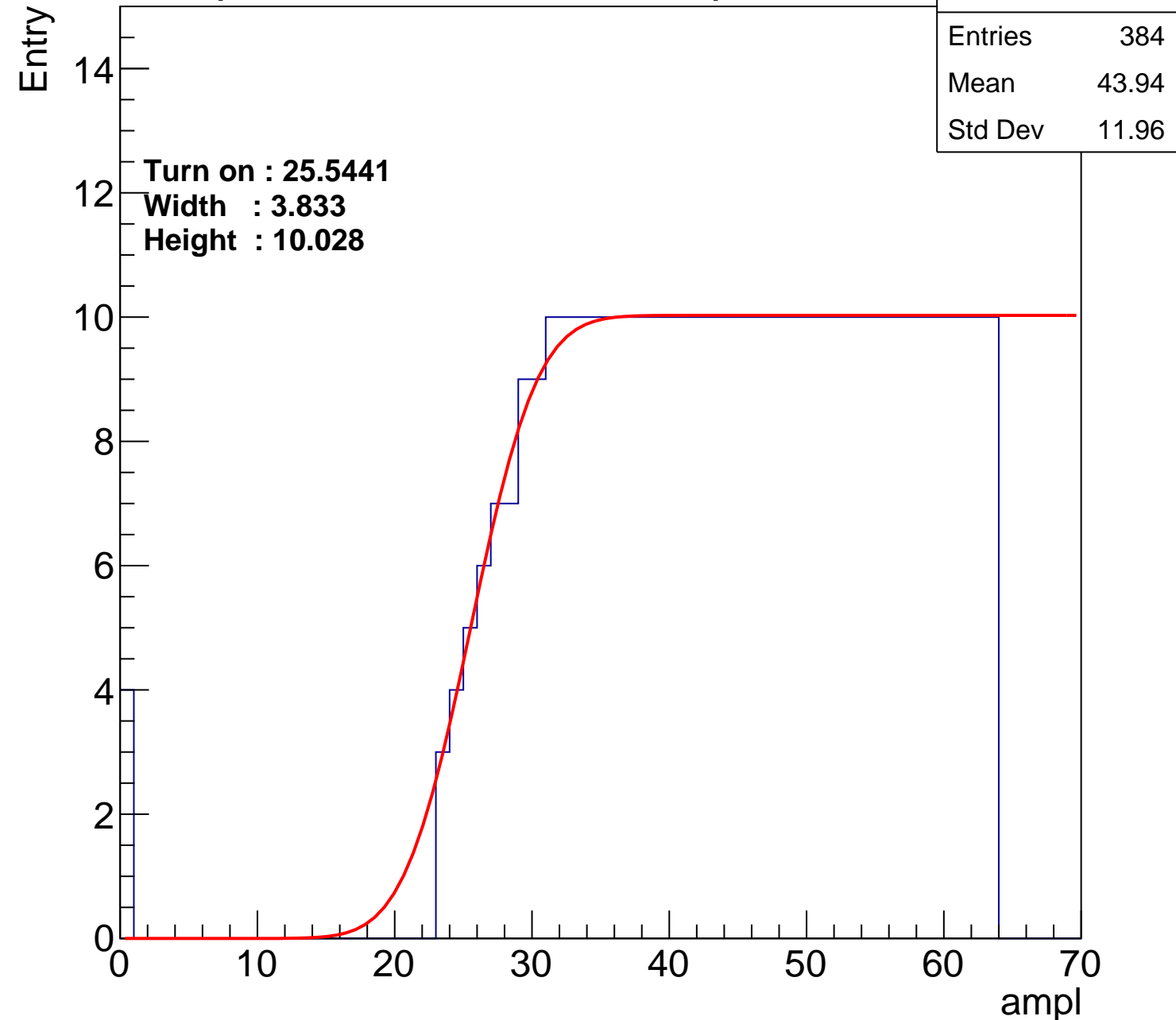
Width : 3.833

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch57

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.15
Std Dev	11.89

Turn on : 26.8792

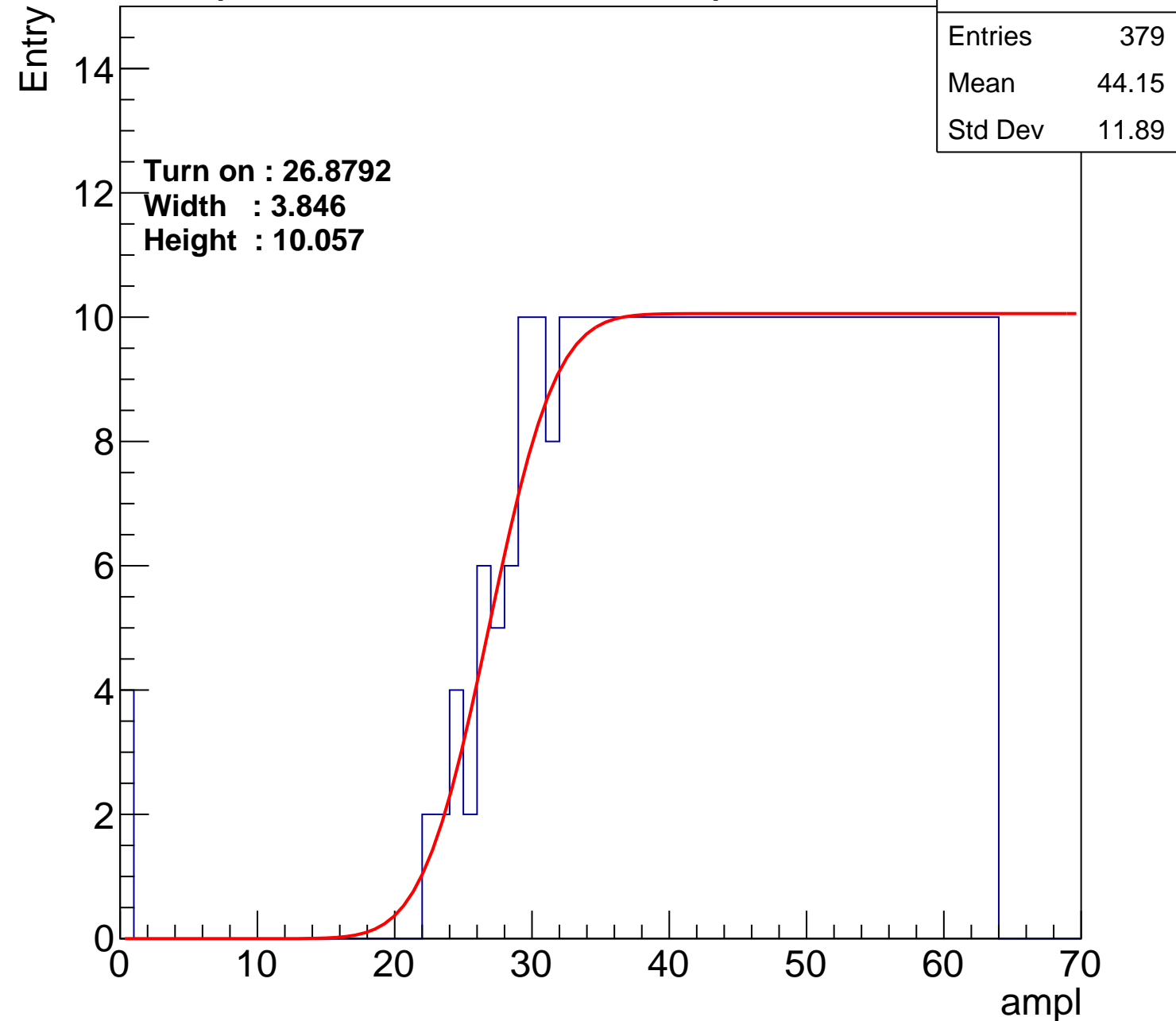
Width : 3.846

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch58

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.49
Std Dev	11.34

Turn on : 27.5095

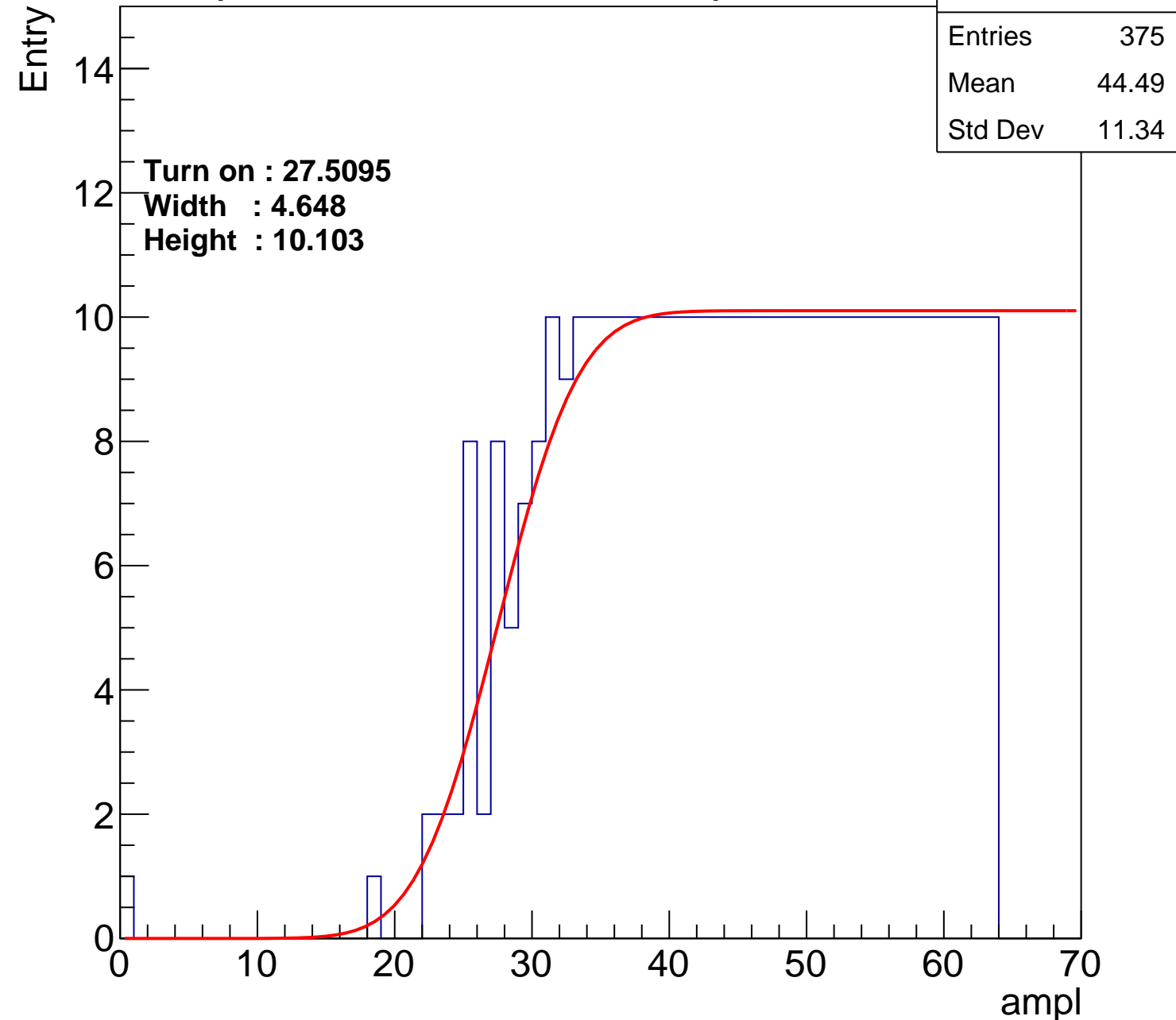
Width : 4.648

Height : 10.103

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch59

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.3531

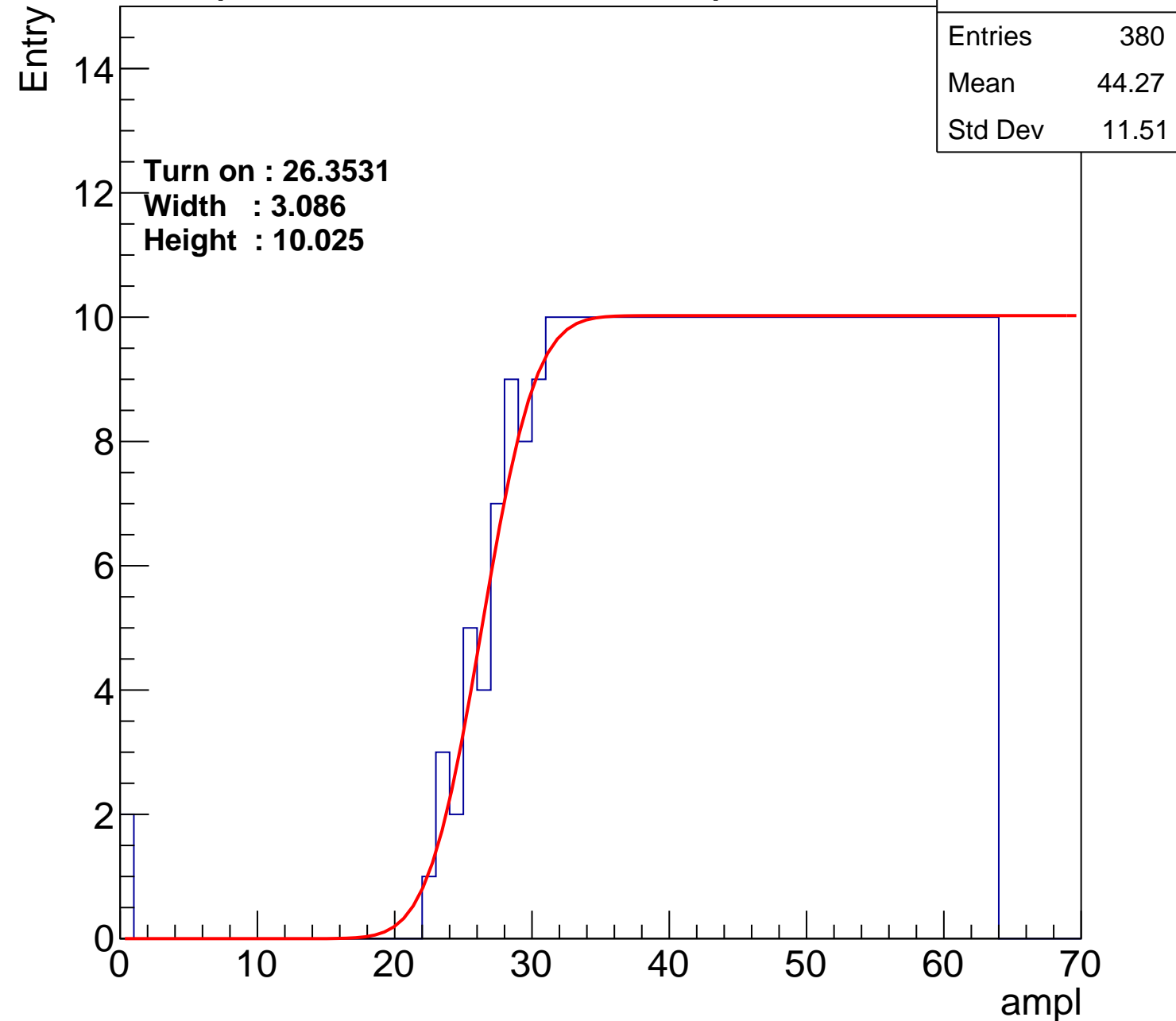
Width : 3.086

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch60

calib\_packv5\_042523\_0143.root, FC#12, port B1

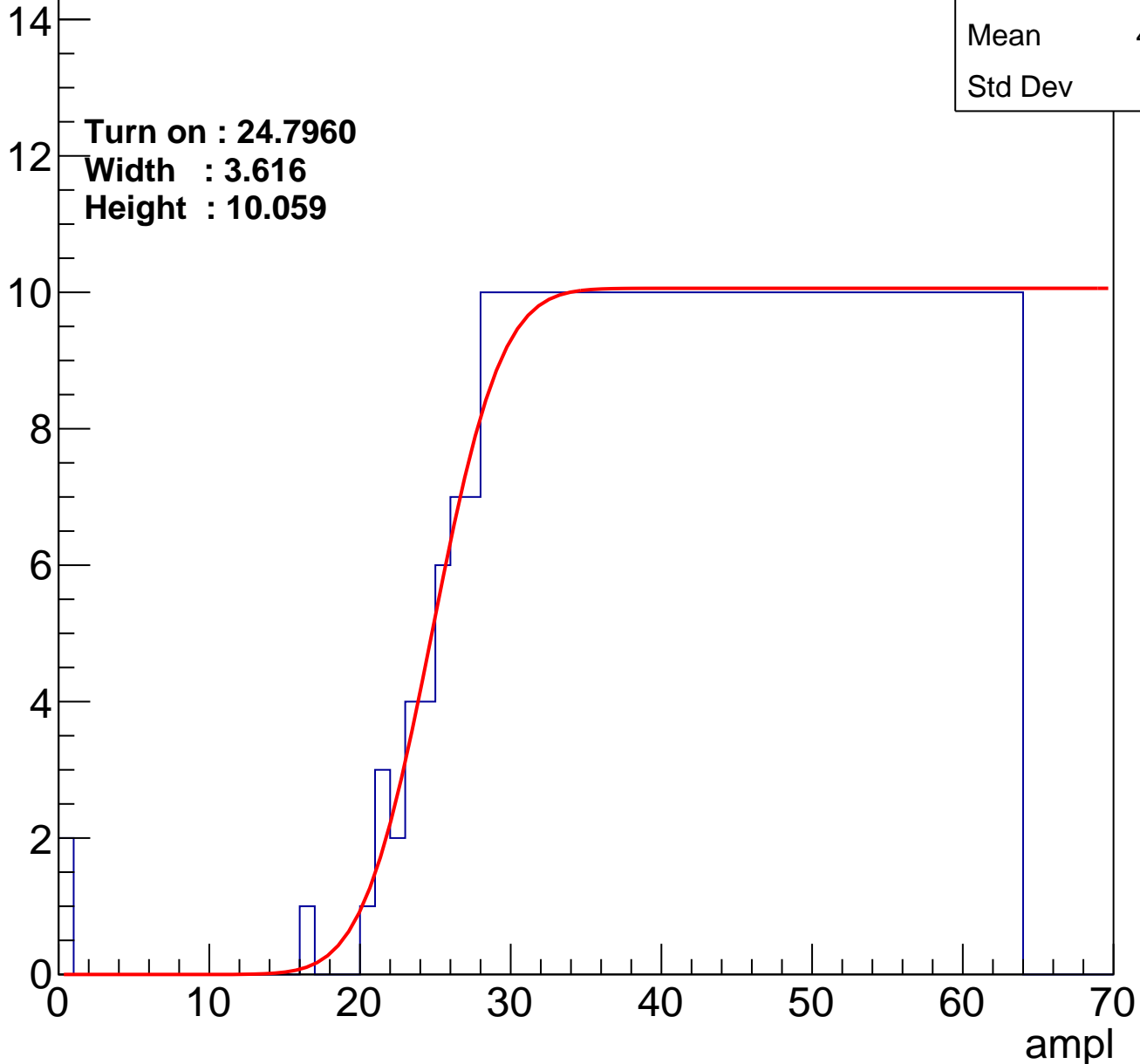
Entries	397
Mean	43.41
Std Dev	12

Turn on : 24.7960

Width : 3.616

Height : 10.059

Entry



# B0L102S, U18-ch61

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.55
Std Dev	12.2

Turn on : 25.6894

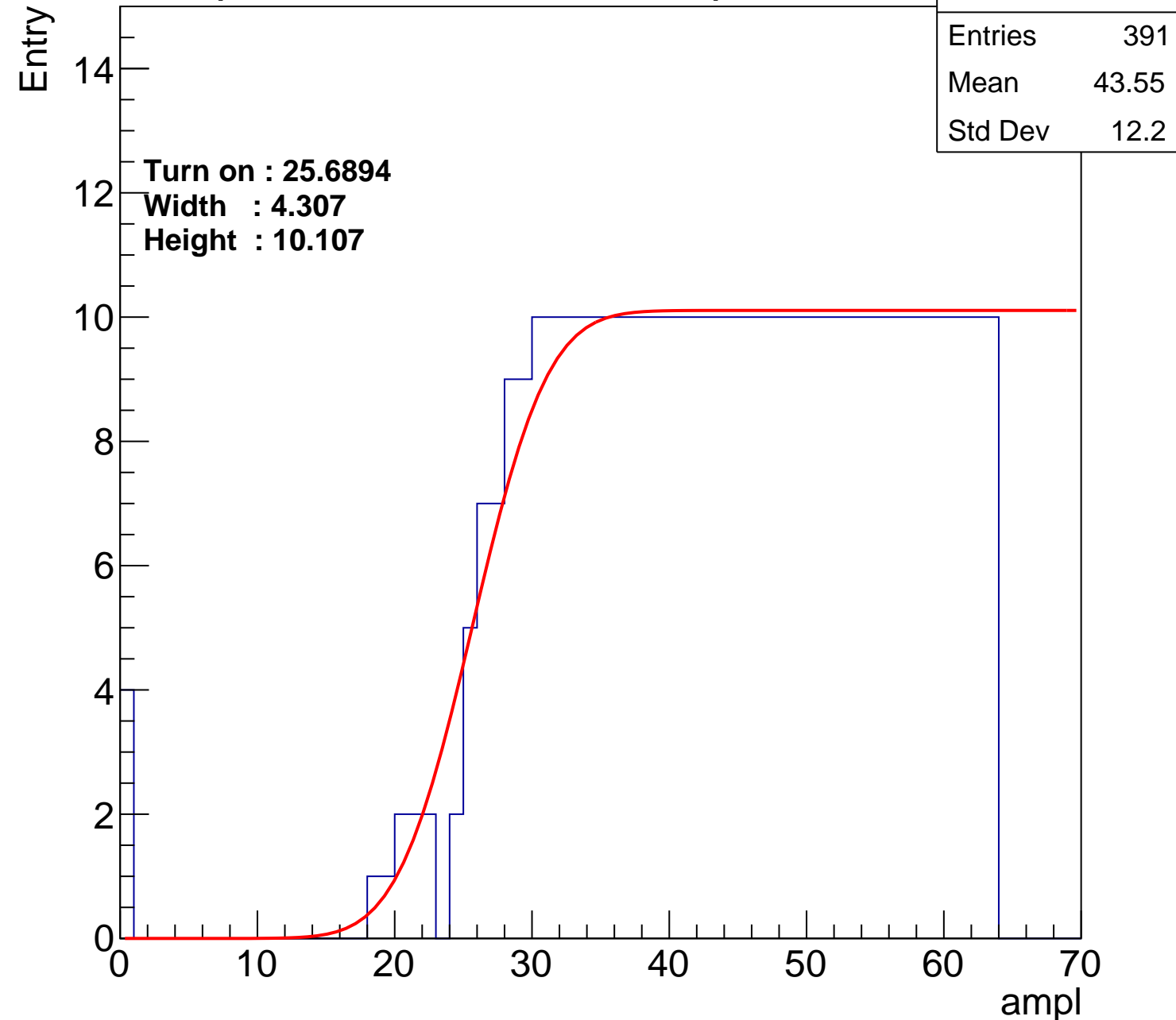
Width : 4.307

Height : 10.107

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch62

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.91
Std Dev	11.81

Turn on : 25.5727

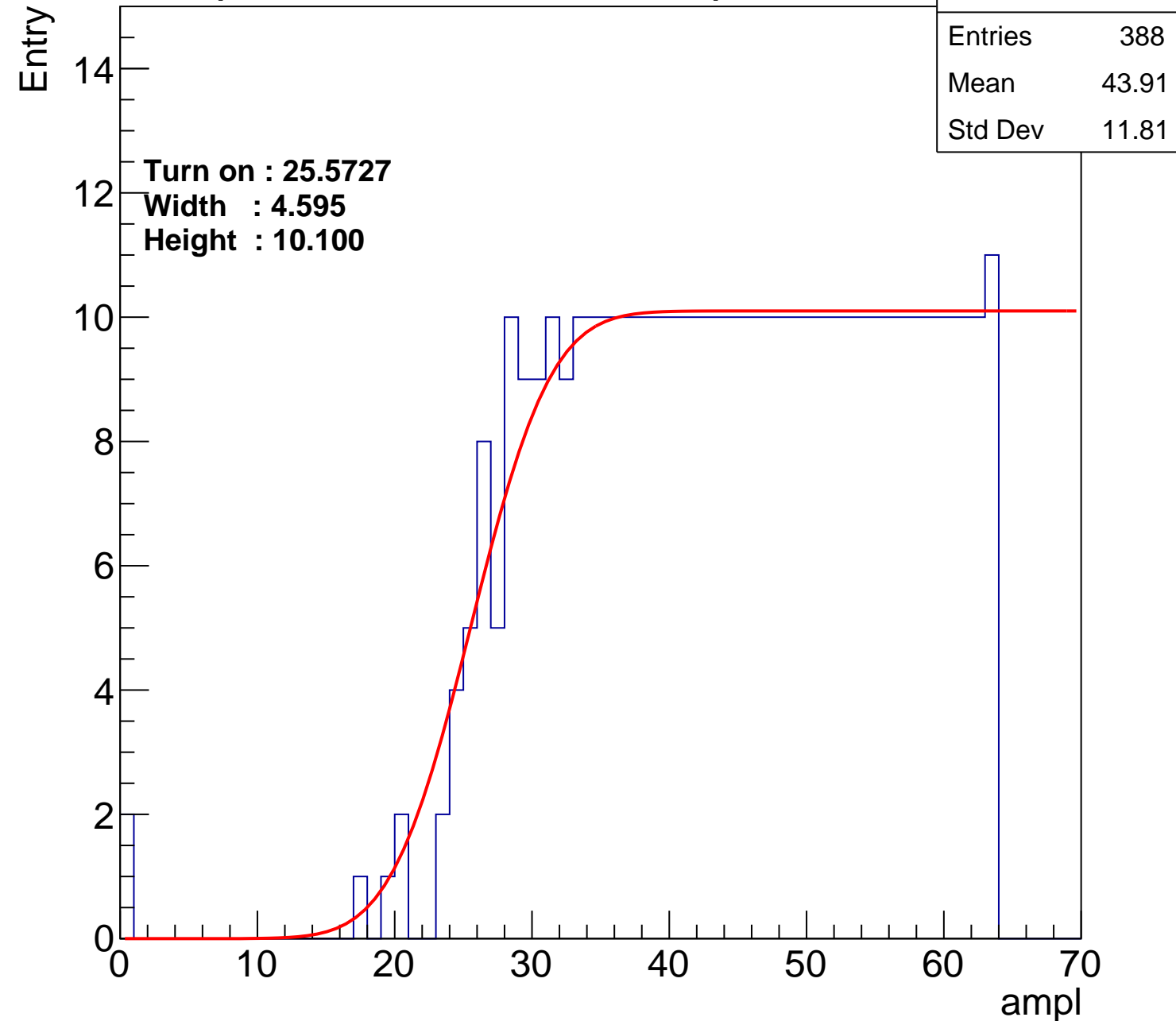
Width : 4.595

Height : 10.100

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch63

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.75
Std Dev	11.26

**Turn on : 27.5773**

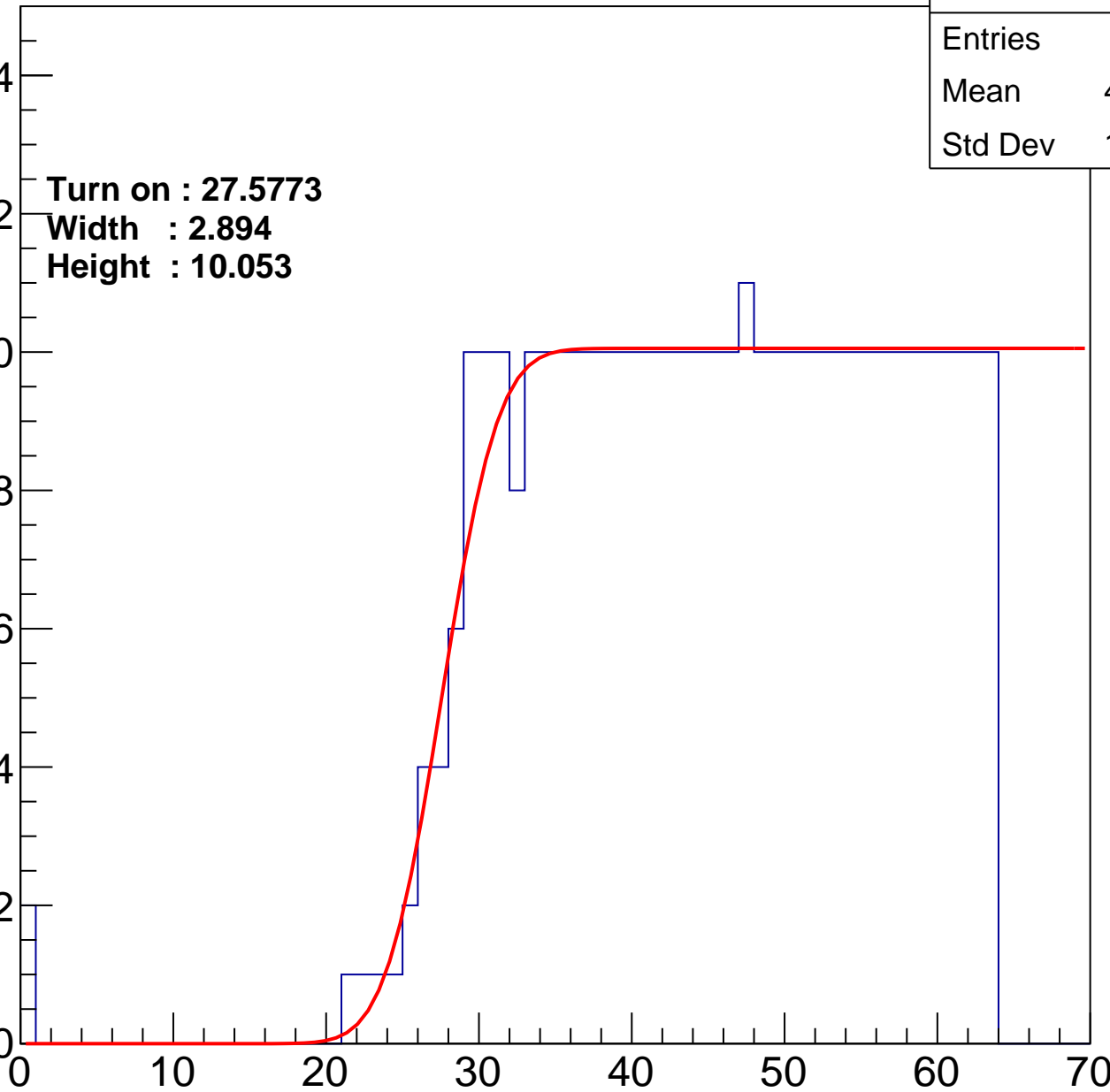
**Width : 2.894**

**Height : 10.053**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch64

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	399
Mean	43.05
Std Dev	12.77

Turn on : 24.9544

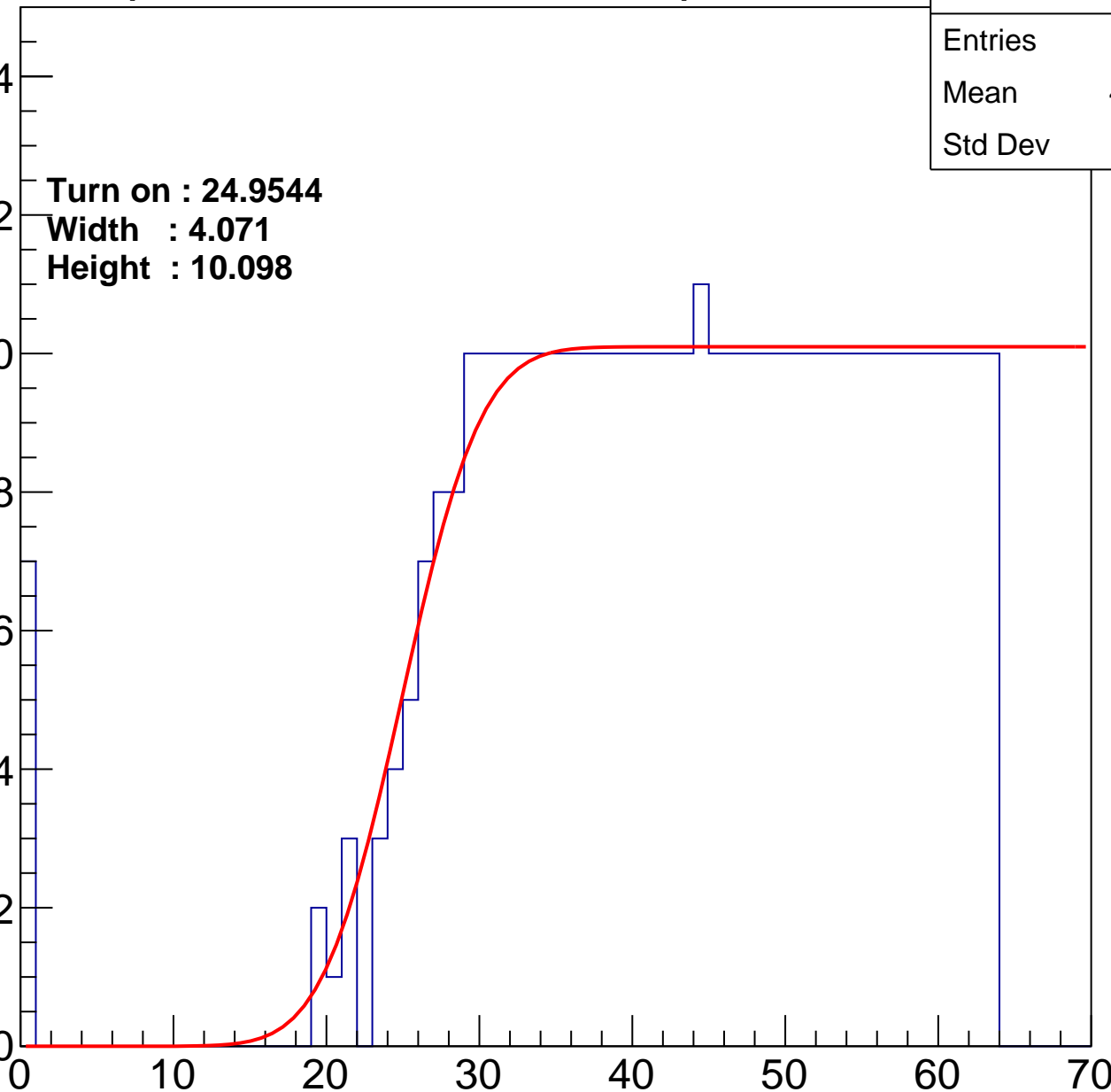
Width : 4.071

Height : 10.098

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch65

calib\_packv5\_042523\_0143.root, FC#12, port B1

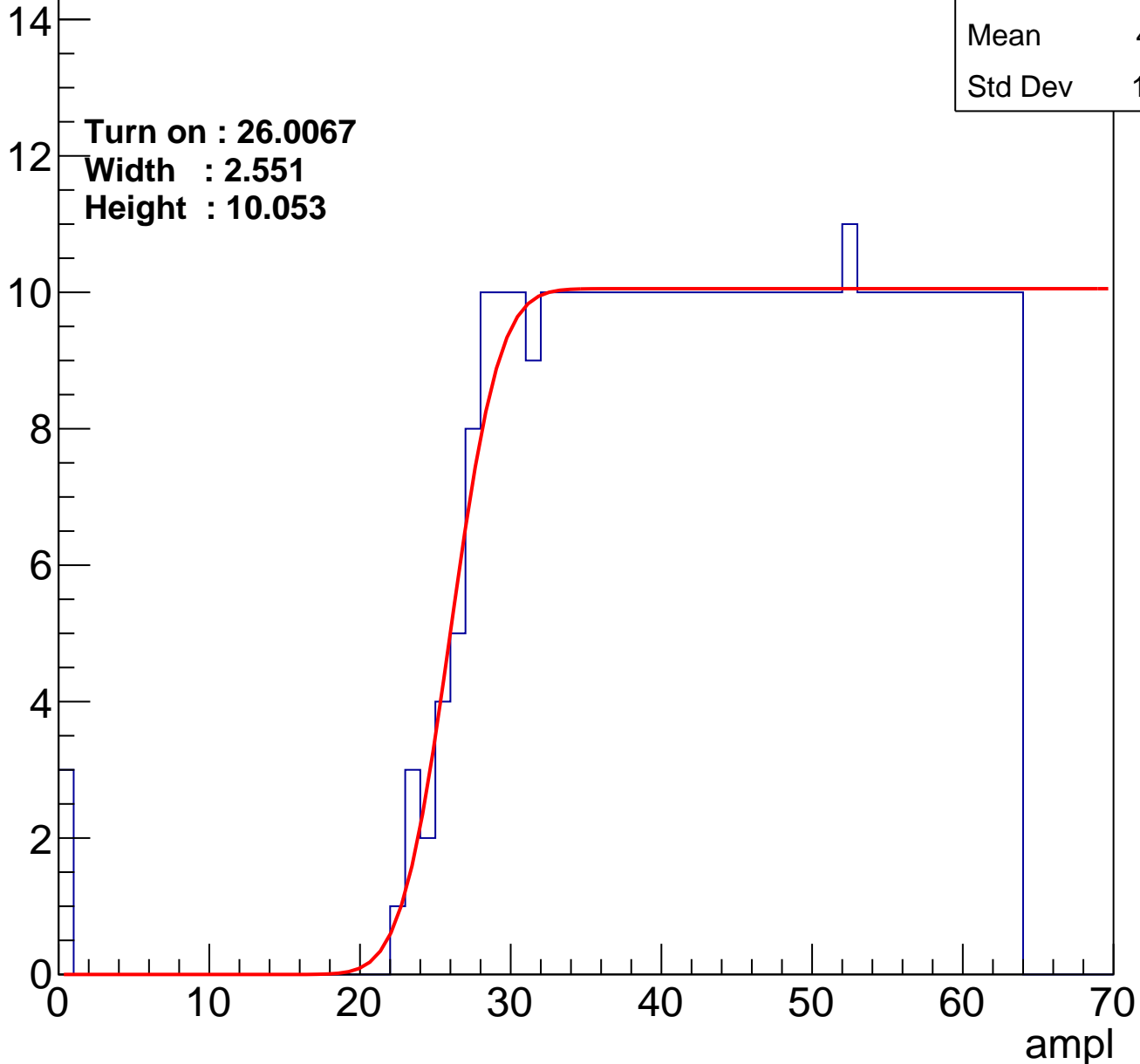
Entries	386
Mean	44.01
Std Dev	11.76

Turn on : 26.0067

Width : 2.551

Height : 10.053

Entry



# B0L102S, U18-ch66

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	359
Mean	45.05
Std Dev	11.64

**Turn on : 28.5477**

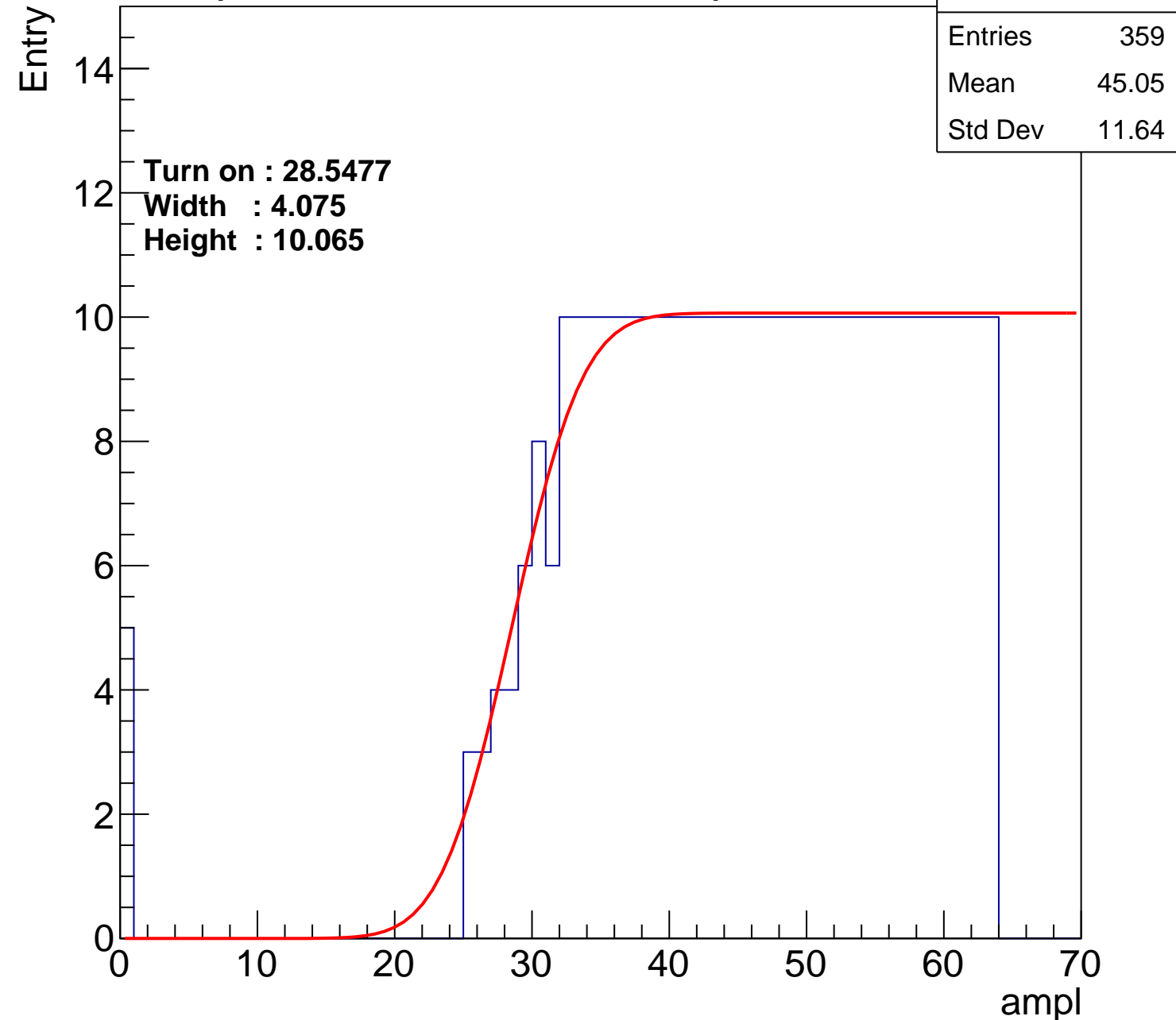
**Width : 4.075**

**Height : 10.065**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch67

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.56
Std Dev	11.79

**Turn on : 25.2894**

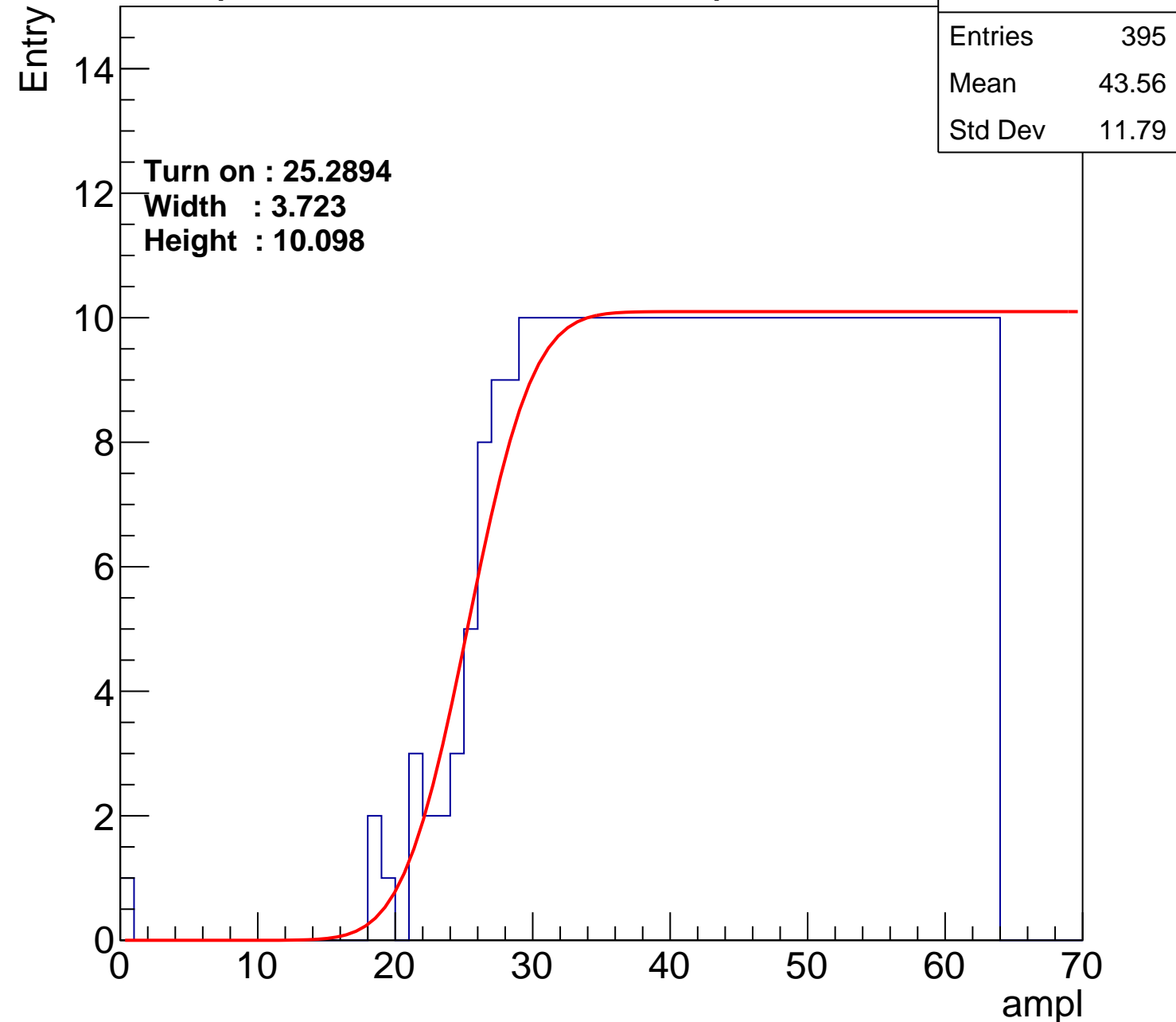
**Width : 3.723**

**Height : 10.098**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch68

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	411
Mean	42.62
Std Dev	12.61

Turn on : 23.9848

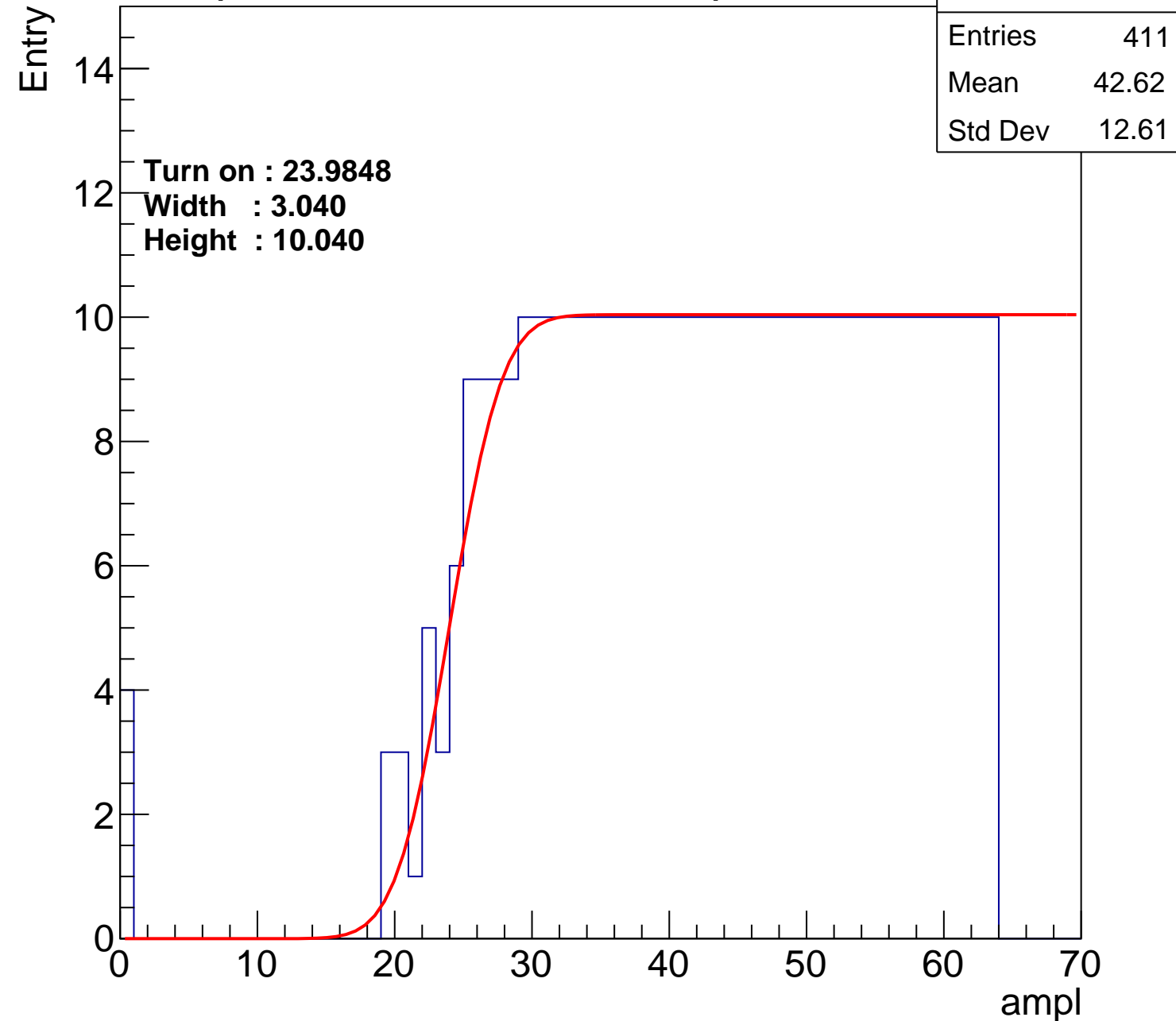
Width : 3.040

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch69

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.4
Std Dev	11.27

**Turn on : 26.3603**

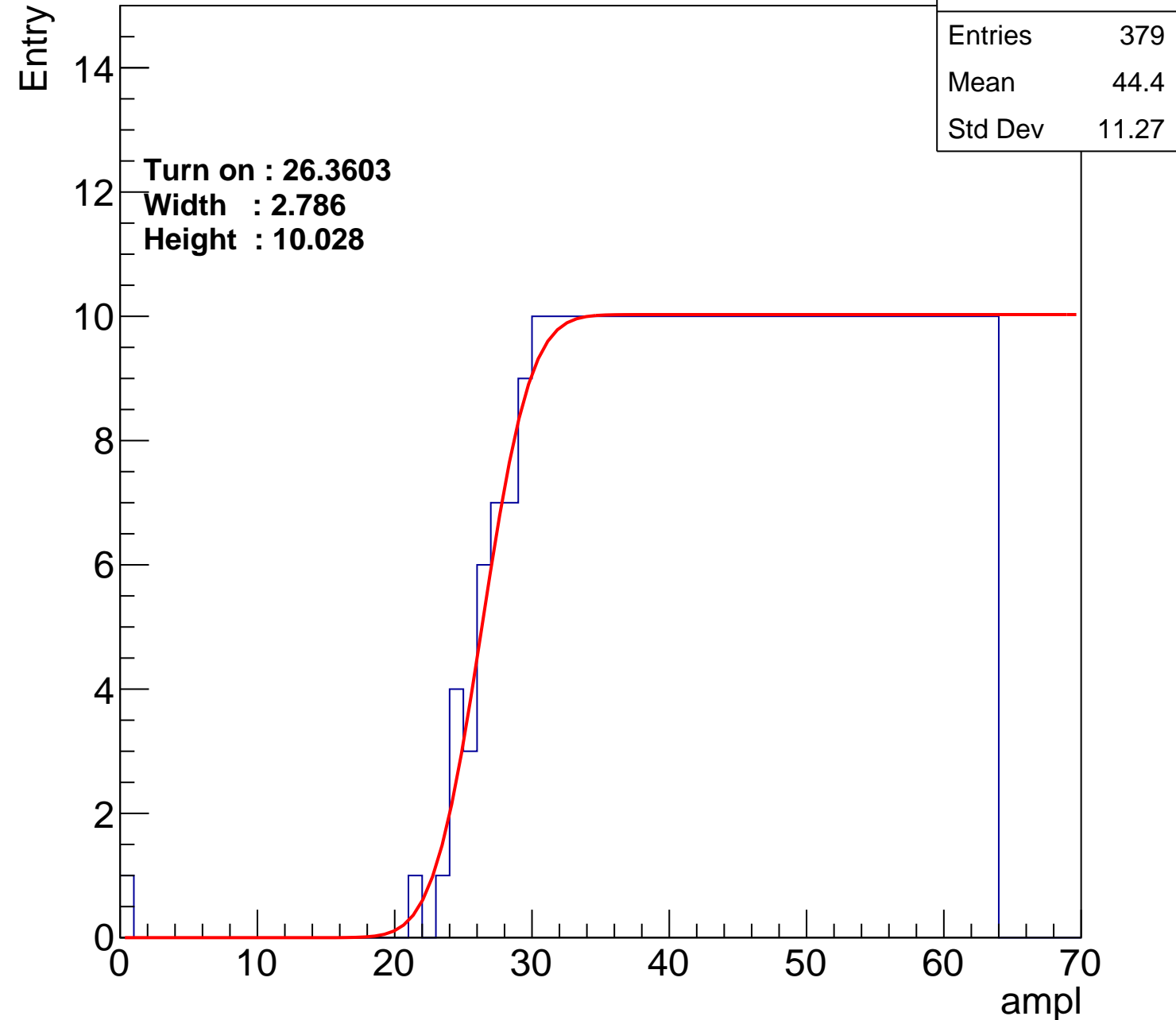
**Width : 2.786**

**Height : 10.028**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch70

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.78
Std Dev	11.81

Turn on : 25.6376

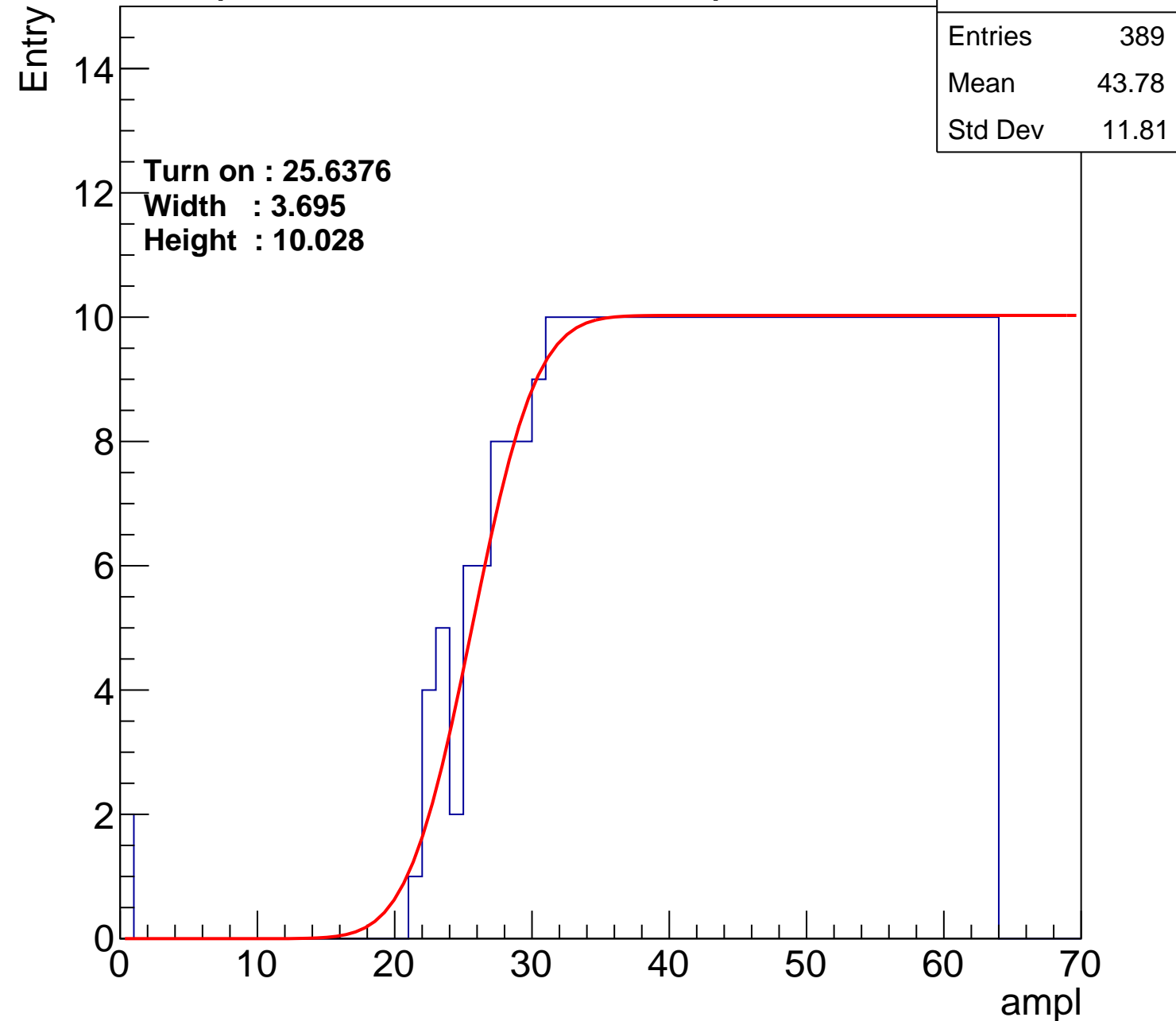
Width : 3.695

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch71

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.5
Std Dev	11.66

Turn on : 27.3983

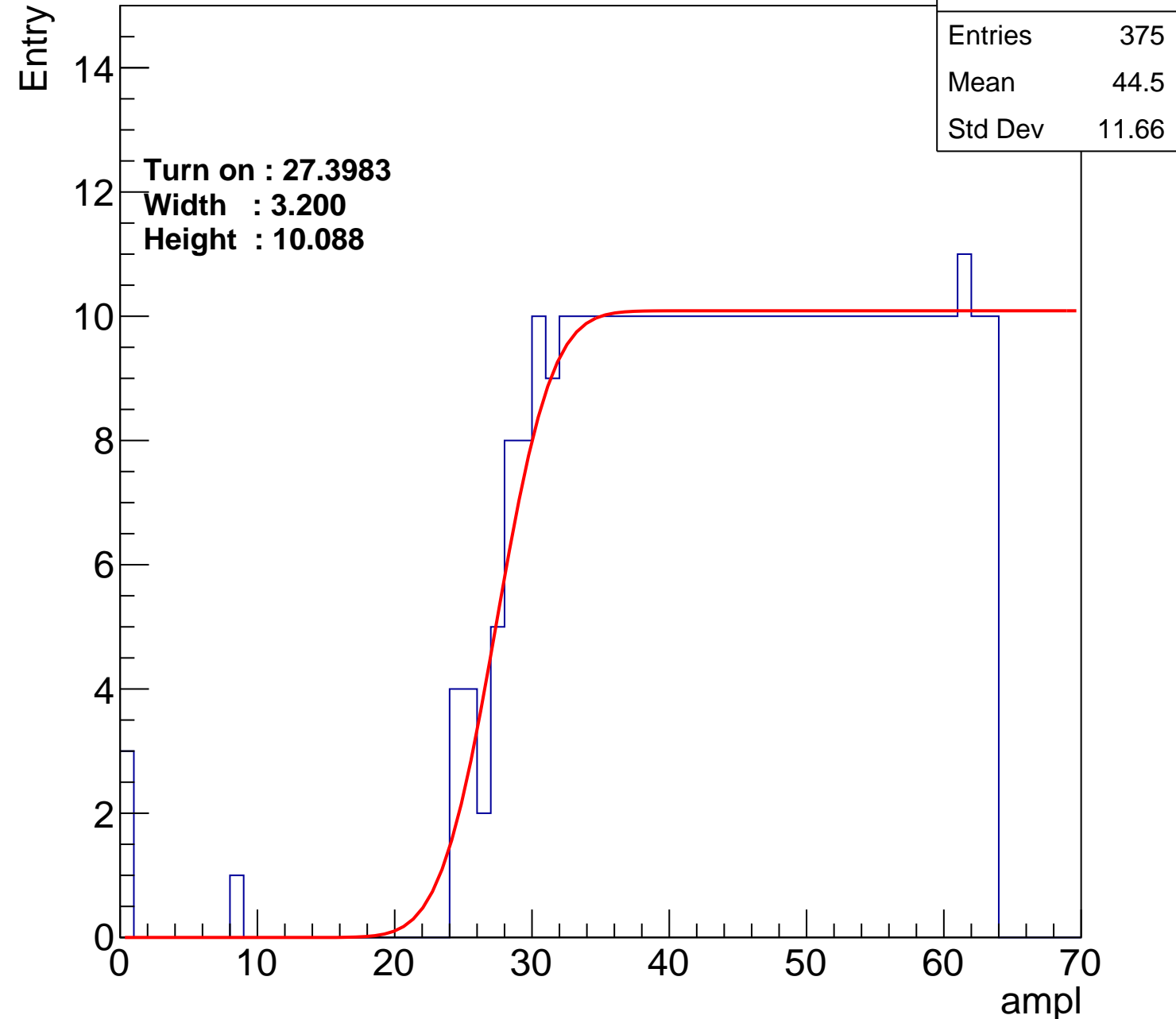
Width : 3.200

Height : 10.088

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch72

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.77
Std Dev	12.01

Turn on : 25.5265

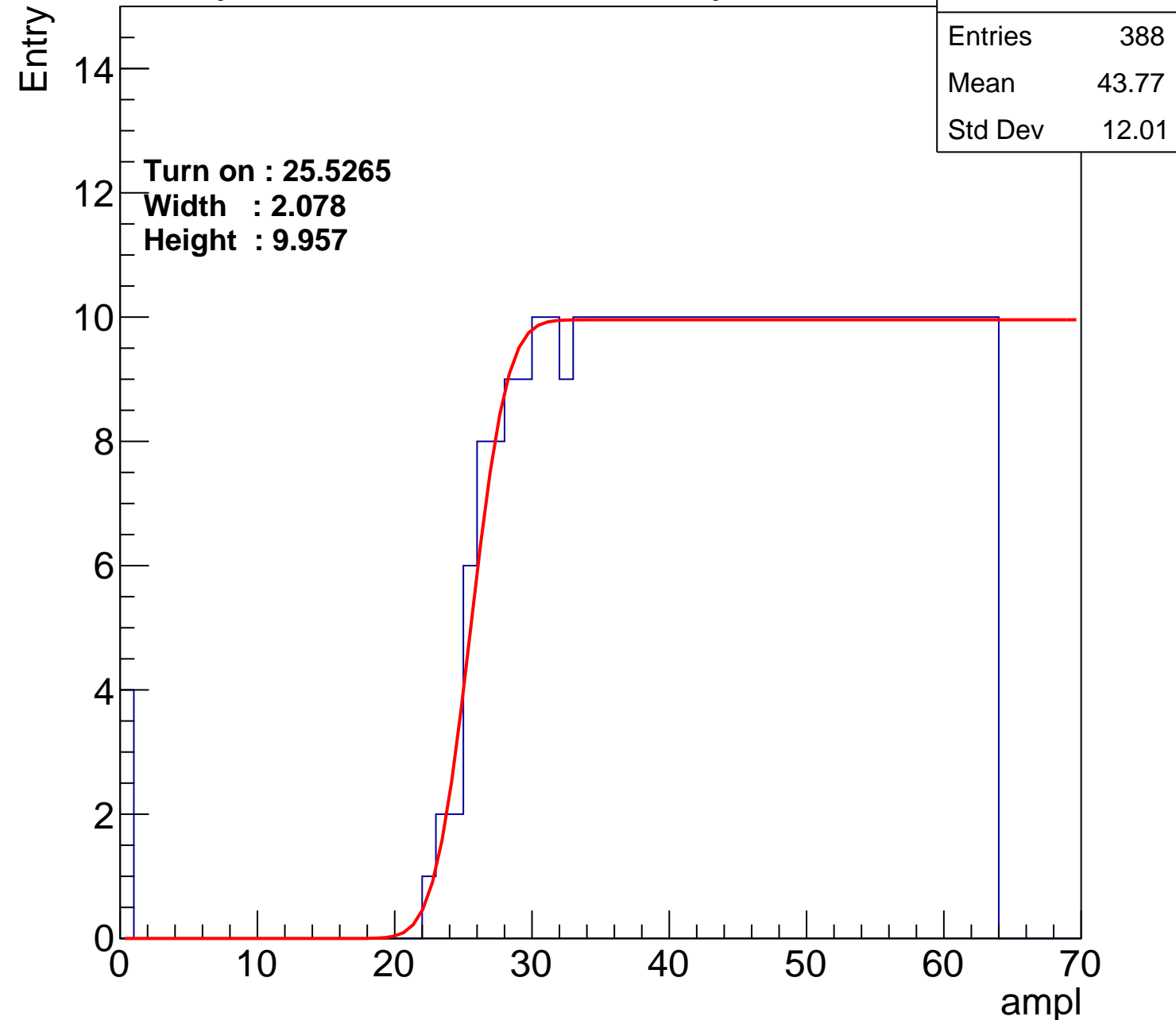
Width : 2.078

Height : 9.957

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch73

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.52
Std Dev	12.09

Turn on : 26.0268

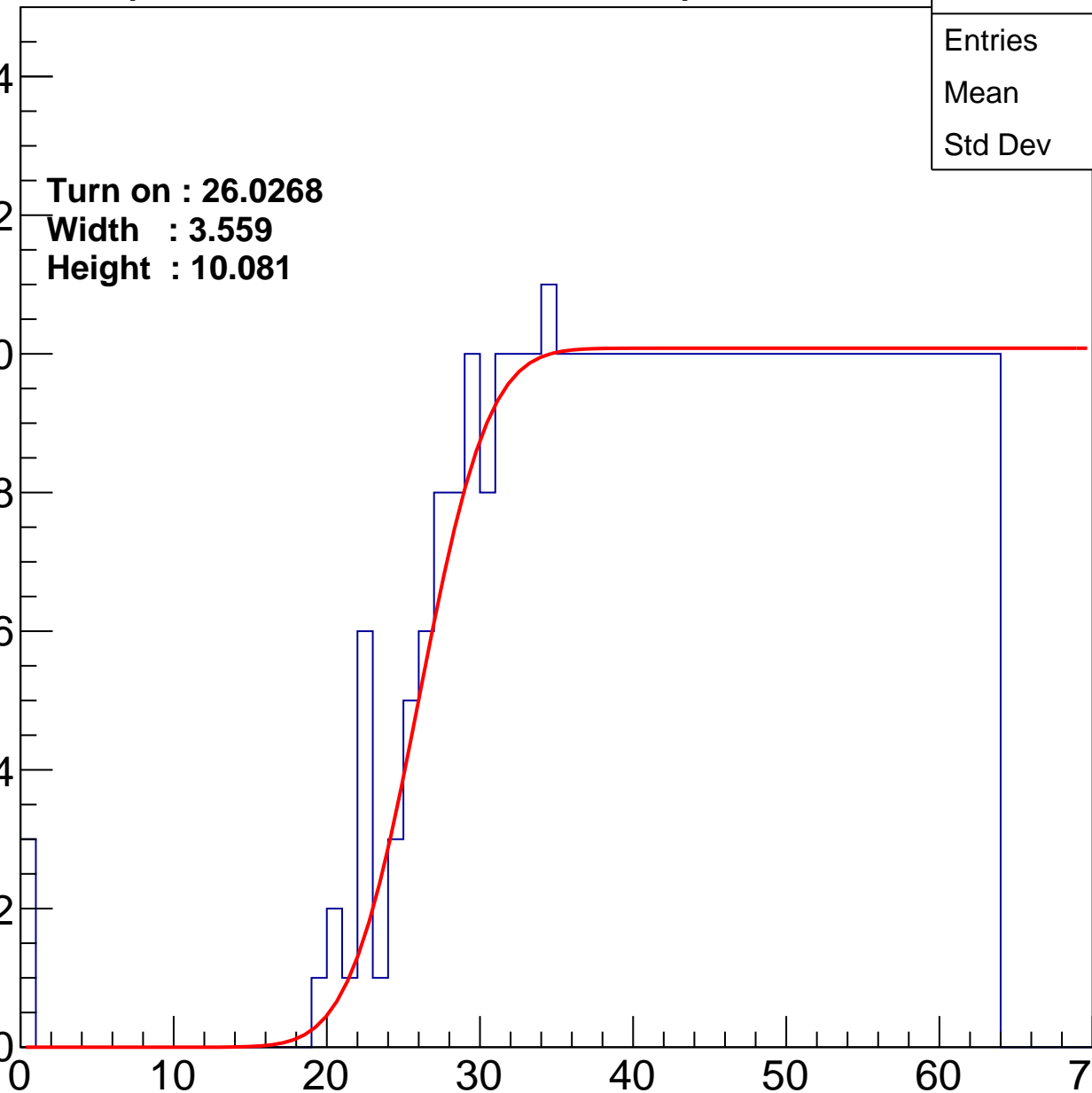
Width : 3.559

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch74

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.53
Std Dev	12.01

Turn on : 24.8705

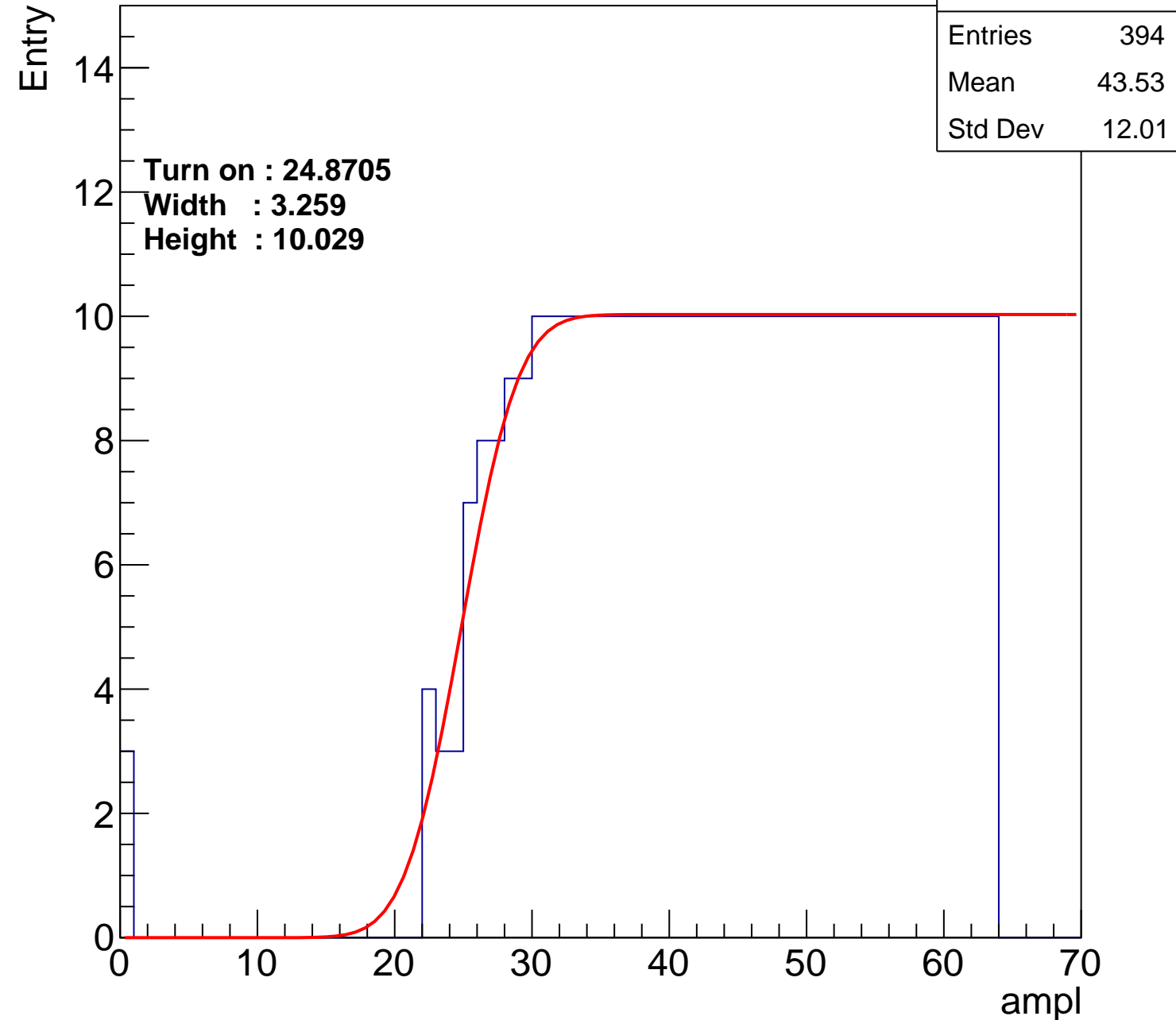
Width : 3.259

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch75

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.74
Std Dev	11.3

Turn on : 28.2217

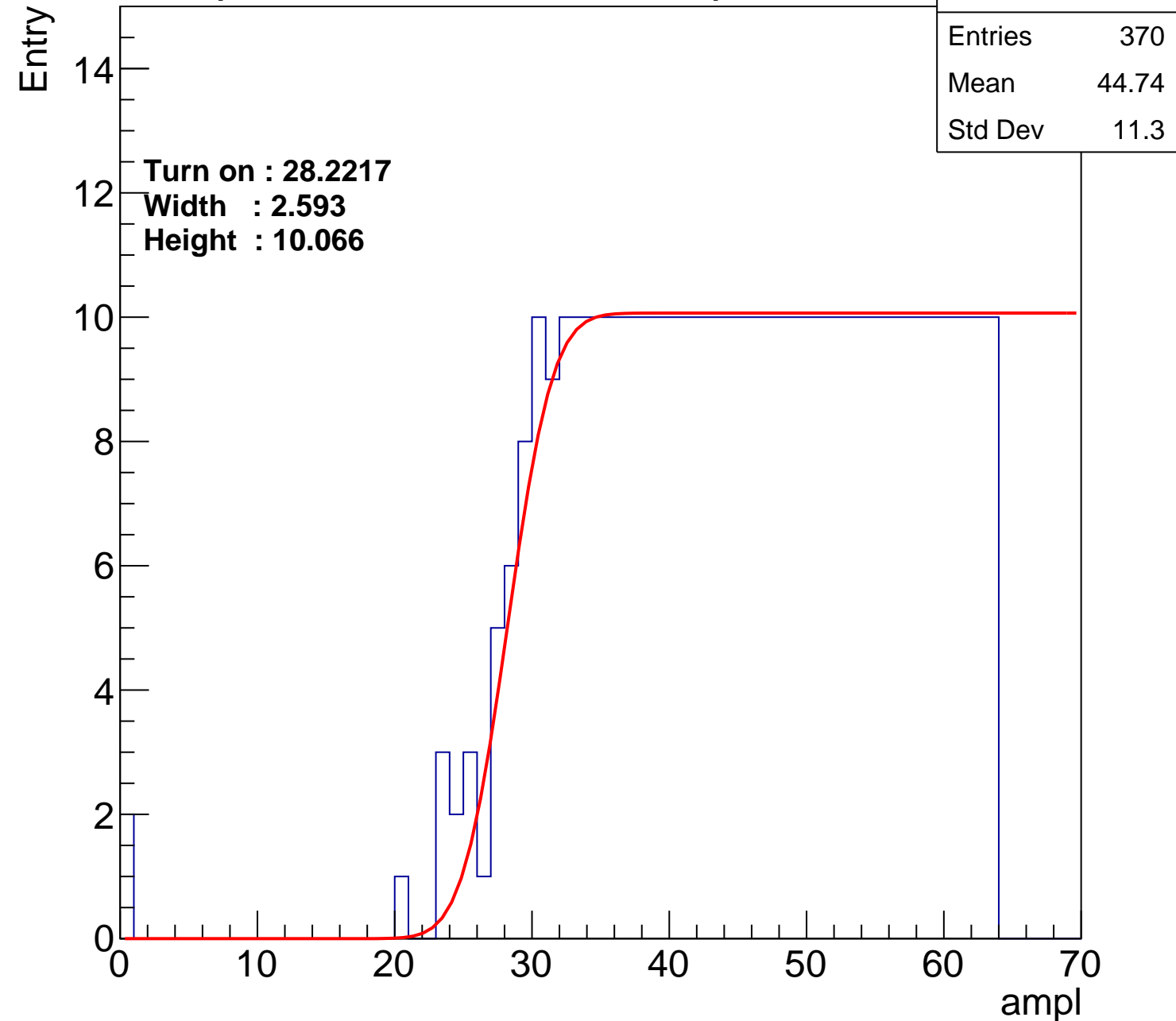
Width : 2.593

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch76

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.77
Std Dev	11.82

Turn on : 25.2562

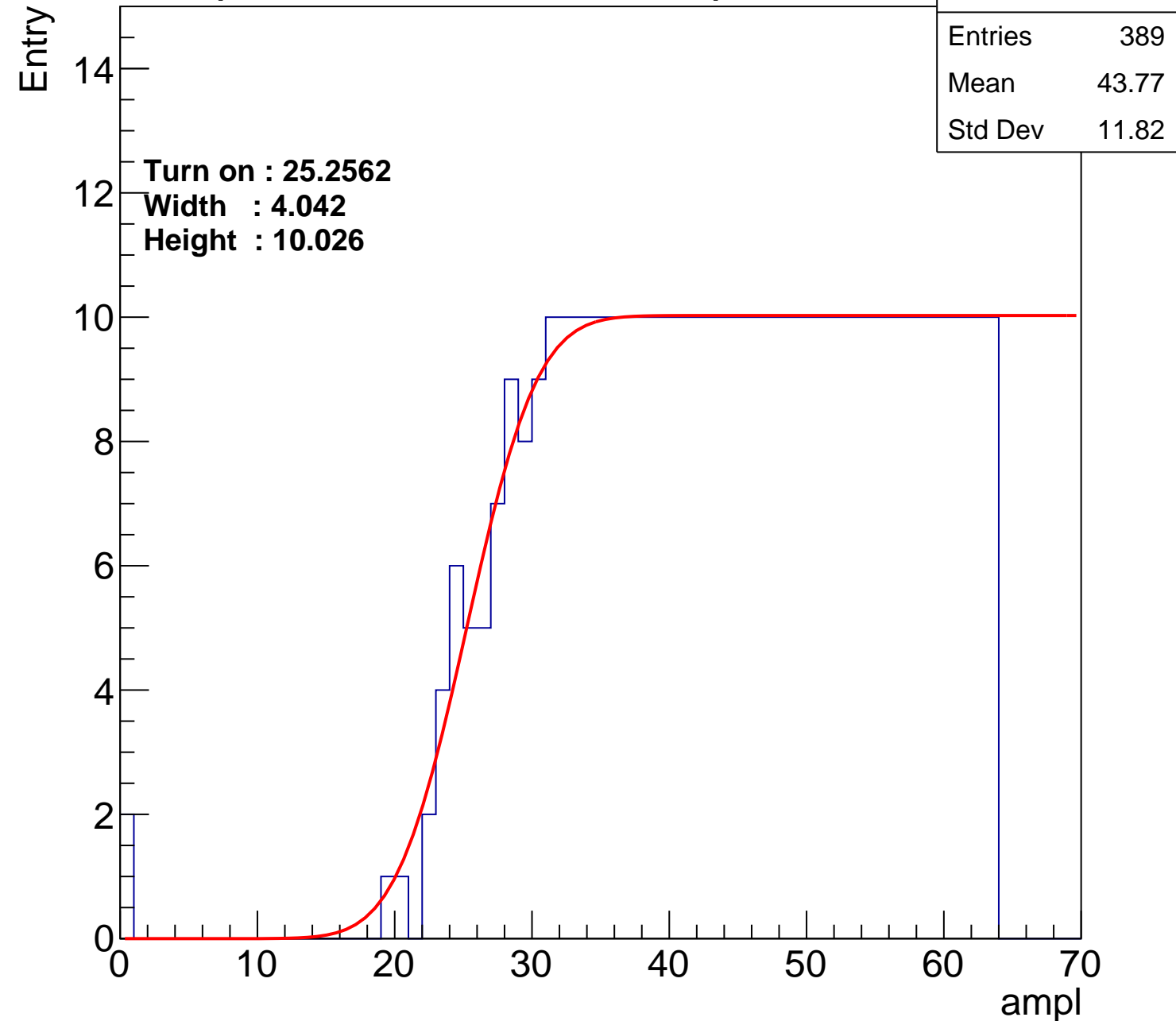
Width : 4.042

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch77

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.28
Std Dev	11.36

**Turn on : 25.6256**

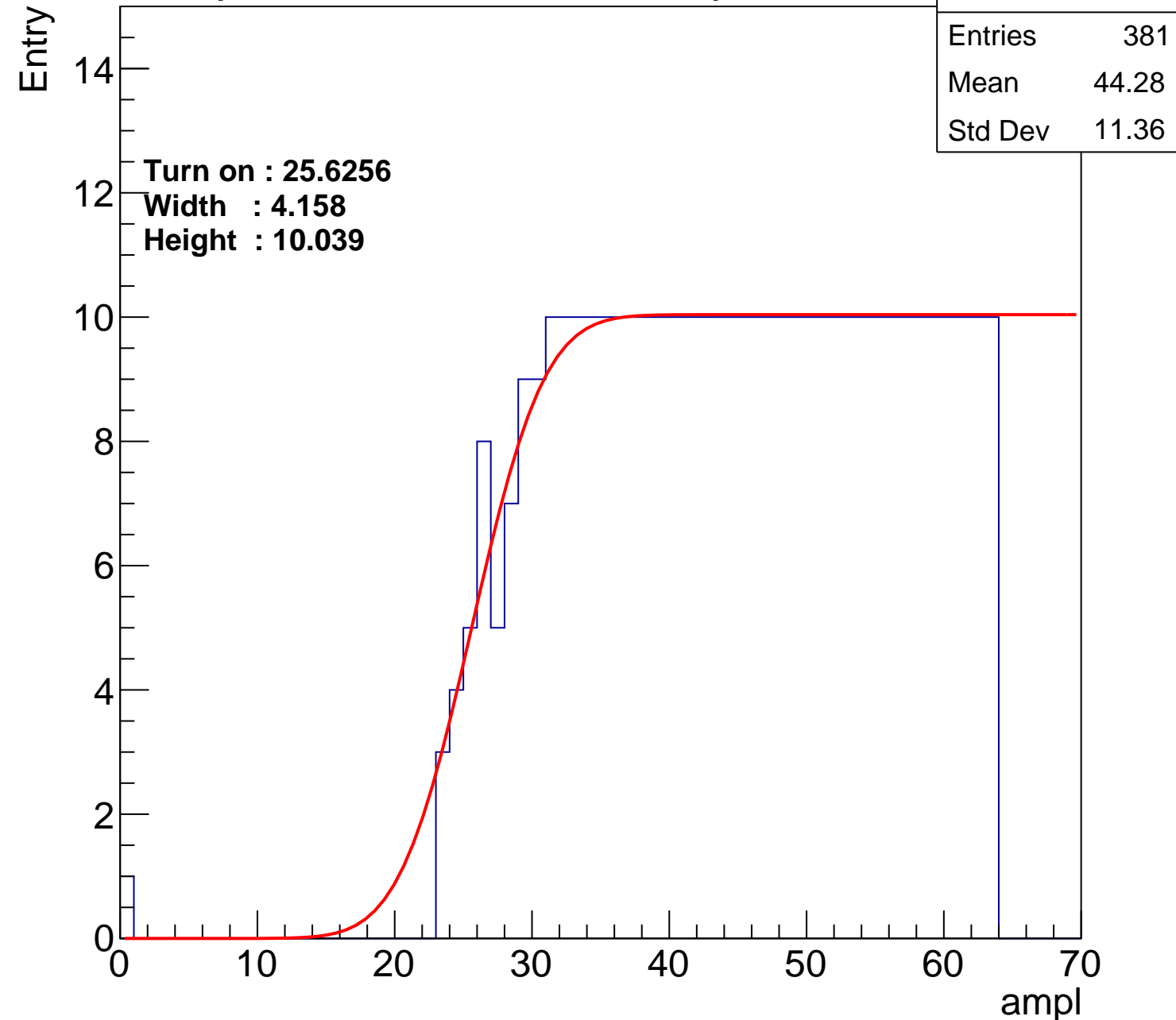
**Width : 4.158**

**Height : 10.039**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch78

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.01
Std Dev	11.82

Turn on : 25.8935

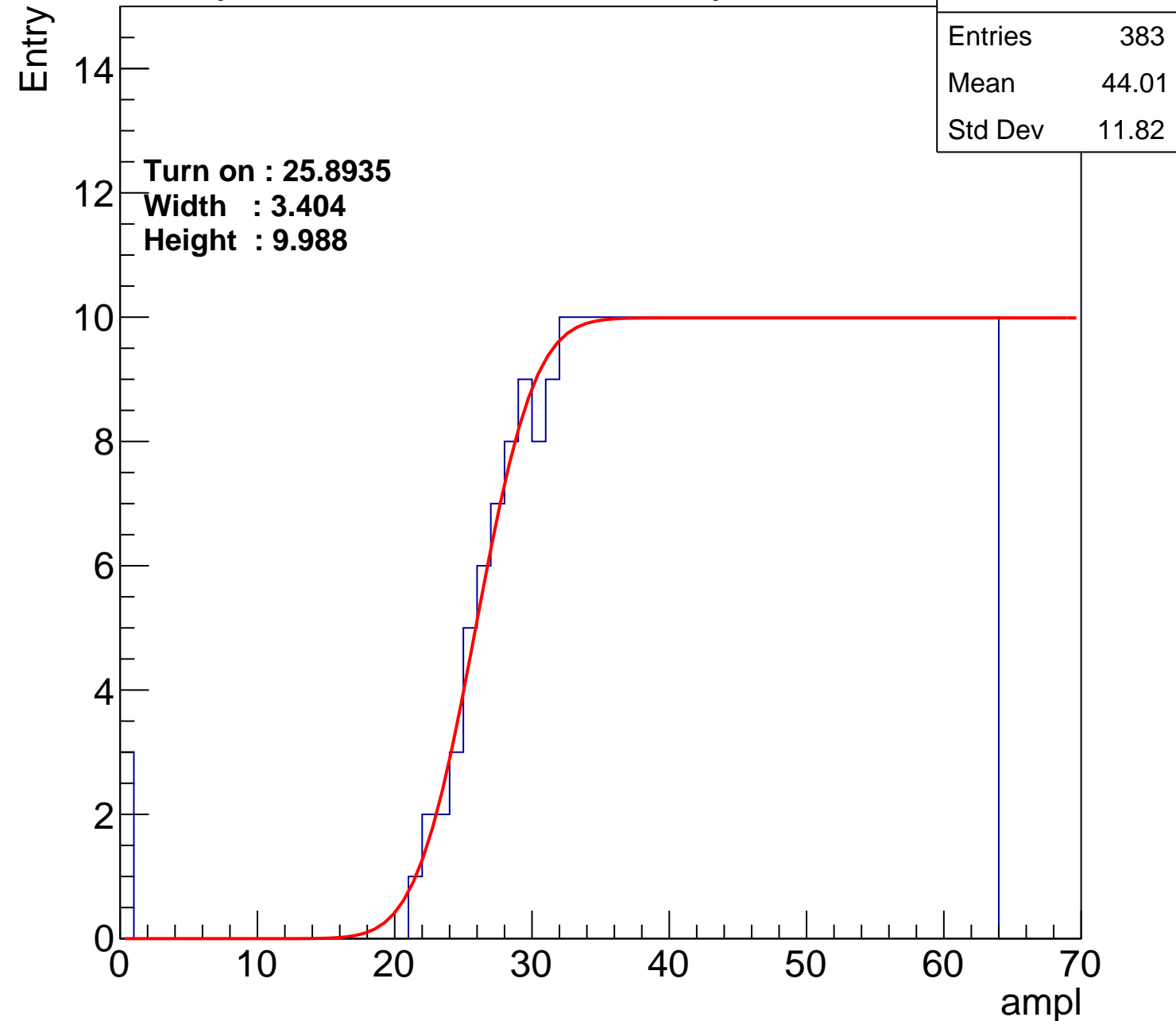
Width : 3.404

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch79

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.76
Std Dev	11.75

Turn on : 25.2725

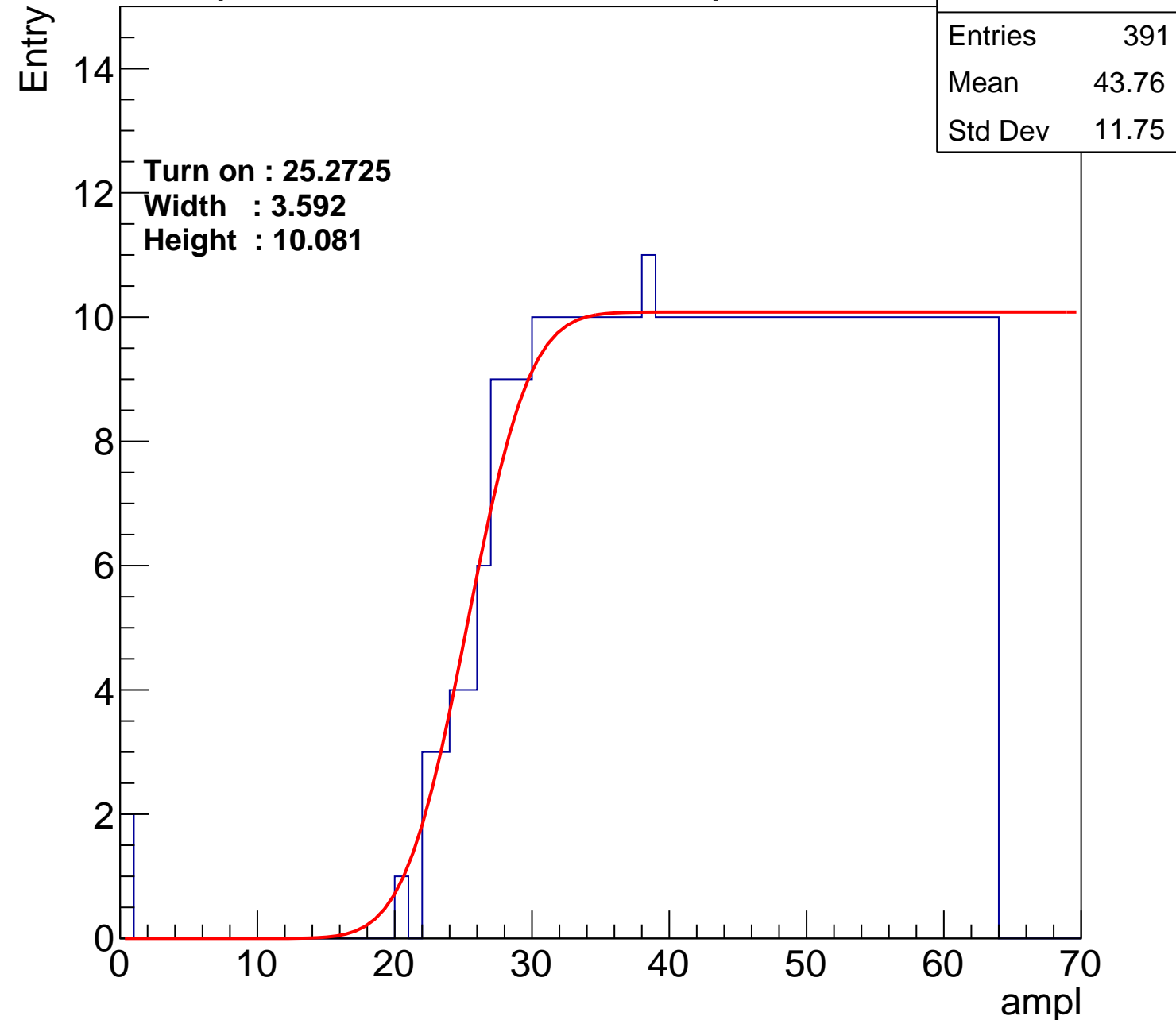
Width : 3.592

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch80

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	410
Mean	42.73
Std Dev	12.49

**Turn on : 23.6827**

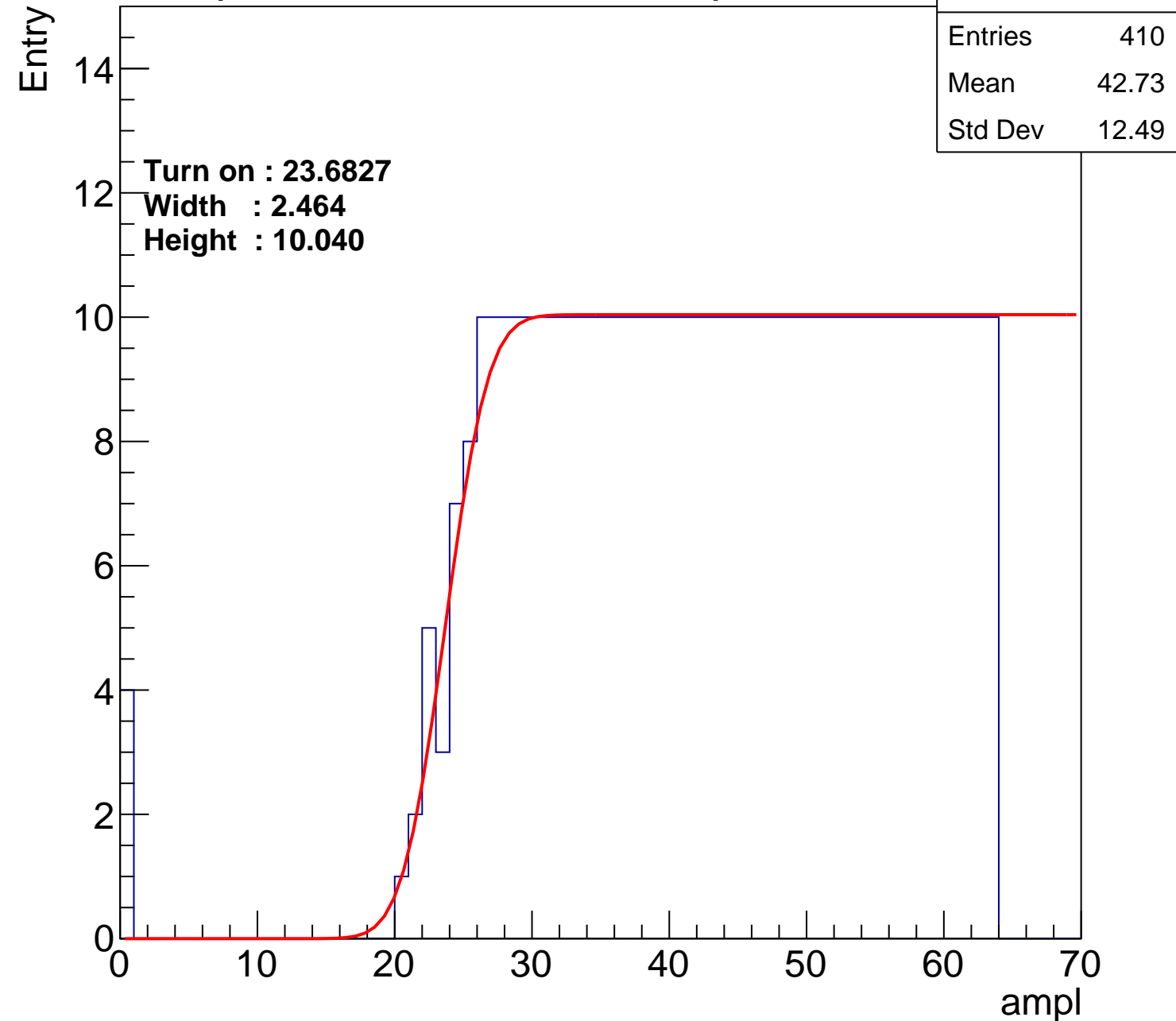
**Width : 2.464**

**Height : 10.040**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch81

calib\_packv5\_042523\_0143.root, FC#12, port B1

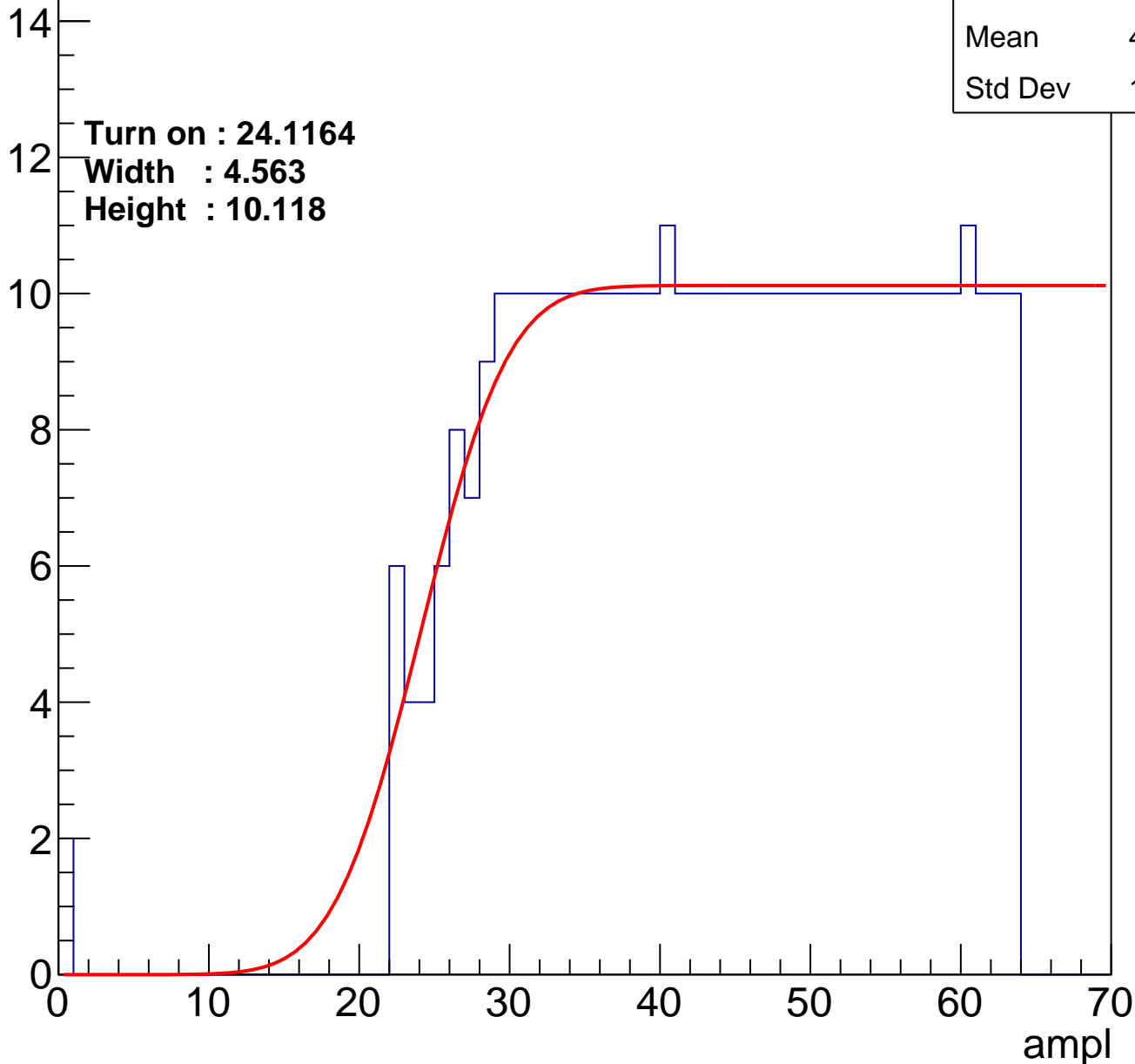
Entries	398
Mean	43.52
Std Dev	11.92

Turn on : 24.1164

Width : 4.563

Height : 10.118

Entry



# B0L102S, U18-ch82

calib\_packv5\_042523\_0143.root, FC#12, port B1

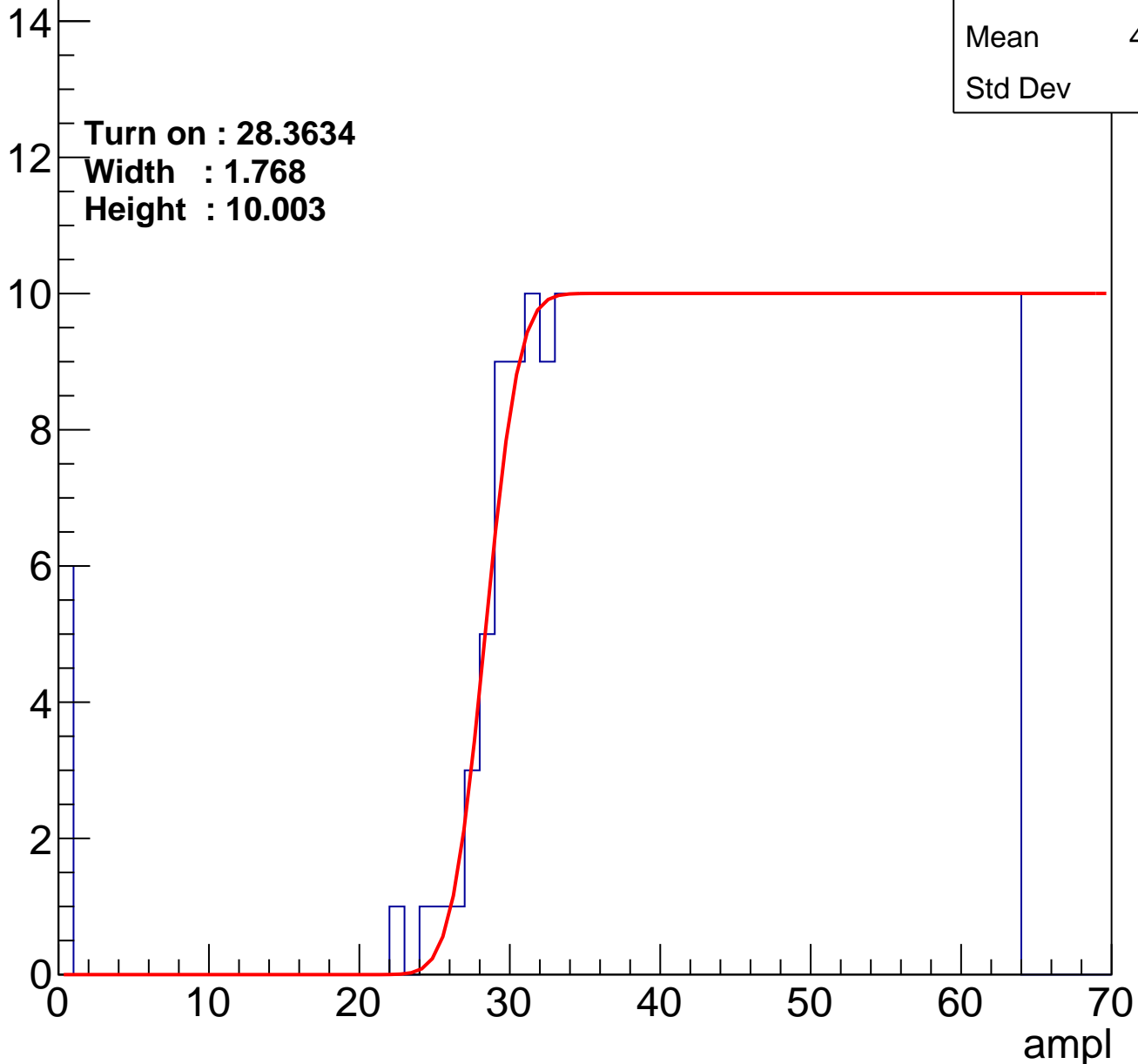
Entries	365
Mean	44.73
Std Dev	11.9

Turn on : 28.3634

Width : 1.768

Height : 10.003

Entry



# B0L102S, U18-ch83

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.65
Std Dev	12.16

Turn on : 26.8011

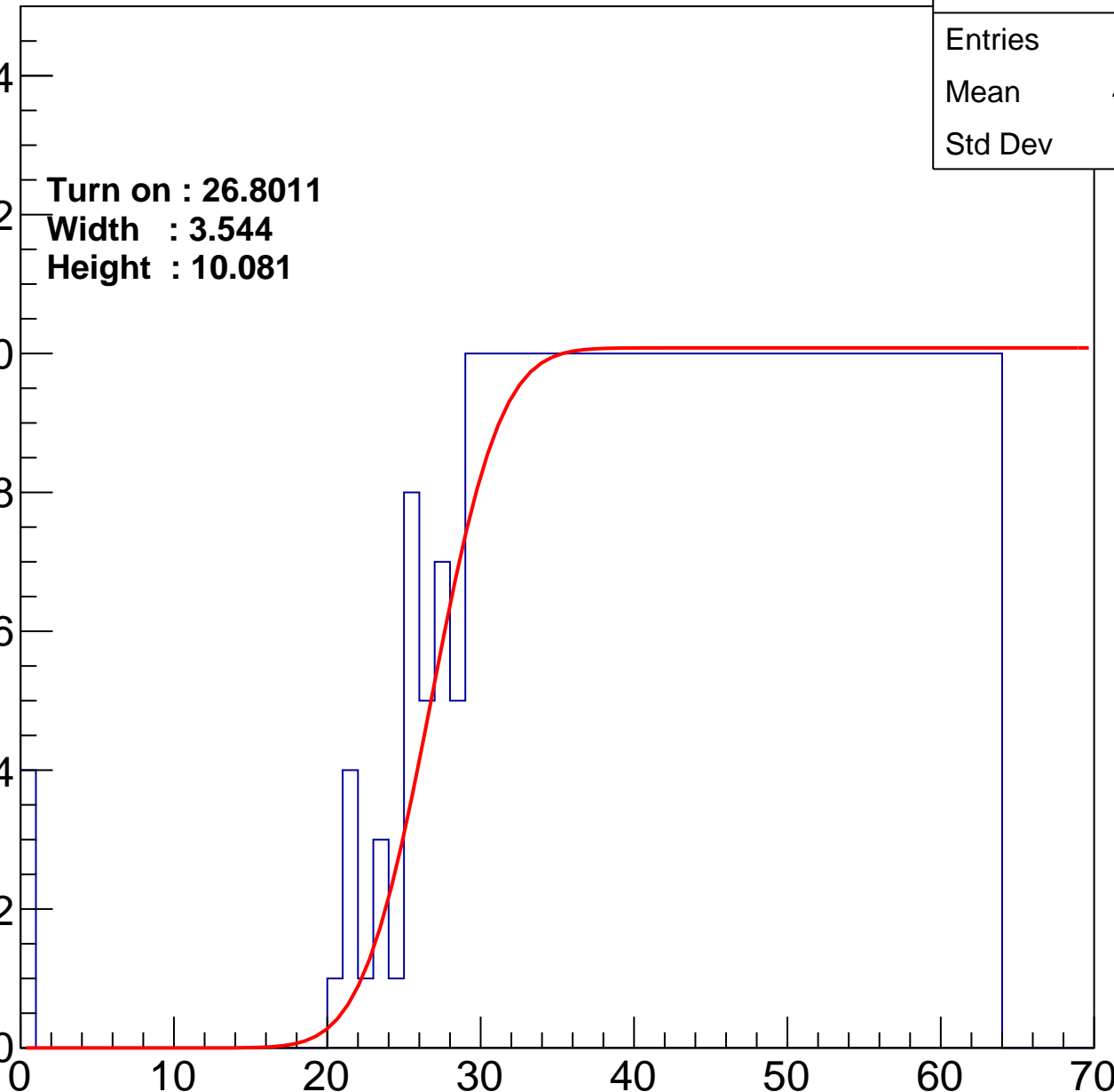
Width : 3.544

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch84

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.04
Std Dev	11.78

**Turn on : 26.5106**

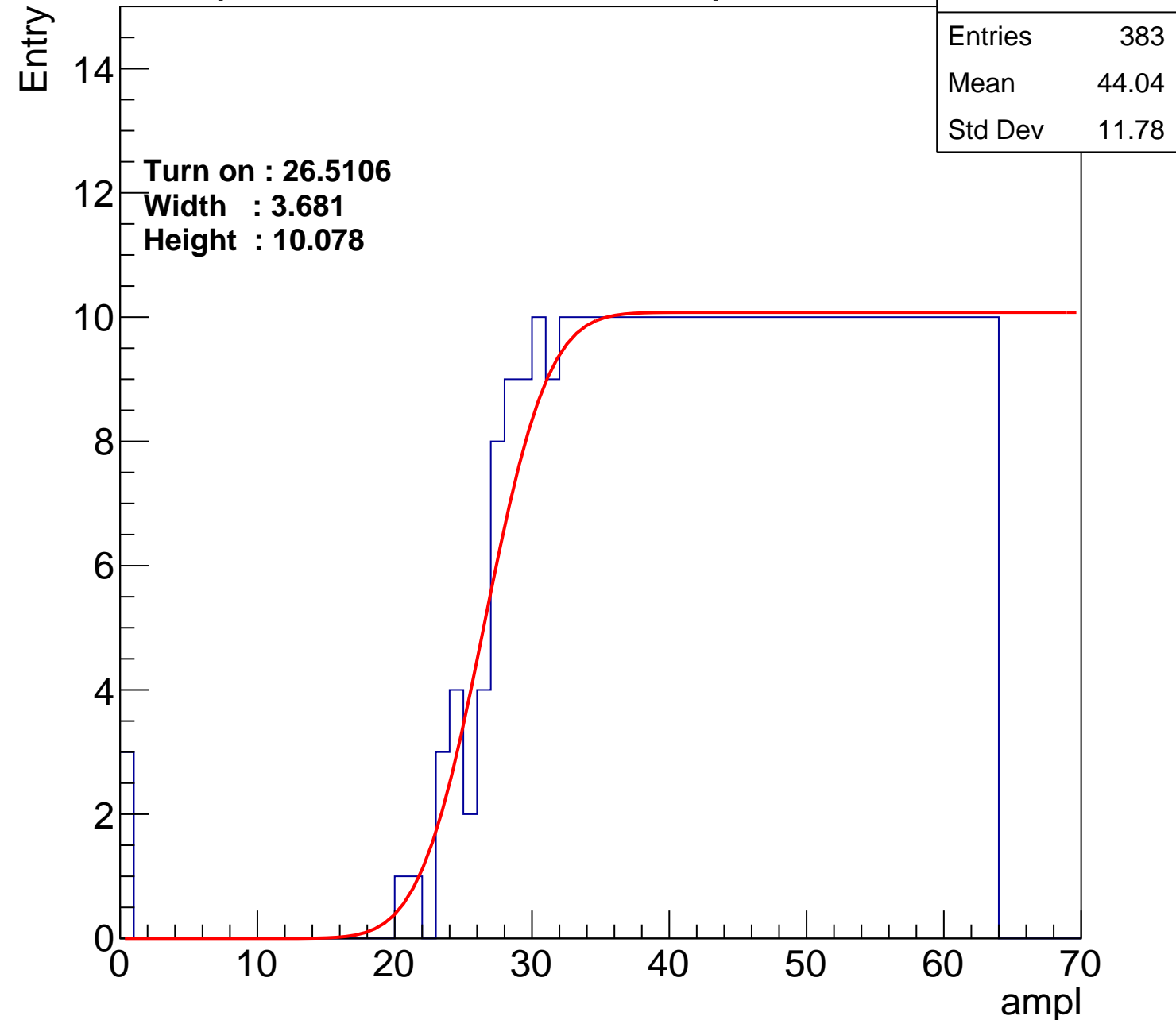
**Width : 3.681**

**Height : 10.078**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch85

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.44
Std Dev	11.76

Turn on : 27.5257

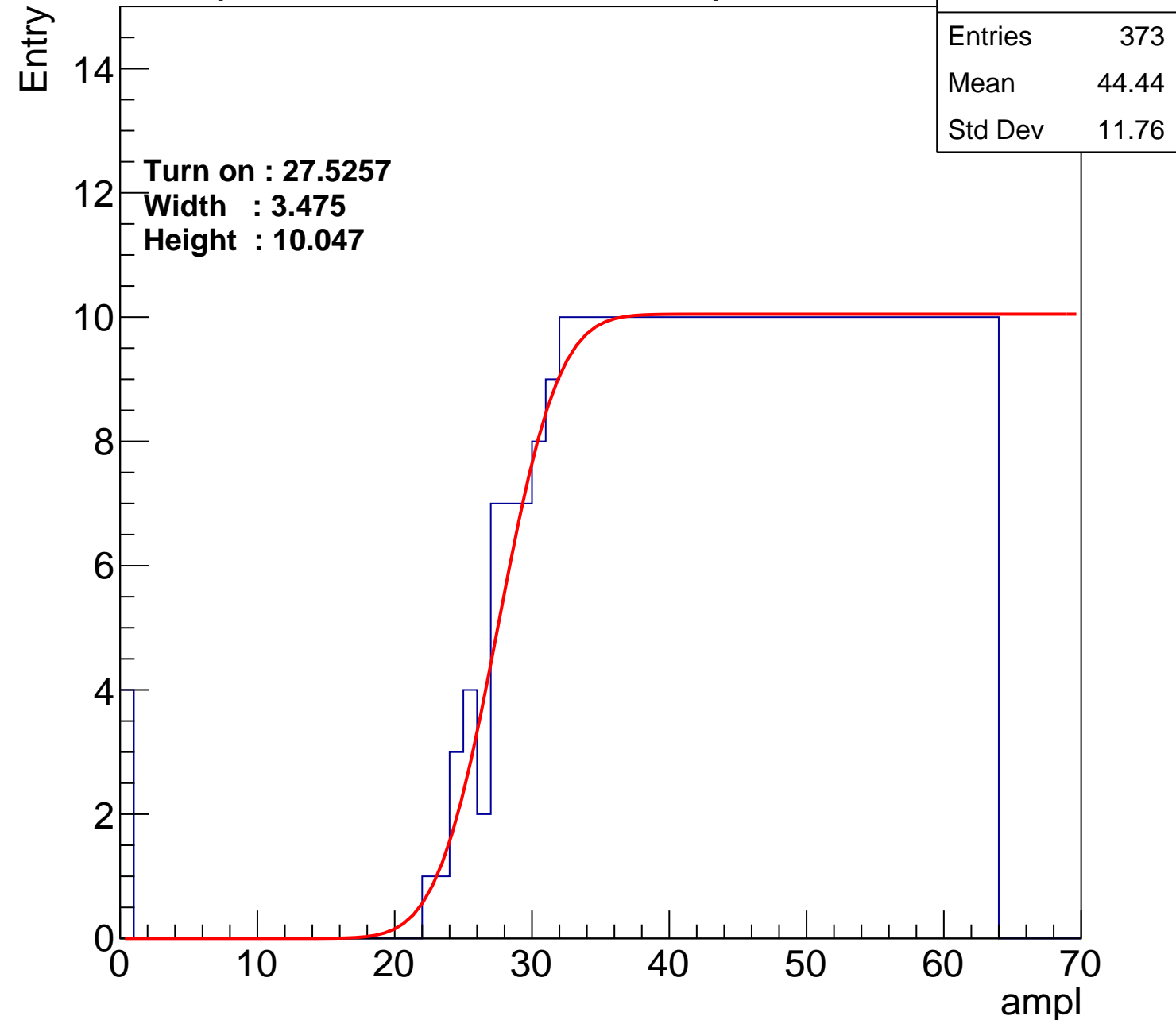
Width : 3.475

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch86

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	390
Mean	43.61
Std Dev	12.16

Turn on : 25.5172

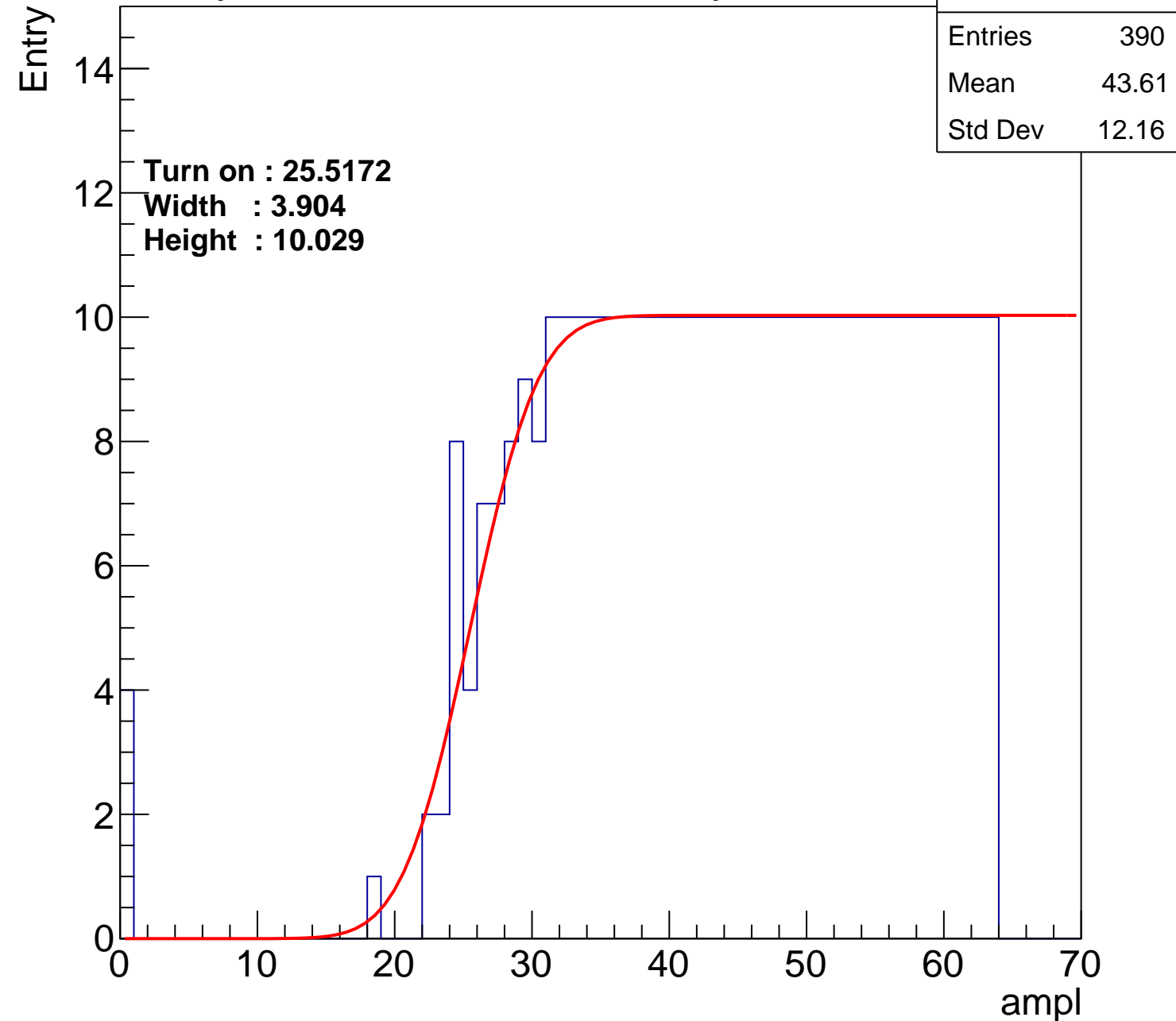
Width : 3.904

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch87

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 26.9206

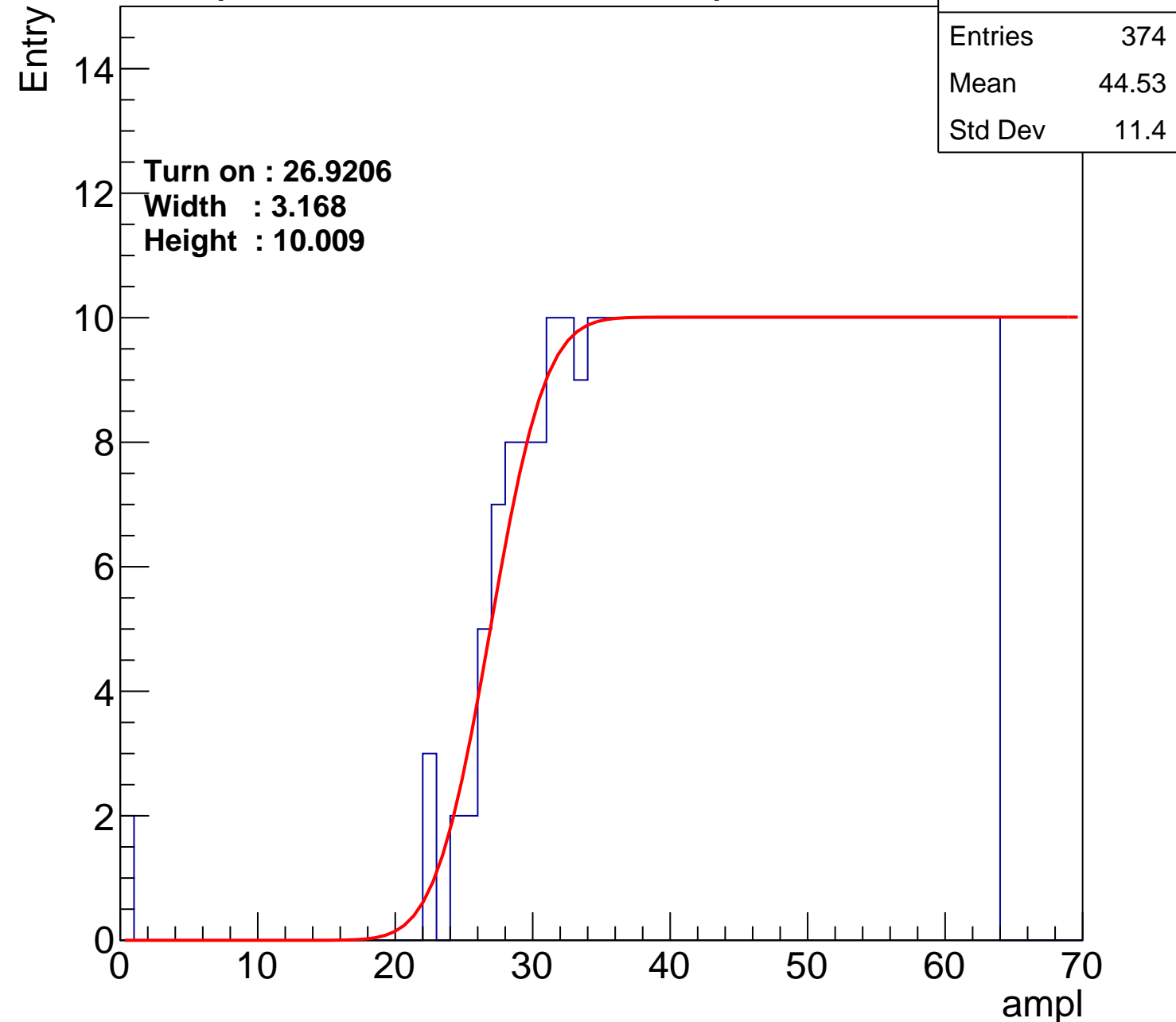
Width : 3.168

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch88

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	412
Mean	42.47
Std Dev	12.89

Turn on : 23.6540

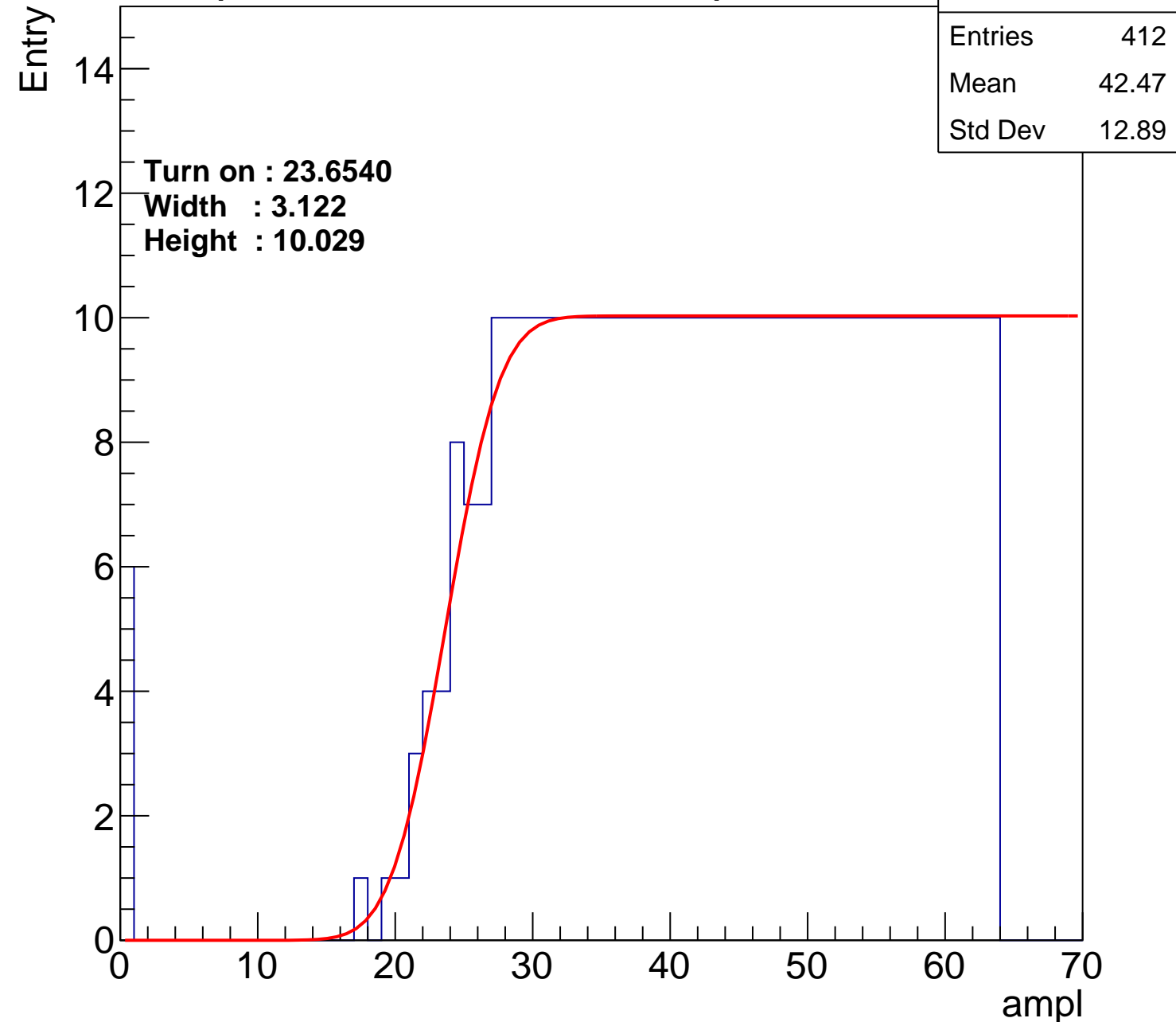
Width : 3.122

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch89

calib\_packv5\_042523\_0143.root, FC#12, port B1

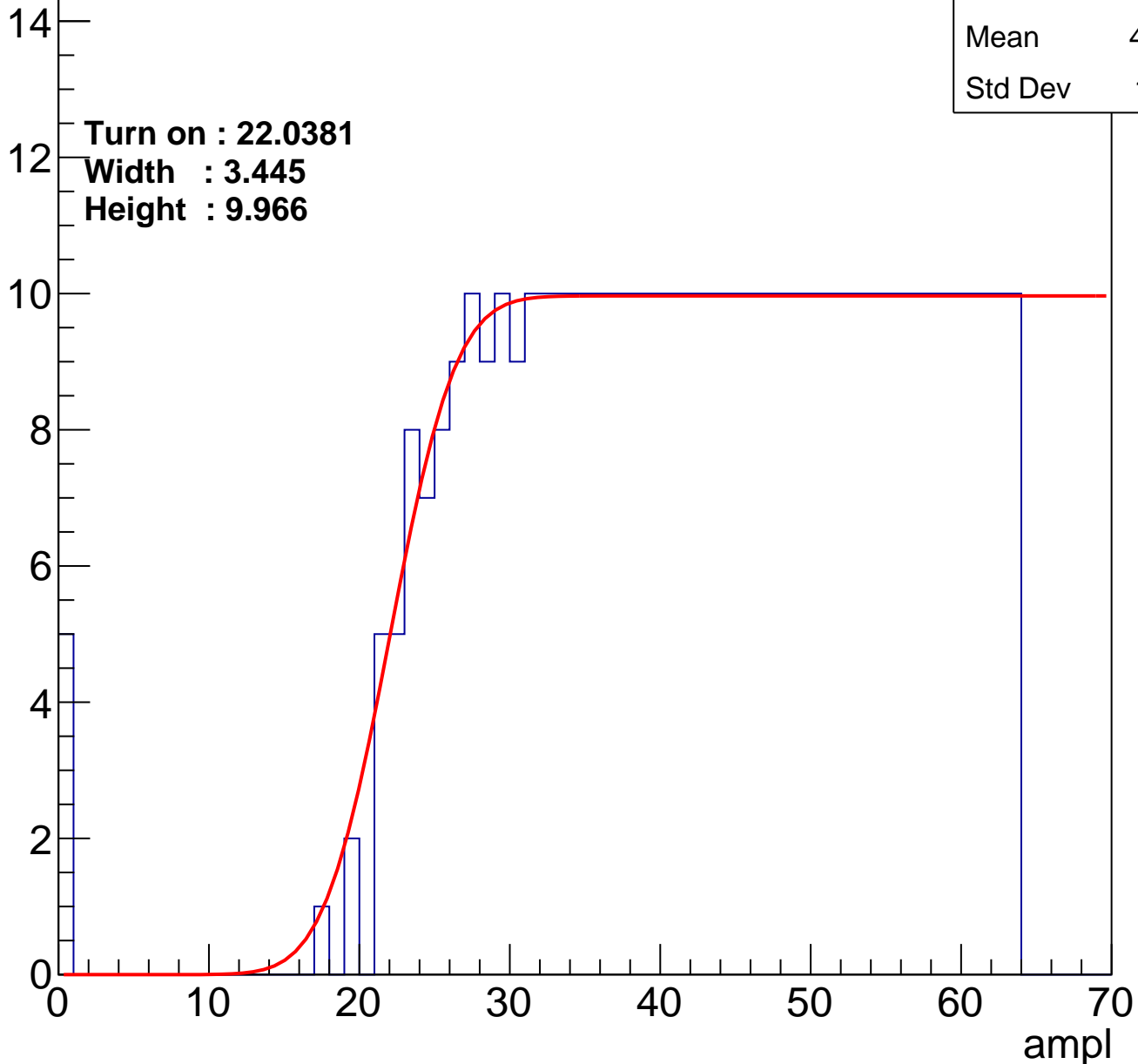
Entries	418
Mean	42.22
Std Dev	12.91

Turn on : 22.0381

Width : 3.445

Height : 9.966

Entry



# B0L102S, U18-ch90

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.51
Std Dev	11.92

Turn on : 24.8064

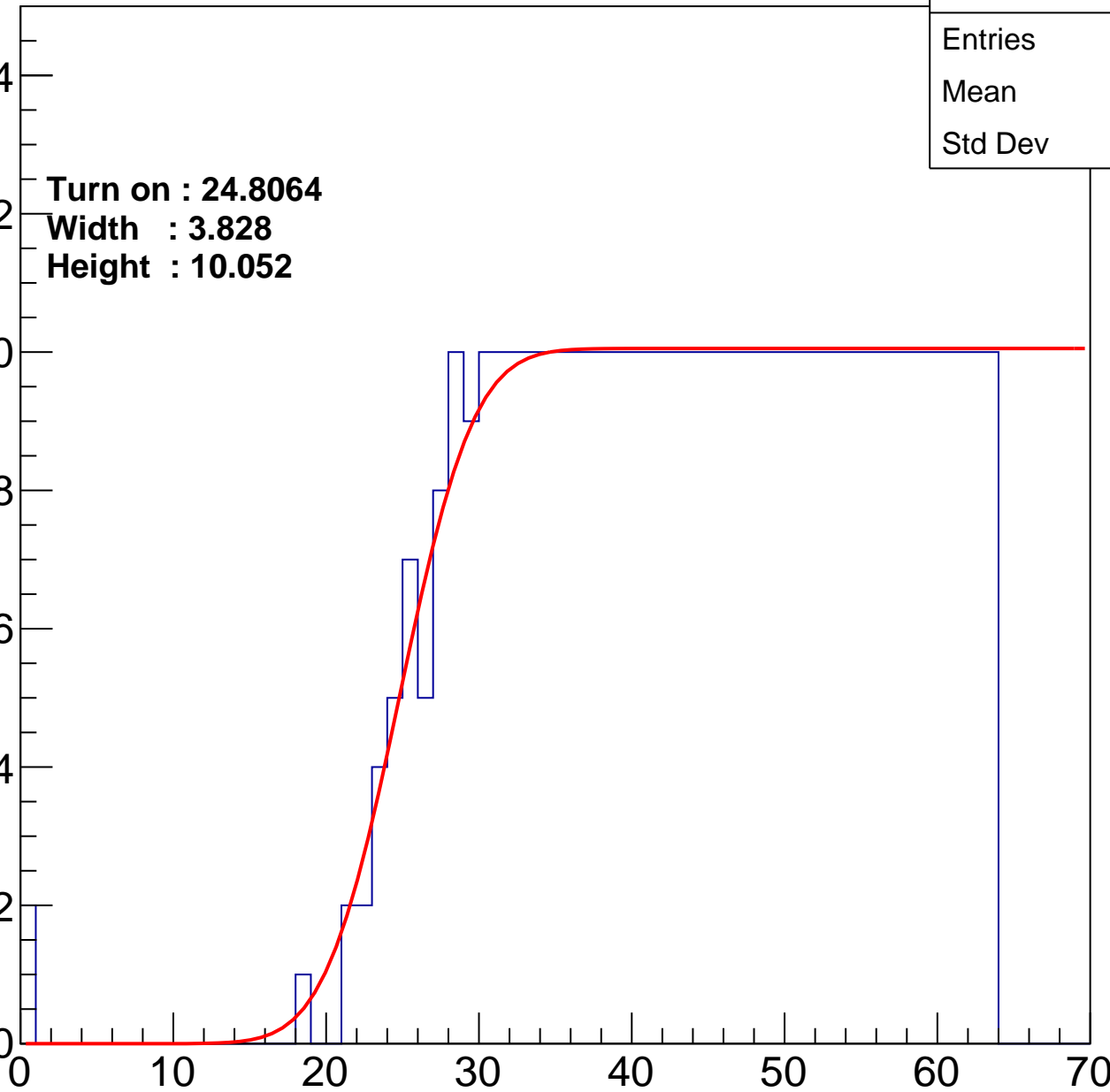
Width : 3.828

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch91

calib\_packv5\_042523\_0143.root, FC#12, port B1

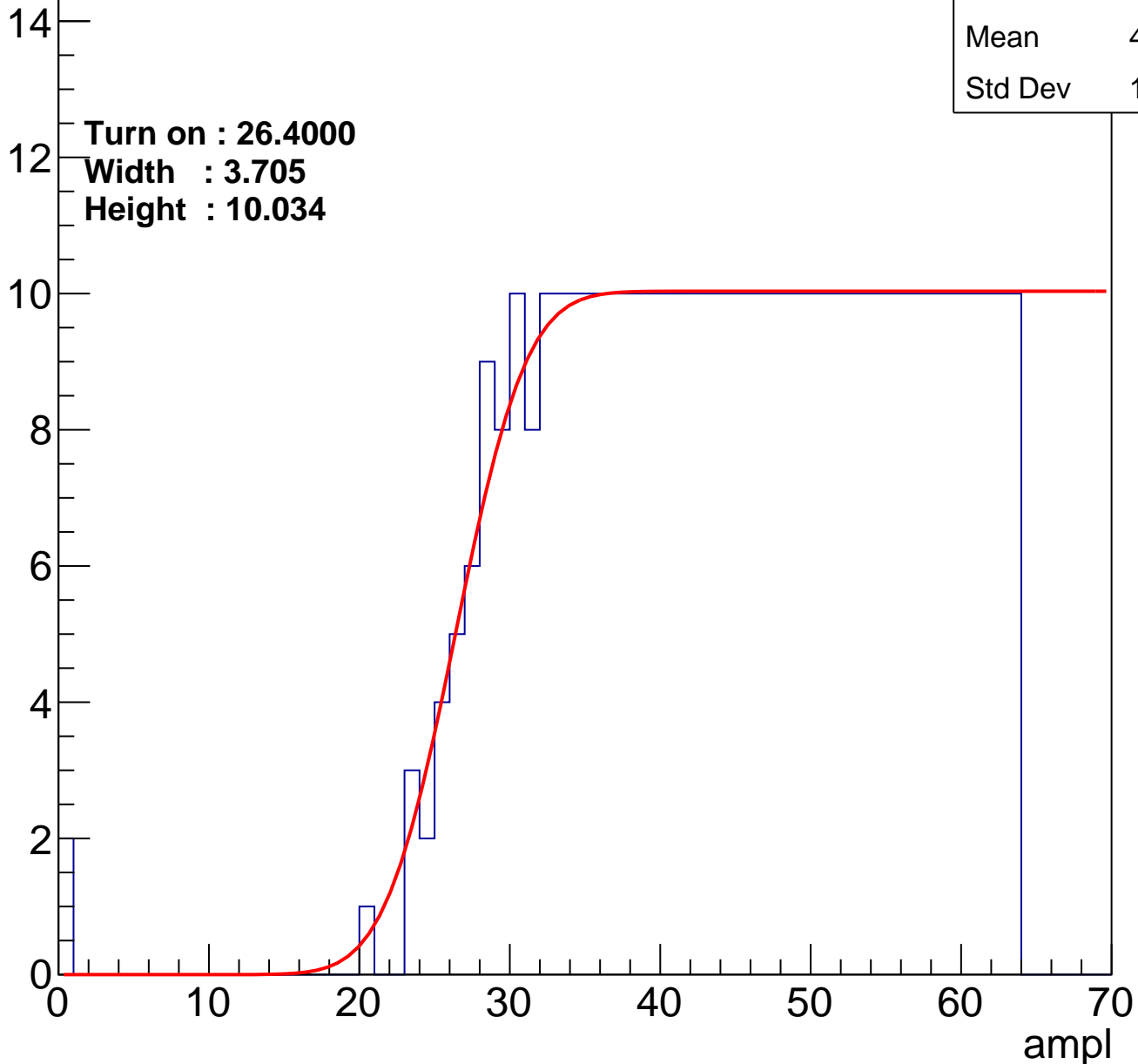
Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.4000

Width : 3.705

Height : 10.034

Entry



# B0L102S, U18-ch92

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.47
Std Dev	12.09

Turn on : 25.2029

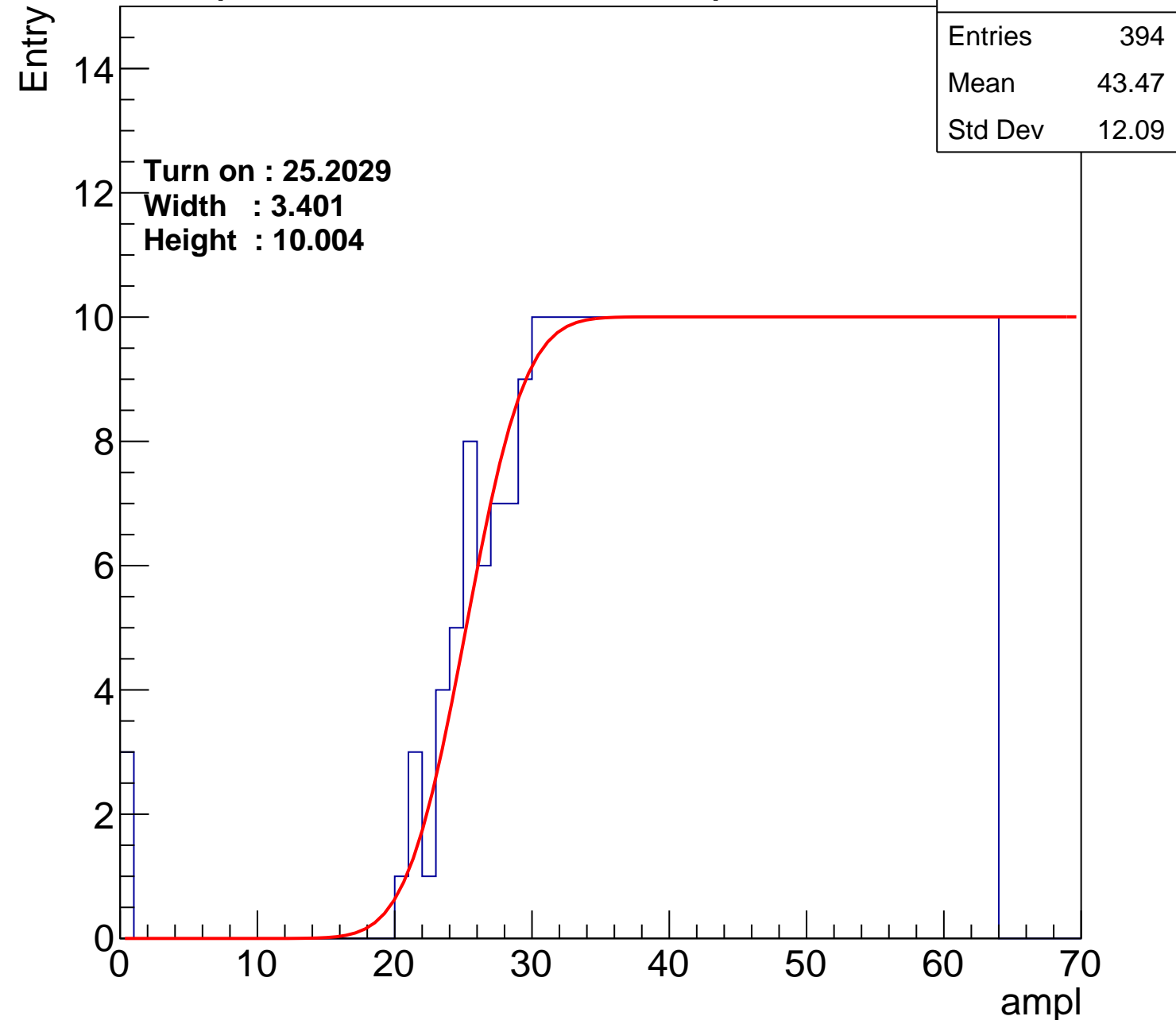
Width : 3.401

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch93

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.16
Std Dev	12.34

**Turn on : 27.0757**

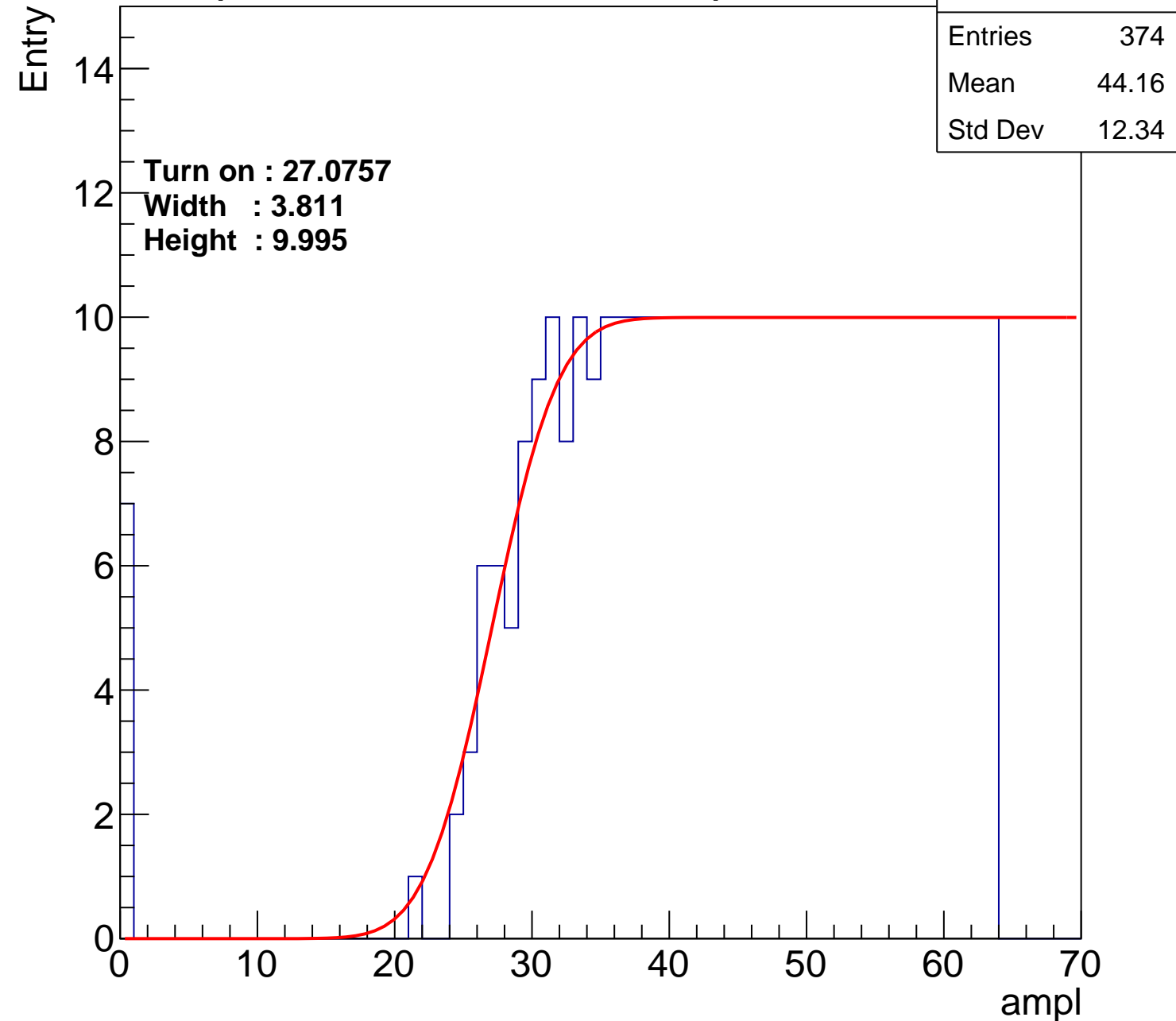
**Width : 3.811**

**Height : 9.995**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch94

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.44
Std Dev	12.2

Turn on : 25.2946

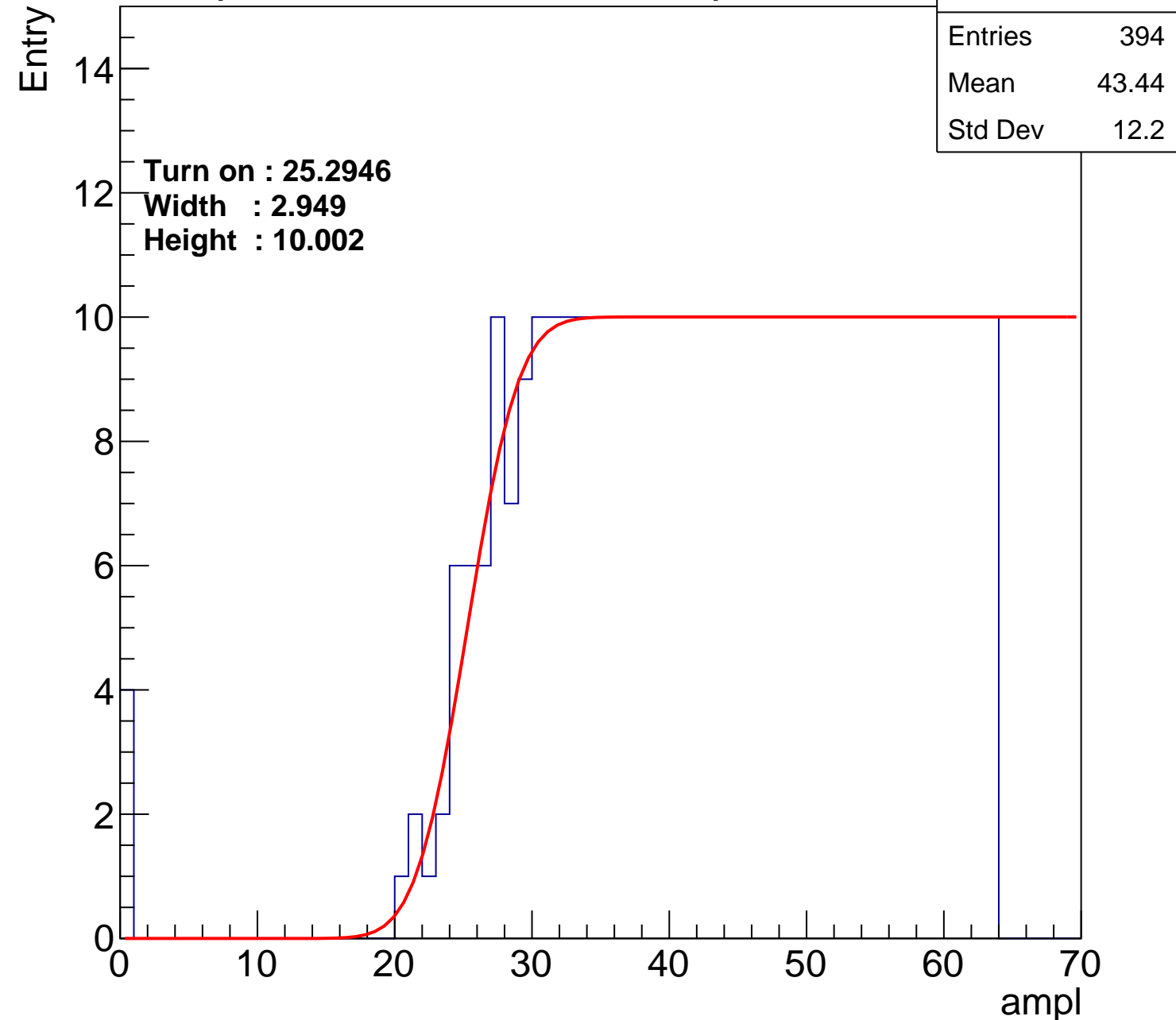
Width : 2.949

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch95

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	410
Mean	42.71
Std Dev	12.47

Turn on : 24.1740

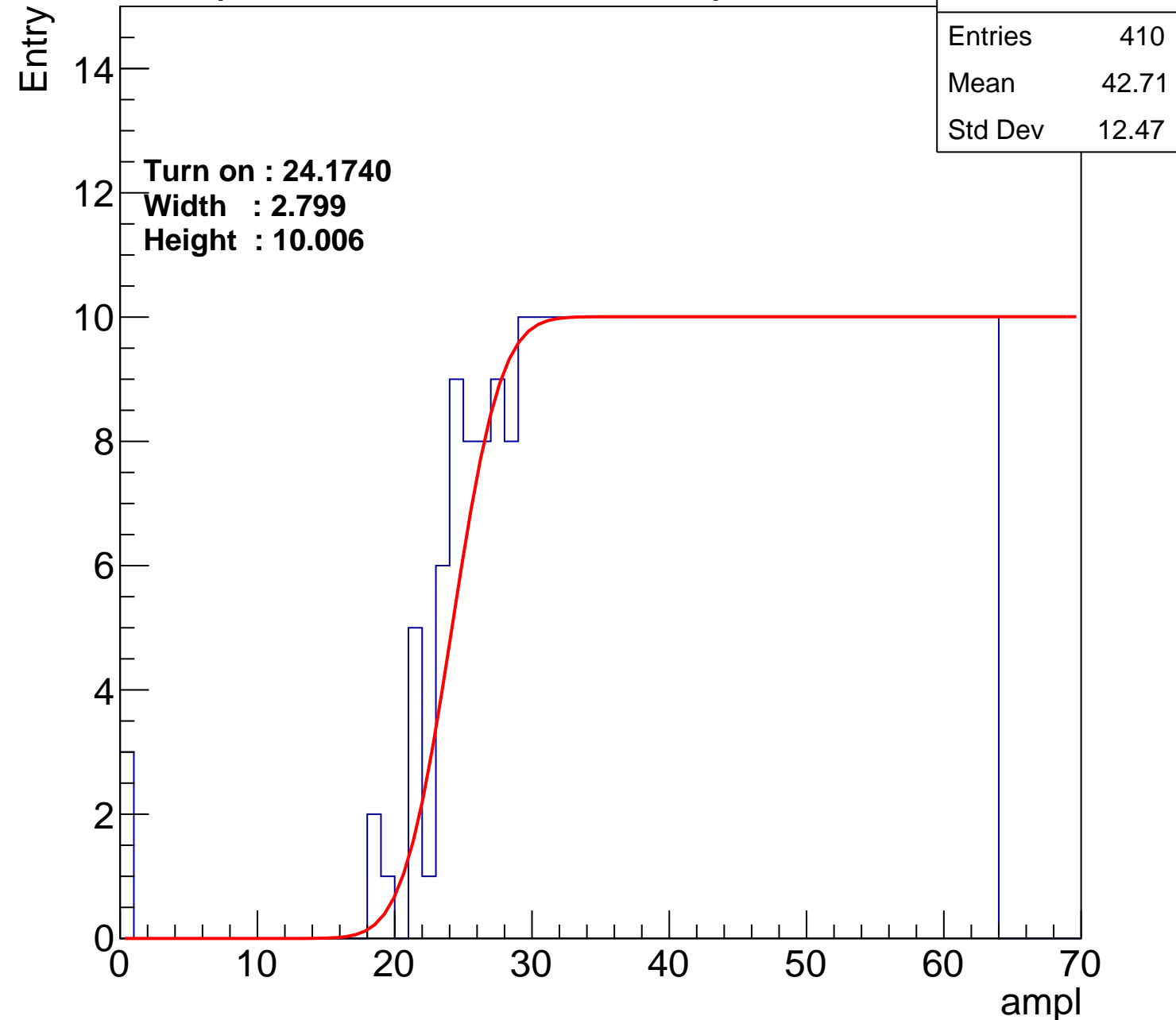
Width : 2.799

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch96

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.51
Std Dev	11.37

**Turn on : 26.3645**

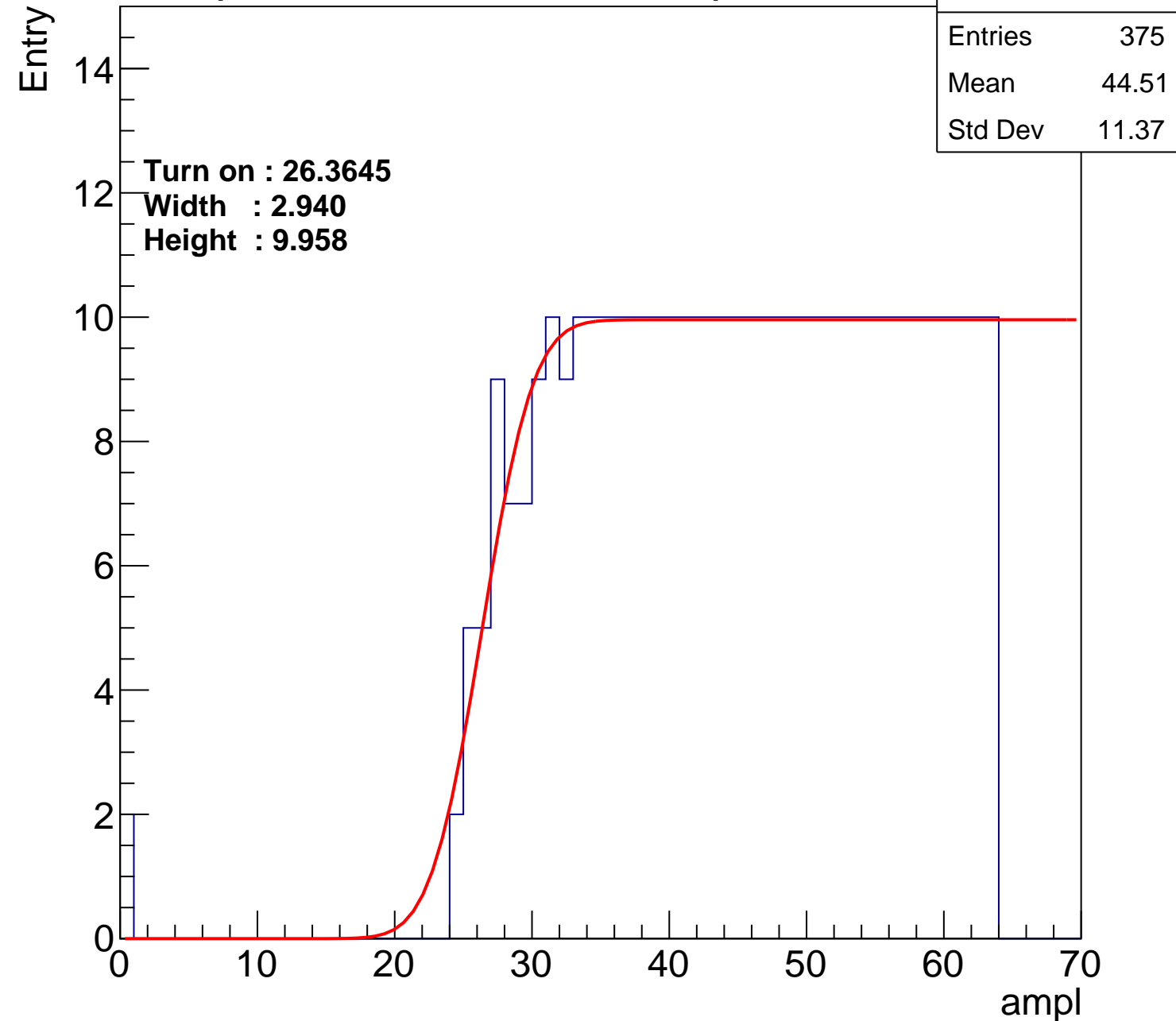
**Width : 2.940**

**Height : 9.958**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch97

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.75
Std Dev	11.12

Turn on : 26.9607

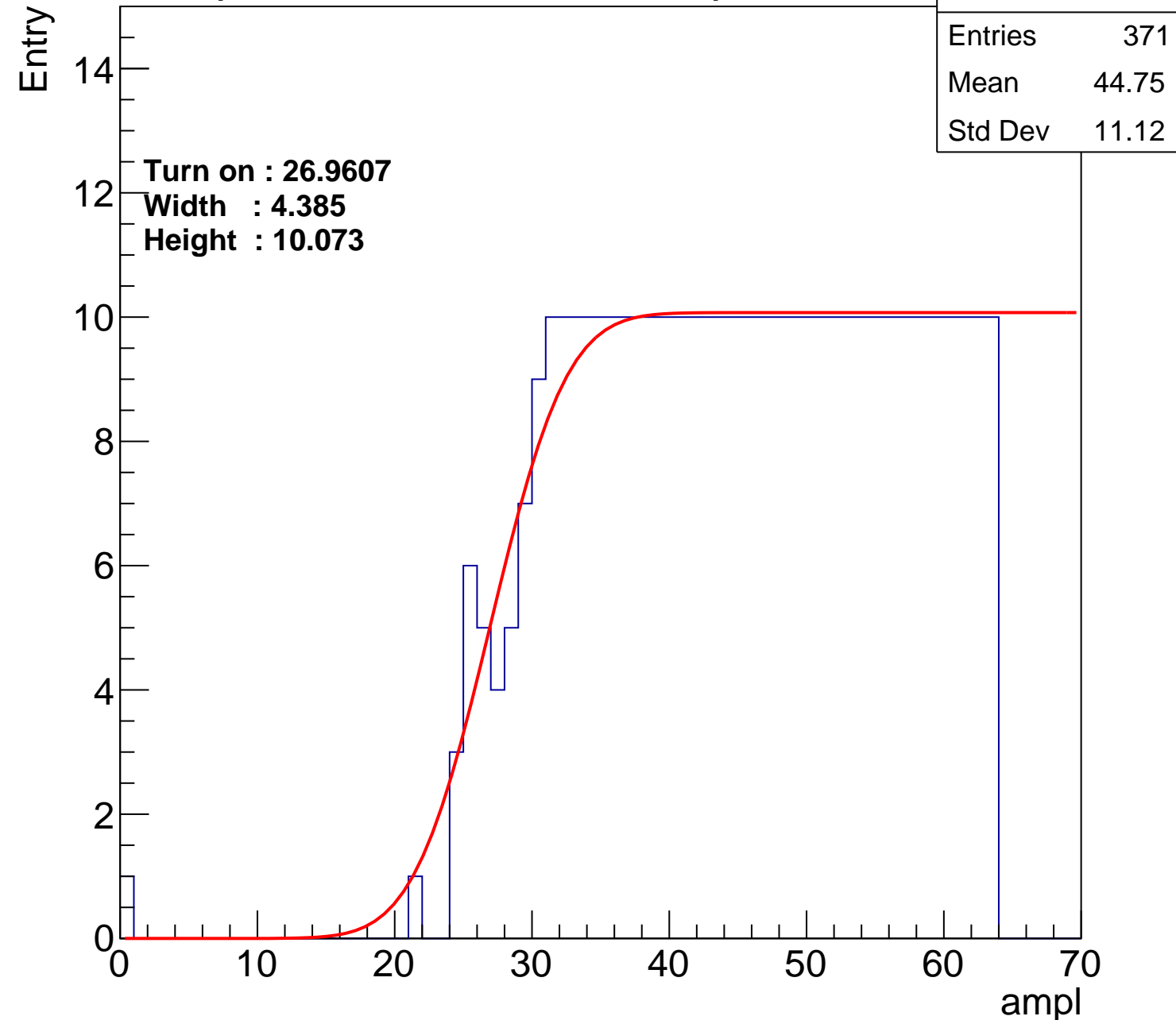
Width : 4.385

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch98

calib\_packv5\_042523\_0143.root, FC#12, port B1

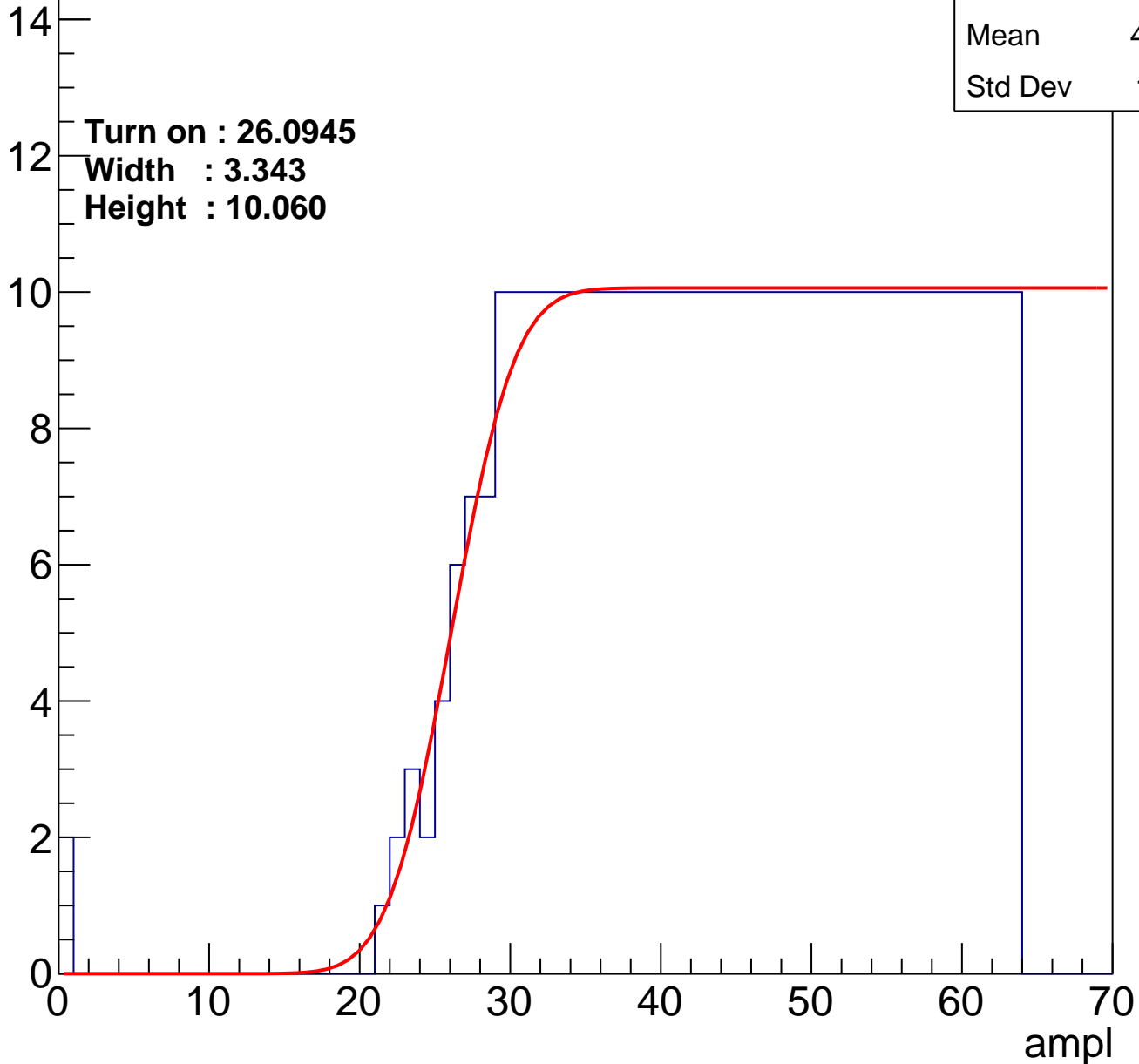
Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 26.0945

Width : 3.343

Height : 10.060

Entry



# B0L102S, U18-ch99

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	390
Mean	43.75
Std Dev	11.73

Turn on : 25.5408

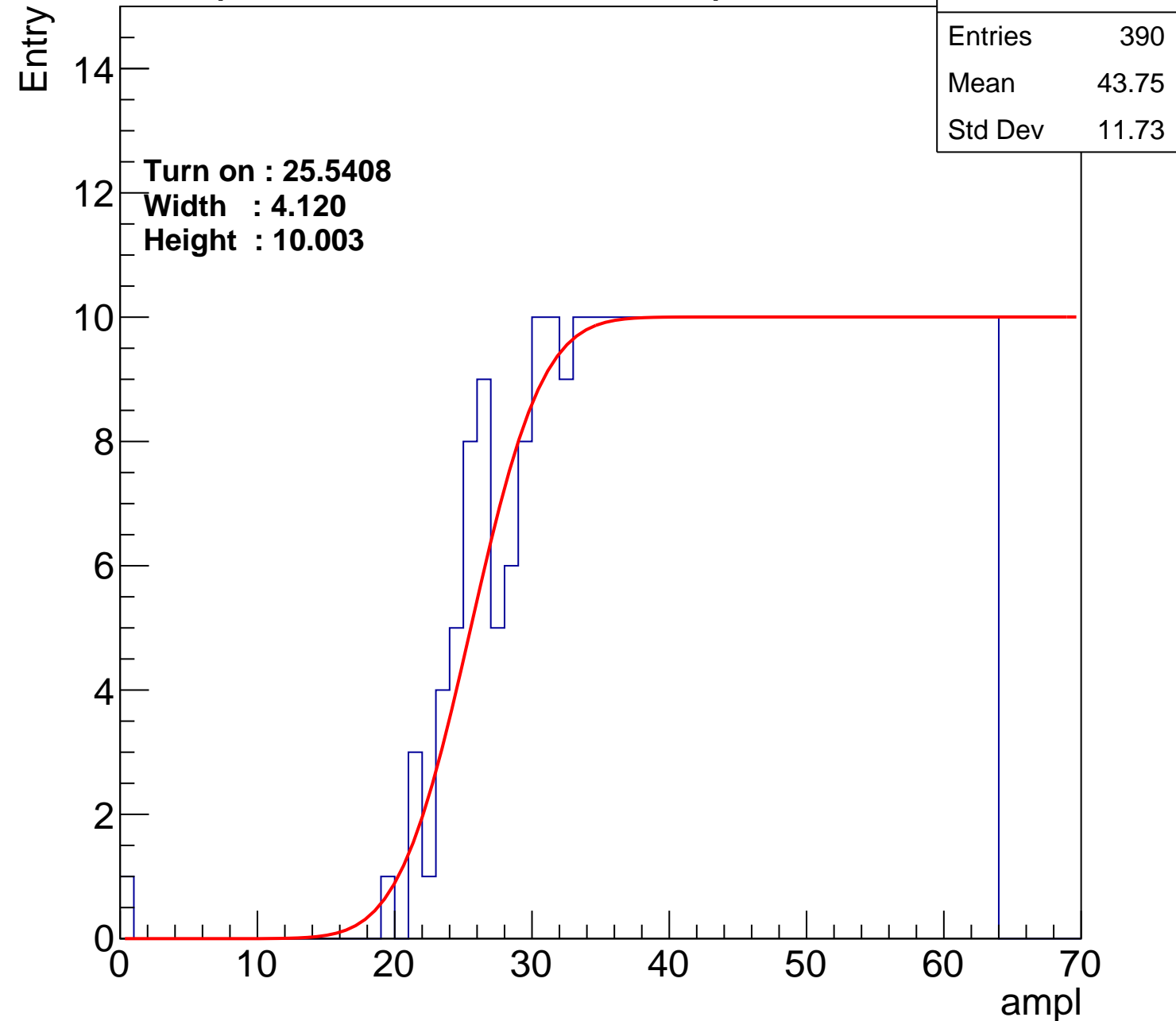
Width : 4.120

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch100

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.73
Std Dev	11.94

**Turn on : 24.0100**

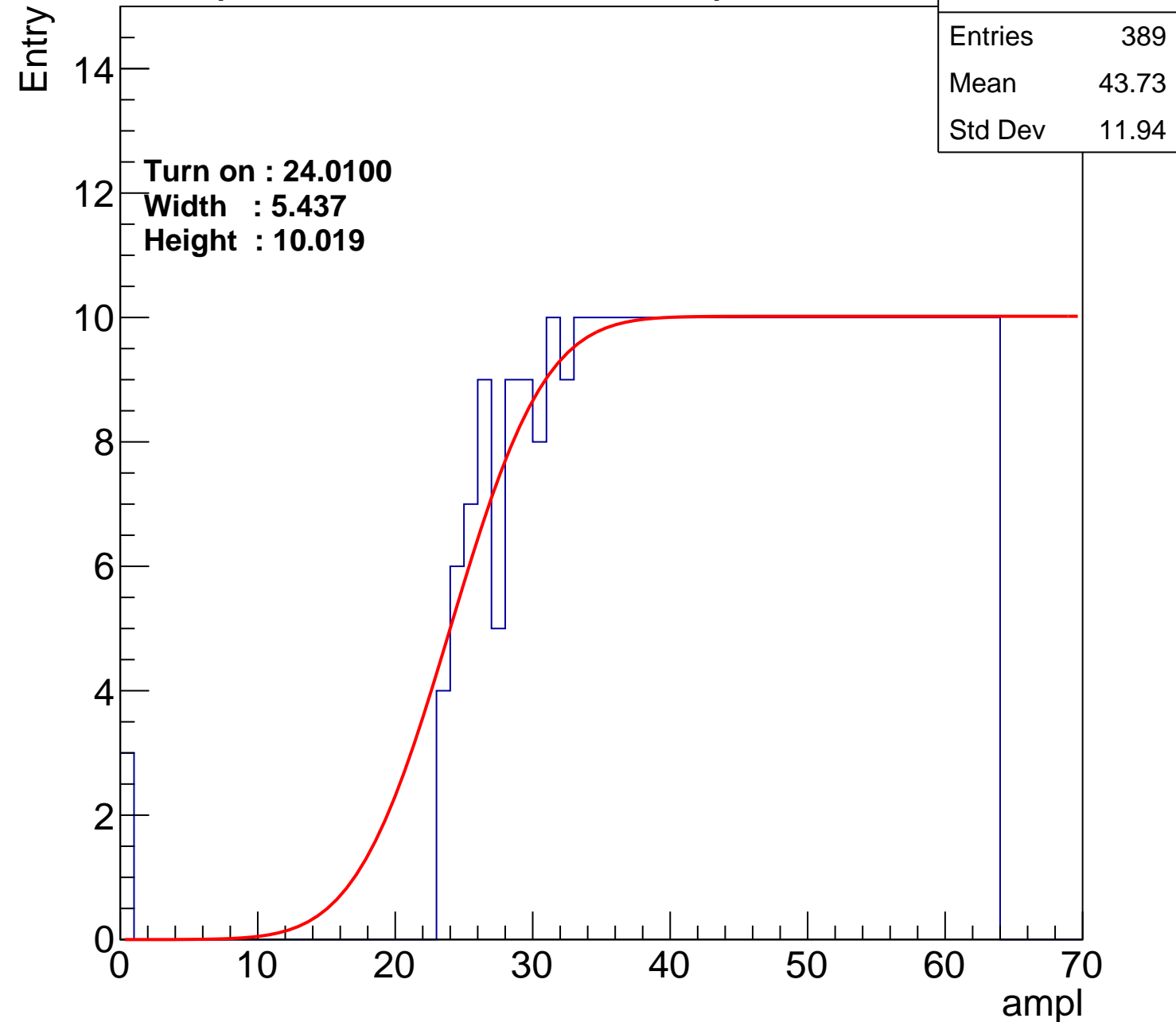
**Width : 5.437**

**Height : 10.019**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch101

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.96
Std Dev	11.14

**Turn on : 27.8637**

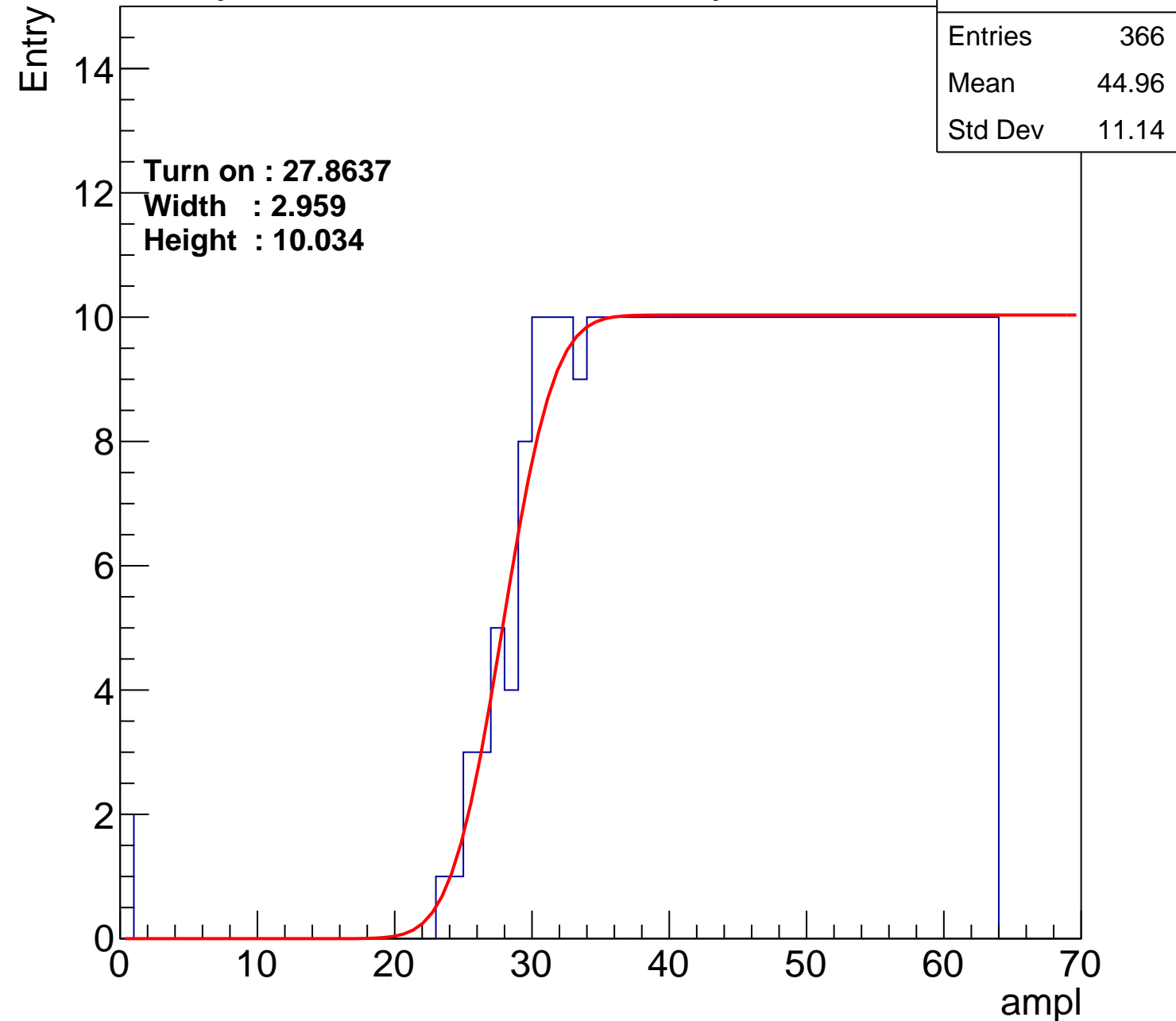
**Width : 2.959**

**Height : 10.034**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch102

calib\_packv5\_042523\_0143.root, FC#12, port B1

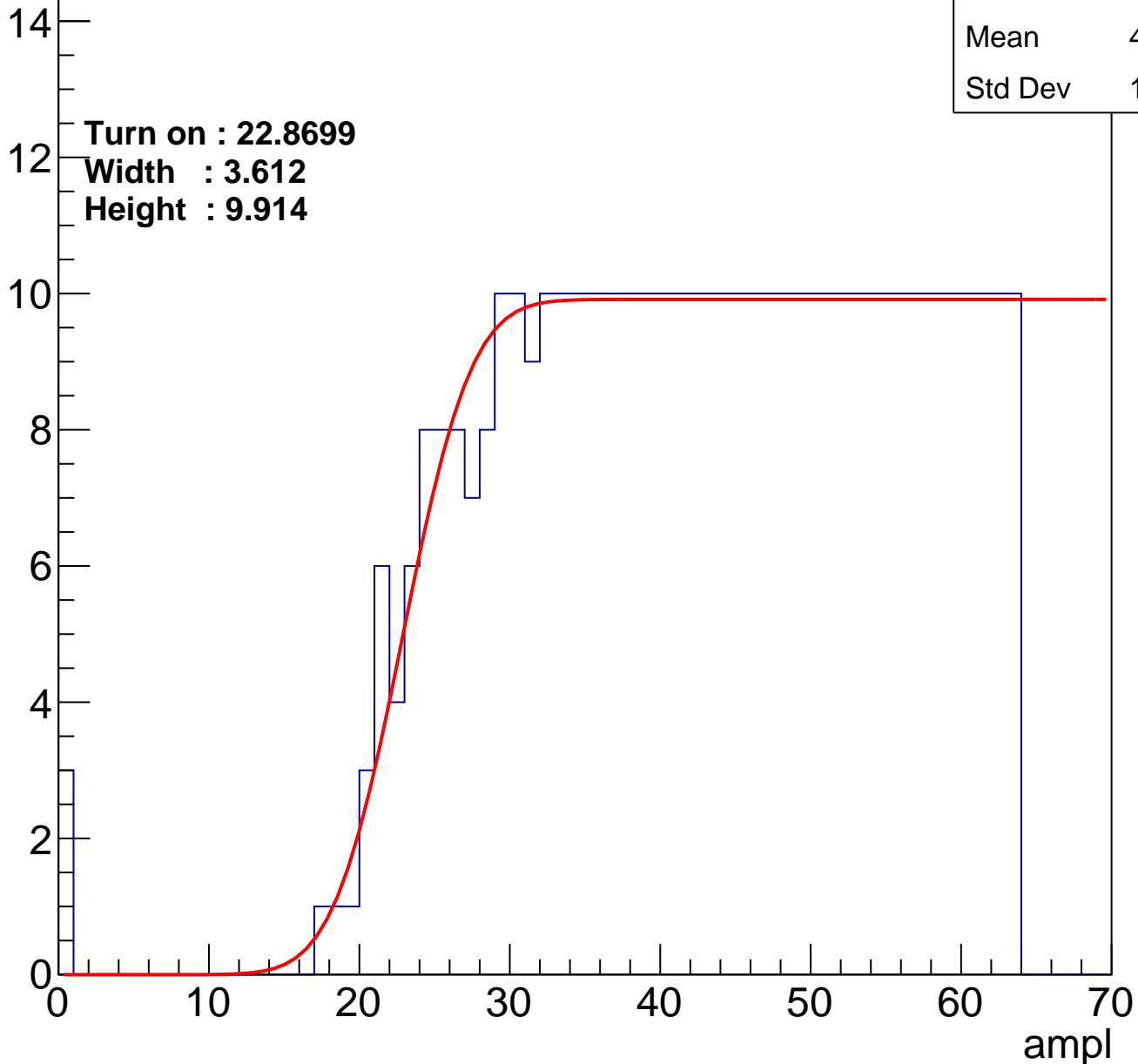
Entries	413
Mean	42.49
Std Dev	12.65

Turn on : 22.8699

Width : 3.612

Height : 9.914

Entry





# B0L102S, U18-ch103

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.02
Std Dev	11.94

**Turn on : 26.4423**

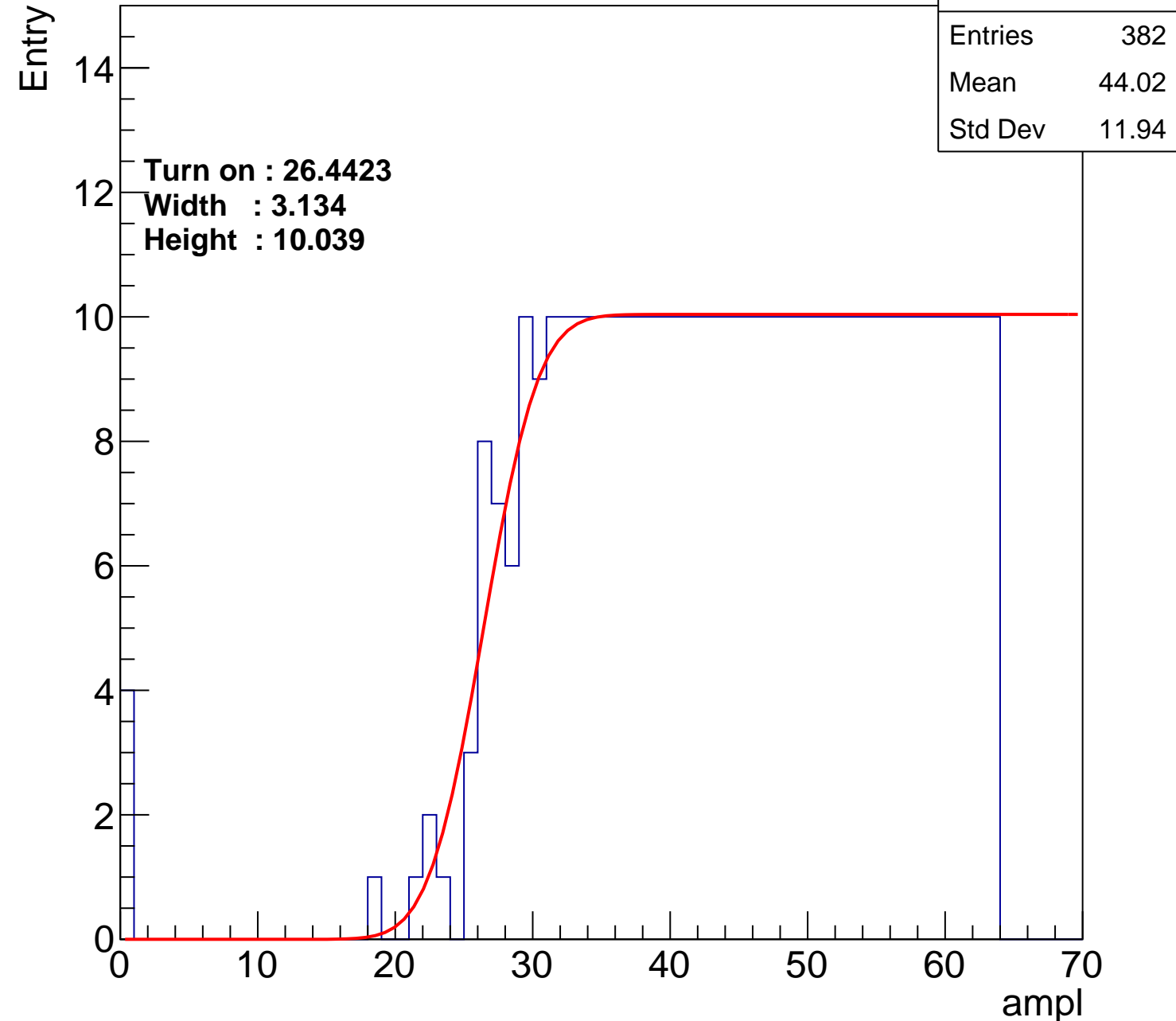
**Width : 3.134**

**Height : 10.039**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch104

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	43.93
Std Dev	12.31

Turn on : 27.0506

Width : 2.928

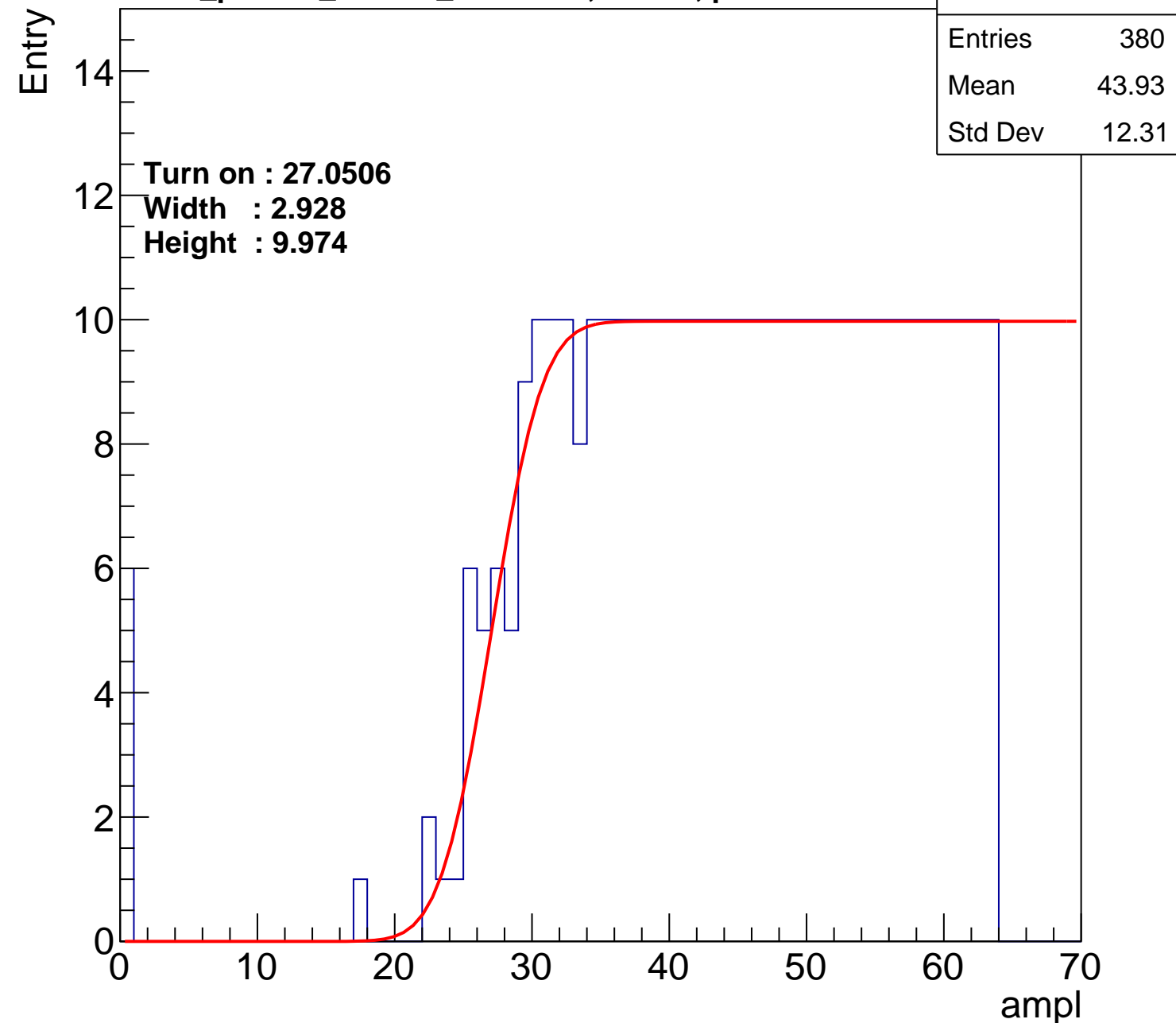
Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U18-ch105

calib\_packv5\_042523\_0143.root, FC#12, port B1

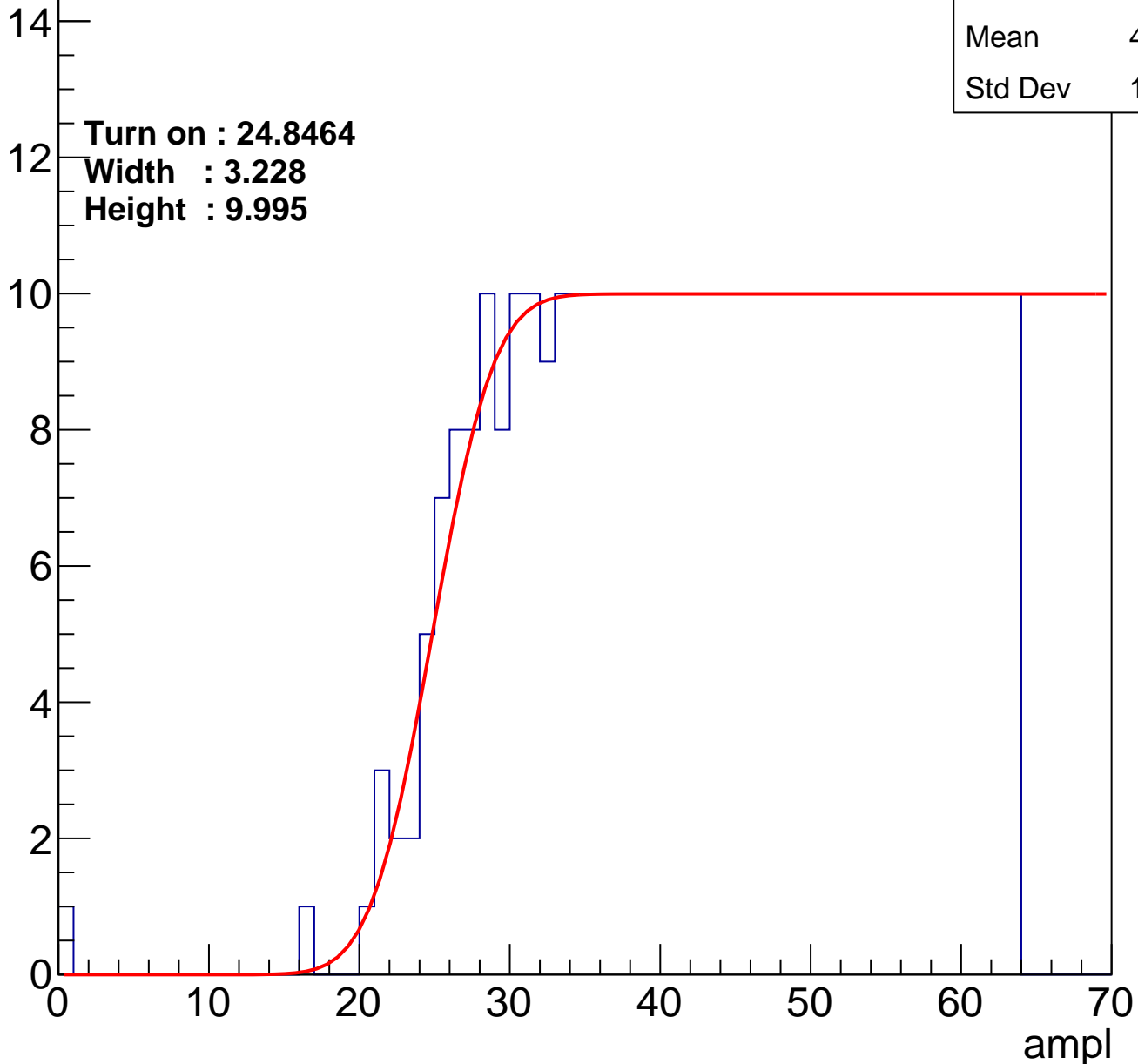
Entries	395
Mean	43.54
Std Dev	11.82

Turn on : 24.8464

Width : 3.228

Height : 9.995

Entry



# B0L102S, U18-ch106

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	402
Mean	43.1
Std Dev	12.33

Turn on : 25.2110

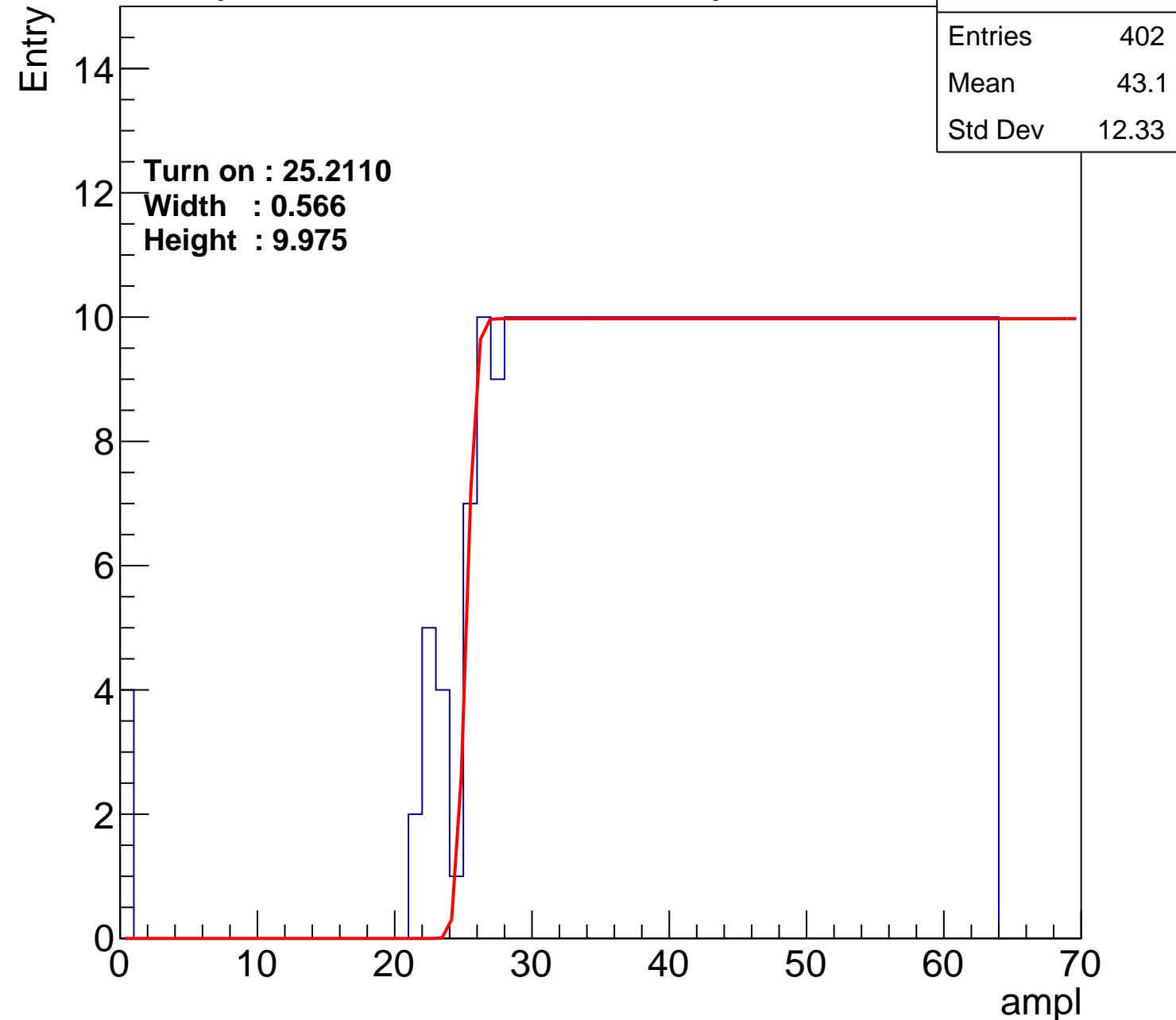
Width : 0.566

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch107

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.74
Std Dev	11.77

**Turn on : 25.3885**

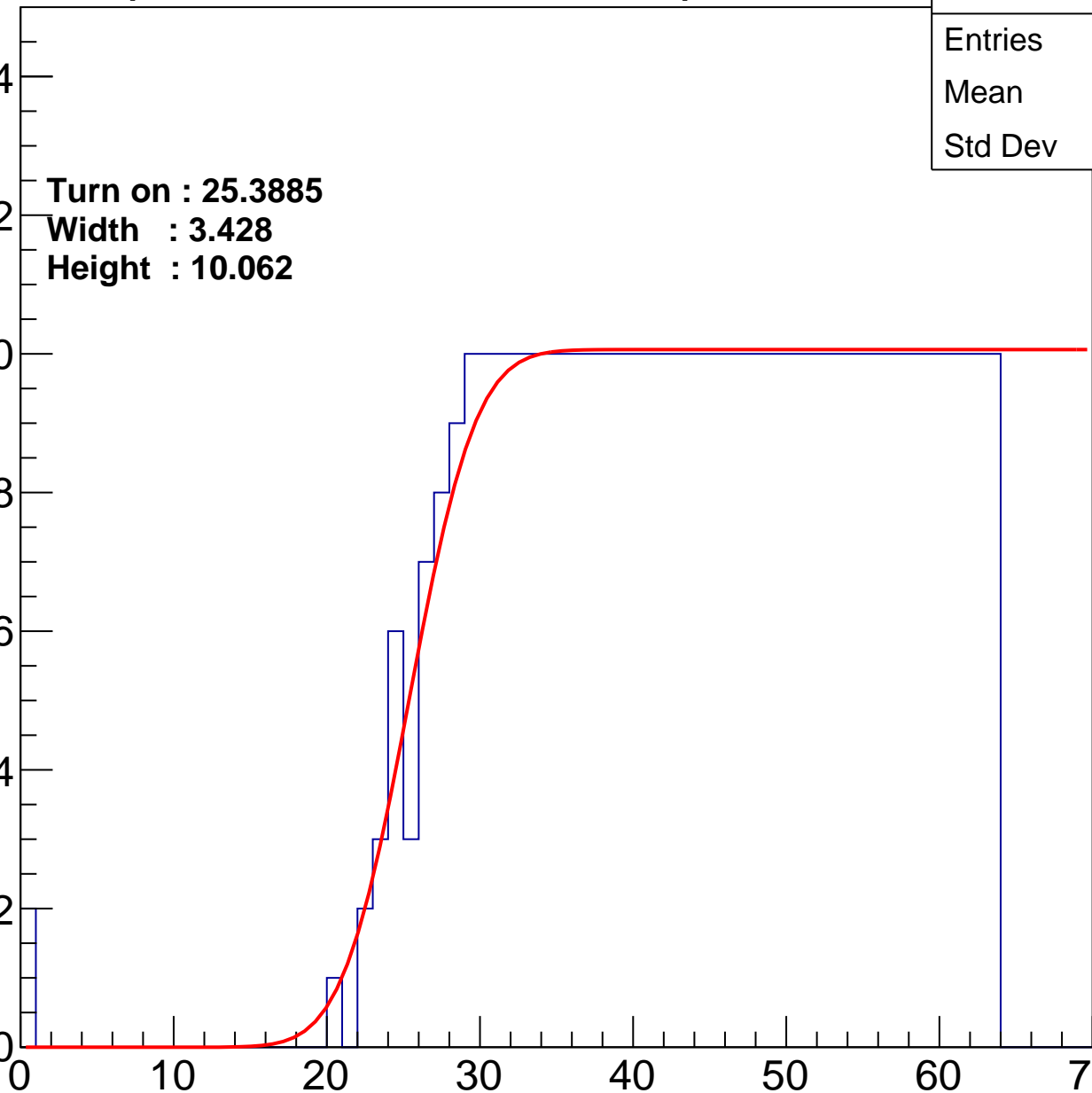
**Width : 3.428**

**Height : 10.062**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch108

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.56
Std Dev	11.9

**Turn on : 24.9754**

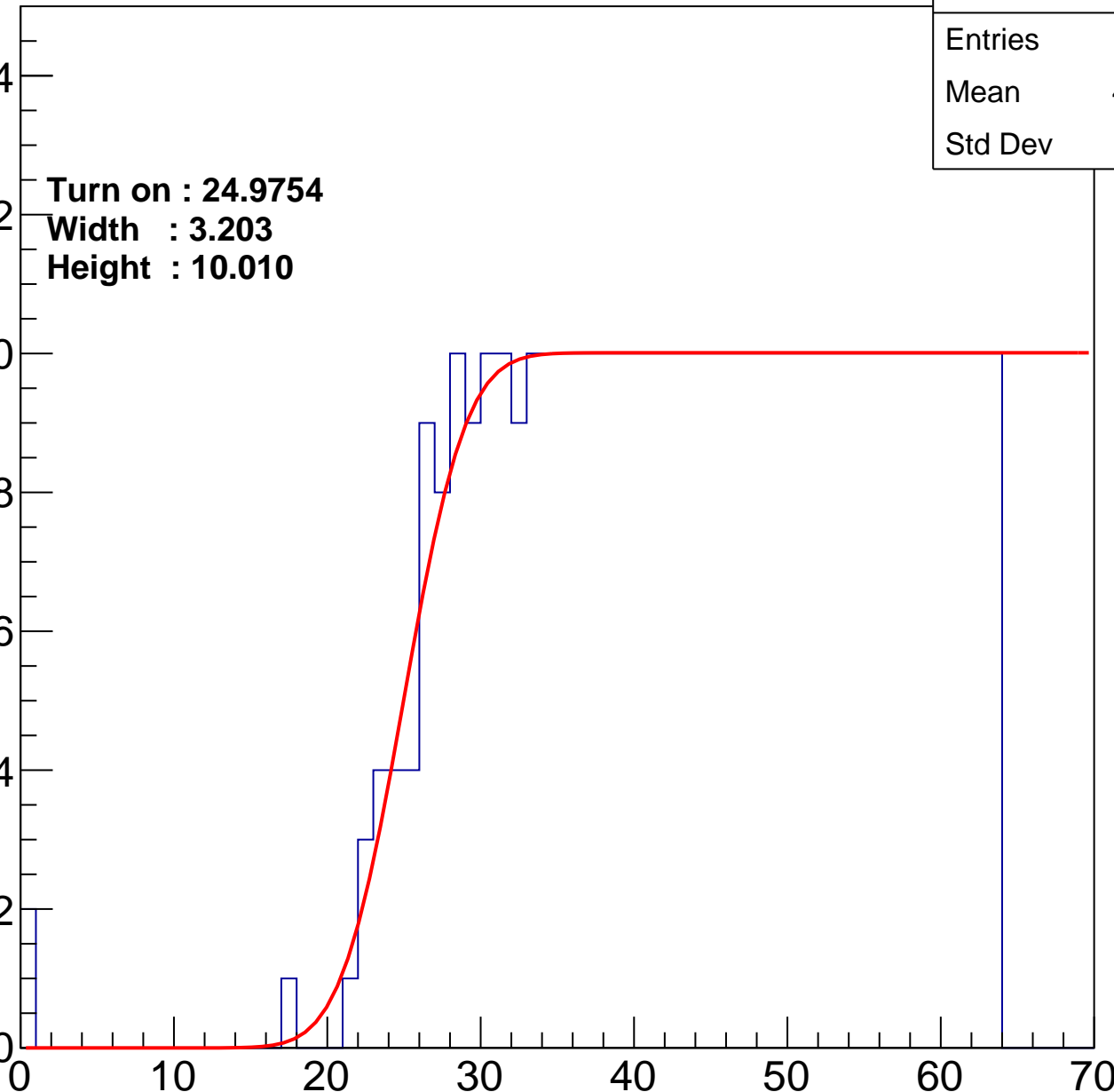
**Width : 3.203**

**Height : 10.010**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch109

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.82
Std Dev	11.79

Turn on : 25.4774

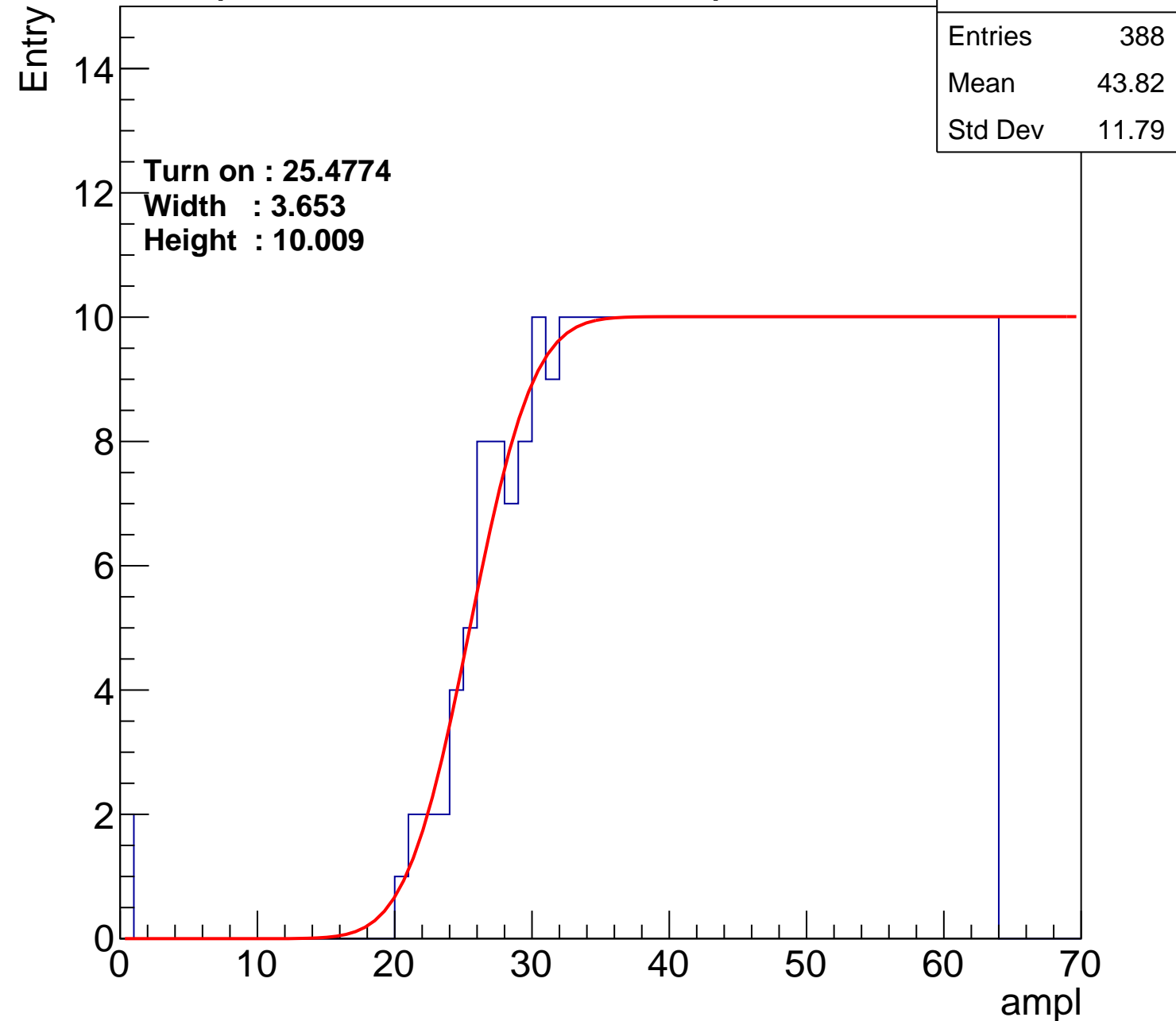
Width : 3.653

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch110

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	401
Mean	43.17
Std Dev	12.16

Turn on : 24.3897

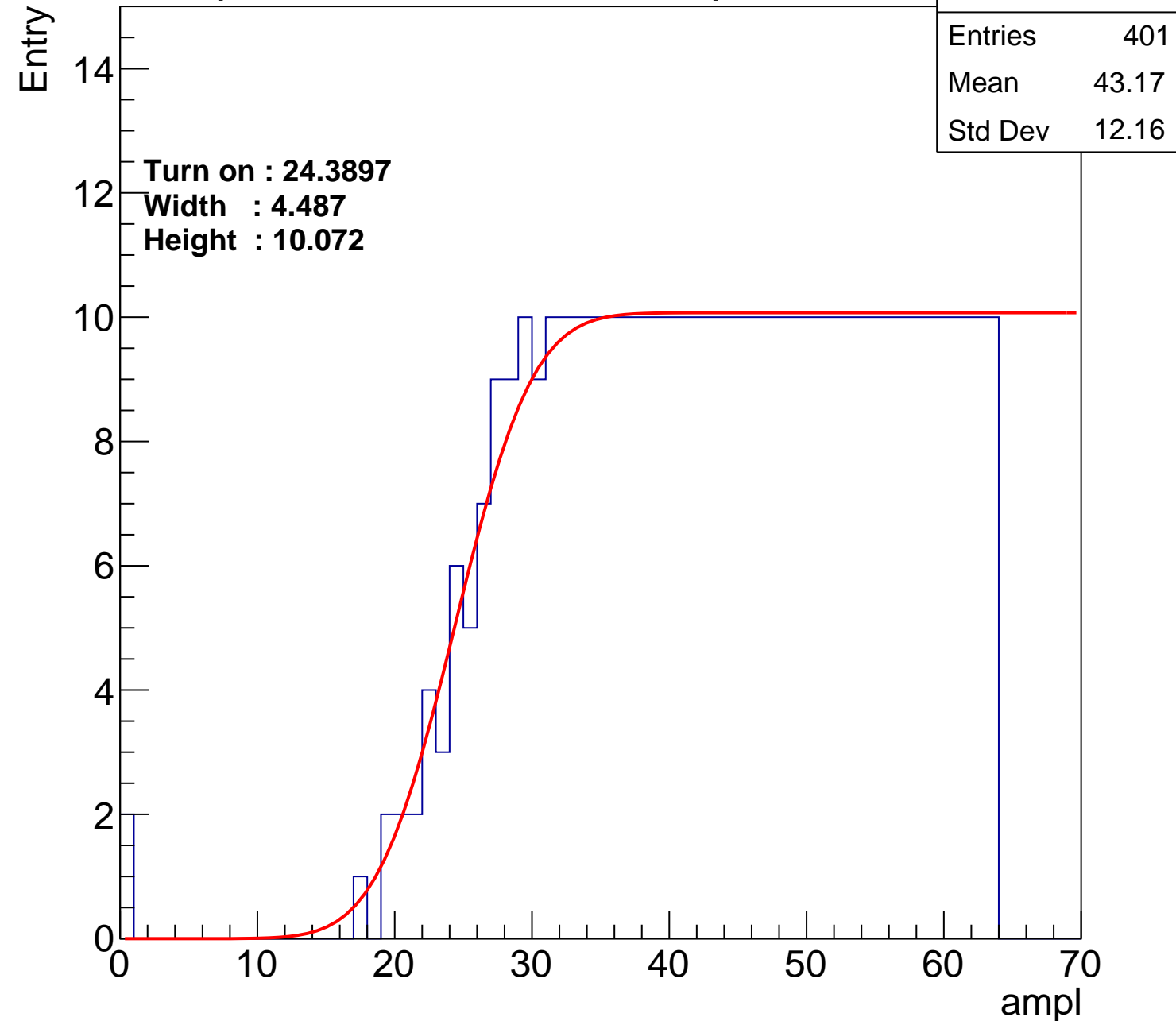
Width : 4.487

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch111

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.82
Std Dev	12.1

Turn on : 26.0774

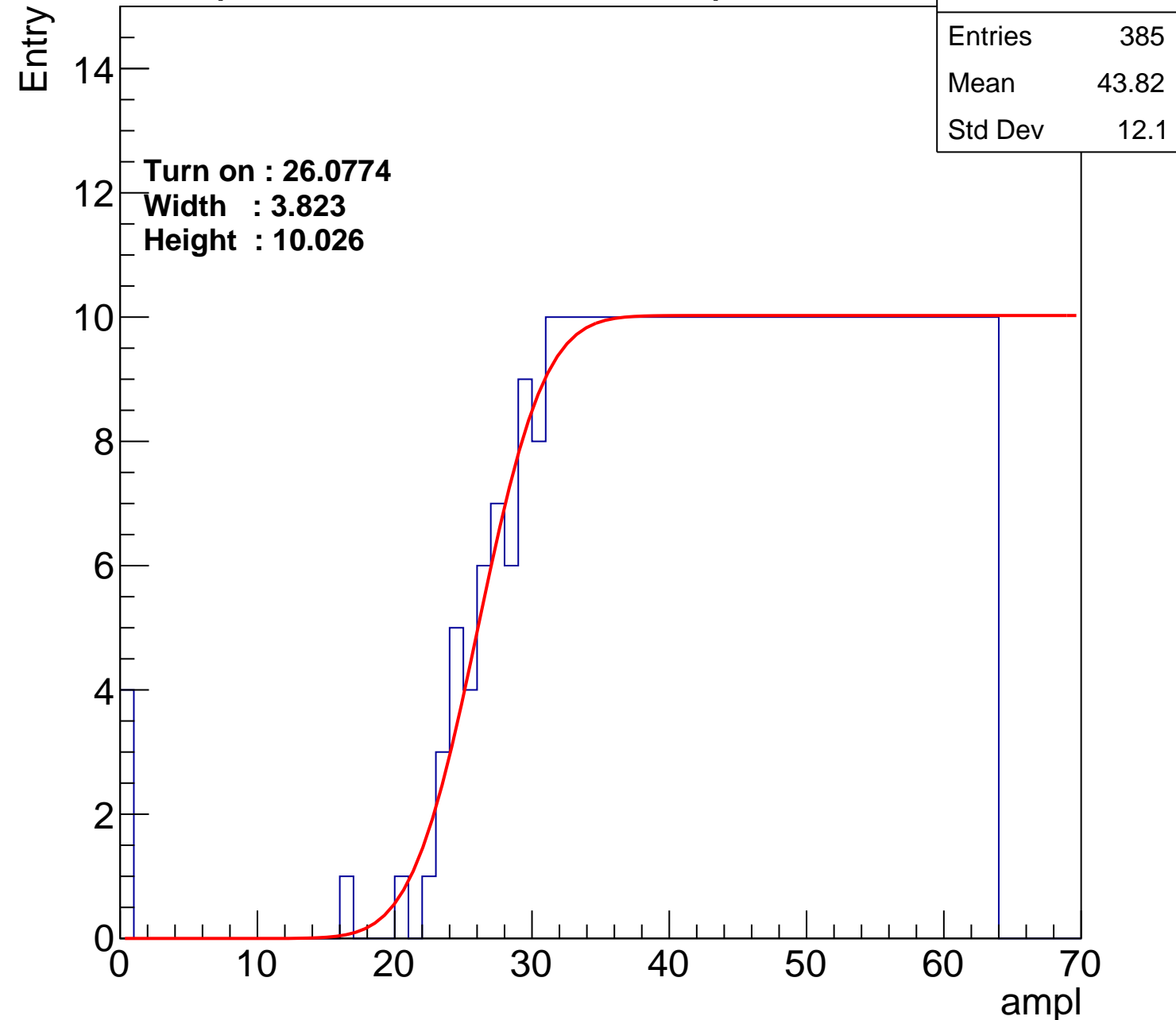
Width : 3.823

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch112

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	408
Mean	42.69
Std Dev	12.76

Turn on : 23.6253

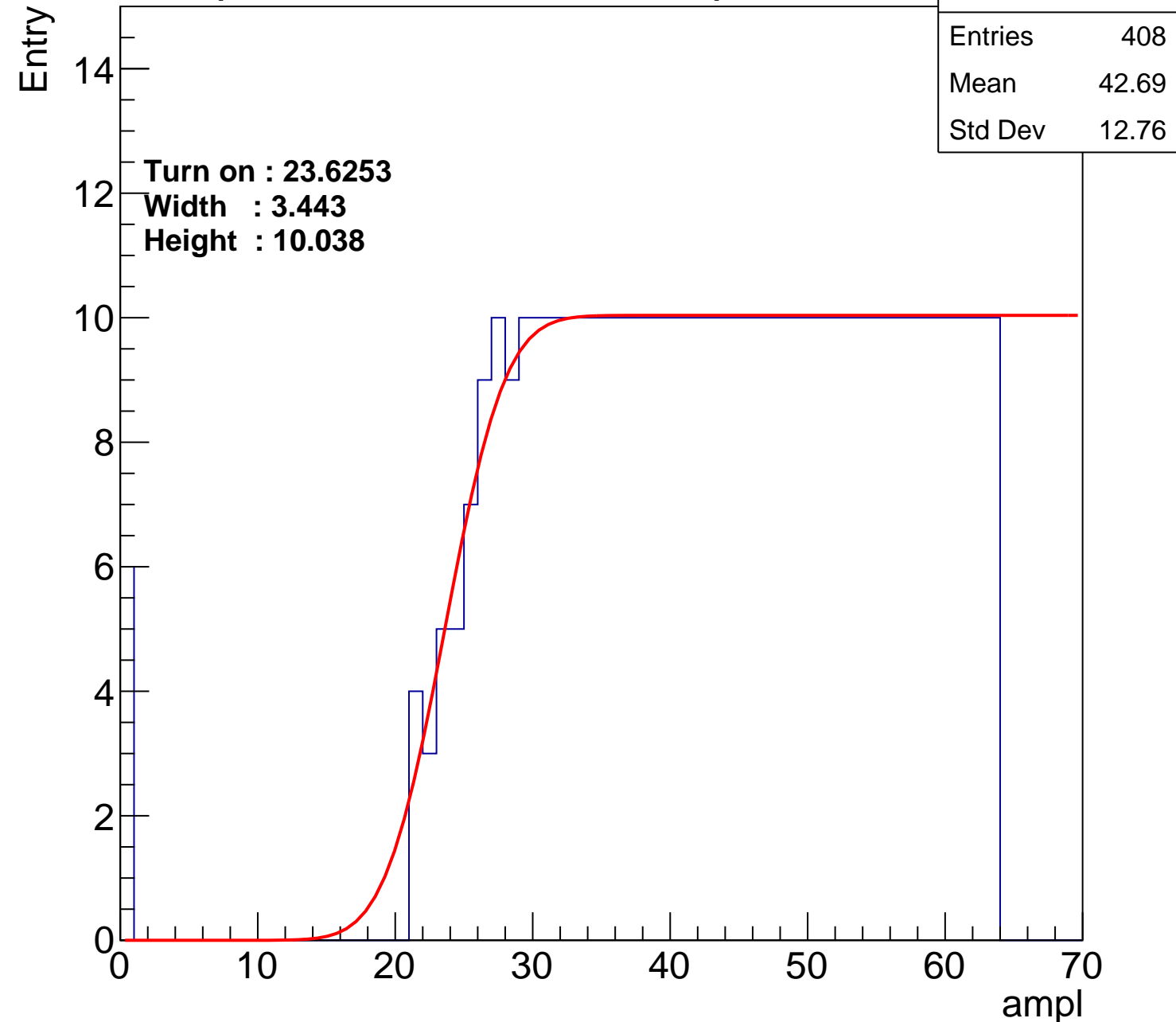
Width : 3.443

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch113

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.42
Std Dev	12.41

Turn on : 25.2555

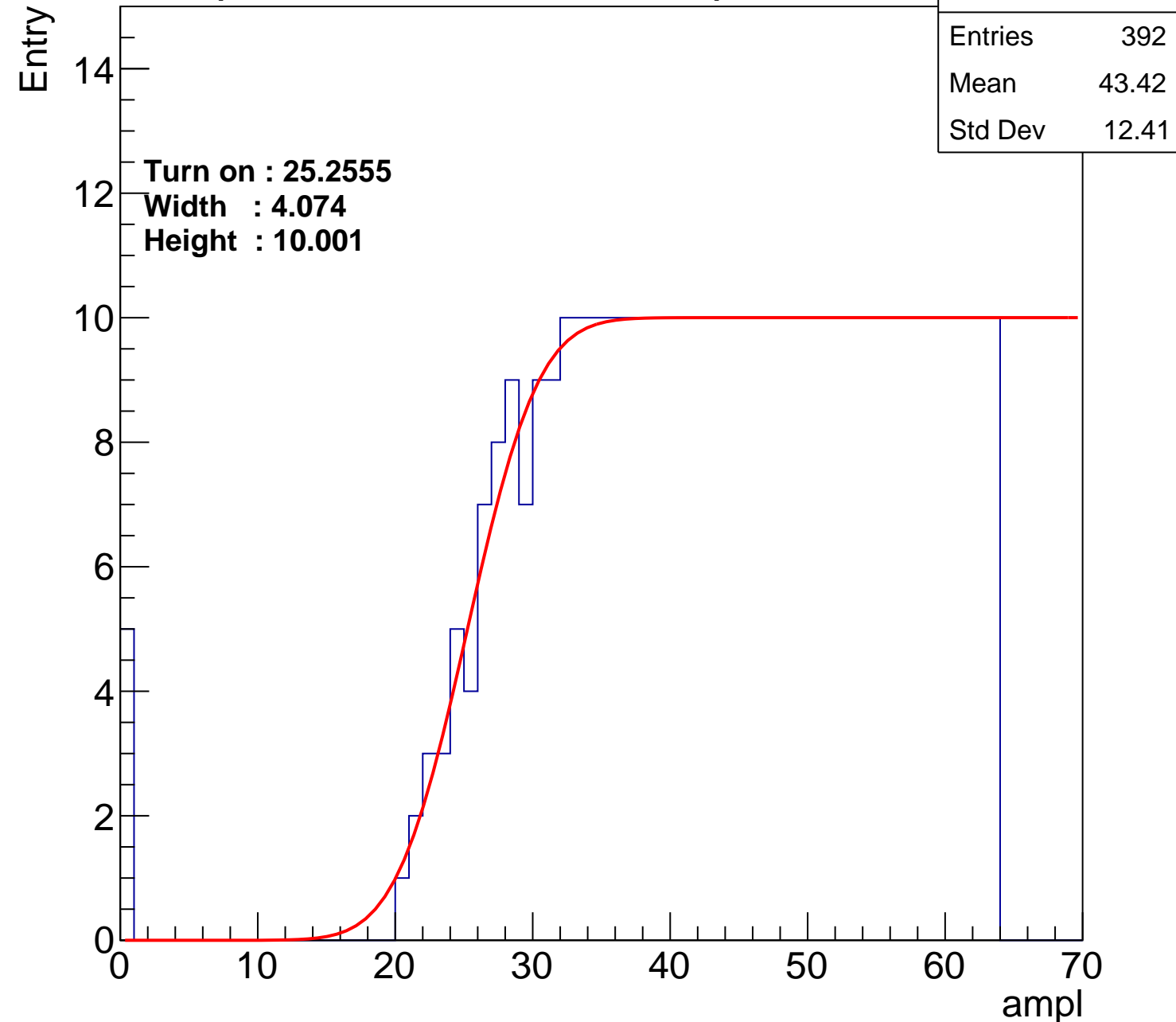
Width : 4.074

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch114

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	414
Mean	42.54
Std Dev	12.73

Turn on : 23.4652

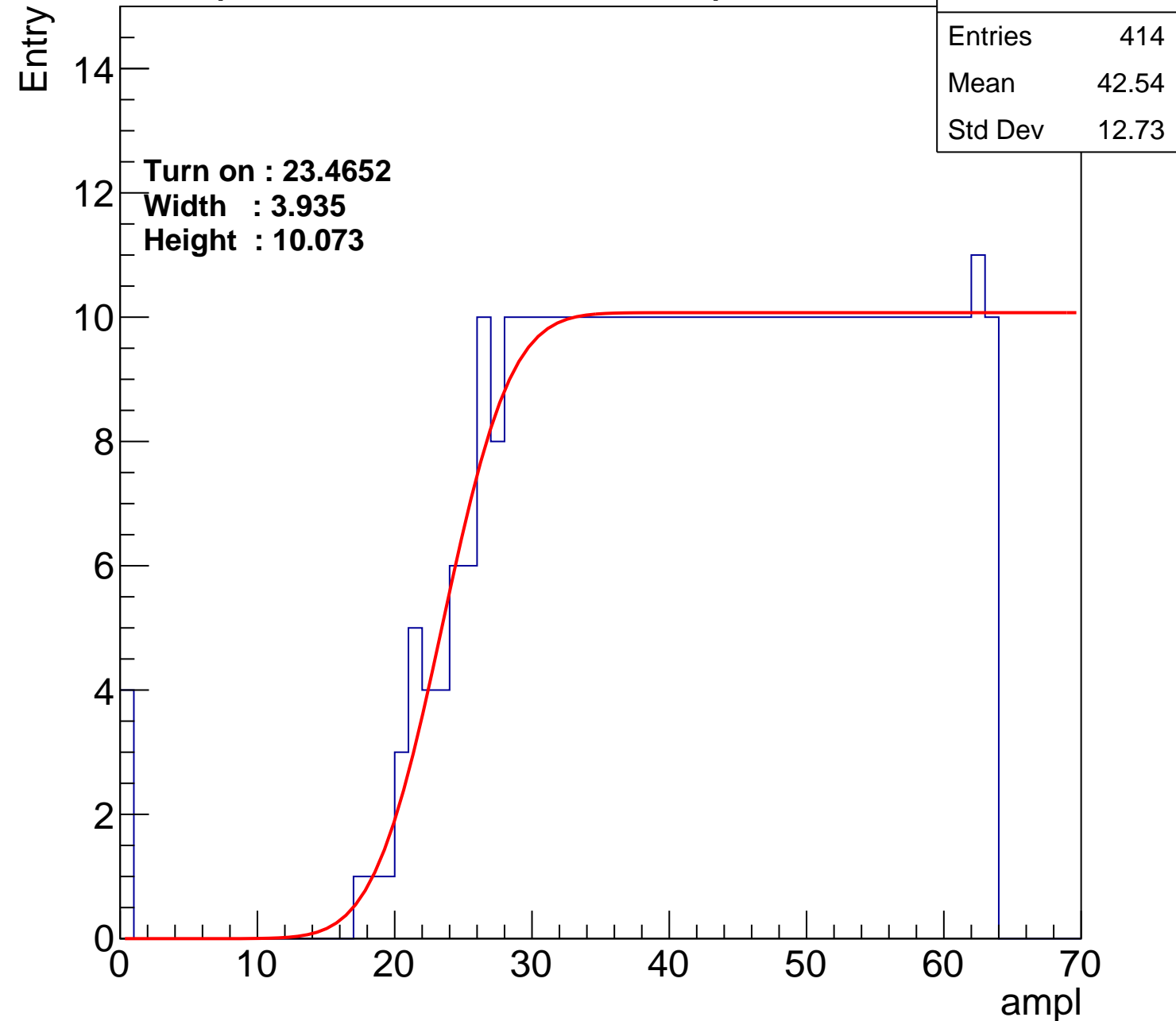
Width : 3.935

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch115

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.13
Std Dev	11.49

Turn on : 26.2266

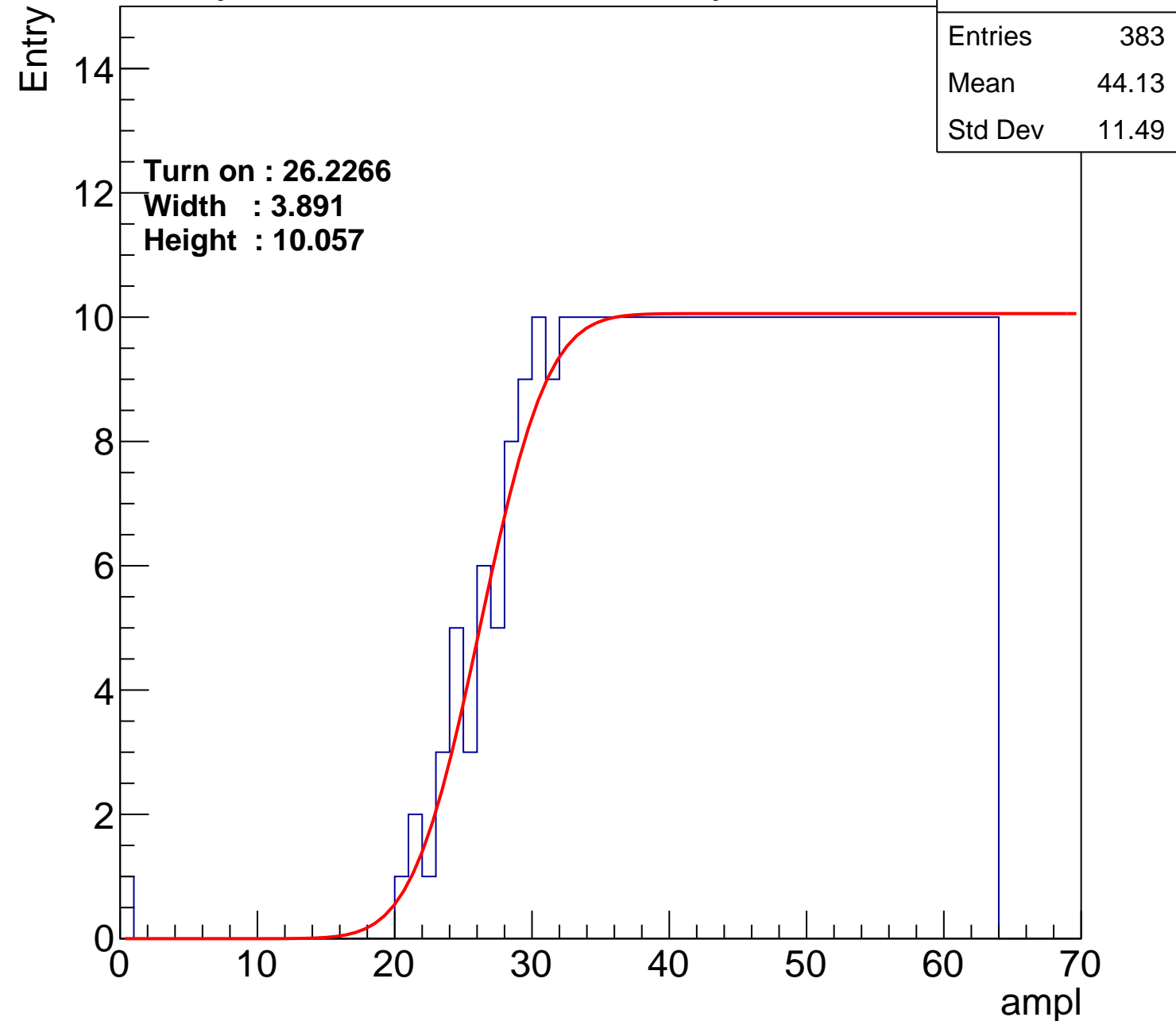
Width : 3.891

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch116

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	420
Mean	42.13
Std Dev	12.95

Turn on : 23.1179

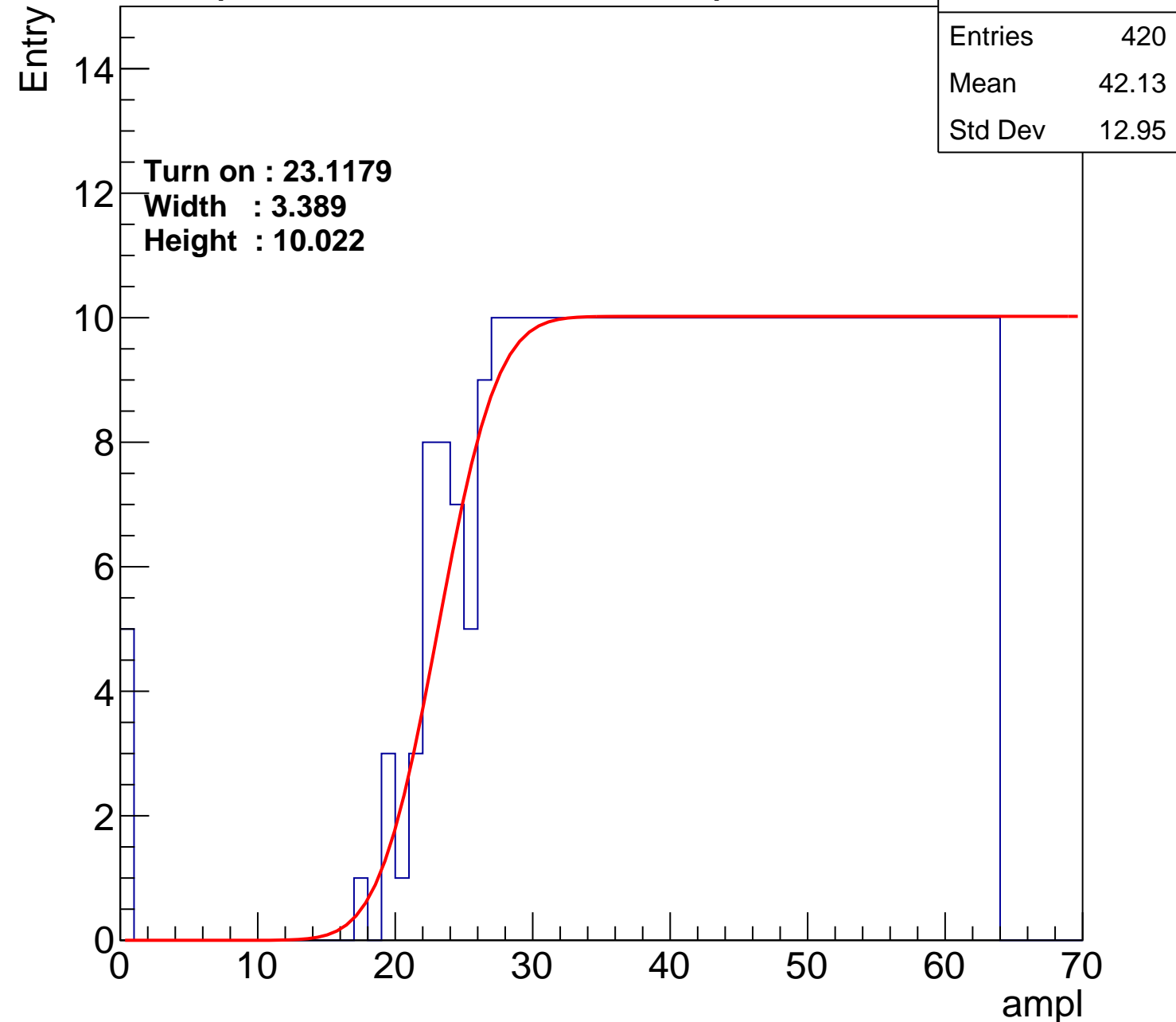
Width : 3.389

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch117

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	44.97
Std Dev	11.31

Turn on : 27.9843

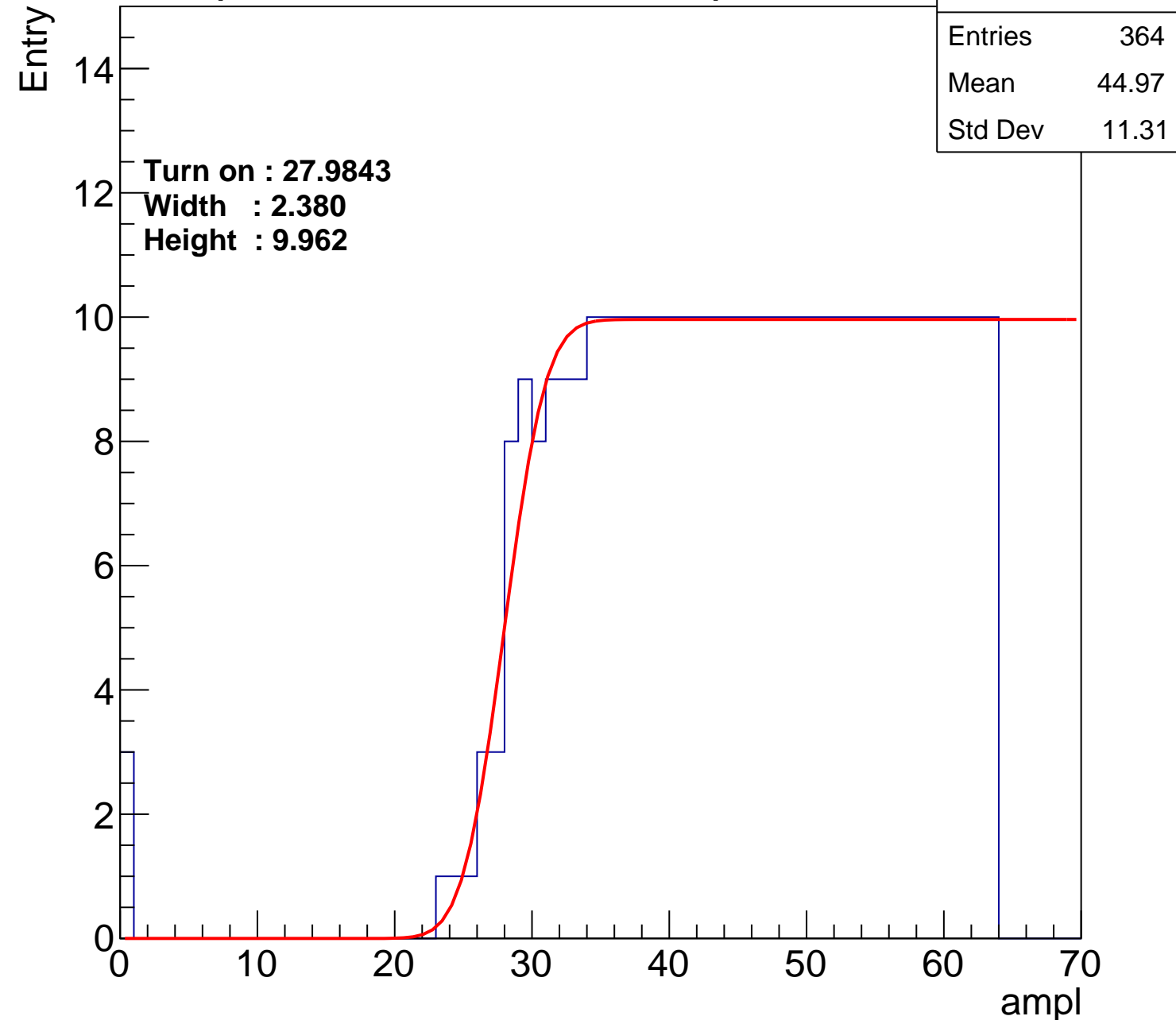
Width : 2.380

Height : 9.962

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch118

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.29
Std Dev	11.67

Turn on : 25.7275

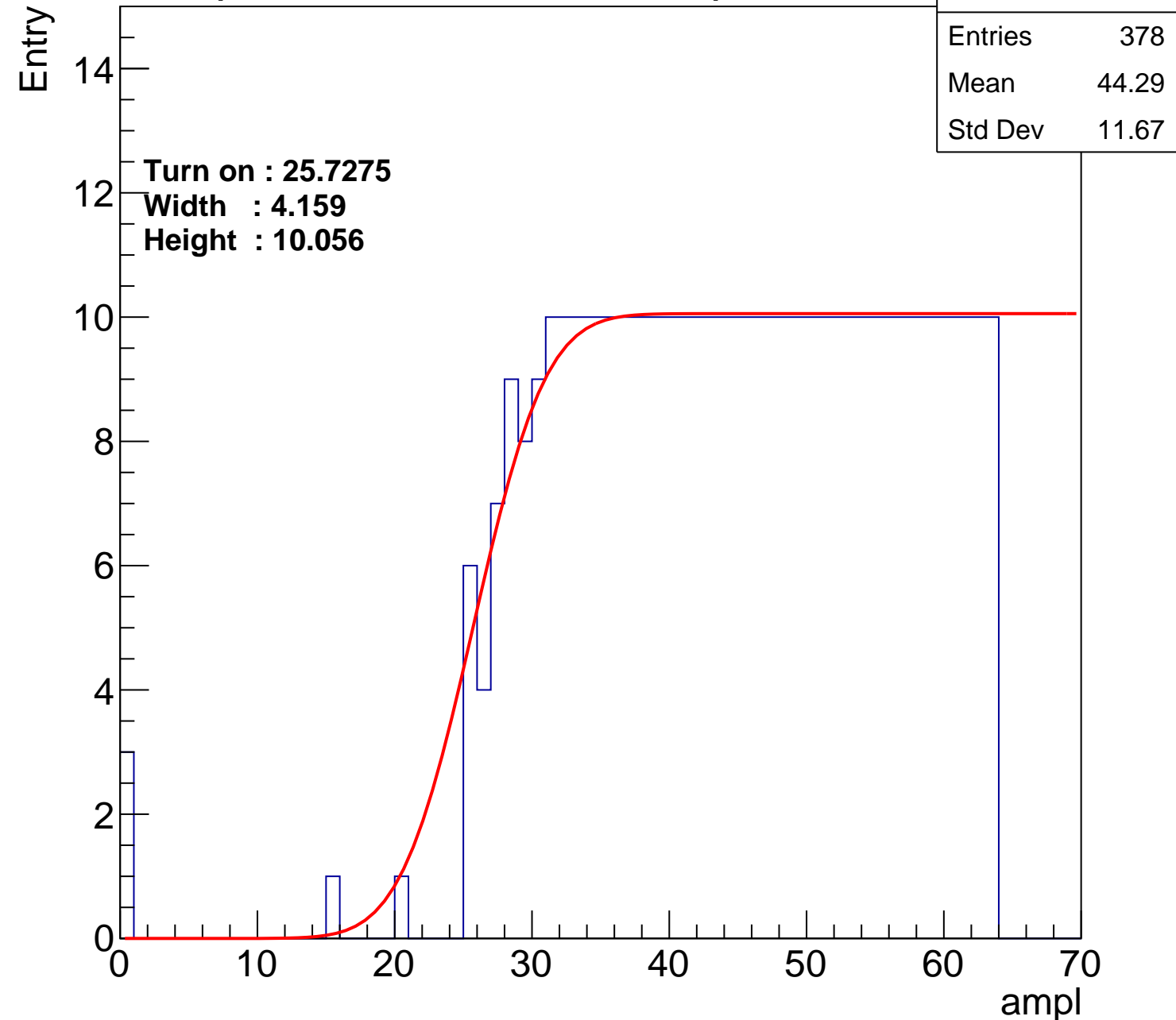
Width : 4.159

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U18-ch119

calib\_packv5\_042523\_0143.root, FC#12, port B1

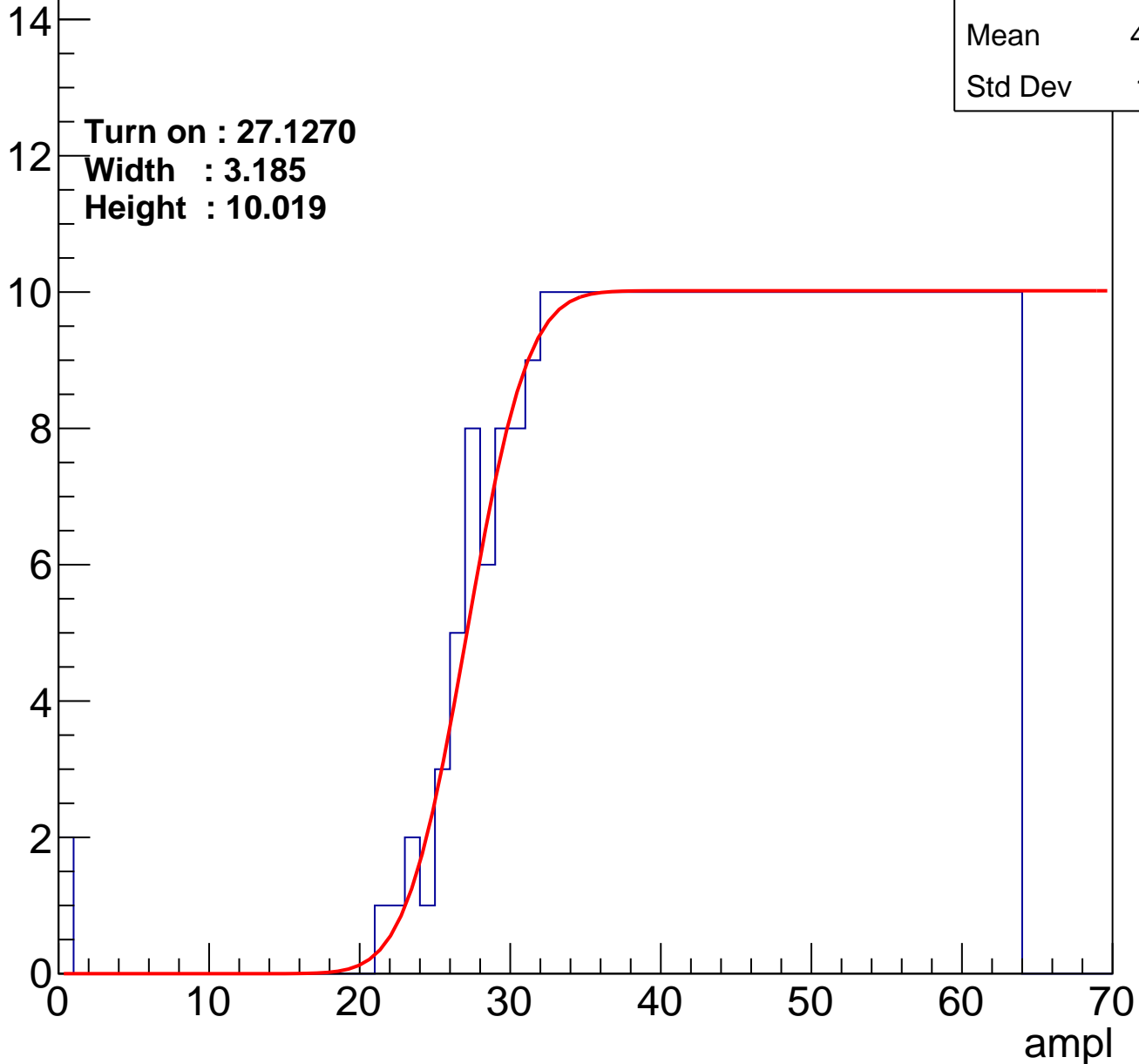
Entries	374
Mean	44.53
Std Dev	11.41

Turn on : 27.1270

Width : 3.185

Height : 10.019

Entry



# B0L102S, U18-ch120

calib\_packv5\_042523\_0143.root, FC#12, port B1

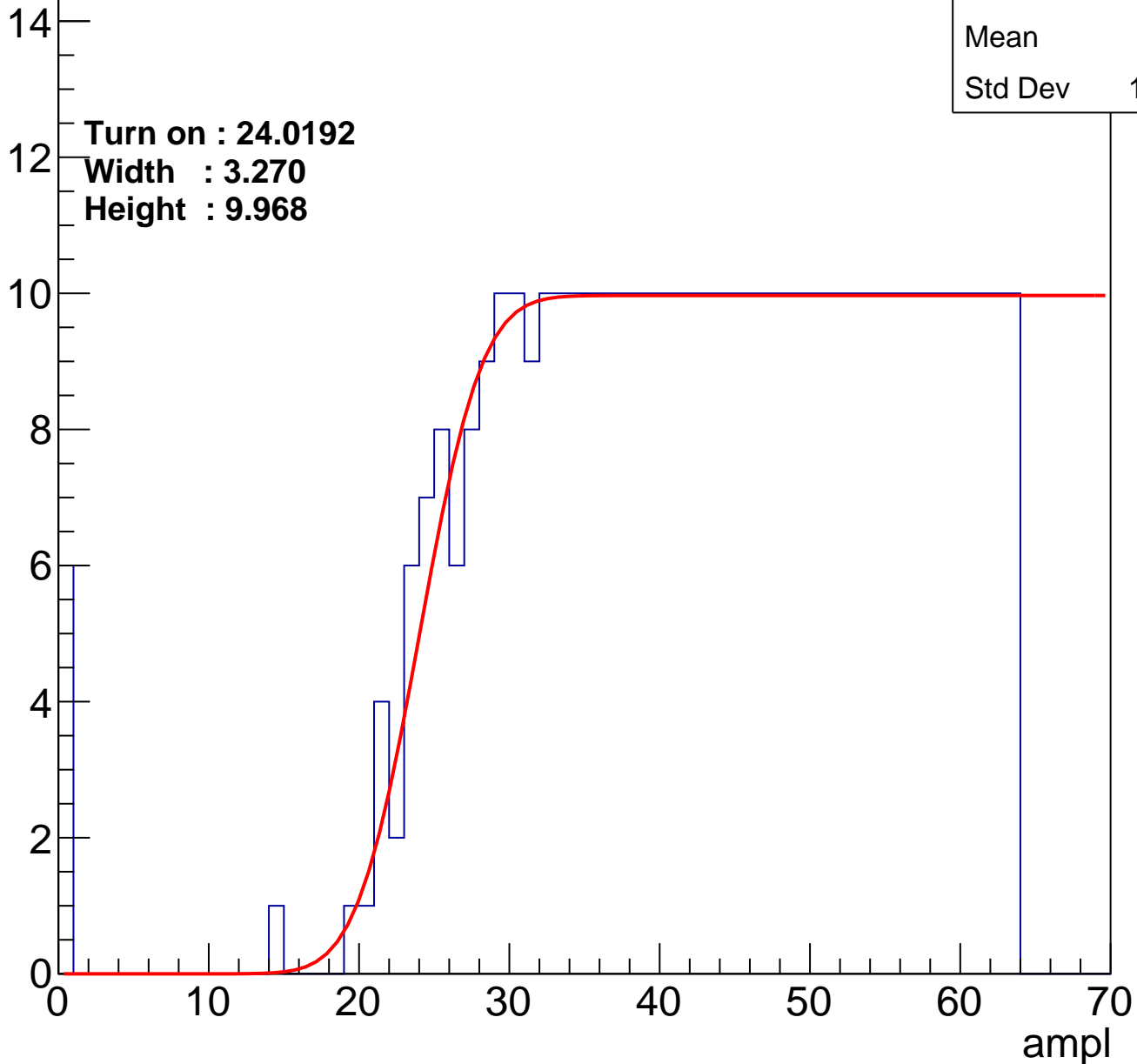
Entries	408
Mean	42.6
Std Dev	12.89

Turn on : 24.0192

Width : 3.270

Height : 9.968

Entry



# B0L102S, U18-ch121

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.18
Std Dev	12.58

Turn on : 24.8606

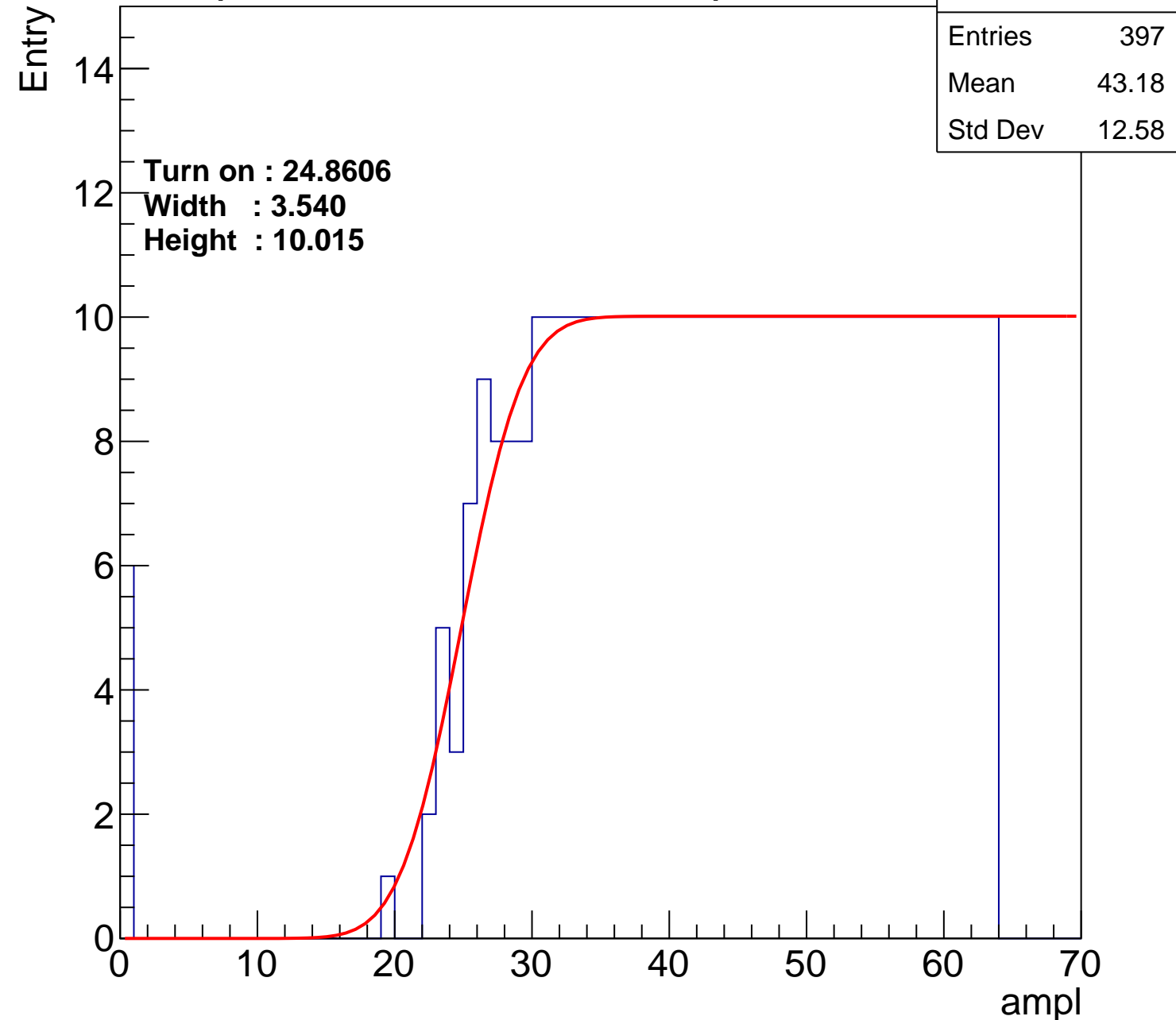
Width : 3.540

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch122

calib\_packv5\_042523\_0143.root, FC#12, port B1

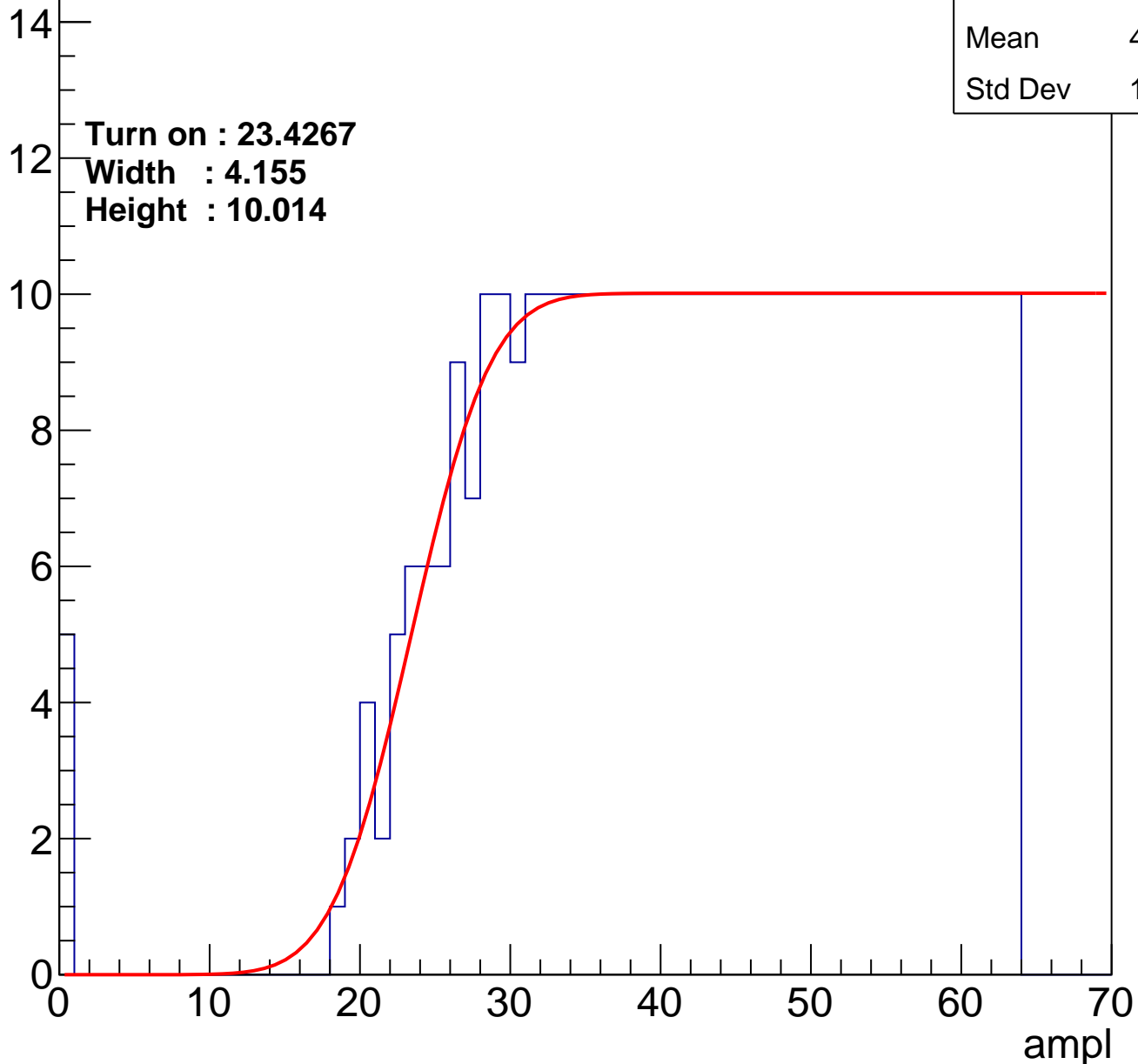
Entries	412
Mean	42.46
Std Dev	12.85

Turn on : 23.4267

Width : 4.155

Height : 10.014

Entry



# B0L102S, U18-ch123

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.58
Std Dev	12.03

Turn on : 25.5310

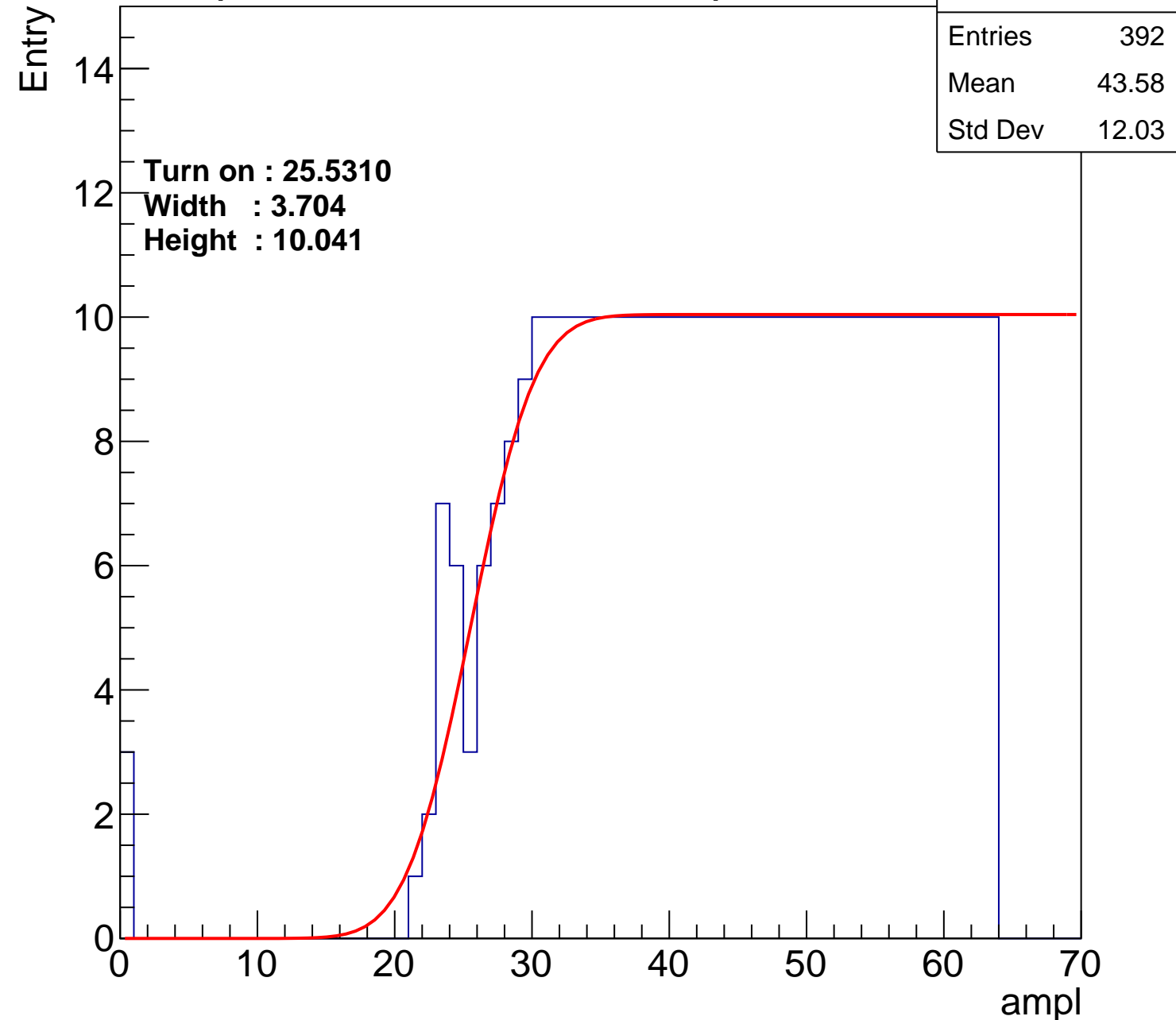
Width : 3.704

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch124

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	408
Mean	42.86
Std Dev	12.48

Turn on : 23.8243

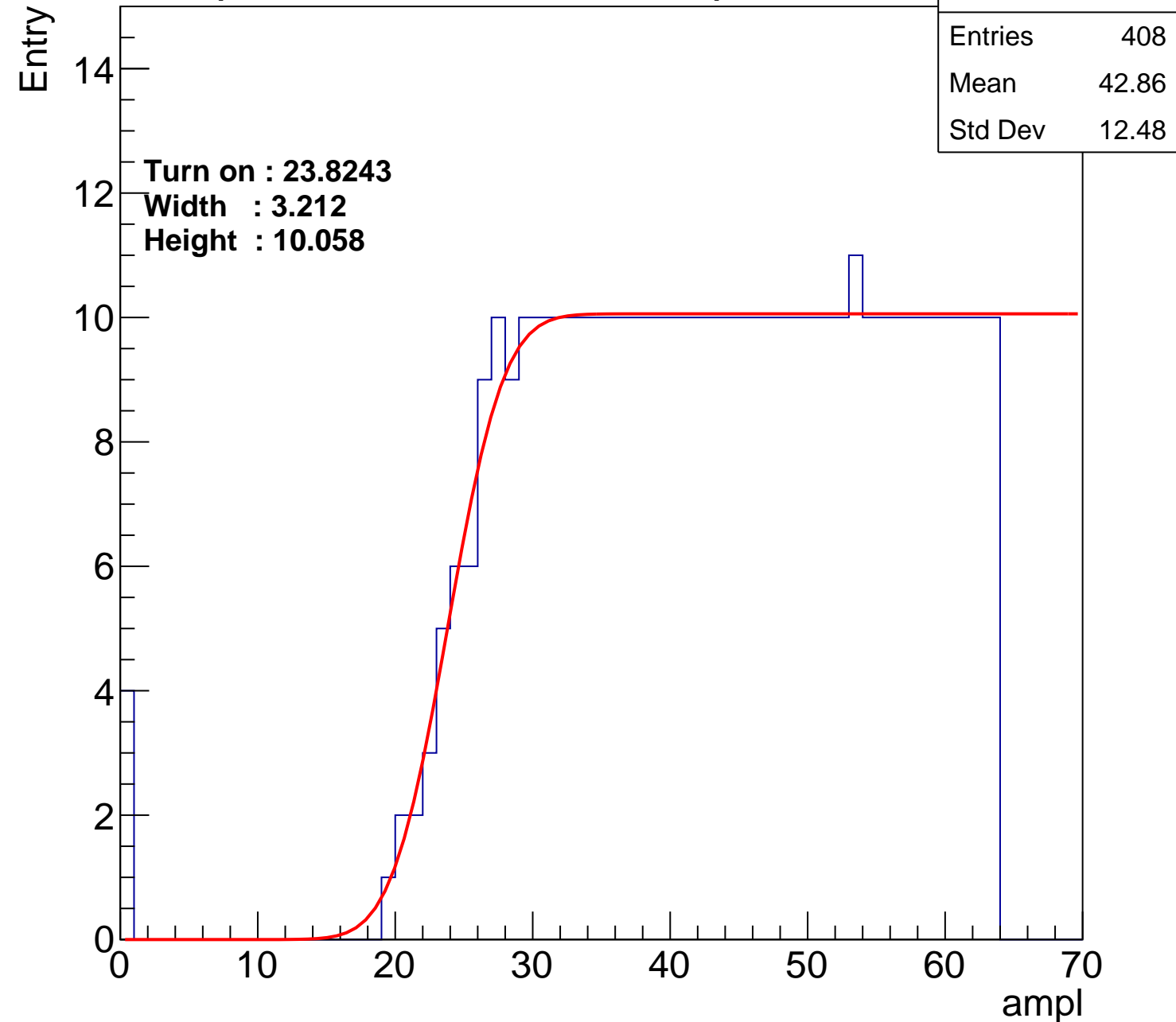
Width : 3.212

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch125

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.6
Std Dev	12.06

Turn on : 25.3301

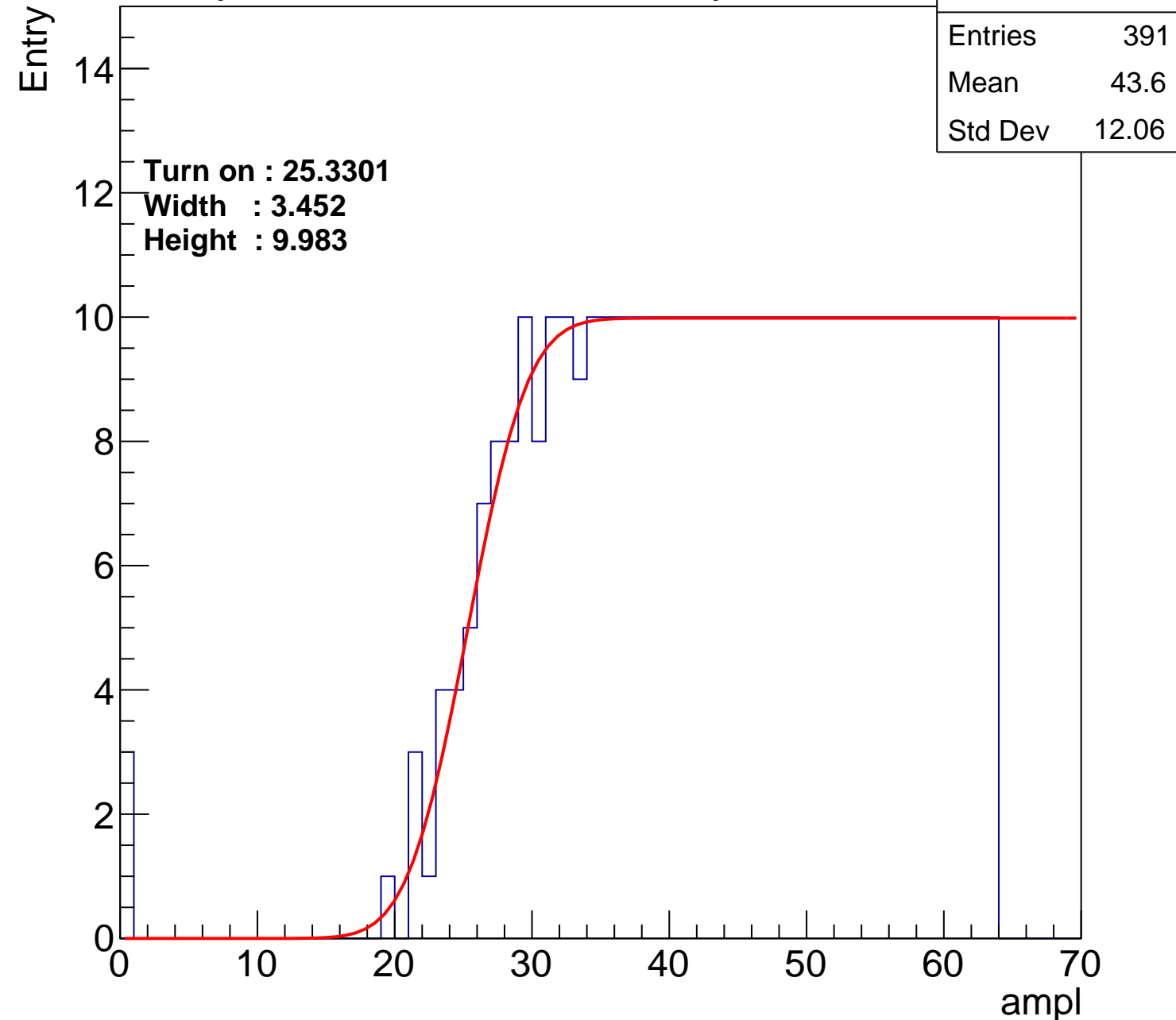
Width : 3.452

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch126

calib\_packv5\_042523\_0143.root, FC#12, port B1

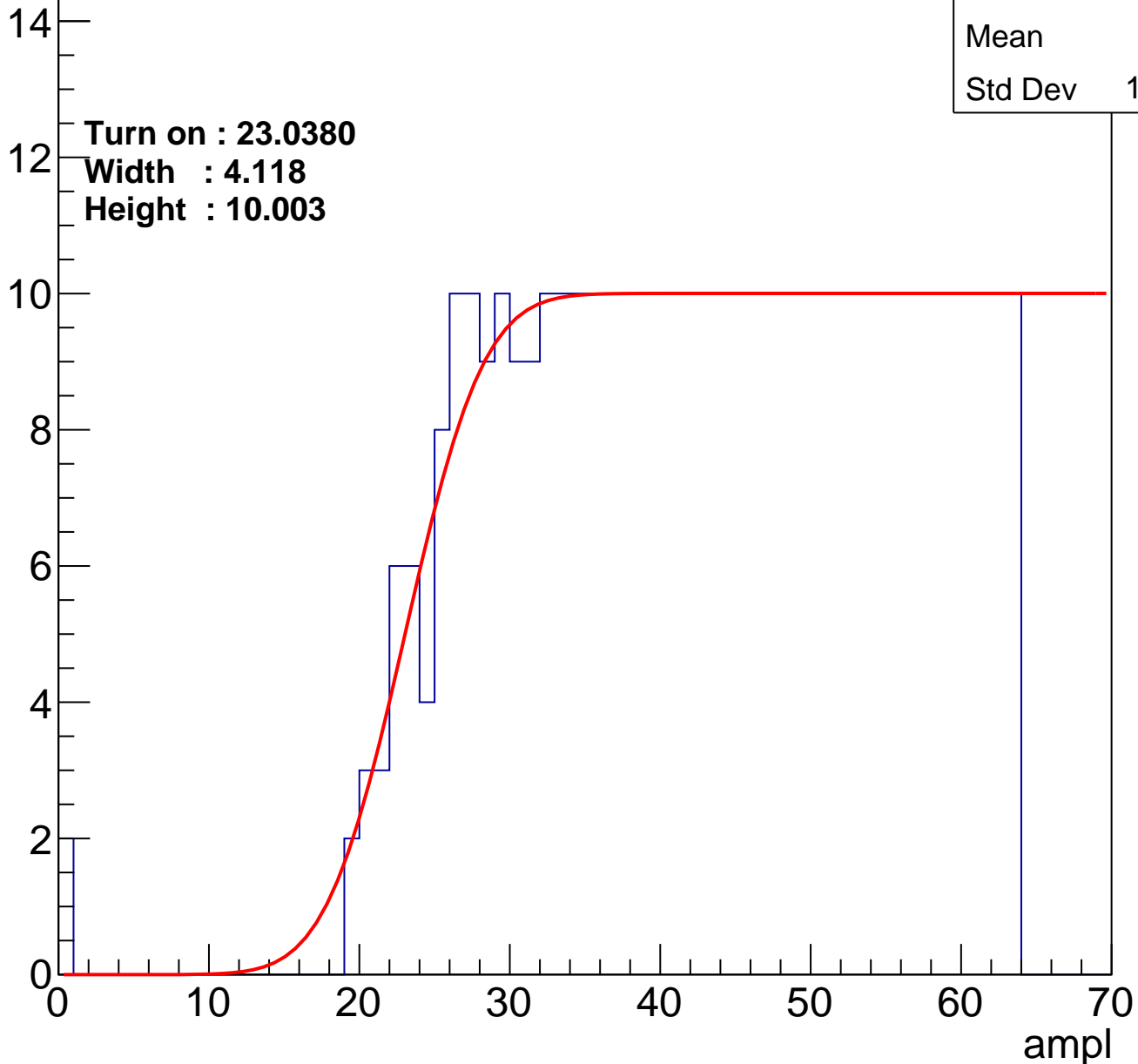
Entries	411
Mean	42.7
Std Dev	12.38

**Turn on : 23.0380**

**Width : 4.118**

**Height : 10.003**

Entry





# B0L102S, U18-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	401
Mean	43.11
Std Dev	12.39

Turn on : 24.8342

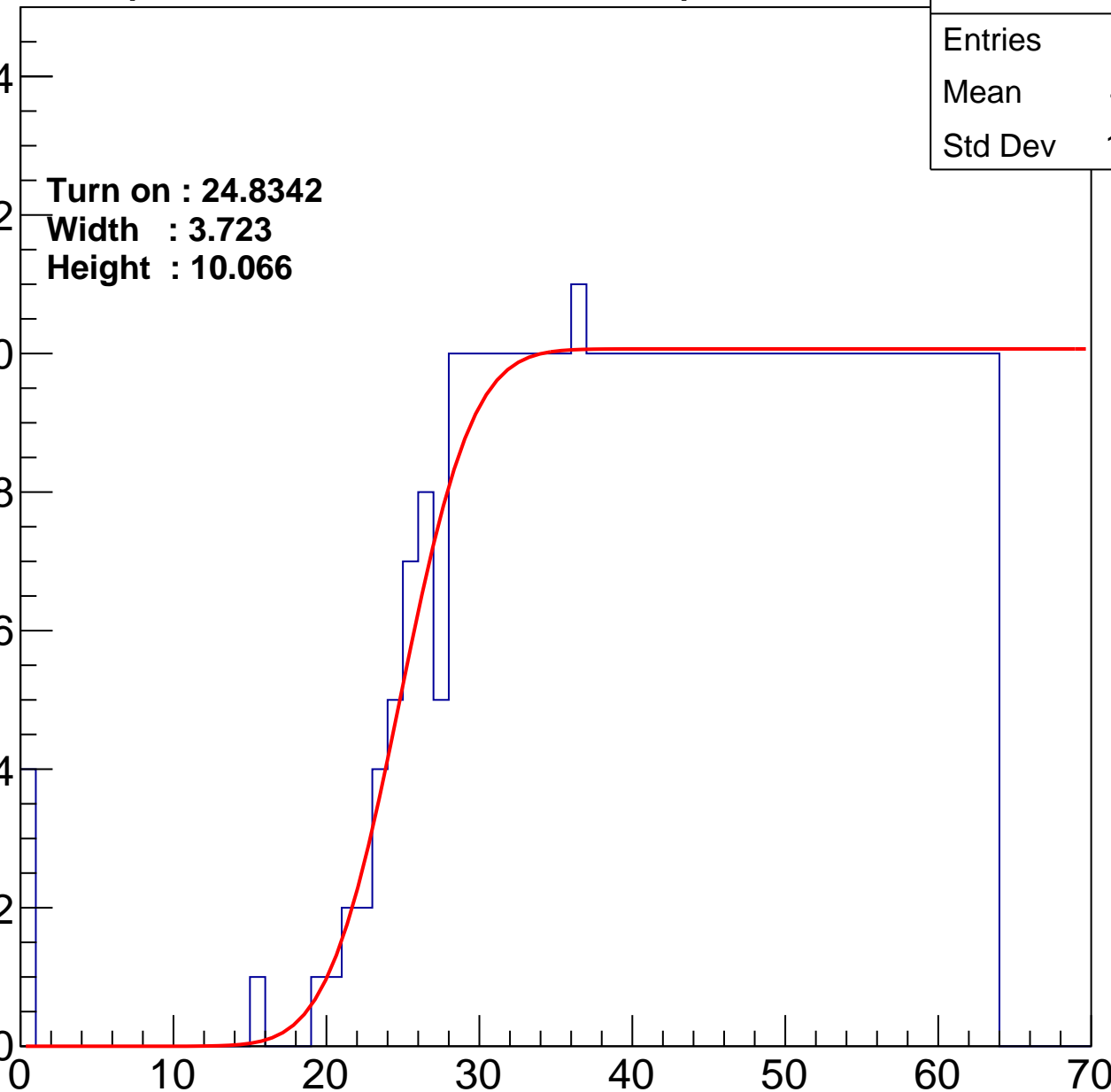
Width : 3.723

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U18-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	401
Mean	43.11
Std Dev	12.39

Turn on : 24.8342

Width : 3.723

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

