

B0L103S, U24-ch0

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.54
Std Dev	6.521

Turn on : 51.7362

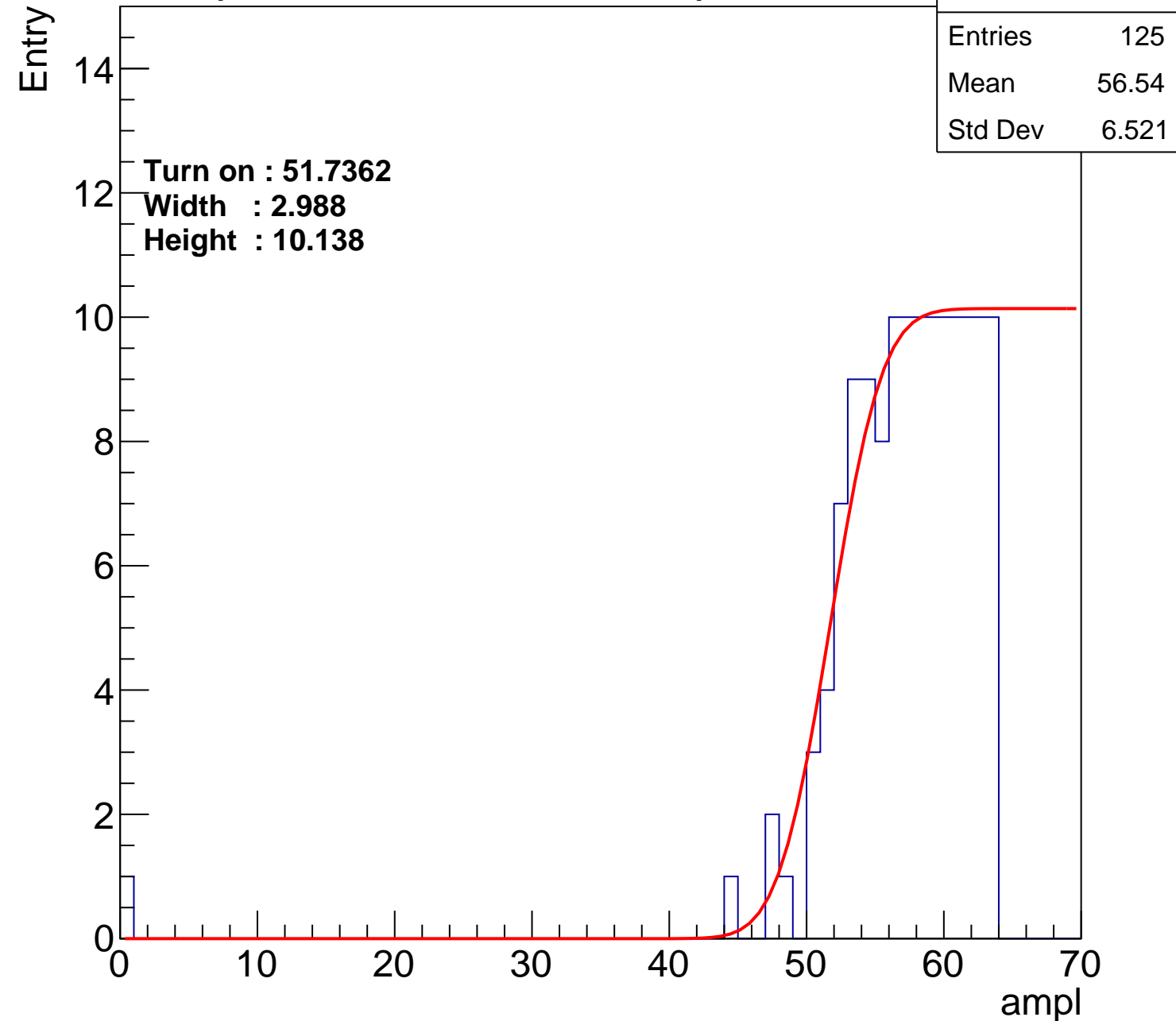
Width : 2.988

Height : 10.138

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch1

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.69
Std Dev	9.63

Turn on : 51.9792

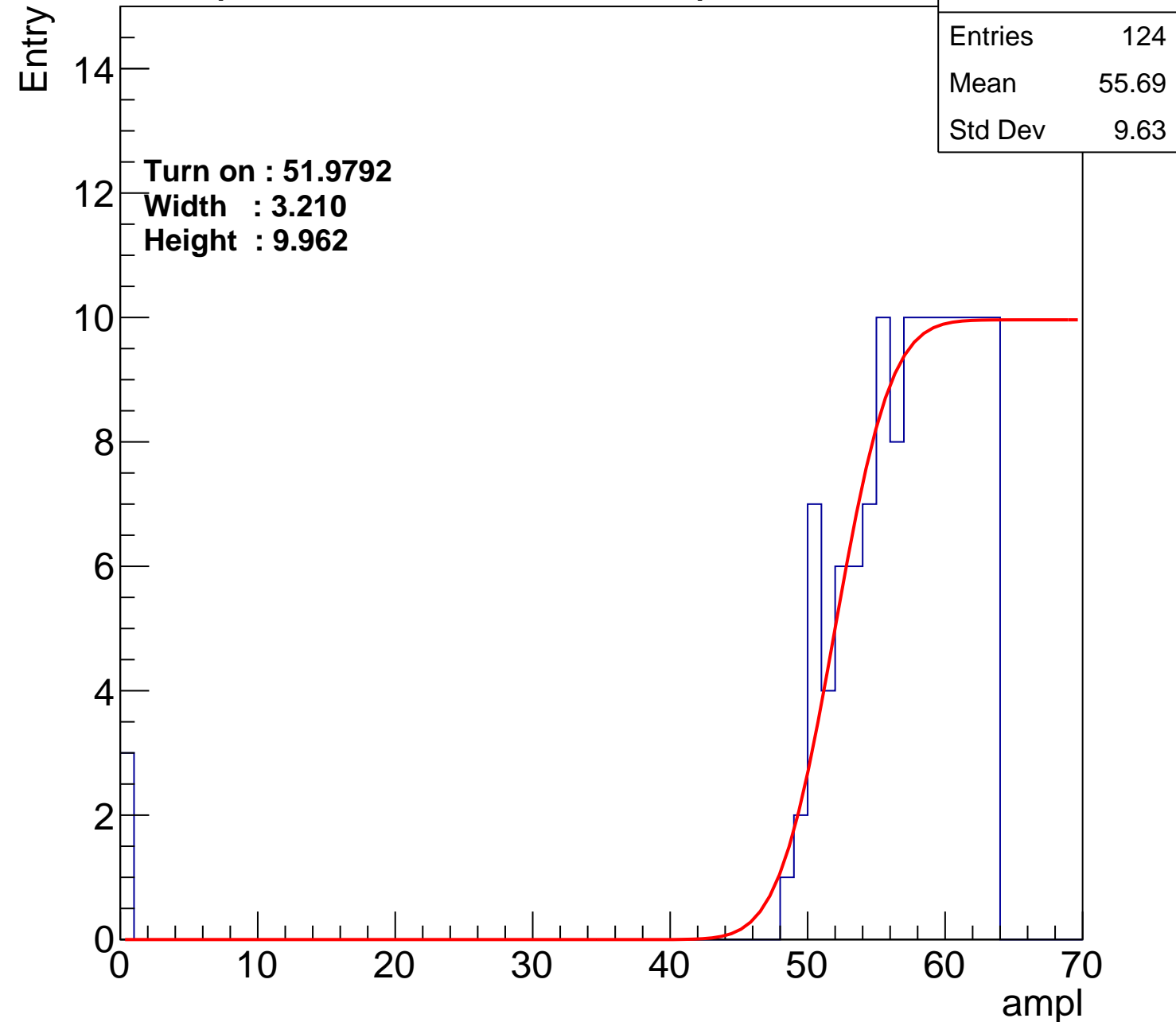
Width : 3.210

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch2

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	57
Std Dev	6.495

Turn on : 52.8736

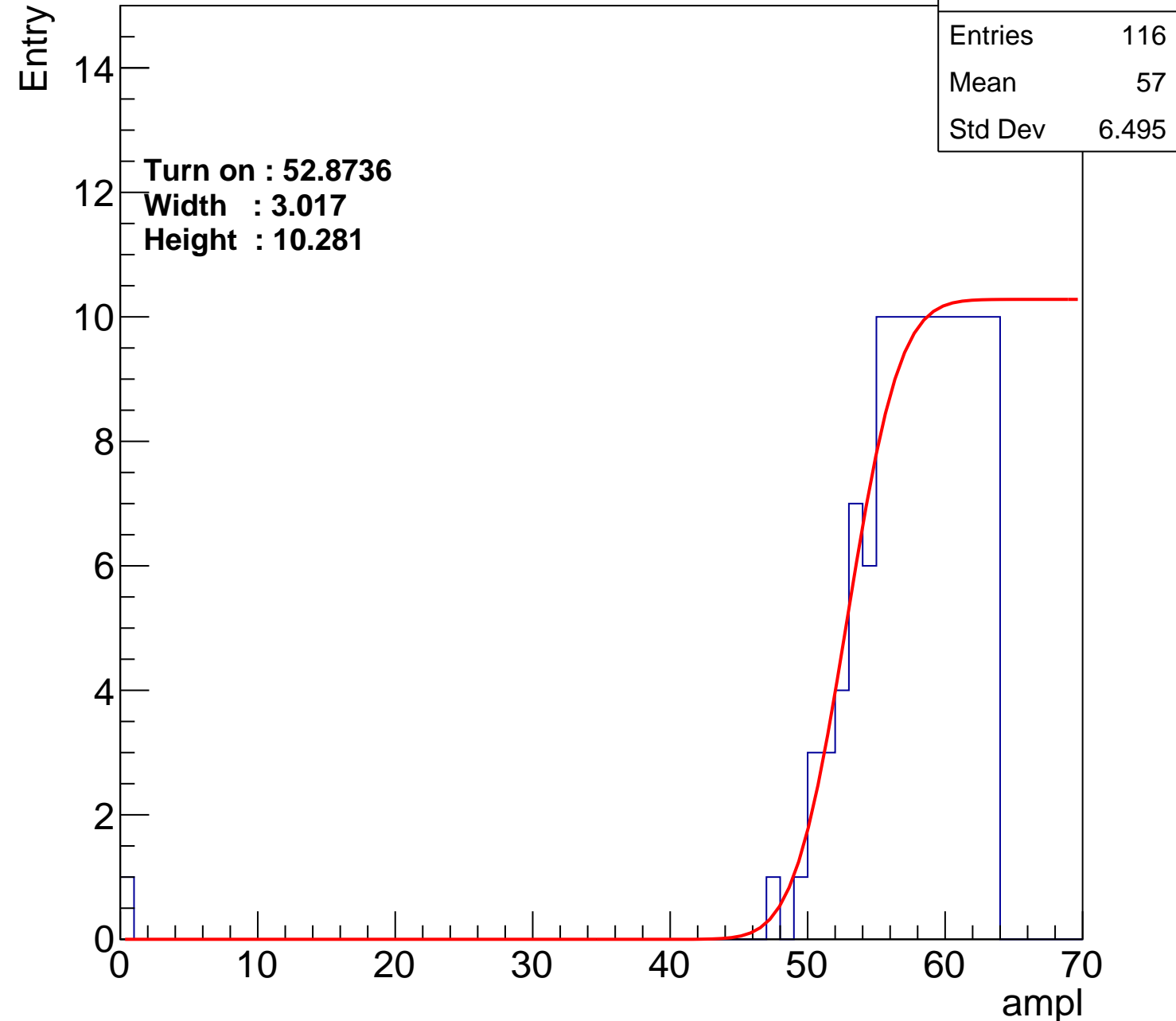
Width : 3.017

Height : 10.281

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch3

calib_packv5_040323_1717.root, FC#2, port C3

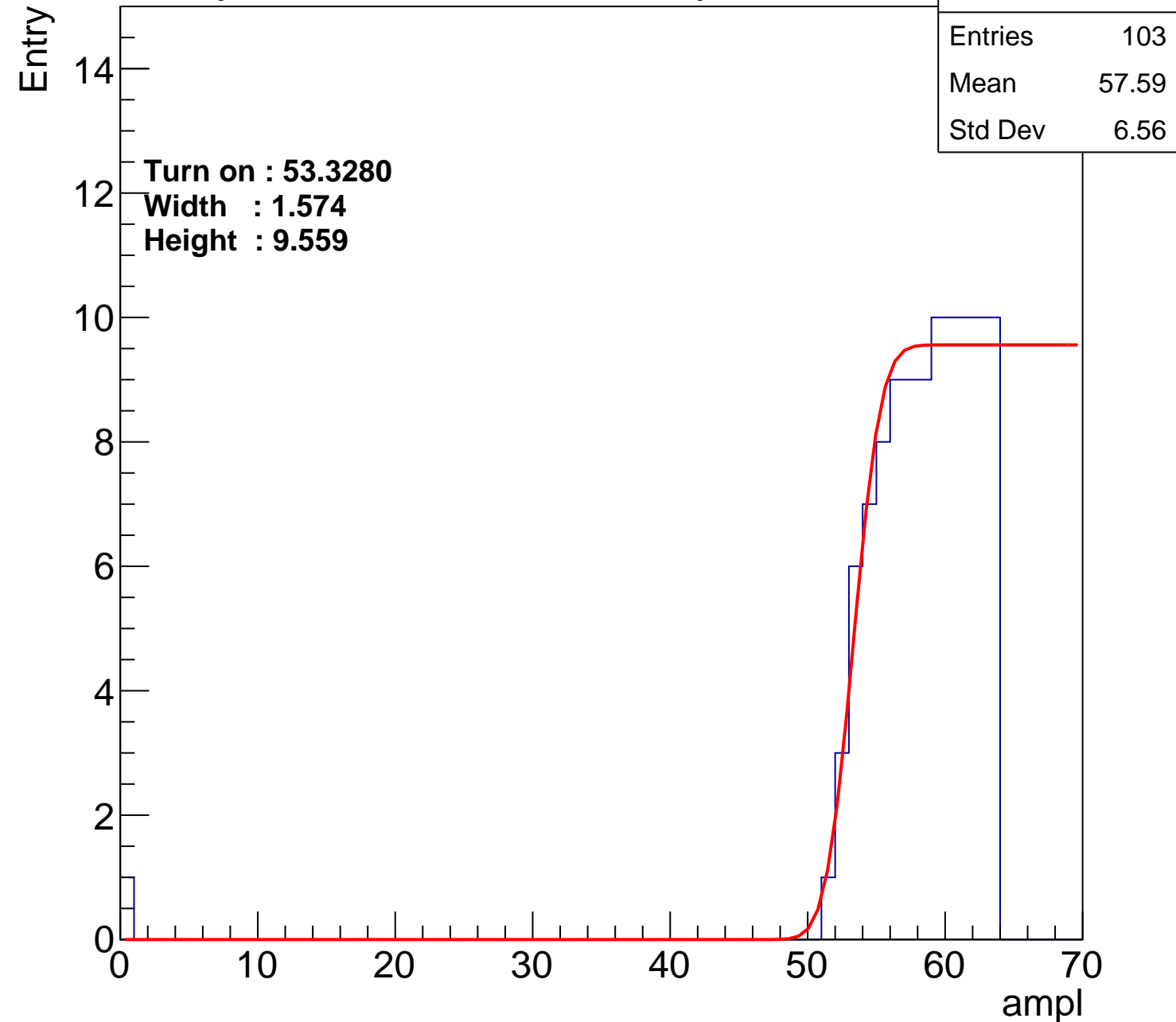
Entry

14
12
10
8
6
4
2
0

Turn on : 53.3280
Width : 1.574
Height : 9.559

Entries	103
Mean	57.59
Std Dev	6.56

ampl



B0L103S, U24-ch4

calib_packv5_040323_1717.root, FC#2, port C3

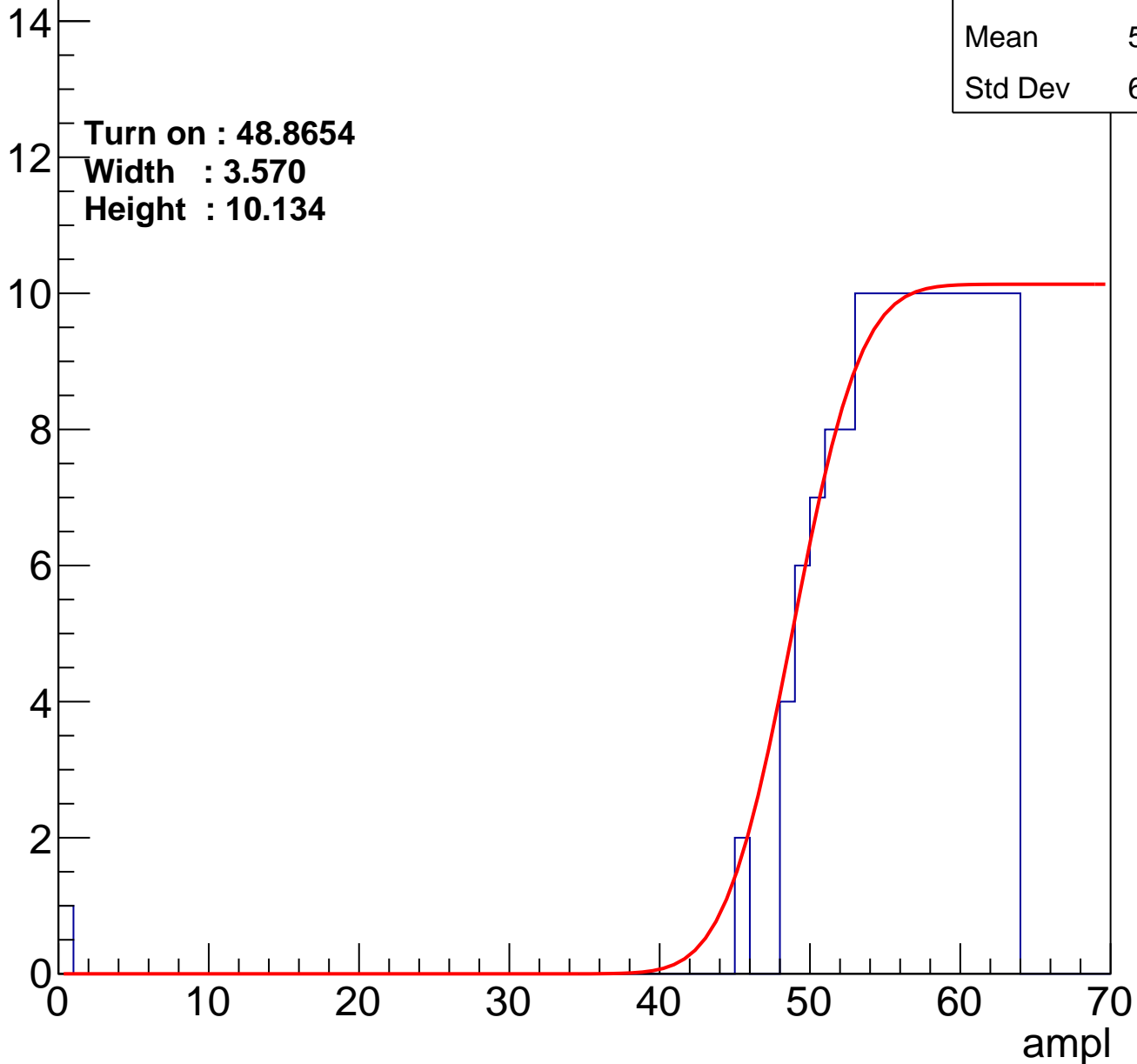
Entry

Entries	146
Mean	55.68
Std Dev	6.428

Turn on : 48.8654

Width : 3.570

Height : 10.134



B0L103S, U24-ch5

calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	54.82
Std Dev	13.43

Turn on : 53.8577

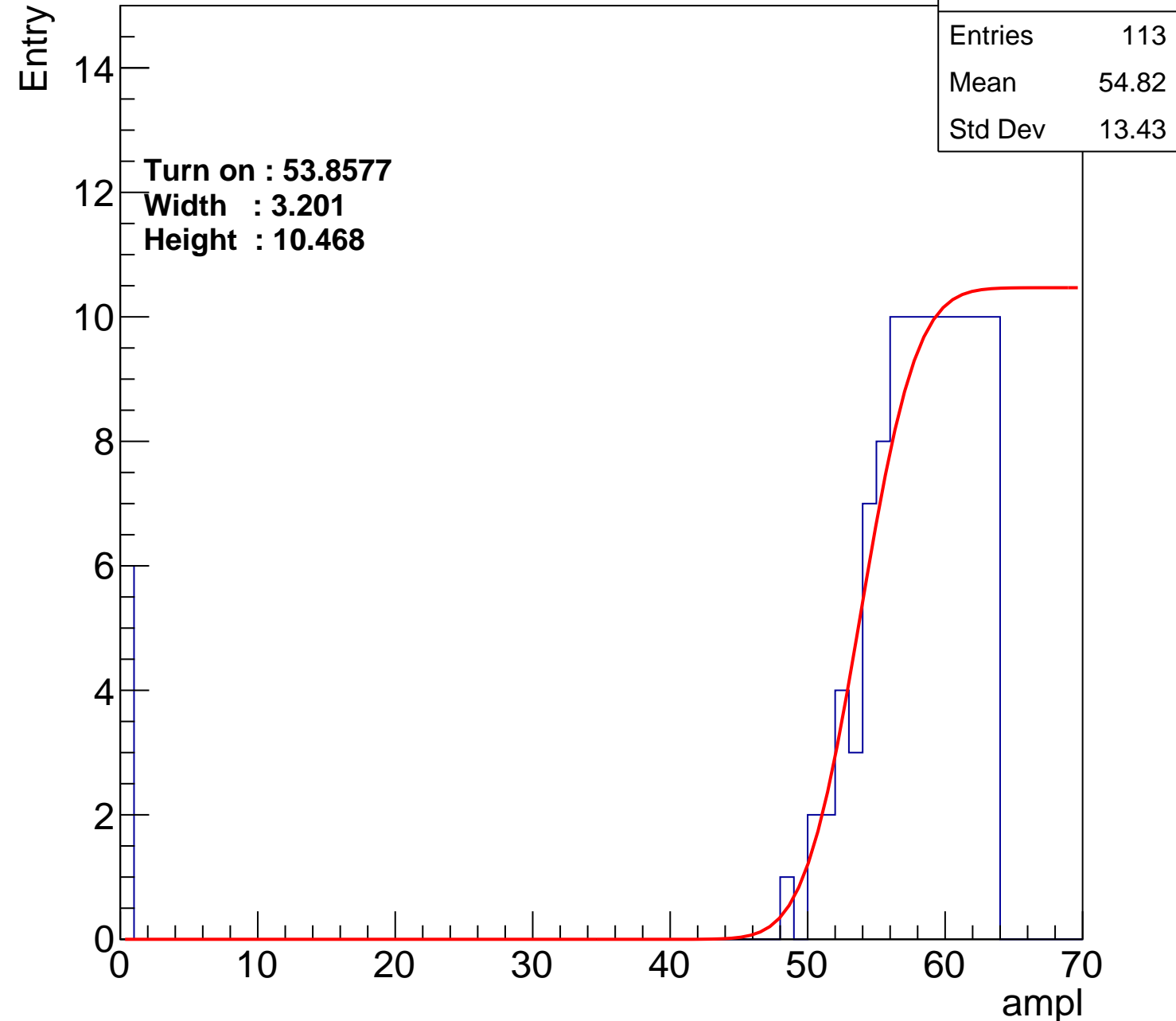
Width : 3.201

Height : 10.468

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch6

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.12
Std Dev	11.89

Turn on : 52.2763

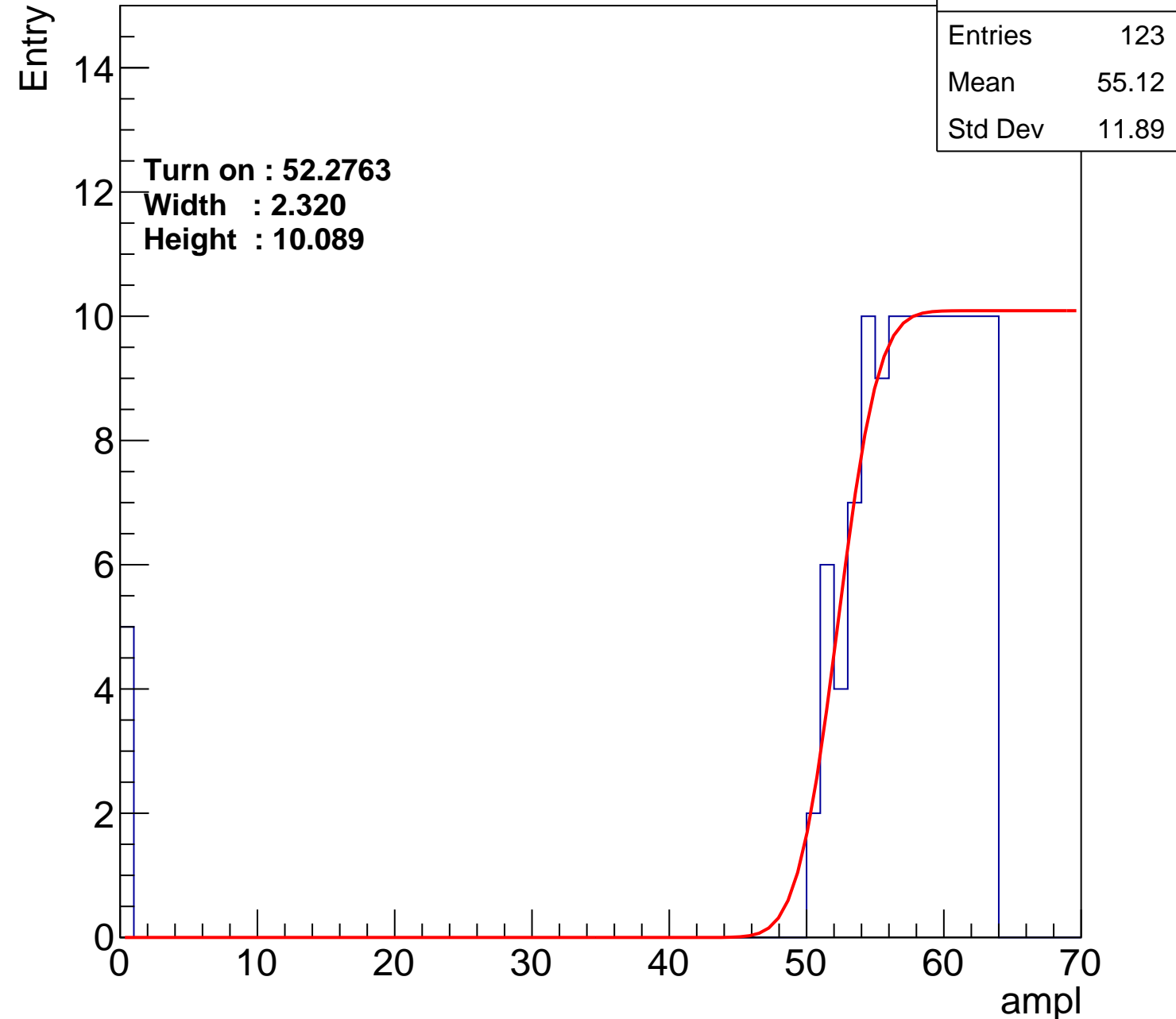
Width : 2.320

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch7

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	54.83
Std Dev	10.19

Turn on : 49.7760

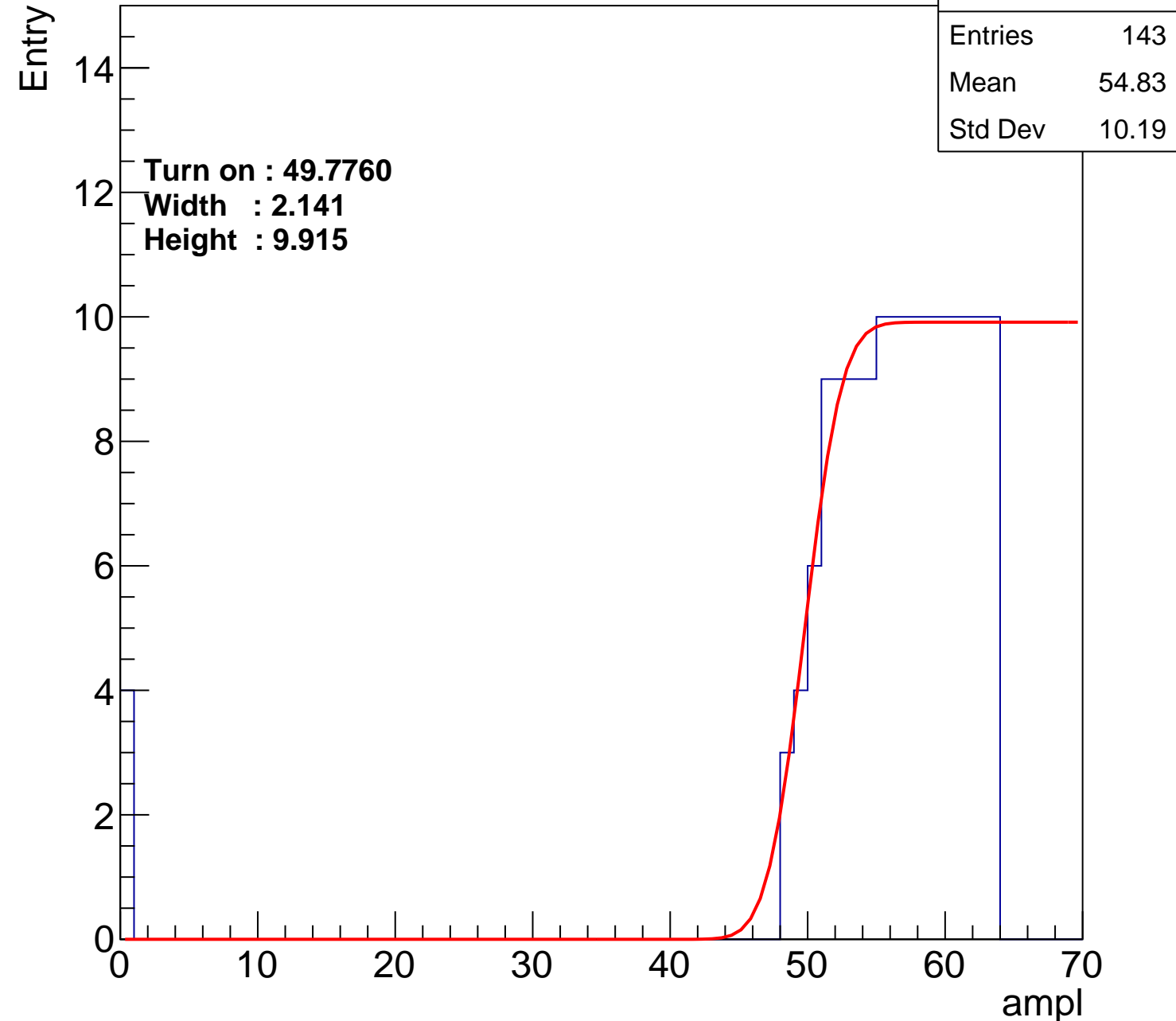
Width : 2.141

Height : 9.915

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch8

calib_packv5_040323_1717.root, FC#2, port C3

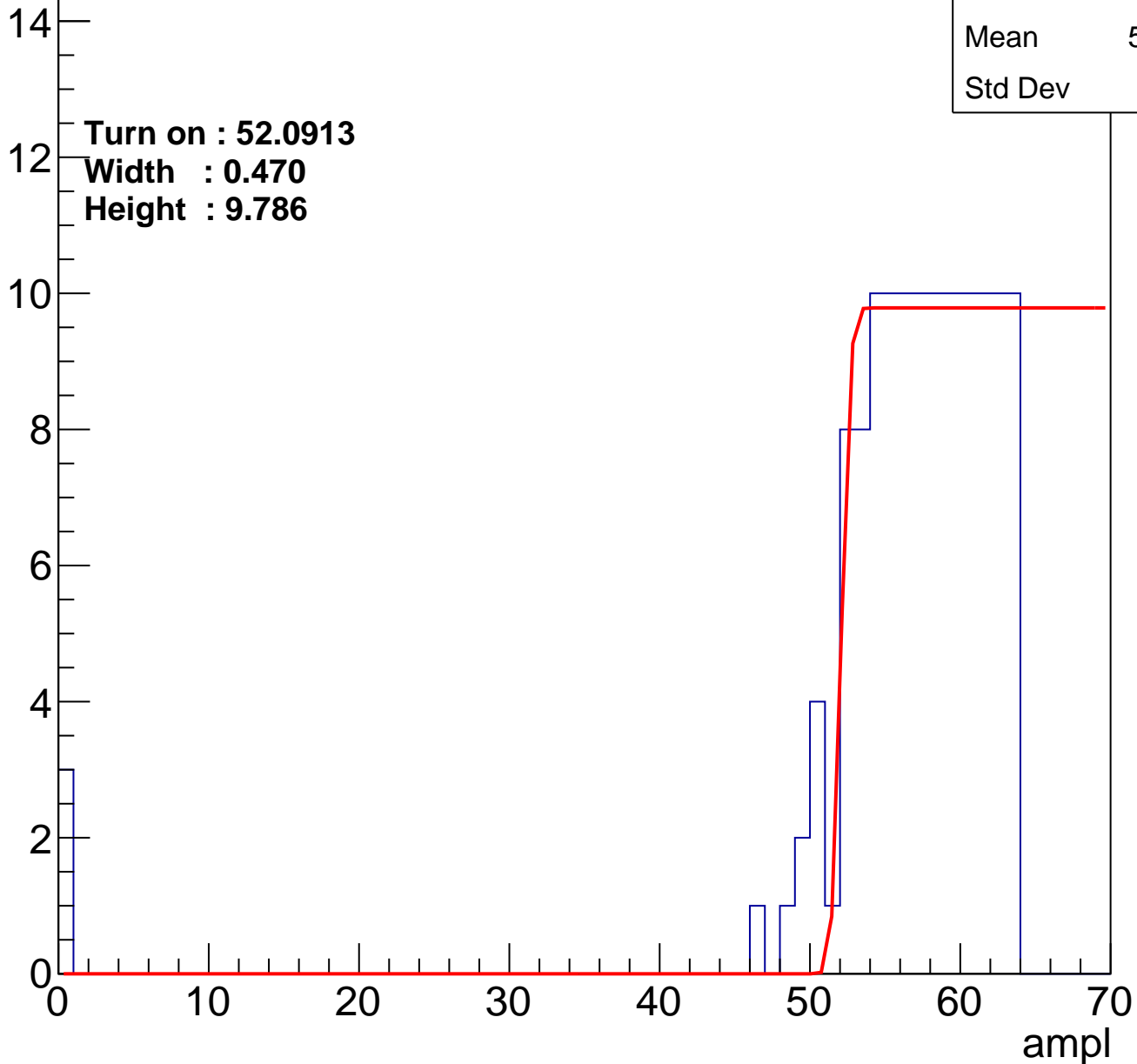
Entry

Entries	128
Mean	55.73
Std Dev	9.47

Turn on : 52.0913

Width : 0.470

Height : 9.786



B0L103S, U24-ch9

calib_packv5_040323_1717.root, FC#2, port C3

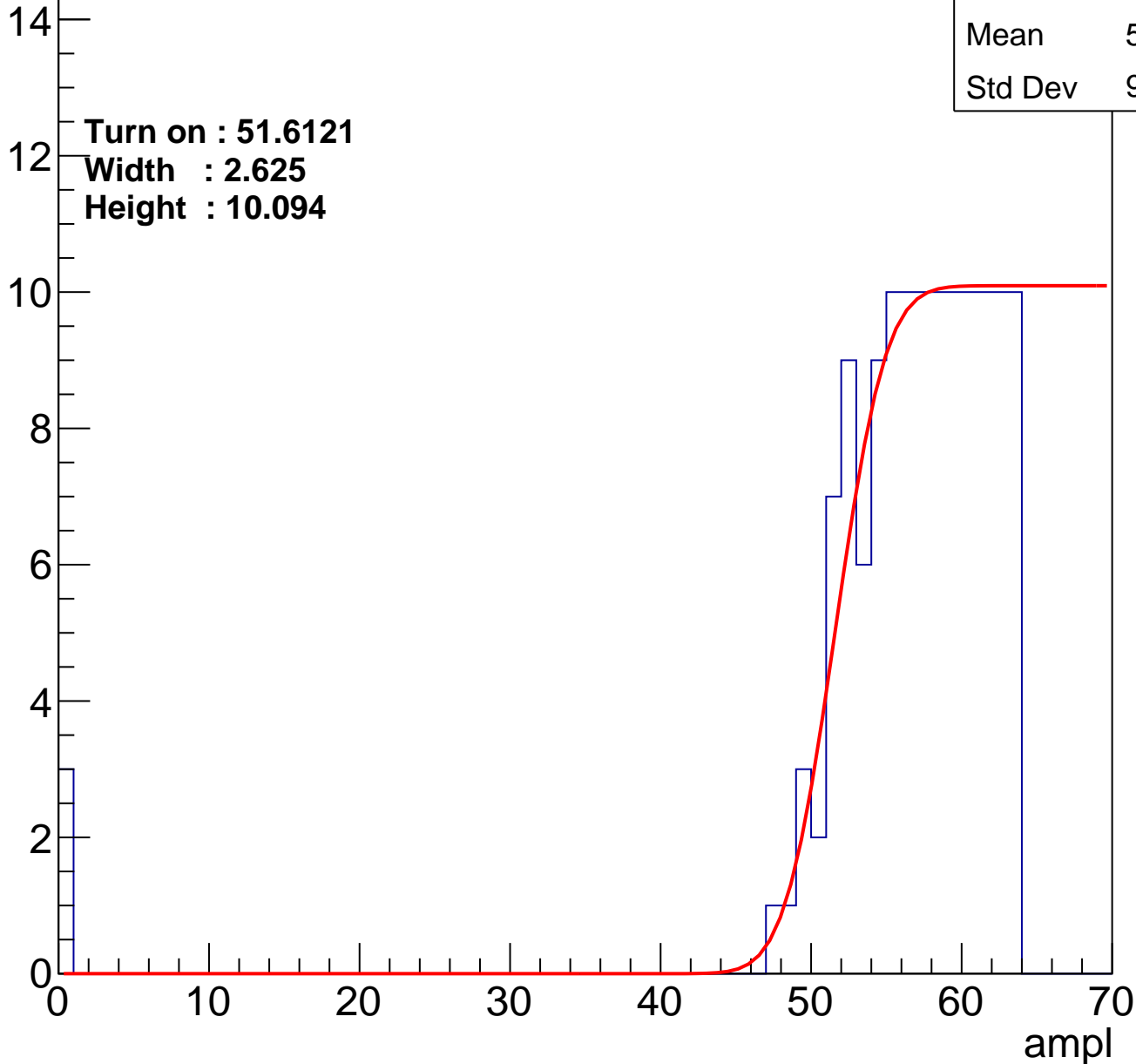
Entry

Entries	131
Mean	55.58
Std Dev	9.397

Turn on : 51.6121

Width : 2.625

Height : 10.094



B0L103S, U24-ch10

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	56.17
Std Dev	8.245

Turn on : 52.2676

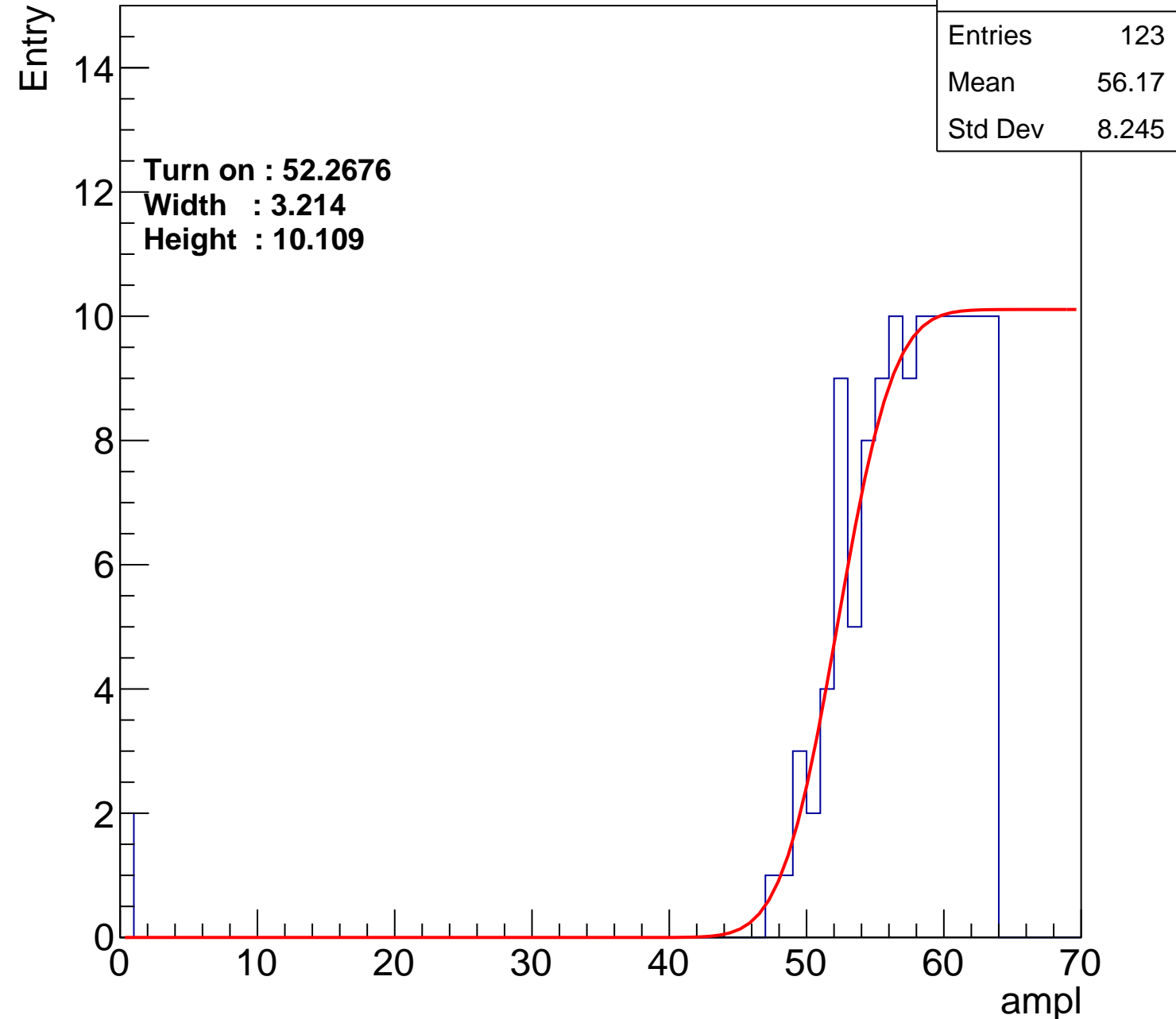
Width : 3.214

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch11

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	55.12
Std Dev	7.935

Turn on : 49.7714

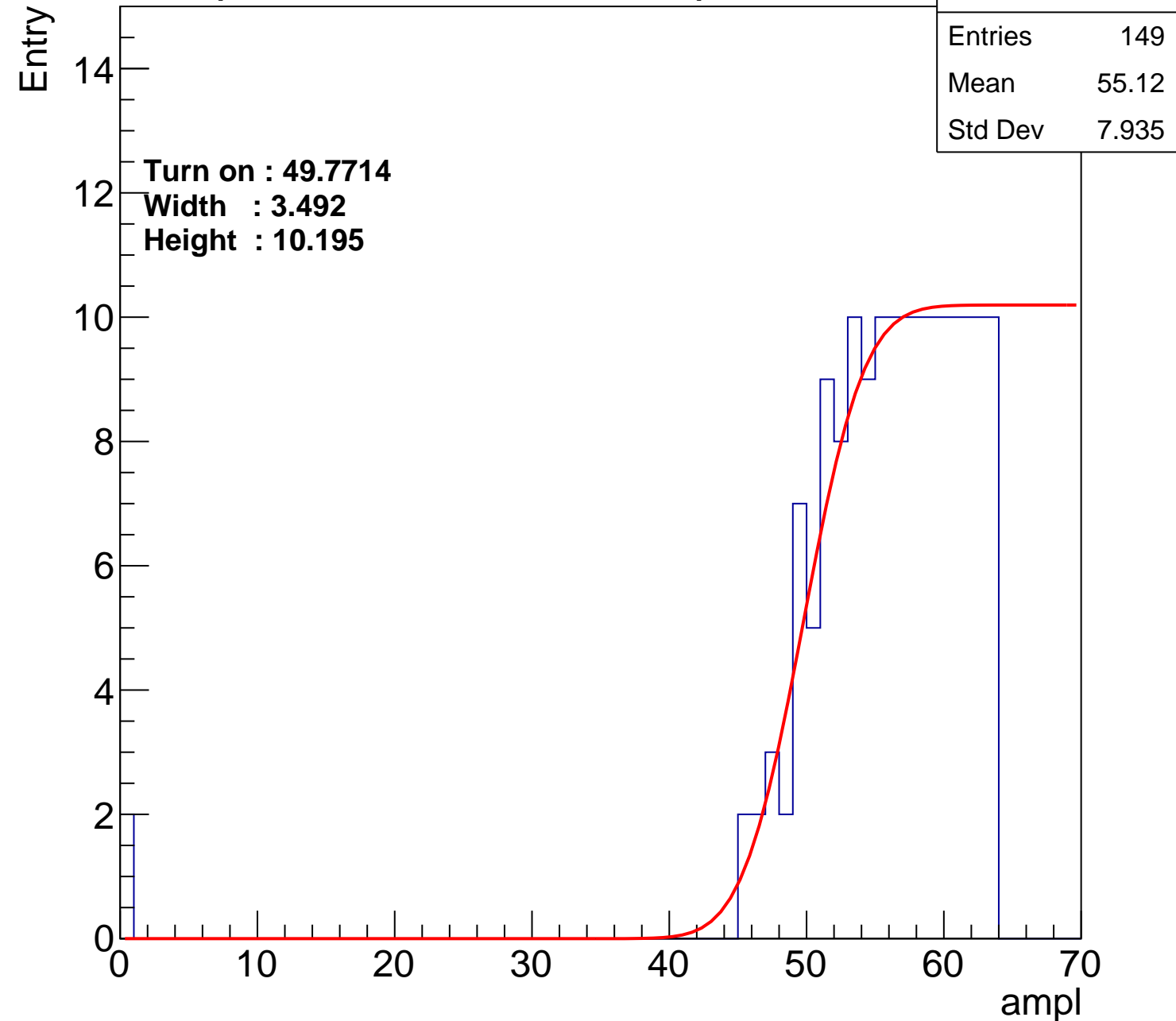
Width : 3.492

Height : 10.195

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch12

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	55.55
Std Dev	9.367

Turn on : 51.1629

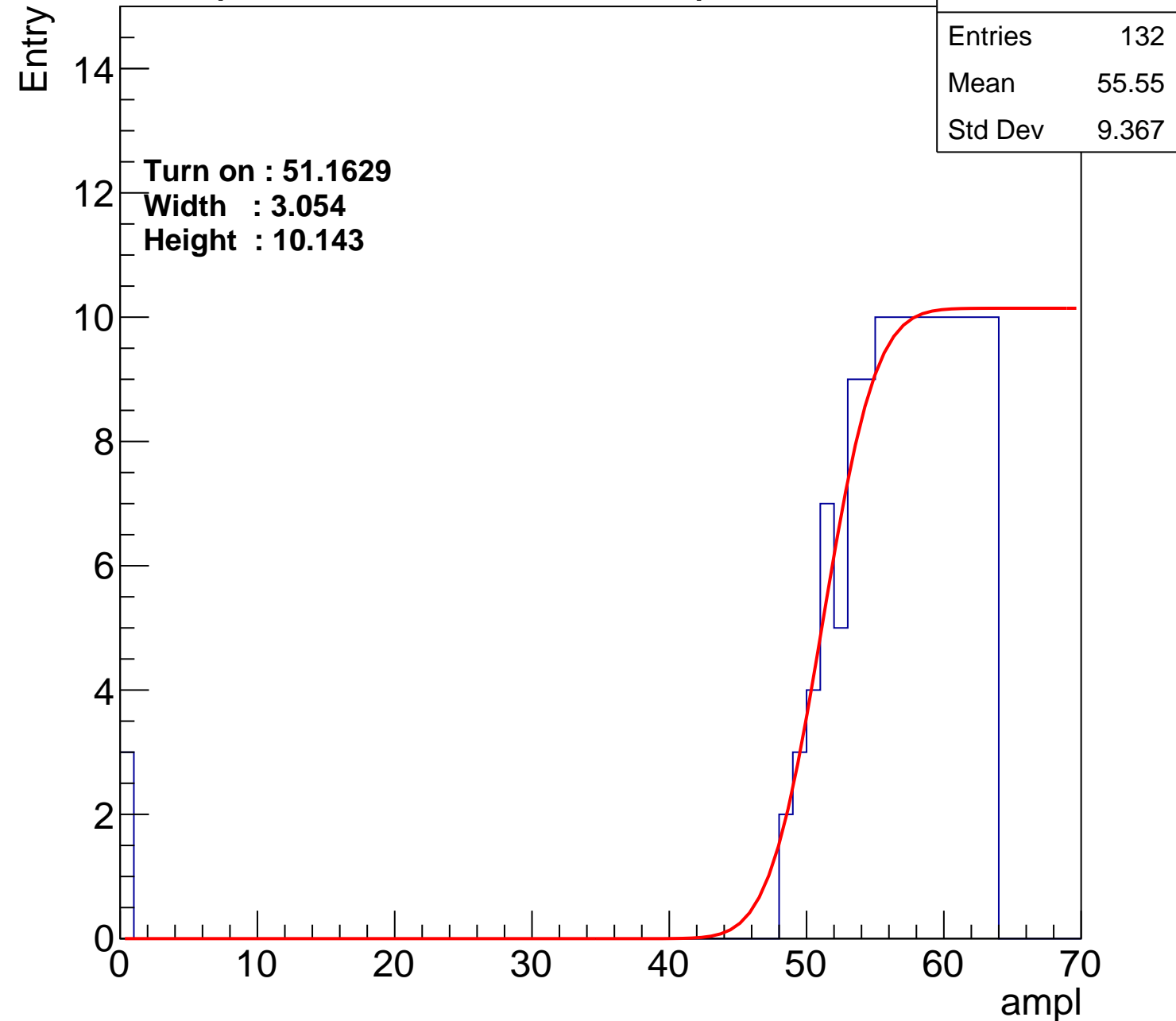
Width : 3.054

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch13

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	56.08
Std Dev	9.798

Turn on : 52.9367

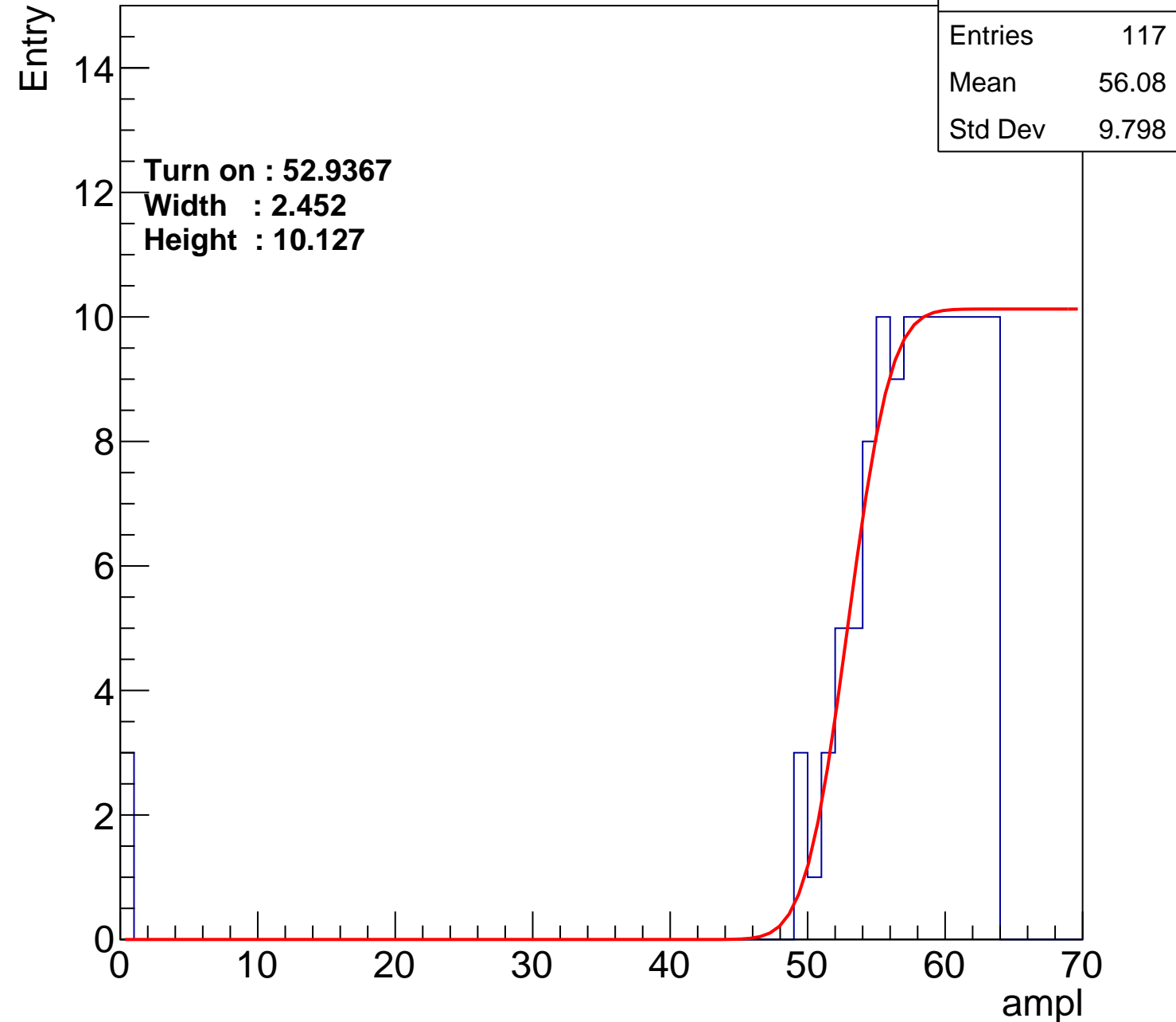
Width : 2.452

Height : 10.127

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch14

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	55.5
Std Dev	10.99

Turn on : 52.3888

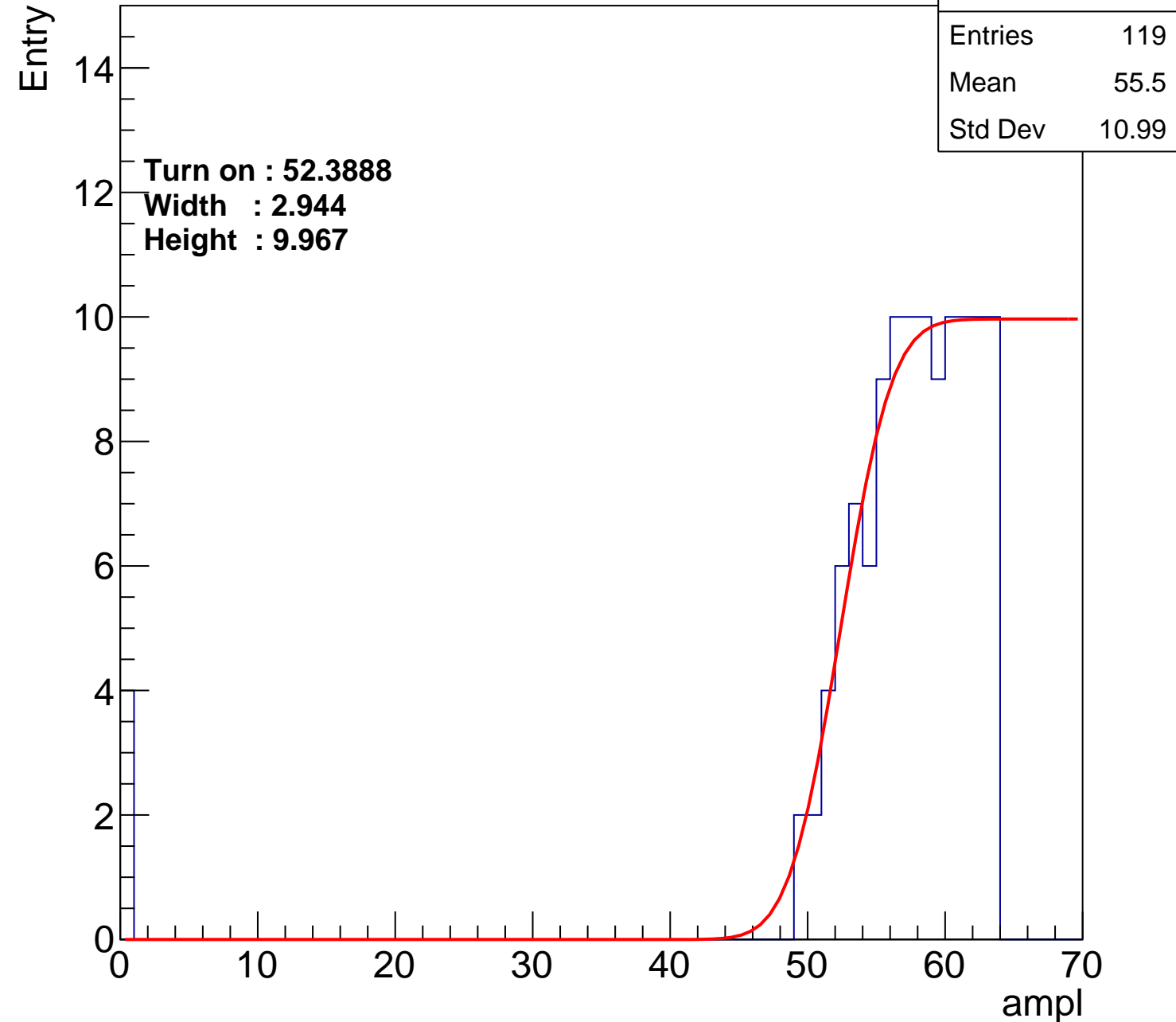
Width : 2.944

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch15

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	55.71
Std Dev	11.31

Turn on : 54.2017

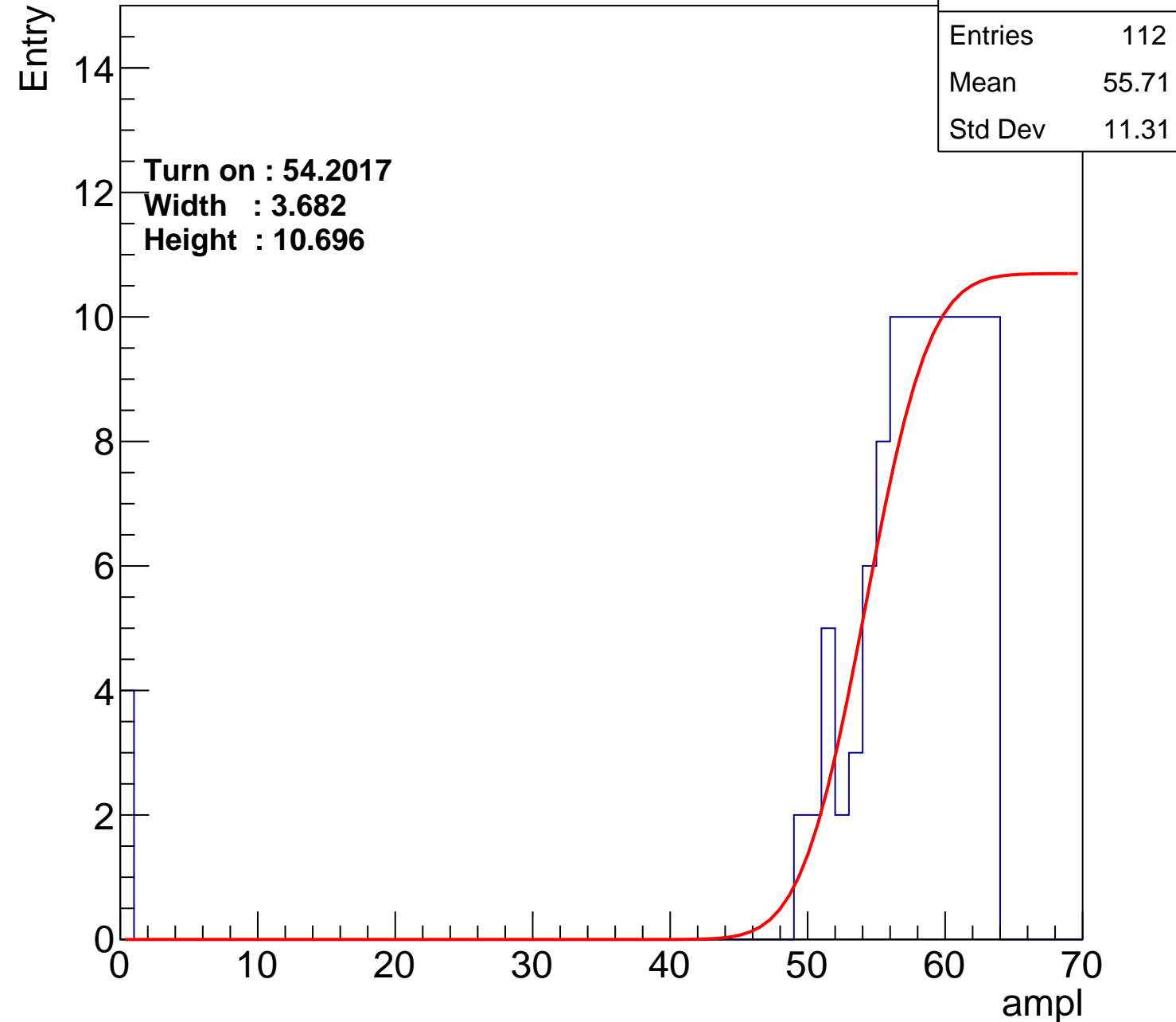
Width : 3.682

Height : 10.696

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch16

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.98
Std Dev	8.046

Turn on : 51.2487

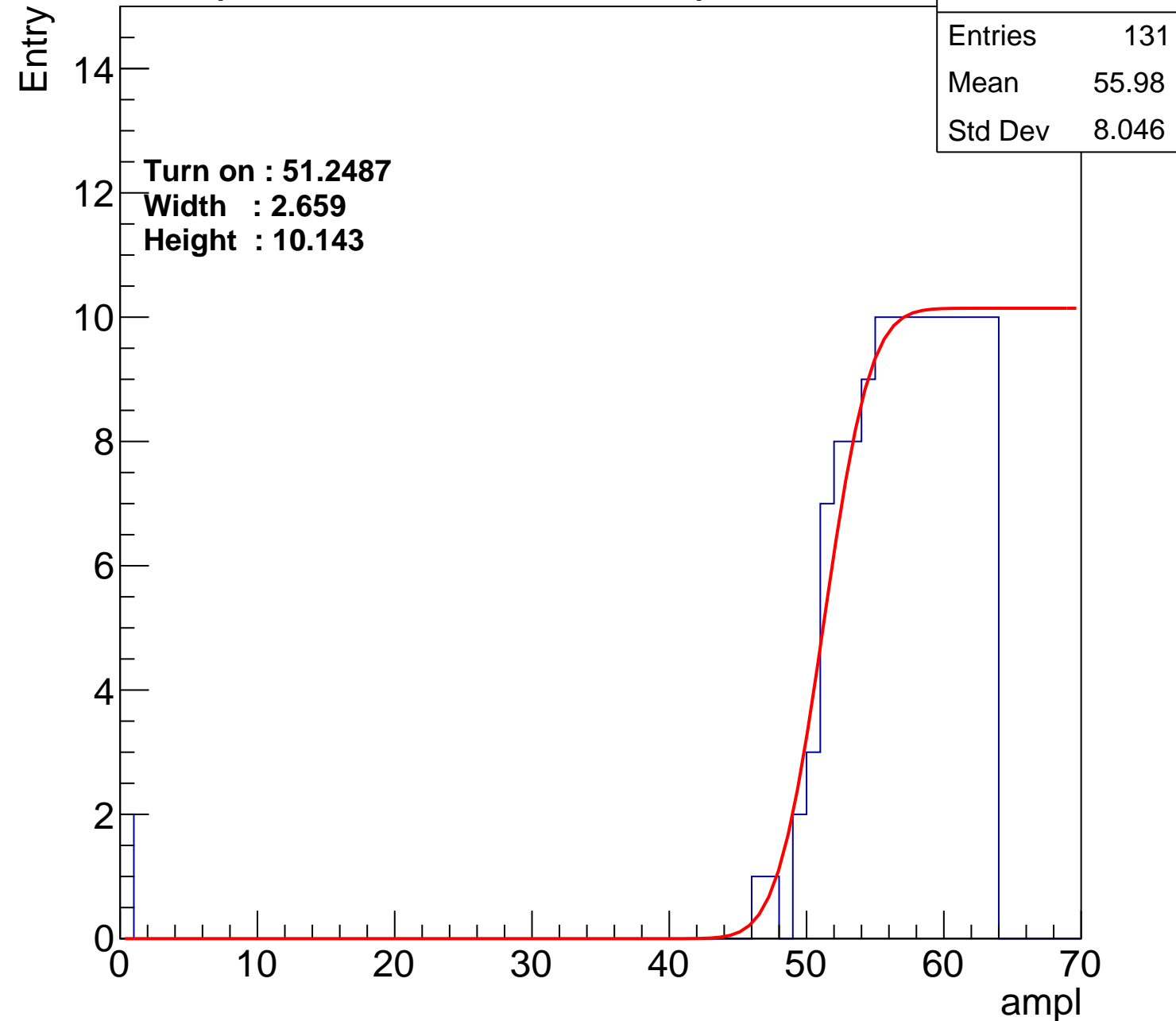
Width : 2.659

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch17

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	55.6
Std Dev	11.26

Turn on : 53.6617

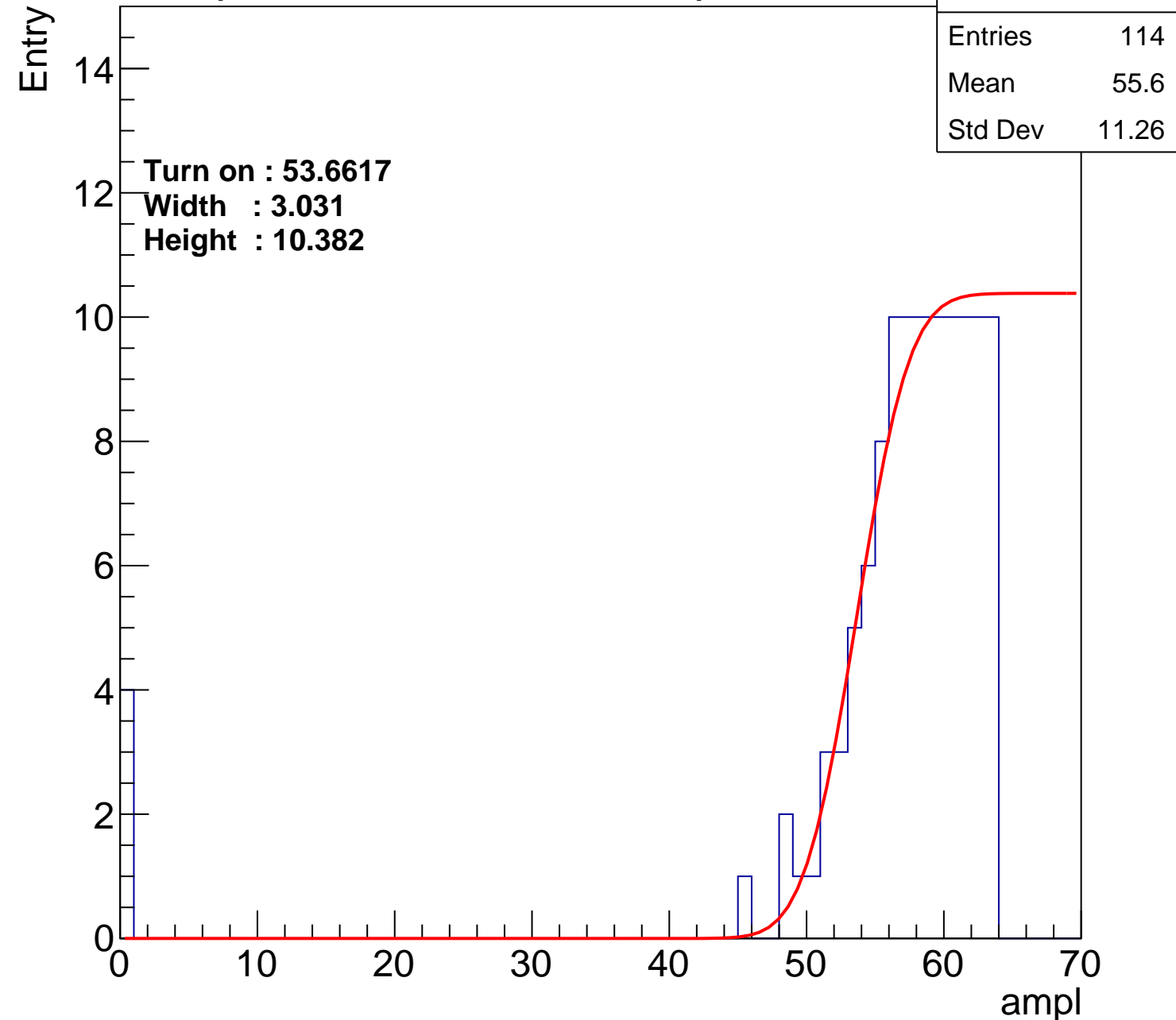
Width : 3.031

Height : 10.382

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch18

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.35
Std Dev	8.055

Turn on : 50.0168

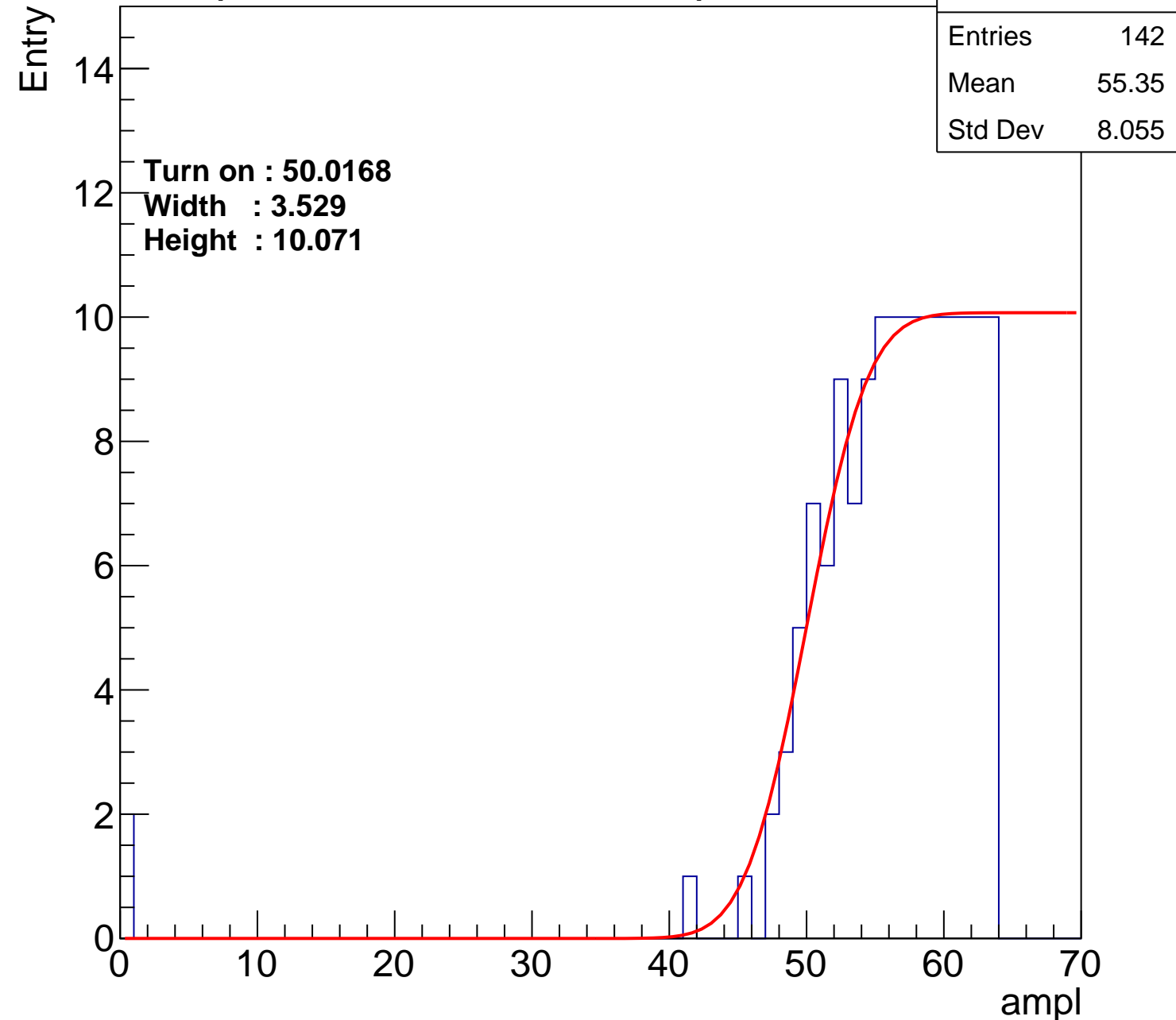
Width : 3.529

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch19

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	54.11
Std Dev	13.69

Turn on : 52.1644

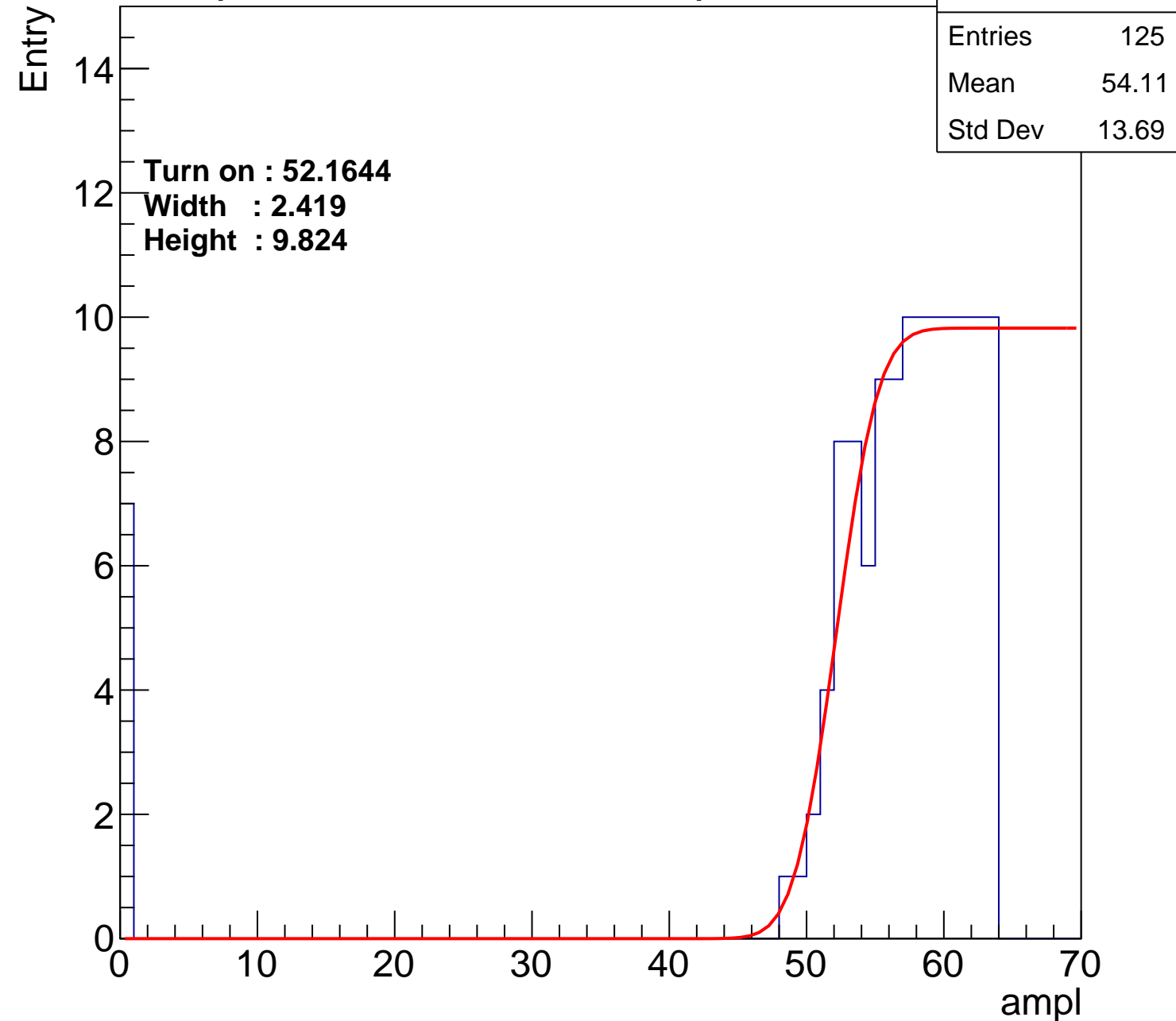
Width : 2.419

Height : 9.824

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch20

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	55.56
Std Dev	9.519

Turn on : 52.0514

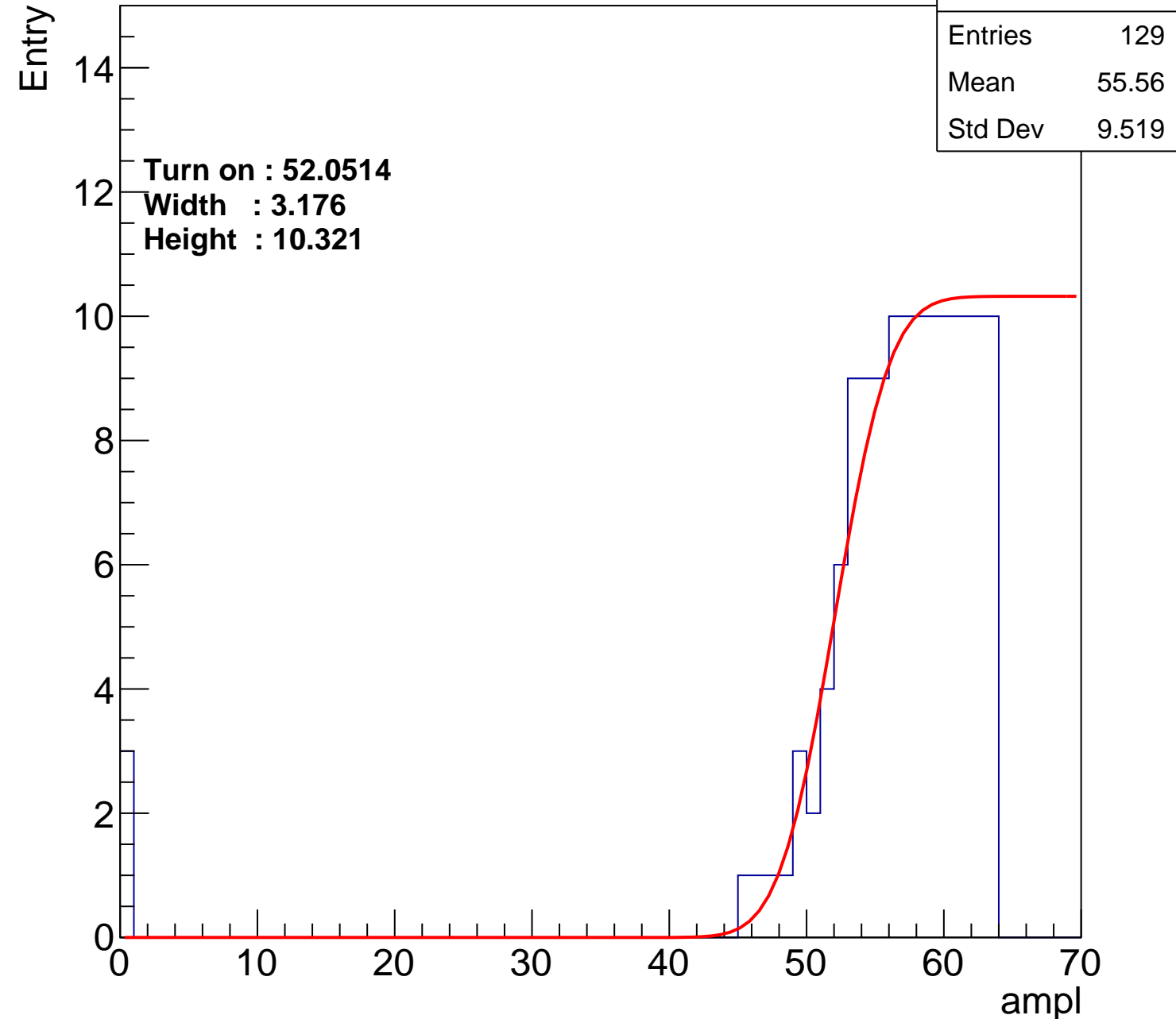
Width : 3.176

Height : 10.321

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch21

calib_packv5_040323_1717.root, FC#2, port C3

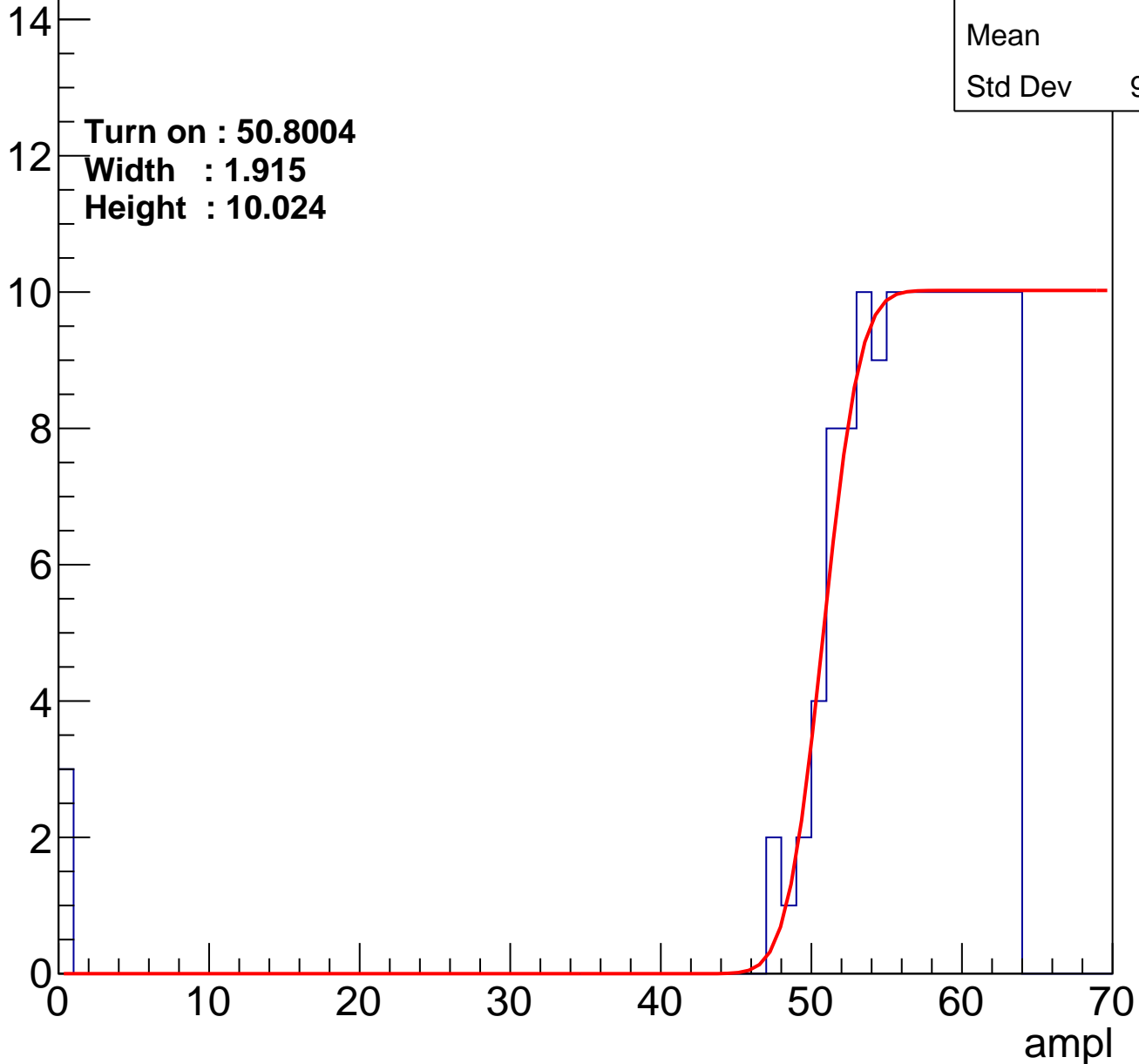
Entries	137
Mean	55.4
Std Dev	9.238

Turn on : 50.8004

Width : 1.915

Height : 10.024

Entry



B0L103S, U24-ch22

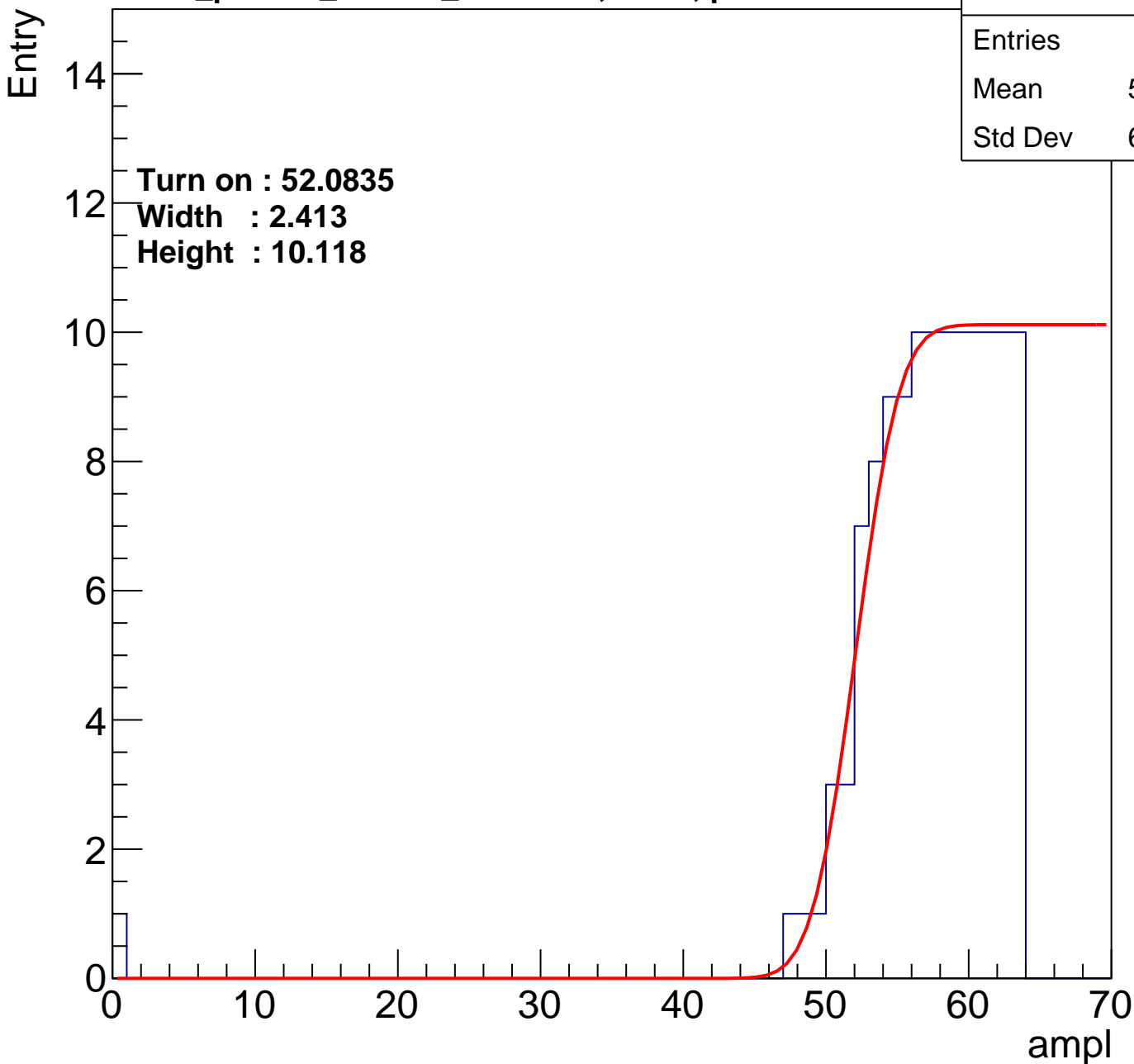
calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	56.72
Std Dev	6.426

Turn on : 52.0835

Width : 2.413

Height : 10.118



B0L103S, U24-ch23

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	56.71
Std Dev	8.339

Turn on : 52.8841

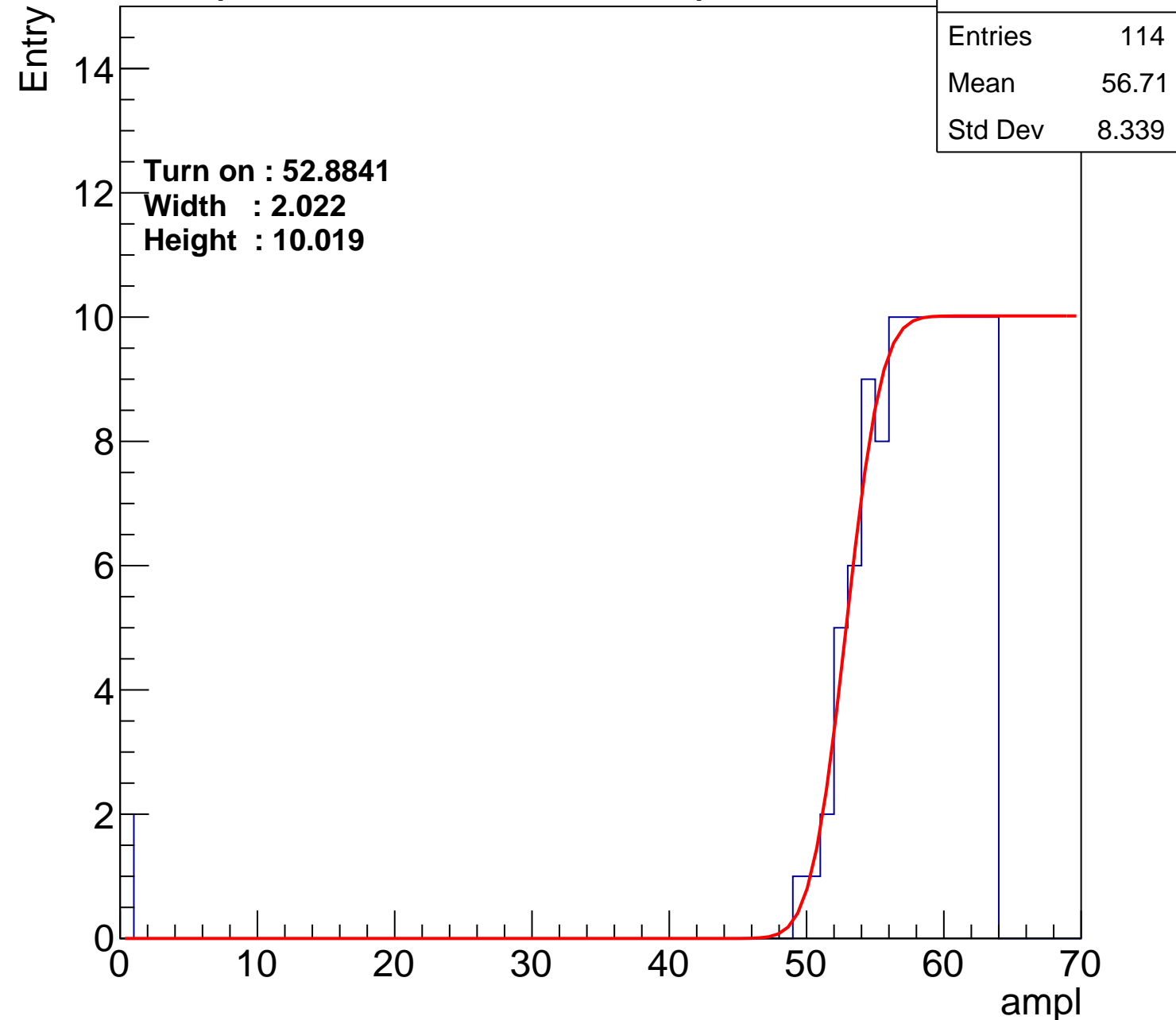
Width : 2.022

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch24

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.03
Std Dev	9.768

Turn on : 52.5748

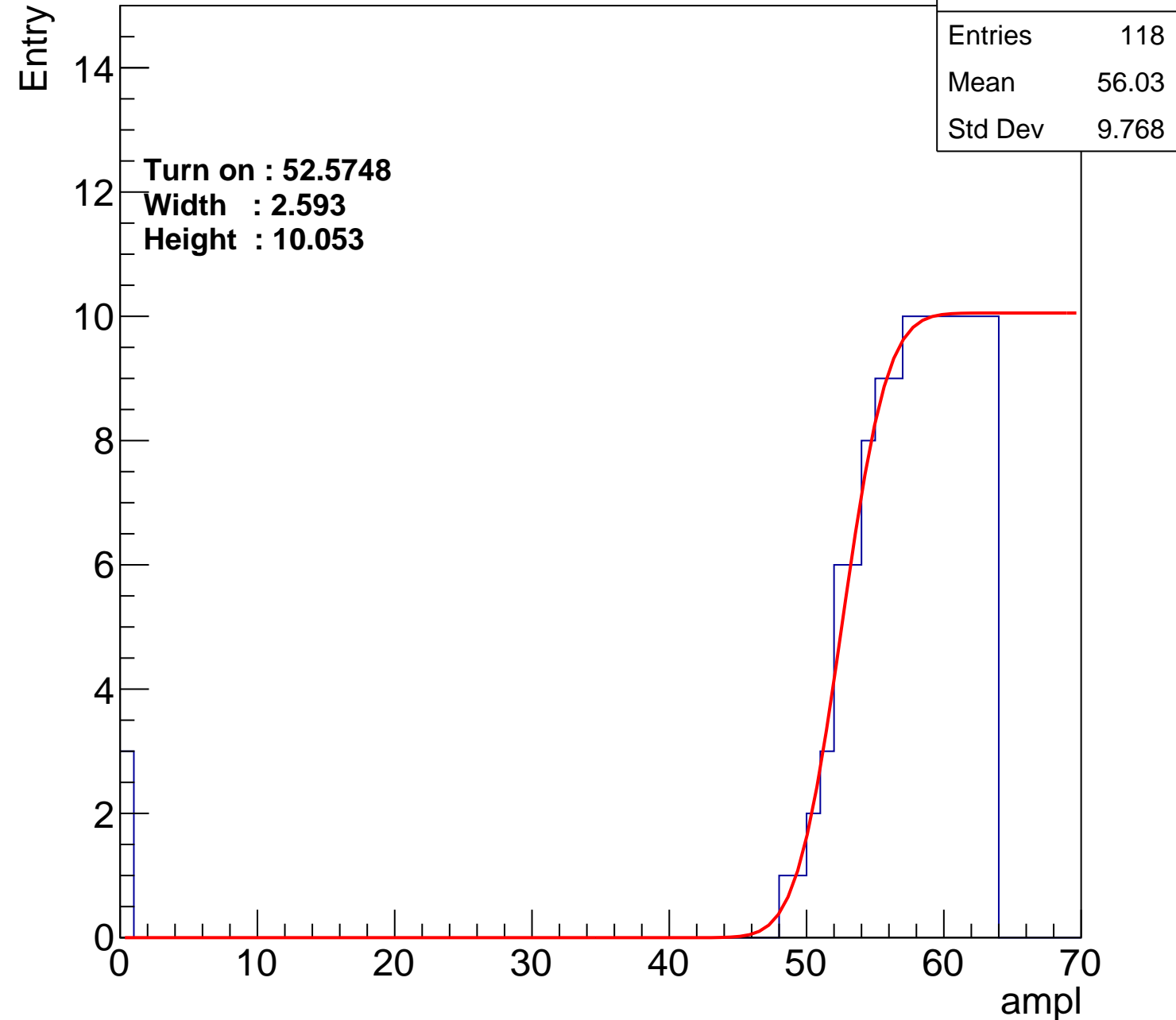
Width : 2.593

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch25

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.51
Std Dev	9.573

Turn on : 51.4956

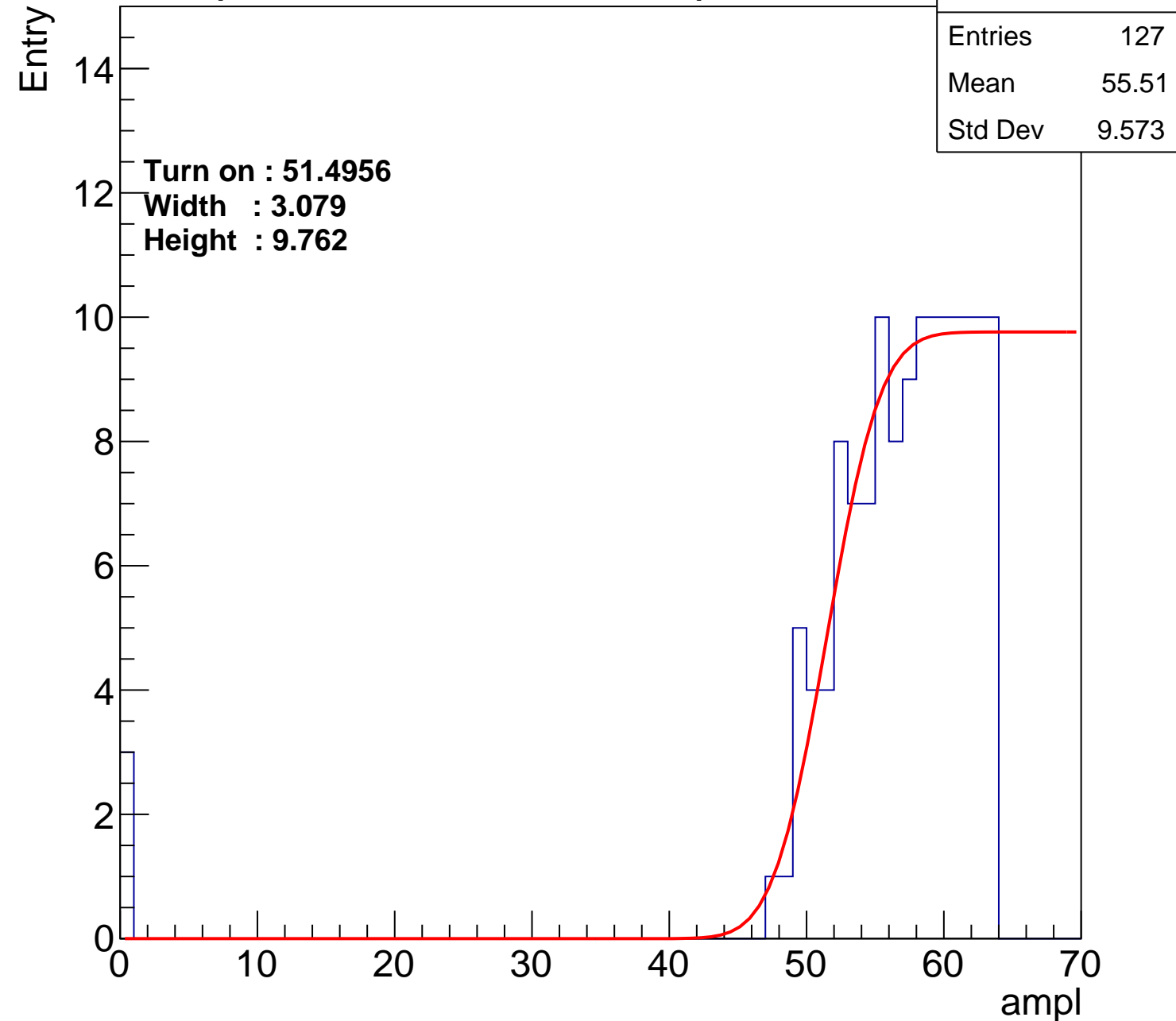
Width : 3.079

Height : 9.762

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch26

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.18
Std Dev	10.56

Turn on : 50.6998

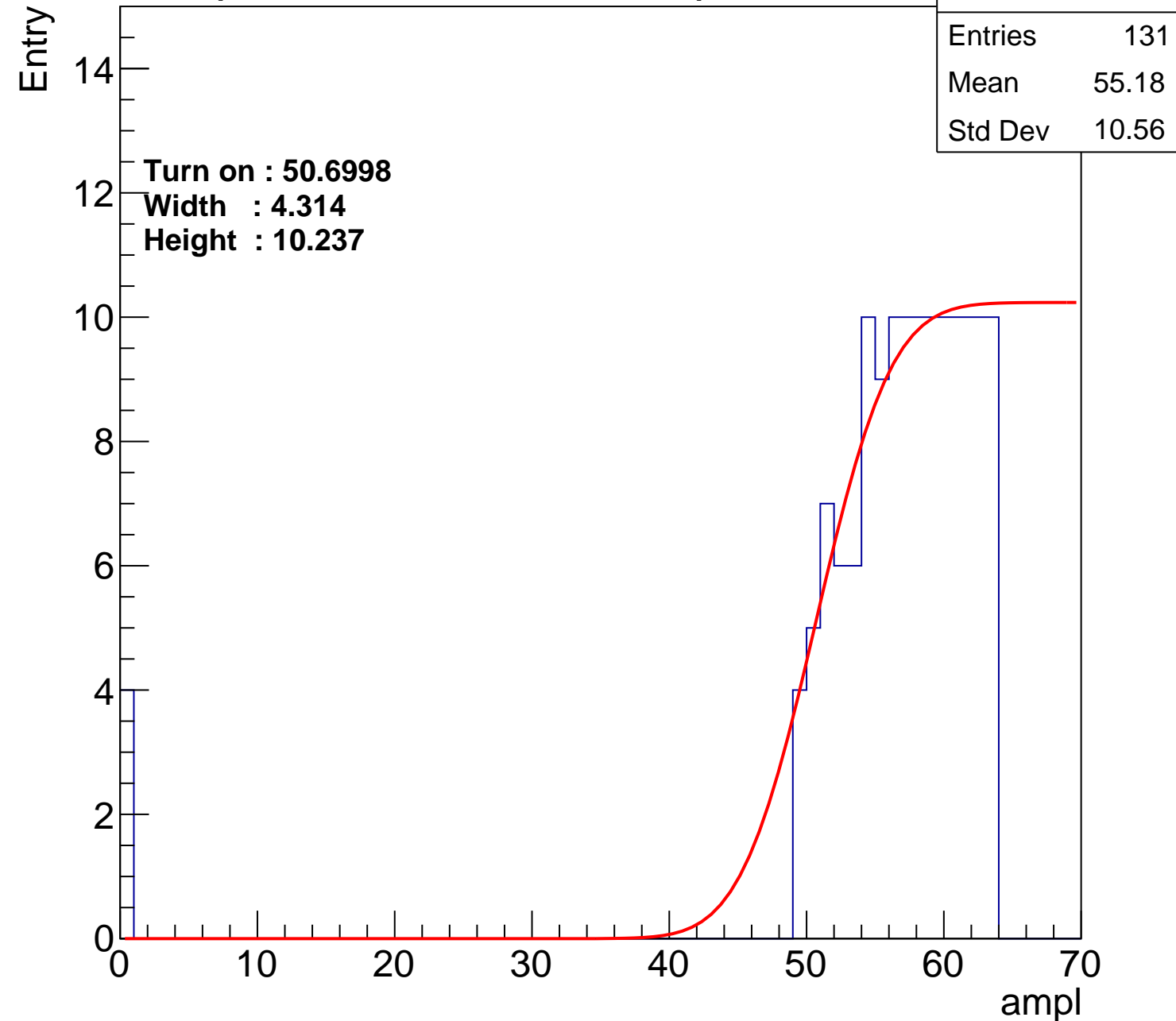
Width : 4.314

Height : 10.237

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch27

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.82
Std Dev	8.149

Turn on : 51.2859

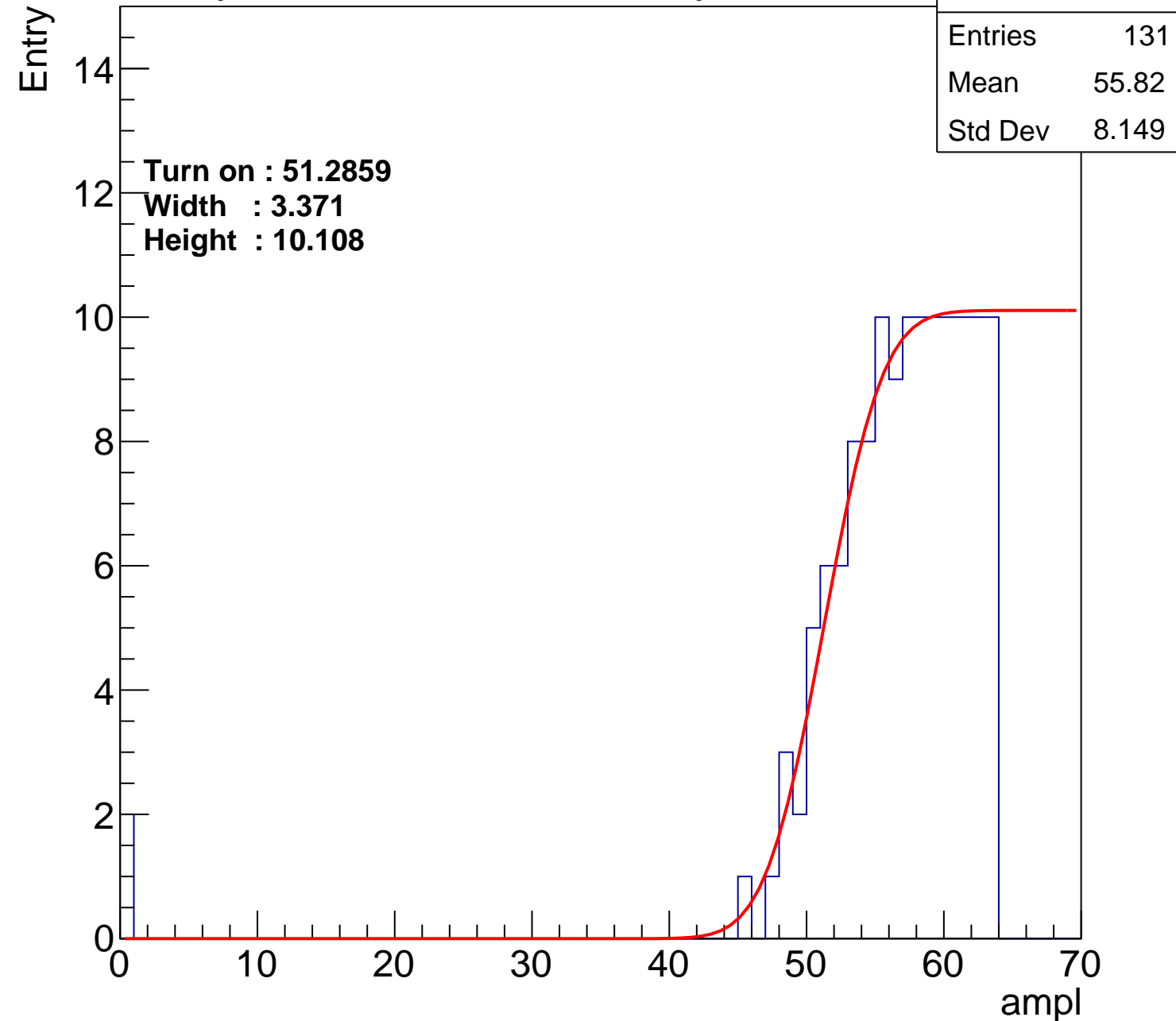
Width : 3.371

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch28

calib_packv5_040323_1717.root, FC#2, port C3

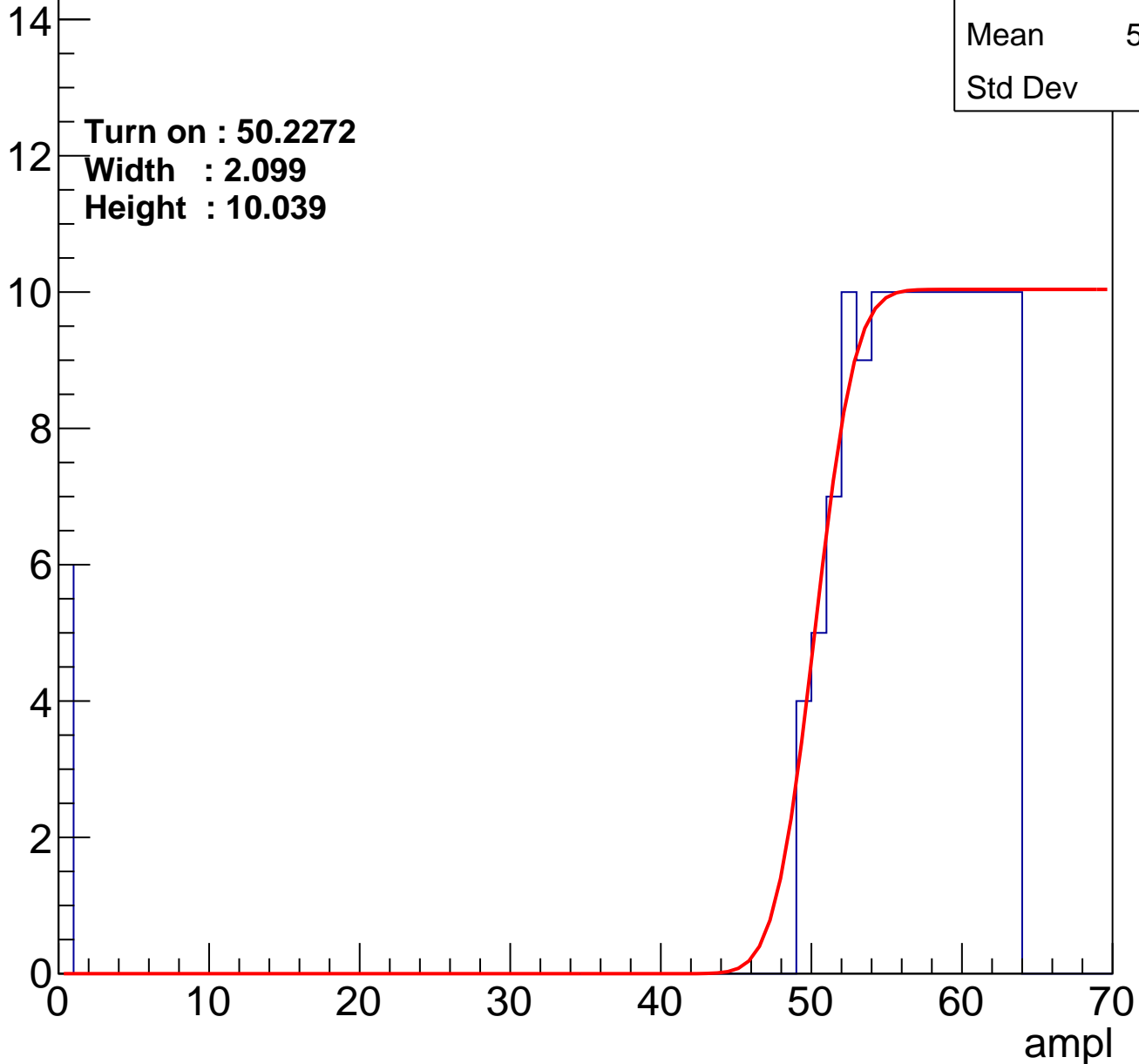
Entries	141
Mean	54.26
Std Dev	12.1

Turn on : 50.2272

Width : 2.099

Height : 10.039

Entry



B0L103S, U24-ch29

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.78
Std Dev	9.626

Turn on : 52.0266

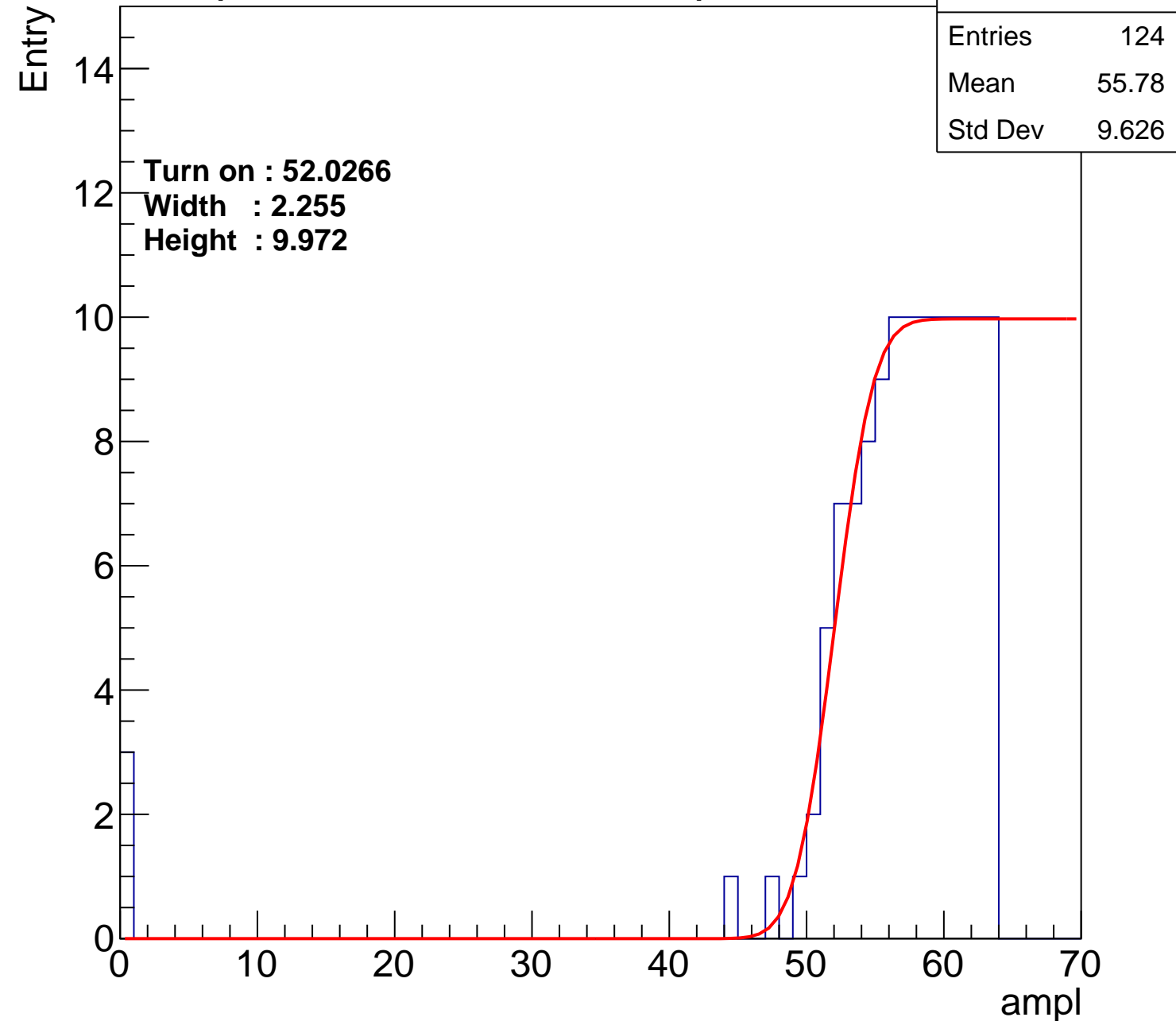
Width : 2.255

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch30

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	55.4
Std Dev	9.275

Turn on : 50.3723

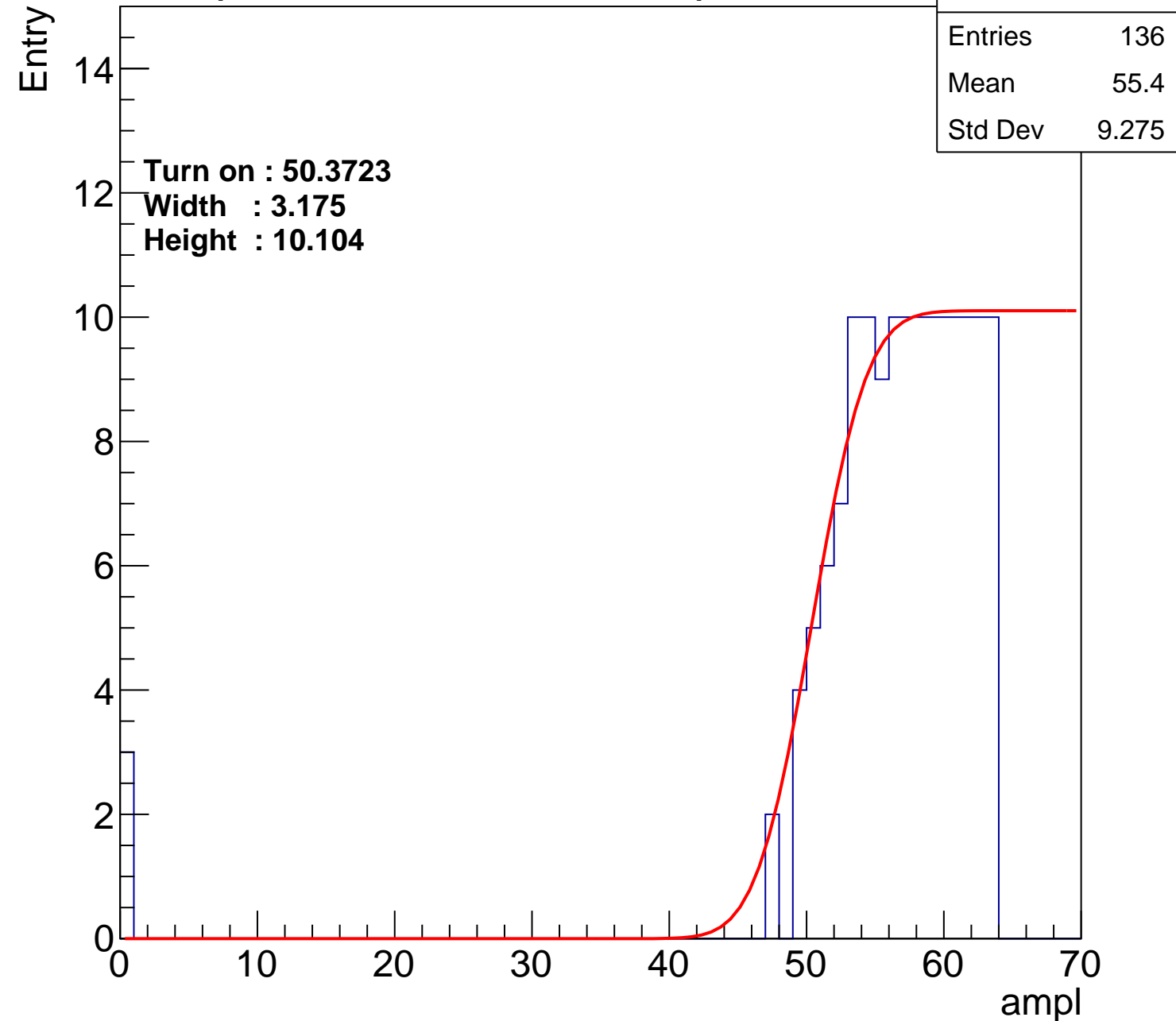
Width : 3.175

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch31

calib_packv5_040323_1717.root, FC#2, port C3

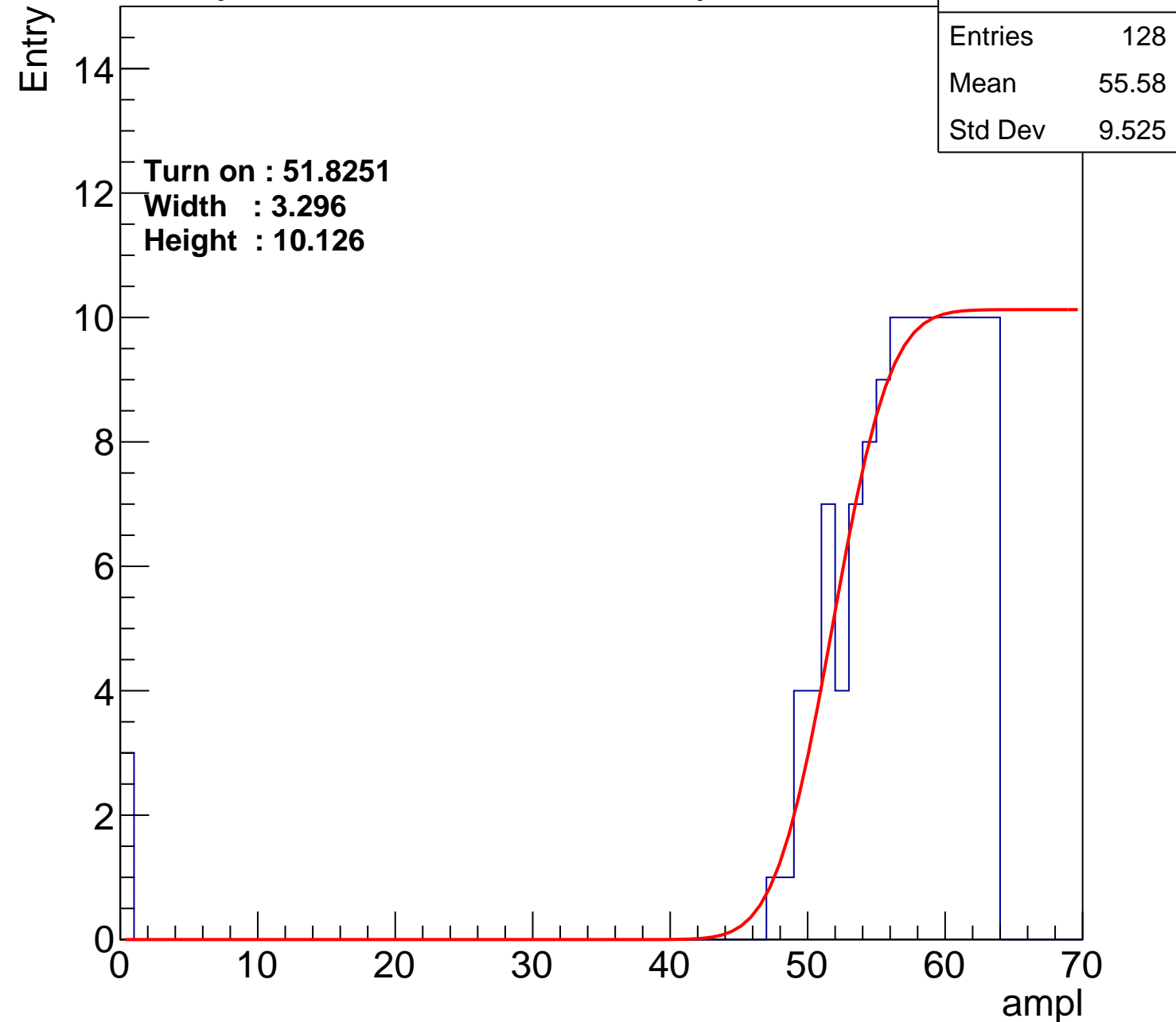
Entry

14
12
10
8
6
4
2
0

Turn on : 51.8251
Width : 3.296
Height : 10.126

Entries	128
Mean	55.58
Std Dev	9.525

ampl



B0L103S, U24-ch32

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.1
Std Dev	8.226

Turn on : 51.5882

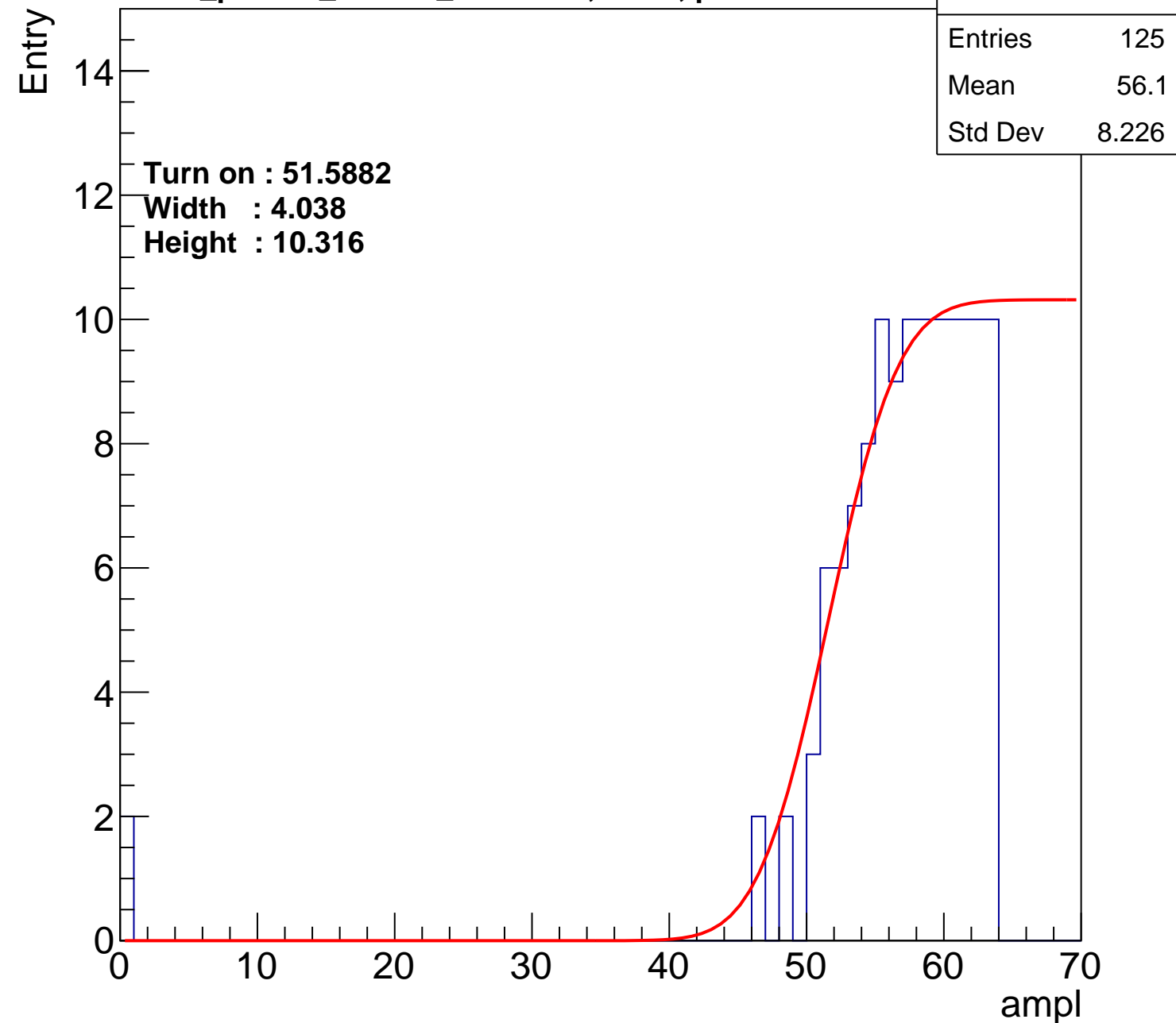
Width : 4.038

Height : 10.316

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch33

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	56.16
Std Dev	8.288

Turn on : 52.9020

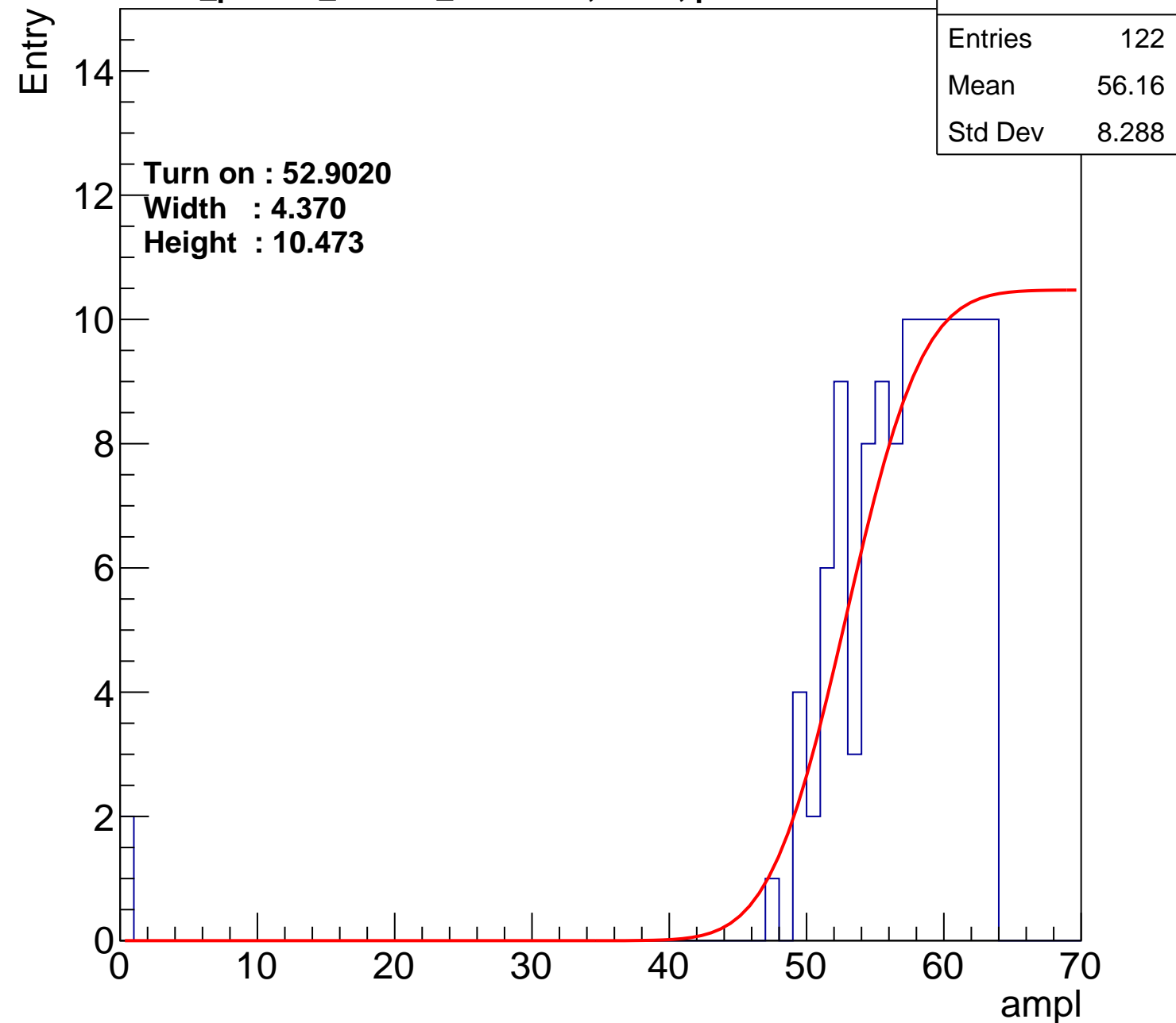
Width : 4.370

Height : 10.473

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch34

calib_packv5_040323_1717.root, FC#2, port C3

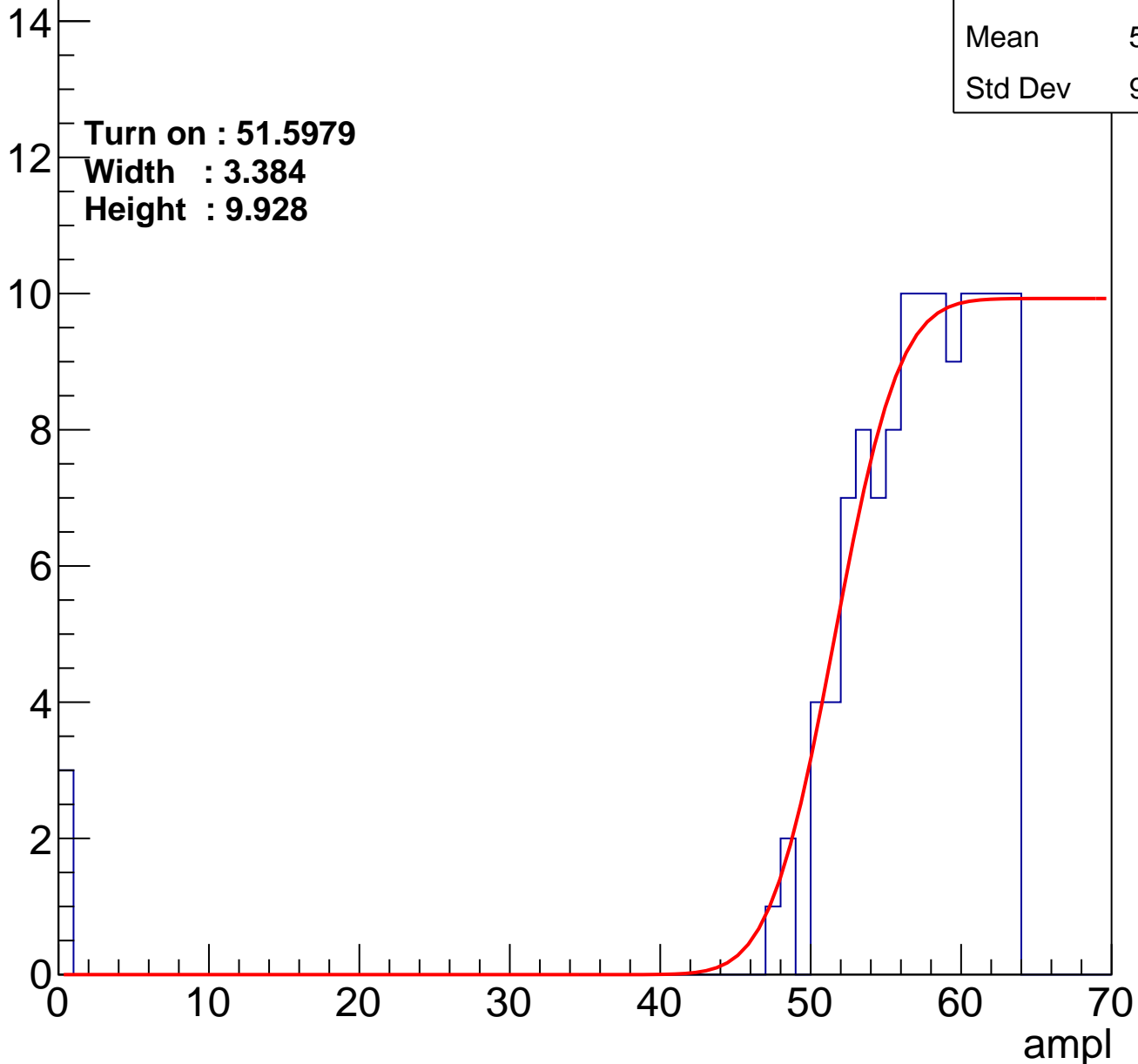
Entries	123
Mean	55.72
Std Dev	9.654

Turn on : 51.5979

Width : 3.384

Height : 9.928

Entry



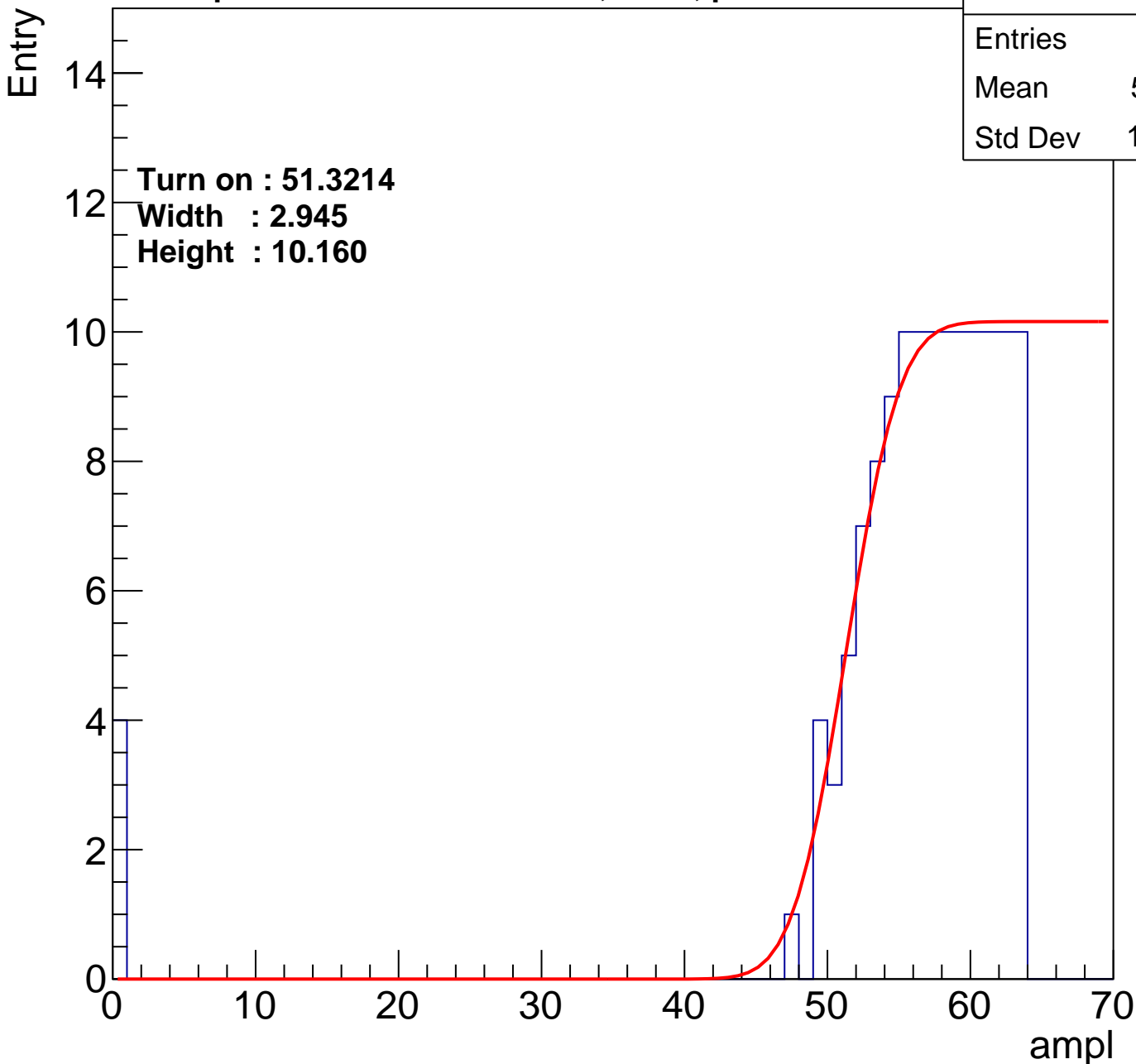
calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.21
Std Dev	10.56

Entries	131
Mean	55.21
Std Dev	10.56

Width : 2.945

Height : 10.160



B0L103S, U24-ch36

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55
Std Dev	10.63

Turn on : 51.3574

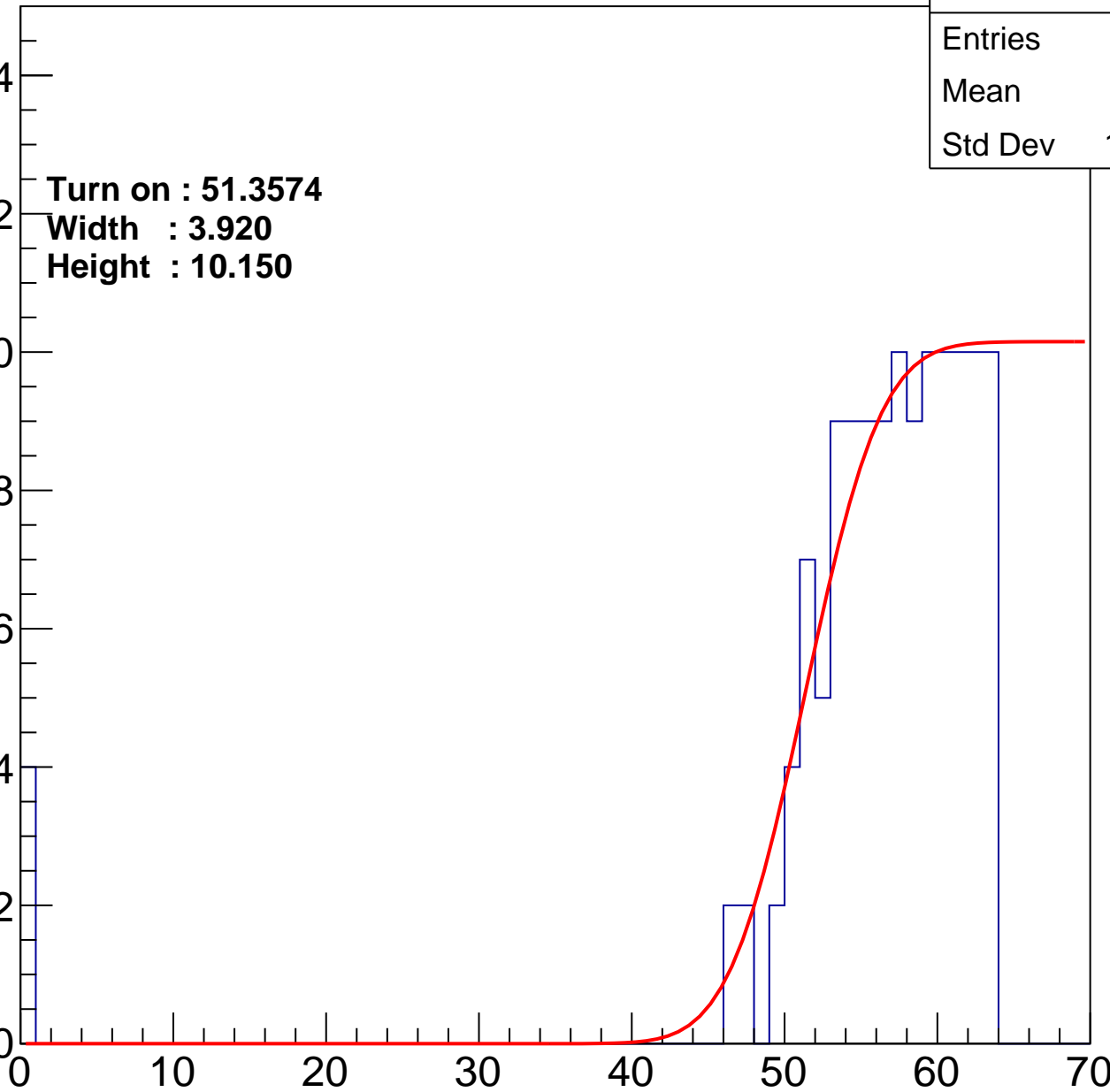
Width : 3.920

Height : 10.150

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch37

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.78
Std Dev	8.013

Turn on : 50.7374

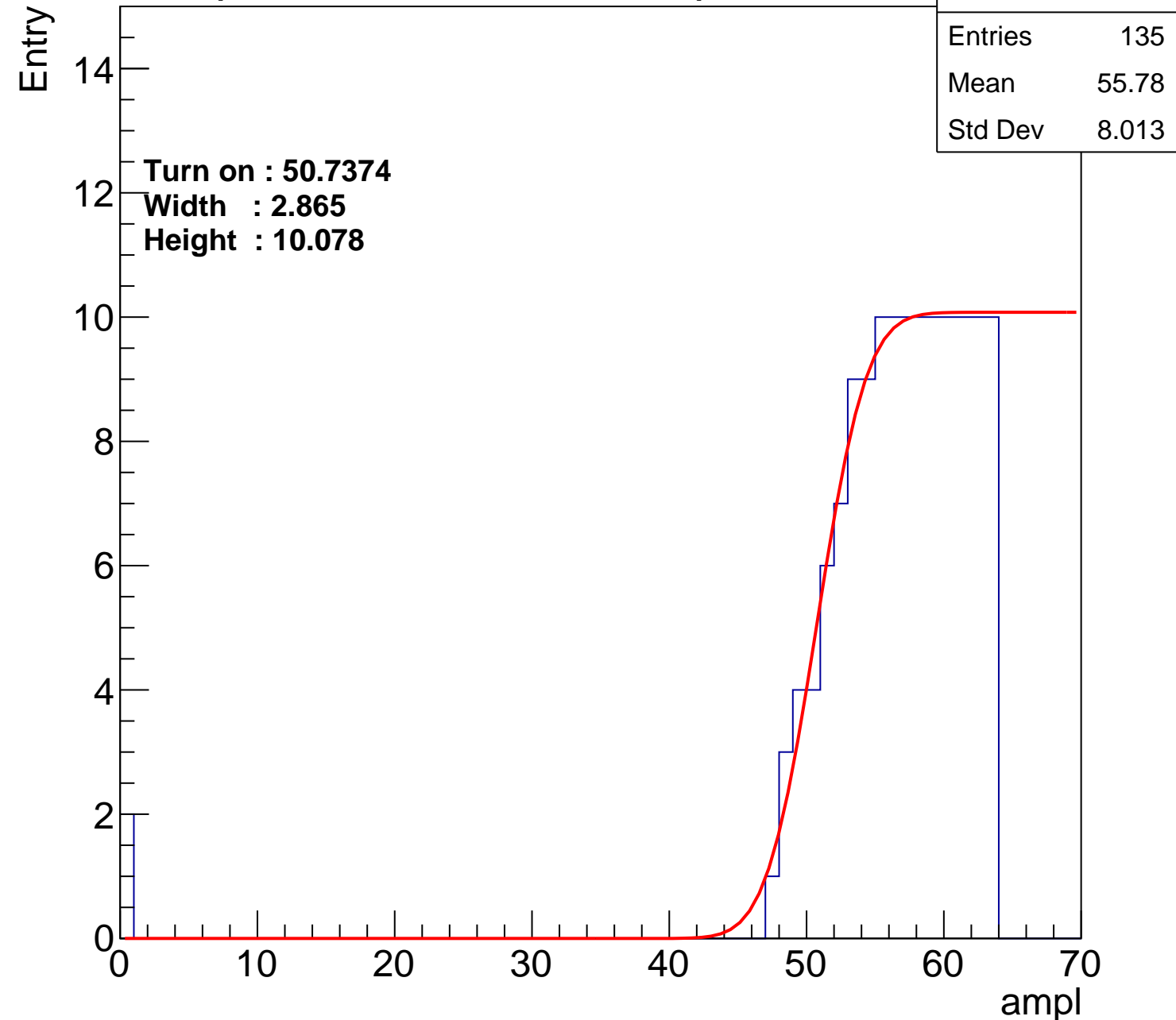
Width : 2.865

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch38

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.05
Std Dev	9.849

Turn on : 52.8558

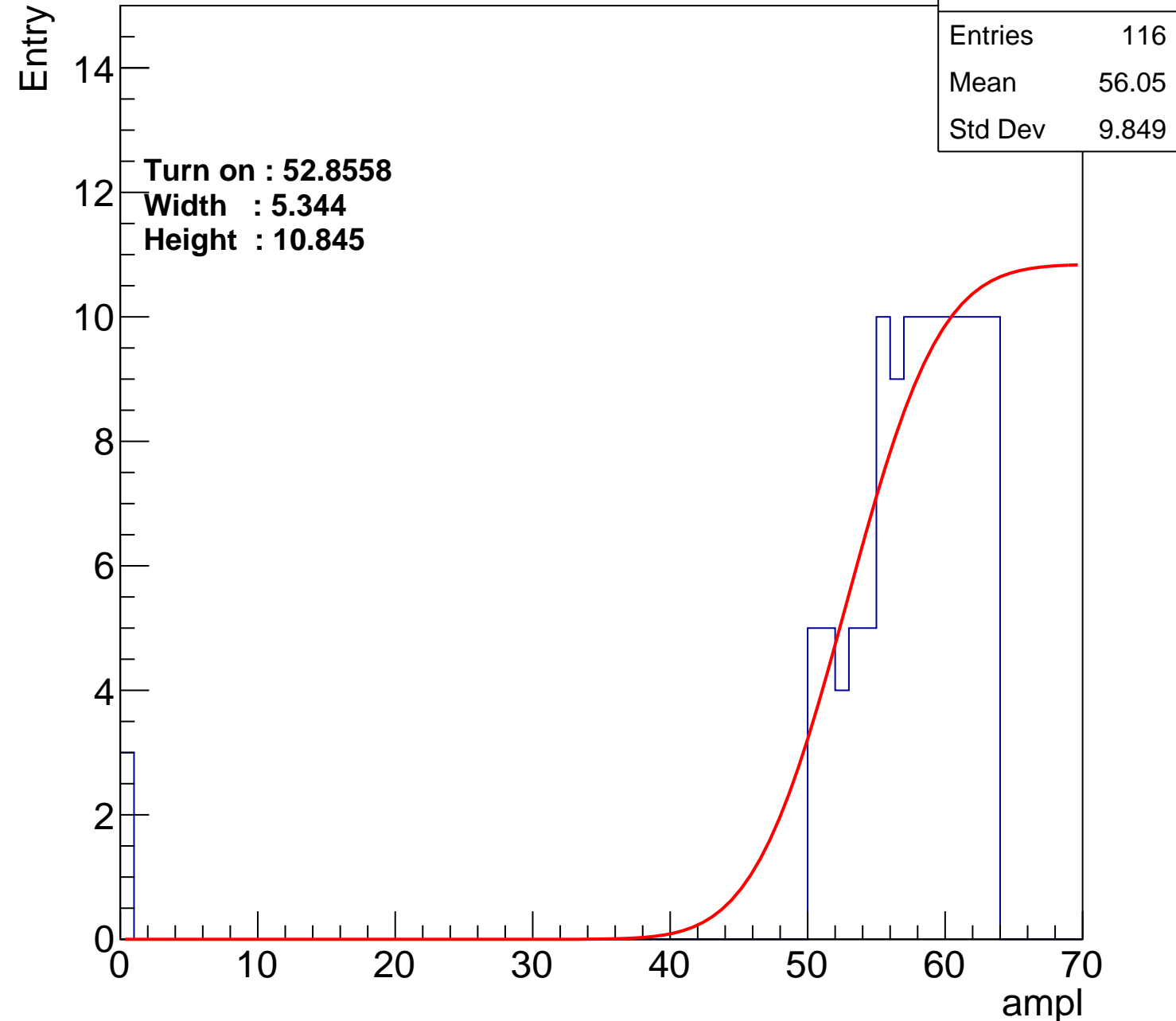
Width : 5.344

Height : 10.845

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch39

calib_packv5_040323_1717.root, FC#2, port C3

Entries	150
Mean	54.76
Std Dev	9.082

Turn on : 49.4834

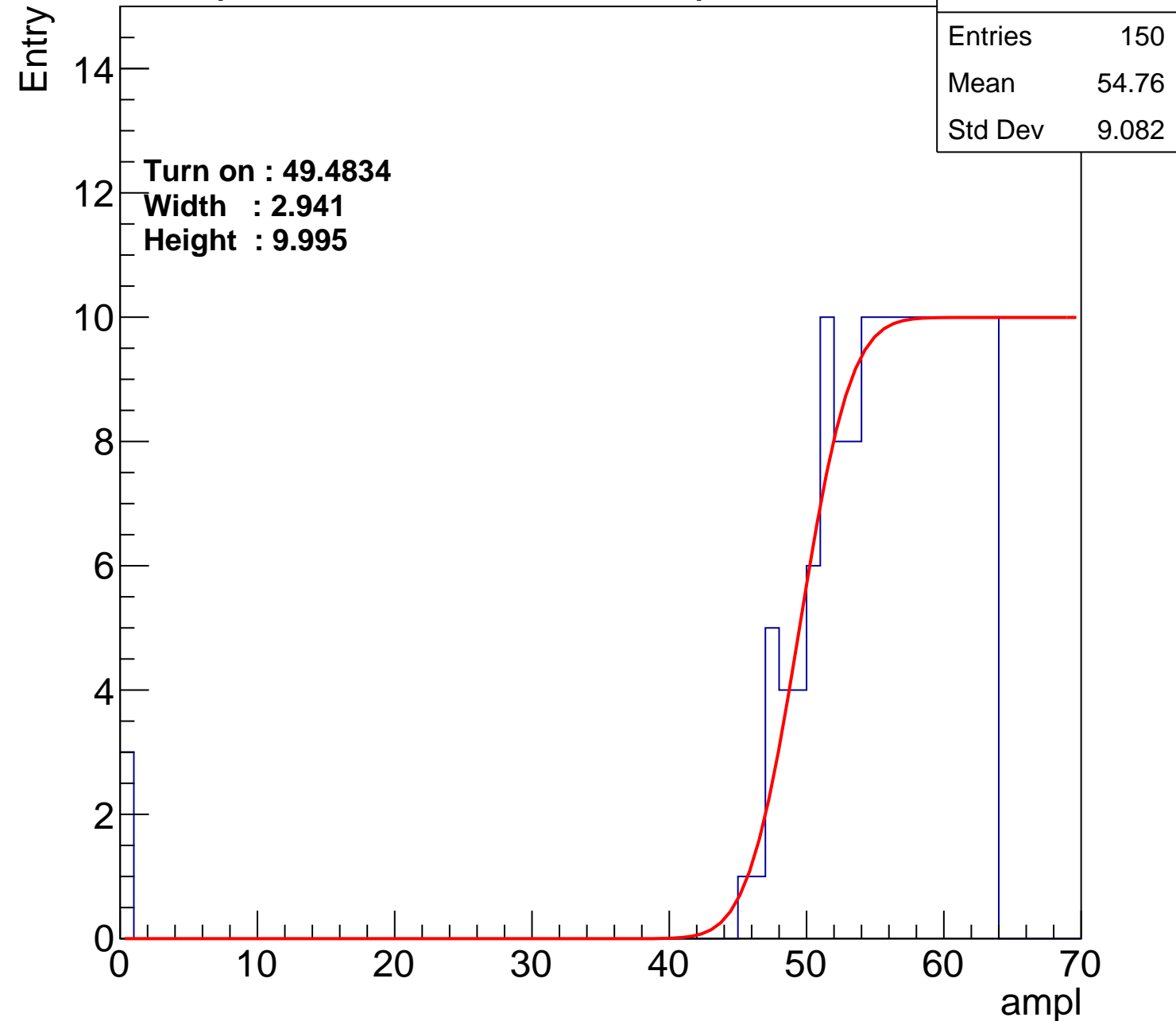
Width : 2.941

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch40

calib_packv5_040323_1717.root, FC#2, port C3

Entries	110
Mean	57.25
Std Dev	6.511

Turn on : 53.2105

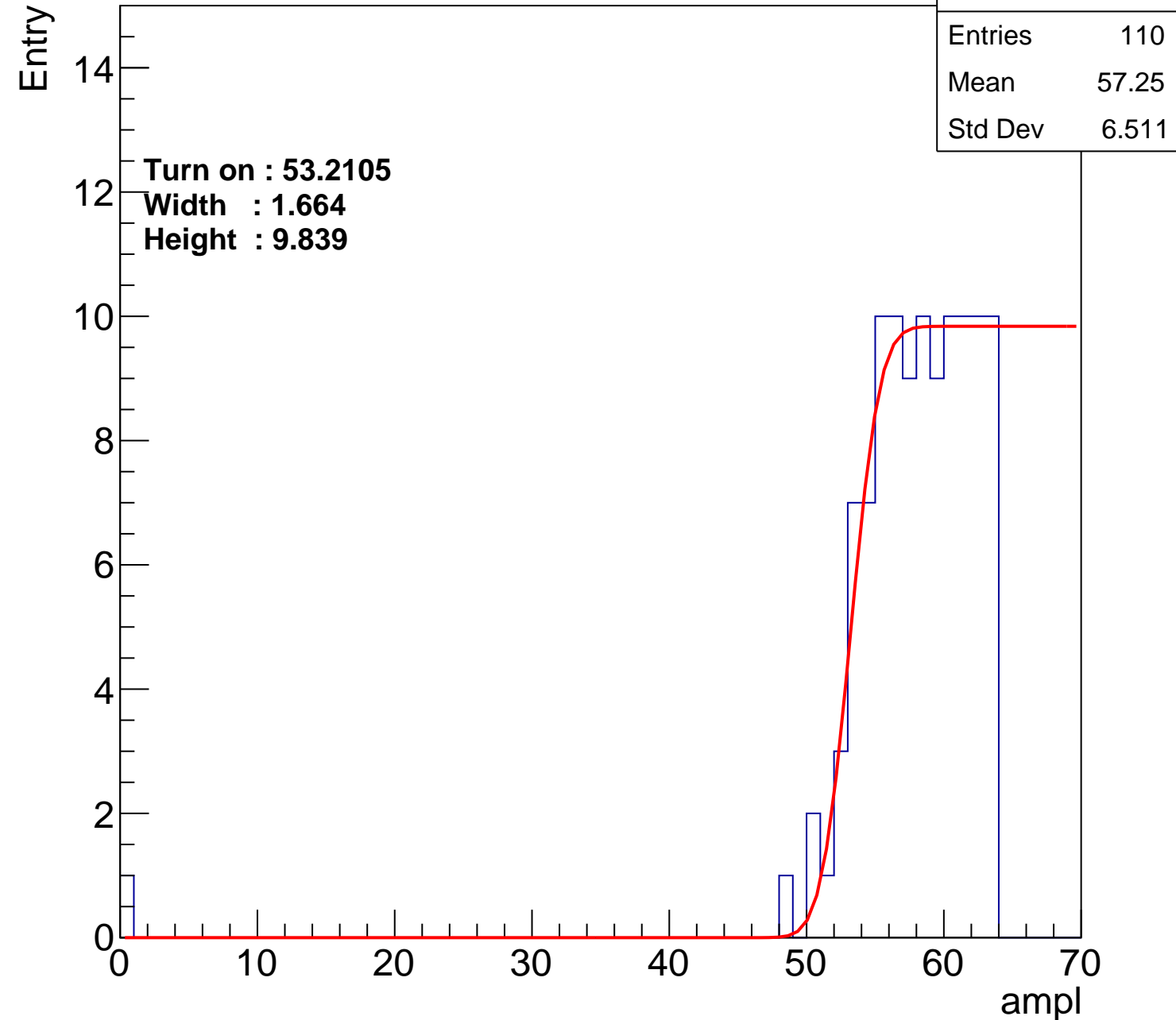
Width : 1.664

Height : 9.839

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch41

calib_packv5_040323_1717.root, FC#2, port C3

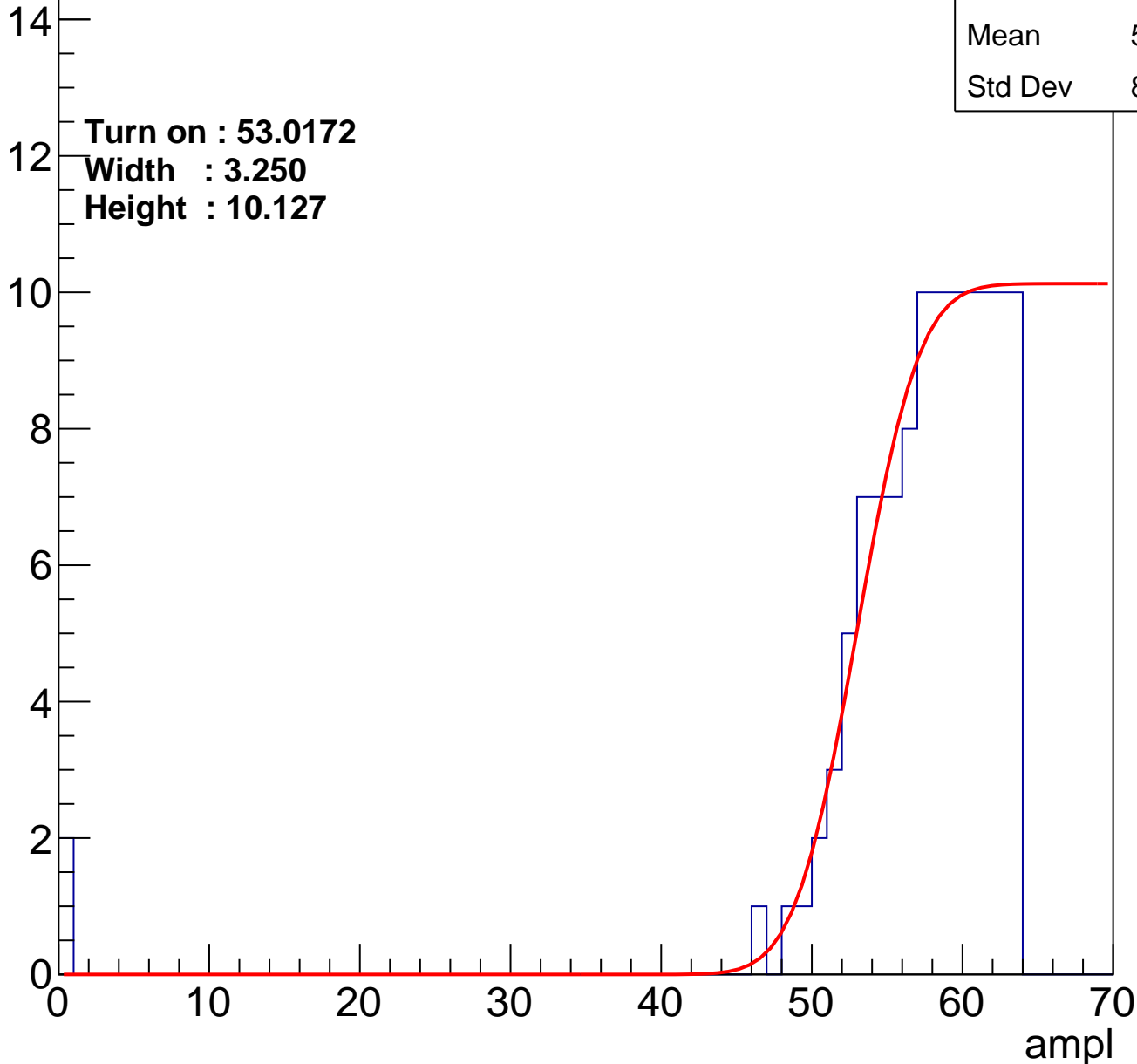
Entries	114
Mean	56.47
Std Dev	8.473

Turn on : 53.0172

Width : 3.250

Height : 10.127

Entry



B0L103S, U24-ch42

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	54.8
Std Dev	9.024

Turn on : 49.3728

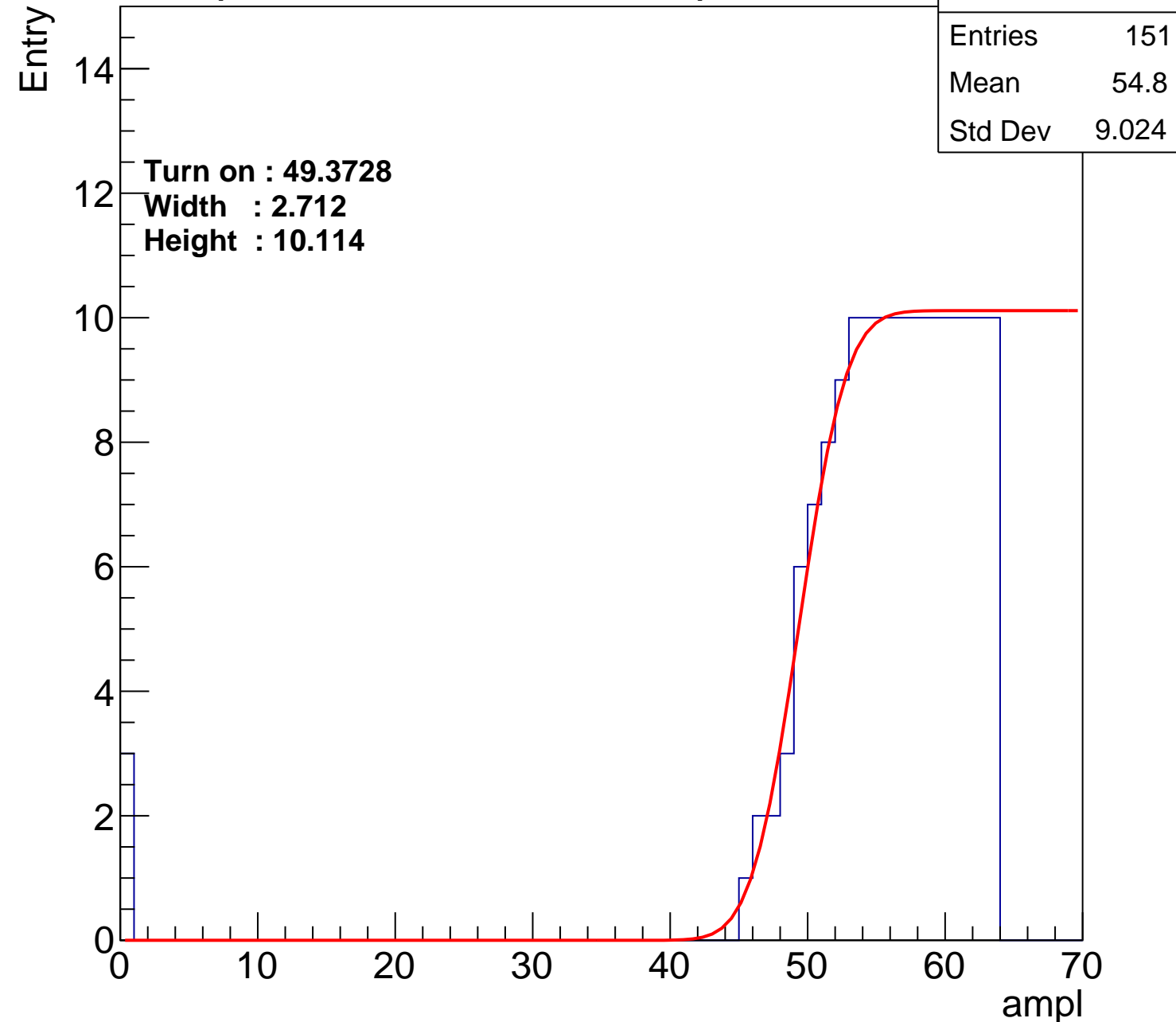
Width : 2.712

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch43

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	54.74
Std Dev	10.15

Turn on : 49.7685

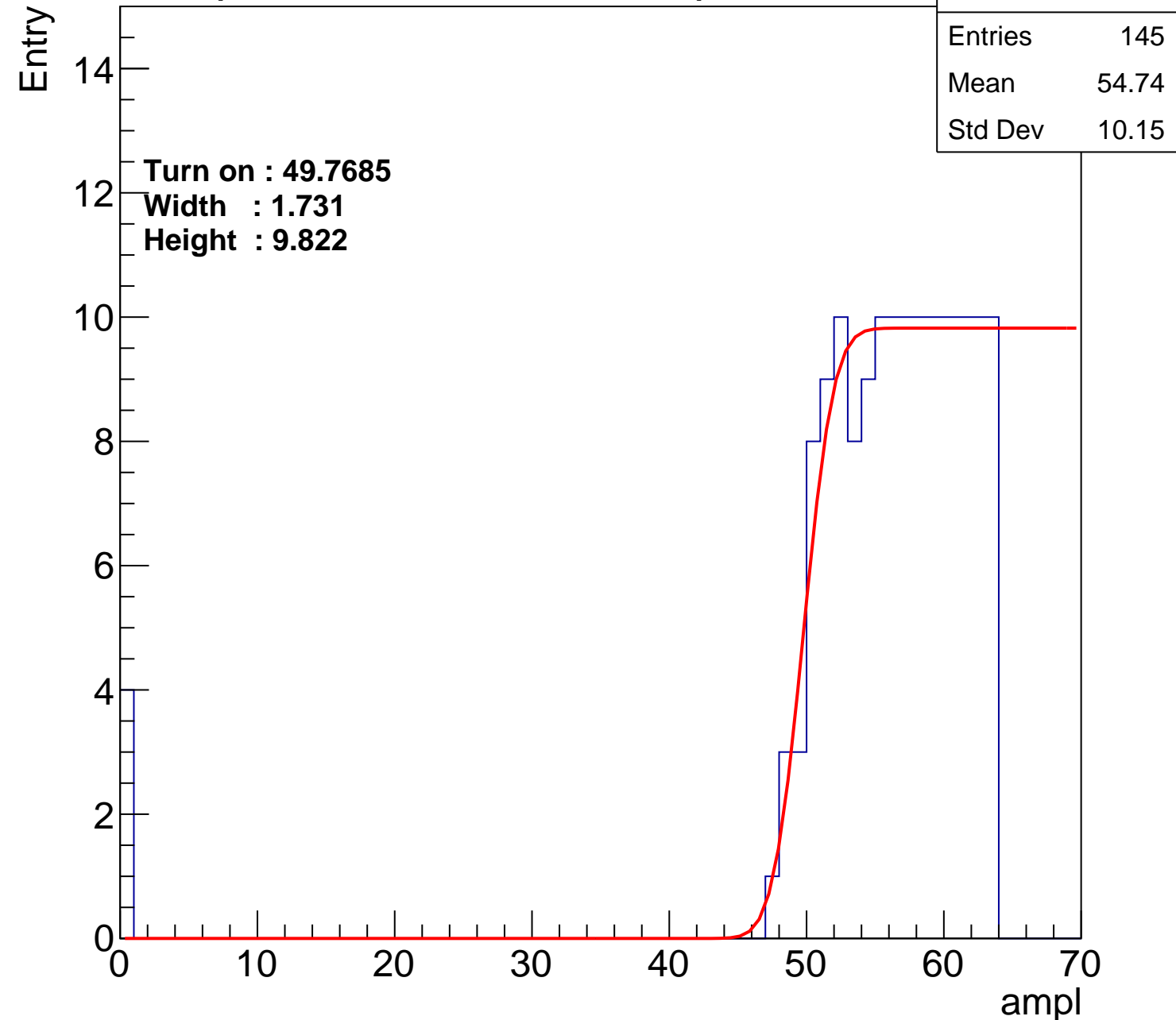
Width : 1.731

Height : 9.822

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch44

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	55.96
Std Dev	9.824

Turn on : 53.2059

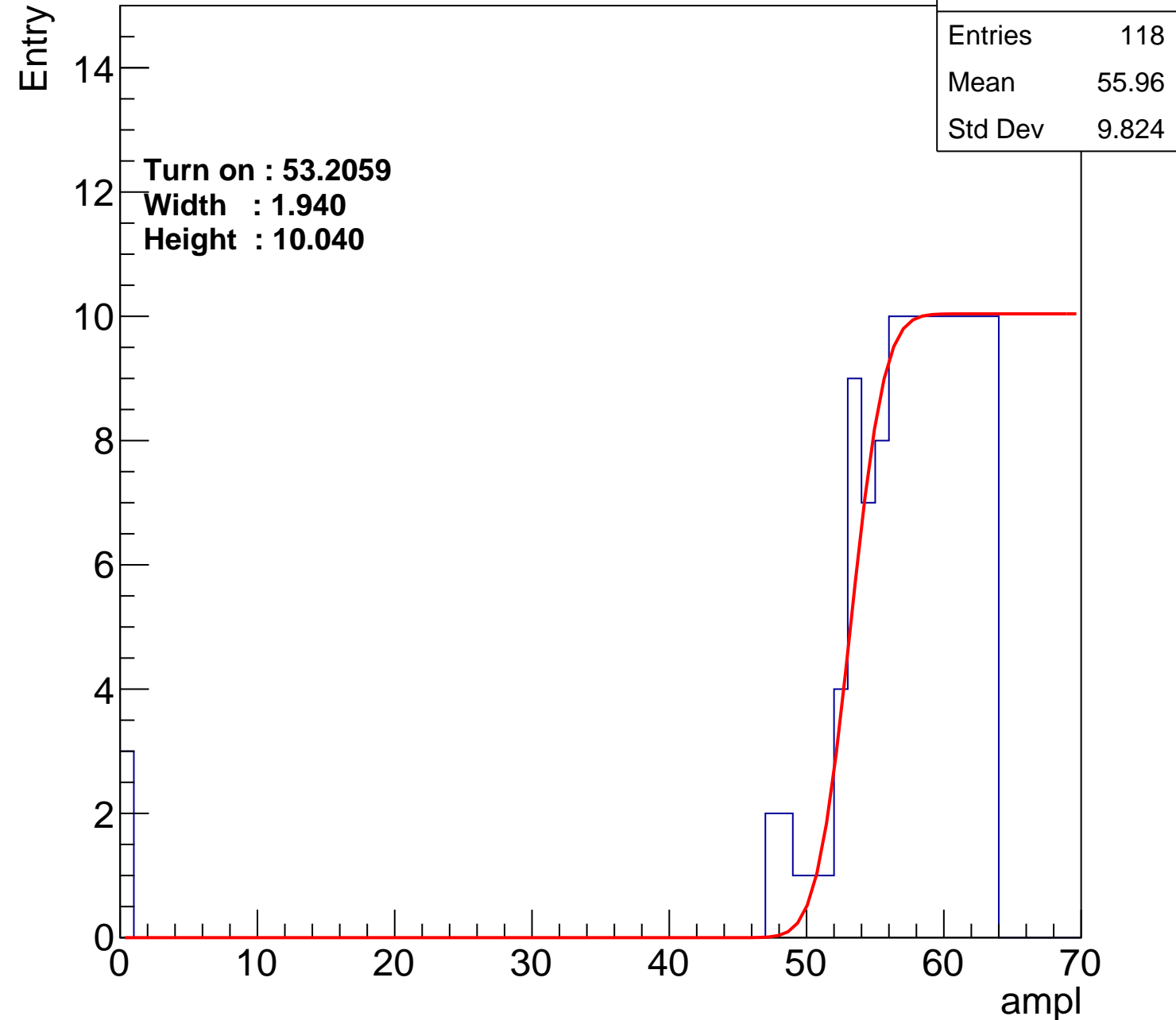
Width : 1.940

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch45

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	56.34
Std Dev	8.3

Turn on : 52.0147

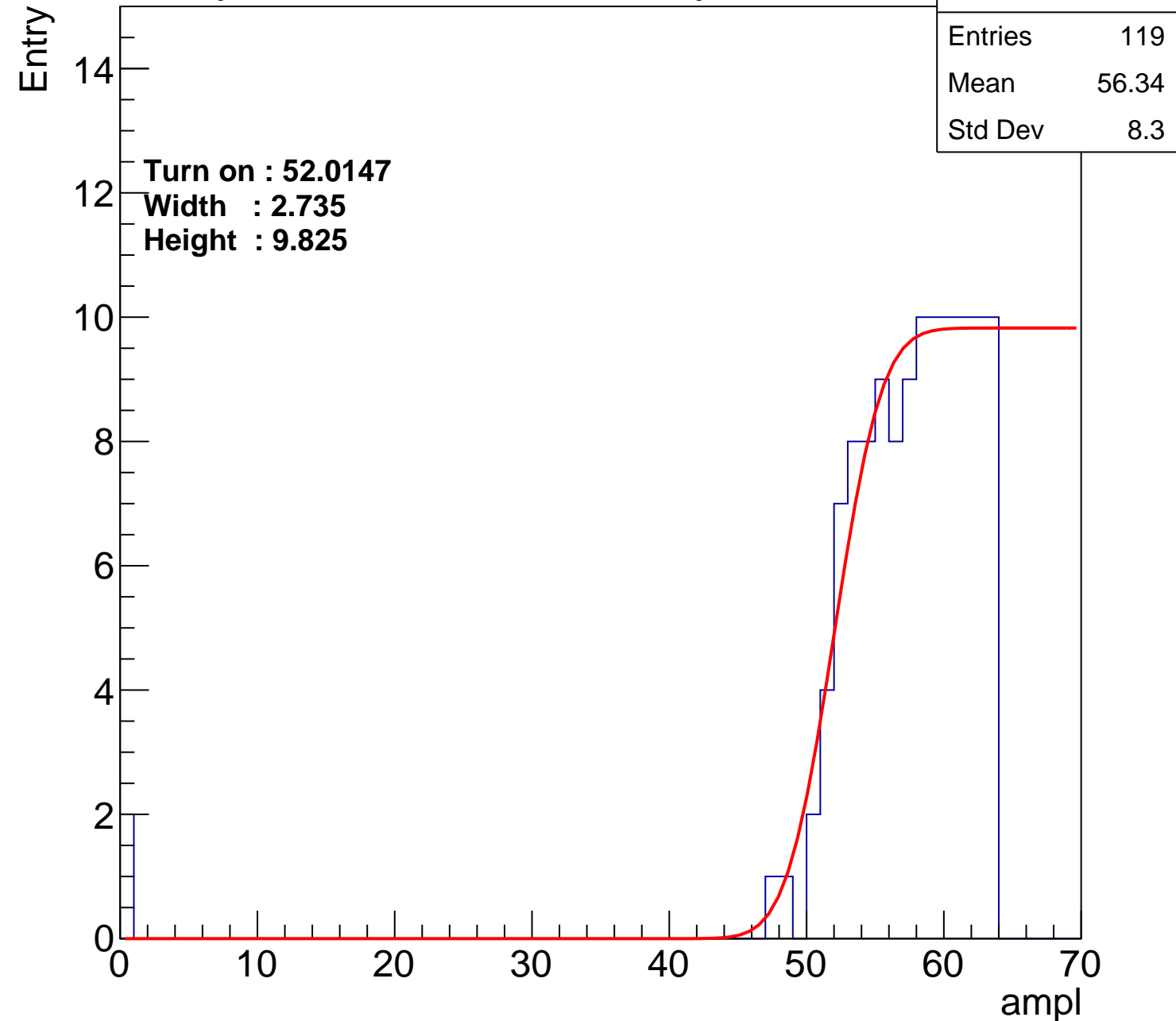
Width : 2.735

Height : 9.825

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch46

calib_packv5_040323_1717.root, FC#2, port C3

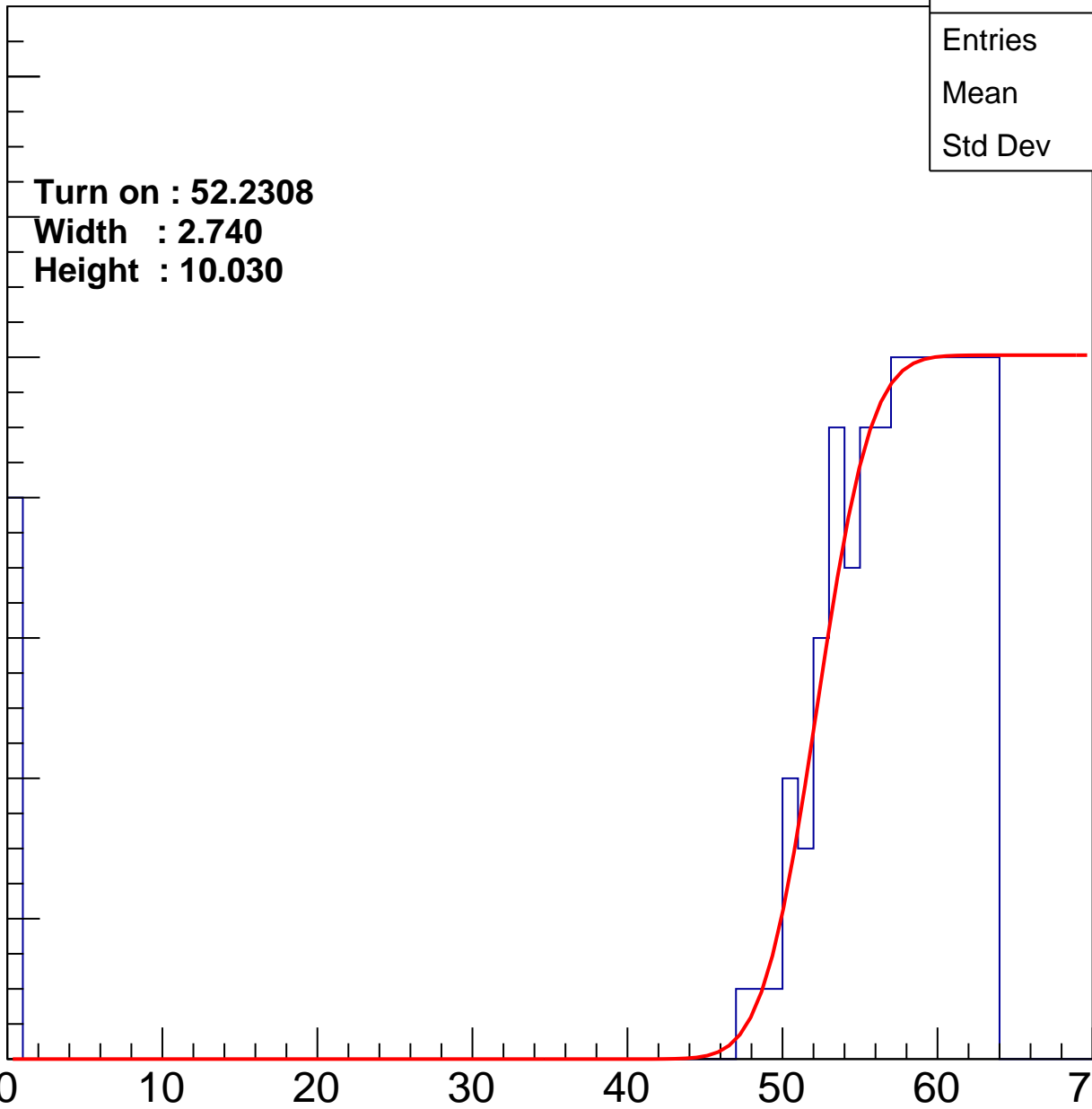
Entry

14
12
10
8
6
4
2
0

Turn on : 52.2308
Width : 2.740
Height : 10.030

Entries	128
Mean	53.62
Std Dev	14.36

ampl



B0L103S, U24-ch47

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	54.69
Std Dev	10.27

Turn on : 50.0817

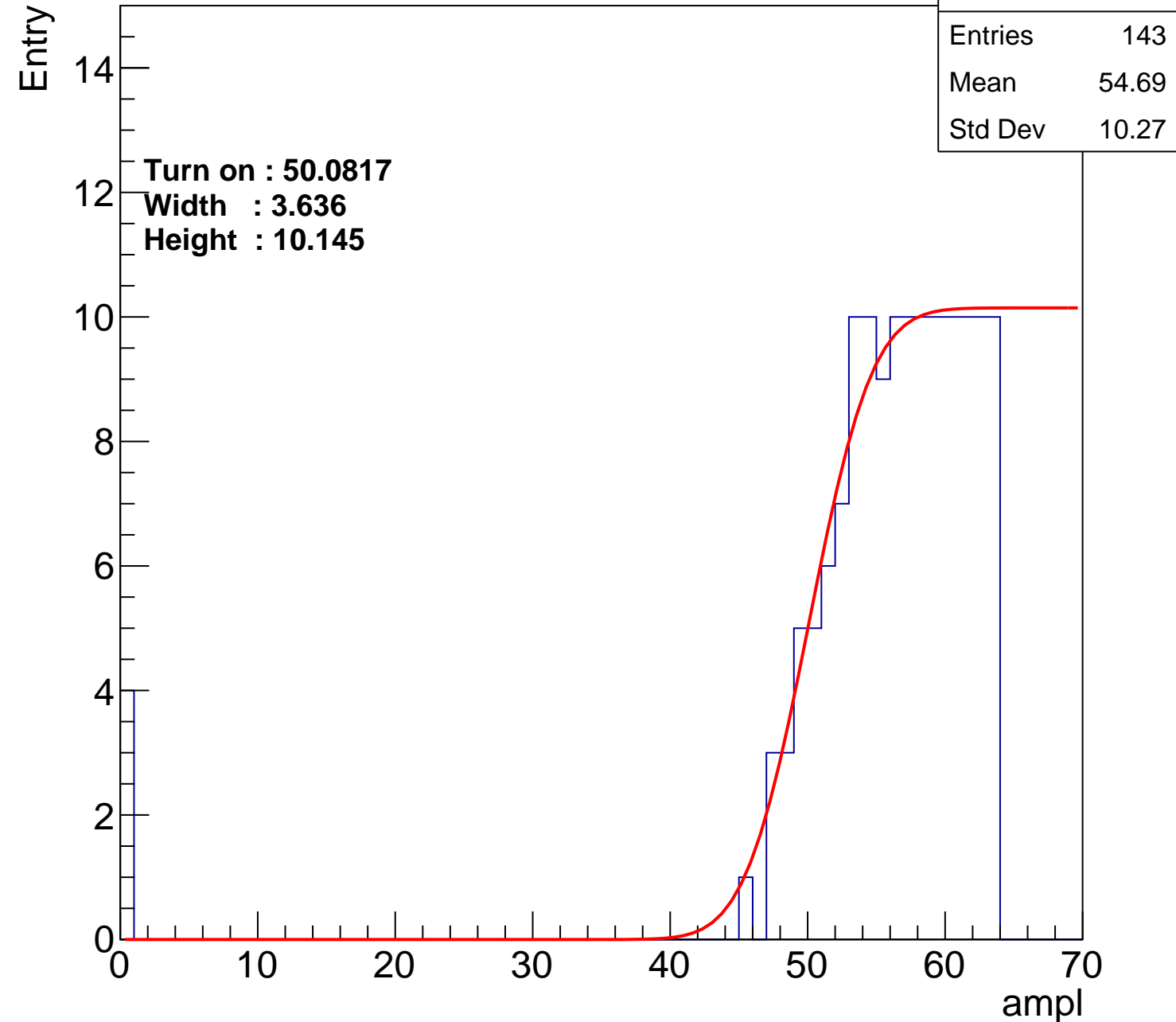
Width : 3.636

Height : 10.145

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch48

calib_packv5_040323_1717.root, FC#2, port C3

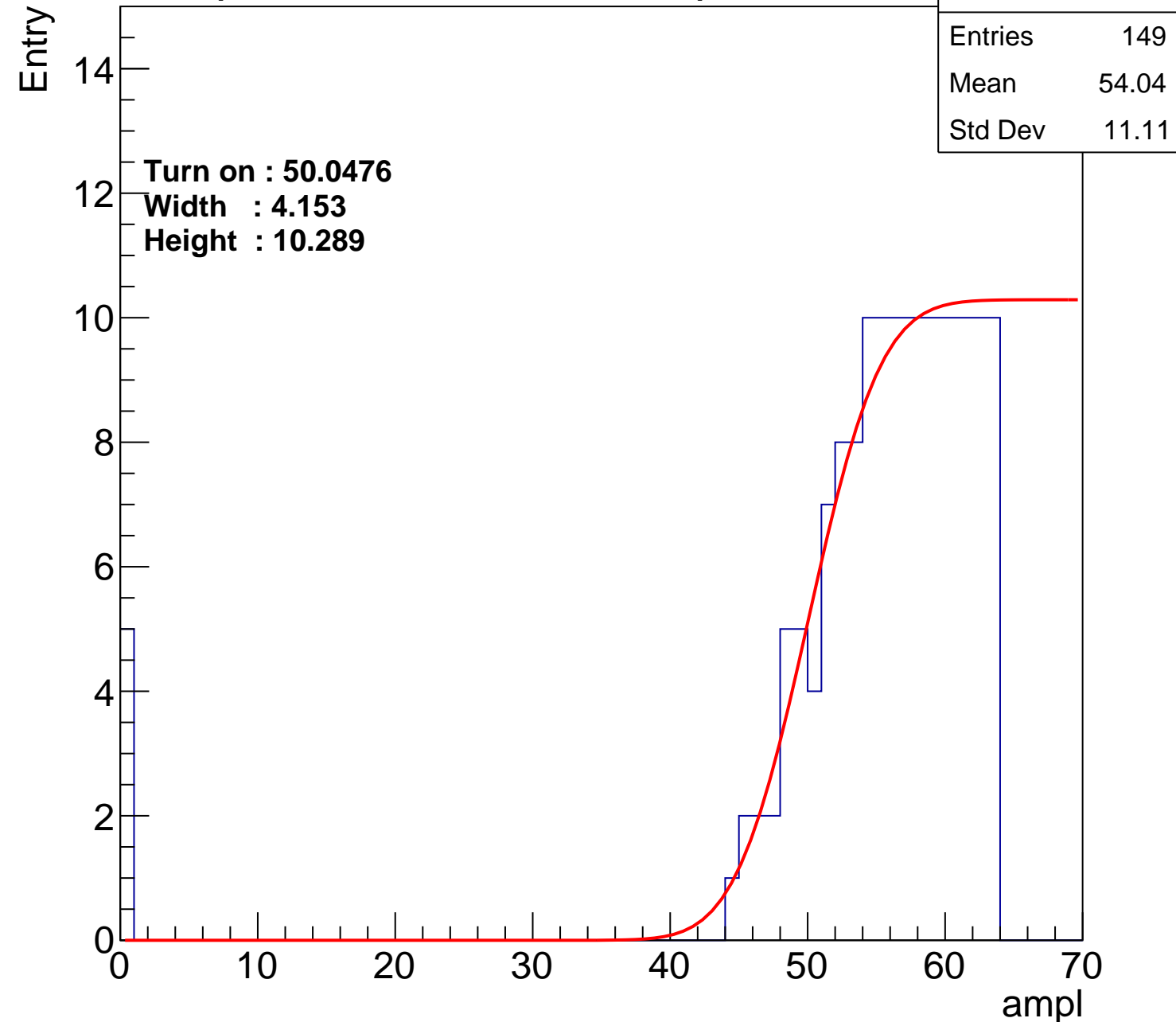
Entry

14
12
10
8
6
4
2
0

Turn on : 50.0476
Width : 4.153
Height : 10.289

Entries	149
Mean	54.04
Std Dev	11.11

ampl



B0L103S, U24-ch49

calib_packv5_040323_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

Turn on : 51.6454

Width : 2.565

Height : 9.990

Entries	126
Mean	55.75
Std Dev	9.525

ampl

0

10

20

30

40

50

60

70

B0L103S, U24-ch50

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	55.1
Std Dev	12.24

Turn on : 52.9471

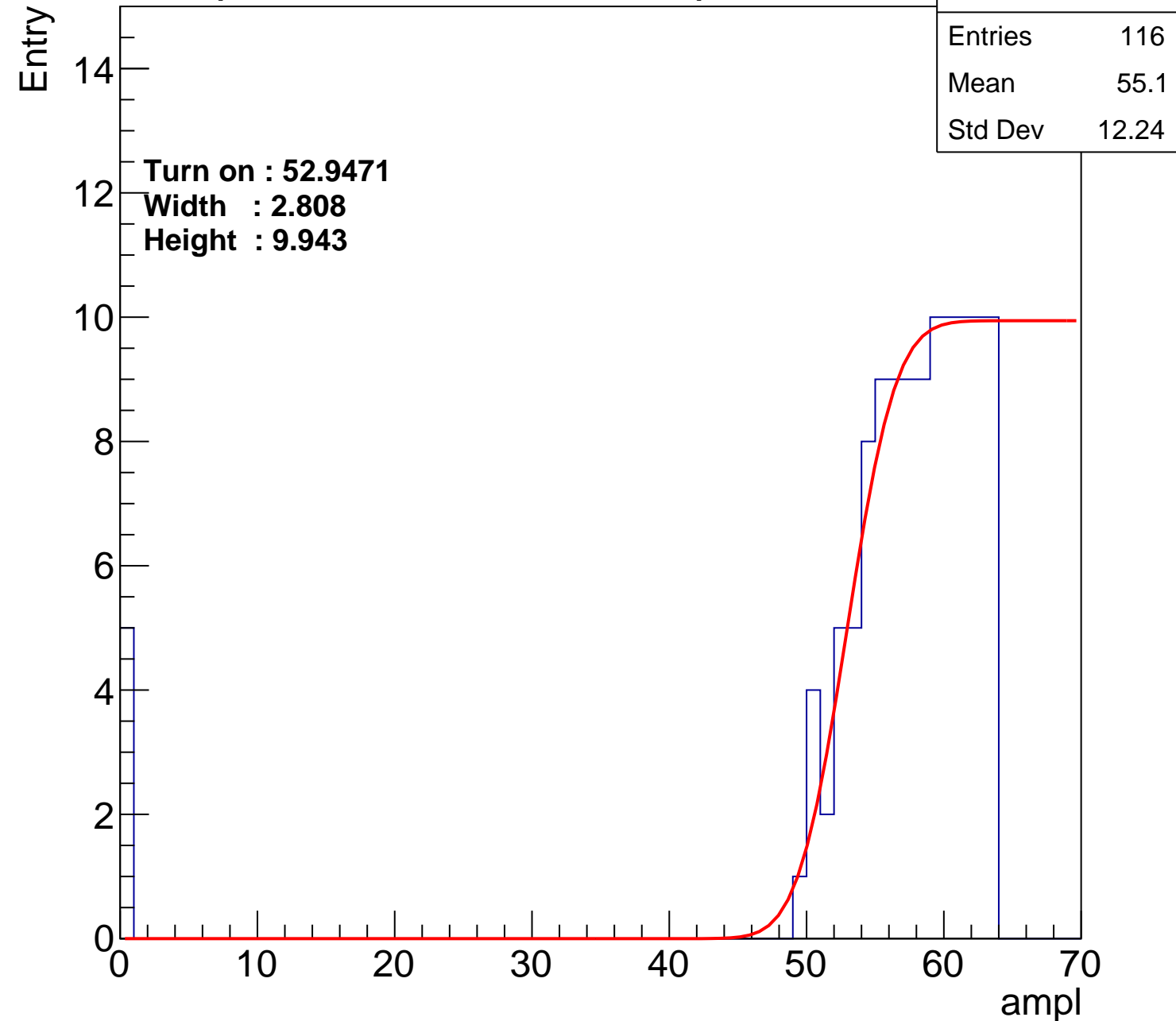
Width : 2.808

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch51

calib_packv5_040323_1717.root, FC#2, port C3

Entries	110
Mean	56.63
Std Dev	8.565

Turn on : 53.7175

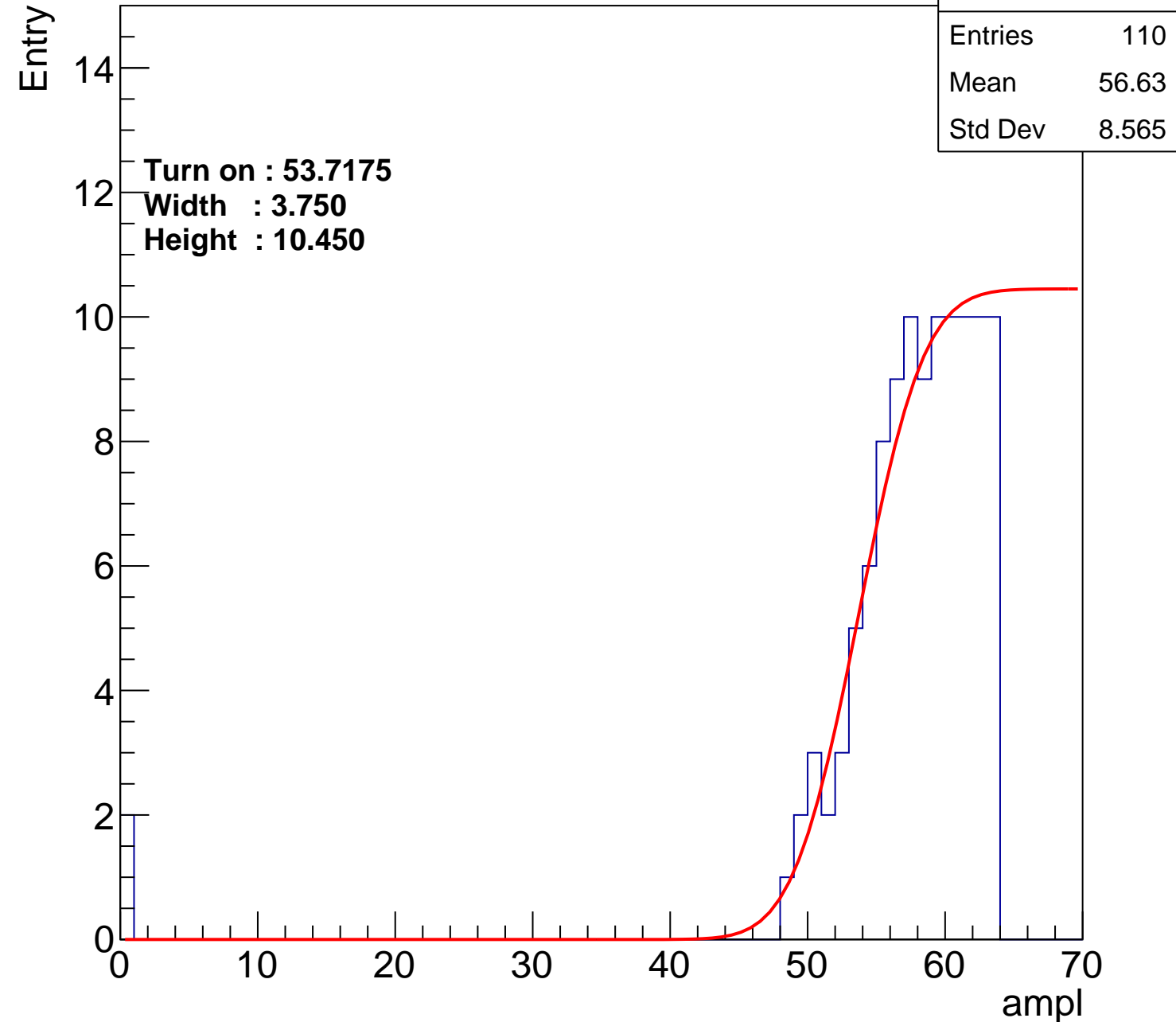
Width : 3.750

Height : 10.450

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch52

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	55.58
Std Dev	11.07

Turn on : 52.4788

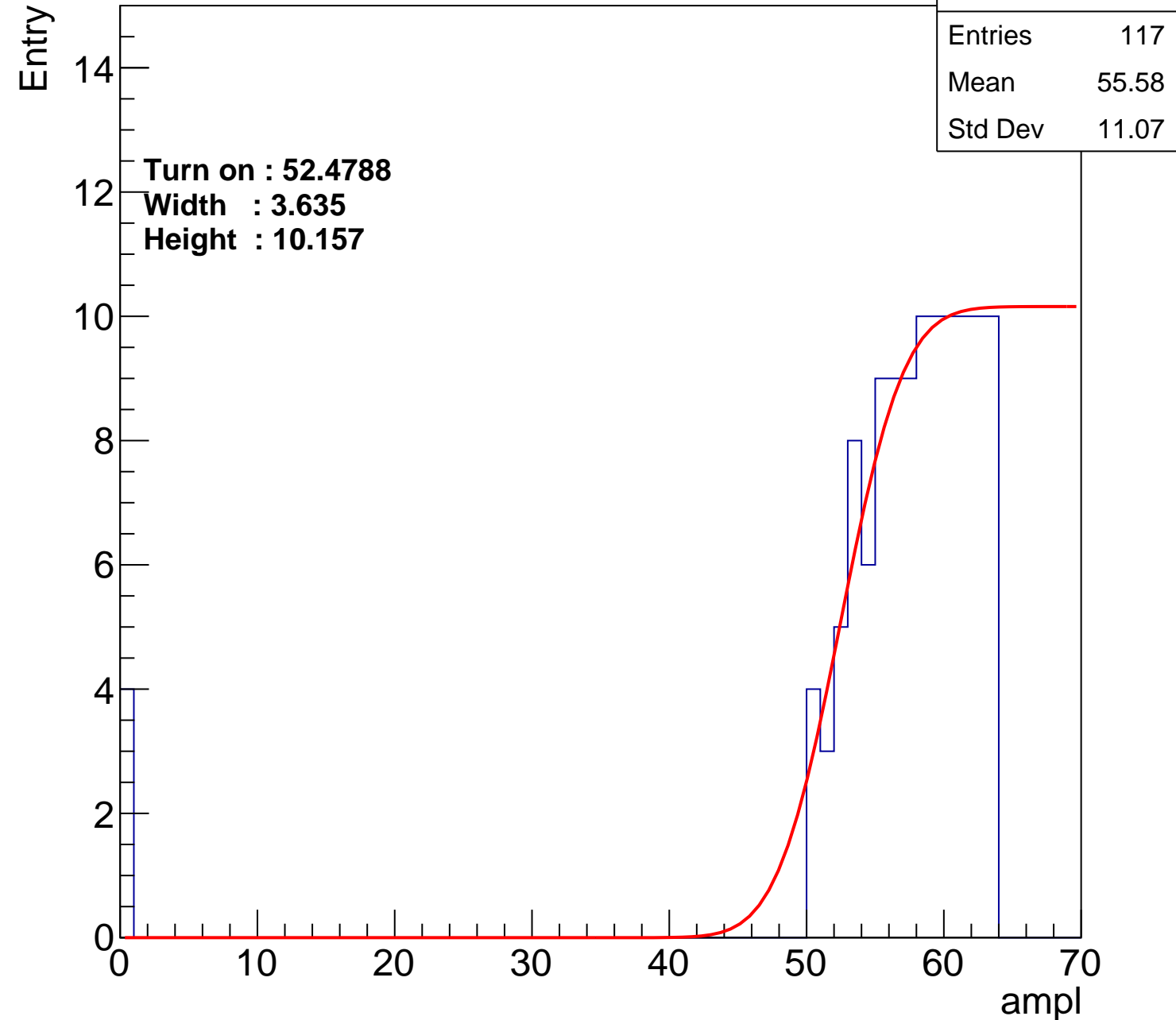
Width : 3.635

Height : 10.157

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch53

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	54.73
Std Dev	11.64

Turn on : 52.2096

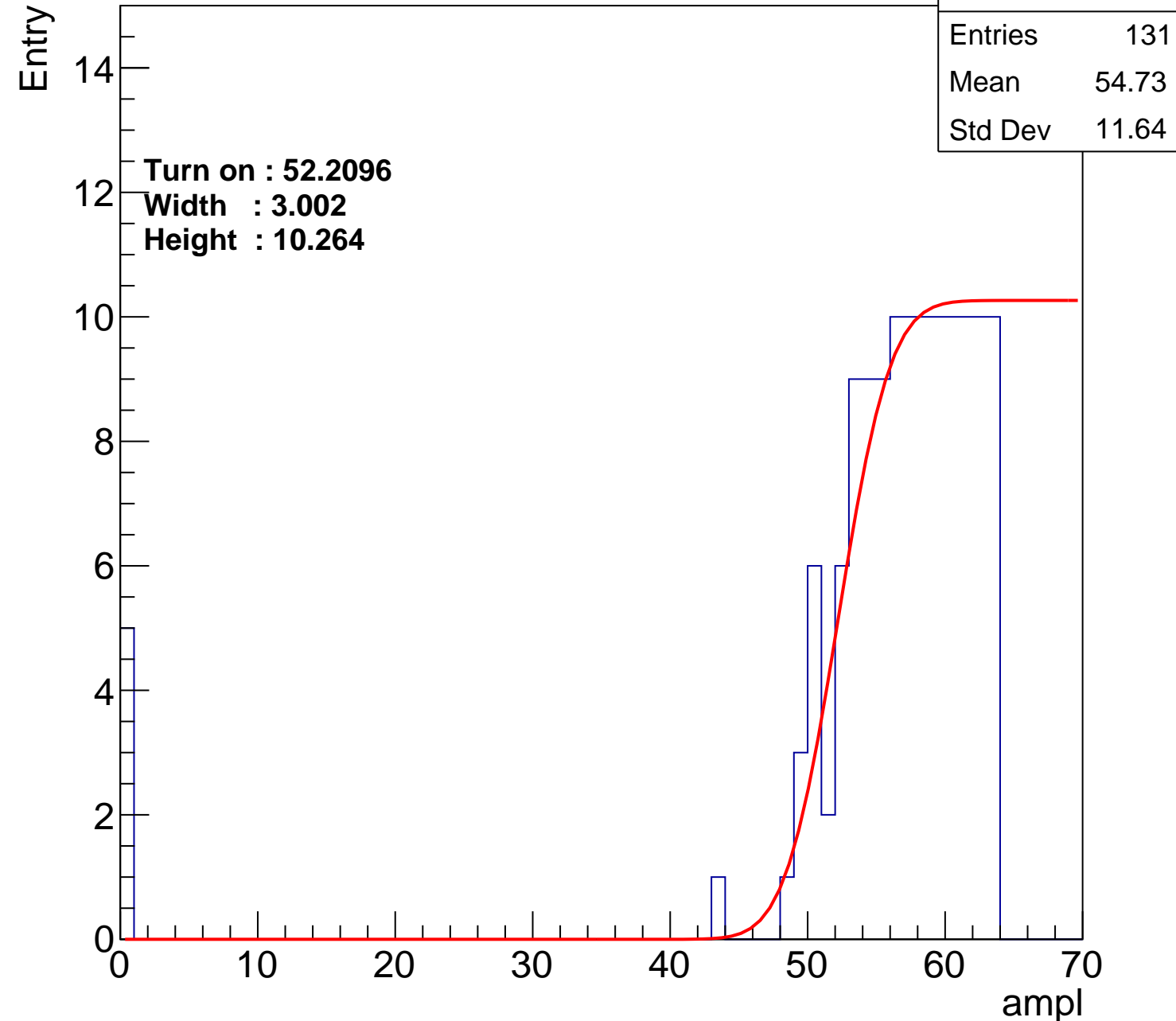
Width : 3.002

Height : 10.264

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch54

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.07
Std Dev	11.91

Turn on : 52.4870

Width : 1.955

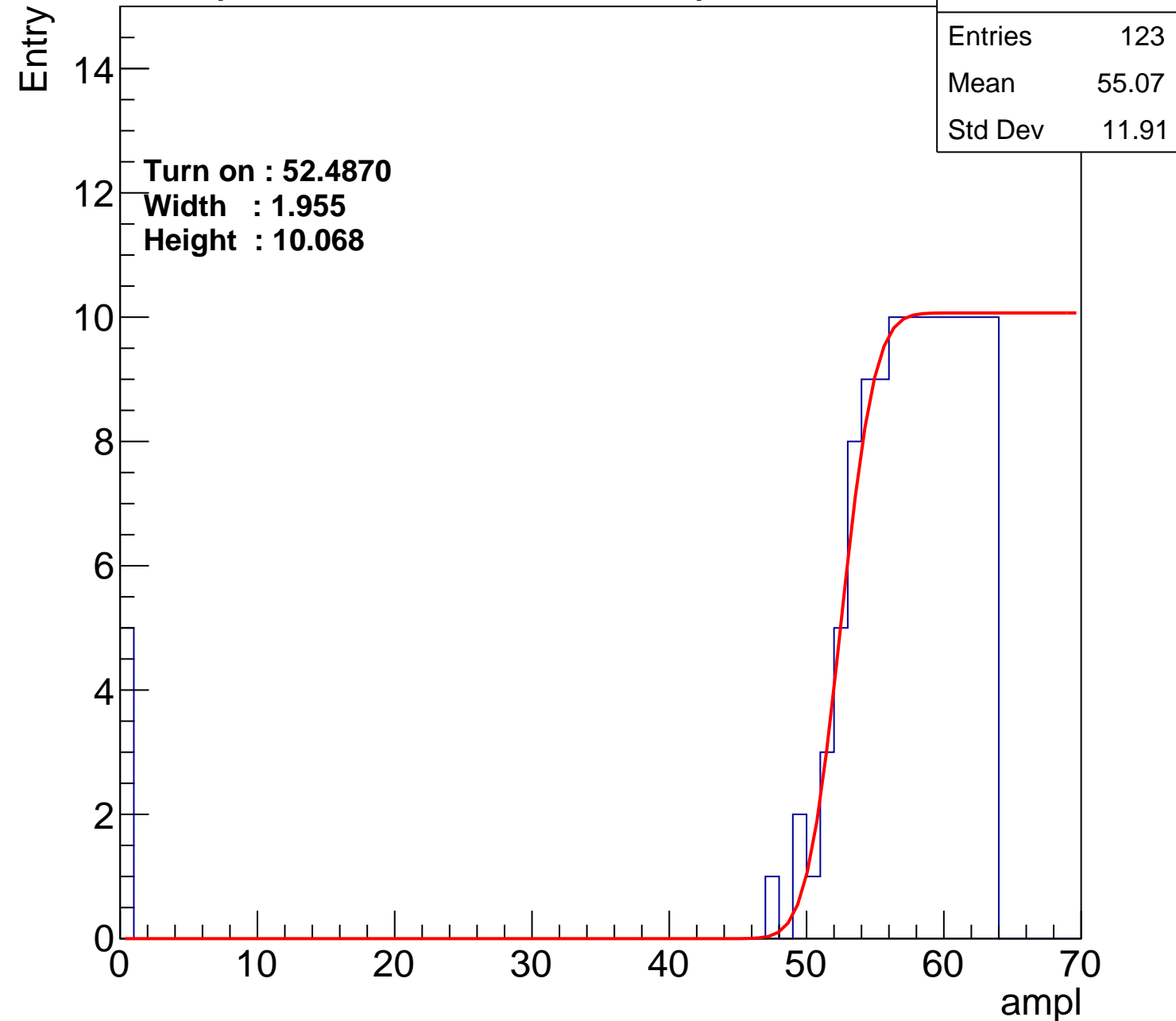
Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U24-ch55

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.24
Std Dev	9.257

Turn on : 51.2157

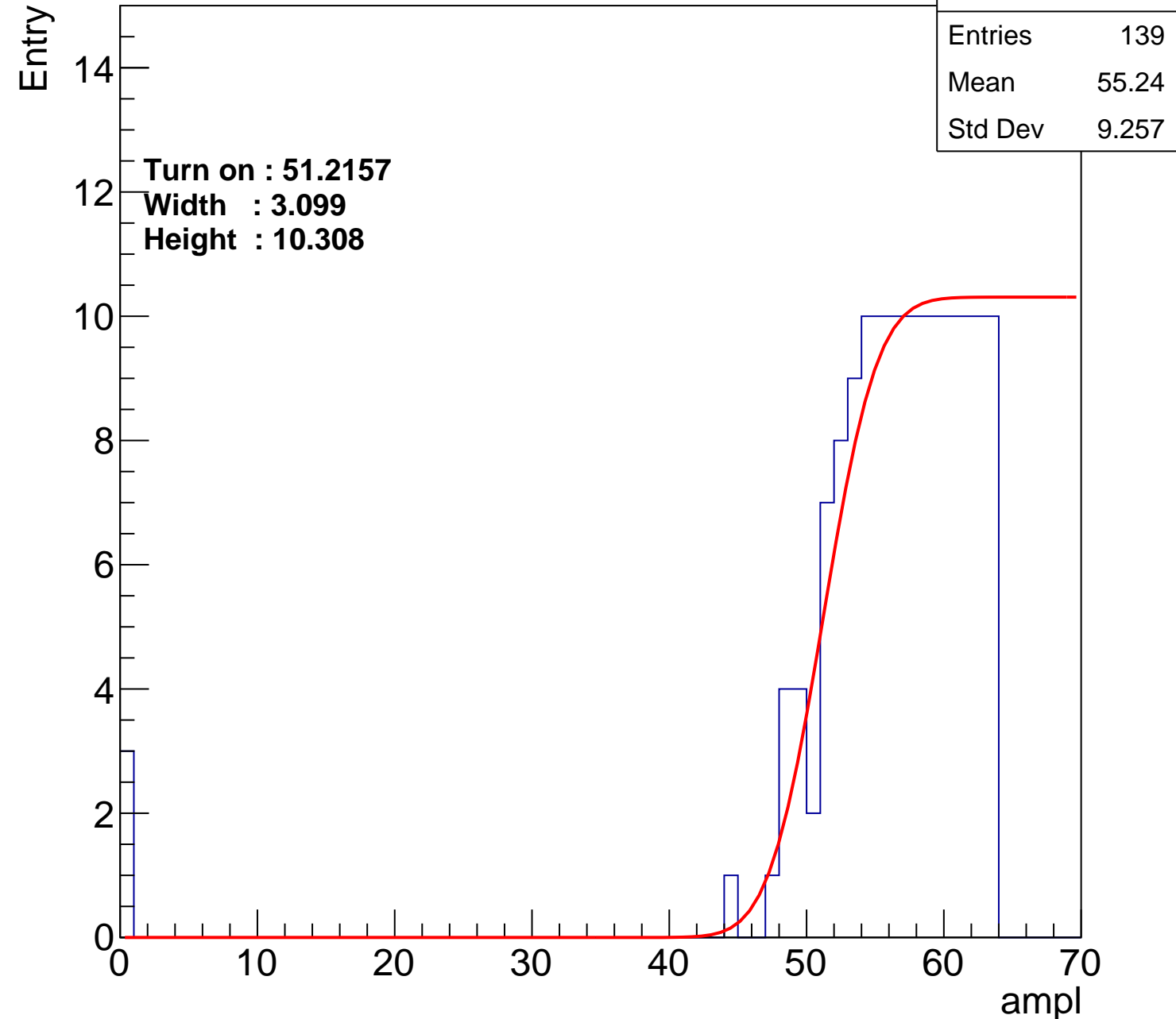
Width : 3.099

Height : 10.308

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch56

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.27
Std Dev	10.66

Turn on : 51.5447

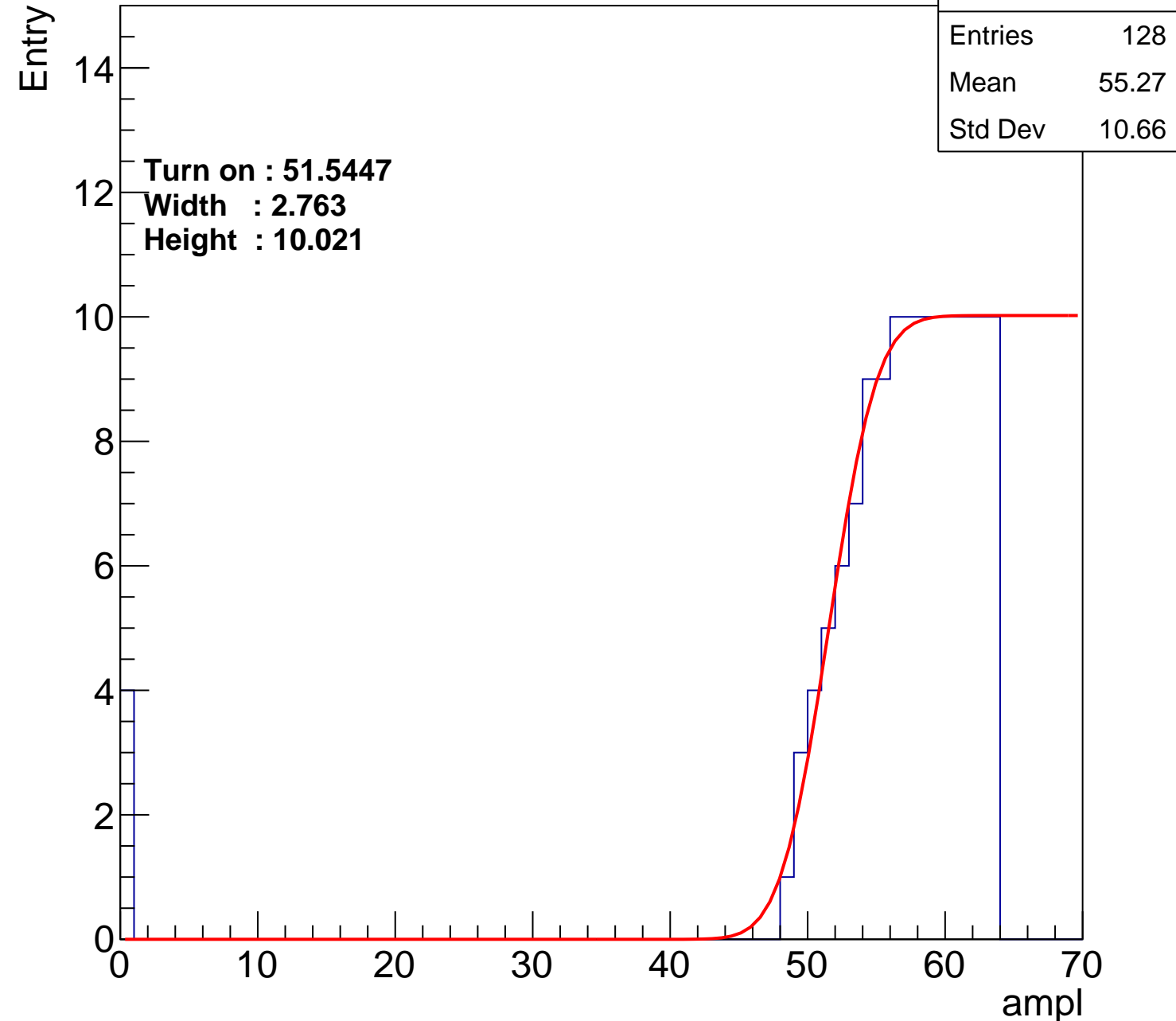
Width : 2.763

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch57

calib_packv5_040323_1717.root, FC#2, port C3

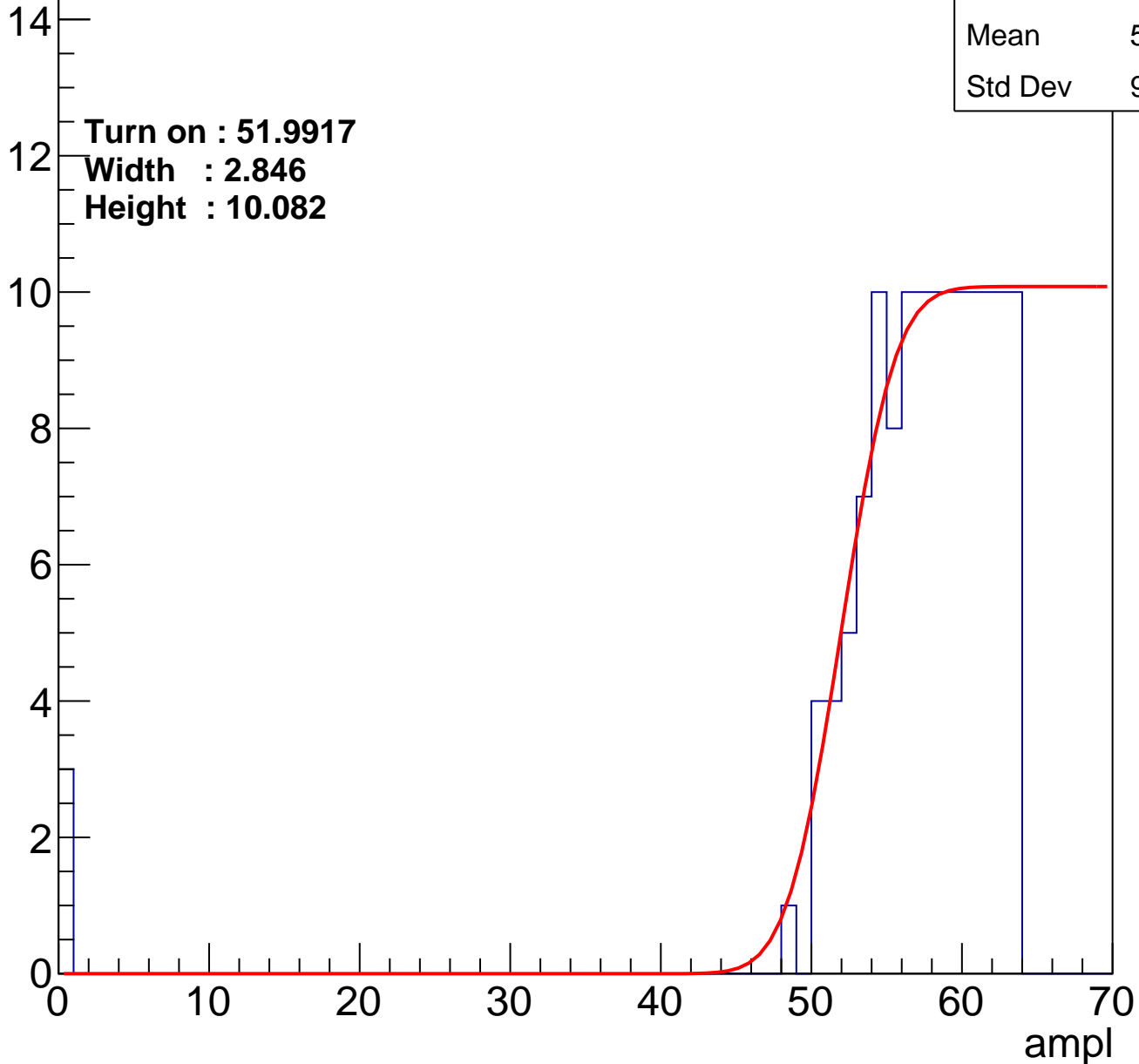
Entries	122
Mean	55.93
Std Dev	9.627

Turn on : 51.9917

Width : 2.846

Height : 10.082

Entry



B0L103S, U24-ch58

calib_packv5_040323_1717.root, FC#2, port C3

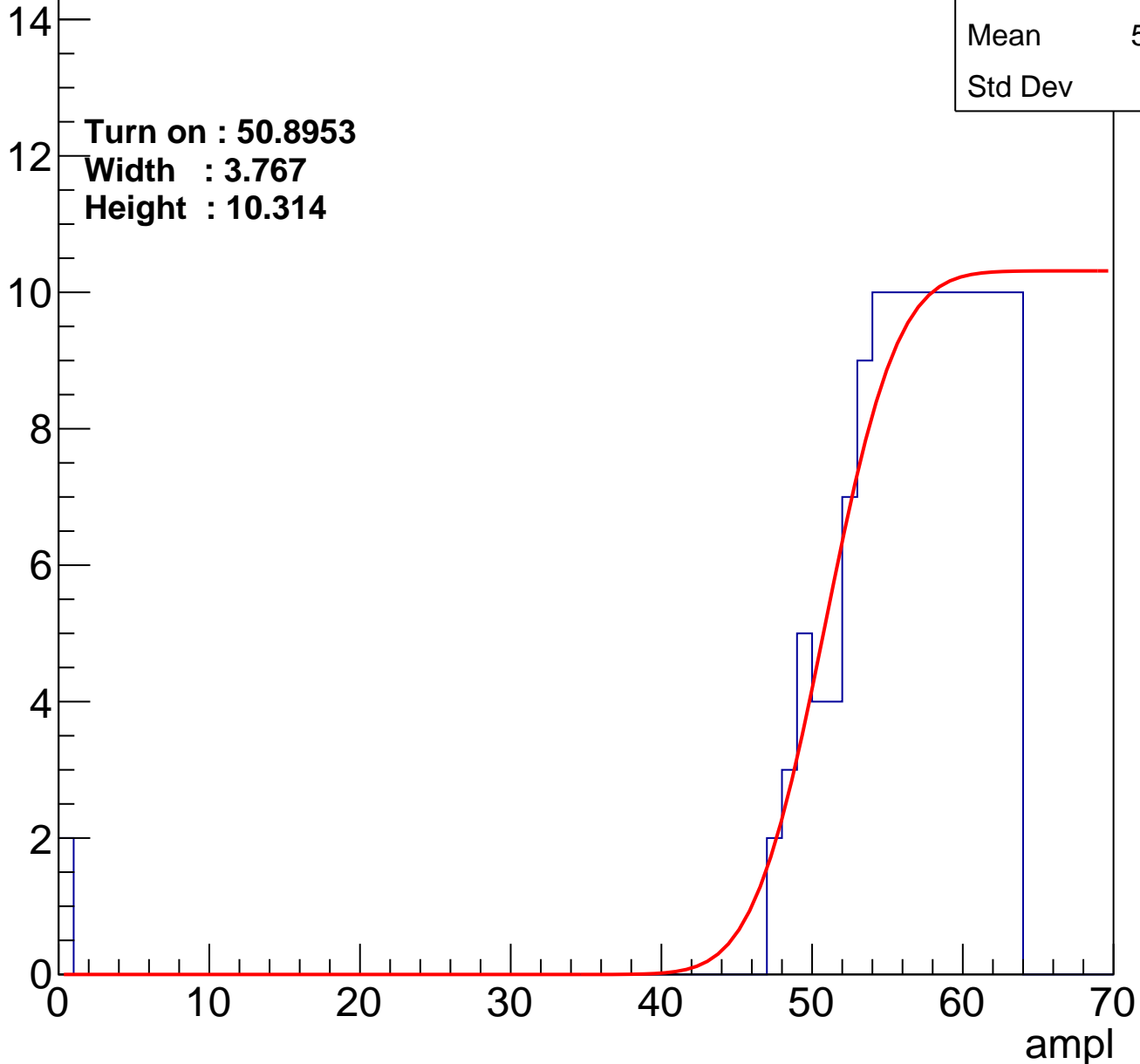
Entries	136
Mean	55.72
Std Dev	8.02

Turn on : 50.8953

Width : 3.767

Height : 10.314

Entry



B0L103S, U24-ch59

calib_packv5_040323_1717.root, FC#2, port C3

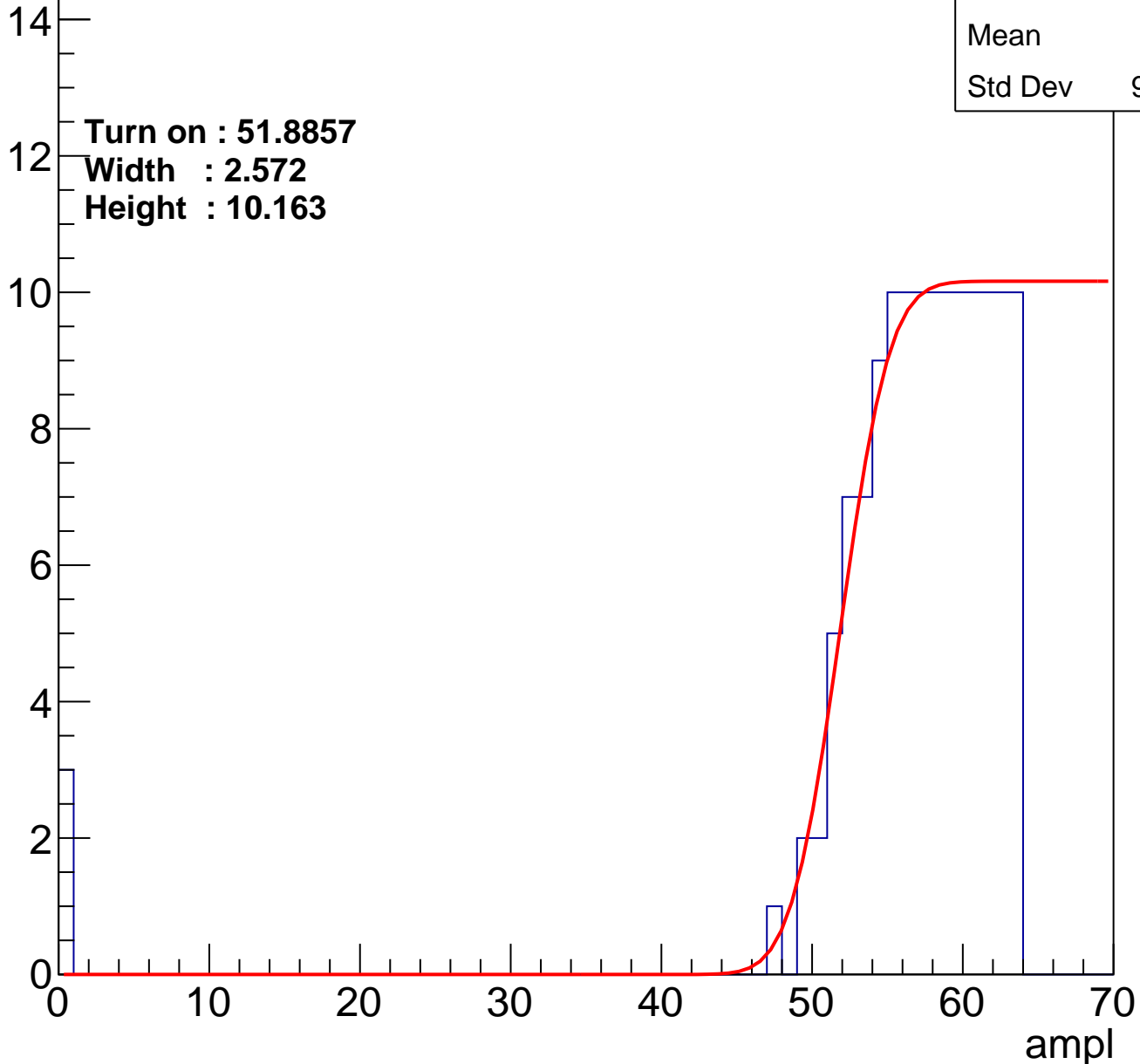
Entries	126
Mean	55.8
Std Dev	9.512

Turn on : 51.8857

Width : 2.572

Height : 10.163

Entry



B0L103S, U24-ch60

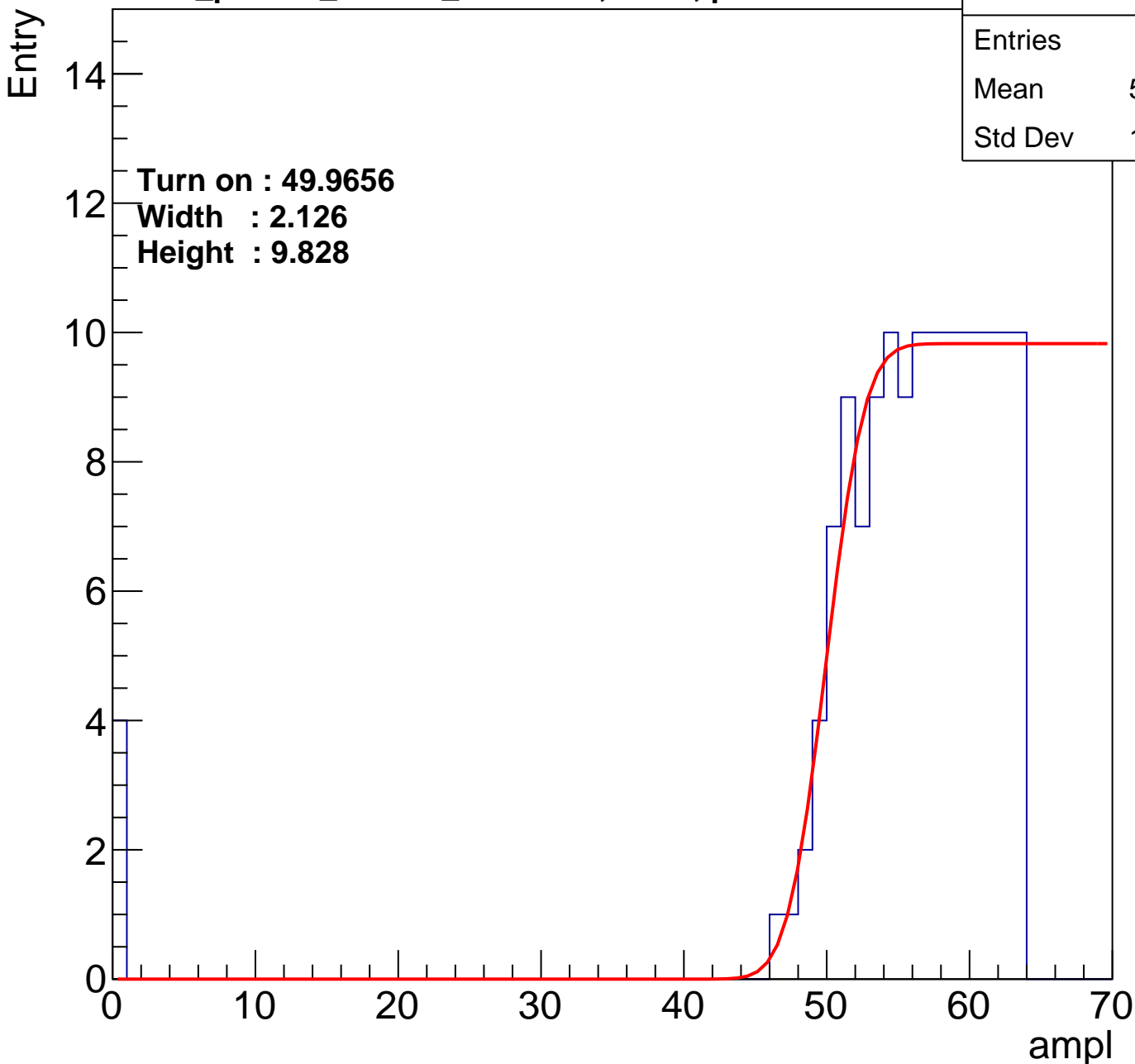
calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	54.76
Std Dev	10.22

Turn on : 49.9656

Width : 2.126

Height : 9.828



B0L103S, U24-ch61

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	56.18
Std Dev	9.979

Turn on : 53.3883

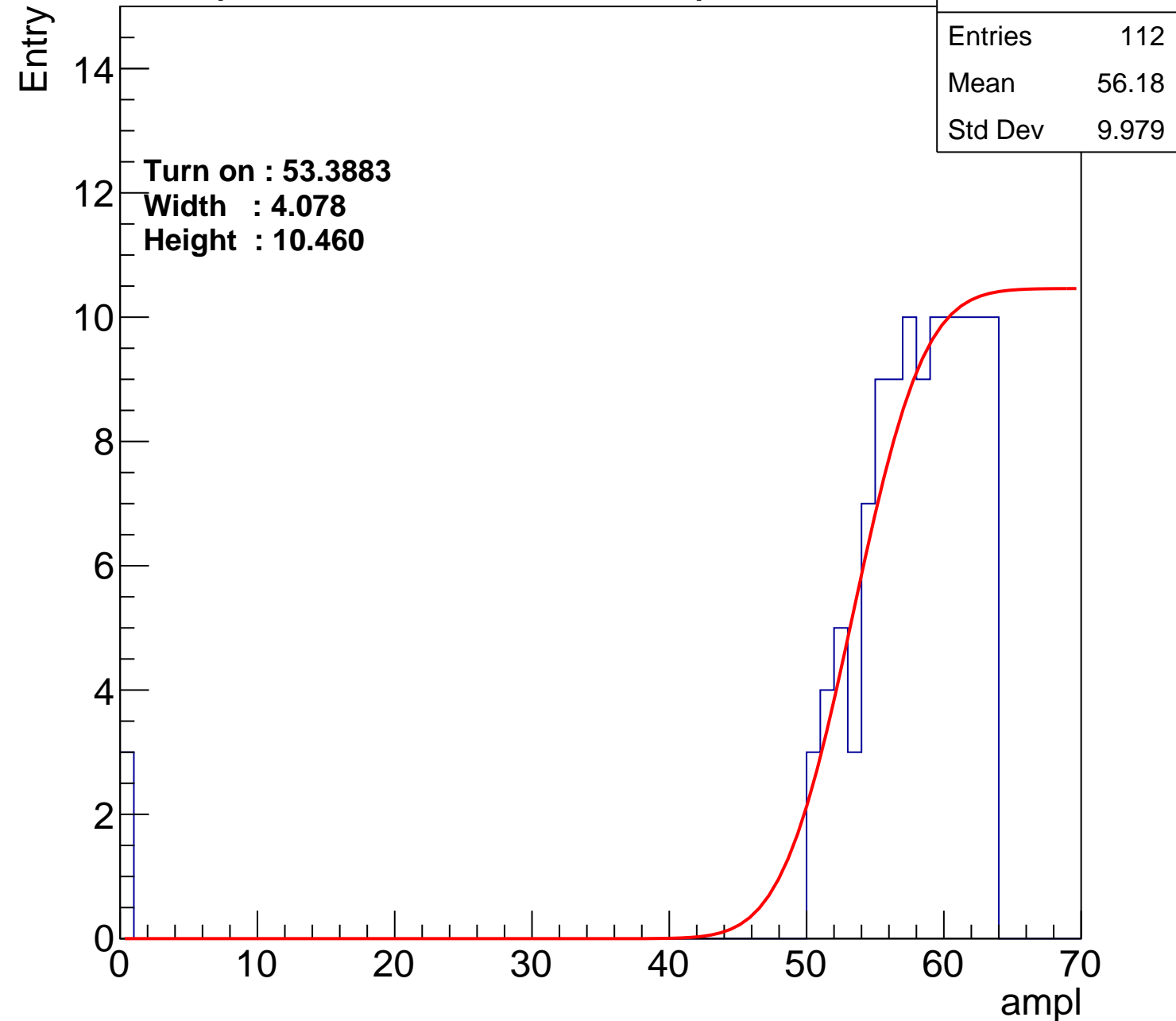
Width : 4.078

Height : 10.460

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch62

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.53
Std Dev	8.373

Turn on : 53.0874

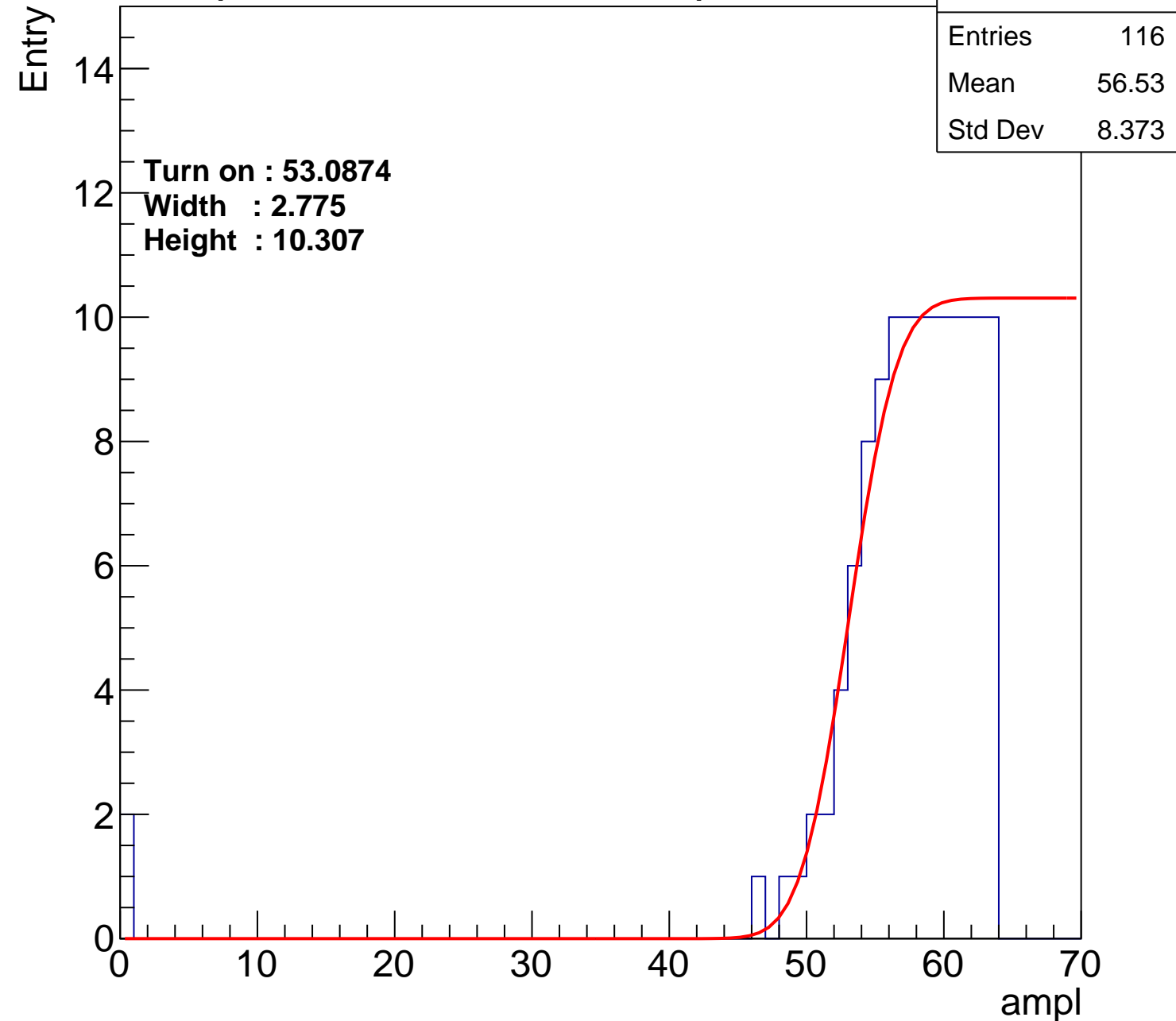
Width : 2.775

Height : 10.307

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch63

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	54.94
Std Dev	11.74

Turn on : 51.7835

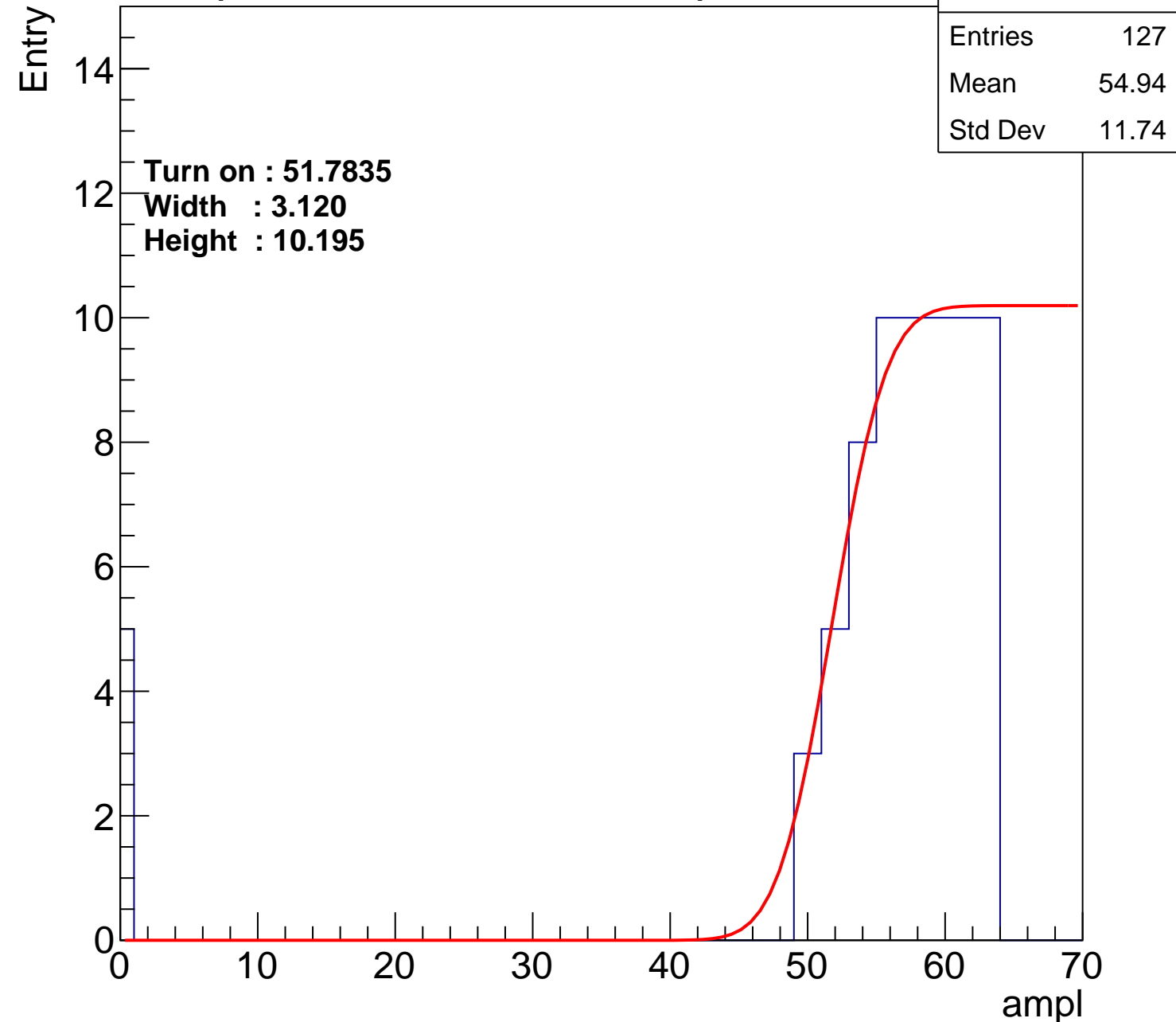
Width : 3.120

Height : 10.195

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch64

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	55.47
Std Dev	11.01

Turn on : 52.1702

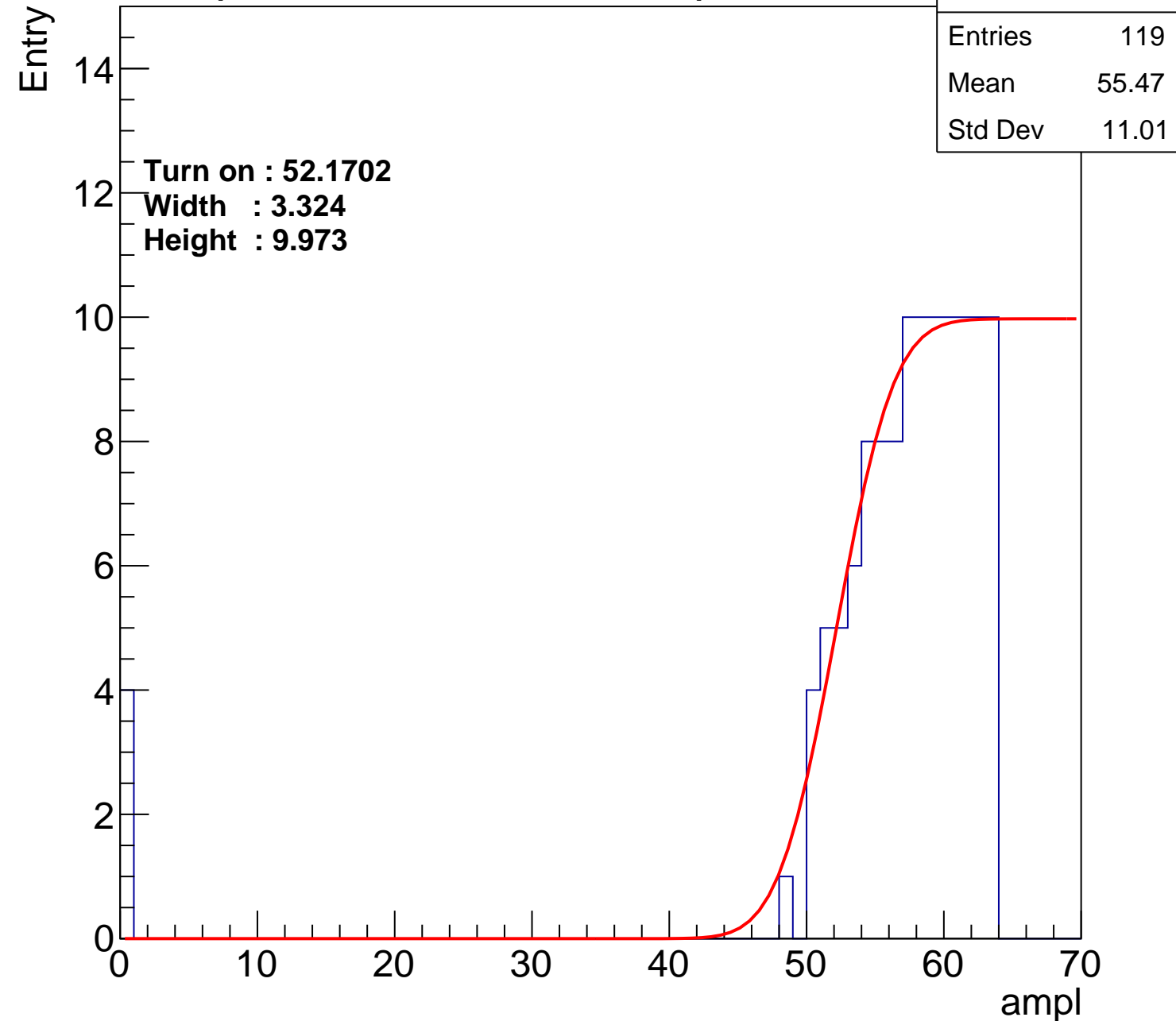
Width : 3.324

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch65

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	55.46
Std Dev	10.91

Turn on : 52.3416

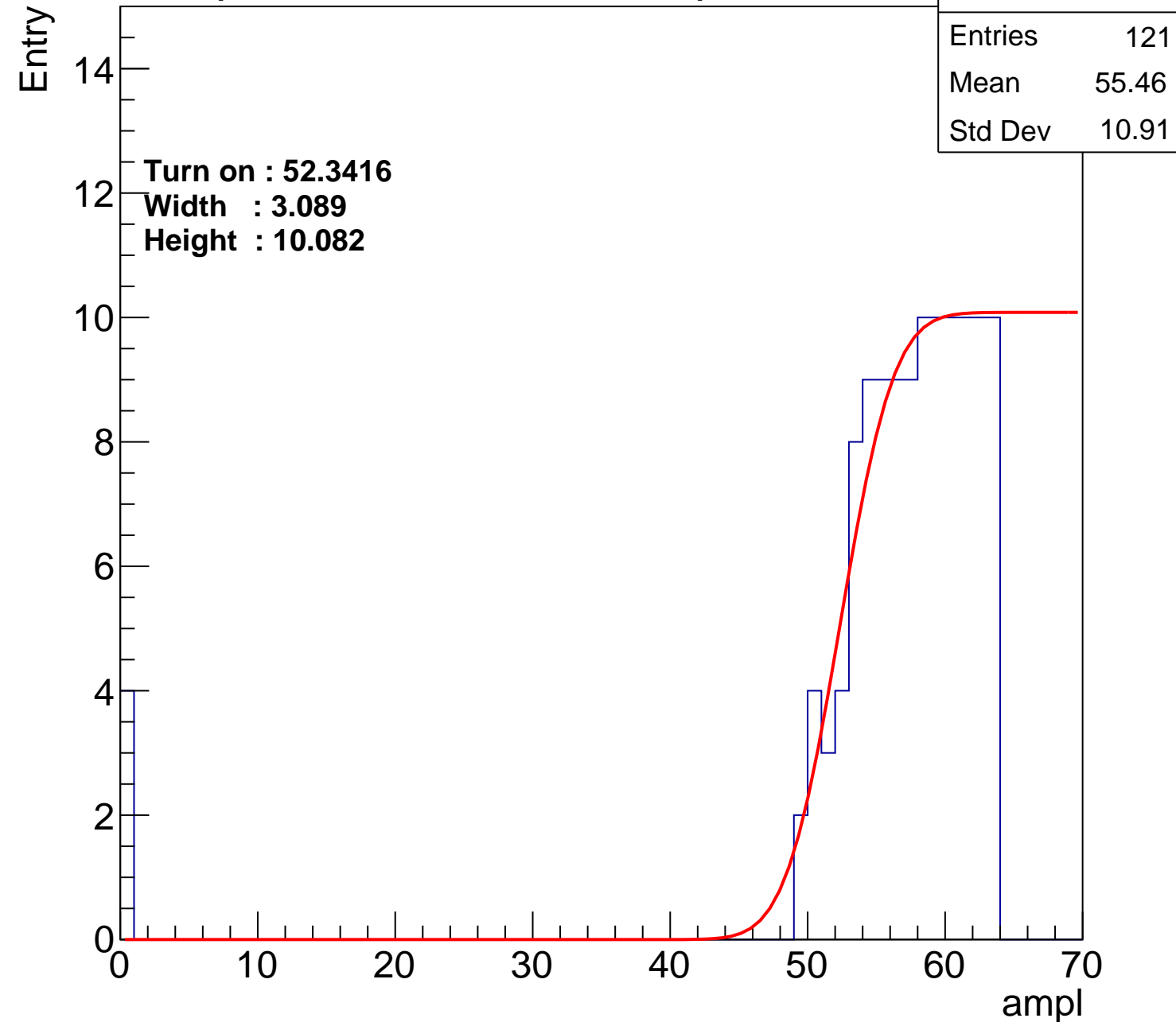
Width : 3.089

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch66

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.38
Std Dev	10.63

Turn on : 51.6726

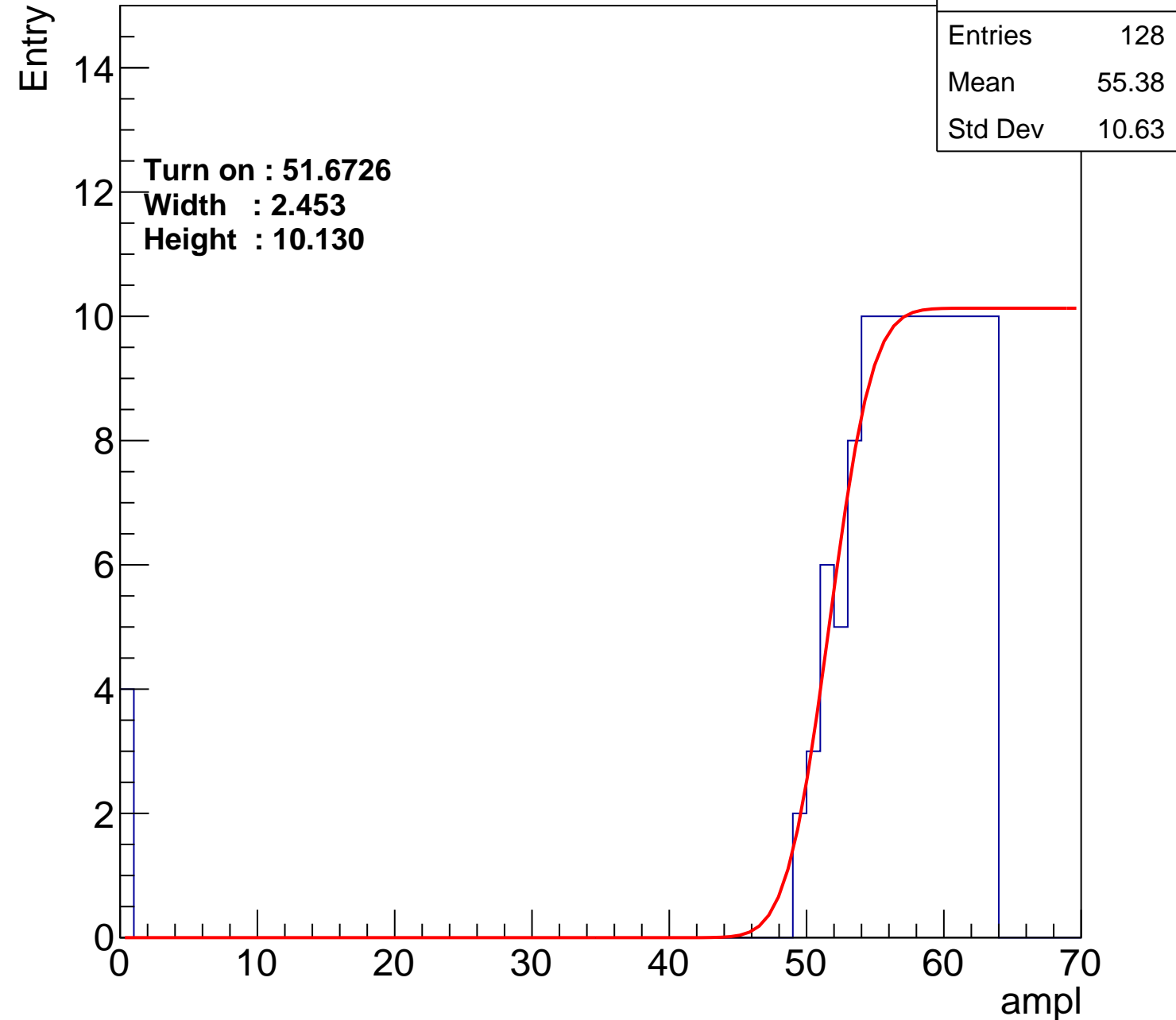
Width : 2.453

Height : 10.130

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch67

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.73
Std Dev	9.635

Turn on : 52.4077

Width : 4.112

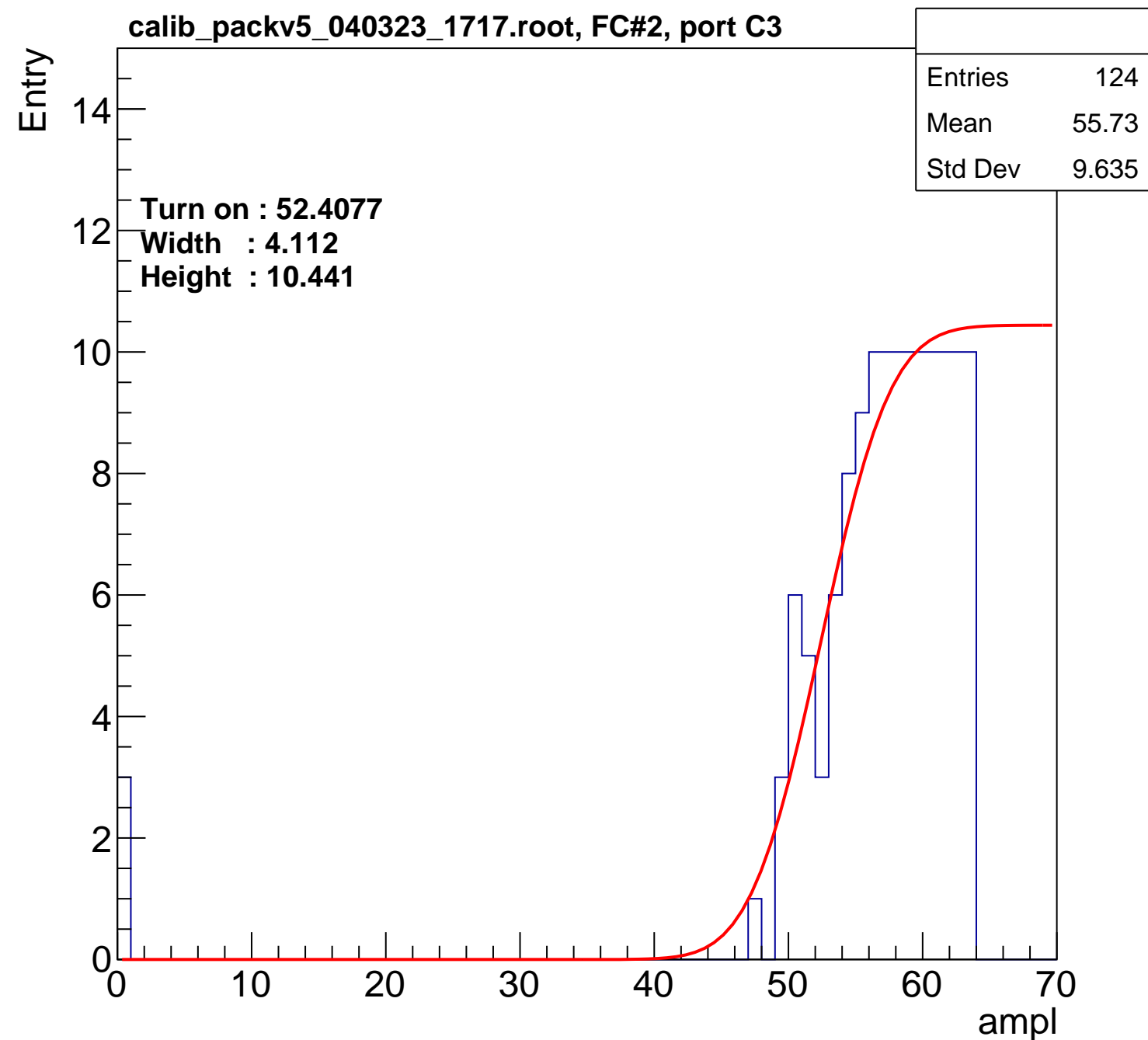
Height : 10.441

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U24-ch68

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	55.89
Std Dev	8.084

Turn on : 51.6041

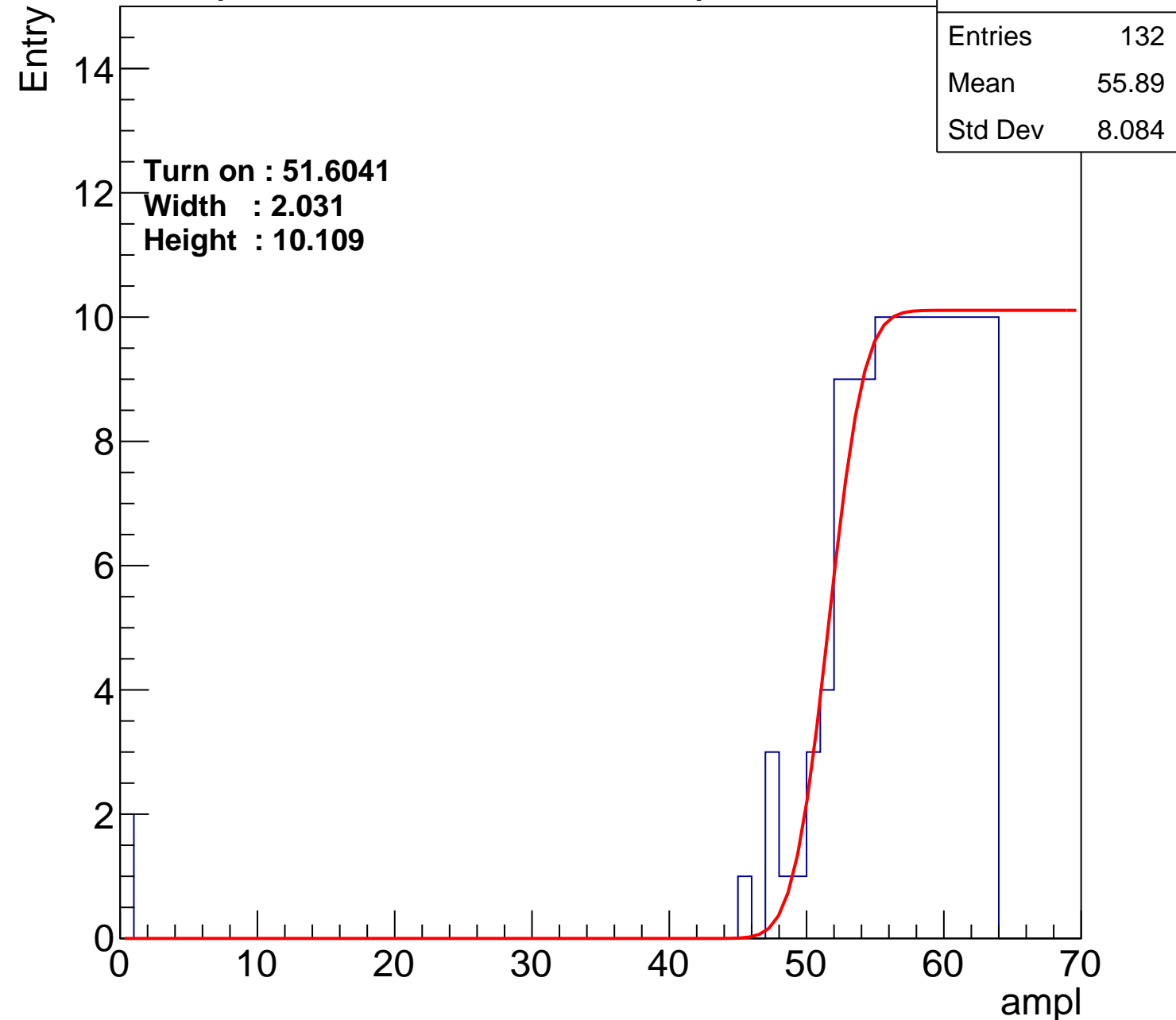
Width : 2.031

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch69

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.69
Std Dev	7.918

Turn on : 50.5920

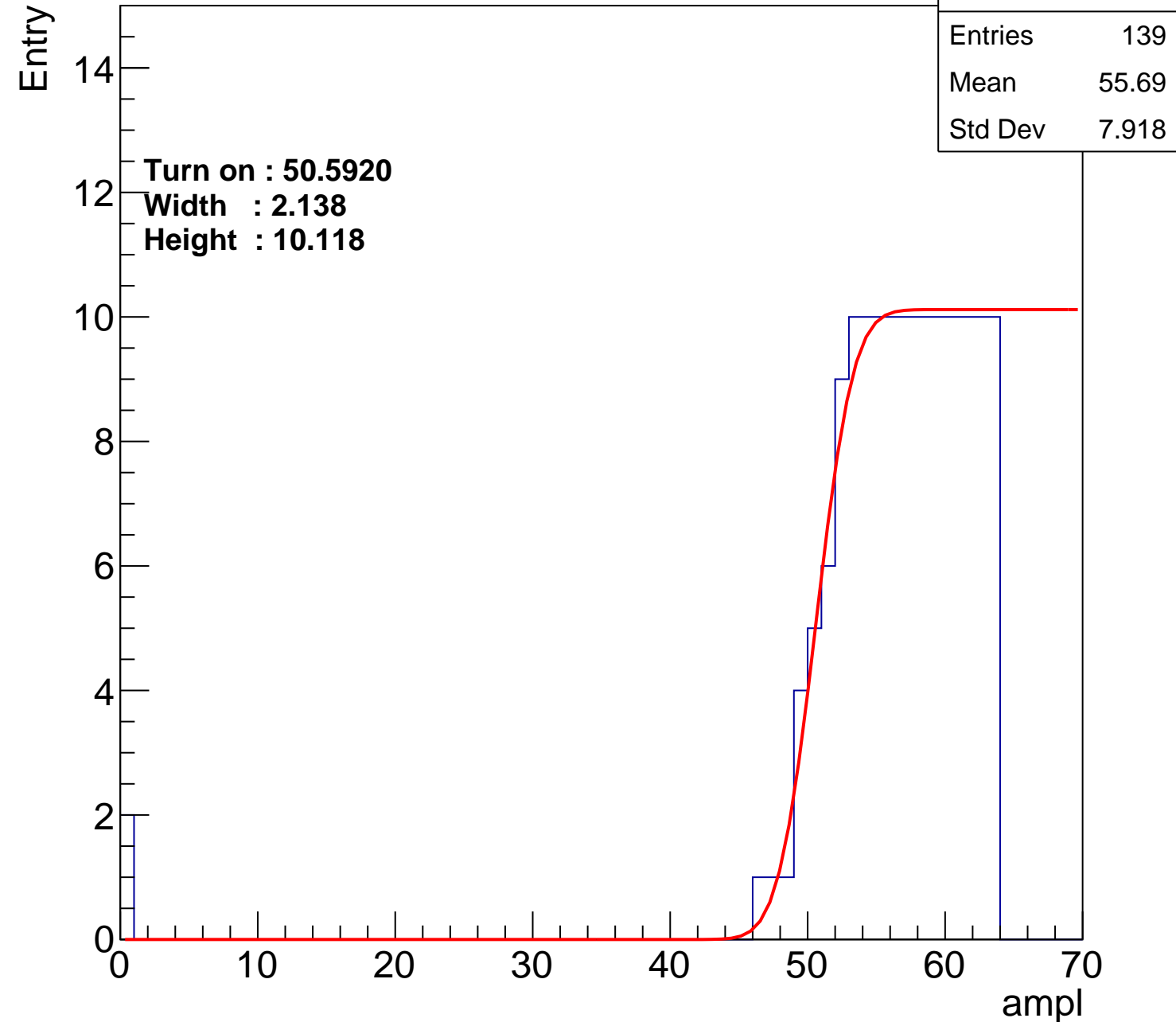
Width : 2.138

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch70

calib_packv5_040323_1717.root, FC#2, port C3

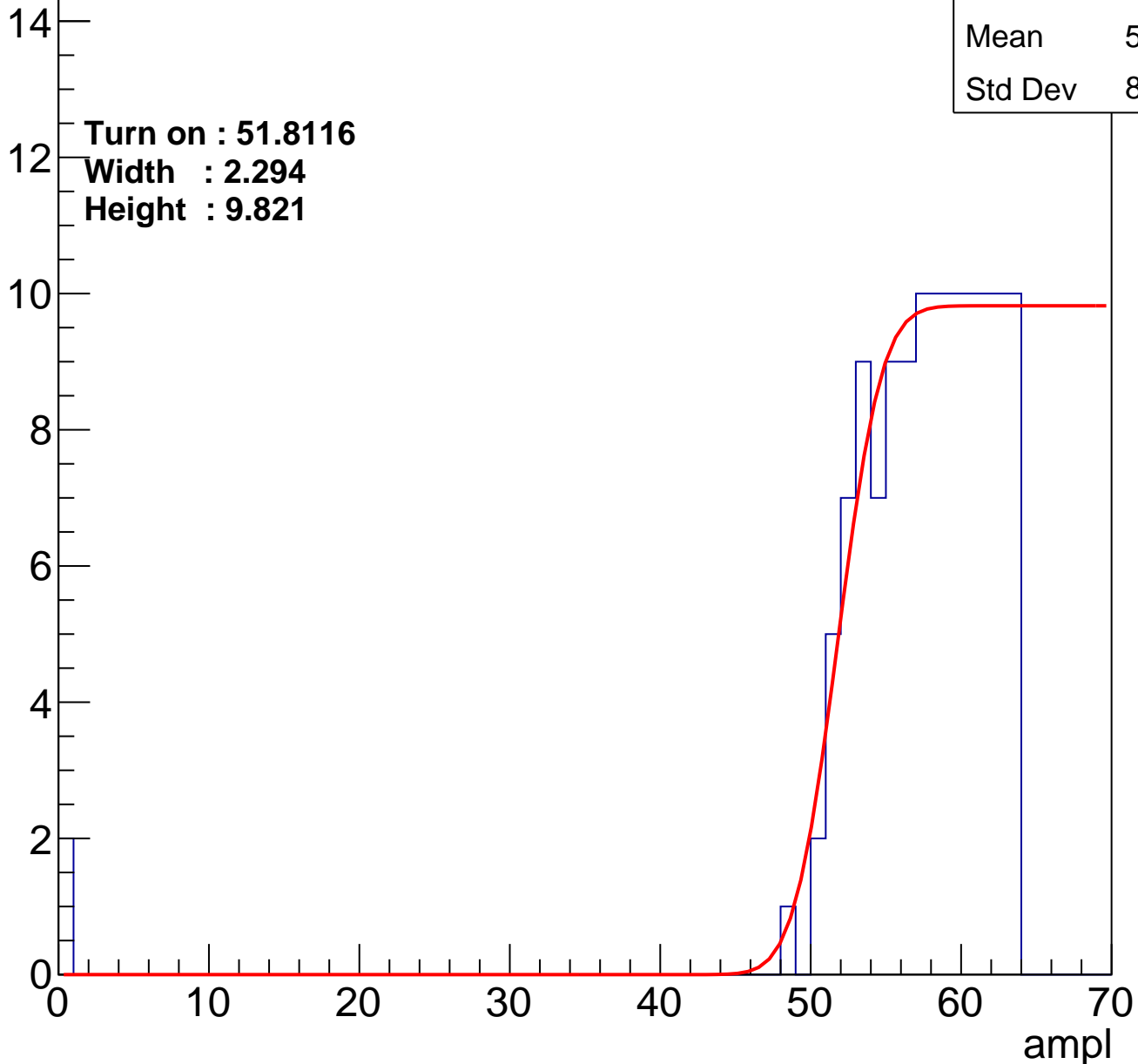
Entries	121
Mean	56.37
Std Dev	8.205

Turn on : 51.8116

Width : 2.294

Height : 9.821

Entry



B0L103S, U24-ch71

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.15
Std Dev	10.61

Turn on : 51.3761

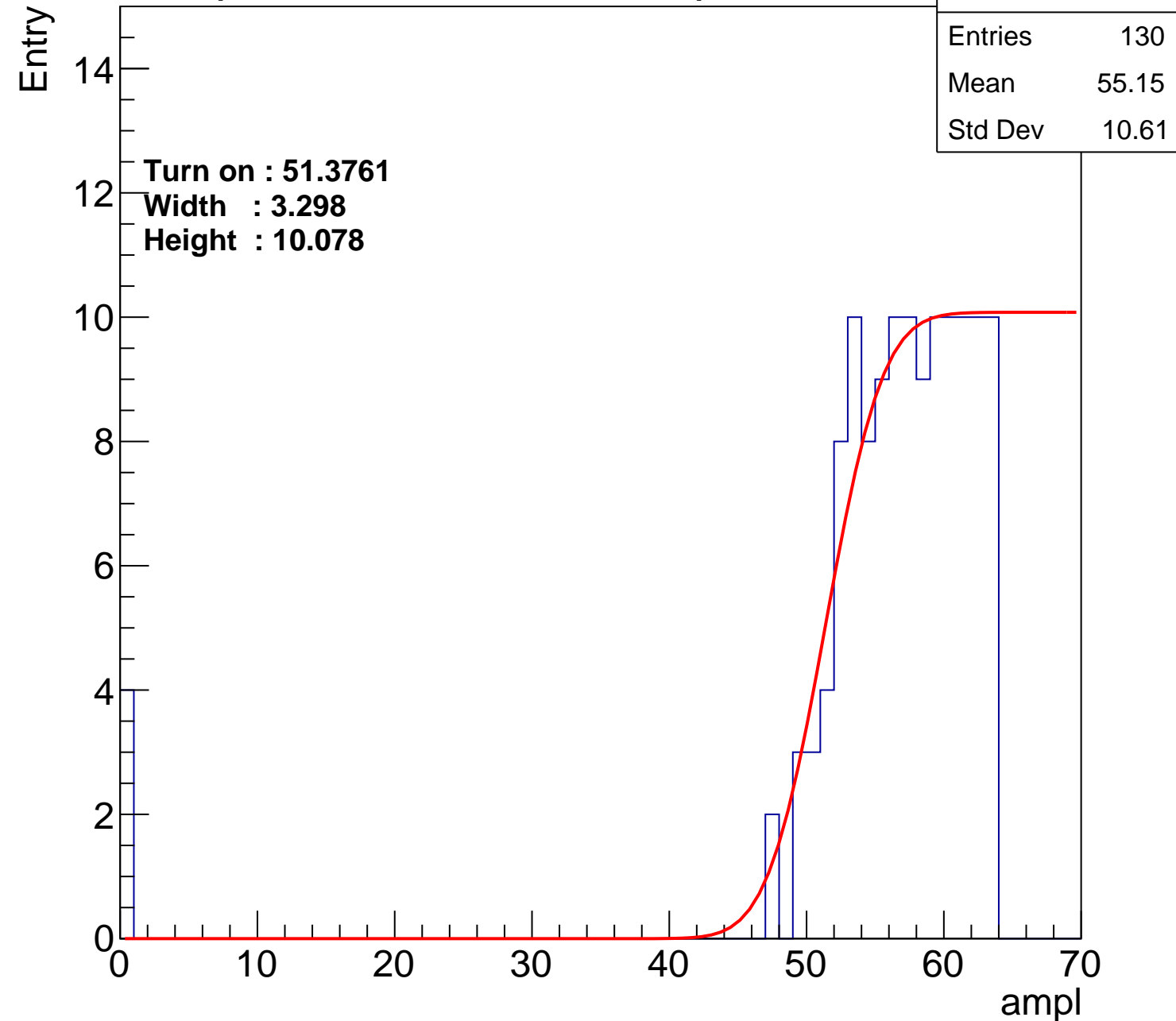
Width : 3.298

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch72

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.72
Std Dev	11.58

Turn on : 51.5834

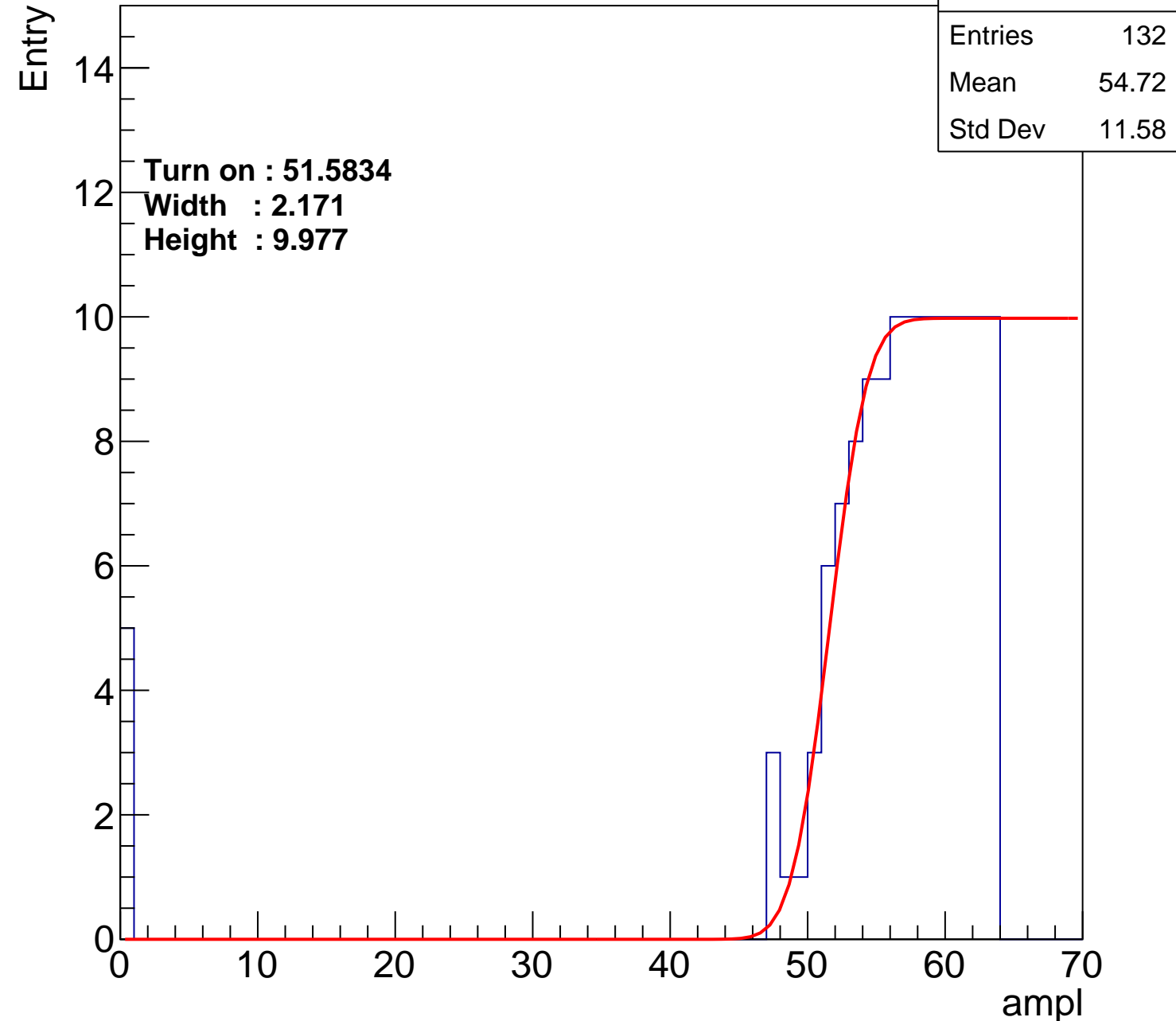
Width : 2.171

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch73

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	54.11
Std Dev	13.65

Turn on : 52.2319

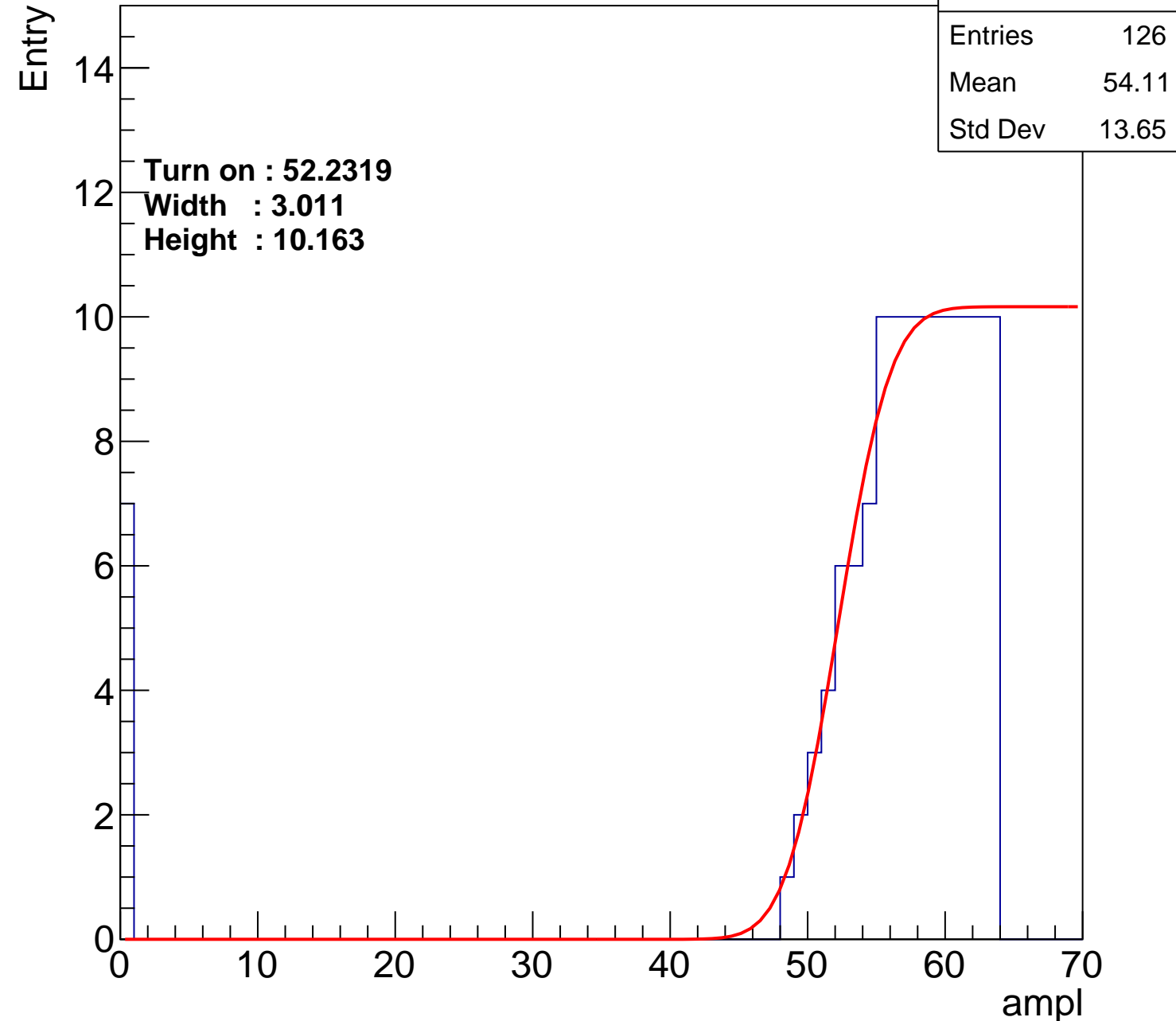
Width : 3.011

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch74

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	55.36
Std Dev	10.95

Turn on : 51.0612

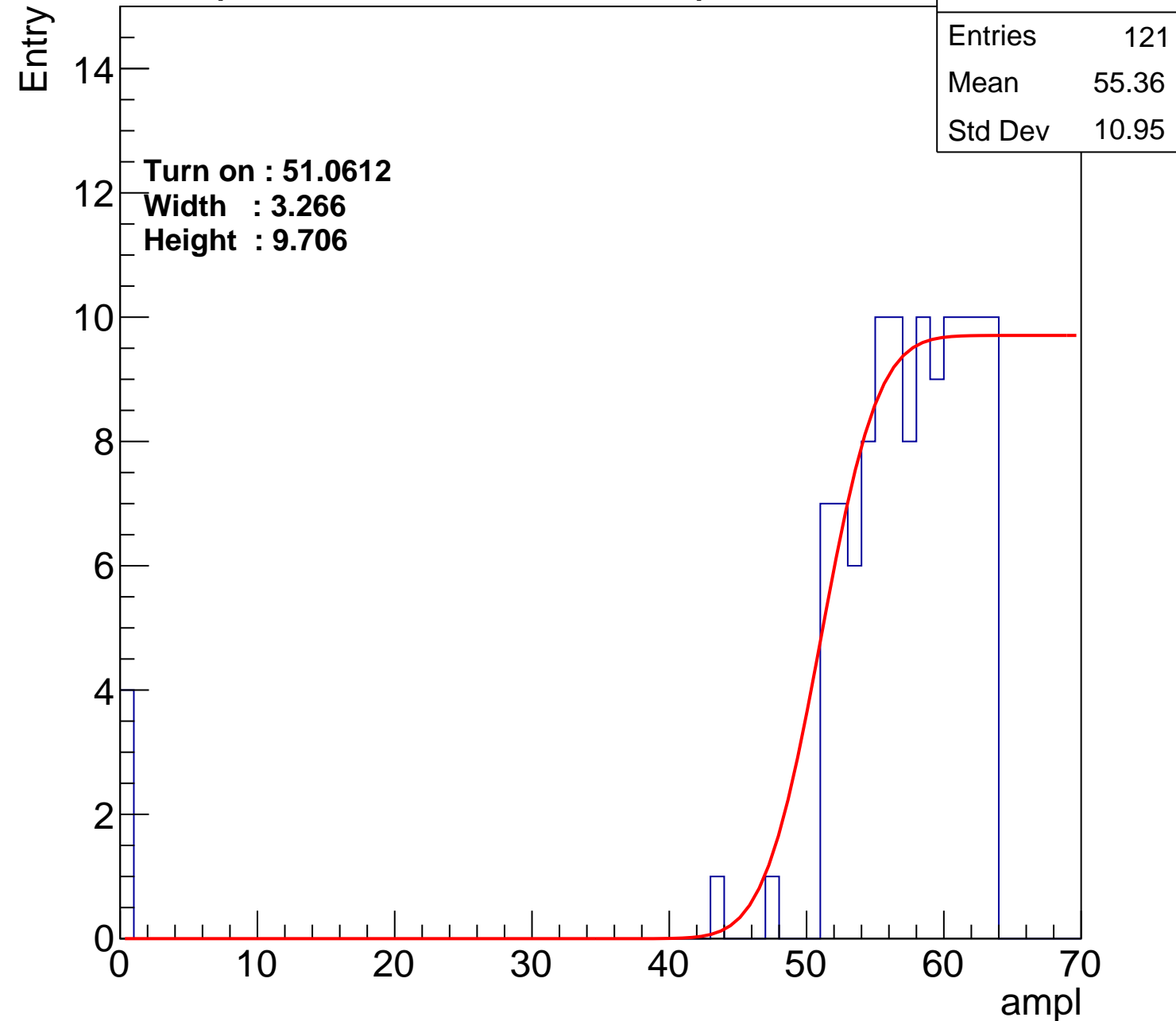
Width : 3.266

Height : 9.706

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch75

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	56.6
Std Dev	8.513

Turn on : 53.6301

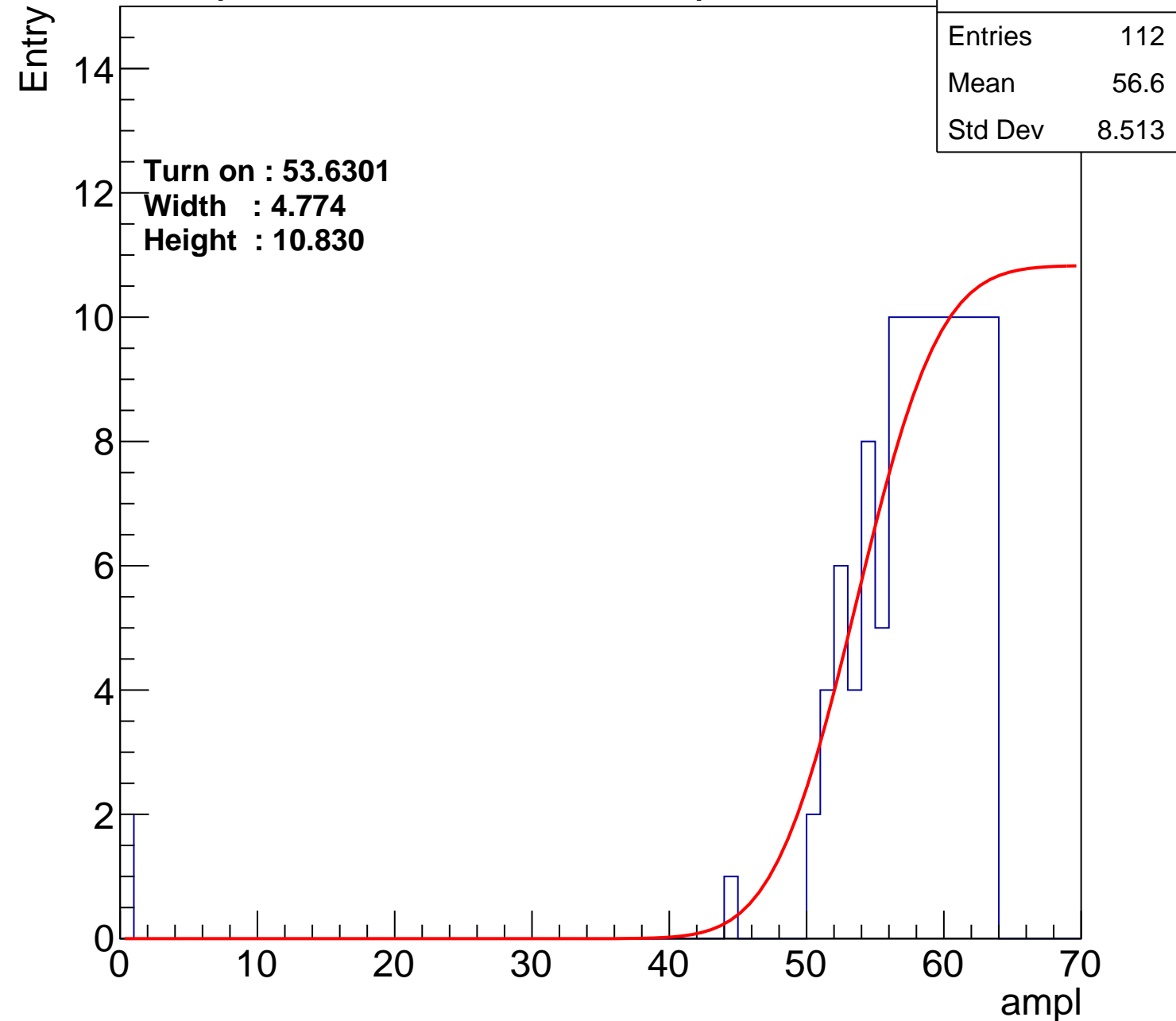
Width : 4.774

Height : 10.830

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch76

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	53.66
Std Dev	15.31

Turn on : 54.2714

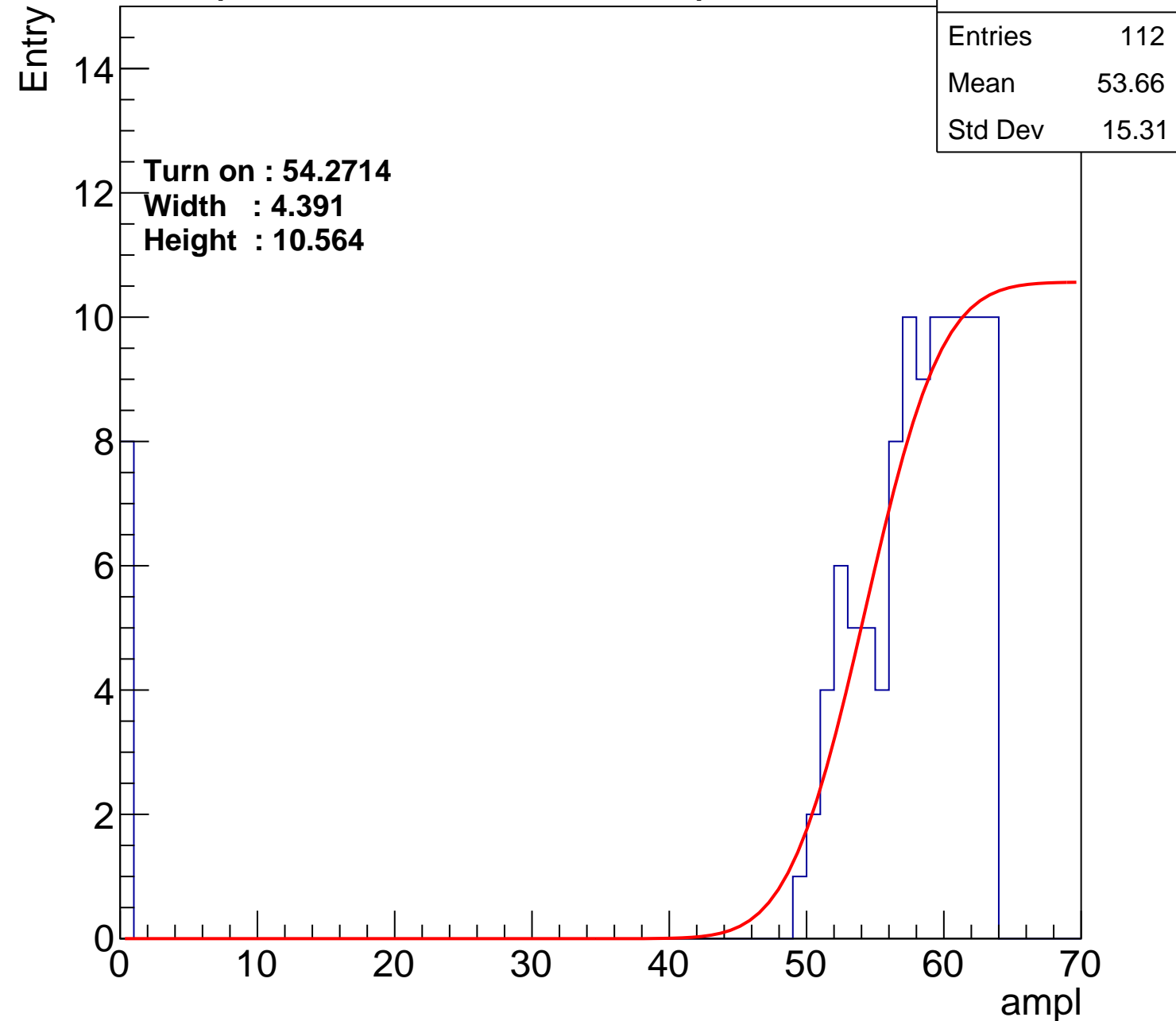
Width : 4.391

Height : 10.564

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch77

calib_packv5_040323_1717.root, FC#2, port C3

Entries	110
Mean	56.33
Std Dev	10.02

Turn on : 53.2540

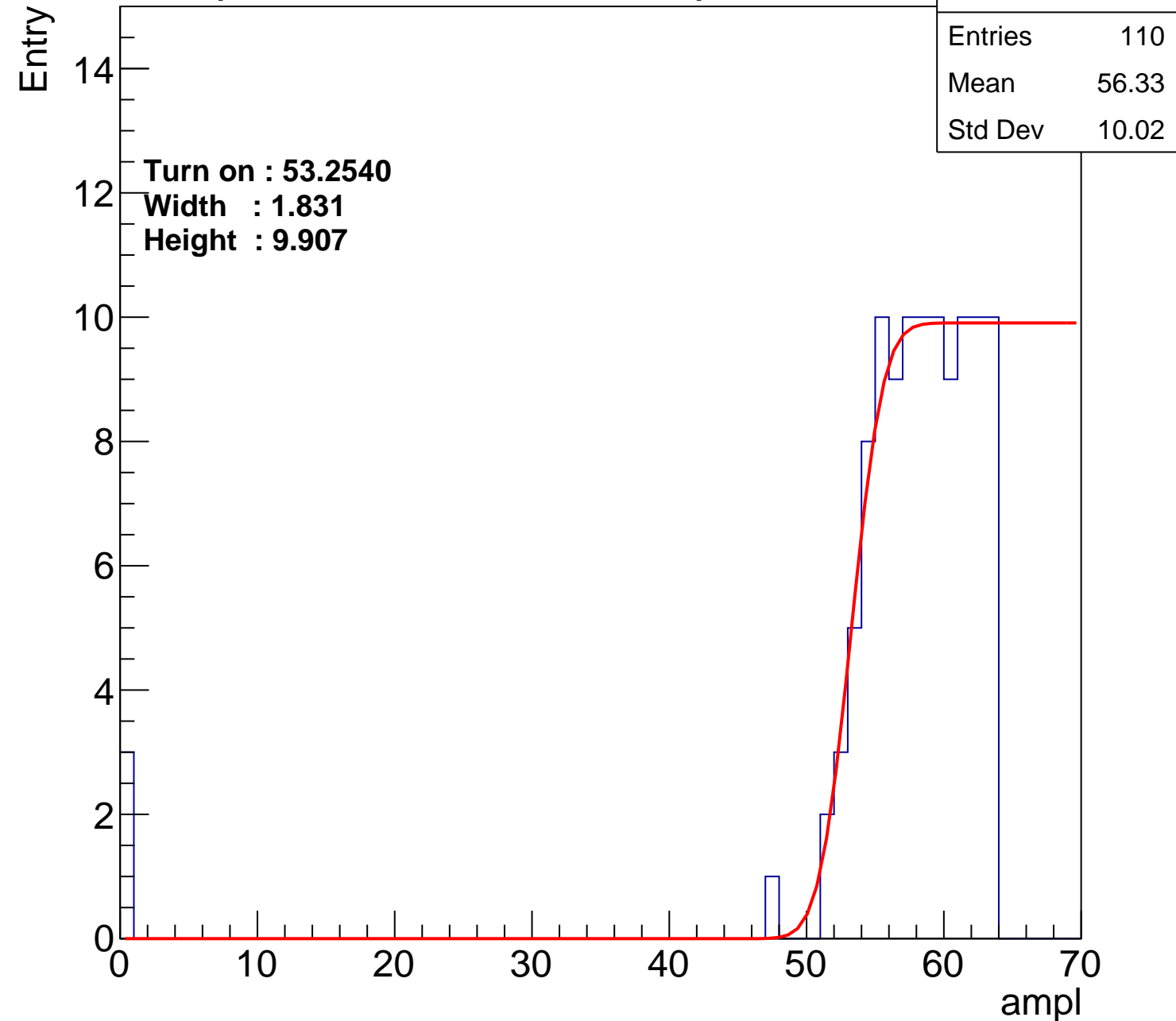
Width : 1.831

Height : 9.907

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch78

calib_packv5_040323_1717.root, FC#2, port C3

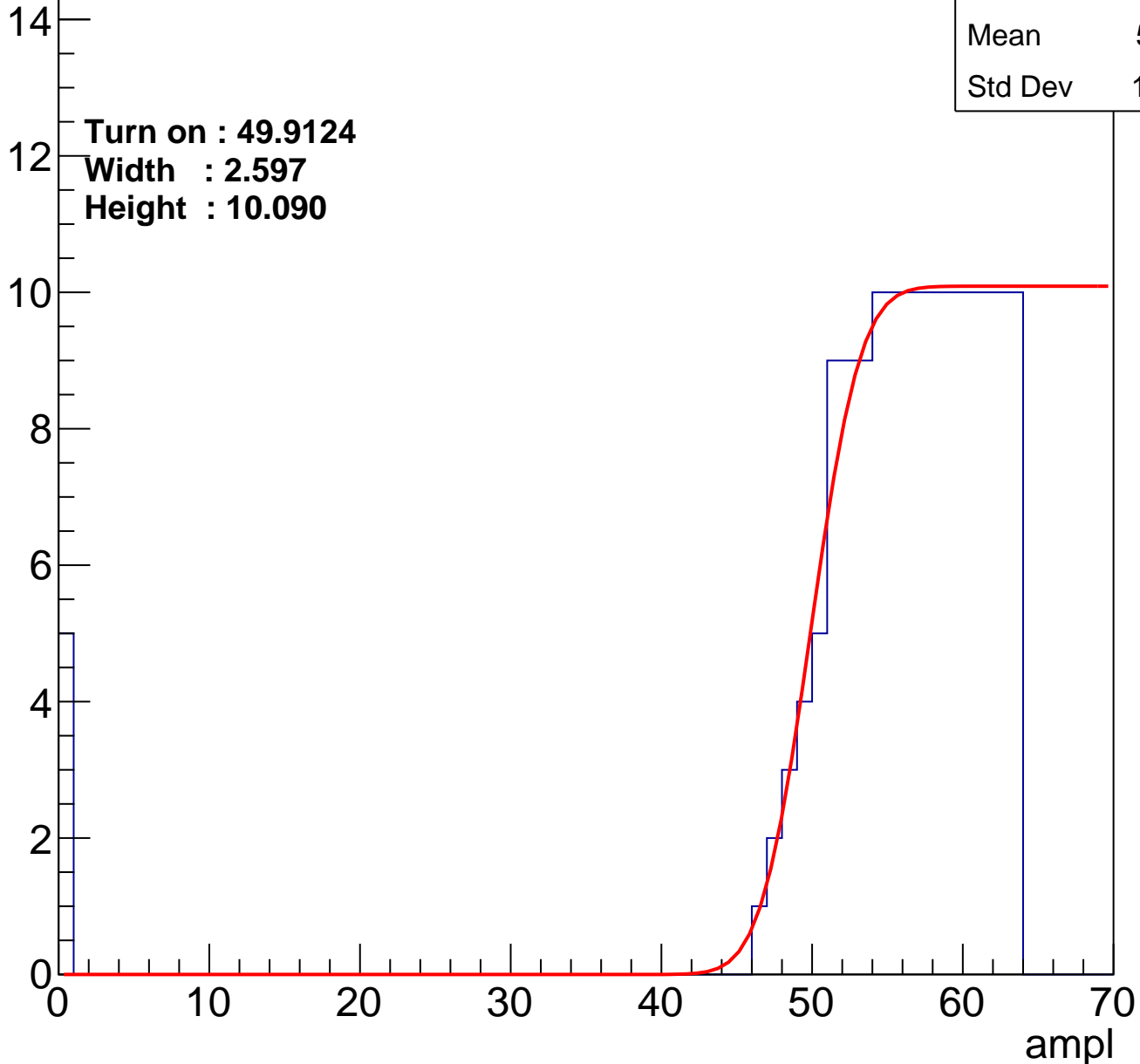
Entries	147
Mean	54.31
Std Dev	11.06

Turn on : 49.9124

Width : 2.597

Height : 10.090

Entry



B0L103S, U24-ch79

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	56.15
Std Dev	8.263

Turn on : 52.0442

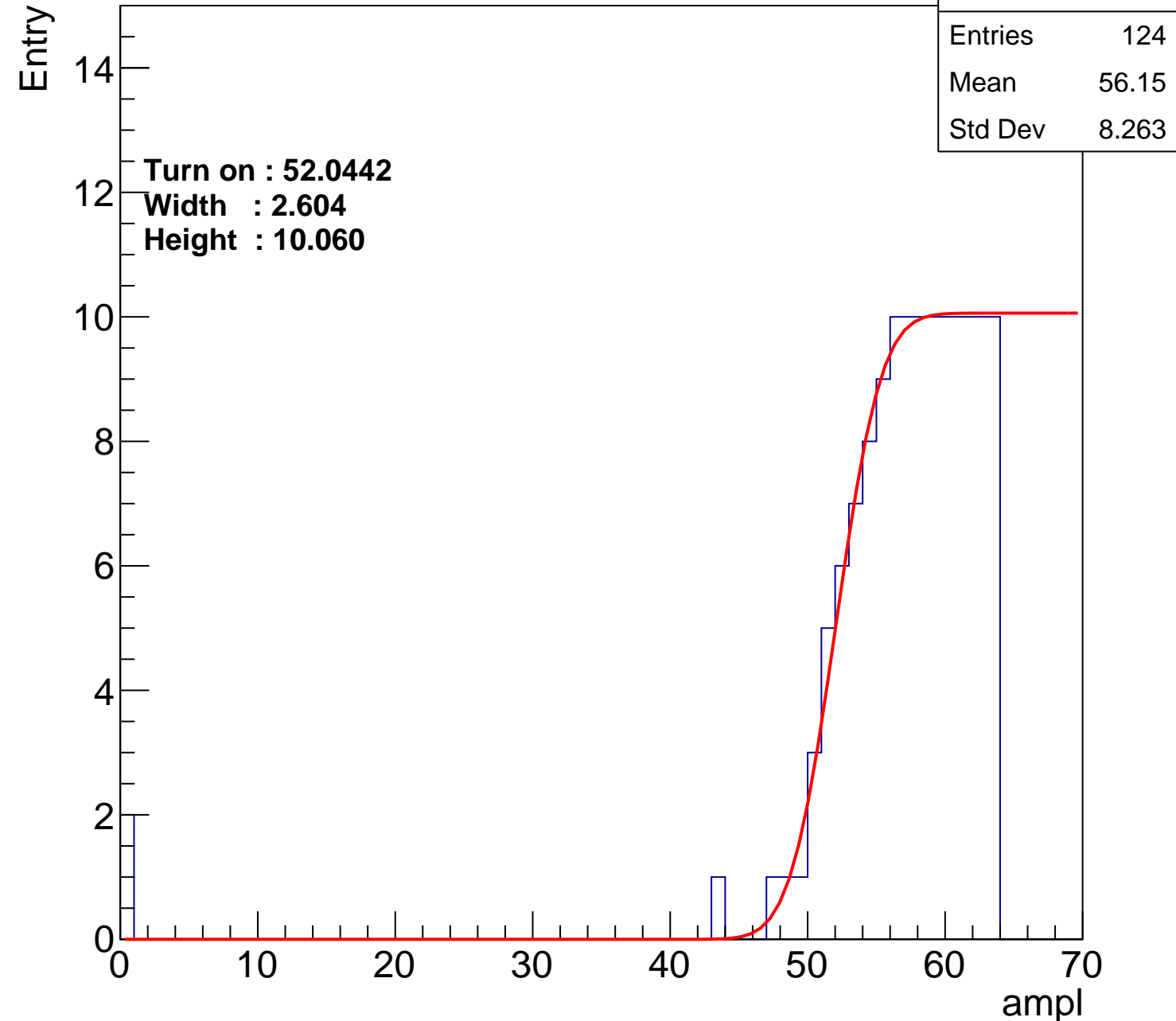
Width : 2.604

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch80

calib_packv5_040323_1717.root, FC#2, port C3

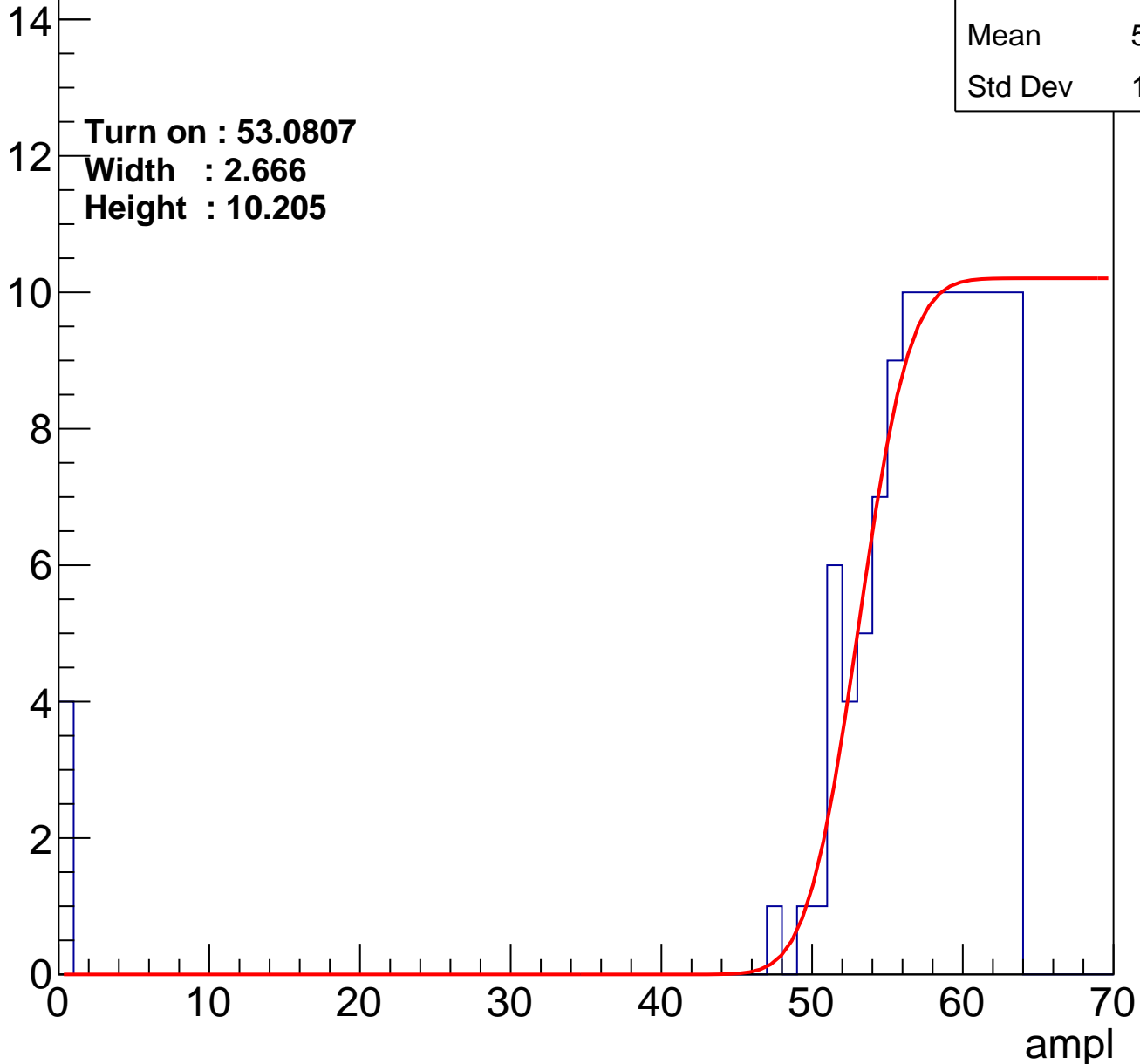
Entries	118
Mean	55.58
Std Dev	11.04

Turn on : 53.0807

Width : 2.666

Height : 10.205

Entry



B0L103S, U24-ch81

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	56.09
Std Dev	8.126

Turn on : 51.2736

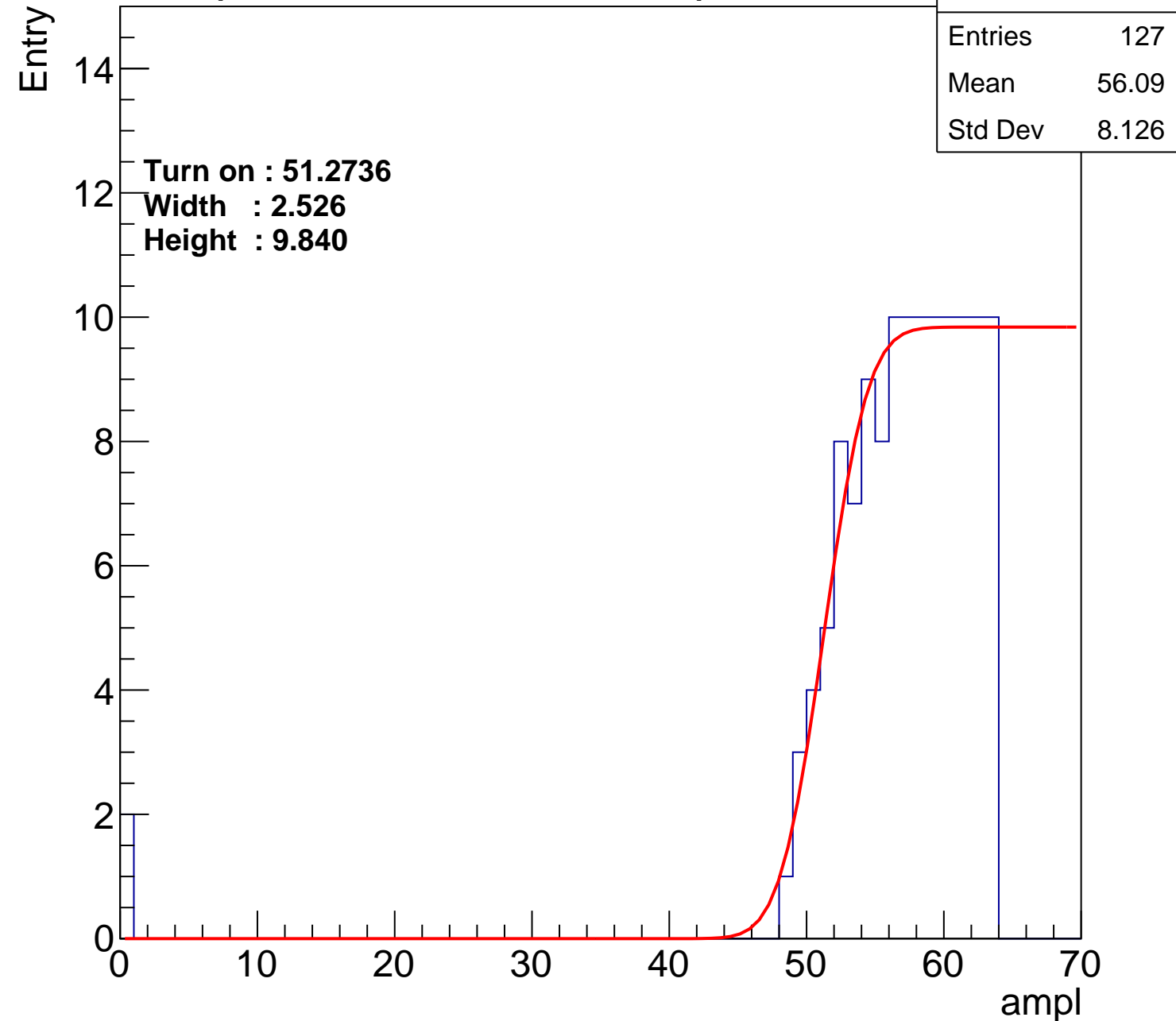
Width : 2.526

Height : 9.840

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch82

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.28
Std Dev	12.43

Turn on : 51.6734

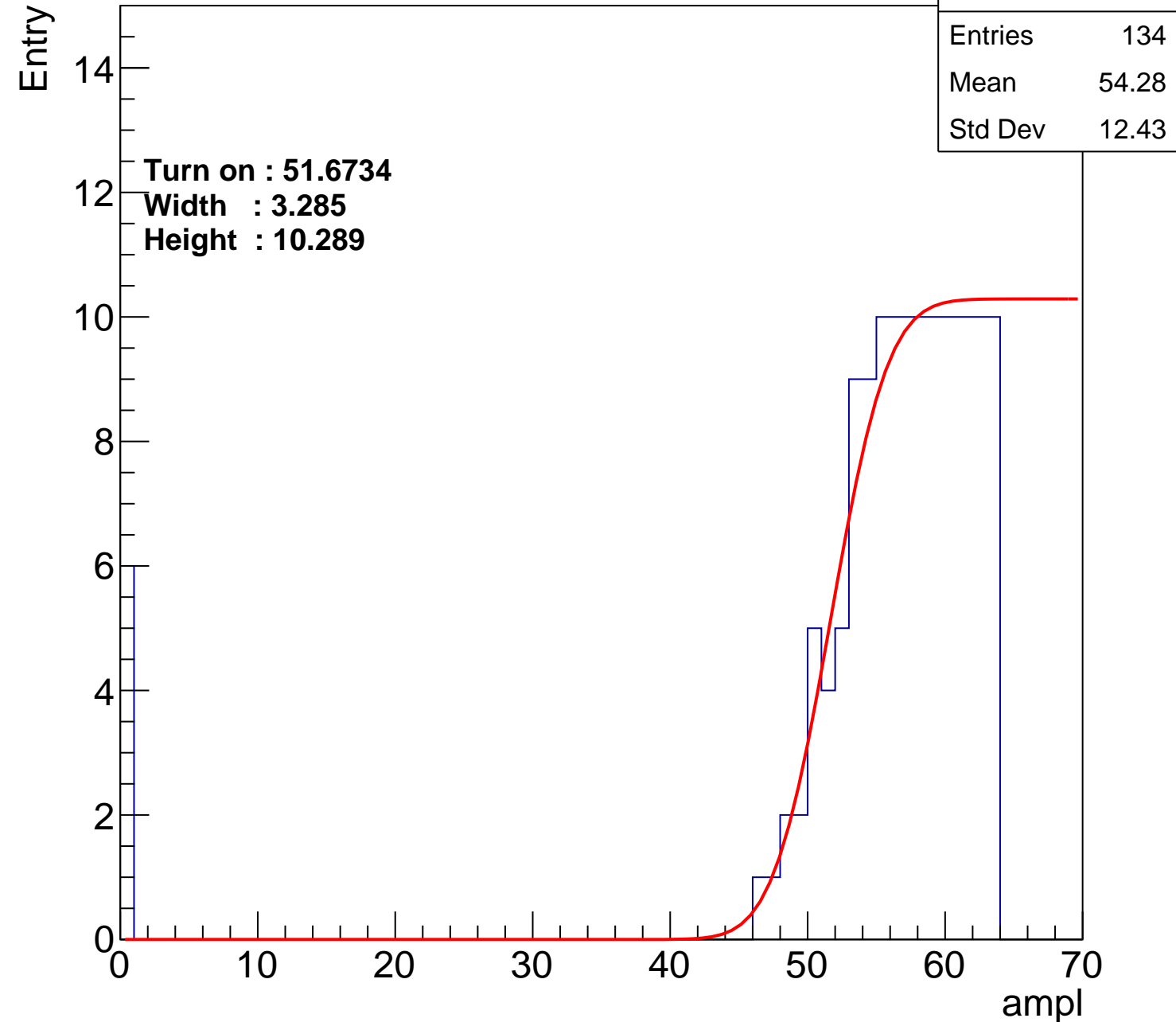
Width : 3.285

Height : 10.289

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch83

calib_packv5_040323_1717.root, FC#2, port C3

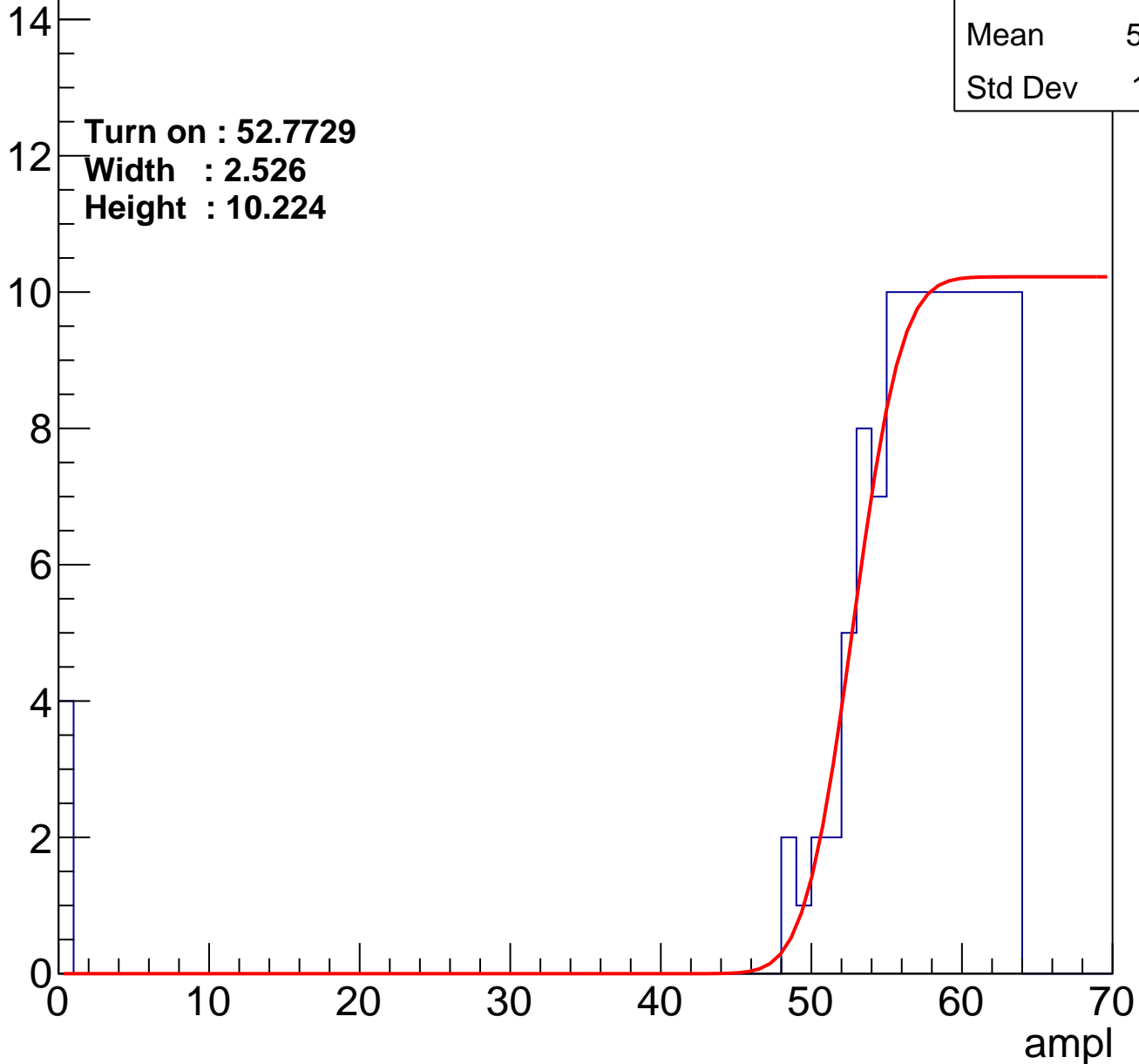
Entries	121
Mean	55.53
Std Dev	10.91

Turn on : 52.7729

Width : 2.526

Height : 10.224

Entry



B0L103S, U24-ch84

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.87
Std Dev	9.633

Turn on : 53.2191

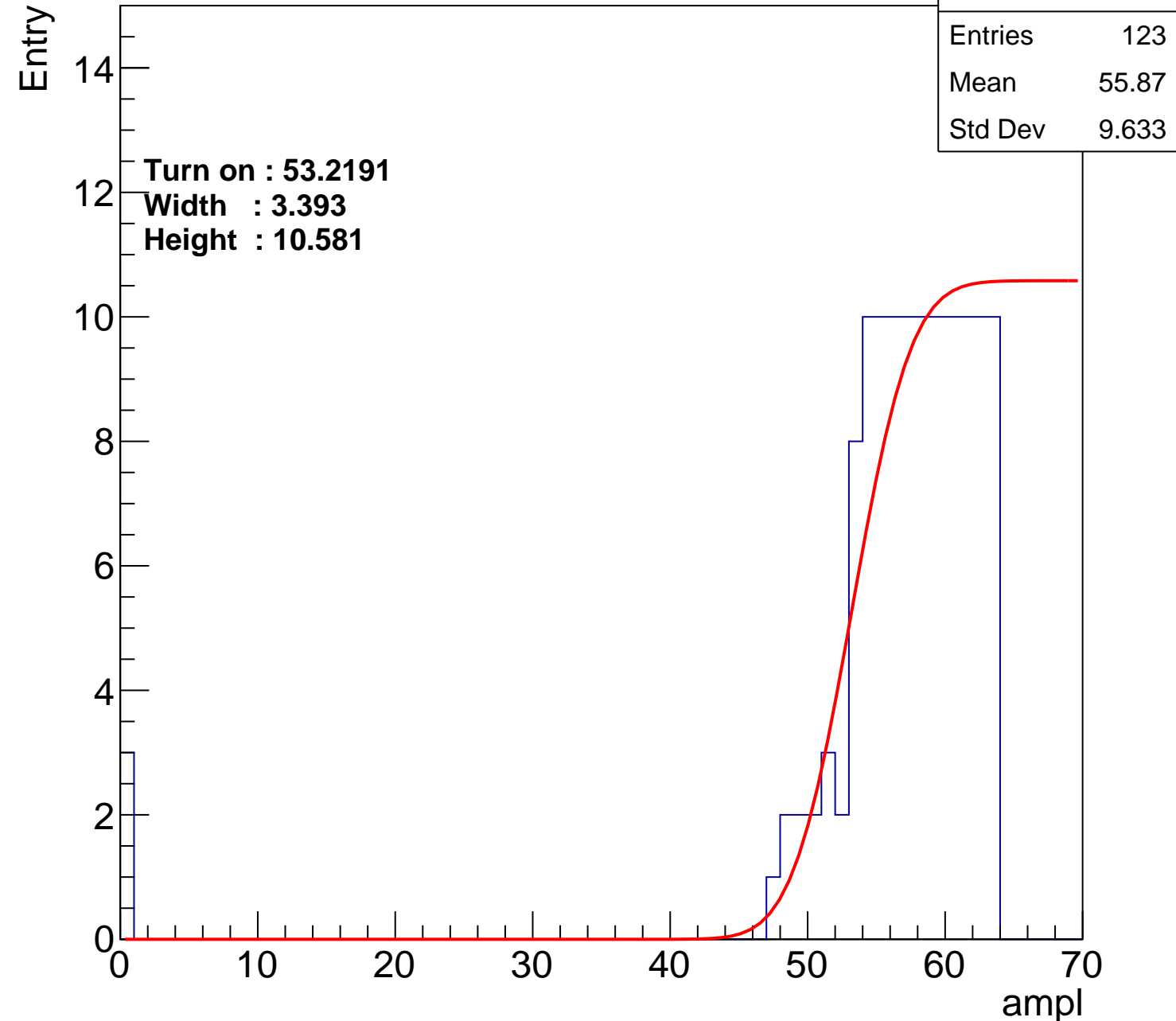
Width : 3.393

Height : 10.581

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch85

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.42
Std Dev	10.74

Turn on : 51.7363

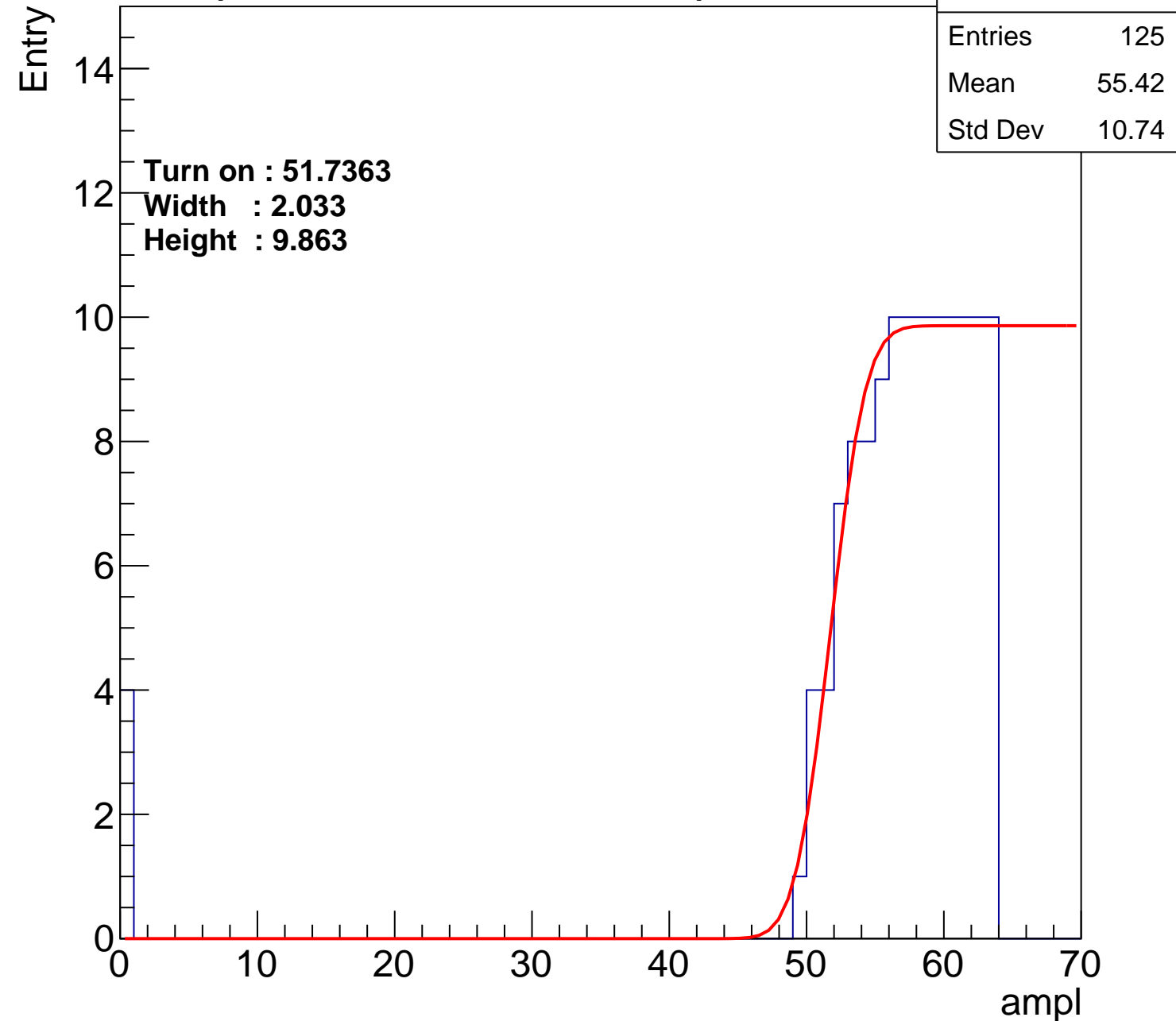
Width : 2.033

Height : 9.863

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch86

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	54.55
Std Dev	11.37

Turn on : 51.1712

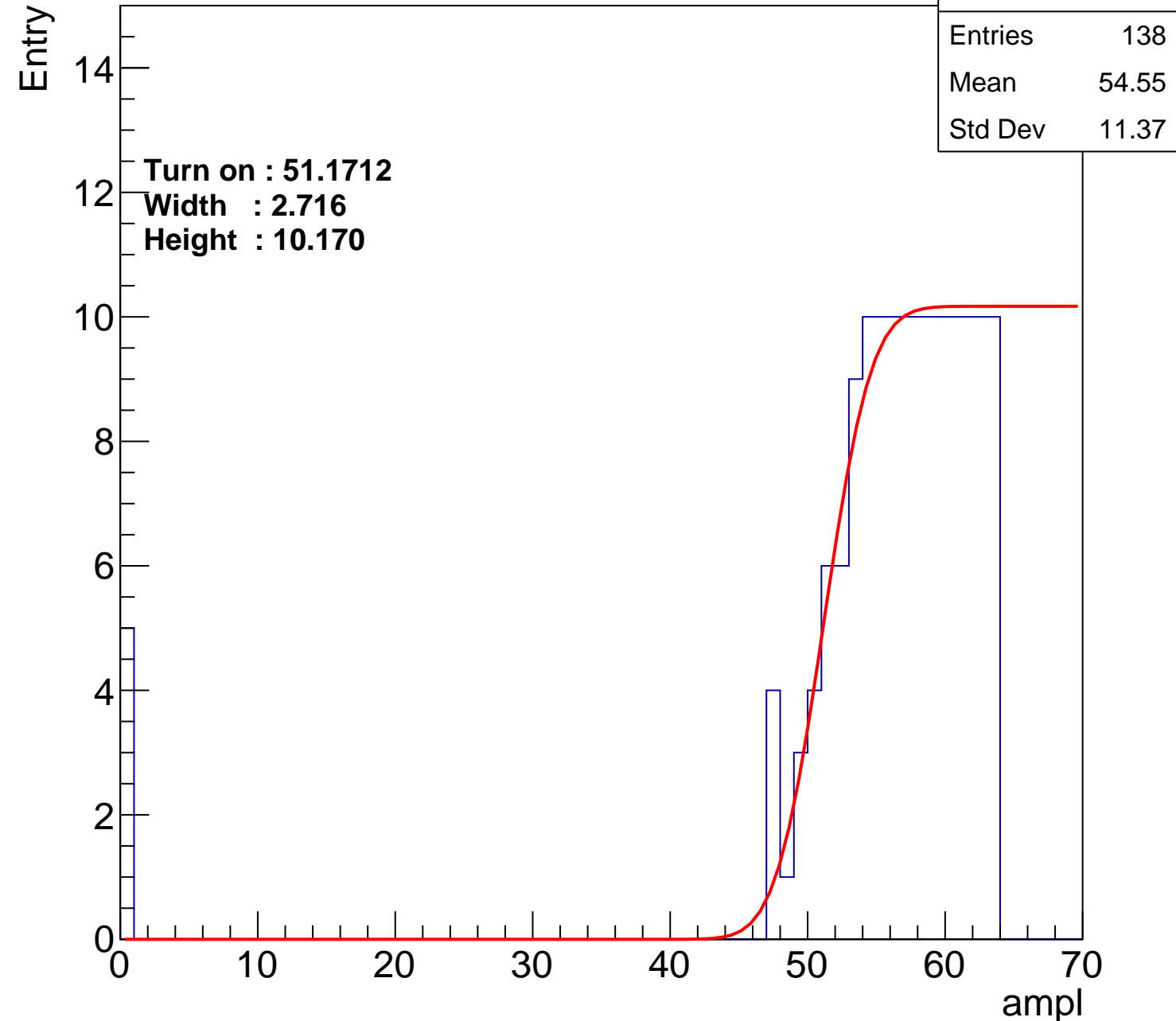
Width : 2.716

Height : 10.170

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch87

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.8
Std Dev	8.208

Turn on : 51.7049

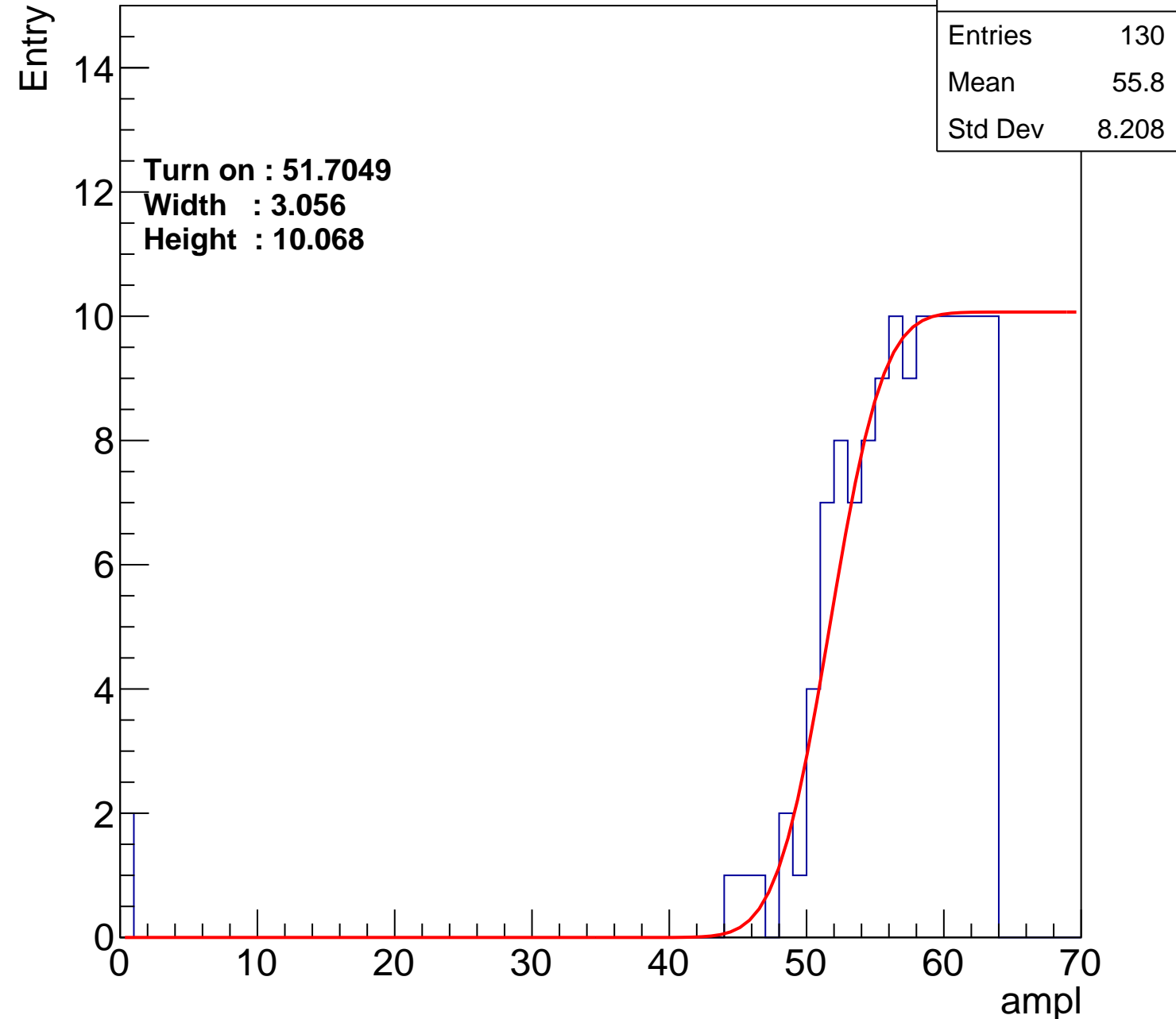
Width : 3.056

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch88

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	55.63
Std Dev	11.22

Turn on : 53.3564

Width : 2.662

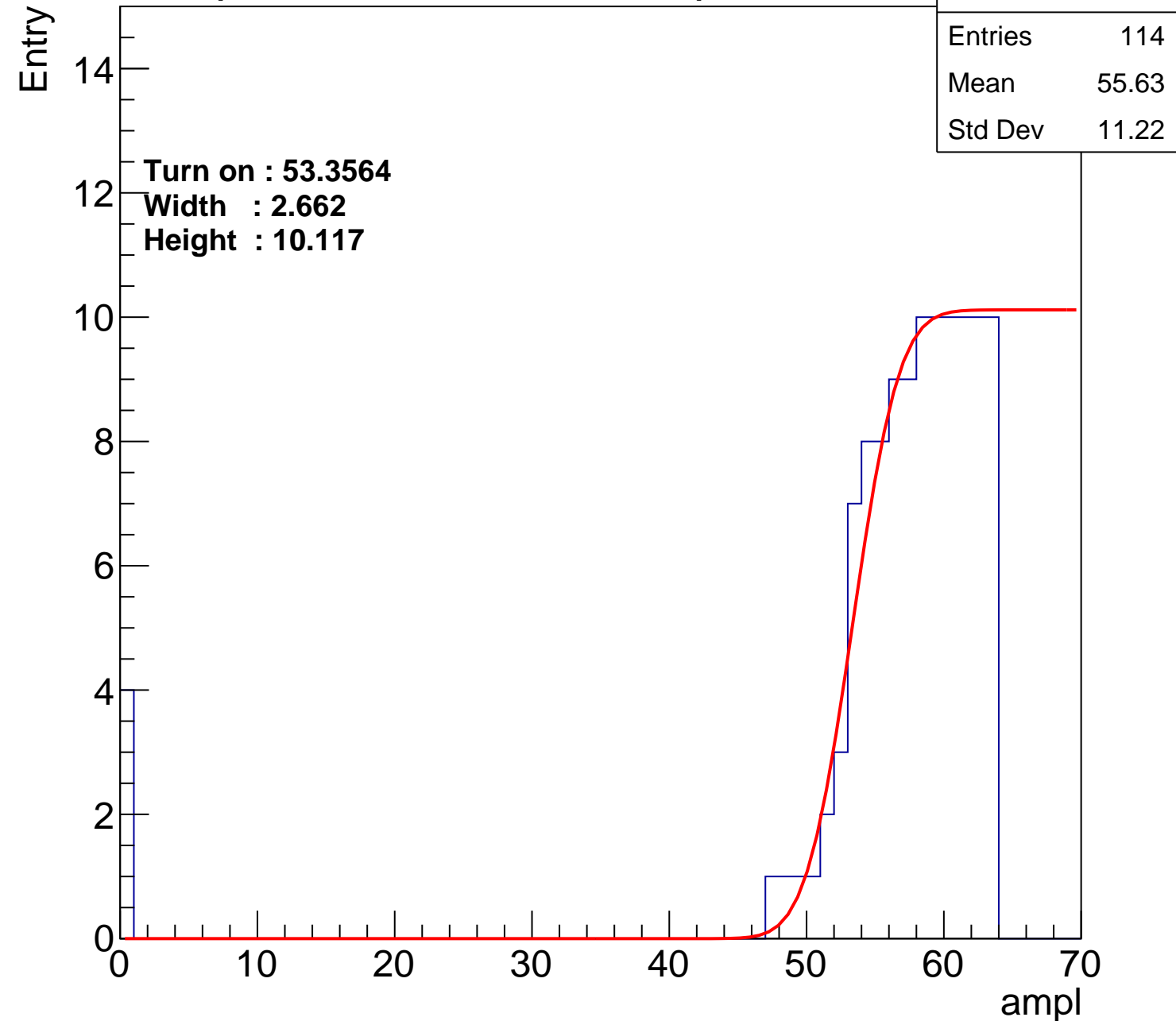
Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U24-ch89

calib_packv5_040323_1717.root, FC#2, port C3

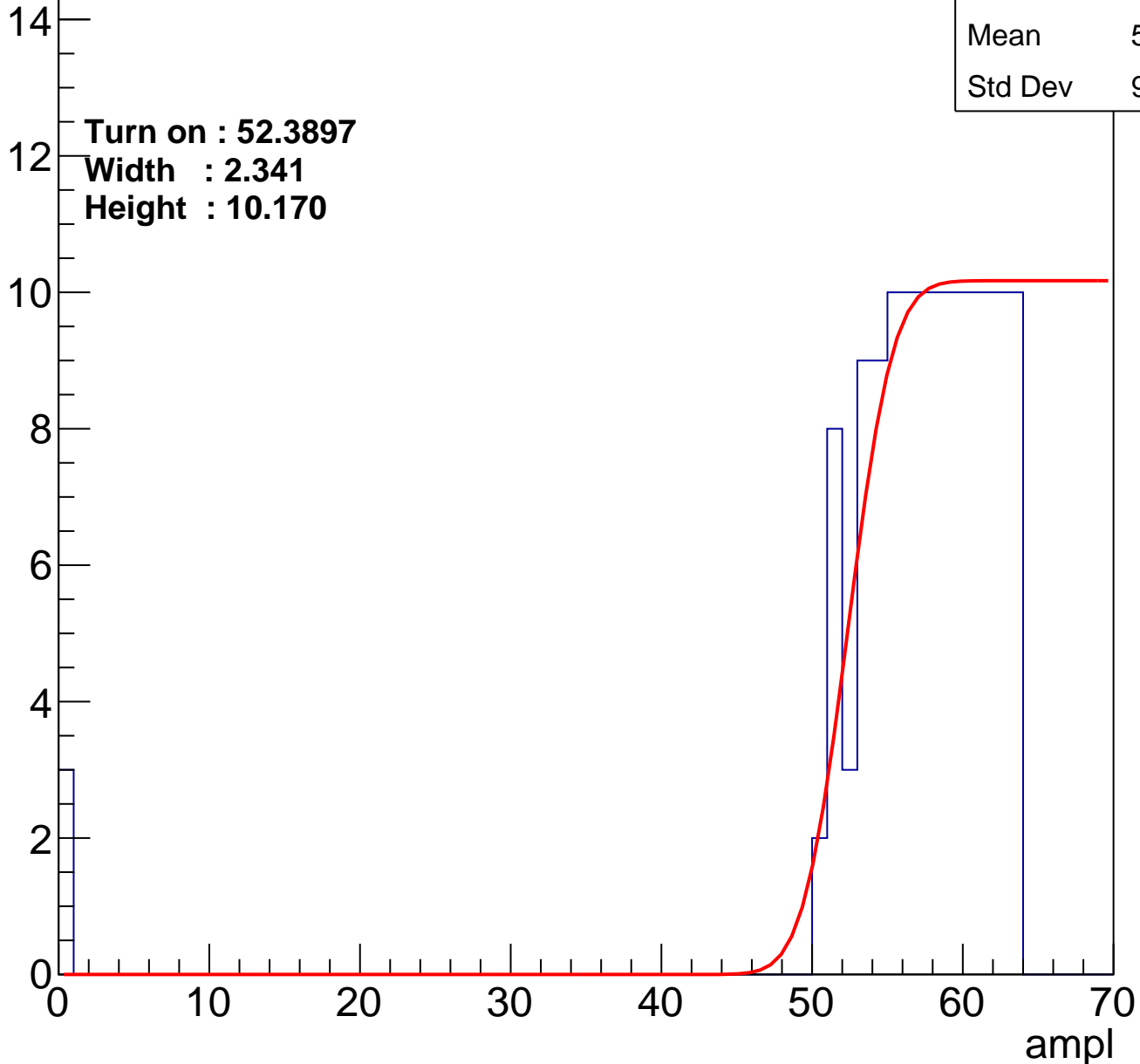
Entries	124
Mean	55.94
Std Dev	9.527

Turn on : 52.3897

Width : 2.341

Height : 10.170

Entry



B0L103S, U24-ch90

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	56.11
Std Dev	8.097

Turn on : 51.5402

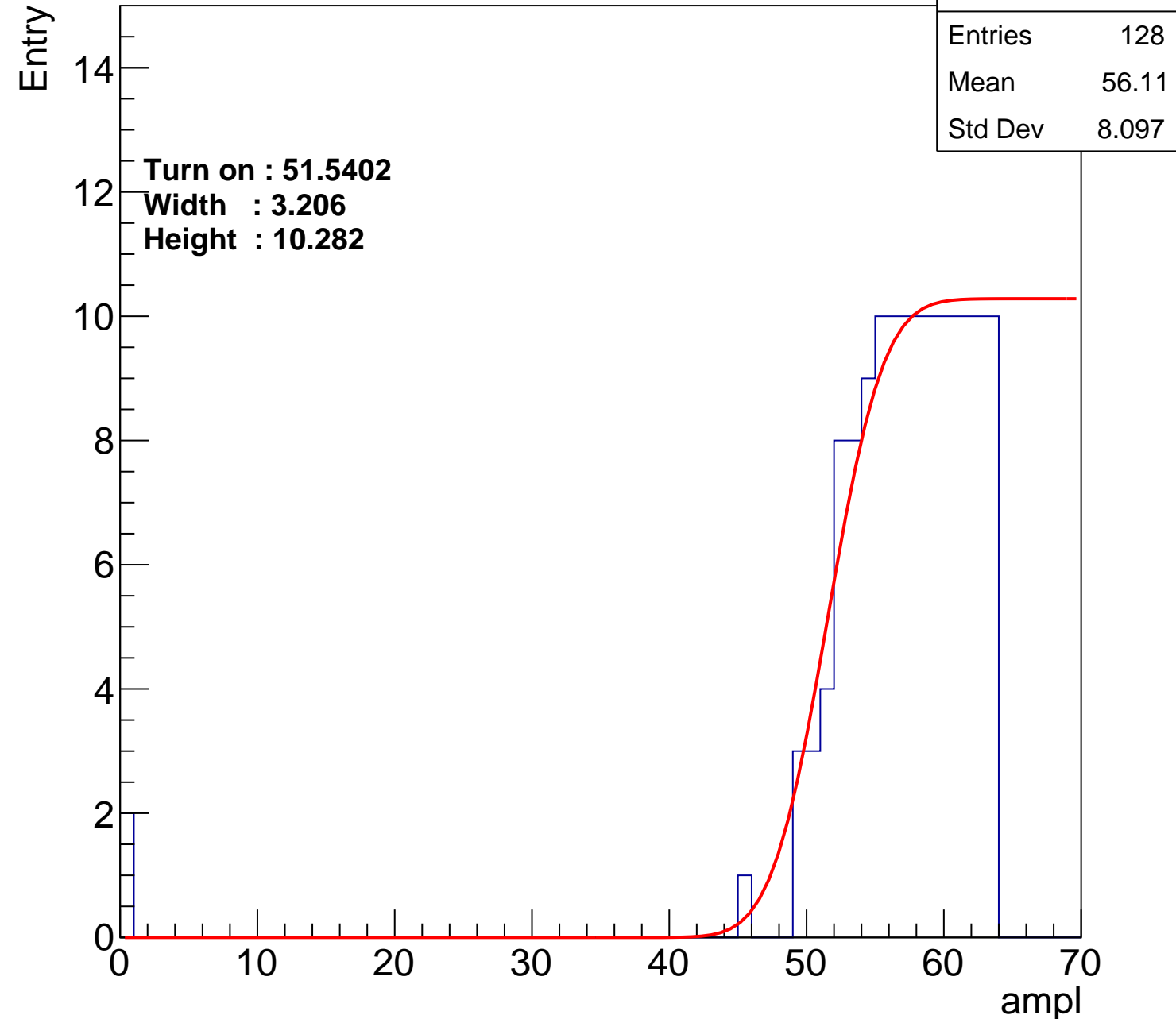
Width : 3.206

Height : 10.282

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch91

calib_packv5_040323_1717.root, FC#2, port C3

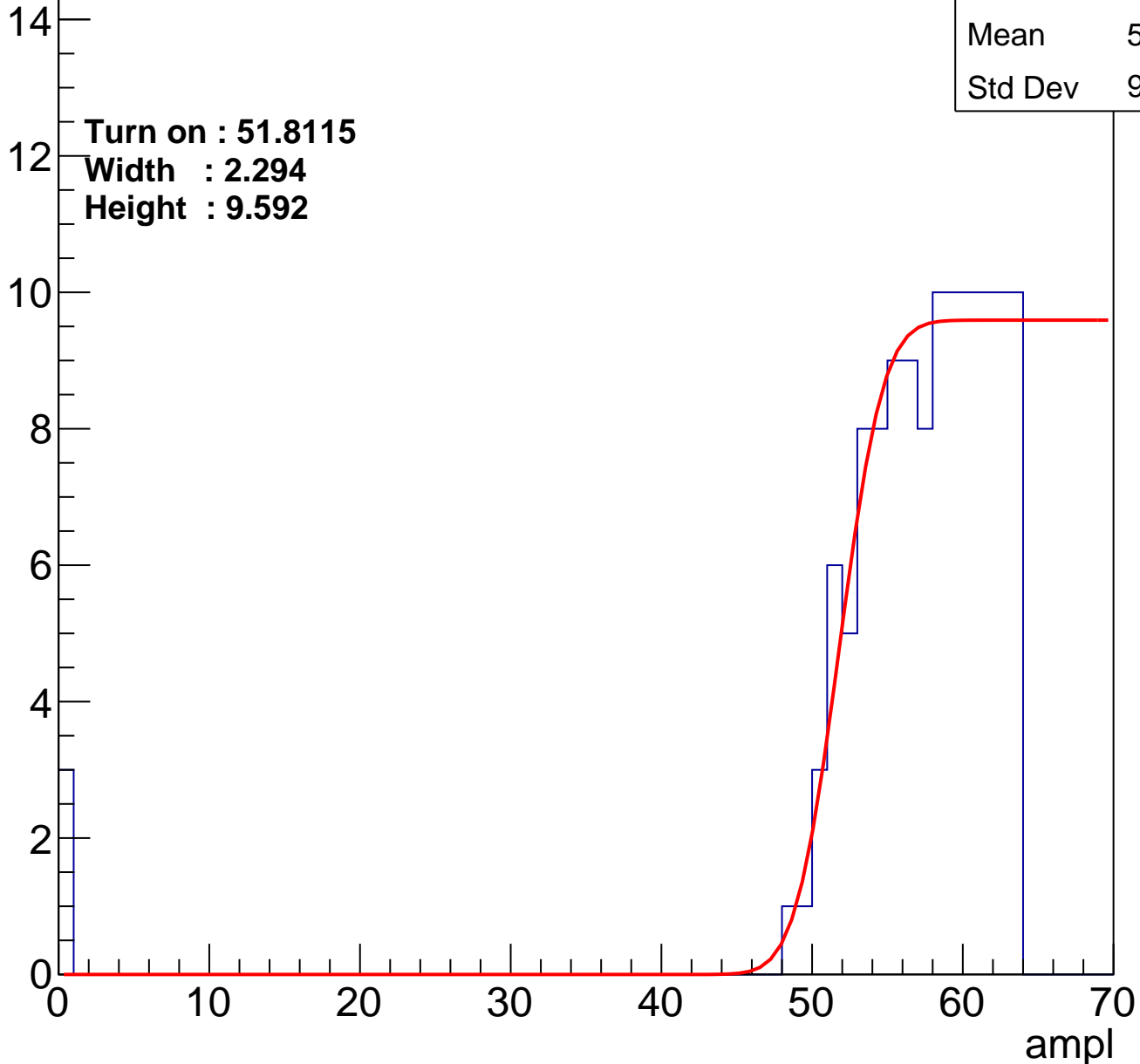
Entries	121
Mean	55.82
Std Dev	9.692

Turn on : 51.8115

Width : 2.294

Height : 9.592

Entry



B0L103S, U24-ch92

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	55.17
Std Dev	10.66

Turn on : 52.0325

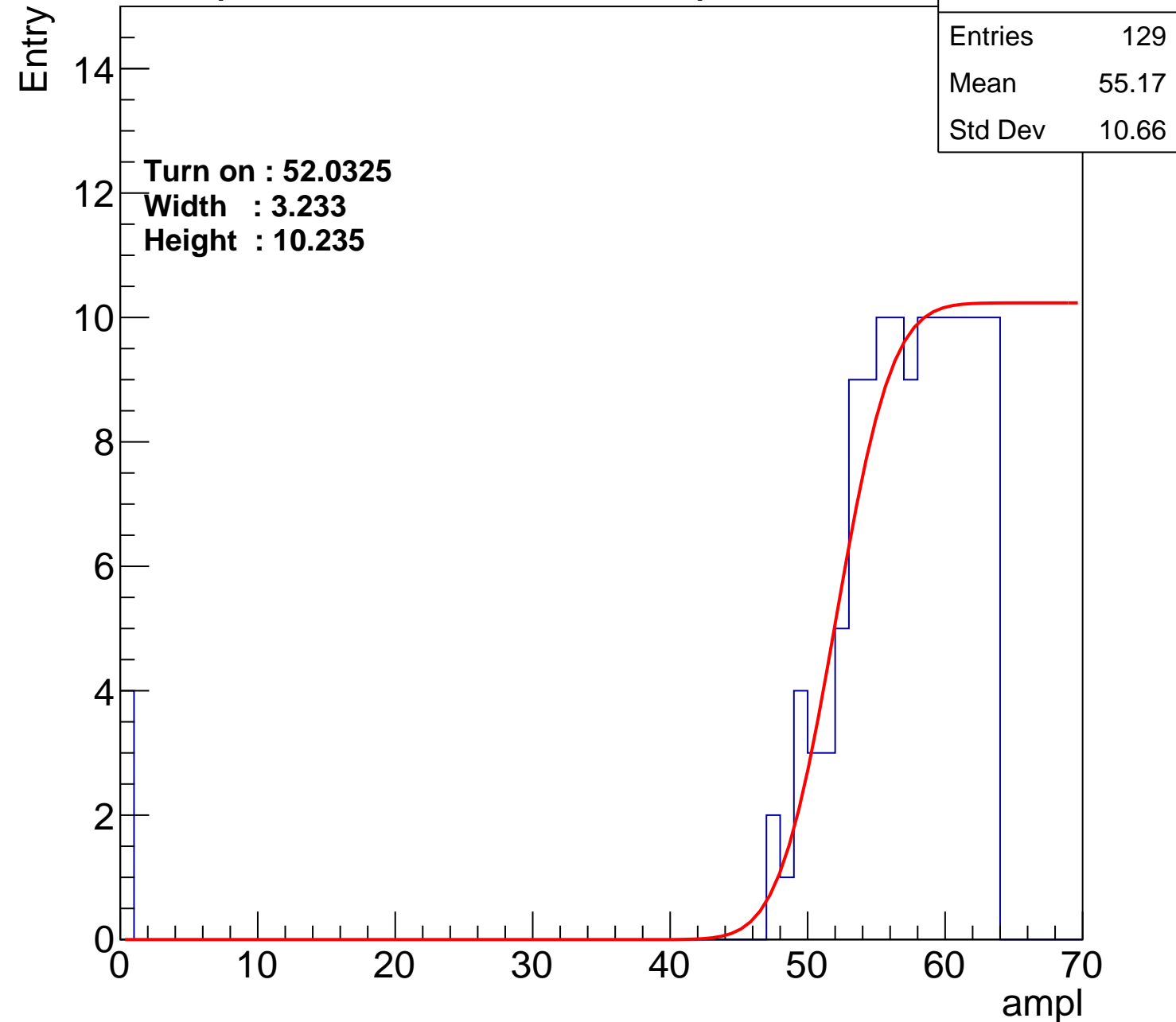
Width : 3.233

Height : 10.235

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch93

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.84
Std Dev	9.653

Turn on : 52.0844

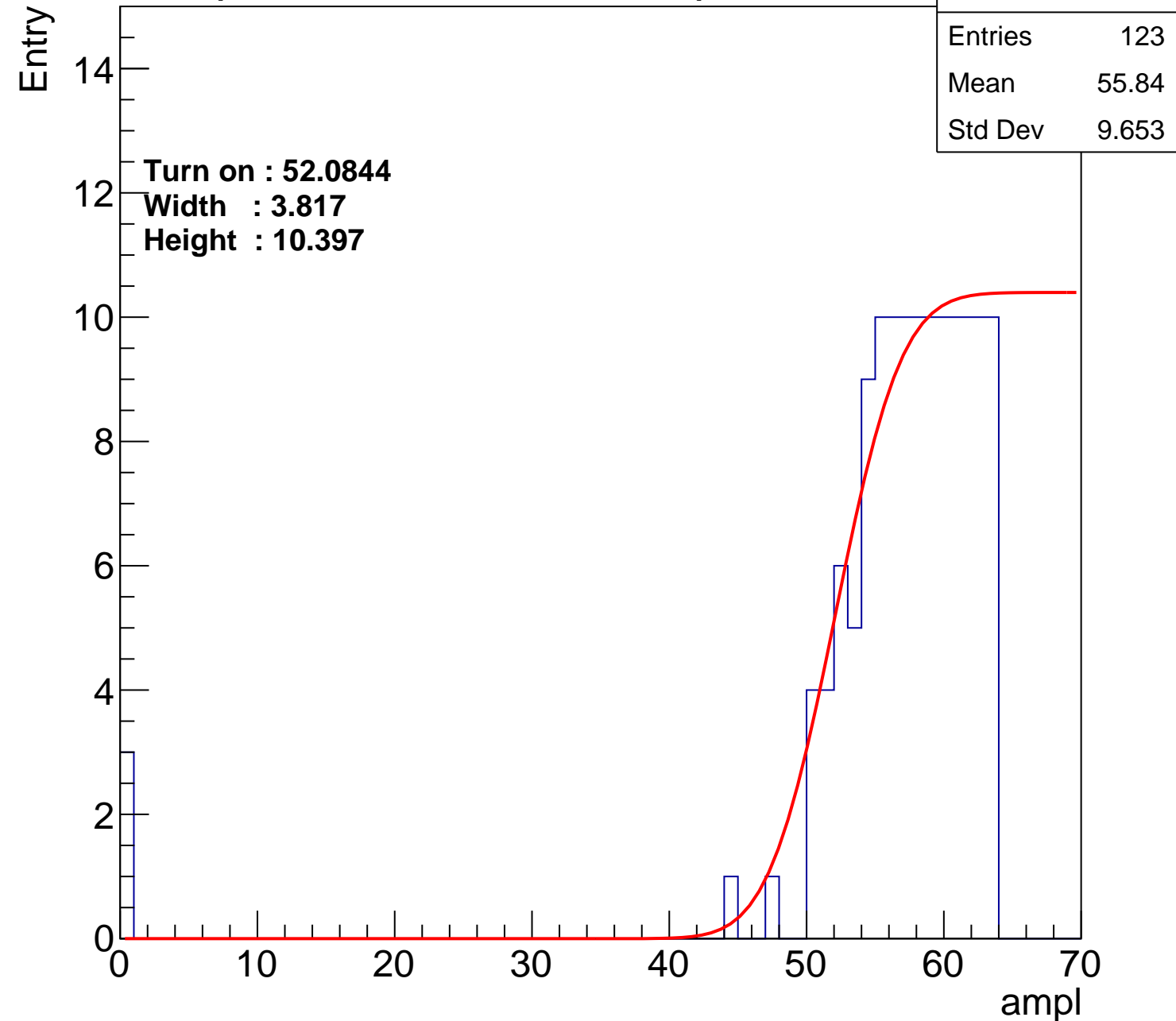
Width : 3.817

Height : 10.397

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch94

calib_packv5_040323_1717.root, FC#2, port C3

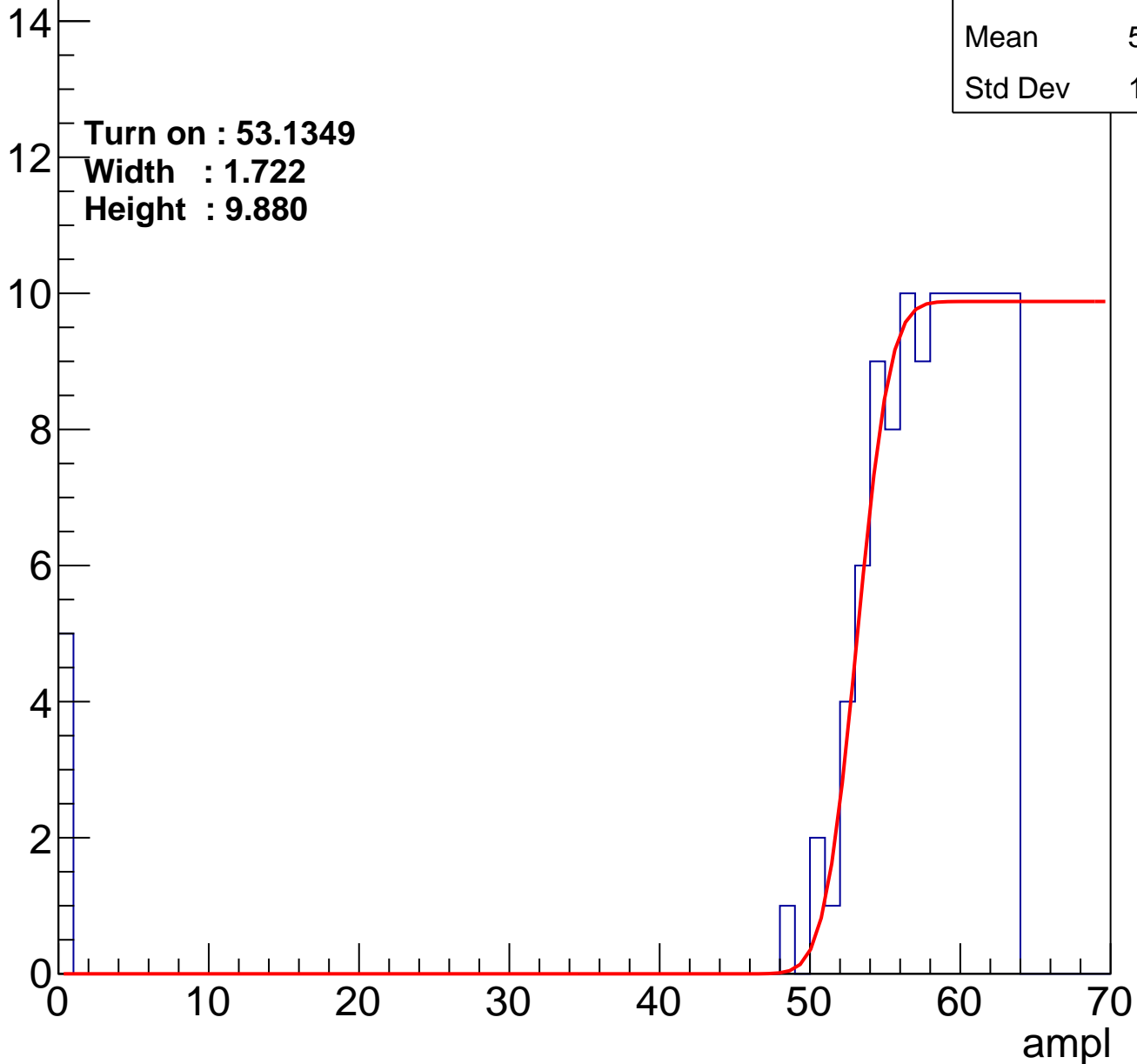
Entries	115
Mean	55.25
Std Dev	12.28

Turn on : 53.1349

Width : 1.722

Height : 9.880

Entry



B0L103S, U24-ch95

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	55.8
Std Dev	6.409

Turn on : 49.7986

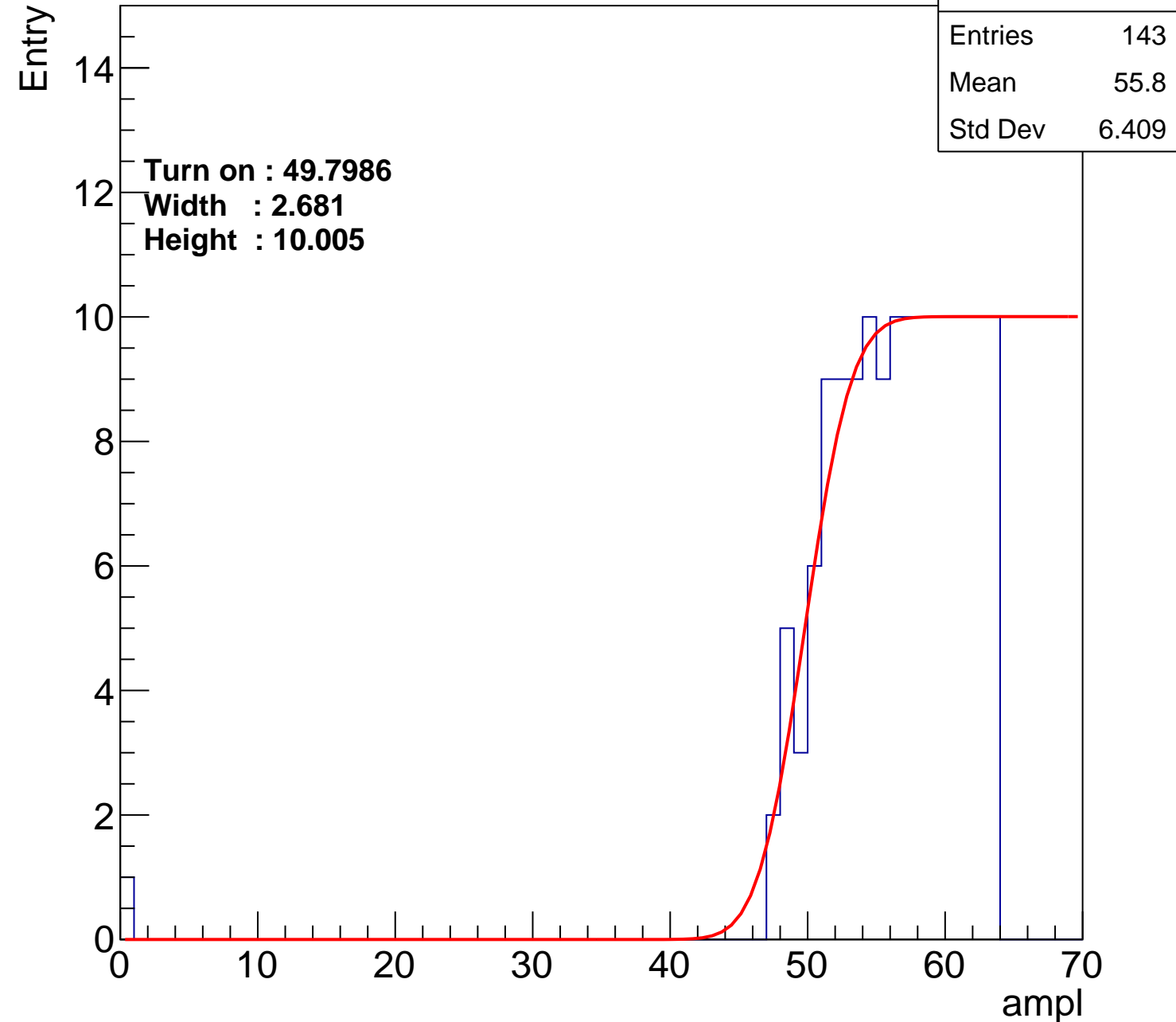
Width : 2.681

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch96

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	53.74
Std Dev	11.91

Turn on : 49.8084

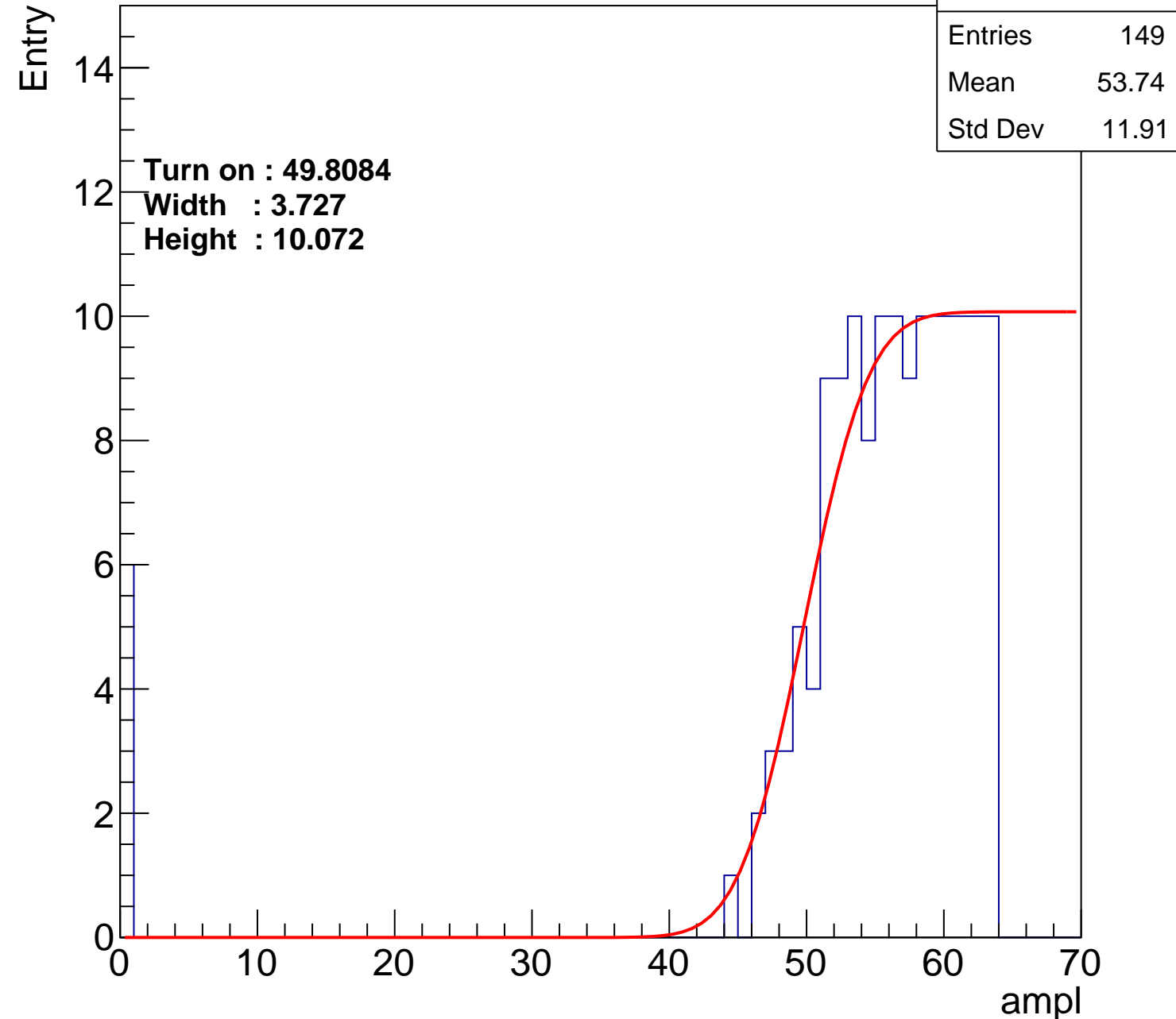
Width : 3.727

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch97

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.07
Std Dev	10.53

Turn on : 51.3239

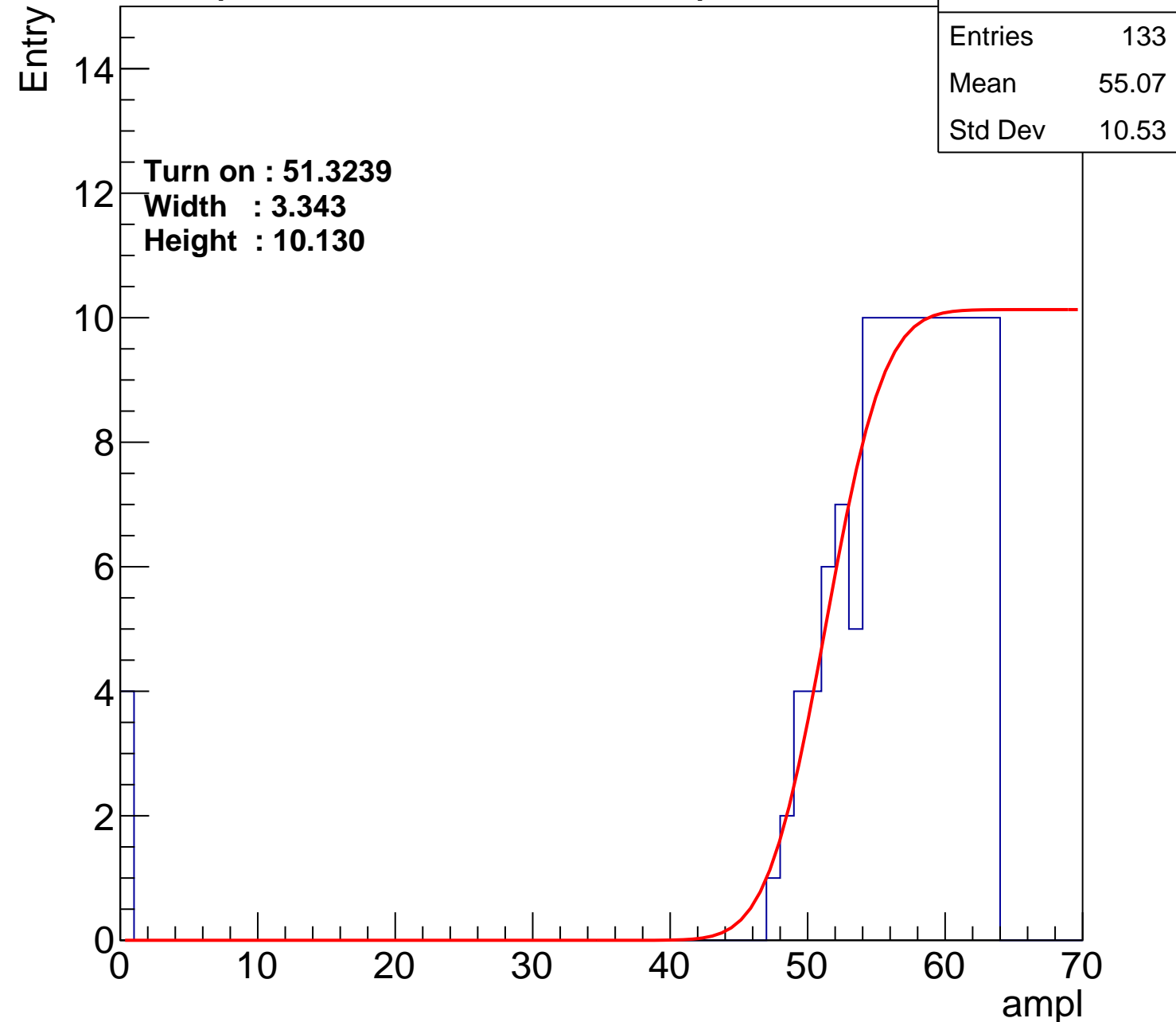
Width : 3.343

Height : 10.130

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch98

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.25
Std Dev	12.44

Turn on : 51.6740

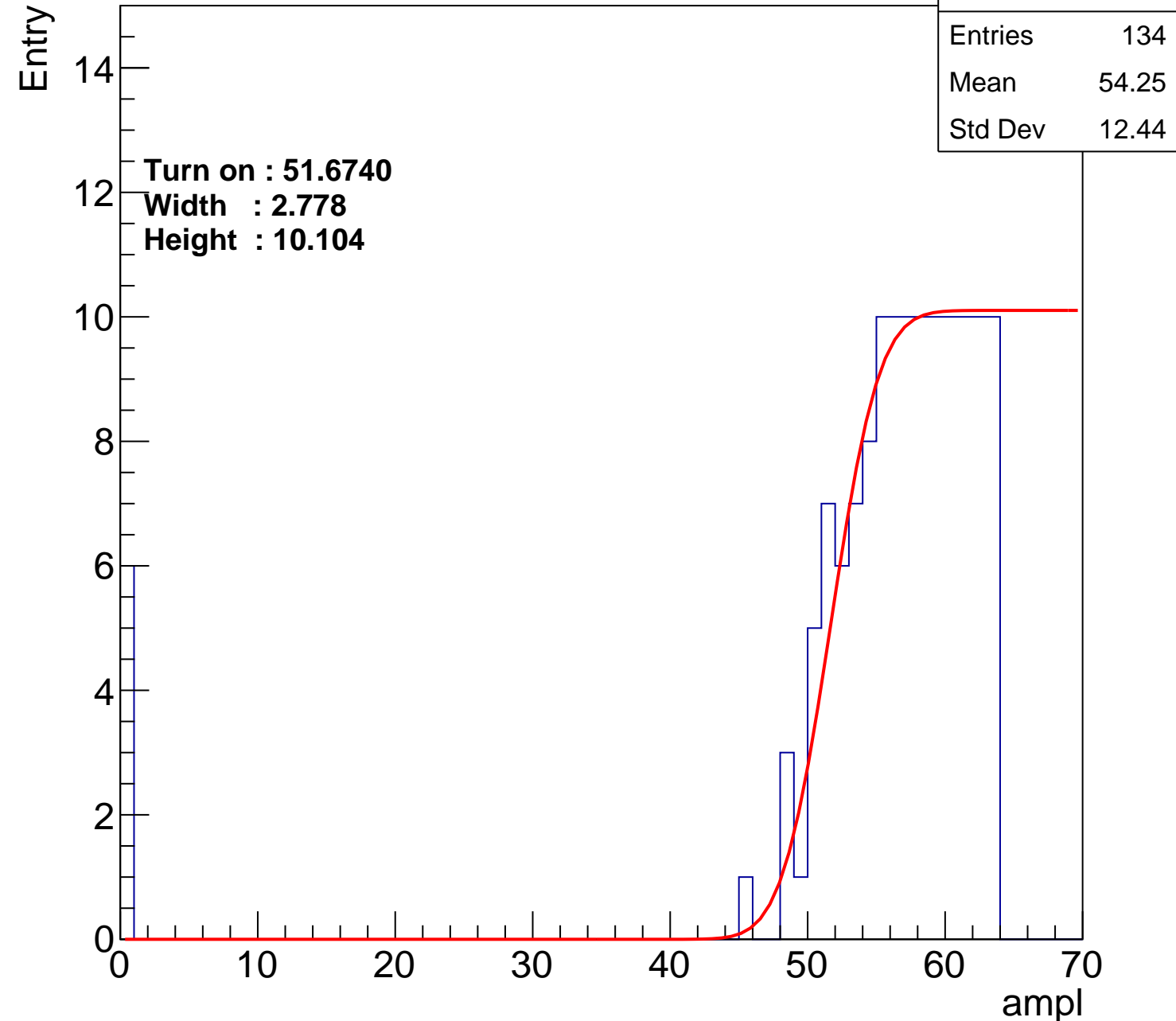
Width : 2.778

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch99

calib_packv5_040323_1717.root, FC#2, port C3

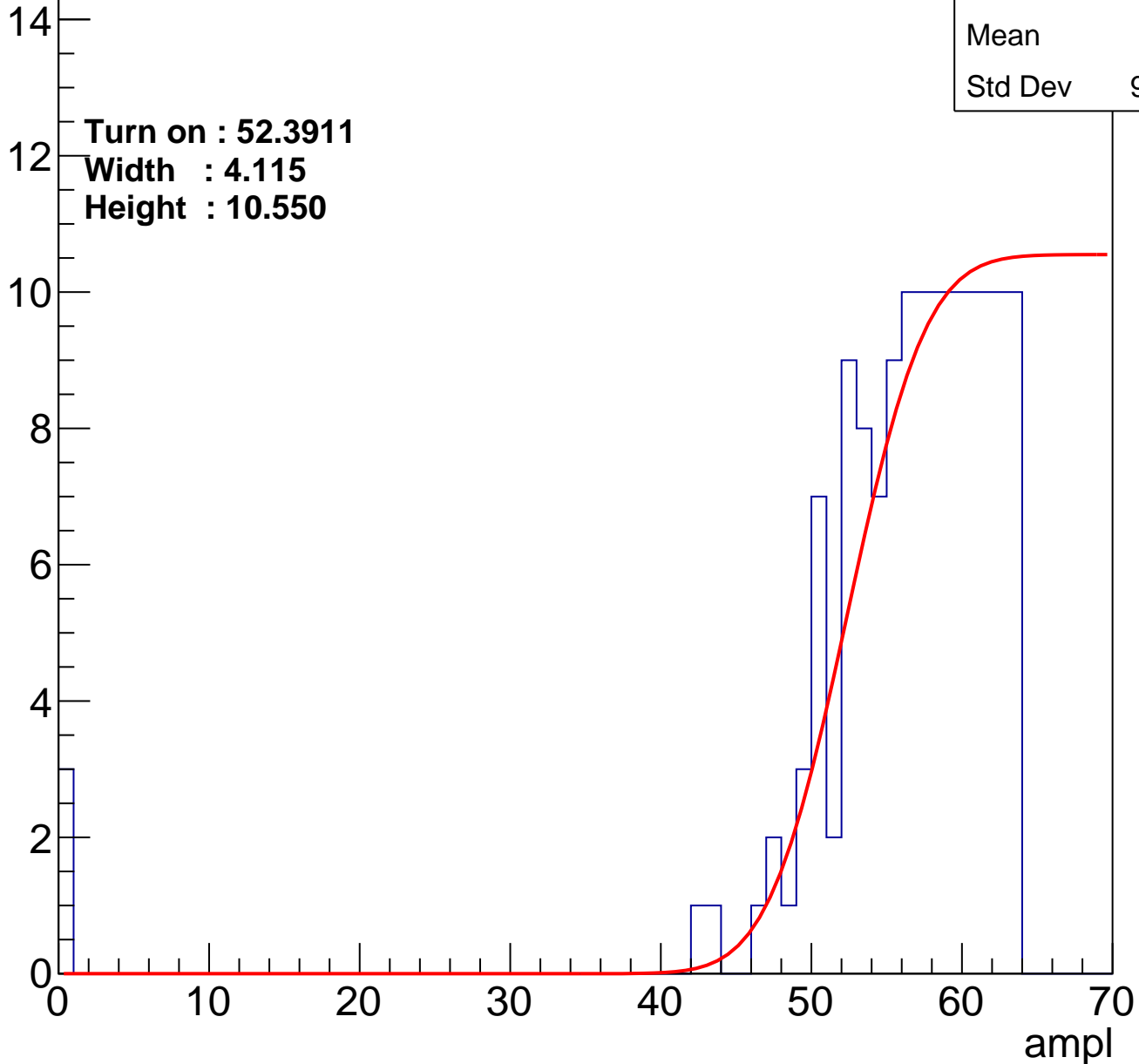
Entries	134
Mean	55.2
Std Dev	9.509

Turn on : 52.3911

Width : 4.115

Height : 10.550

Entry



B0L103S, U24-ch100

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.69
Std Dev	9.451

Turn on : 51.2512

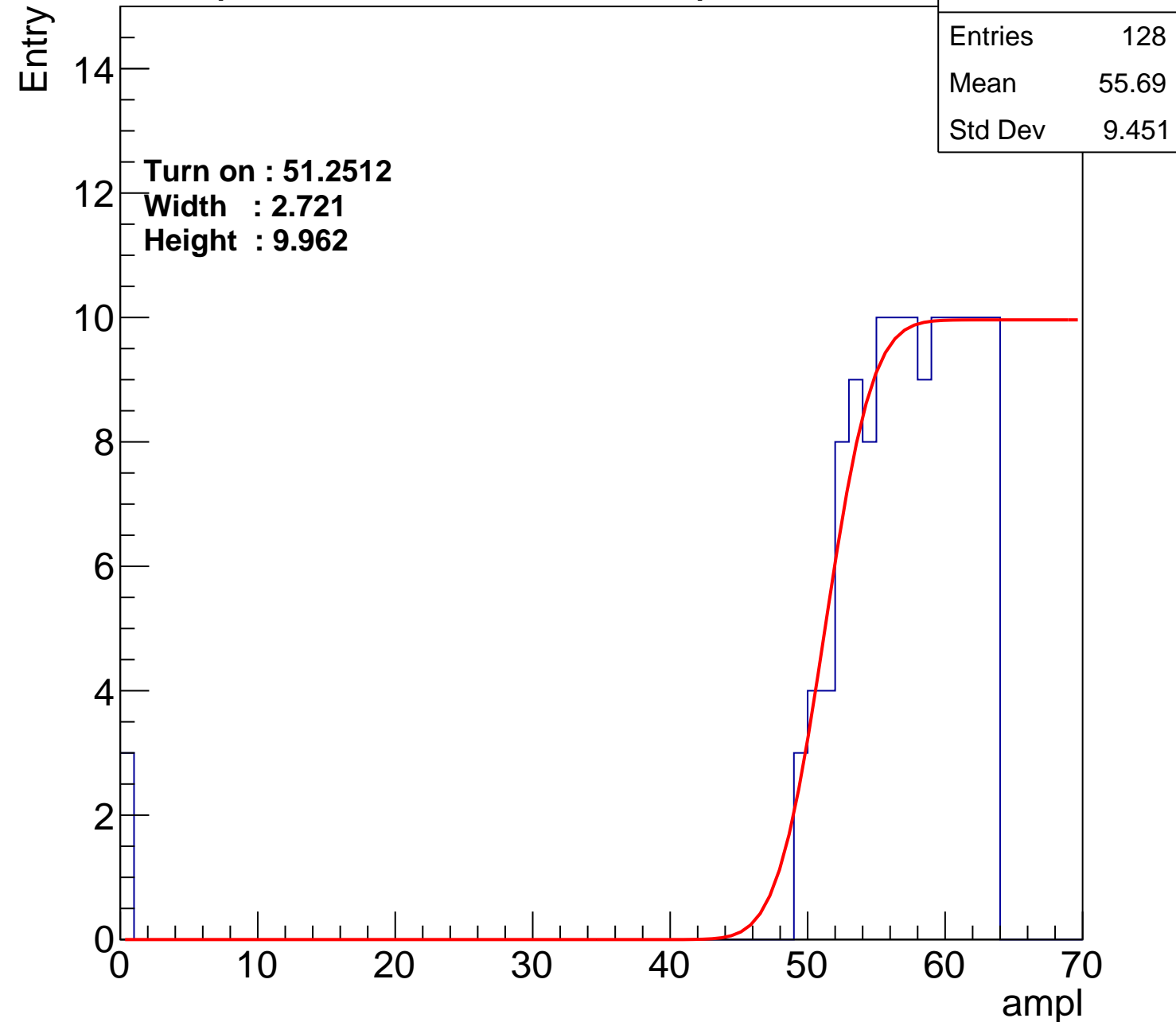
Width : 2.721

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch101

calib_packv5_040323_1717.root, FC#2, port C3

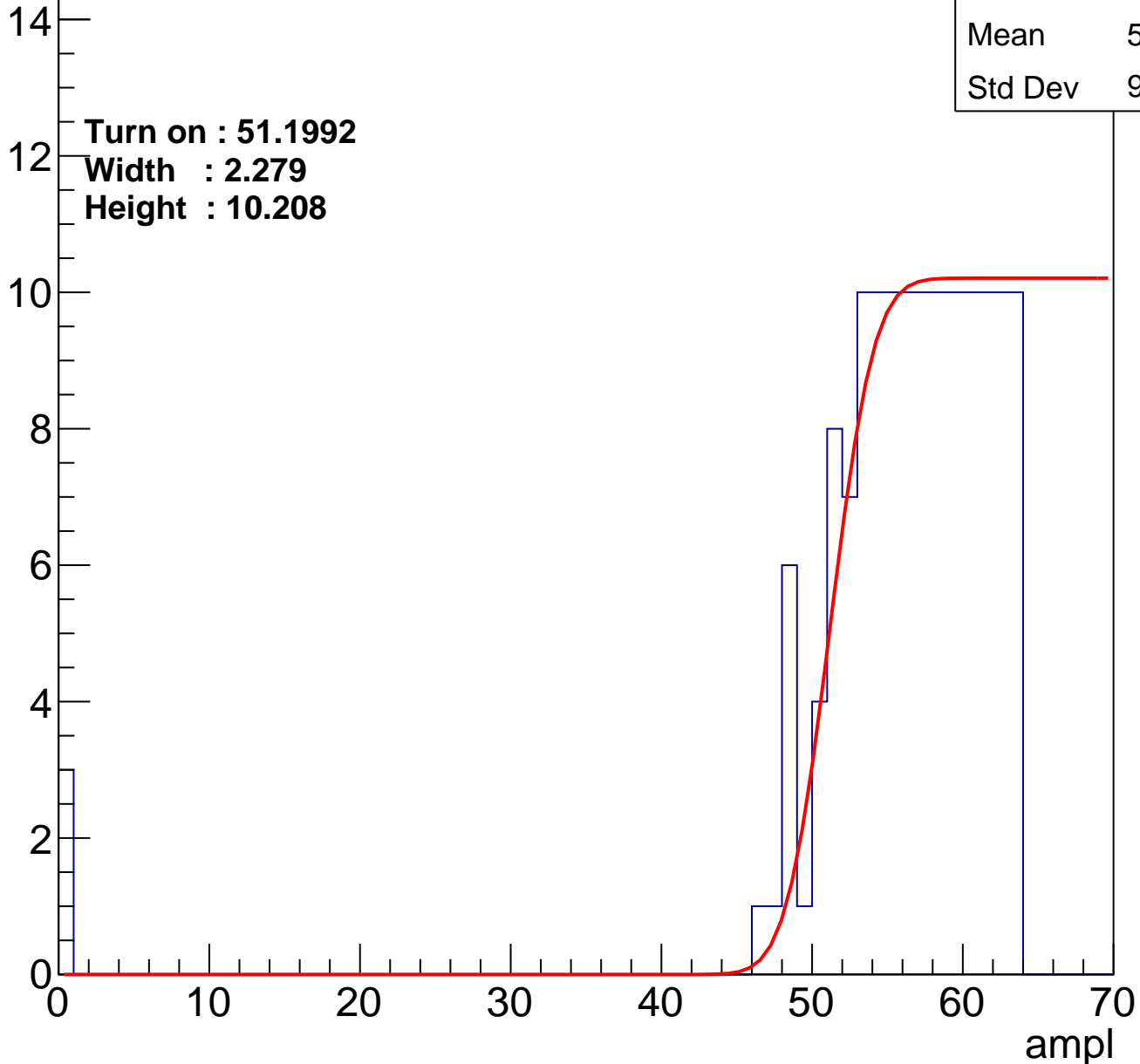
Entries	141
Mean	55.19
Std Dev	9.197

Turn on : 51.1992

Width : 2.279

Height : 10.208

Entry



B0L103S, U24-ch102

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.23
Std Dev	12.3

Turn on : 51.3276

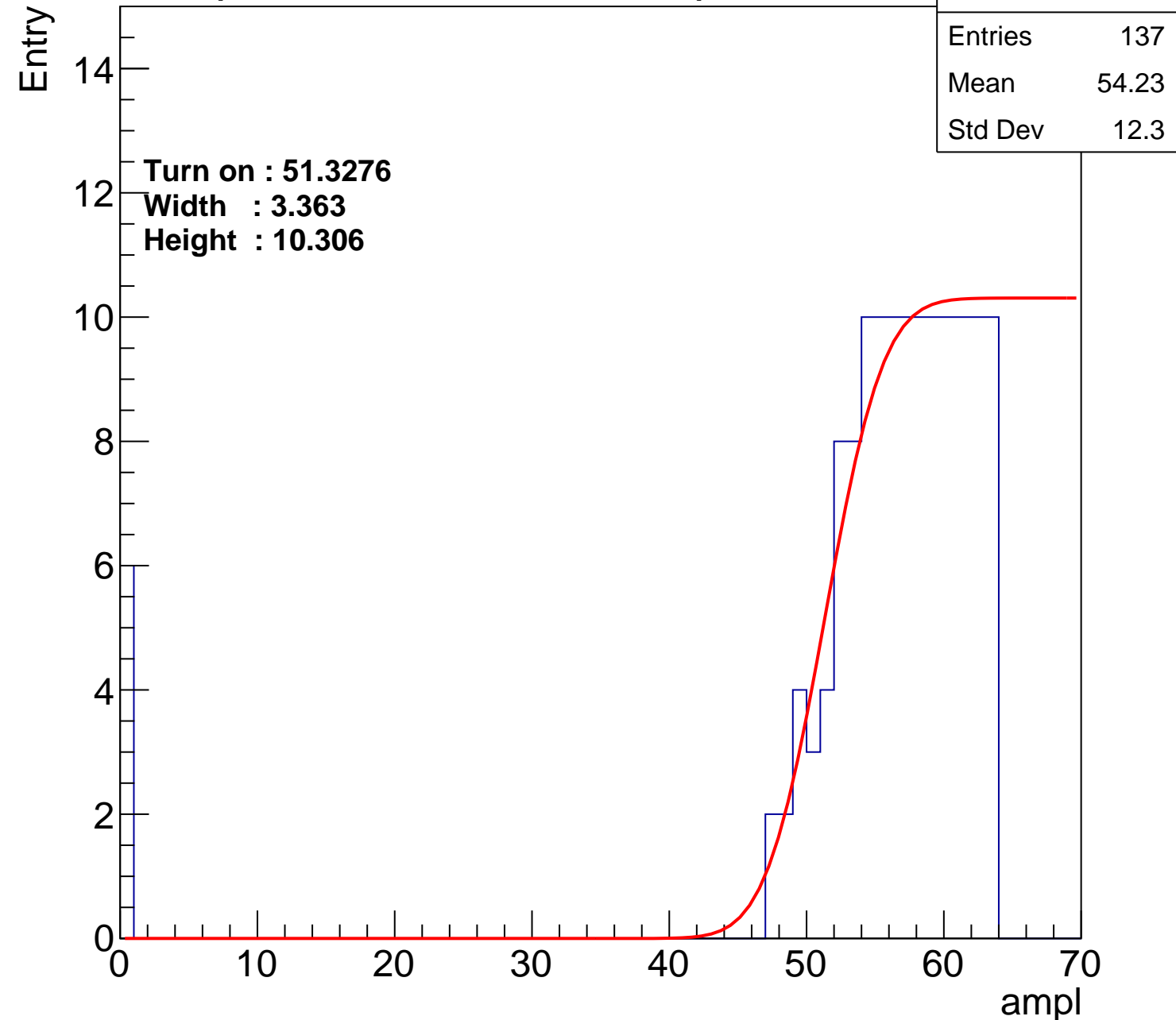
Width : 3.363

Height : 10.306

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch103

calib_packv5_040323_1717.root, FC#2, port C3

Entries	150
Mean	55.25
Std Dev	6.742

Turn on : 49.2314

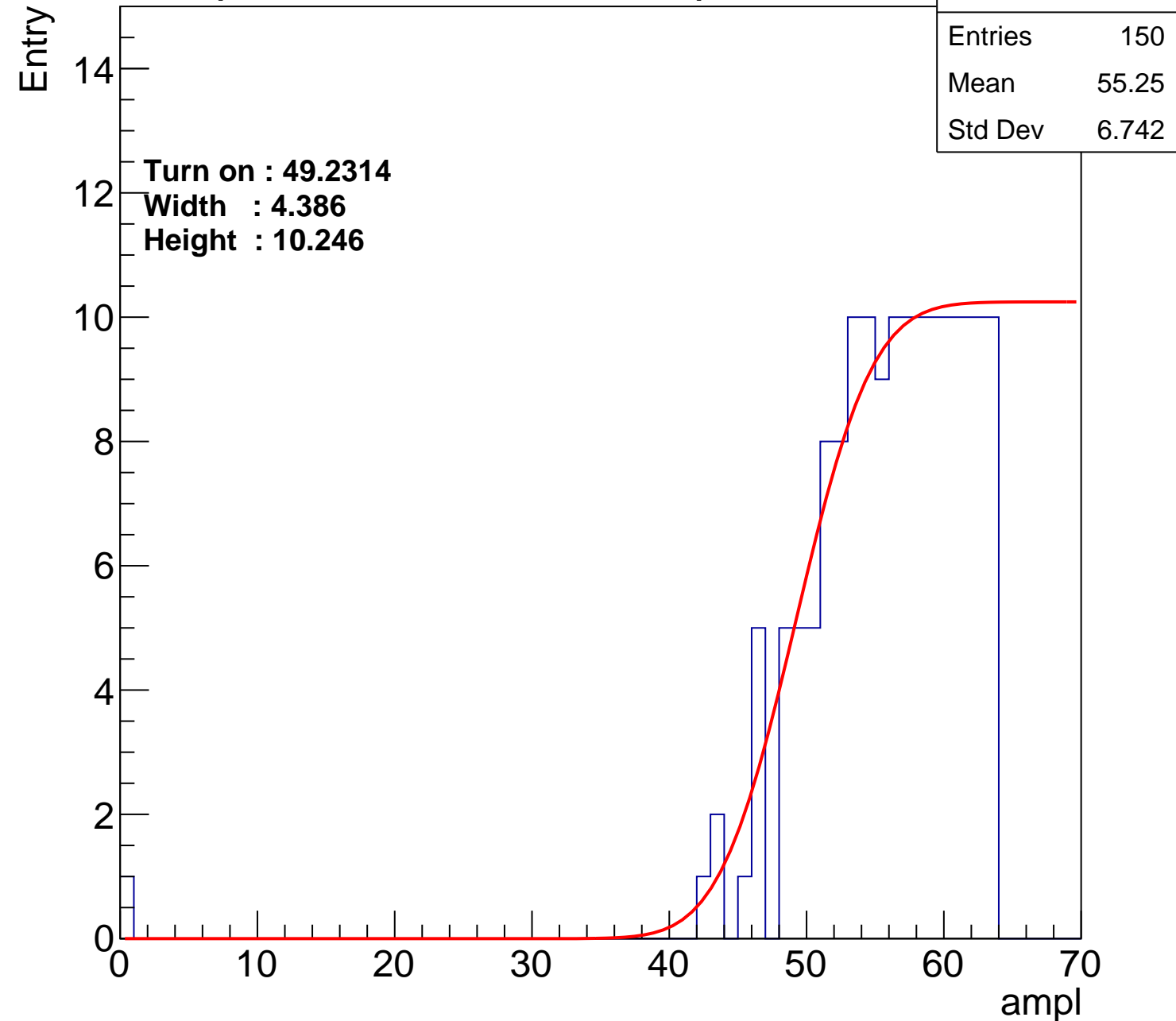
Width : 4.386

Height : 10.246

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch104

calib_packv5_040323_1717.root, FC#2, port C3

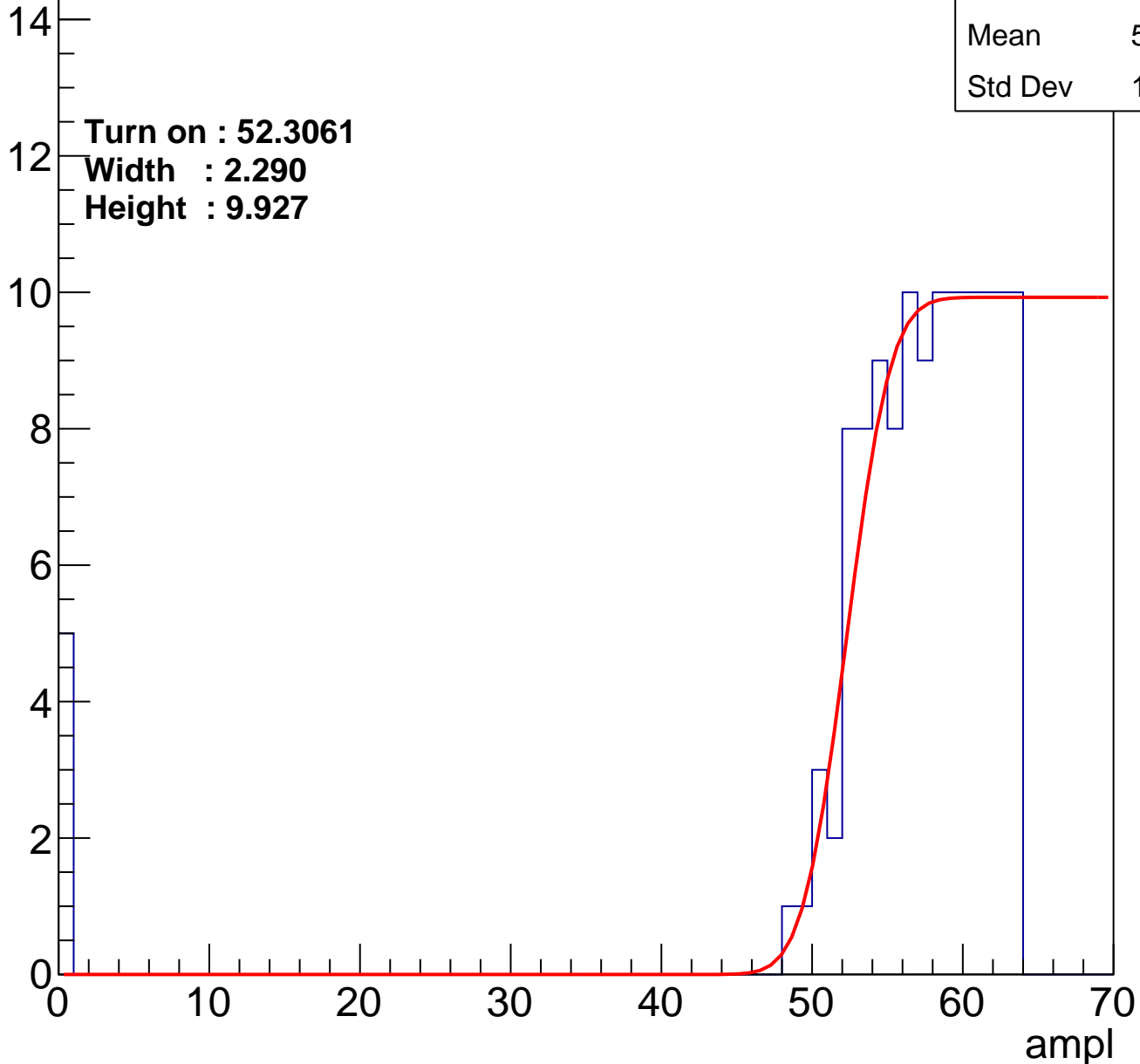
Entries	124
Mean	54.98
Std Dev	11.87

Turn on : 52.3061

Width : 2.290

Height : 9.927

Entry



B0L103S, U24-ch105

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	56.59
Std Dev	8.549

Turn on : 53.8140

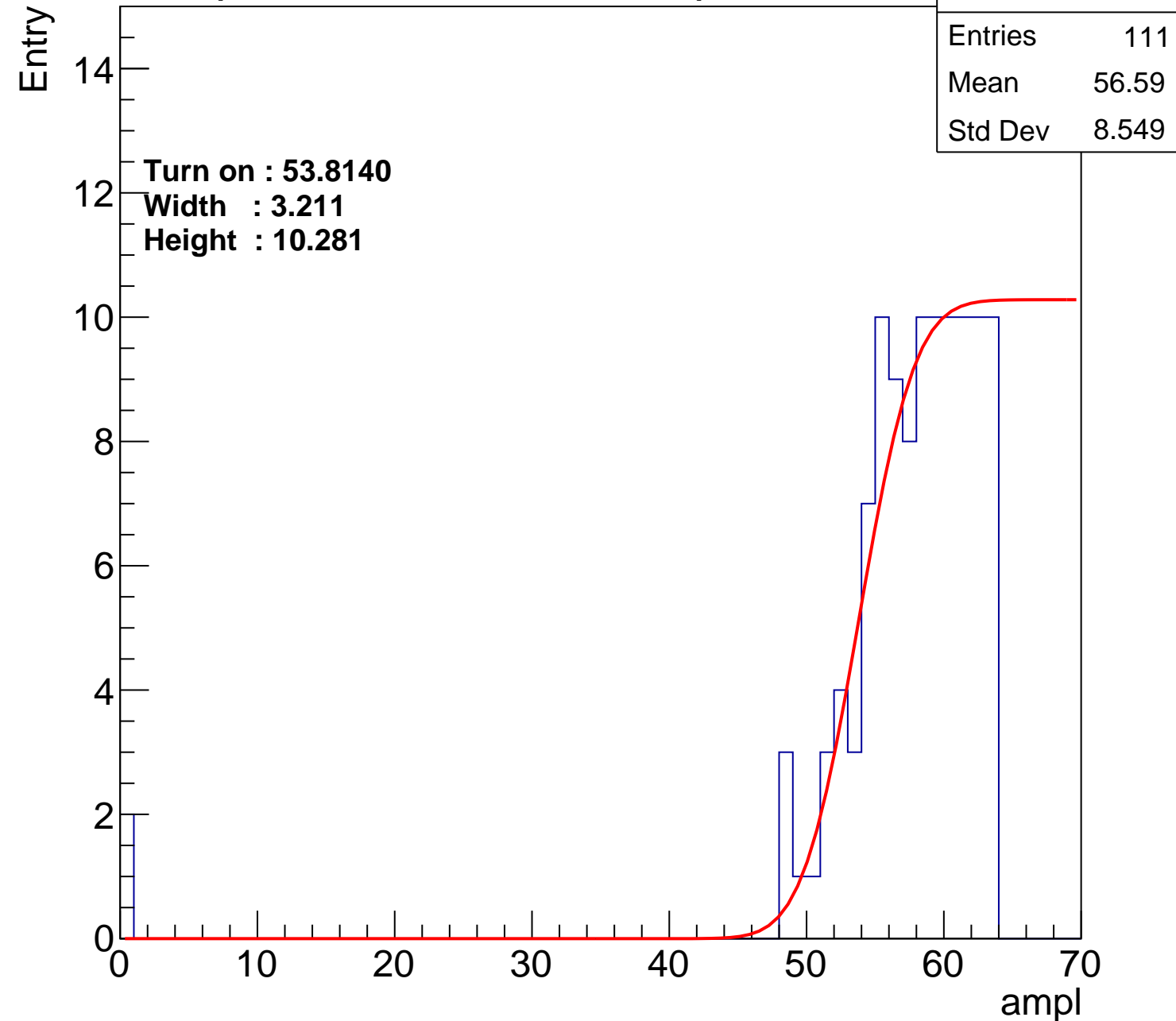
Width : 3.211

Height : 10.281

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch106

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.35
Std Dev	9.609

Turn on : 51.7154

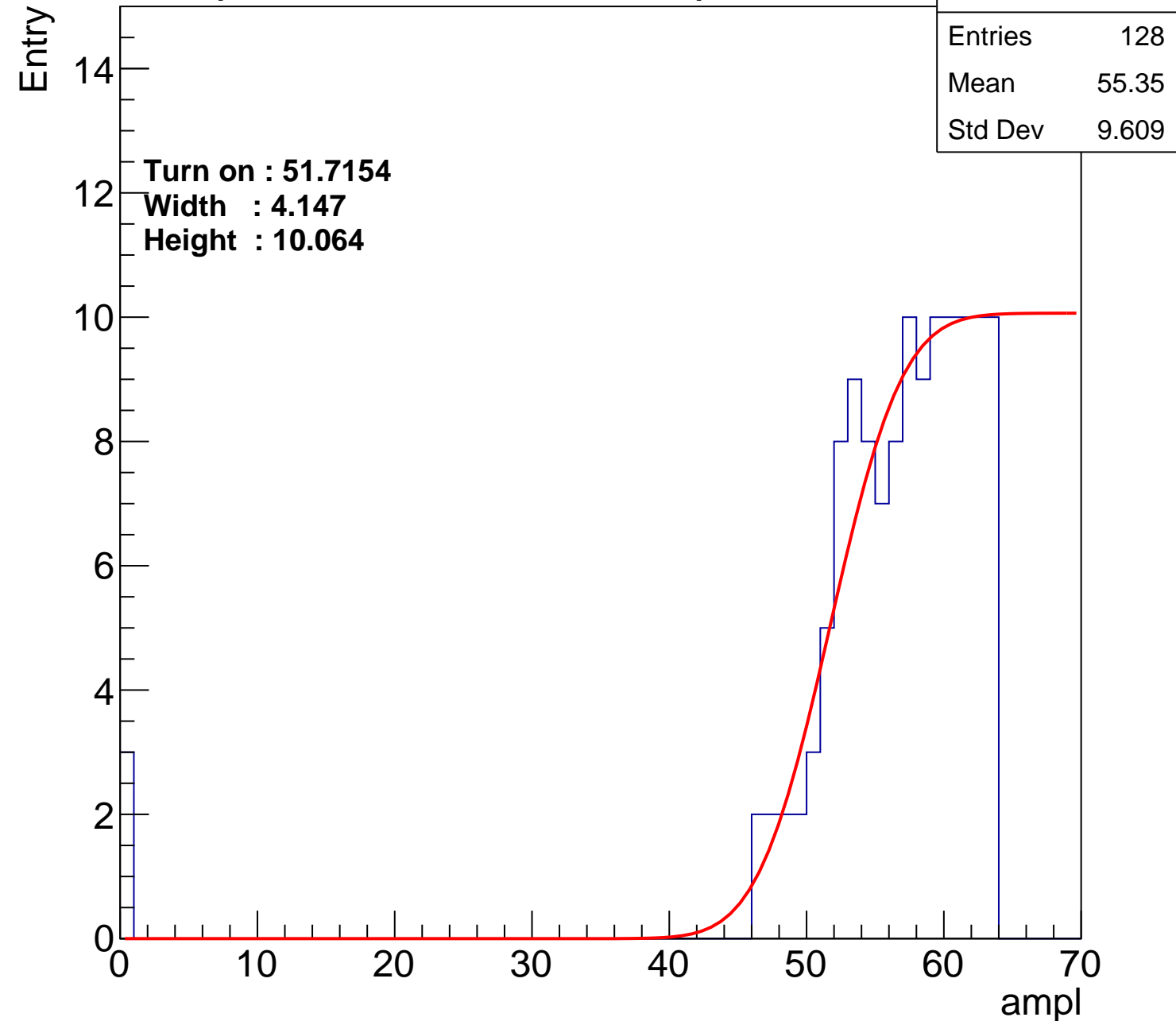
Width : 4.147

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch107

calib_packv5_040323_1717.root, FC#2, port C3

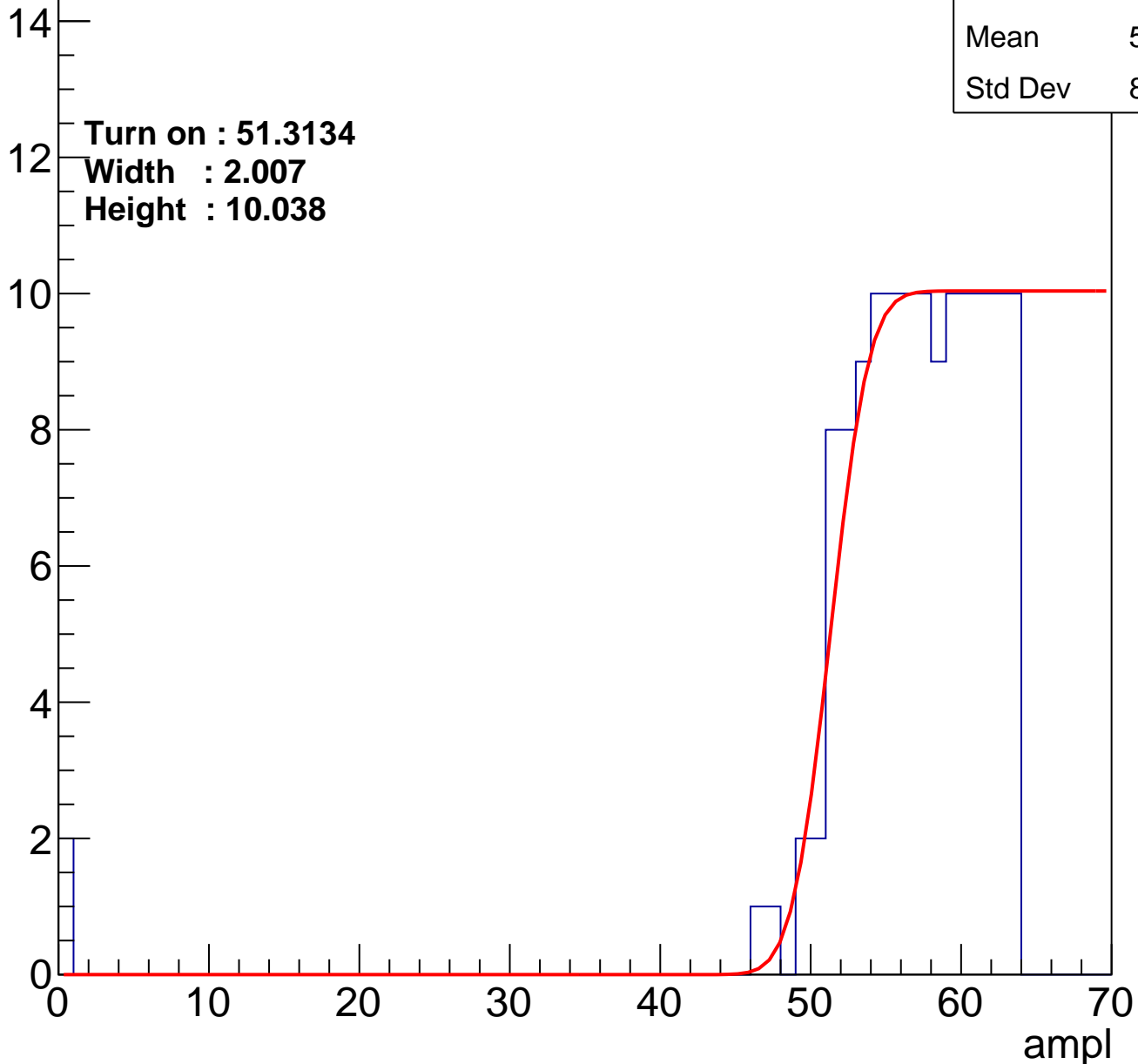
Entries	132
Mean	55.94
Std Dev	8.014

Turn on : 51.3134

Width : 2.007

Height : 10.038

Entry



B0L103S, U24-ch108

calib_packv5_040323_1717.root, FC#2, port C3

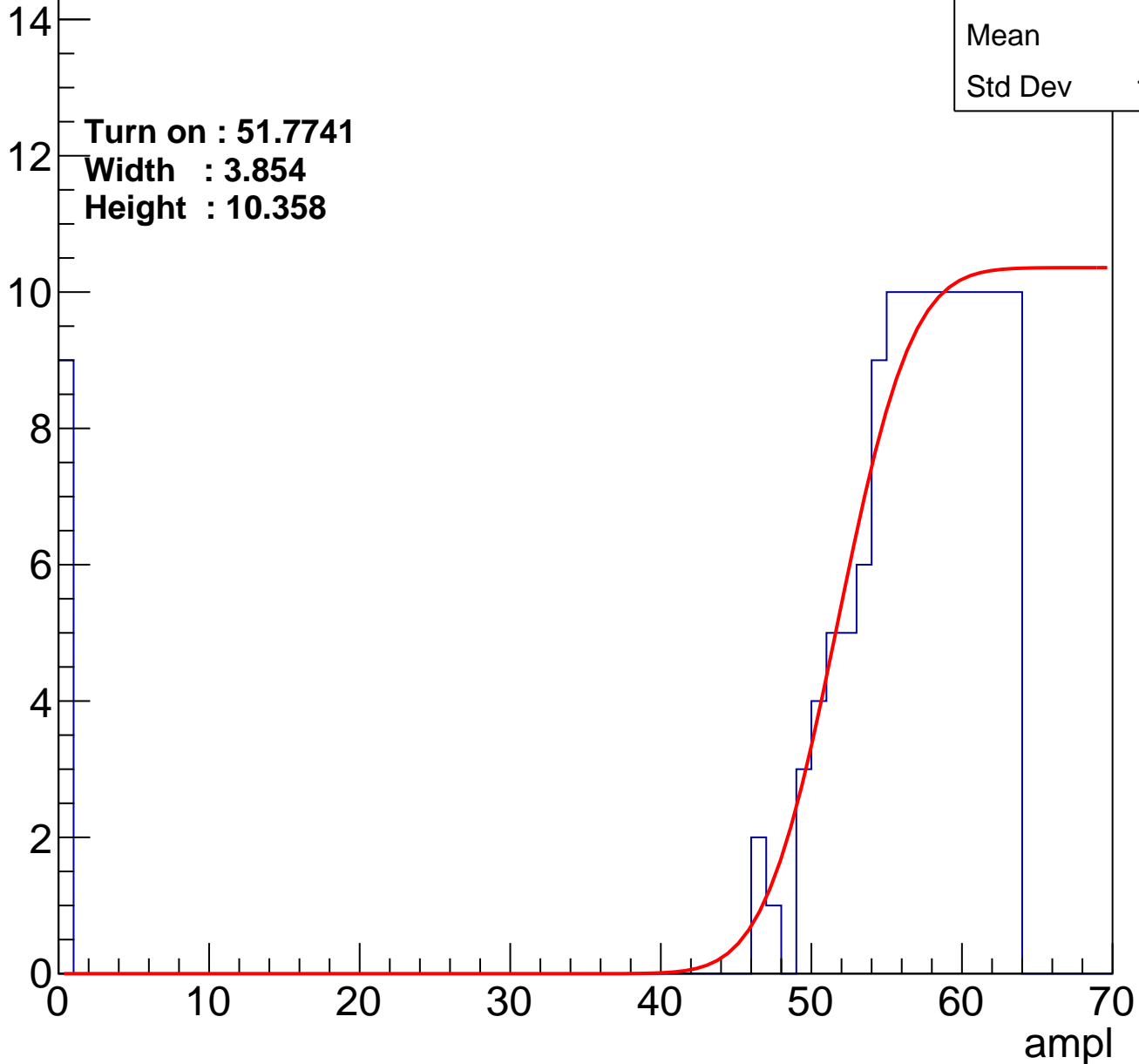
Entries	134
Mean	53.1
Std Dev	14.81

Turn on : 51.7741

Width : 3.854

Height : 10.358

Entry



B0L103S, U24-ch109

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	54.7
Std Dev	11.8

Turn on : 50.7293

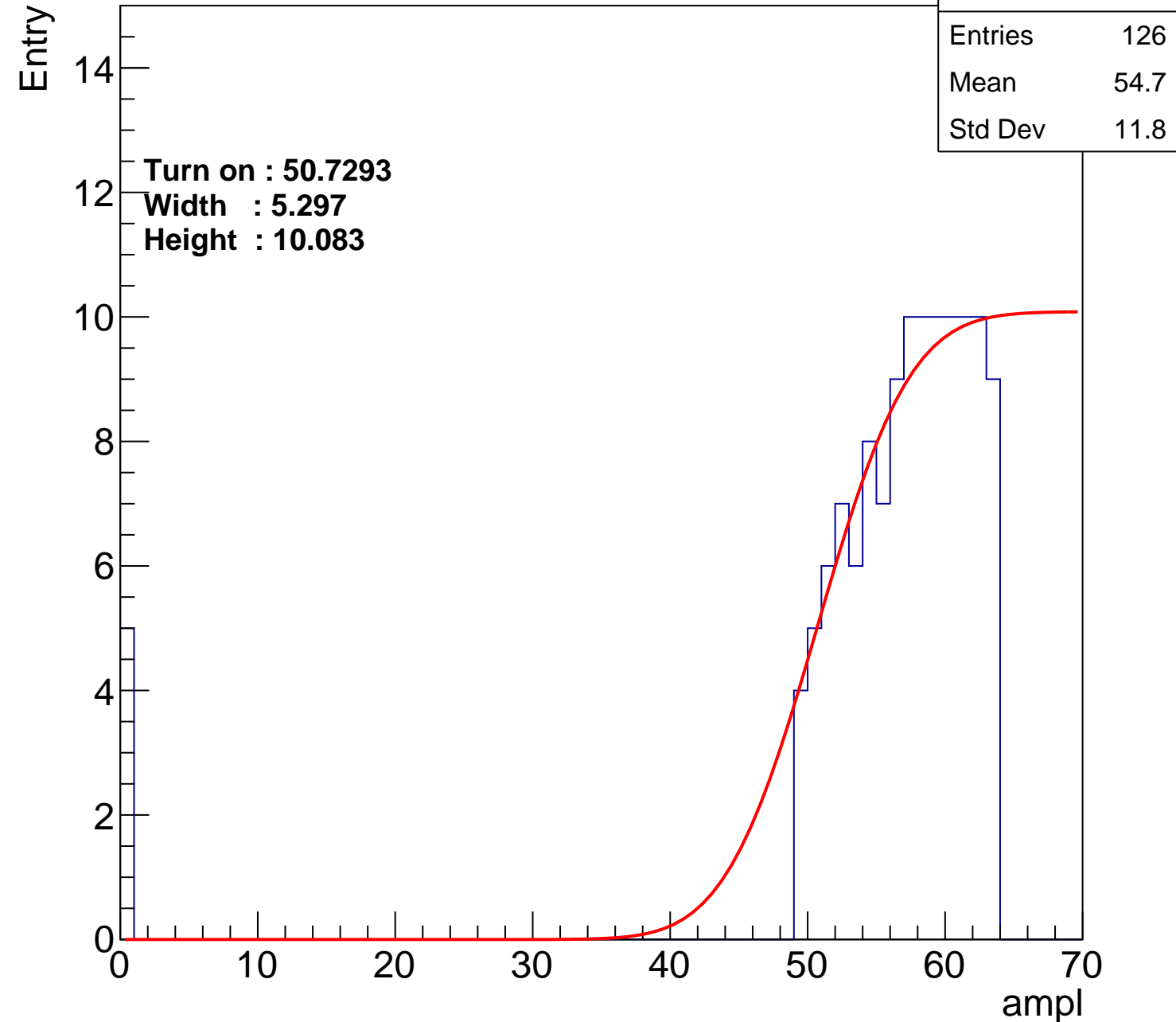
Width : 5.297

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch110

calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	55.64
Std Dev	11.26

Turn on : 53.2132

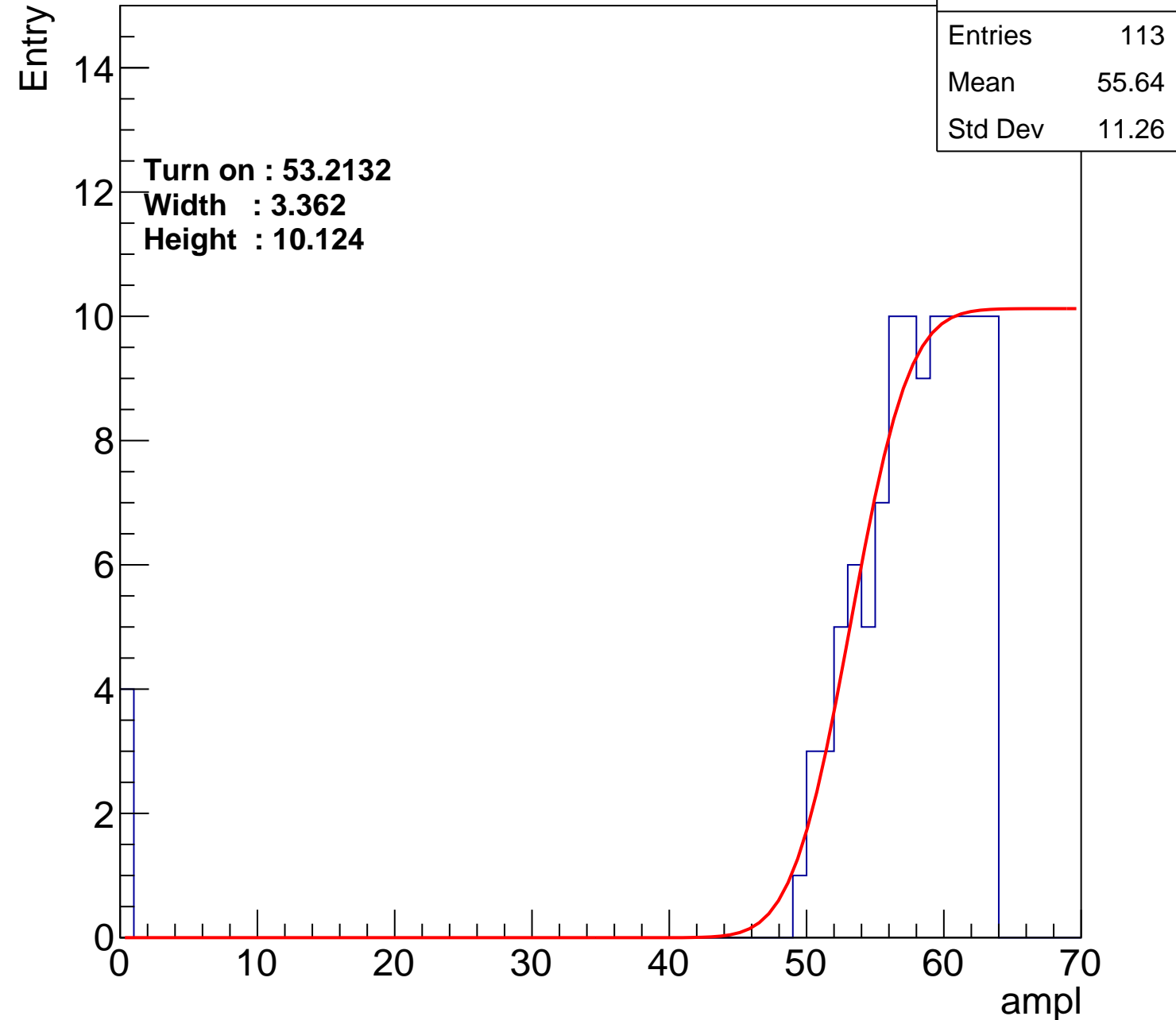
Width : 3.362

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch111

calib_packv5_040323_1717.root, FC#2, port C3

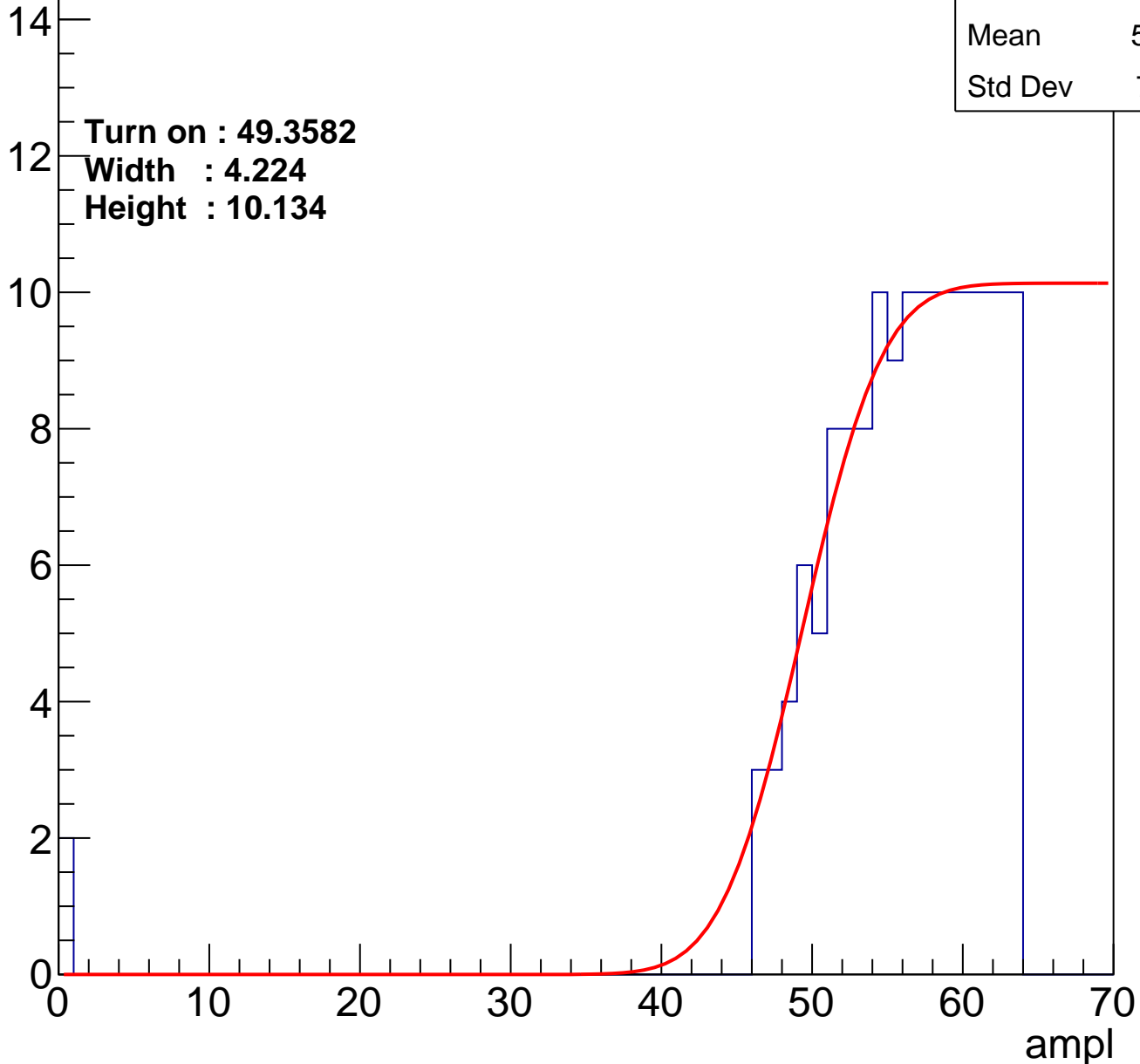
Entries	146
Mean	55.19
Std Dev	7.981

Turn on : 49.3582

Width : 4.224

Height : 10.134

Entry



B0L103S, U24-ch112

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	53.88
Std Dev	13.11

Turn on : 50.3275

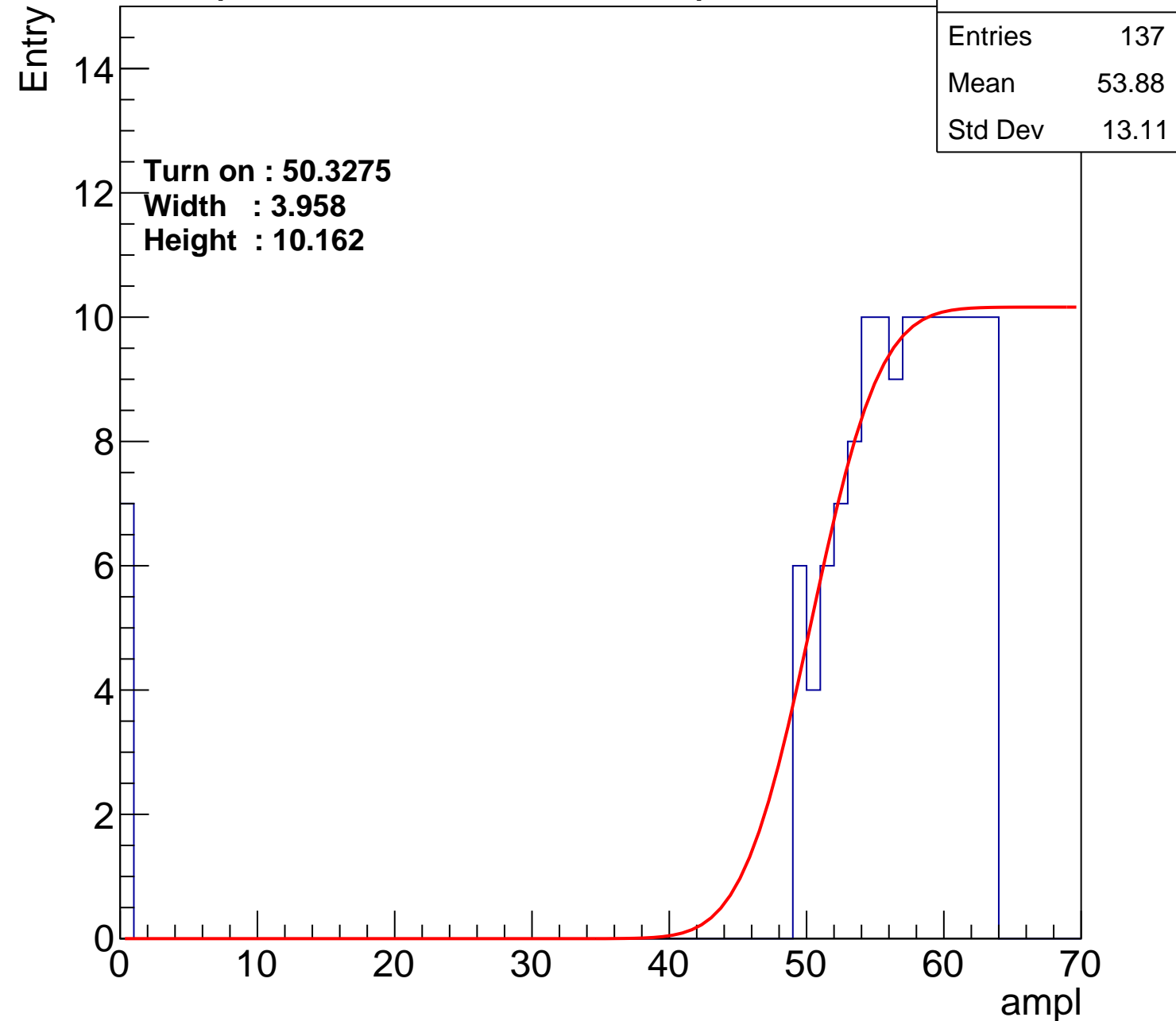
Width : 3.958

Height : 10.162

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch113

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	55.43
Std Dev	9.552

Turn on : 51.8490

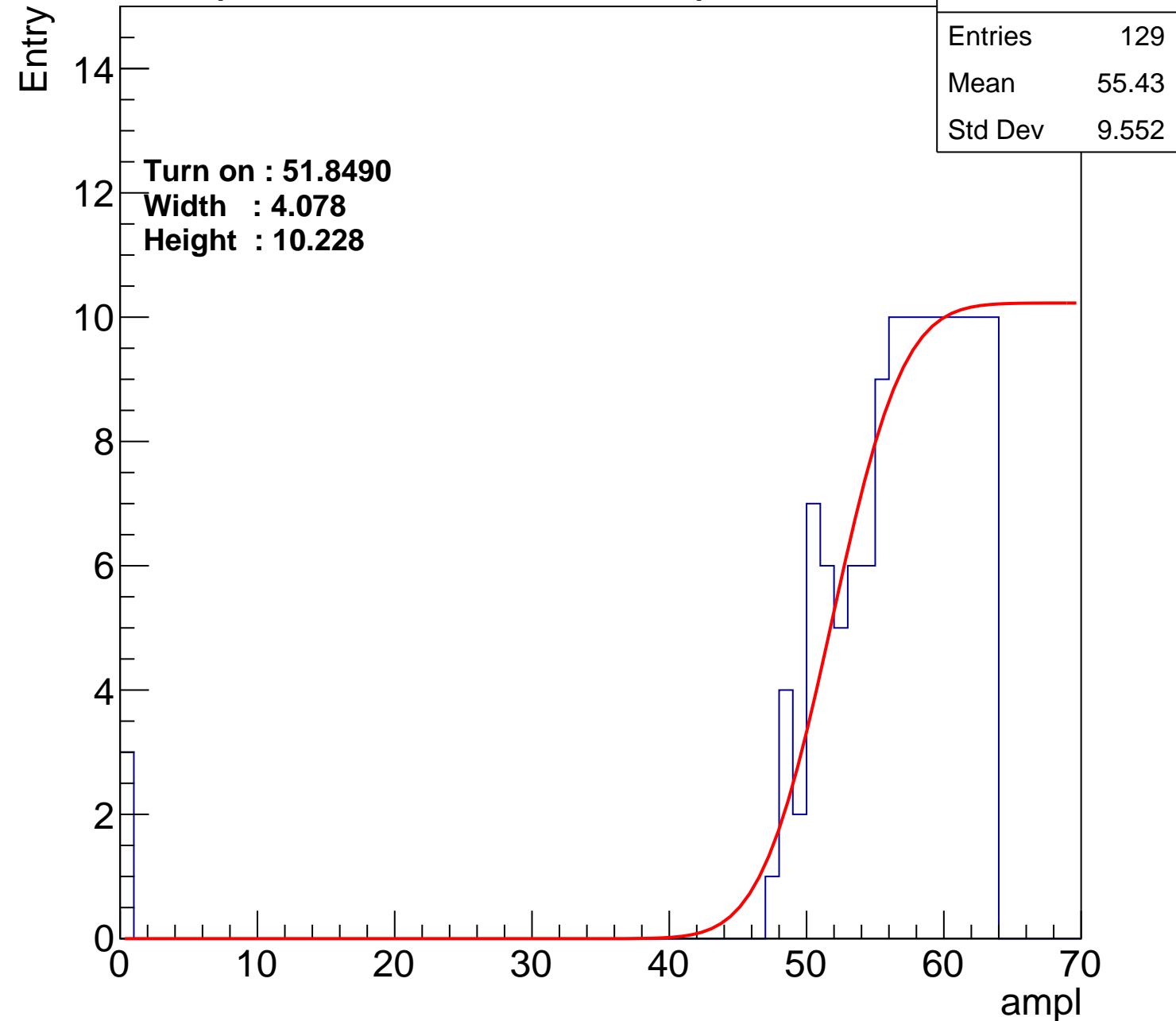
Width : 4.078

Height : 10.228

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch114

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.98
Std Dev	8.291

Turn on : 52.1706

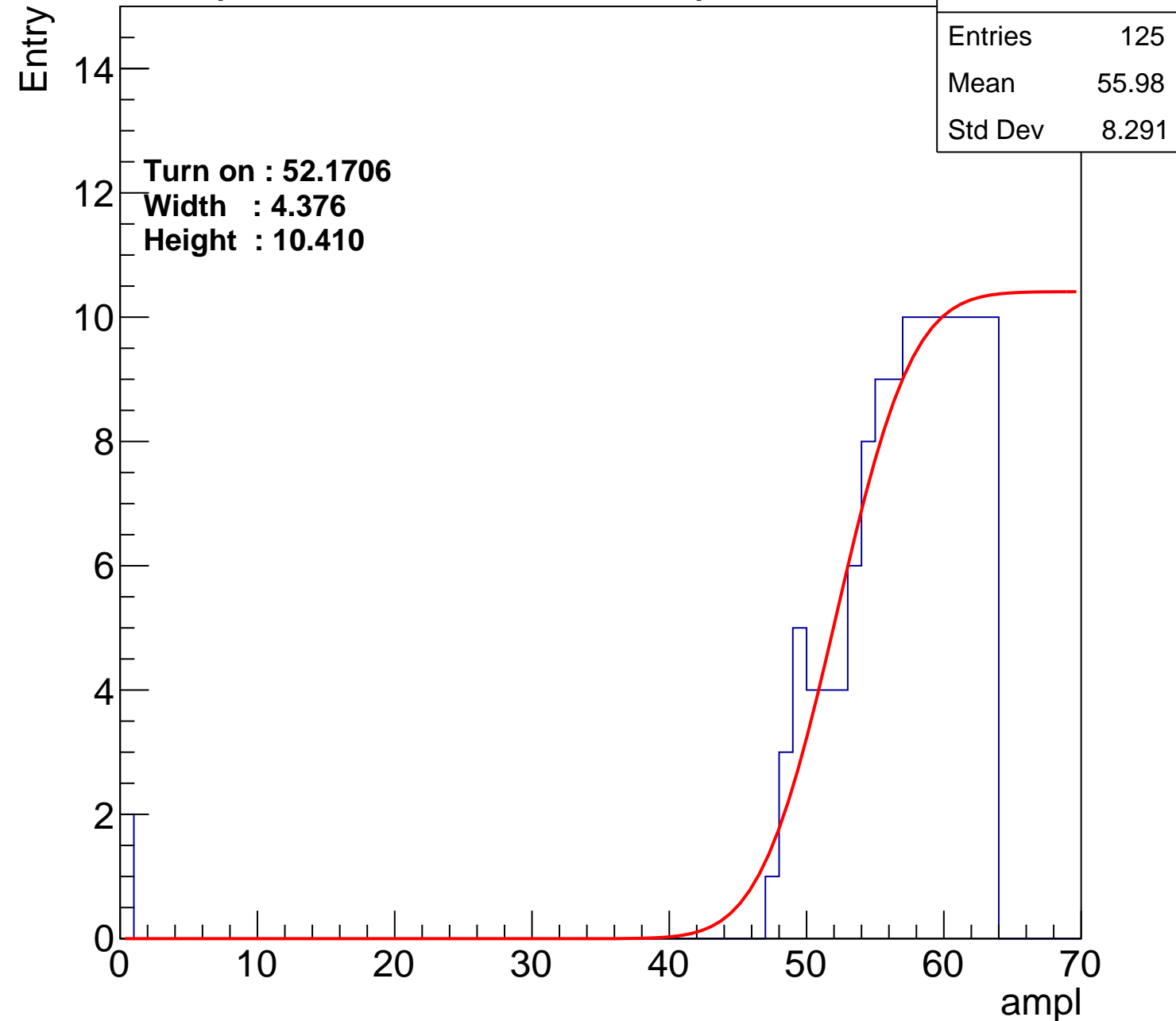
Width : 4.376

Height : 10.410

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch115

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	54.83
Std Dev	10.4

Turn on : 50.3942

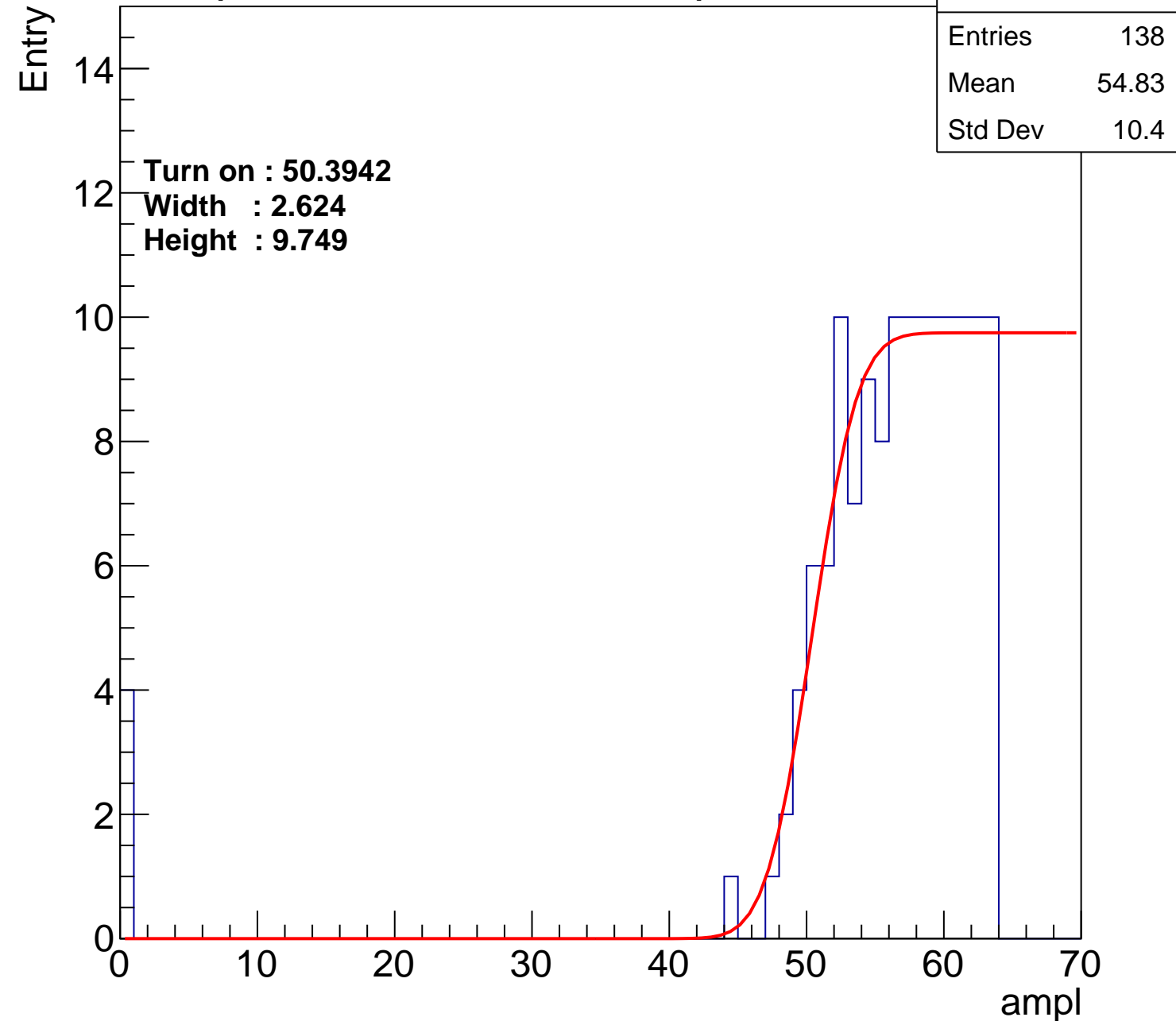
Width : 2.624

Height : 9.749

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch116

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	53.3
Std Dev	13.65

Turn on : 51.1691

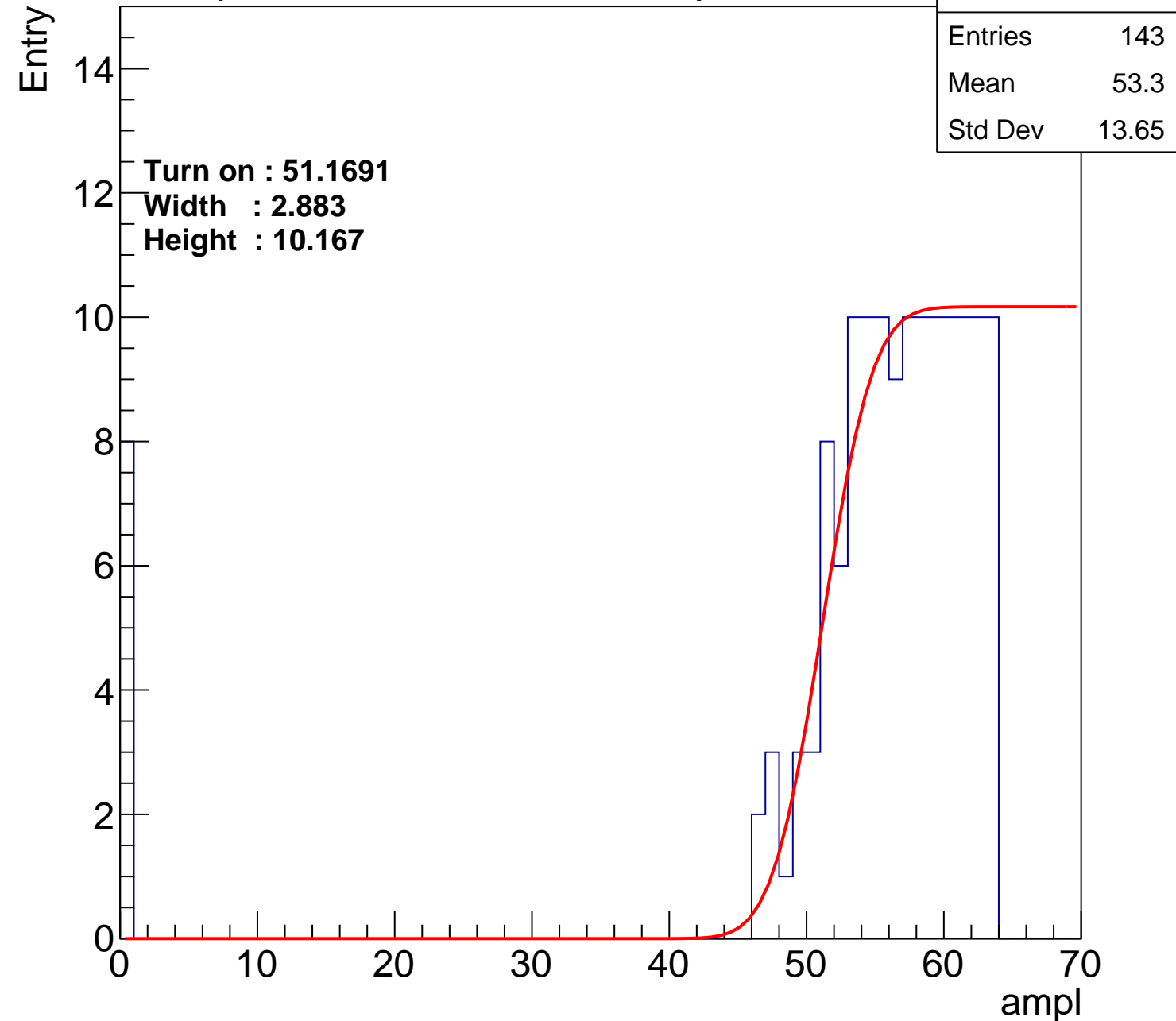
Width : 2.883

Height : 10.167

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch117

calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	55.62
Std Dev	11.17

Turn on : 53.5146

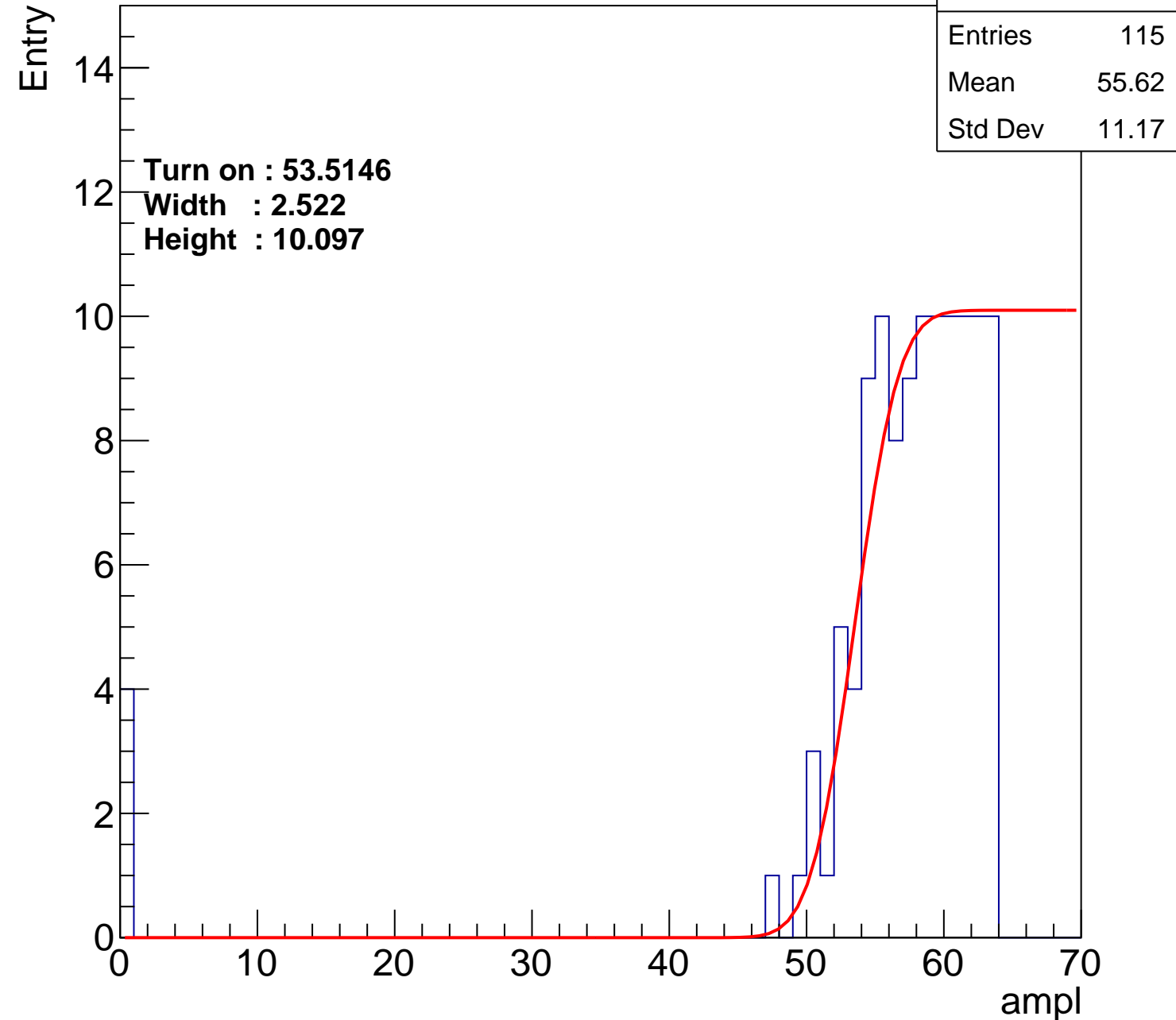
Width : 2.522

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch118

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	54.51
Std Dev	12.86

Turn on : 52.1991

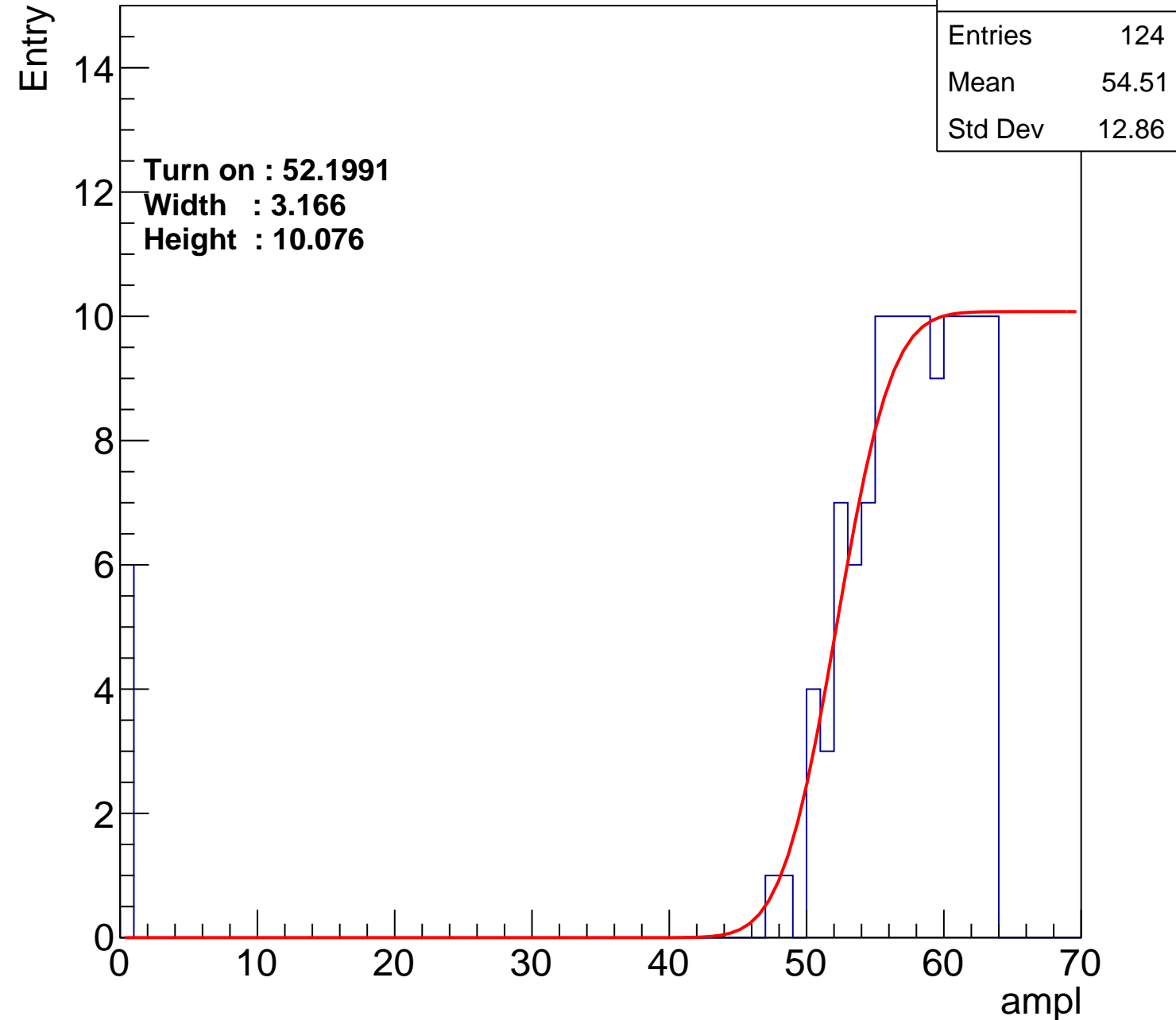
Width : 3.166

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch119

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.36
Std Dev	9.478

Turn on : 50.9842

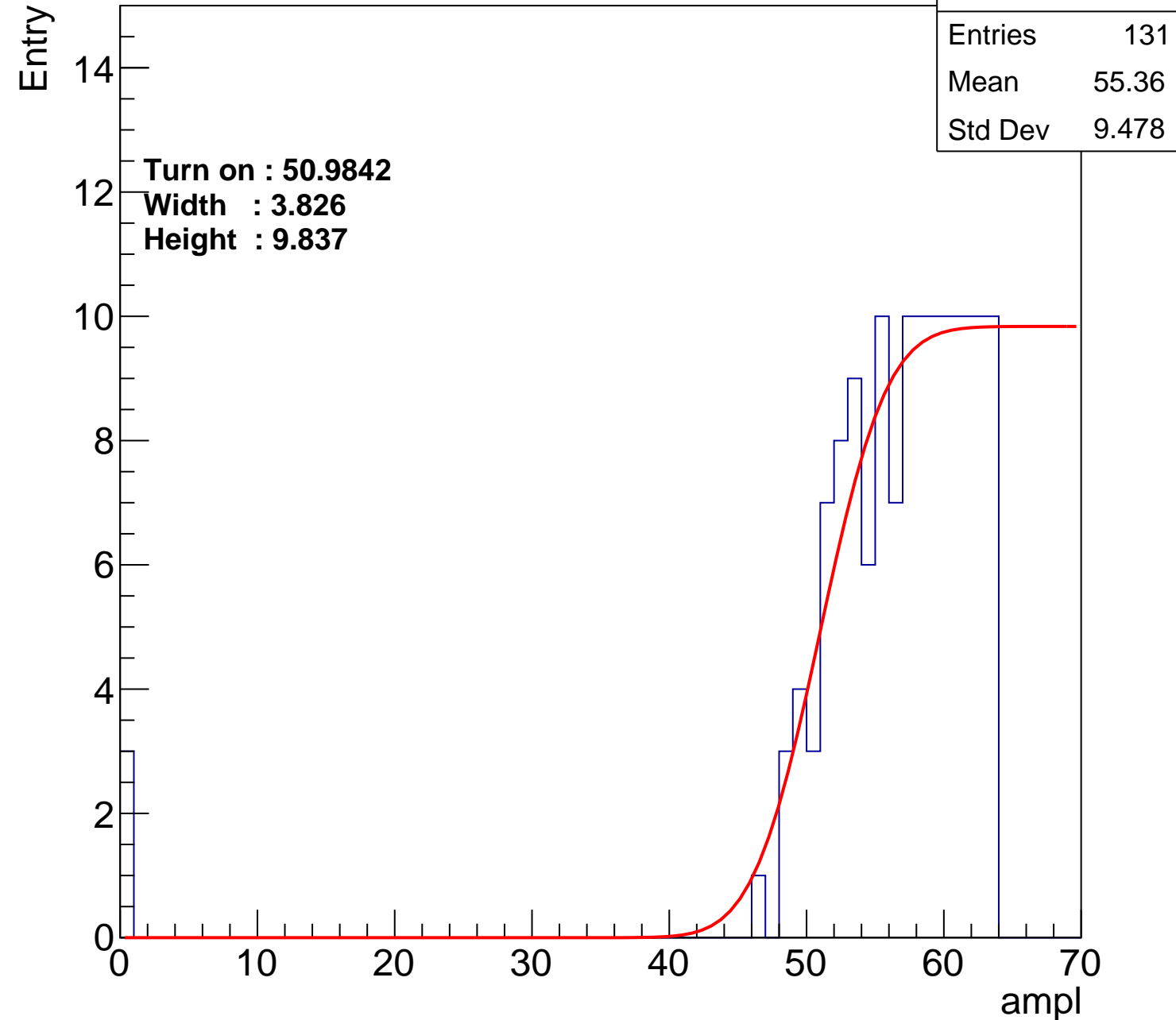
Width : 3.826

Height : 9.837

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch120

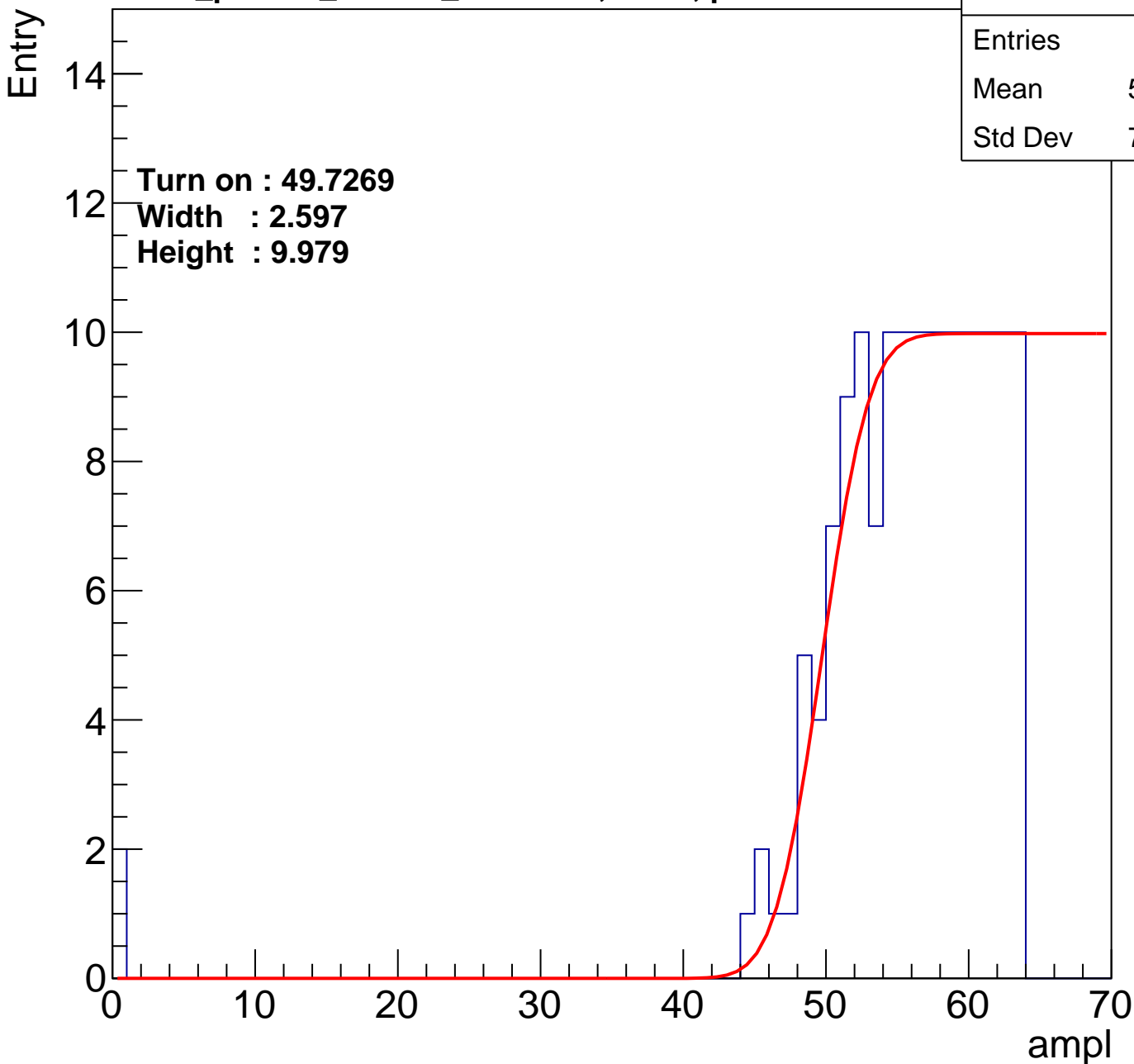
calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	55.12
Std Dev	7.938

Turn on : 49.7269

Width : 2.597

Height : 9.979



B0L103S, U24-ch121

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.09
Std Dev	9.301

Turn on : 51.0287

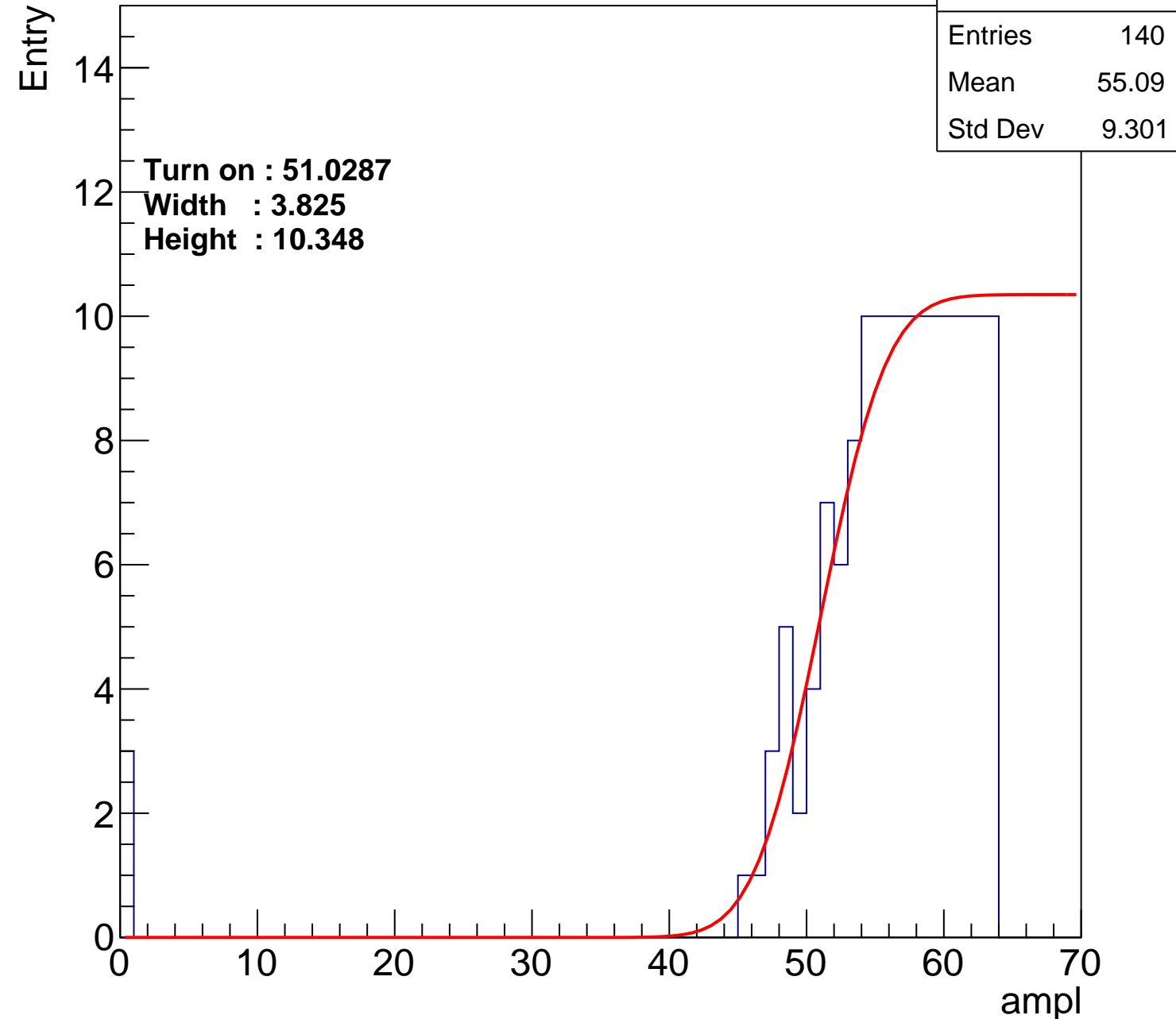
Width : 3.825

Height : 10.348

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch122

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	54.93
Std Dev	11.95

Turn on : 52.3600

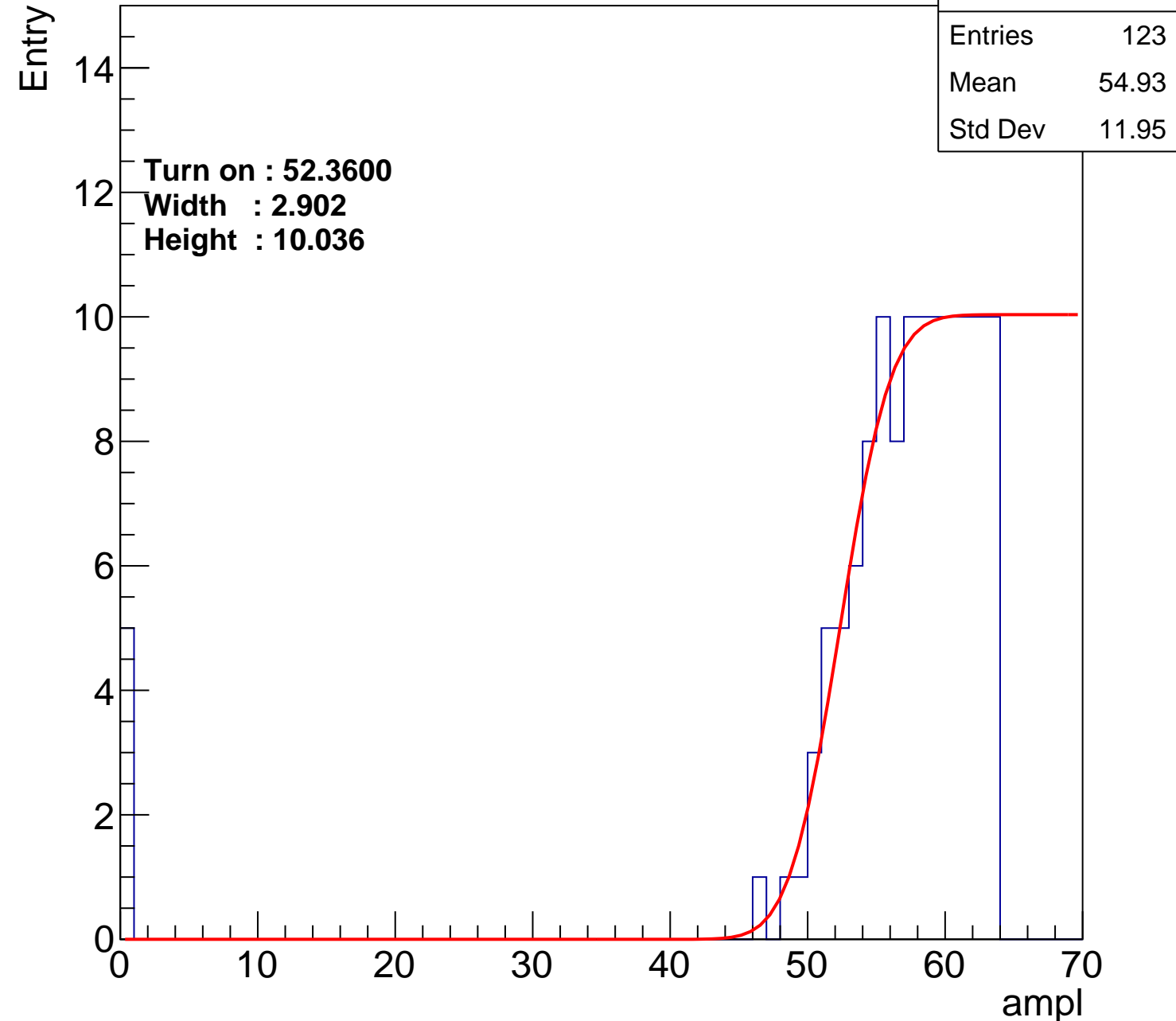
Width : 2.902

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch123

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	56.45
Std Dev	6.543

Turn on : 51.3834

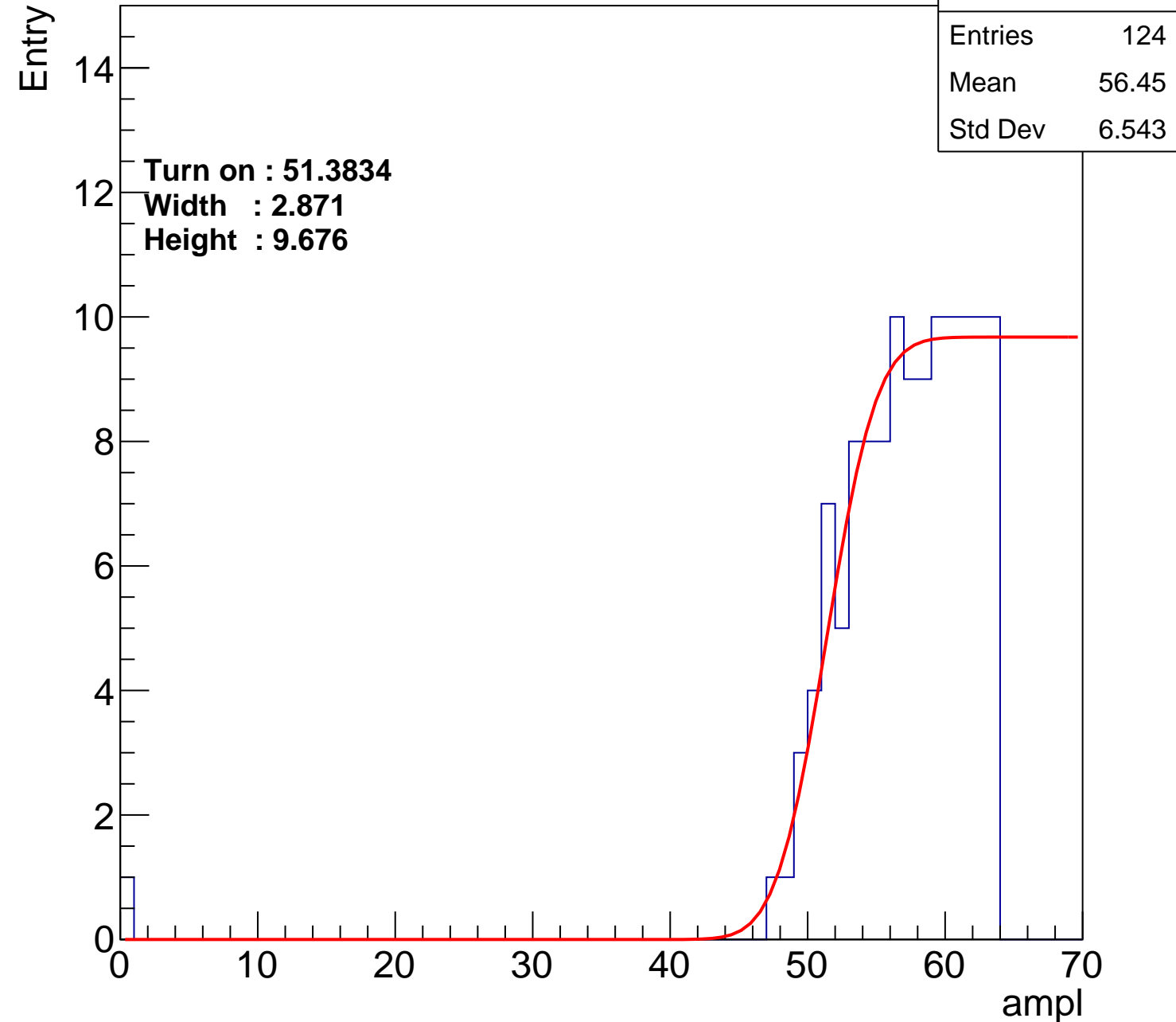
Width : 2.871

Height : 9.676

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch124

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	53.23
Std Dev	14.18

Turn on : 52.0570

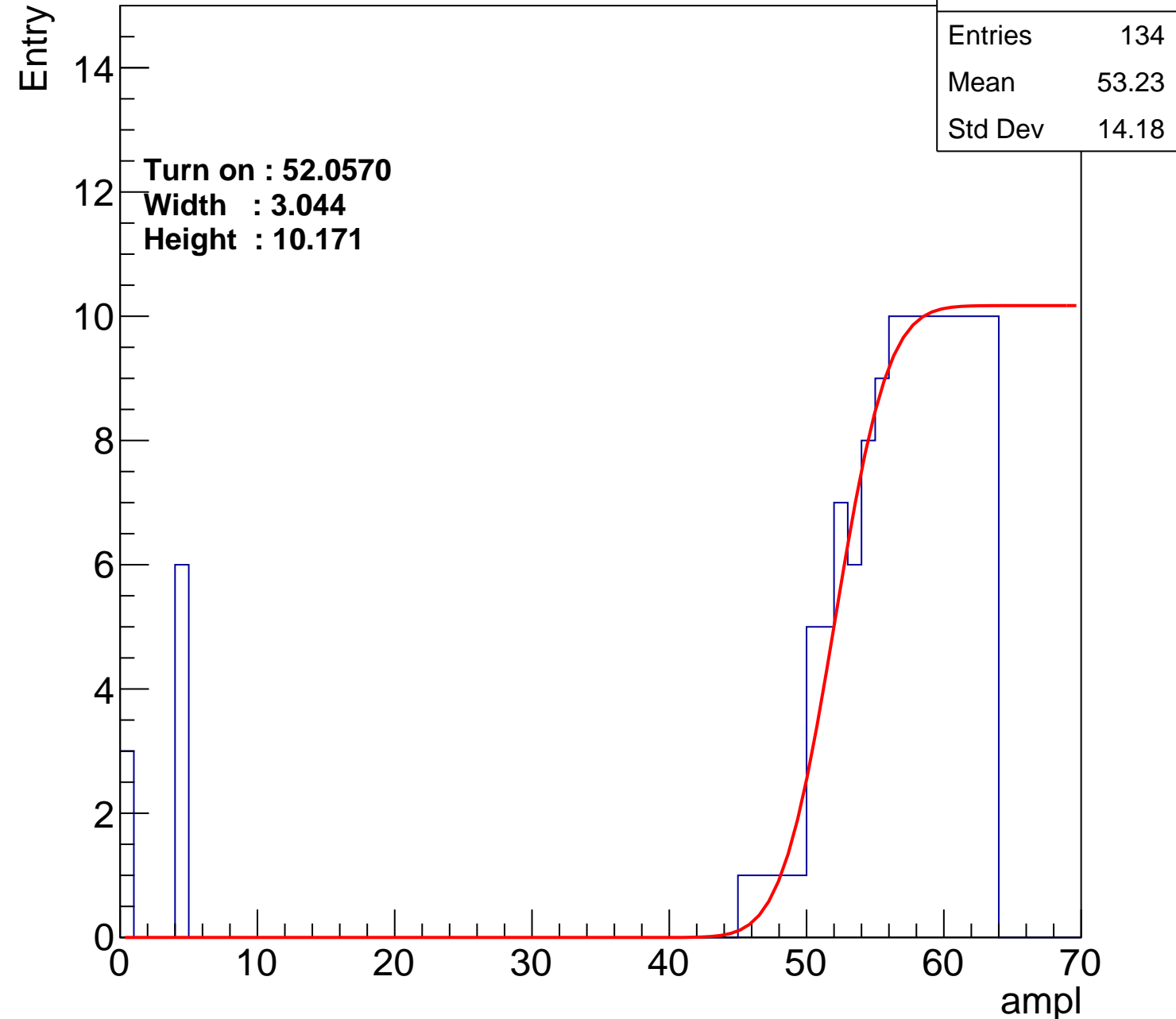
Width : 3.044

Height : 10.171

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch125

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	54.71
Std Dev	11.67

Turn on : 51.5829

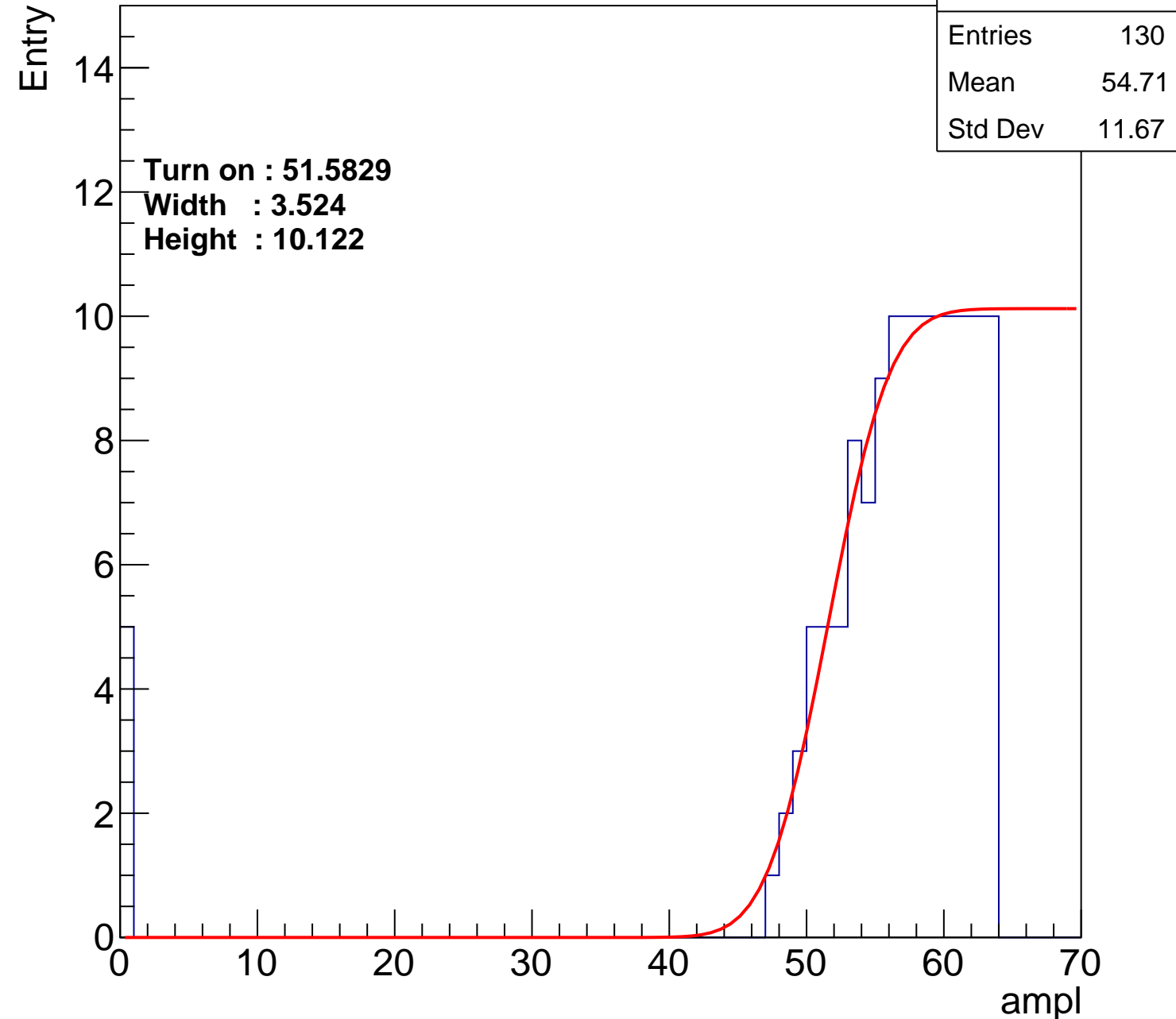
Width : 3.524

Height : 10.122

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch126

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.6
Std Dev	8.032

Turn on : 50.8747

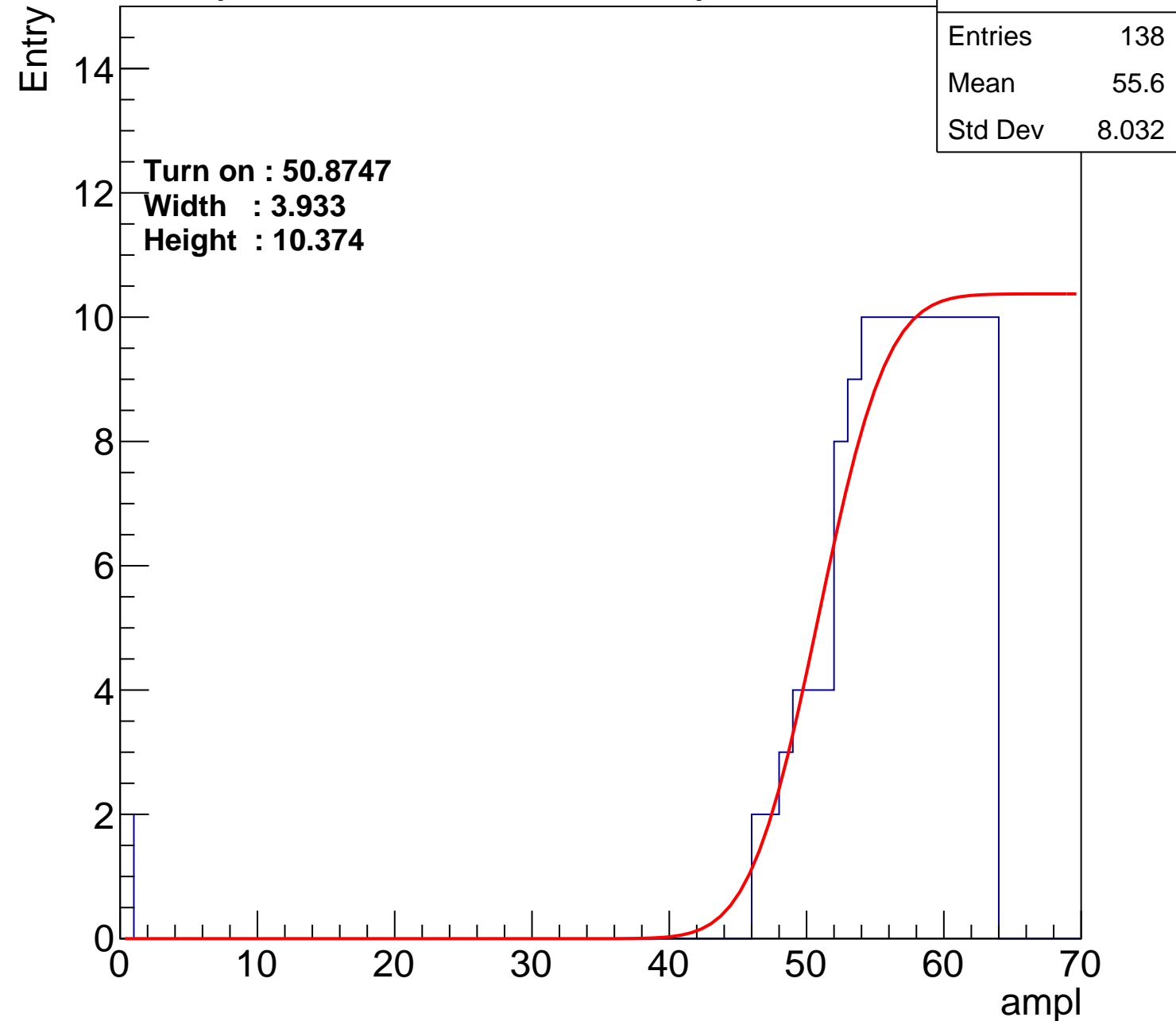
Width : 3.933

Height : 10.374

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	55.9
Std Dev	6.628

Turn on : 50.5594

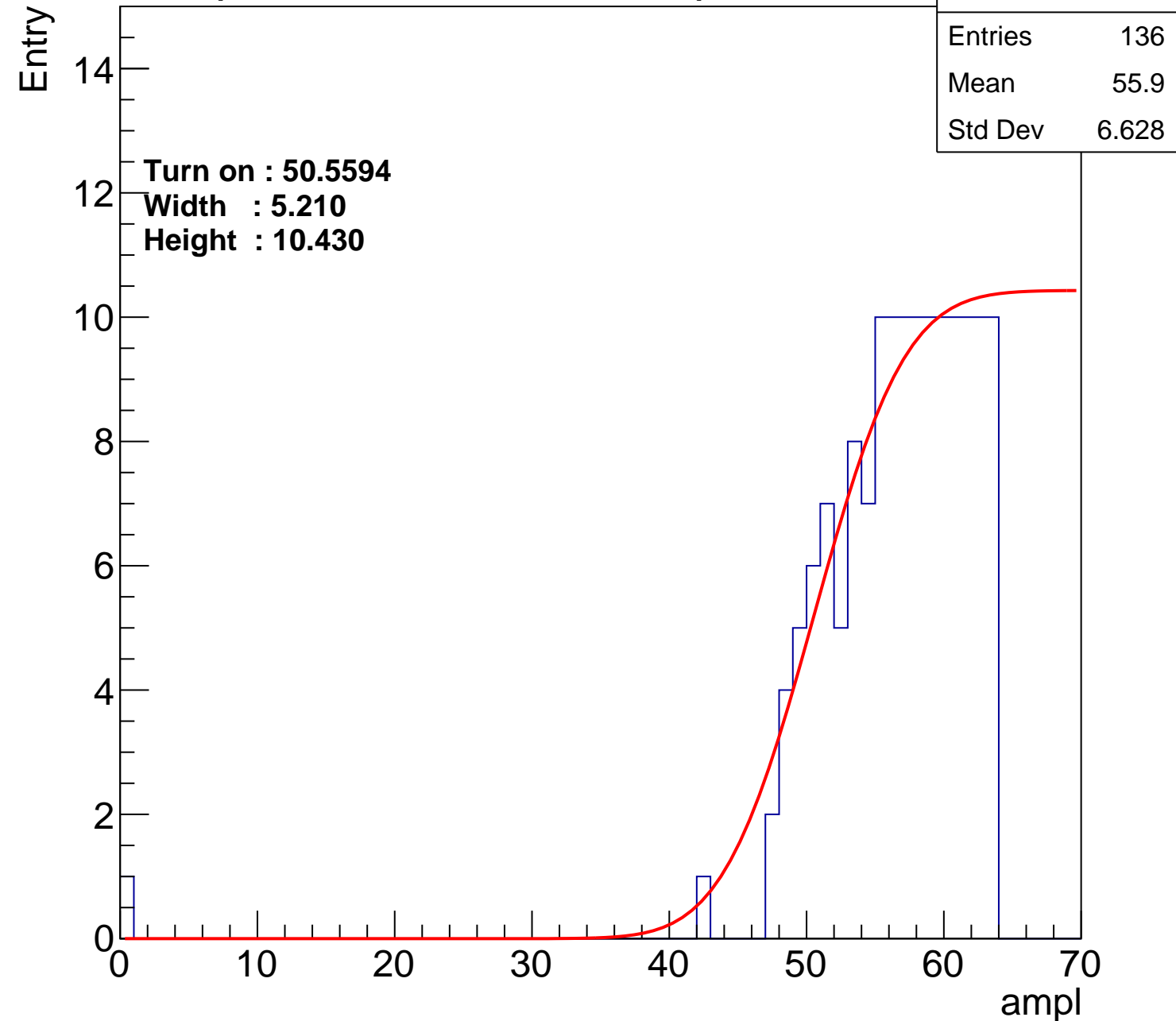
Width : 5.210

Height : 10.430

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U24-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	55.9
Std Dev	6.628

Turn on : 50.5594

Width : 5.210

Height : 10.430

Entry

14
12
10
8
6
4
2
0

ampl

