



# B1L003S, U23-ch0, adc0

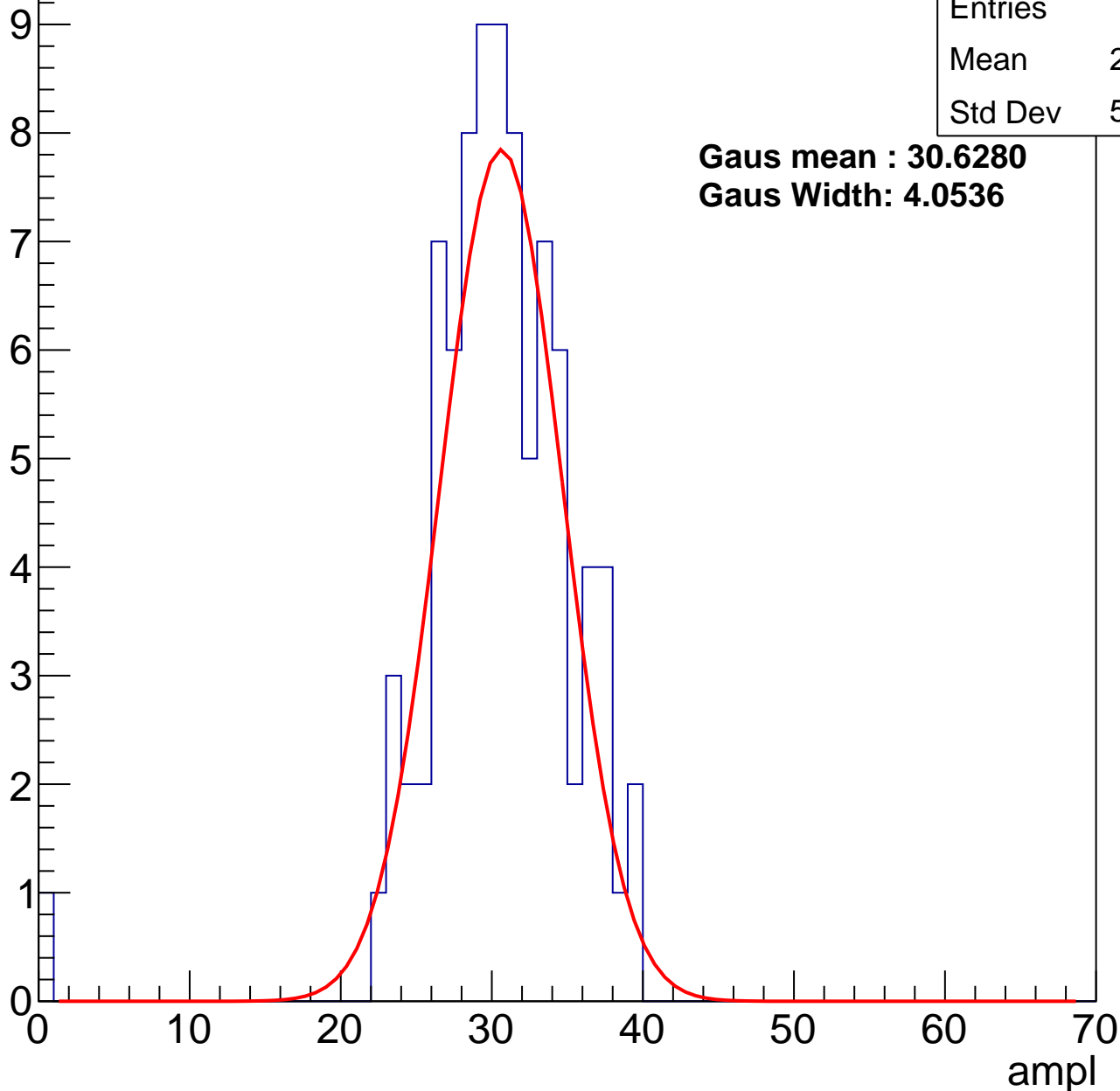
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	87
Mean	29.99
Std Dev	5.078

**Gaus mean : 30.6280**

**Gaus Width: 4.0536**



# B1L003S, U23-ch0, adc1

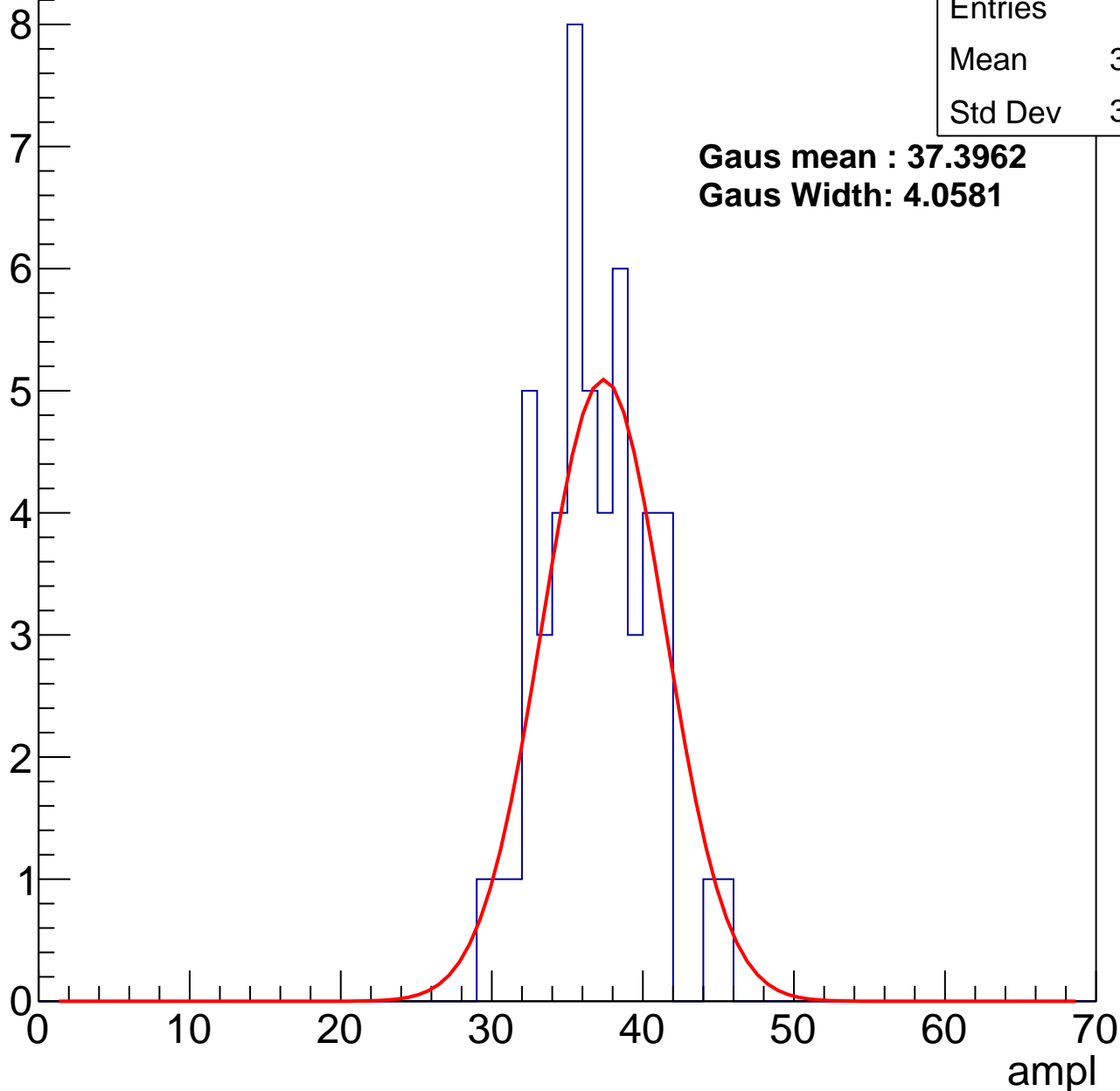
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	36.29
Std Dev	3.432

**Gaus mean : 37.3962**

**Gaus Width: 4.0581**



# B1L003S, U23-ch0, adc2

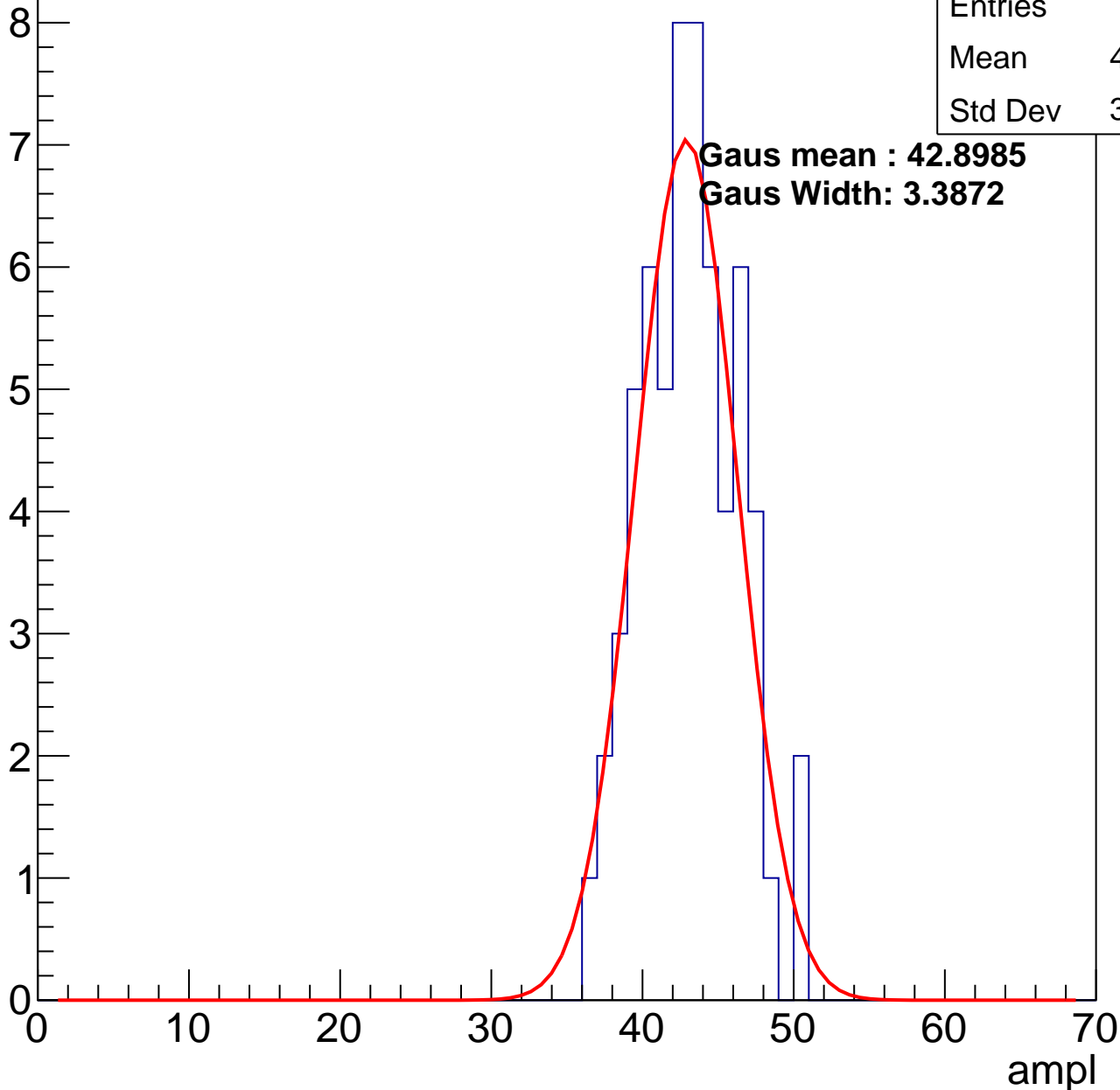
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.62
Std Dev	3.168

**Gaus mean : 42.8985**

**Gaus Width: 3.3872**



# B1L003S, U23-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

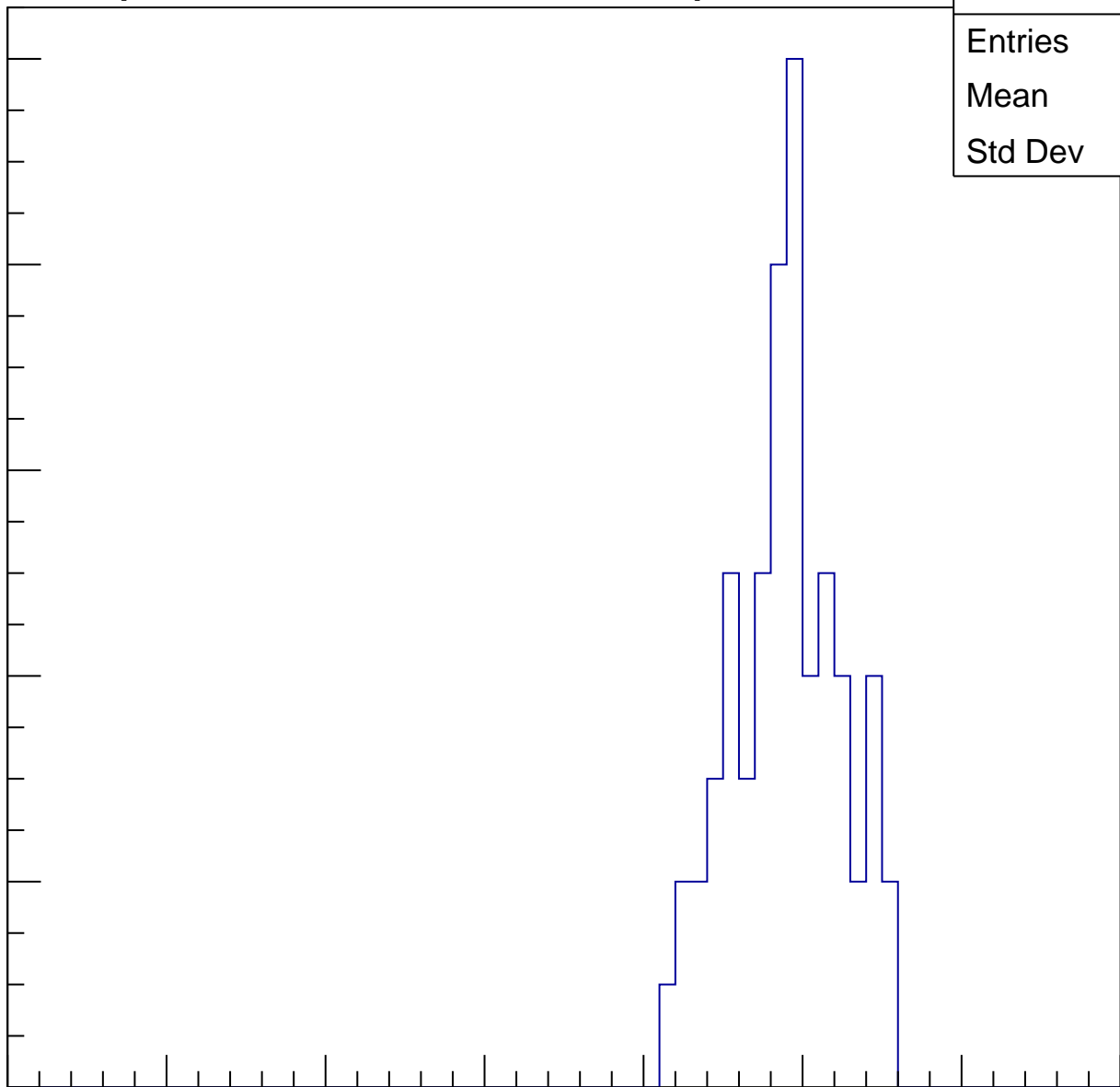
Entries	60
Mean	48.5
Std Dev	3.399

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

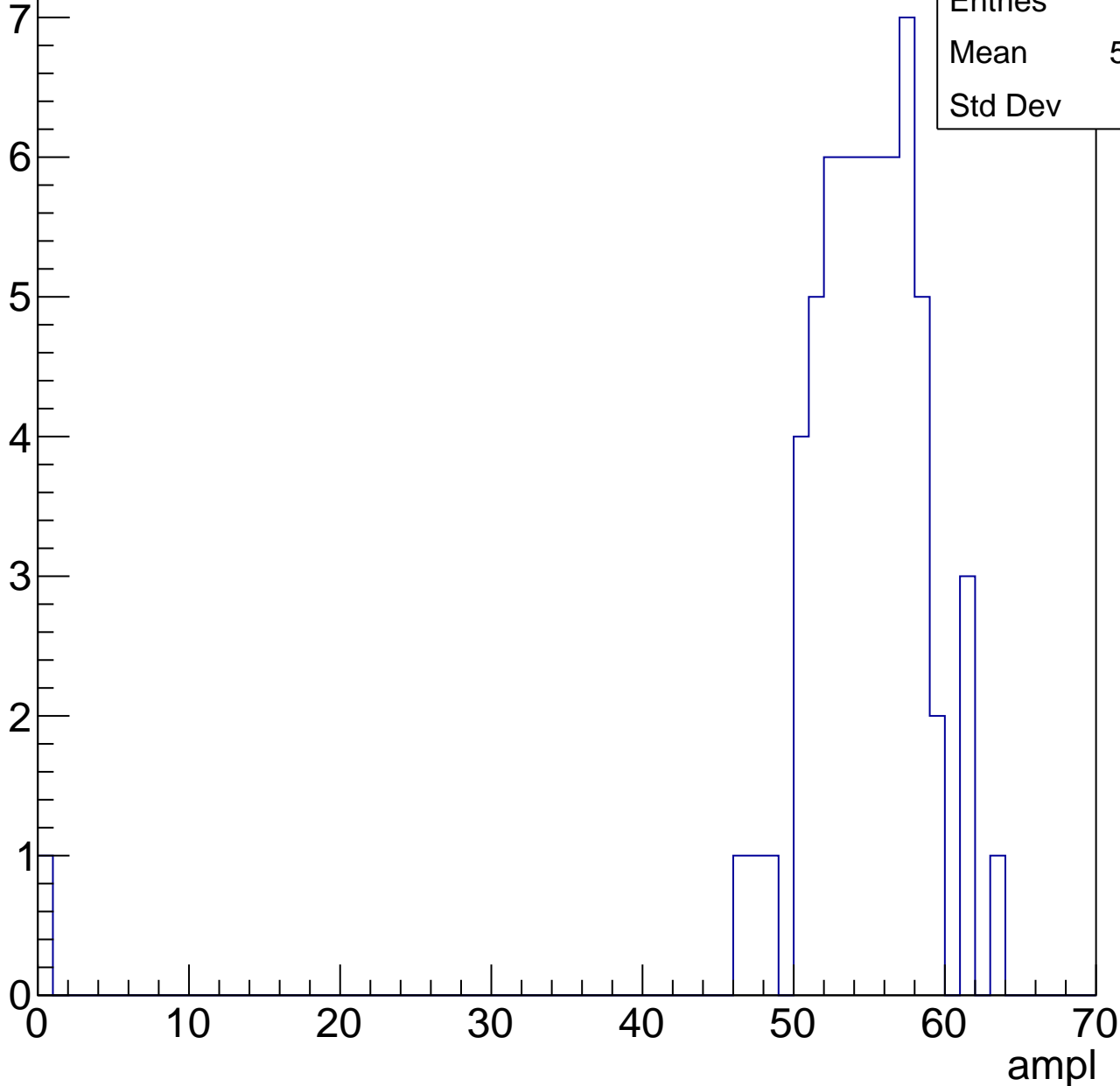


# B1L003S, U23-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	53.59
Std Dev	7.73

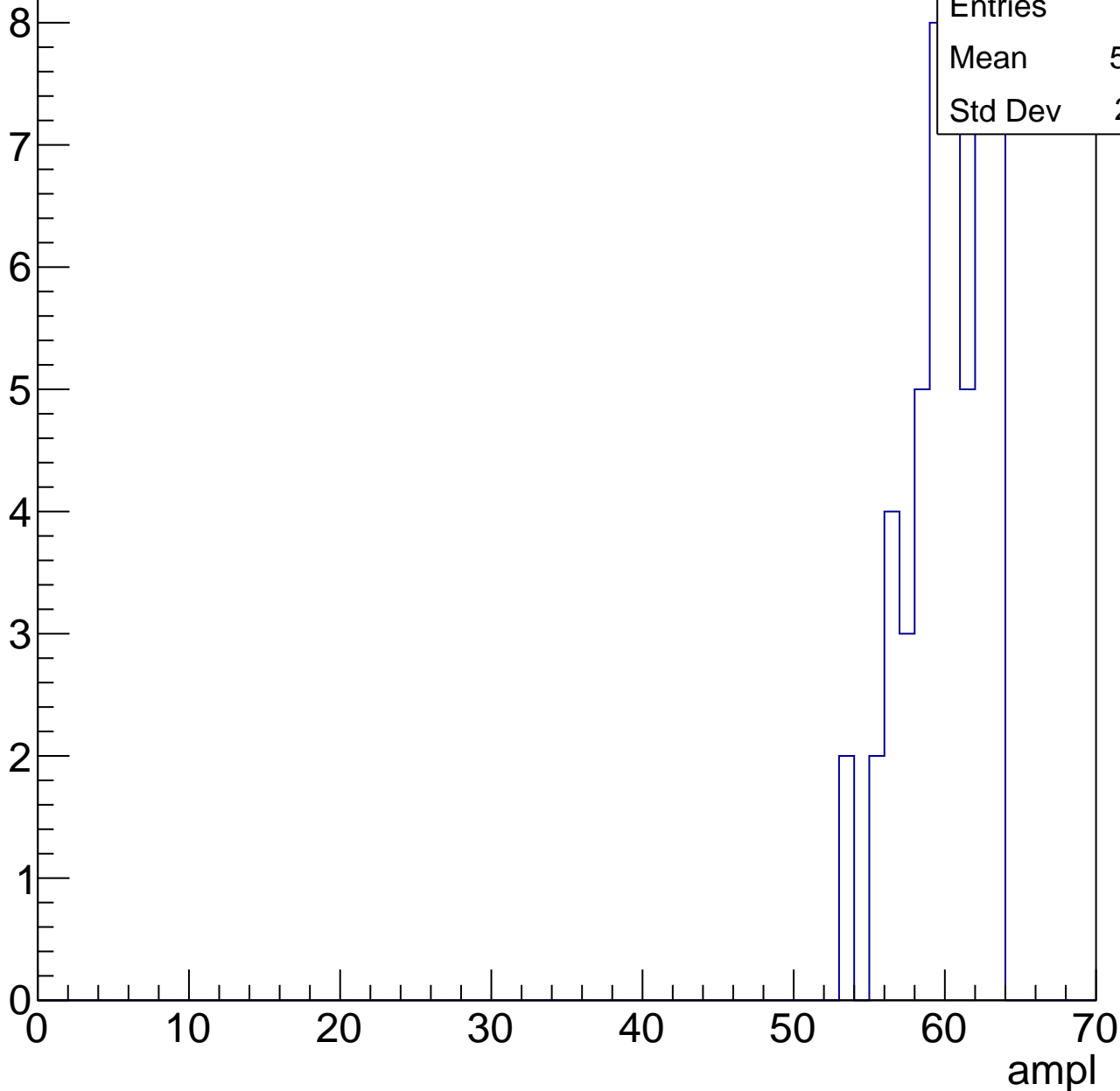


# B1L003S, U23-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	59.58
Std Dev	2.631



# B1L003S, U23-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

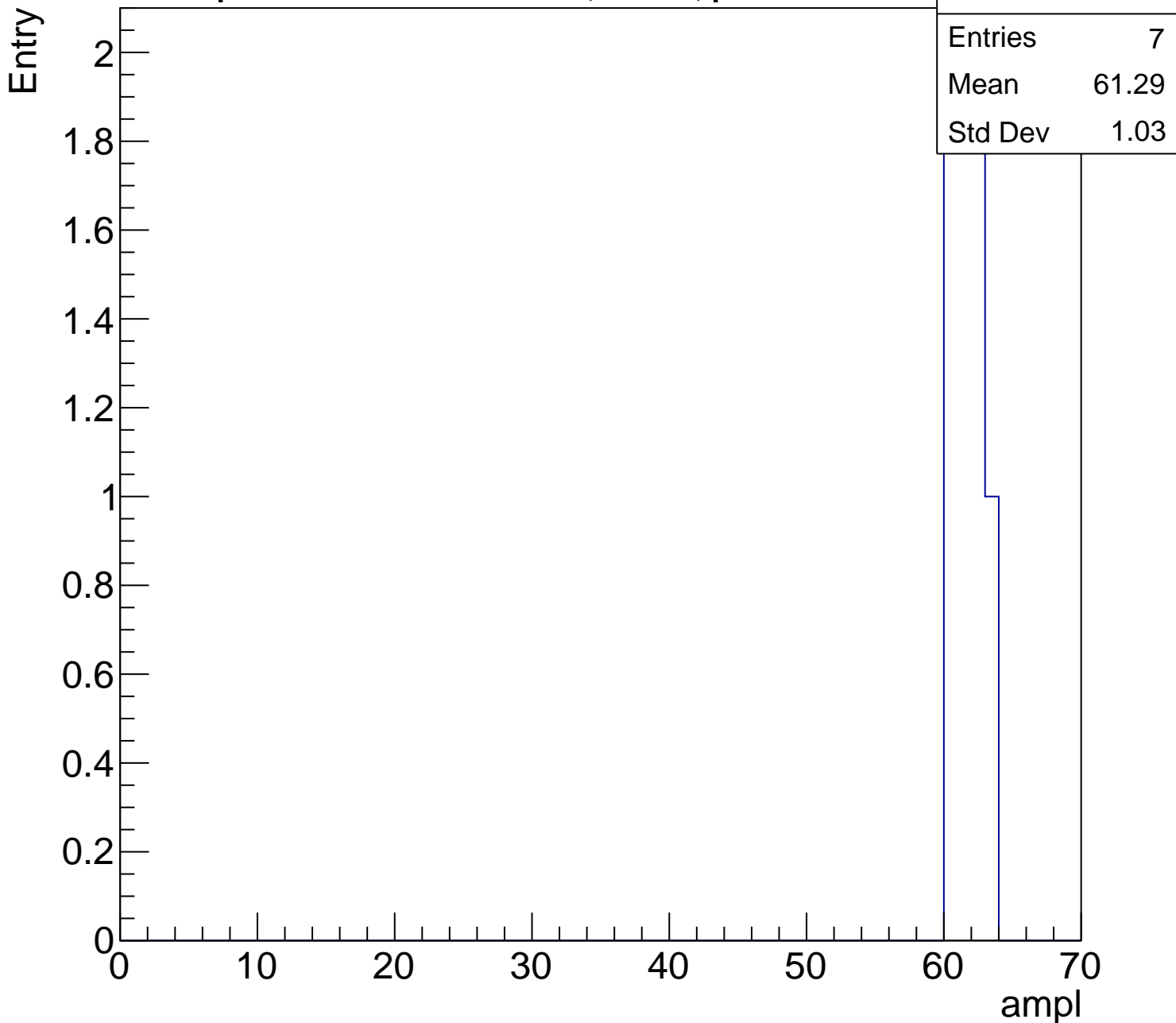
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.29
Std Dev	1.03

0 10 20 30 40 50 60 70

ampl





# B1L003S, U23-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch1, adc0

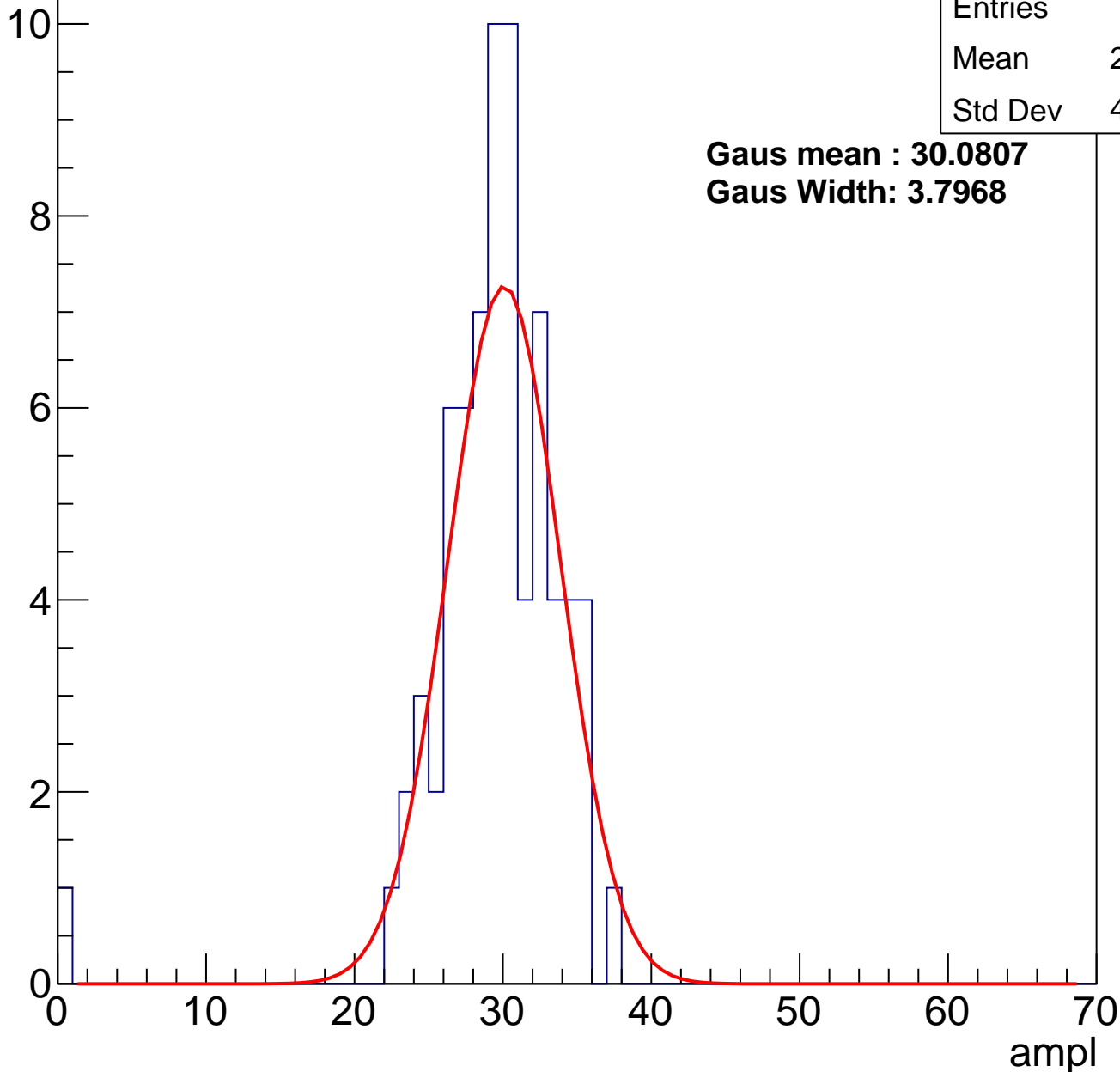
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	72
Mean	28.99
Std Dev	4.736

**Gaus mean : 30.0807**

**Gaus Width: 3.7968**

Entry



# B1L003S, U23-ch1, adc1

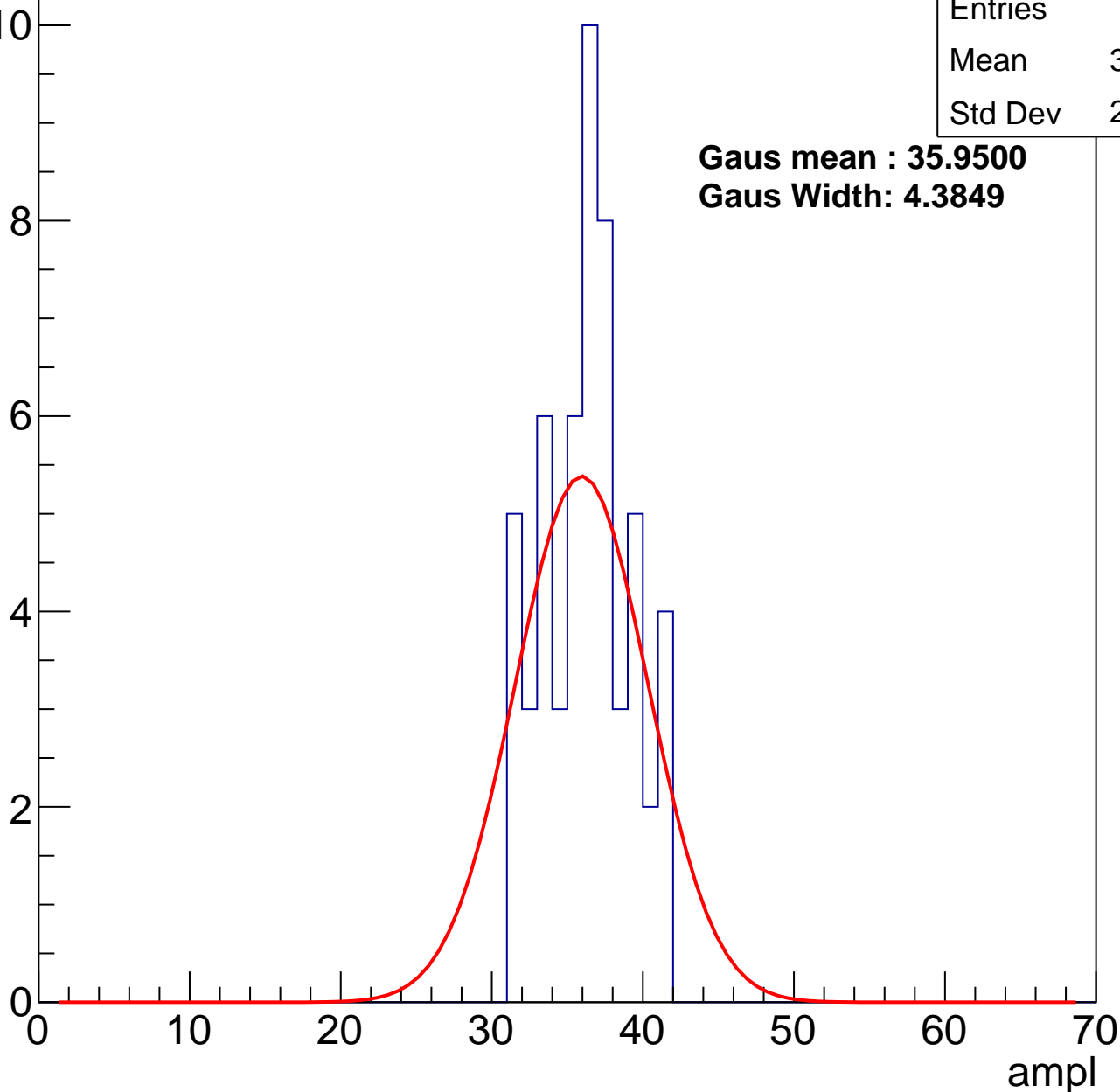
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	35.82
Std Dev	2.829

**Gaus mean : 35.9500**

**Gaus Width: 4.3849**



# B1L003S, U23-ch1, adc2

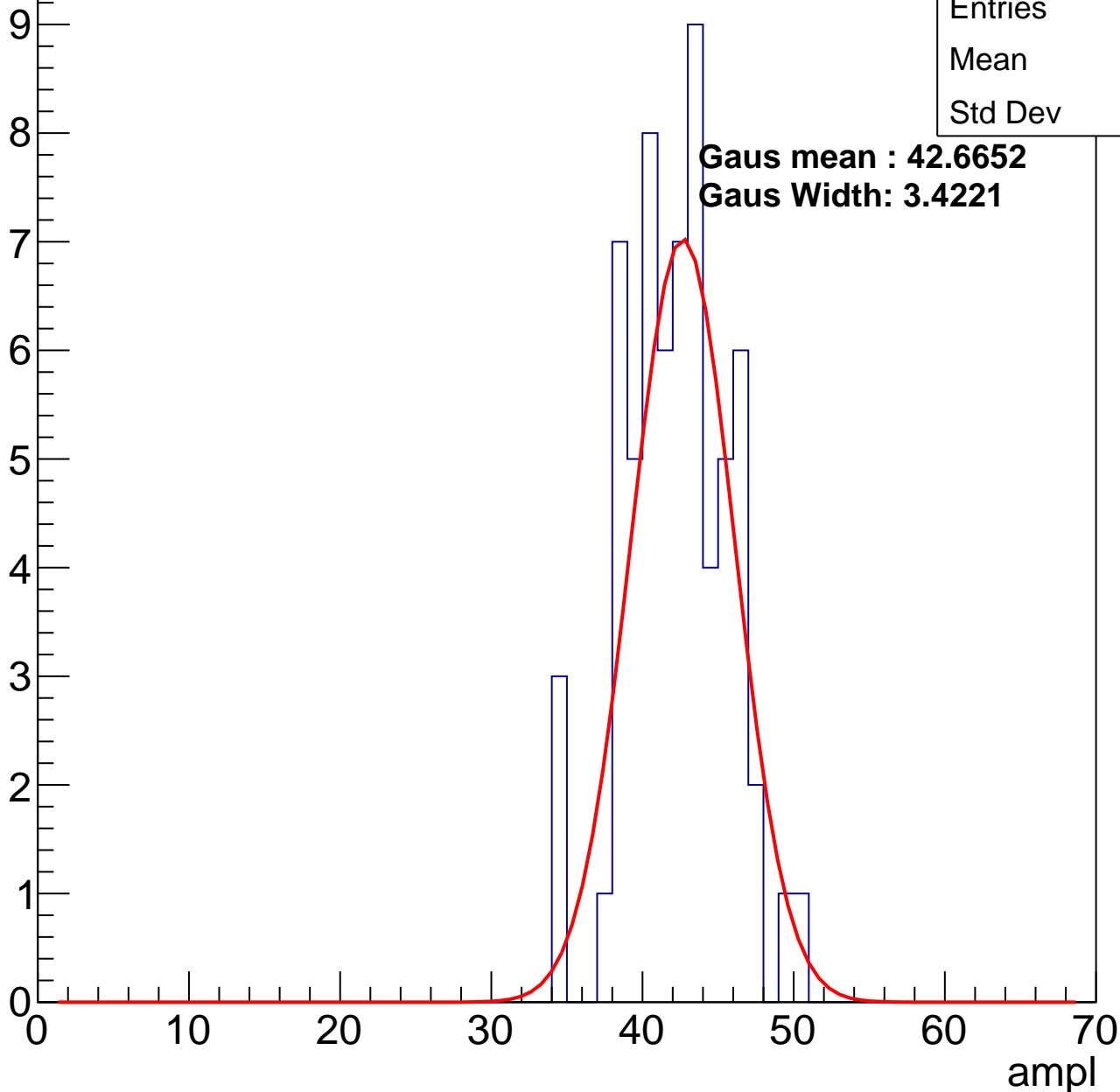
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	41.8
Std Dev	3.37

**Gaus mean : 42.6652**

**Gaus Width: 3.4221**

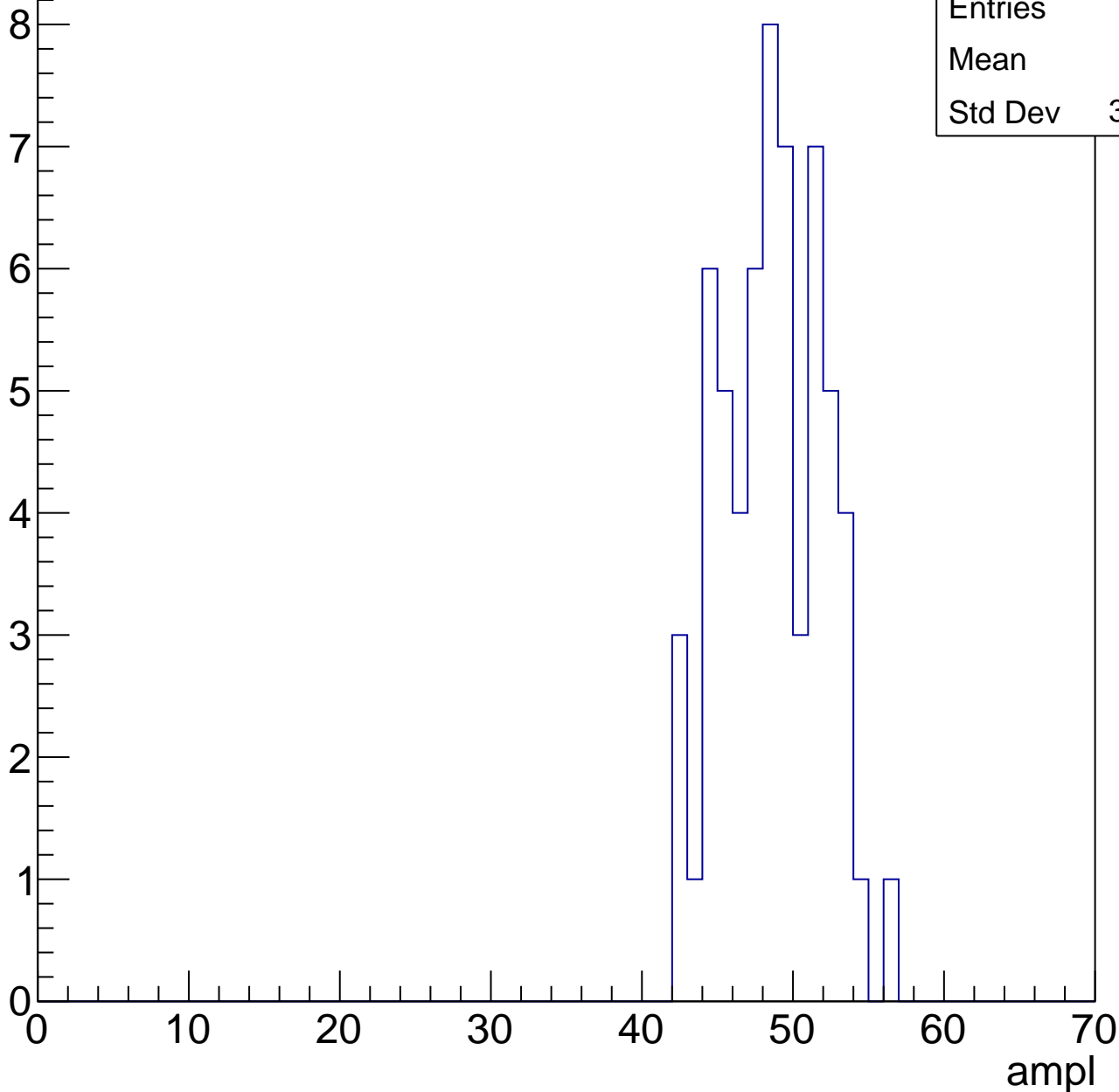


# B1L003S, U23-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.2
Std Dev	3.278

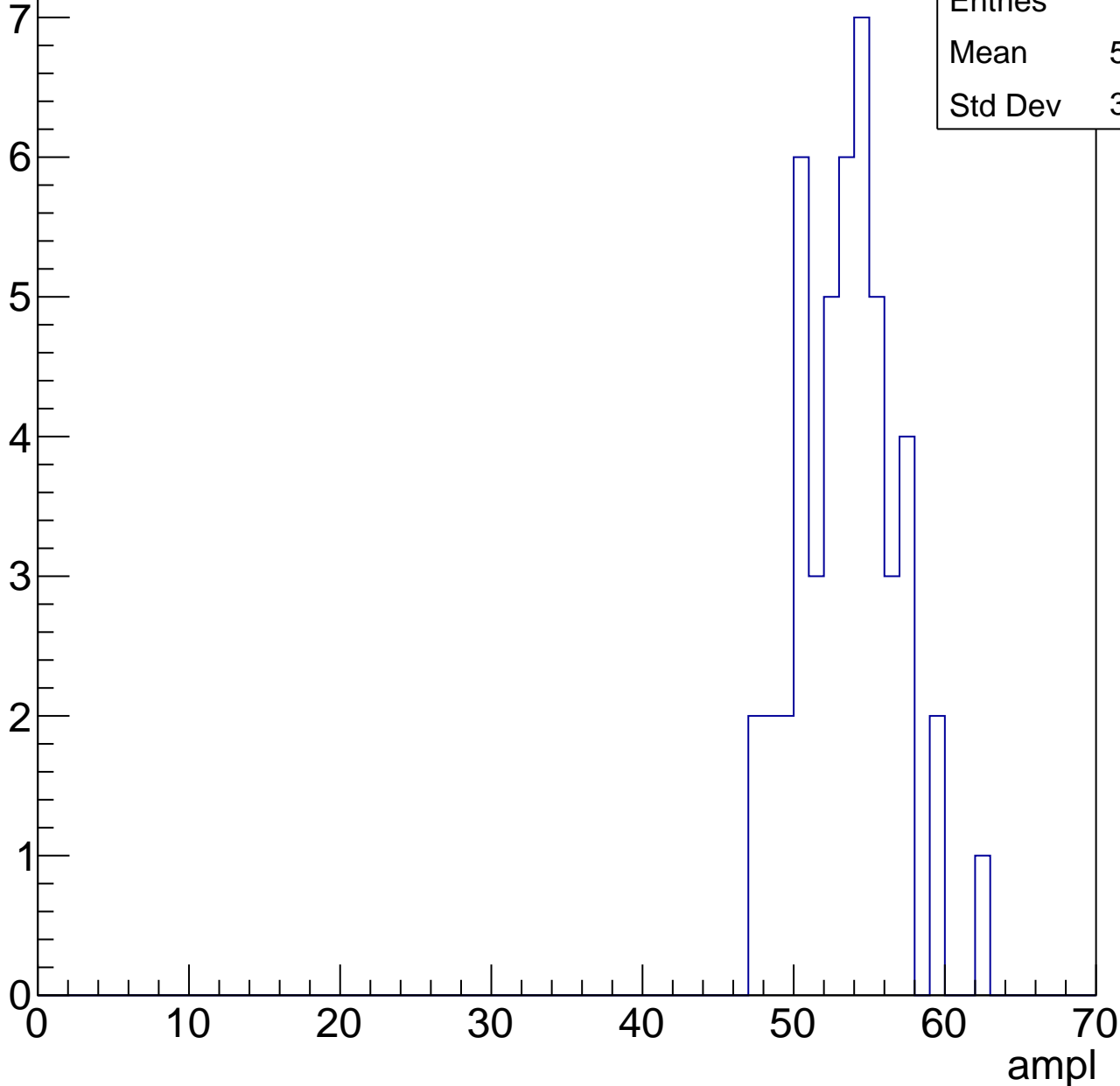


# B1L003S, U23-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	53.08
Std Dev	3.213

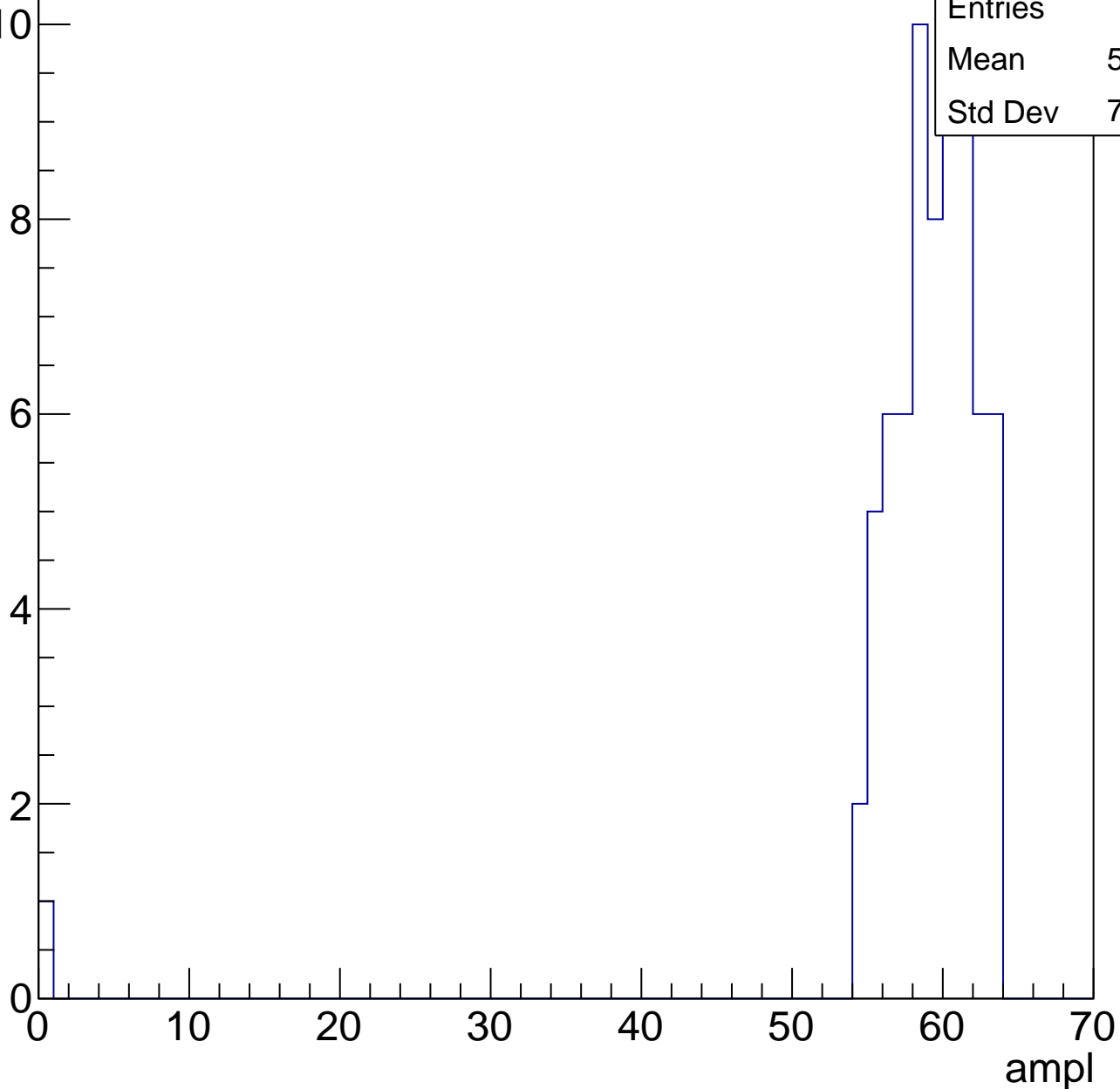


# B1L003S, U23-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	58.19
Std Dev	7.419



# B1L003S, U23-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B1L003S, U23-ch2, adc0

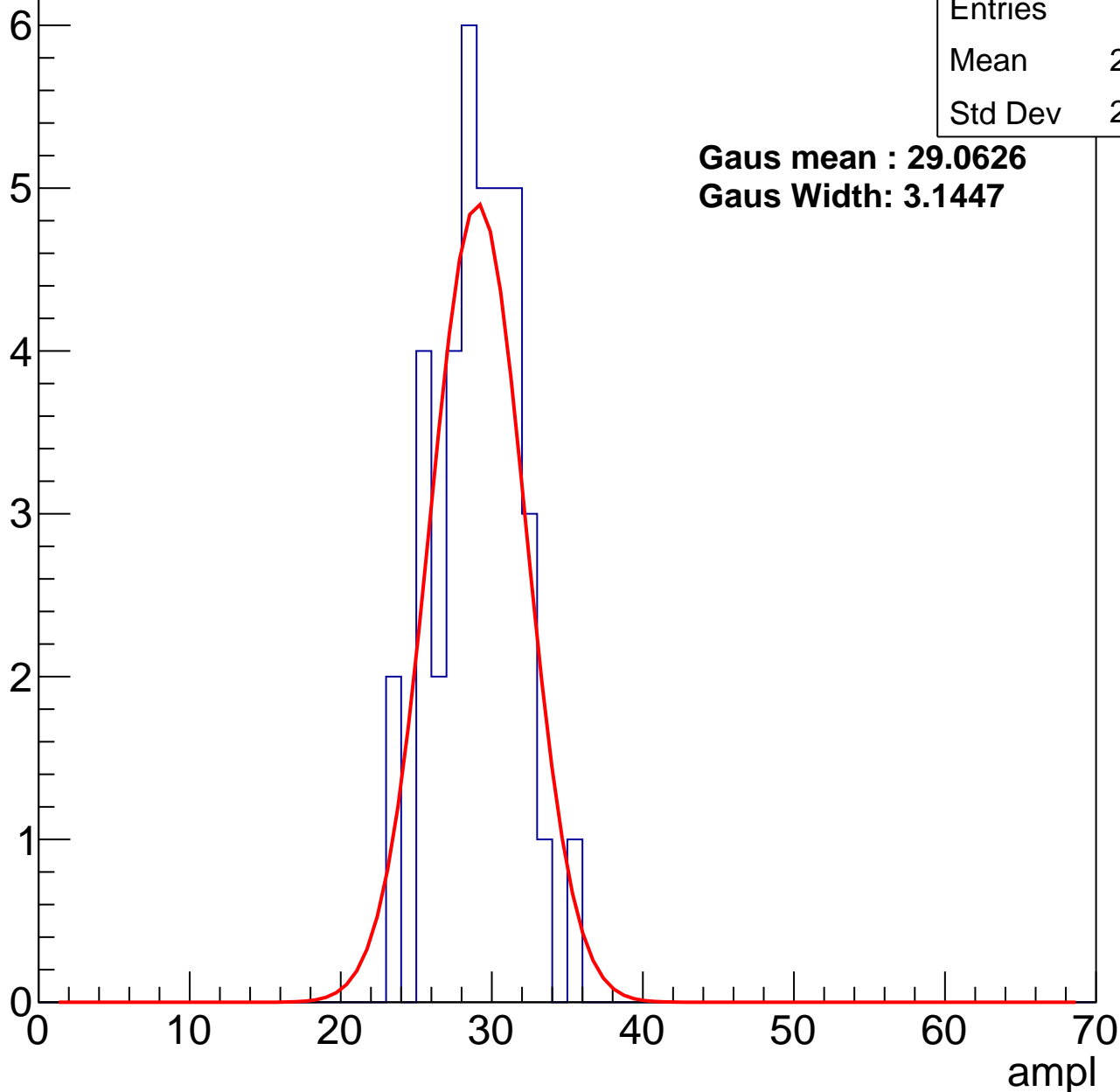
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	38
Mean	28.63
Std Dev	2.689

**Gaus mean : 29.0626**

**Gaus Width: 3.1447**



# B1L003S, U23-ch2, adc1

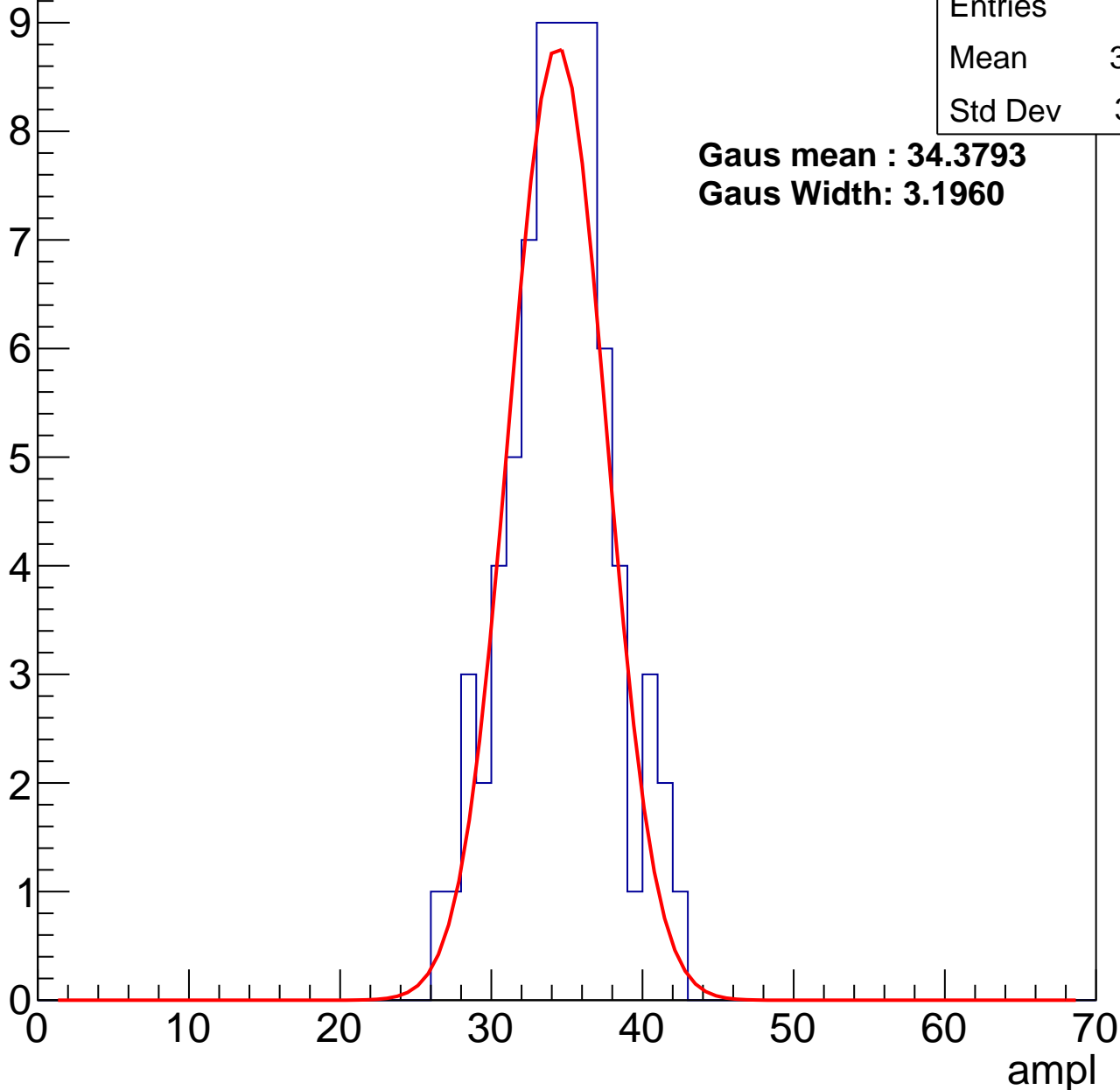
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	34.12
Std Dev	3.391

**Gaus mean : 34.3793**

**Gaus Width: 3.1960**



# B1L003S, U23-ch2, adc2

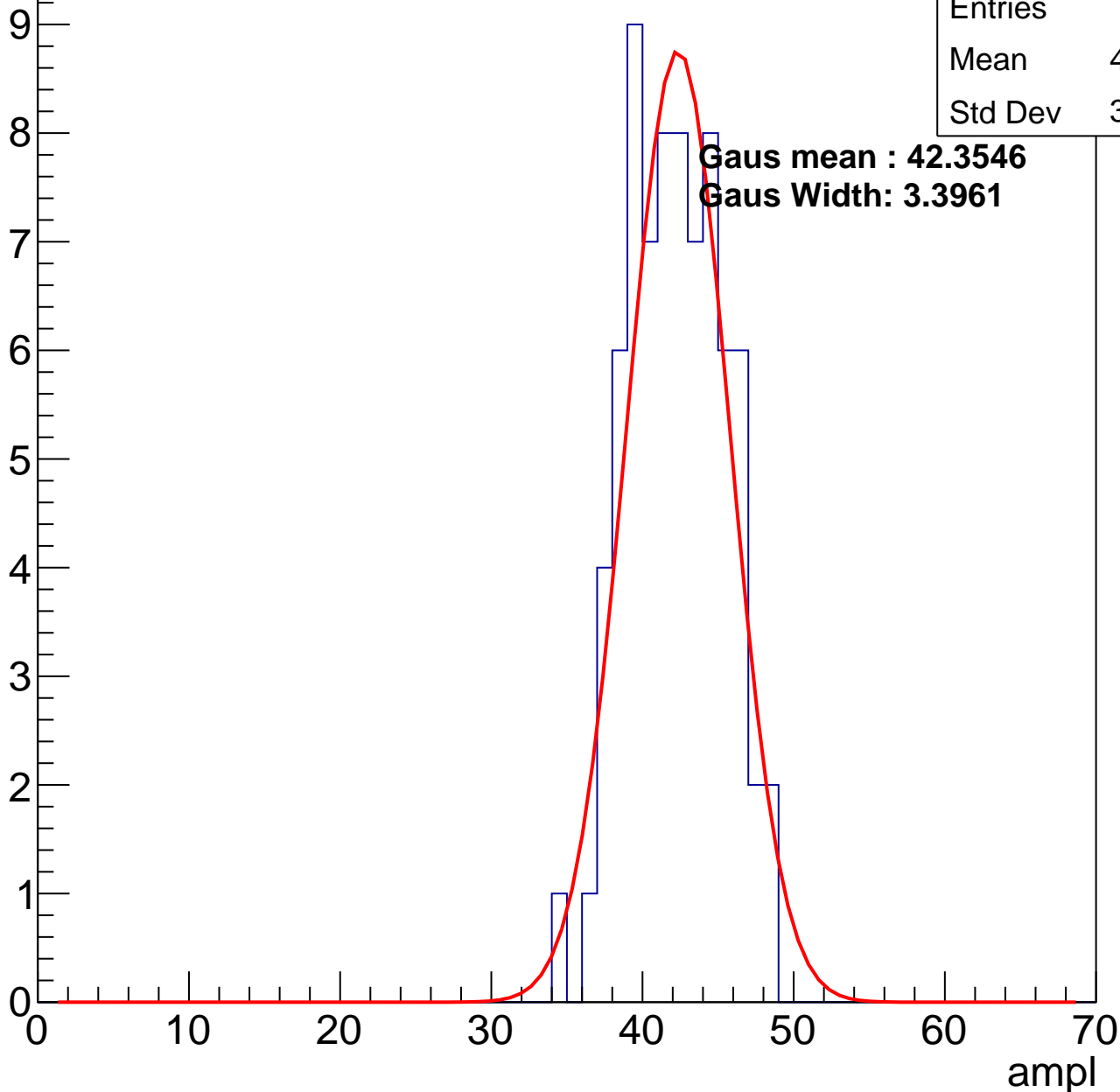
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	41.73
Std Dev	3.096

**Gaus mean : 42.3546**

**Gaus Width: 3.3961**

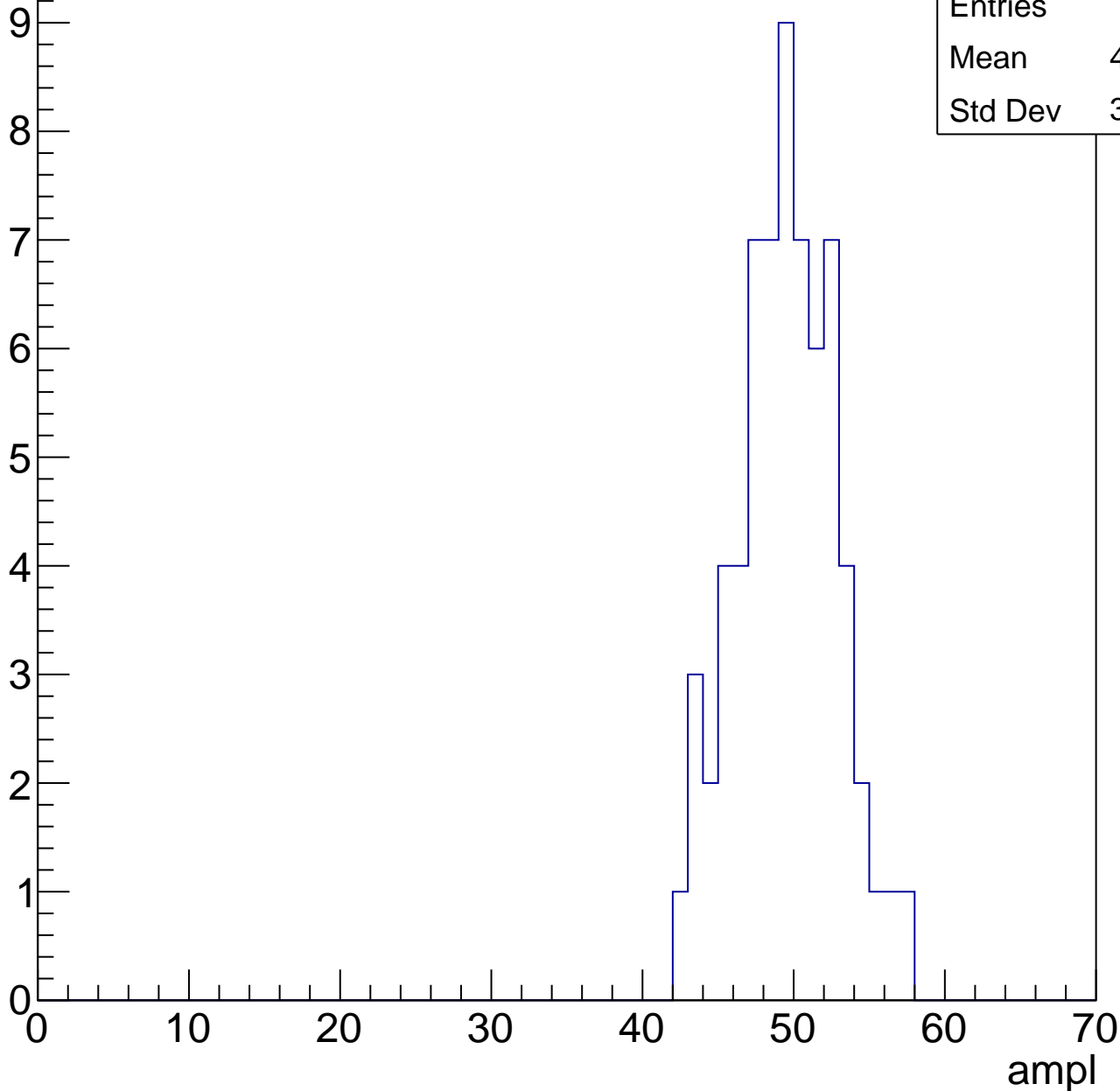


# B1L003S, U23-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

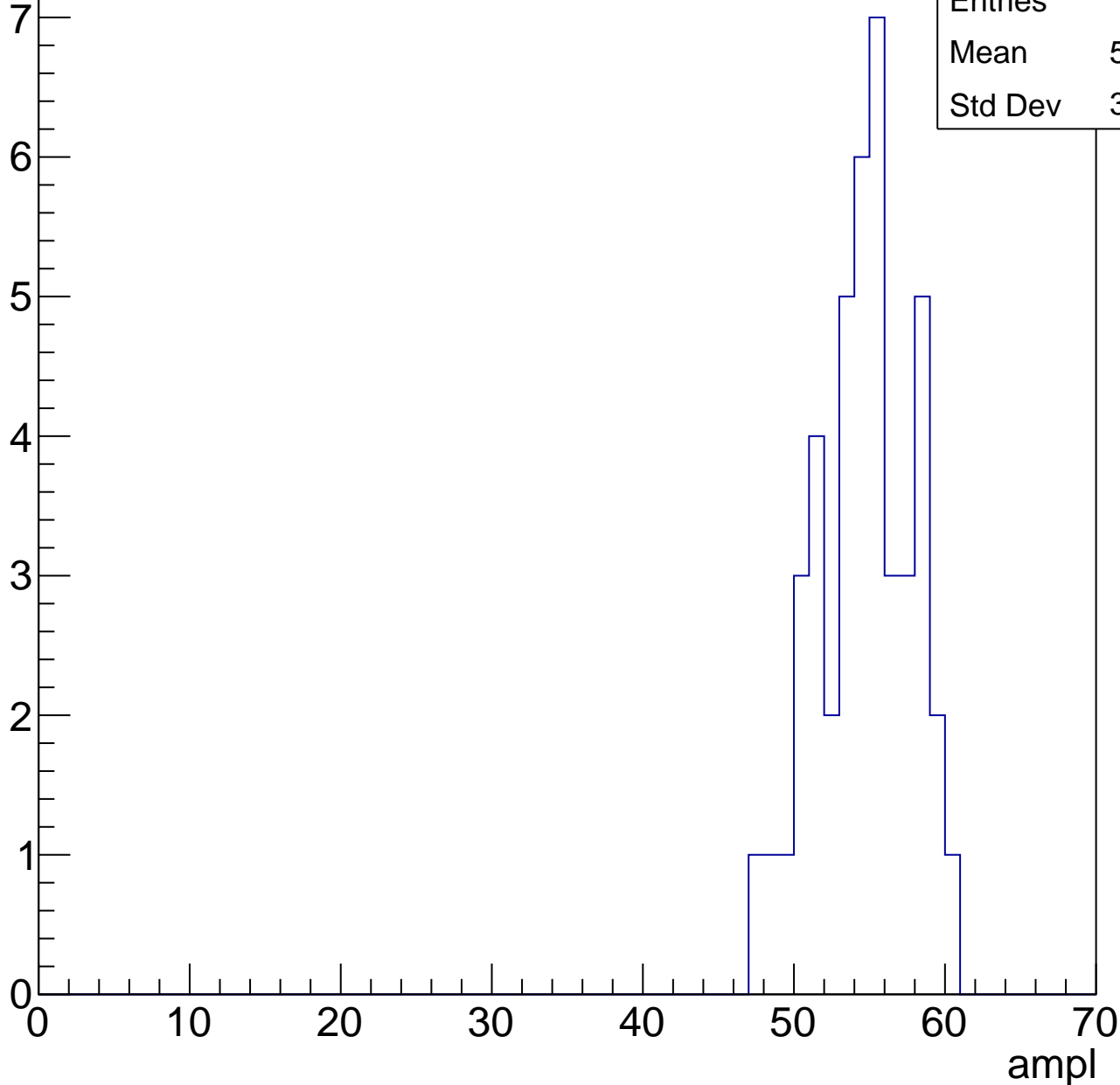
Entries	66
Mean	49.05
Std Dev	3.254



# B1L003S, U23-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch2, adc5

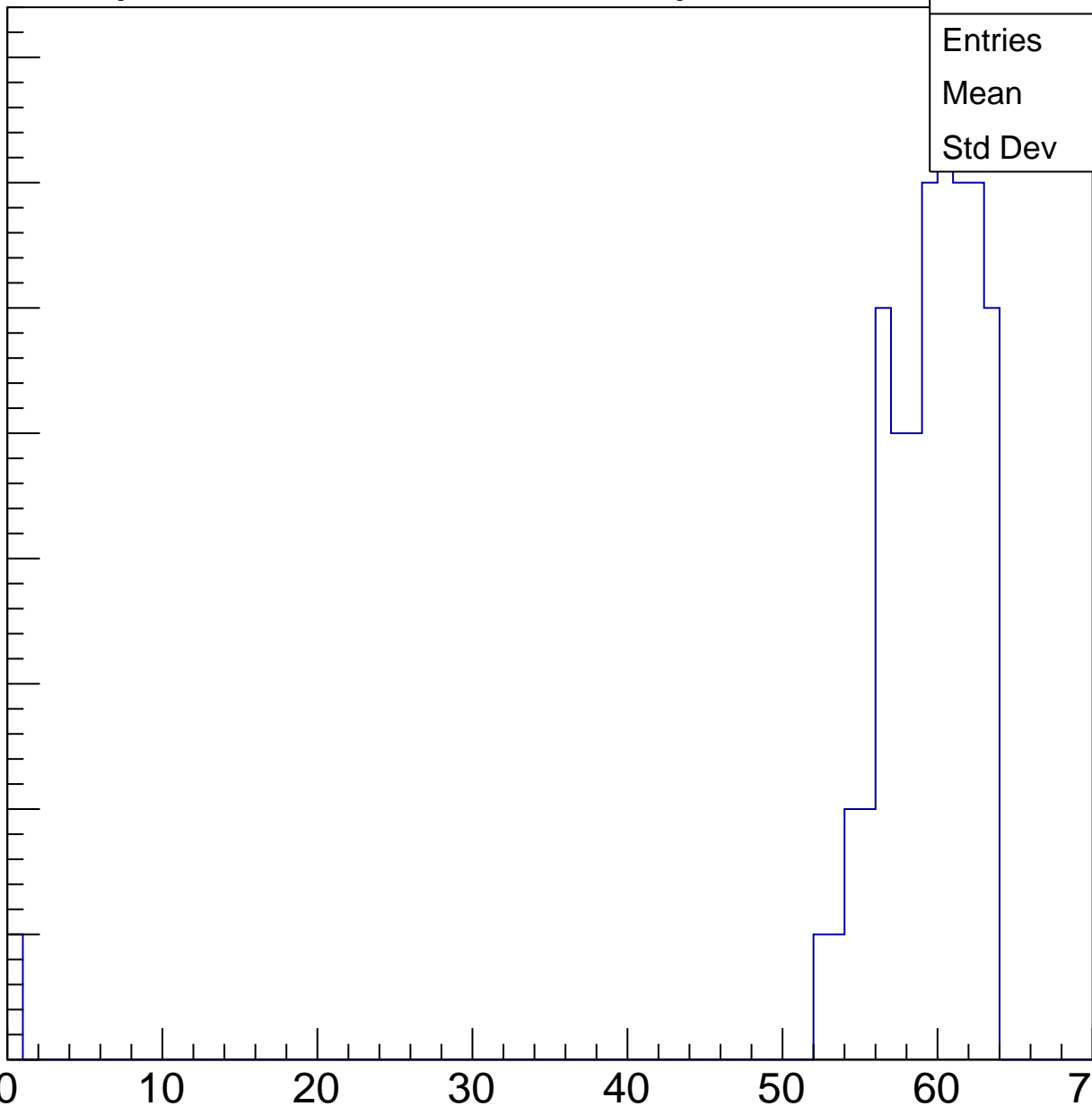
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	58.03
Std Dev	8.166

ampl



# B1L003S, U23-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries	11
Mean	61
Std Dev	1.706



# B1L003S, U23-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch3, adc0

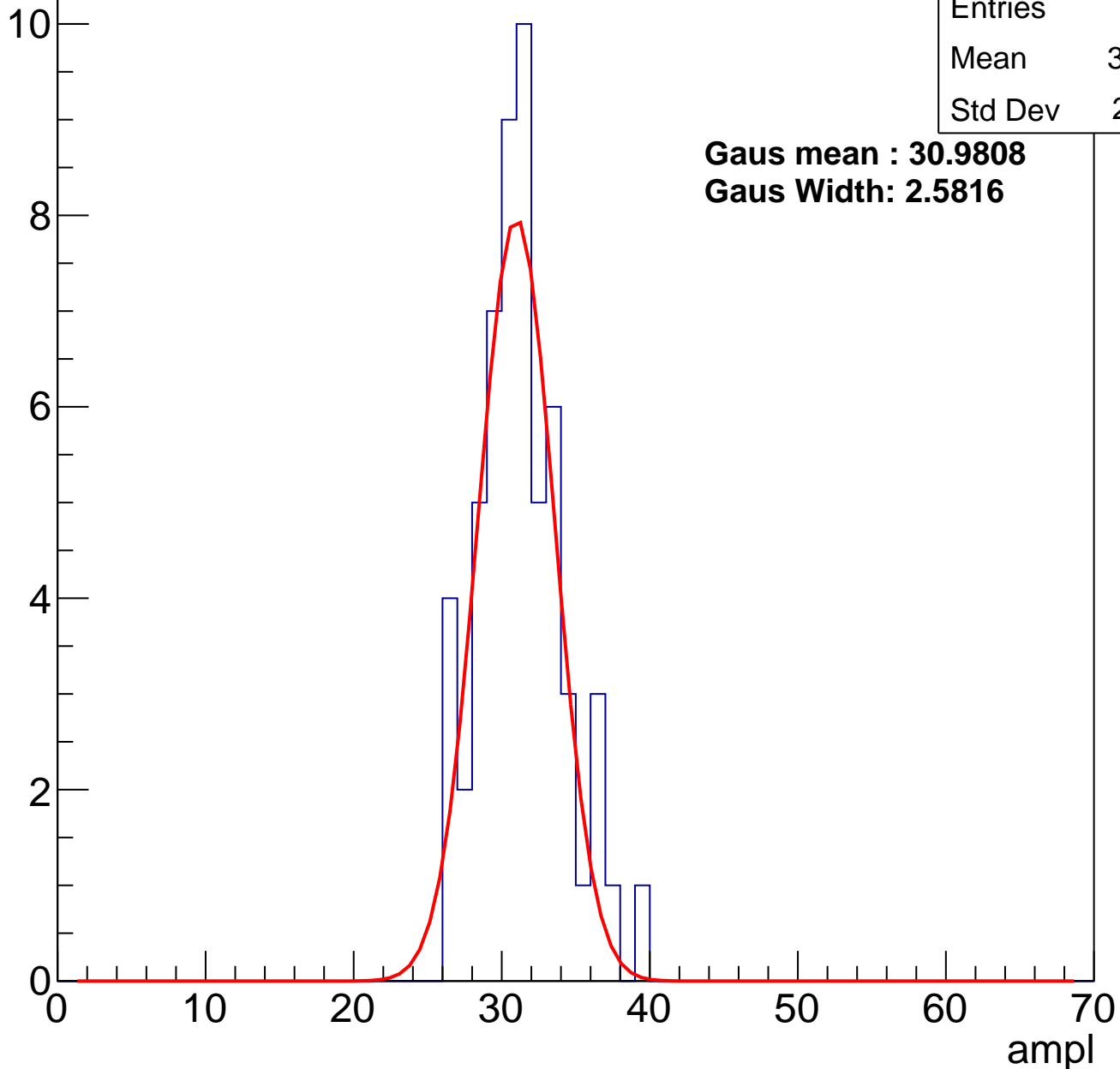
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	57
Mean	30.88
Std Dev	2.841

**Gaus mean : 30.9808**

**Gaus Width: 2.5816**

Entry



# B1L003S, U23-ch3, adc1

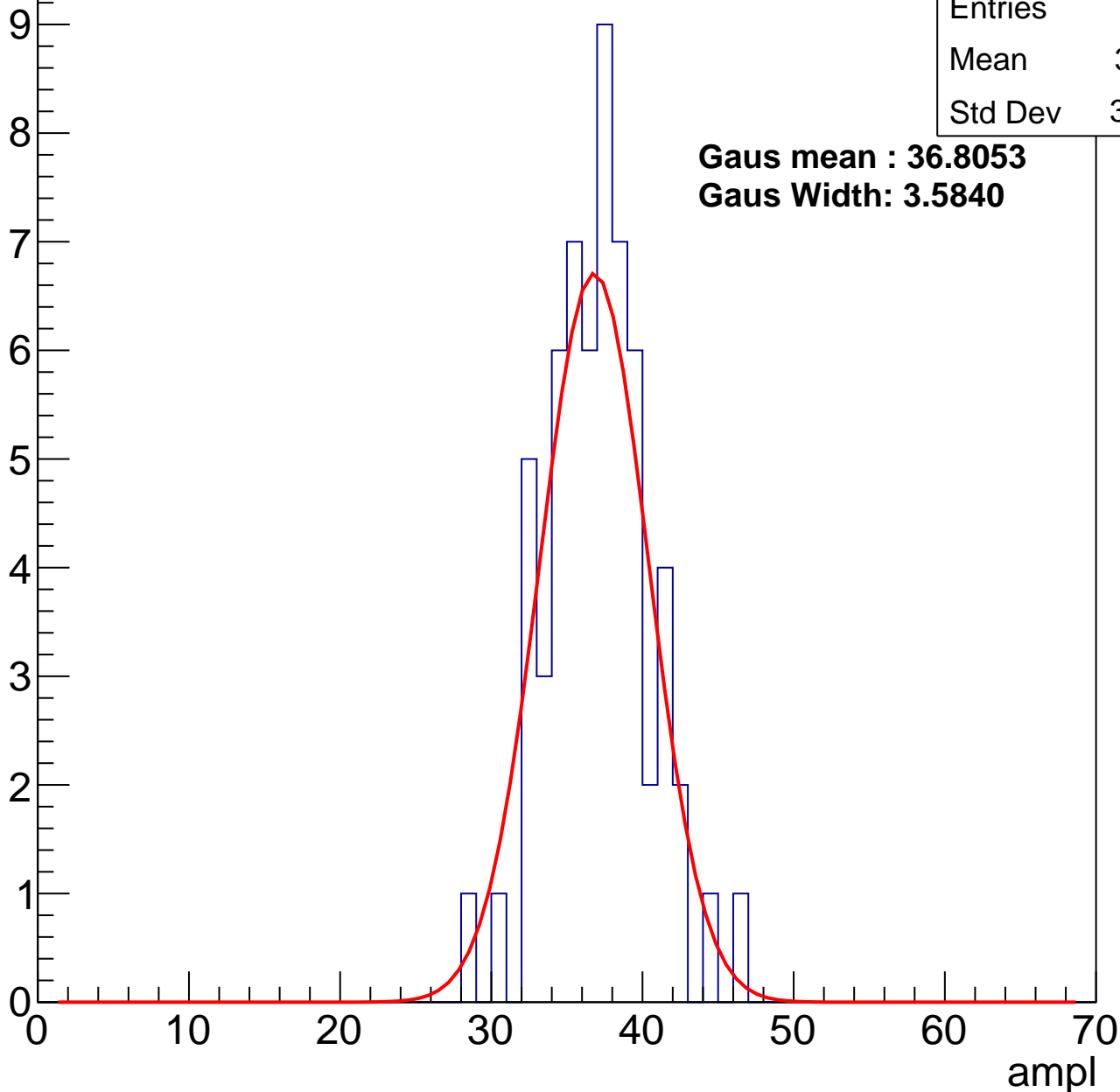
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.61
Std Dev	3.325

**Gaus mean : 36.8053**

**Gaus Width: 3.5840**



# B1L003S, U23-ch3, adc2

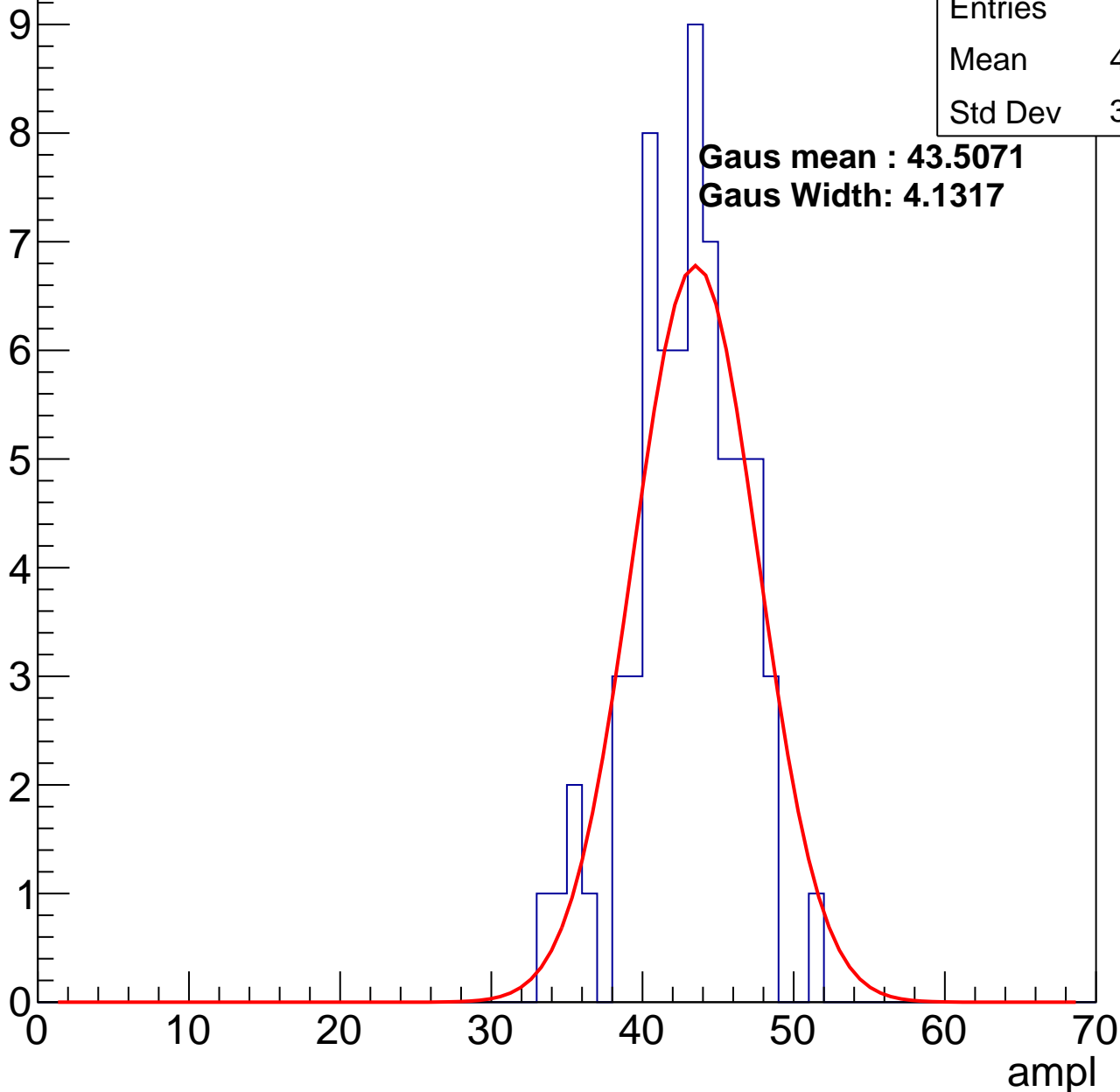
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	42.45
Std Dev	3.598

**Gaus mean : 43.5071**

**Gaus Width: 4.1317**

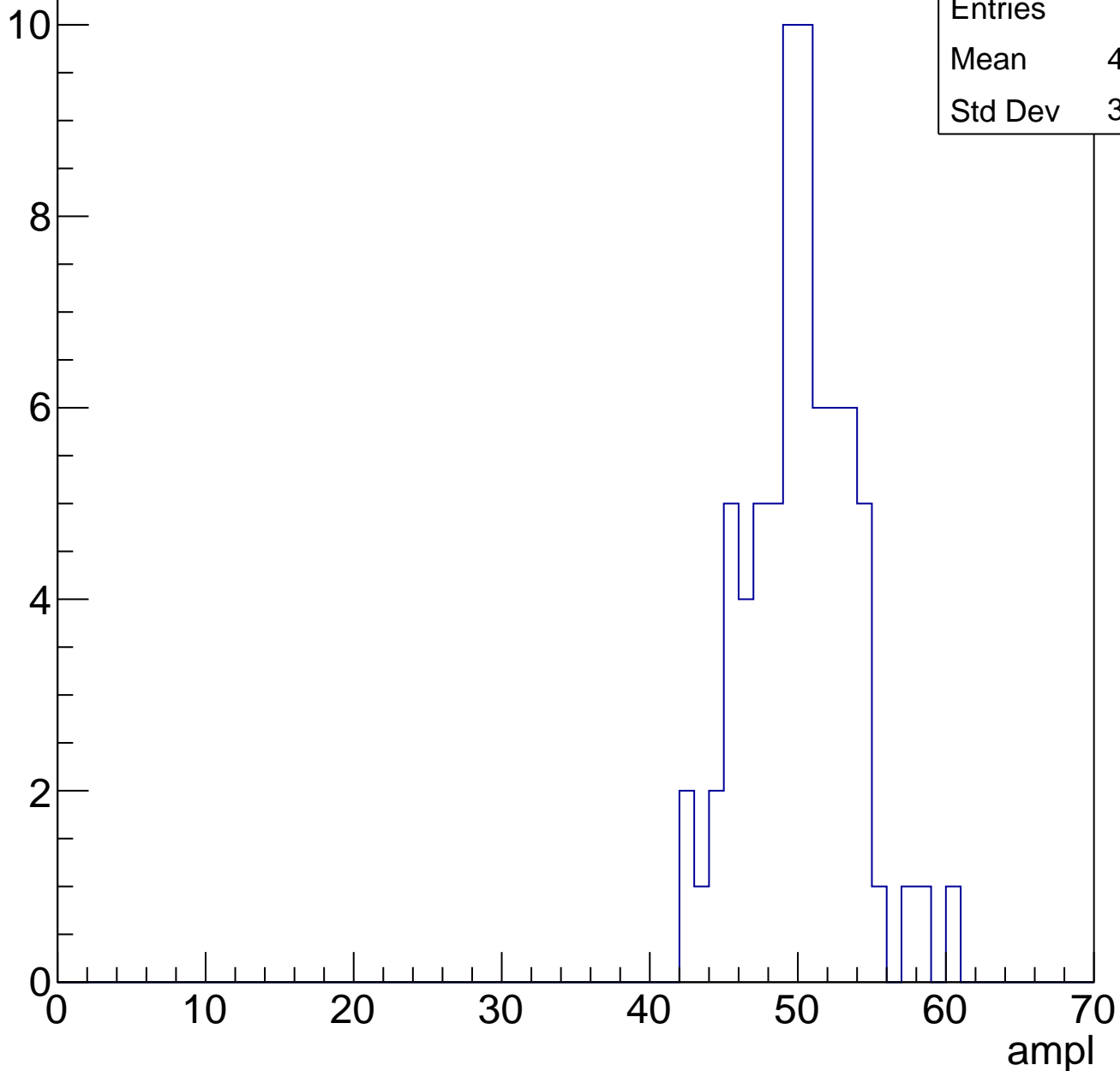


# B1L003S, U23-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	49.65
Std Dev	3.573

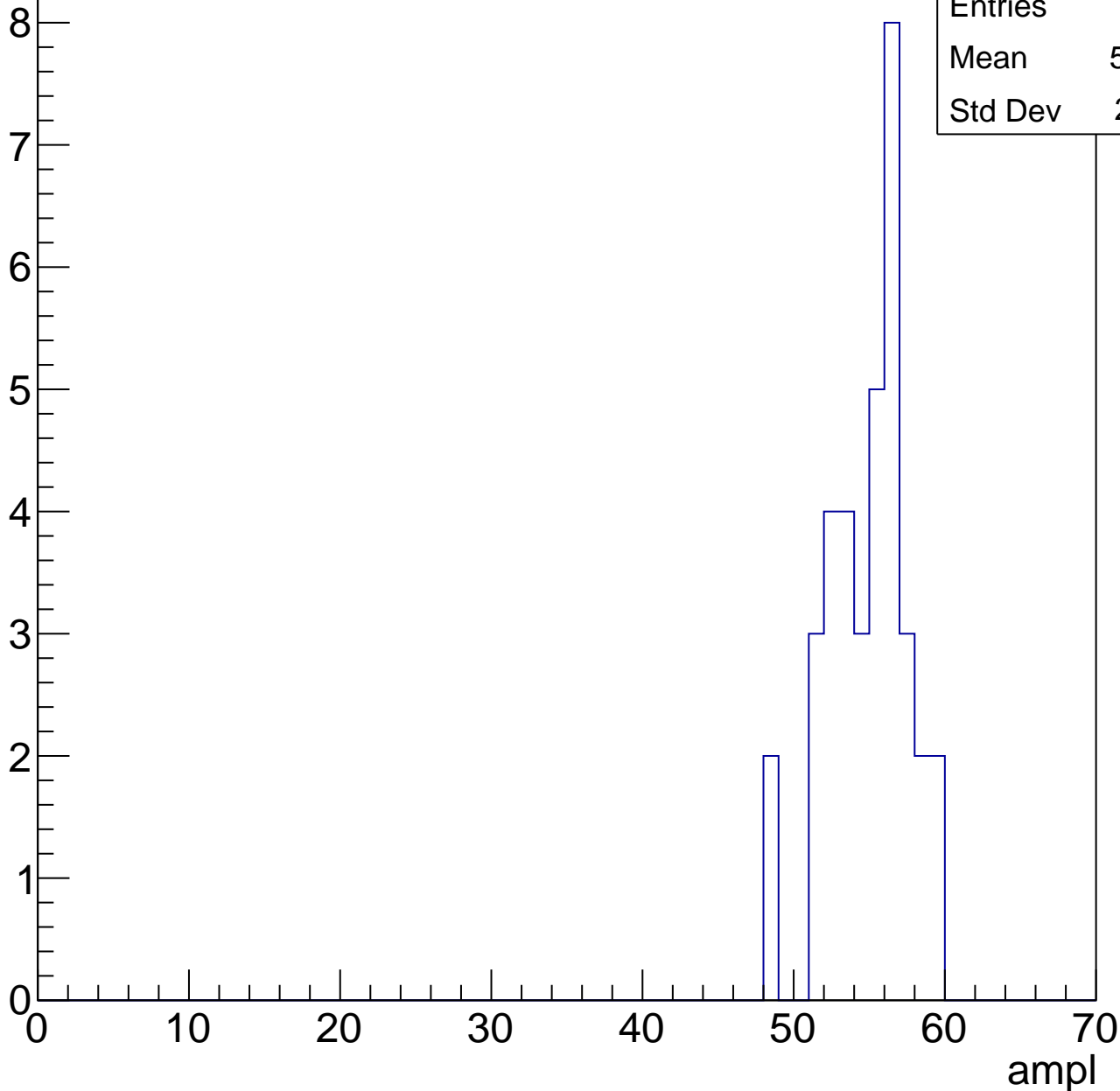


# B1L003S, U23-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

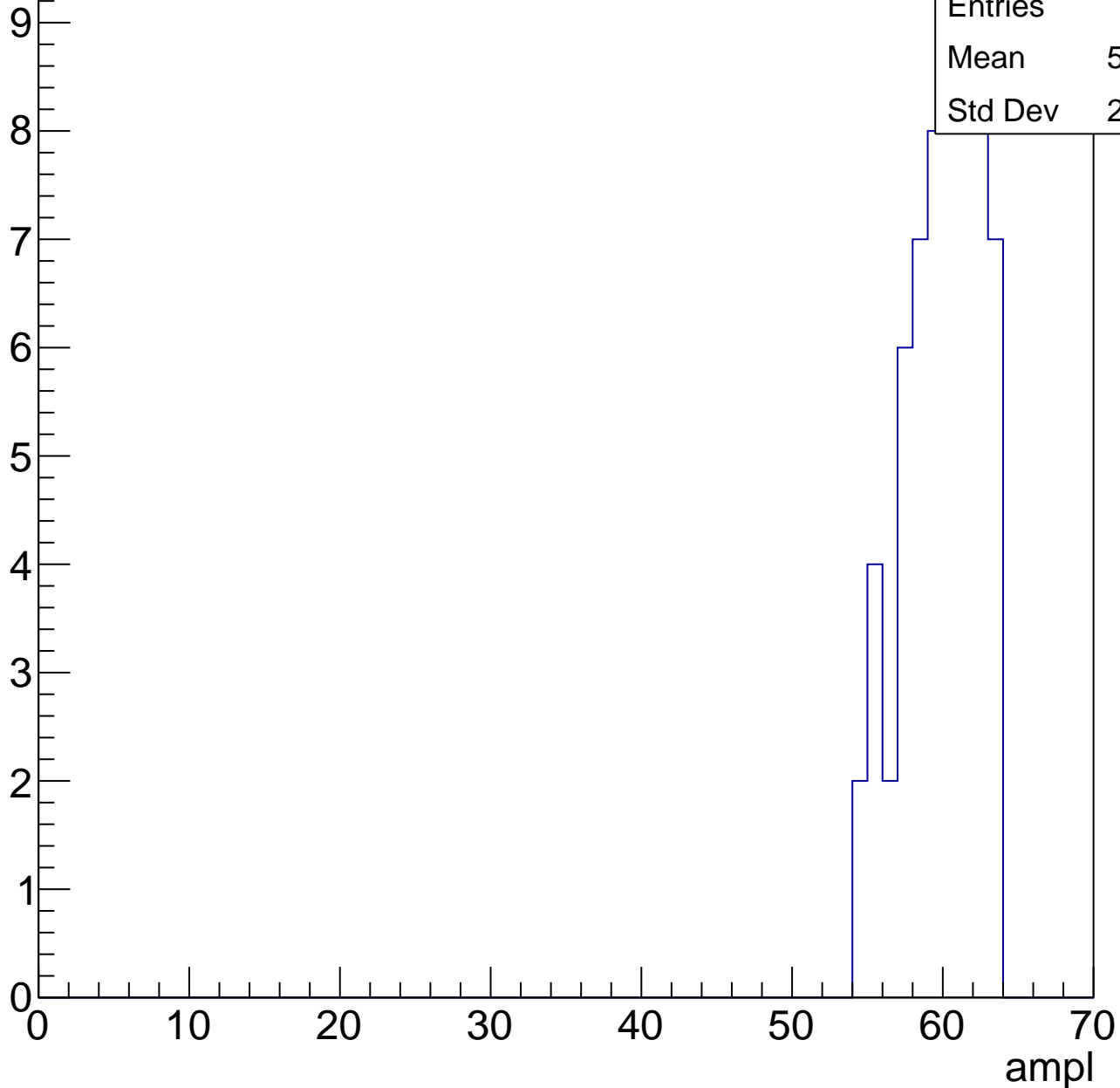
Entries	36
Mean	54.42
Std Dev	2.681



# B1L003S, U23-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

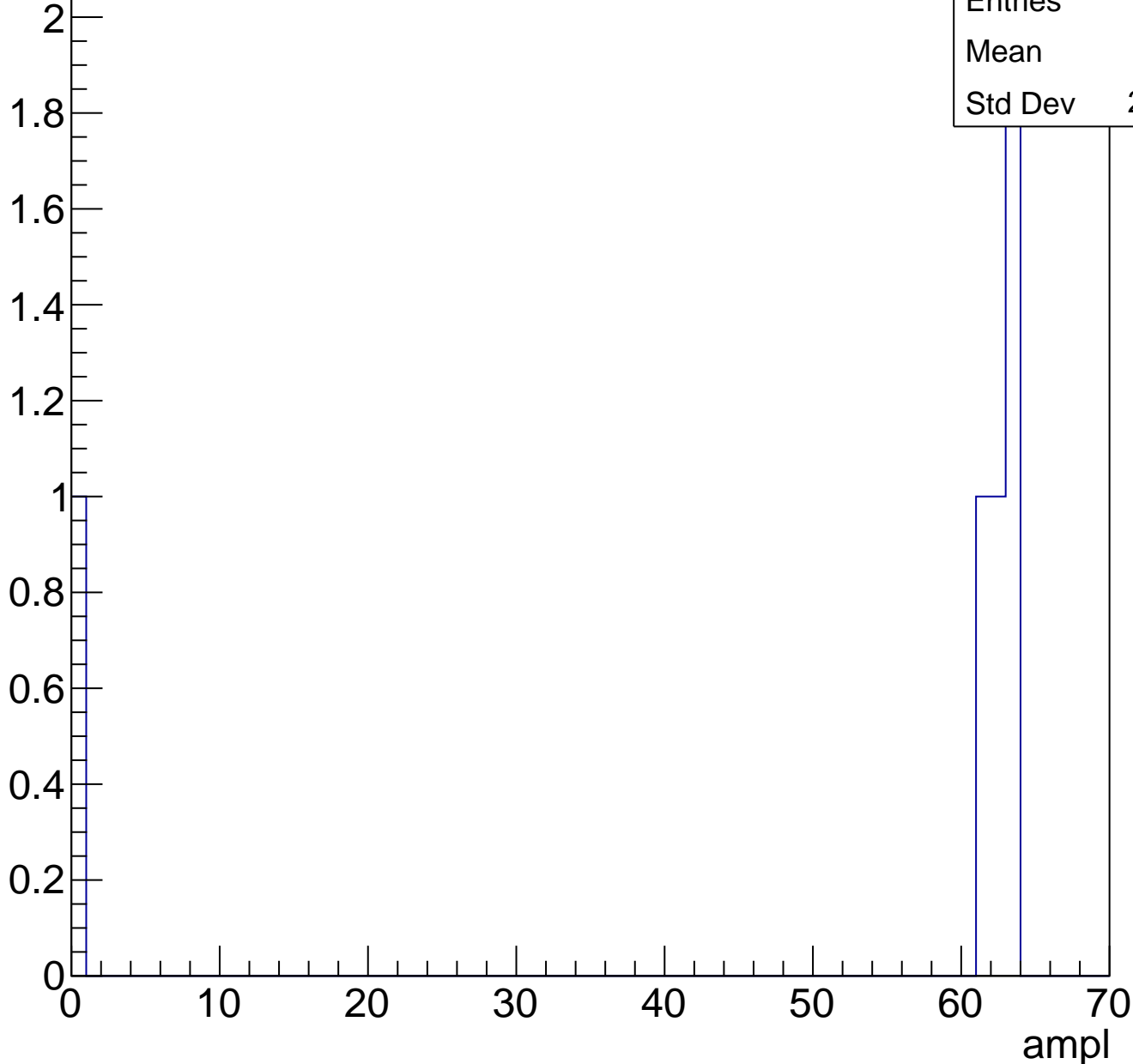


Entries	63
Mean	59.49
Std Dev	2.474

# B1L003S, U23-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



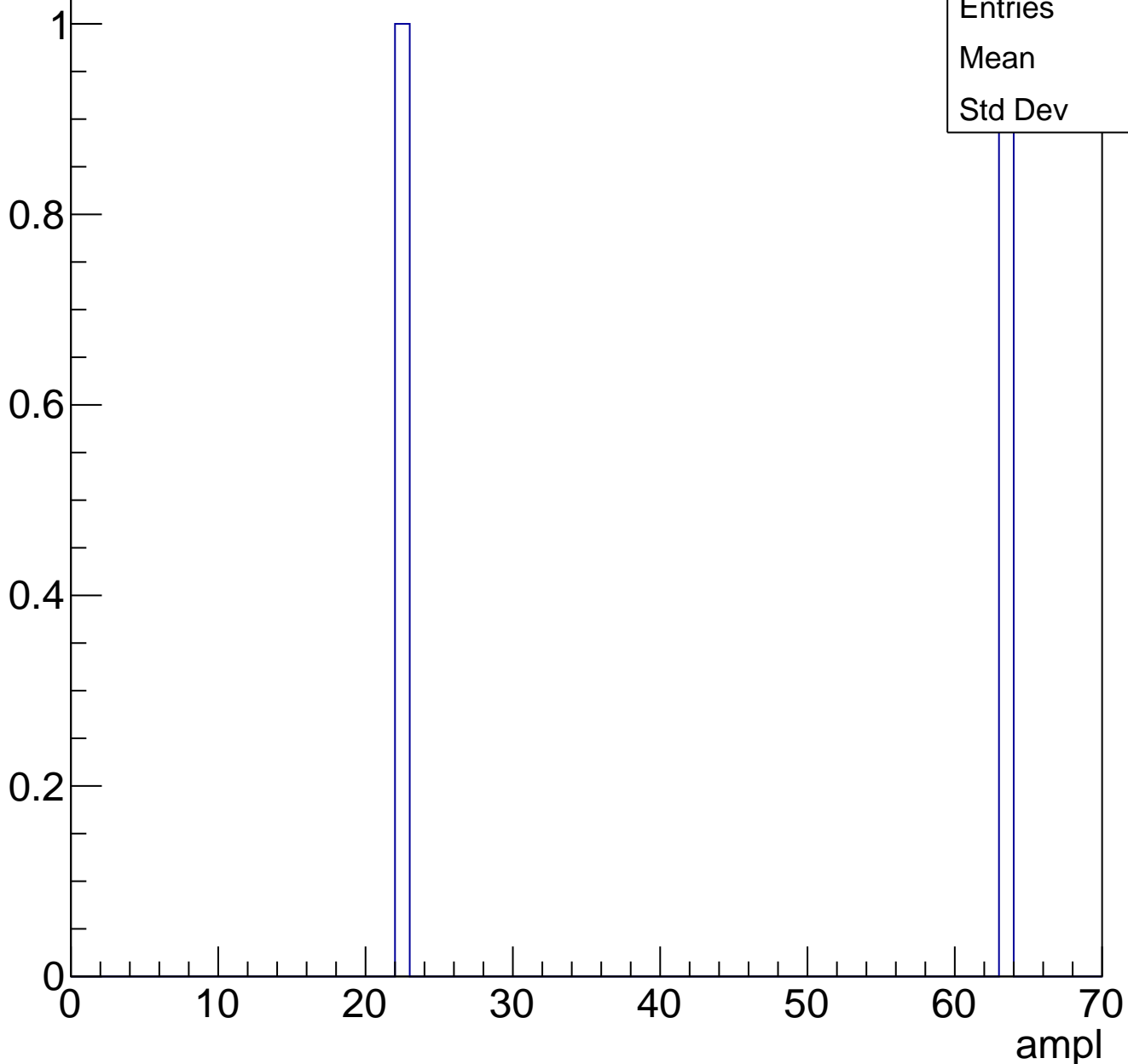
Entries	5
Mean	49.8
Std Dev	24.91



# B1L003S, U23-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	42.5
Std Dev	20.5

# B1L003S, U23-ch4, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	30.47
Std Dev	3.255

**Gaus mean : 31.1610**

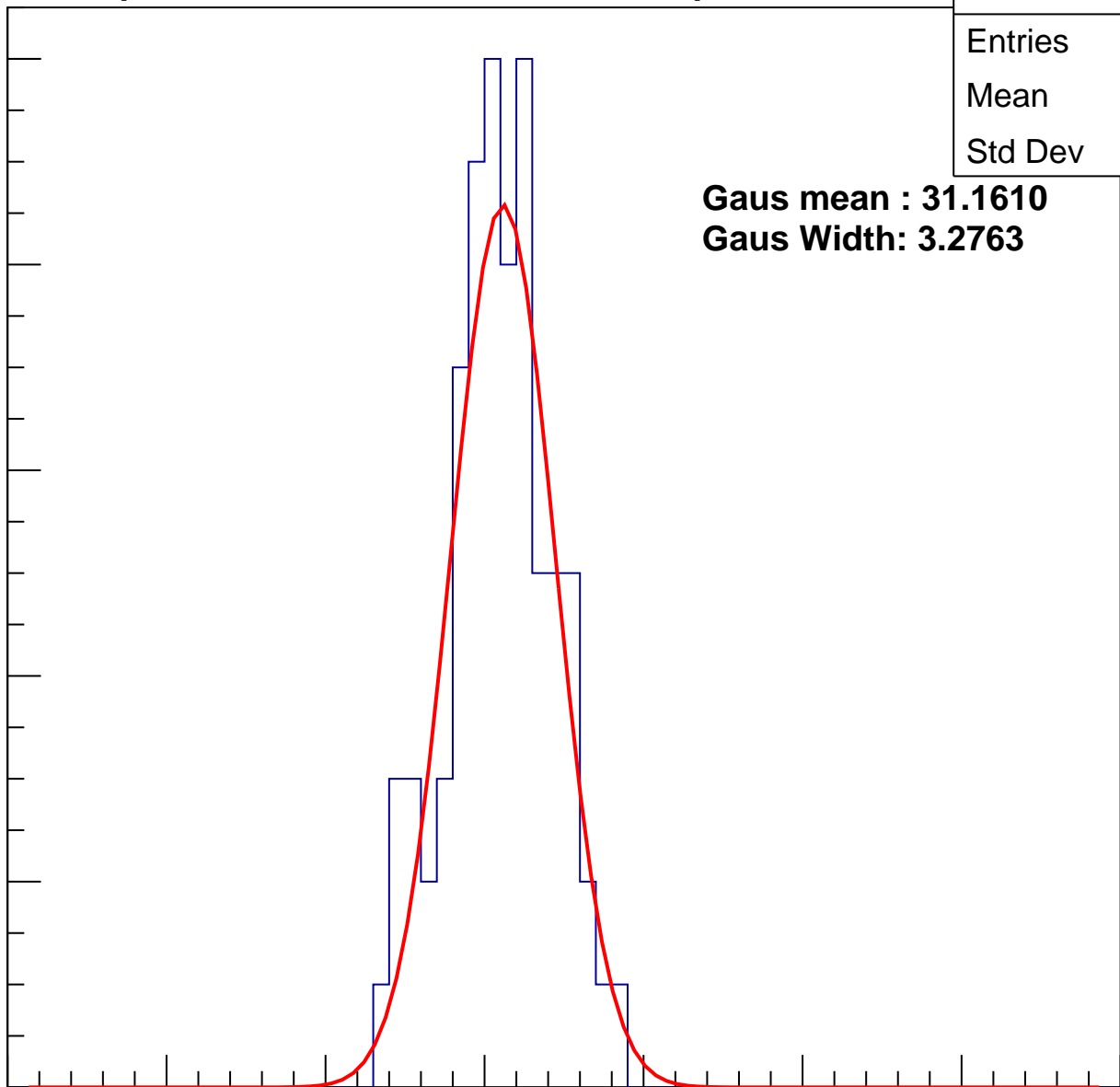
**Gaus Width: 3.2763**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch4, adc1

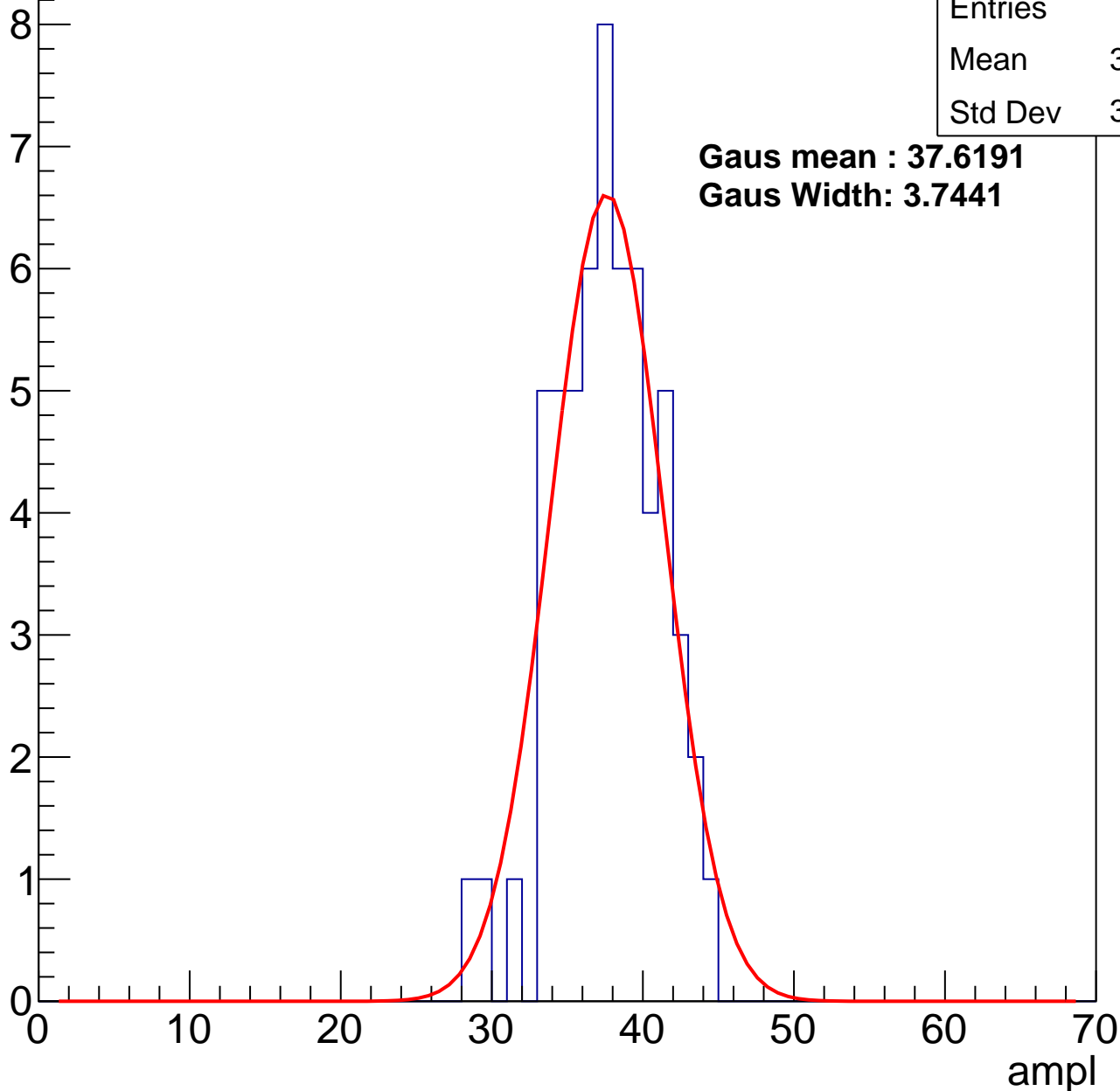
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	37.17
Std Dev	3.376

**Gaus mean : 37.6191**

**Gaus Width: 3.7441**



# B1L003S, U23-ch4, adc2

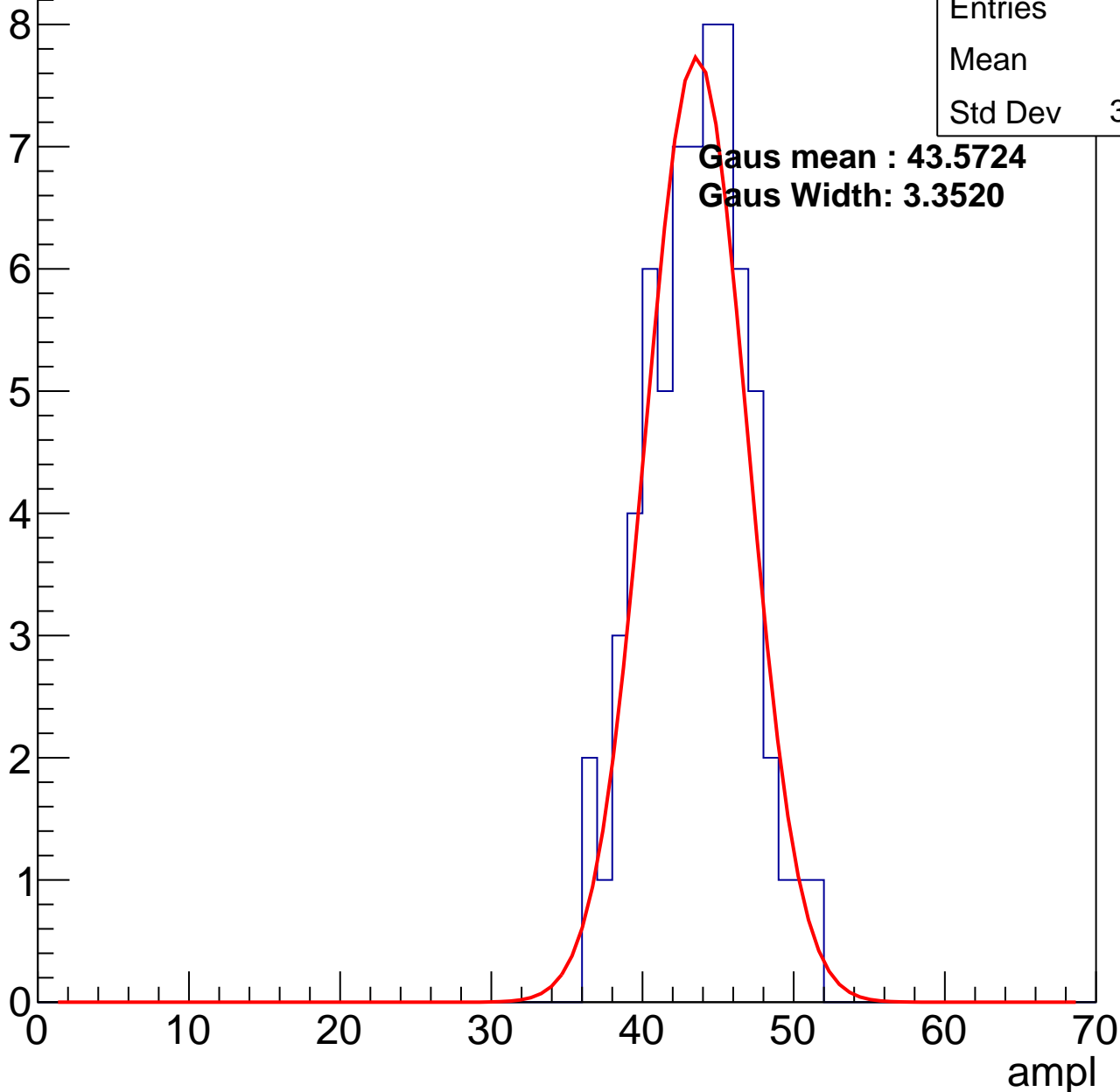
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	43.1
Std Dev	3.292

**Gaus mean : 43.5724**

**Gaus Width: 3.3520**

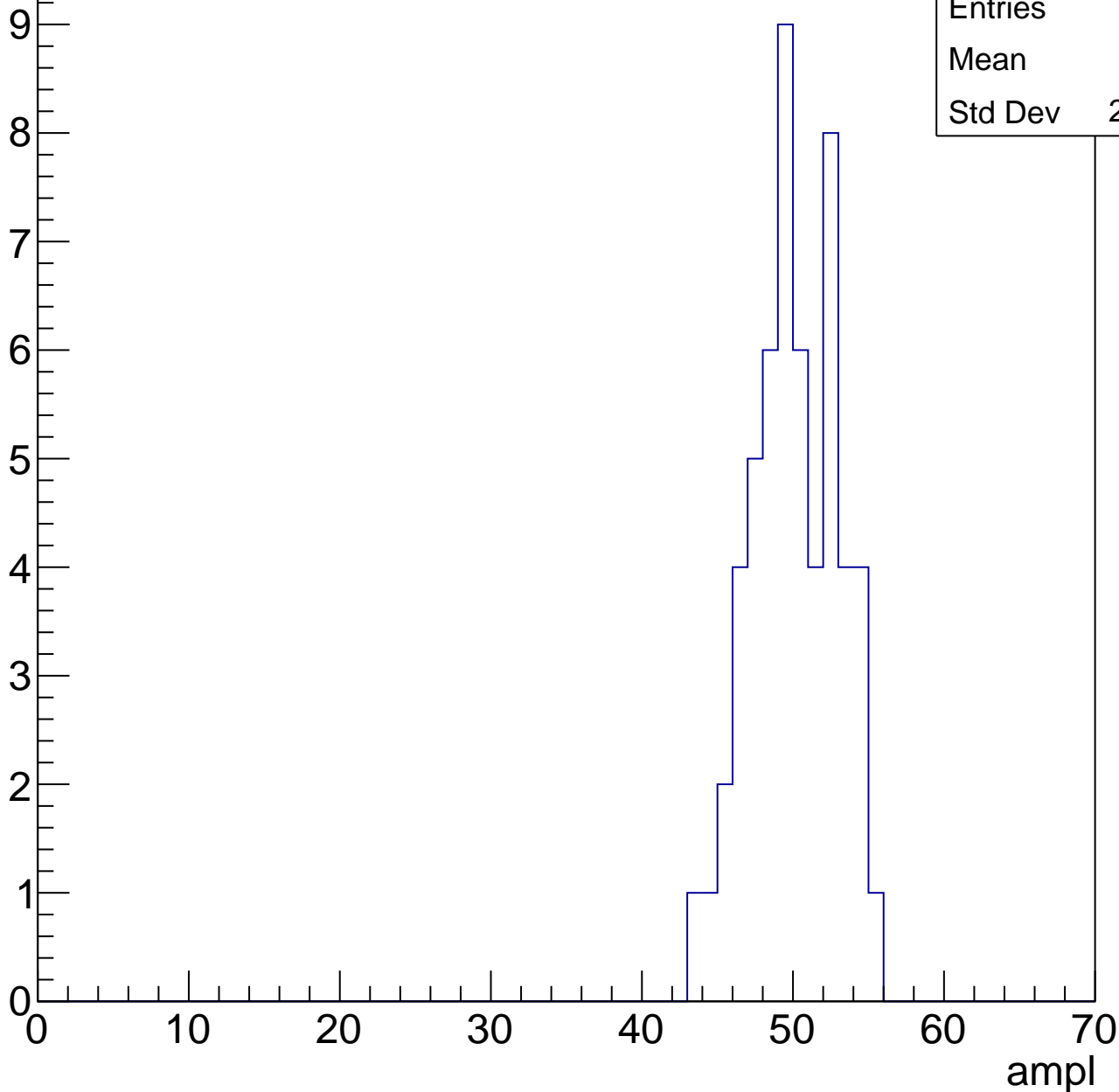


# B1L003S, U23-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	49.6
Std Dev	2.793

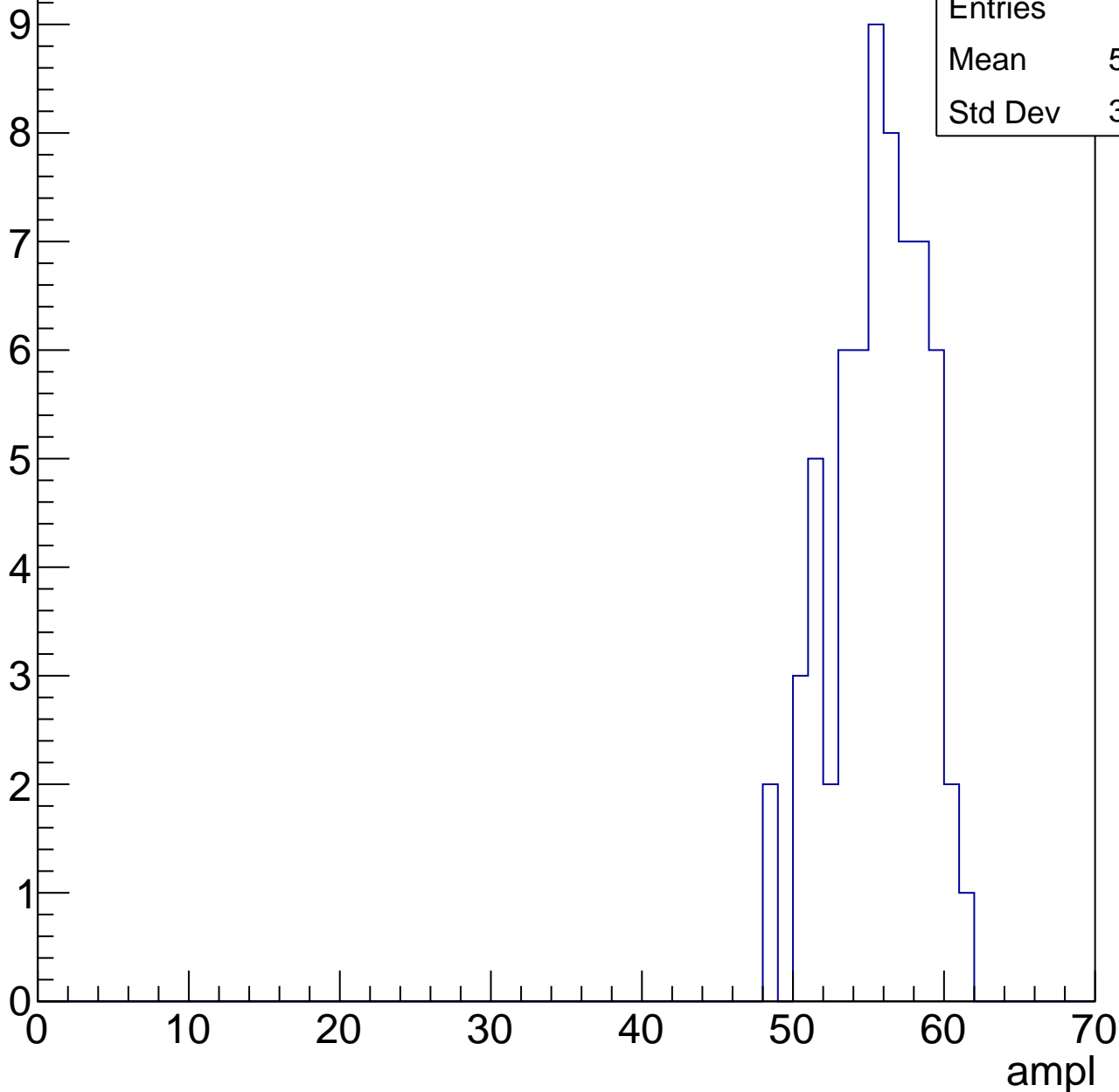


# B1L003S, U23-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.16
Std Dev	3.012



# B1L003S, U23-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	59.02
Std Dev	9.555

ampl

0

10

20

30

40

50

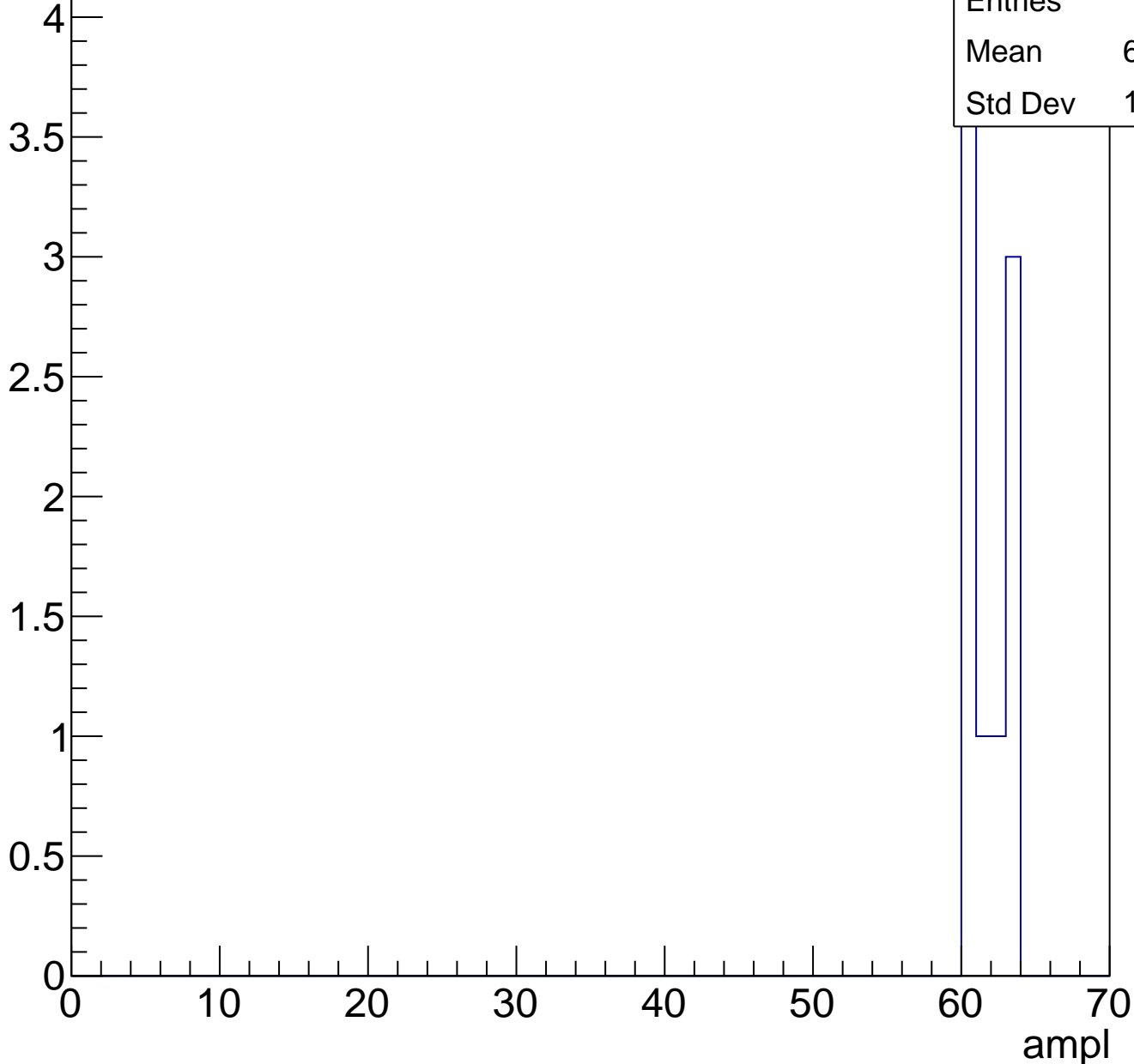
60

70

# B1L003S, U23-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch5, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	80
Mean	29.52
Std Dev	4.558

**Gaus mean : 31.0778**

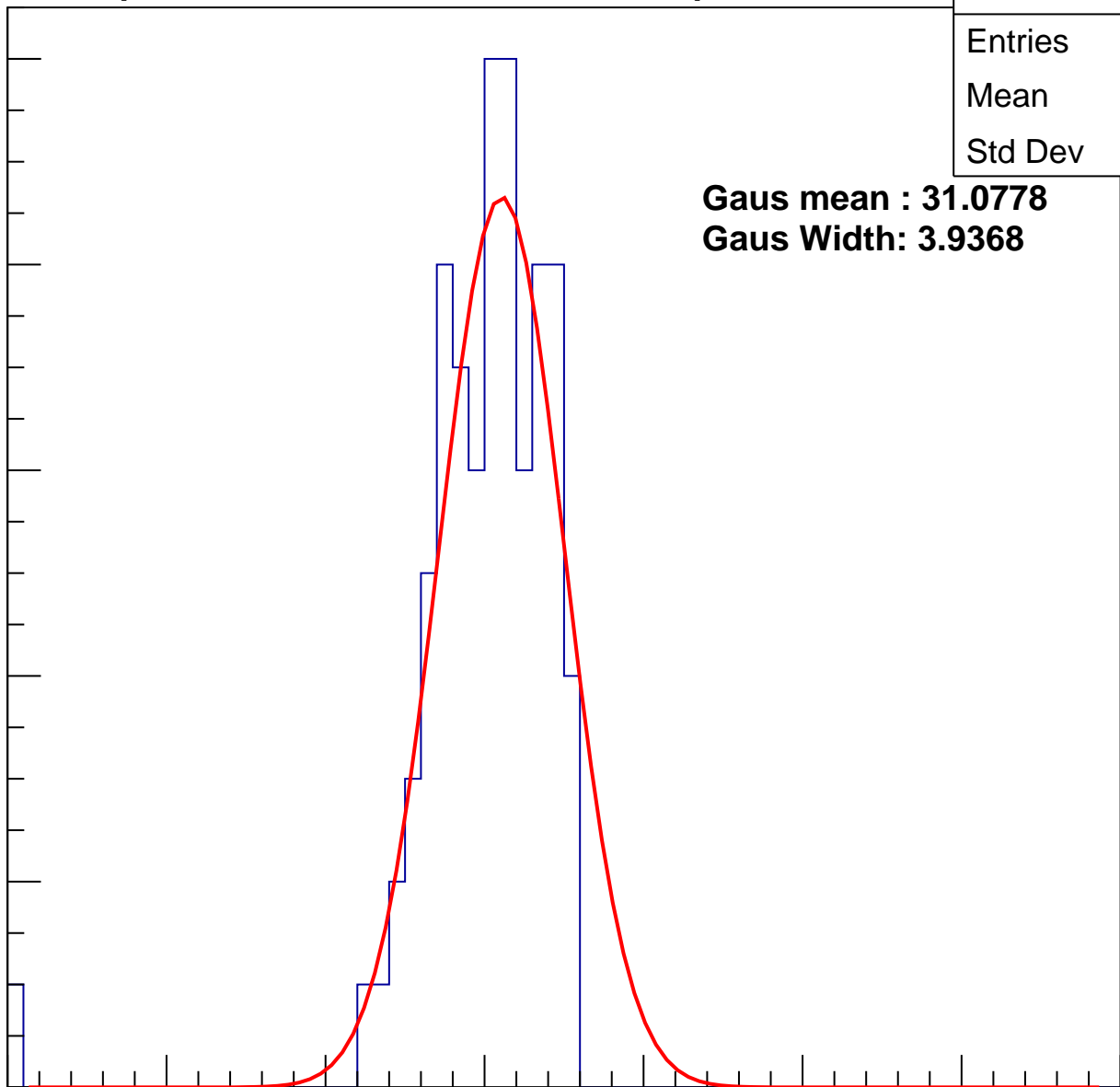
**Gaus Width: 3.9368**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch5, adc1

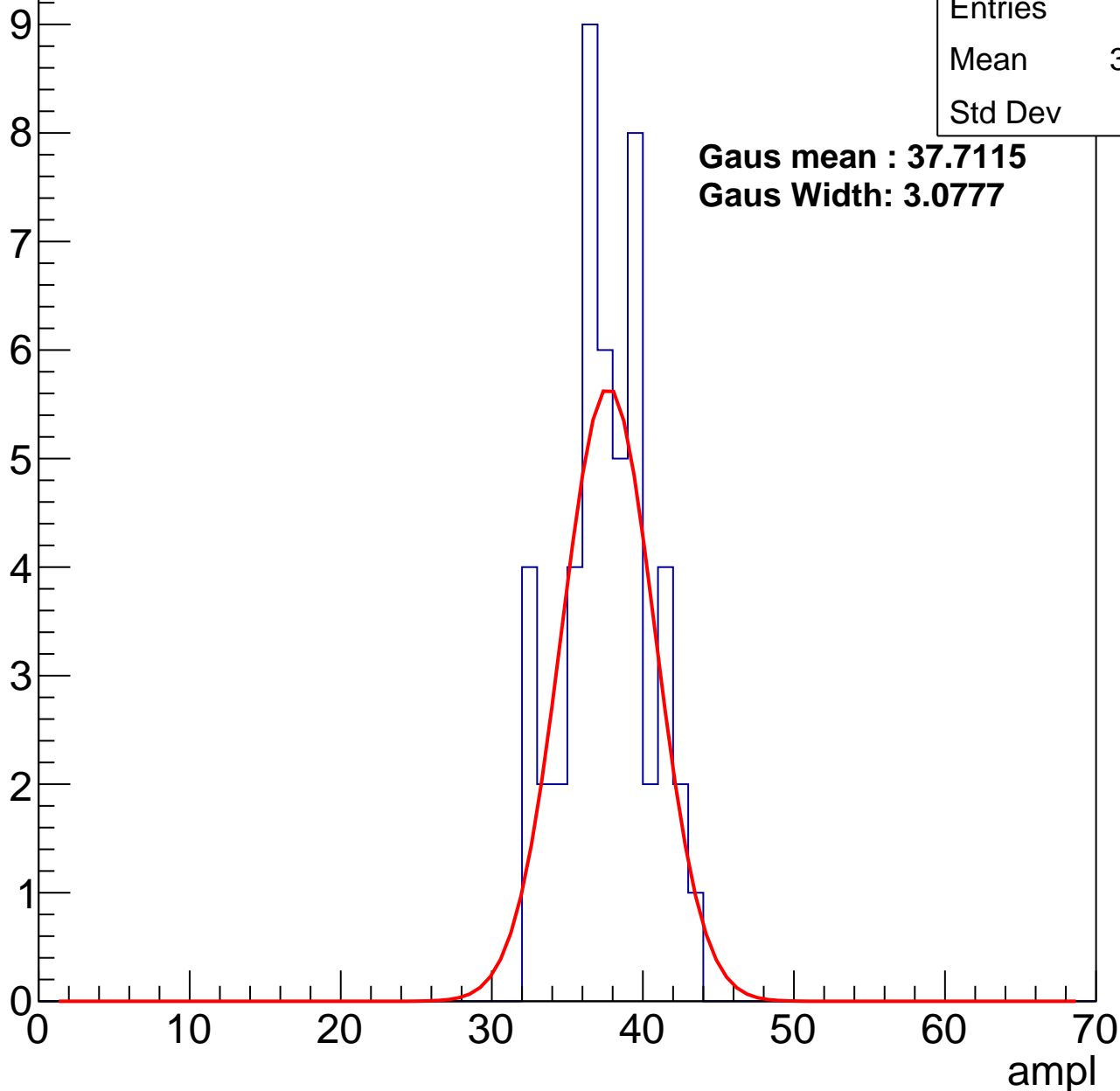
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	37.16
Std Dev	2.78

**Gaus mean : 37.7115**

**Gaus Width: 3.0777**



# B1L003S, U23-ch5, adc2

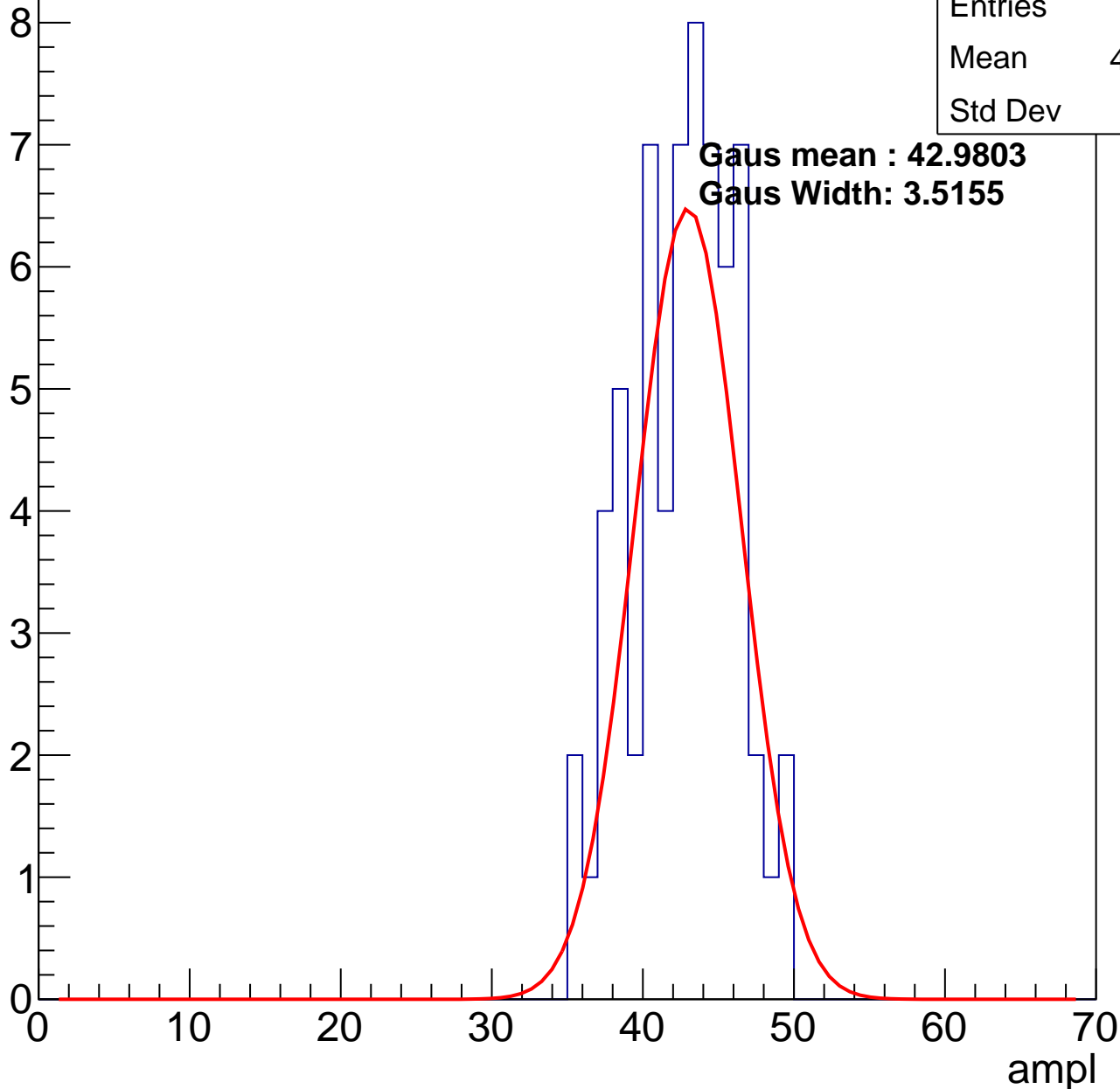
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.22
Std Dev	3.39

**Gaus mean : 42.9803**

**Gaus Width: 3.5155**

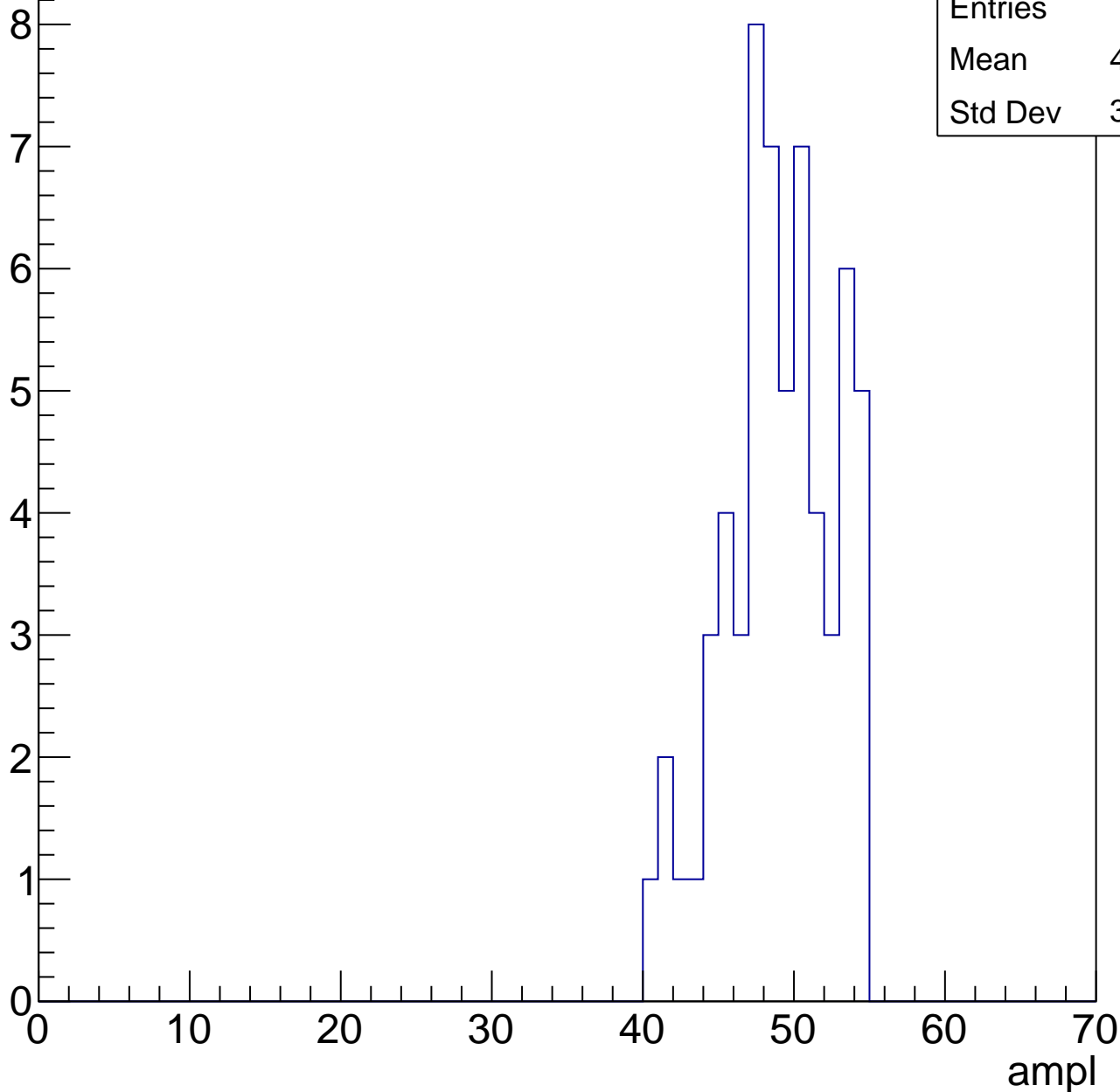


# B1L003S, U23-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.53
Std Dev	3.552



# B1L003S, U23-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	71
Mean	54.75
Std Dev	3.322

Entry

10

8

6

4

2

0

0

10

20

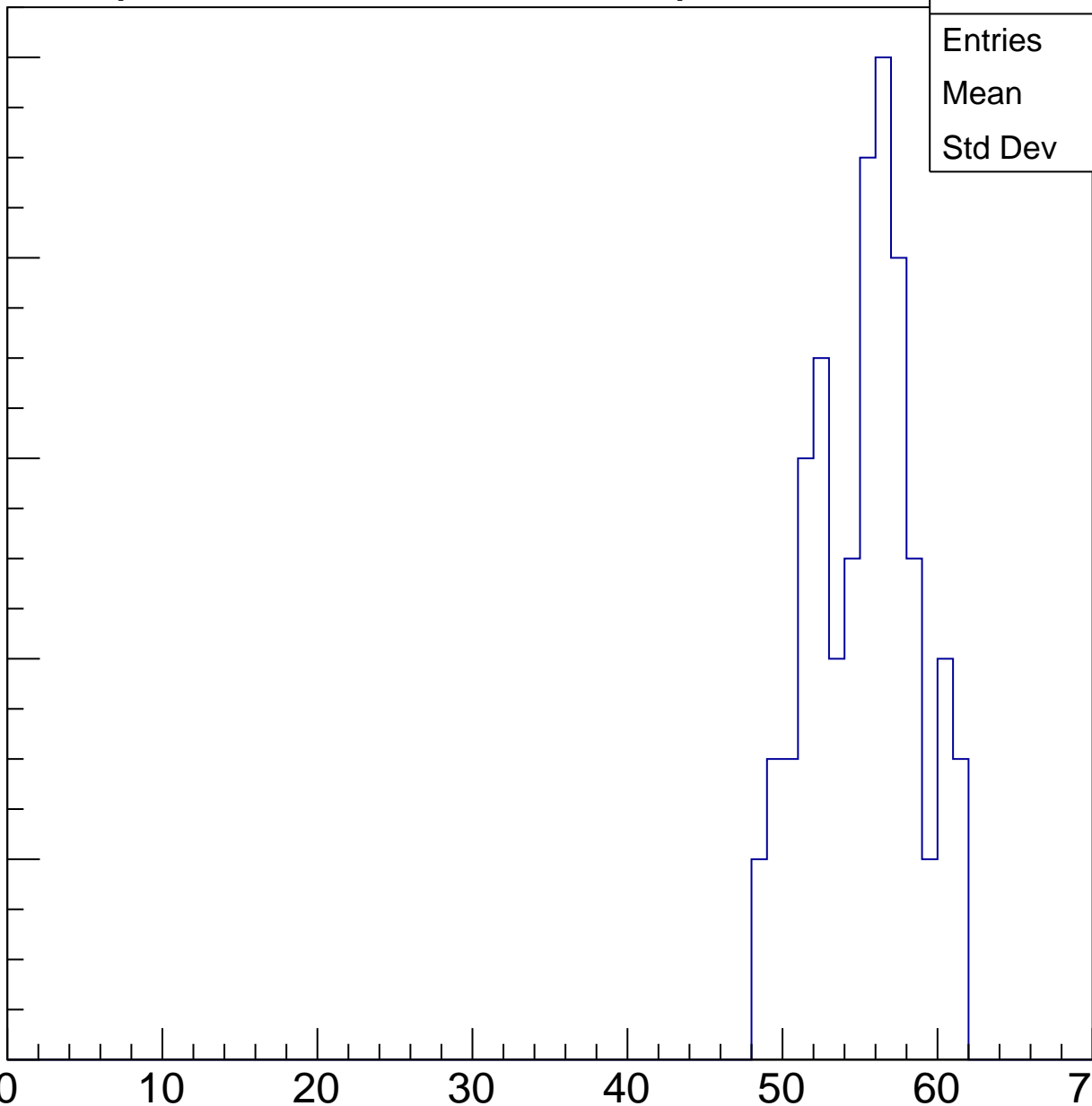
30

40

50

60

ampl

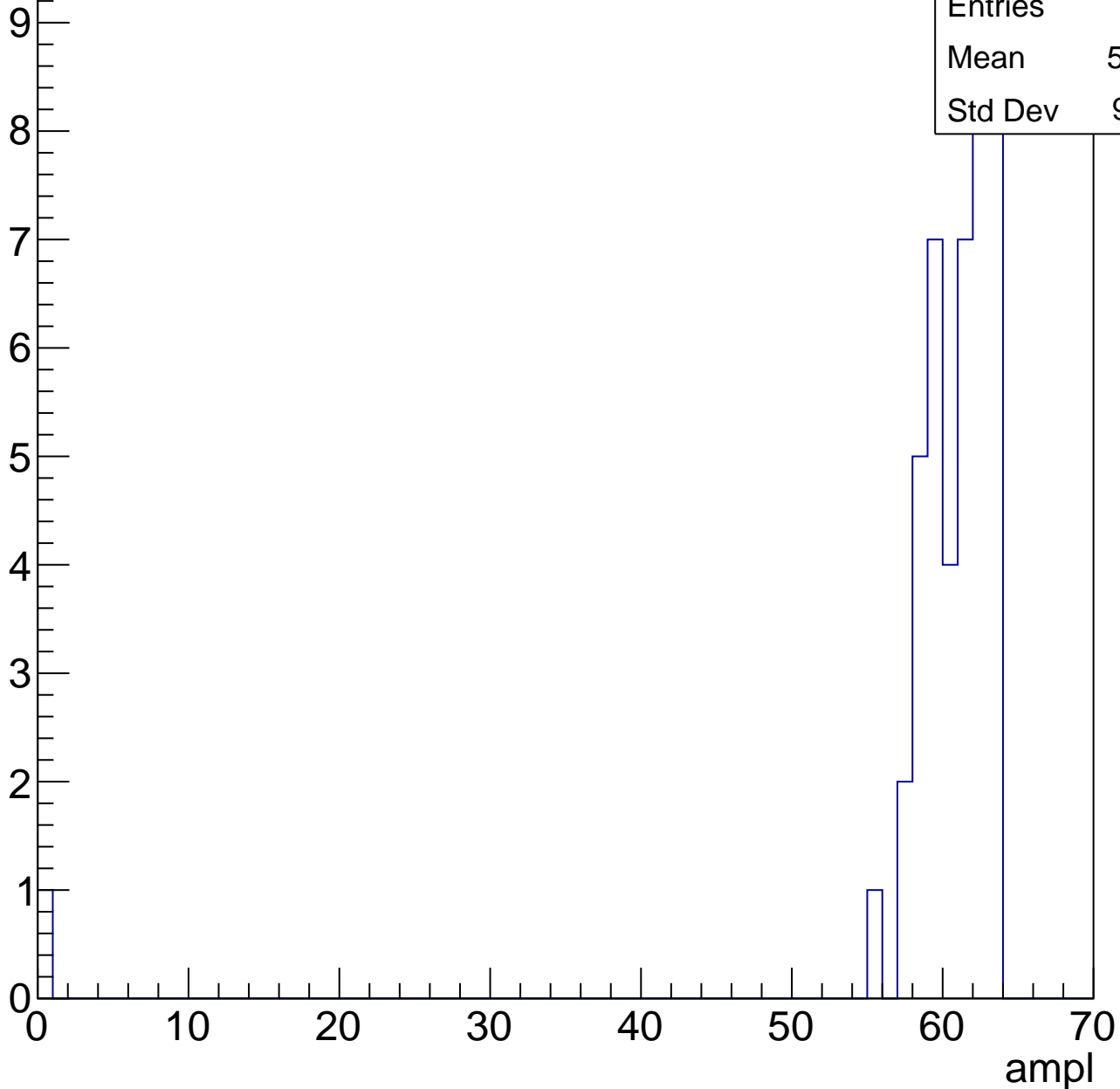


# B1L003S, U23-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	59.14
Std Dev	9.241



# B1L003S, U23-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

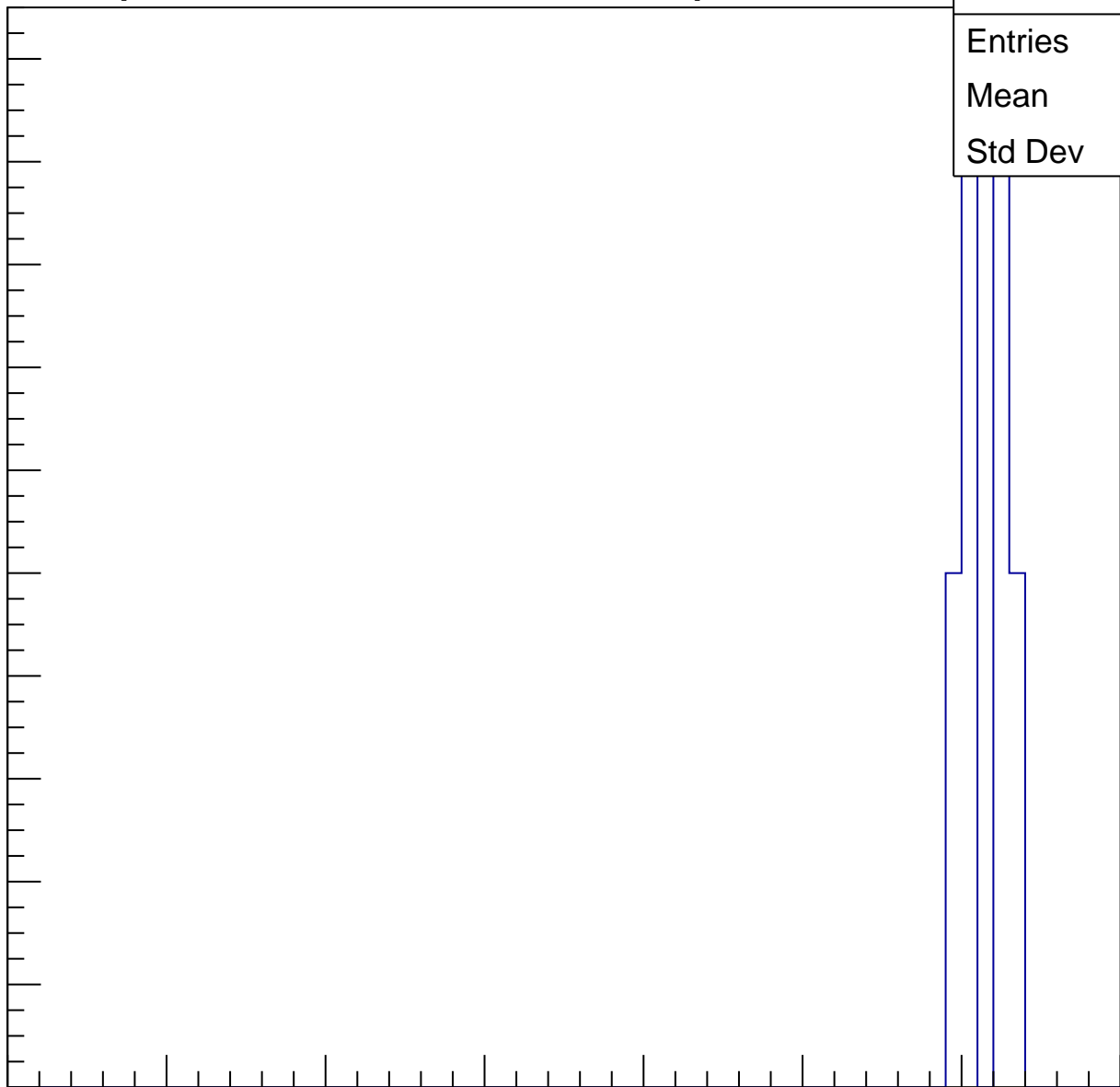
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61
Std Dev	1.414

0 10 20 30 40 50 60 70

ampl





# B1L003S, U23-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch6, adc0

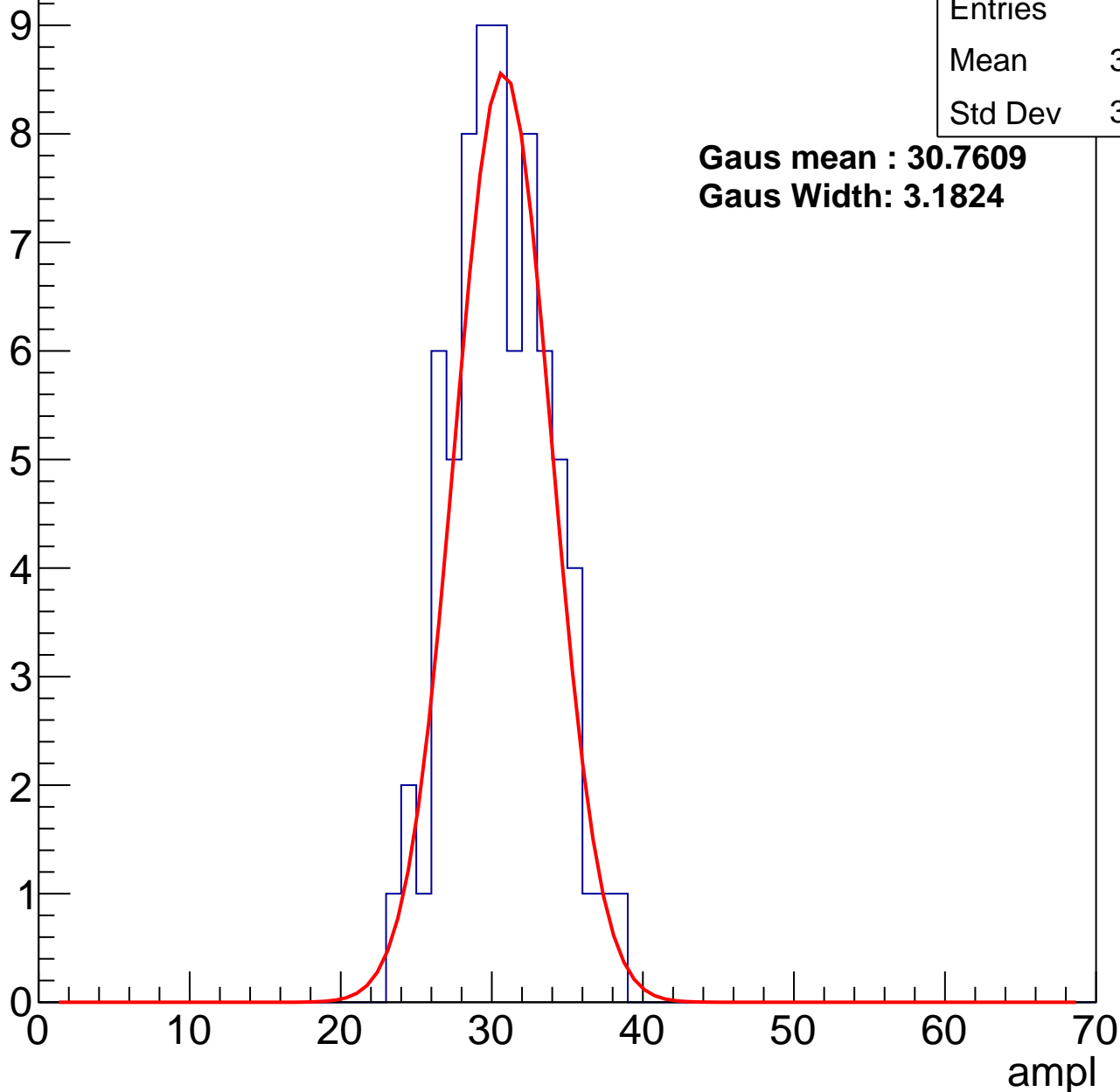
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	30.18
Std Dev	3.198

**Gaus mean : 30.7609**

**Gaus Width: 3.1824**



# B1L003S, U23-ch6, adc1

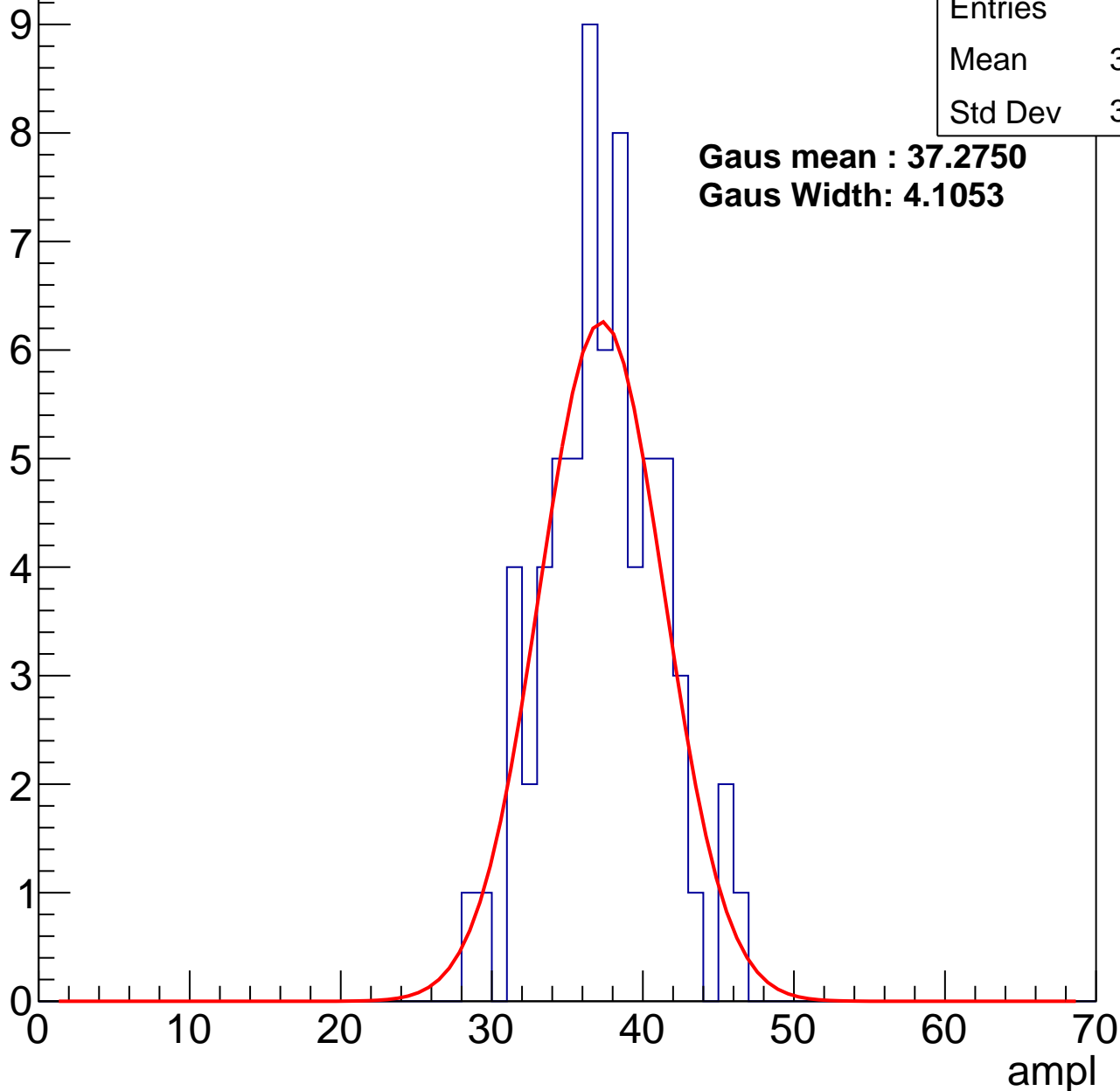
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.94
Std Dev	3.785

**Gaus mean : 37.2750**

**Gaus Width: 4.1053**



# B1L003S, U23-ch6, adc2

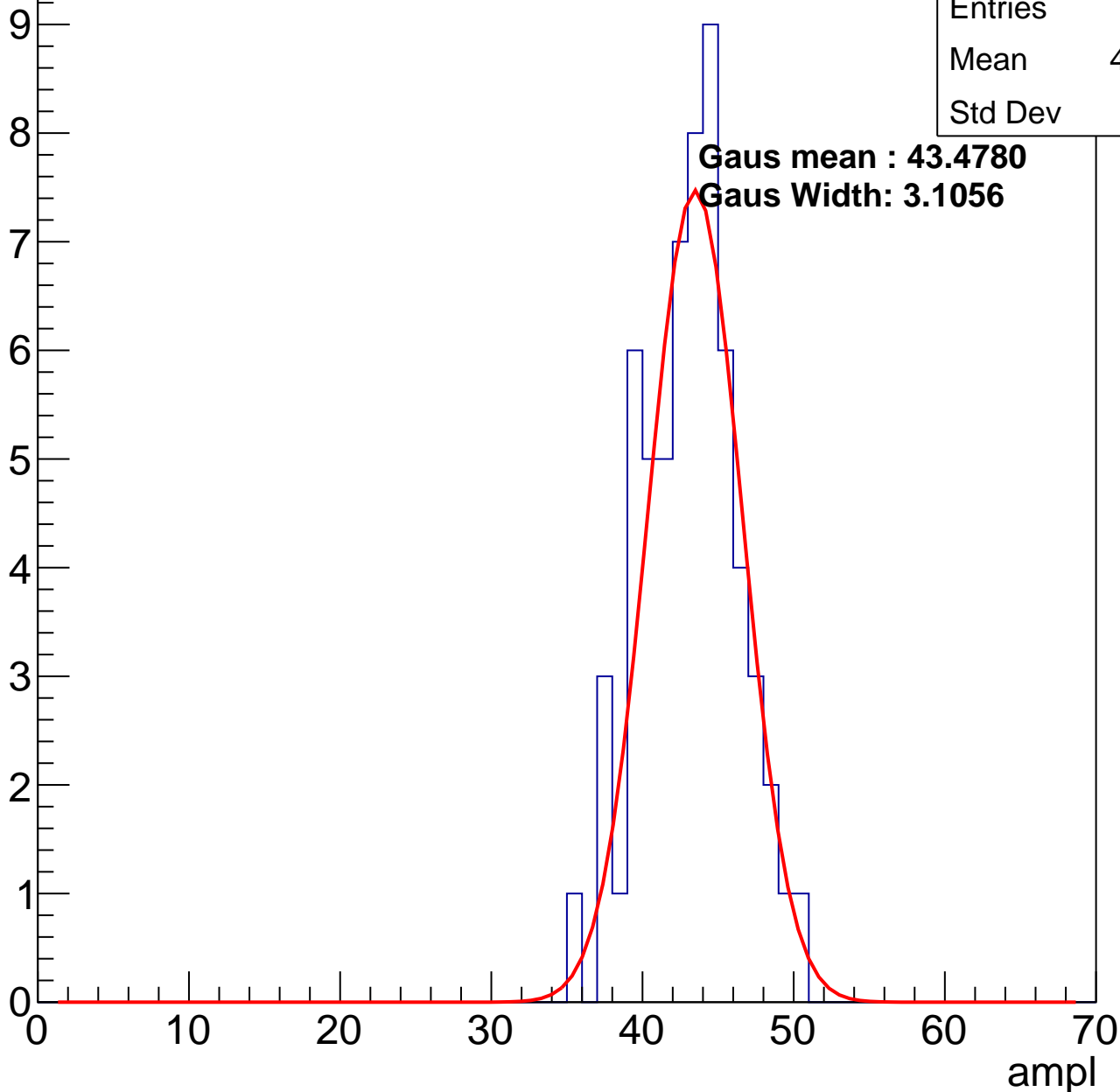
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.69
Std Dev	3.14

**Gaus mean : 43.4780**

**Gaus Width: 3.1056**

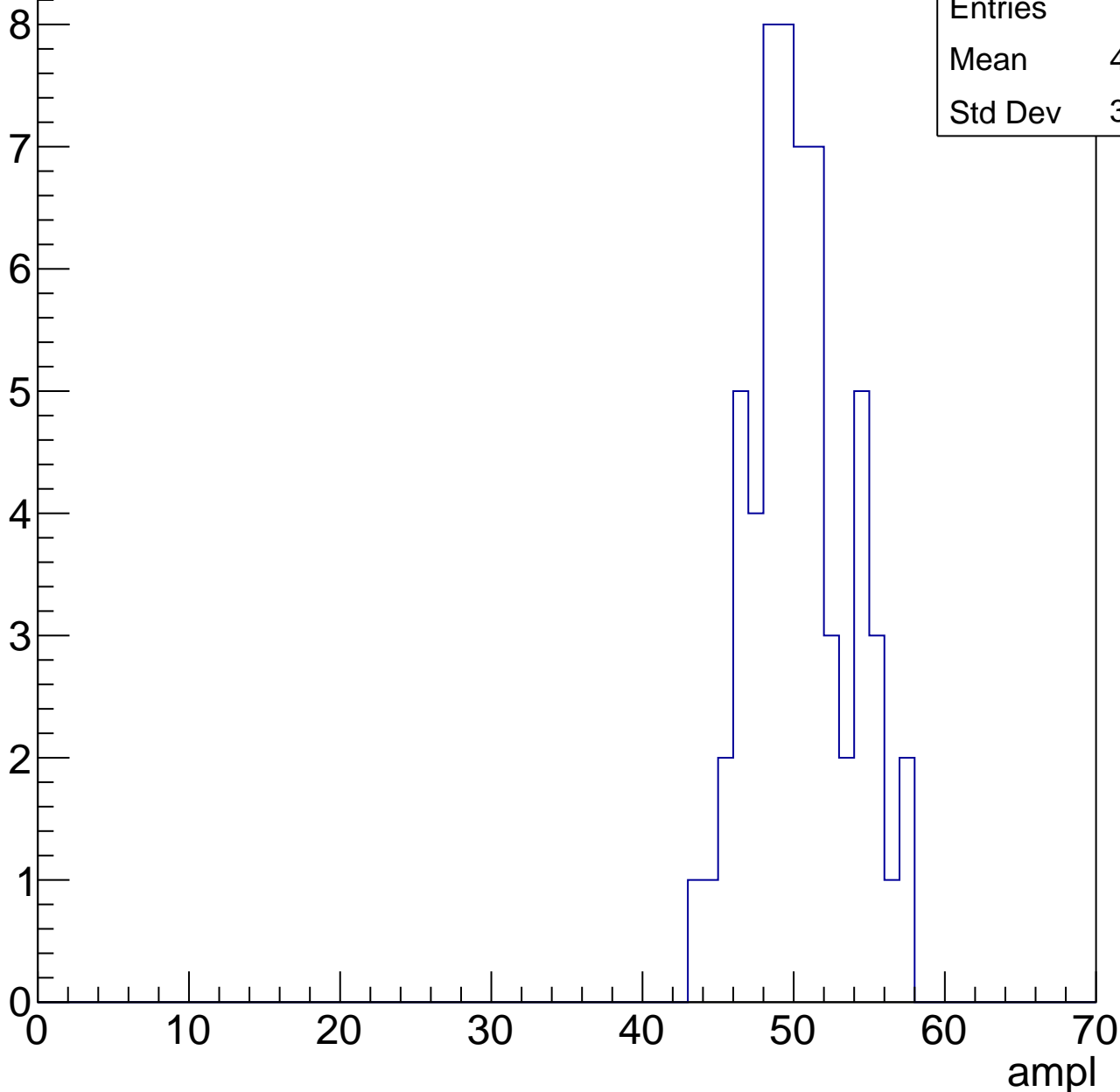


# B1L003S, U23-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	49.92
Std Dev	3.233

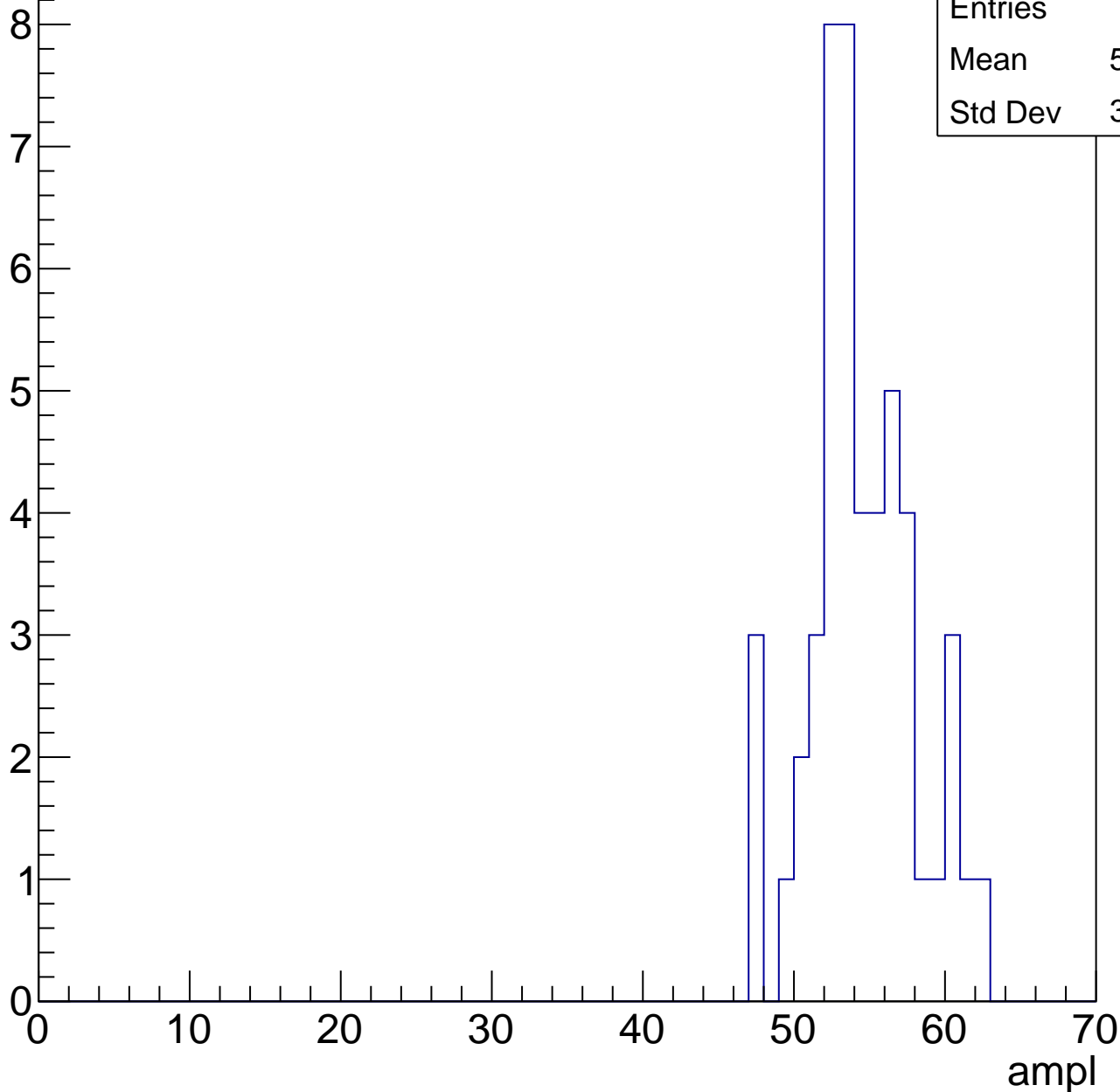


# B1L003S, U23-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

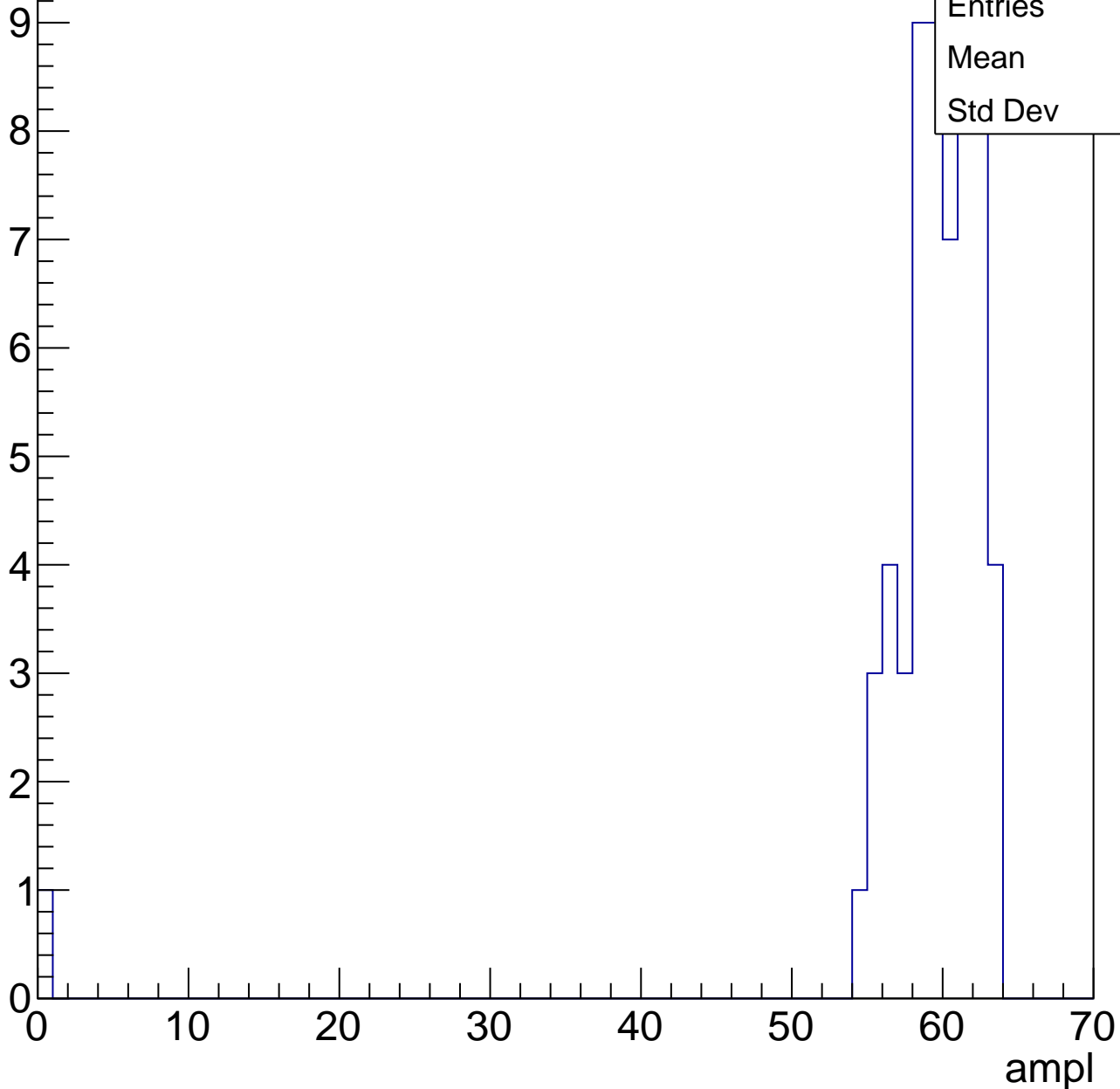
Entries	49
Mean	54.02
Std Dev	3.479



# B1L003S, U23-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

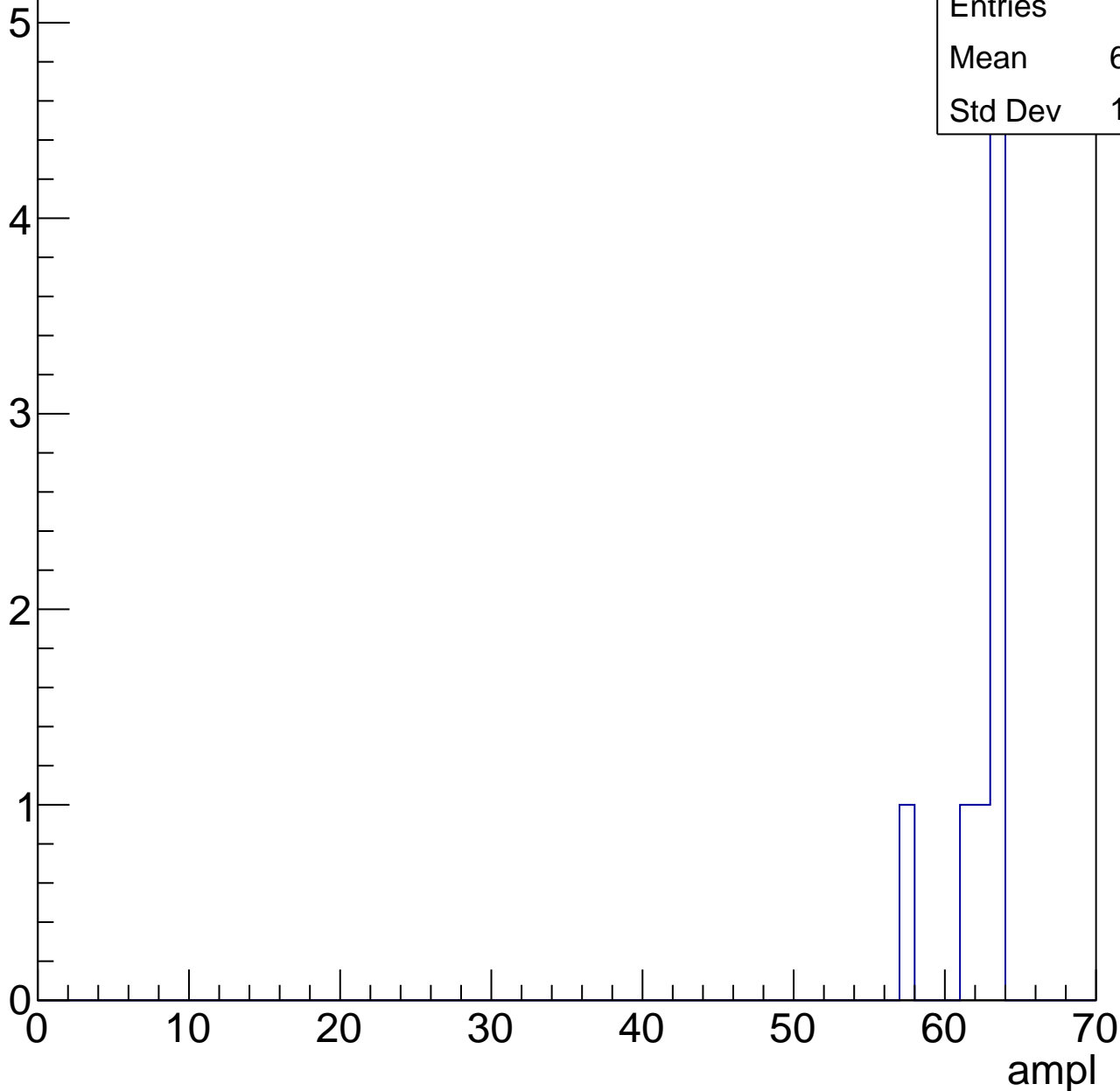


# B1L003S, U23-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	8
Mean	61.88
Std Dev	1.965

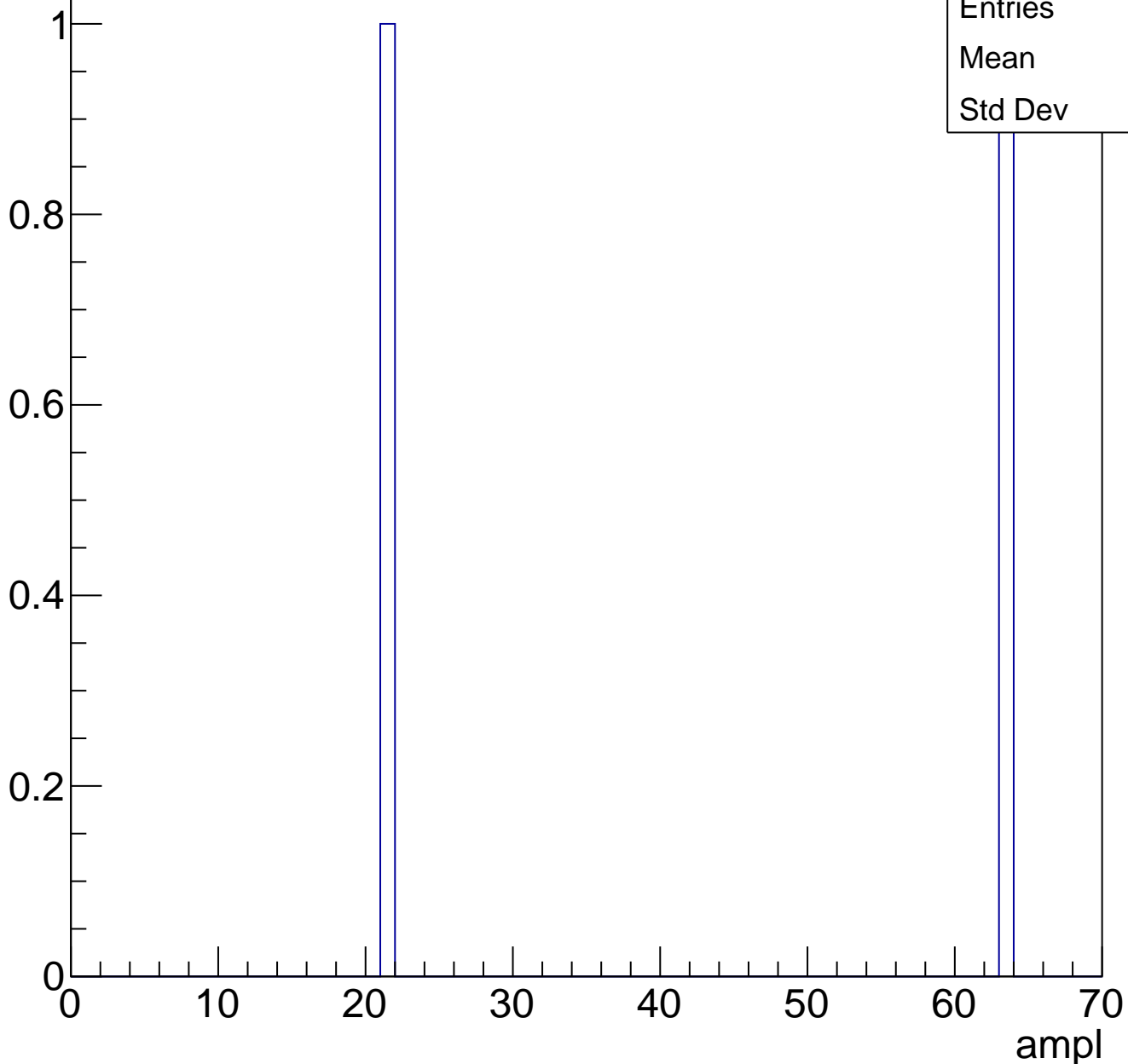




# B1L003S, U23-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	42
Std Dev	21

# B1L003S, U23-ch7, adc0

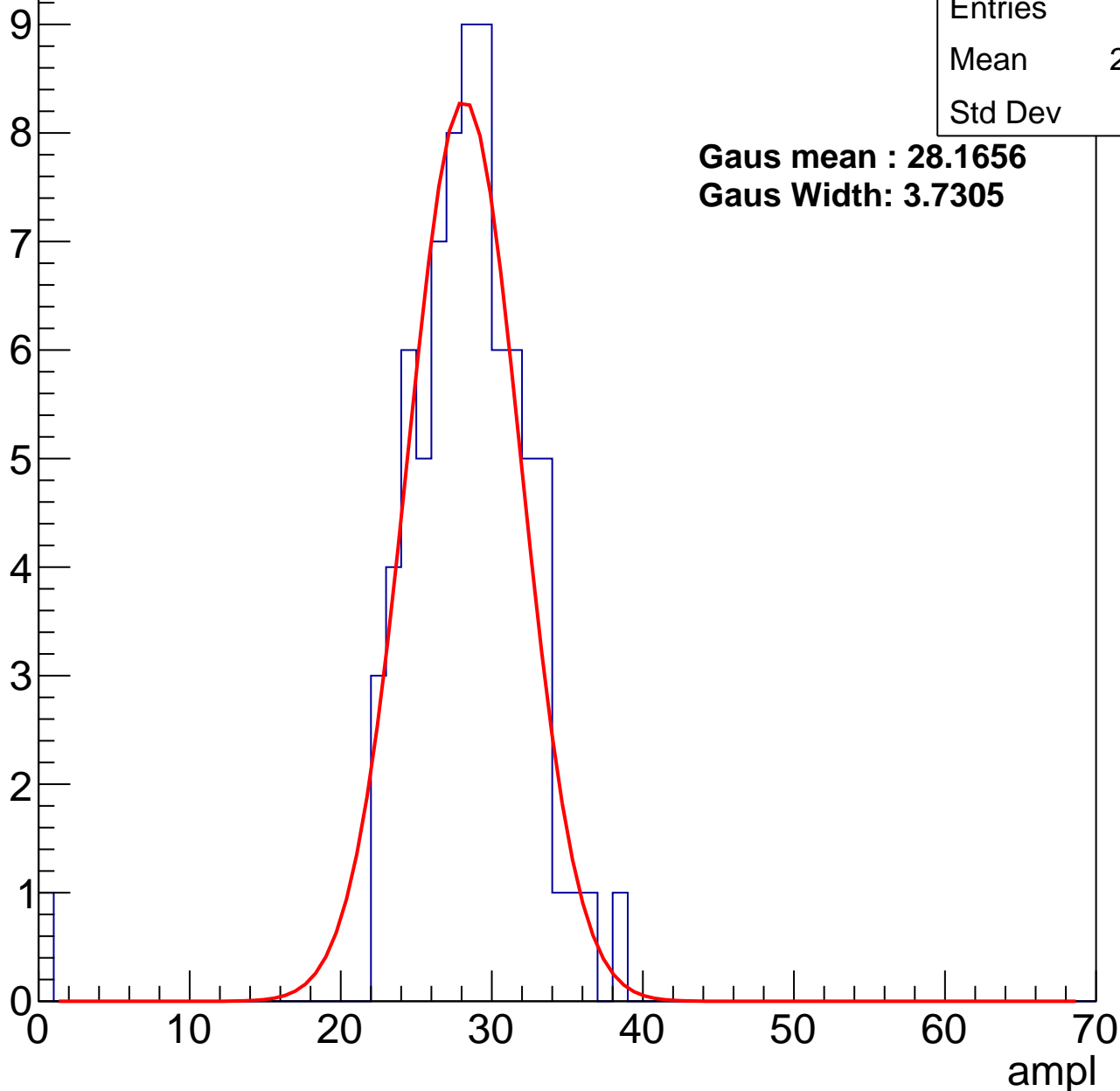
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	27.85
Std Dev	4.68

**Gaus mean : 28.1656**

**Gaus Width: 3.7305**



# B1L003S, U23-ch7, adc1

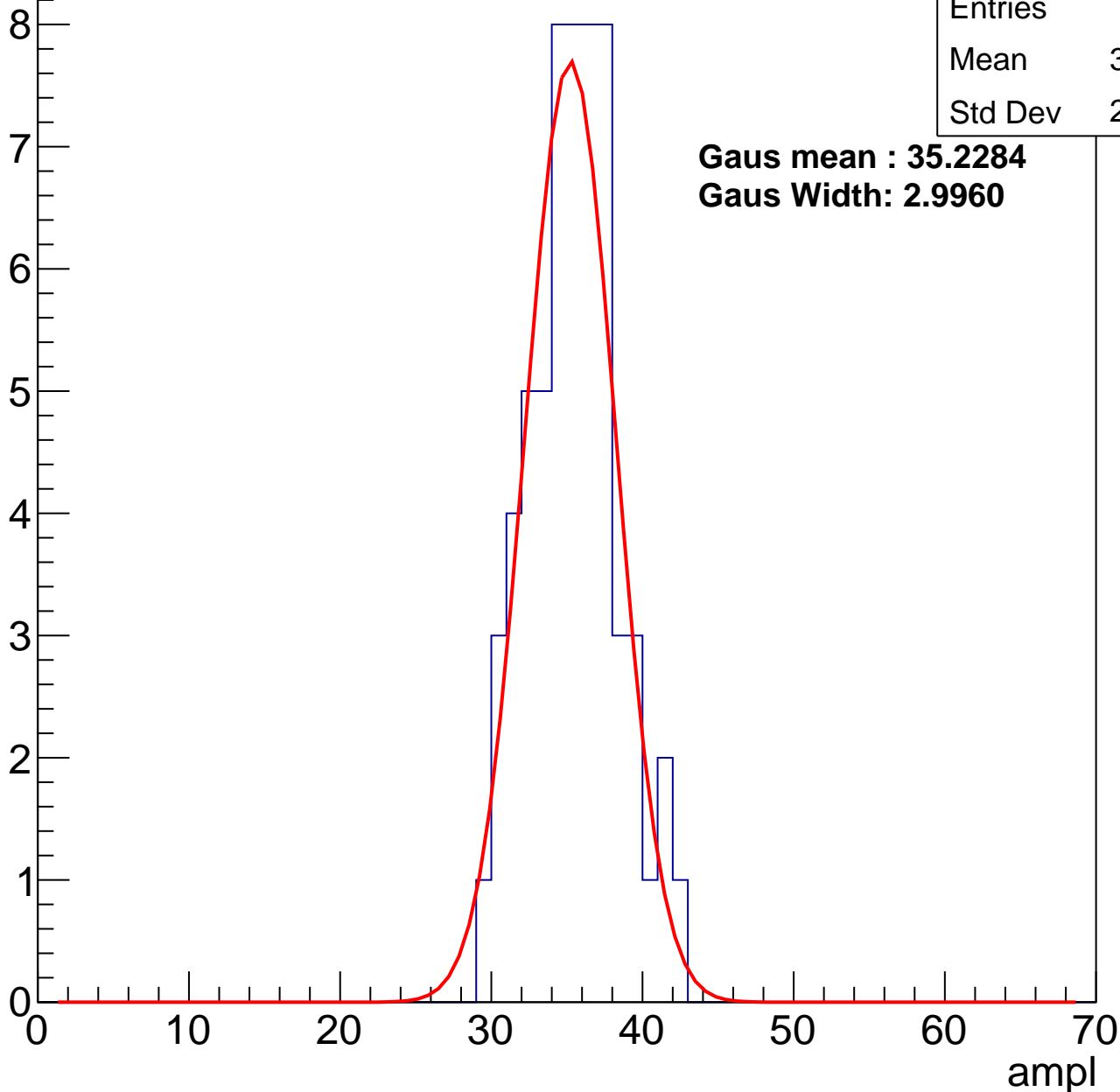
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	34.98
Std Dev	2.913

**Gaus mean : 35.2284**

**Gaus Width: 2.9960**



# B1L003S, U23-ch7, adc2

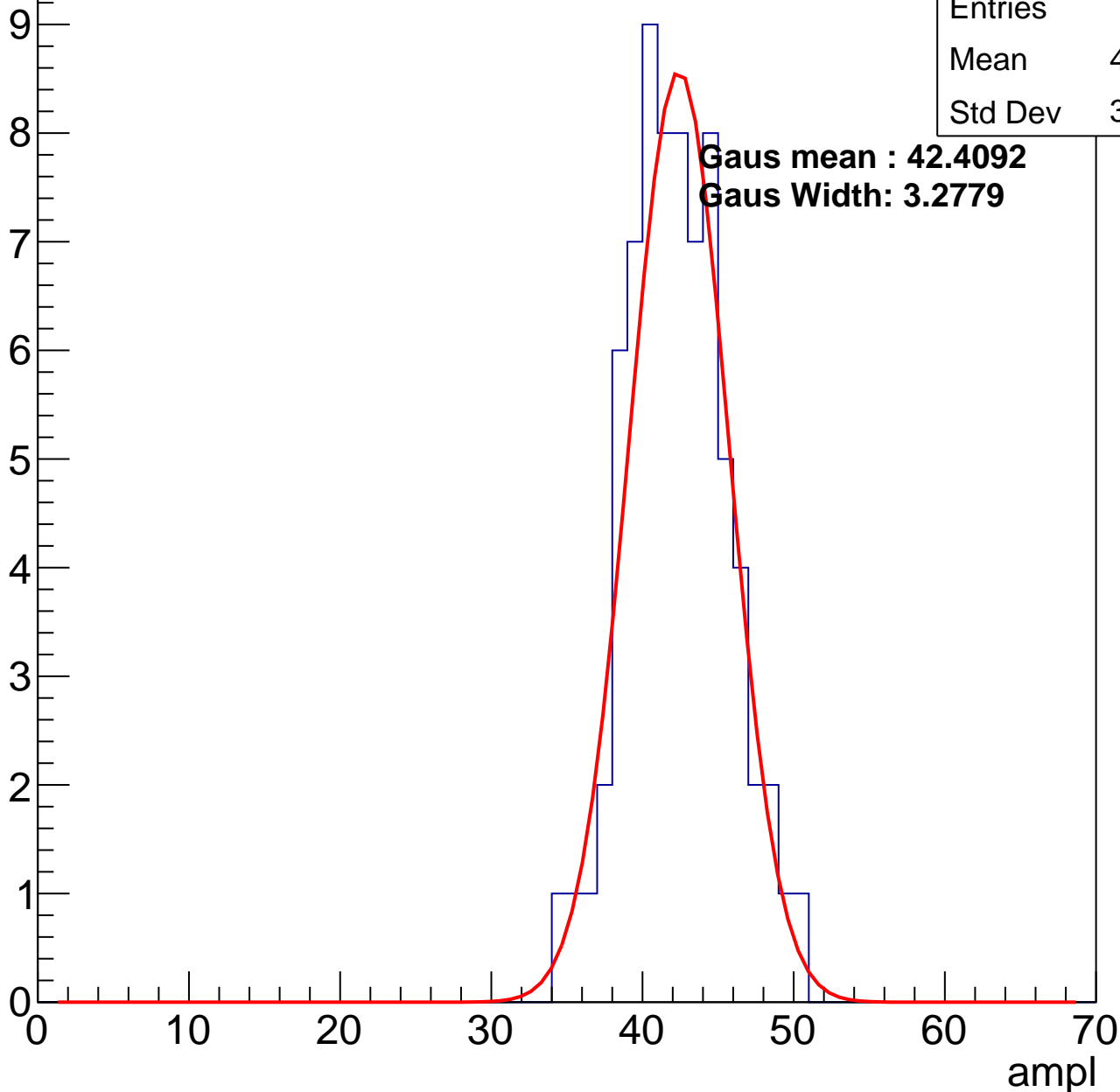
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	41.85
Std Dev	3.276

**Gaus mean : 42.4092**

**Gaus Width: 3.2779**

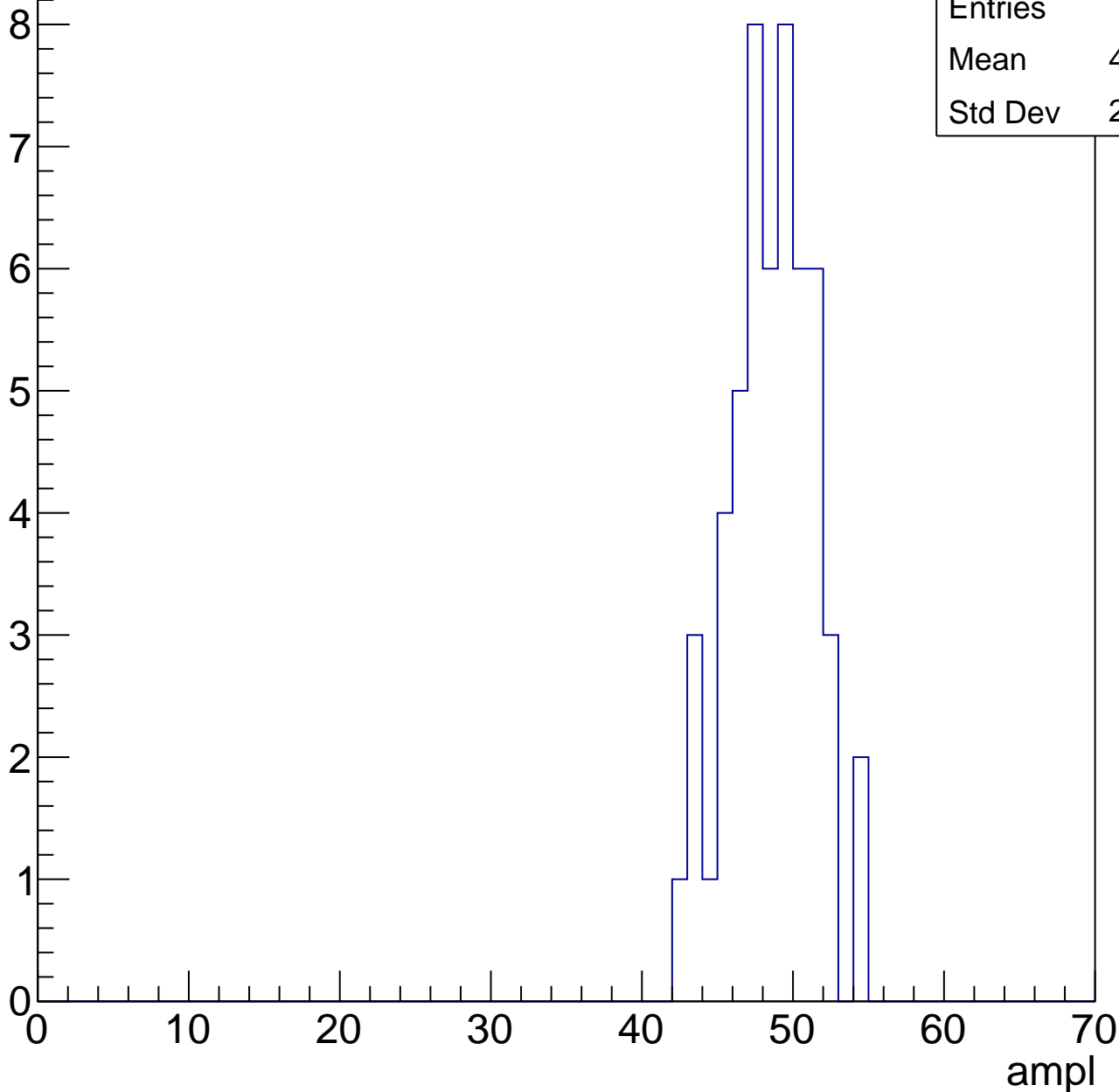


# B1L003S, U23-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	48.13
Std Dev	2.734

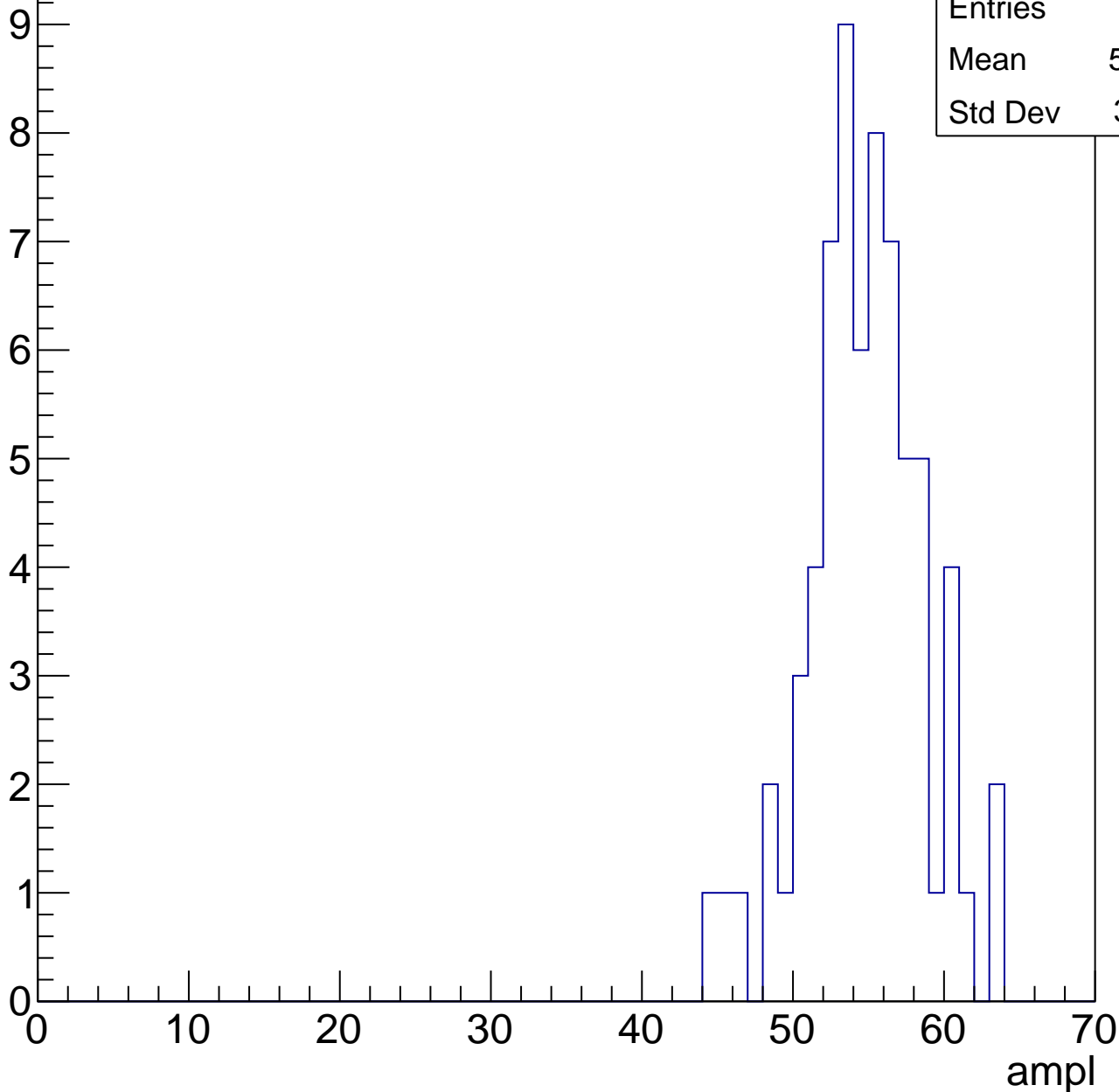


# B1L003S, U23-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	54.29
Std Dev	3.831



# B1L003S, U23-ch7, adc5

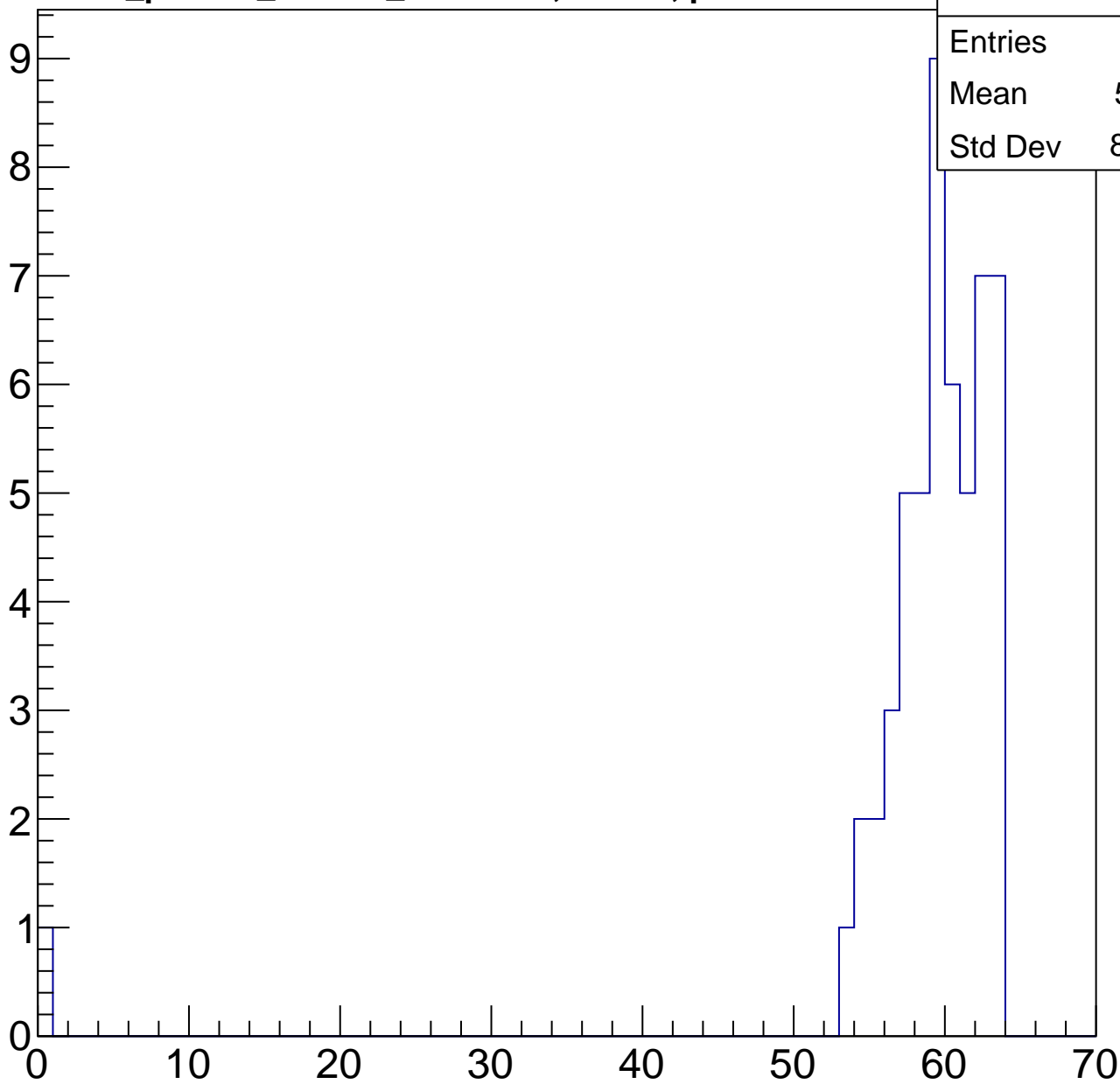
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	58.21
Std Dev	8.488

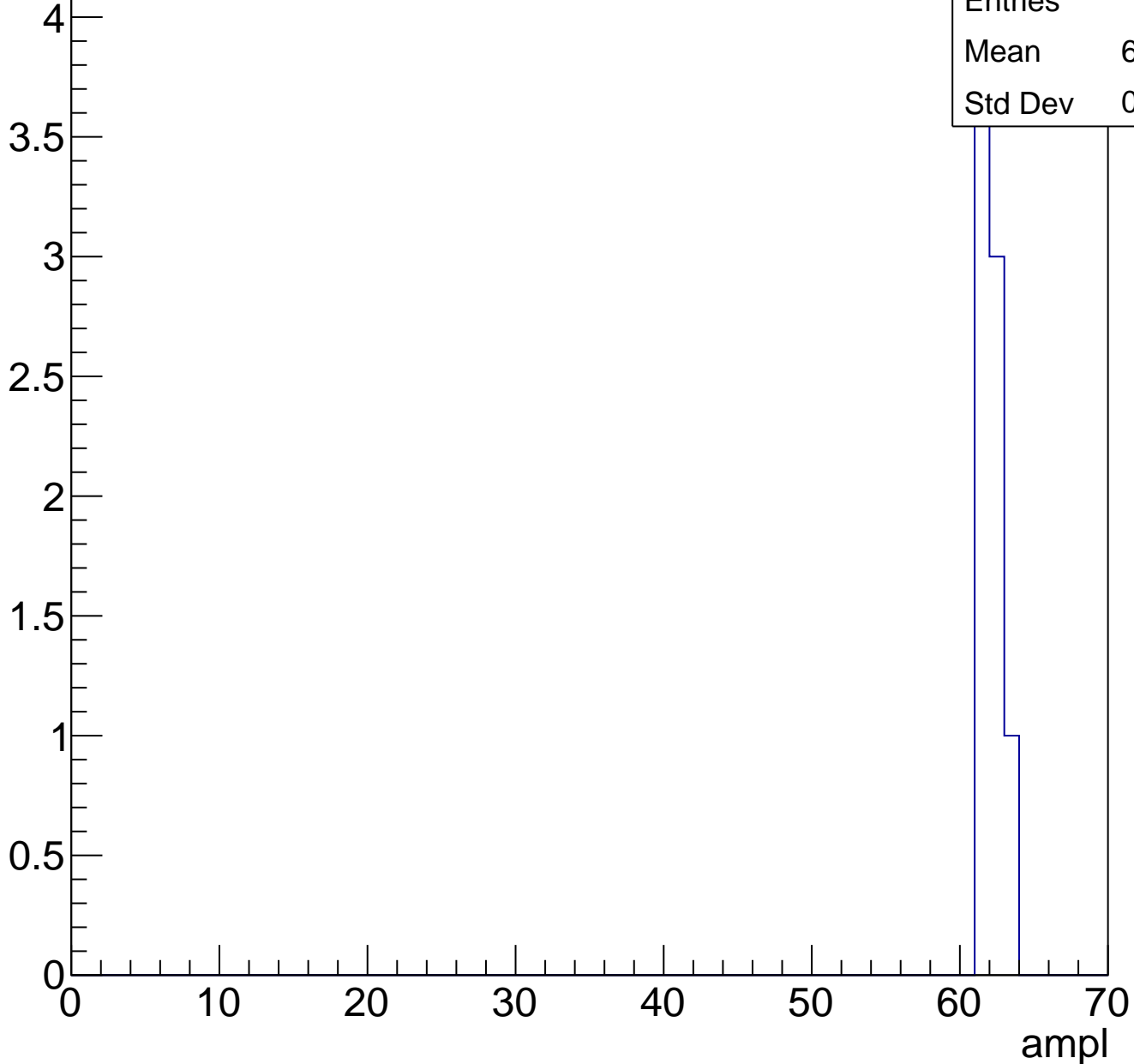
ampl



# B1L003S, U23-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch8, adc0

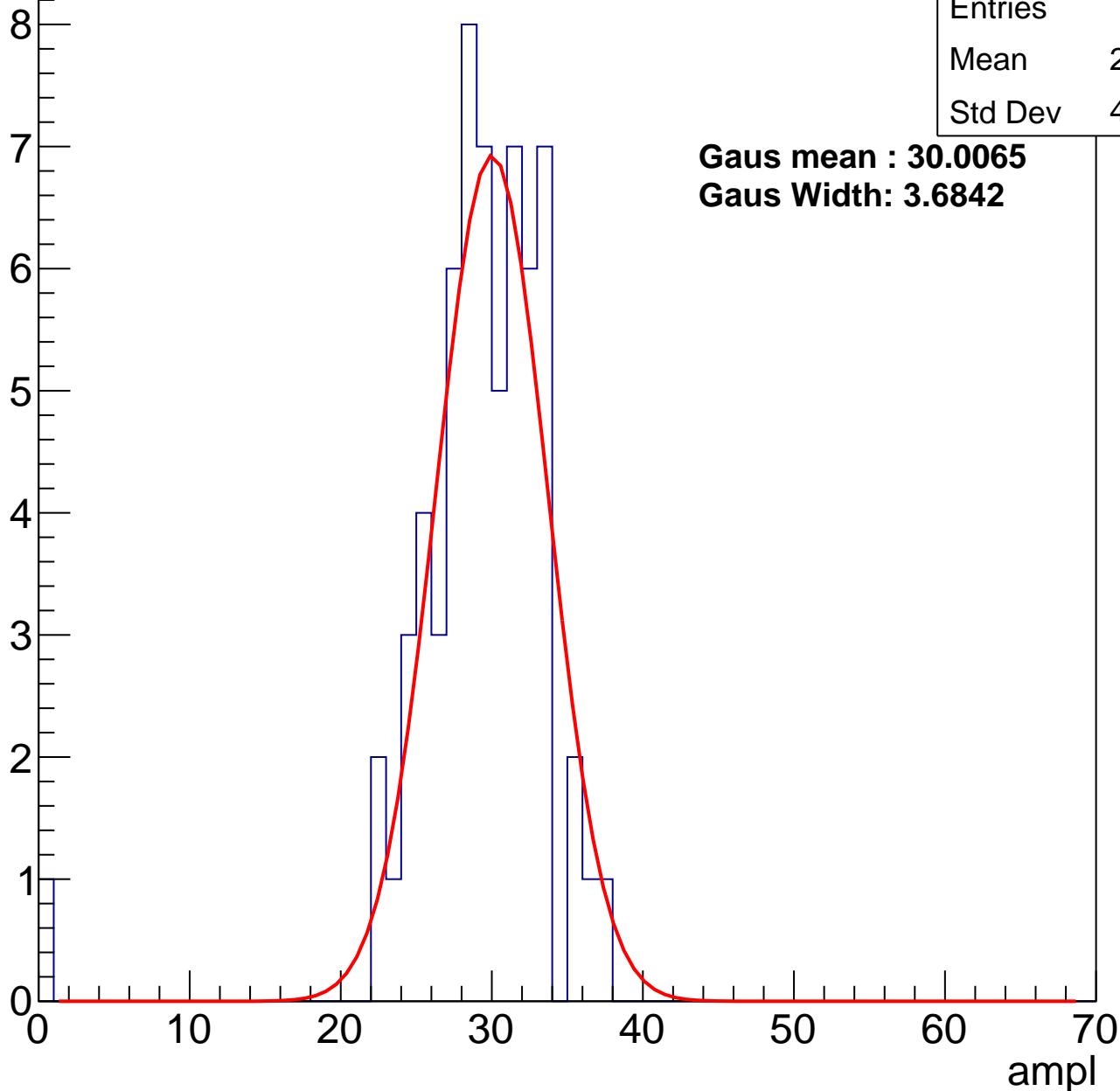
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.73
Std Dev	4.925

**Gaus mean : 30.0065**

**Gaus Width: 3.6842**



# B1L003S, U23-ch8, adc1

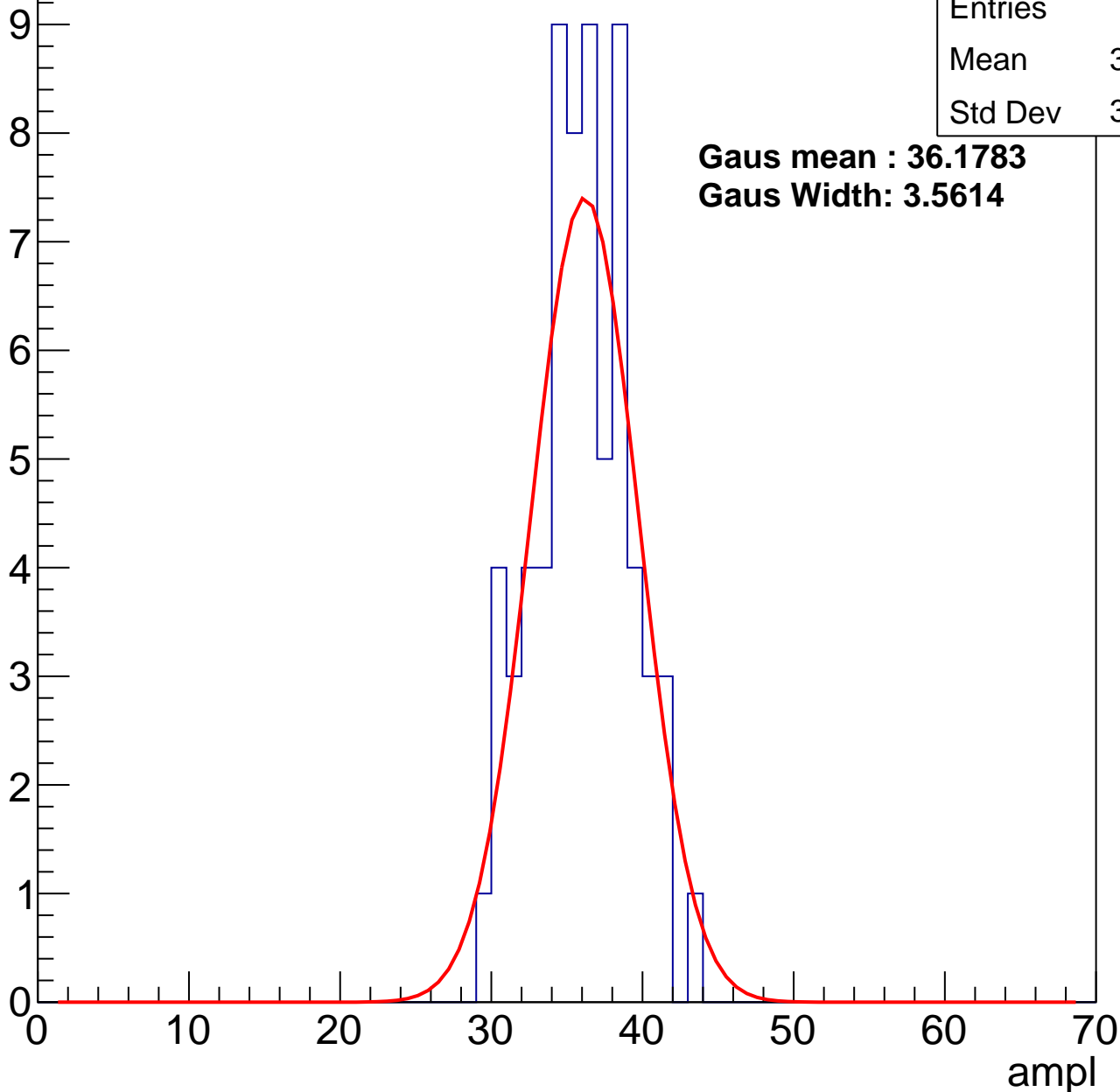
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	35.54
Std Dev	3.107

**Gaus mean : 36.1783**

**Gaus Width: 3.5614**



# B1L003S, U23-ch8, adc2

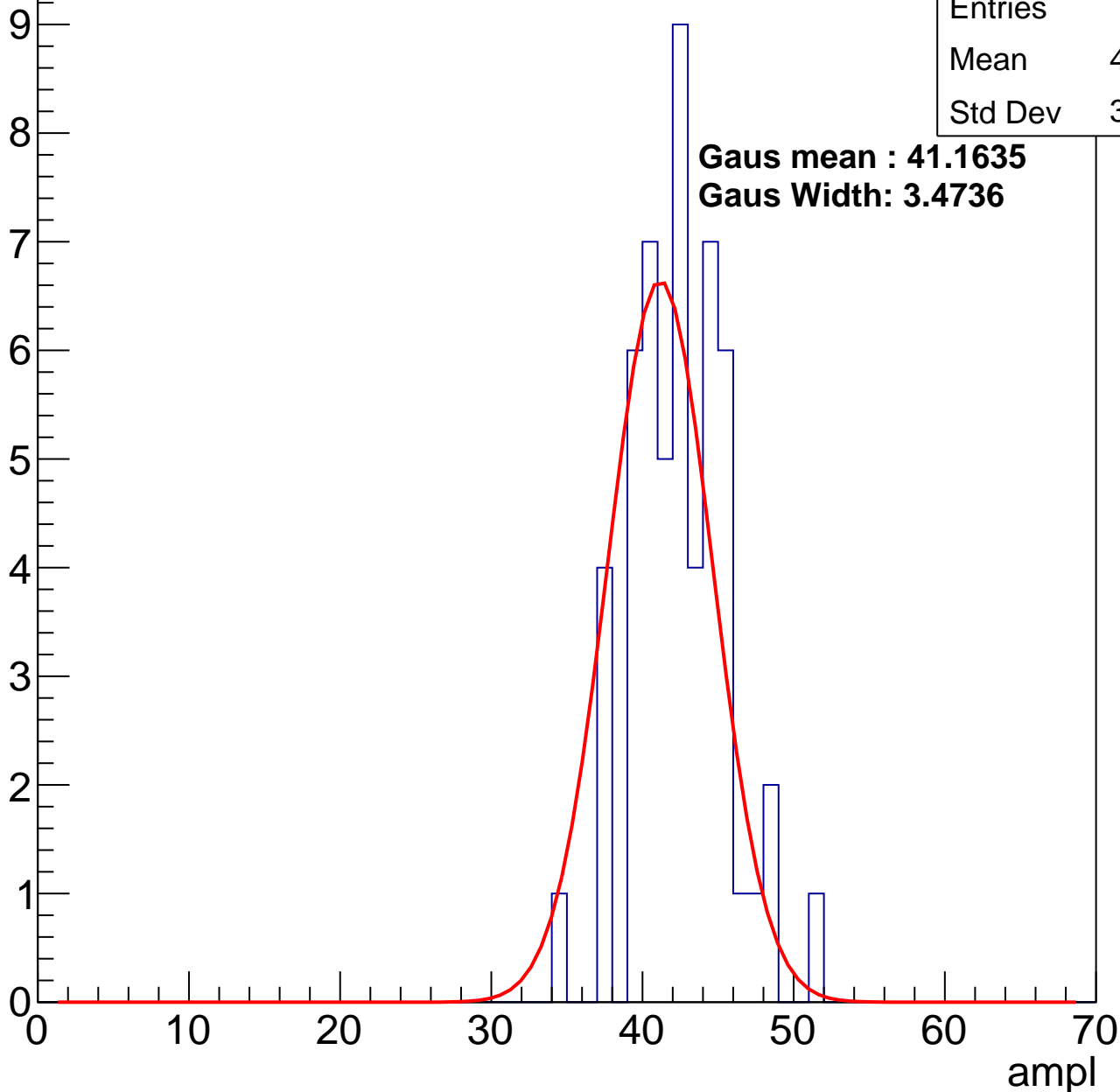
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.02
Std Dev	3.136

**Gaus mean : 41.1635**

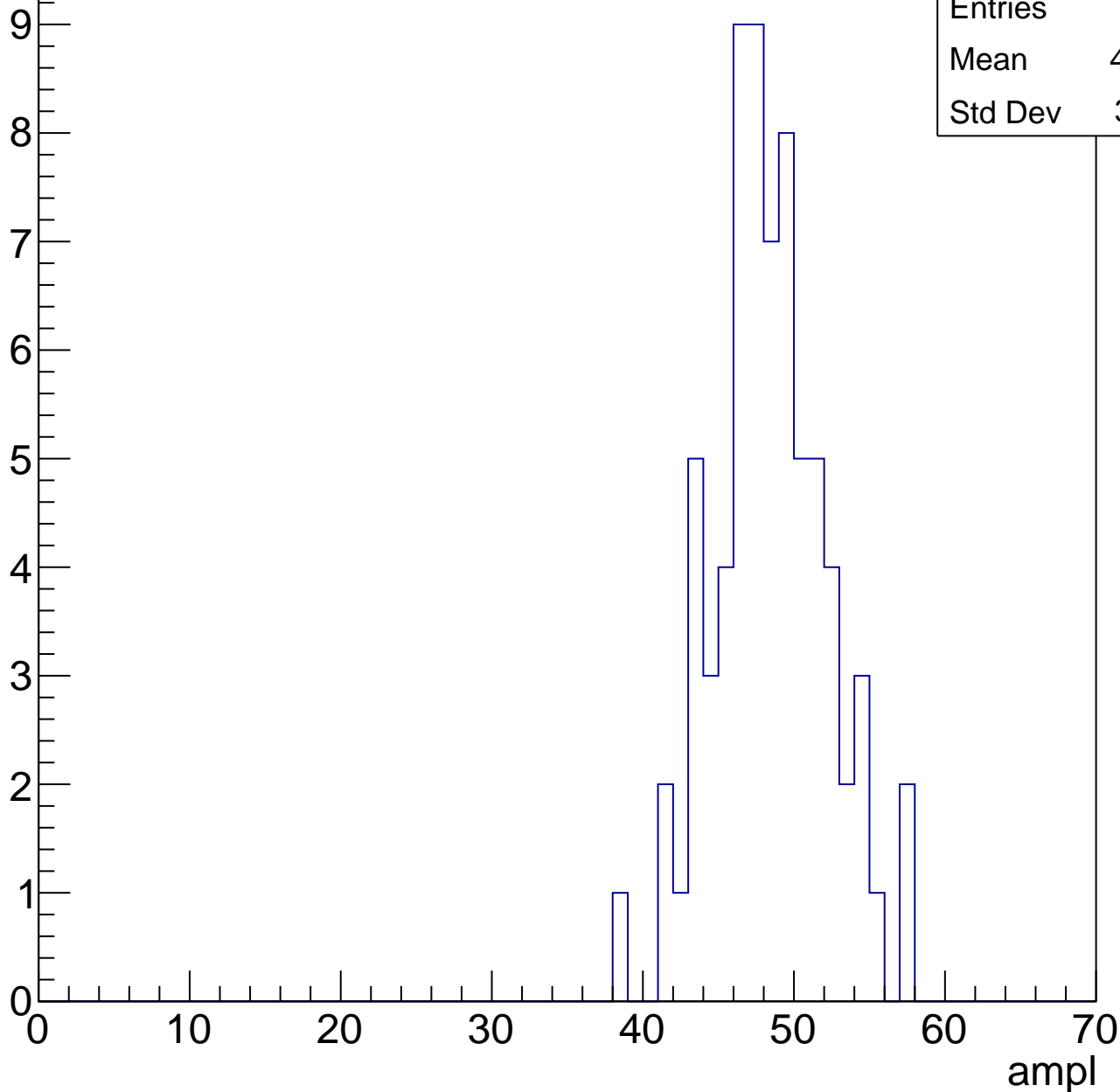
**Gaus Width: 3.4736**



# B1L003S, U23-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



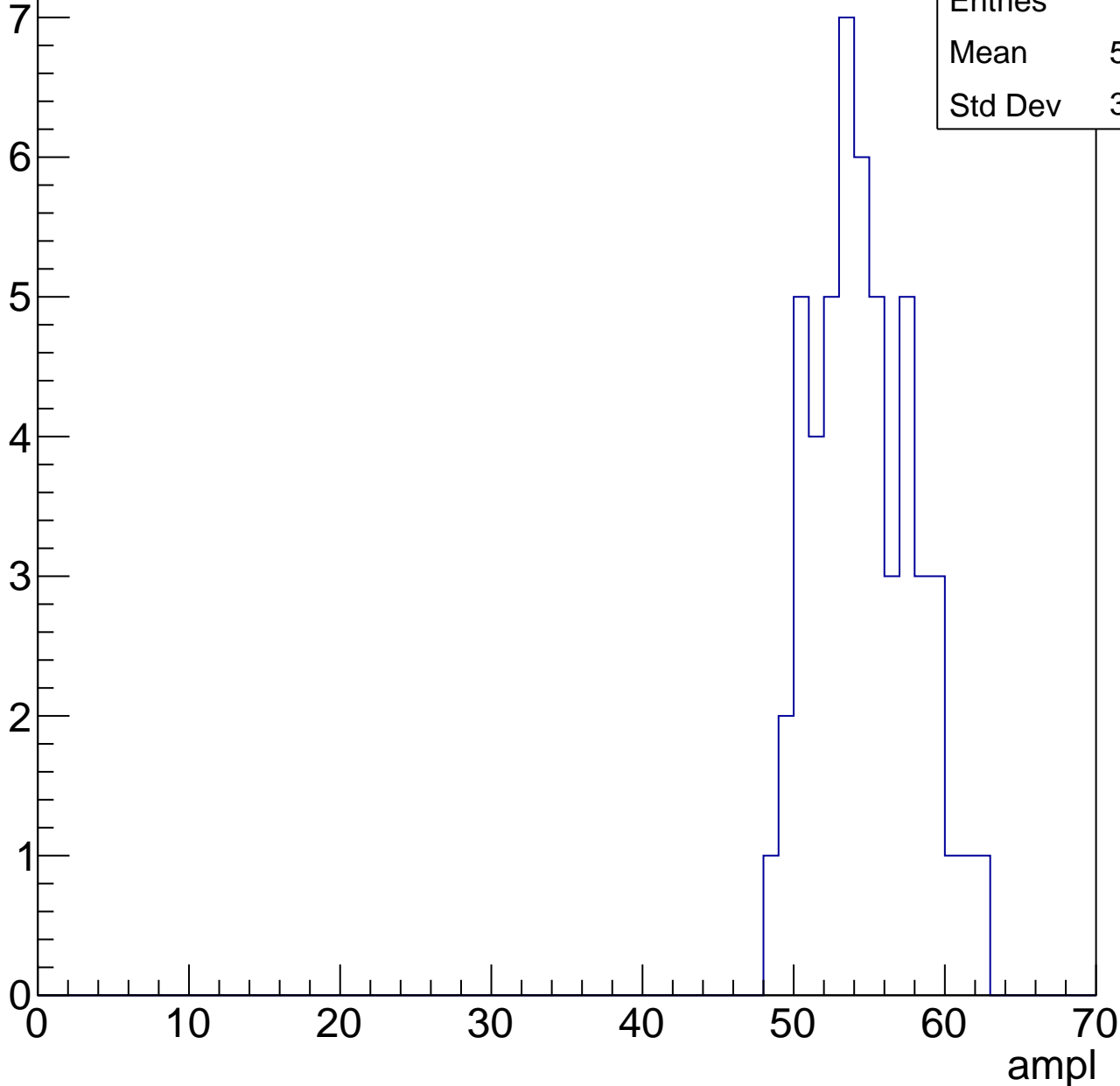
Entries	71
Mean	47.94
Std Dev	3.741

# B1L003S, U23-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	54.17
Std Dev	3.286

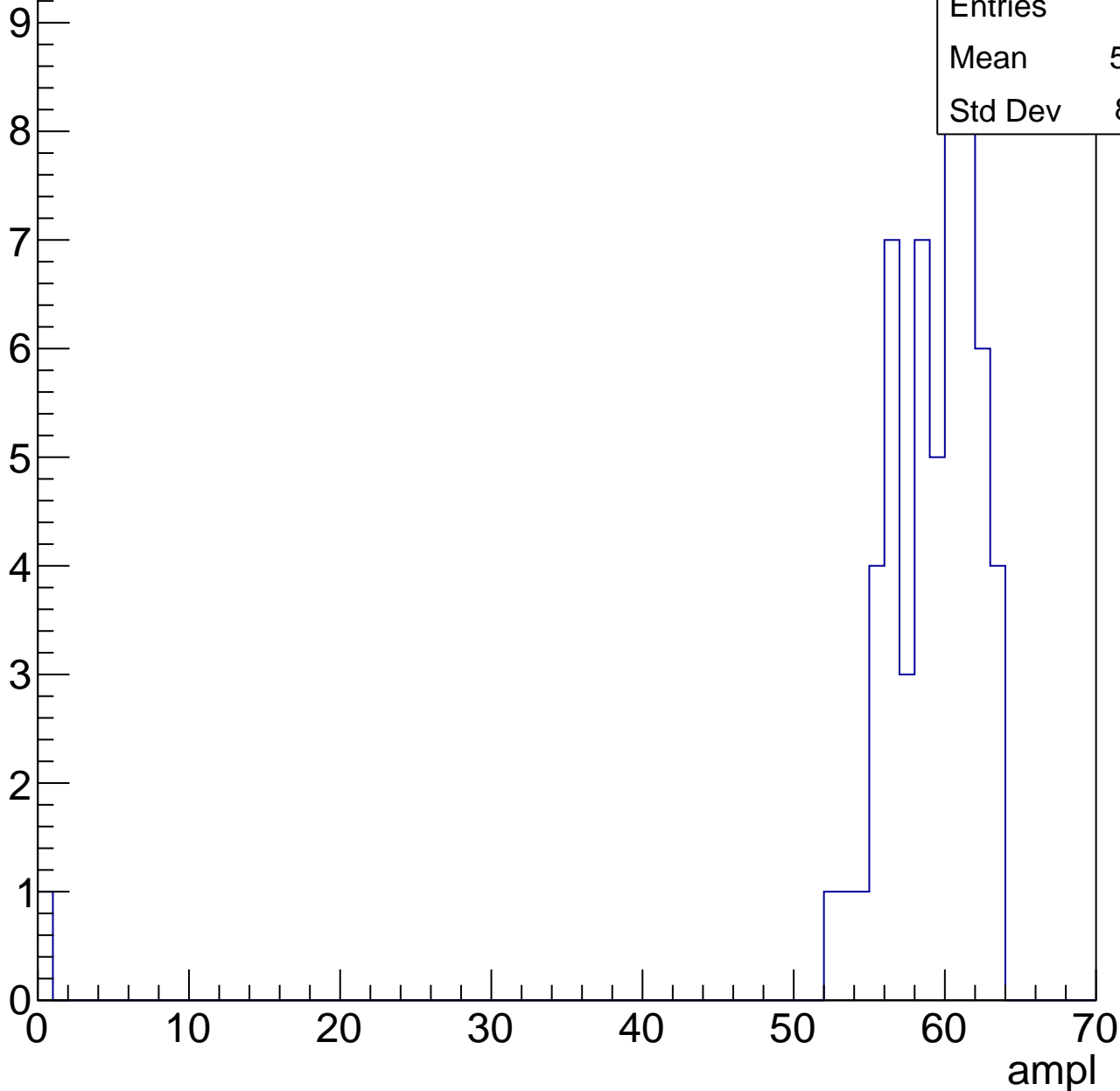


# B1L003S, U23-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	57.86
Std Dev	8.121

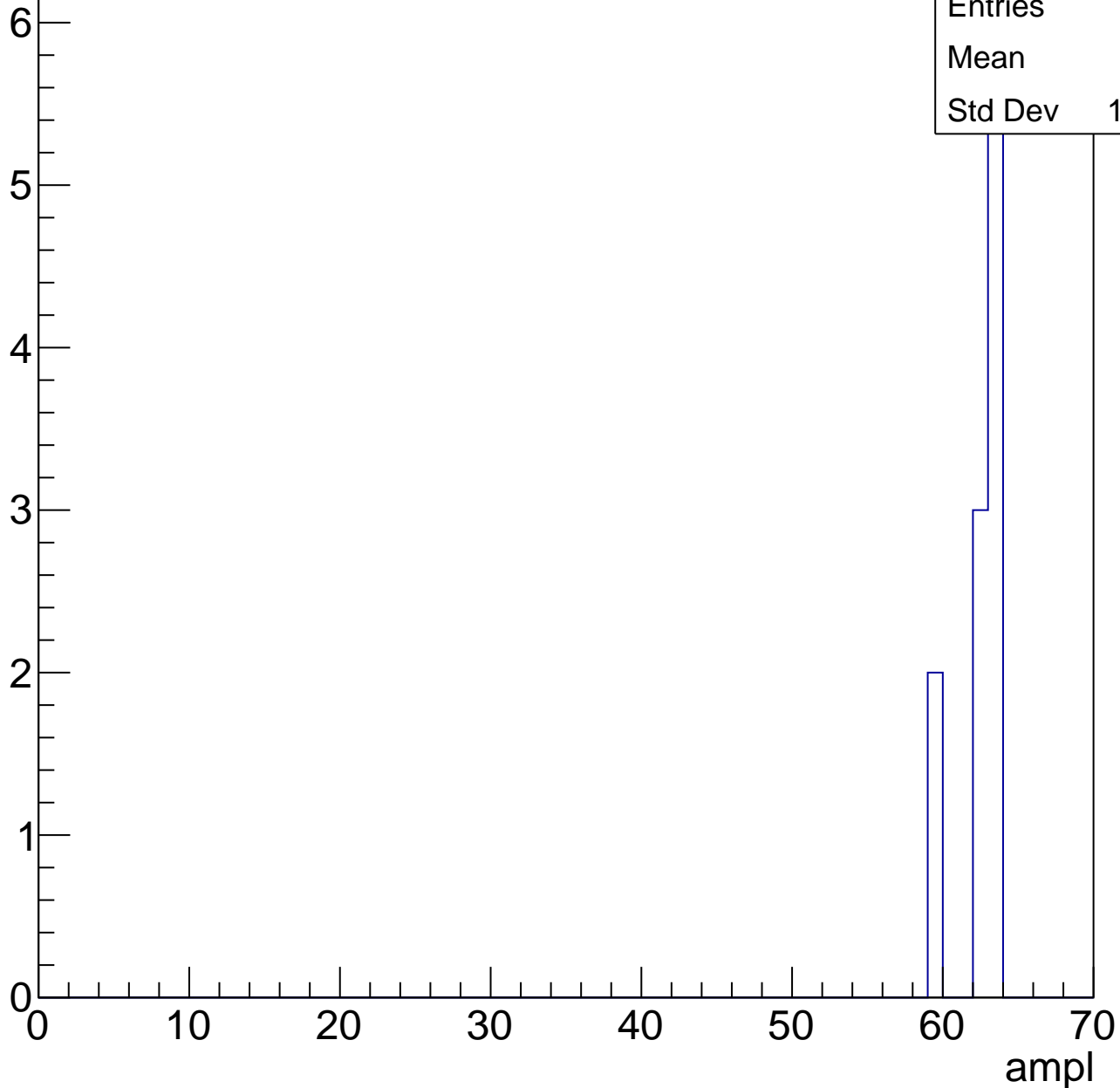


# B1L003S, U23-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62
Std Dev	1.477





# B1L003S, U23-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch9, adc0

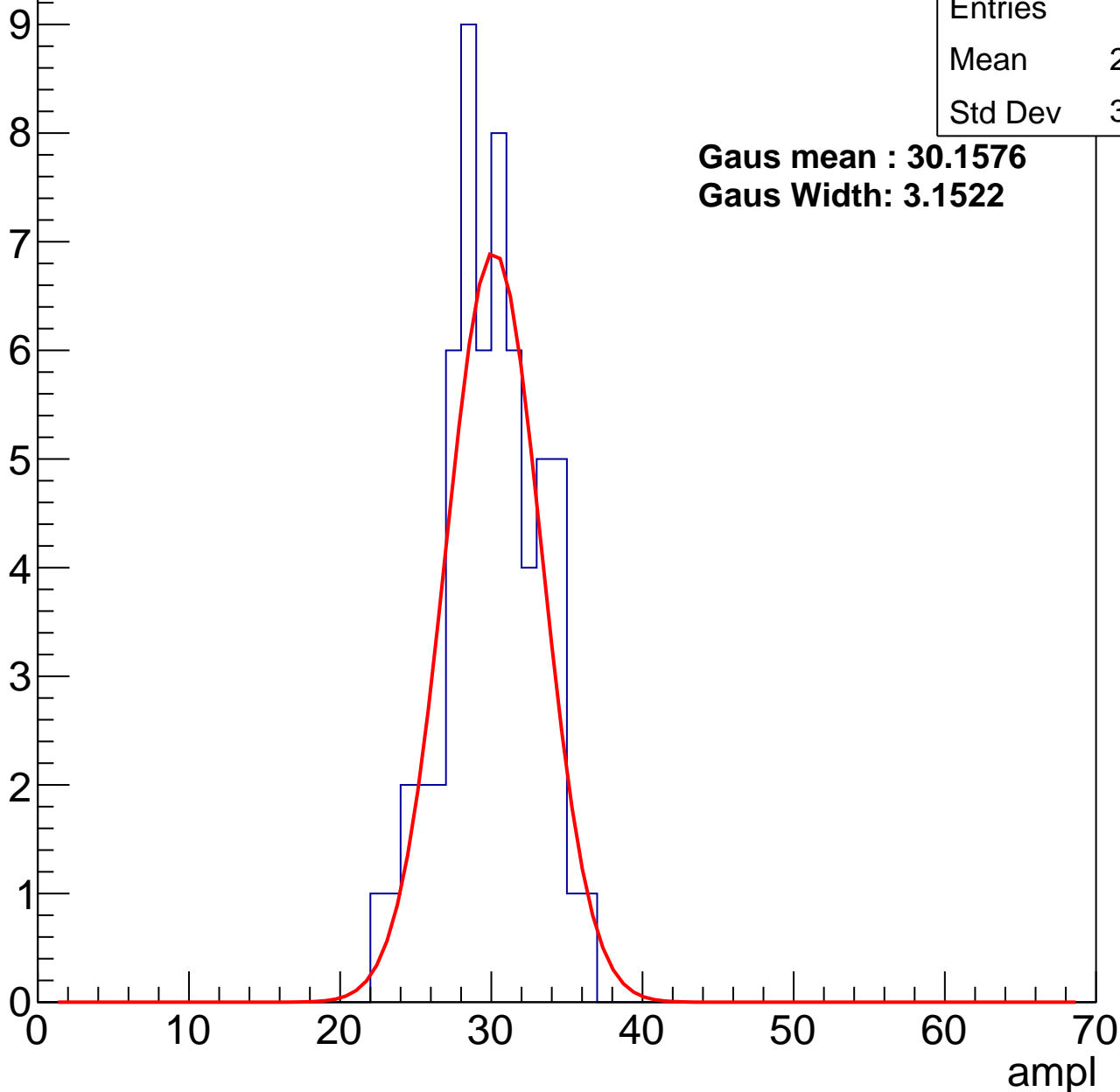
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.54
Std Dev	3.077

**Gaus mean : 30.1576**

**Gaus Width: 3.1522**



# B1L003S, U23-ch9, adc1

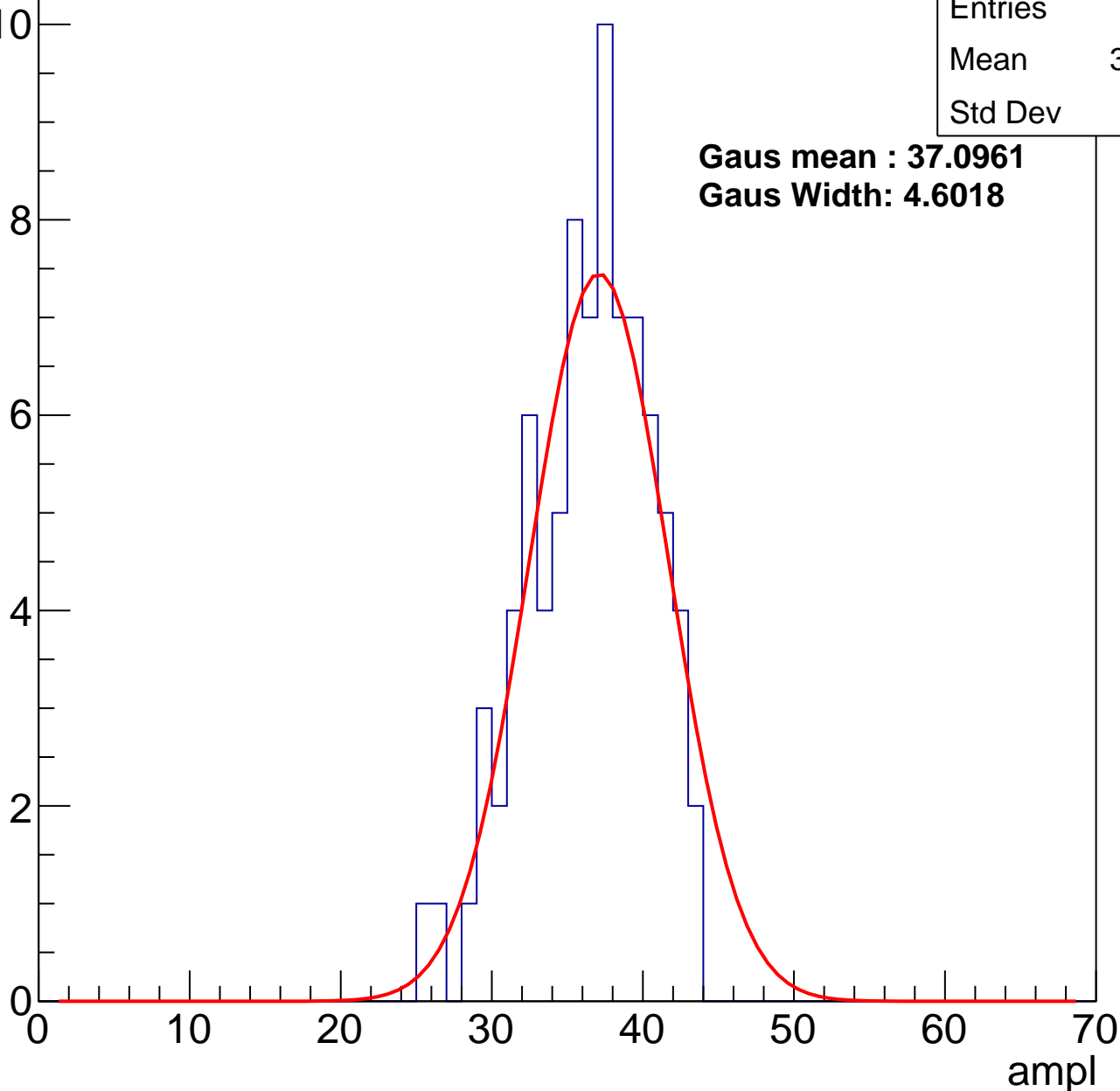
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	35.95
Std Dev	4

**Gaus mean : 37.0961**

**Gaus Width: 4.6018**



# B1L003S, U23-ch9, adc2

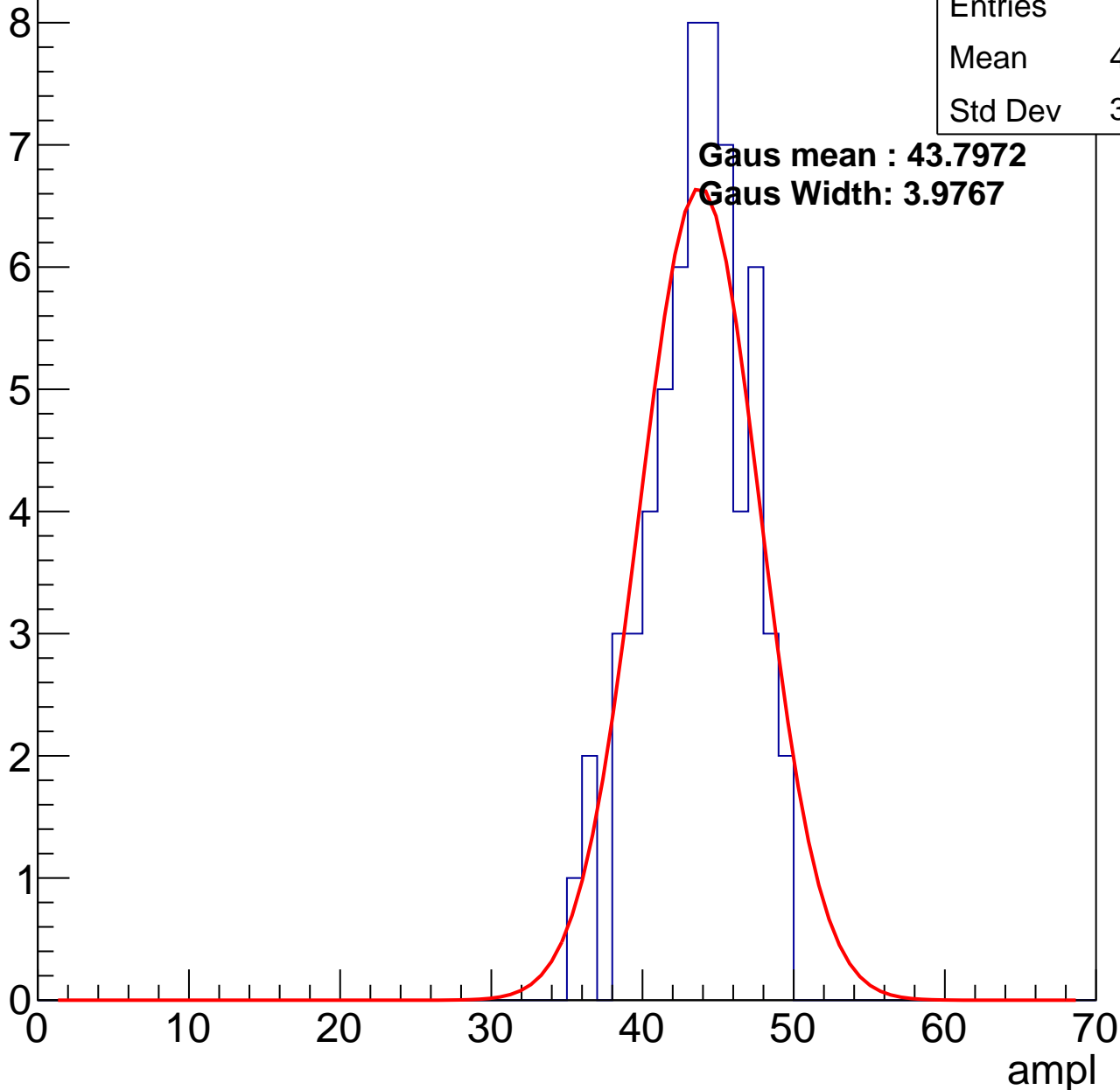
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.13
Std Dev	3.265

**Gaus mean : 43.7972**

**Gaus Width: 3.9767**

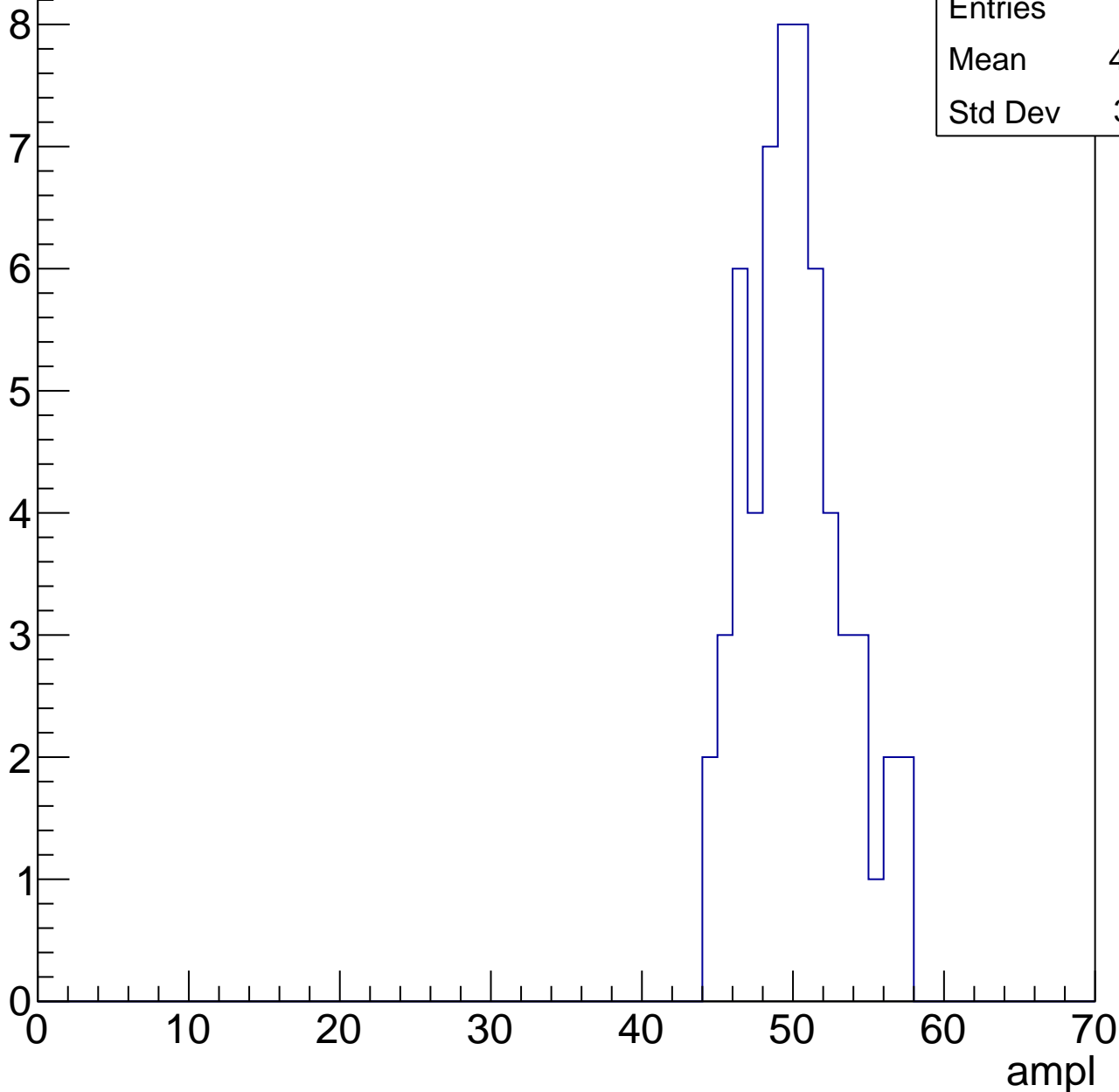


# B1L003S, U23-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	49.68
Std Dev	3.191

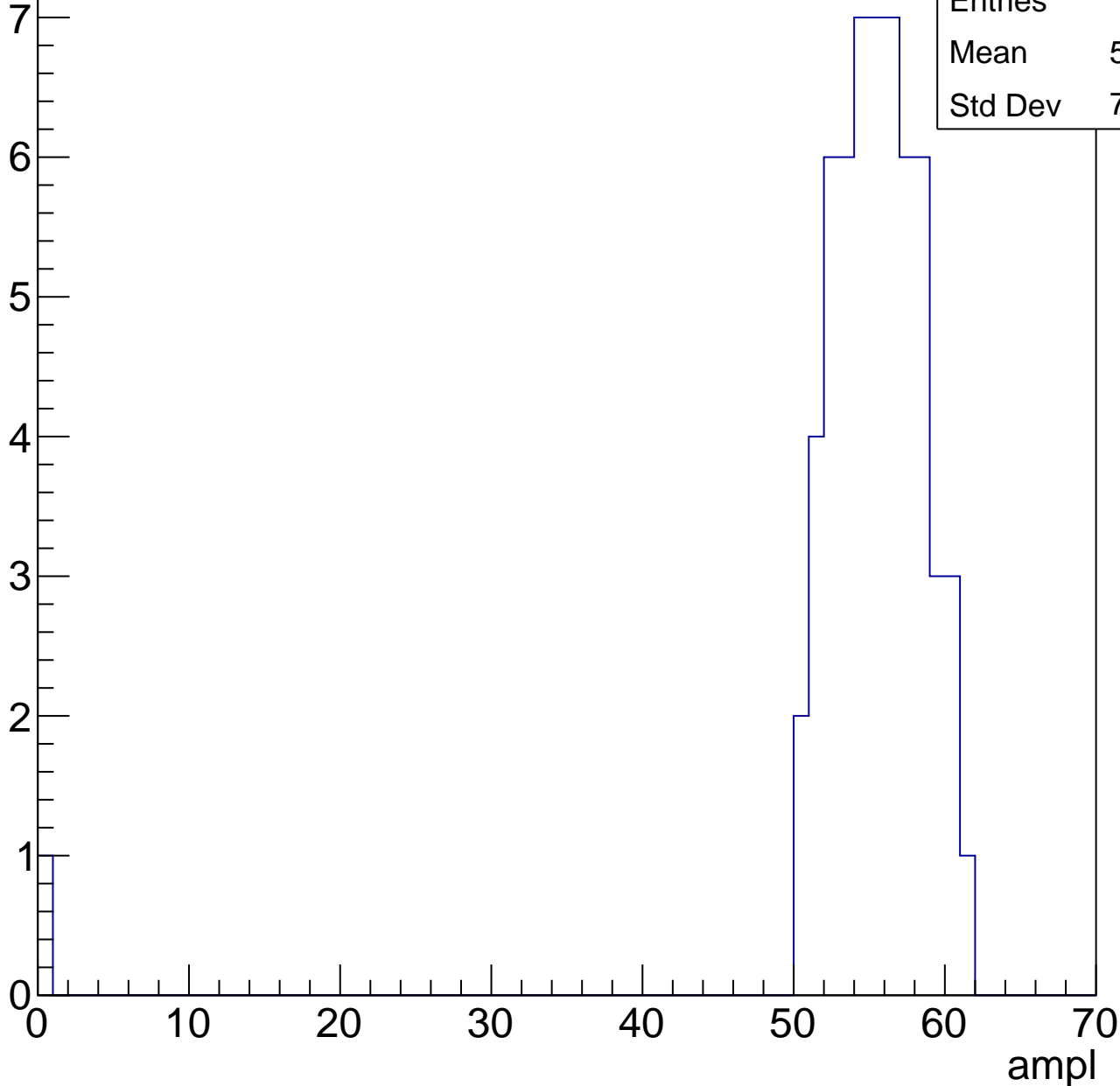


# B1L003S, U23-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.19
Std Dev	7.624

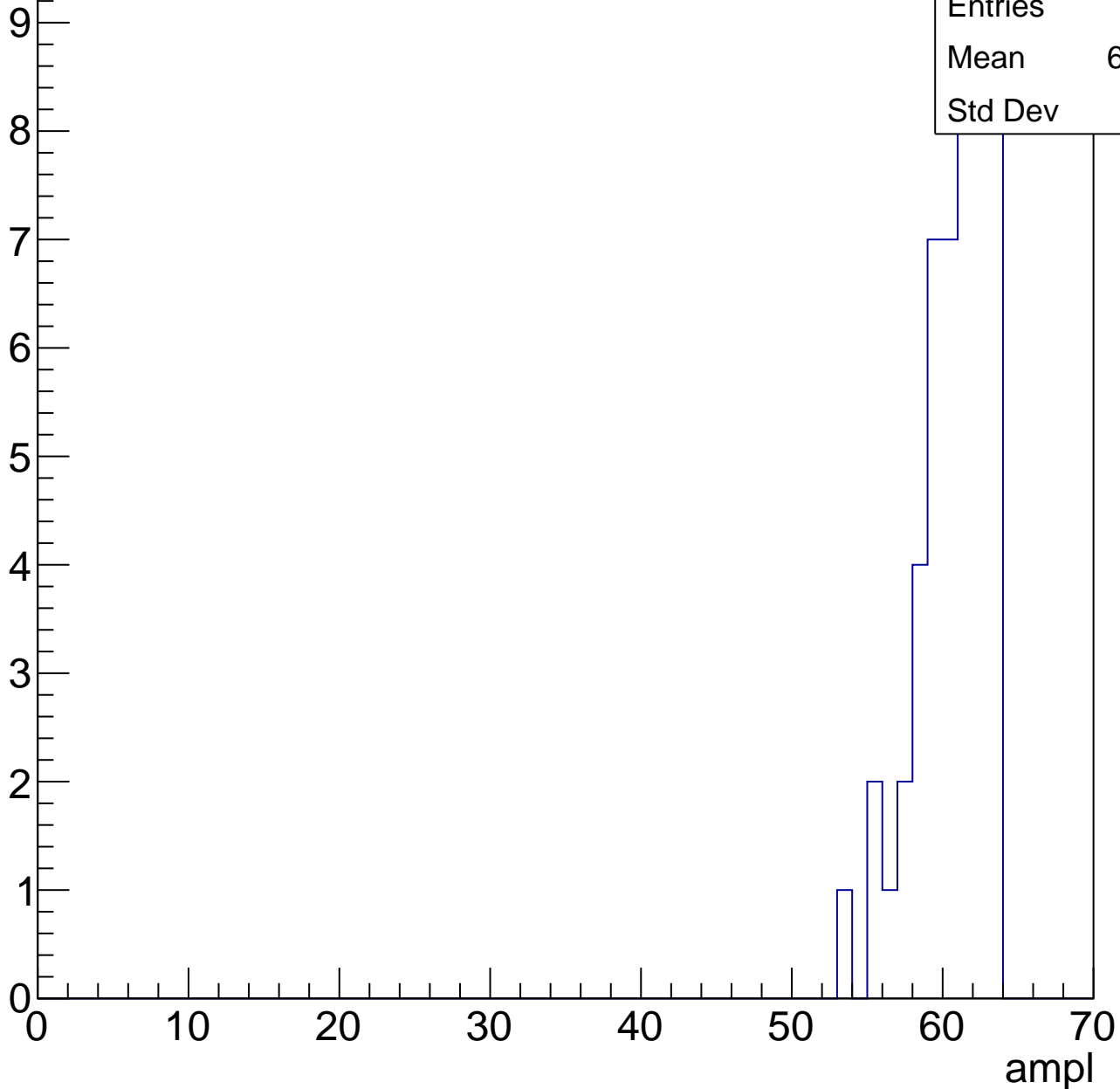


# B1L003S, U23-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	60.22
Std Dev	2.36



# B1L003S, U23-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B1L003S, U23-ch10, adc0

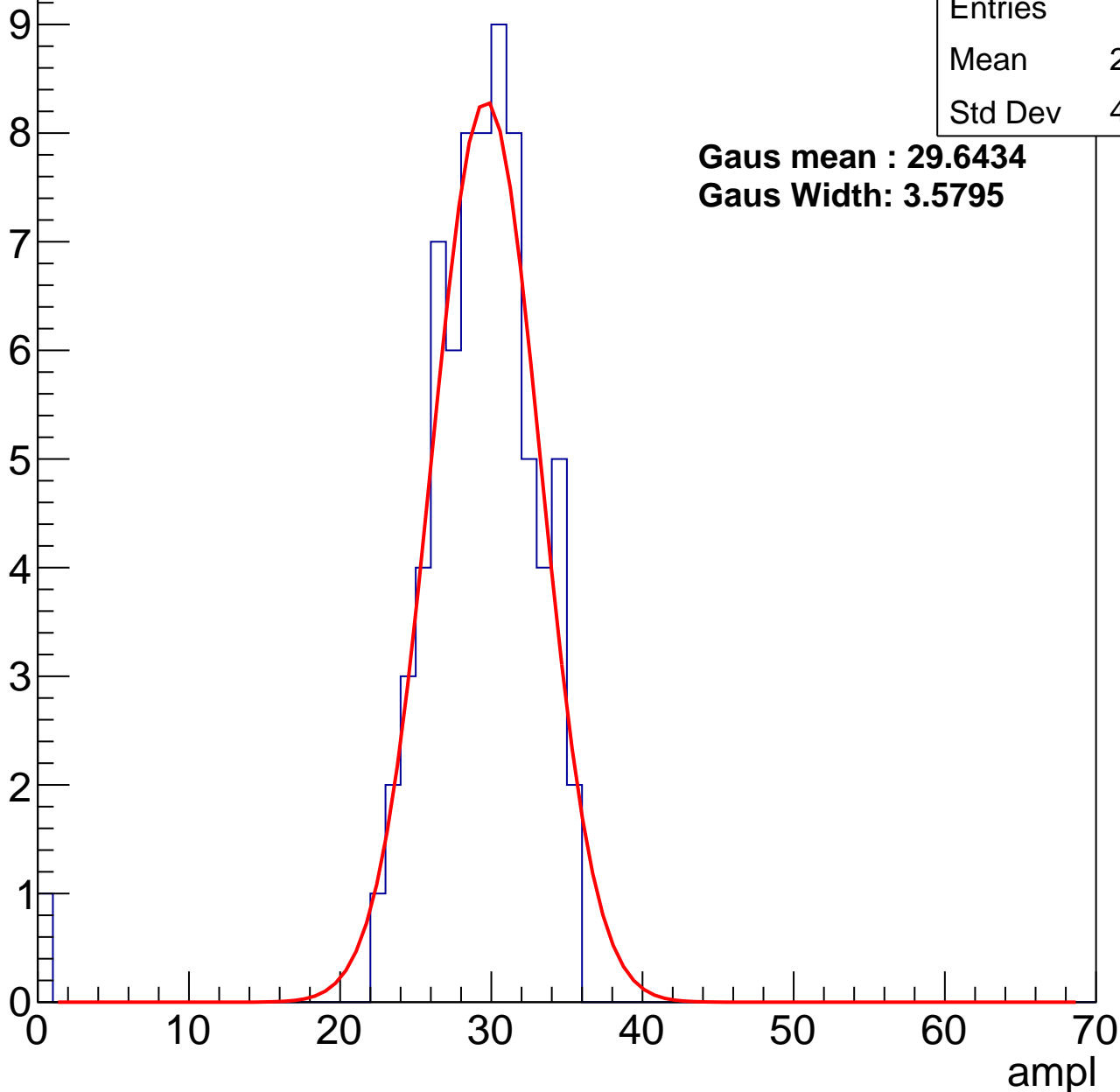
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	28.63
Std Dev	4.583

**Gaus mean : 29.6434**

**Gaus Width: 3.5795**



# B1L003S, U23-ch10, adc1

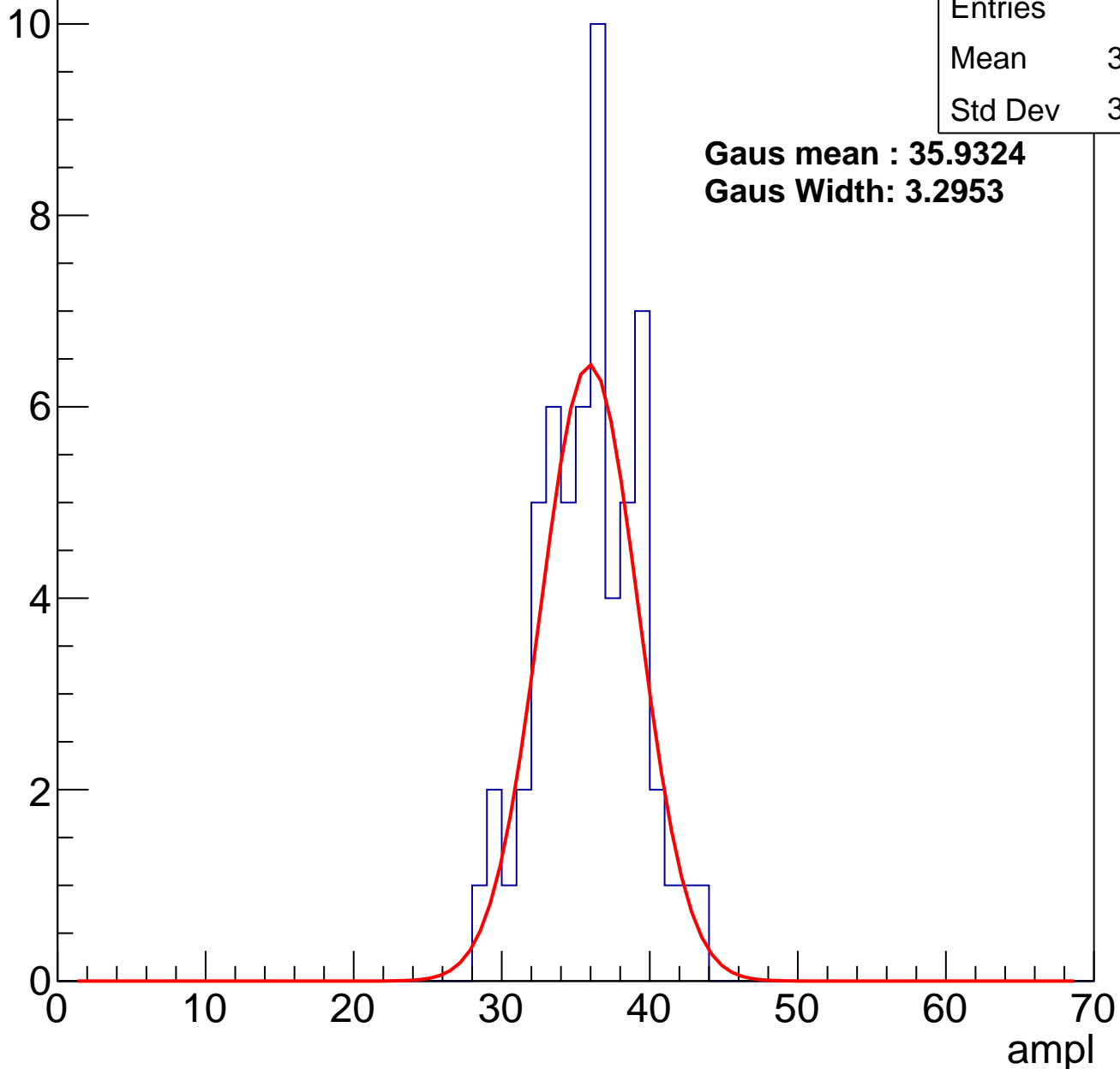
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	59
Mean	35.47
Std Dev	3.244

**Gaus mean : 35.9324**

**Gaus Width: 3.2953**

Entry



# B1L003S, U23-ch10, adc2

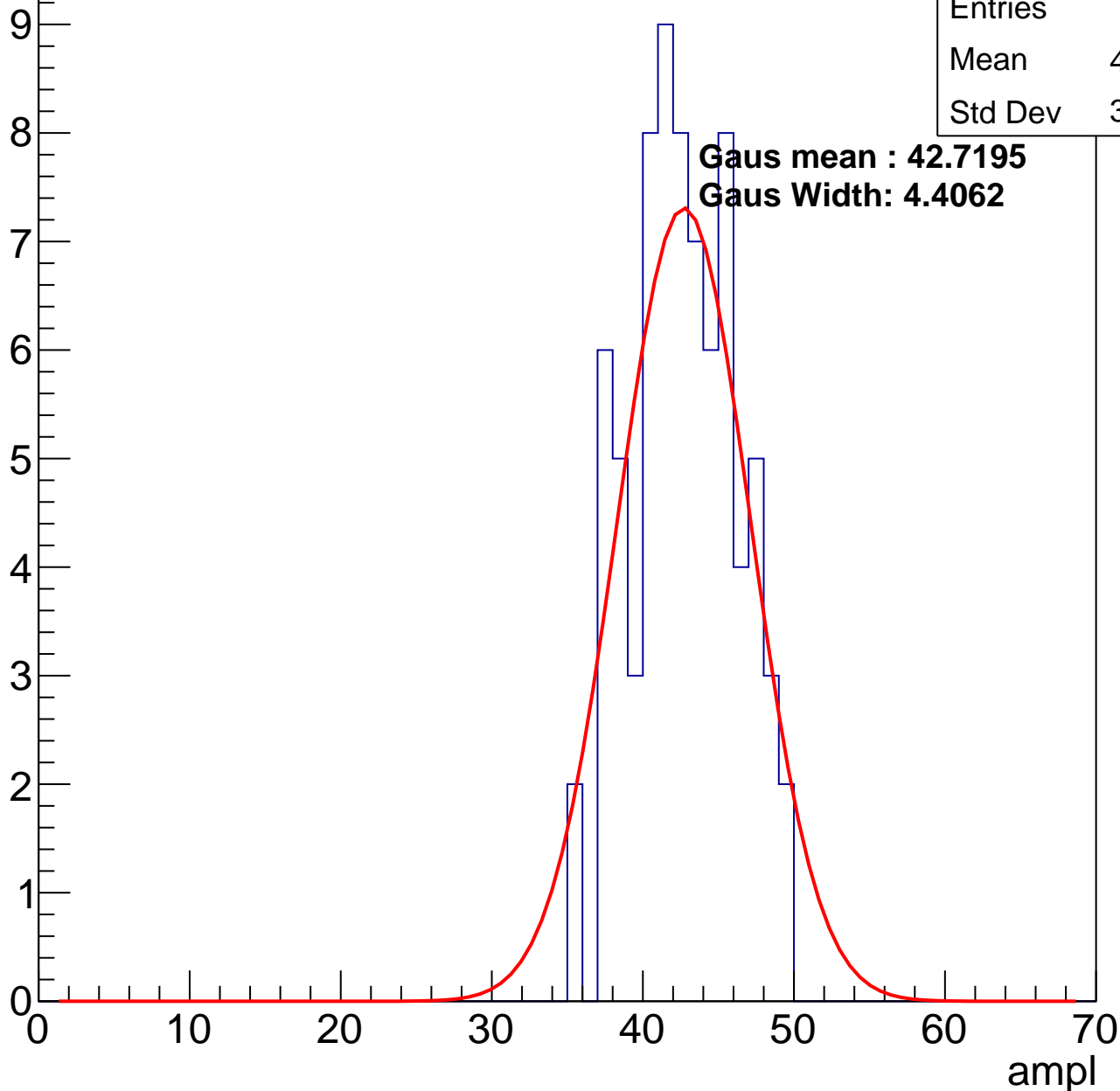
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	42.24
Std Dev	3.422

**Gaus mean : 42.7195**

**Gaus Width: 4.4062**

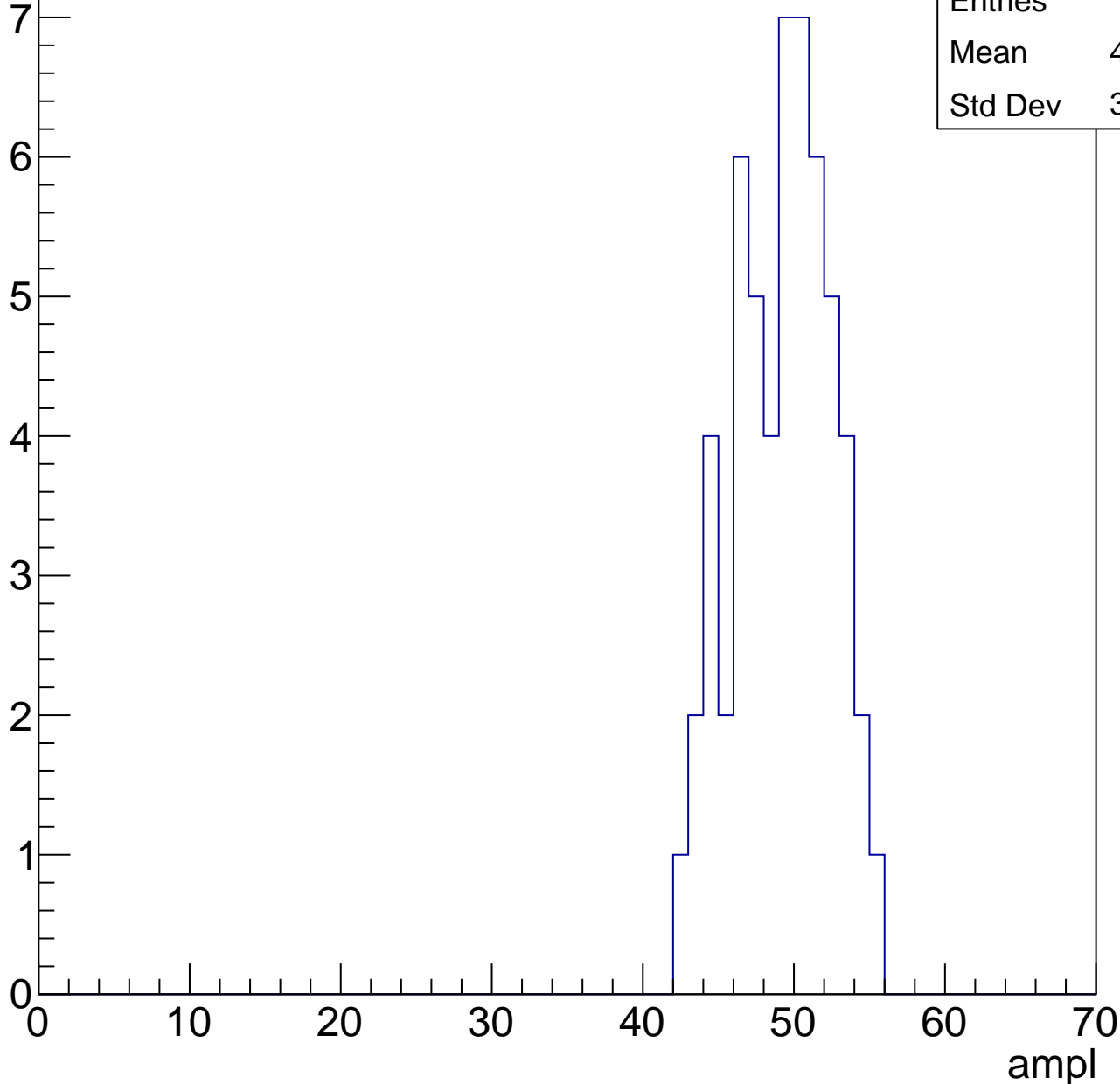


# B1L003S, U23-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

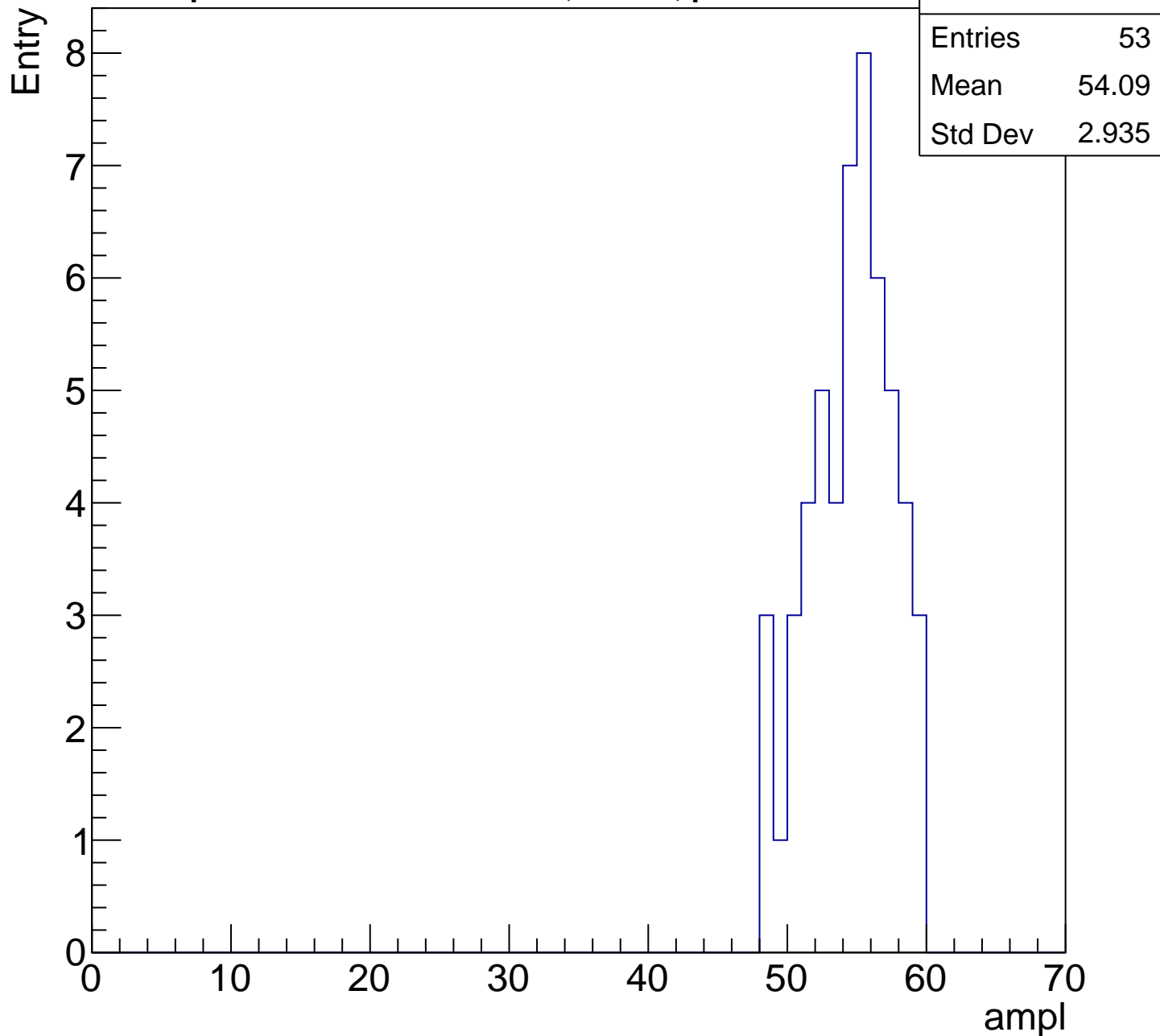
Entry

Entries	56
Mean	48.77
Std Dev	3.145



# B1L003S, U23-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

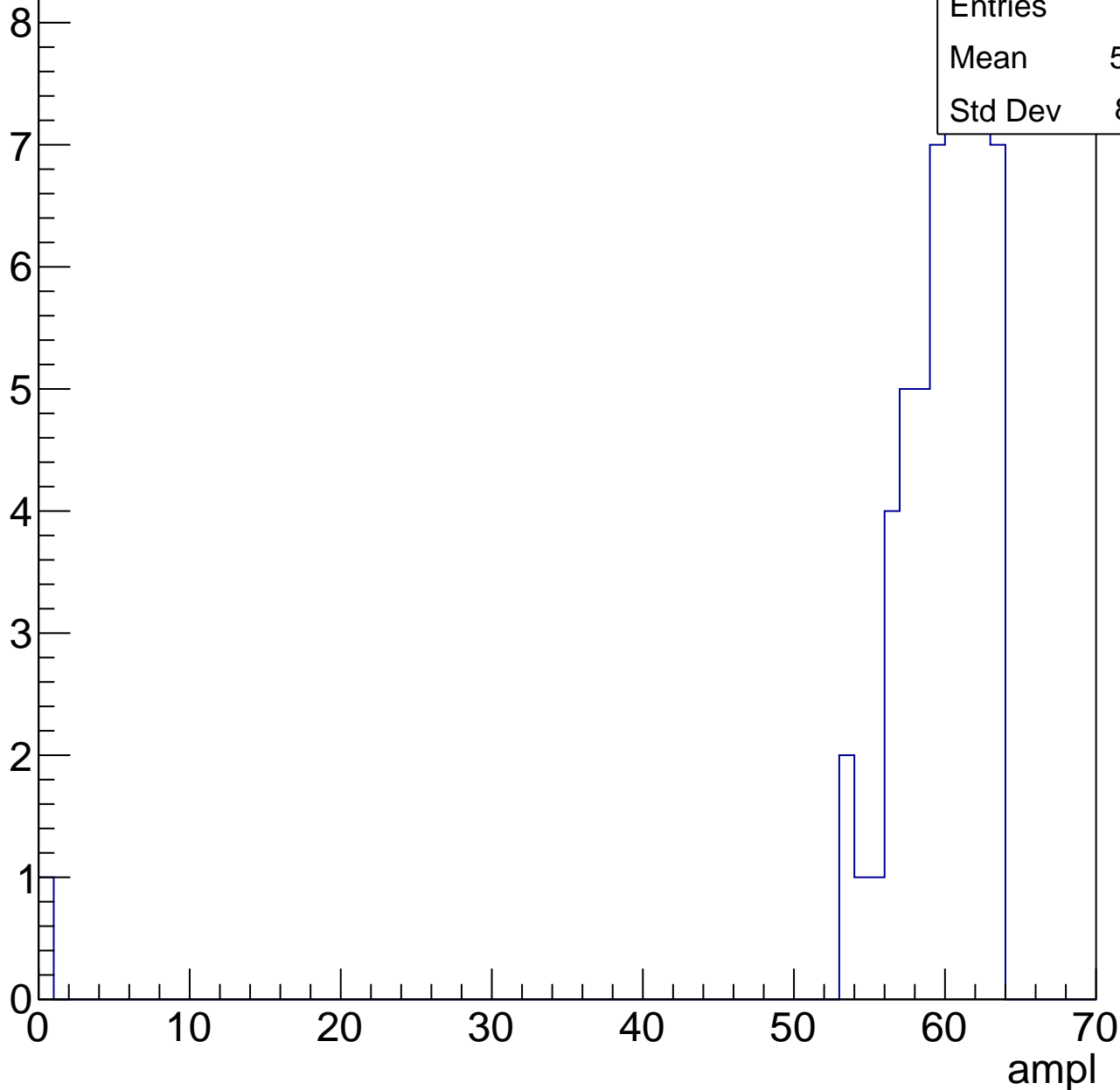


# B1L003S, U23-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

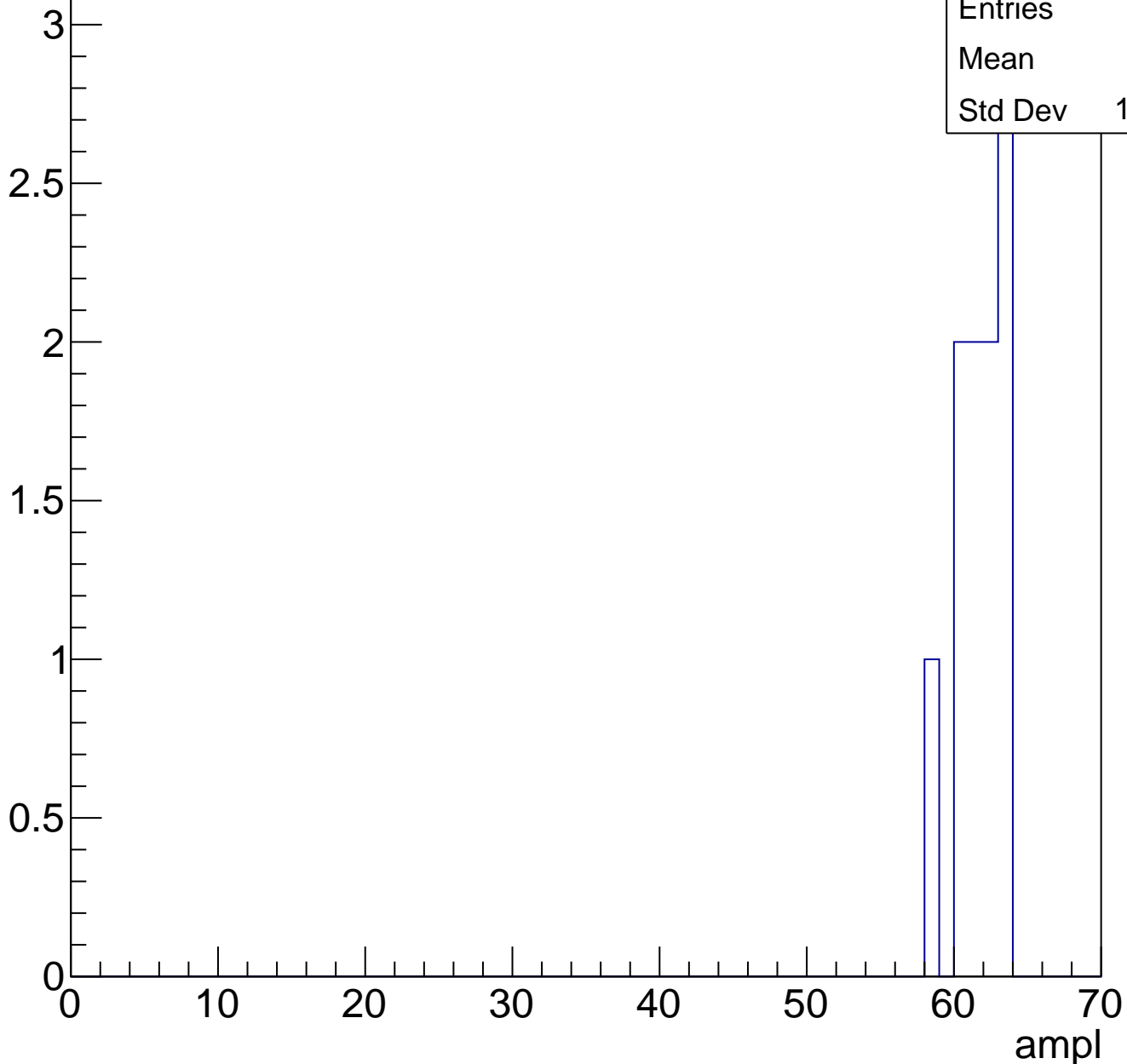
Entries	57
Mean	58.46
Std Dev	8.231



# B1L003S, U23-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

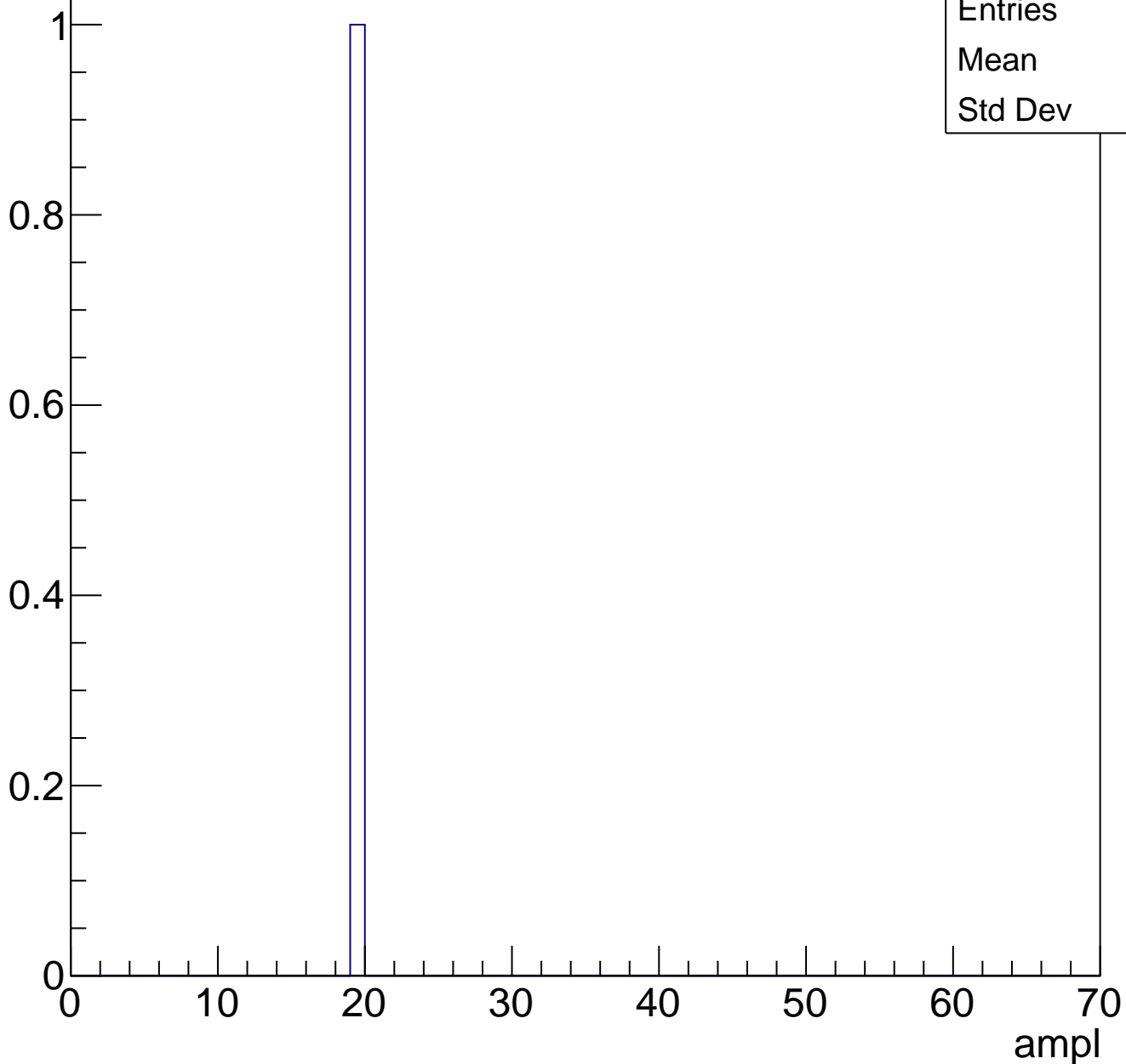




# B1L003S, U23-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	19
Std Dev	0

# B1L003S, U23-ch11, adc0

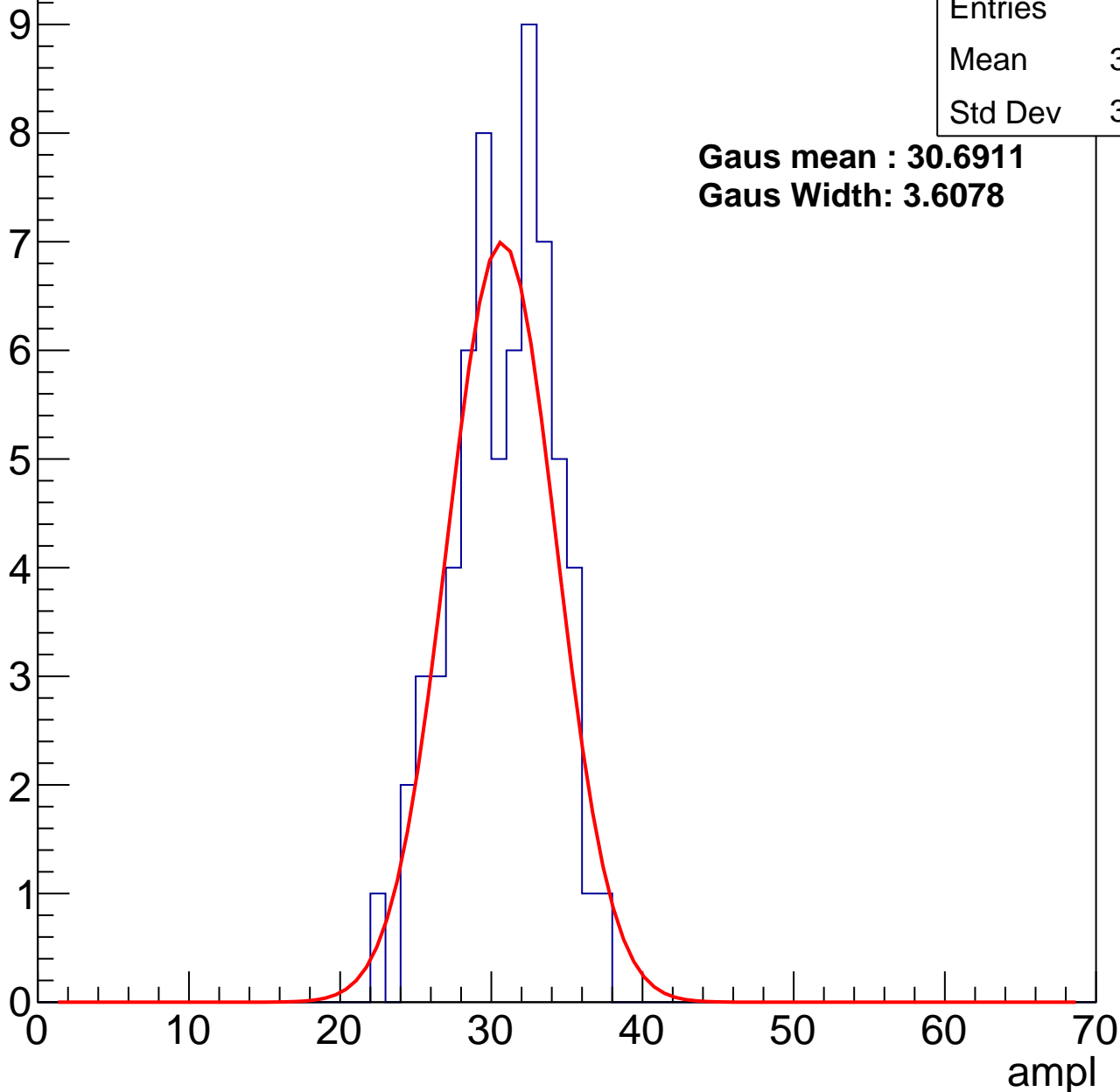
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.29
Std Dev	3.262

**Gaus mean : 30.6911**

**Gaus Width: 3.6078**

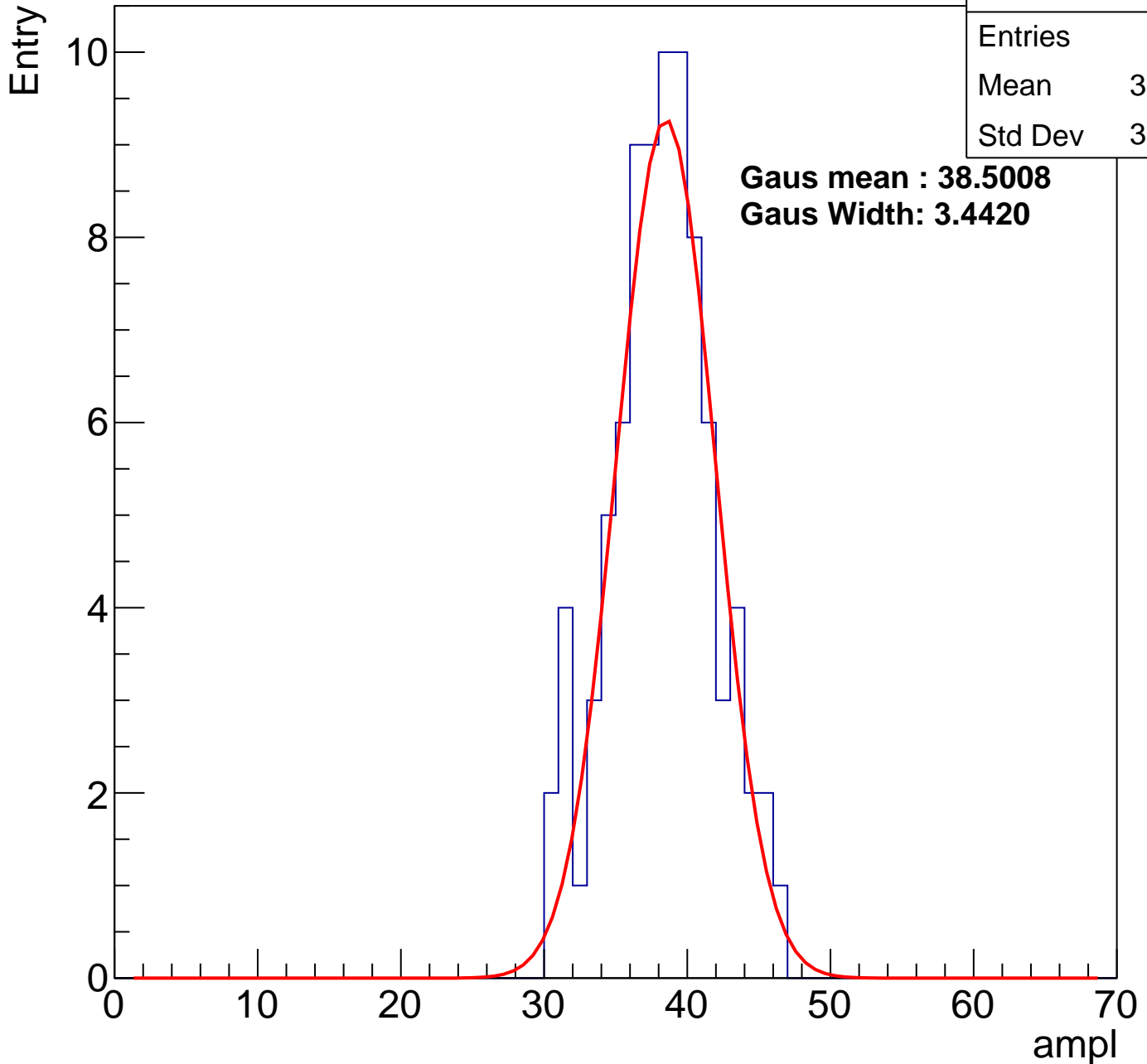


# B1L003S, U23-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	85
Mean	37.76
Std Dev	3.577

**Gaus mean : 38.5008**  
**Gaus Width: 3.4420**



# B1L003S, U23-ch11, adc2

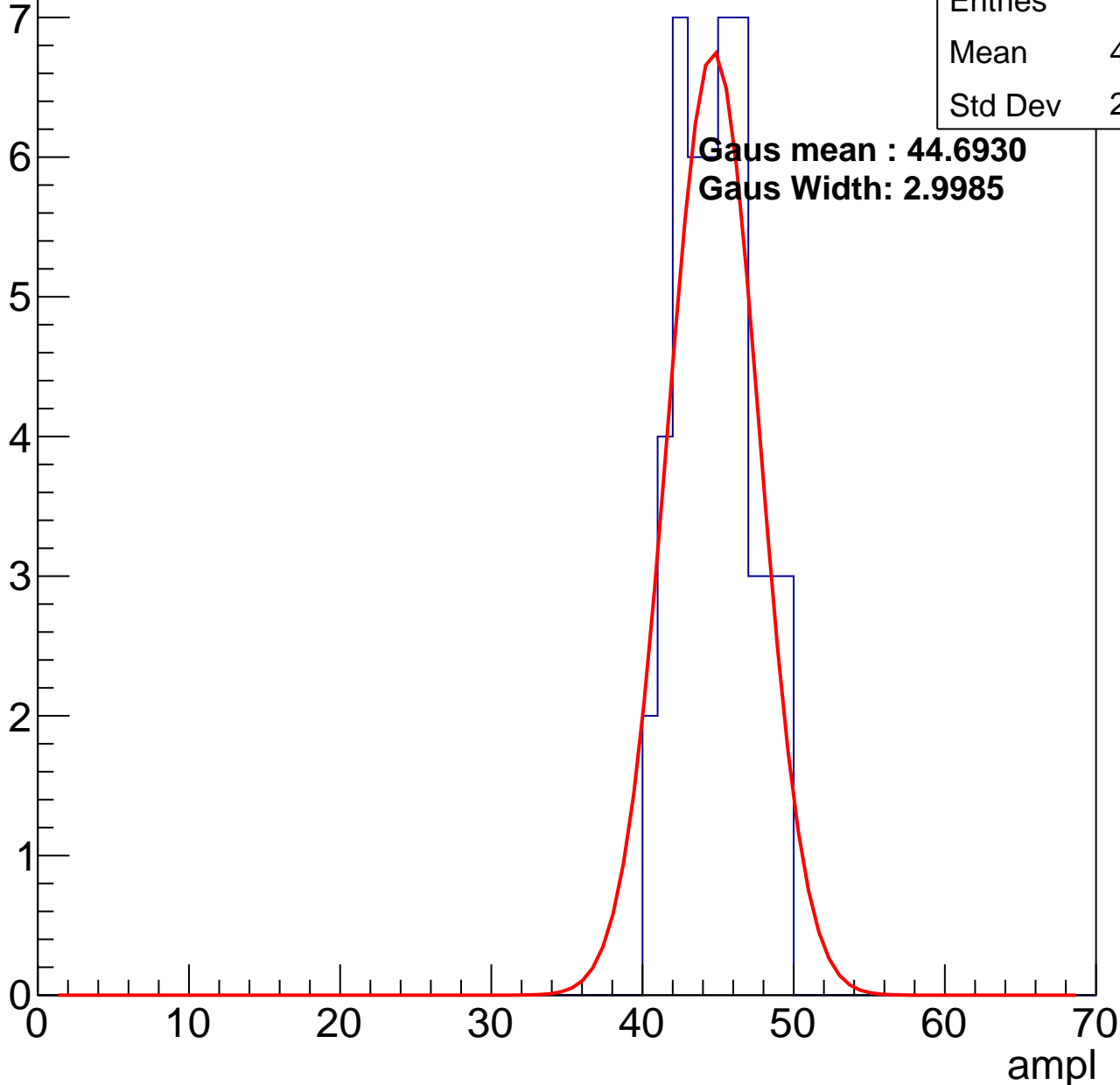
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	44.35
Std Dev	2.419

**Gaus mean : 44.6930**

**Gaus Width: 2.9985**



# B1L003S, U23-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

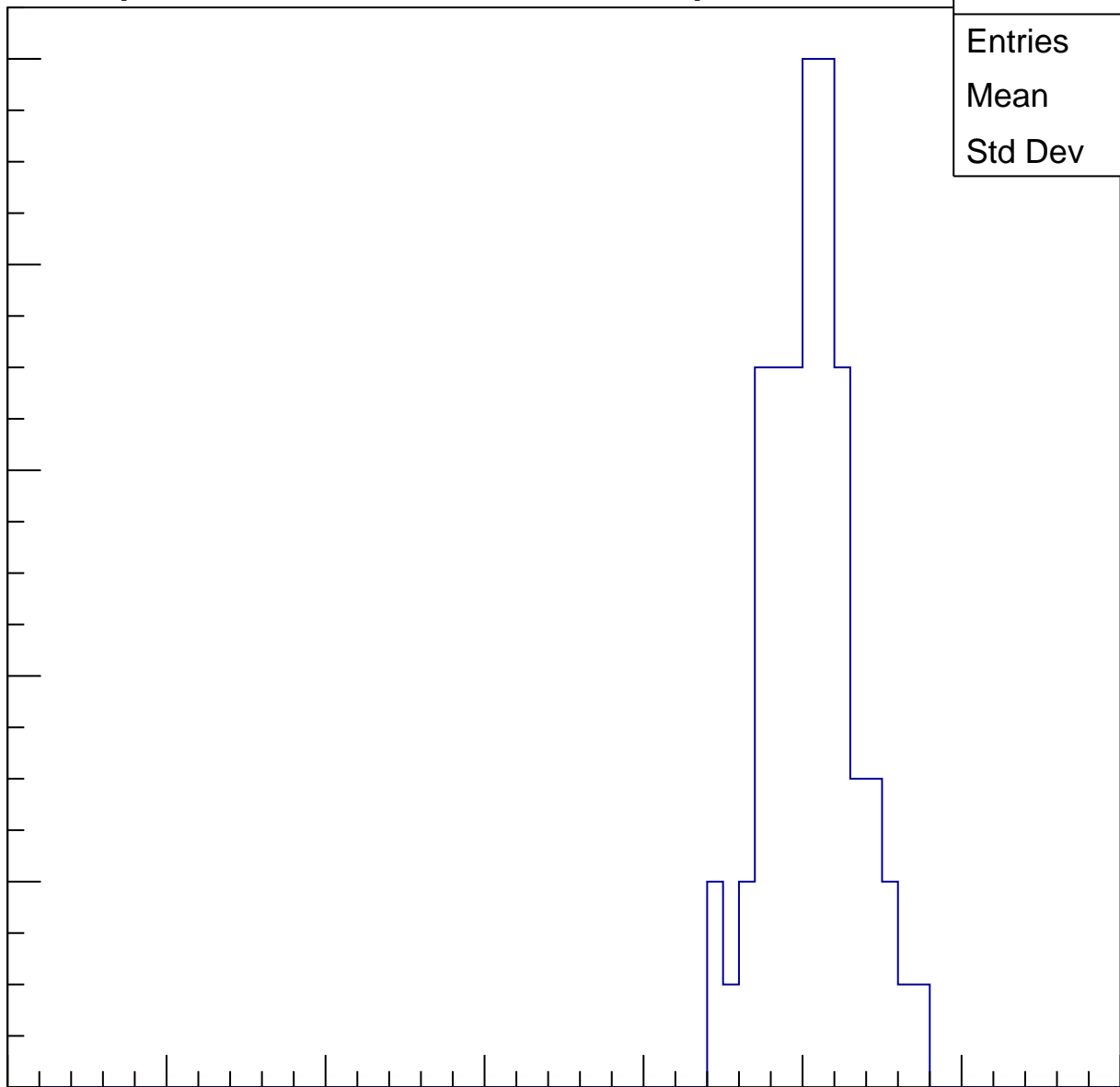
Entries	63
Mean	50.02
Std Dev	2.746

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

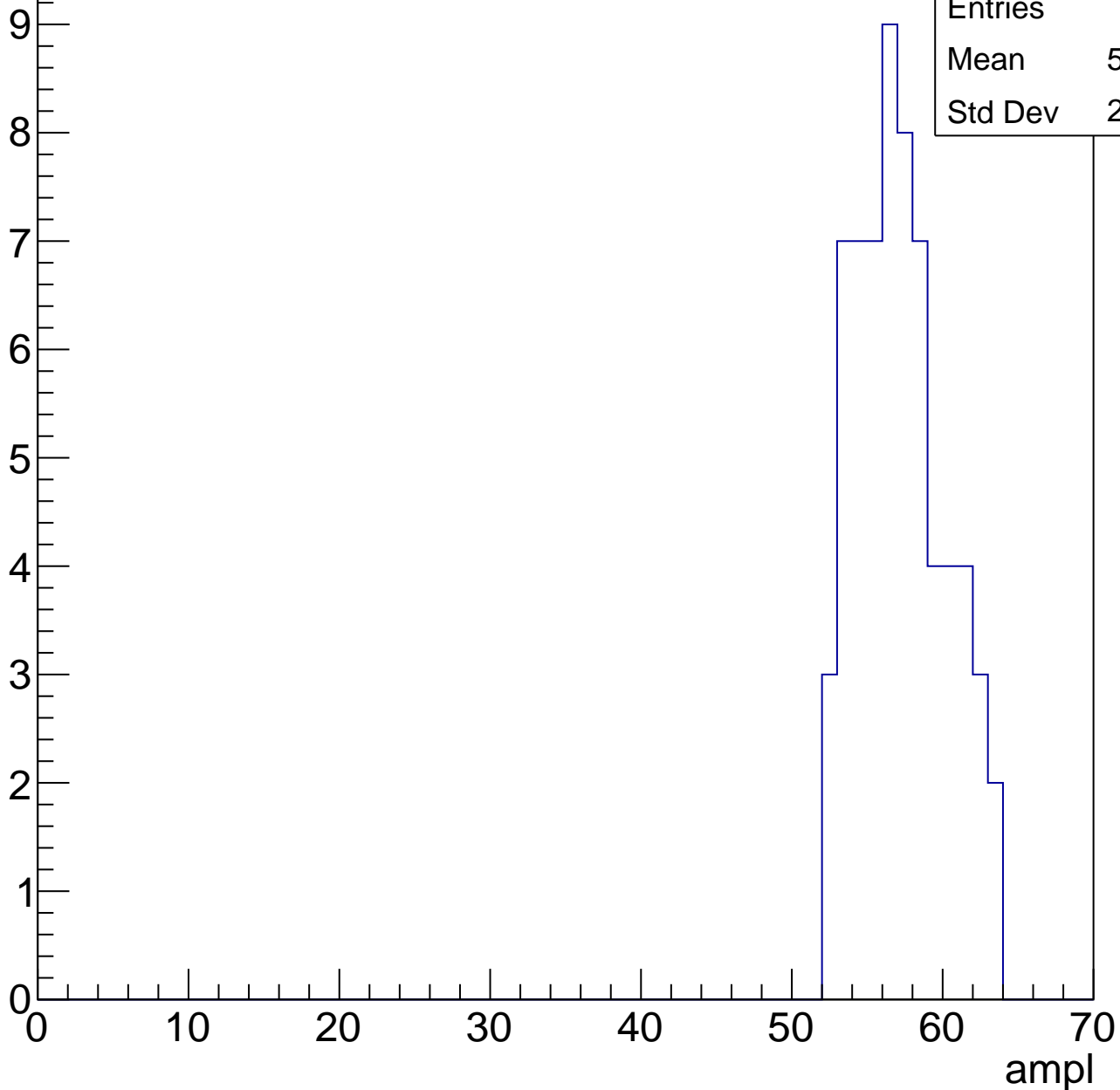
ampl



# B1L003S, U23-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



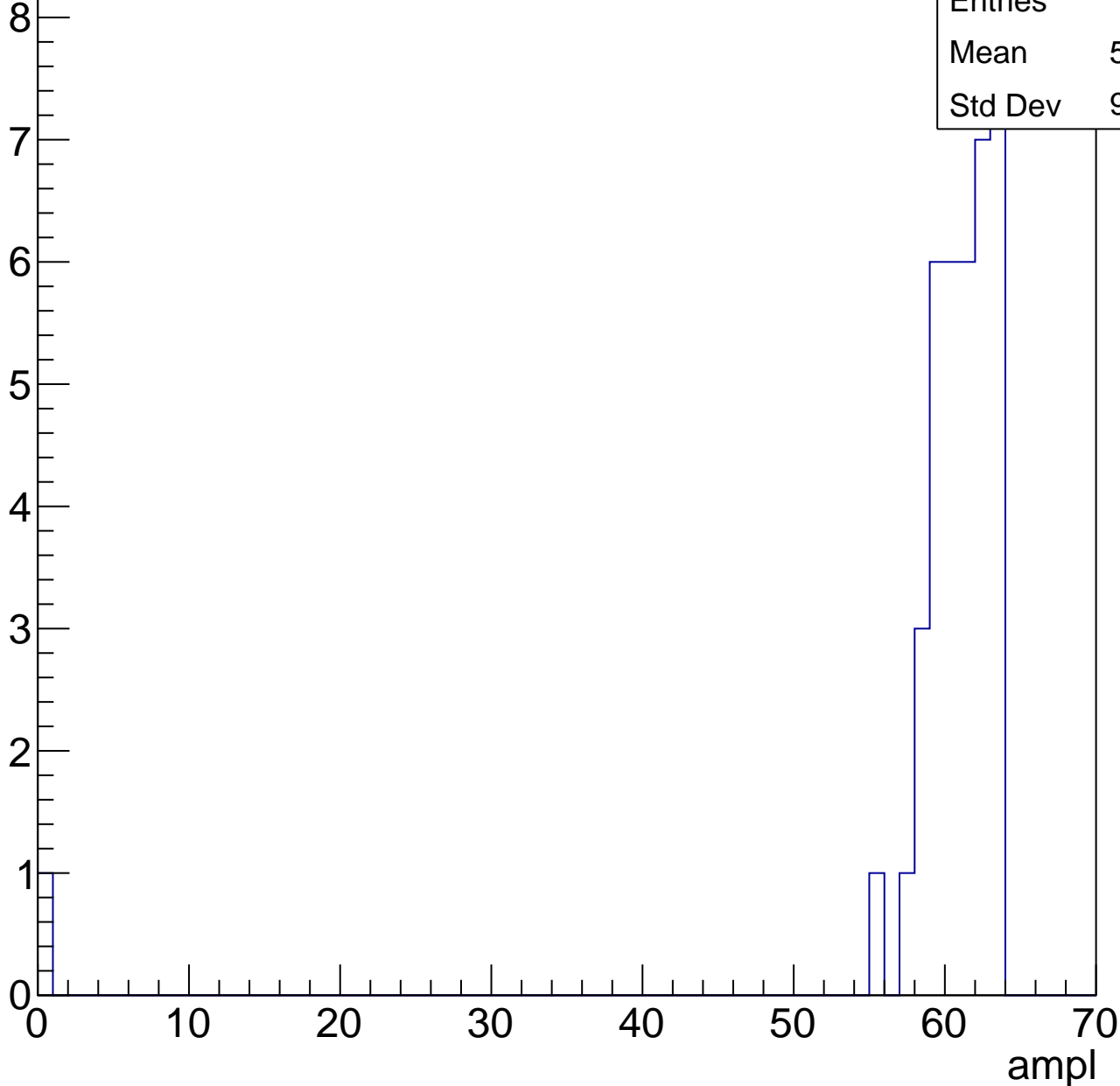
Entries	65
Mean	56.74
Std Dev	2.916

# B1L003S, U23-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	59.08
Std Dev	9.773



# B1L003S, U23-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0



# B1L003S, U23-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch12, adc0

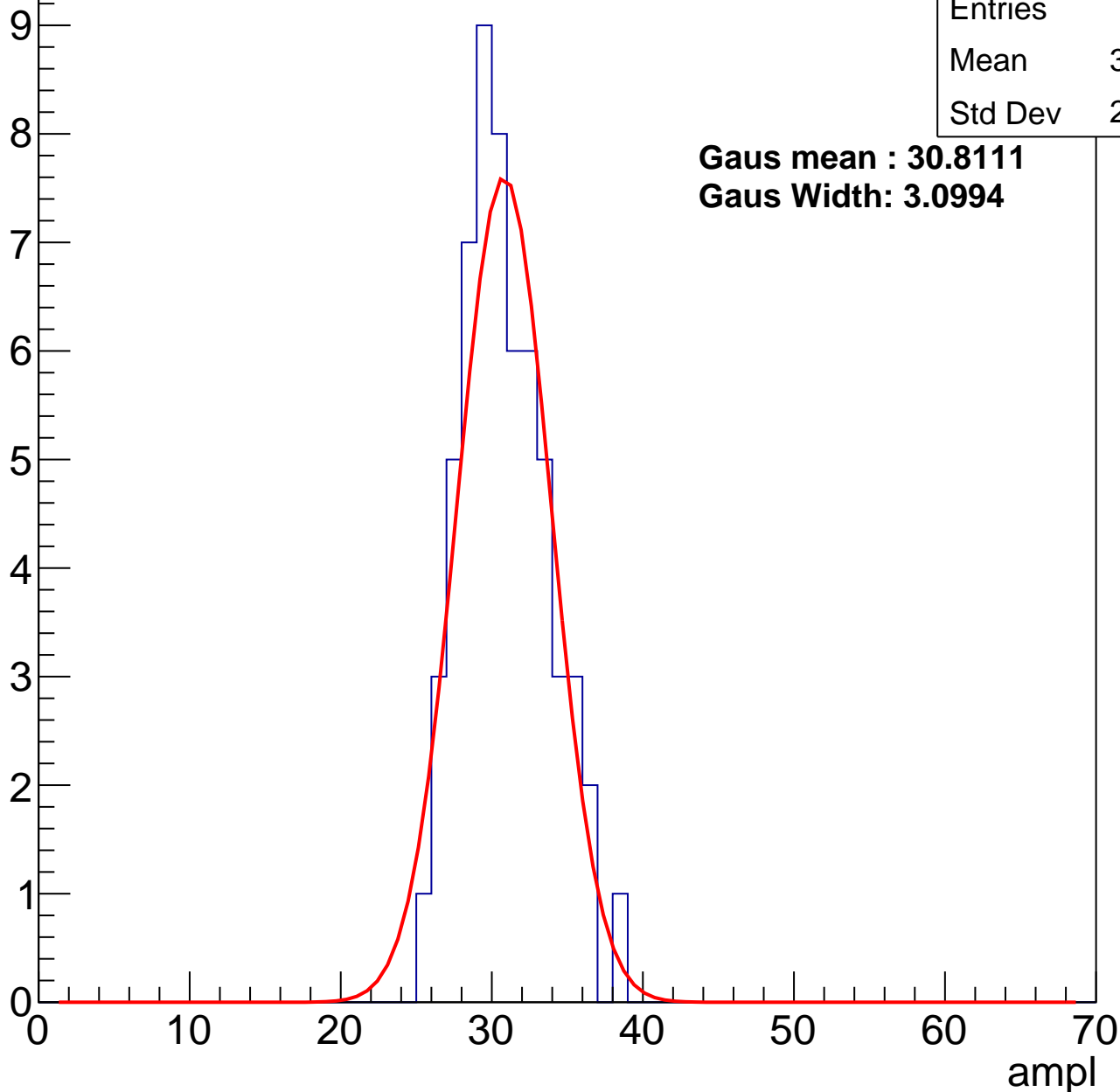
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	30.42
Std Dev	2.848

**Gaus mean : 30.8111**

**Gaus Width: 3.0994**



# B1L003S, U23-ch12, adc1

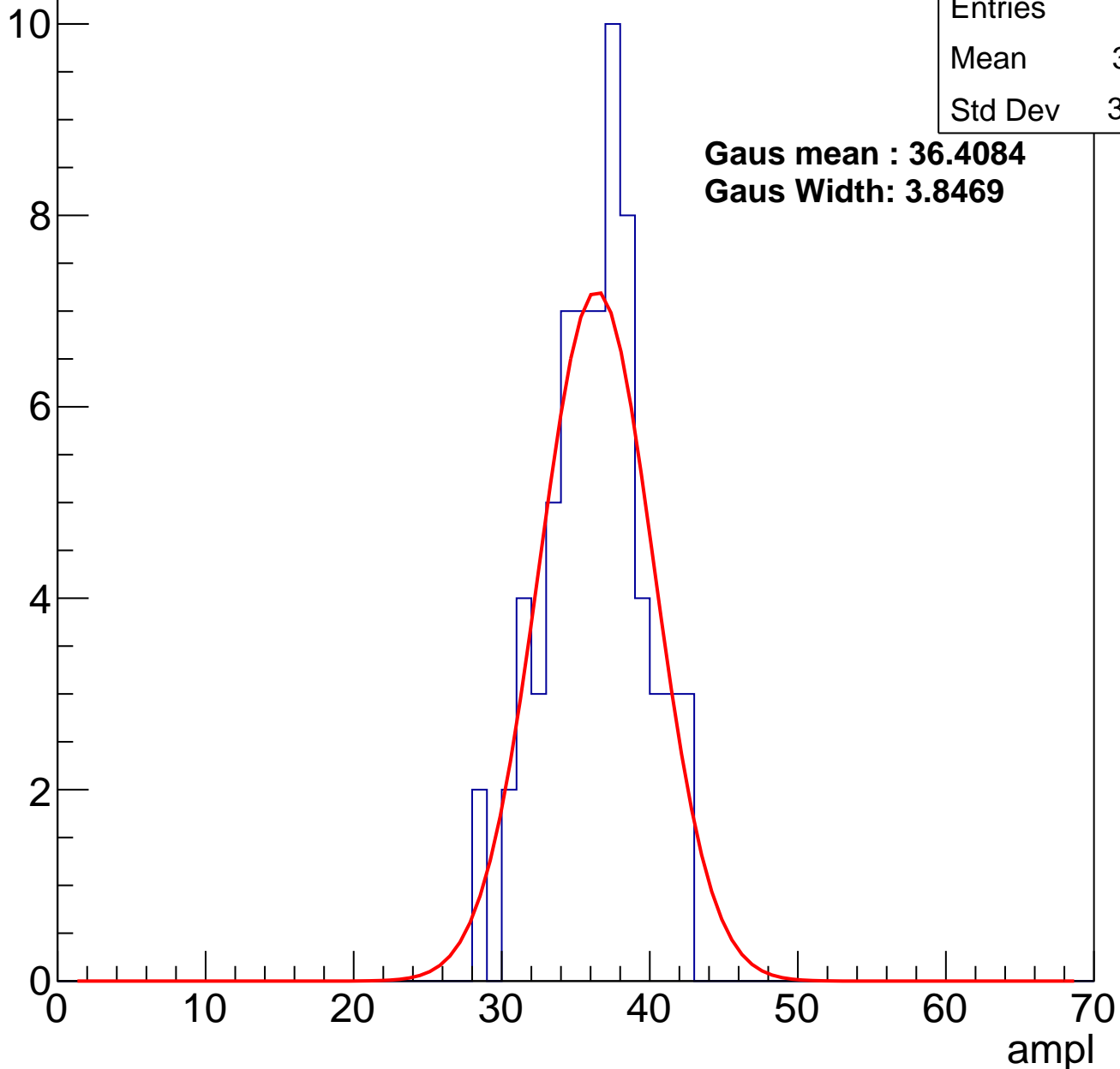
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	68
Mean	35.81
Std Dev	3.287

**Gaus mean : 36.4084**

**Gaus Width: 3.8469**

Entry



# B1L003S, U23-ch12, adc2

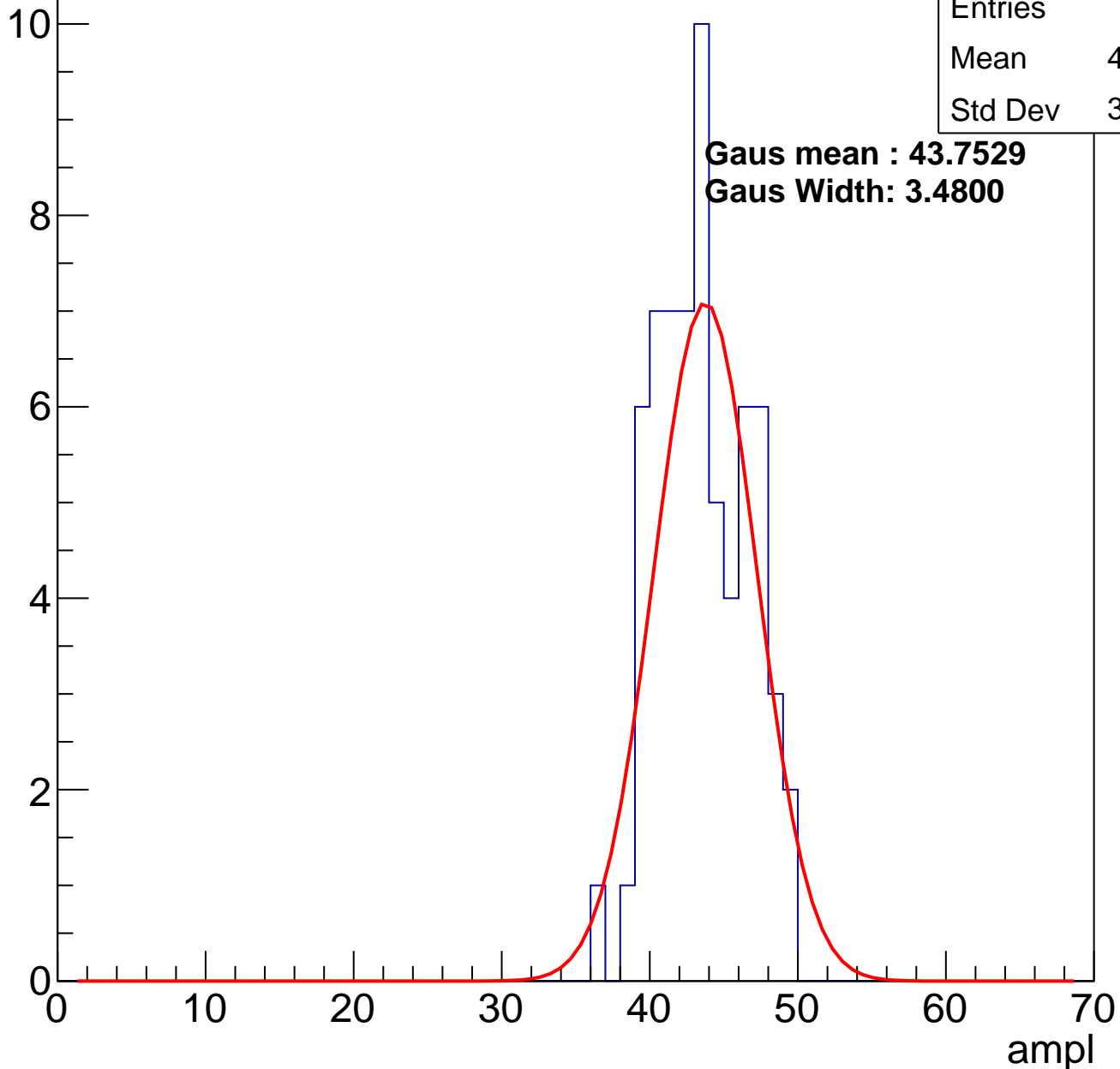
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	65
Mean	43.06
Std Dev	3.002

**Gaus mean : 43.7529**

**Gaus Width: 3.4800**

Entry

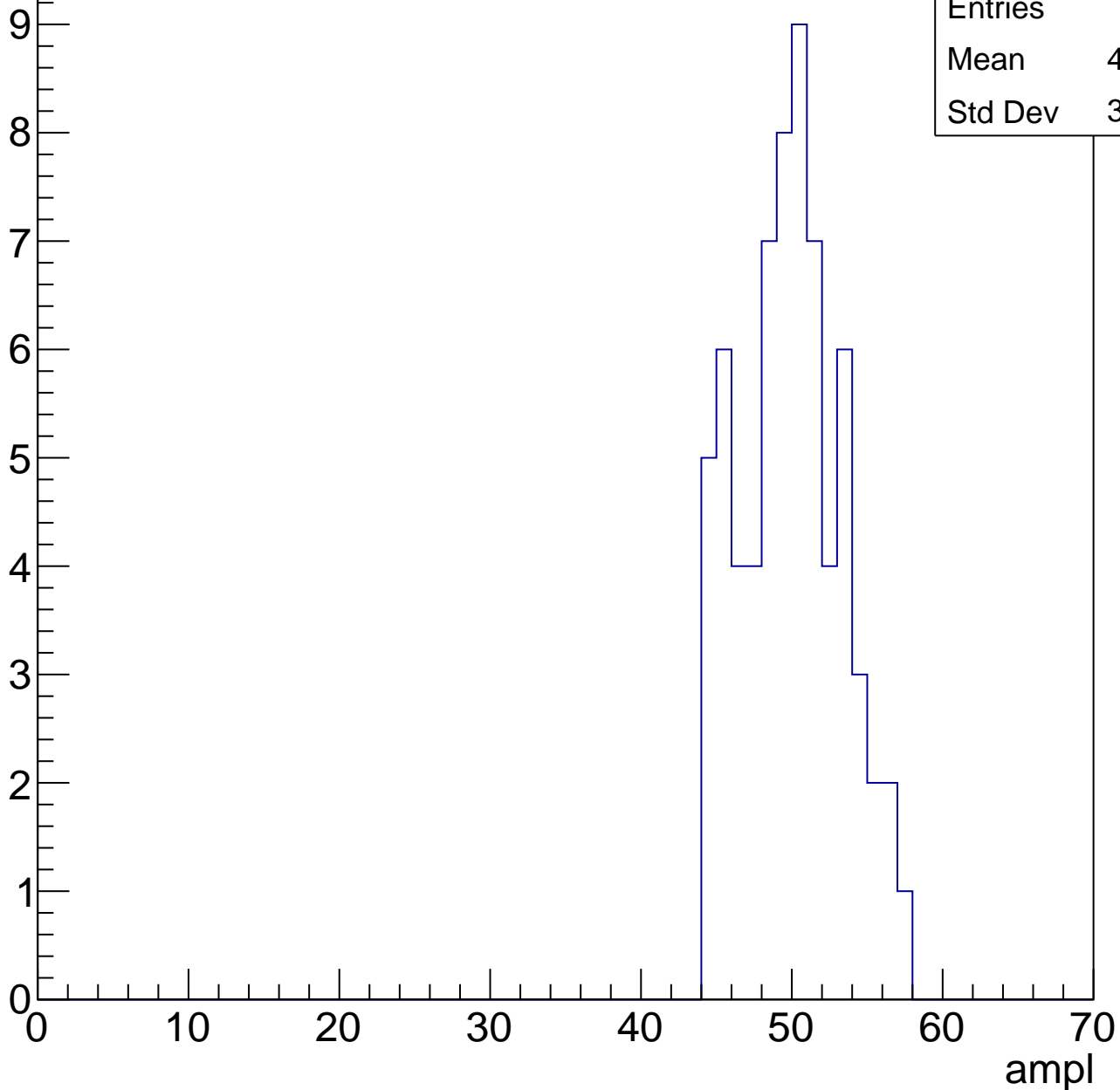


# B1L003S, U23-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.47
Std Dev	3.305

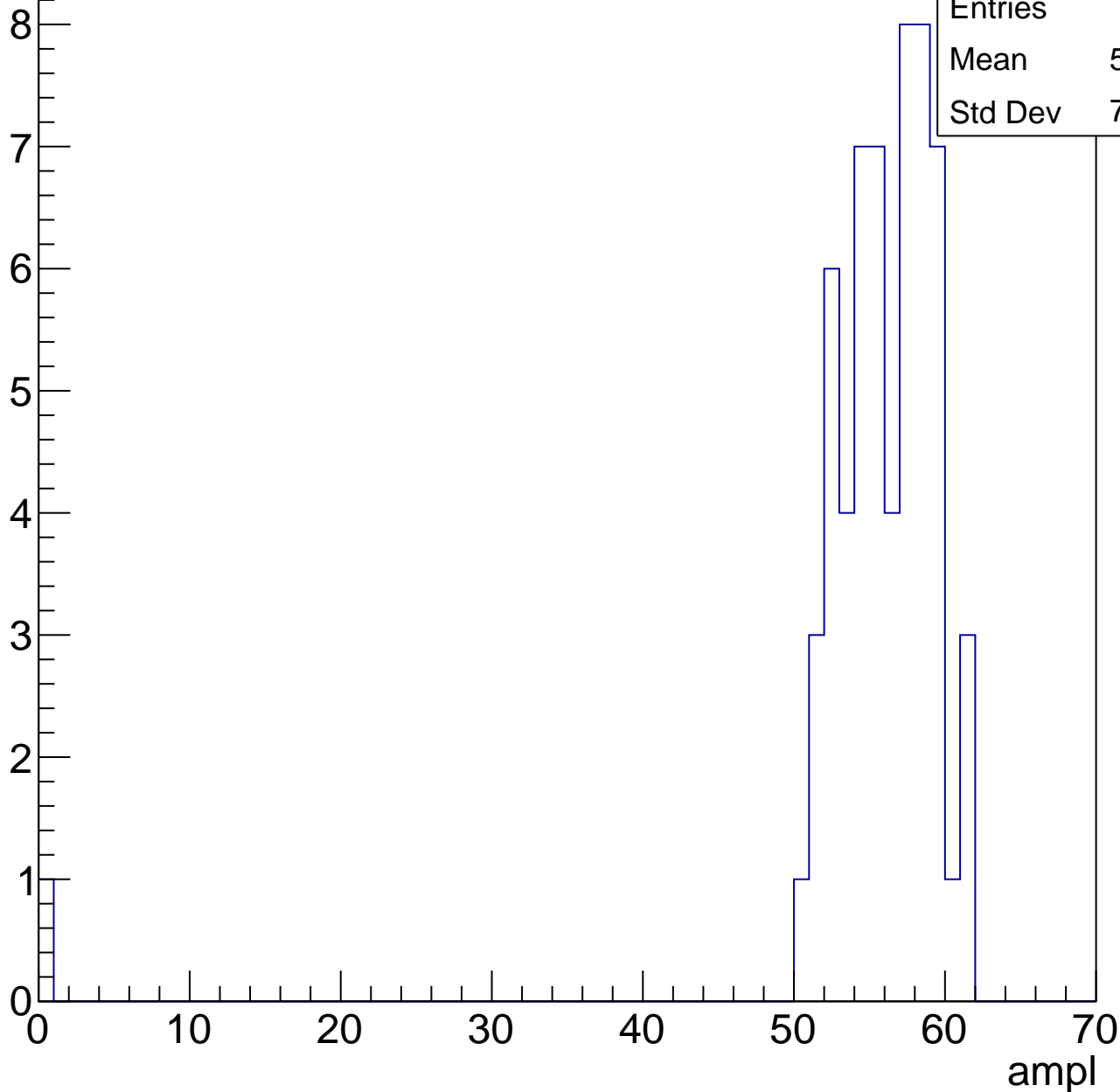


# B1L003S, U23-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.83
Std Dev	7.666

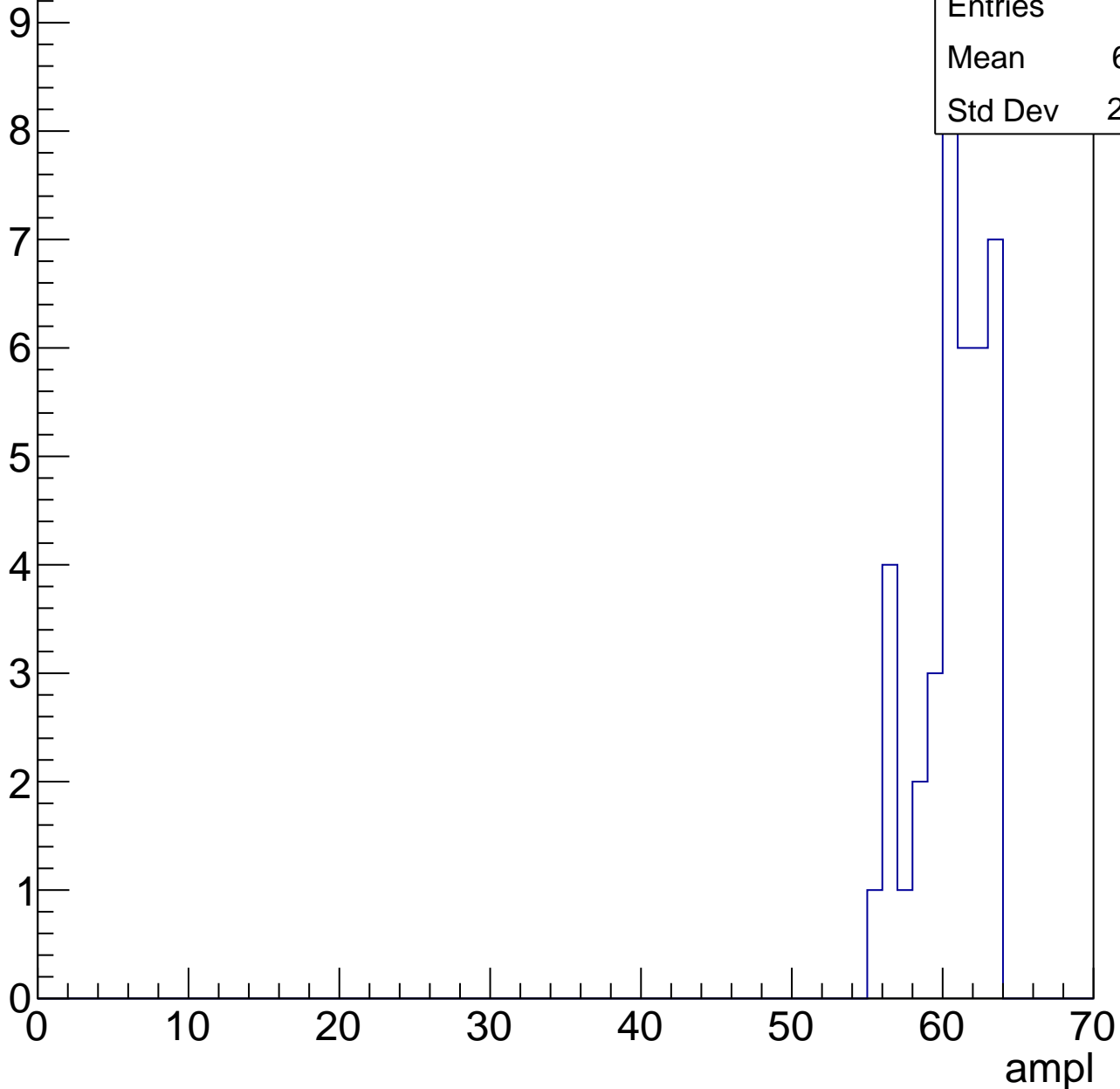


# B1L003S, U23-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

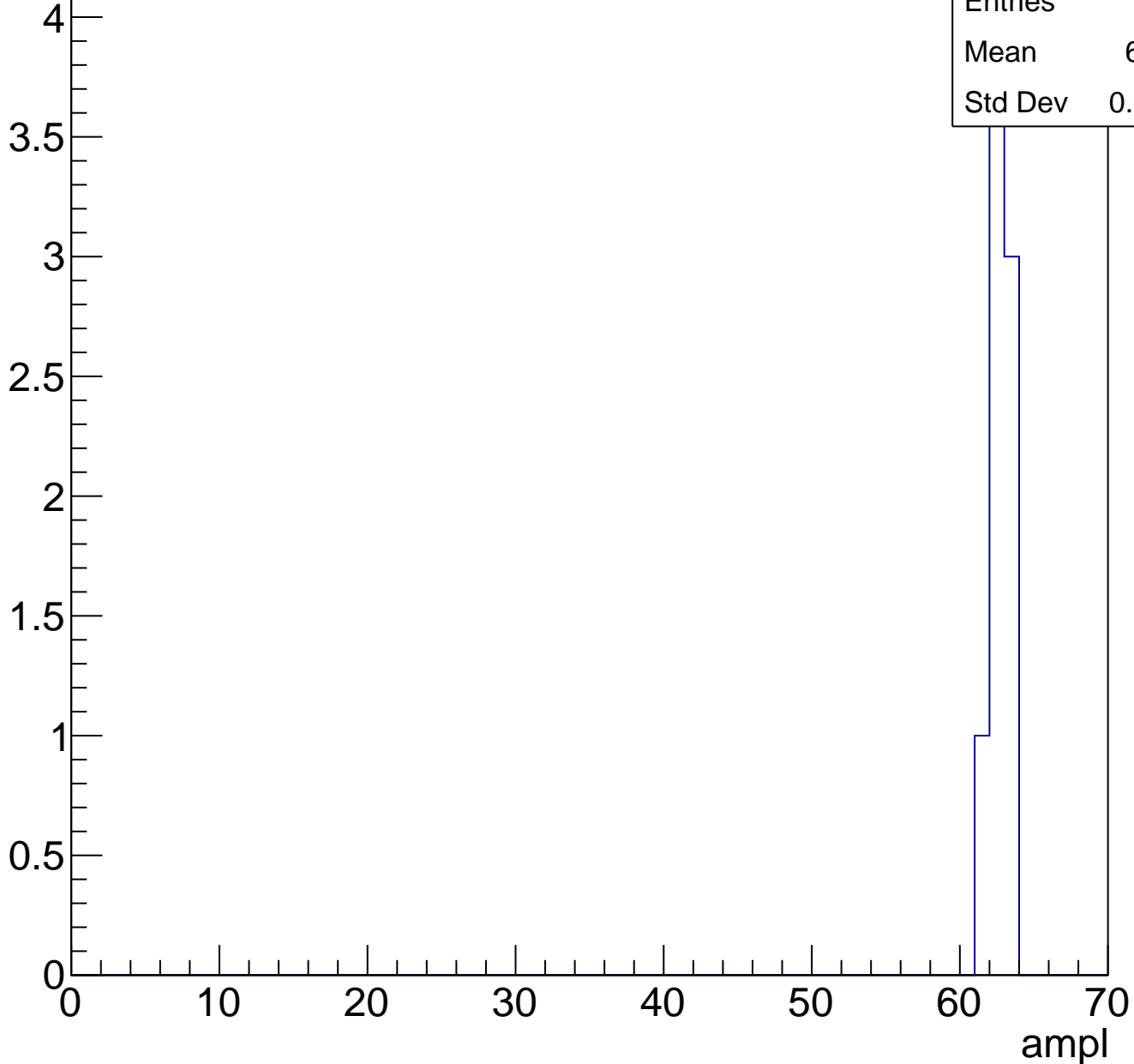
Entries	39
Mean	60.21
Std Dev	2.267



# B1L003S, U23-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch13, adc0

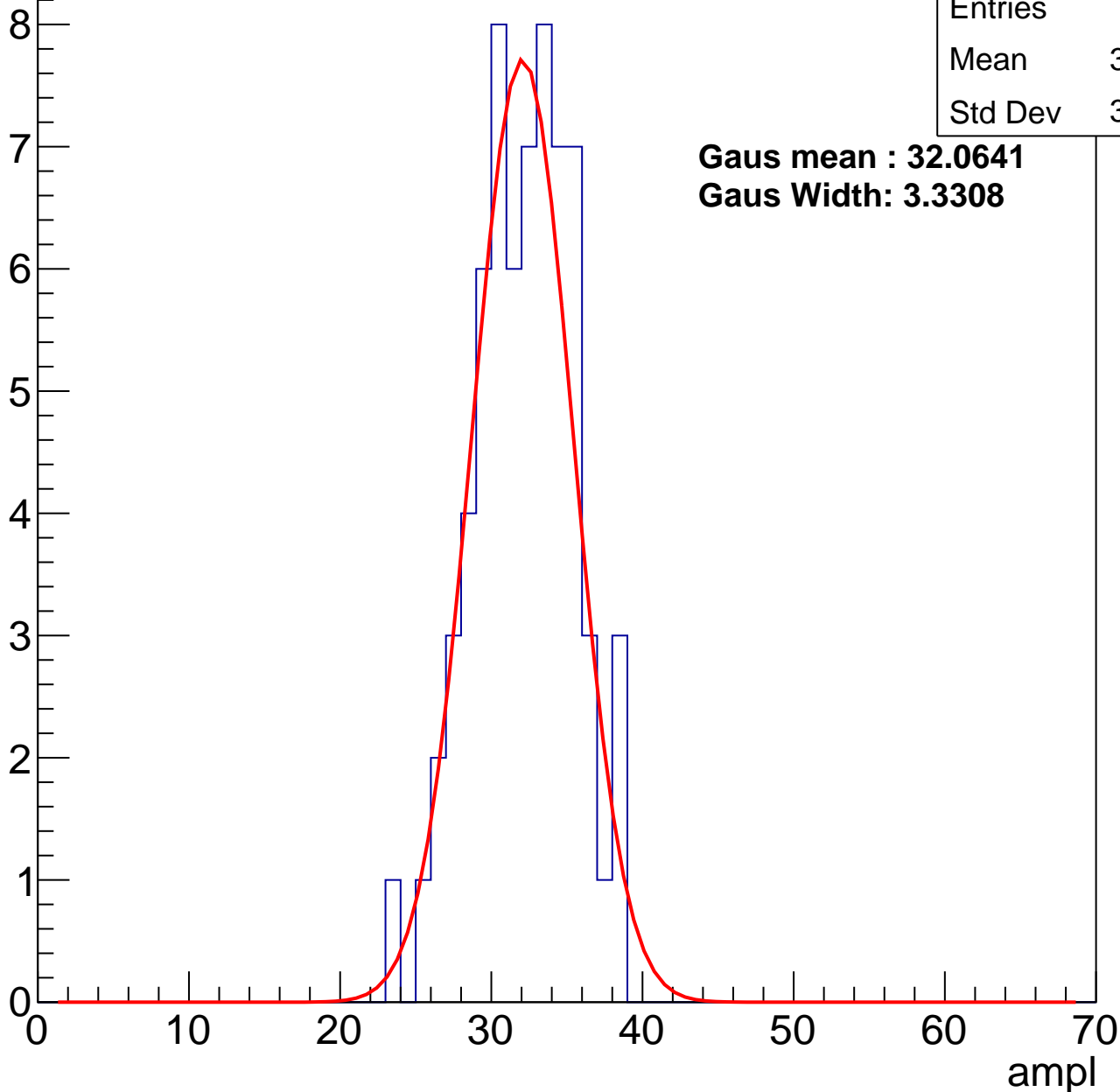
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	31.69
Std Dev	3.247

**Gaus mean : 32.0641**

**Gaus Width: 3.3308**



# B1L003S, U23-ch13, adc1

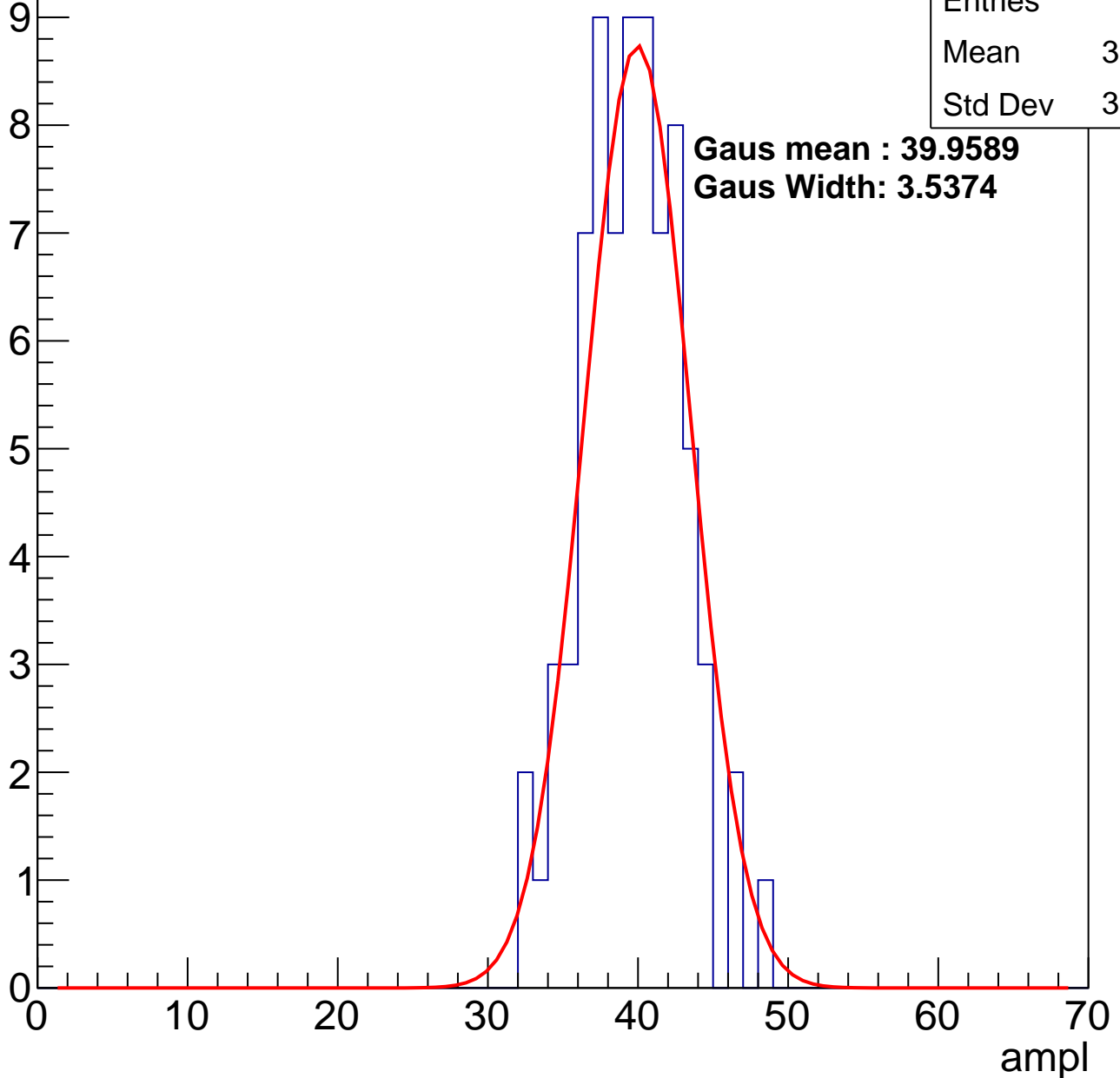
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	39.16
Std Dev	3.253

**Gaus mean : 39.9589**

**Gaus Width: 3.5374**



# B1L003S, U23-ch13, adc2

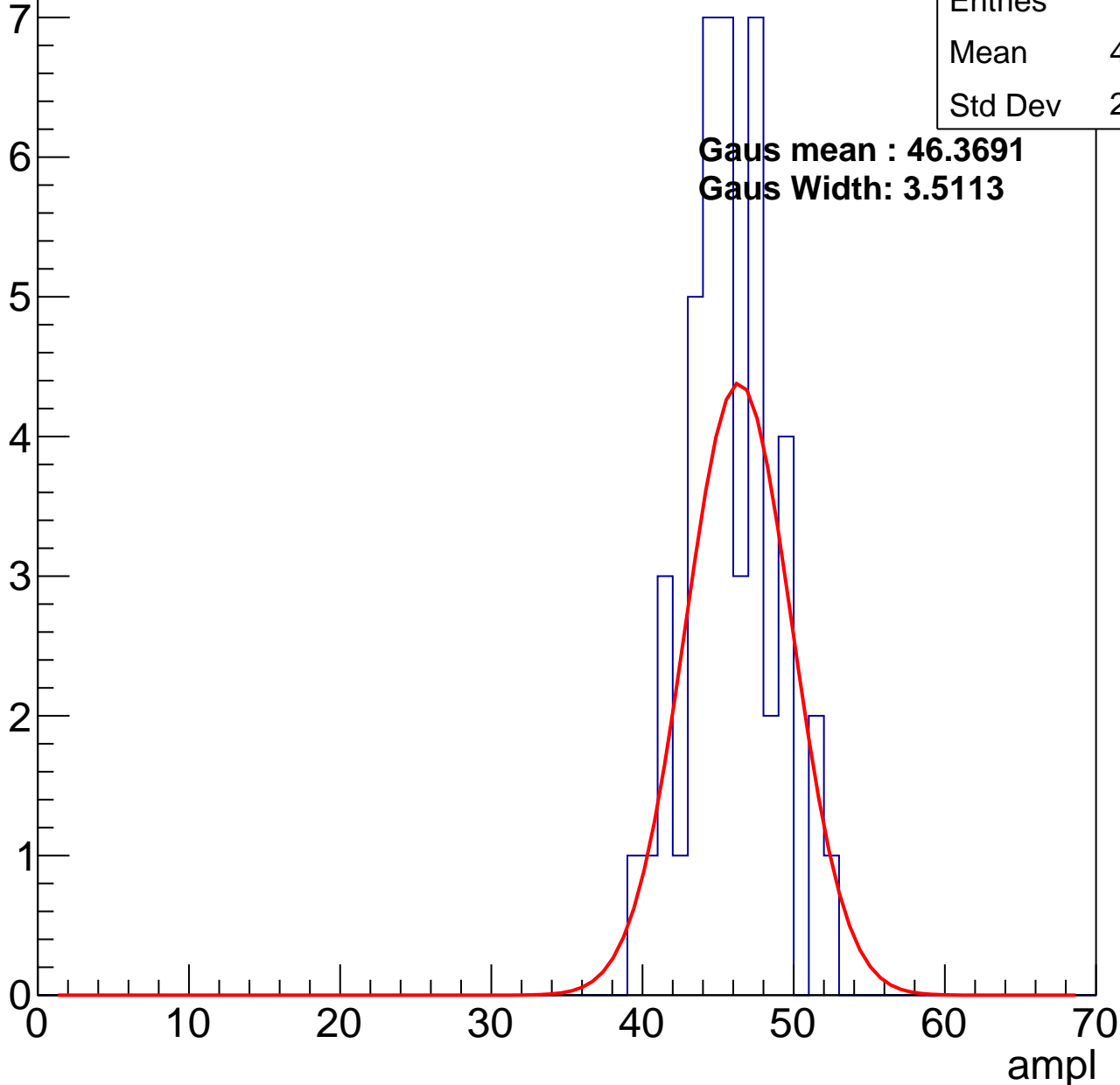
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	45.34
Std Dev	2.915

**Gaus mean : 46.3691**

**Gaus Width: 3.5113**

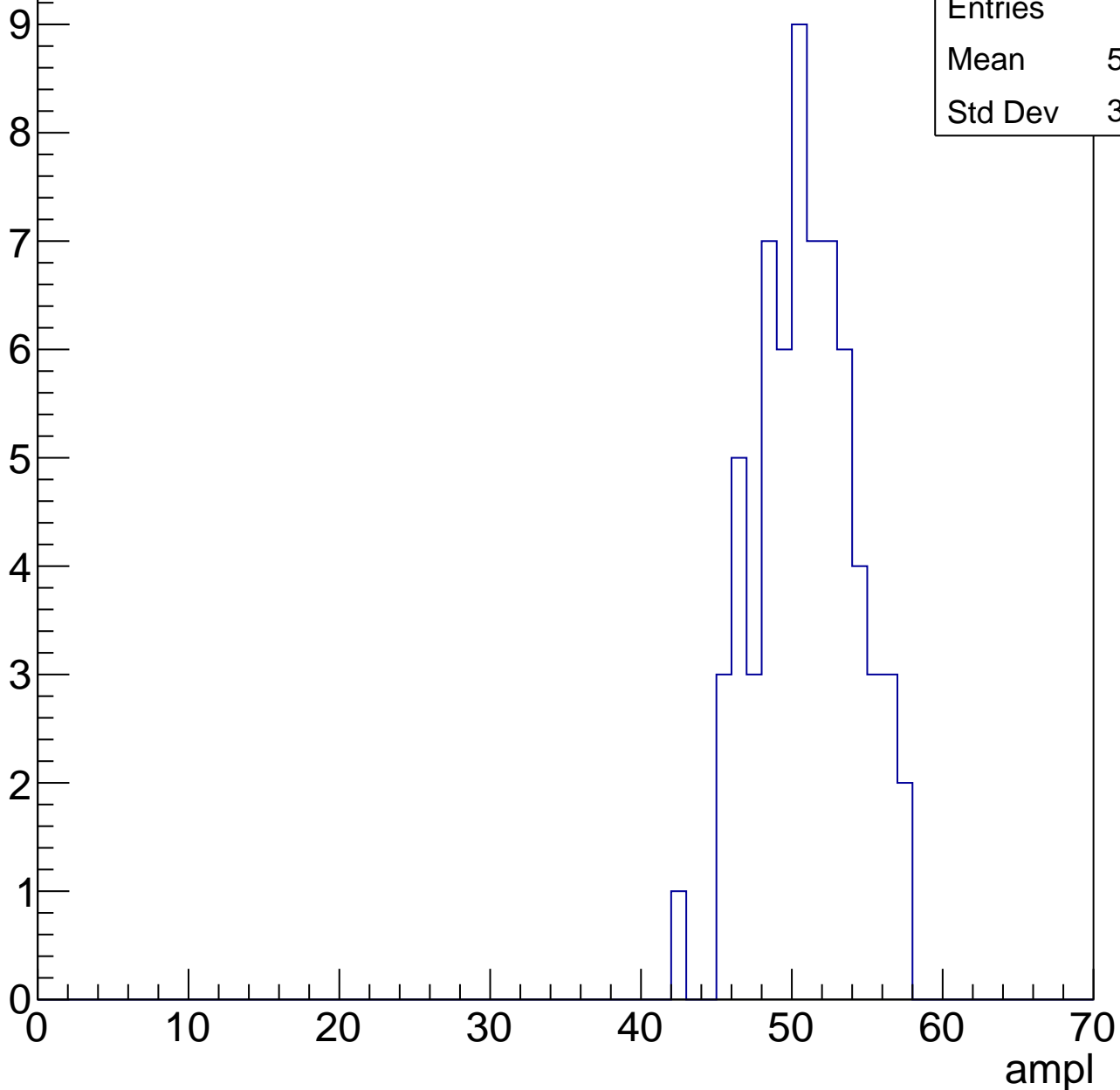


# B1L003S, U23-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	50.45
Std Dev	3.258



# B1L003S, U23-ch13, adc4

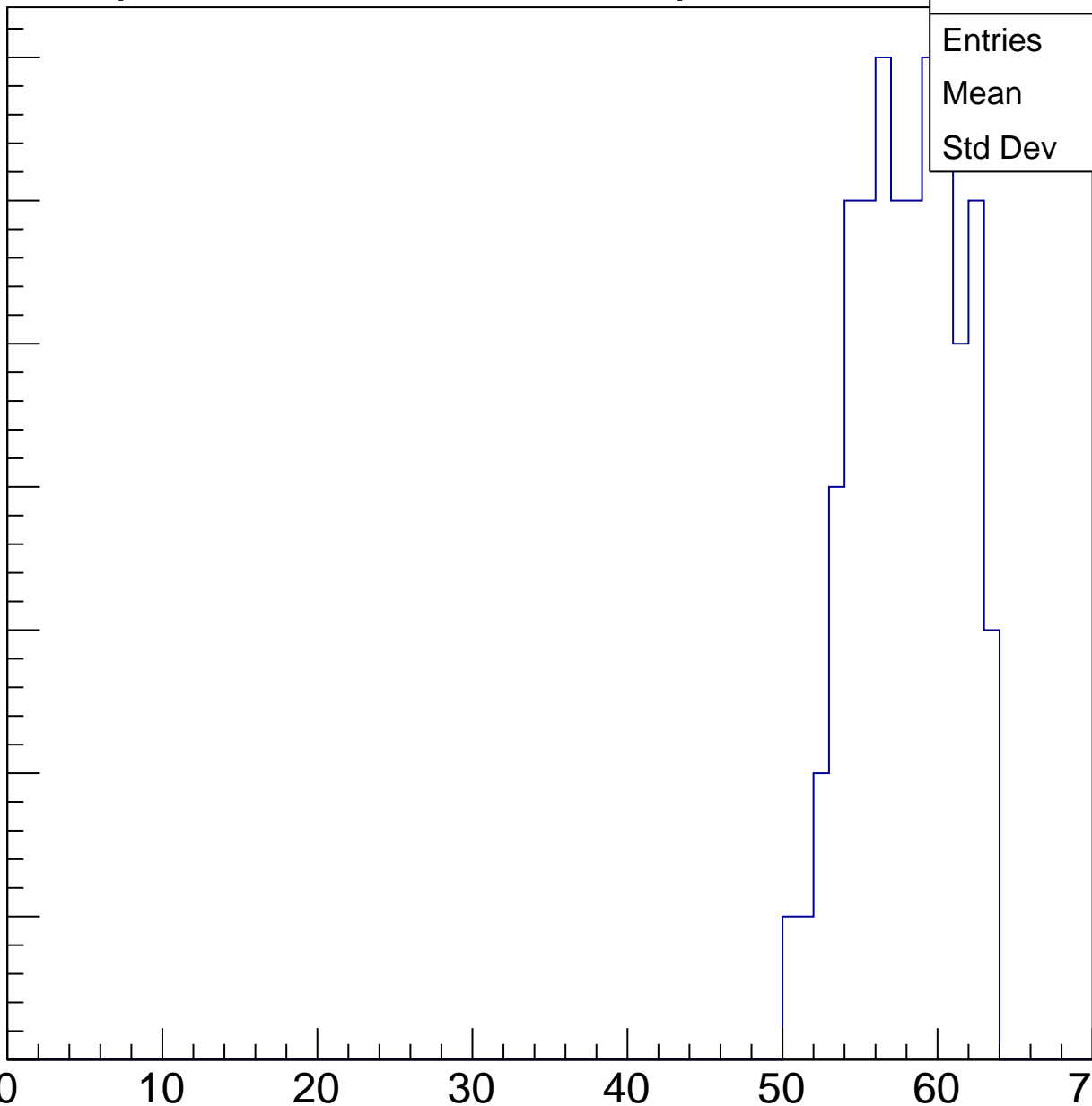
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	67
Mean	57.49
Std Dev	3.239

ampl

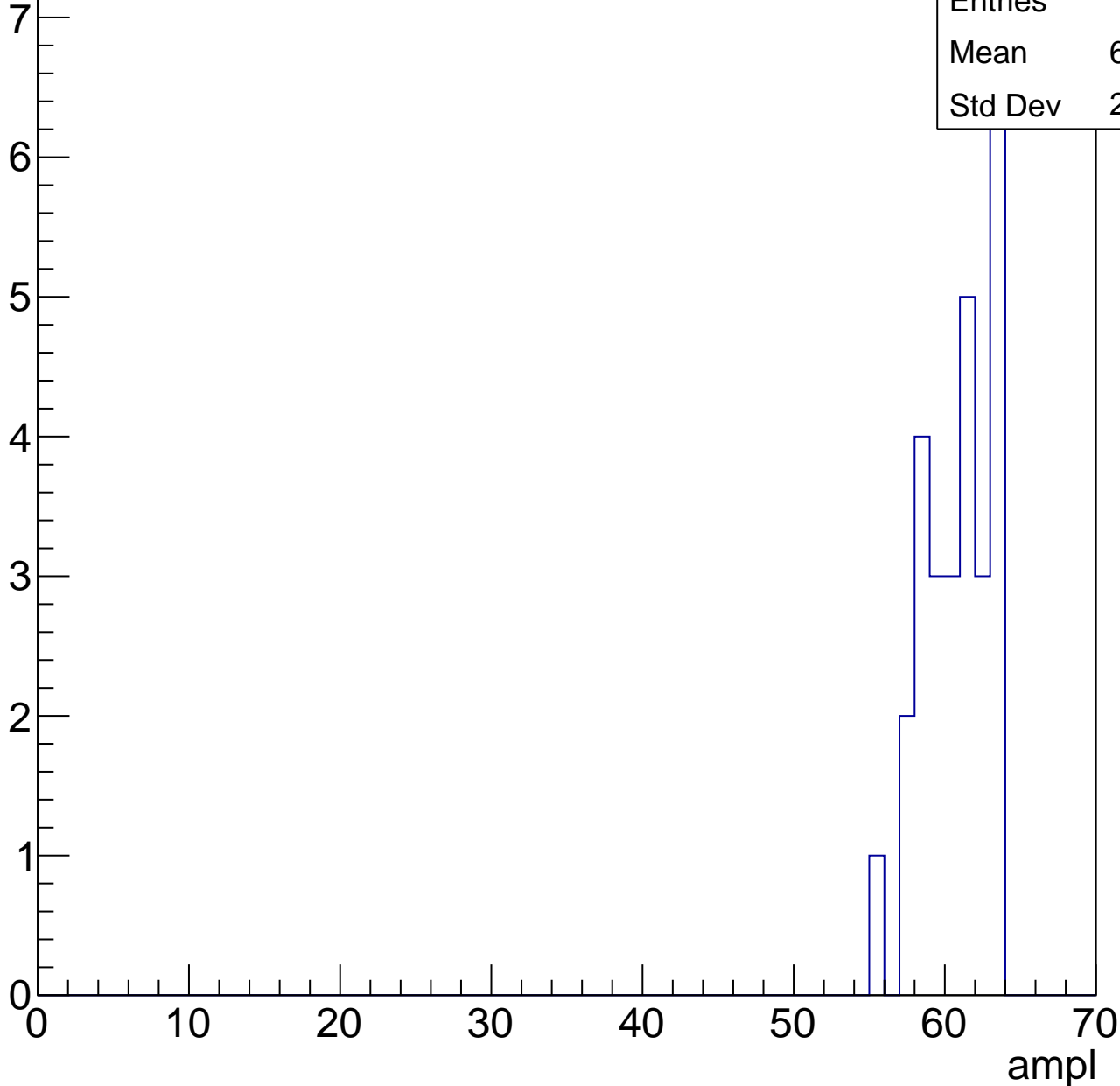


# B1L003S, U23-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

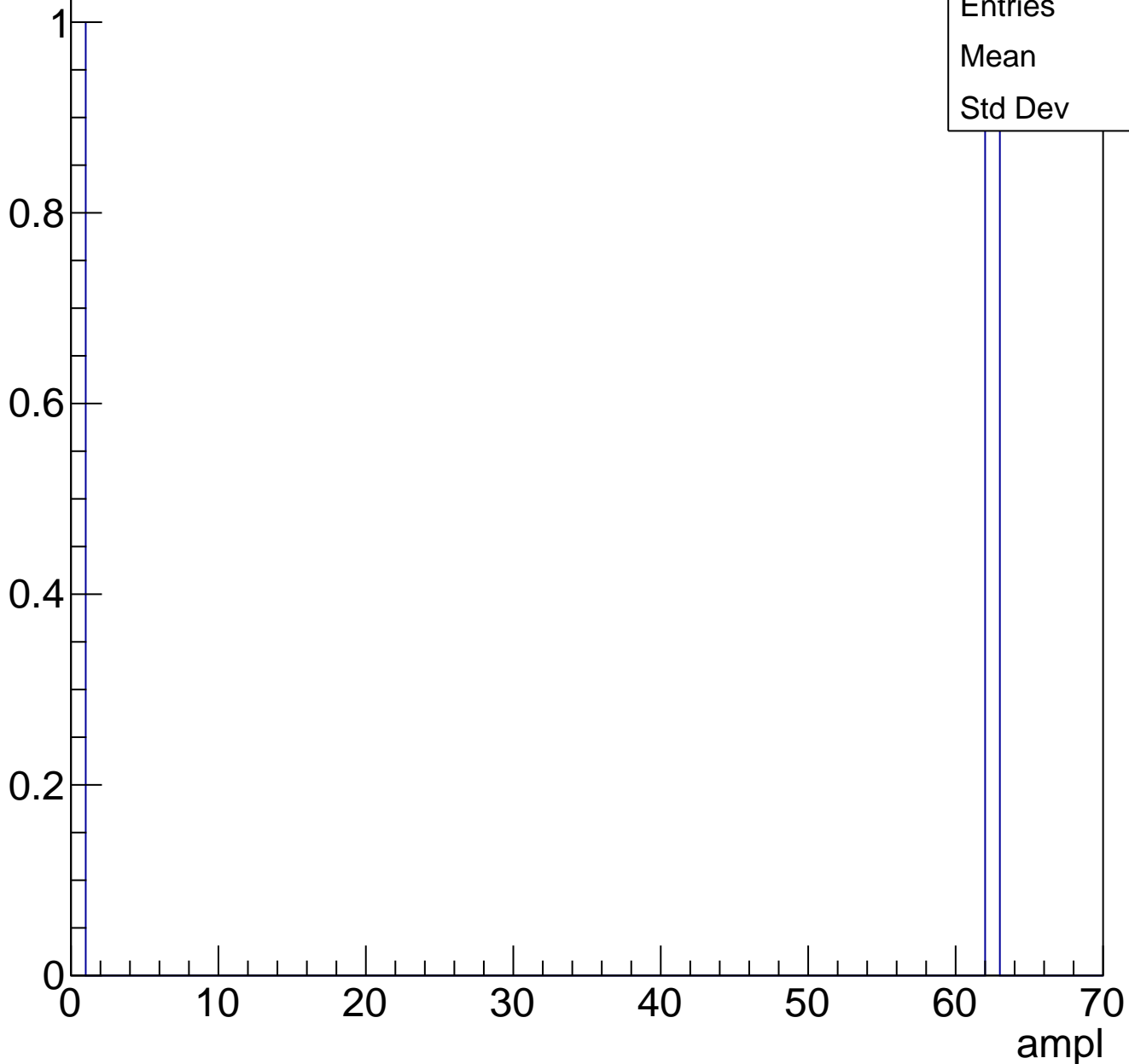
Entries	28
Mean	60.36
Std Dev	2.223



# B1L003S, U23-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch14, adc0

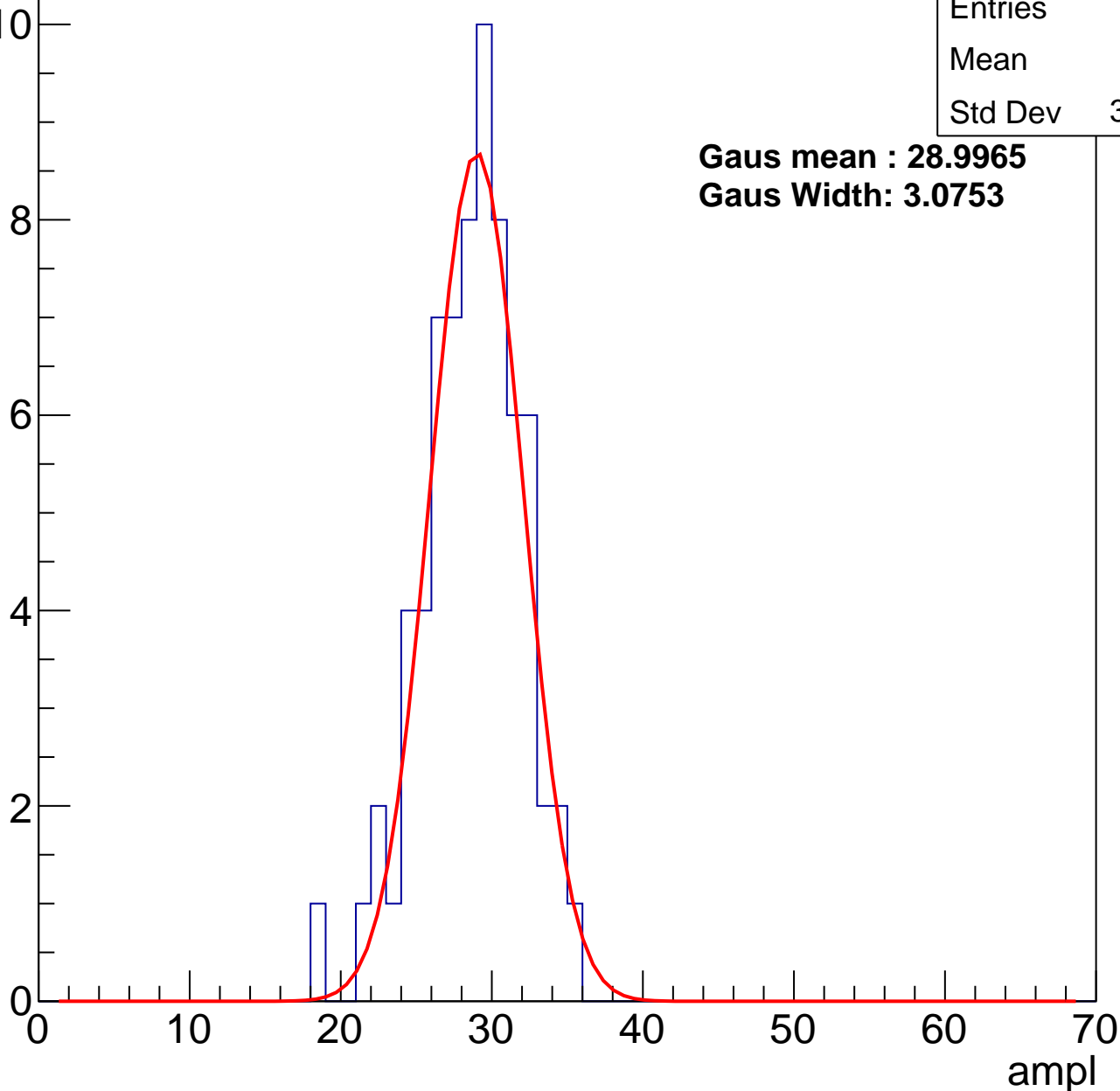
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.2
Std Dev	3.254

**Gaus mean : 28.9965**

**Gaus Width: 3.0753**



# B1L003S, U23-ch14, adc1

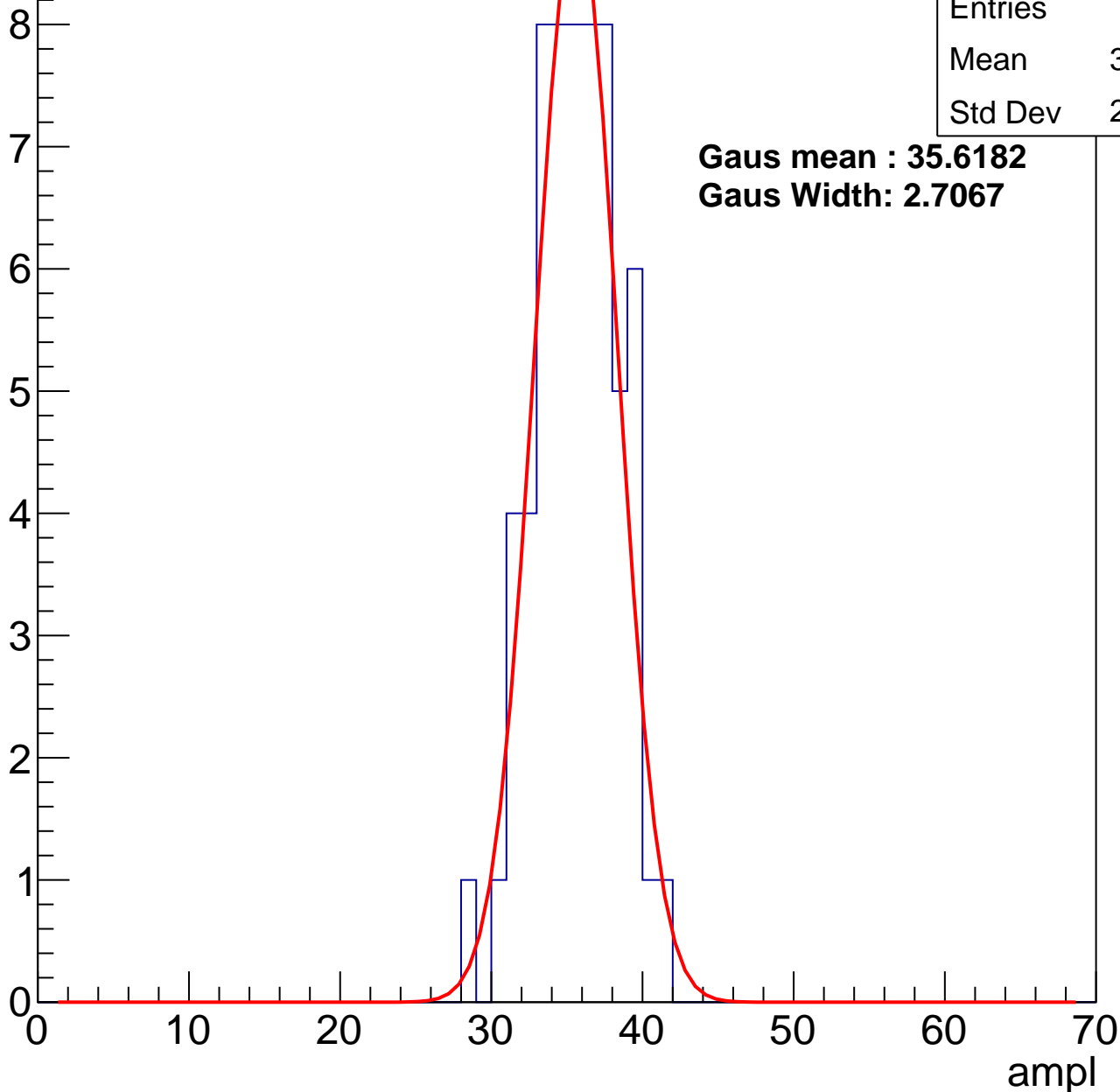
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	35.16
Std Dev	2.686

**Gaus mean : 35.6182**

**Gaus Width: 2.7067**



# B1L003S, U23-ch14, adc2

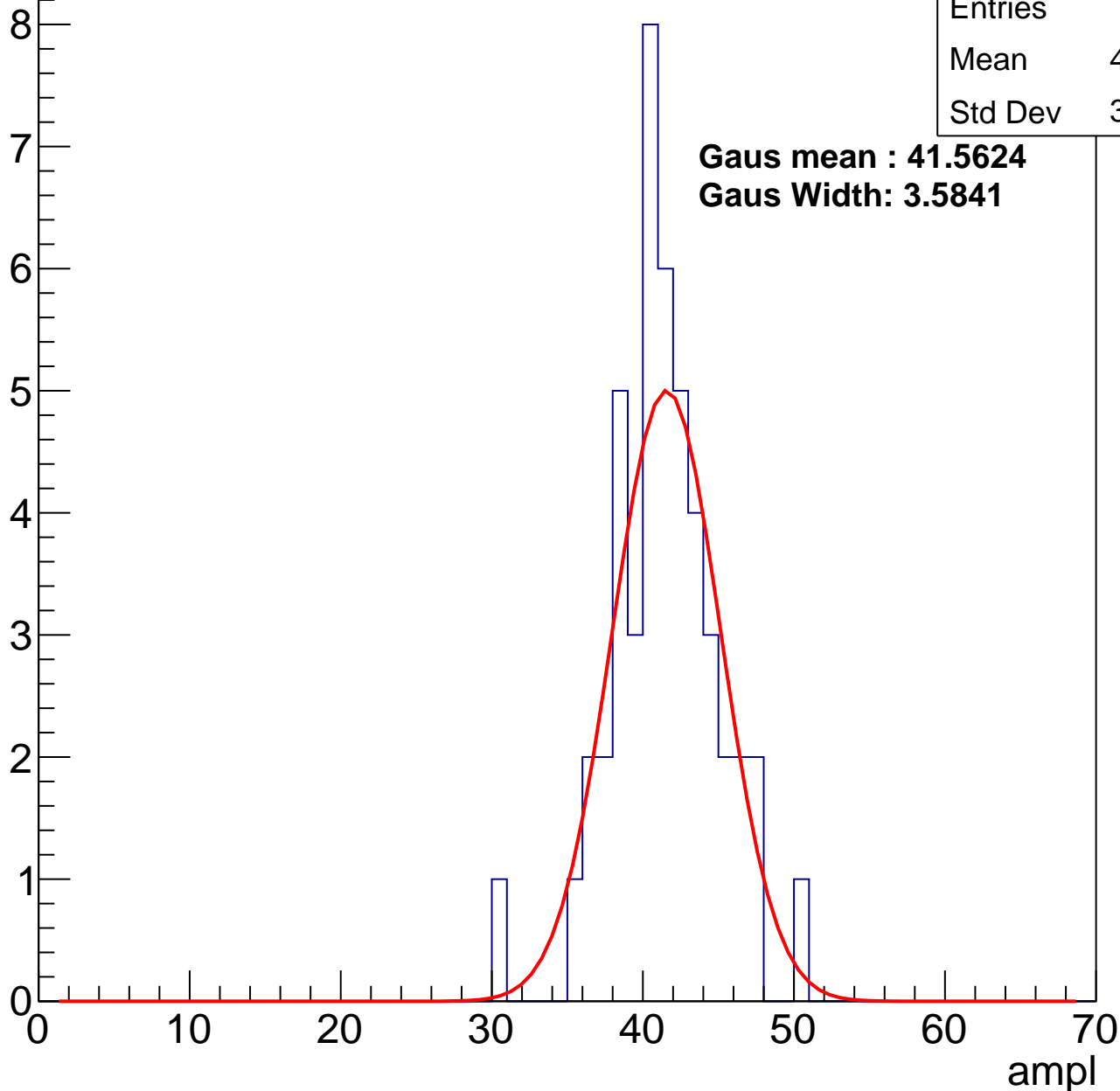
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	40.94
Std Dev	3.533

**Gaus mean : 41.5624**

**Gaus Width: 3.5841**

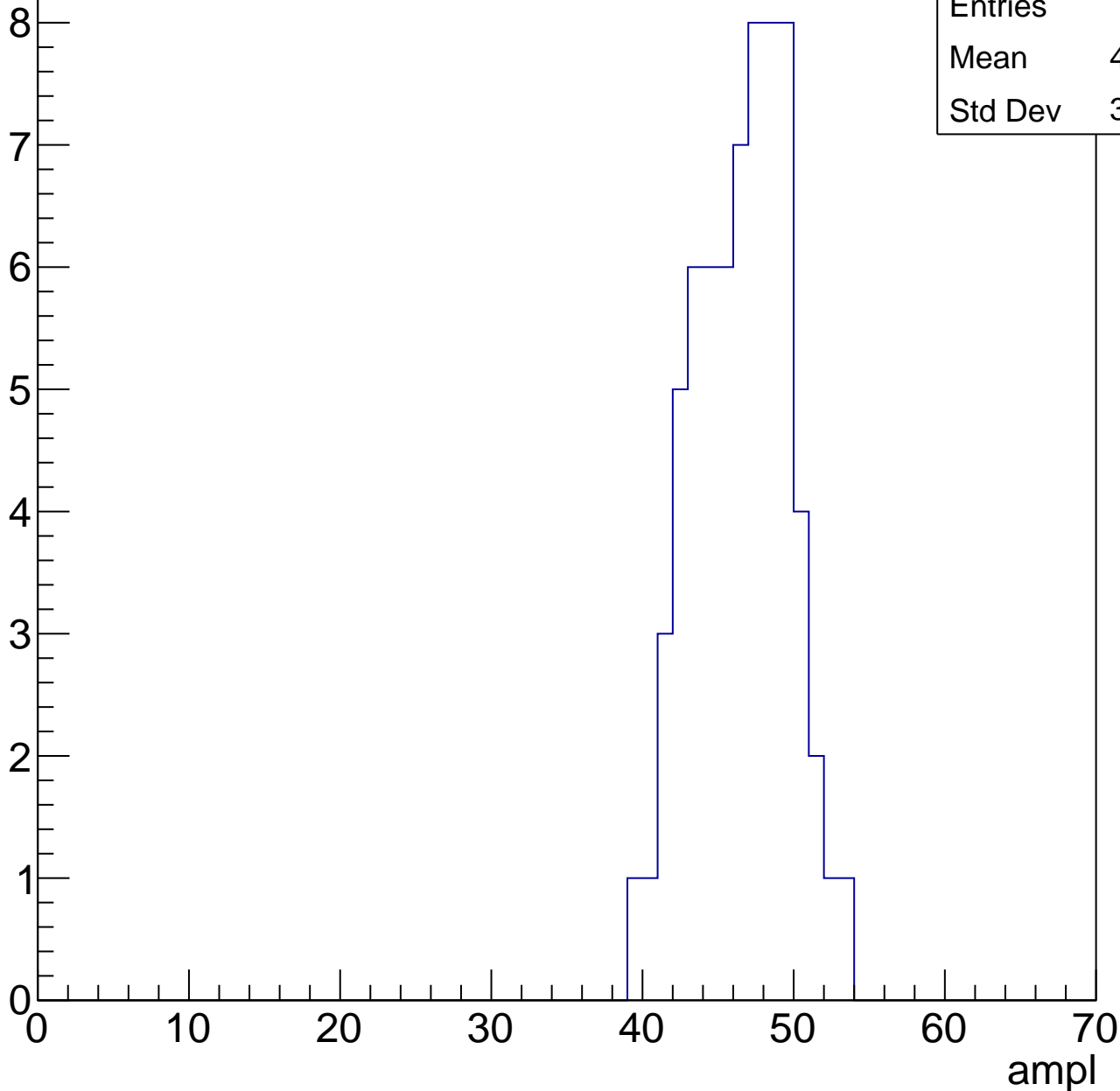


# B1L003S, U23-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

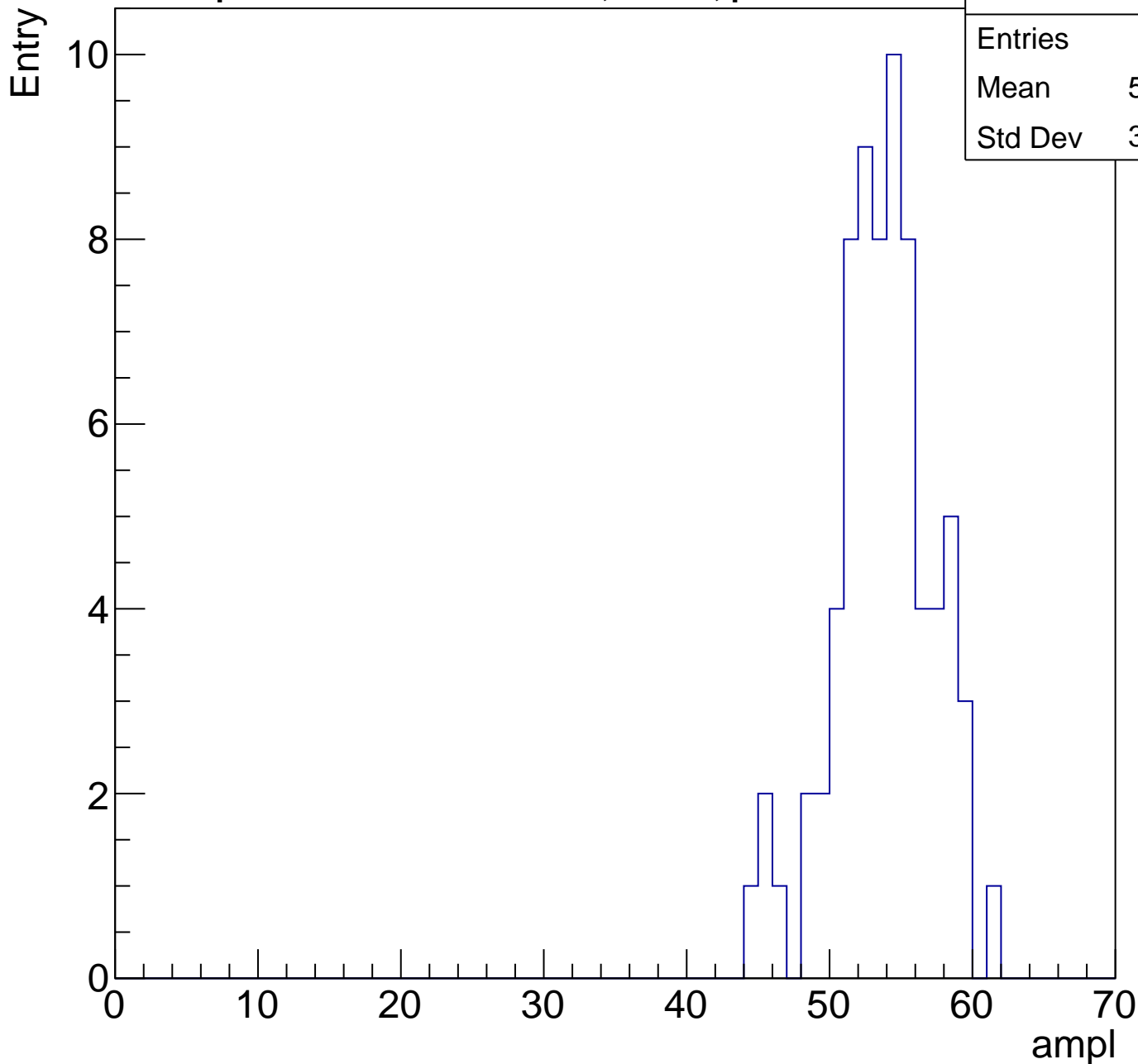
Entries	67
Mean	46.04
Std Dev	3.078



# B1L003S, U23-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	72
Mean	53.25
Std Dev	3.439

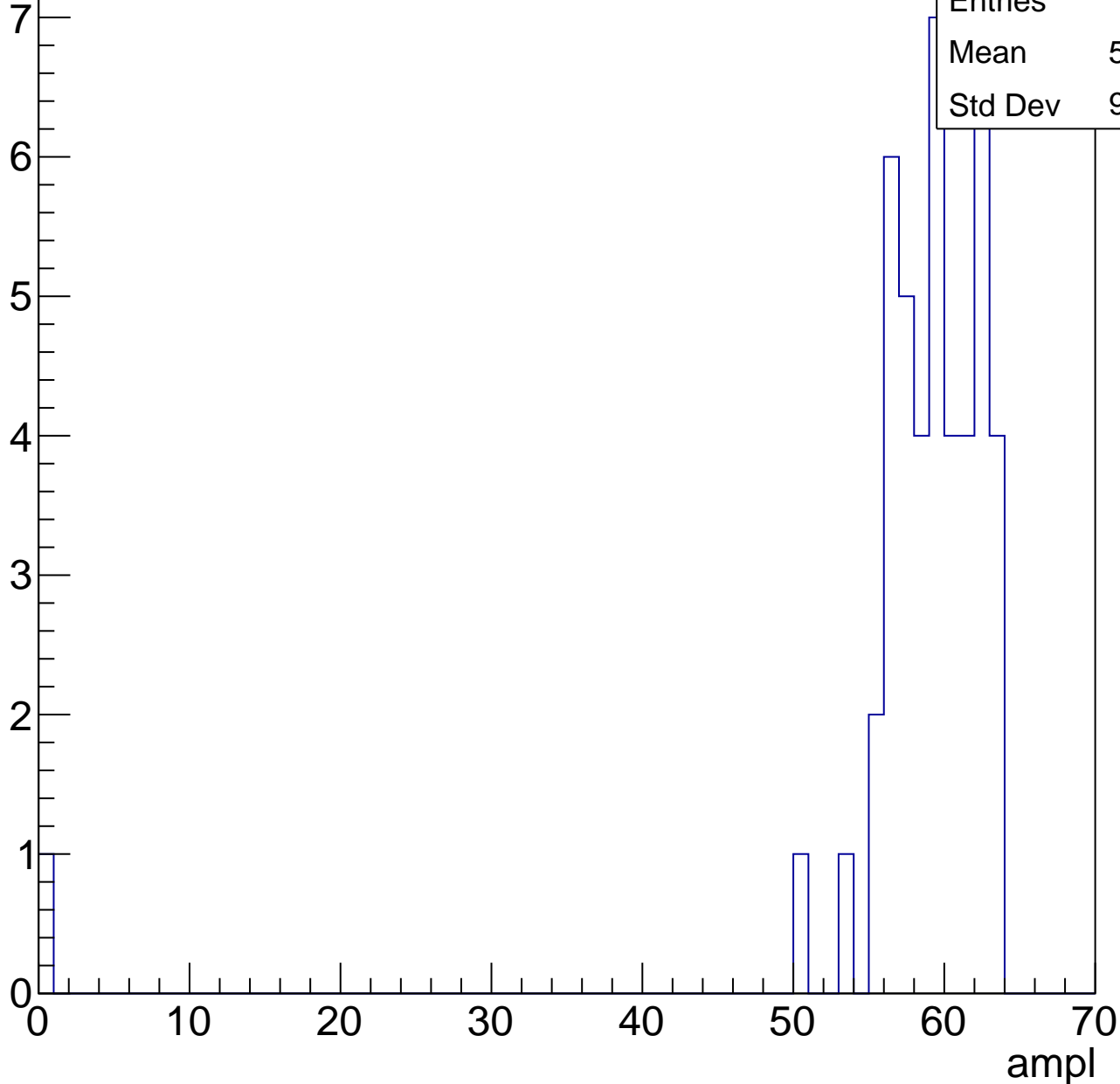


# B1L003S, U23-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

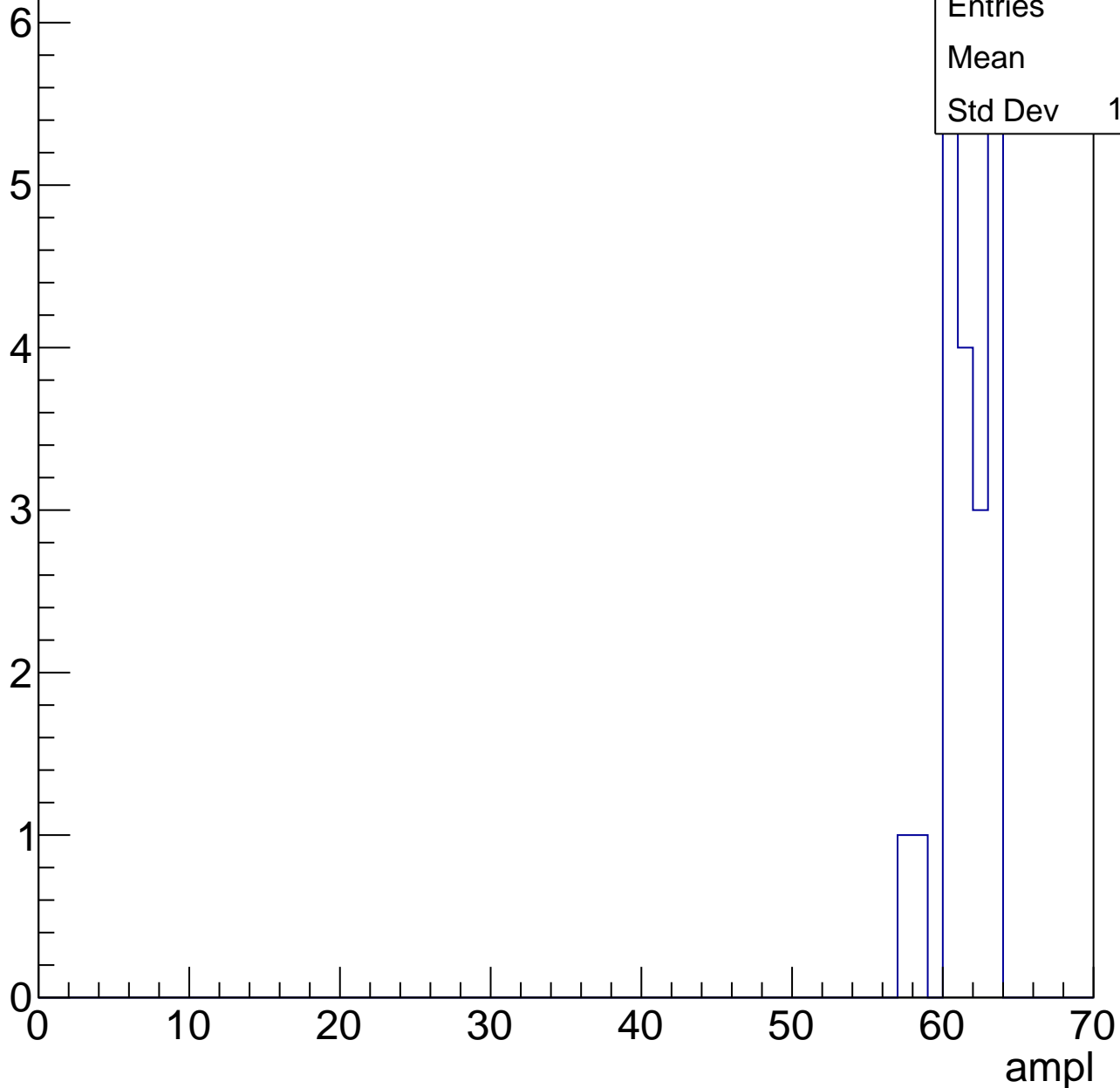
Entries	46
Mean	57.59
Std Dev	9.045



# B1L003S, U23-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch15, adc0

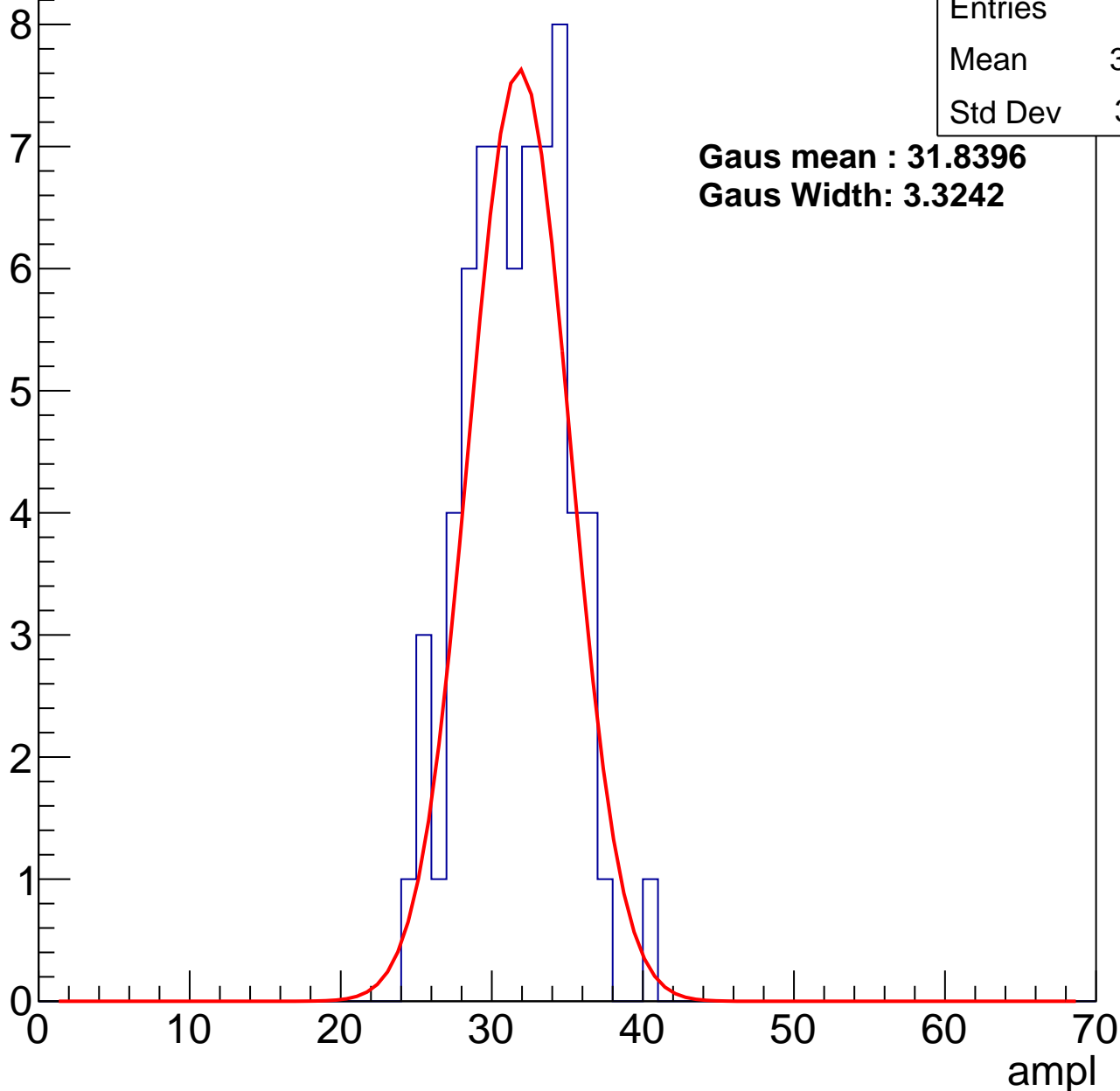
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	31.16
Std Dev	3.281

**Gaus mean : 31.8396**

**Gaus Width: 3.3242**



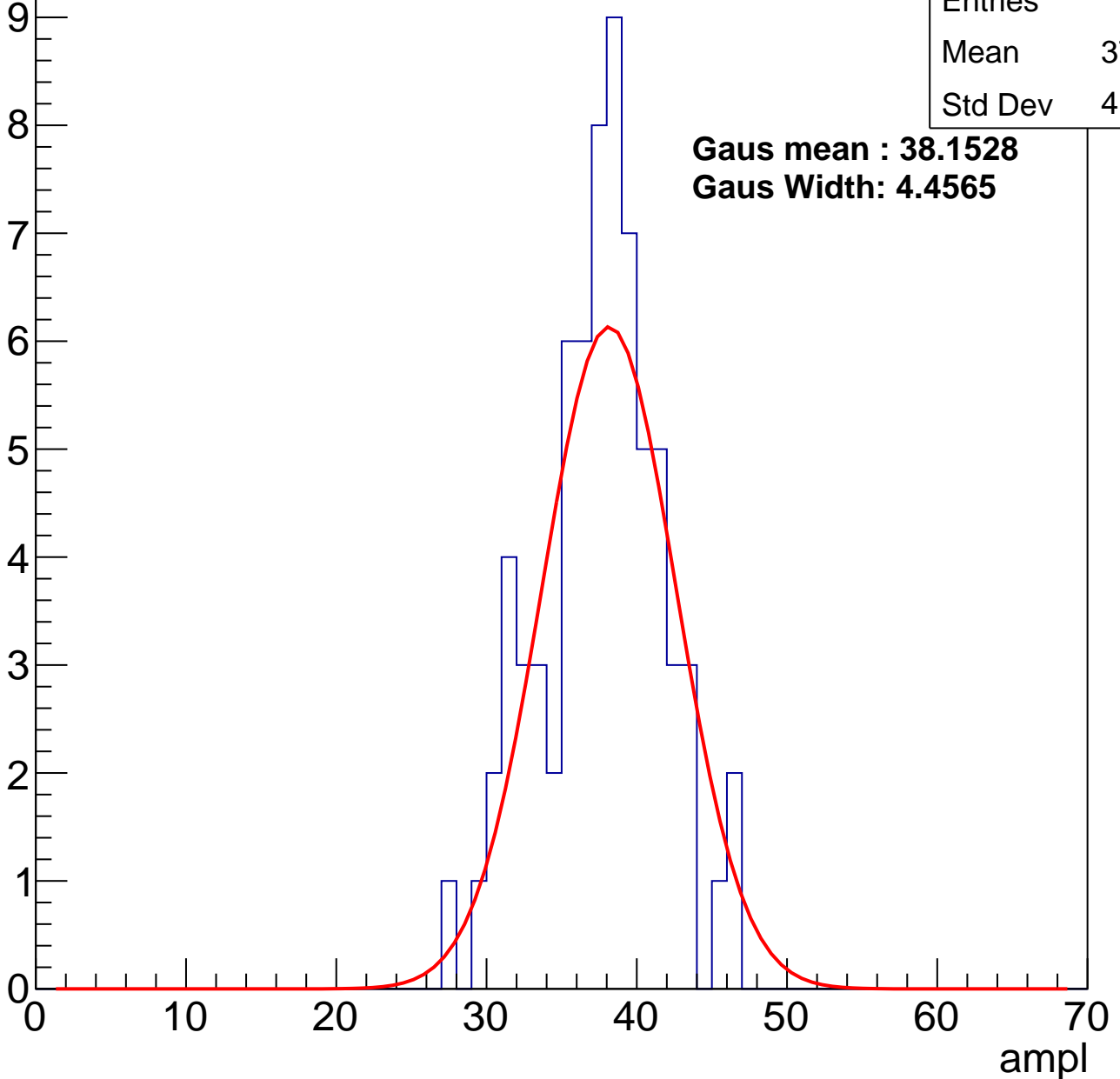
# B1L003S, U23-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	37.14
Std Dev	4.019

**Gaus mean : 38.1528**  
**Gaus Width: 4.4565**



# B1L003S, U23-ch15, adc2

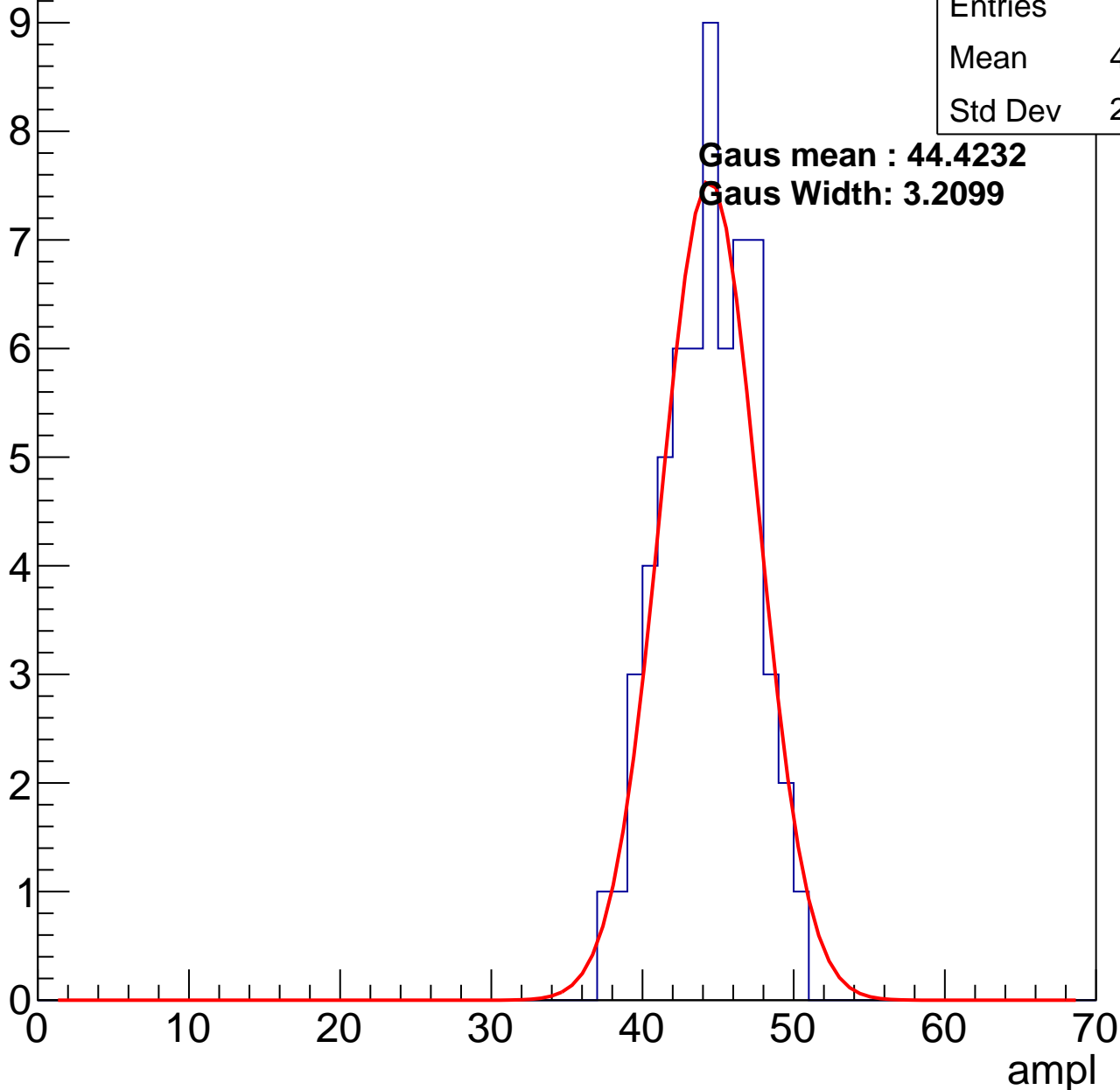
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.87
Std Dev	2.945

**Gaus mean : 44.4232**

**Gaus Width: 3.2099**

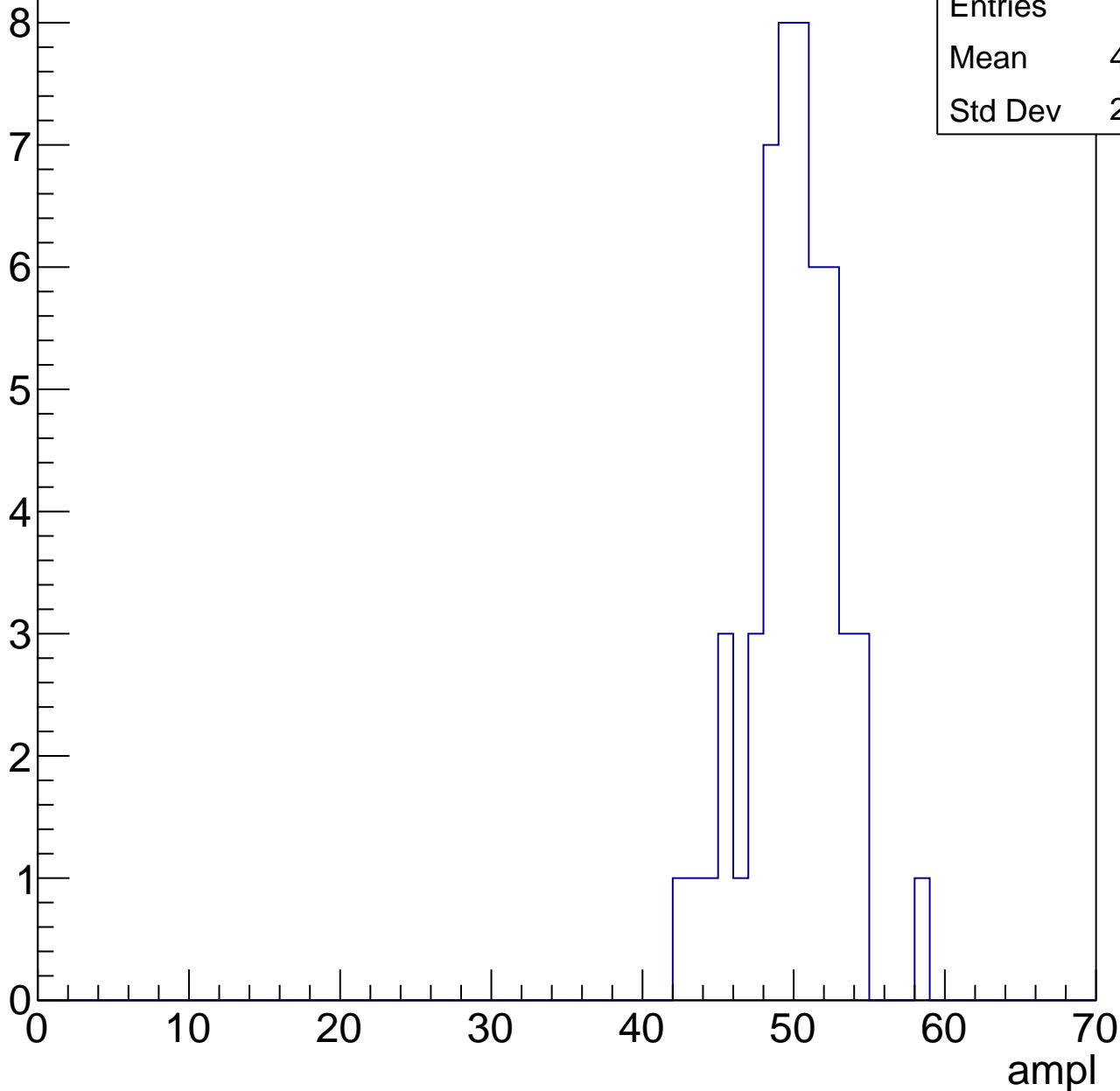


# B1L003S, U23-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	49.54
Std Dev	2.977

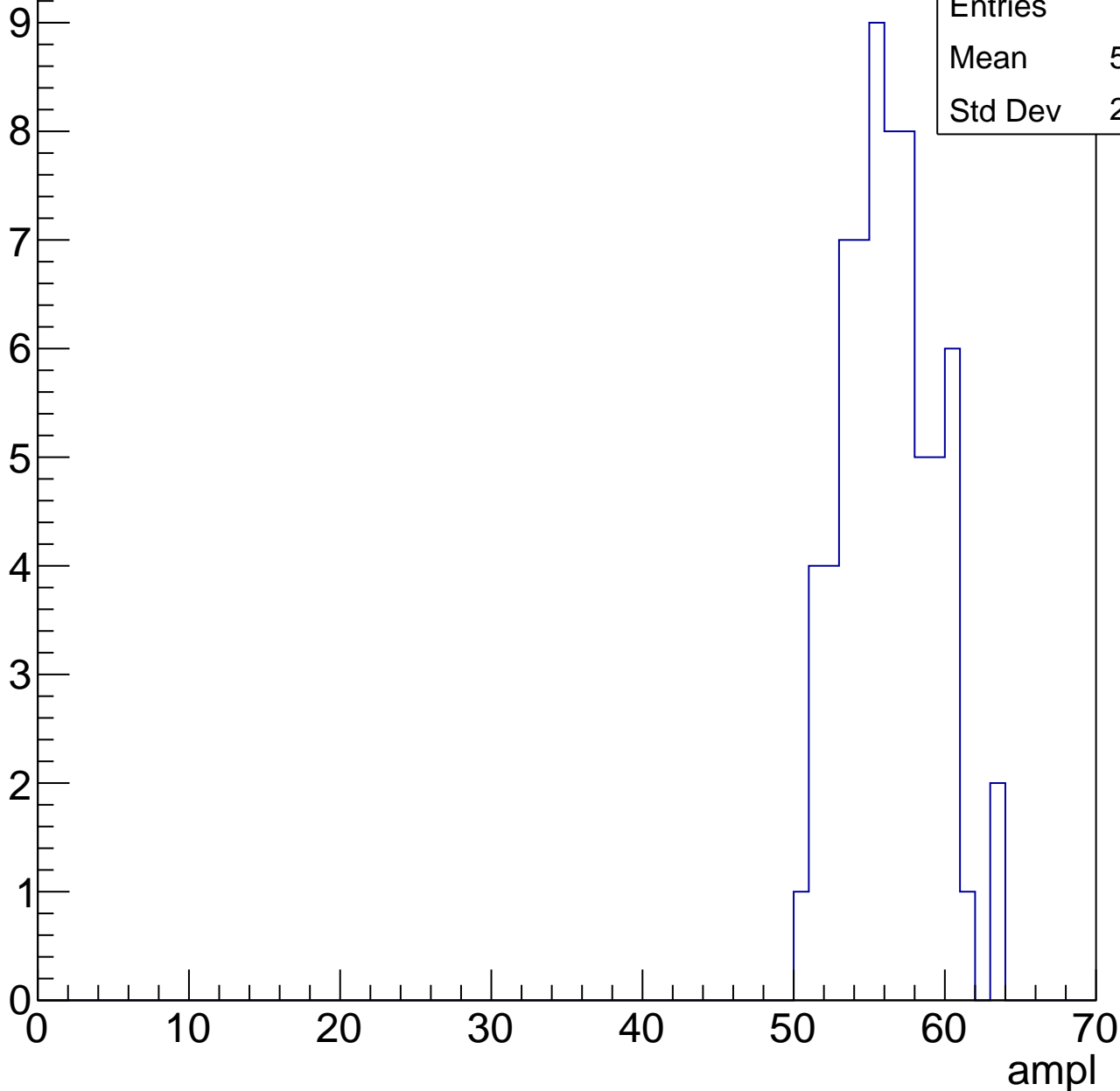


# B1L003S, U23-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.85
Std Dev	2.969

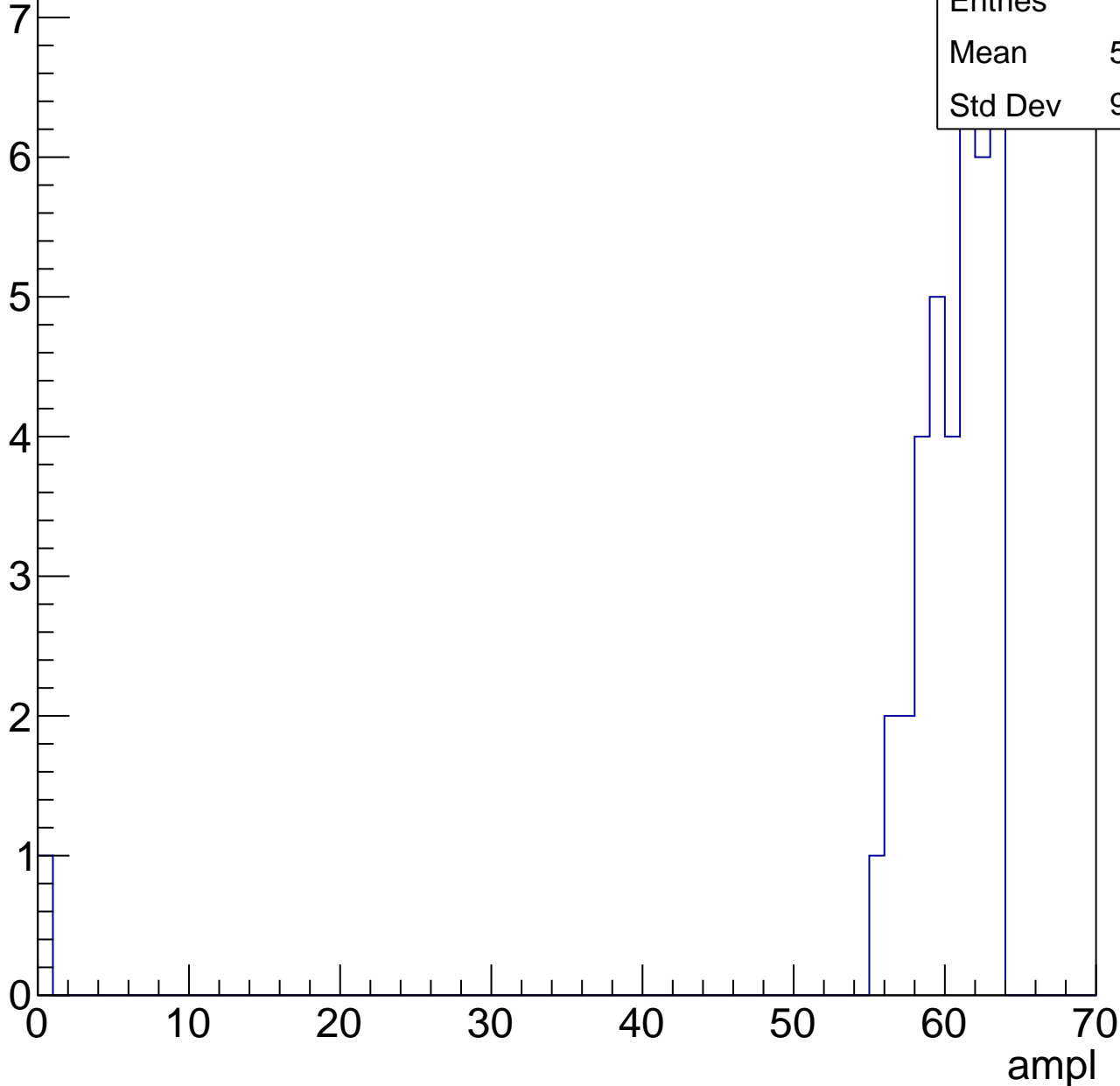


# B1L003S, U23-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

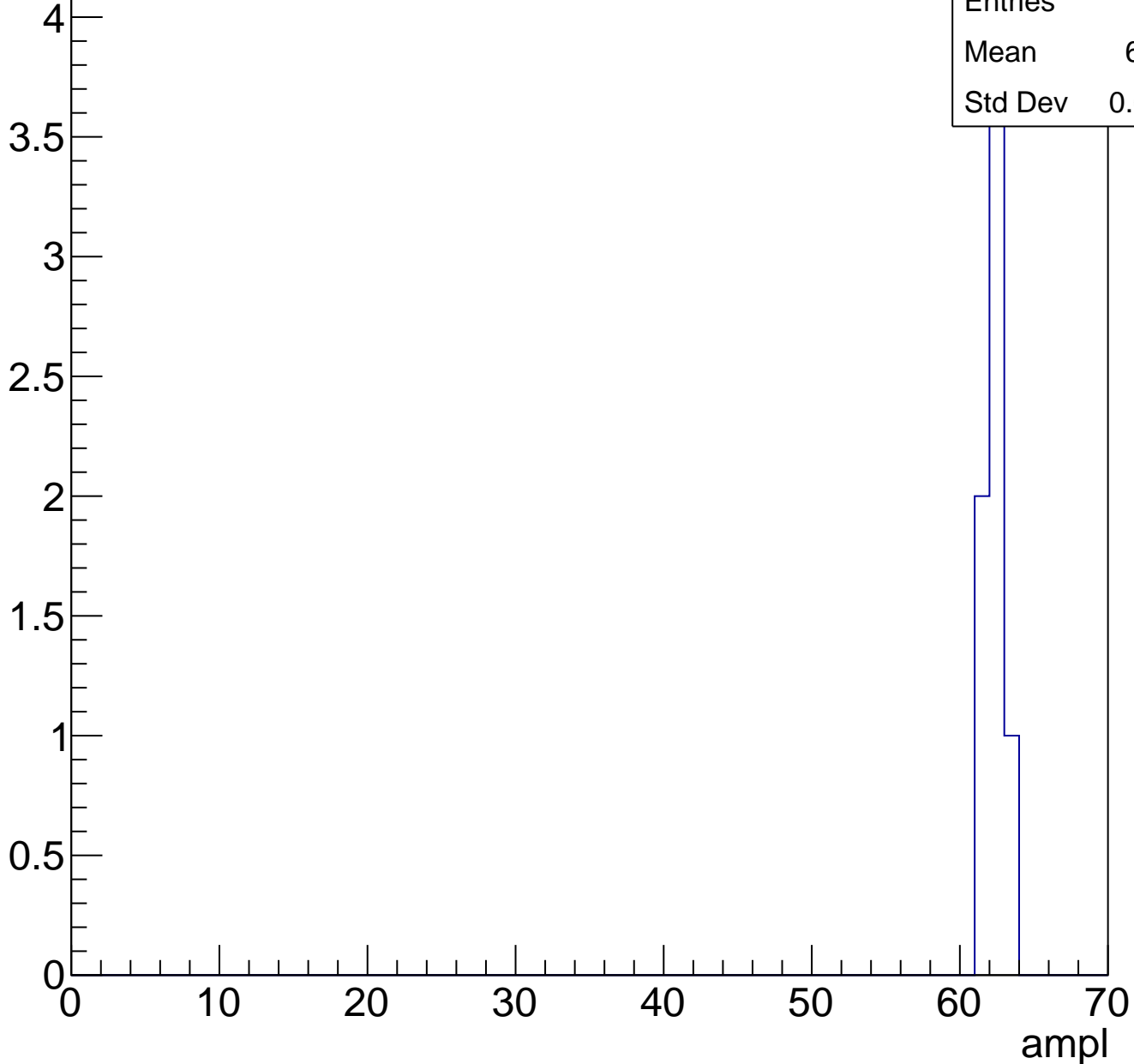
Entries	39
Mean	58.67
Std Dev	9.767



# B1L003S, U23-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch16, adc0

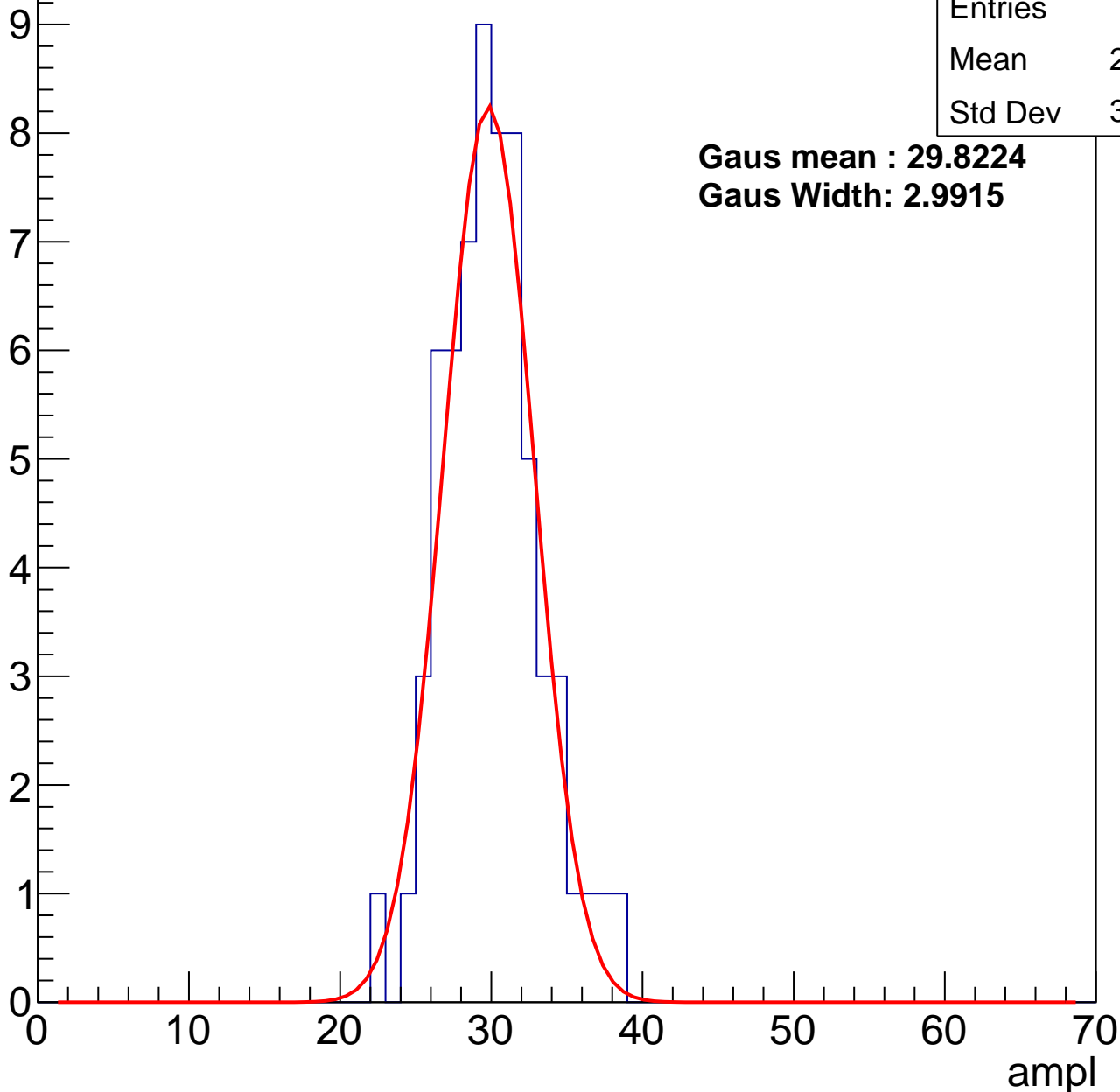
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.55
Std Dev	3.127

**Gaus mean : 29.8224**

**Gaus Width: 2.9915**



# B1L003S, U23-ch16, adc1

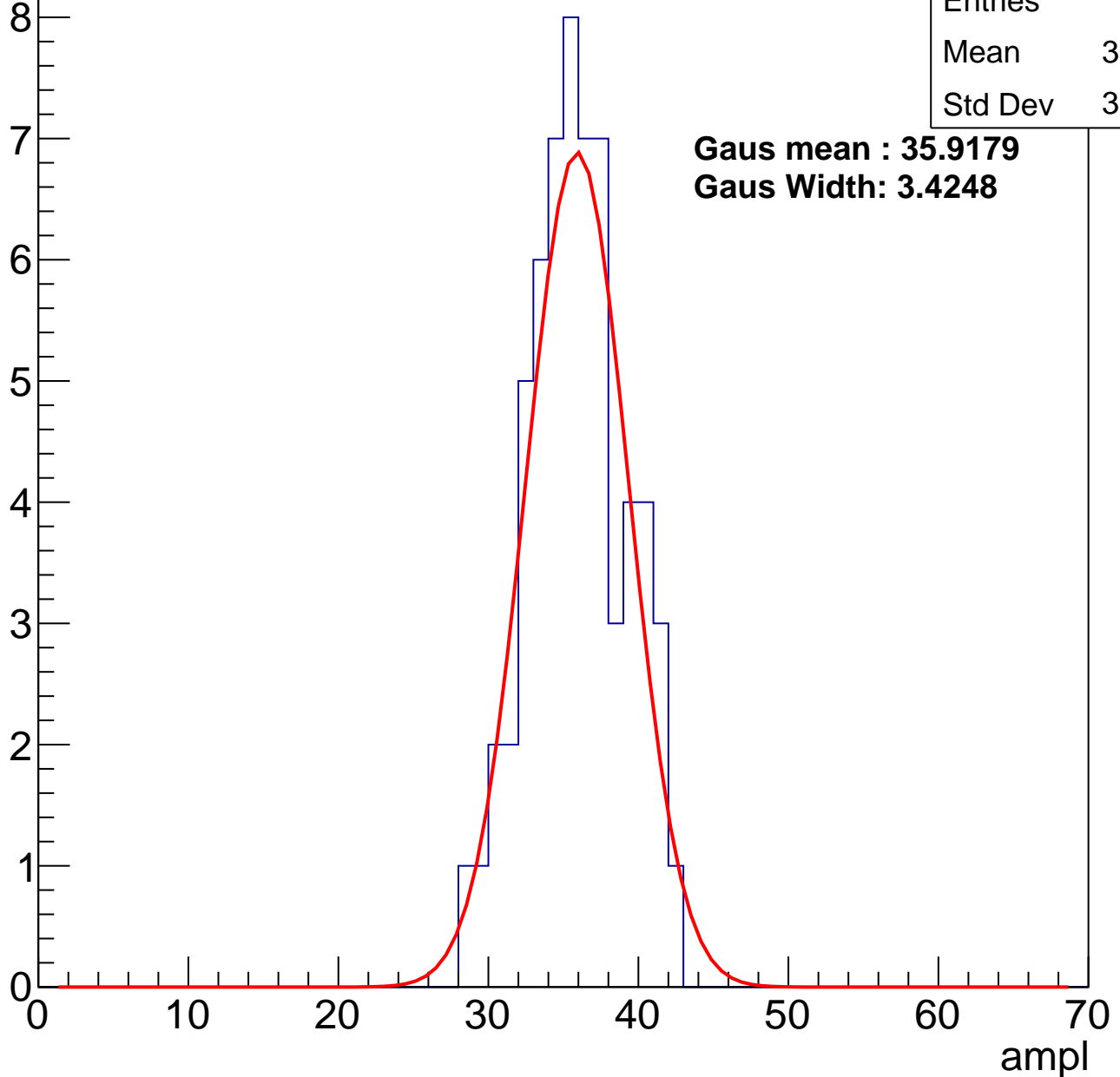
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.43
Std Dev	3.175

**Gaus mean : 35.9179**

**Gaus Width: 3.4248**



# B1L003S, U23-ch16, adc2

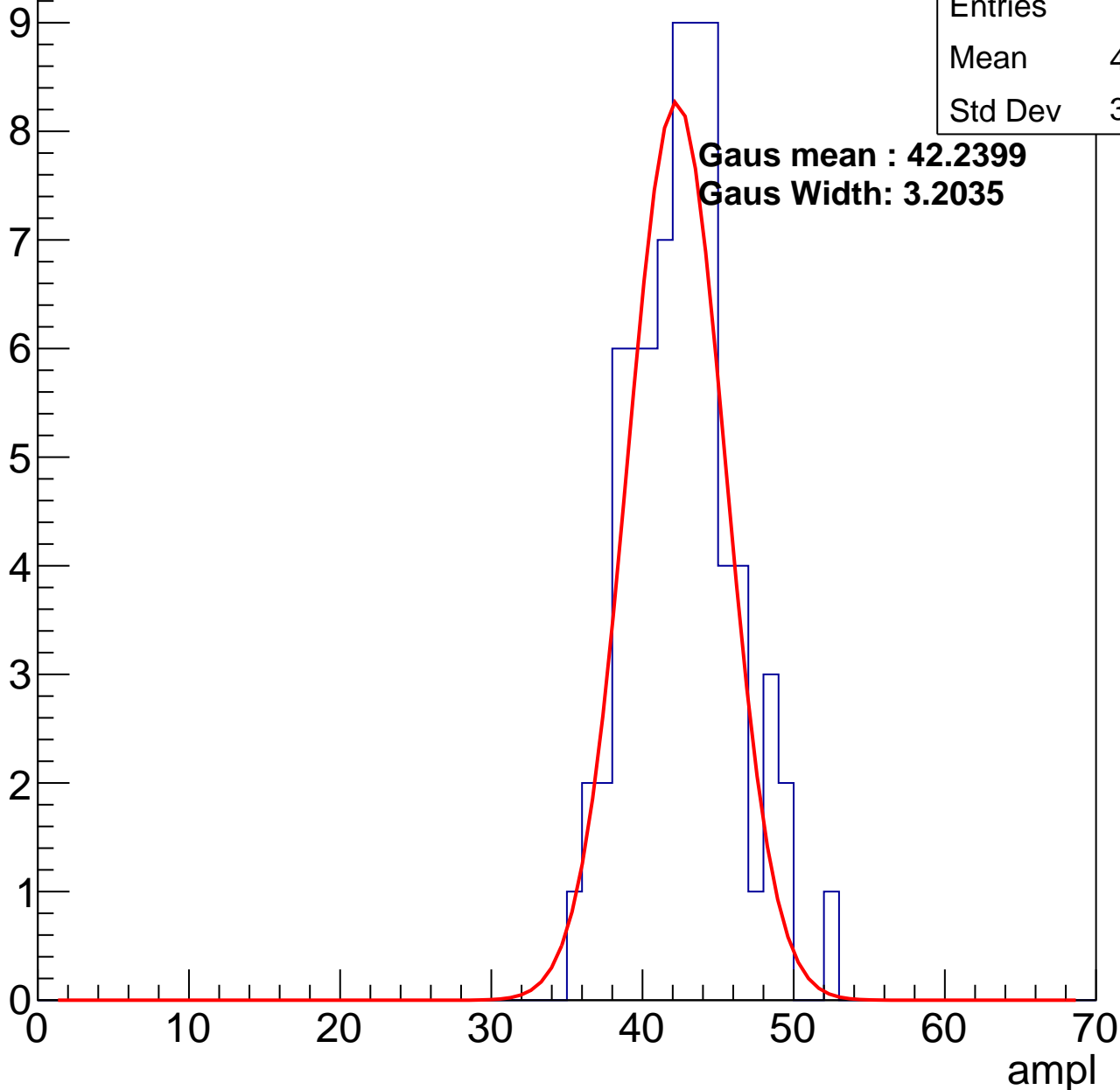
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.17
Std Dev	3.387

**Gaus mean : 42.2399**

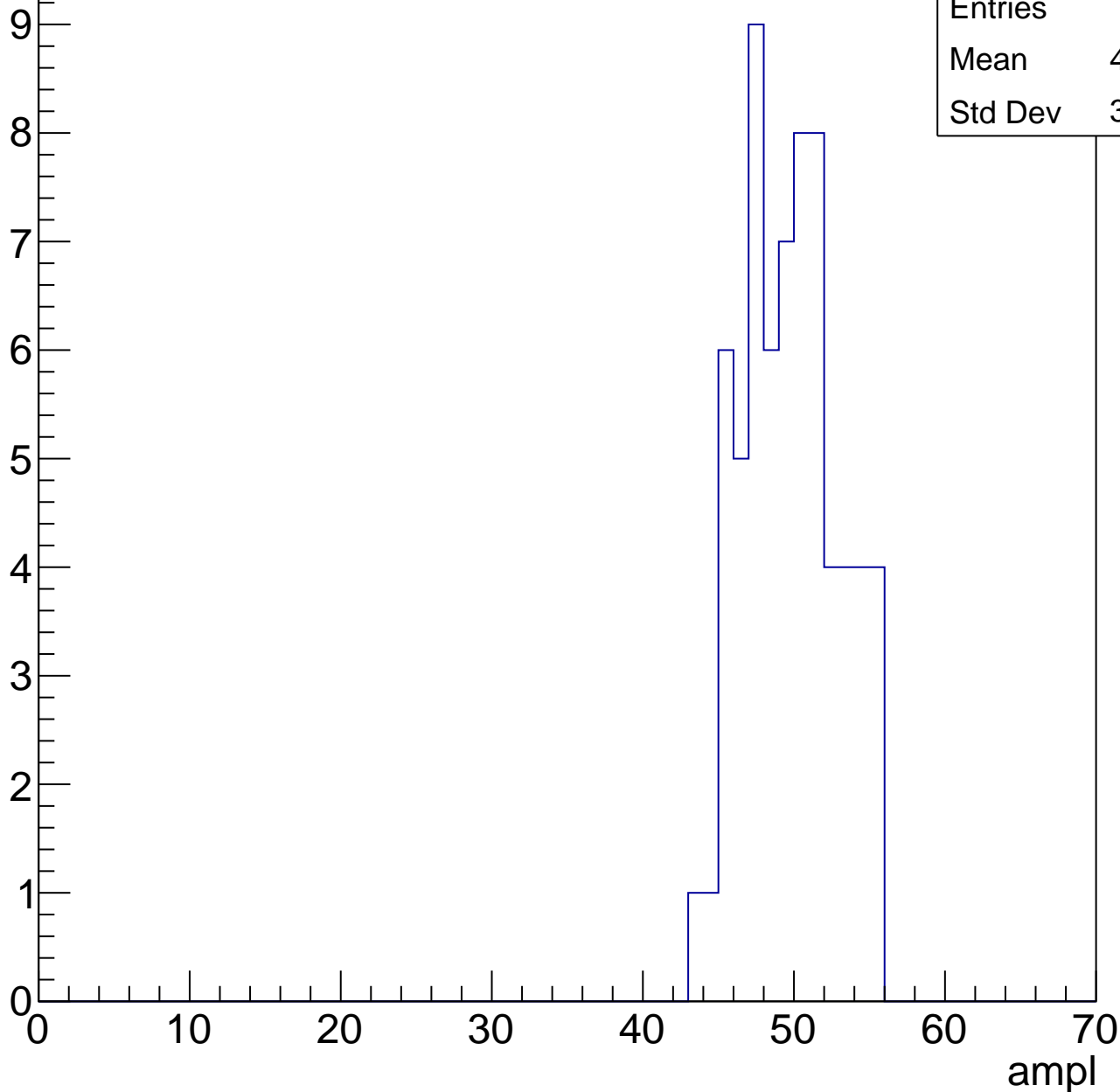
**Gaus Width: 3.2035**



# B1L003S, U23-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

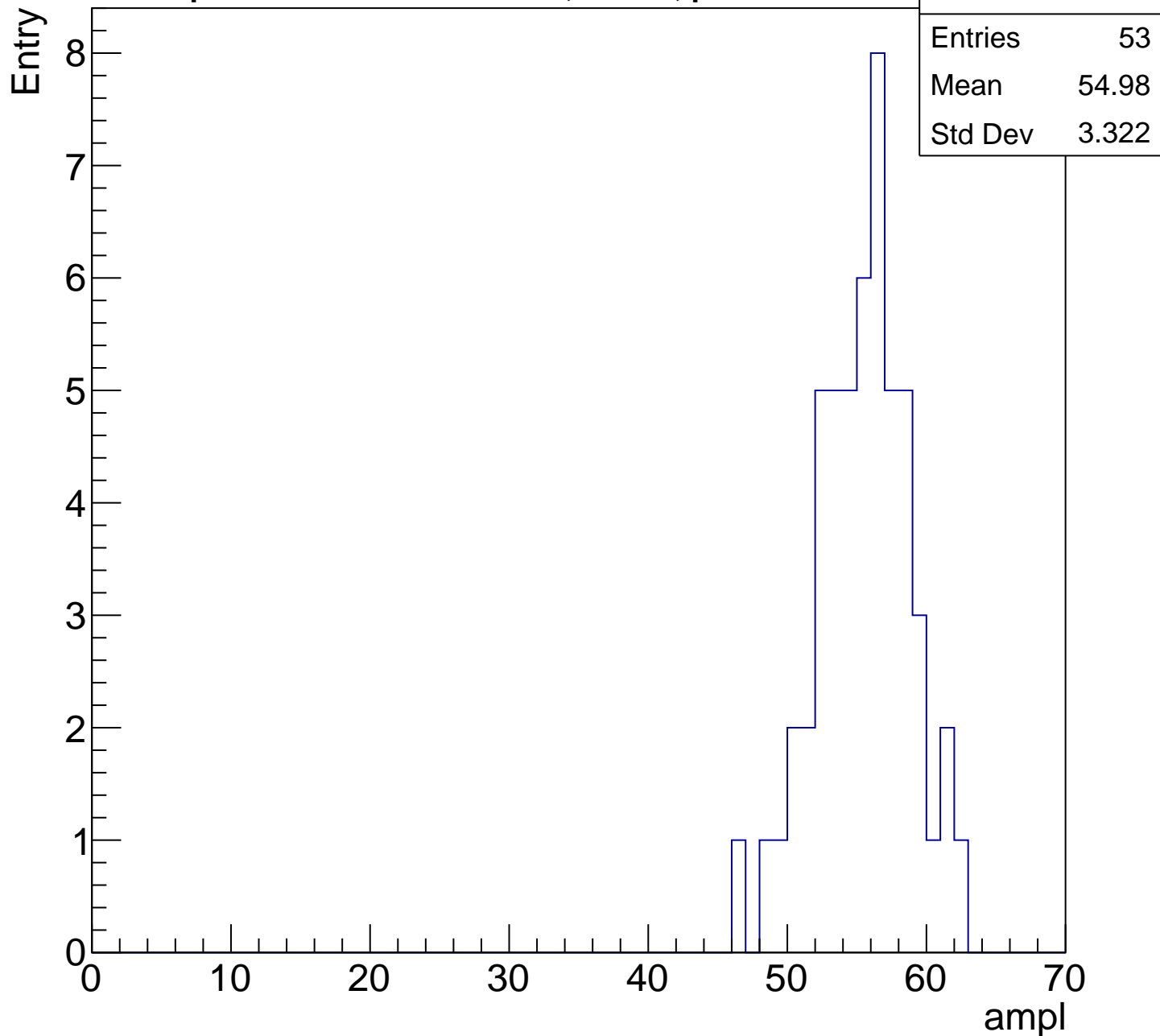
Entry



Entries	67
Mean	49.33
Std Dev	3.044

# B1L003S, U23-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

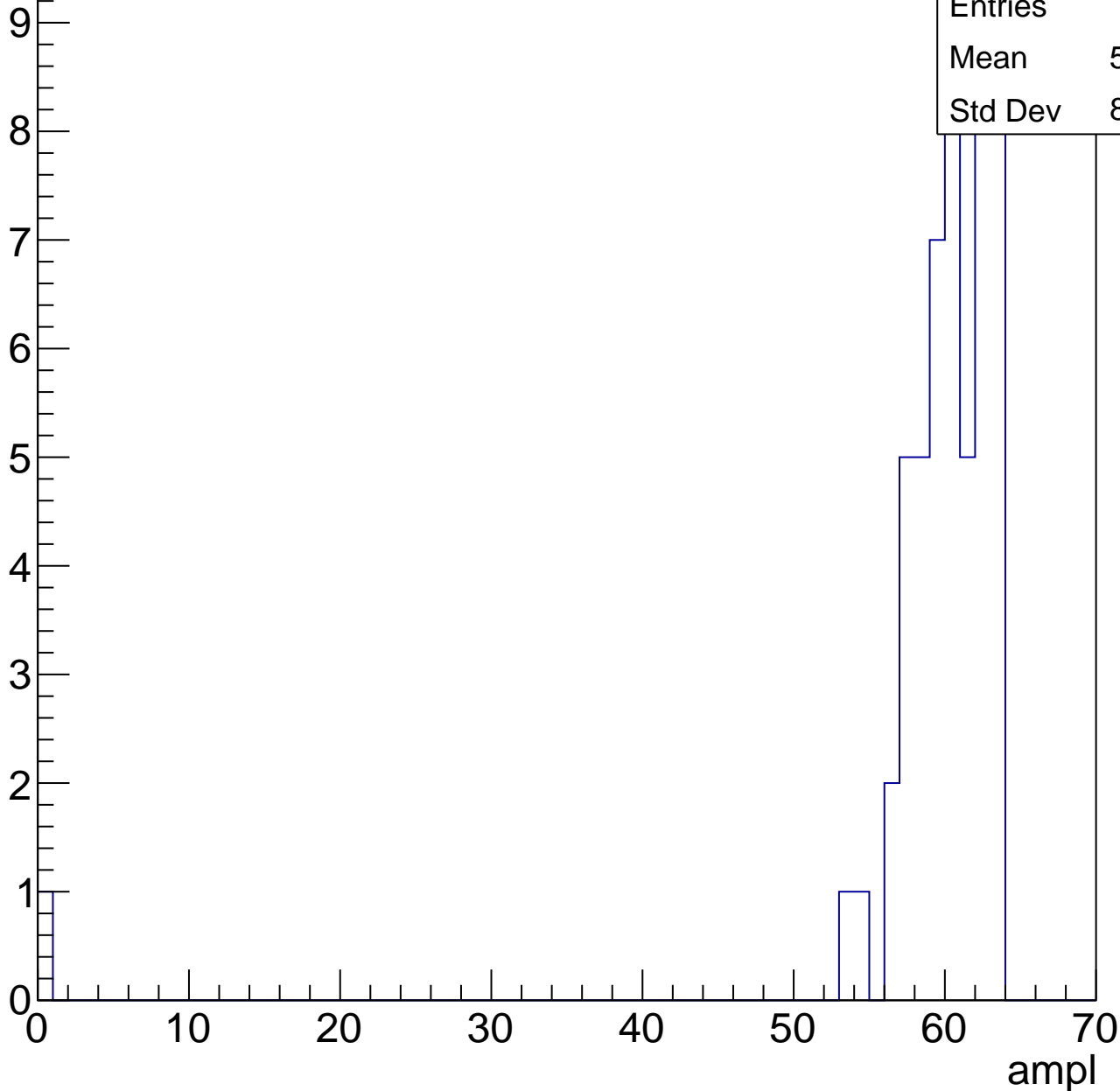


# B1L003S, U23-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

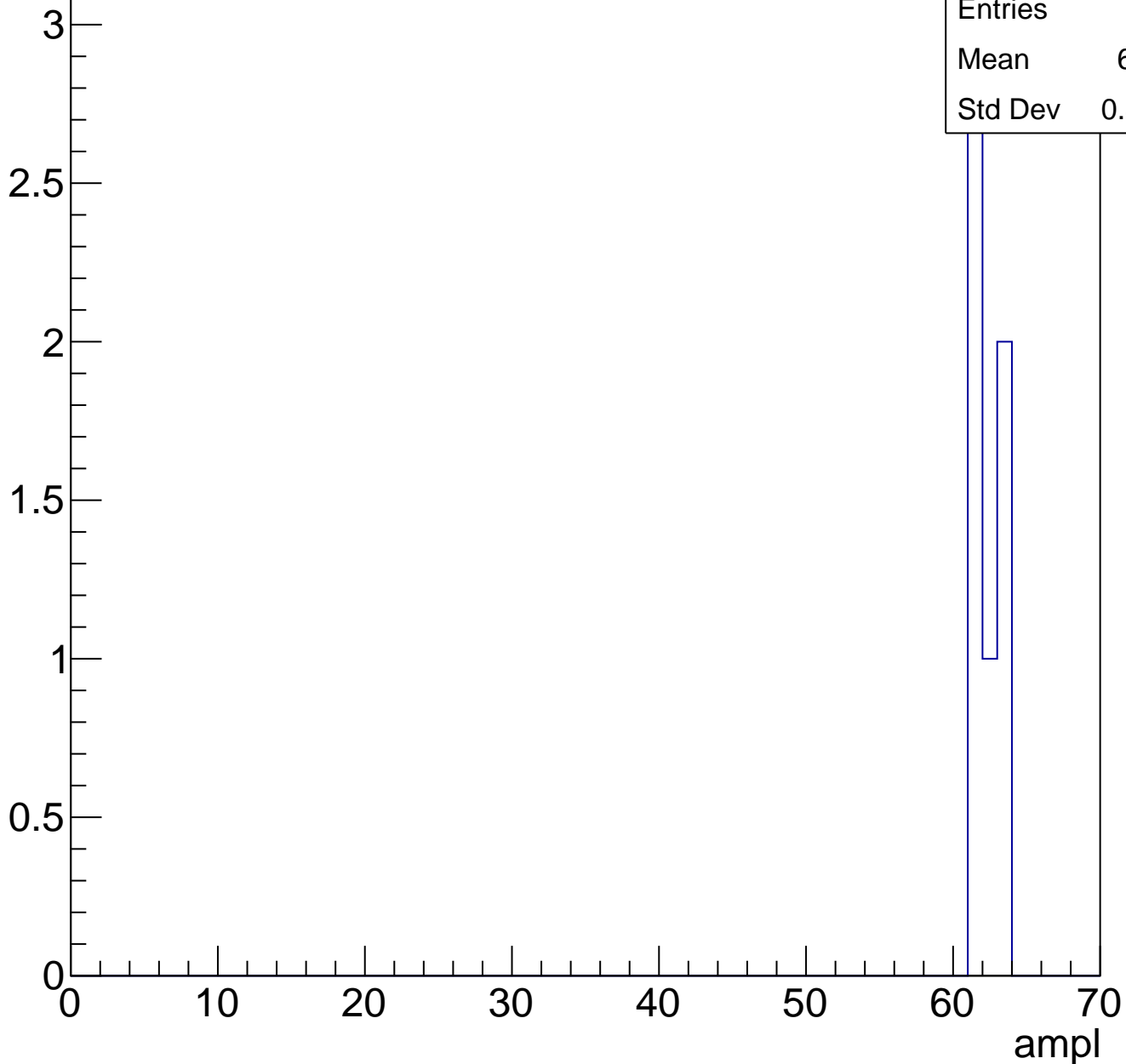
Entries	52
Mean	58.69
Std Dev	8.559



# B1L003S, U23-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch17, adc0

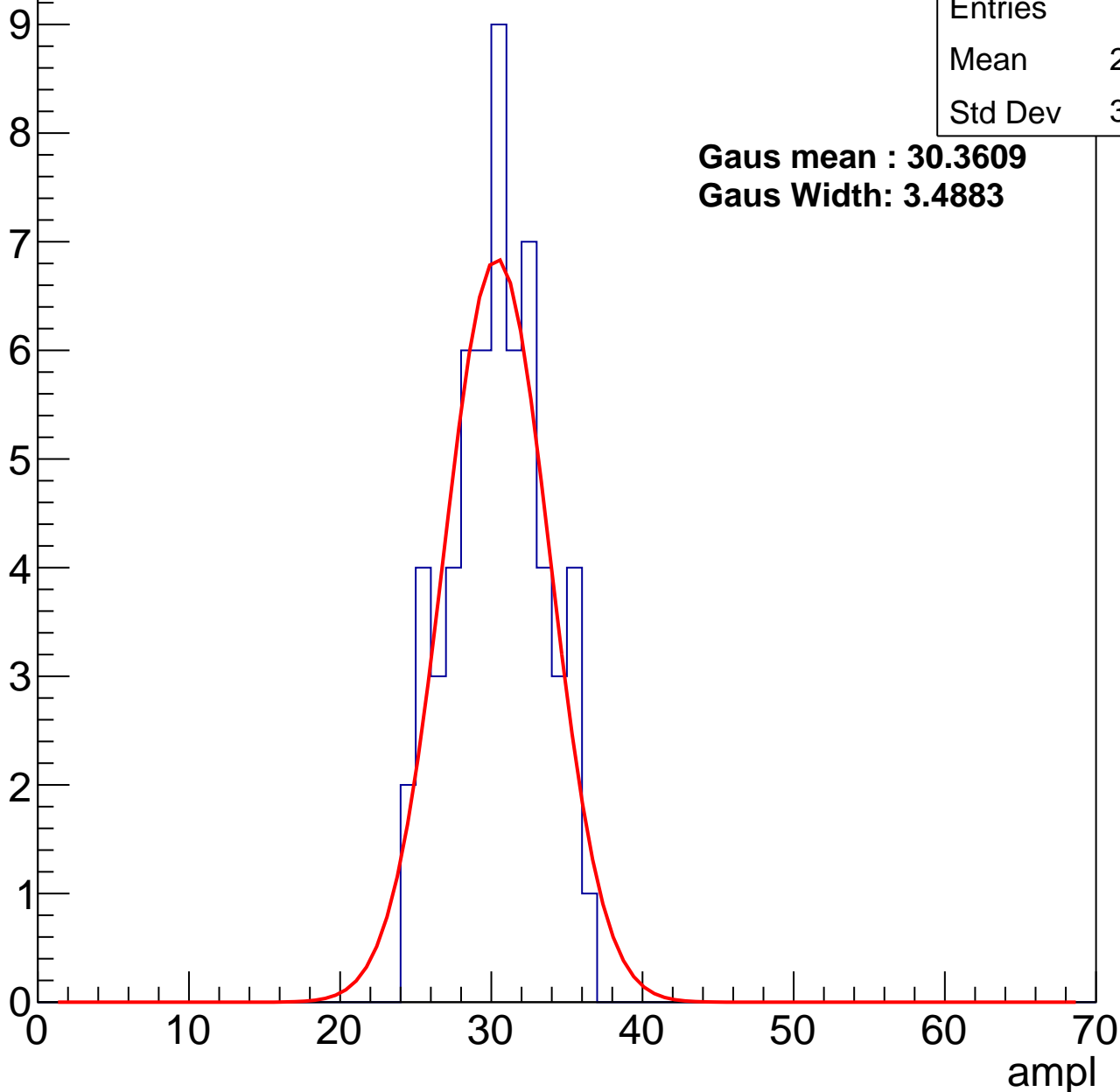
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.93
Std Dev	3.025

**Gaus mean : 30.3609**

**Gaus Width: 3.4883**



# B1L003S, U23-ch17, adc1

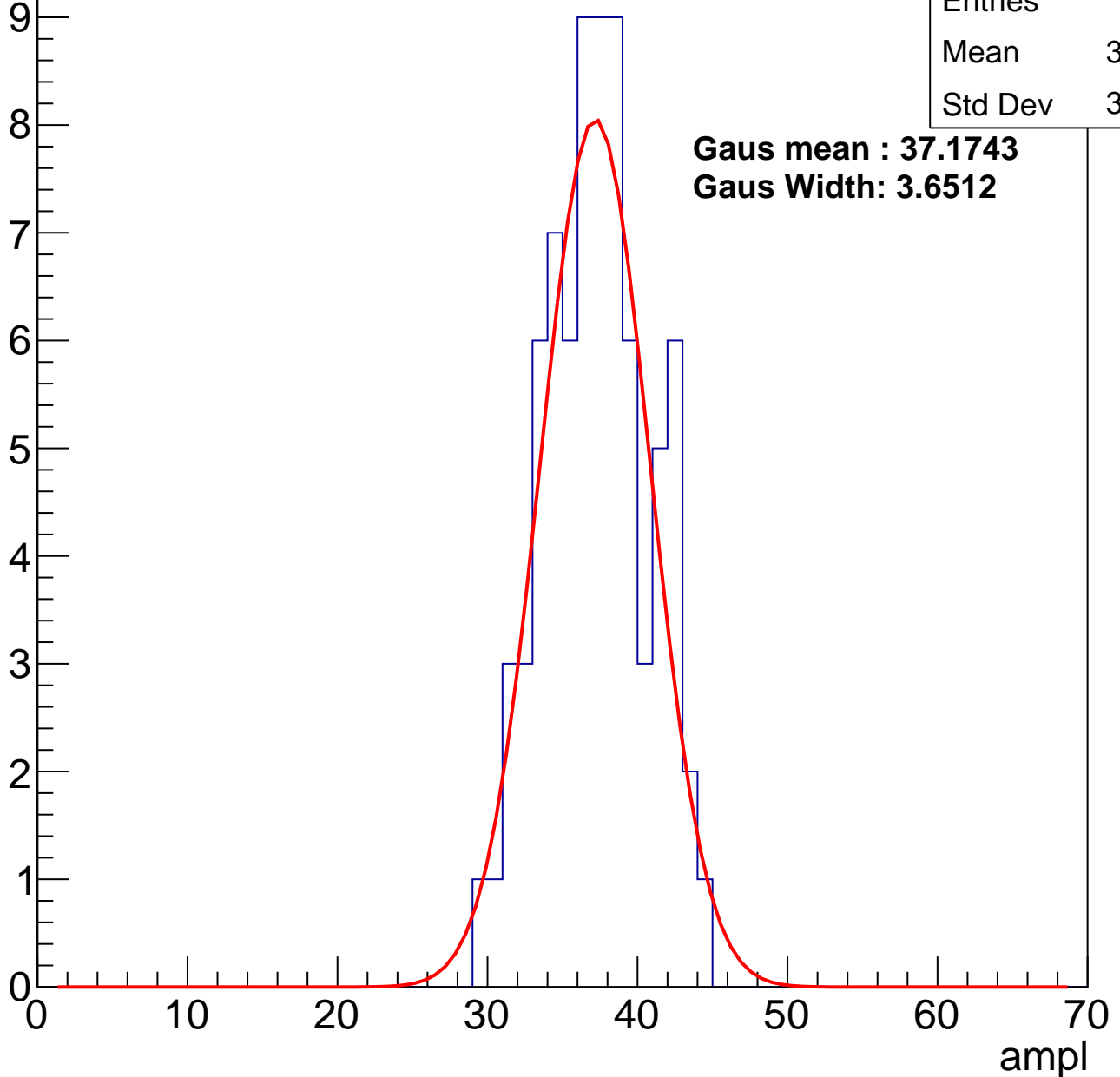
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	36.81
Std Dev	3.411

**Gaus mean : 37.1743**

**Gaus Width: 3.6512**



# B1L003S, U23-ch17, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	64
Mean	43.52
Std Dev	2.899

**Gaus mean : 44.1866**

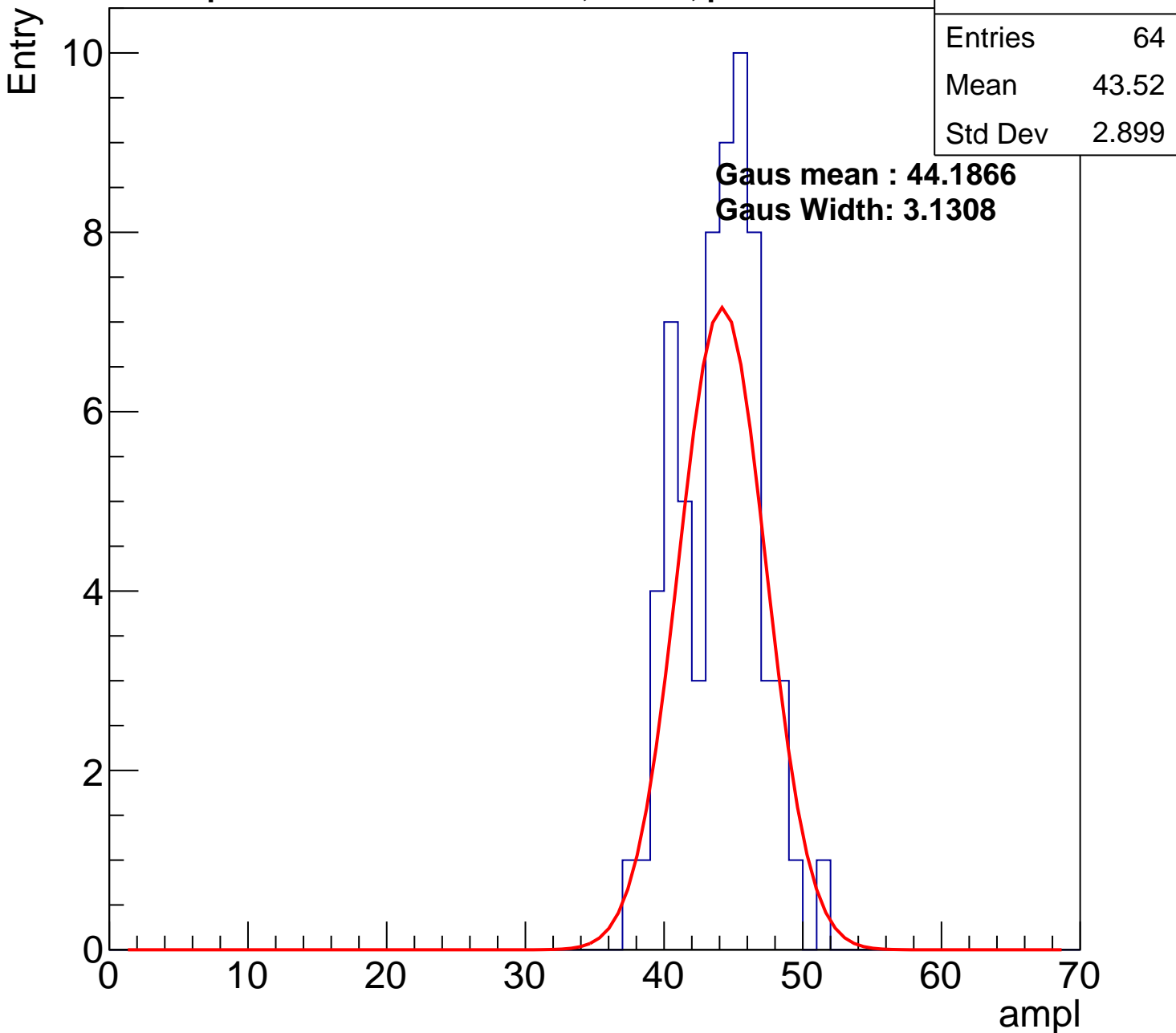
**Gaus Width: 3.1308**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

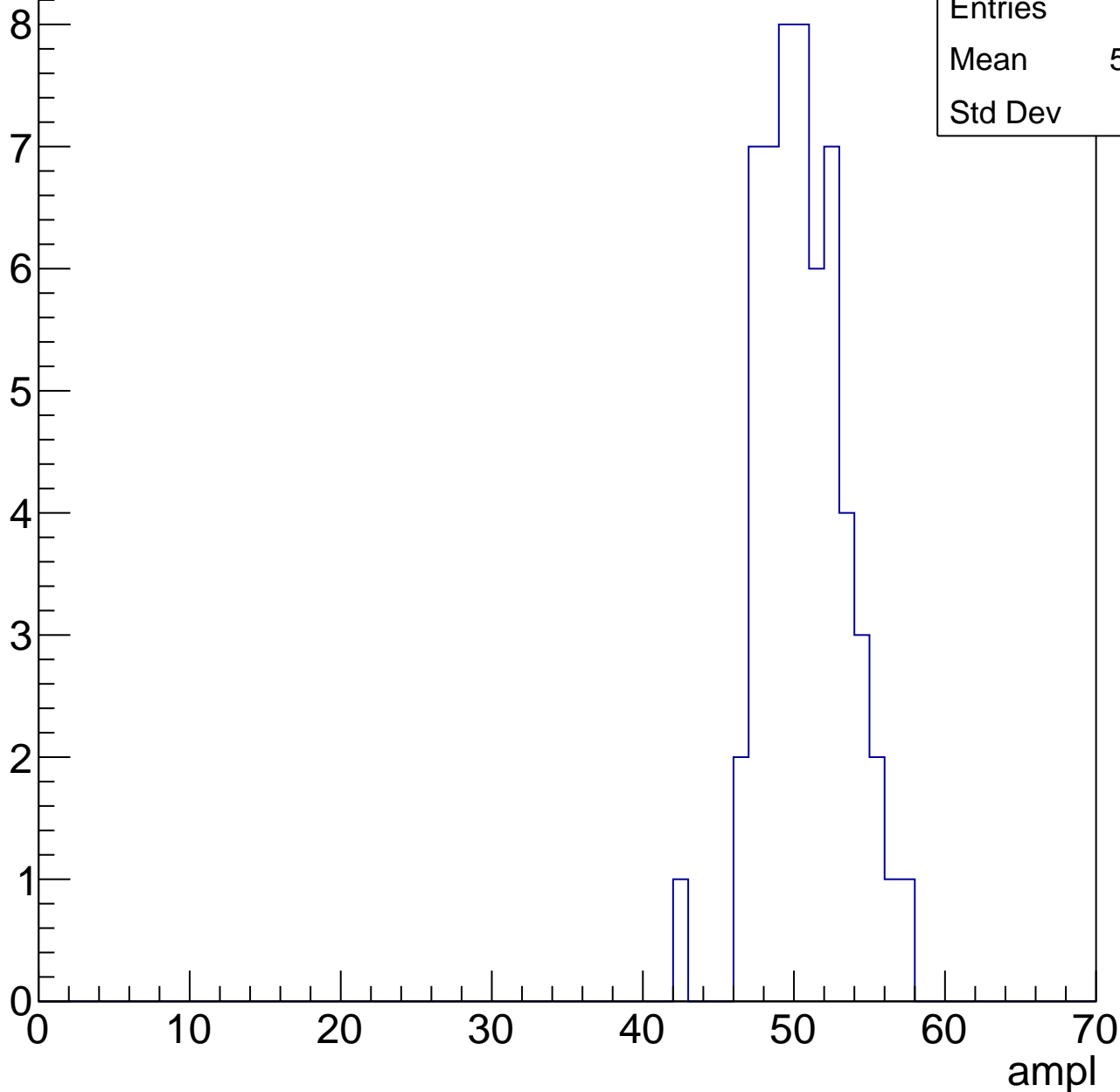


# B1L003S, U23-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	50.14
Std Dev	2.8

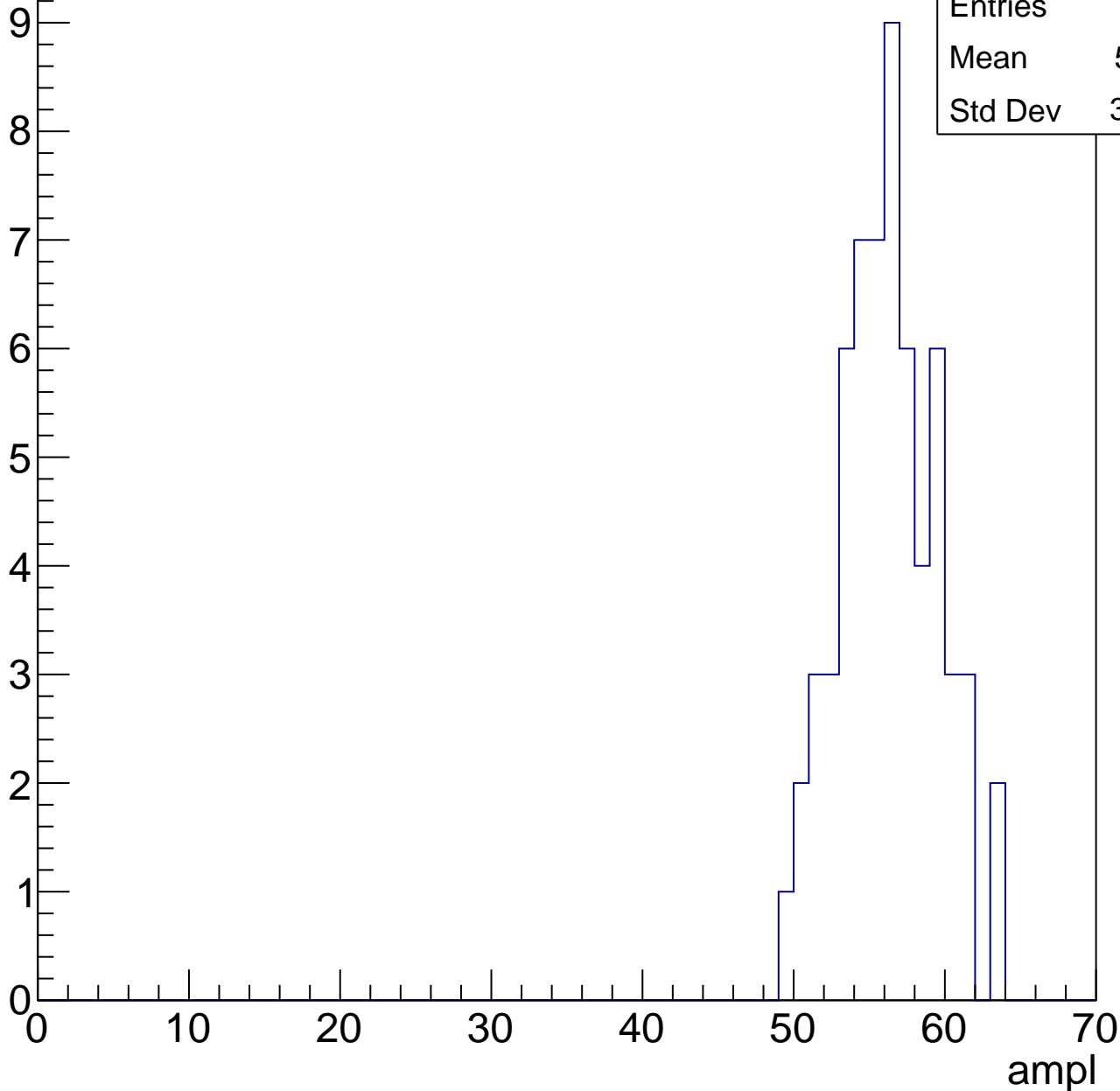


# B1L003S, U23-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	55.81
Std Dev	3.182



# B1L003S, U23-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	45
Mean	59.04
Std Dev	9.126

Entry

10

8

6

4

2

0

0

10

20

30

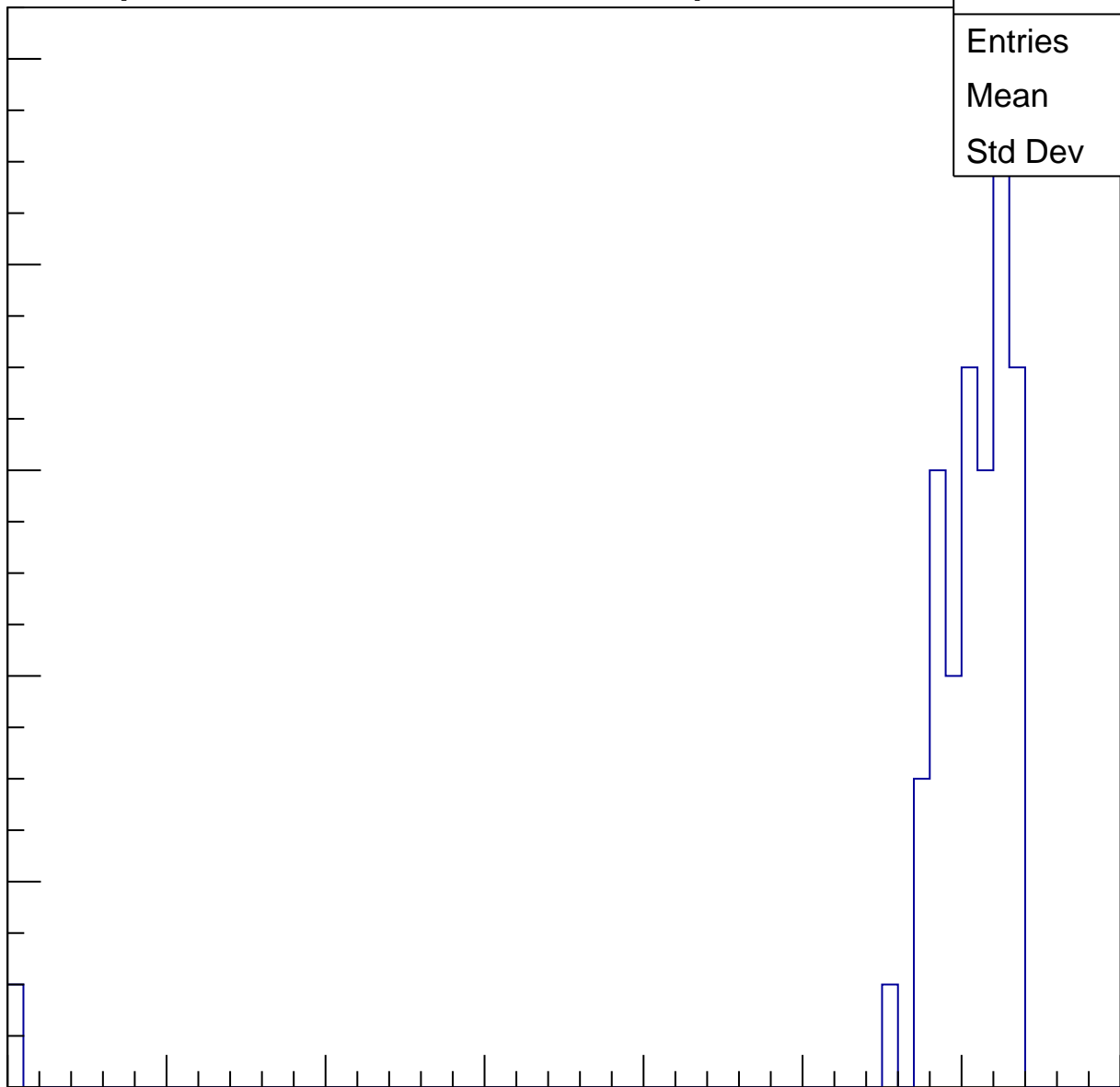
40

50

60

70

ampl



# B1L003S, U23-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch18, adc0

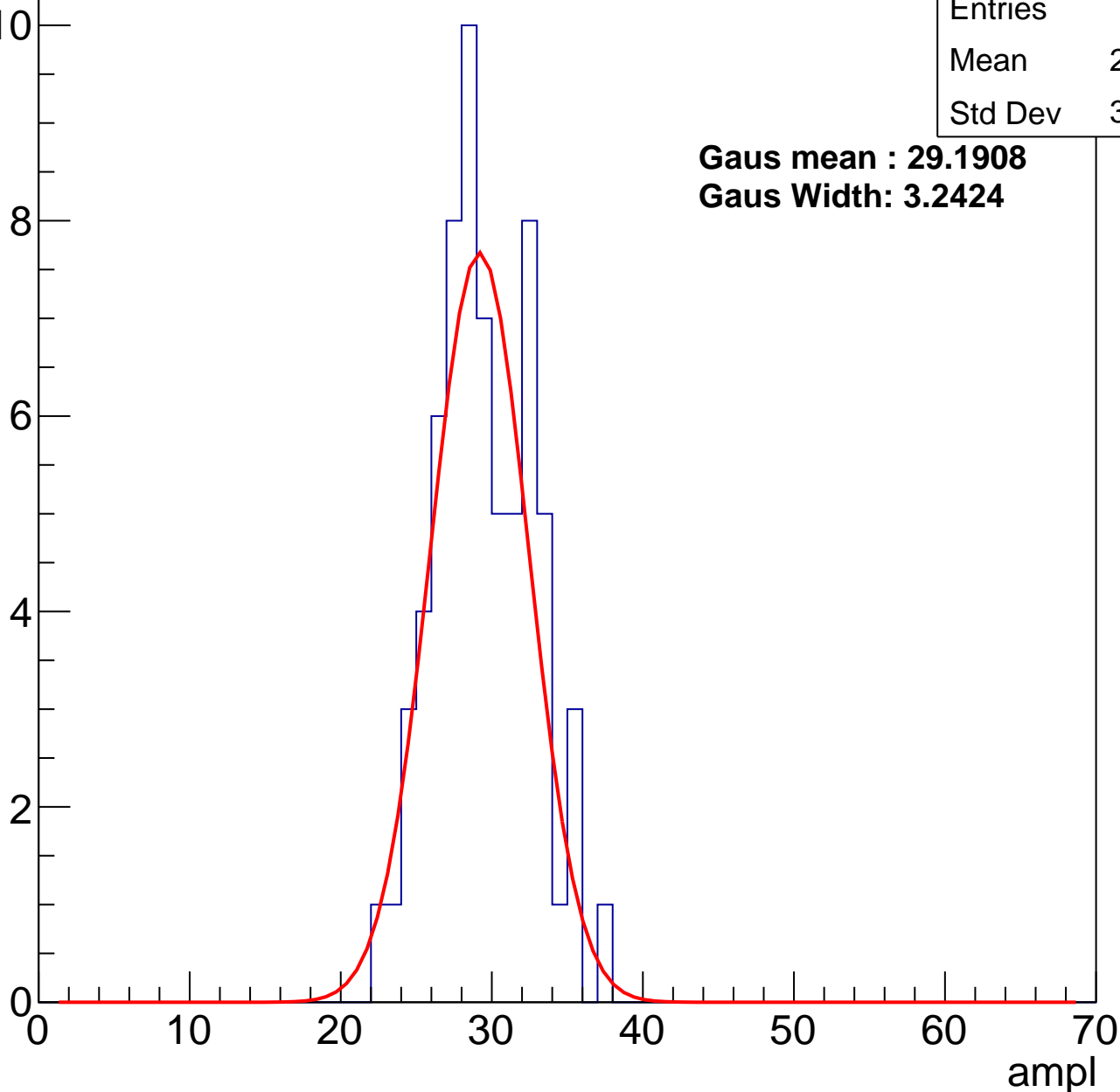
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.03
Std Dev	3.195

**Gaus mean : 29.1908**

**Gaus Width: 3.2424**



# B1L003S, U23-ch18, adc1

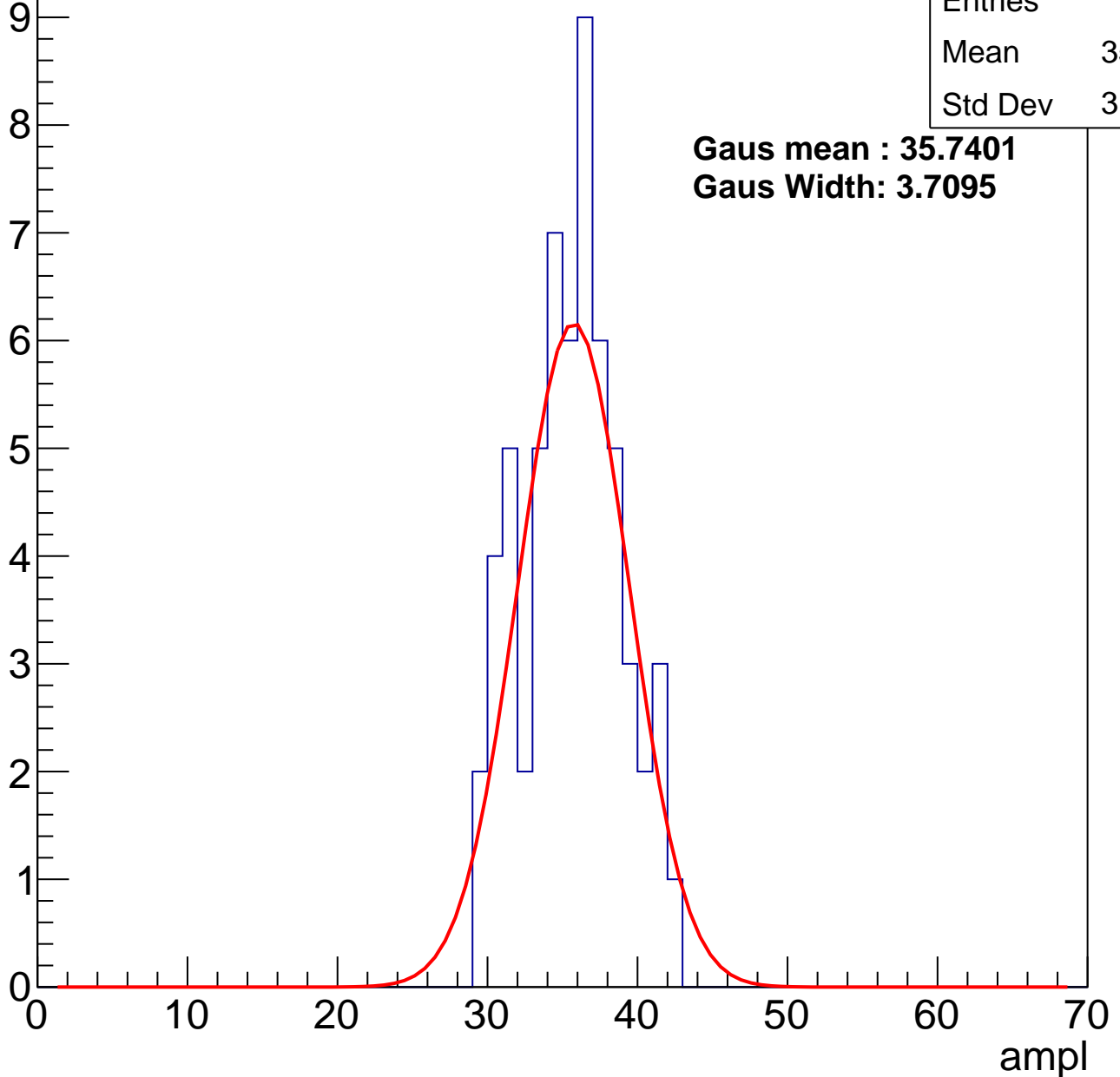
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.13
Std Dev	3.238

**Gaus mean : 35.7401**

**Gaus Width: 3.7095**



# B1L003S, U23-ch18, adc2

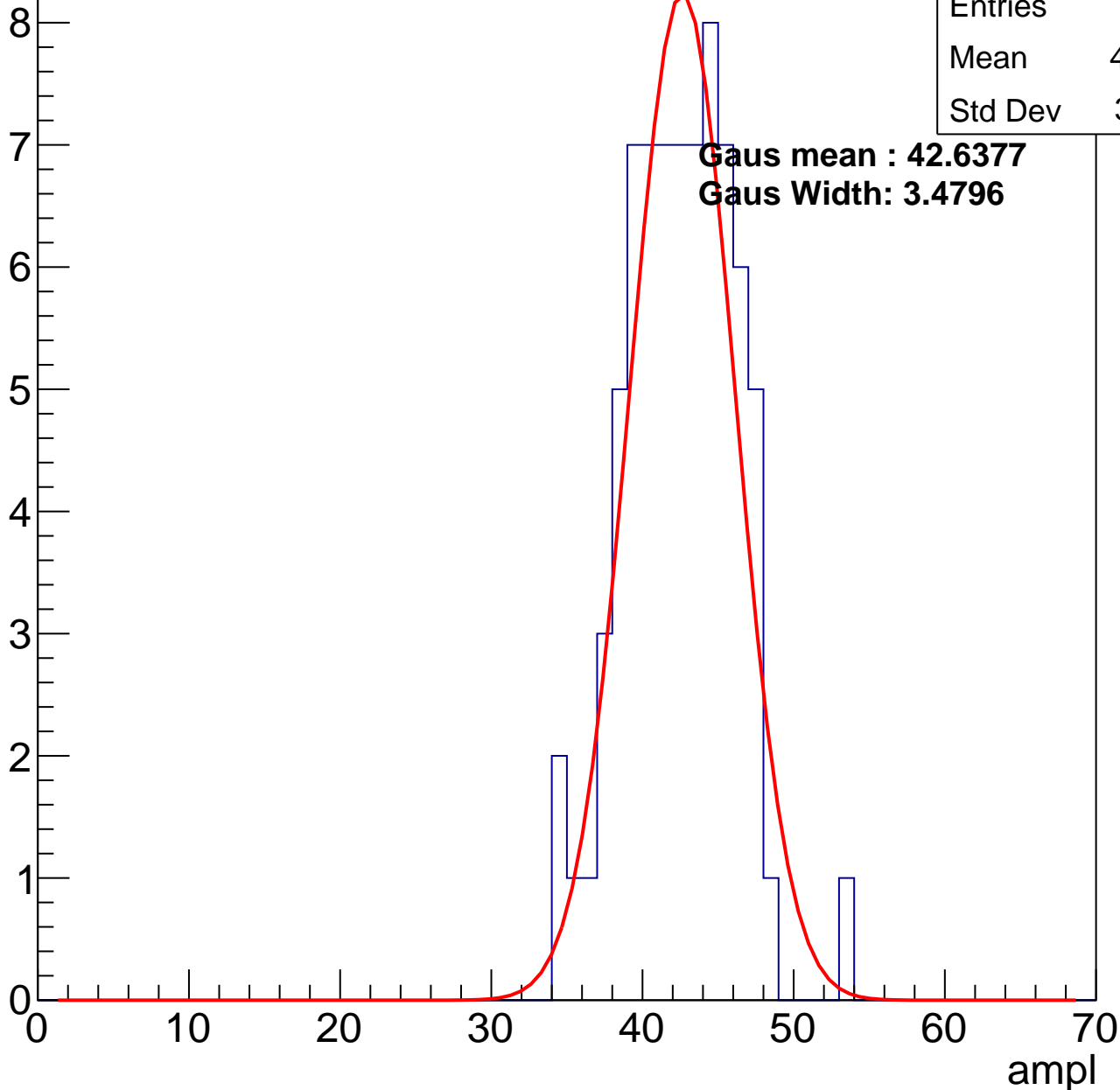
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	42.05
Std Dev	3.551

**Gaus mean : 42.6377**

**Gaus Width: 3.4796**

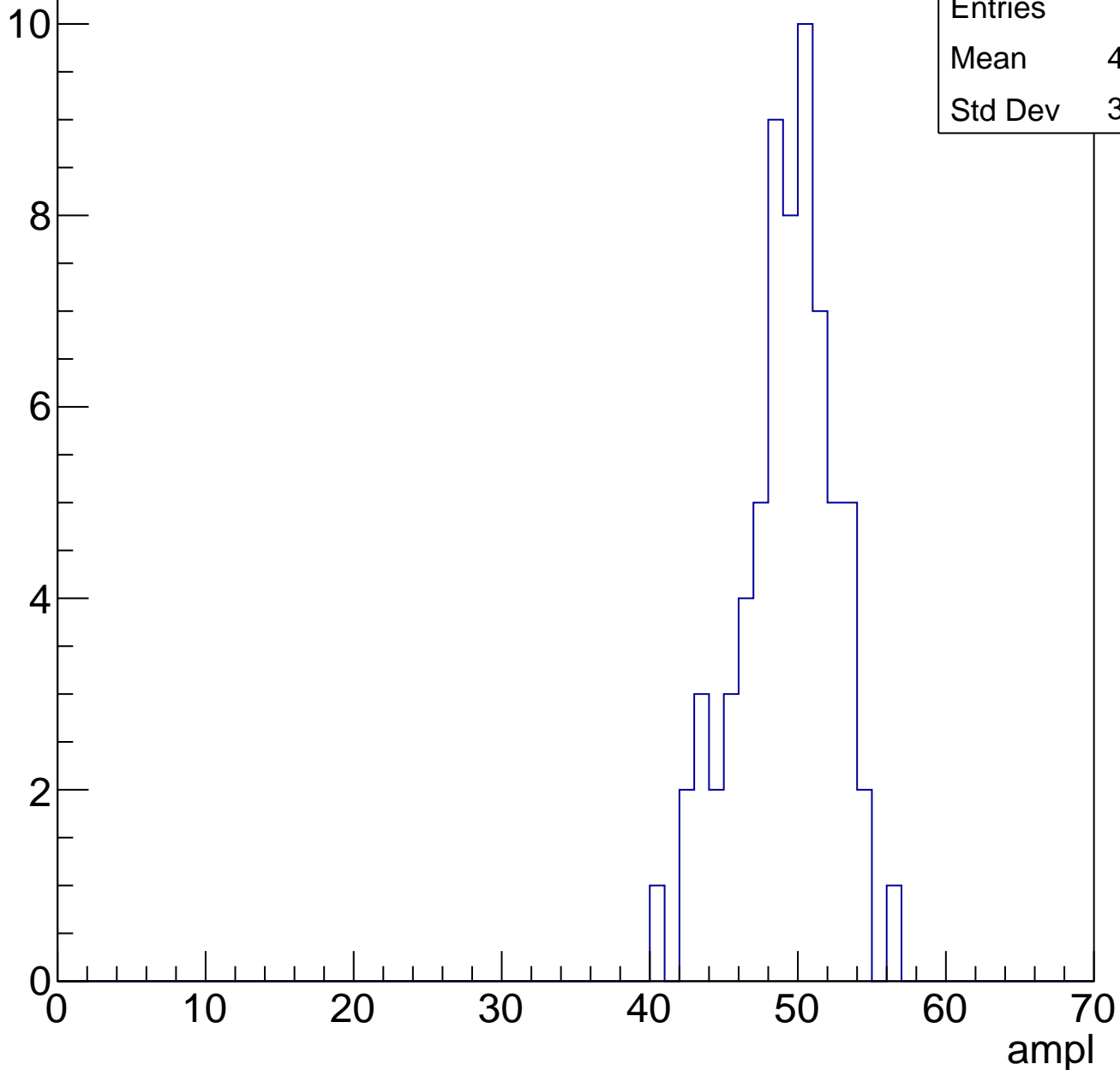


# B1L003S, U23-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	48.73
Std Dev	3.249

Entry



# B1L003S, U23-ch18, adc4

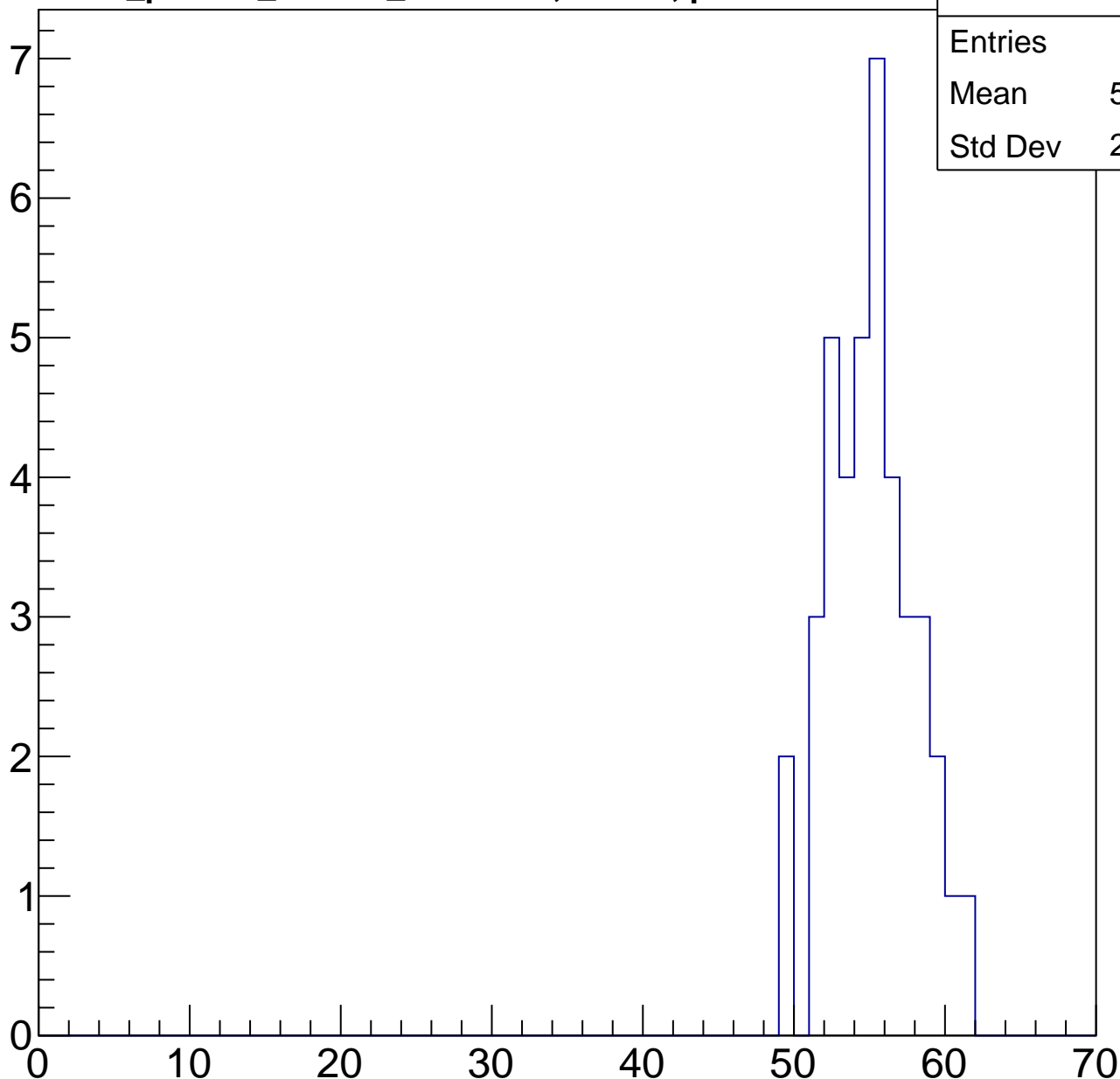
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	54.65
Std Dev	2.816

ampl

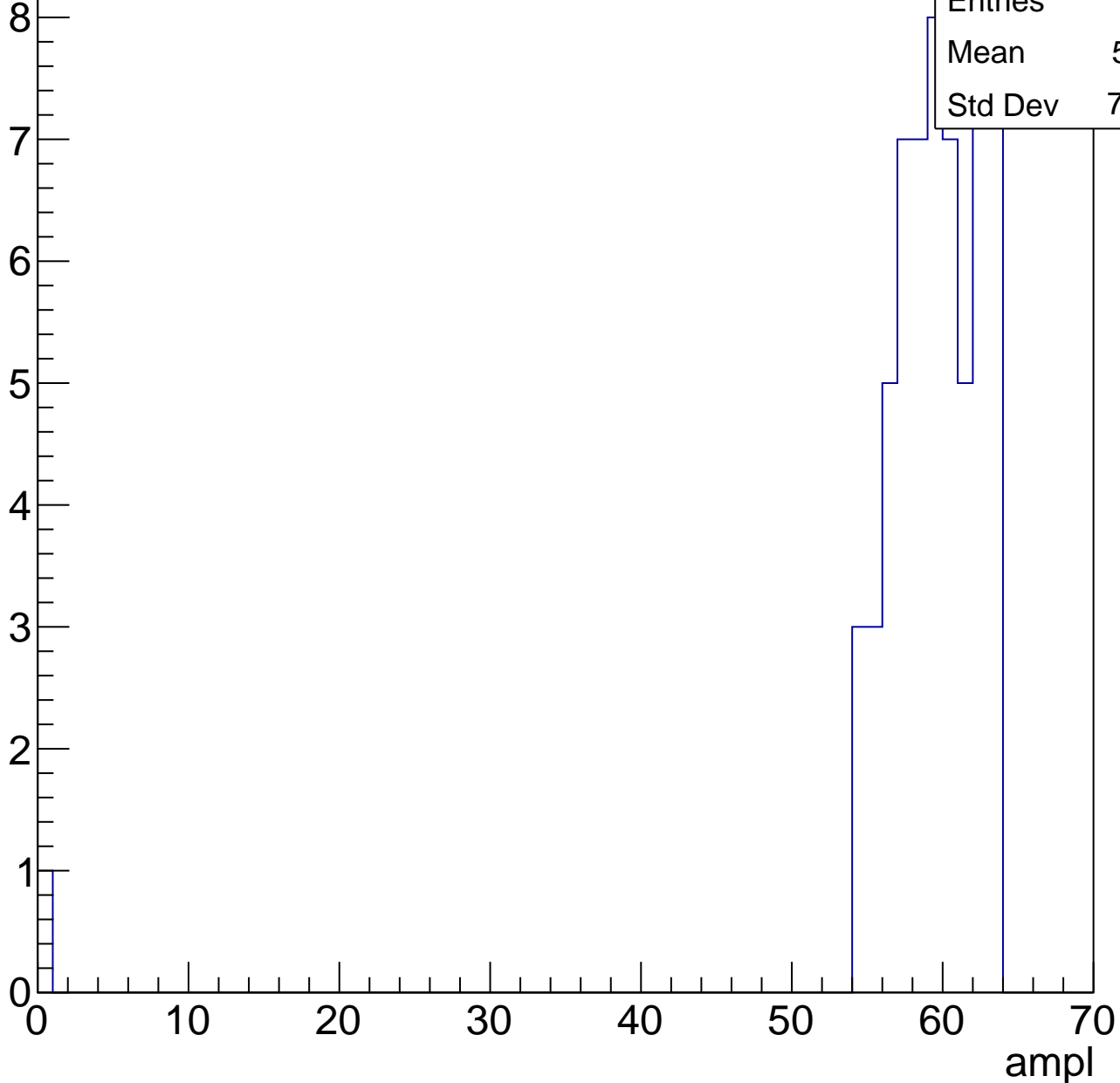


# B1L003S, U23-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

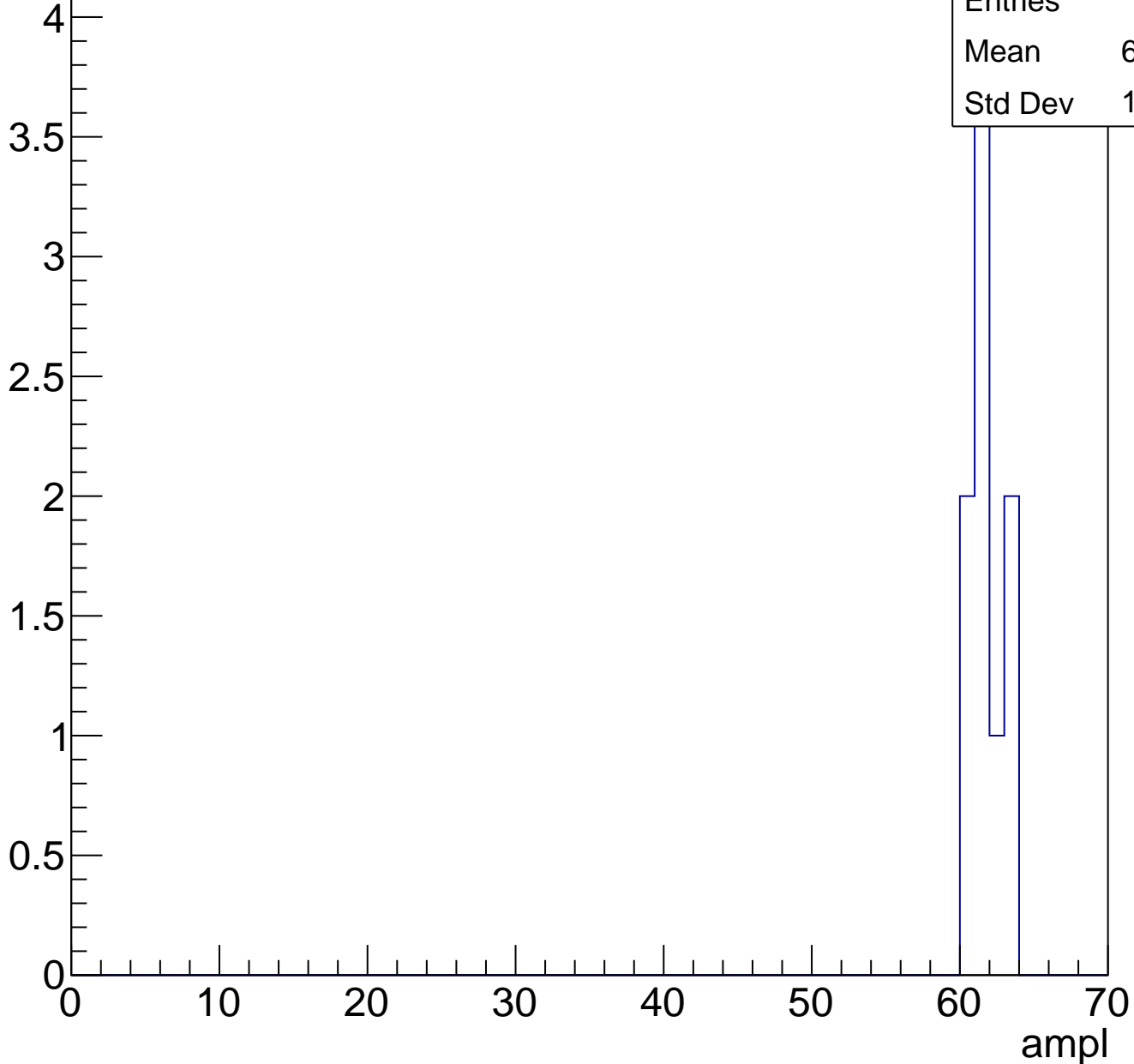
Entries	62
Mean	58.21
Std Dev	7.903



# B1L003S, U23-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

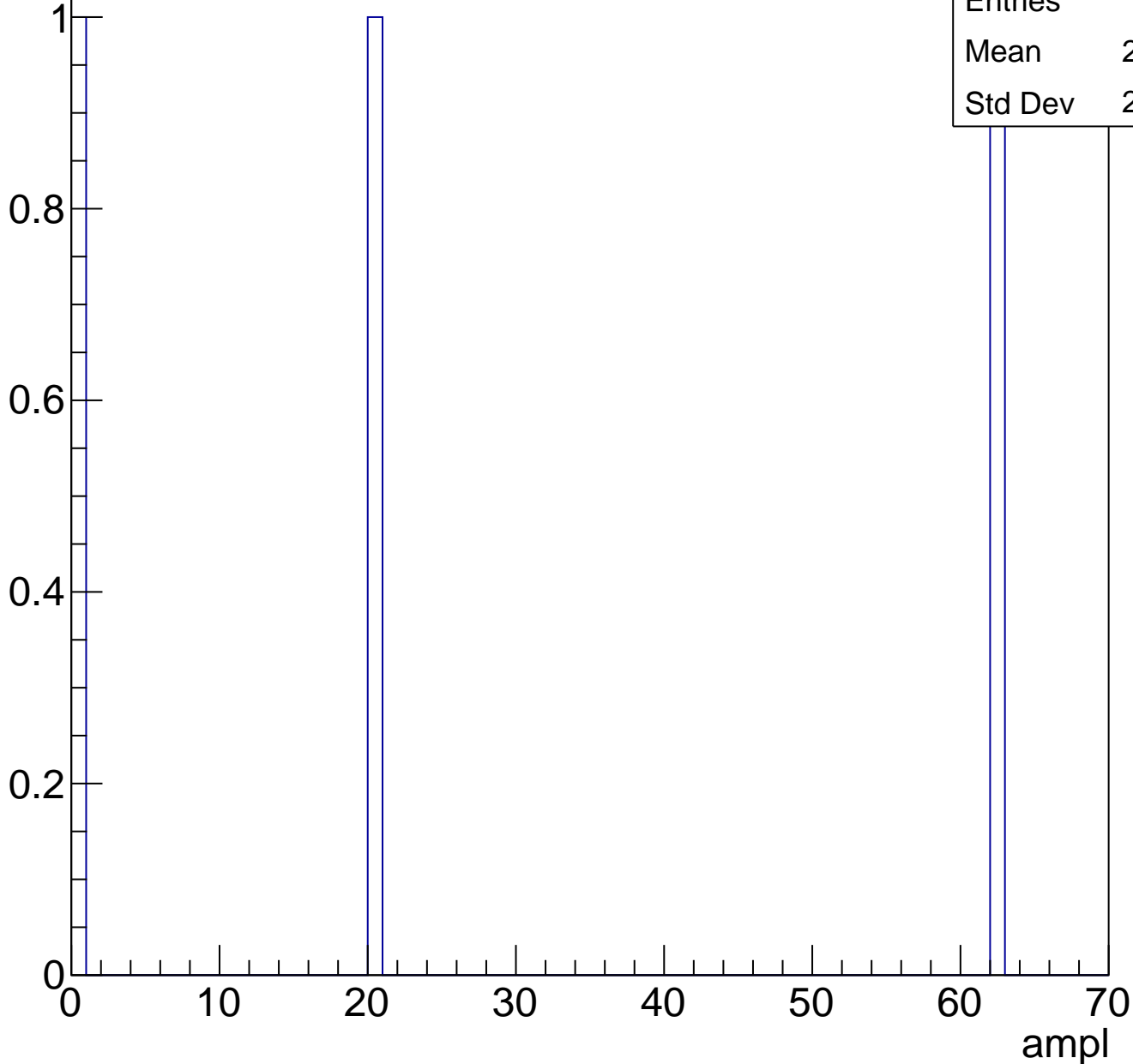




# B1L003S, U23-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	27.33
Std Dev	25.84

# B1L003S, U23-ch19, adc0

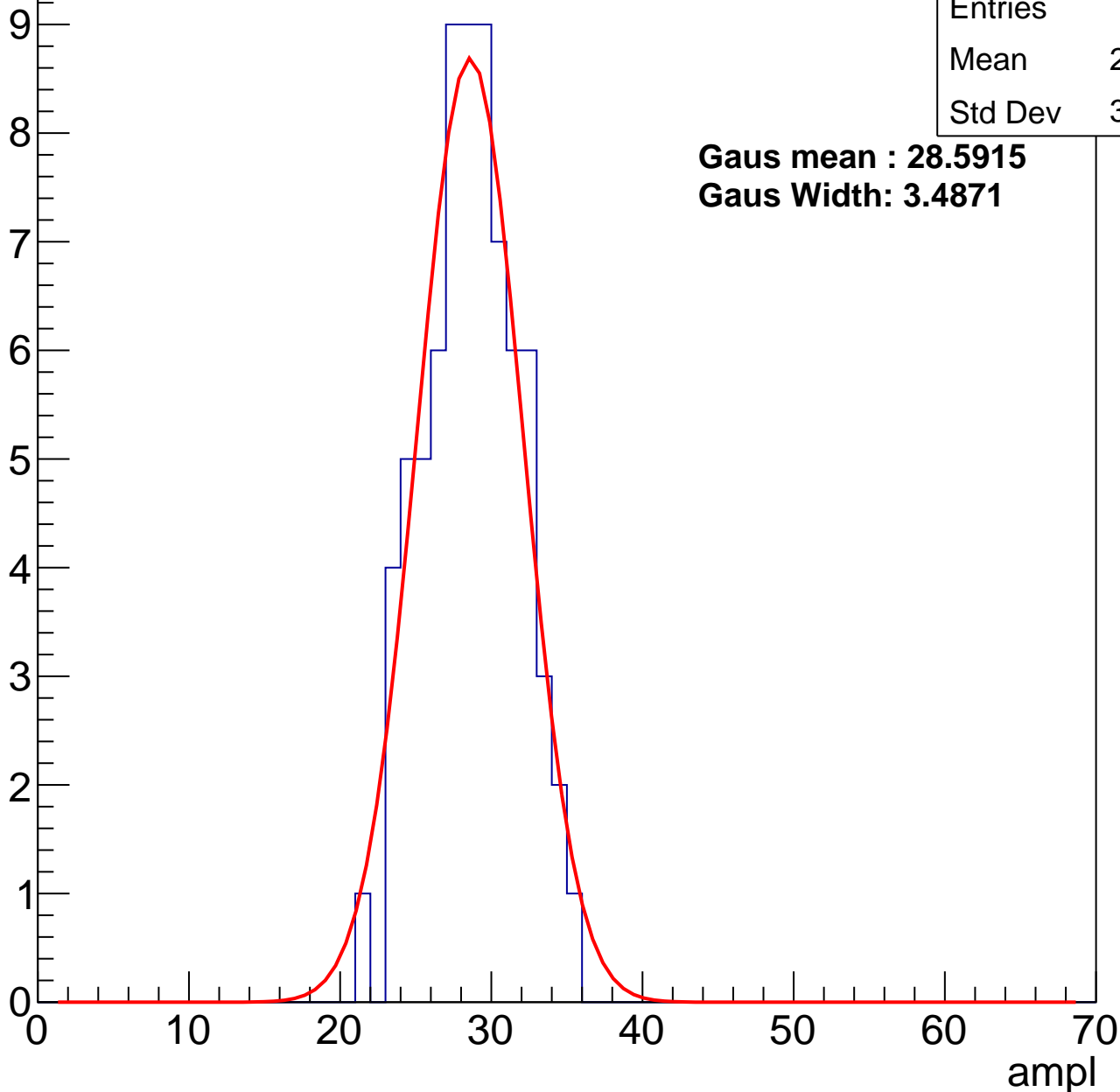
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	28.22
Std Dev	3.067

**Gaus mean : 28.5915**

**Gaus Width: 3.4871**



# B1L003S, U23-ch19, adc1

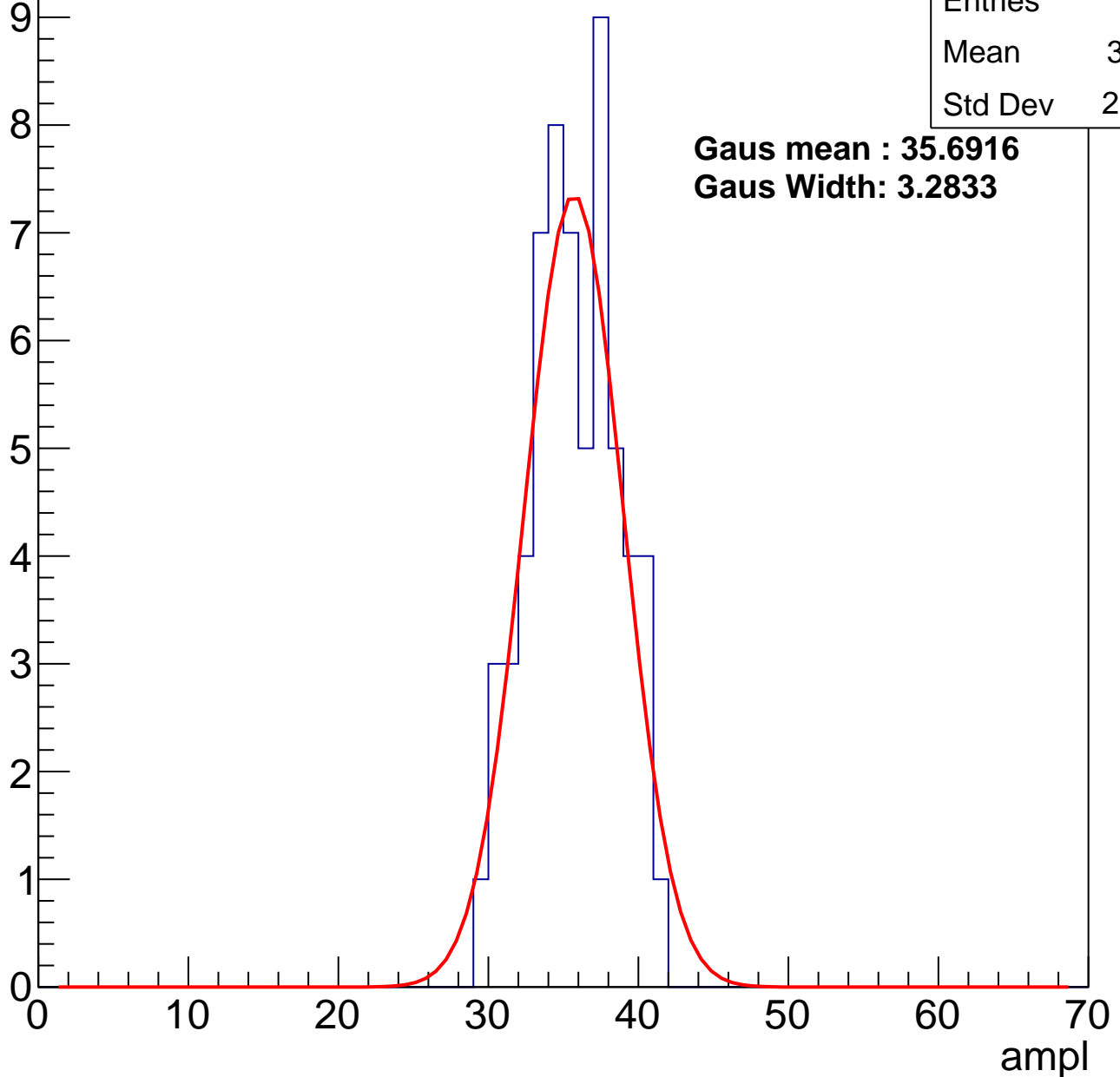
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.21
Std Dev	2.903

**Gaus mean : 35.6916**

**Gaus Width: 3.2833**



# B1L003S, U23-ch19, adc2

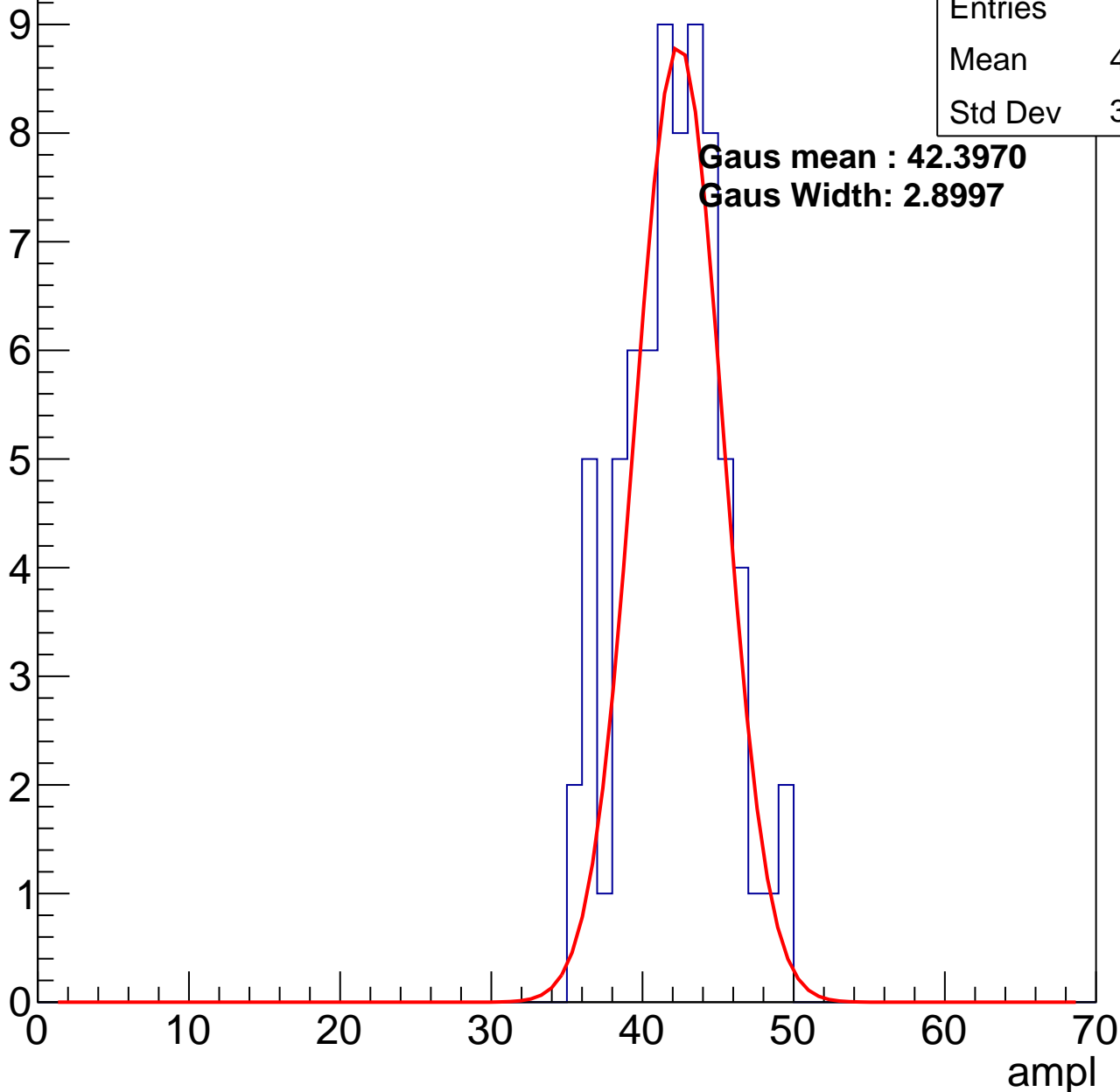
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	41.62
Std Dev	3.268

**Gaus mean : 42.3970**

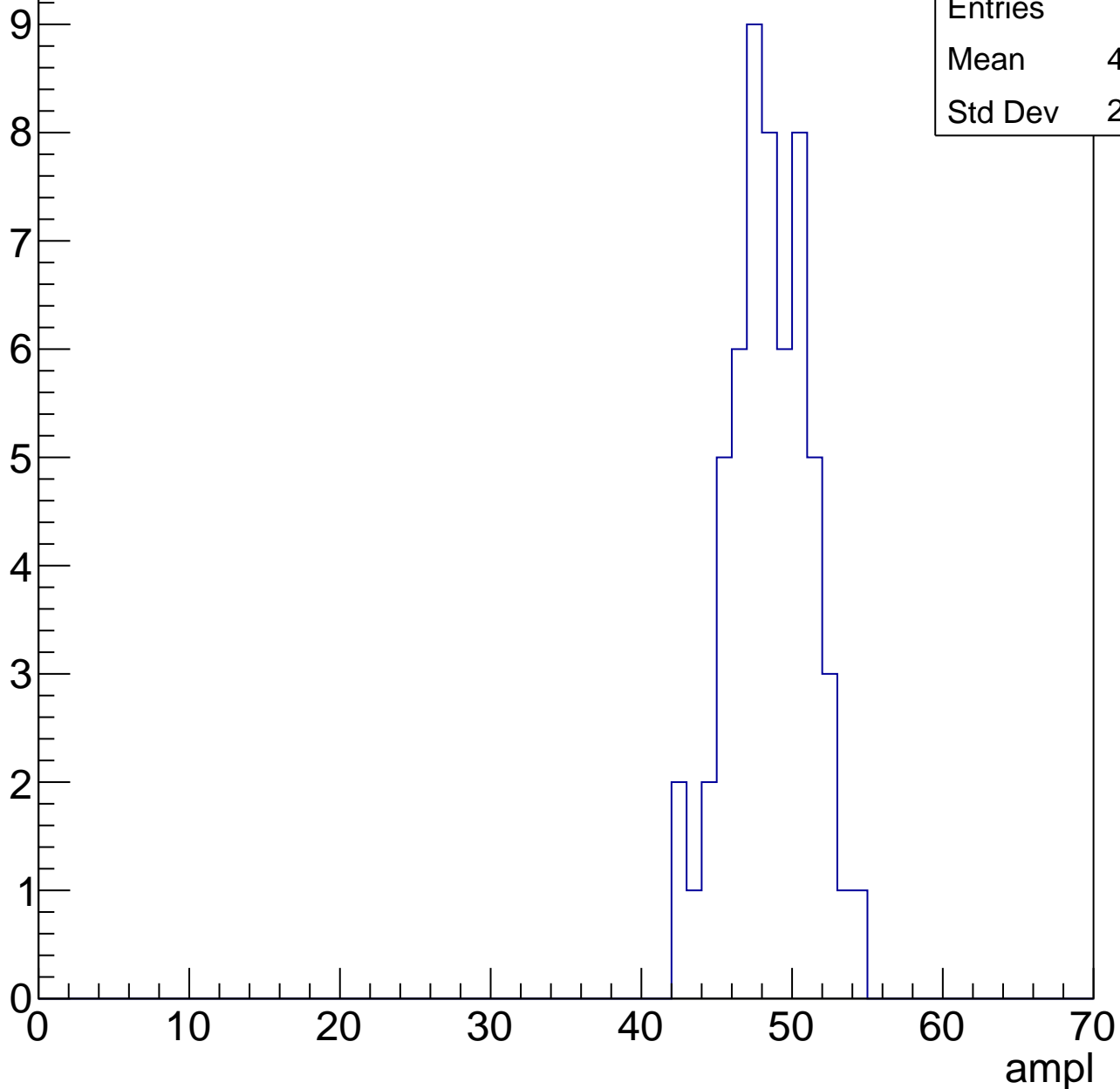
**Gaus Width: 2.8997**



# B1L003S, U23-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

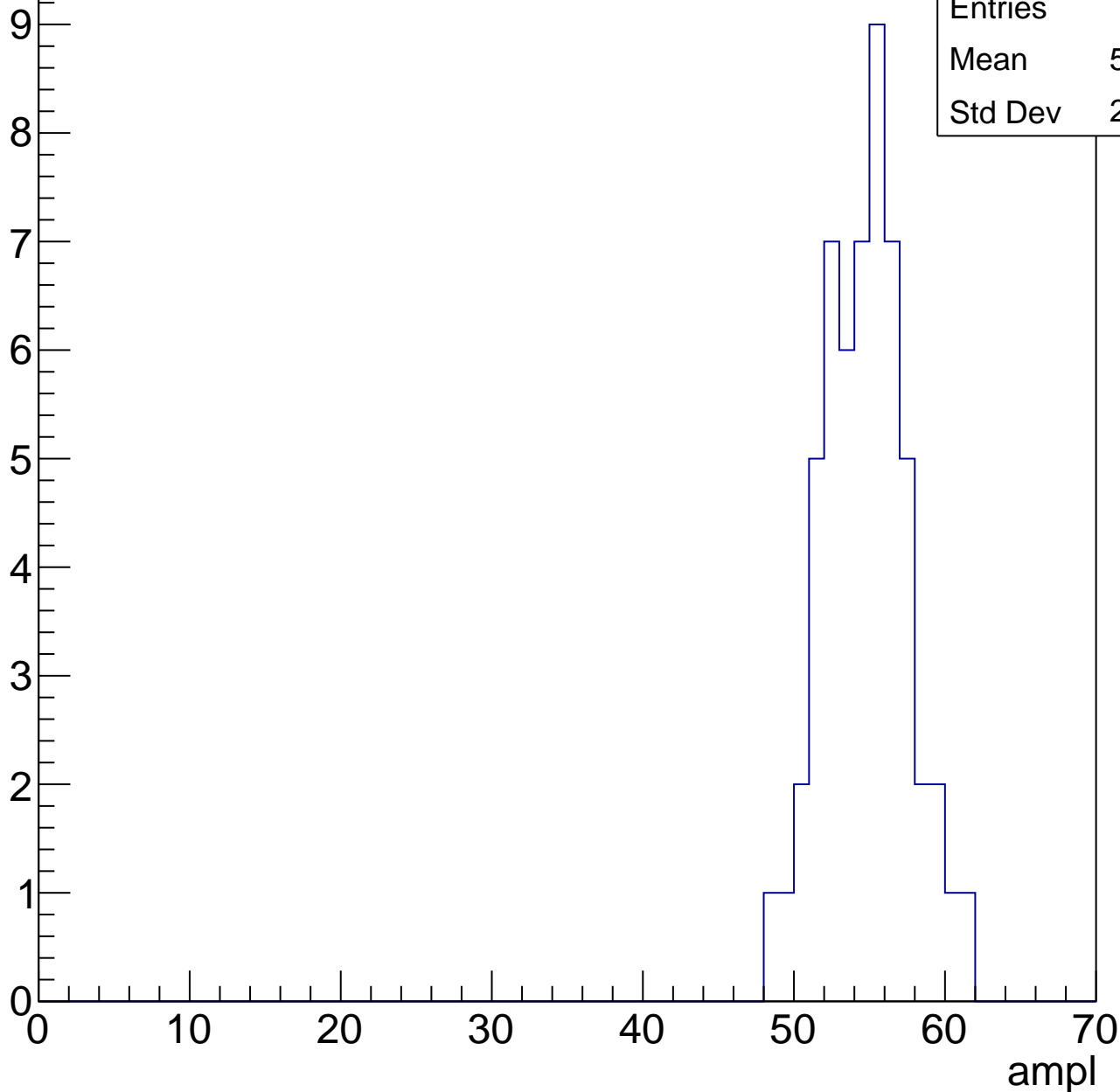


Entries	57
Mean	47.98
Std Dev	2.646

# B1L003S, U23-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



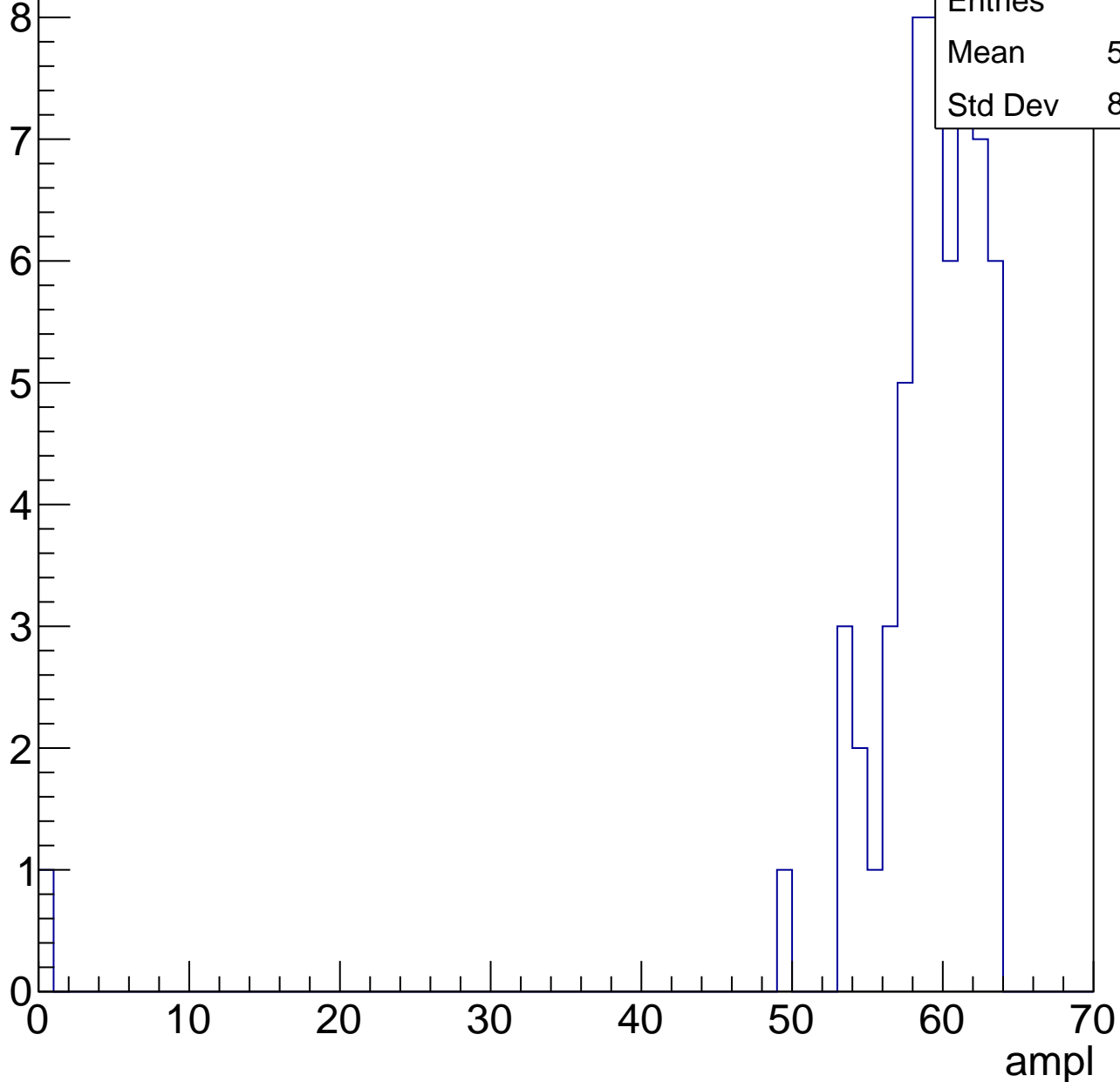
Entries	56
Mean	54.27
Std Dev	2.729

# B1L003S, U23-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

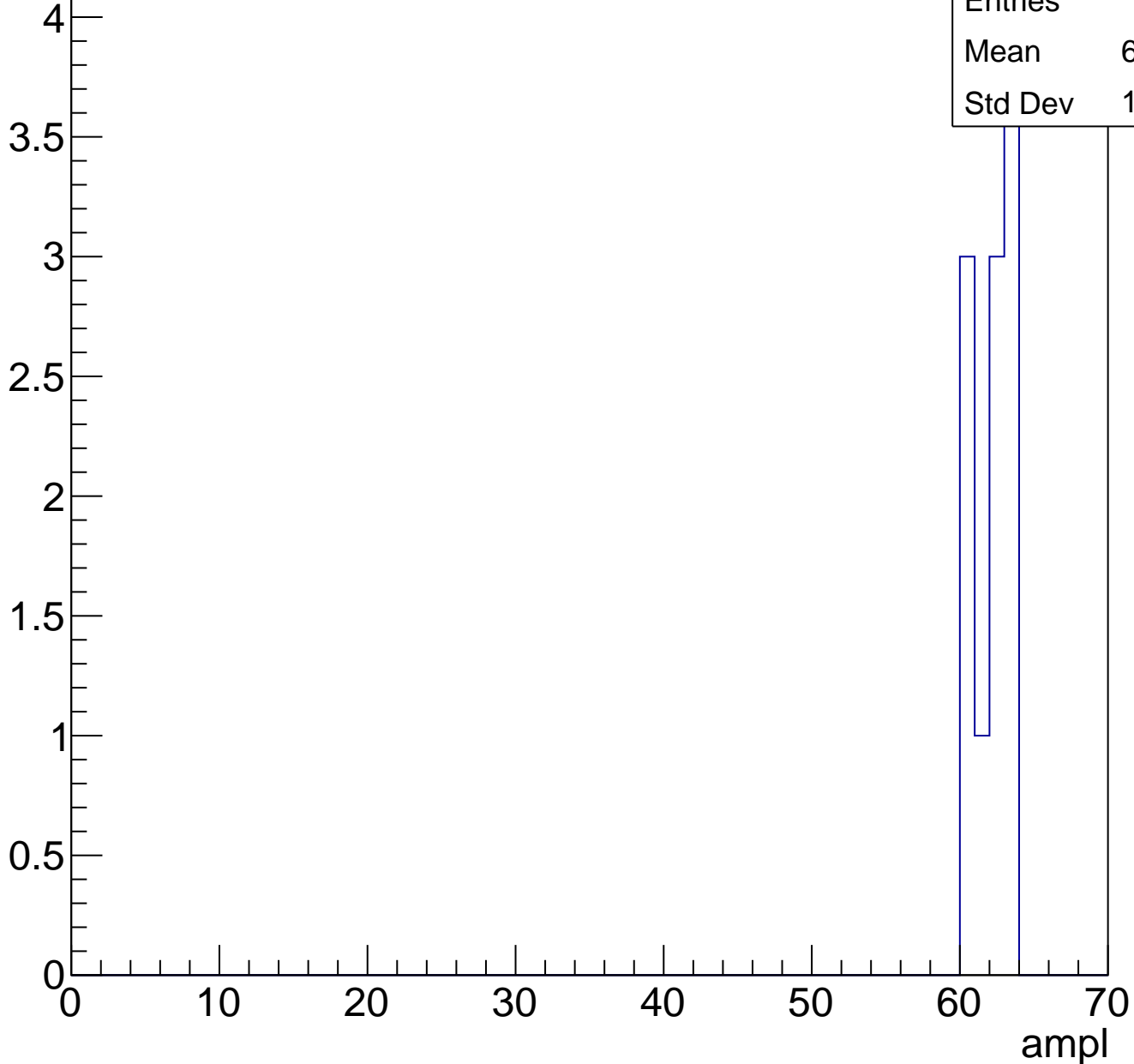
Entries	59
Mean	57.97
Std Dev	8.176



# B1L003S, U23-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch20, adc0

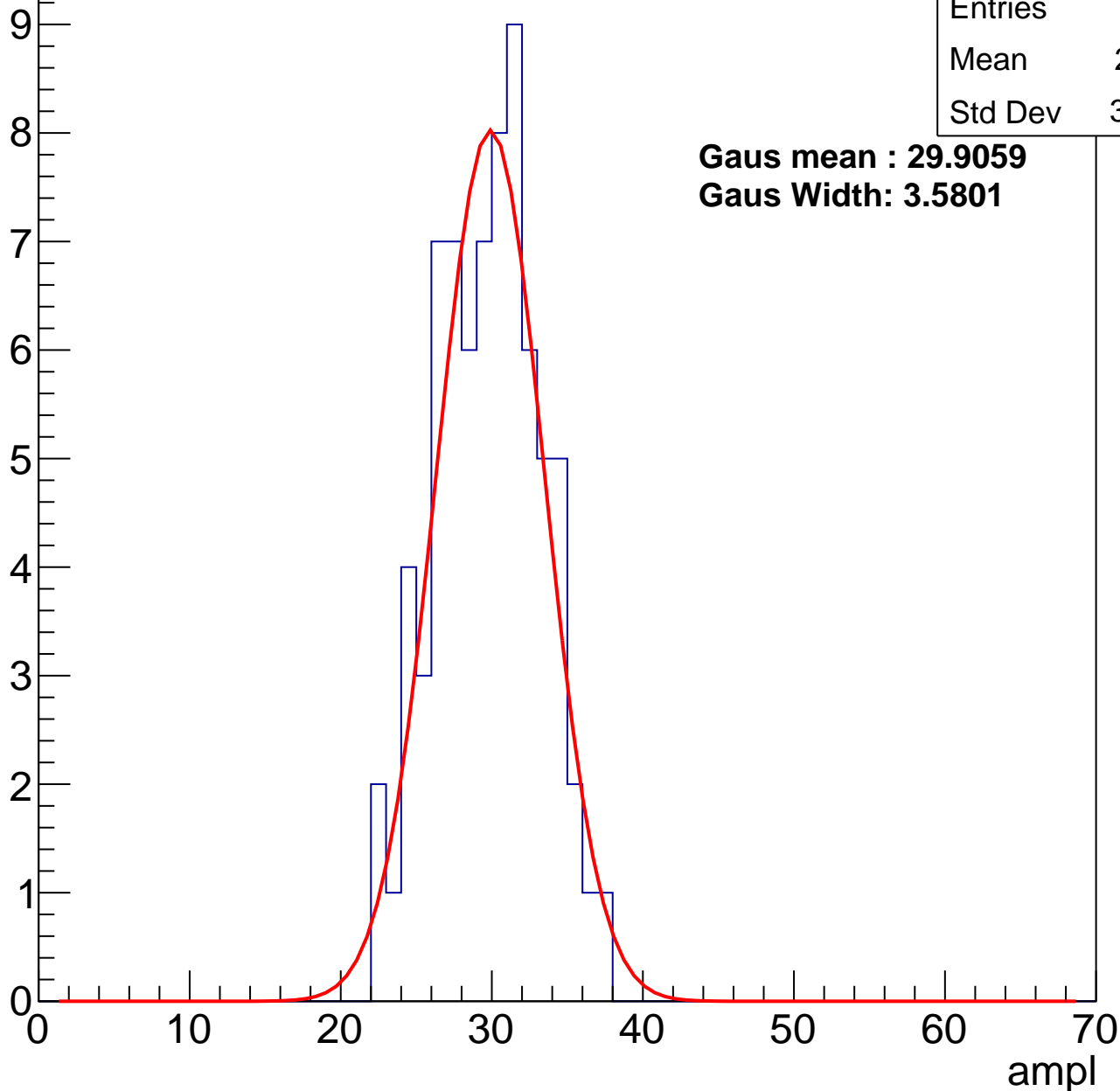
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	29.31
Std Dev	3.409

**Gaus mean : 29.9059**

**Gaus Width: 3.5801**



# B1L003S, U23-ch20, adc1

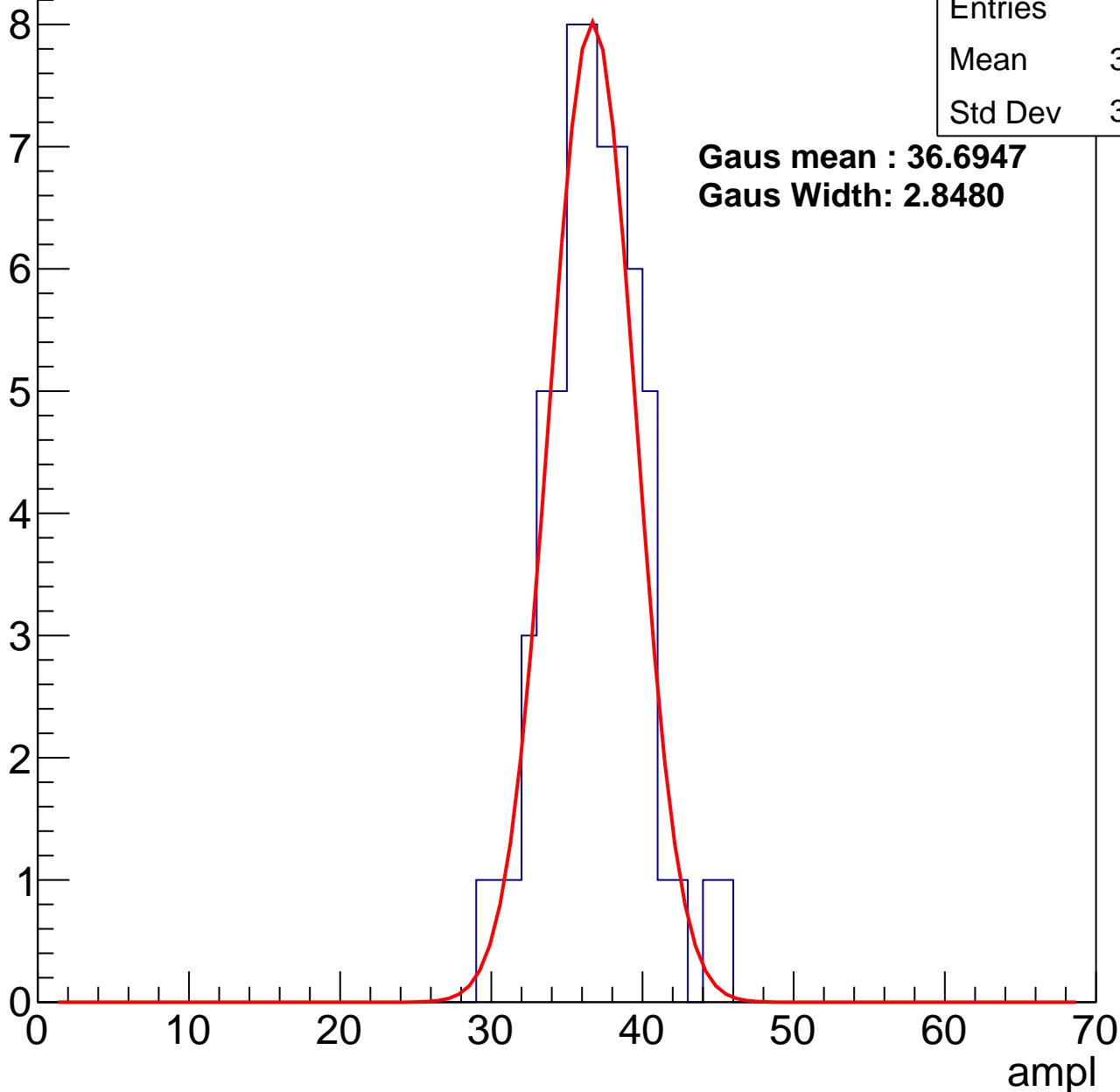
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.39
Std Dev	3.127

**Gaus mean : 36.6947**

**Gaus Width: 2.8480**



# B1L003S, U23-ch20, adc2

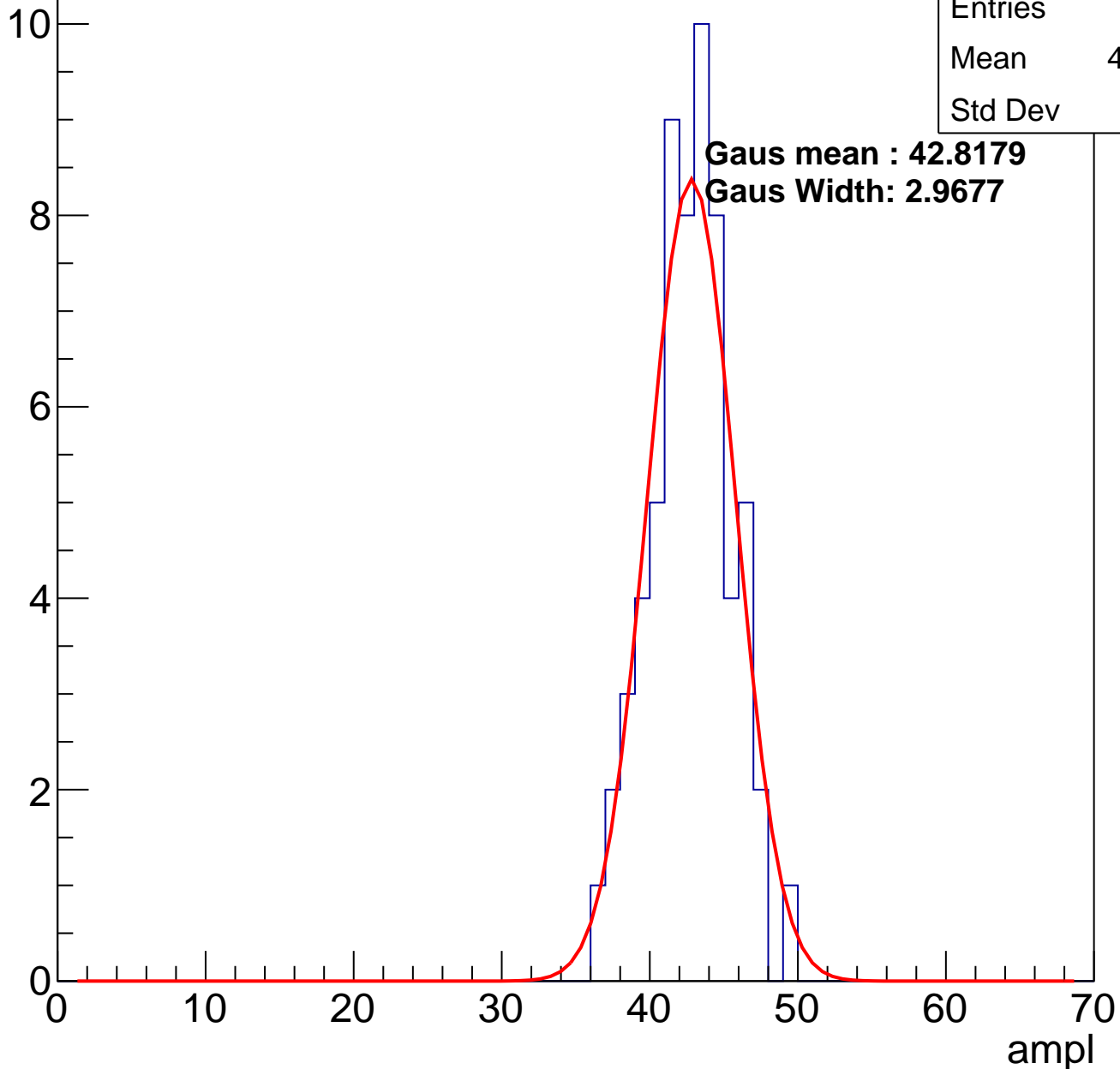
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	62
Mean	42.26
Std Dev	2.7

**Gaus mean : 42.8179**

**Gaus Width: 2.9677**

Entry

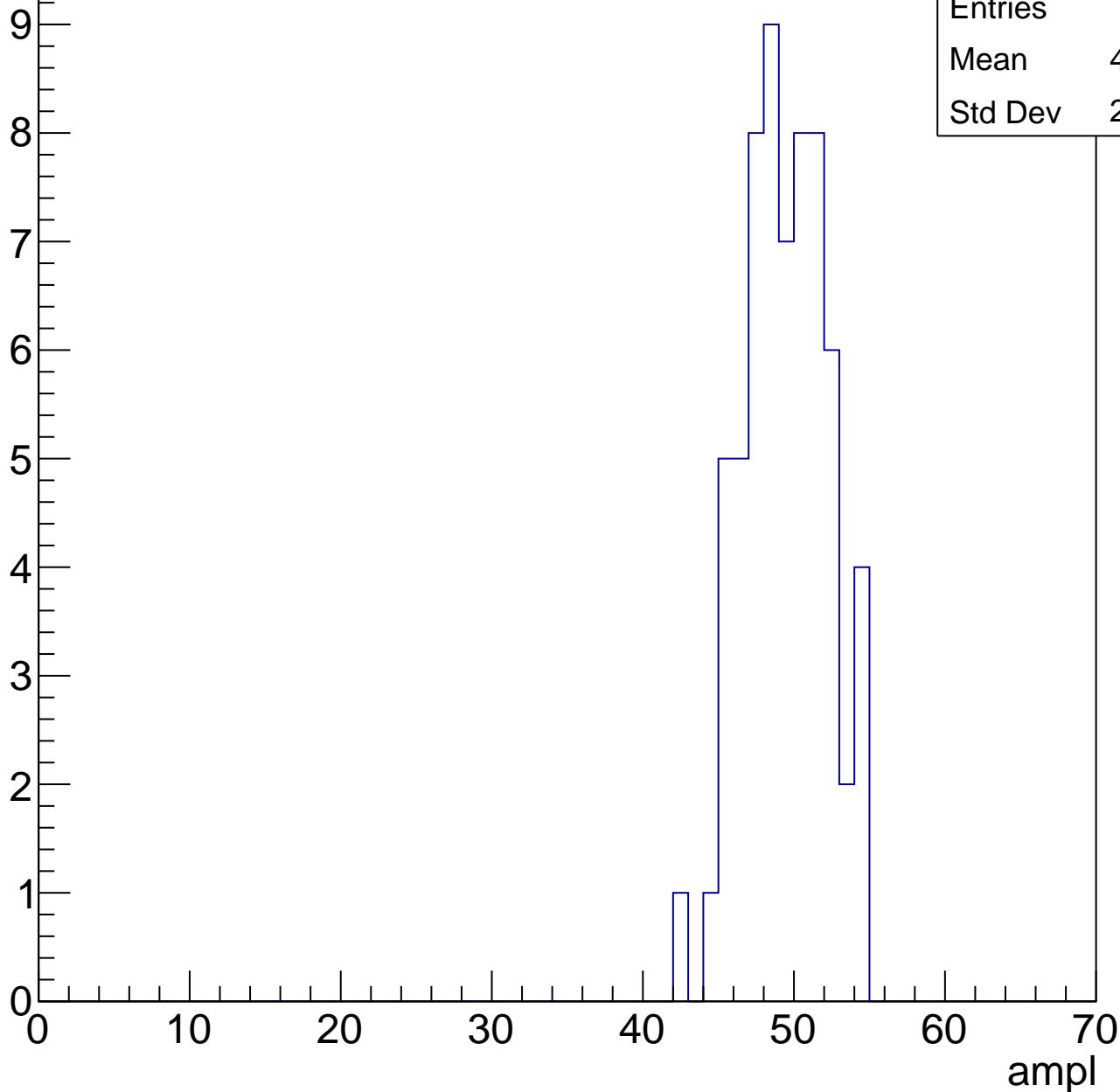


# B1L003S, U23-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.97
Std Dev	2.698

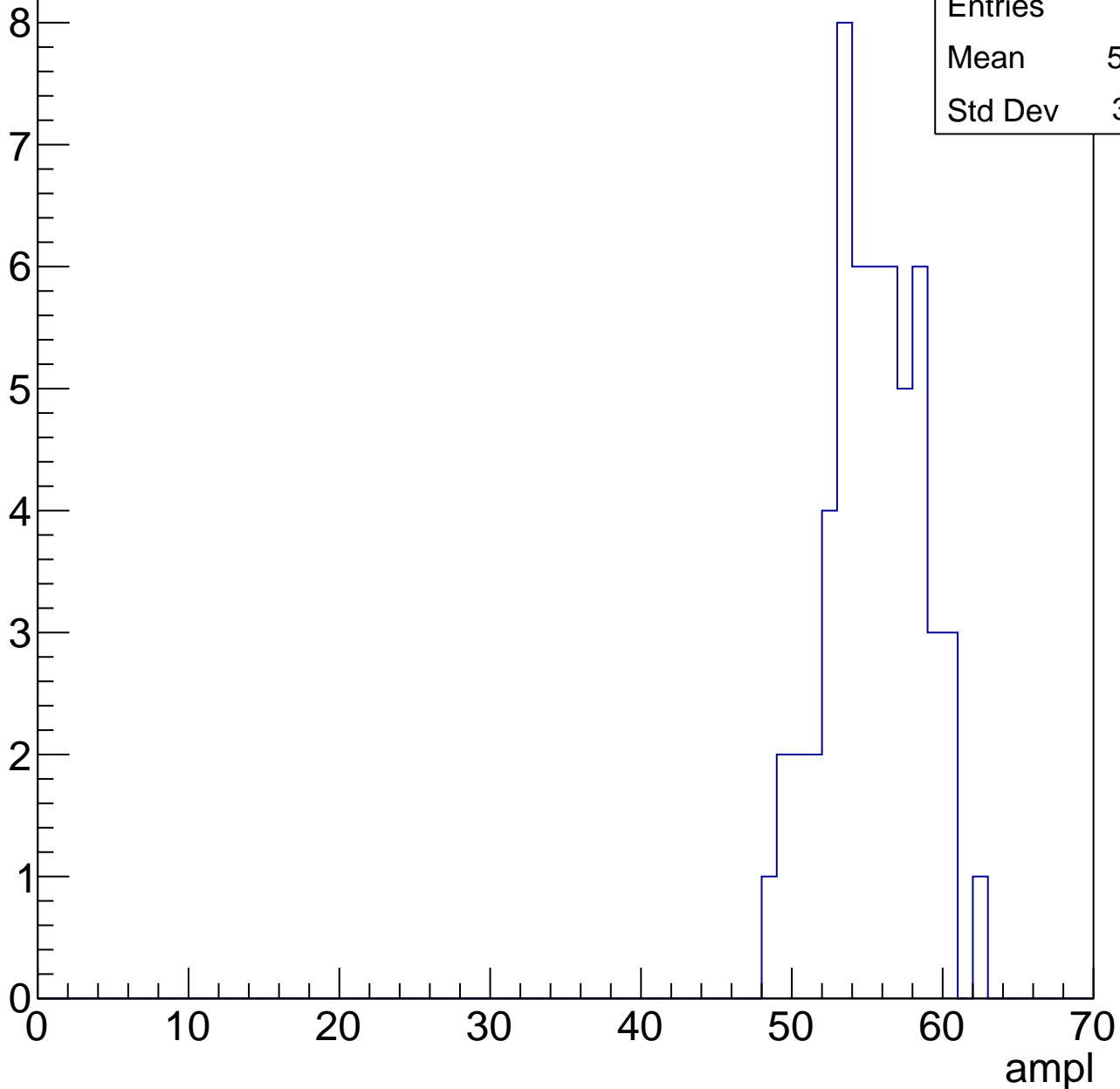


# B1L003S, U23-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.95
Std Dev	3.101



# B1L003S, U23-ch20, adc5

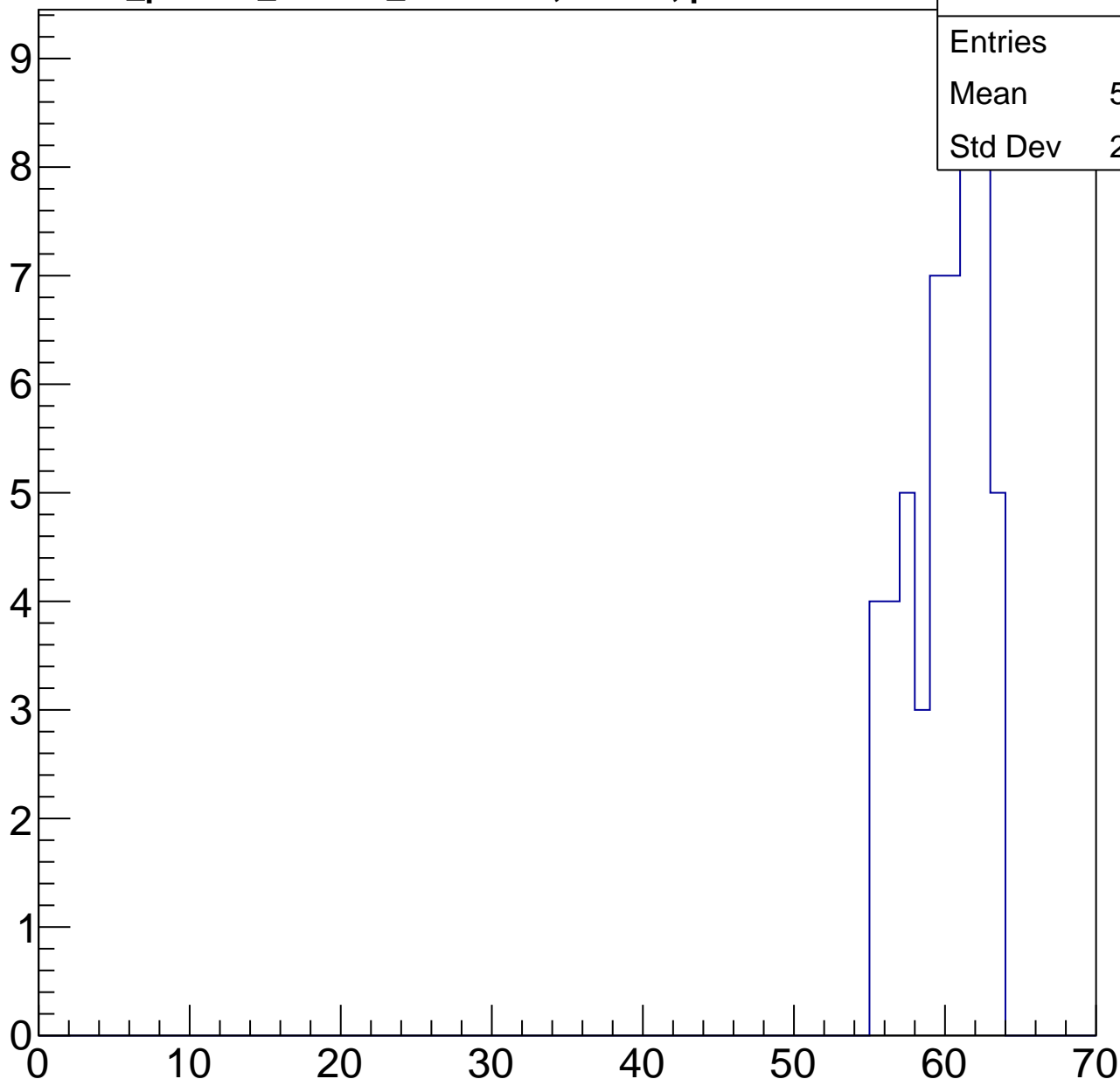
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	59.54
Std Dev	2.414

ampl

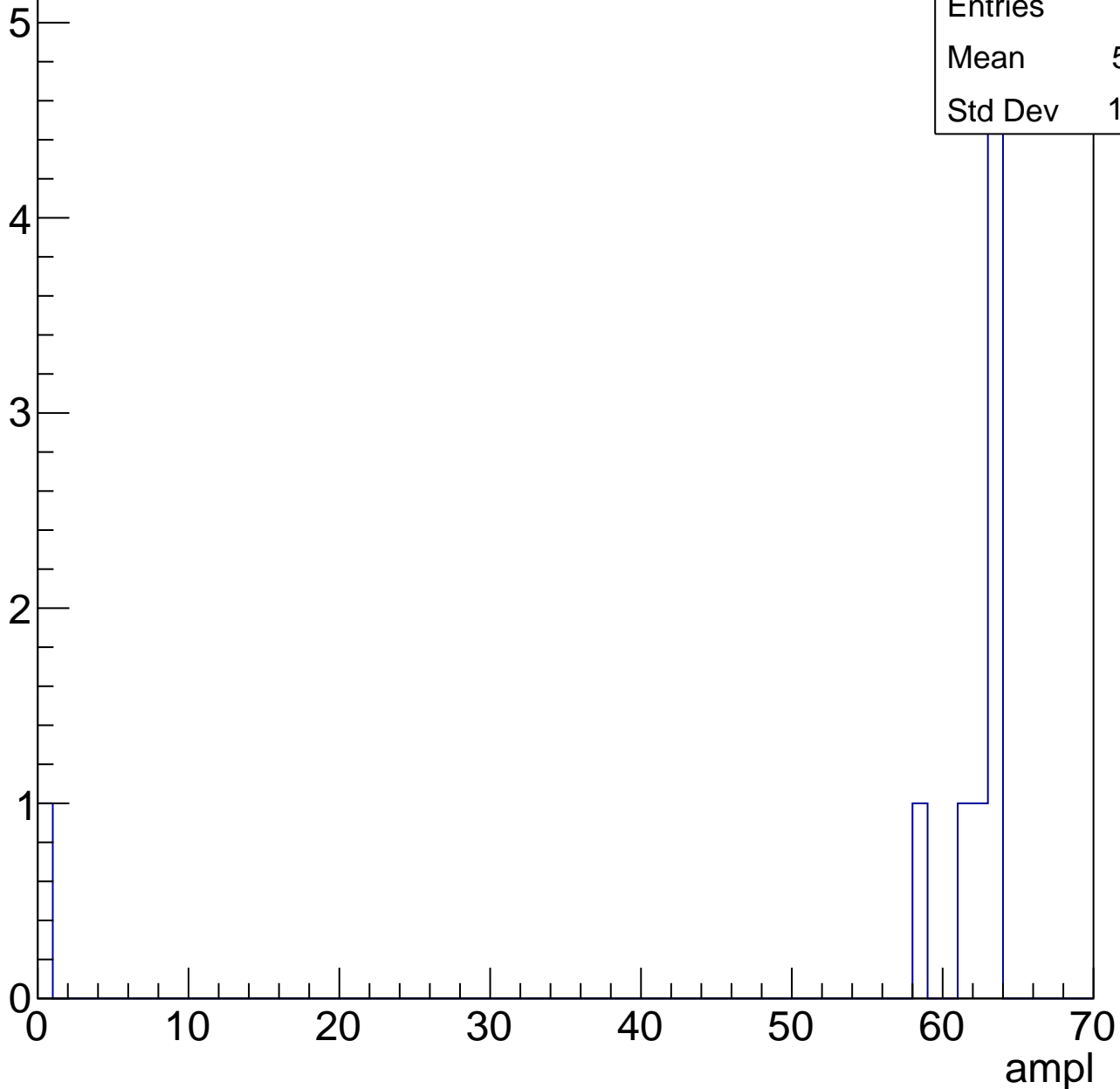


# B1L003S, U23-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	9
Mean	55.11
Std Dev	19.55





# B1L003S, U23-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch21, adc0

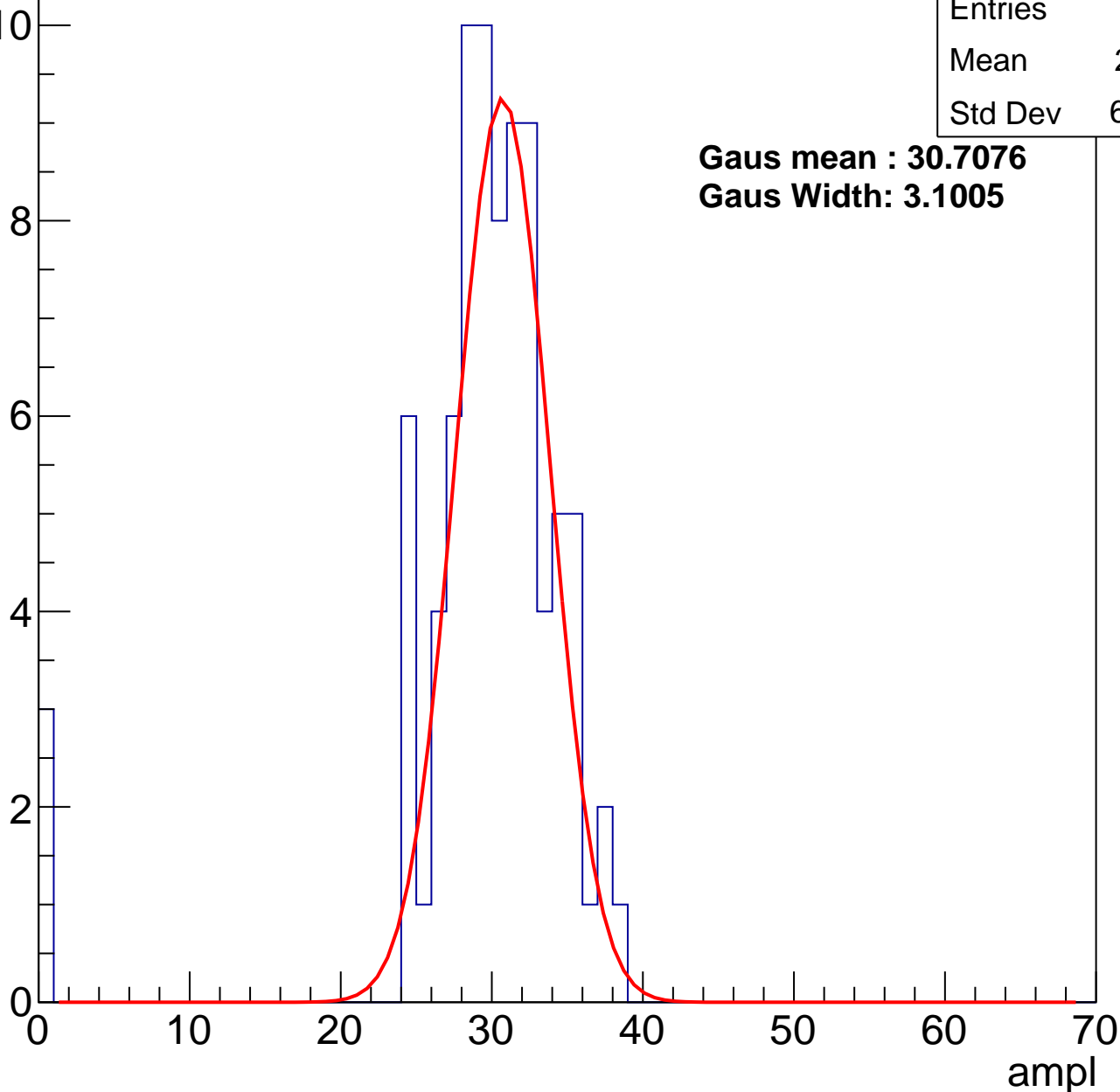
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	84
Mean	29.01
Std Dev	6.465

**Gaus mean : 30.7076**

**Gaus Width: 3.1005**



# B1L003S, U23-ch21, adc1

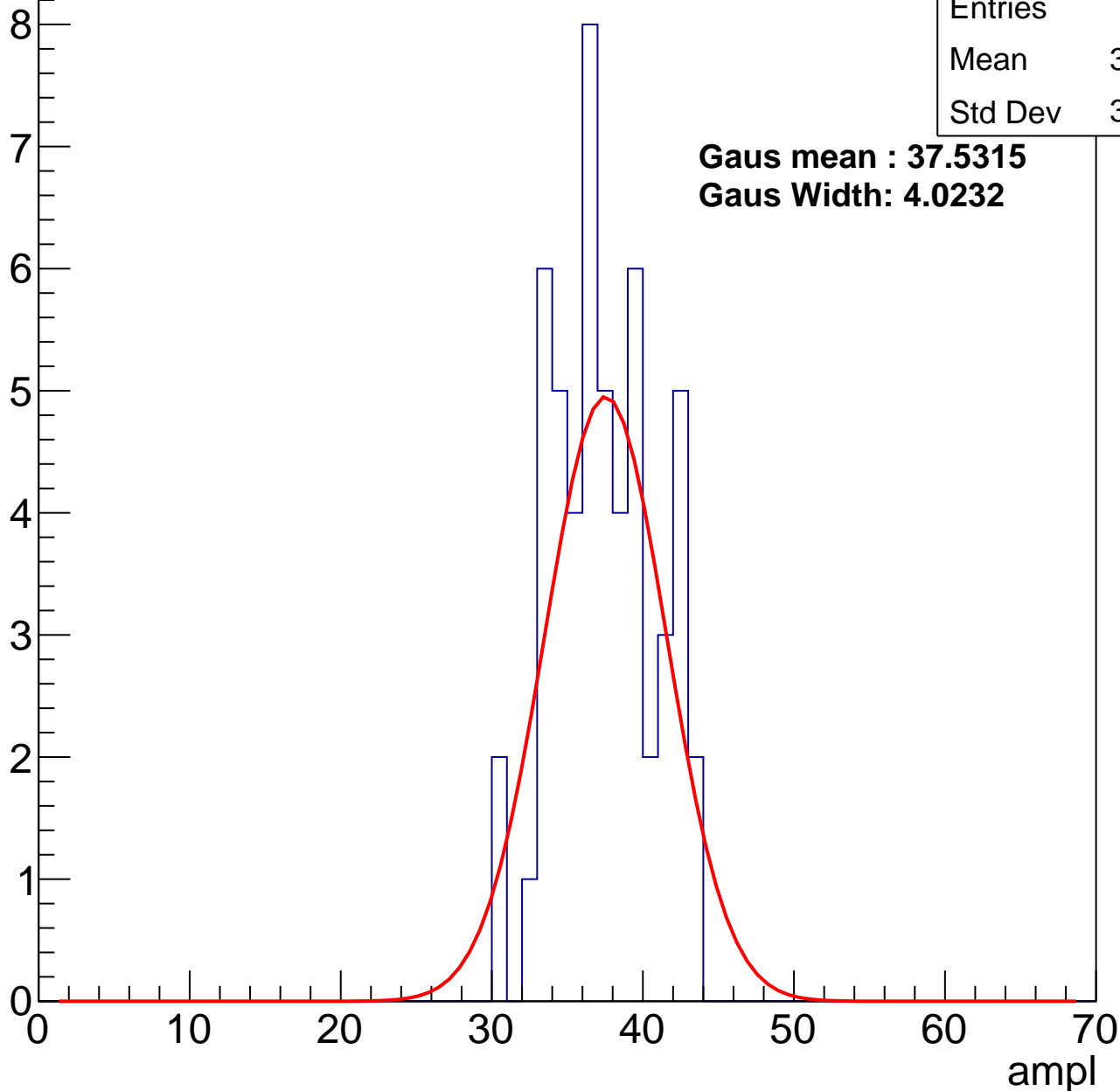
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	36.94
Std Dev	3.305

**Gaus mean : 37.5315**

**Gaus Width: 4.0232**



# B1L003S, U23-ch21, adc2

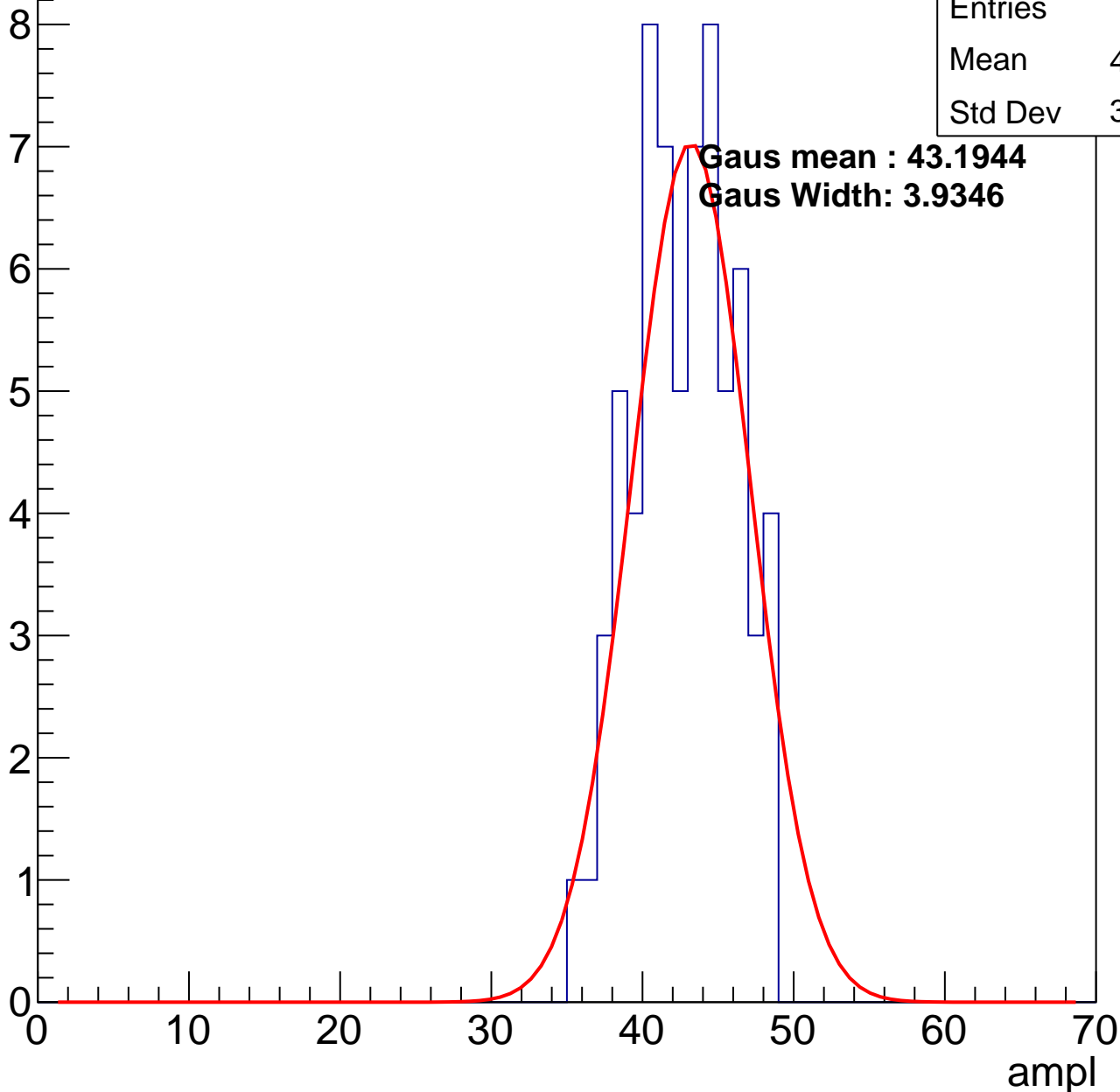
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.27
Std Dev	3.253

**Gaus mean : 43.1944**

**Gaus Width: 3.9346**

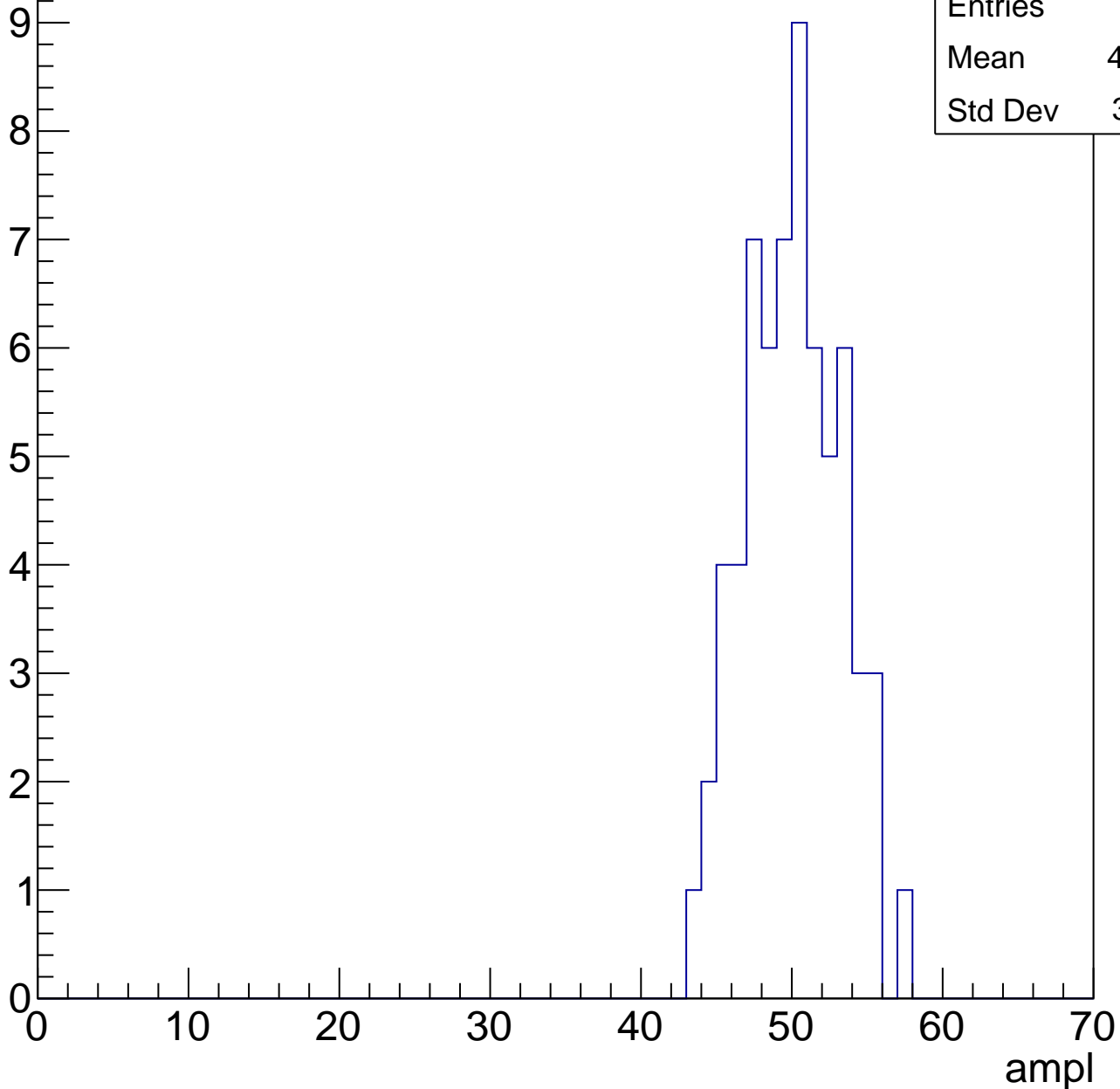


# B1L003S, U23-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.58
Std Dev	3.111

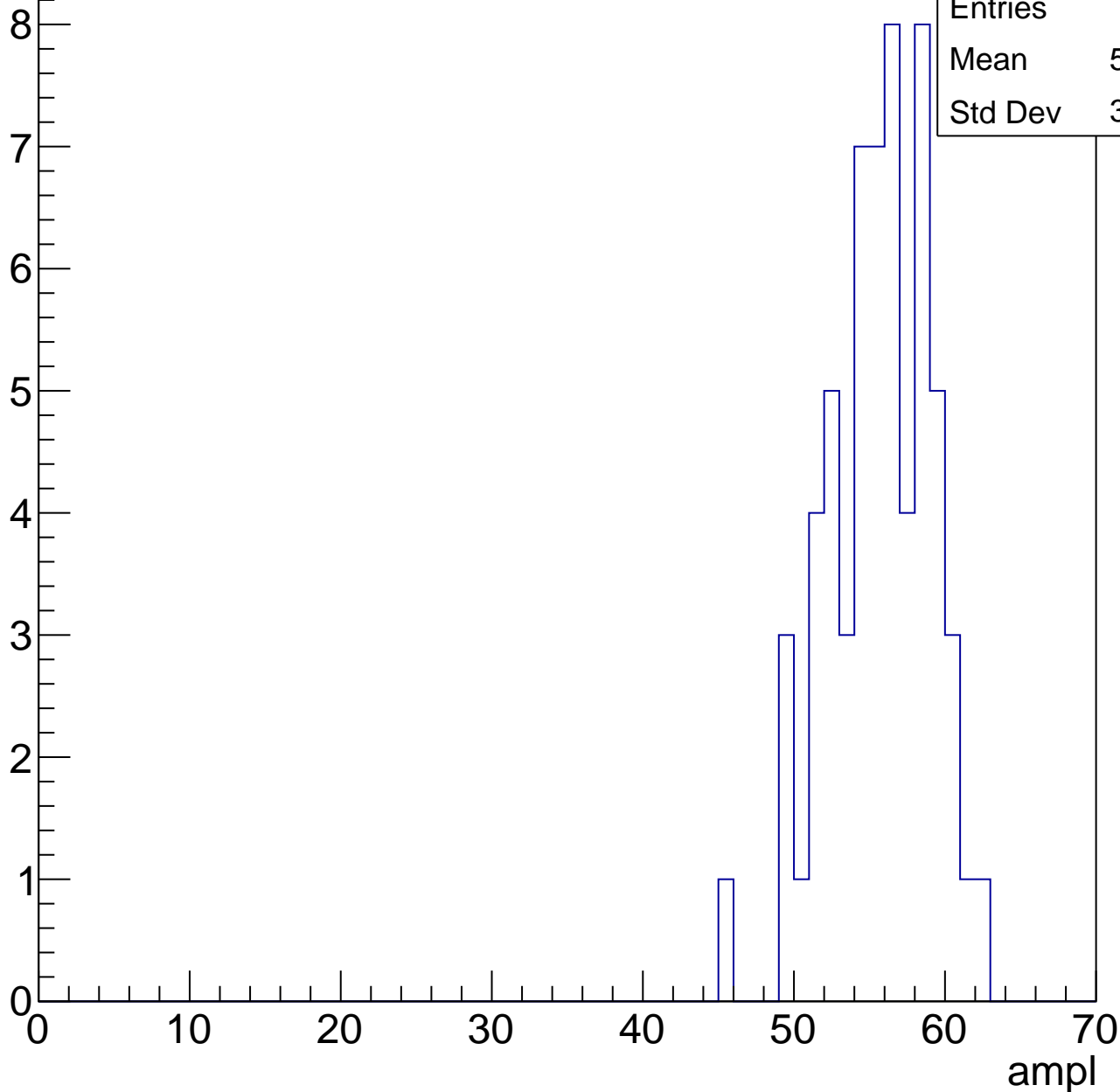


# B1L003S, U23-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	55.18
Std Dev	3.375

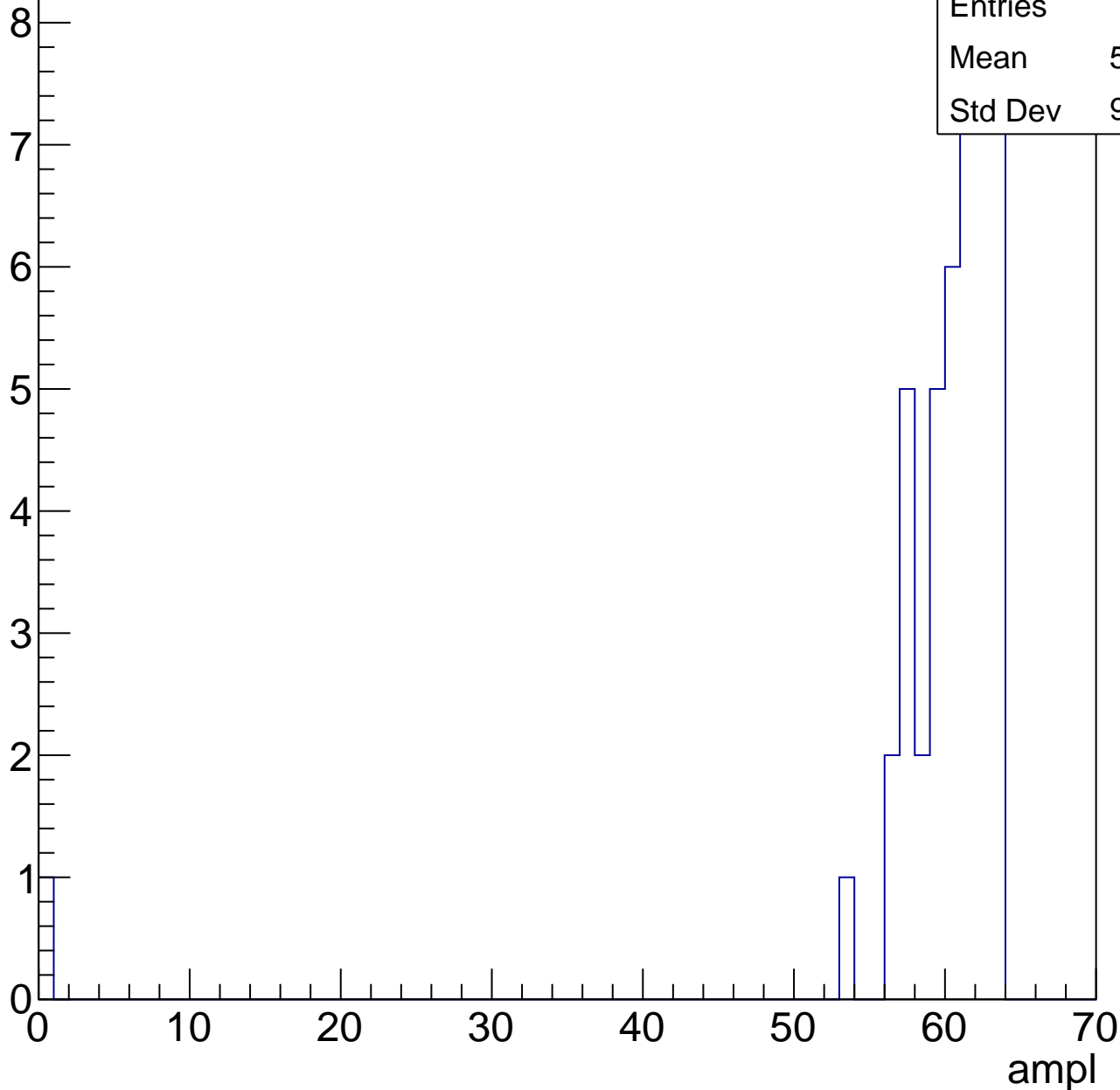


# B1L003S, U23-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.89
Std Dev	9.082



# B1L003S, U23-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

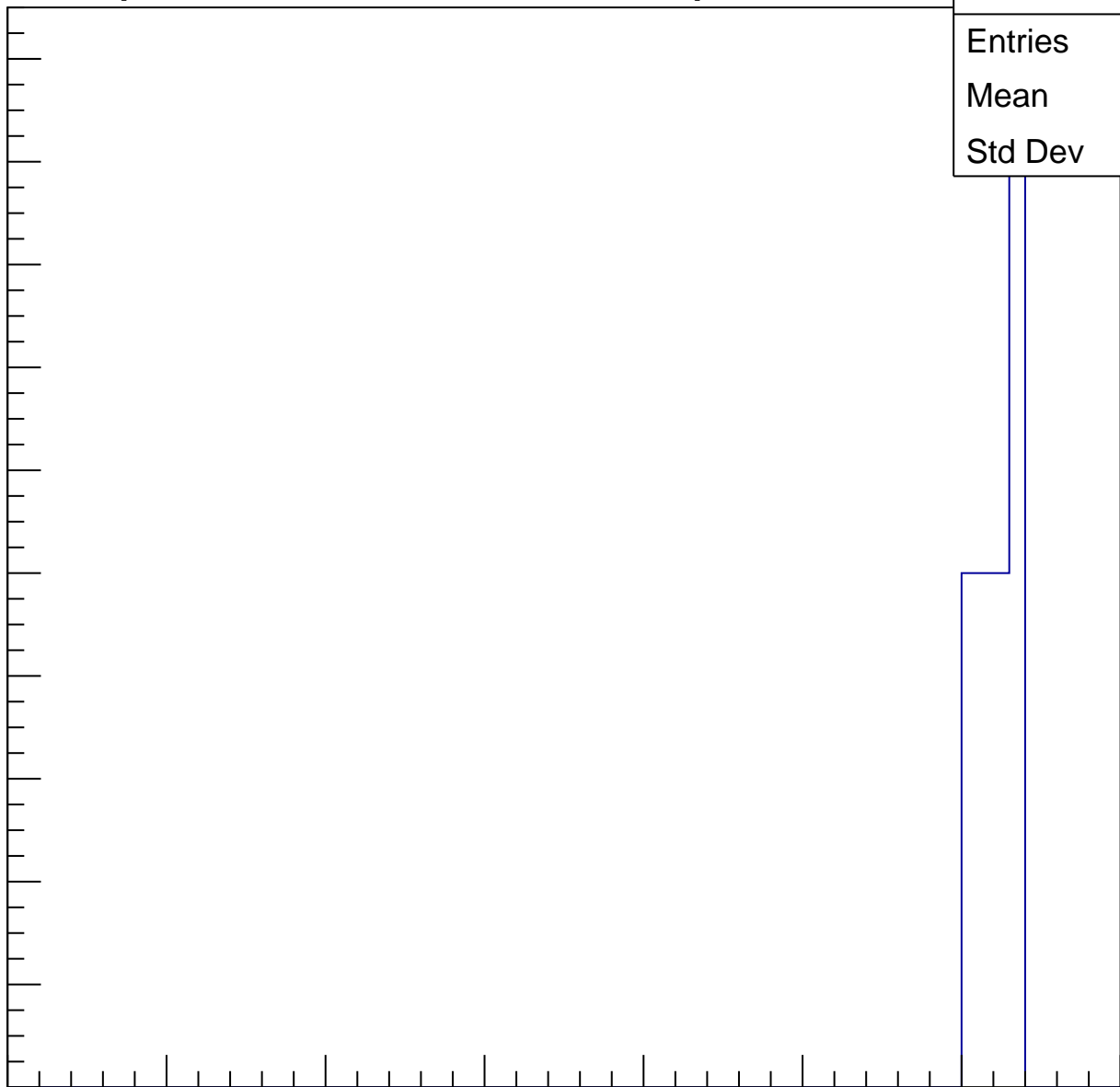
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.8
Std Dev	1.166

0 10 20 30 40 50 60 70

ampl





# B1L003S, U23-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch22, adc0

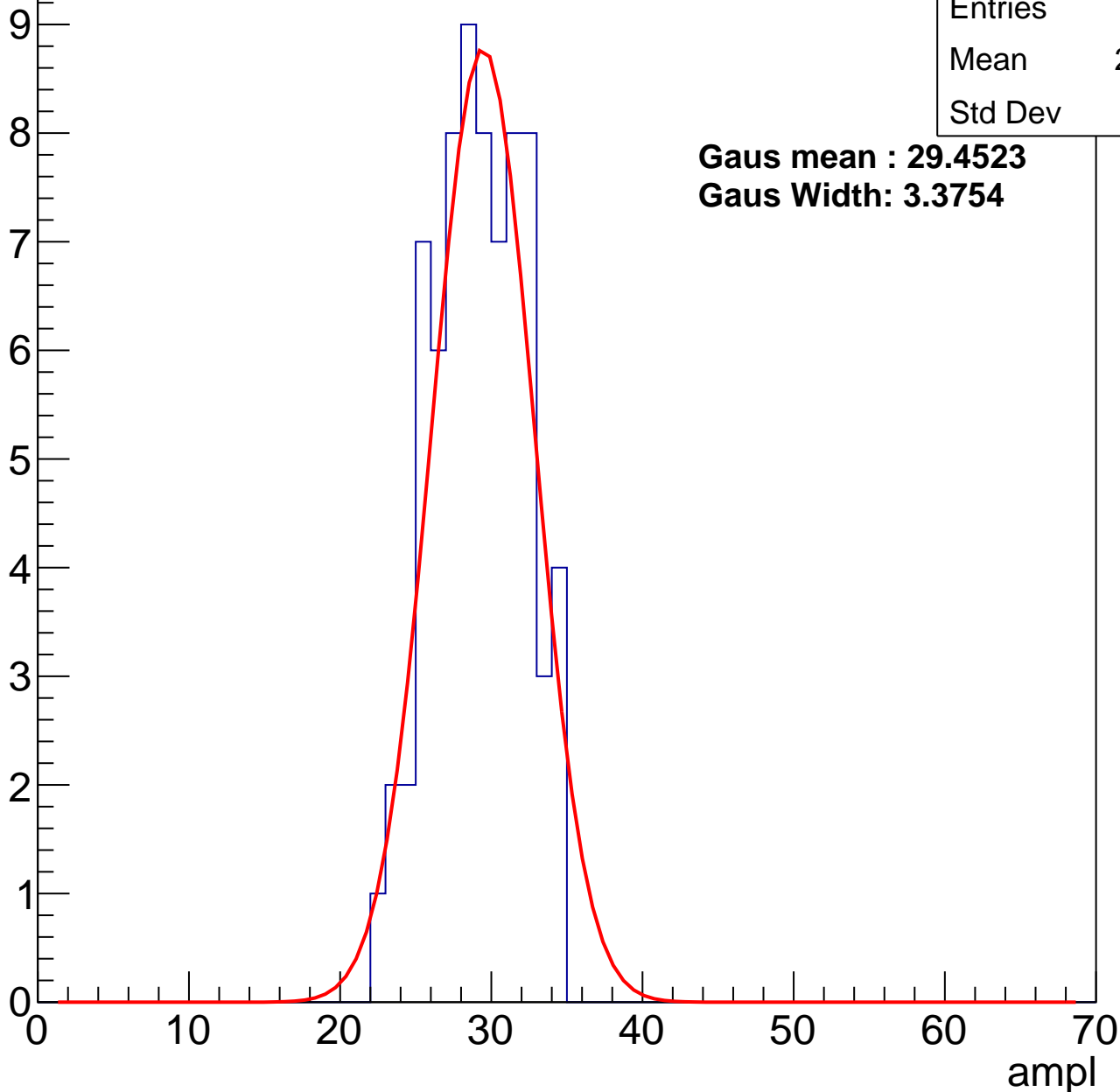
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	28.71
Std Dev	2.94

**Gaus mean : 29.4523**

**Gaus Width: 3.3754**



# B1L003S, U23-ch22, adc1

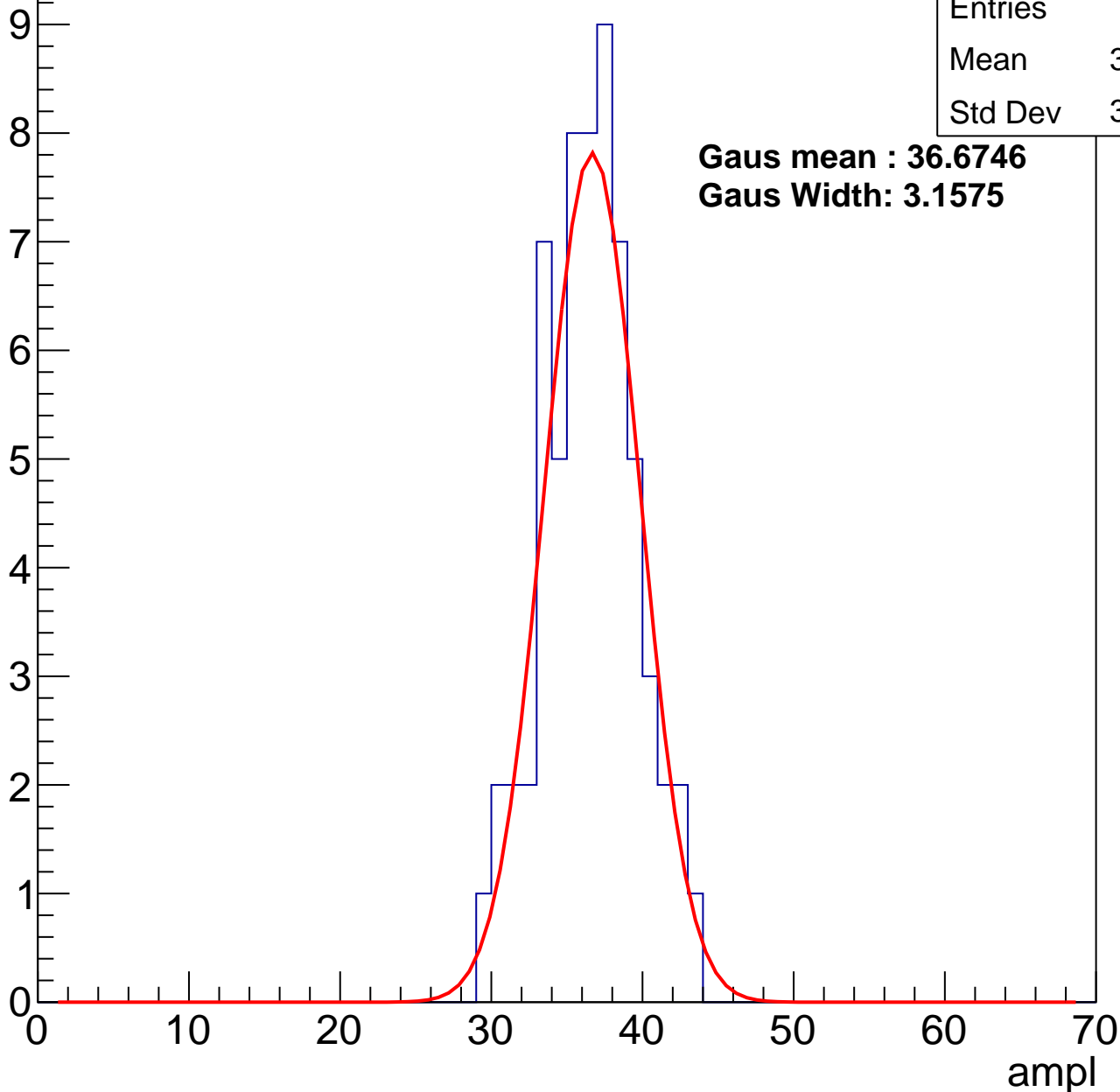
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	36.05
Std Dev	3.049

**Gaus mean : 36.6746**

**Gaus Width: 3.1575**



# B1L003S, U23-ch22, adc2

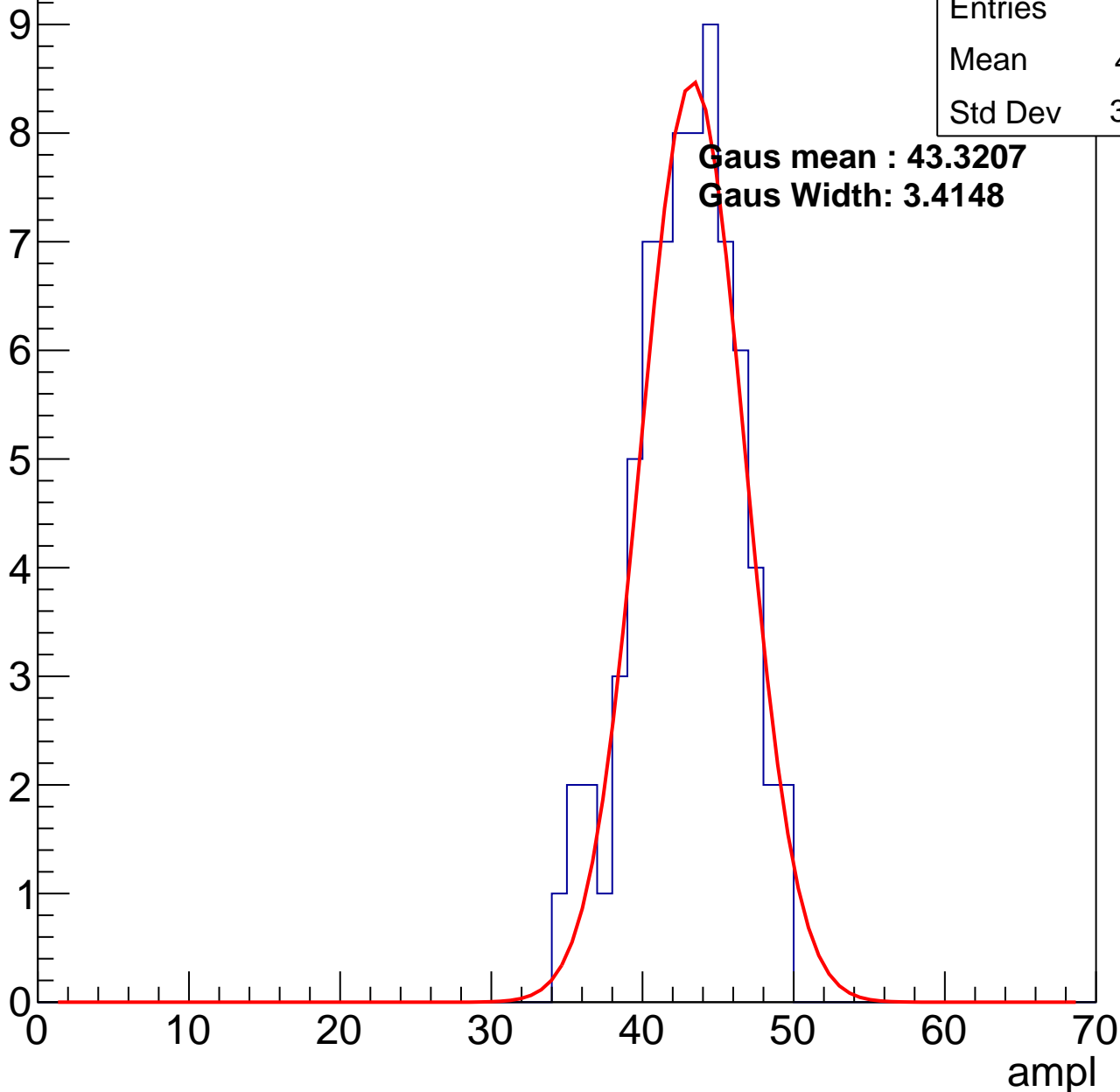
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	42.41
Std Dev	3.385

**Gaus mean : 43.3207**

**Gaus Width: 3.4148**

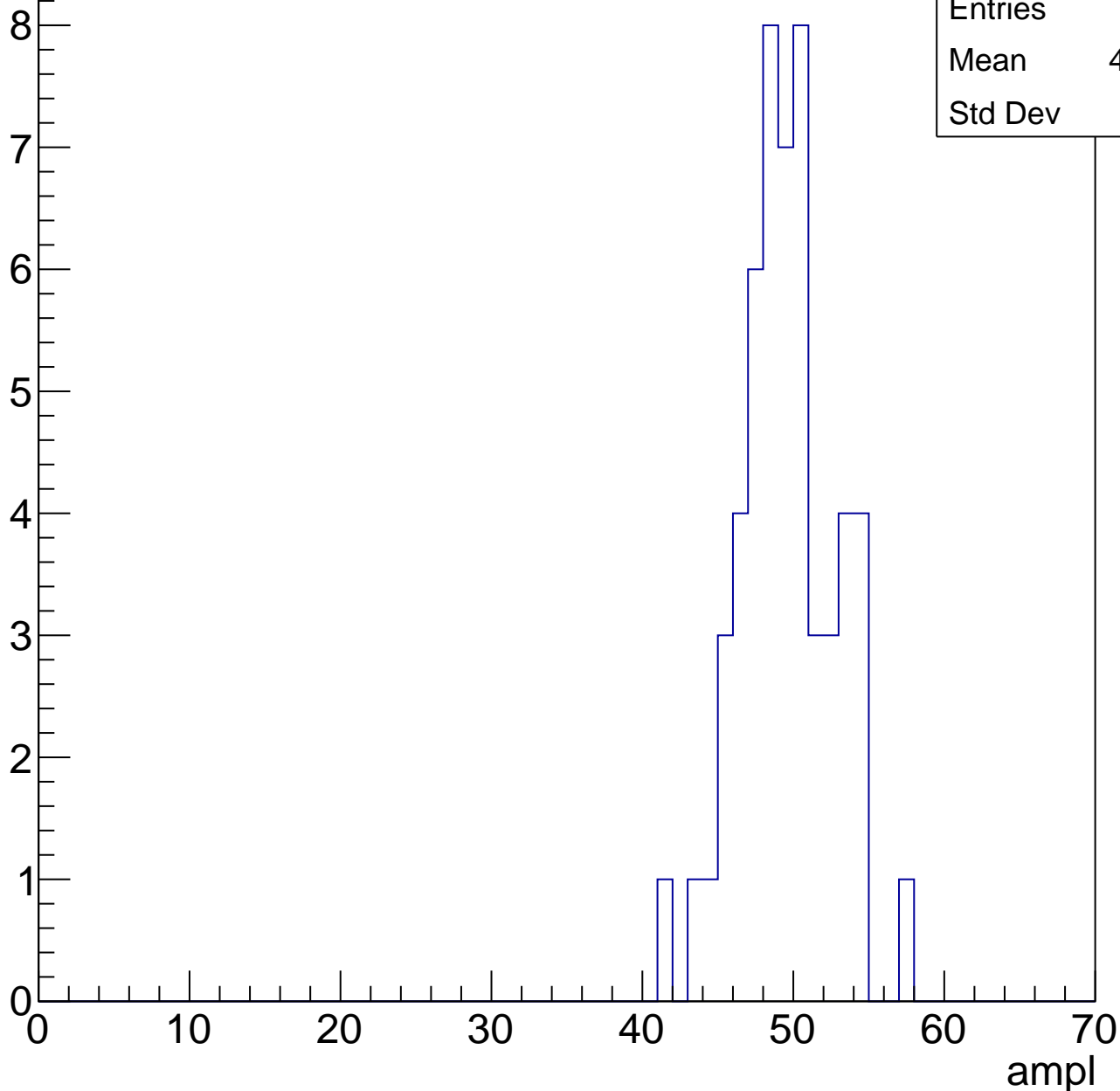


# B1L003S, U23-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	49.07
Std Dev	3.09



# B1L003S, U23-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

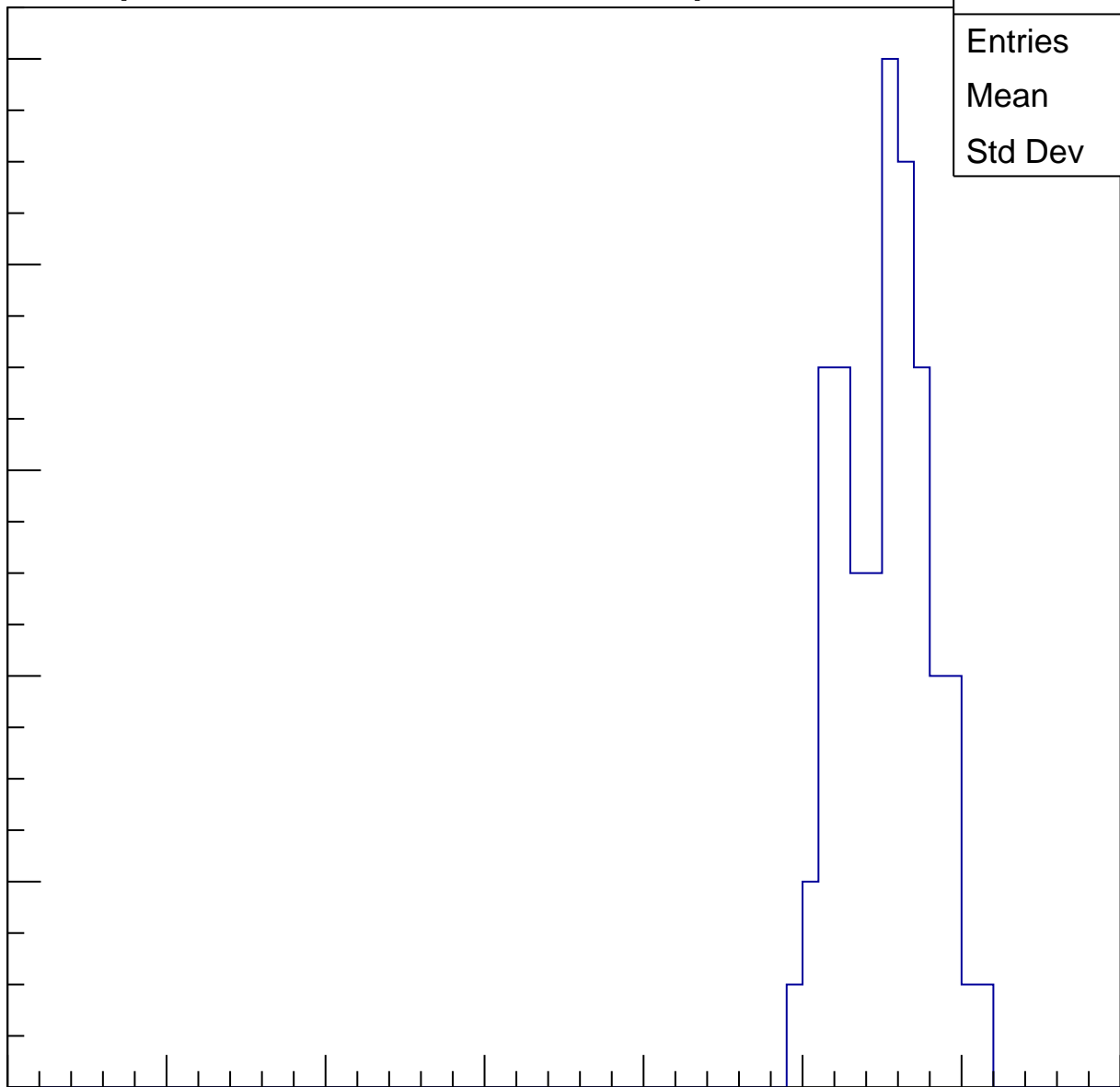
Entries	63
Mean	54.71
Std Dev	2.757

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U23-ch22, adc5

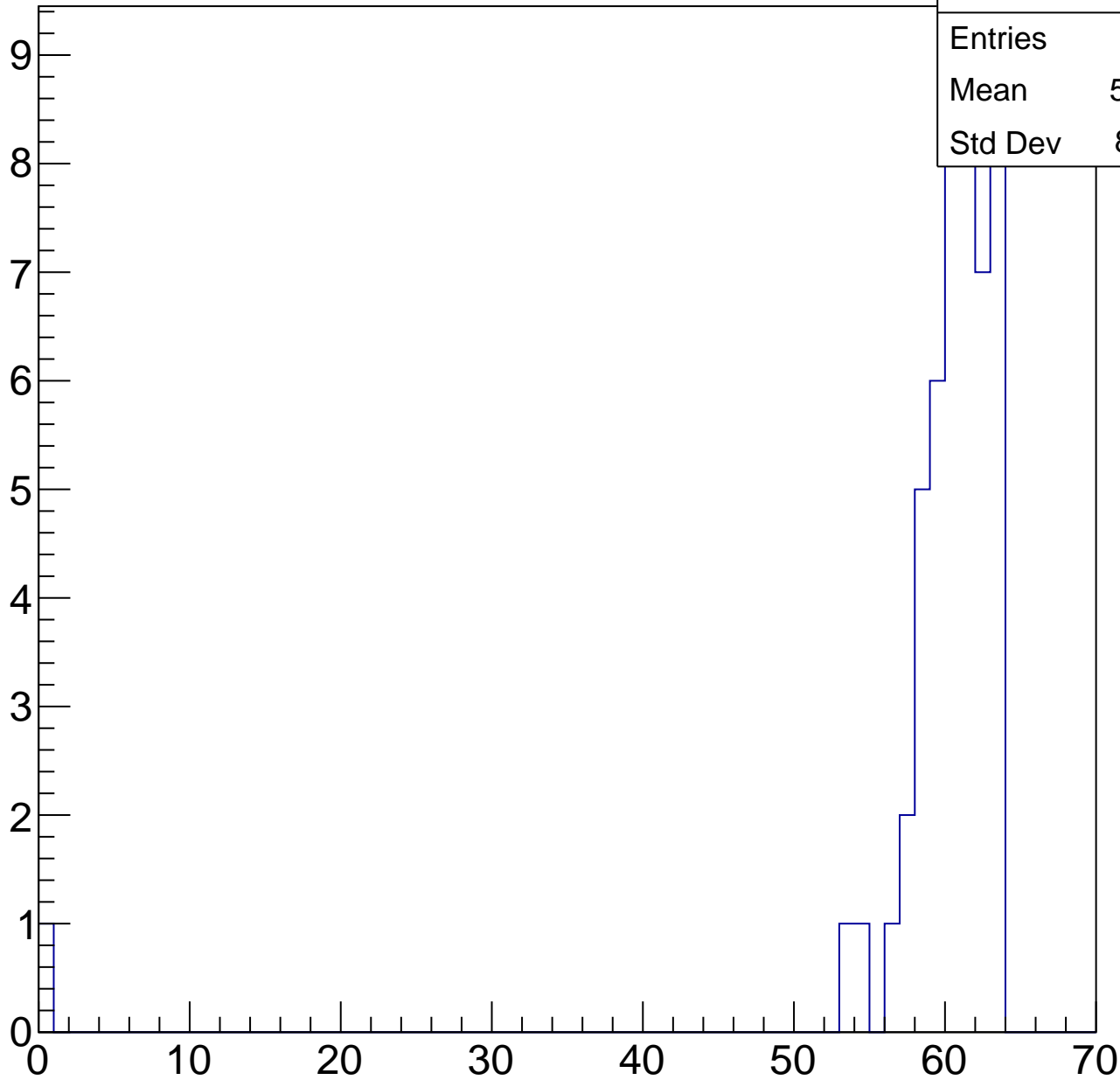
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.92
Std Dev	8.801

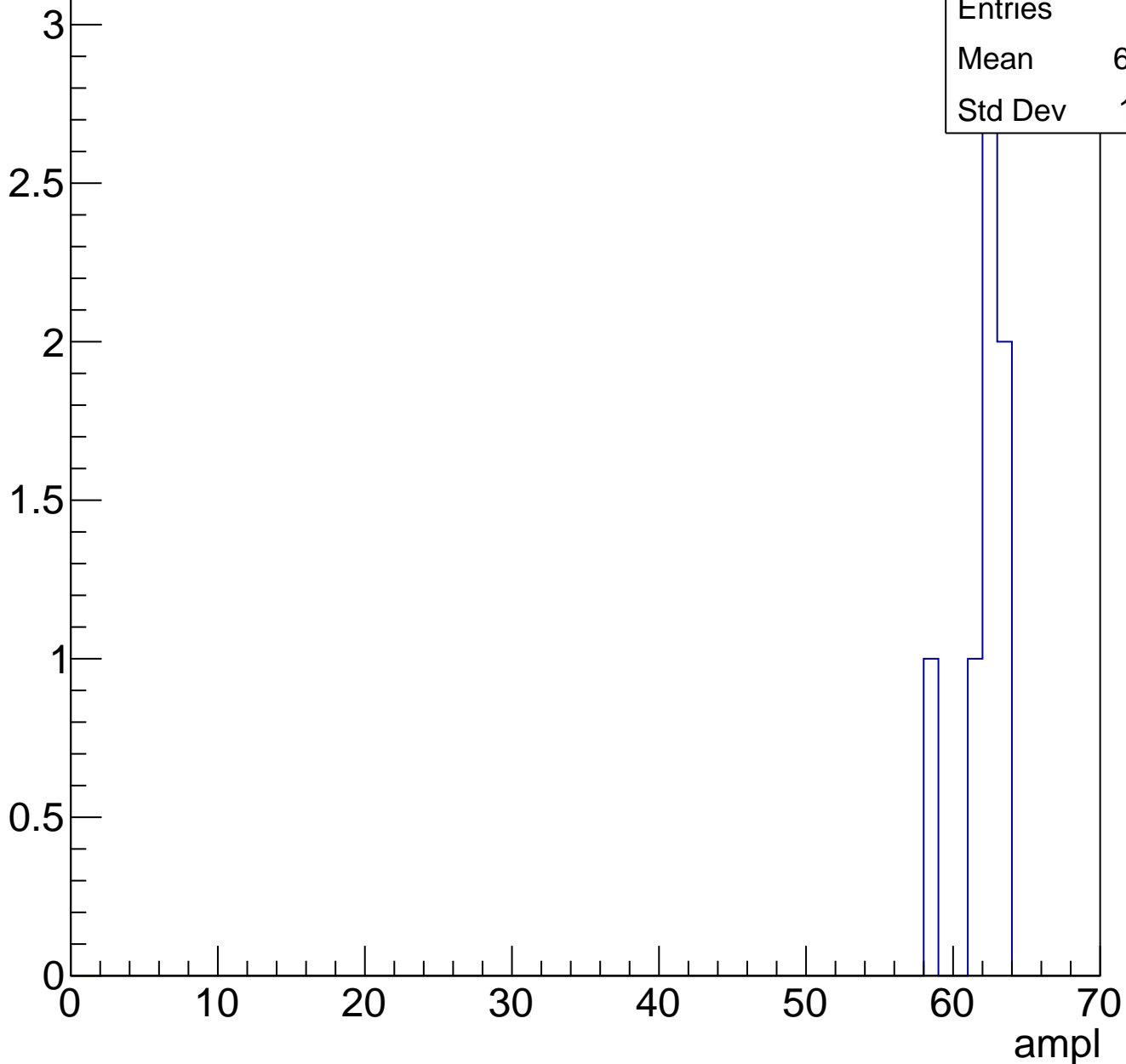
ampl



# B1L003S, U23-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch23, adc0

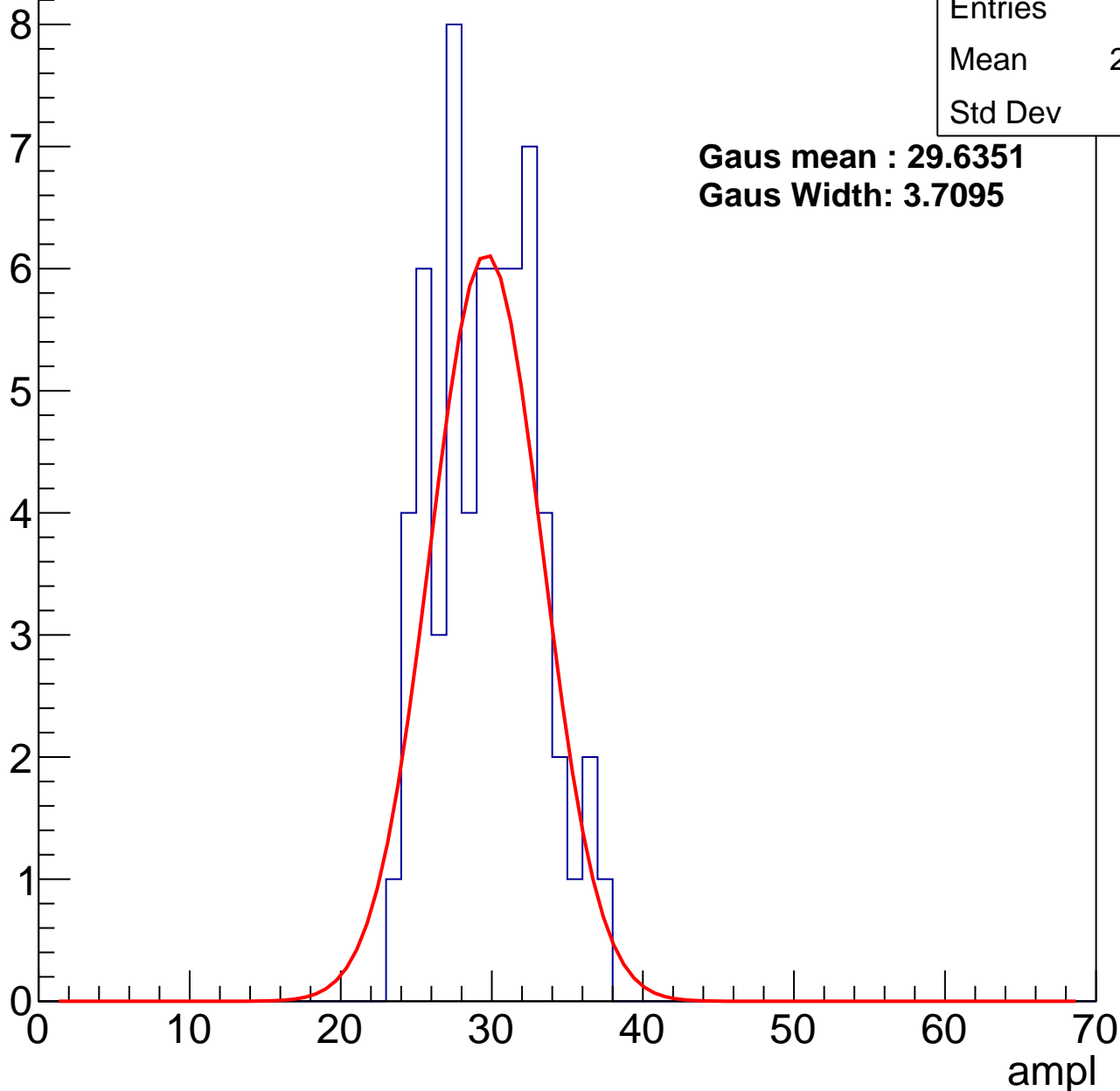
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.23
Std Dev	3.38

**Gaus mean : 29.6351**

**Gaus Width: 3.7095**



# B1L003S, U23-ch23, adc1

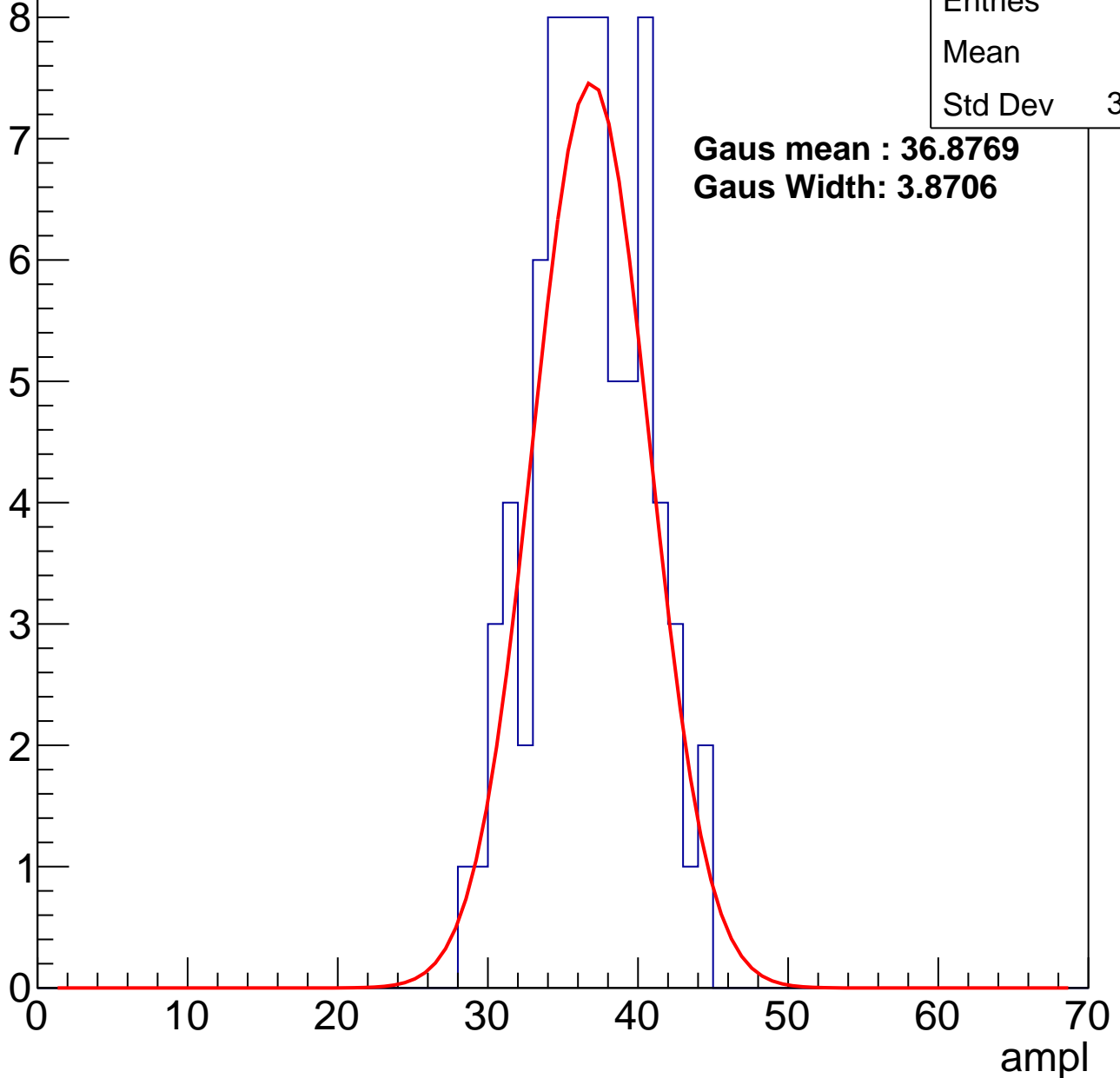
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	36.3
Std Dev	3.651

**Gaus mean : 36.8769**

**Gaus Width: 3.8706**



# B1L003S, U23-ch23, adc2

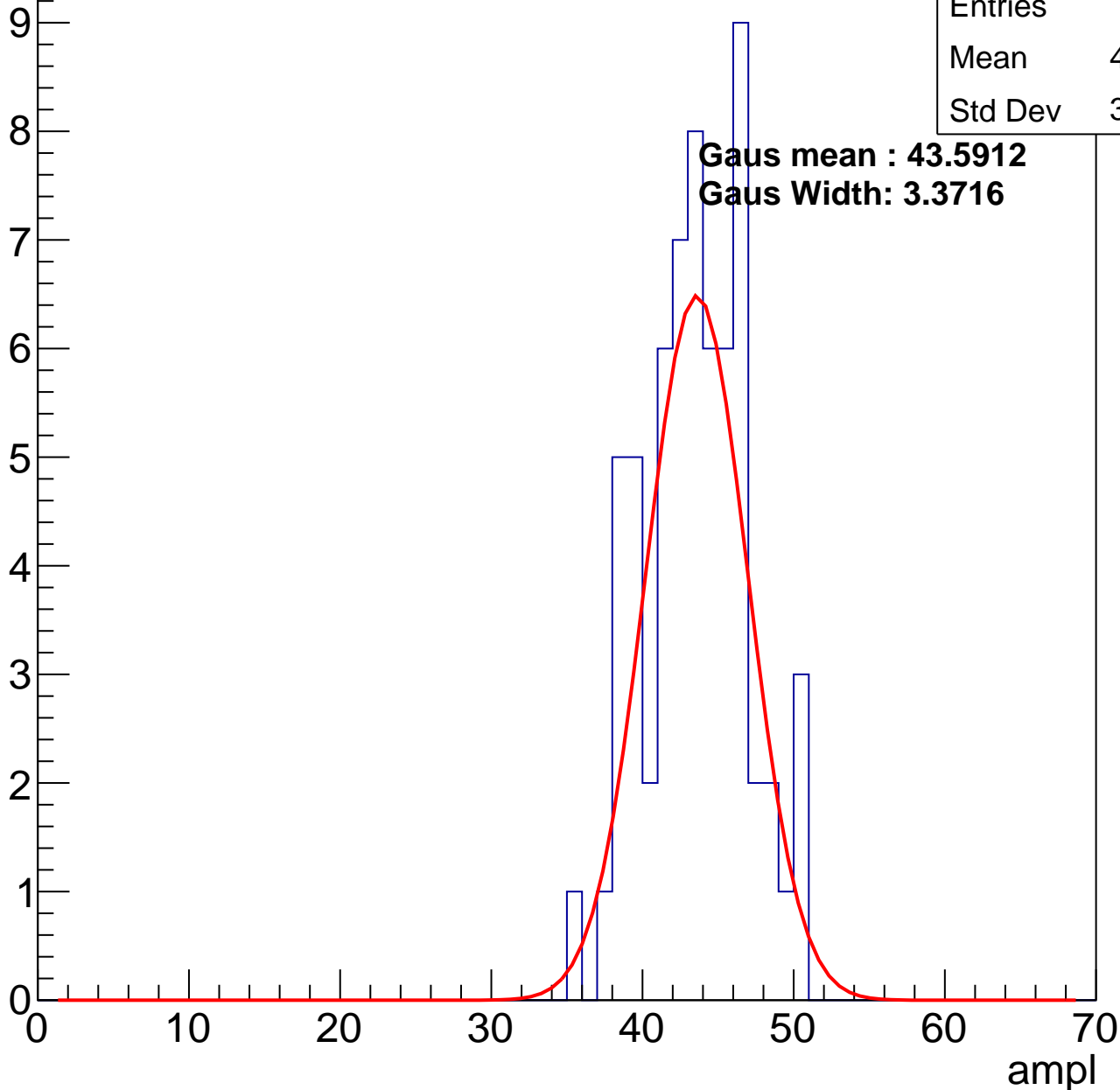
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.09
Std Dev	3.376

**Gaus mean : 43.5912**

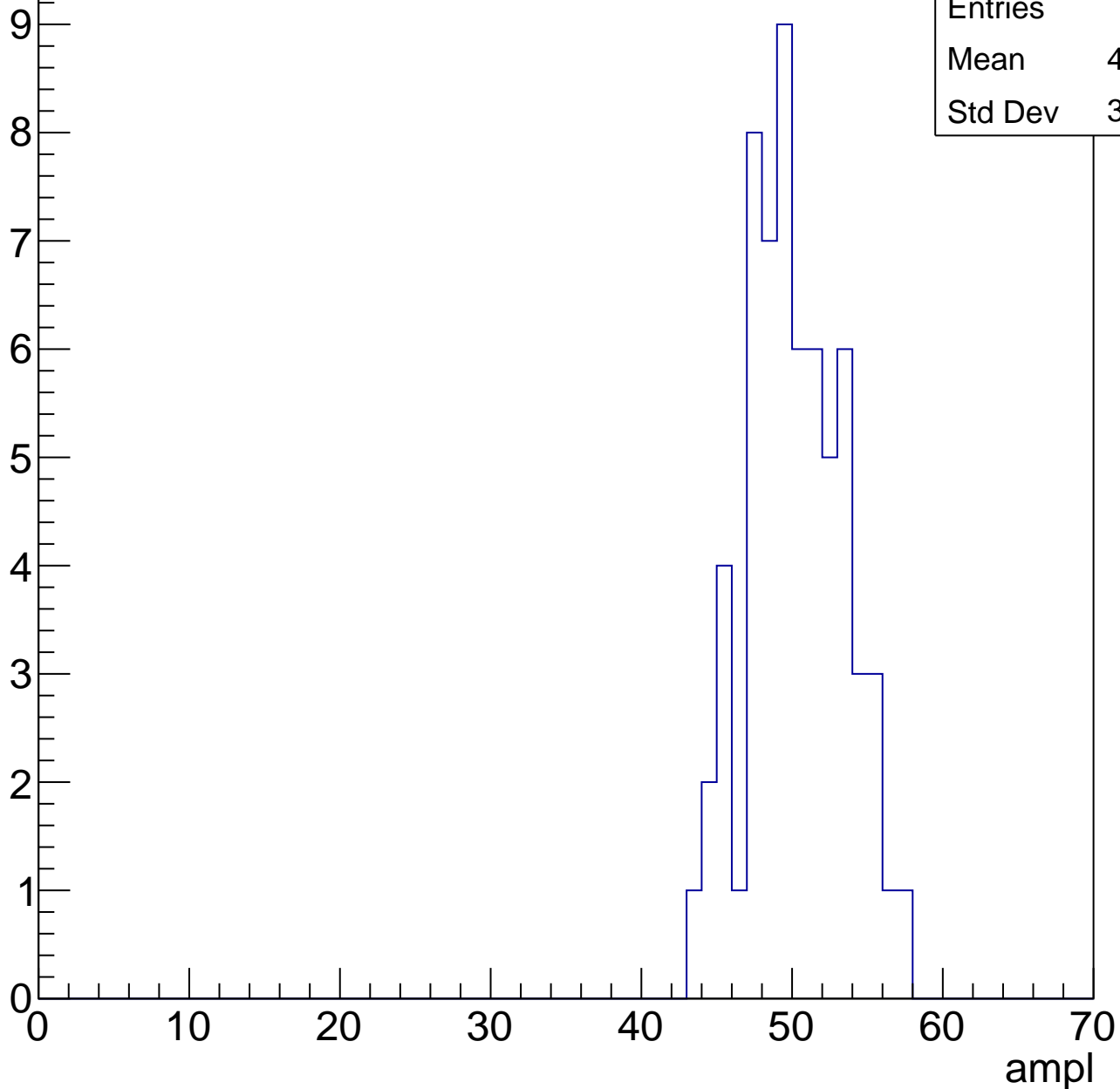
**Gaus Width: 3.3716**



# B1L003S, U23-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



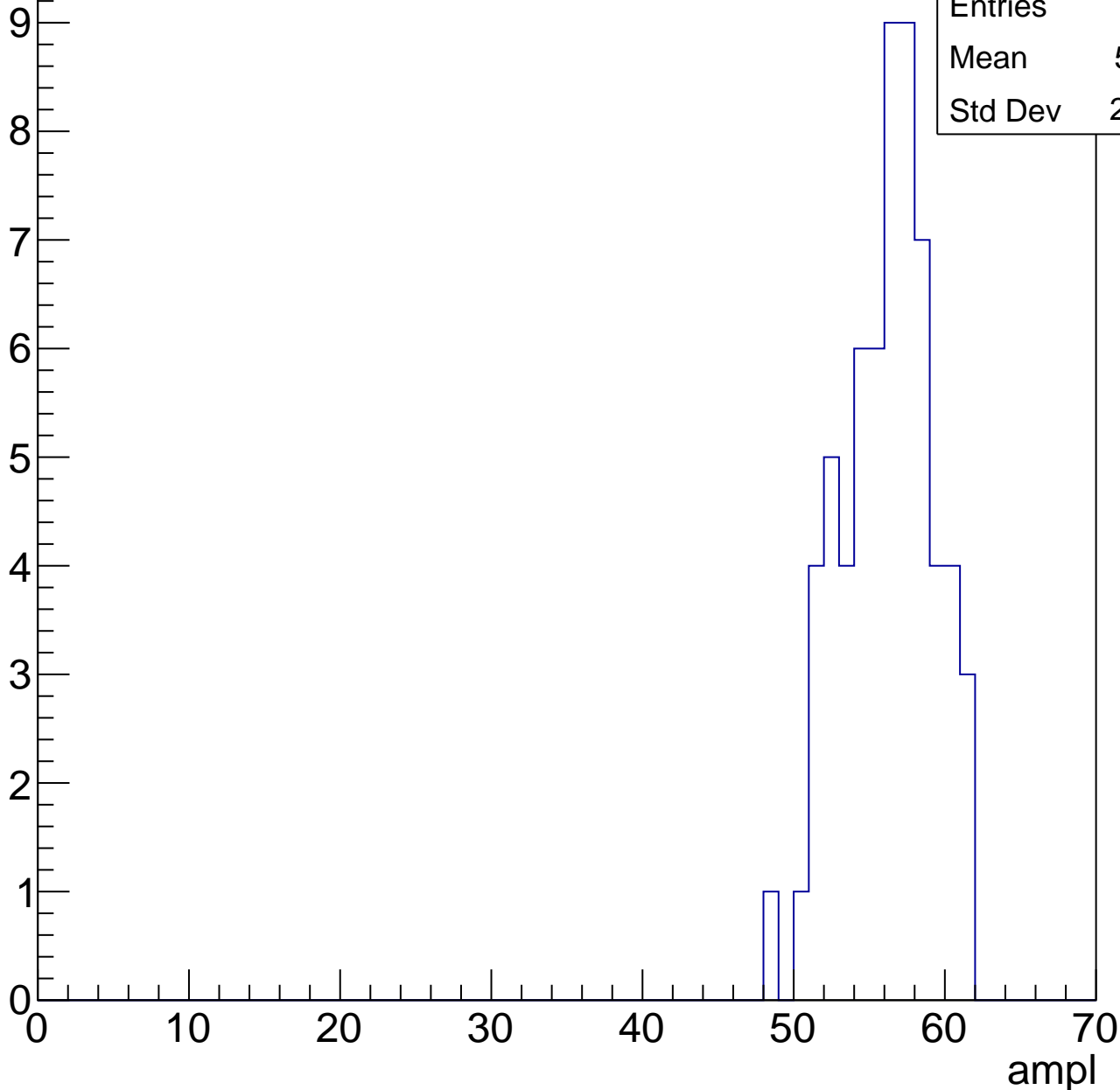
Entries	63
Mean	49.75
Std Dev	3.162

# B1L003S, U23-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	55.71
Std Dev	2.962

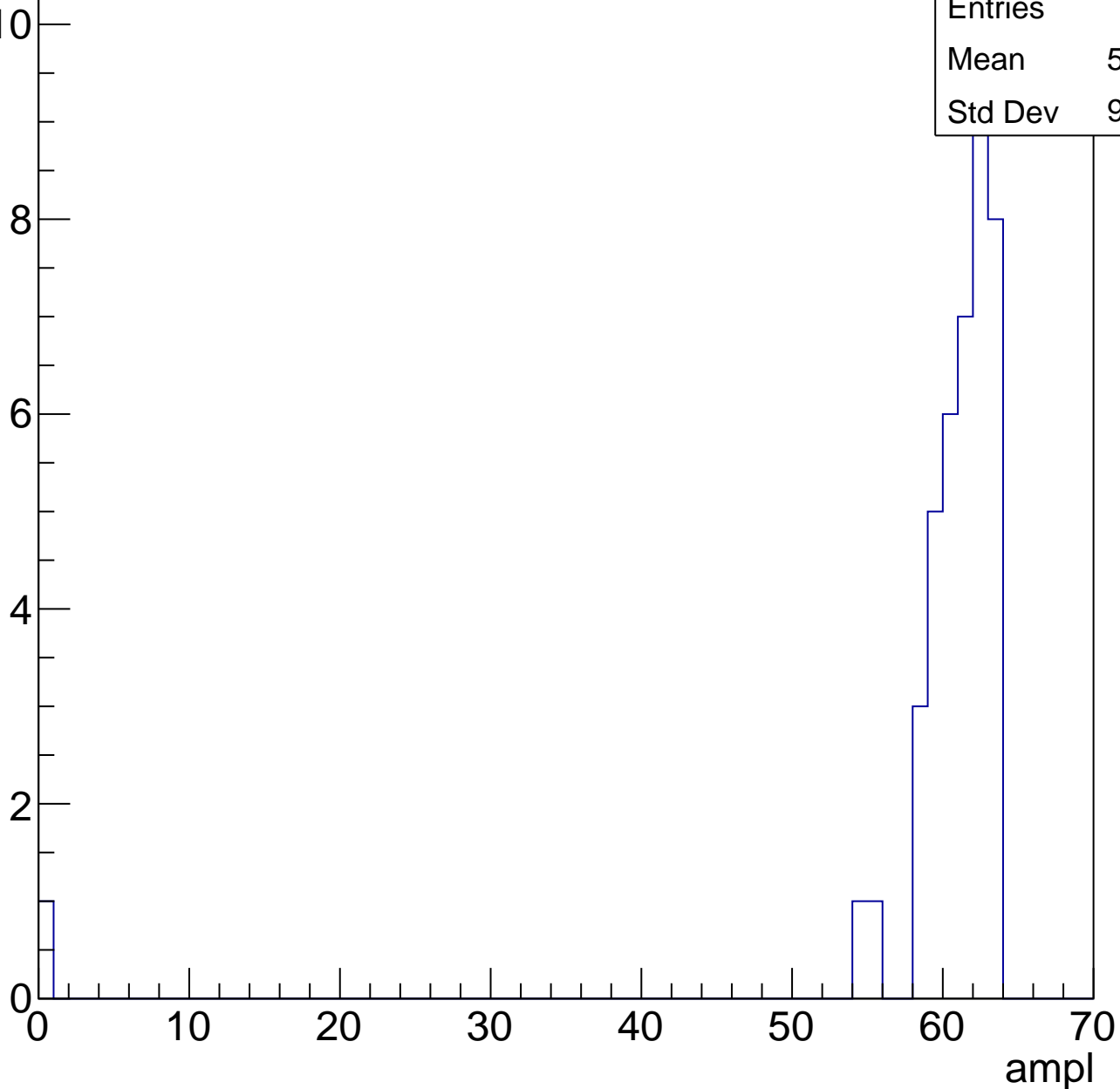


# B1L003S, U23-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	42
Mean	59.26
Std Dev	9.479



# B1L003S, U23-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L003S, U23-ch24, adc0

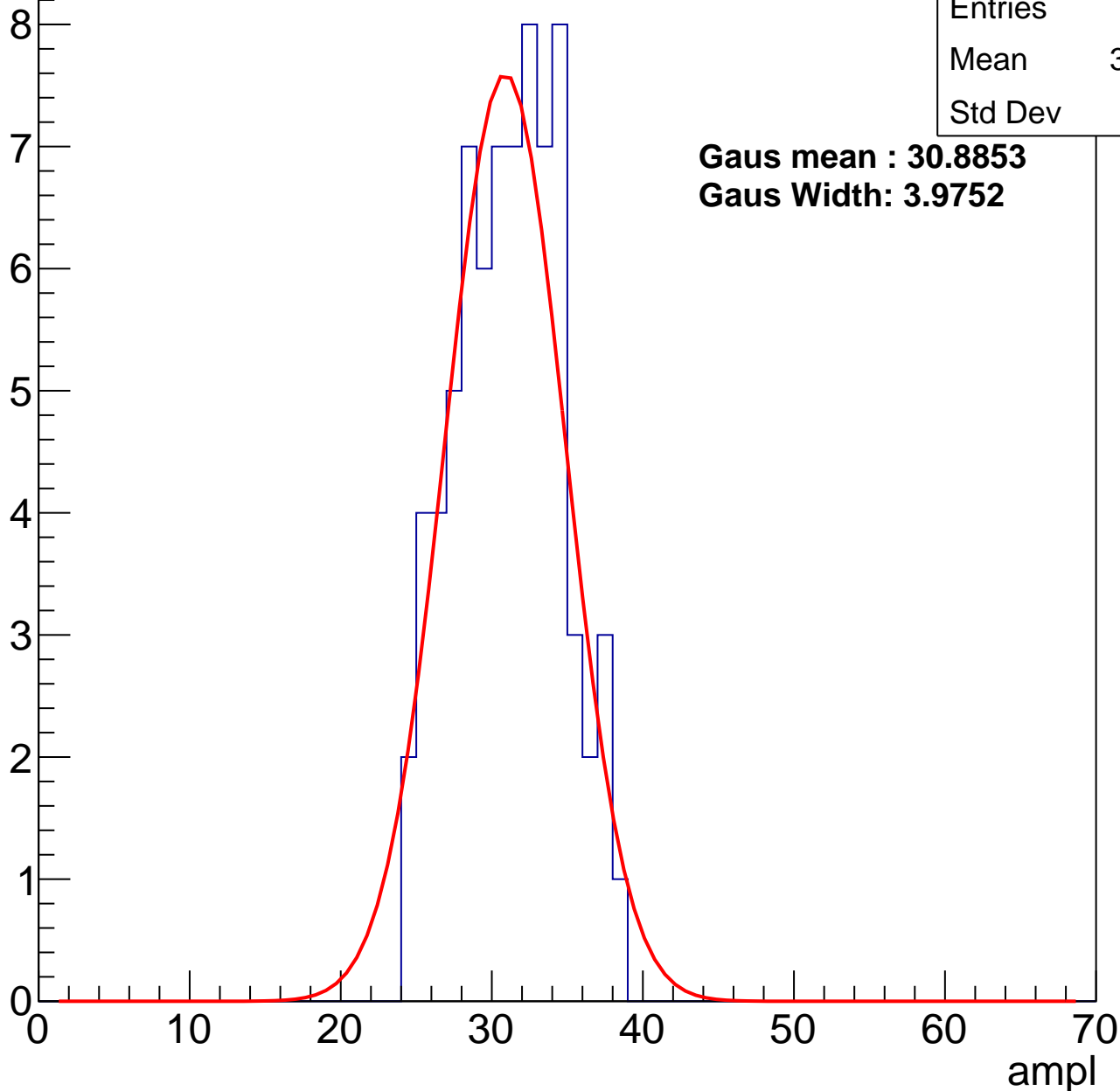
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	30.66
Std Dev	3.43

**Gaus mean : 30.8853**

**Gaus Width: 3.9752**



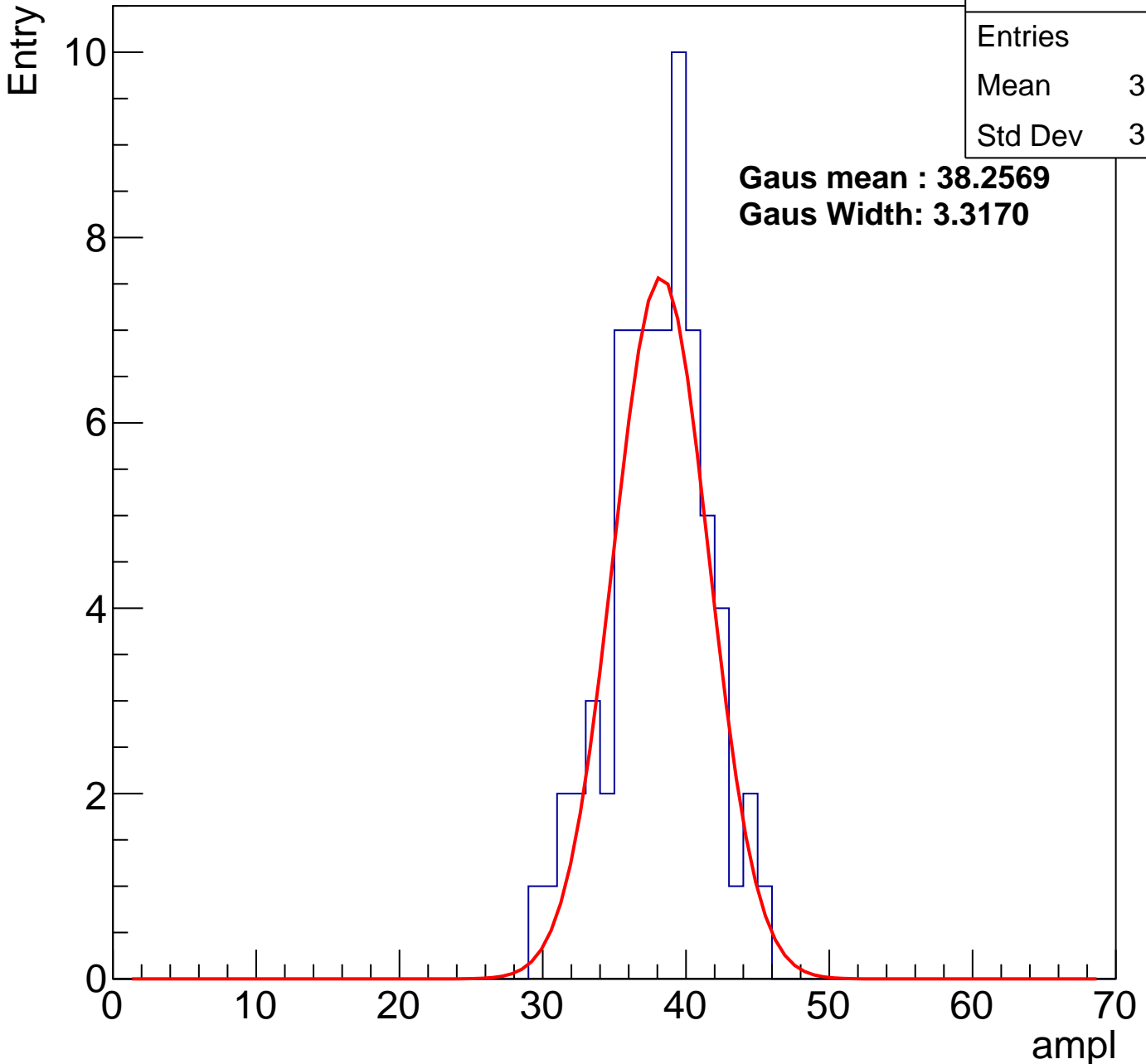
# B1L003S, U23-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	37.58
Std Dev	3.407

**Gaus mean : 38.2569**

**Gaus Width: 3.3170**



# B1L003S, U23-ch24, adc2

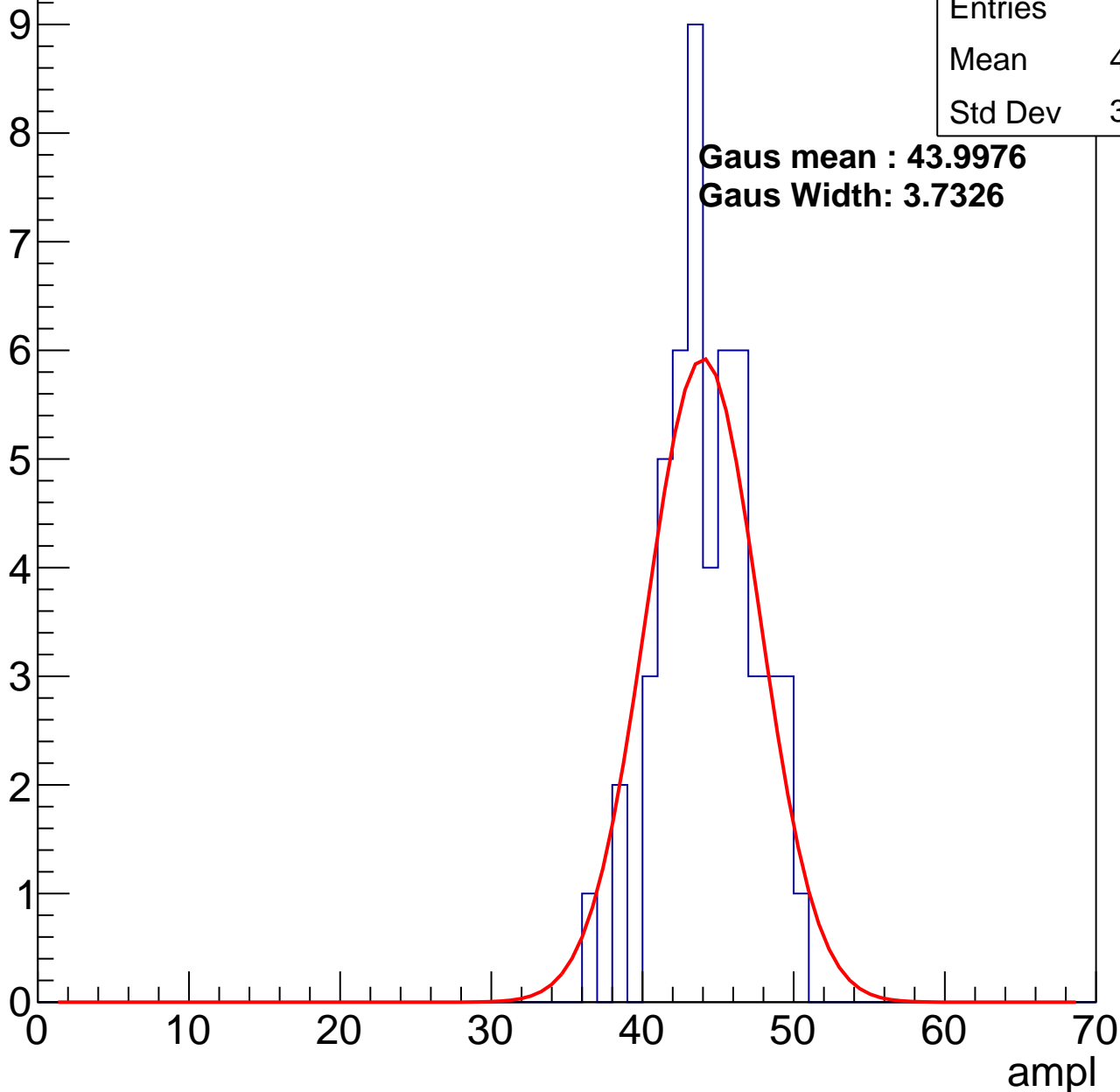
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	43.85
Std Dev	3.028

**Gaus mean : 43.9976**

**Gaus Width: 3.7326**

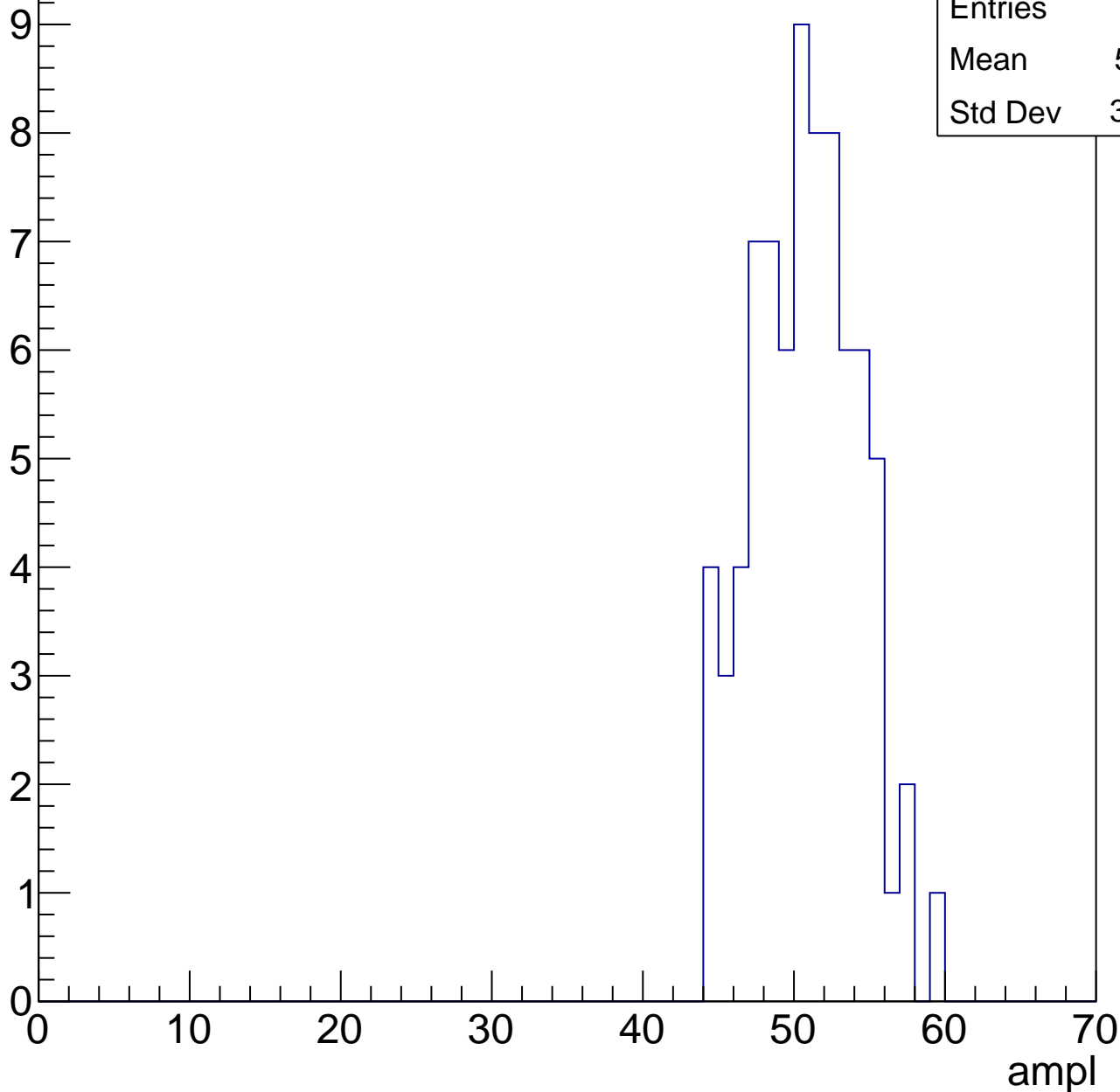


# B1L003S, U23-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	50.31
Std Dev	3.424

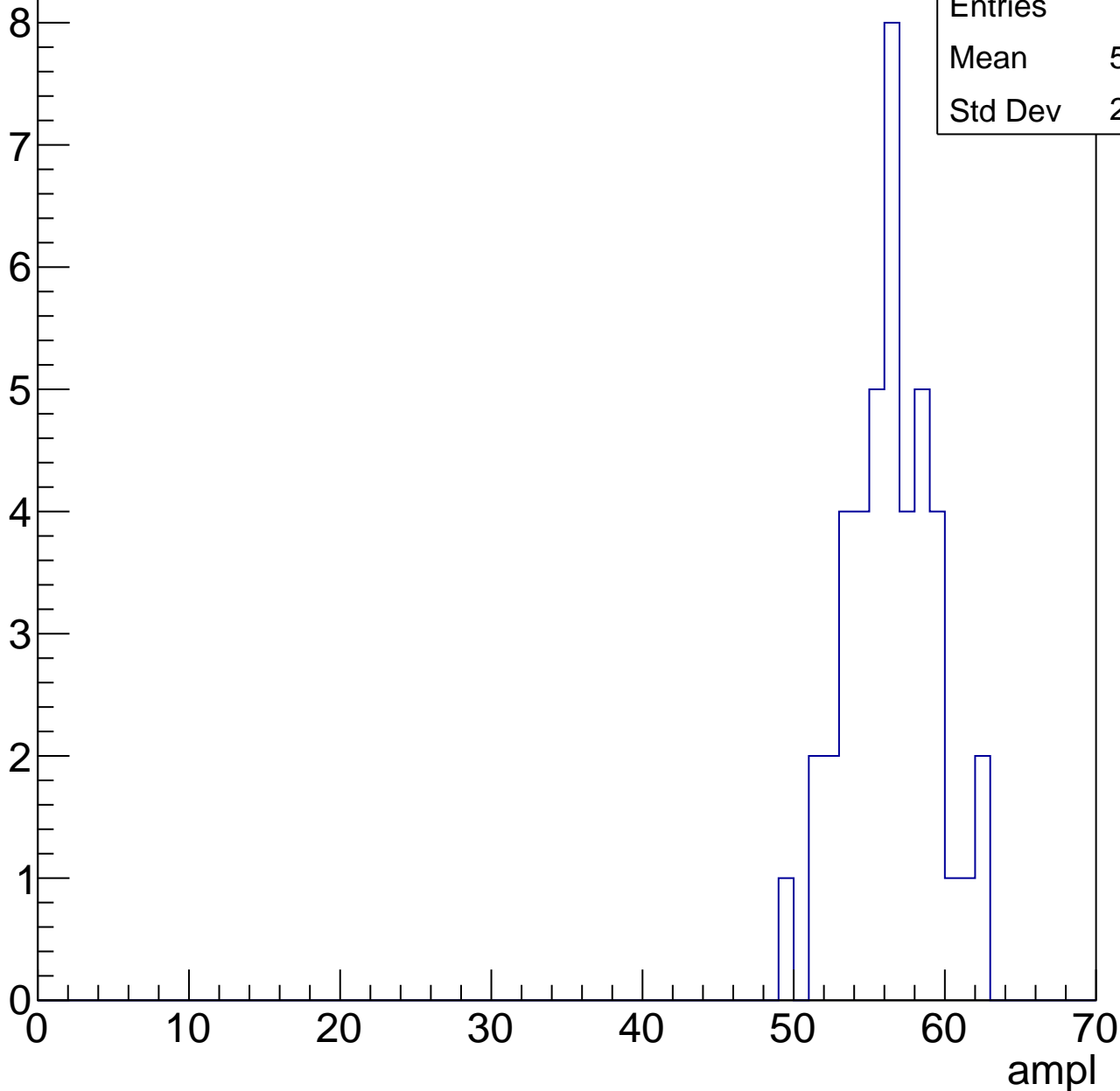


# B1L003S, U23-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	55.93
Std Dev	2.897



# B1L003S, U23-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	59.16
Std Dev	8.666

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

# B1L003S, U23-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	63
Std Dev	0



# B1L003S, U23-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B1L003S, U23-ch25, adc0

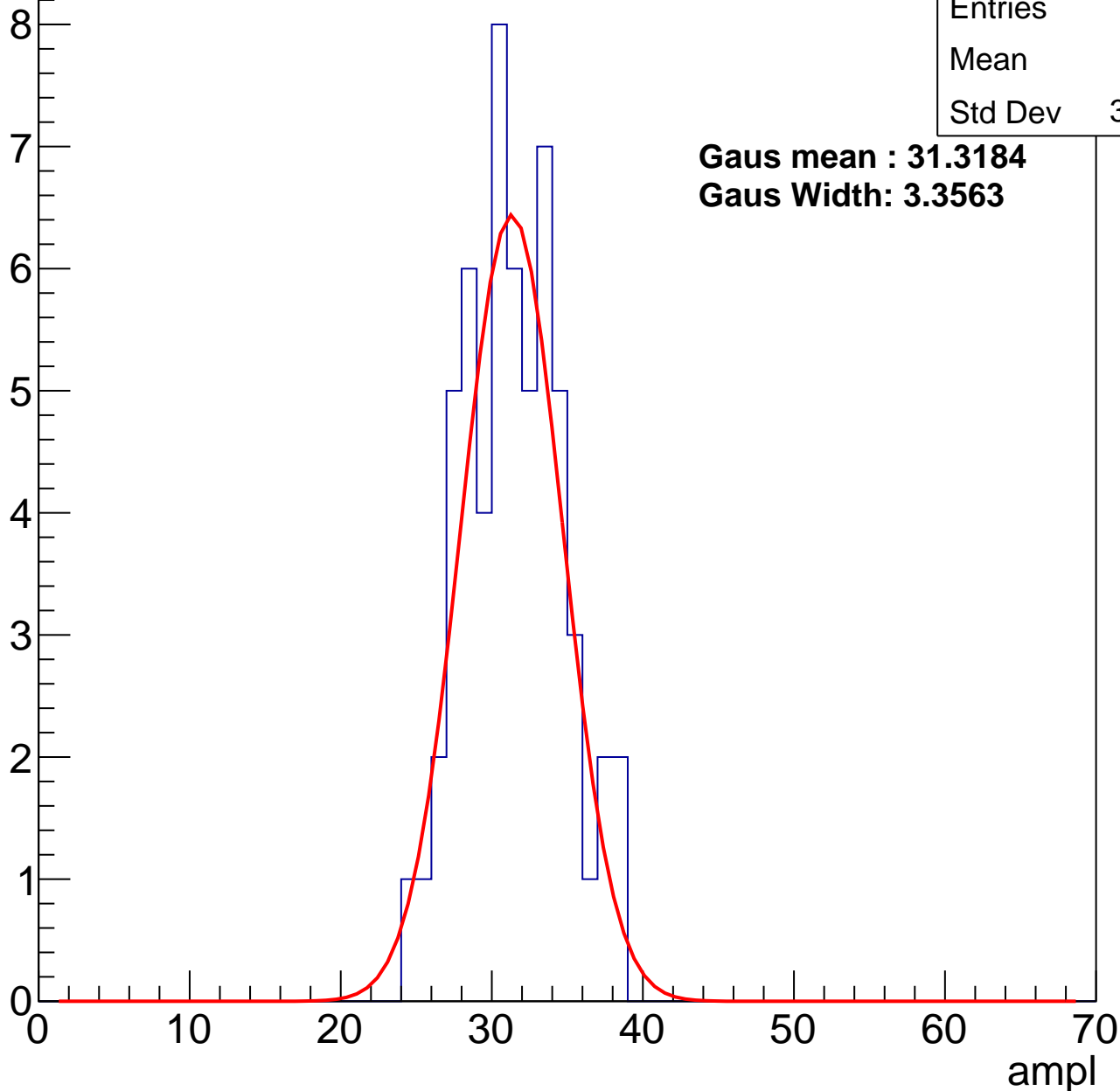
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	31
Std Dev	3.254

**Gaus mean : 31.3184**

**Gaus Width: 3.3563**



# B1L003S, U23-ch25, adc1

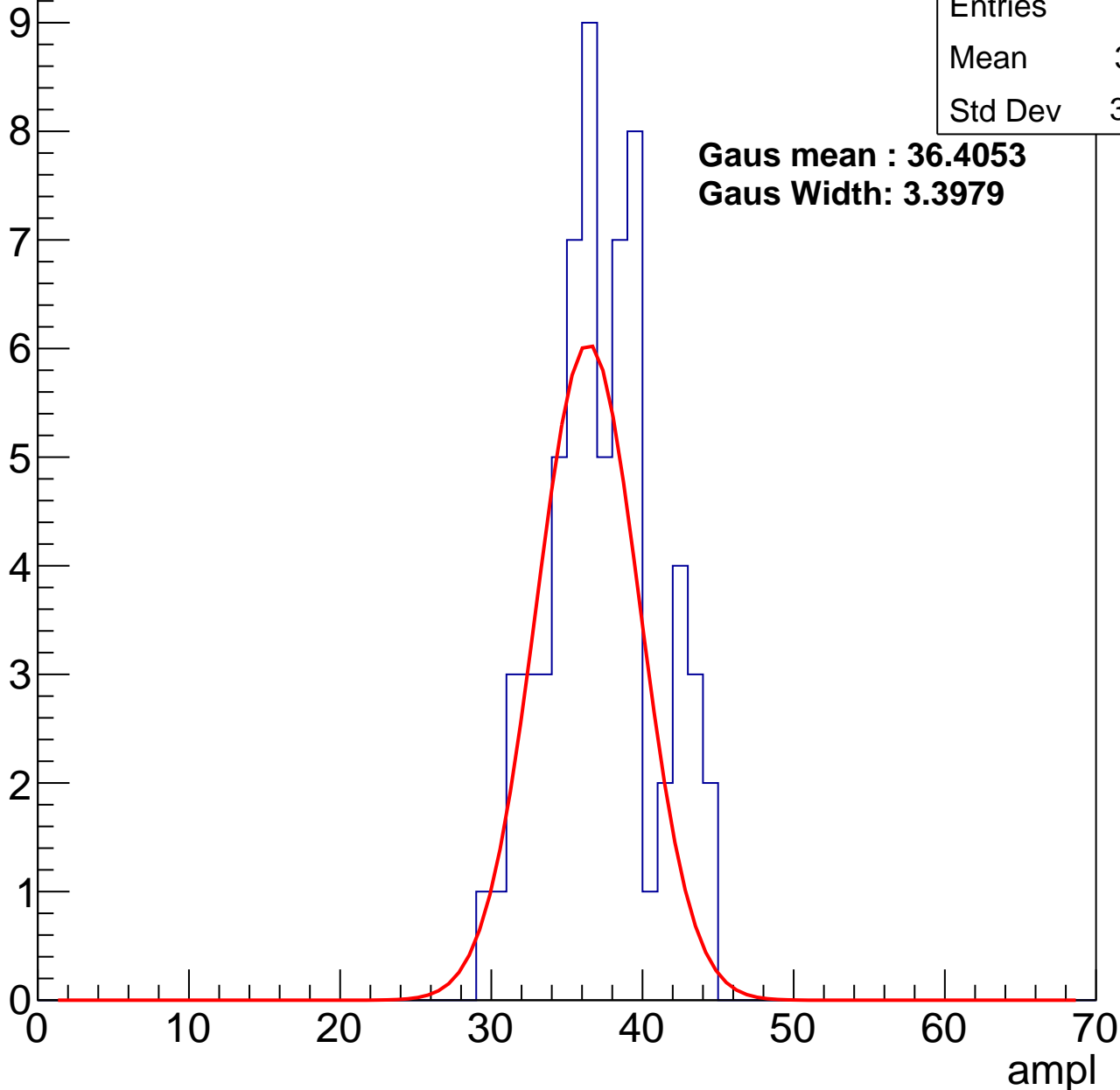
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	36.81
Std Dev	3.557

**Gaus mean : 36.4053**

**Gaus Width: 3.3979**



# B1L003S, U23-ch25, adc2

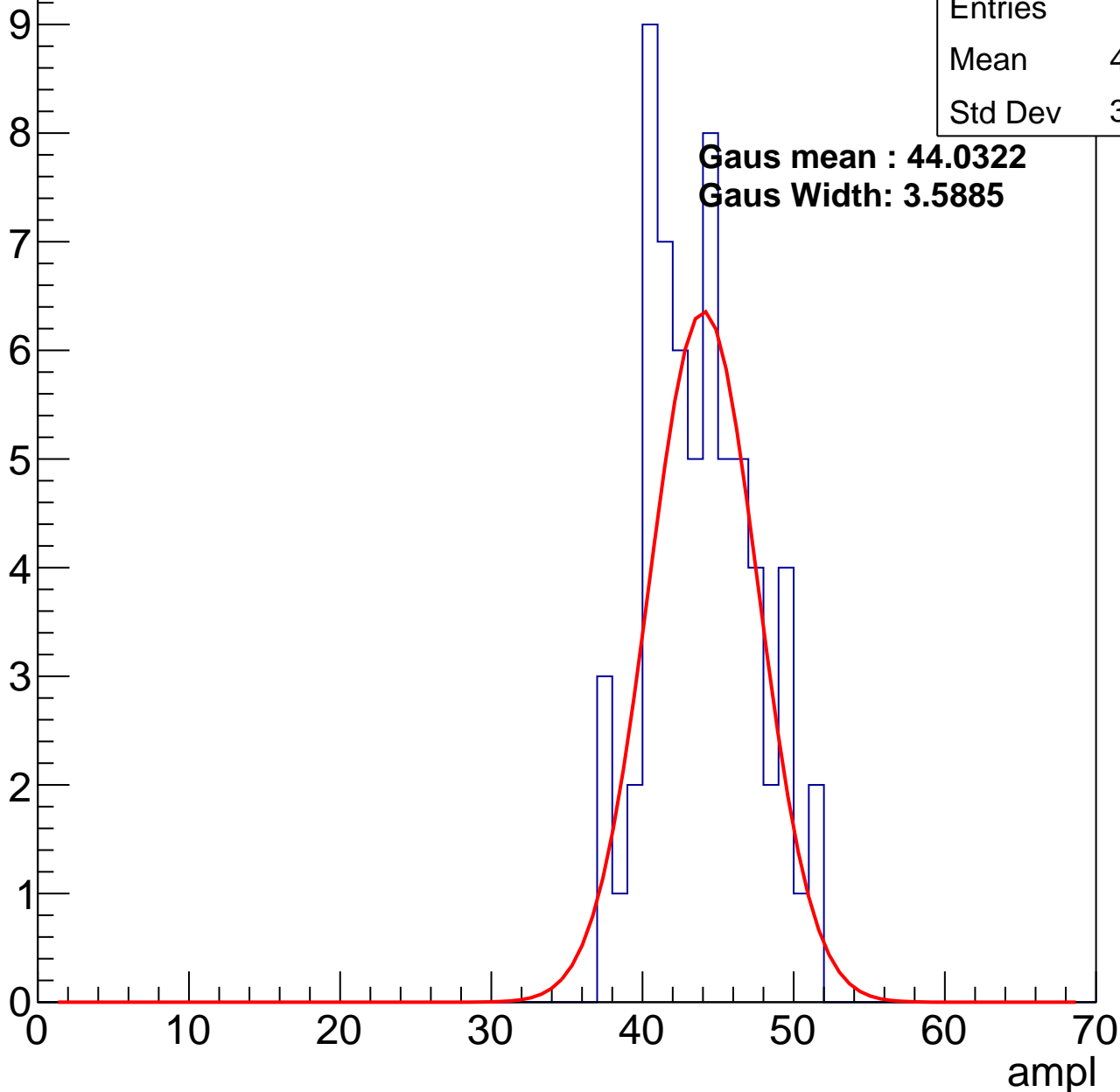
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.44
Std Dev	3.482

**Gaus mean : 44.0322**

**Gaus Width: 3.5885**

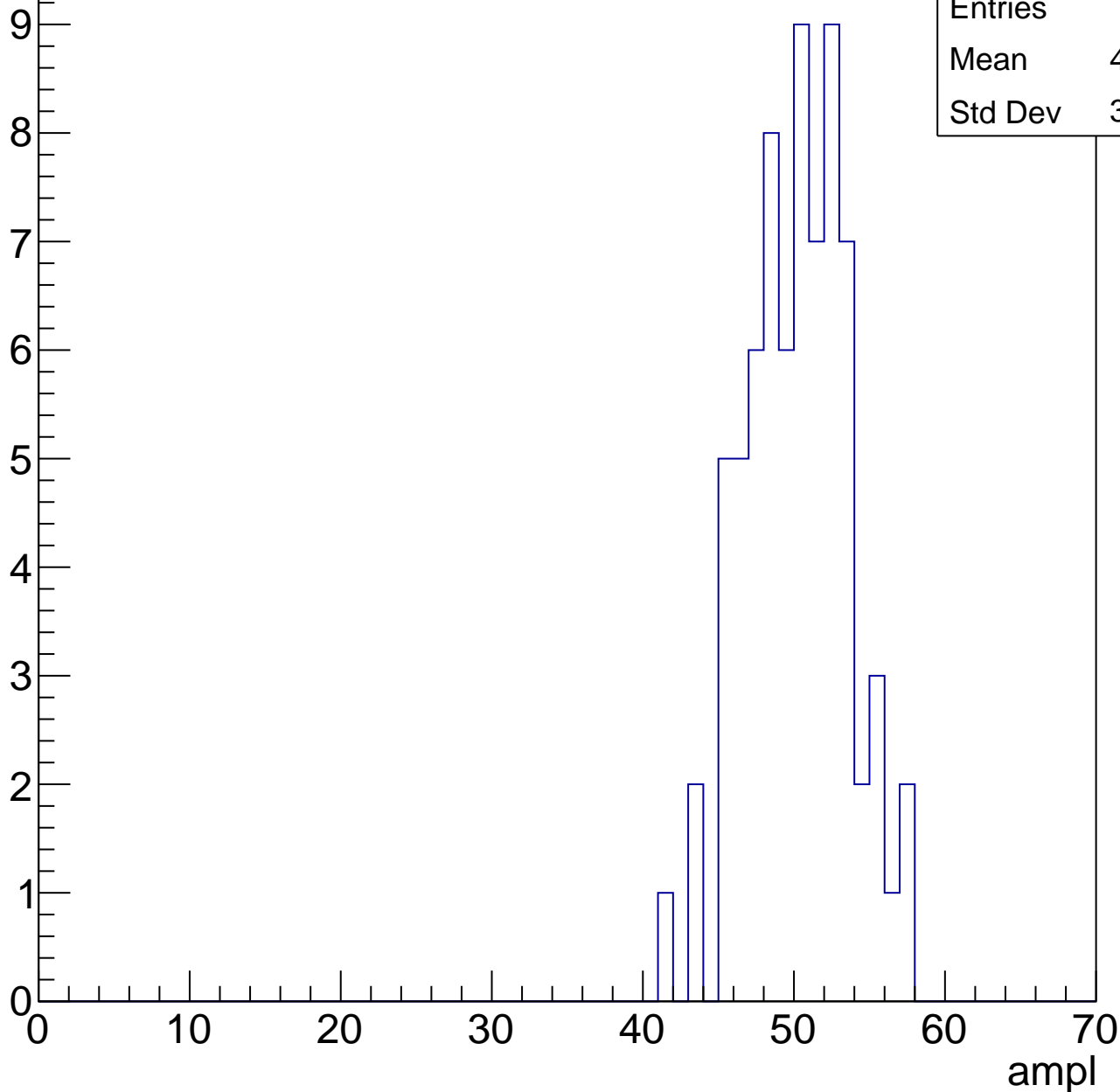


# B1L003S, U23-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	49.74
Std Dev	3.348

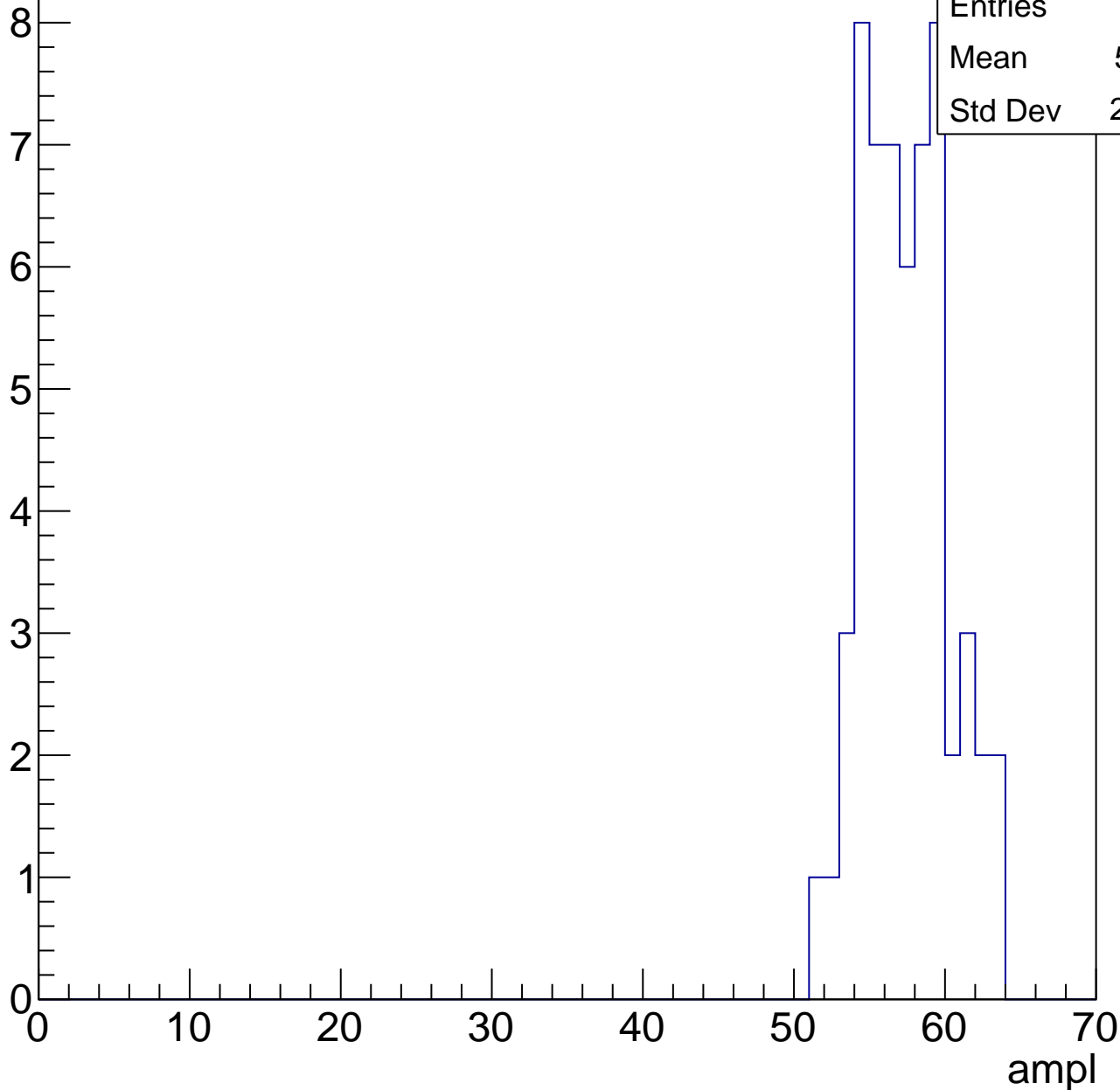


# B1L003S, U23-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

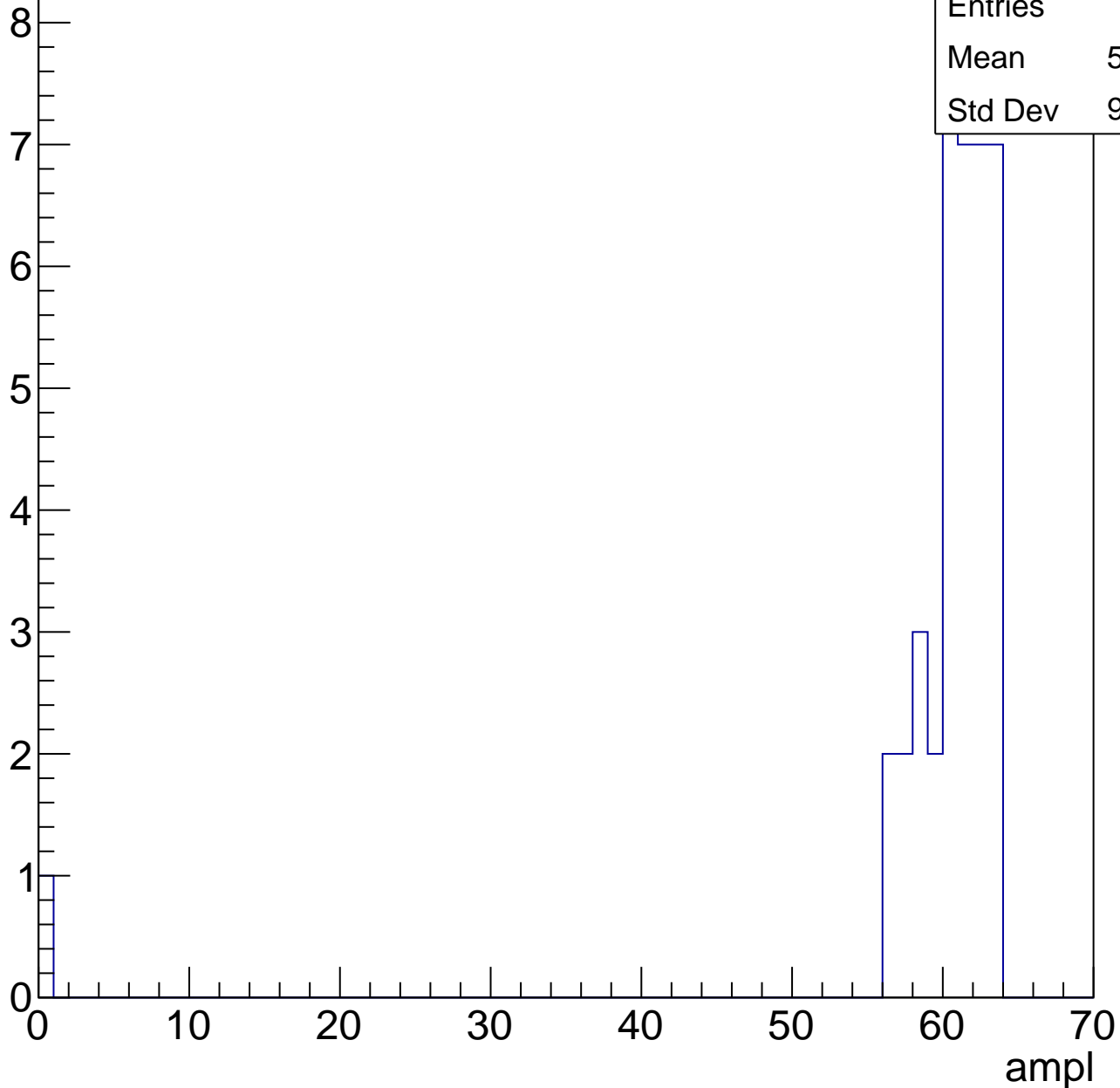
Entries	57
Mean	56.91
Std Dev	2.786



# B1L003S, U23-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	39
Mean	58.97
Std Dev	9.768

# B1L003S, U23-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch26, adc0

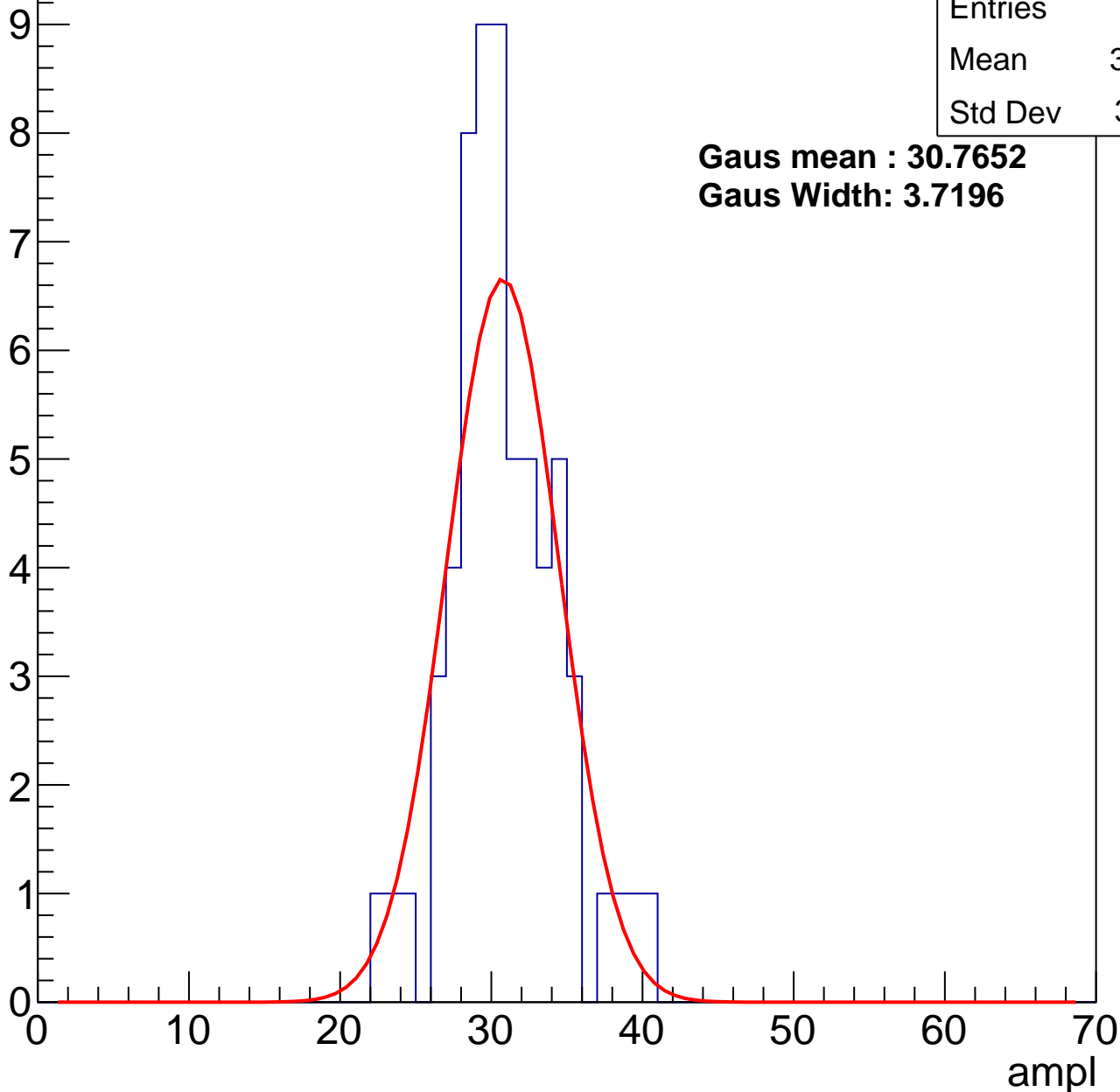
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	30.42
Std Dev	3.531

**Gaus mean : 30.7652**

**Gaus Width: 3.7196**



# B1L003S, U23-ch26, adc1

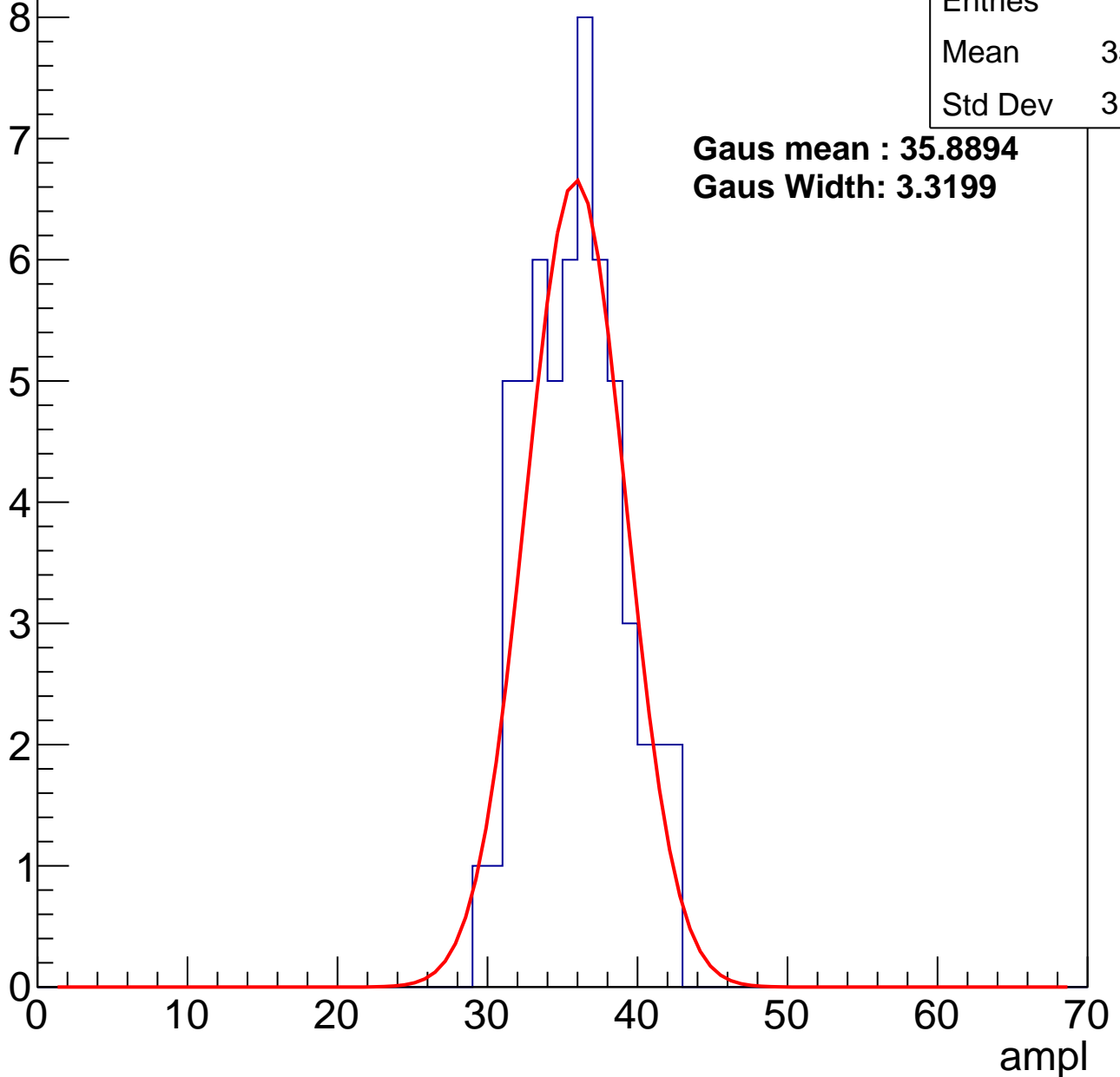
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.35
Std Dev	3.115

**Gaus mean : 35.8894**

**Gaus Width: 3.3199**



# B1L003S, U23-ch26, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.91
Std Dev	3.18

**Gaus mean : 42.3932**

**Gaus Width: 3.4766**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl



# B1L003S, U23-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	65
Mean	48.71
Std Dev	3.224

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

1

1

5

4

10

6

8

7

6

5

4

3

1

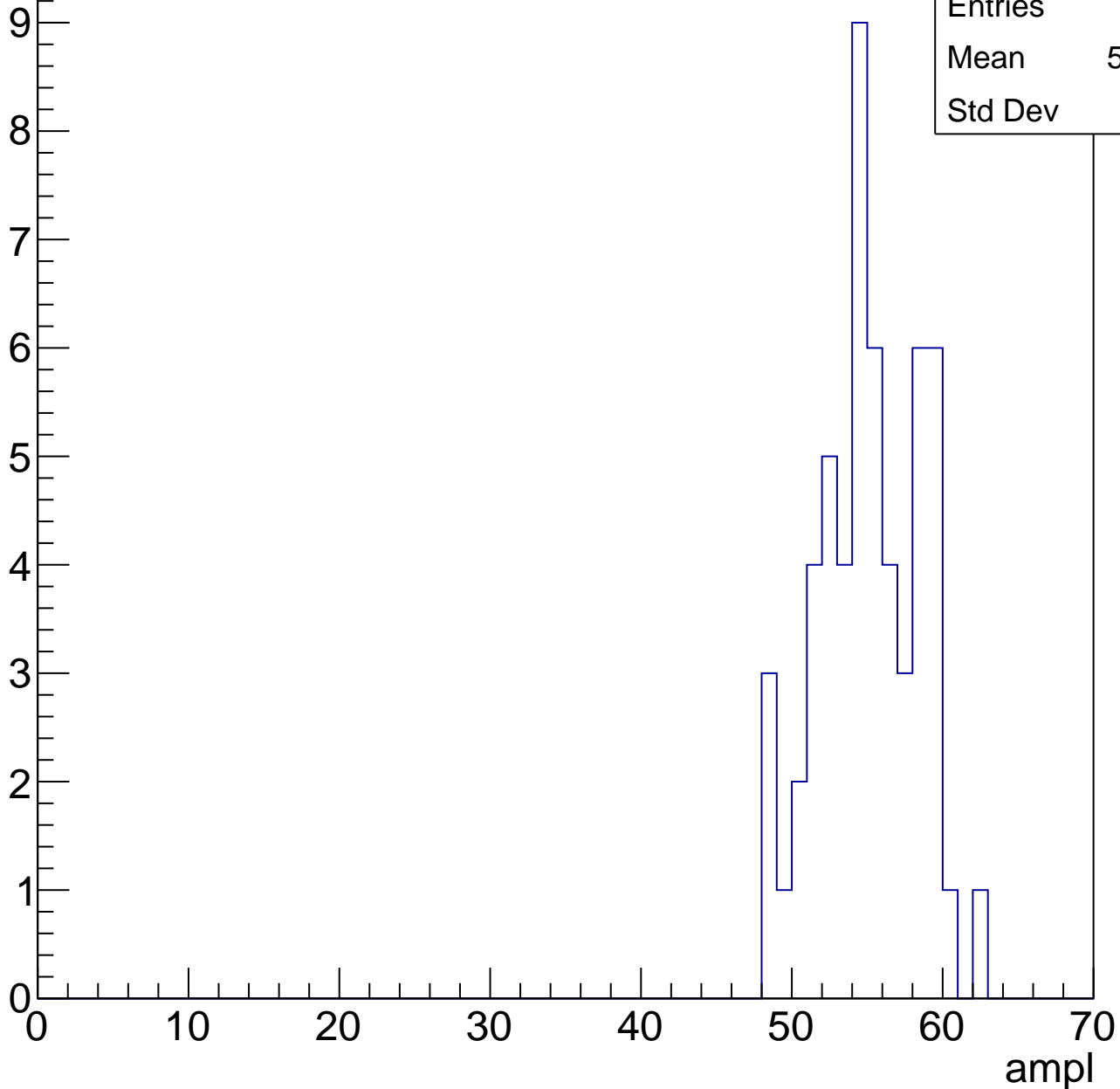
0

# B1L003S, U23-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.62
Std Dev	3.3

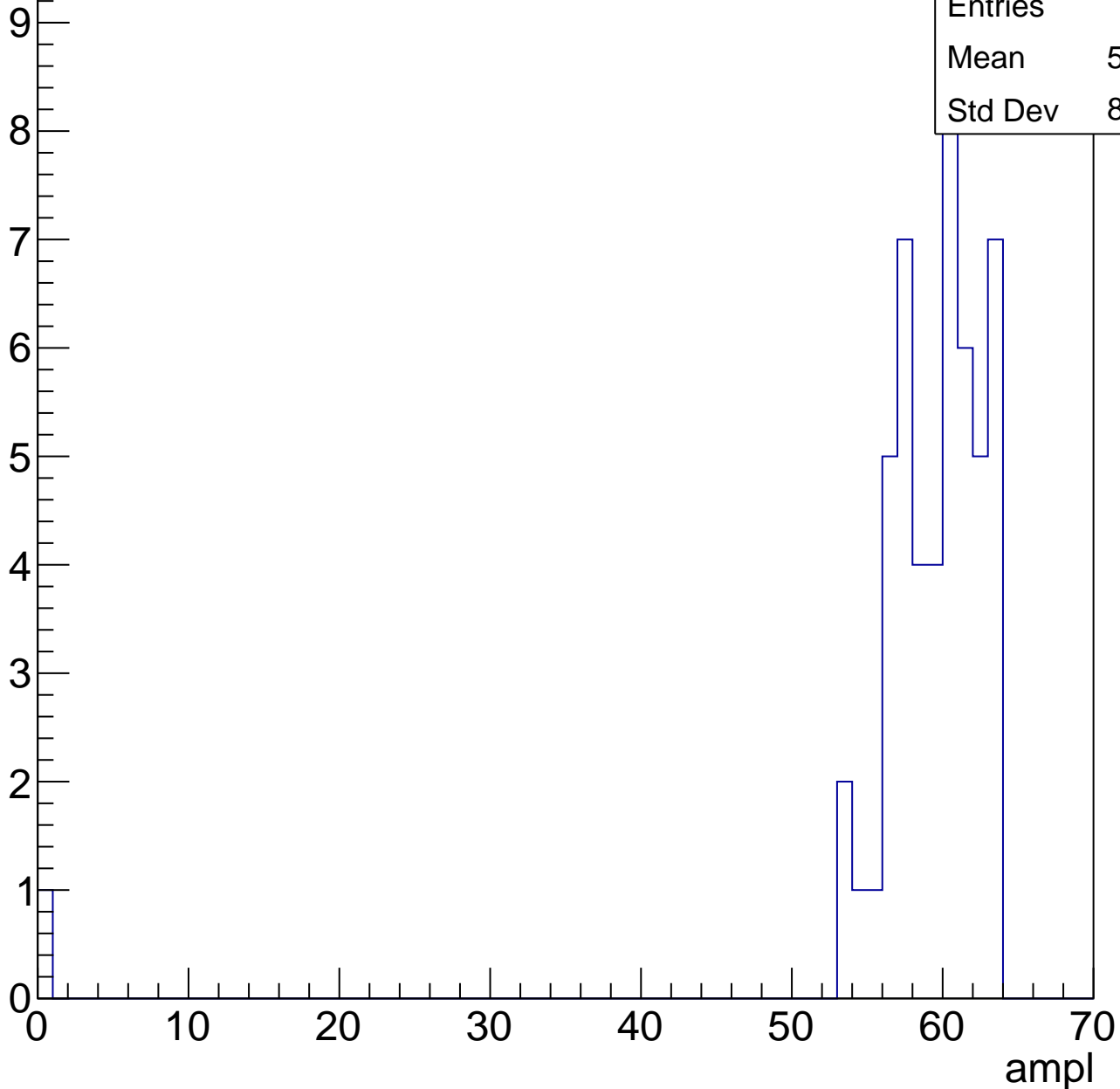


# B1L003S, U23-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

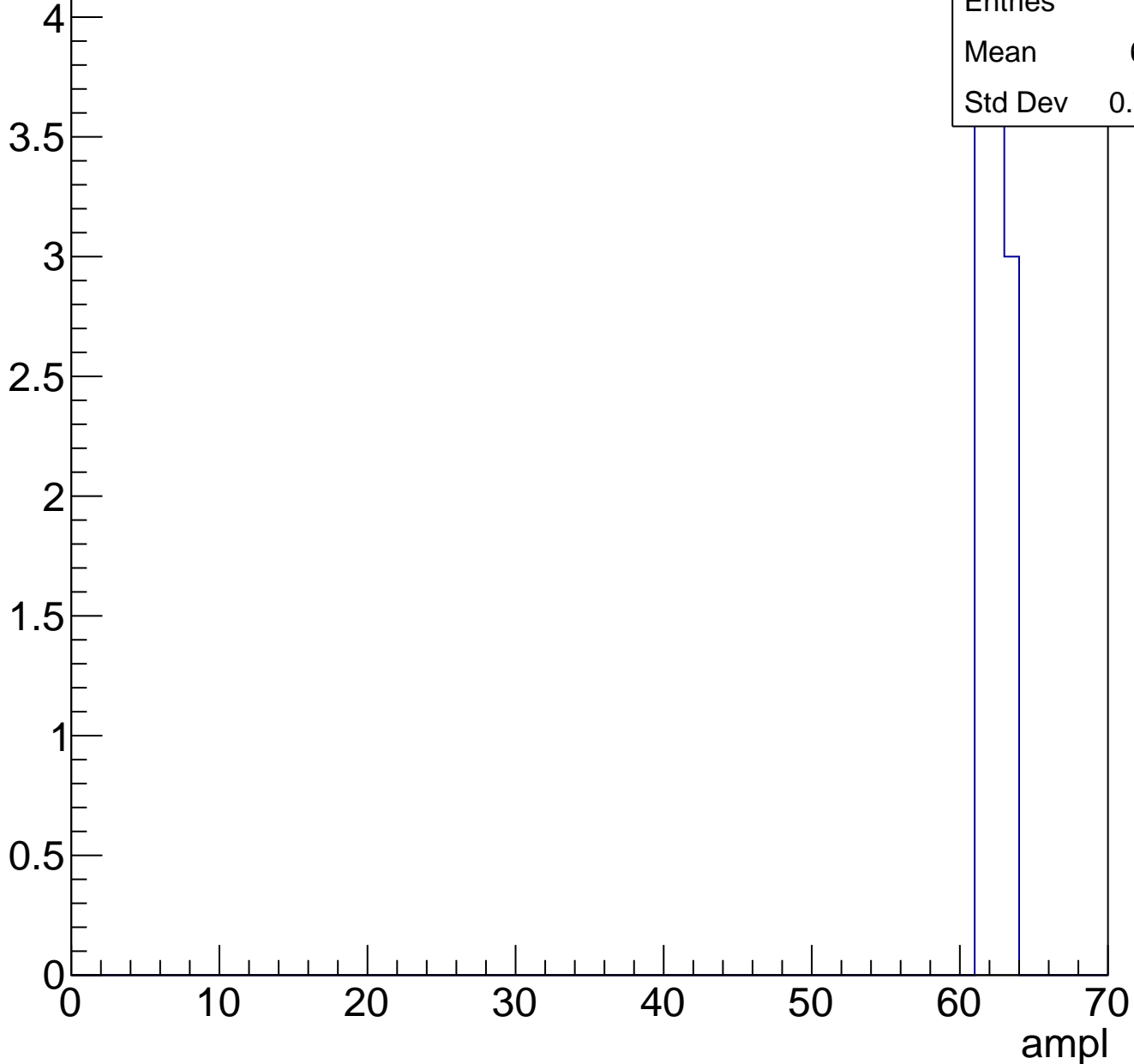
Entries	52
Mean	58.06
Std Dev	8.565



# B1L003S, U23-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch27, adc0

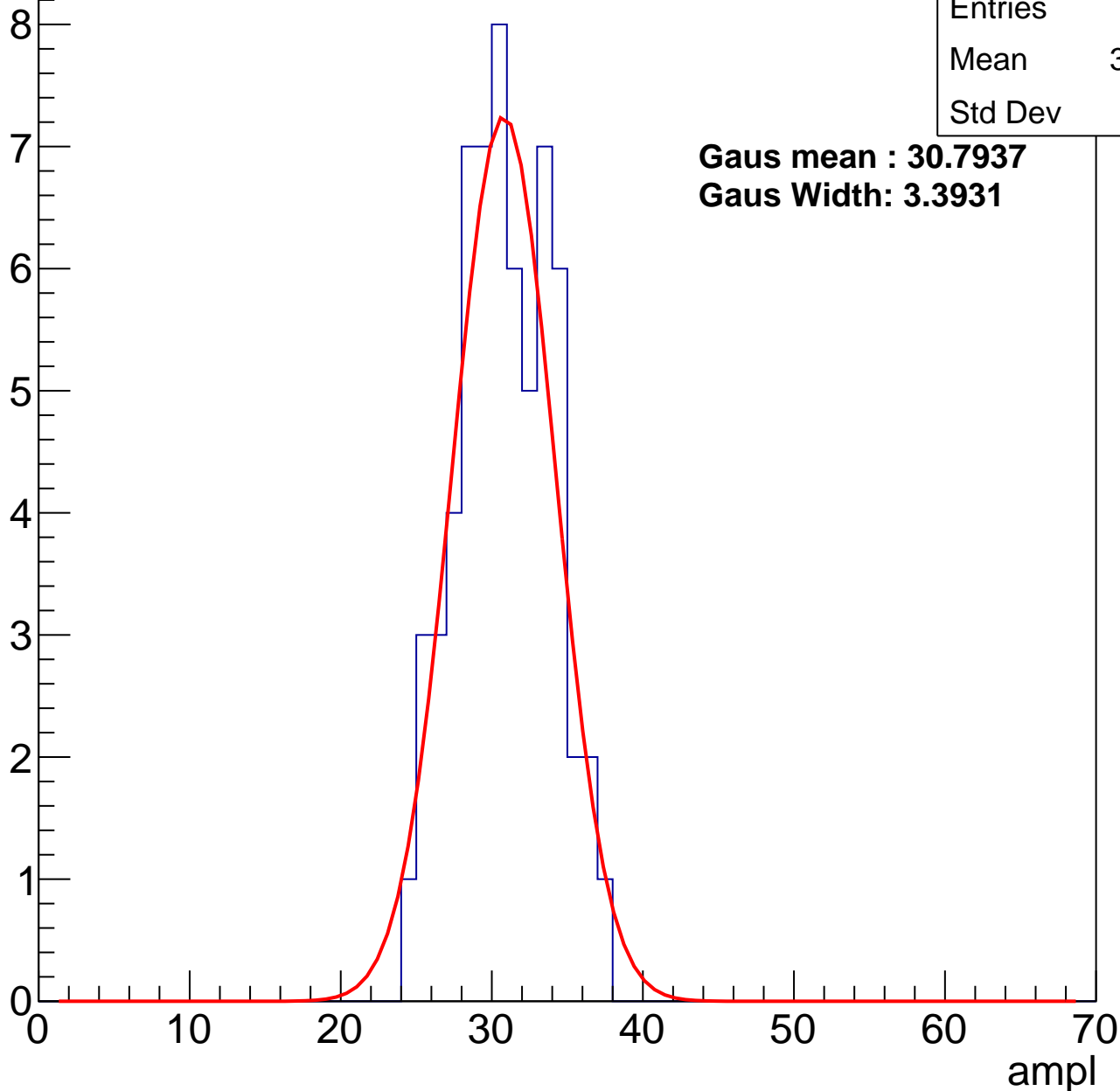
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	30.39
Std Dev	3.05

**Gaus mean : 30.7937**

**Gaus Width: 3.3931**



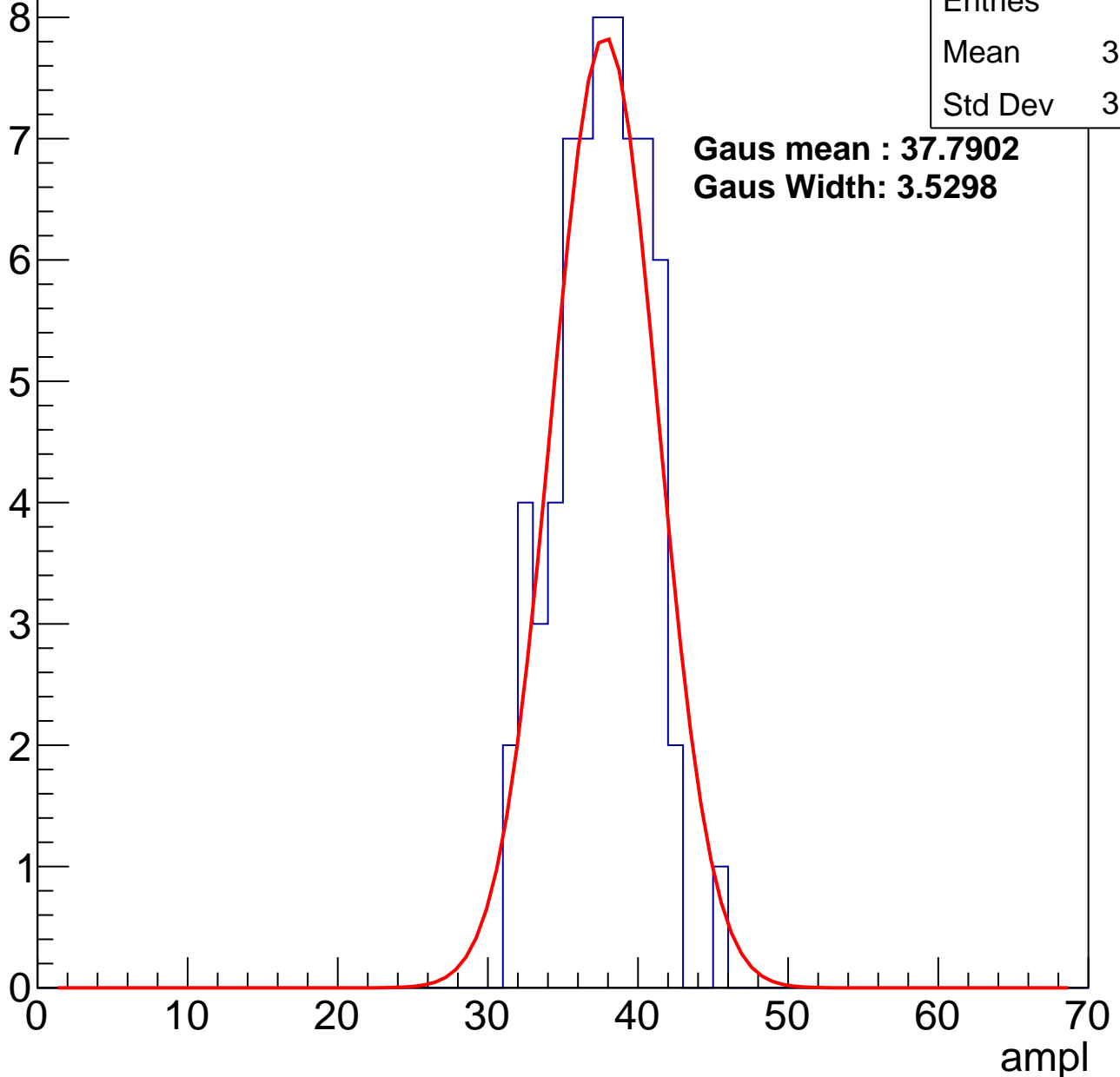
# B1L003S, U23-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	37.12
Std Dev	3.013

**Gaus mean : 37.7902**  
**Gaus Width: 3.5298**



# B1L003S, U23-ch27, adc2

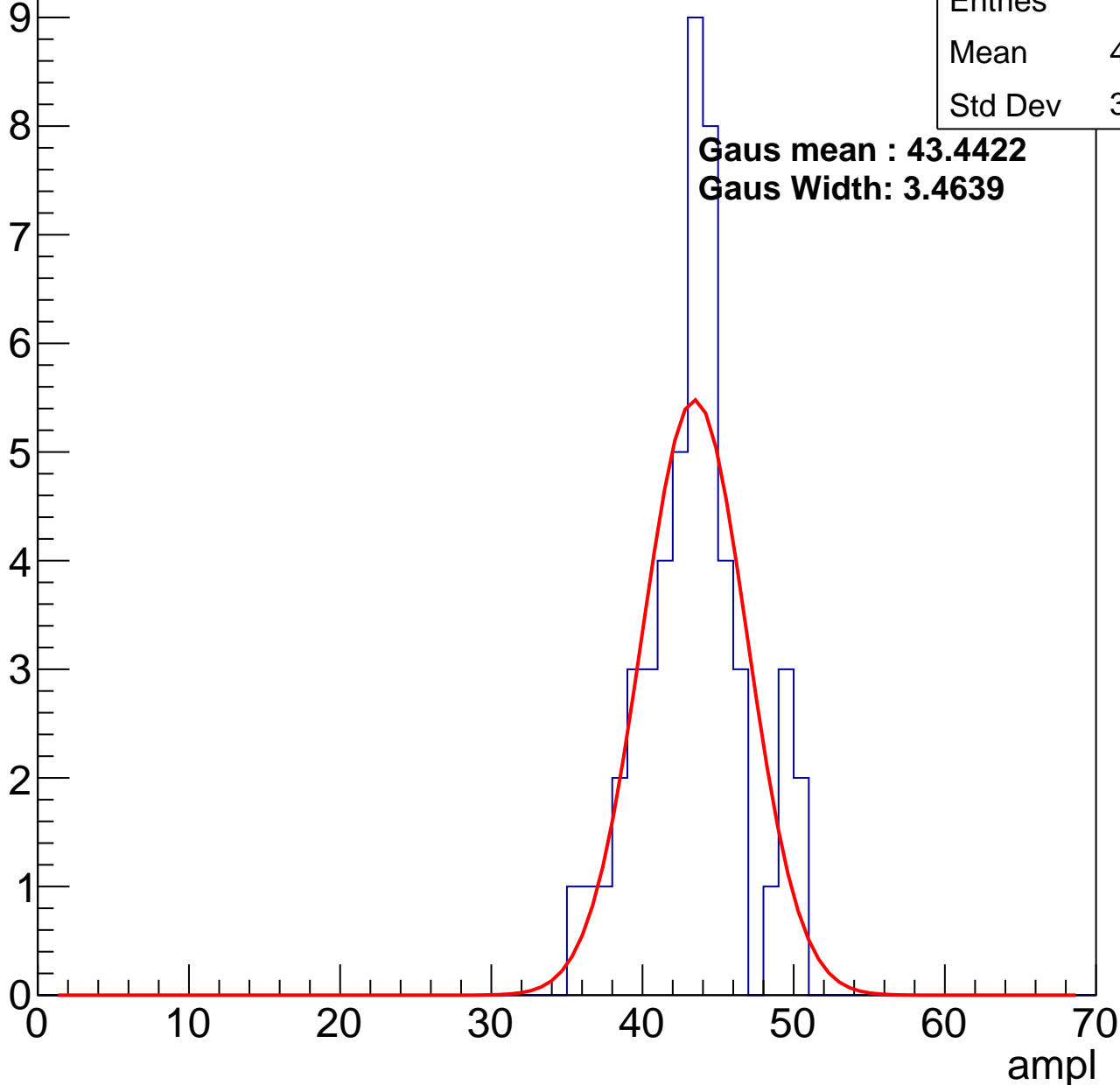
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	42.94
Std Dev	3.397

**Gaus mean : 43.4422**

**Gaus Width: 3.4639**



# B1L003S, U23-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

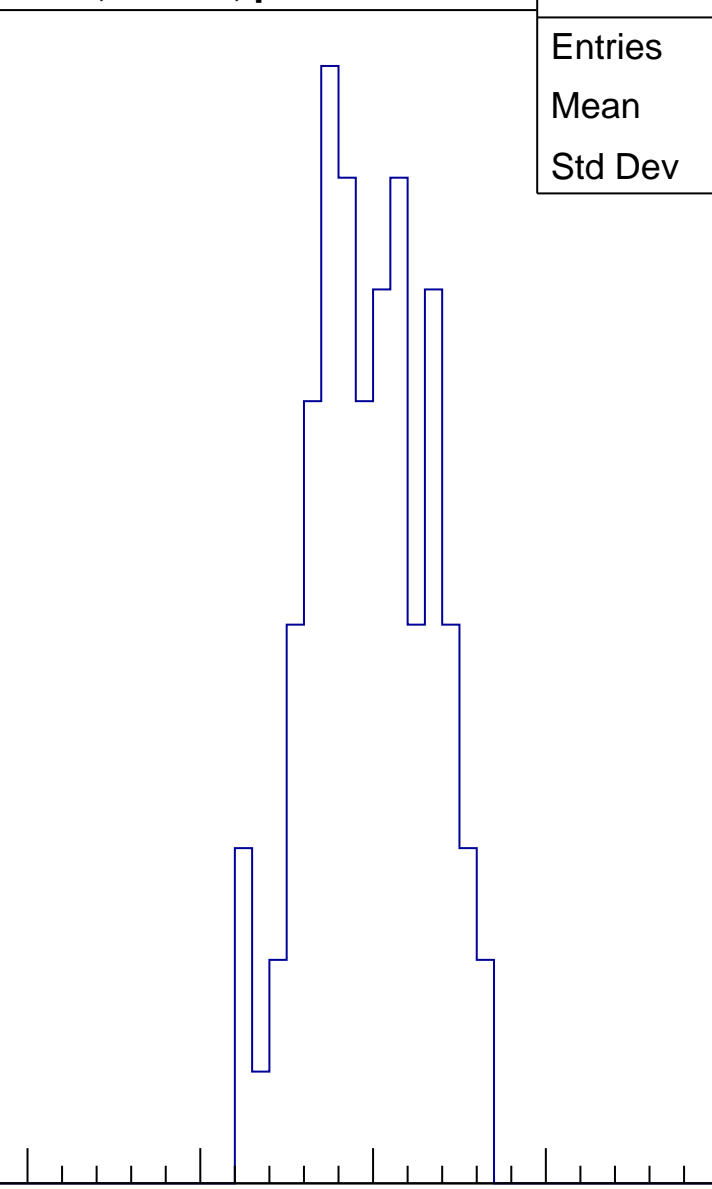
Entries	84
Mean	49.27
Std Dev	3.392

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

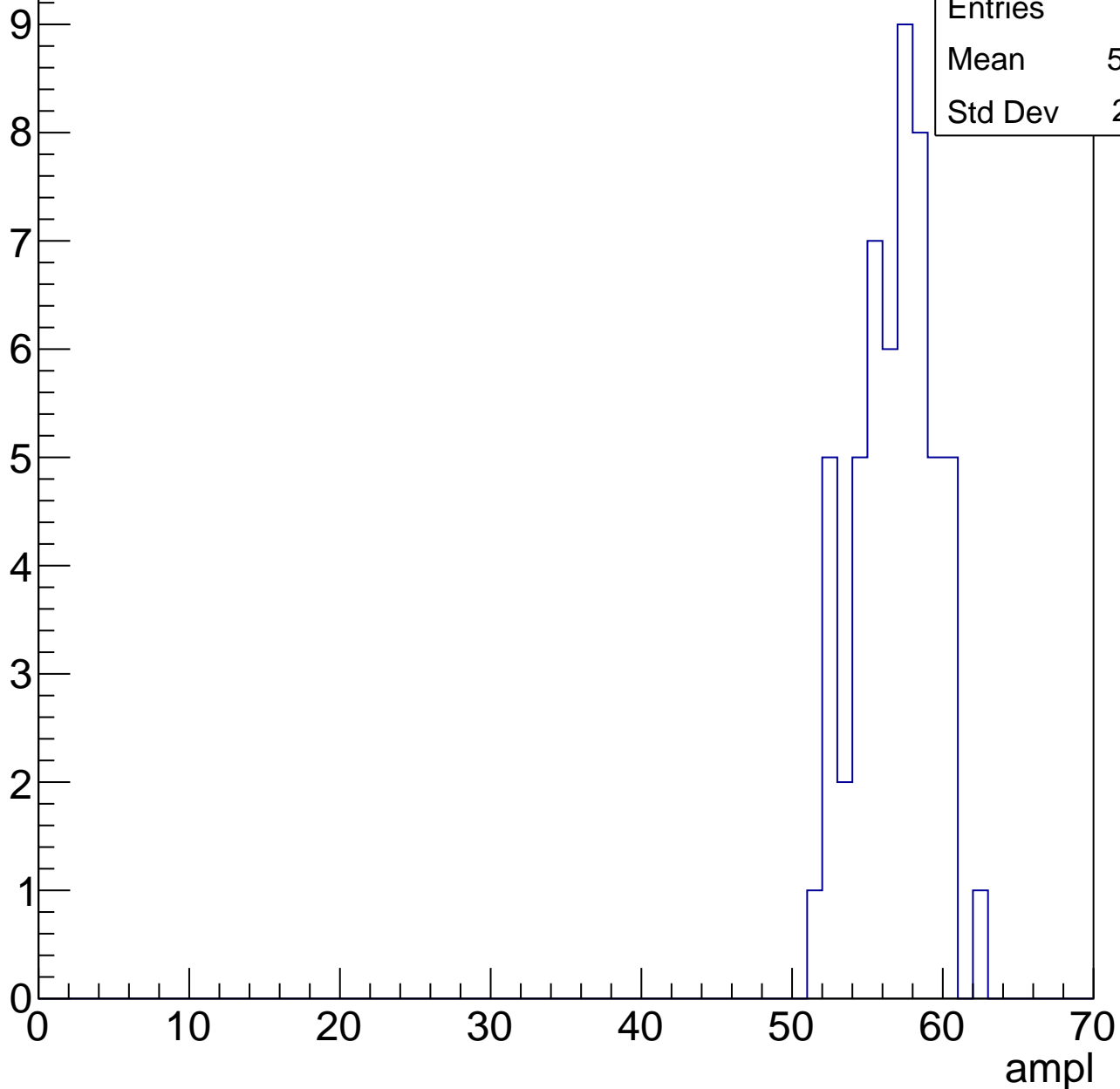
ampl



# B1L003S, U23-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

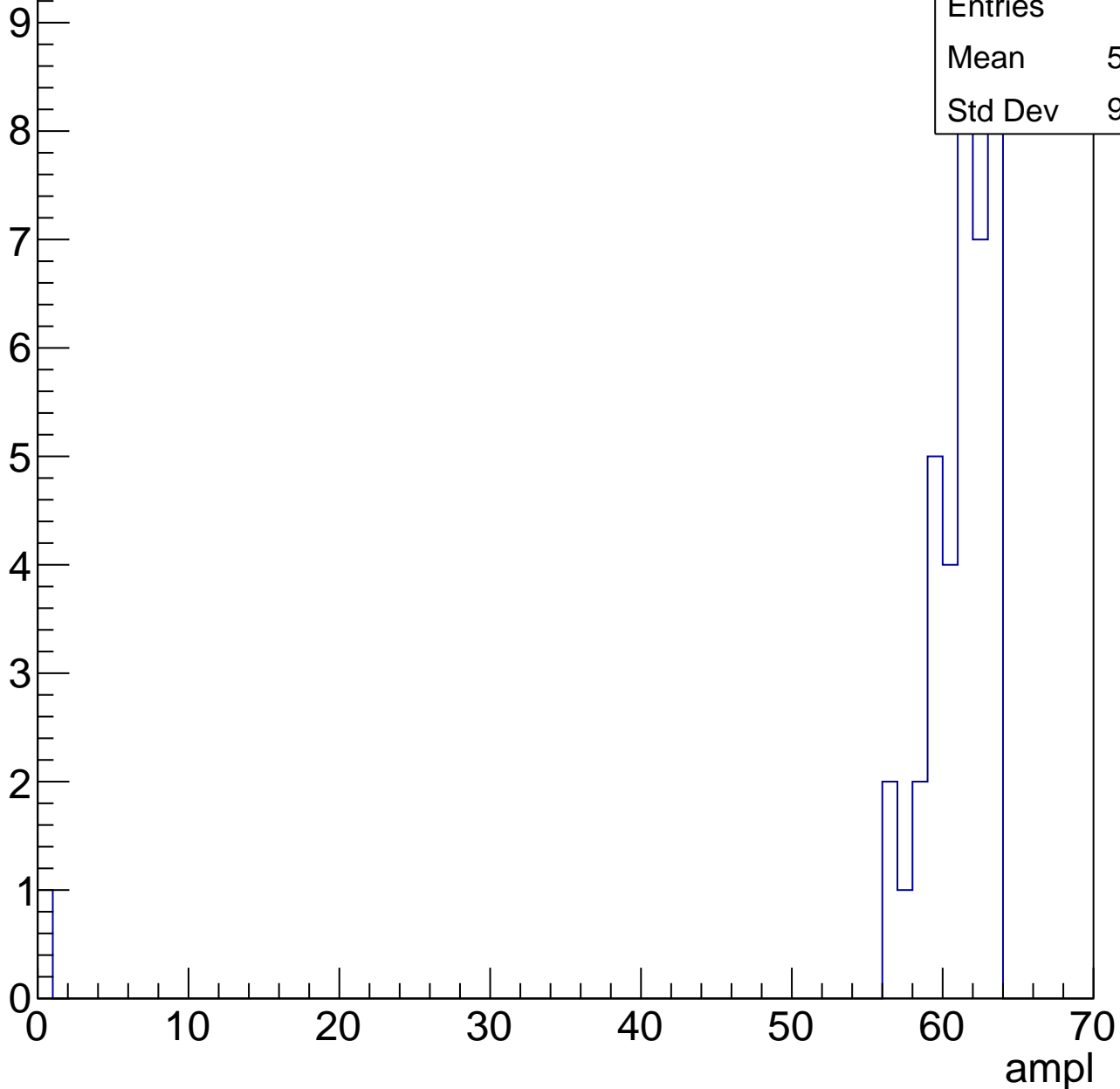


# B1L003S, U23-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

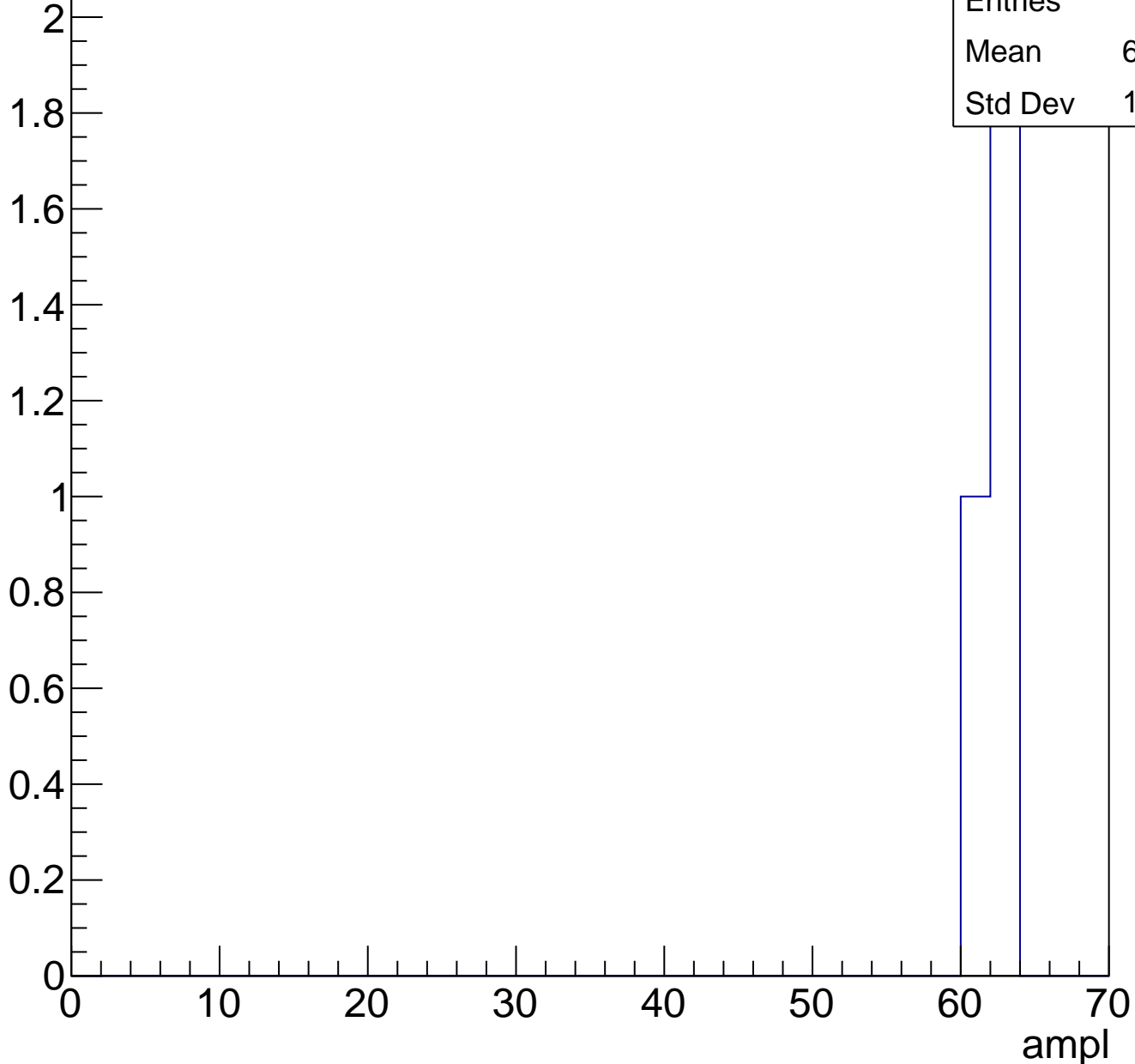
Entries	39
Mean	59.15
Std Dev	9.786



# B1L003S, U23-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch28, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	80
Mean	29.19
Std Dev	3.432

**Gaus mean : 30.0344**

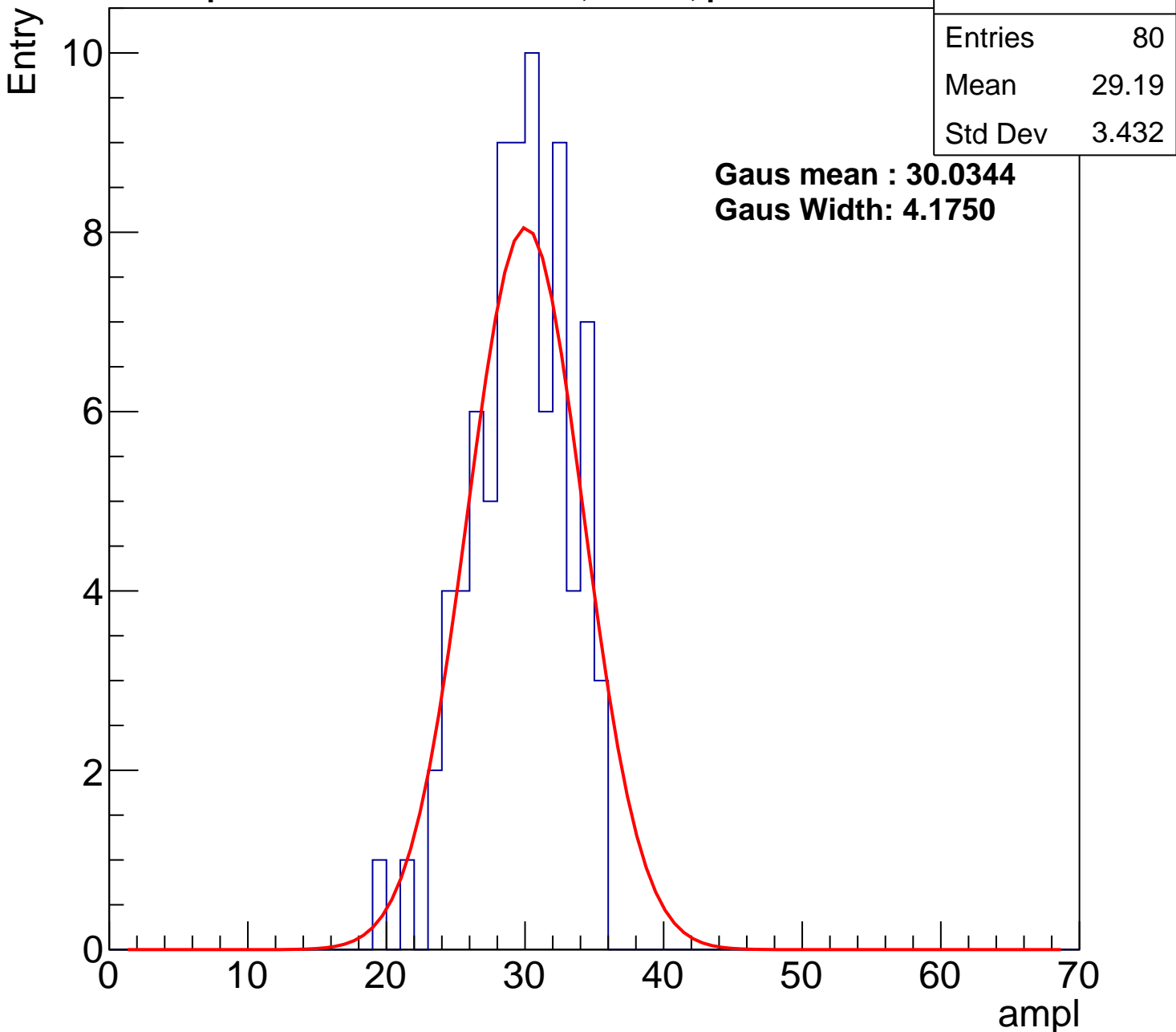
**Gaus Width: 4.1750**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch28, adc1

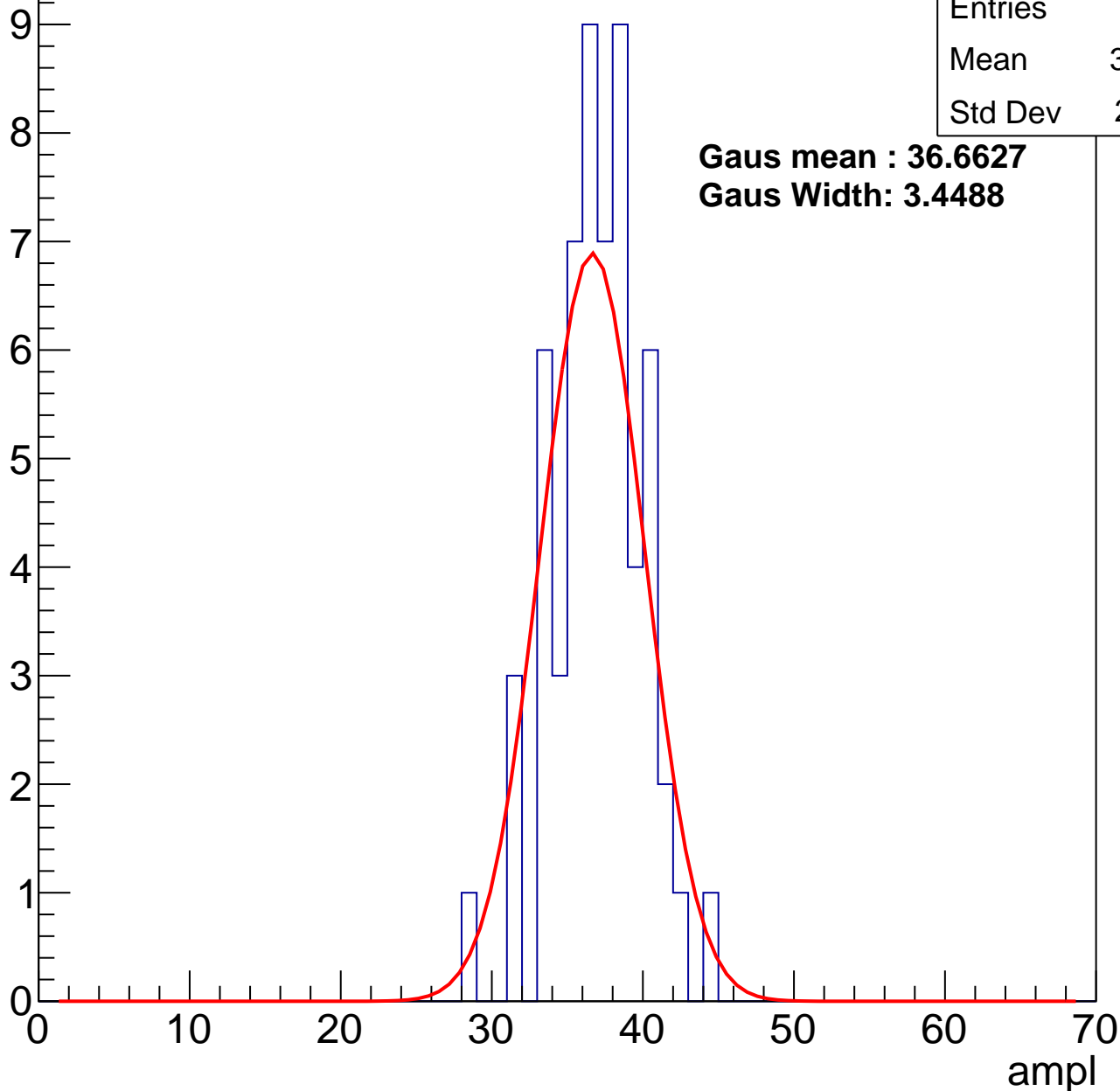
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.53
Std Dev	2.971

**Gaus mean : 36.6627**

**Gaus Width: 3.4488**



# B1L003S, U23-ch28, adc2

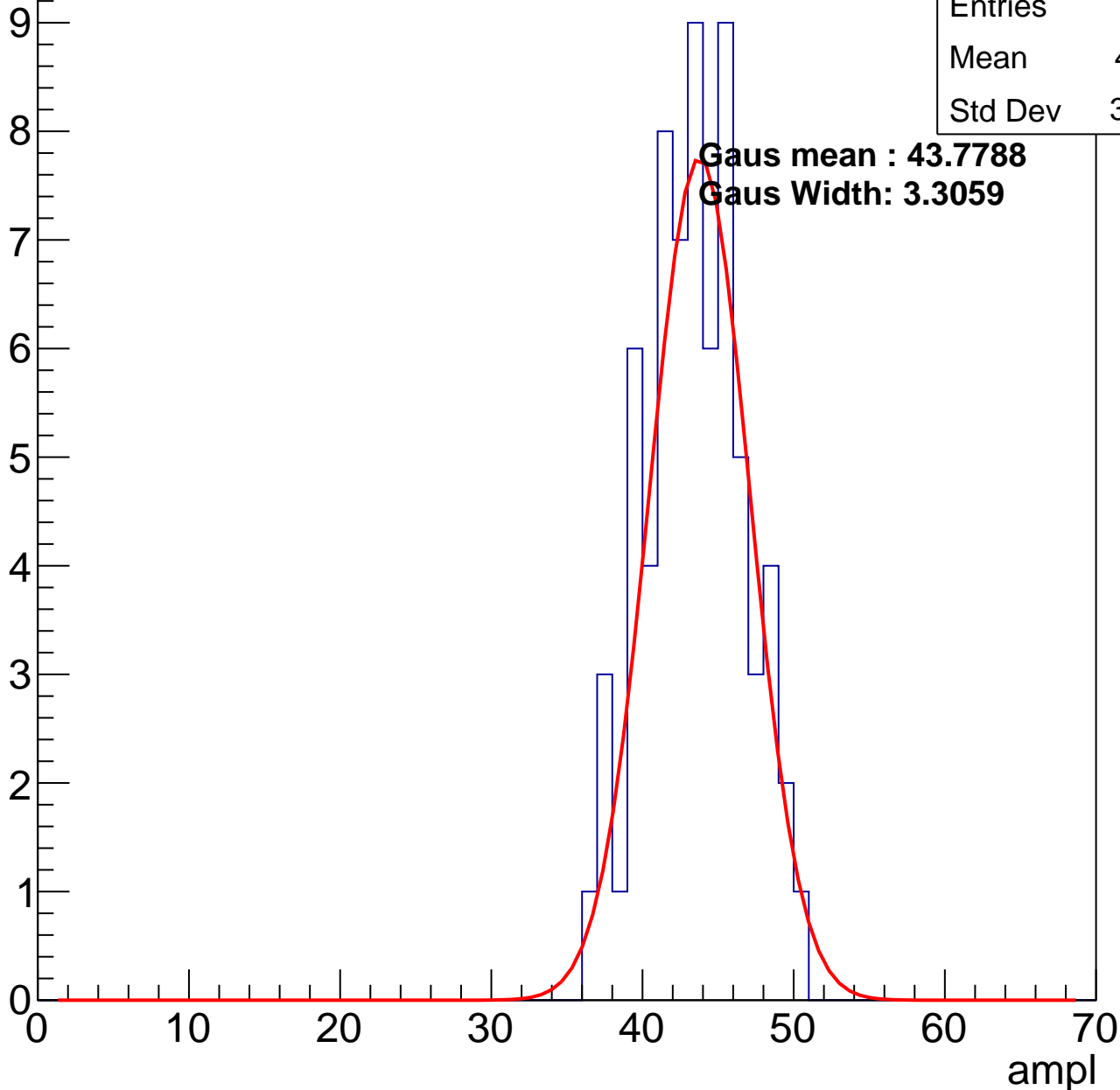
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.01
Std Dev	3.205

**Gaus mean : 43.7788**

**Gaus Width: 3.3059**

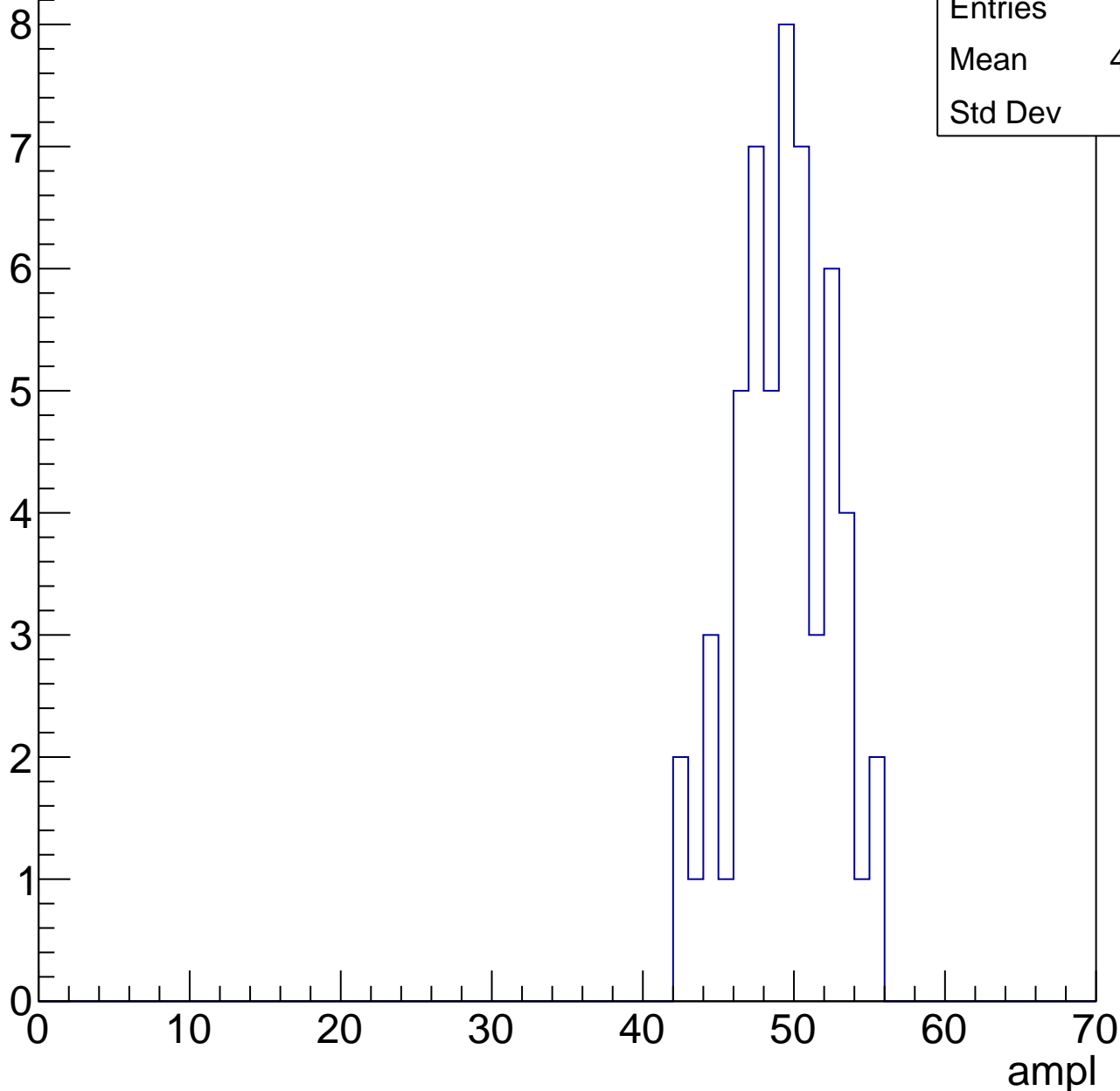


# B1L003S, U23-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	48.84
Std Dev	3.12

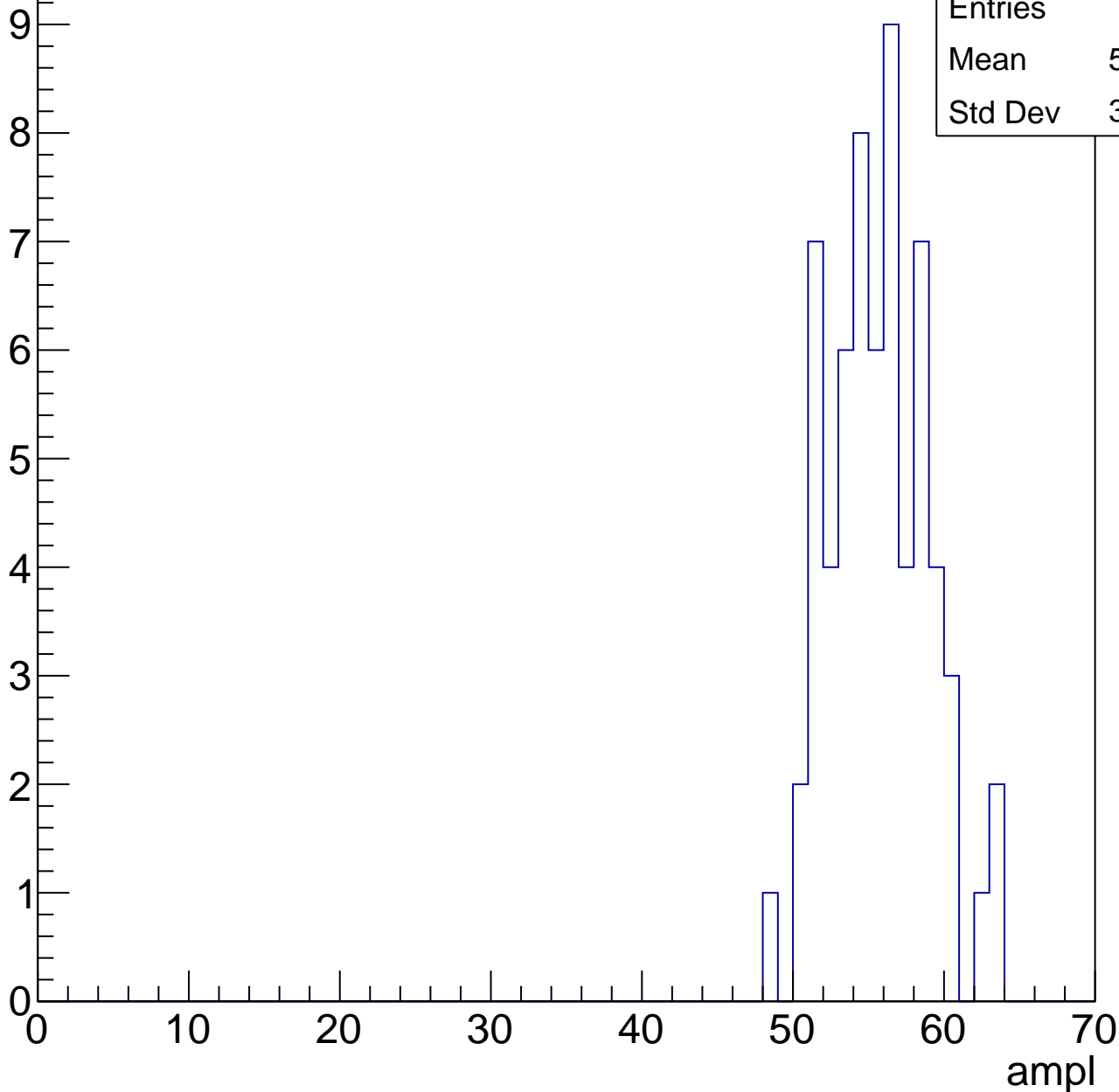


# B1L003S, U23-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.23
Std Dev	3.258

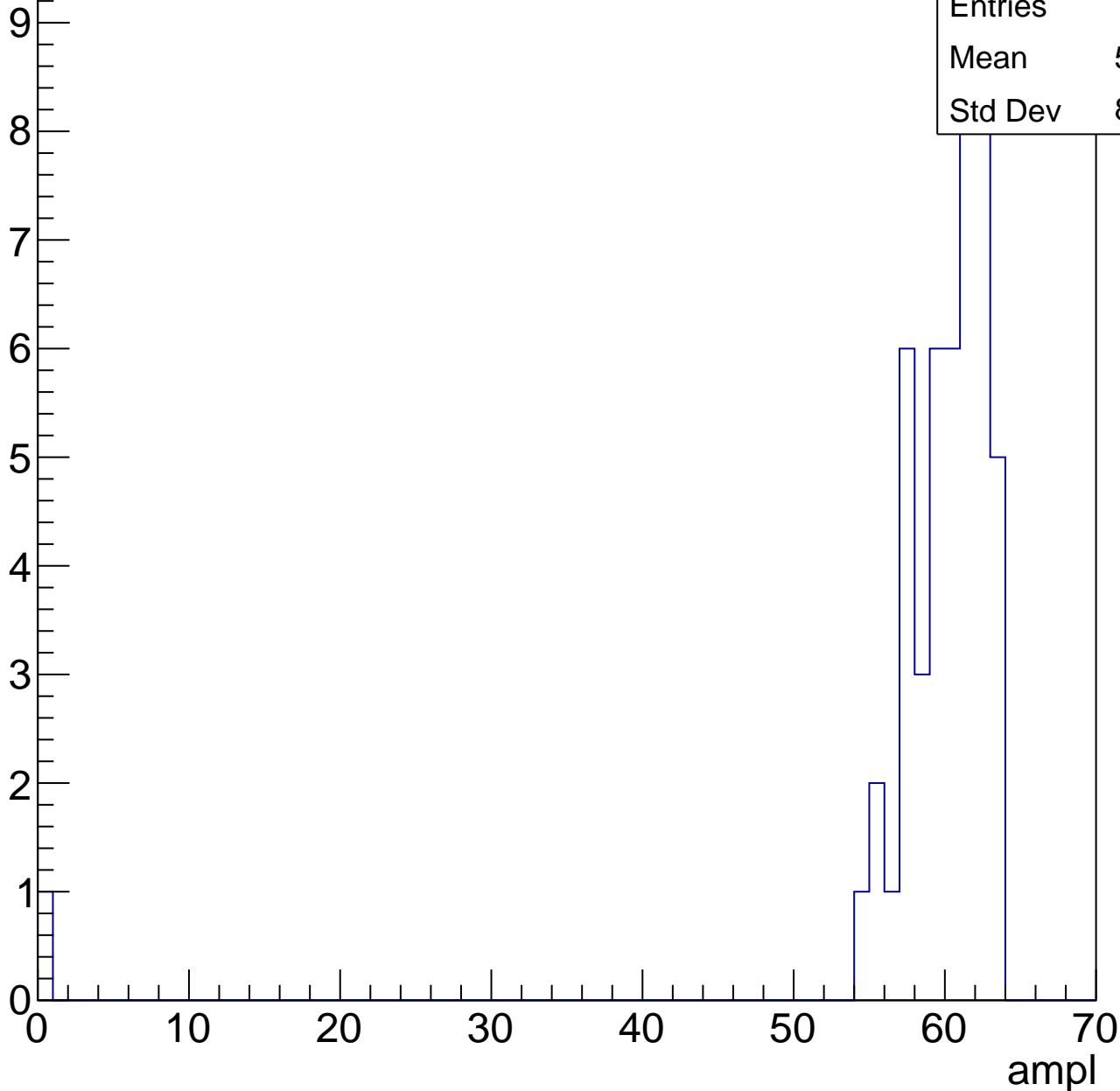


# B1L003S, U23-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.61
Std Dev	8.771



# B1L003S, U23-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch29, adc0

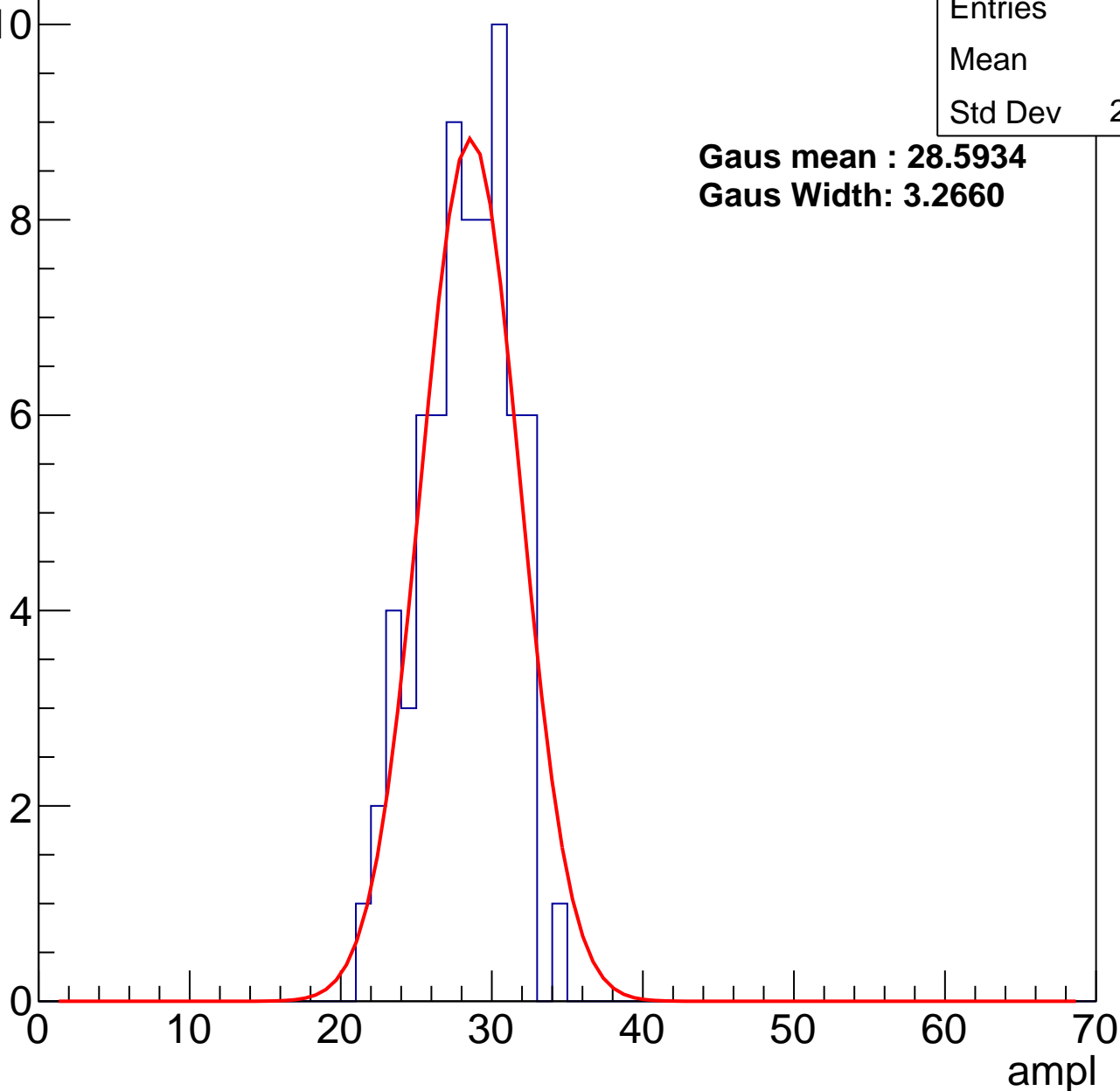
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	27.8
Std Dev	2.896

**Gaus mean : 28.5934**

**Gaus Width: 3.2660**



# B1L003S, U23-ch29, adc1

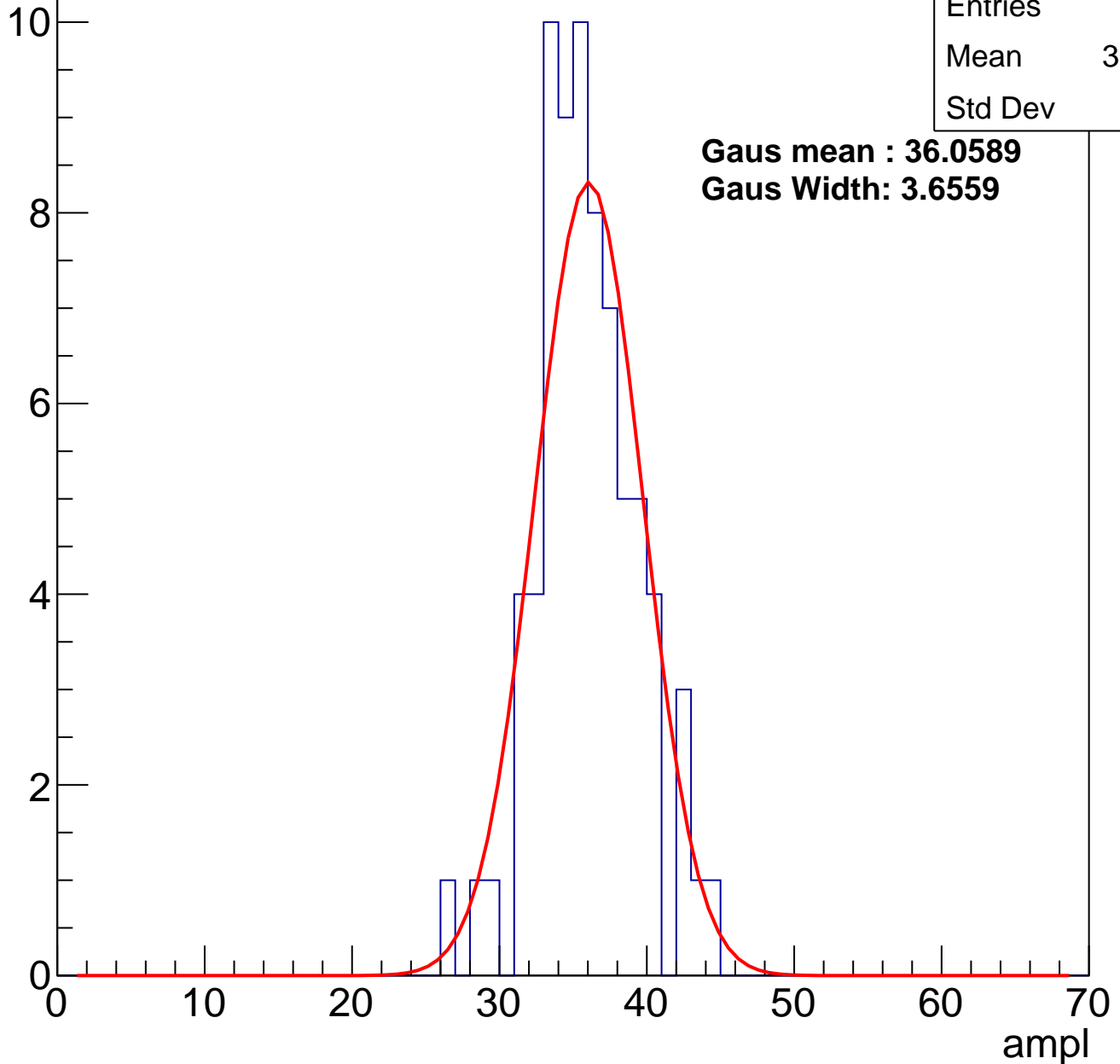
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	74
Mean	35.49
Std Dev	3.39

**Gaus mean : 36.0589**

**Gaus Width: 3.6559**

Entry



# B1L003S, U23-ch29, adc2

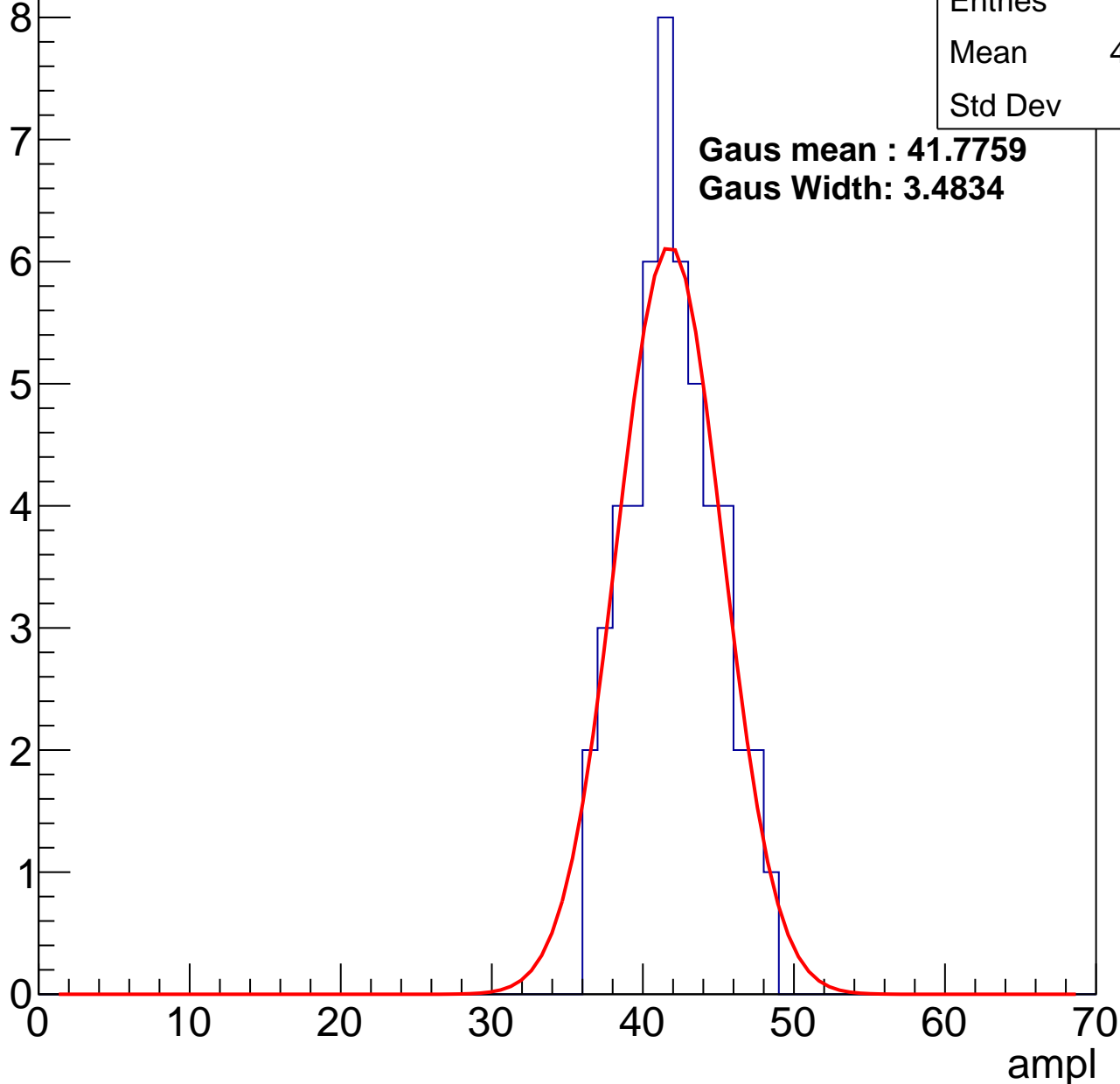
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	41.49
Std Dev	2.94

**Gaus mean : 41.7759**

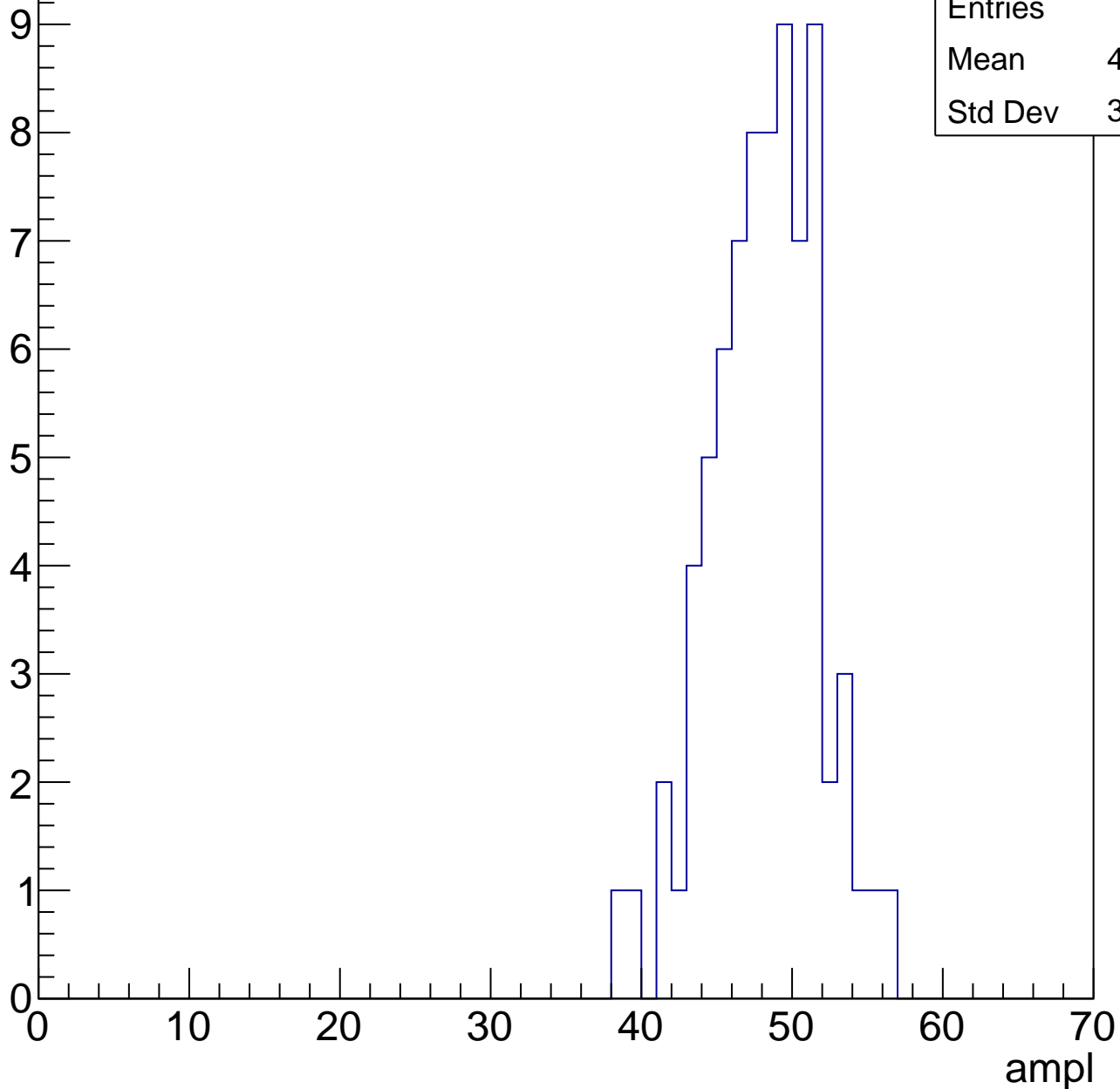
**Gaus Width: 3.4834**



# B1L003S, U23-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

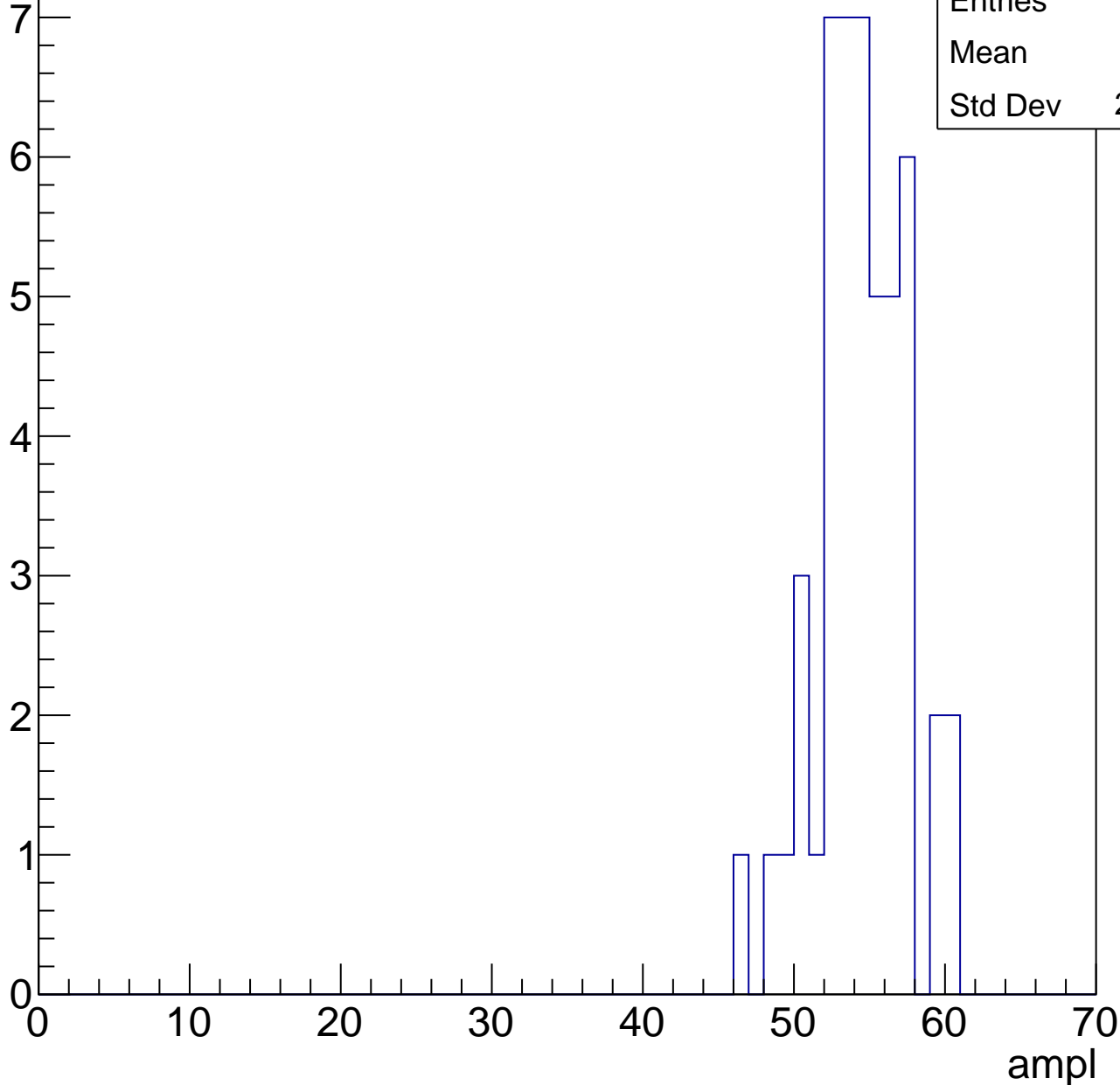


# B1L003S, U23-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	54
Std Dev	2.951

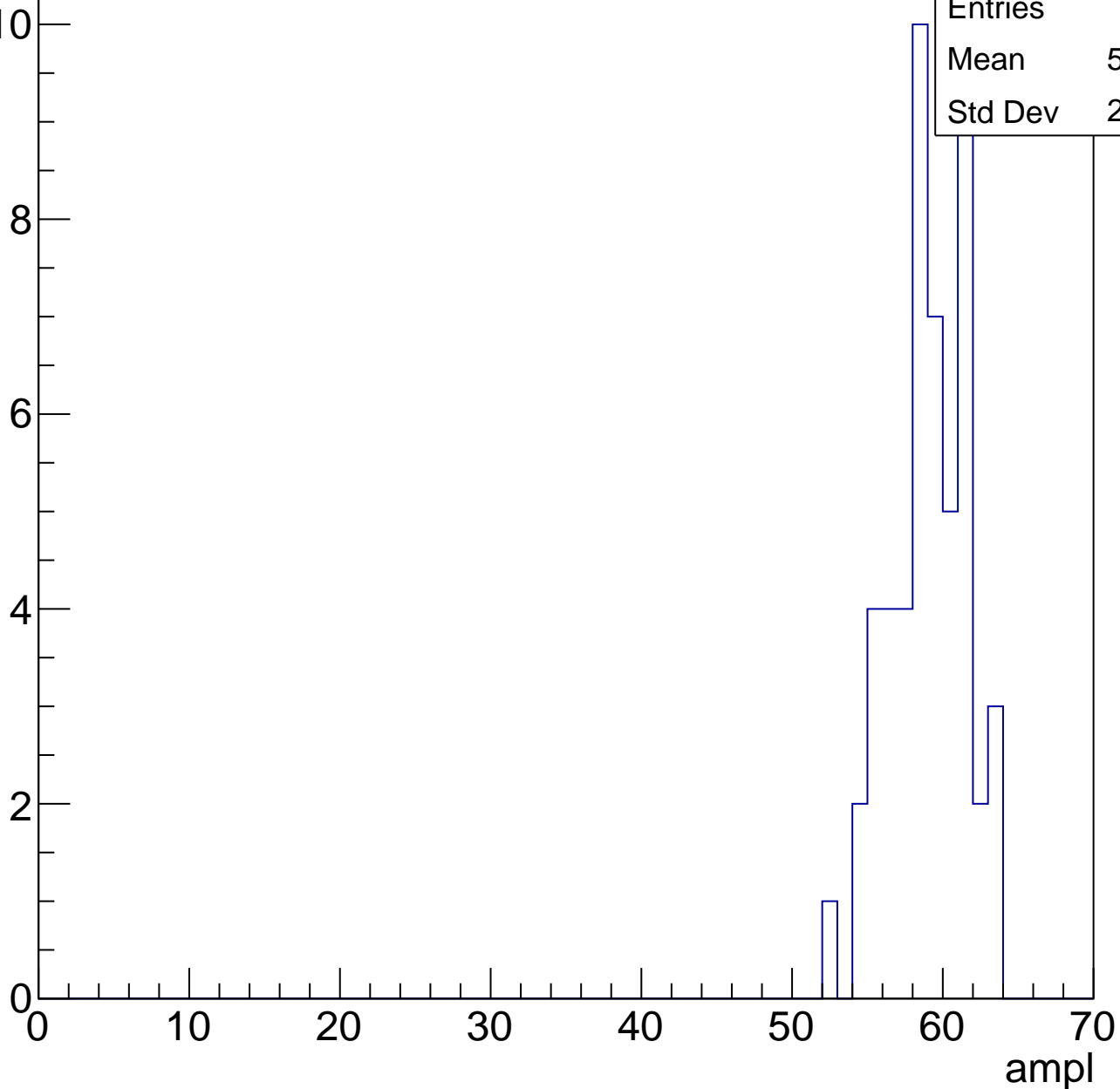


# B1L003S, U23-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.57
Std Dev	2.515

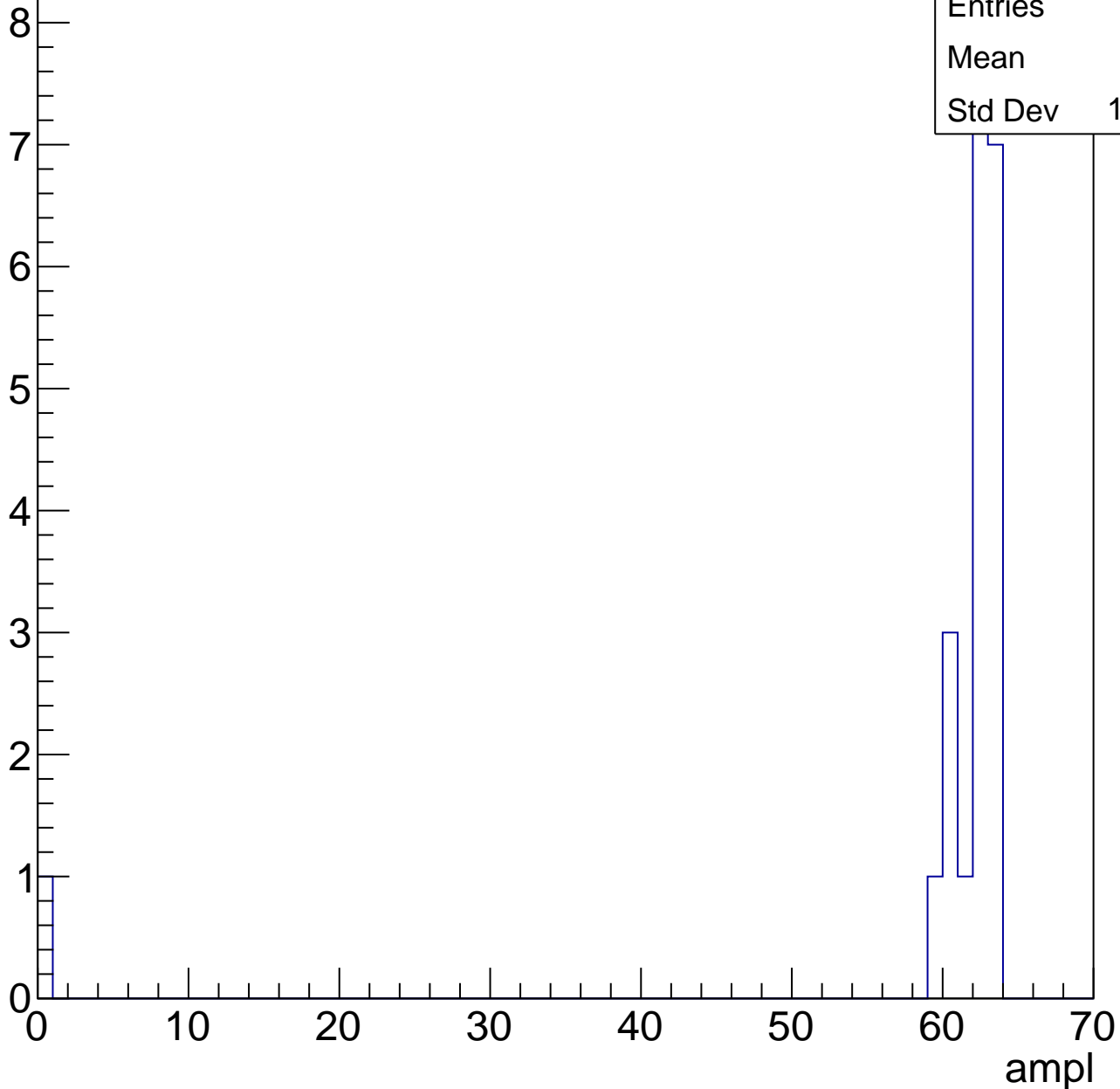


# B1L003S, U23-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	21
Mean	58.9
Std Dev	13.22





# B1L003S, U23-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch30, adc0

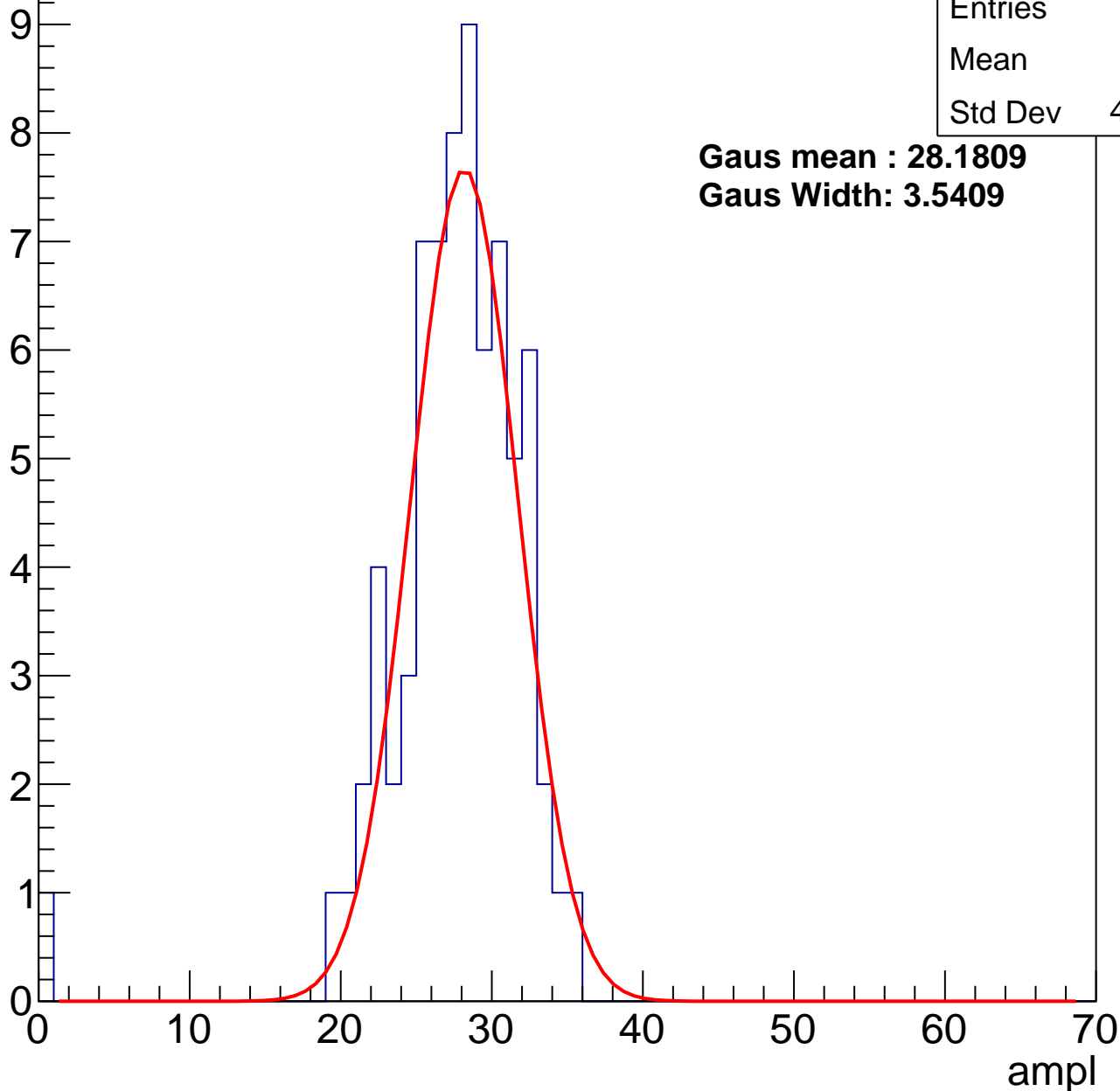
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	27.1
Std Dev	4.708

**Gaus mean : 28.1809**

**Gaus Width: 3.5409**



# B1L003S, U23-ch30, adc1

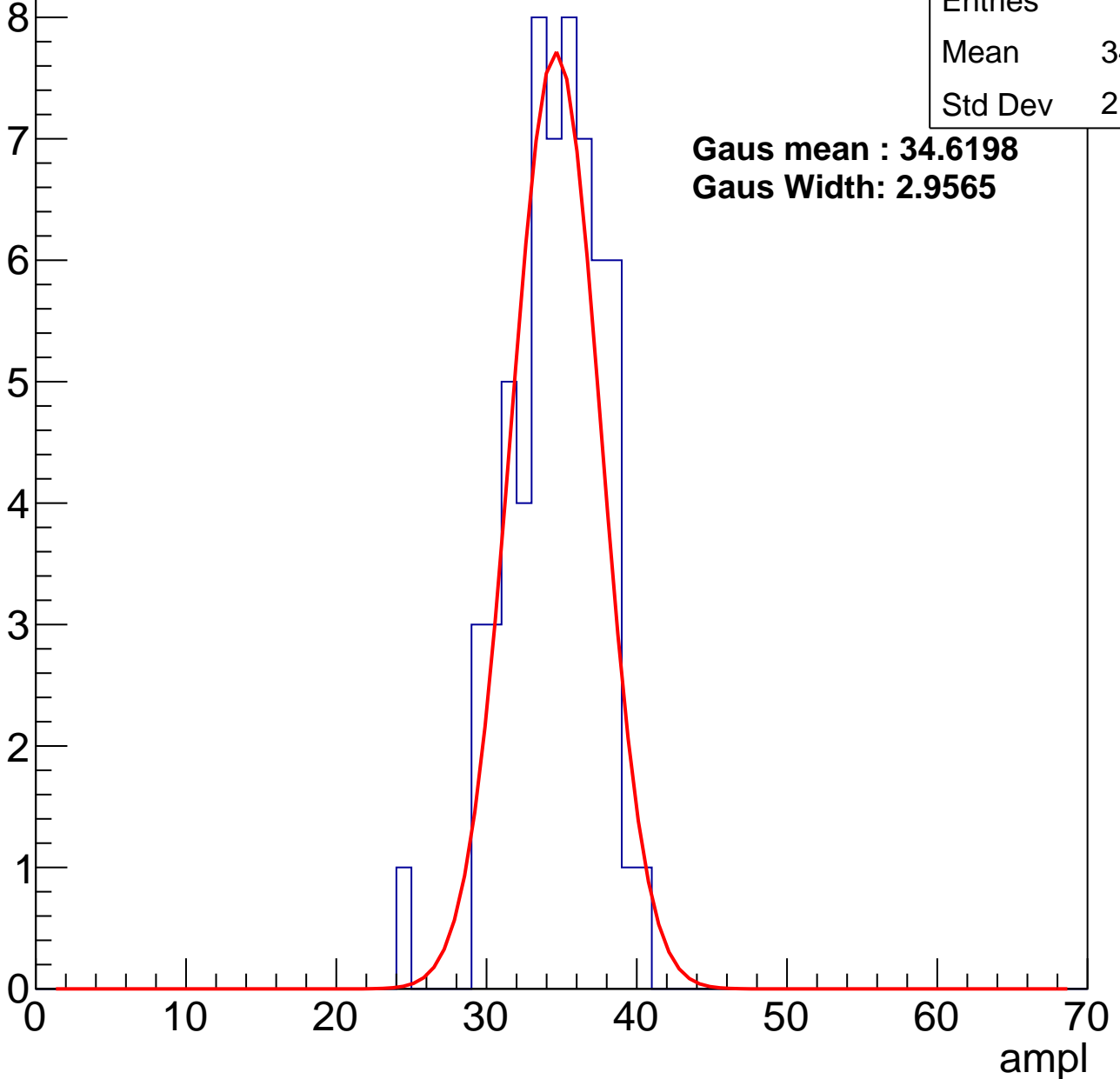
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	34.12
Std Dev	2.995

**Gaus mean : 34.6198**

**Gaus Width: 2.9565**



# B1L003S, U23-ch30, adc2

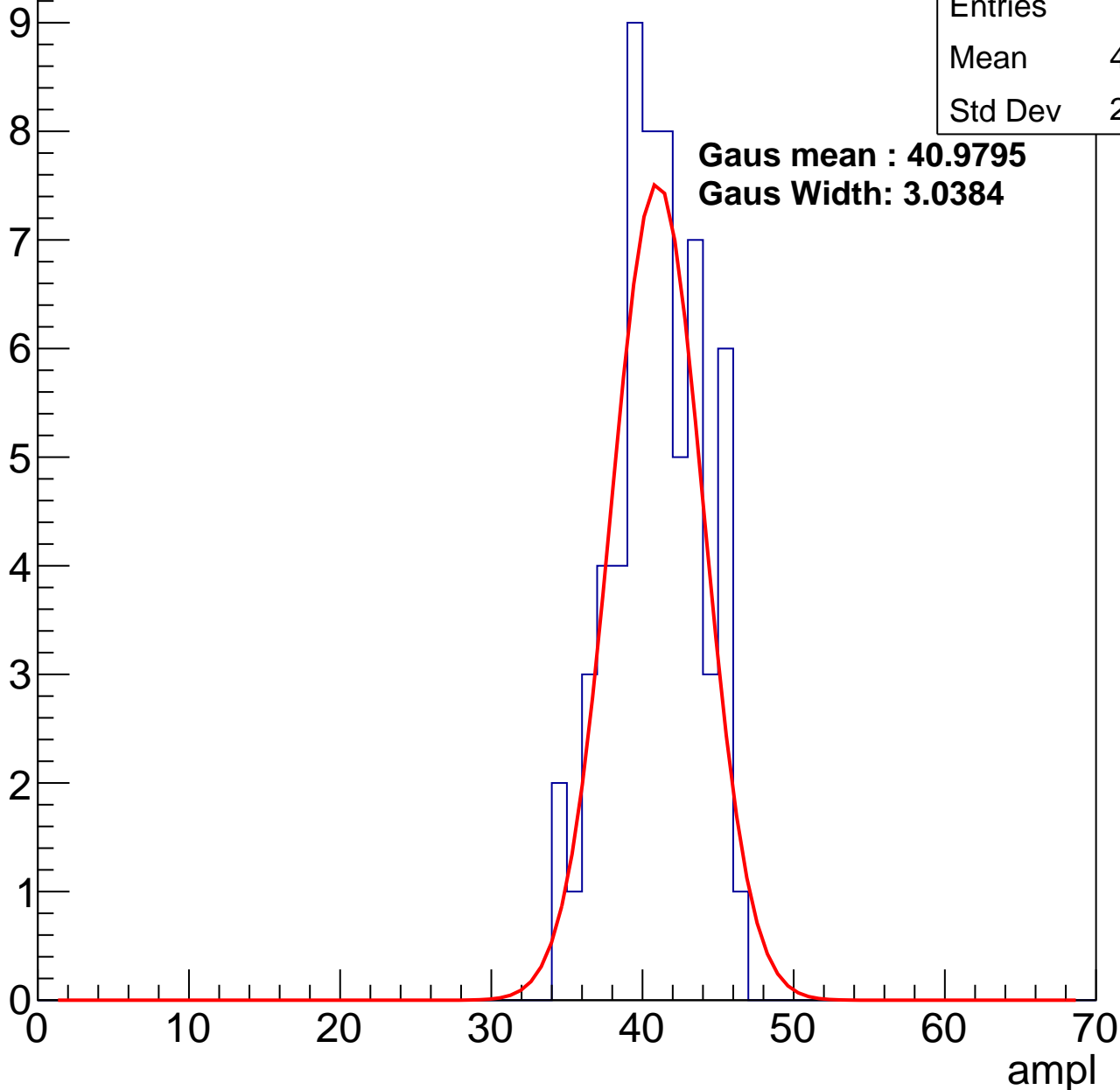
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	40.48
Std Dev	2.912

**Gaus mean : 40.9795**

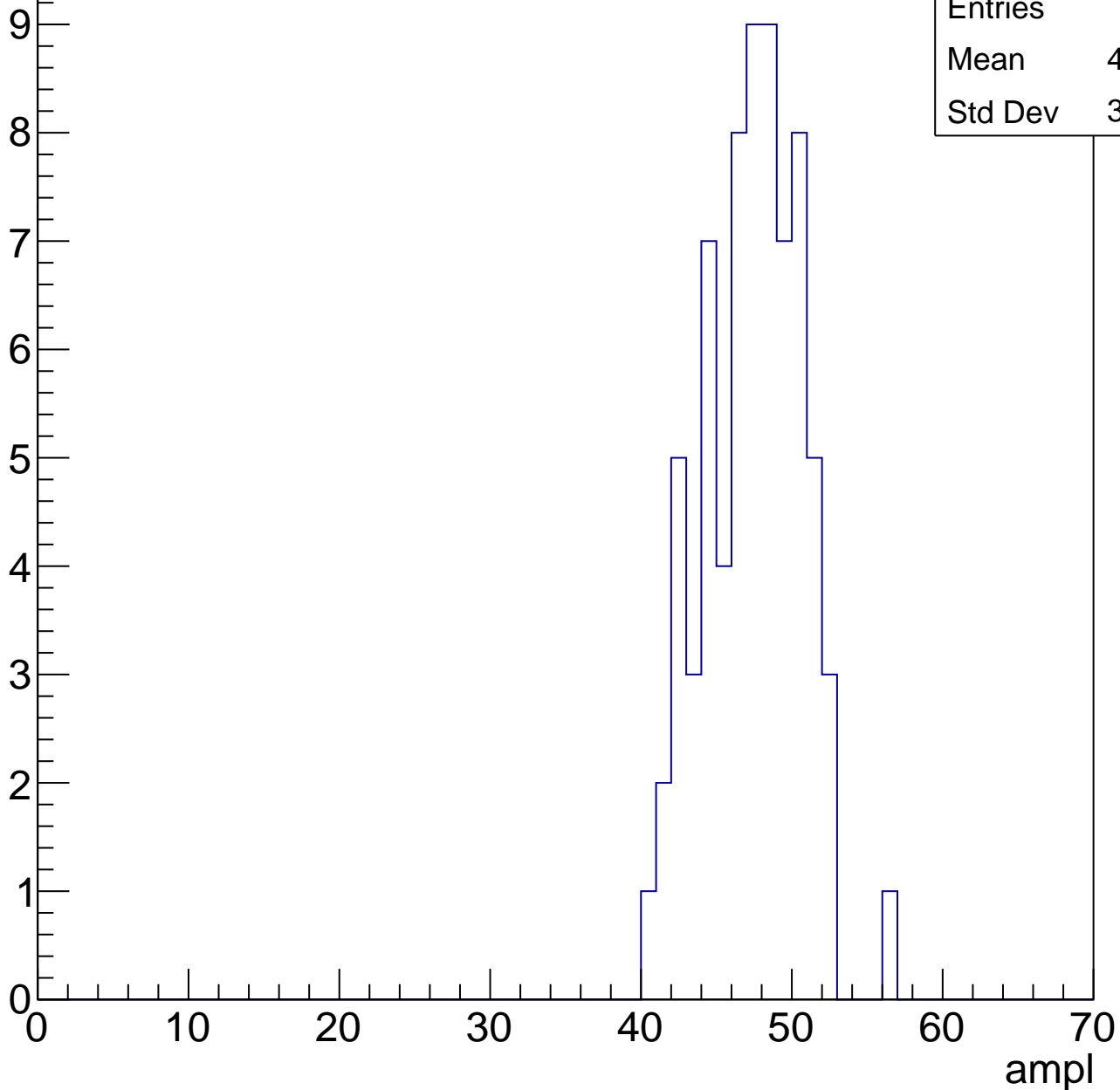
**Gaus Width: 3.0384**



# B1L003S, U23-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

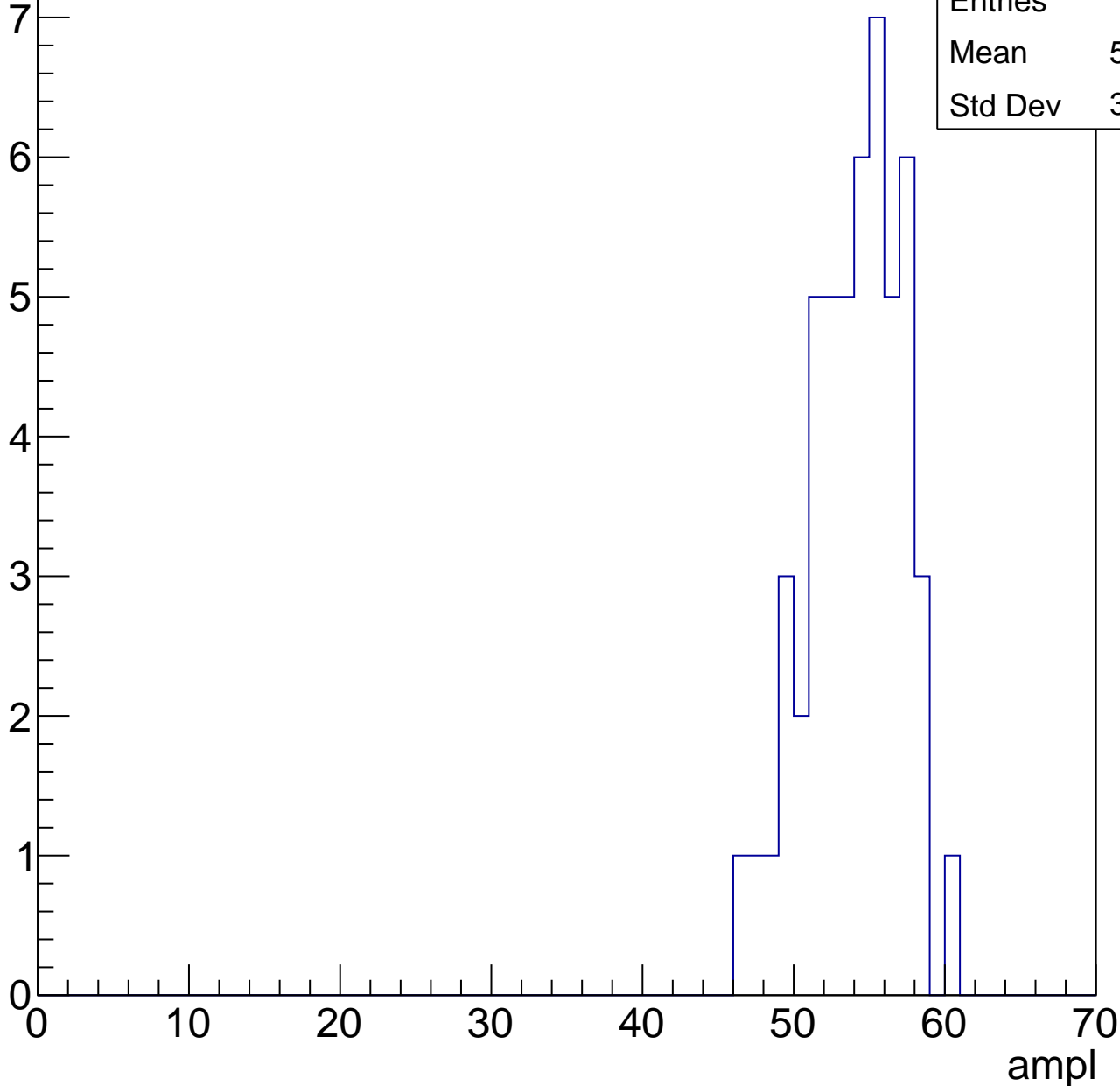


# B1L003S, U23-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

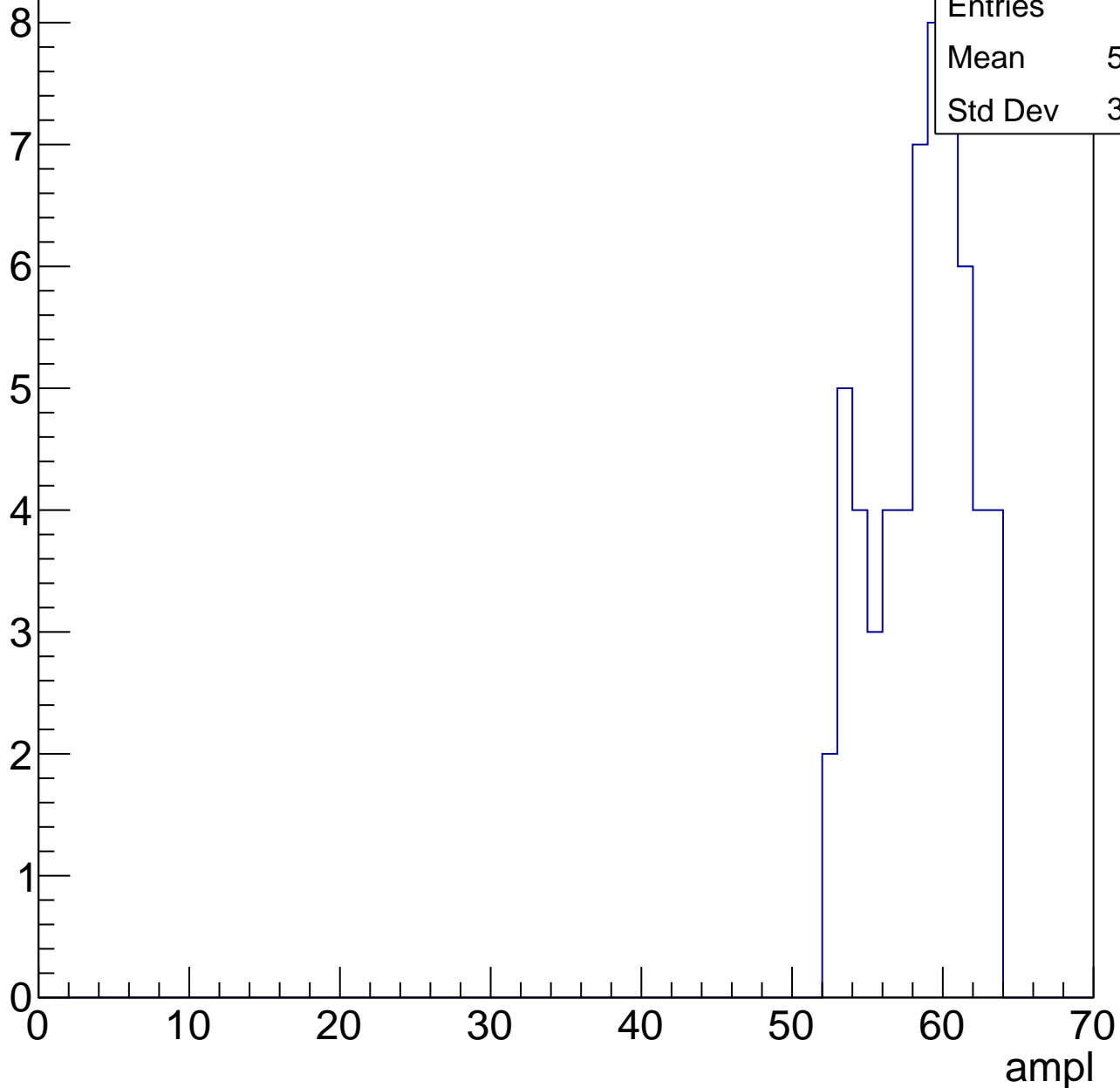
Entries	51
Mean	53.59
Std Dev	3.075



# B1L003S, U23-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



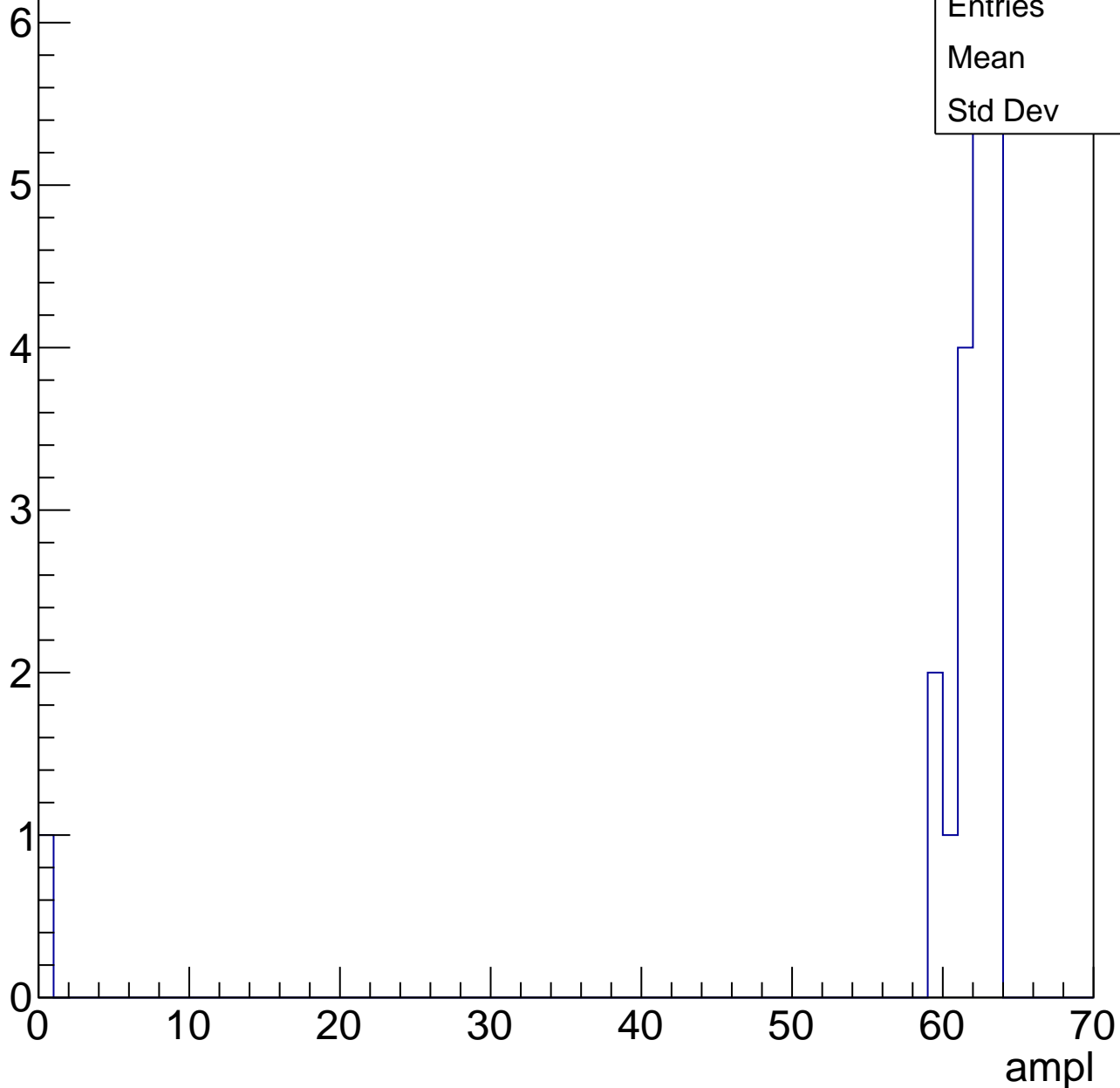
Entries	59
Mean	58.07
Std Dev	3.097

# B1L003S, U23-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	20
Mean	58.6
Std Dev	13.5





# B1L003S, U23-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch31, adc0

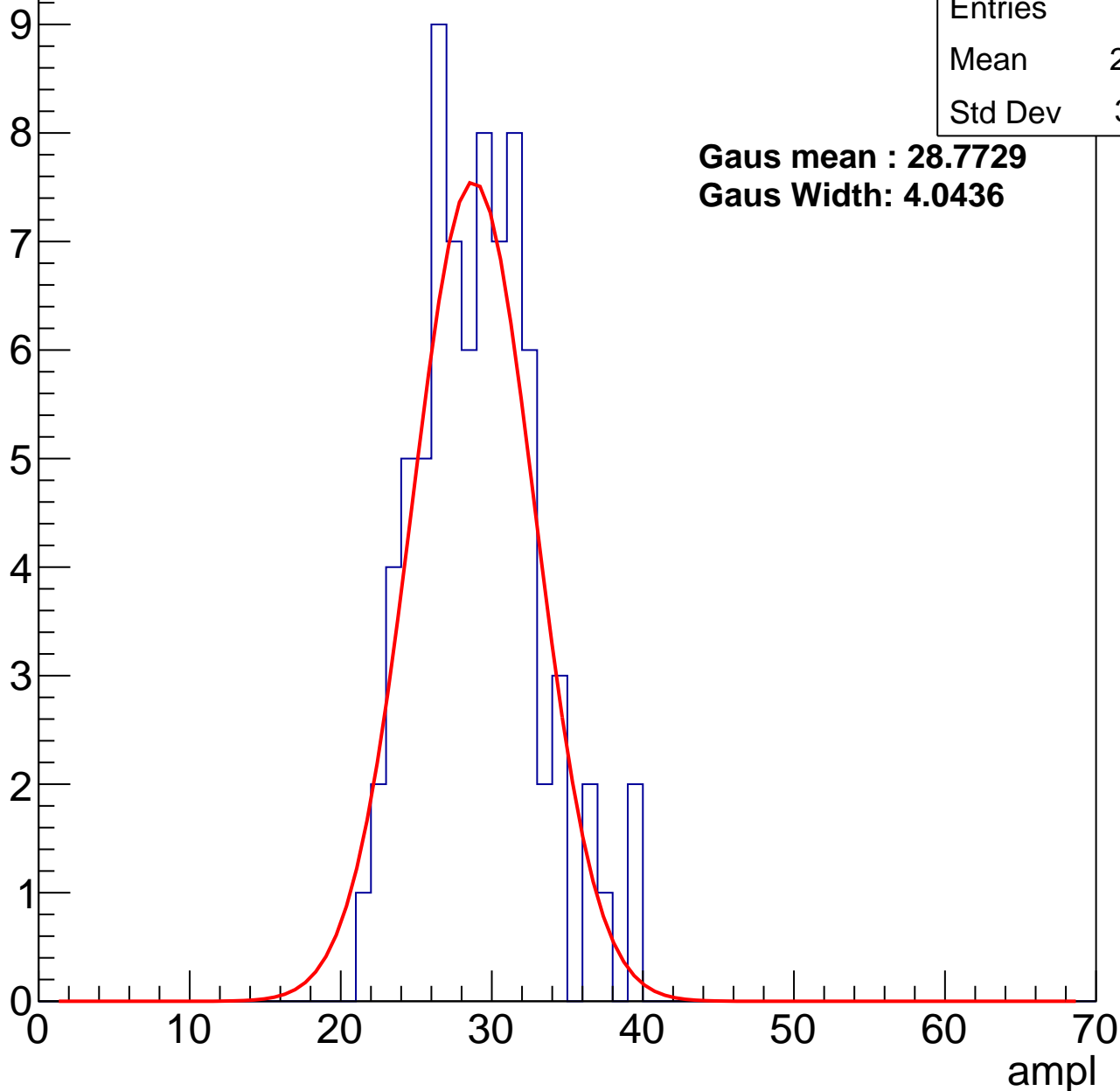
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	28.59
Std Dev	3.881

**Gaus mean : 28.7729**

**Gaus Width: 4.0436**



# B1L003S, U23-ch31, adc1

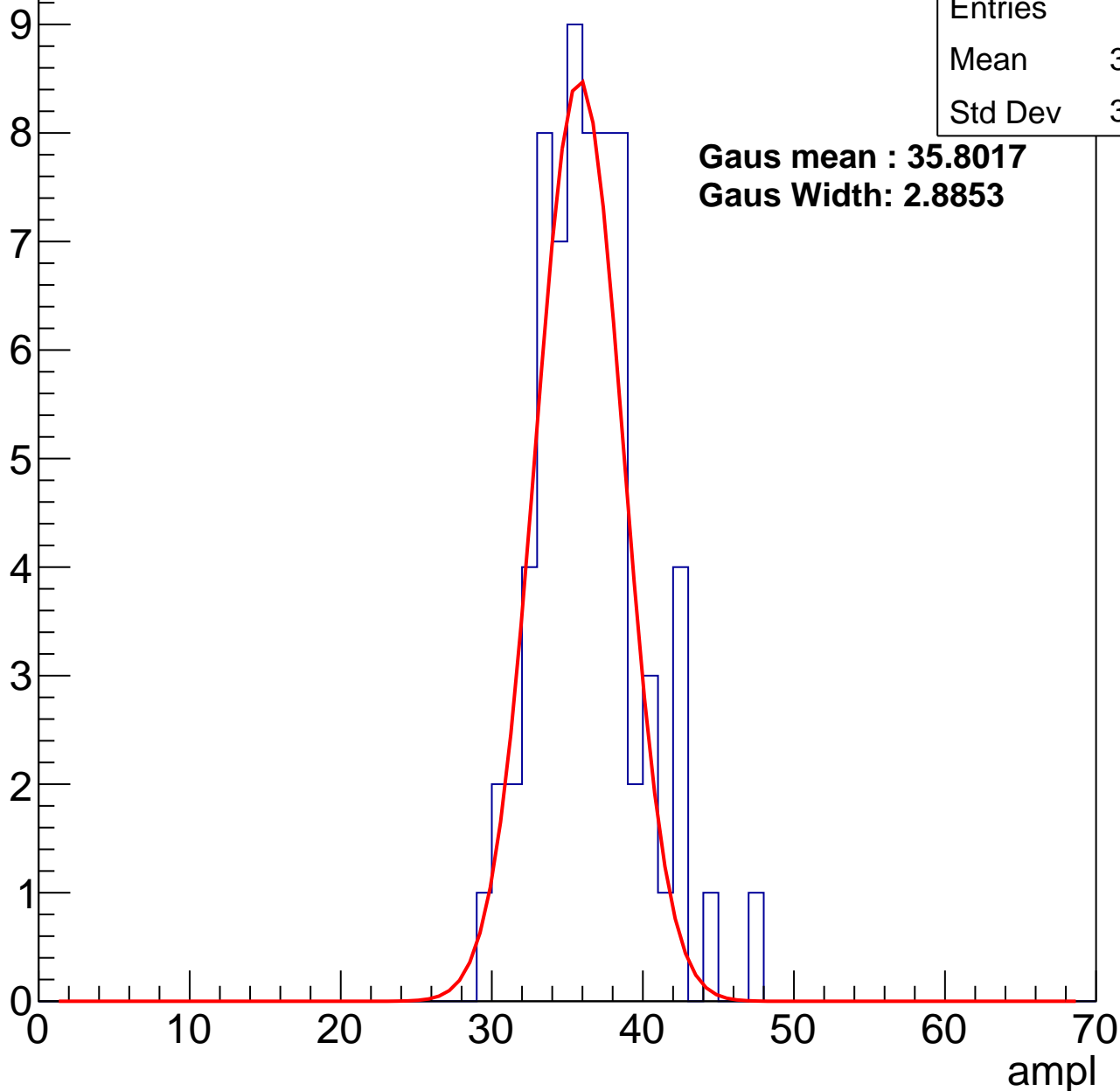
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.97
Std Dev	3.413

**Gaus mean : 35.8017**

**Gaus Width: 2.8853**



# B1L003S, U23-ch31, adc2

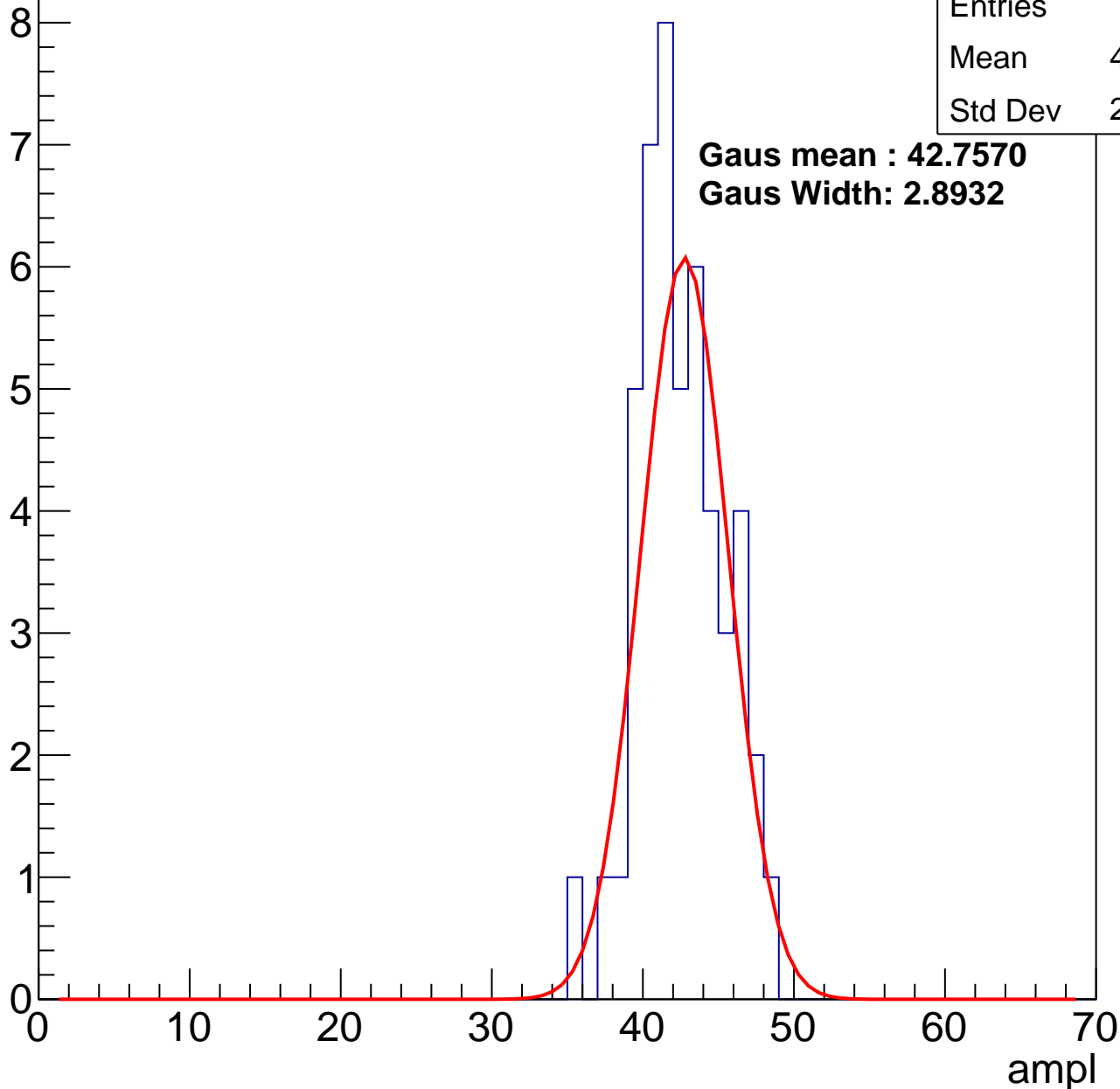
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	42.04
Std Dev	2.776

**Gaus mean : 42.7570**

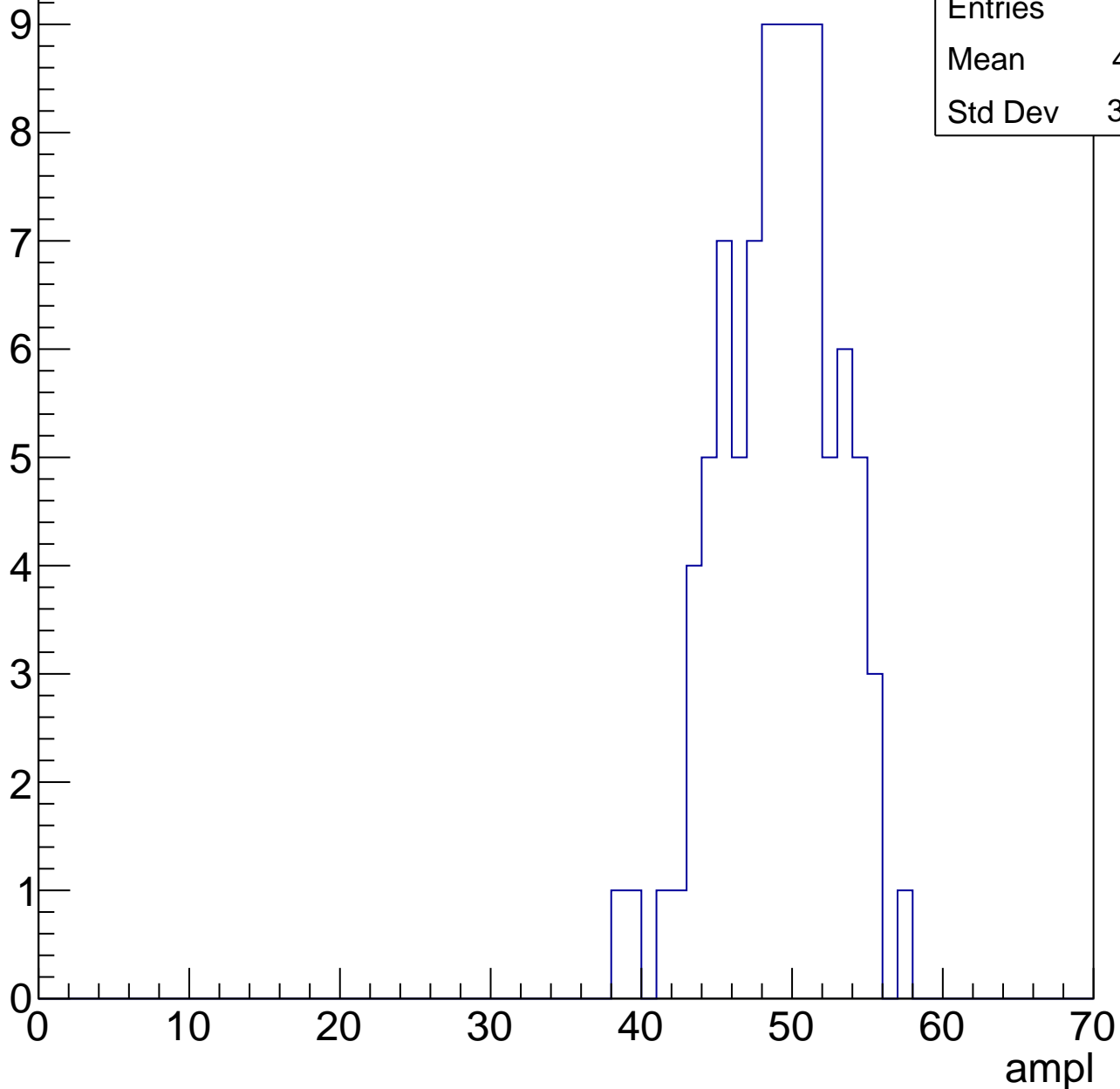
**Gaus Width: 2.8932**



# B1L003S, U23-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

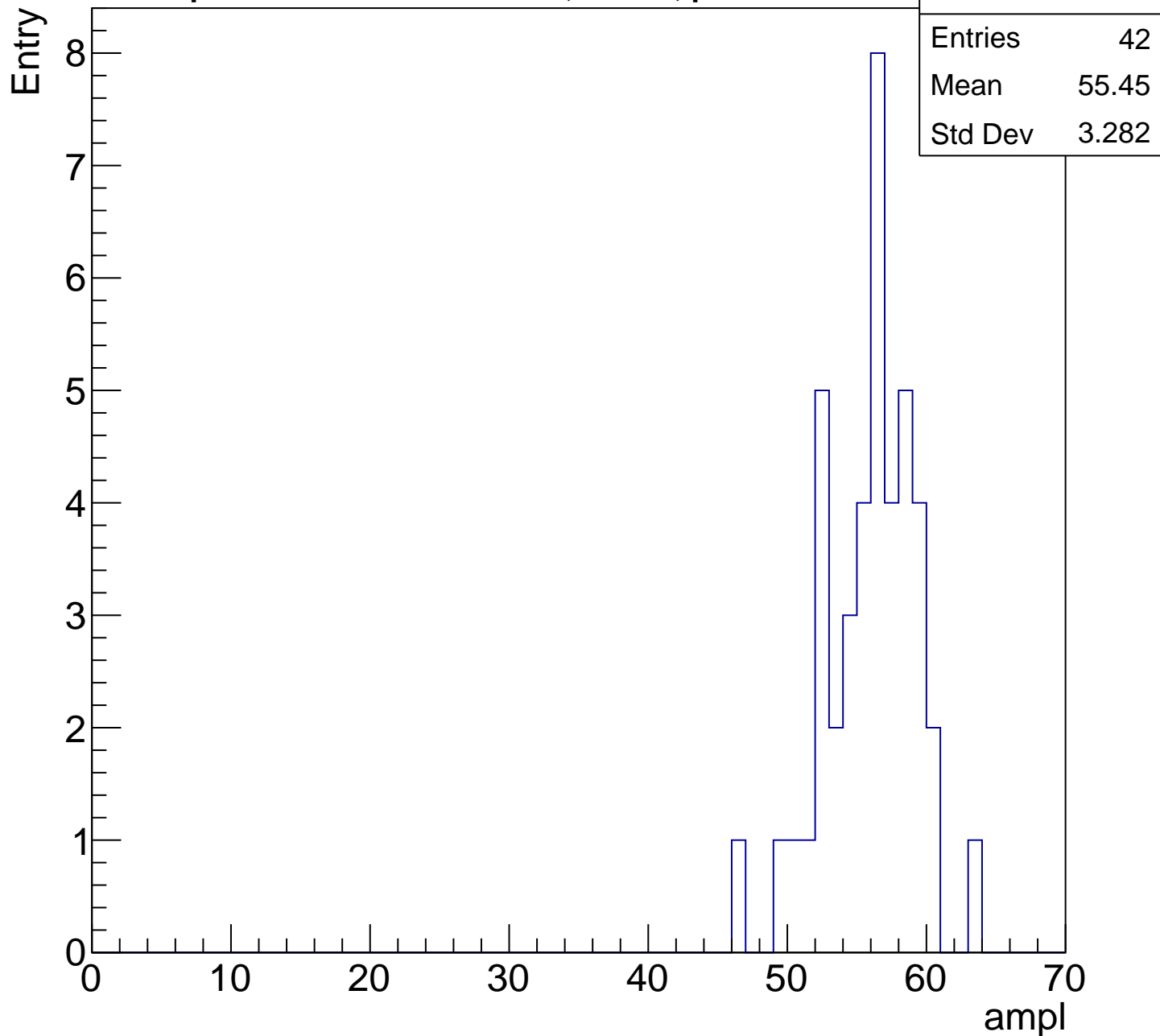
Entry



Entries	88
Mean	48.61
Std Dev	3.794

# B1L003S, U23-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2



# B1L003S, U23-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

6

5

4

3

2

1

0

Entries	40
Mean	58.75
Std Dev	2.791

ampl

0

10

20

30

40

50

60

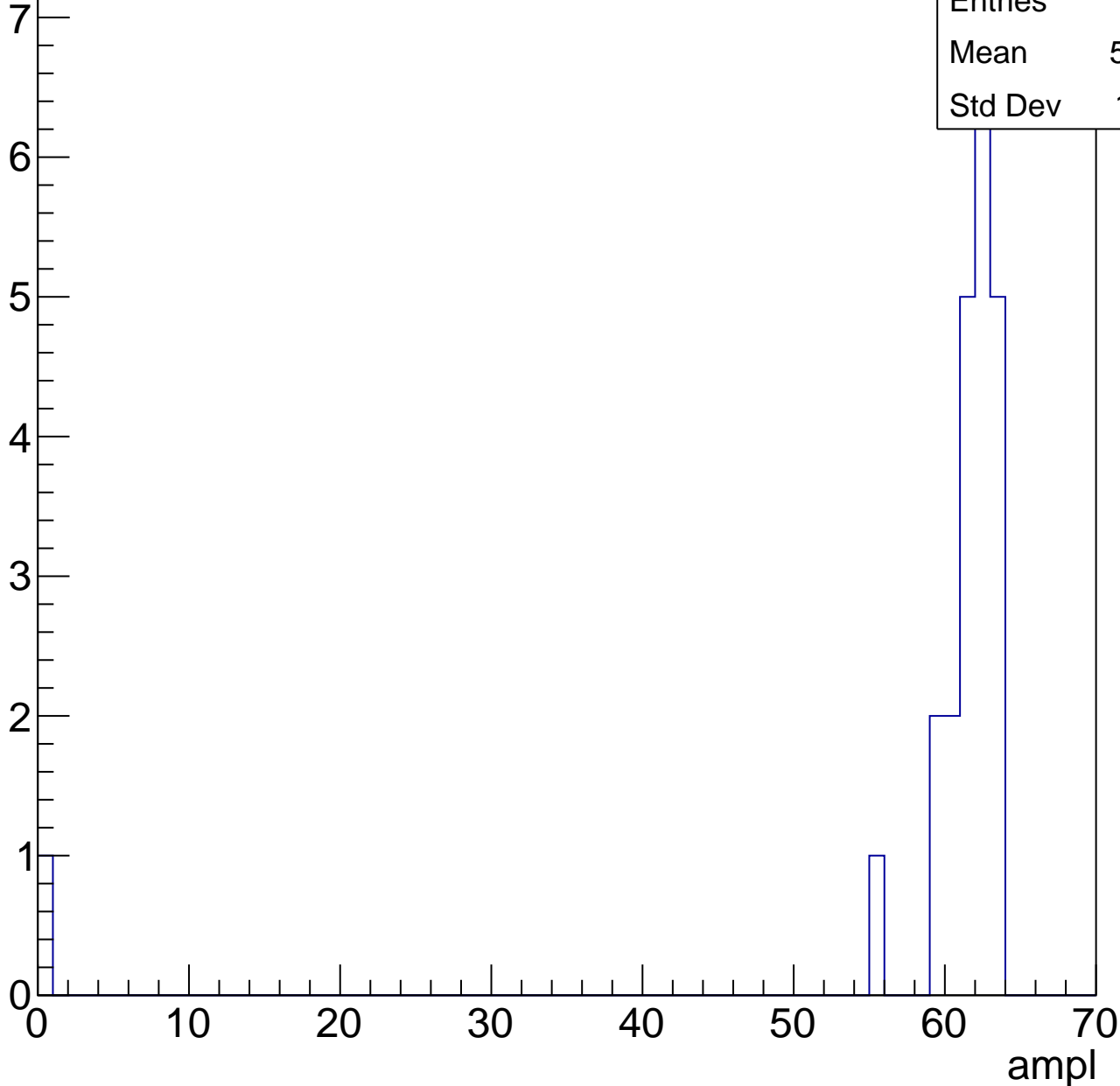
70

# B1L003S, U23-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	23
Mean	58.57
Std Dev	12.61





# B1L003S, U23-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch32, adc0

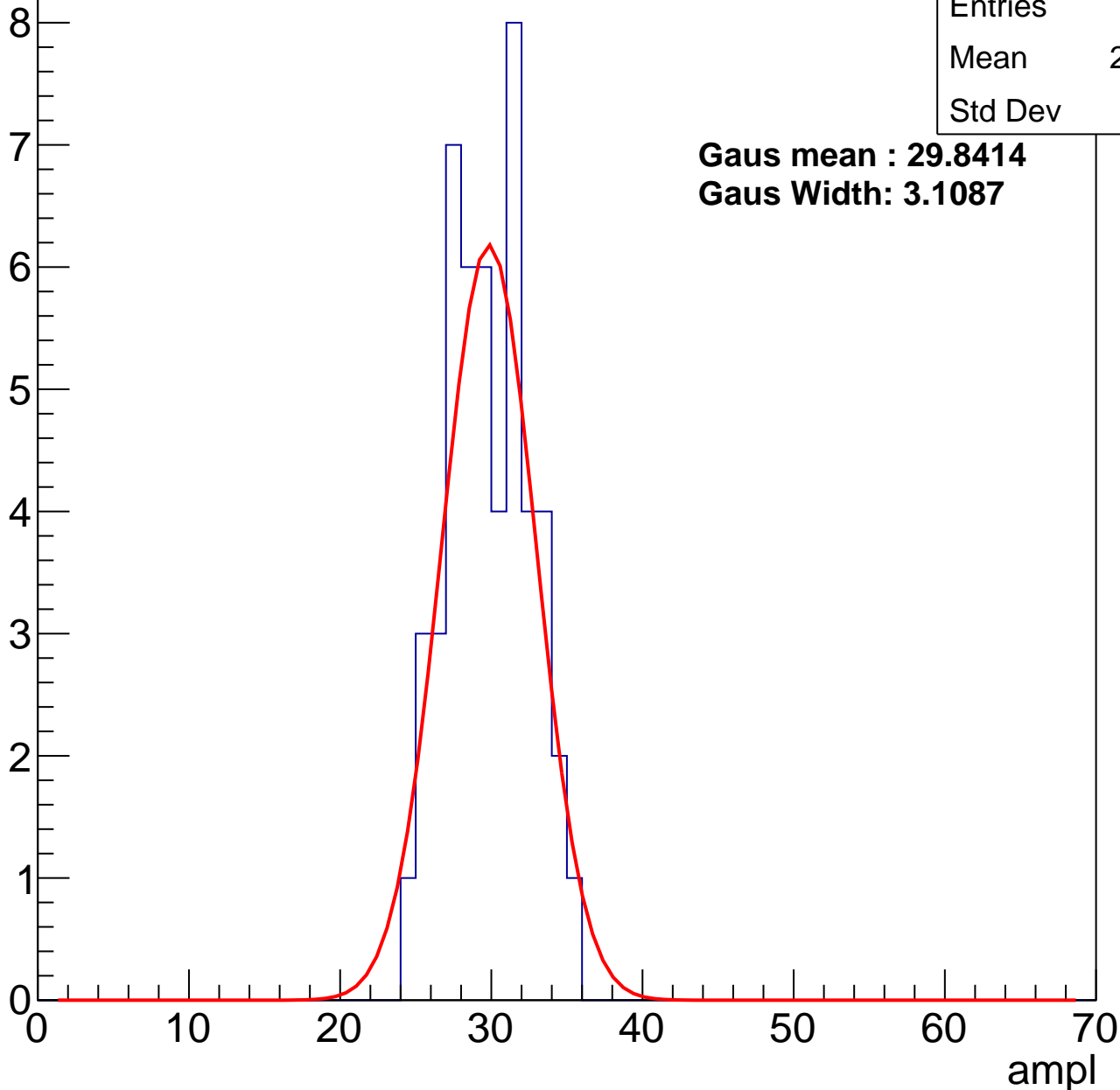
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	29.37
Std Dev	2.67

**Gaus mean : 29.8414**

**Gaus Width: 3.1087**



# B1L003S, U23-ch32, adc1

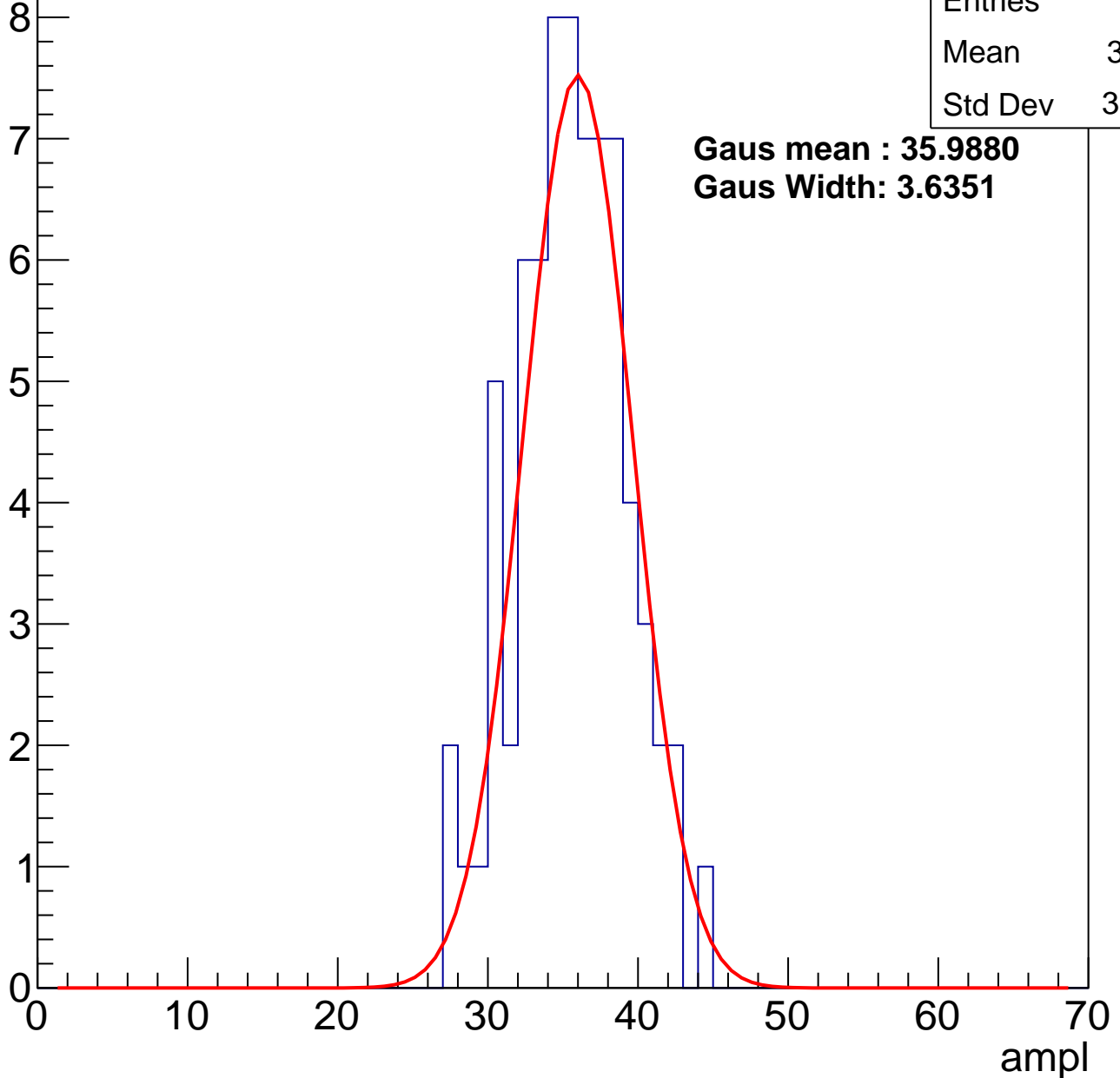
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	35.11
Std Dev	3.619

**Gaus mean : 35.9880**

**Gaus Width: 3.6351**



# B1L003S, U23-ch32, adc2

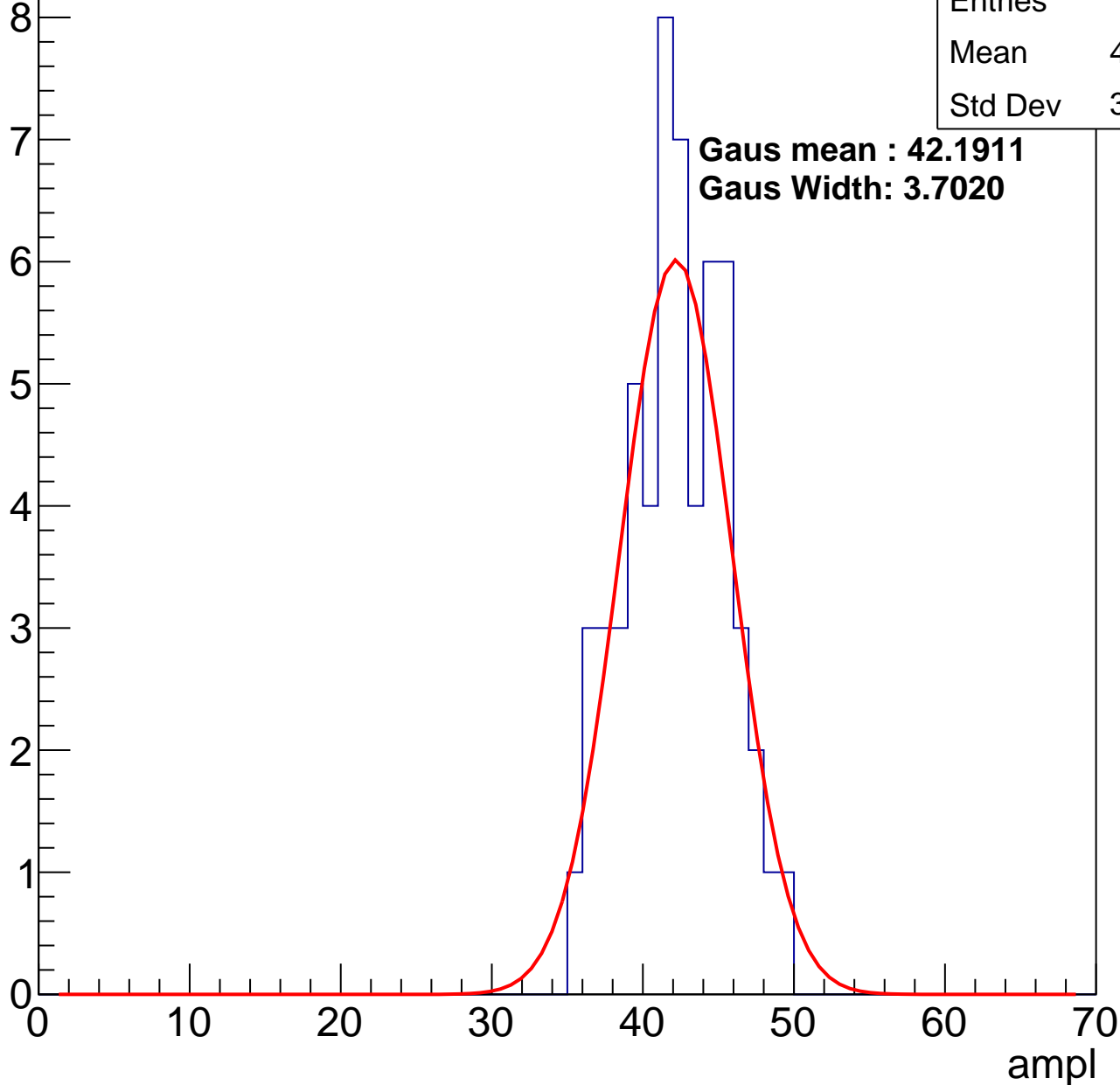
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	41.75
Std Dev	3.273

**Gaus mean : 42.1911**

**Gaus Width: 3.7020**

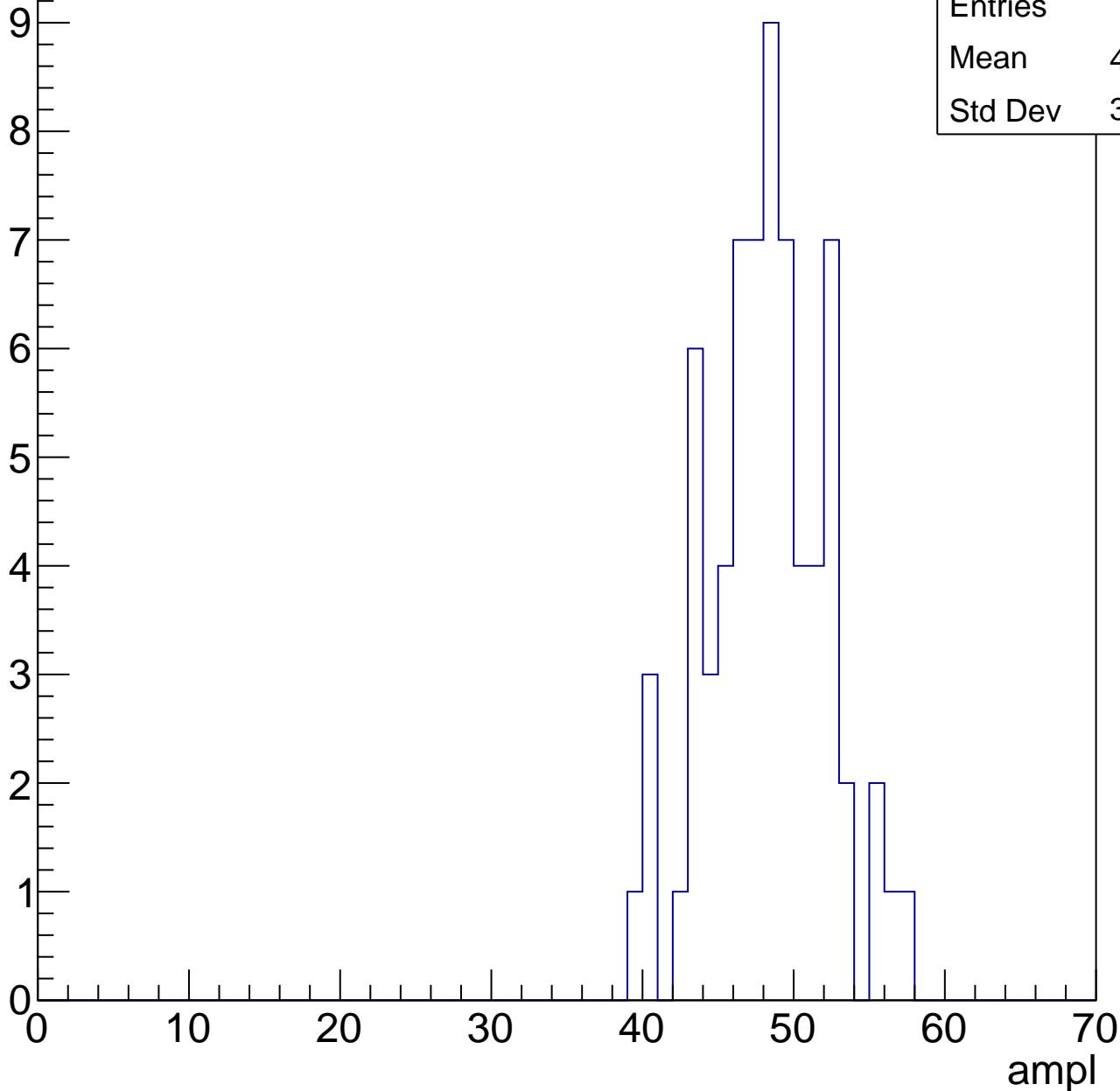


# B1L003S, U23-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	47.74
Std Dev	3.877

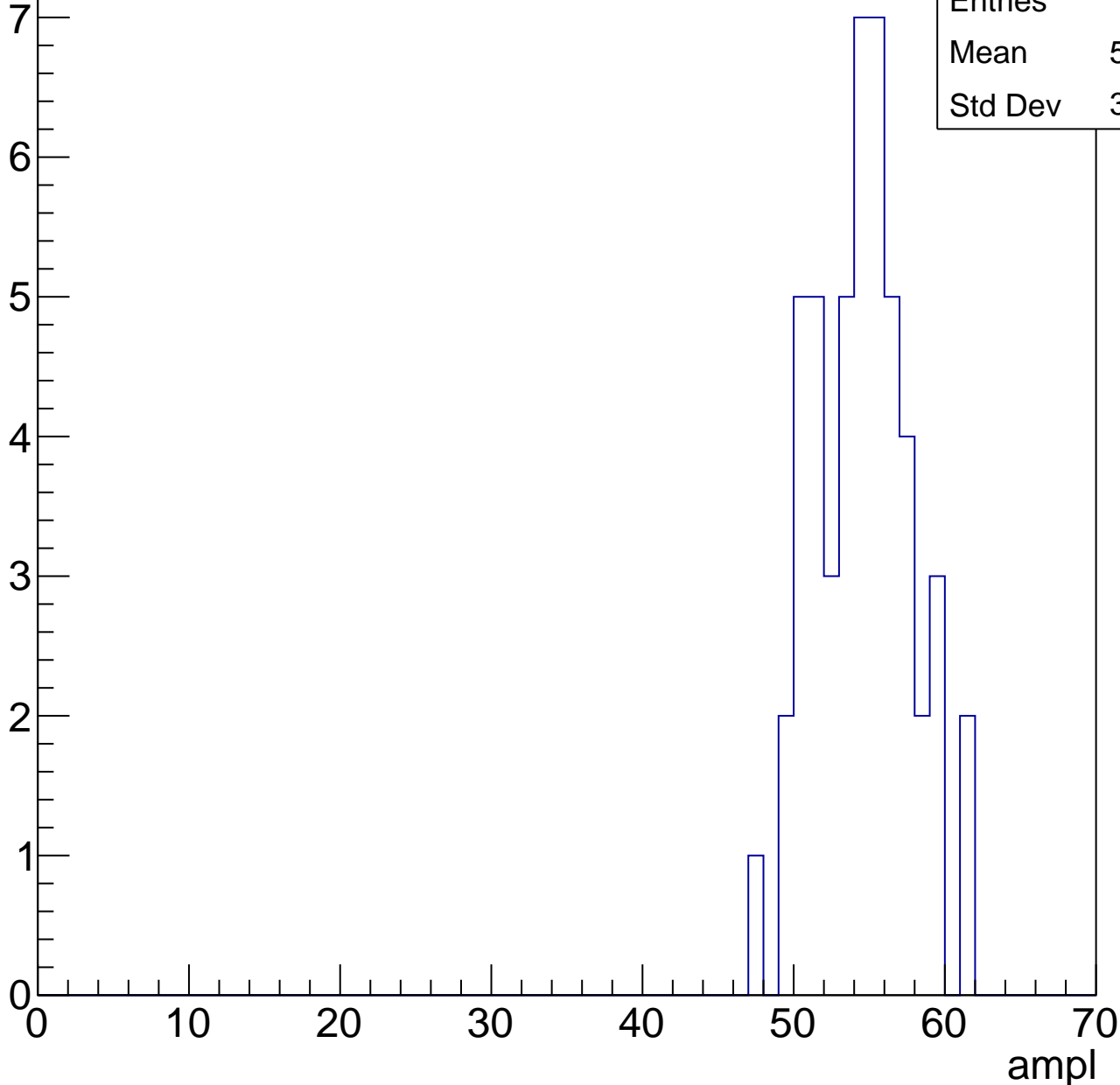


# B1L003S, U23-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	54.06
Std Dev	3.159



# B1L003S, U23-ch32, adc5

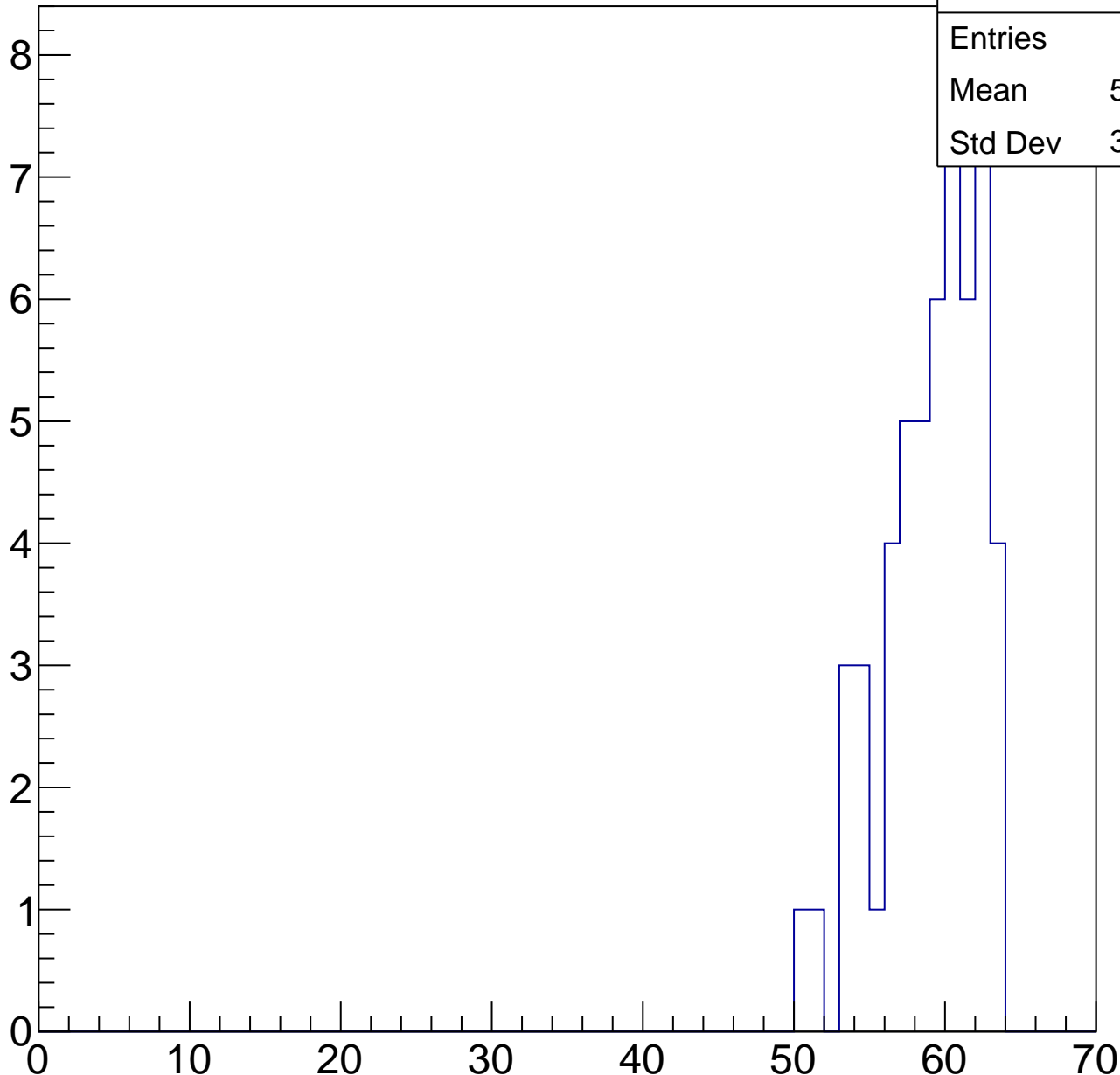
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	58.62
Std Dev	3.205

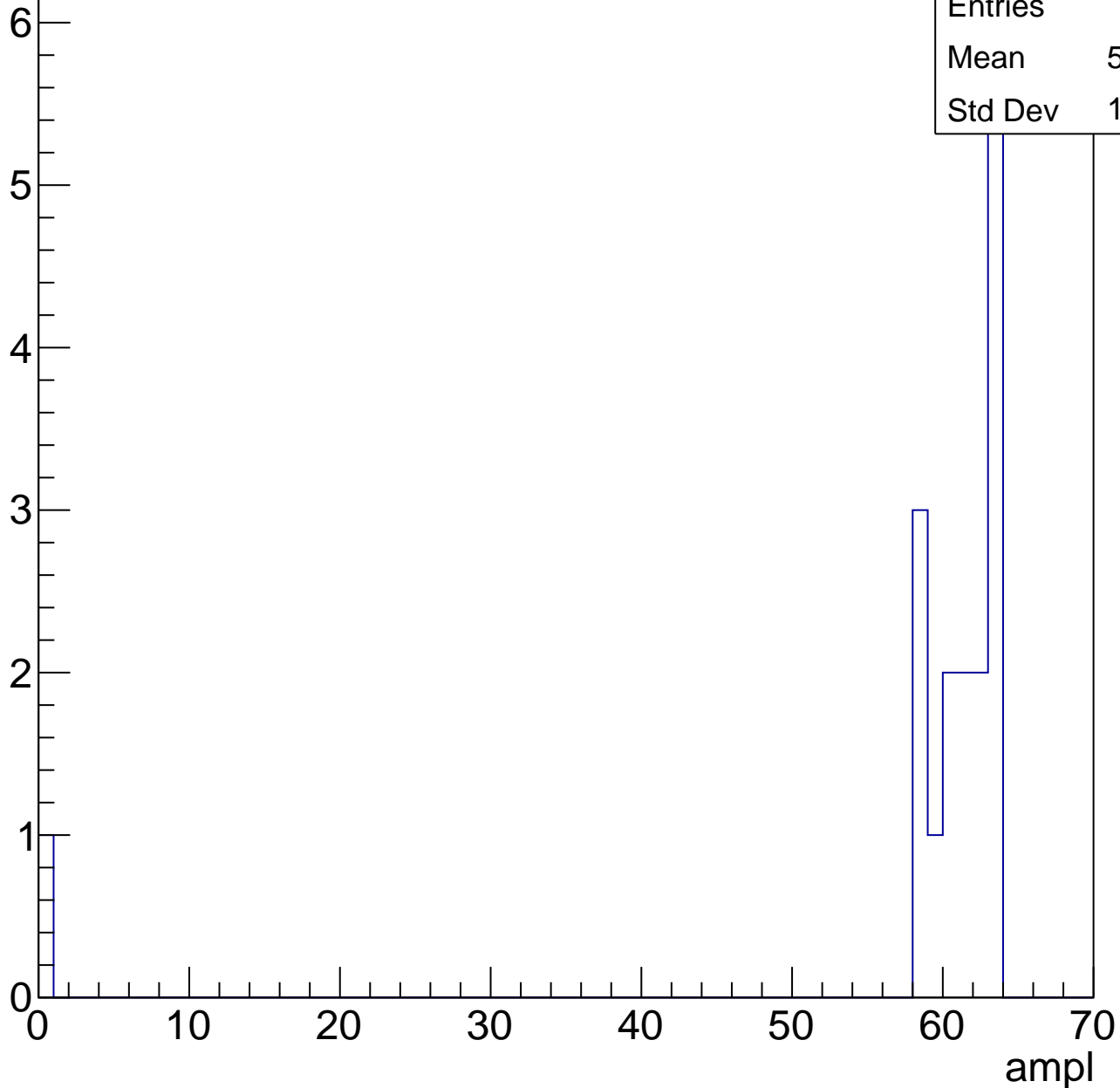
ampl



# B1L003S, U23-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch33, adc0

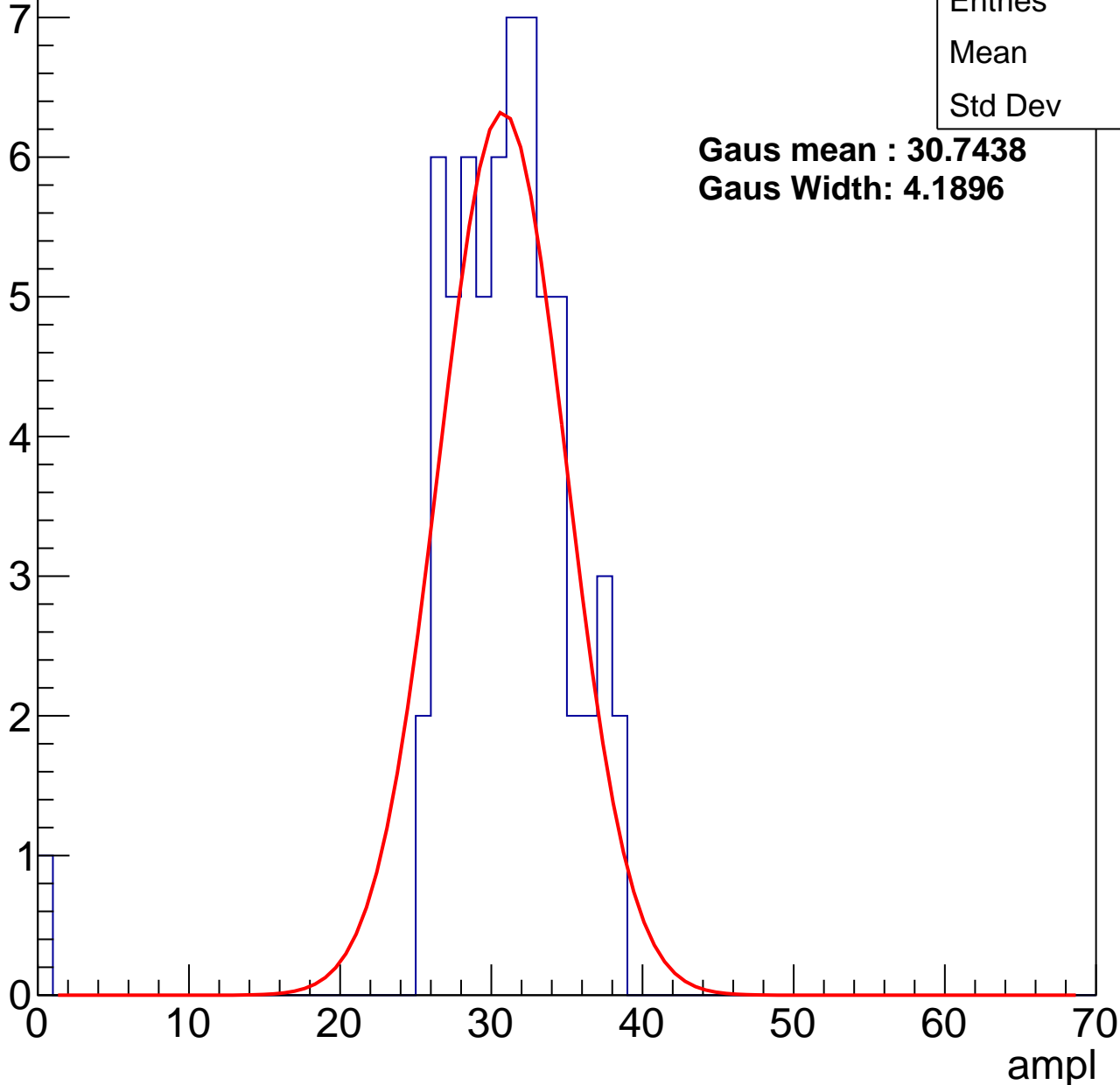
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	30.3
Std Dev	5.11

**Gaus mean : 30.7438**

**Gaus Width: 4.1896**



# B1L003S, U23-ch33, adc1

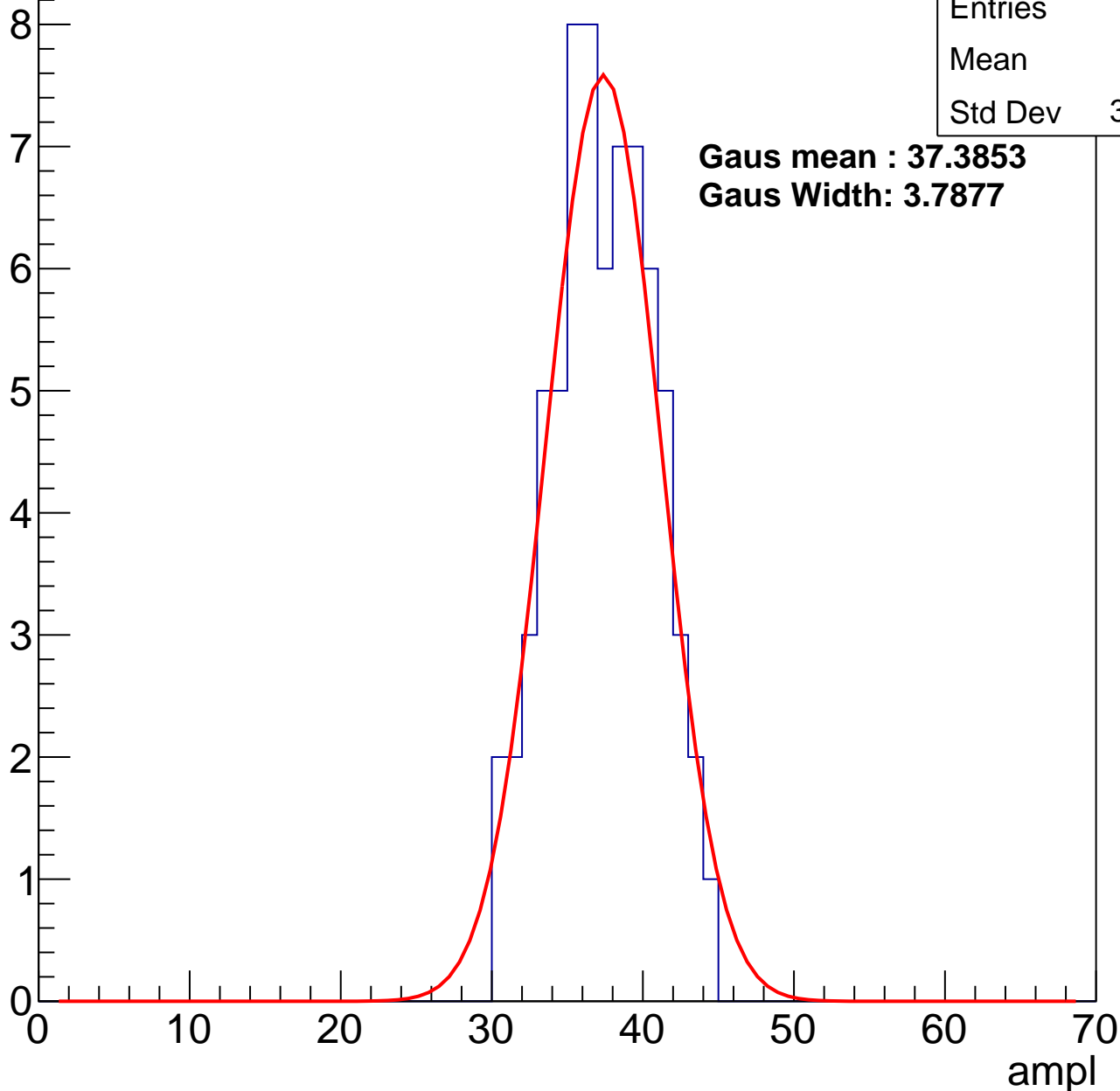
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.9
Std Dev	3.326

**Gaus mean : 37.3853**

**Gaus Width: 3.7877**



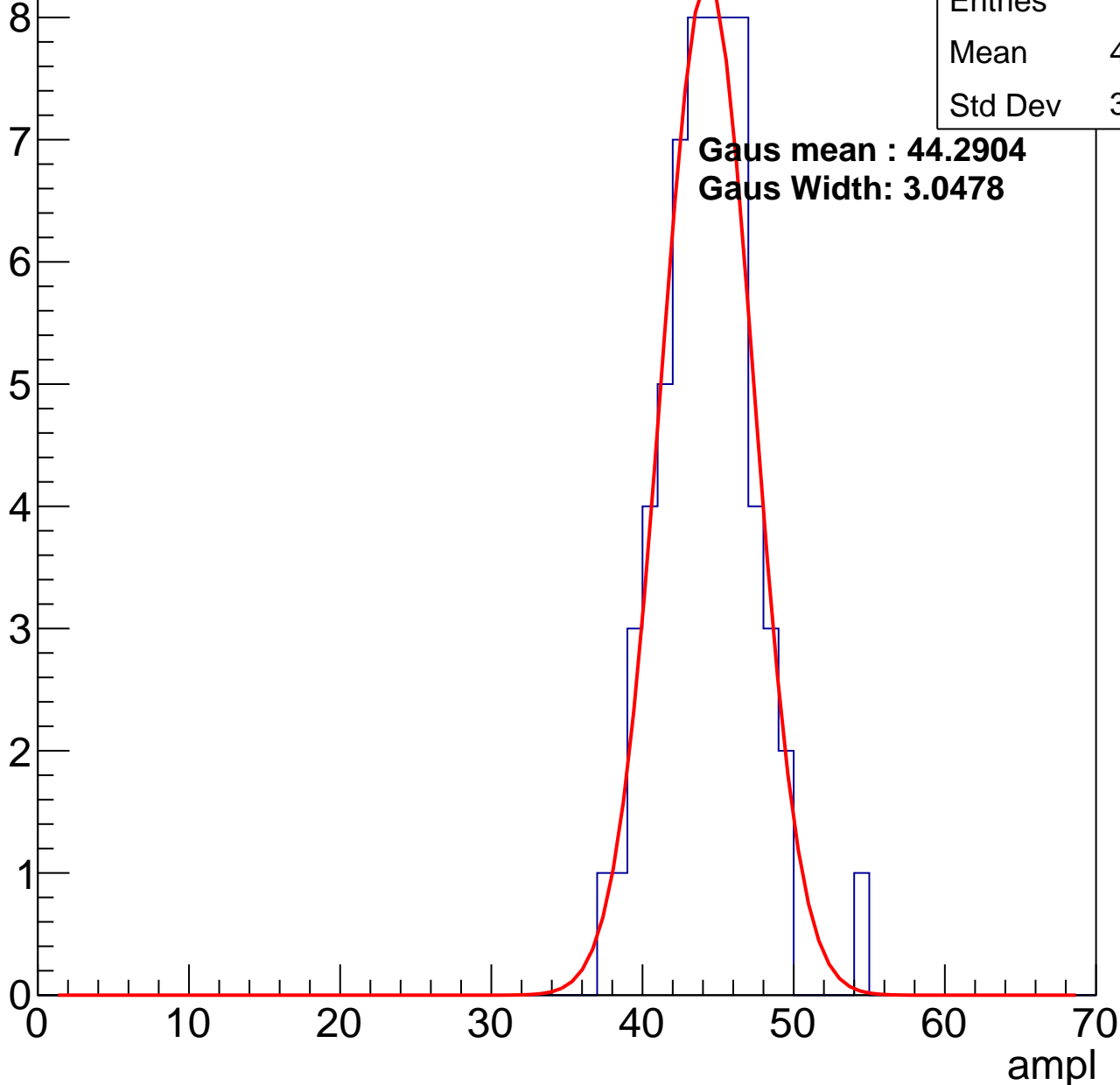
# B1L003S, U23-ch33, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	43.79
Std Dev	3.025

**Gaus mean : 44.2904**  
**Gaus Width: 3.0478**

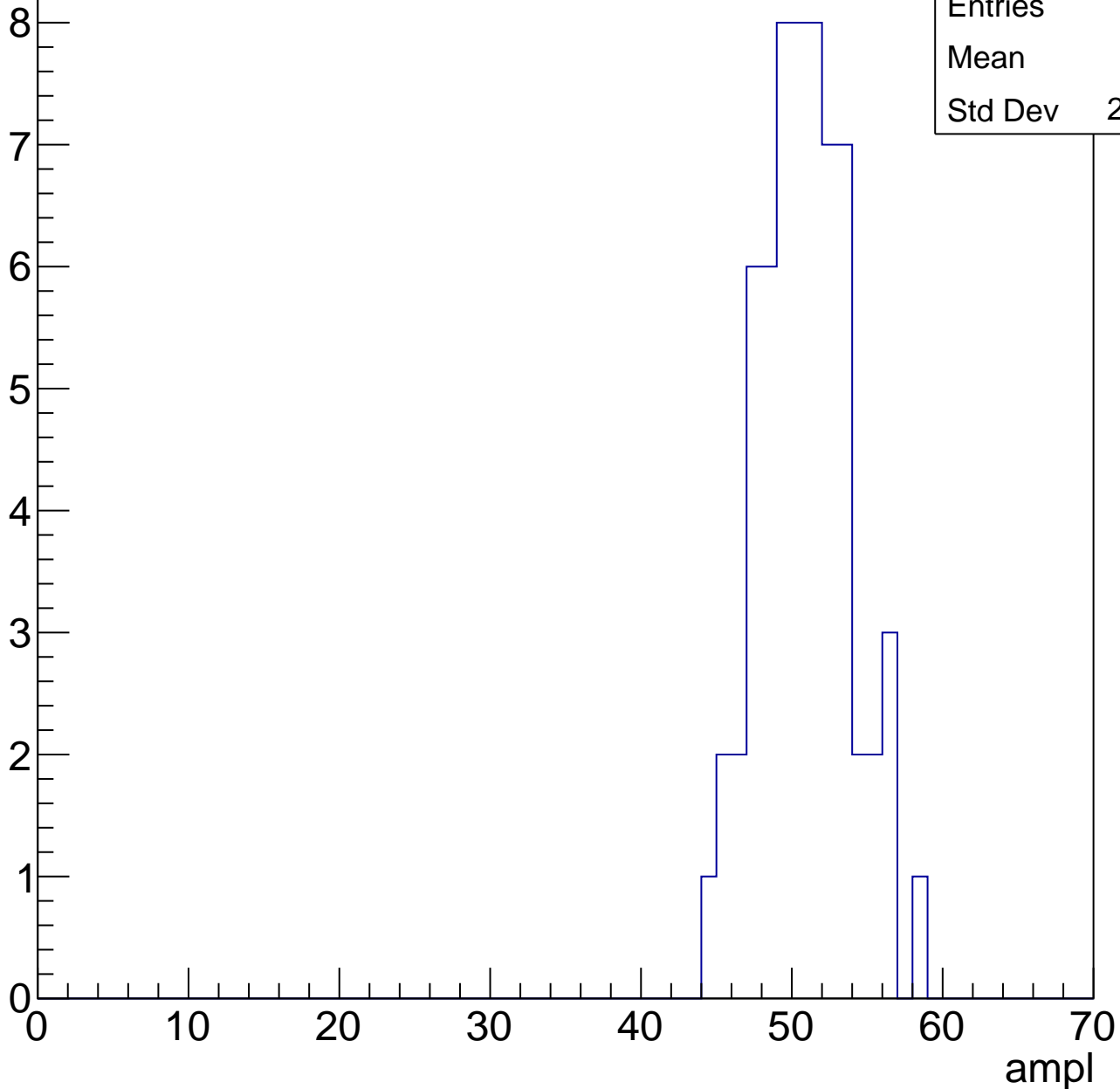


# B1L003S, U23-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	50.4
Std Dev	2.947

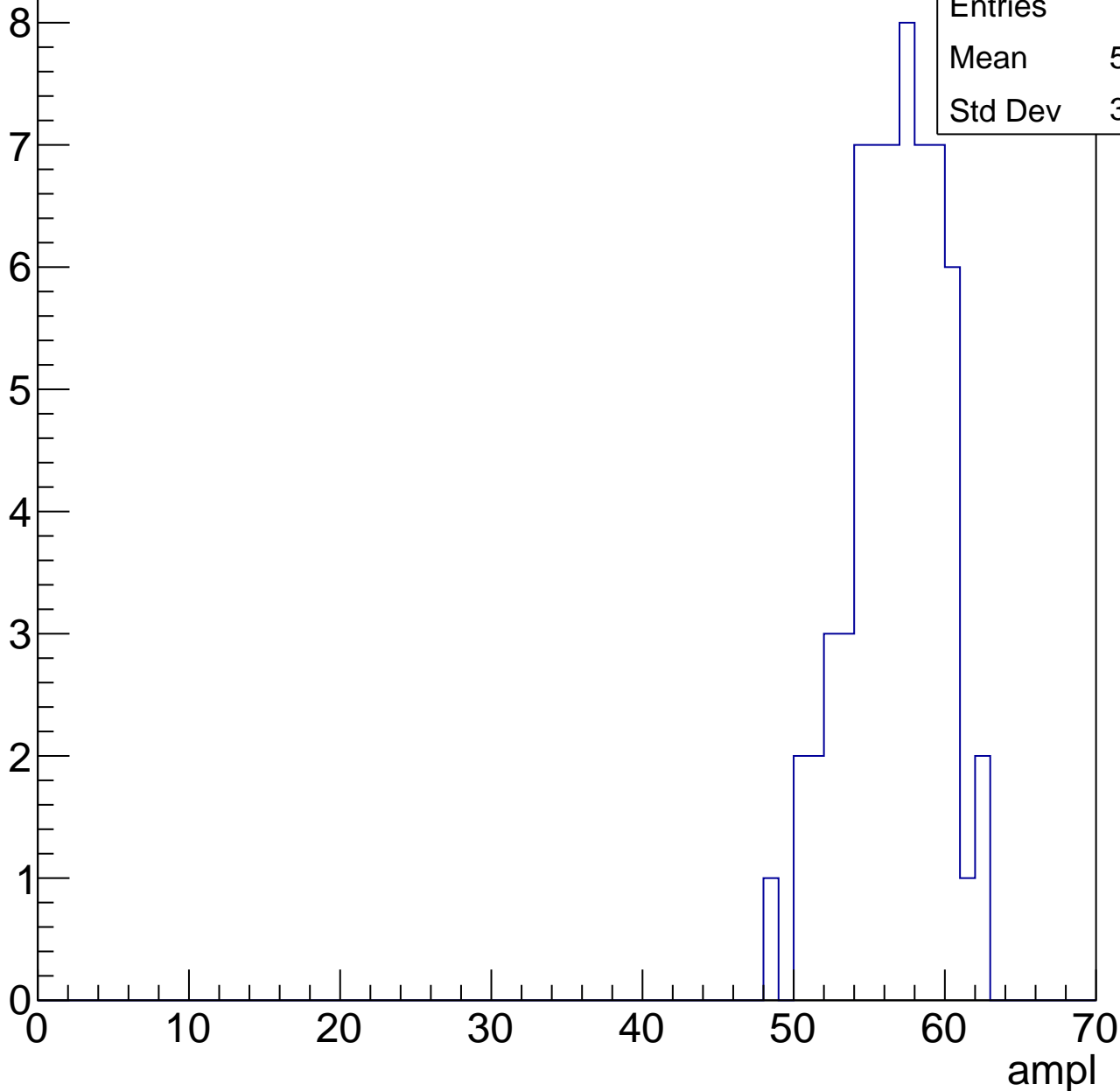


# B1L003S, U23-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	56.19
Std Dev	3.049

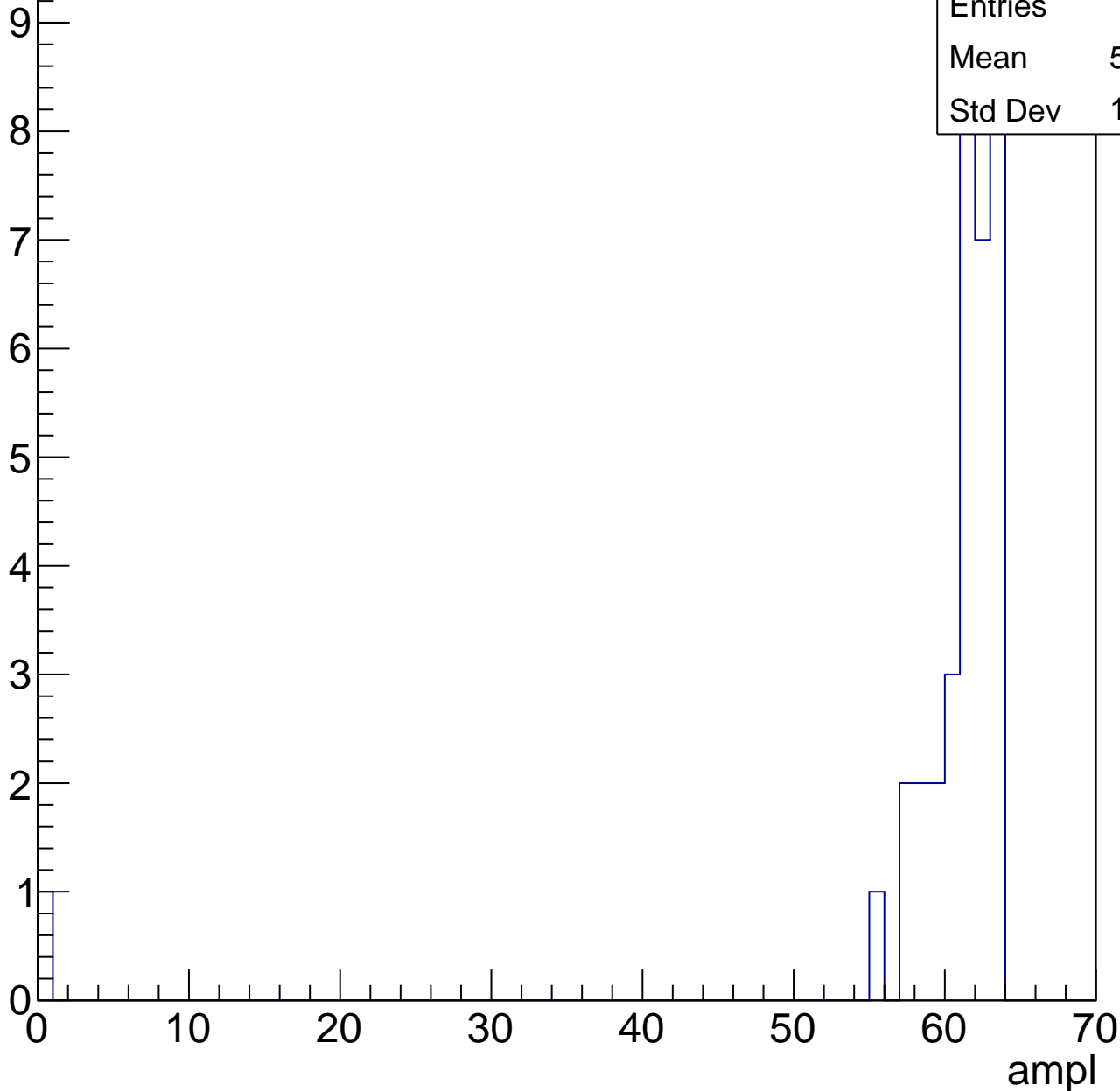


# B1L003S, U23-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

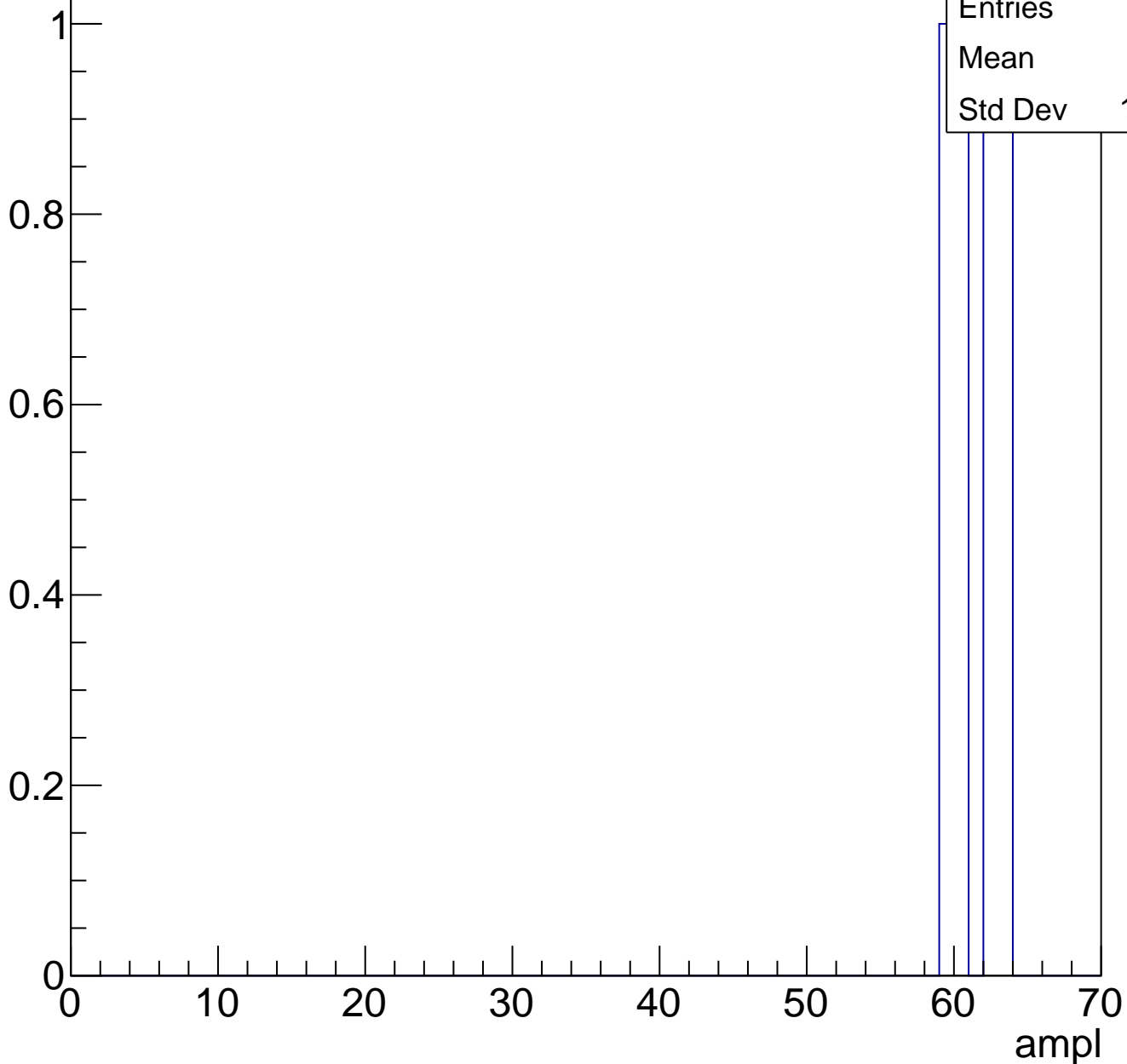
Entries	35
Mean	59.14
Std Dev	10.33



# B1L003S, U23-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

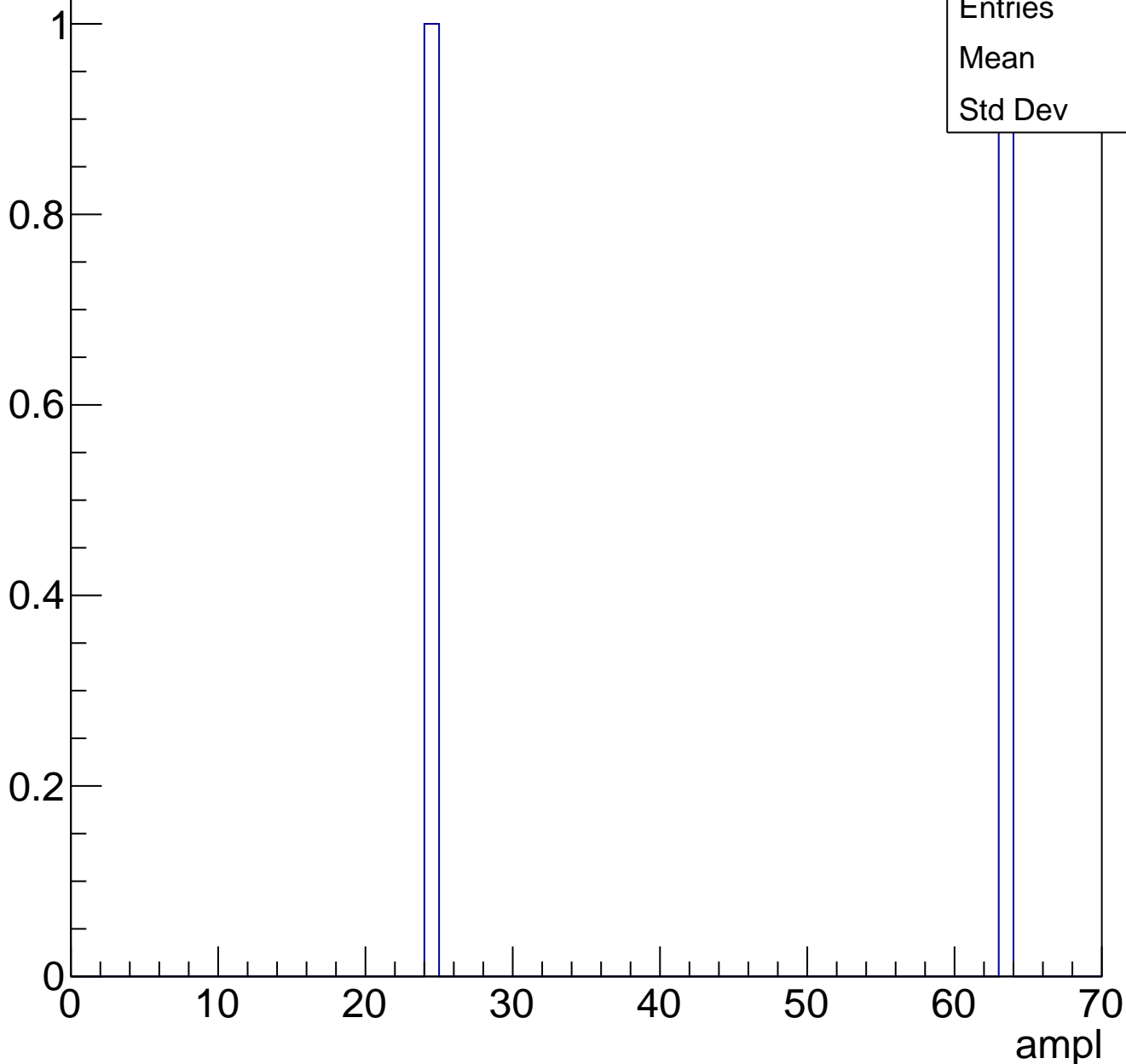




# B1L003S, U23-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	43.5
Std Dev	19.5

# B1L003S, U23-ch34, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	72
Mean	28.36
Std Dev	4.568

**Gaus mean : 29.3848**

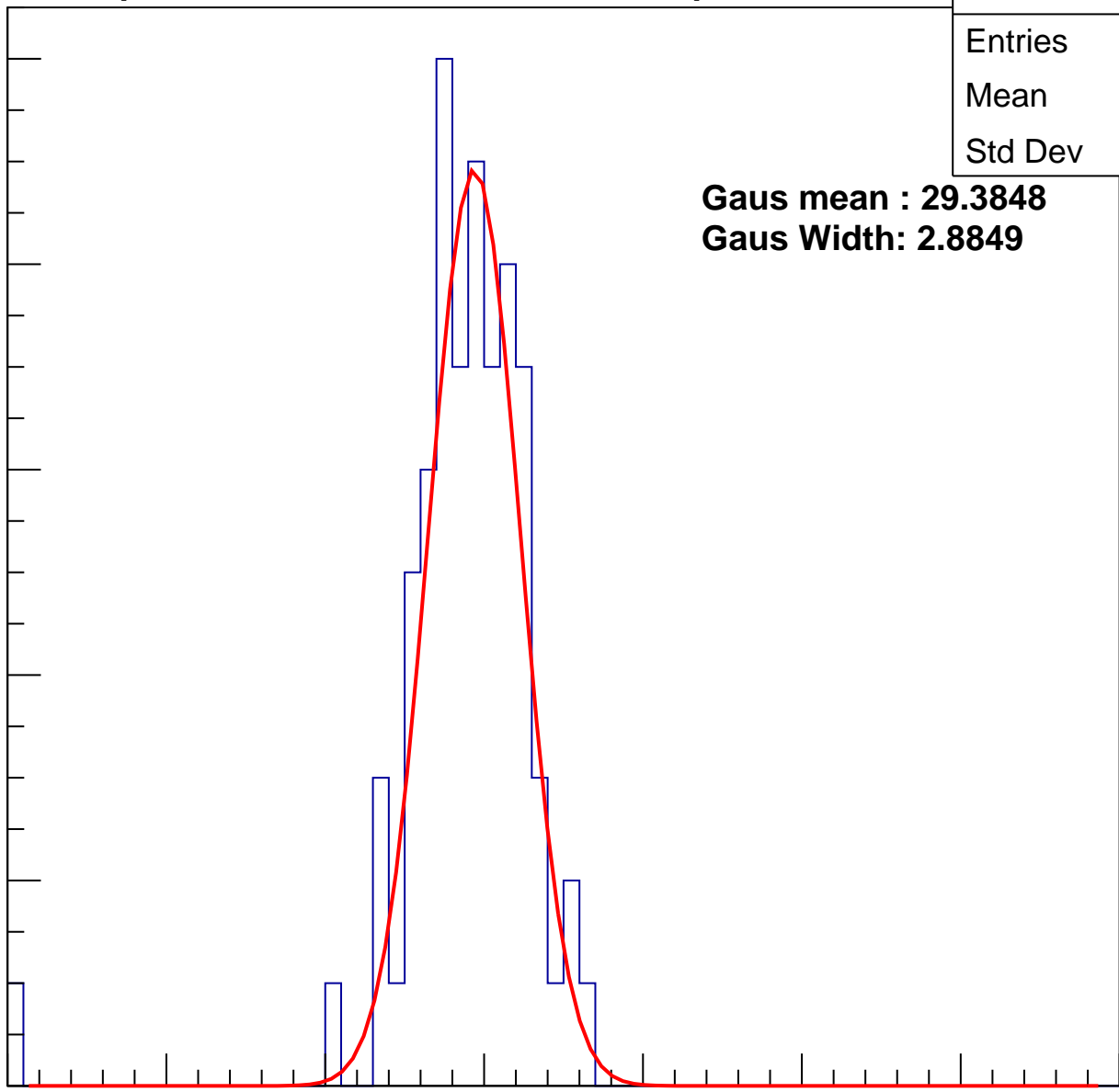
**Gaus Width: 2.8849**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



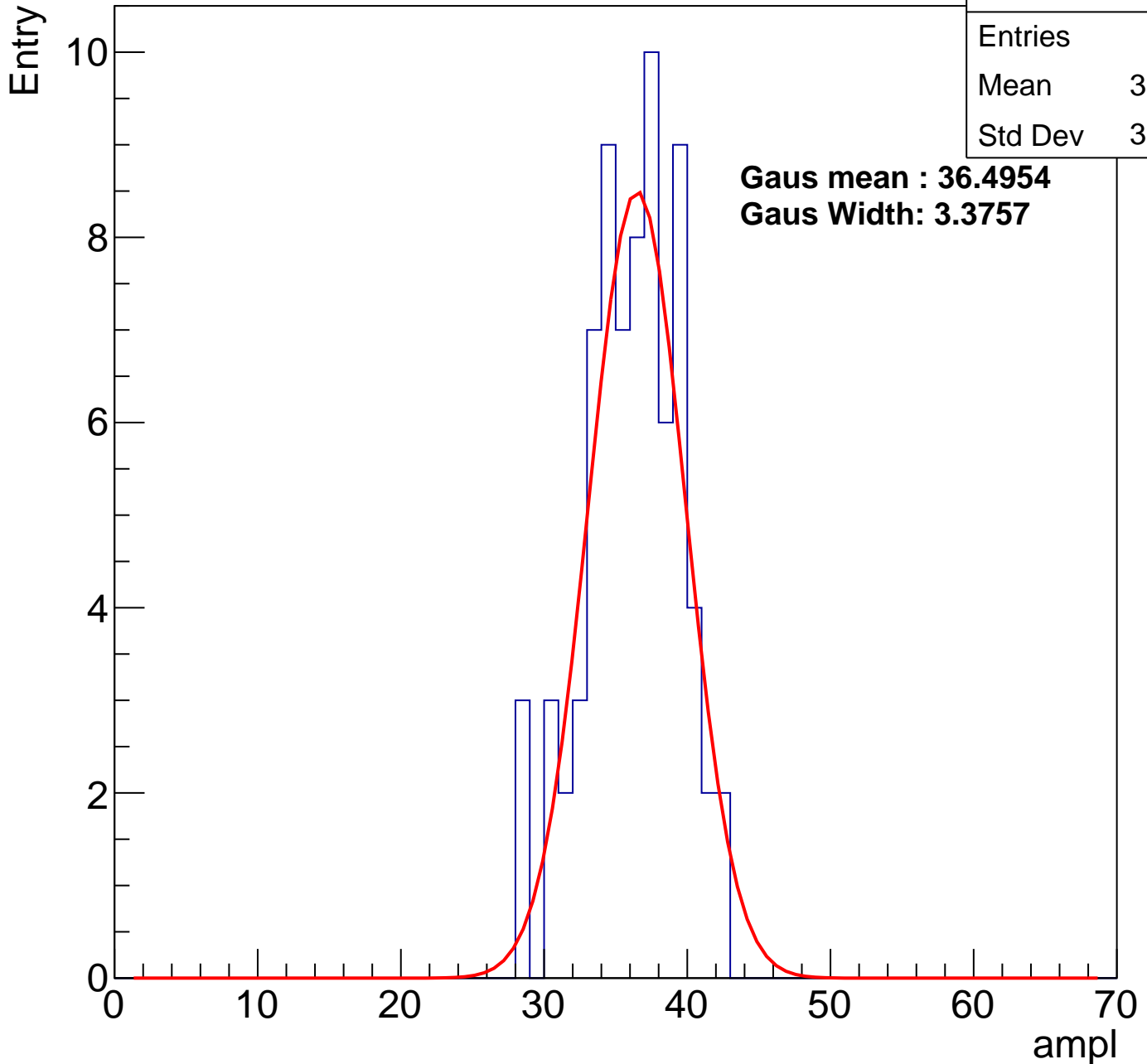
# B1L003S, U23-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	35.69
Std Dev	3.262

**Gaus mean : 36.4954**

**Gaus Width: 3.3757**



# B1L003S, U23-ch34, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.94
Std Dev	2.94

**Gaus mean : 43.7506**

**Gaus Width: 2.2784**

10

8

6

4

2

0

0

2

4

6

8

10

ampl

70

60

50

40

30

20

10

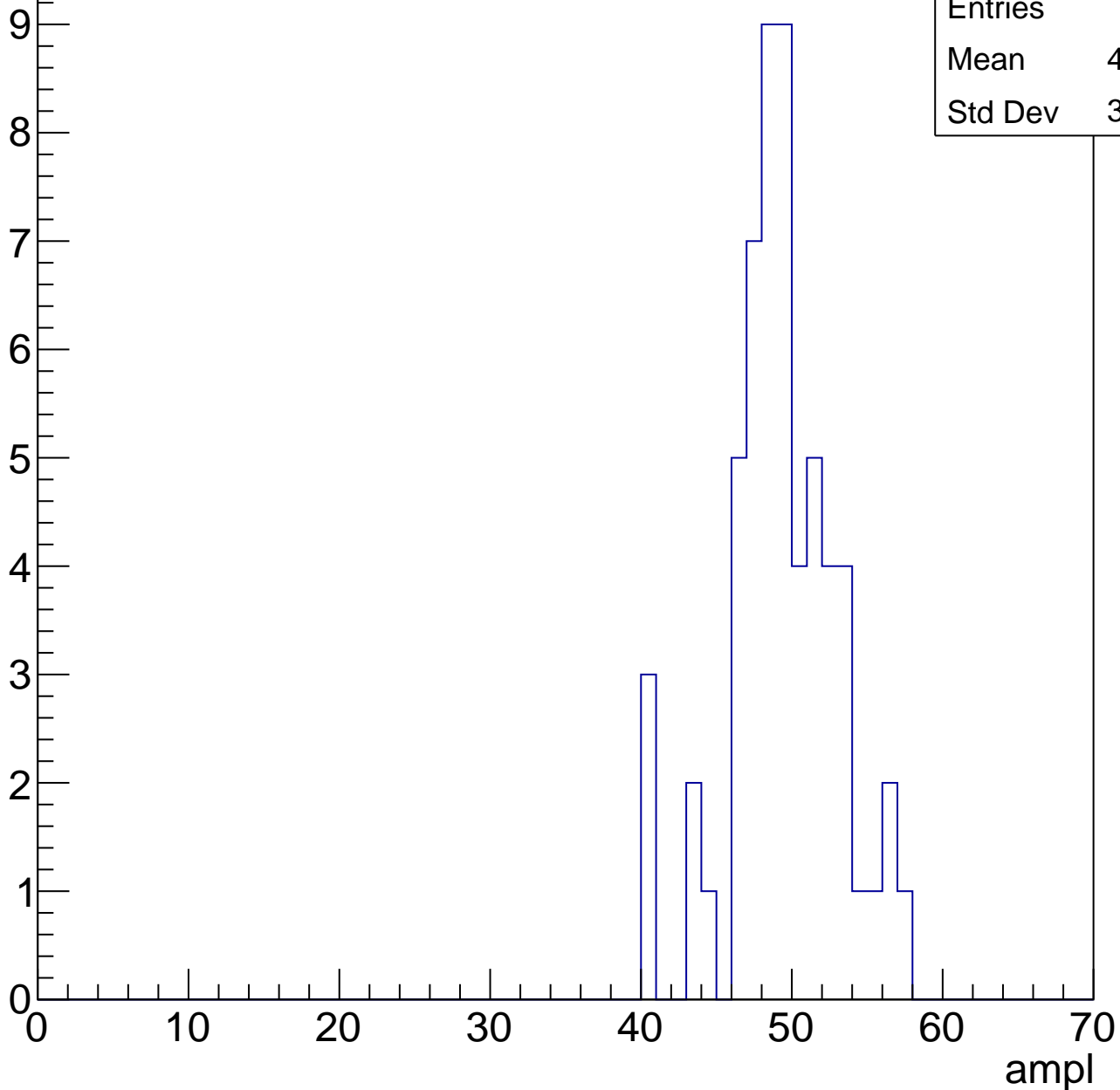
0

# B1L003S, U23-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	48.88
Std Dev	3.639

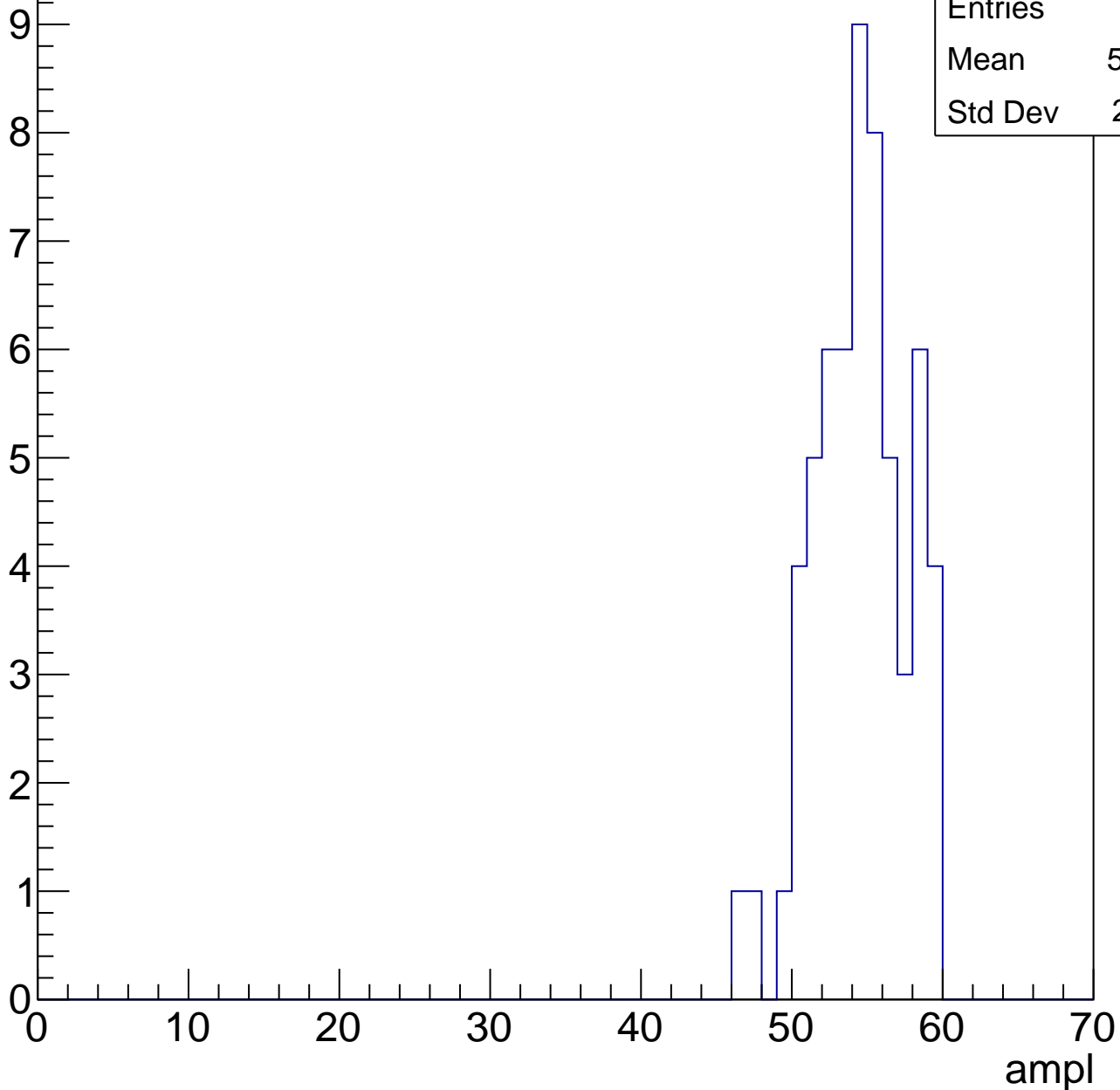


# B1L003S, U23-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.03
Std Dev	2.991



# B1L003S, U23-ch34, adc5

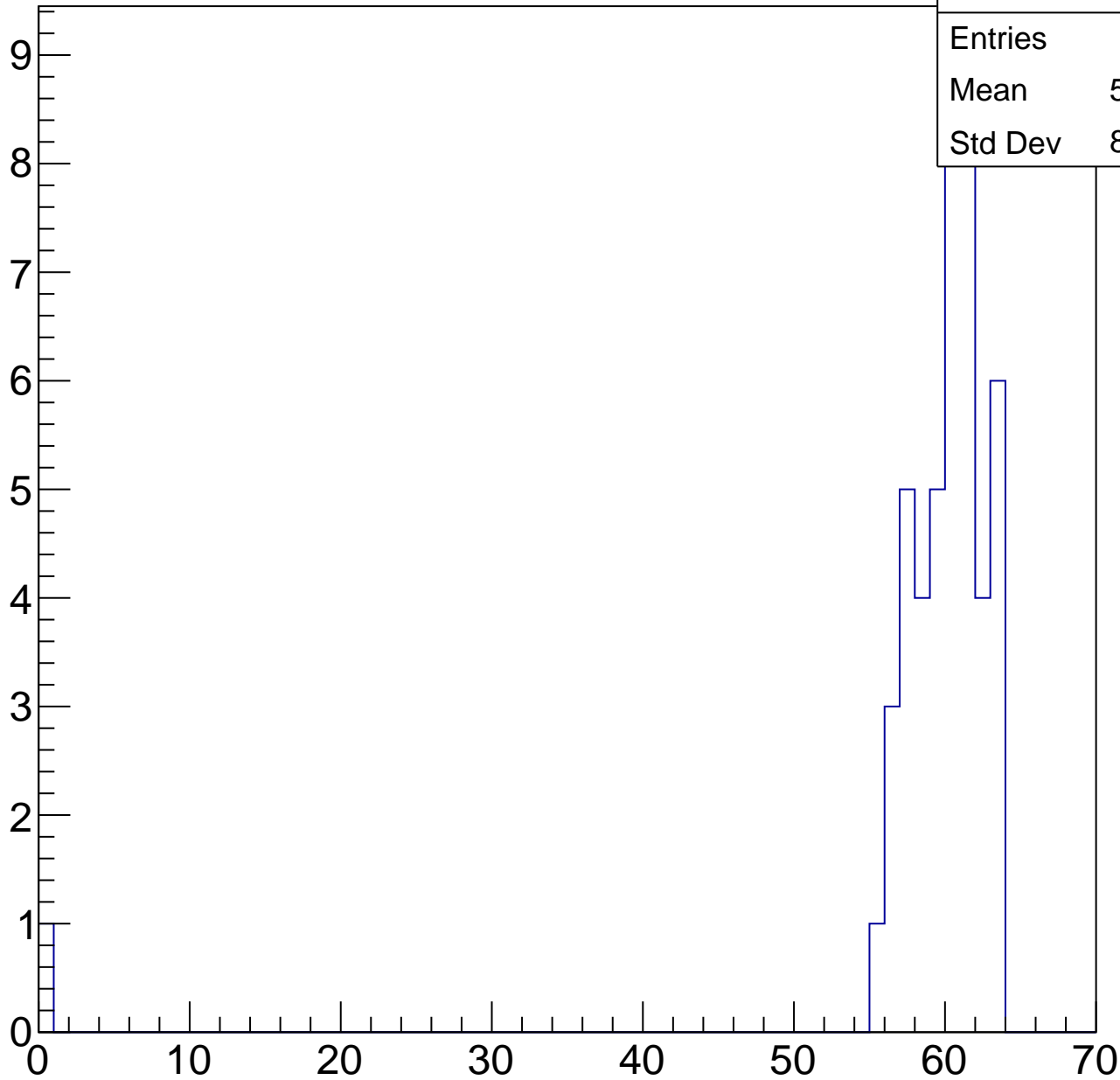
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	58.46
Std Dev	8.978

ampl

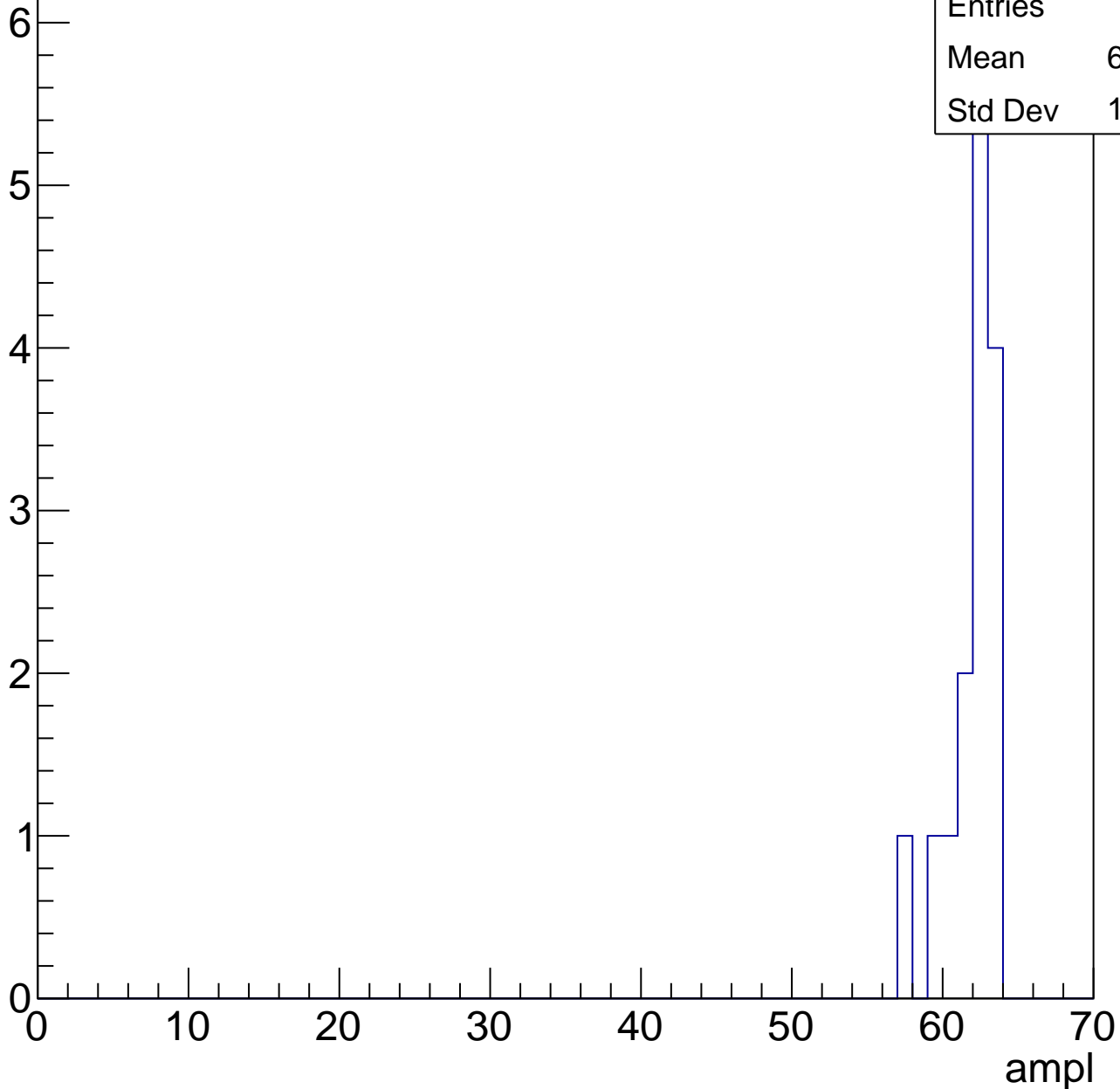


# B1L003S, U23-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.47
Std Dev	1.628





# B1L003S, U23-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L003S, U23-ch35, adc0

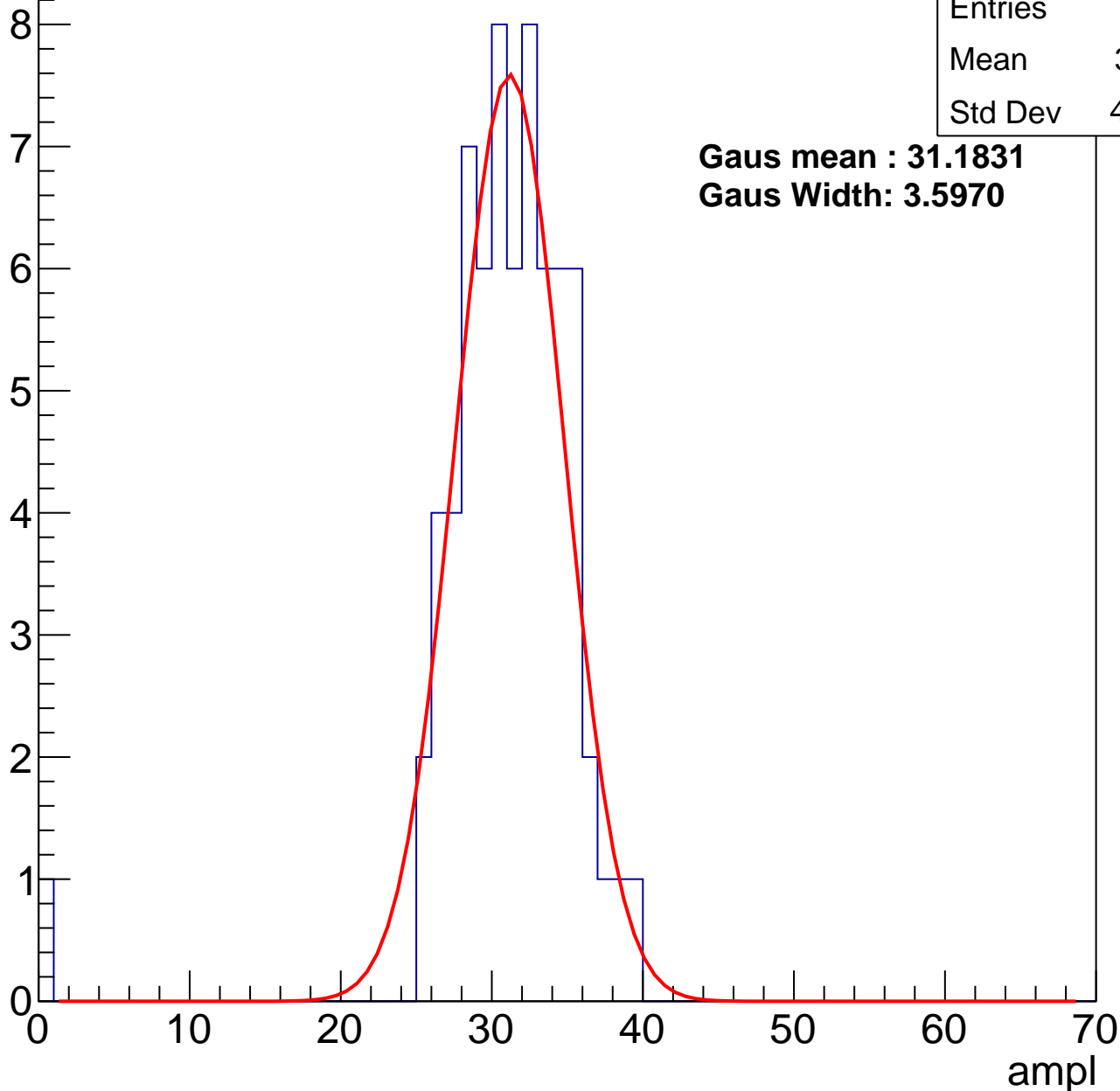
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	30.61
Std Dev	4.908

**Gaus mean : 31.1831**

**Gaus Width: 3.5970**



# B1L003S, U23-ch35, adc1

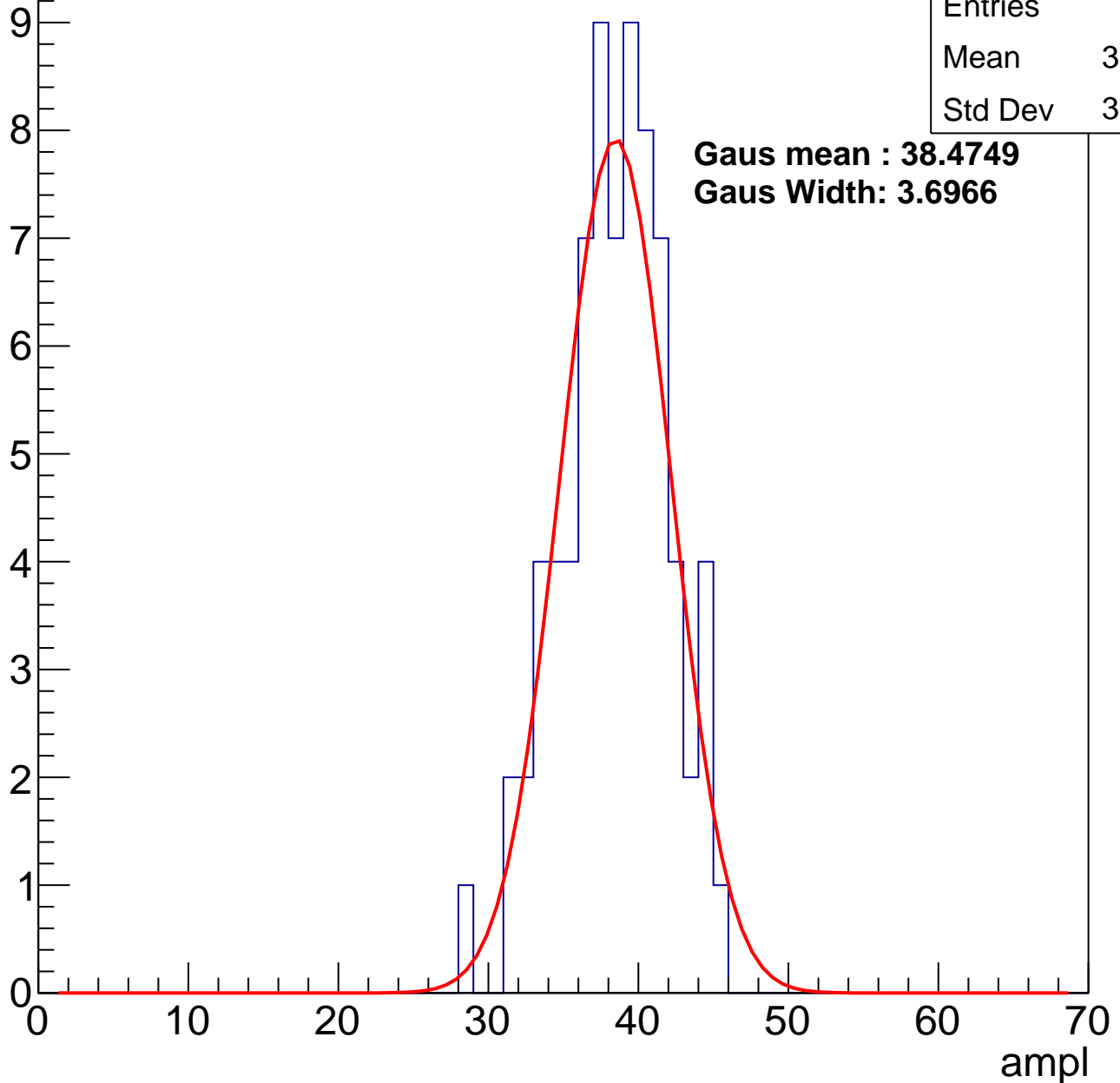
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	37.95
Std Dev	3.498

**Gaus mean : 38.4749**

**Gaus Width: 3.6966**



# B1L003S, U23-ch35, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	65
Mean	44.8
Std Dev	2.993

**Gaus mean : 45.6082**

**Gaus Width: 3.3952**

Entry

10

8

6

4

2

0

0

10

20

30

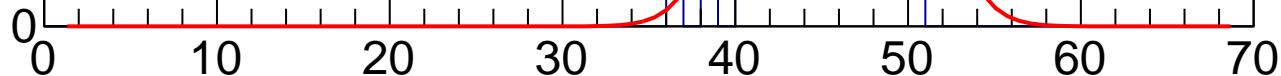
40

50

60

70

ampl

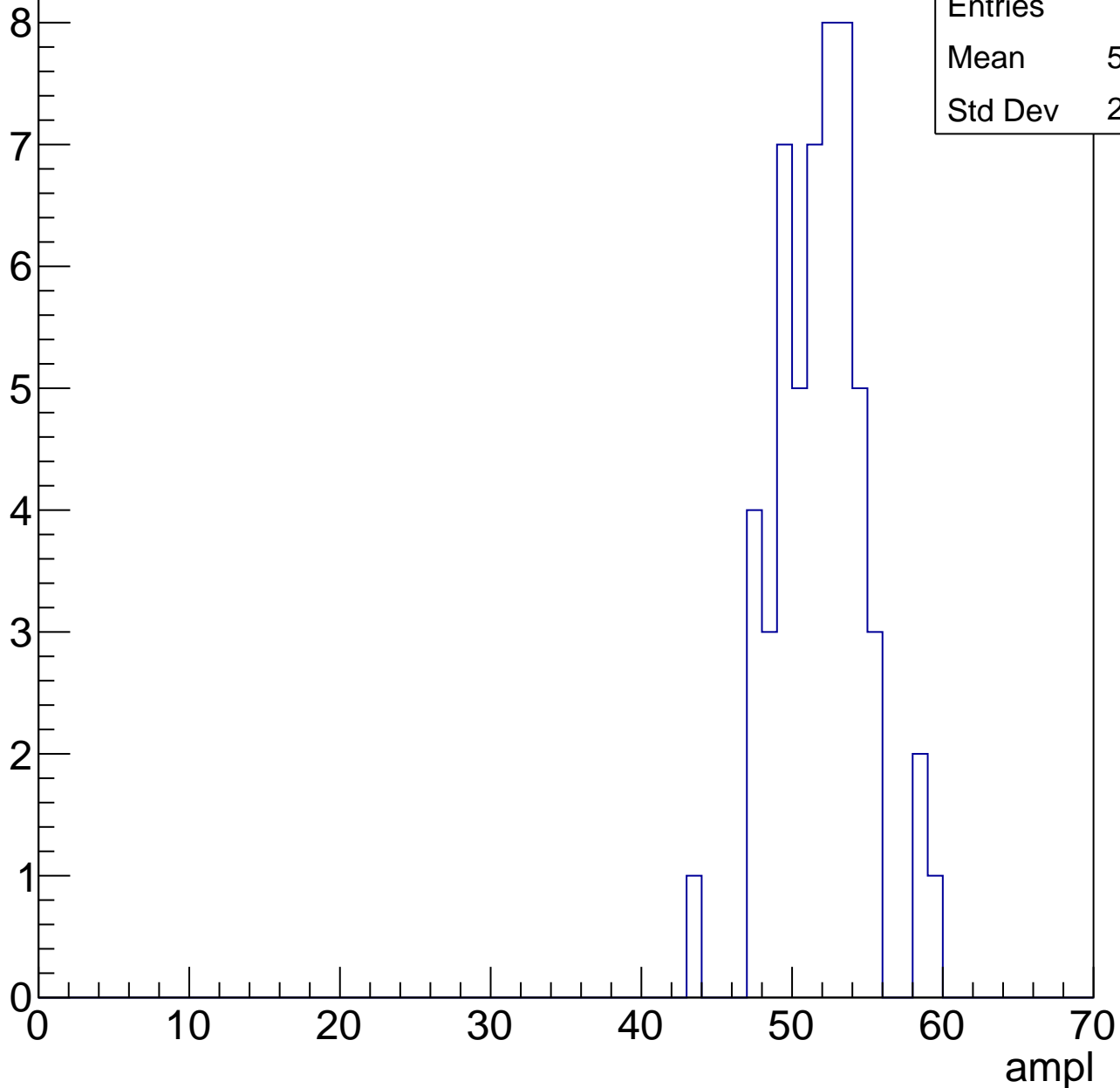


# B1L003S, U23-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	51.39
Std Dev	2.965



# B1L003S, U23-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	54
Mean	56.22
Std Dev	3.023

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

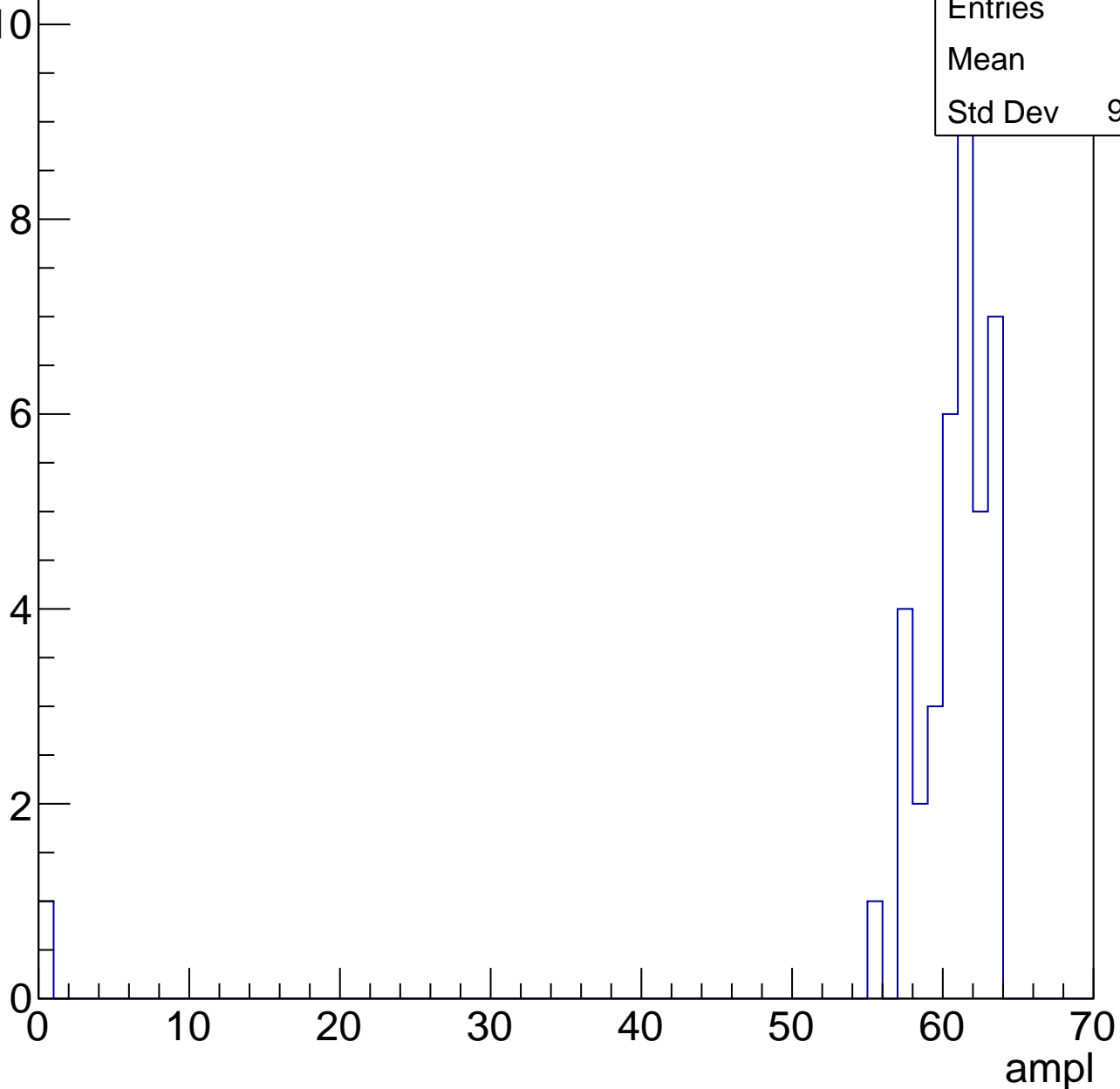
70

# B1L003S, U23-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

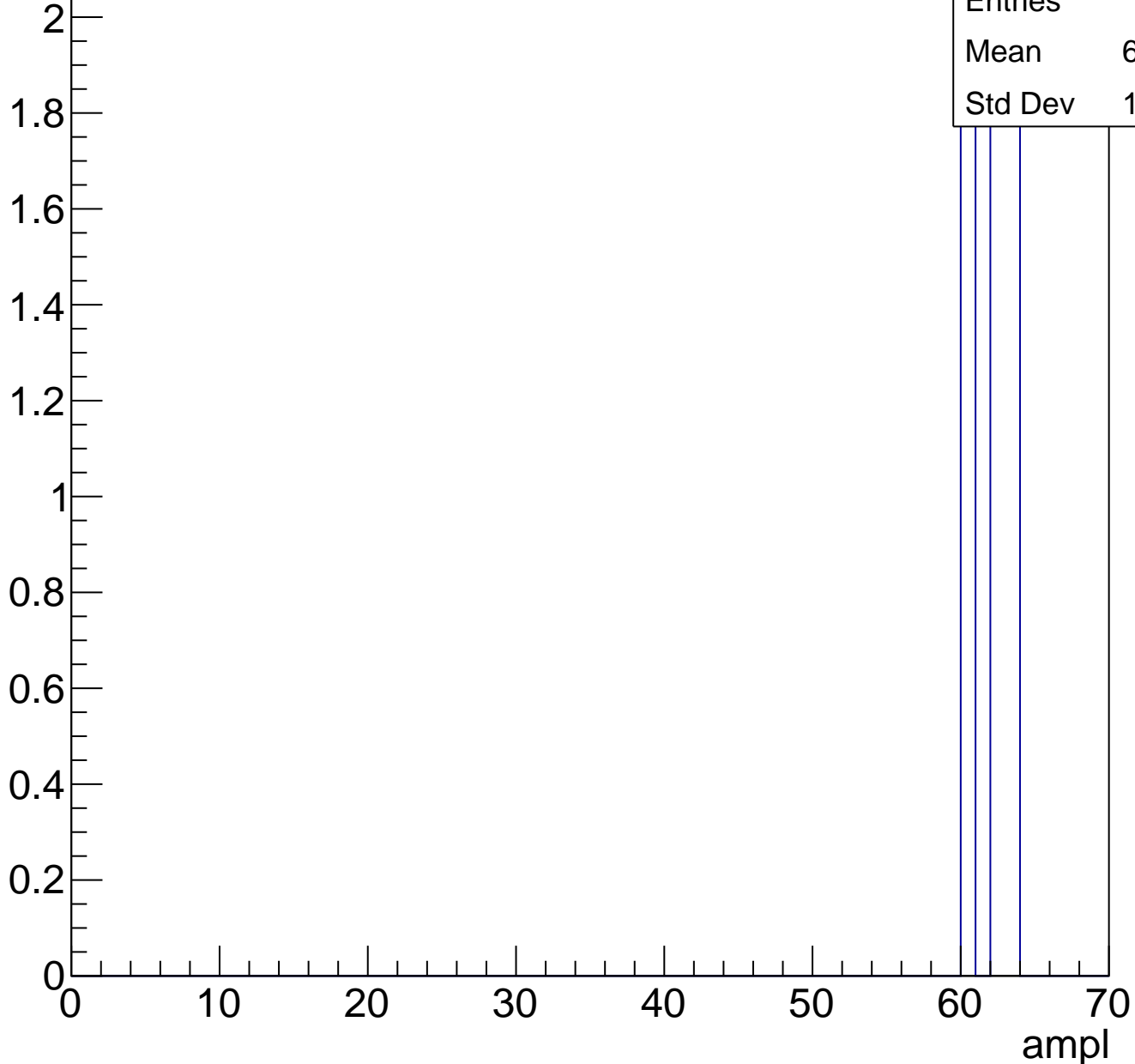
Entries	39
Mean	58.9
Std Dev	9.763



# B1L003S, U23-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch36, adc0

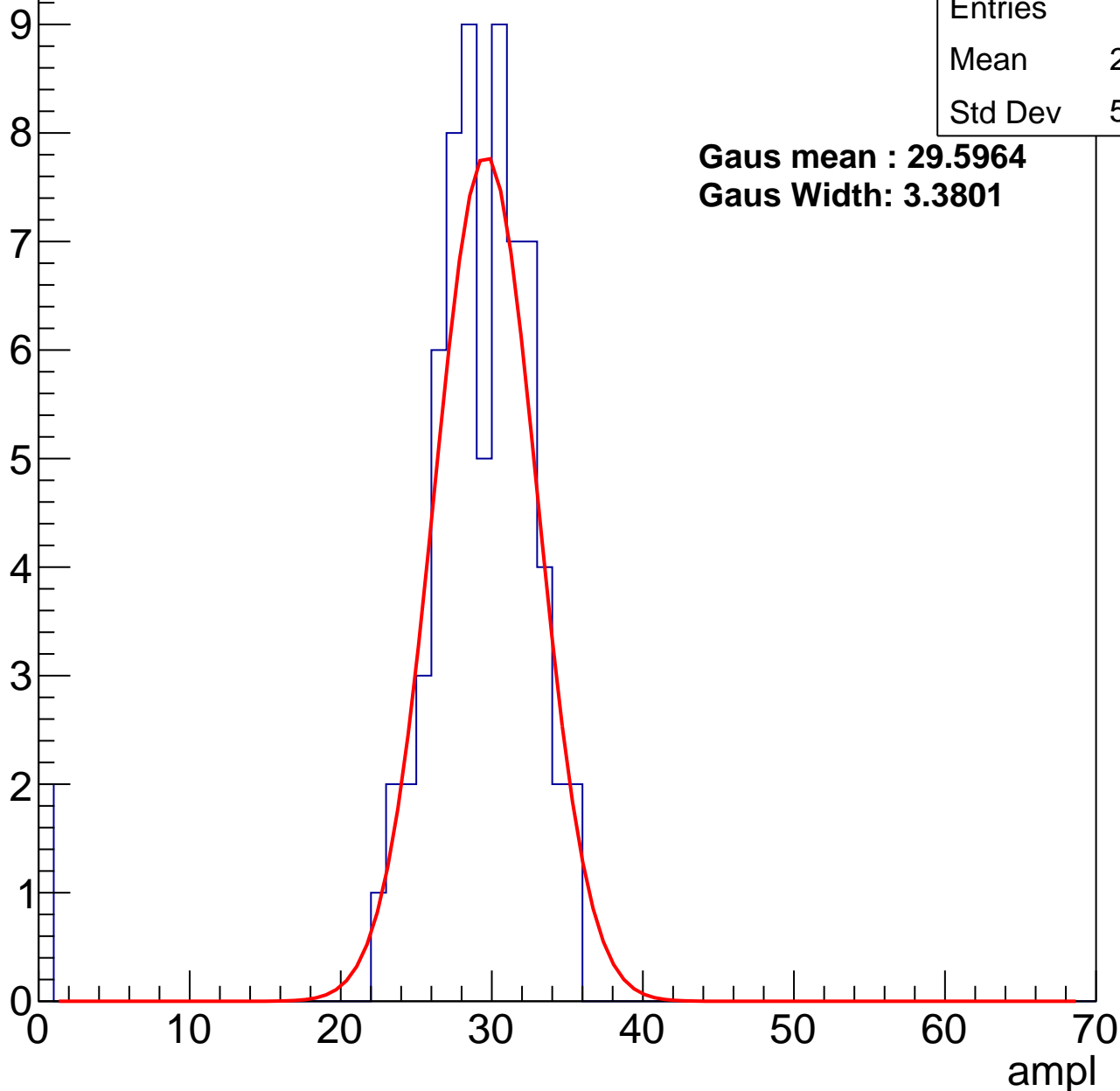
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	28.13
Std Dev	5.685

**Gaus mean : 29.5964**

**Gaus Width: 3.3801**



# B1L003S, U23-ch36, adc1

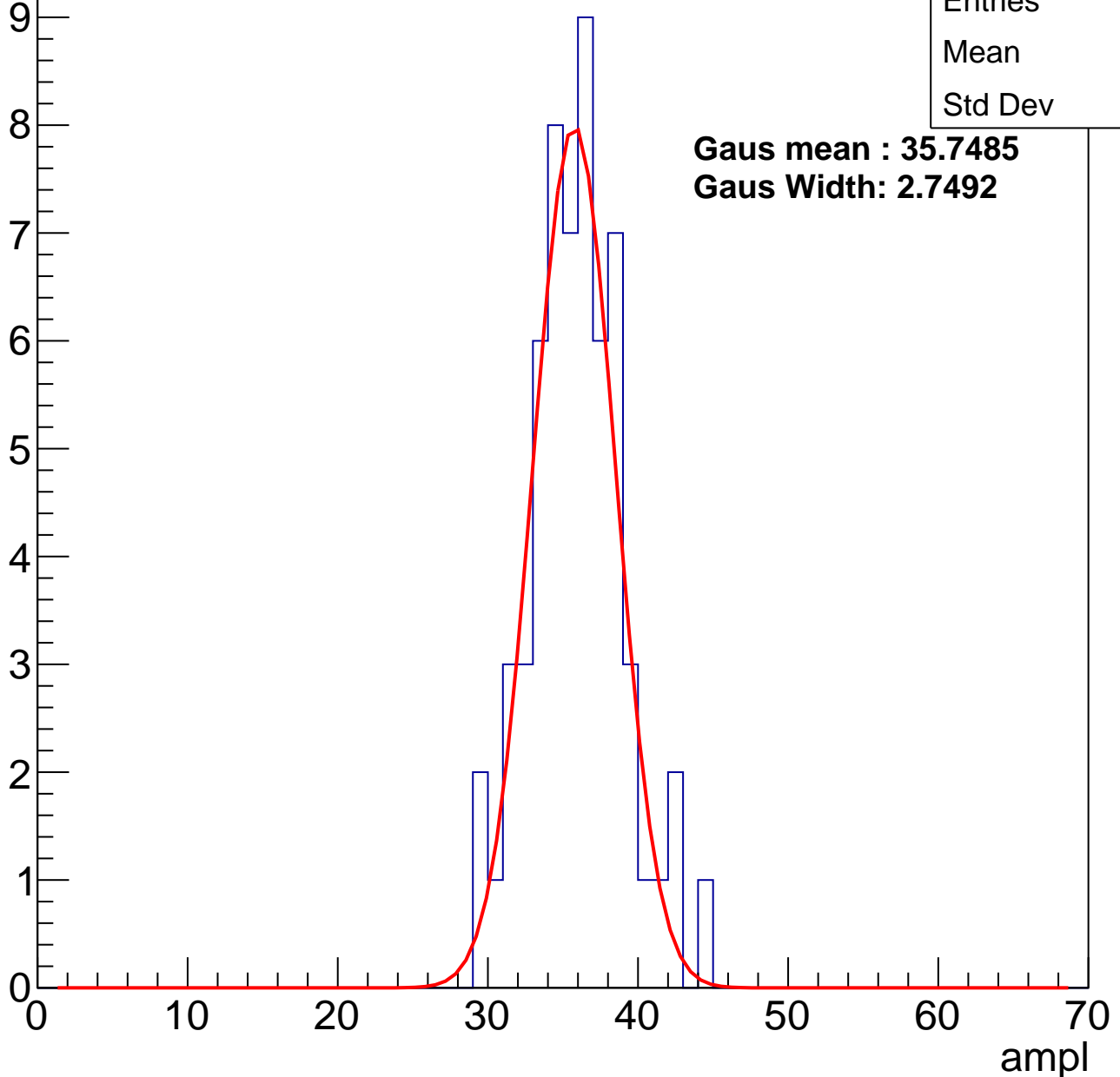
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.5
Std Dev	3.09

**Gaus mean : 35.7485**

**Gaus Width: 2.7492**



# B1L003S, U23-ch36, adc2

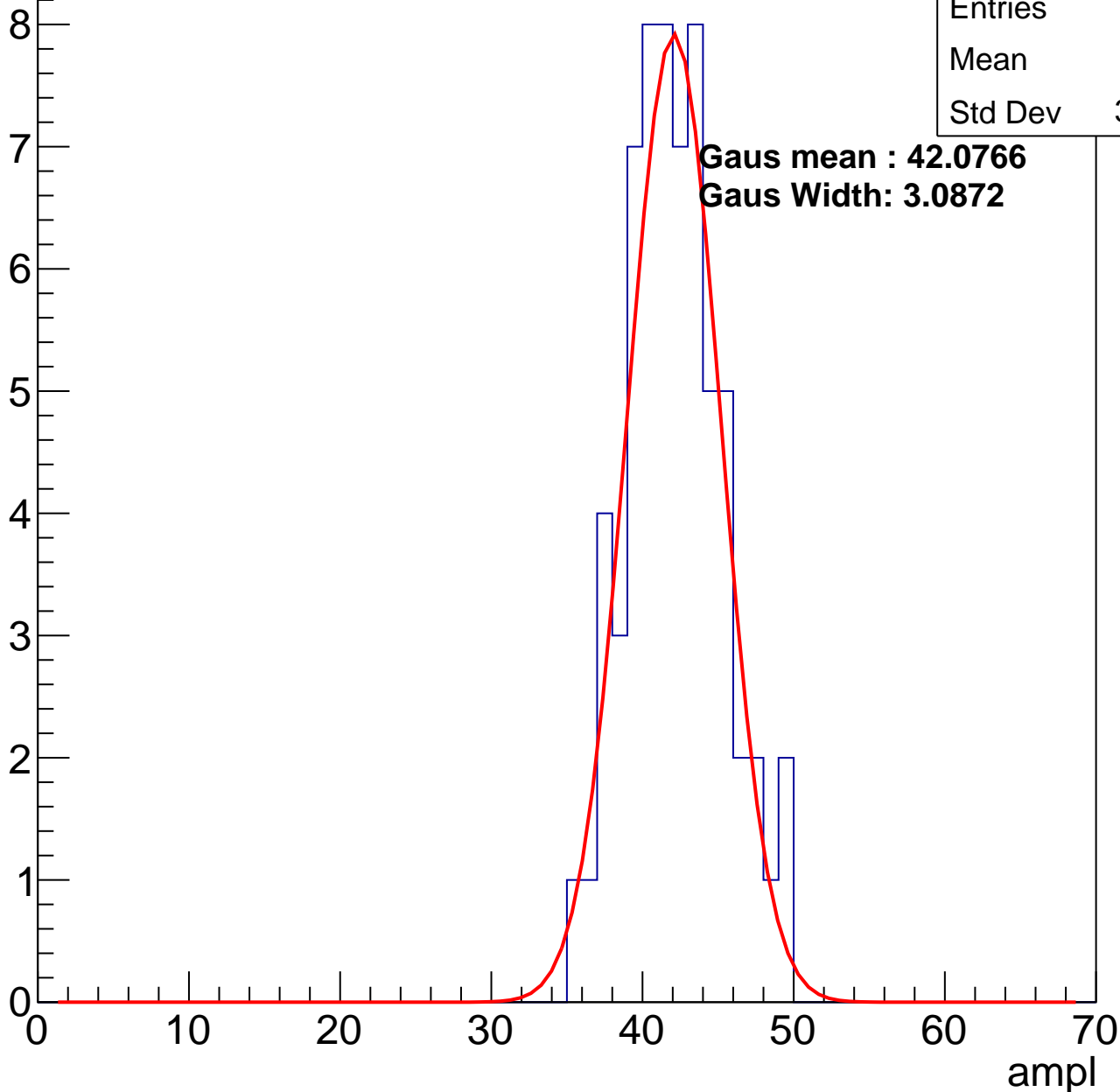
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	41.7
Std Dev	3.111

**Gaus mean : 42.0766**

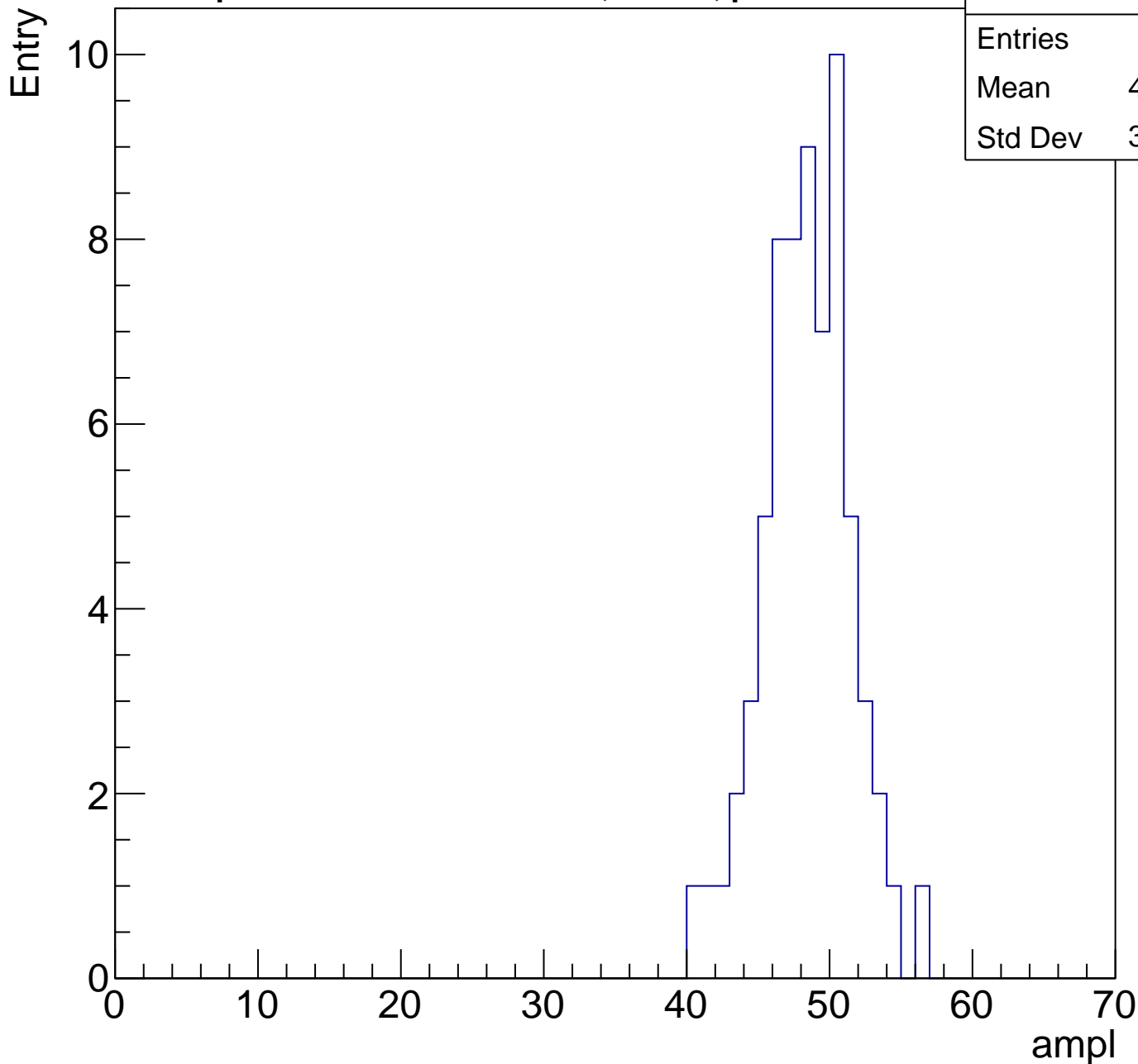
**Gaus Width: 3.0872**



# B1L003S, U23-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	47.94
Std Dev	3.046



# B1L003S, U23-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

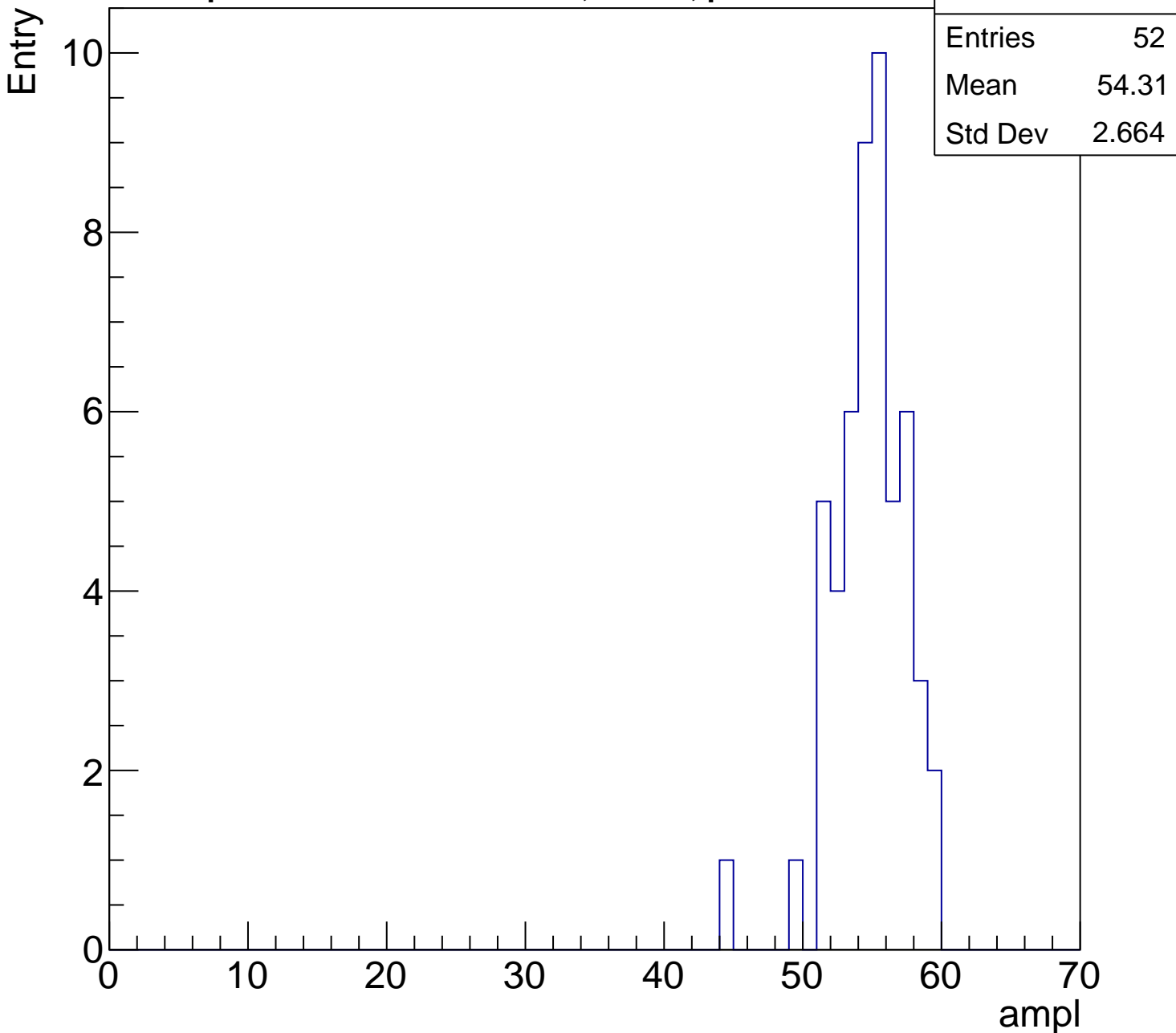
Entries	52
Mean	54.31
Std Dev	2.664

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U23-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

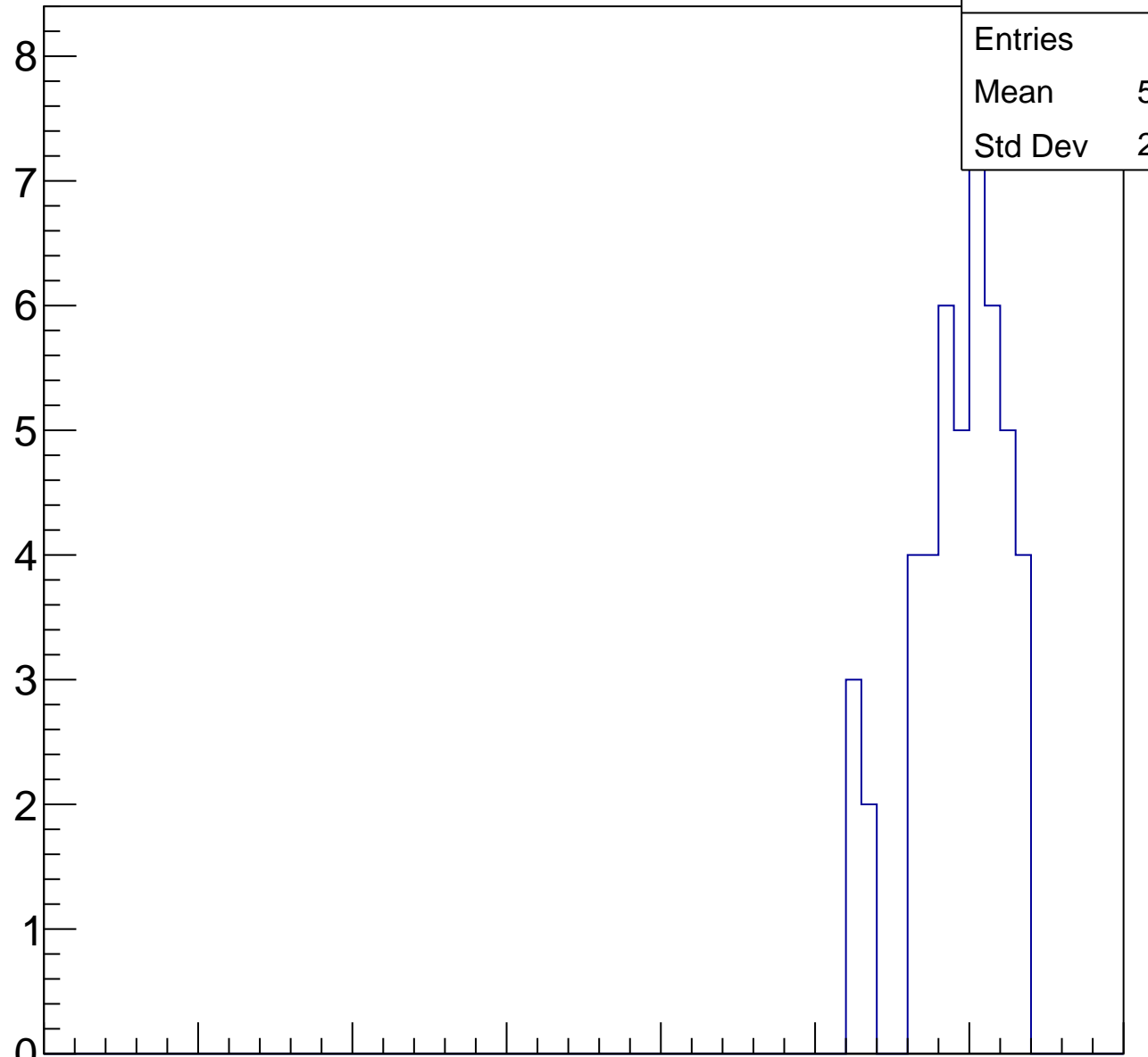
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	58.83
Std Dev	2.977

ampl

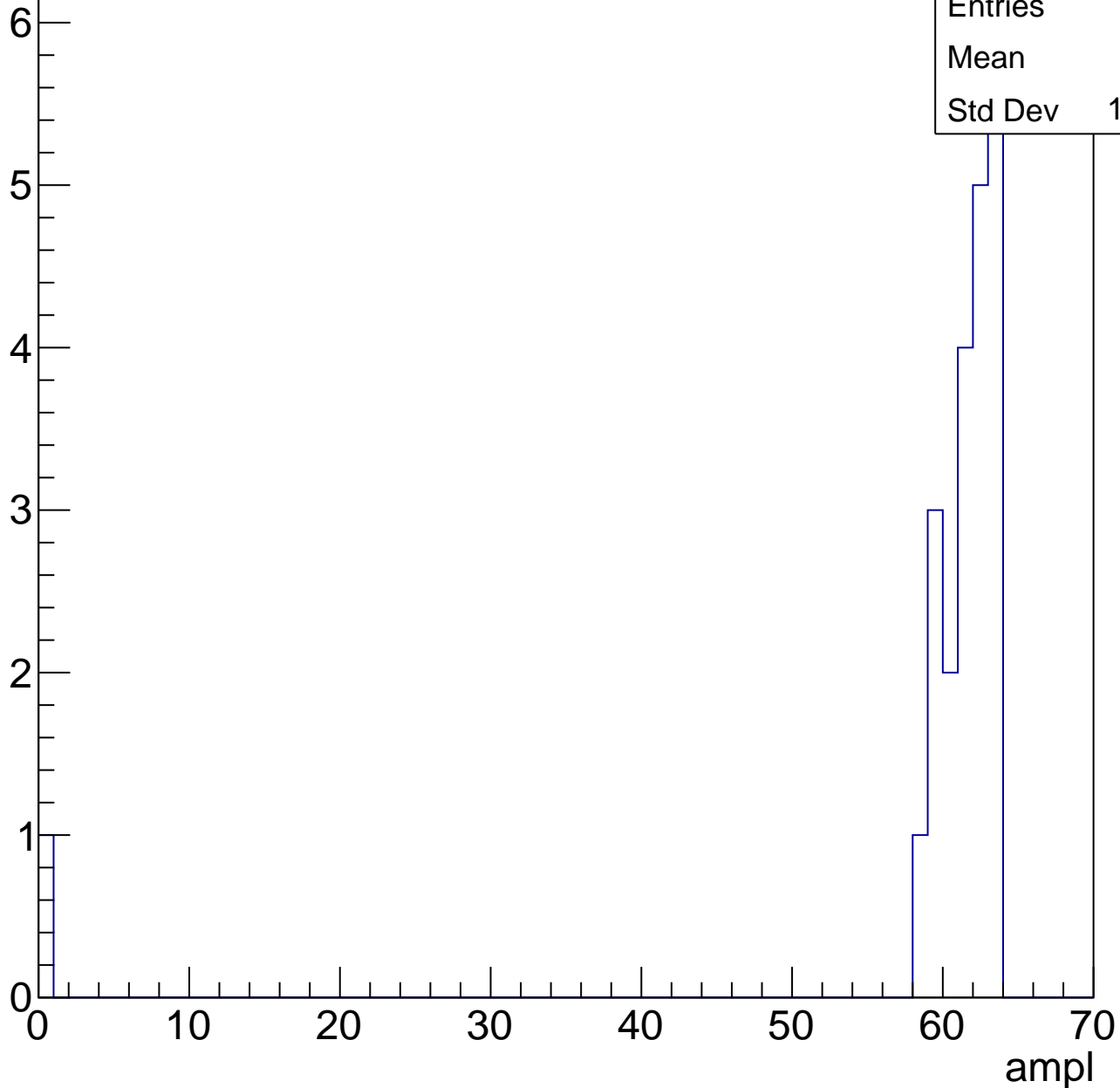
0 10 20 30 40 50 60 70



# B1L003S, U23-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch37, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	70
Mean	29.46
Std Dev	3.201

**Gaus mean : 30.0565**

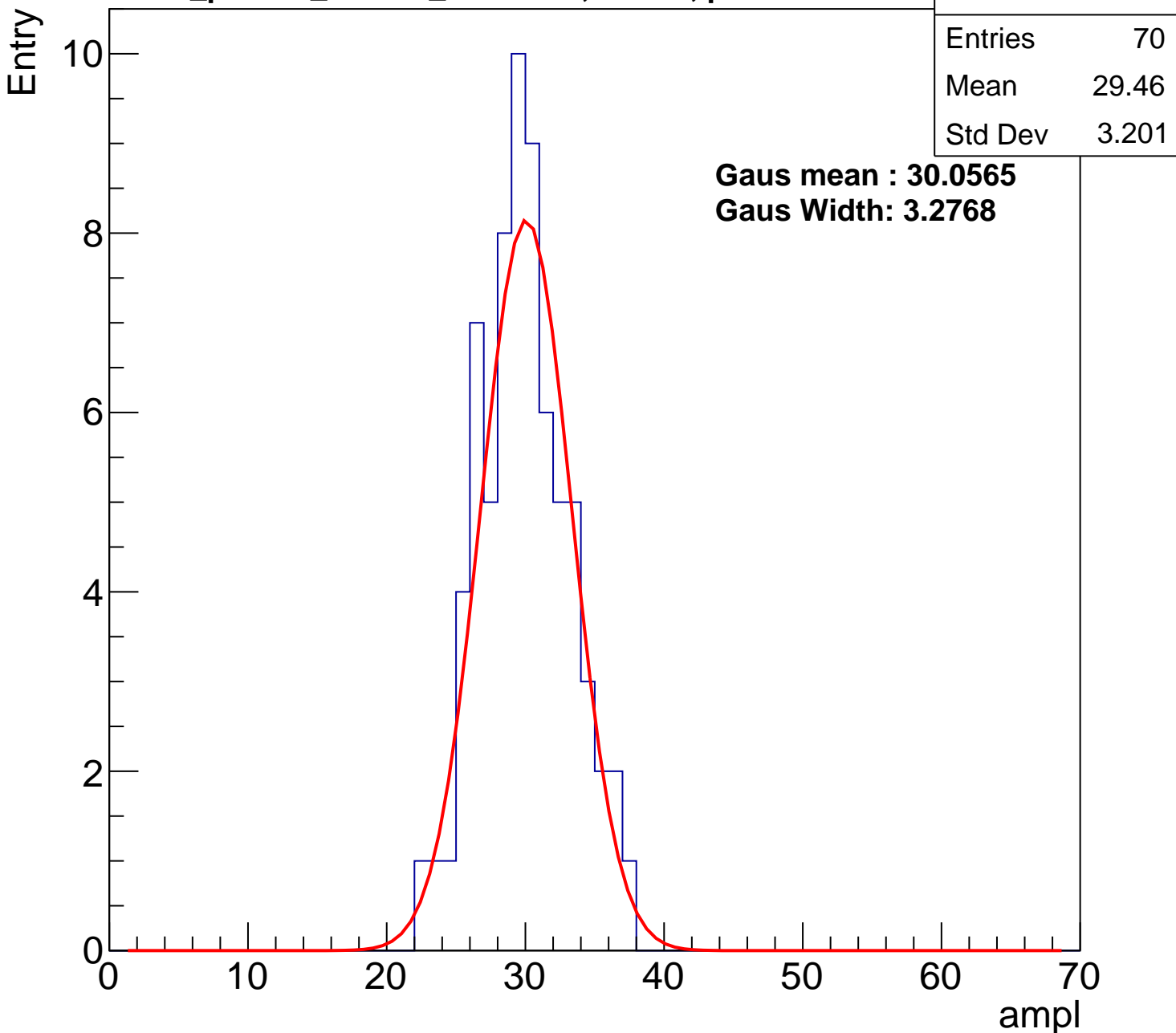
**Gaus Width: 3.2768**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch37, adc1

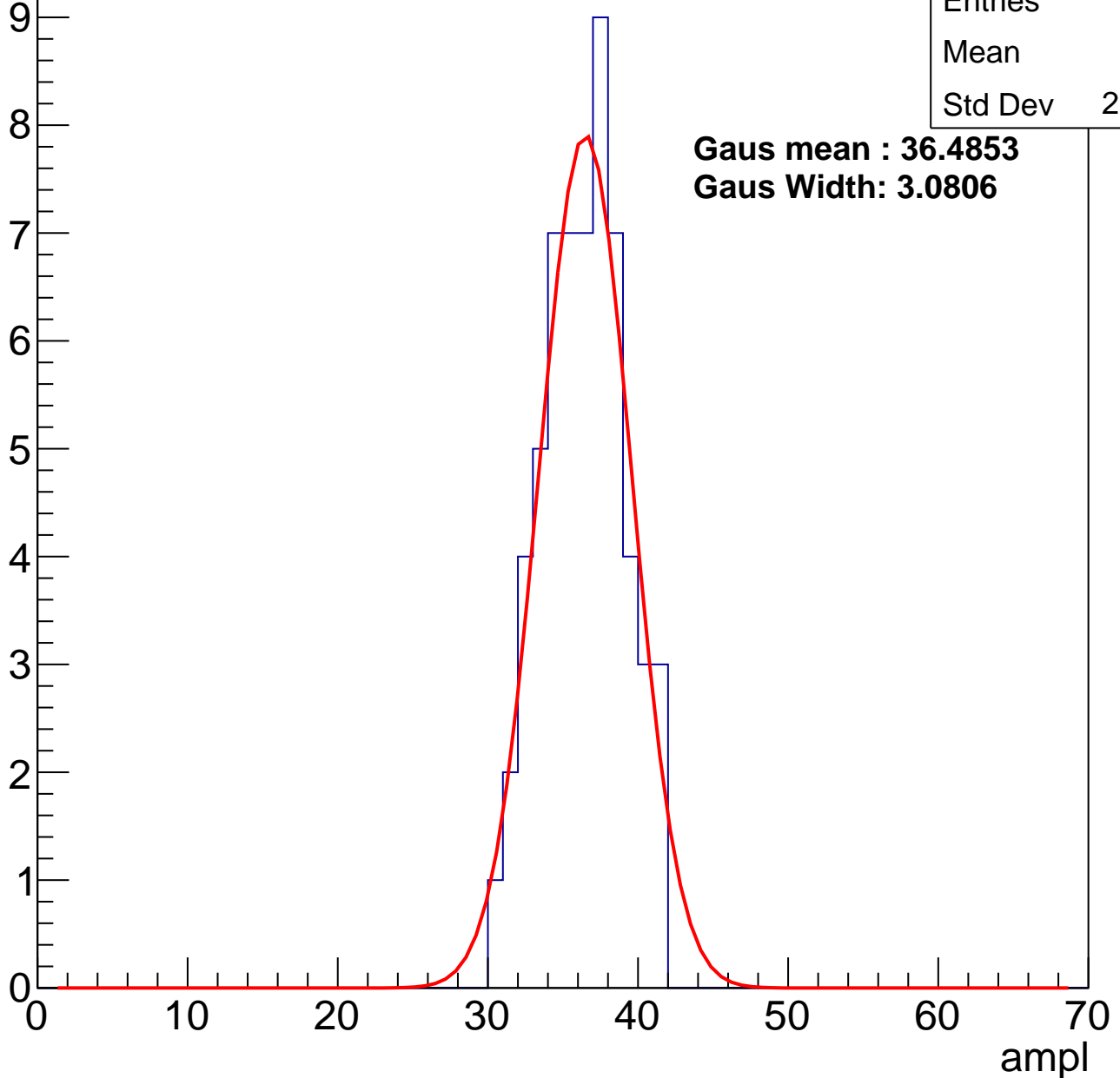
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.9
Std Dev	2.685

**Gaus mean : 36.4853**

**Gaus Width: 3.0806**



# B1L003S, U23-ch37, adc2

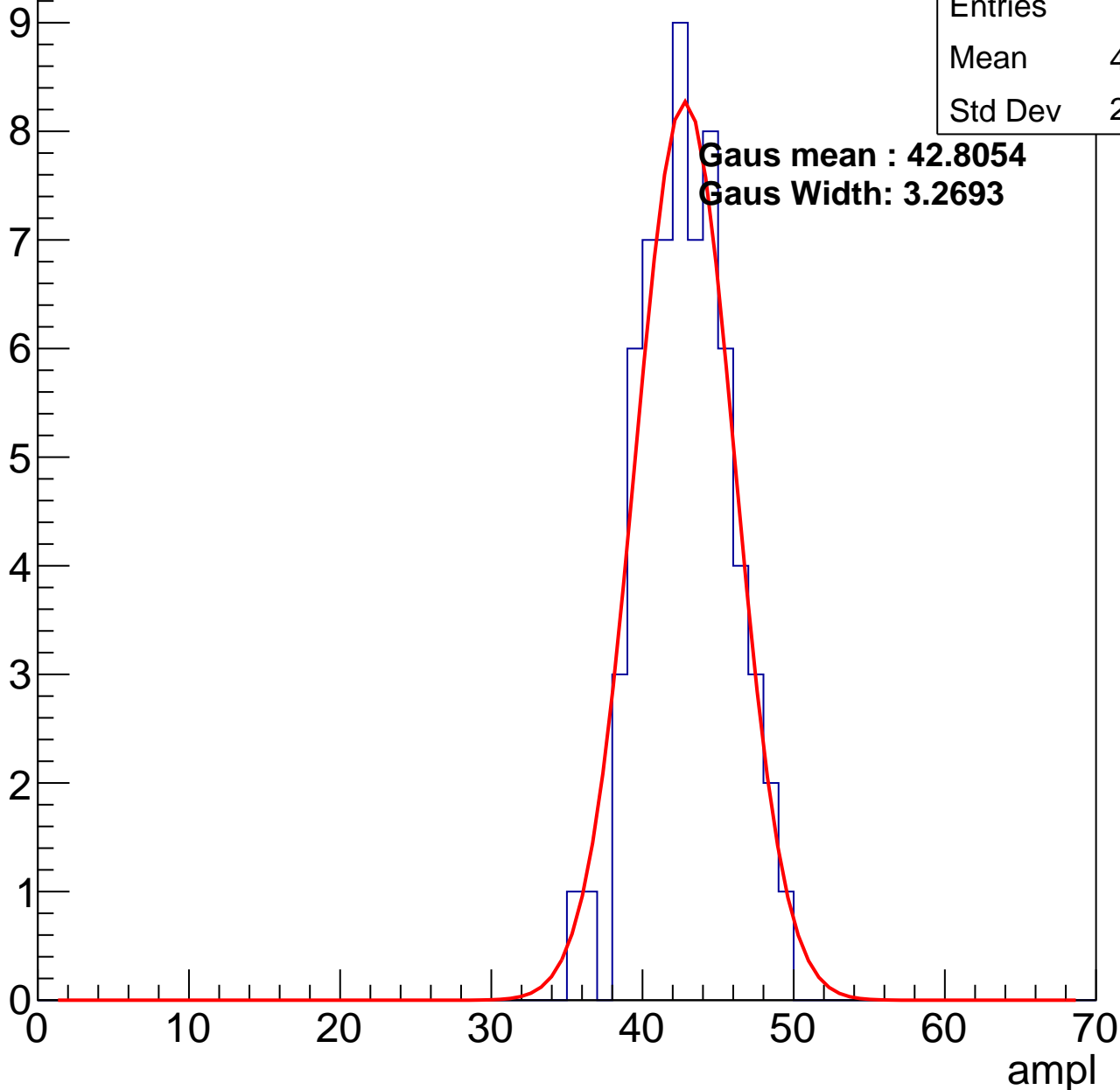
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.42
Std Dev	2.945

**Gaus mean : 42.8054**

**Gaus Width: 3.2693**

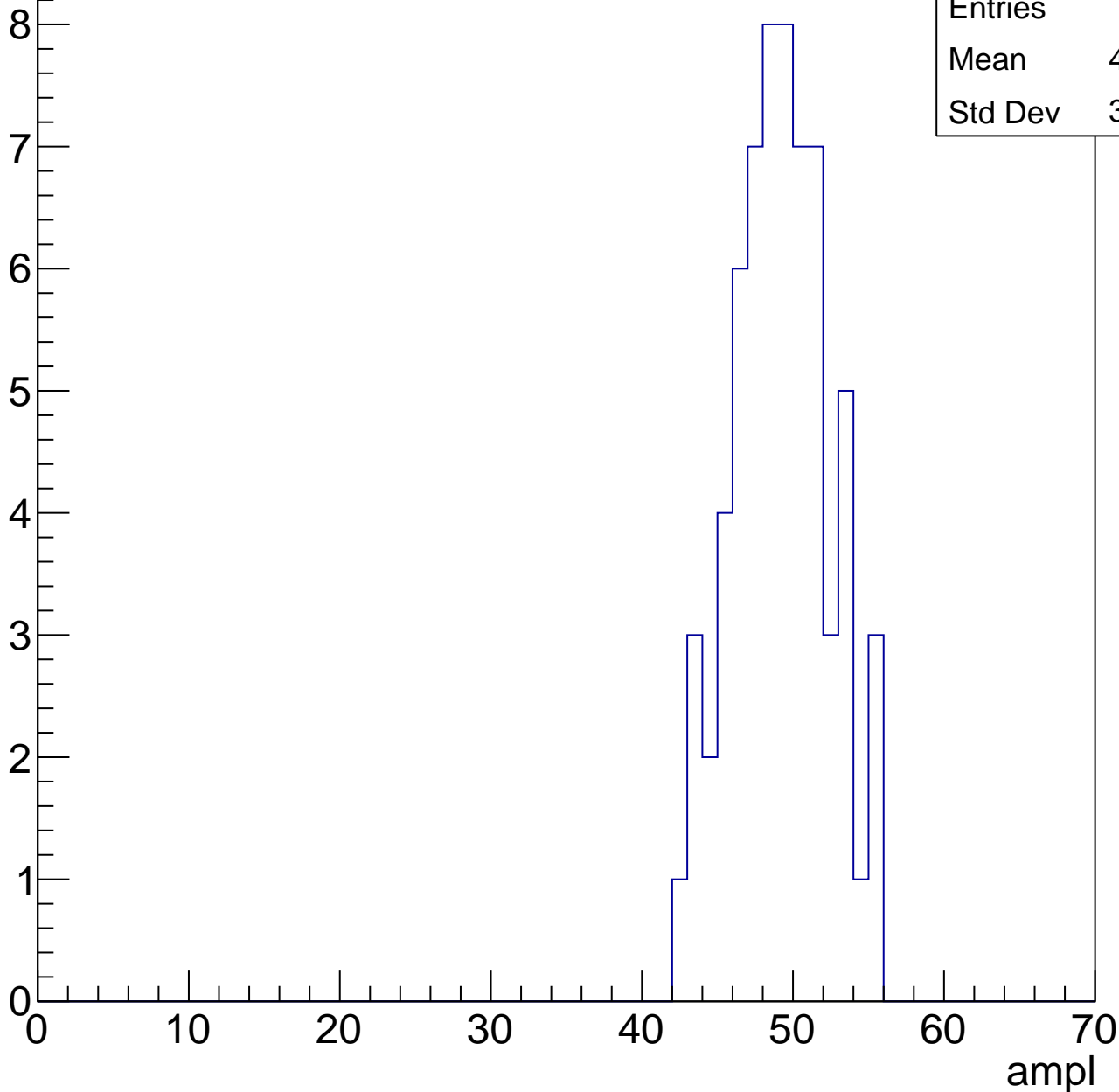


# B1L003S, U23-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

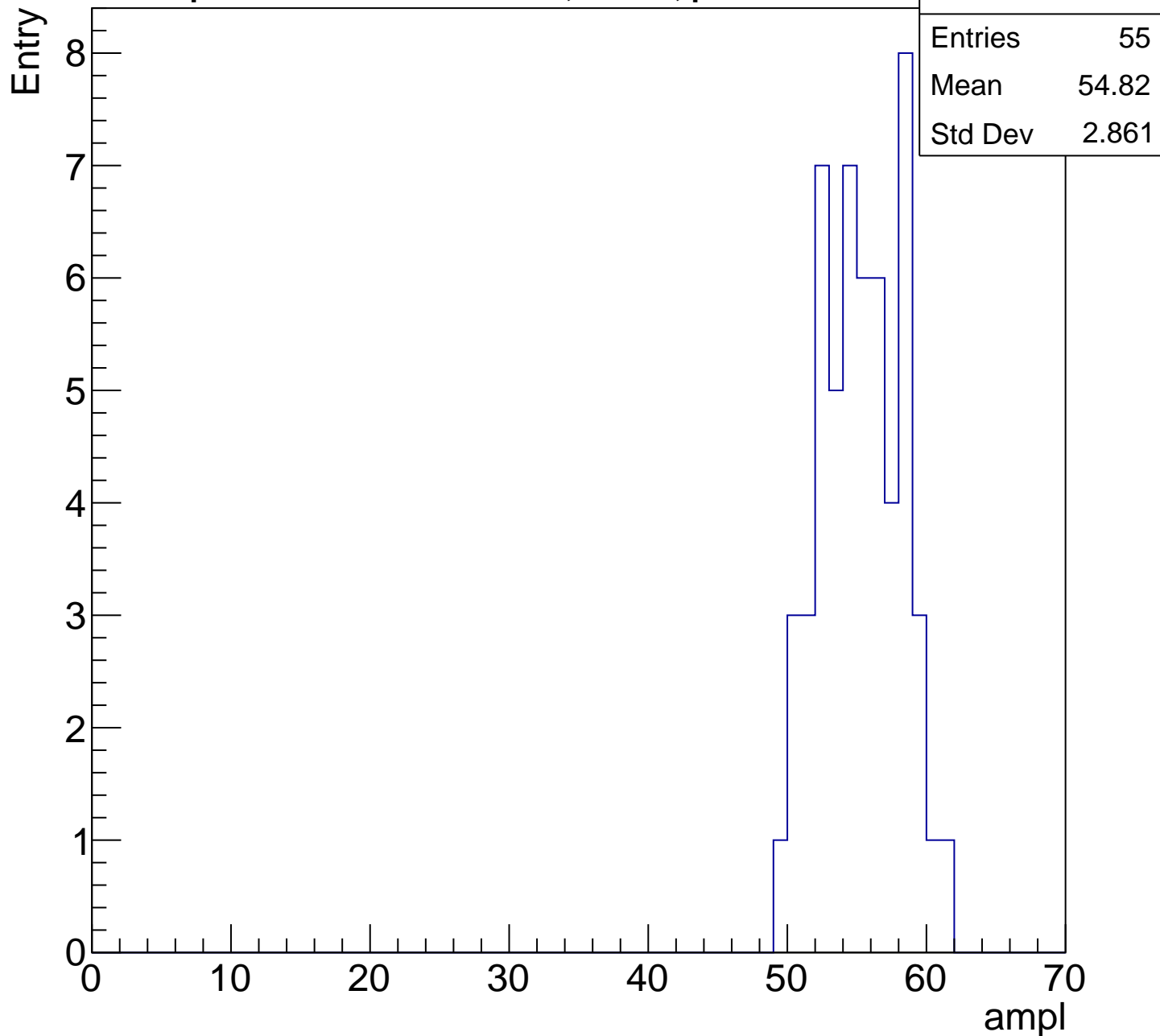
Entry

Entries	65
Mean	48.72
Std Dev	3.116



# B1L003S, U23-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

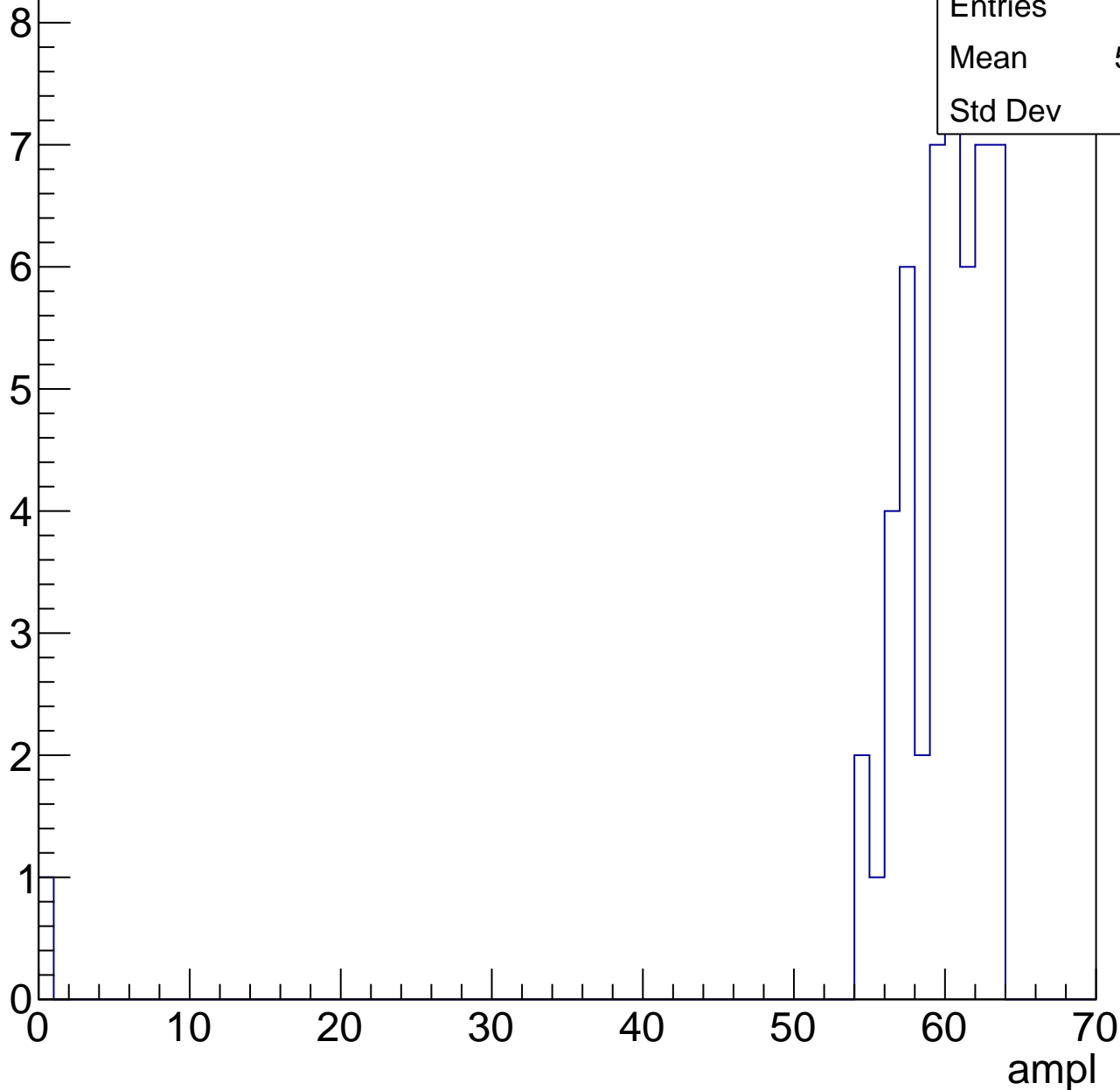


# B1L003S, U23-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.41
Std Dev	8.63



# B1L003S, U23-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

10

Mean

61.8

Std Dev

0.9798



# B1L003S, U23-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch38, adc0

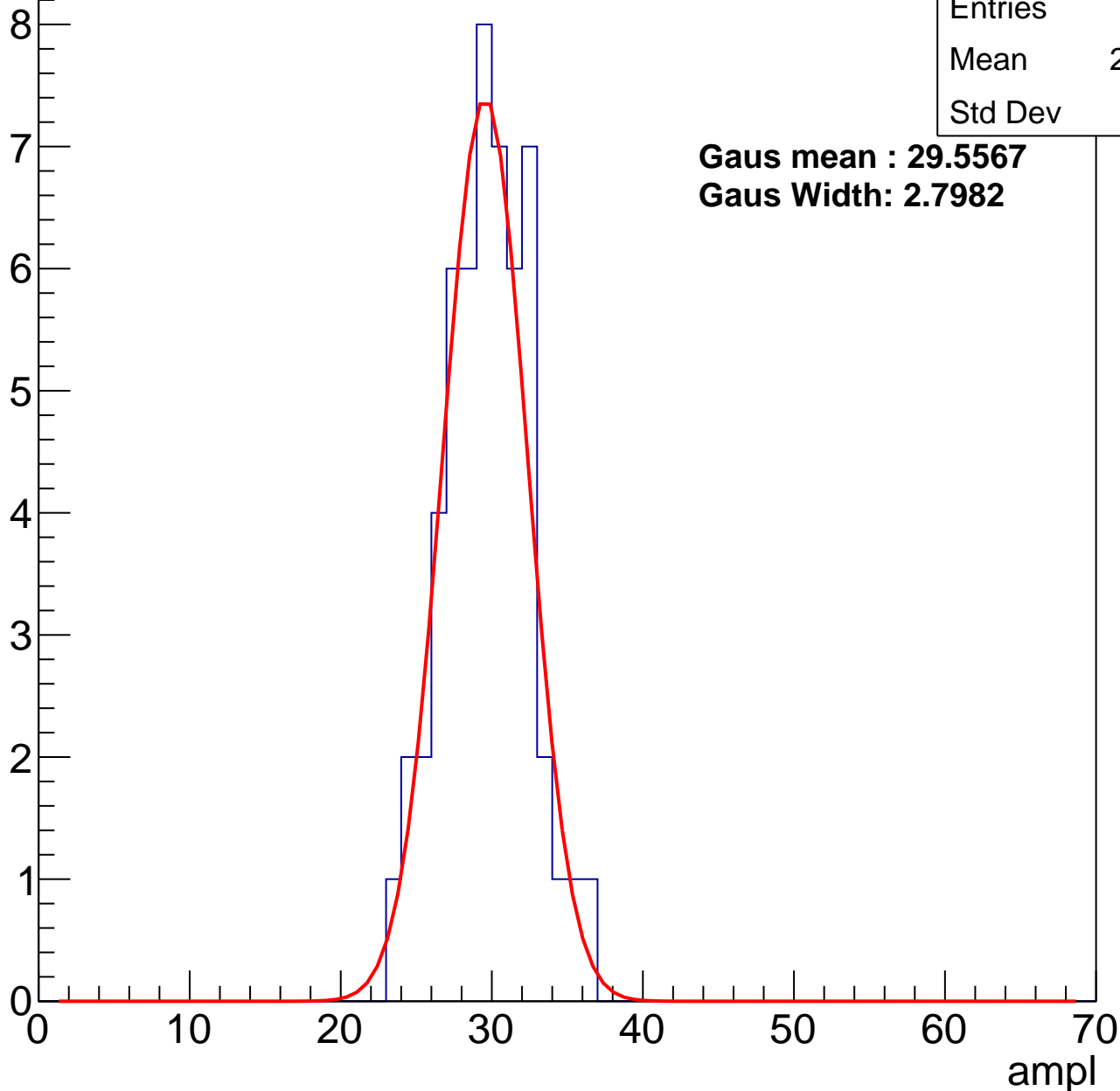
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	29.22
Std Dev	2.78

**Gaus mean : 29.5567**

**Gaus Width: 2.7982**



# B1L003S, U23-ch38, adc1

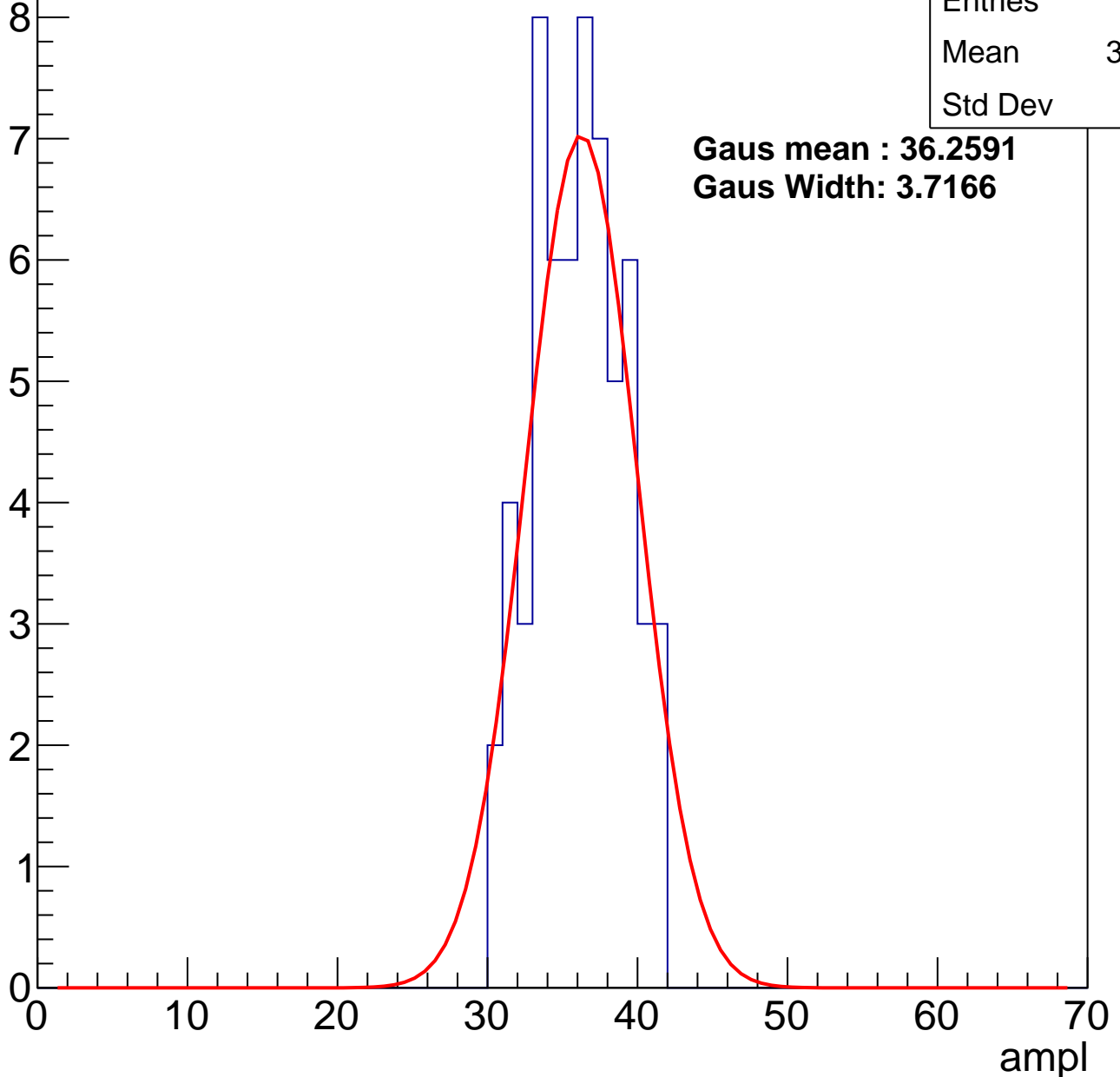
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.61
Std Dev	2.91

**Gaus mean : 36.2591**

**Gaus Width: 3.7166**



# B1L003S, U23-ch38, adc2

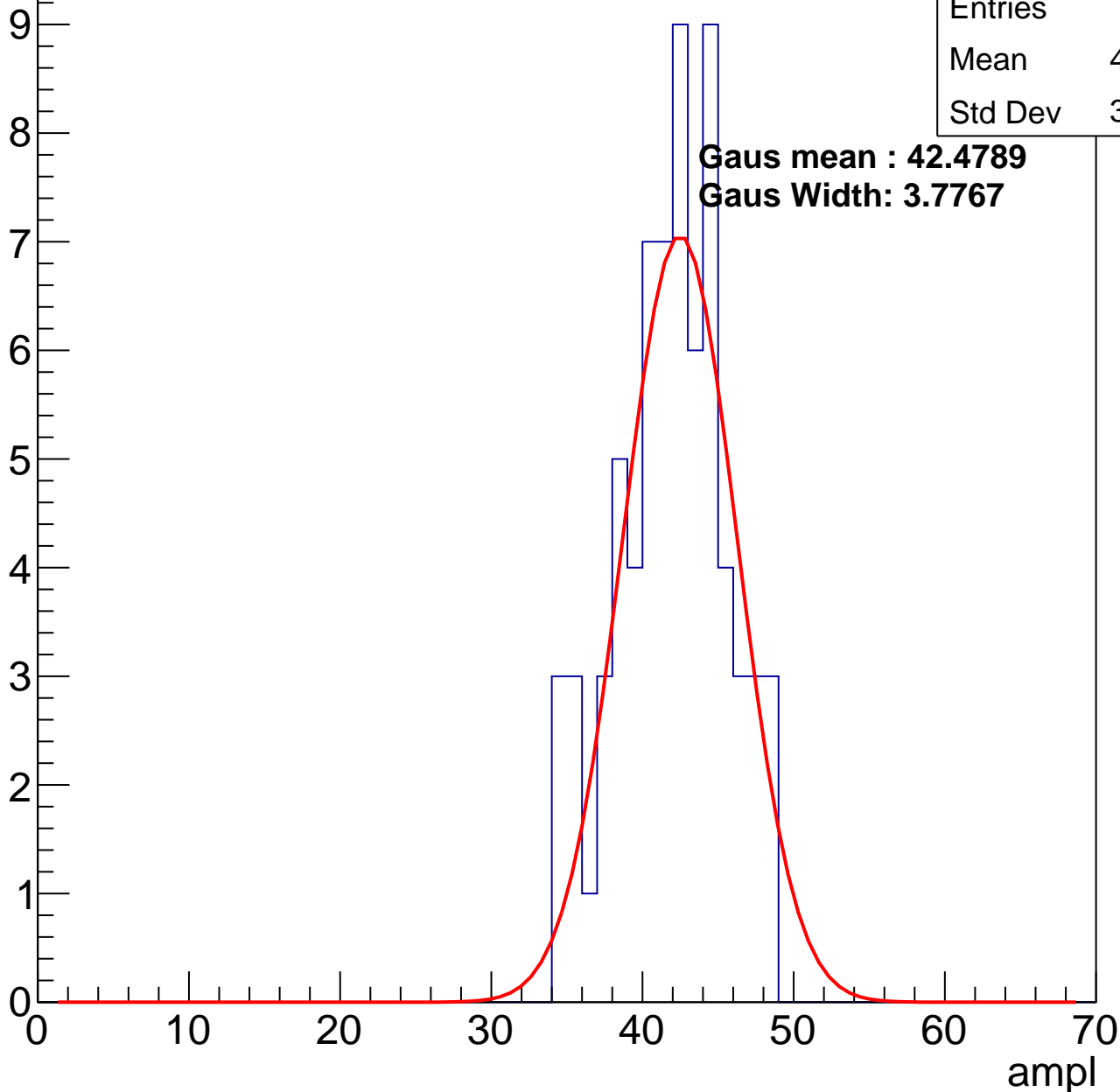
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	41.46
Std Dev	3.564

**Gaus mean : 42.4789**

**Gaus Width: 3.7767**

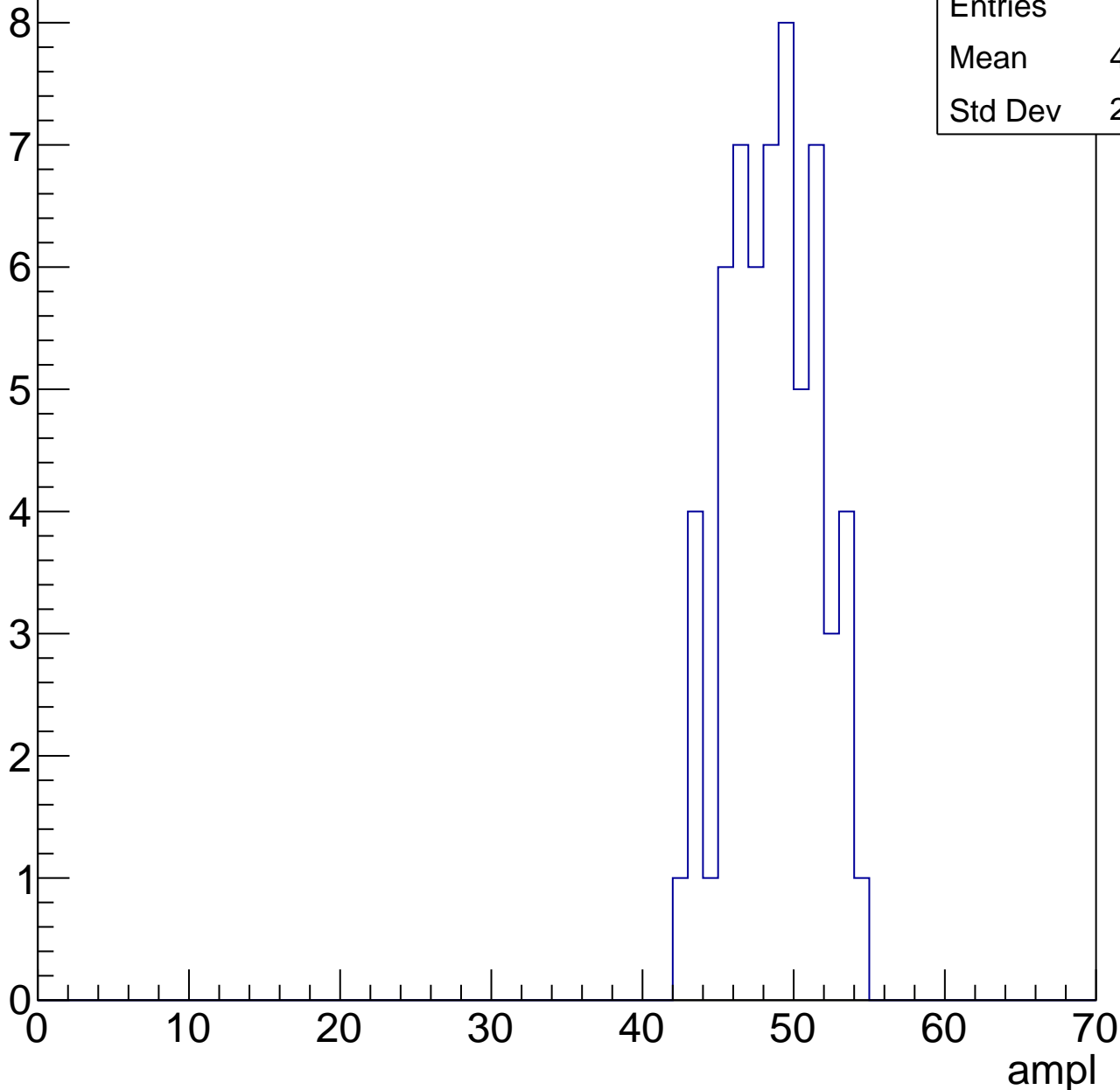


# B1L003S, U23-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.15
Std Dev	2.926

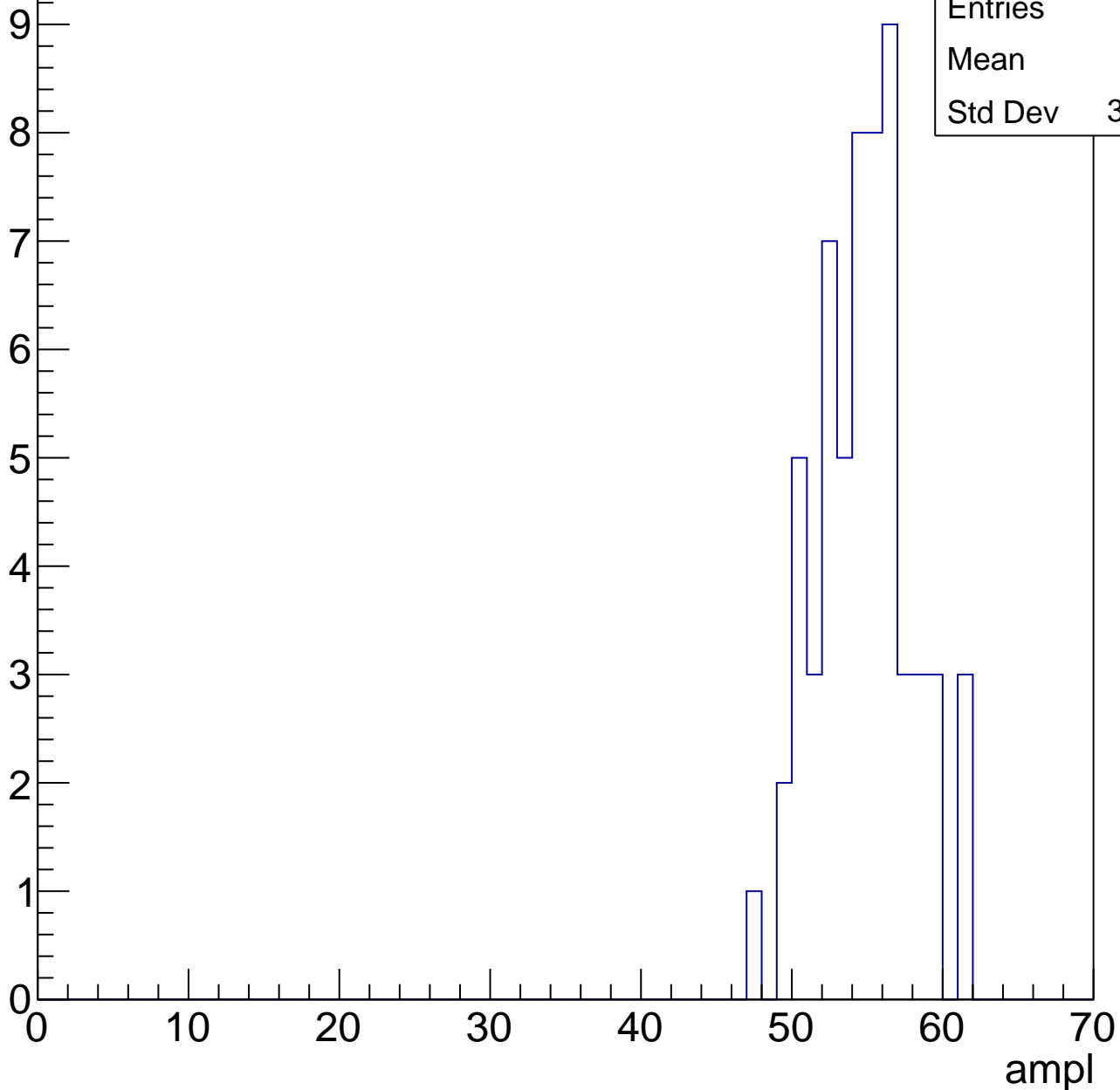


# B1L003S, U23-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.3
Std Dev	3.095

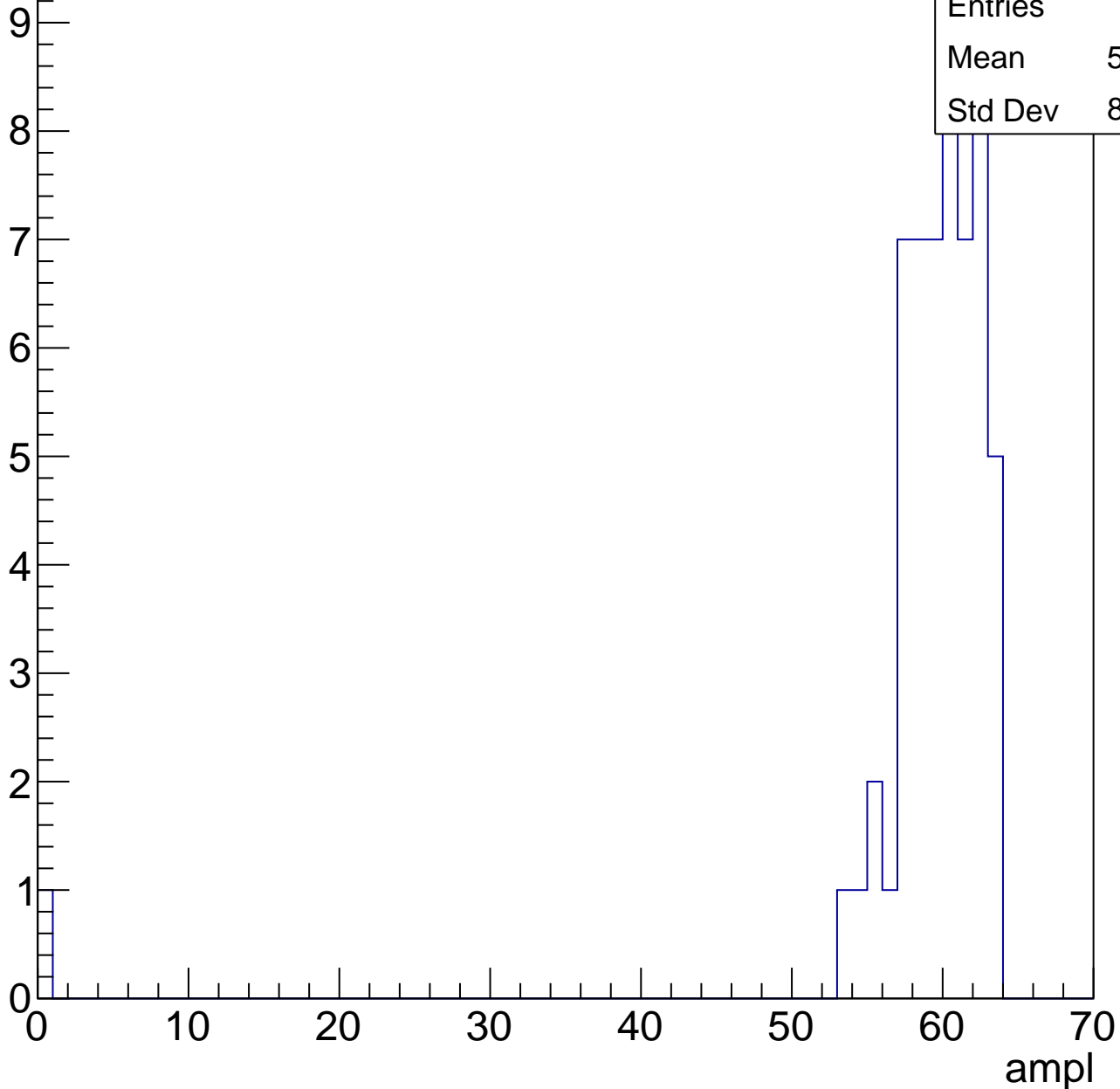


# B1L003S, U23-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	58.44
Std Dev	8.163

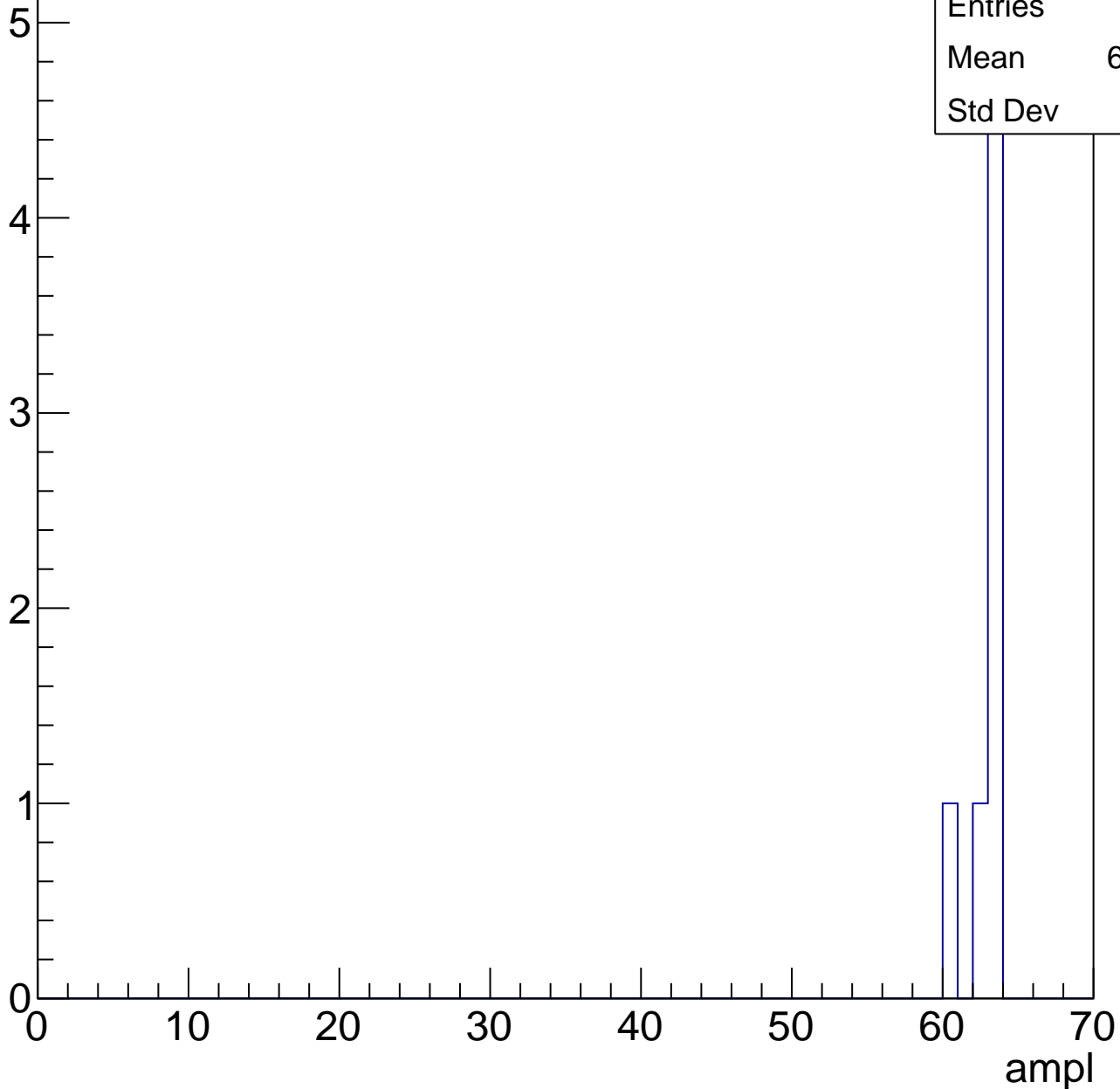


# B1L003S, U23-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	7
Mean	62.43
Std Dev	1.05





# B1L003S, U23-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L003S, U23-ch39, adc0

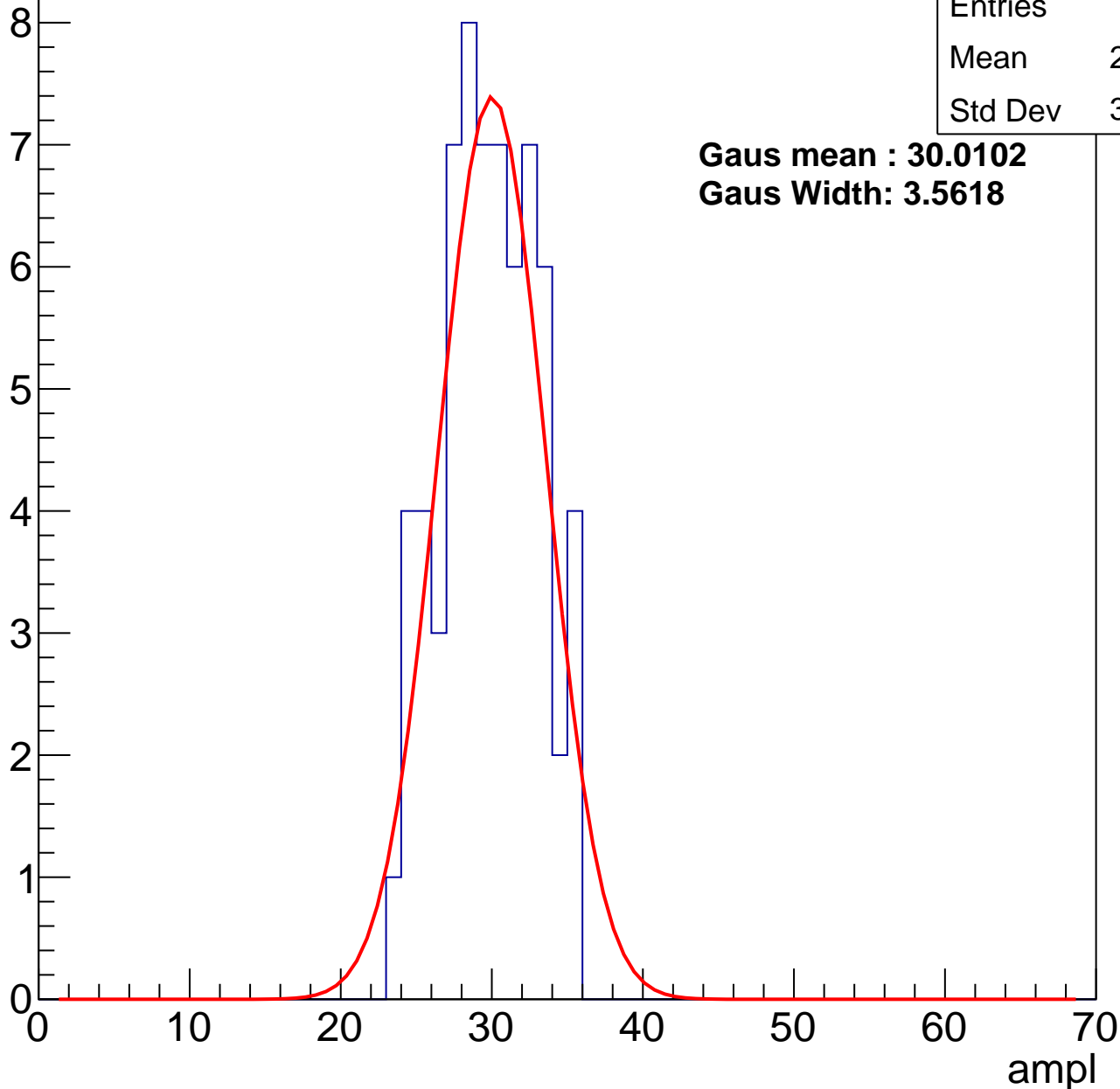
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.38
Std Dev	3.108

**Gaus mean : 30.0102**

**Gaus Width: 3.5618**



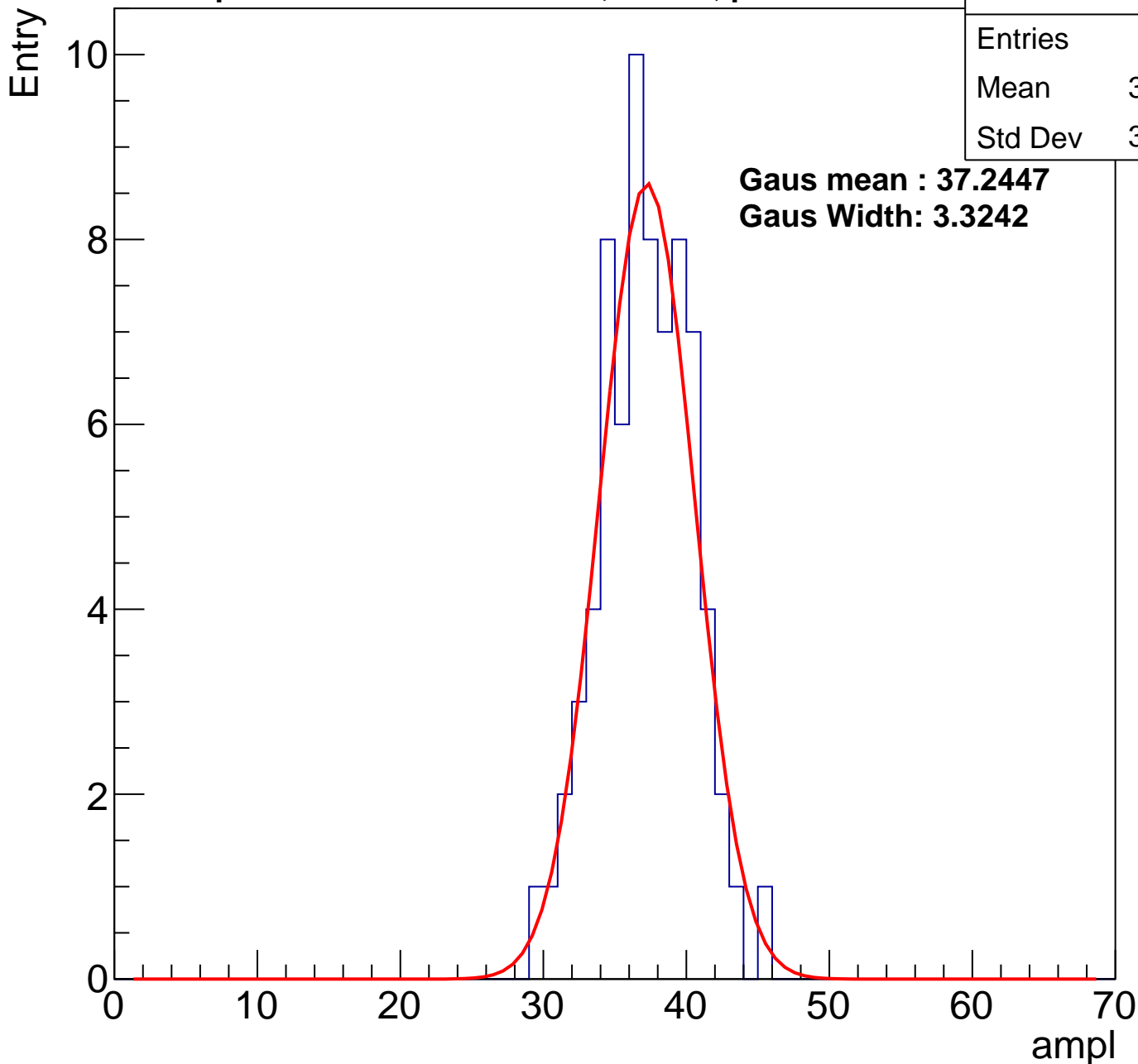
# B1L003S, U23-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	36.73
Std Dev	3.185

**Gaus mean : 37.2447**

**Gaus Width: 3.3242**



# B1L003S, U23-ch39, adc2

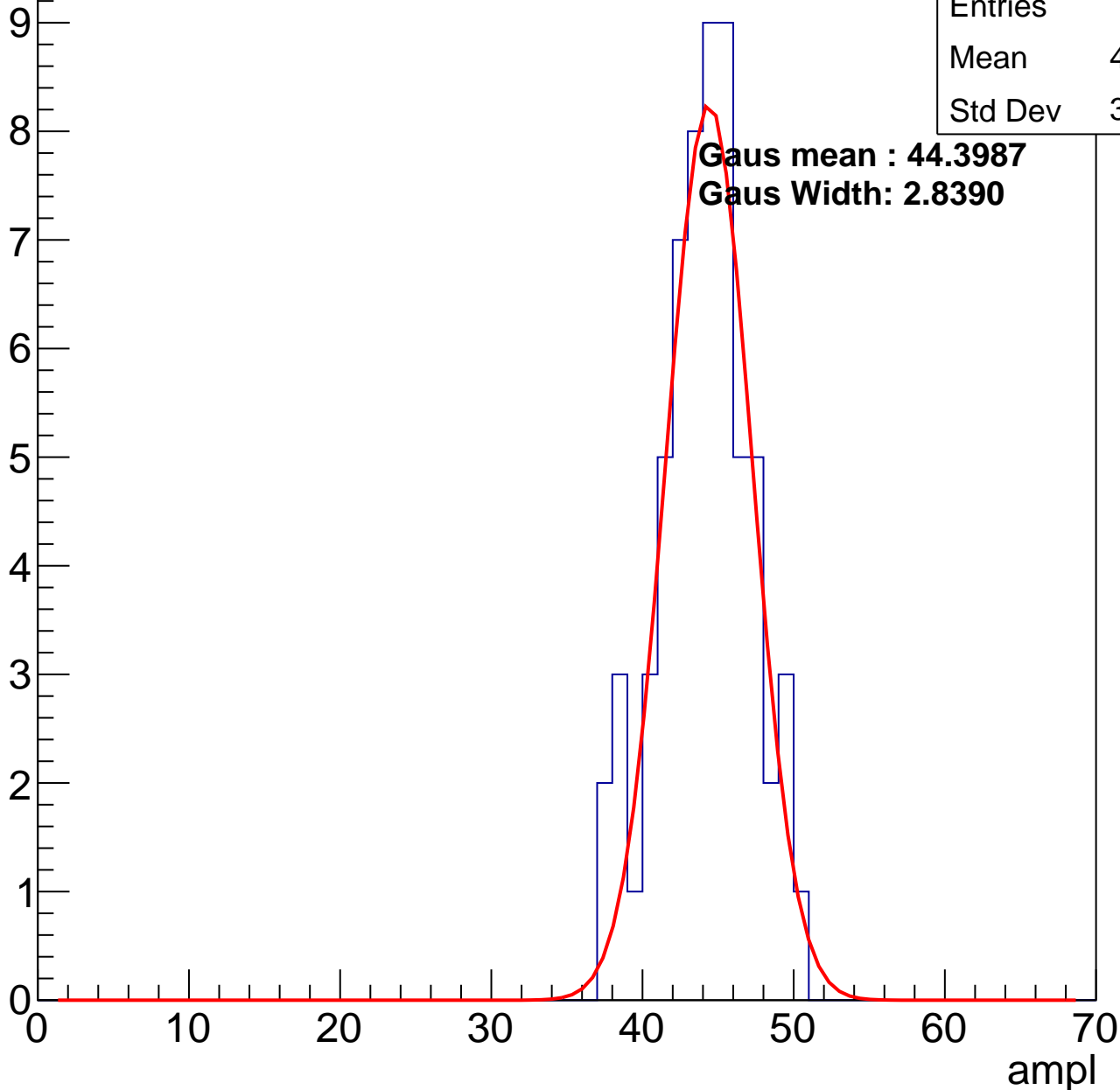
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	43.63
Std Dev	3.004

**Gaus mean : 44.3987**

**Gaus Width: 2.8390**

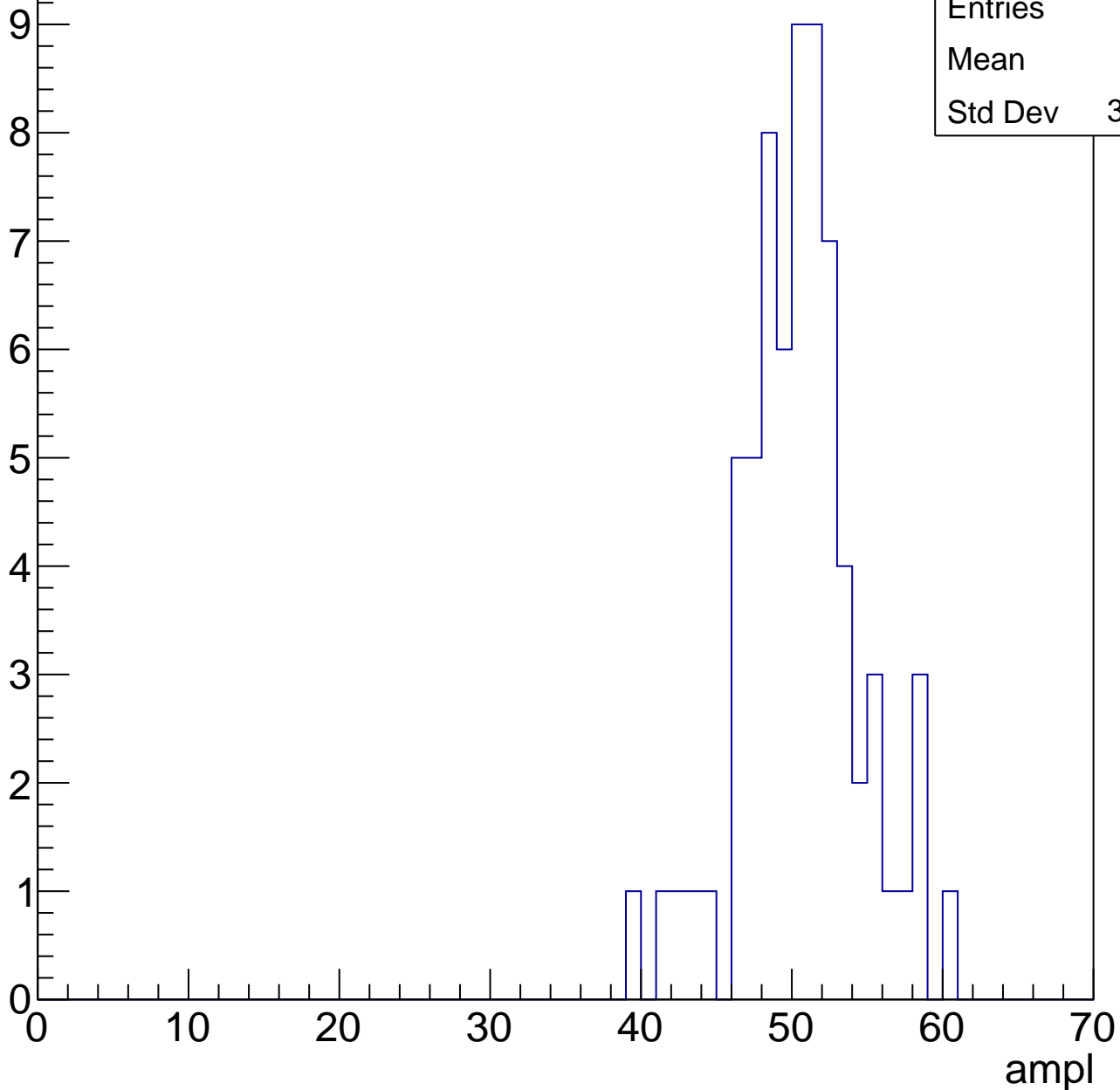


# B1L003S, U23-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	50.1
Std Dev	3.938

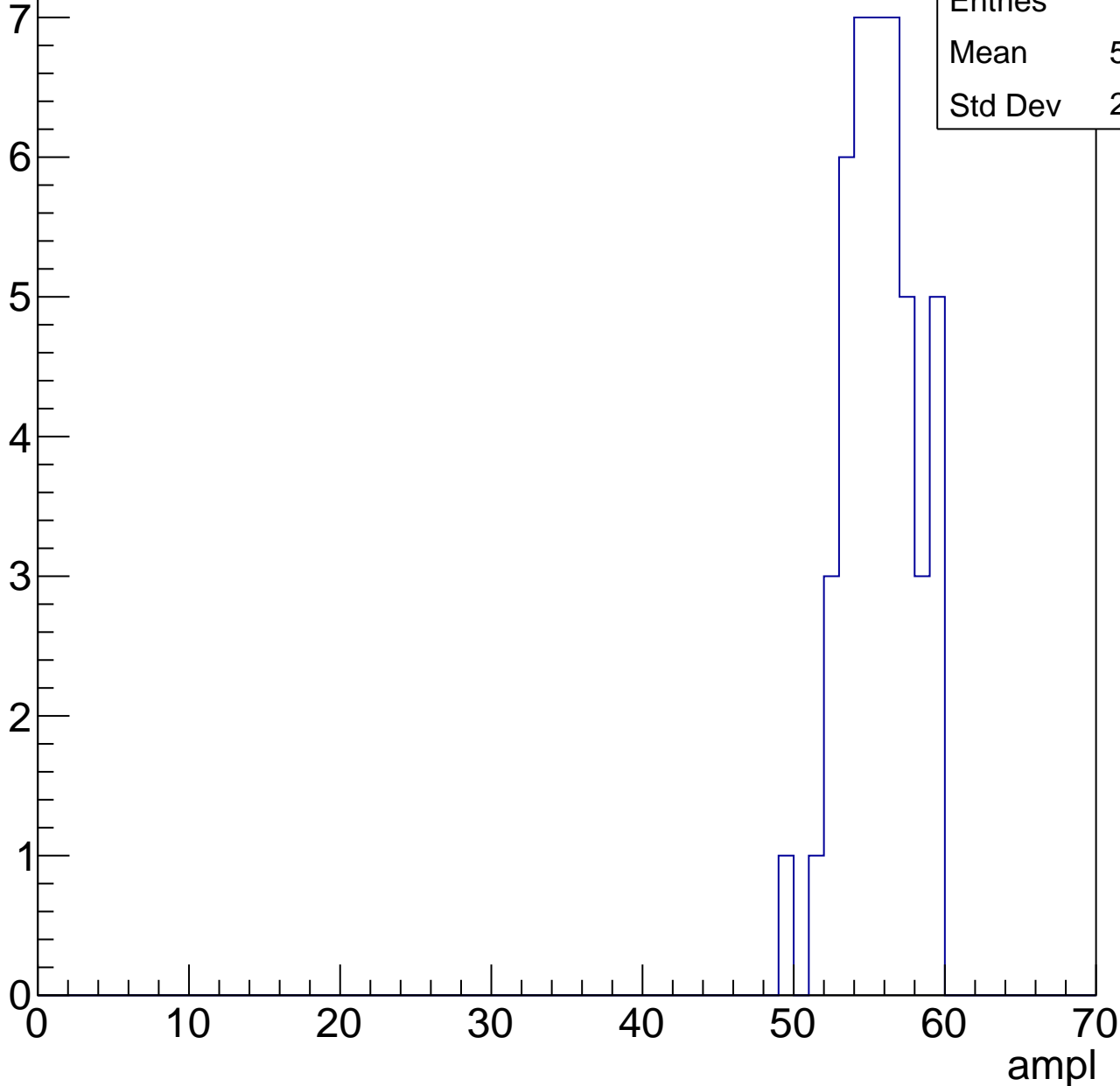


# B1L003S, U23-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	55.18
Std Dev	2.322



# B1L003S, U23-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	58.39
Std Dev	9.492

ampl

0

10

20

30

40

50

60

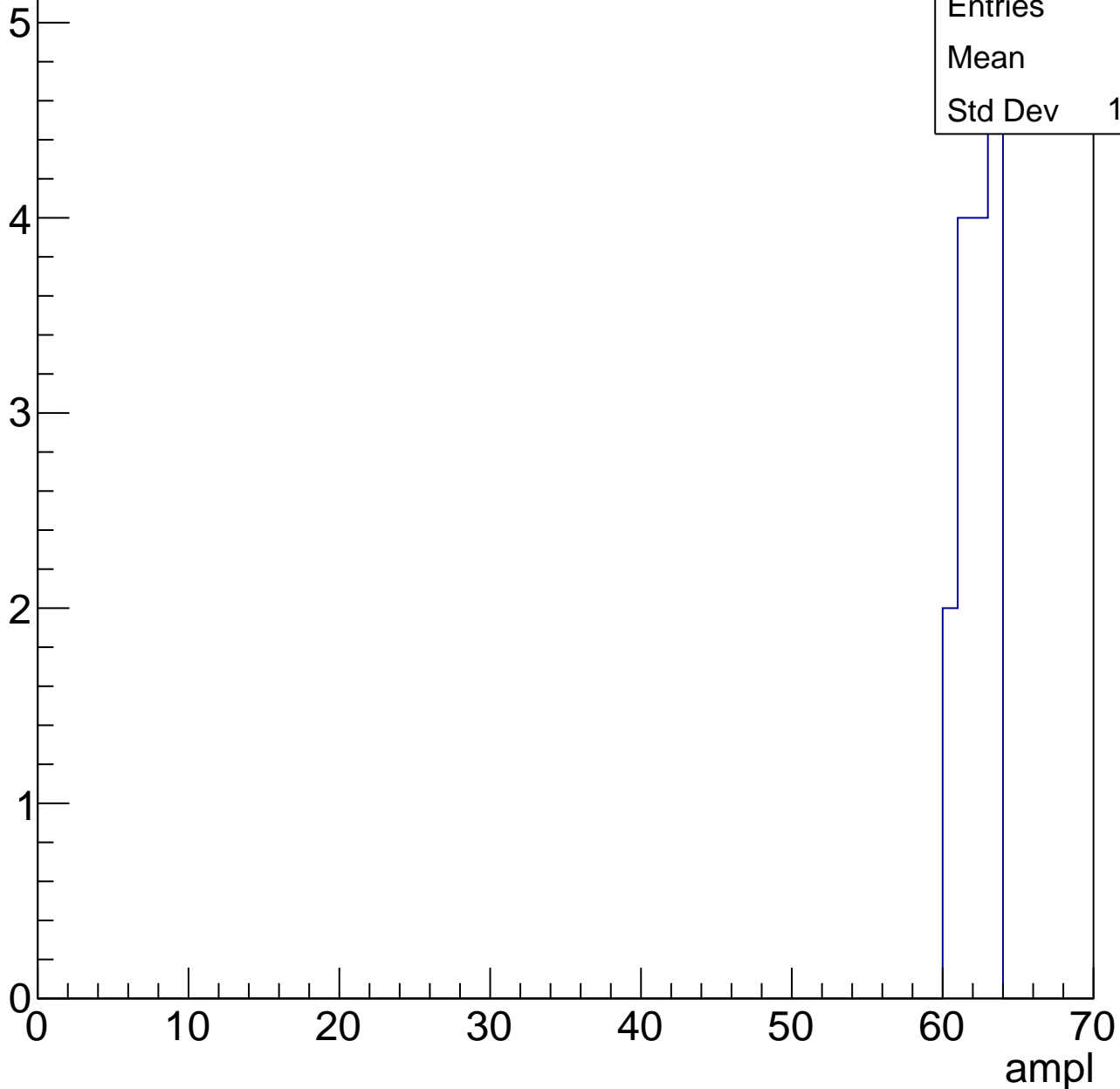
70

# B1L003S, U23-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.8
Std Dev	1.046





# B1L003S, U23-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch40, adc0

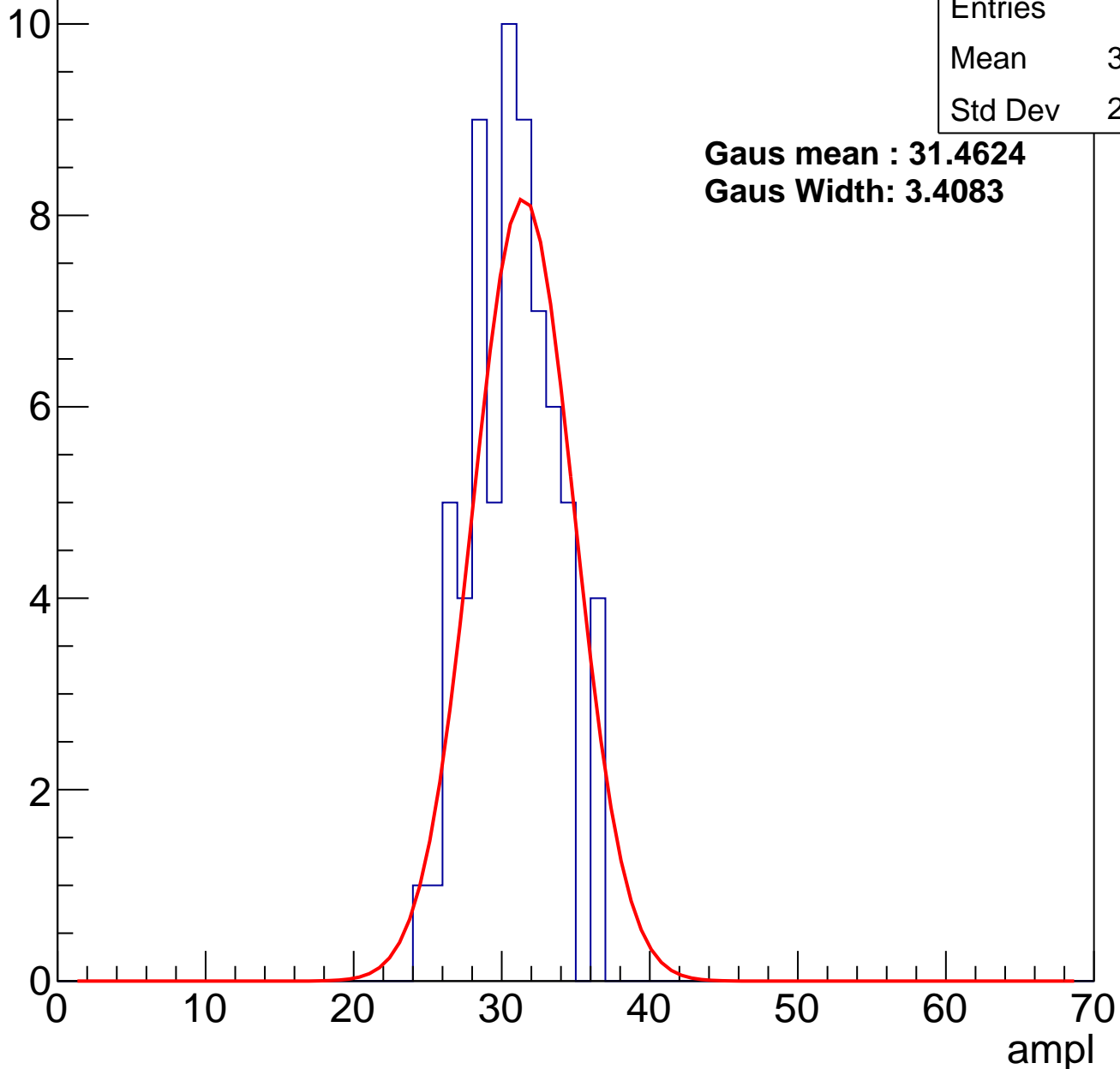
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	66
Mean	30.29
Std Dev	2.827

**Gaus mean : 31.4624**

**Gaus Width: 3.4083**

Entry



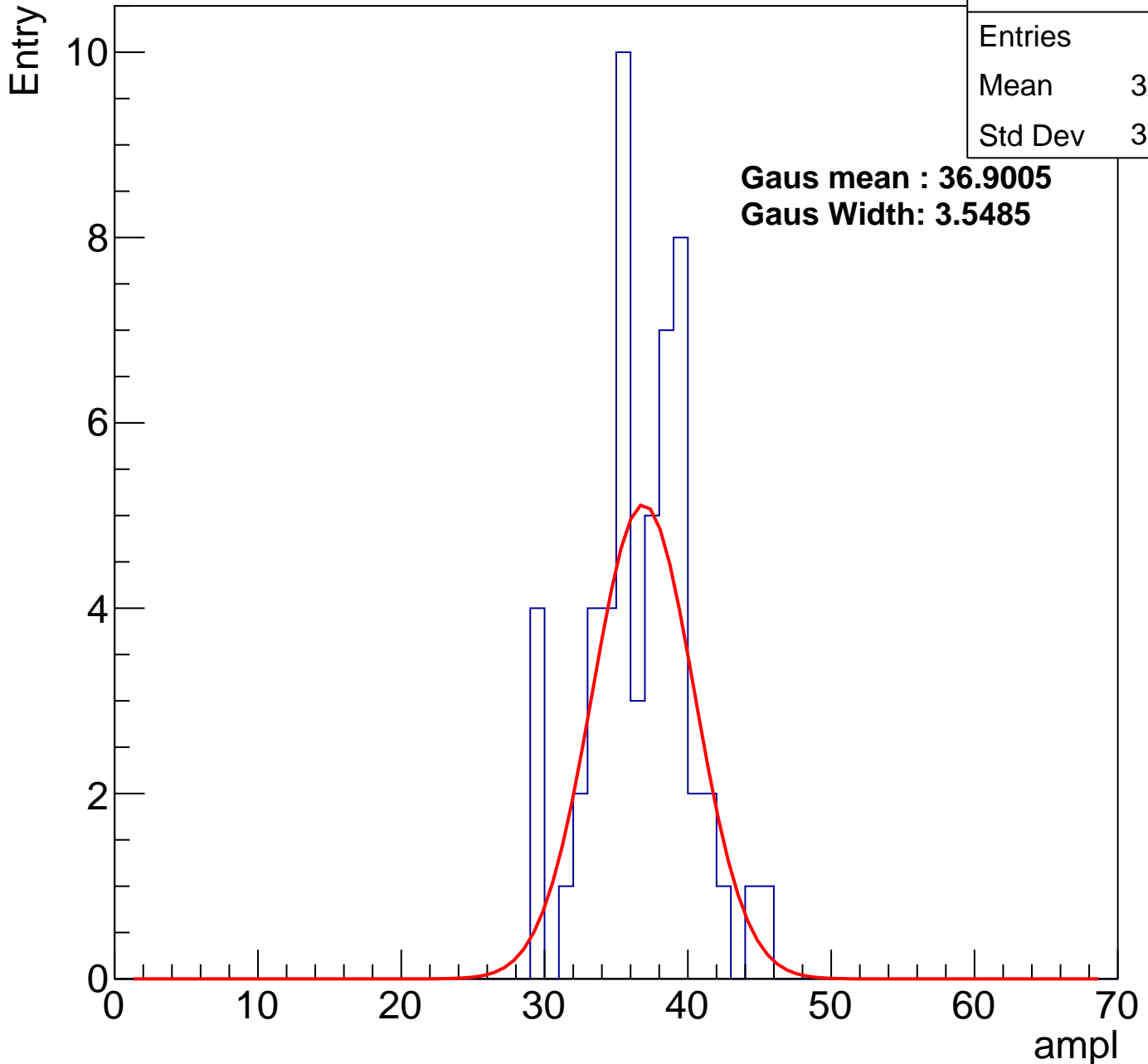
# B1L003S, U23-ch40, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	55
Mean	36.24
Std Dev	3.516

**Gaus mean : 36.9005**

**Gaus Width: 3.5485**



# B1L003S, U23-ch40, adc2

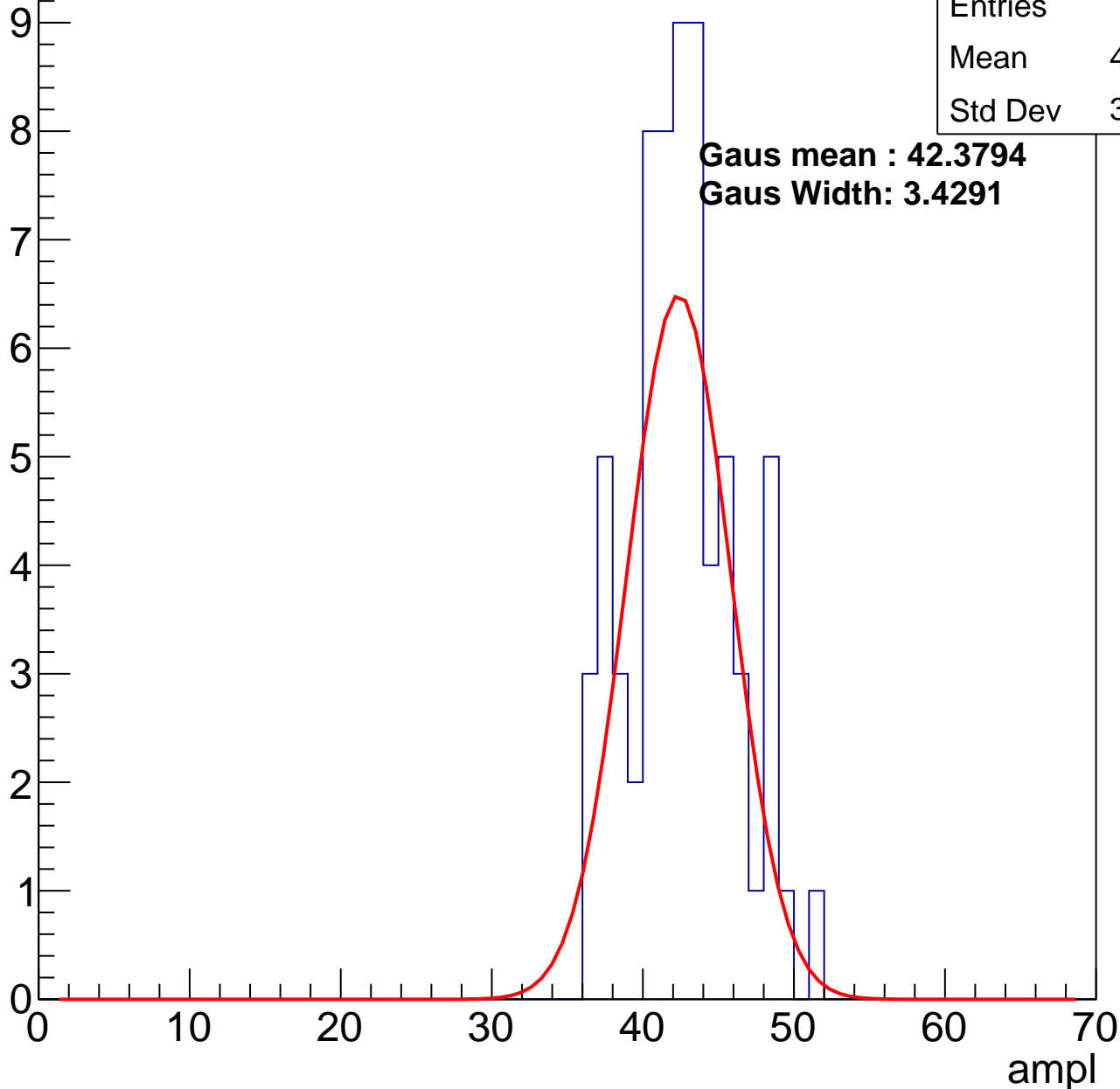
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.15
Std Dev	3.435

**Gaus mean : 42.3794**

**Gaus Width: 3.4291**

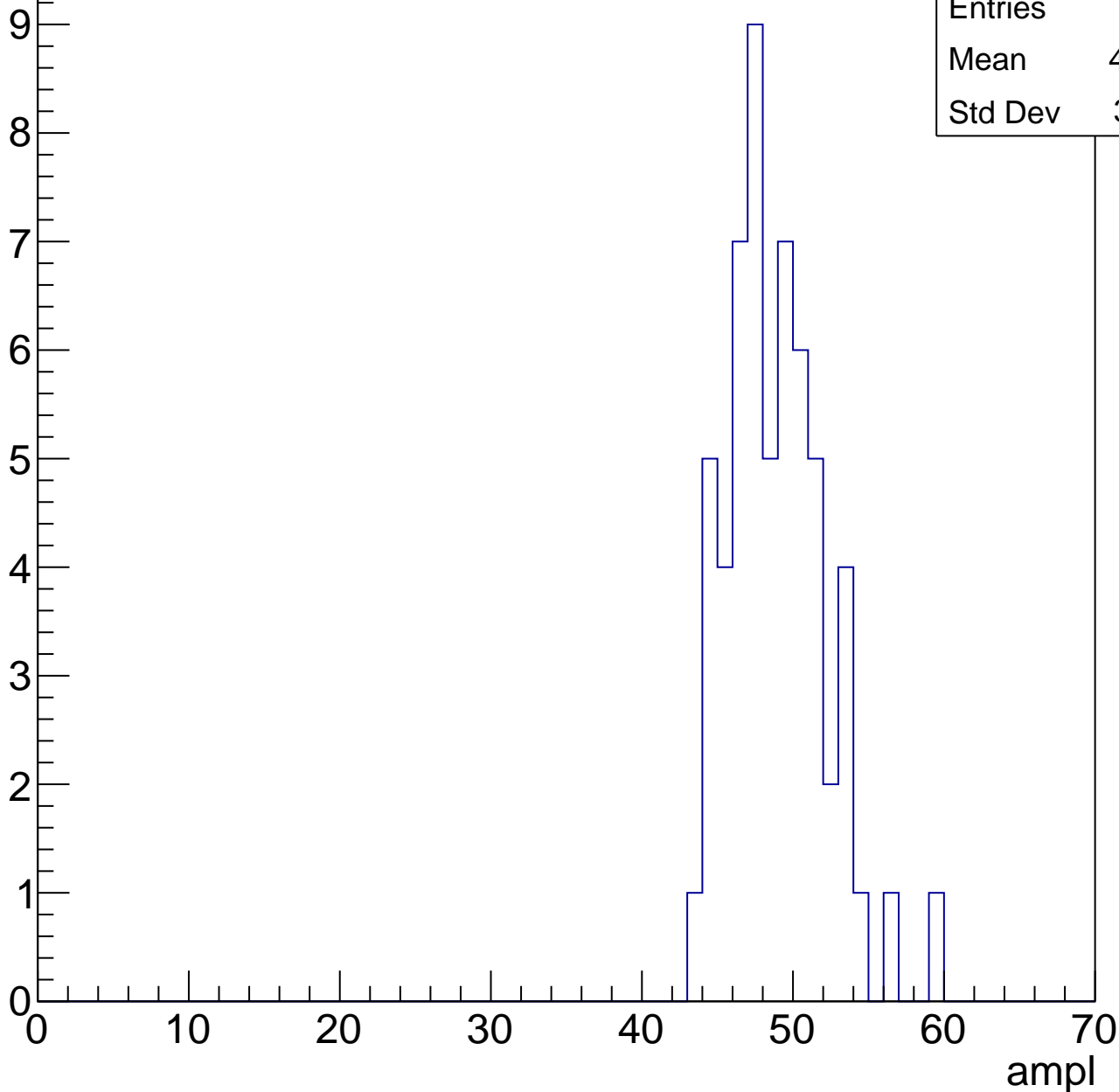


# B1L003S, U23-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	48.47
Std Dev	3.201

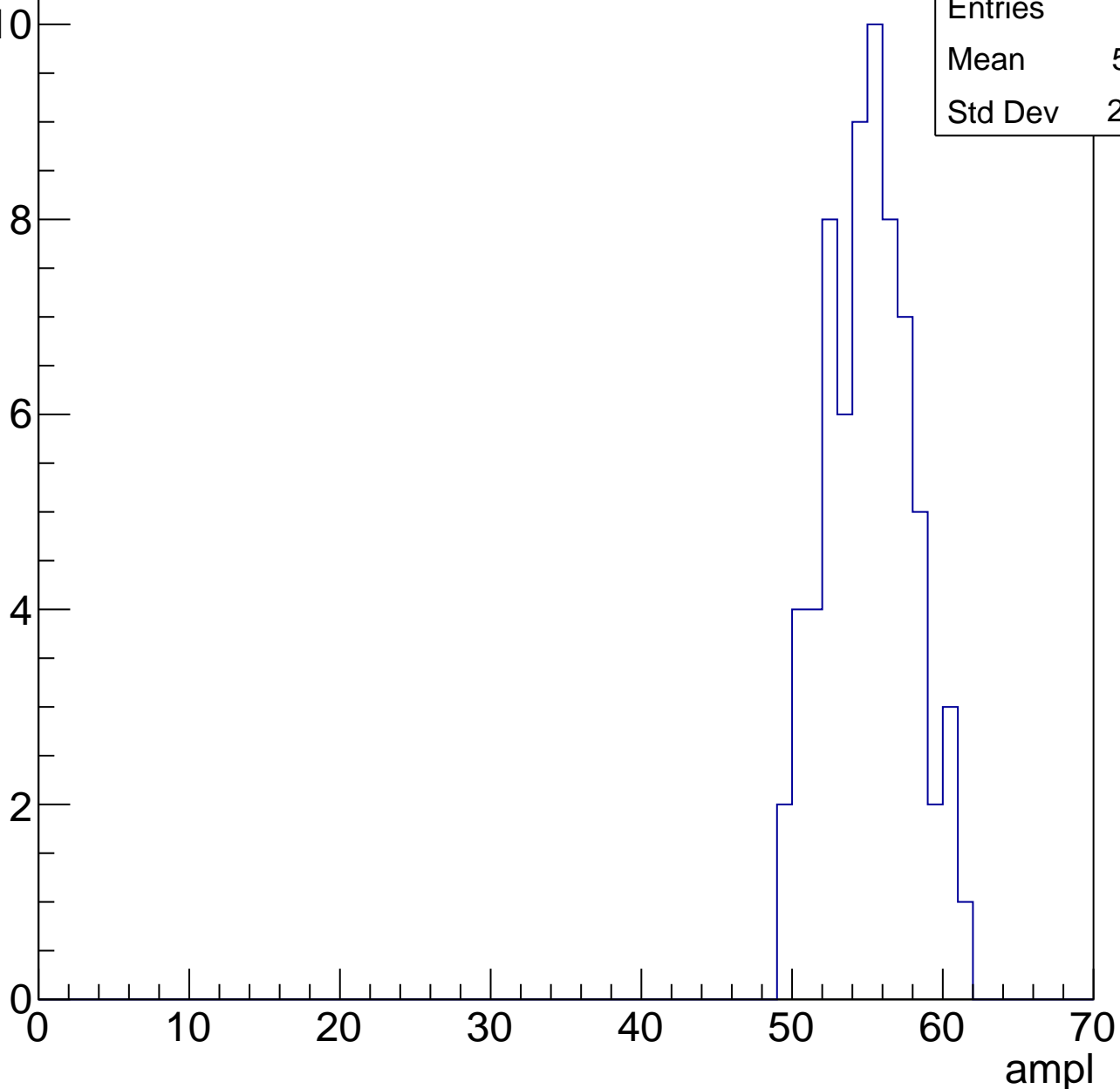


# B1L003S, U23-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

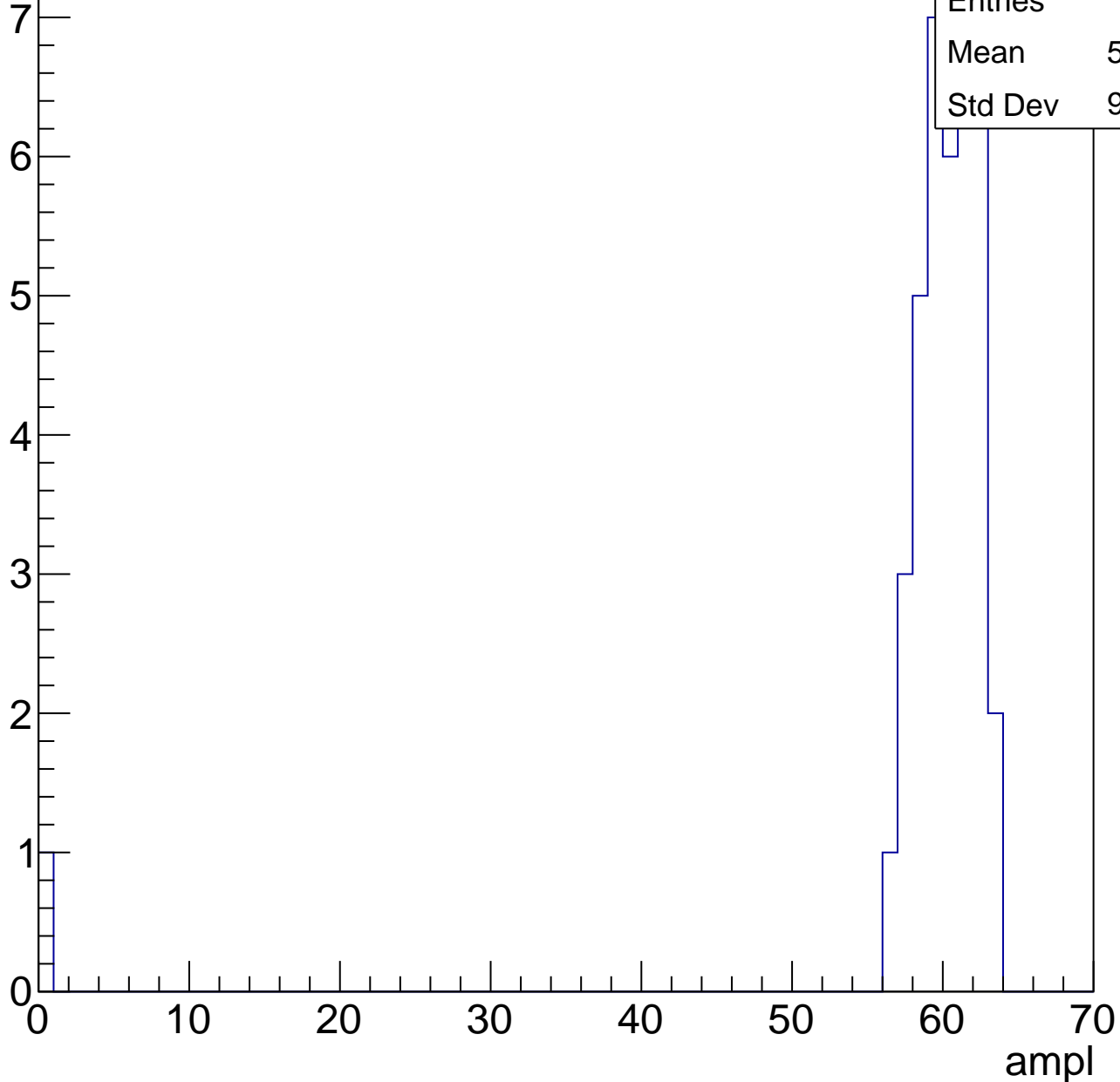
Entries	69
Mean	54.61
Std Dev	2.835



# B1L003S, U23-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



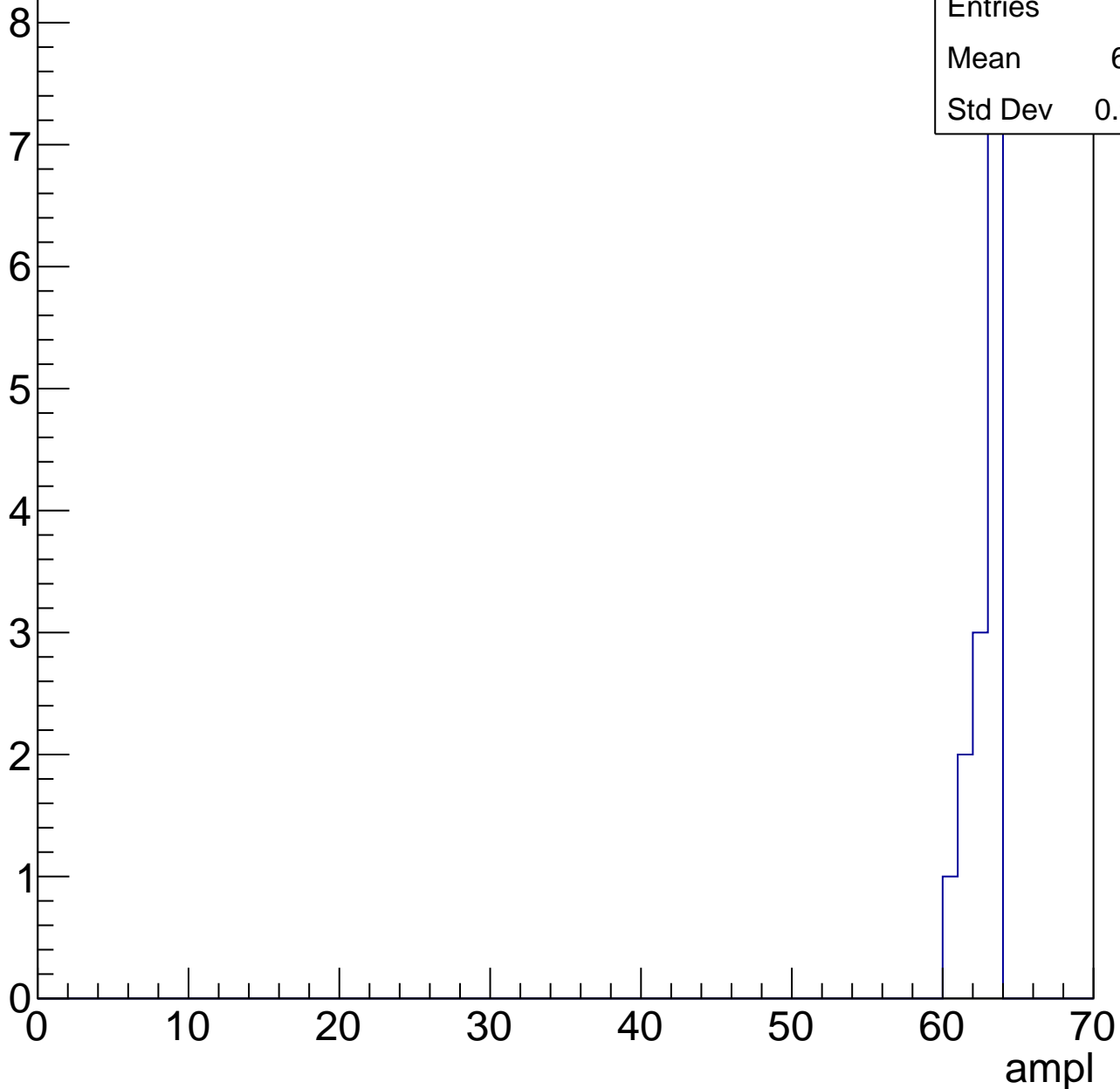
Entries	39
Mean	58.38
Std Dev	9.636

# B1L003S, U23-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	14
Mean	62.29
Std Dev	0.9583





# B1L003S, U23-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L003S, U23-ch41, adc0

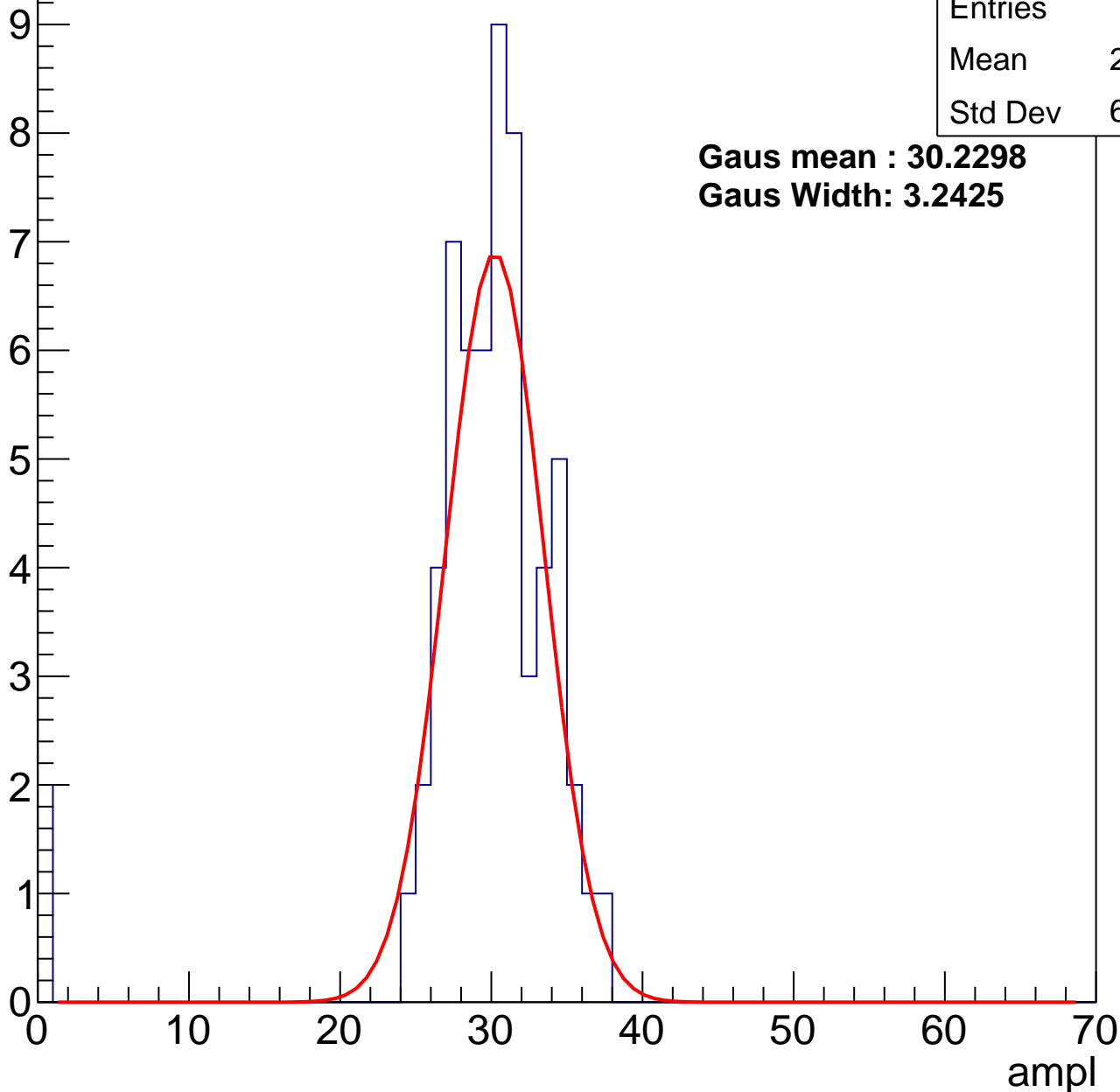
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.98
Std Dev	6.075

**Gaus mean : 30.2298**

**Gaus Width: 3.2425**

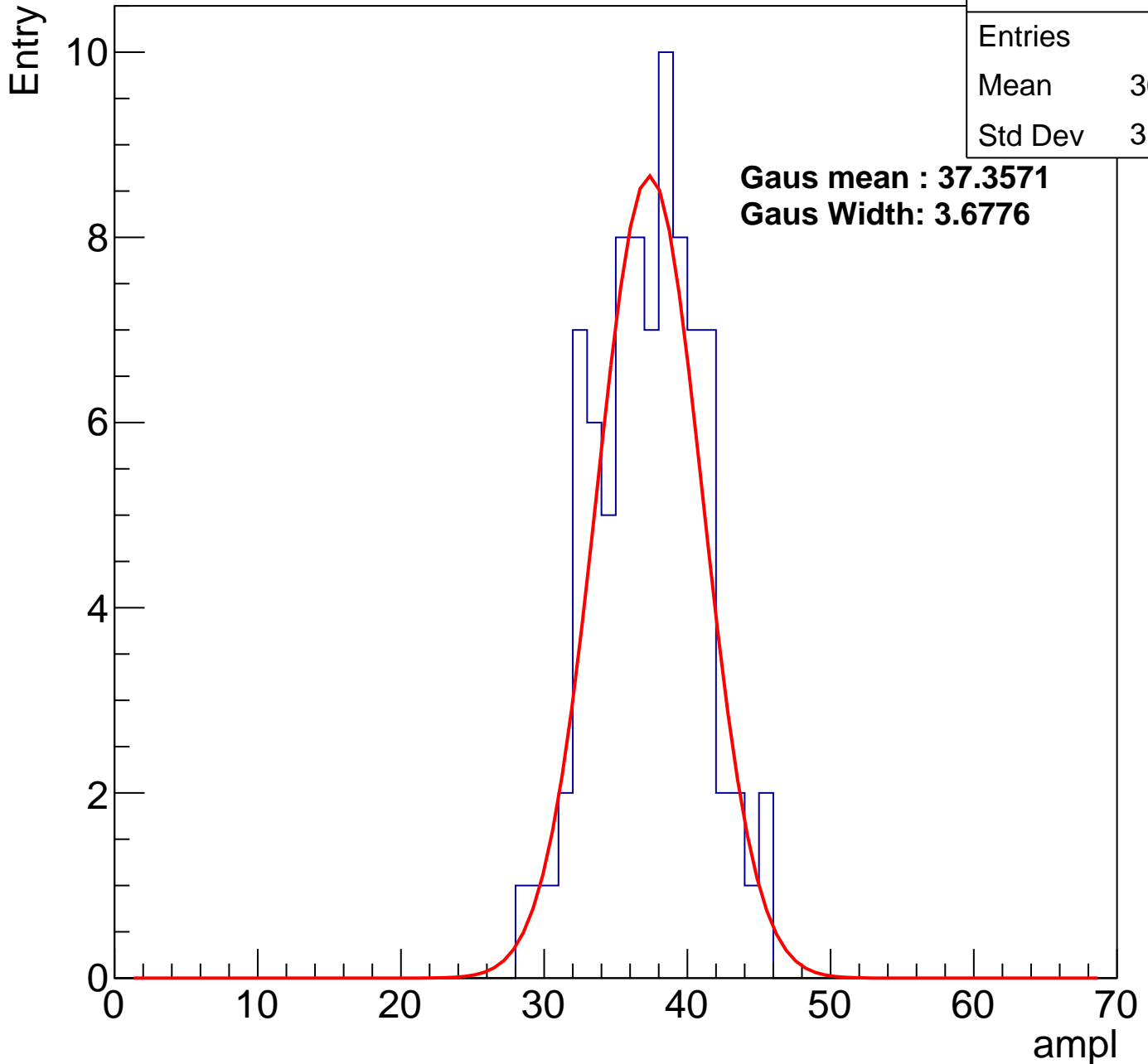


# B1L003S, U23-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	85
Mean	36.84
Std Dev	3.655

**Gaus mean : 37.3571**  
**Gaus Width: 3.6776**

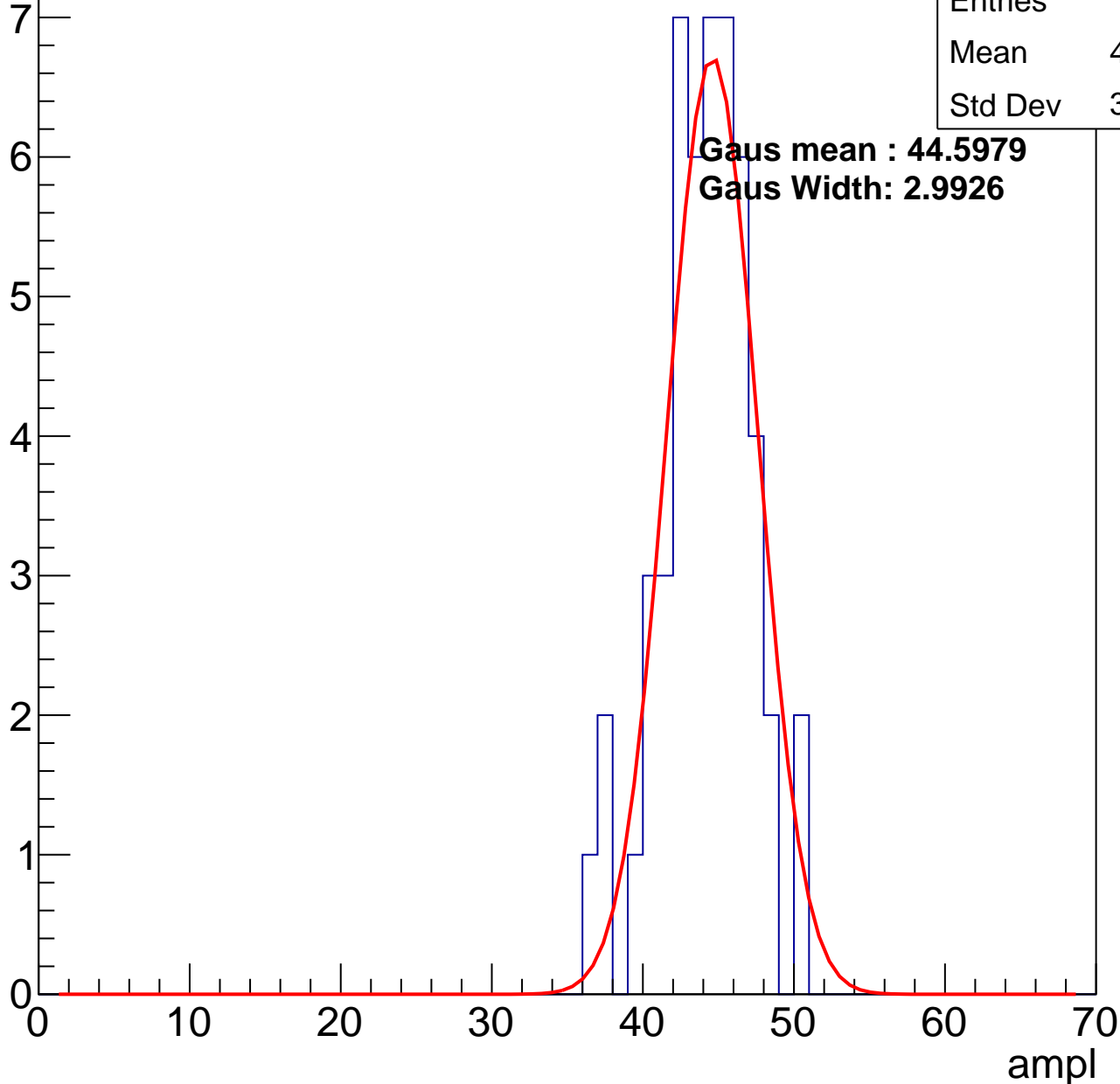


# B1L003S, U23-ch41, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	43.67
Std Dev	3.008

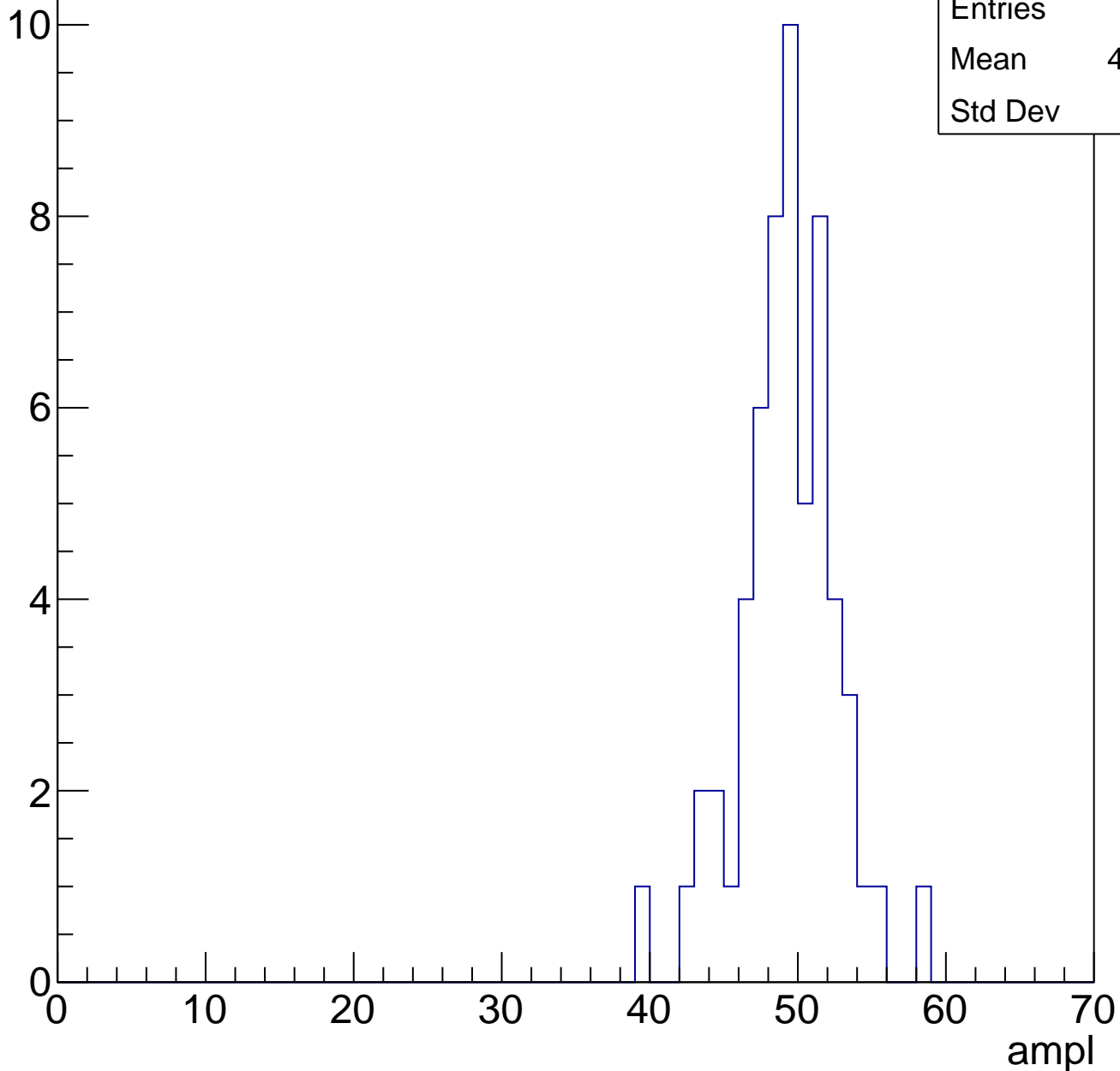


# B1L003S, U23-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	58
Mean	48.83
Std Dev	3.26

Entry

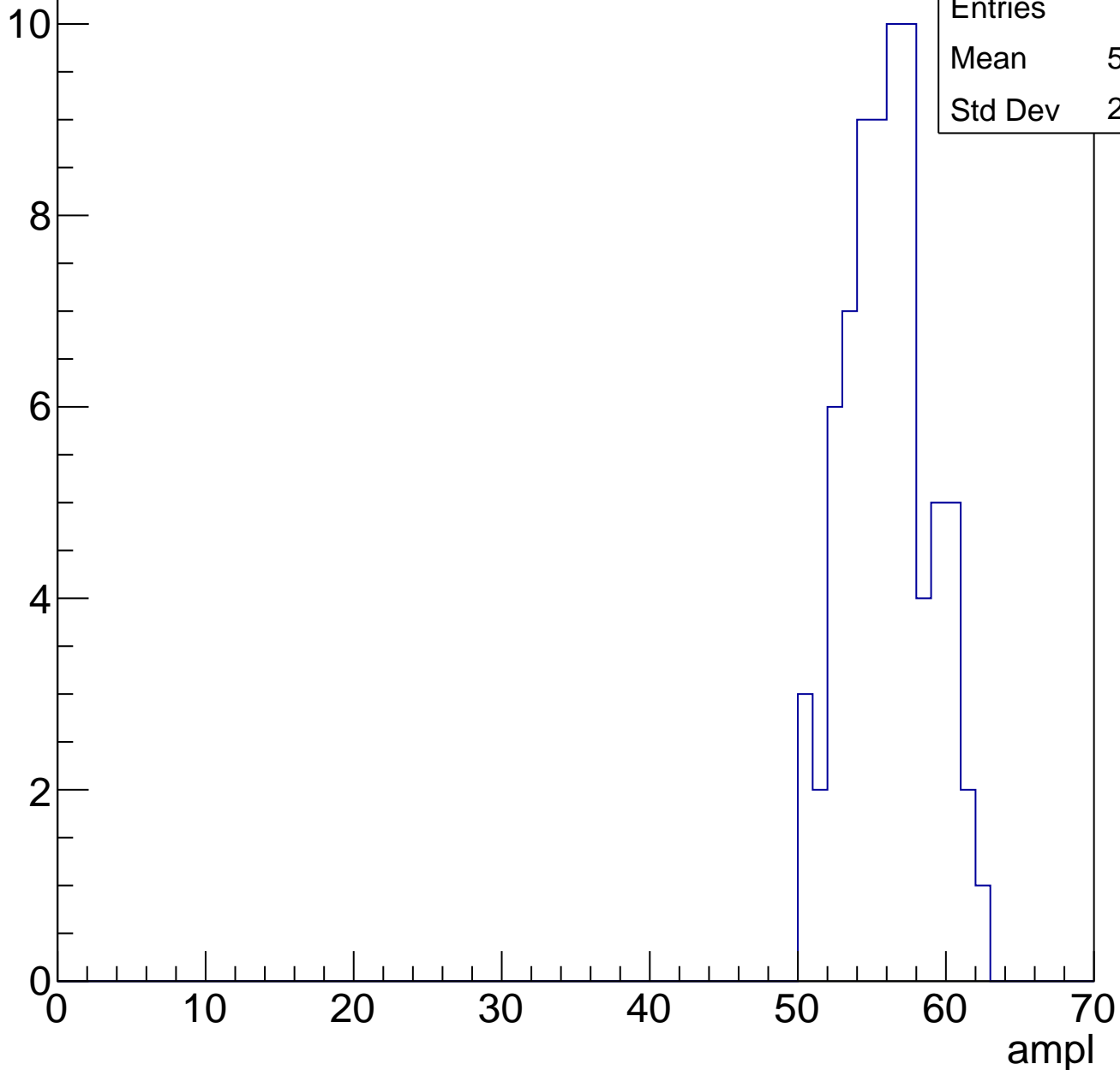


# B1L003S, U23-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	55.58
Std Dev	2.833

Entry

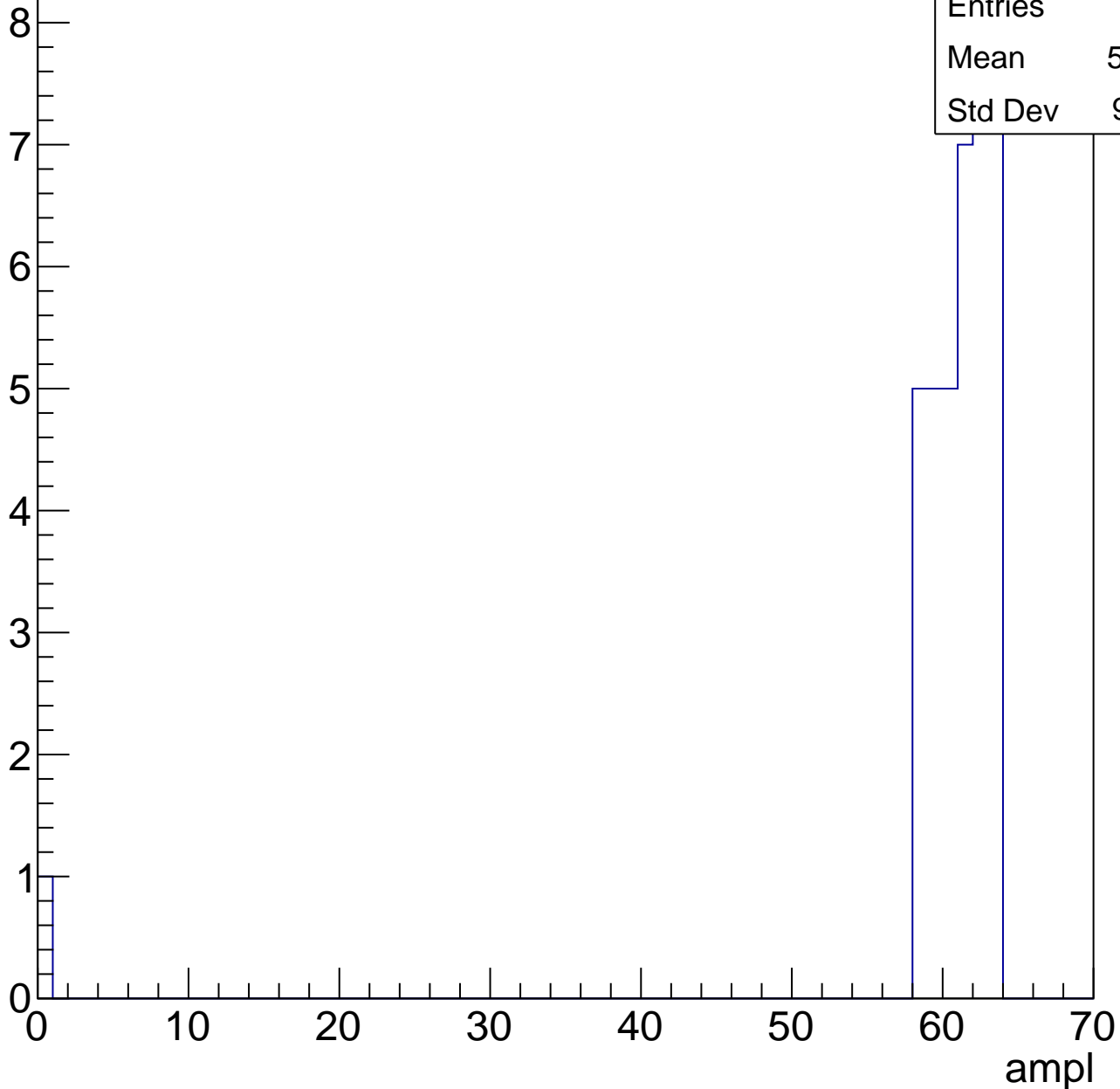


# B1L003S, U23-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	59.28
Std Dev	9.761



# B1L003S, U23-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch42, adc0

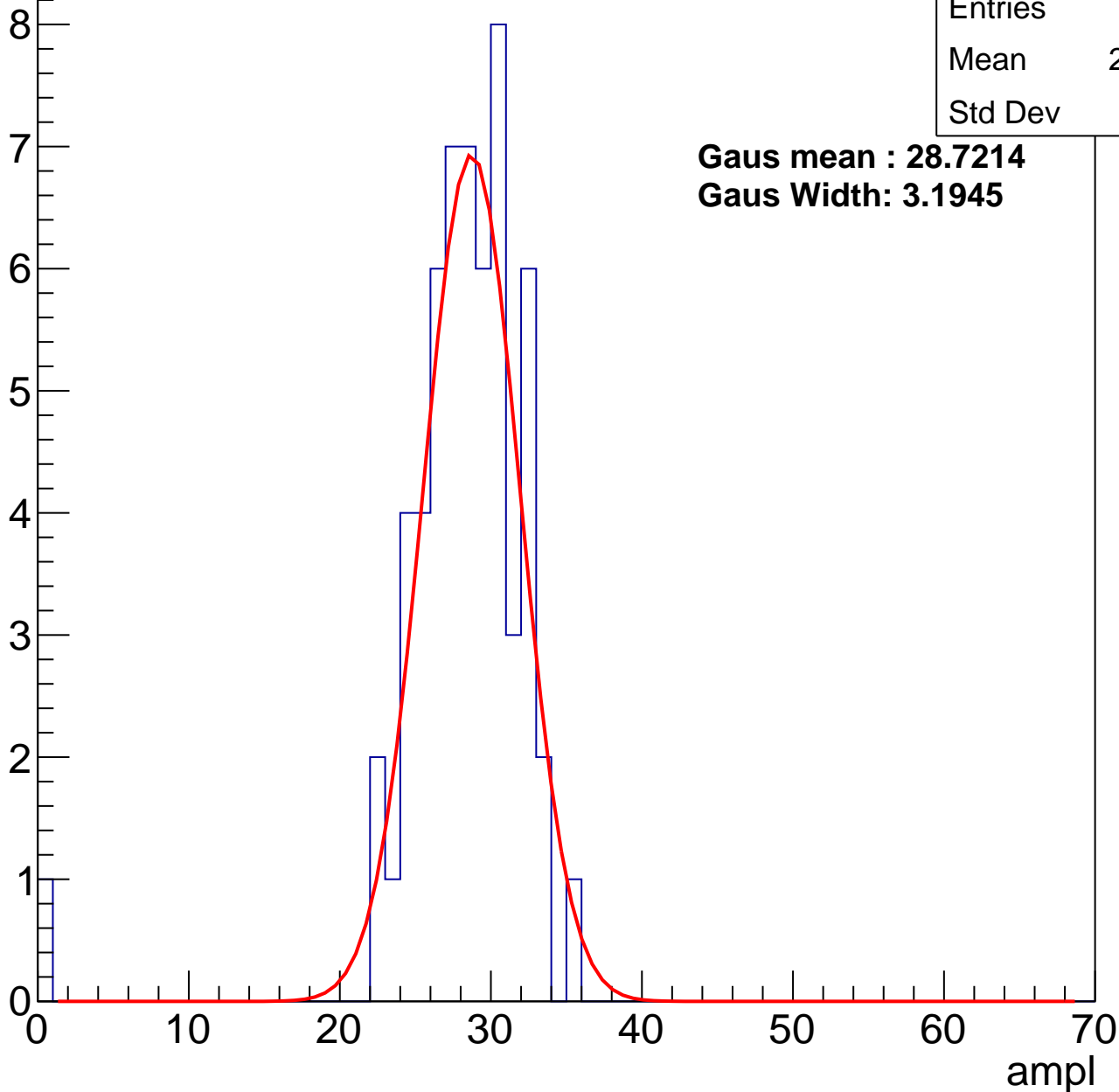
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	27.66
Std Dev	4.67

**Gaus mean : 28.7214**

**Gaus Width: 3.1945**



# B1L003S, U23-ch42, adc1

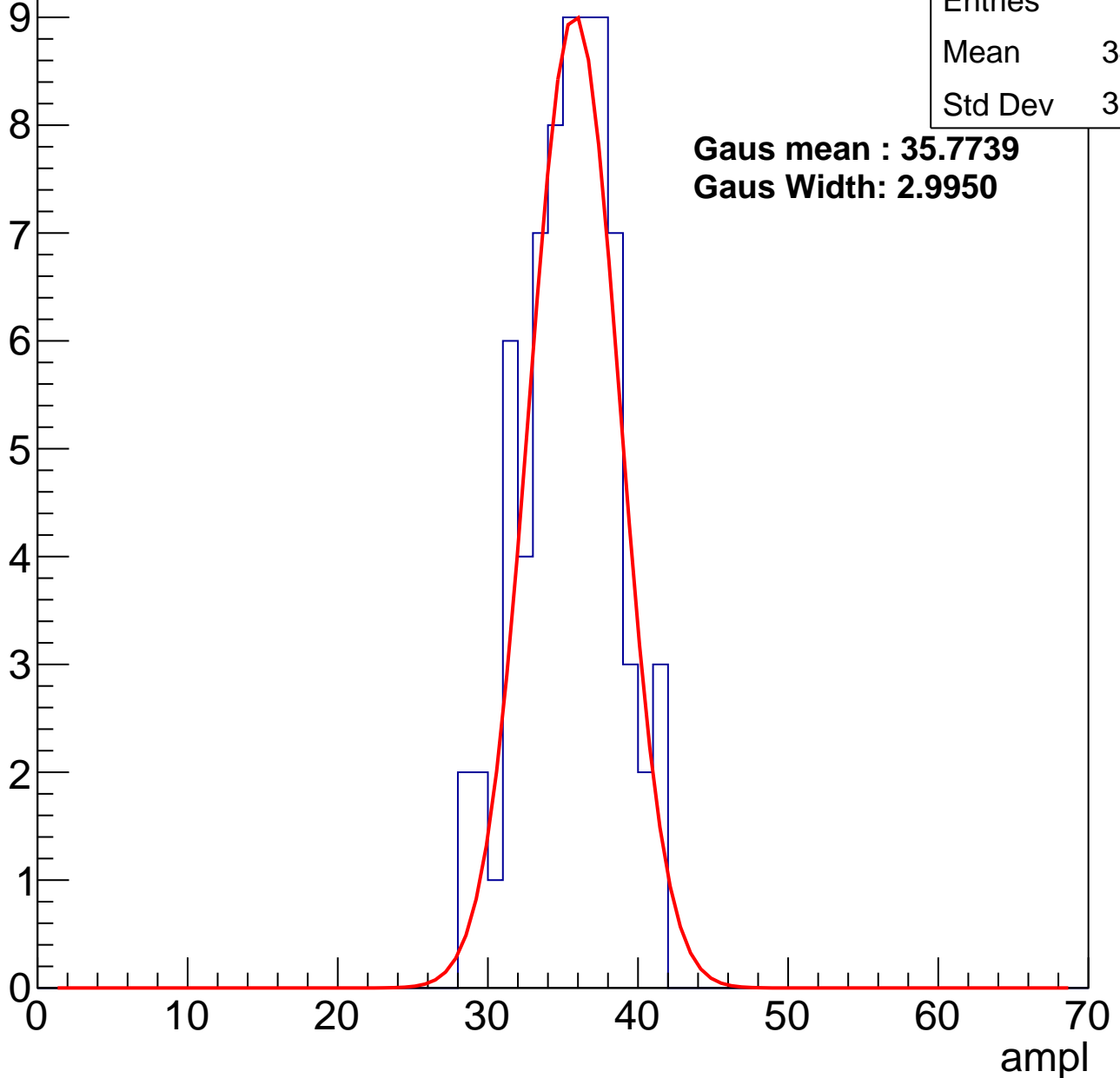
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	34.99
Std Dev	3.066

**Gaus mean : 35.7739**

**Gaus Width: 2.9950**



# B1L003S, U23-ch42, adc2

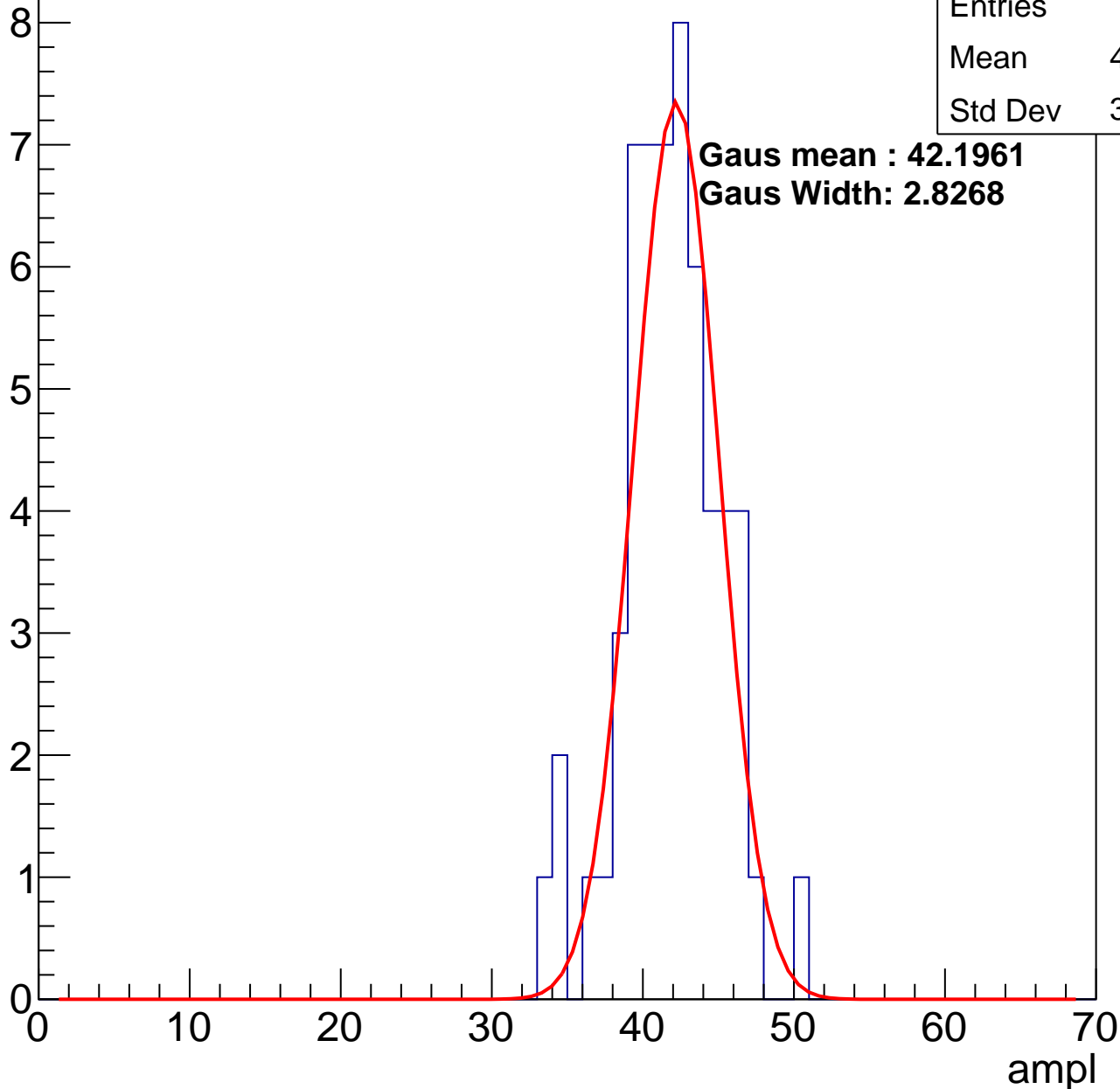
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	41.39
Std Dev	3.259

**Gaus mean : 42.1961**

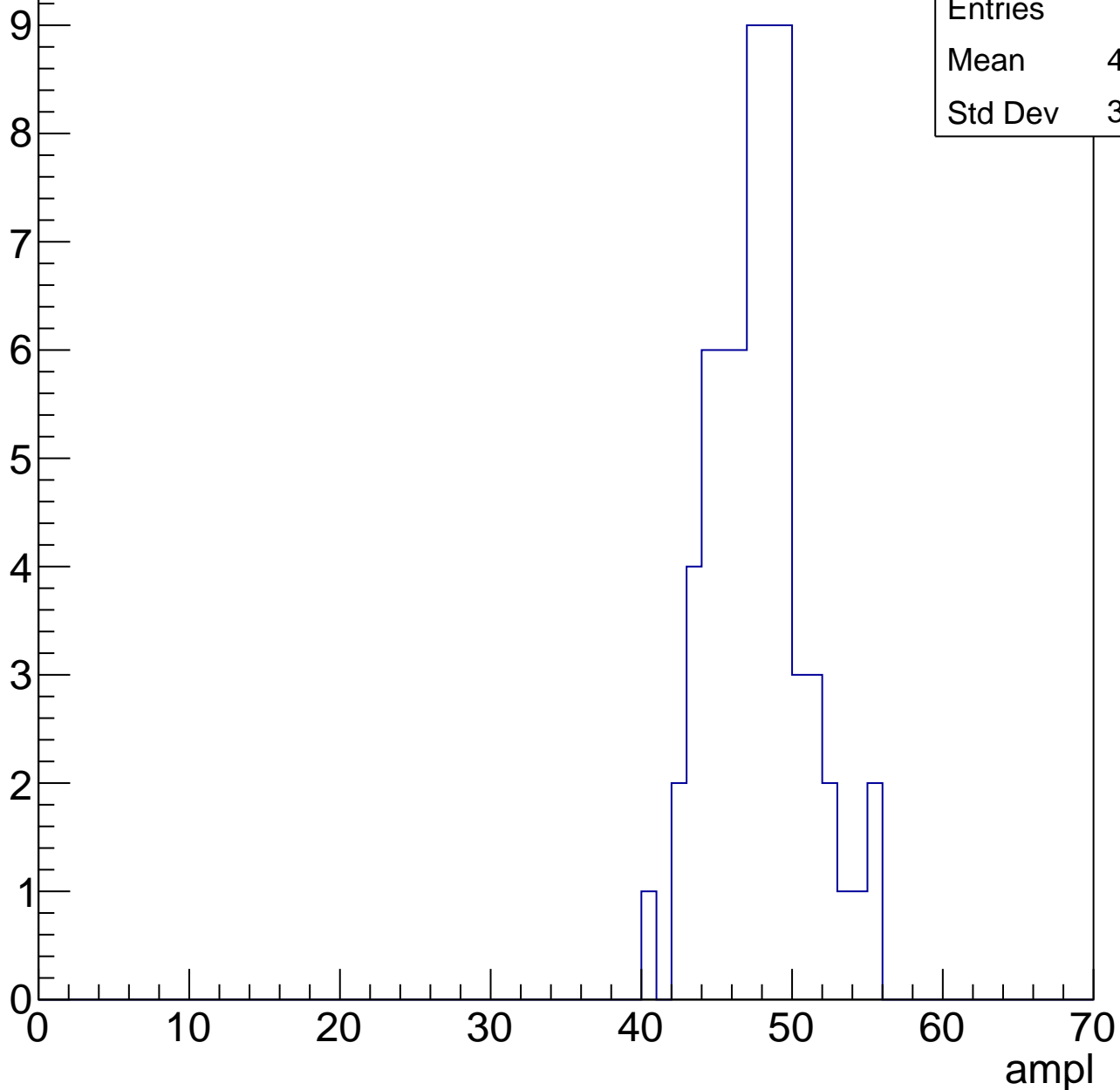
**Gaus Width: 2.8268**



# B1L003S, U23-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



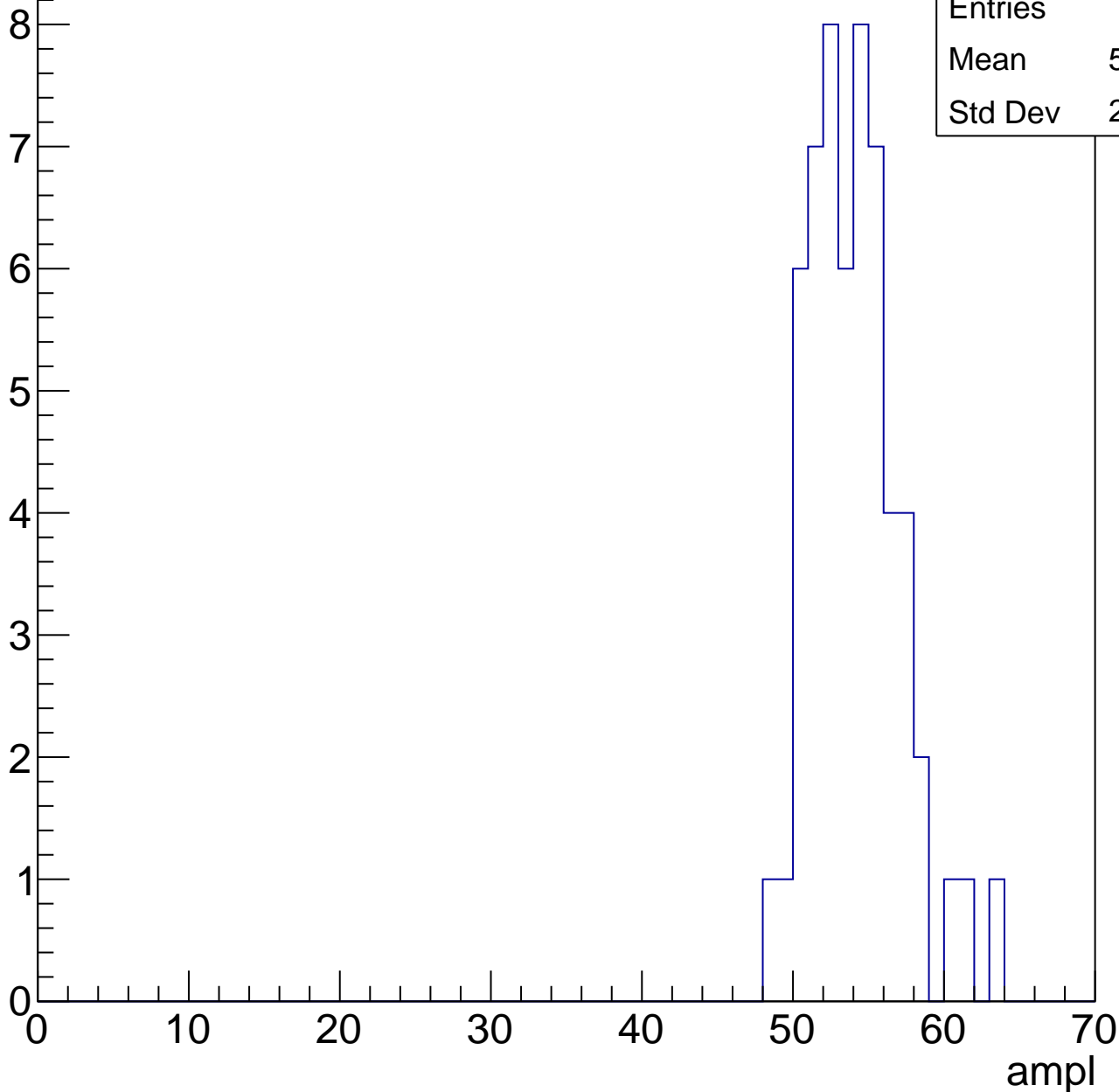
Entries	64
Mean	47.28
Std Dev	3.125

# B1L003S, U23-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	53.63
Std Dev	2.983

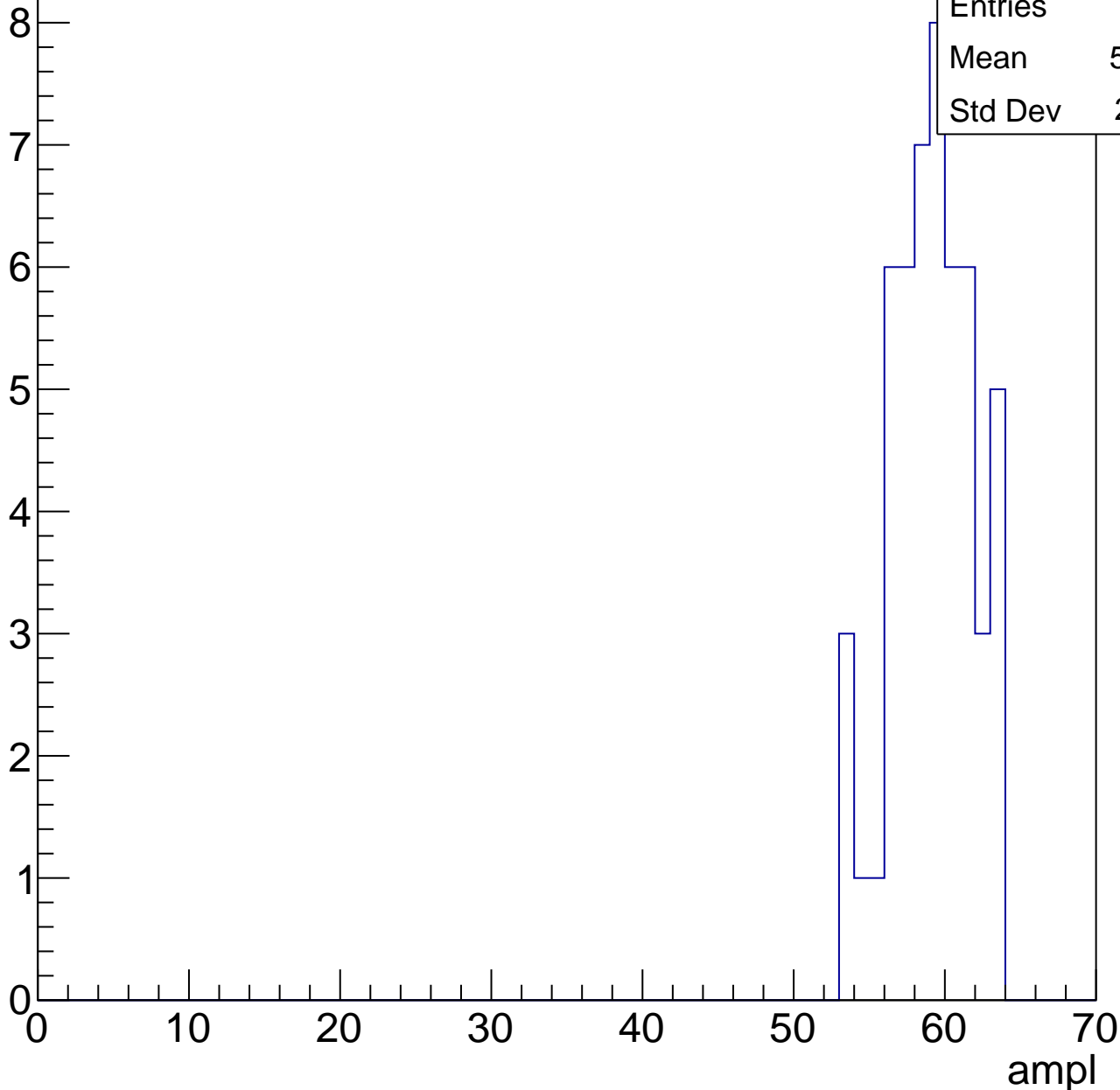


# B1L003S, U23-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	58.67
Std Dev	2.651

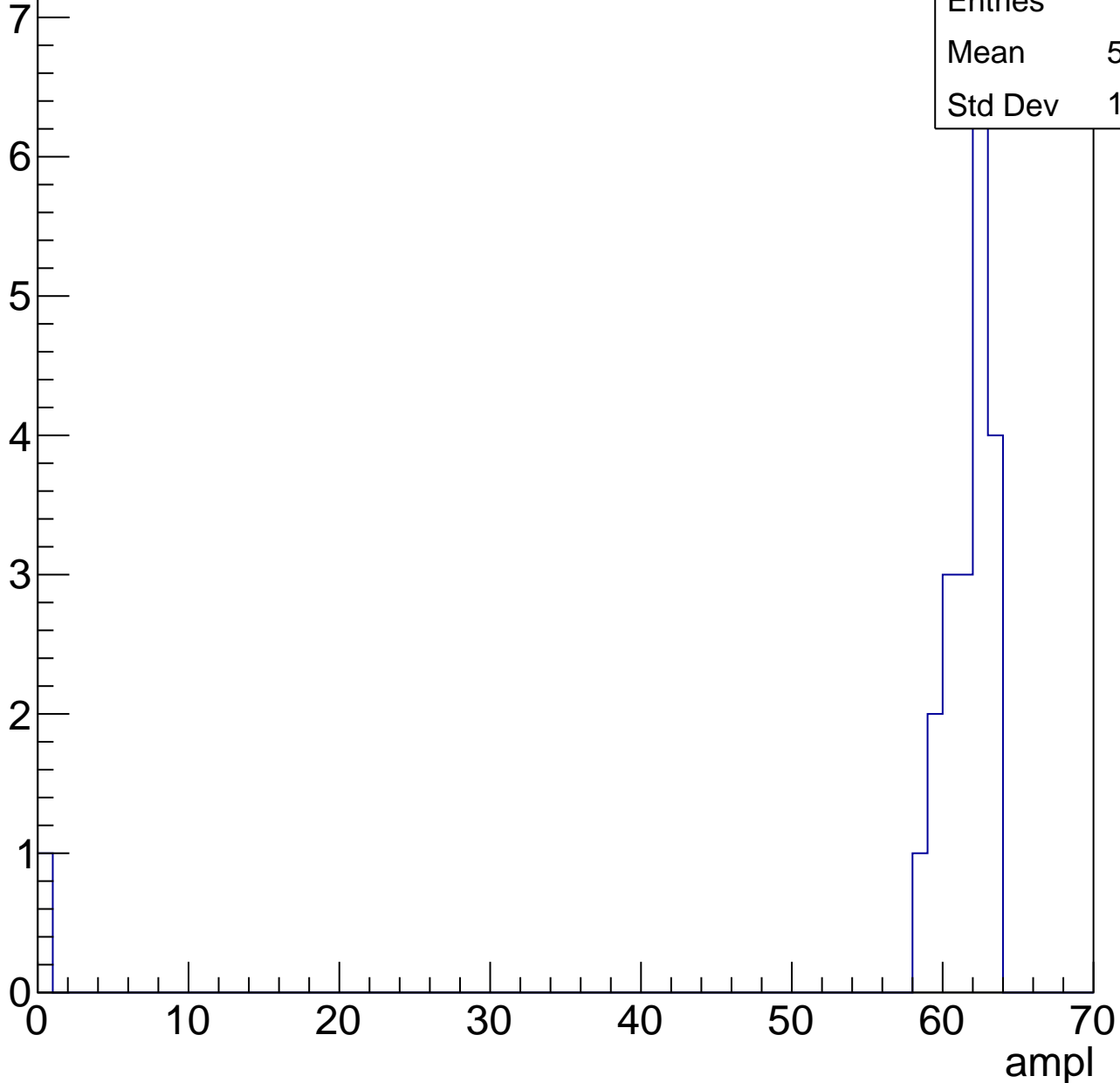


# B1L003S, U23-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	21
Mean	58.33
Std Dev	13.12





# B1L003S, U23-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch43, adc0

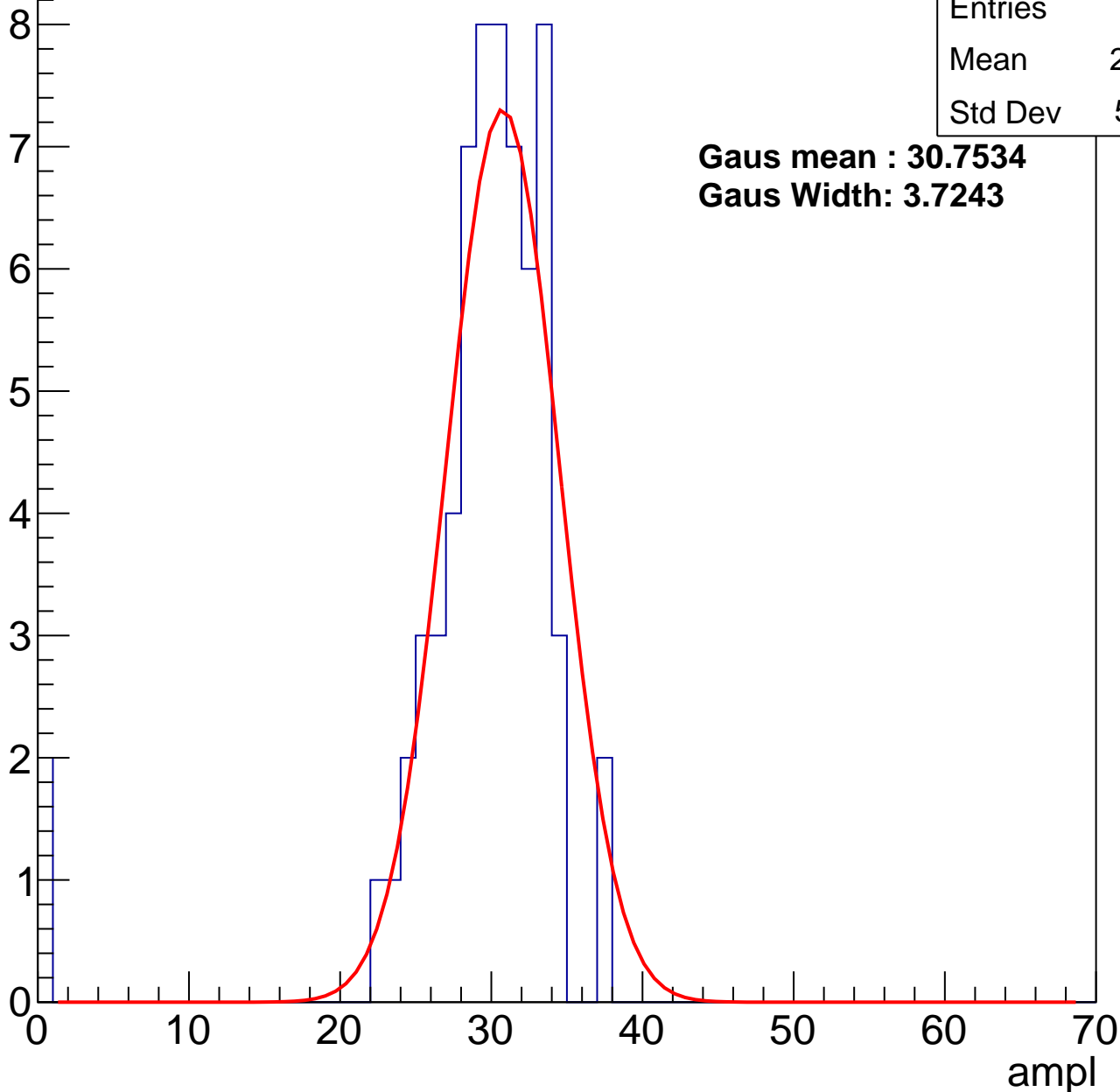
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.78
Std Dev	5.991

**Gaus mean : 30.7534**

**Gaus Width: 3.7243**



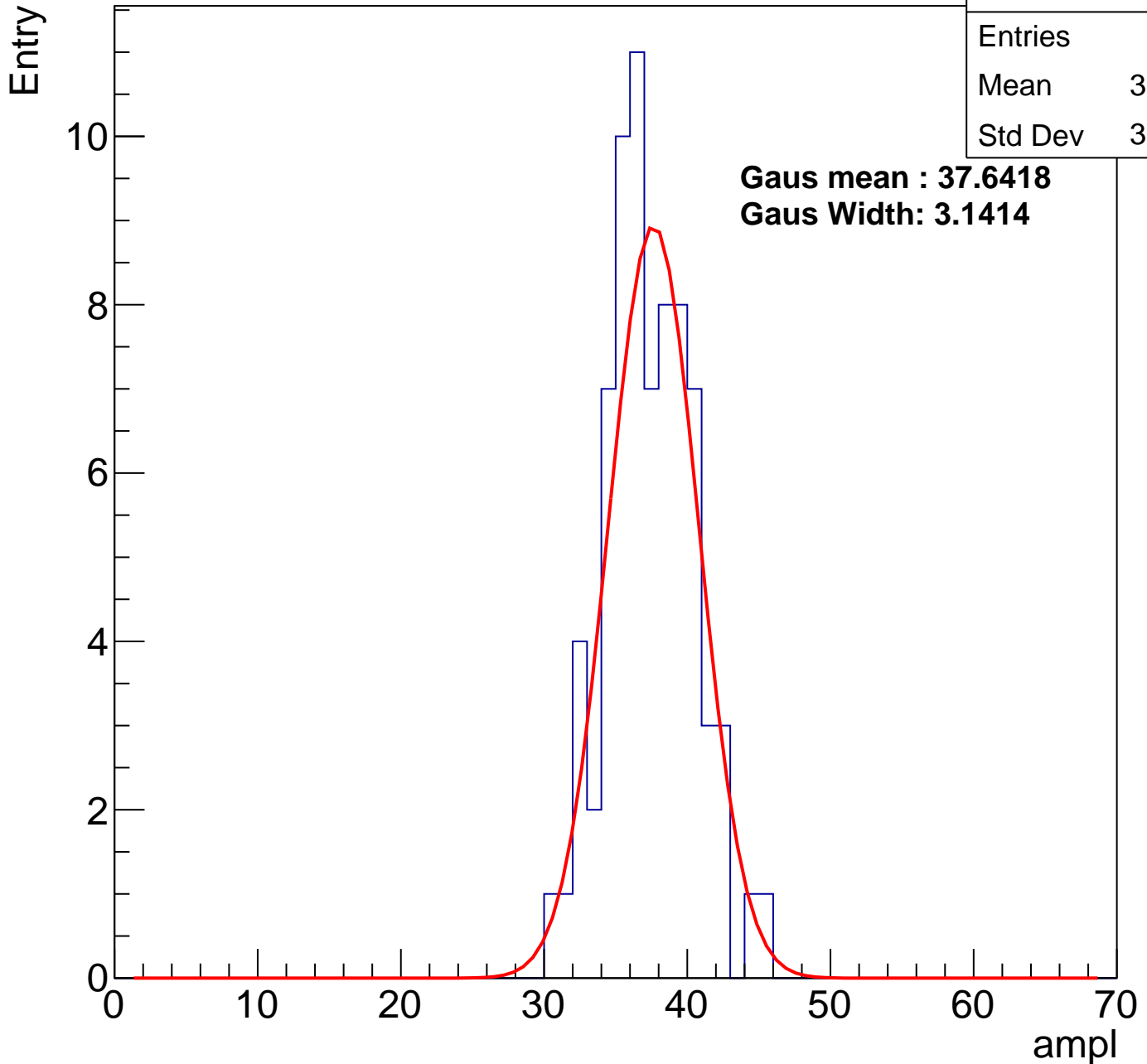
# B1L003S, U23-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	74
Mean	36.92
Std Dev	3.008

**Gaus mean : 37.6418**

**Gaus Width: 3.1414**



# B1L003S, U23-ch43, adc2

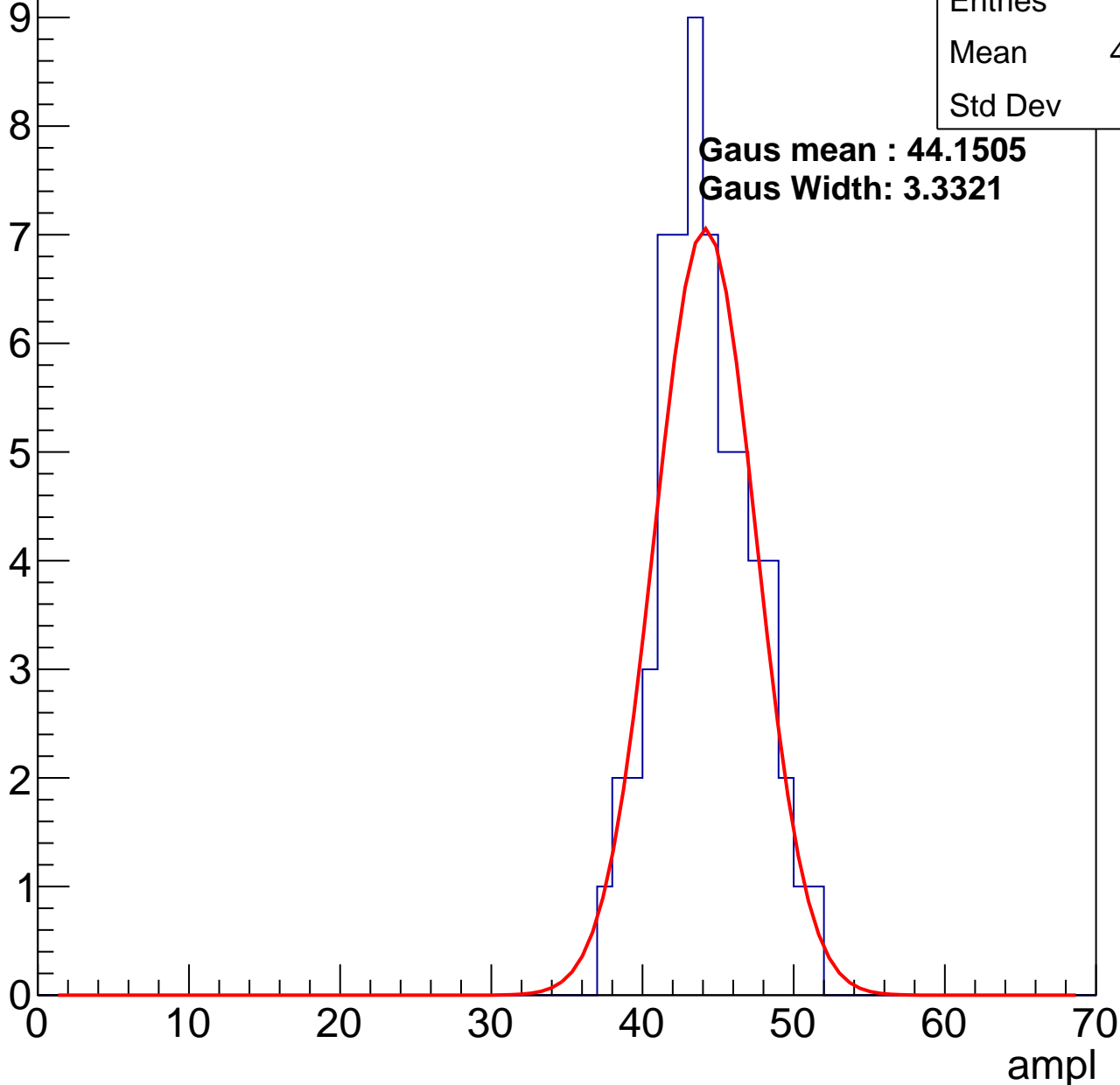
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	43.68
Std Dev	3.09

**Gaus mean : 44.1505**

**Gaus Width: 3.3321**

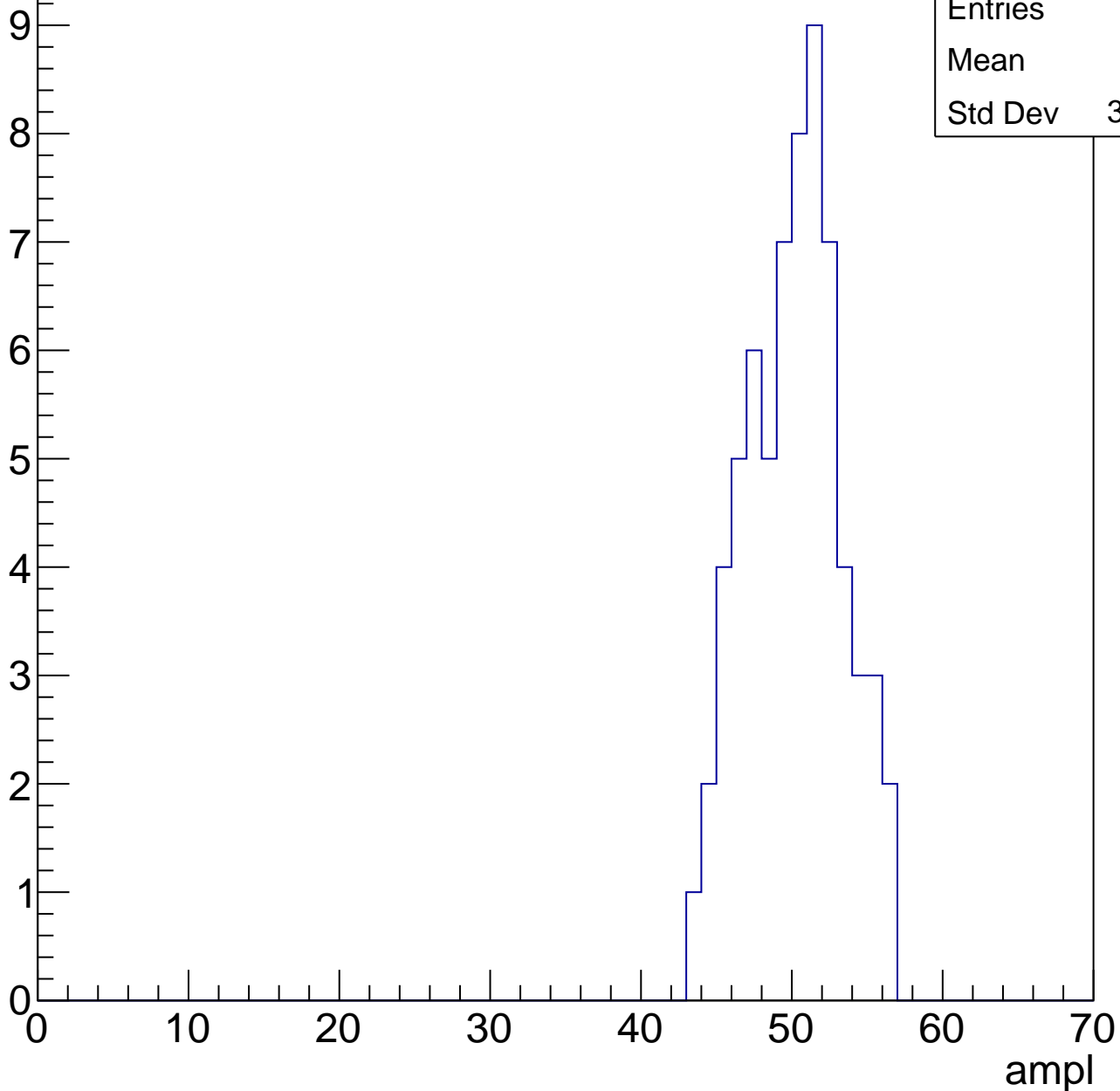


# B1L003S, U23-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	49.7
Std Dev	3.124

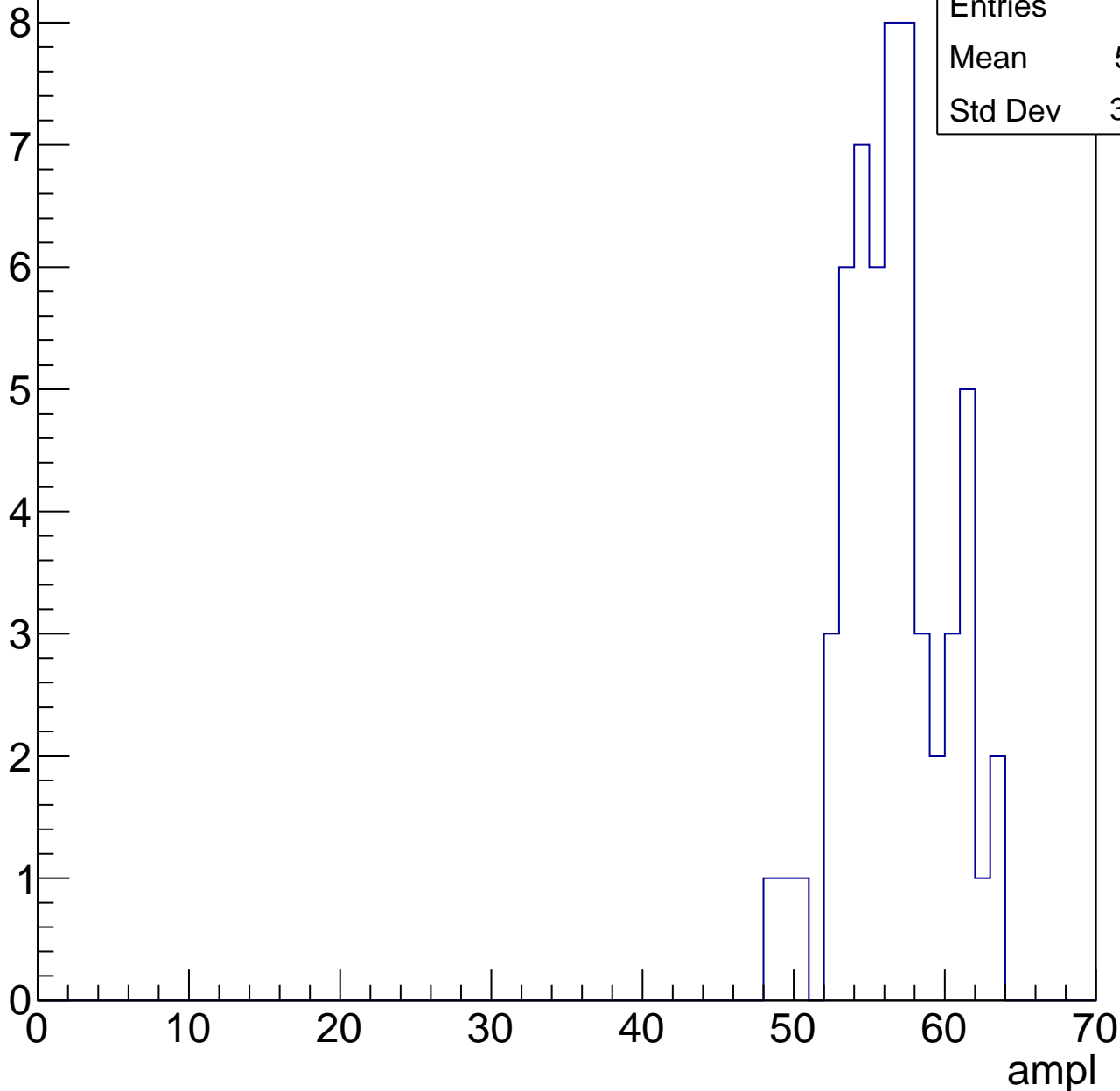


# B1L003S, U23-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	56.11
Std Dev	3.323



# B1L003S, U23-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

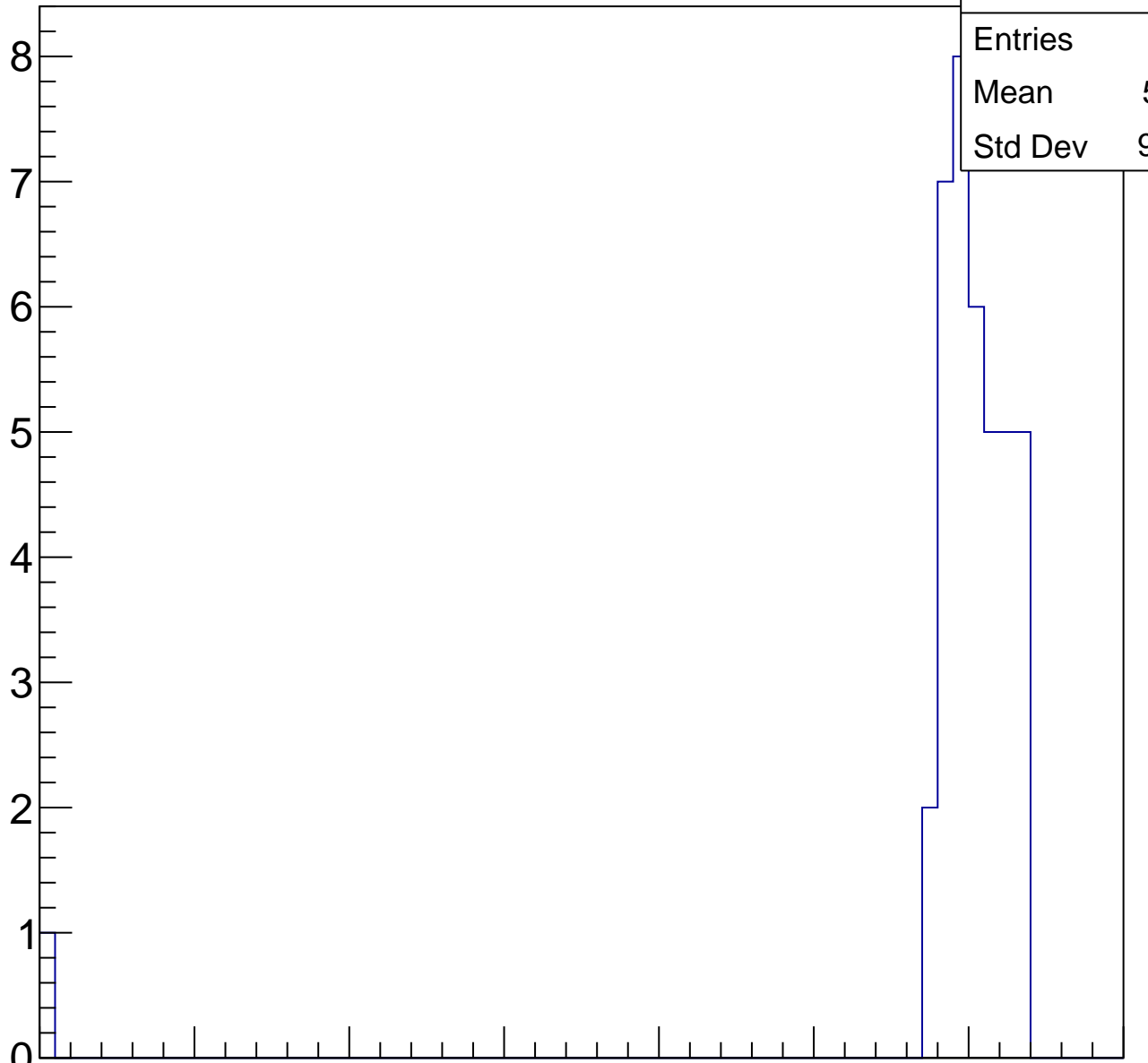
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	39
Mean	58.51
Std Dev	9.658

ampl

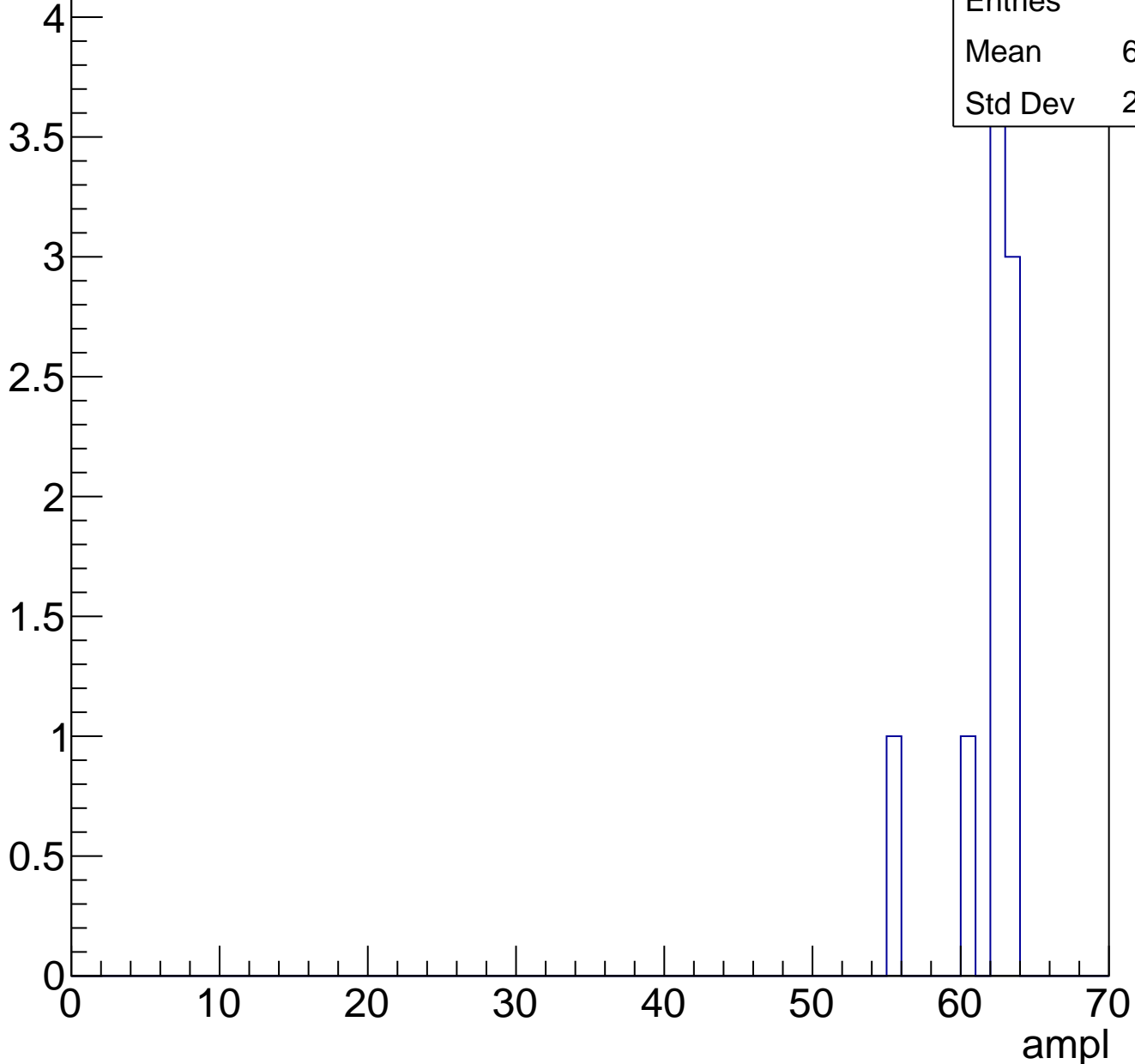
0 10 20 30 40 50 60 70



# B1L003S, U23-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	9
Mean	61.33
Std Dev	2.404



# B1L003S, U23-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch44, adc0

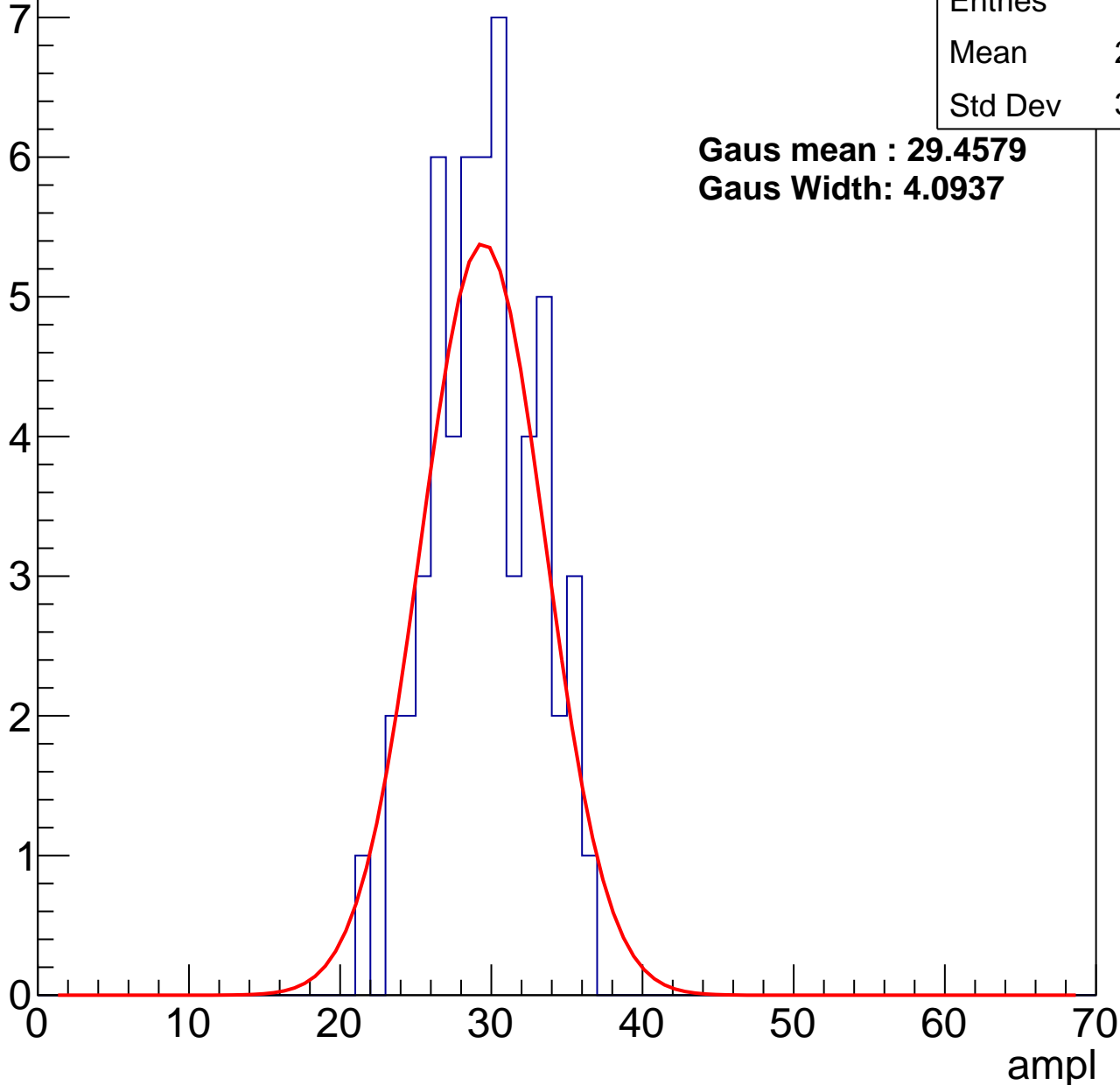
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.11
Std Dev	3.441

**Gaus mean : 29.4579**

**Gaus Width: 4.0937**



# B1L003S, U23-ch44, adc1

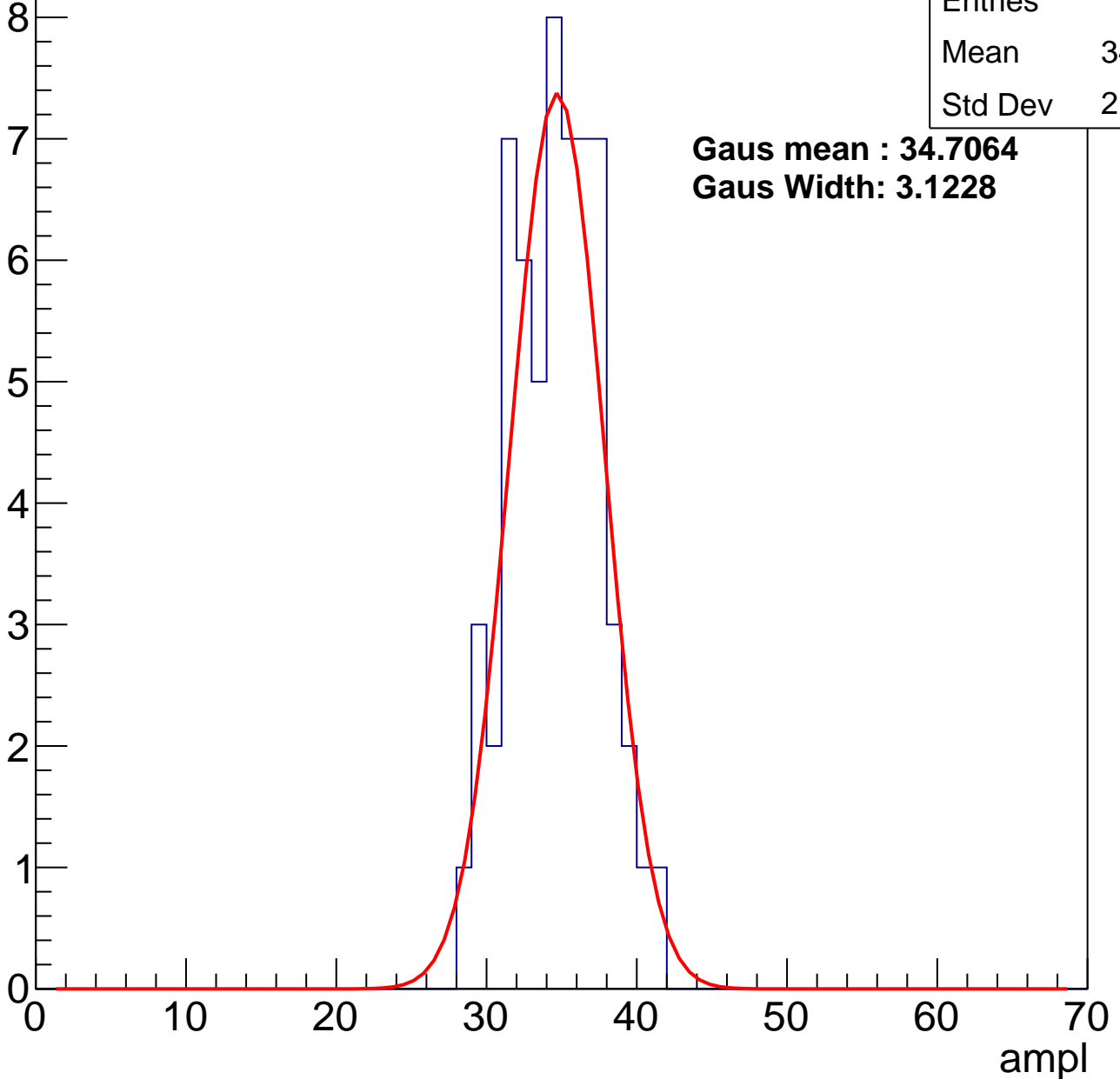
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	34.17
Std Dev	2.928

**Gaus mean : 34.7064**

**Gaus Width: 3.1228**



# B1L003S, U23-ch44, adc2

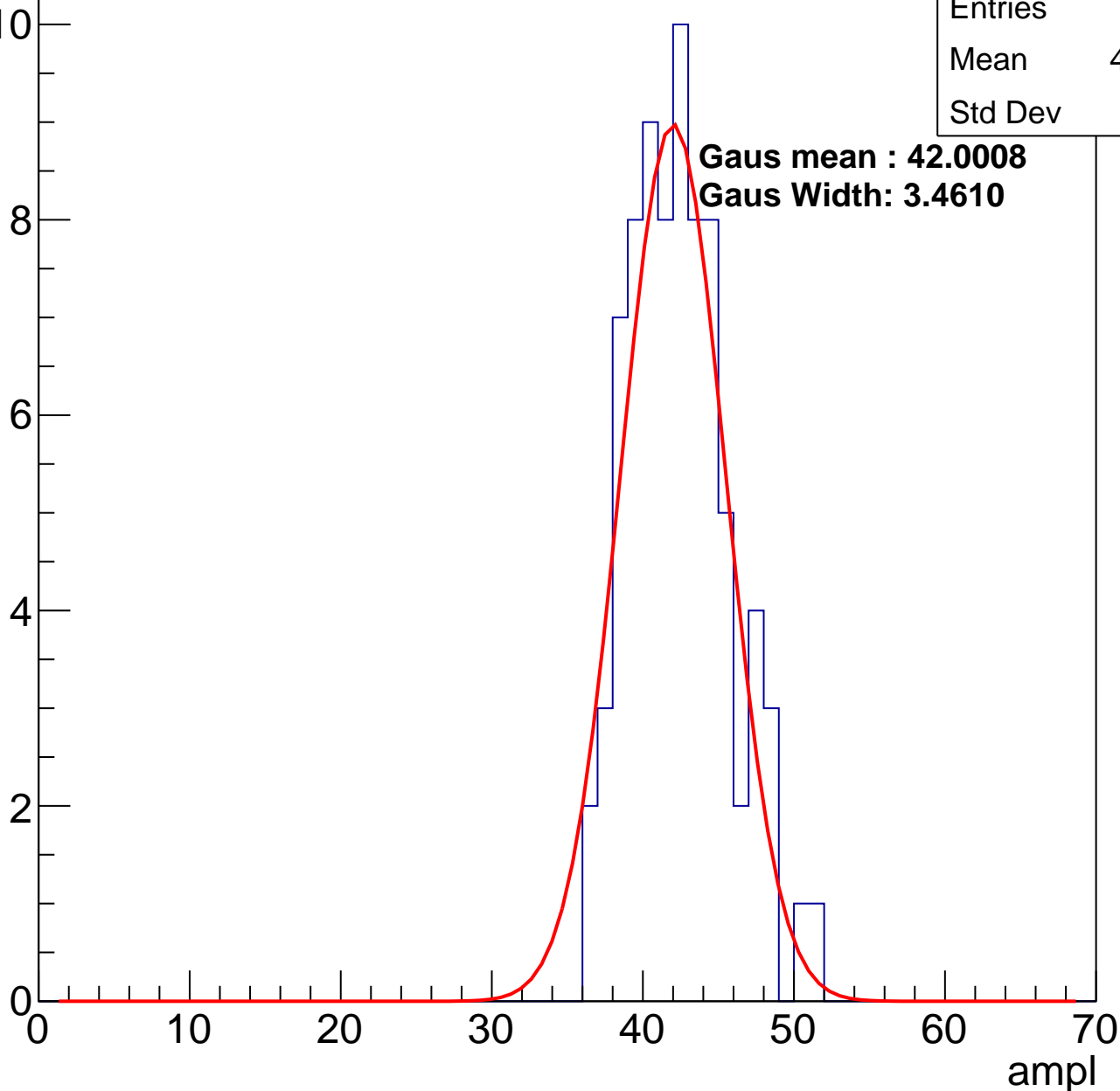
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	41.96
Std Dev	3.27

**Gaus mean : 42.0008**

**Gaus Width: 3.4610**

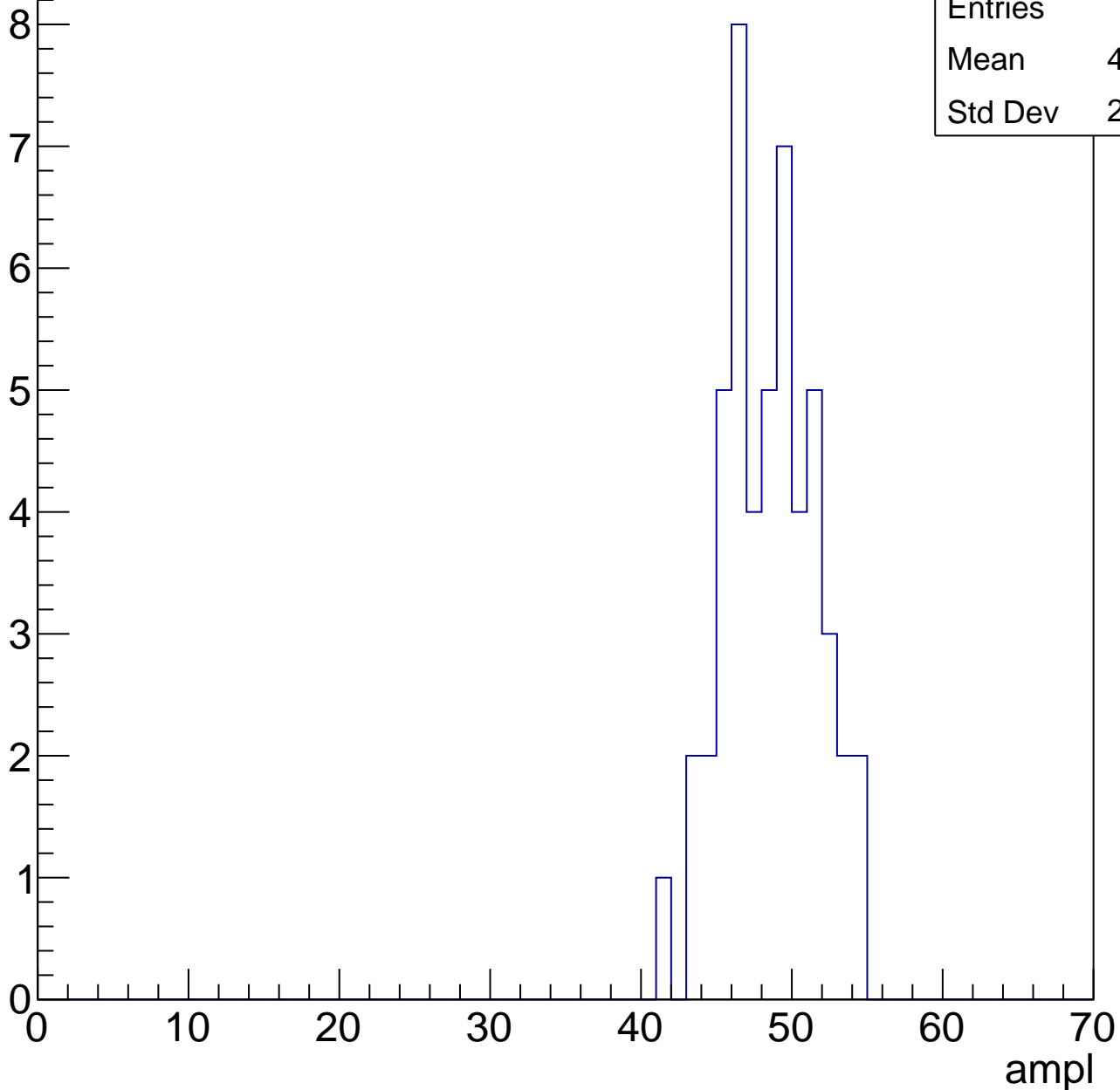


# B1L003S, U23-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	48.08
Std Dev	2.999

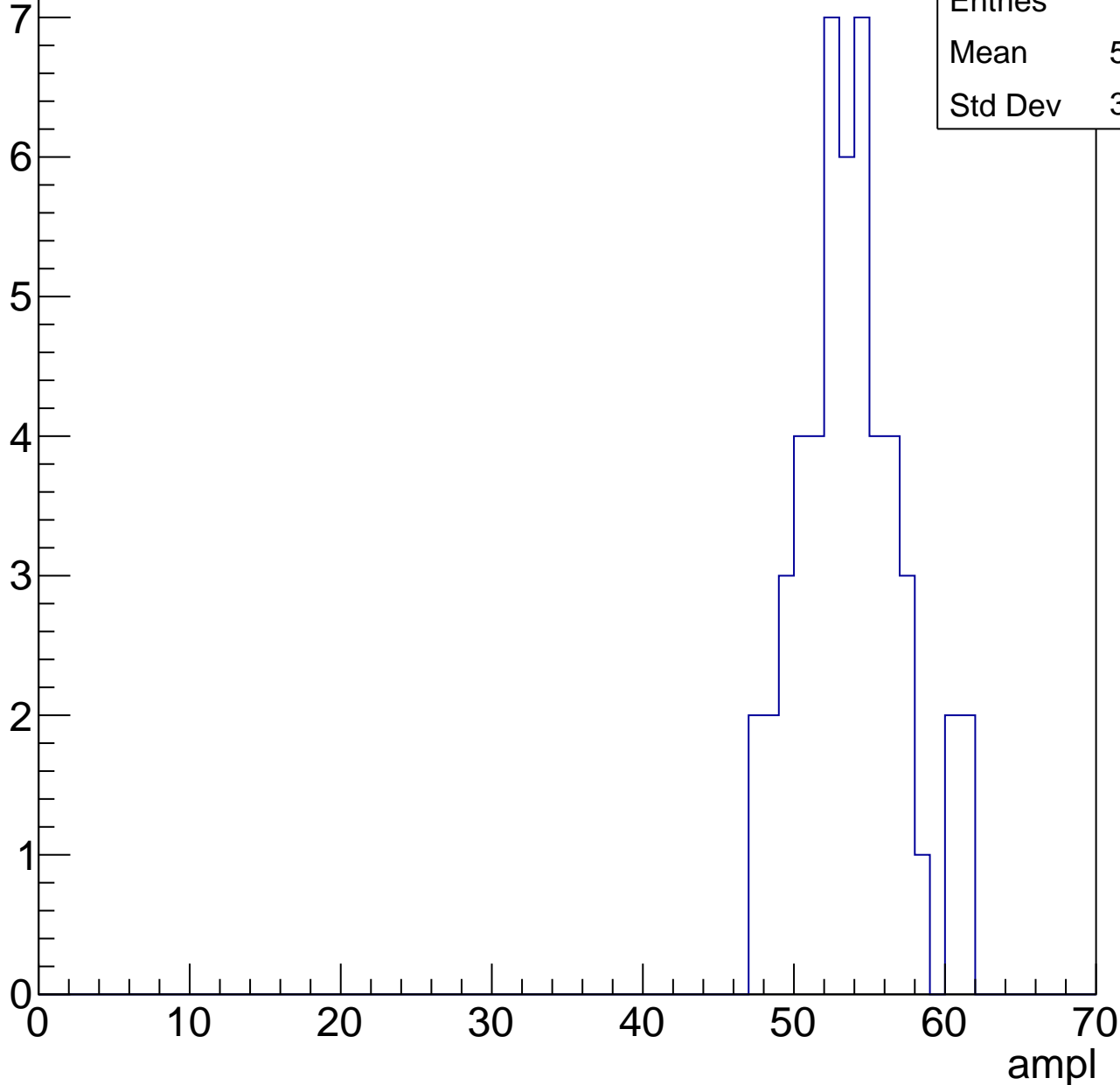


# B1L003S, U23-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	53.25
Std Dev	3.383

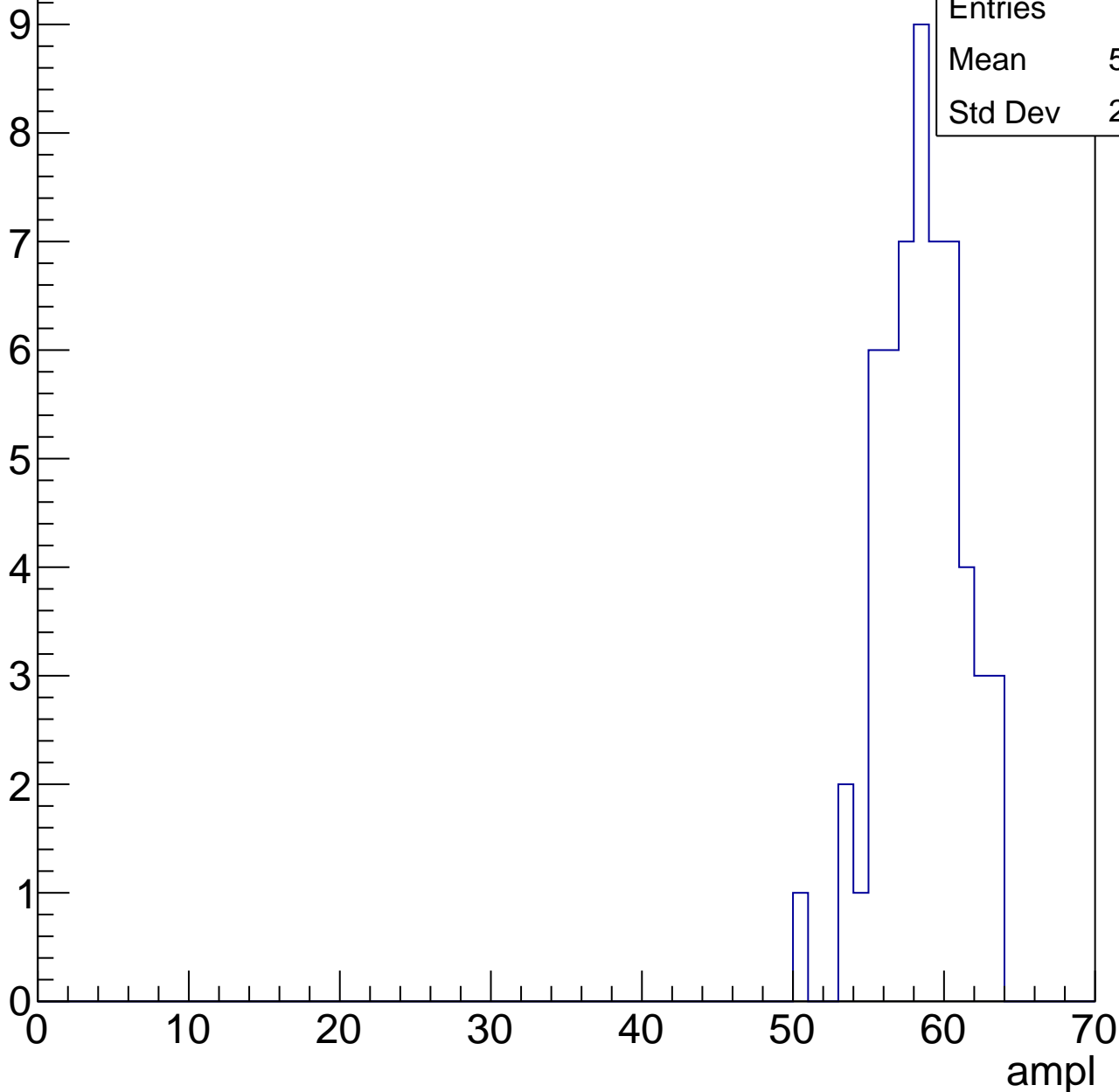


# B1L003S, U23-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.02
Std Dev	2.702

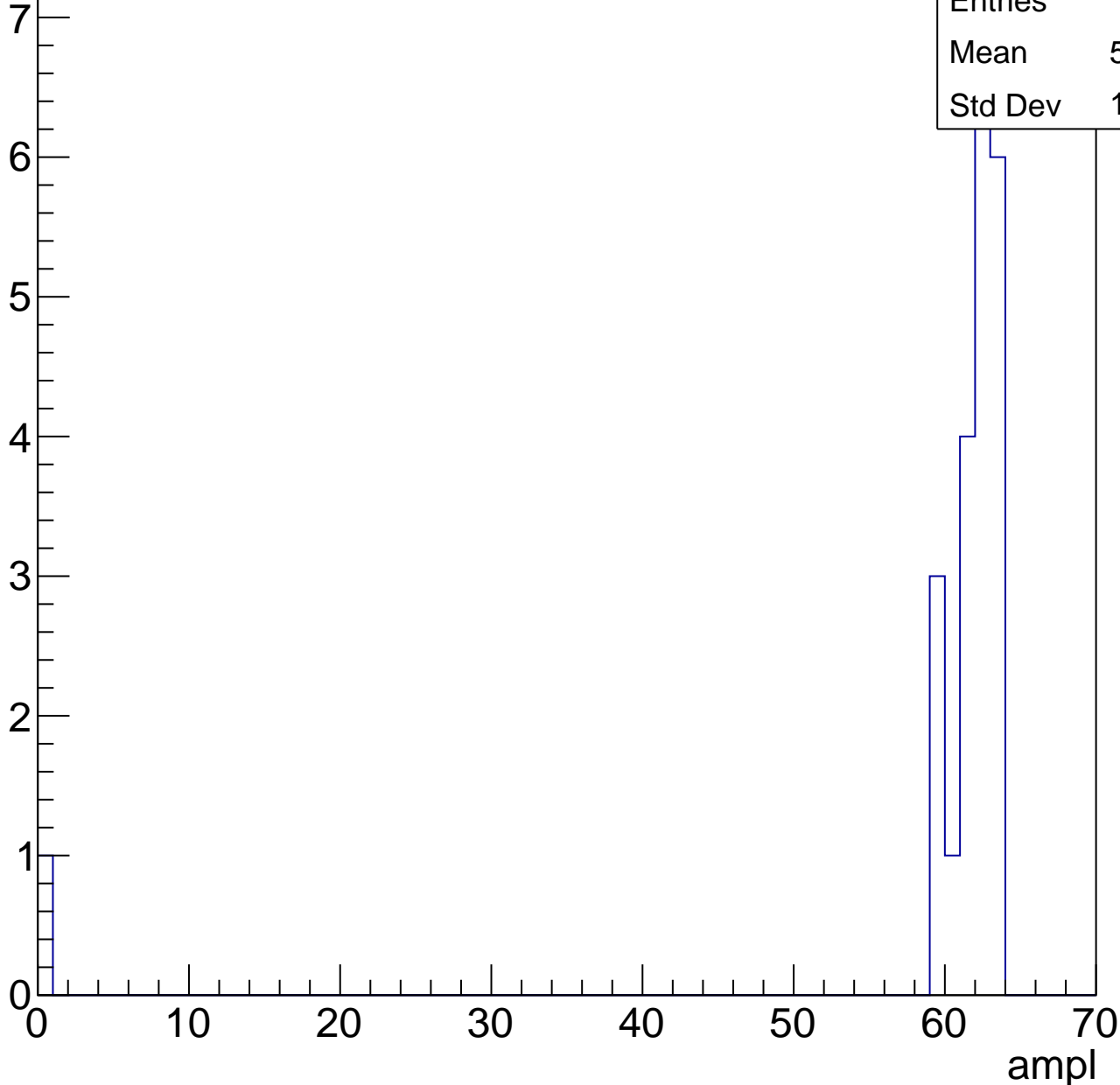


# B1L003S, U23-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	22
Mean	58.77
Std Dev	12.89





# B1L003S, U23-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L003S, U23-ch45, adc0

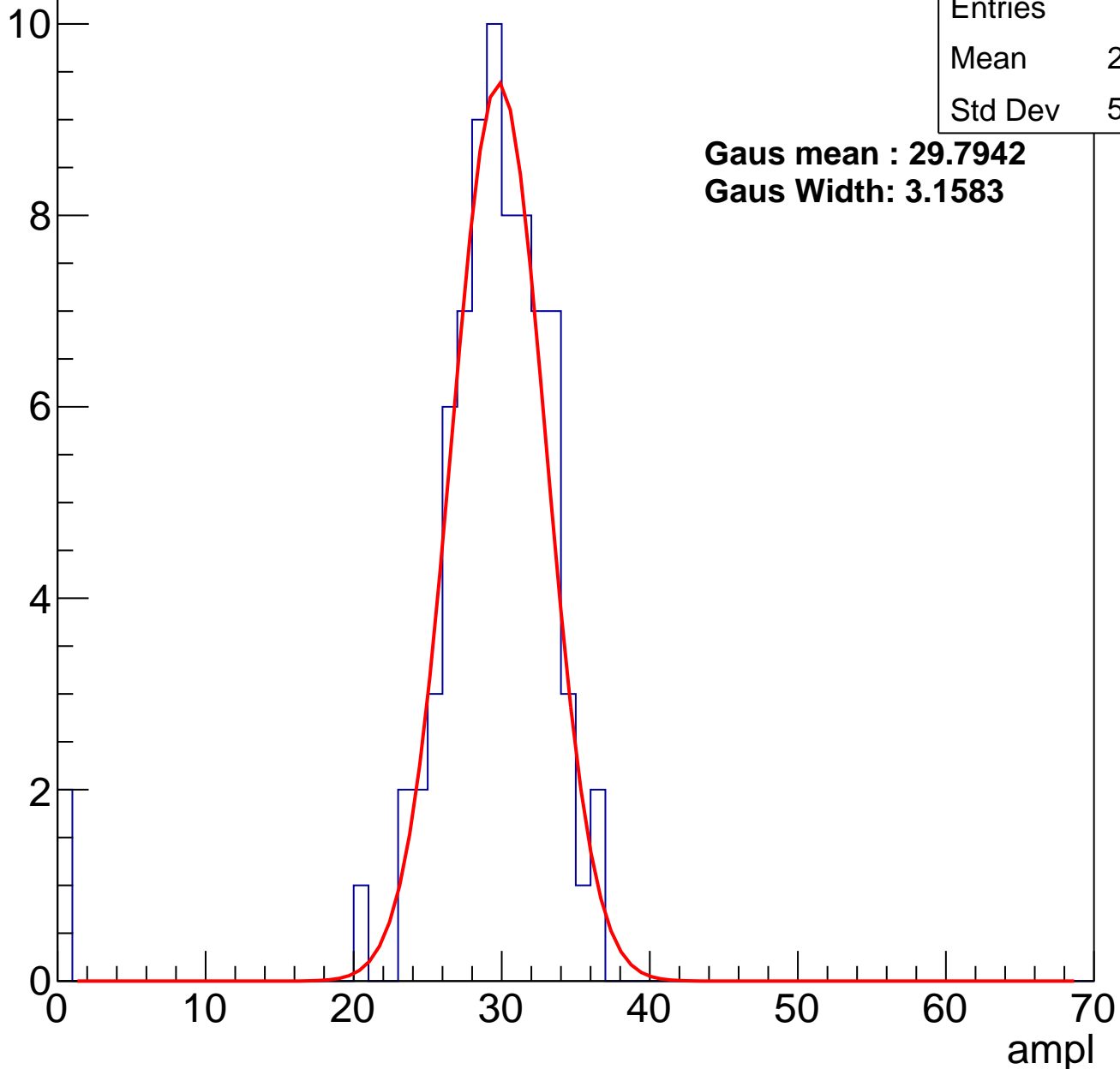
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	28.56
Std Dev	5.585

**Gaus mean : 29.7942**

**Gaus Width: 3.1583**

Entry



# B1L003S, U23-ch45, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	37.2
Std Dev	3.29

**Gaus mean : 37.2592**

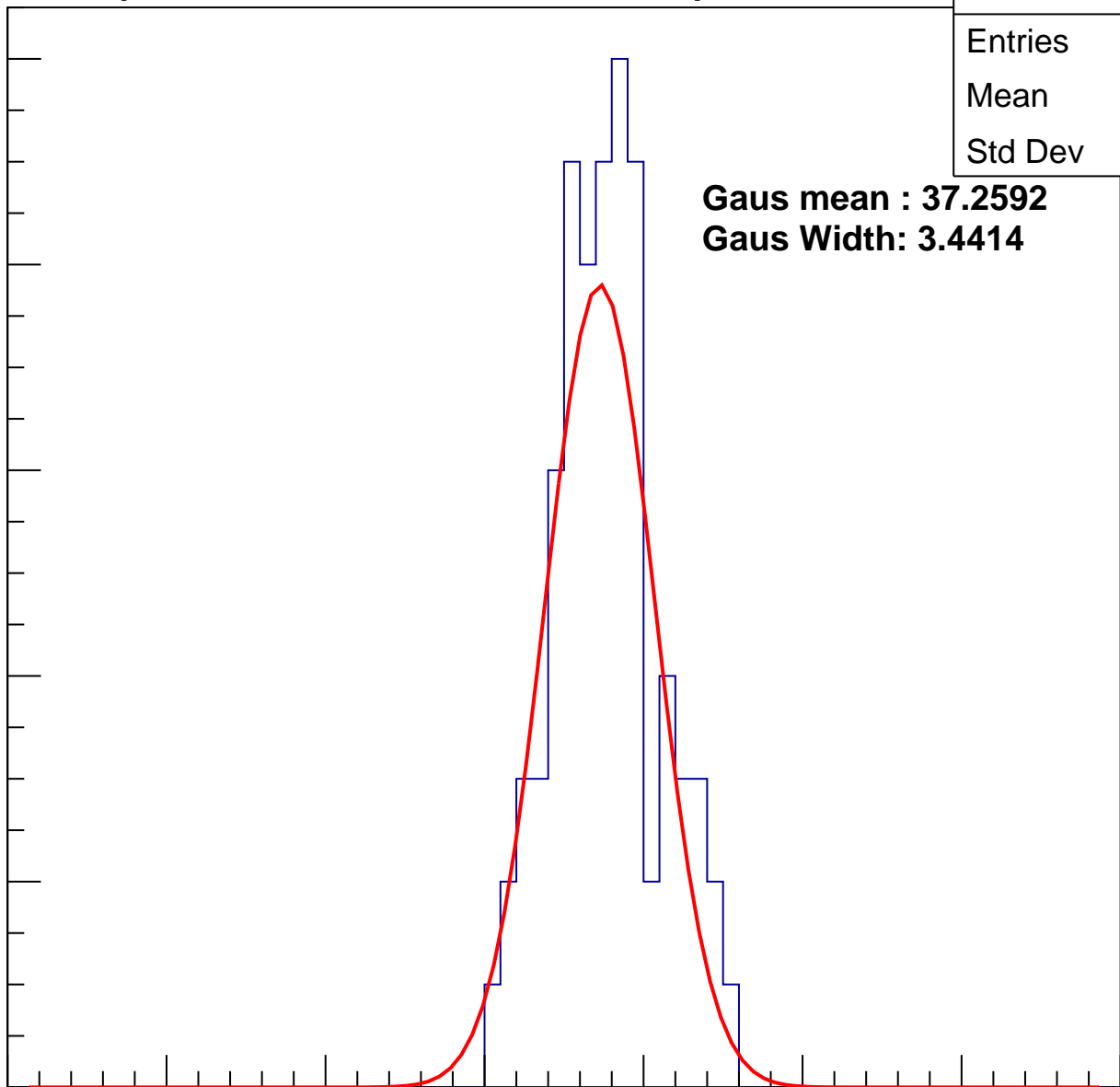
**Gaus Width: 3.4414**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch45, adc2

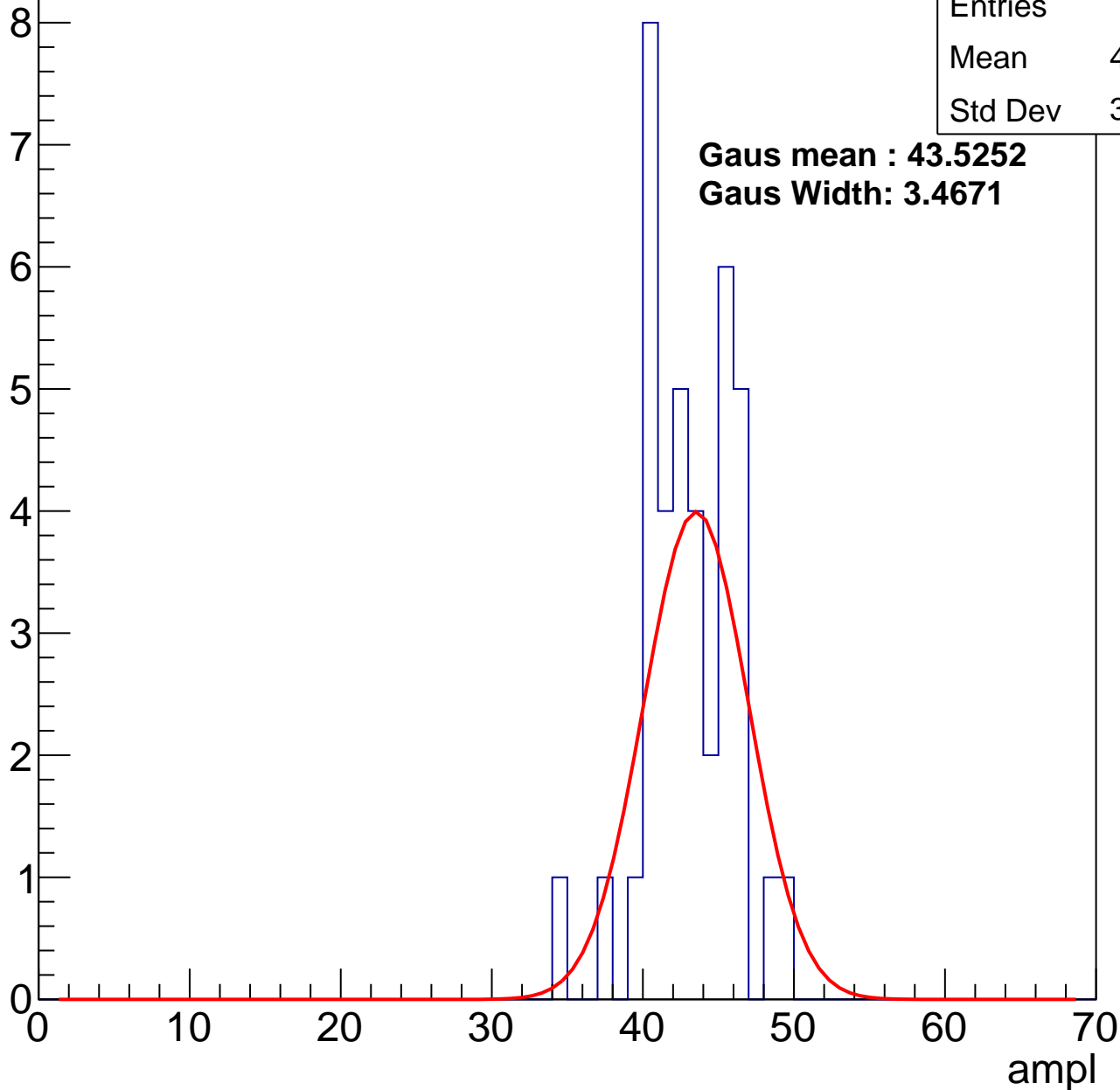
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	42.59
Std Dev	3.002

**Gaus mean : 43.5252**

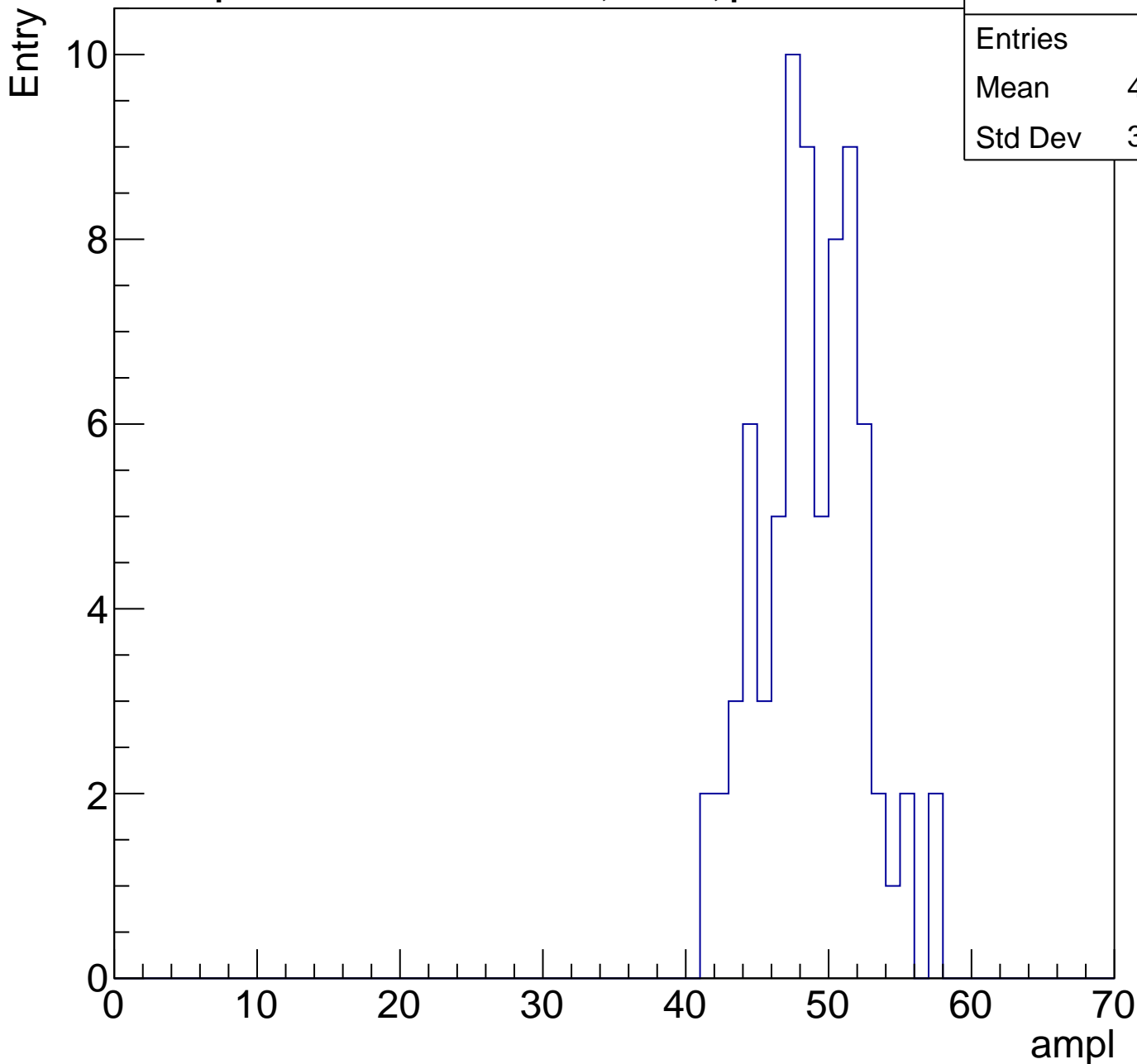
**Gaus Width: 3.4671**



# B1L003S, U23-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	48.35
Std Dev	3.557

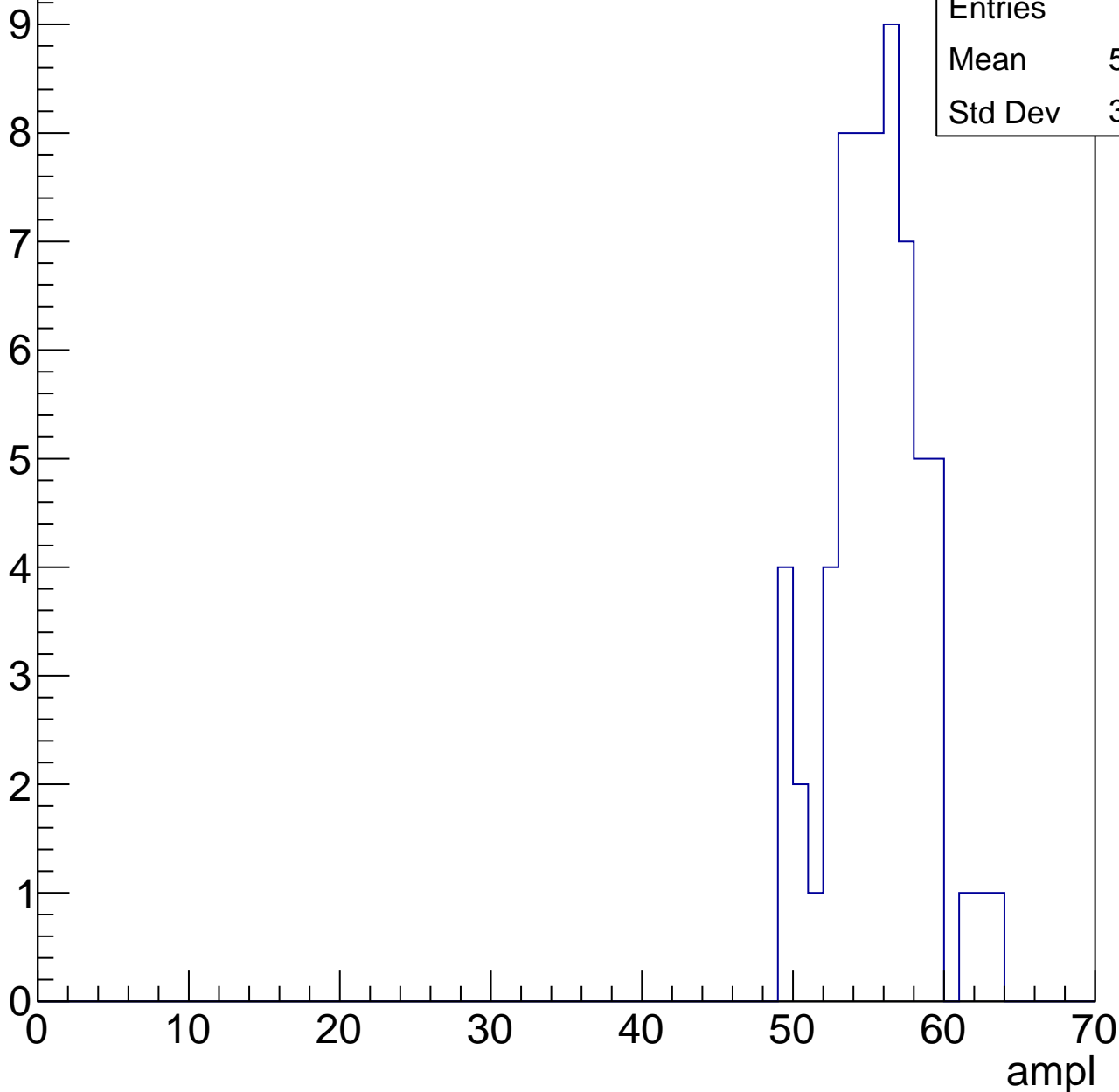


# B1L003S, U23-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.08
Std Dev	3.053

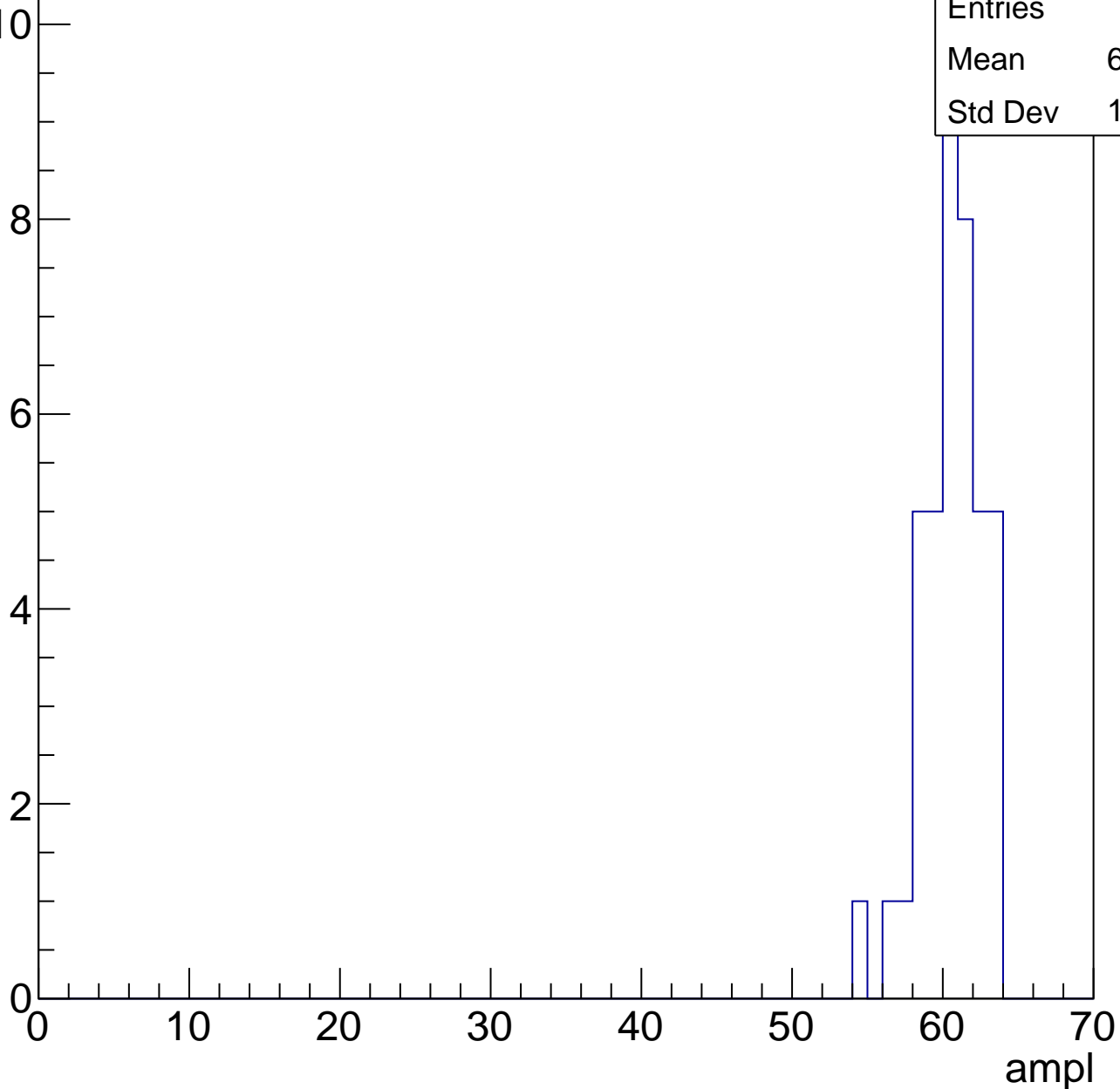


# B1L003S, U23-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

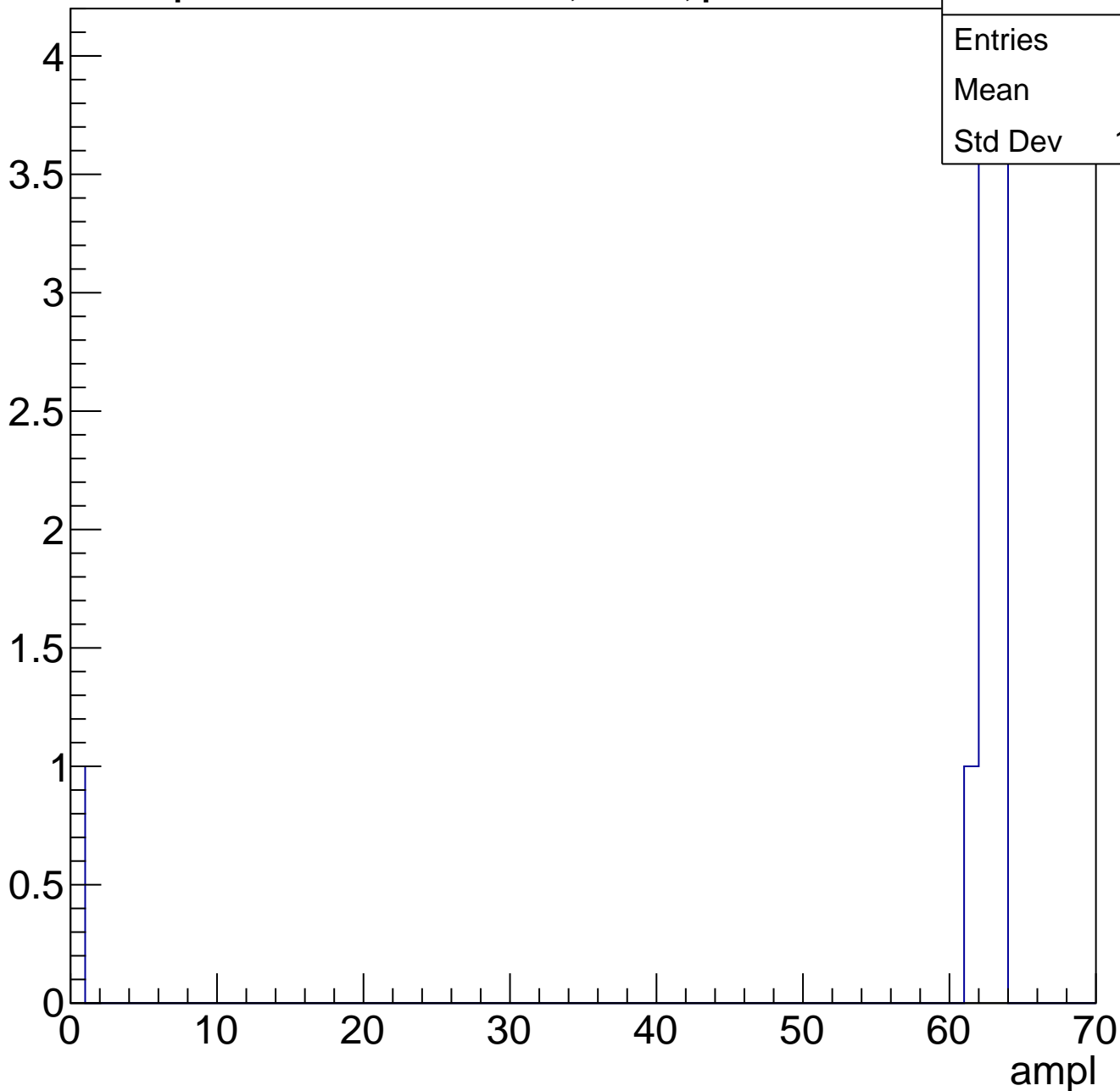
Entries	41
Mean	60.12
Std Dev	1.965



# B1L003S, U23-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

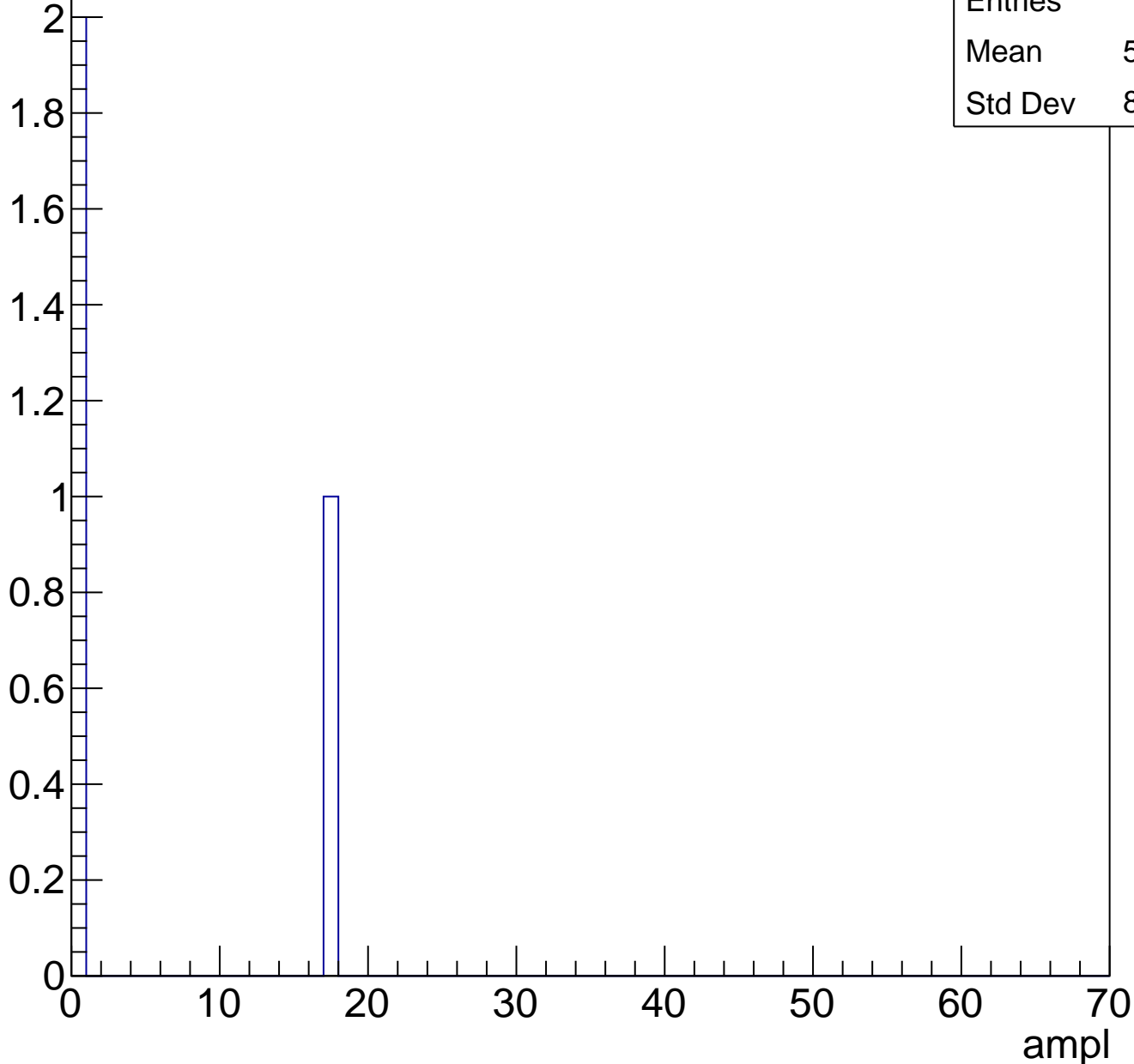




# B1L003S, U23-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



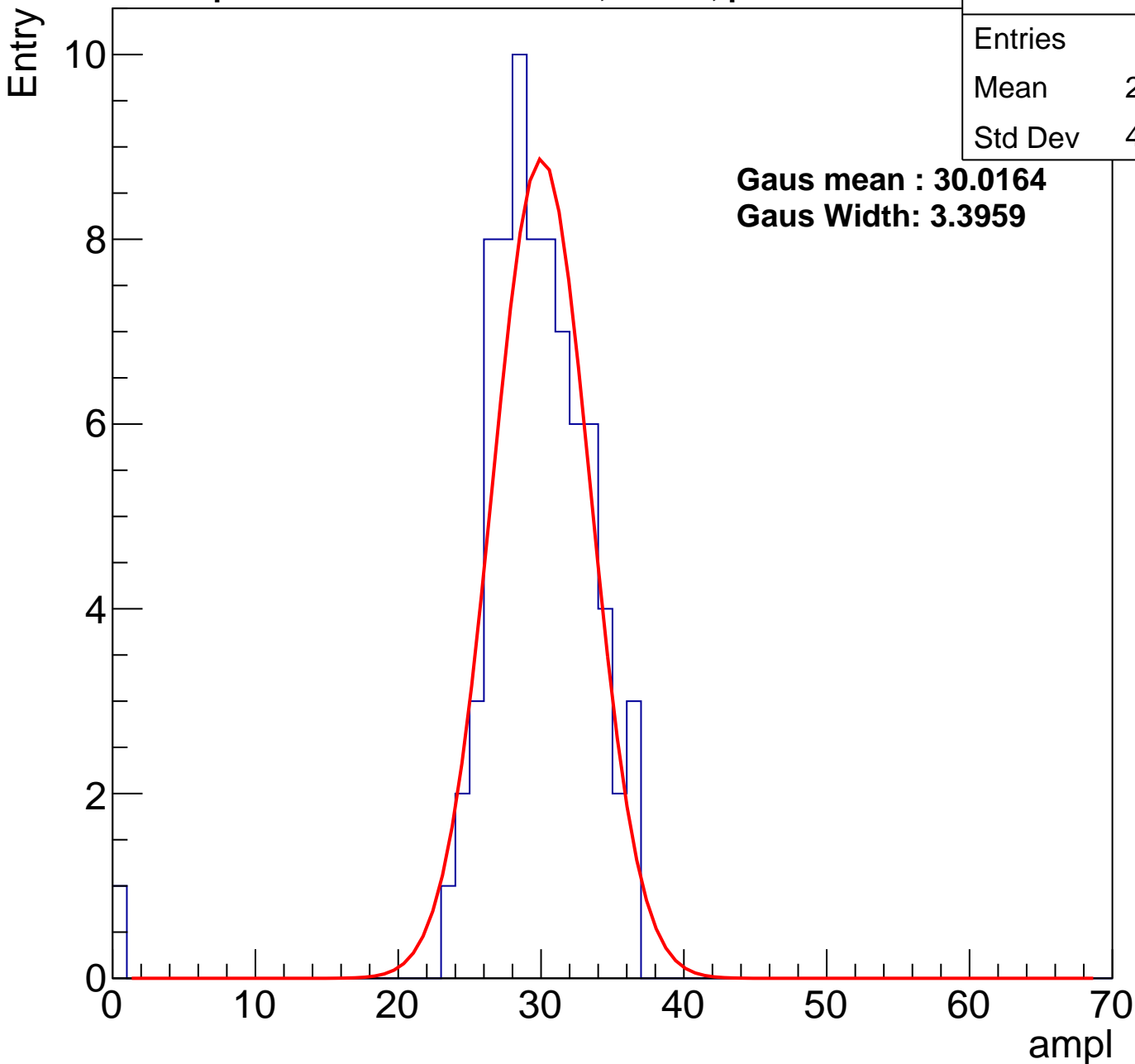
Entries	3
Mean	5.667
Std Dev	8.014

# B1L003S, U23-ch46, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	77
Mean	29.13
Std Dev	4.548

**Gaus mean : 30.0164**  
**Gaus Width: 3.3959**



# B1L003S, U23-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	36.68
Std Dev	3.331

**Gaus mean : 36.9355**

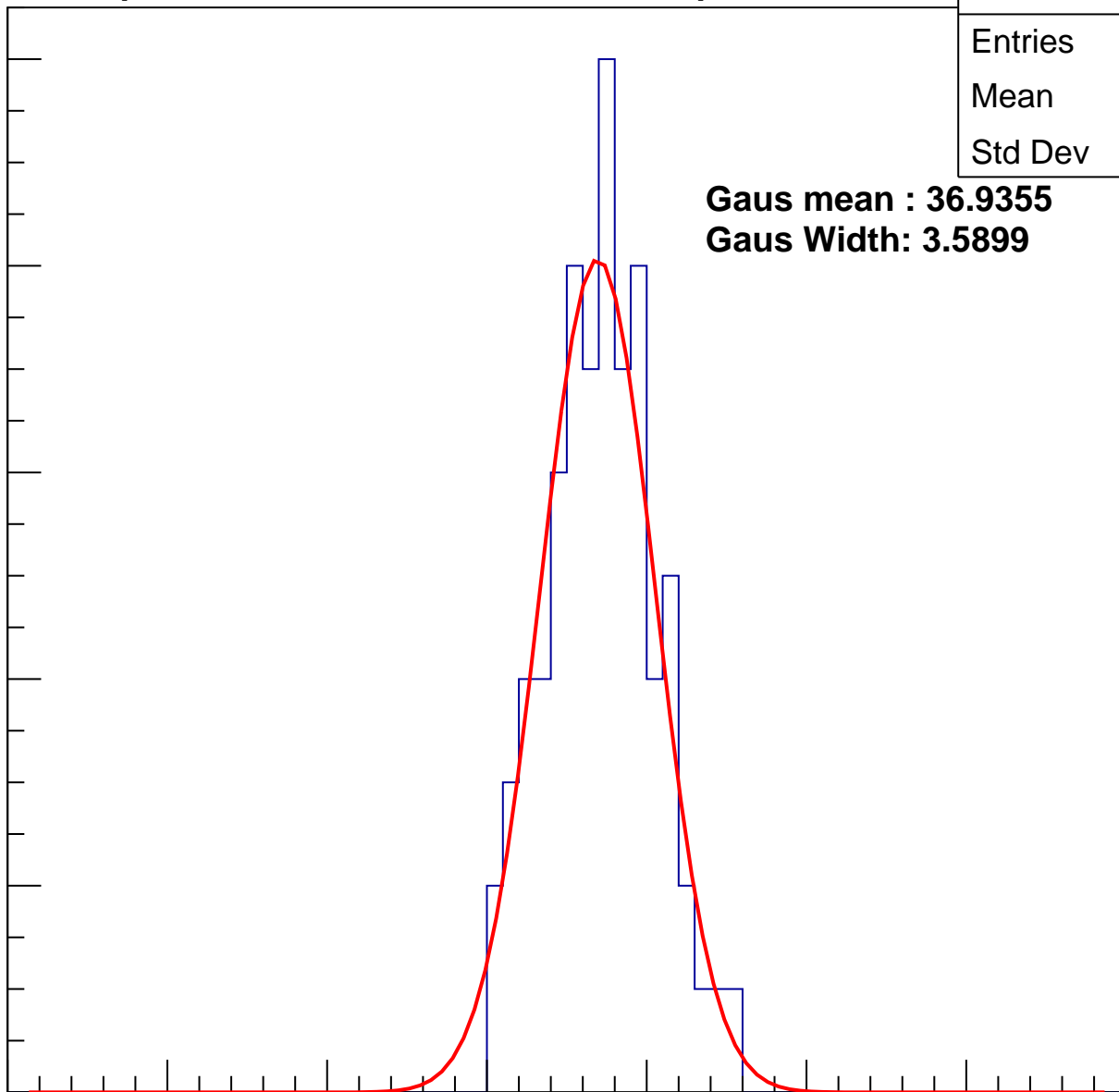
**Gaus Width: 3.5899**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

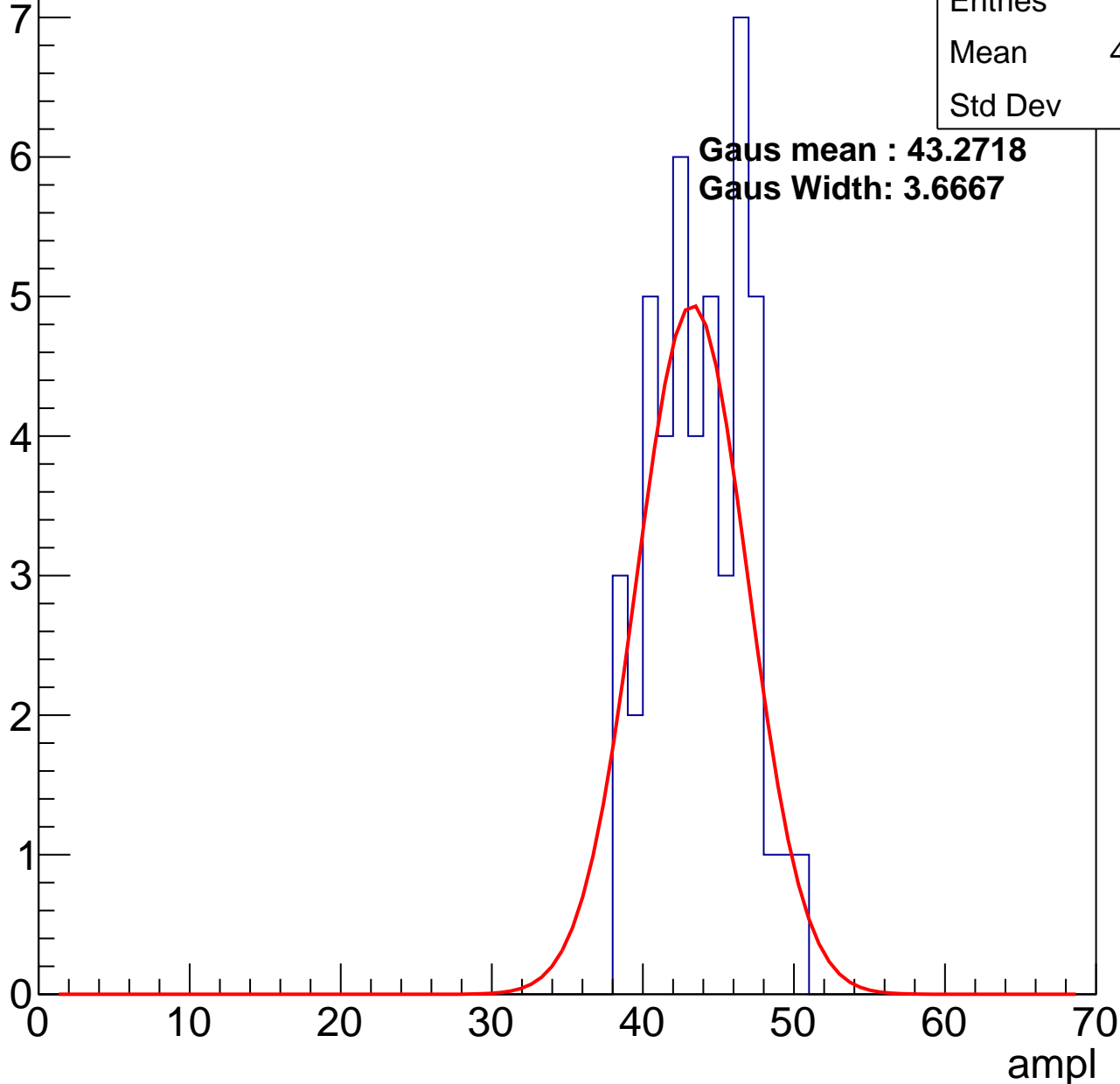
ampl



# B1L003S, U23-ch46, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

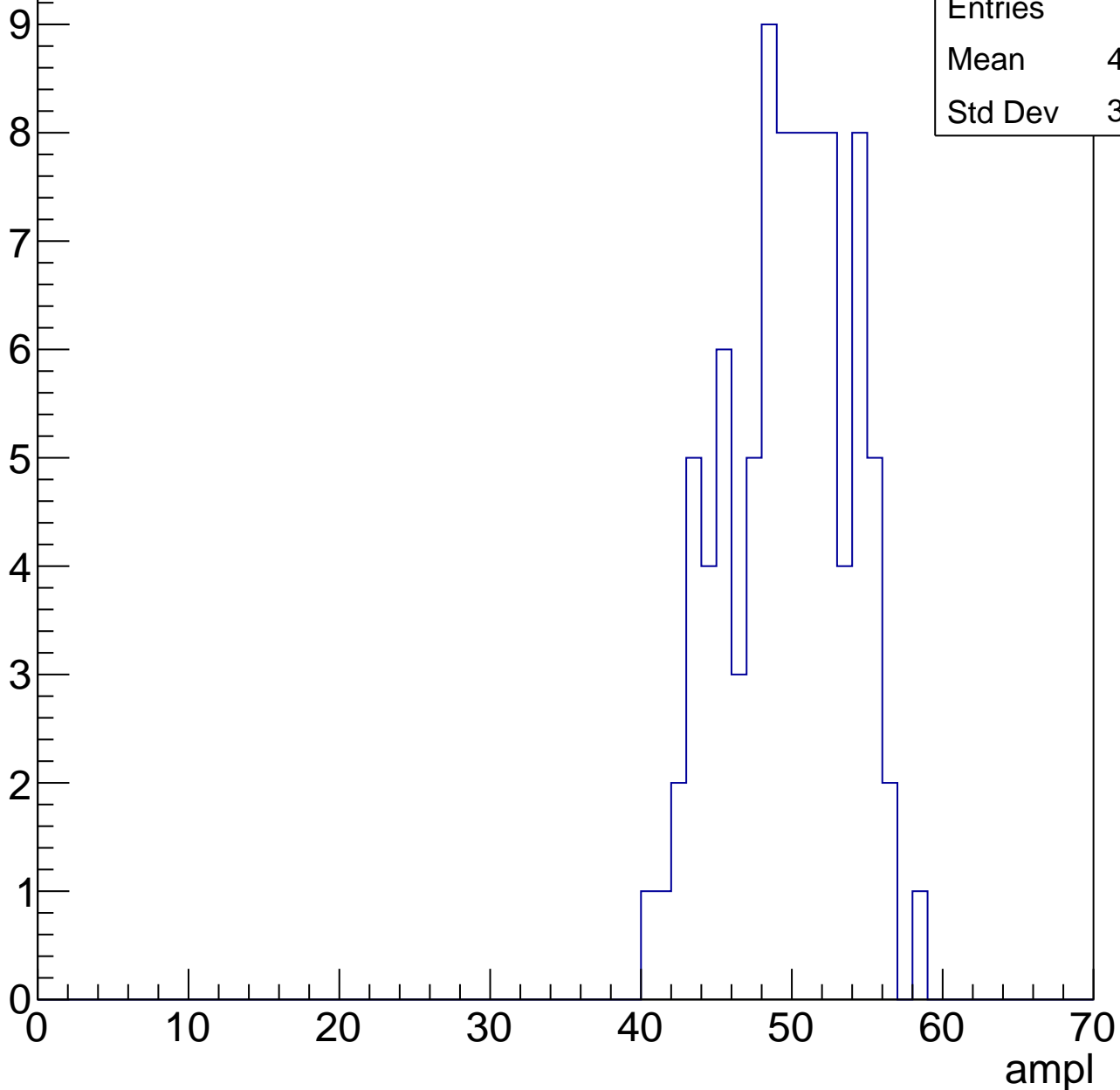


# B1L003S, U23-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

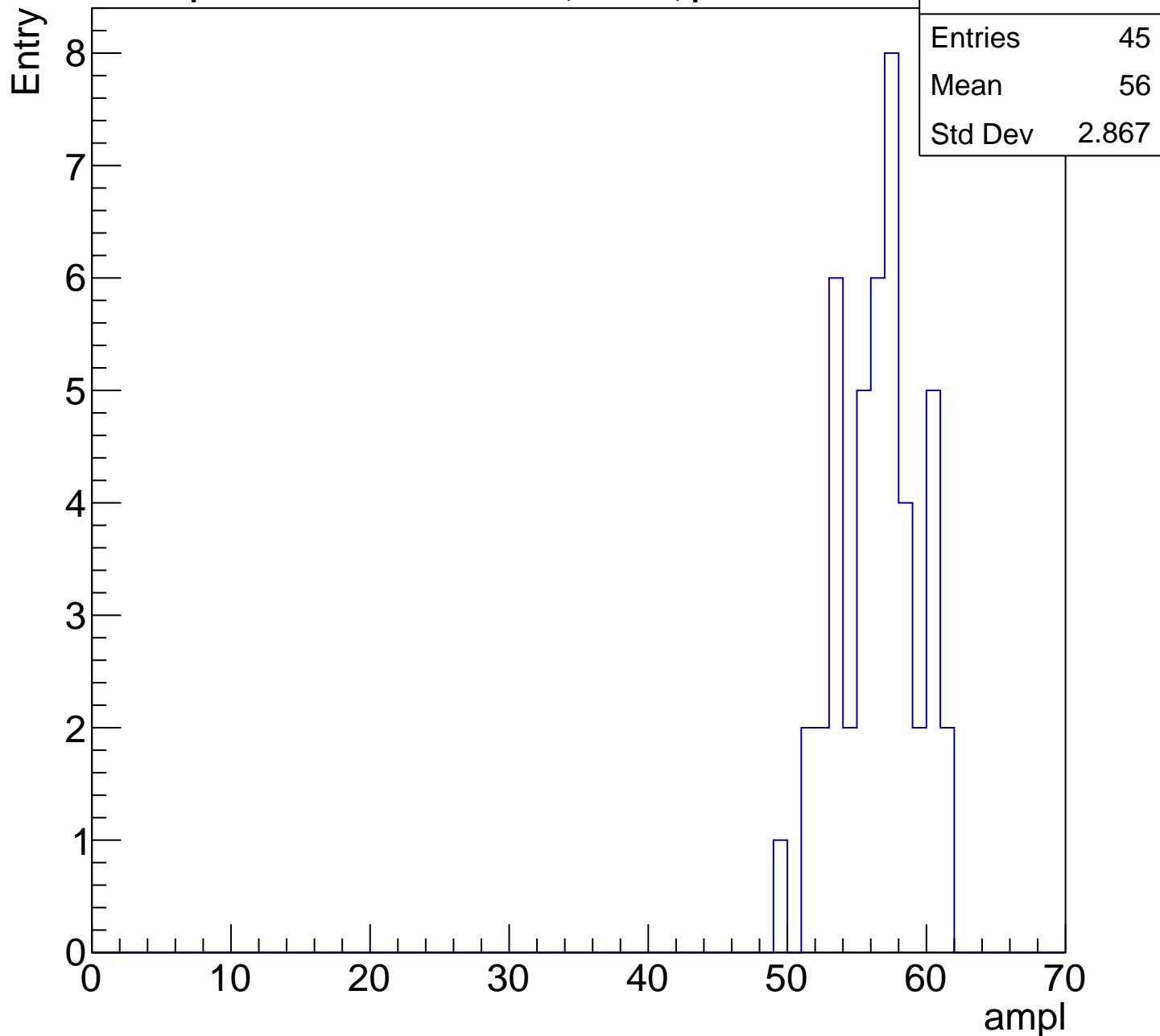
Entry

Entries	88
Mean	49.27
Std Dev	3.994



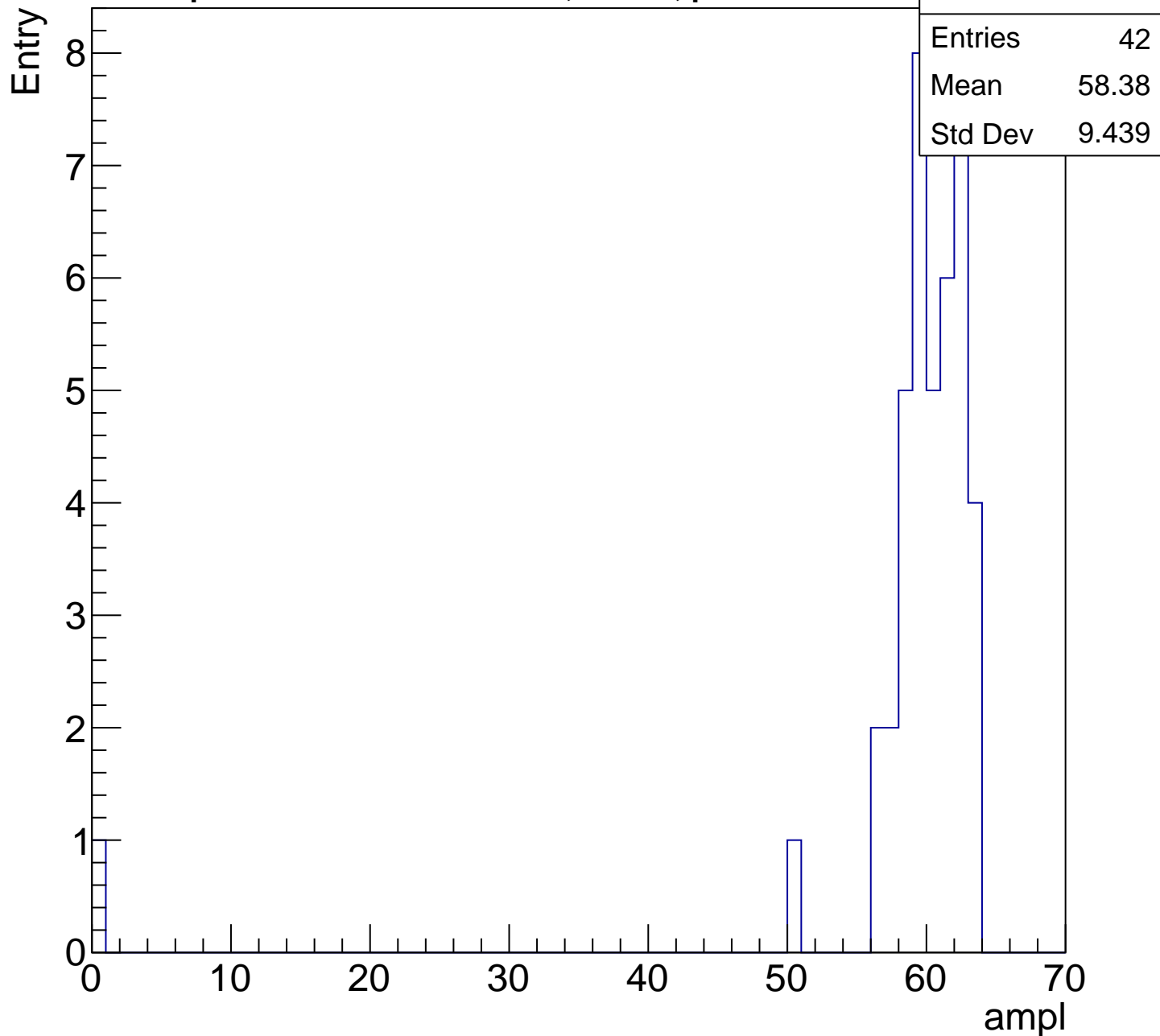
# B1L003S, U23-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2



# B1L003S, U23-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

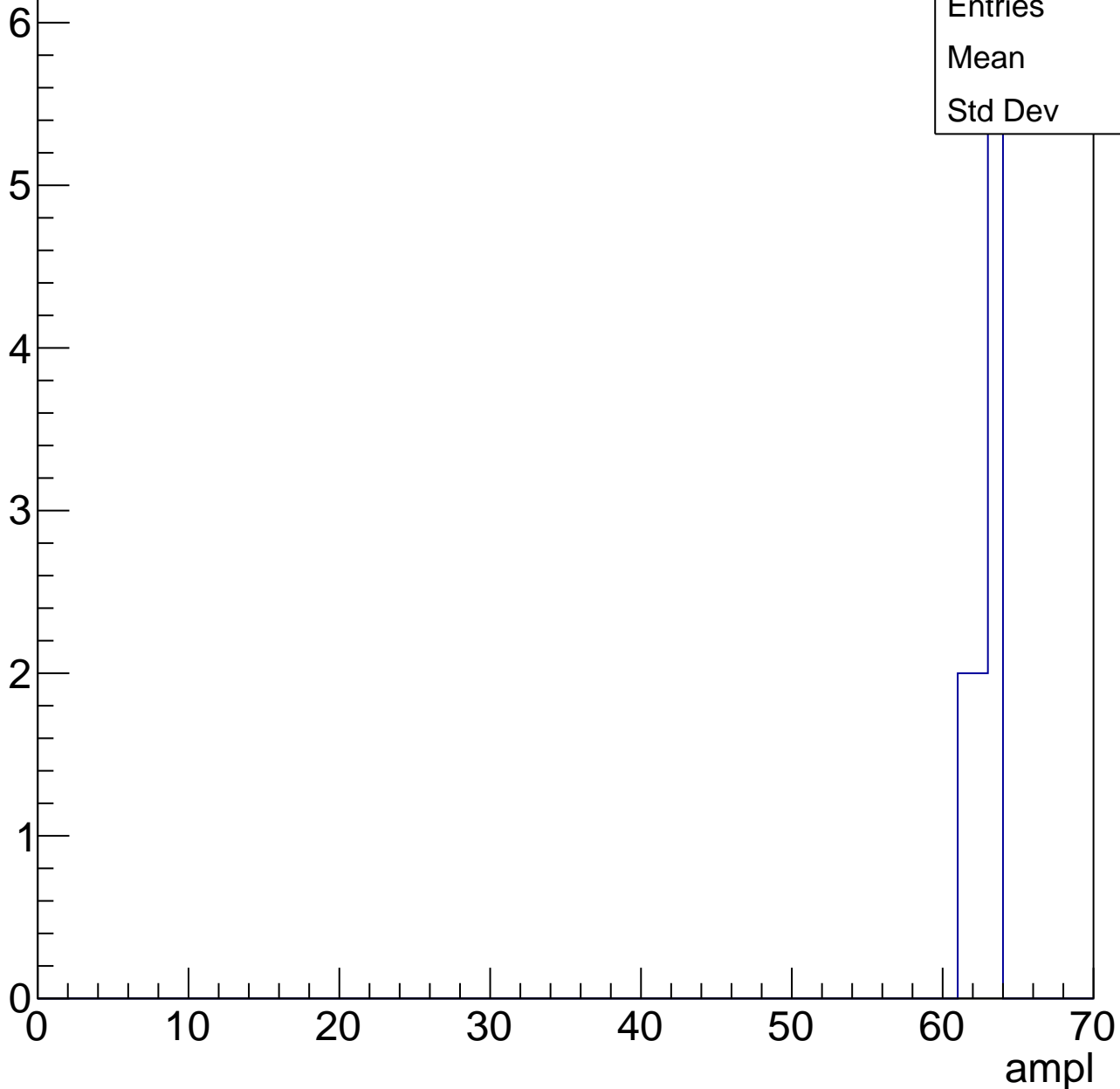


# B1L003S, U23-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	10
Mean	62.4
Std Dev	0.8





# B1L003S, U23-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	83
Mean	28.43
Std Dev	4.539

**Gaus mean : 29.5212**

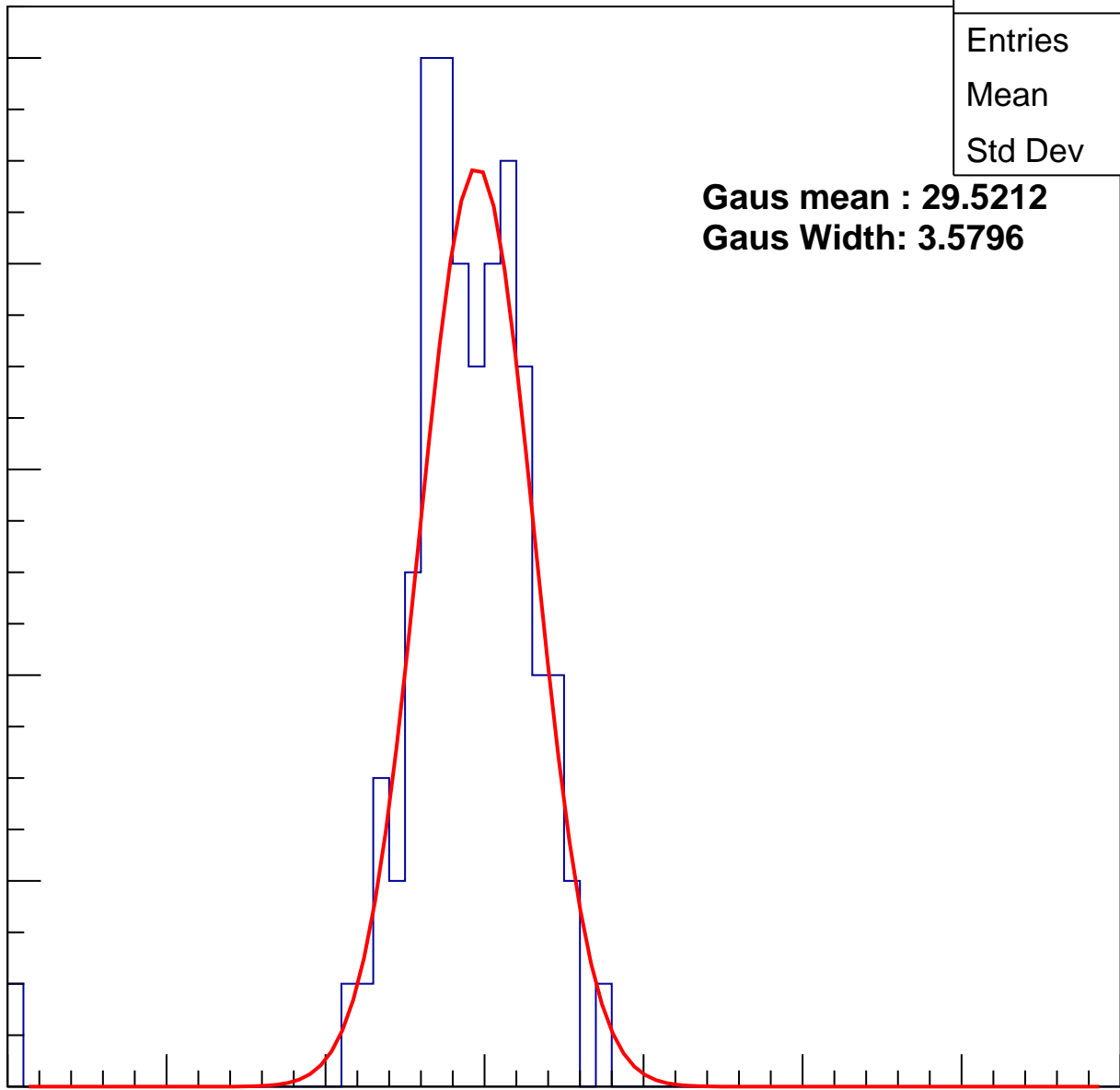
**Gaus Width: 3.5796**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch47, adc1

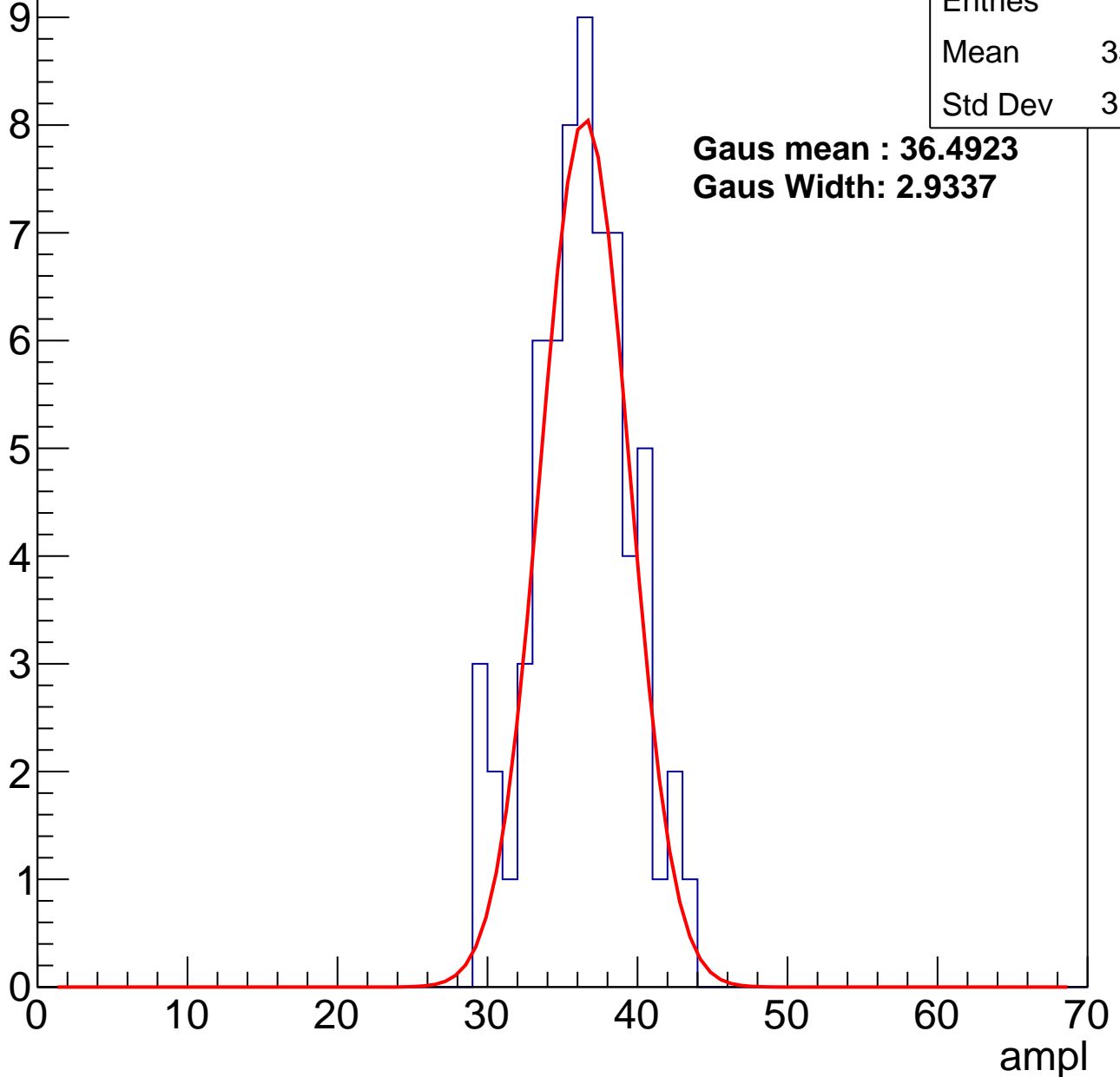
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.83
Std Dev	3.218

**Gaus mean : 36.4923**

**Gaus Width: 2.9337**



# B1L003S, U23-ch47, adc2

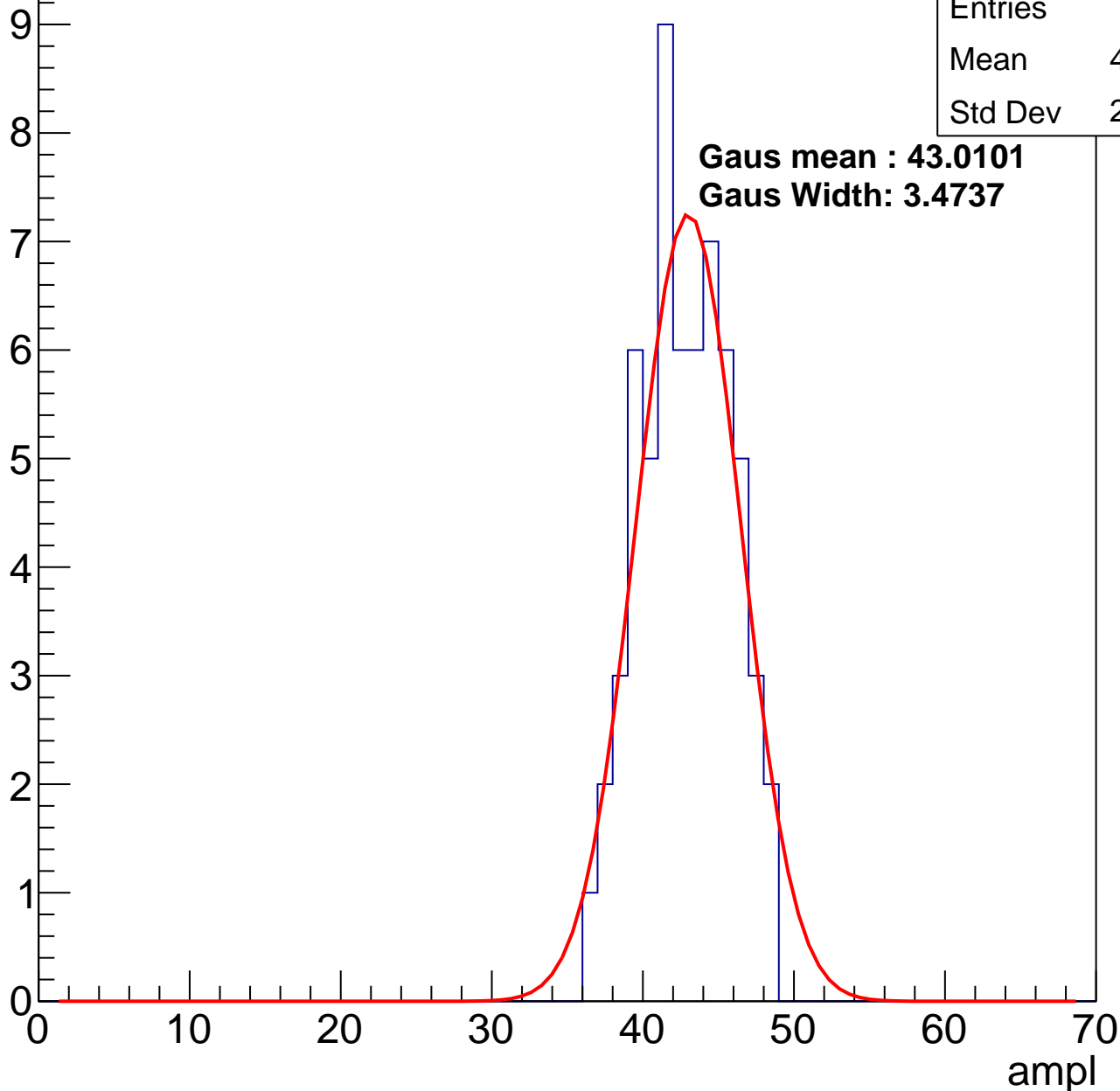
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.33
Std Dev	2.935

**Gaus mean : 43.0101**

**Gaus Width: 3.4737**



# B1L003S, U23-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

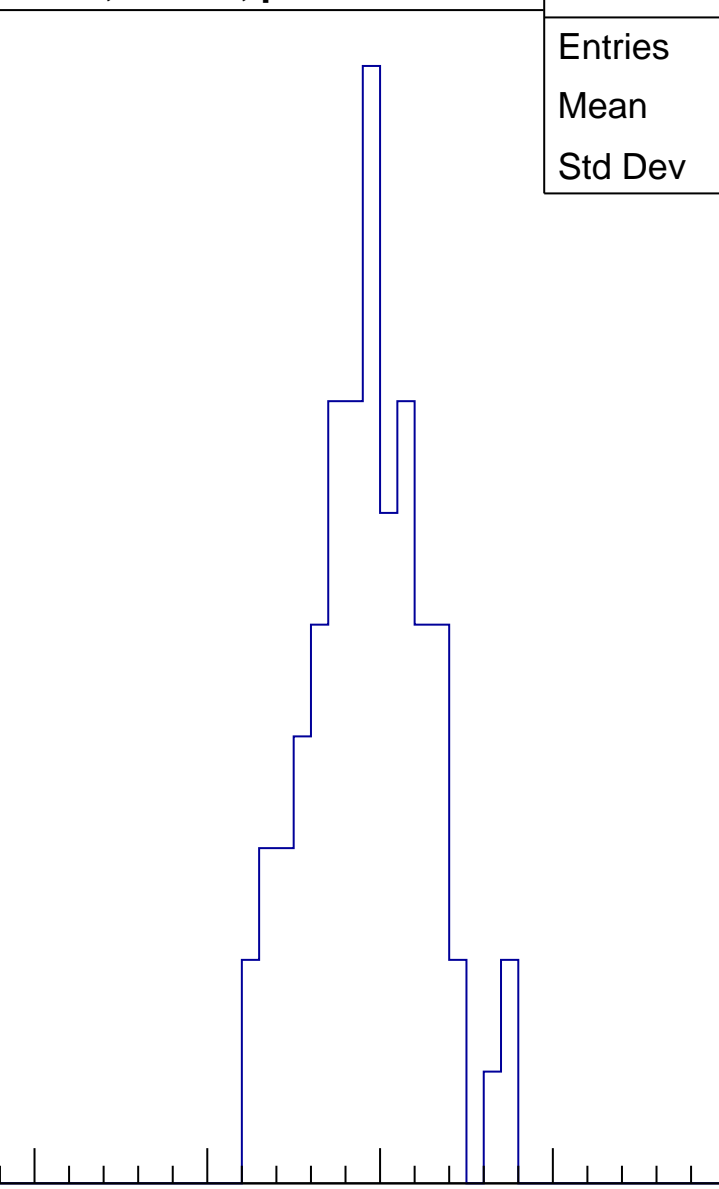
Entries	69
Mean	48.84
Std Dev	3.425

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

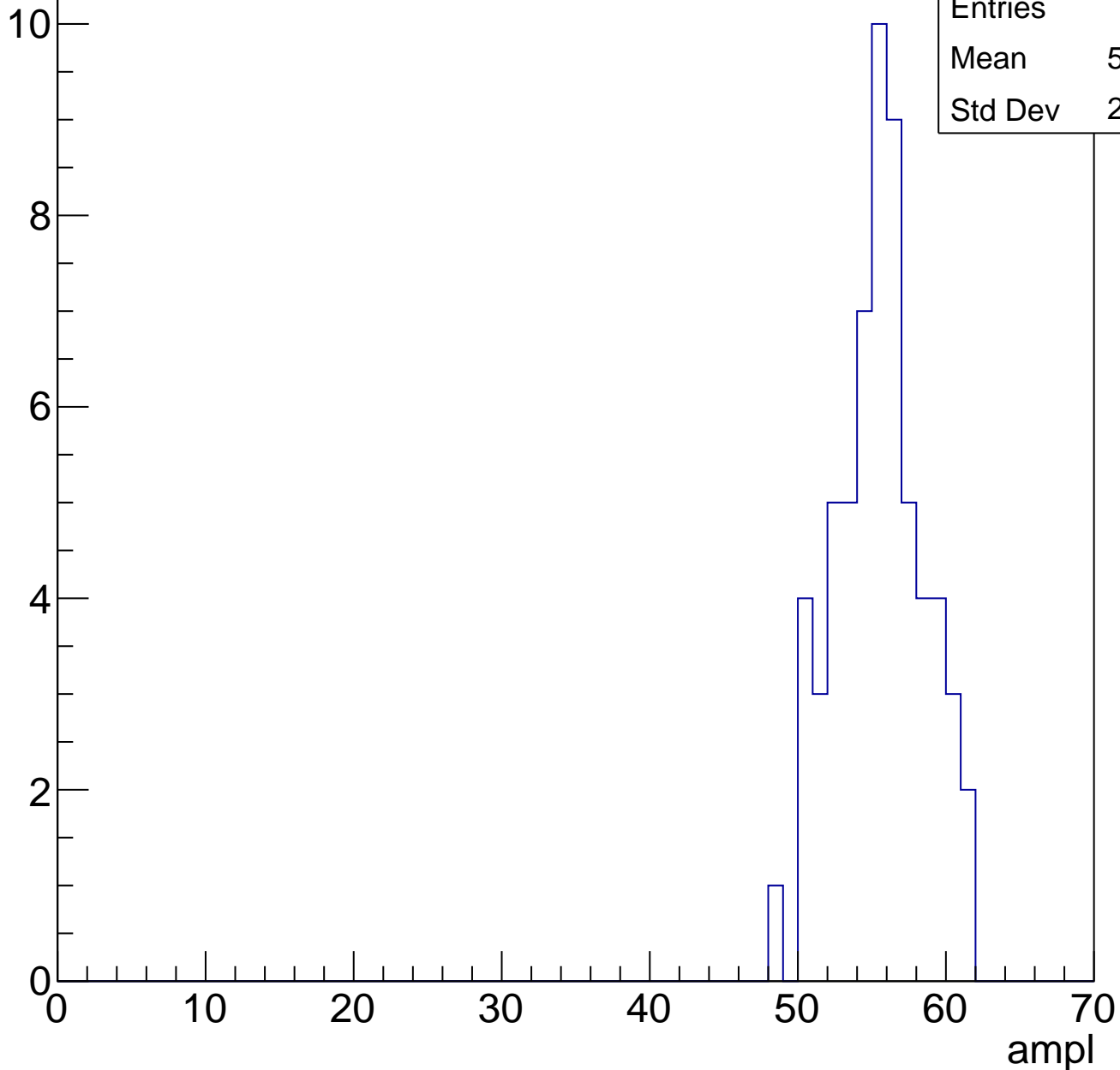


# B1L003S, U23-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	62
Mean	55.05
Std Dev	2.964

Entry

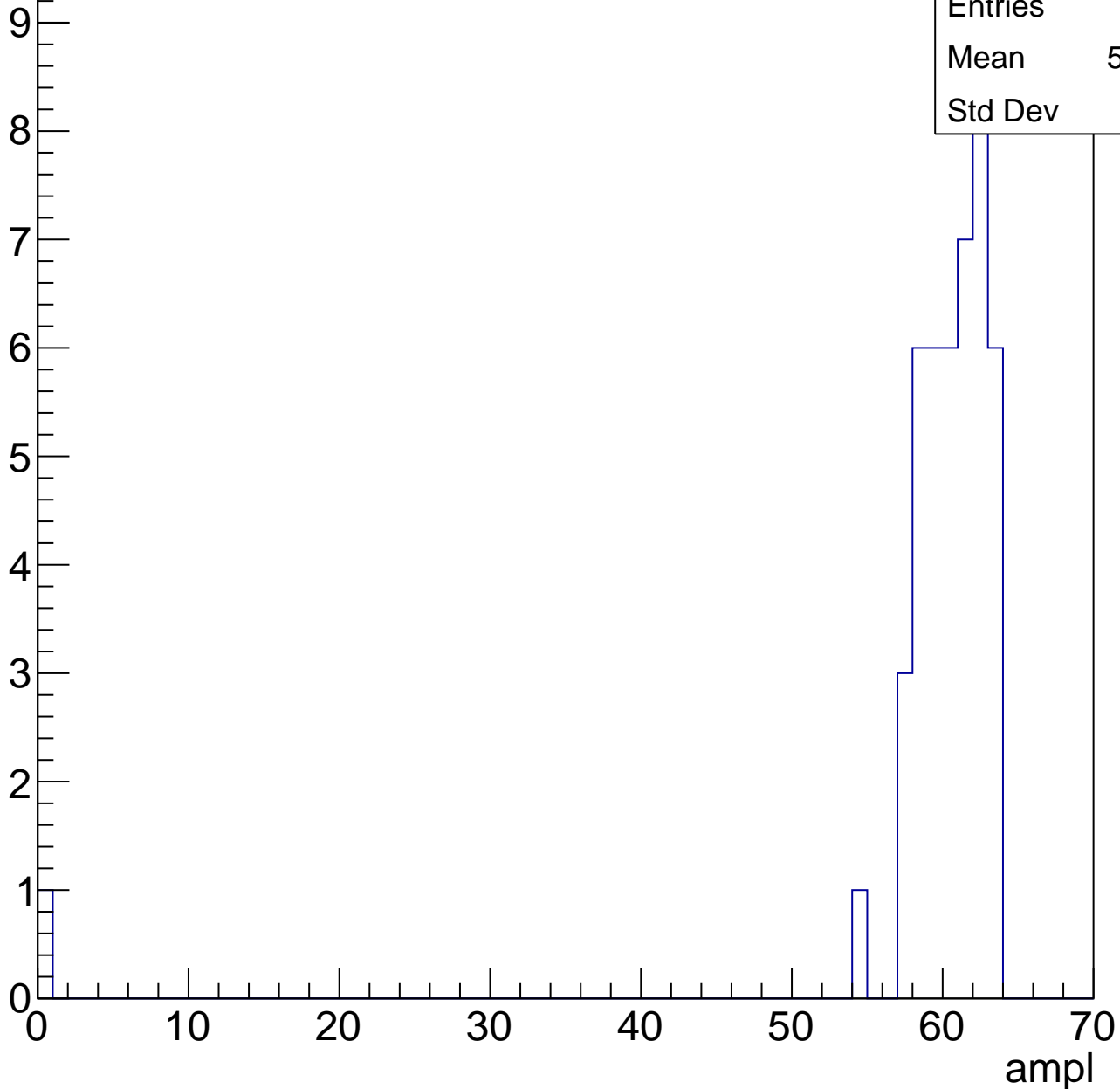


# B1L003S, U23-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

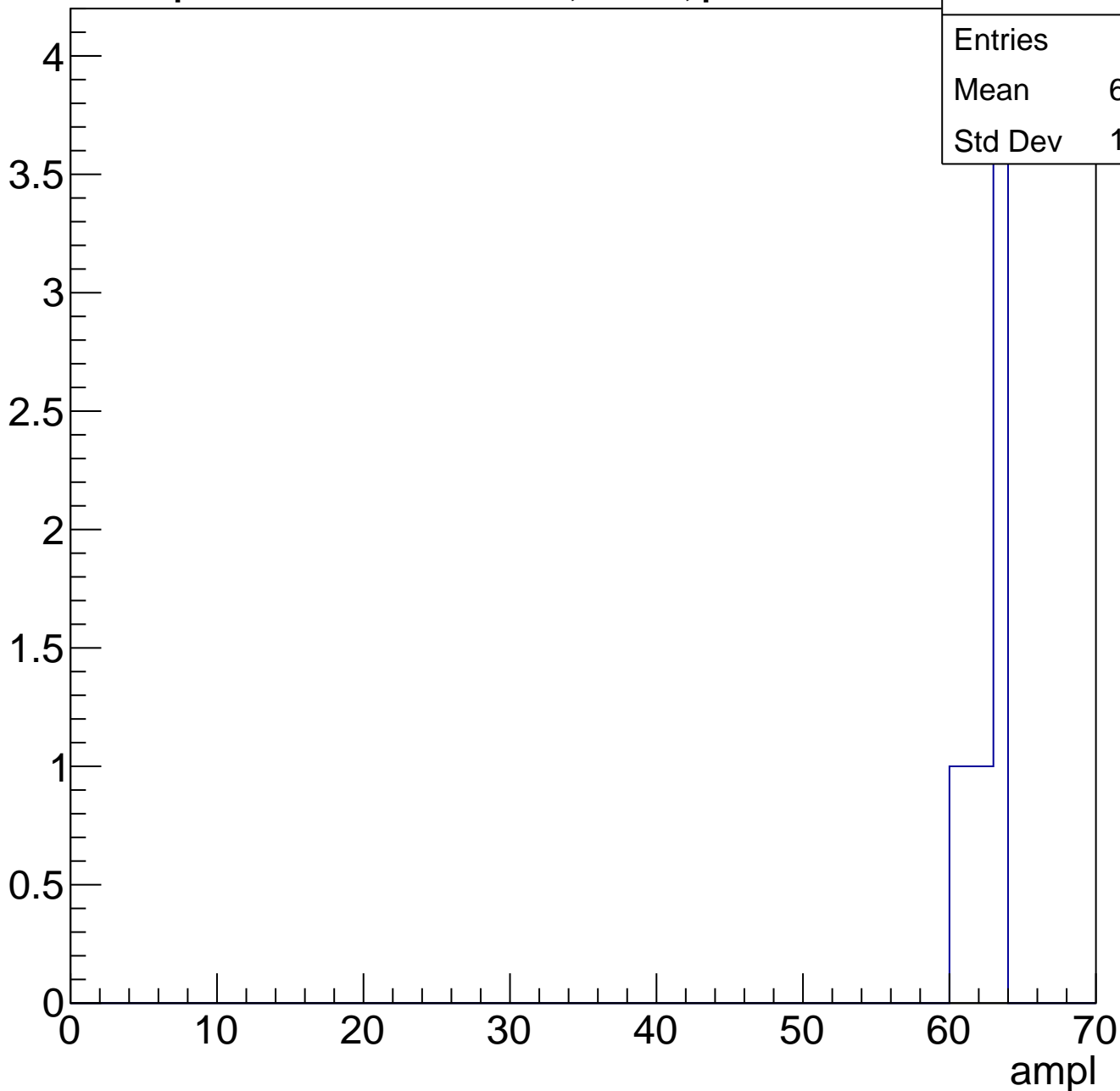
Entries	45
Mean	58.89
Std Dev	9.11



# B1L003S, U23-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch48, adc0

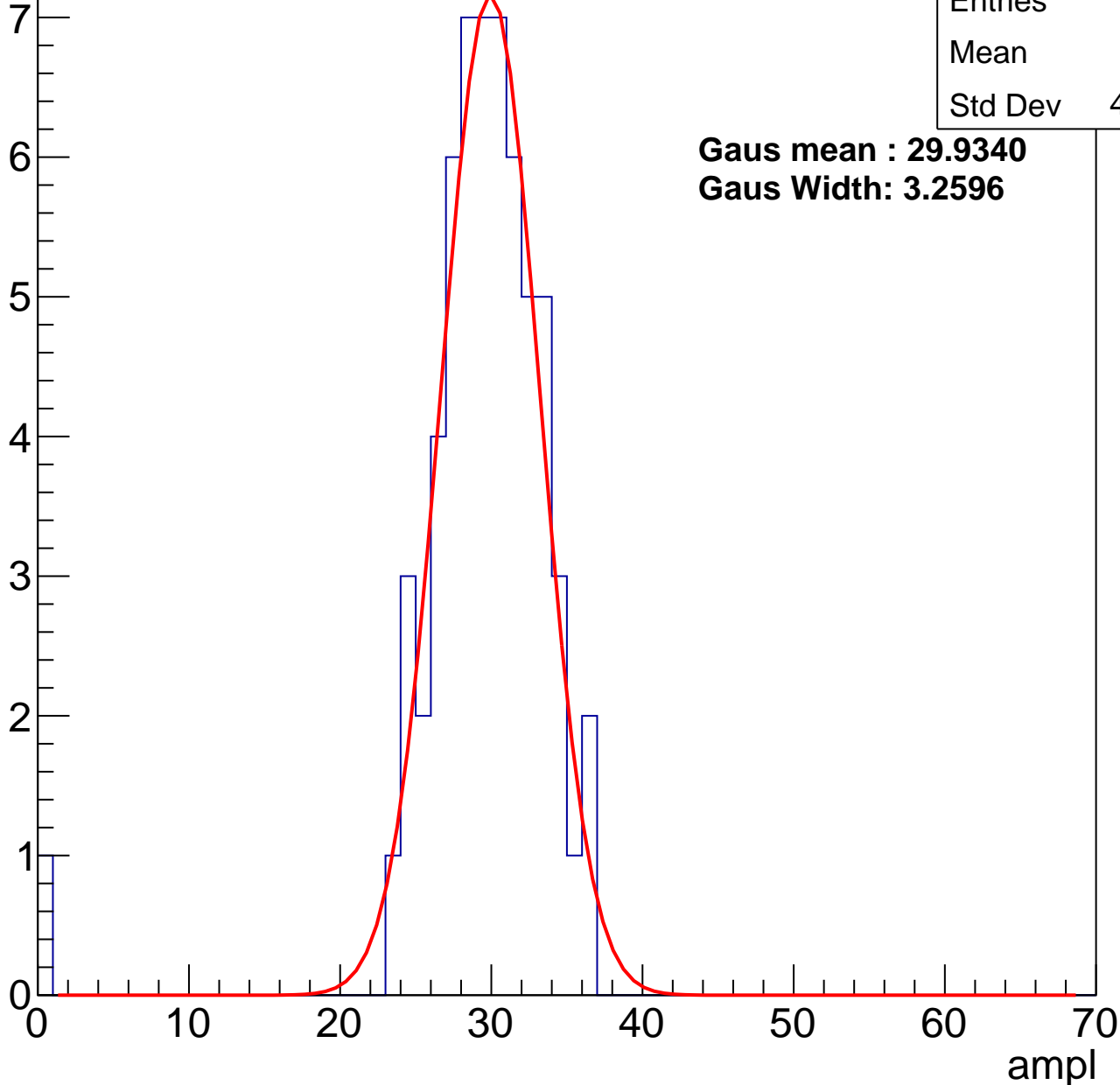
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29
Std Dev	4.858

**Gaus mean : 29.9340**

**Gaus Width: 3.2596**



# B1L003S, U23-ch48, adc1

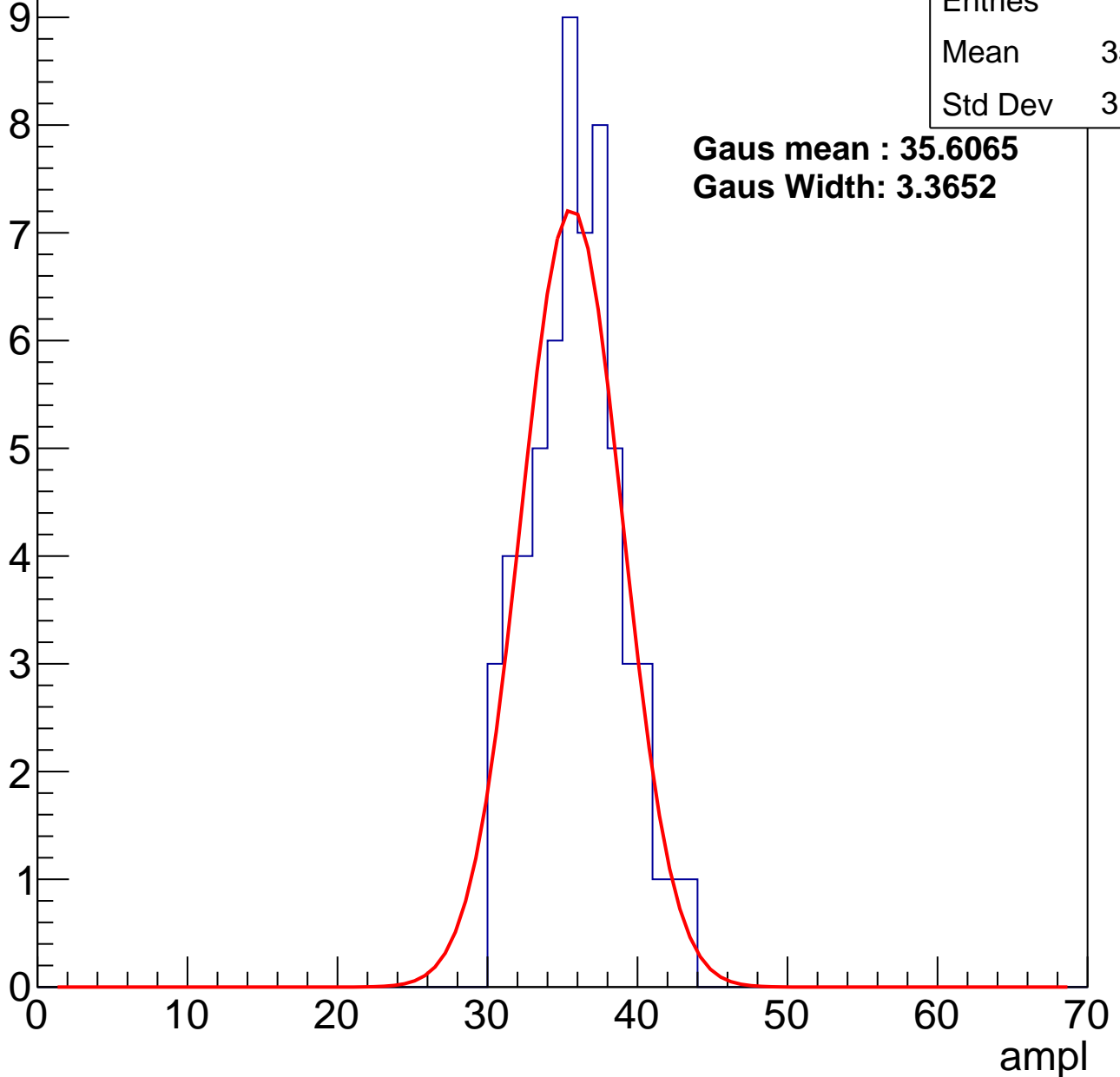
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.45
Std Dev	3.013

**Gaus mean : 35.6065**

**Gaus Width: 3.3652**



# B1L003S, U23-ch48, adc2

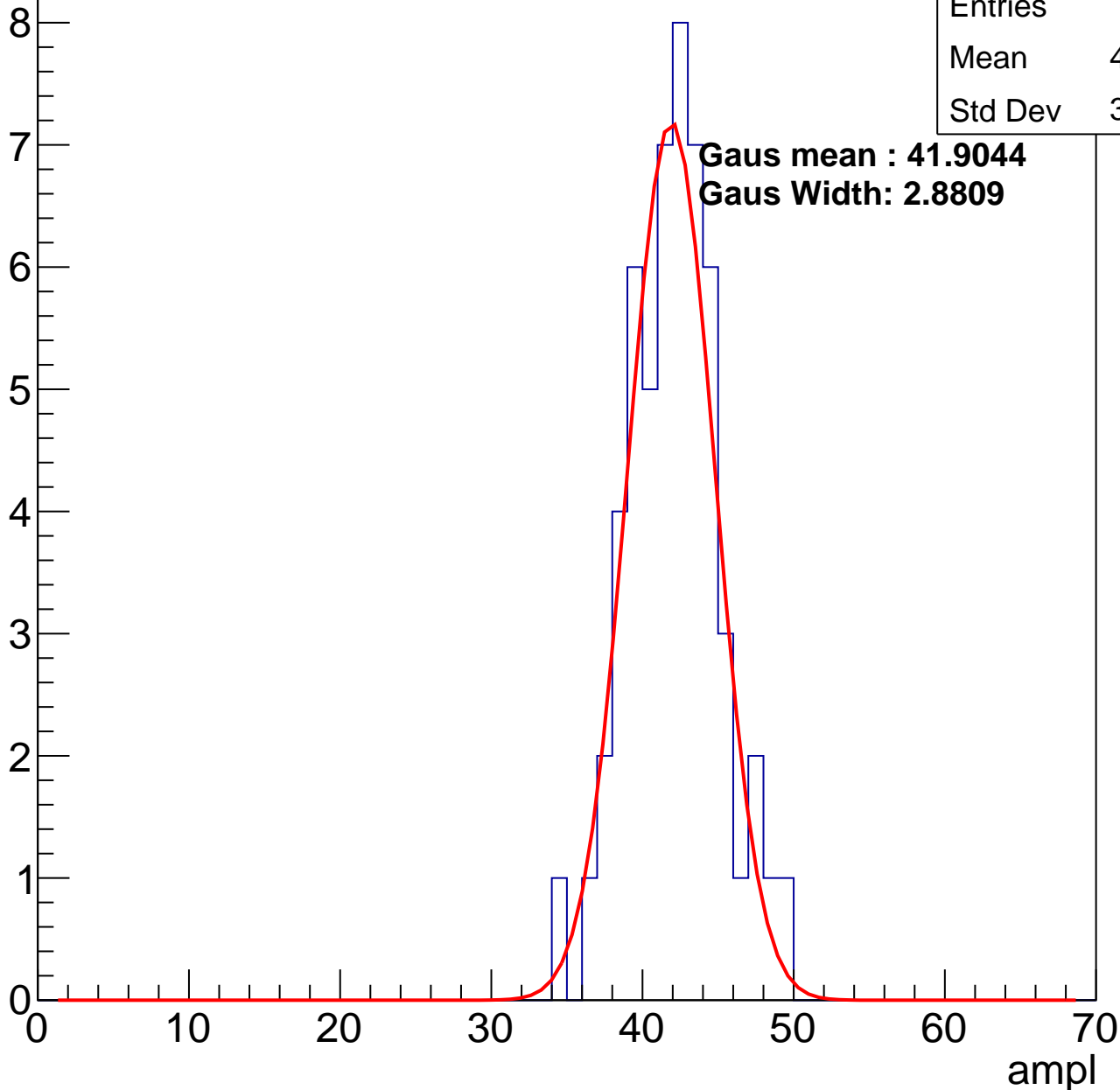
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	41.64
Std Dev	3.005

**Gaus mean : 41.9044**

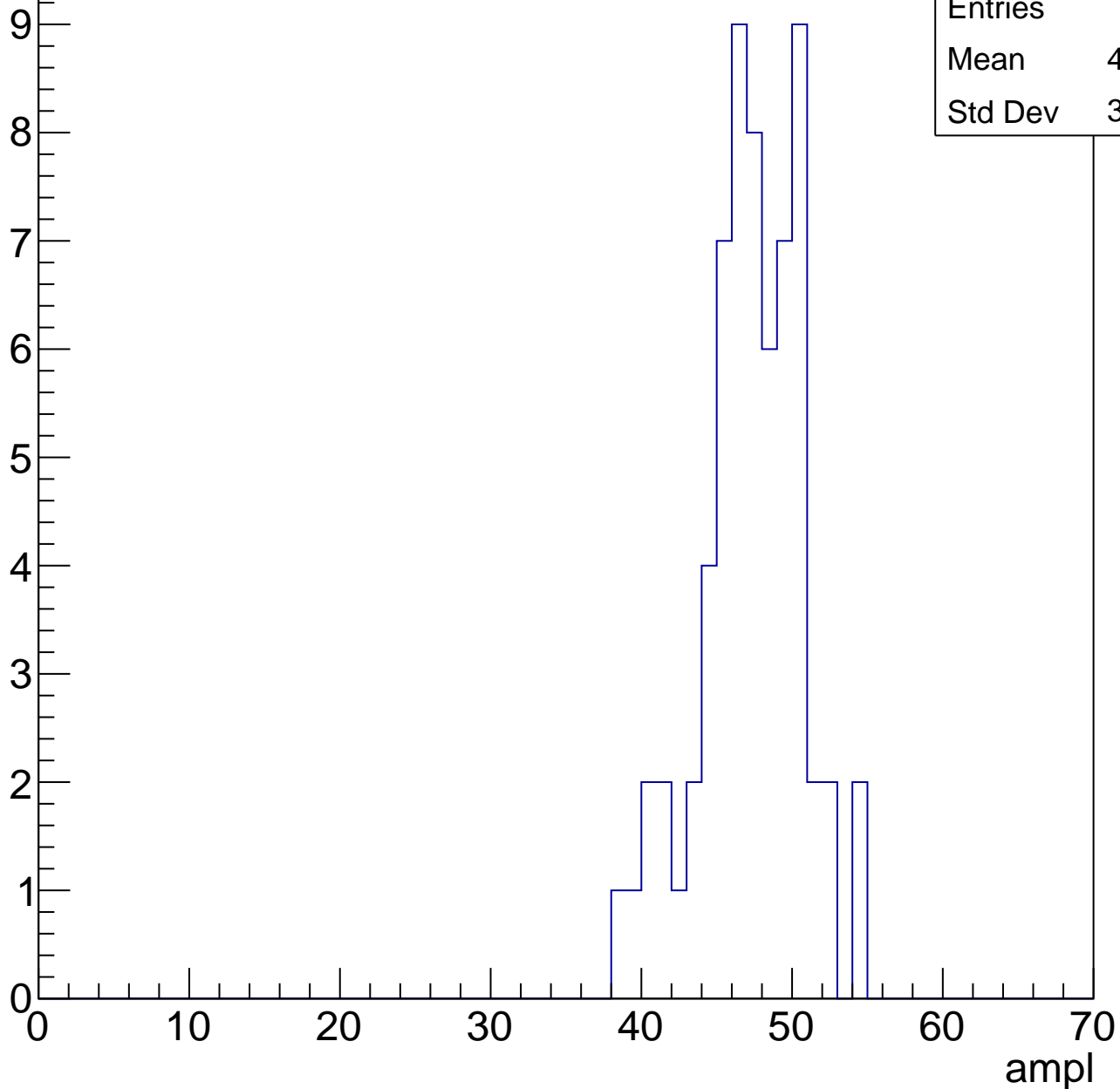
**Gaus Width: 2.8809**



# B1L003S, U23-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



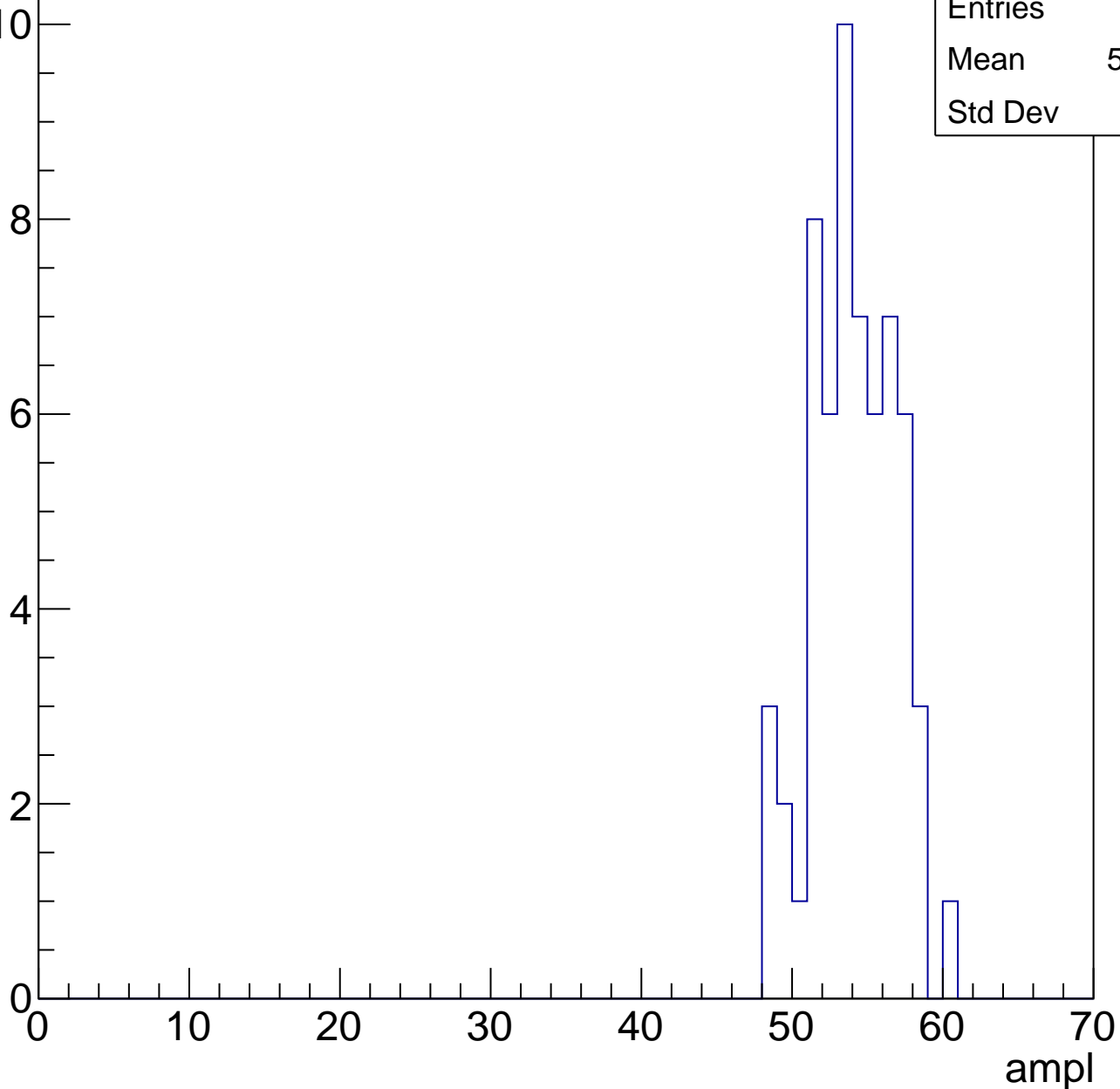
Entries	65
Mean	46.82
Std Dev	3.369

# B1L003S, U23-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	53.63
Std Dev	2.72

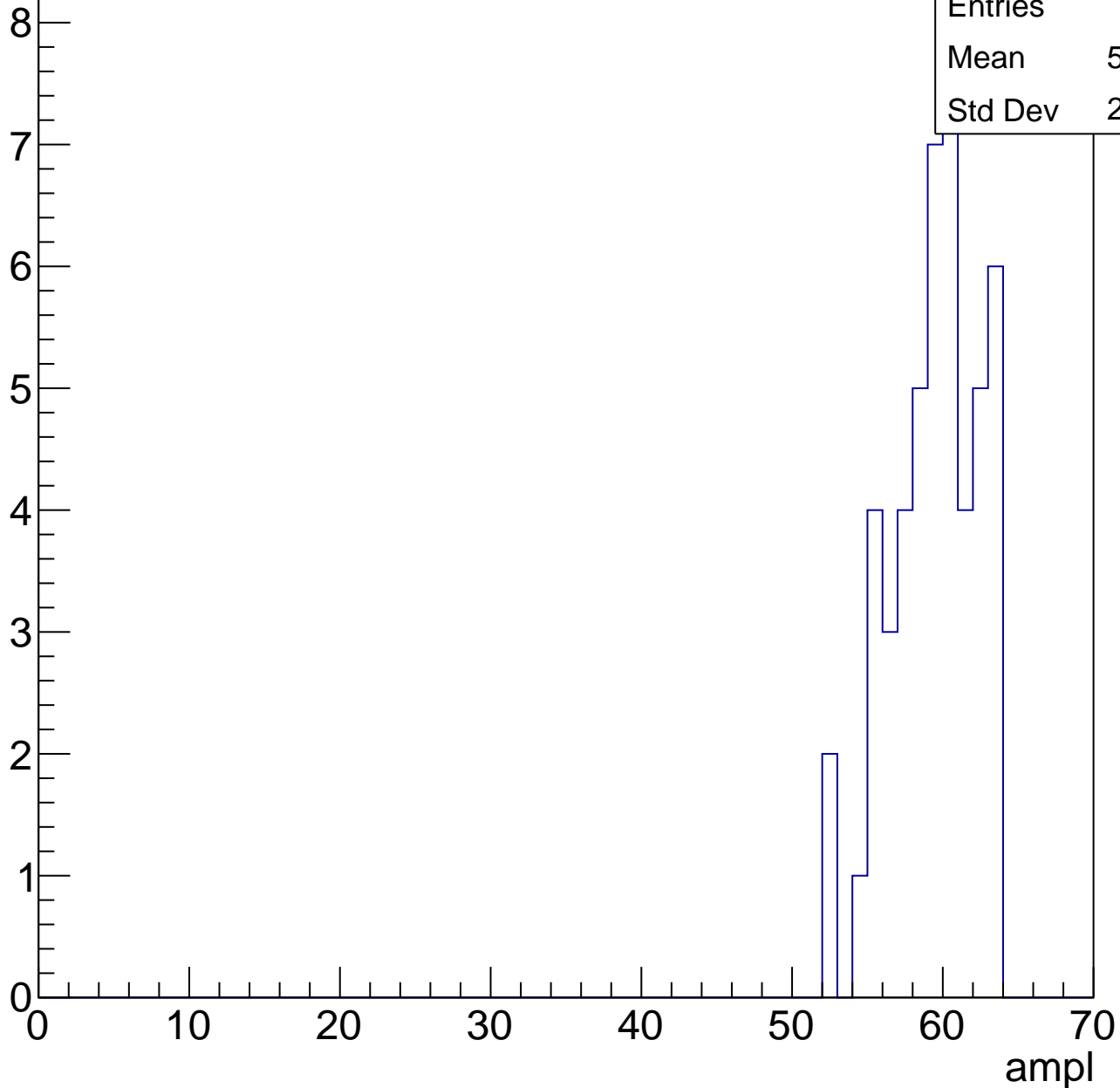


# B1L003S, U23-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

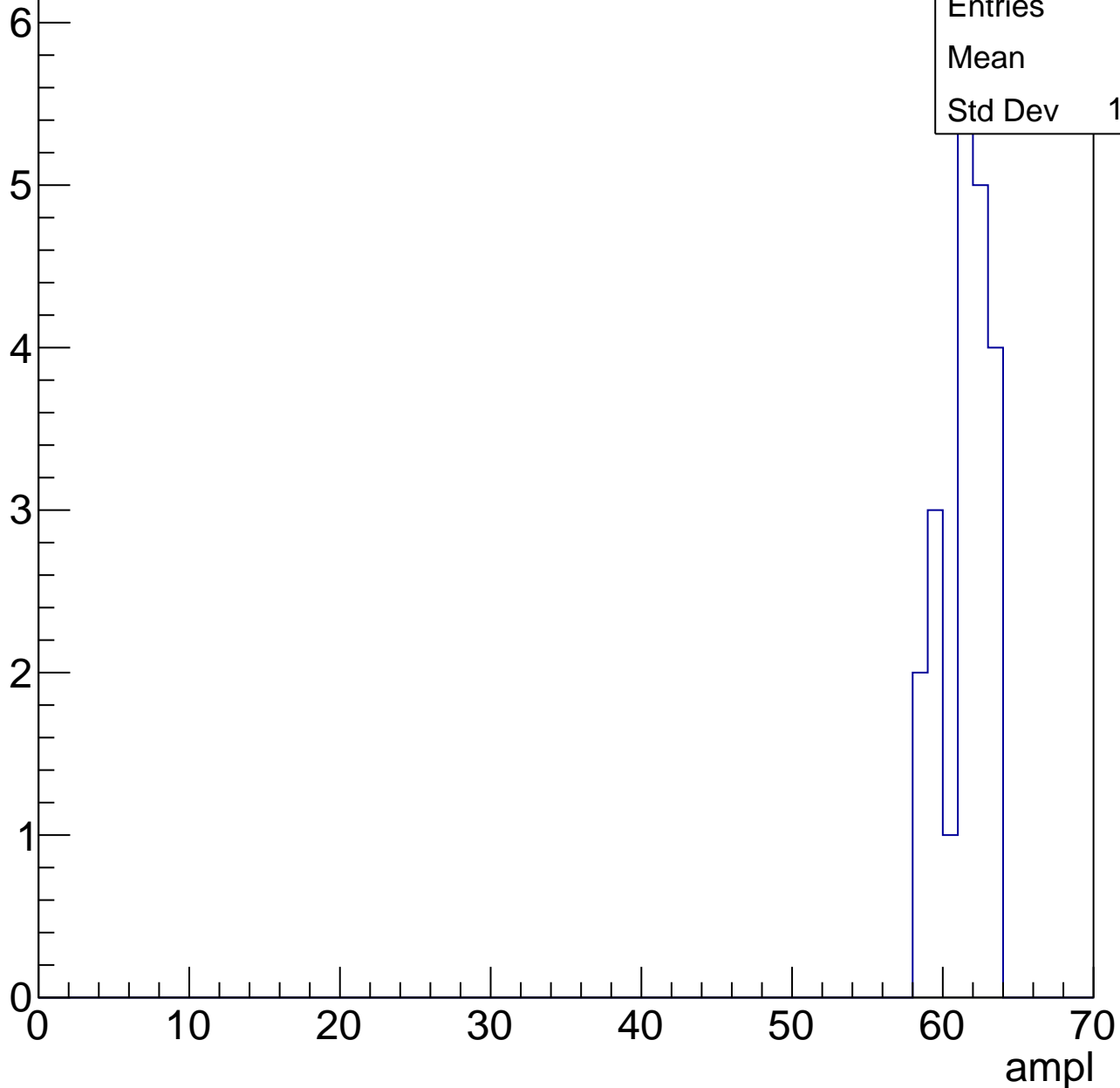
Entries	49
Mean	58.96
Std Dev	2.857



# B1L003S, U23-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

# B1L003S, U23-ch49, adc0

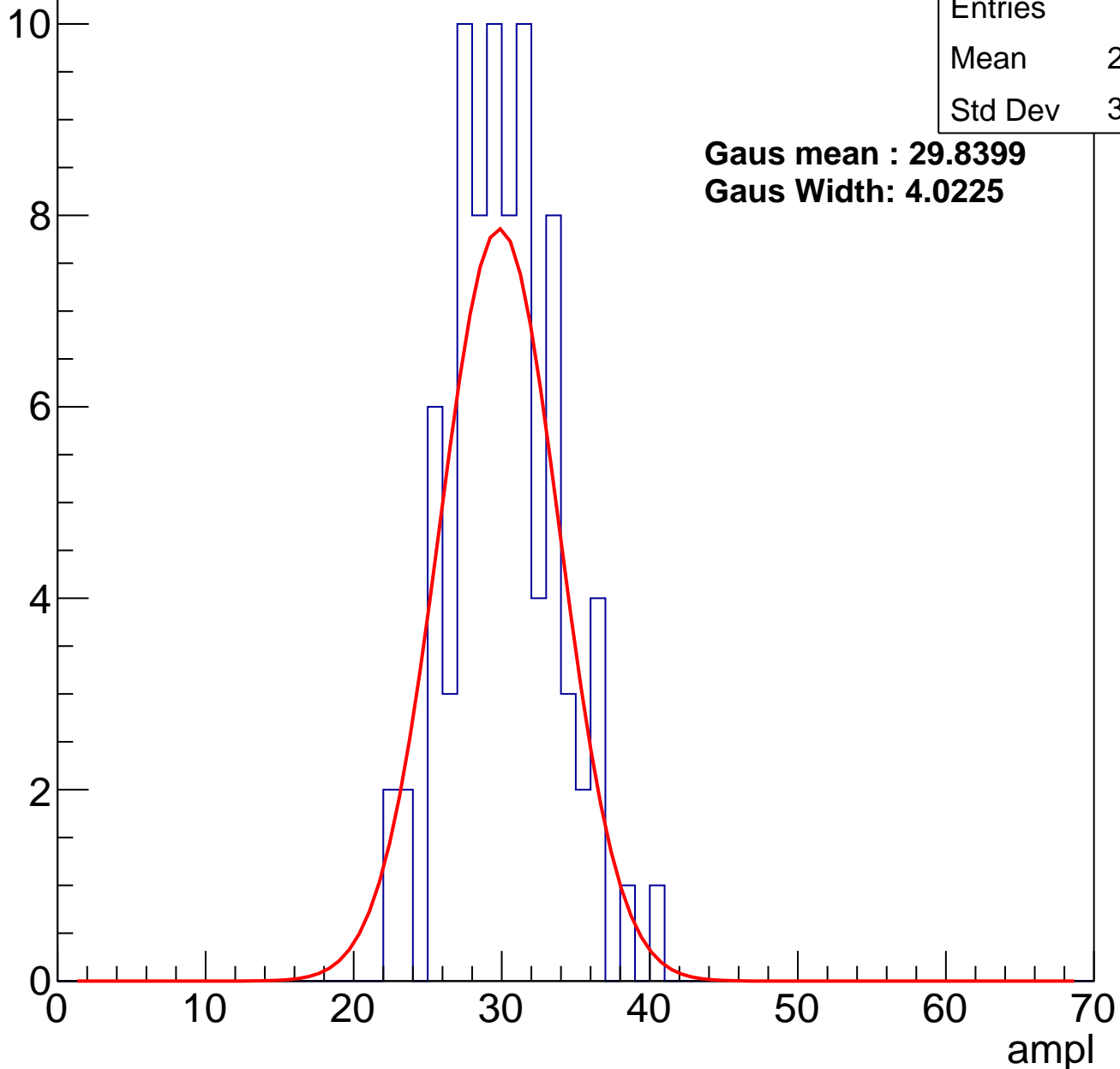
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	82
Mean	29.73
Std Dev	3.585

**Gaus mean : 29.8399**

**Gaus Width: 4.0225**

Entry



# B1L003S, U23-ch49, adc1

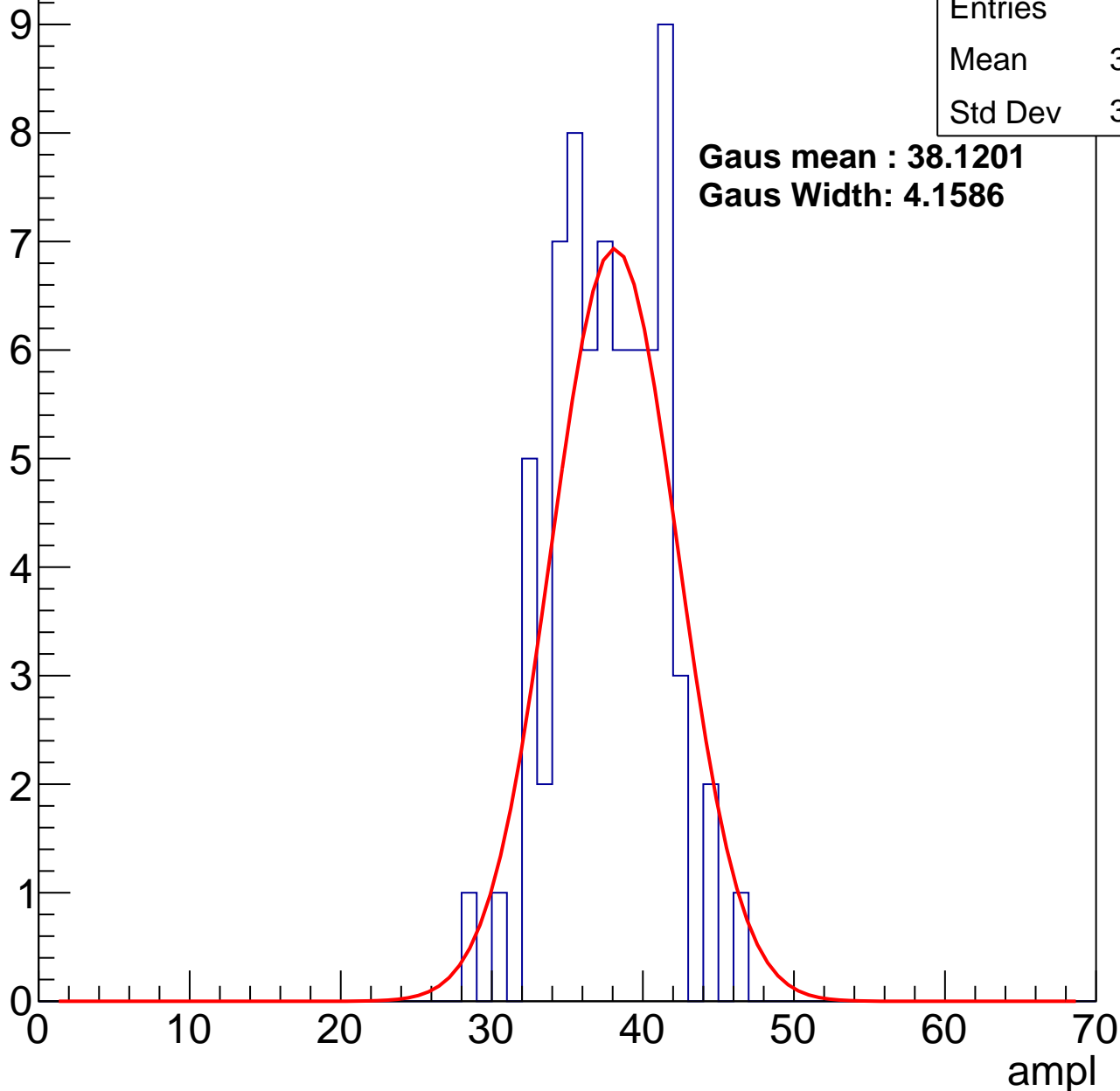
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	37.26
Std Dev	3.516

**Gaus mean : 38.1201**

**Gaus Width: 4.1586**



# B1L003S, U23-ch49, adc2

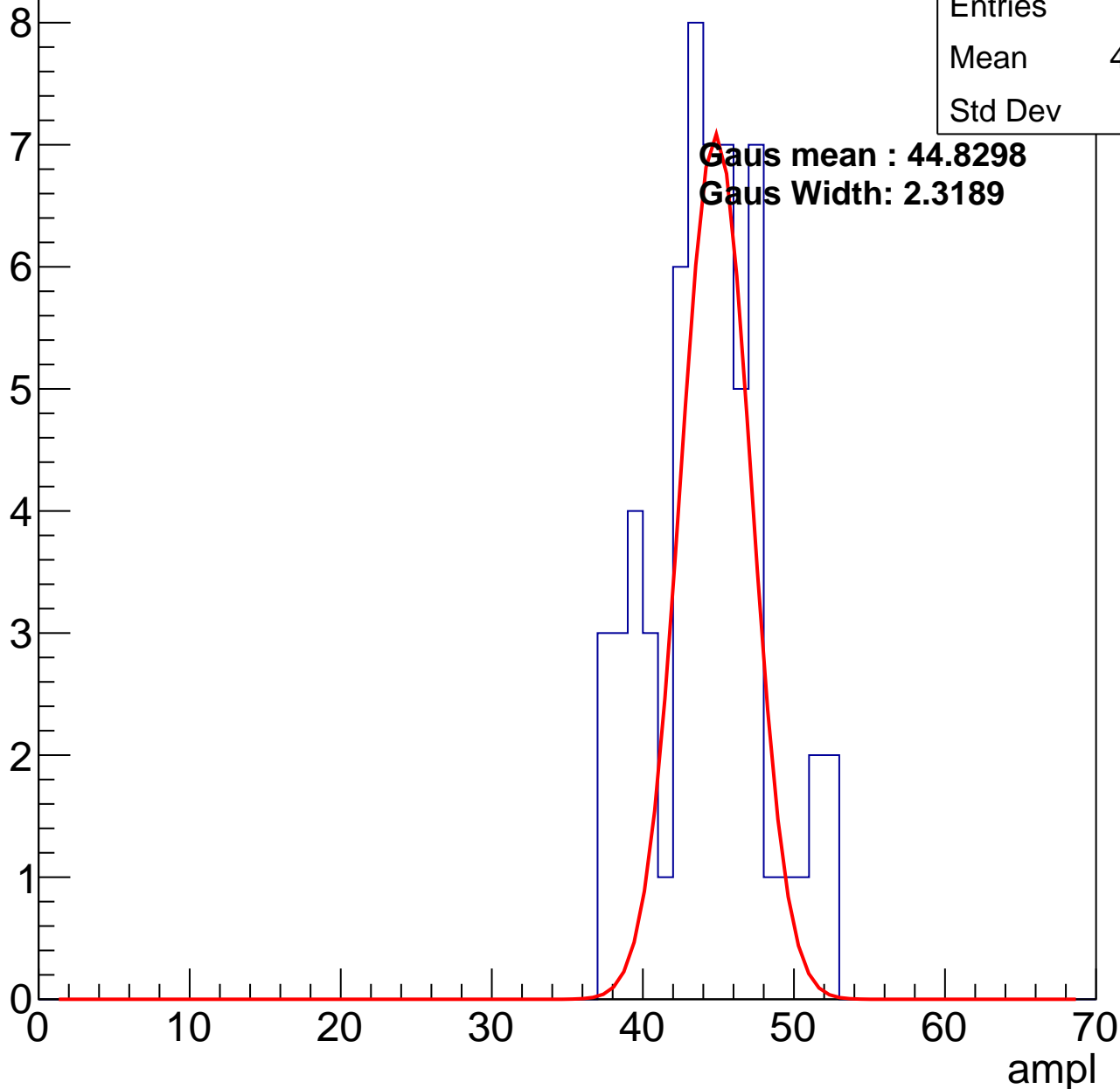
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.82
Std Dev	3.7

Gaus mean : 44.8298

Gaus Width: 2.3189

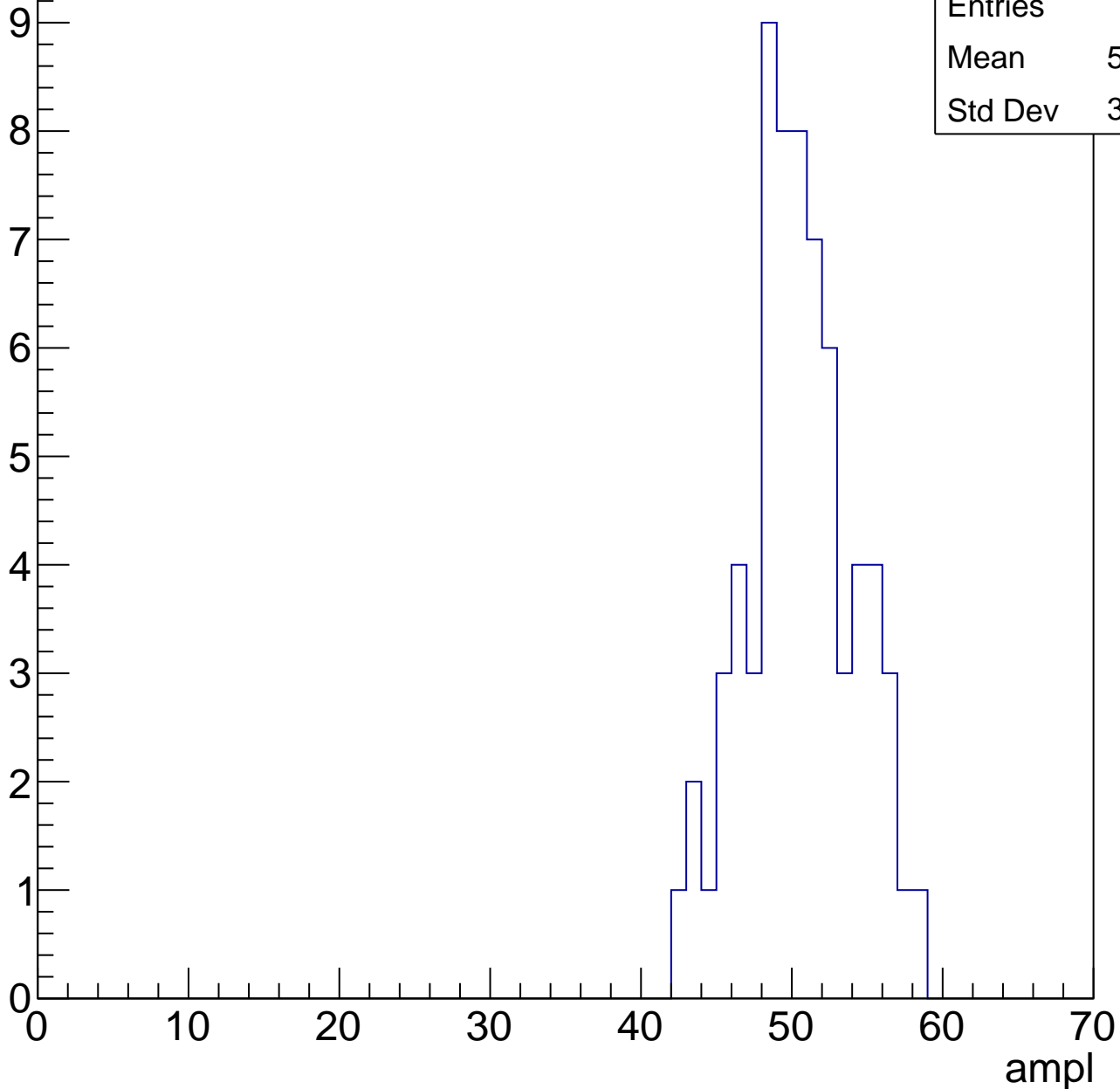


# B1L003S, U23-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	50.04
Std Dev	3.537

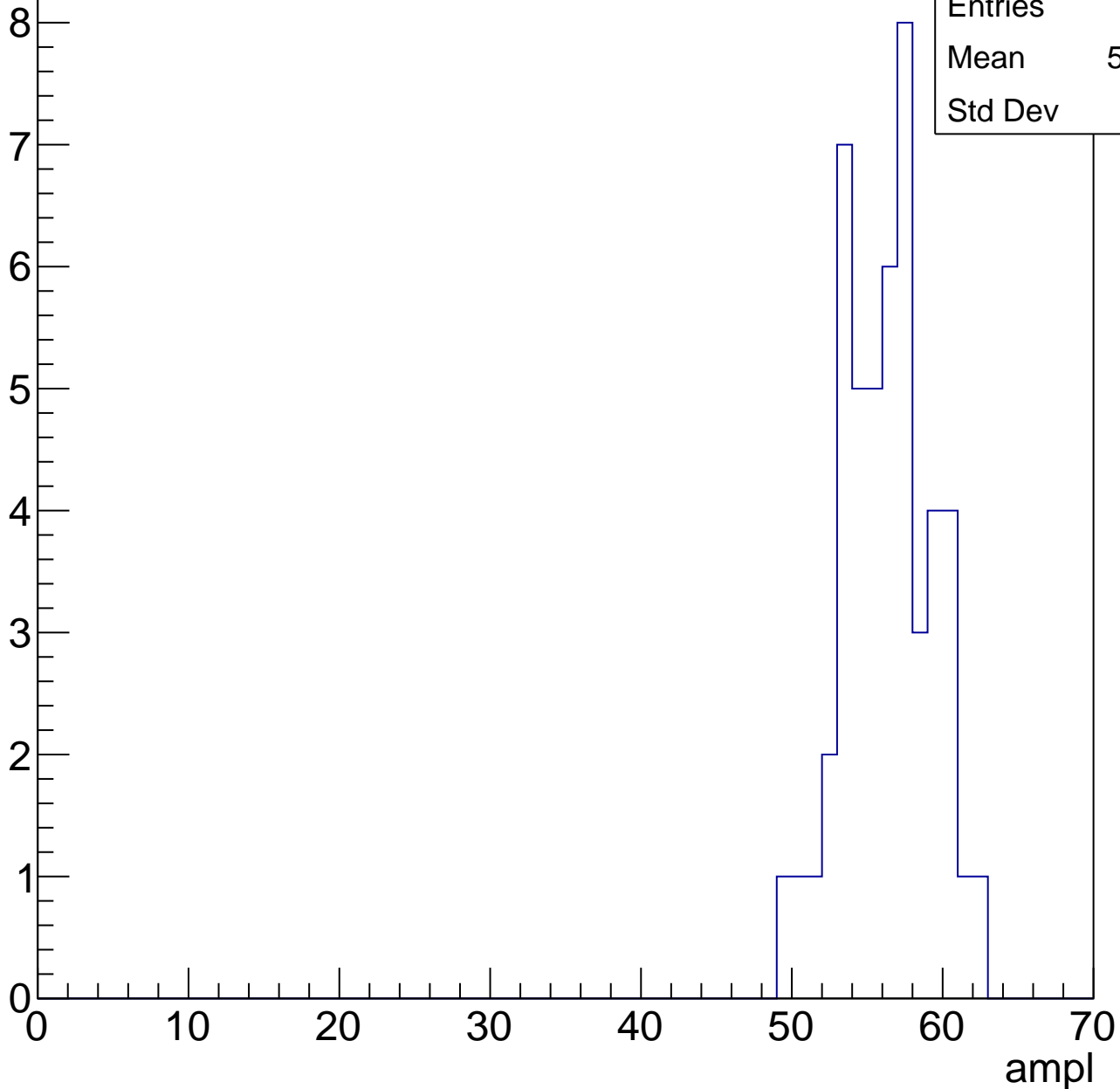


# B1L003S, U23-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55.82
Std Dev	2.89



# B1L003S, U23-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	59.06
Std Dev	8.891

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

# B1L003S, U23-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	85
Mean	29.24
Std Dev	3.52

**Gaus mean : 29.5257**

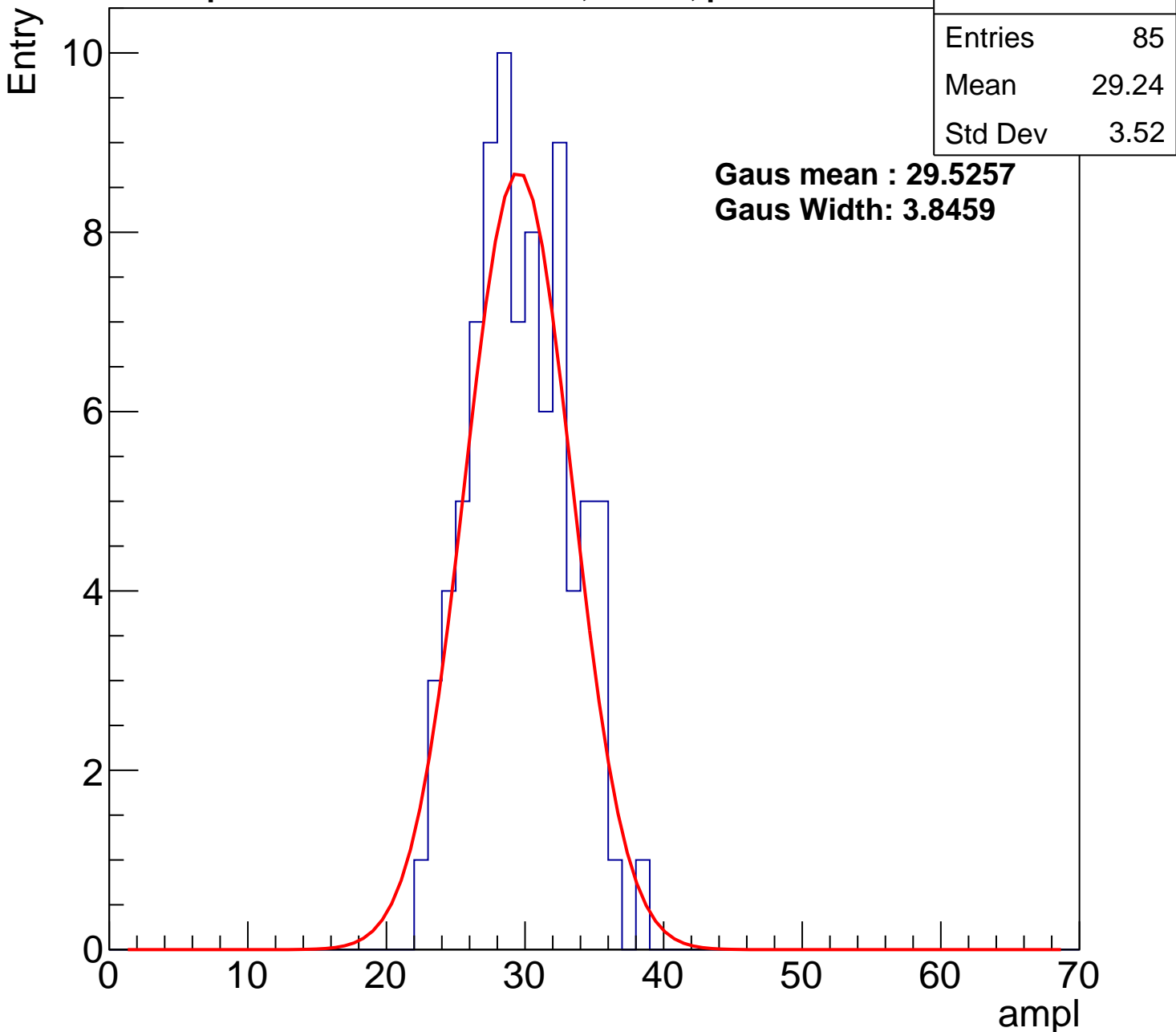
**Gaus Width: 3.8459**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch50, adc1

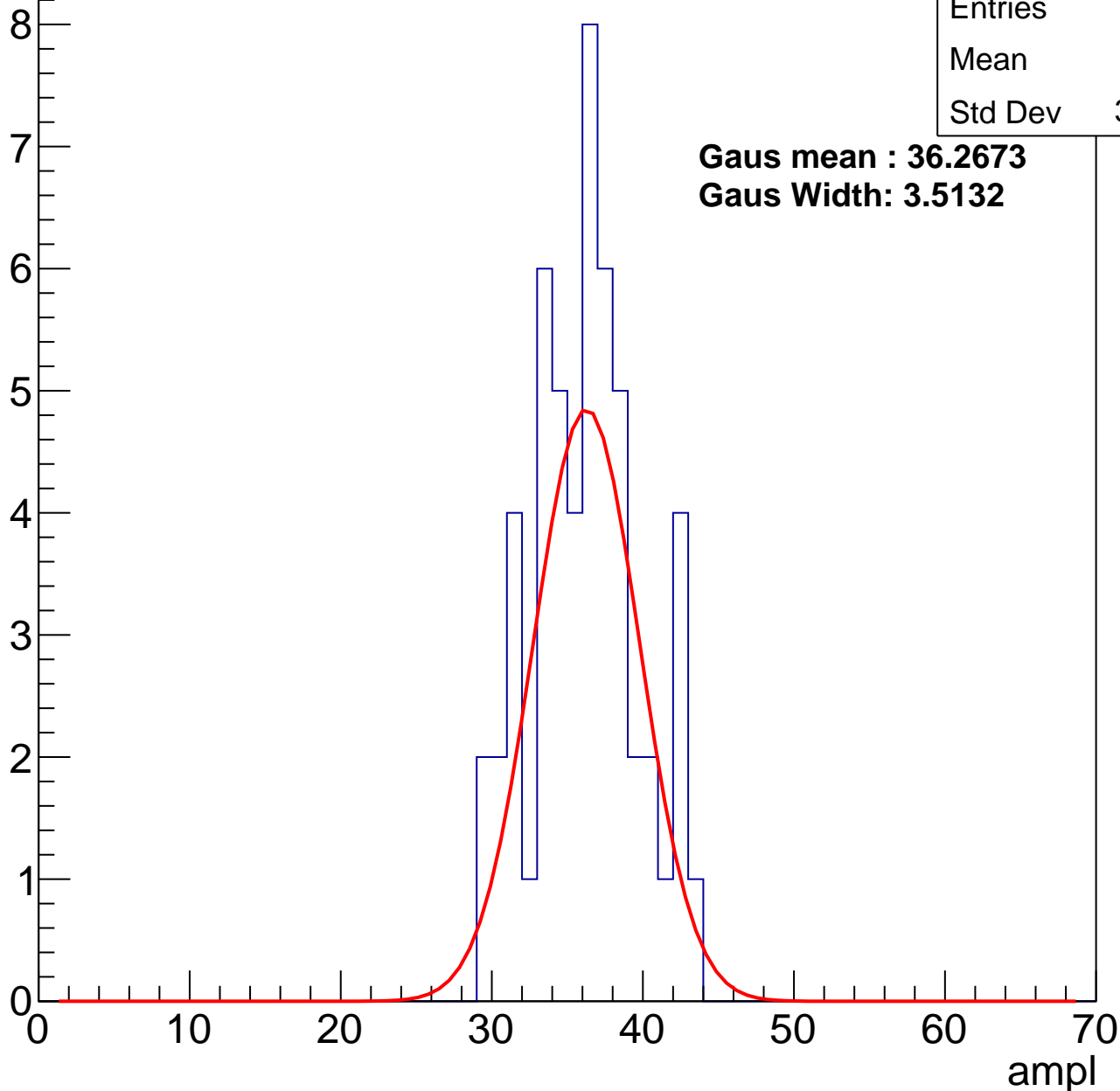
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.7
Std Dev	3.511

**Gaus mean : 36.2673**

**Gaus Width: 3.5132**



# B1L003S, U23-ch50, adc2

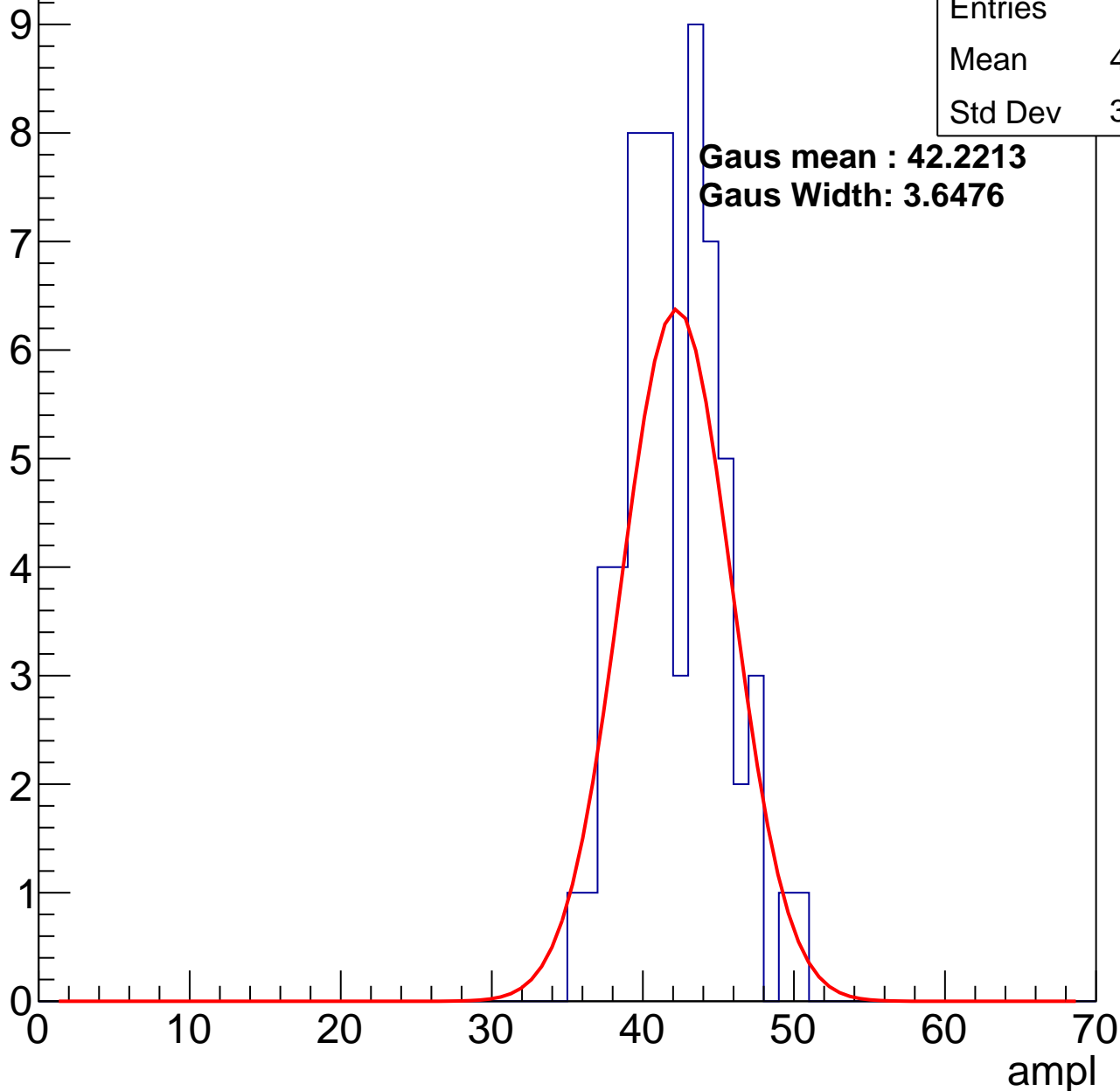
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	41.68
Std Dev	3.177

**Gaus mean : 42.2213**

**Gaus Width: 3.6476**

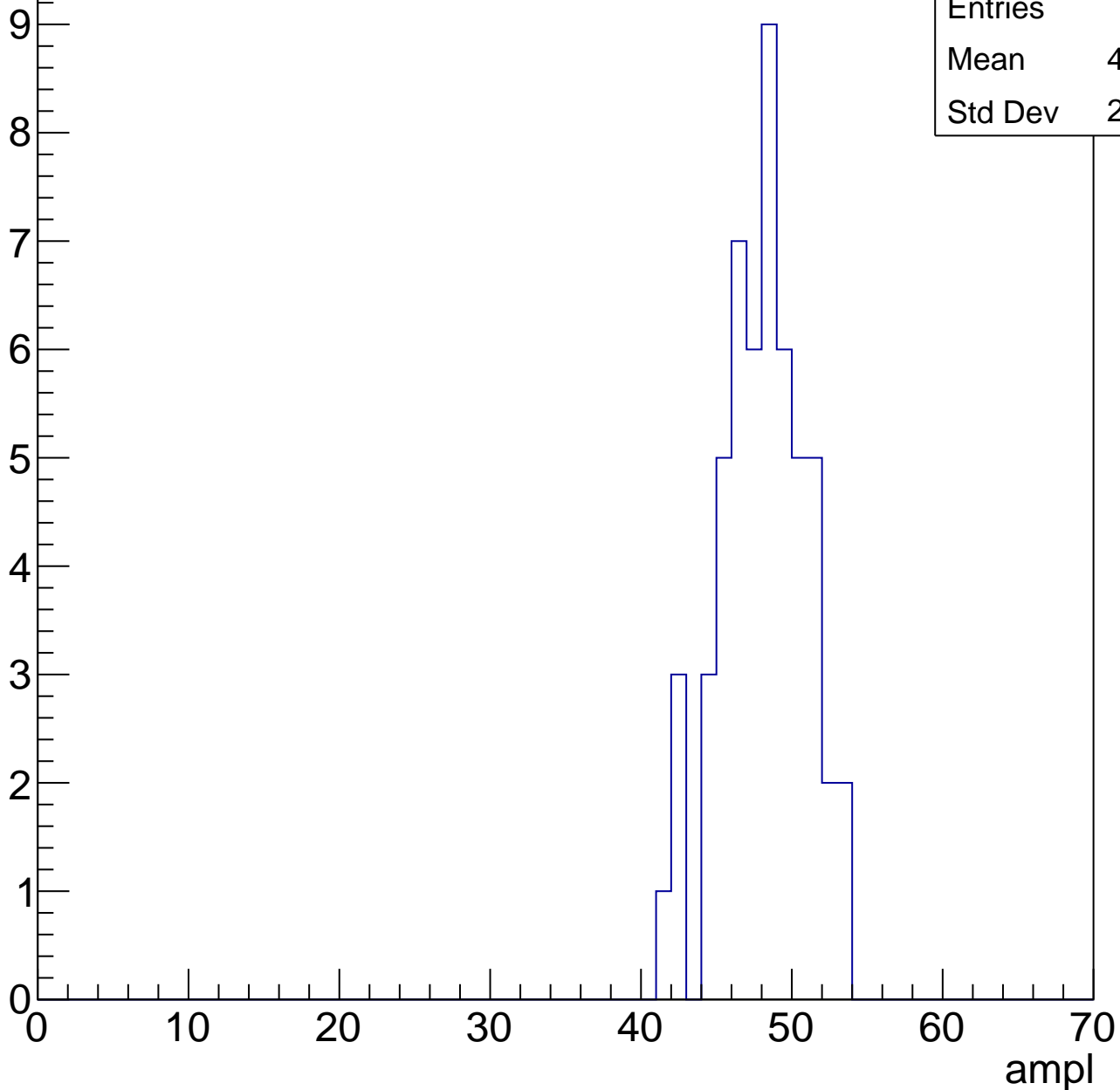


# B1L003S, U23-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	47.57
Std Dev	2.813

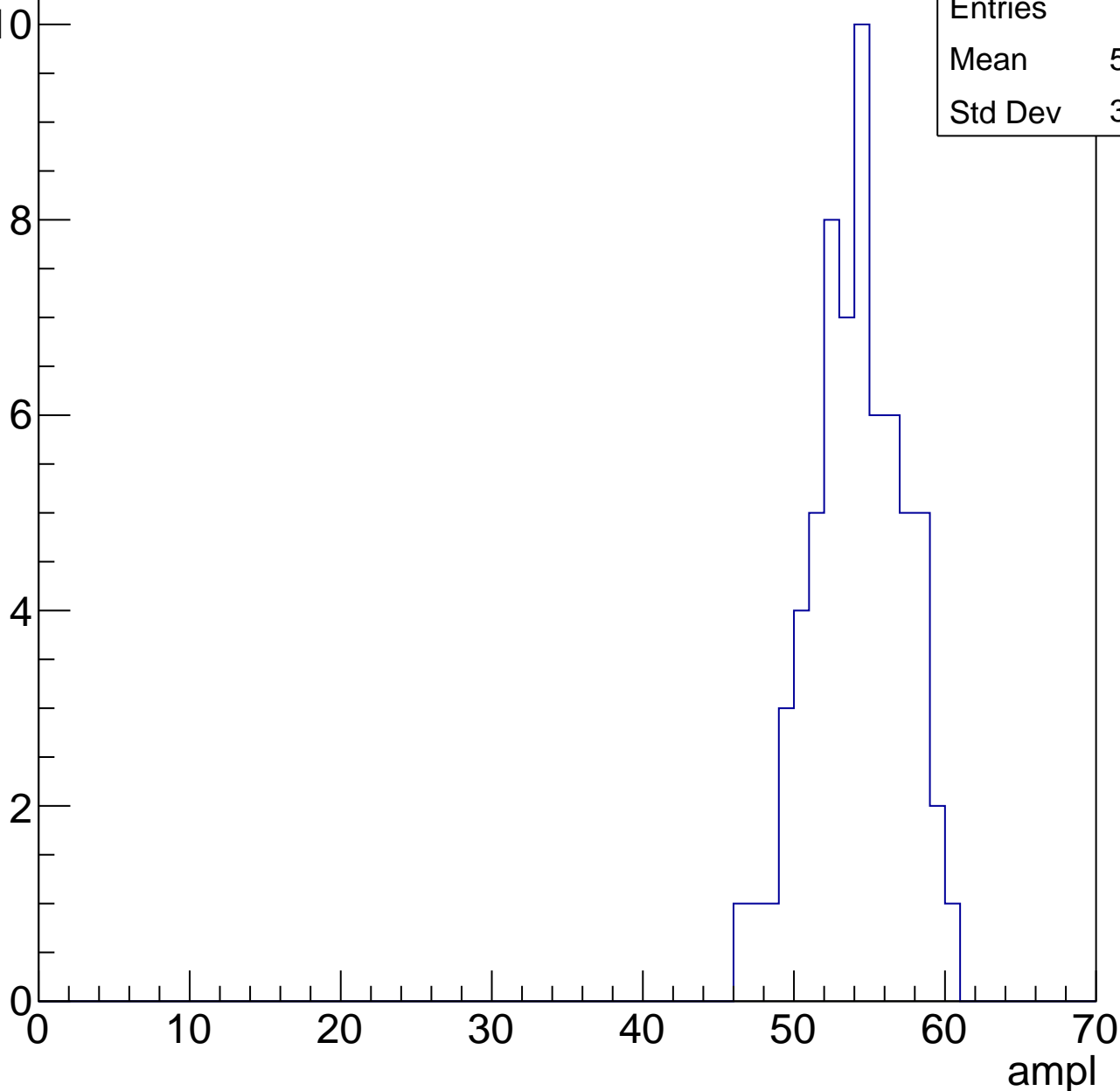


# B1L003S, U23-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	53.68
Std Dev	3.054

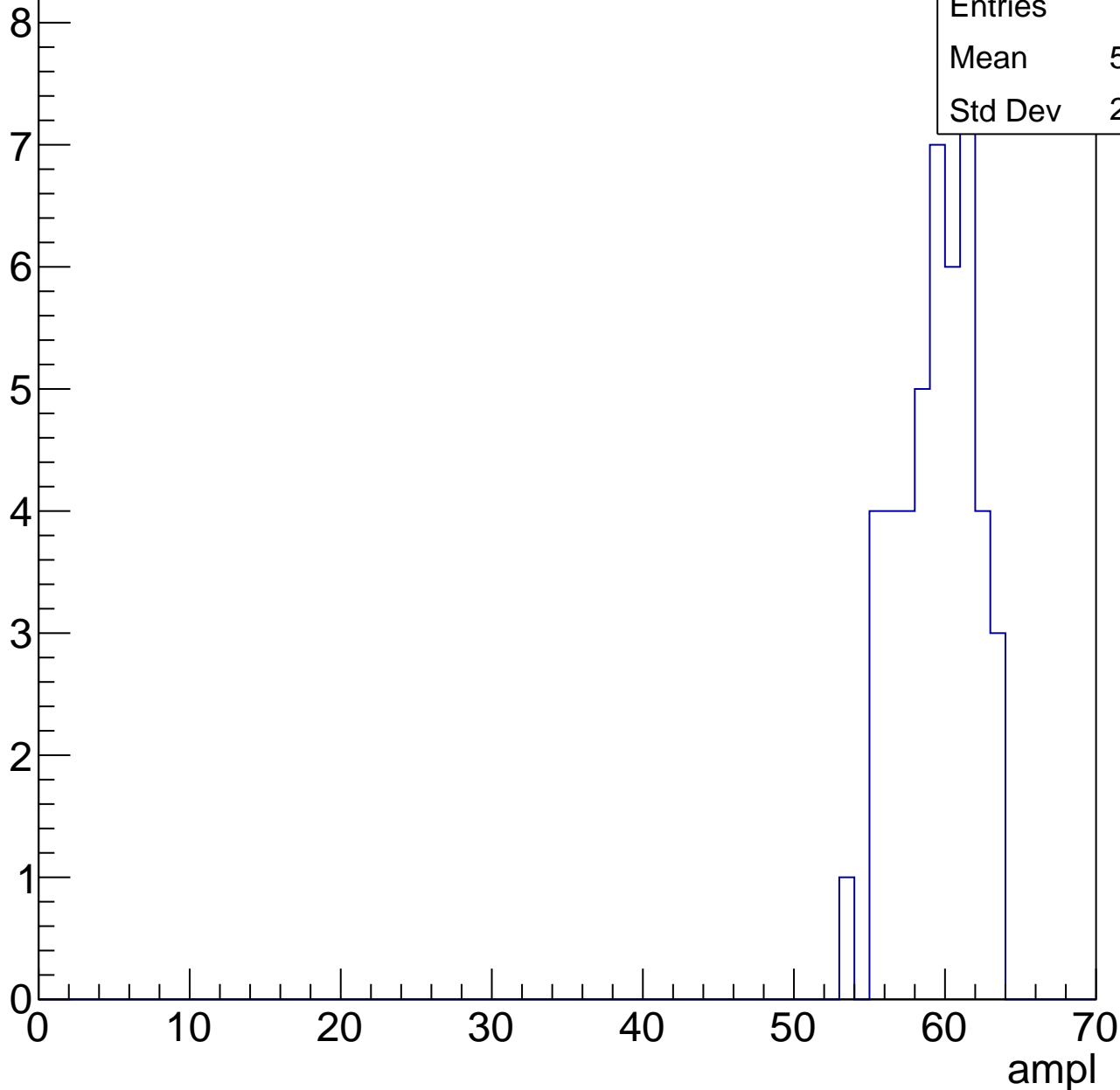


# B1L003S, U23-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.98
Std Dev	2.463

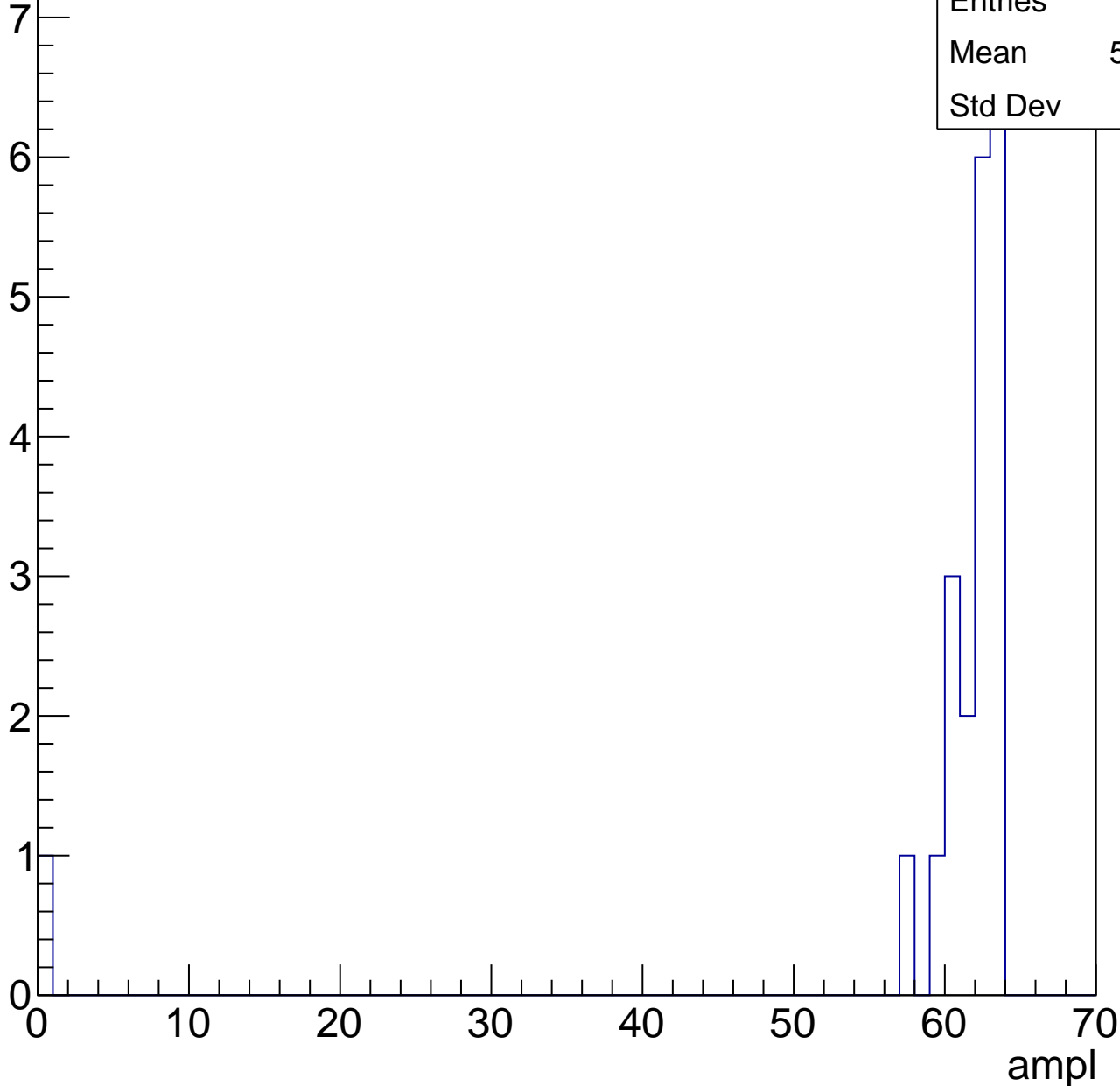


# B1L003S, U23-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	21
Mean	58.62
Std Dev	13.2





# B1L003S, U23-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch51, adc0

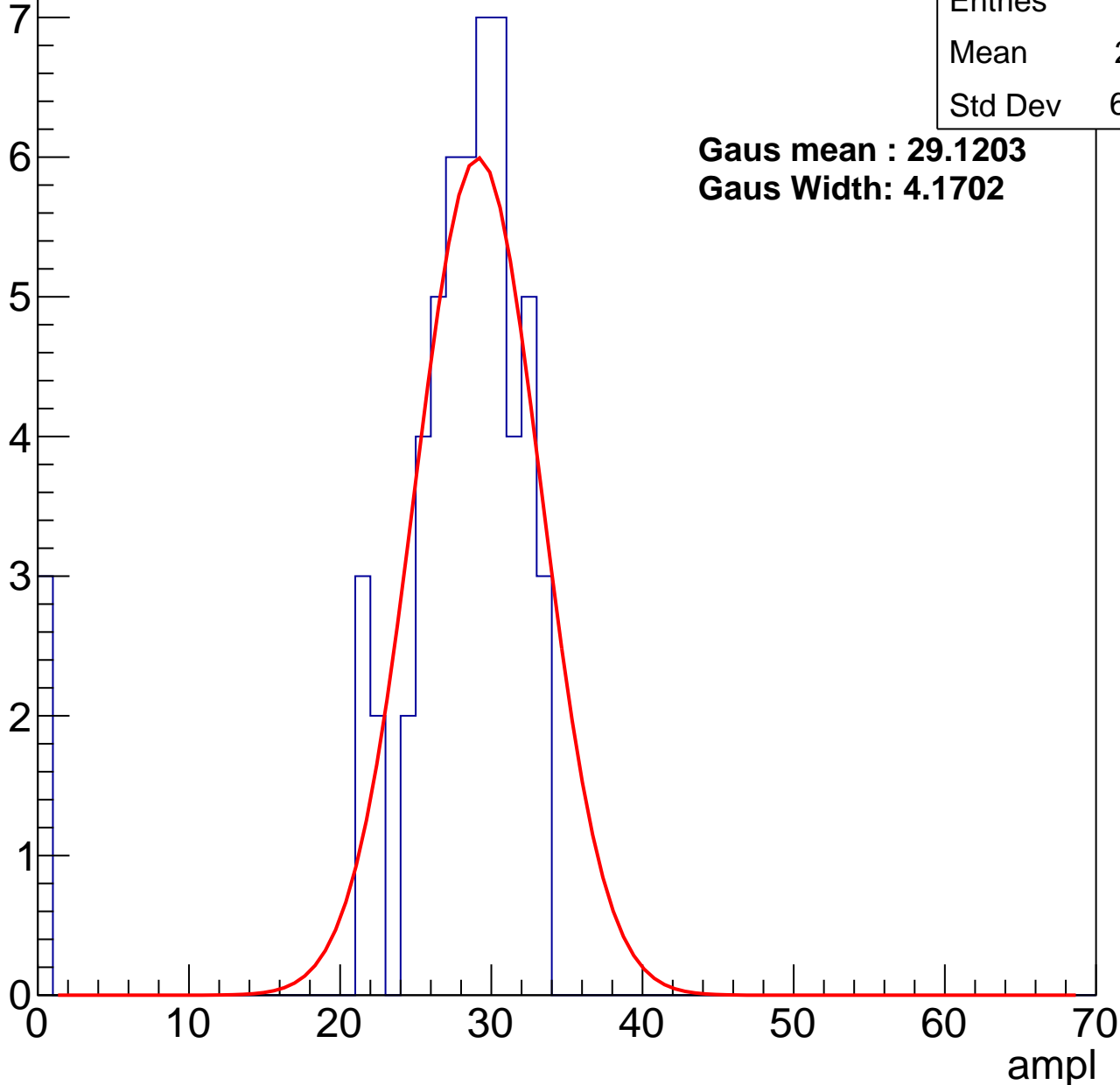
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	26.51
Std Dev	6.964

**Gaus mean : 29.1203**

**Gaus Width: 4.1702**

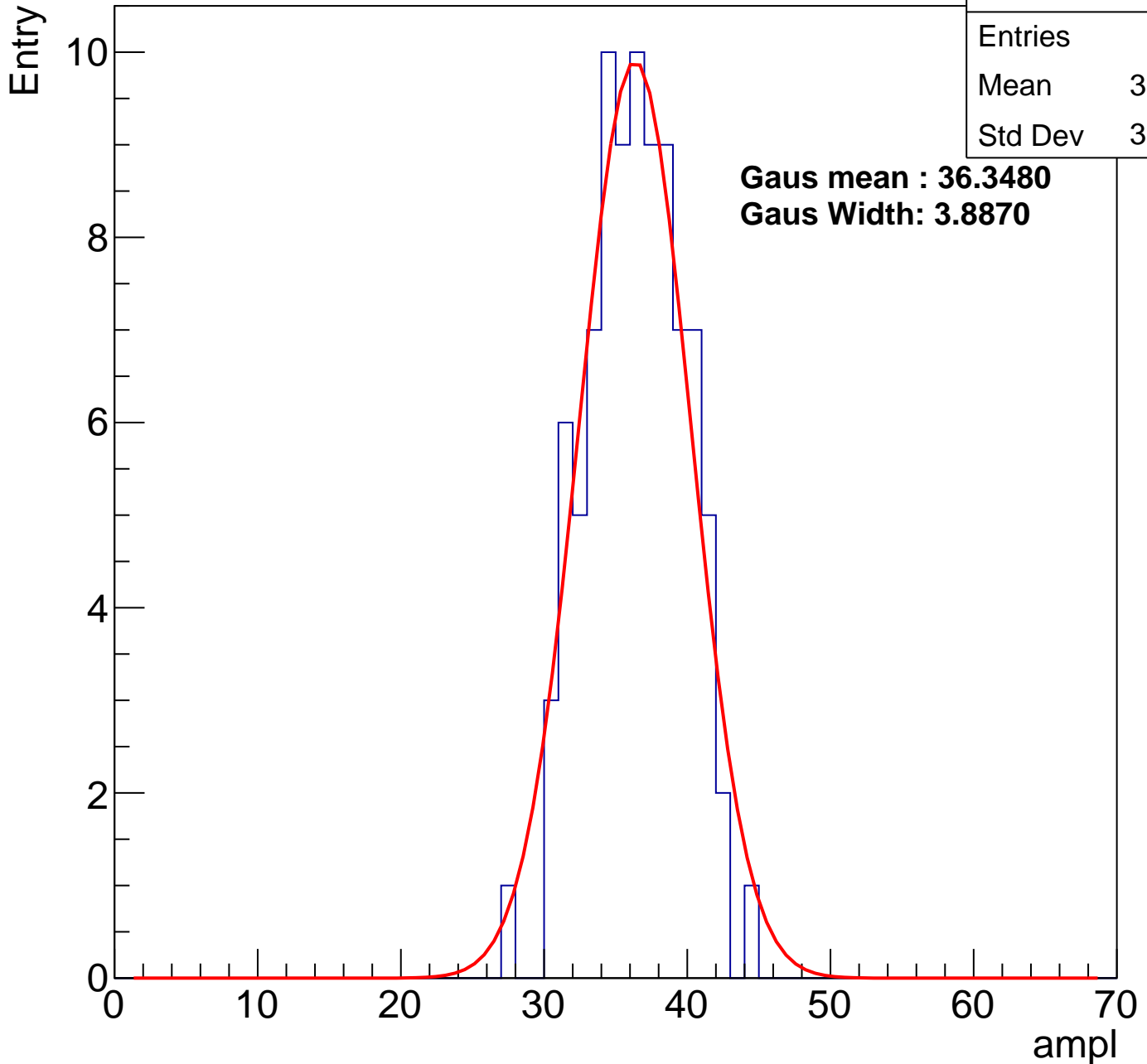


# B1L003S, U23-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	91
Mean	35.93
Std Dev	3.334

**Gaus mean : 36.3480**  
**Gaus Width: 3.8870**



# B1L003S, U23-ch51, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.24
Std Dev	2.99

**Gaus mean : 43.7728**

**Gaus Width: 2.7363**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

0

2

4

6

8

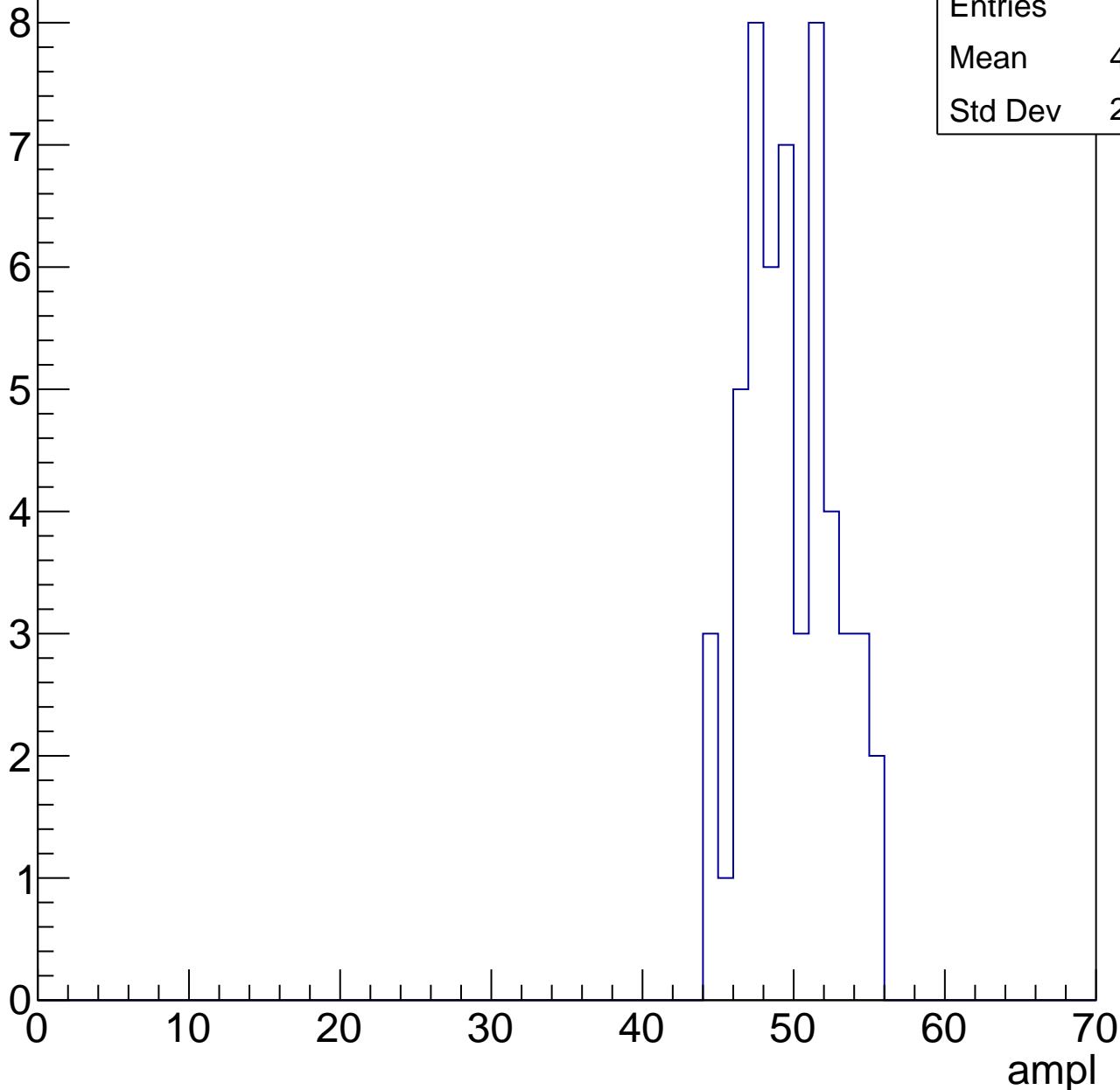
10

# B1L003S, U23-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	49.26
Std Dev	2.869

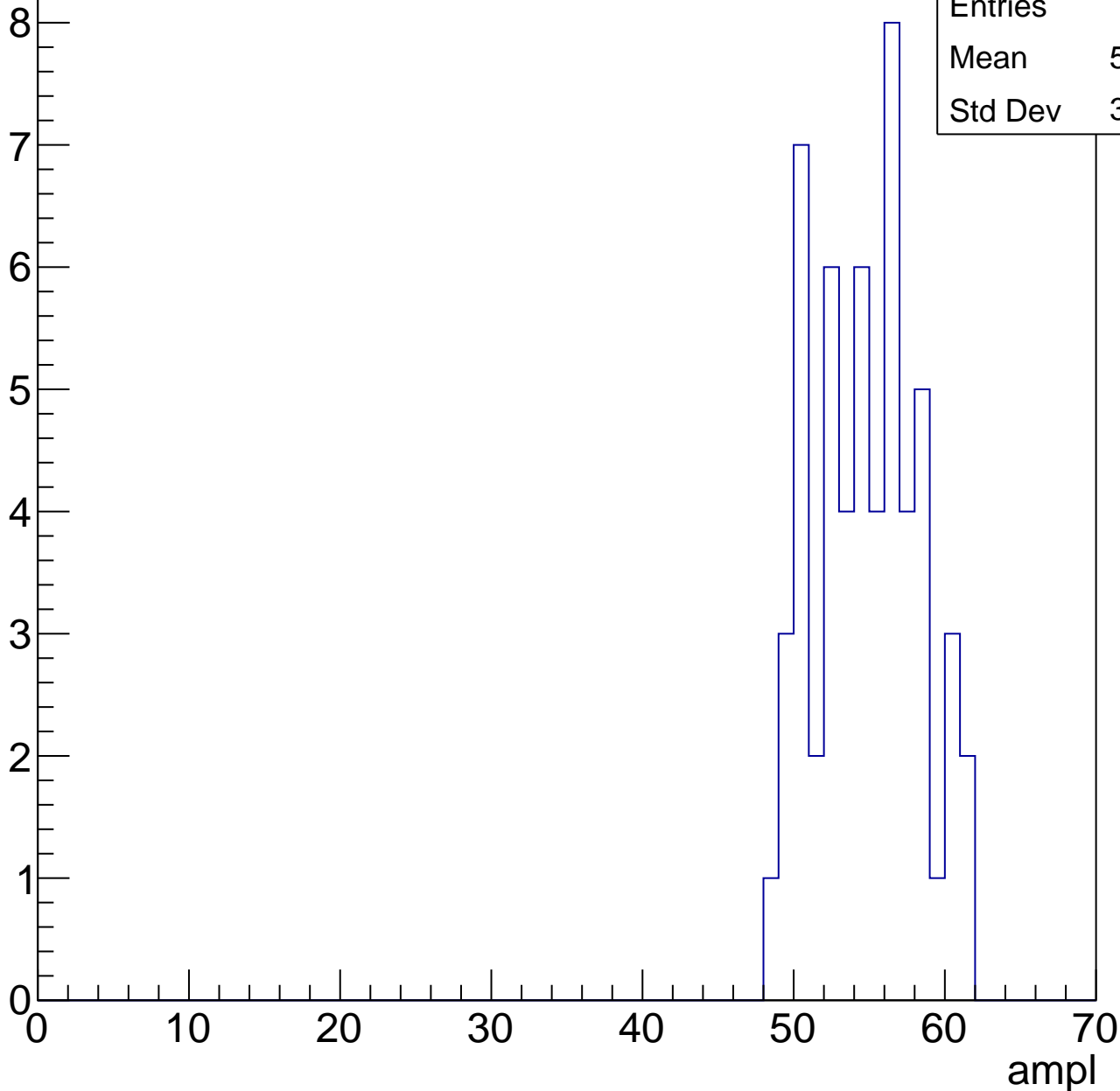


# B1L003S, U23-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

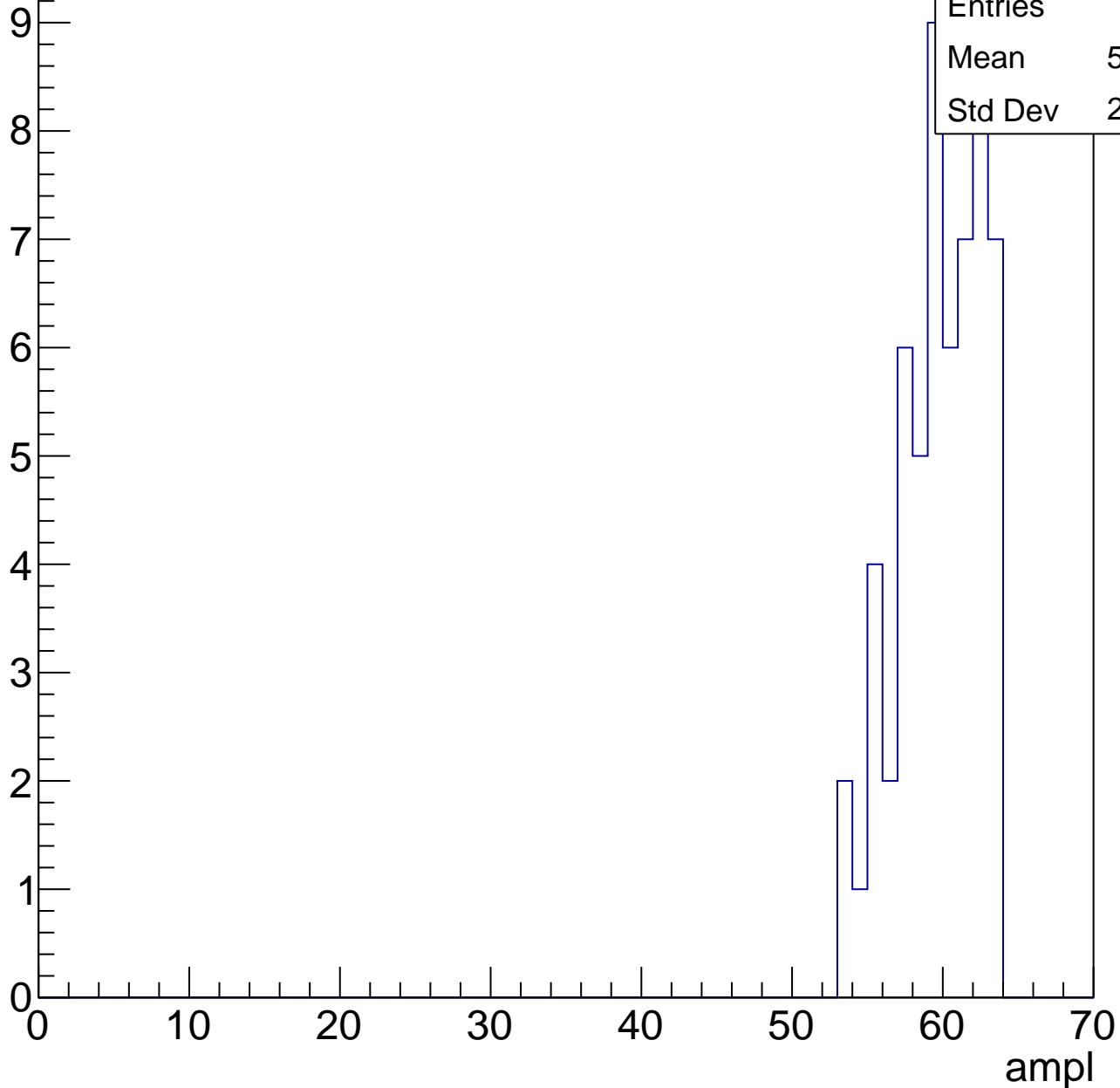
Entries	56
Mean	54.32
Std Dev	3.397



# B1L003S, U23-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

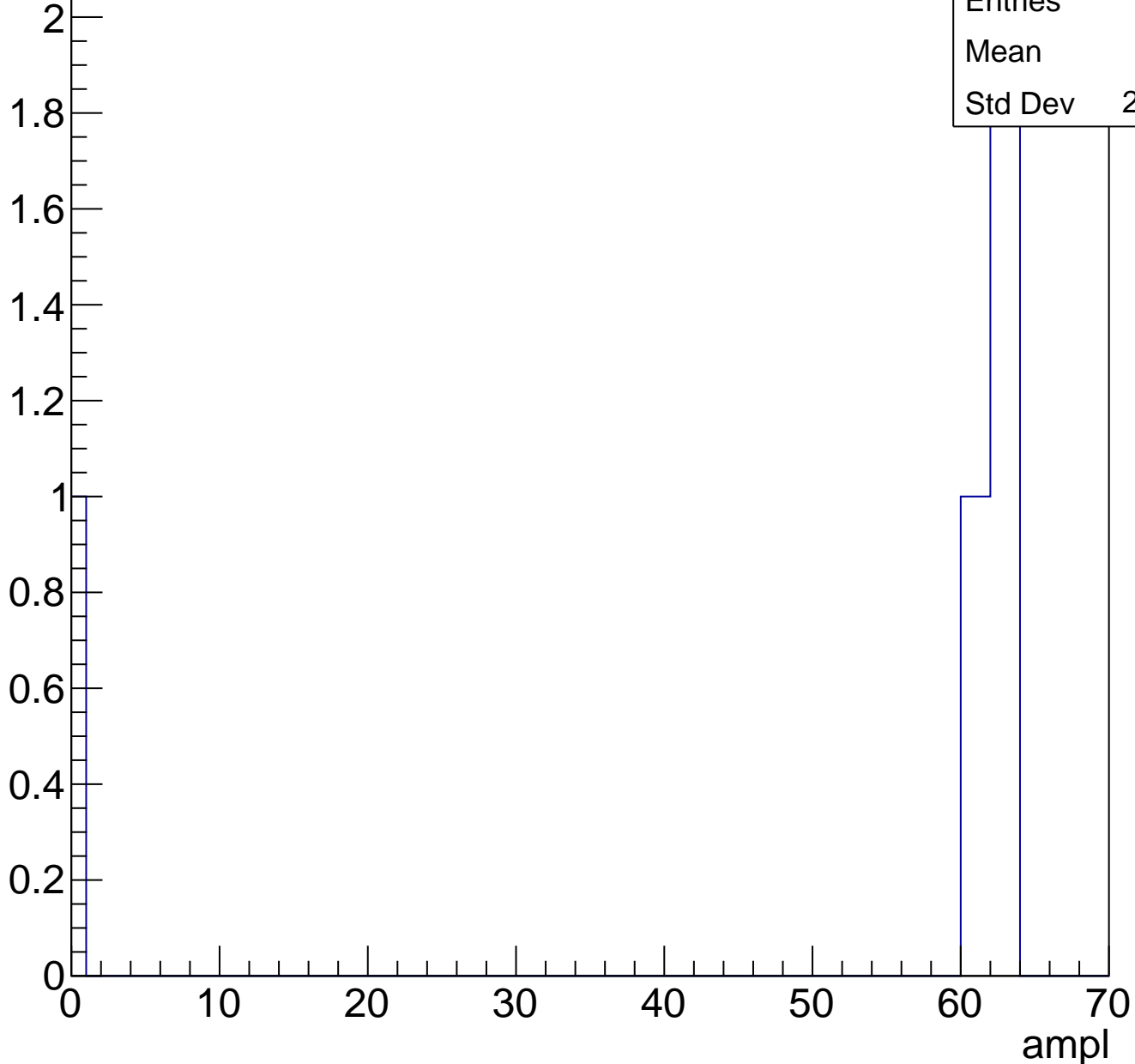


Entries	57
Mean	59.28
Std Dev	2.719

# B1L003S, U23-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch52, adc0

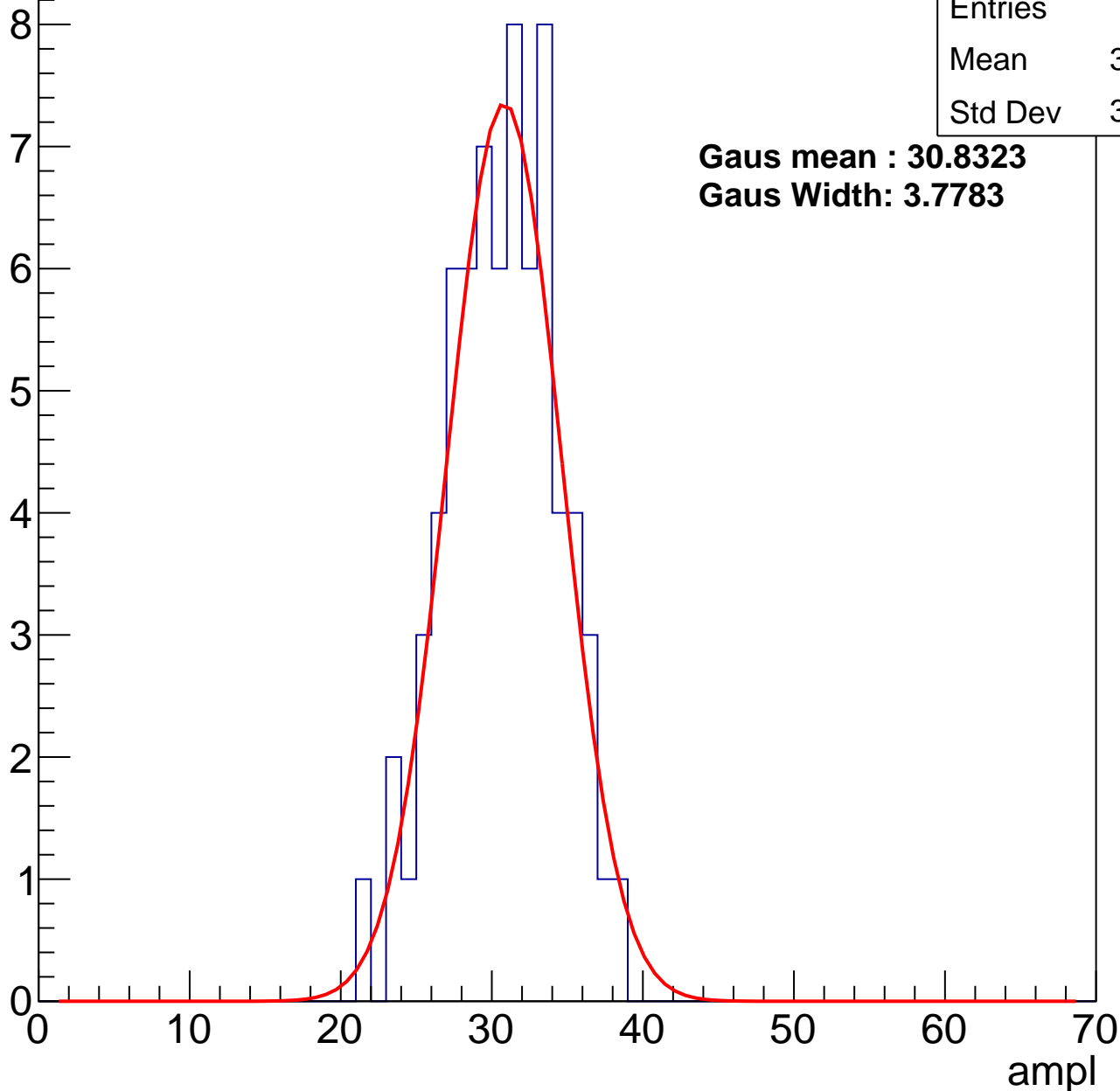
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	30.23
Std Dev	3.608

**Gaus mean : 30.8323**

**Gaus Width: 3.7783**



# B1L003S, U23-ch52, adc1

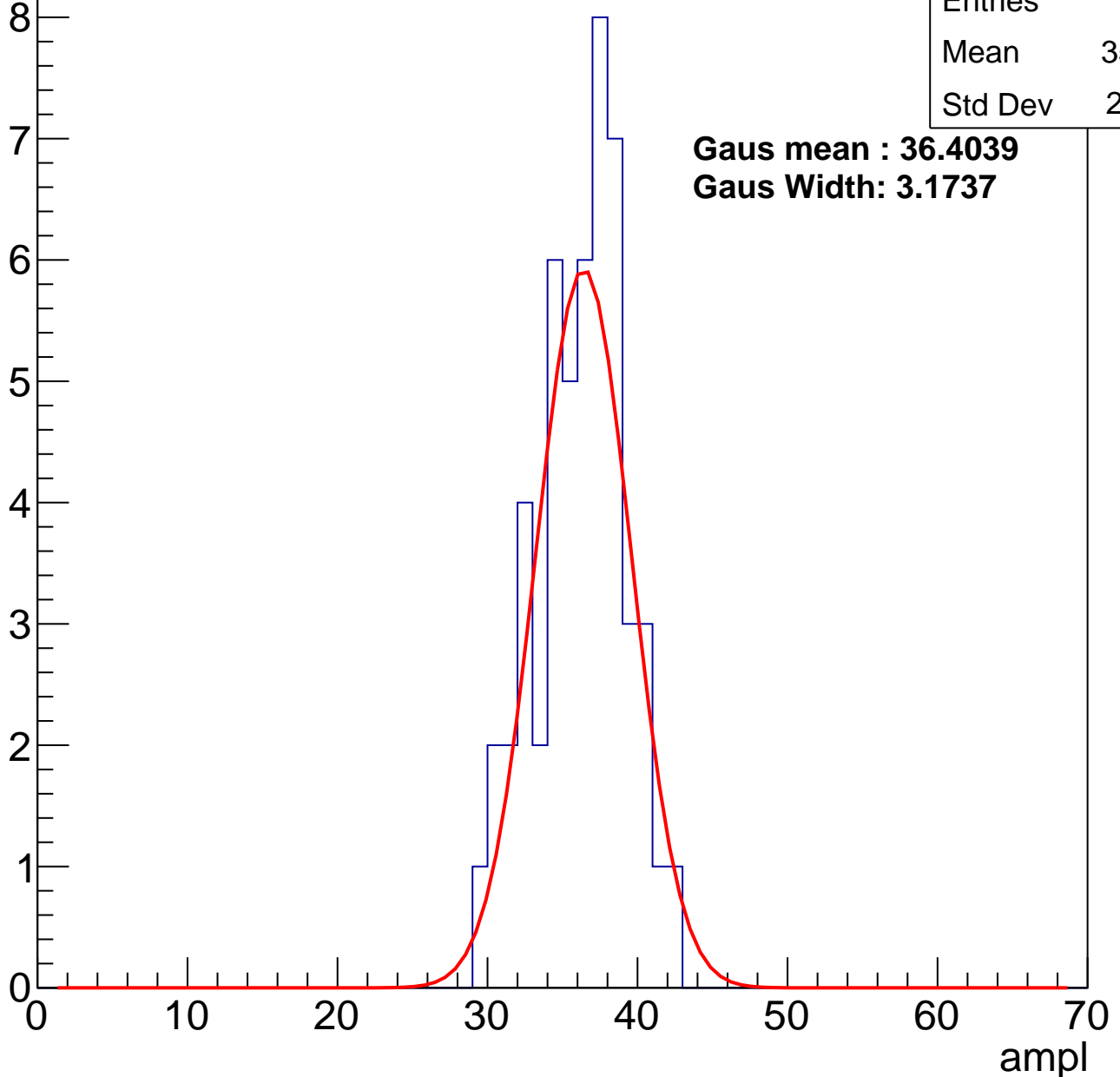
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	35.73
Std Dev	2.971

**Gaus mean : 36.4039**

**Gaus Width: 3.1737**



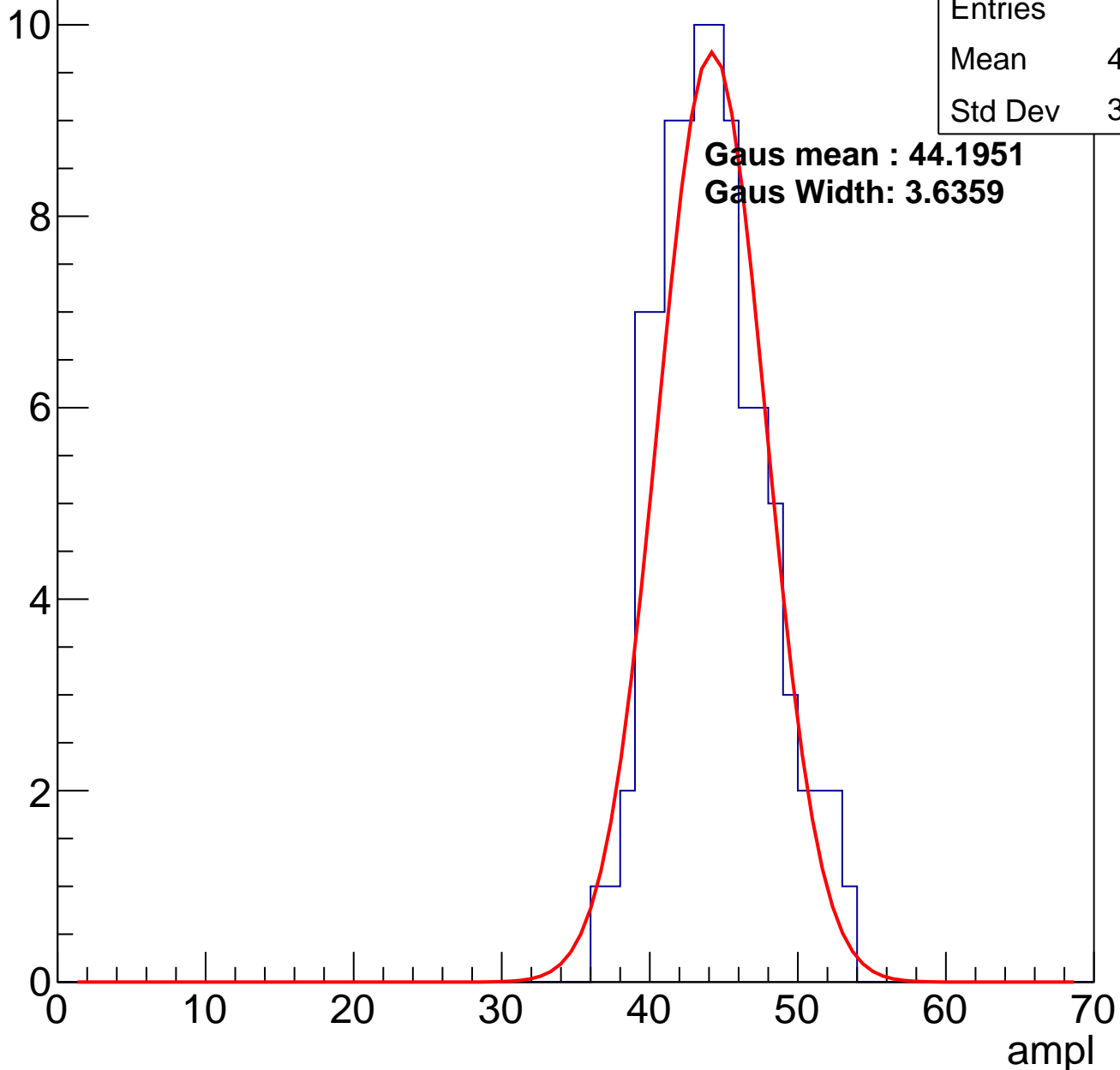
# B1L003S, U23-ch52, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	92
Mean	43.78
Std Dev	3.635

**Gaus mean : 44.1951**  
**Gaus Width: 3.6359**

Entry

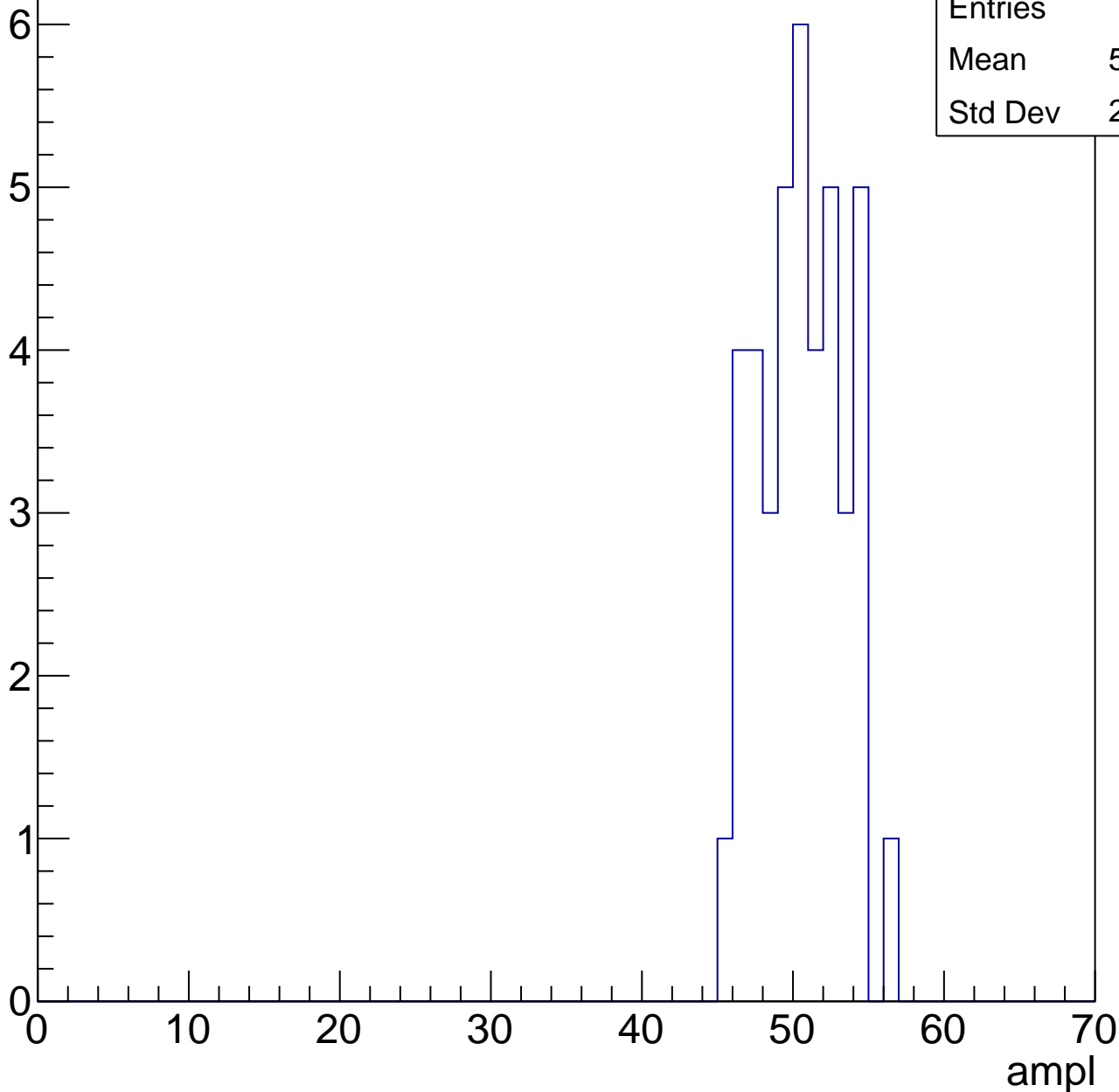


# B1L003S, U23-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

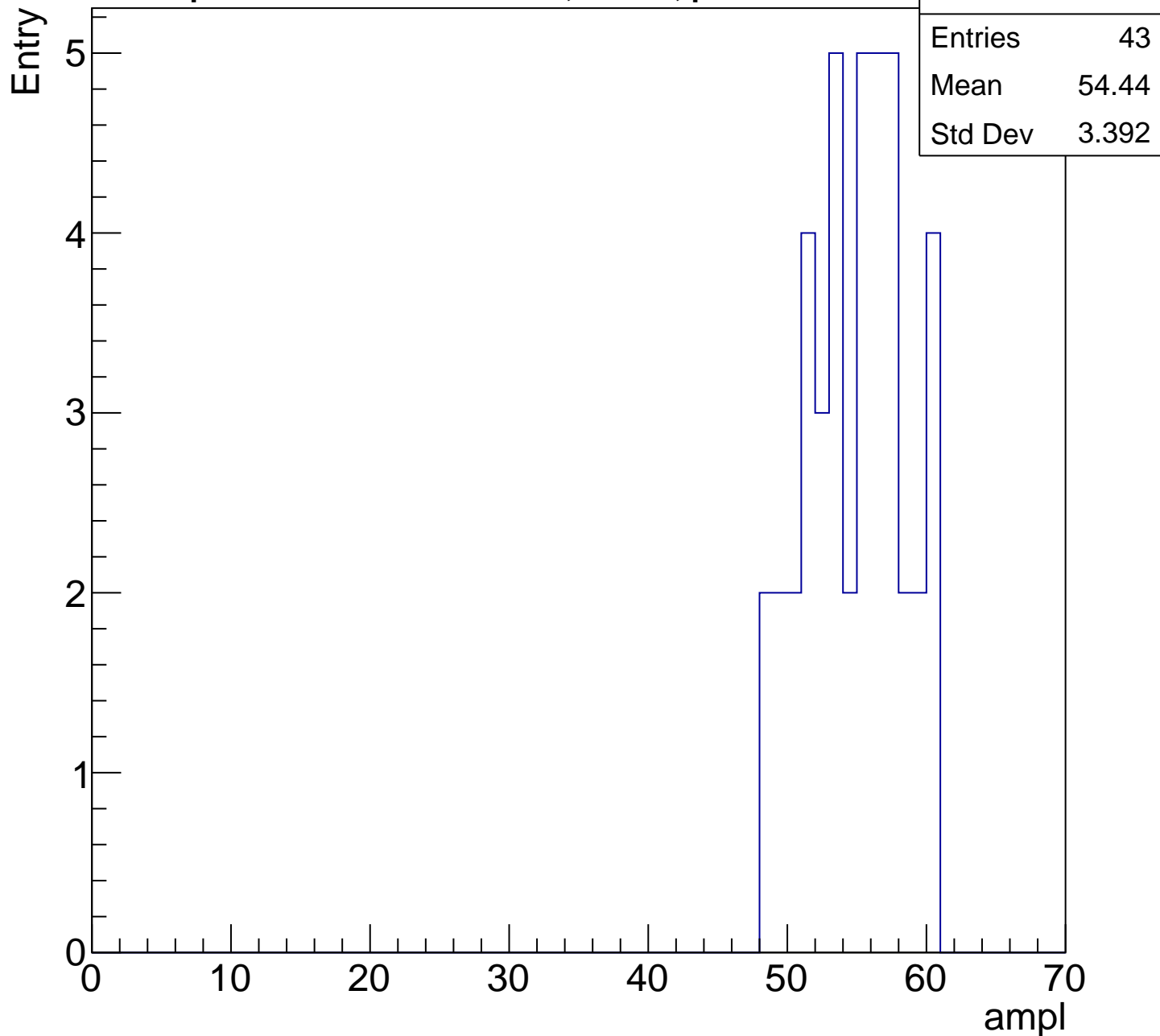
Entry

Entries	41
Mean	50.12
Std Dev	2.743



# B1L003S, U23-ch52, adc4

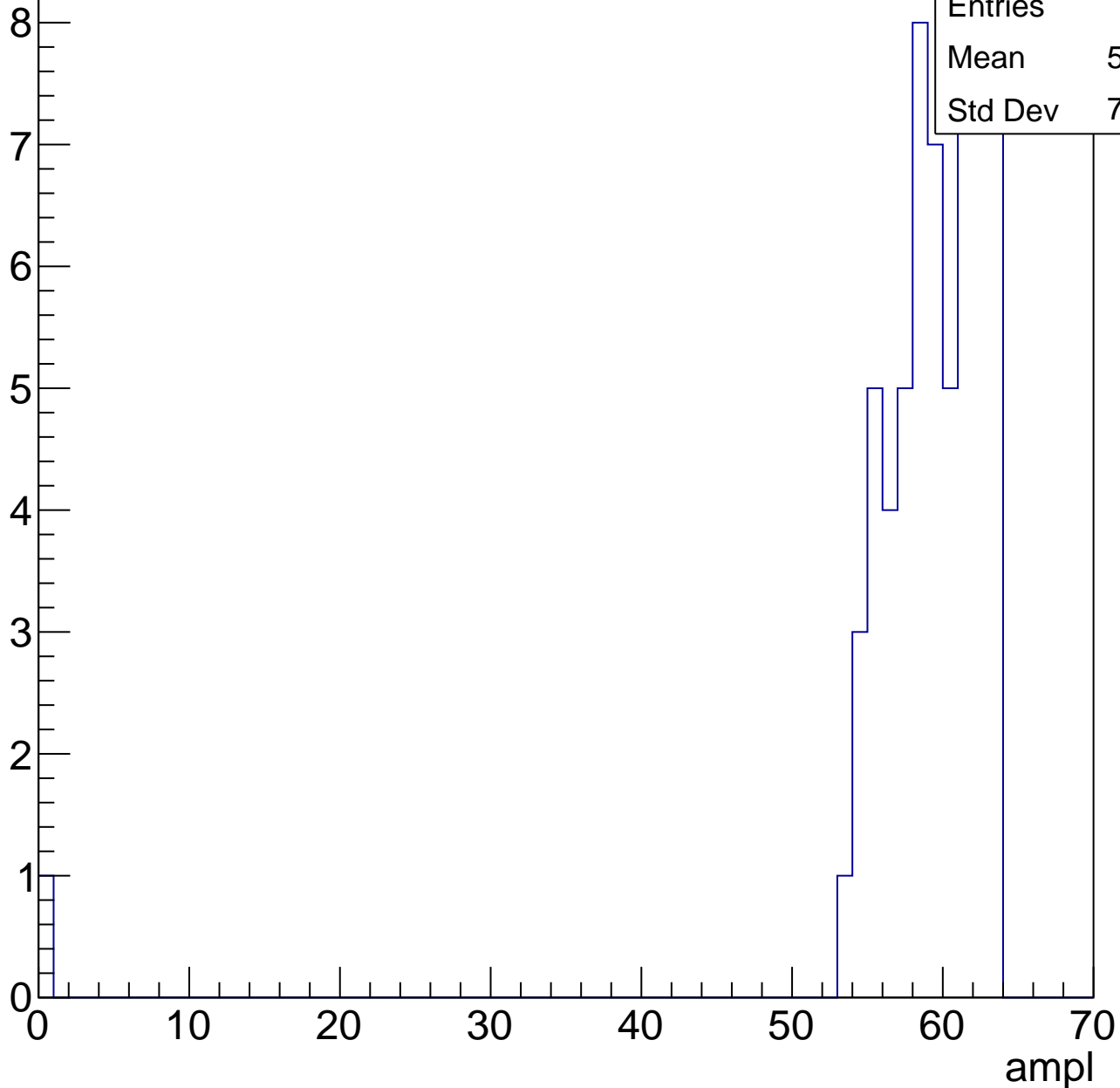
calib\_packv5\_042523\_0143.root, FC#13, port D2



# B1L003S, U23-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	63
Mean	58.16
Std Dev	7.897

# B1L003S, U23-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

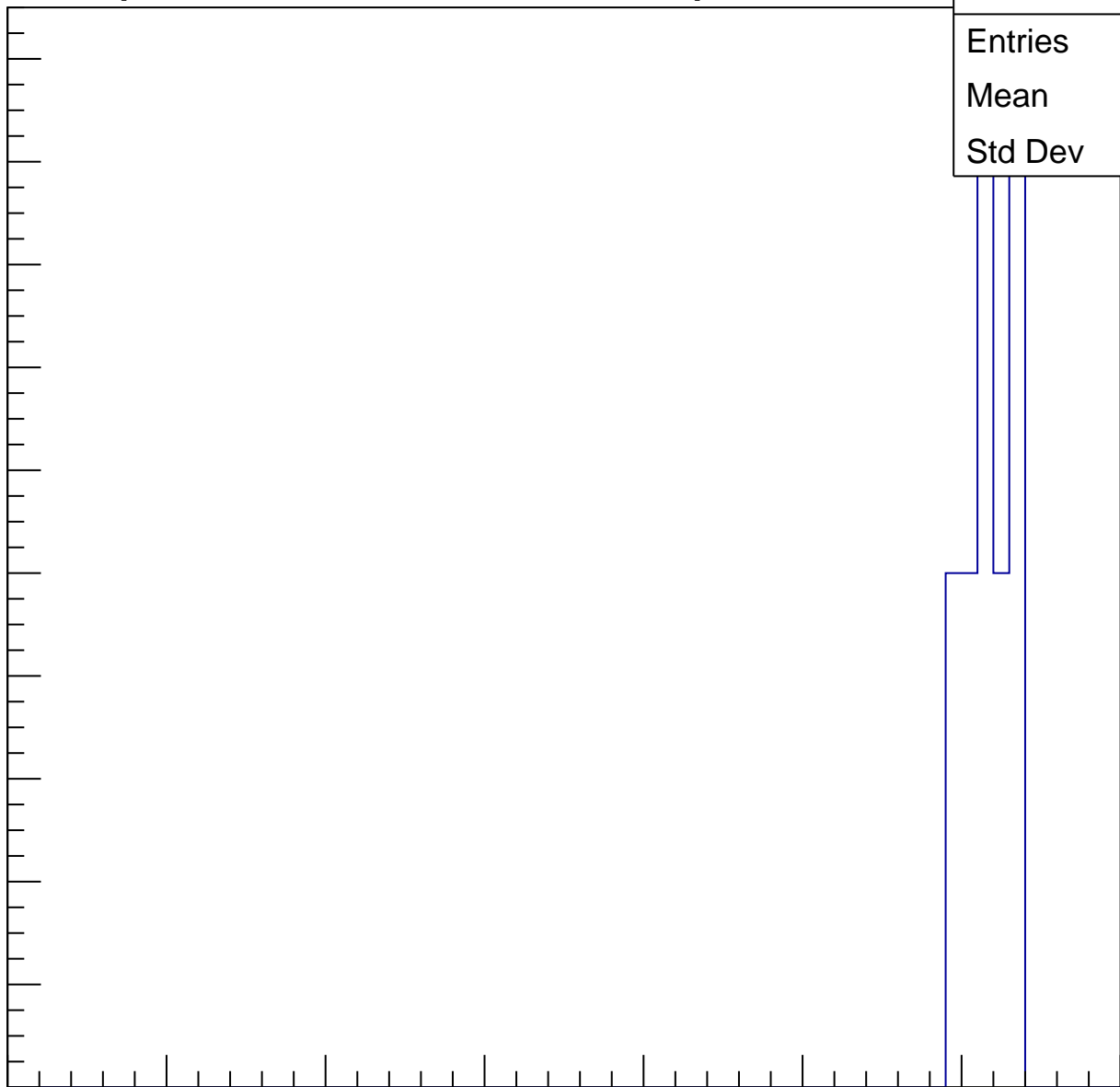
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.29
Std Dev	1.385

0 10 20 30 40 50 60 70

ampl





# B1L003S, U23-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch53, adc0

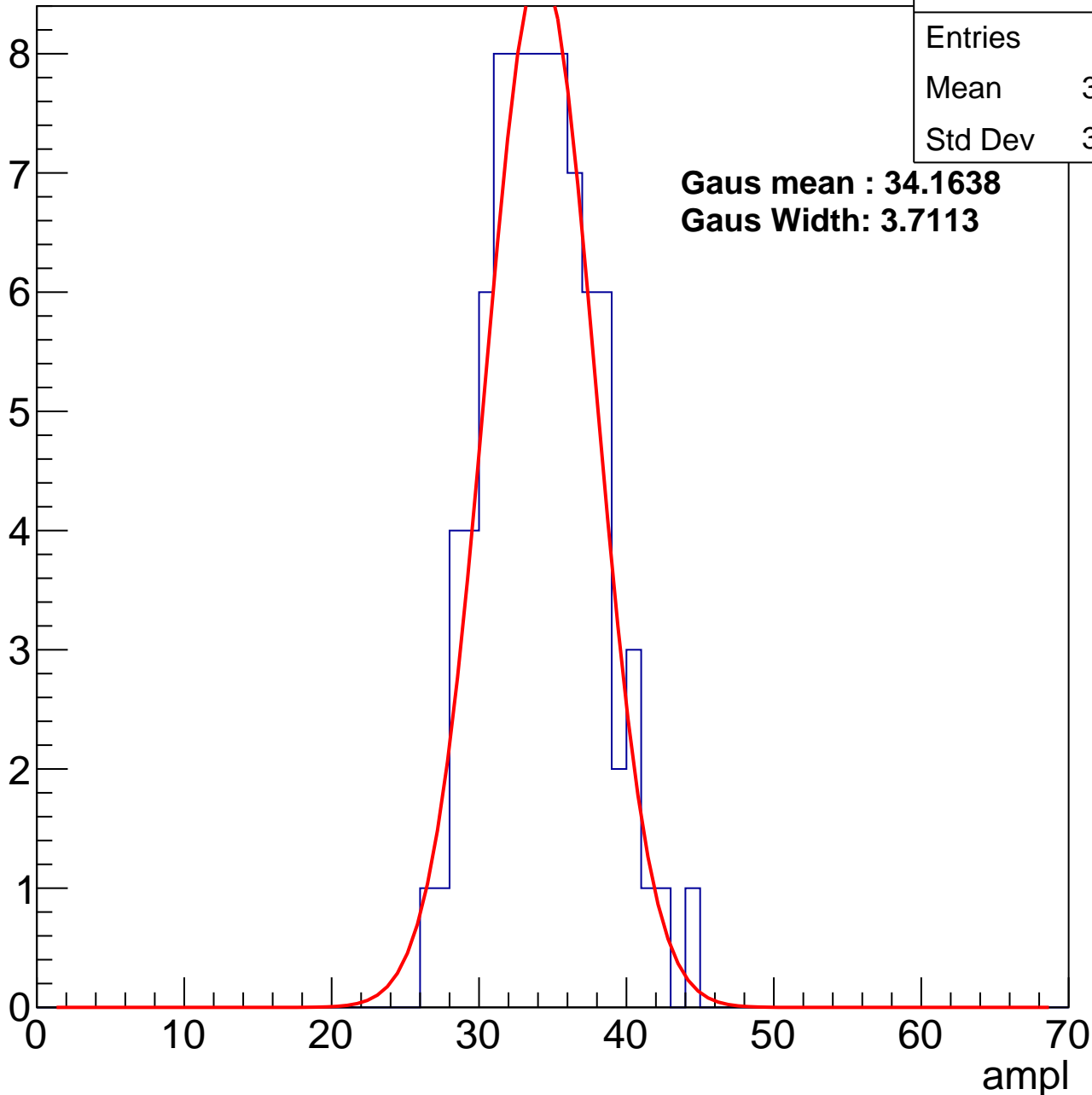
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	33.83
Std Dev	3.673

**Gaus mean : 34.1638**

**Gaus Width: 3.7113**



# B1L003S, U23-ch53, adc1

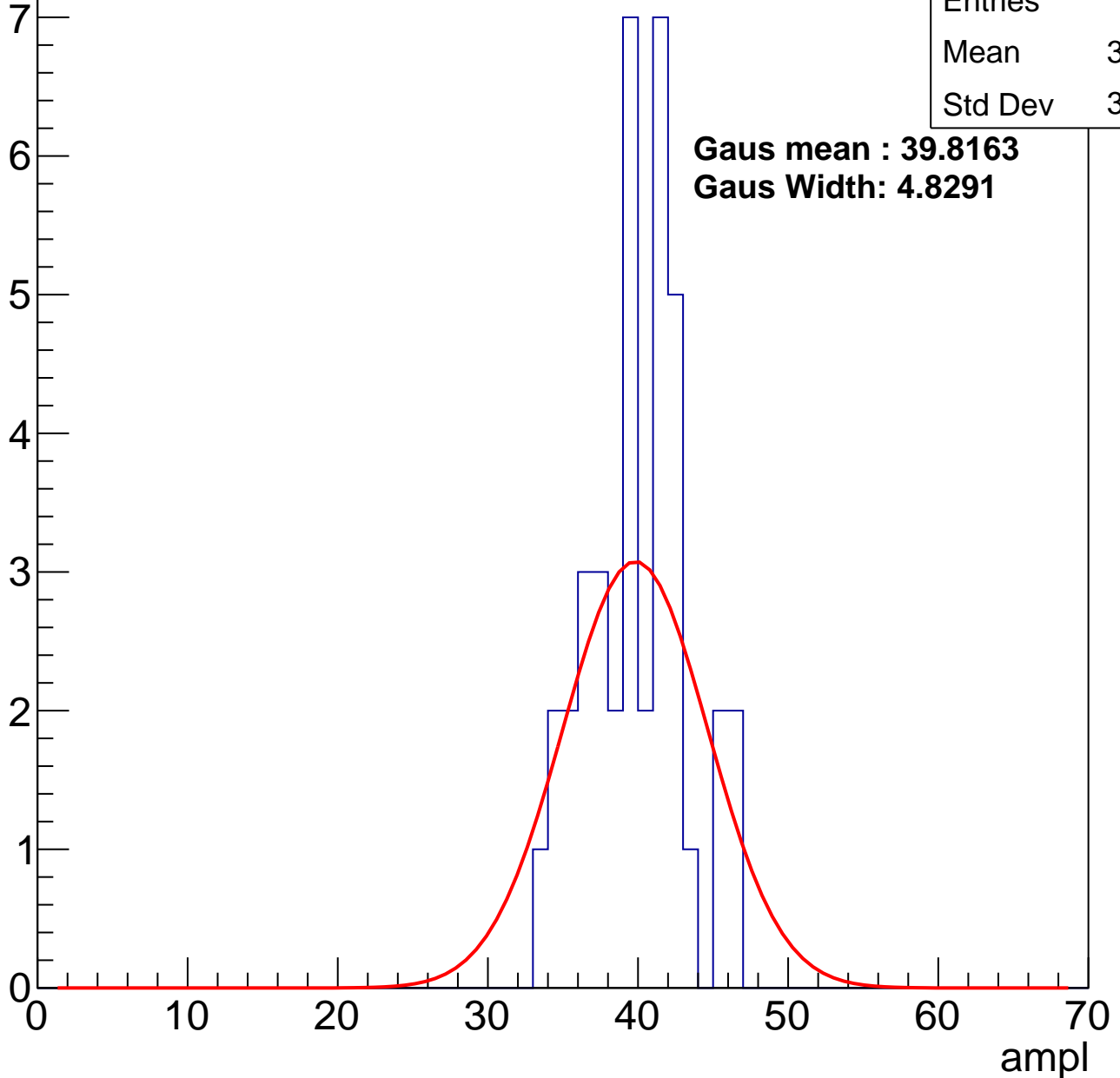
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	39.51
Std Dev	3.241

**Gaus mean : 39.8163**

**Gaus Width: 4.8291**



# B1L003S, U23-ch53, adc2

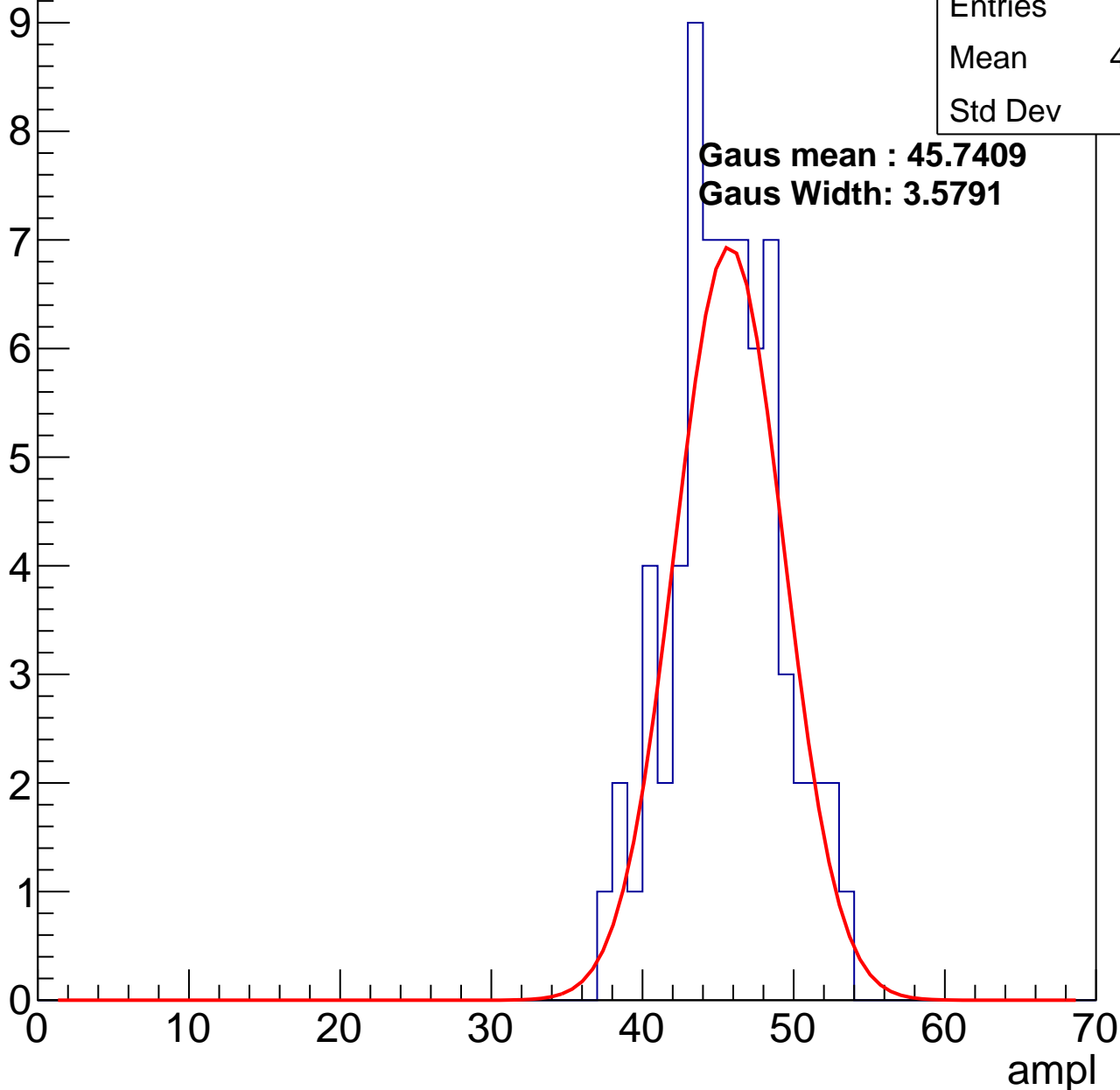
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	45.04
Std Dev	3.53

**Gaus mean : 45.7409**

**Gaus Width: 3.5791**

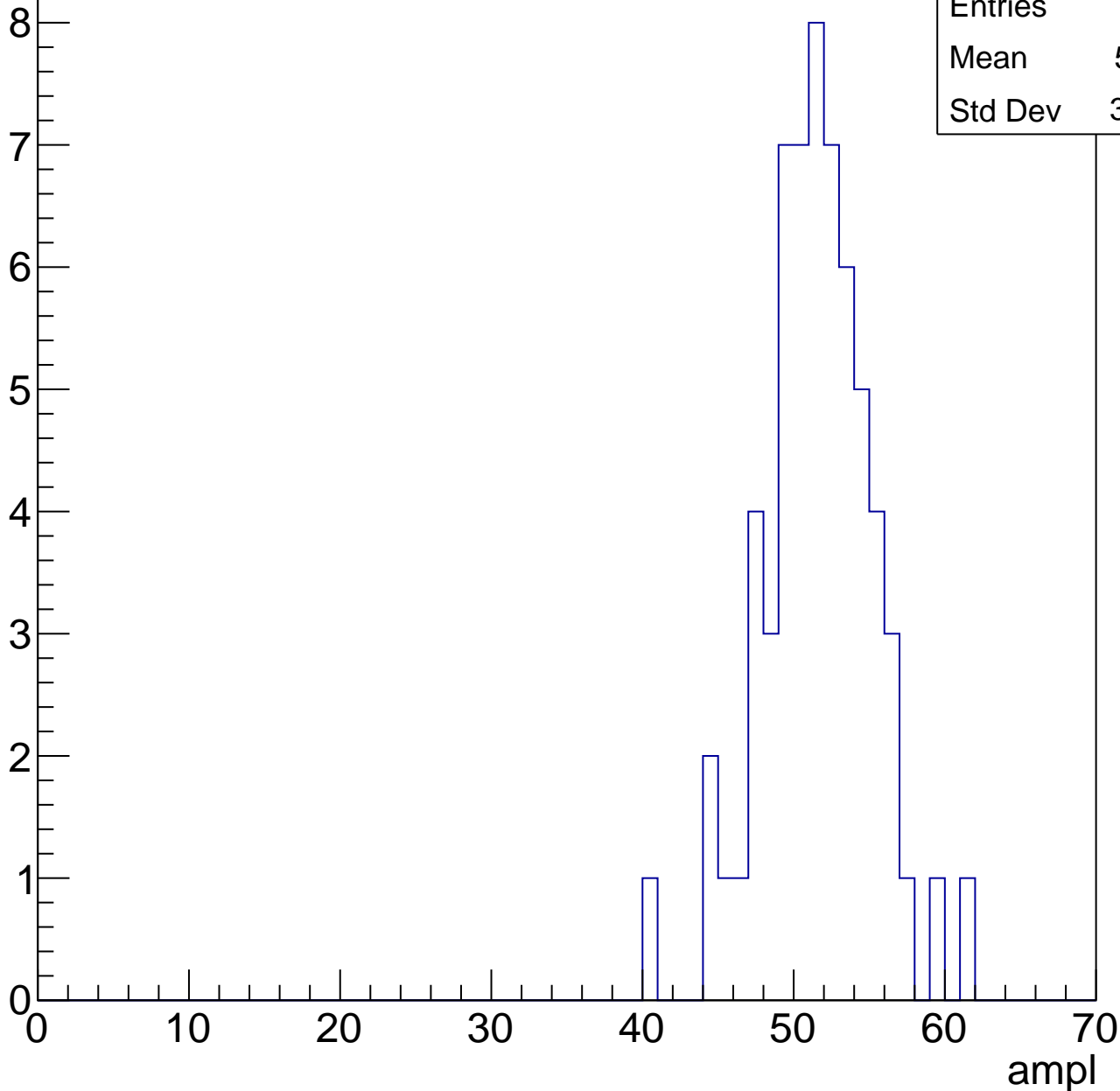


# B1L003S, U23-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

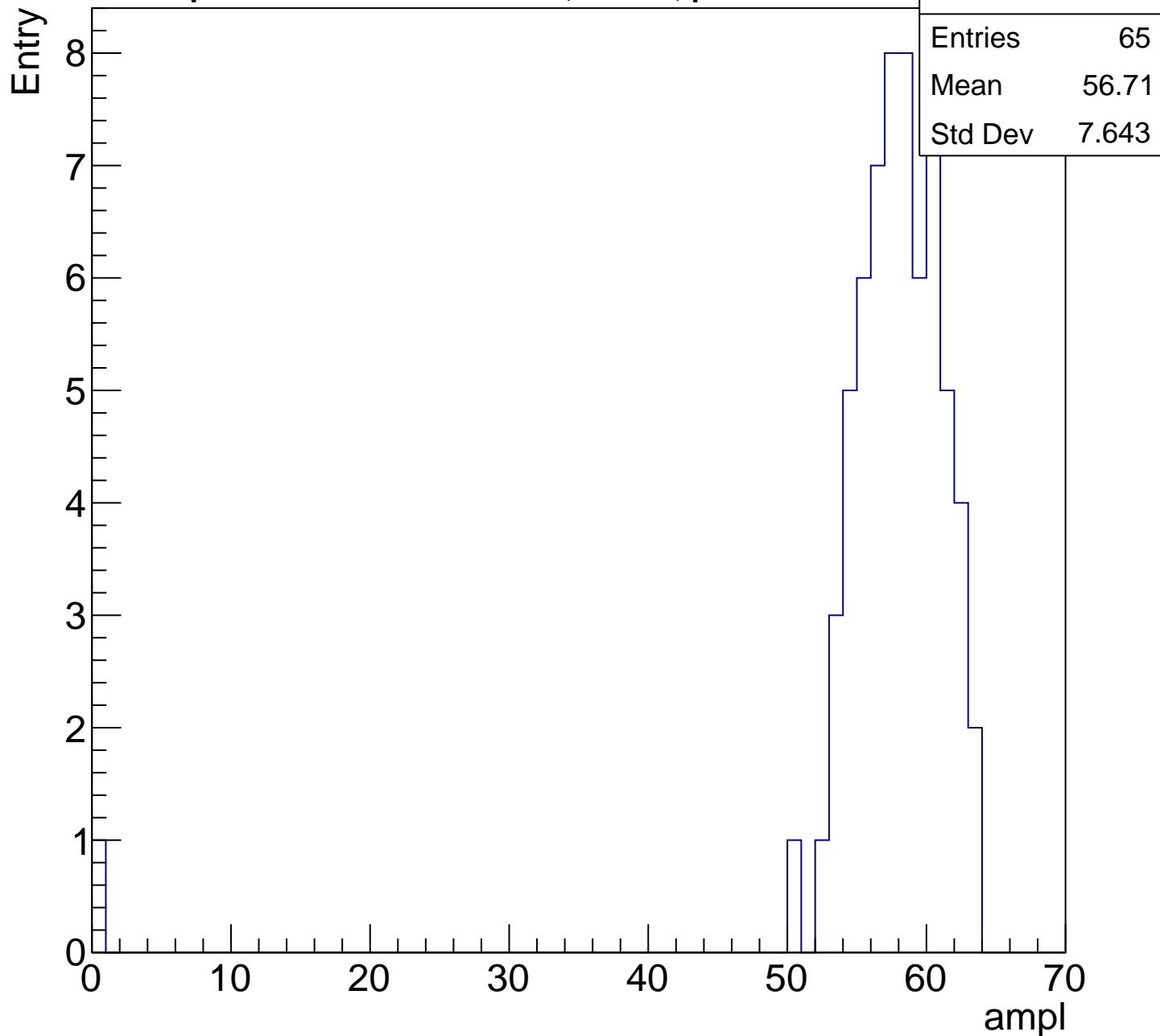
Entry

Entries	62
Mean	51.11
Std Dev	3.637



# B1L003S, U23-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

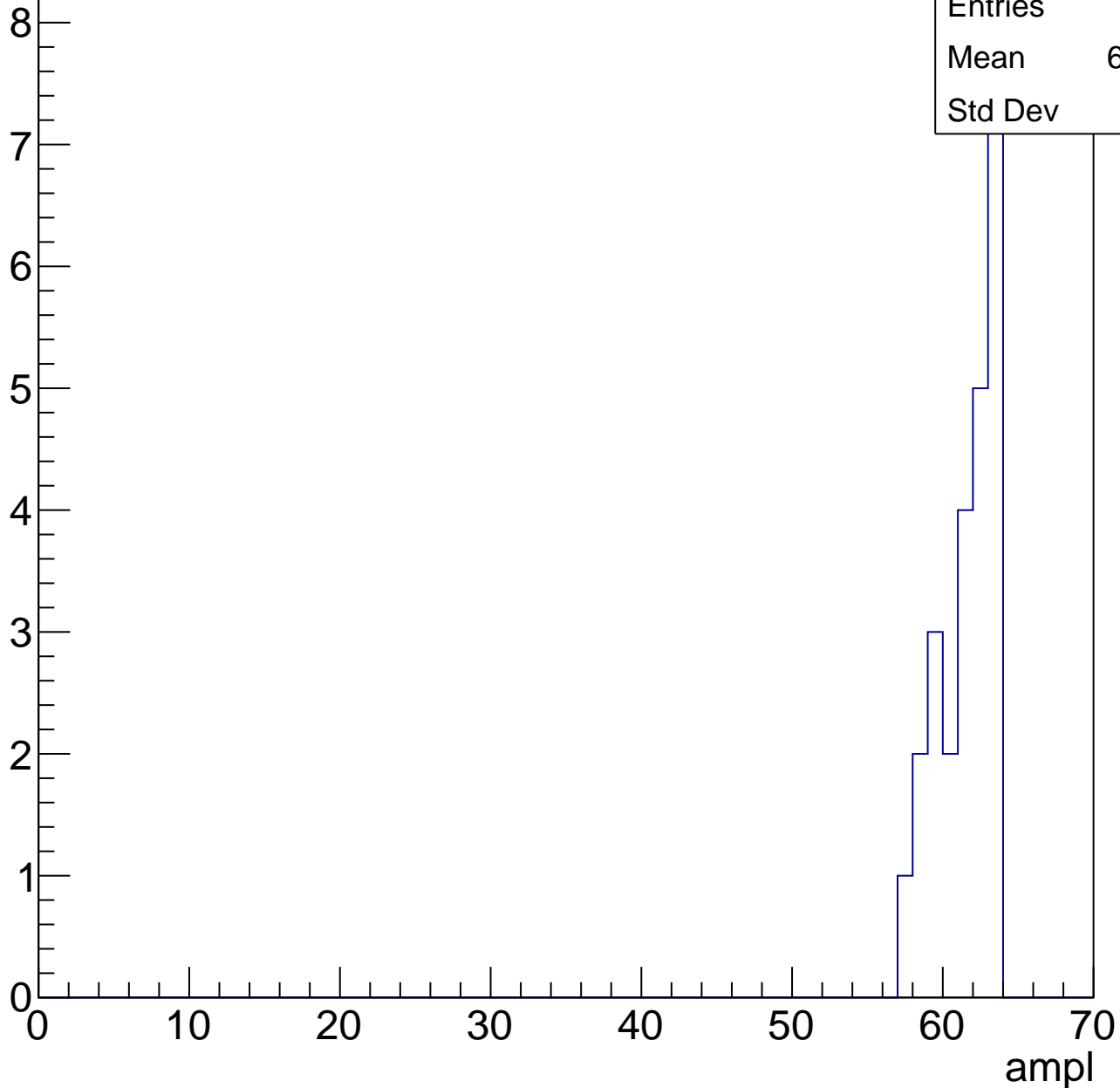


# B1L003S, U23-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	25
Mean	61.12
Std Dev	1.84



# B1L003S, U23-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch54, adc0

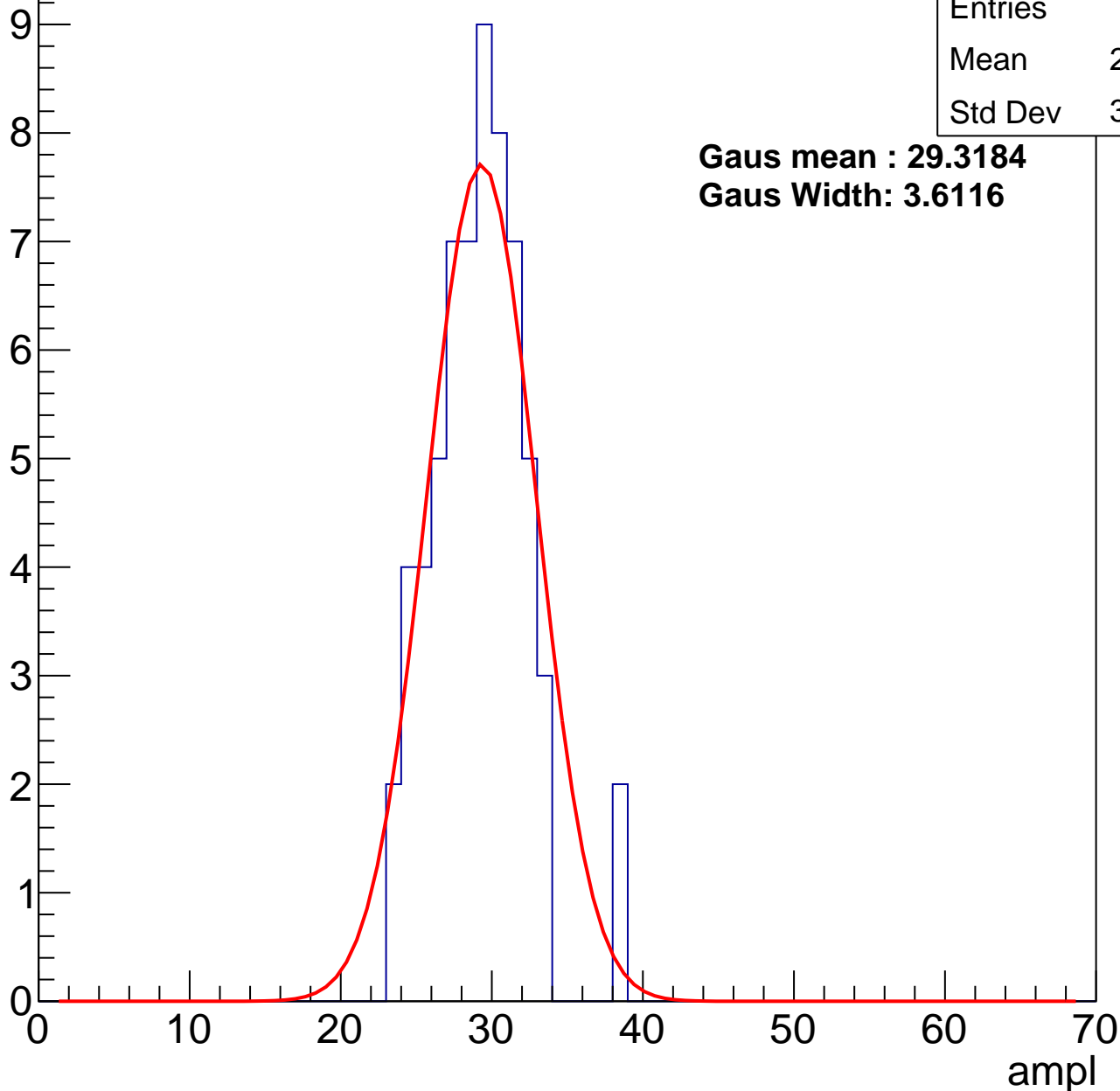
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	28.73
Std Dev	3.092

**Gaus mean : 29.3184**

**Gaus Width: 3.6116**



# B1L003S, U23-ch54, adc1

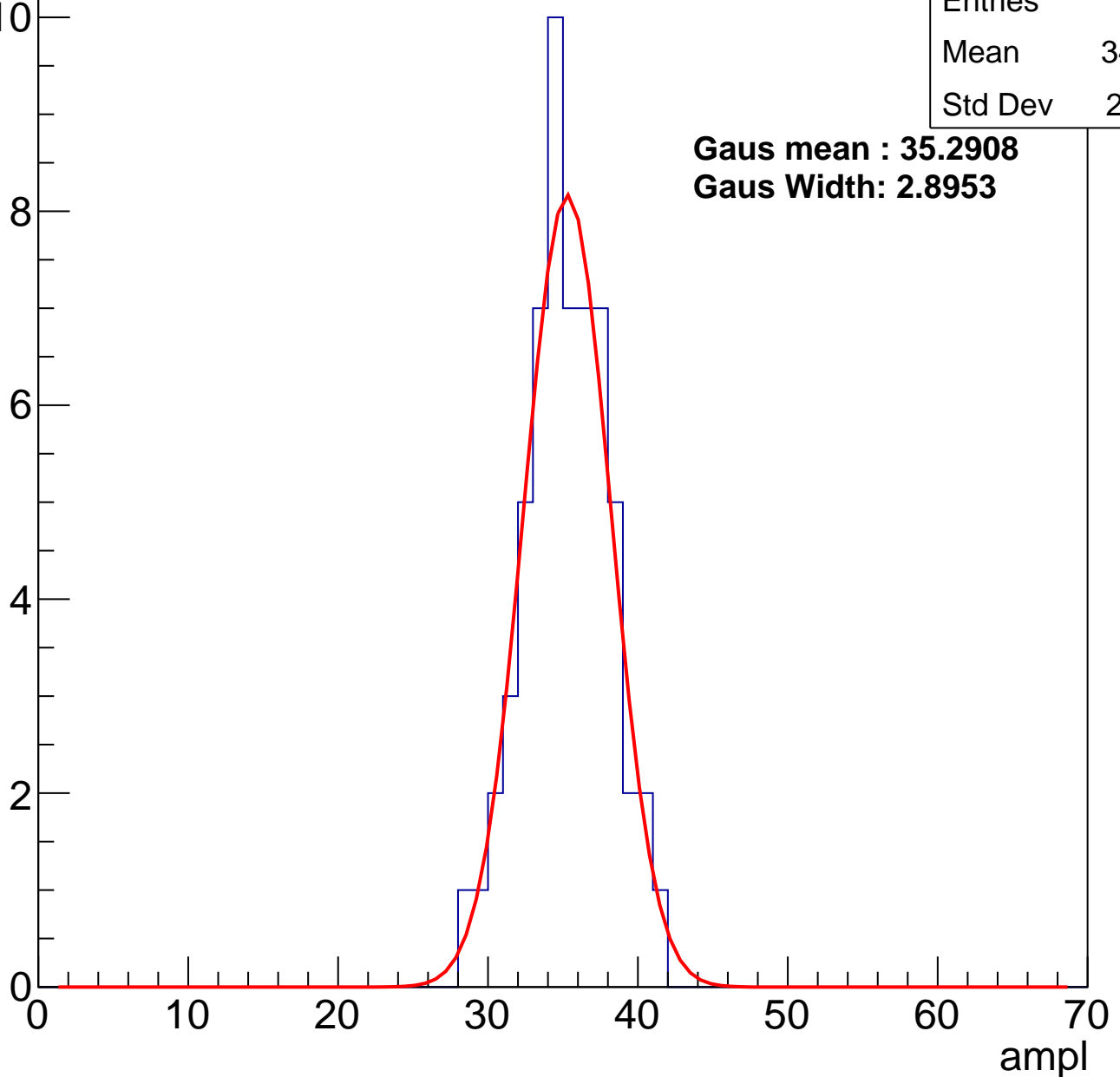
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	34.77
Std Dev	2.771

**Gaus mean : 35.2908**

**Gaus Width: 2.8953**



# B1L003S, U23-ch54, adc2

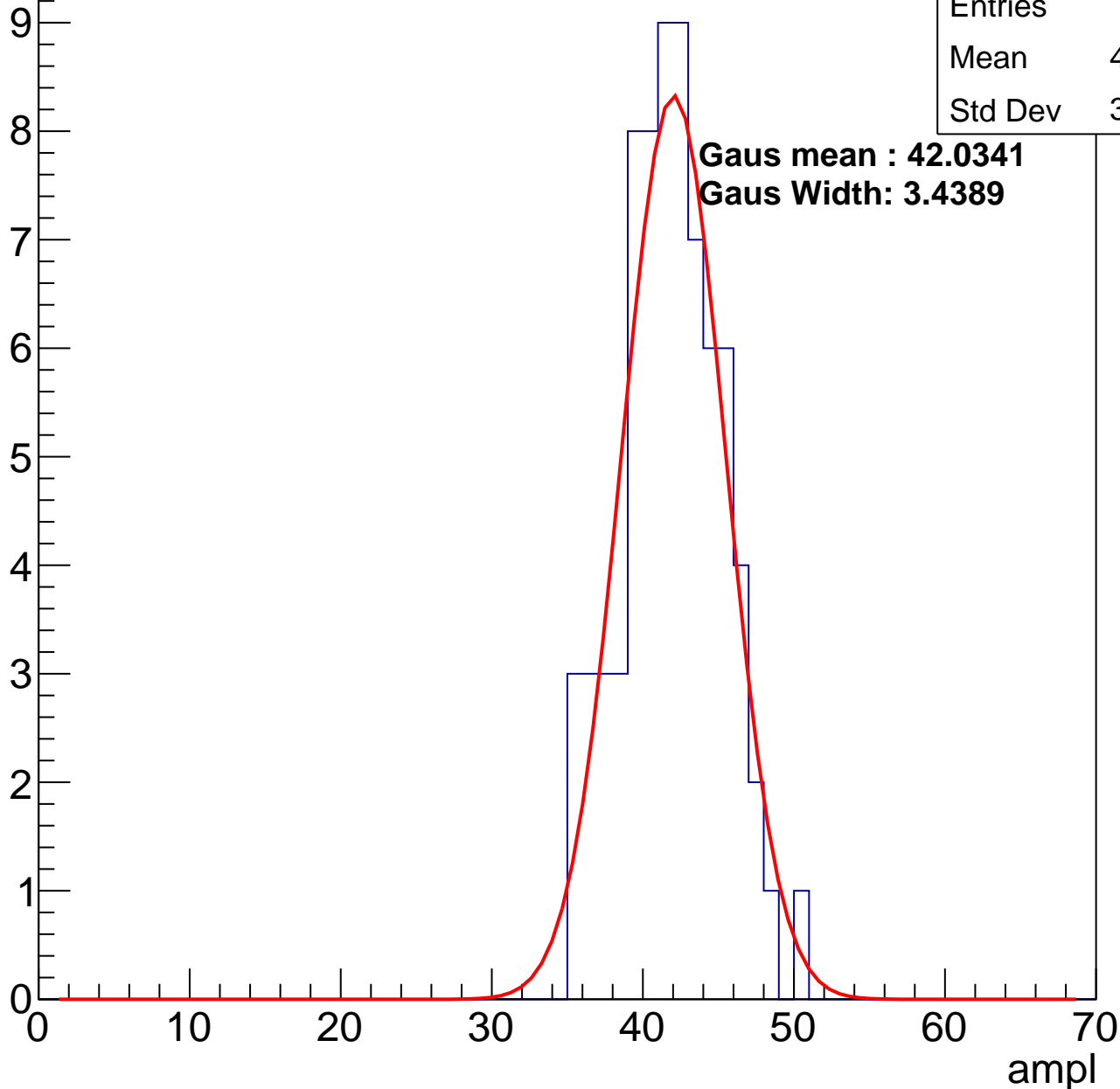
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	41.48
Std Dev	3.248

**Gaus mean : 42.0341**

**Gaus Width: 3.4389**

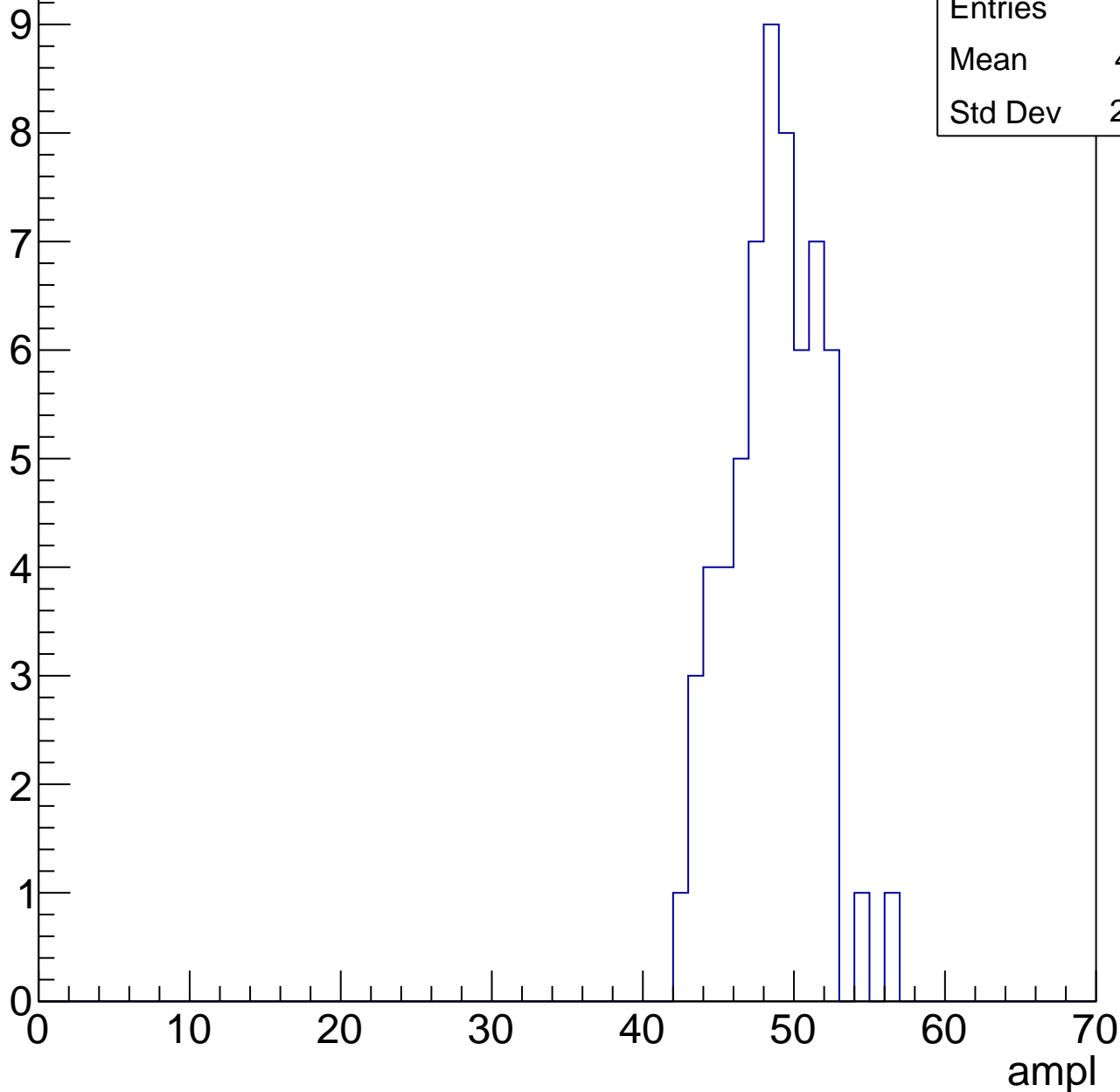


# B1L003S, U23-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	48.21
Std Dev	2.913

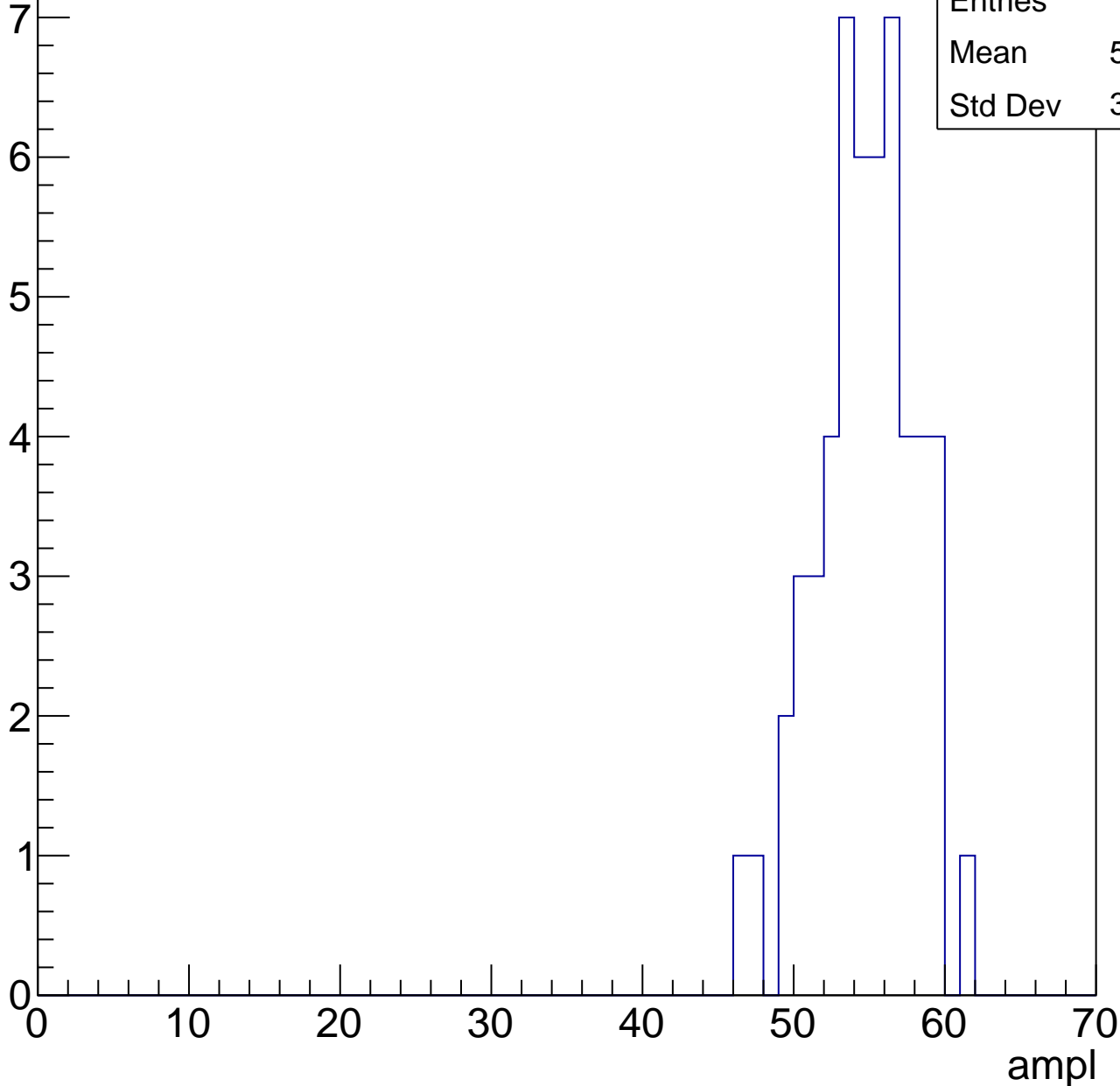


# B1L003S, U23-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	54.26
Std Dev	3.193

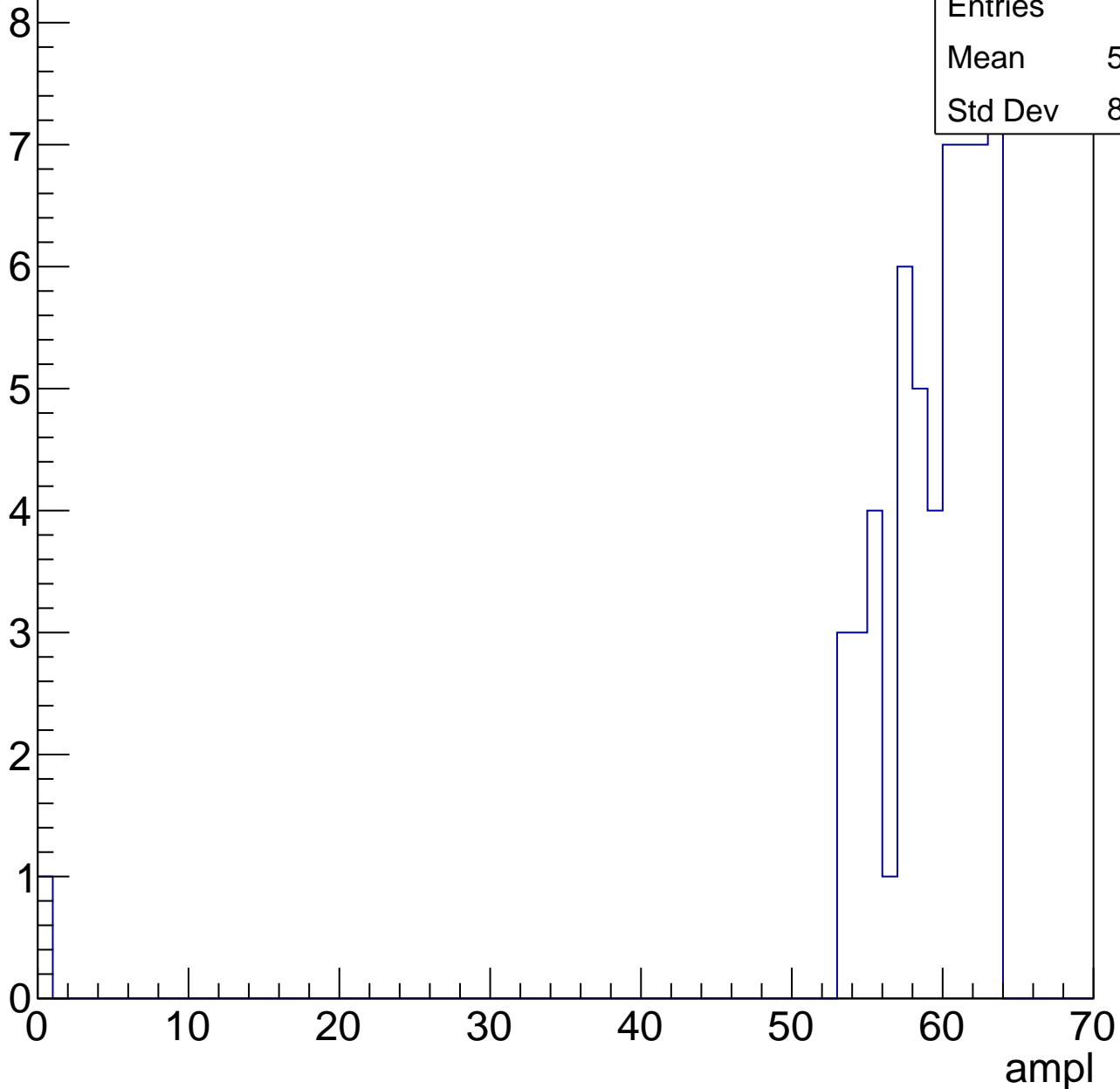


# B1L003S, U23-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.04
Std Dev	8.388



# B1L003S, U23-ch54, adc6

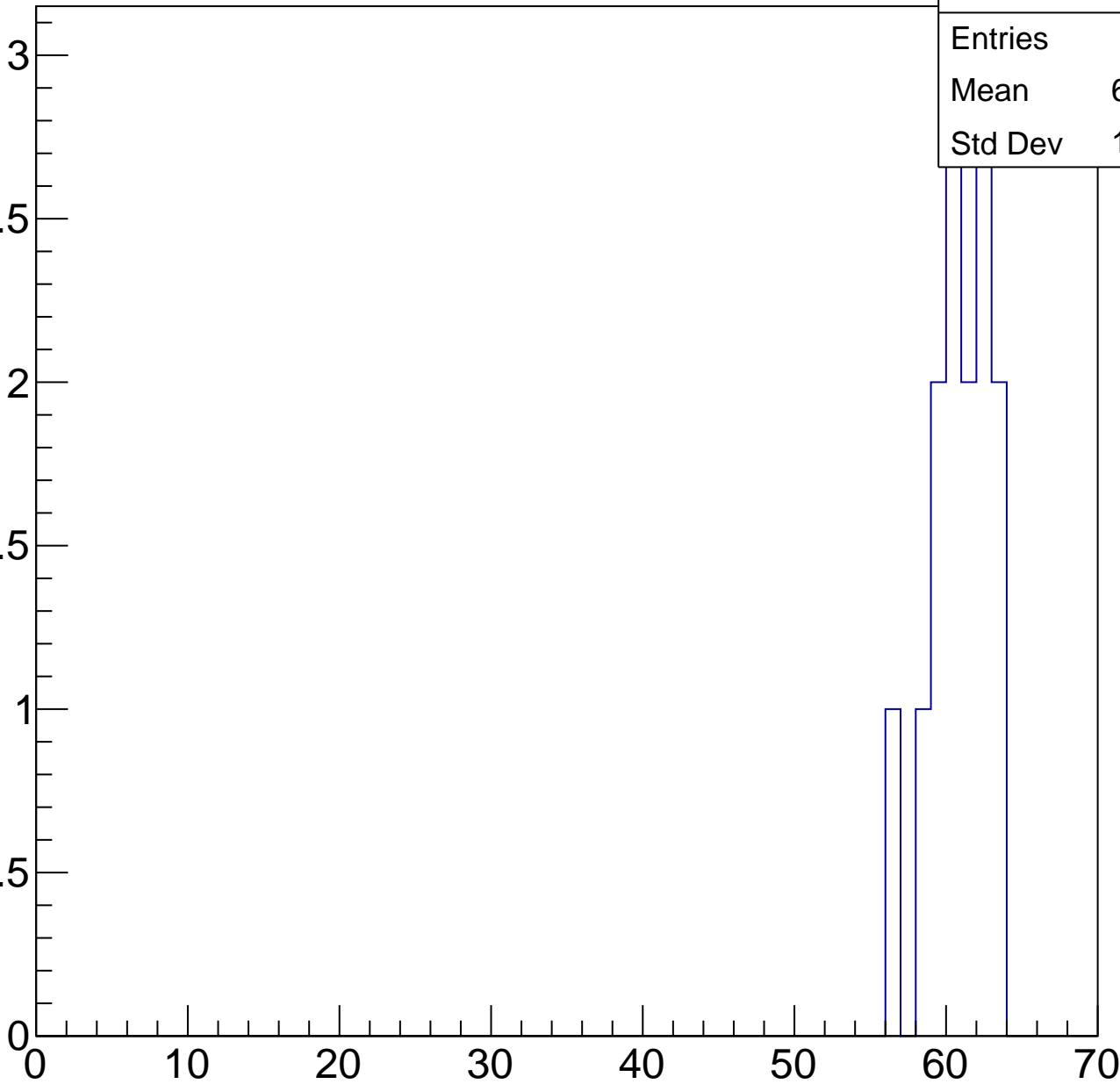
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	14
Mean	60.43
Std Dev	1.917

ampl

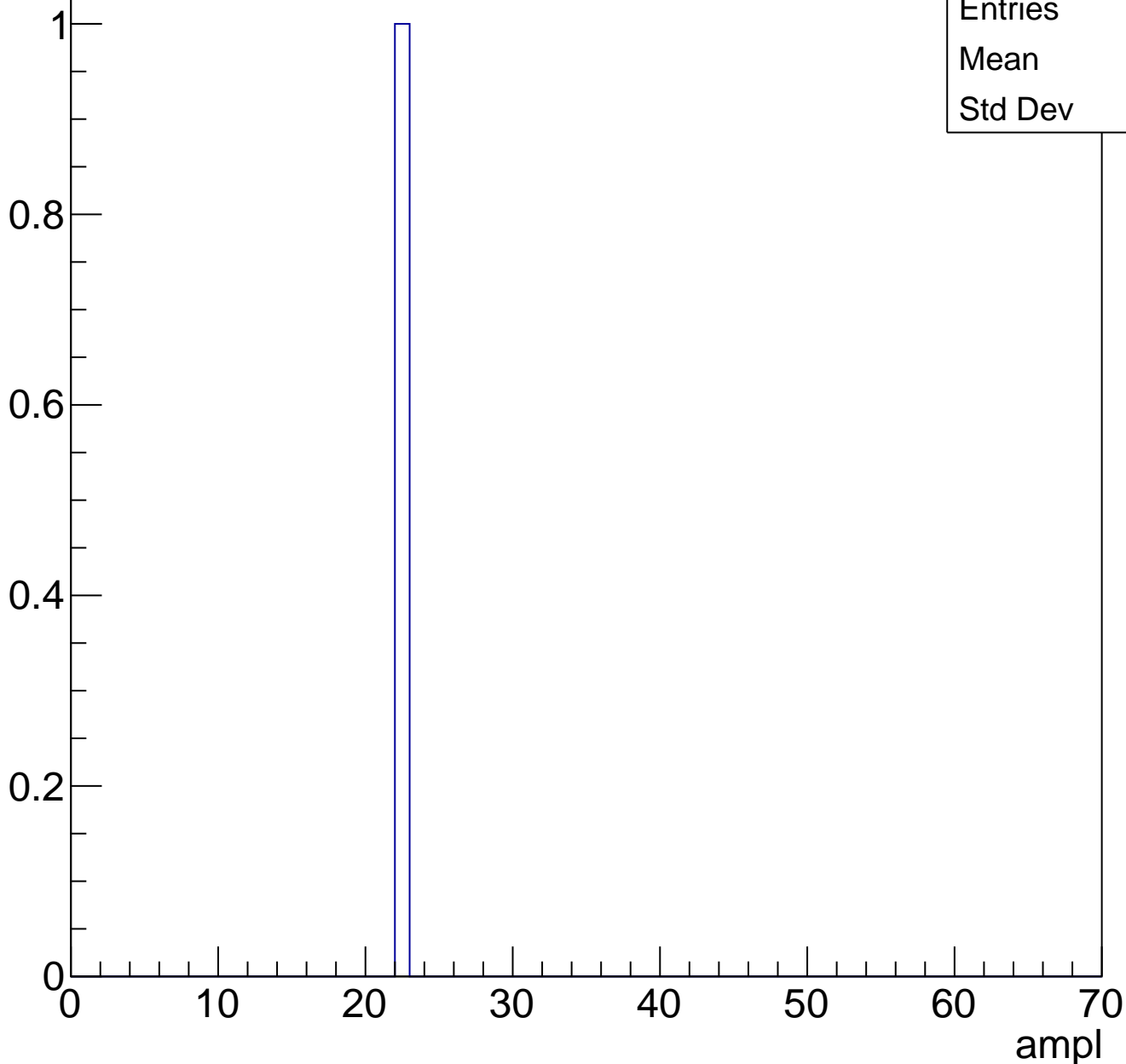




# B1L003S, U23-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch55, adc0

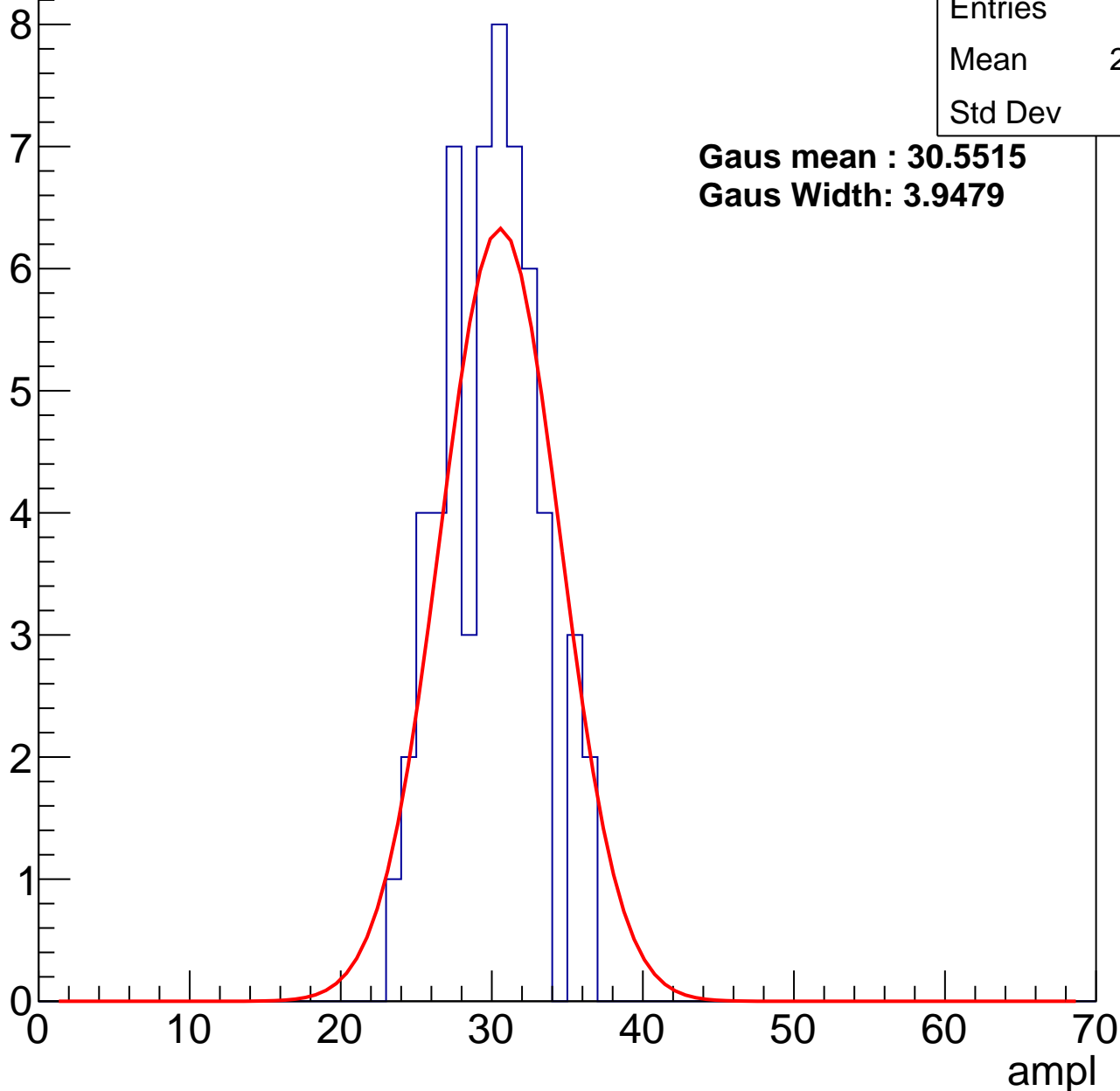
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	29.47
Std Dev	3.12

**Gaus mean : 30.5515**

**Gaus Width: 3.9479**



# B1L003S, U23-ch55, adc1

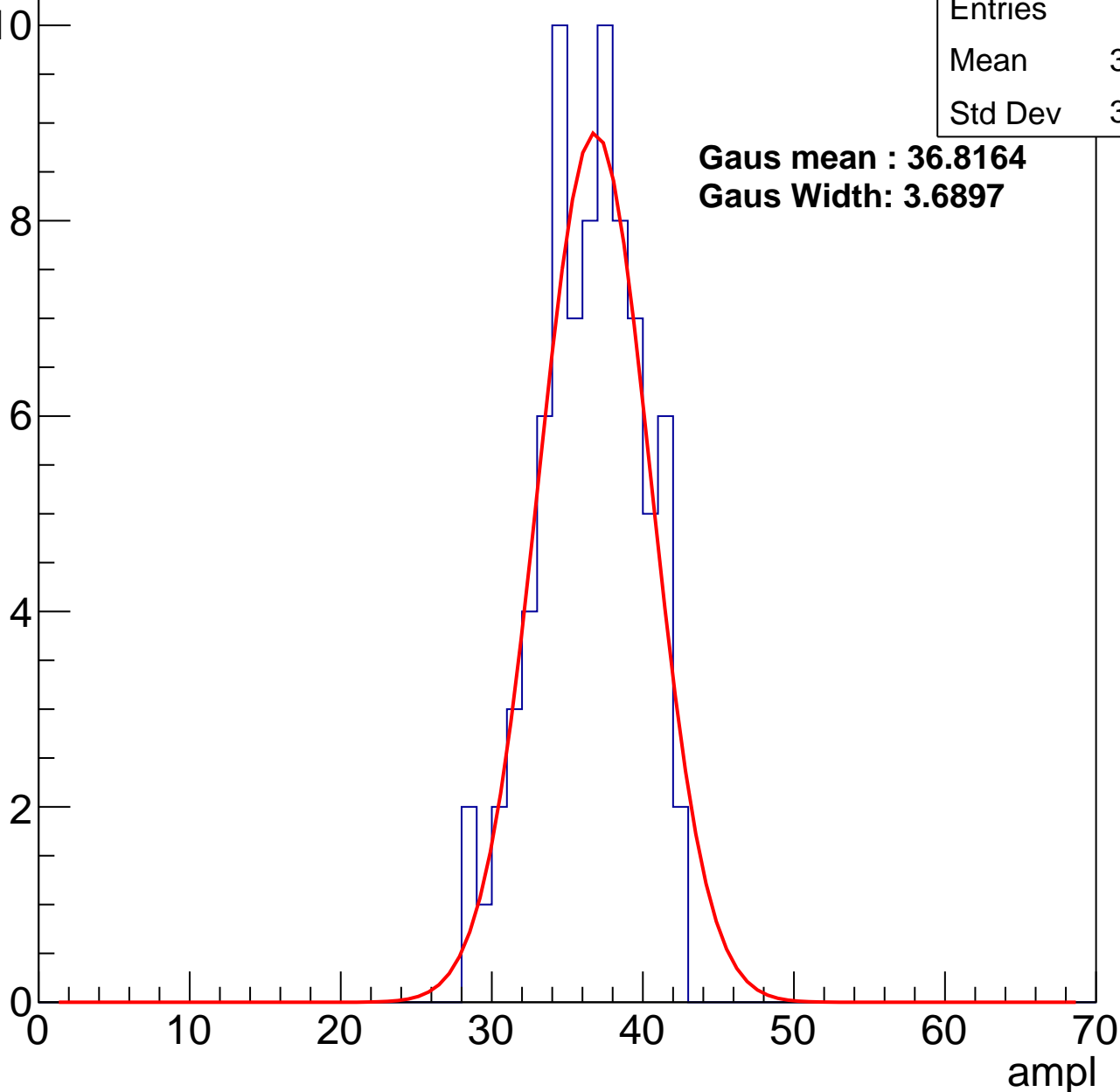
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	81
Mean	35.98
Std Dev	3.326

**Gaus mean : 36.8164**

**Gaus Width: 3.6897**



# B1L003S, U23-ch55, adc2

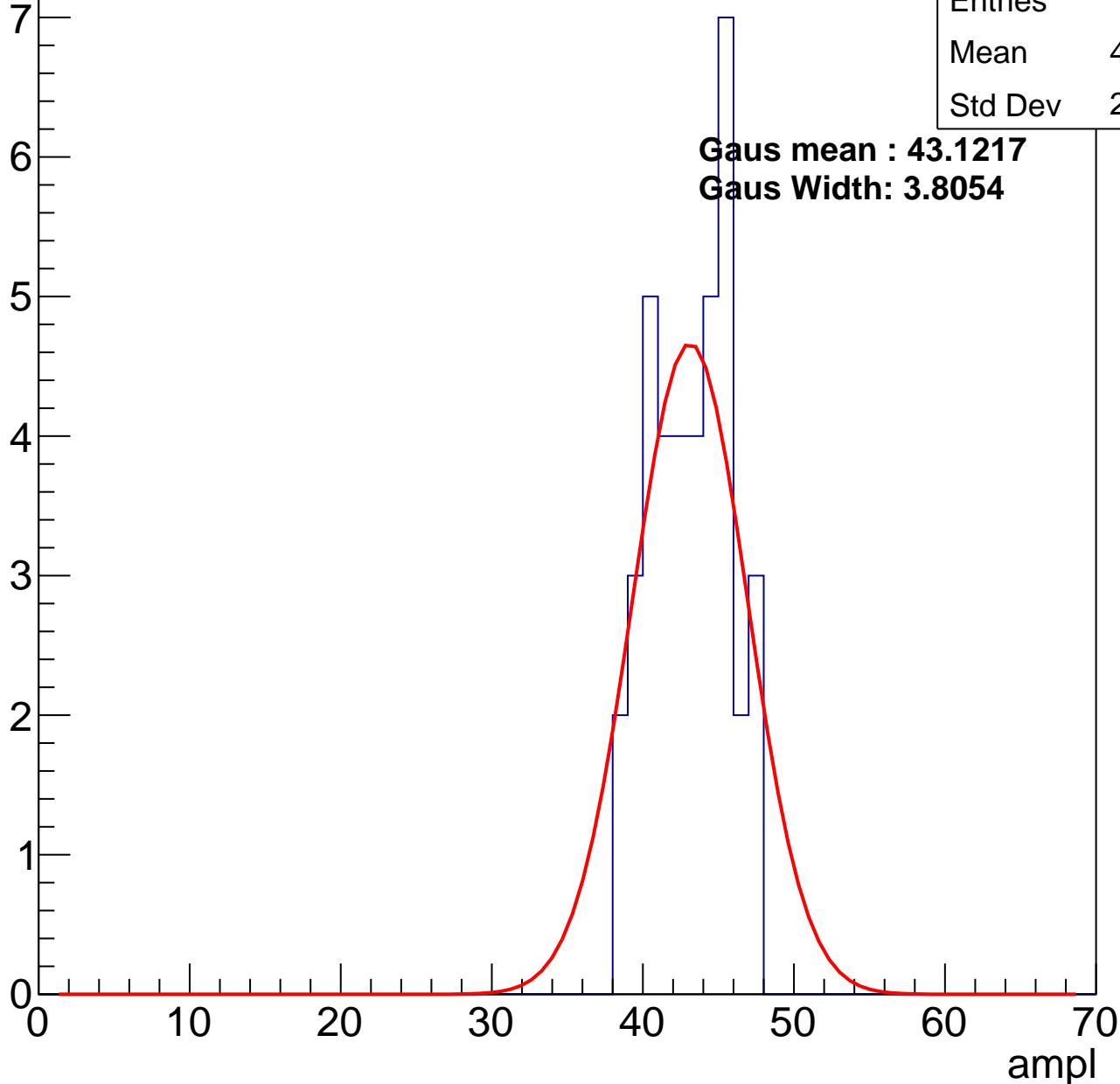
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	42.69
Std Dev	2.574

**Gaus mean : 43.1217**

**Gaus Width: 3.8054**

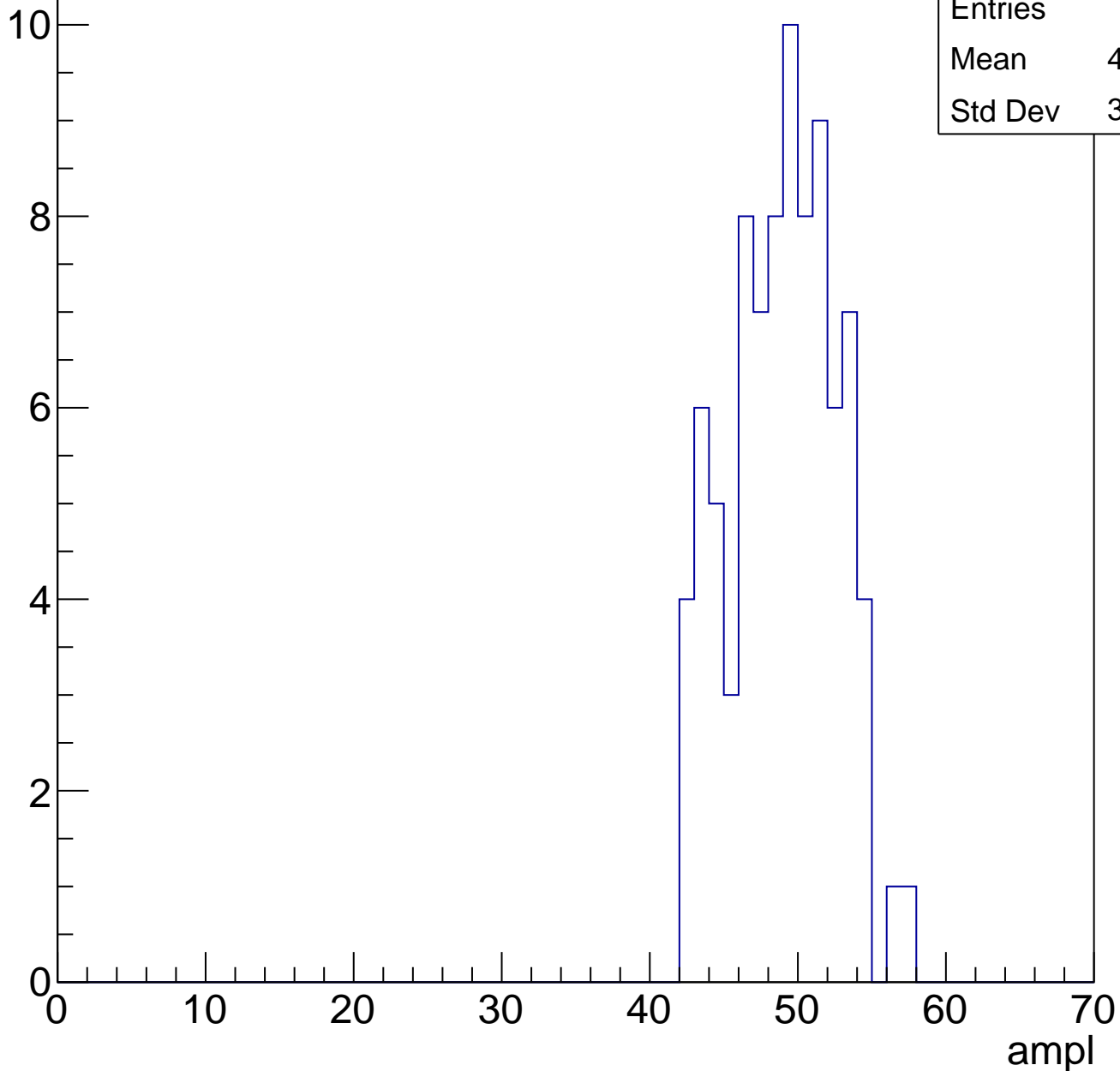


# B1L003S, U23-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	87
Mean	48.54
Std Dev	3.552

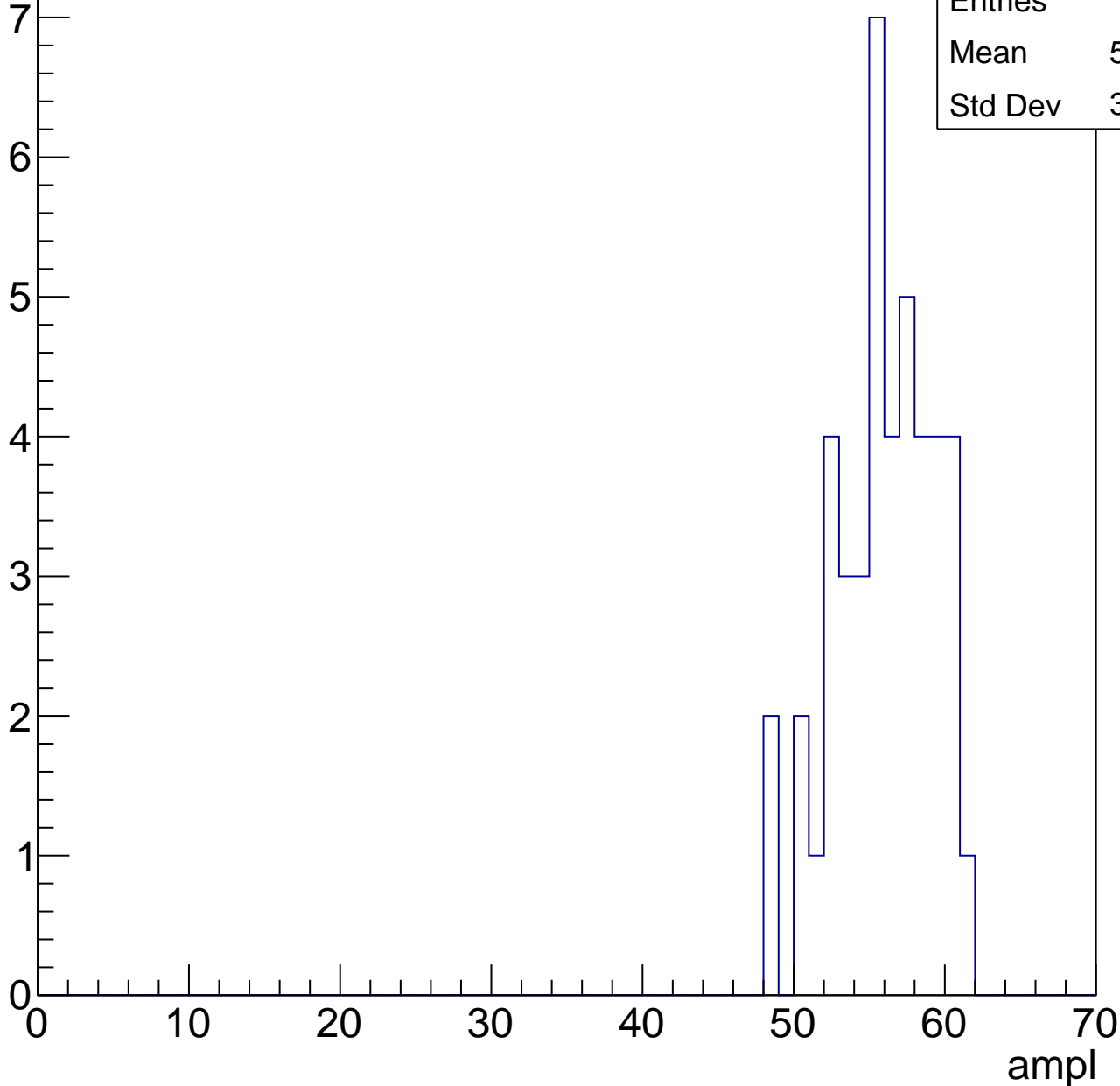


# B1L003S, U23-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	55.43
Std Dev	3.257

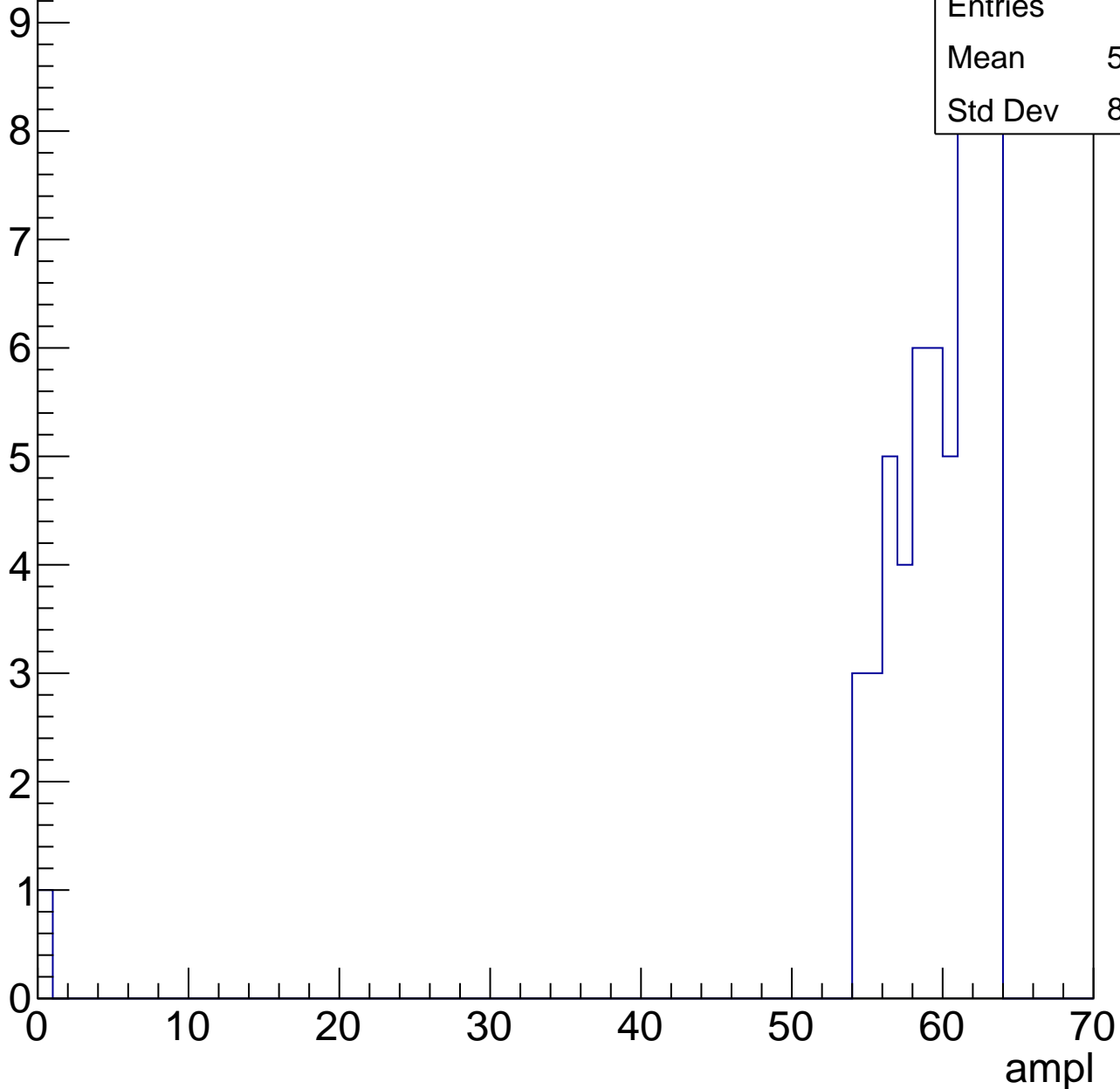


# B1L003S, U23-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	58.38
Std Dev	8.189



# B1L003S, U23-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch56, adc0

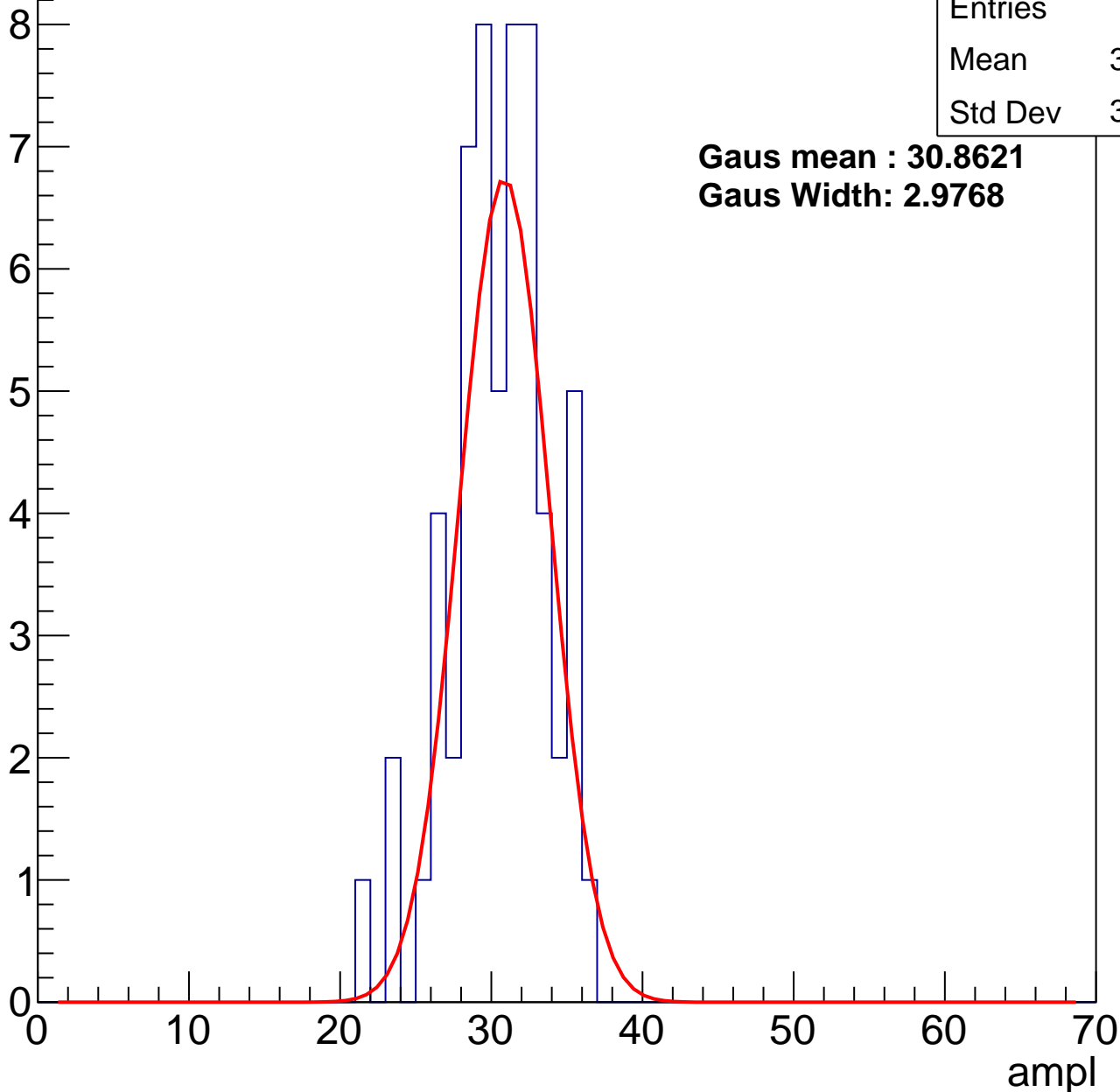
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	30.05
Std Dev	3.192

**Gaus mean : 30.8621**

**Gaus Width: 2.9768**



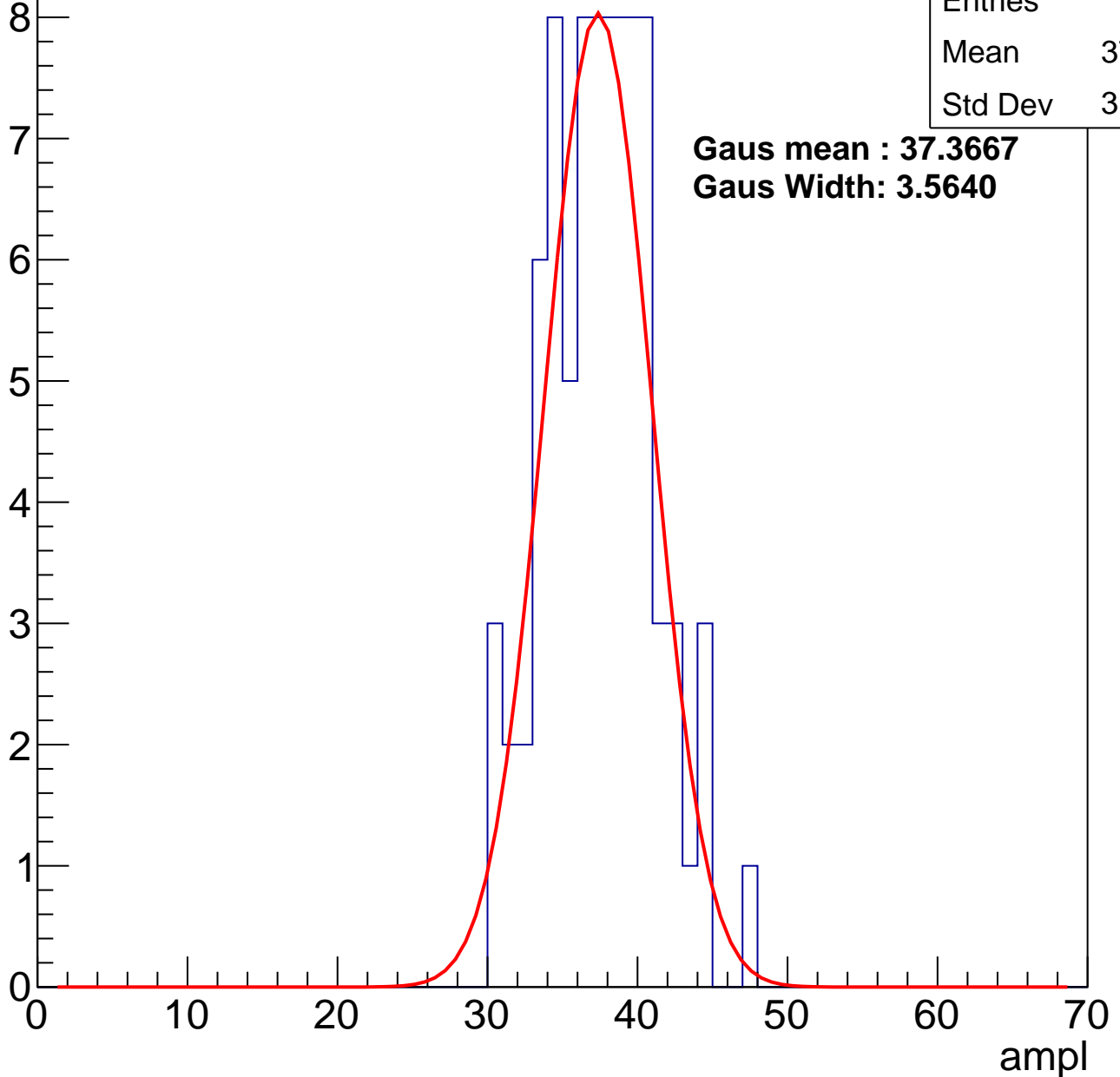
# B1L003S, U23-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	37.04
Std Dev	3.573

**Gaus mean : 37.3667**  
**Gaus Width: 3.5640**



# B1L003S, U23-ch56, adc2

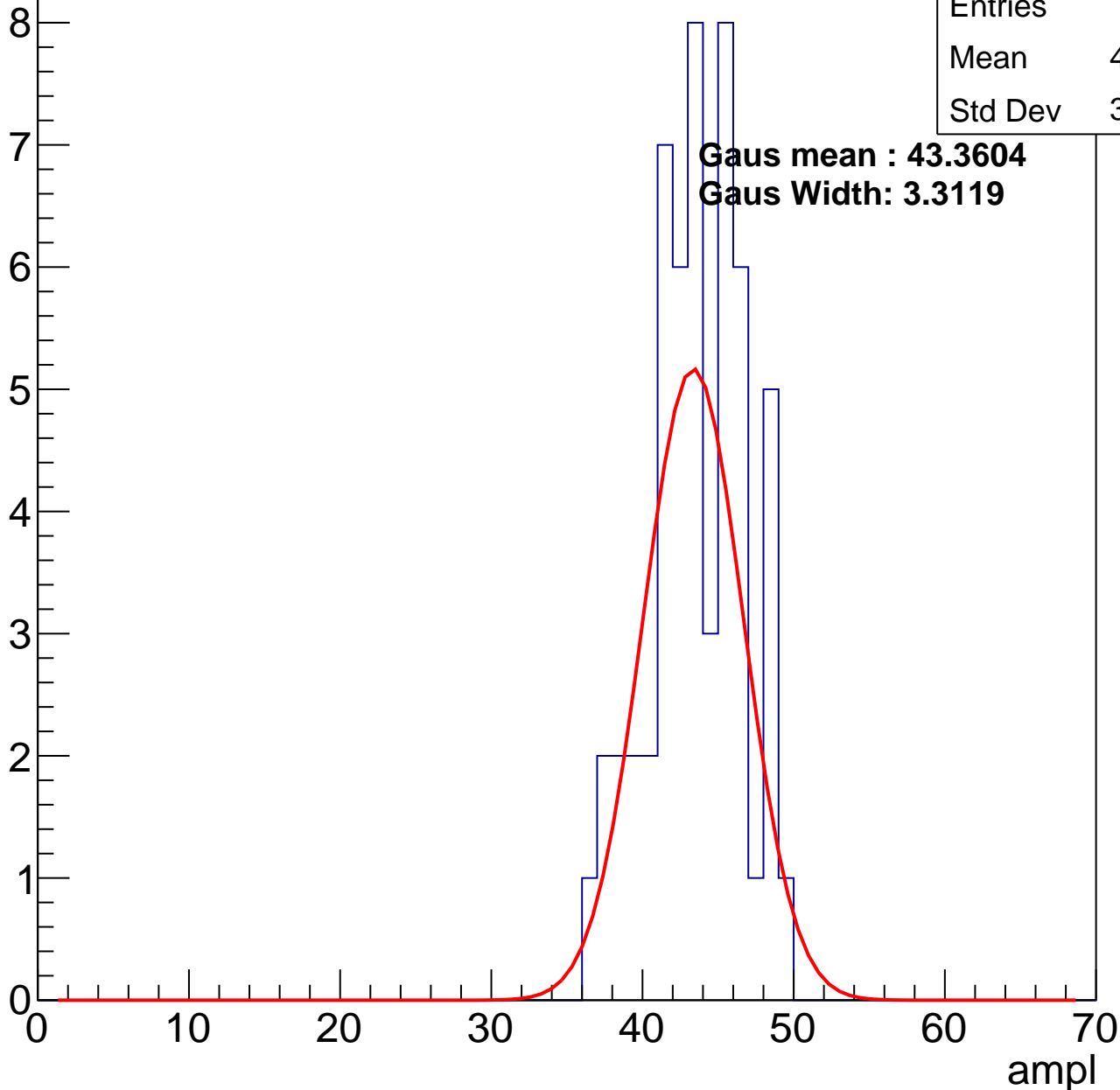
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.17
Std Dev	3.102

**Gaus mean : 43.3604**

**Gaus Width: 3.3119**

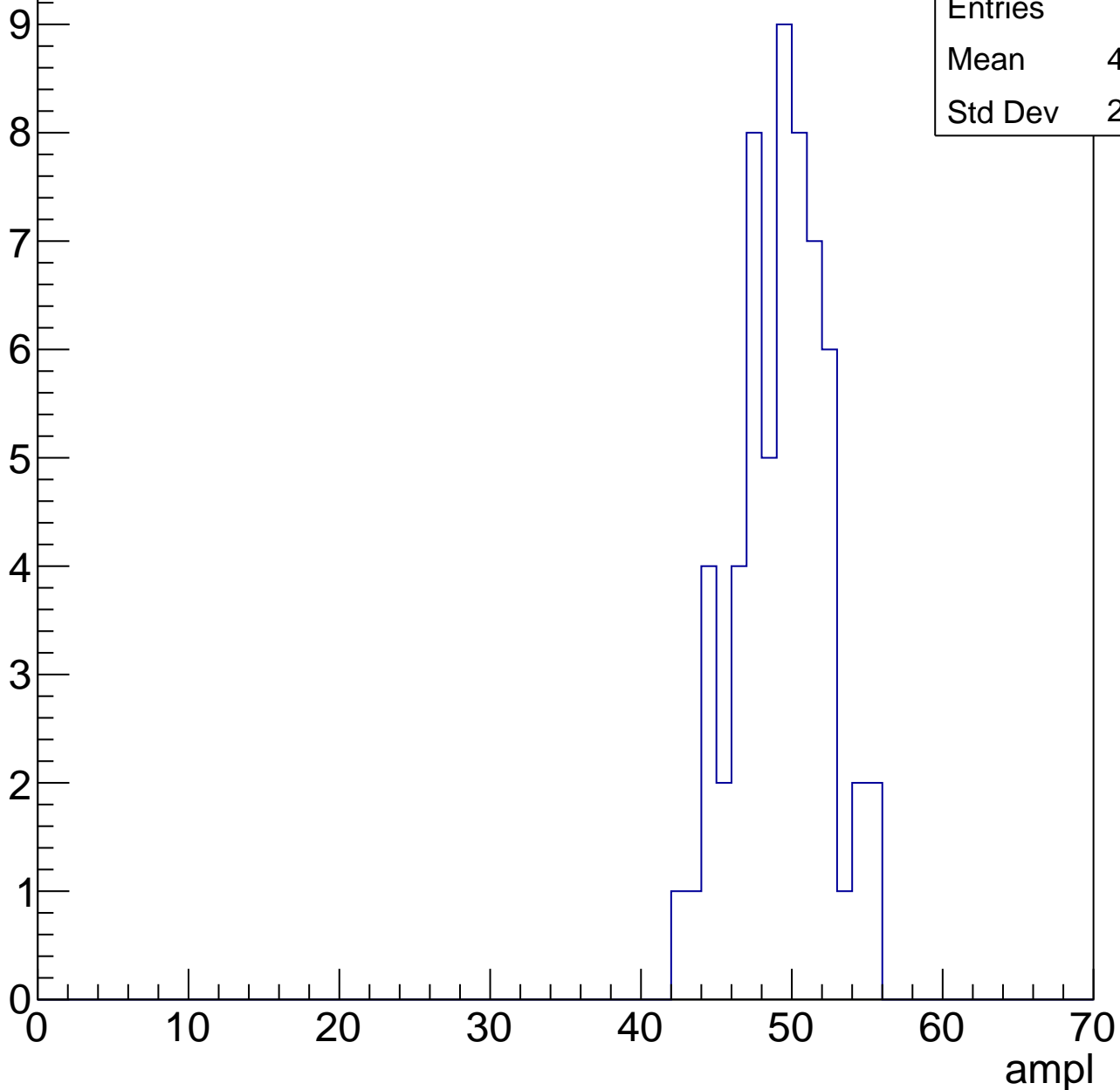


# B1L003S, U23-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.87
Std Dev	2.935

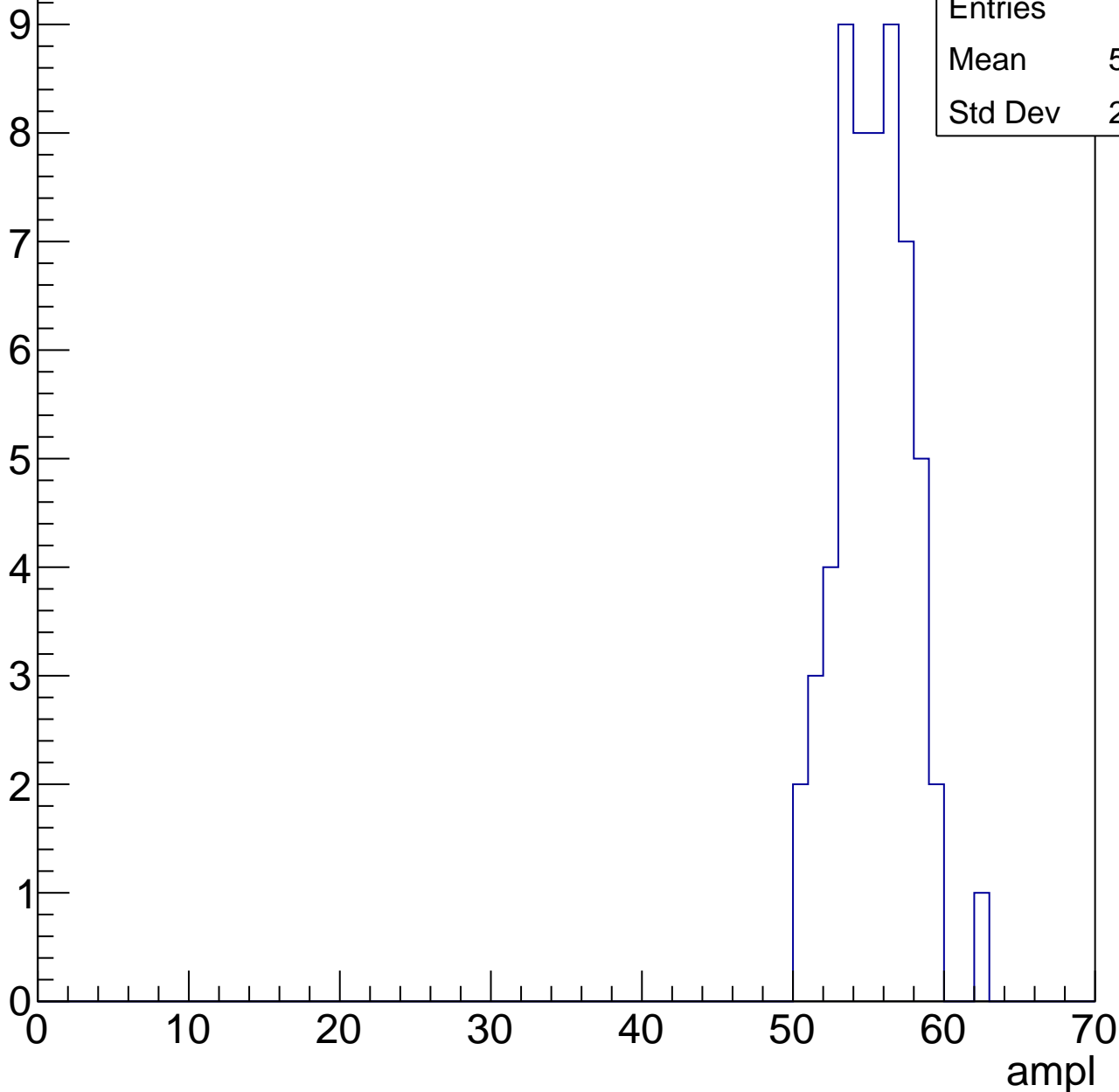


# B1L003S, U23-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.88
Std Dev	2.422



# B1L003S, U23-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	54
Mean	59.24
Std Dev	8.338

Entry

10

8

6

4

2

0

0

10

20

30

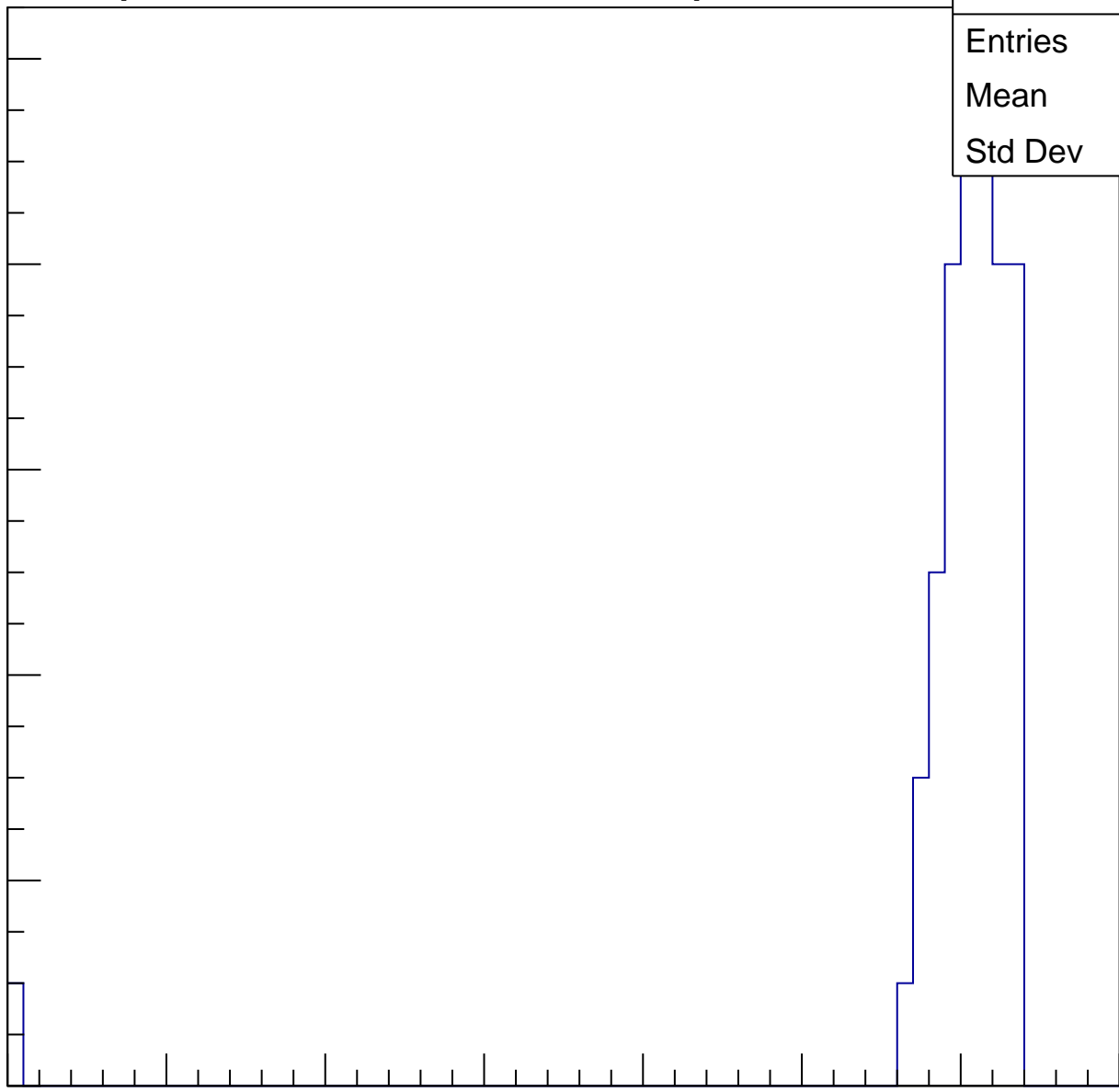
40

50

60

70

ampl



# B1L003S, U23-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch57, adc0

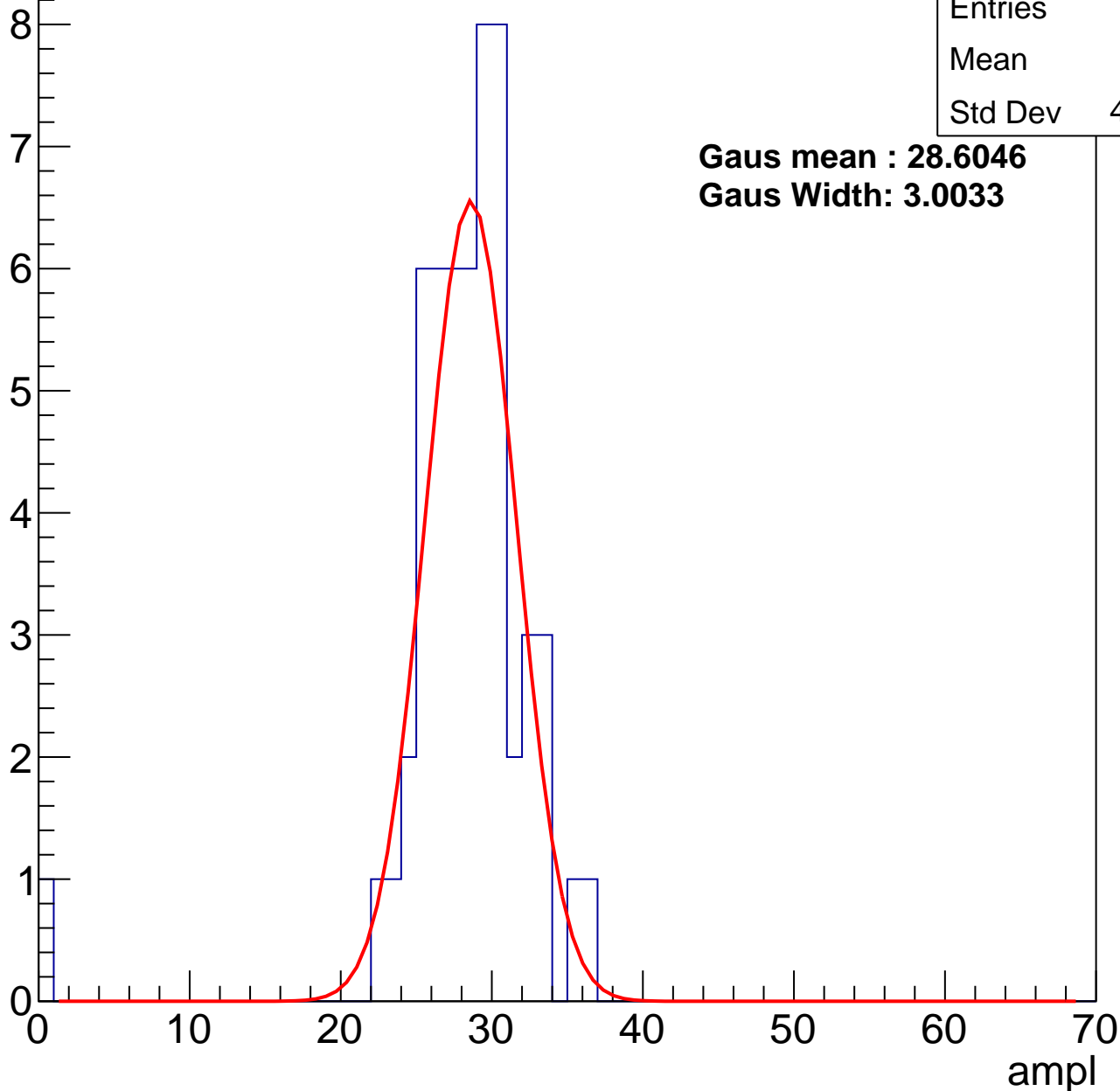
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	27.8
Std Dev	4.773

**Gaus mean : 28.6046**

**Gaus Width: 3.0033**

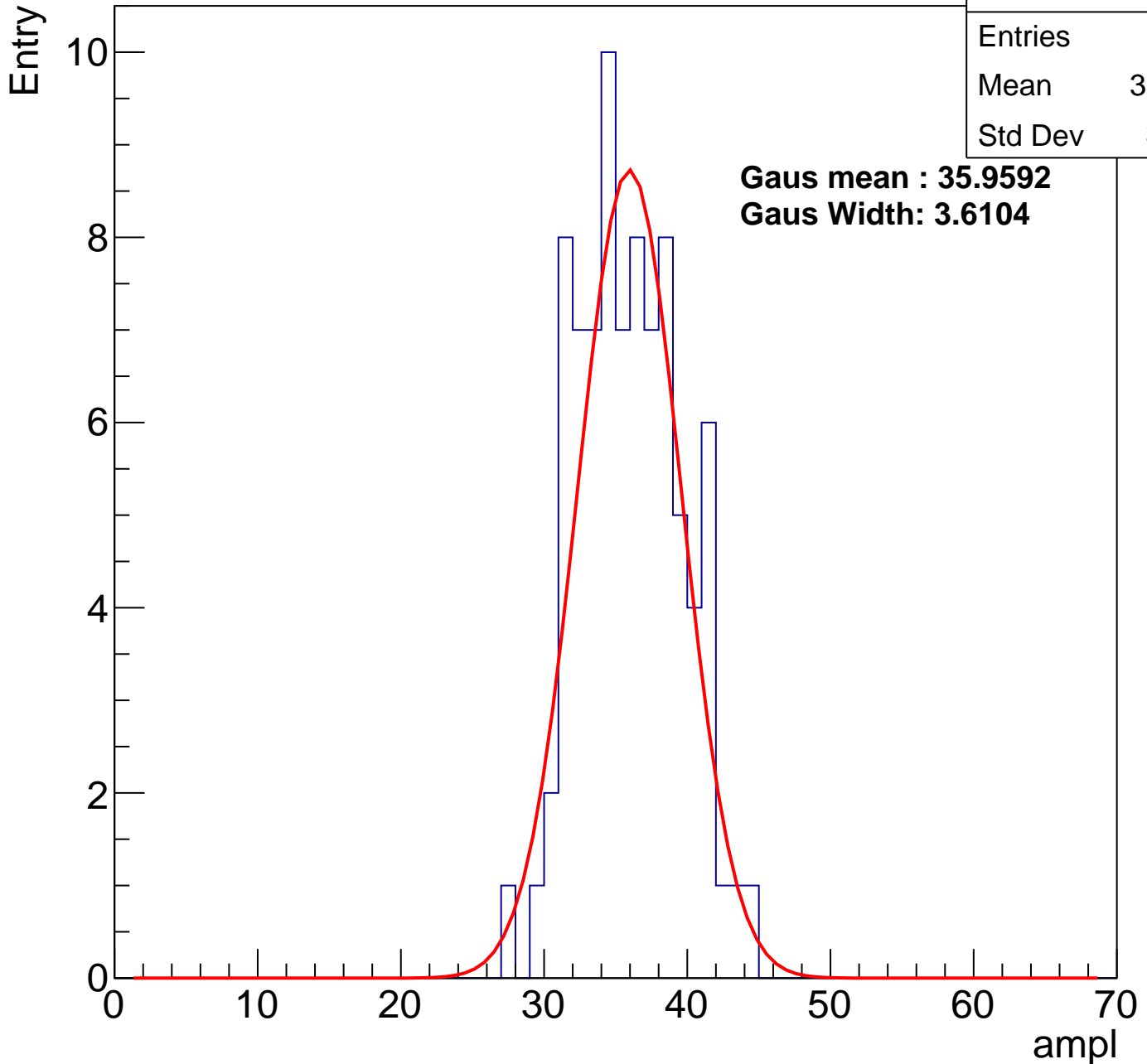


# B1L003S, U23-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	84
Mean	35.54
Std Dev	3.54

**Gaus mean : 35.9592**  
**Gaus Width: 3.6104**



# B1L003S, U23-ch57, adc2

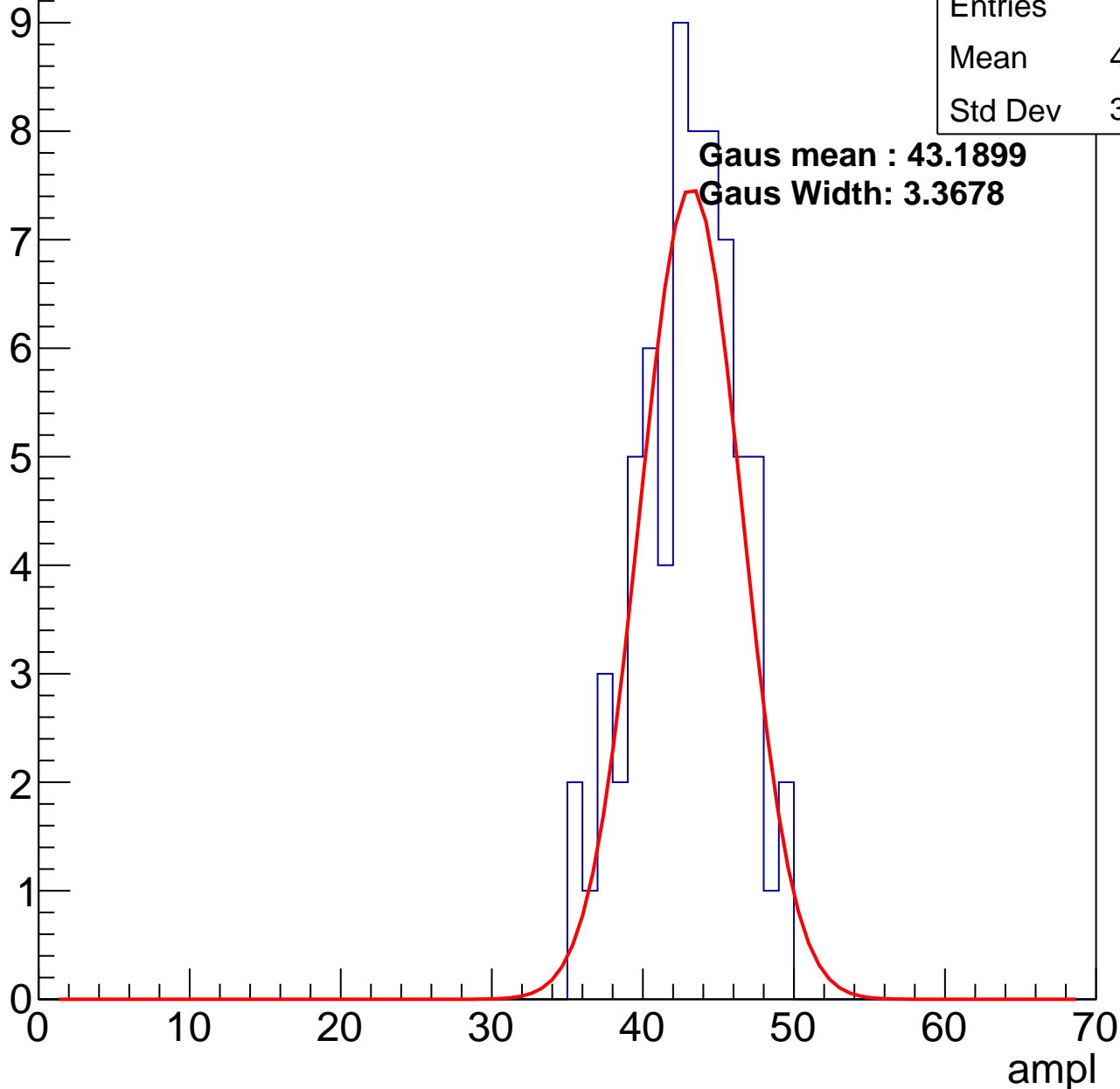
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.53
Std Dev	3.292

**Gaus mean : 43.1899**

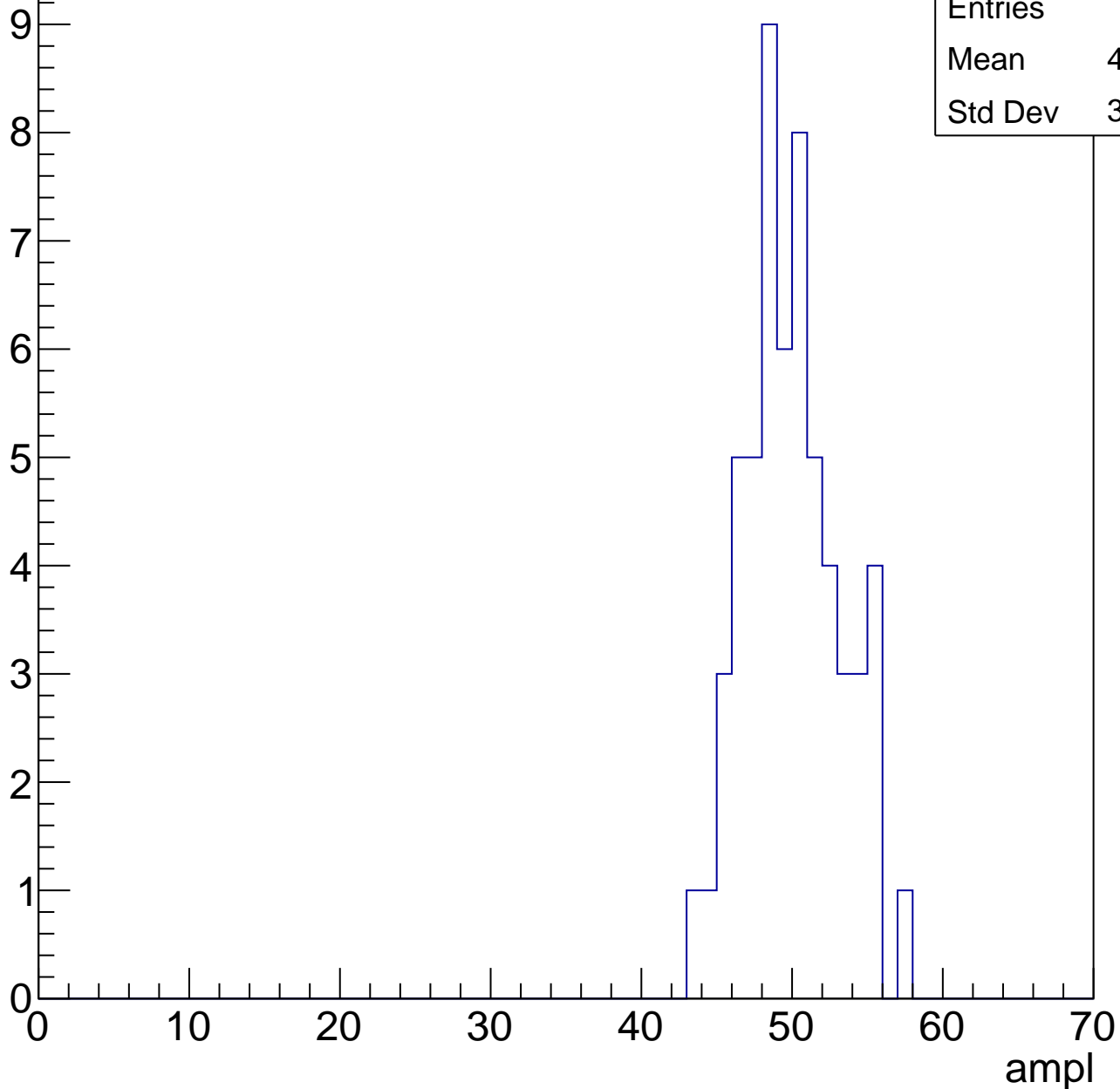
**Gaus Width: 3.3678**



# B1L003S, U23-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

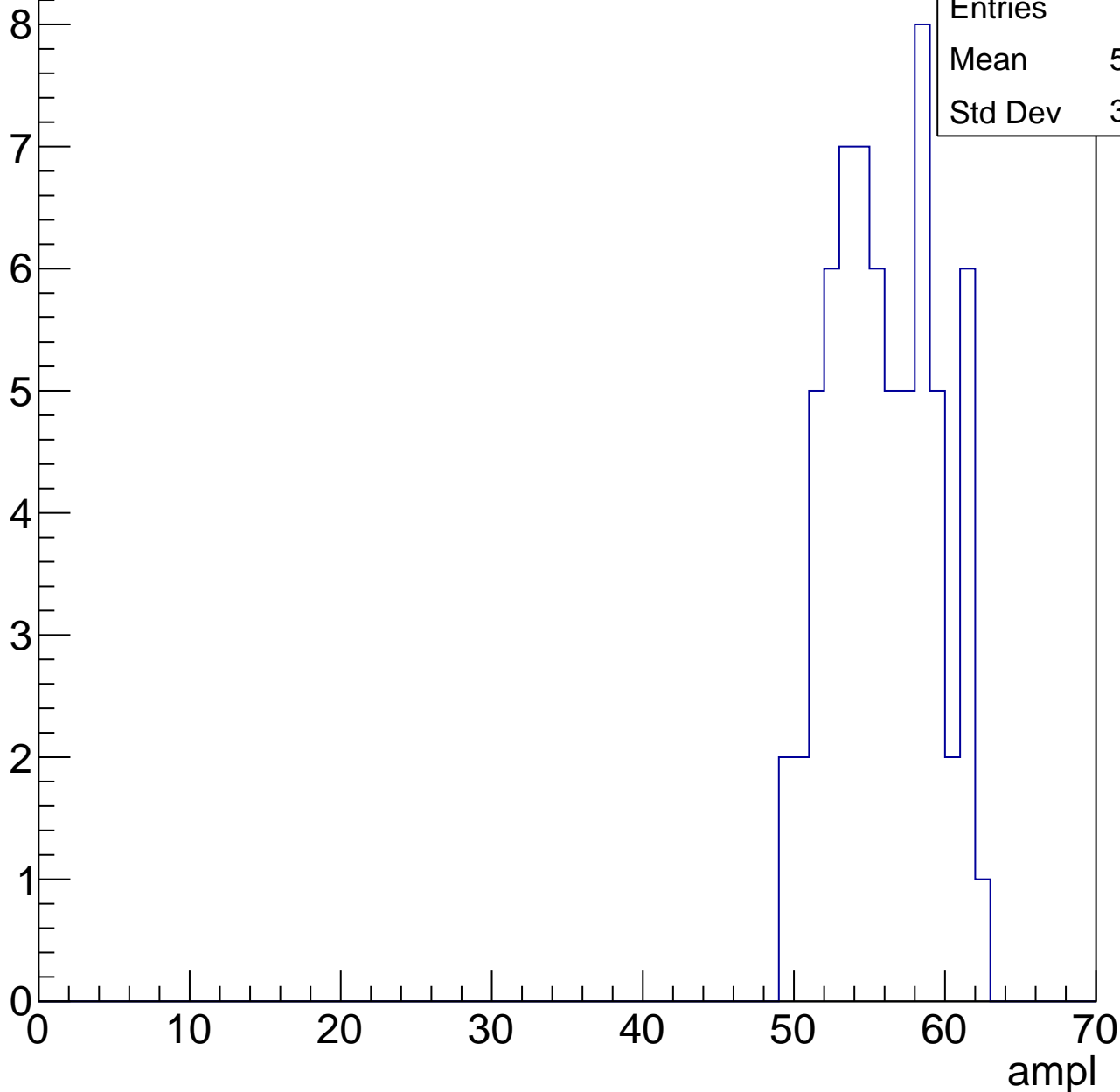


# B1L003S, U23-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.46
Std Dev	3.387

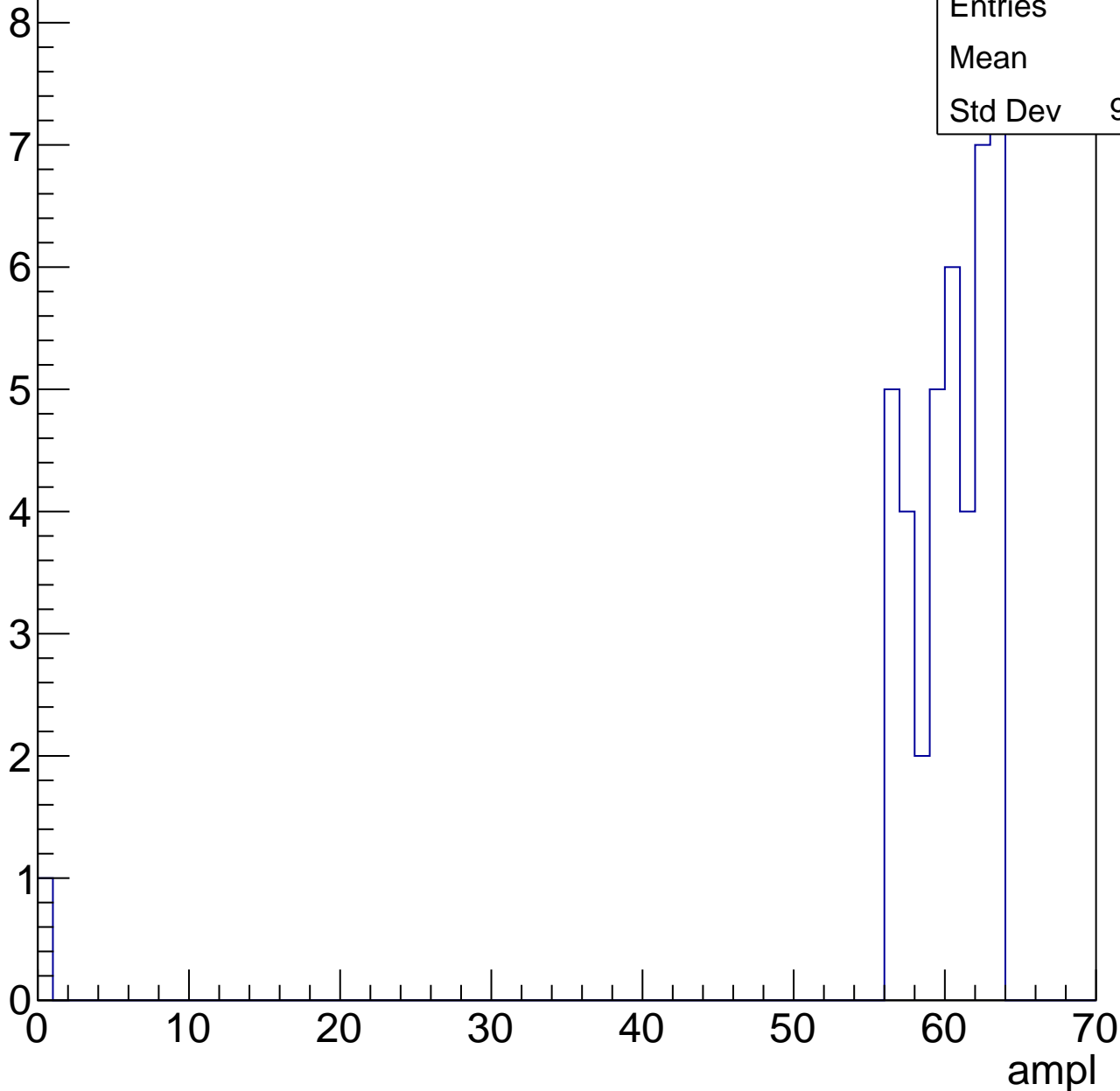


# B1L003S, U23-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.6
Std Dev	9.449



# B1L003S, U23-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.67
Std Dev	1.247

0 10 20 30 40 50 60 70

ampl



# B1L003S, U23-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch58, adc0

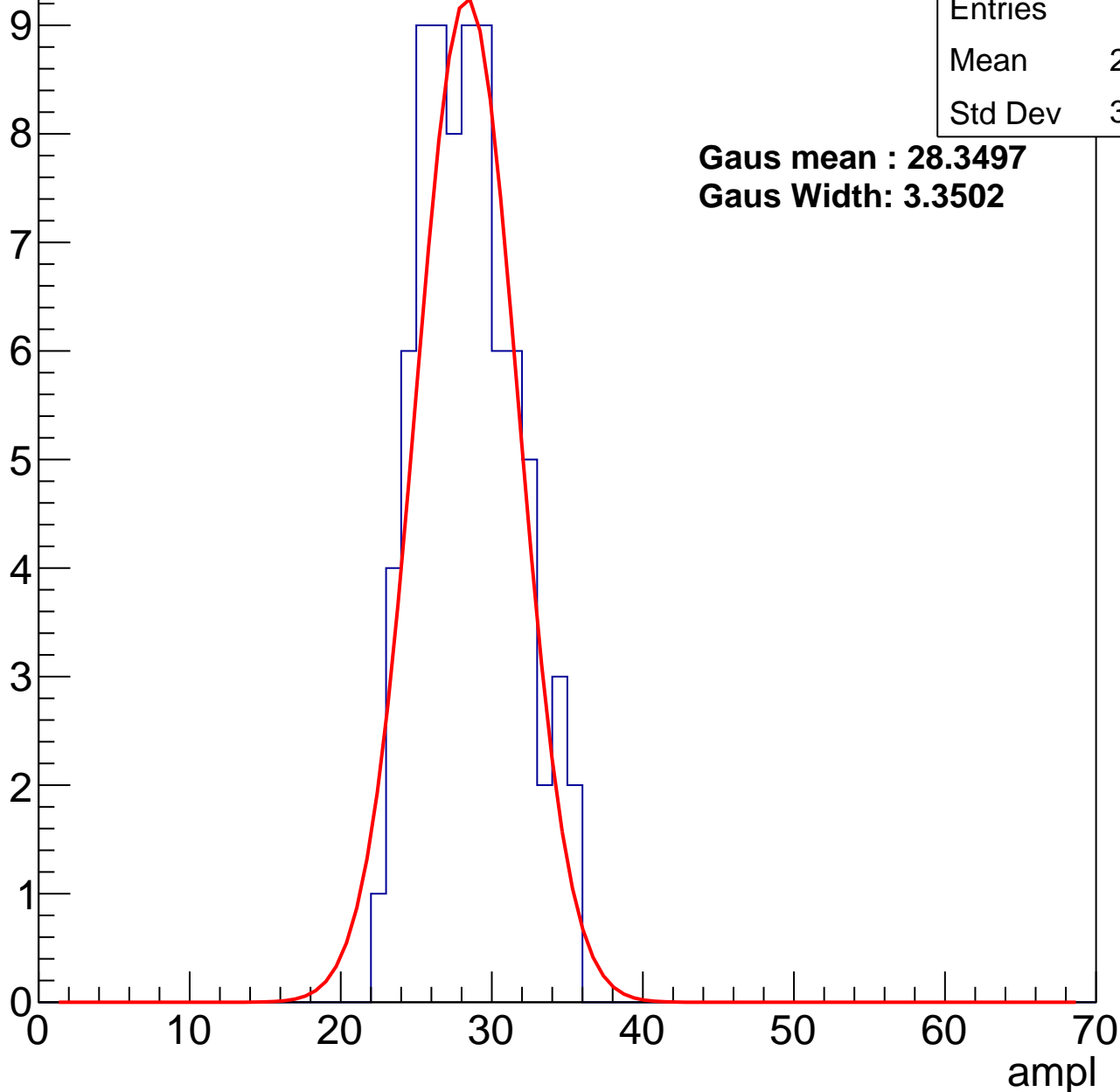
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	27.97
Std Dev	3.142

**Gaus mean : 28.3497**

**Gaus Width: 3.3502**



# B1L003S, U23-ch58, adc1

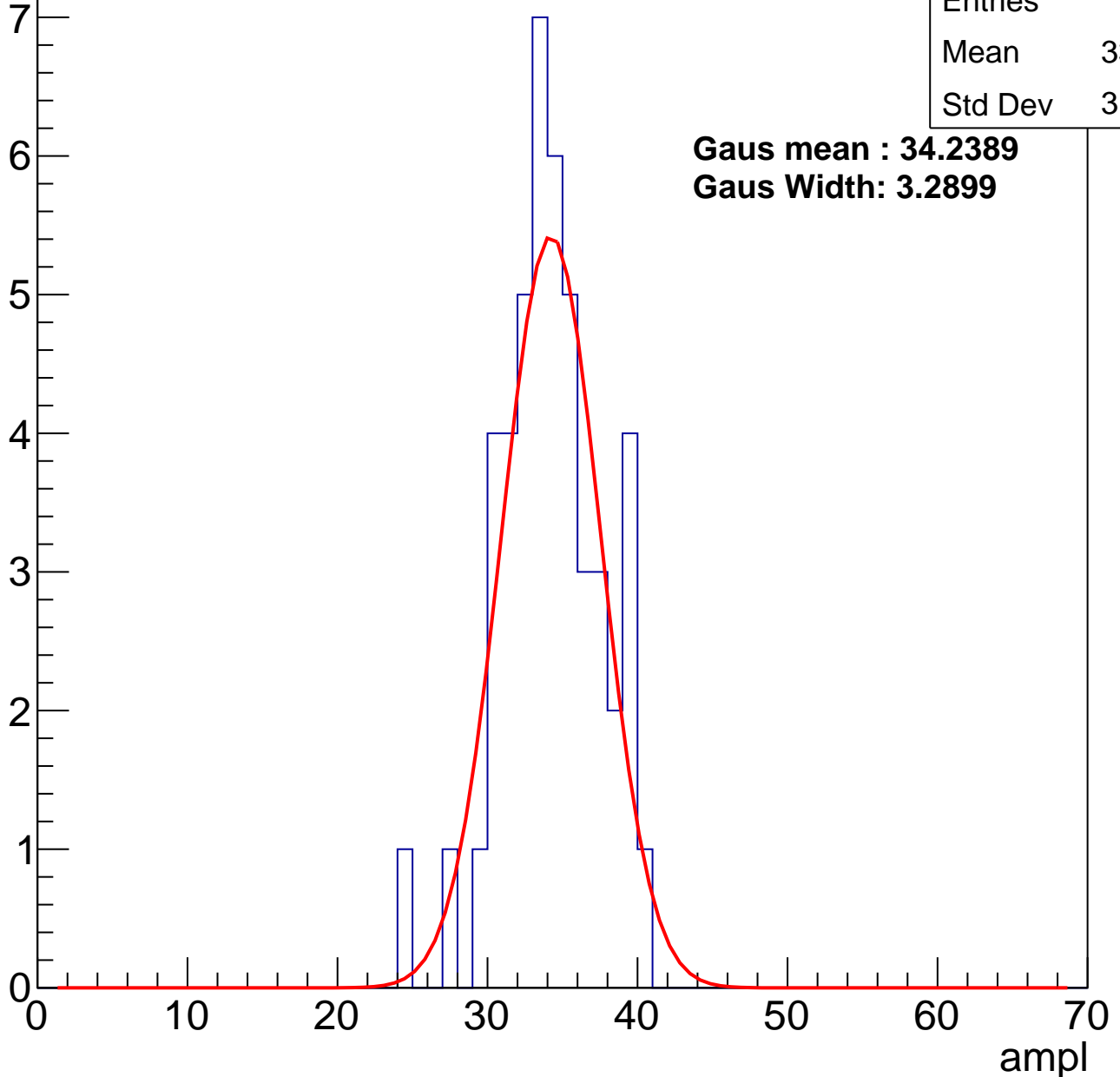
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	33.72
Std Dev	3.292

**Gaus mean : 34.2389**

**Gaus Width: 3.2899**



# B1L003S, U23-ch58, adc2

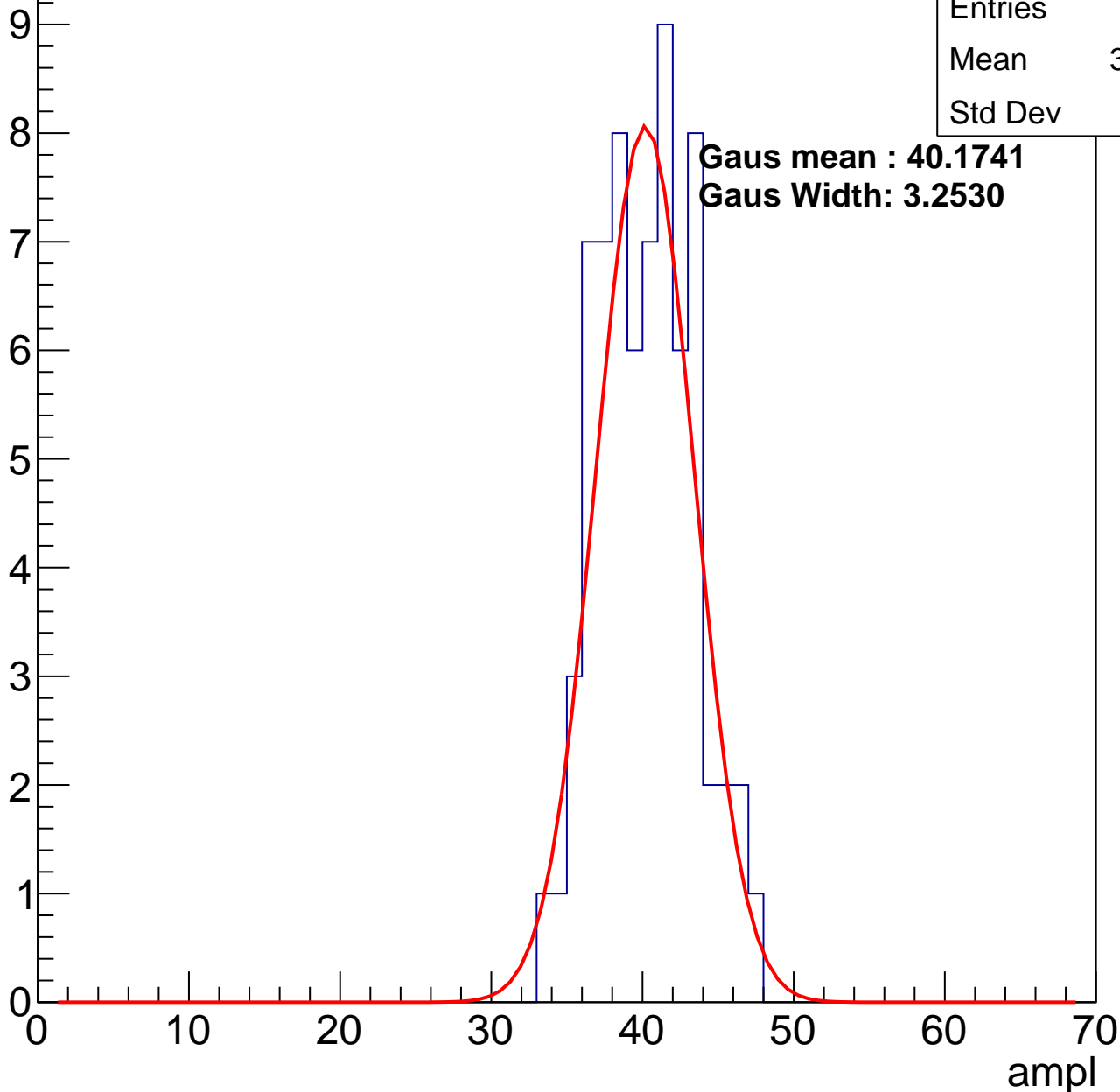
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	39.76
Std Dev	3.11

**Gaus mean : 40.1741**

**Gaus Width: 3.2530**

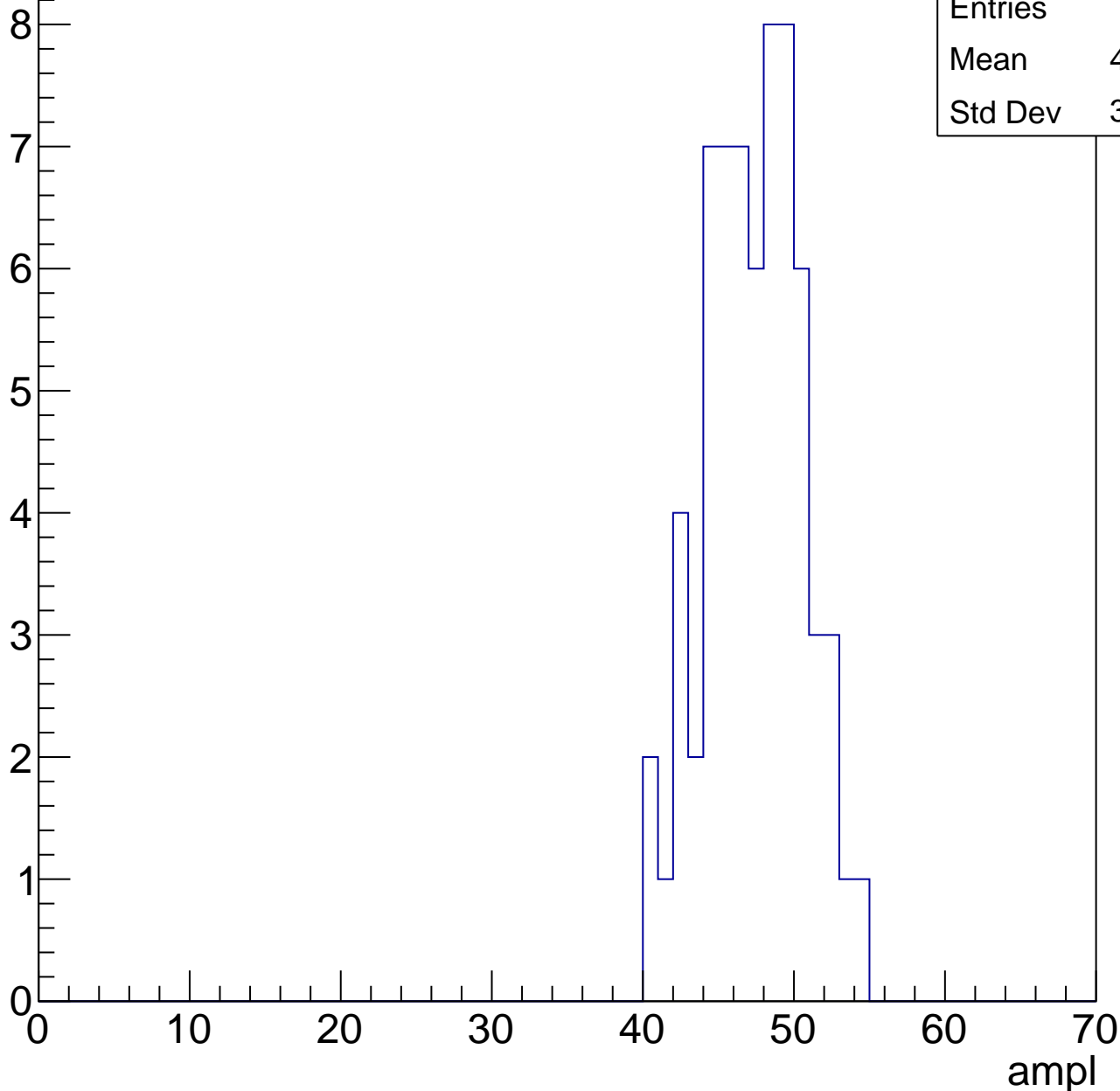


# B1L003S, U23-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

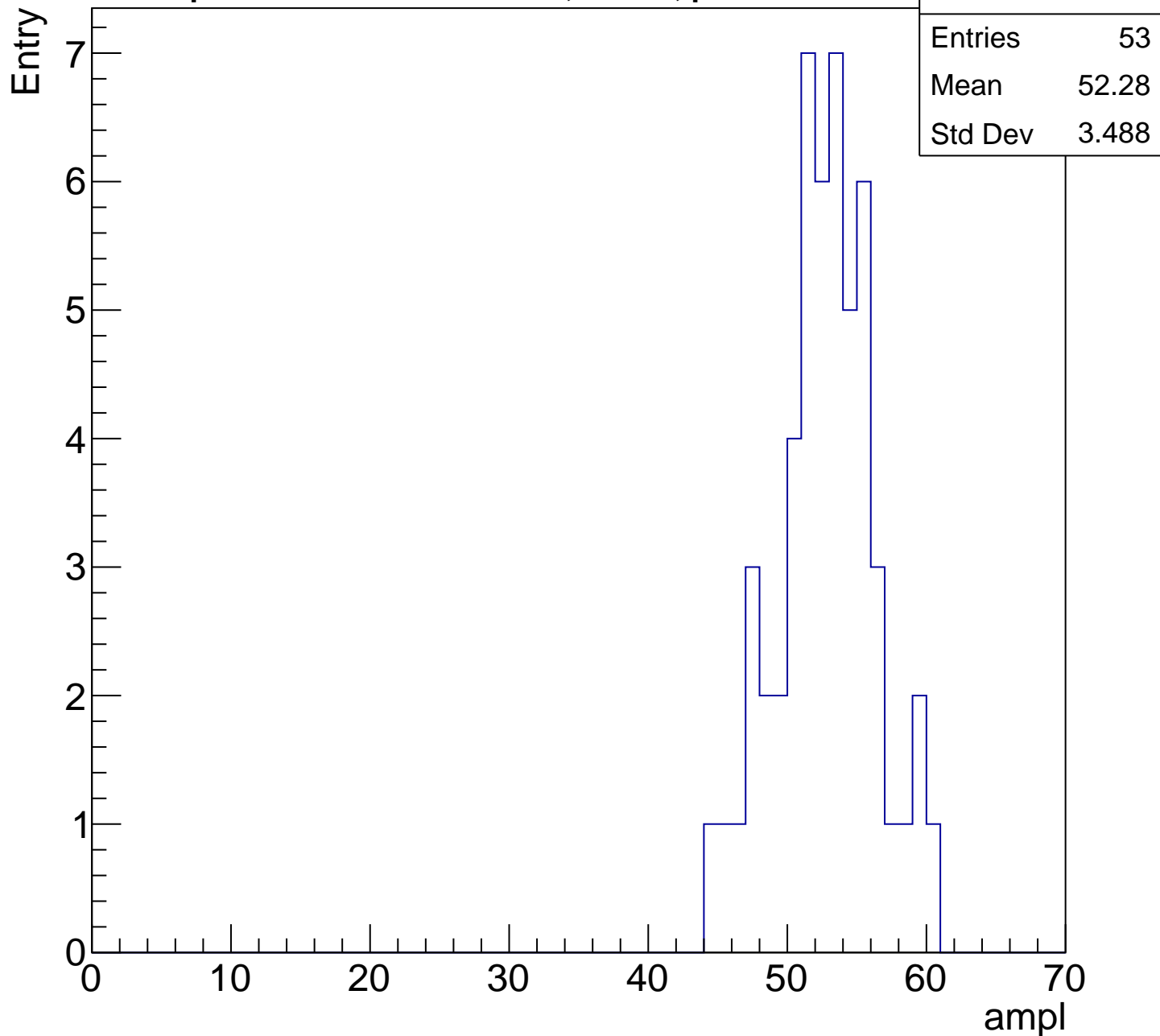
Entry

Entries	66
Mean	46.88
Std Dev	3.174



# B1L003S, U23-ch58, adc4

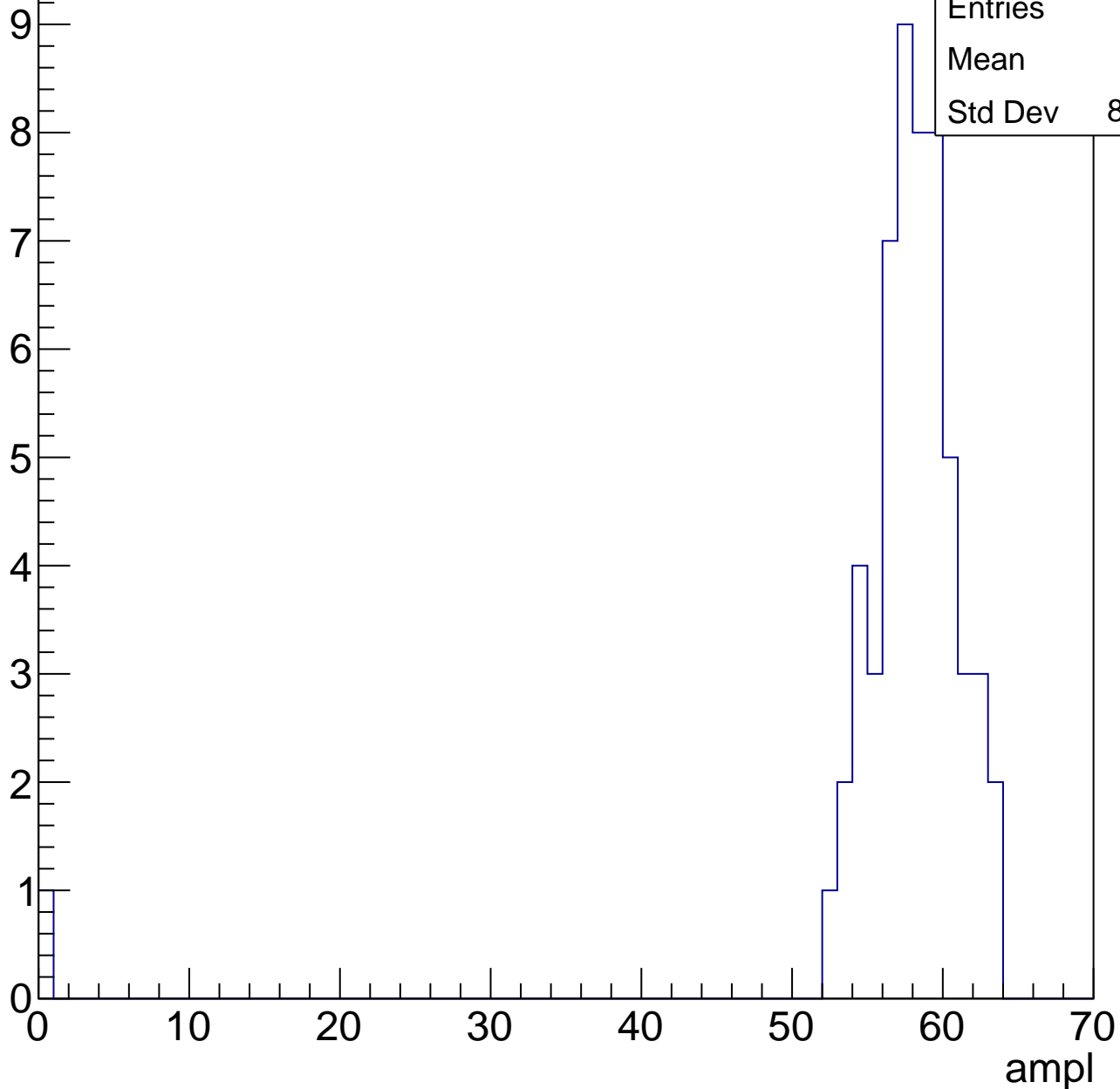
calib\_packv5\_042523\_0143.root, FC#13, port D2



# B1L003S, U23-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



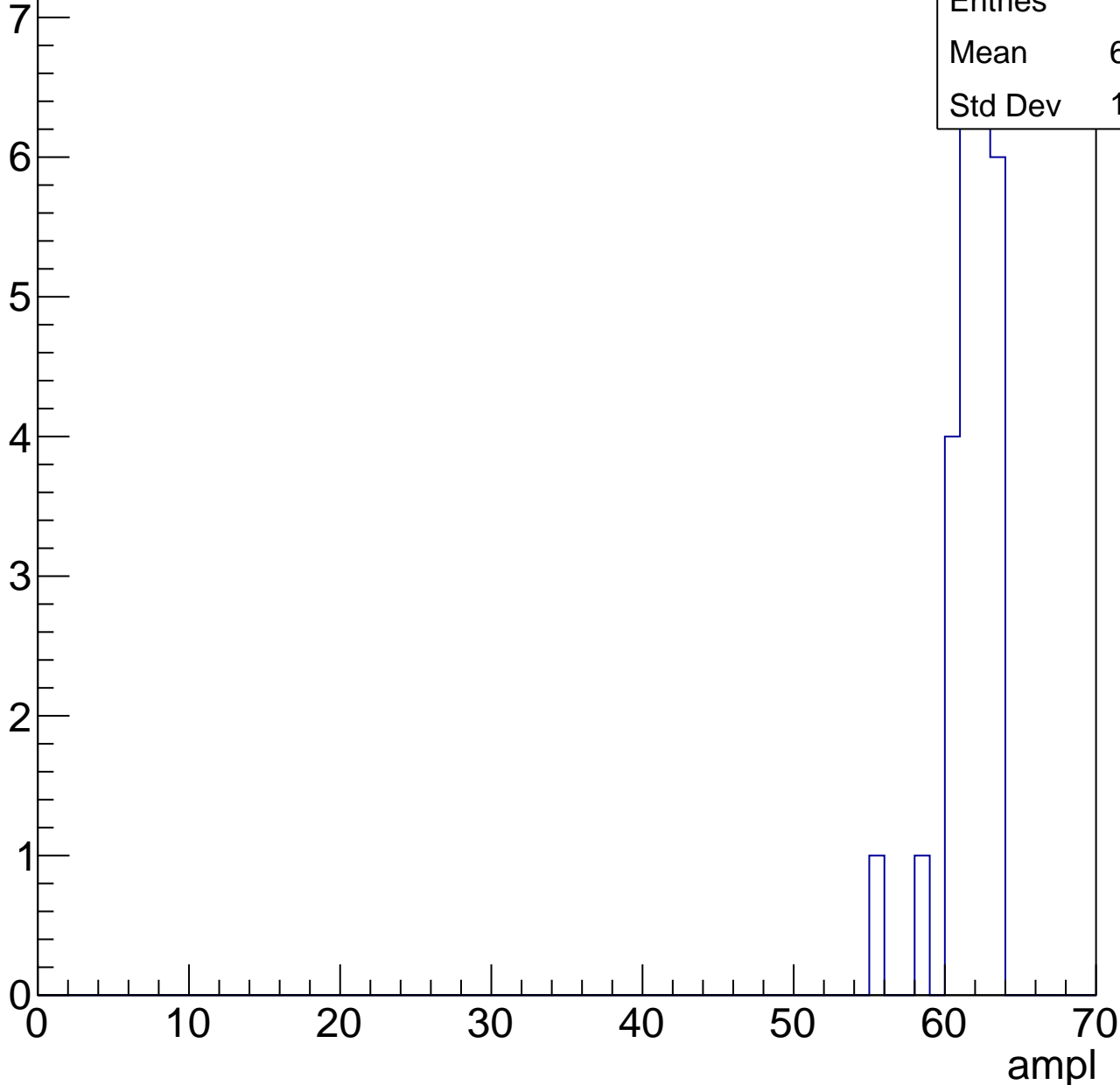
Entries	56
Mean	56.7
Std Dev	8.058

# B1L003S, U23-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	26
Mean	61.23
Std Dev	1.739





# B1L003S, U23-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch59, adc0

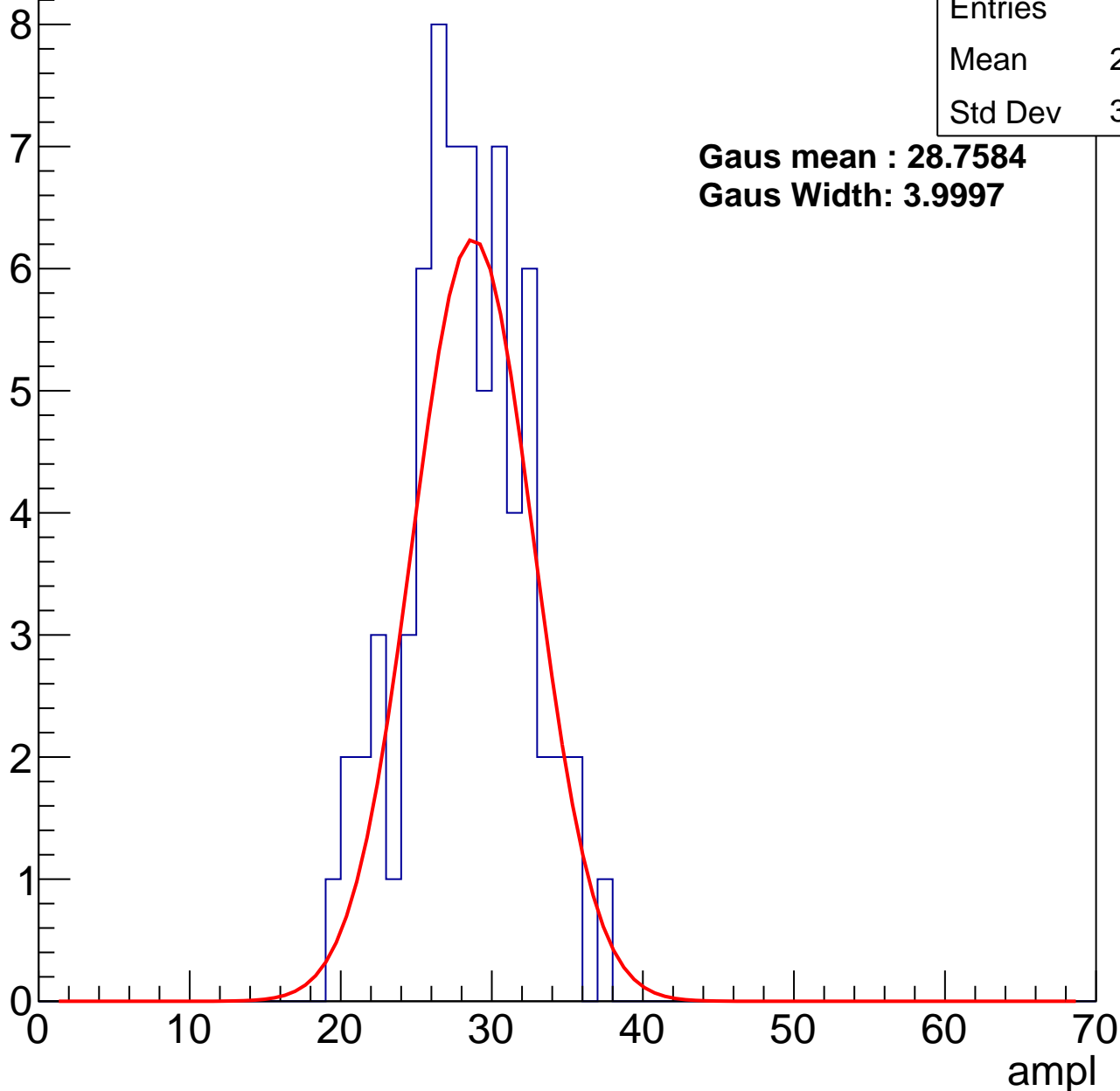
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	27.78
Std Dev	3.889

**Gaus mean : 28.7584**

**Gaus Width: 3.9997**



# B1L003S, U23-ch59, adc1

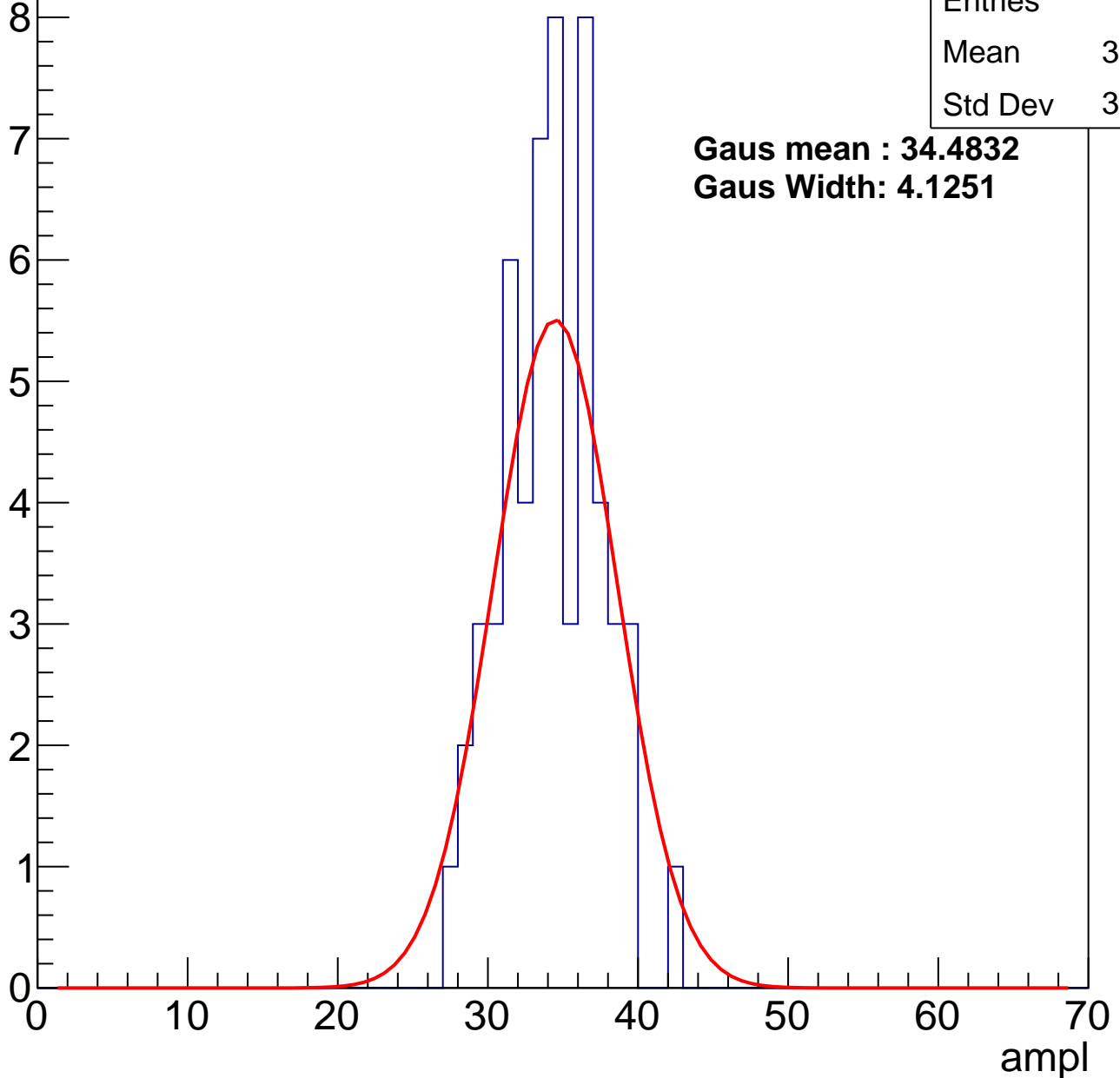
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	33.77
Std Dev	3.207

**Gaus mean : 34.4832**

**Gaus Width: 4.1251**



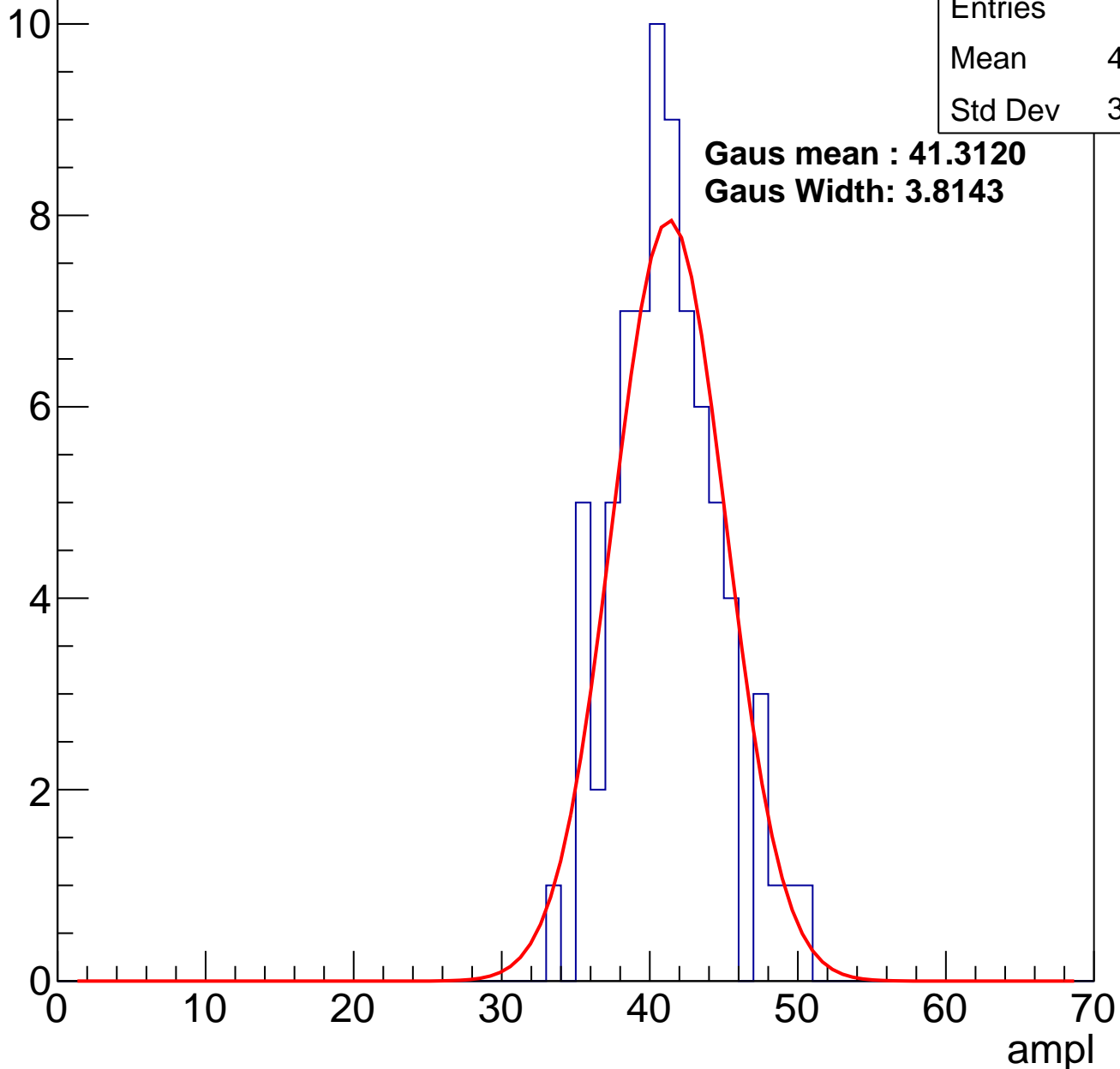
# B1L003S, U23-ch59, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	74
Mean	40.72
Std Dev	3.509

**Gaus mean : 41.3120**  
**Gaus Width: 3.8143**

Entry

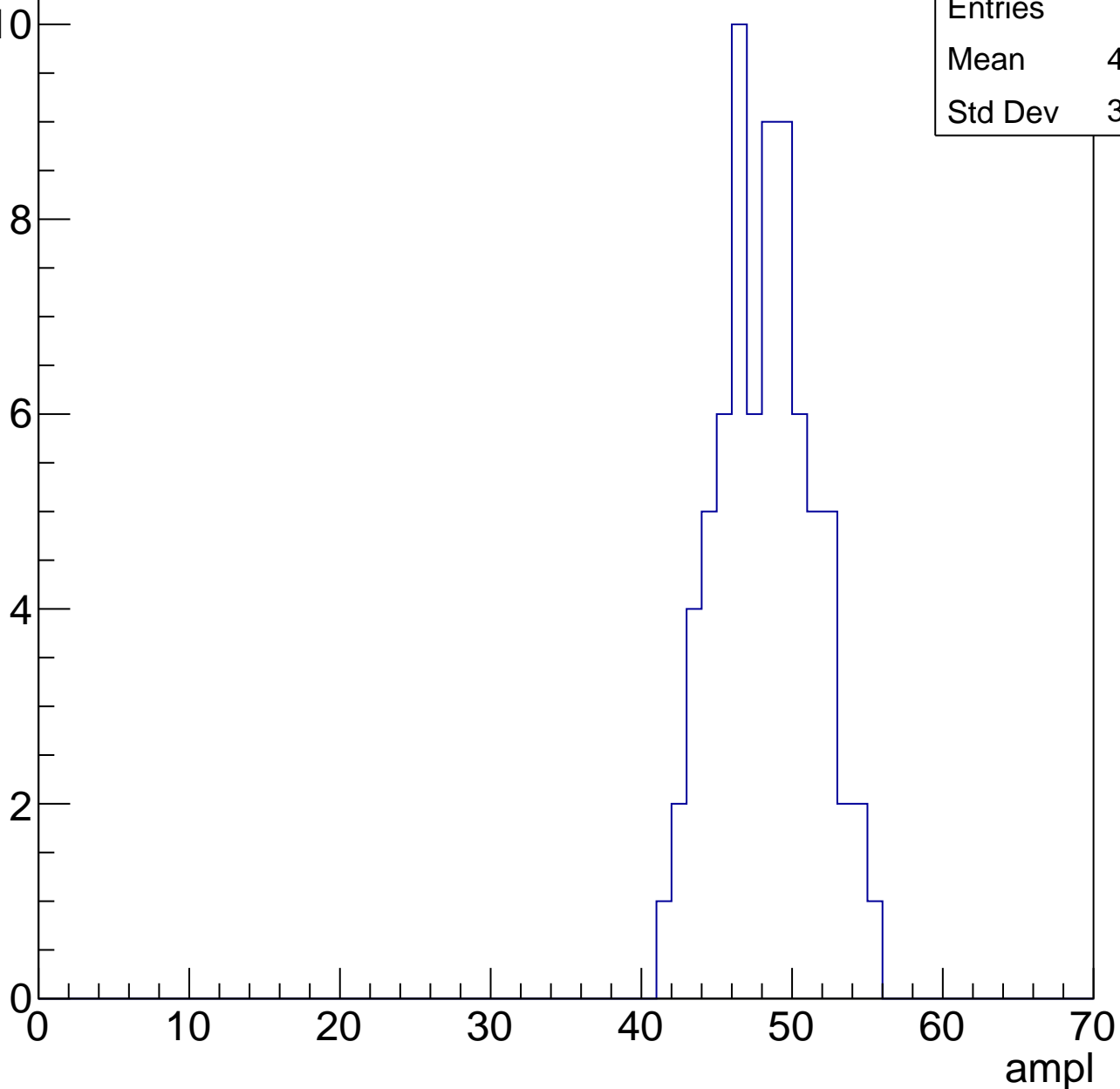


# B1L003S, U23-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	47.75
Std Dev	3.153

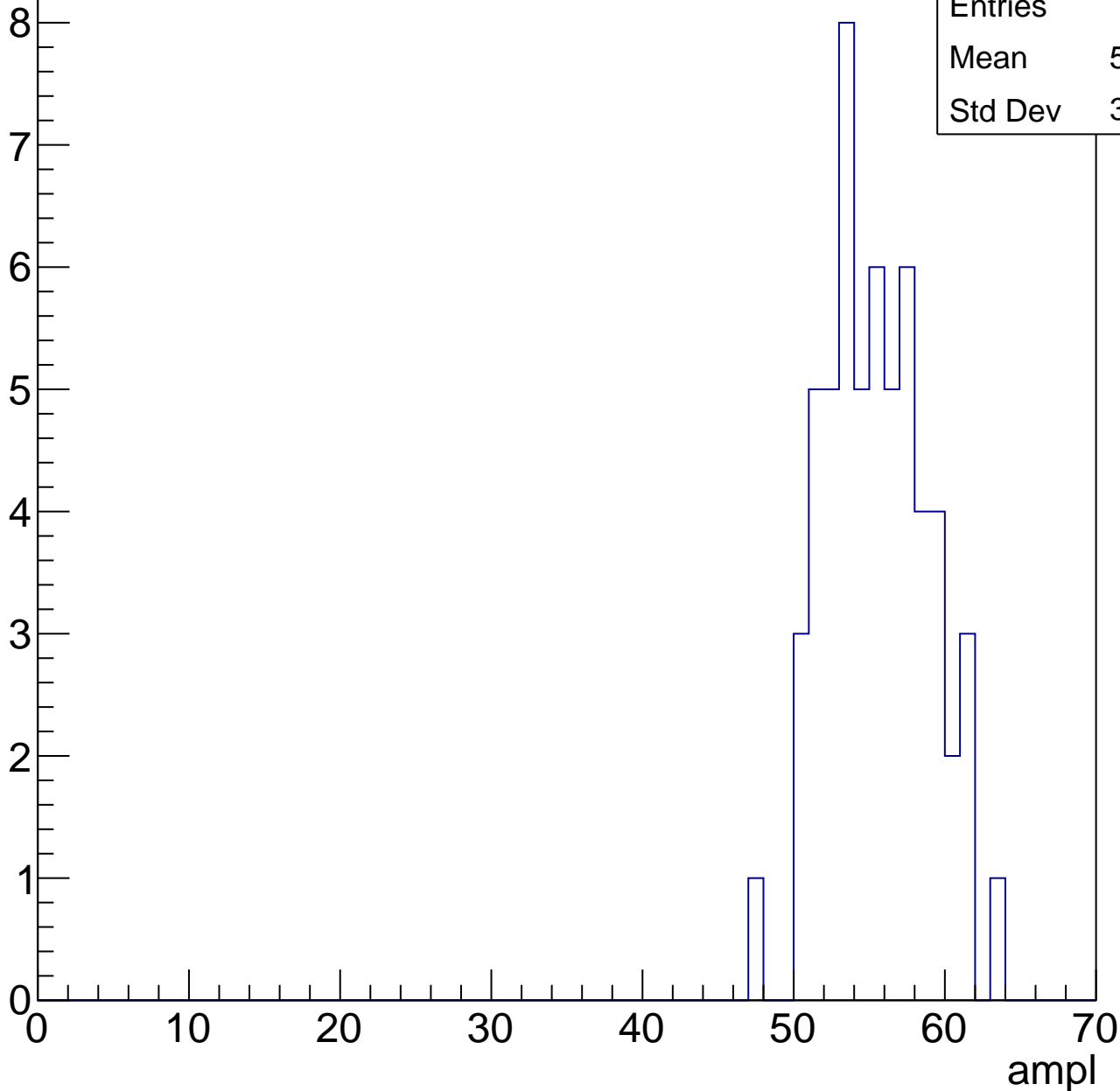


# B1L003S, U23-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.03
Std Dev	3.353

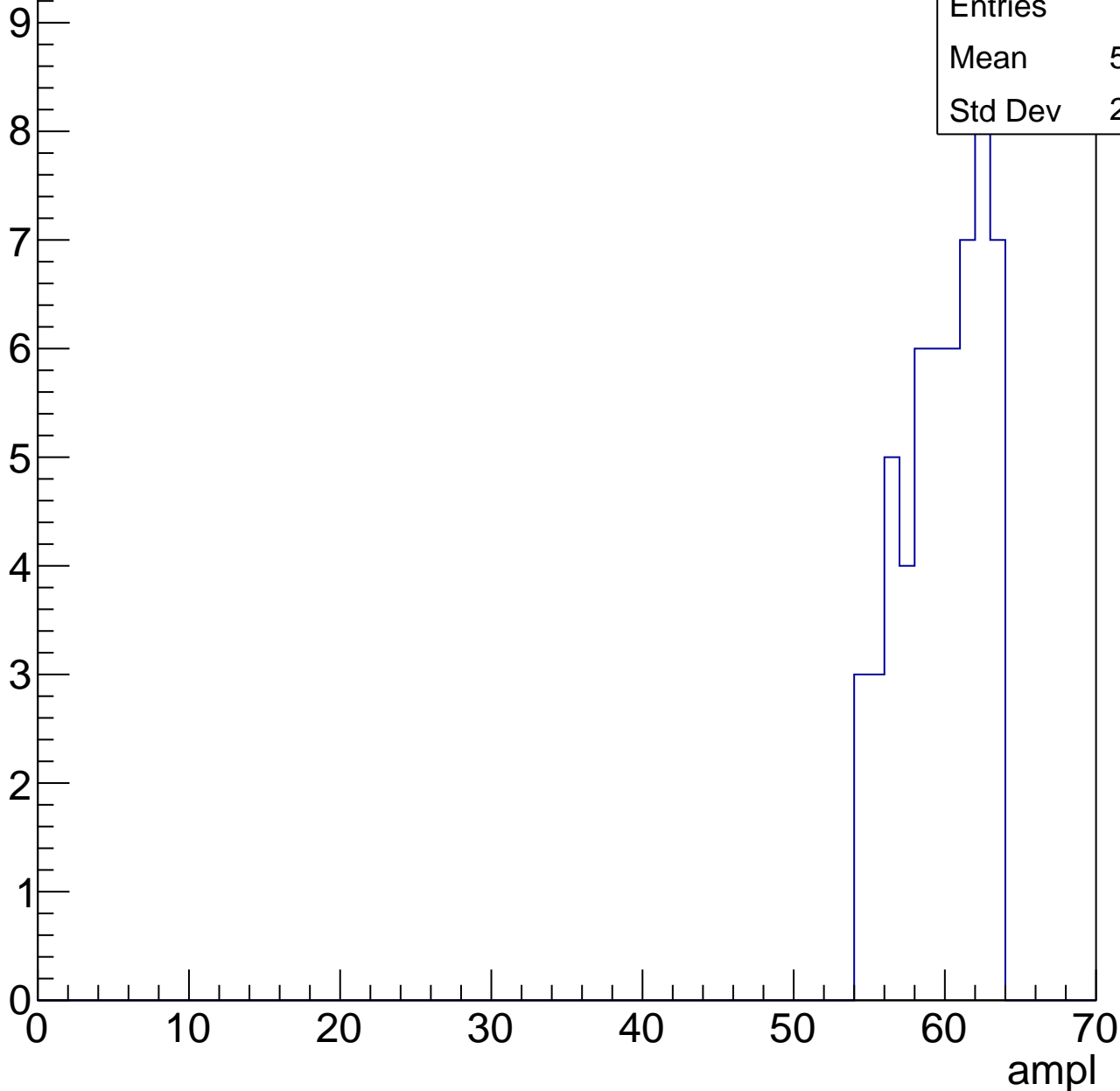


# B1L003S, U23-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	59.34
Std Dev	2.708



# B1L003S, U23-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

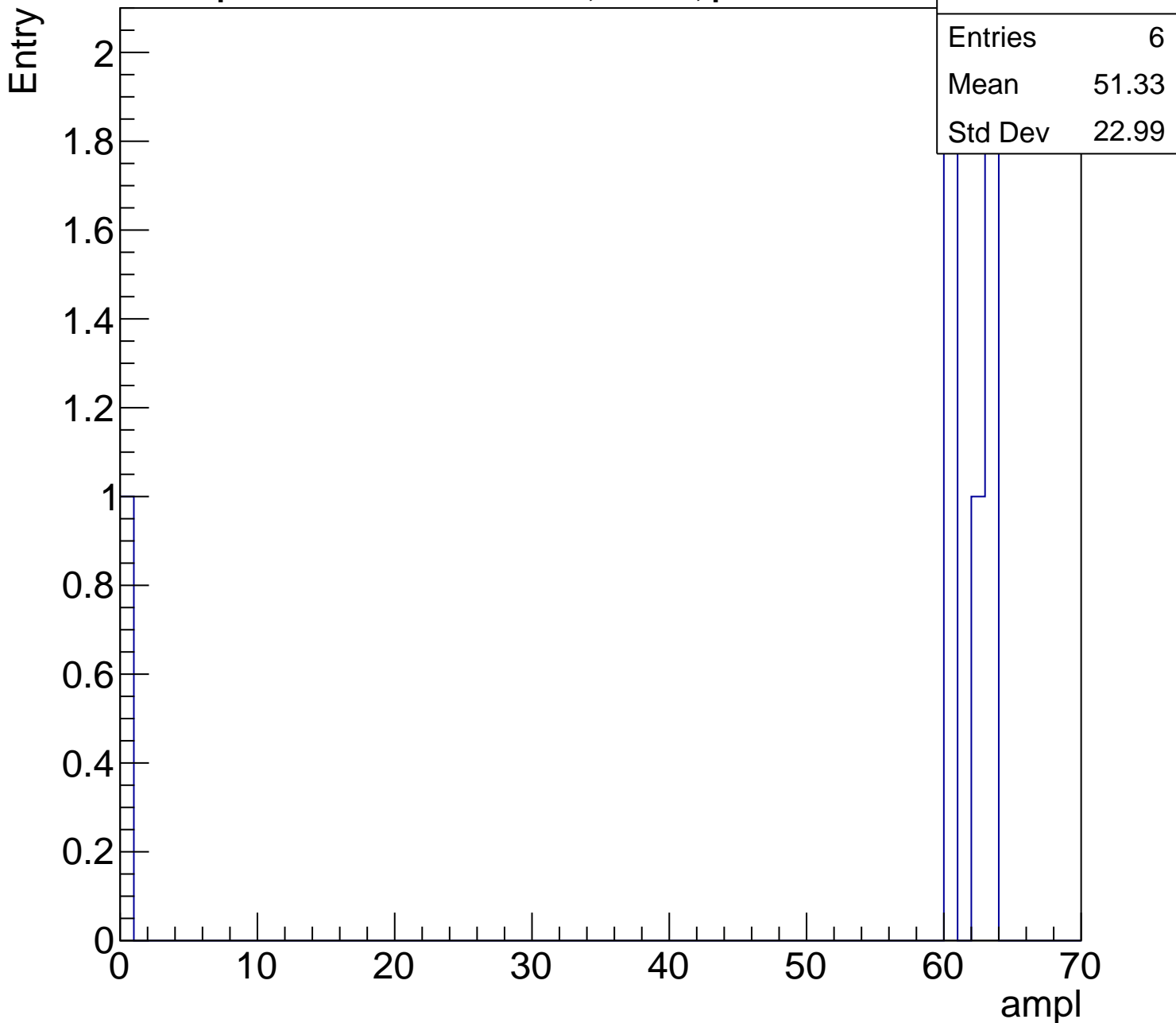
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.33
Std Dev	22.99

0 10 20 30 40 50 60 70

ampl





# B1L003S, U23-ch59, adc7

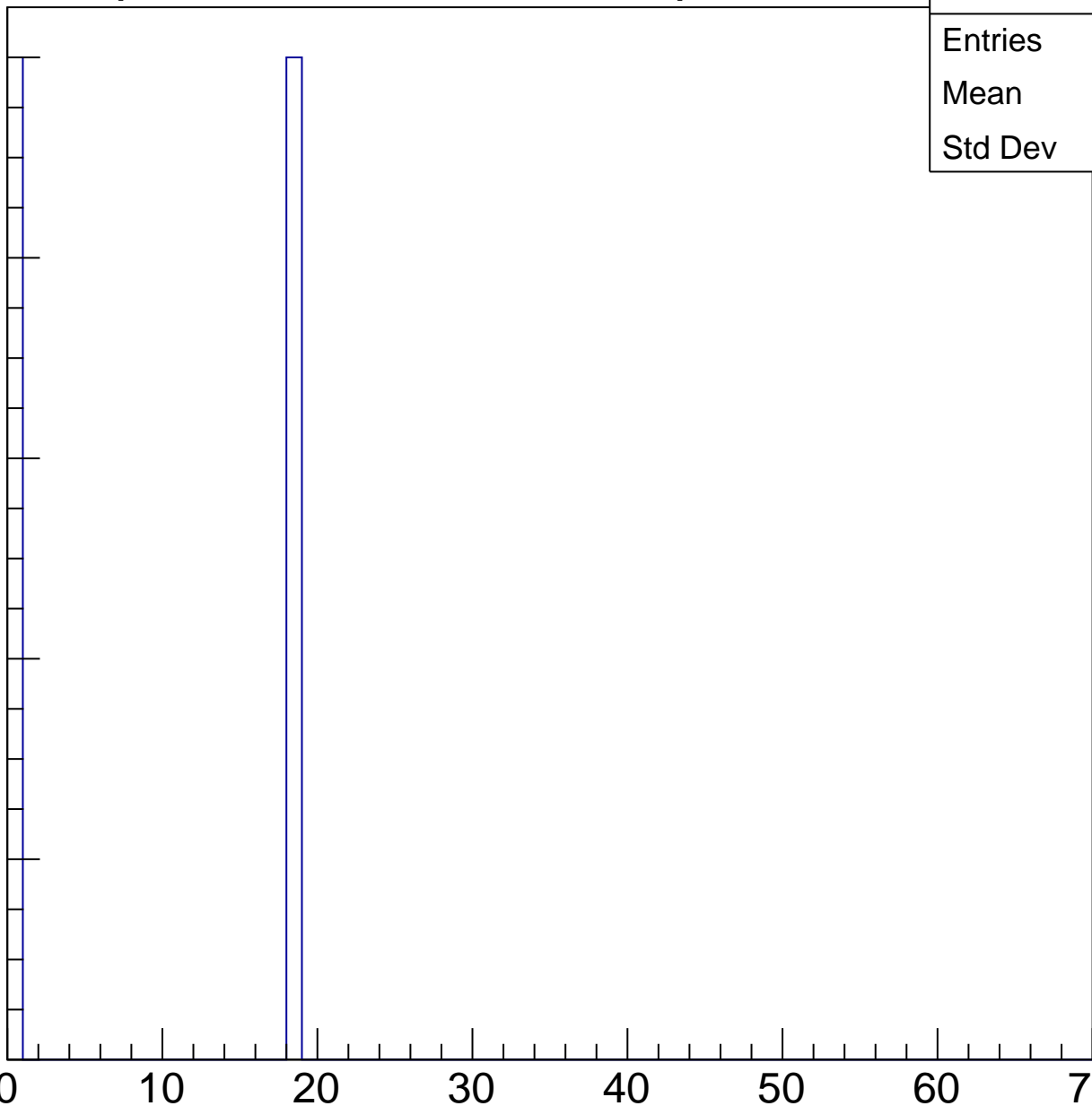
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	9
Std Dev	9

ampl



# B1L003S, U23-ch60, adc0

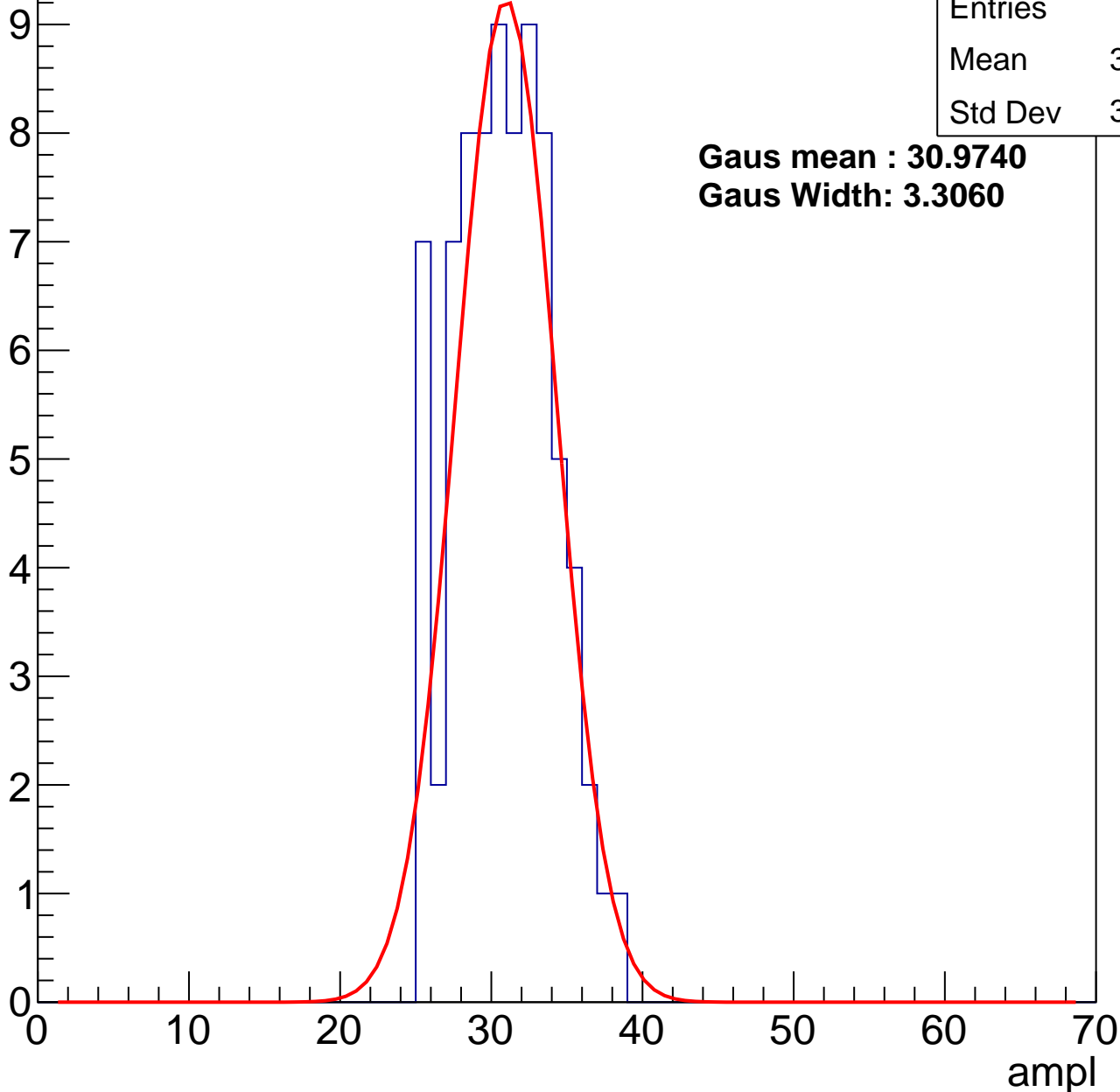
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	30.37
Std Dev	3.143

**Gaus mean : 30.9740**

**Gaus Width: 3.3060**



# B1L003S, U23-ch60, adc1

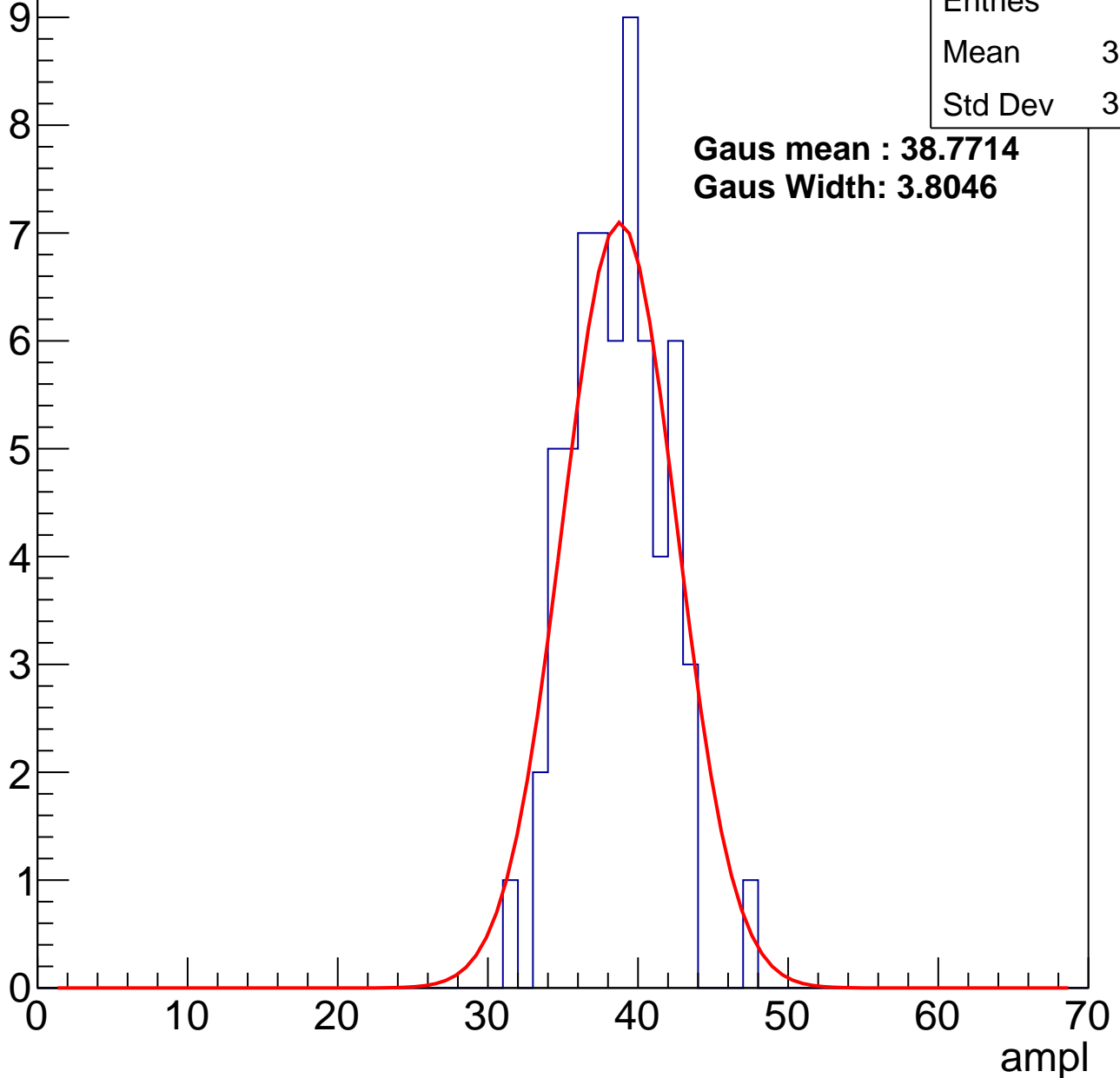
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	38.13
Std Dev	3.056

**Gaus mean : 38.7714**

**Gaus Width: 3.8046**



# B1L003S, U23-ch60, adc2

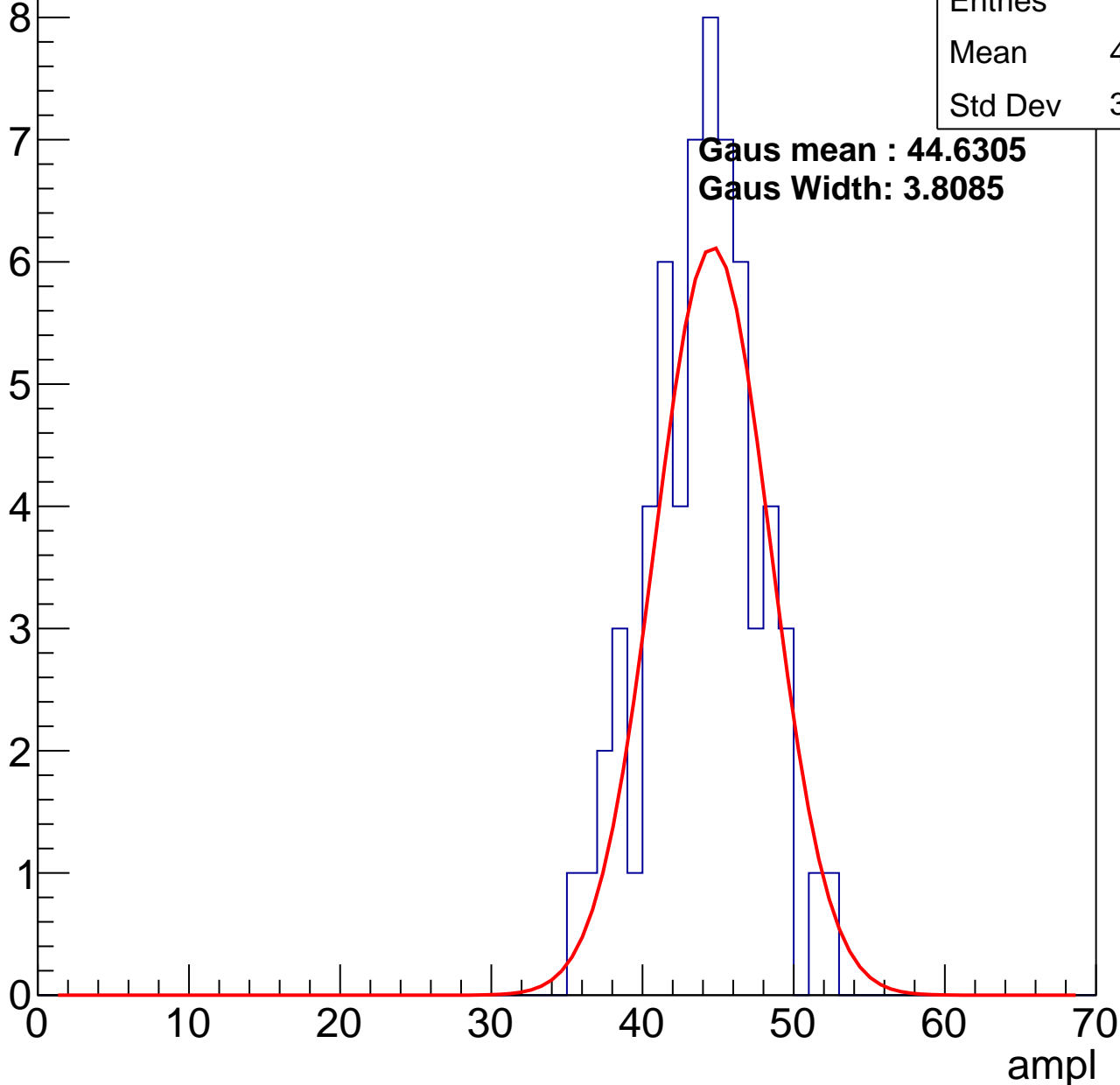
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.53
Std Dev	3.622

**Gaus mean : 44.6305**

**Gaus Width: 3.8085**

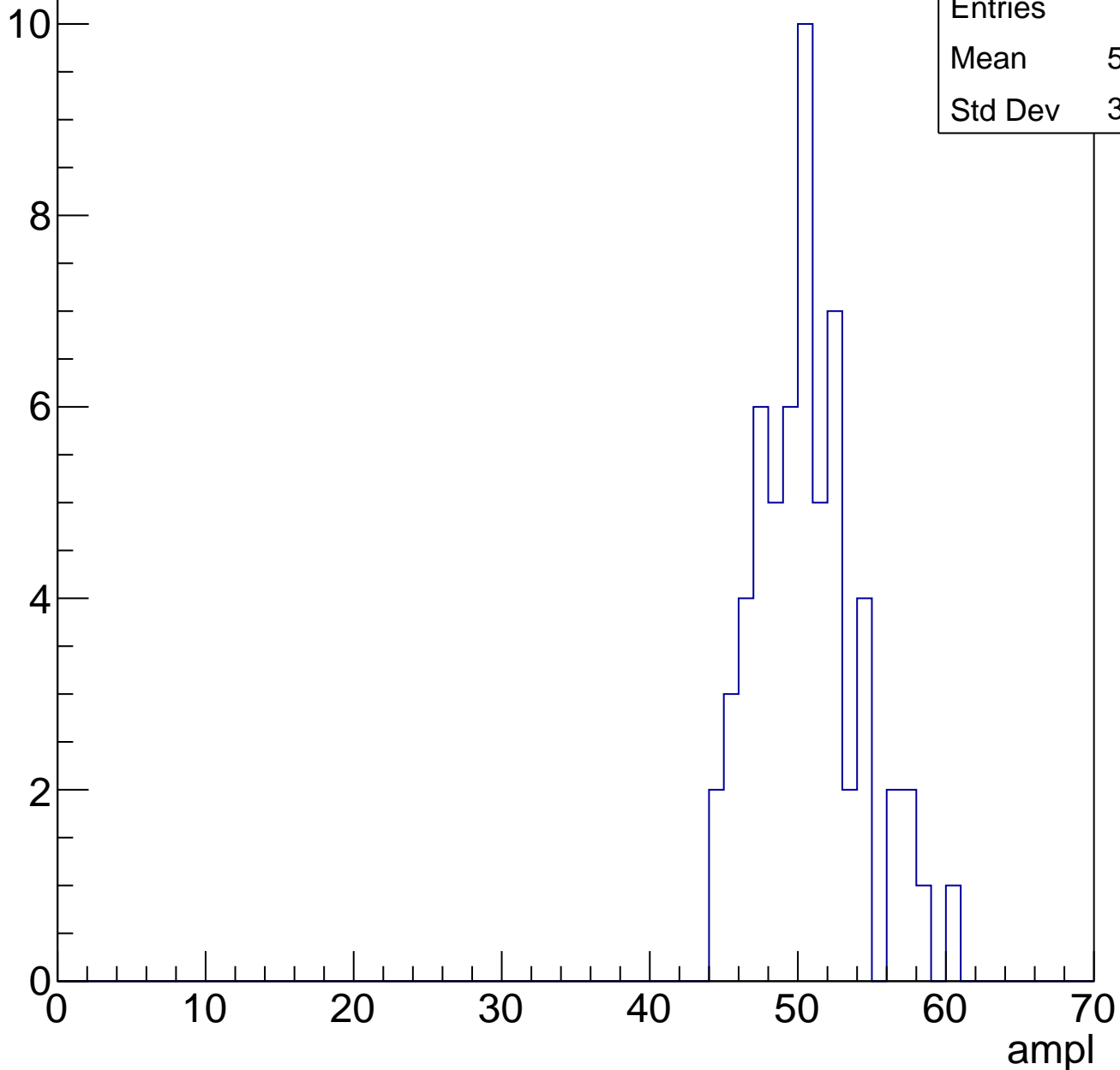


# B1L003S, U23-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	60
Mean	50.13
Std Dev	3.509

Entry

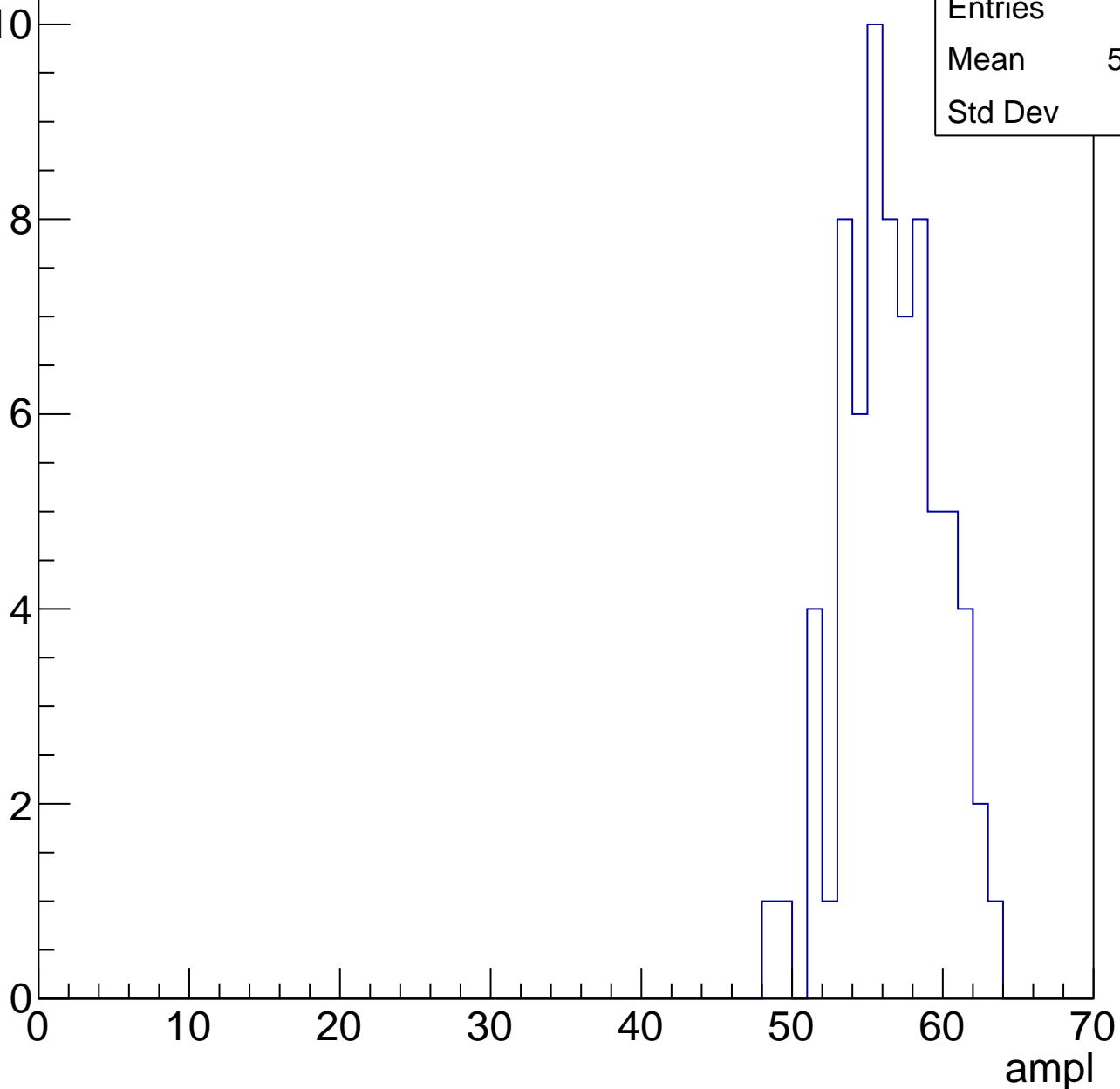


# B1L003S, U23-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	56.17
Std Dev	3.18

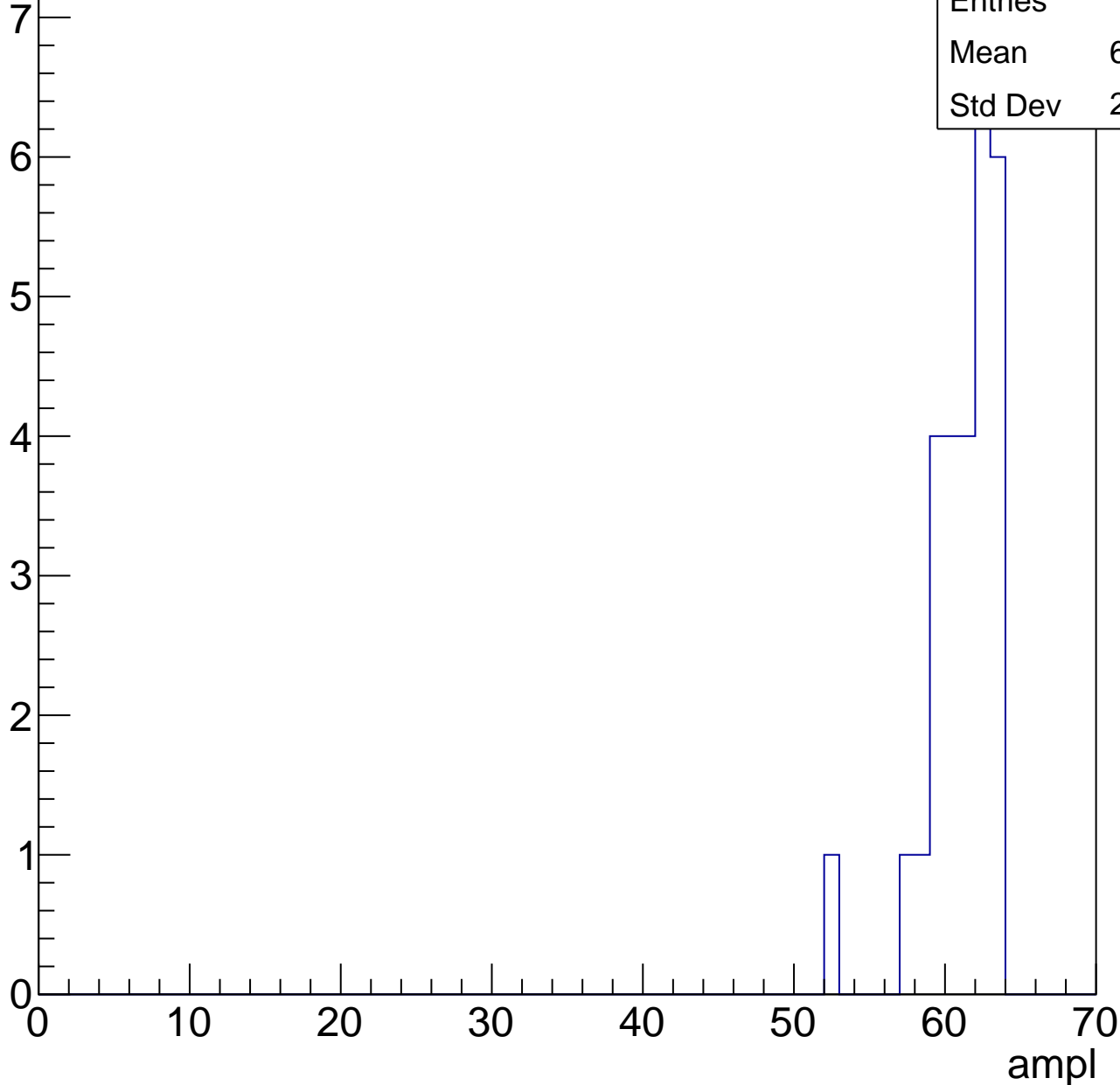


# B1L003S, U23-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

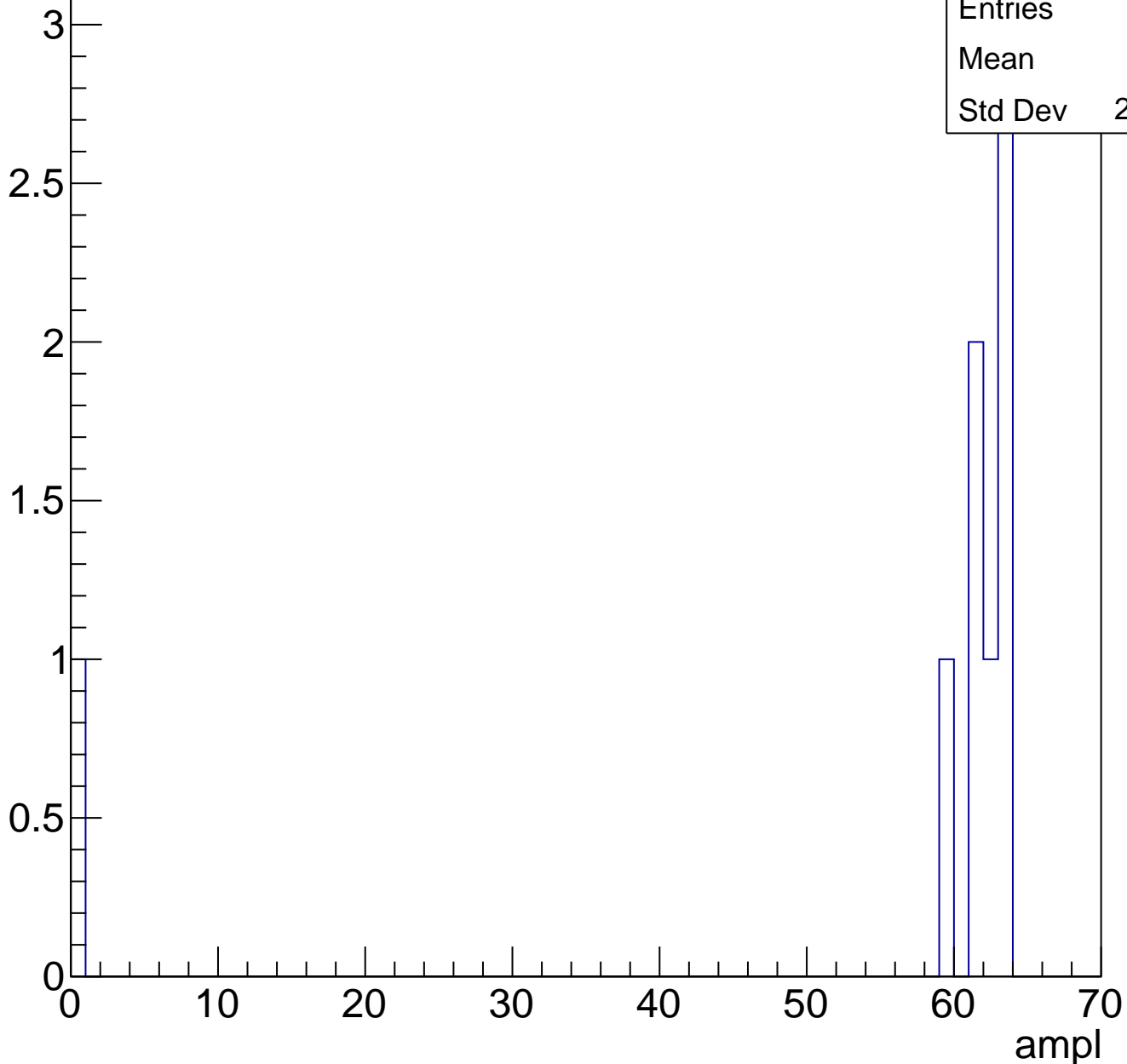
Entries	28
Mean	60.68
Std Dev	2.346



# B1L003S, U23-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	12
Std Dev	12

ampl

# B1L003S, U23-ch61, adc0

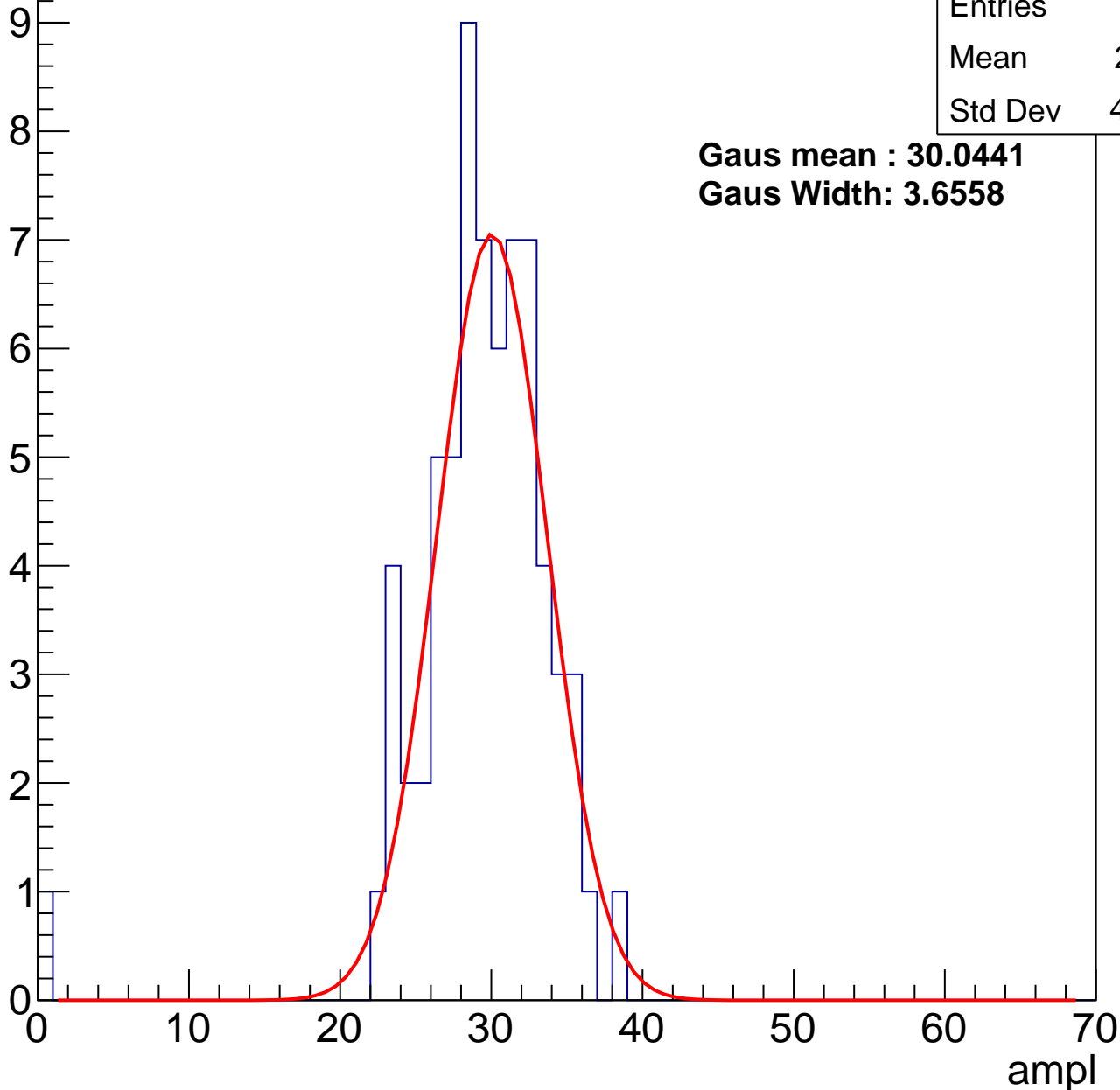
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.91
Std Dev	4.946

**Gaus mean : 30.0441**

**Gaus Width: 3.6558**



# B1L003S, U23-ch61, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	71
Mean	36.25
Std Dev	3.527

**Gaus mean : 36.4965**

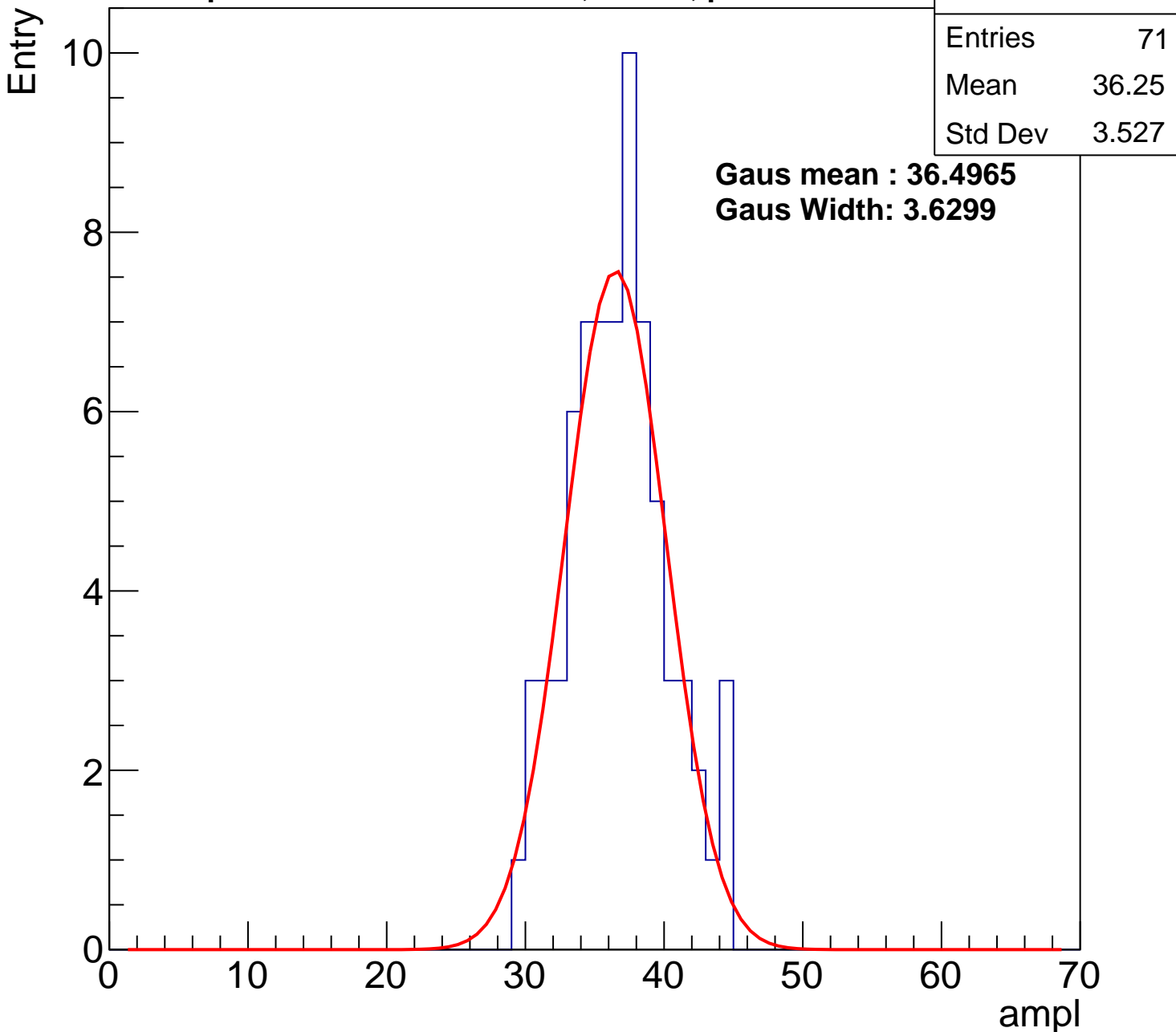
**Gaus Width: 3.6299**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



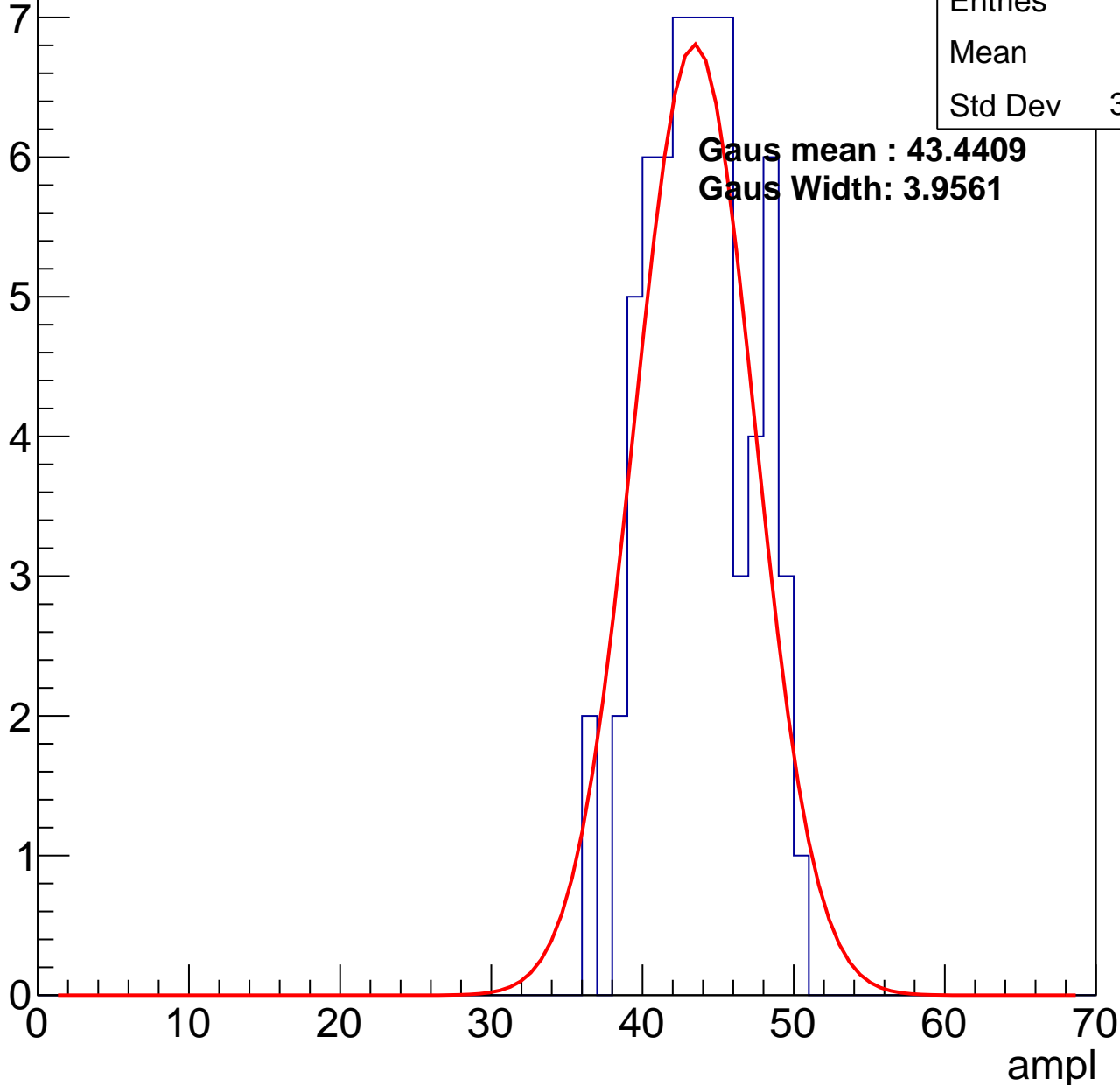
# B1L003S, U23-ch61, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	43.3
Std Dev	3.348

**Gaus mean : 43.4409**  
**Gaus Width: 3.9561**

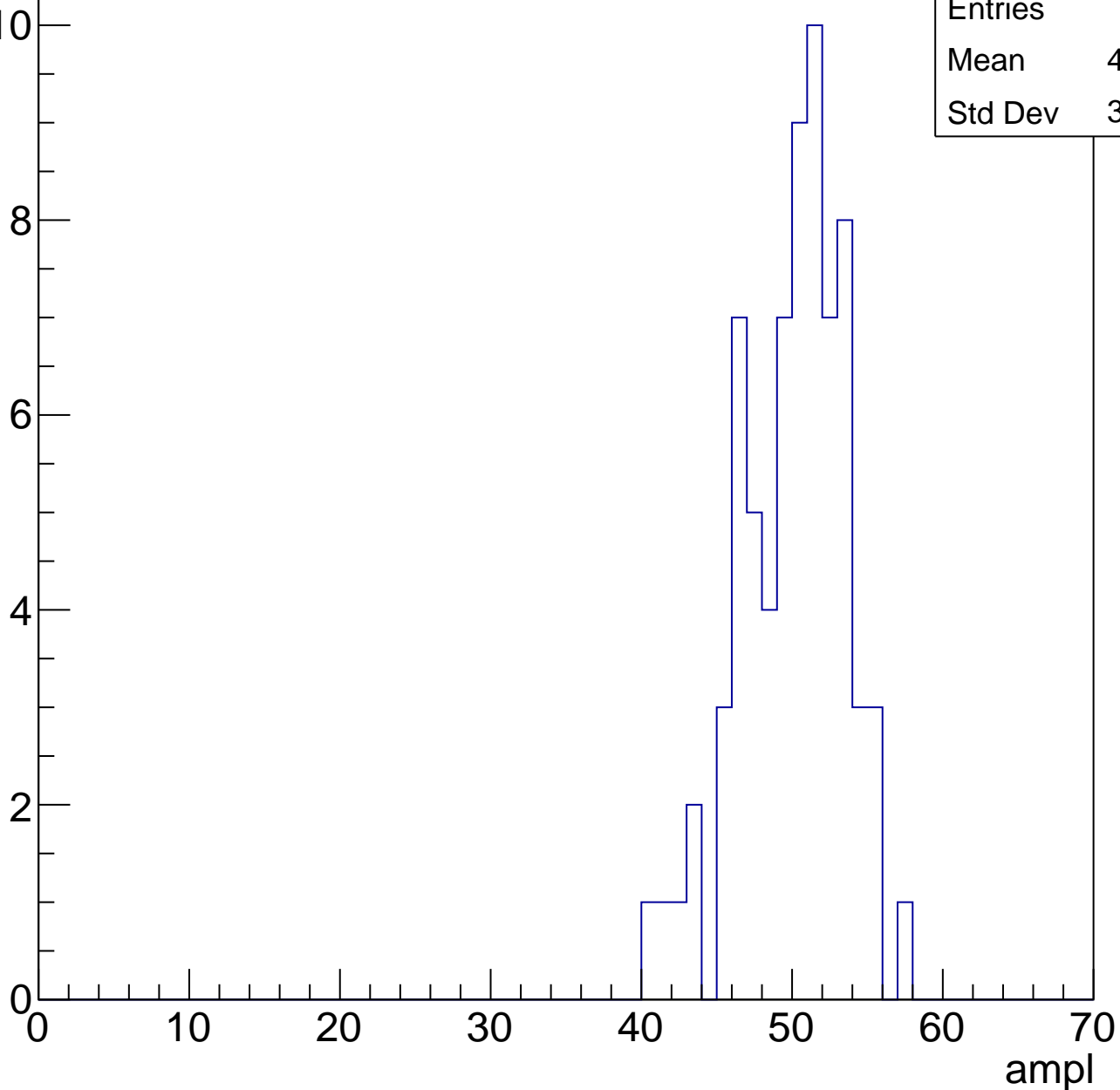


# B1L003S, U23-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	49.56
Std Dev	3.468

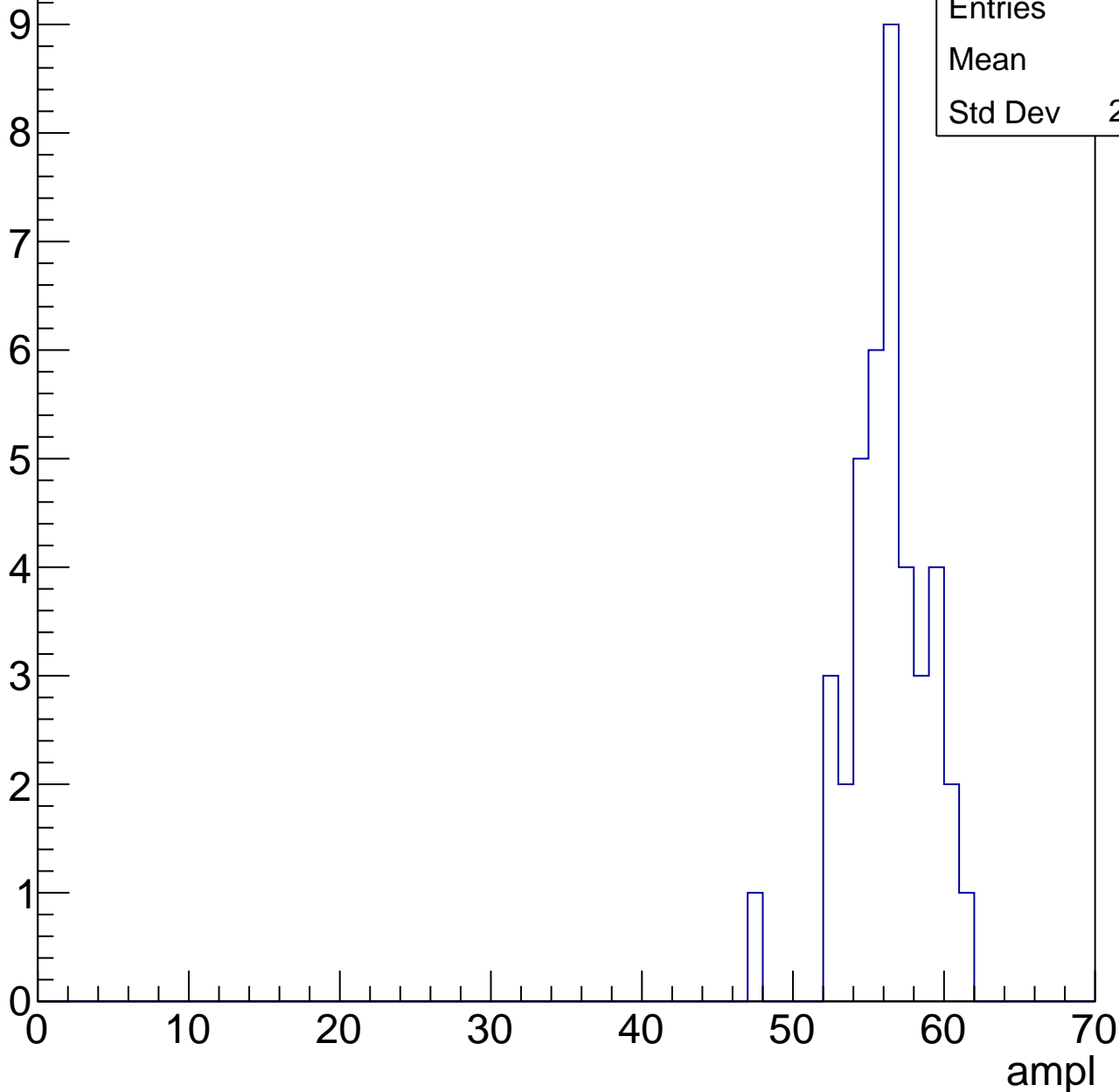


# B1L003S, U23-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	40
Mean	55.8
Std Dev	2.648

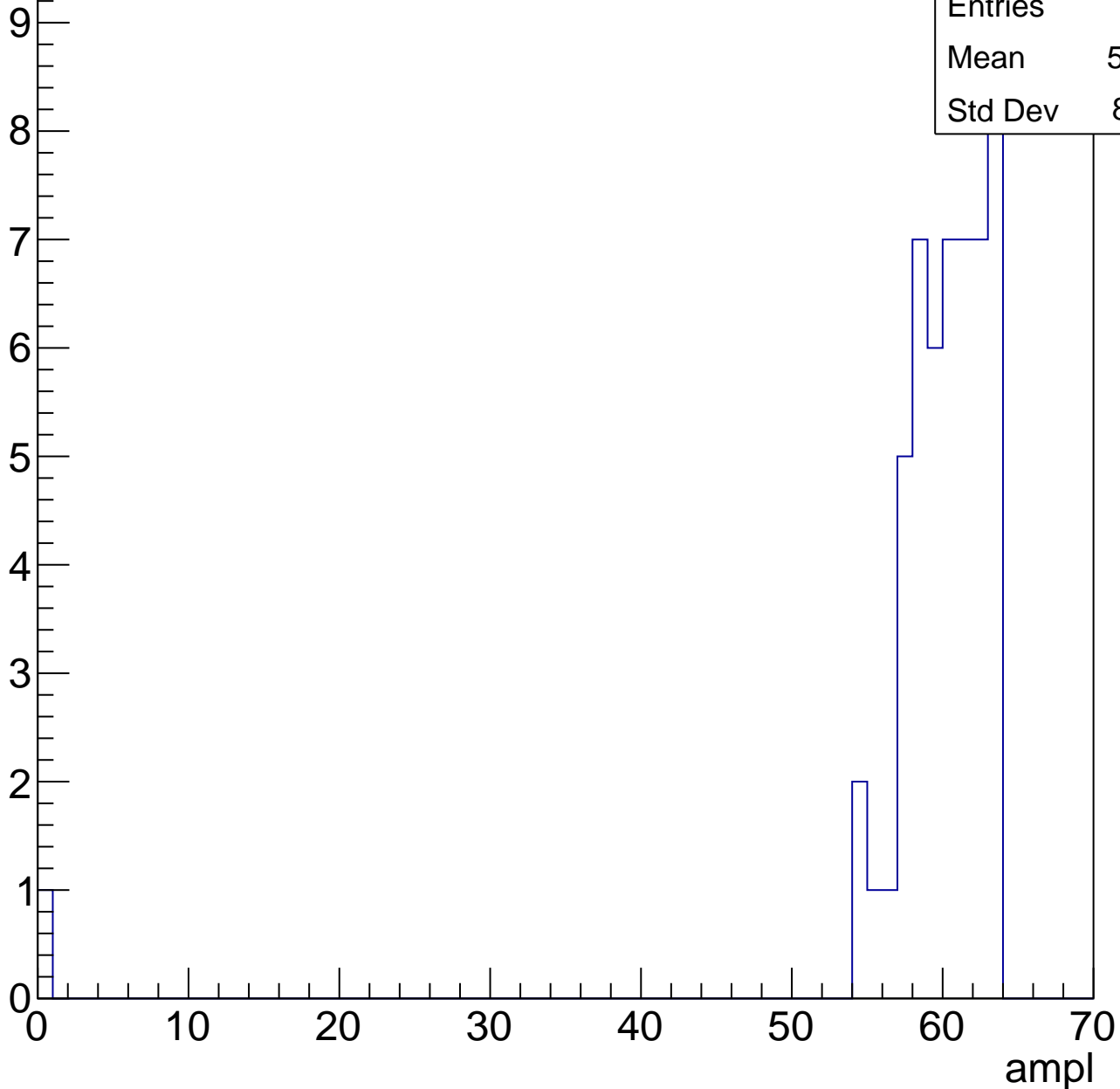


# B1L003S, U23-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

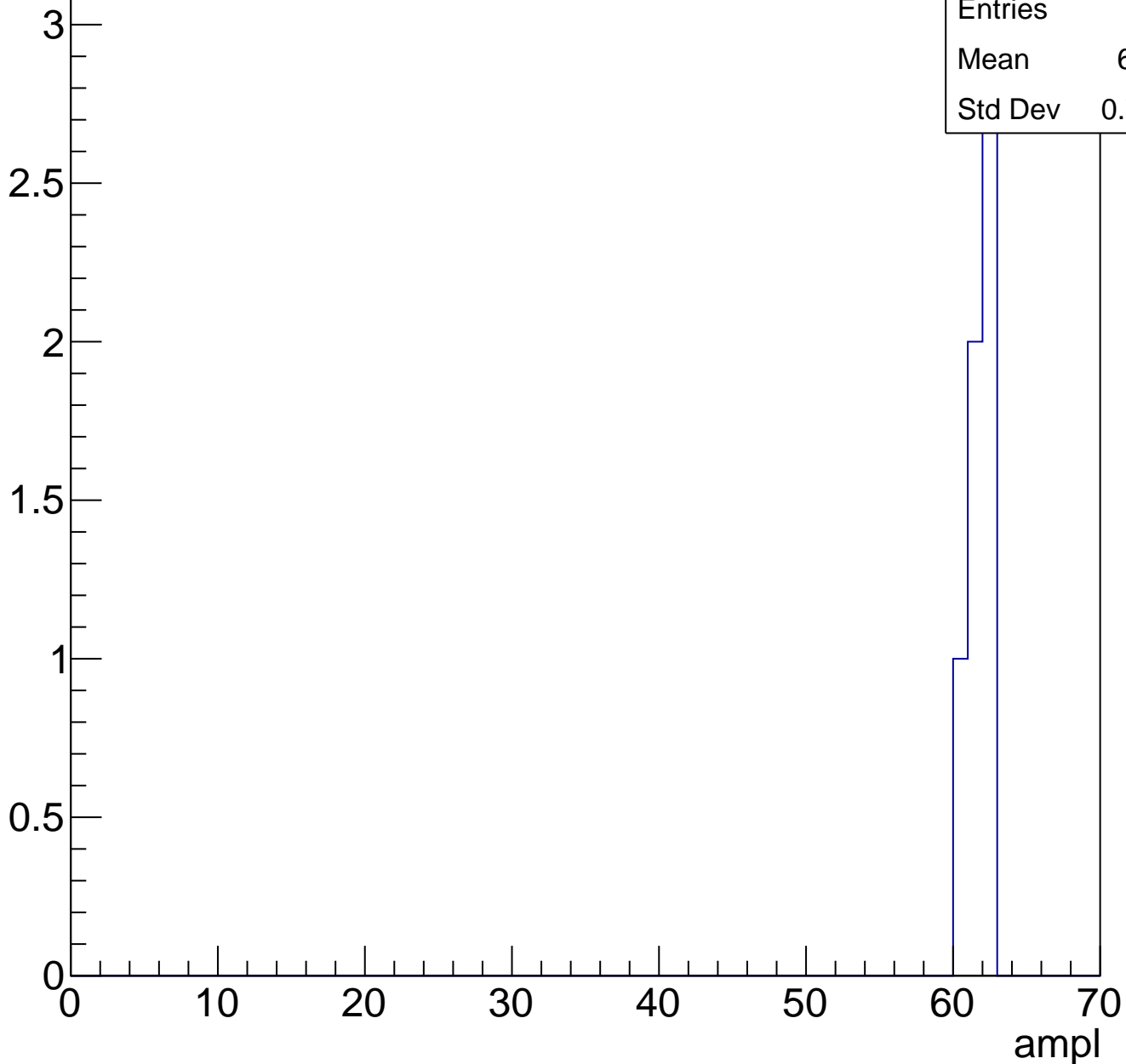
Entries	53
Mean	58.72
Std Dev	8.491



# B1L003S, U23-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch62, adc0

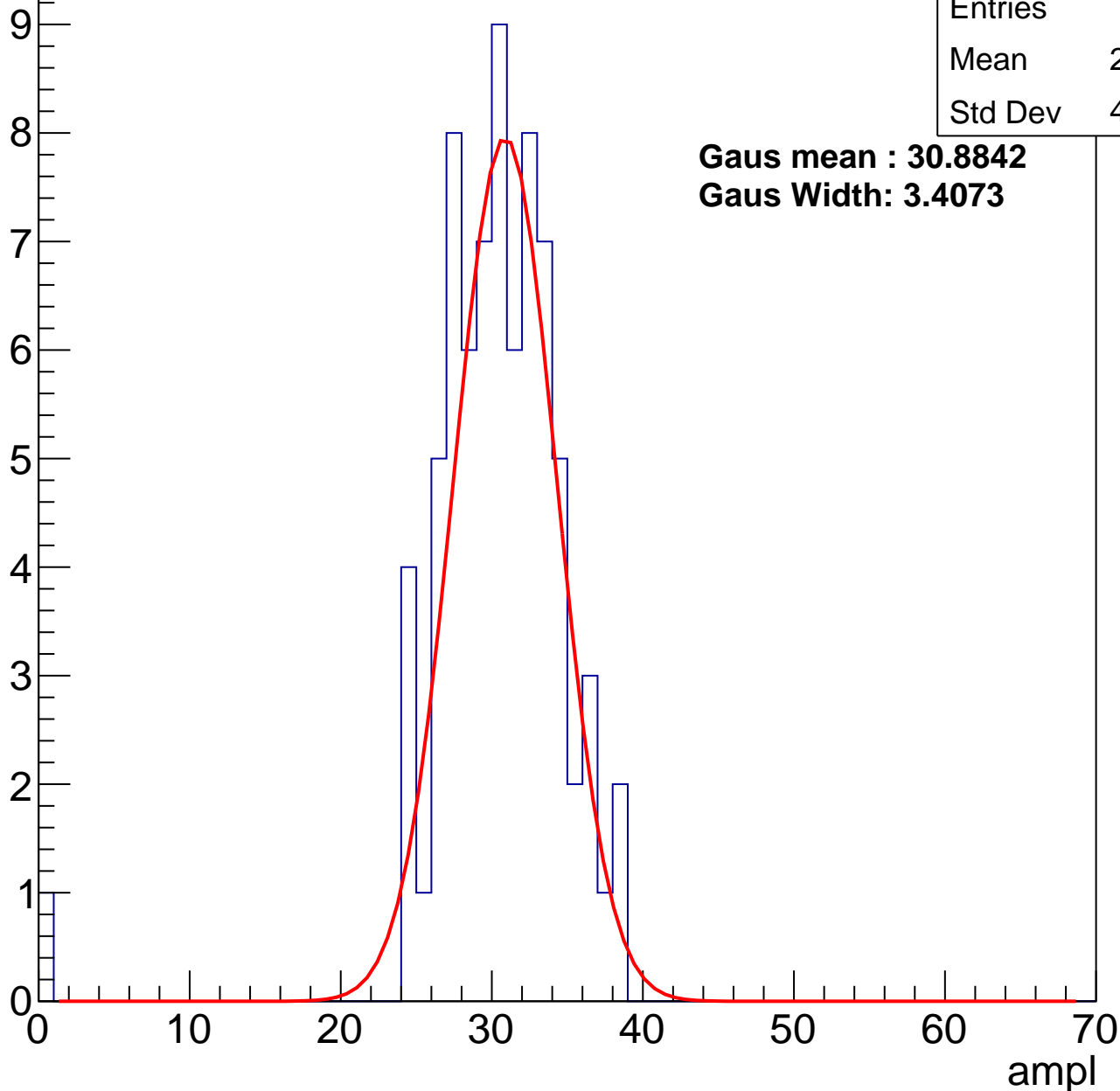
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	29.89
Std Dev	4.854

**Gaus mean : 30.8842**

**Gaus Width: 3.4073**



# B1L003S, U23-ch62, adc1

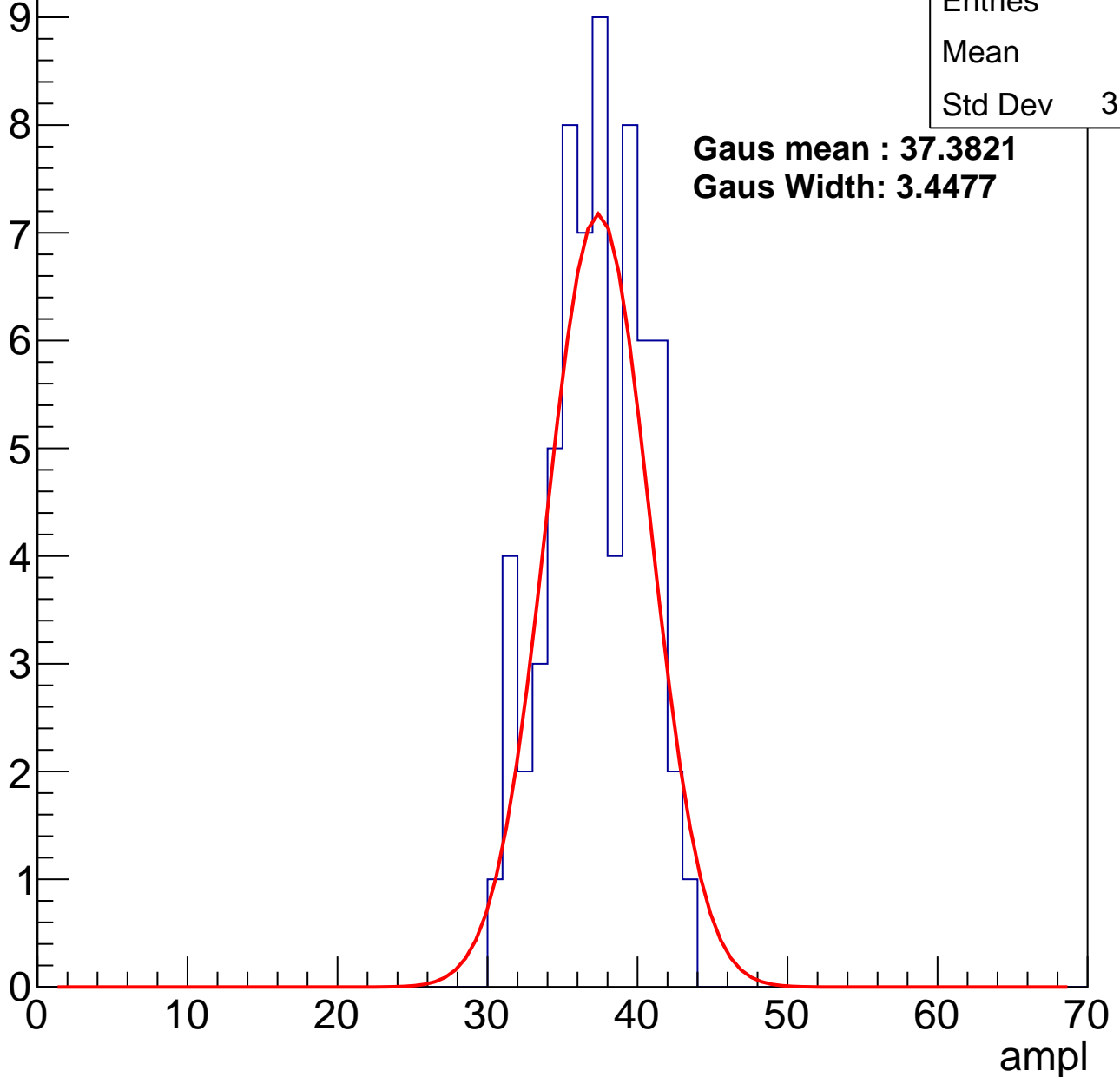
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.8
Std Dev	3.125

**Gaus mean : 37.3821**

**Gaus Width: 3.4477**



# B1L003S, U23-ch62, adc2

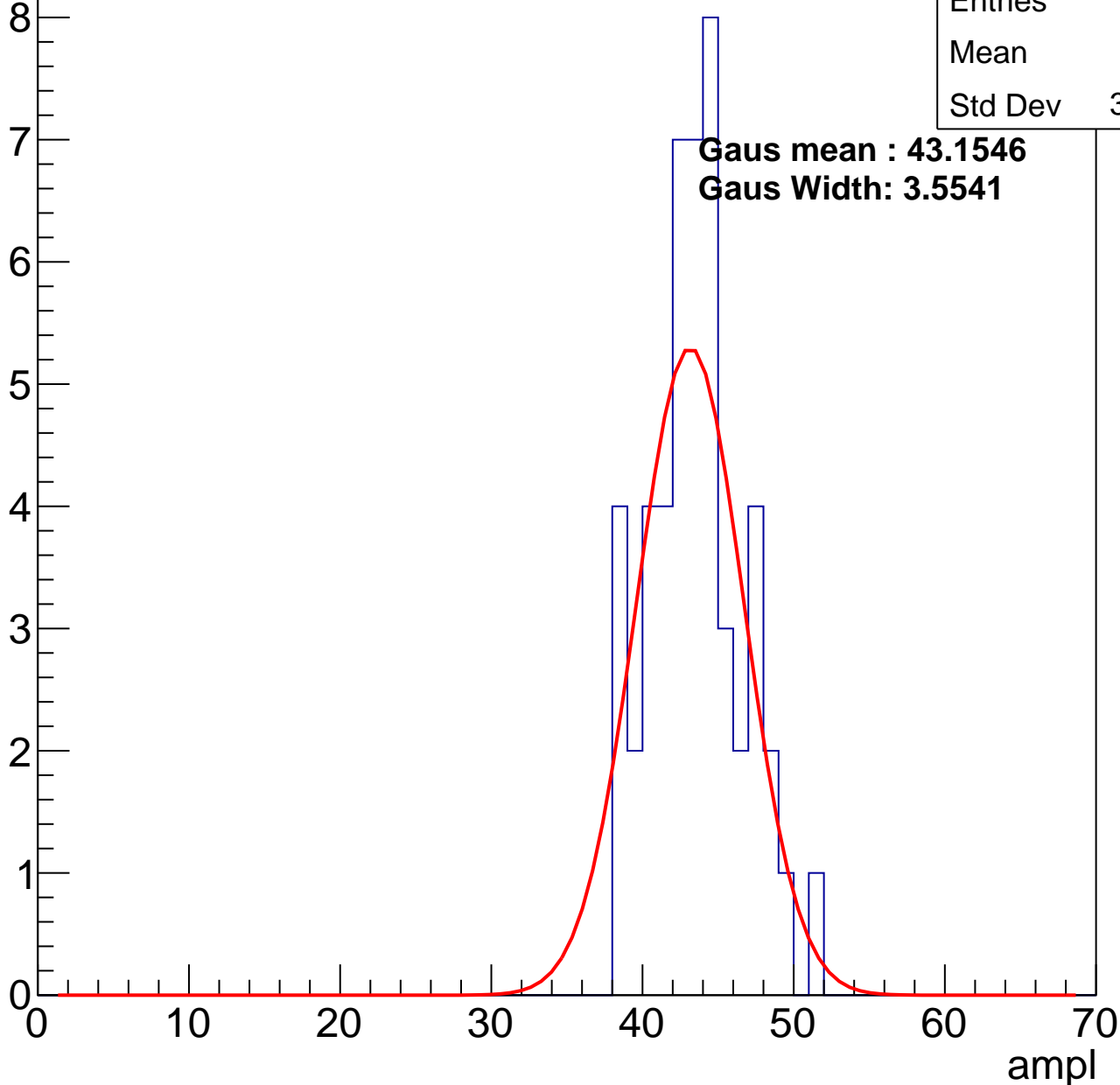
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	43.1
Std Dev	3.005

**Gaus mean : 43.1546**

**Gaus Width: 3.5541**

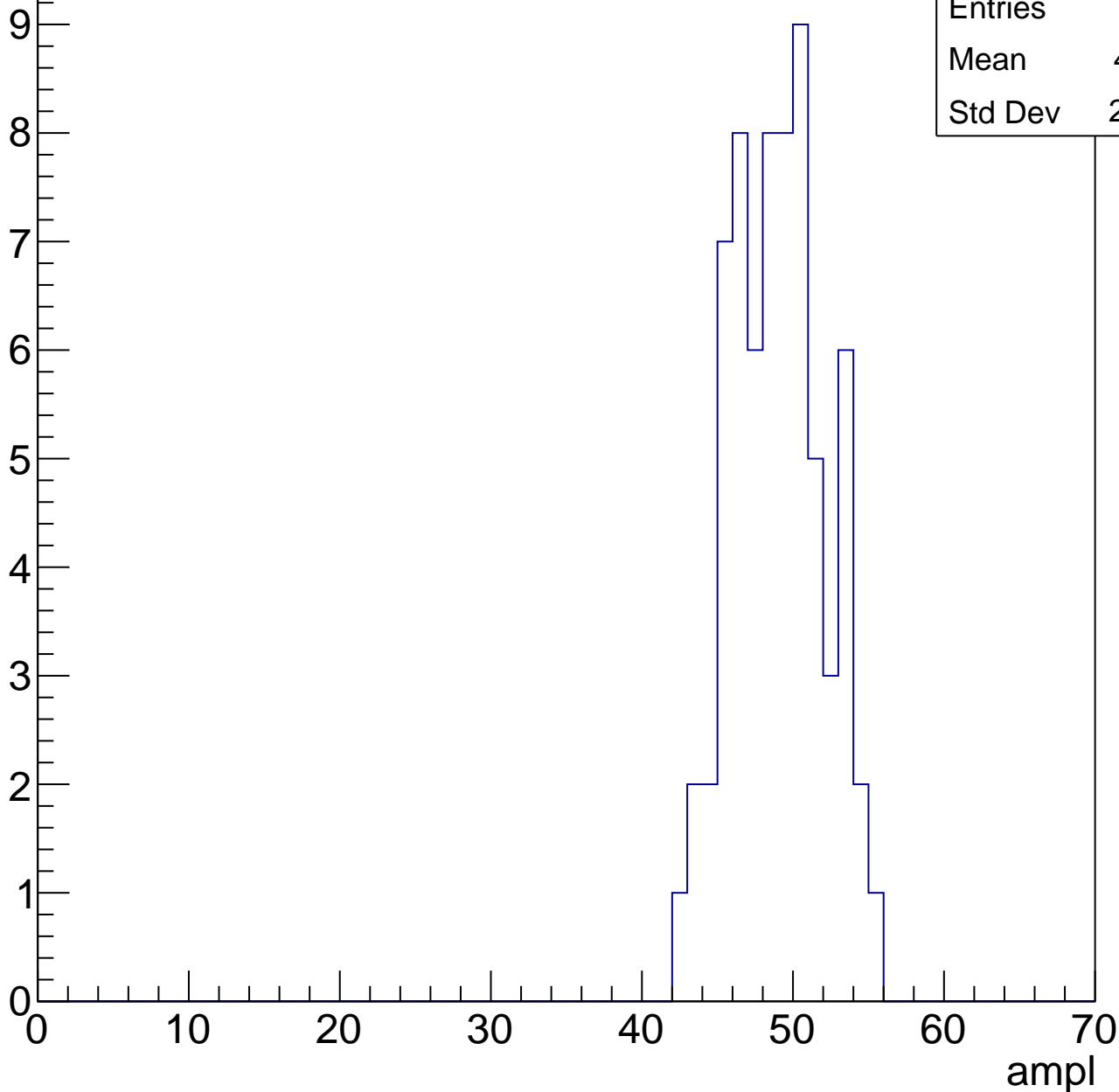


# B1L003S, U23-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

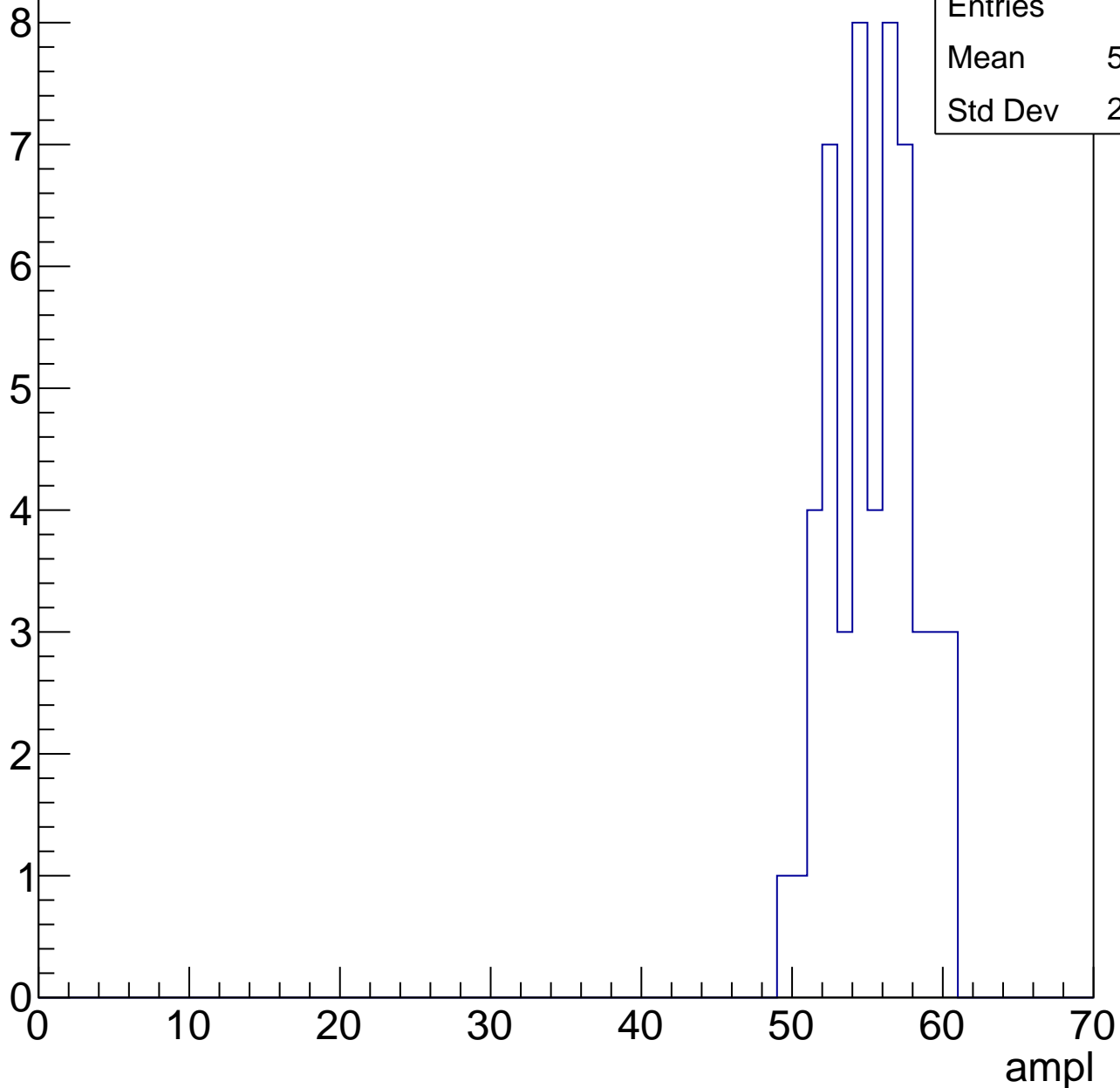
Entries	68
Mean	48.51
Std Dev	2.993



# B1L003S, U23-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

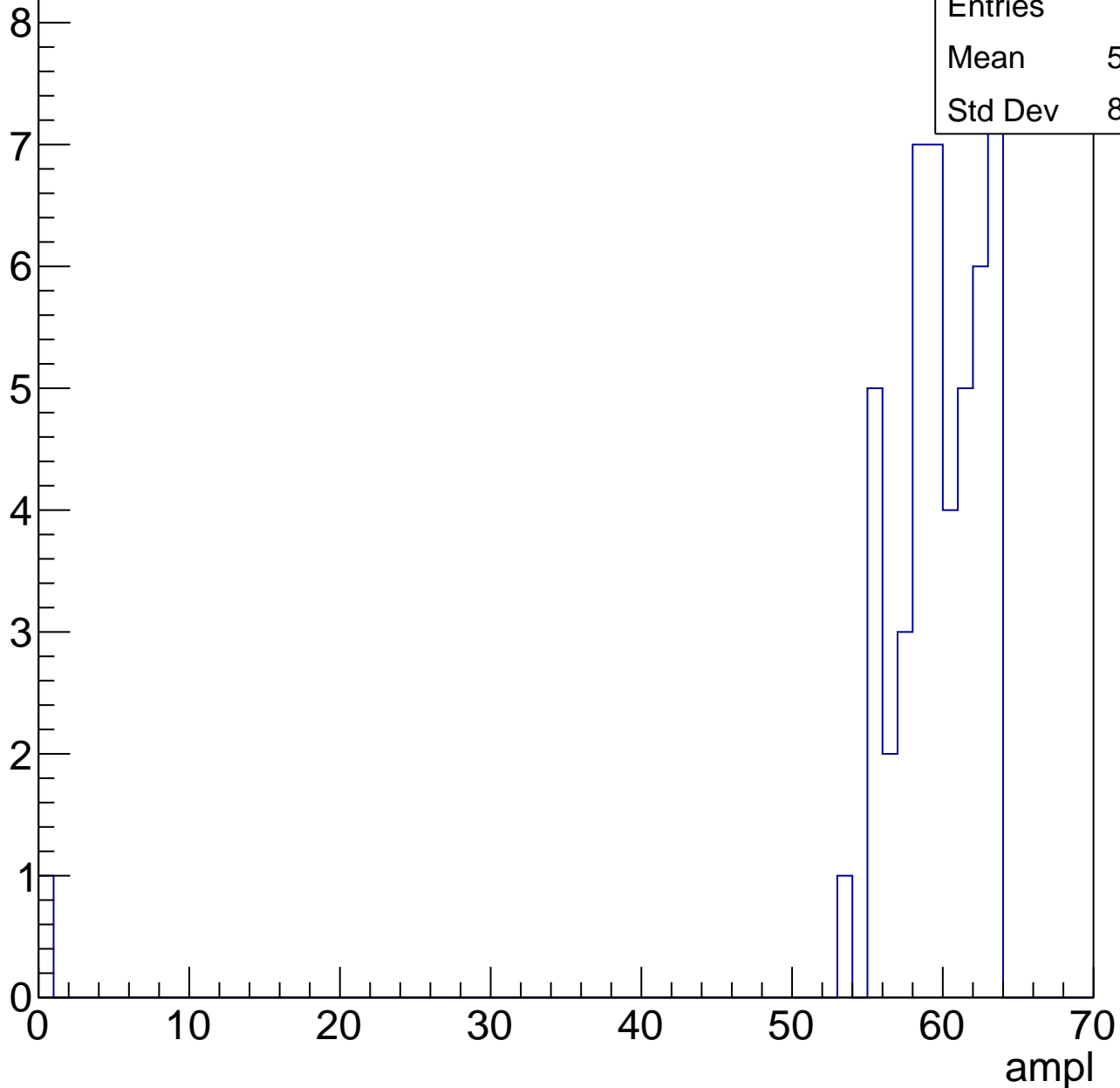


# B1L003S, U23-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.18
Std Dev	8.815

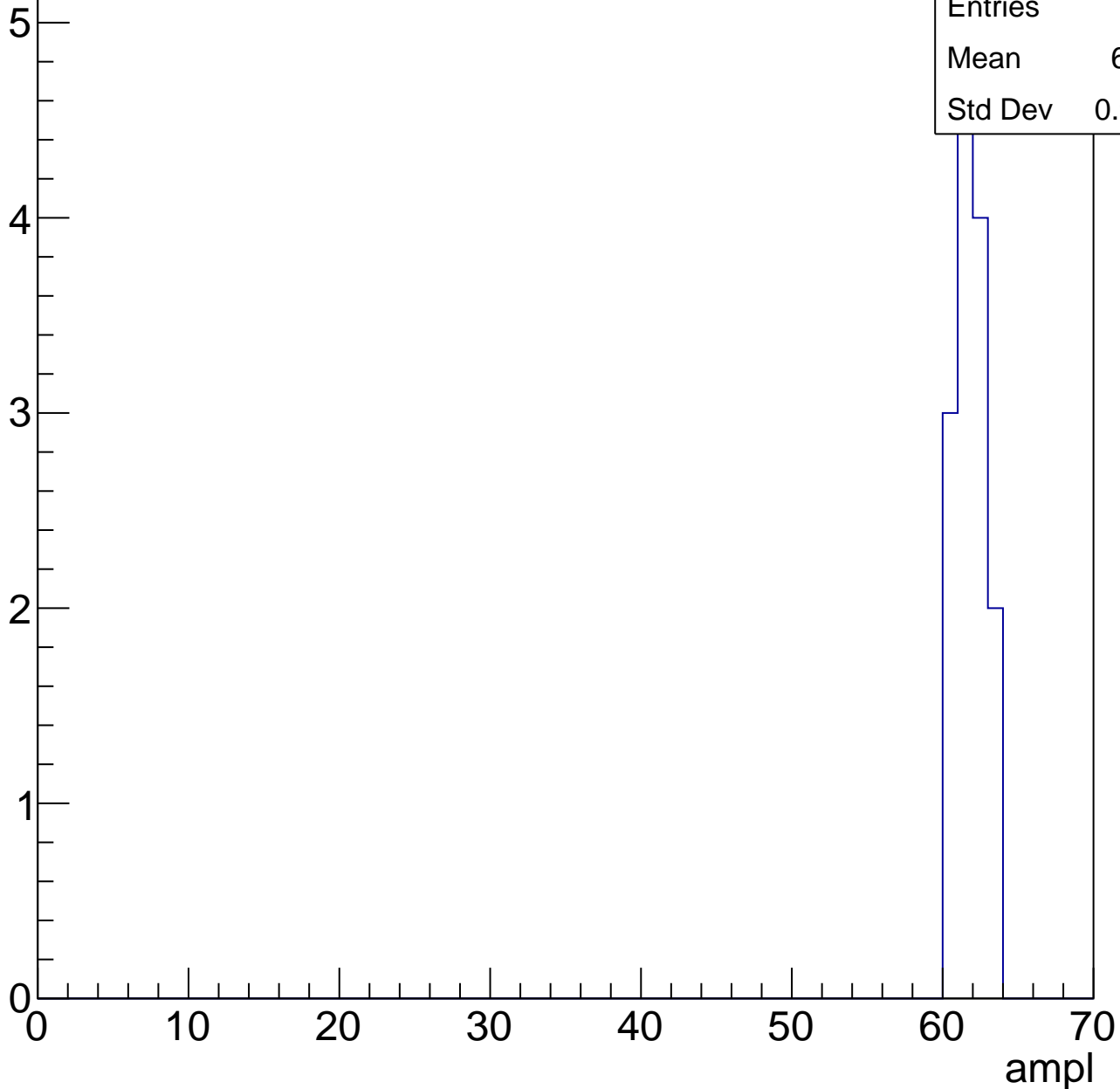


# B1L003S, U23-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	14
Mean	61.36
Std Dev	0.9715





# B1L003S, U23-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	79
Mean	30.29
Std Dev	3.515

**Gaus mean : 30.2909**

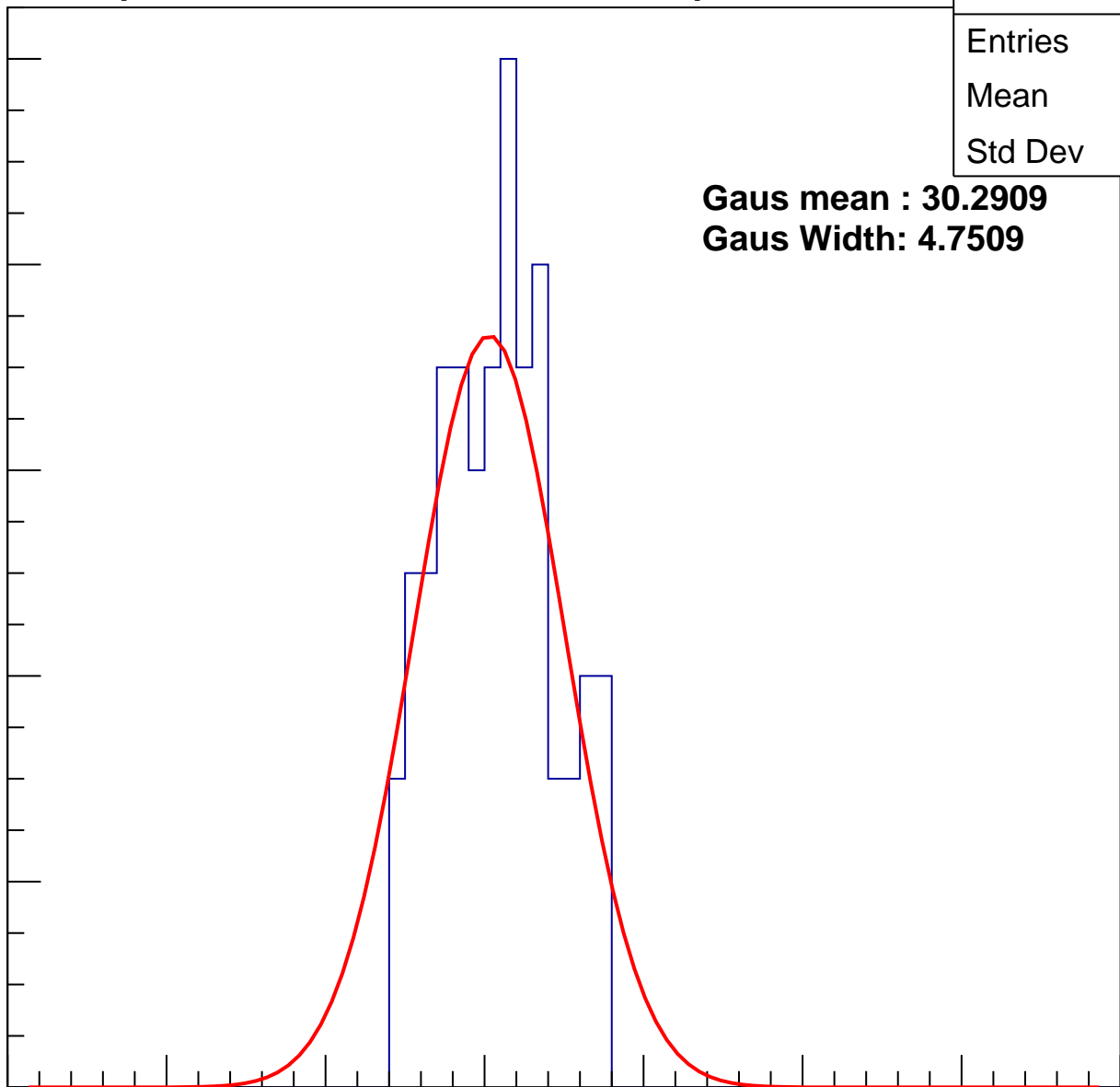
**Gaus Width: 4.7509**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch63, adc1

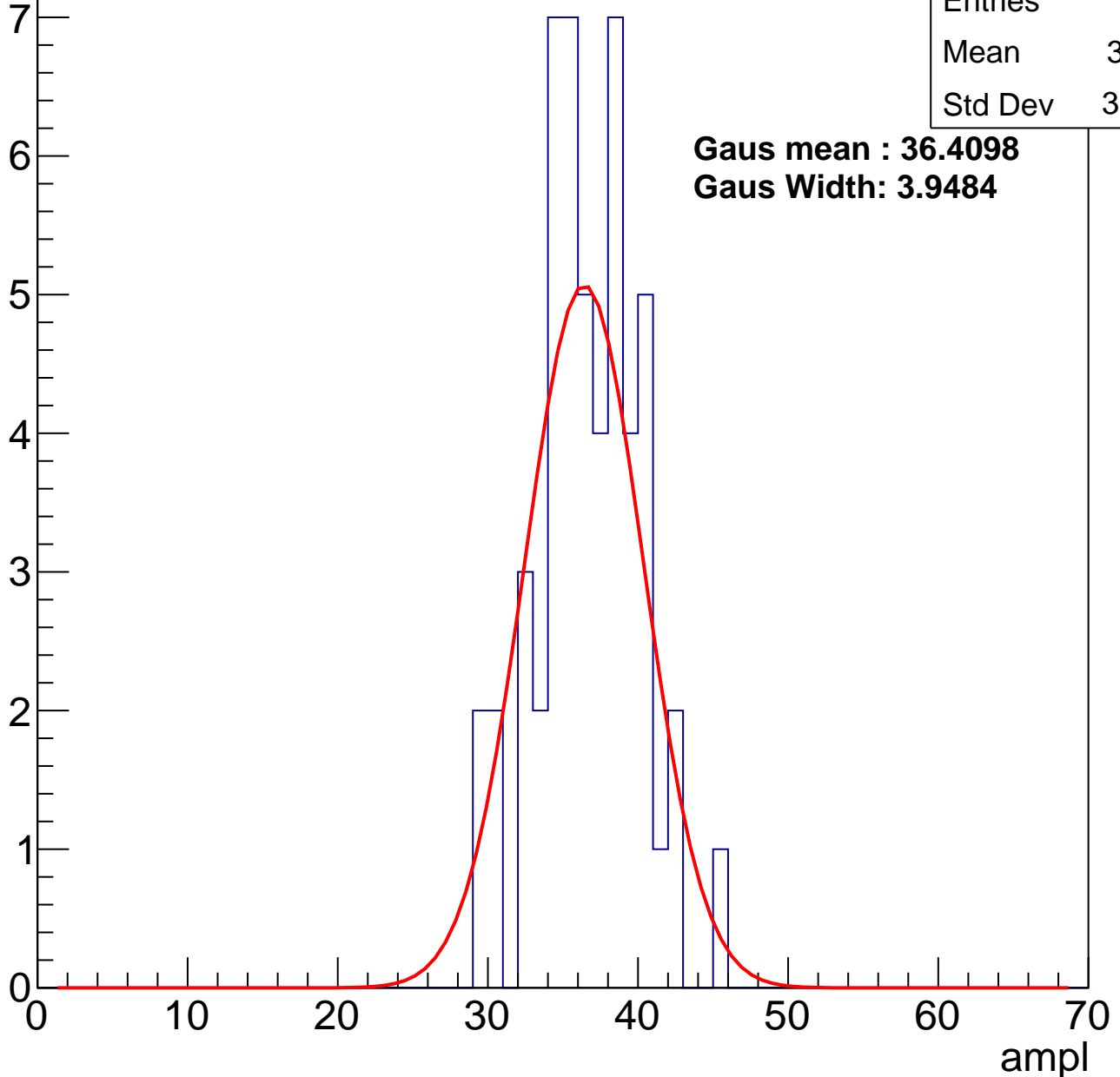
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	36.21
Std Dev	3.382

**Gaus mean : 36.4098**

**Gaus Width: 3.9484**



# B1L003S, U23-ch63, adc2

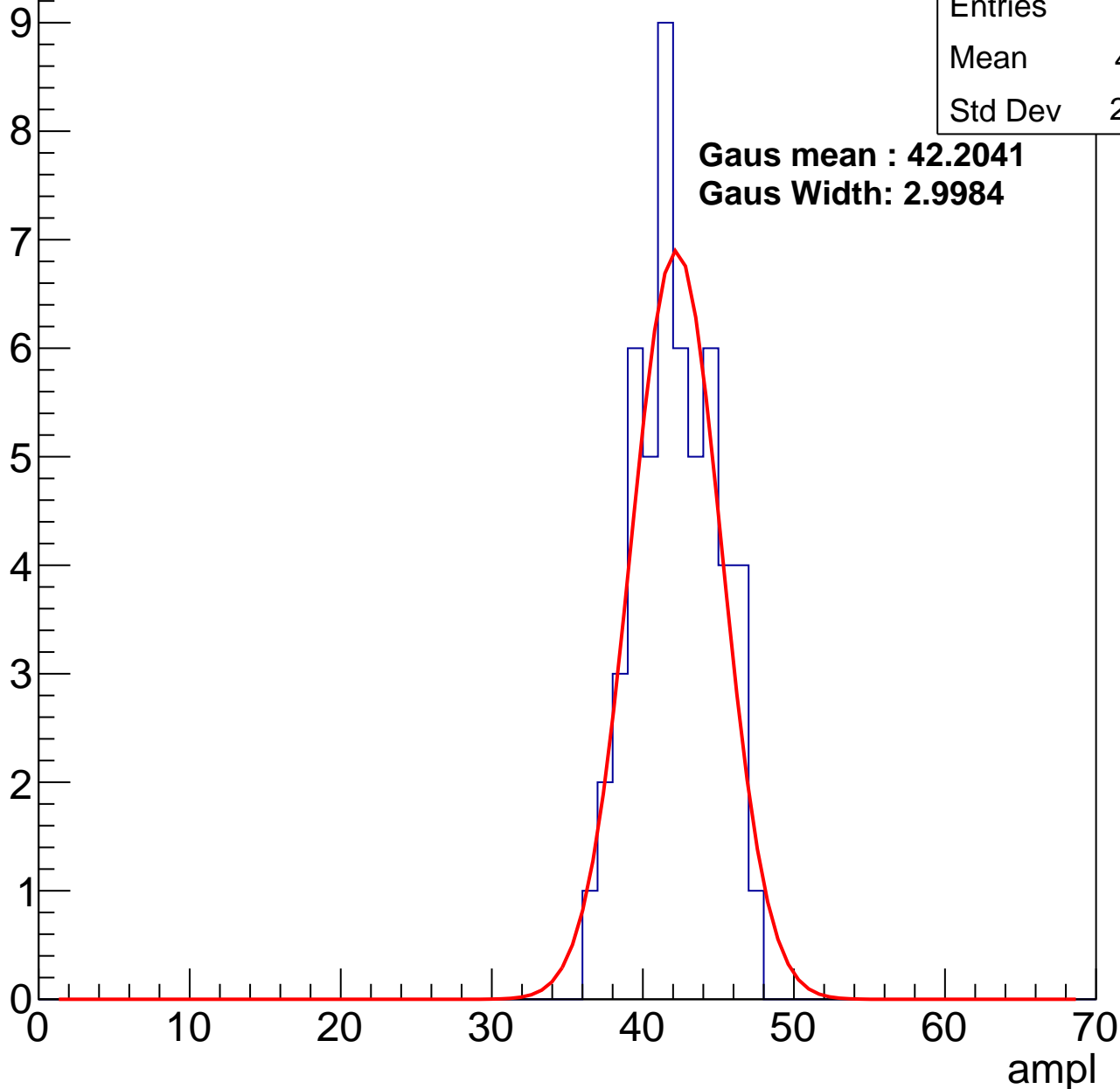
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	41.71
Std Dev	2.655

**Gaus mean : 42.2041**

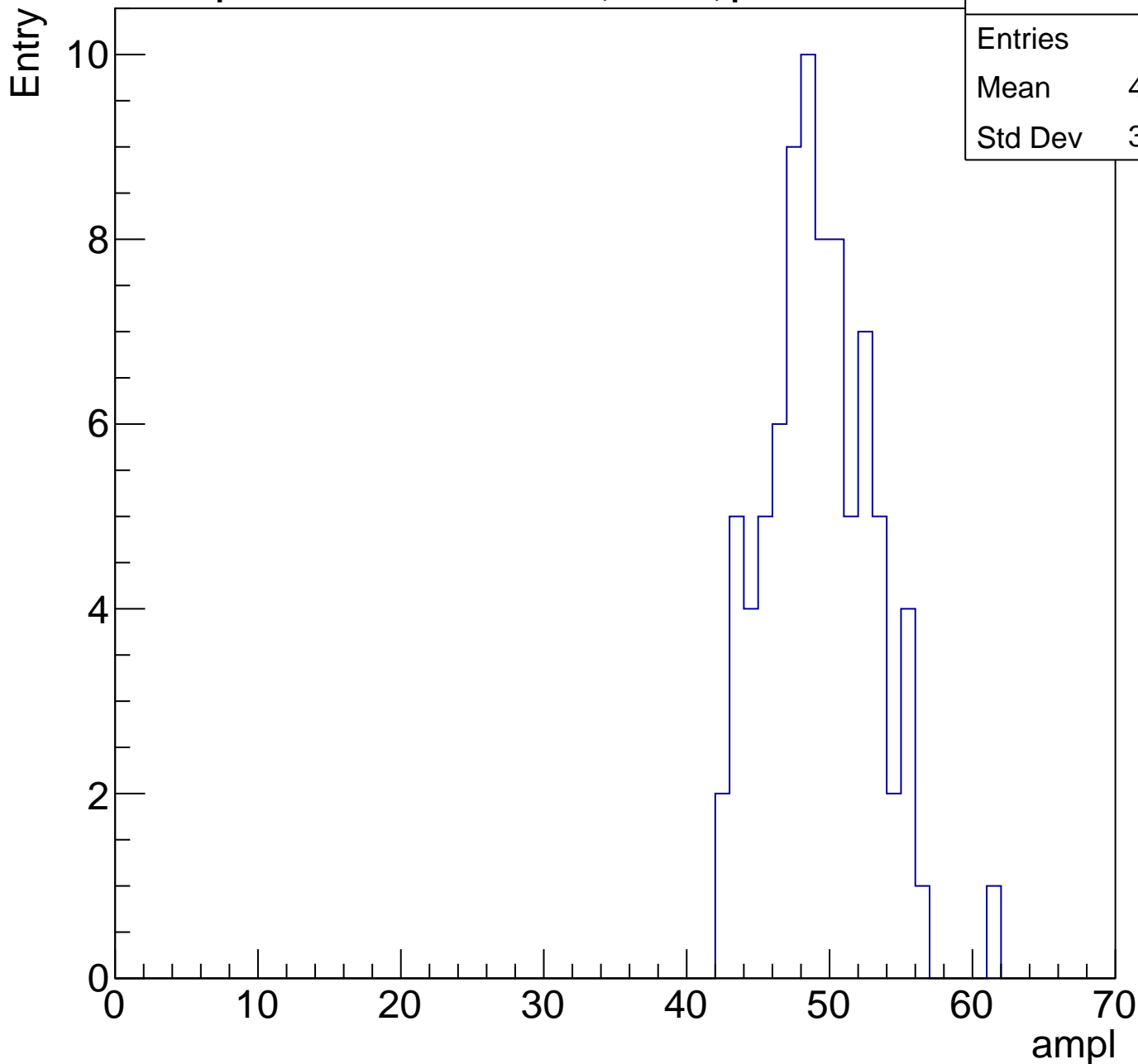
**Gaus Width: 2.9984**



# B1L003S, U23-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	82
Mean	48.78
Std Dev	3.683

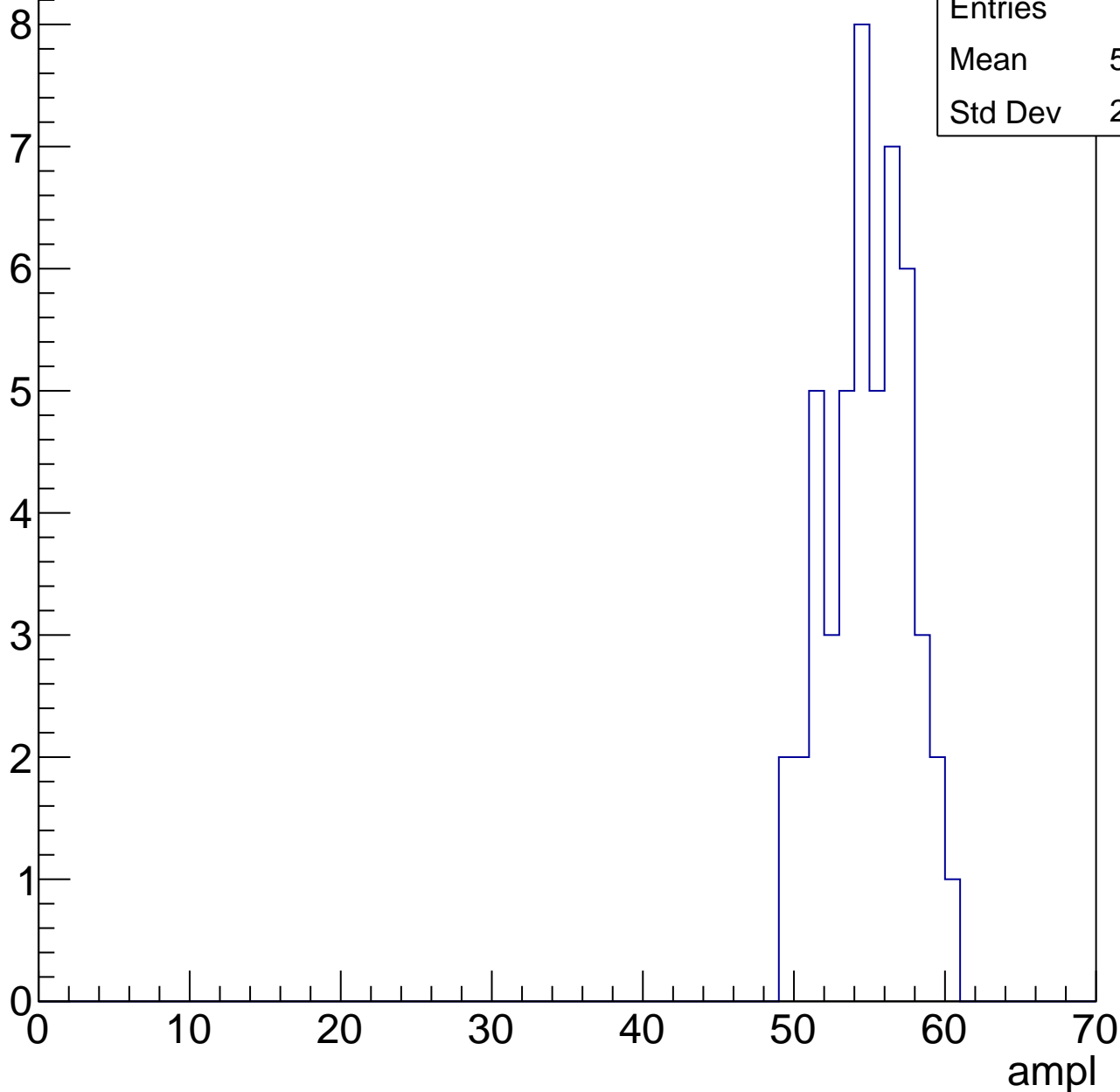


# B1L003S, U23-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	54.43
Std Dev	2.695

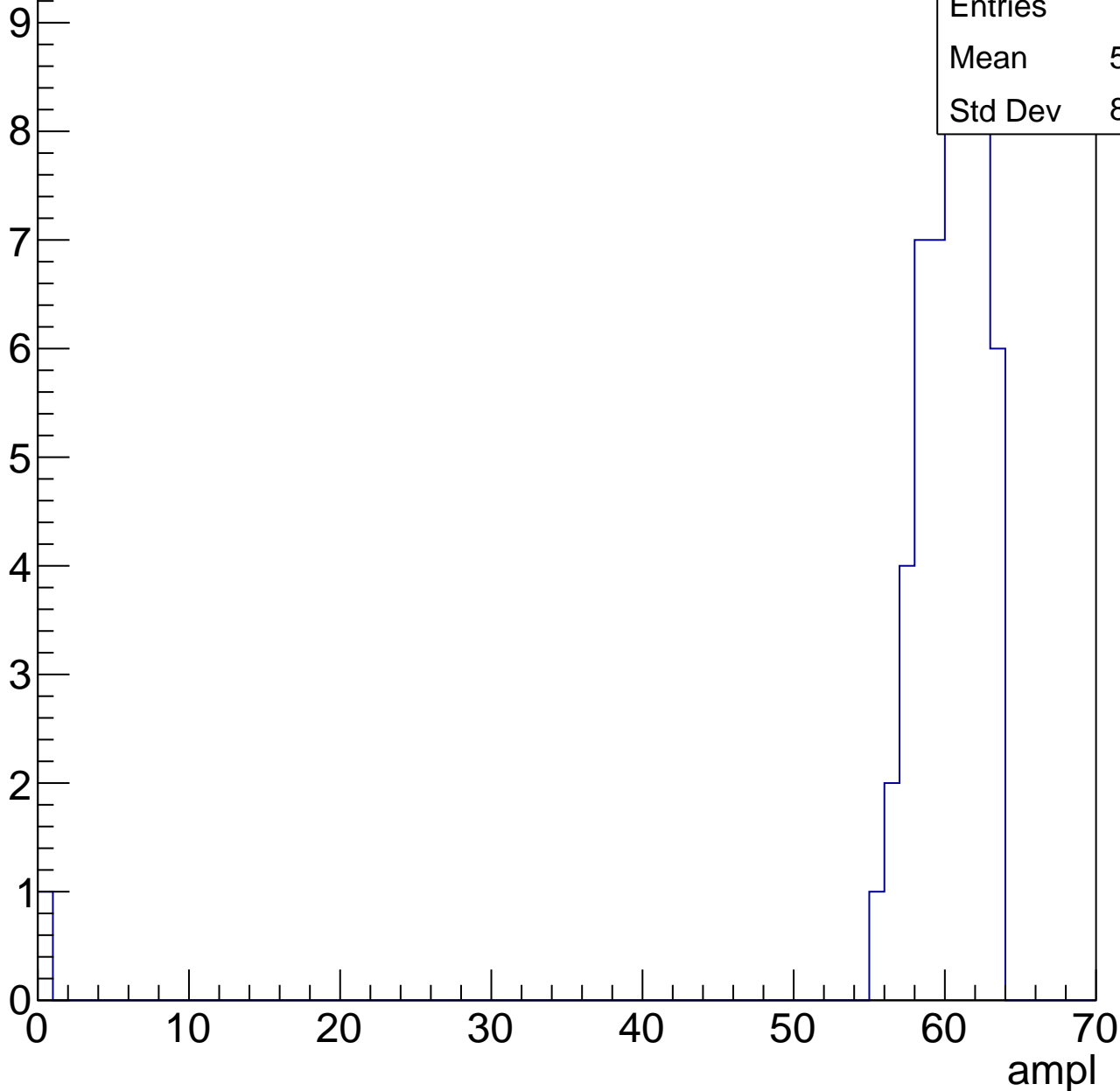


# B1L003S, U23-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

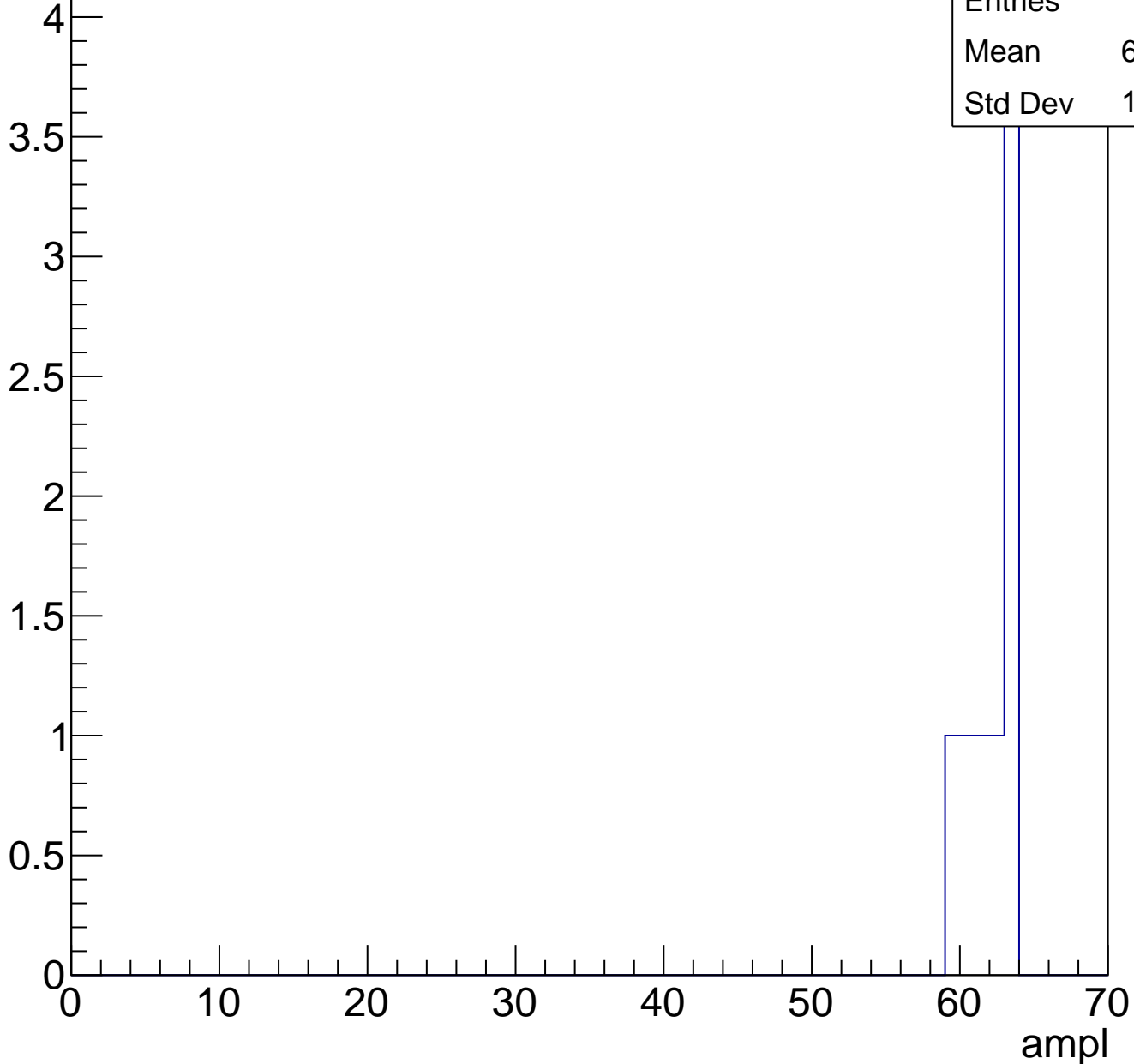
Entries	53
Mean	58.83
Std Dev	8.415



# B1L003S, U23-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch64, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	66
Mean	28.92
Std Dev	3.221

**Gaus mean : 28.7469**

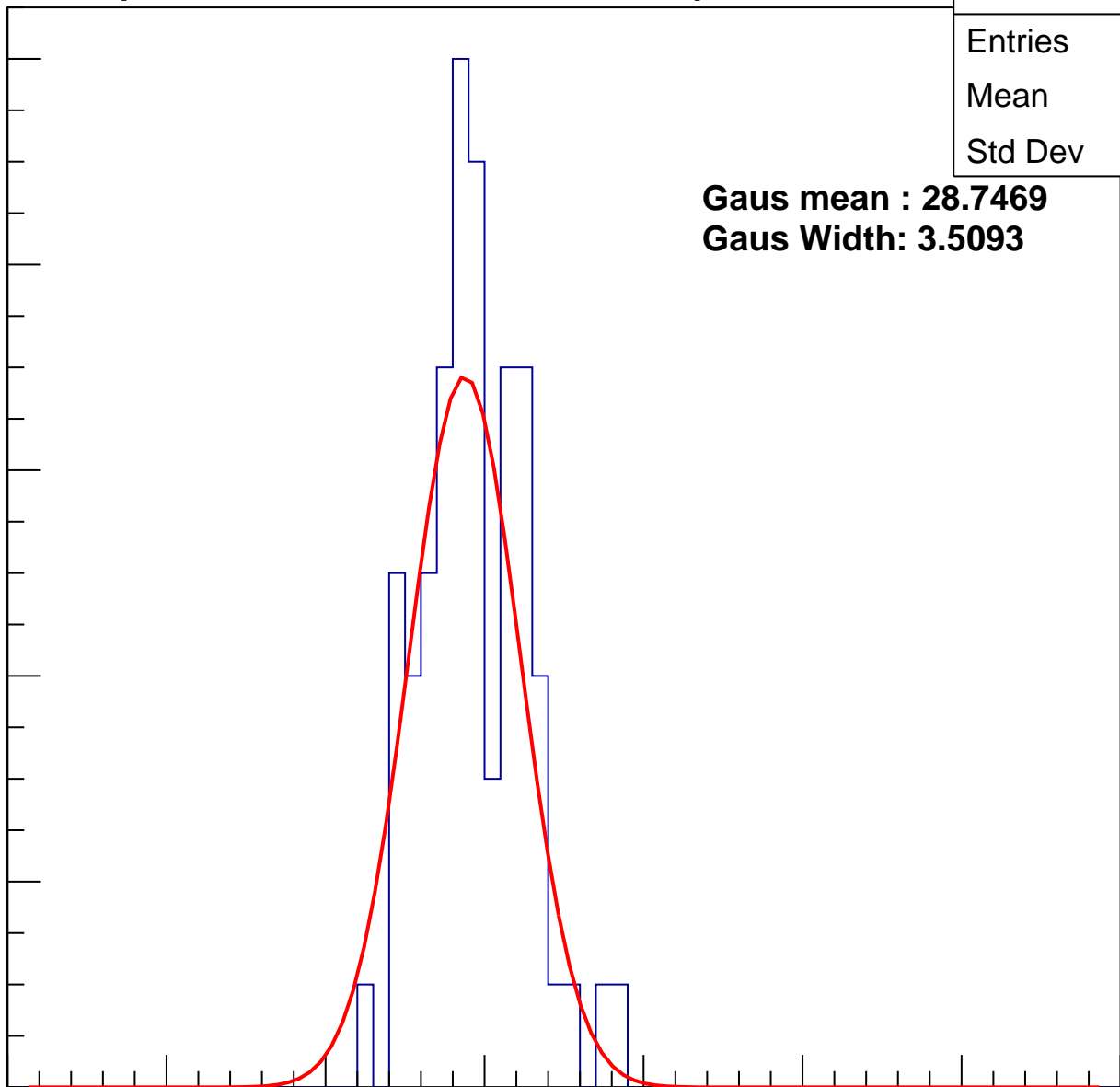
**Gaus Width: 3.5093**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch64, adc1

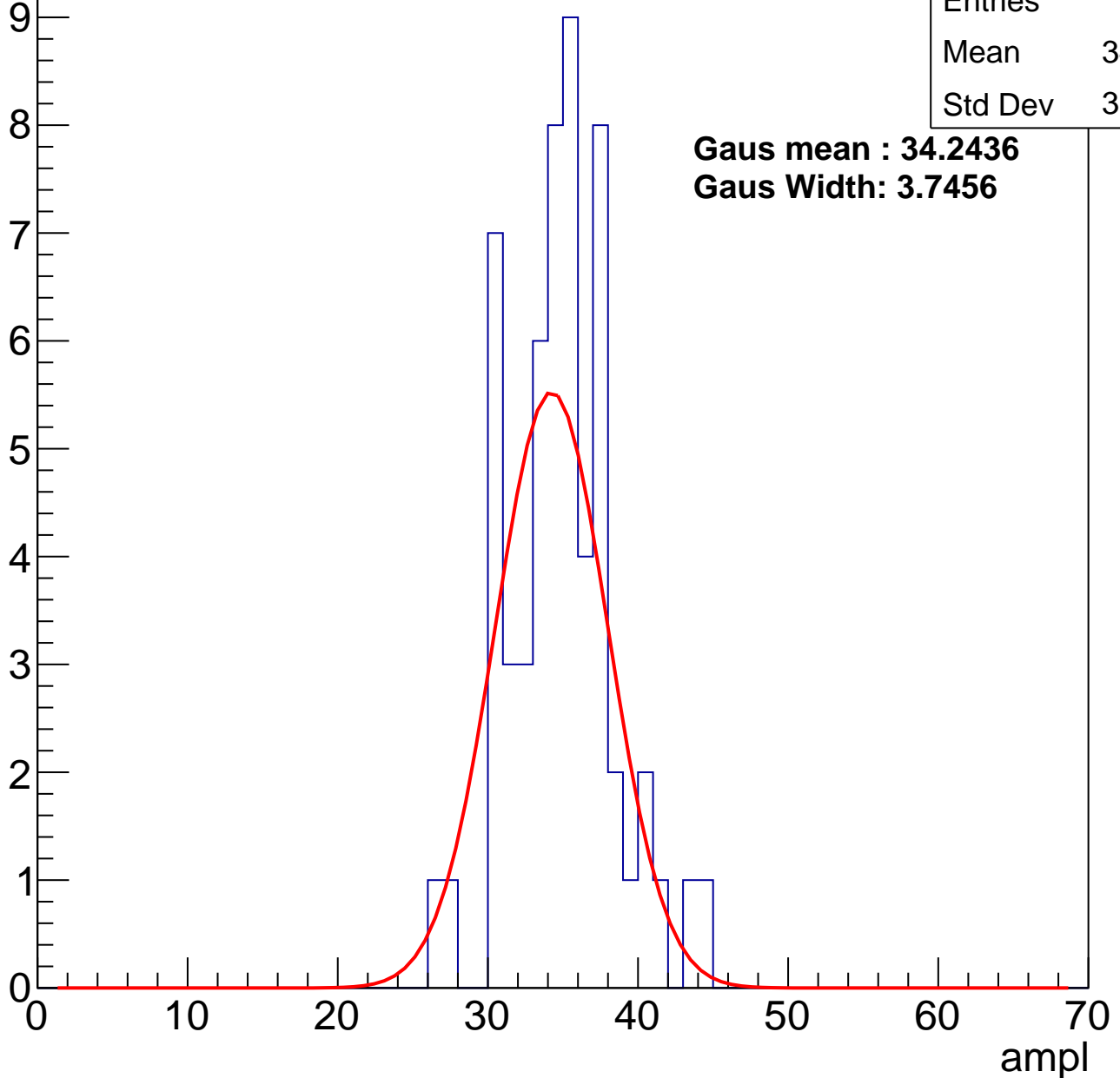
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	34.48
Std Dev	3.505

**Gaus mean : 34.2436**

**Gaus Width: 3.7456**



# B1L003S, U23-ch64, adc2

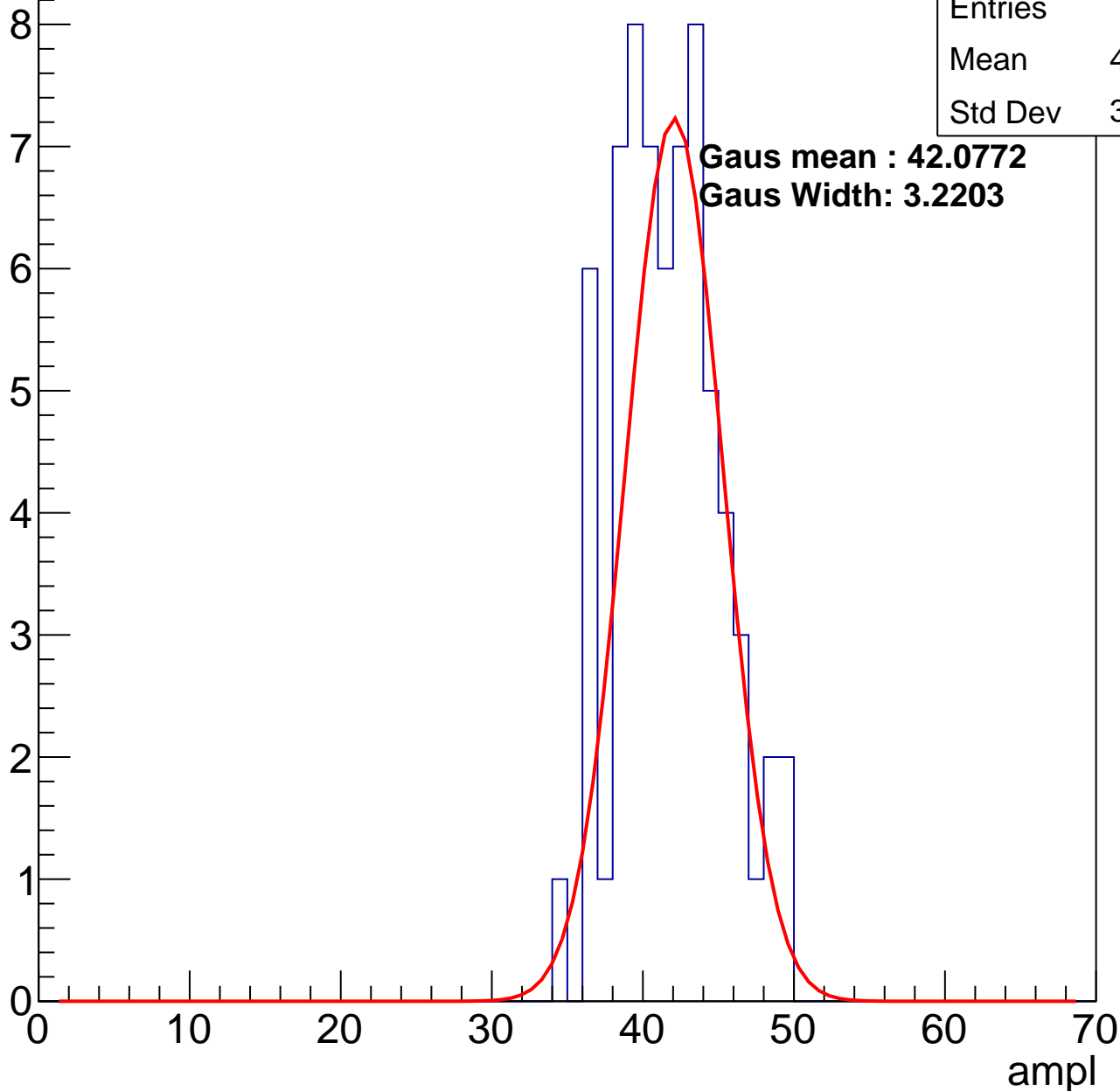
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	41.29
Std Dev	3.422

**Gaus mean : 42.0772**

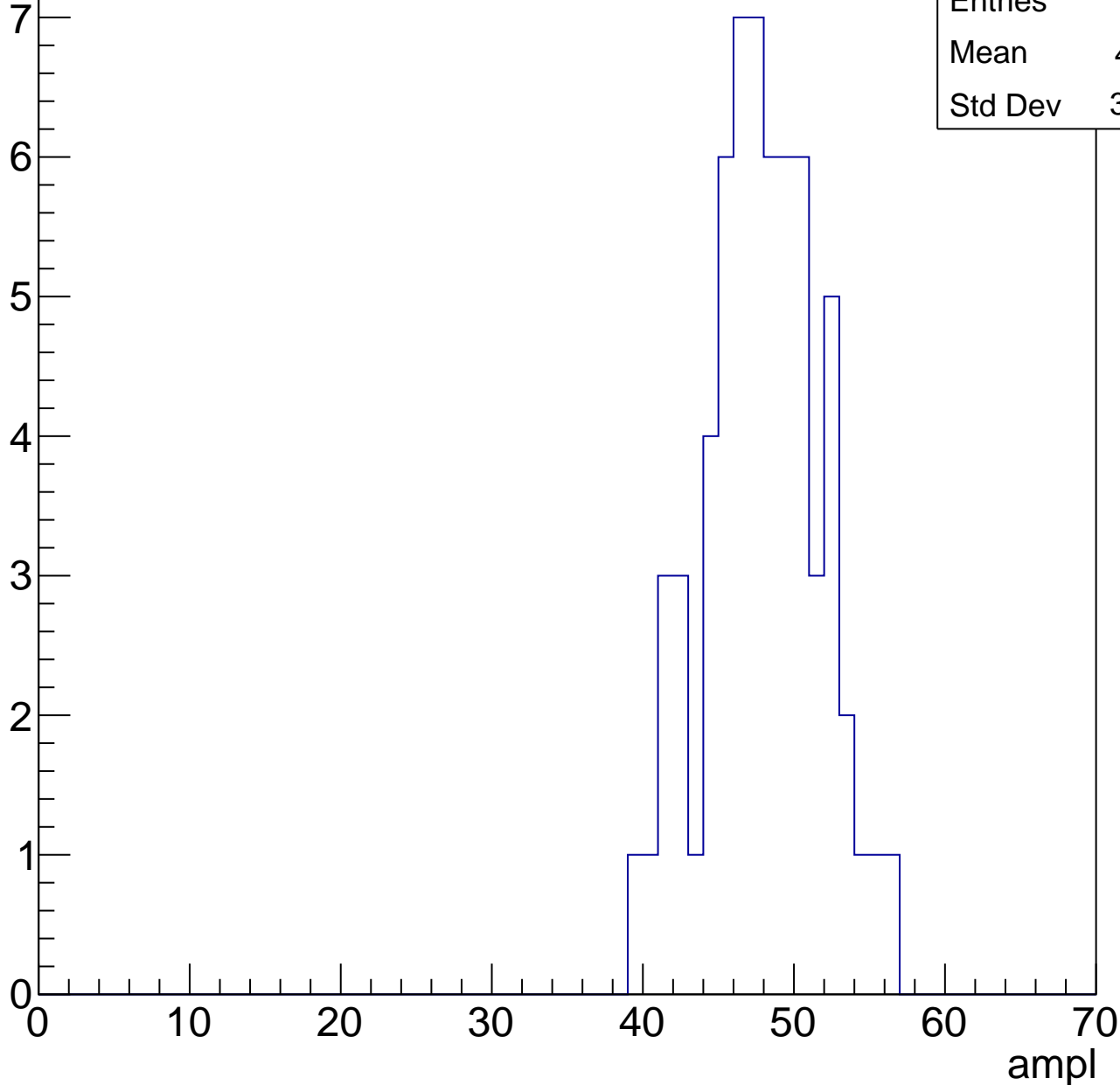
**Gaus Width: 3.2203**



# B1L003S, U23-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



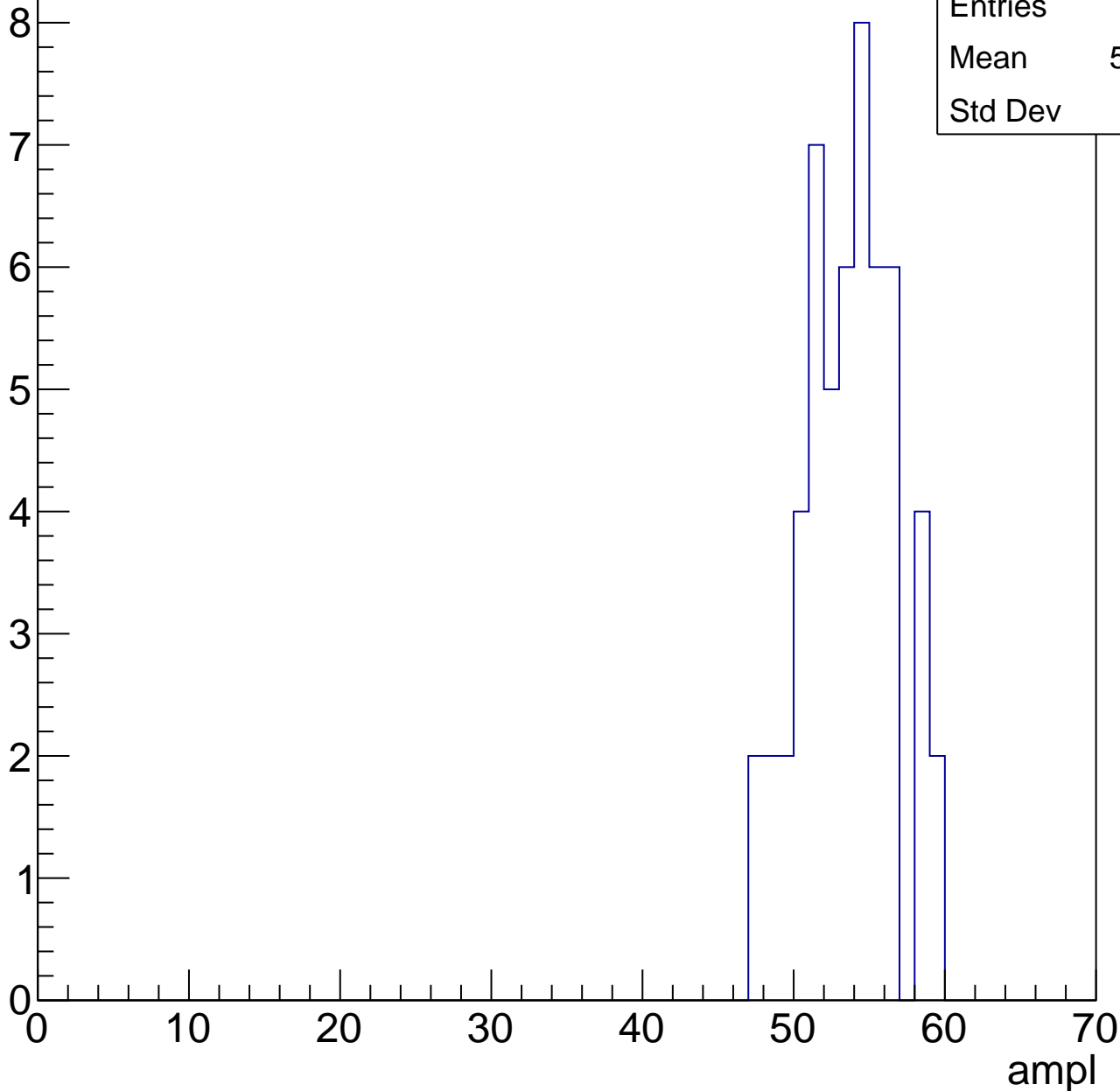
Entries	64
Mean	47.41
Std Dev	3.732

# B1L003S, U23-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	53.17
Std Dev	2.98

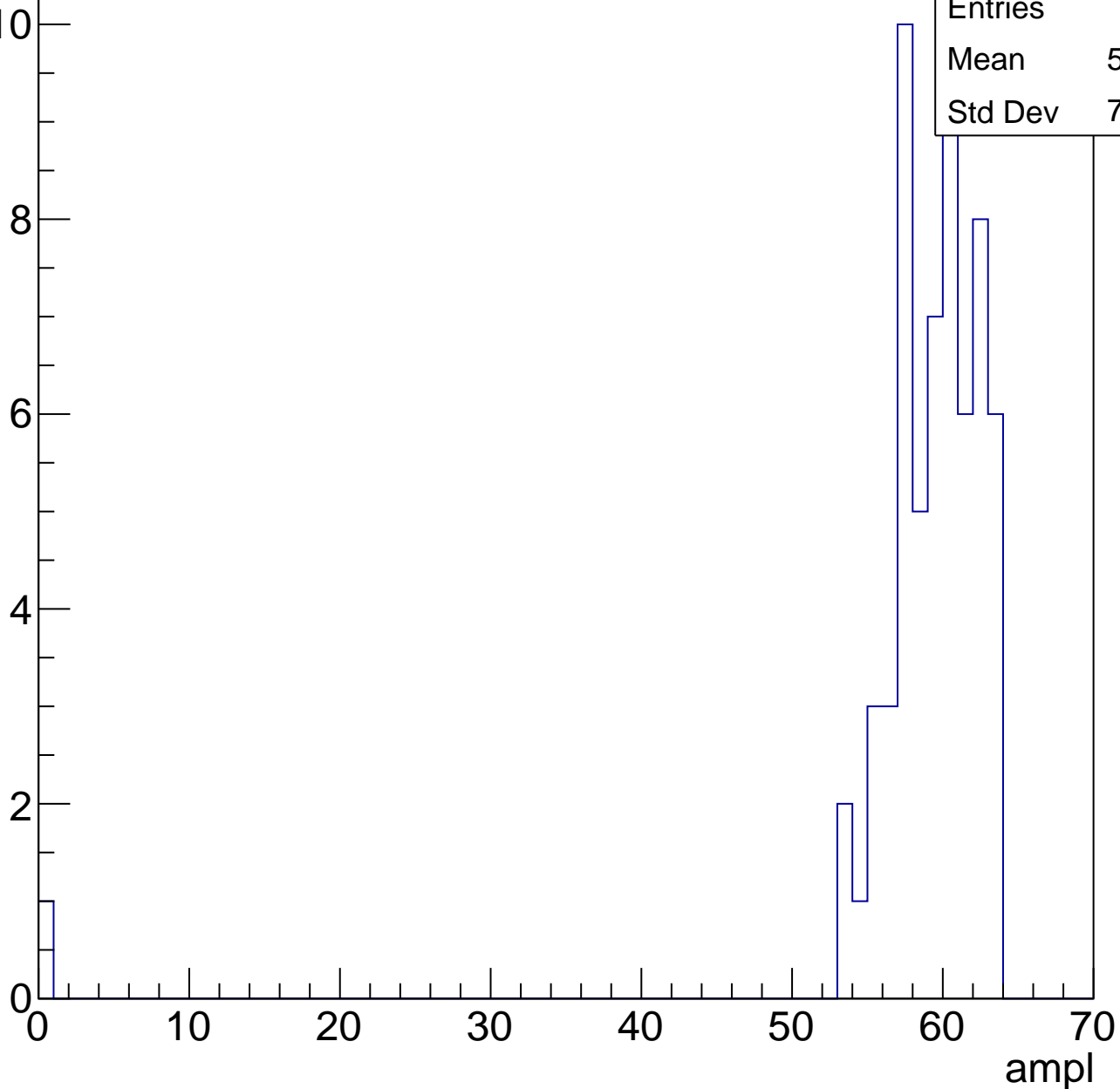


# B1L003S, U23-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

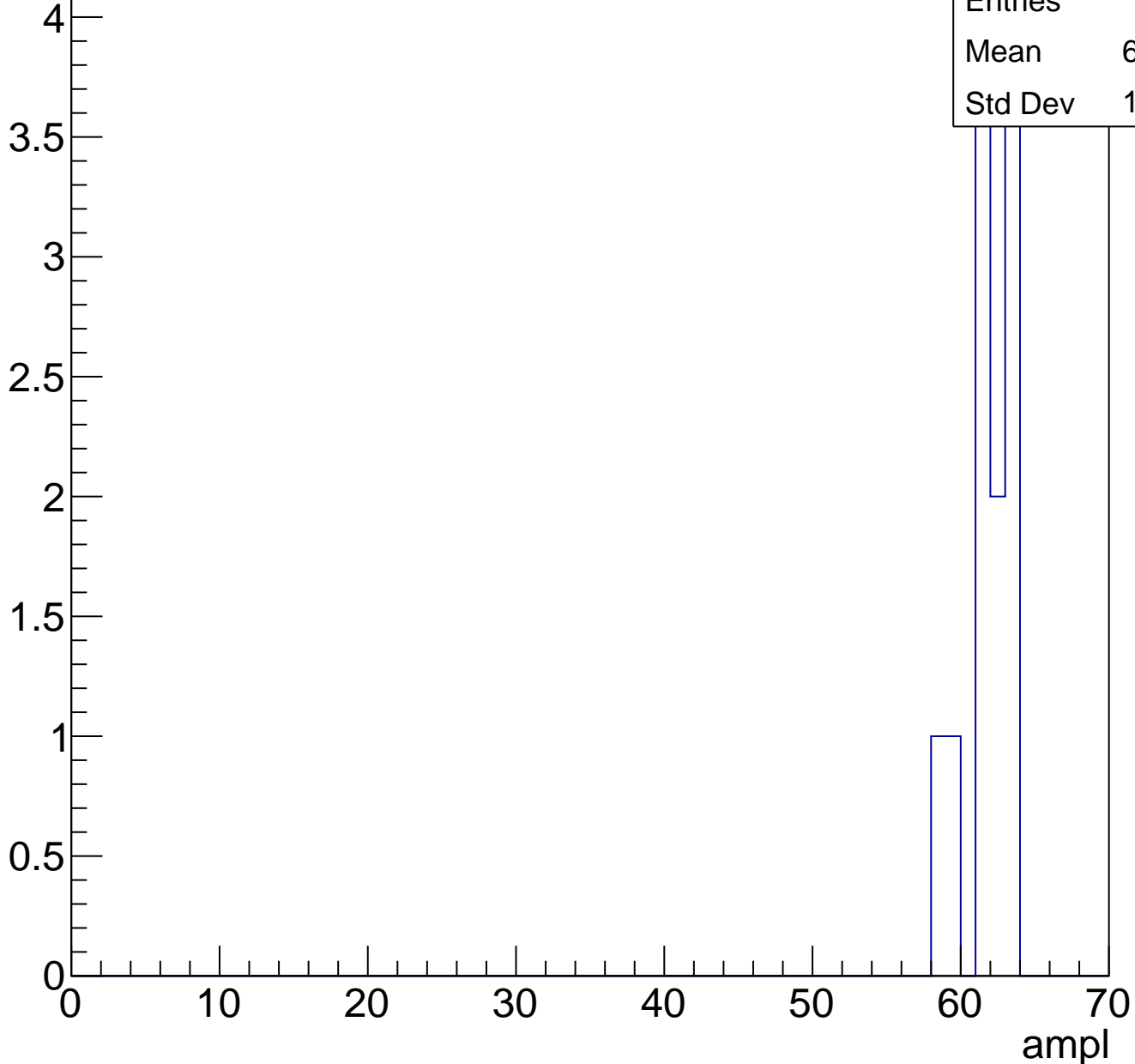
Entries	61
Mean	58.13
Std Dev	7.948



# B1L003S, U23-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

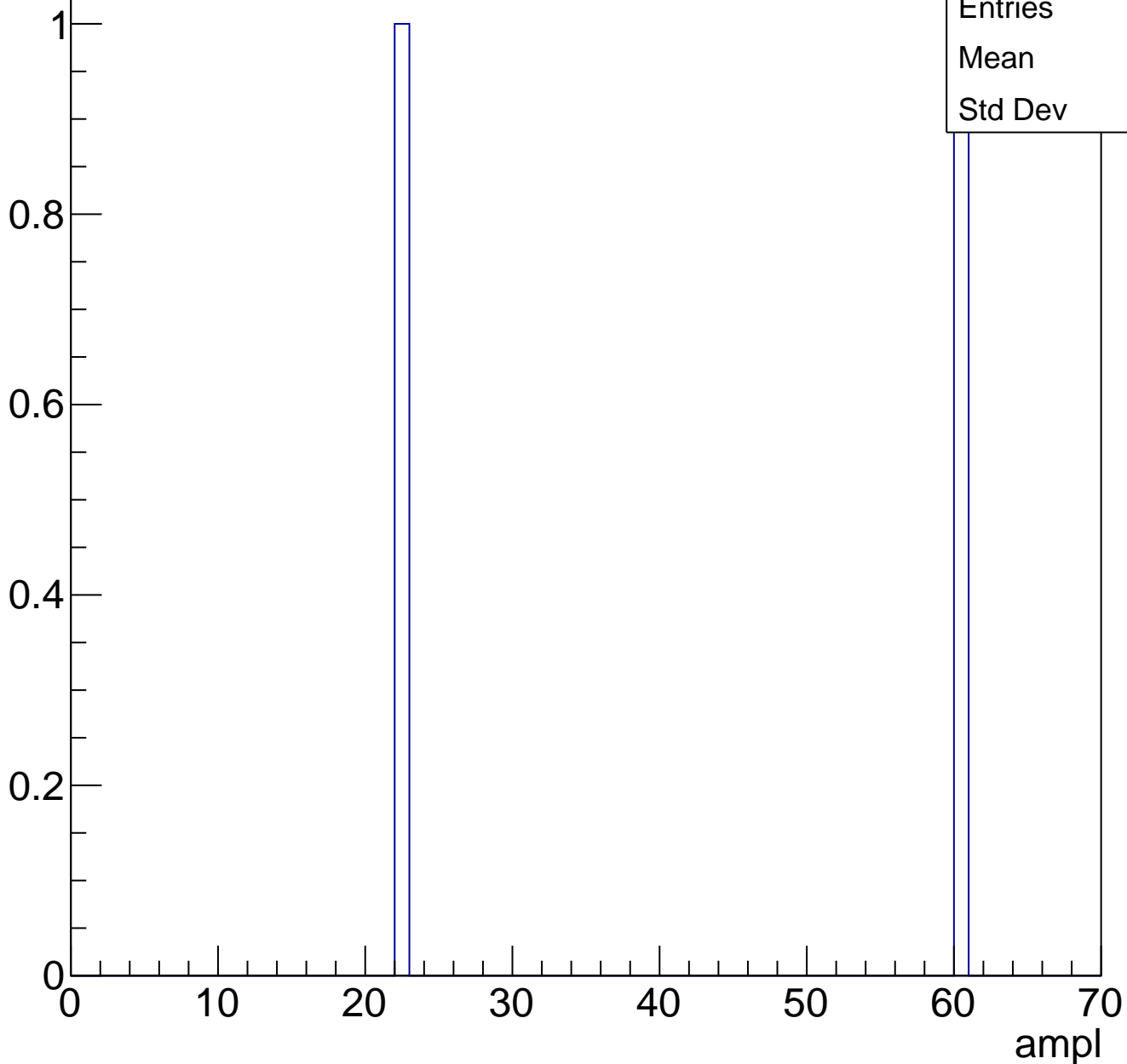




# B1L003S, U23-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch65, adc0

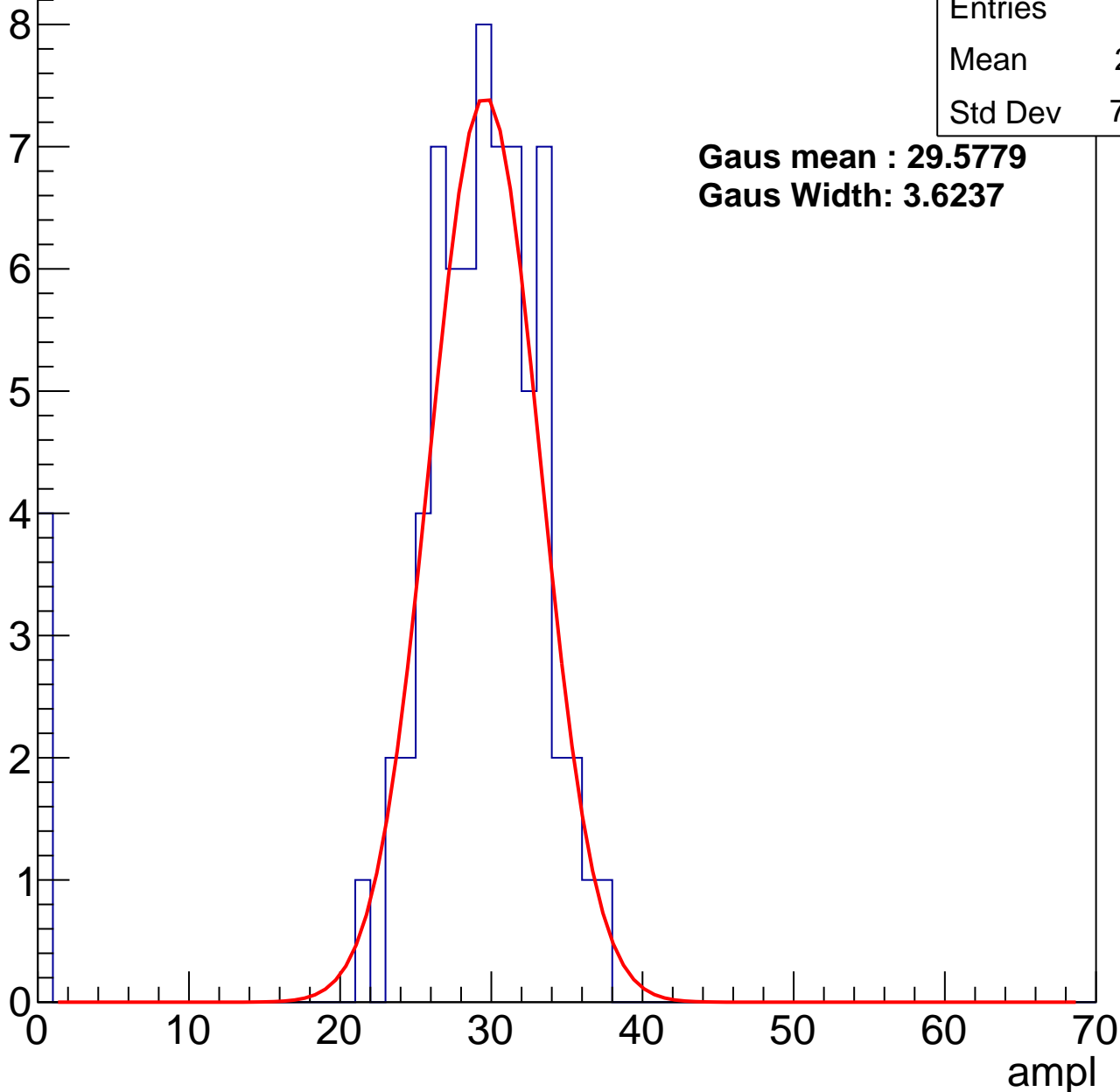
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	27.61
Std Dev	7.449

**Gaus mean : 29.5779**

**Gaus Width: 3.6237**



# B1L003S, U23-ch65, adc1

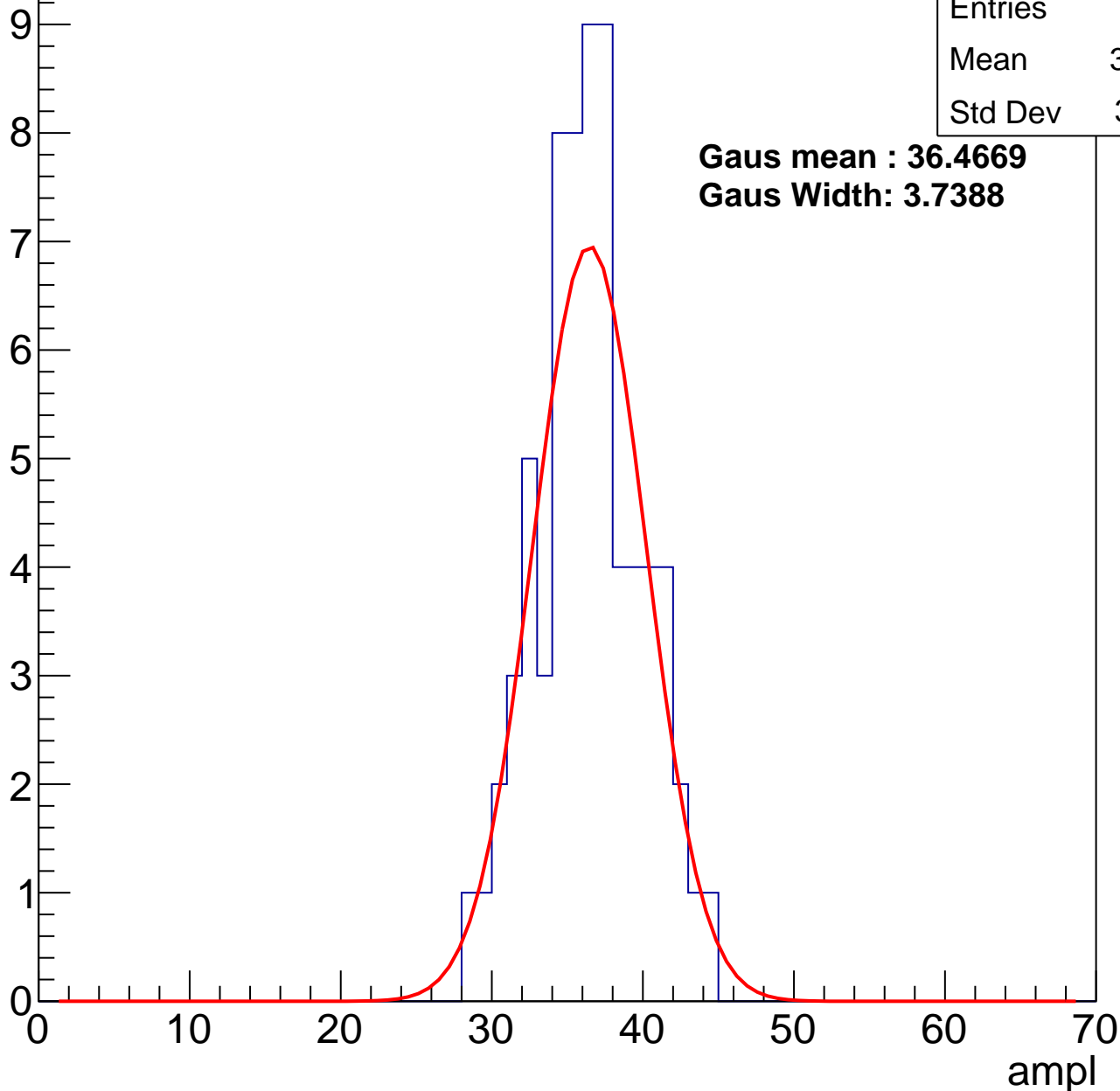
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.96
Std Dev	3.441

**Gaus mean : 36.4669**

**Gaus Width: 3.7388**



# B1L003S, U23-ch65, adc2

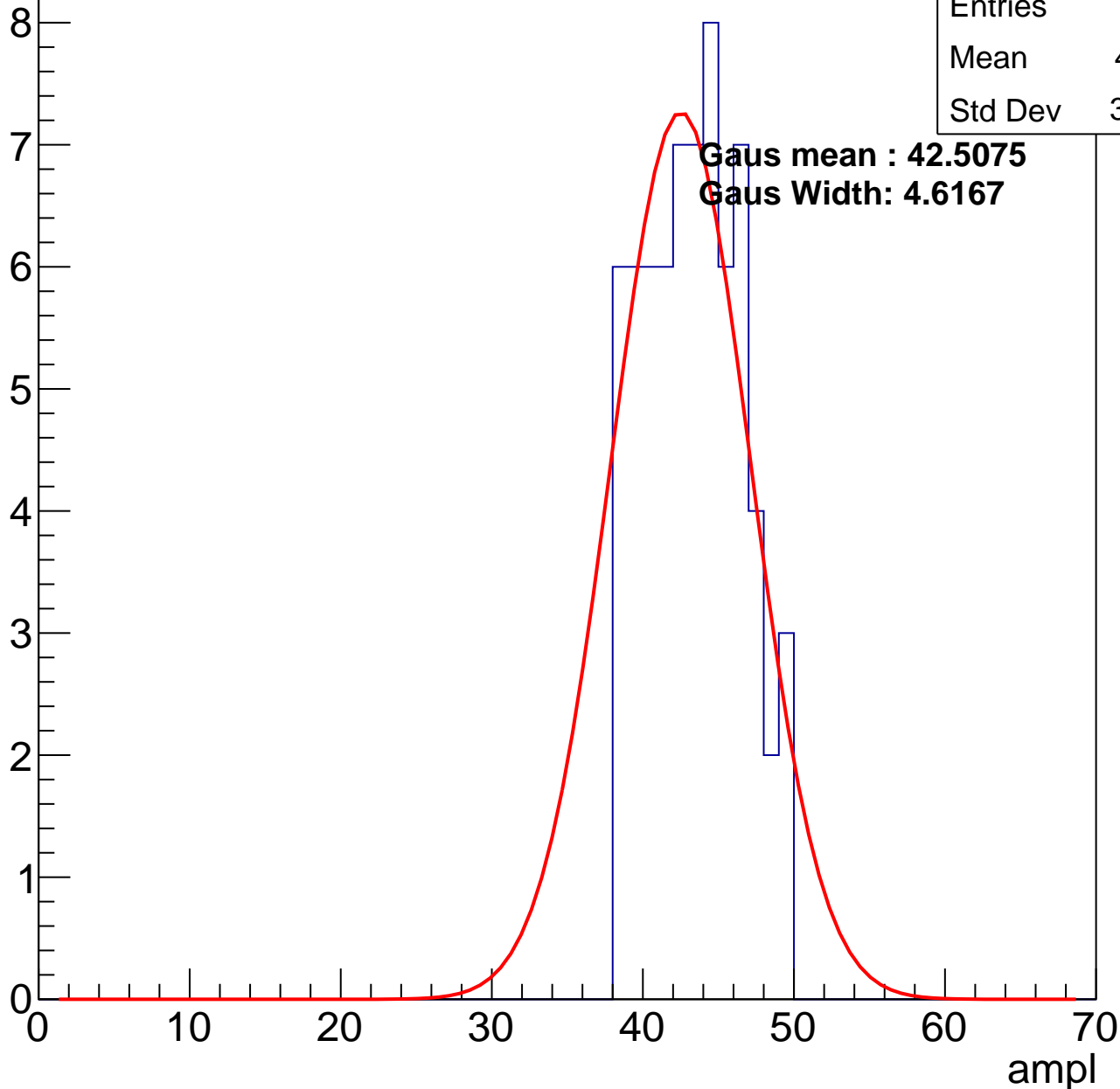
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.91
Std Dev	3.086

**Gaus mean : 42.5075**

**Gaus Width: 4.6167**

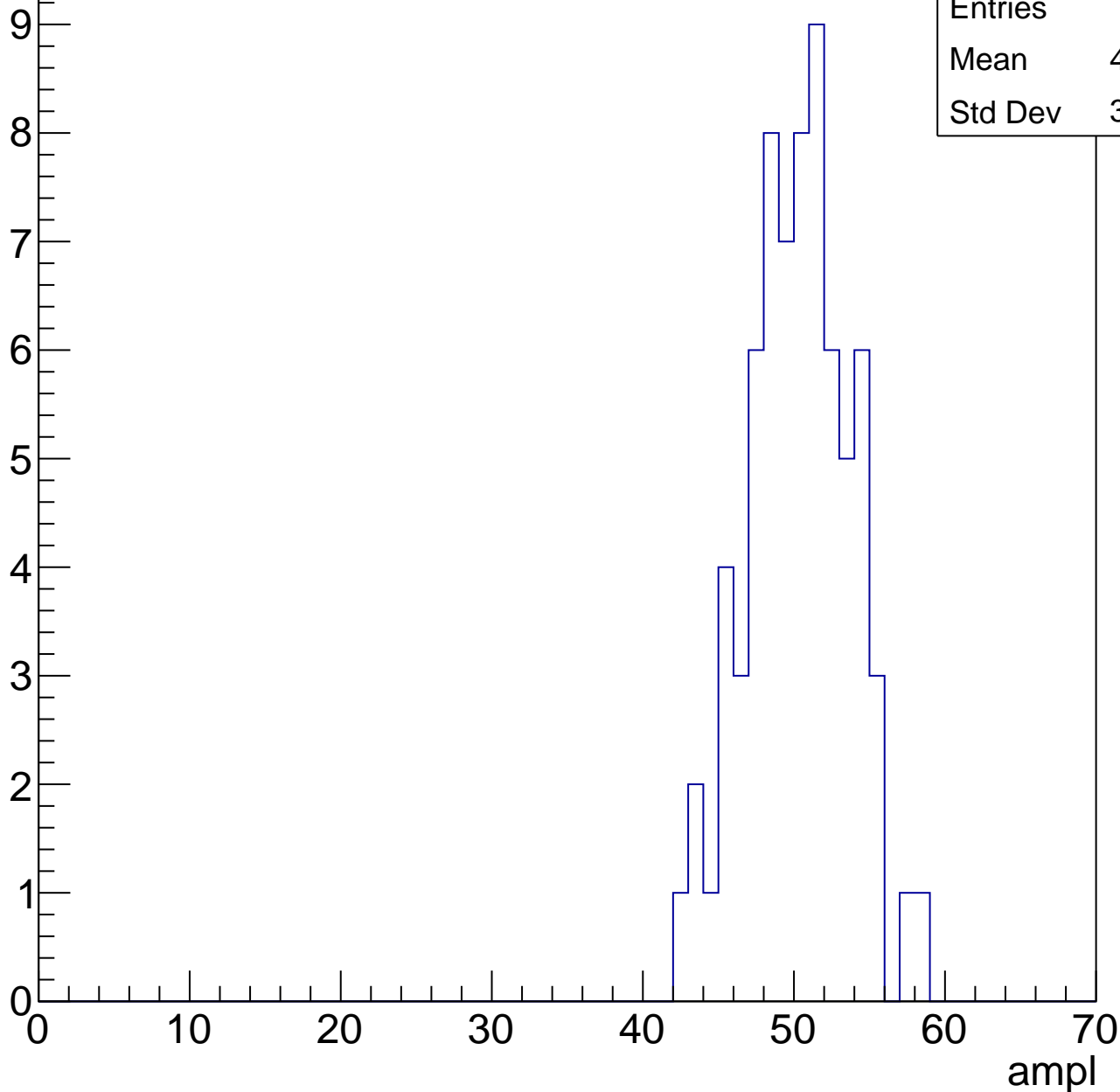


# B1L003S, U23-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

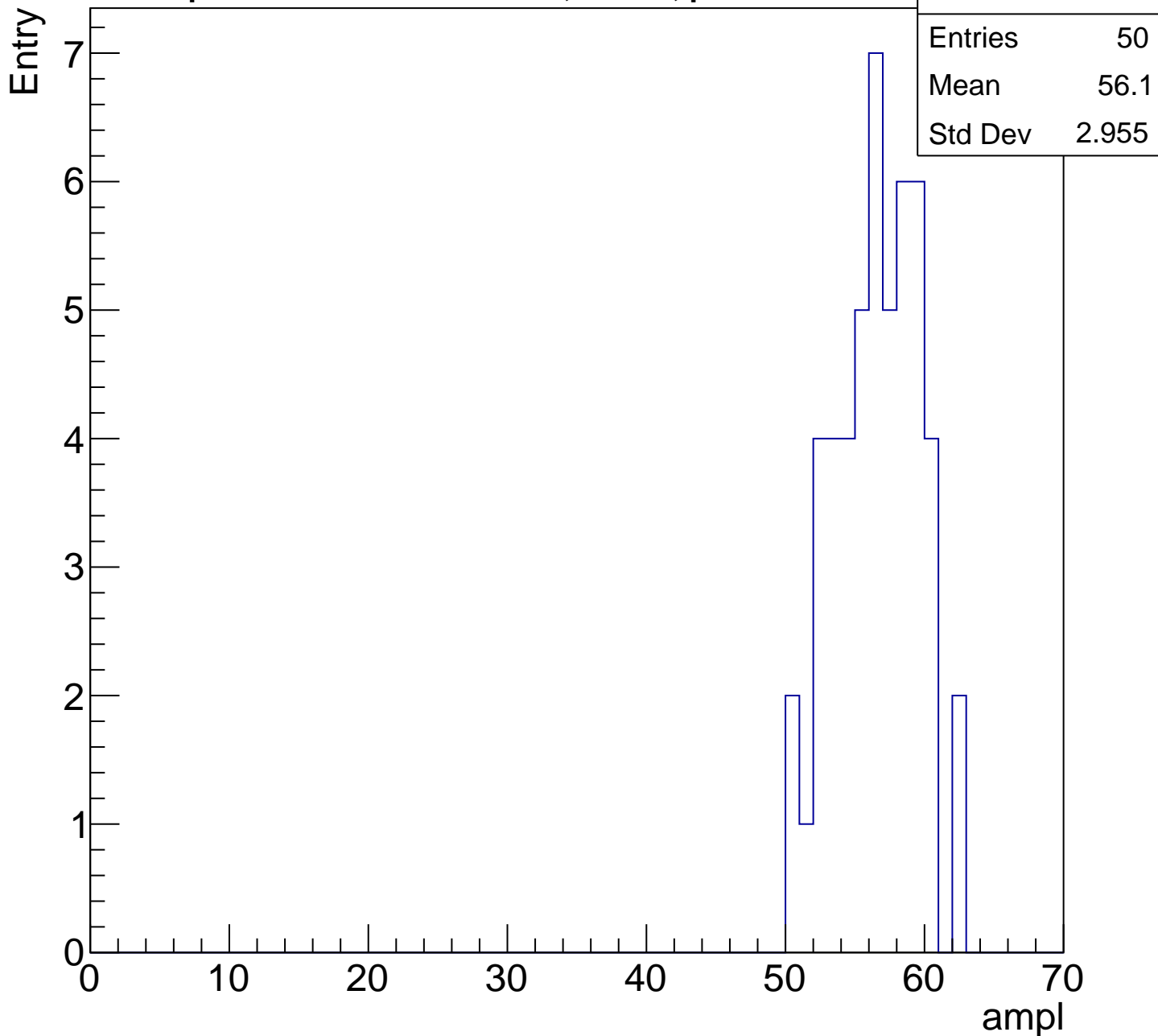
Entry

Entries	71
Mean	49.85
Std Dev	3.355



# B1L003S, U23-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

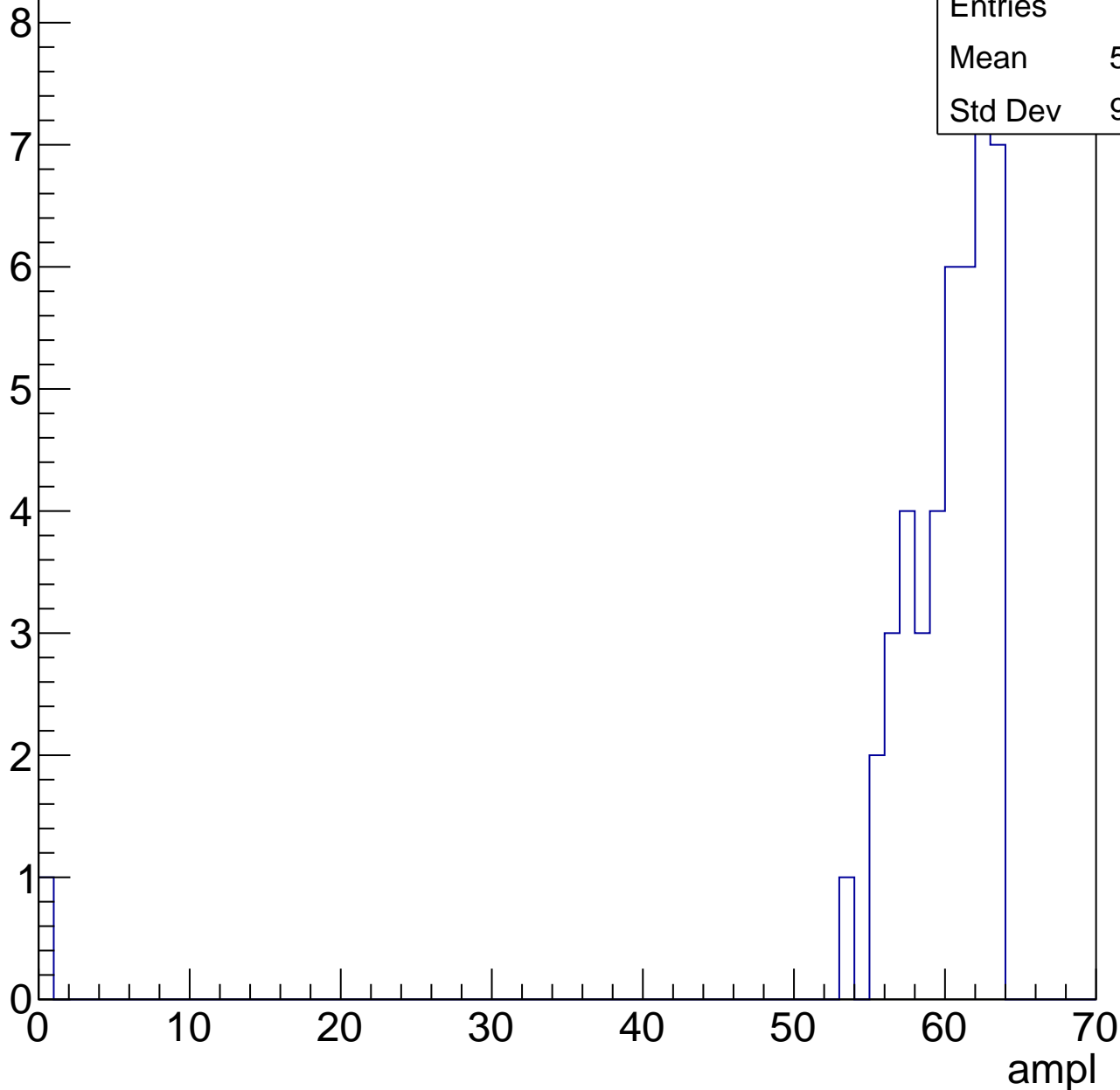


# B1L003S, U23-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

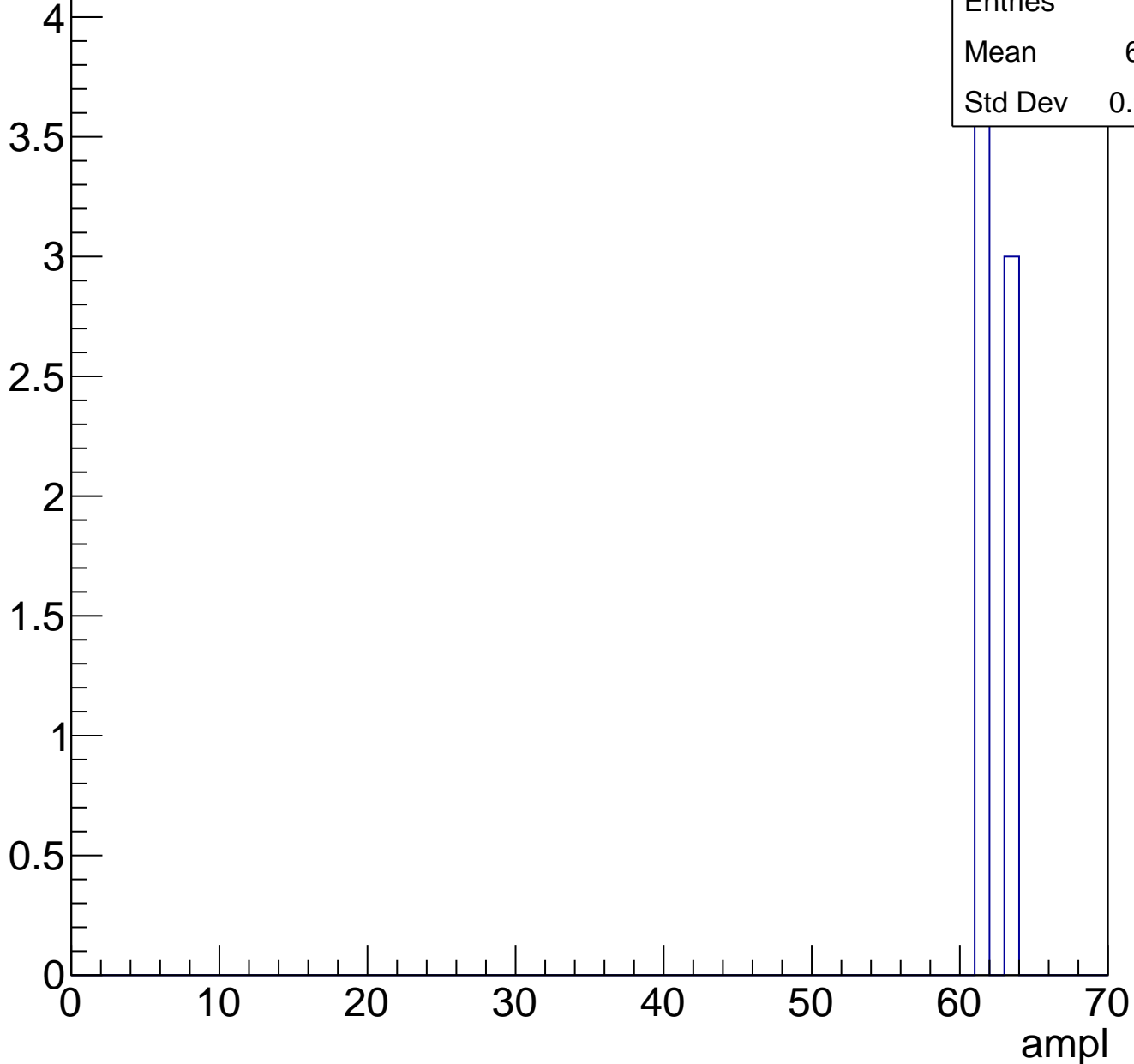
Entries	45
Mean	58.49
Std Dev	9.186



# B1L003S, U23-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L003S, U23-ch66, adc0

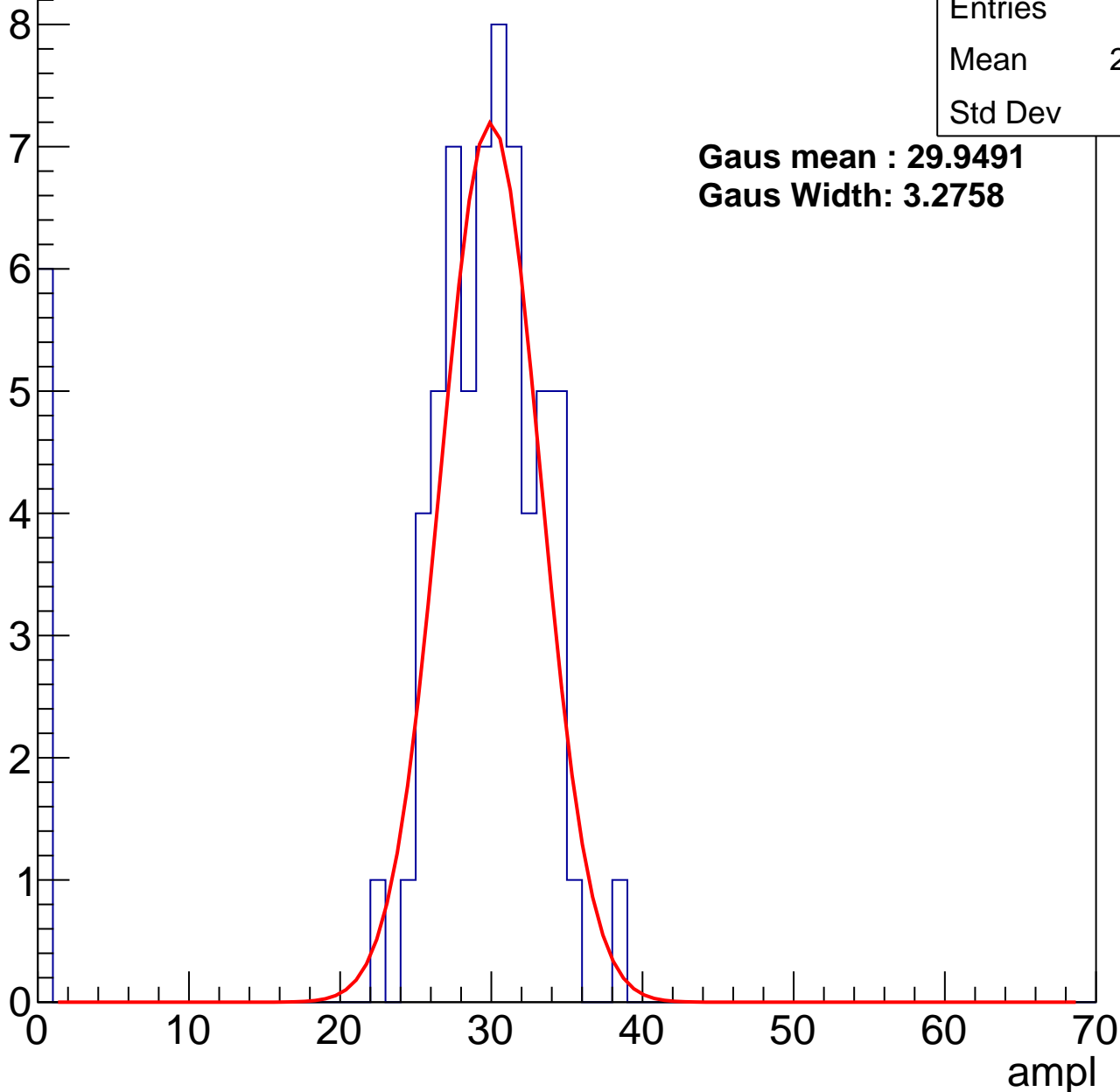
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	26.88
Std Dev	8.94

**Gaus mean : 29.9491**

**Gaus Width: 3.2758**



# B1L003S, U23-ch66, adc1

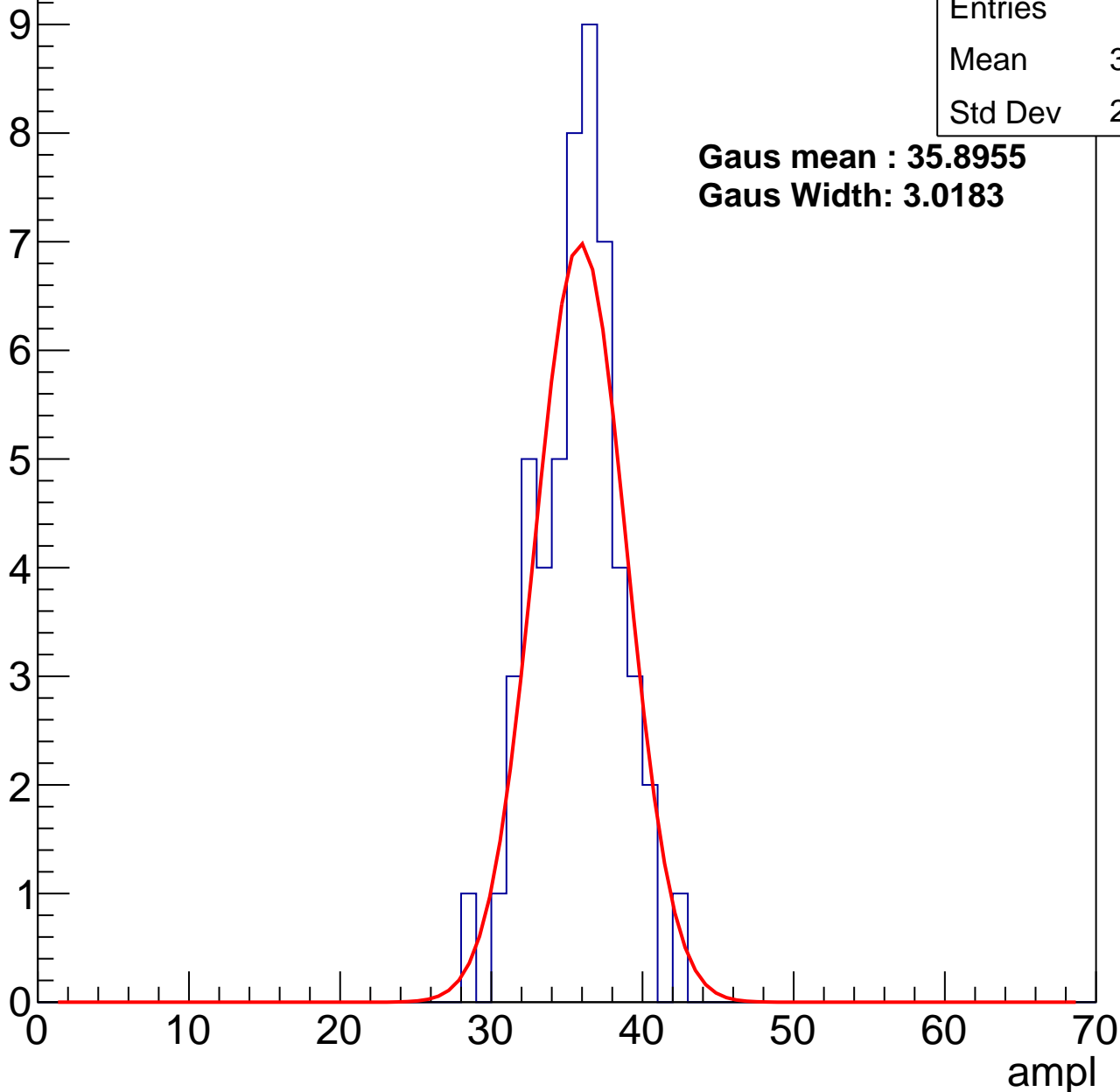
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.23
Std Dev	2.765

**Gaus mean : 35.8955**

**Gaus Width: 3.0183**



# B1L003S, U23-ch66, adc2

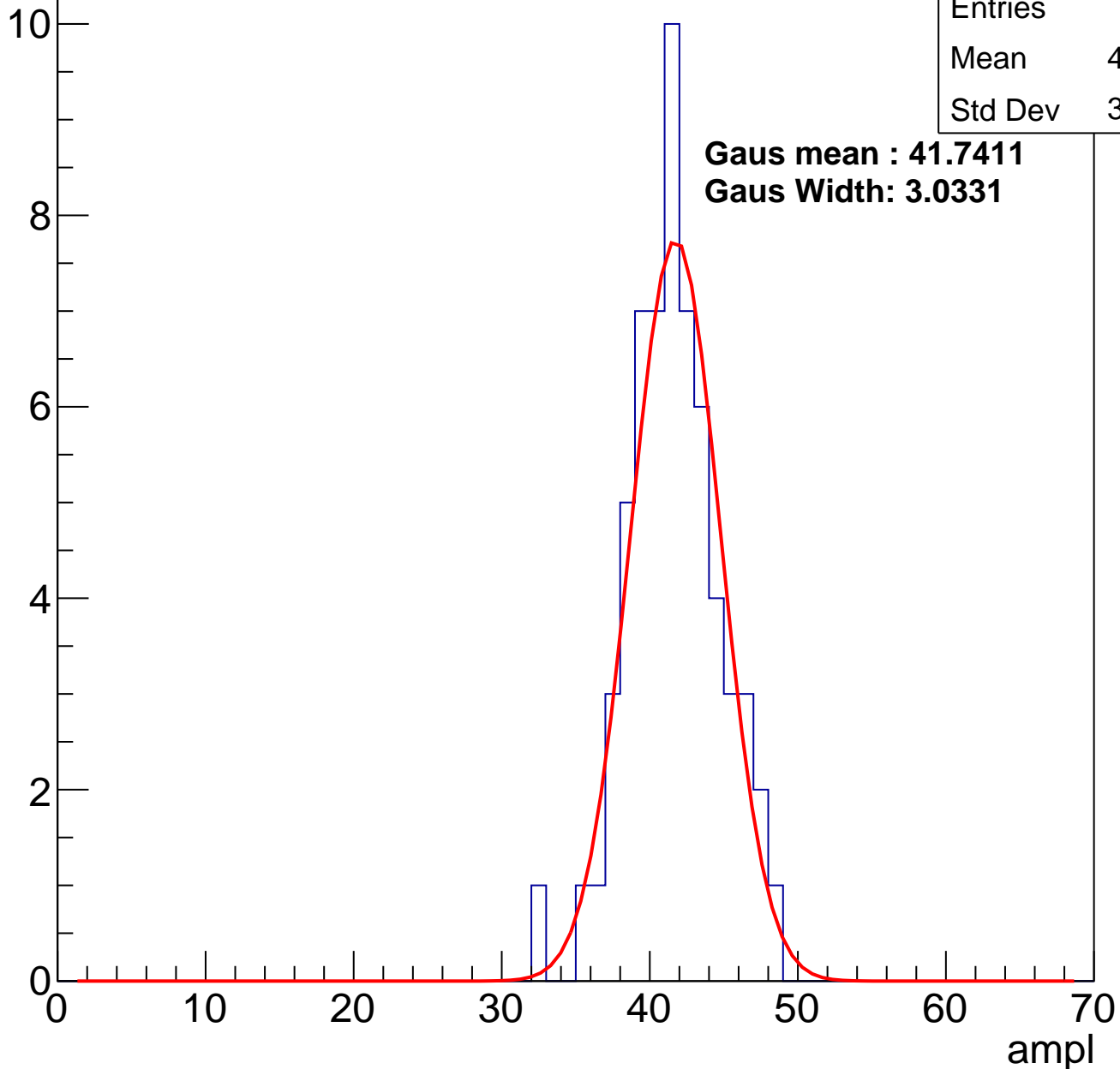
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	61
Mean	41.15
Std Dev	3.083

**Gaus mean : 41.7411**

**Gaus Width: 3.0331**

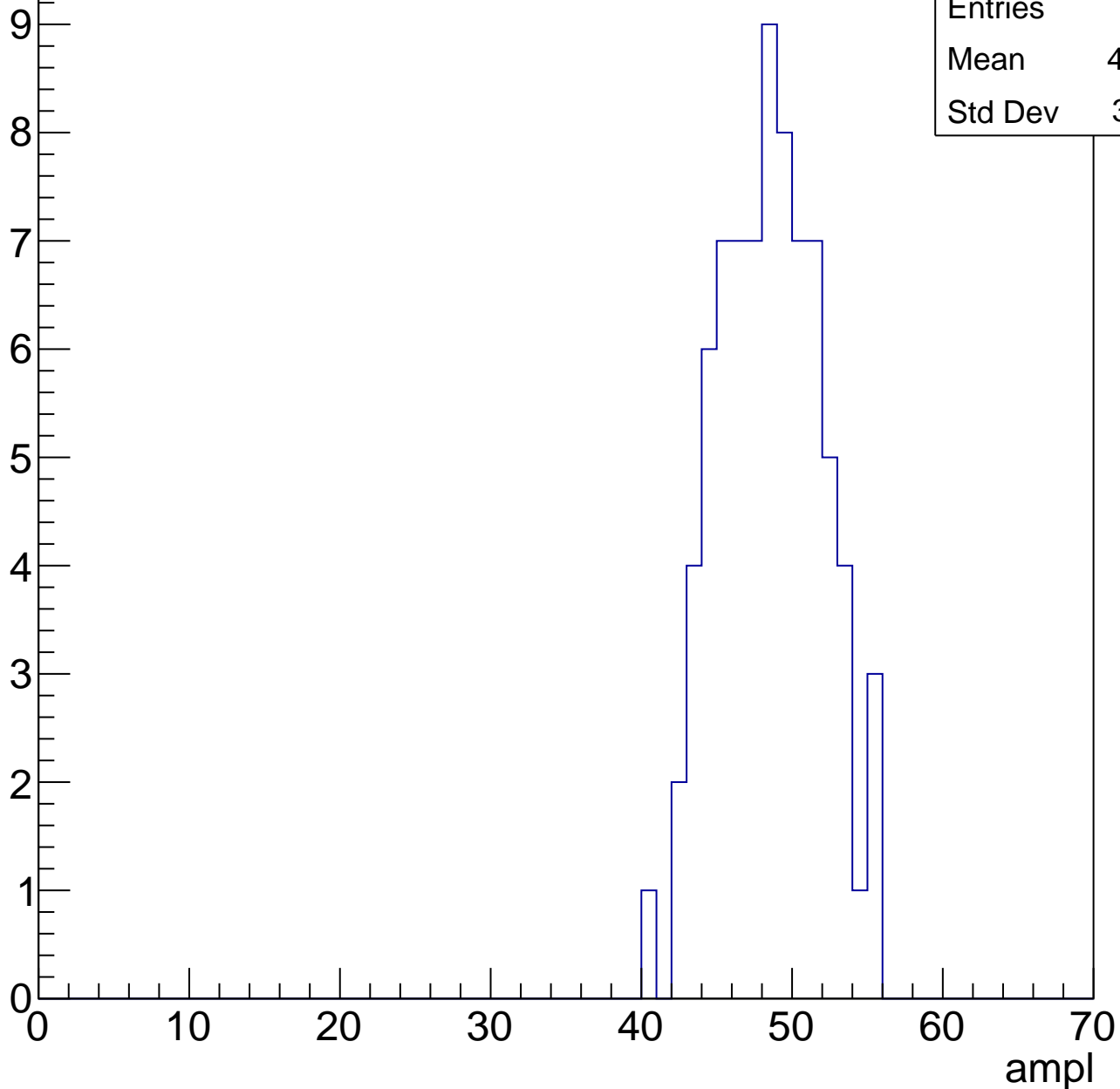
Entry



# B1L003S, U23-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



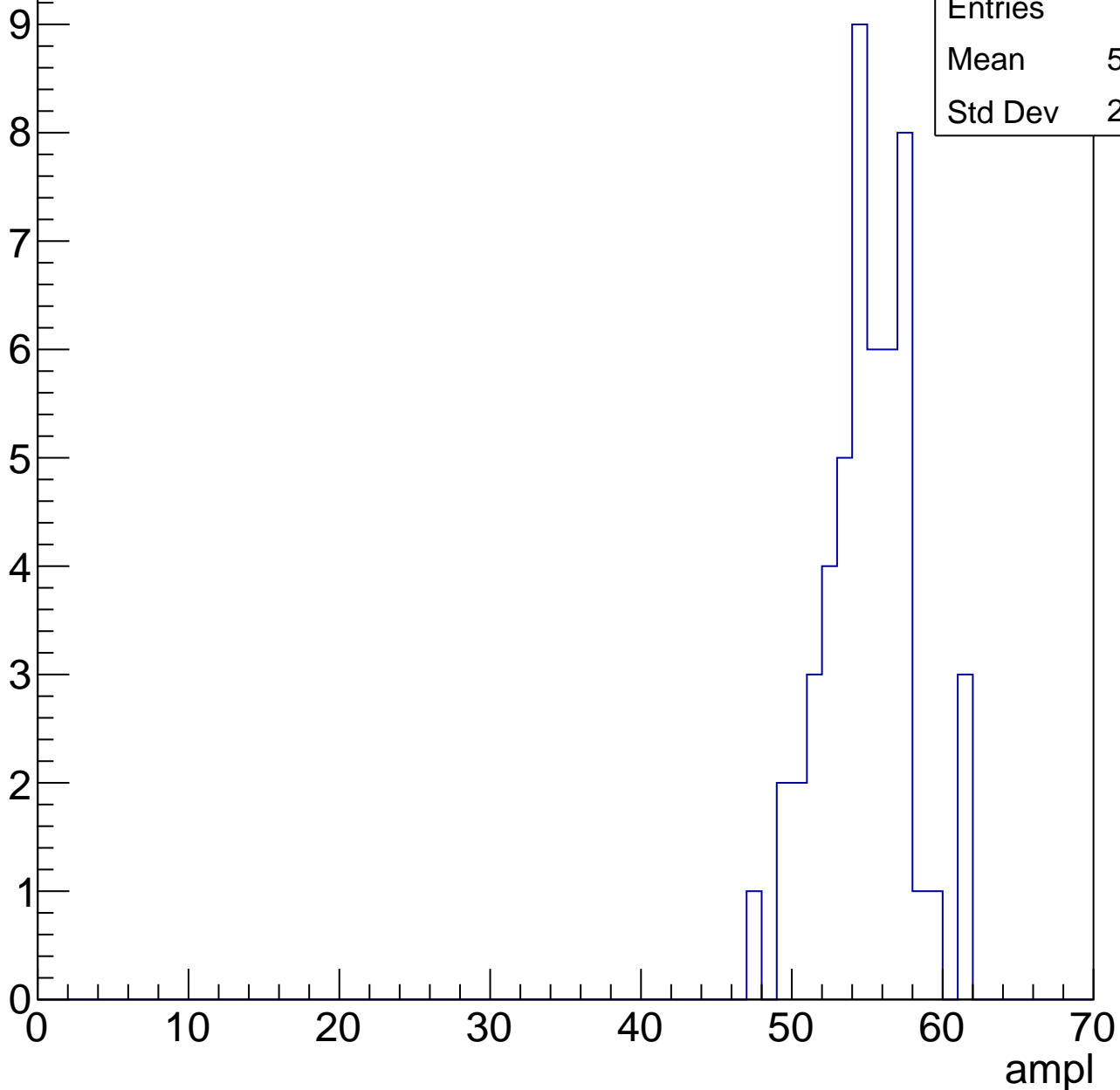
Entries	78
Mean	48.05
Std Dev	3.381

# B1L003S, U23-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

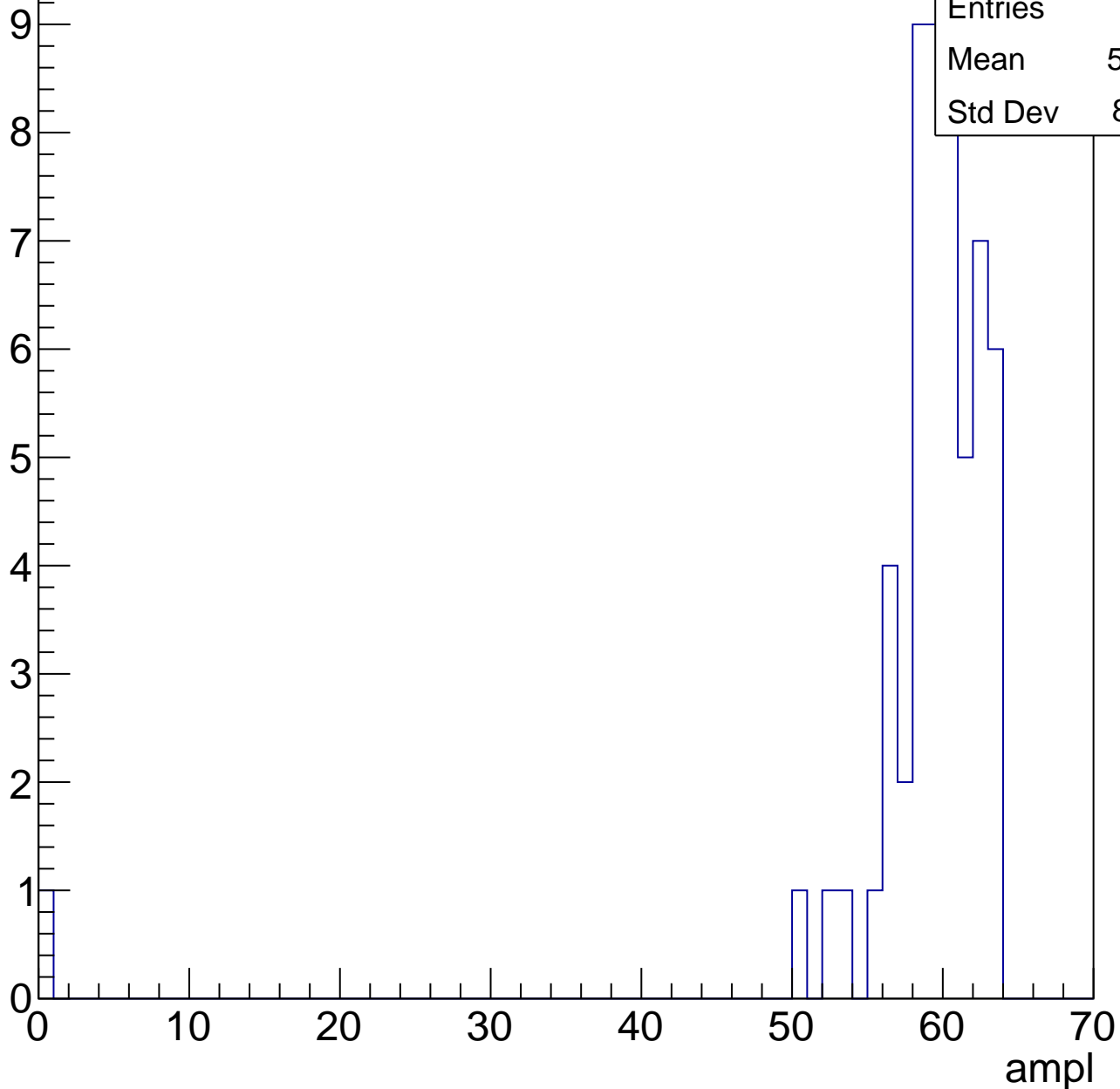
Entries	51
Mean	54.49
Std Dev	2.993



# B1L003S, U23-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

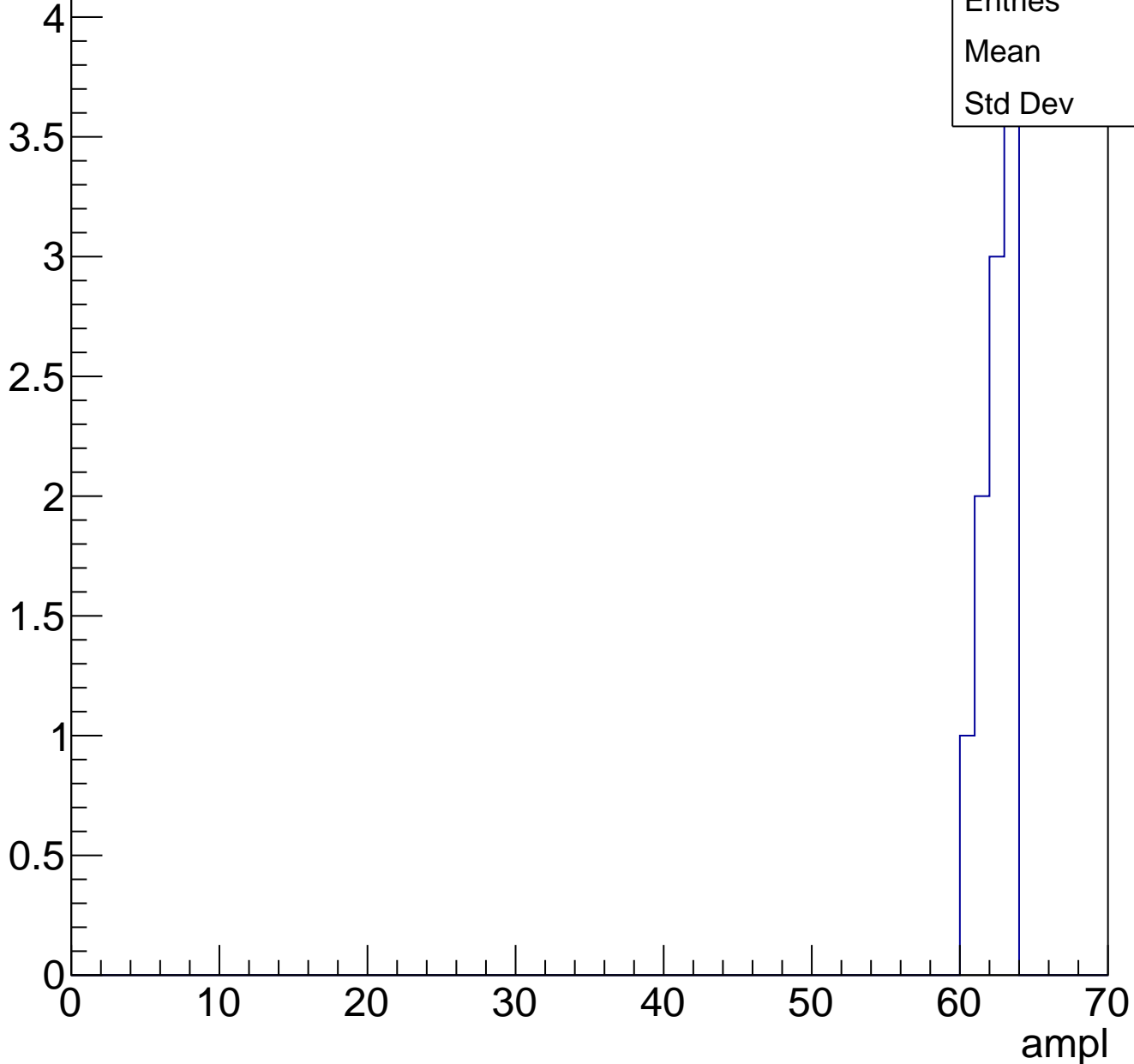


Entries	55
Mean	58.15
Std Dev	8.381

# B1L003S, U23-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

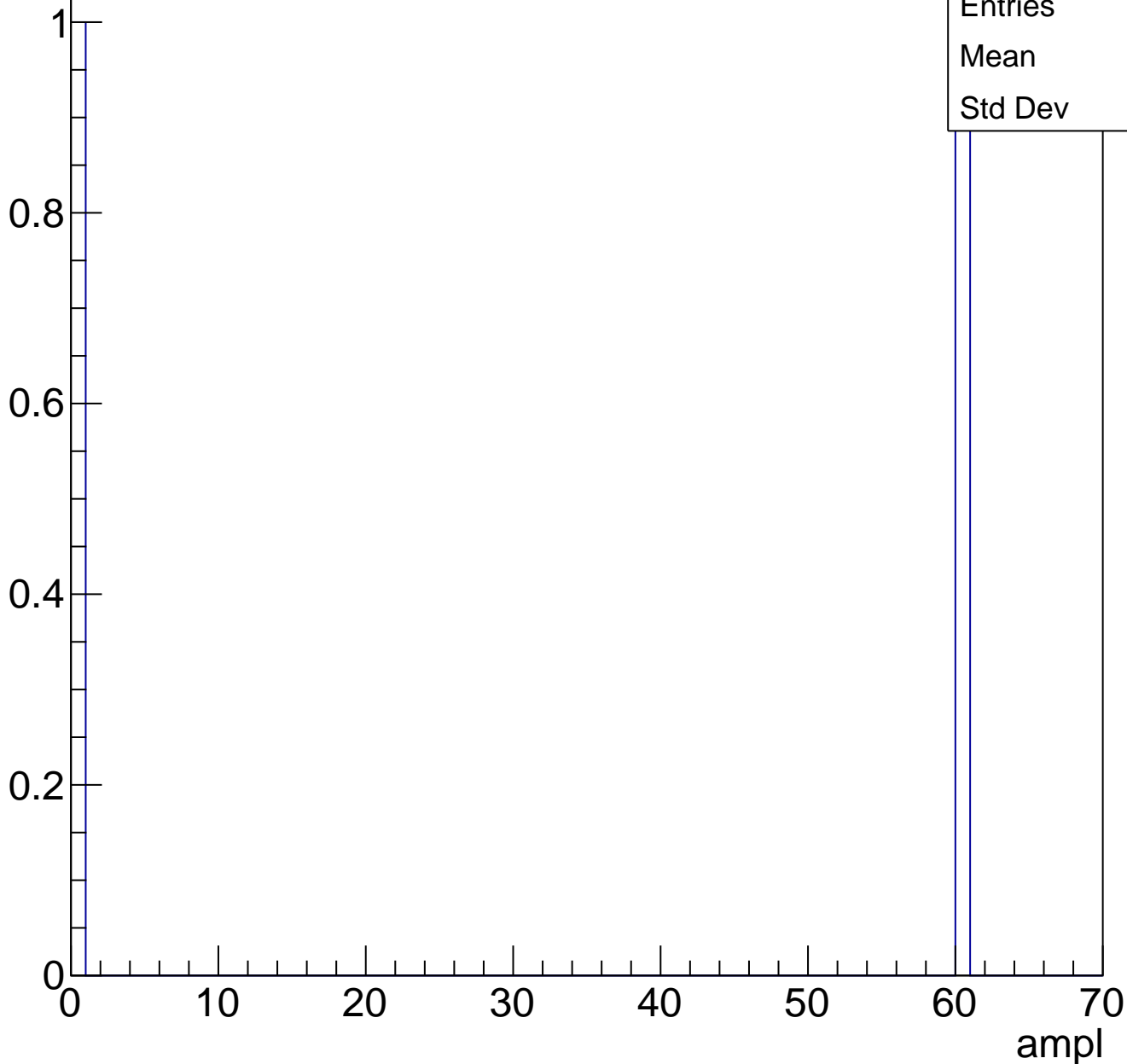




# B1L003S, U23-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch67, adc0

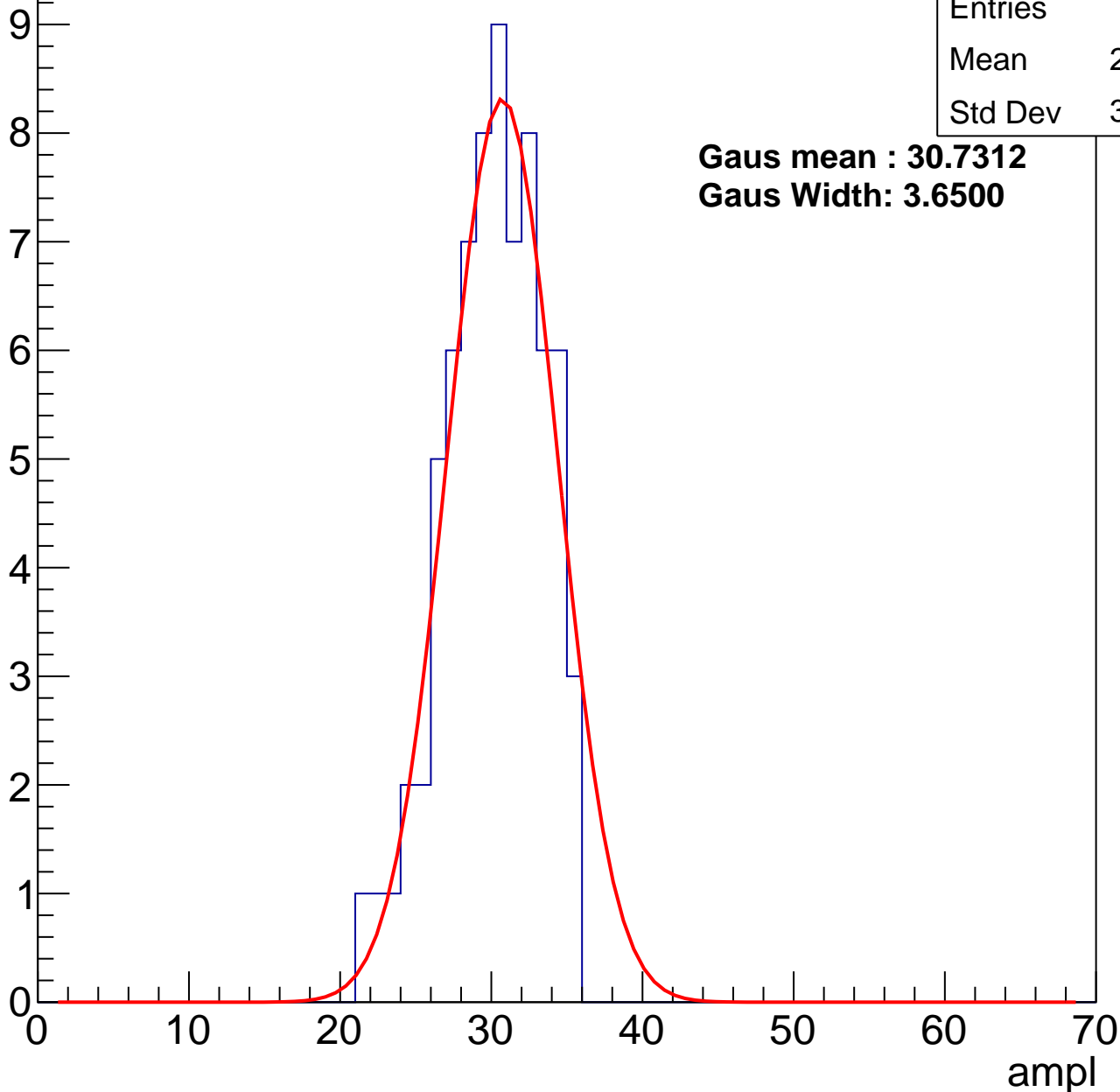
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	29.64
Std Dev	3.207

**Gaus mean : 30.7312**

**Gaus Width: 3.6500**



# B1L003S, U23-ch67, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	60
Mean	36.47
Std Dev	3.364

**Gaus mean : 37.8686**

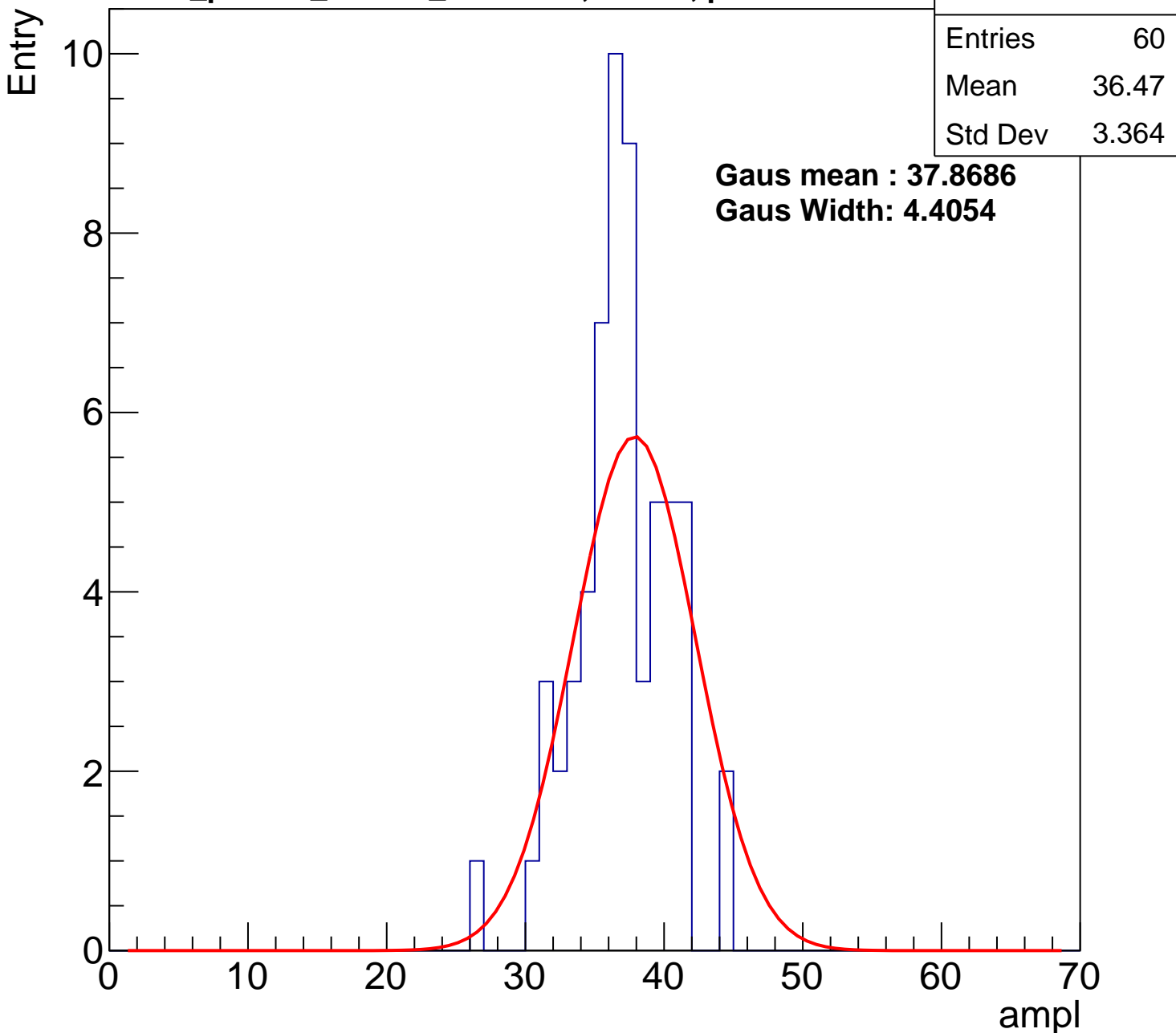
**Gaus Width: 4.4054**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch67, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	42.62
Std Dev	3.284

**Gaus mean : 43.2491**

**Gaus Width: 3.2373**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

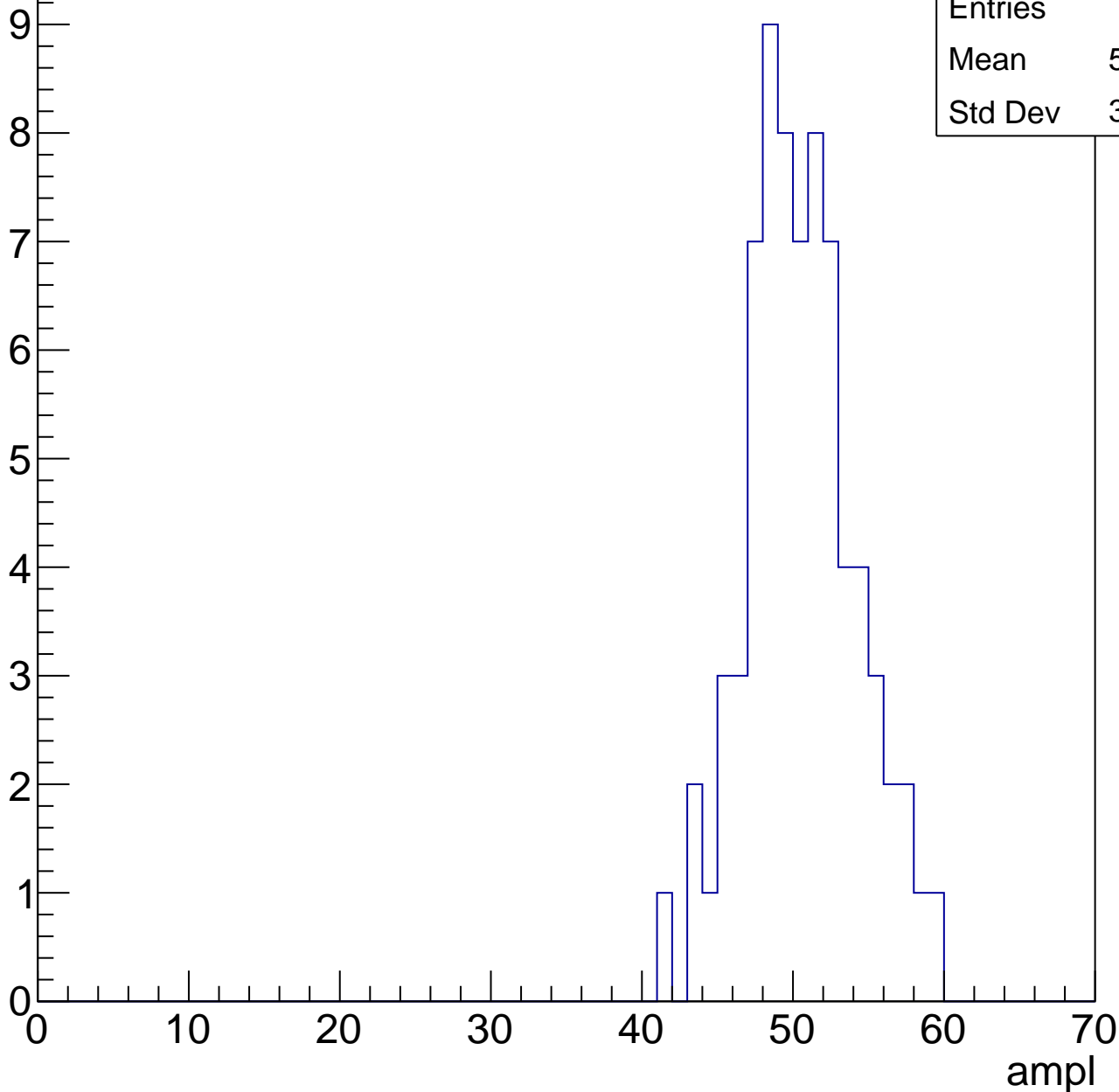
70

# B1L003S, U23-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	50.07
Std Dev	3.646

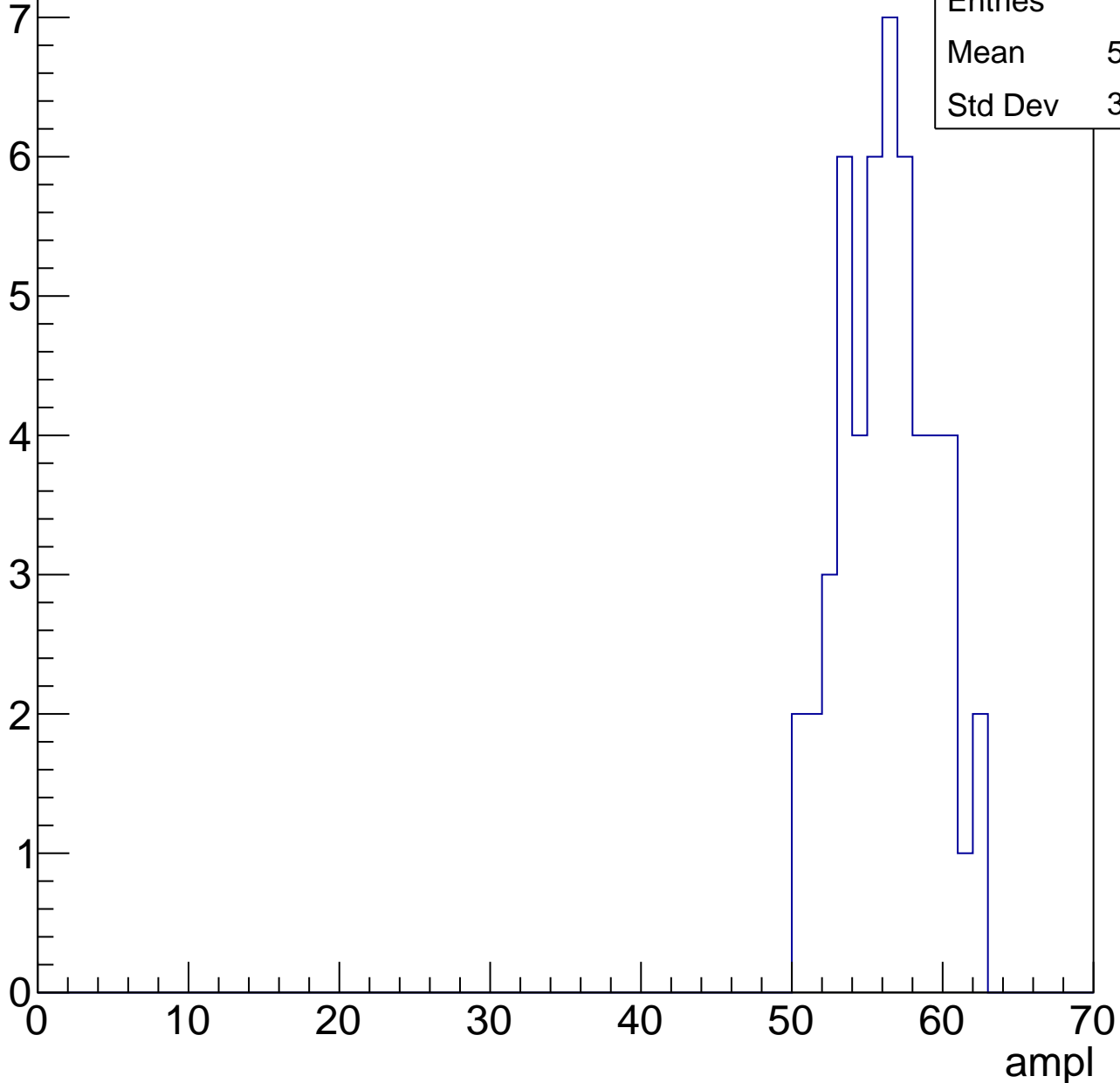


# B1L003S, U23-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	55.86
Std Dev	3.016

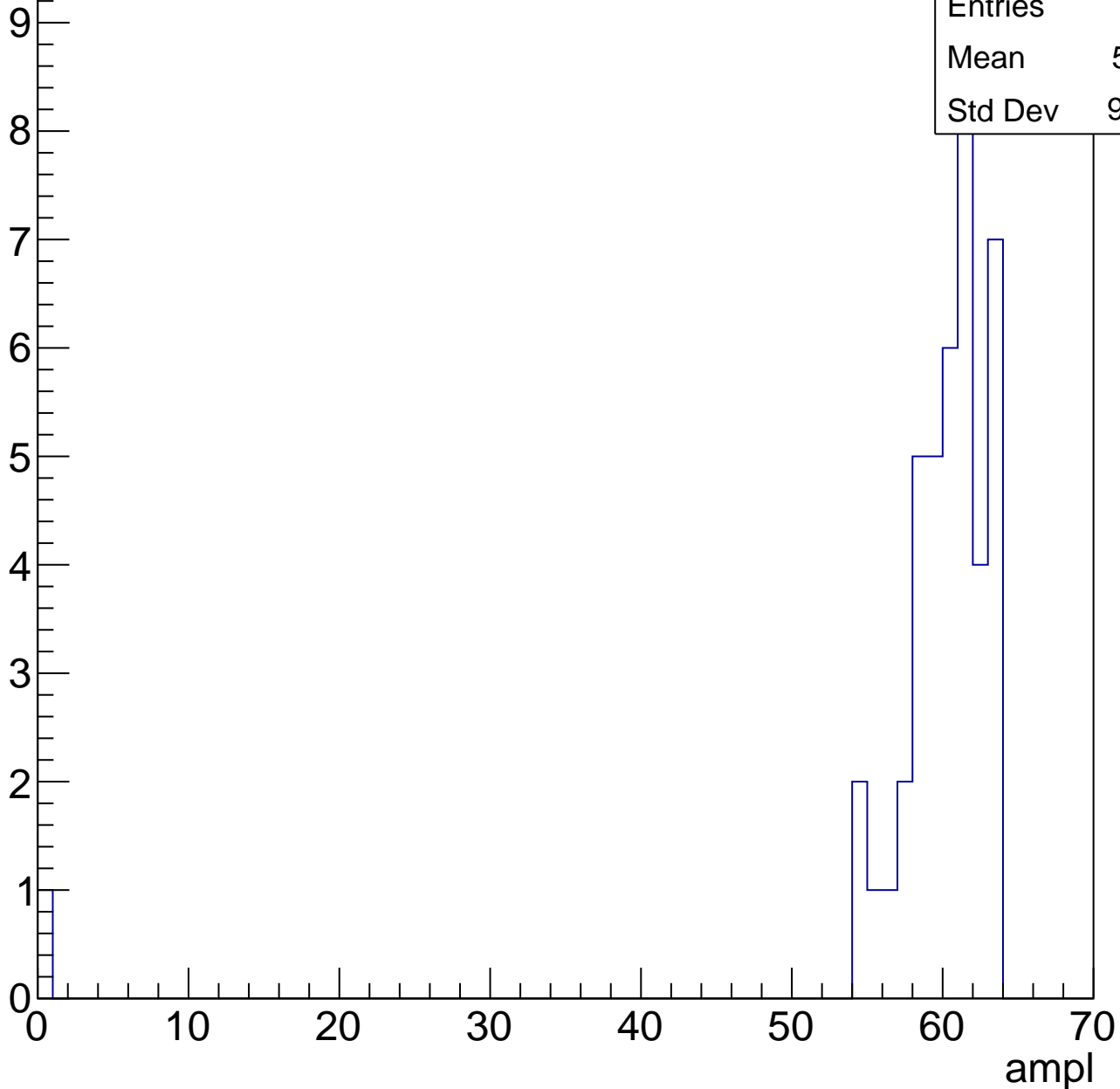


# B1L003S, U23-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

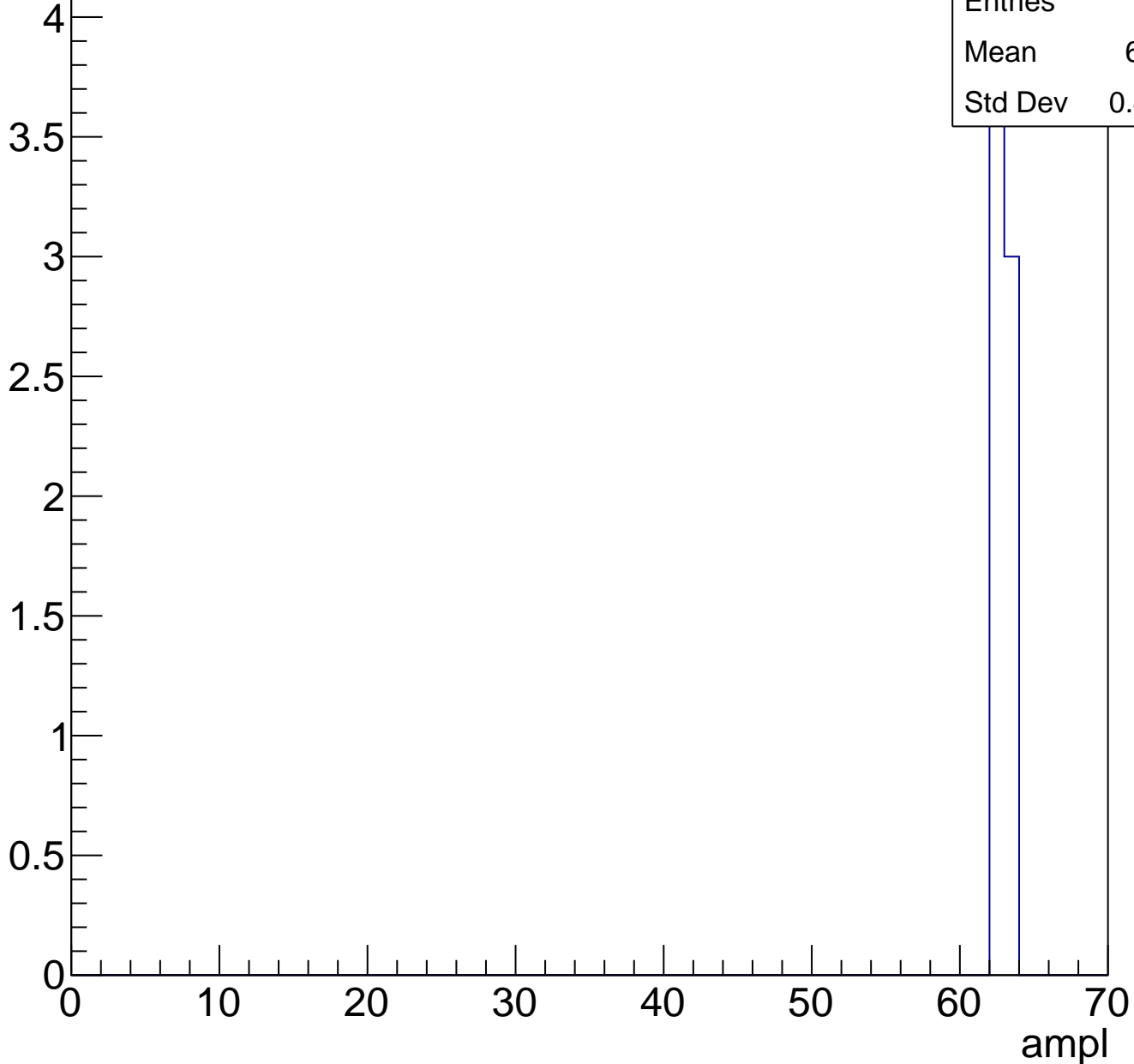
Entries	43
Mean	58.51
Std Dev	9.337



# B1L003S, U23-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch68, adc0

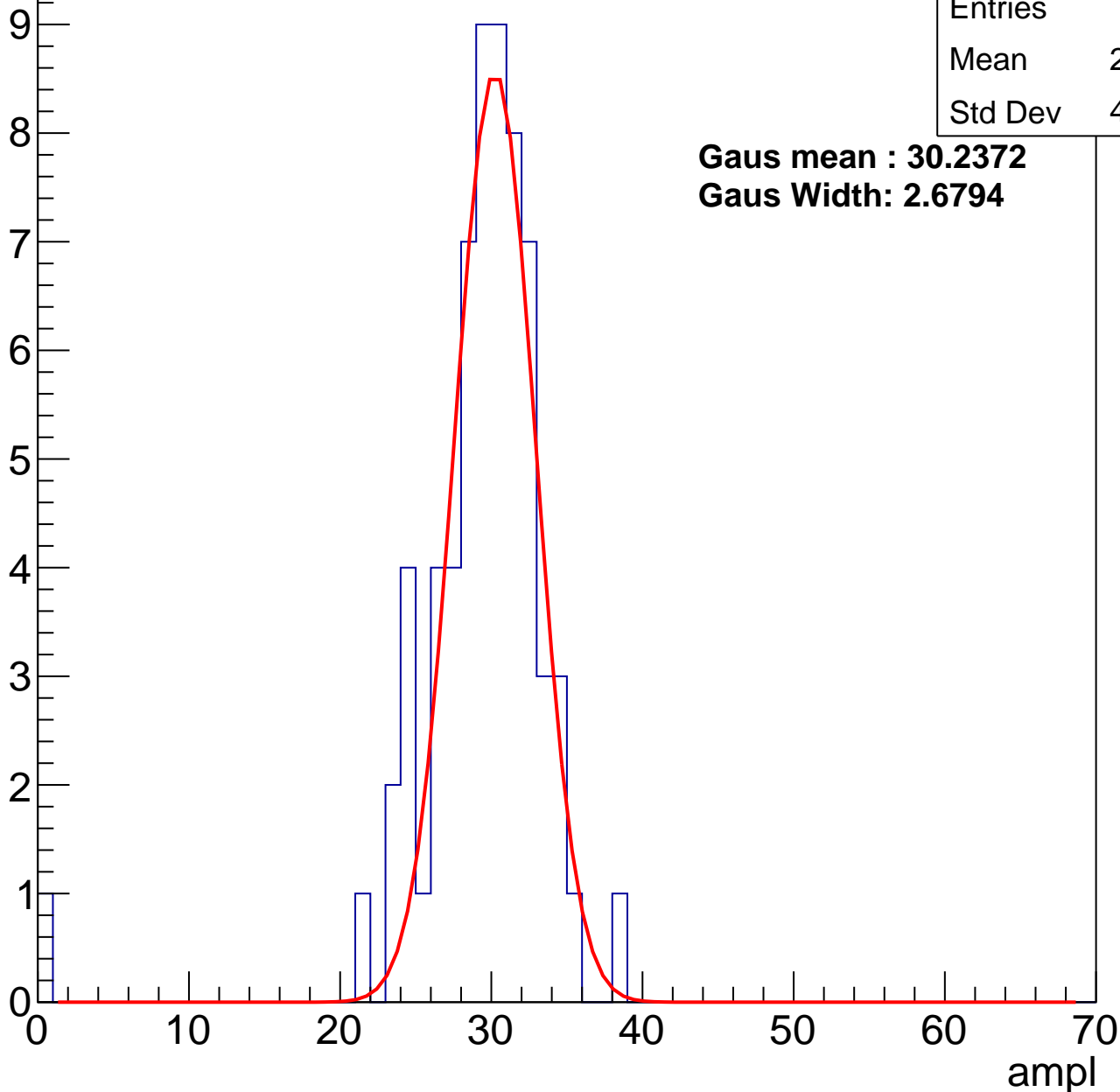
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.82
Std Dev	4.794

**Gaus mean : 30.2372**

**Gaus Width: 2.6794**



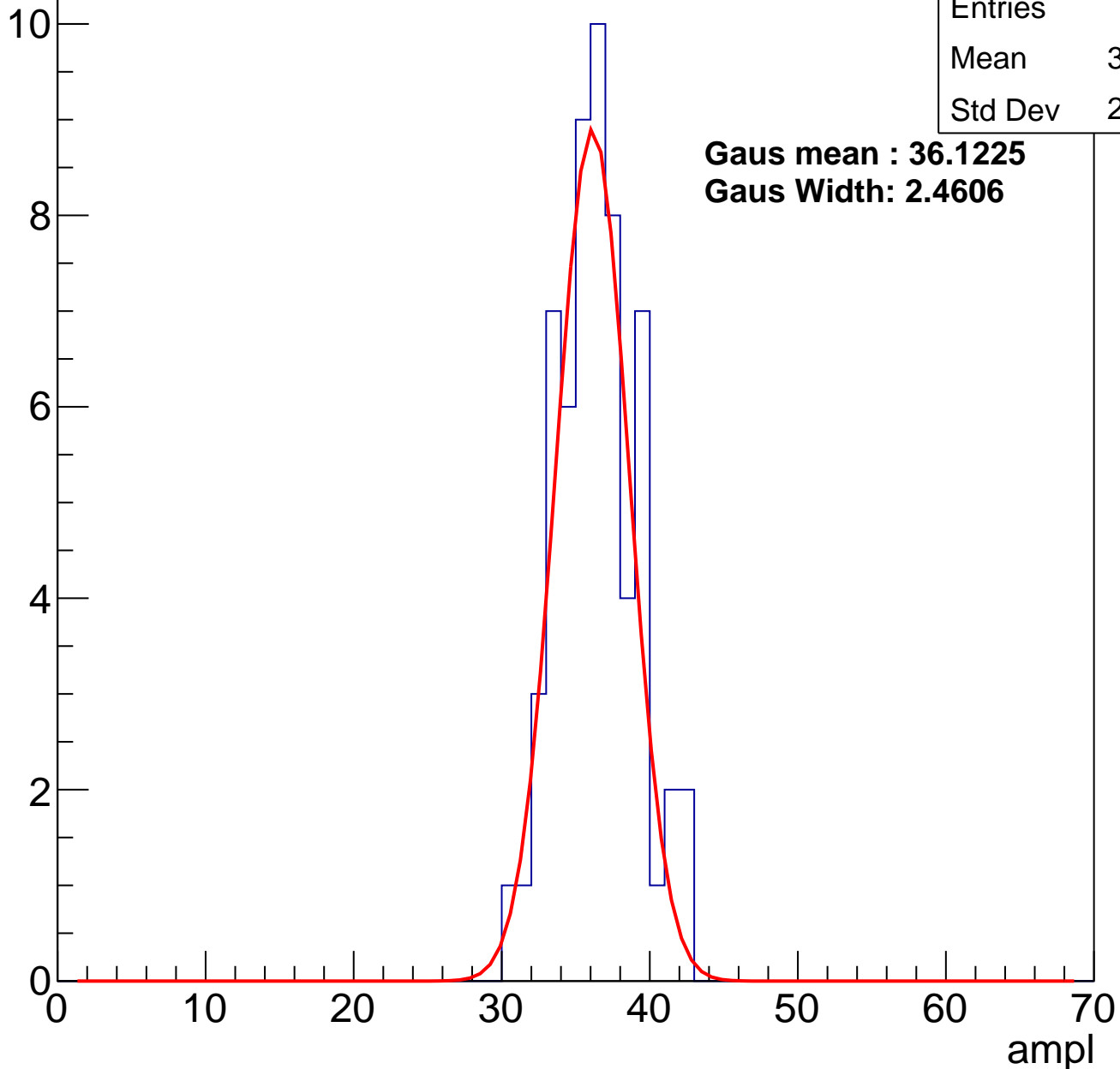
# B1L003S, U23-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	61
Mean	35.97
Std Dev	2.655

**Gaus mean : 36.1225**  
**Gaus Width: 2.4606**

Entry



# B1L003S, U23-ch68, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	42.46
Std Dev	3.211

**Gaus mean : 42.8298**

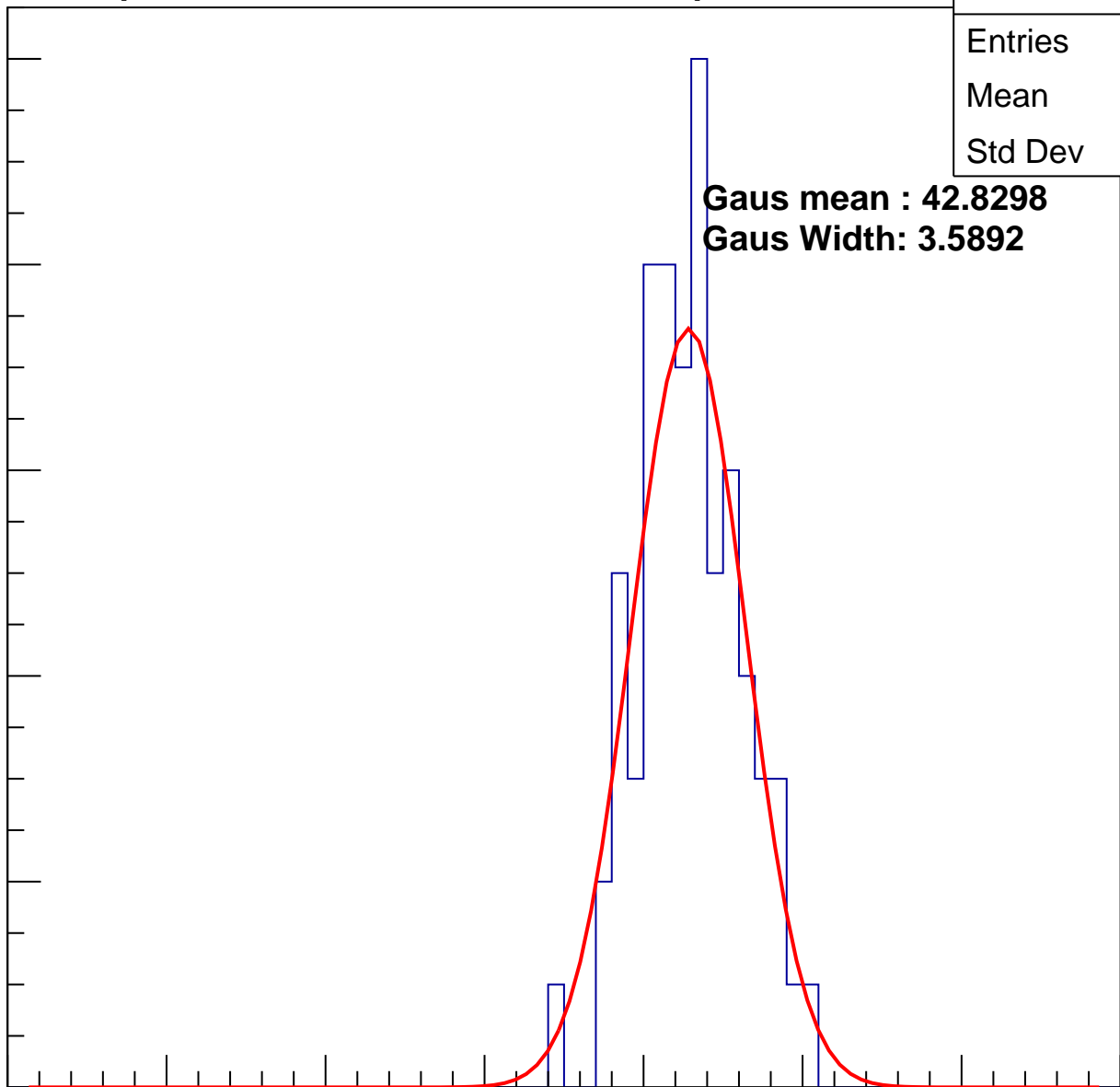
**Gaus Width: 3.5892**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

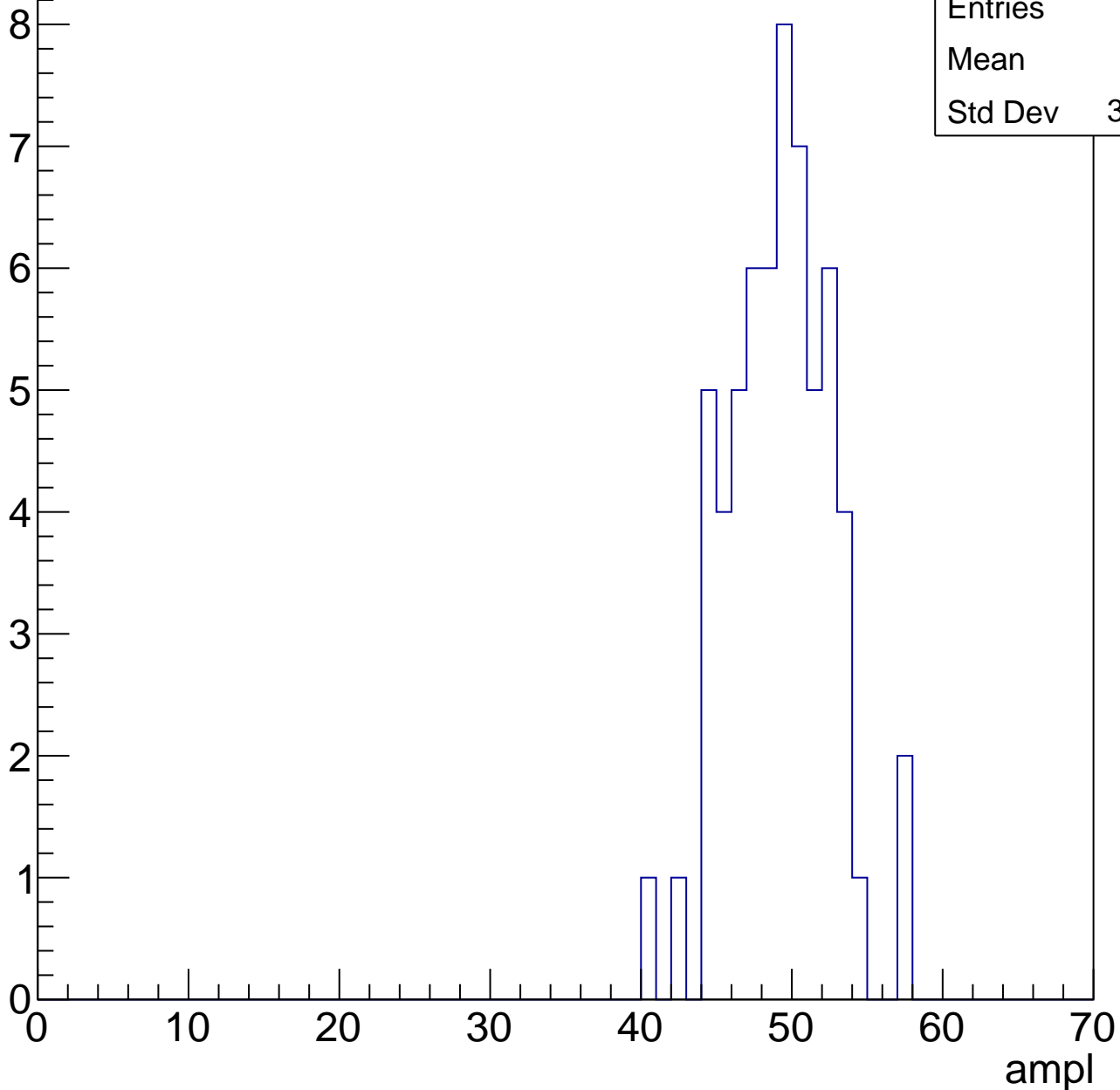


# B1L003S, U23-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

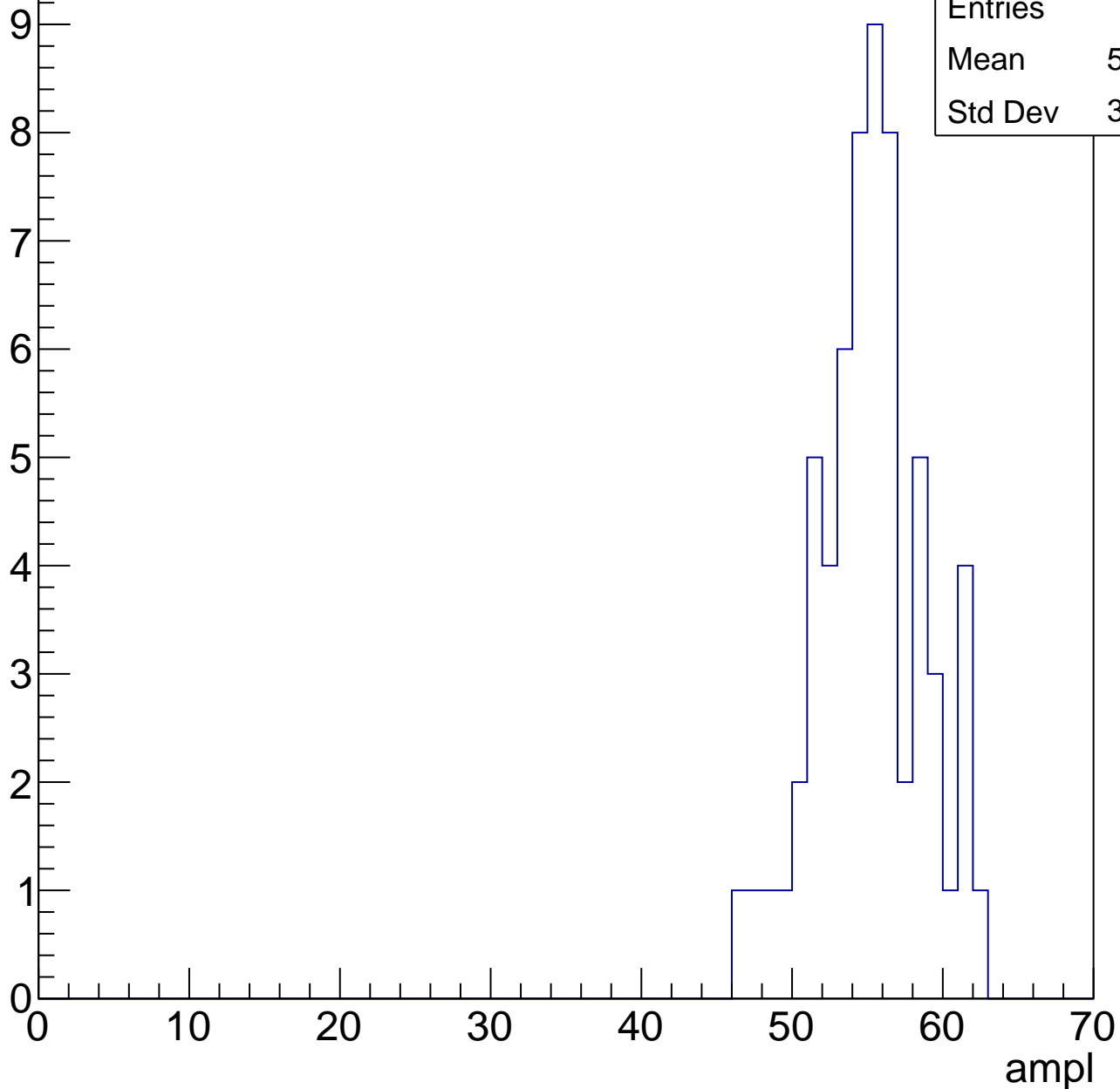
Entries	61
Mean	48.7
Std Dev	3.355



# B1L003S, U23-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch68, adc5

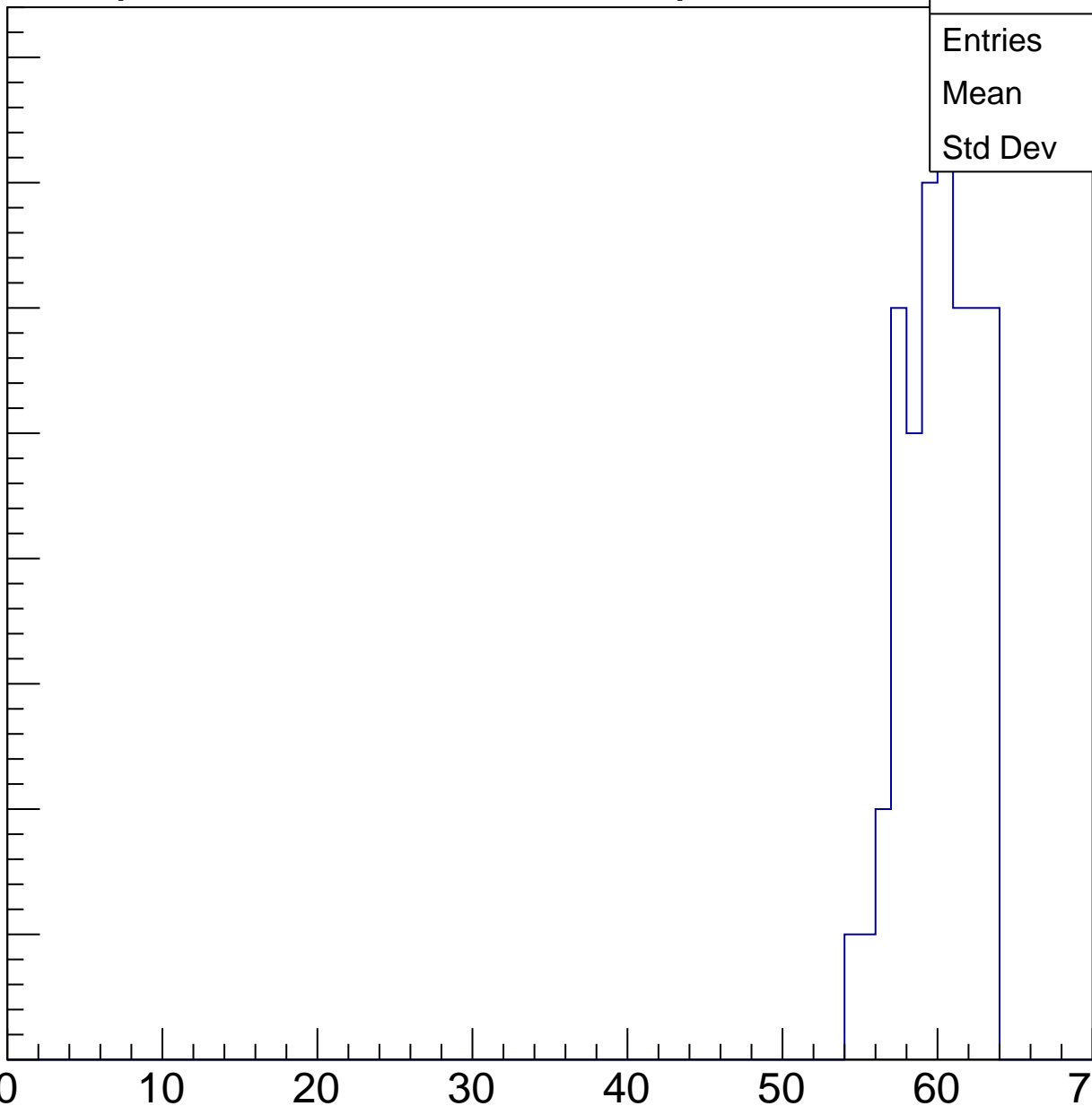
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	59.62
Std Dev	2.288

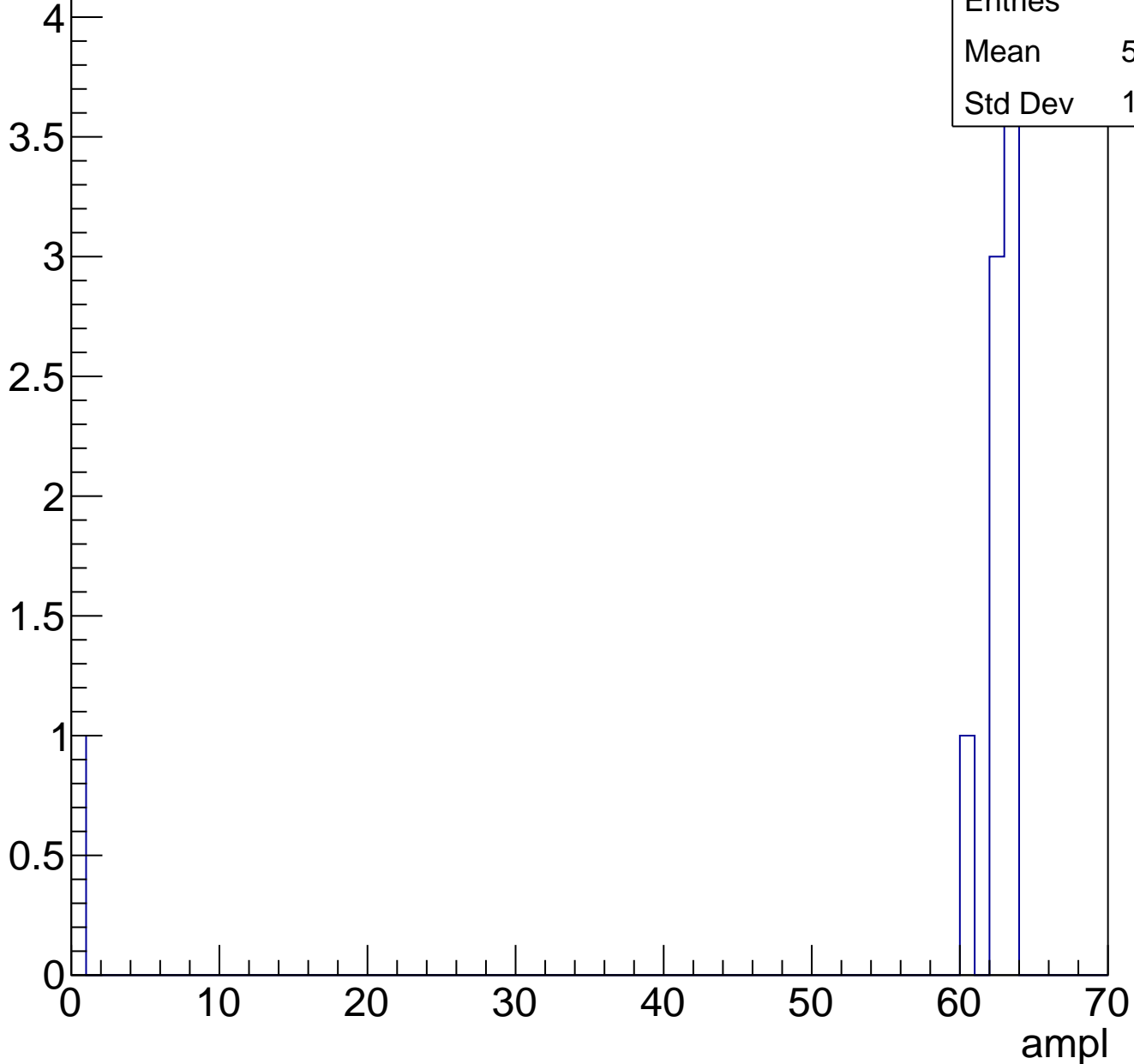
ampl



# B1L003S, U23-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch69, adc0

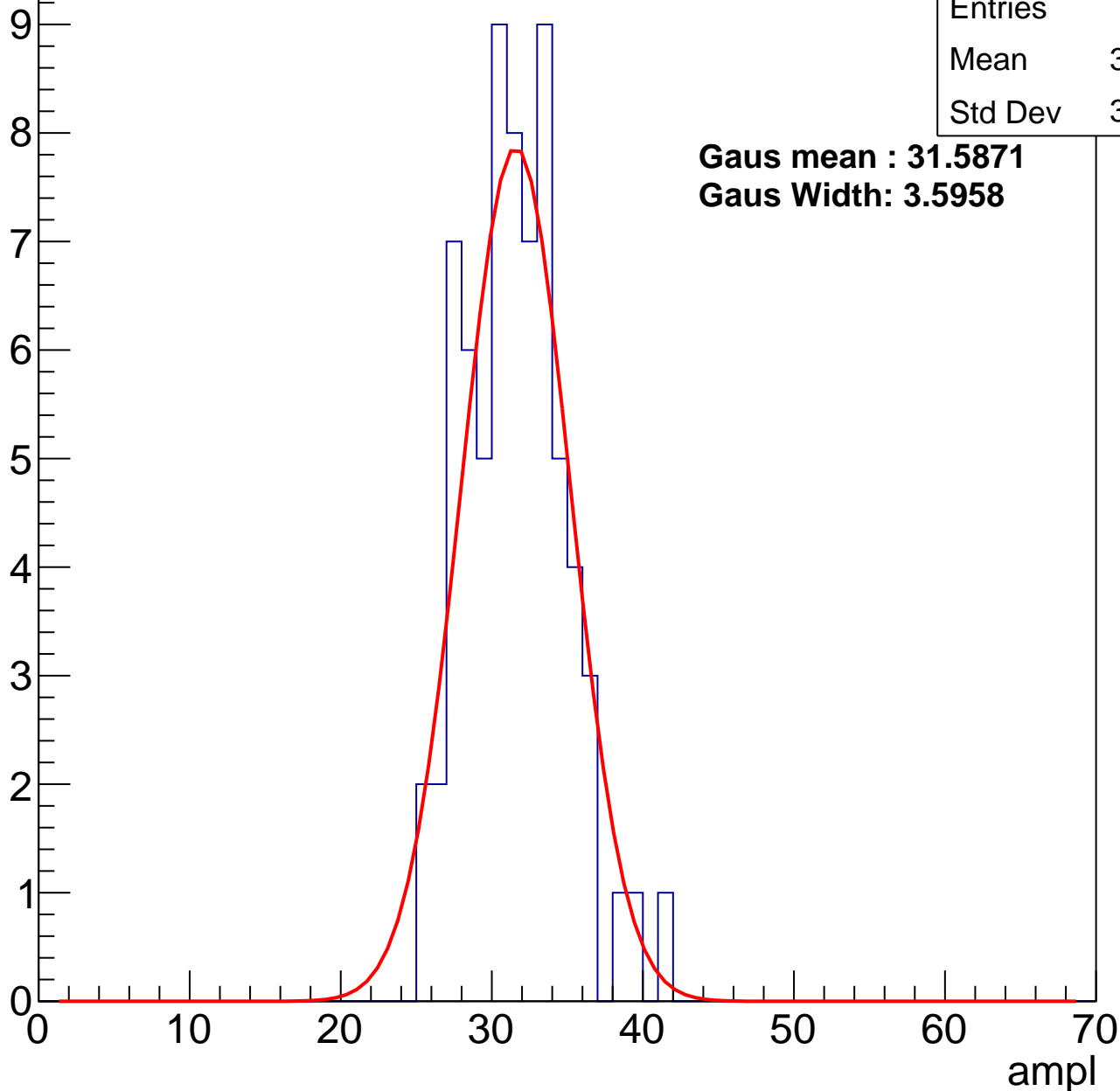
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	31.13
Std Dev	3.286

**Gaus mean : 31.5871**

**Gaus Width: 3.5958**



# B1L003S, U23-ch69, adc1

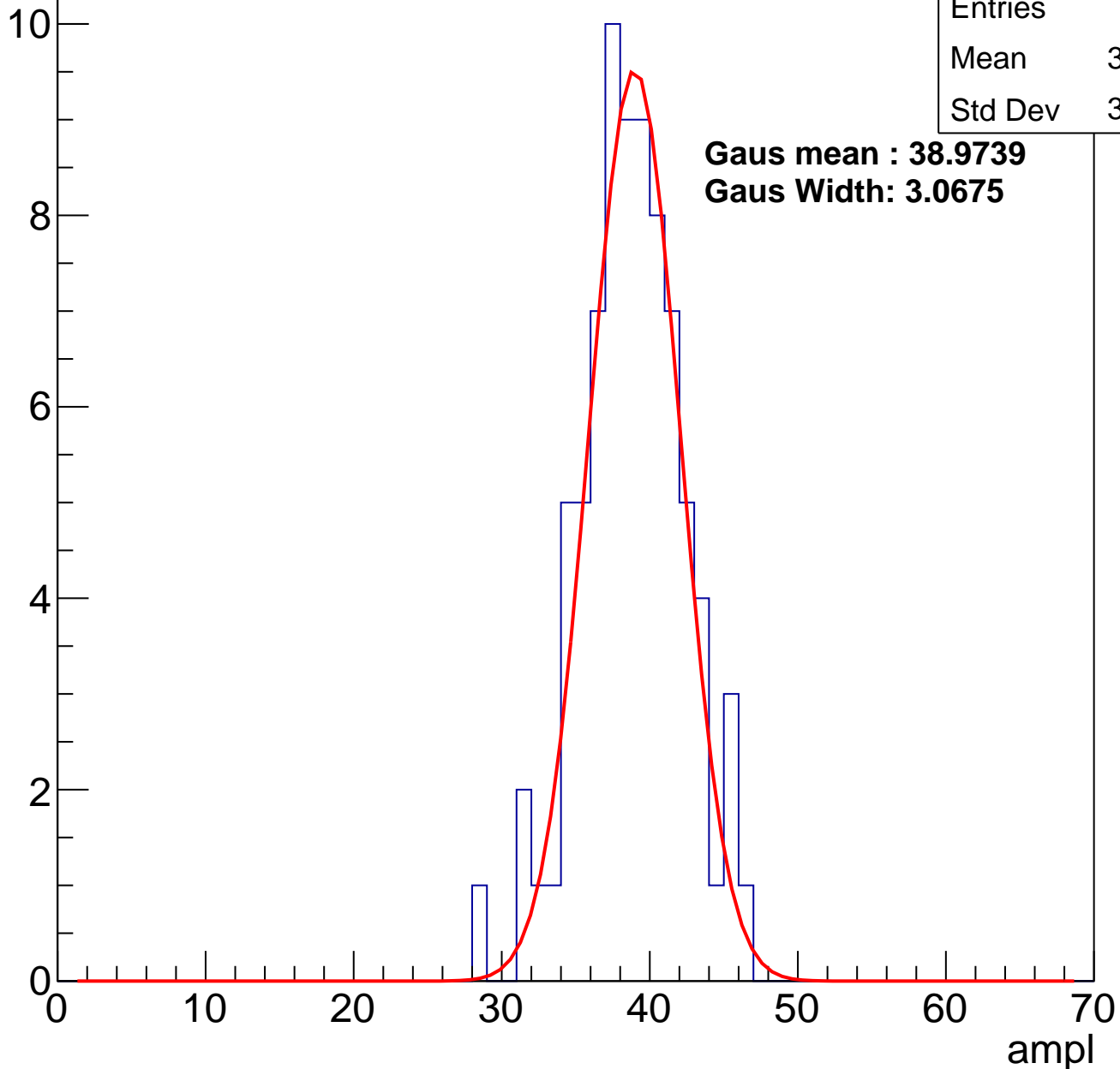
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	79
Mean	38.34
Std Dev	3.456

**Gaus mean : 38.9739**

**Gaus Width: 3.0675**

Entry



# B1L003S, U23-ch69, adc2

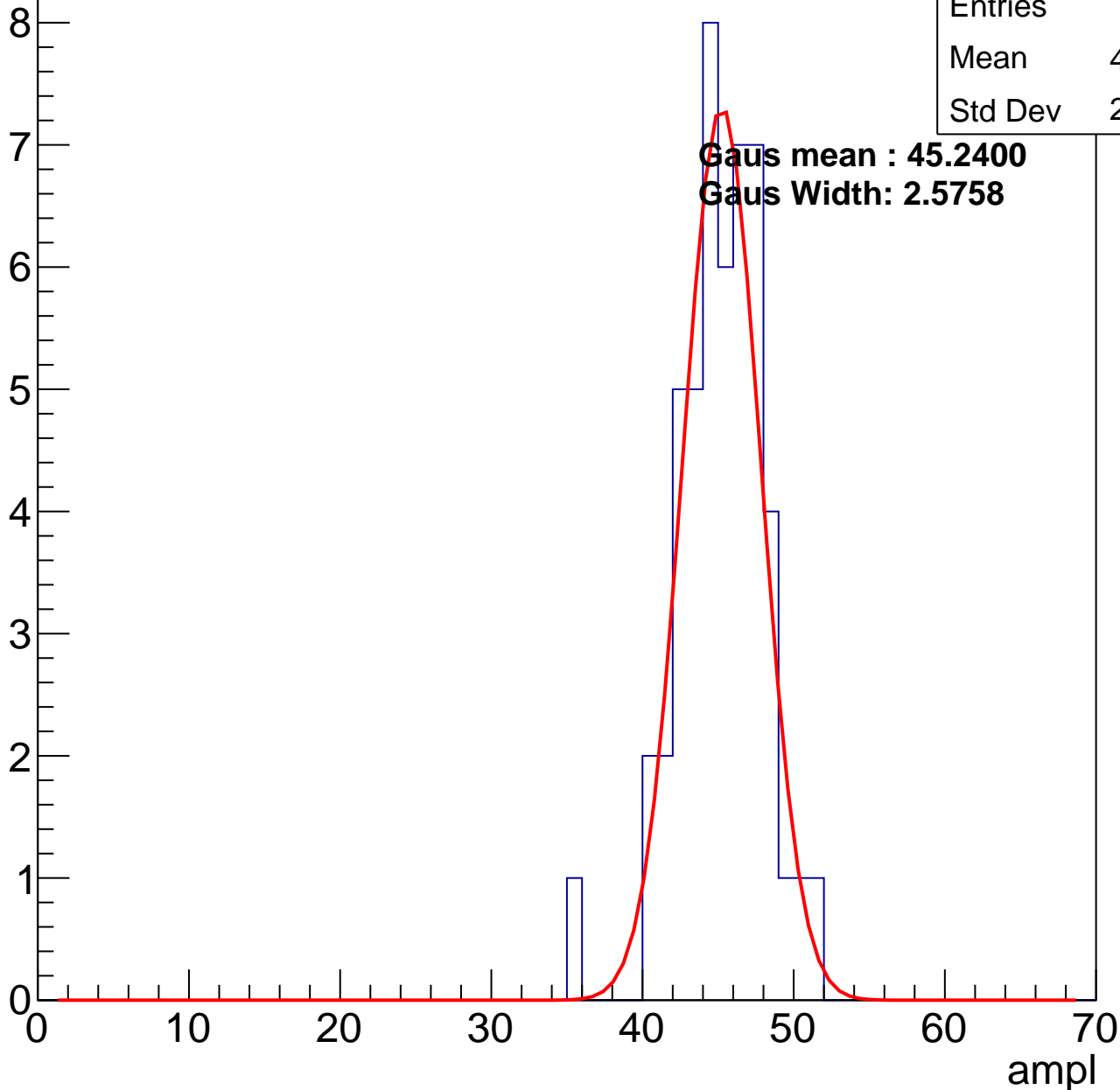
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	44.74
Std Dev	2.827

**Gaus mean : 45.2400**

**Gaus Width: 2.5758**

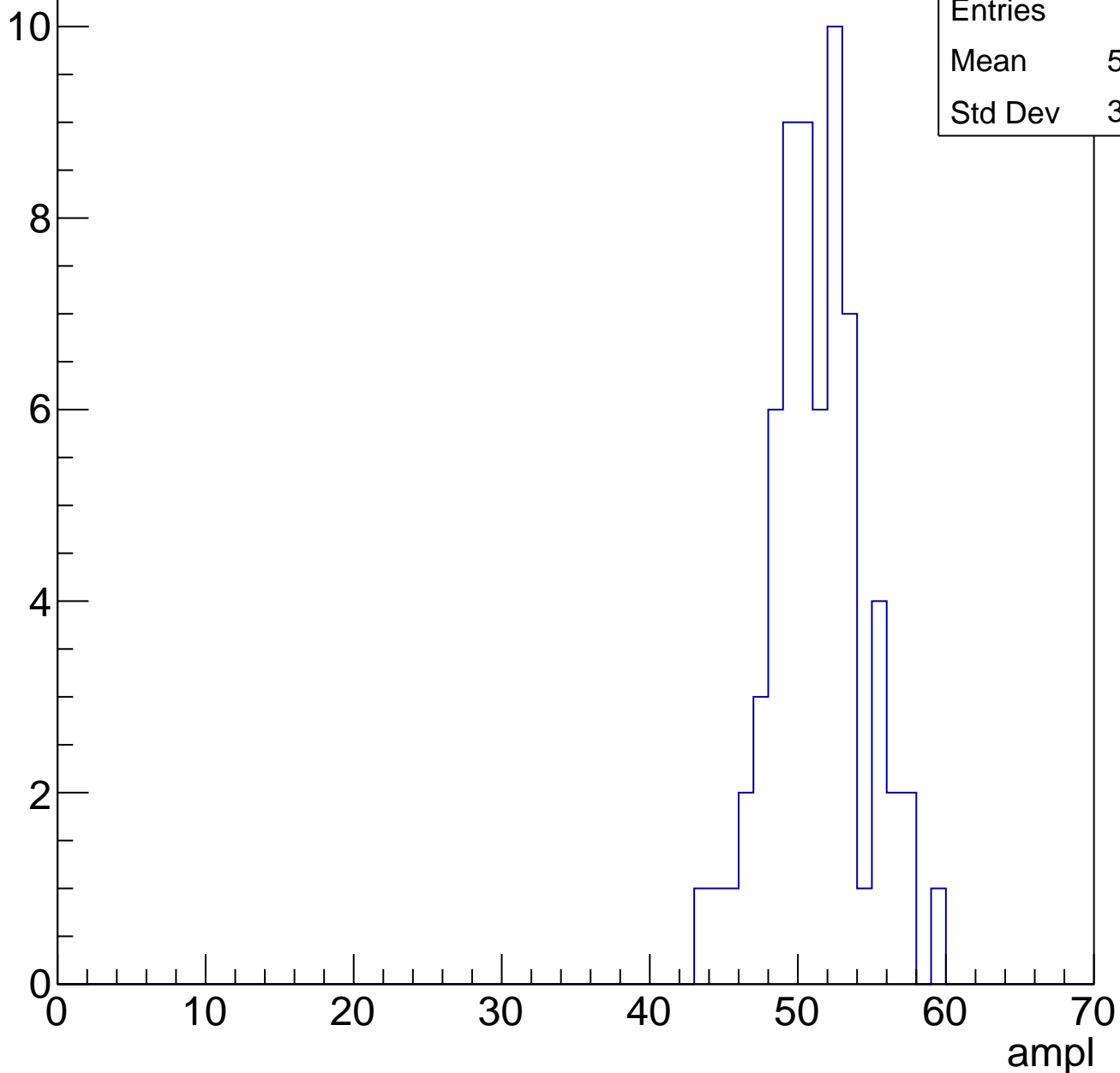


# B1L003S, U23-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	50.77
Std Dev	3.137

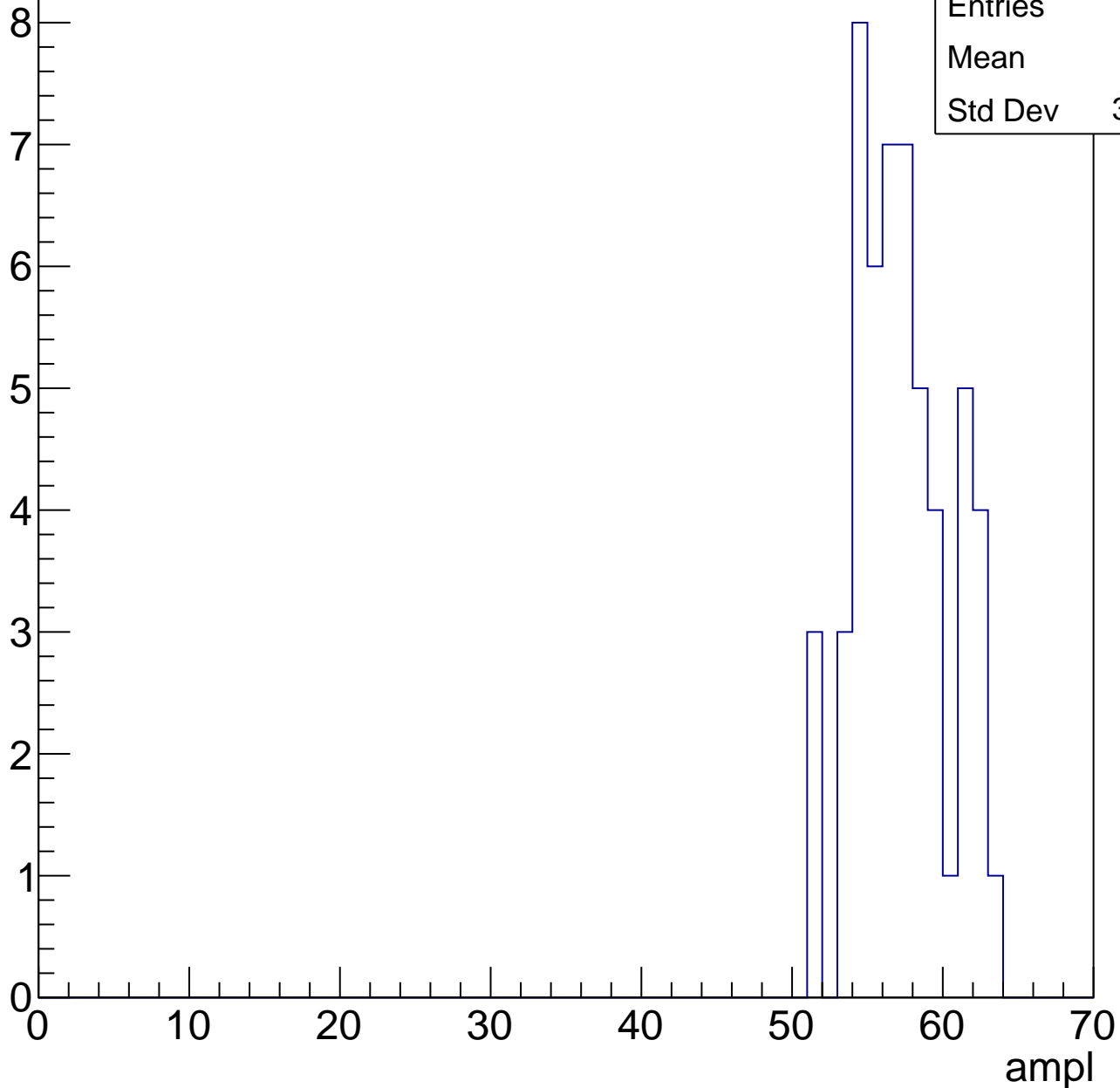


# B1L003S, U23-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	56.8
Std Dev	3.051

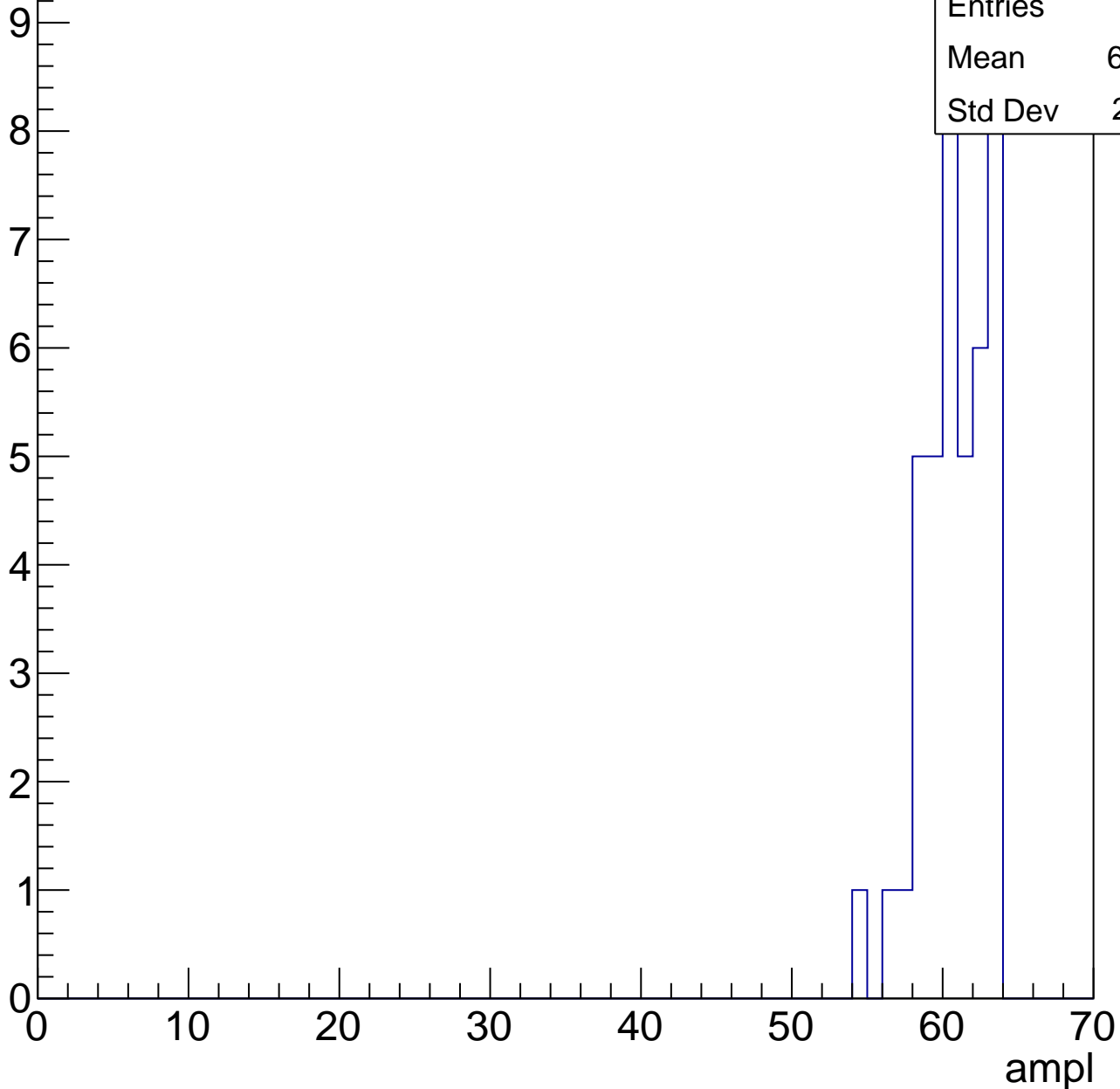


# B1L003S, U23-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

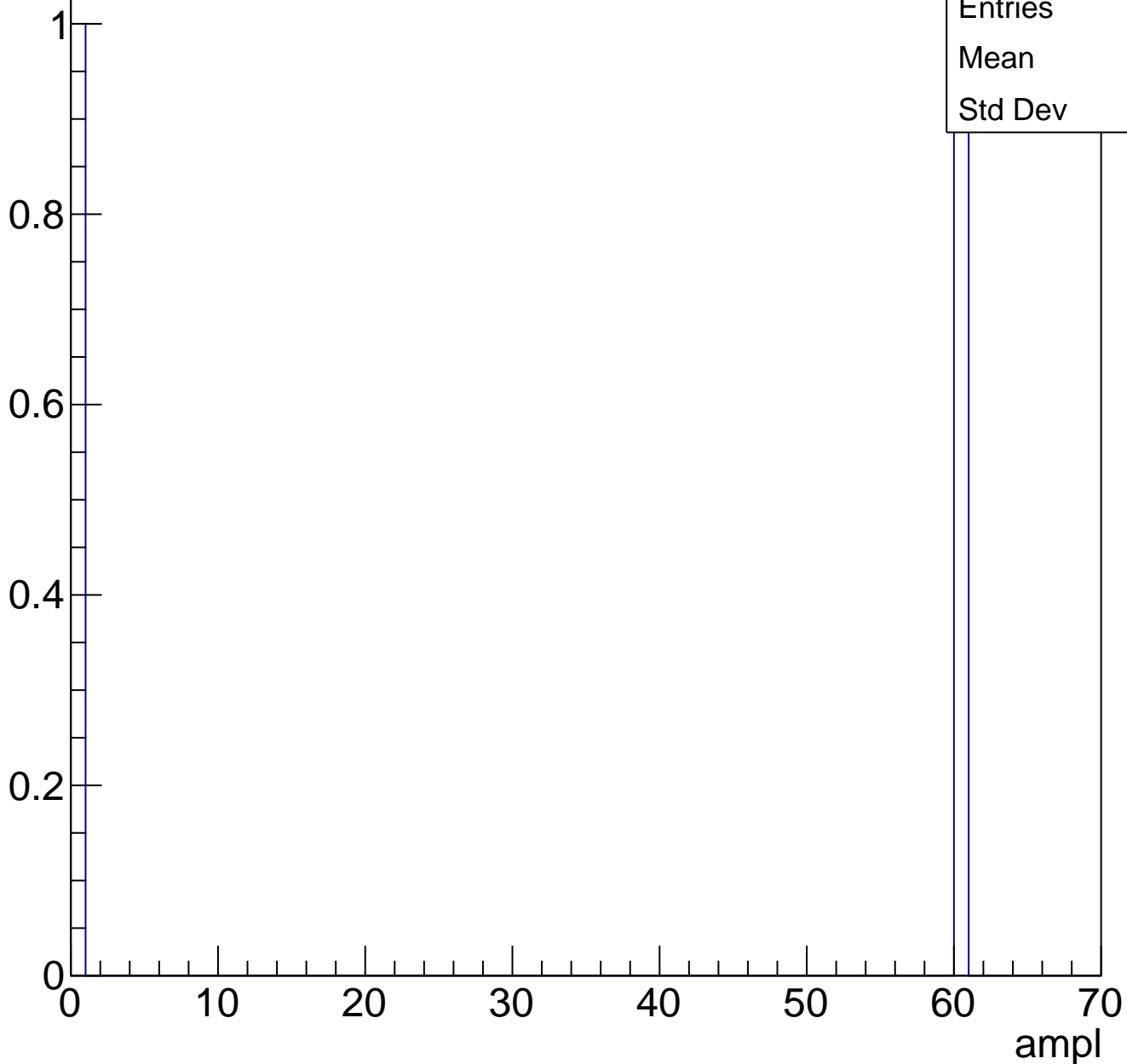
Entries	41
Mean	60.39
Std Dev	2.151



# B1L003S, U23-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

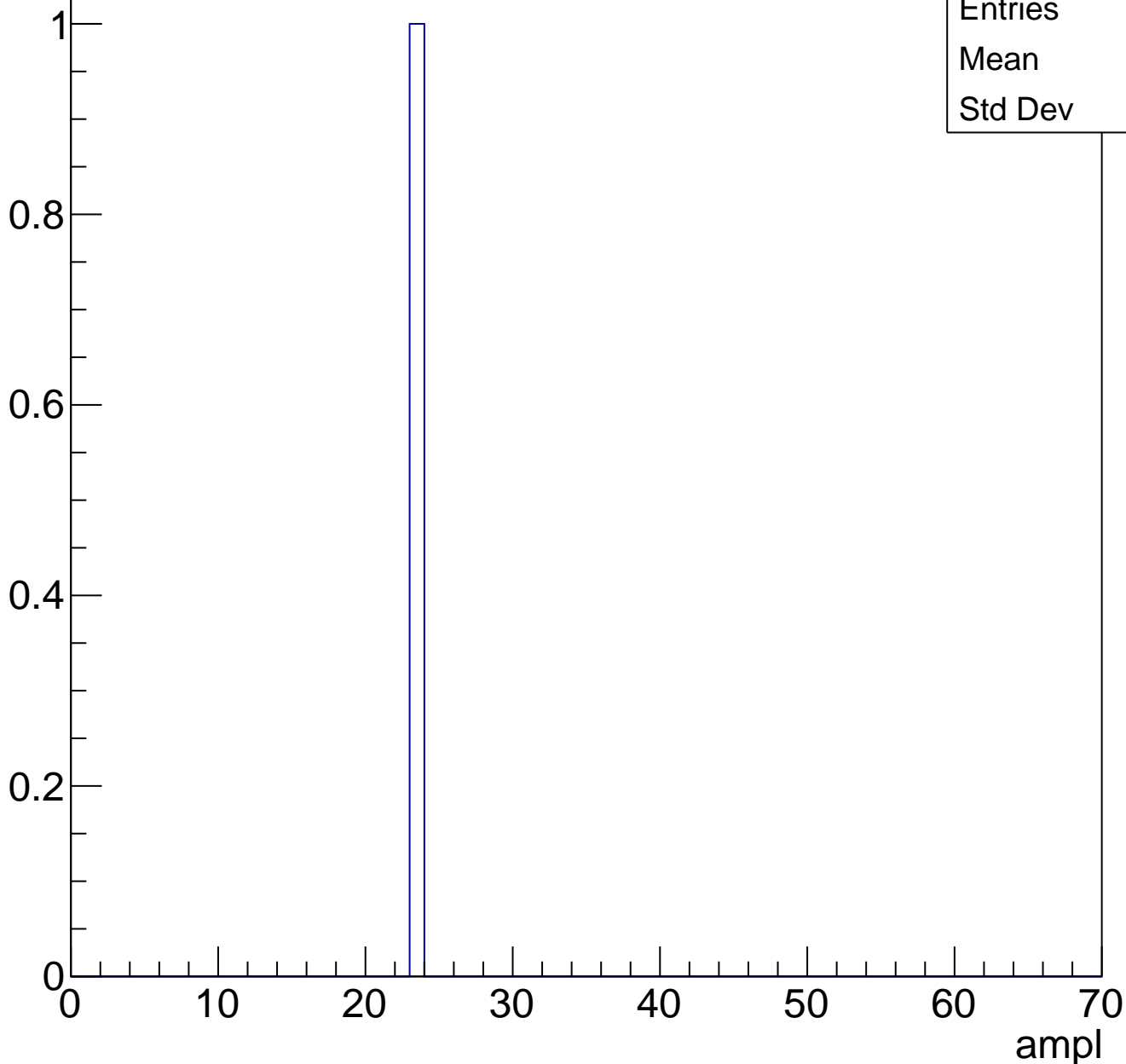




# B1L003S, U23-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	23
Std Dev	0

# B1L003S, U23-ch70, adc0

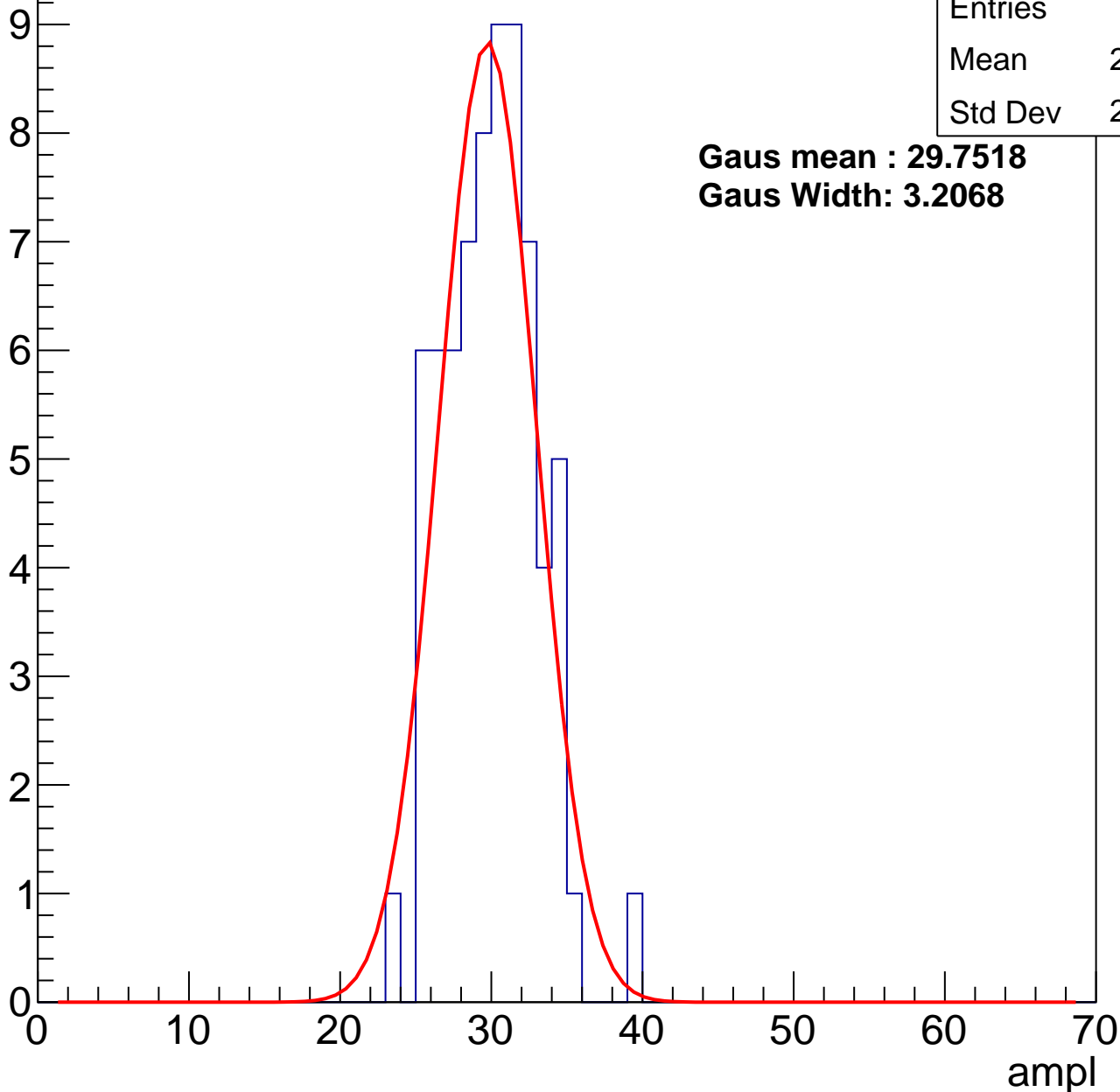
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.54
Std Dev	2.999

**Gaus mean : 29.7518**

**Gaus Width: 3.2068**



# B1L003S, U23-ch70, adc1

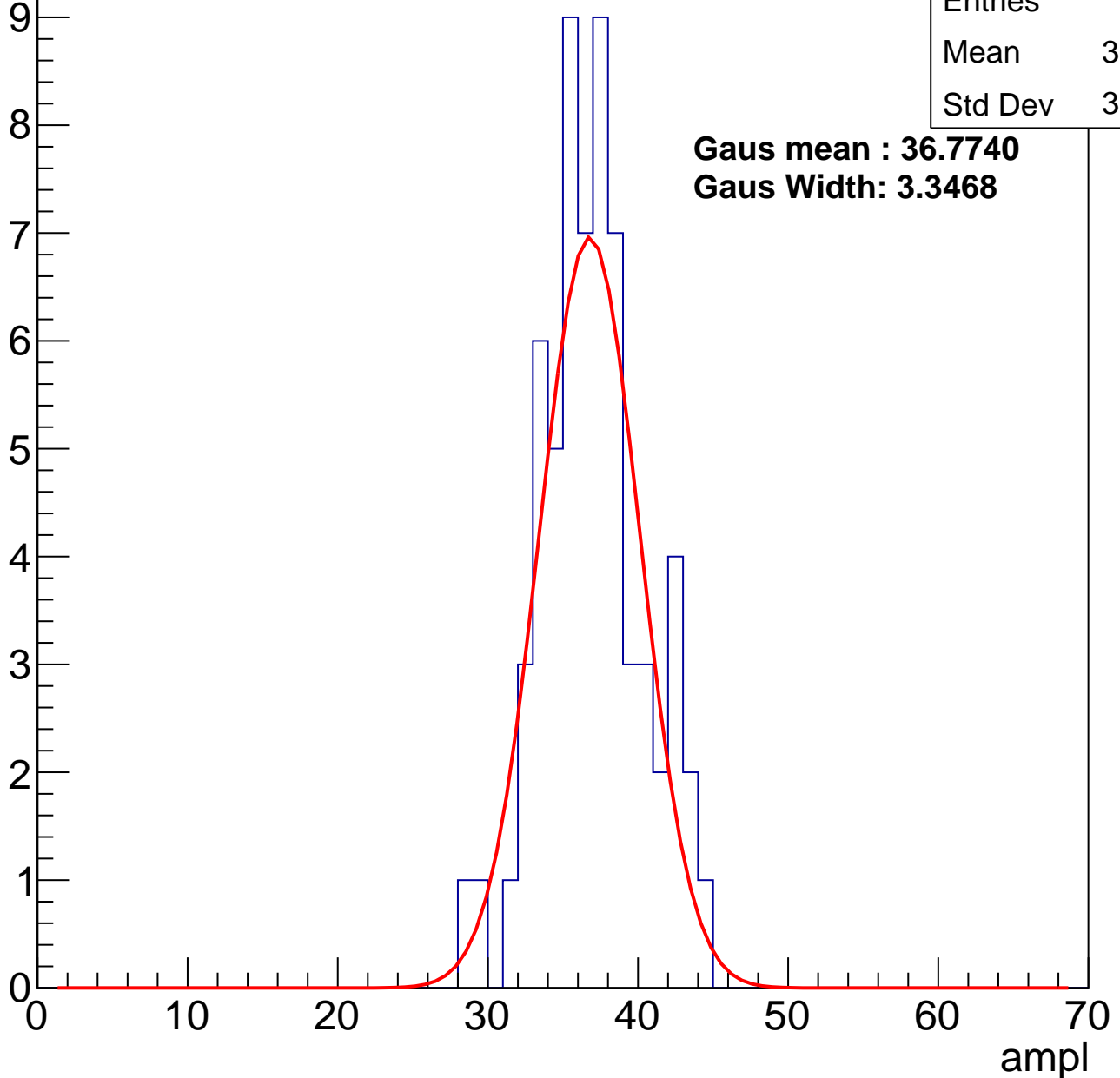
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	36.48
Std Dev	3.359

**Gaus mean : 36.7740**

**Gaus Width: 3.3468**



# B1L003S, U23-ch70, adc2

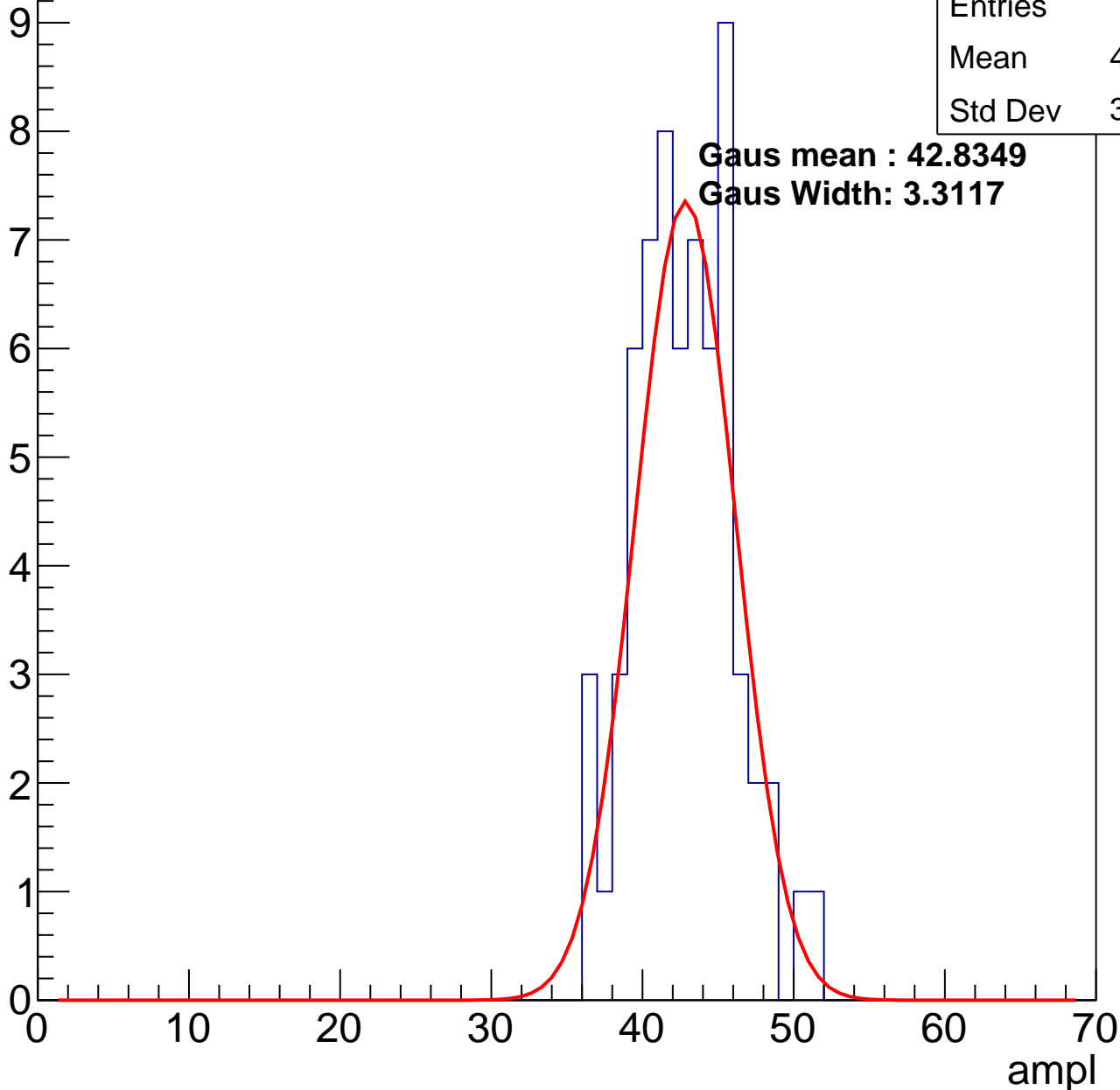
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.34
Std Dev	3.259

**Gaus mean : 42.8349**

**Gaus Width: 3.3117**

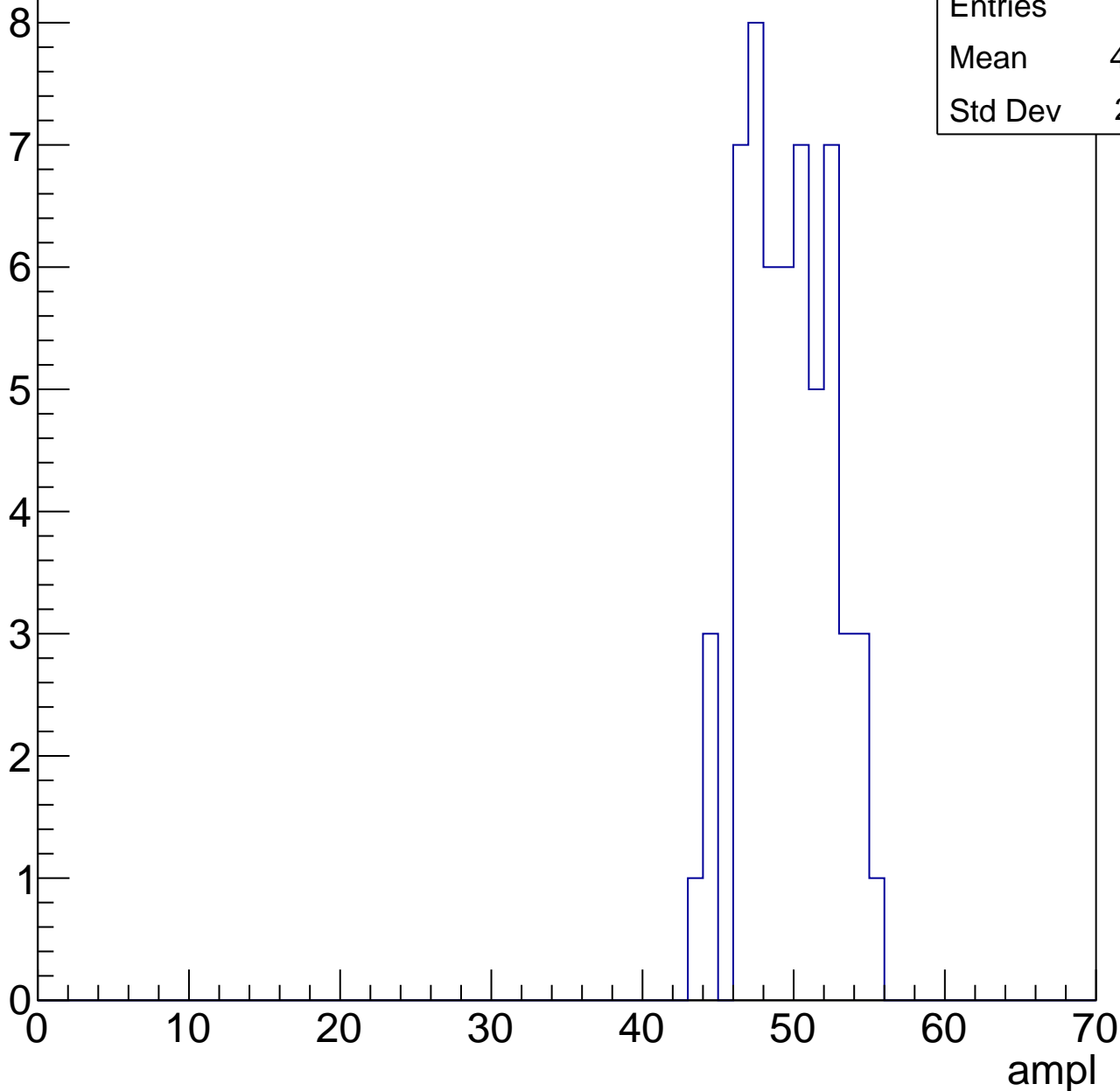


# B1L003S, U23-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	49.12
Std Dev	2.841

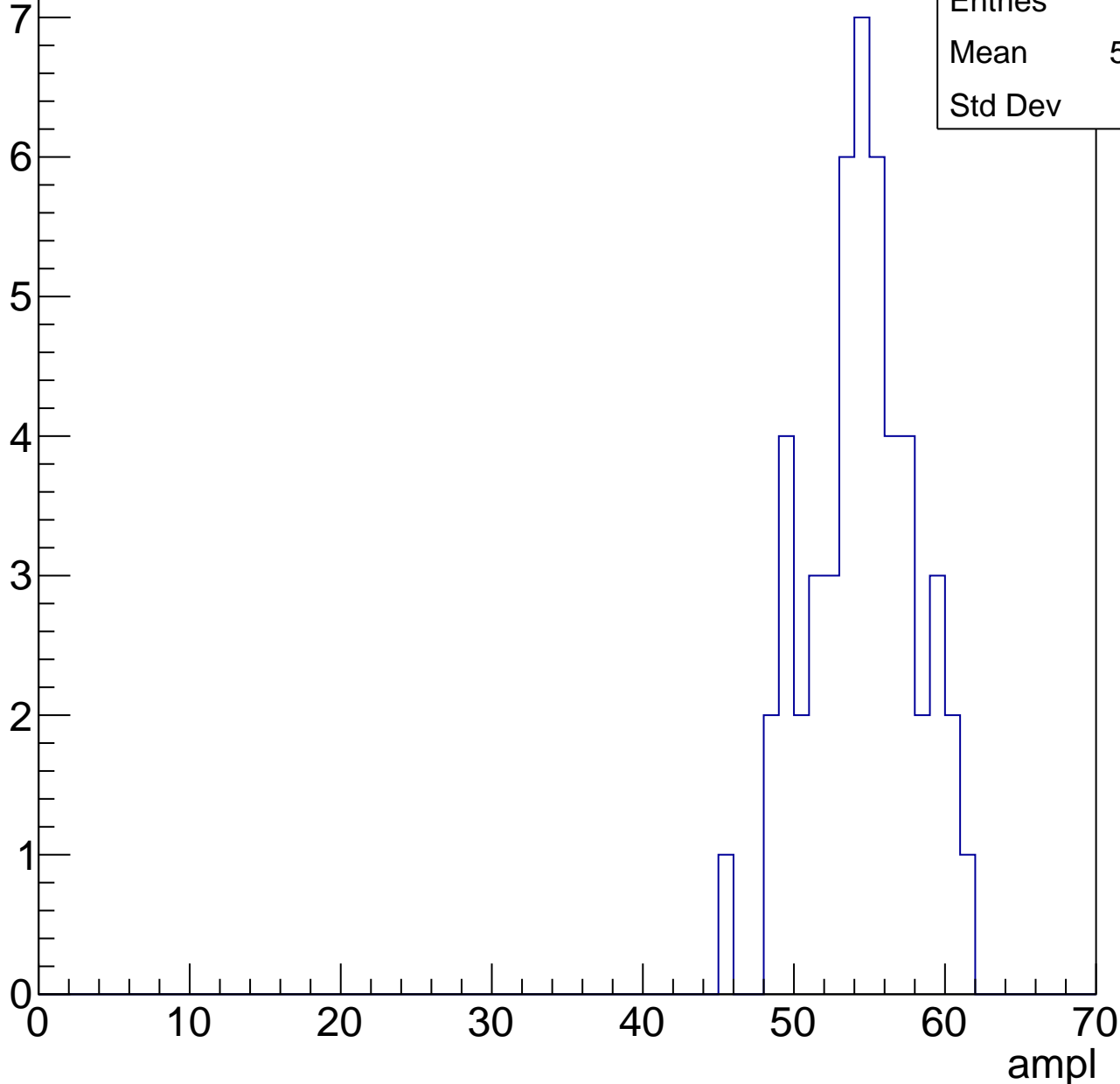


# B1L003S, U23-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	53.96
Std Dev	3.51

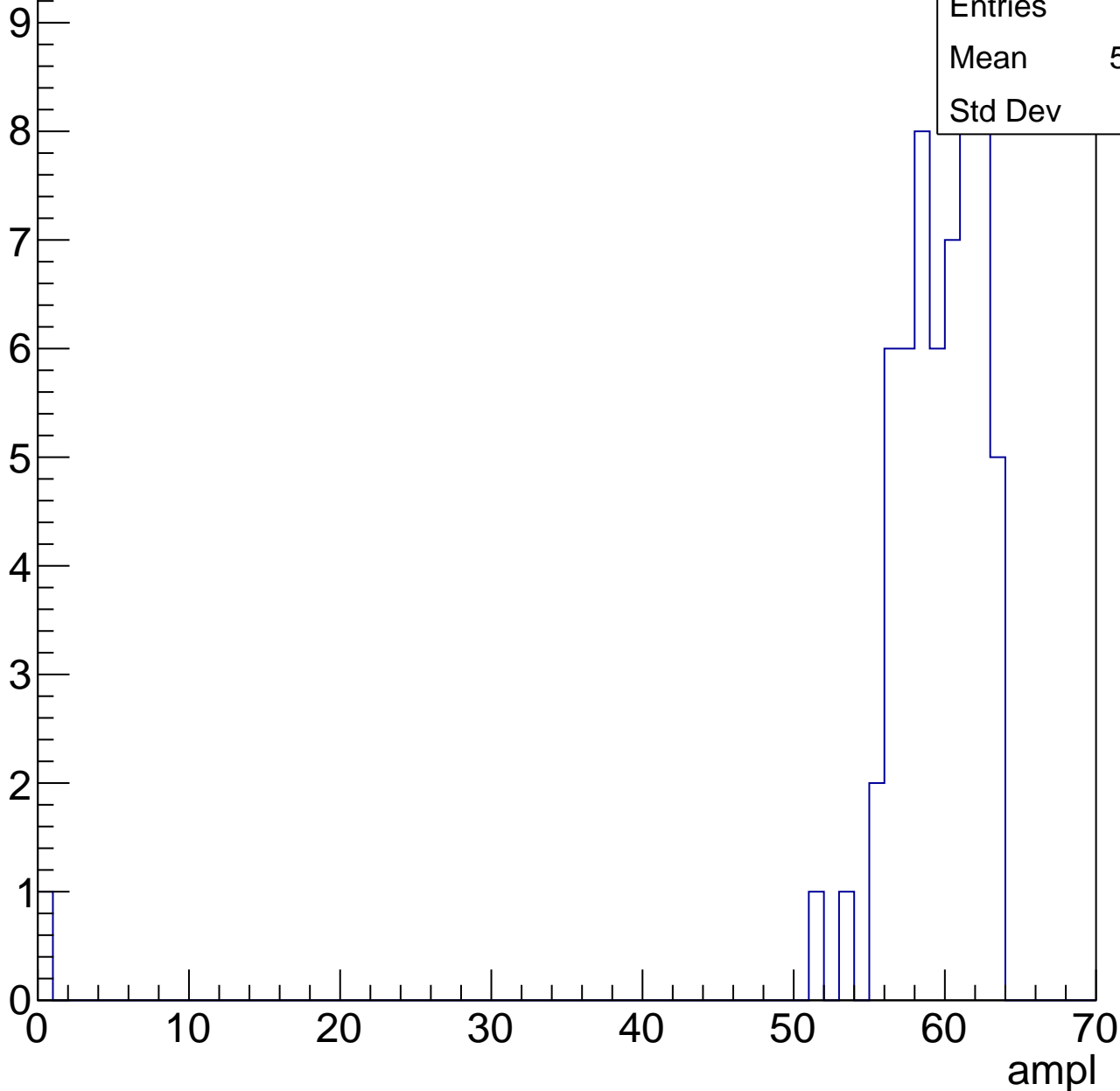


# B1L003S, U23-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

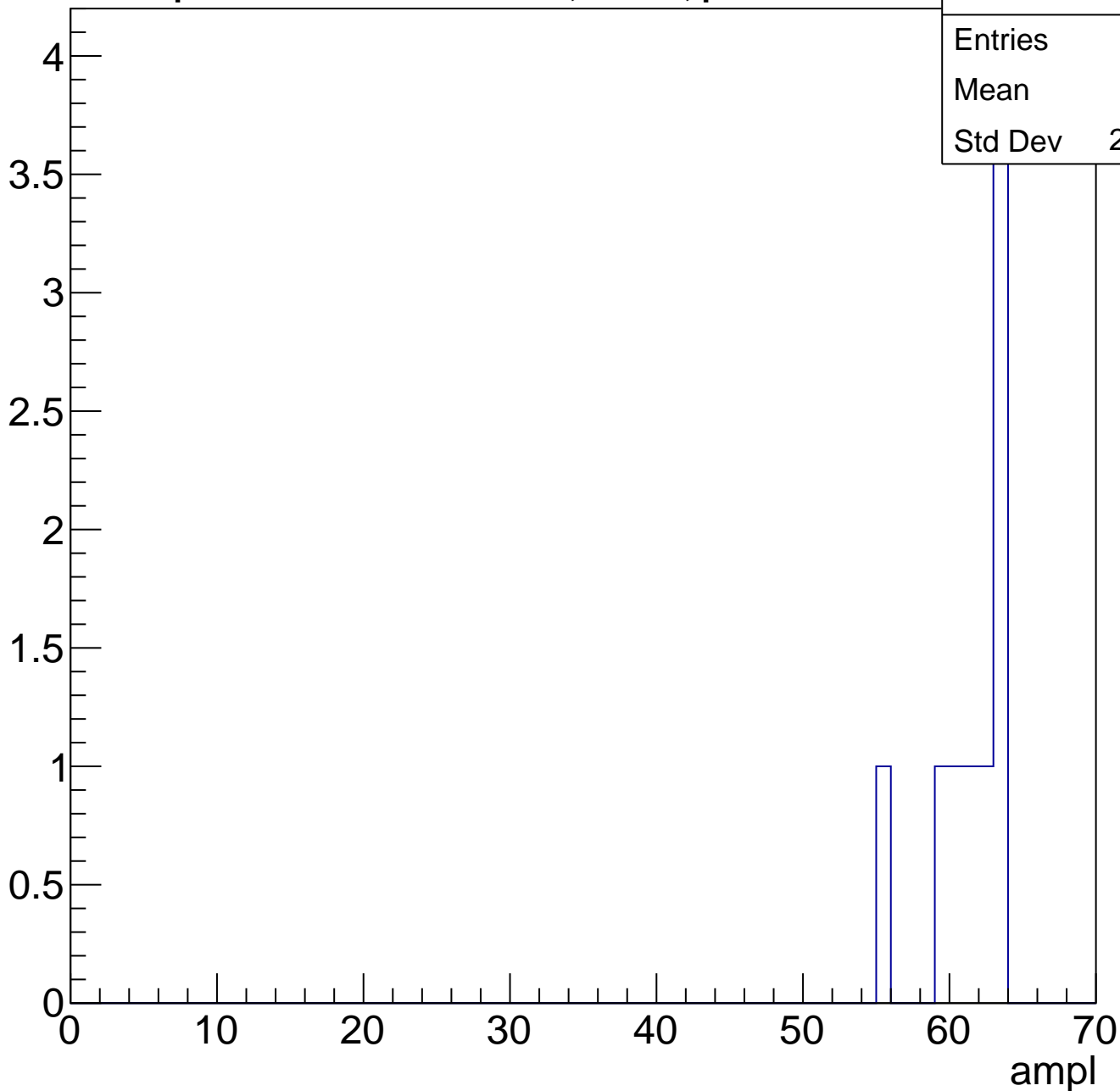
Entries	60
Mean	58.18
Std Dev	8.02



# B1L003S, U23-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch71, adc0

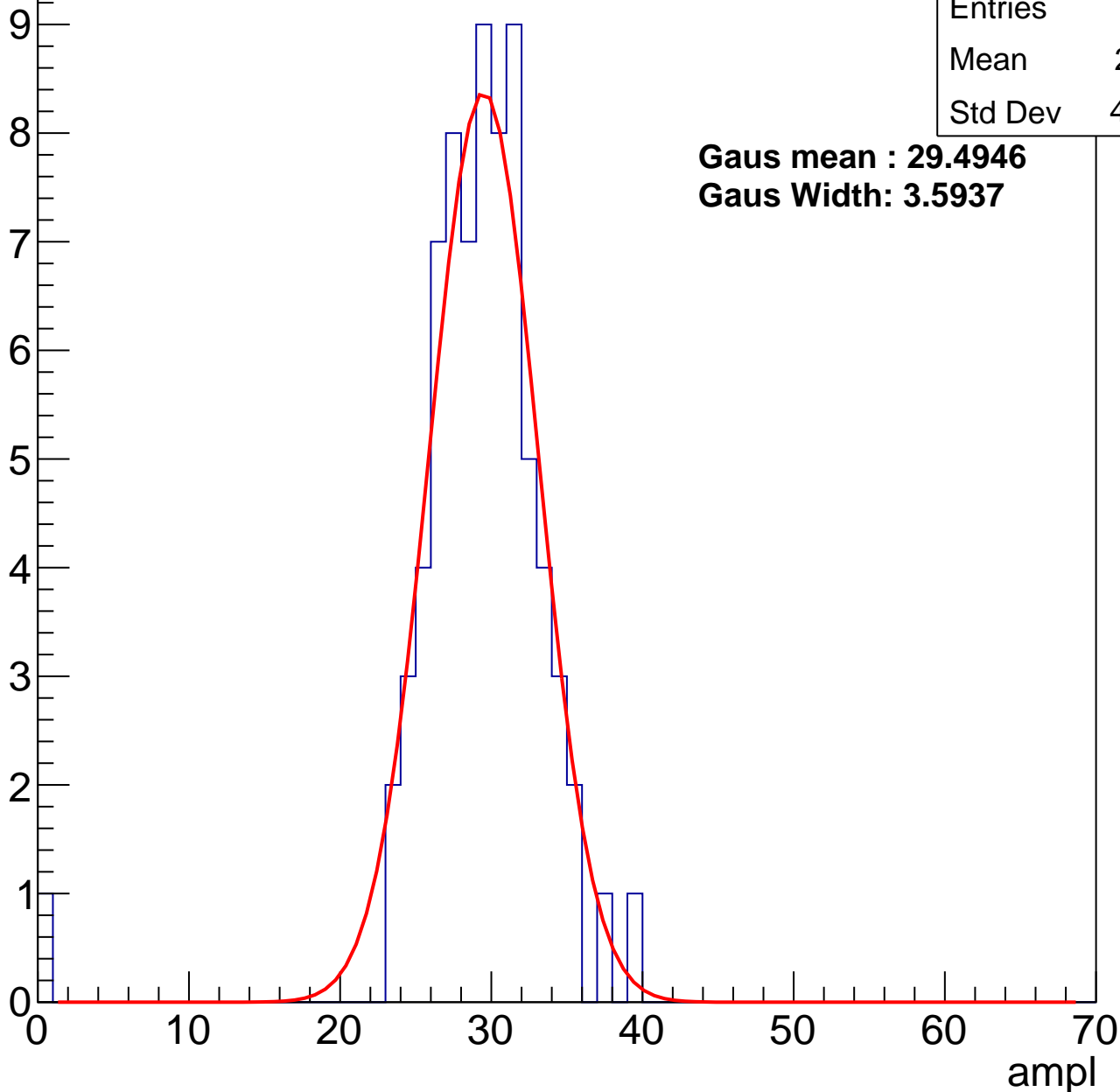
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	28.81
Std Dev	4.672

**Gaus mean : 29.4946**

**Gaus Width: 3.5937**



# B1L003S, U23-ch71, adc1

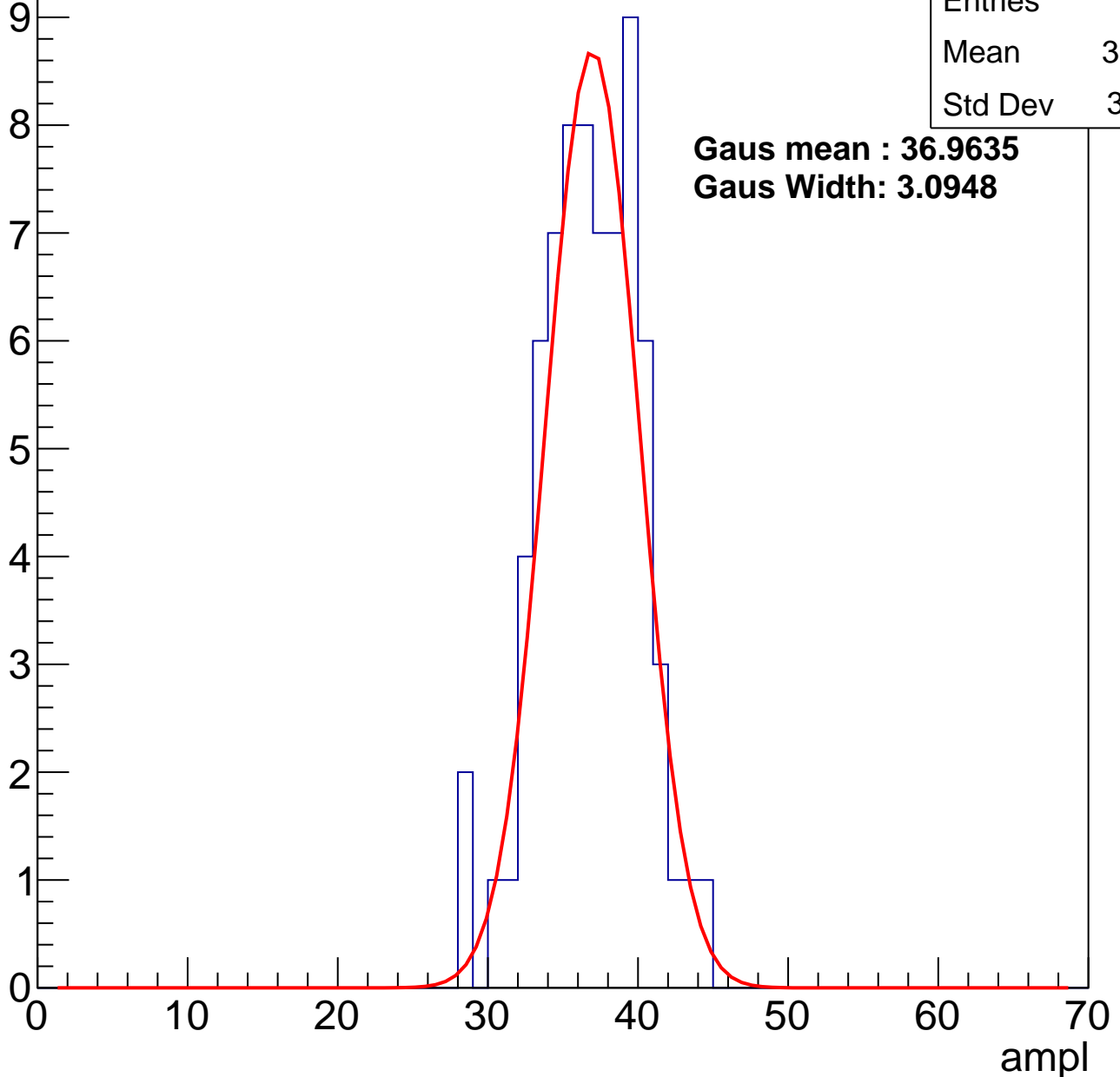
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	36.35
Std Dev	3.271

**Gaus mean : 36.9635**

**Gaus Width: 3.0948**



# B1L003S, U23-ch71, adc2

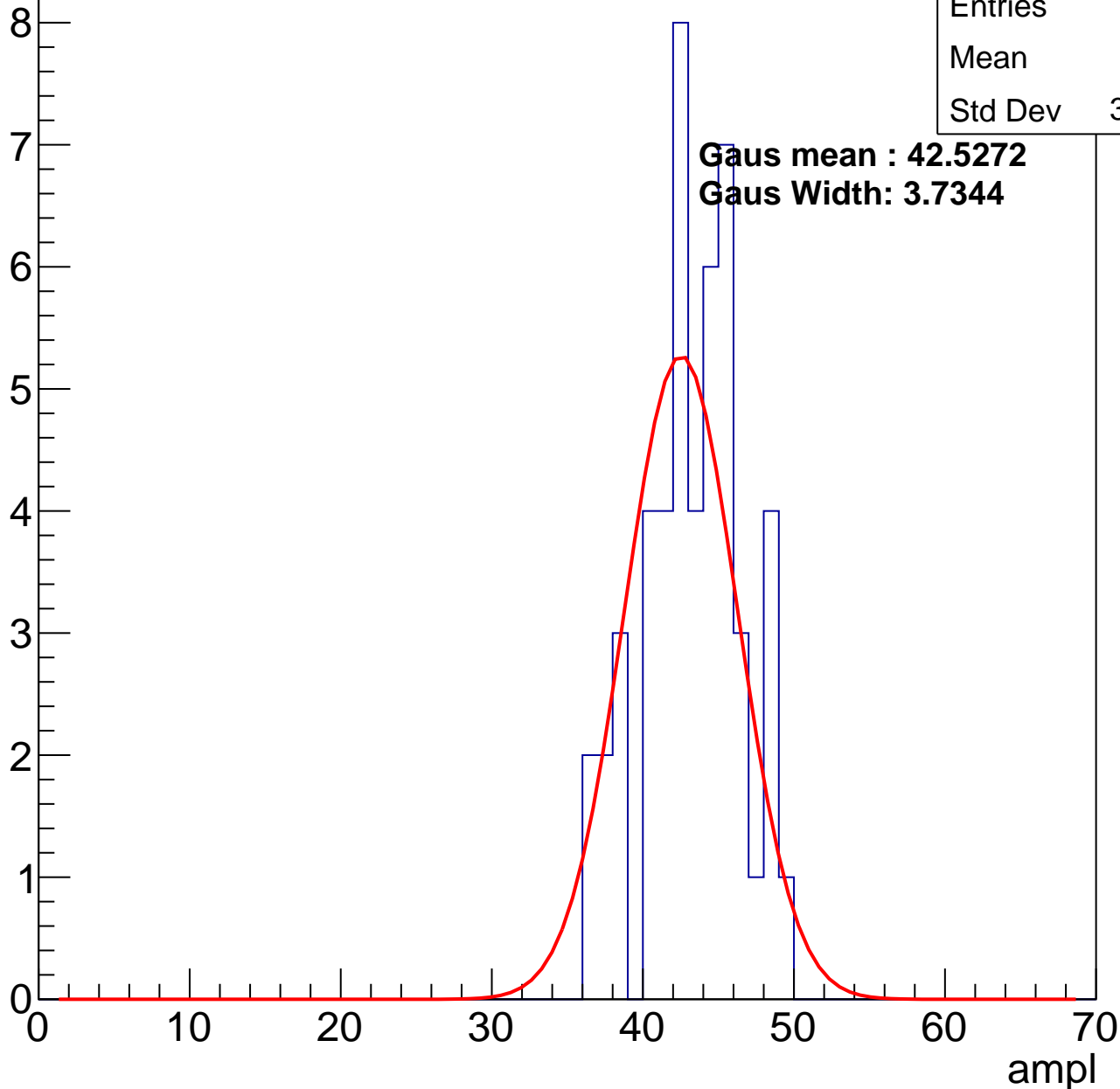
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	42.8
Std Dev	3.245

**Gaus mean : 42.5272**

**Gaus Width: 3.7344**

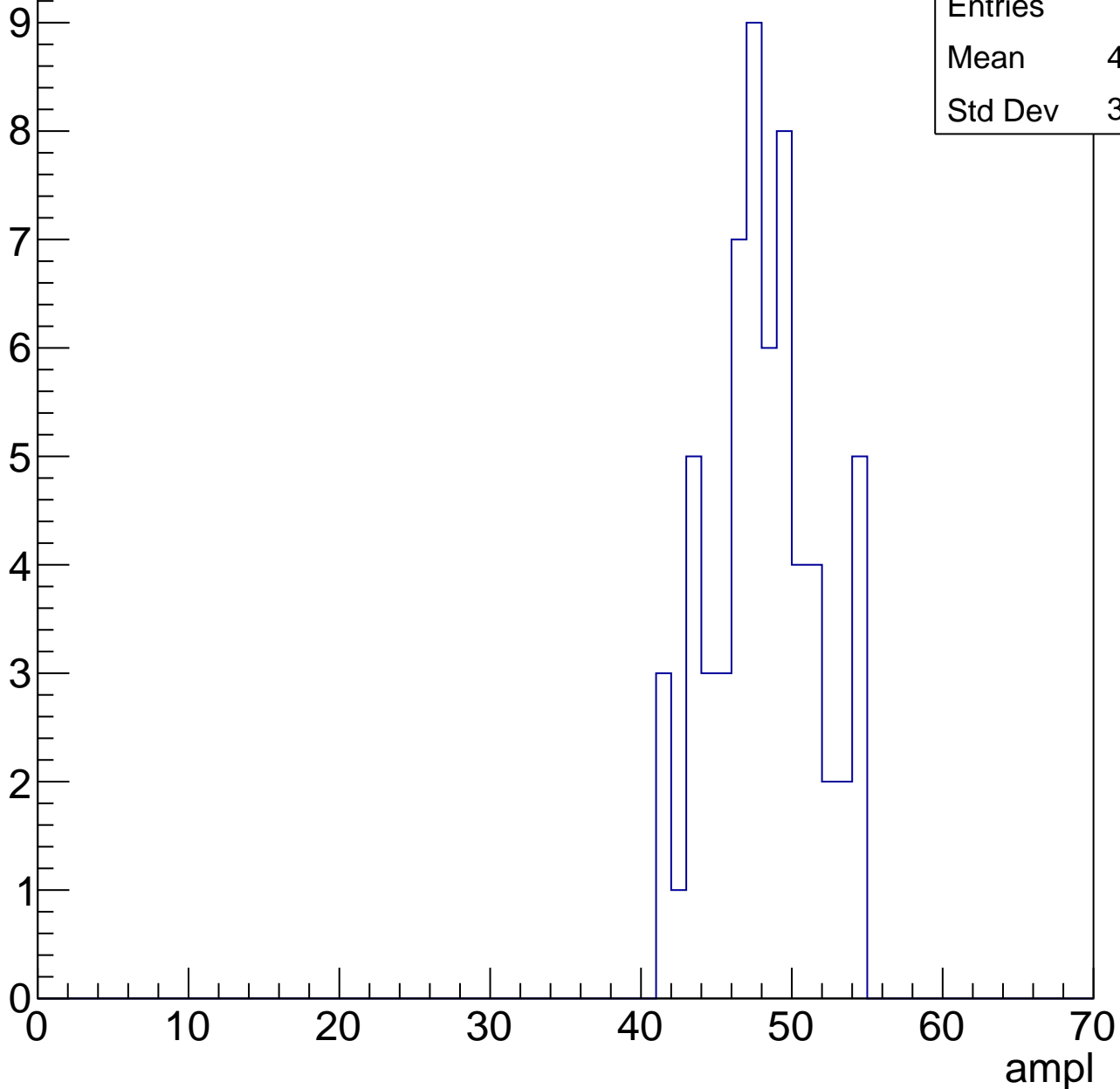


# B1L003S, U23-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	47.68
Std Dev	3.444

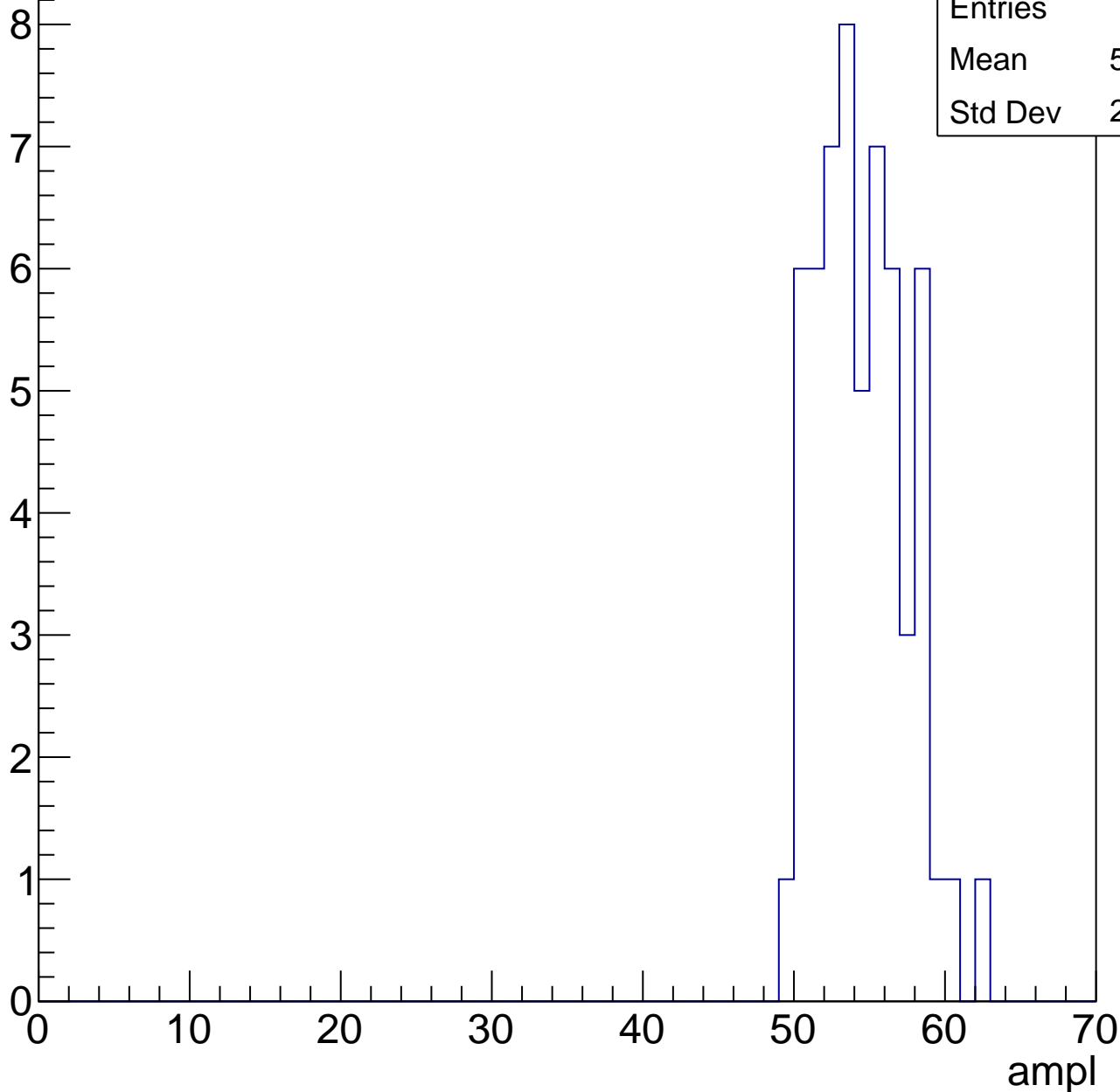


# B1L003S, U23-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

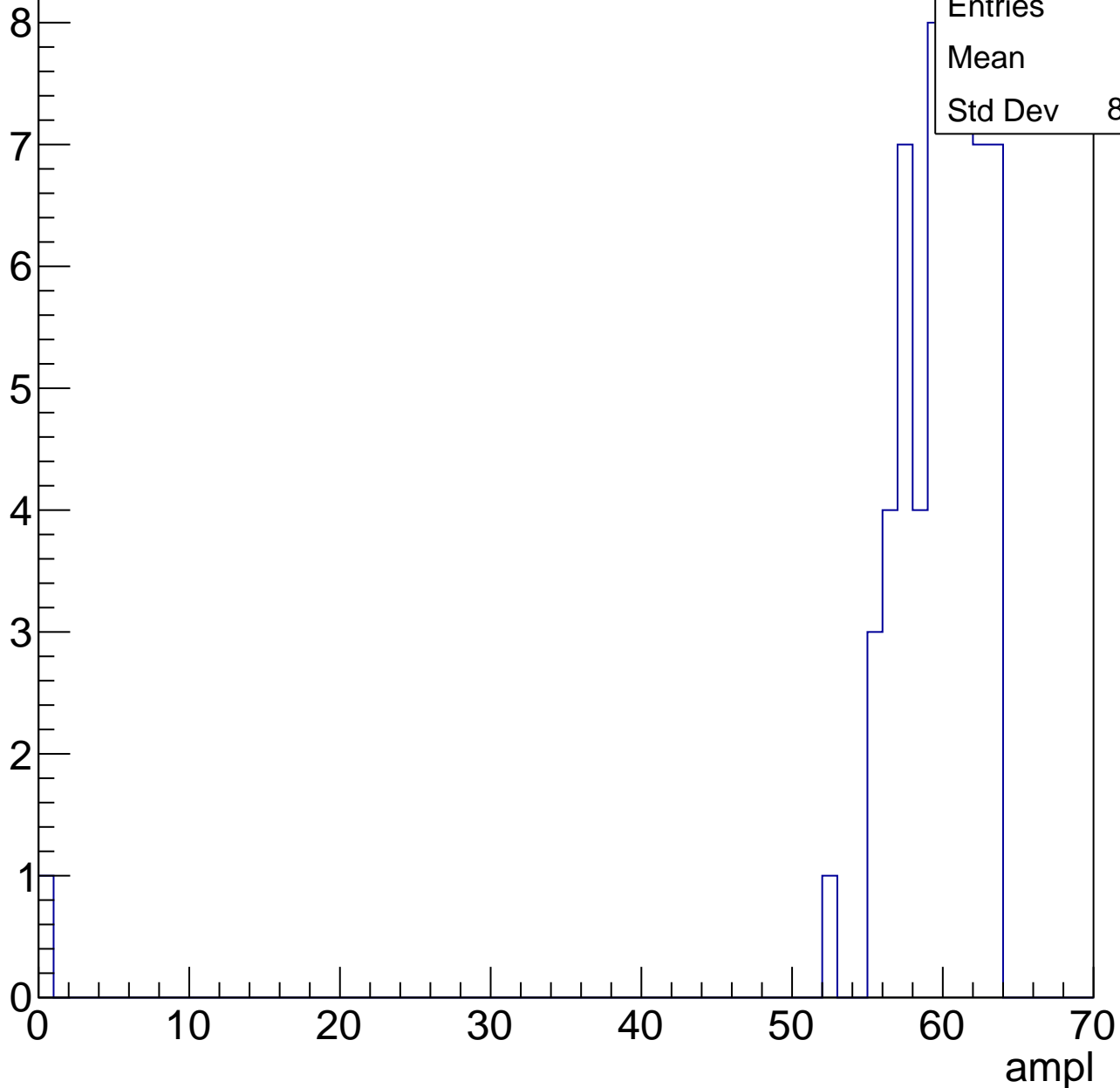
Entries	58
Mean	54.03
Std Dev	2.906



# B1L003S, U23-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

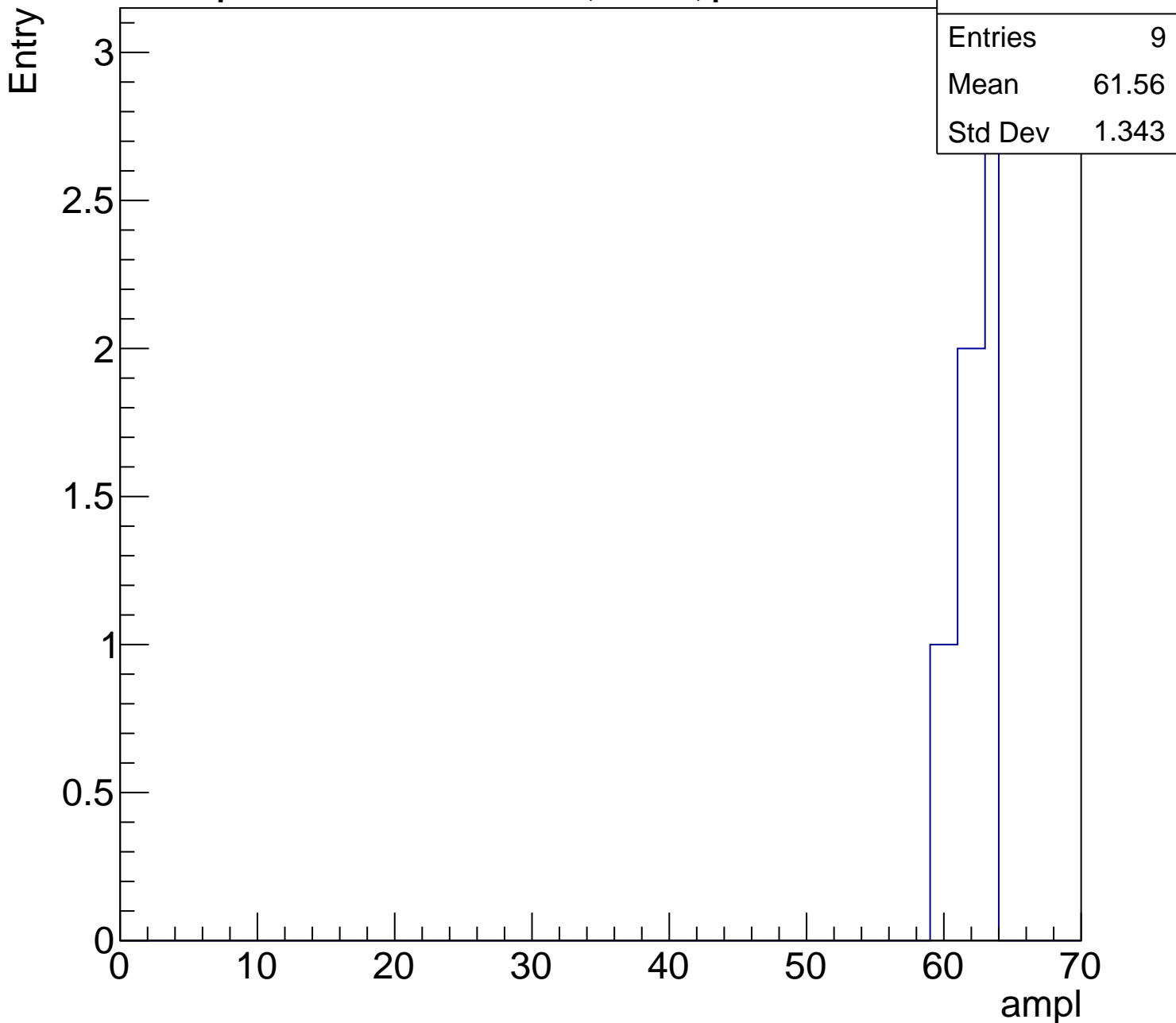
9

Mean

61.56

Std Dev

1.343

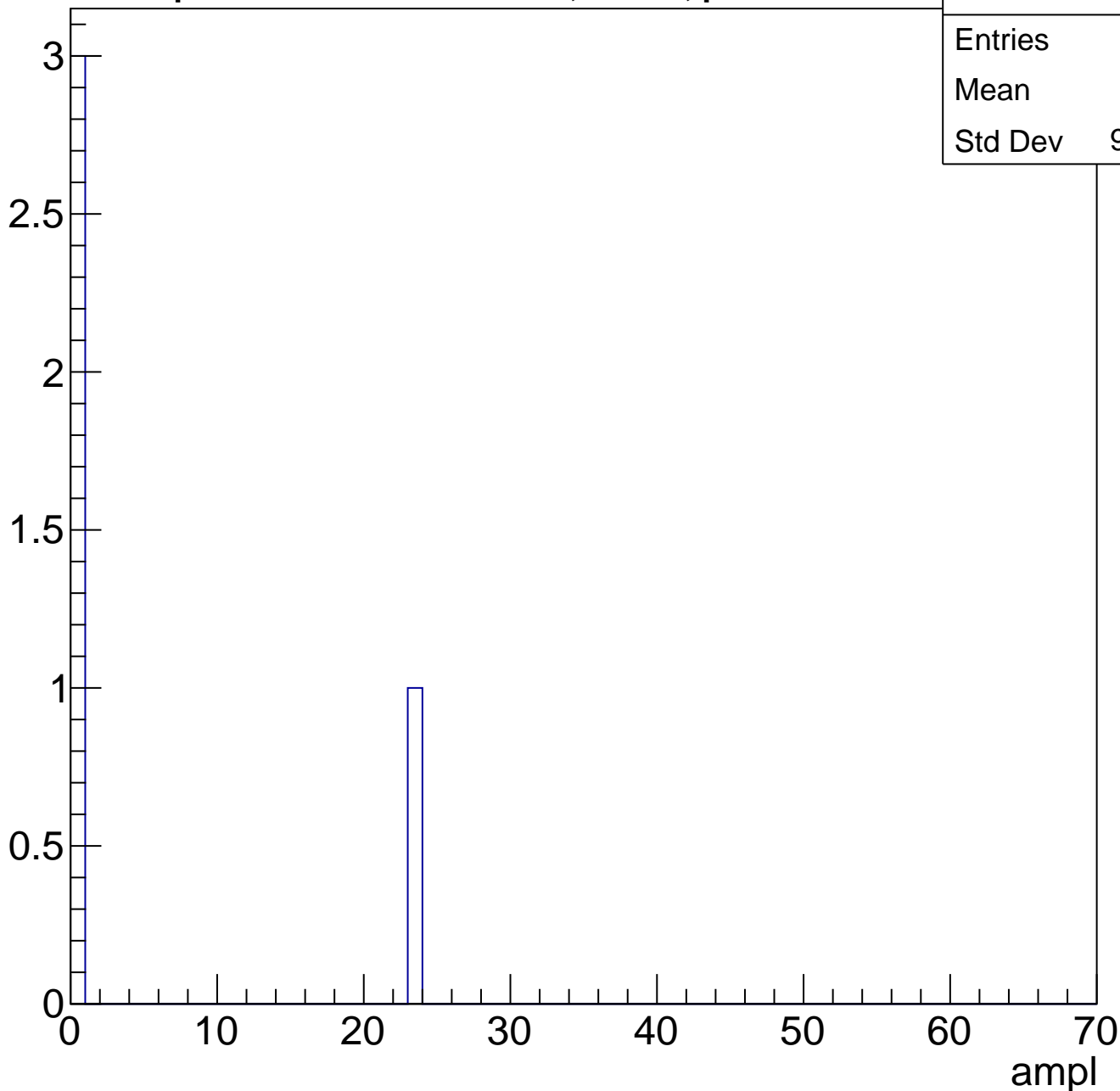




# B1L003S, U23-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	4
Mean	5.75
Std Dev	9.959

# B1L003S, U23-ch72, adc0

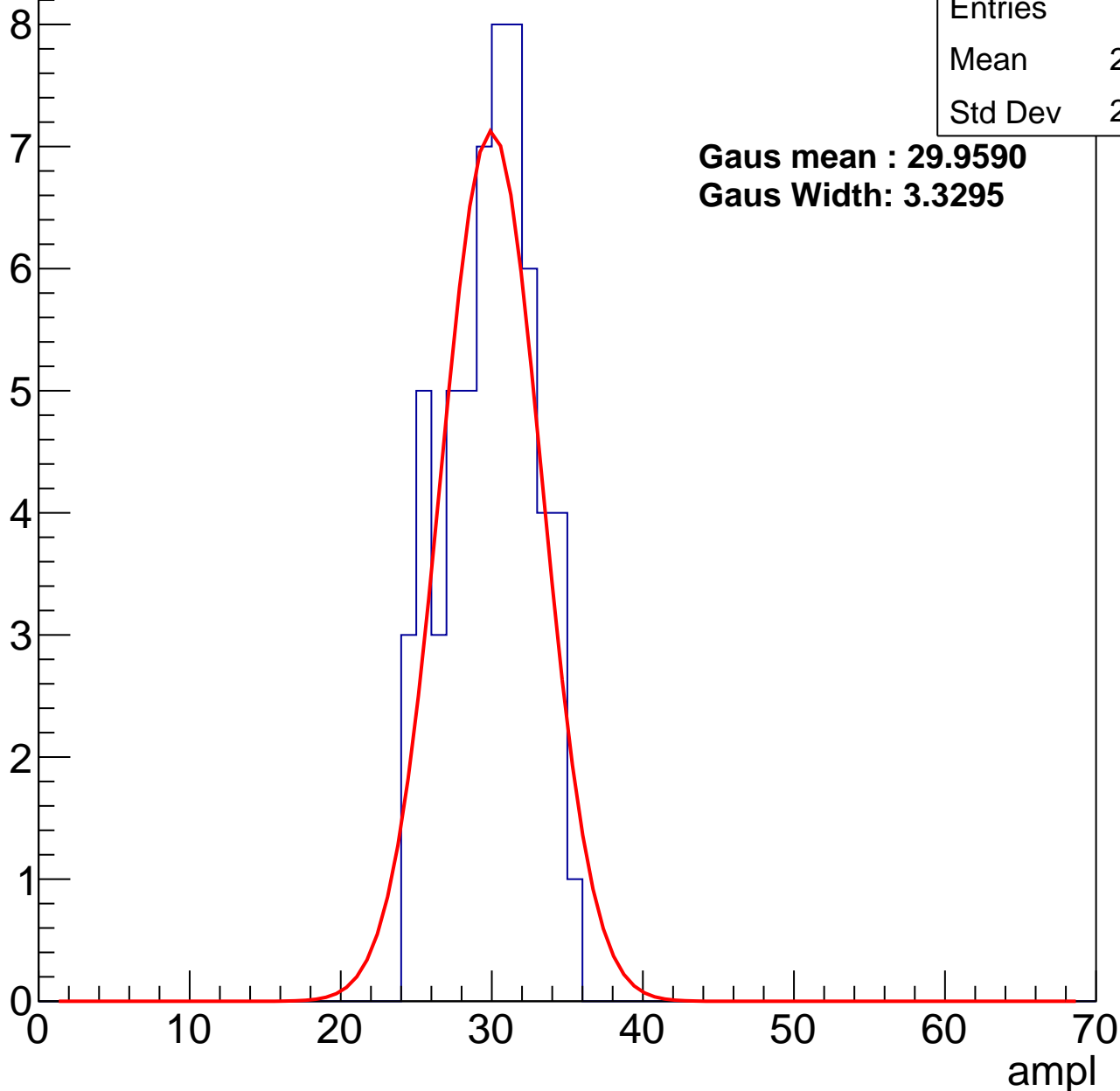
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.42
Std Dev	2.883

**Gaus mean : 29.9590**

**Gaus Width: 3.3295**



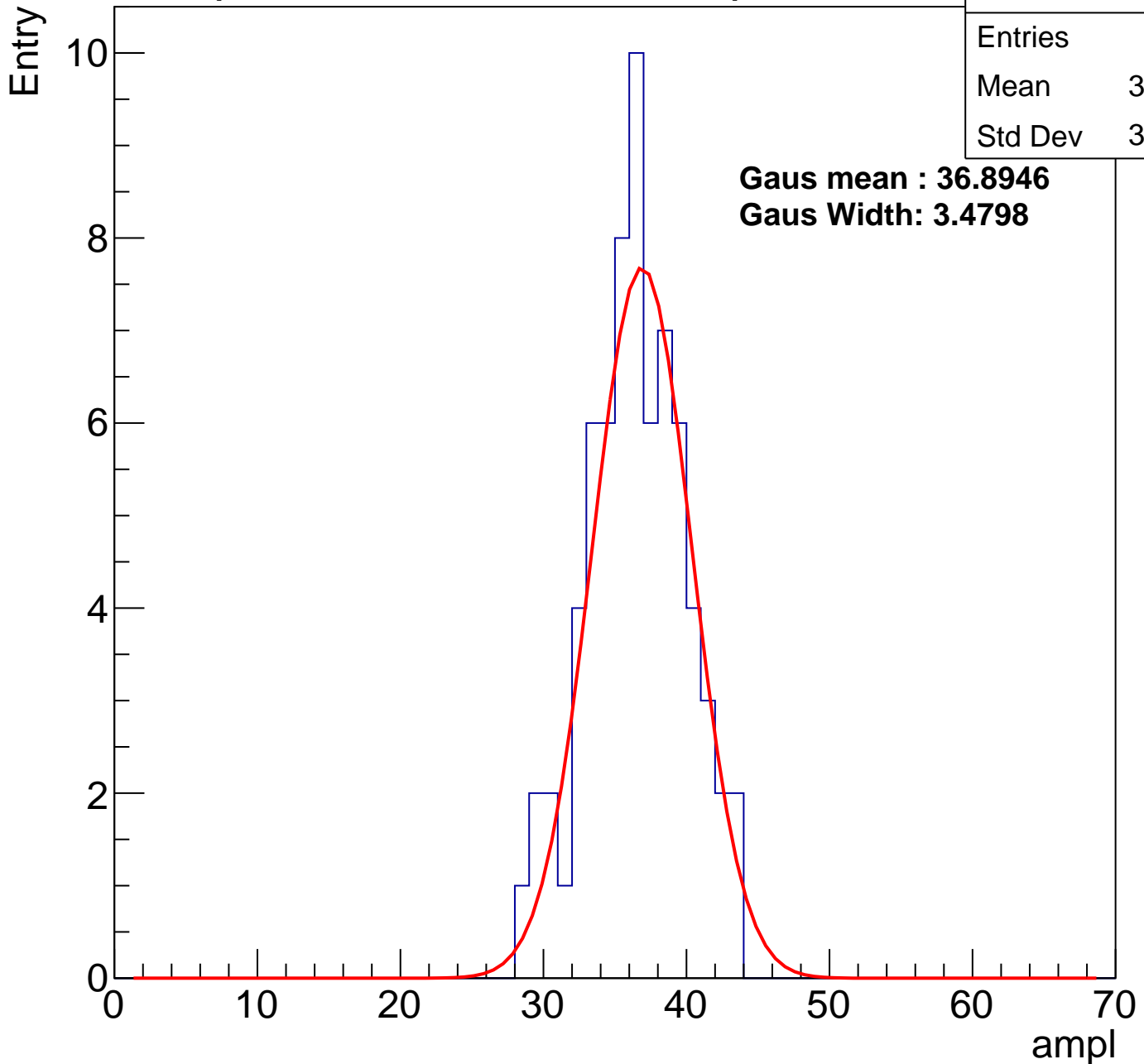
# B1L003S, U23-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	70
Mean	36.03
Std Dev	3.393

**Gaus mean : 36.8946**

**Gaus Width: 3.4798**



# B1L003S, U23-ch72, adc2

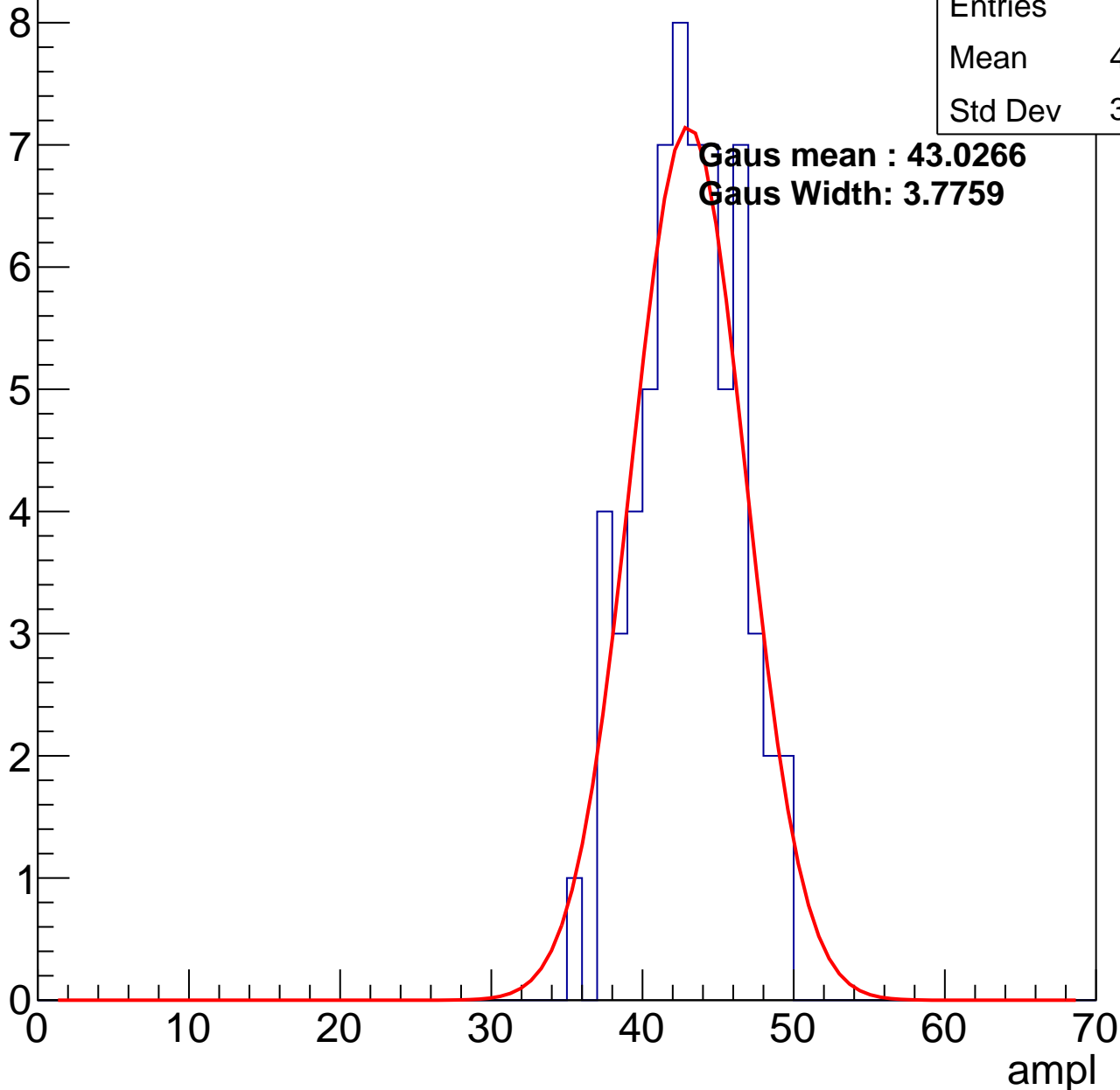
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.57
Std Dev	3.225

**Gaus mean : 43.0266**

**Gaus Width: 3.7759**

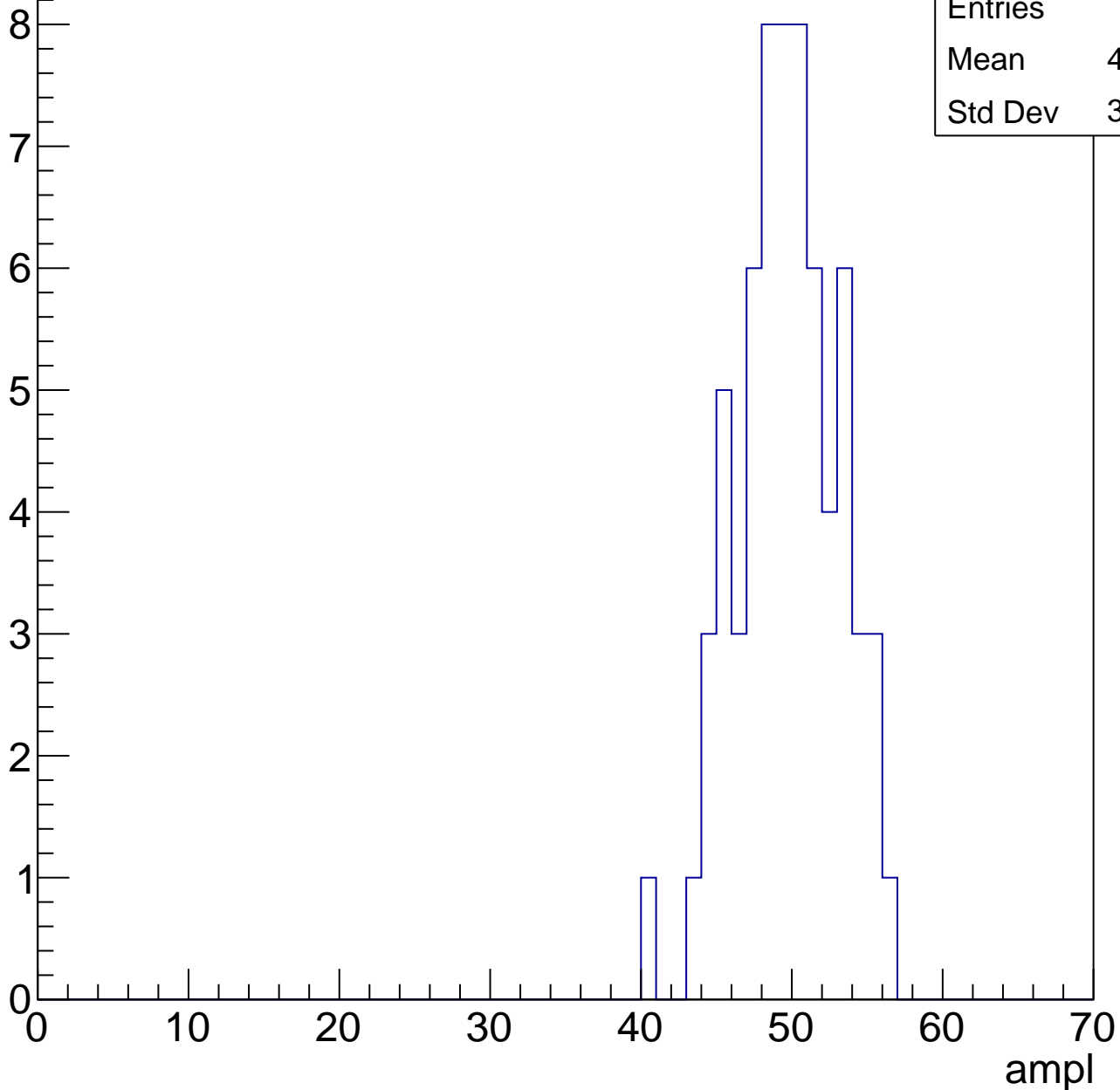


# B1L003S, U23-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

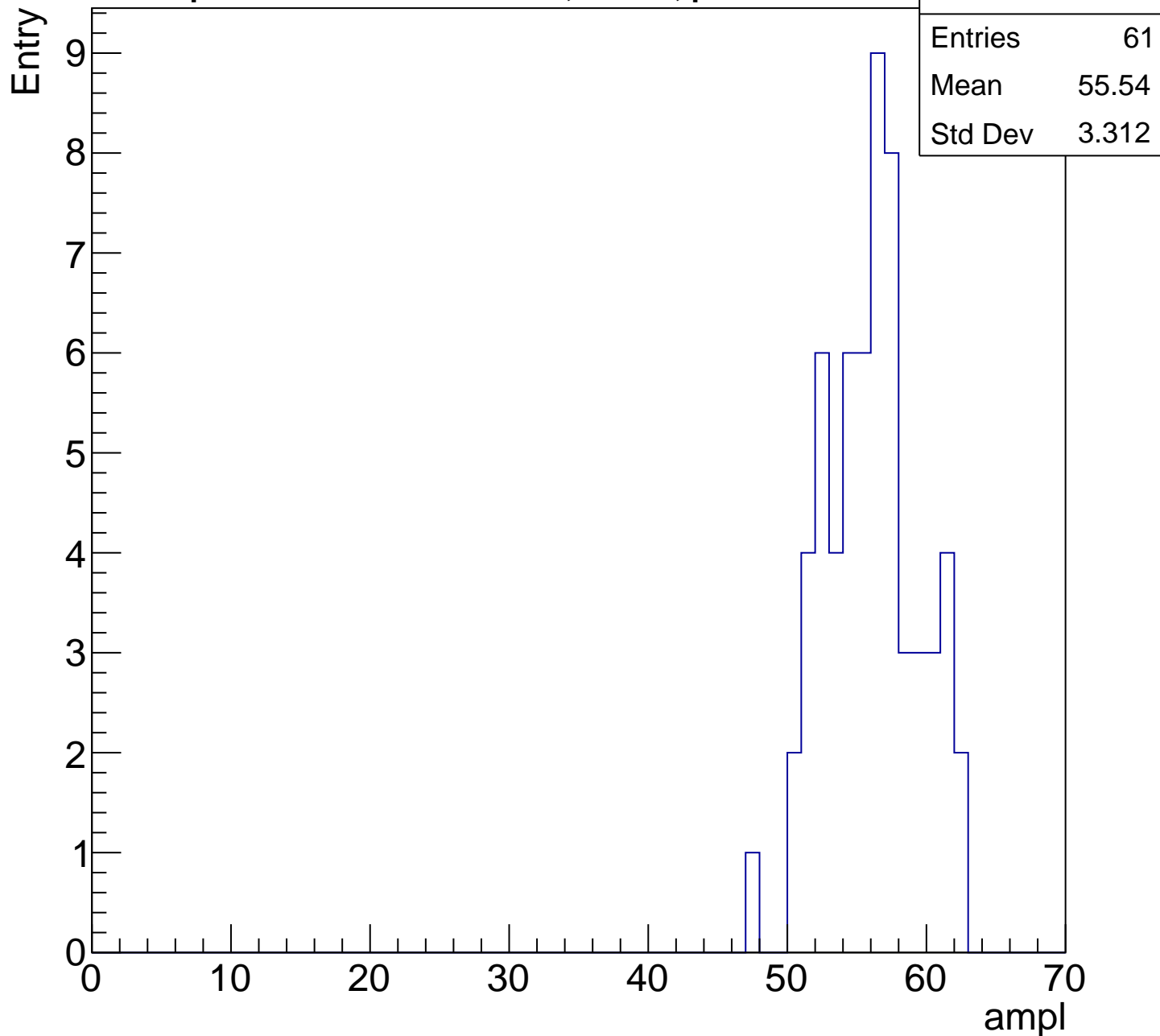
Entry

Entries	66
Mean	49.26
Std Dev	3.309



# B1L003S, U23-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

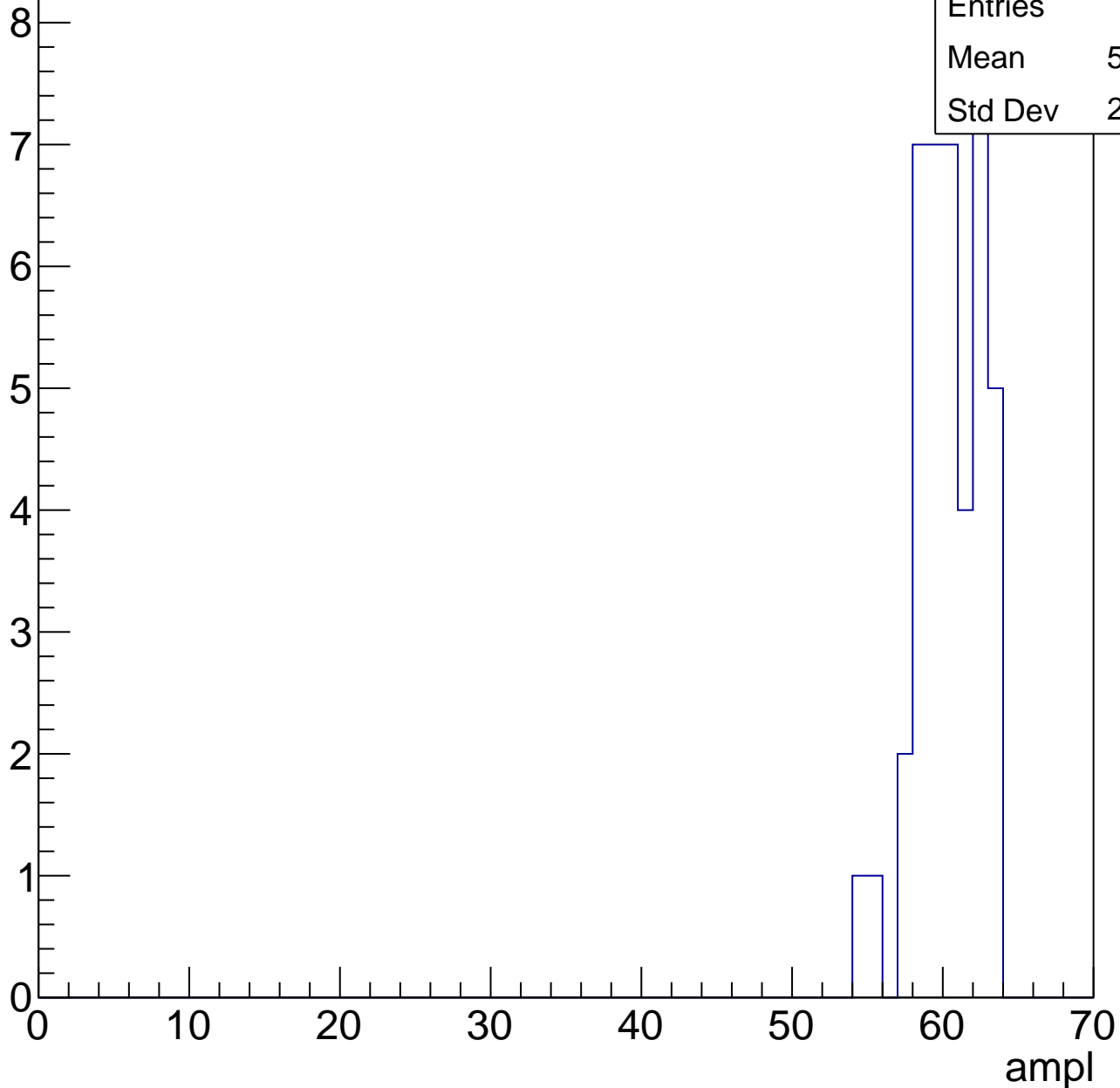


# B1L003S, U23-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	42
Mean	59.93
Std Dev	2.154

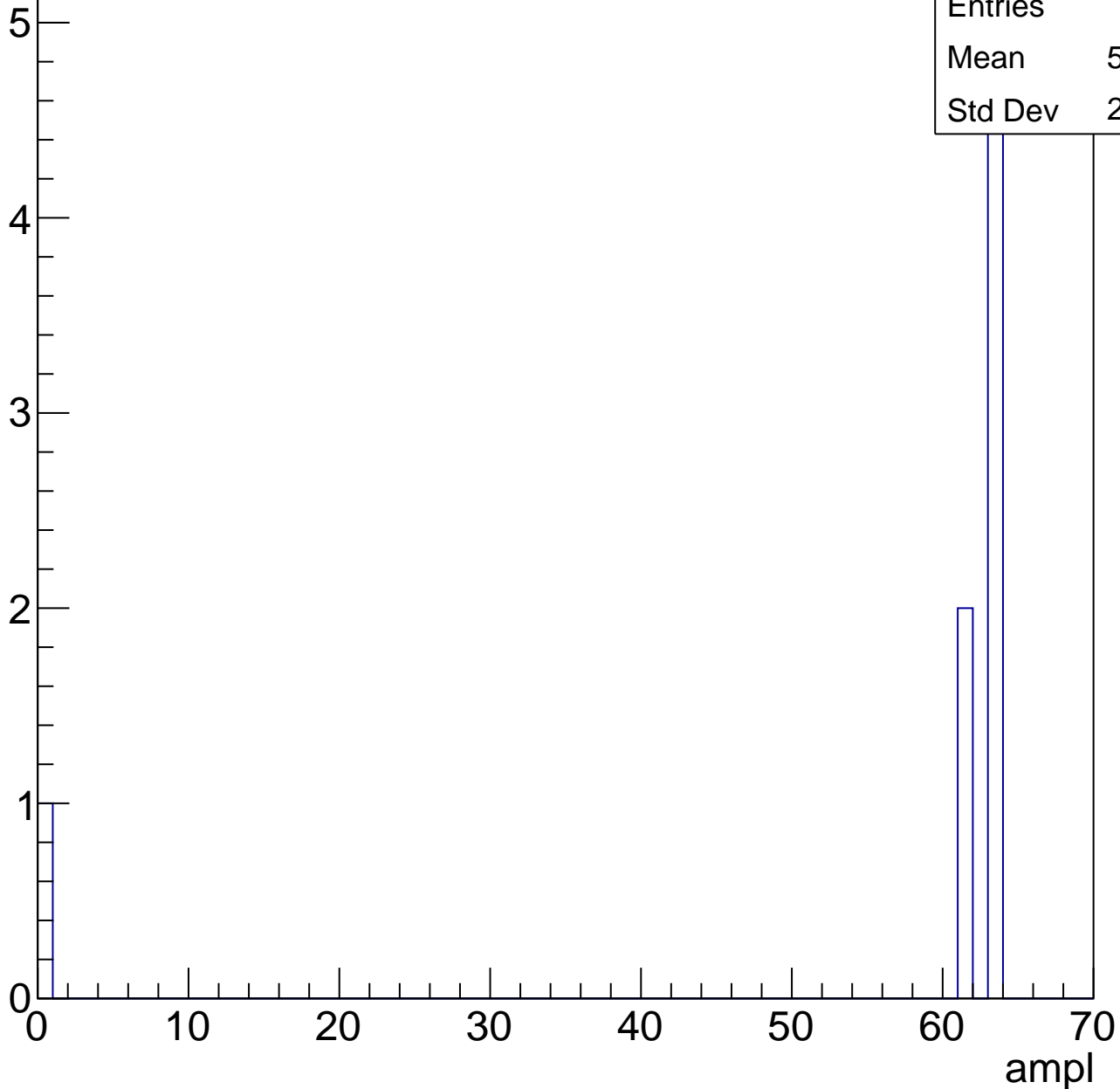


# B1L003S, U23-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	8
Mean	54.62
Std Dev	20.66





# B1L003S, U23-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch73, adc0

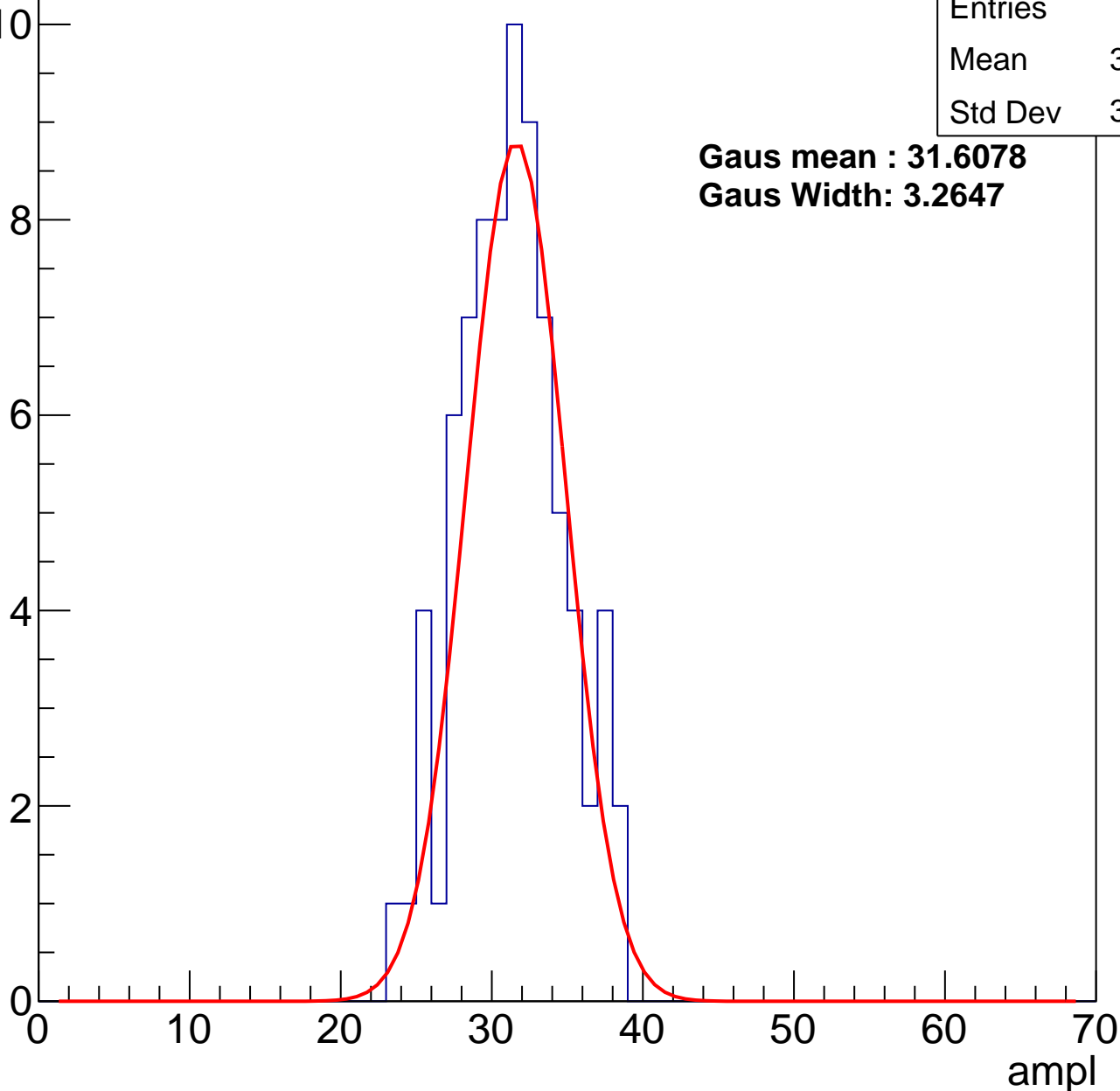
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	30.86
Std Dev	3.408

**Gaus mean : 31.6078**

**Gaus Width: 3.2647**



# B1L003S, U23-ch73, adc1

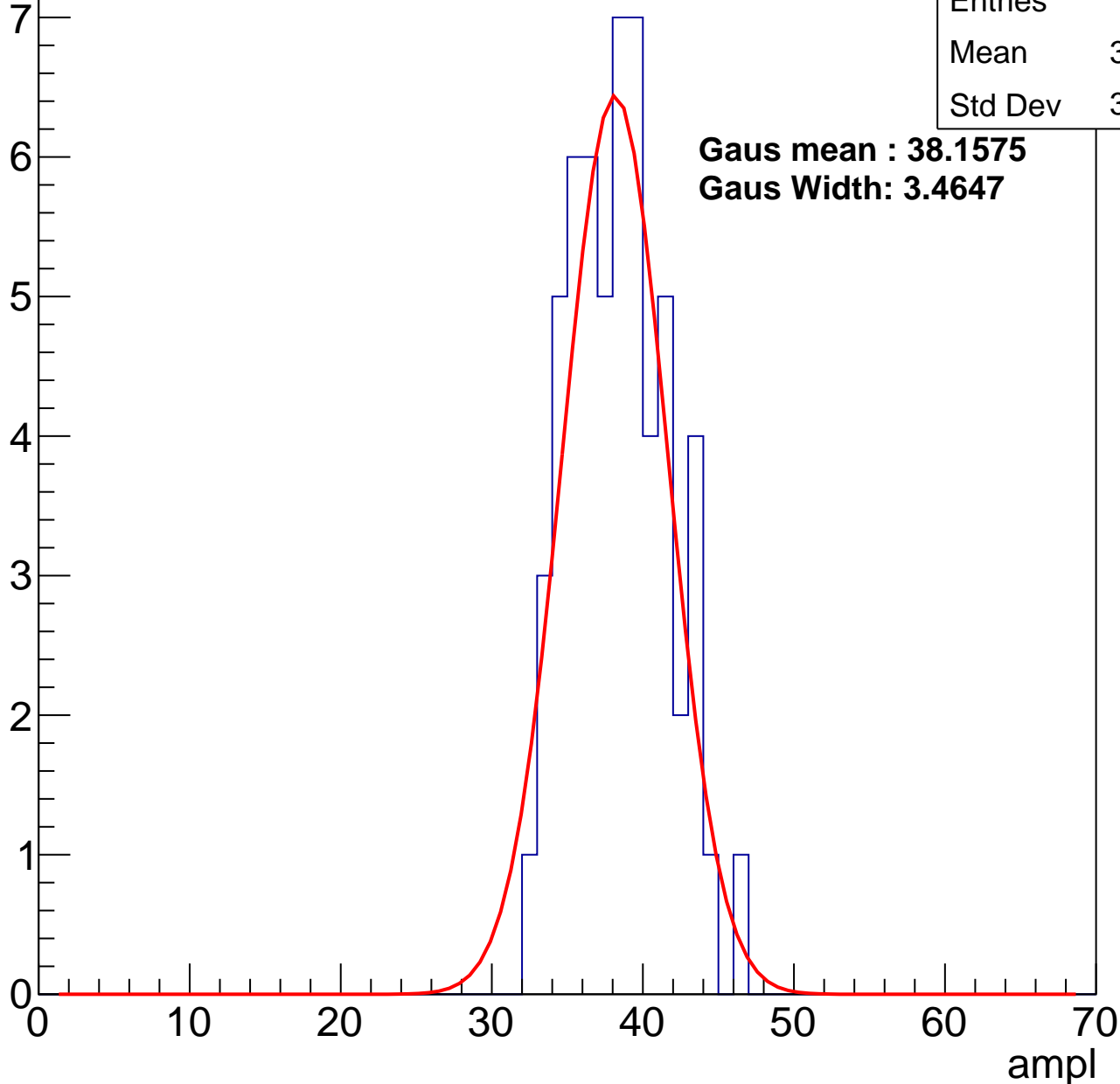
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	37.93
Std Dev	3.173

**Gaus mean : 38.1575**

**Gaus Width: 3.4647**



# B1L003S, U23-ch73, adc2

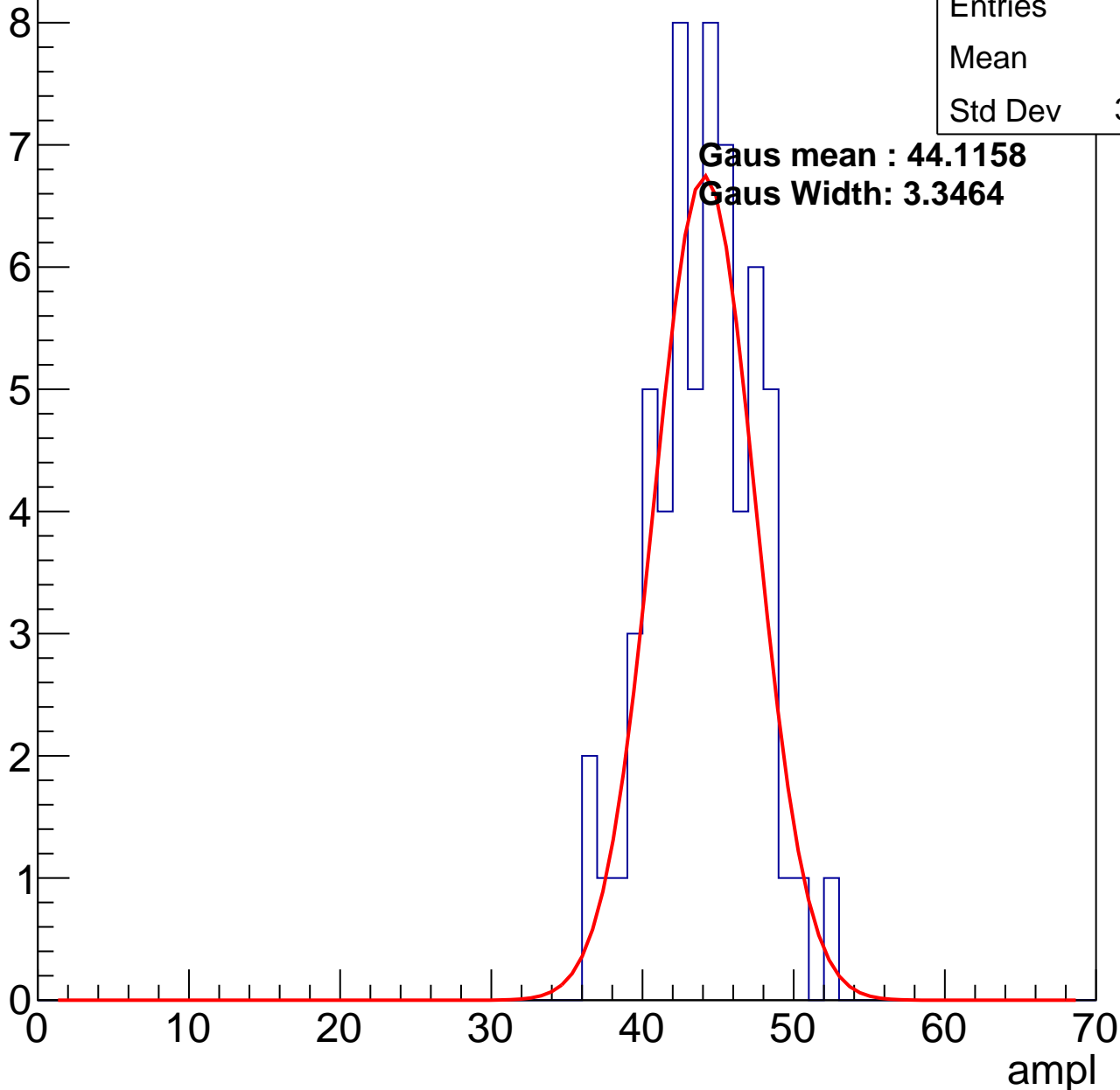
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.6
Std Dev	3.391

**Gaus mean : 44.1158**

**Gaus Width: 3.3464**

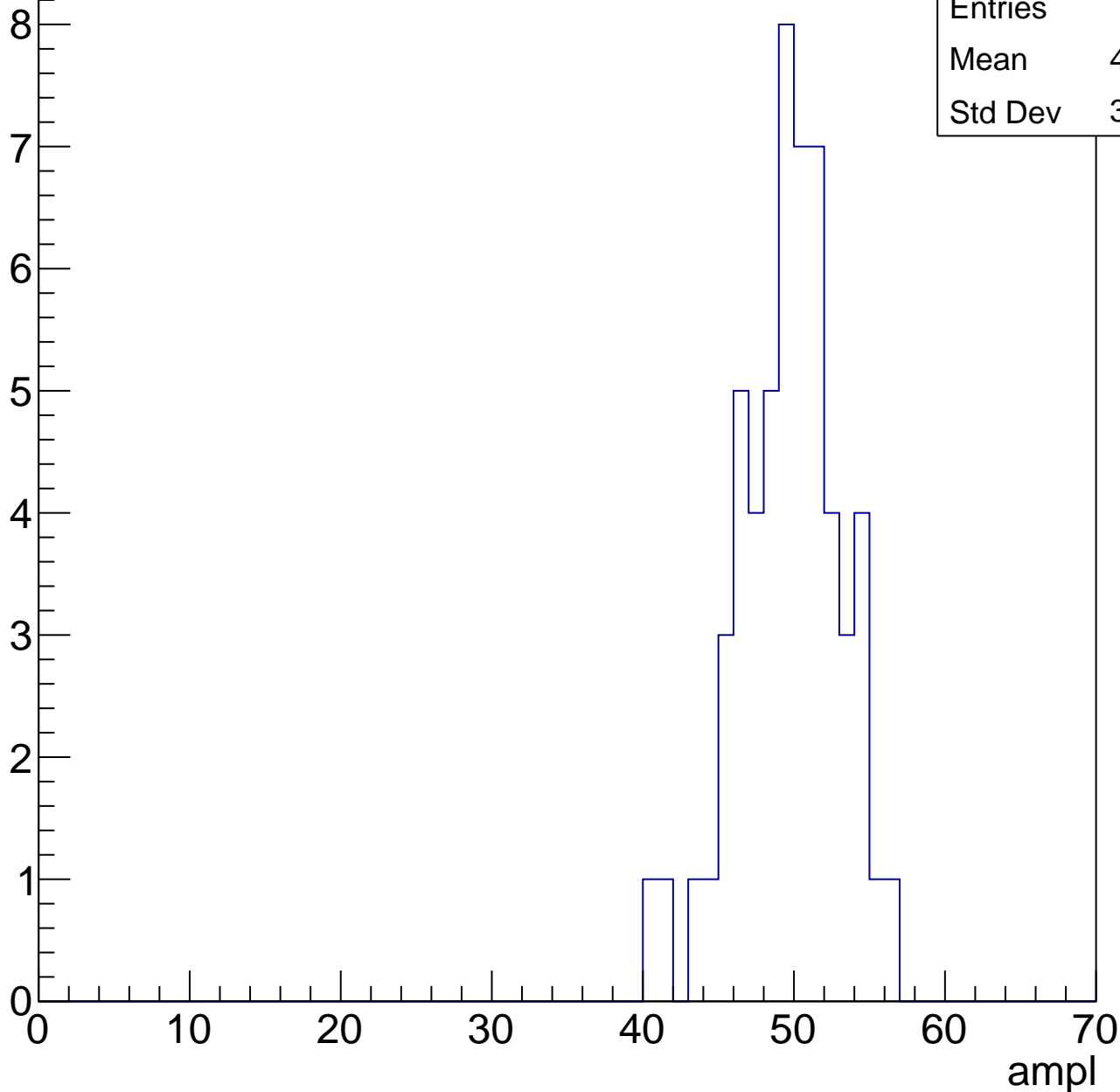


# B1L003S, U23-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	49.18
Std Dev	3.333

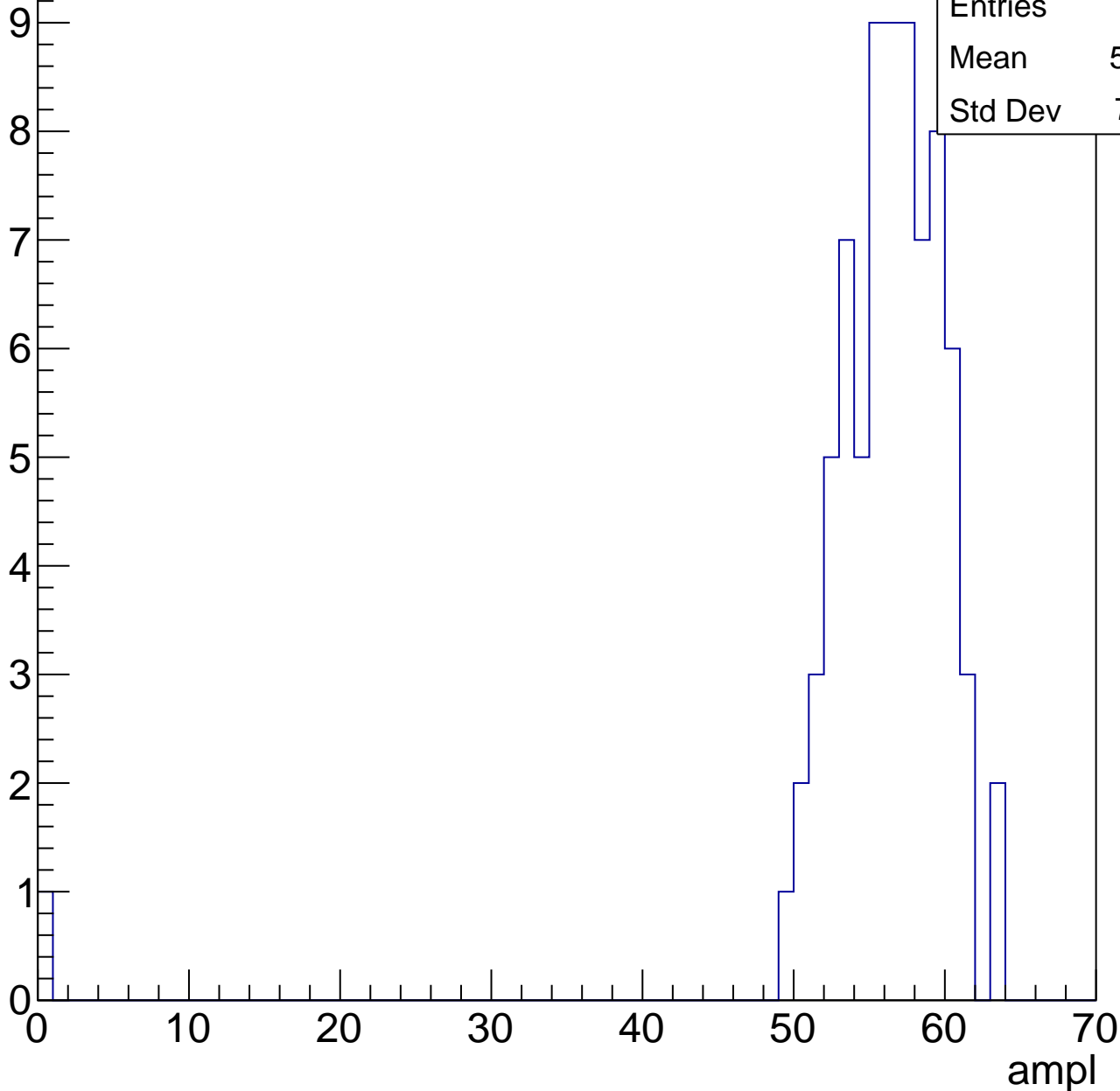


# B1L003S, U23-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	55.35
Std Dev	7.071

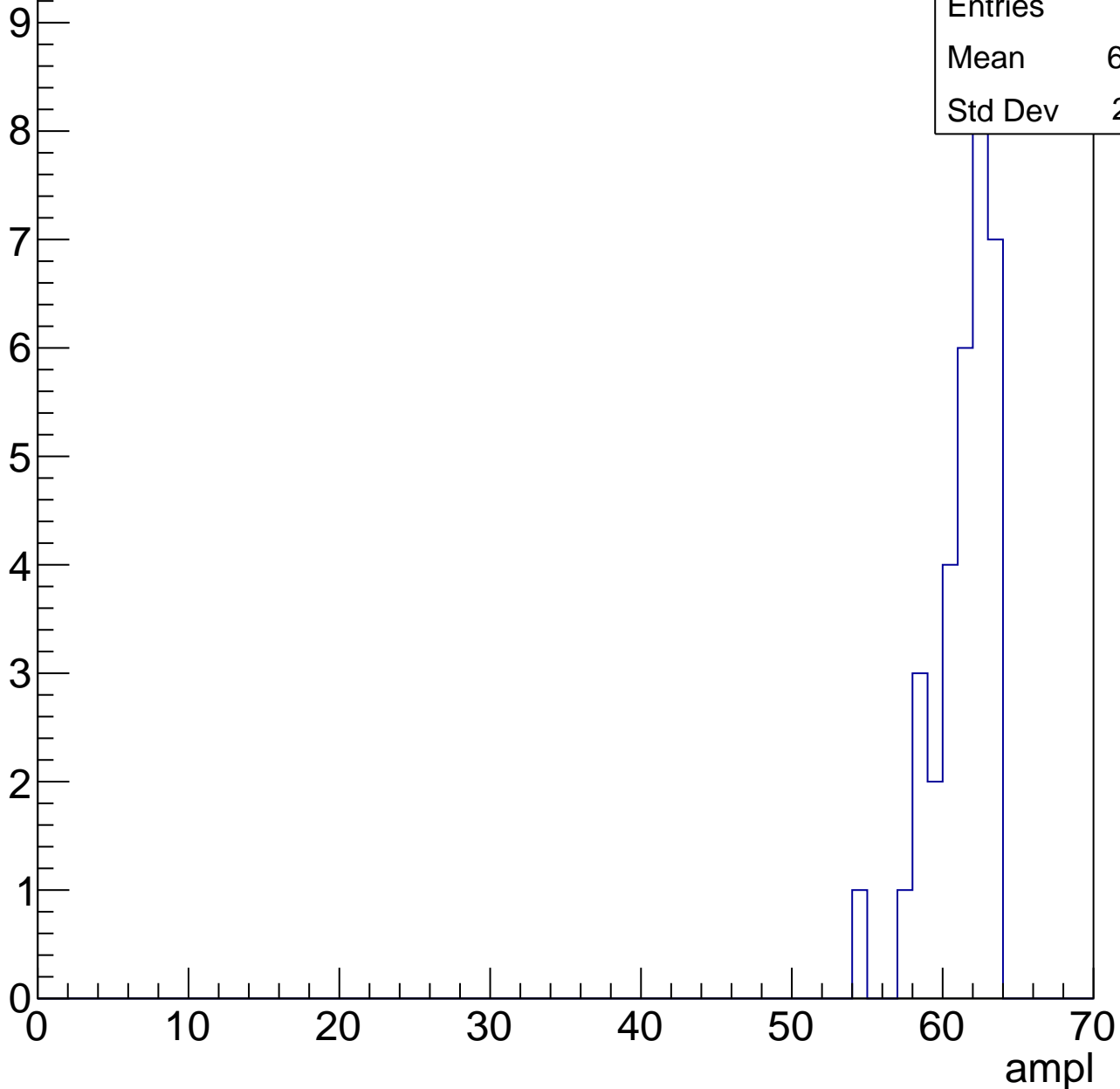


# B1L003S, U23-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	33
Mean	60.85
Std Dev	2.061



# B1L003S, U23-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch74, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	66
Mean	28.86
Std Dev	7.757

**Gaus mean : 30.8012**

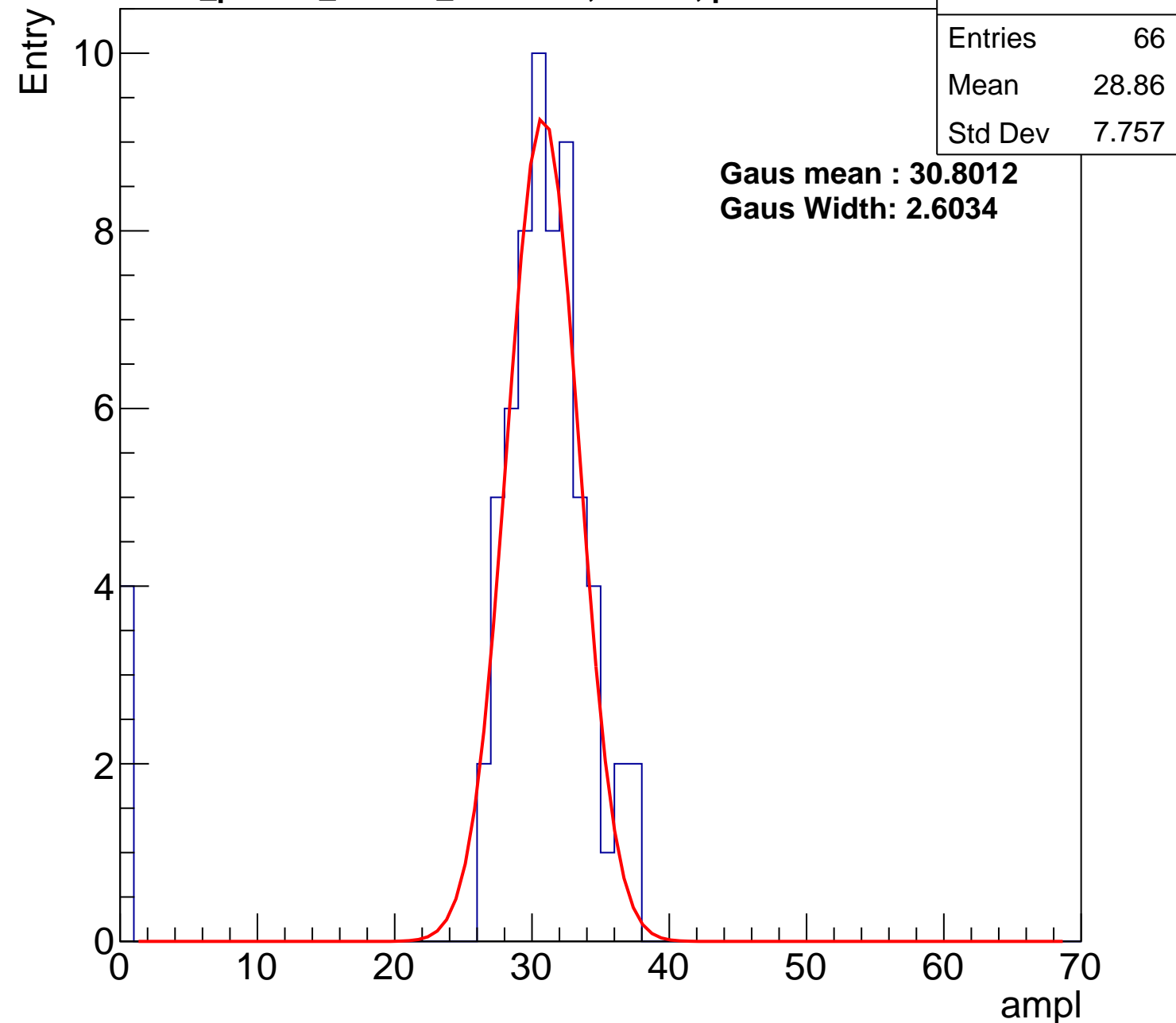
**Gaus Width: 2.6034**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch74, adc1

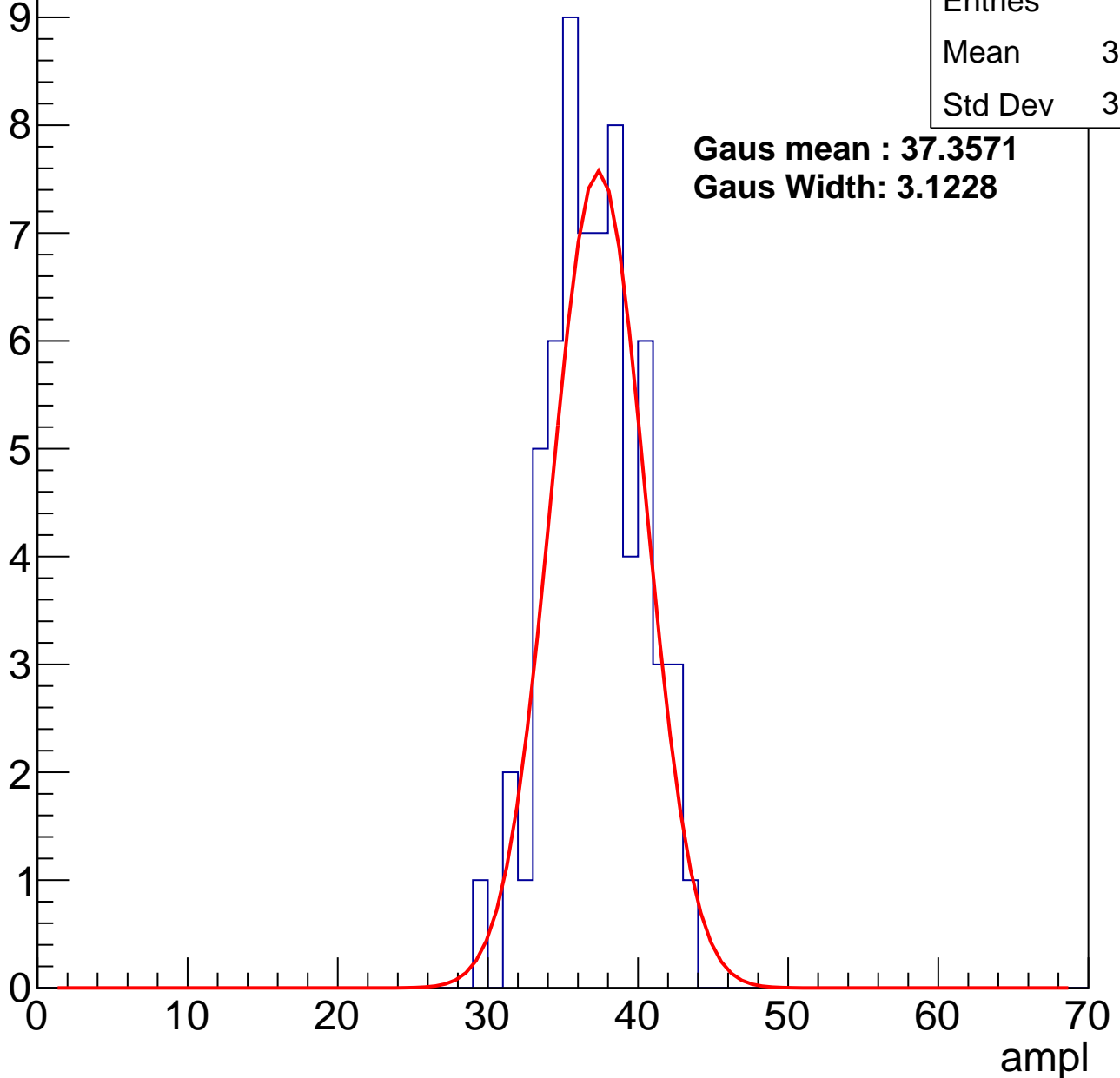
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	36.67
Std Dev	3.003

**Gaus mean : 37.3571**

**Gaus Width: 3.1228**



# B1L003S, U23-ch74, adc2

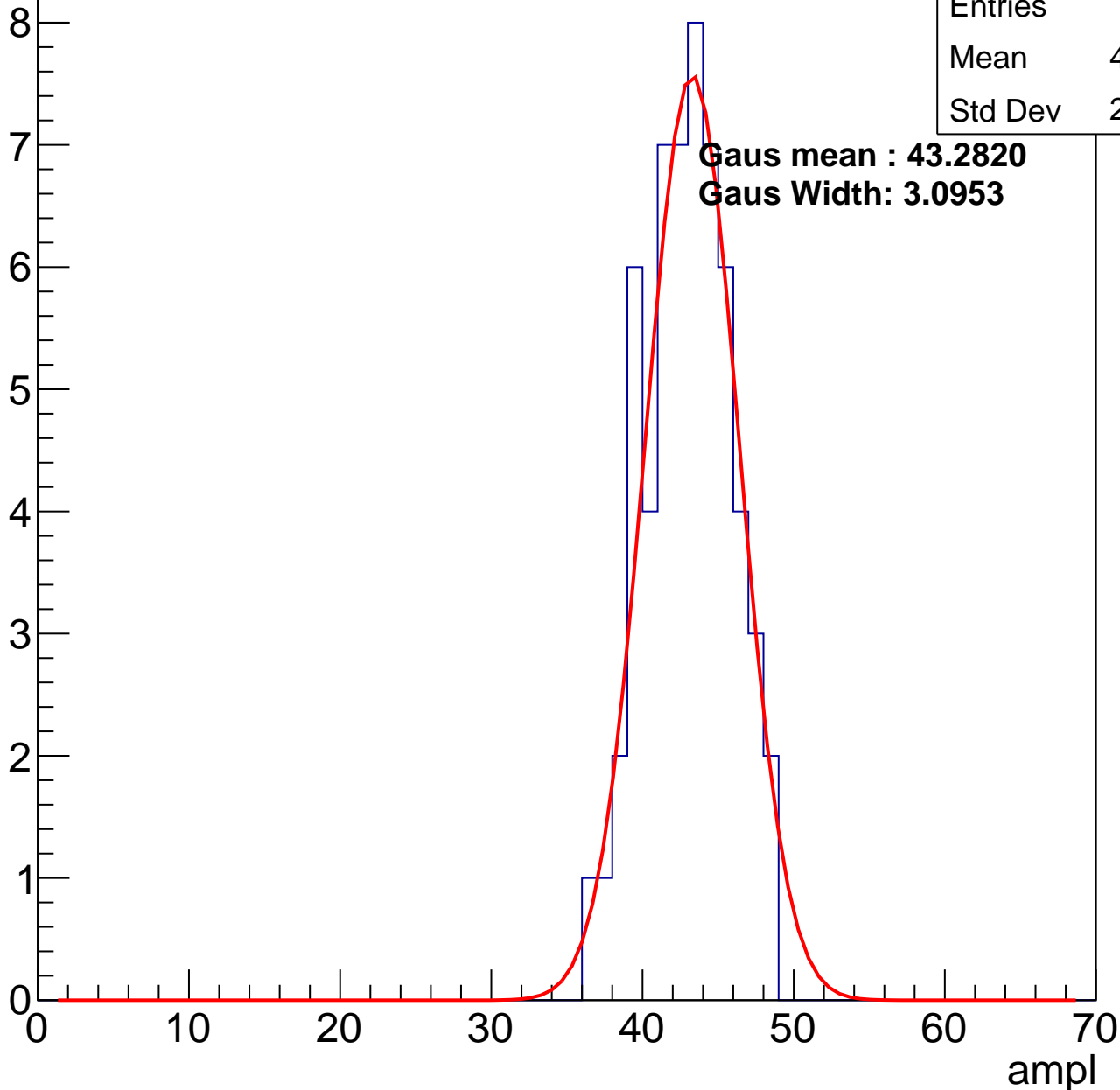
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.53
Std Dev	2.799

**Gaus mean : 43.2820**

**Gaus Width: 3.0953**



# B1L003S, U23-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

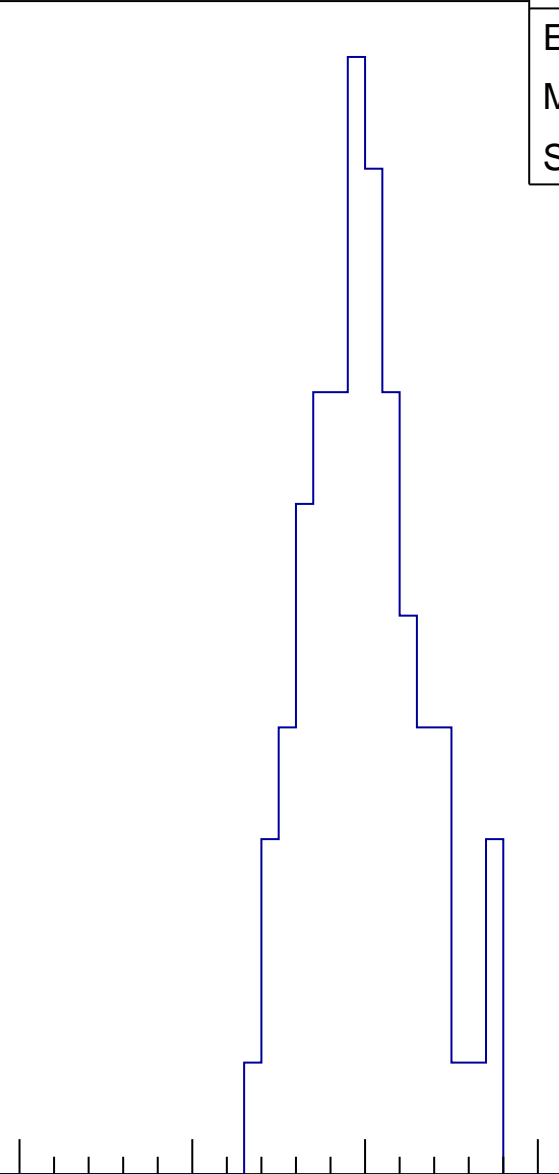
Entries	72
Mean	49.49
Std Dev	3.27

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

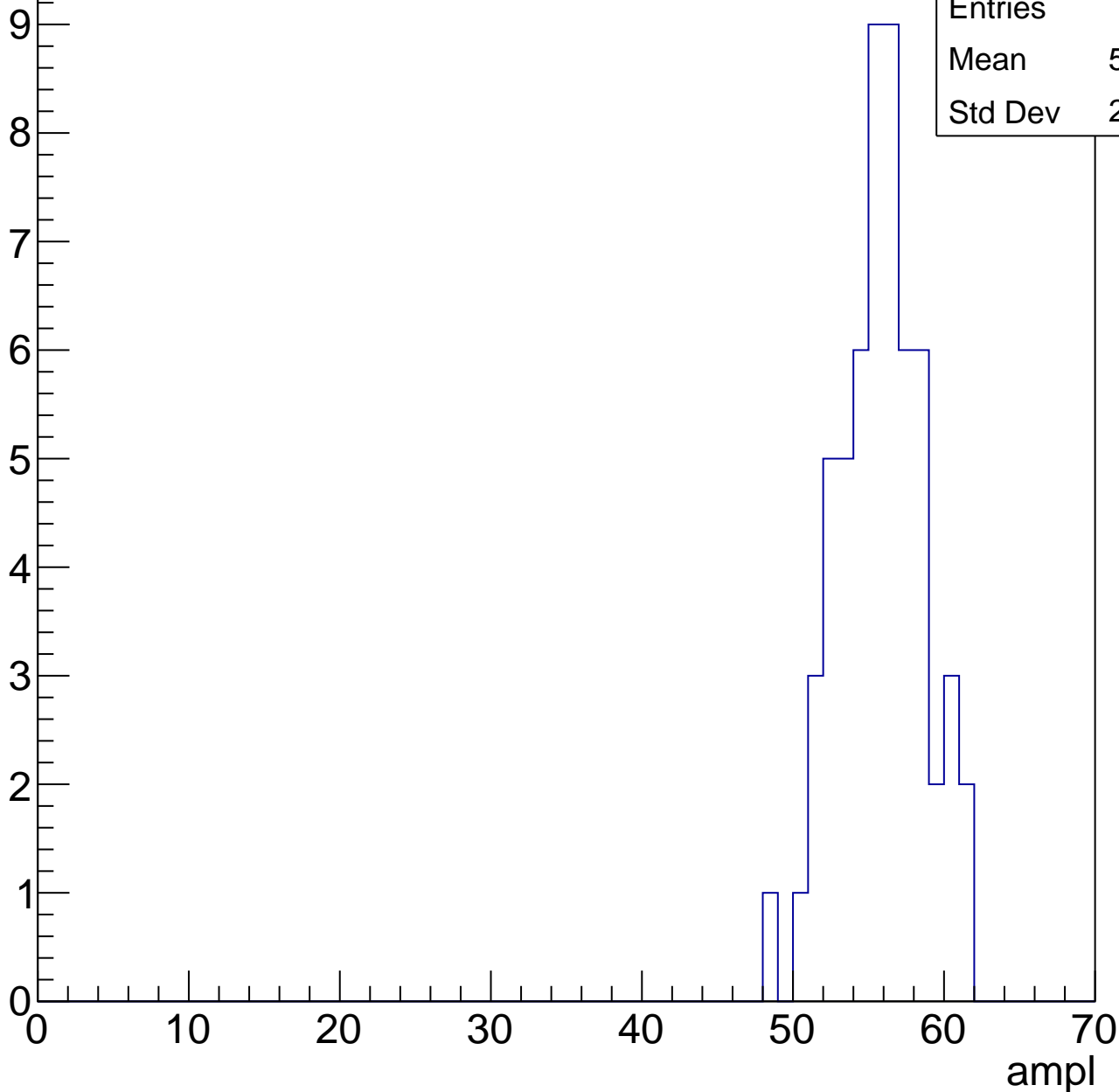


# B1L003S, U23-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

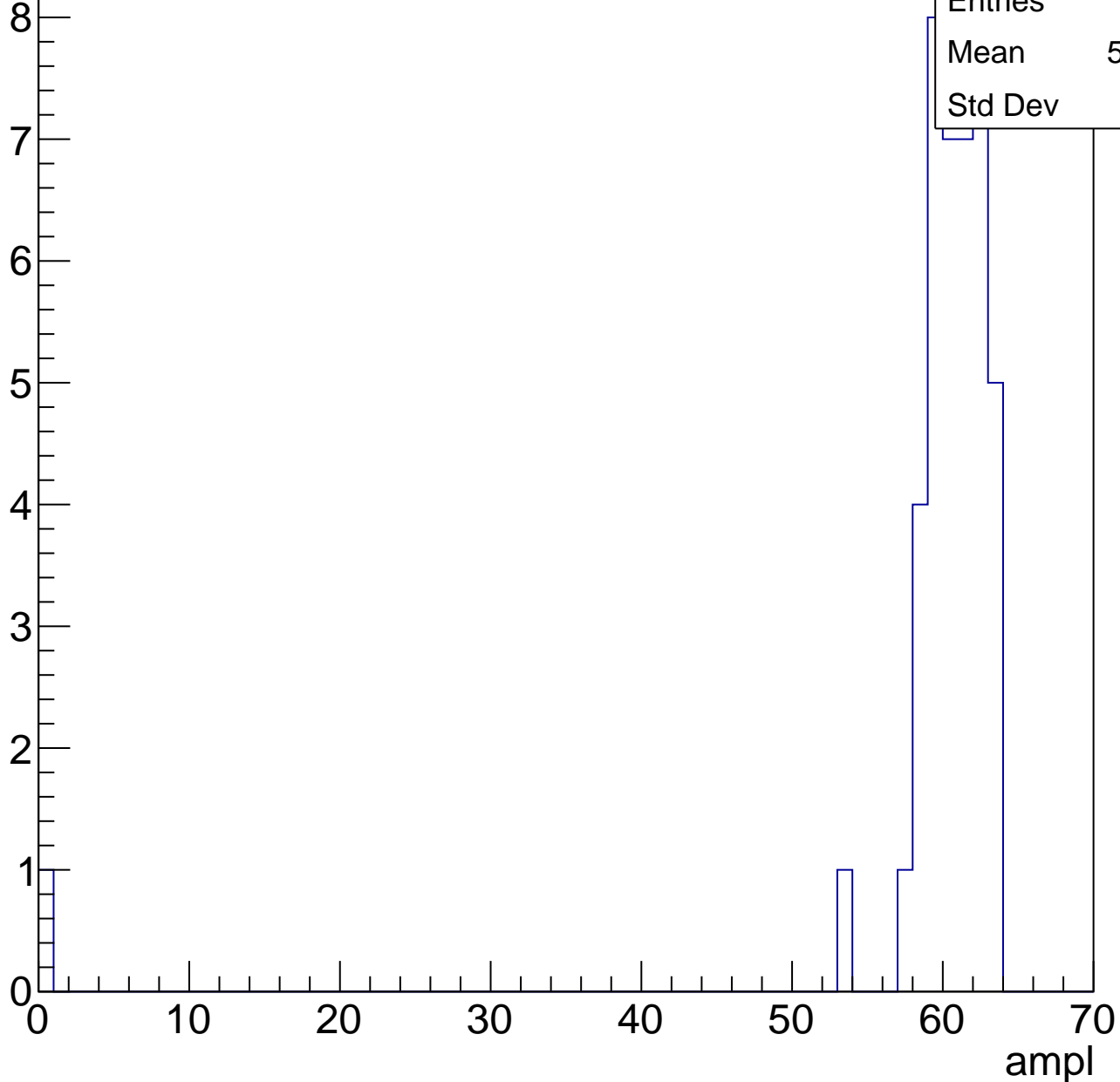
Entries	58
Mean	55.33
Std Dev	2.794



# B1L003S, U23-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

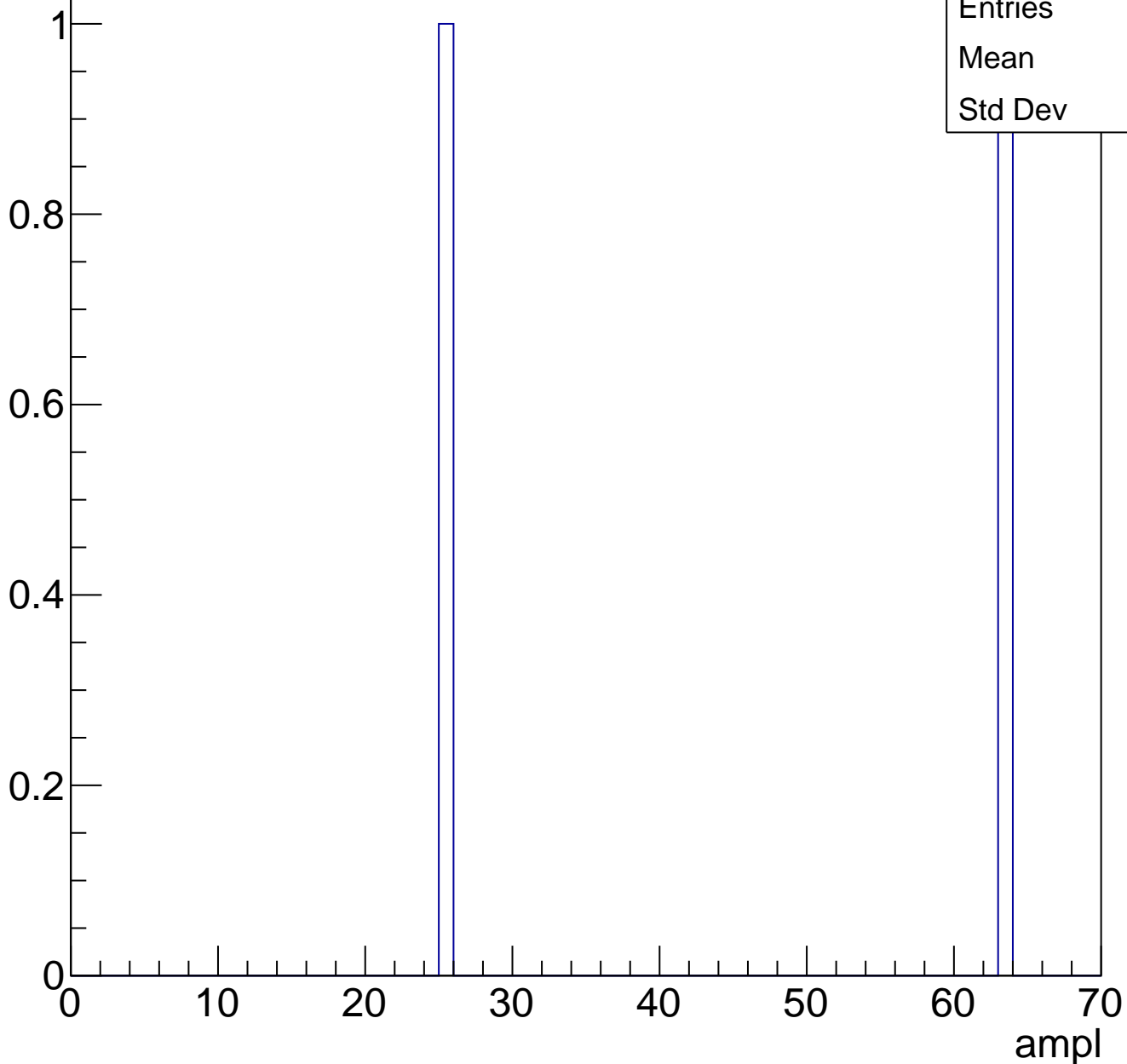




# B1L003S, U23-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	27.49
Std Dev	5.747

**Gaus mean : 28.8612**

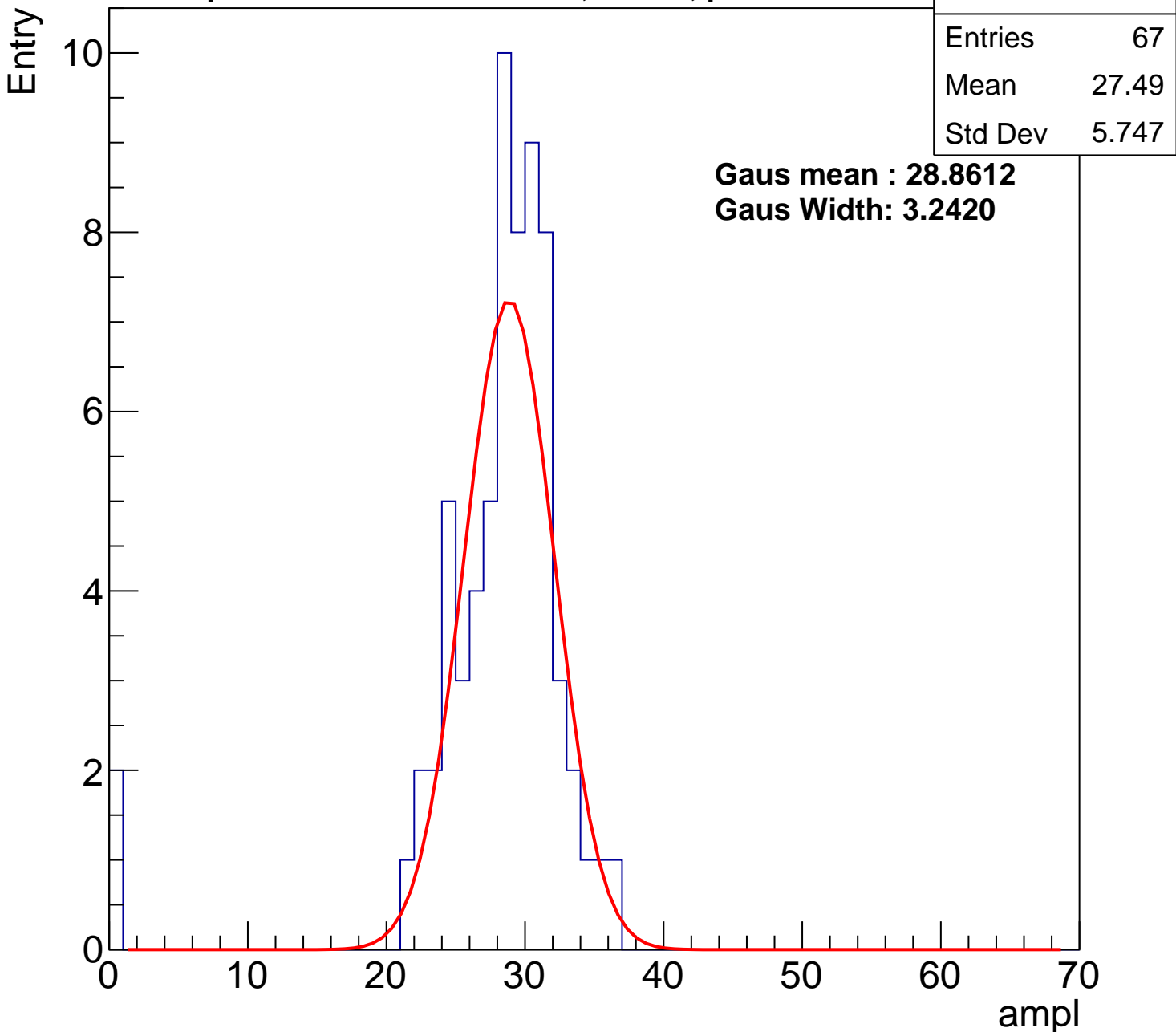
**Gaus Width: 3.2420**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	72
Mean	35.99
Std Dev	2.984

**Gaus mean : 36.3052**

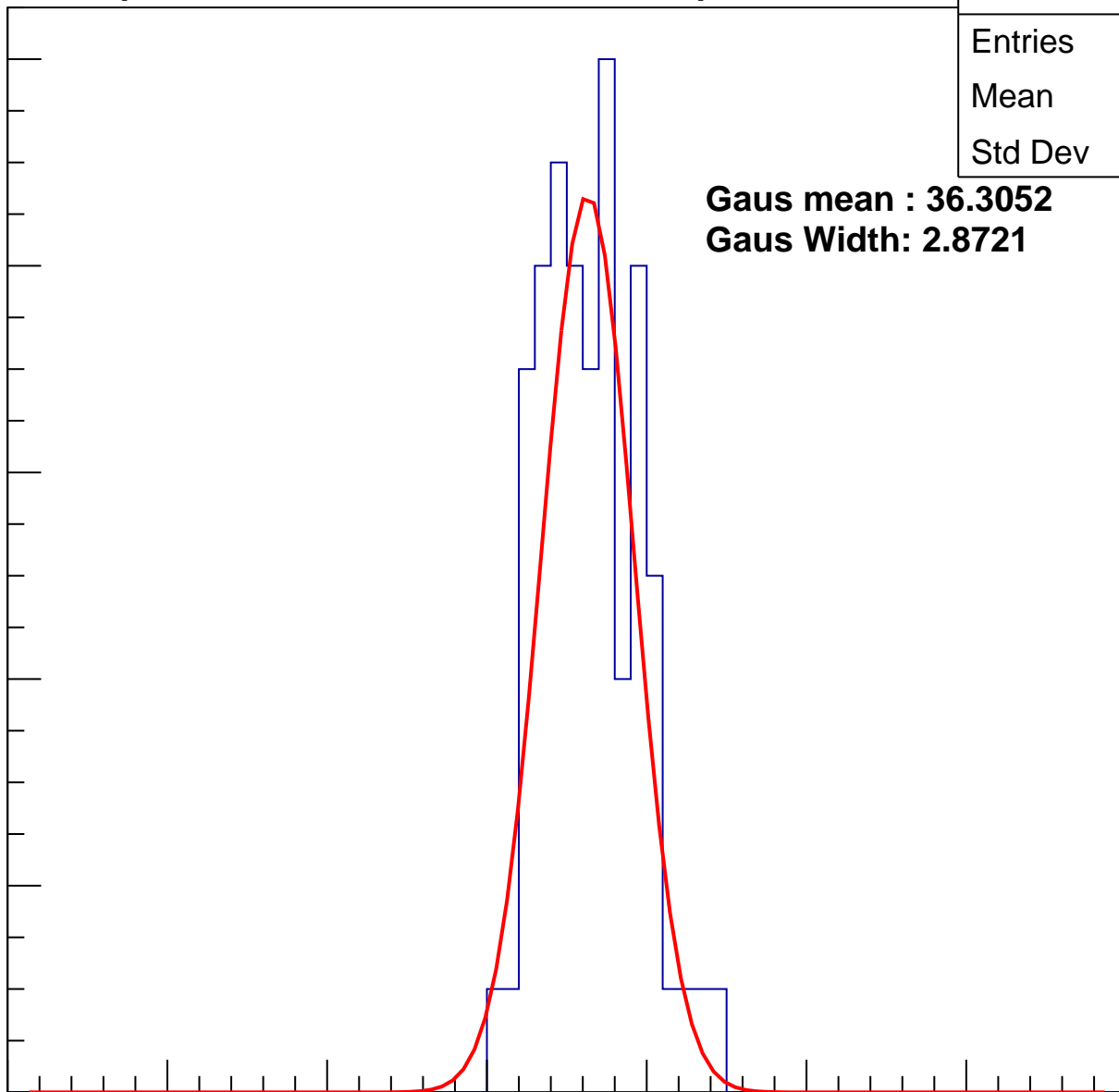
**Gaus Width: 2.8721**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U23-ch75, adc2

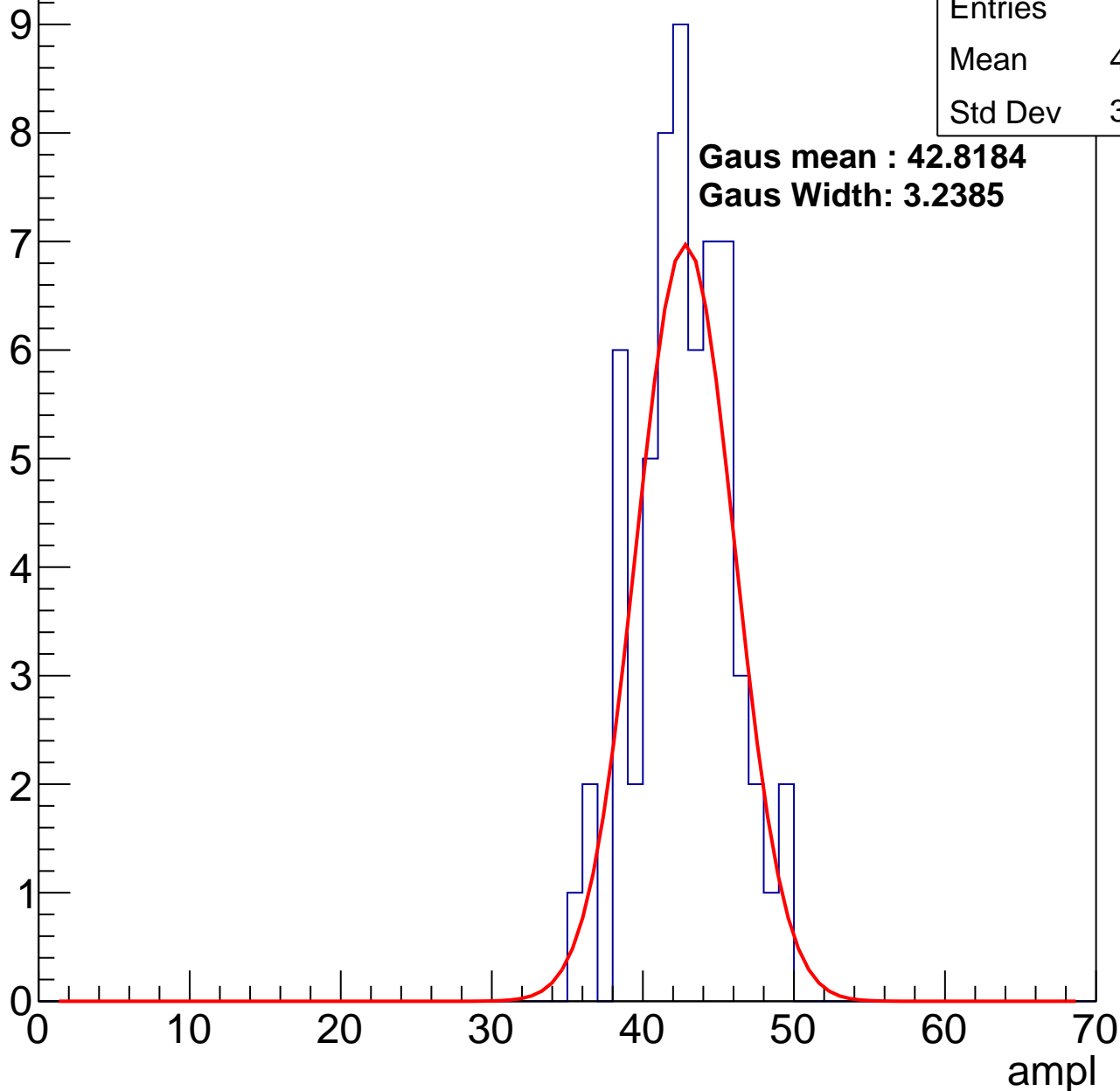
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.26
Std Dev	3.104

**Gaus mean : 42.8184**

**Gaus Width: 3.2385**

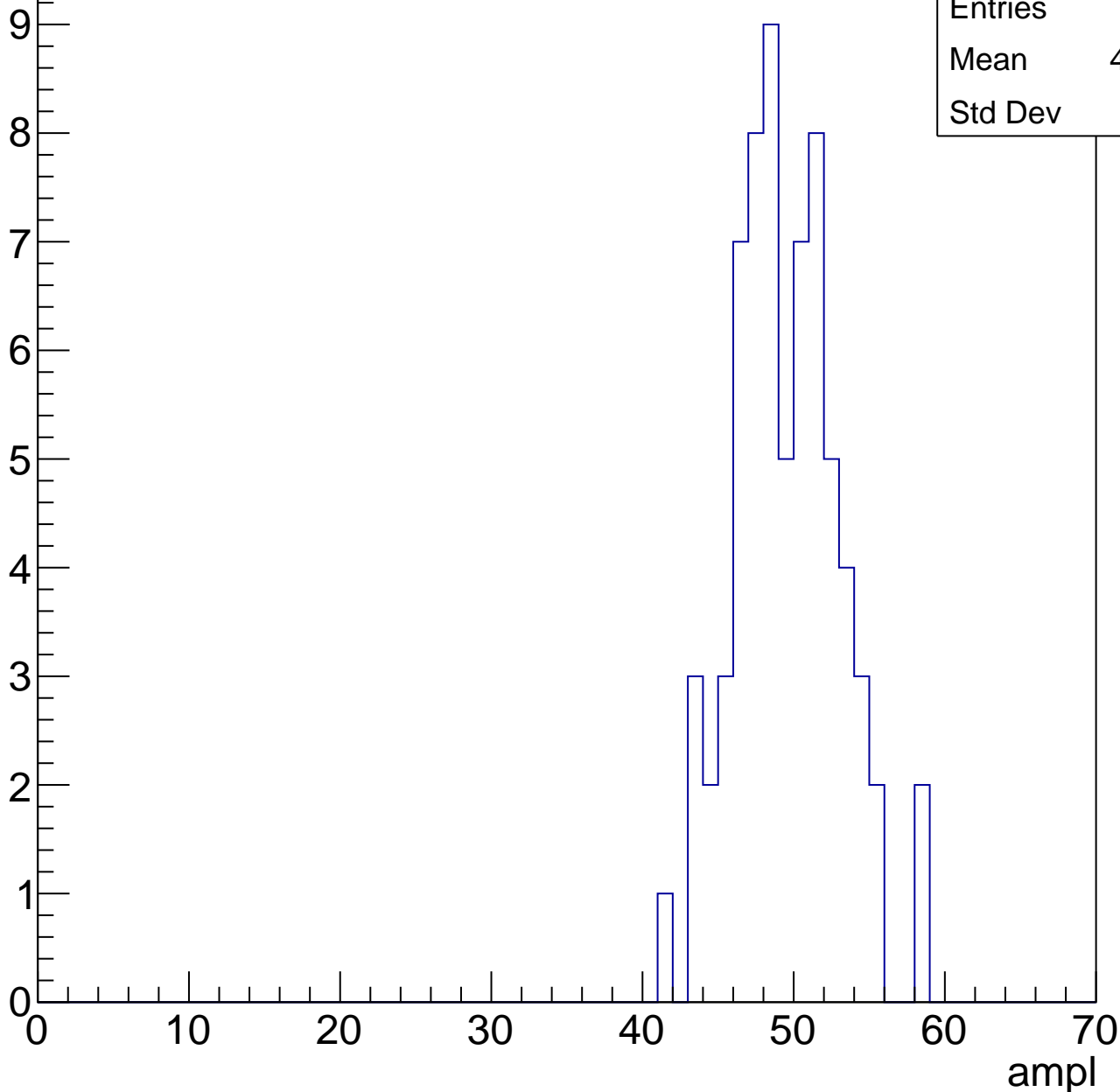


# B1L003S, U23-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

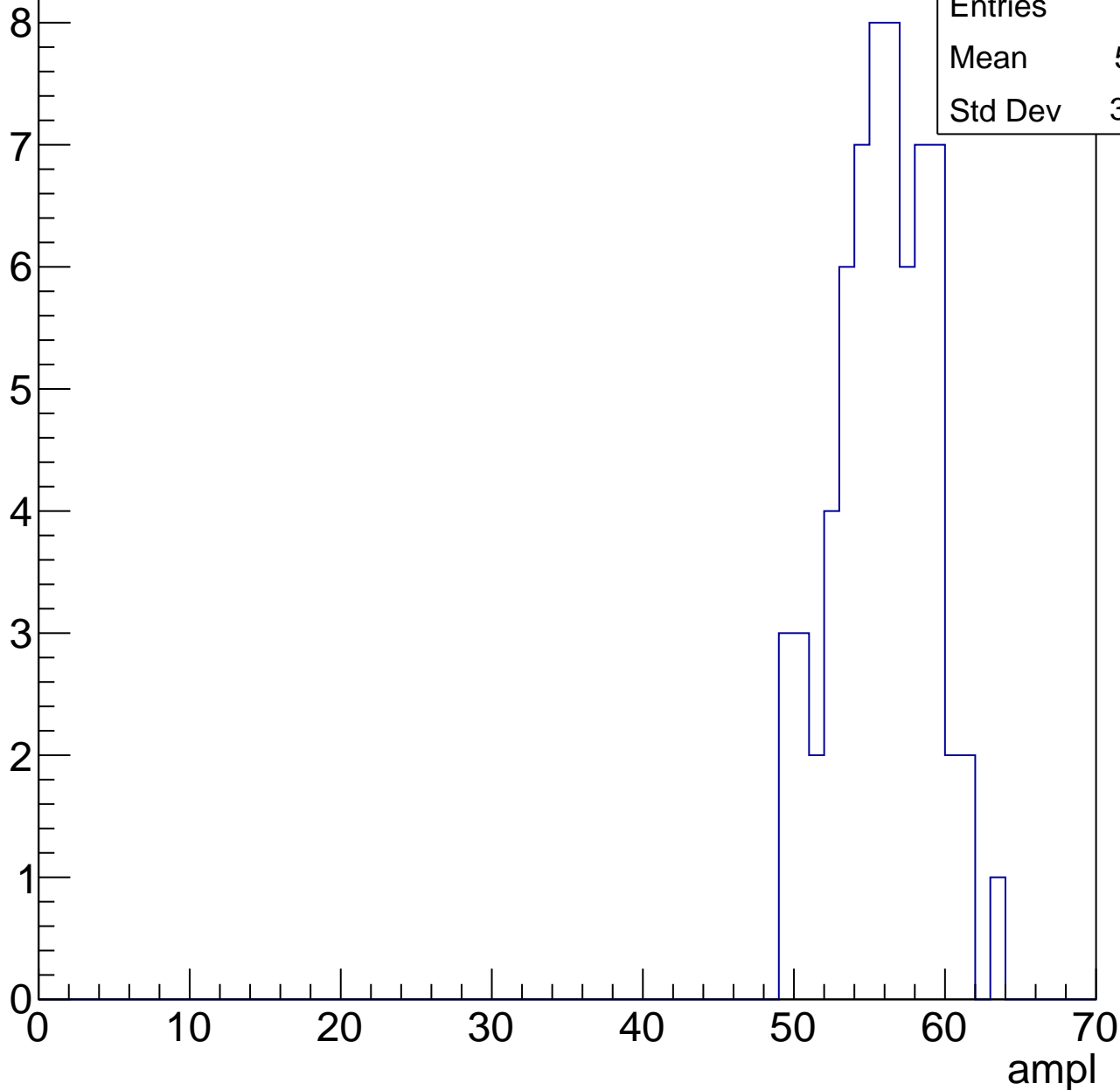
Entries	69
Mean	49.07
Std Dev	3.47



# B1L003S, U23-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

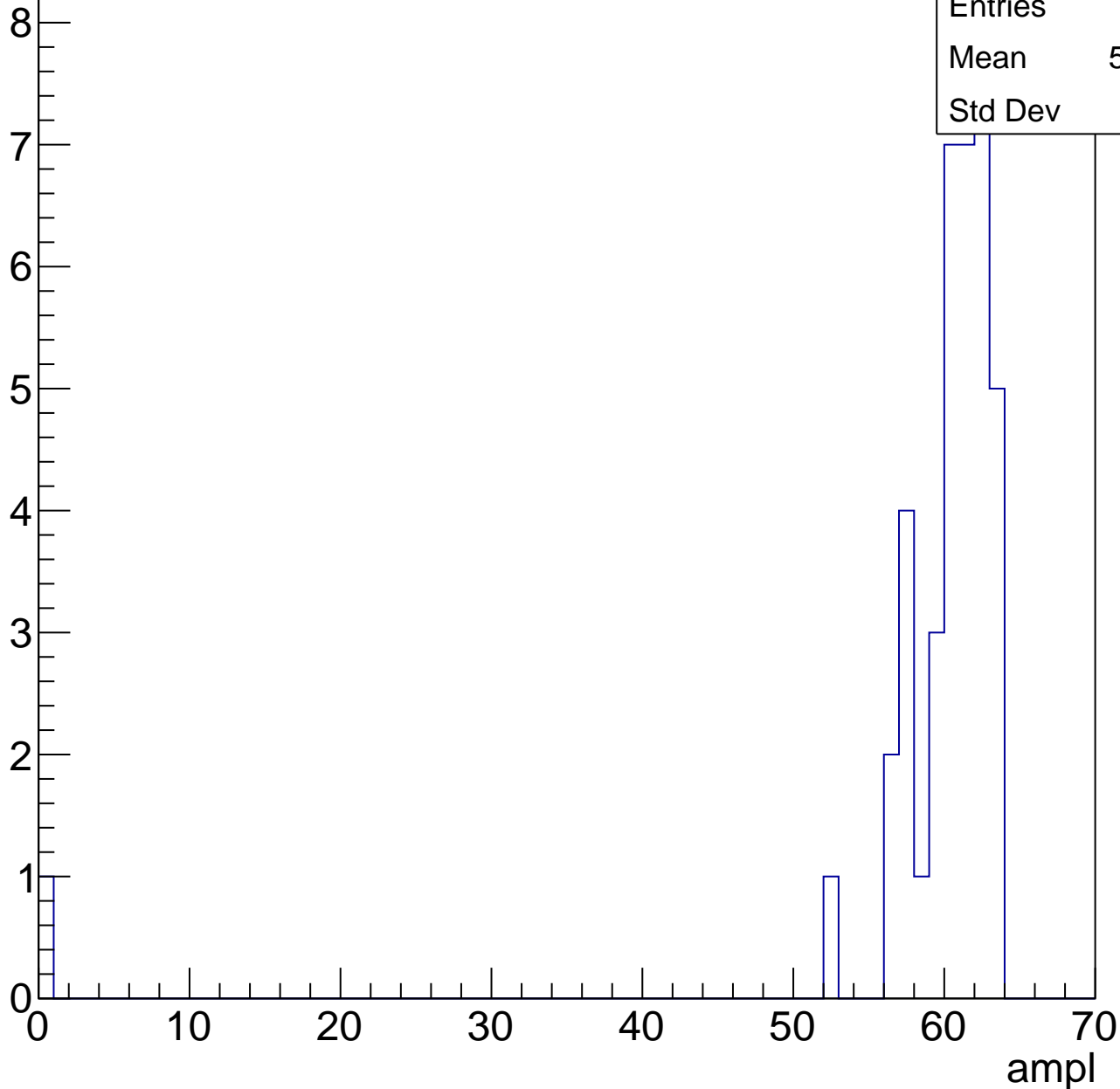


# B1L003S, U23-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

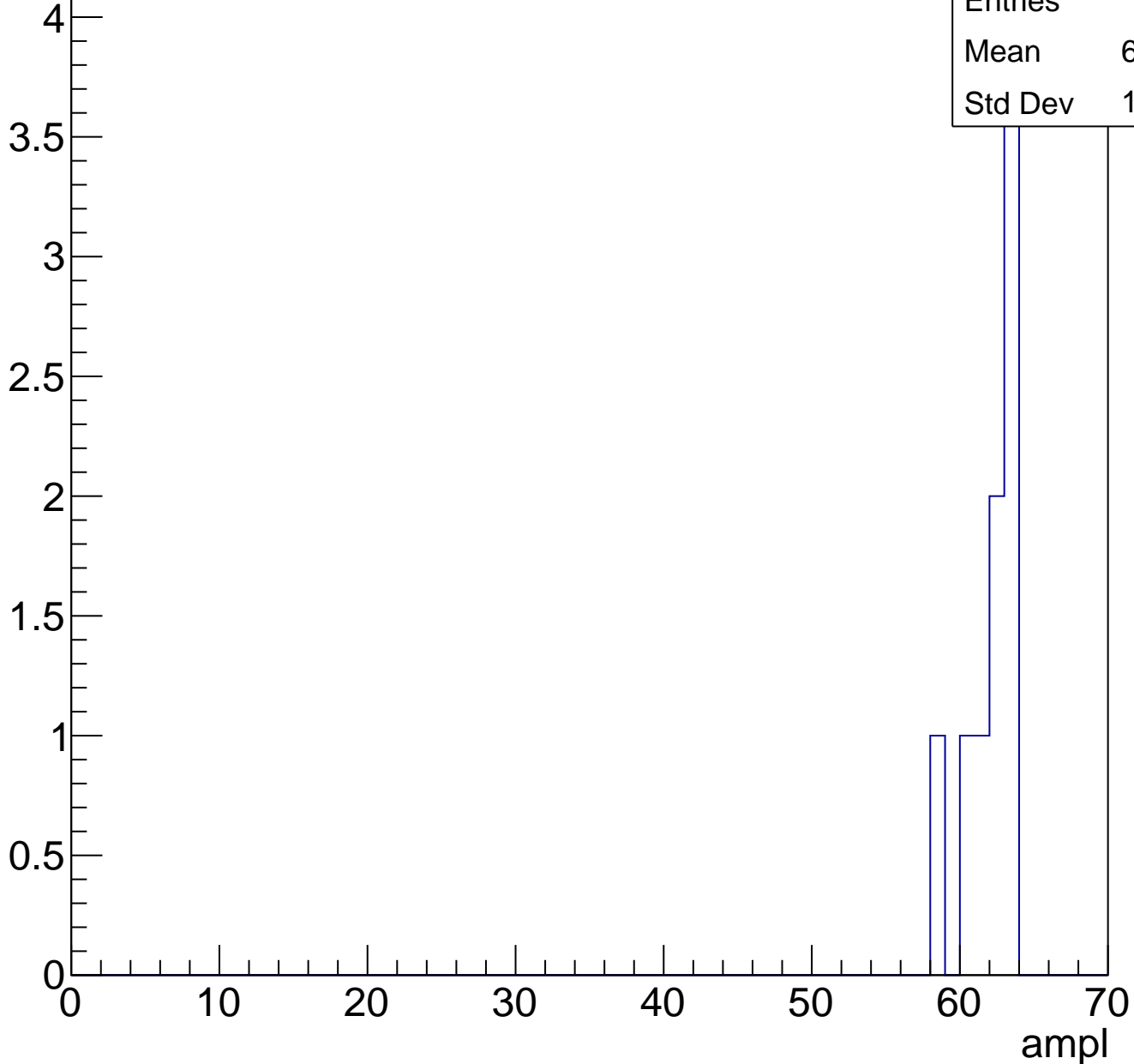
Entries	39
Mean	58.59
Std Dev	9.8



# B1L003S, U23-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch76, adc0

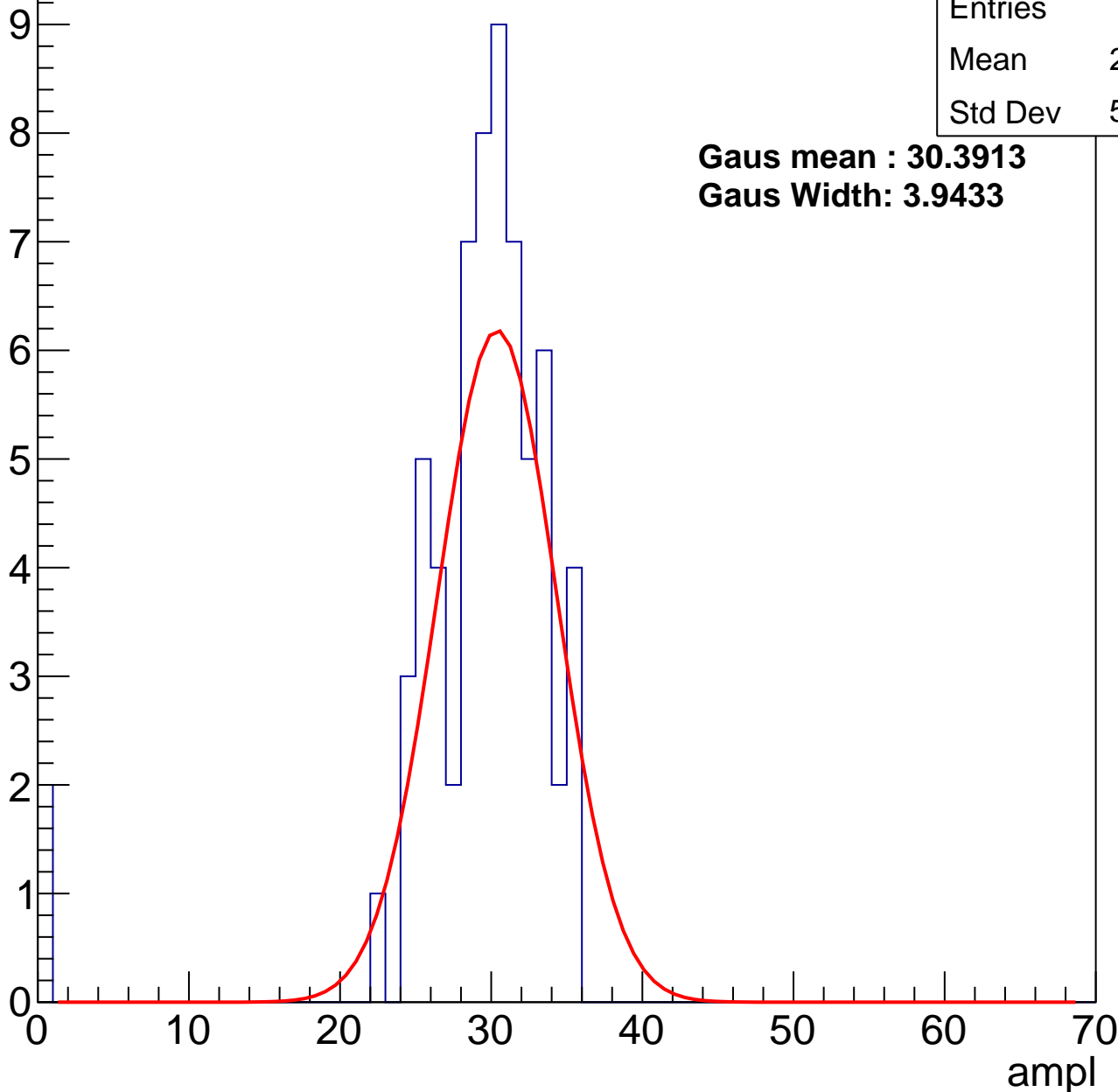
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.58
Std Dev	5.946

**Gaus mean : 30.3913**

**Gaus Width: 3.9433**



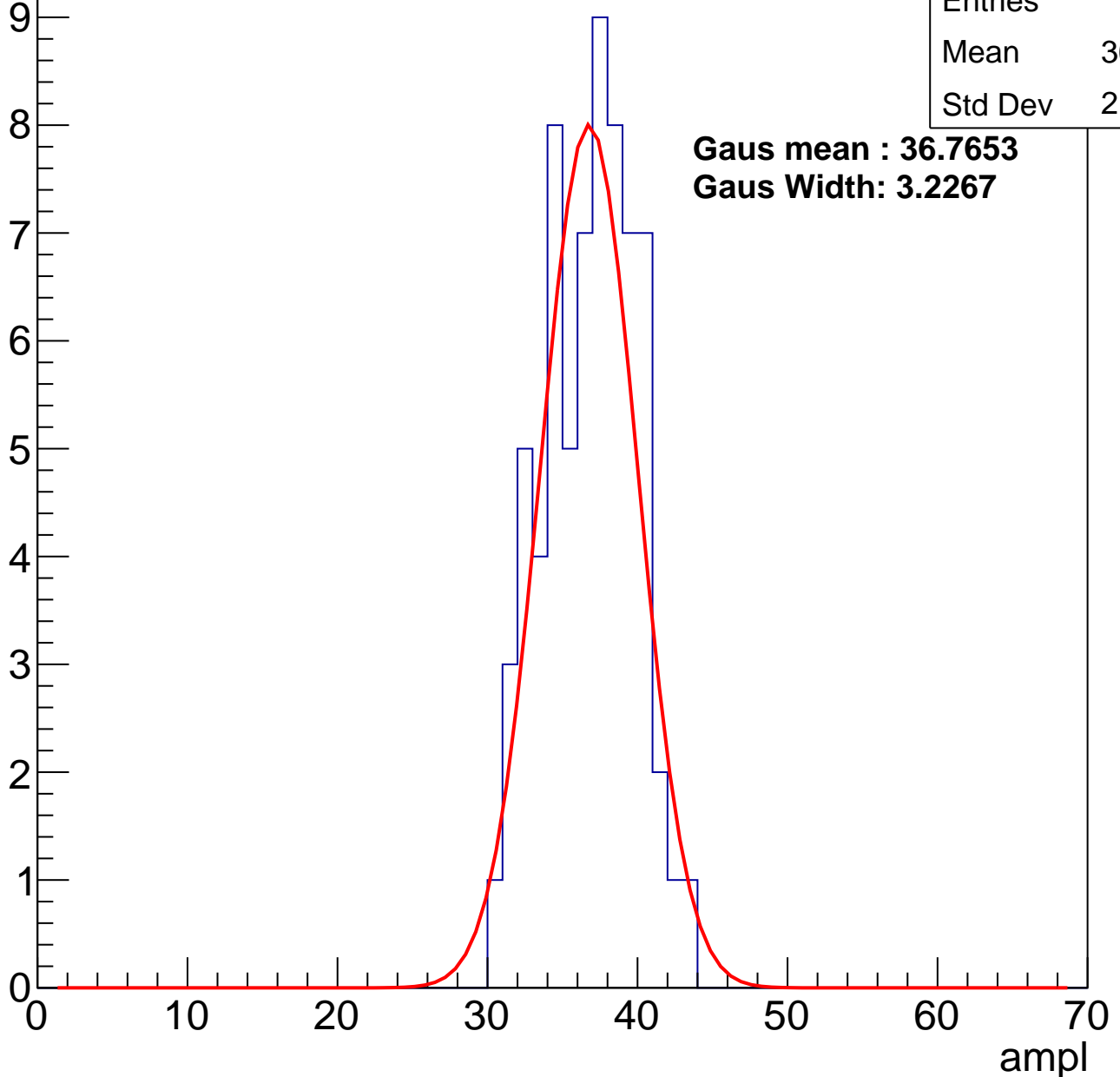
# B1L003S, U23-ch76, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.34
Std Dev	2.988

**Gaus mean : 36.7653**  
**Gaus Width: 3.2267**



# B1L003S, U23-ch76, adc2

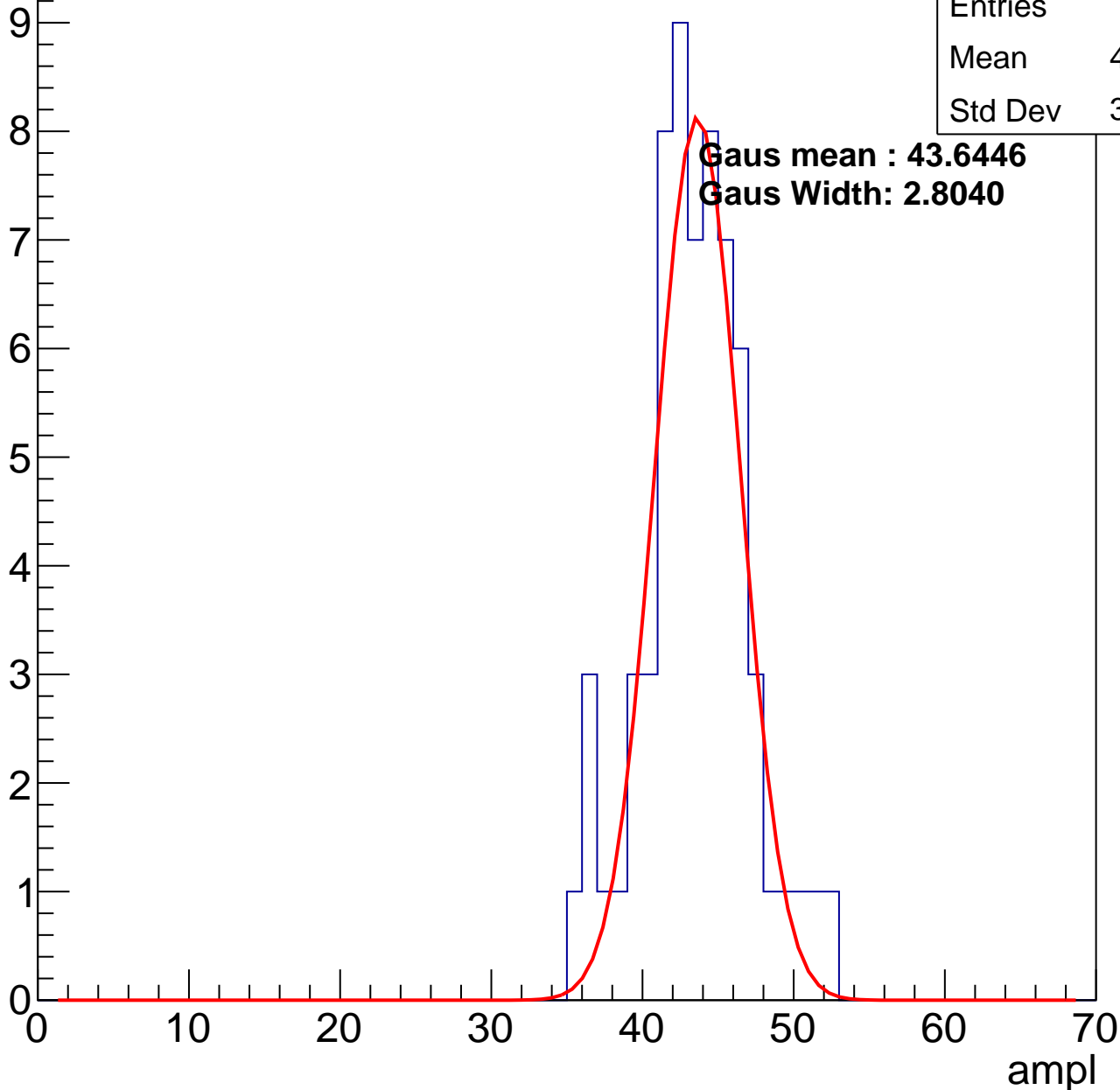
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	43.02
Std Dev	3.466

**Gaus mean : 43.6446**

**Gaus Width: 2.8040**

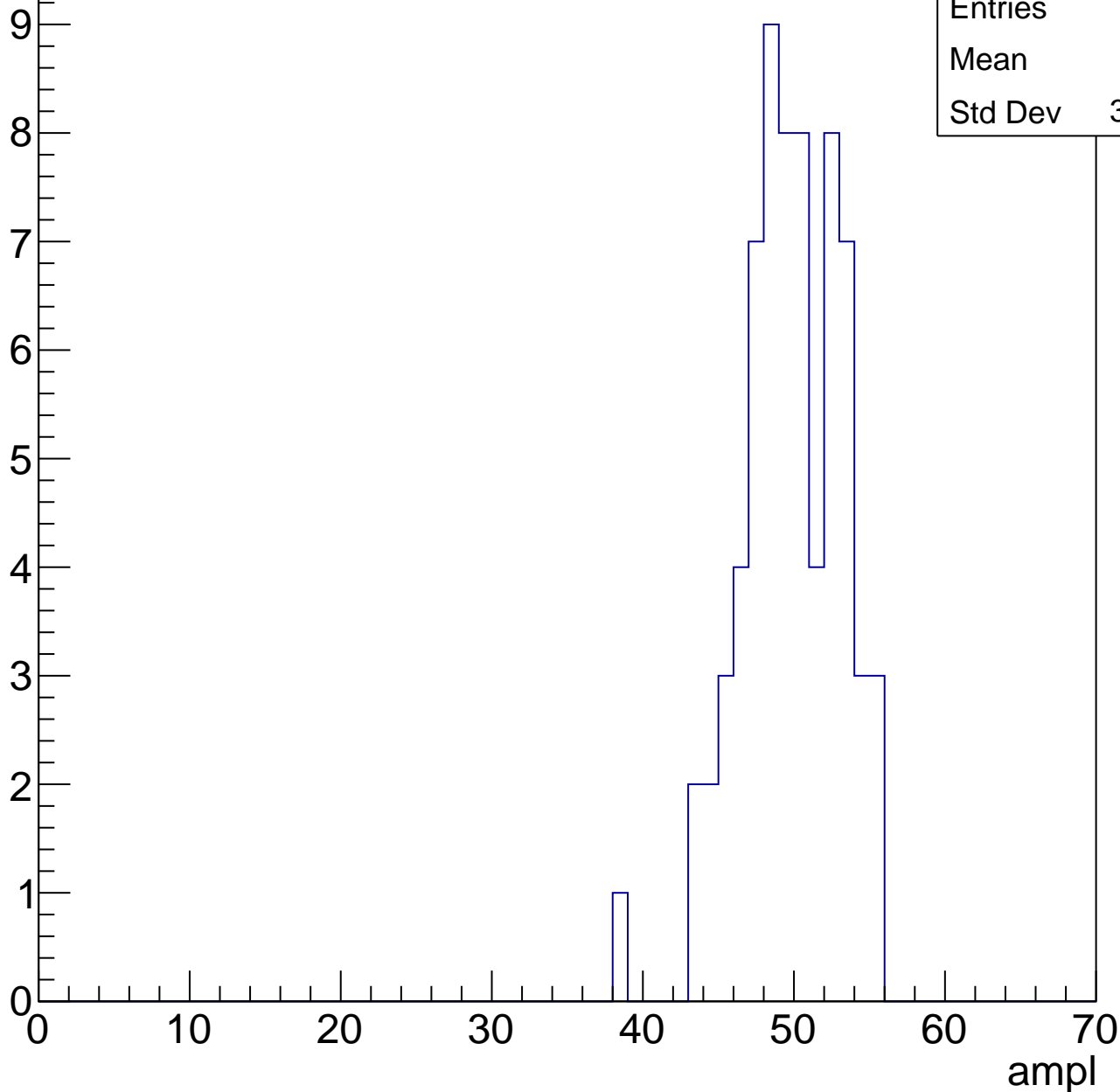


# B1L003S, U23-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.3
Std Dev	3.294

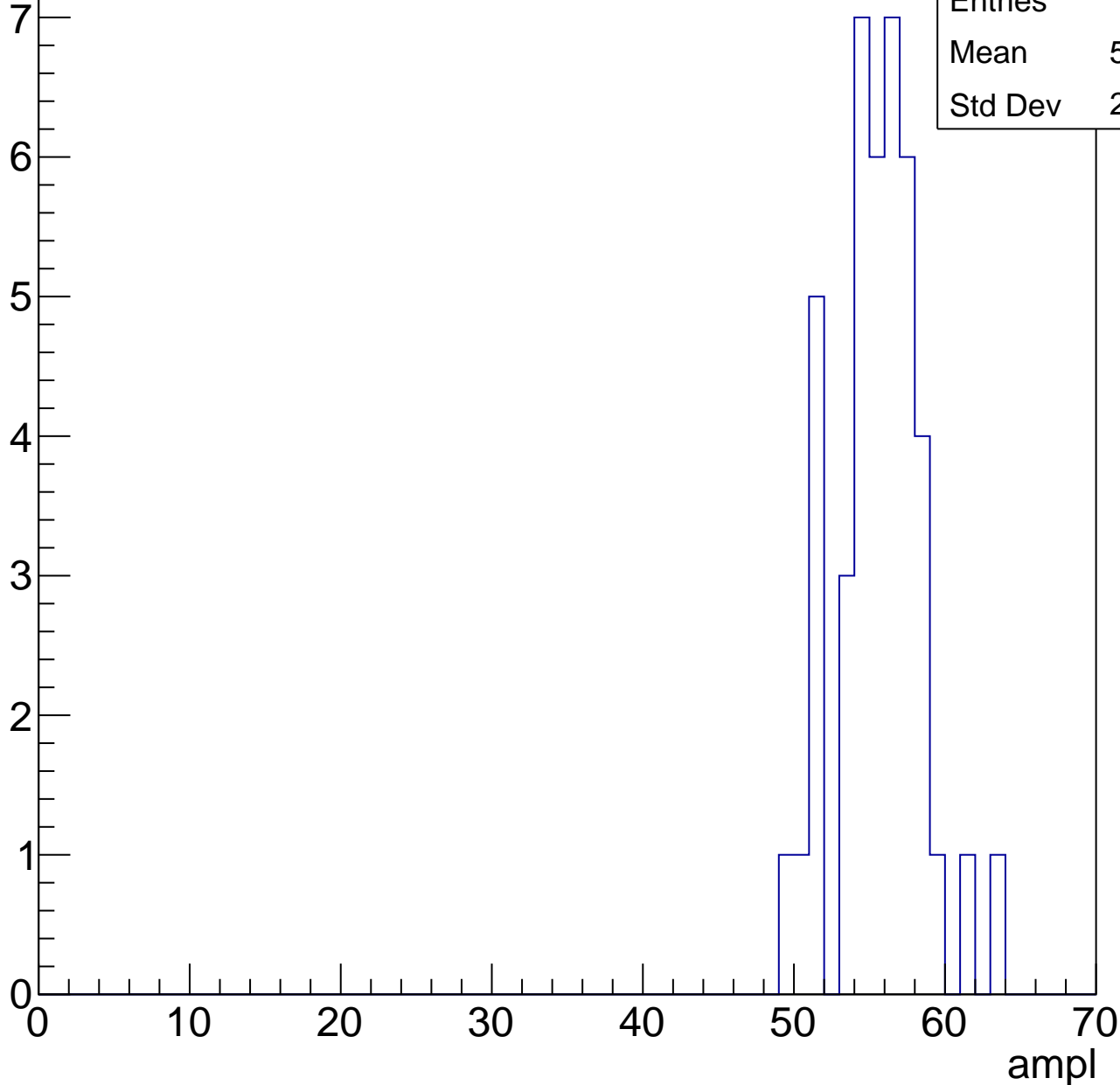


# B1L003S, U23-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	55.12
Std Dev	2.822

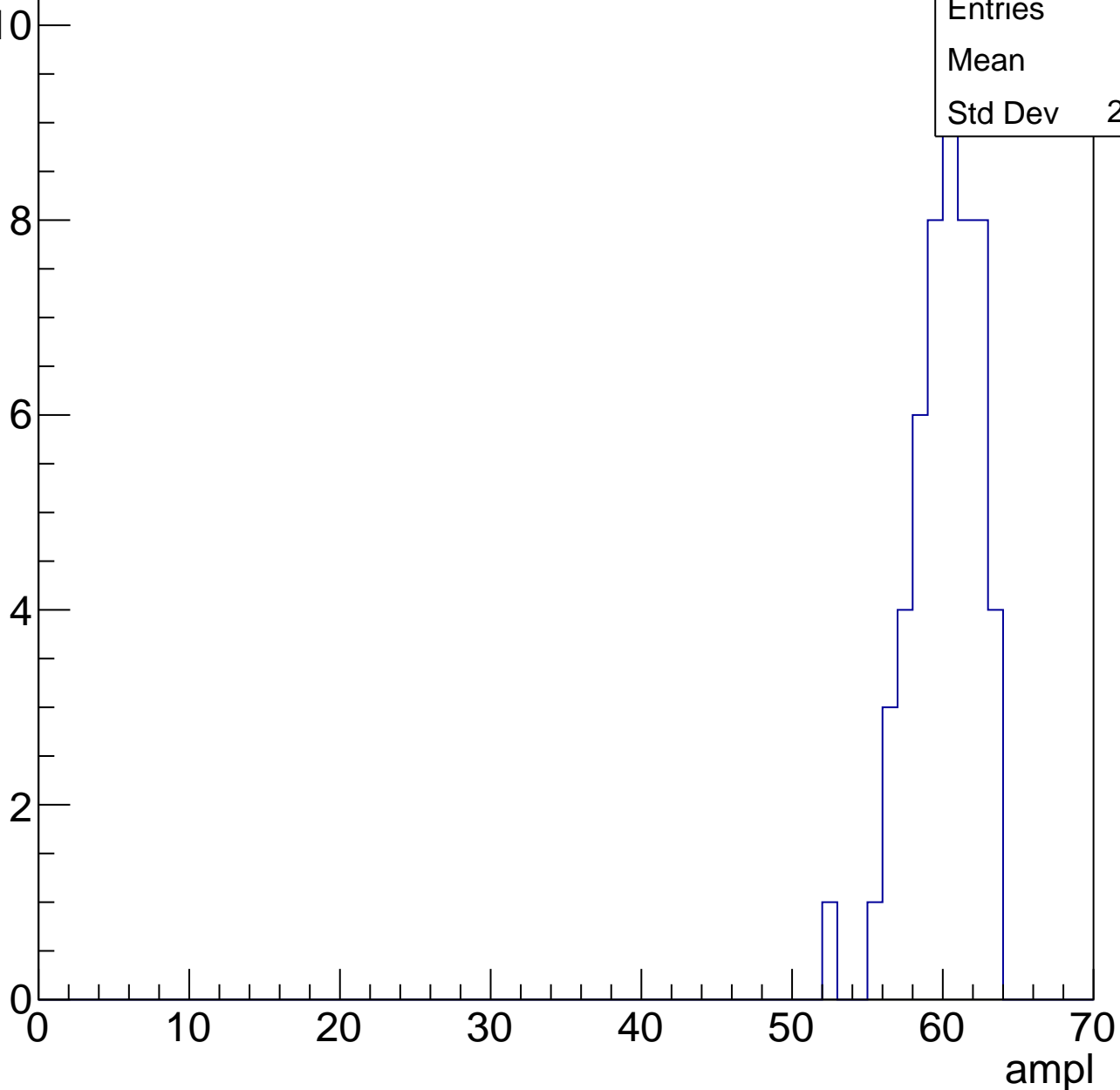


# B1L003S, U23-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	59.6
Std Dev	2.268

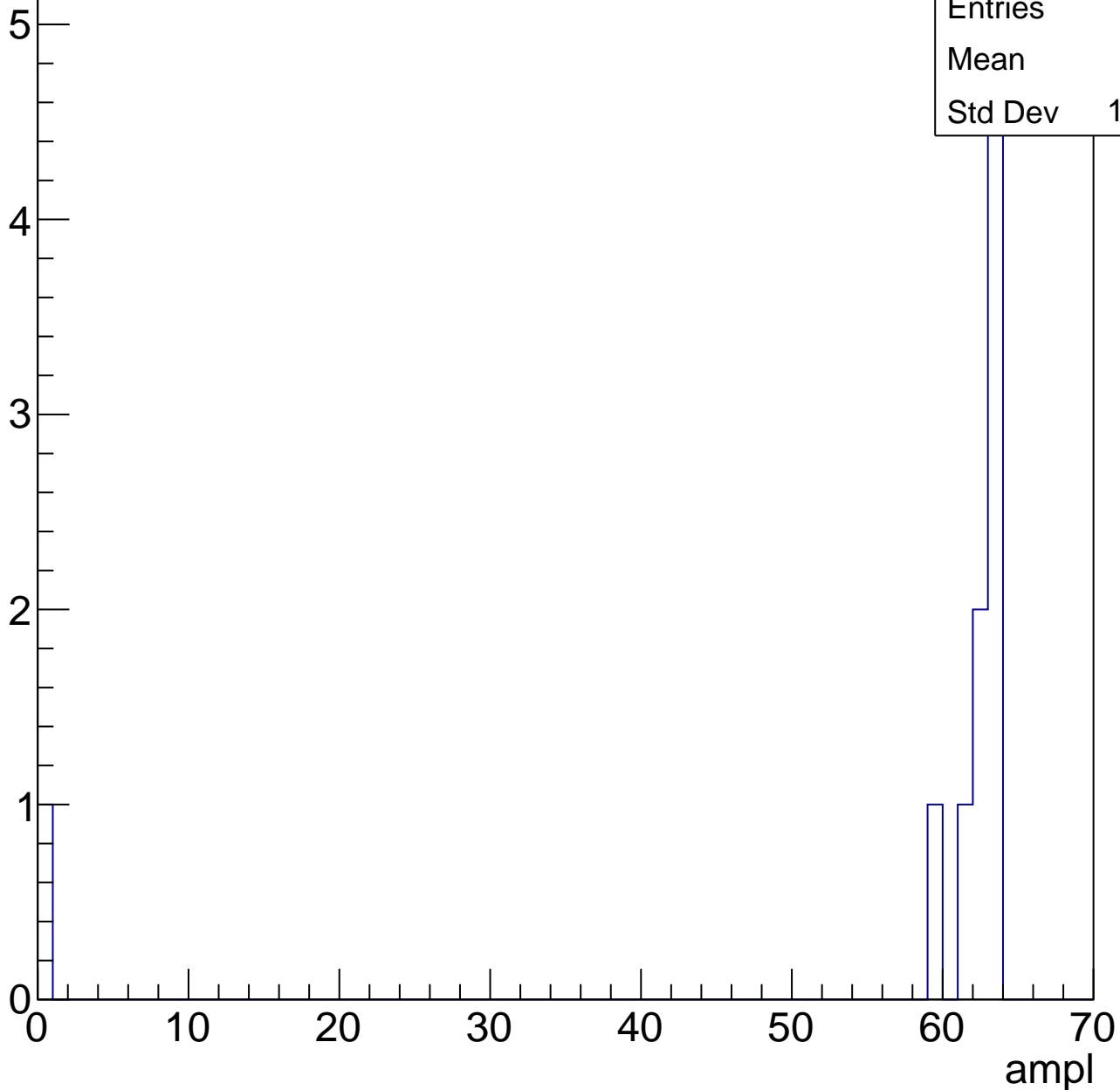


# B1L003S, U23-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	10
Mean	55.9
Std Dev	18.67





# B1L003S, U23-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch77, adc0

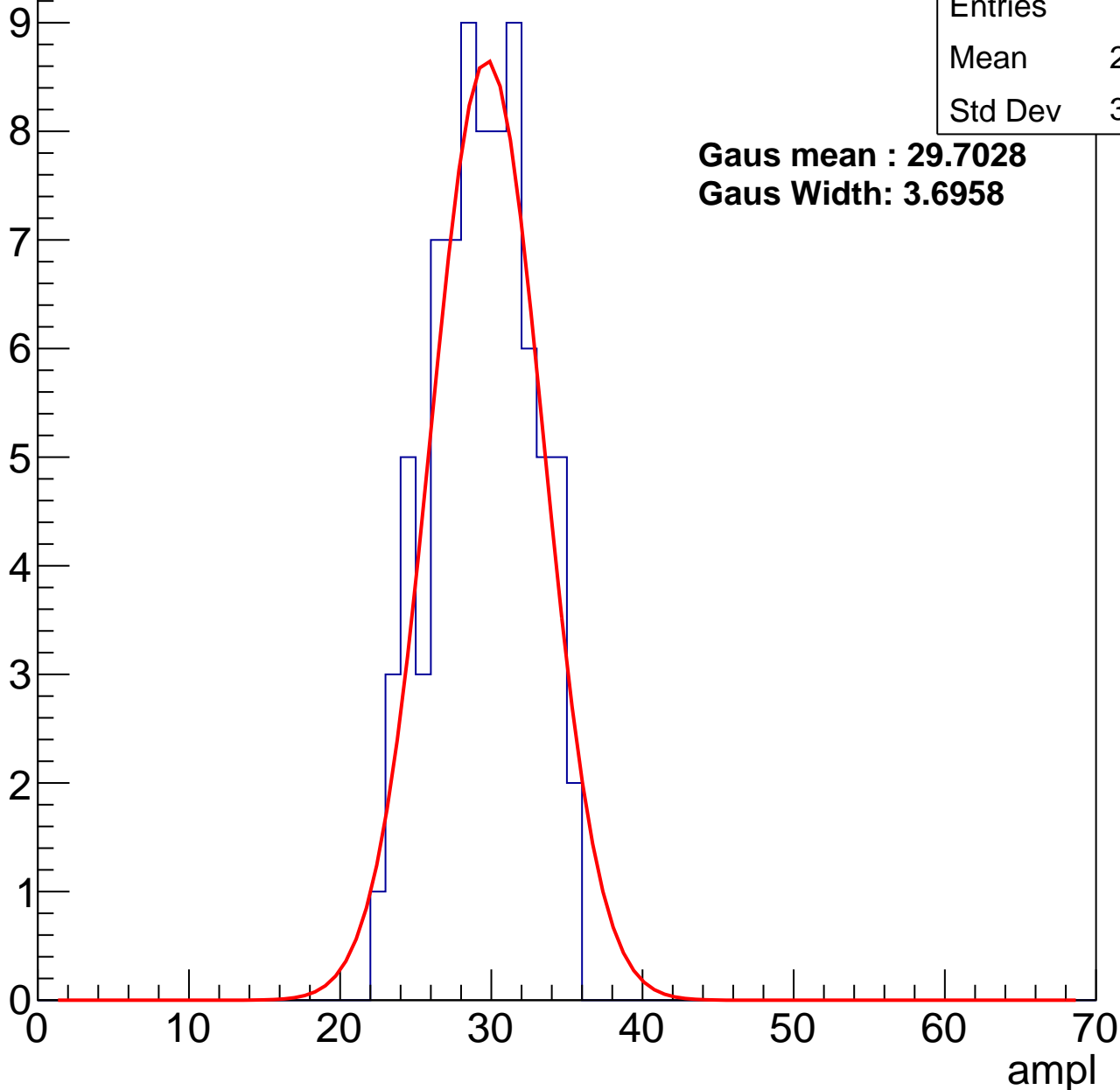
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	28.94
Std Dev	3.212

**Gaus mean : 29.7028**

**Gaus Width: 3.6958**



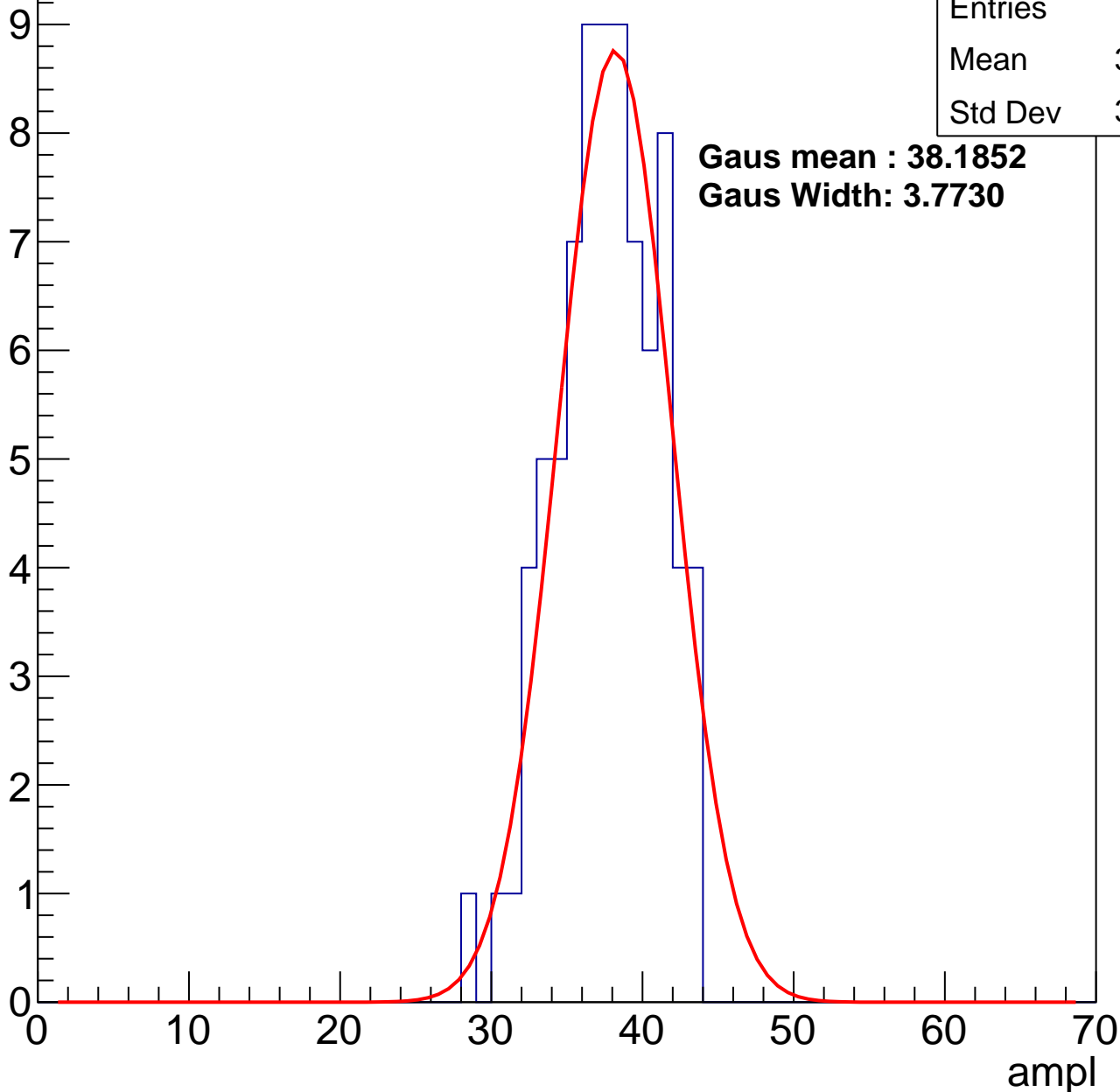
# B1L003S, U23-ch77, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	37.21
Std Dev	3.331

**Gaus mean : 38.1852**  
**Gaus Width: 3.7730**



# B1L003S, U23-ch77, adc2

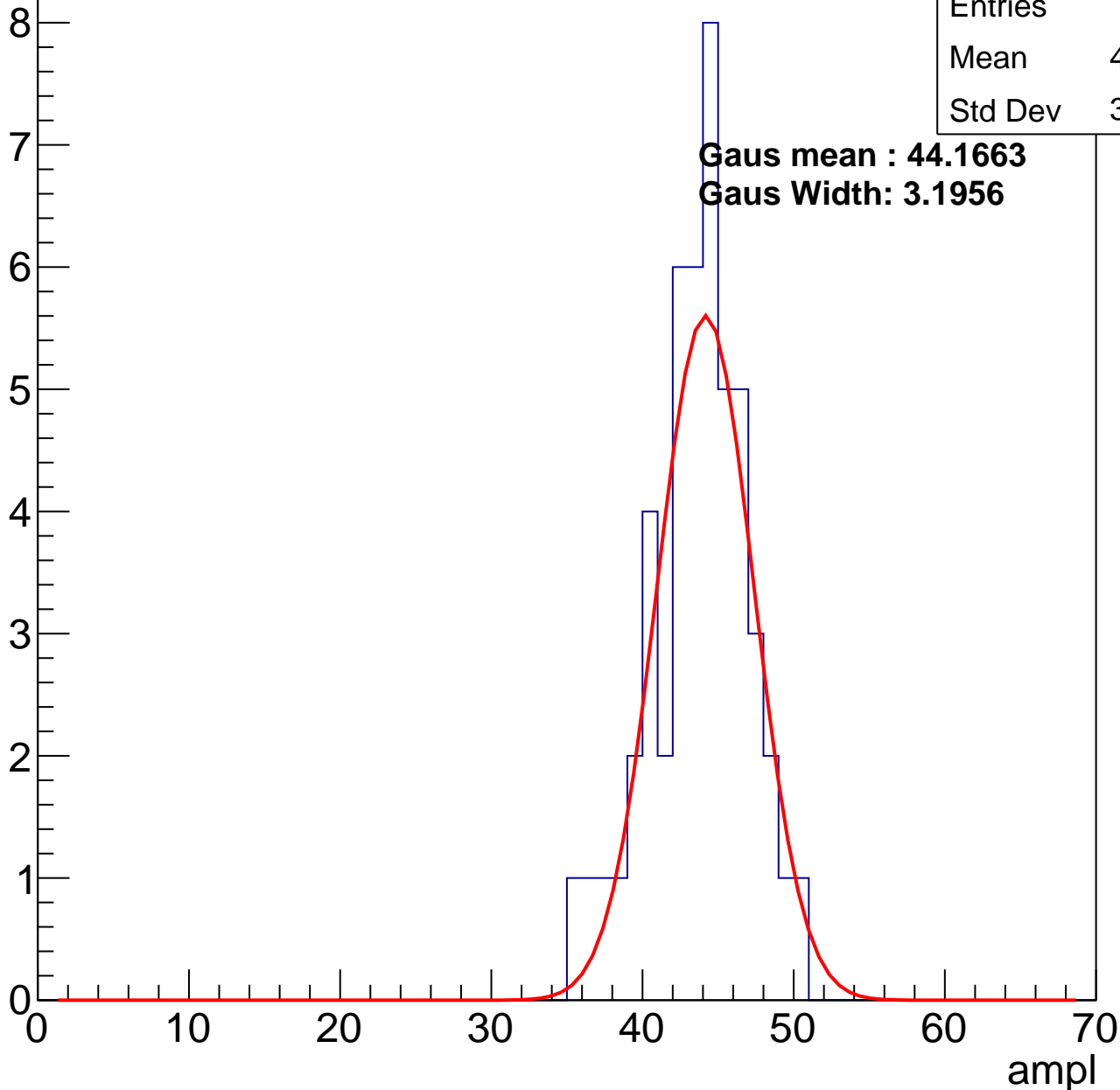
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	43.24
Std Dev	3.223

**Gaus mean : 44.1663**

**Gaus Width: 3.1956**

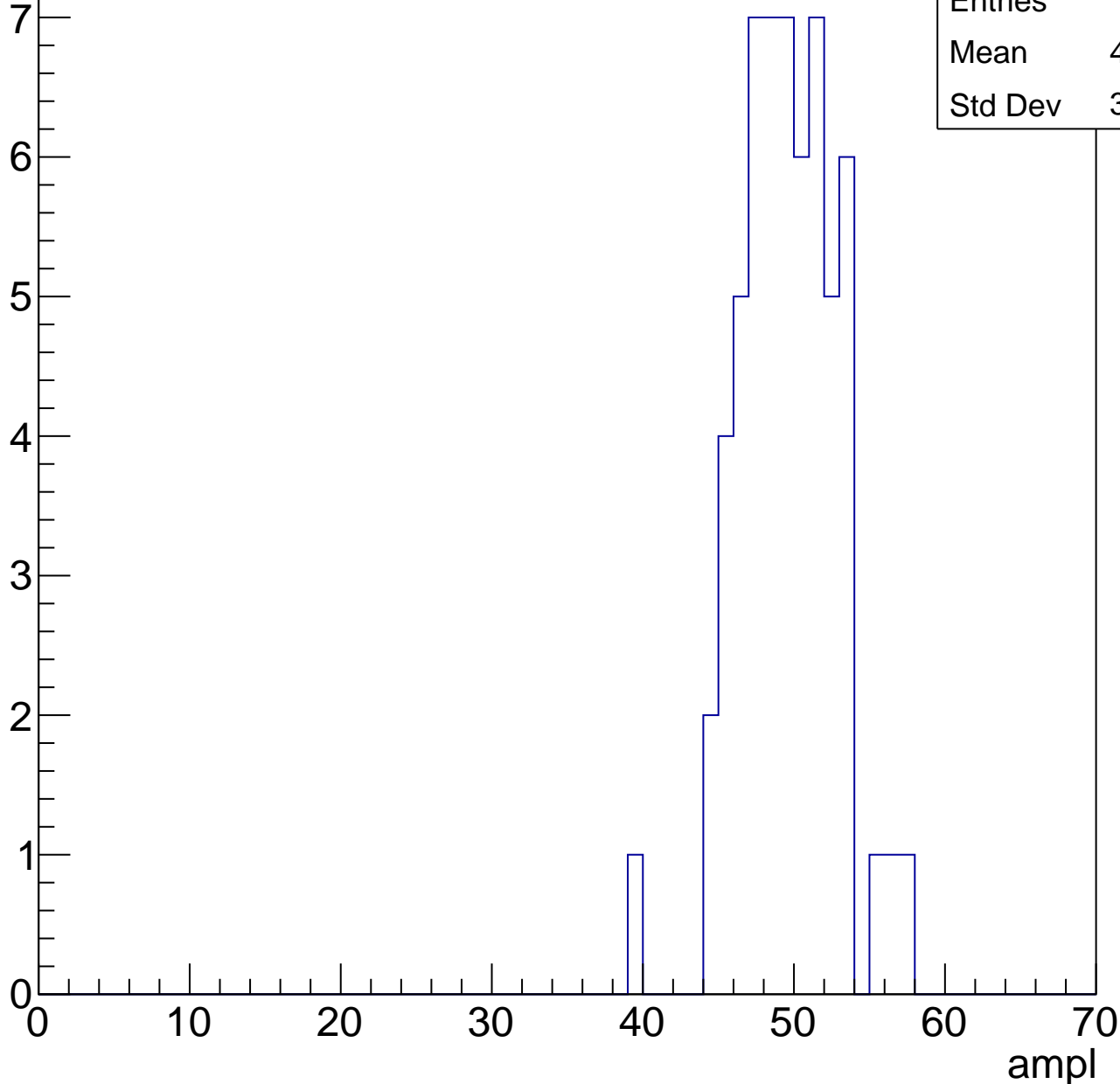


# B1L003S, U23-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	49.13
Std Dev	3.207

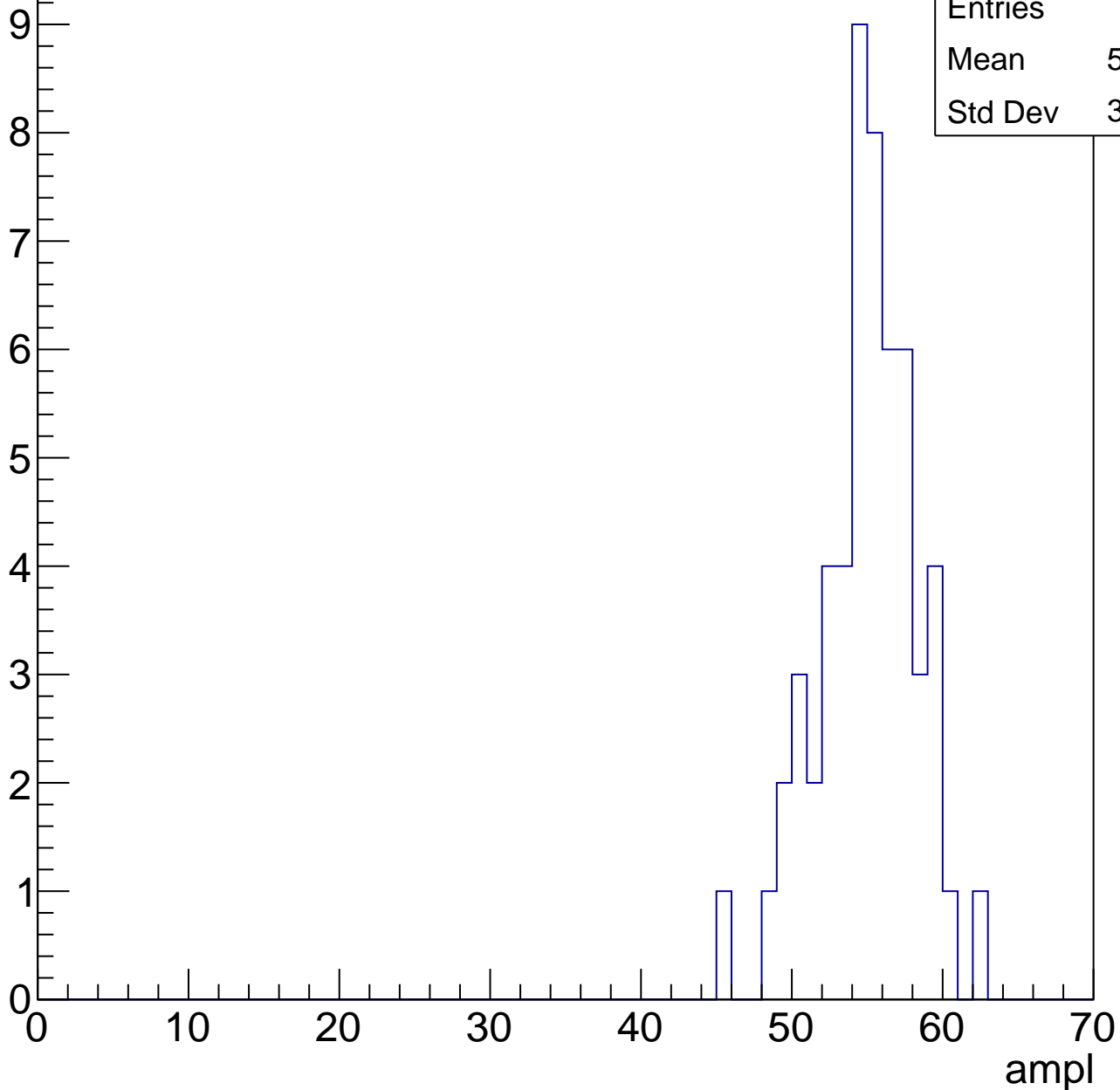


# B1L003S, U23-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.53
Std Dev	3.224

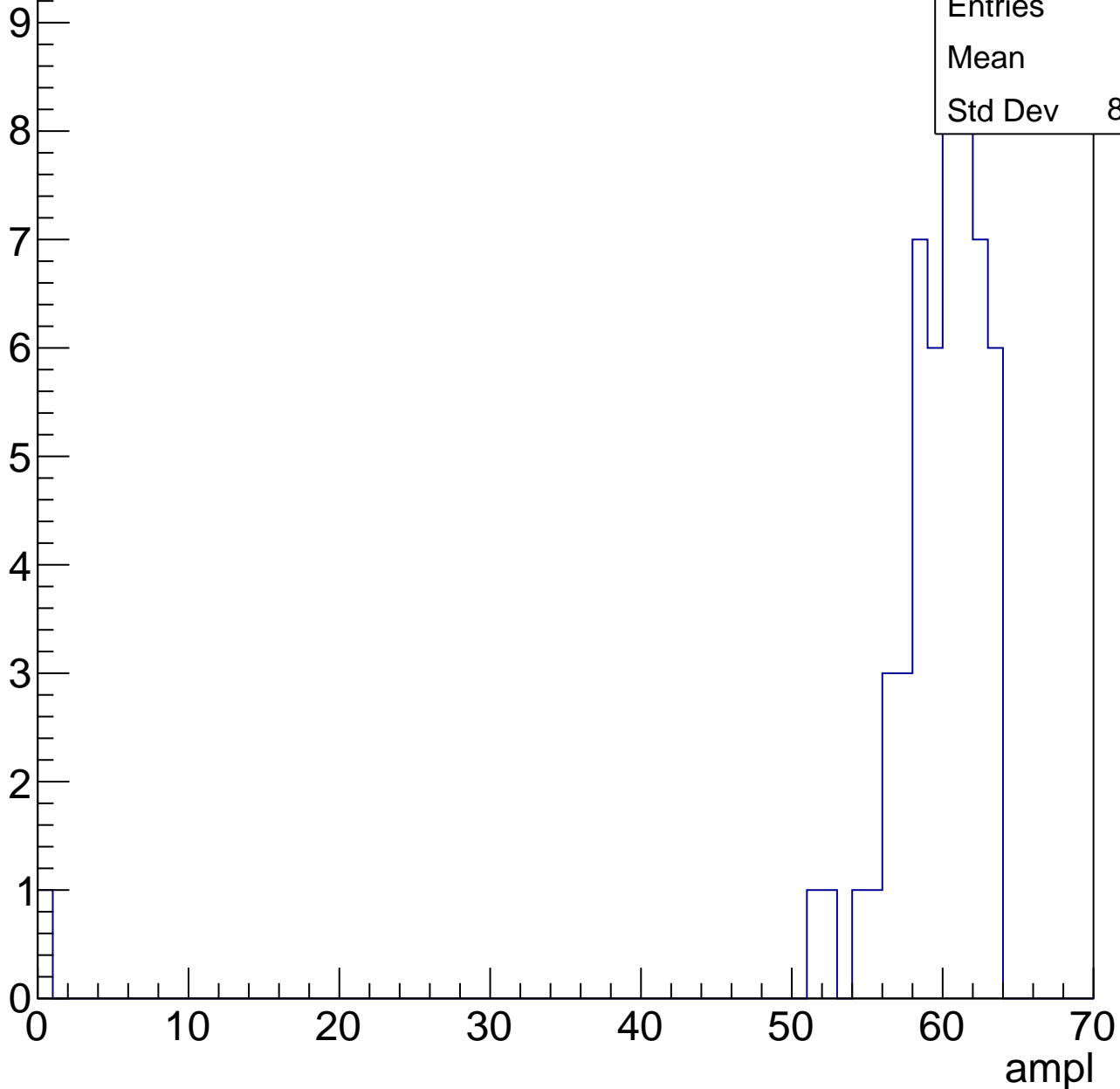


# B1L003S, U23-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

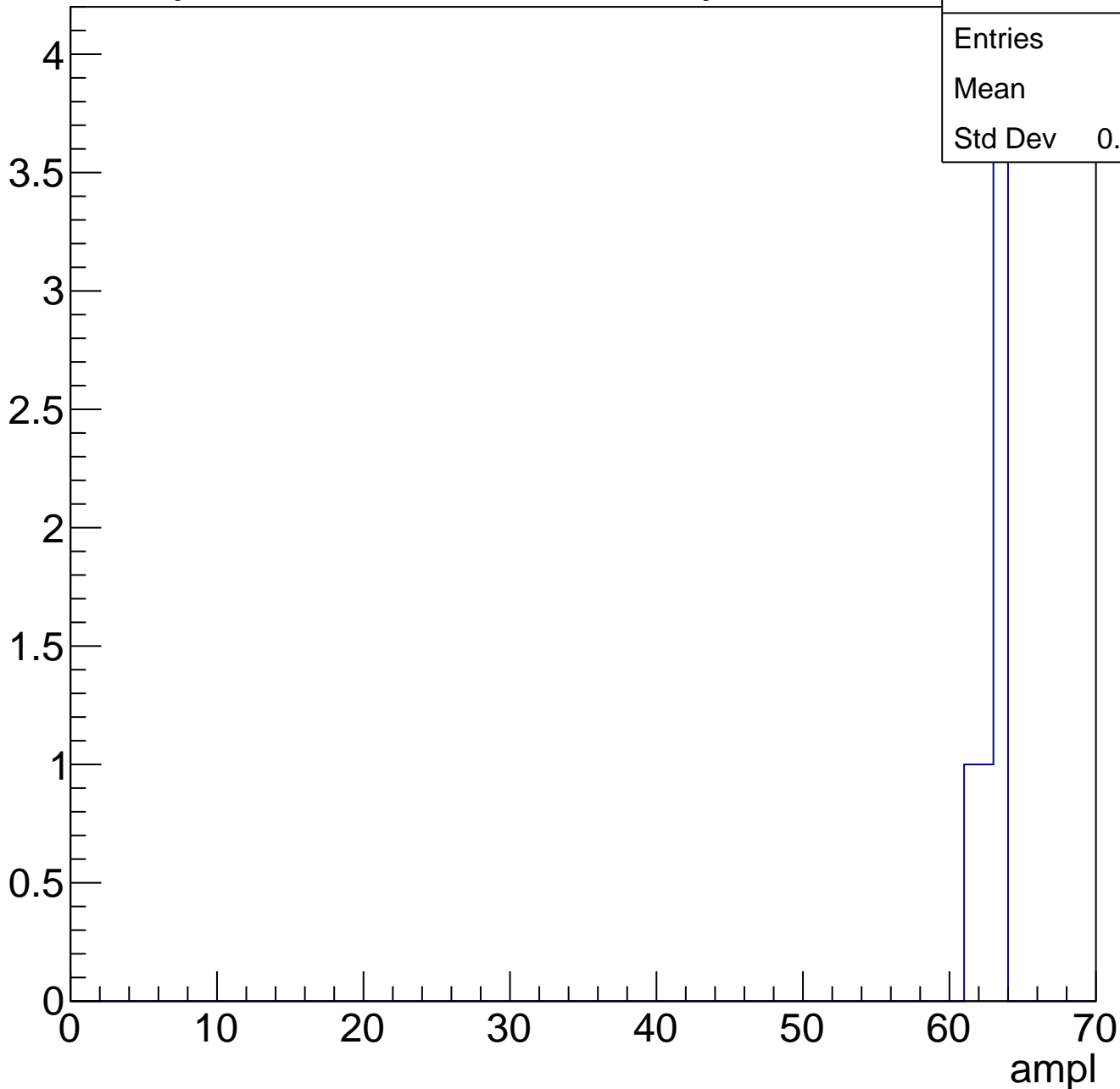
Entries	55
Mean	58.4
Std Dev	8.383



# B1L003S, U23-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch78, adc0

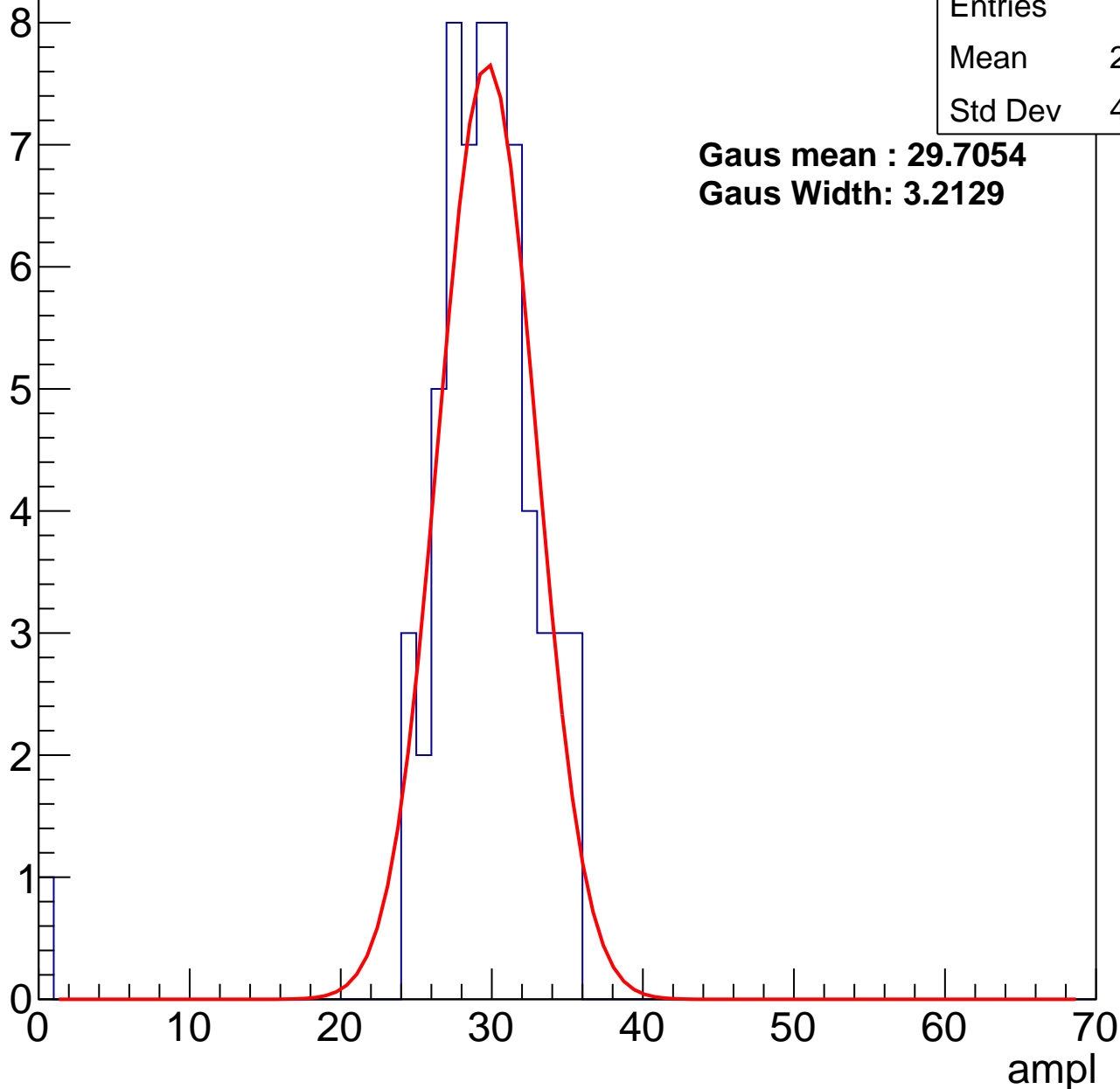
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	28.82
Std Dev	4.637

**Gaus mean : 29.7054**

**Gaus Width: 3.2129**



# B1L003S, U23-ch78, adc1

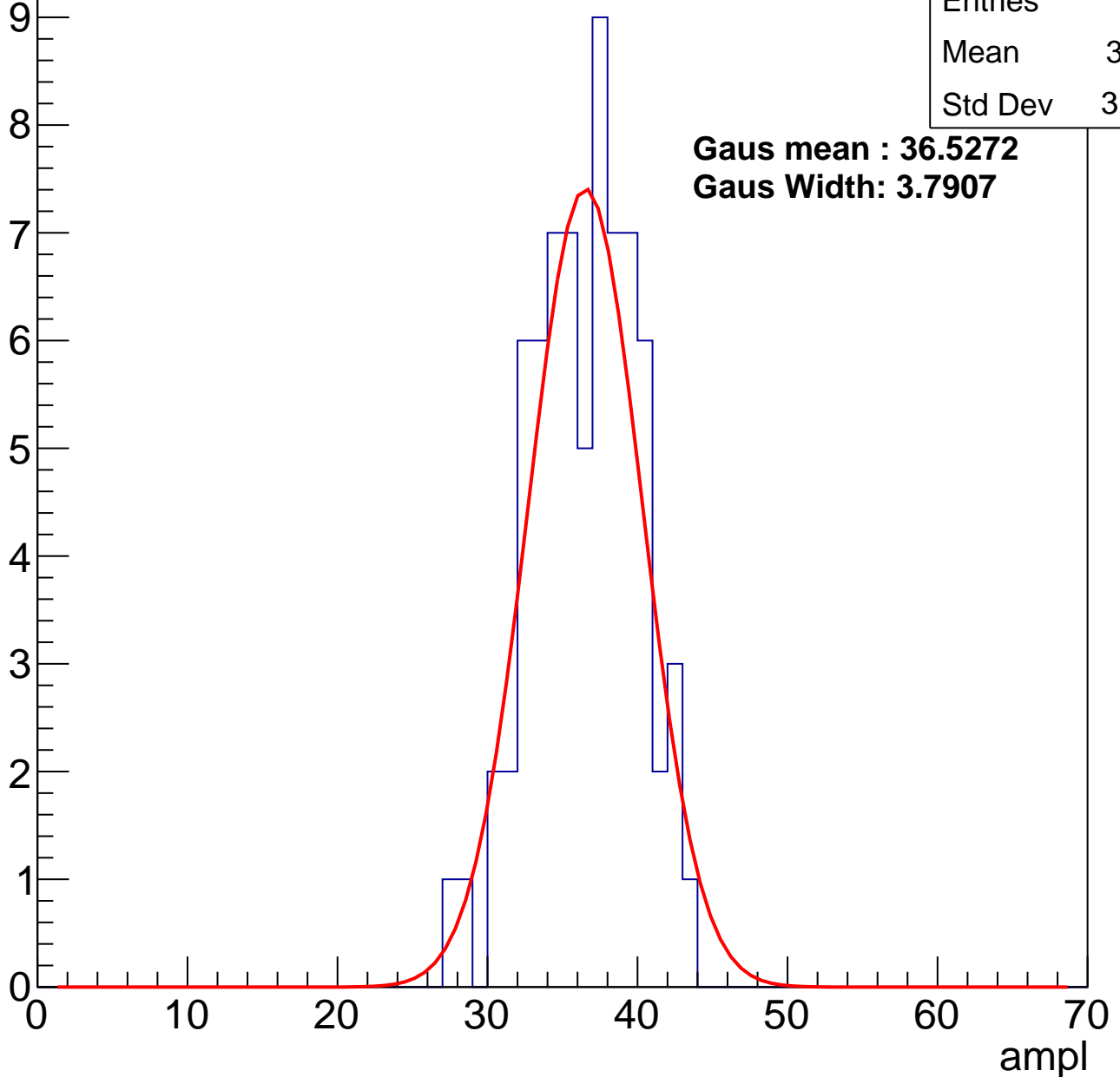
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	36.01
Std Dev	3.446

**Gaus mean : 36.5272**

**Gaus Width: 3.7907**



# B1L003S, U23-ch78, adc2

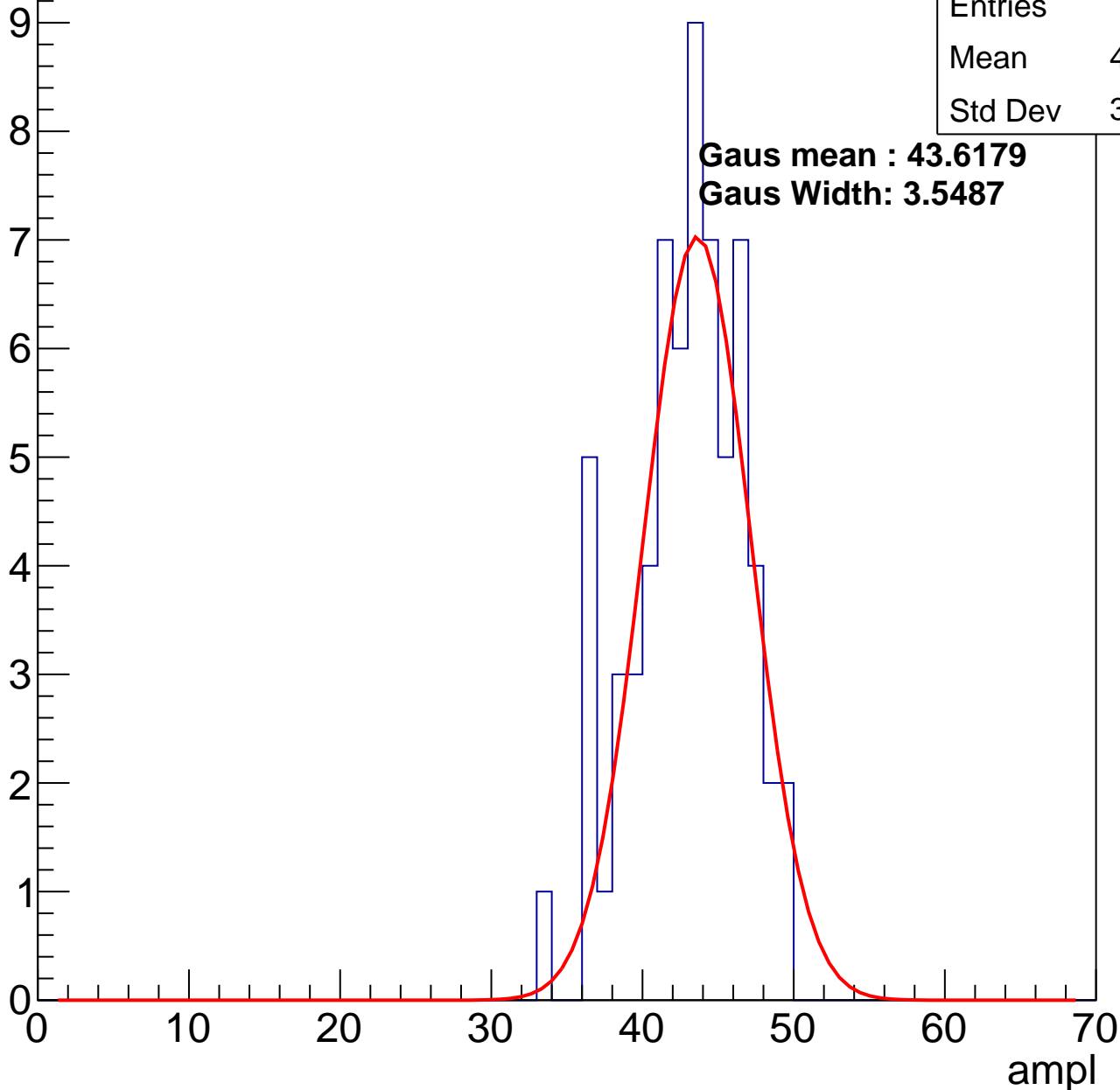
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	42.48
Std Dev	3.552

**Gaus mean : 43.6179**

**Gaus Width: 3.5487**

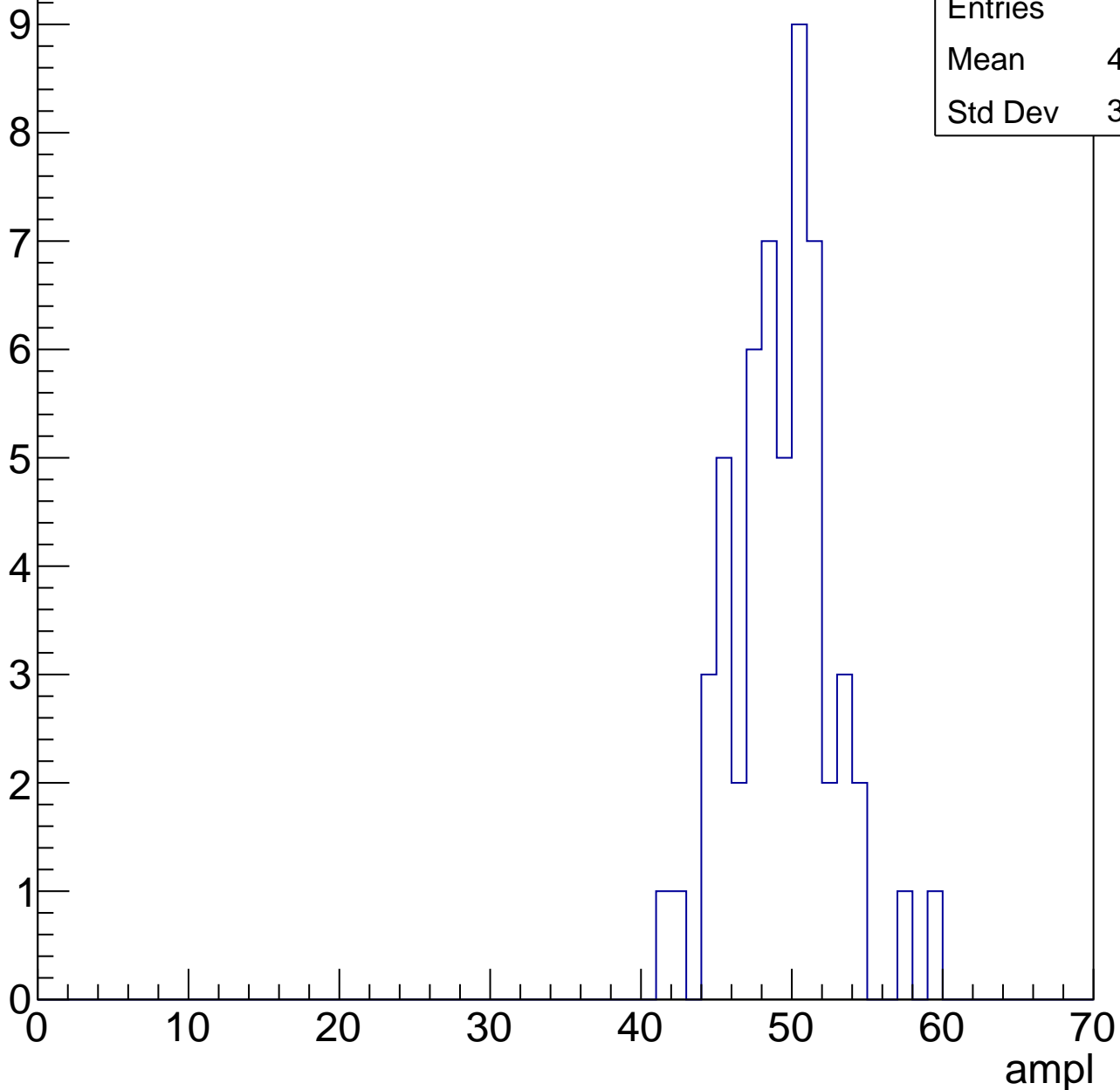


# B1L003S, U23-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	48.89
Std Dev	3.393

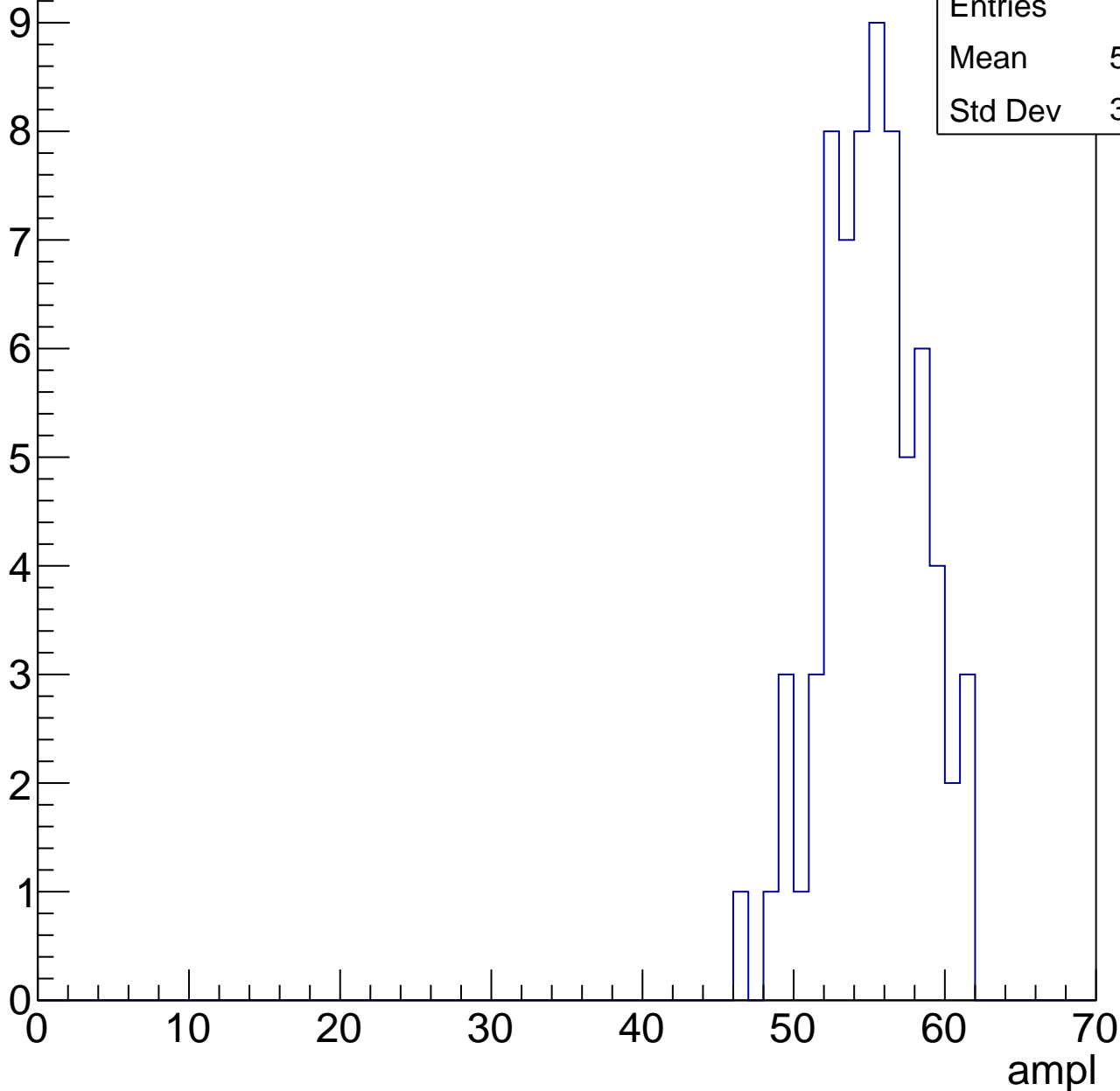


# B1L003S, U23-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	54.75
Std Dev	3.228



# B1L003S, U23-ch78, adc5

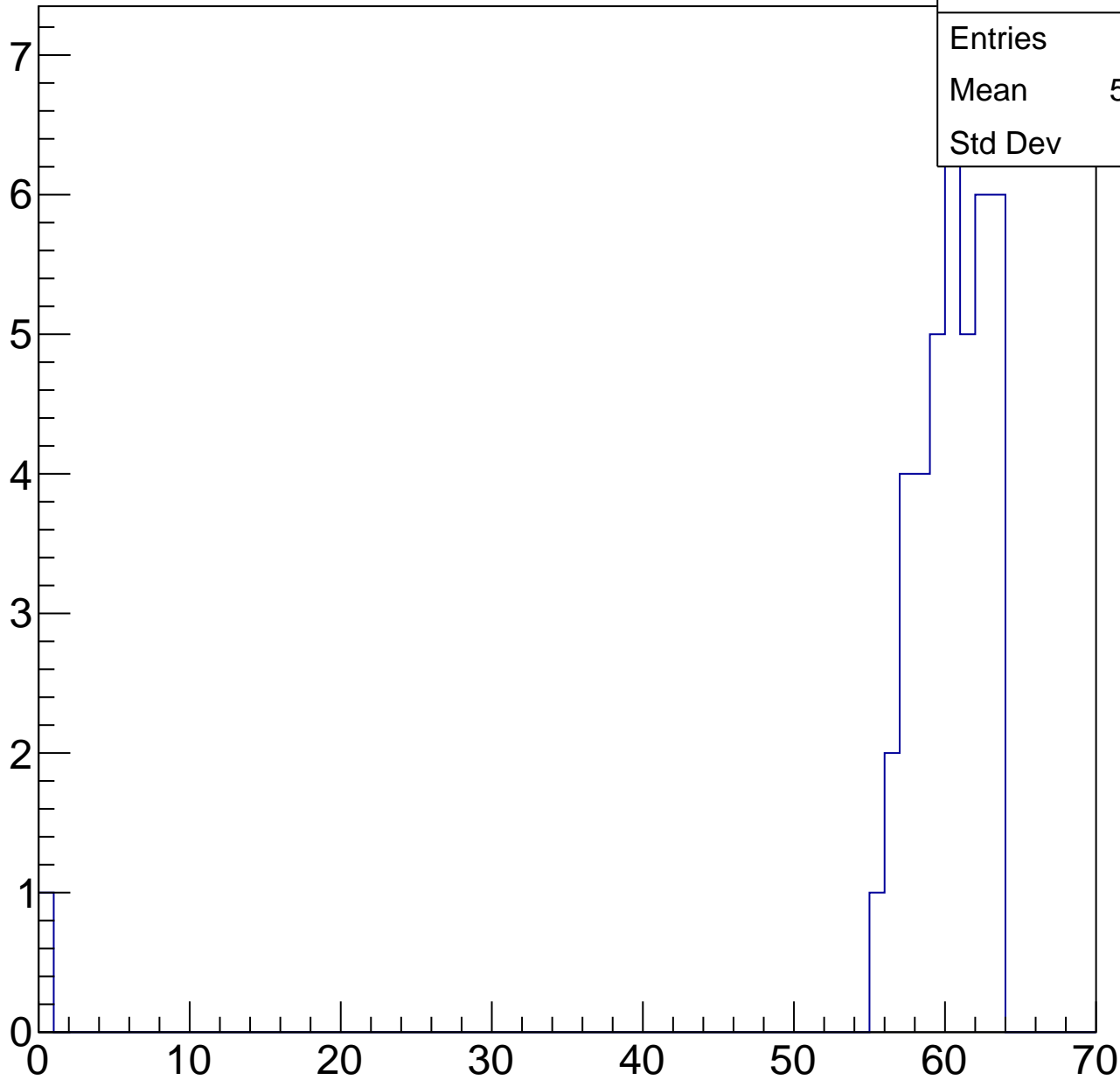
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	58.46
Std Dev	9.5

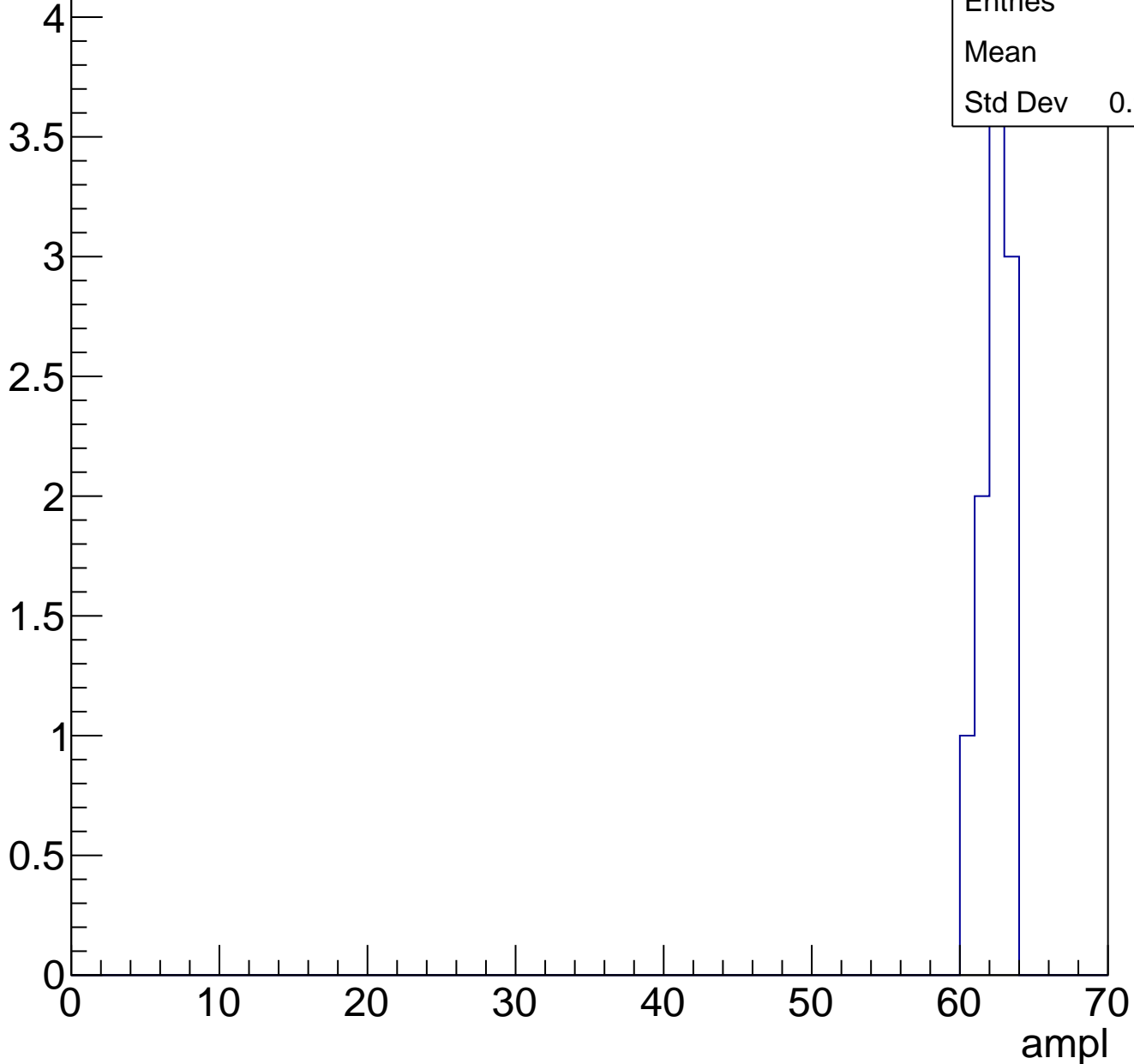
ampl



# B1L003S, U23-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

# B1L003S, U23-ch79, adc0

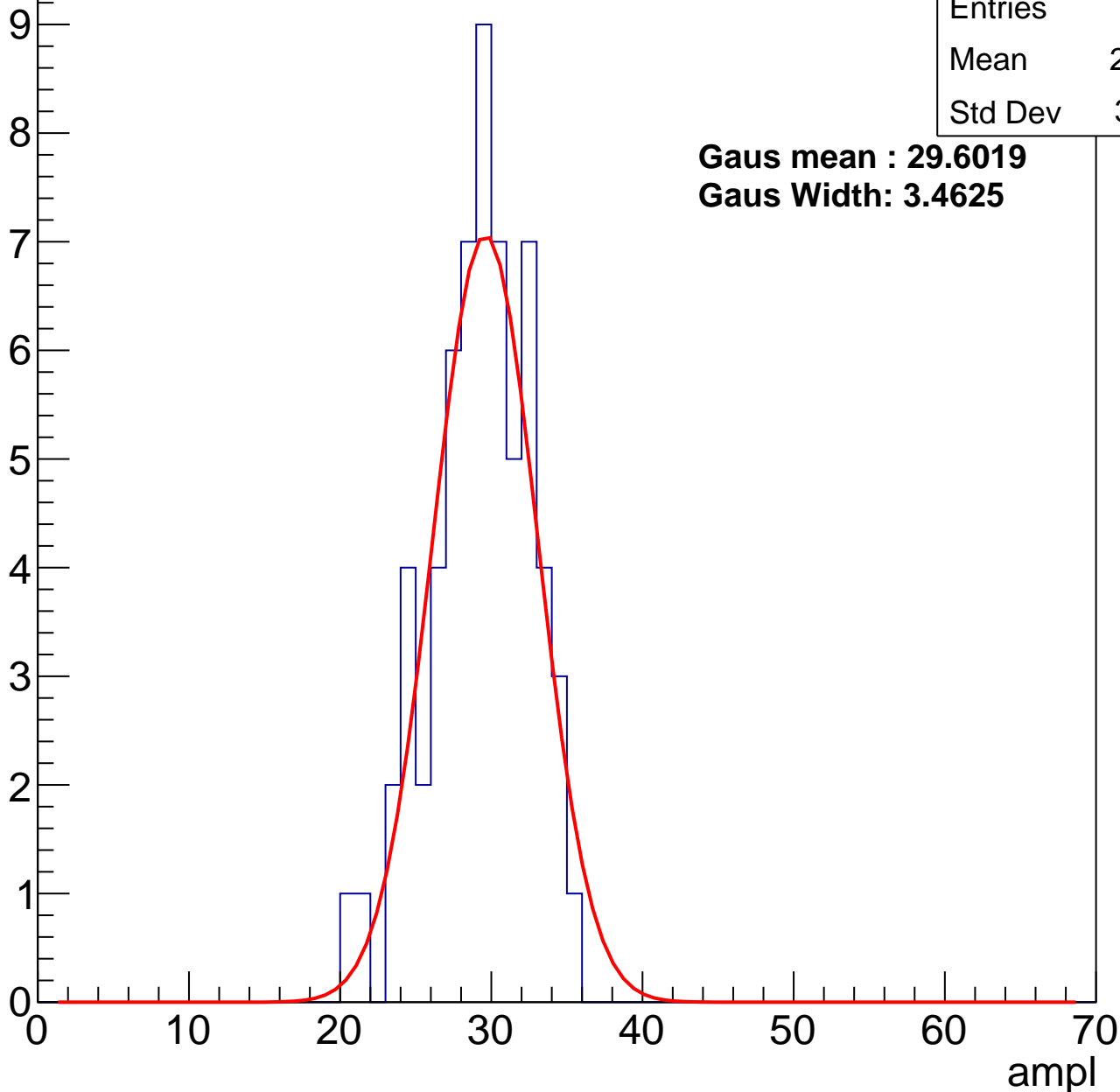
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	28.79
Std Dev	3.281

**Gaus mean : 29.6019**

**Gaus Width: 3.4625**



# B1L003S, U23-ch79, adc1

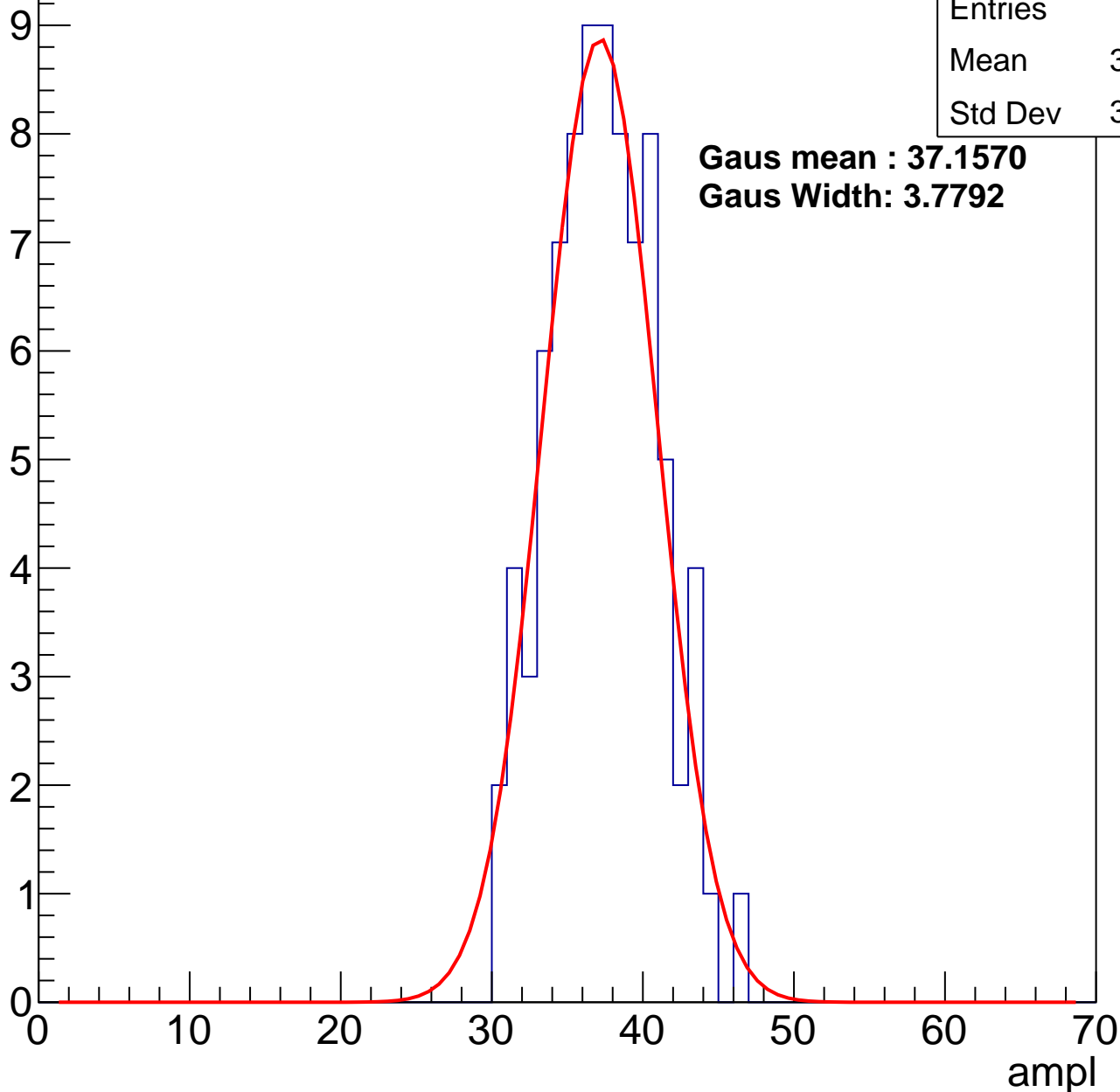
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	84
Mean	36.92
Std Dev	3.499

**Gaus mean : 37.1570**

**Gaus Width: 3.7792**



# B1L003S, U23-ch79, adc2

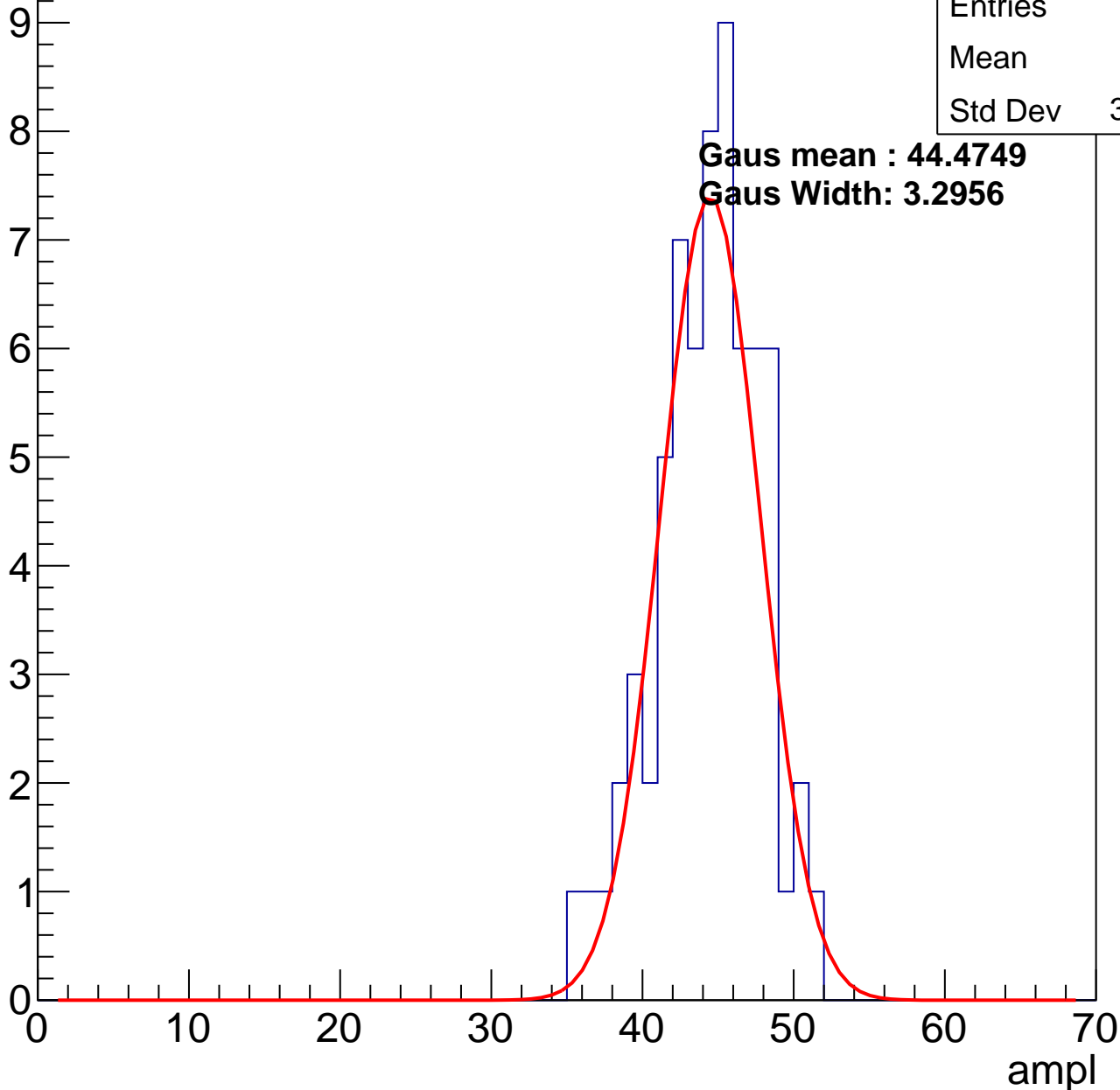
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	43.9
Std Dev	3.413

**Gaus mean : 44.4749**

**Gaus Width: 3.2956**

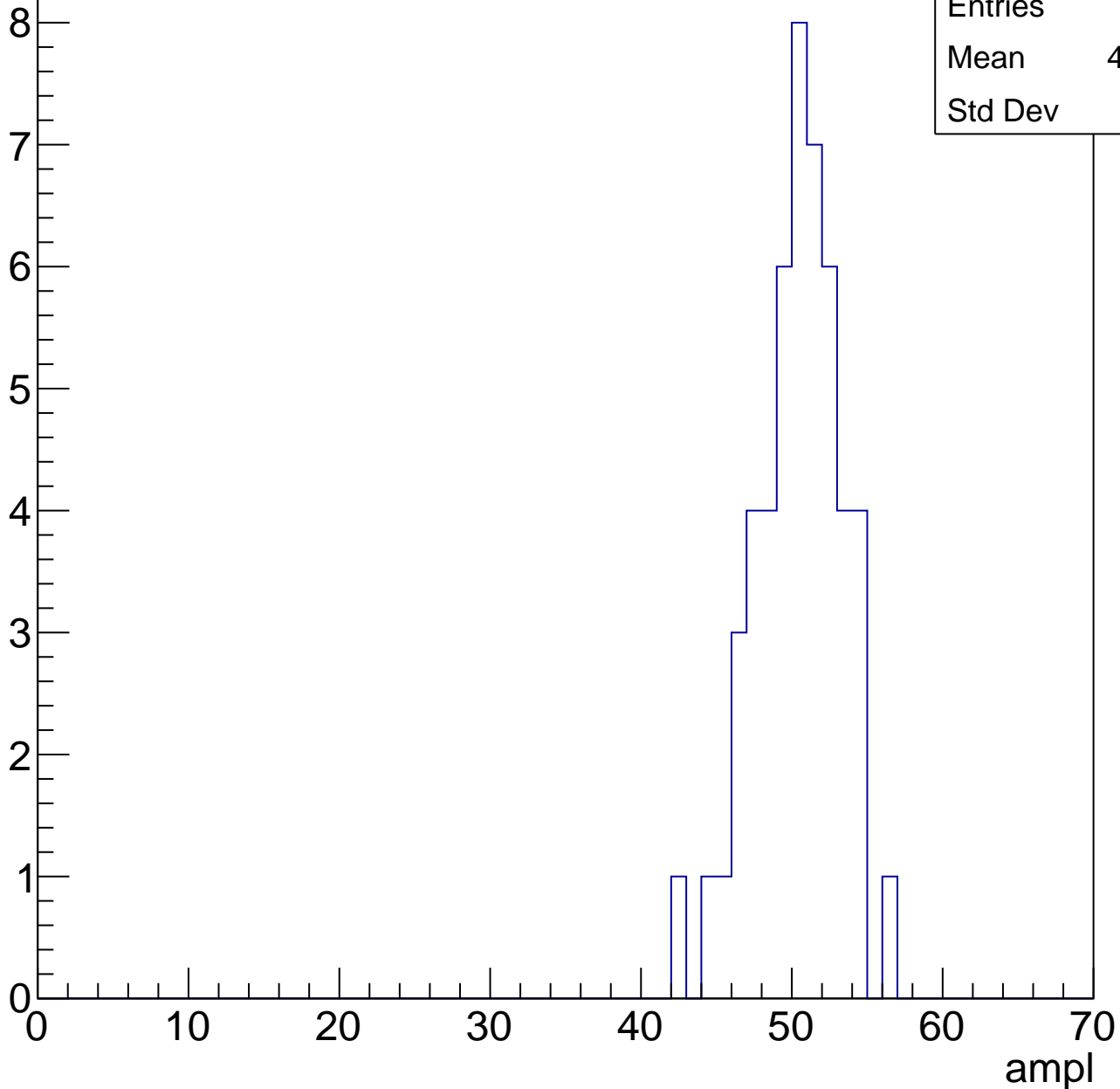


# B1L003S, U23-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	49.92
Std Dev	2.82

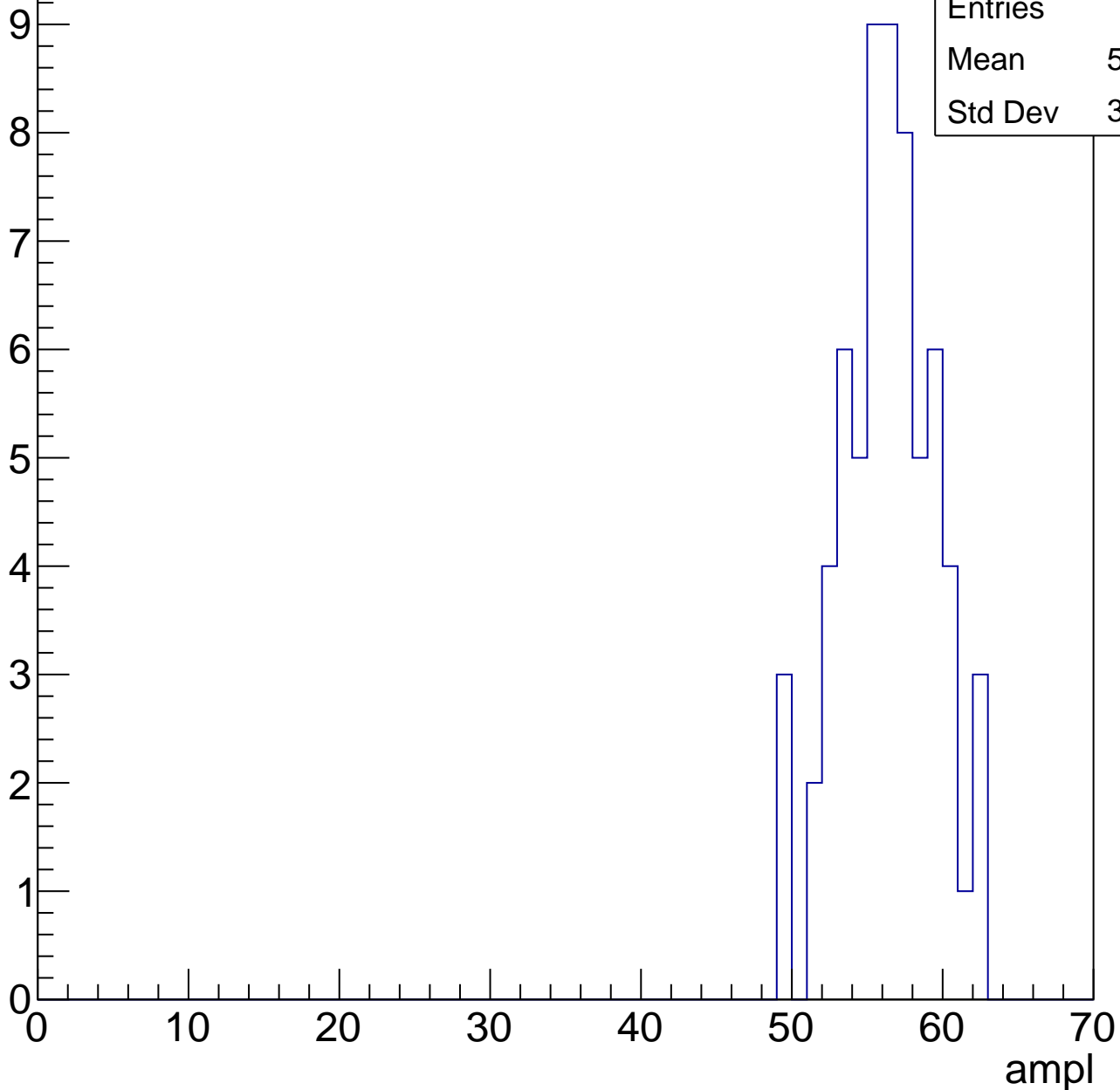


# B1L003S, U23-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	55.86
Std Dev	3.093

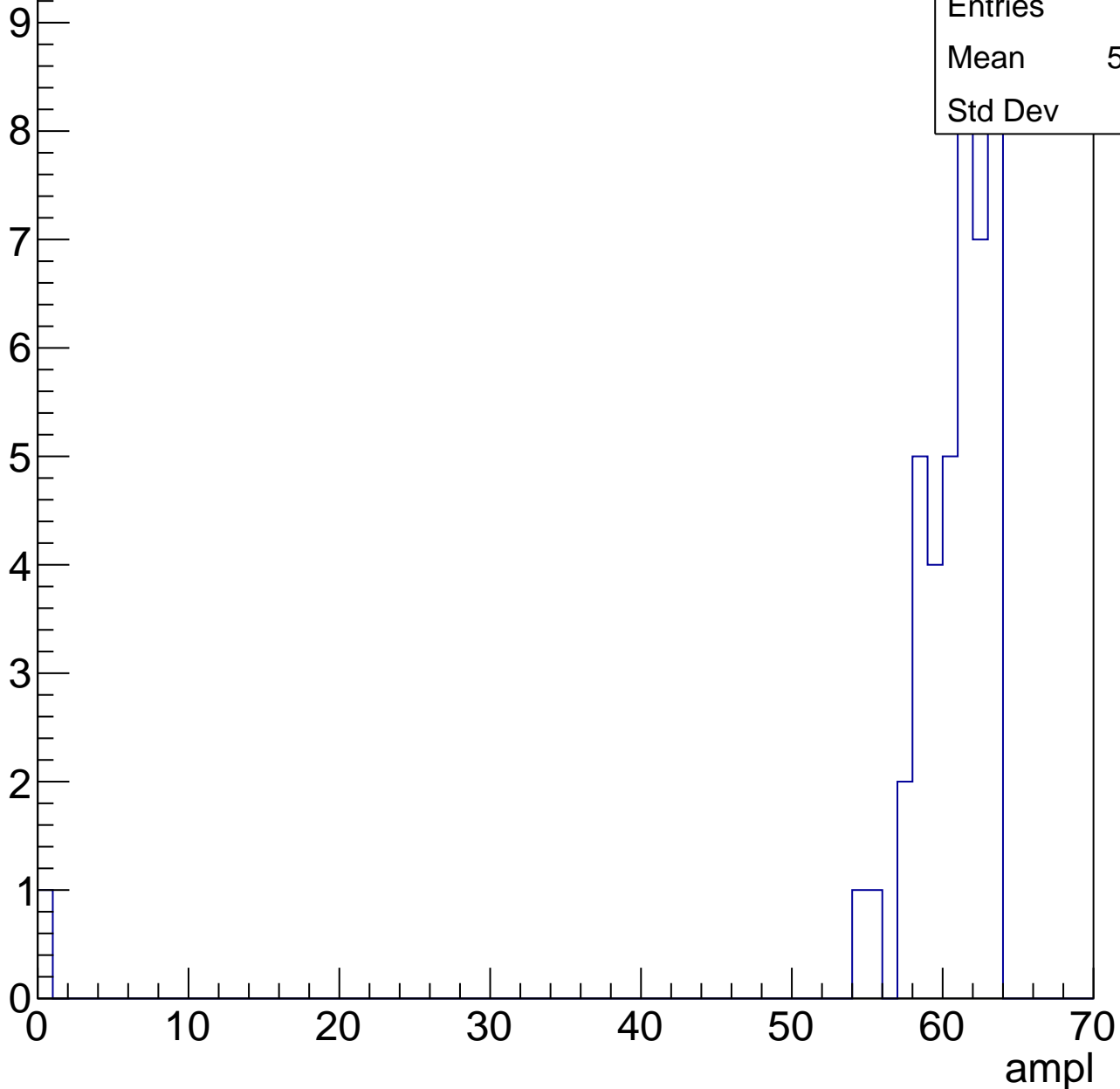


# B1L003S, U23-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	58.98
Std Dev	9.36



# B1L003S, U23-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch80, adc0

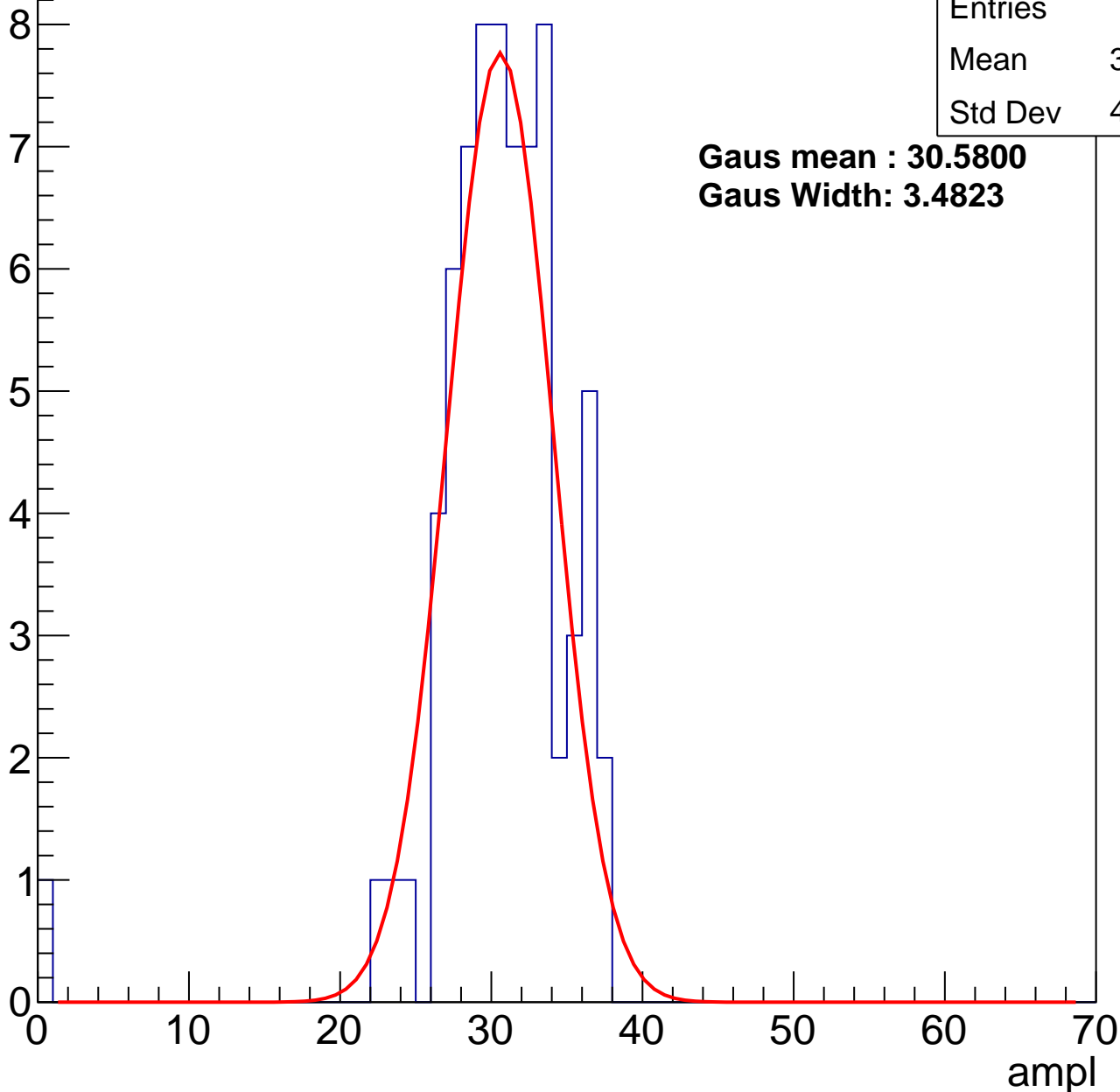
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	30.07
Std Dev	4.886

**Gaus mean : 30.5800**

**Gaus Width: 3.4823**



# B1L003S, U23-ch80, adc1

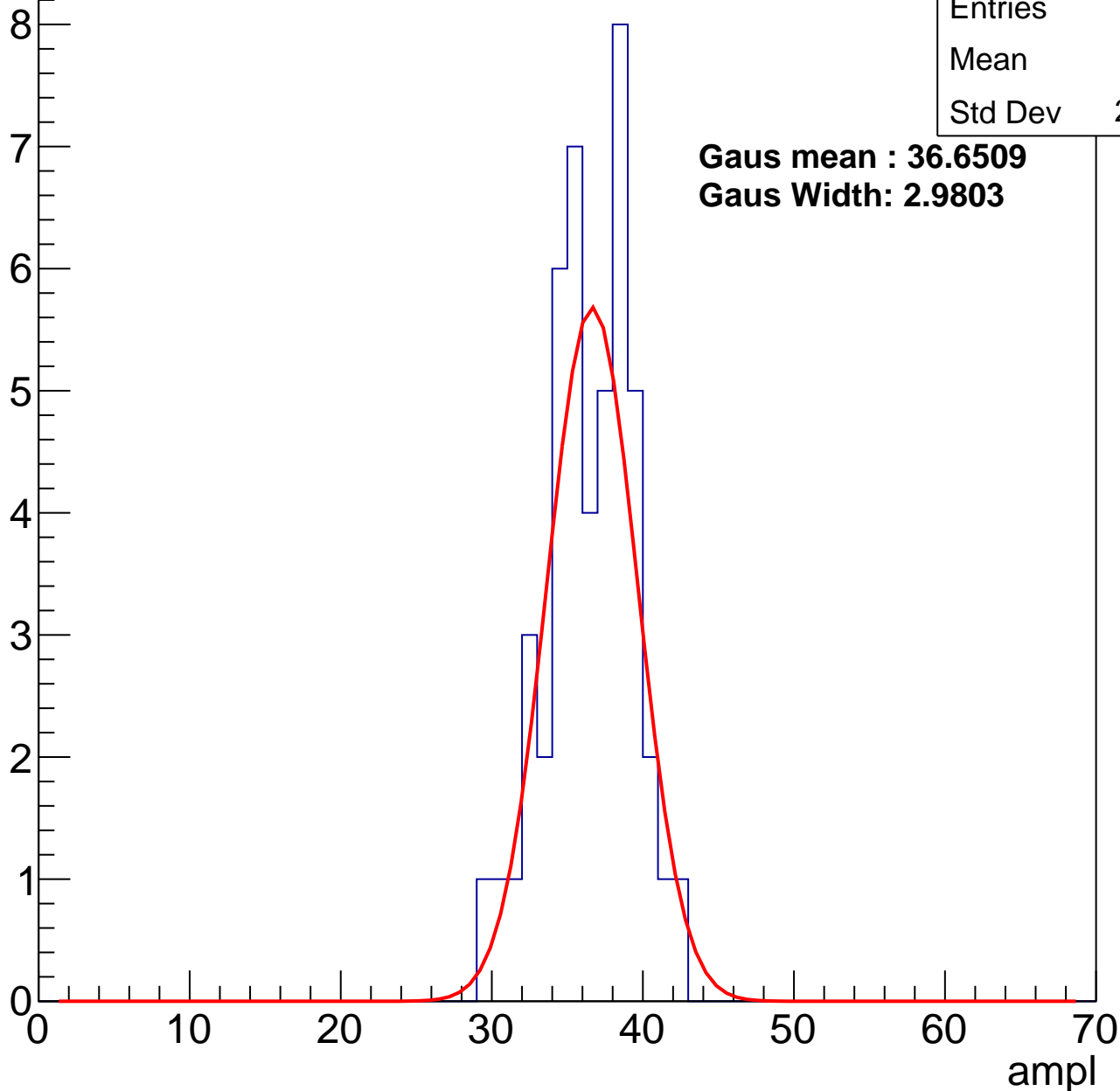
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	36
Std Dev	2.851

**Gaus mean : 36.6509**

**Gaus Width: 2.9803**



# B1L003S, U23-ch80, adc2

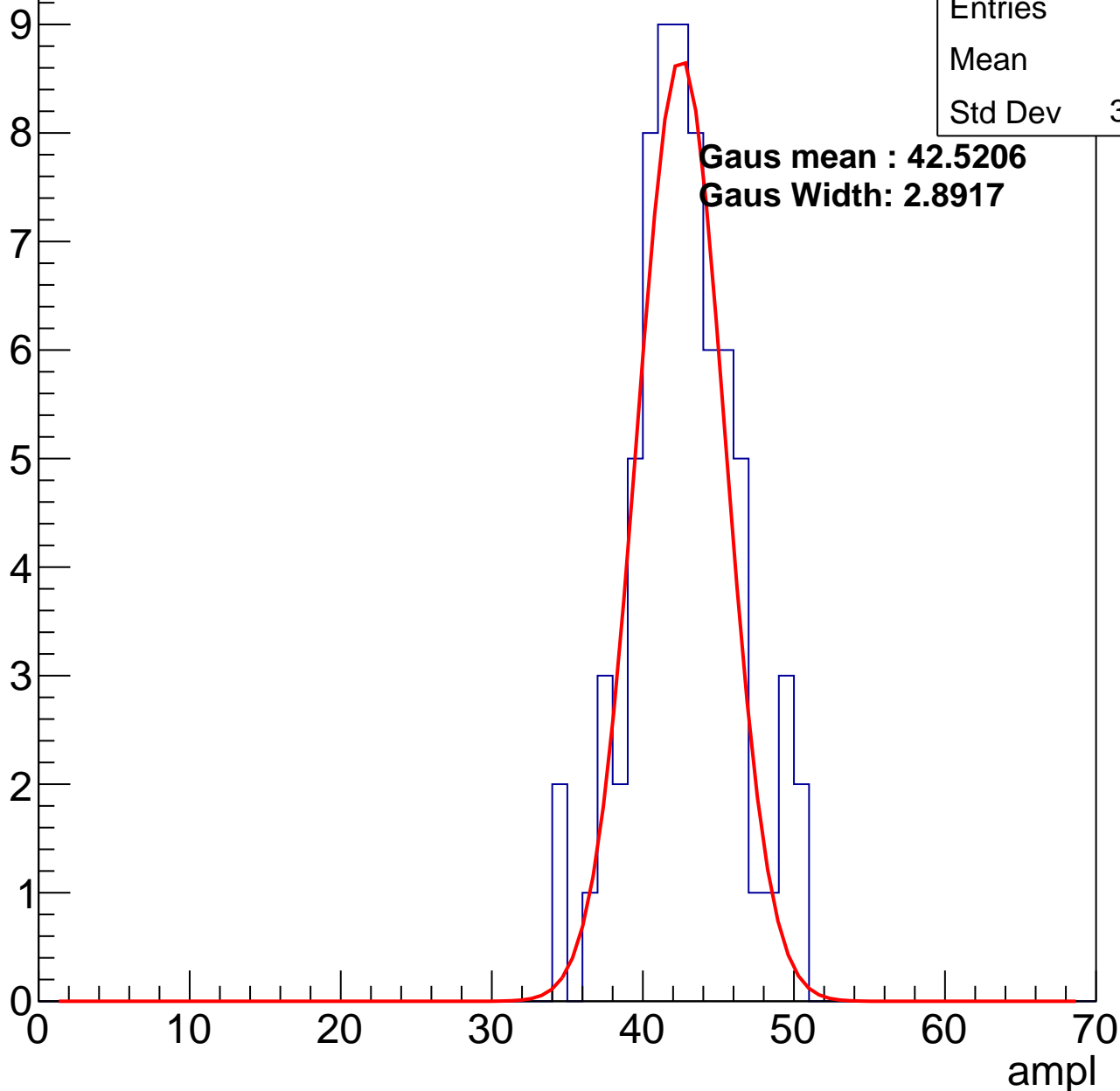
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	42.3
Std Dev	3.466

**Gaus mean : 42.5206**

**Gaus Width: 2.8917**

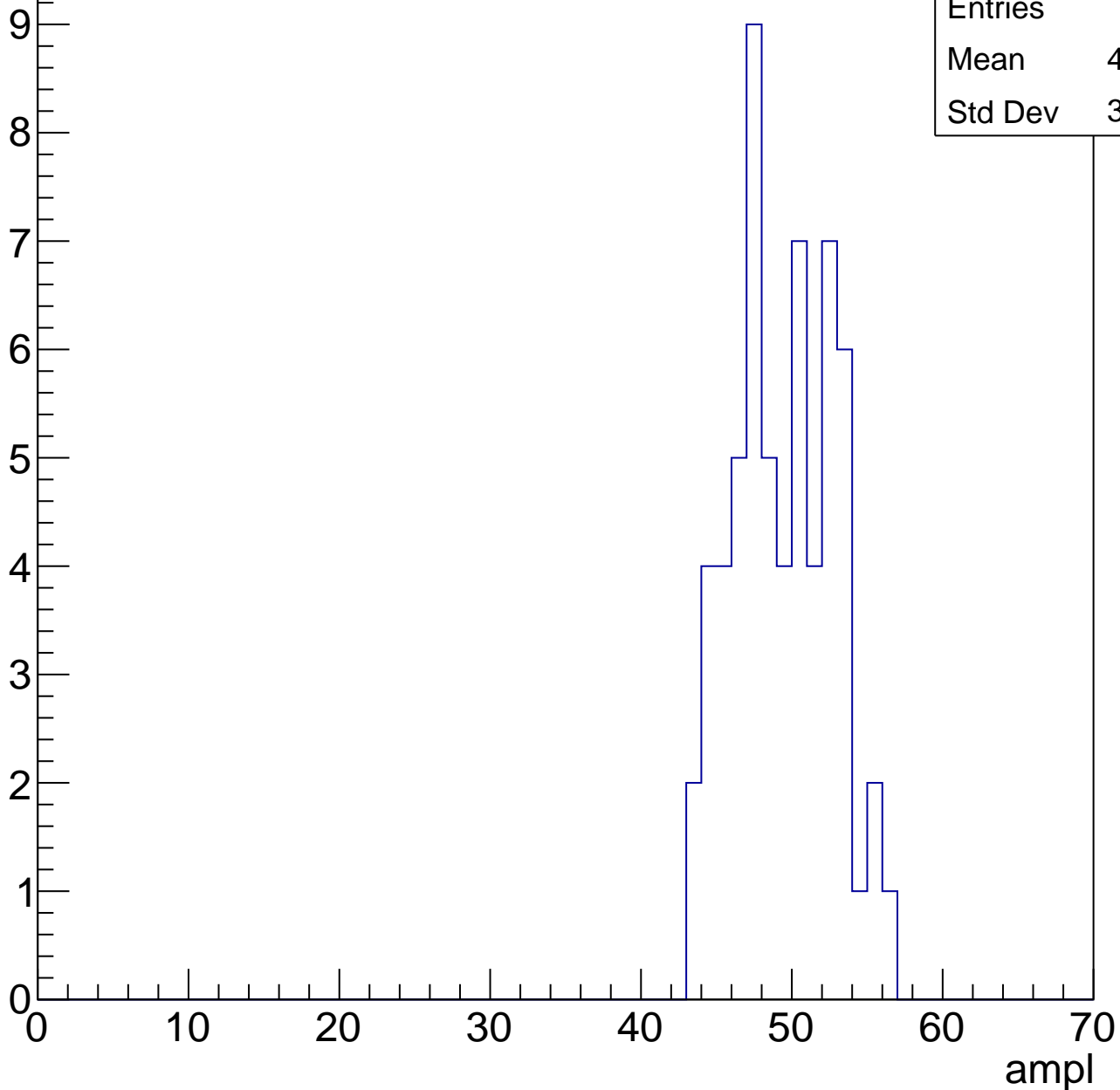


# B1L003S, U23-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.97
Std Dev	3.264

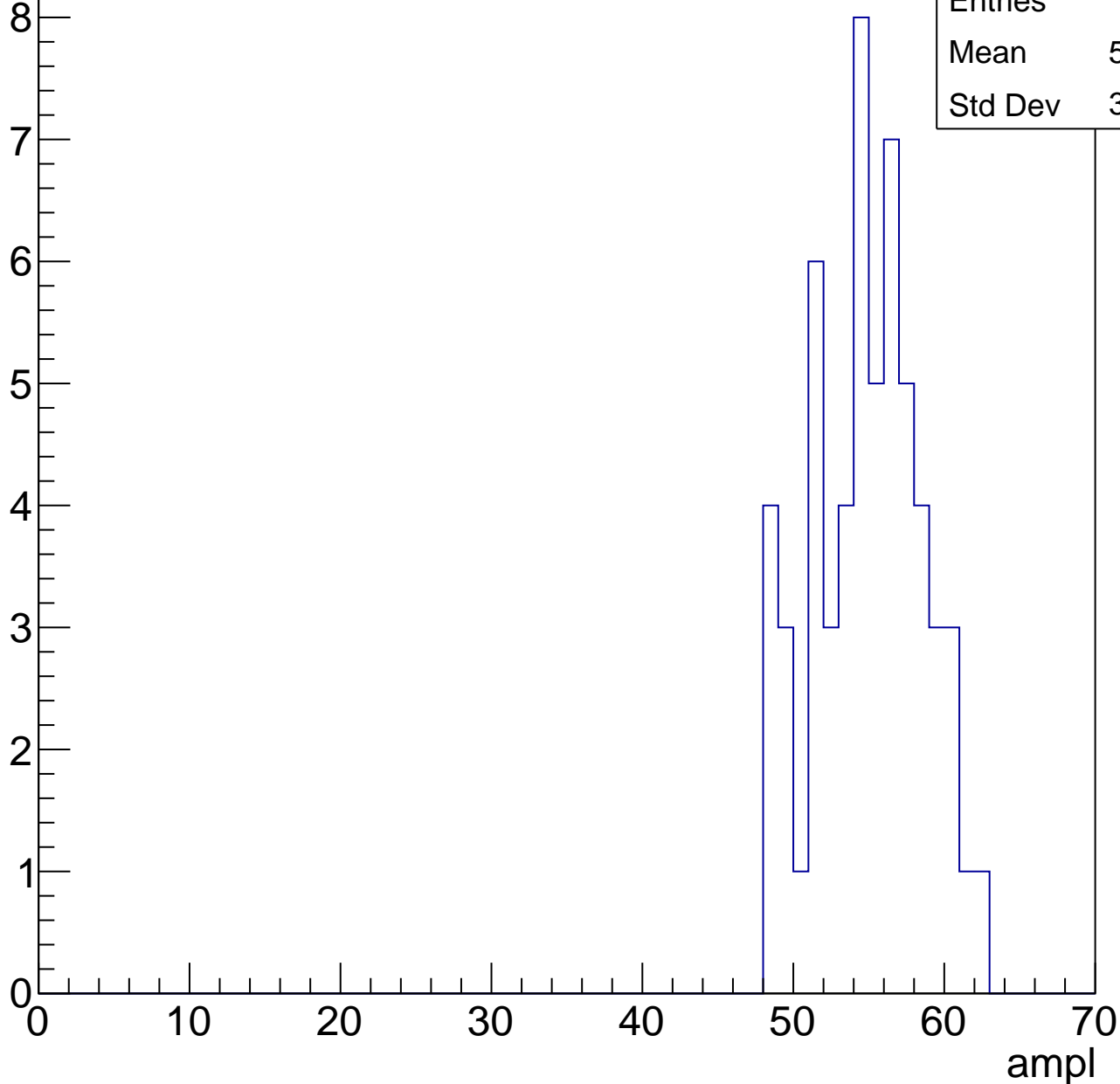


# B1L003S, U23-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.47
Std Dev	3.549

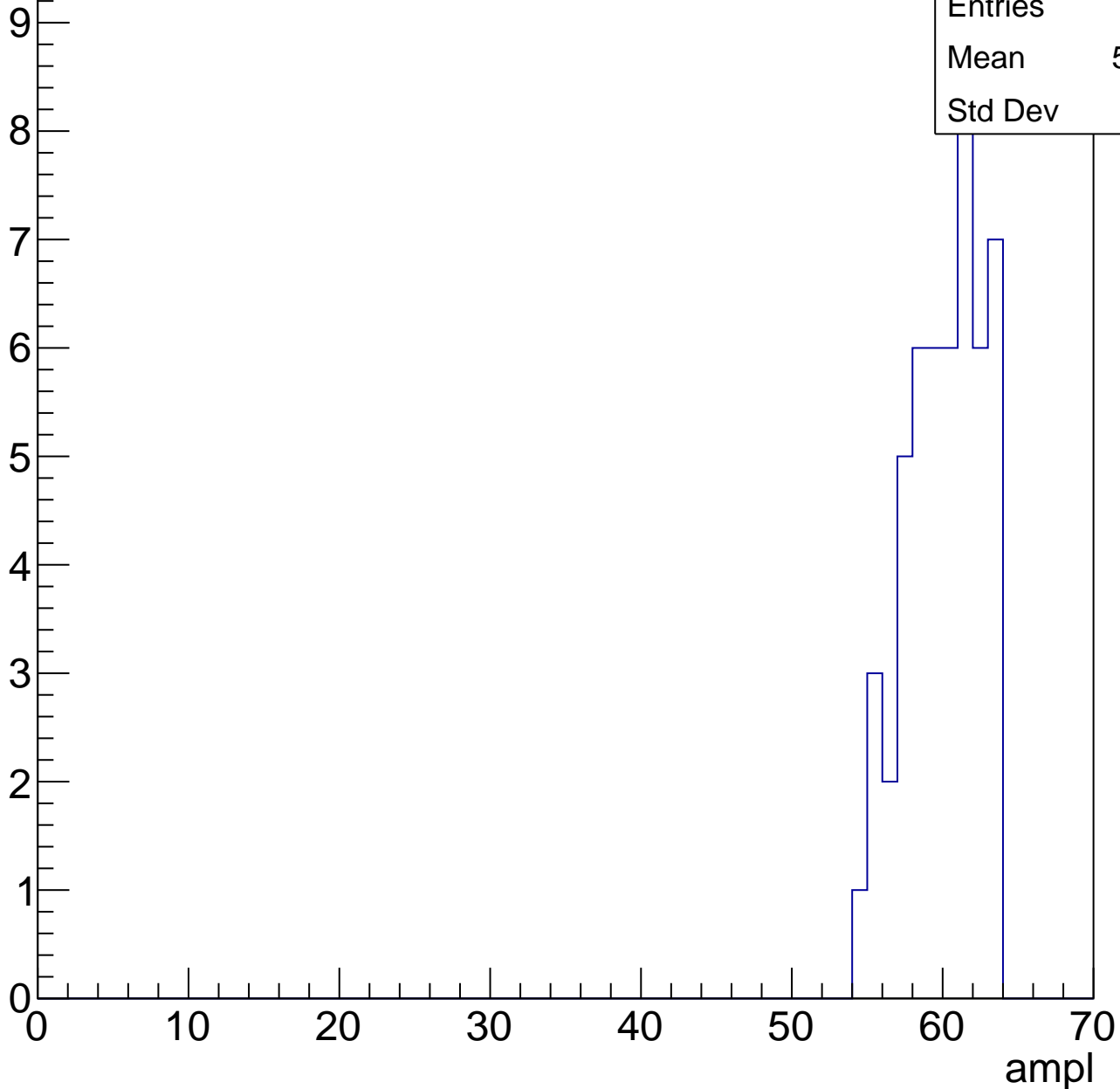


# B1L003S, U23-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	59.61
Std Dev	2.45



# B1L003S, U23-ch80, adc6

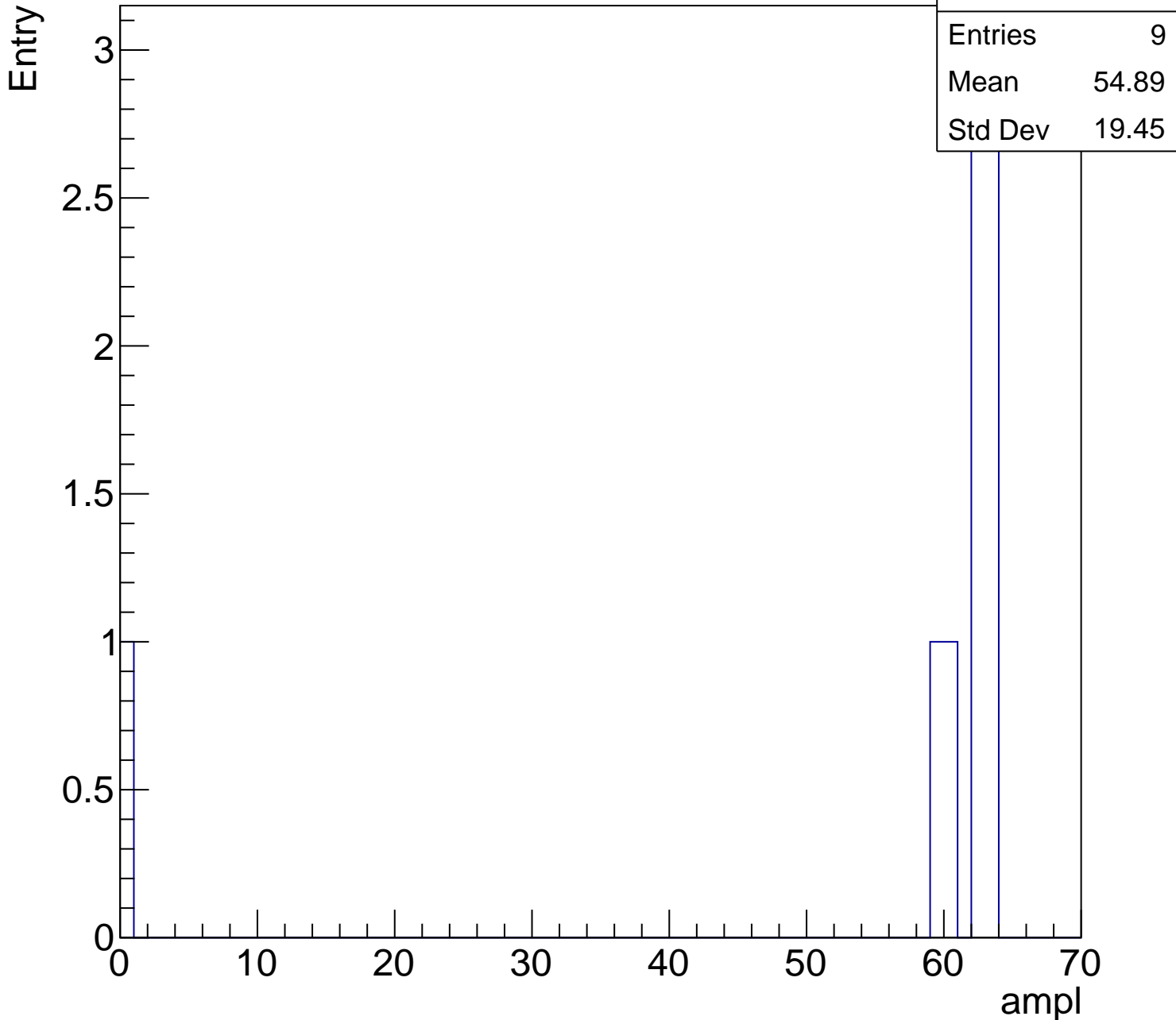
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	9
Mean	54.89
Std Dev	19.45

ampl





# B1L003S, U23-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch81, adc0

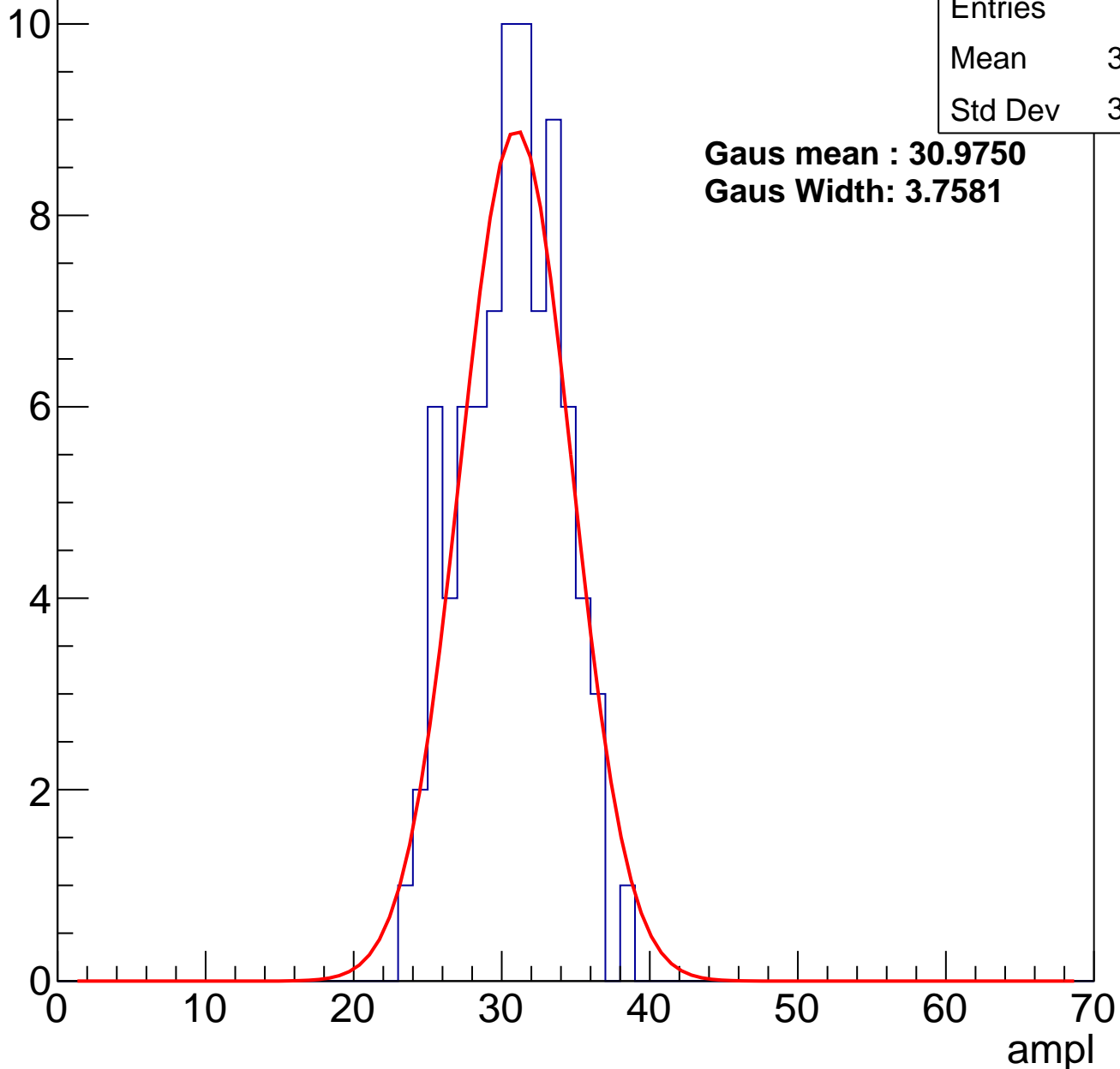
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	82
Mean	30.23
Std Dev	3.318

**Gaus mean : 30.9750**

**Gaus Width: 3.7581**

Entry

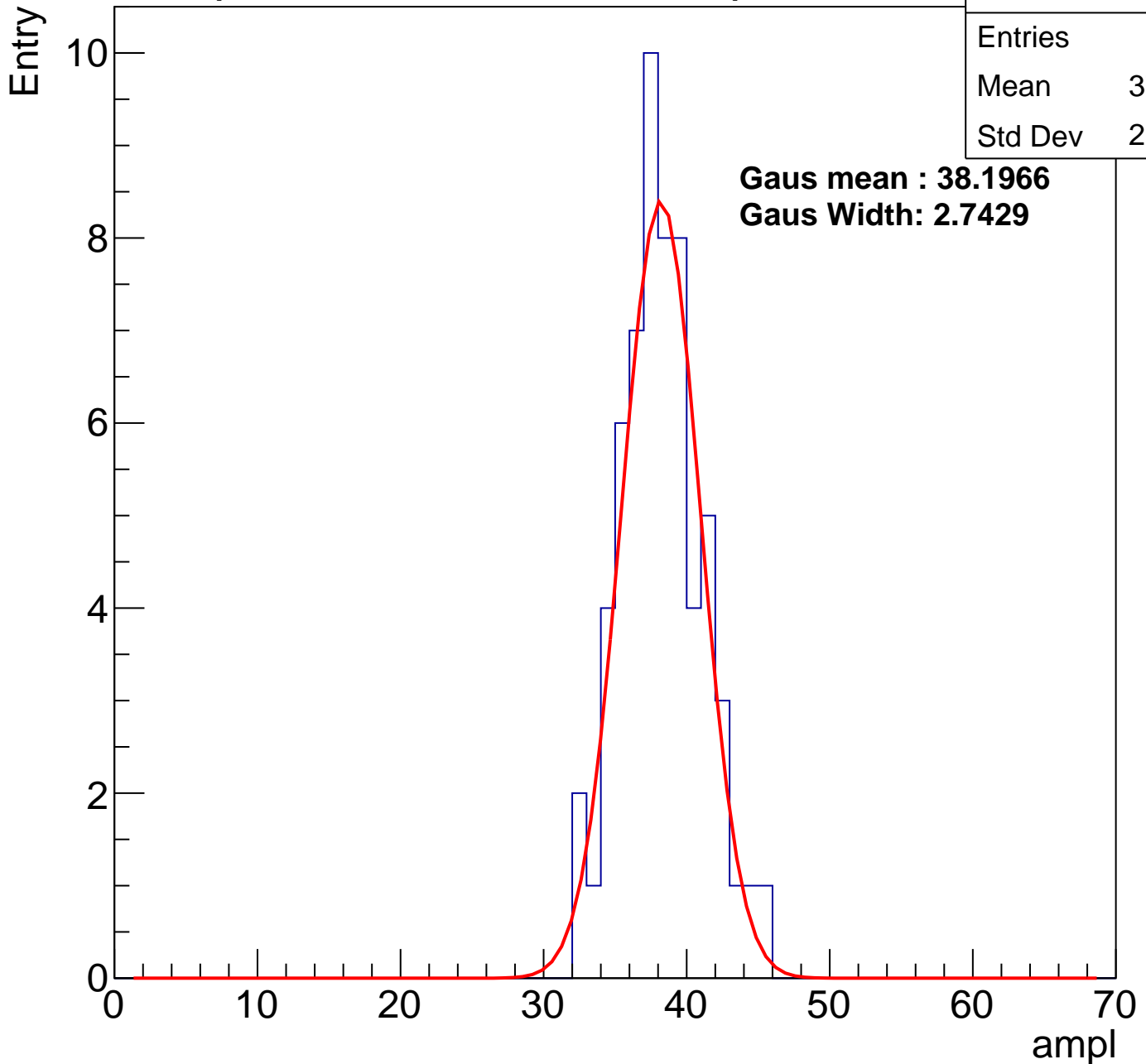


# B1L003S, U23-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	61
Mean	37.77
Std Dev	2.796

**Gaus mean : 38.1966**  
**Gaus Width: 2.7429**



# B1L003S, U23-ch81, adc2

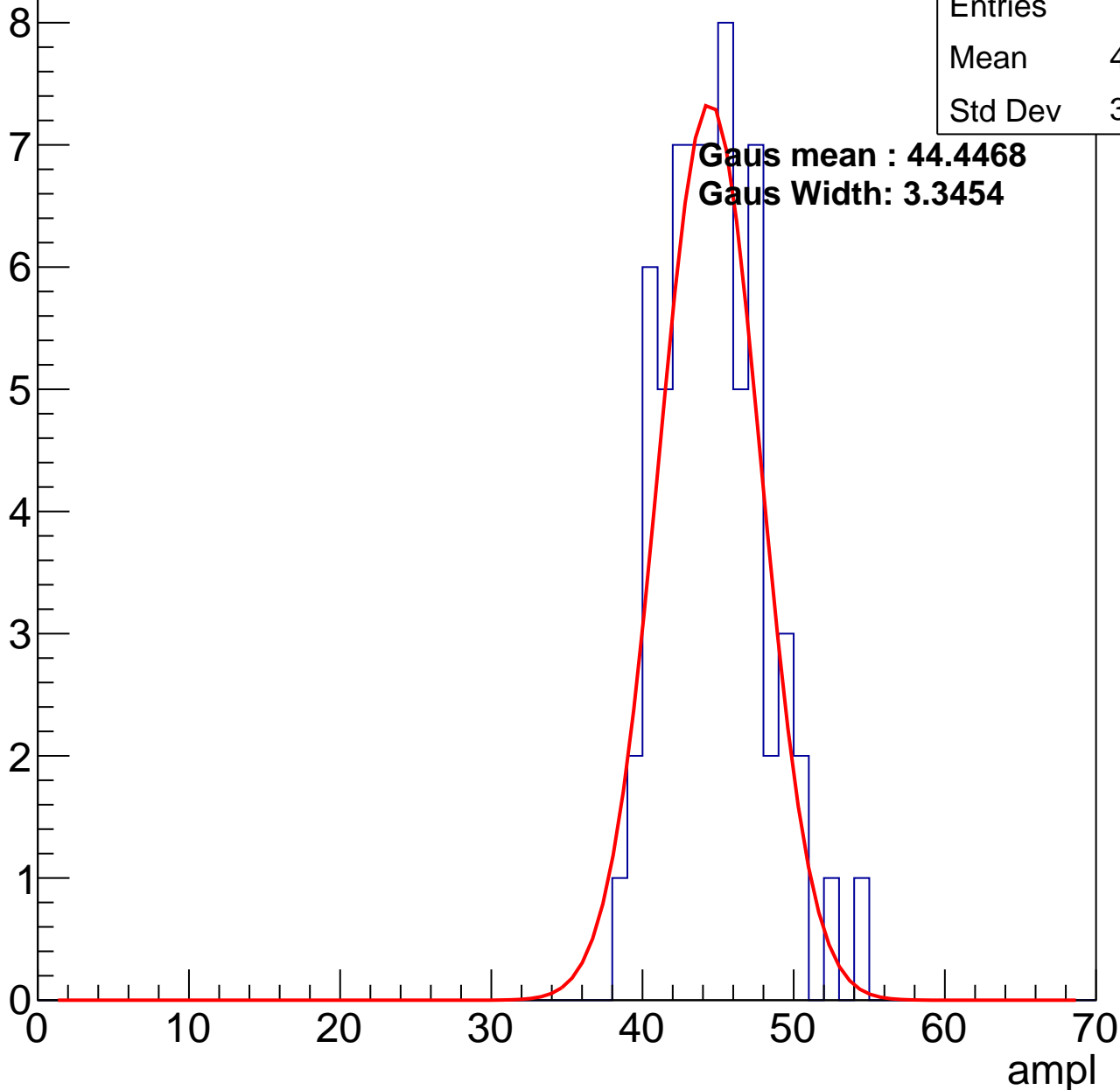
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	44.25
Std Dev	3.288

**Gaus mean : 44.4468**

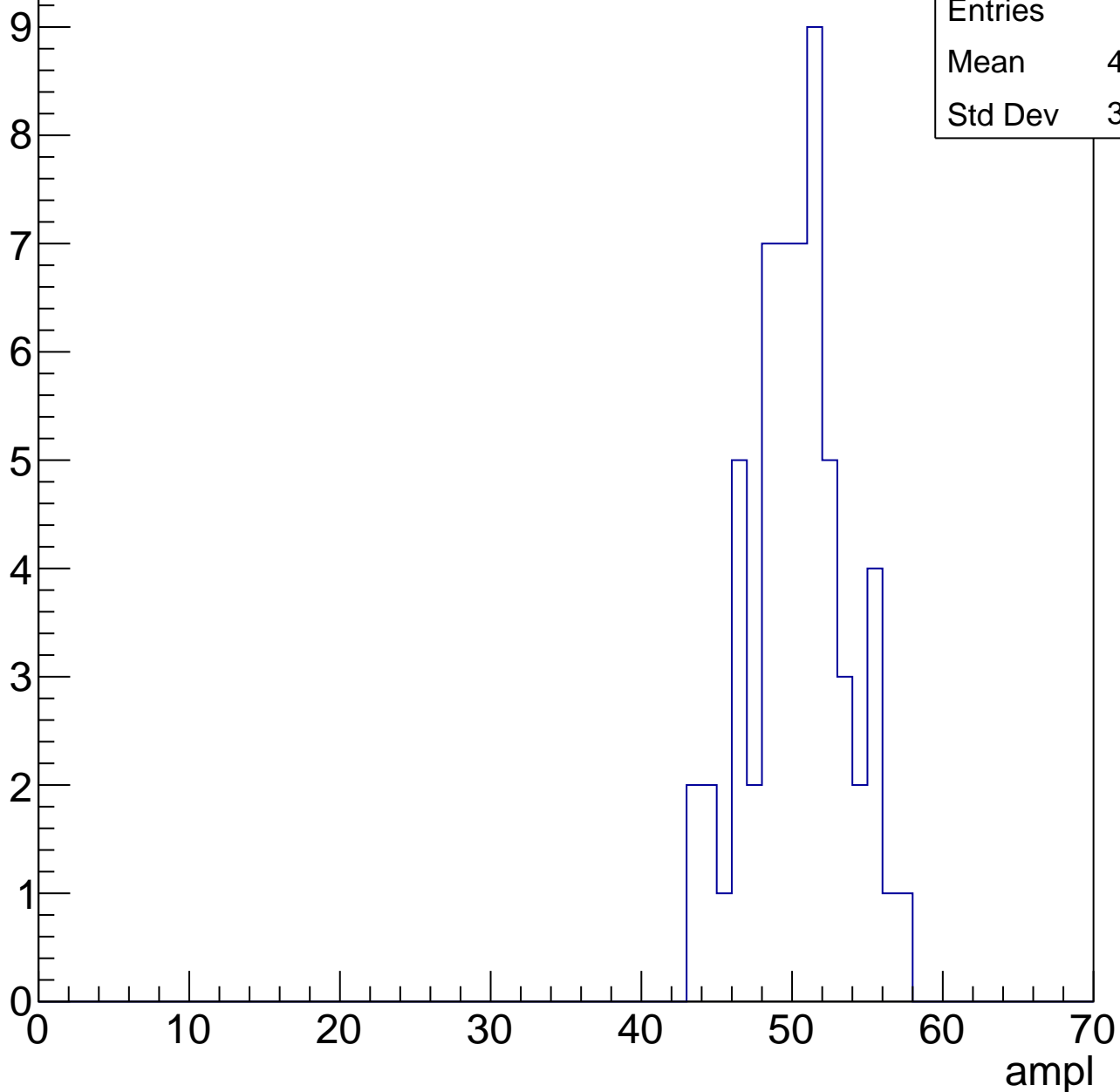
**Gaus Width: 3.3454**



# B1L003S, U23-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

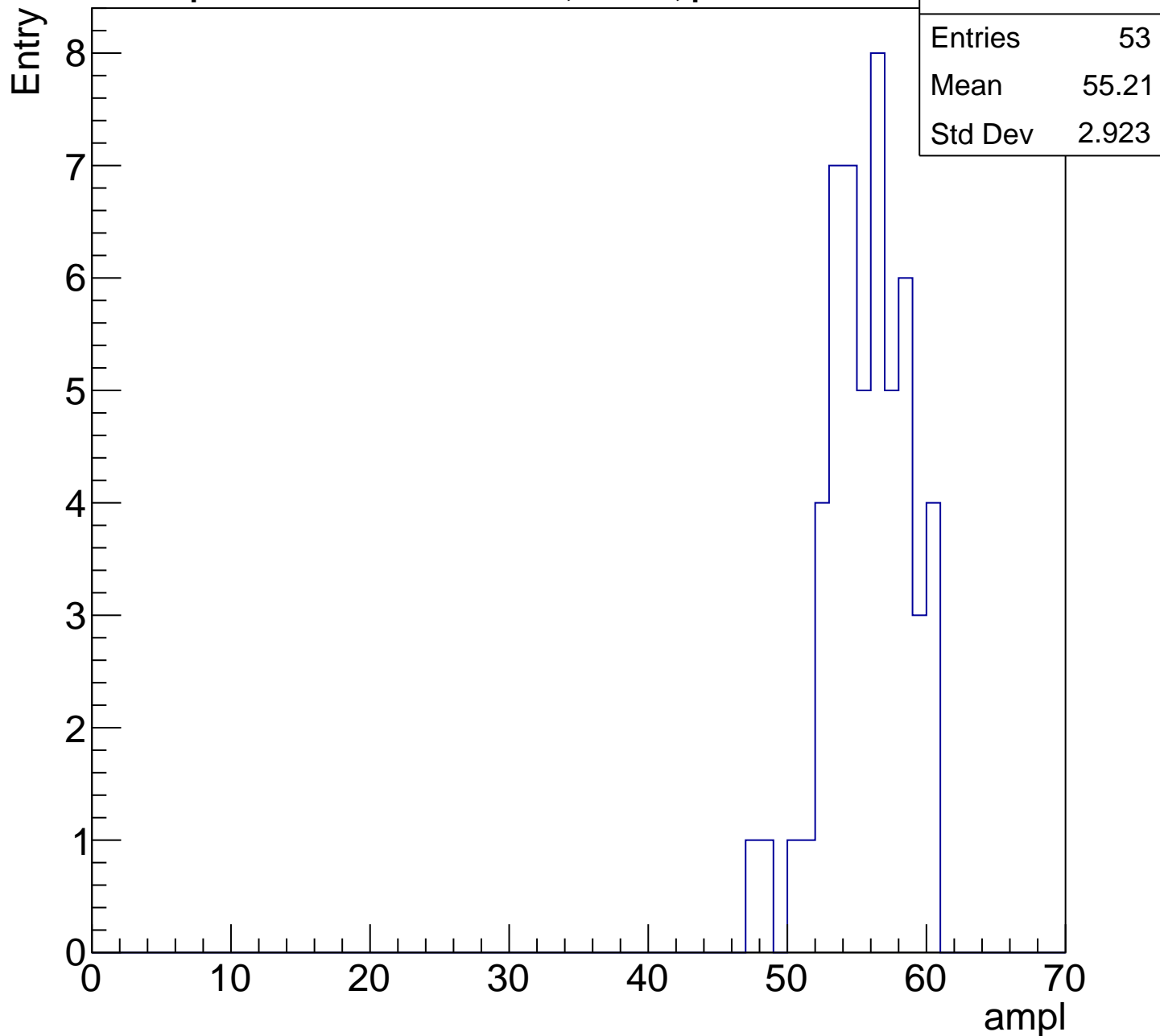
Entry



Entries	58
Mean	49.84
Std Dev	3.215

# B1L003S, U23-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

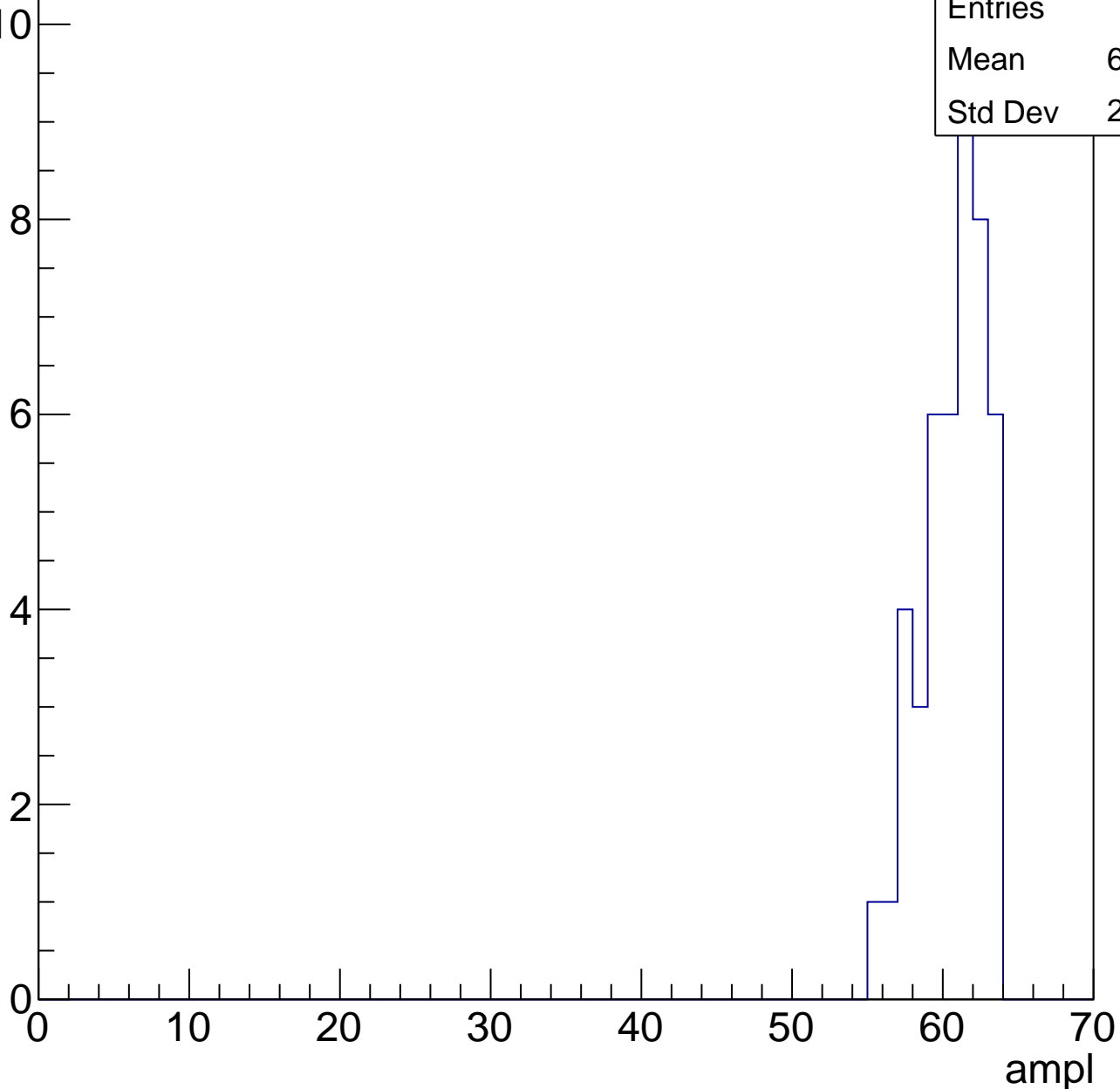


# B1L003S, U23-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

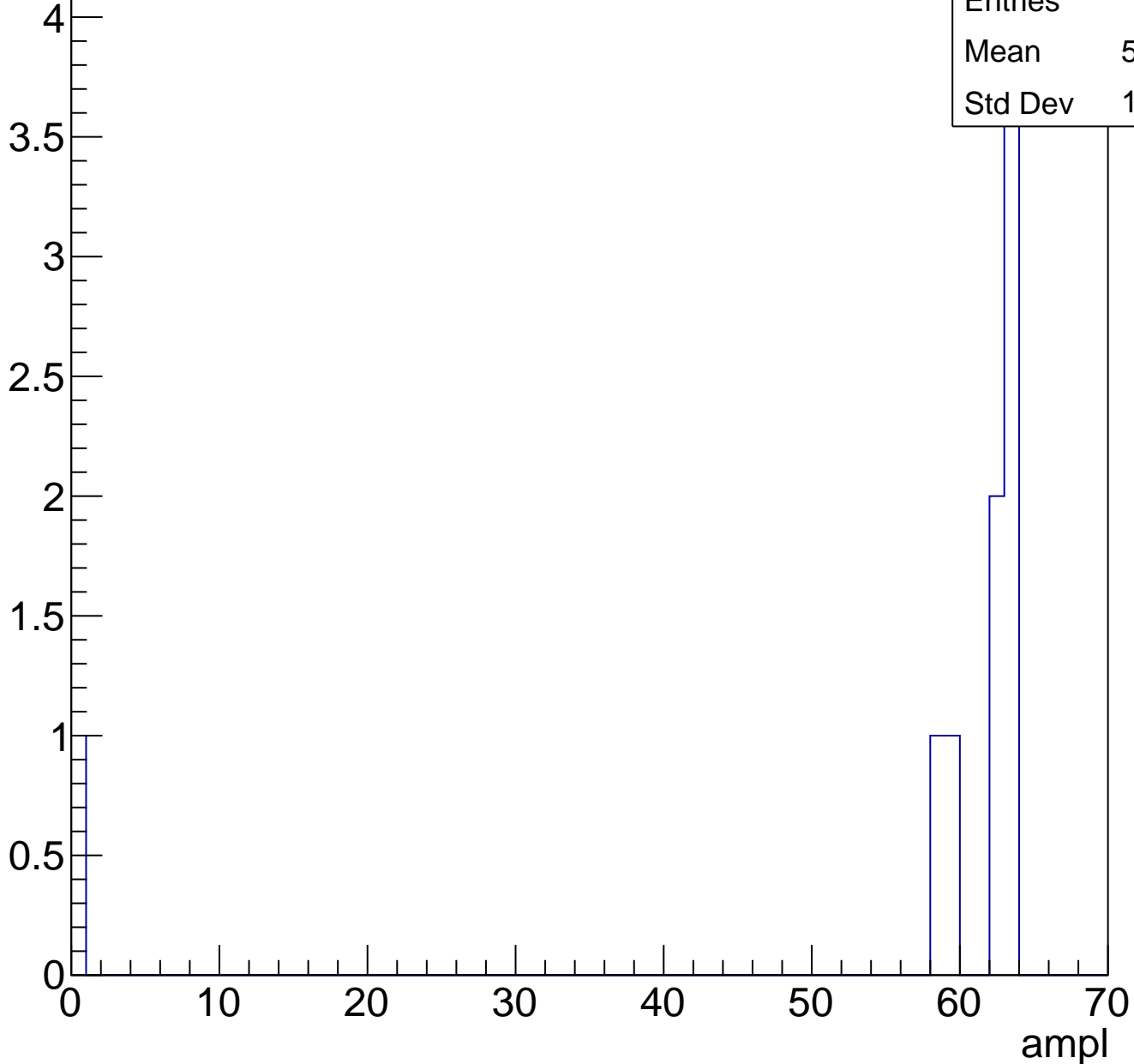
Entries	45
Mean	60.24
Std Dev	2.046



# B1L003S, U23-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	9
Mean	54.78
Std Dev	19.45



# B1L003S, U23-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	28.09
Std Dev	5.795

**Gaus mean : 29.3653**

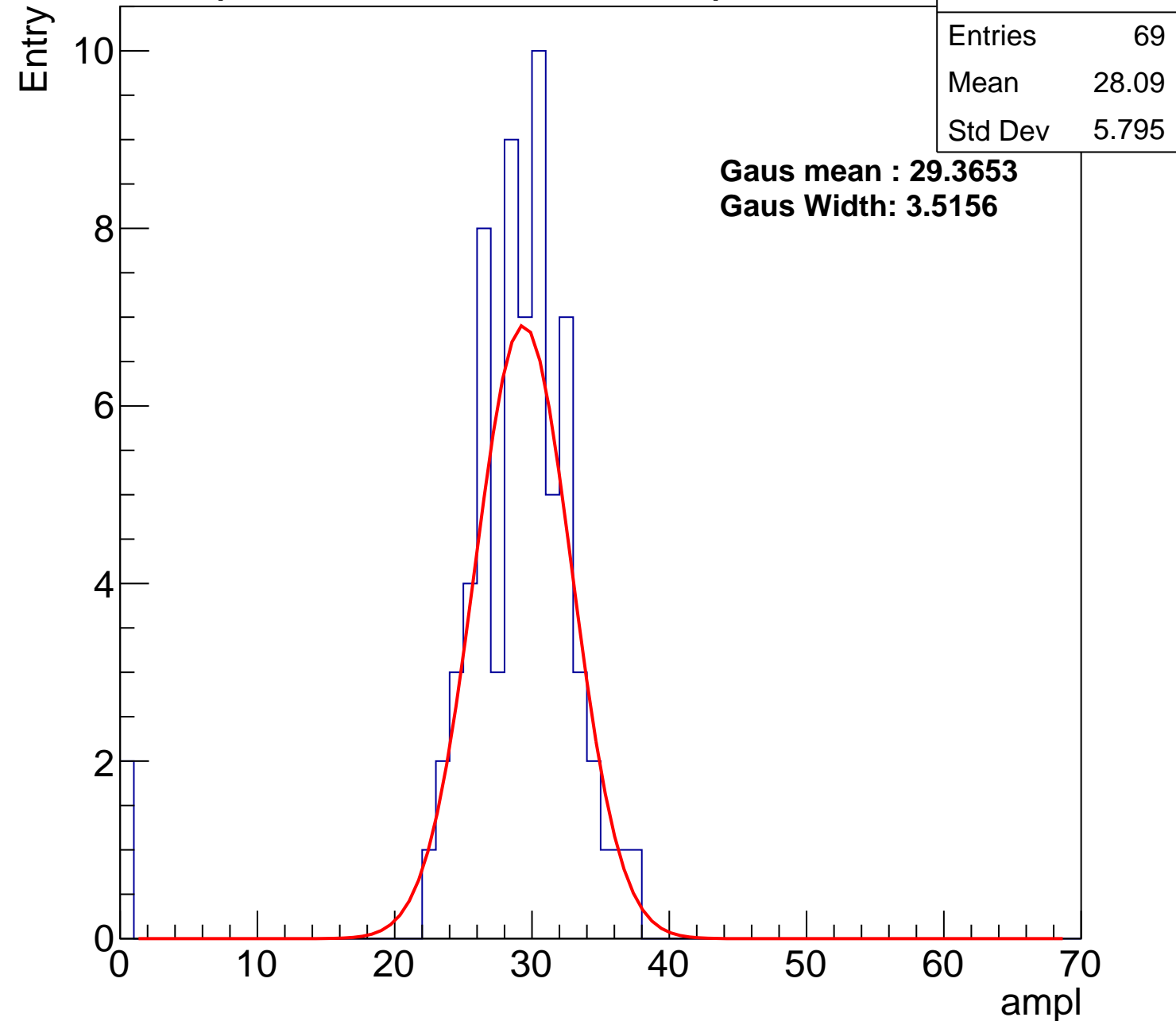
**Gaus Width: 3.5156**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch82, adc1

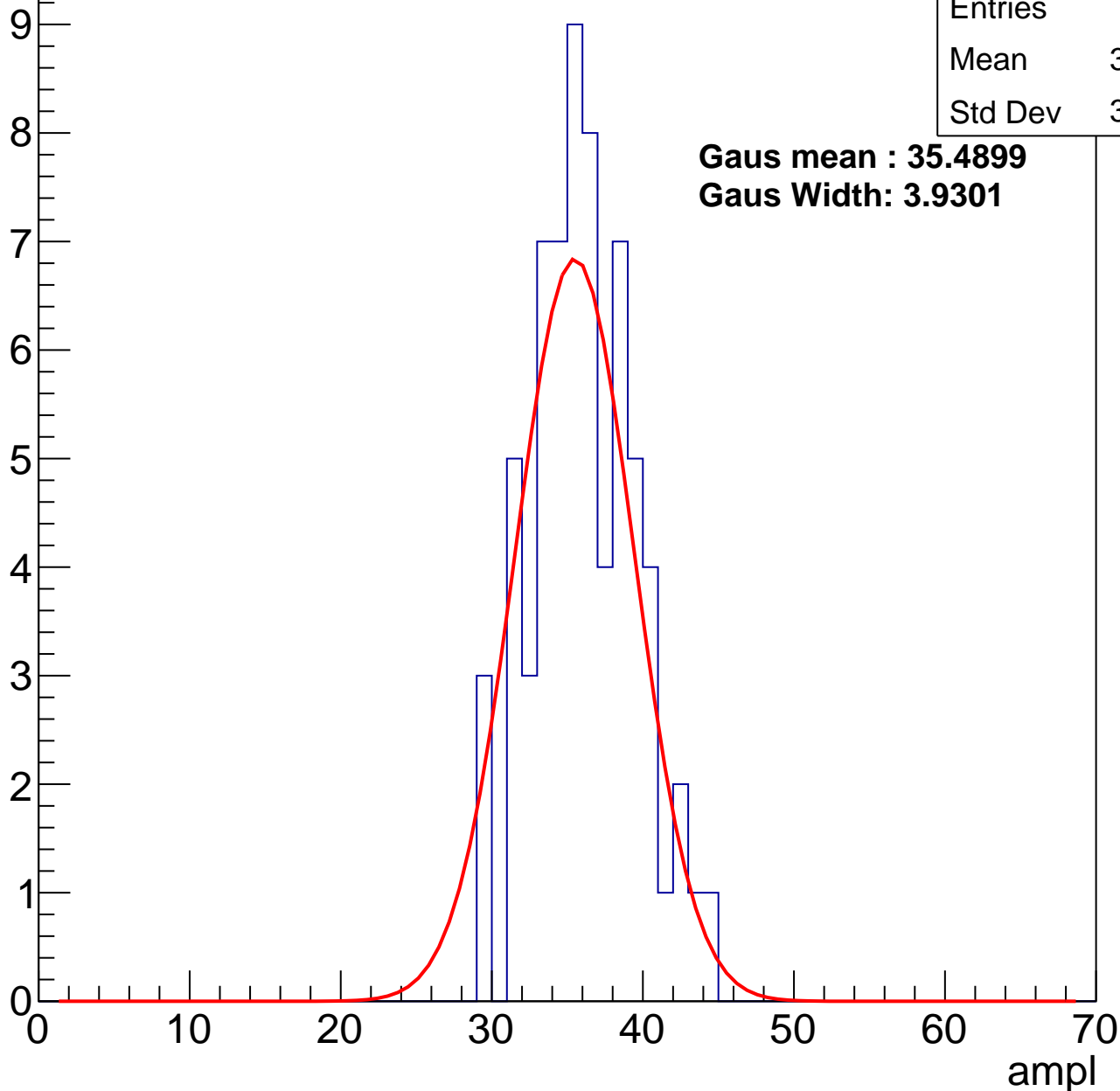
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	35.69
Std Dev	3.378

**Gaus mean : 35.4899**

**Gaus Width: 3.9301**



# B1L003S, U23-ch82, adc2

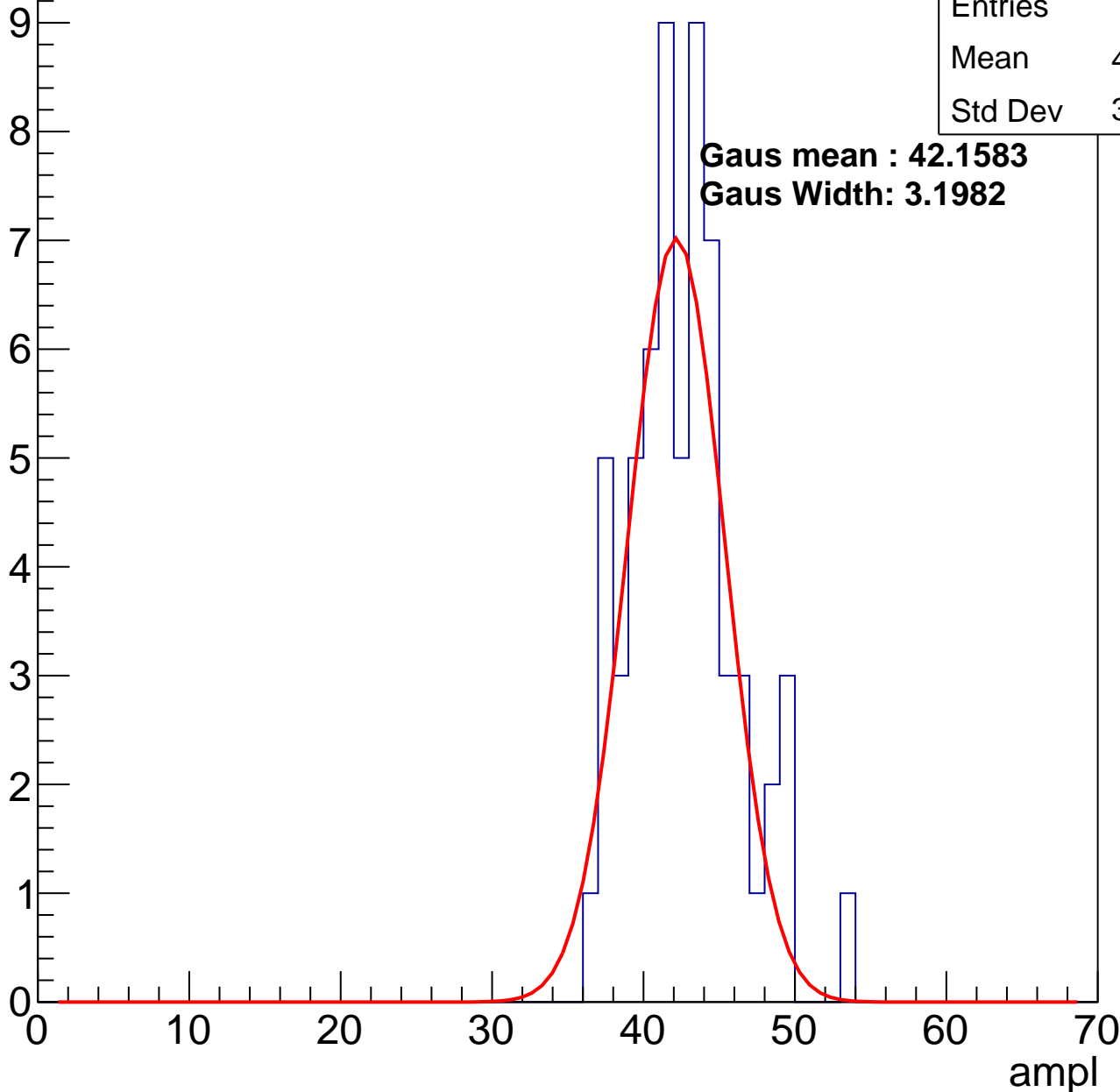
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.22
Std Dev	3.475

**Gaus mean : 42.1583**

**Gaus Width: 3.1982**



# B1L003S, U23-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	48.49
Std Dev	3.097

Entry

10

8

6

4

2

0

0

10

20

30

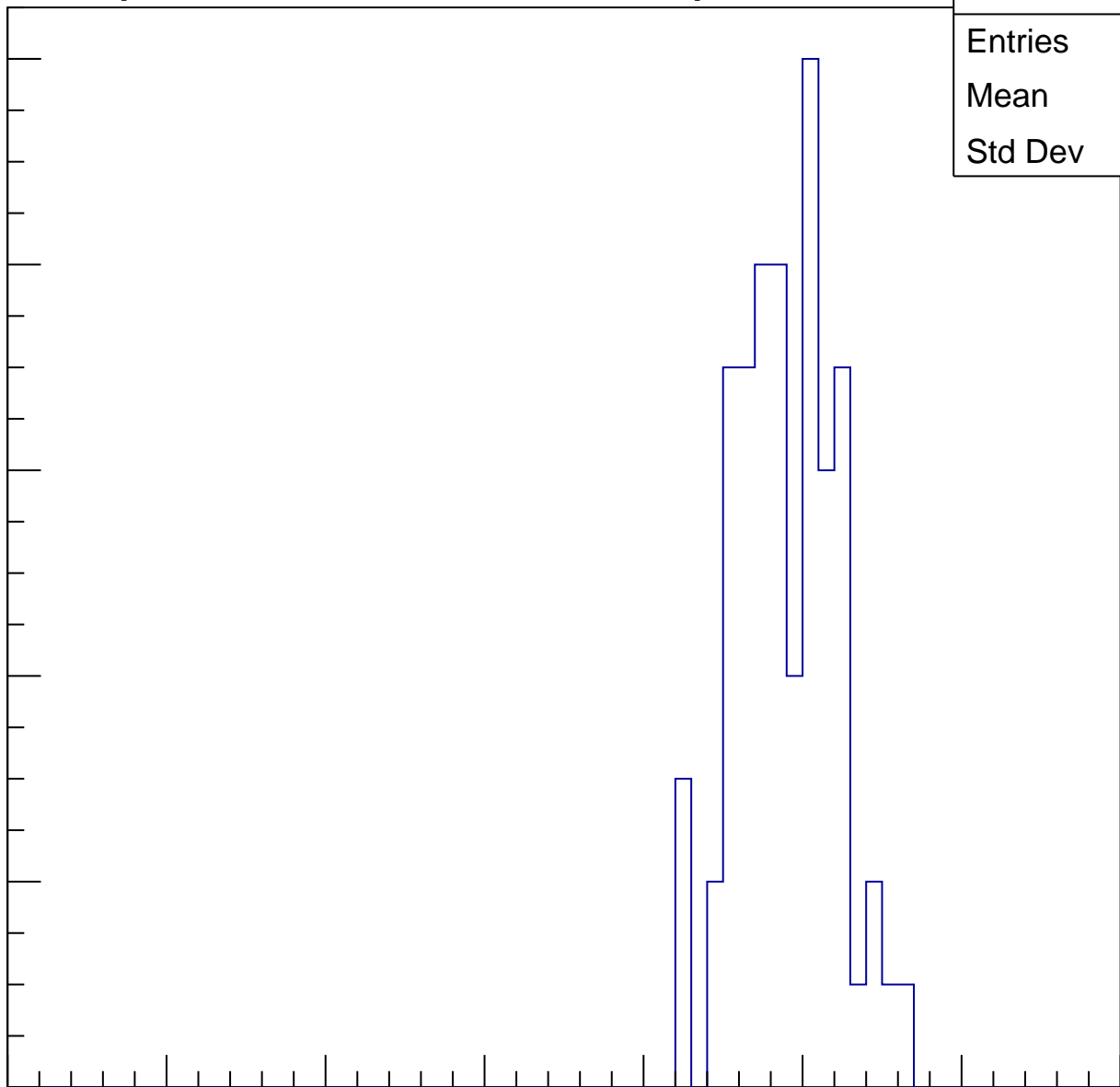
40

50

60

ampl

70

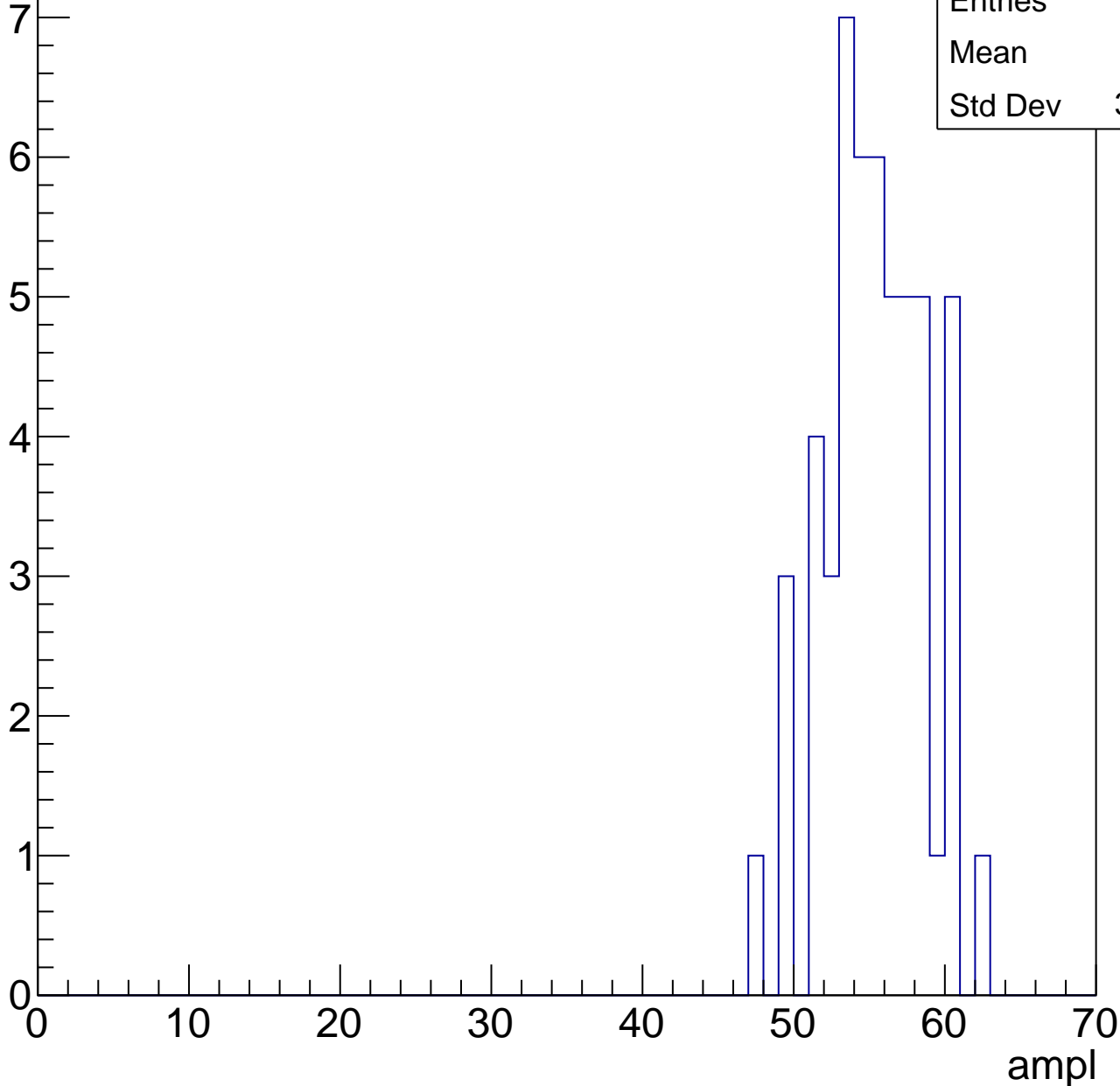


# B1L003S, U23-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

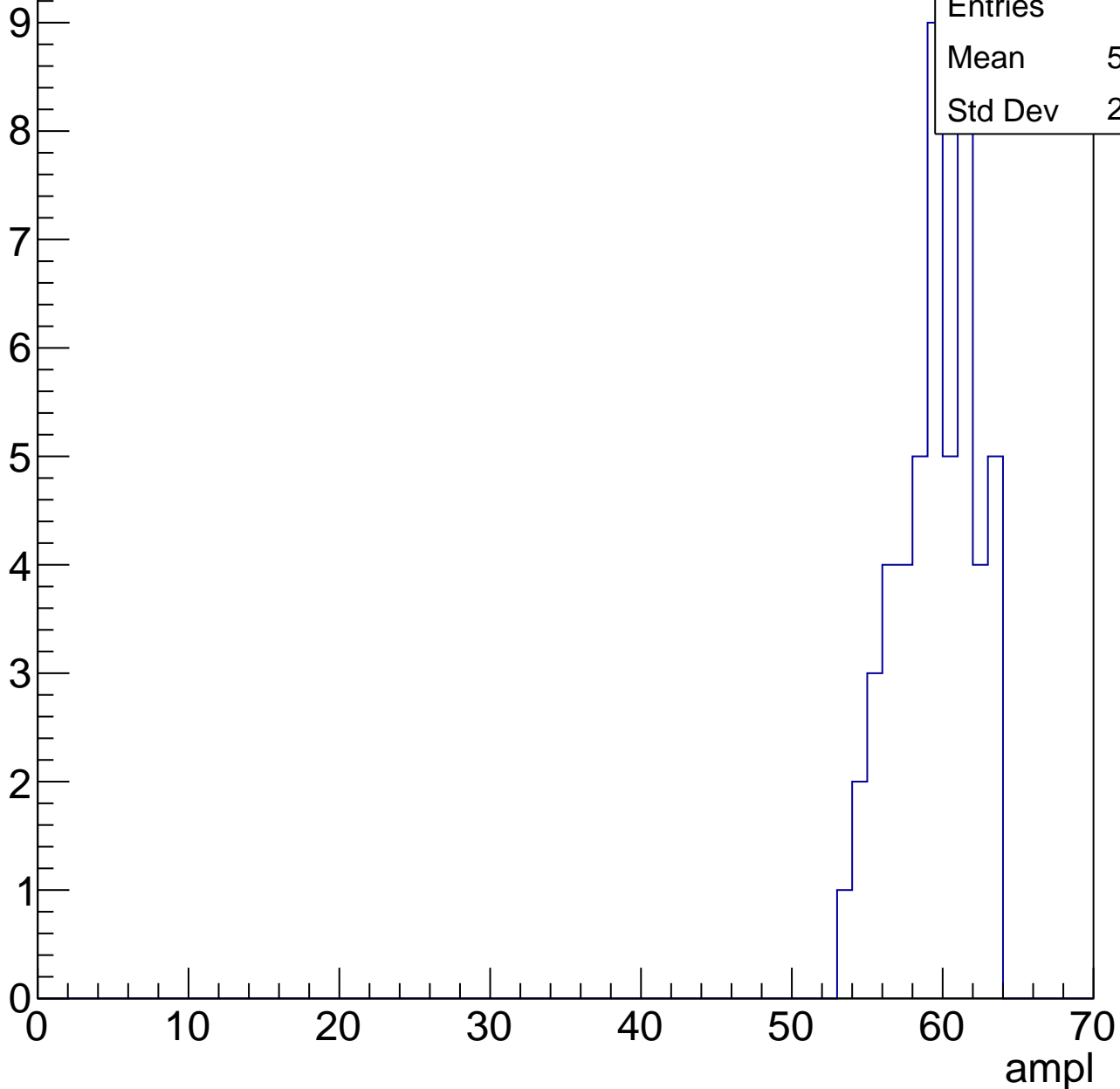
Entries	52
Mean	54.9
Std Dev	3.271



# B1L003S, U23-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



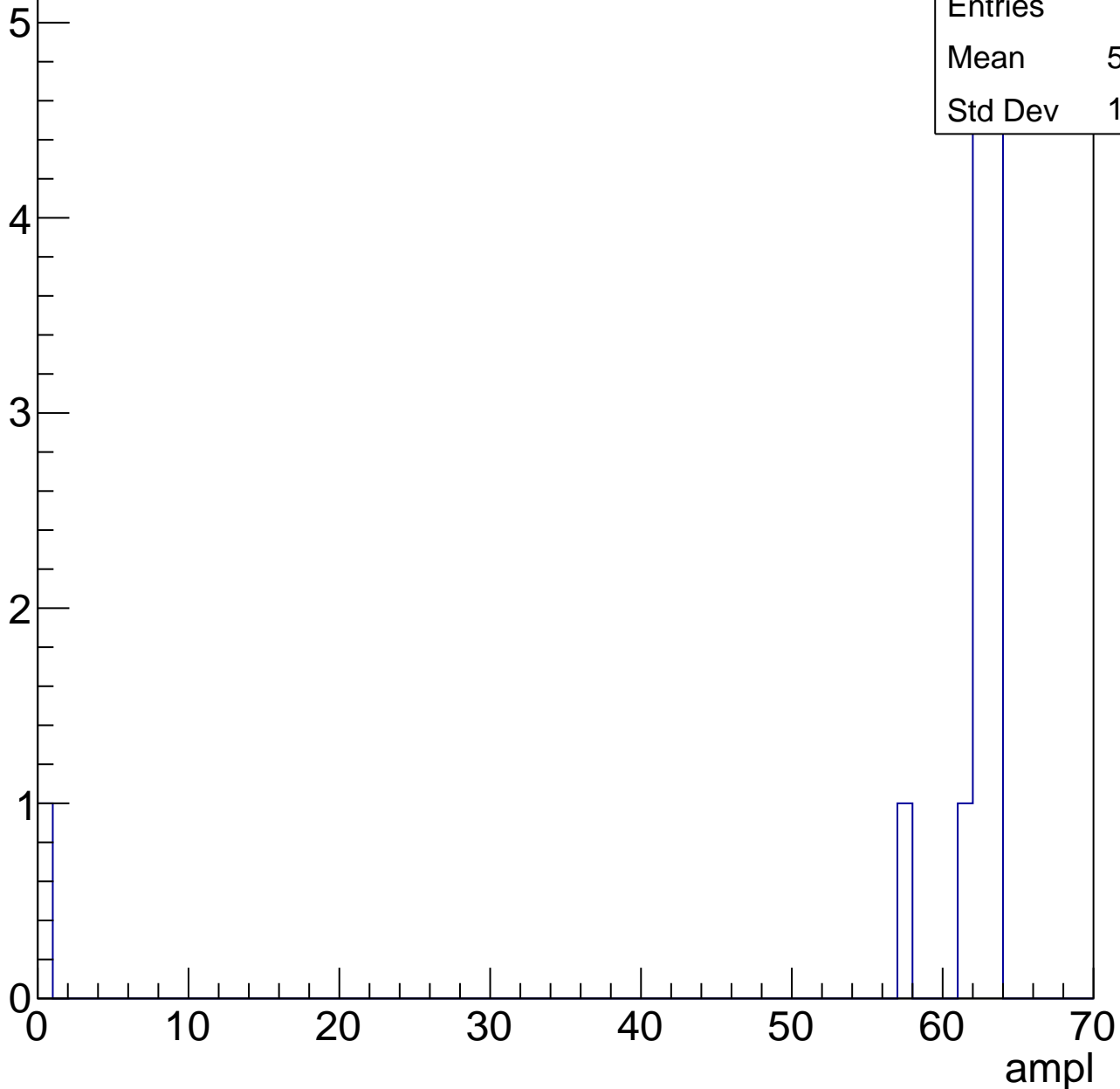
Entries	51
Mean	59.04
Std Dev	2.612

# B1L003S, U23-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	13
Mean	57.15
Std Dev	16.57





# B1L003S, U23-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

# B1L003S, U23-ch83, adc0

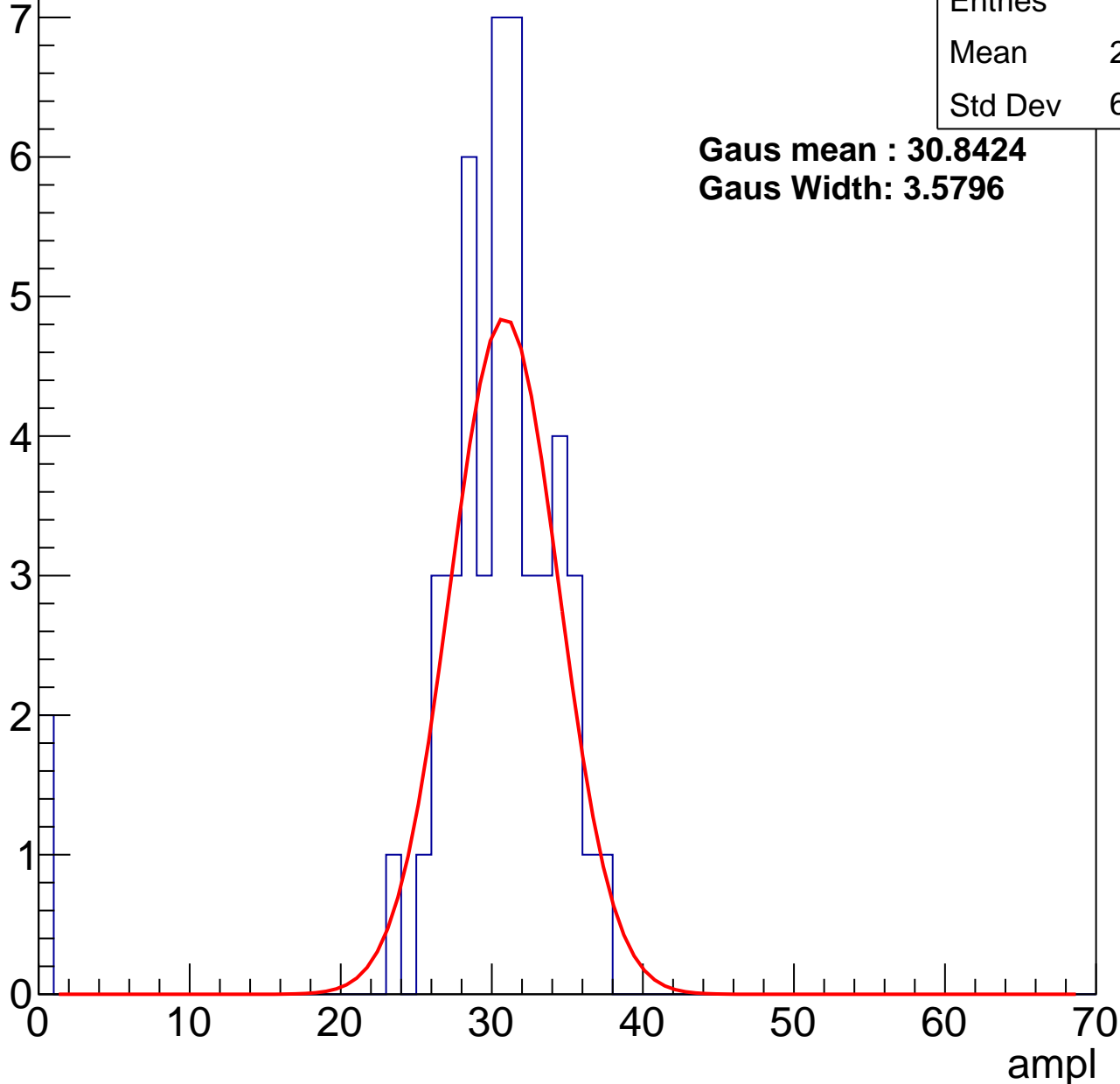
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	29.12
Std Dev	6.784

**Gaus mean : 30.8424**

**Gaus Width: 3.5796**



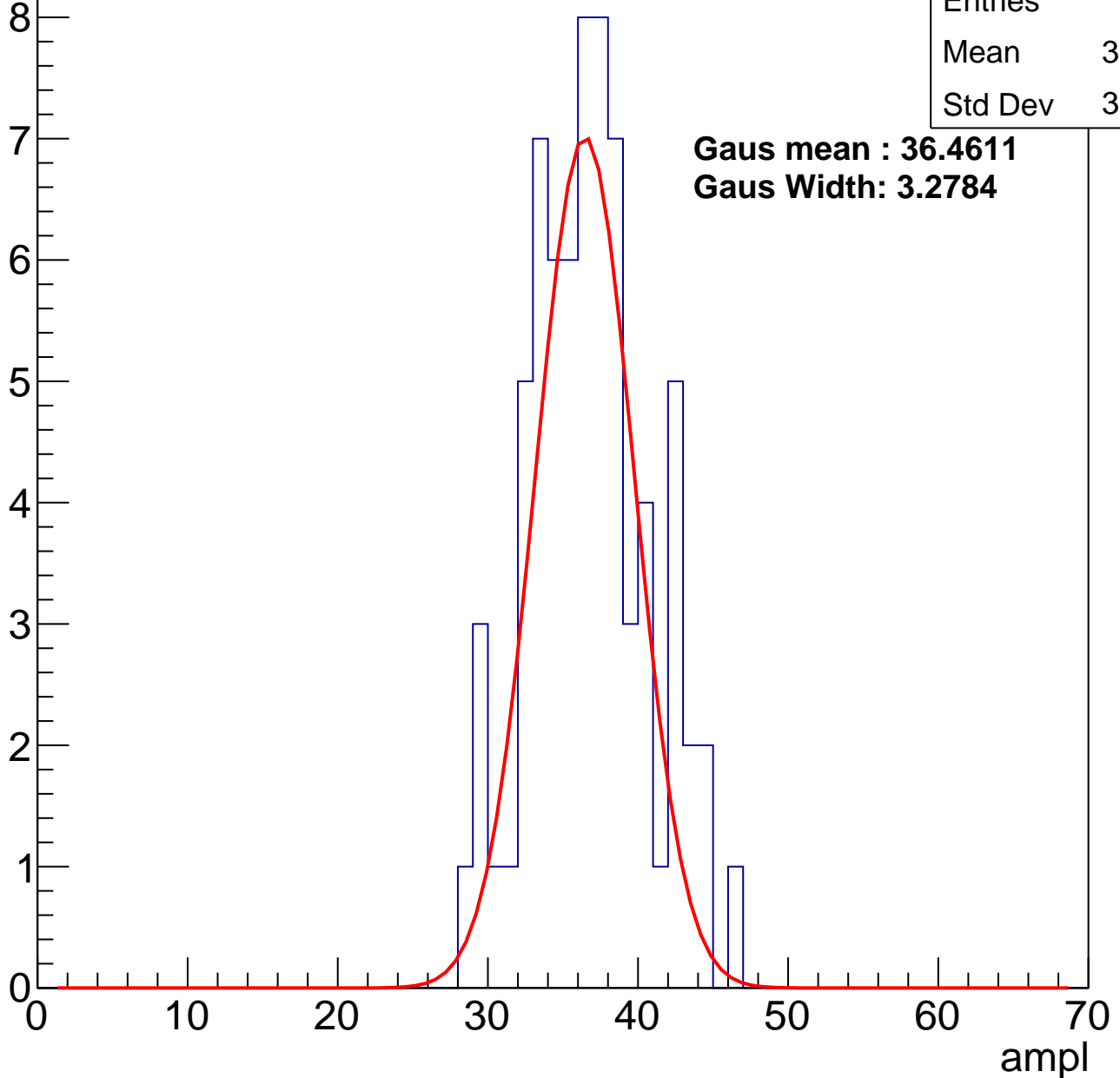
# B1L003S, U23-ch83, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.32
Std Dev	3.928

**Gaus mean : 36.4611**  
**Gaus Width: 3.2784**



# B1L003S, U23-ch83, adc2

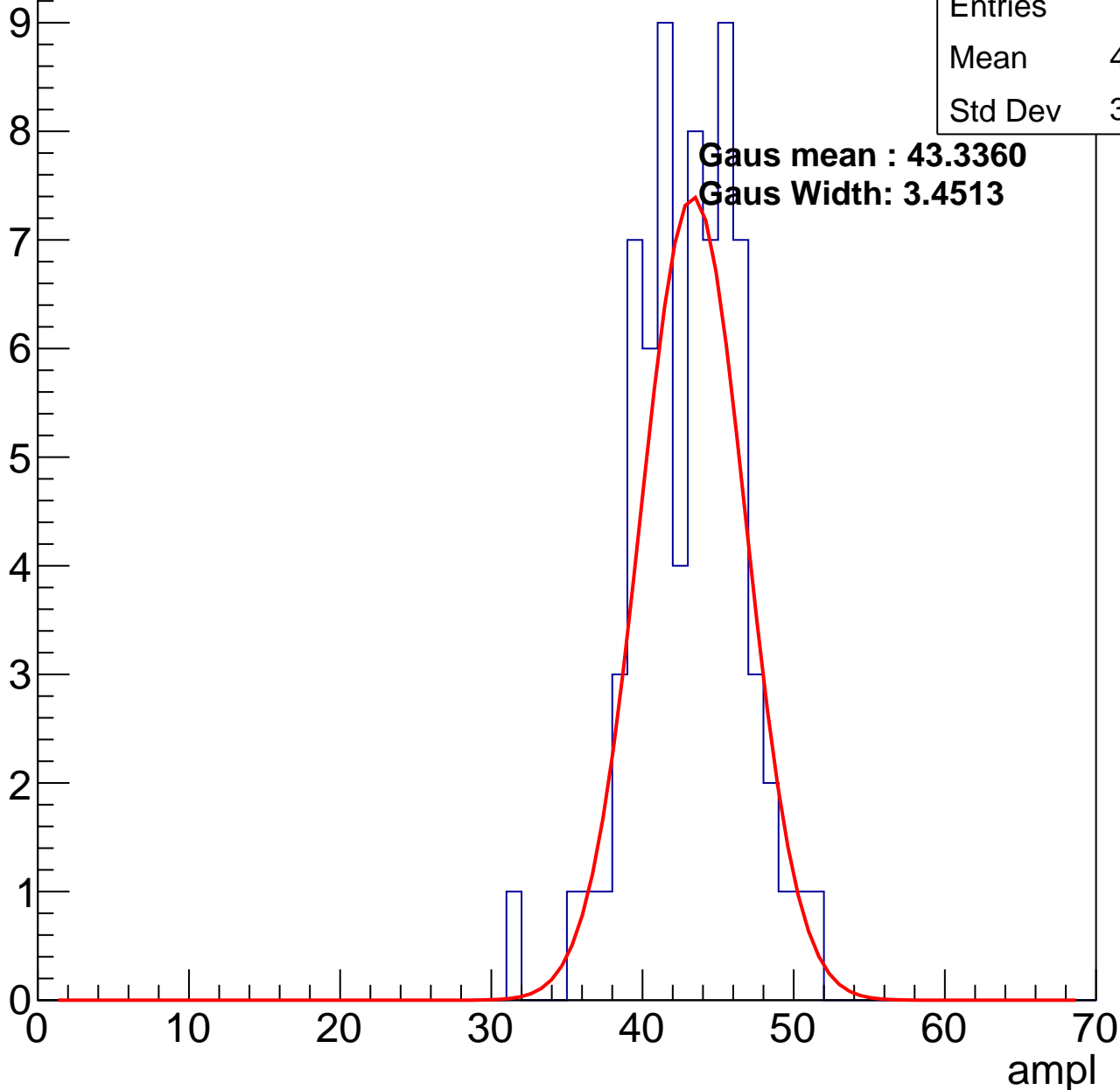
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.62
Std Dev	3.569

**Gaus mean : 43.3360**

**Gaus Width: 3.4513**

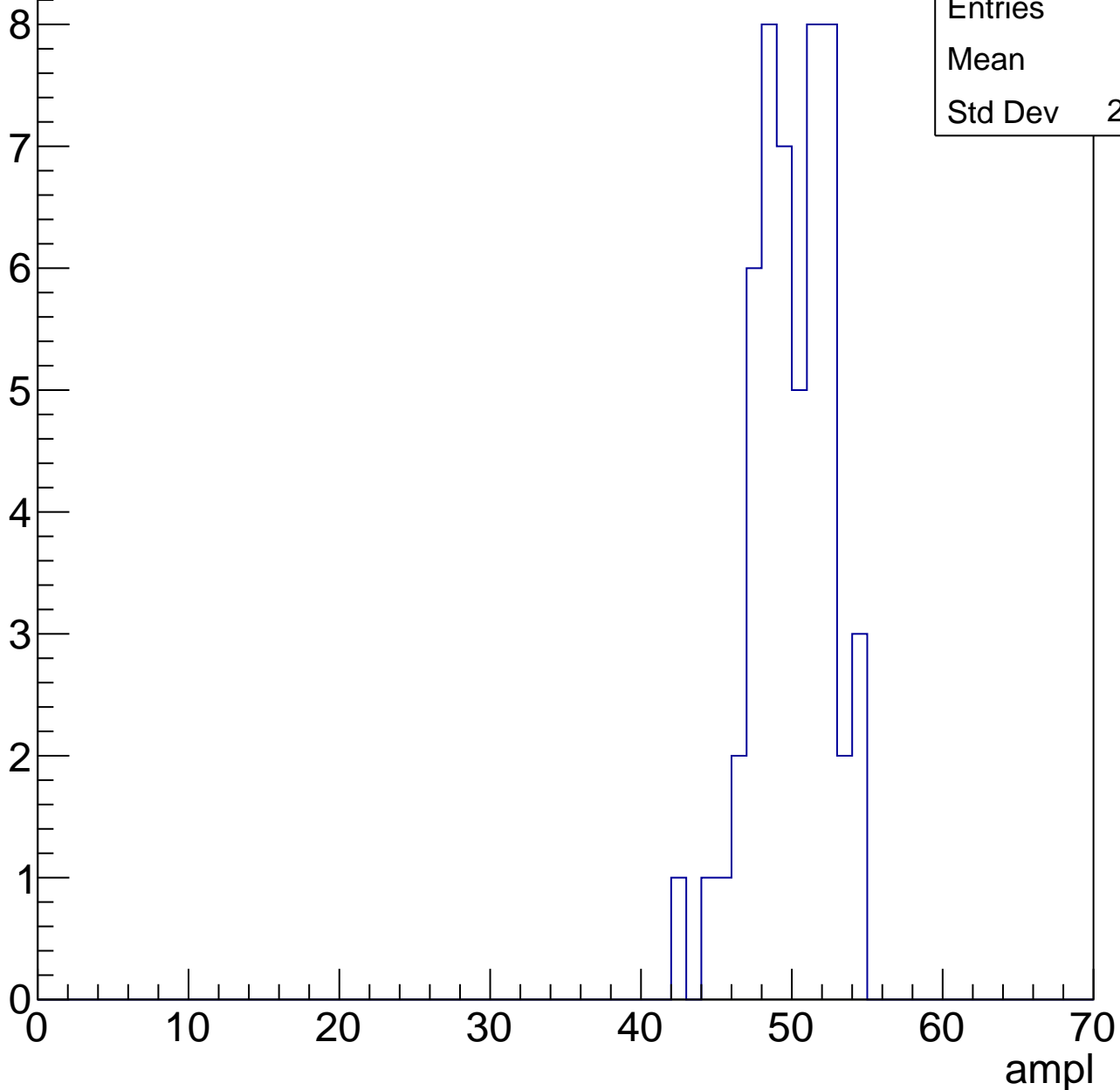


# B1L003S, U23-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

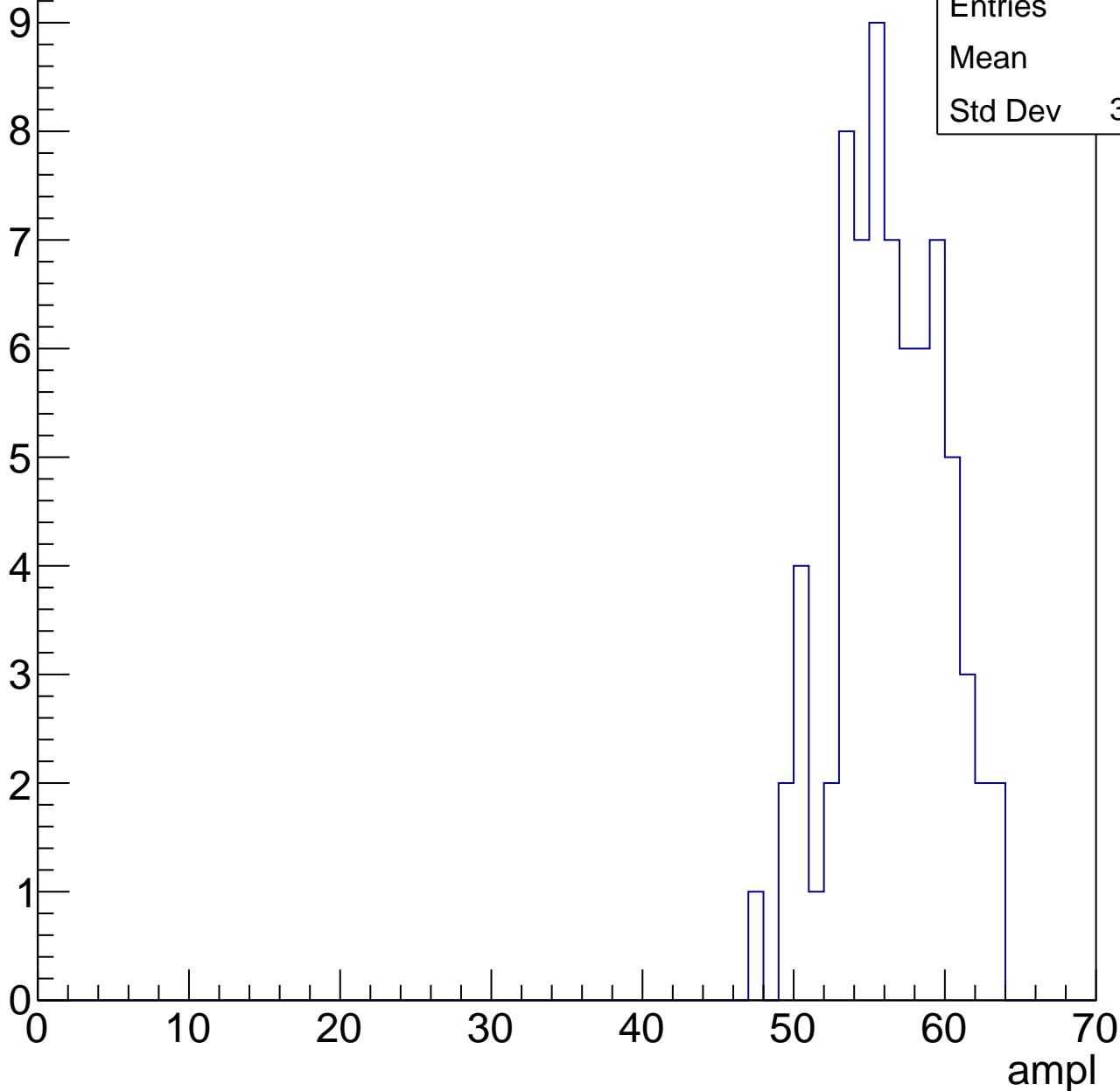
Entries	52
Mean	49.5
Std Dev	2.568



# B1L003S, U23-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



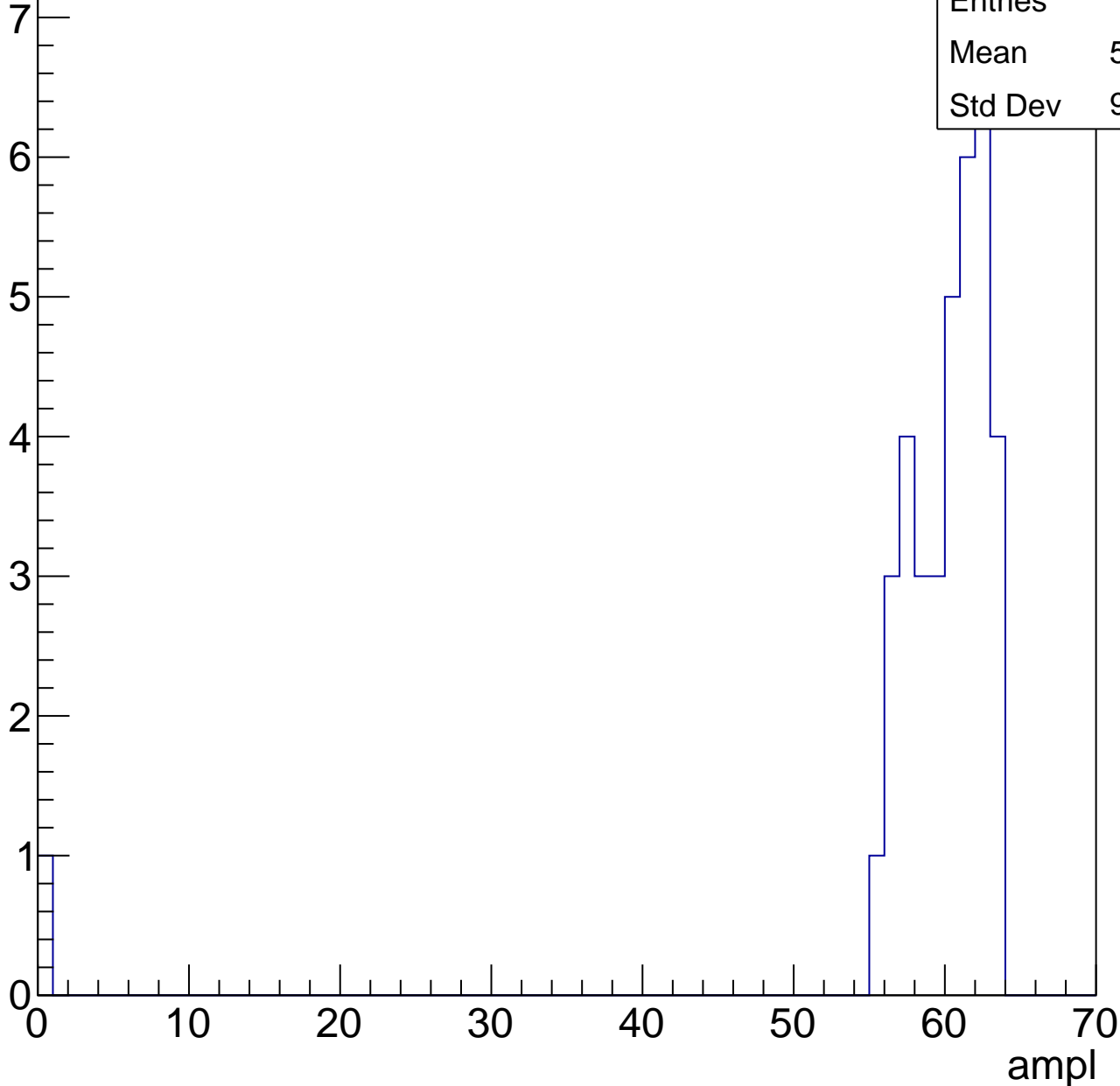
Entries	72
Mean	55.9
Std Dev	3.548

# B1L003S, U23-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

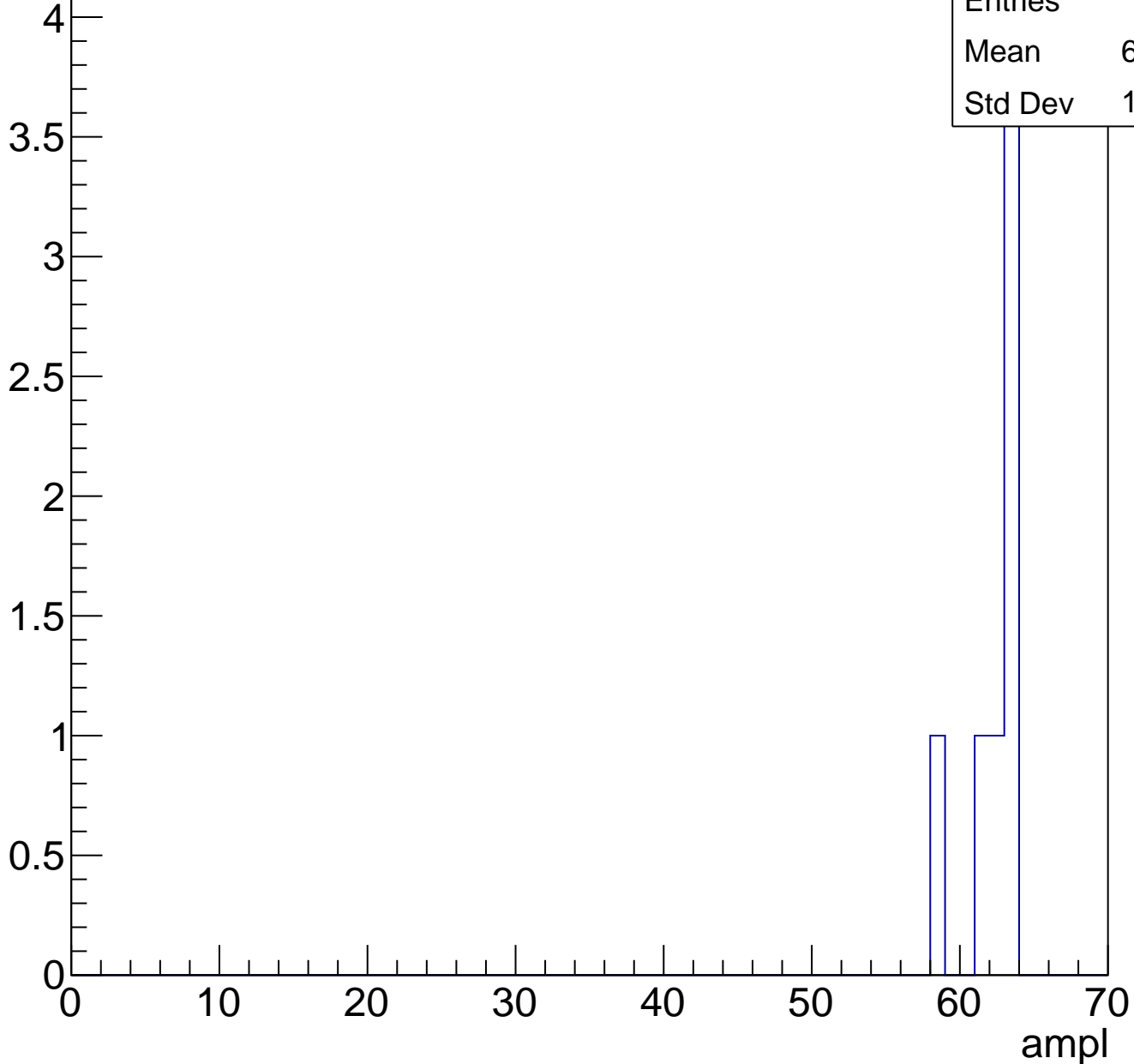
Entries	37
Mean	58.22
Std Dev	9.968



# B1L003S, U23-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch84, adc0

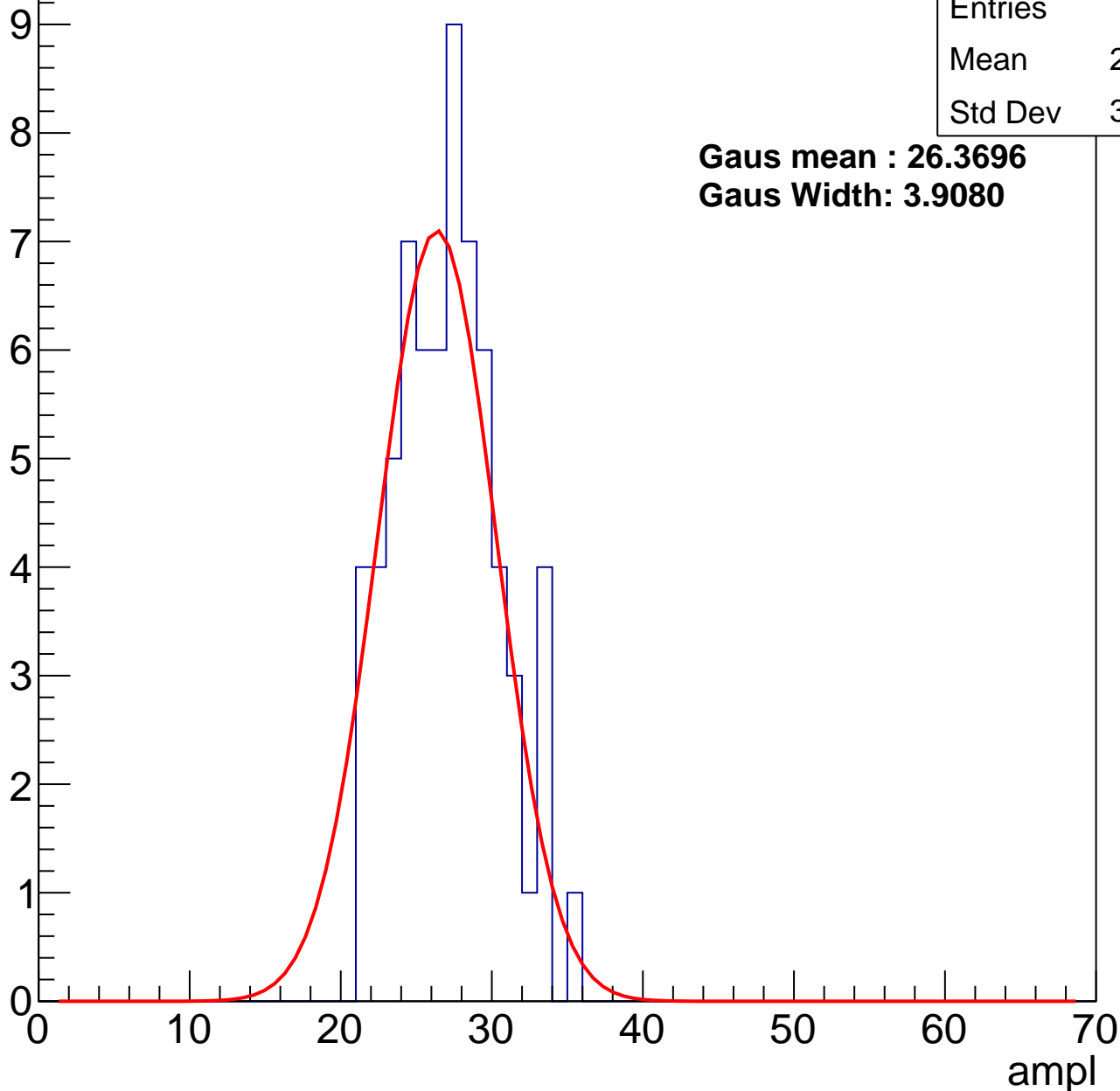
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	26.66
Std Dev	3.362

**Gaus mean : 26.3696**

**Gaus Width: 3.9080**



# B1L003S, U23-ch84, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	76
Mean	33.55
Std Dev	3.45

**Gaus mean : 33.7943**

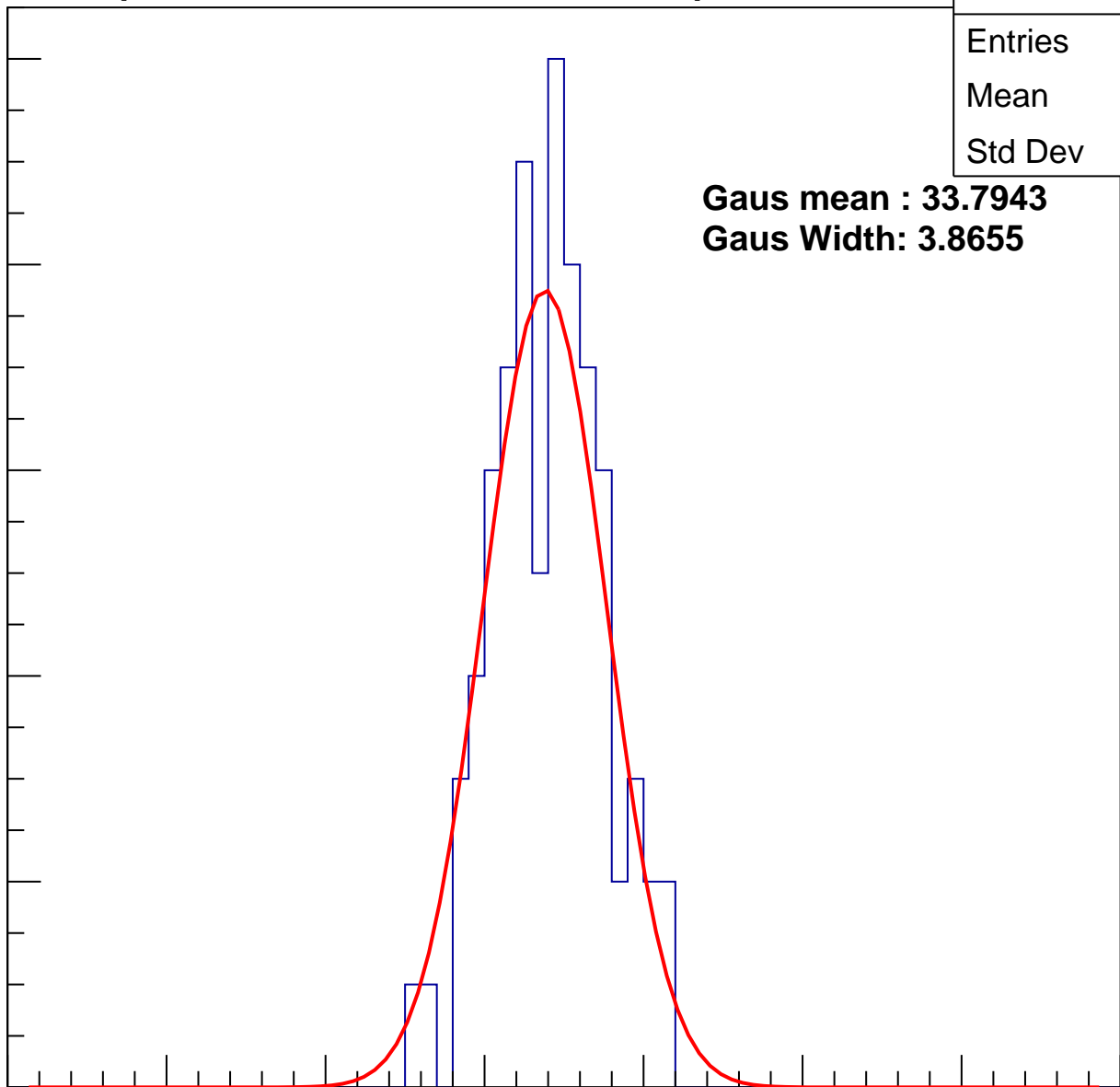
**Gaus Width: 3.8655**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch84, adc2

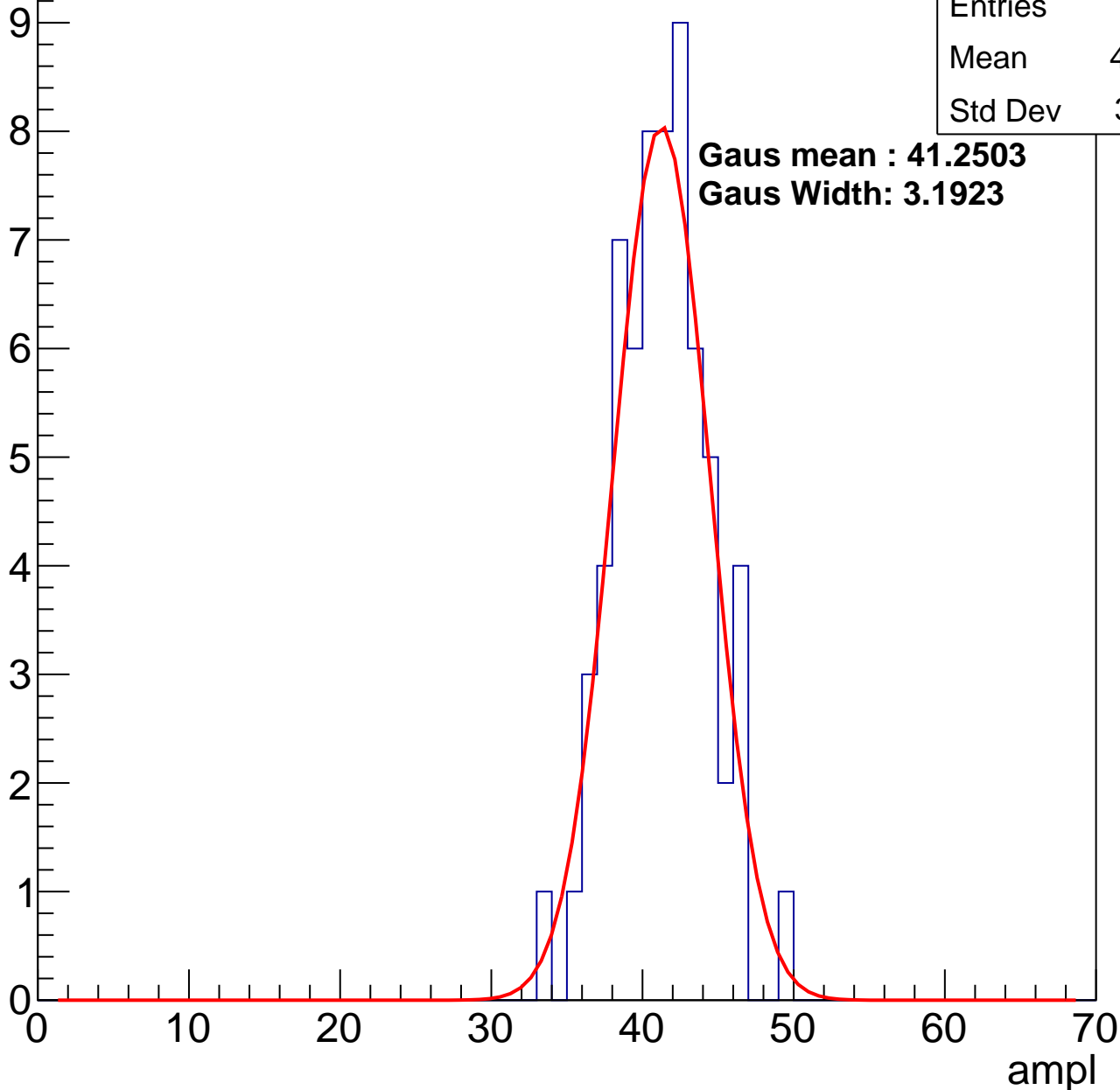
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	40.78
Std Dev	3.051

**Gaus mean : 41.2503**

**Gaus Width: 3.1923**

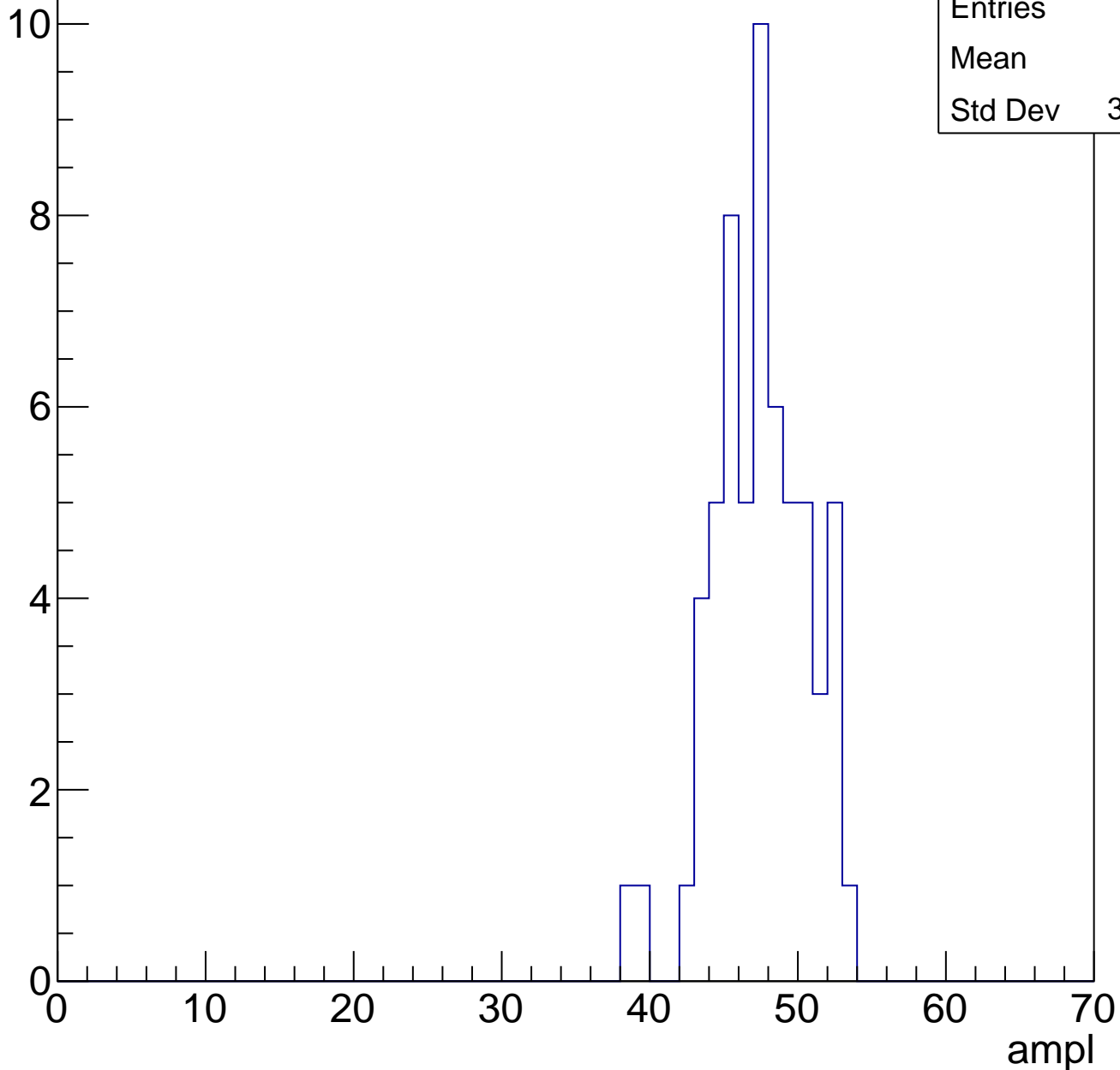


# B1L003S, U23-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	60
Mean	47
Std Dev	3.152

Entry

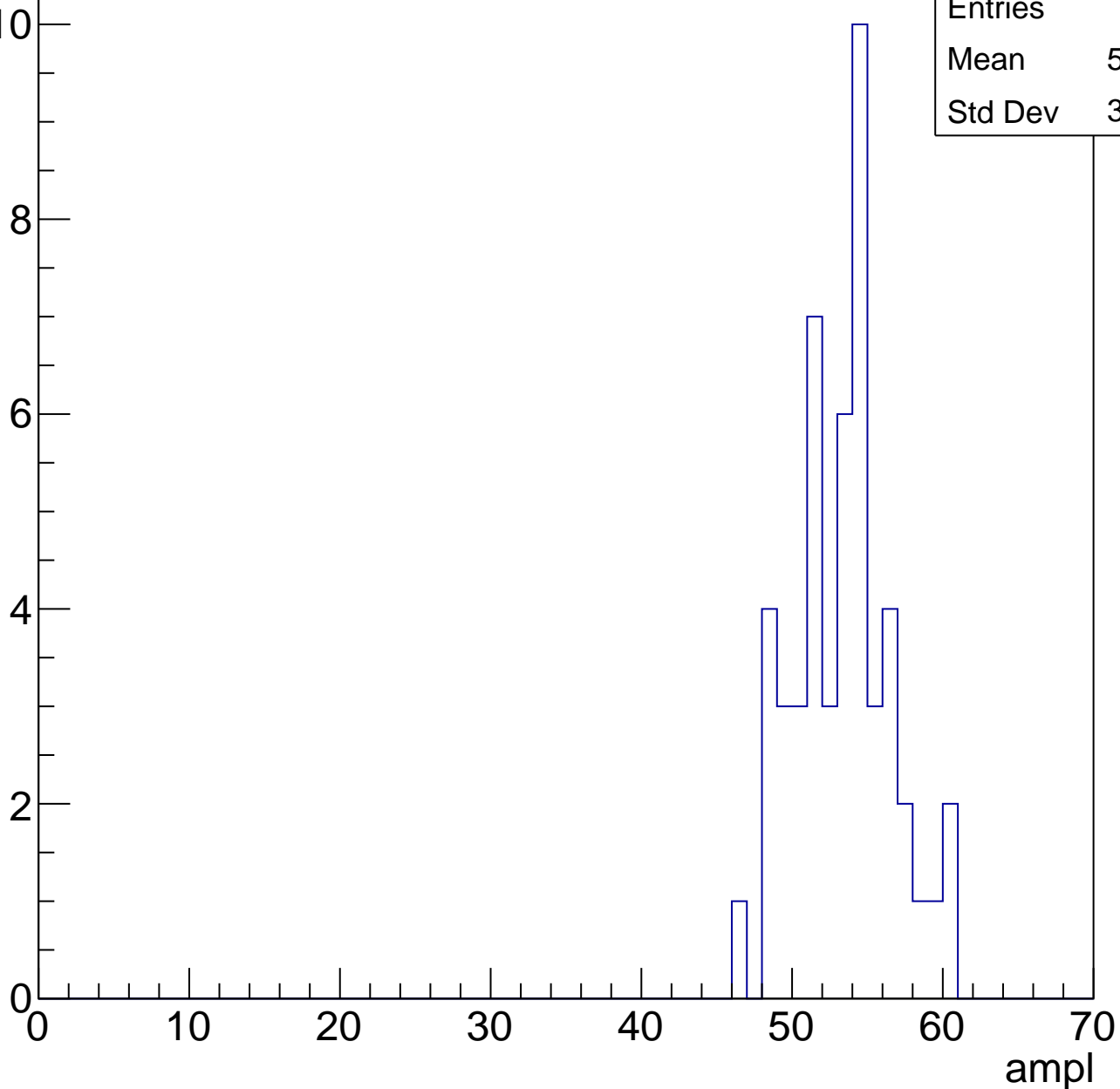


# B1L003S, U23-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	52.92
Std Dev	3.174

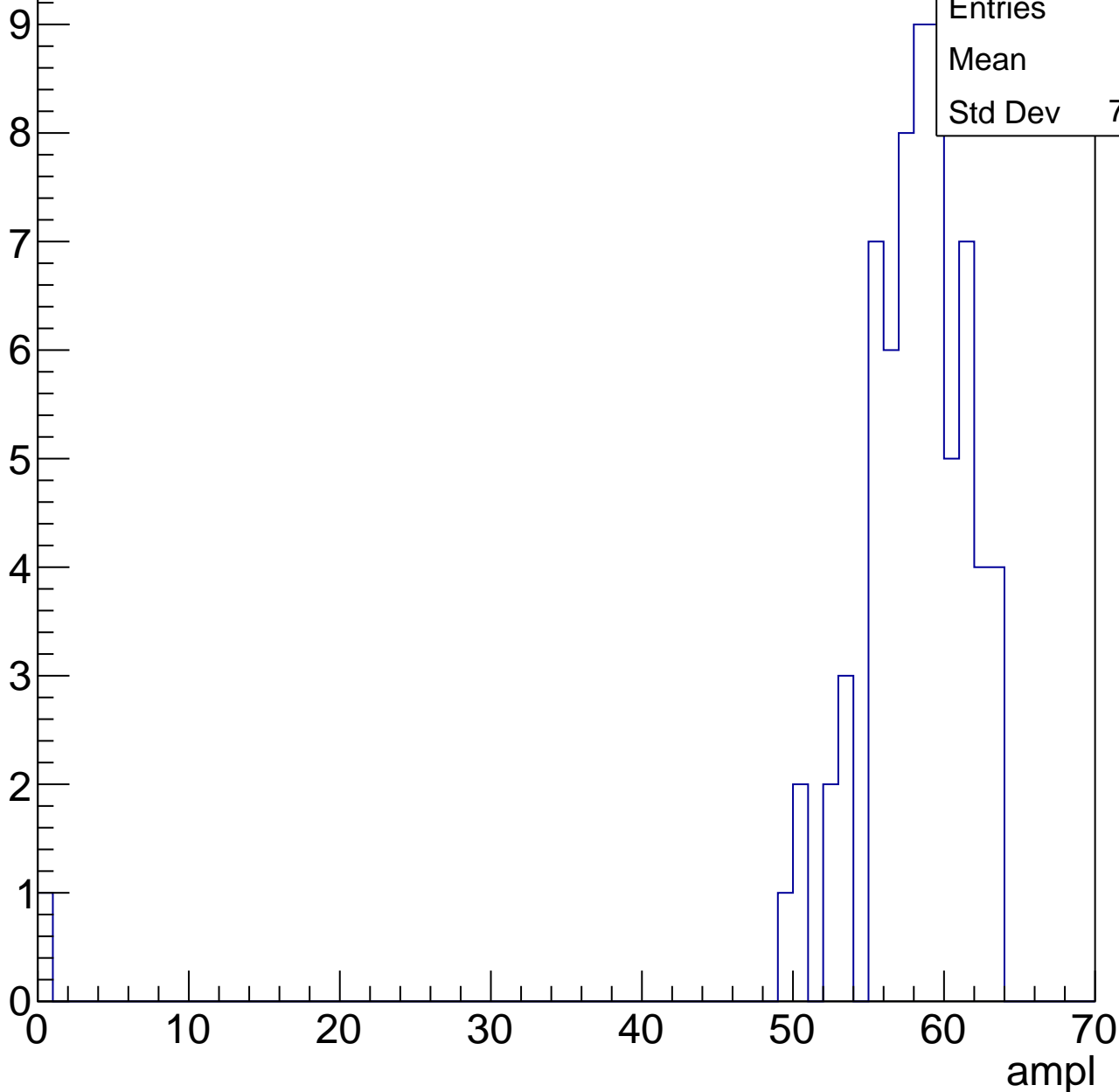


# B1L003S, U23-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	56.9
Std Dev	7.658

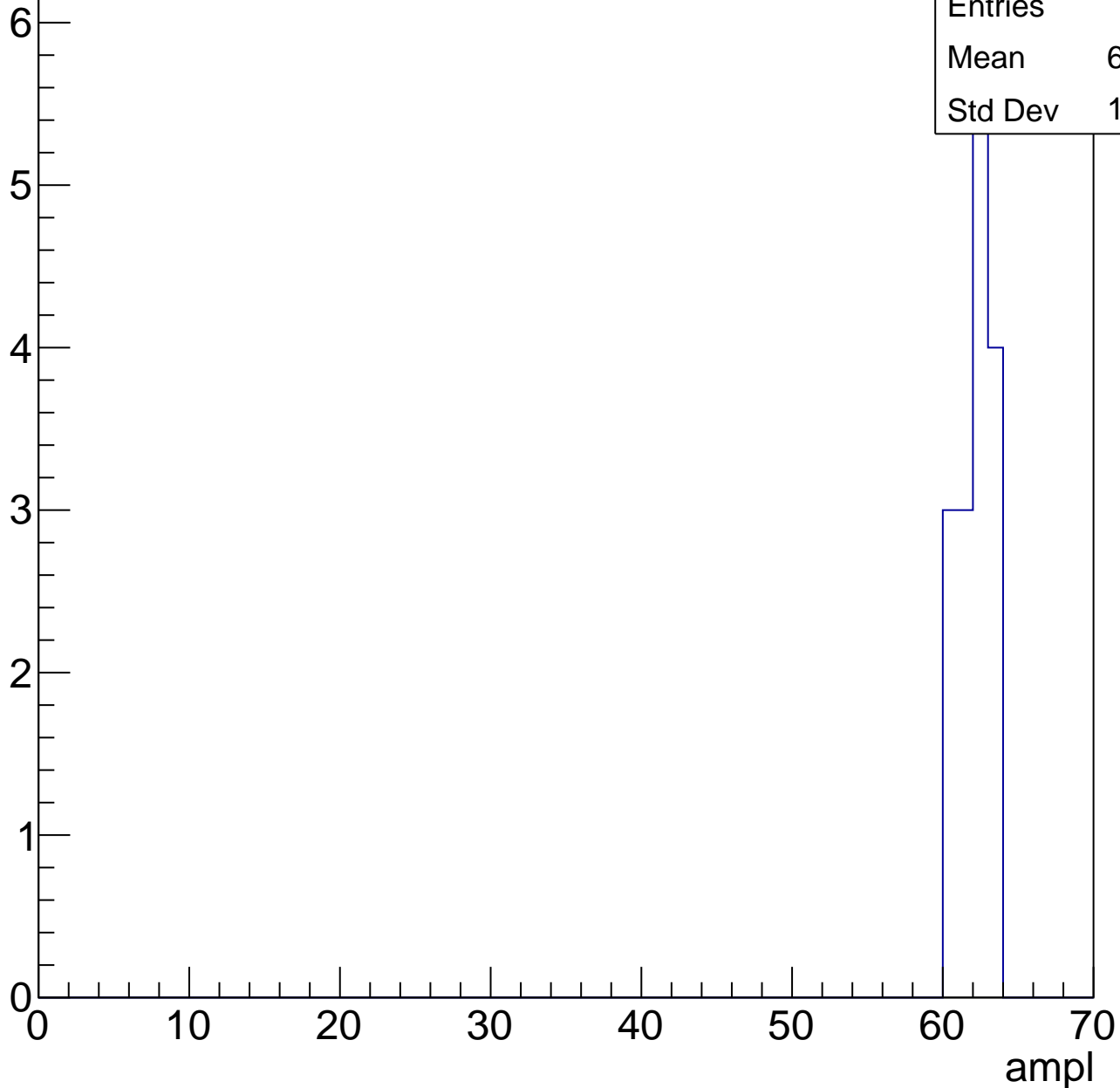


# B1L003S, U23-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	16
Mean	61.69
Std Dev	1.044

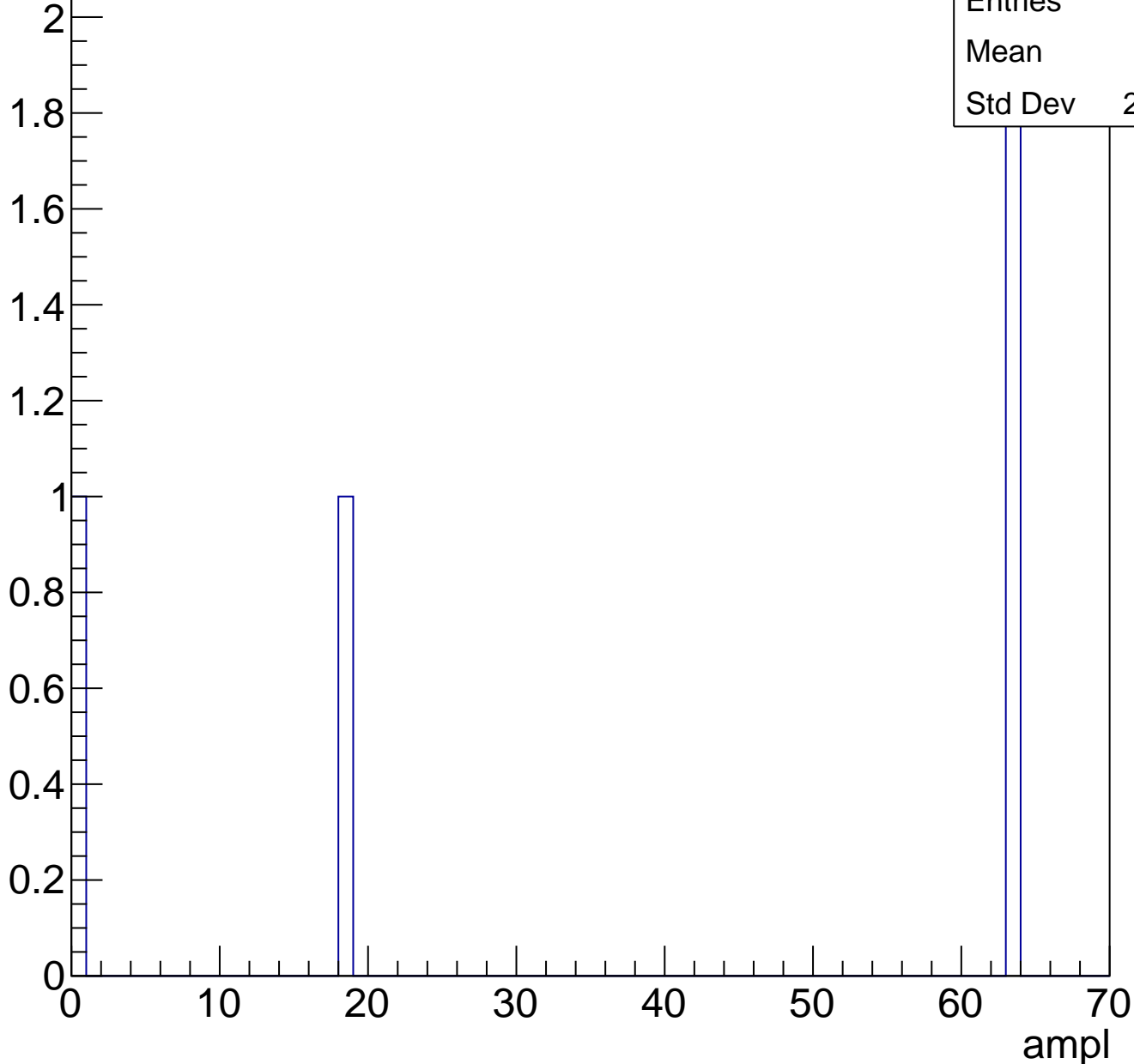




# B1L003S, U23-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch85, adc0

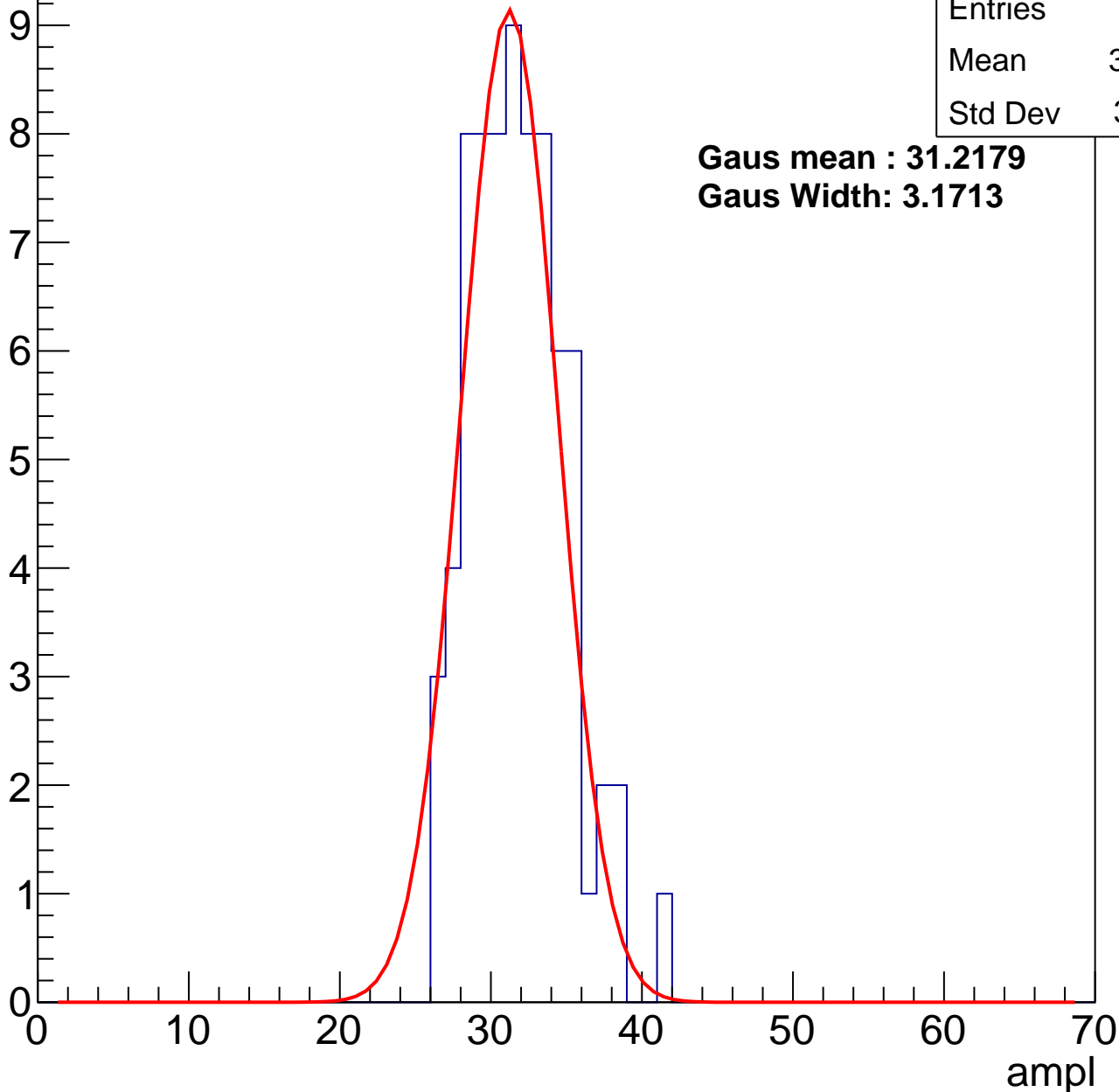
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	31.38
Std Dev	3.131

**Gaus mean : 31.2179**

**Gaus Width: 3.1713**



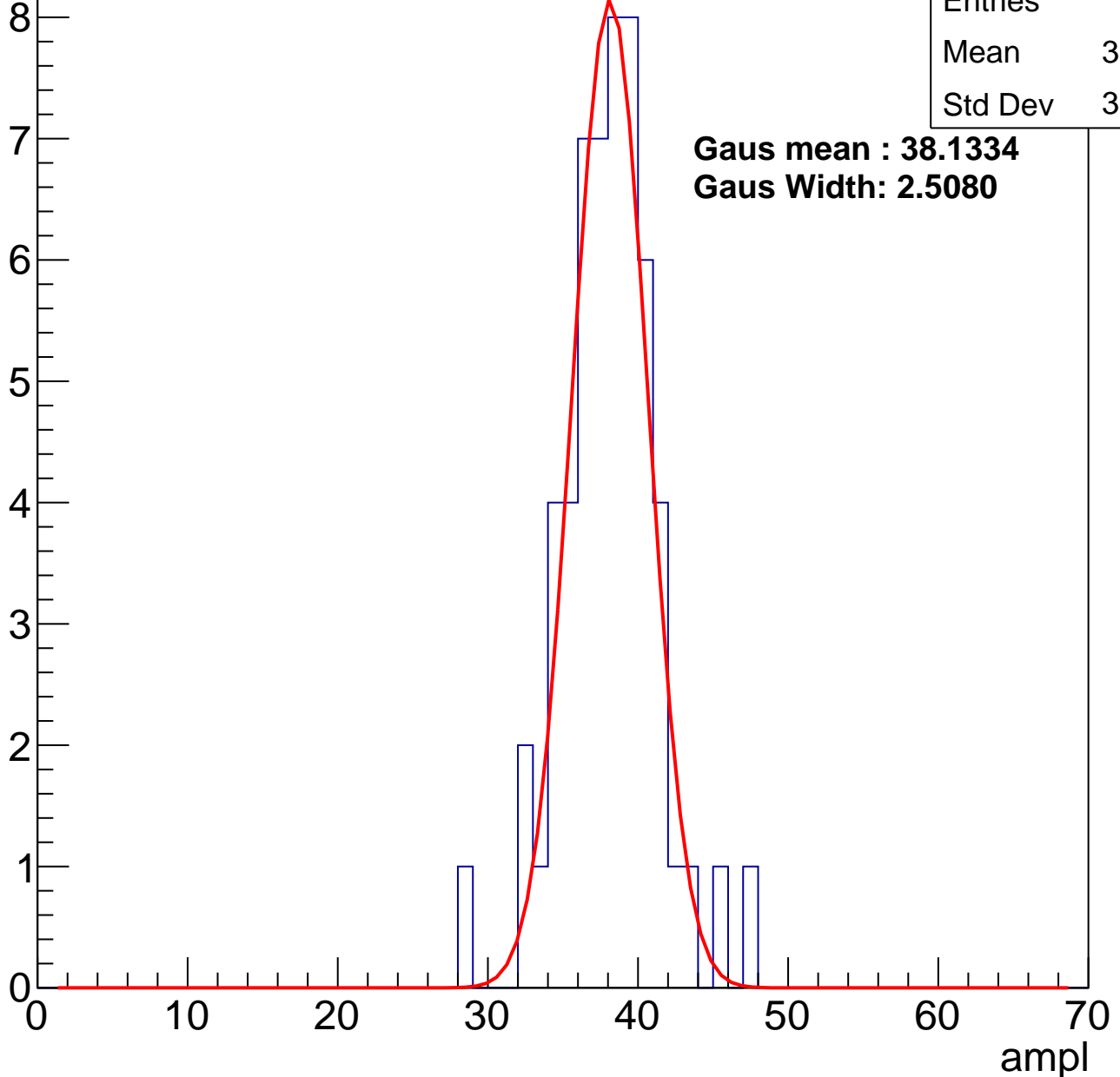
# B1L003S, U23-ch85, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	37.66
Std Dev	3.175

**Gaus mean : 38.1334**  
**Gaus Width: 2.5080**



# B1L003S, U23-ch85, adc2

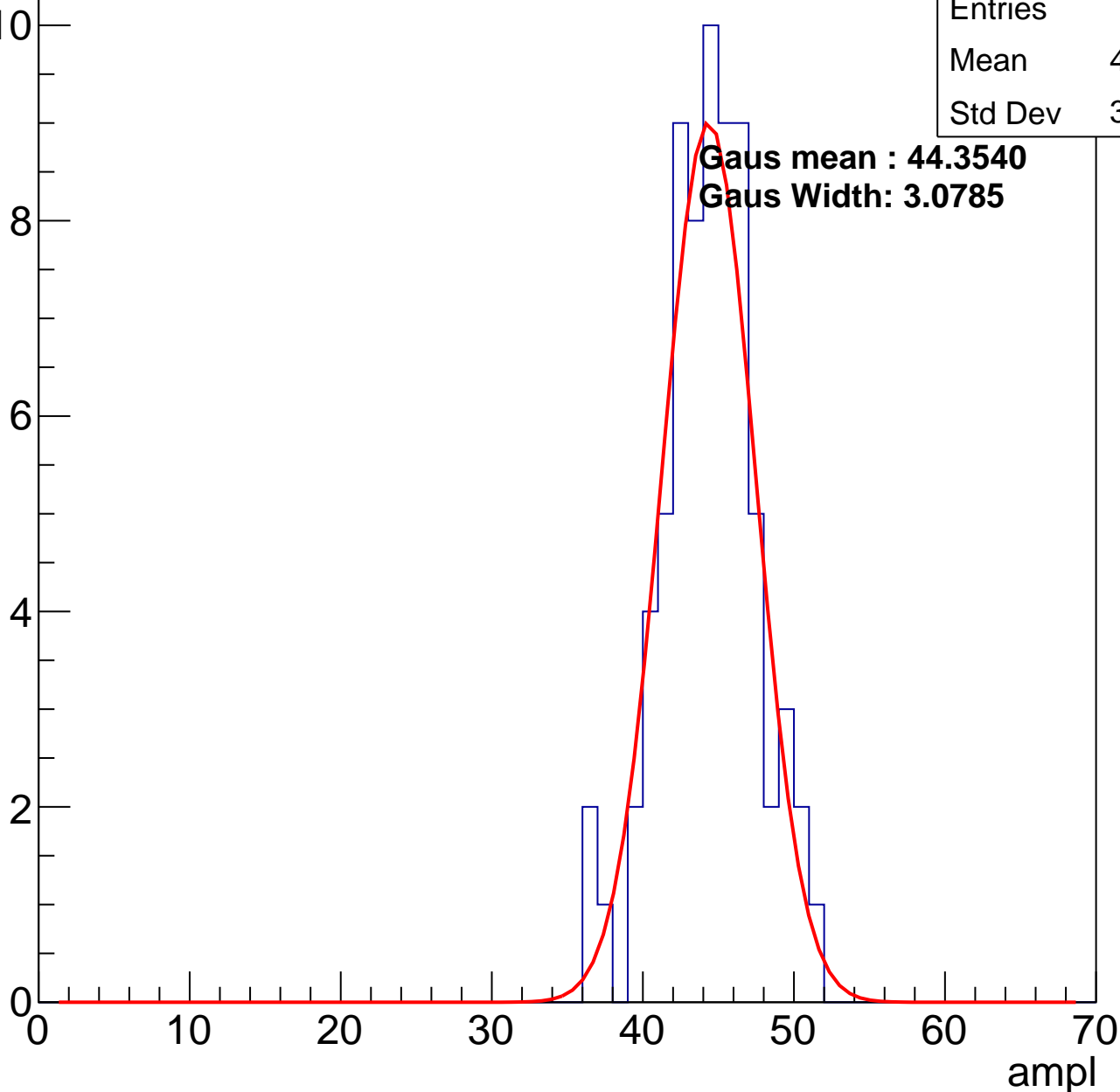
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	43.92
Std Dev	3.112

**Gaus mean : 44.3540**

**Gaus Width: 3.0785**

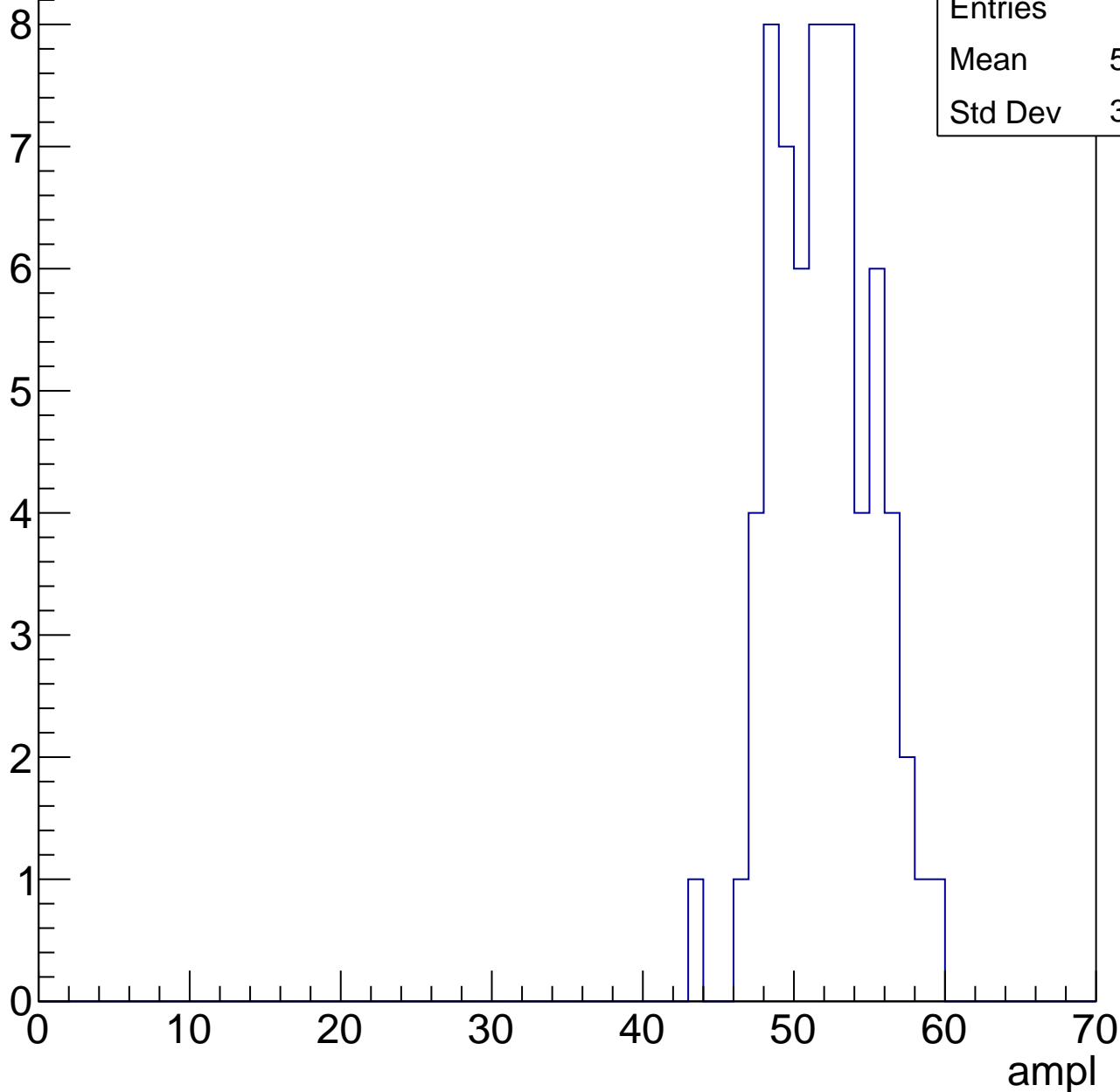


# B1L003S, U23-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

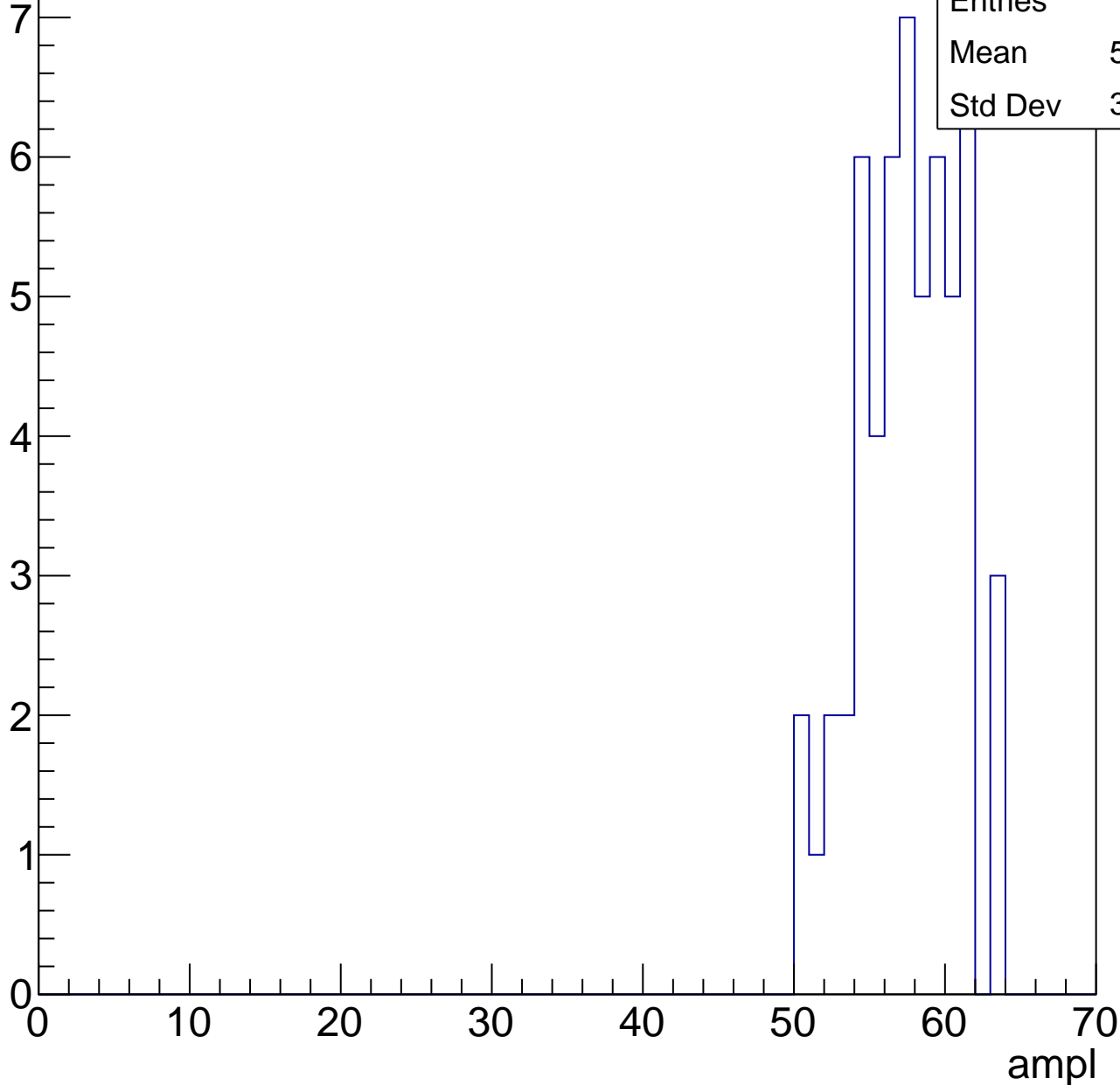
Entries	69
Mean	51.49
Std Dev	3.183



# B1L003S, U23-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



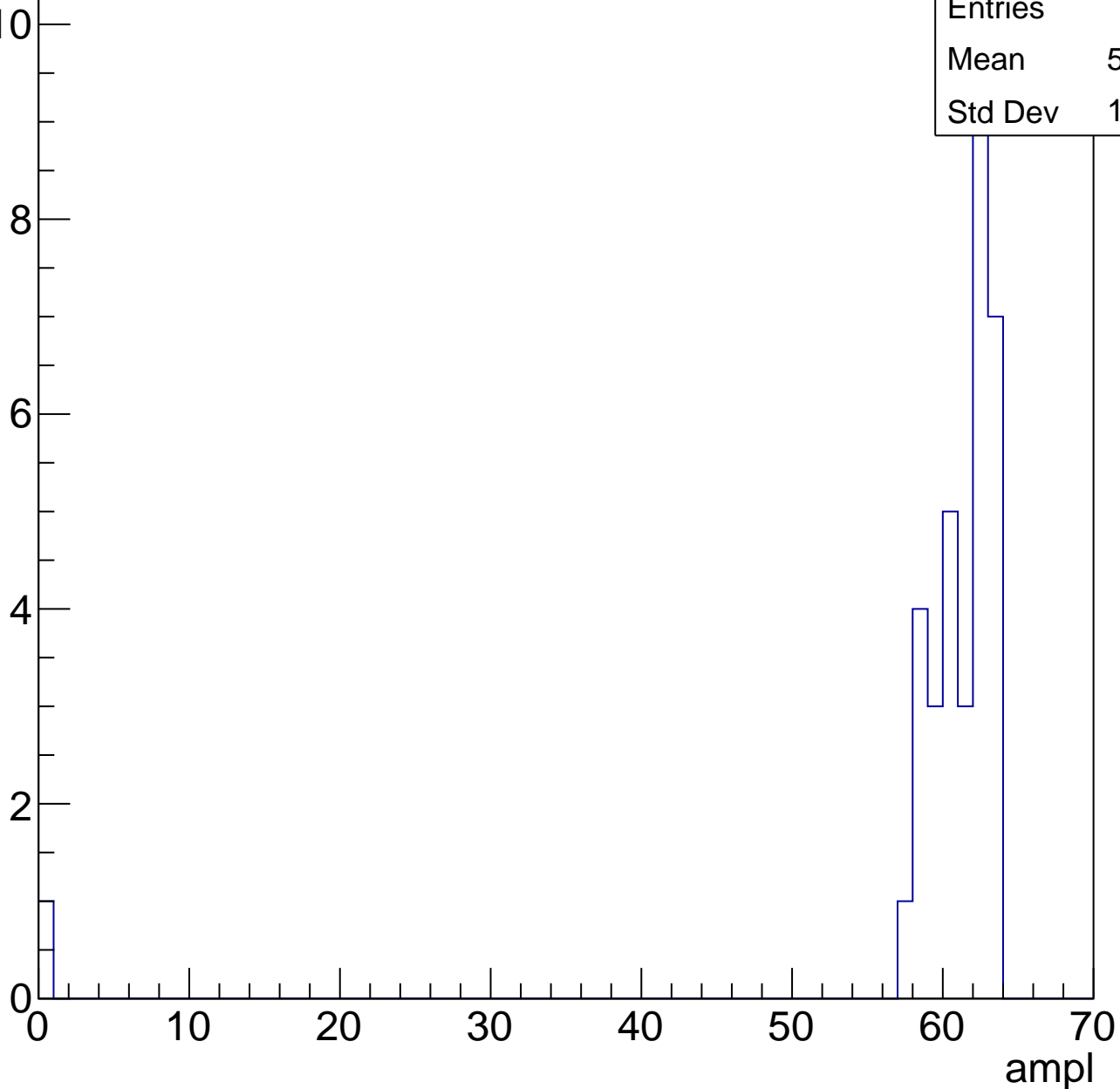
Entries	56
Mean	57.14
Std Dev	3.232

# B1L003S, U23-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	34
Mean	59.12
Std Dev	10.44



# B1L003S, U23-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0



# B1L003S, U23-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch86, adc0

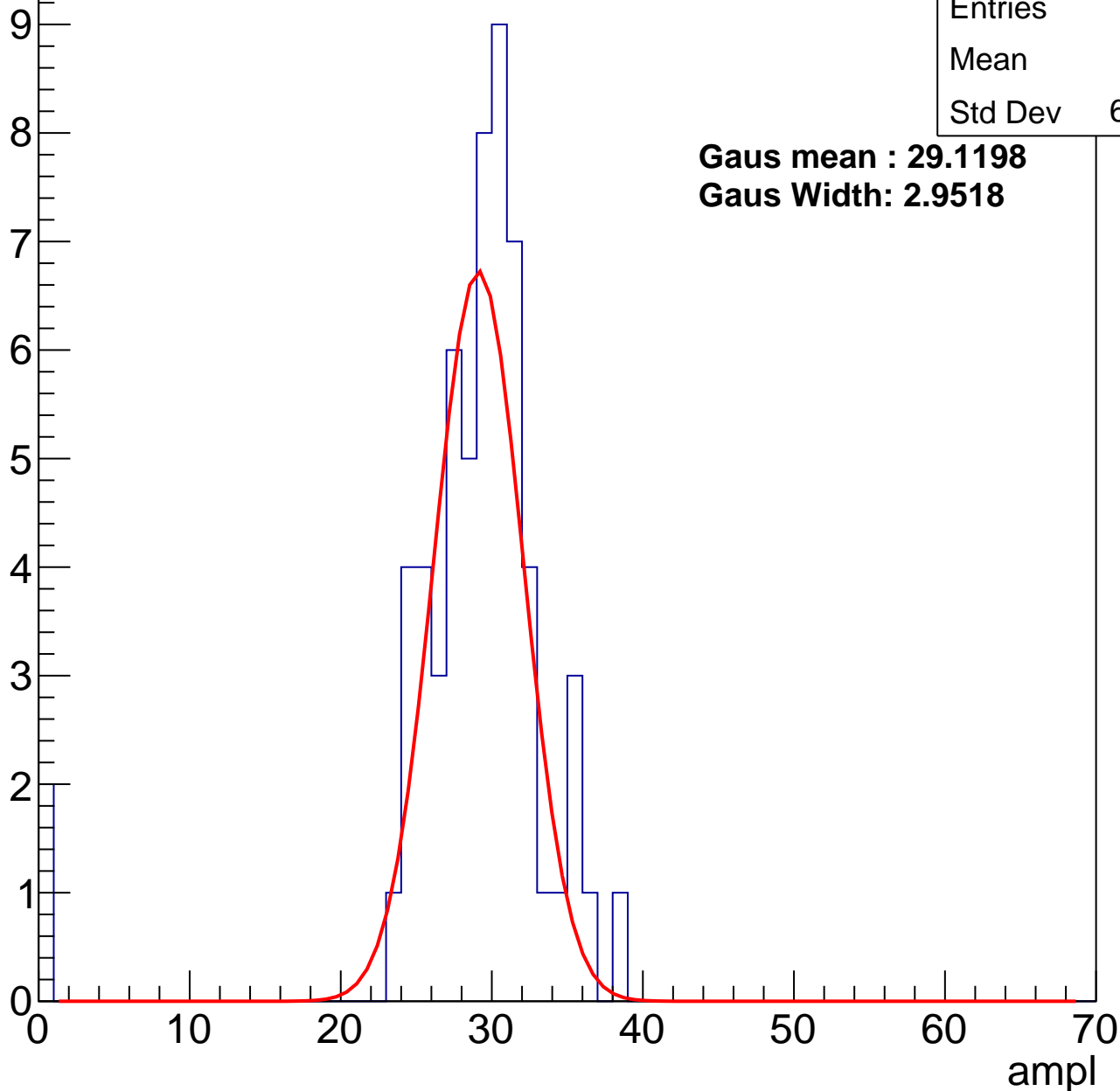
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	28.2
Std Dev	6.126

**Gaus mean : 29.1198**

**Gaus Width: 2.9518**



# B1L003S, U23-ch86, adc1

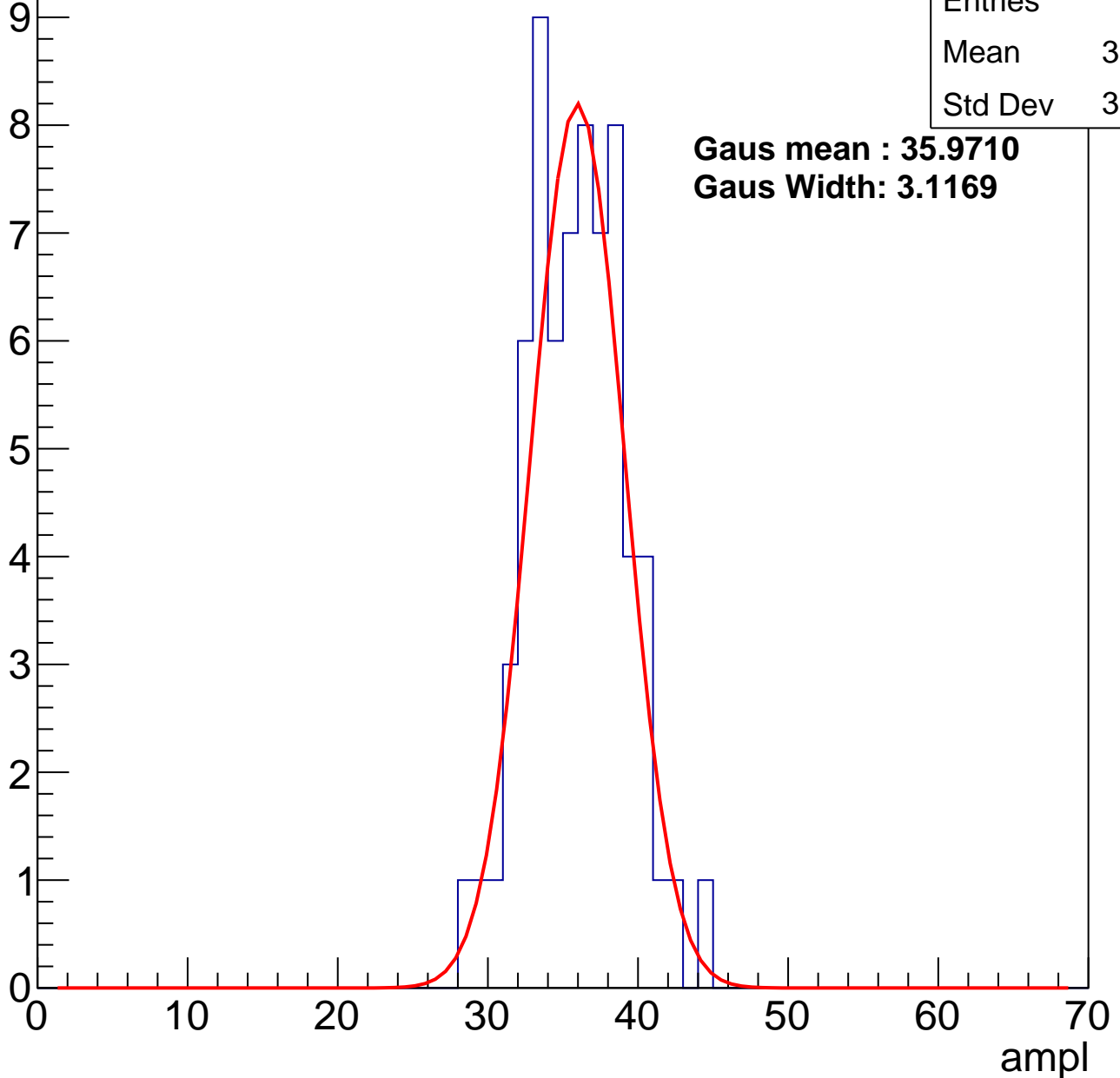
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.47
Std Dev	3.155

**Gaus mean : 35.9710**

**Gaus Width: 3.1169**



# B1L003S, U23-ch86, adc2

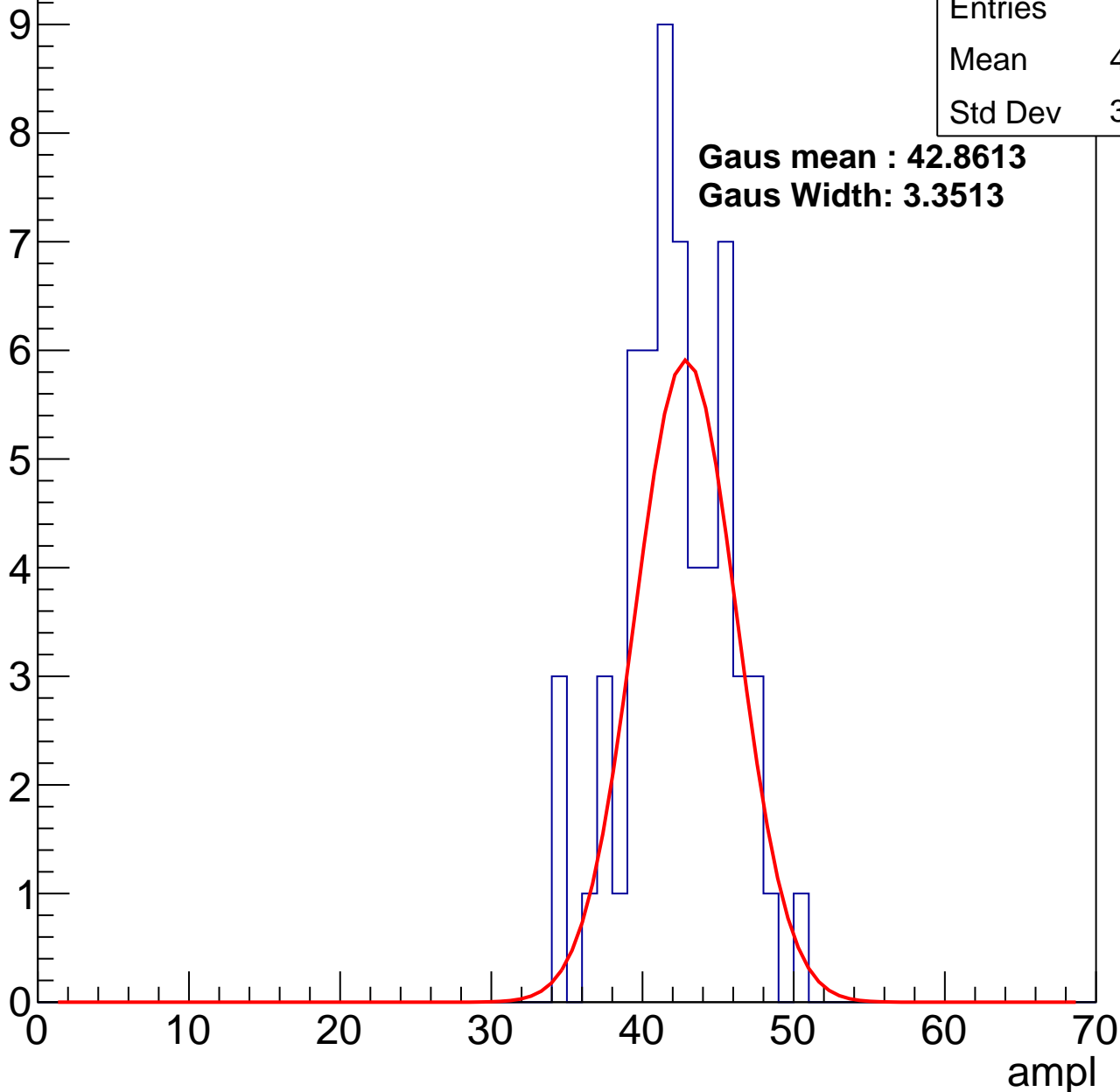
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.76
Std Dev	3.466

**Gaus mean : 42.8613**

**Gaus Width: 3.3513**

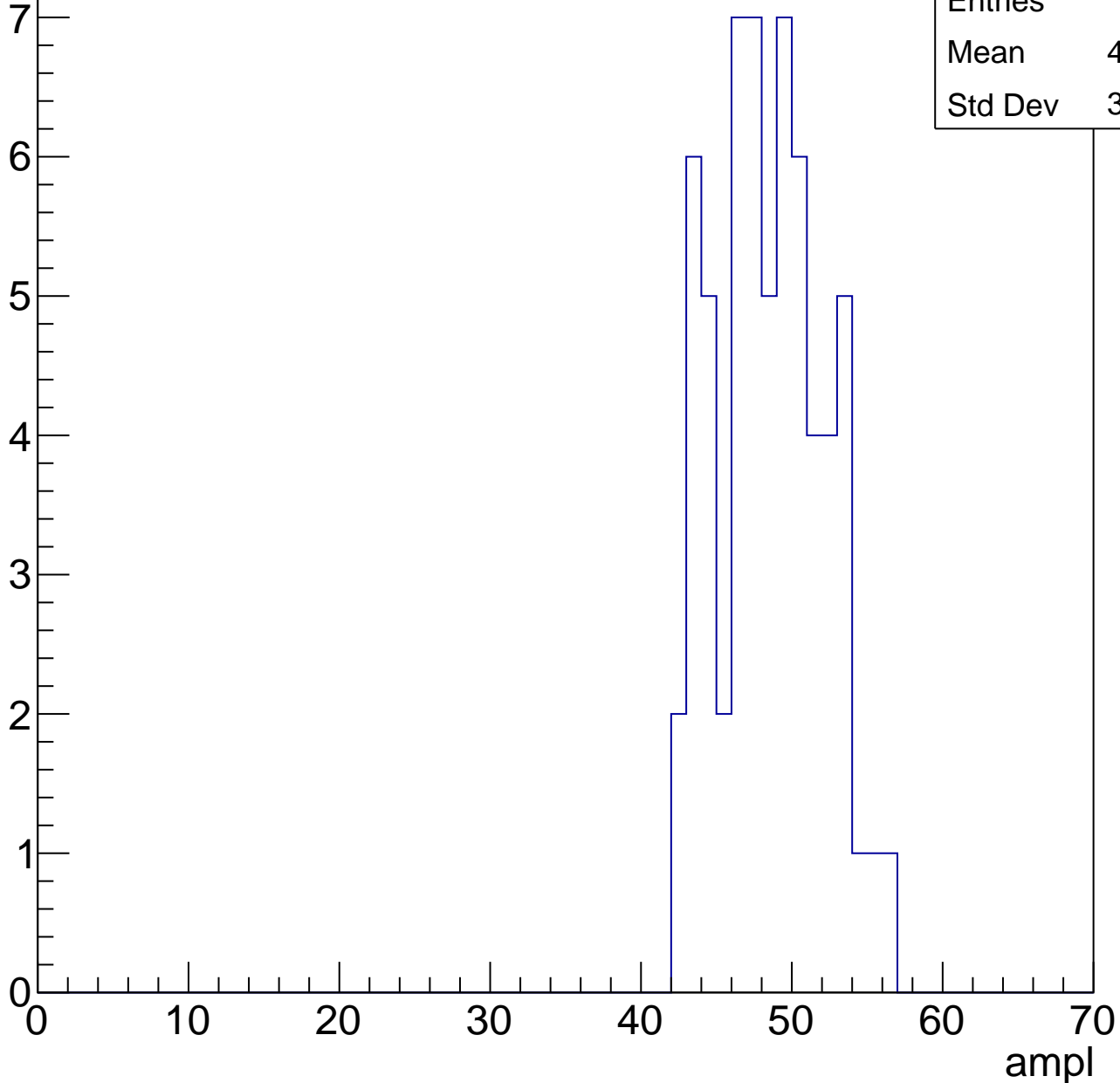


# B1L003S, U23-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	48.06
Std Dev	3.473

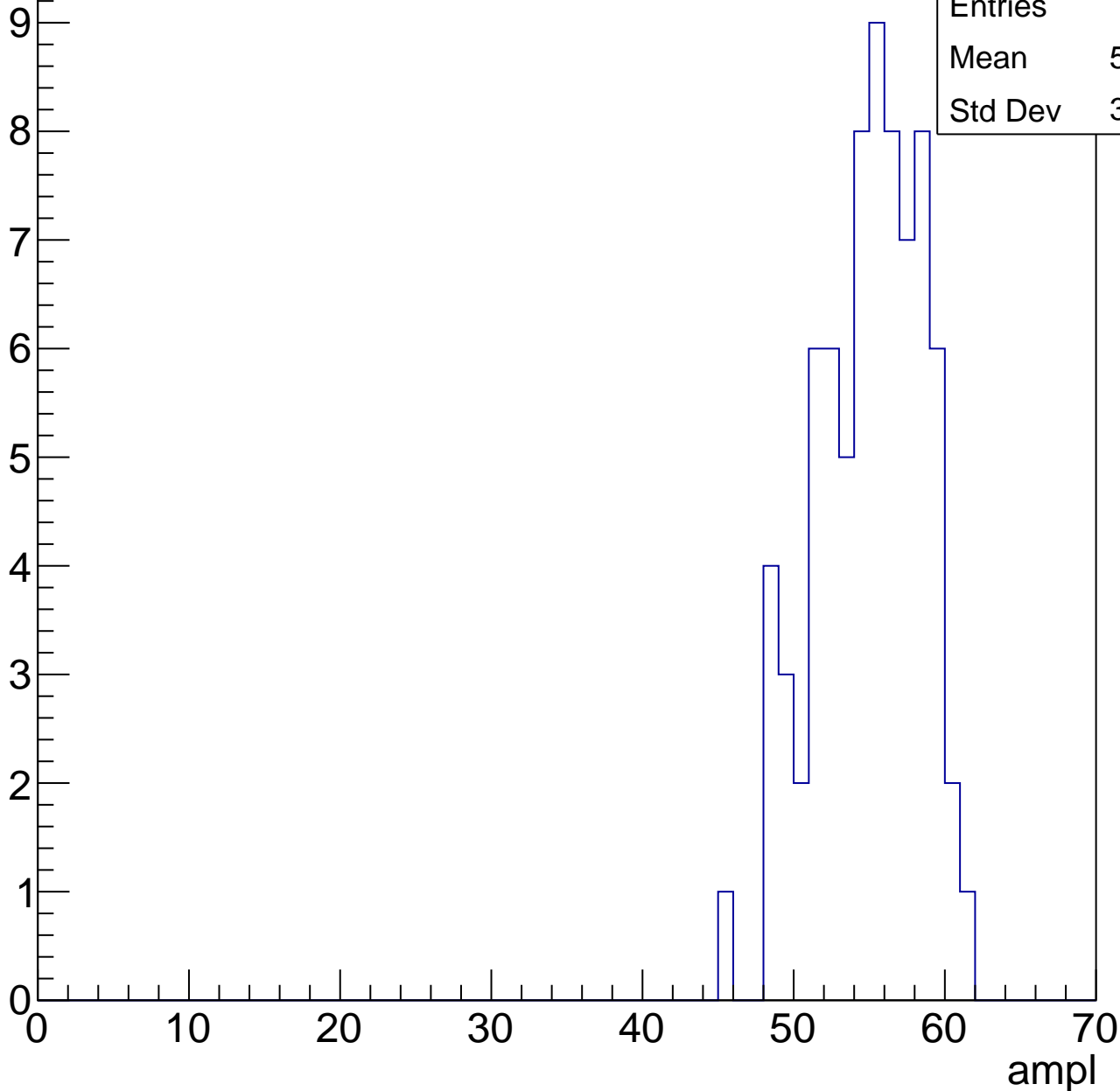


# B1L003S, U23-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

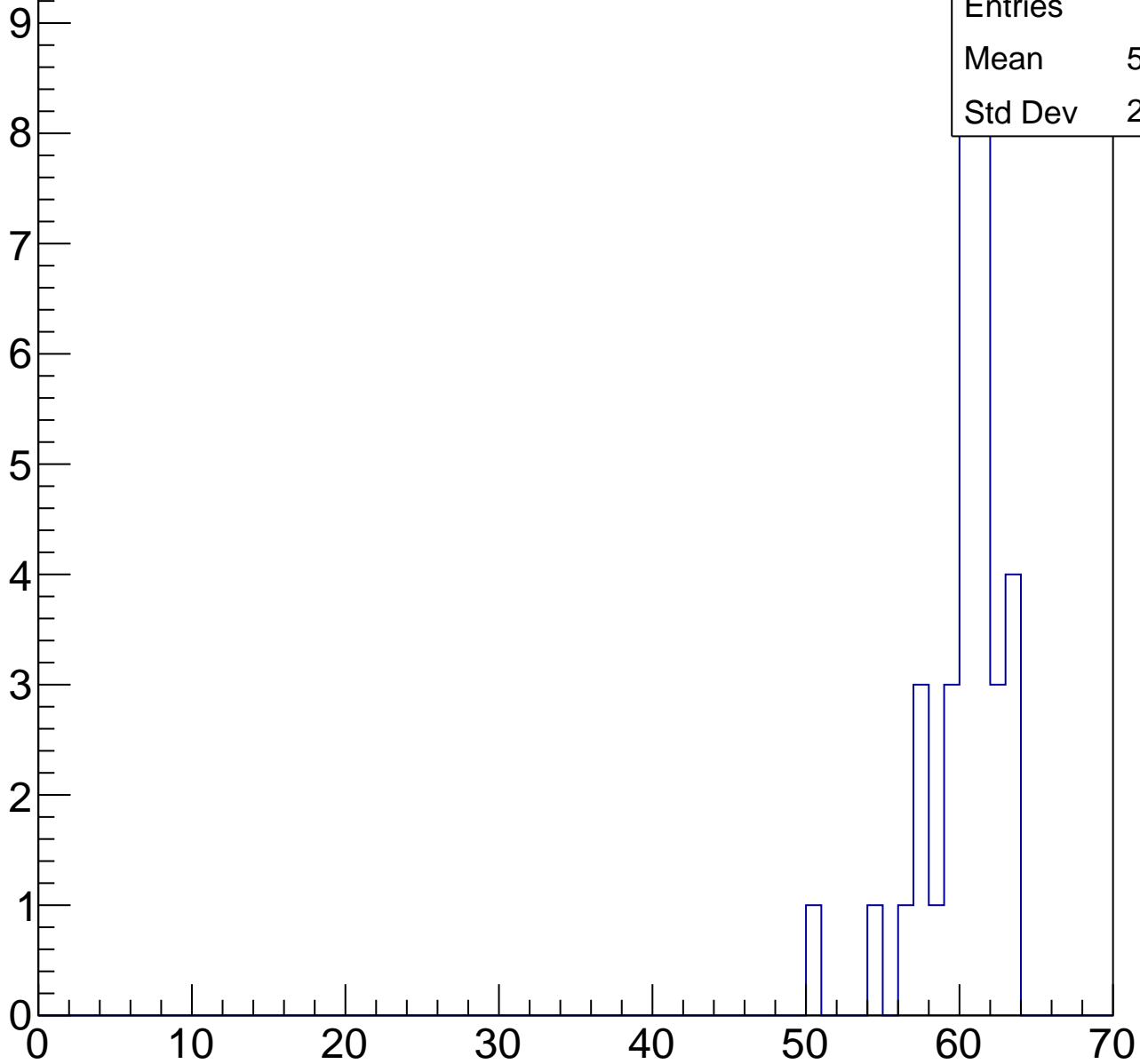
Entries	76
Mean	54.47
Std Dev	3.443



# B1L003S, U23-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



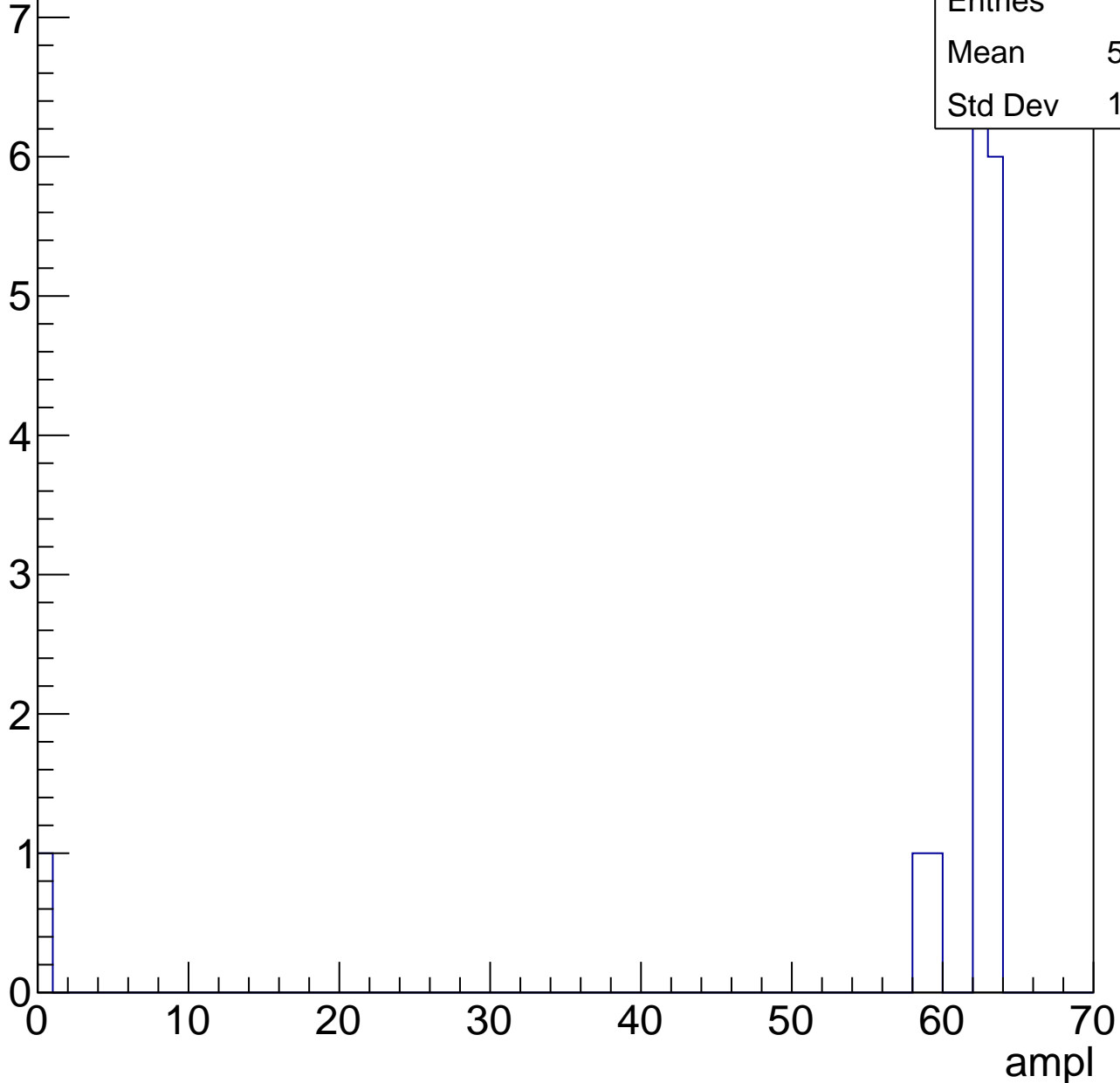
ampl

# B1L003S, U23-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	16
Mean	58.06
Std Dev	15.06





# B1L003S, U23-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L003S, U23-ch87, adc0

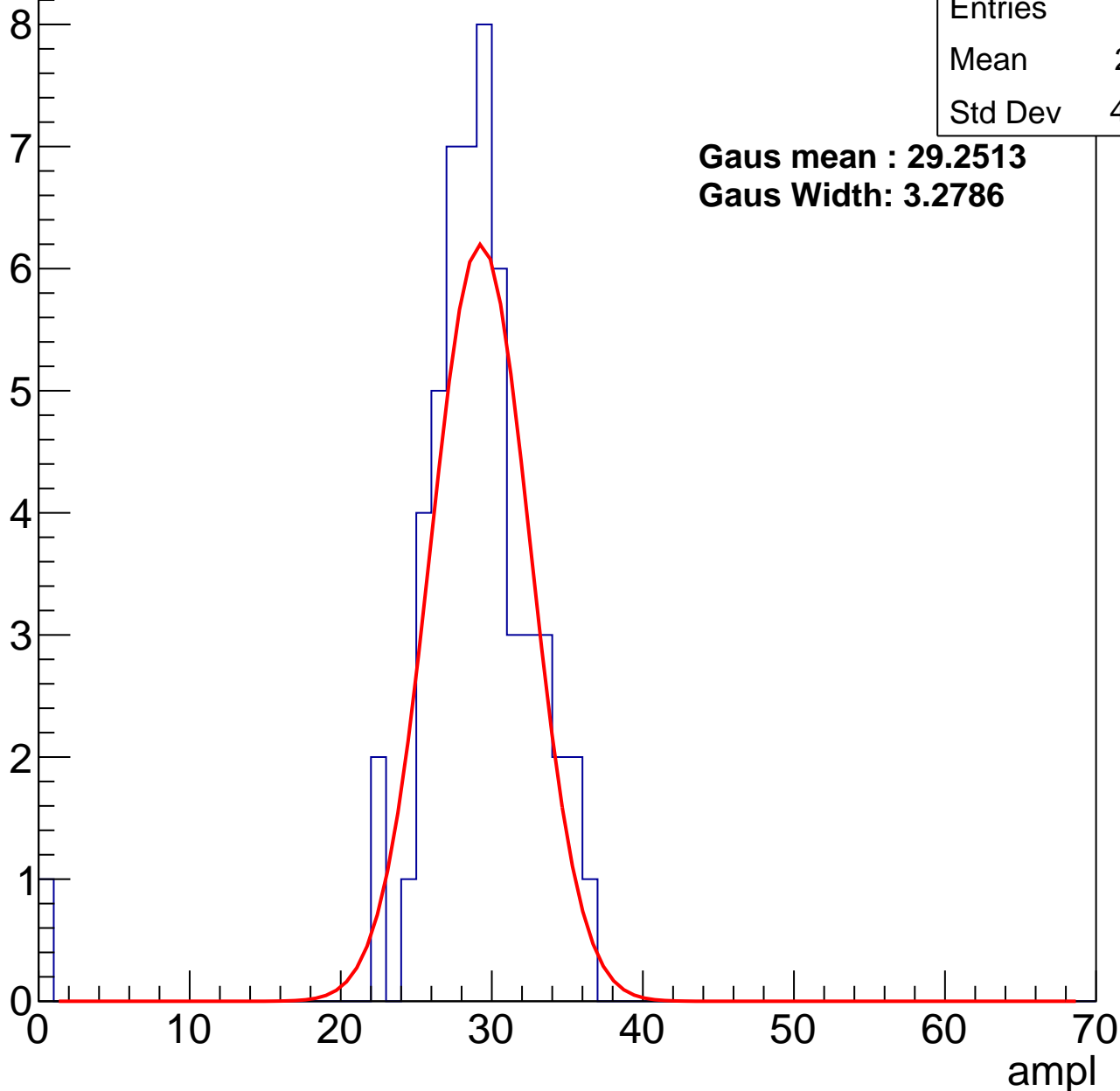
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	28.31
Std Dev	4.947

**Gaus mean : 29.2513**

**Gaus Width: 3.2786**



# B1L003S, U23-ch87, adc1

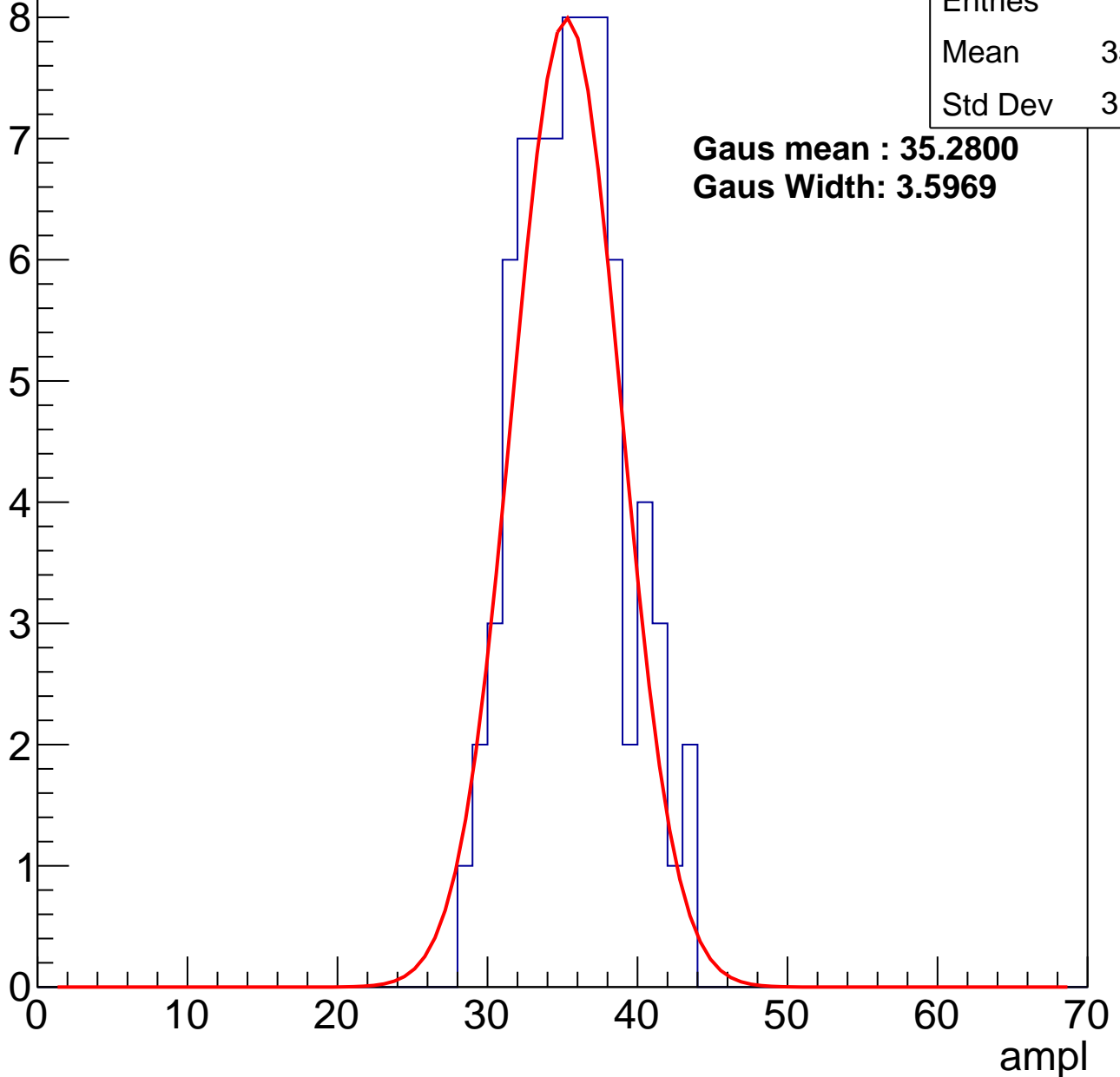
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	35.15
Std Dev	3.463

**Gaus mean : 35.2800**

**Gaus Width: 3.5969**



# B1L003S, U23-ch87, adc2

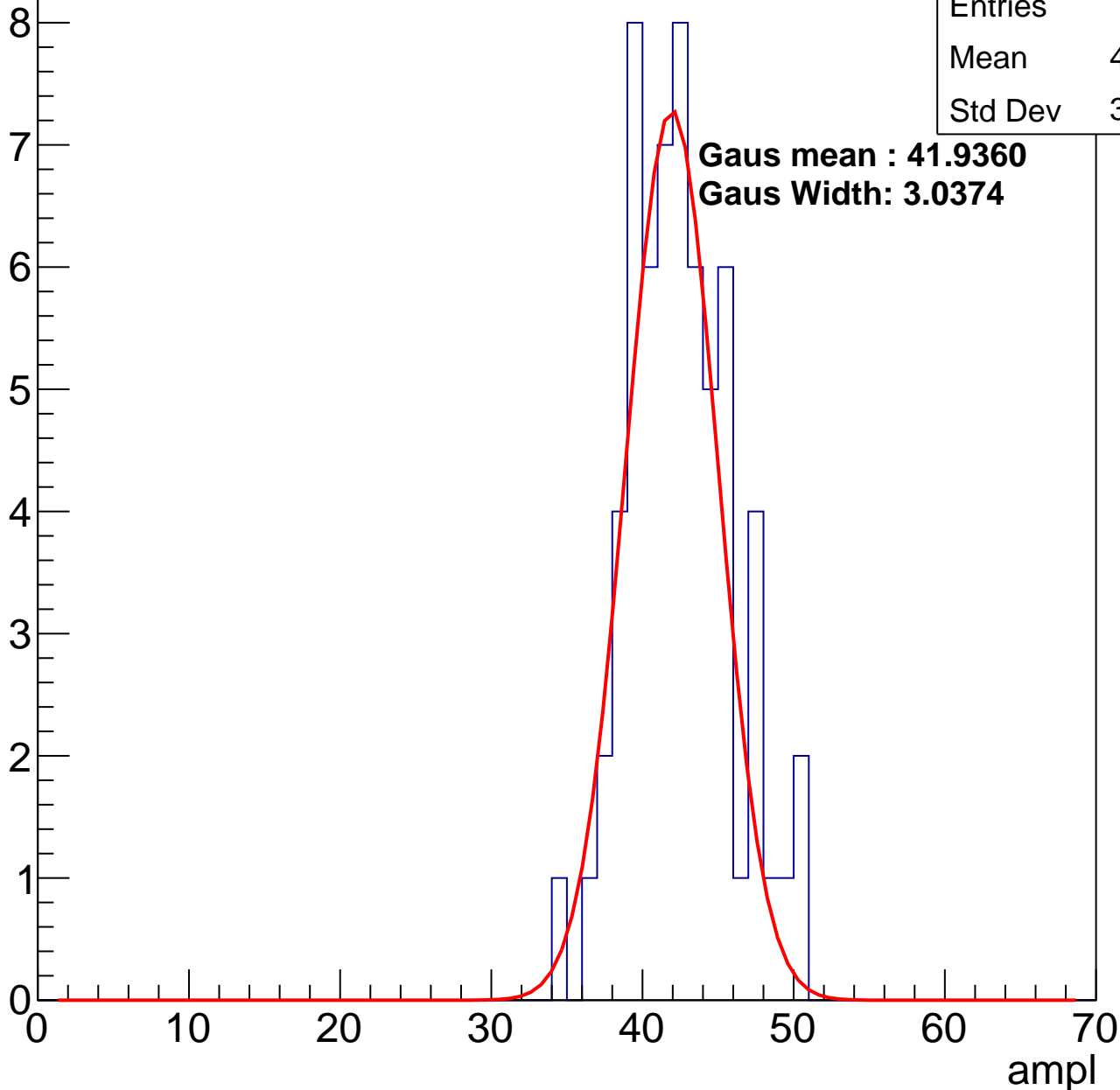
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.06
Std Dev	3.394

**Gaus mean : 41.9360**

**Gaus Width: 3.0374**

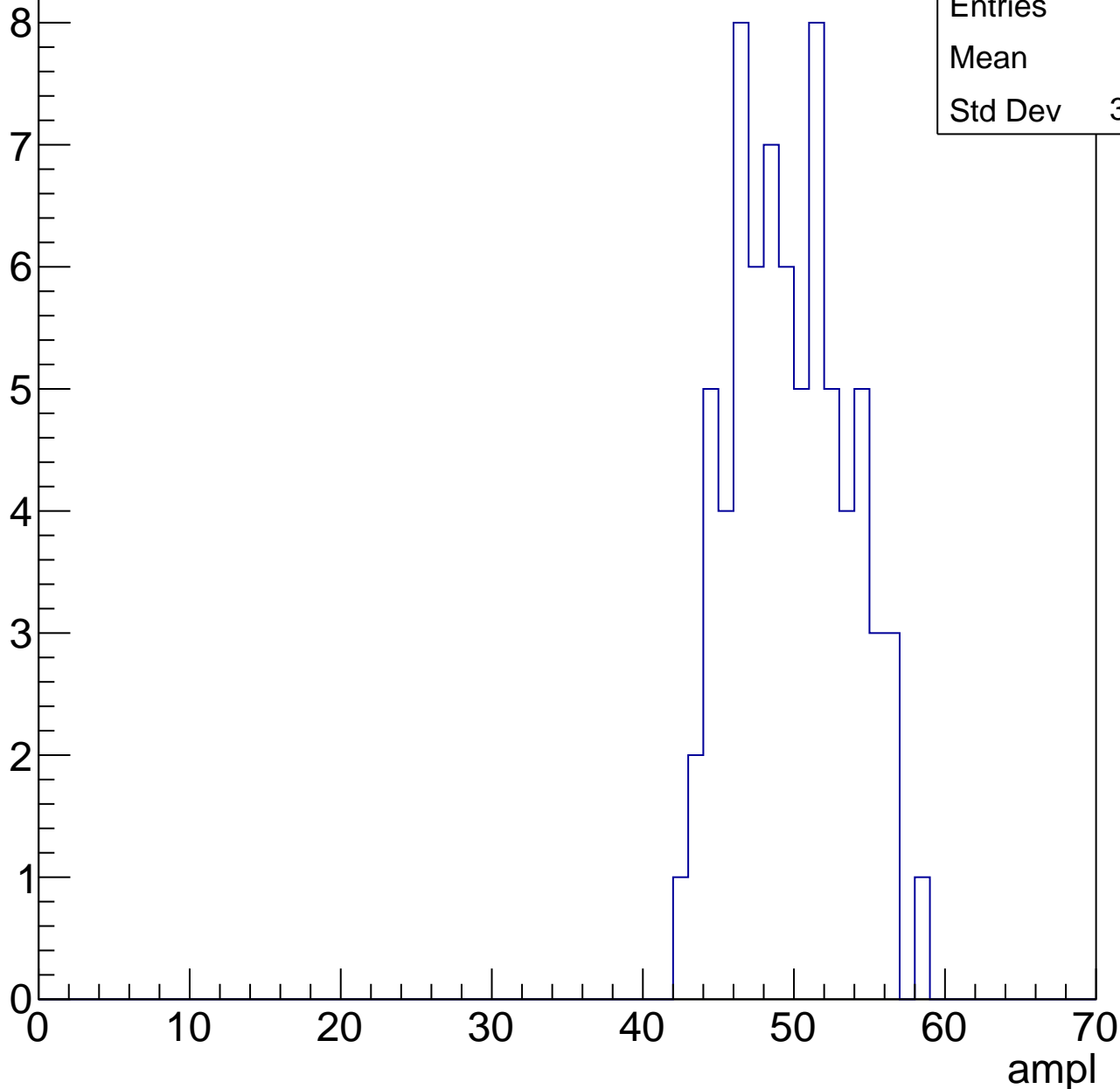


# B1L003S, U23-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	49.3
Std Dev	3.726

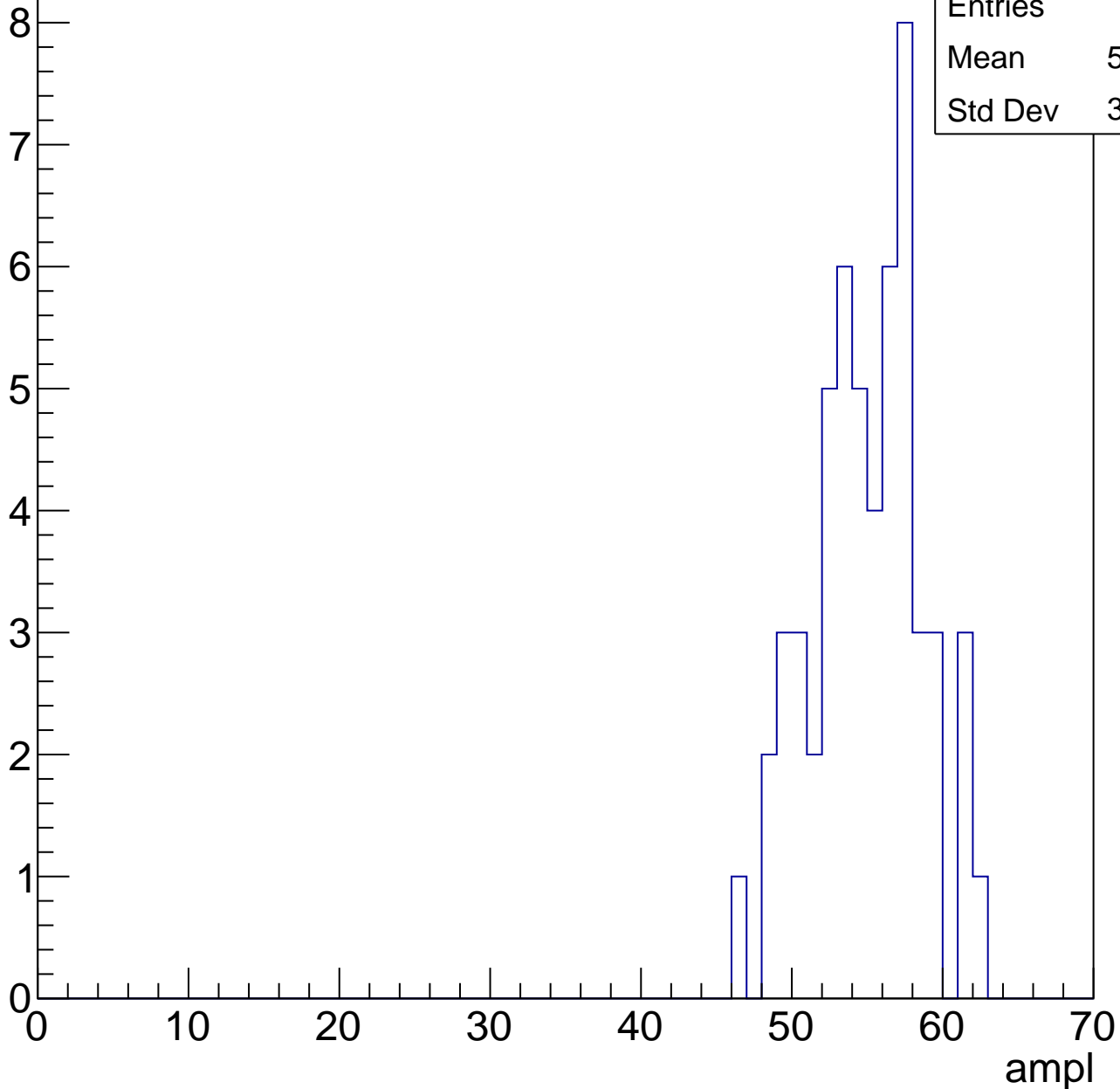


# B1L003S, U23-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.49
Std Dev	3.628

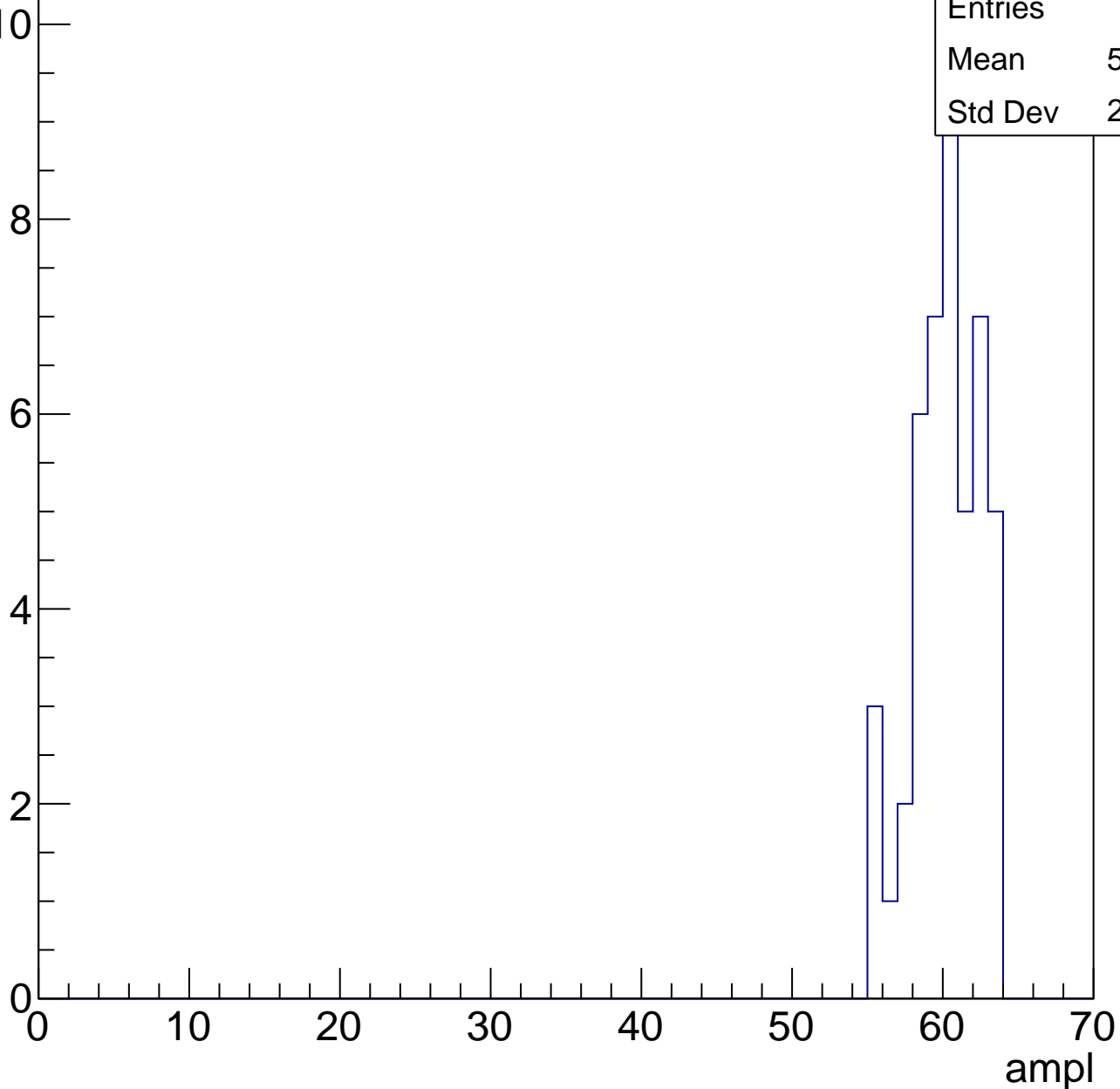


# B1L003S, U23-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	59.78
Std Dev	2.166

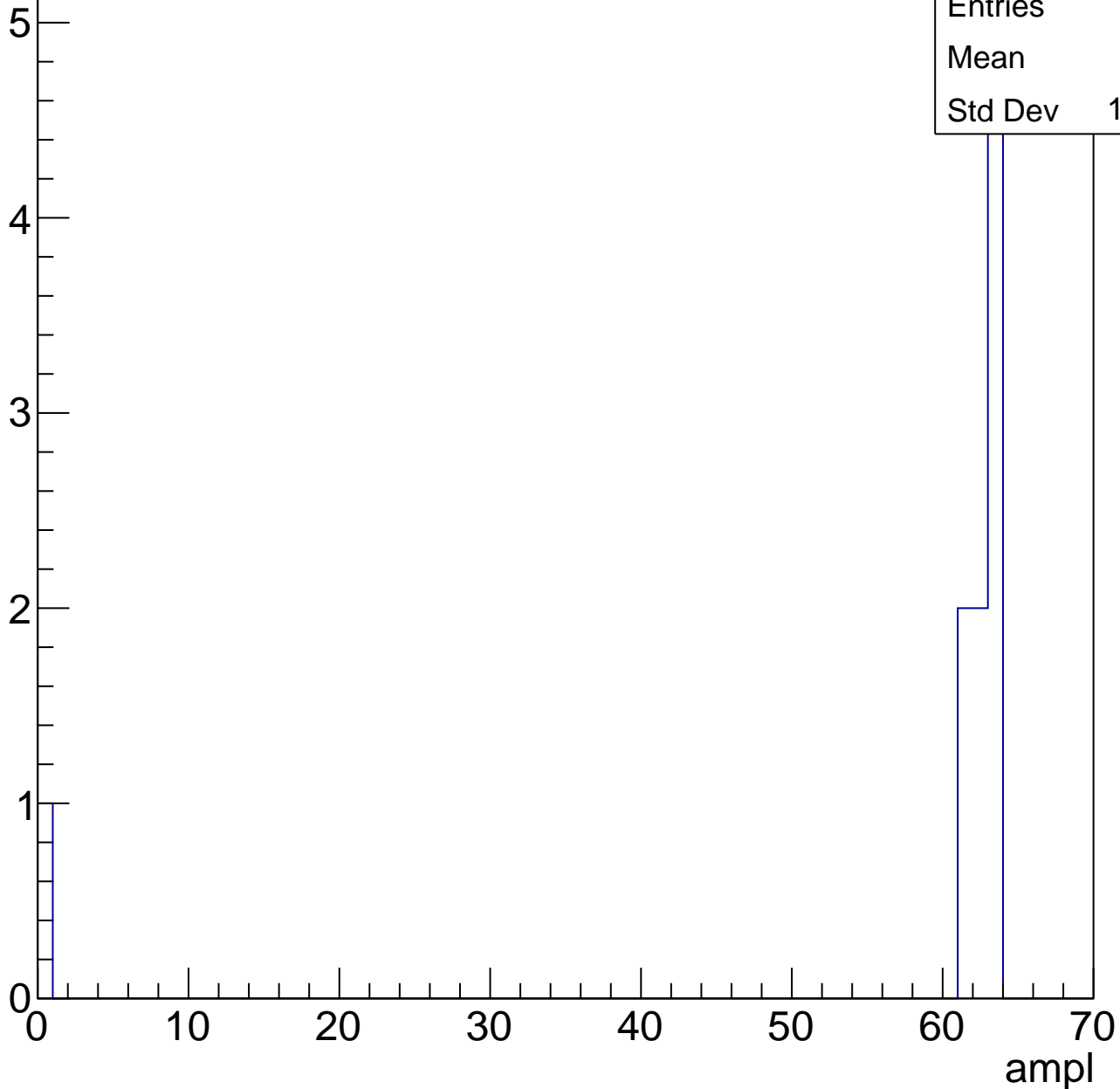


# B1L003S, U23-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	10
Mean	56.1
Std Dev	18.72





# B1L003S, U23-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch88, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	62
Mean	30.24
Std Dev	6.288

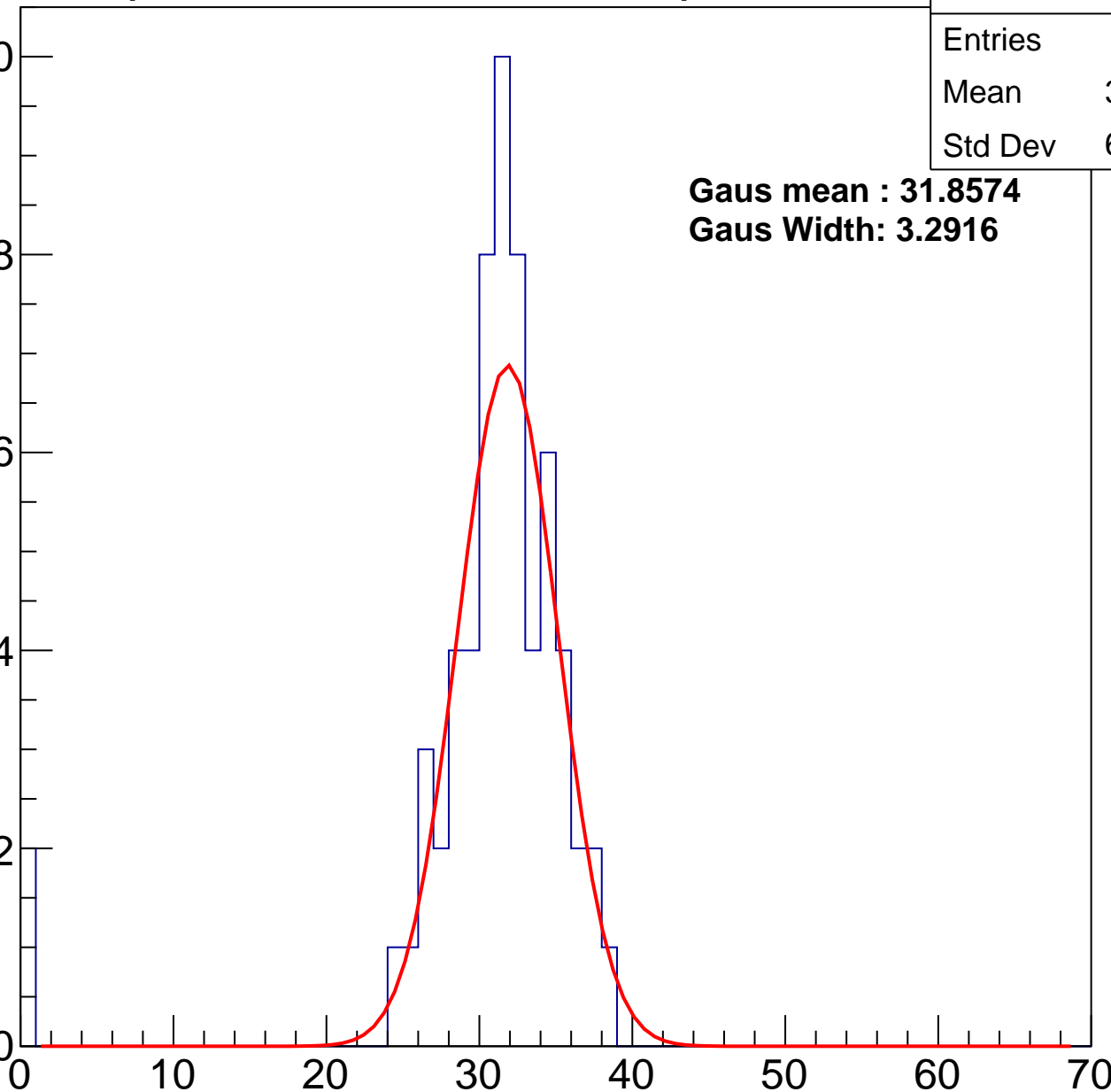
**Gaus mean : 31.8574**

**Gaus Width: 3.2916**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L003S, U23-ch88, adc1

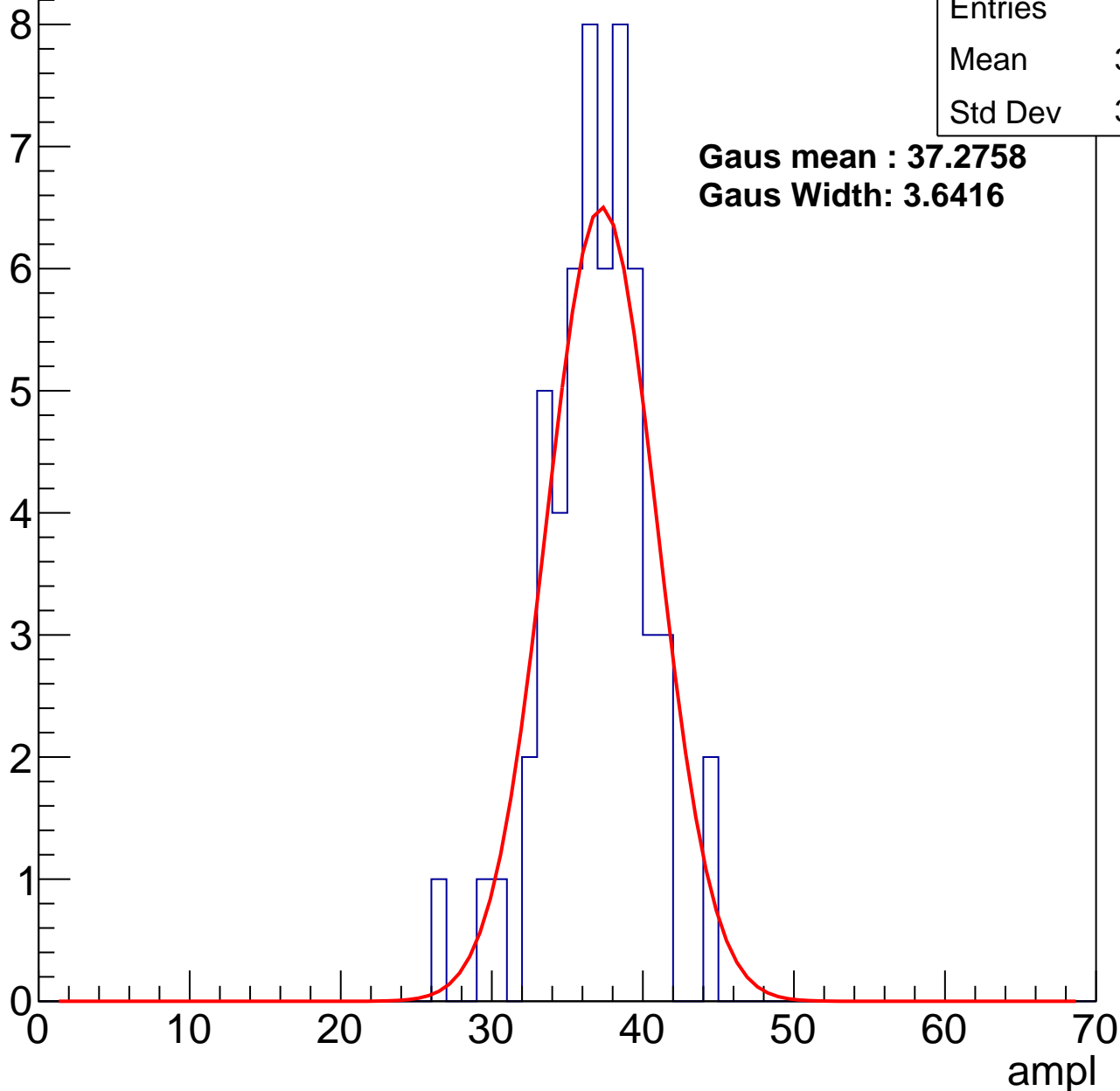
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	36.41
Std Dev	3.321

**Gaus mean : 37.2758**

**Gaus Width: 3.6416**



# B1L003S, U23-ch88, adc2

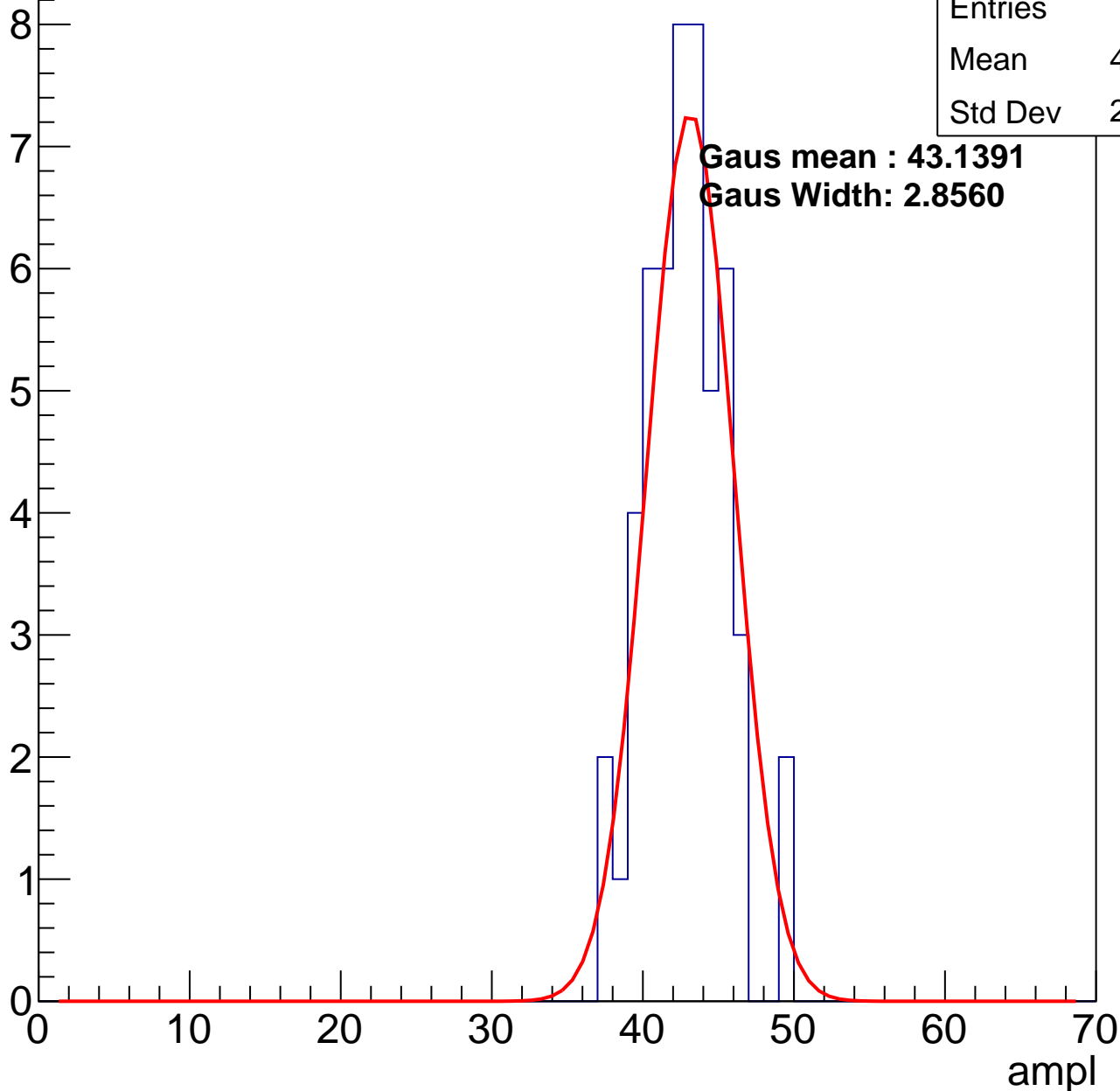
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	42.35
Std Dev	2.633

**Gaus mean : 43.1391**

**Gaus Width: 2.8560**

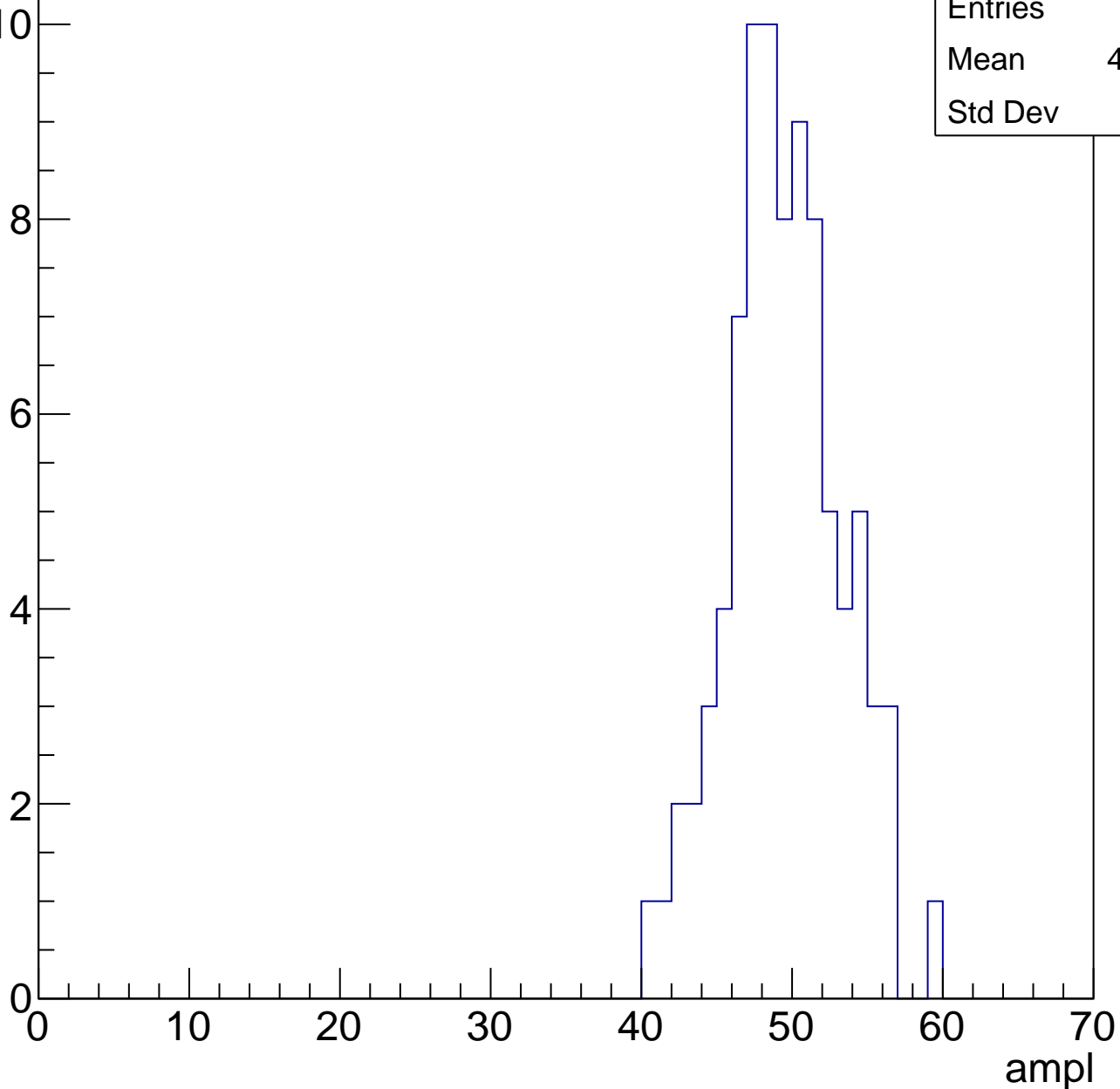


# B1L003S, U23-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	86
Mean	49.06
Std Dev	3.73

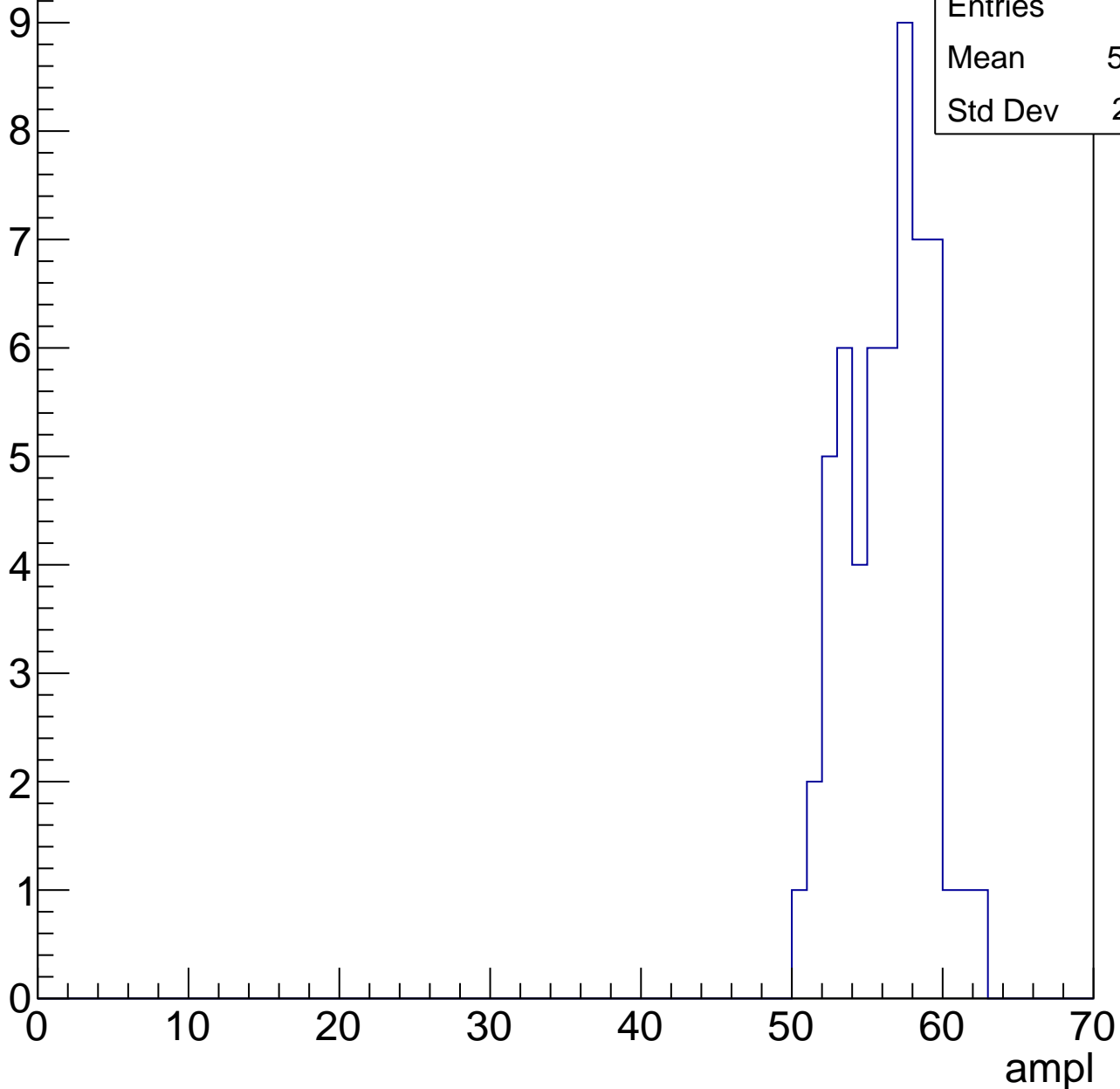


# B1L003S, U23-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	55.84
Std Dev	2.731

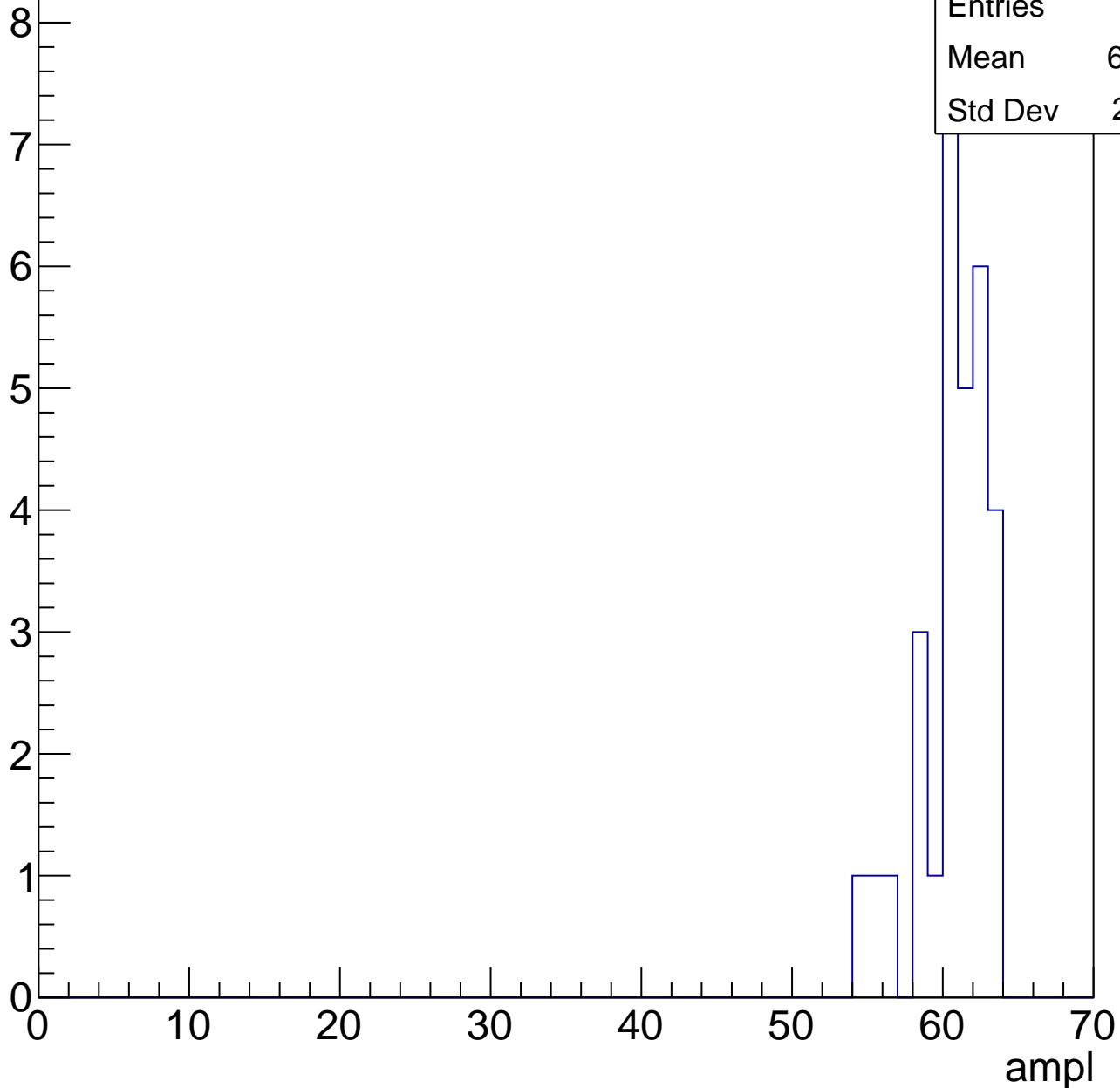


# B1L003S, U23-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	30
Mean	60.23
Std Dev	2.261

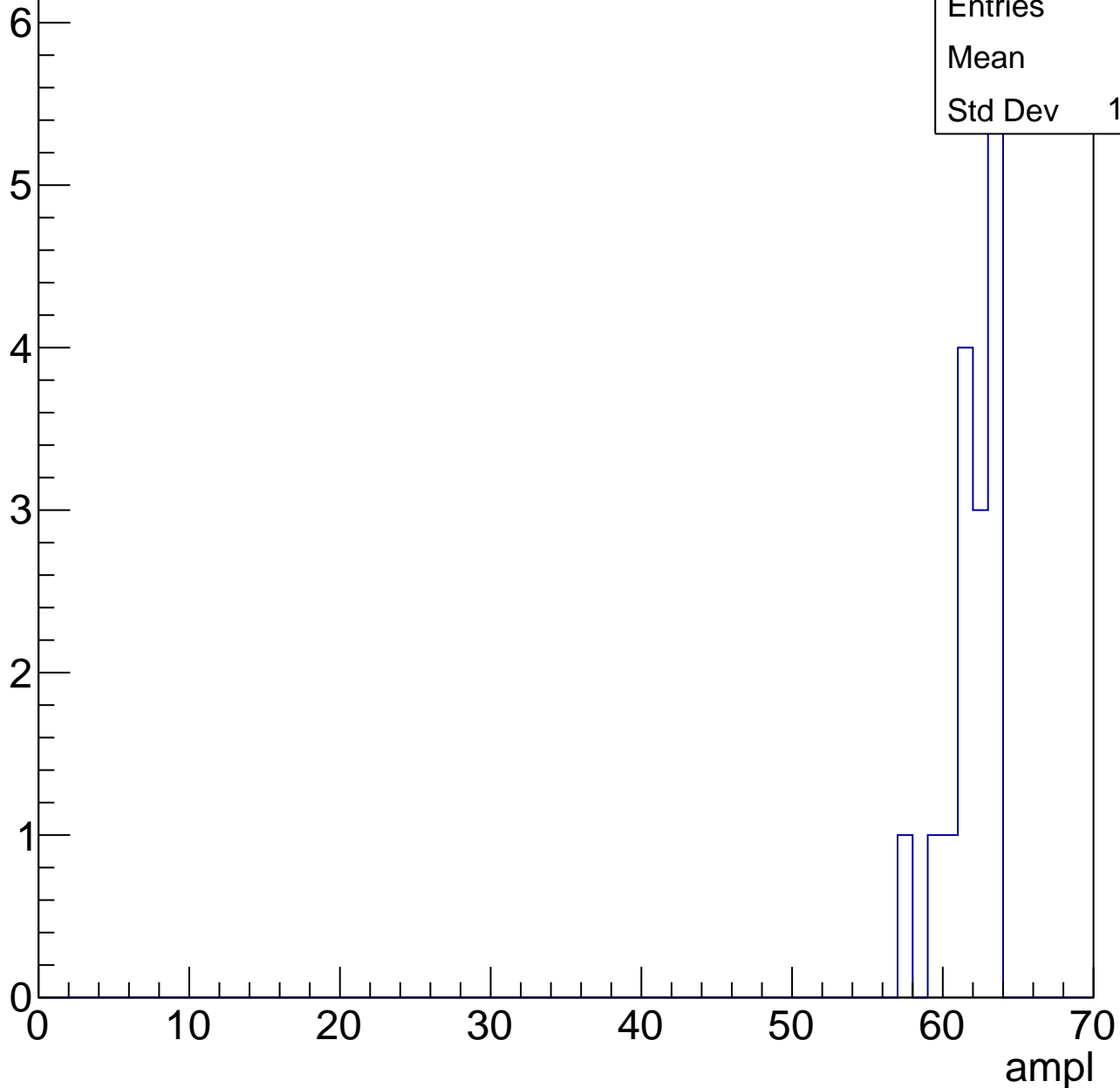


# B1L003S, U23-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	16
Mean	61.5
Std Dev	1.658

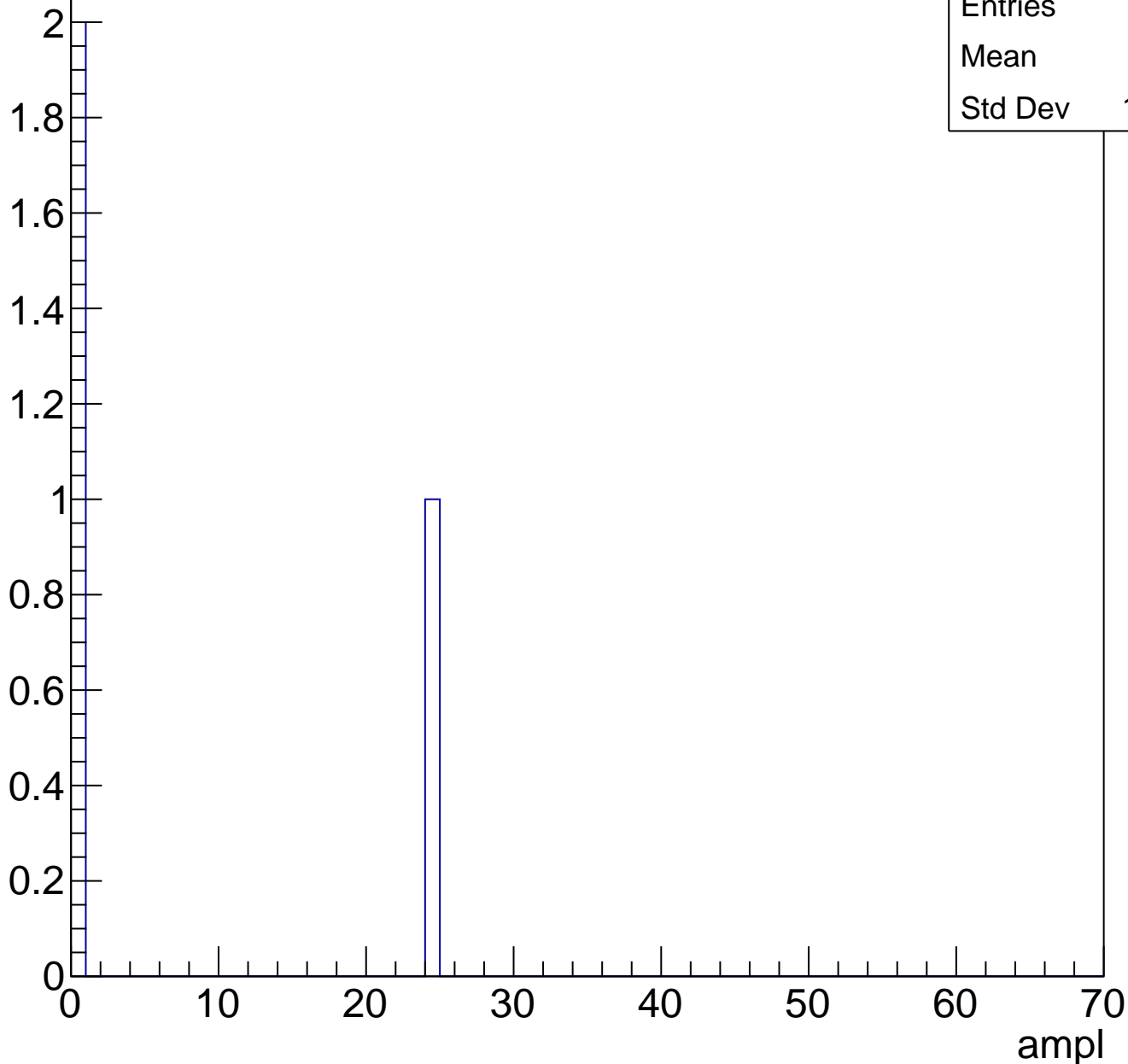




# B1L003S, U23-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	8
Std Dev	11.31

# B1L003S, U23-ch89, adc0

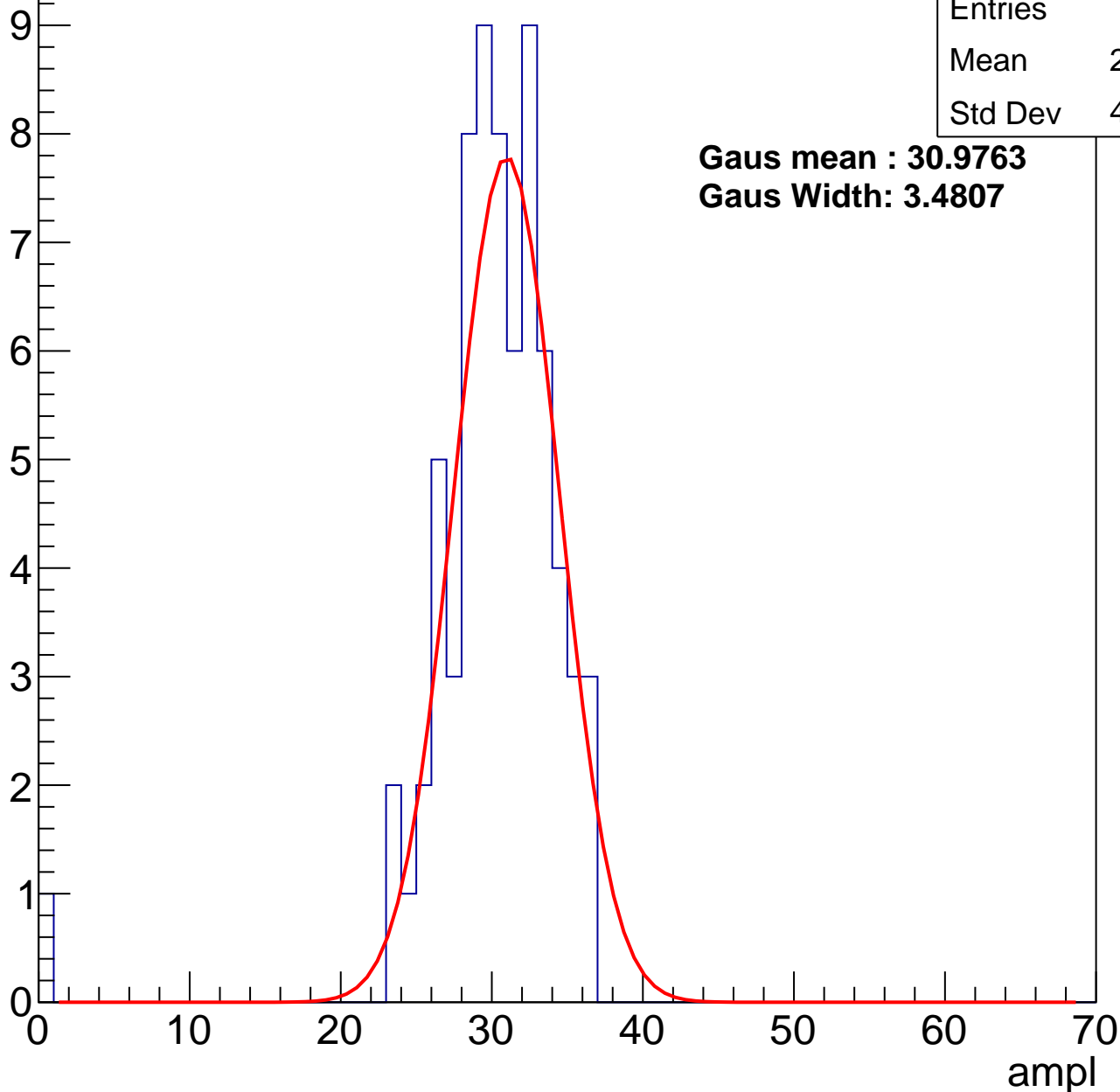
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.67
Std Dev	4.732

**Gaus mean : 30.9763**

**Gaus Width: 3.4807**



# B1L003S, U23-ch89, adc1

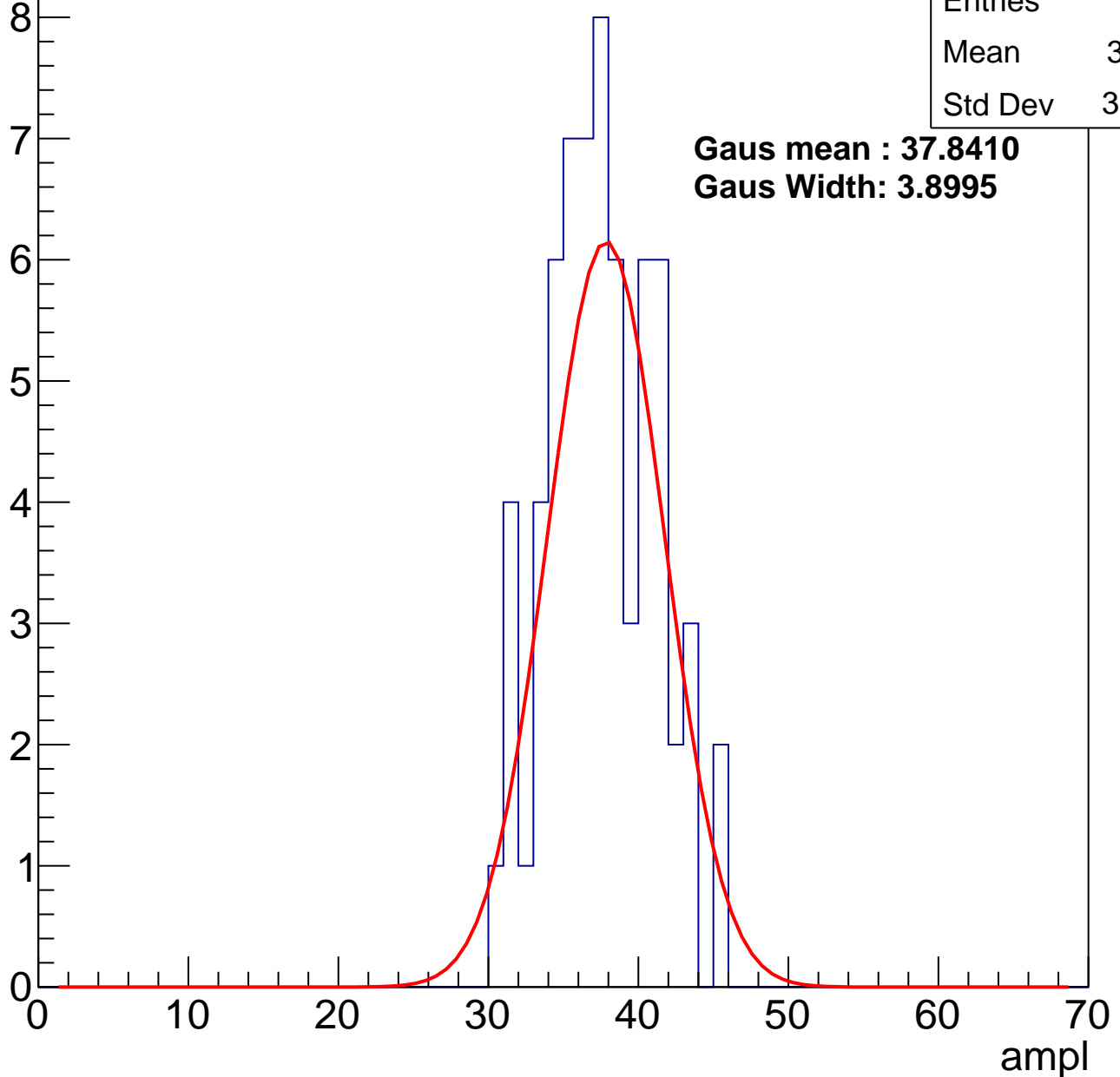
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	37.11
Std Dev	3.534

**Gaus mean : 37.8410**

**Gaus Width: 3.8995**



# B1L003S, U23-ch89, adc2

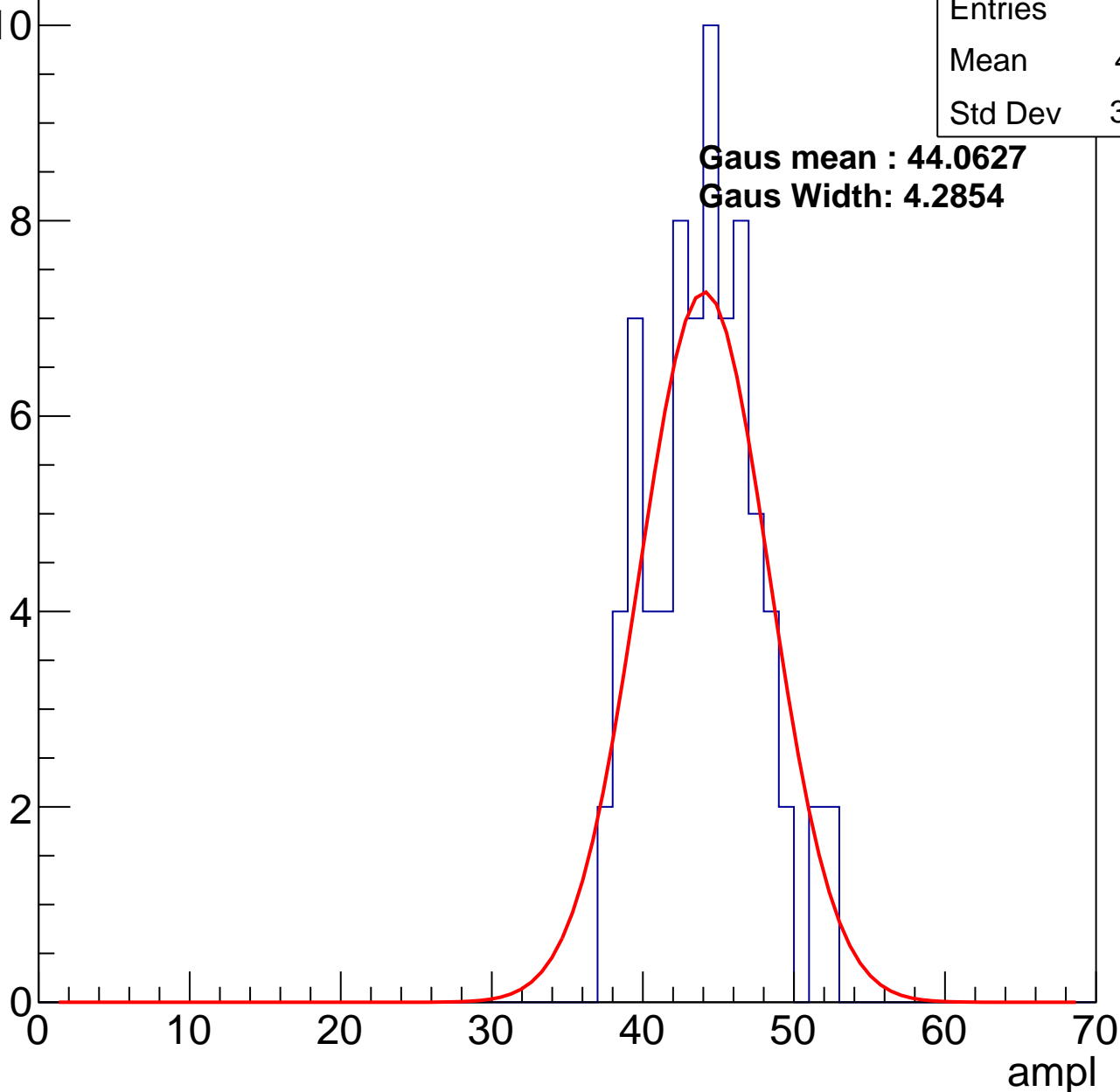
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	43.61
Std Dev	3.569

**Gaus mean : 44.0627**

**Gaus Width: 4.2854**

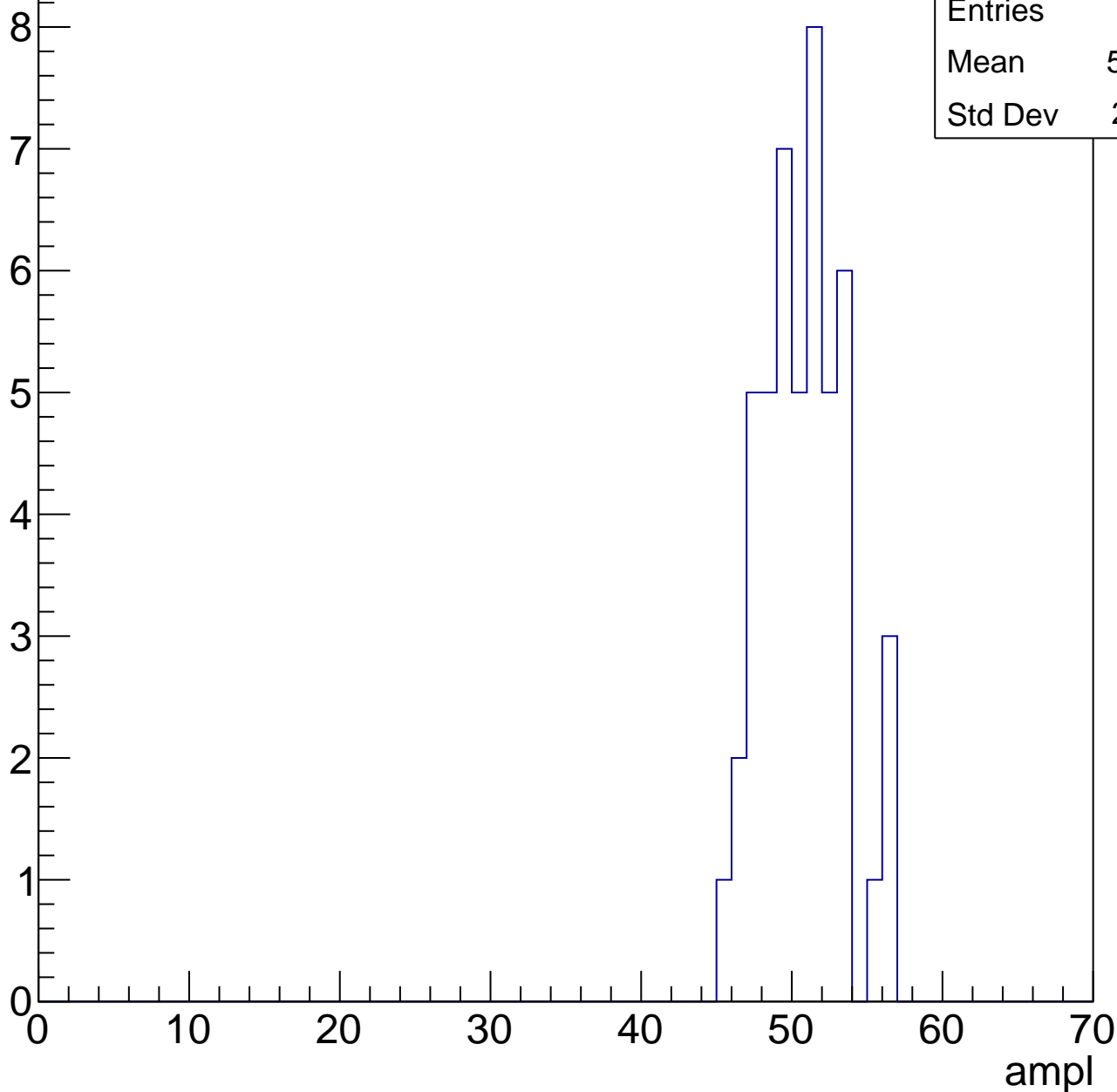


# B1L003S, U23-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	50.29
Std Dev	2.661

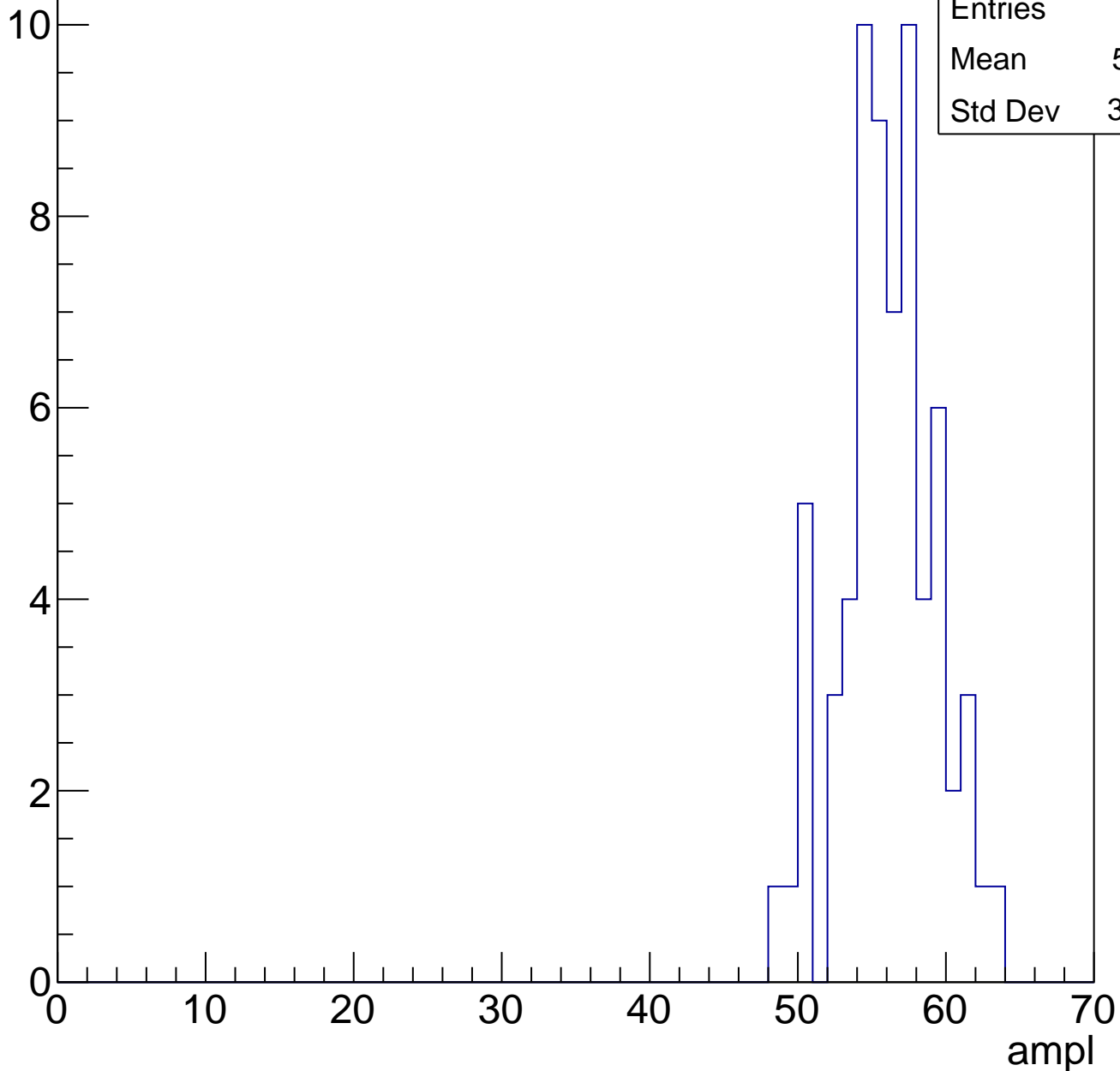


# B1L003S, U23-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	55.61
Std Dev	3.204

Entry



# B1L003S, U23-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	38
Mean	60.68
Std Dev	1.64

ampl

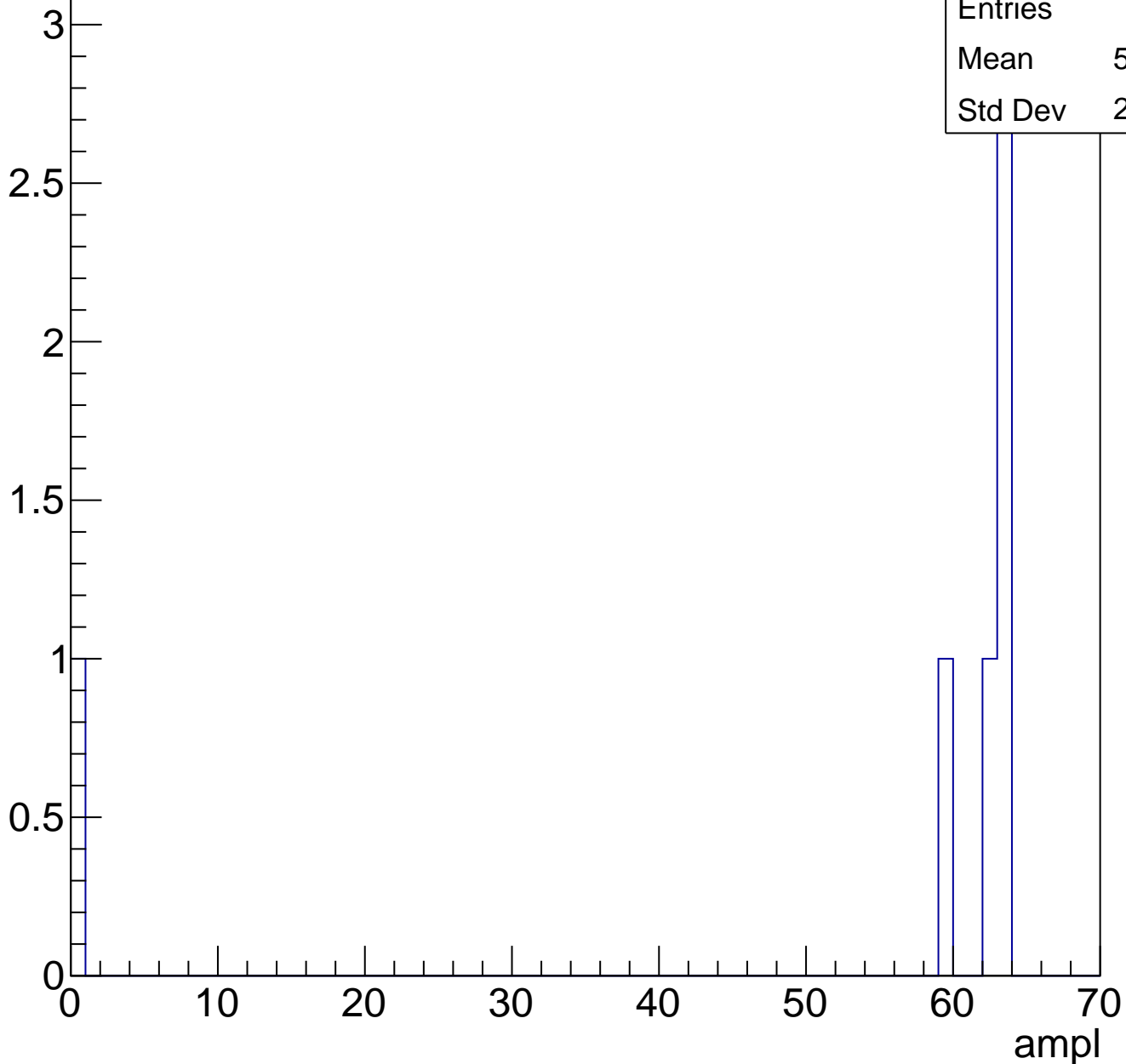
0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8

# B1L003S, U23-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	22
Std Dev	0

# B1L003S, U23-ch90, adc0

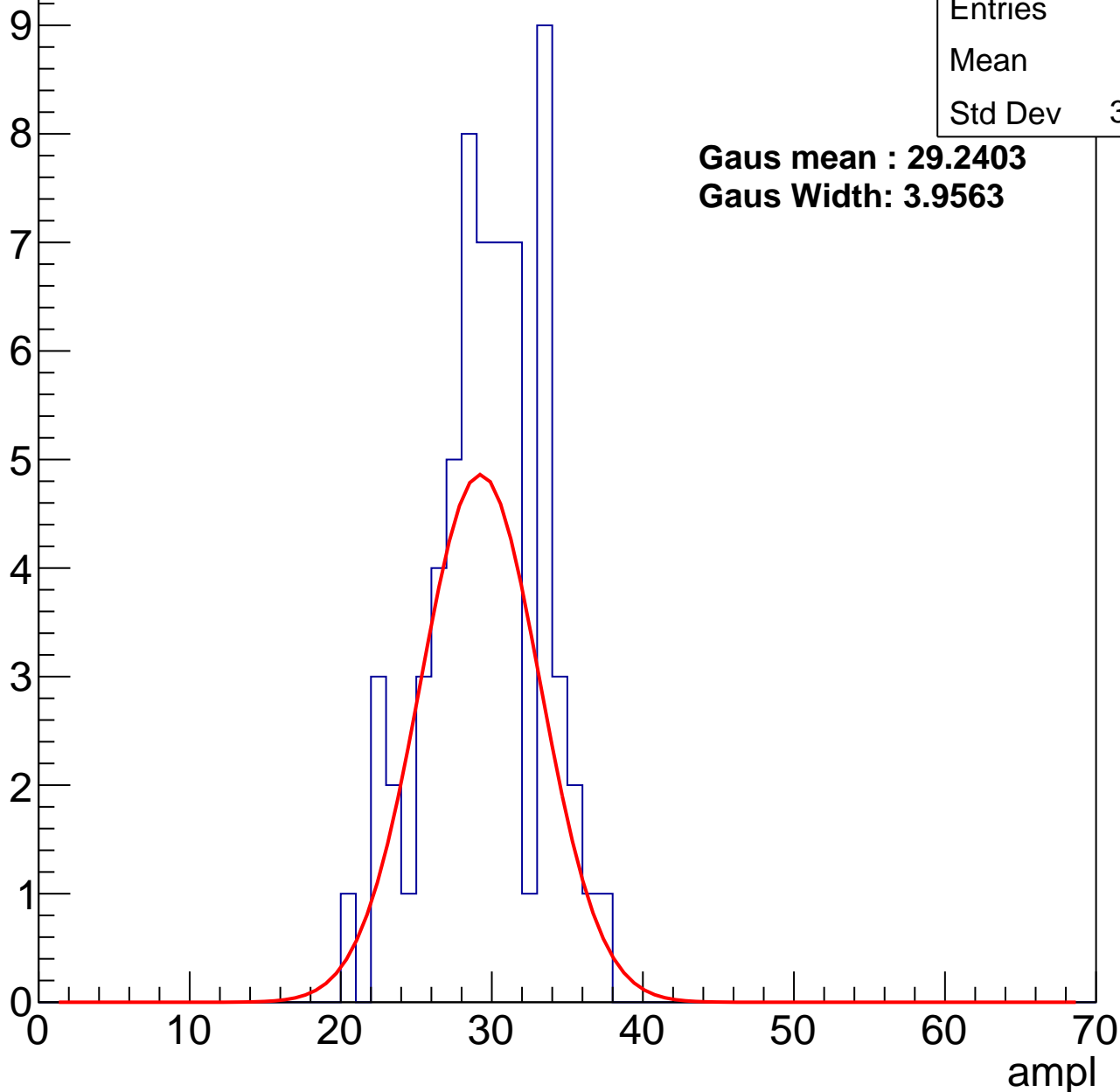
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.2
Std Dev	3.684

**Gaus mean : 29.2403**

**Gaus Width: 3.9563**



# B1L003S, U23-ch90, adc1

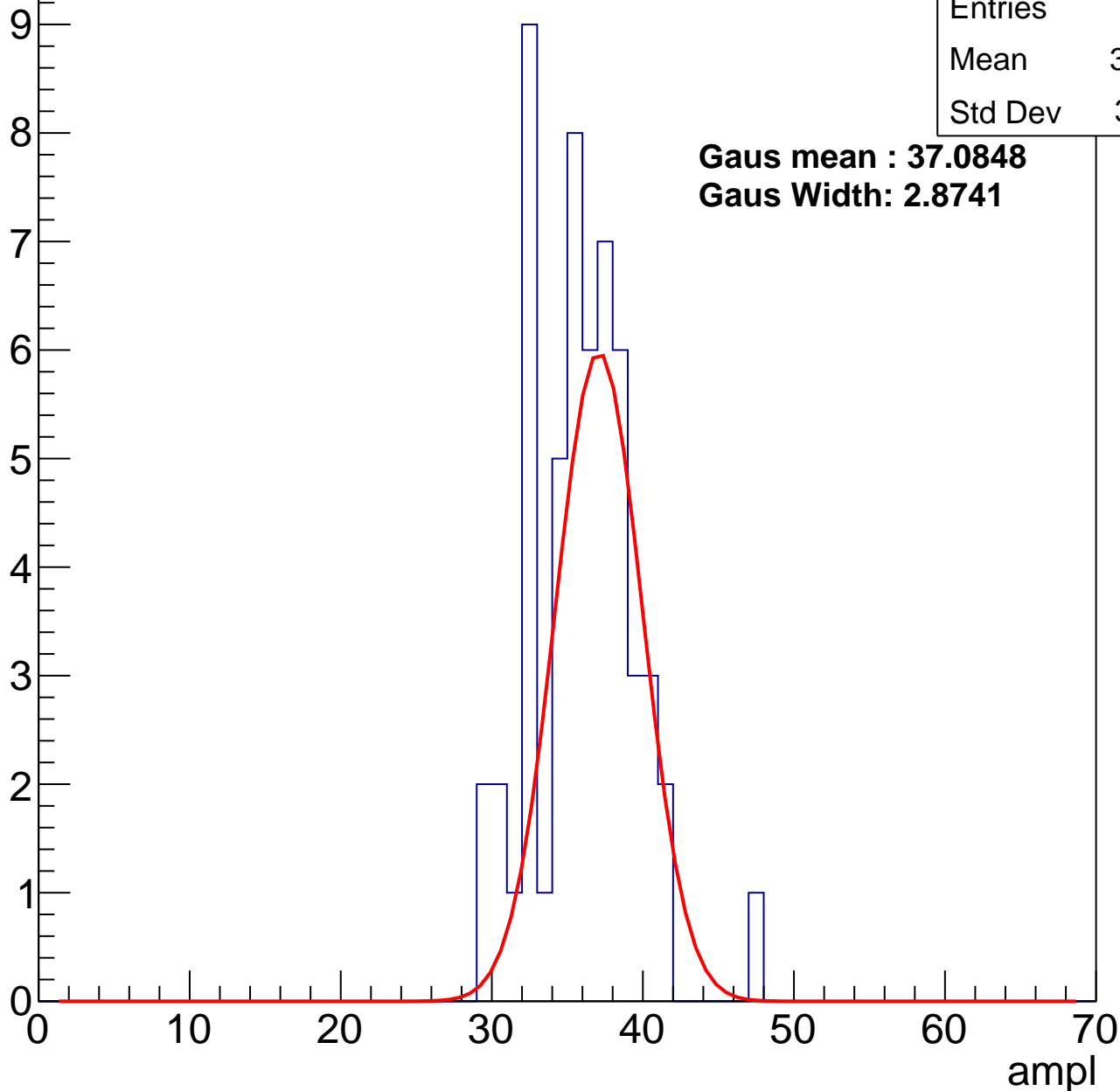
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	35.52
Std Dev	3.381

**Gaus mean : 37.0848**

**Gaus Width: 2.8741**



# B1L003S, U23-ch90, adc2

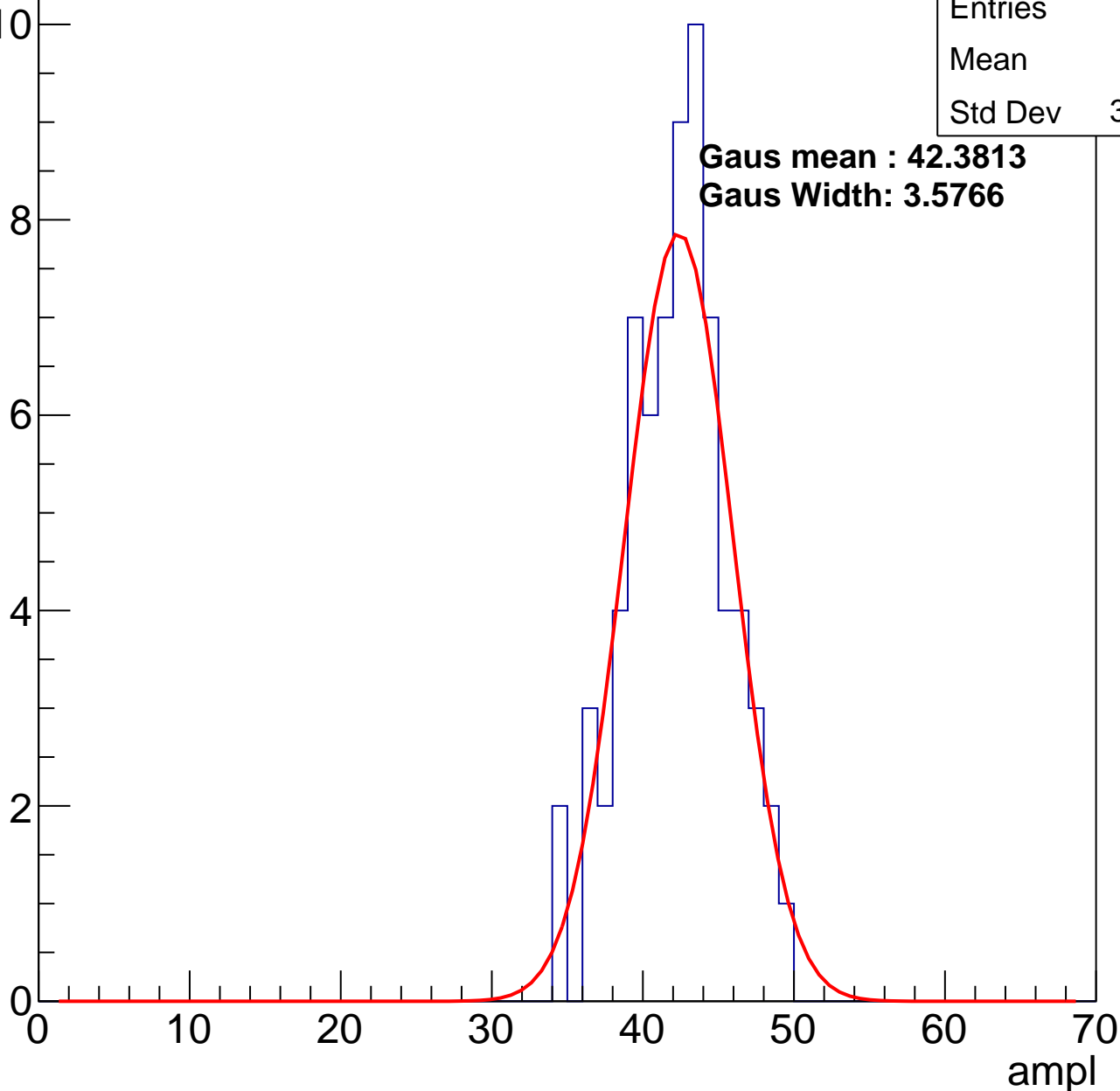
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	41.8
Std Dev	3.304

**Gaus mean : 42.3813**

**Gaus Width: 3.5766**

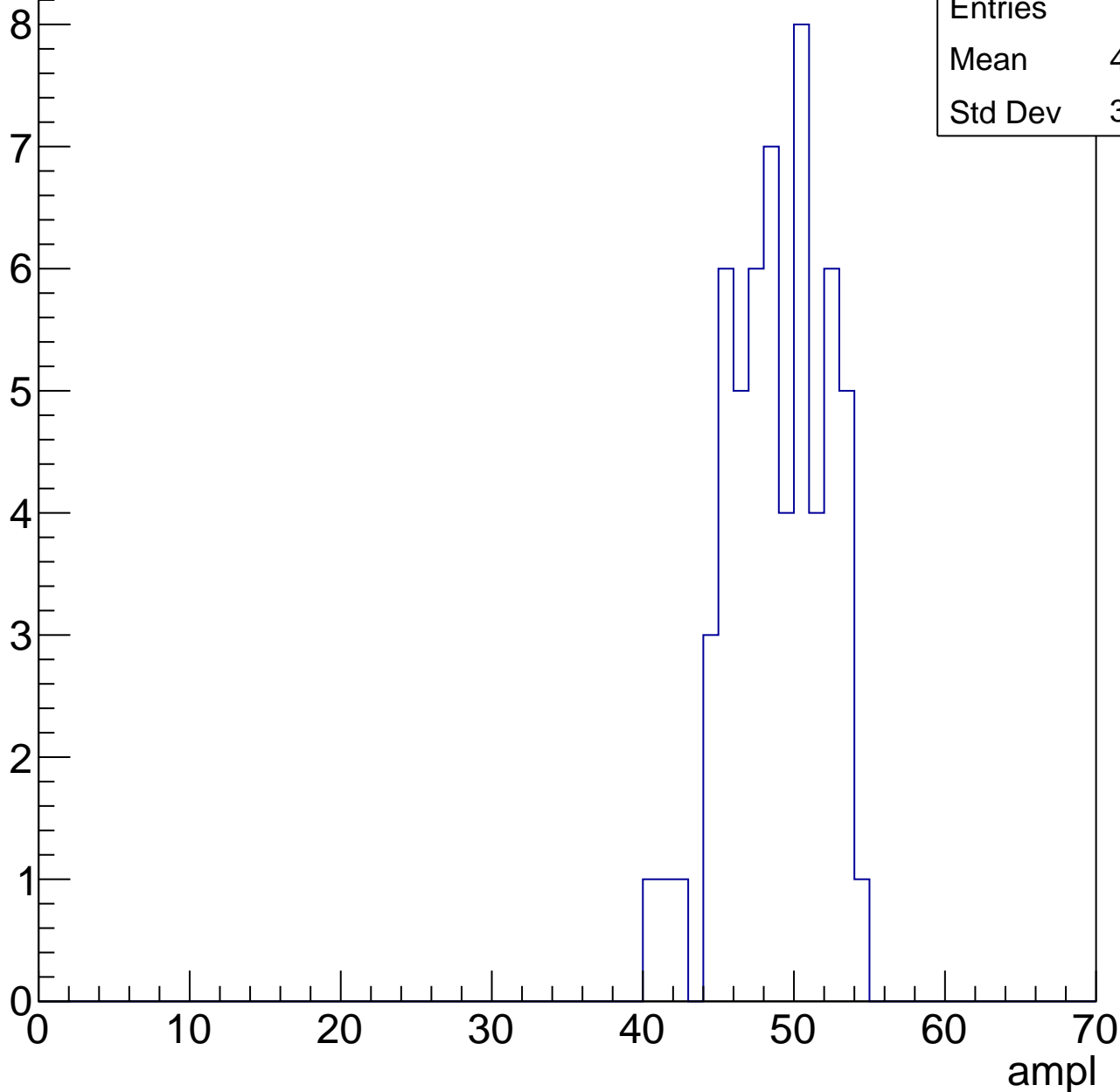


# B1L003S, U23-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

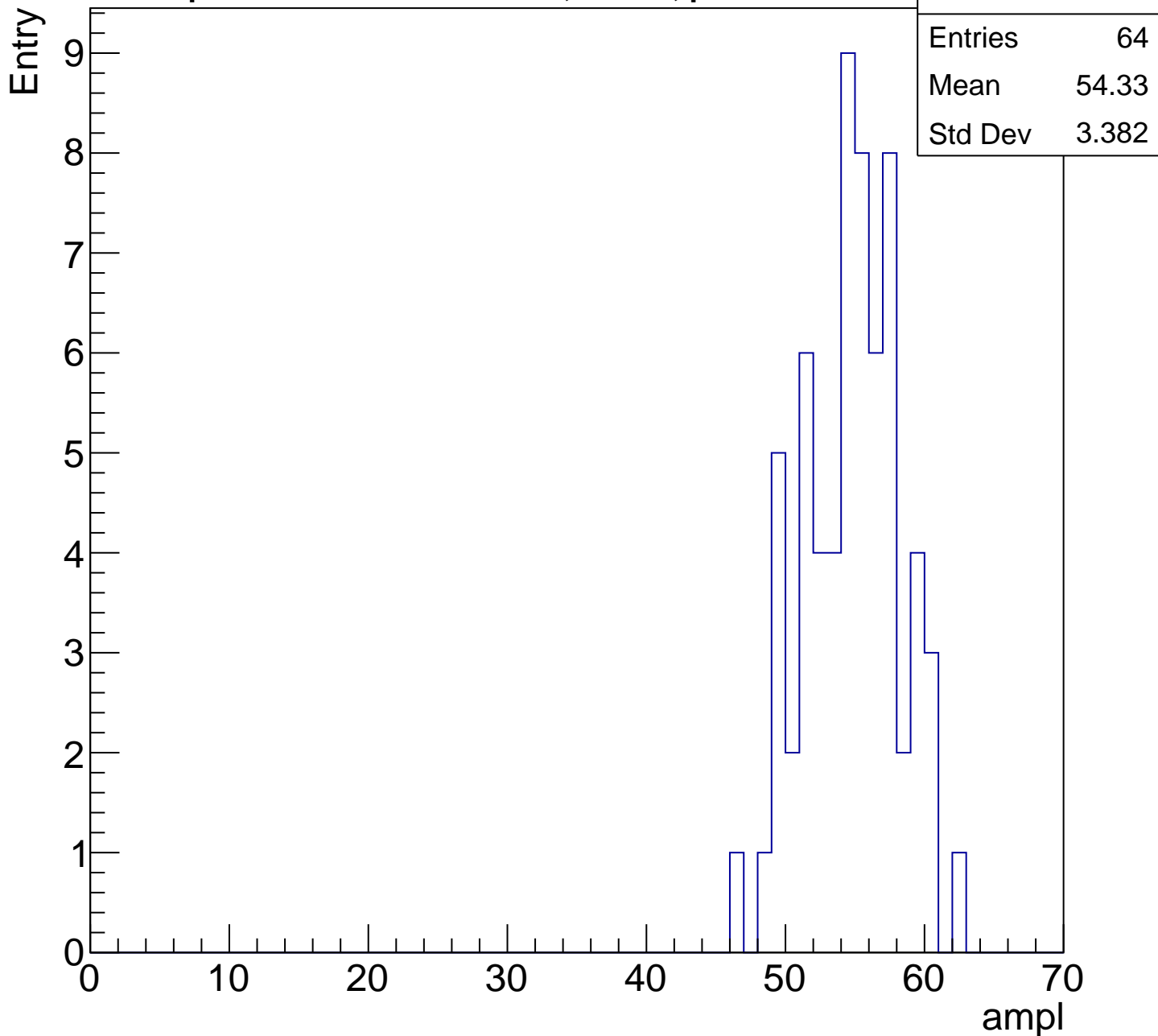
Entry

Entries	58
Mean	48.34
Std Dev	3.214



# B1L003S, U23-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

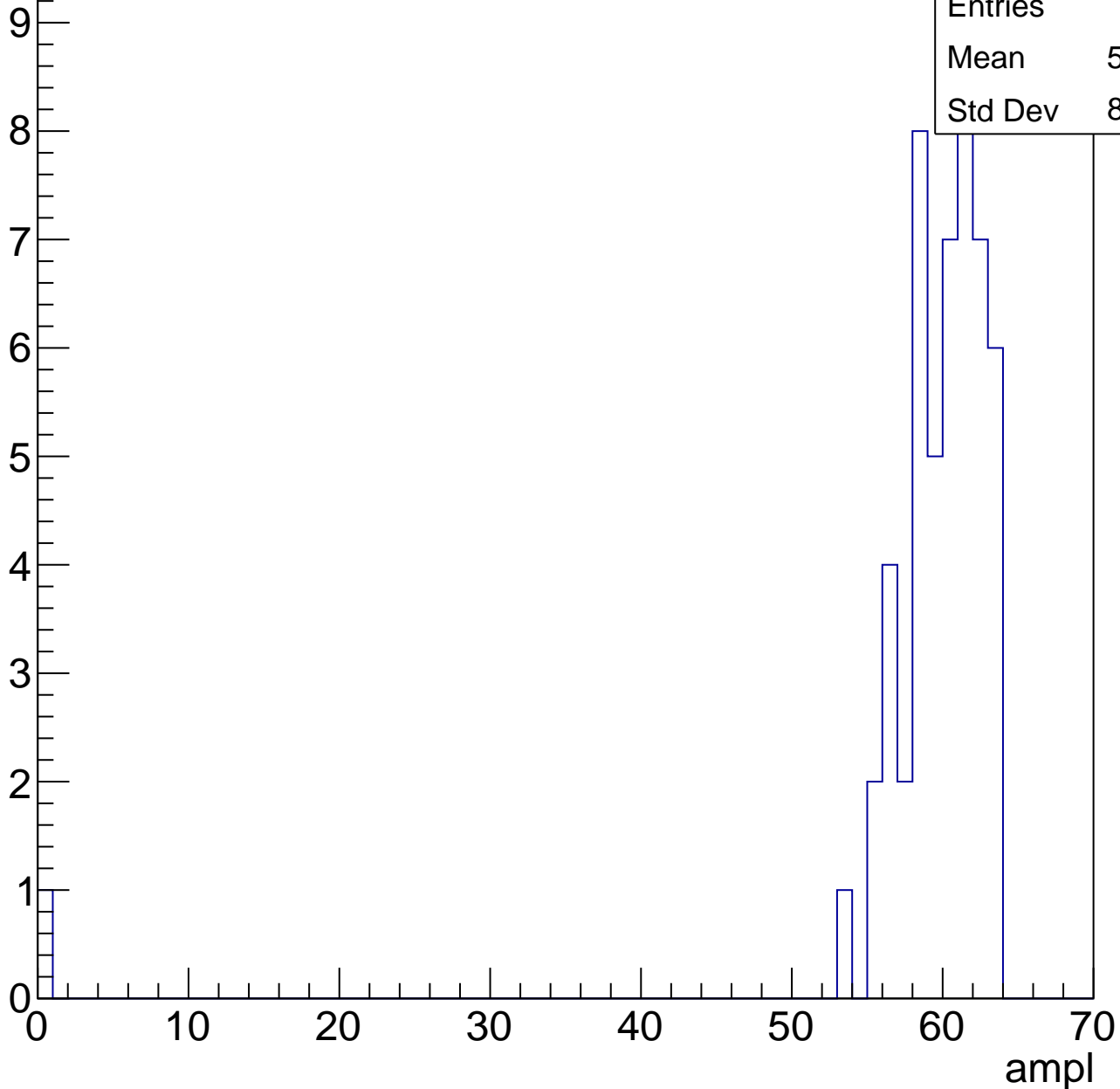


# B1L003S, U23-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

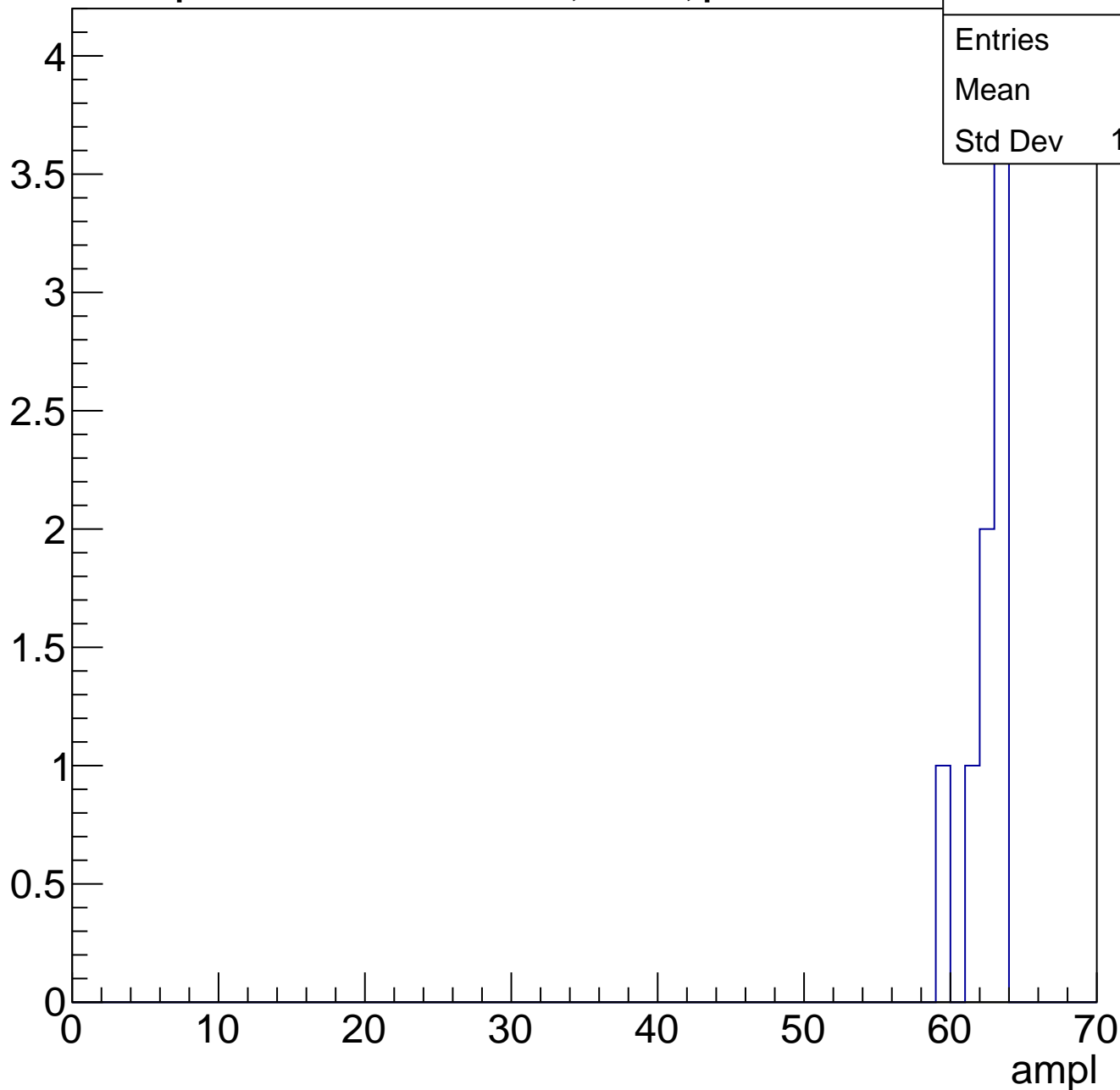
Entries	52
Mean	58.48
Std Dev	8.536



# B1L003S, U23-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch91, adc0

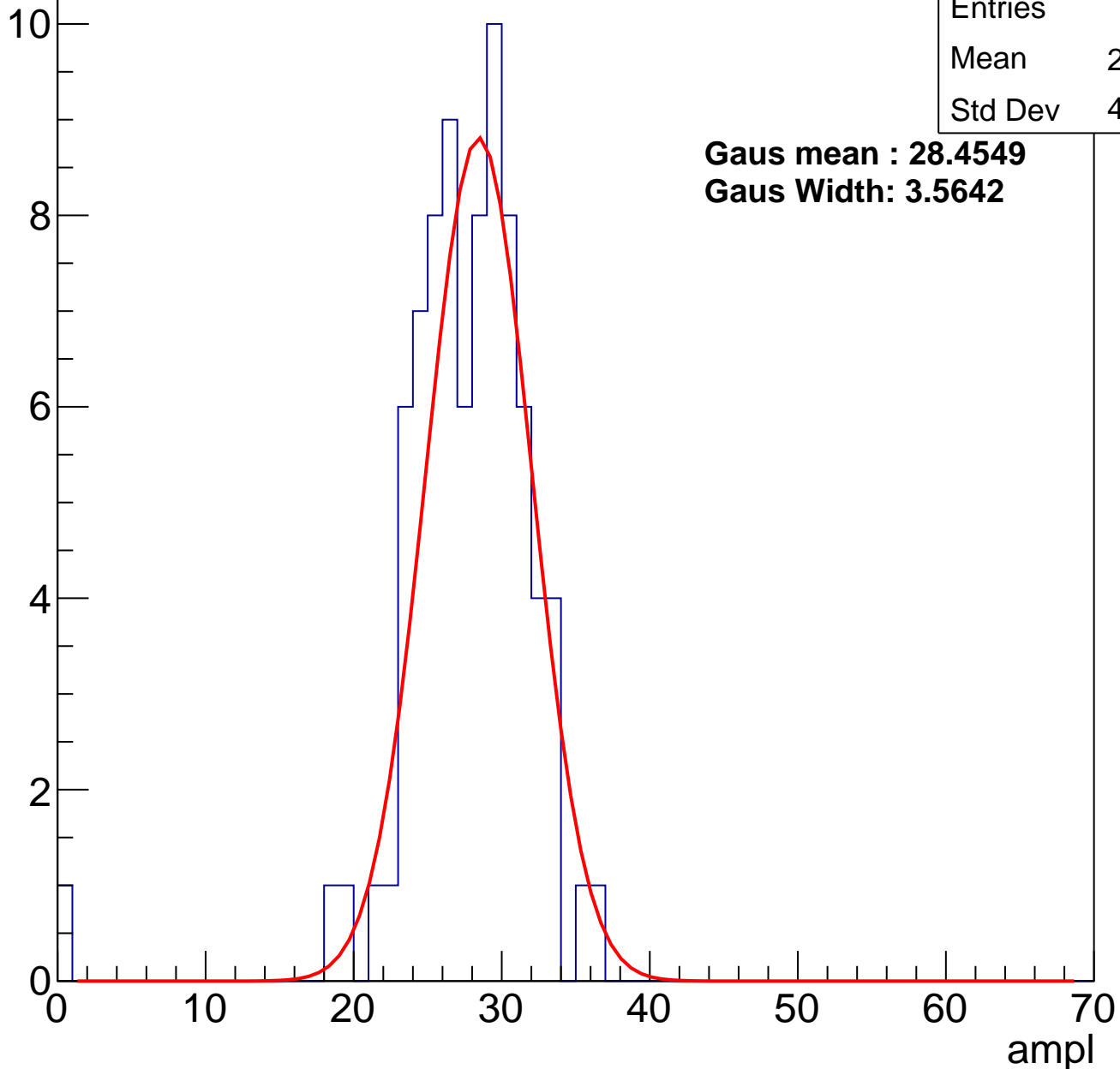
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	83
Mean	27.14
Std Dev	4.576

**Gaus mean : 28.4549**

**Gaus Width: 3.5642**

Entry



# B1L003S, U23-ch91, adc1

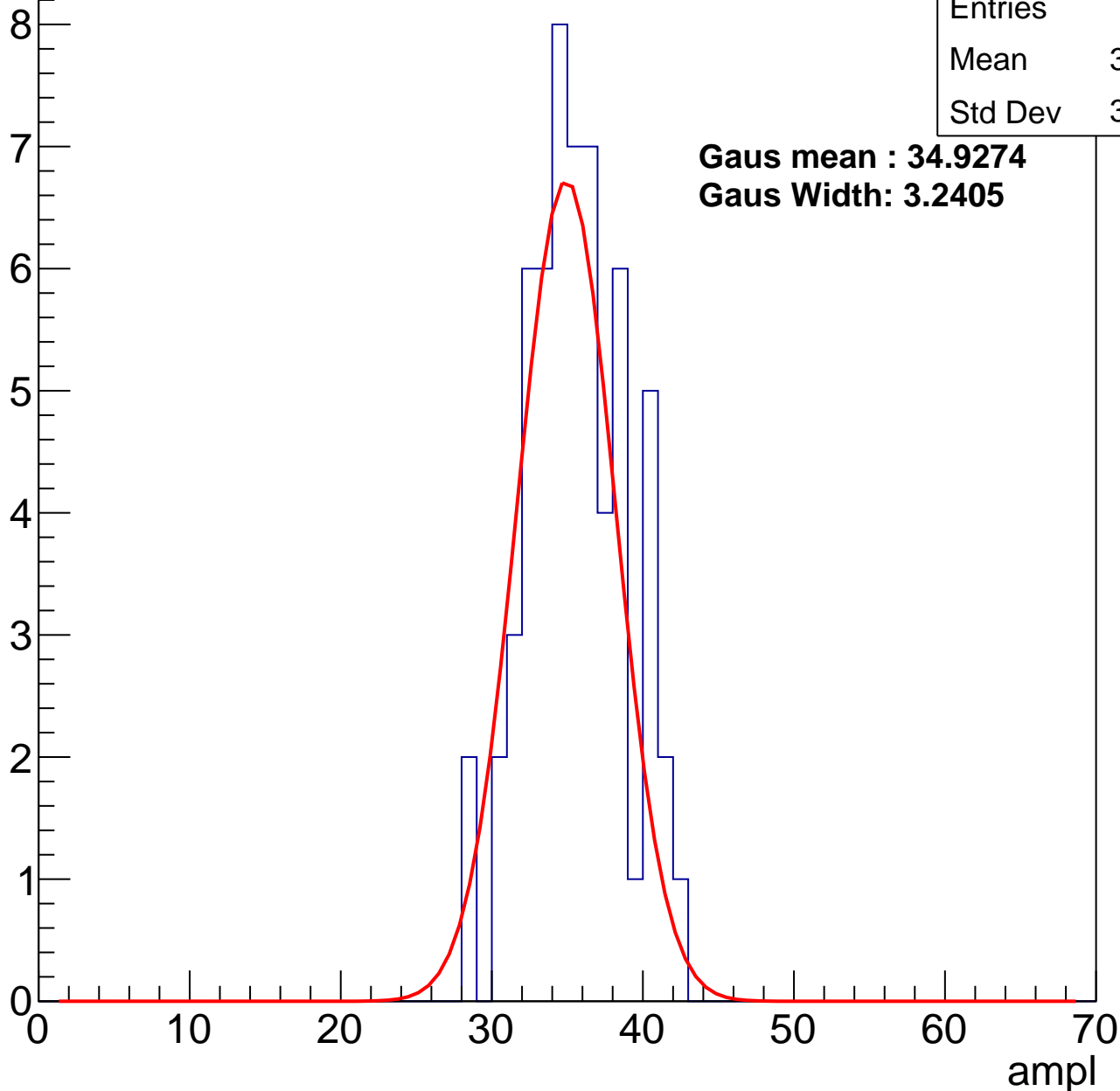
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.12
Std Dev	3.215

**Gaus mean : 34.9274**

**Gaus Width: 3.2405**



# B1L003S, U23-ch91, adc2

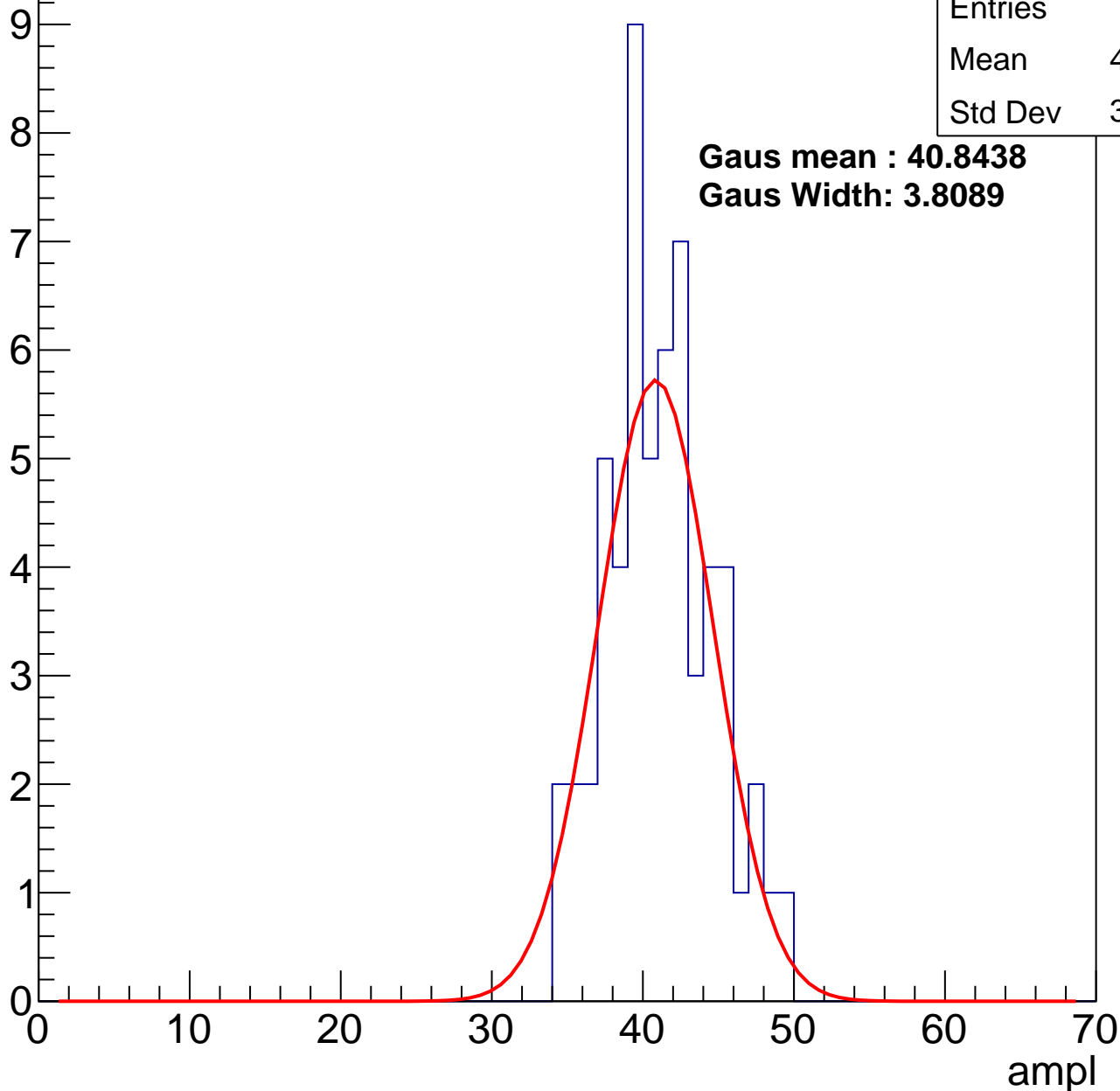
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	40.69
Std Dev	3.475

**Gaus mean : 40.8438**

**Gaus Width: 3.8089**

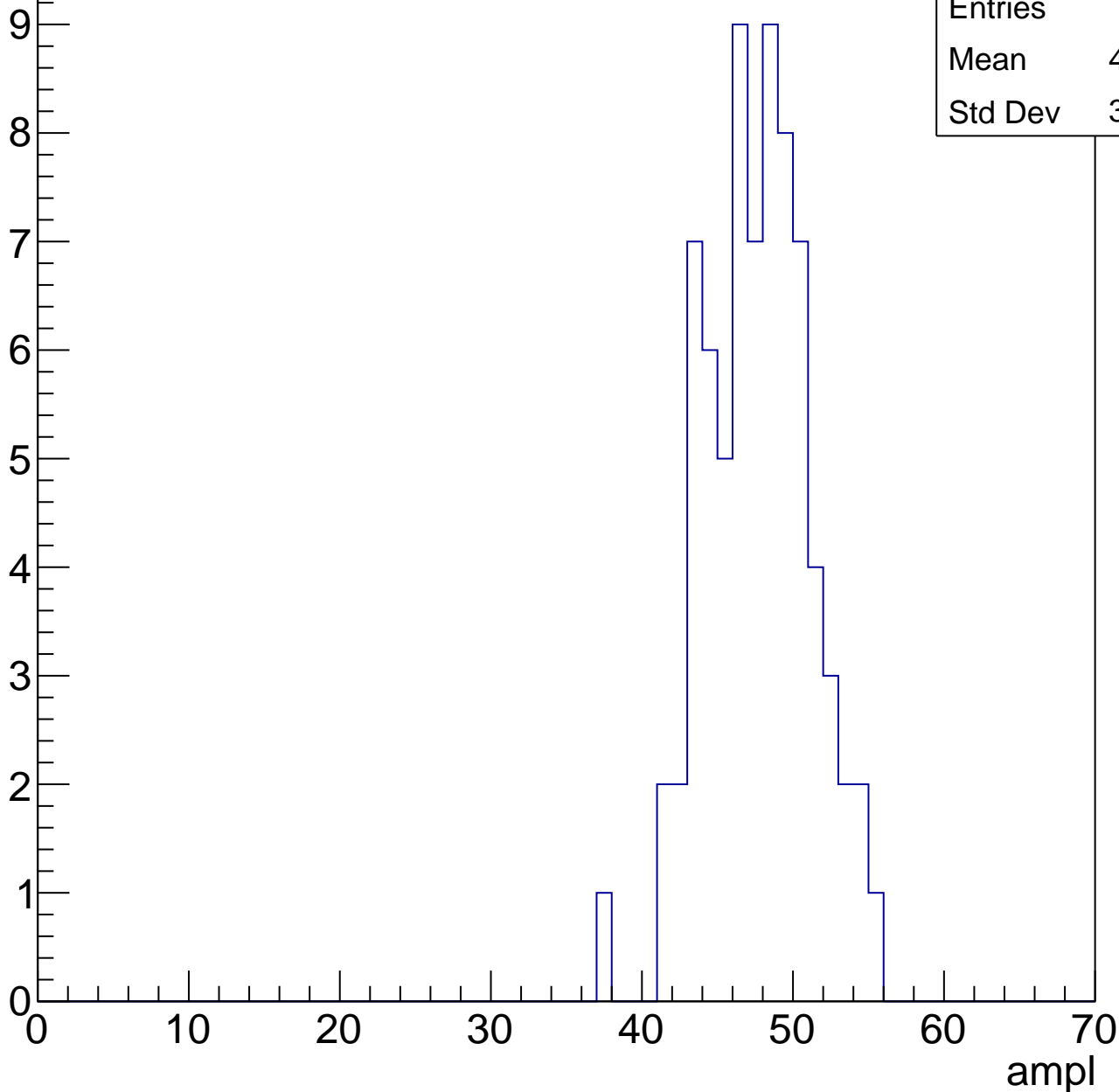


# B1L003S, U23-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	47.19
Std Dev	3.447

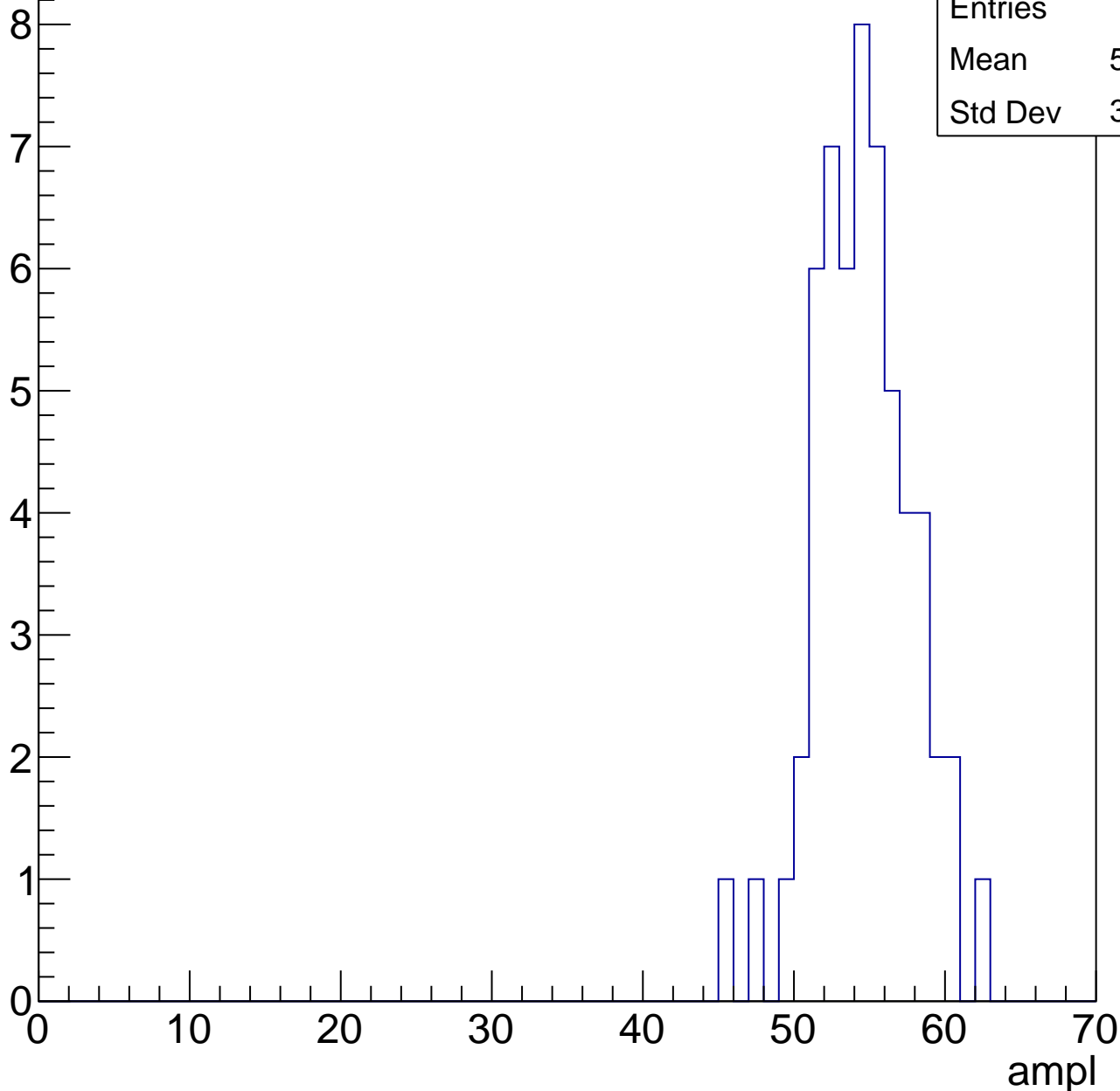


# B1L003S, U23-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	54.14
Std Dev	3.209

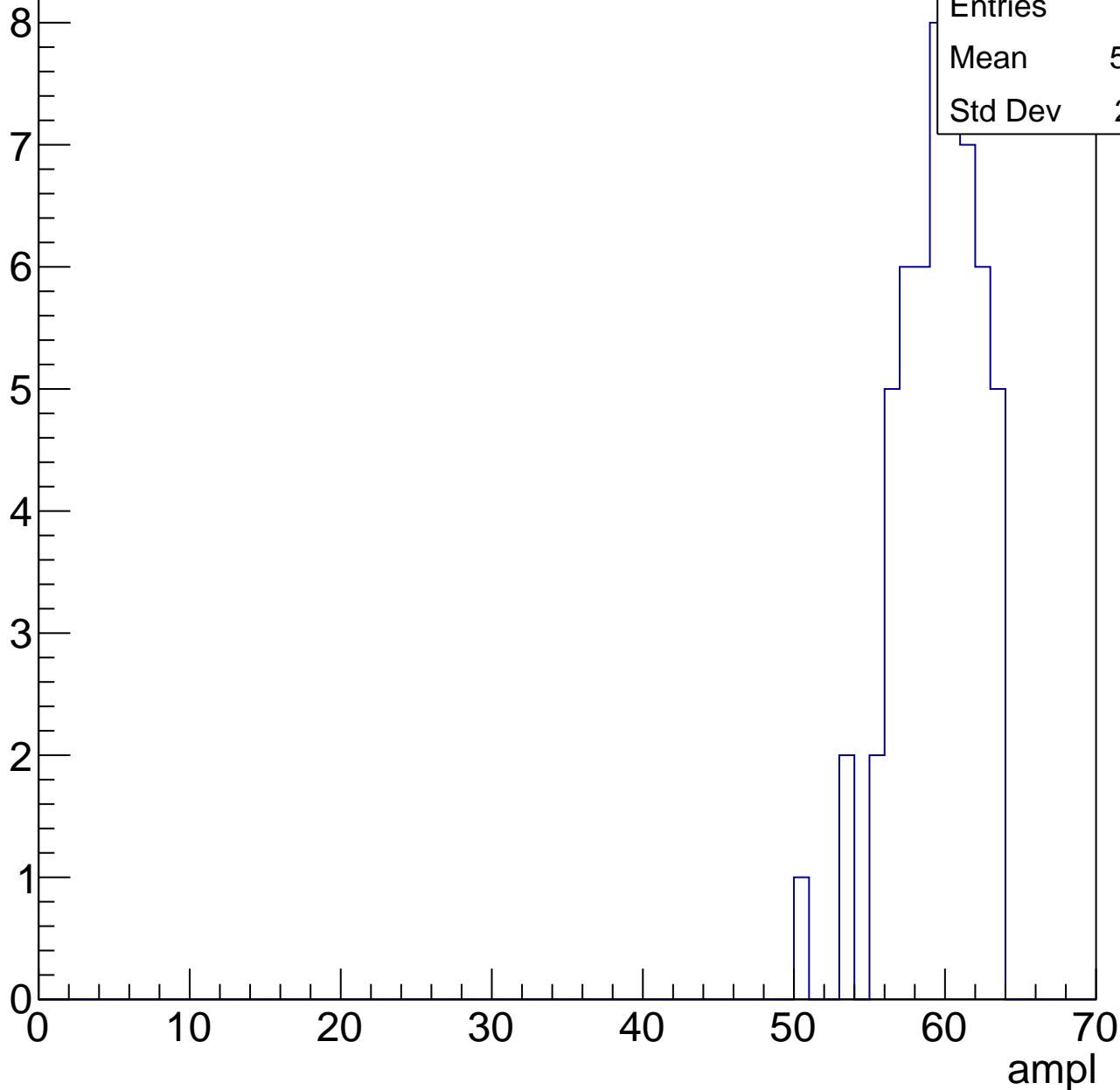


# B1L003S, U23-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.96
Std Dev	2.771

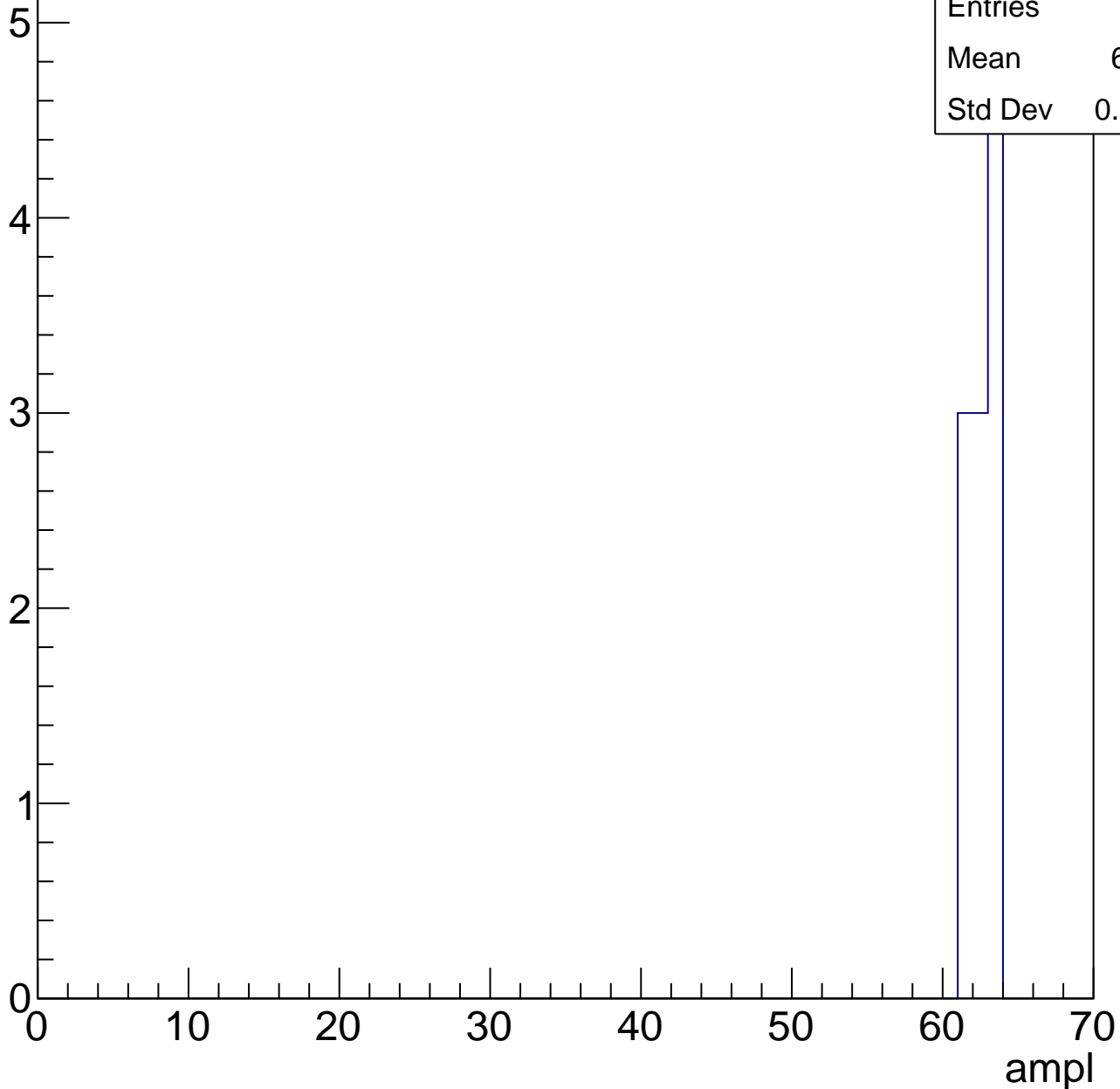


# B1L003S, U23-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.18
Std Dev	0.8332

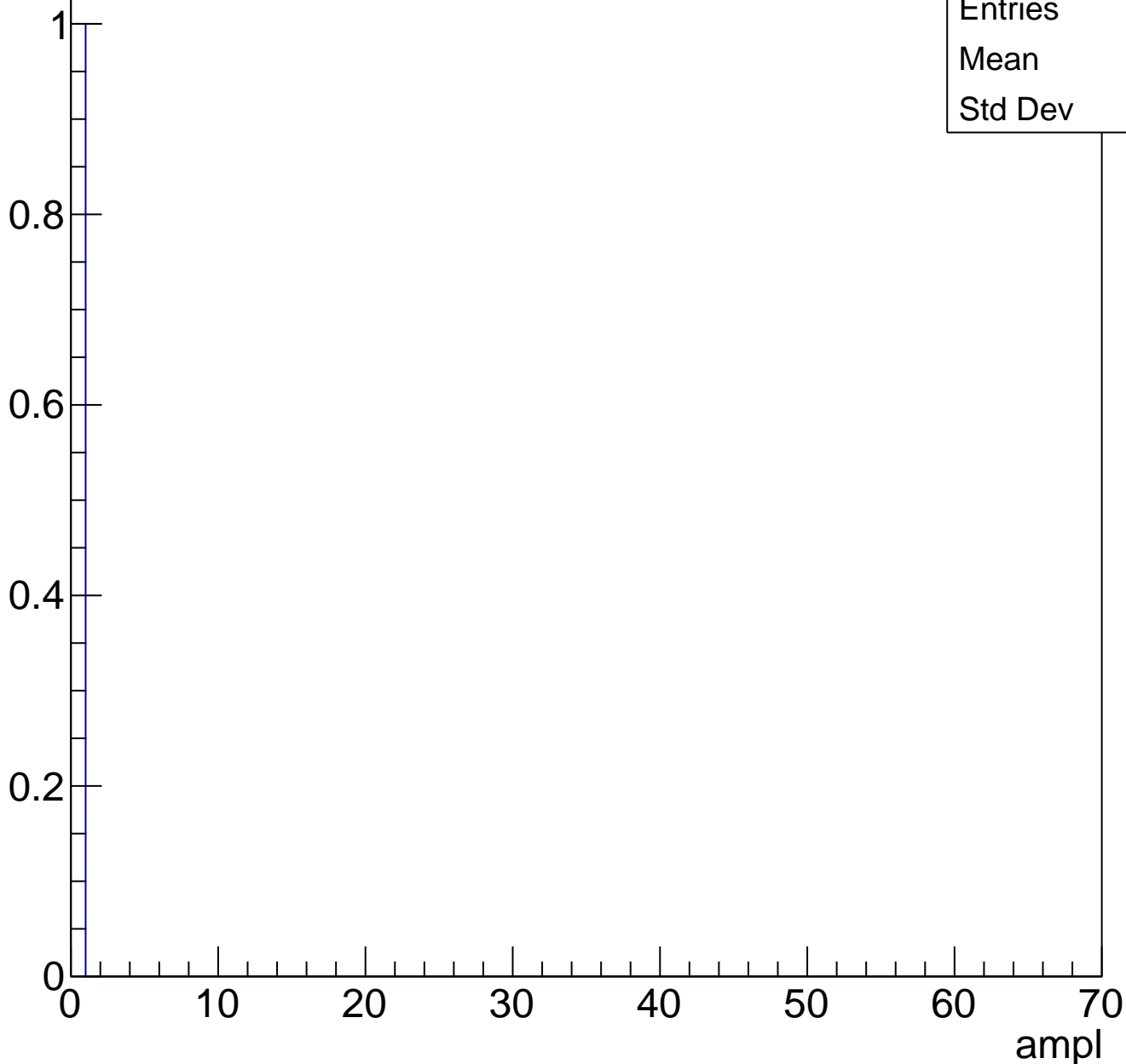




# B1L003S, U23-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch92, adc0

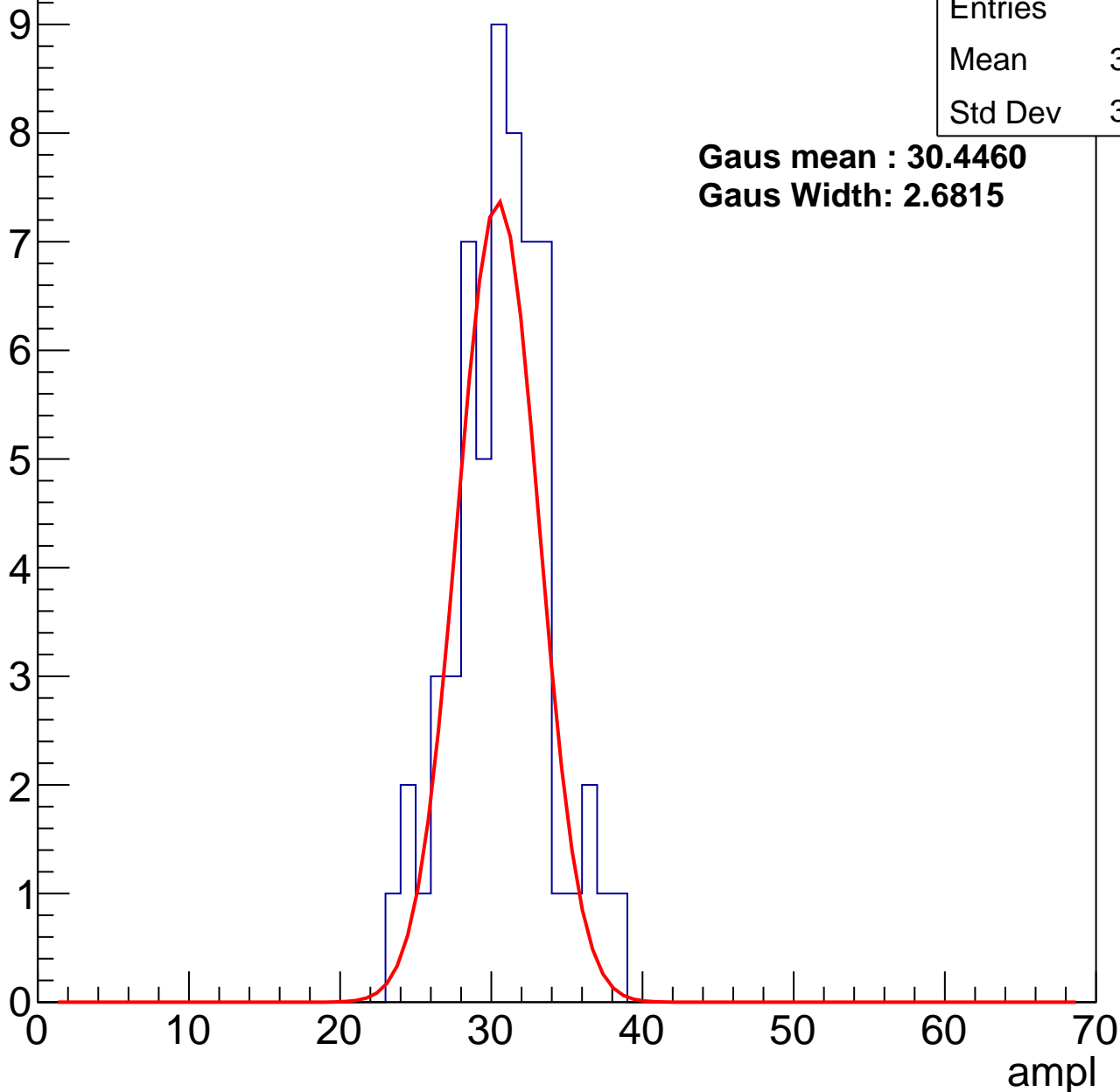
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	30.25
Std Dev	3.122

**Gaus mean : 30.4460**

**Gaus Width: 2.6815**



# B1L003S, U23-ch92, adc1

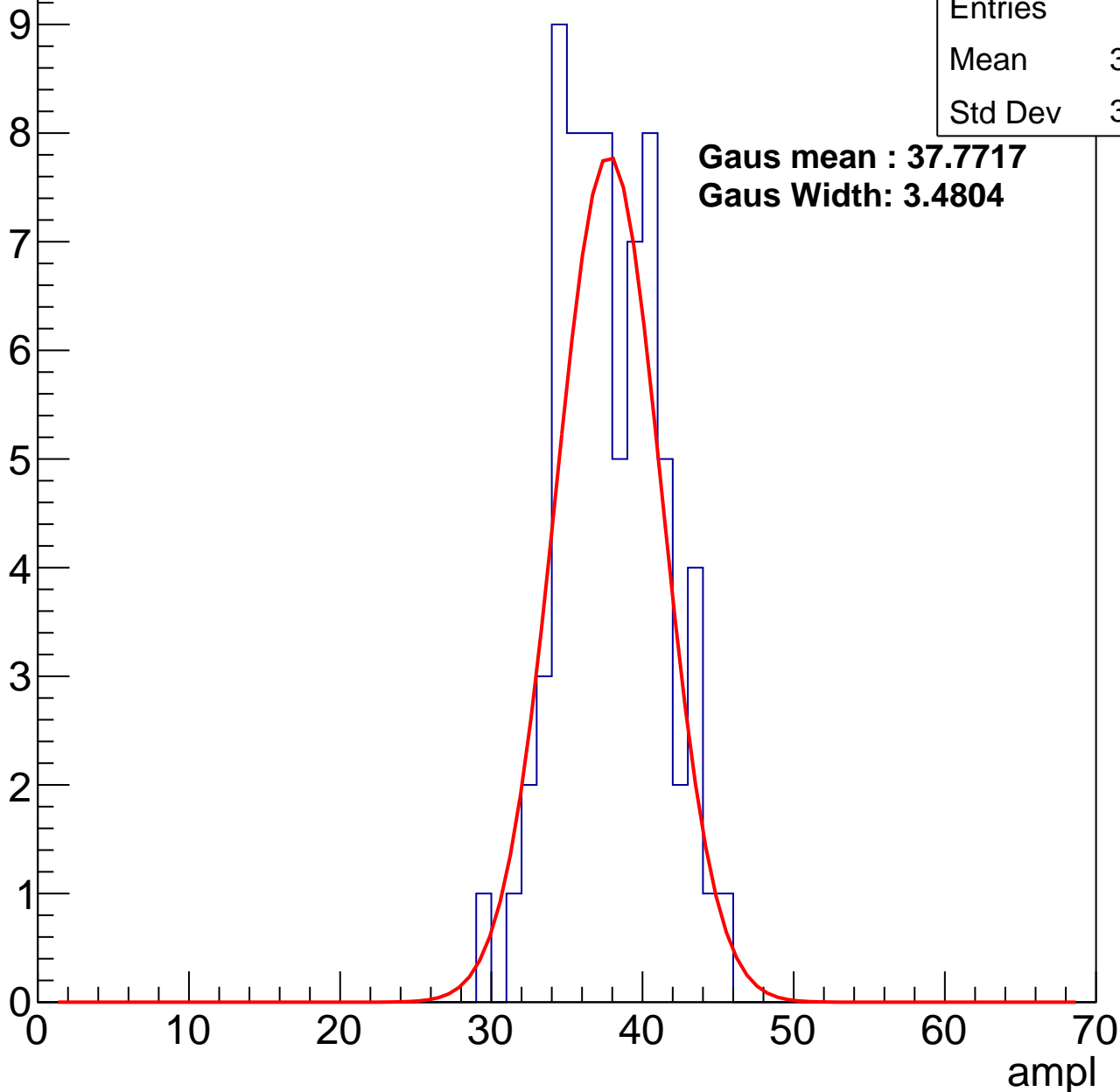
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37.34
Std Dev	3.315

**Gaus mean : 37.7717**

**Gaus Width: 3.4804**



# B1L003S, U23-ch92, adc2

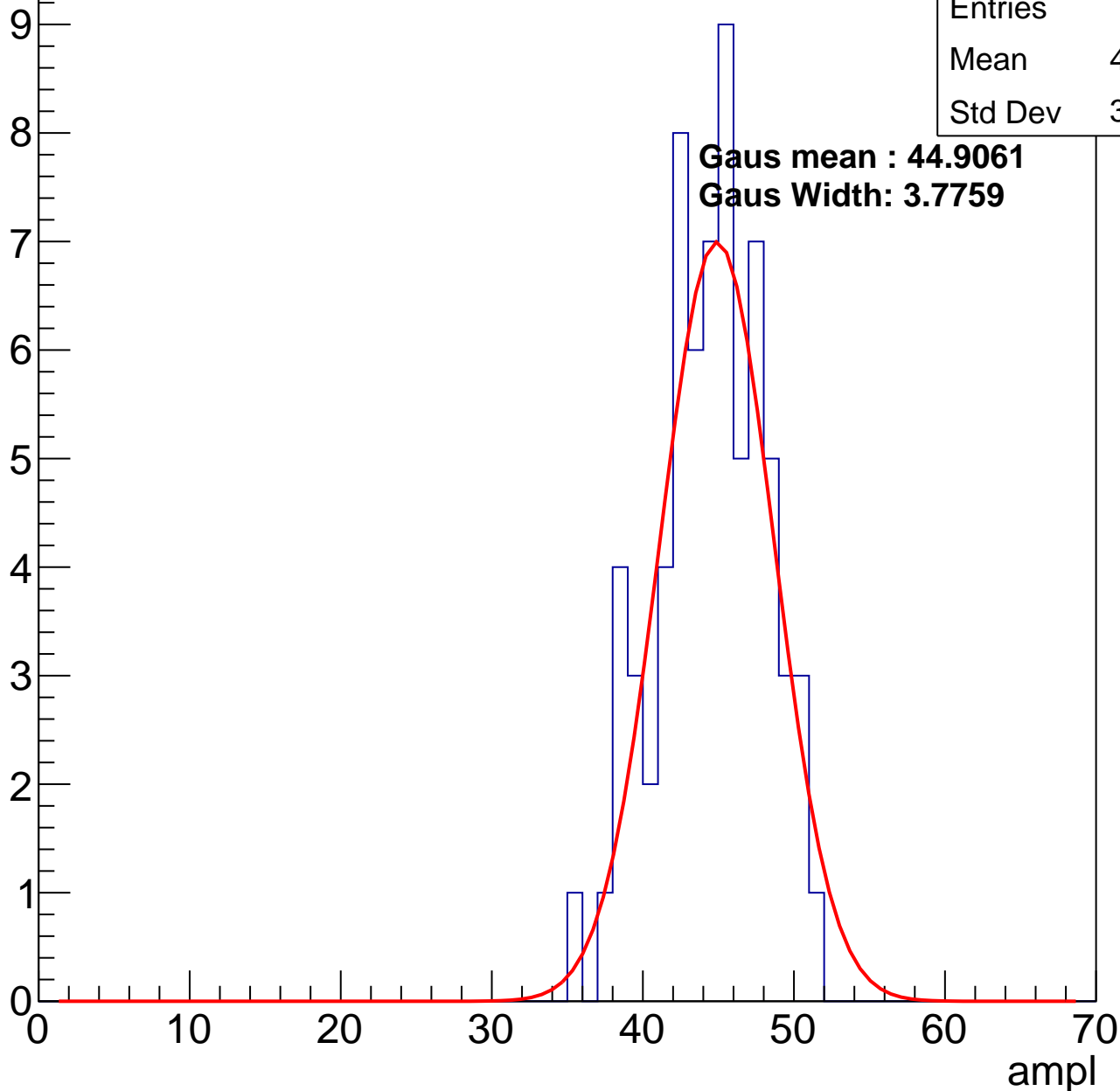
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	44.04
Std Dev	3.528

**Gaus mean : 44.9061**

**Gaus Width: 3.7759**

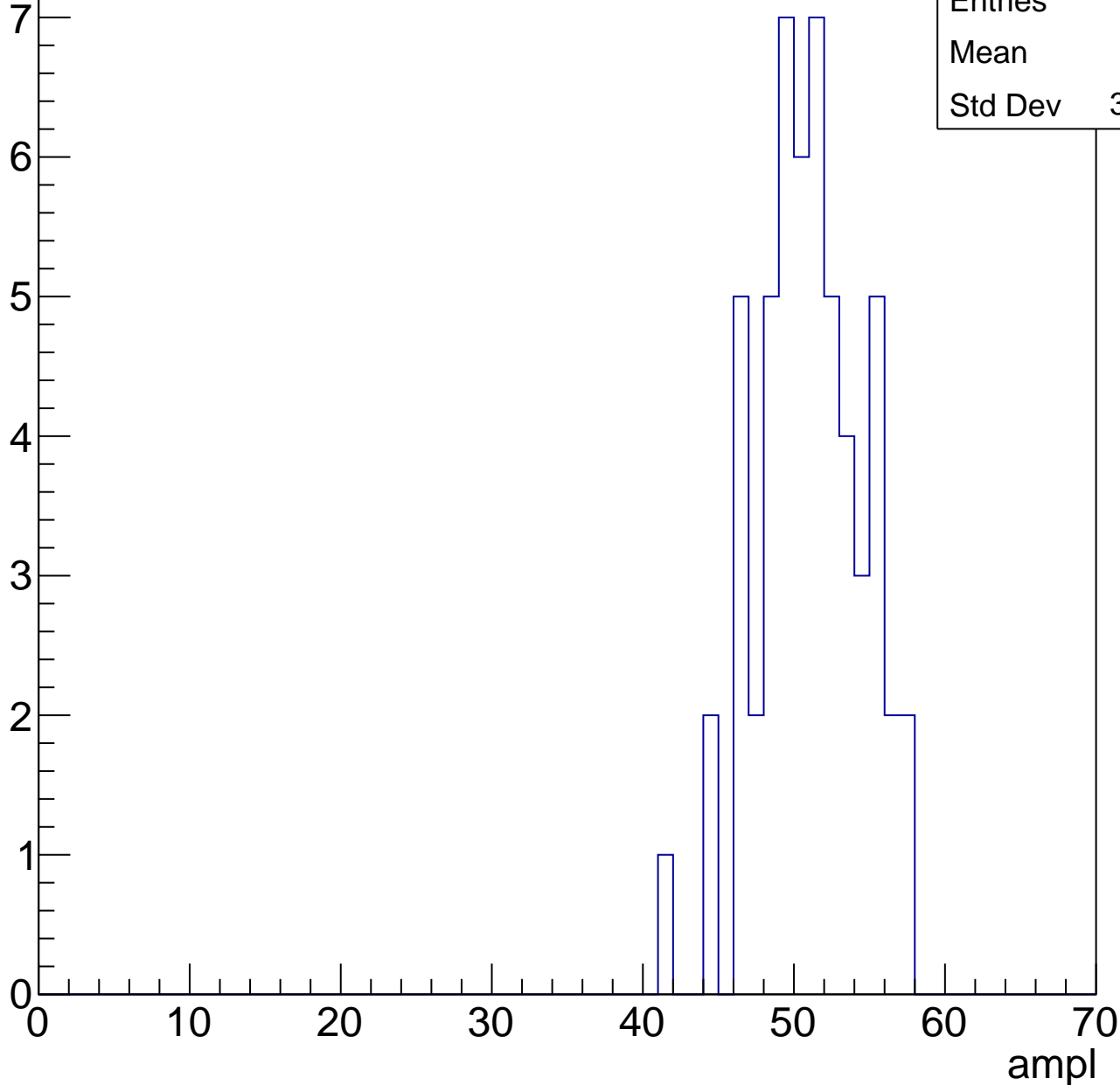


# B1L003S, U23-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	50.5
Std Dev	3.459

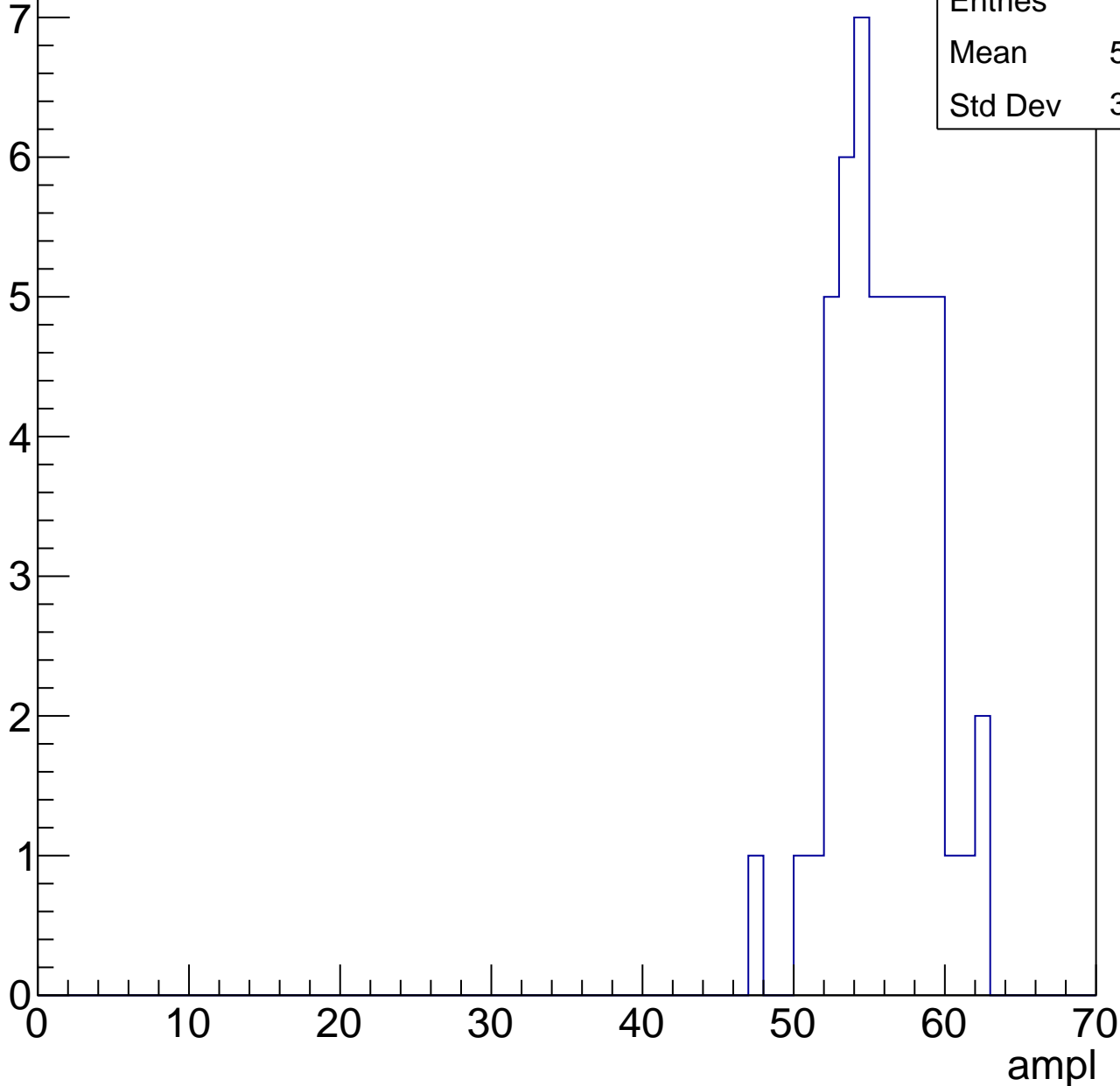


# B1L003S, U23-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	55.48
Std Dev	3.093

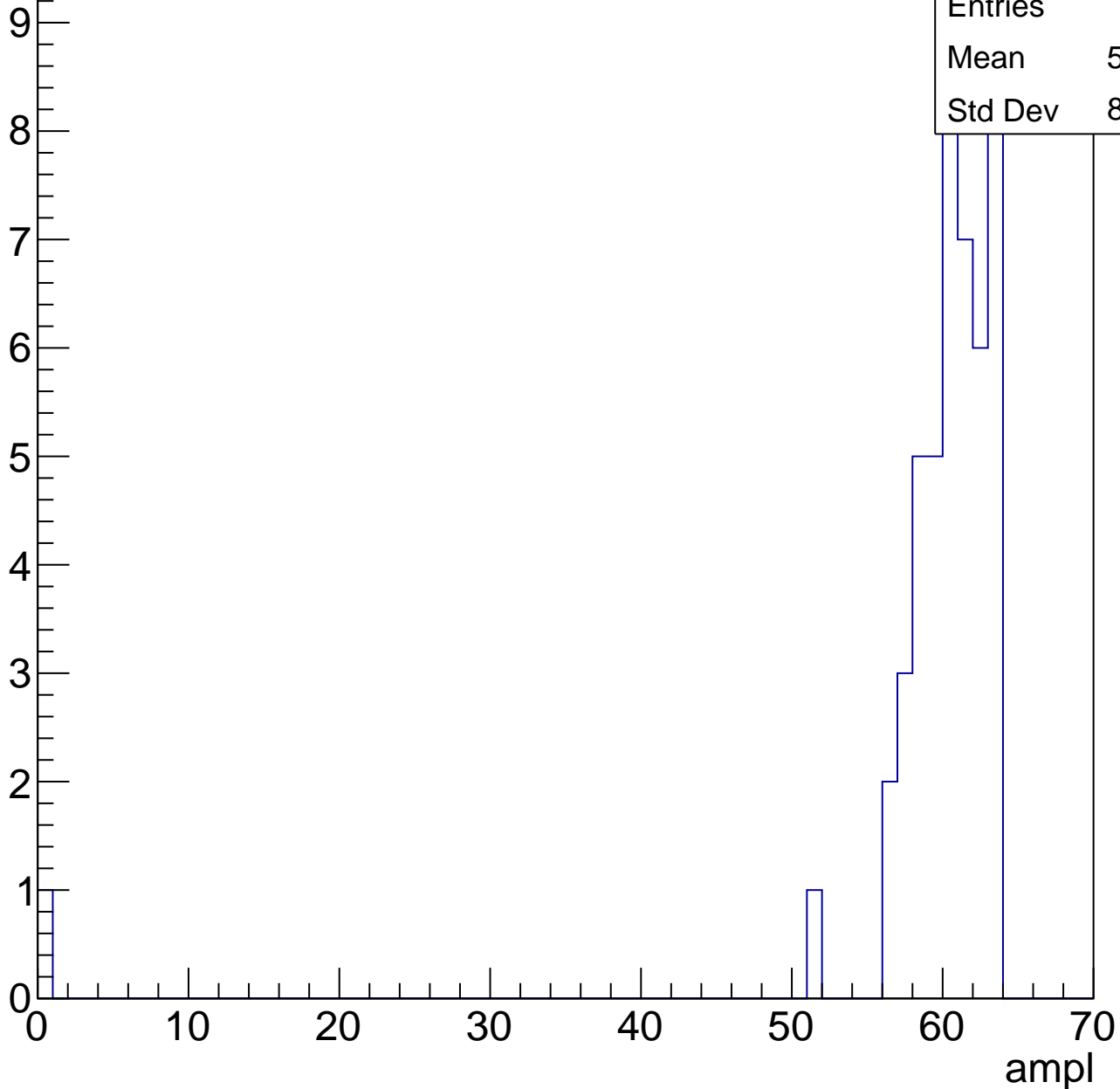


# B1L003S, U23-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

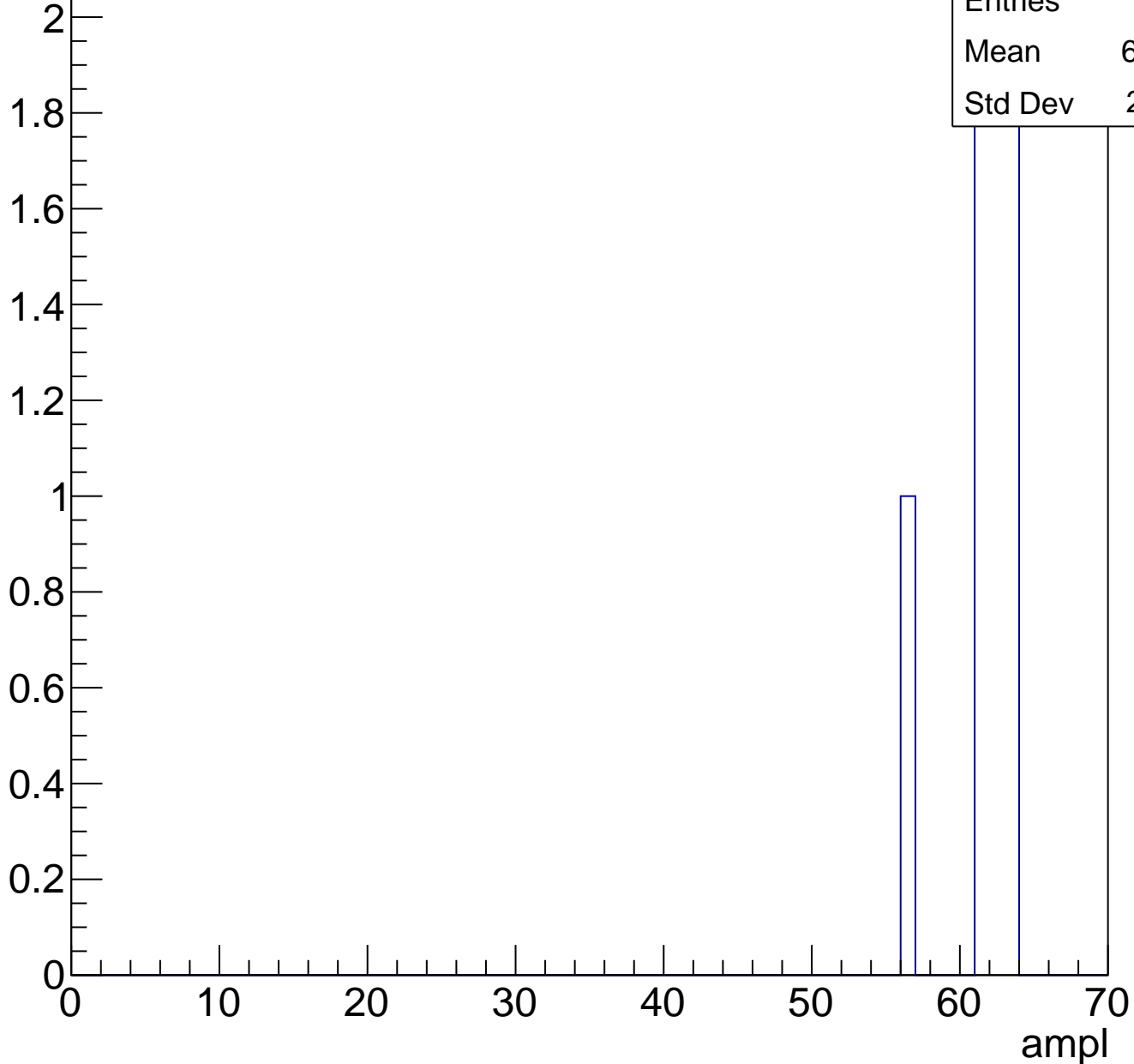
Entries	47
Mean	58.77
Std Dev	8.987



# B1L003S, U23-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch93, adc0

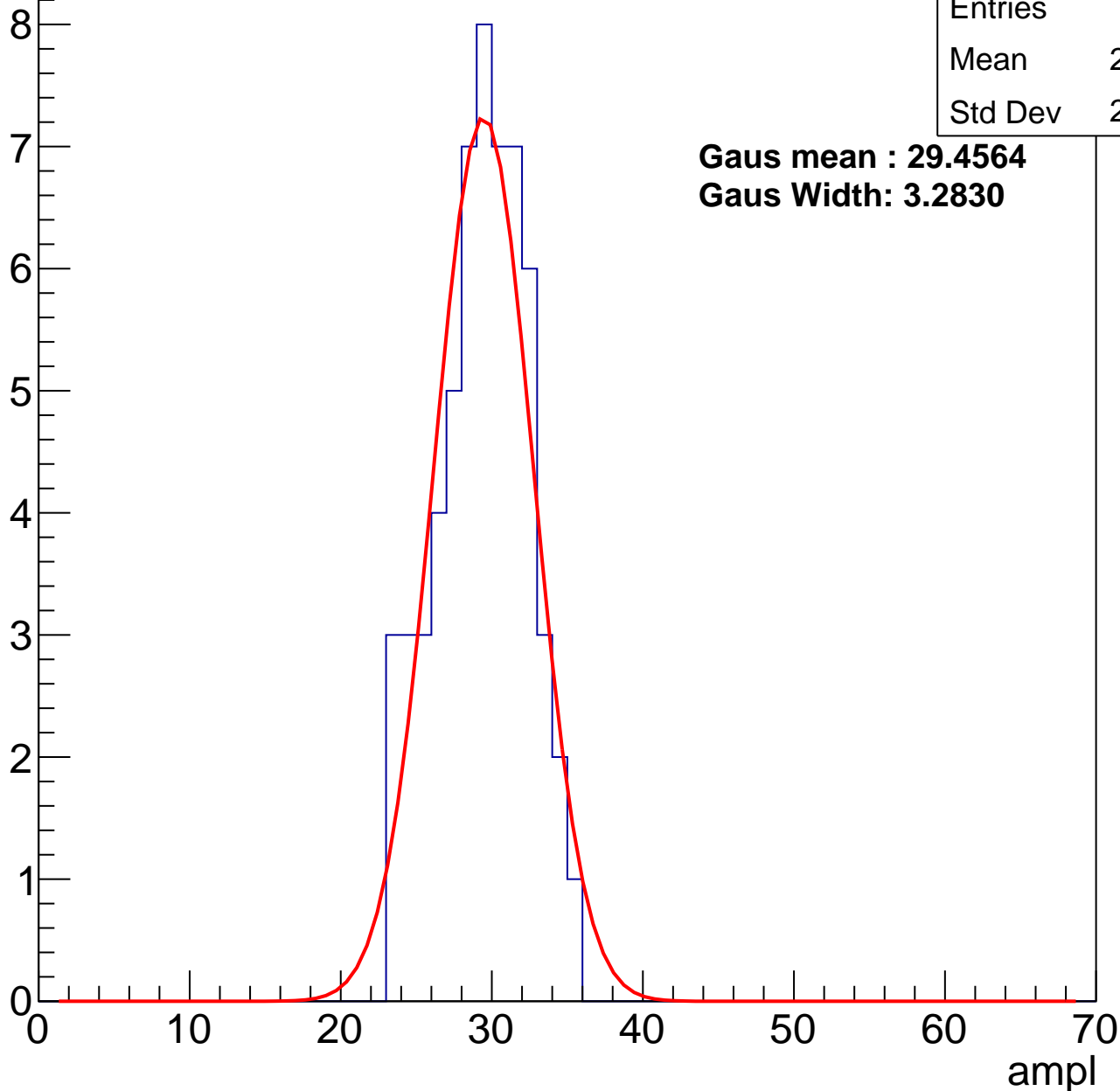
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	28.88
Std Dev	2.958

**Gaus mean : 29.4564**

**Gaus Width: 3.2830**



# B1L003S, U23-ch93, adc1

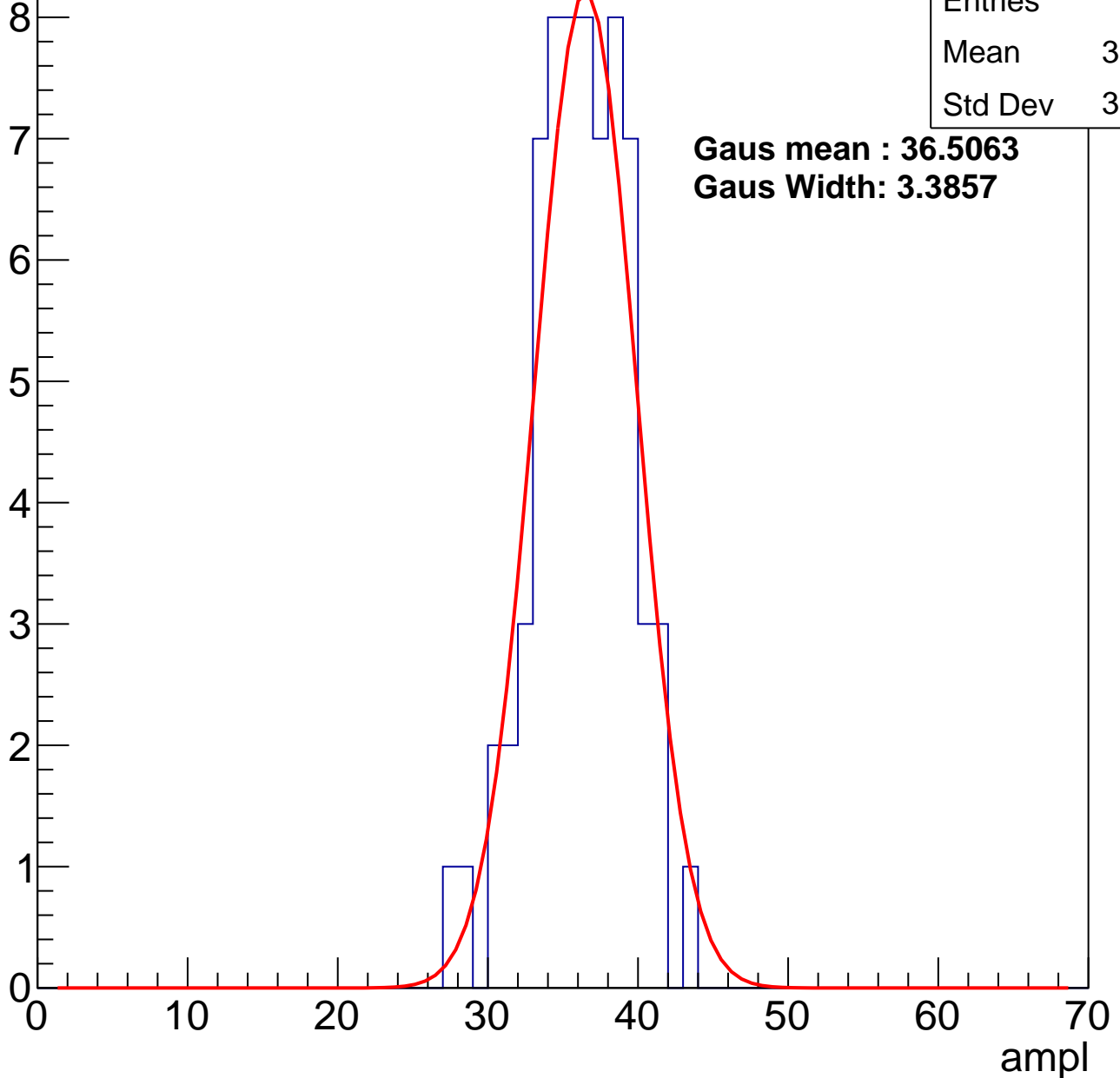
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.74
Std Dev	3.156

**Gaus mean : 36.5063**

**Gaus Width: 3.3857**



# B1L003S, U23-ch93, adc2

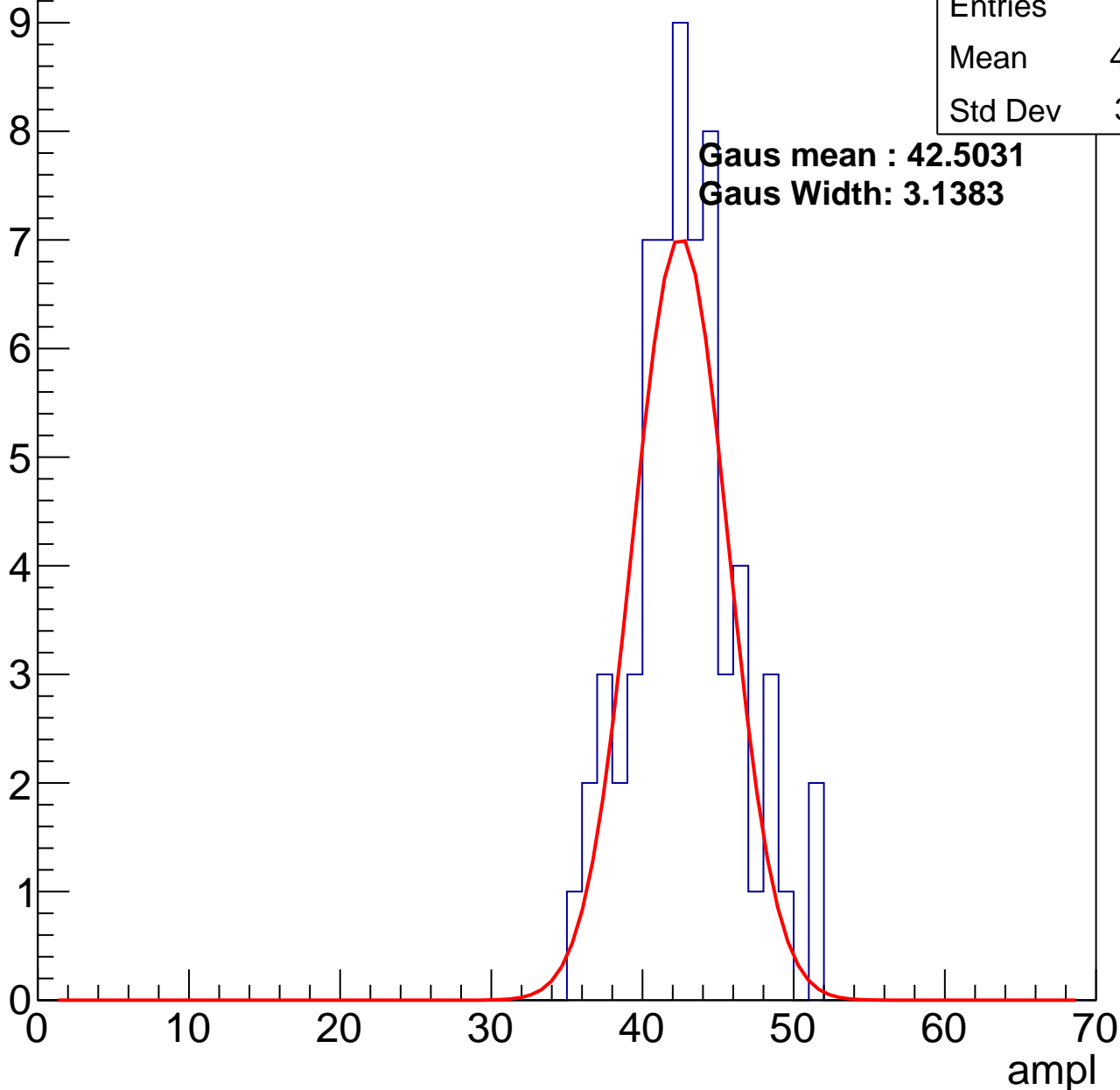
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.38
Std Dev	3.461

**Gaus mean : 42.5031**

**Gaus Width: 3.1383**

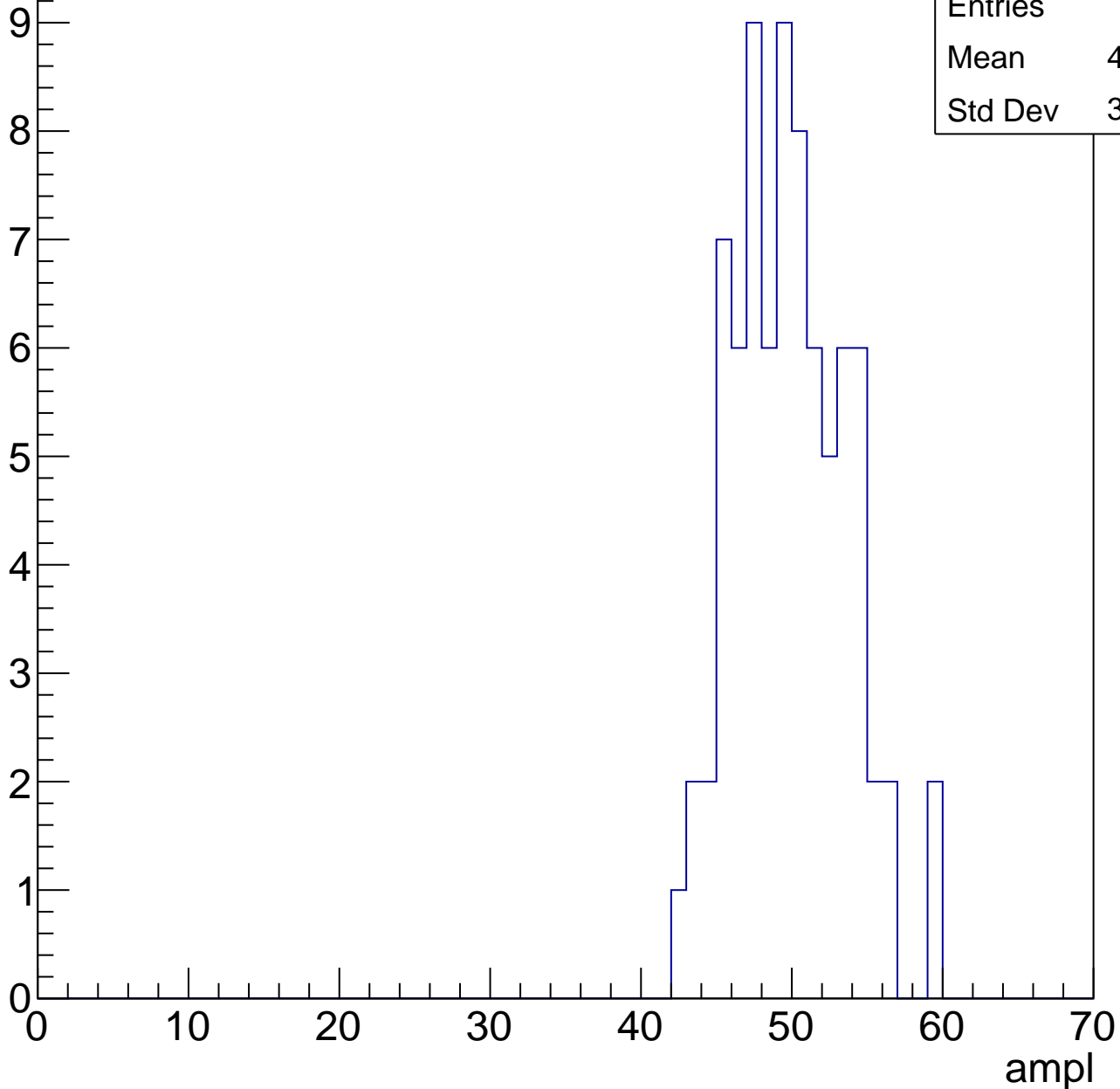


# B1L003S, U23-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	49.46
Std Dev	3.655

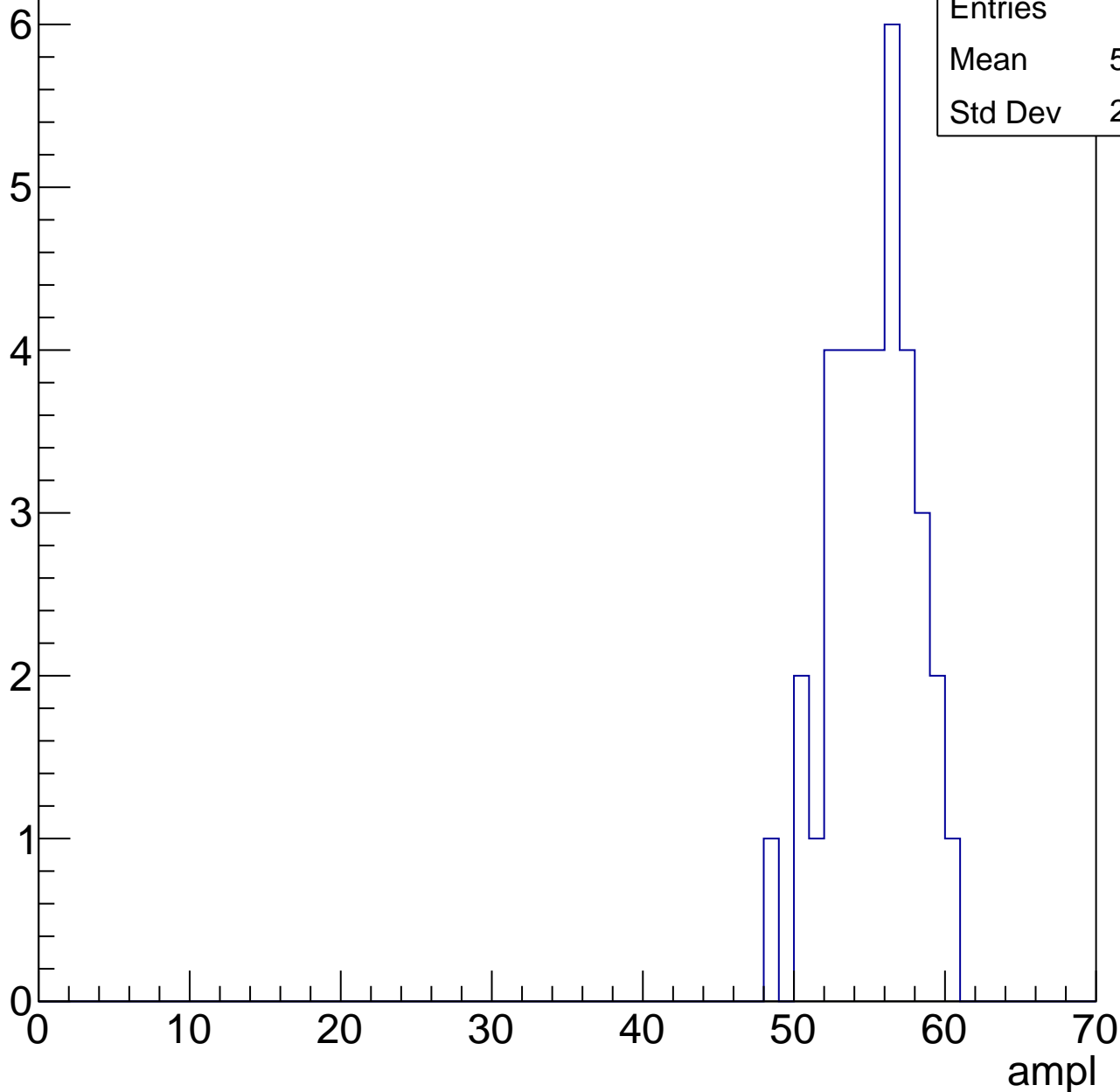


# B1L003S, U23-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	36
Mean	54.75
Std Dev	2.763

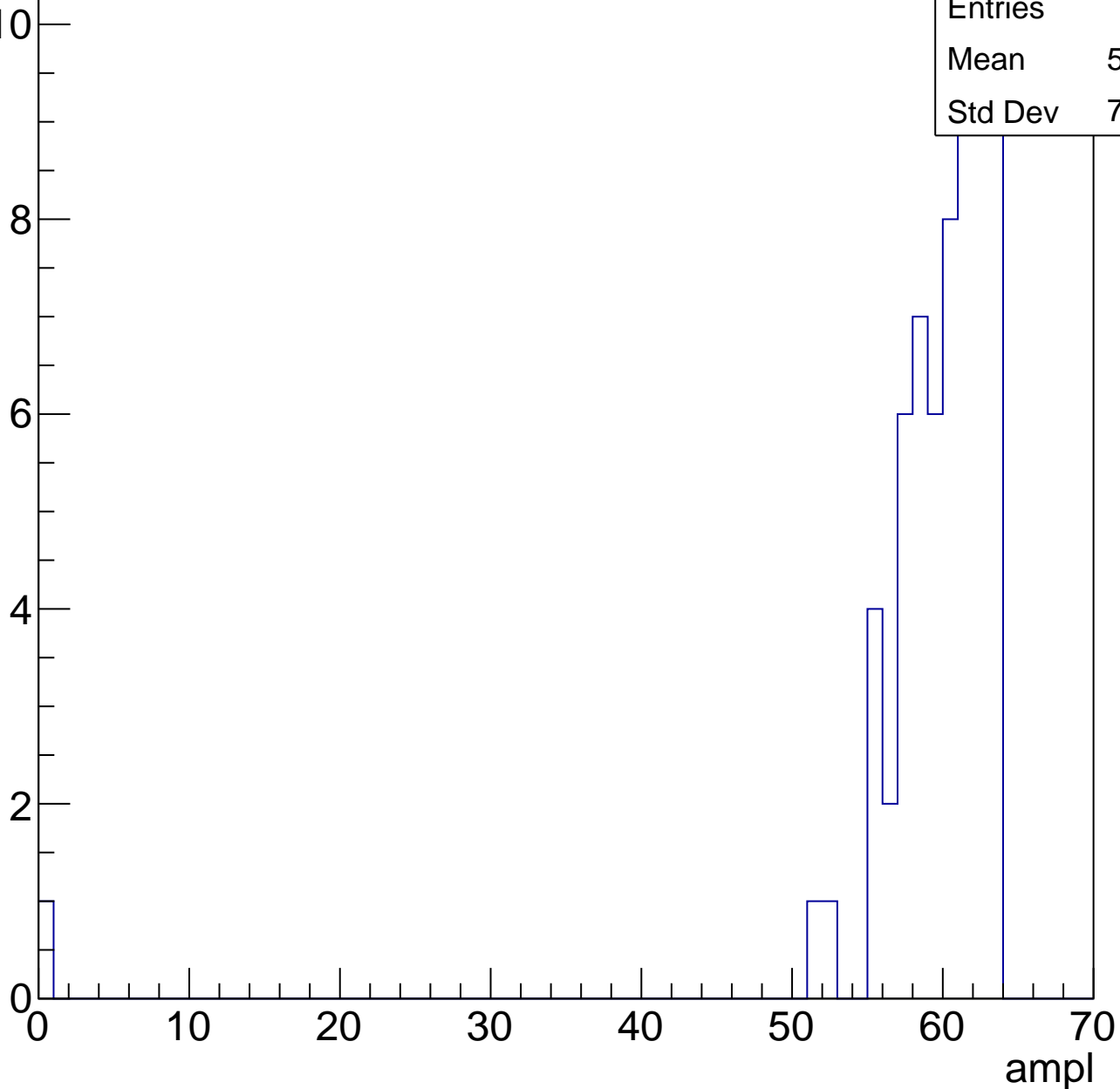


# B1L003S, U23-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	58.62
Std Dev	7.877



# B1L003S, U23-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

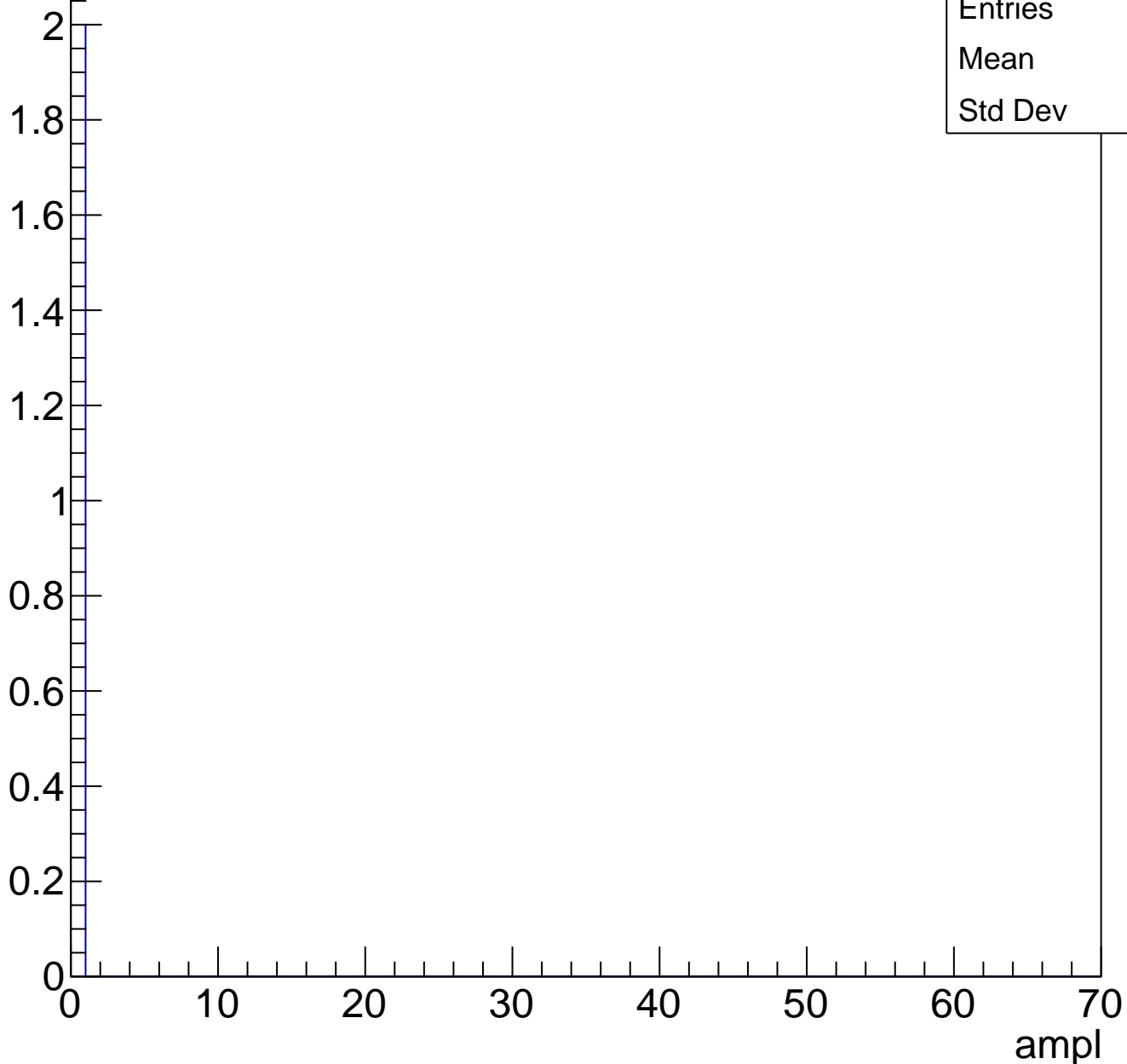




# B1L003S, U23-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch94, adc0

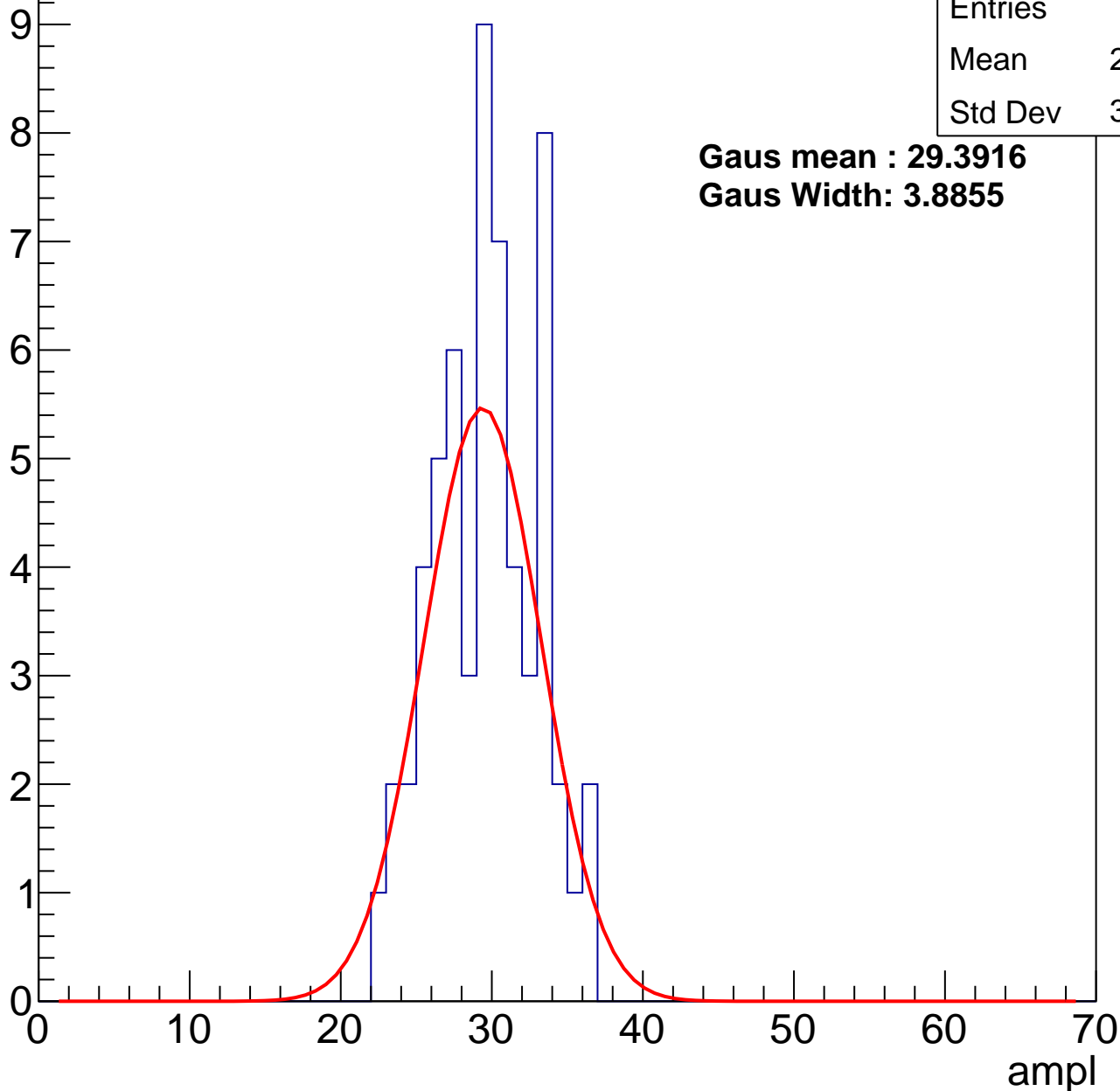
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.19
Std Dev	3.362

**Gaus mean : 29.3916**

**Gaus Width: 3.8855**



# B1L003S, U23-ch94, adc1

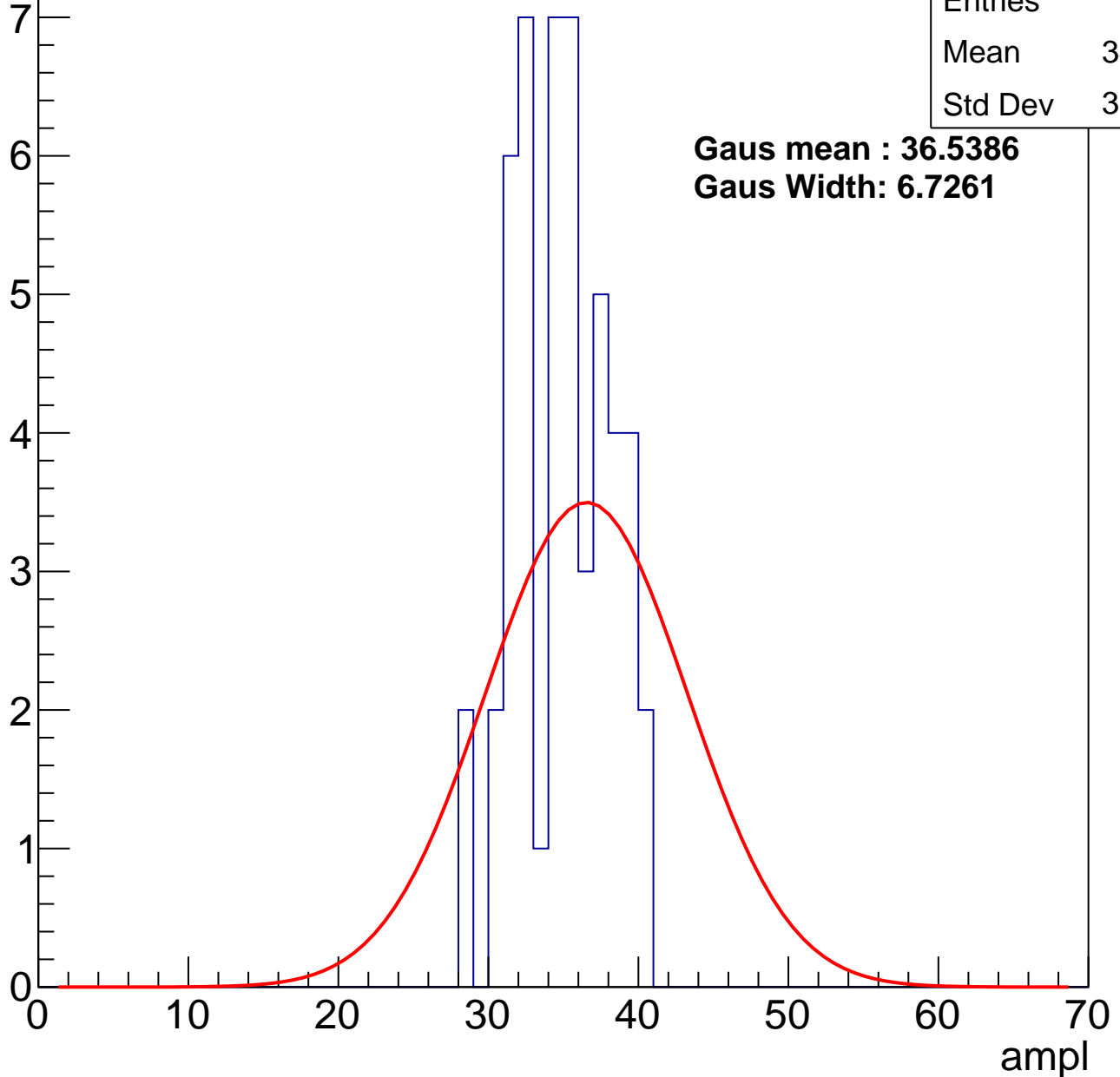
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	34.46
Std Dev	3.087

**Gaus mean : 36.5386**

**Gaus Width: 6.7261**



# B1L003S, U23-ch94, adc2

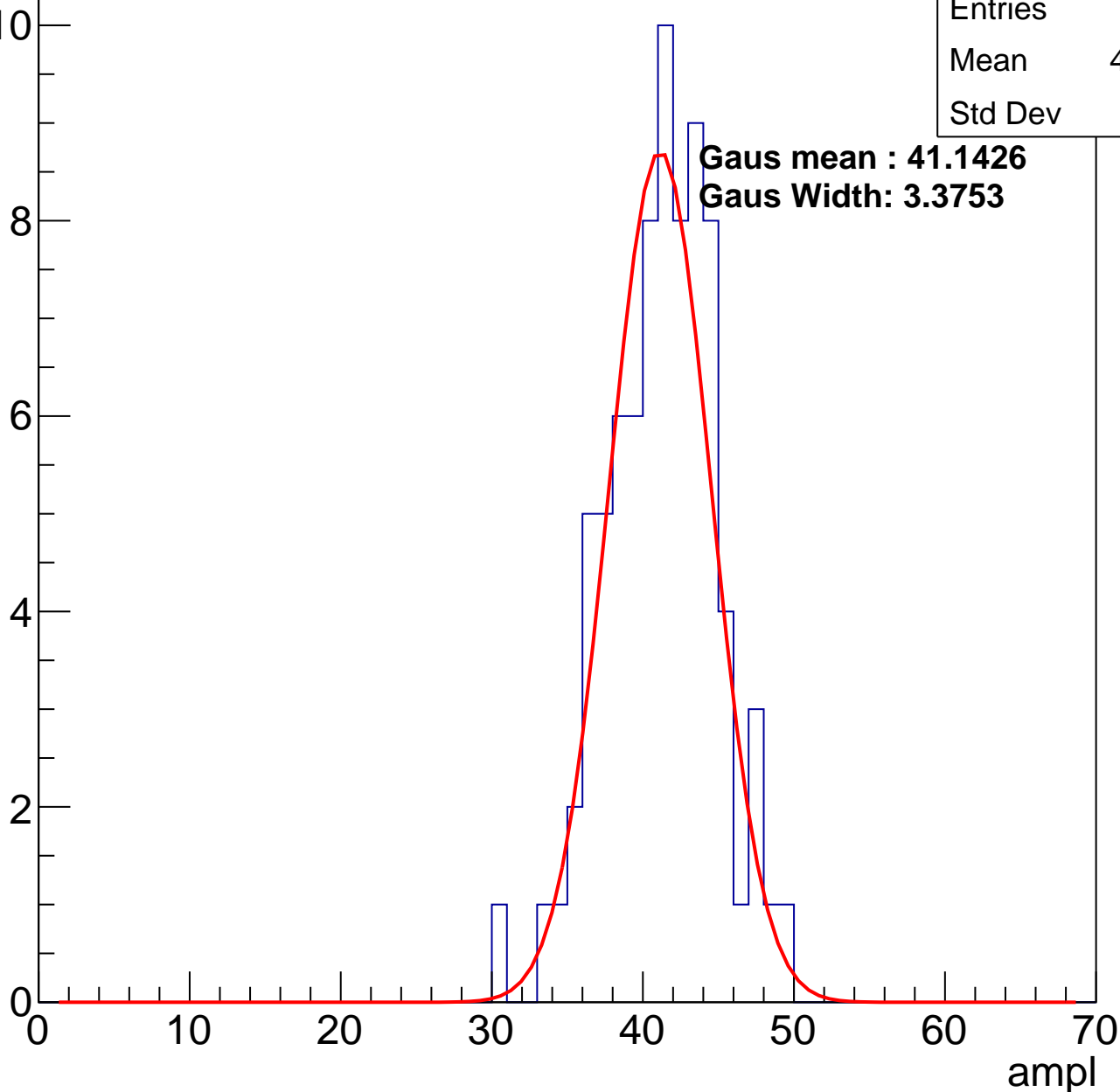
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	40.79
Std Dev	3.57

**Gaus mean : 41.1426**

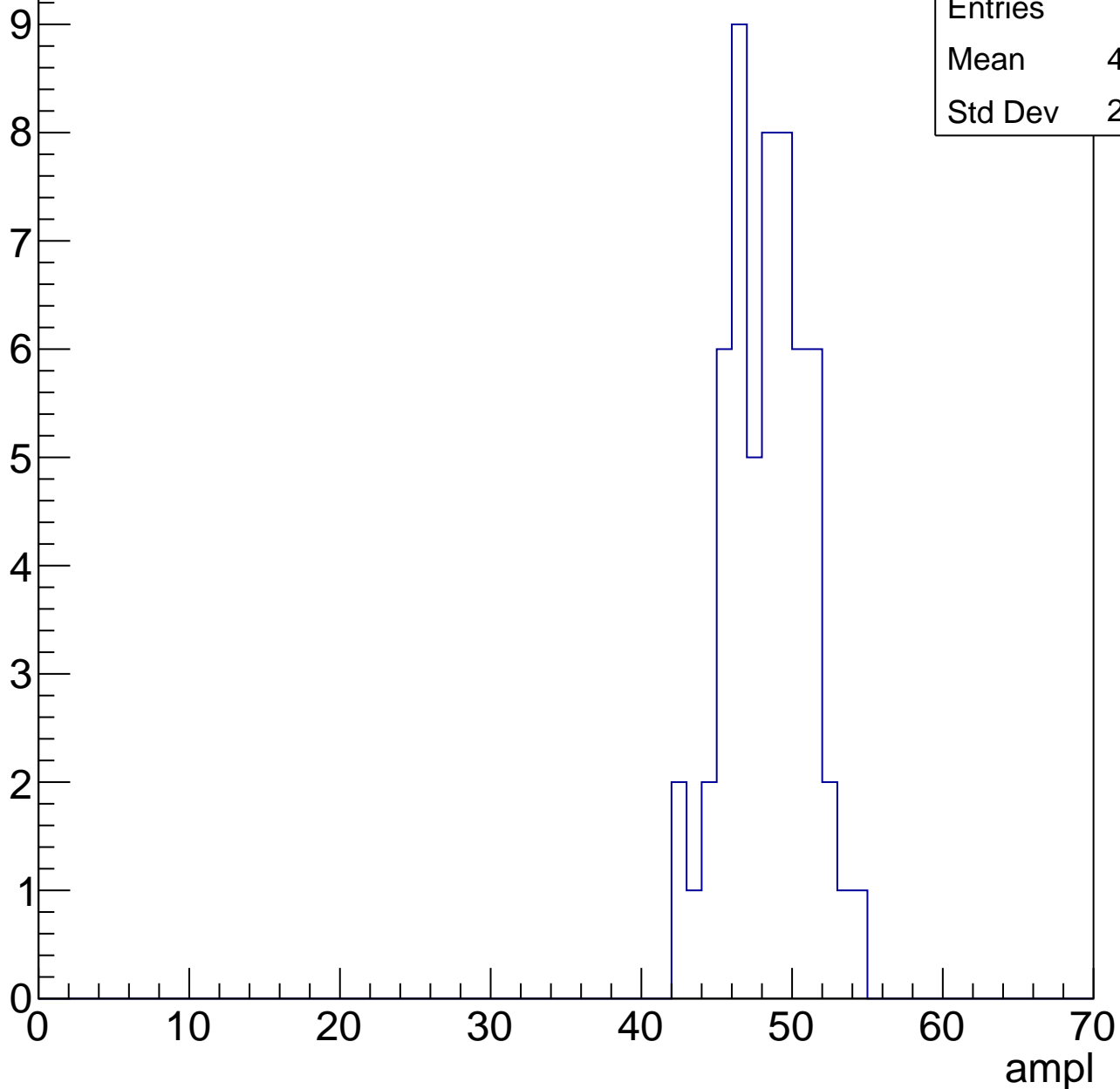
**Gaus Width: 3.3753**



# B1L003S, U23-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

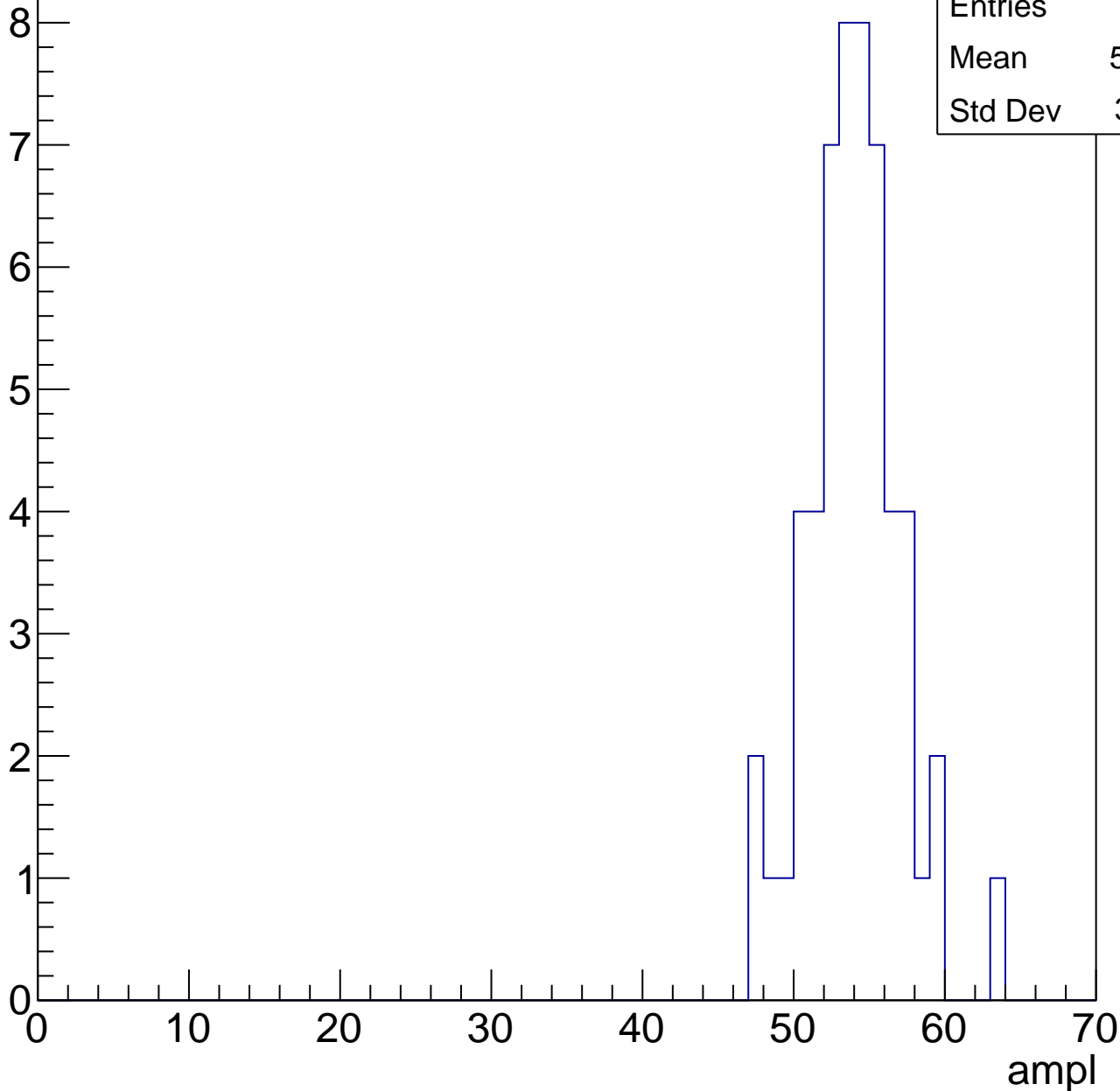


# B1L003S, U23-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

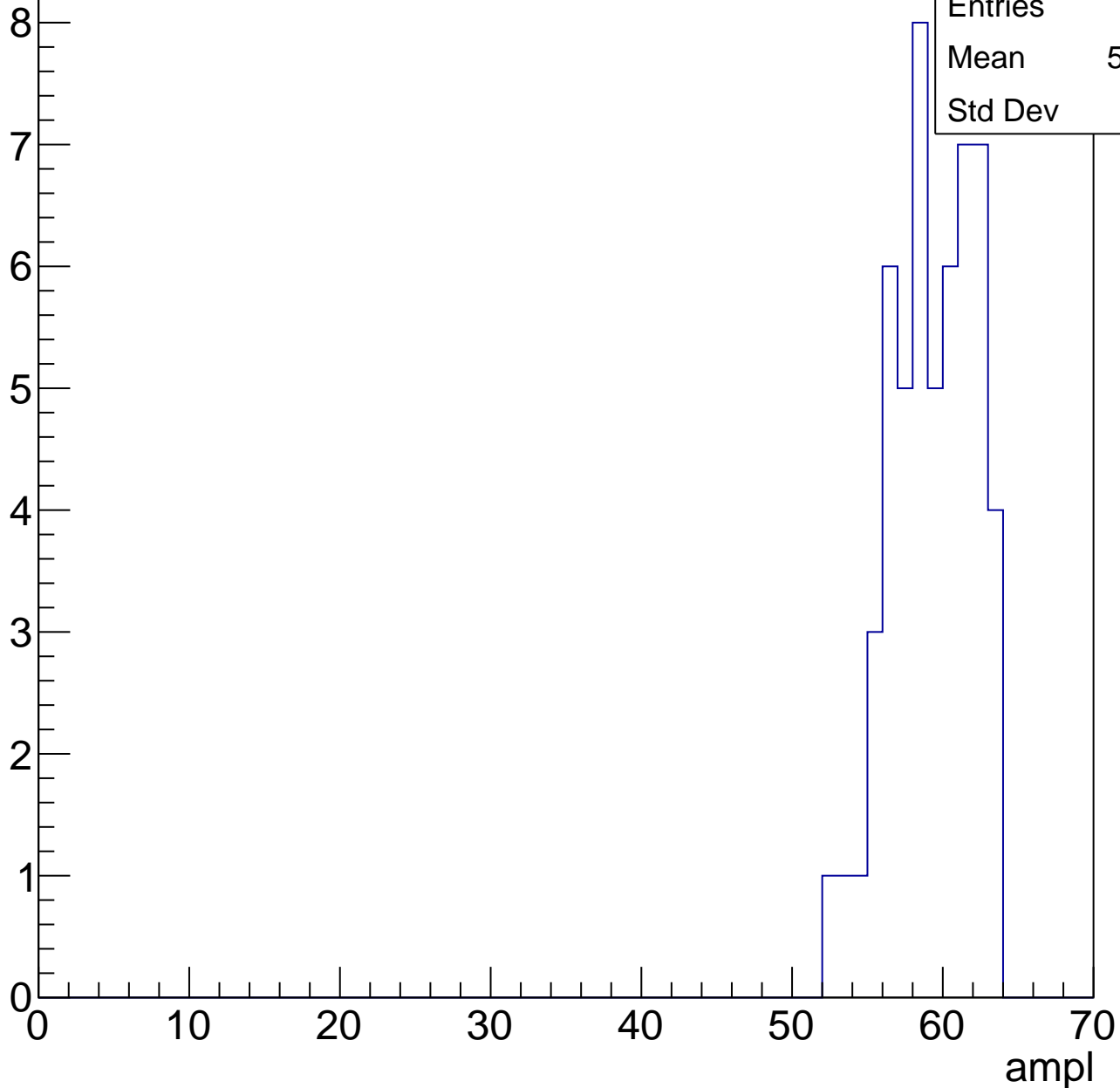
Entries	54
Mean	53.54
Std Dev	3.011



# B1L003S, U23-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

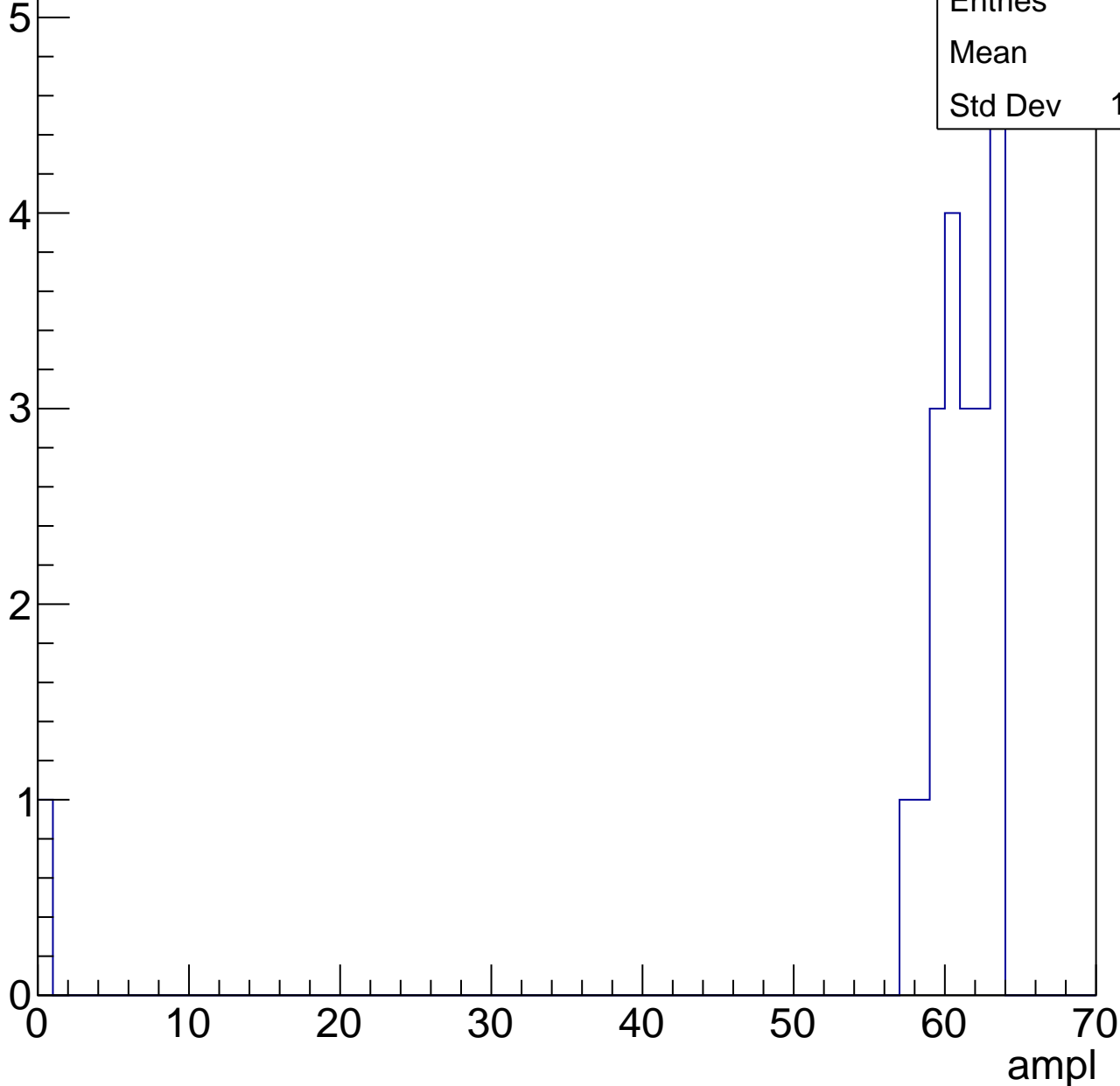


# B1L003S, U23-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	21
Mean	57.9
Std Dev	13.06





# B1L003S, U23-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U23-ch95, adc0

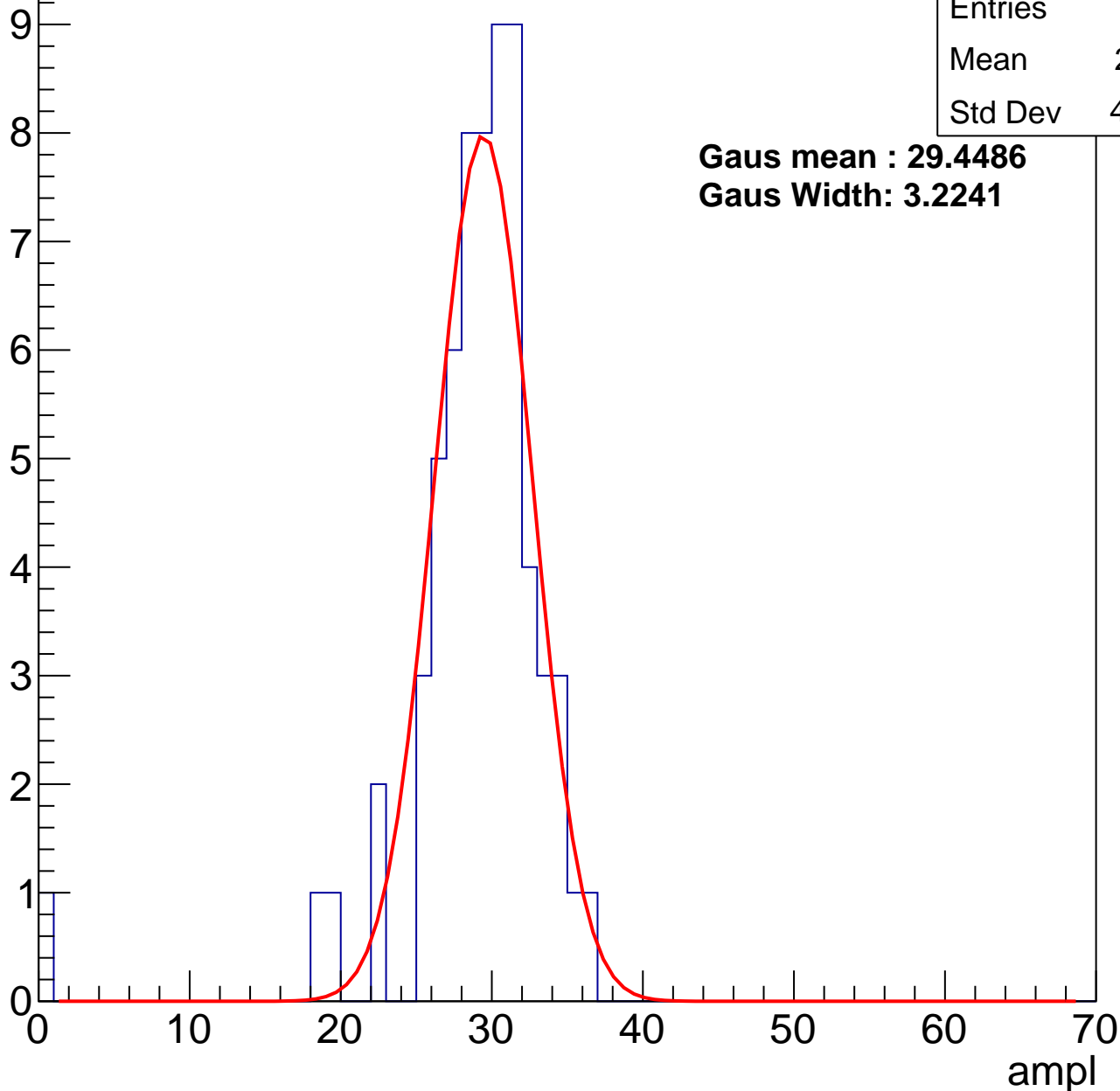
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.51
Std Dev	4.896

**Gaus mean : 29.4486**

**Gaus Width: 3.2241**



# B1L003S, U23-ch95, adc1

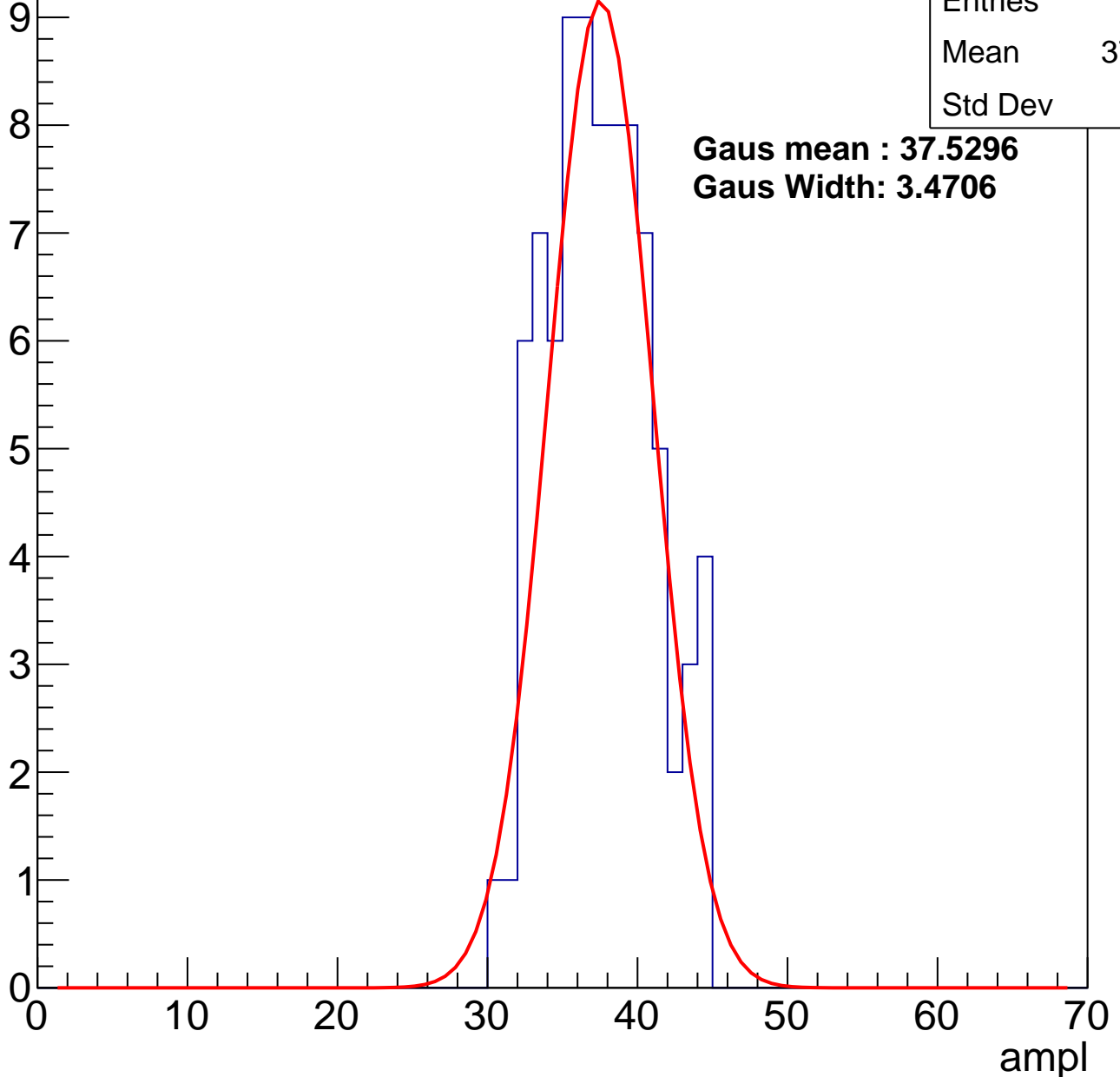
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	84
Mean	37.06
Std Dev	3.42

**Gaus mean : 37.5296**

**Gaus Width: 3.4706**



# B1L003S, U23-ch95, adc2

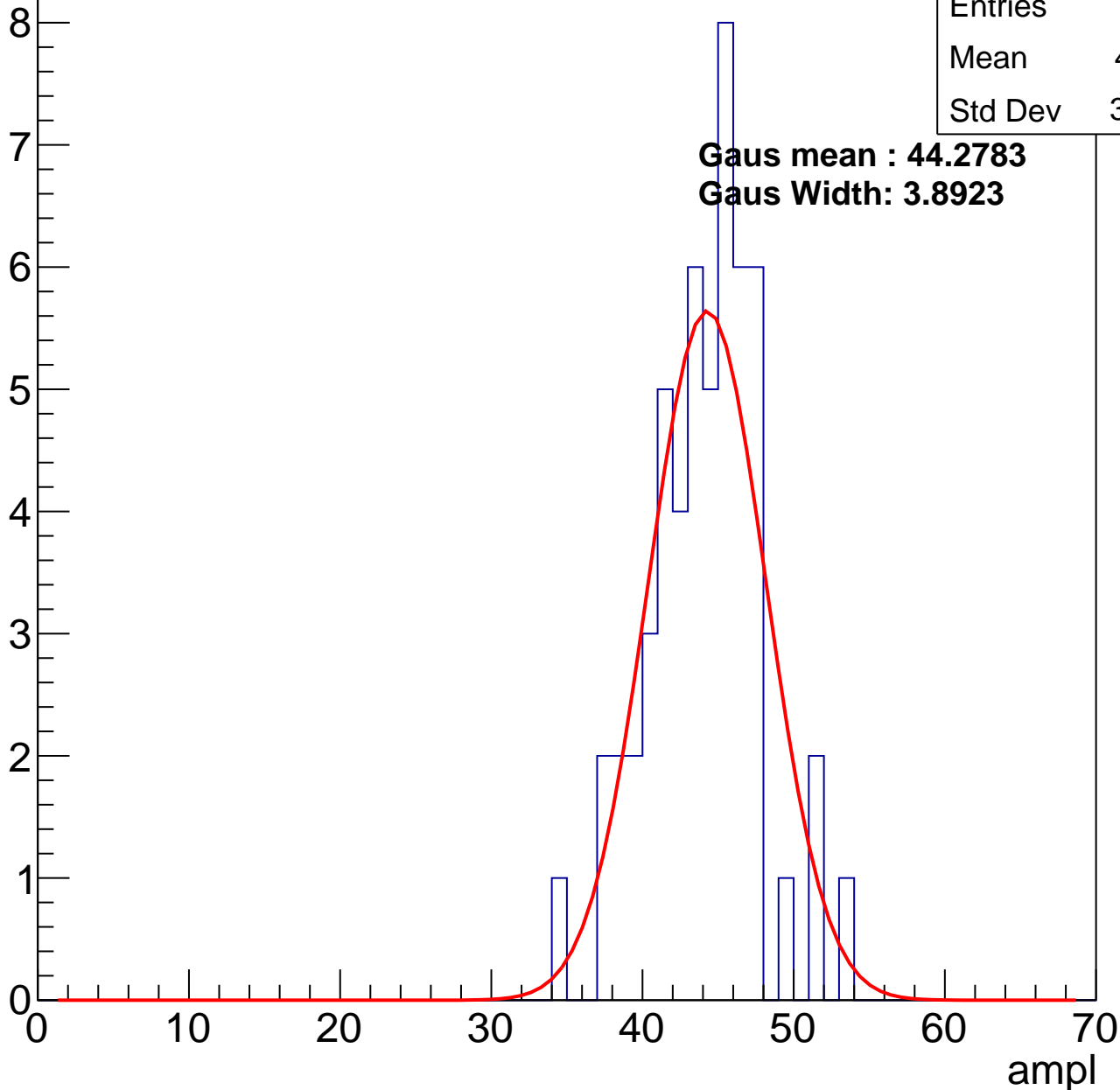
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.61
Std Dev	3.643

**Gaus mean : 44.2783**

**Gaus Width: 3.8923**



# B1L003S, U23-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

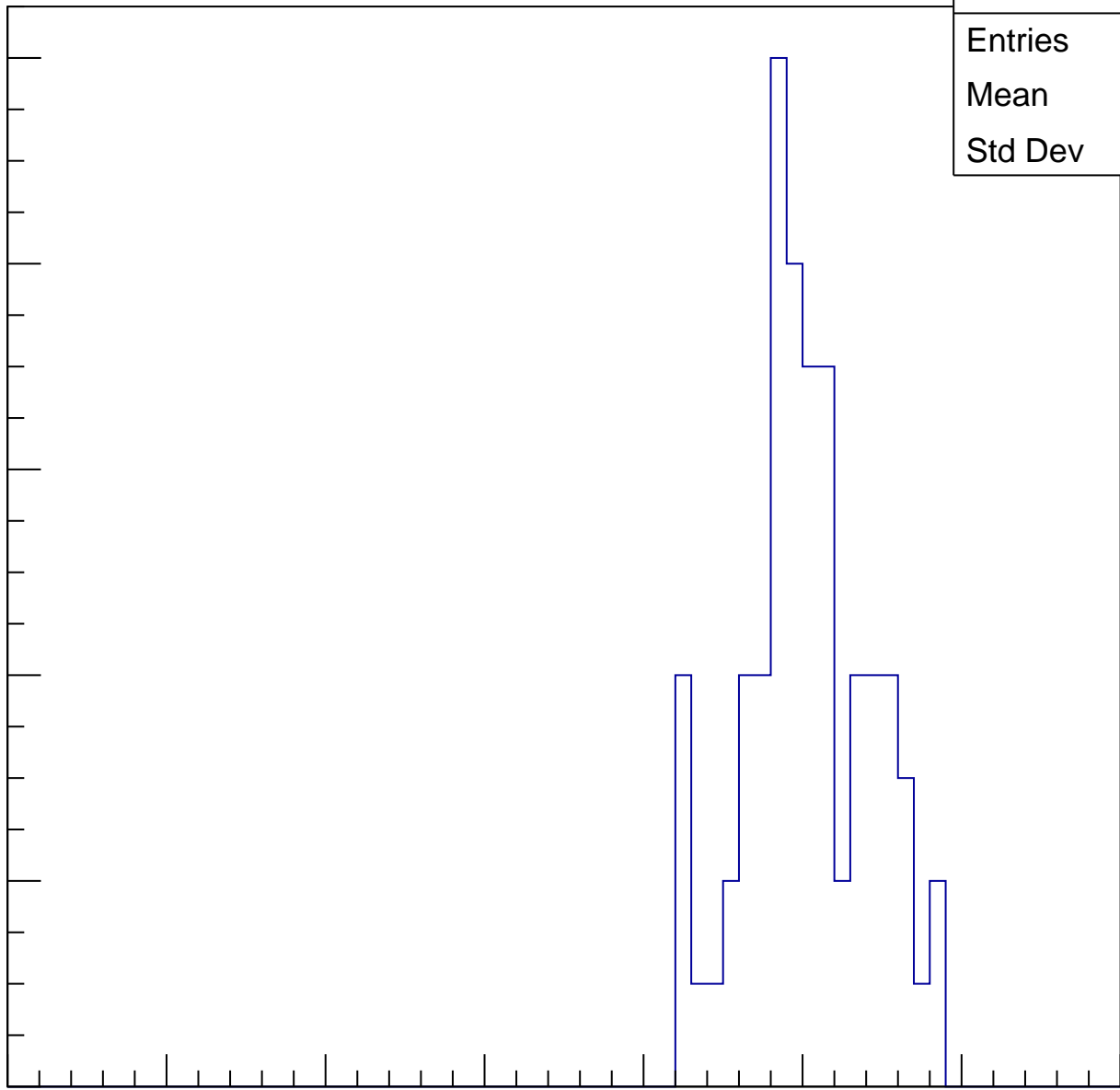
Entries	68
Mean	49.84
Std Dev	3.906

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

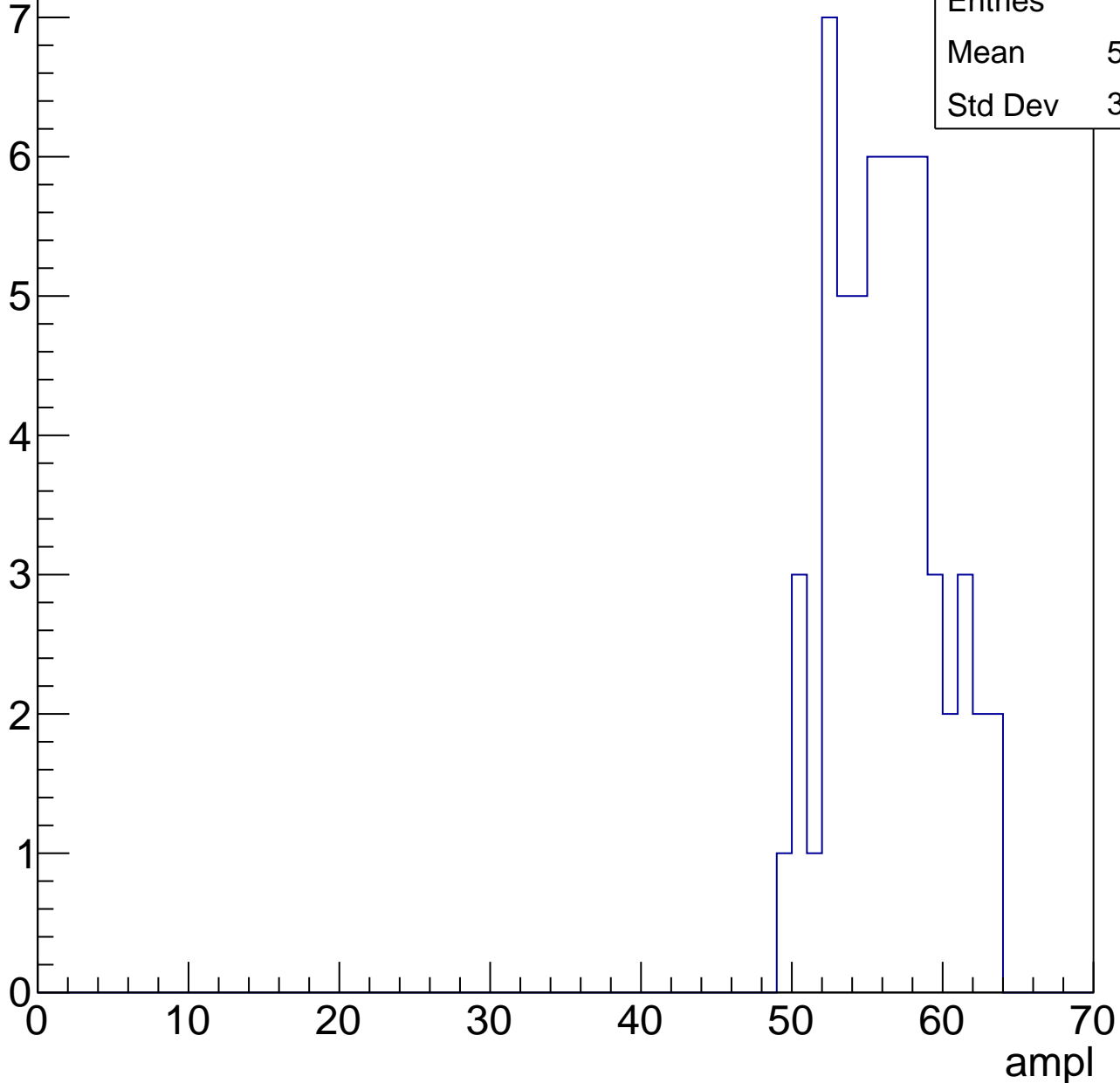


# B1L003S, U23-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.78
Std Dev	3.464



# B1L003S, U23-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	58.52
Std Dev	9.695

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

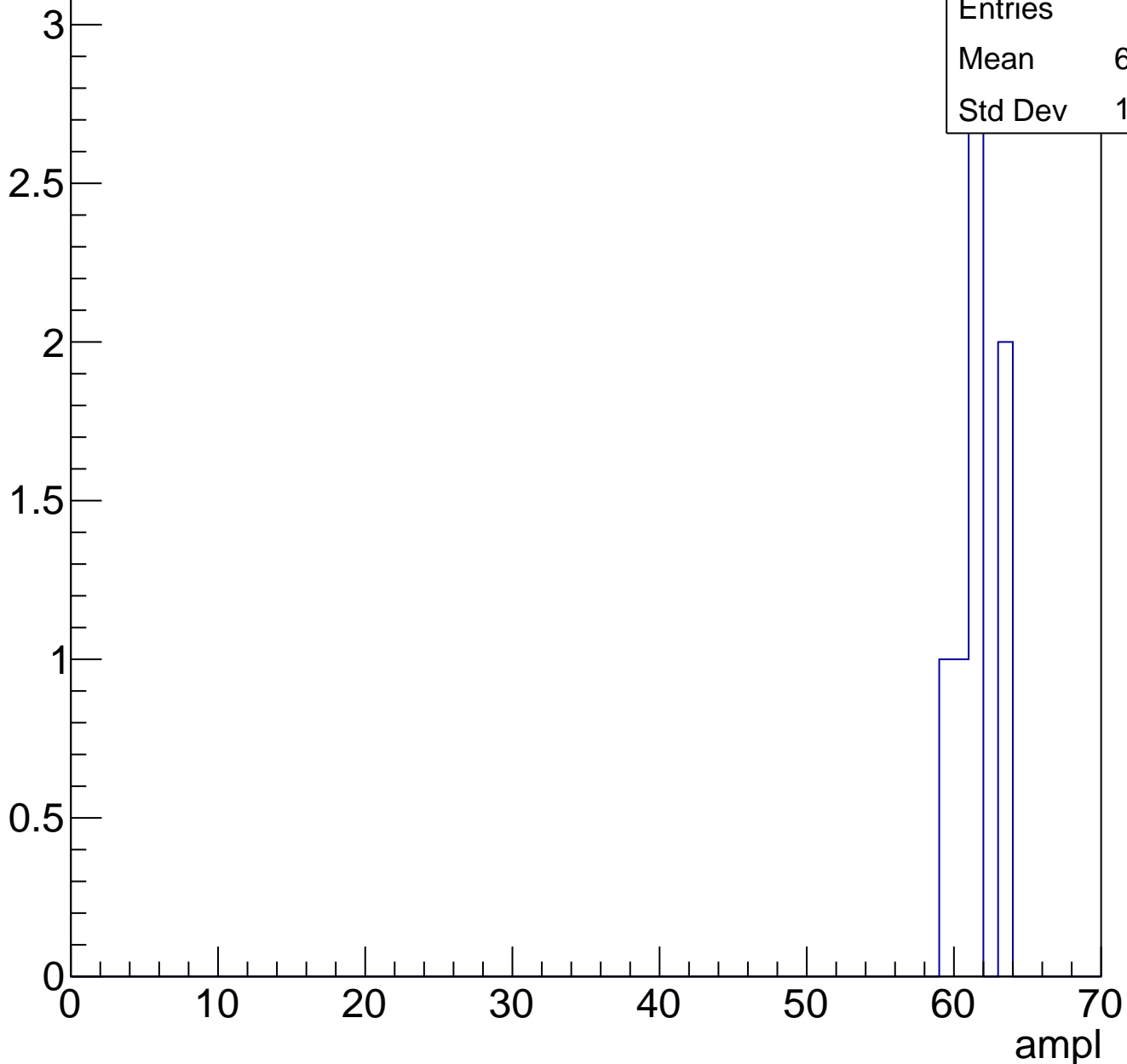
7

8

# B1L003S, U23-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	7
Mean	61.14
Std Dev	1.355



# B1L003S, U23-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch96, adc0

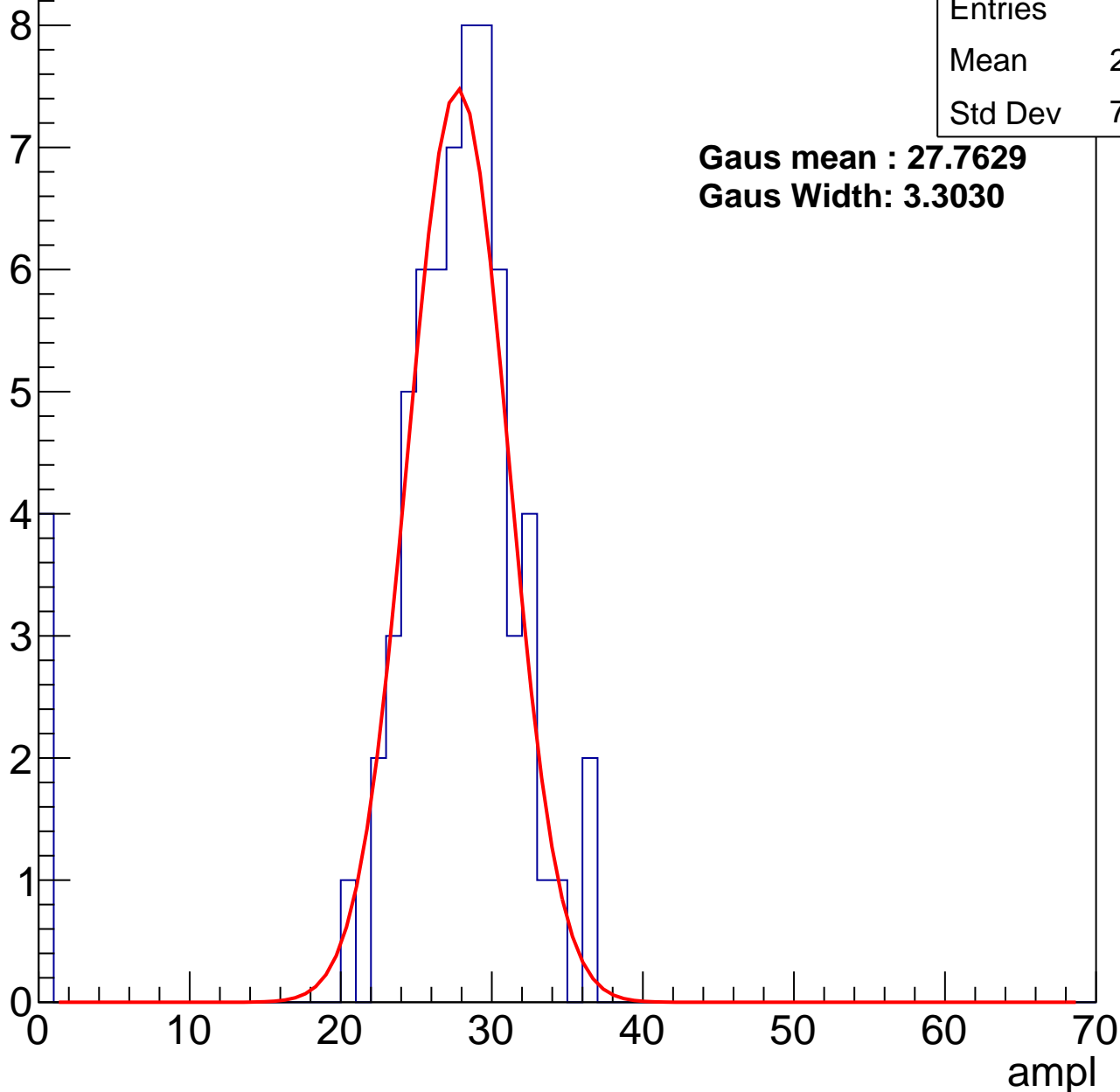
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	26.03
Std Dev	7.293

**Gaus mean : 27.7629**

**Gaus Width: 3.3030**



# B1L003S, U23-ch96, adc1

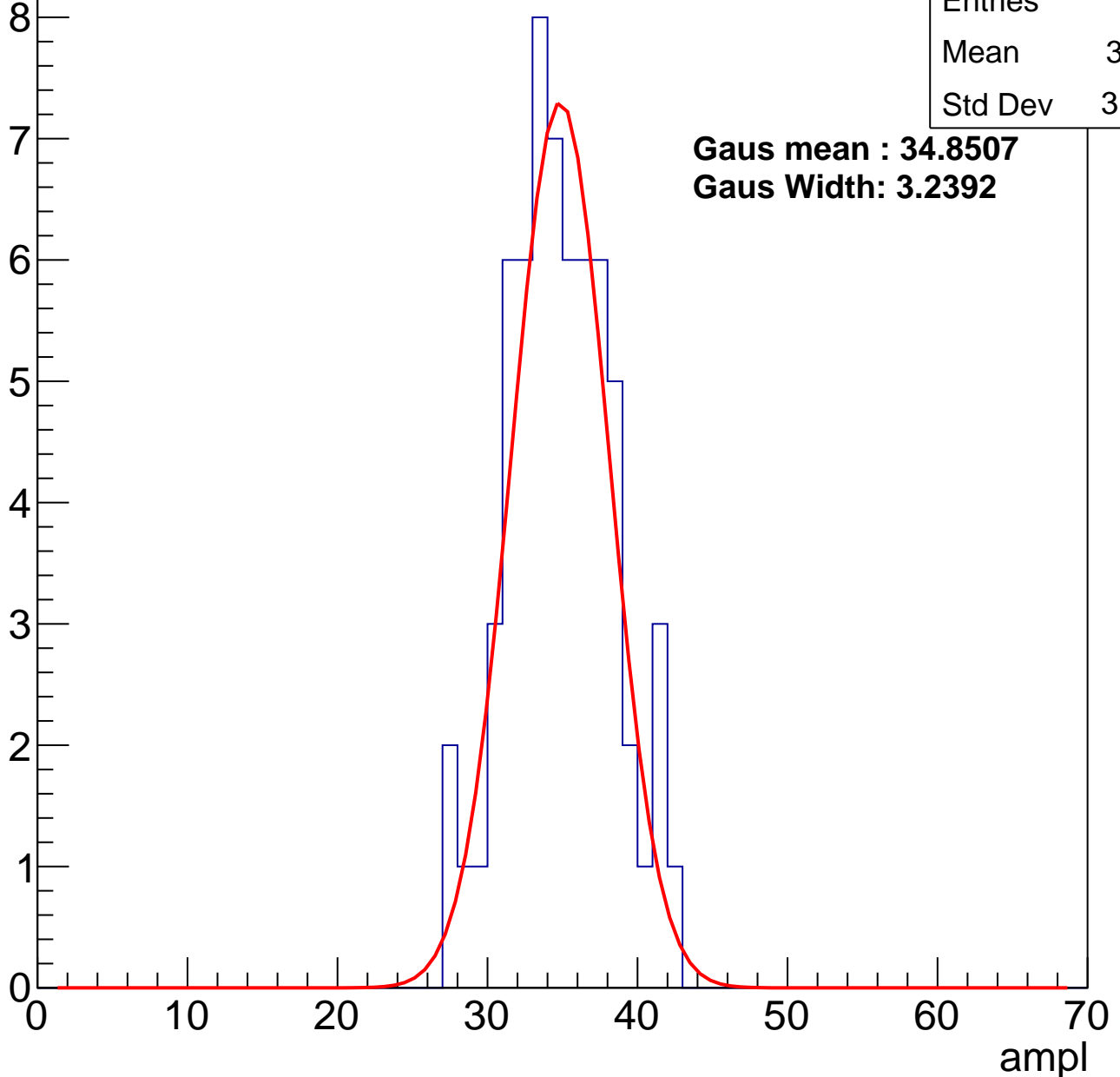
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	34.41
Std Dev	3.408

**Gaus mean : 34.8507**

**Gaus Width: 3.2392**



# B1L003S, U23-ch96, adc2

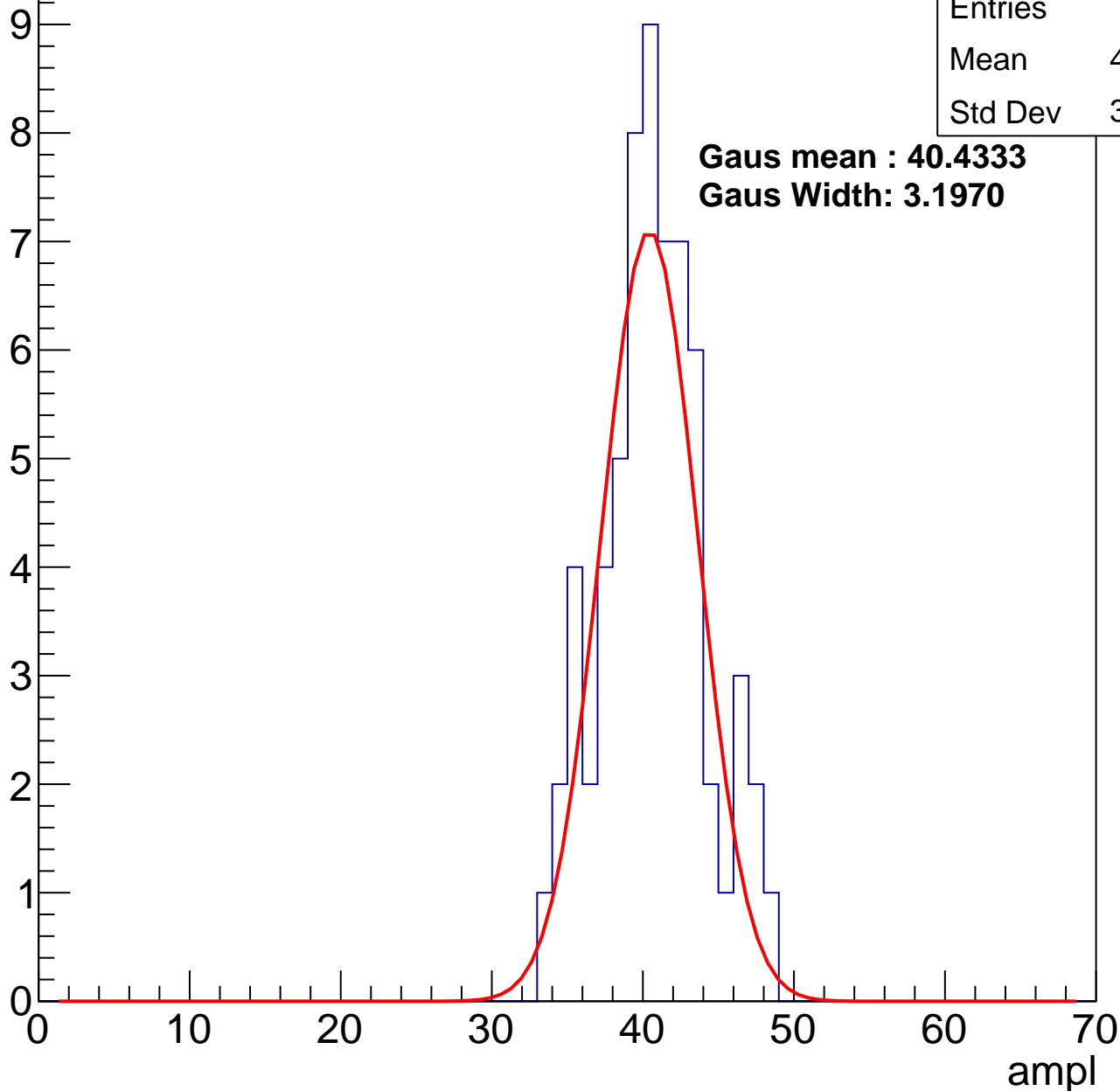
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	40.23
Std Dev	3.376

**Gaus mean : 40.4333**

**Gaus Width: 3.1970**

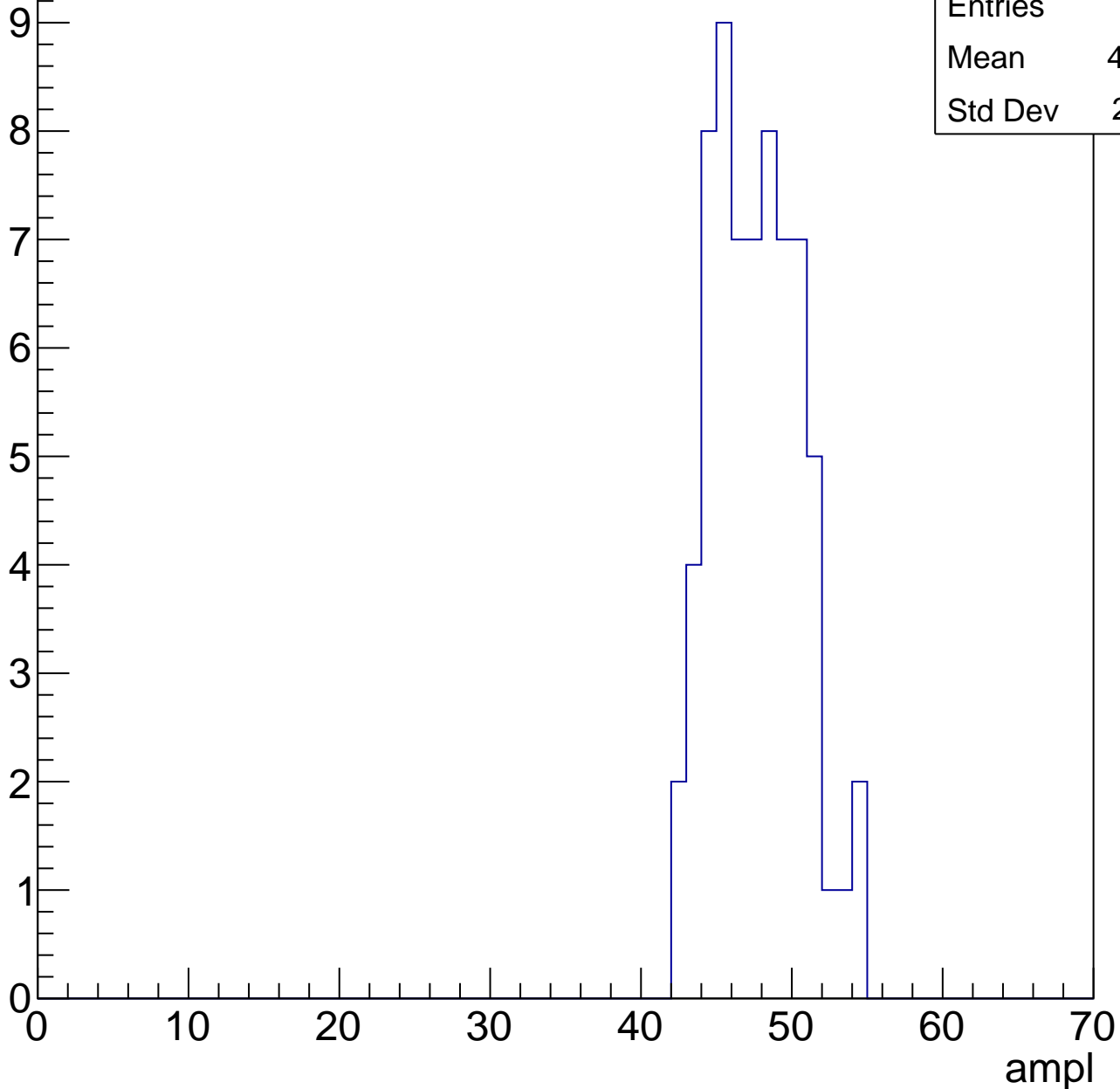


# B1L003S, U23-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	47.19
Std Dev	2.881



# B1L003S, U23-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	56
Mean	53.68
Std Dev	3.03

Entry

10

8

6

4

2

0

0

10

20

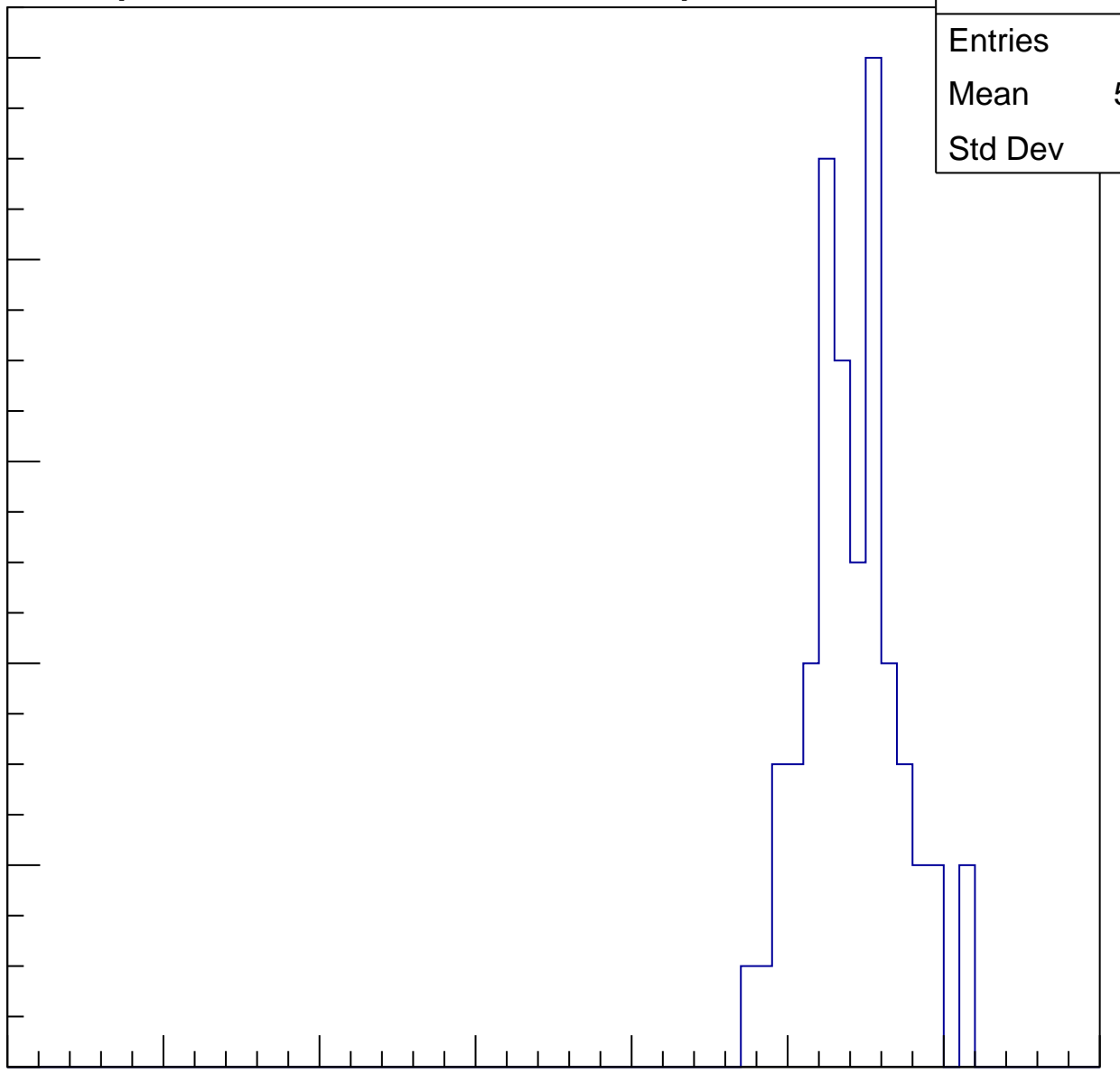
30

40

50

60

ampl



# B1L003S, U23-ch96, adc5

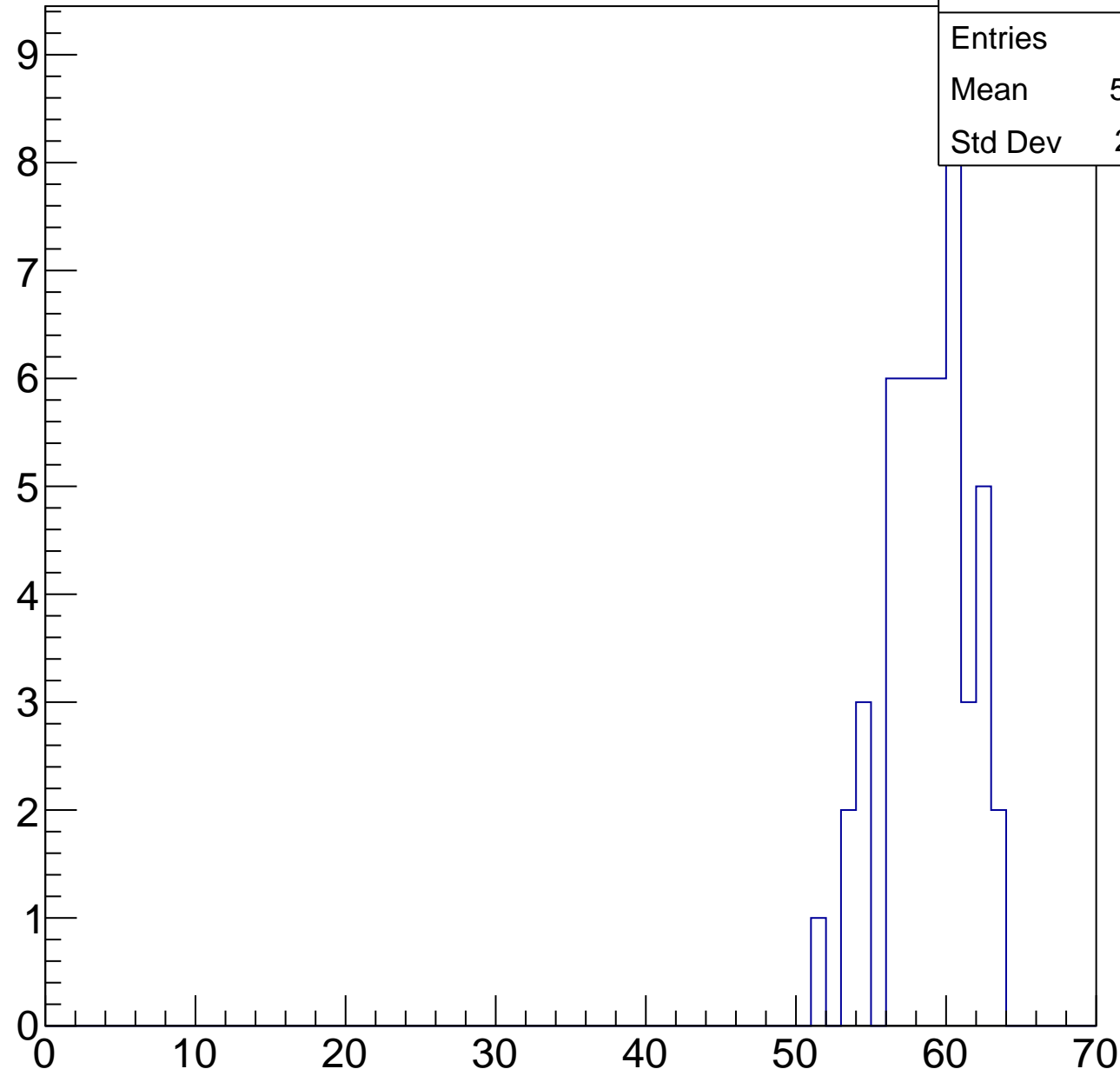
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.33
Std Dev	2.751

ampl

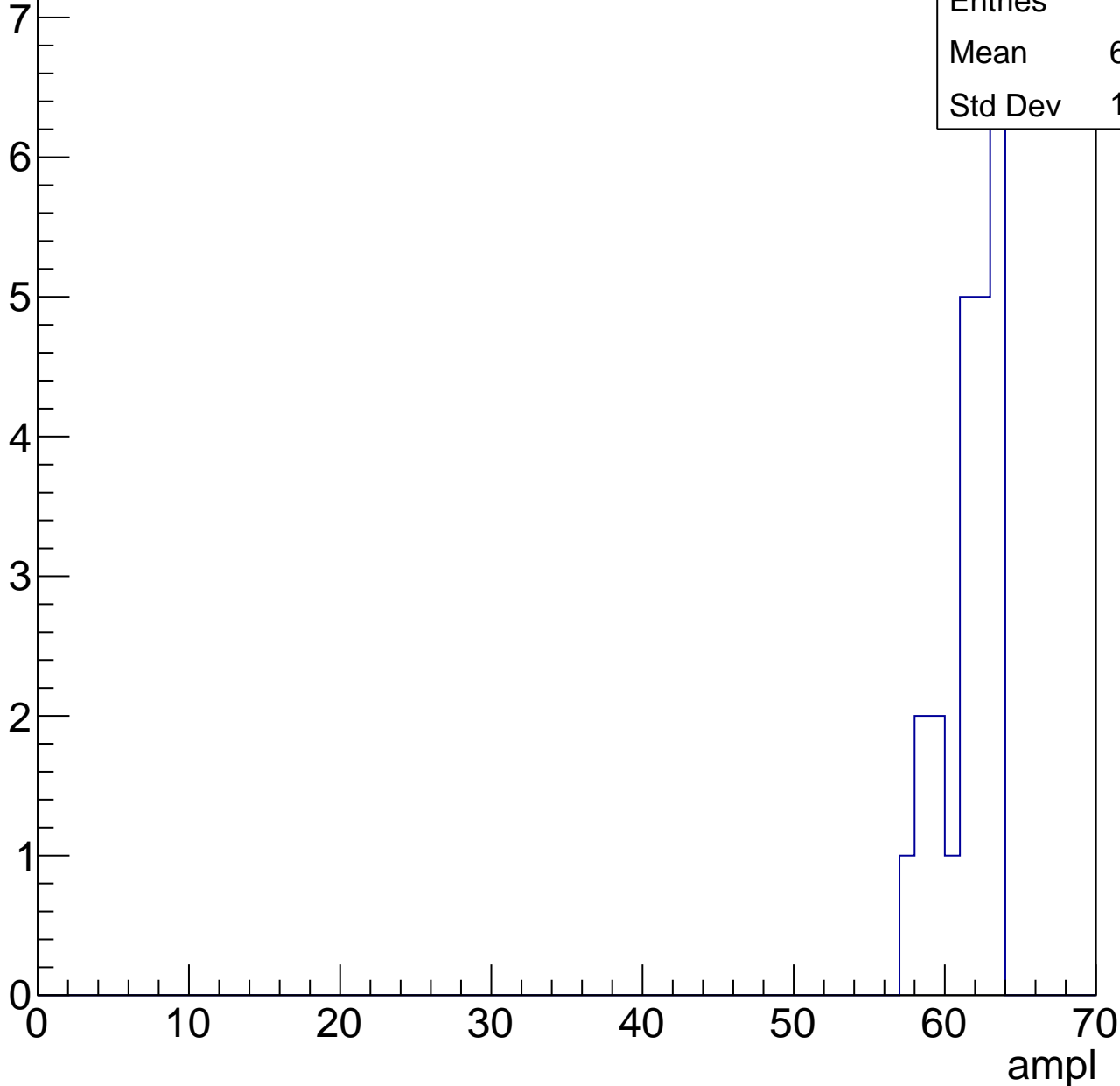


# B1L003S, U23-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	23
Mean	61.17
Std Dev	1.809





# B1L003S, U23-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	79
Mean	30.95
Std Dev	3.515

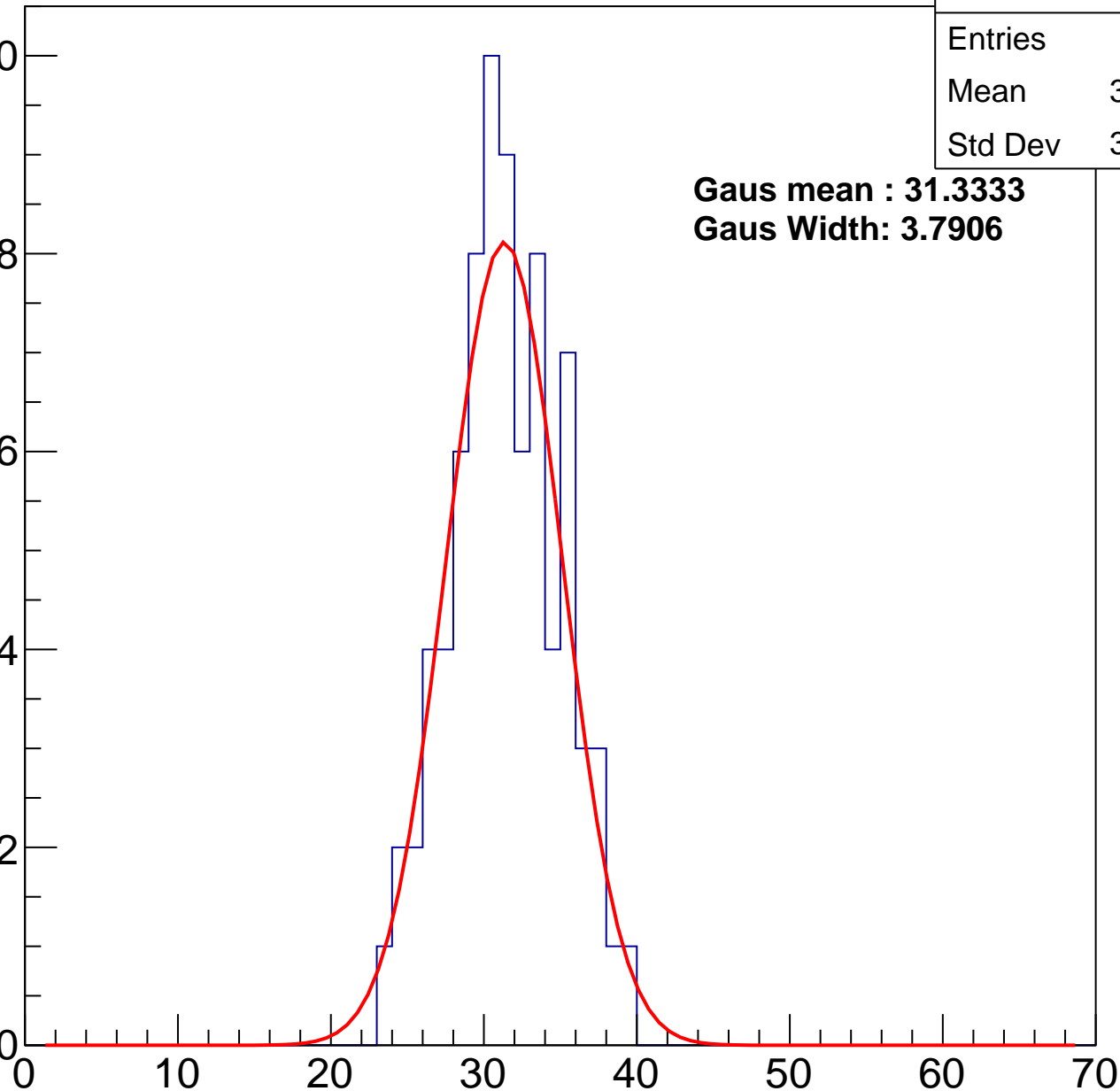
**Gaus mean : 31.3333**

**Gaus Width: 3.7906**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L003S, U23-ch97, adc1

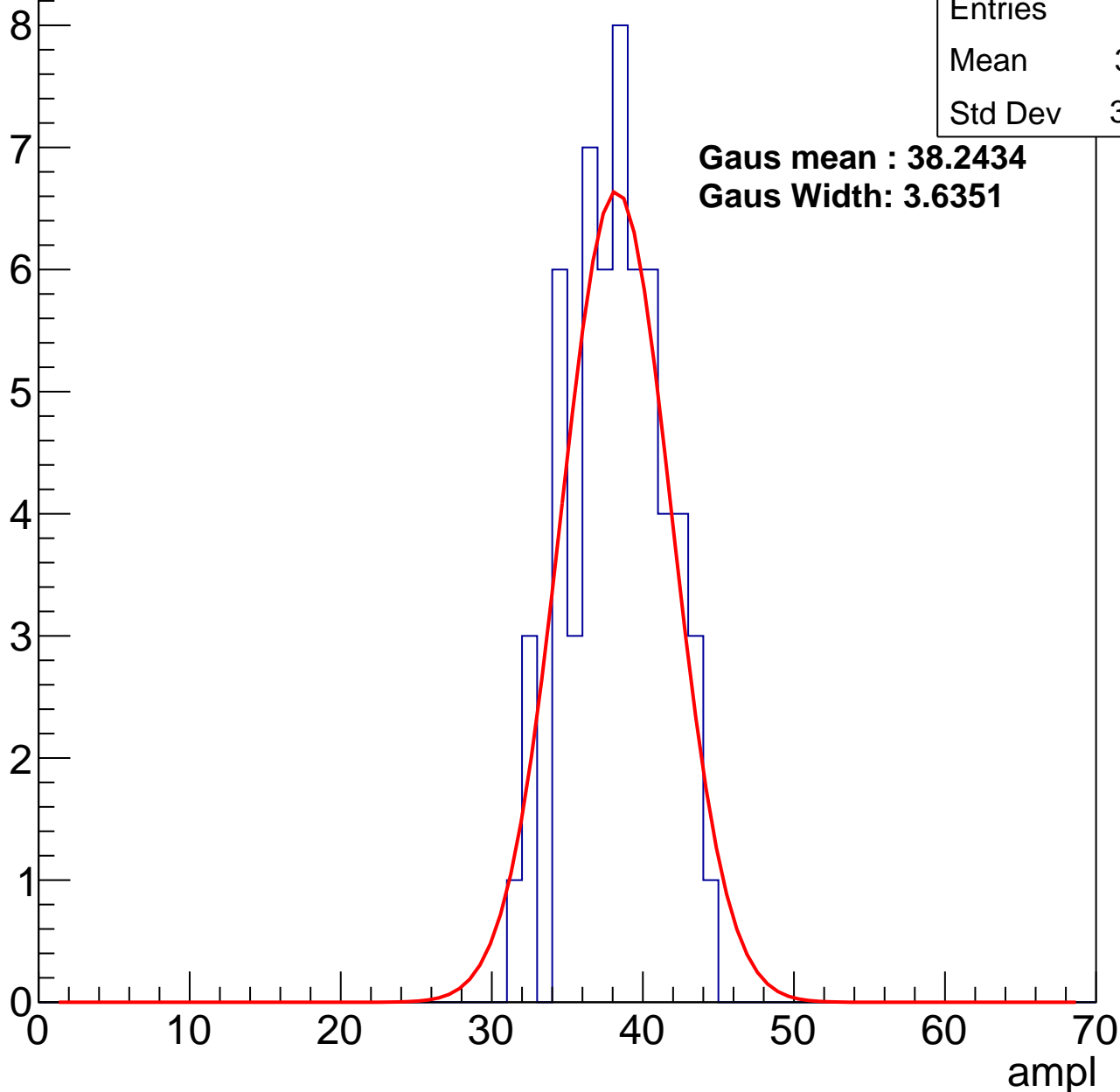
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	37.81
Std Dev	3.088

**Gaus mean : 38.2434**

**Gaus Width: 3.6351**



# B1L003S, U23-ch97, adc2

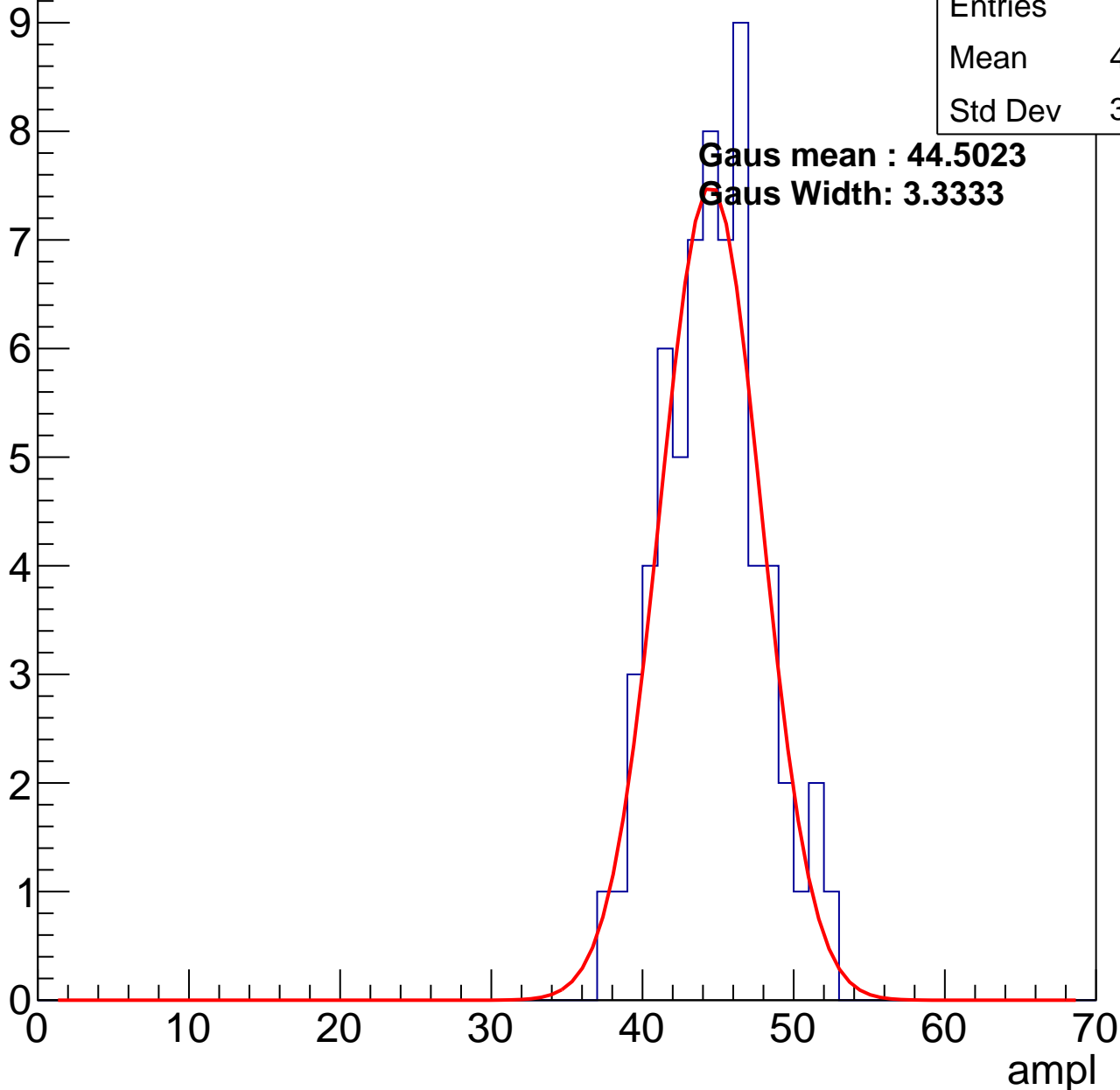
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	44.18
Std Dev	3.267

Gaus mean : 44.5023

Gaus Width: 3.3333

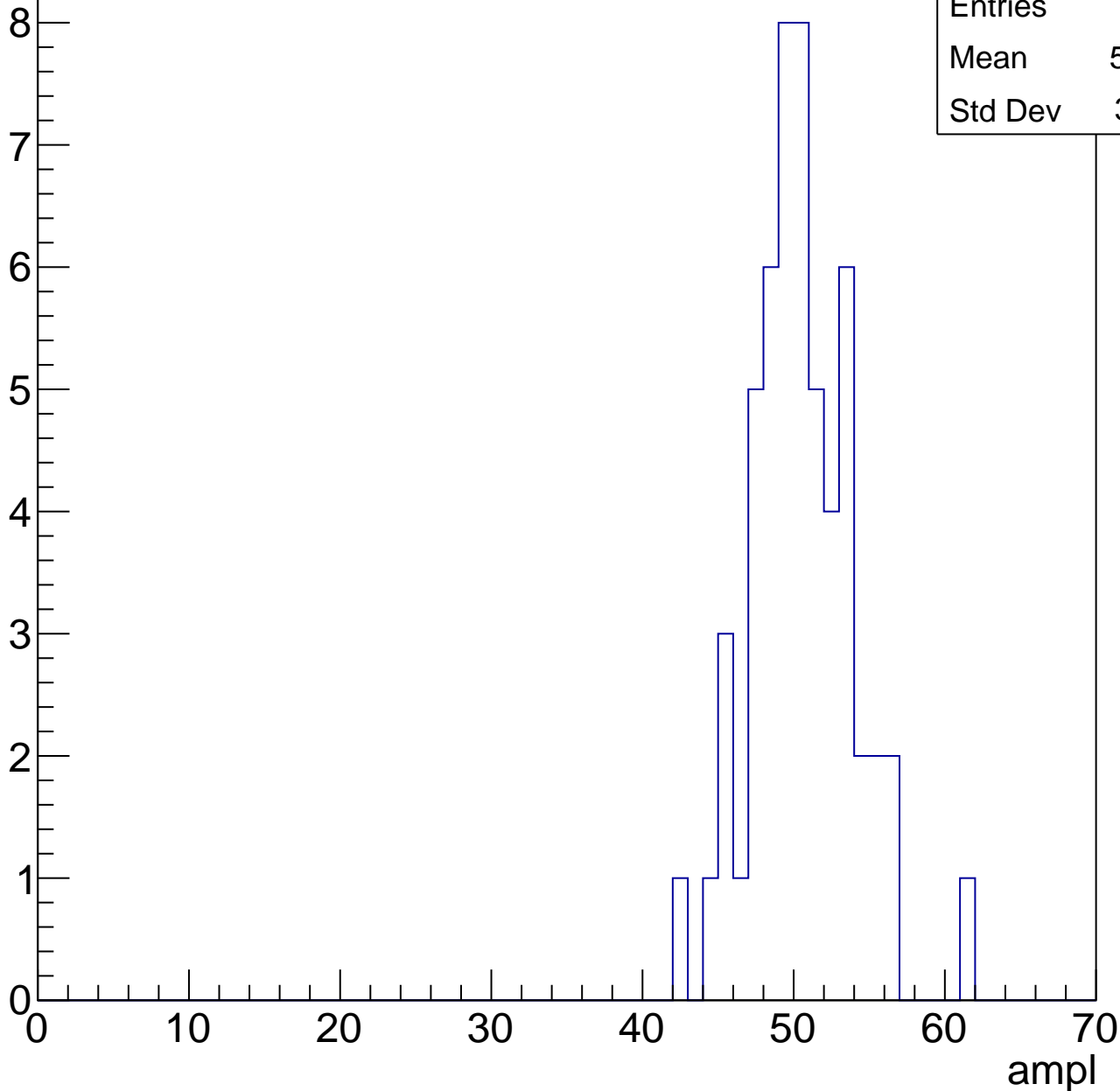


# B1L003S, U23-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	50.07
Std Dev	3.351



# B1L003S, U23-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	56.54
Std Dev	3.3

Entry

10

8

6

4

2

0

0

10

20

30

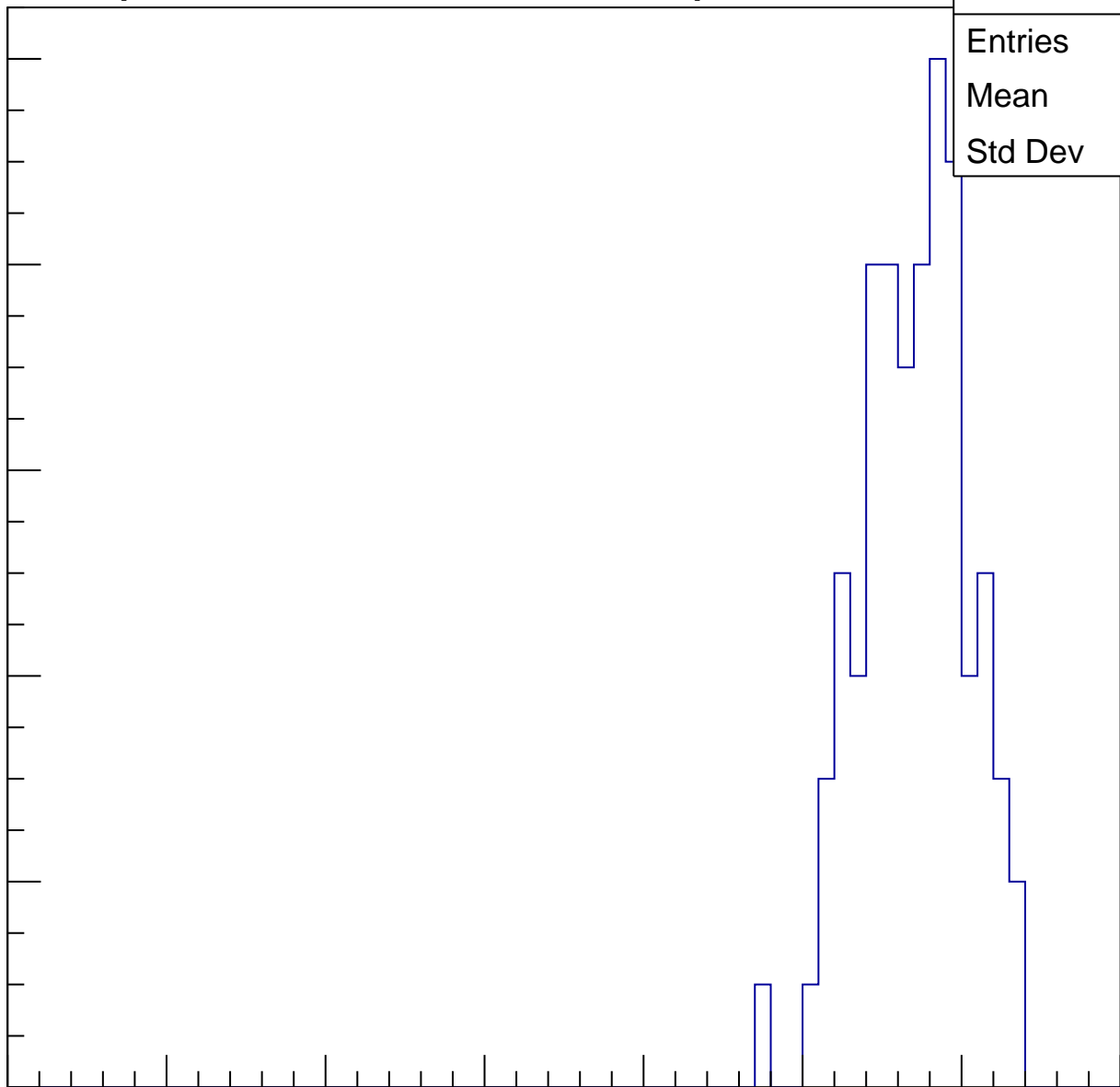
40

50

60

70

ampl

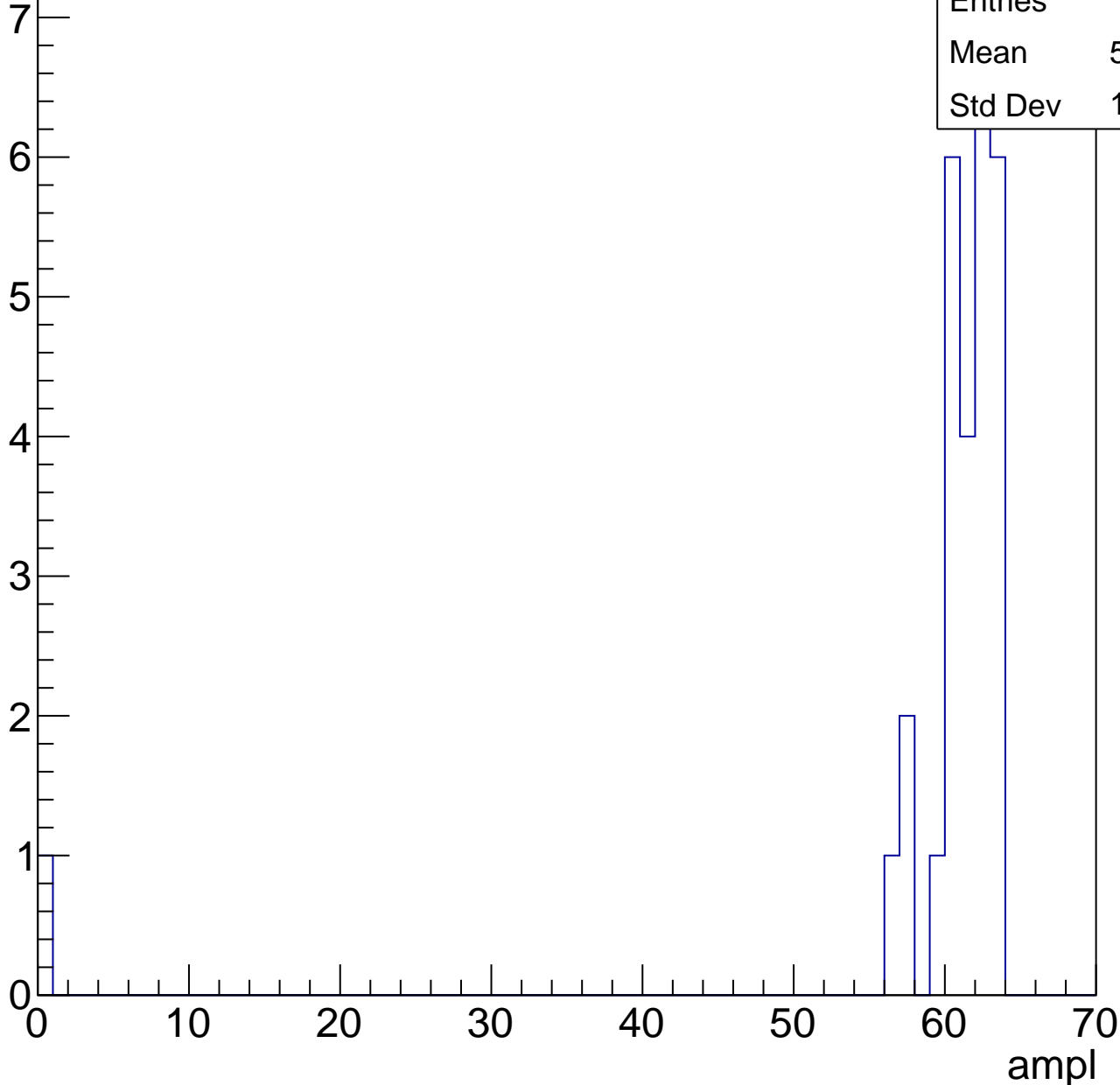


# B1L003S, U23-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	28
Mean	58.75
Std Dev	11.46



# B1L003S, U23-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U23-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch98, adc0

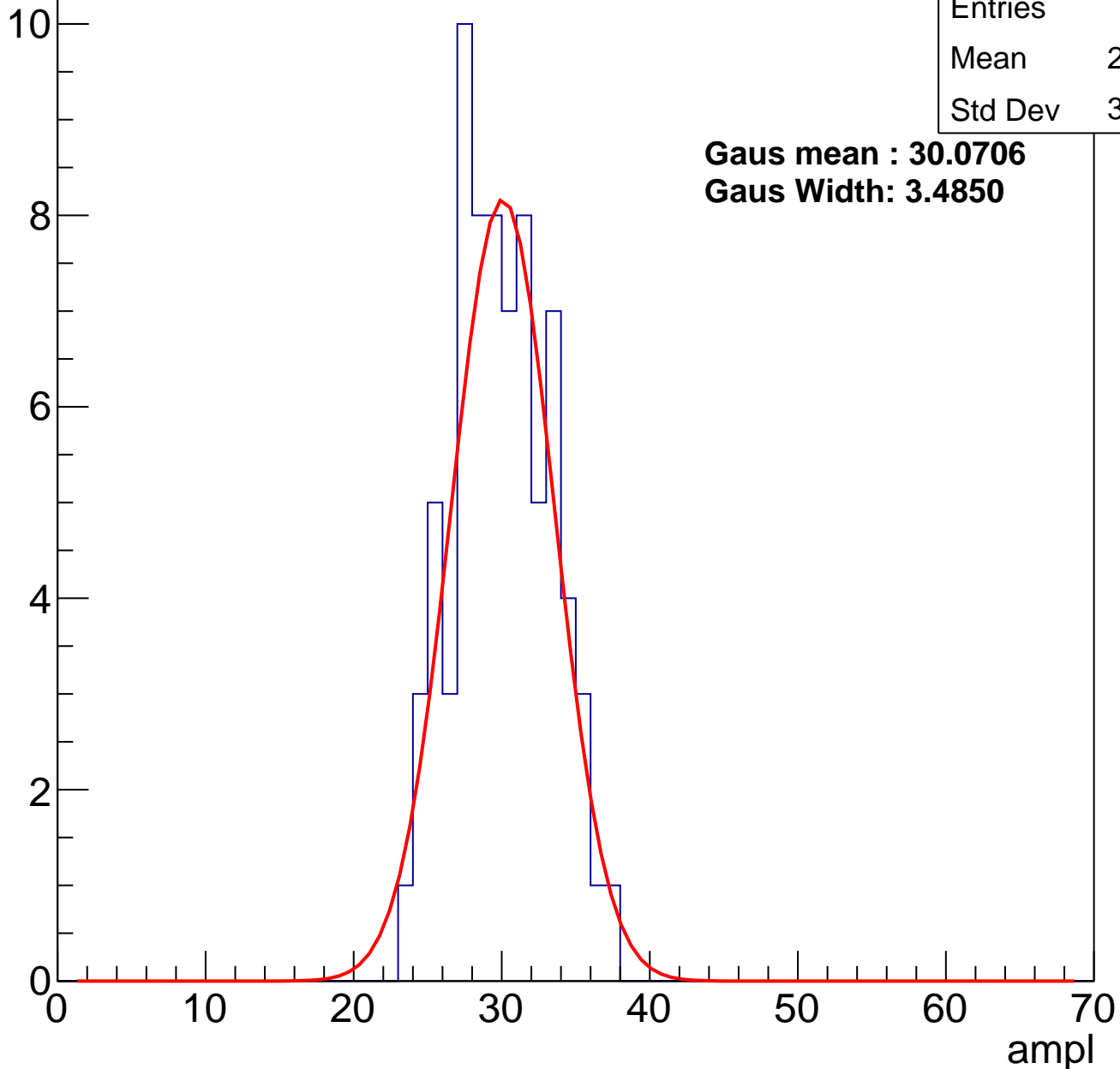
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	74
Mean	29.55
Std Dev	3.205

**Gaus mean : 30.0706**

**Gaus Width: 3.4850**

Entry



# B1L003S, U23-ch98, adc1

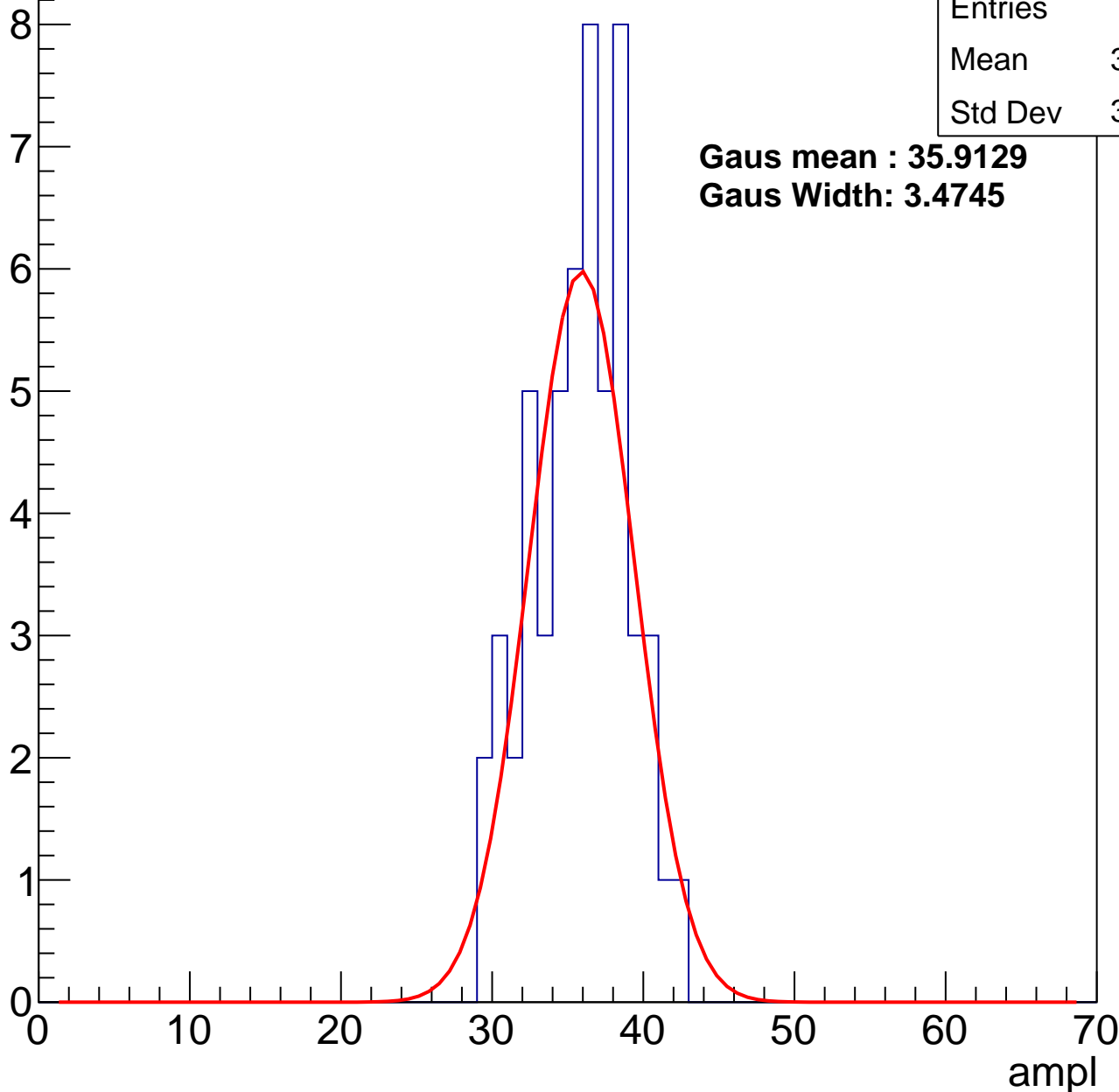
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	35.38
Std Dev	3.136

**Gaus mean : 35.9129**

**Gaus Width: 3.4745**



# B1L003S, U23-ch98, adc2

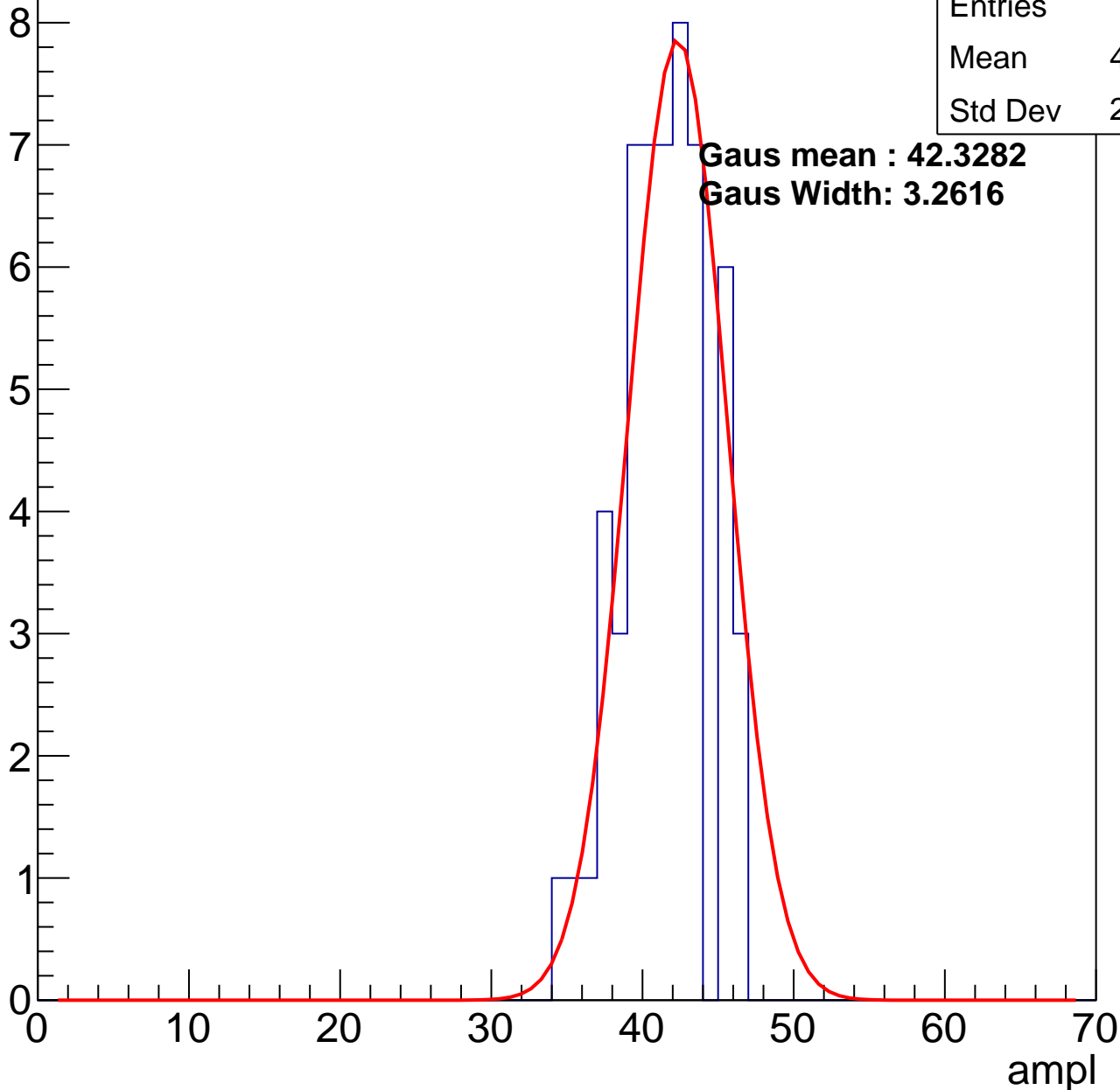
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	40.95
Std Dev	2.838

**Gaus mean : 42.3282**

**Gaus Width: 3.2616**



# B1L003S, U23-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	62
Mean	47.05
Std Dev	3.008

Entry

10

8

6

4

2

0

0

10

20

30

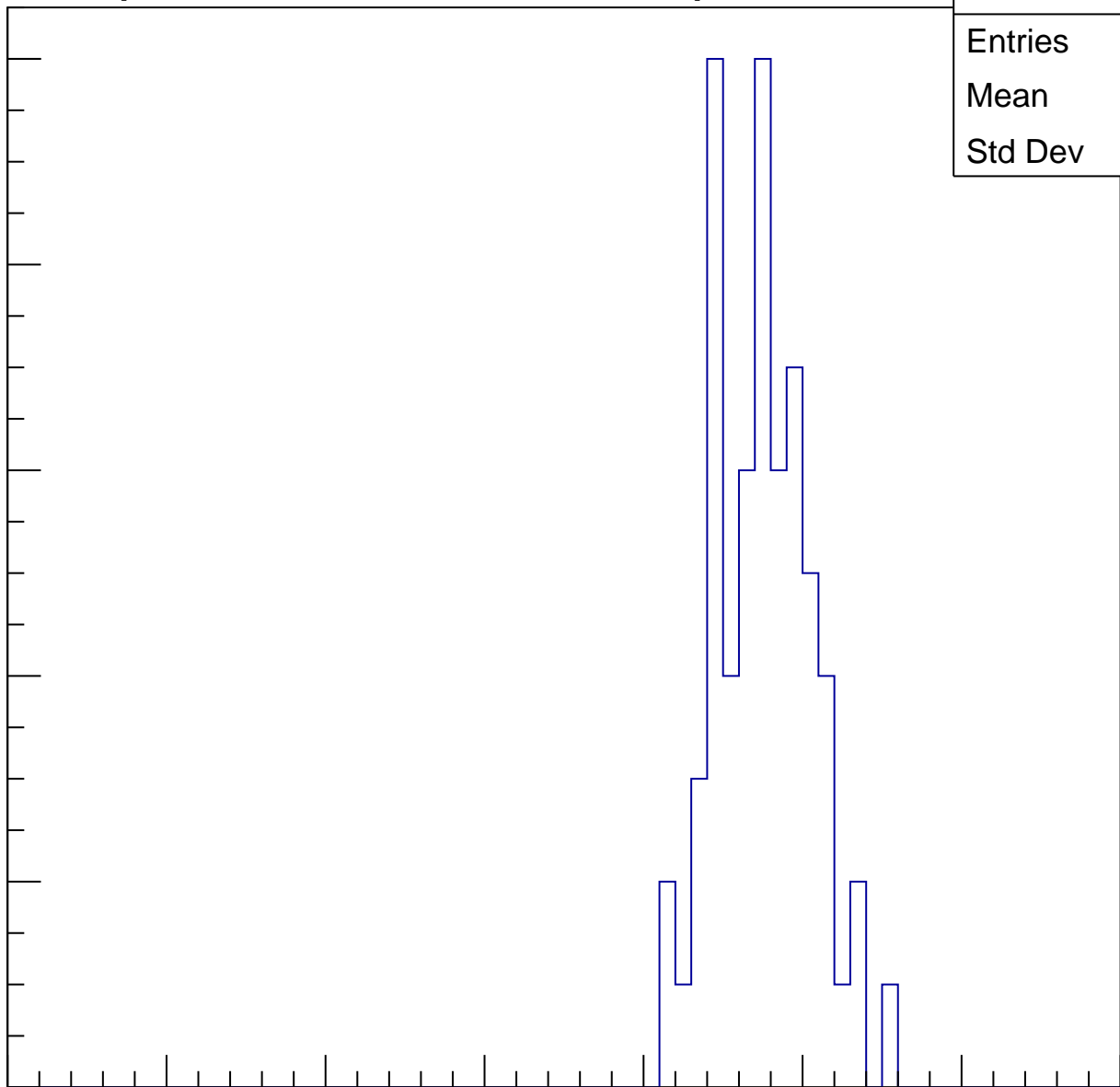
40

50

60

70

ampl

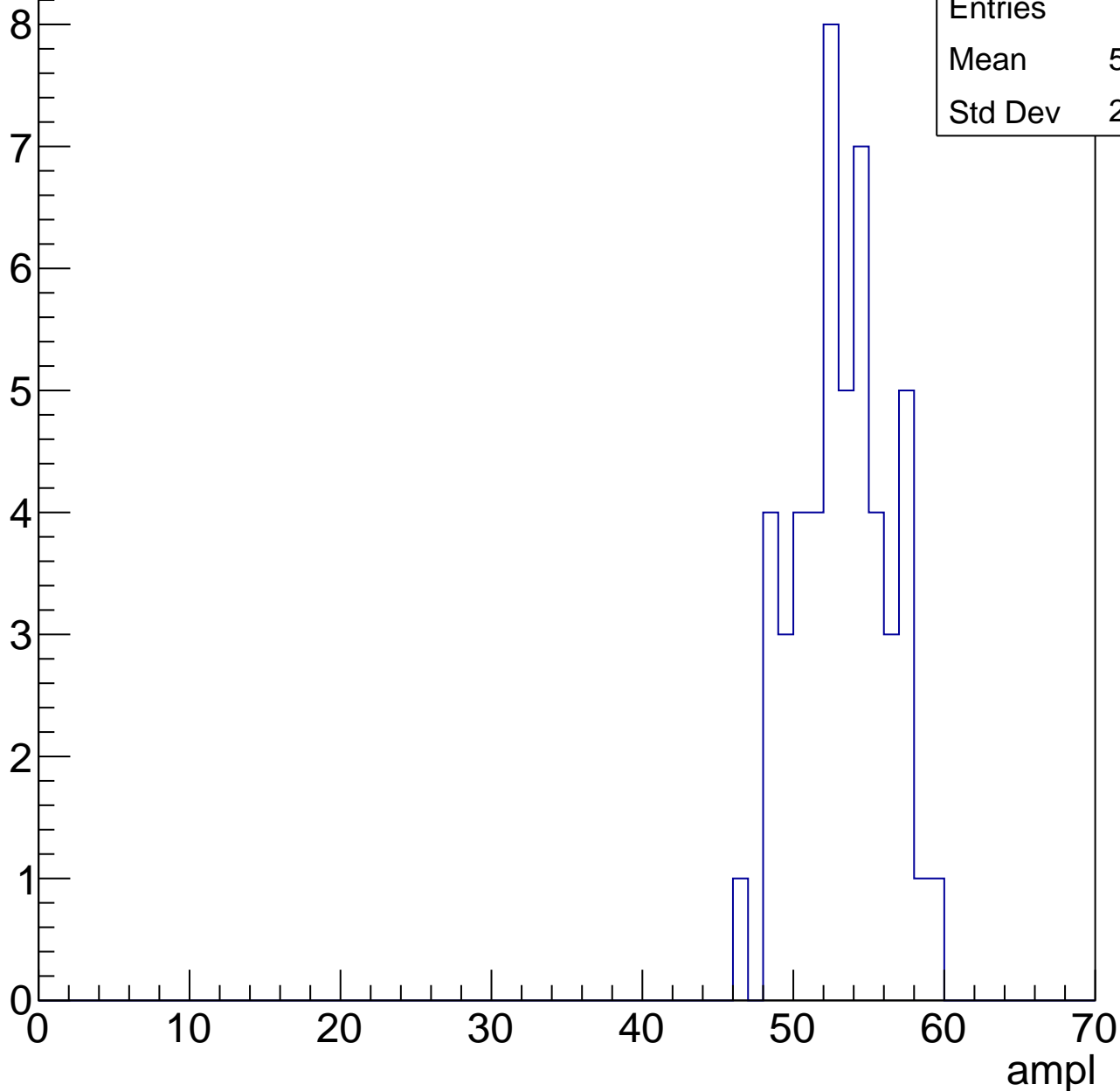


# B1L003S, U23-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	52.76
Std Dev	2.984

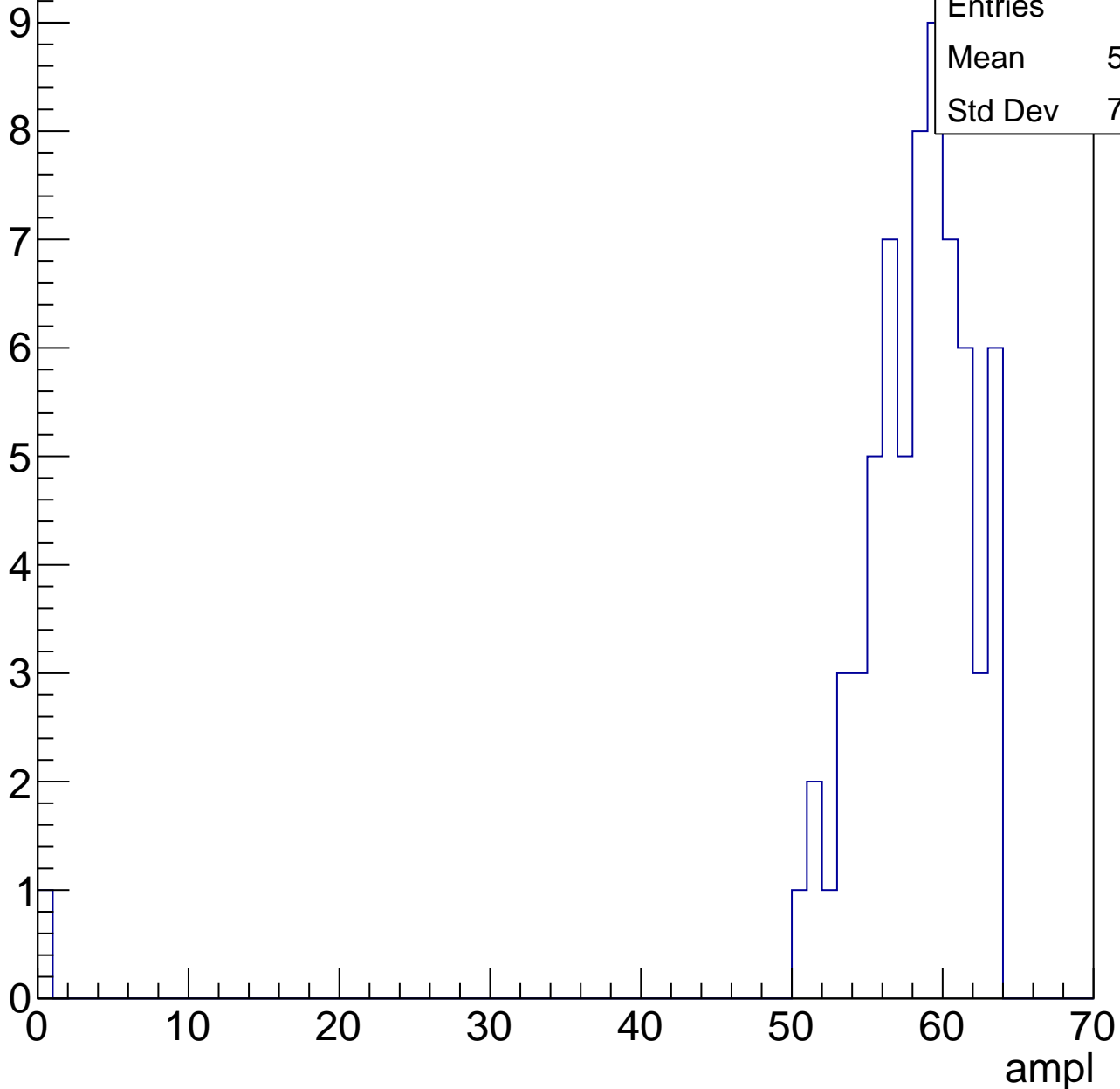


# B1L003S, U23-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	57.04
Std Dev	7.718

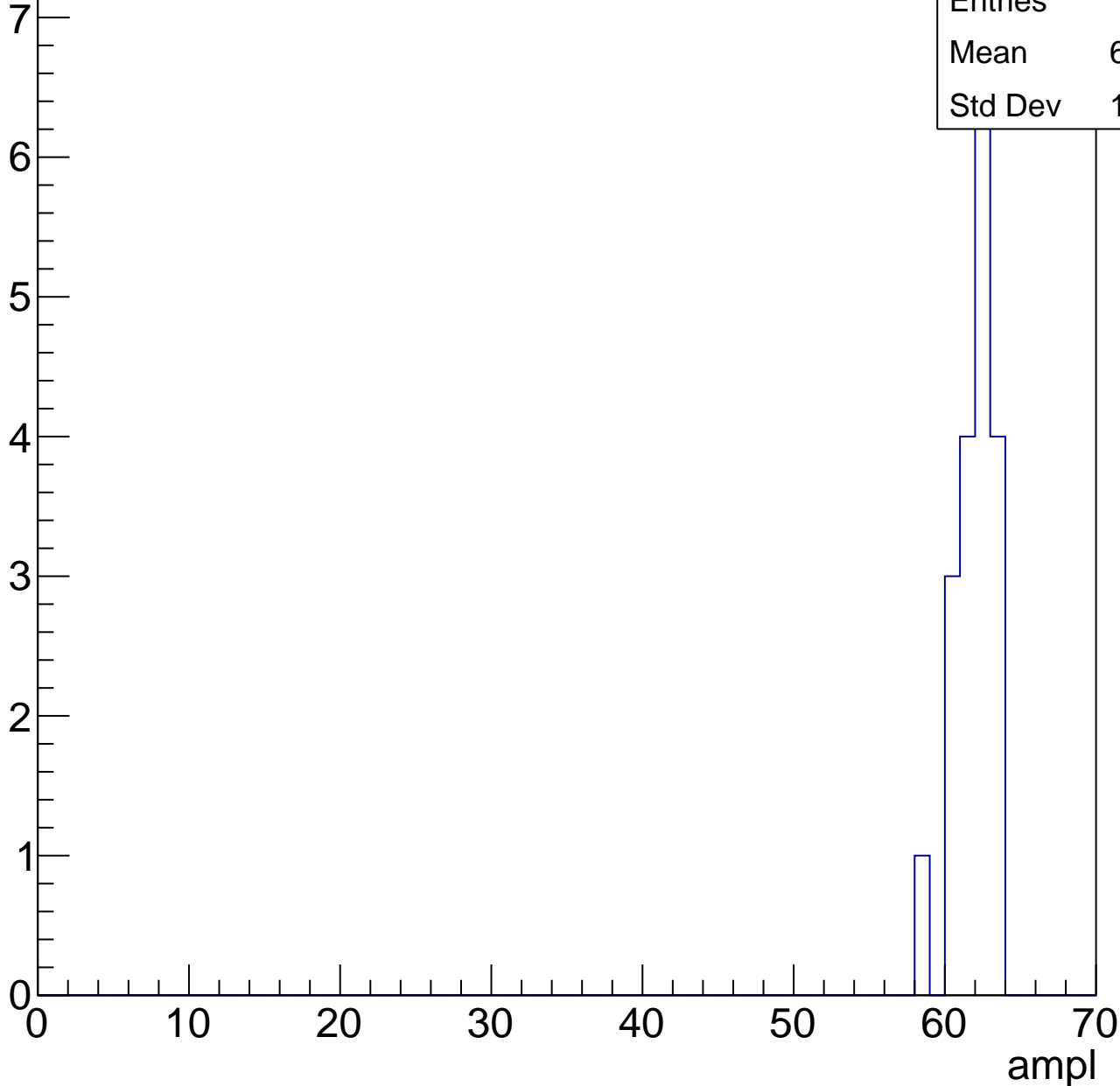


# B1L003S, U23-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	19
Mean	61.47
Std Dev	1.272





# B1L003S, U23-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch99, adc0

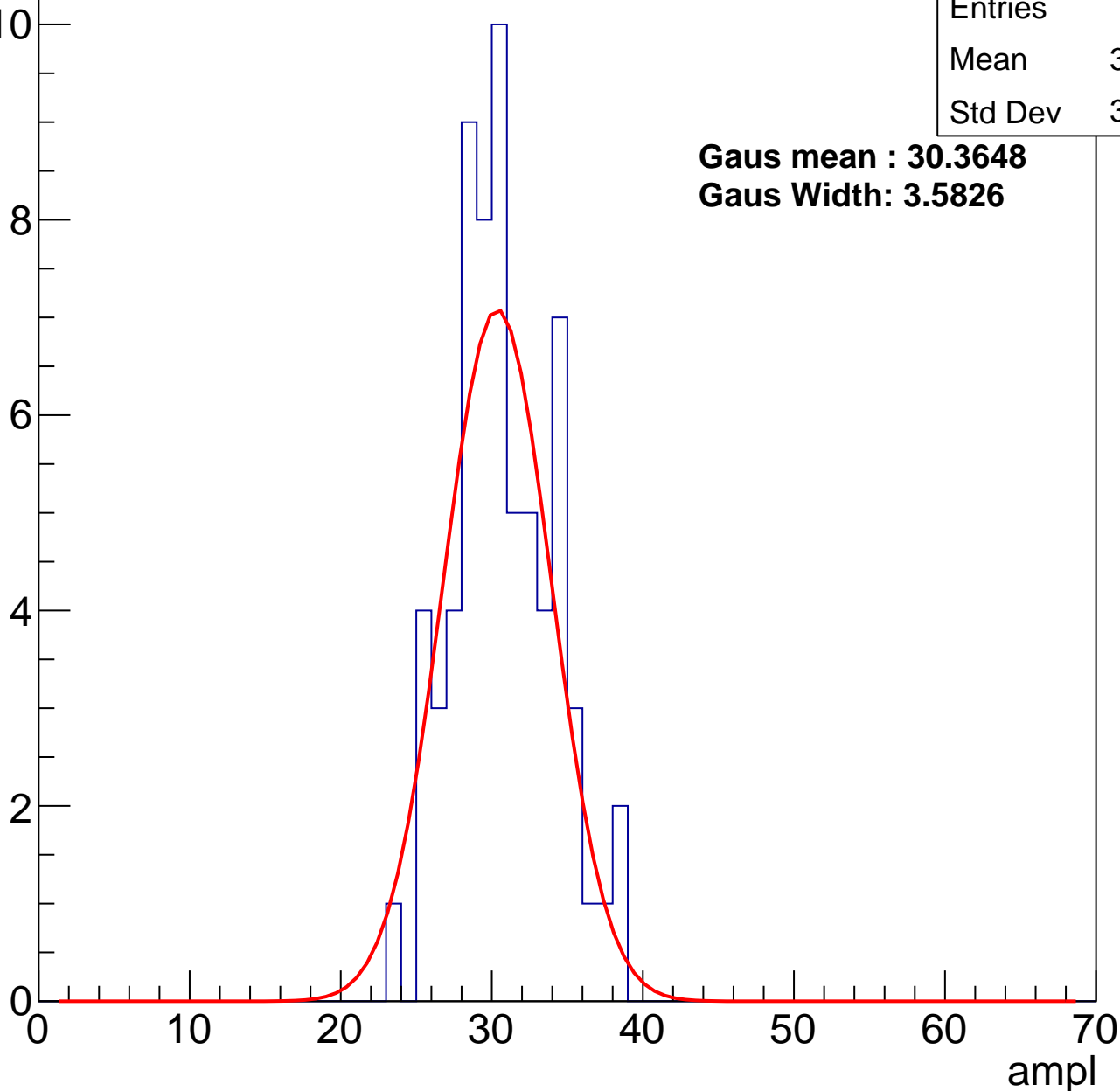
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	30.33
Std Dev	3.303

**Gaus mean : 30.3648**

**Gaus Width: 3.5826**



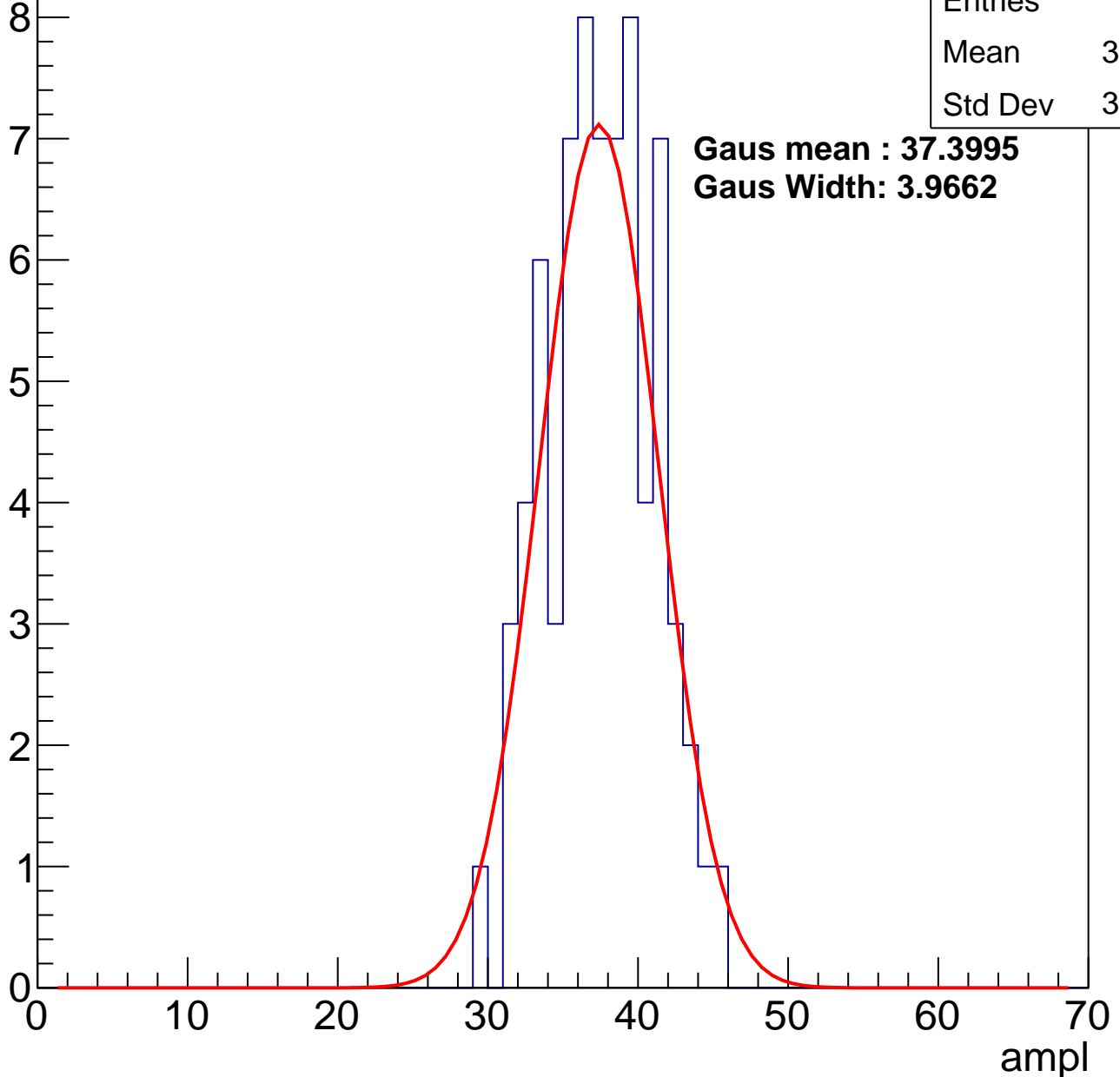
# B1L003S, U23-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	37.06
Std Dev	3.492

**Gaus mean : 37.3995**  
**Gaus Width: 3.9662**



# B1L003S, U23-ch99, adc2

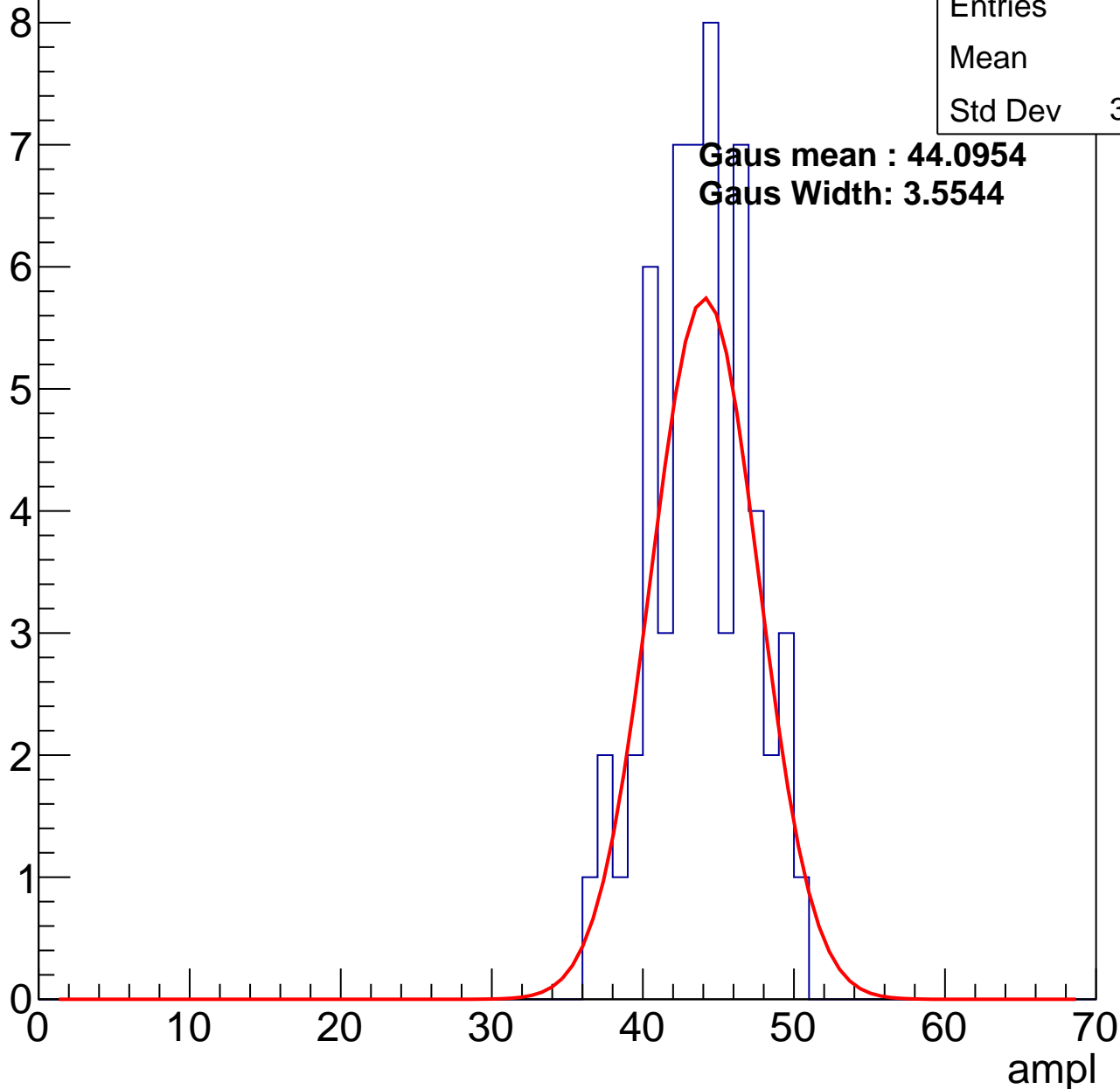
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	43.4
Std Dev	3.233

**Gaus mean : 44.0954**

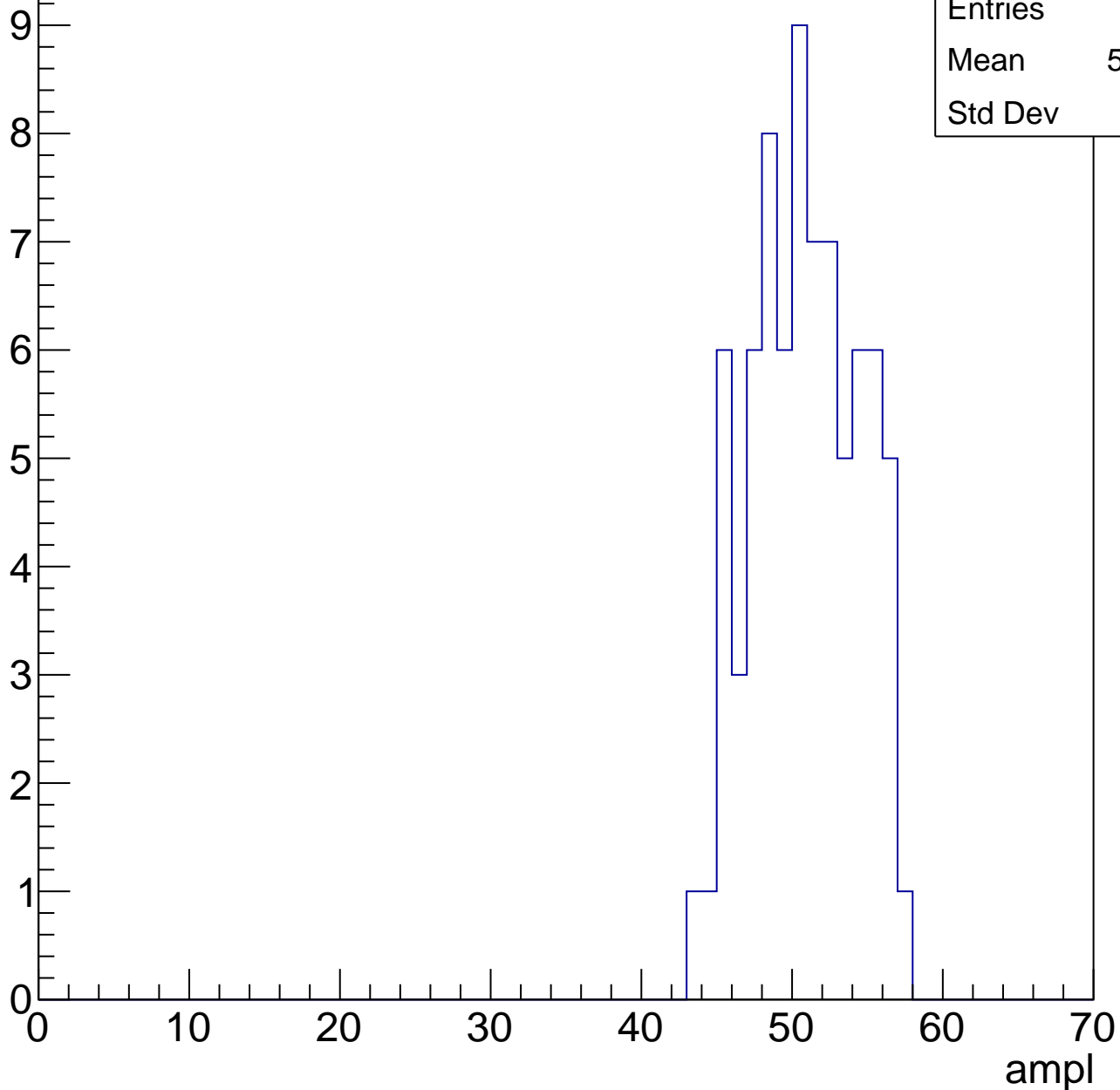
**Gaus Width: 3.5544**



# B1L003S, U23-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



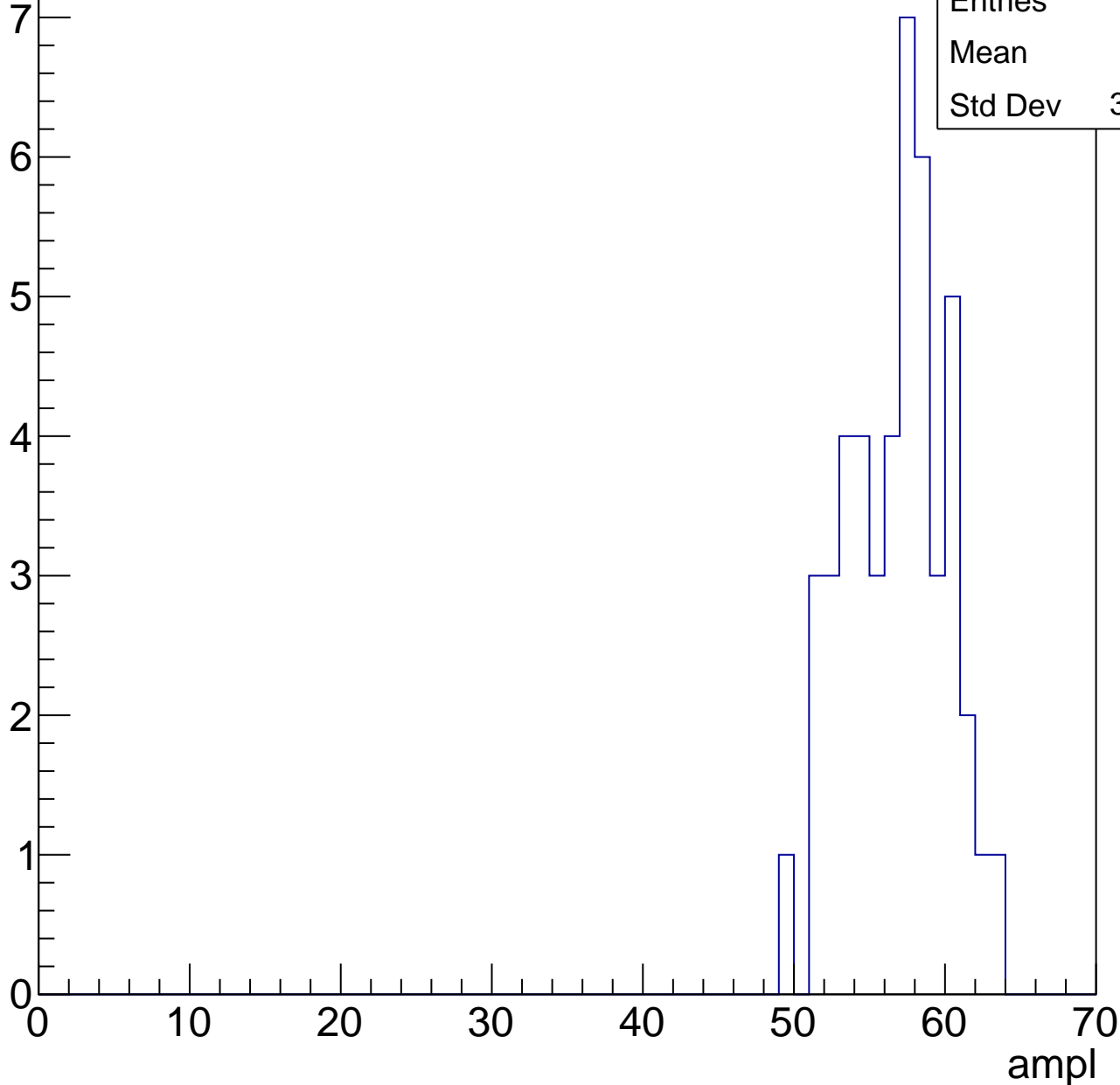
Entries	77
Mean	50.42
Std Dev	3.45

# B1L003S, U23-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	56.3
Std Dev	3.248

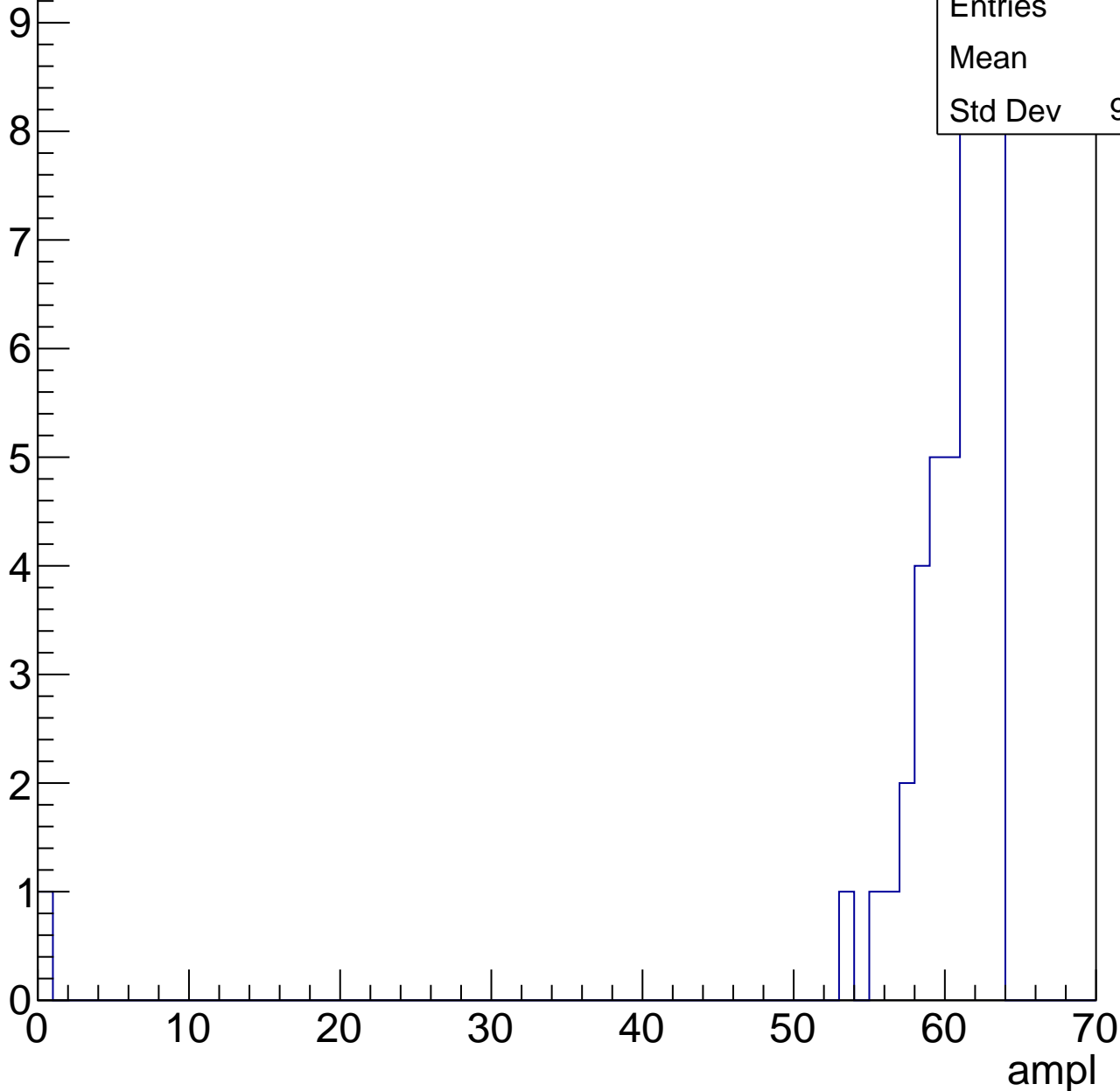


# B1L003S, U23-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59
Std Dev	9.189



# B1L003S, U23-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

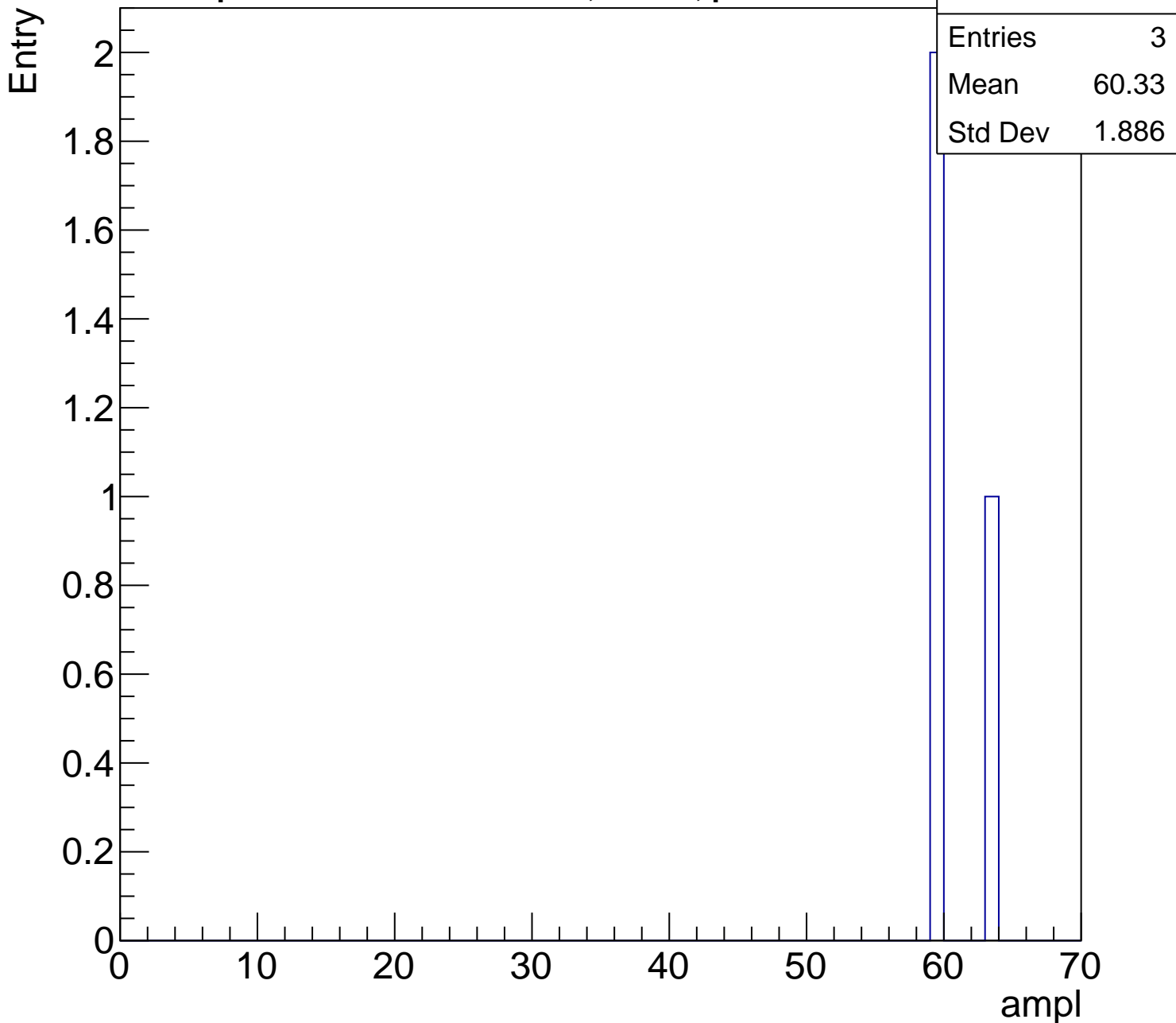
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	60.33
Std Dev	1.886

0 10 20 30 40 50 60 70

ampl





# B1L003S, U23-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch100, adc0

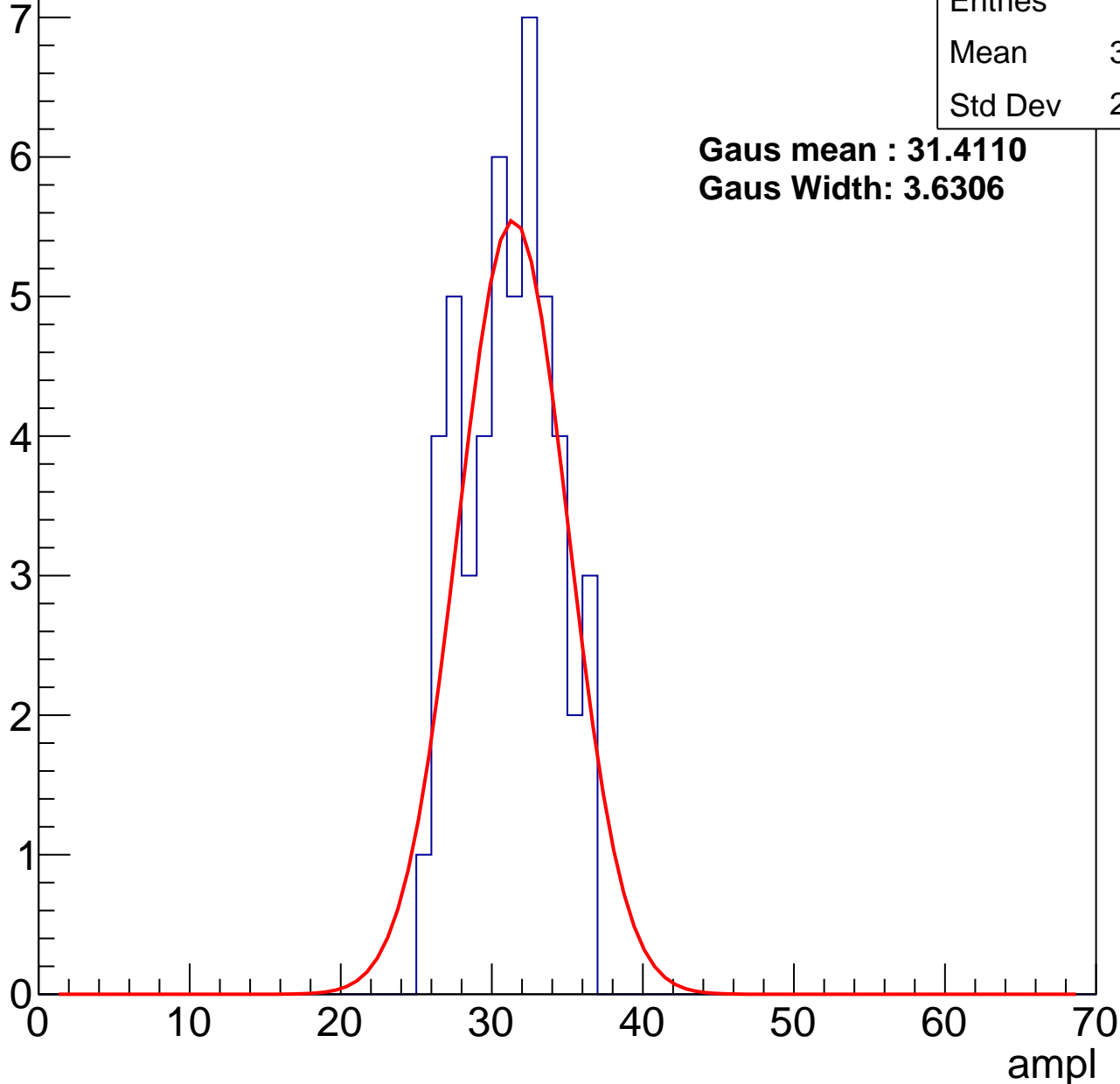
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	30.65
Std Dev	2.959

**Gaus mean : 31.4110**

**Gaus Width: 3.6306**



# B1L003S, U23-ch100, adc1

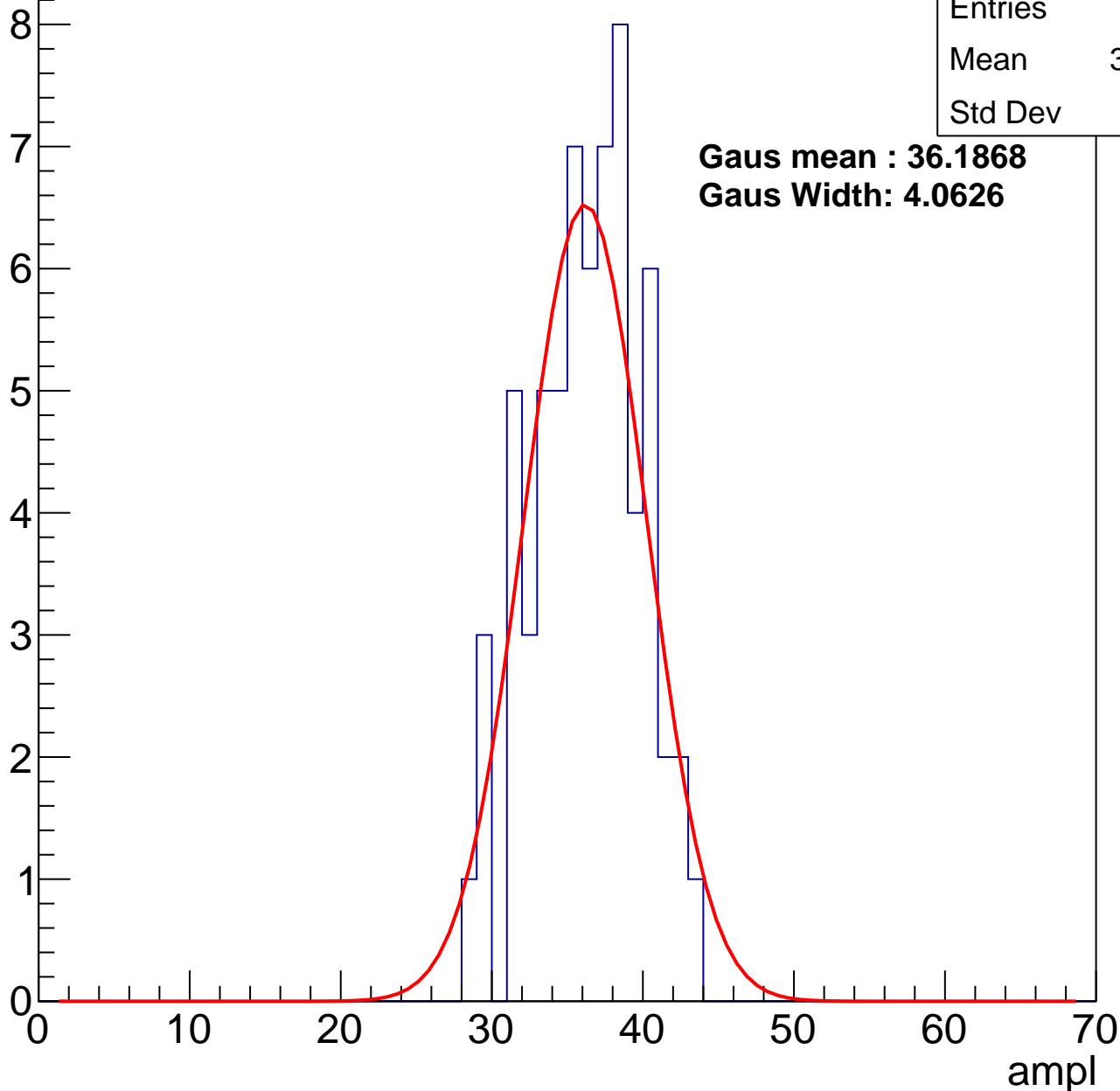
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.85
Std Dev	3.5

**Gaus mean : 36.1868**

**Gaus Width: 4.0626**



# B1L003S, U23-ch100, adc2

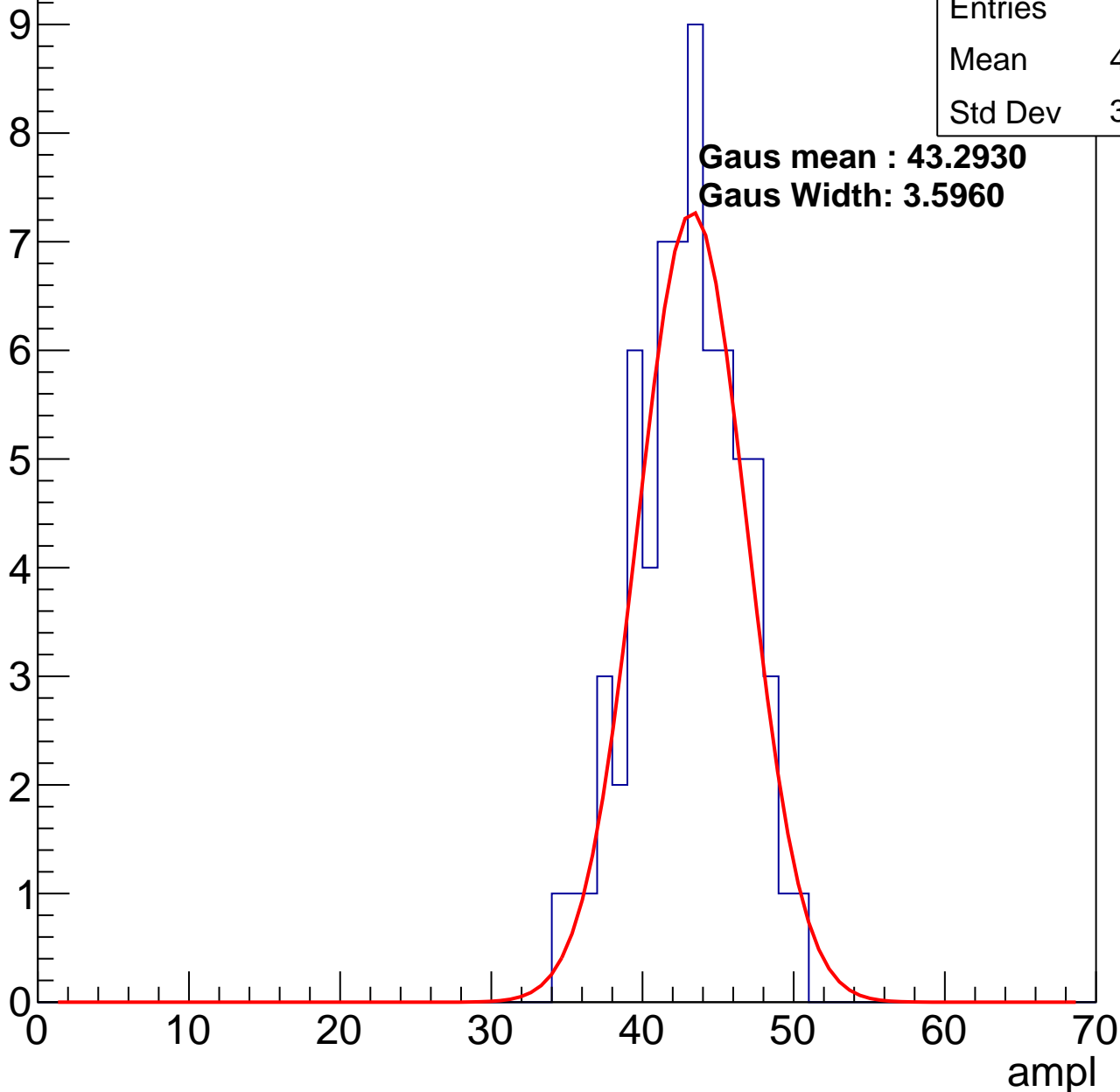
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.59
Std Dev	3.478

**Gaus mean : 43.2930**

**Gaus Width: 3.5960**

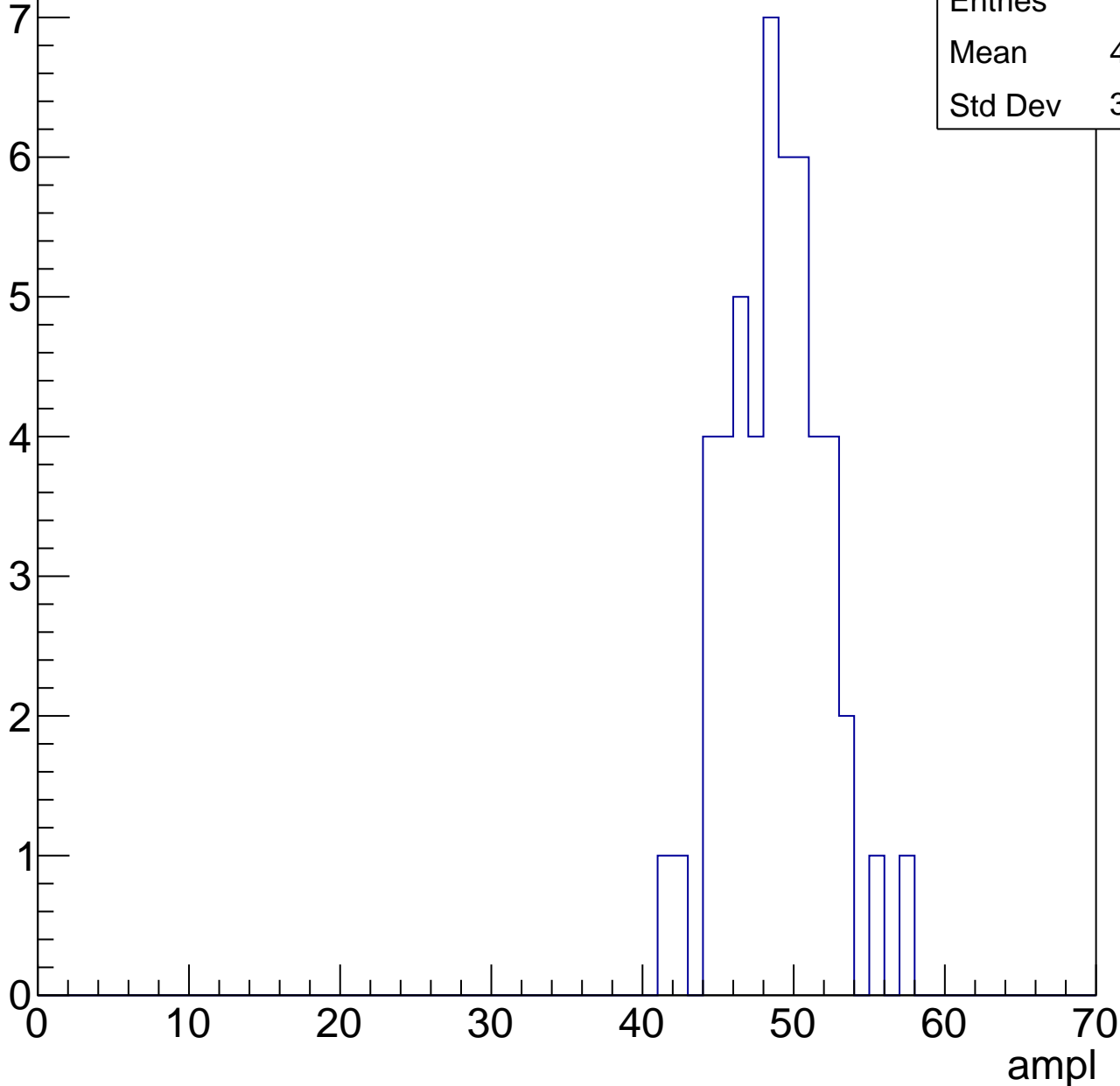


# B1L003S, U23-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	48.34
Std Dev	3.204

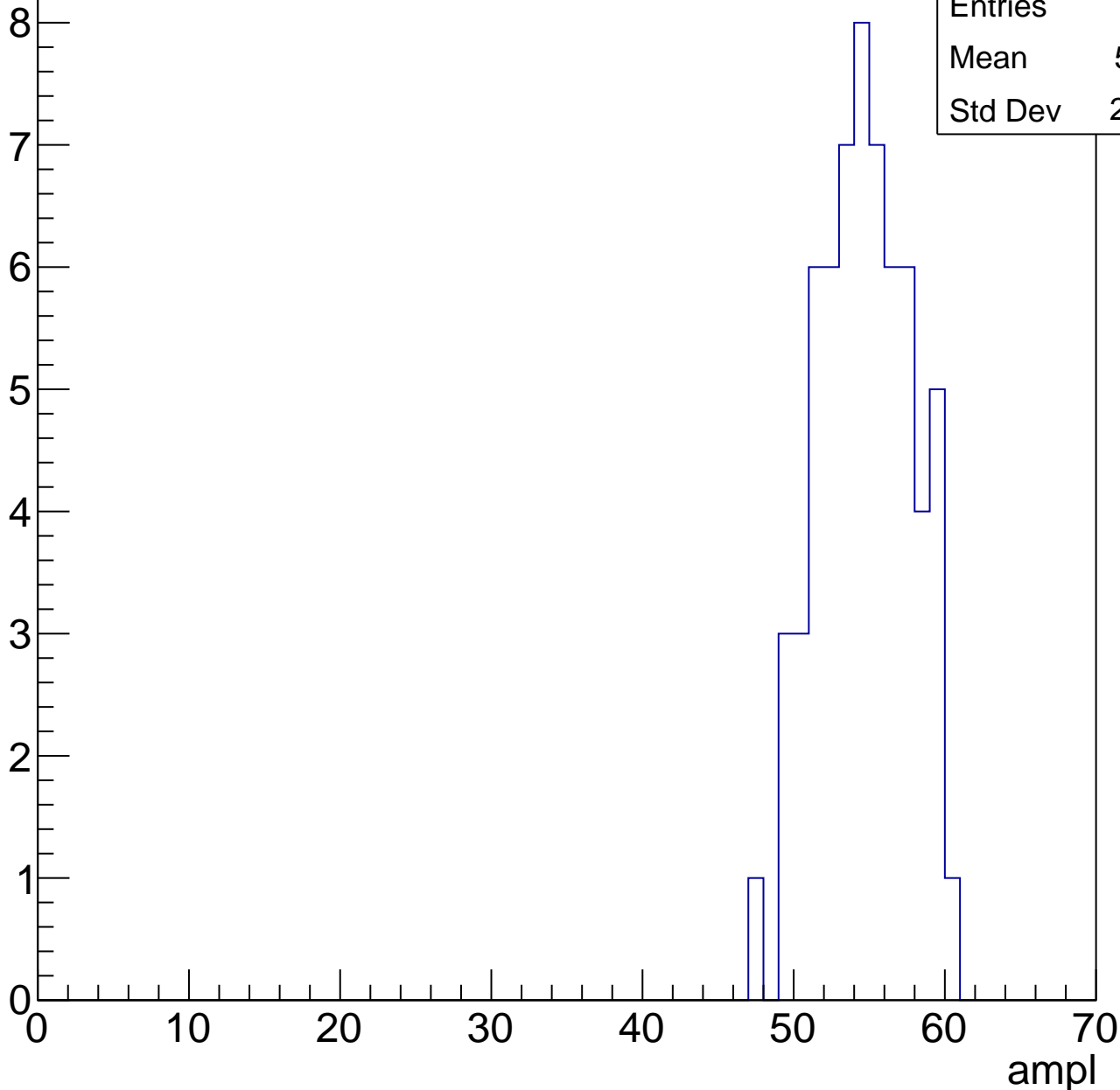


# B1L003S, U23-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	54.21
Std Dev	2.993

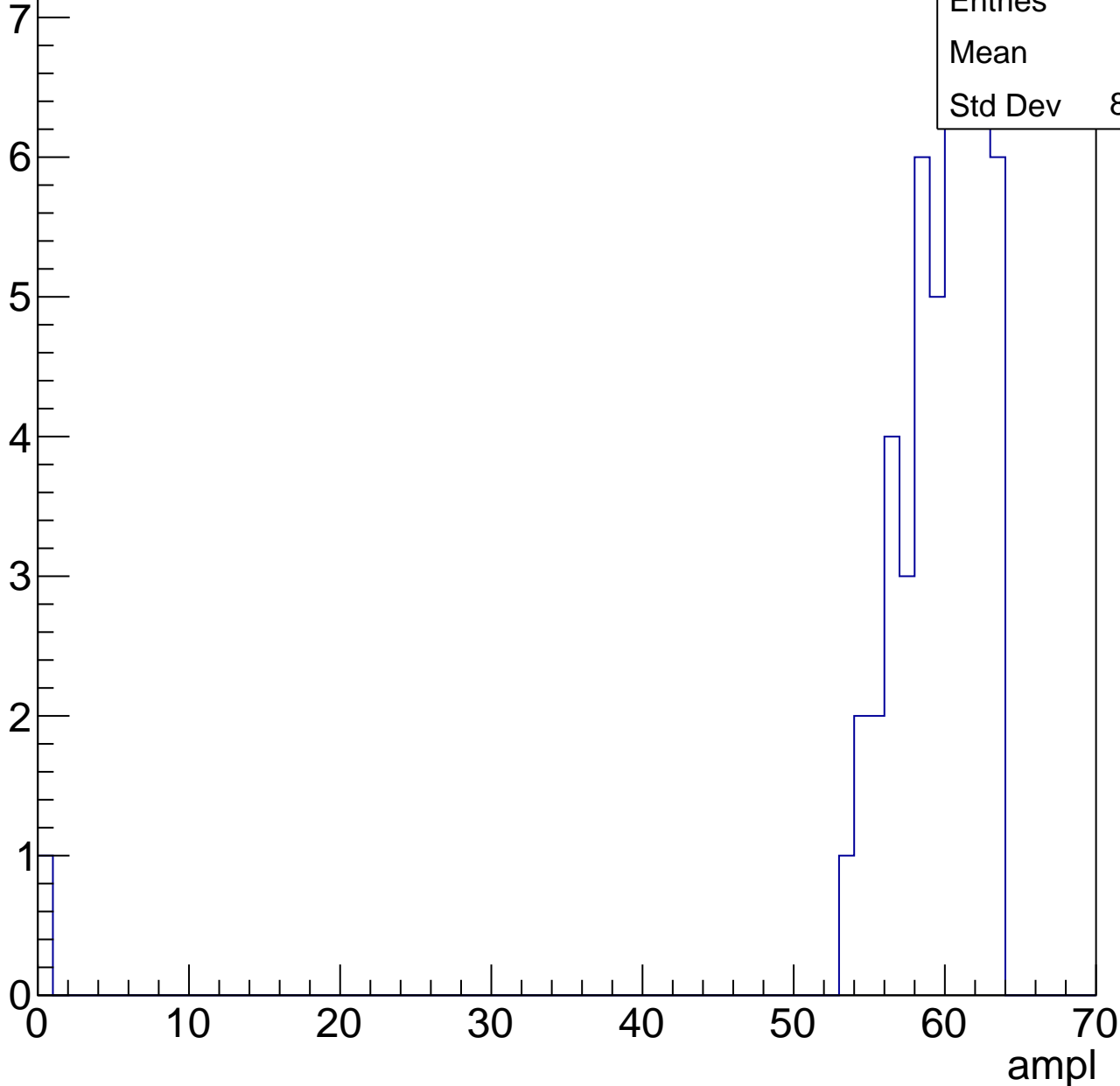


# B1L003S, U23-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.2
Std Dev	8.648



# B1L003S, U23-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

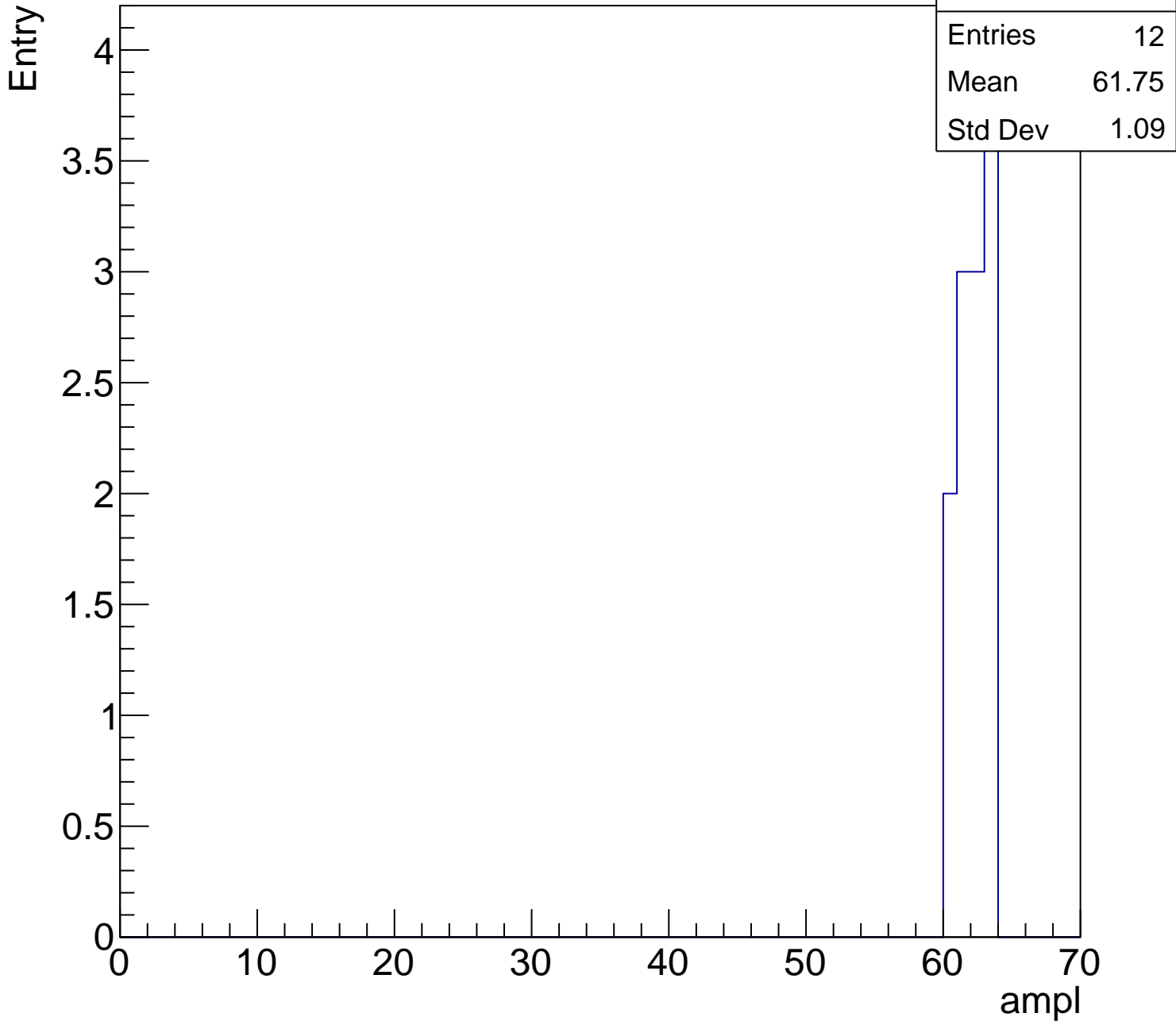
Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	12
Mean	61.75
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

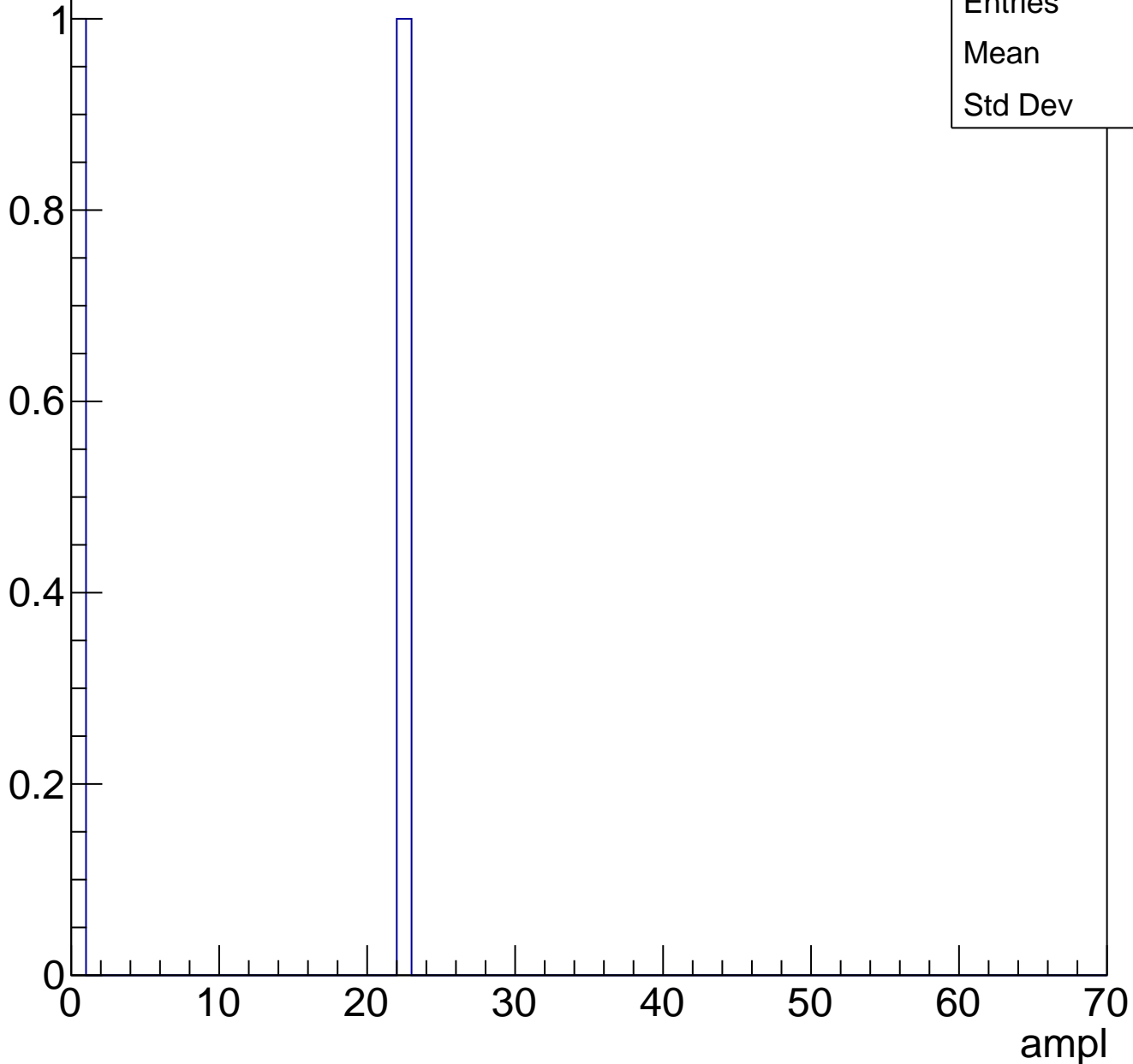




# B1L003S, U23-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L003S, U23-ch101, adc0

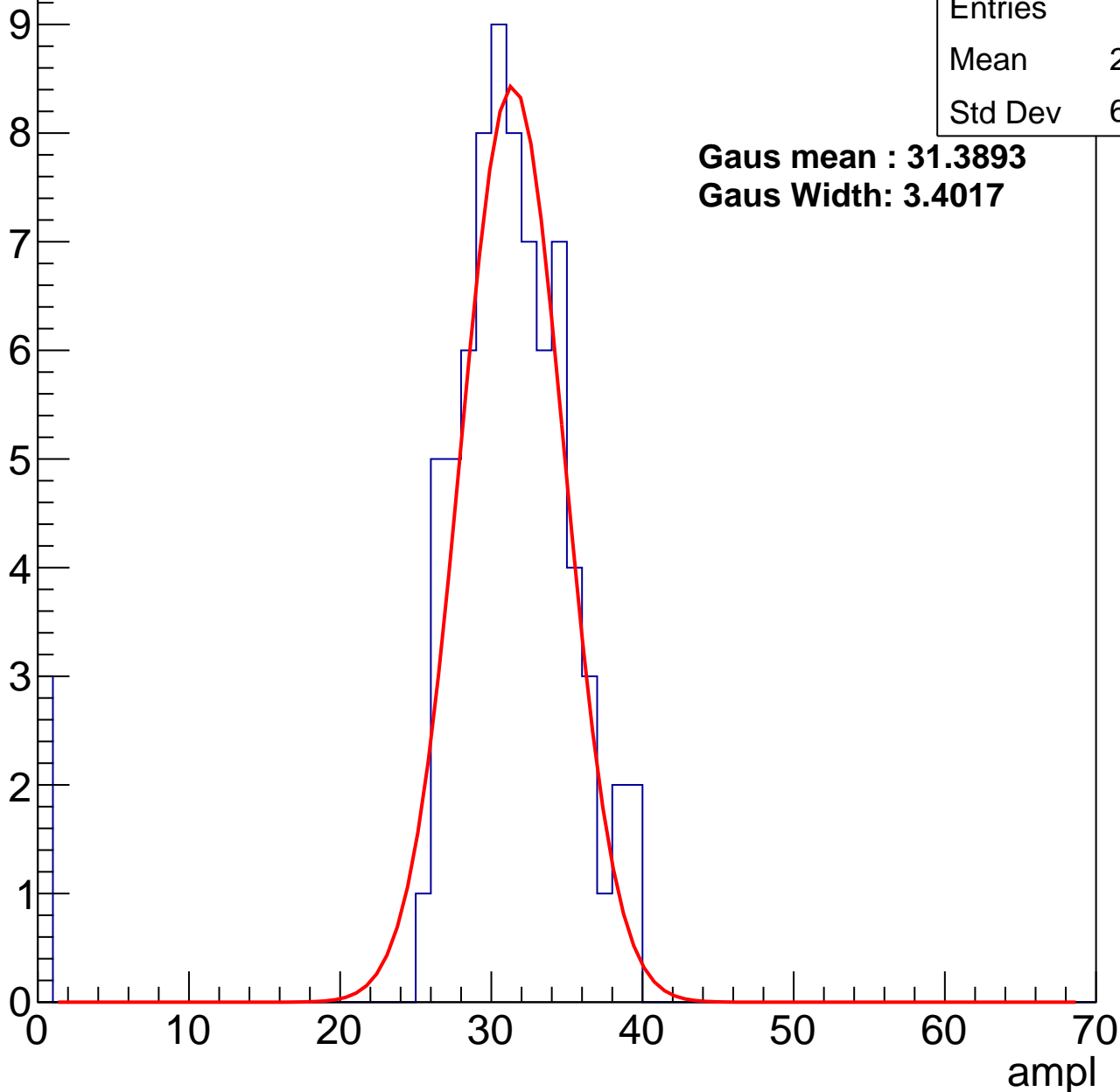
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	29.96
Std Dev	6.867

**Gaus mean : 31.3893**

**Gaus Width: 3.4017**



# B1L003S, U23-ch101, adc1

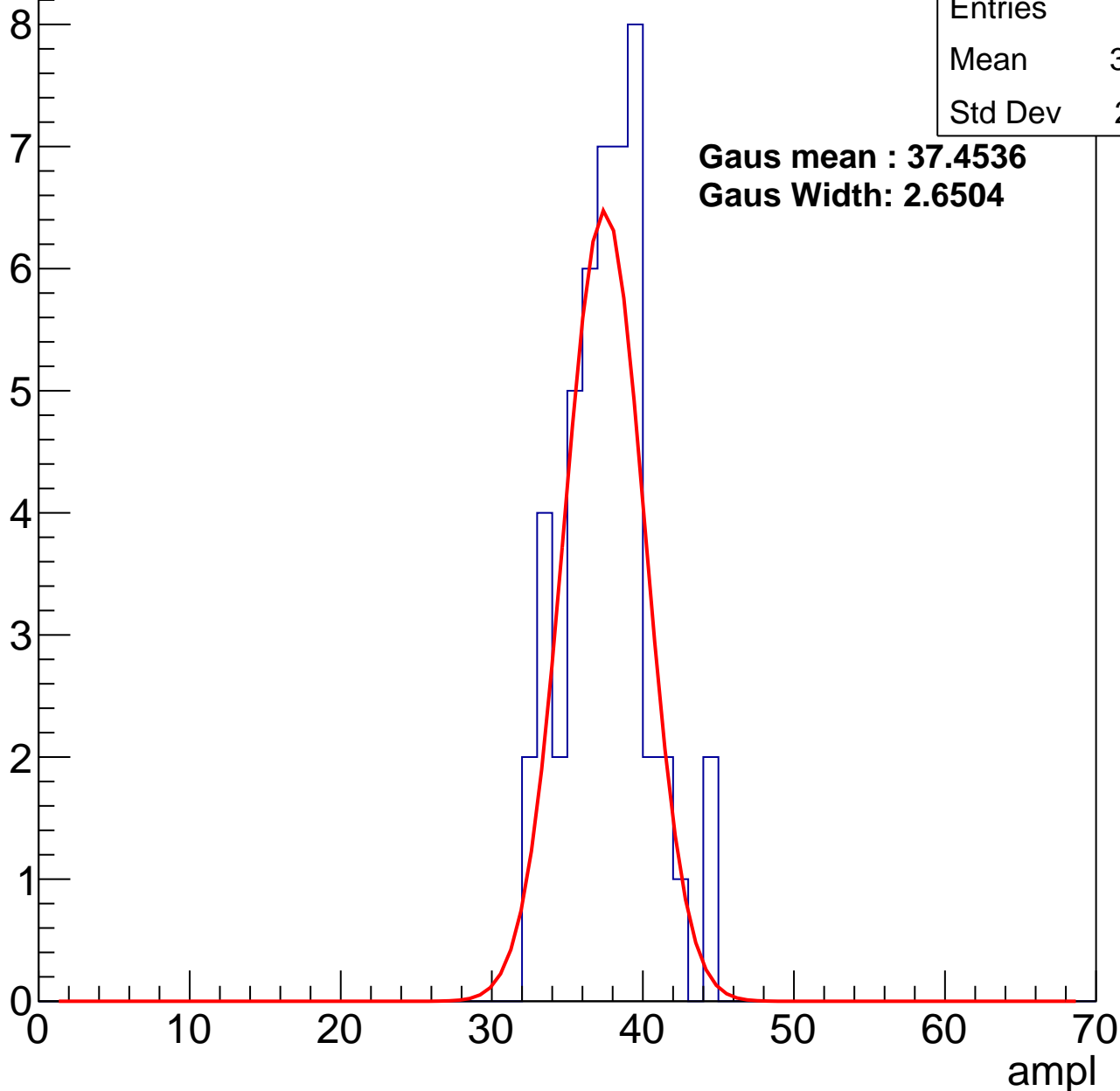
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	37.17
Std Dev	2.771

**Gaus mean : 37.4536**

**Gaus Width: 2.6504**



# B1L003S, U23-ch101, adc2

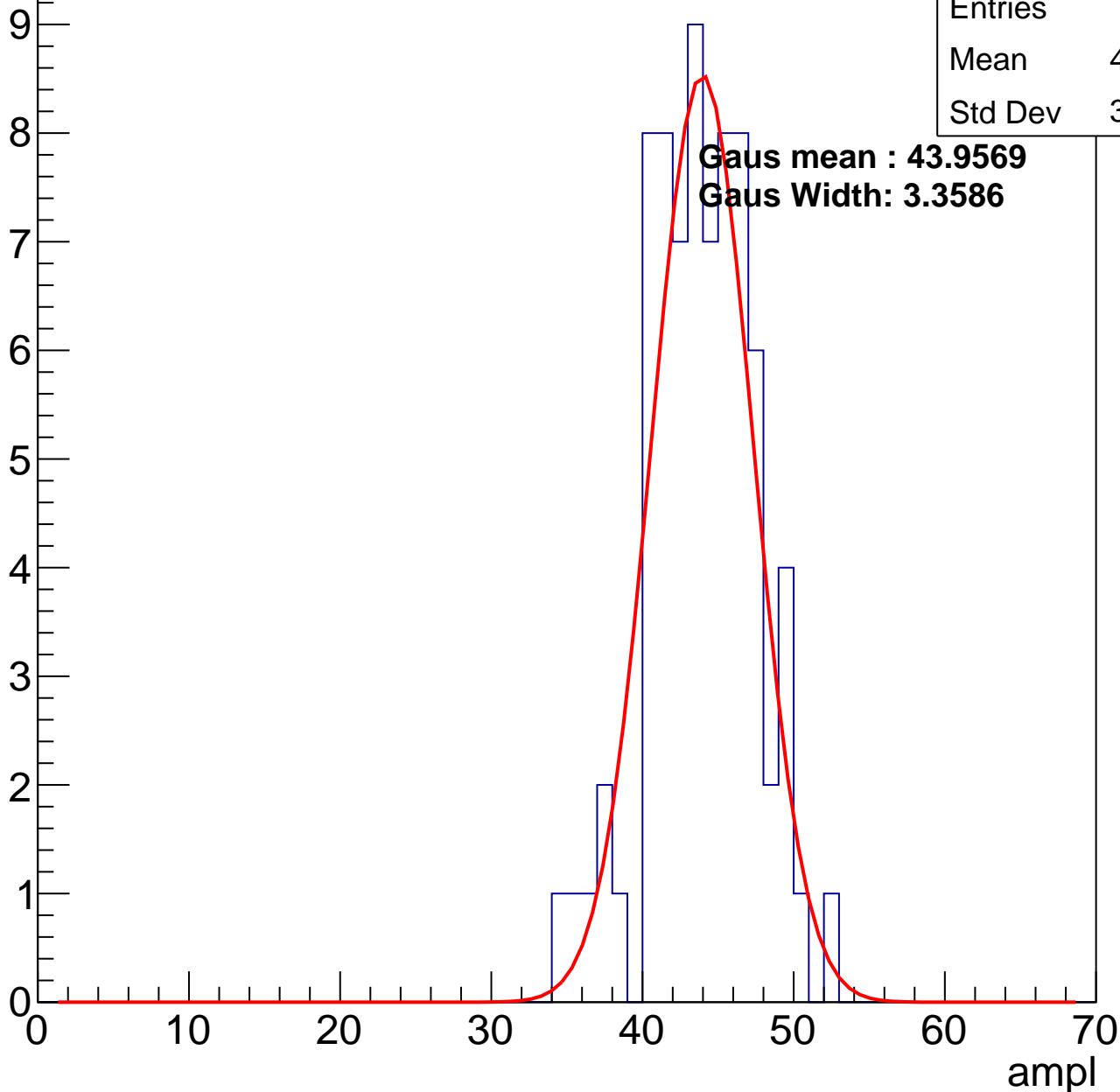
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	43.44
Std Dev	3.499

**Gaus mean : 43.9569**

**Gaus Width: 3.3586**

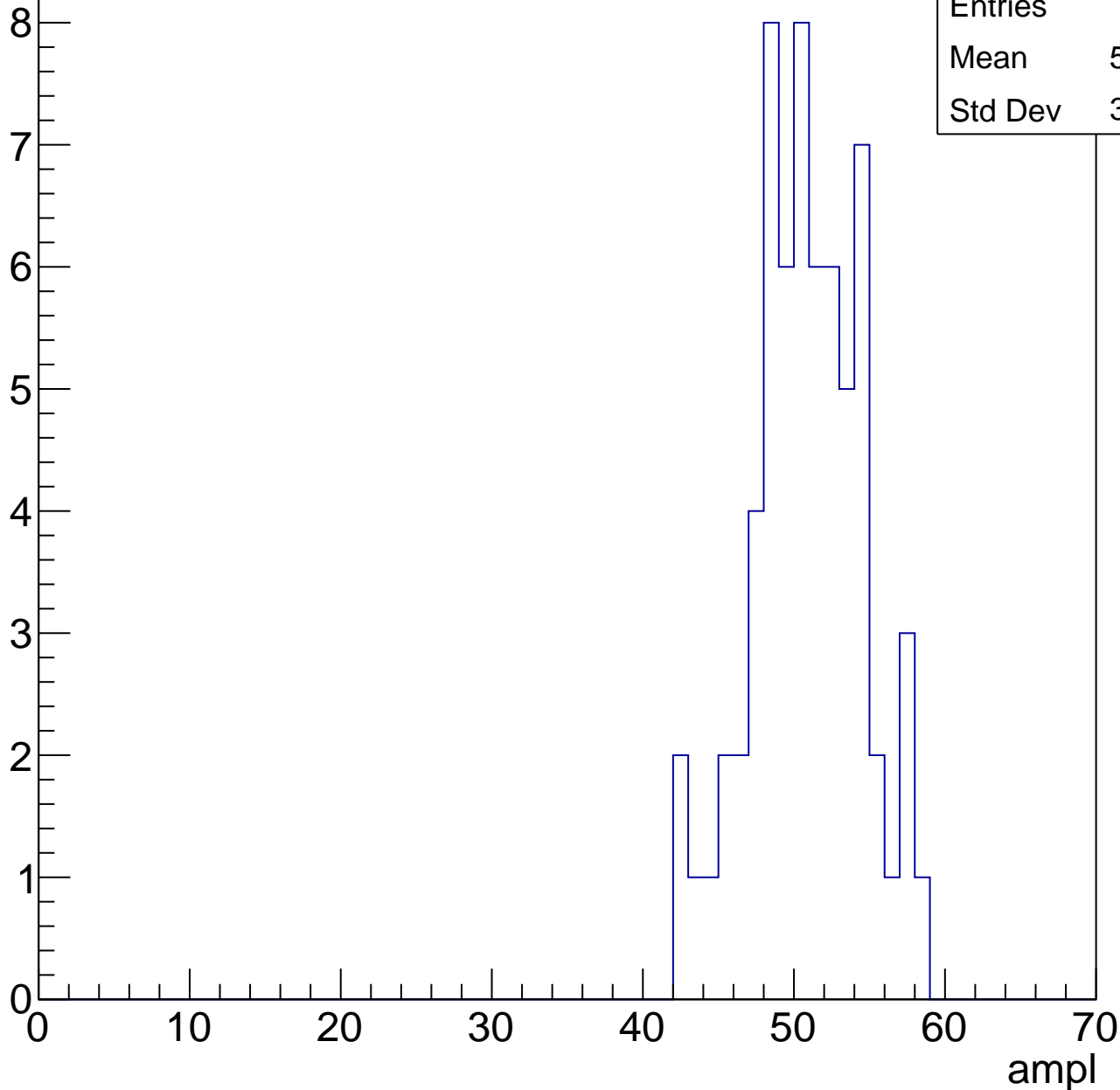


# B1L003S, U23-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	50.38
Std Dev	3.602

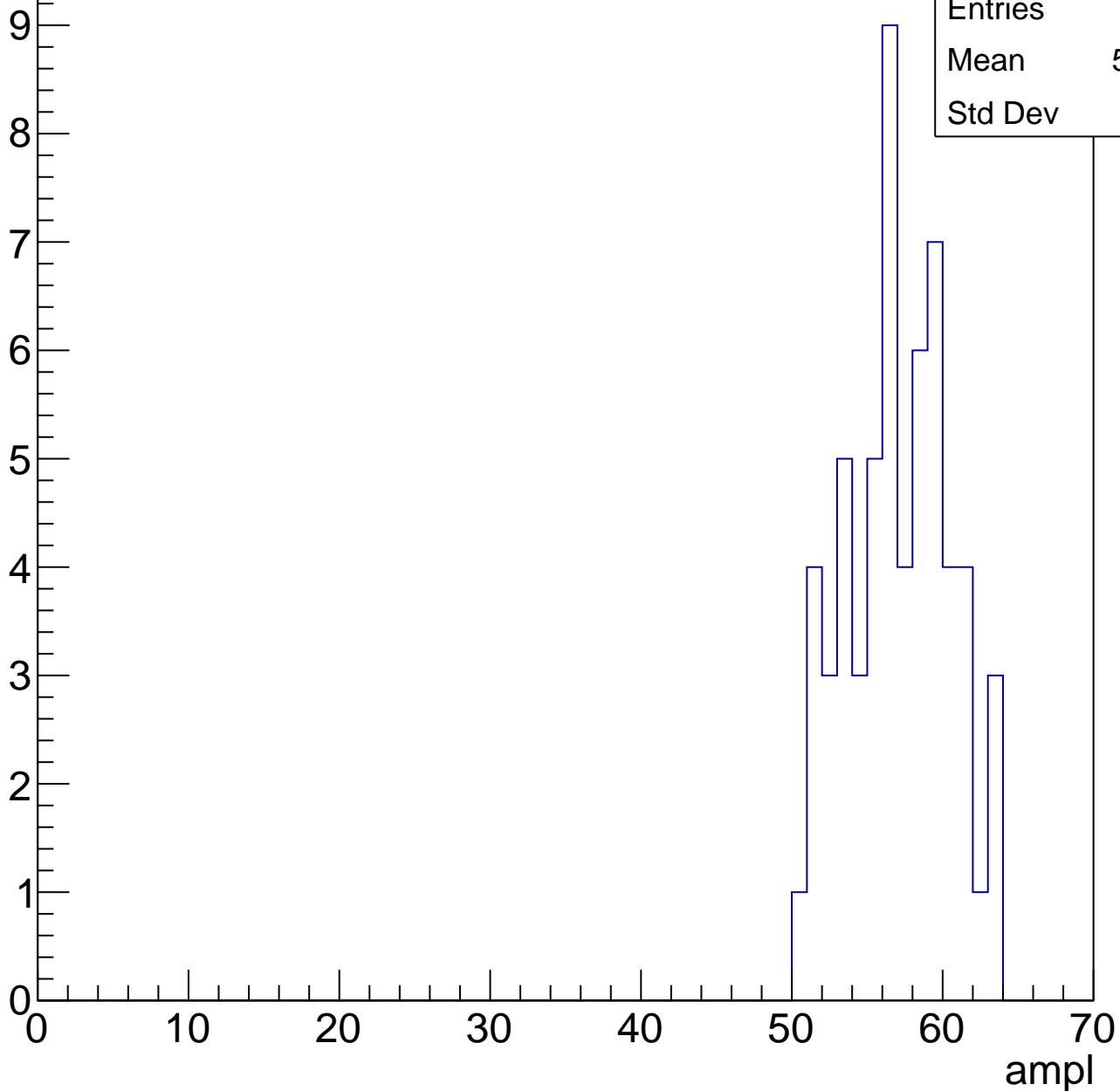


# B1L003S, U23-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.61
Std Dev	3.35

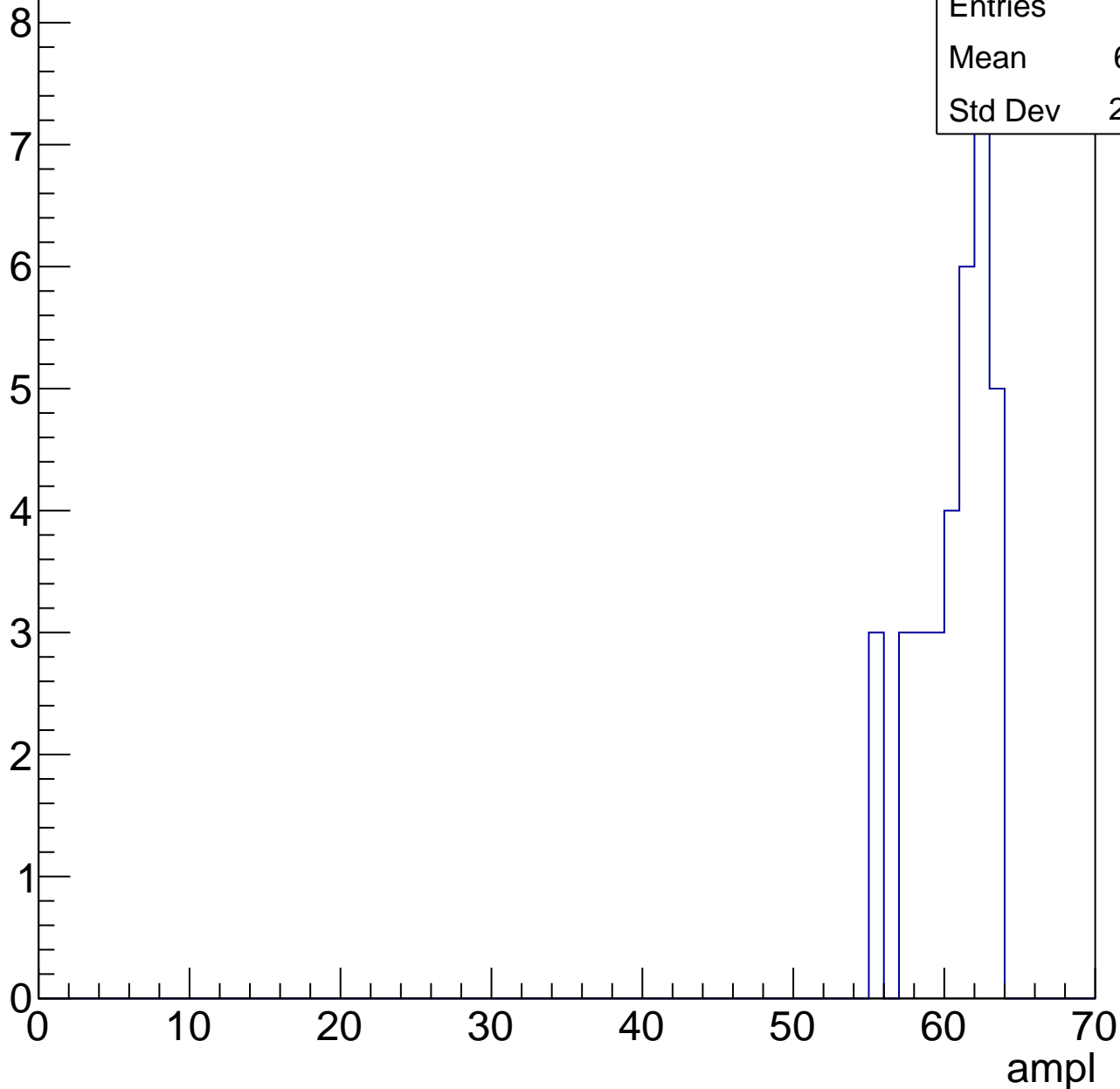


# B1L003S, U23-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	35
Mean	60.11
Std Dev	2.388



# B1L003S, U23-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

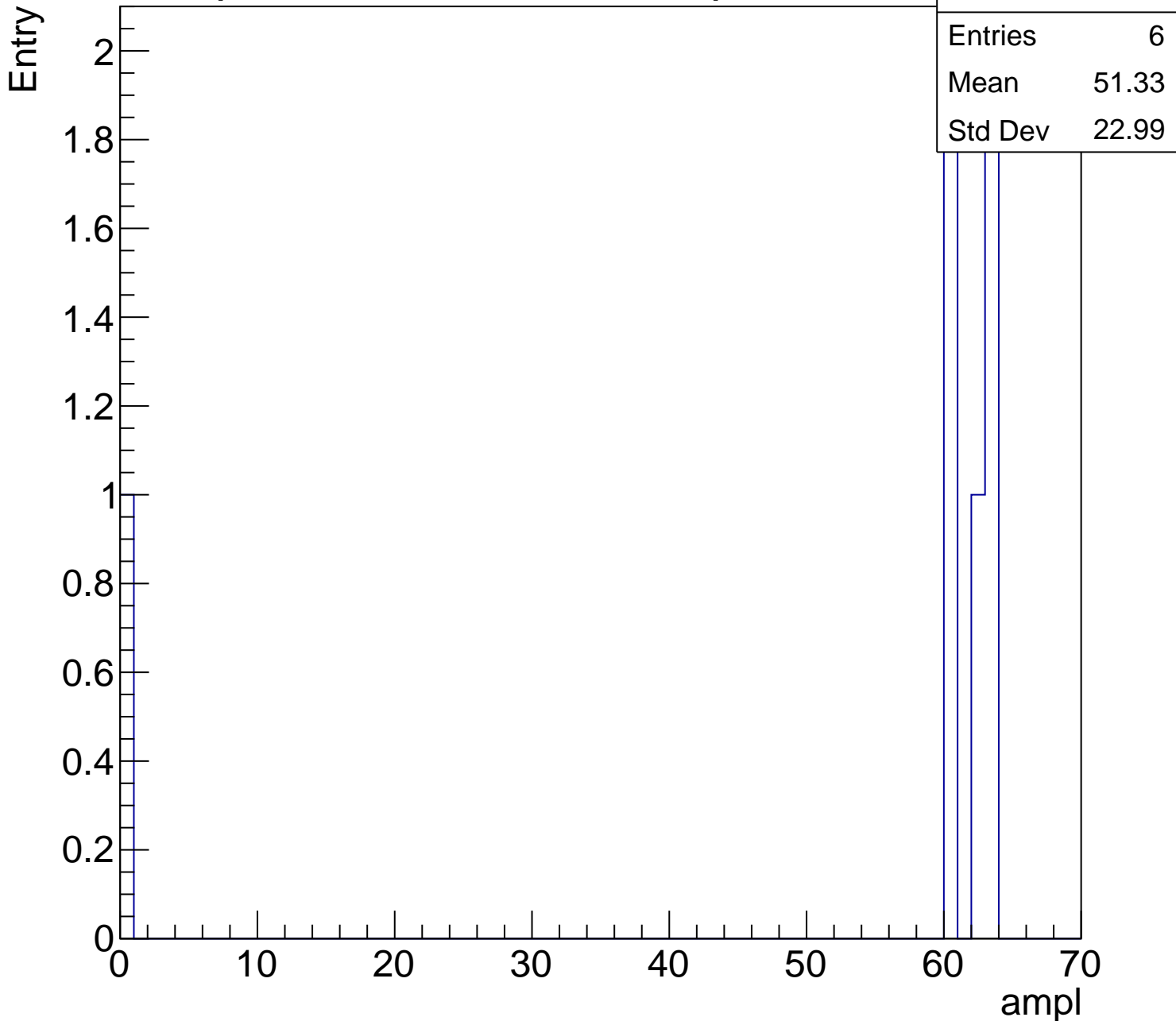
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.33
Std Dev	22.99

0 10 20 30 40 50 60 70

ampl

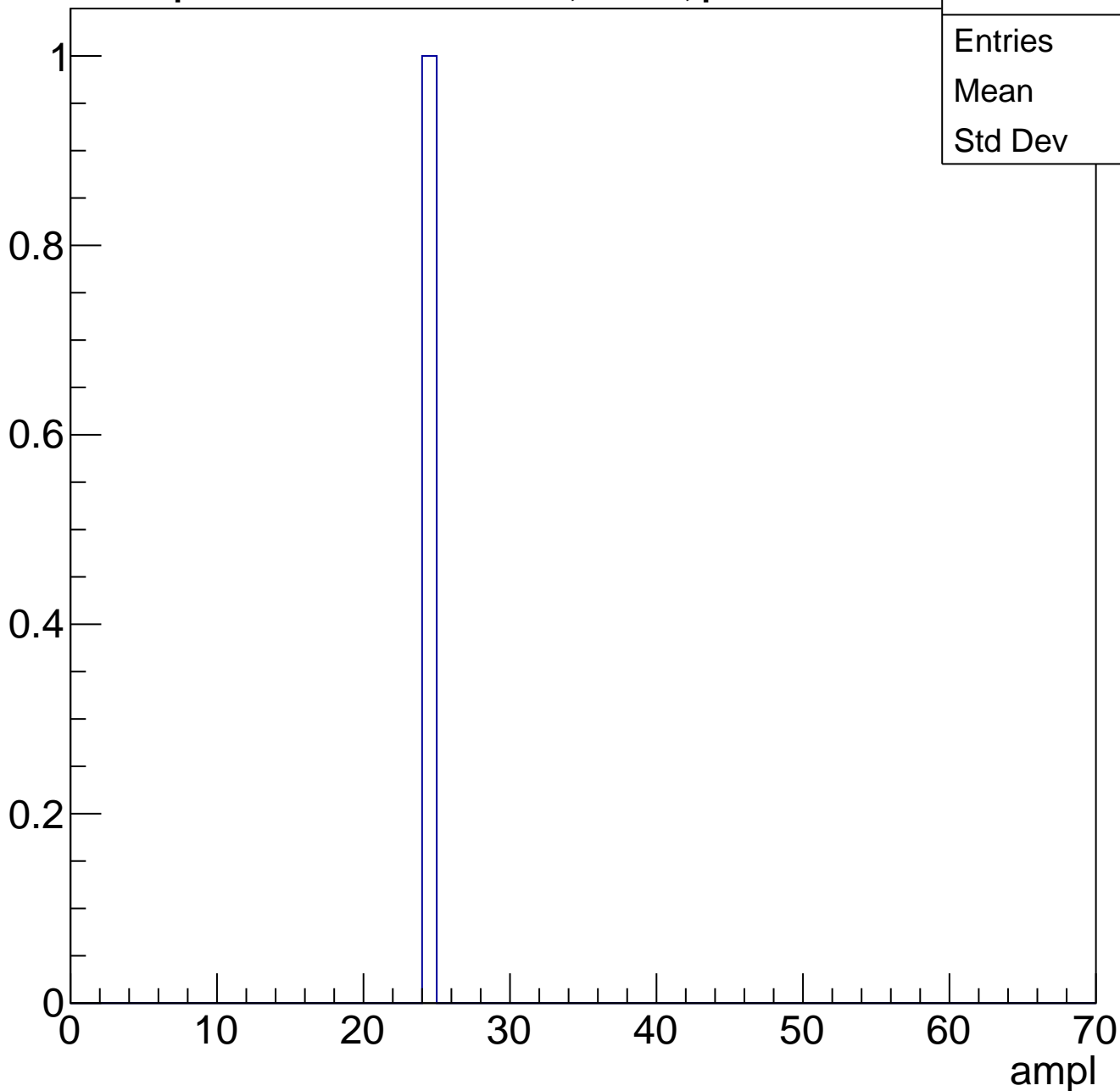




# B1L003S, U23-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch102, adc0

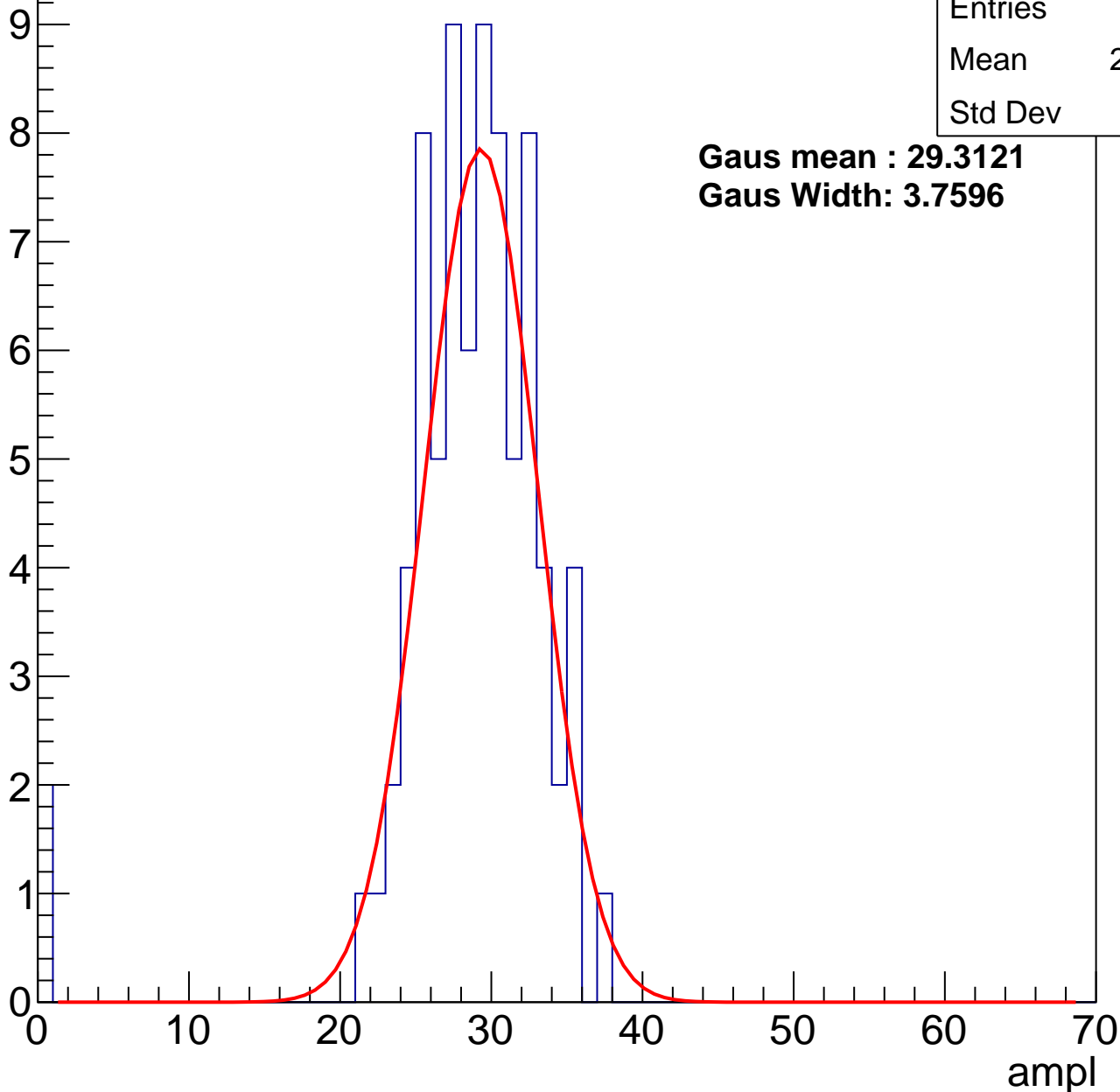
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	28.04
Std Dev	5.66

**Gaus mean : 29.3121**

**Gaus Width: 3.7596**



# B1L003S, U23-ch102, adc1

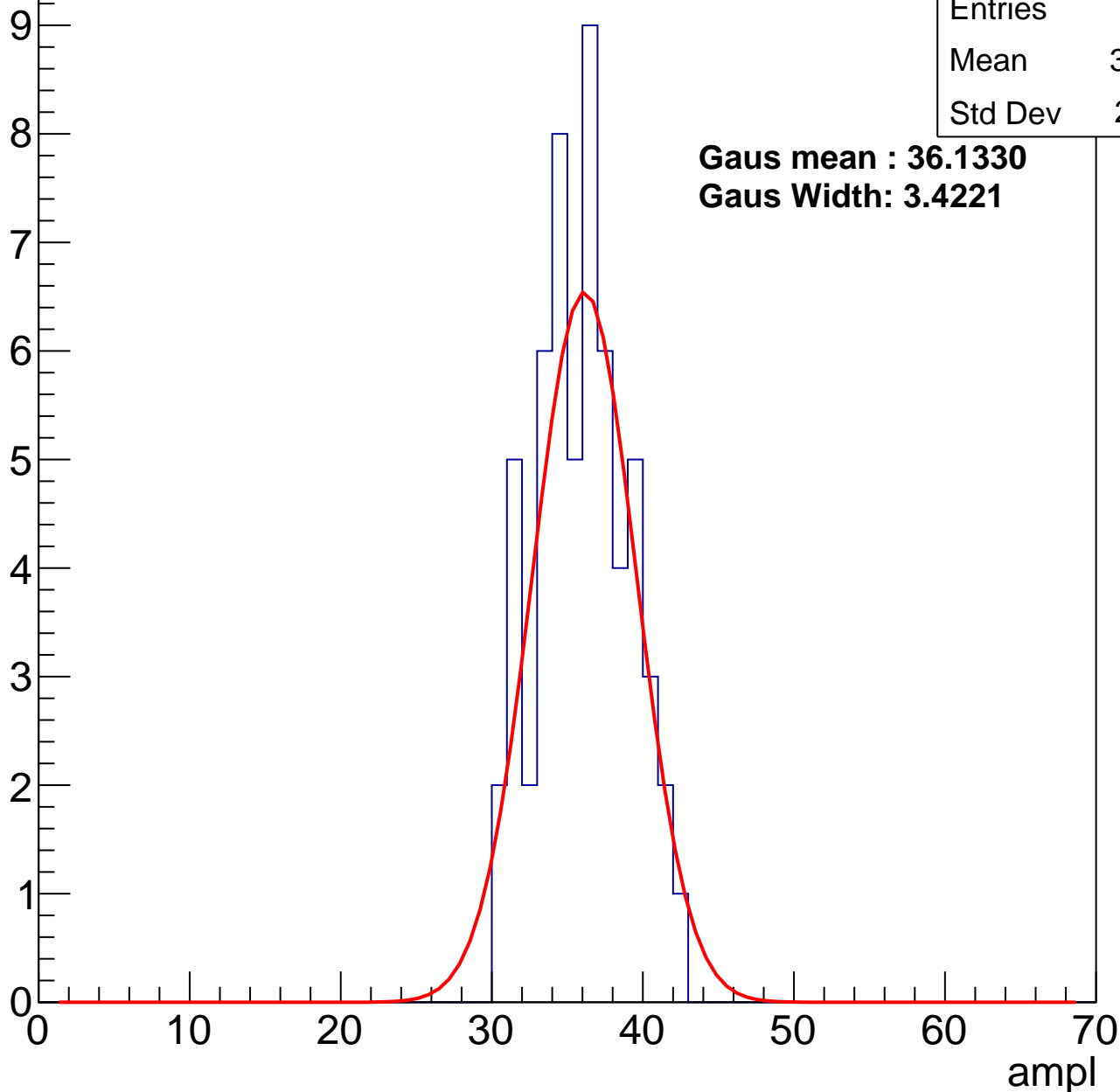
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	35.53
Std Dev	2.961

**Gaus mean : 36.1330**

**Gaus Width: 3.4221**



# B1L003S, U23-ch102, adc2

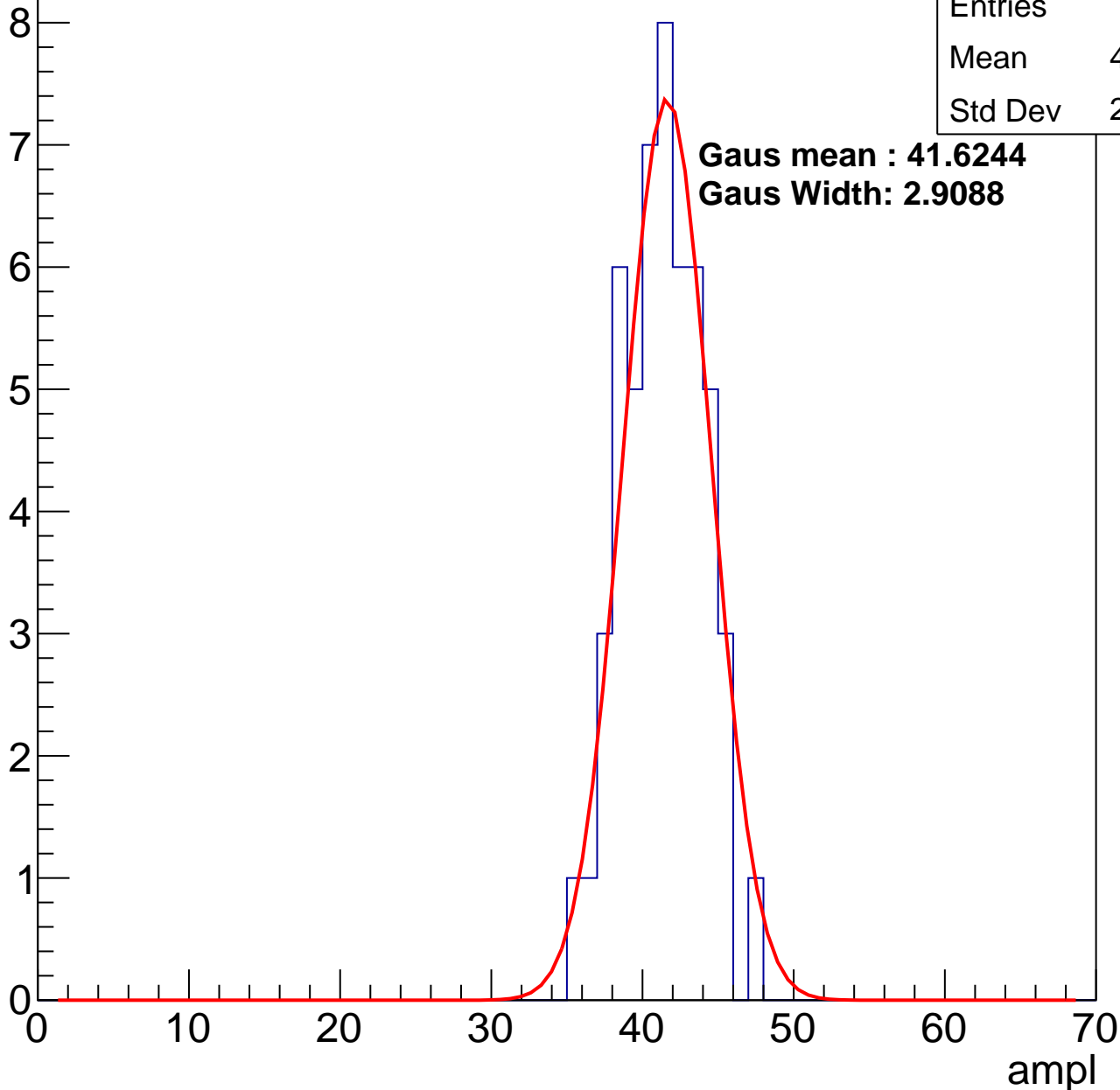
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	40.87
Std Dev	2.587

**Gaus mean : 41.6244**

**Gaus Width: 2.9088**

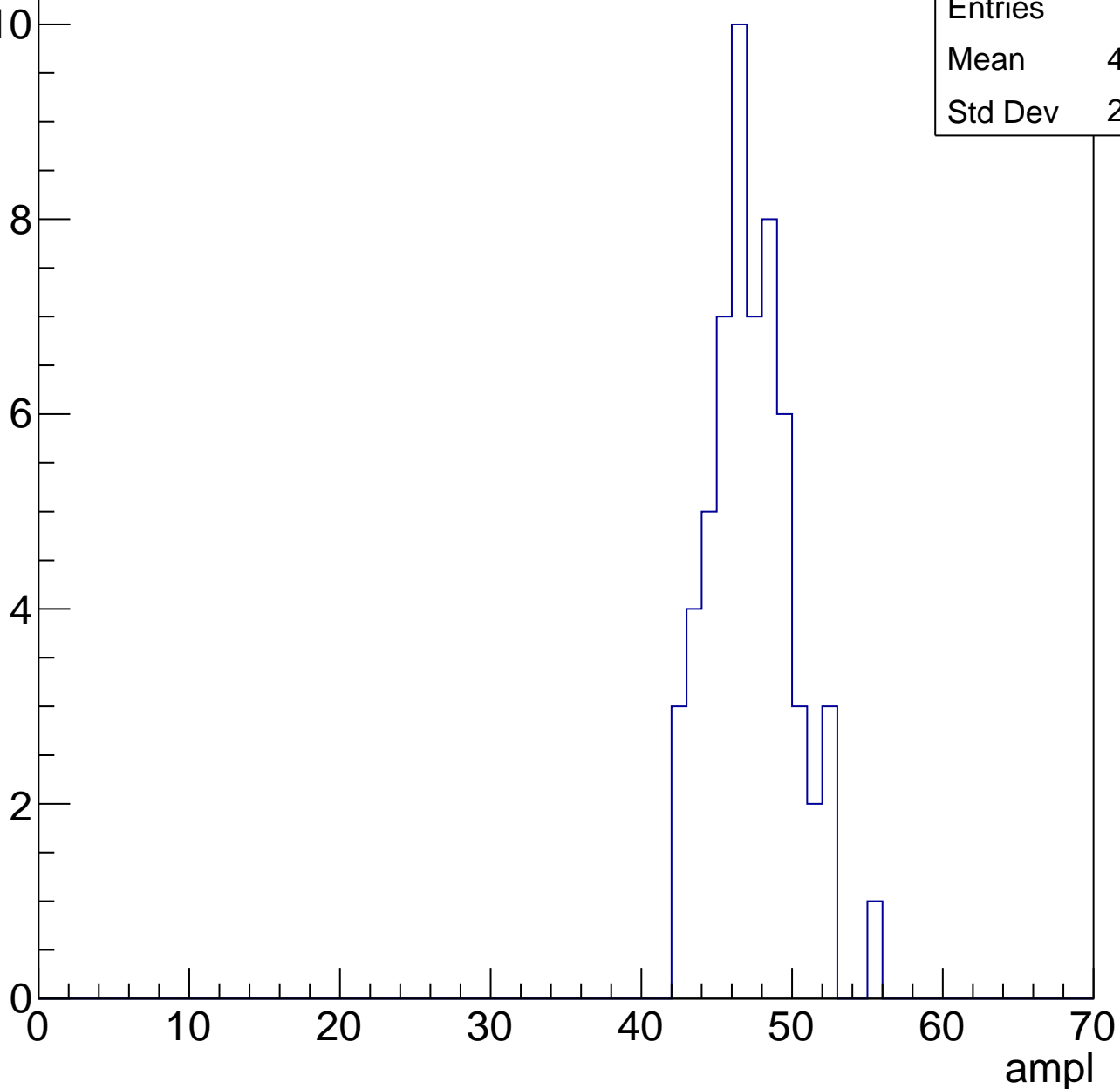


# B1L003S, U23-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	46.83
Std Dev	2.763

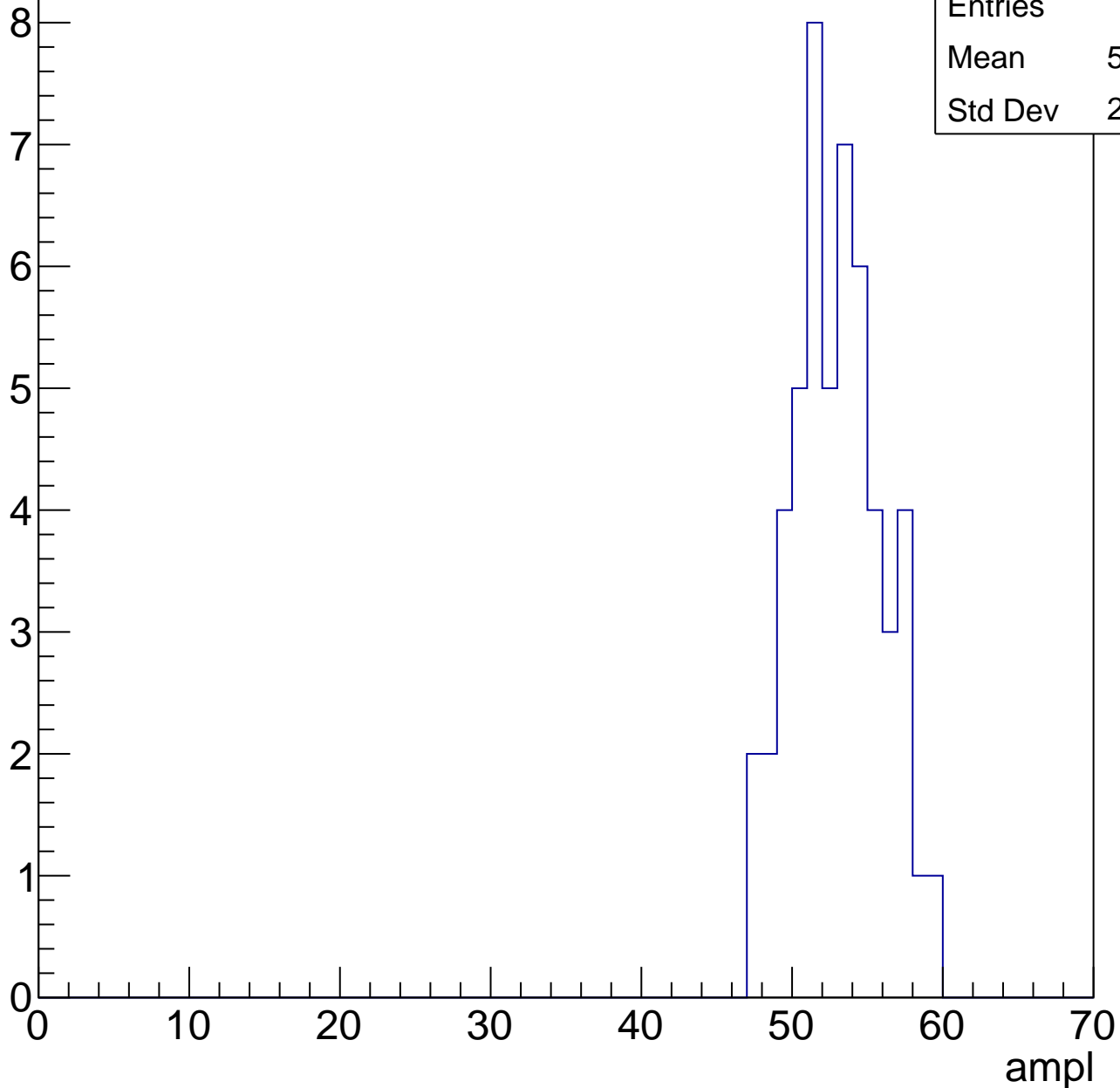


# B1L003S, U23-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	52.54
Std Dev	2.879

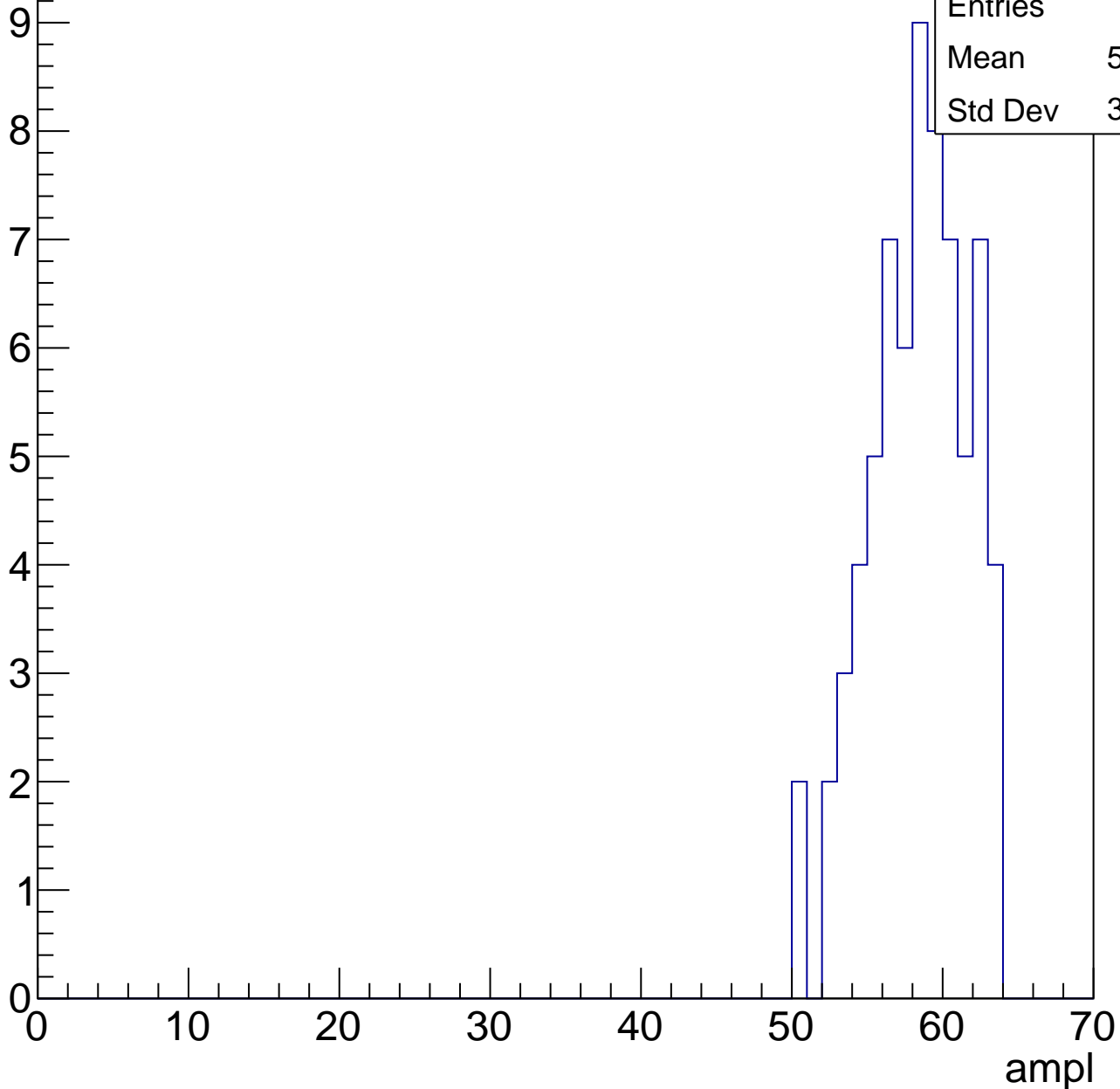


# B1L003S, U23-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	57.87
Std Dev	3.207

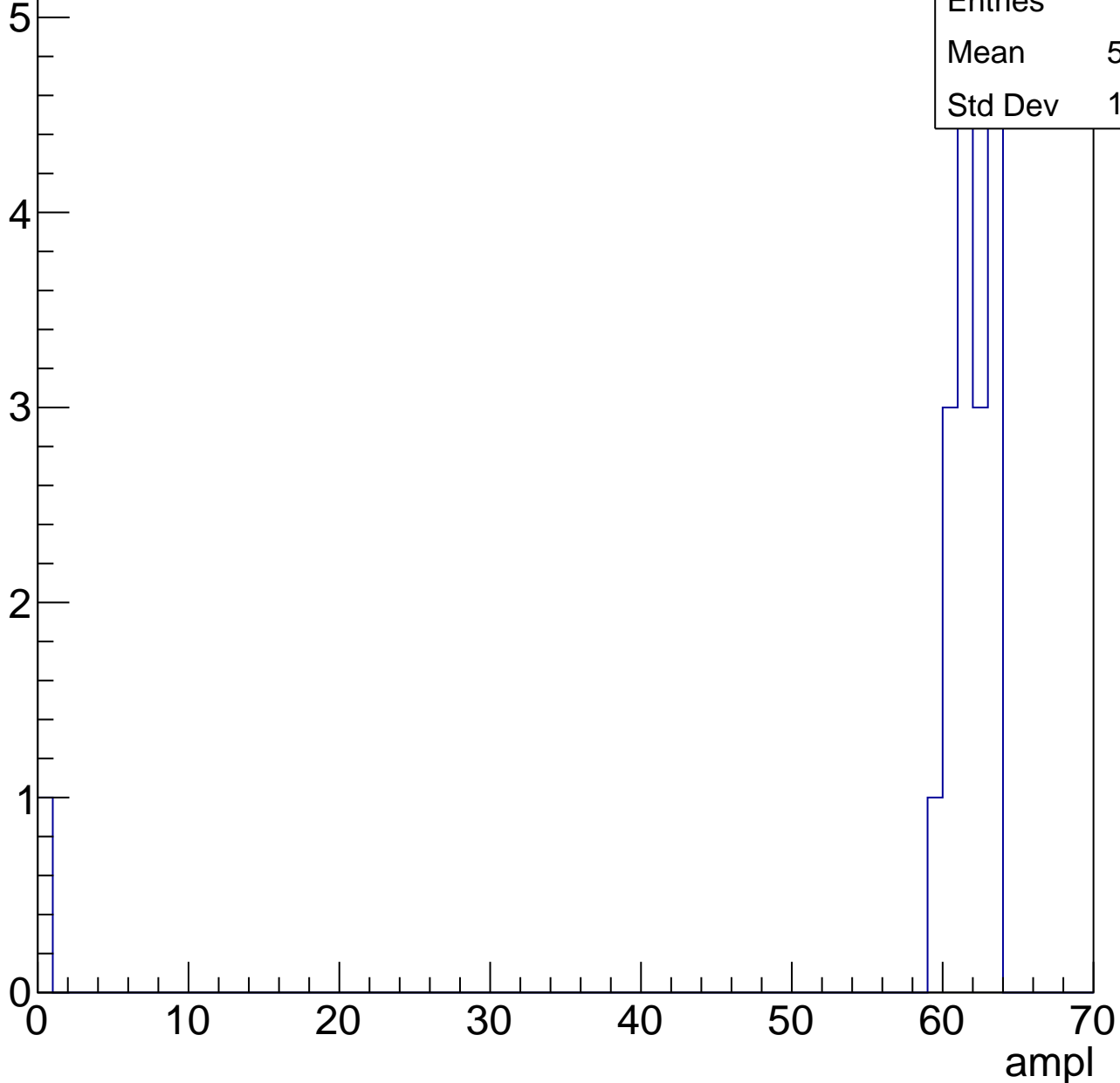


# B1L003S, U23-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	18
Mean	58.06
Std Dev	14.13





# B1L003S, U23-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch103, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	28.3
Std Dev	6.085

**Gaus mean : 29.8861**

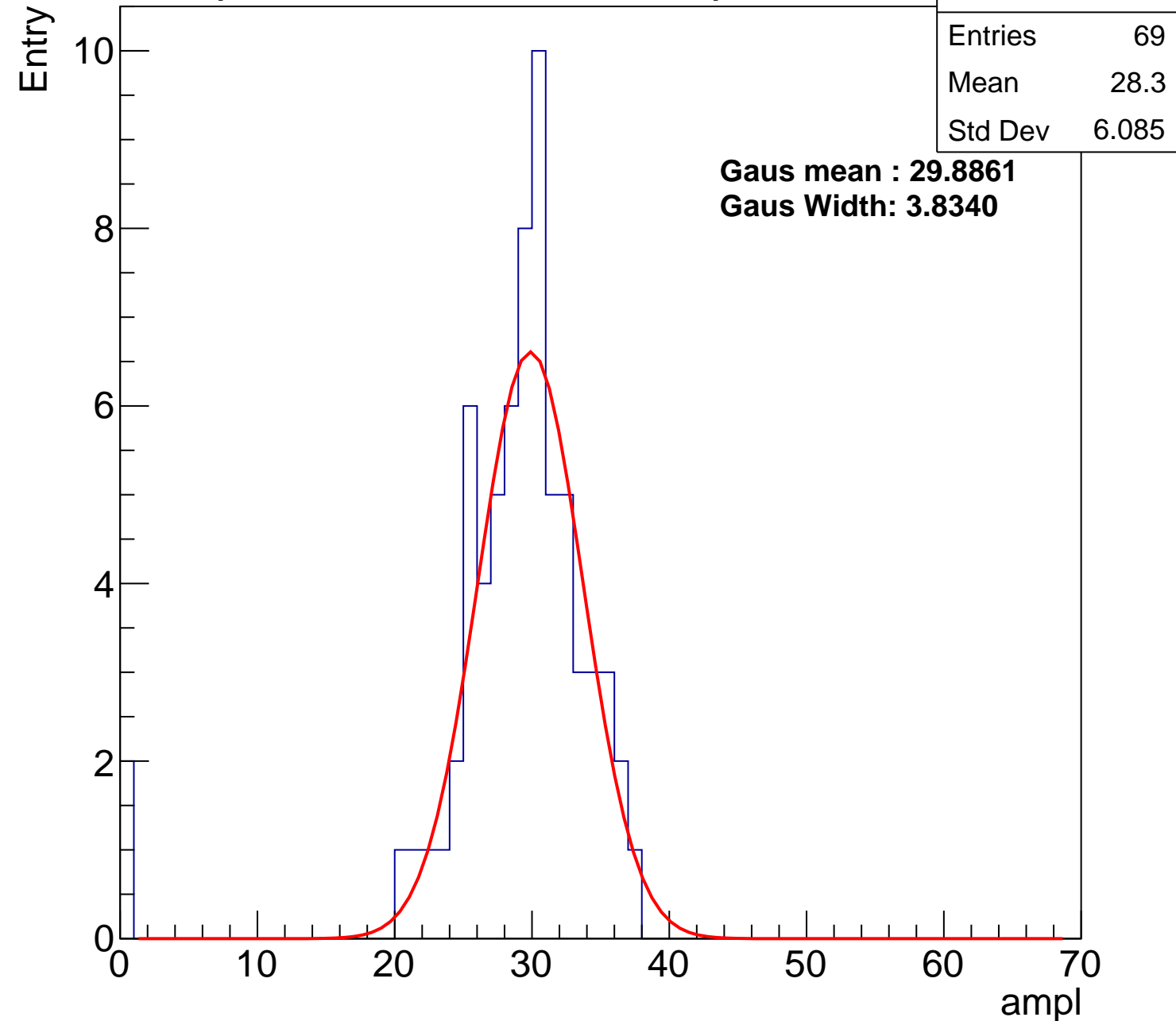
**Gaus Width: 3.8340**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch103, adc1

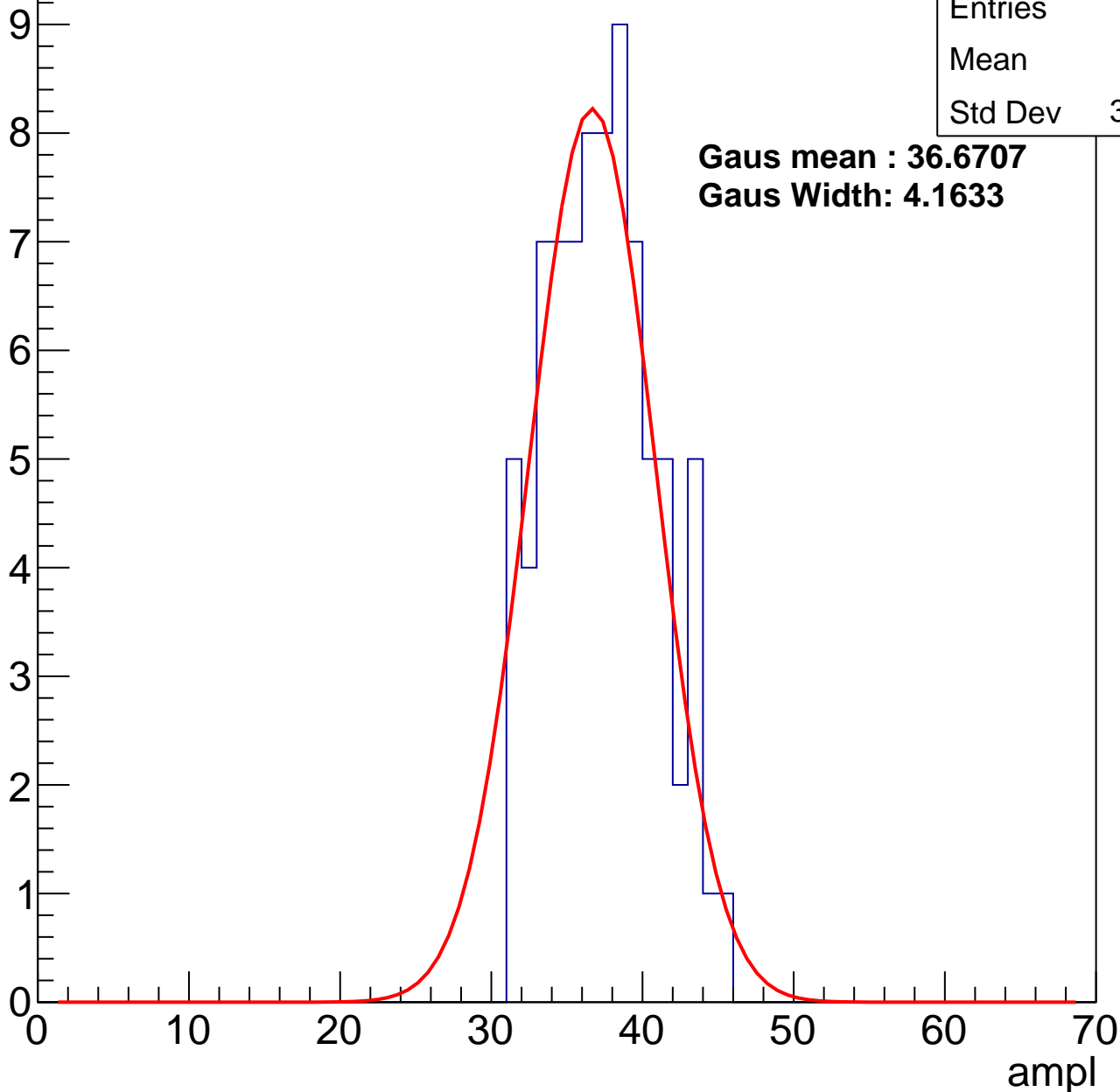
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	81
Mean	36.9
Std Dev	3.505

**Gaus mean : 36.6707**

**Gaus Width: 4.1633**



# B1L003S, U23-ch103, adc2

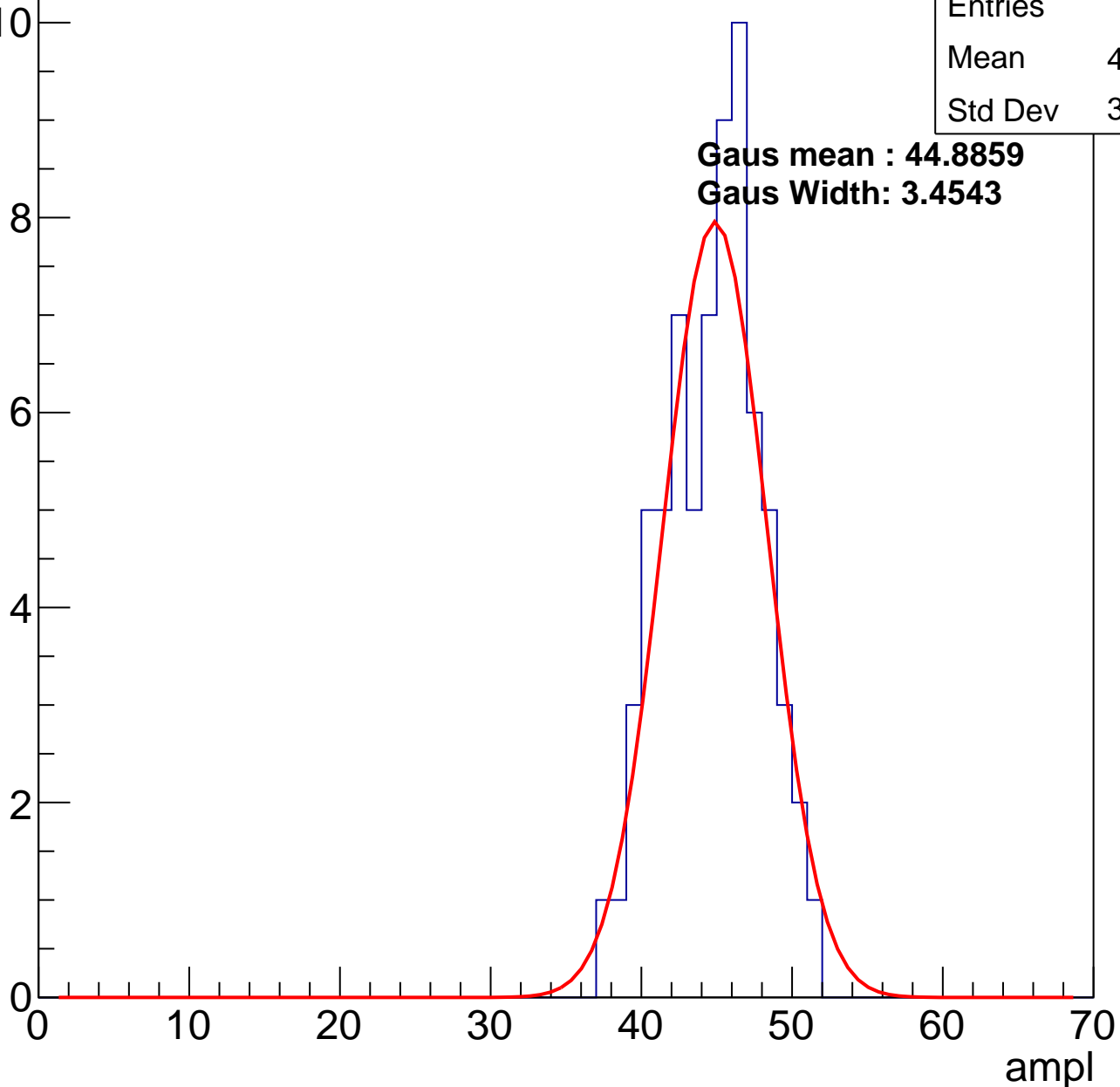
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	70
Mean	44.27
Std Dev	3.144

**Gaus mean : 44.8859**

**Gaus Width: 3.4543**

Entry

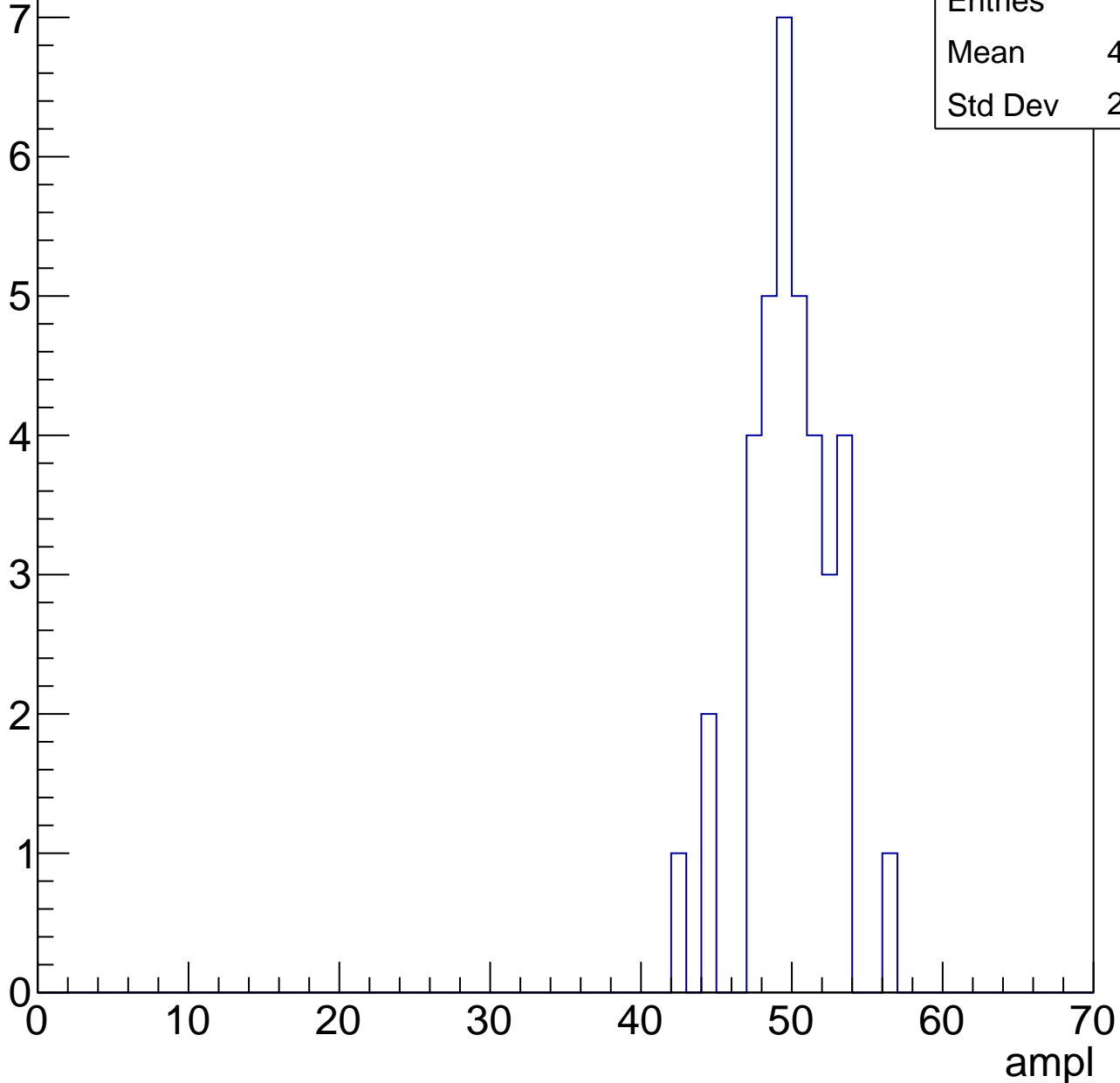


# B1L003S, U23-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	36
Mean	49.42
Std Dev	2.763

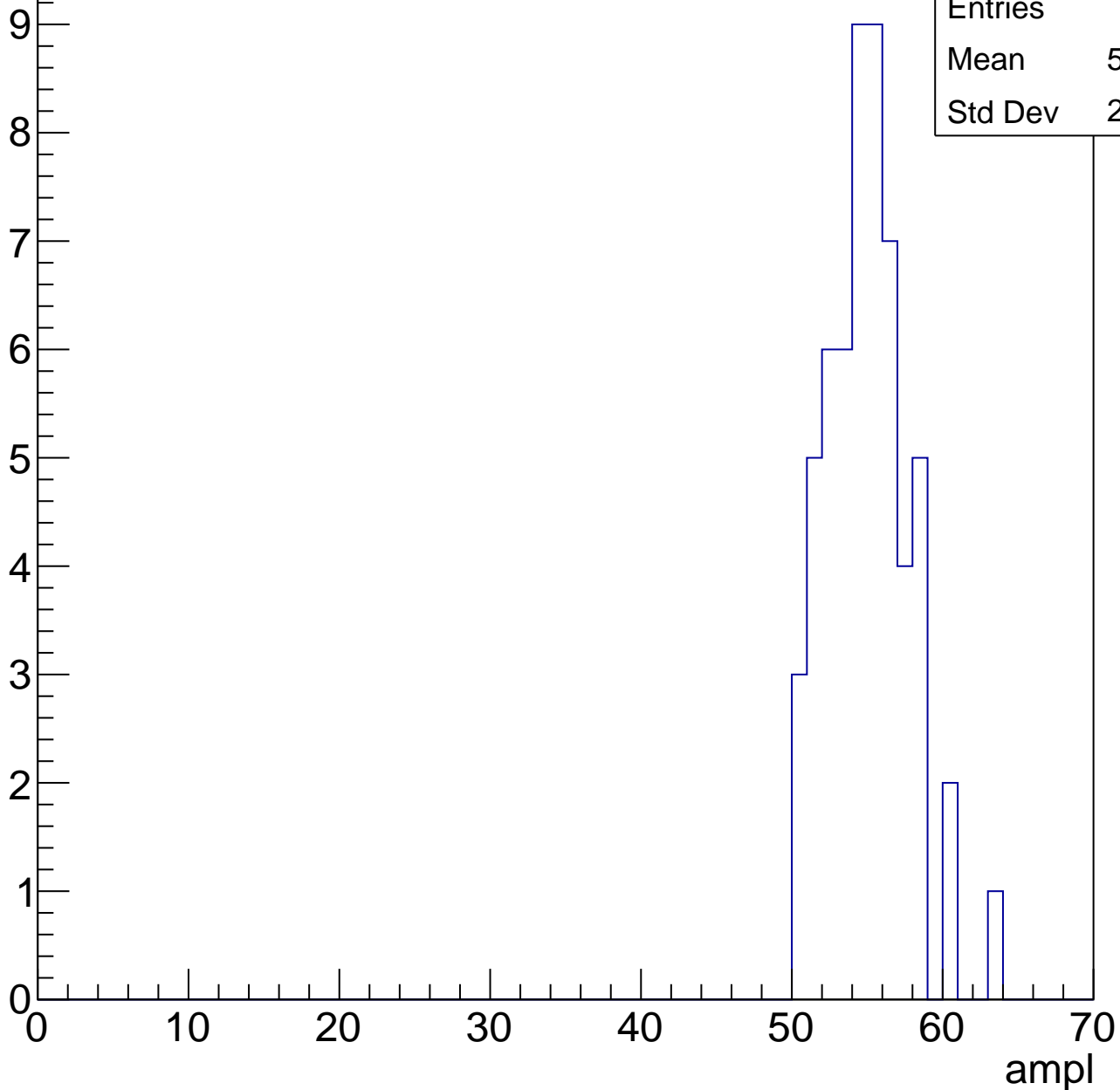


# B1L003S, U23-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	54.54
Std Dev	2.689

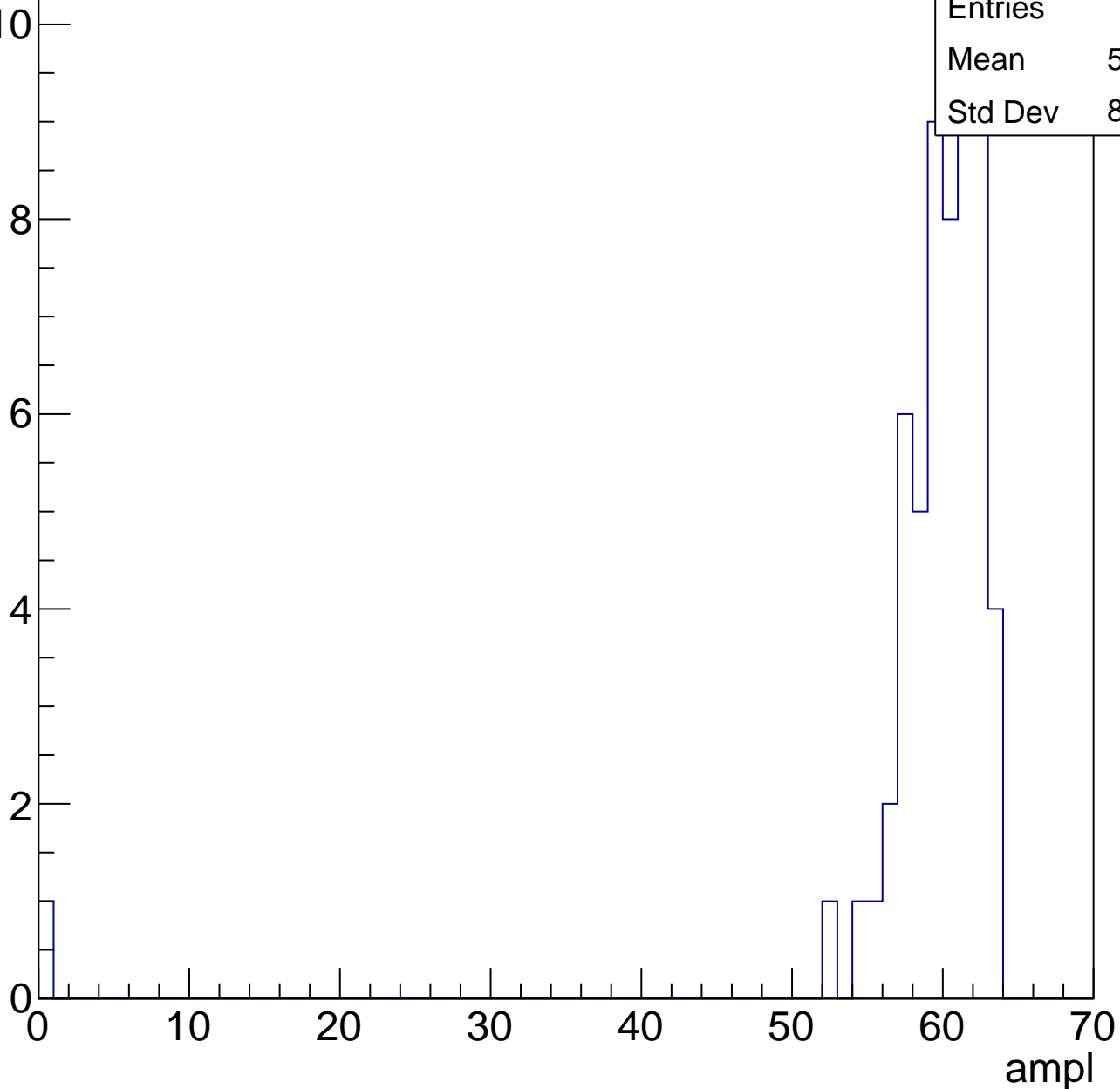


# B1L003S, U23-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

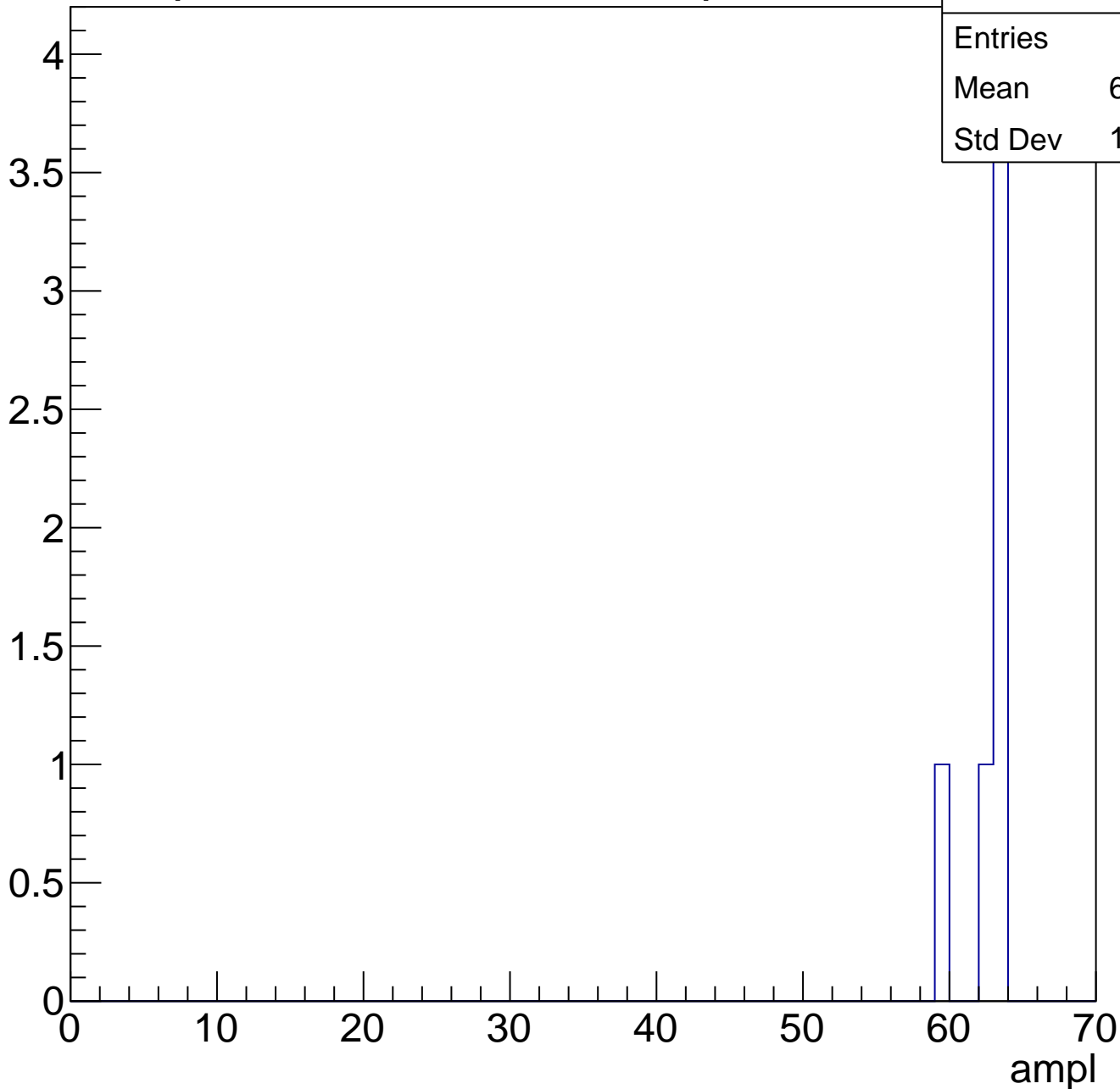
Entries	57
Mean	58.53
Std Dev	8.163



# B1L003S, U23-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch104, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	79
Mean	27.67
Std Dev	4.781

**Gaus mean : 28.1730**

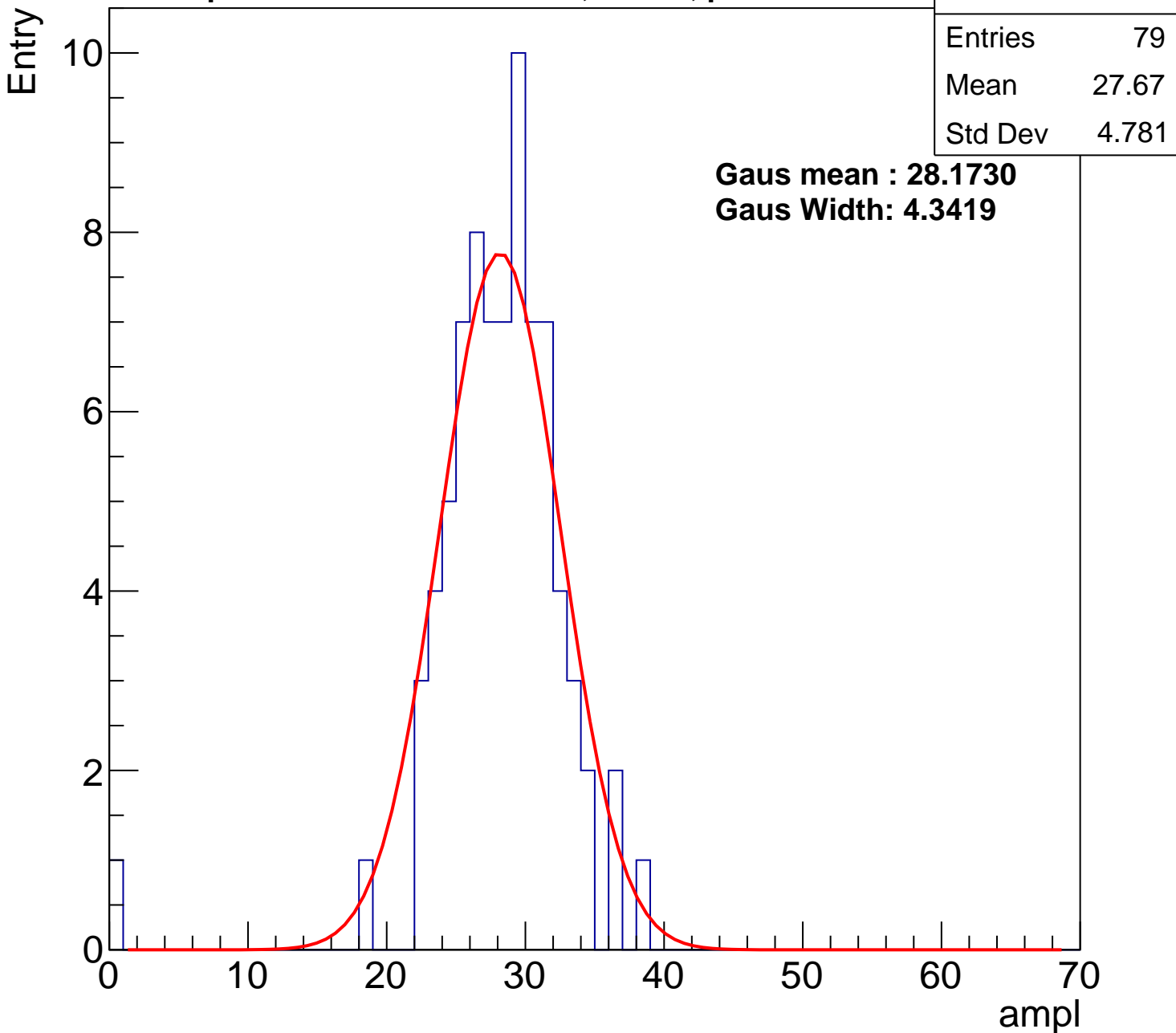
**Gaus Width: 4.3419**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	72
Mean	35.53
Std Dev	2.867

**Gaus mean : 36.0091**

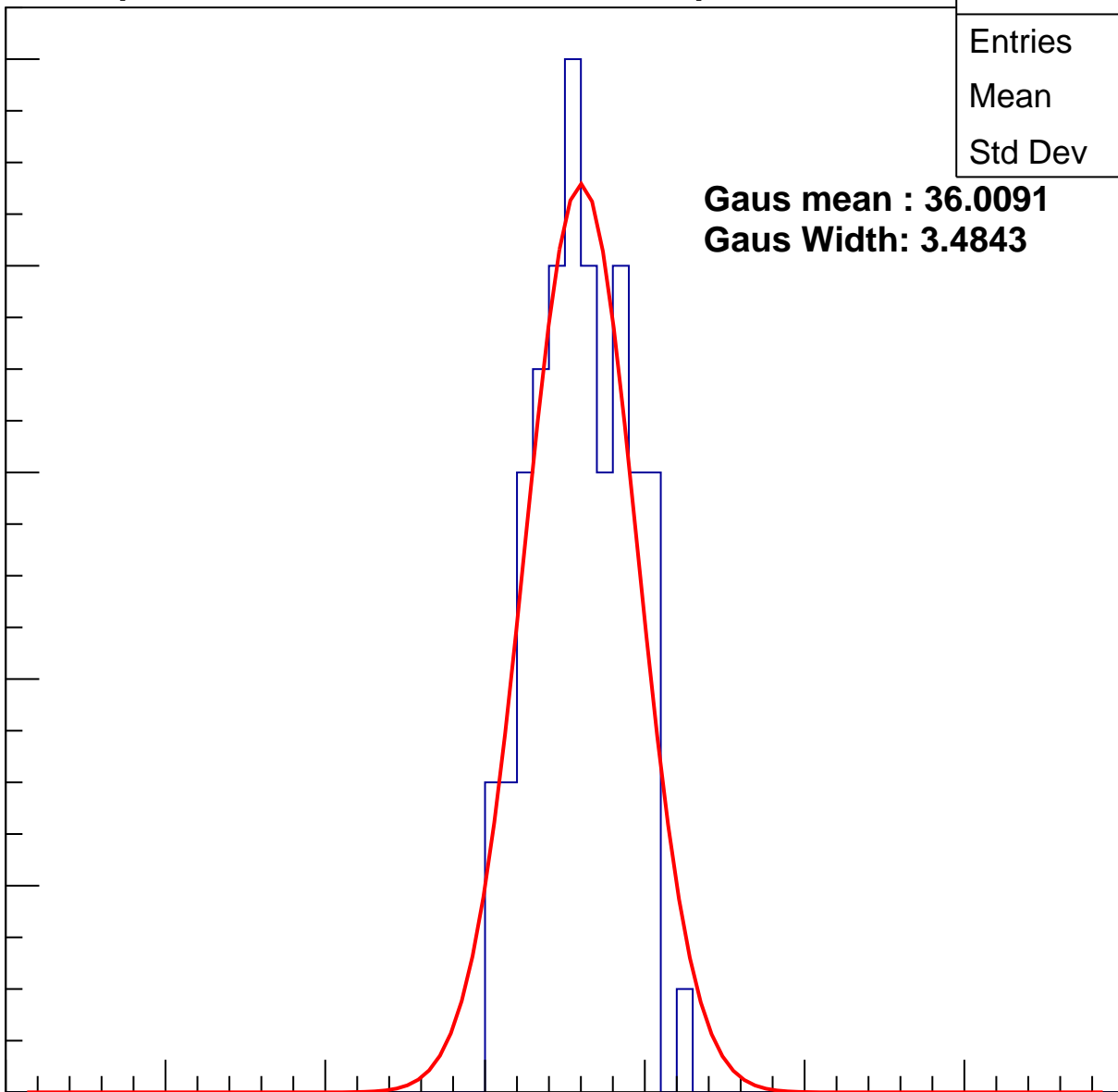
**Gaus Width: 3.4843**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U23-ch104, adc2

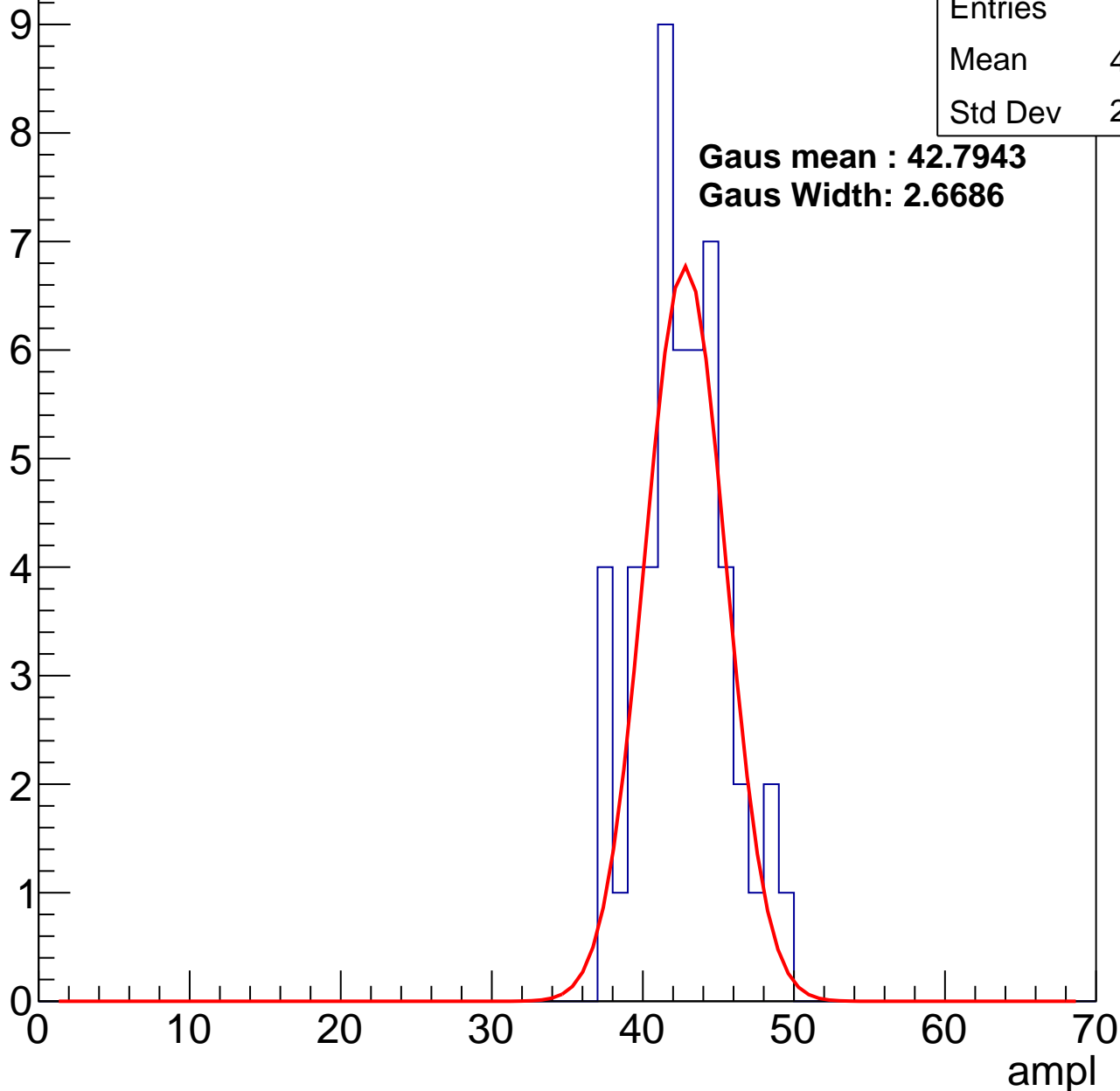
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	42.22
Std Dev	2.879

**Gaus mean : 42.7943**

**Gaus Width: 2.6686**

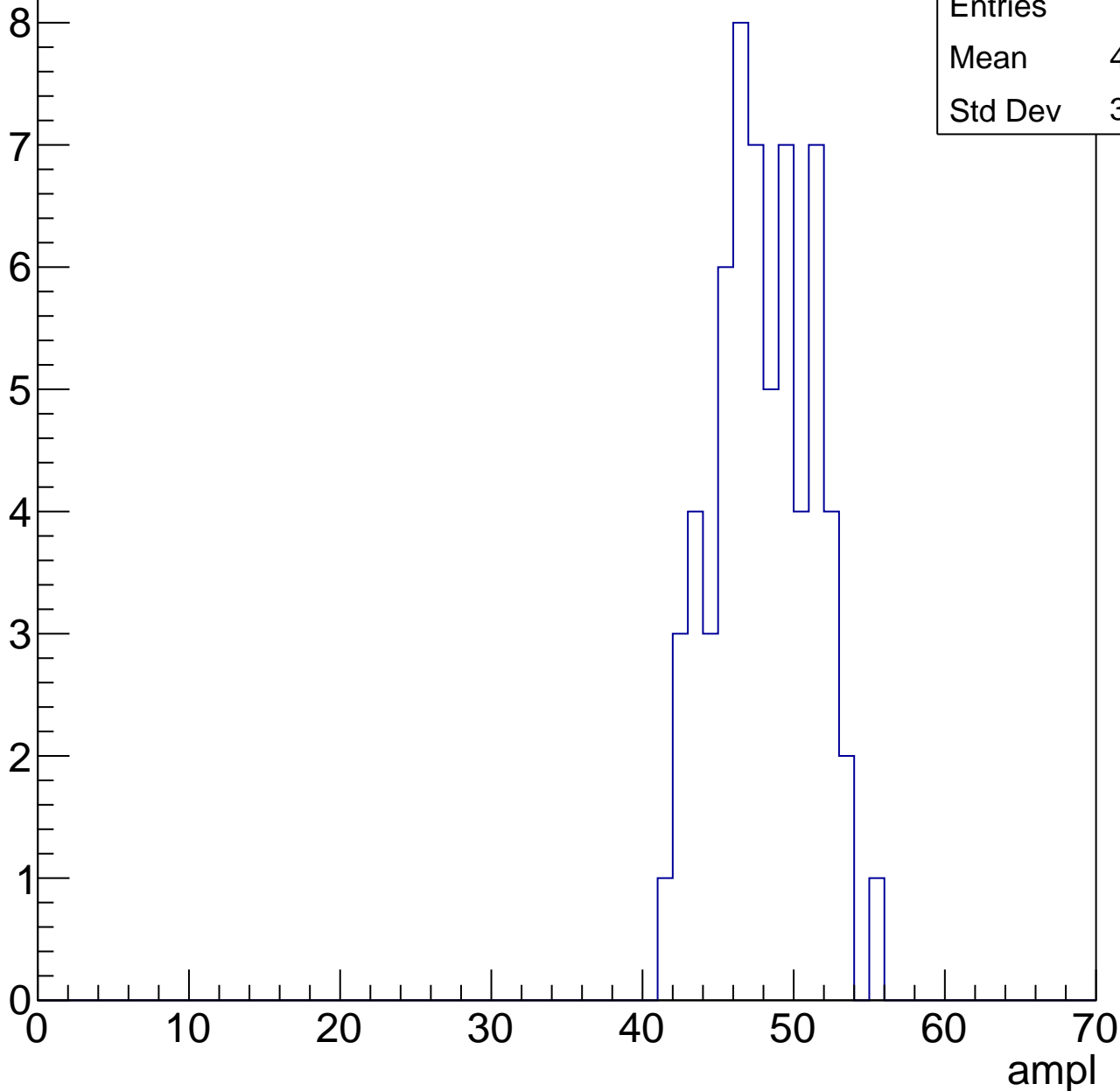


# B1L003S, U23-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	47.53
Std Dev	3.196

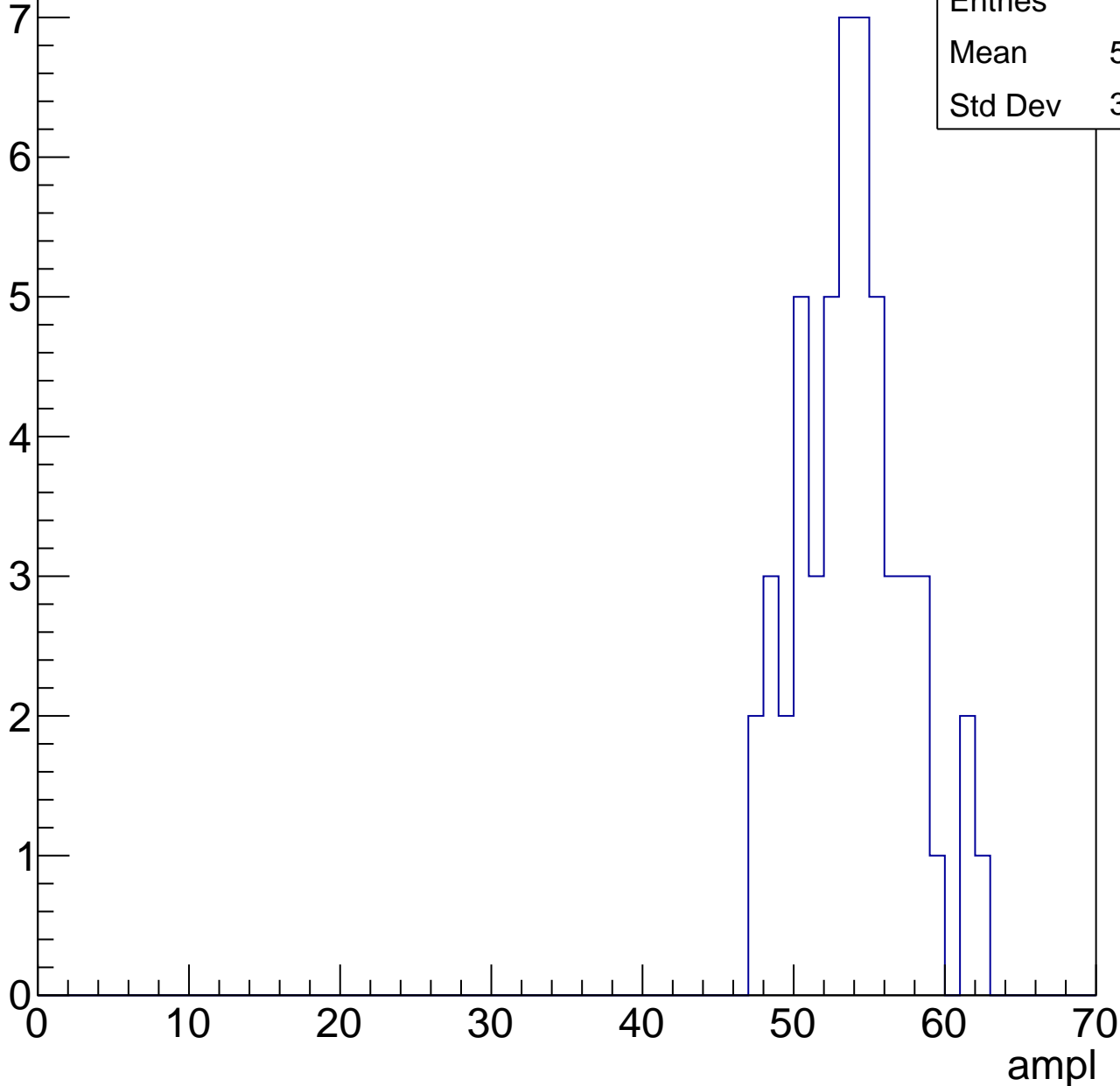


# B1L003S, U23-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	53.44
Std Dev	3.543

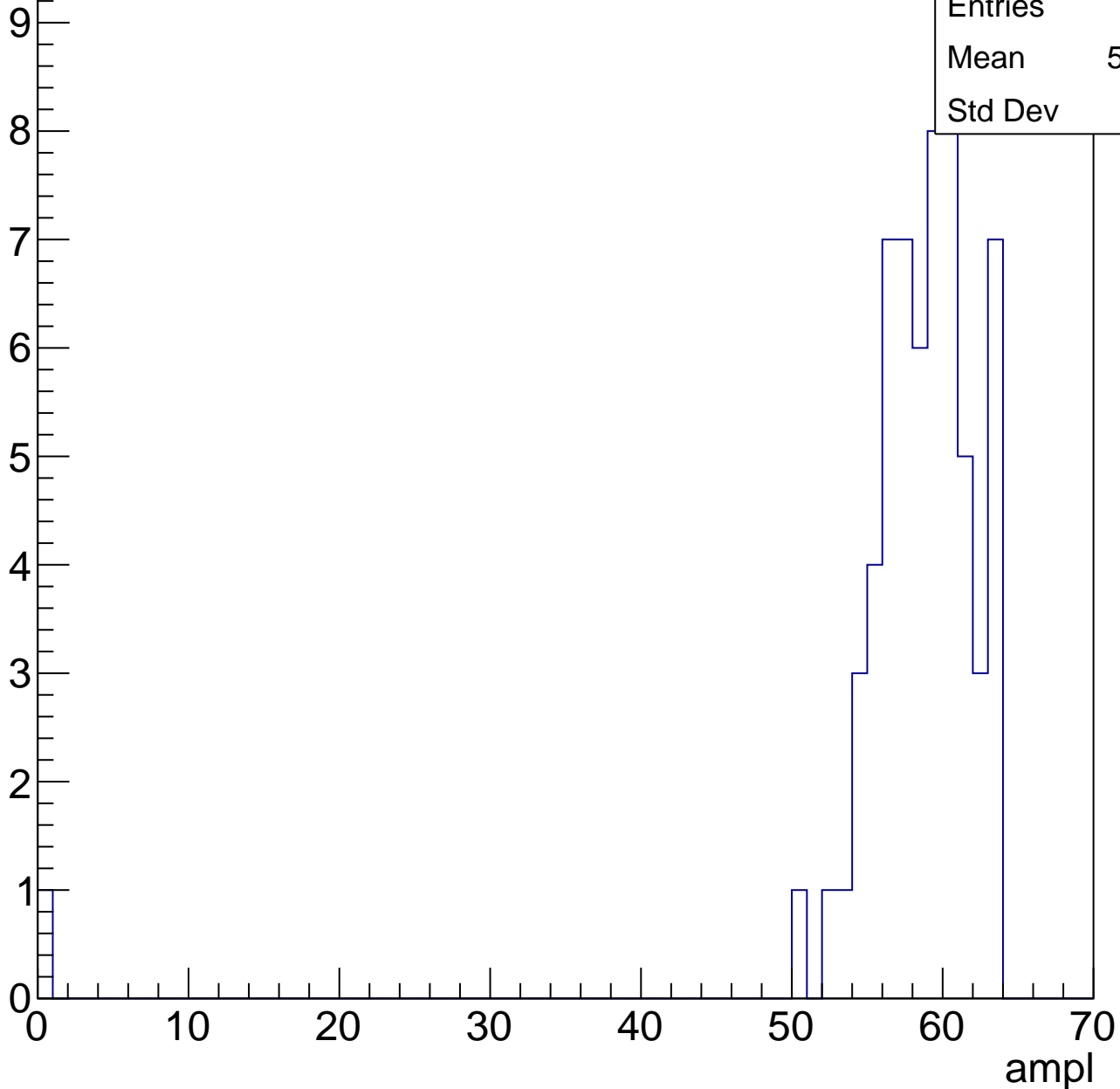


# B1L003S, U23-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	57.46
Std Dev	7.87

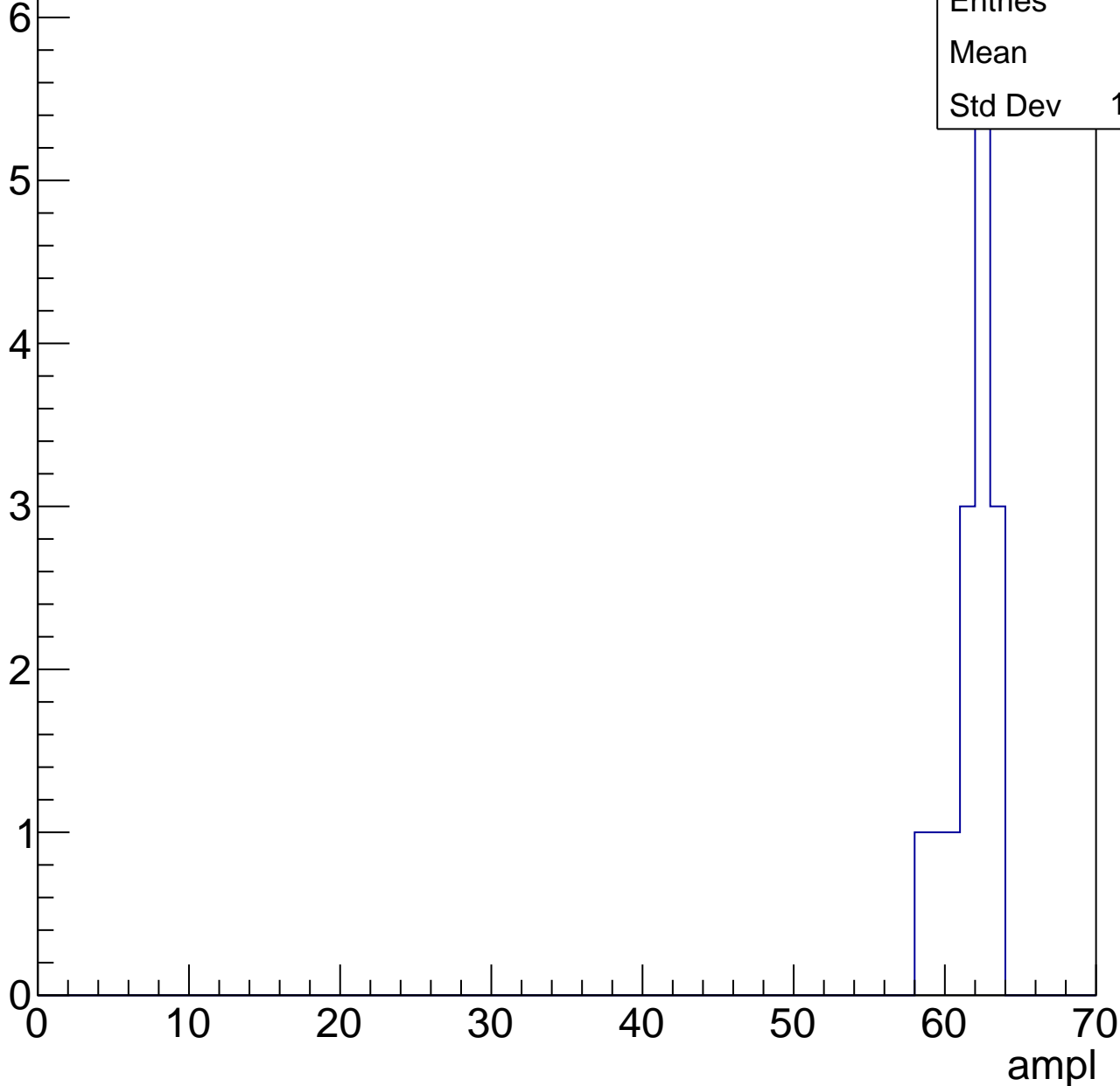


# B1L003S, U23-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.4
Std Dev	1.405





# B1L003S, U23-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch105, adc0

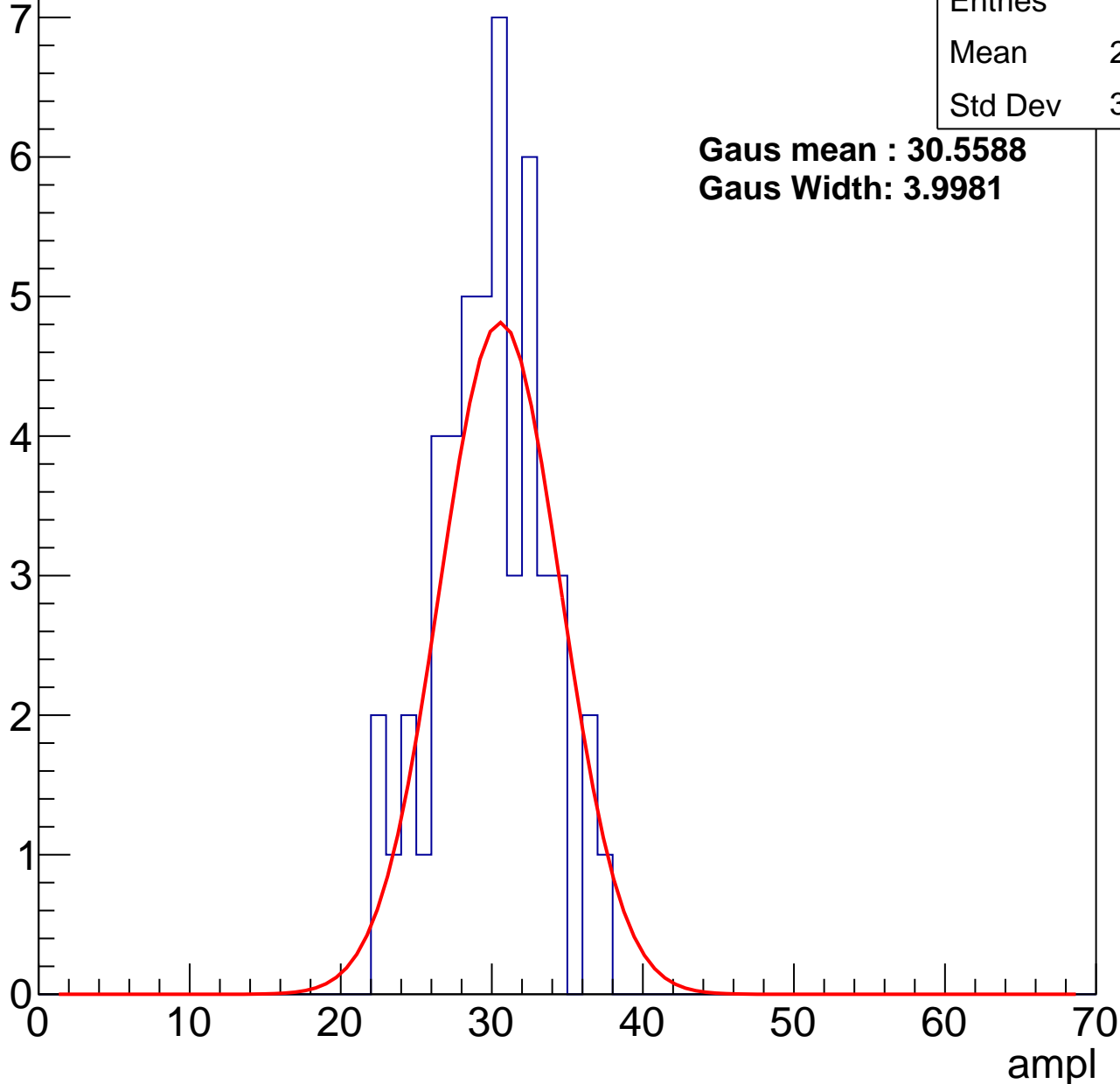
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	29.43
Std Dev	3.517

**Gaus mean : 30.5588**

**Gaus Width: 3.9981**



# B1L003S, U23-ch105, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	79
Mean	35.06
Std Dev	5.328

**Gaus mean : 36.2722**

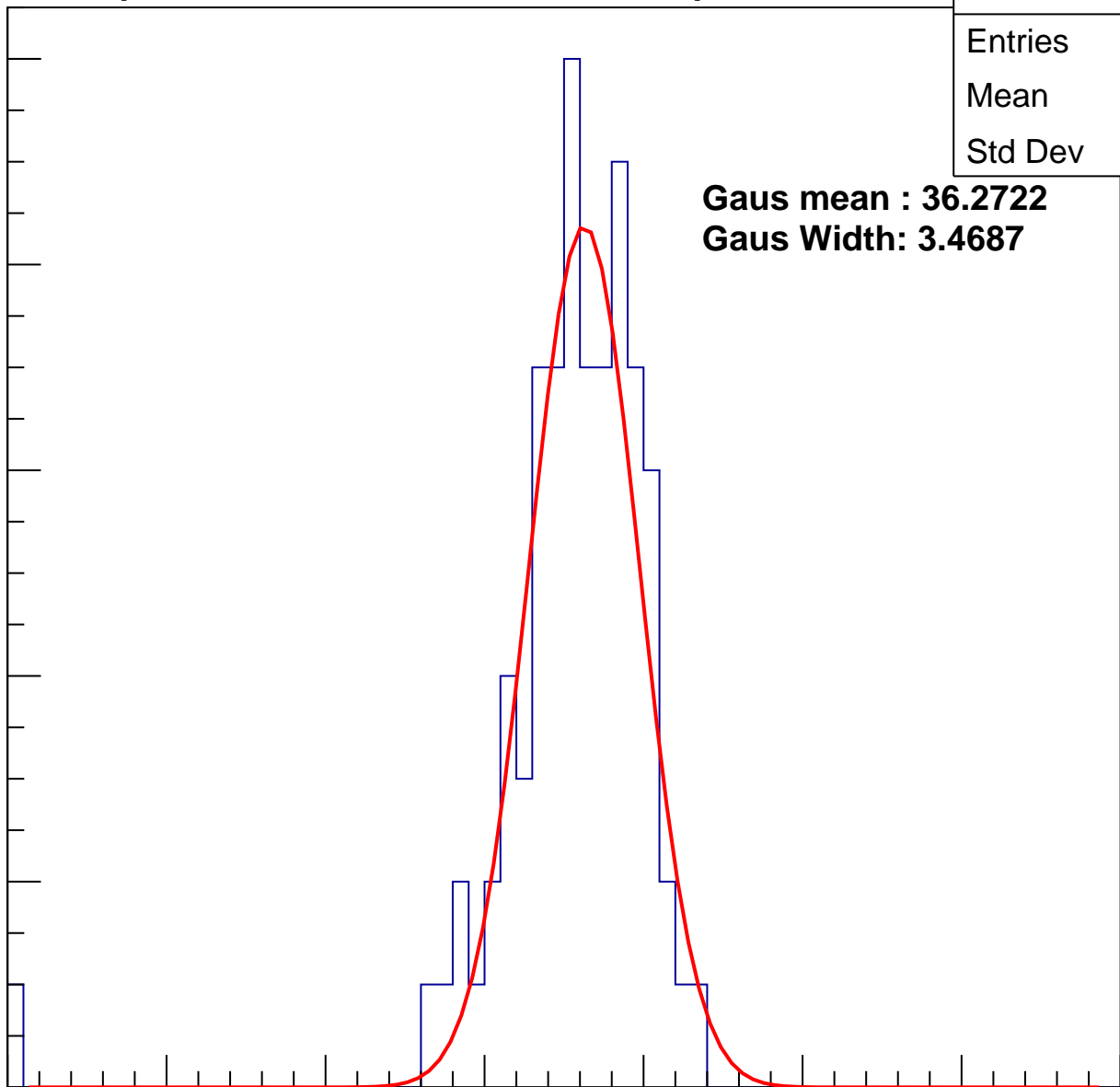
**Gaus Width: 3.4687**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch105, adc2

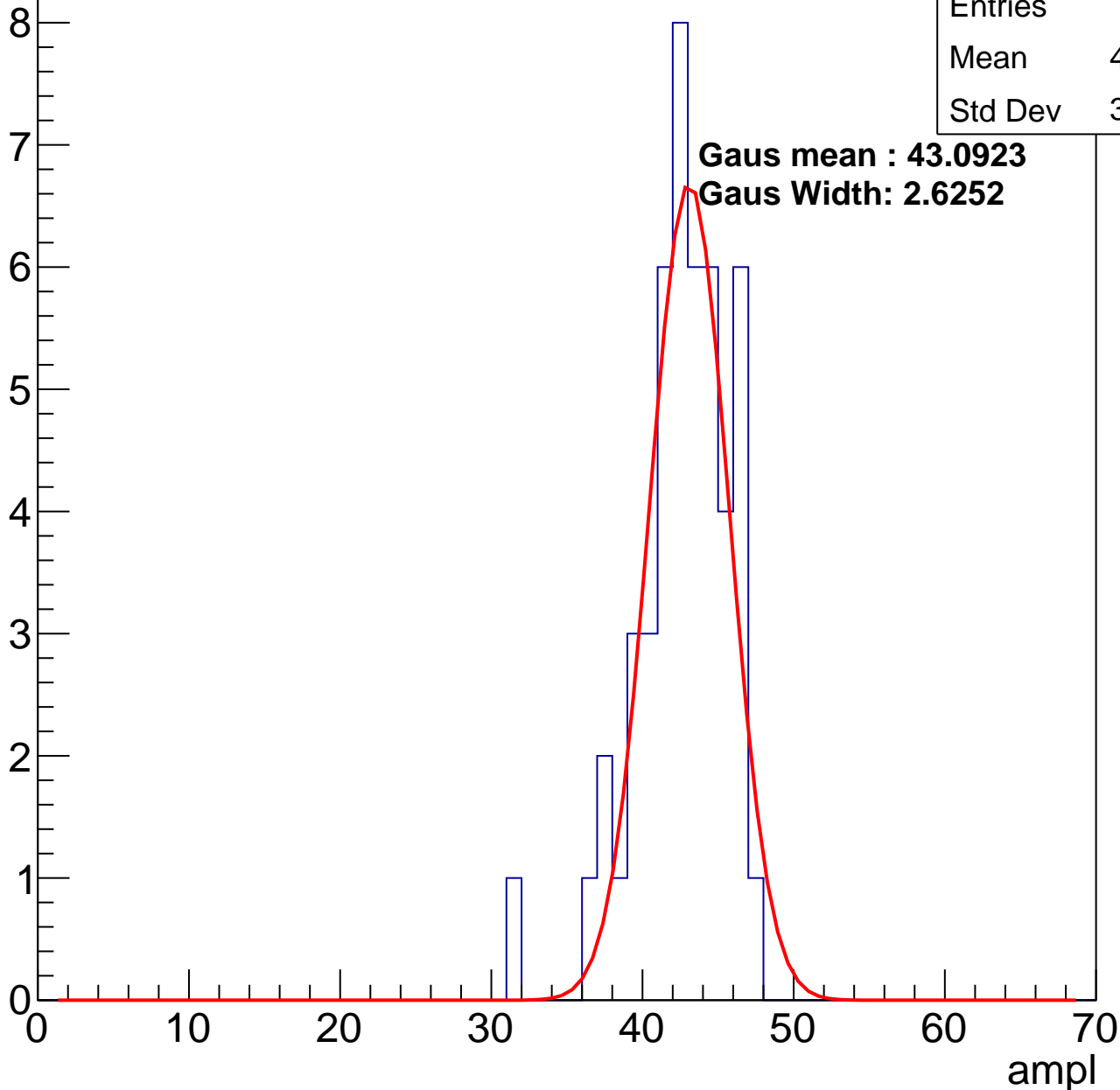
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	42.15
Std Dev	3.075

**Gaus mean : 43.0923**

**Gaus Width: 2.6252**

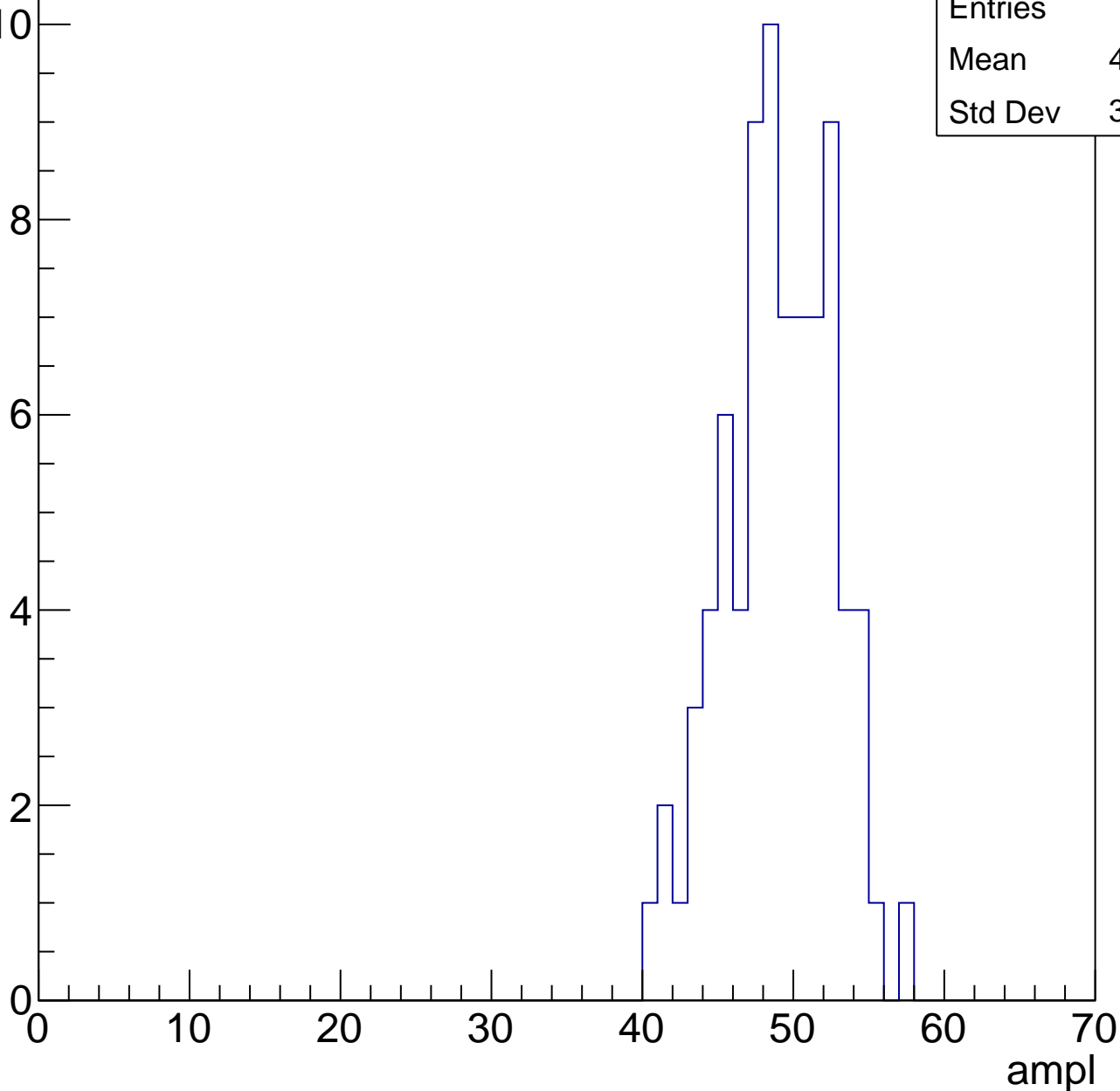


# B1L003S, U23-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	48.55
Std Dev	3.549

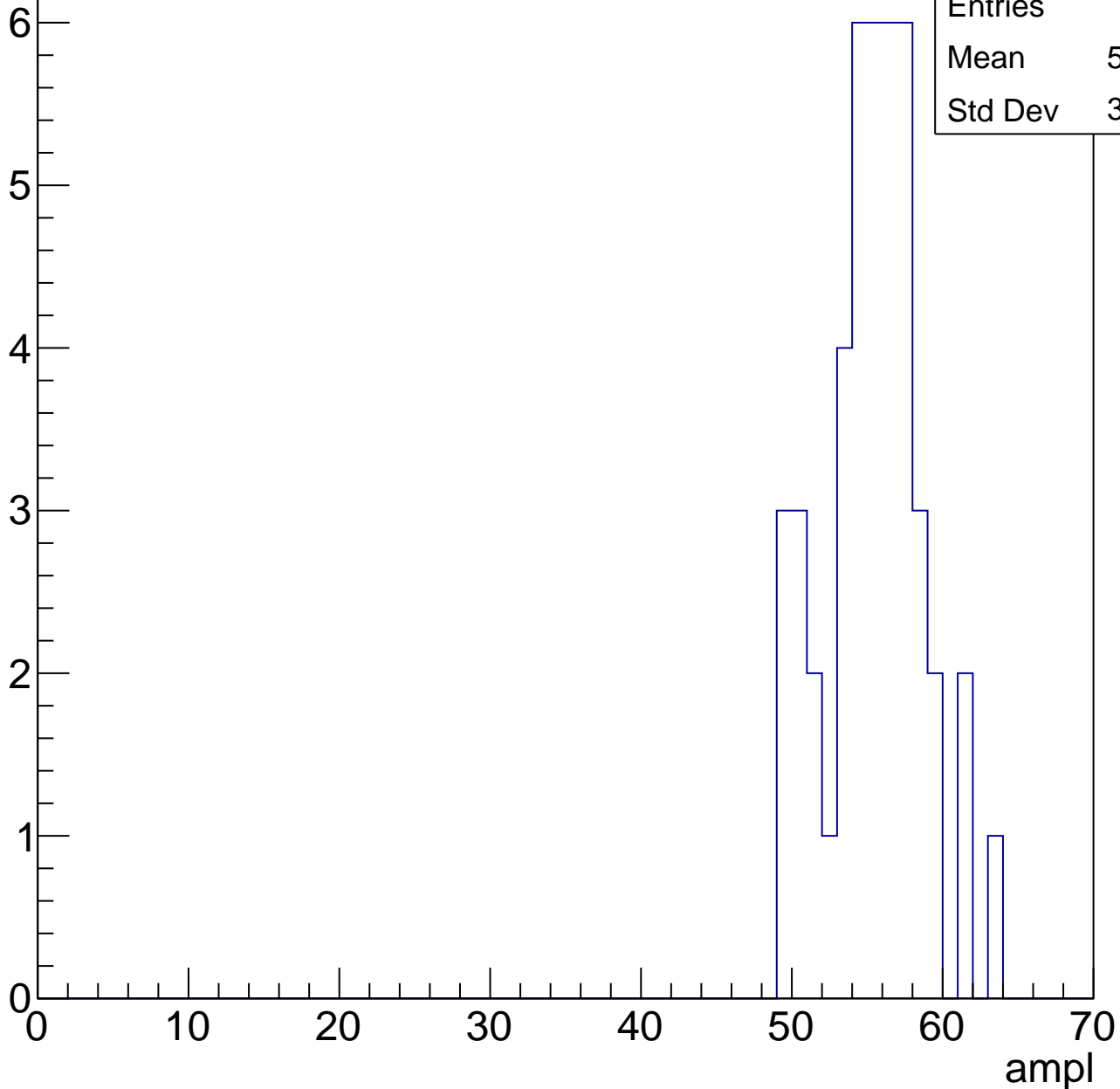


# B1L003S, U23-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	54.93
Std Dev	3.235

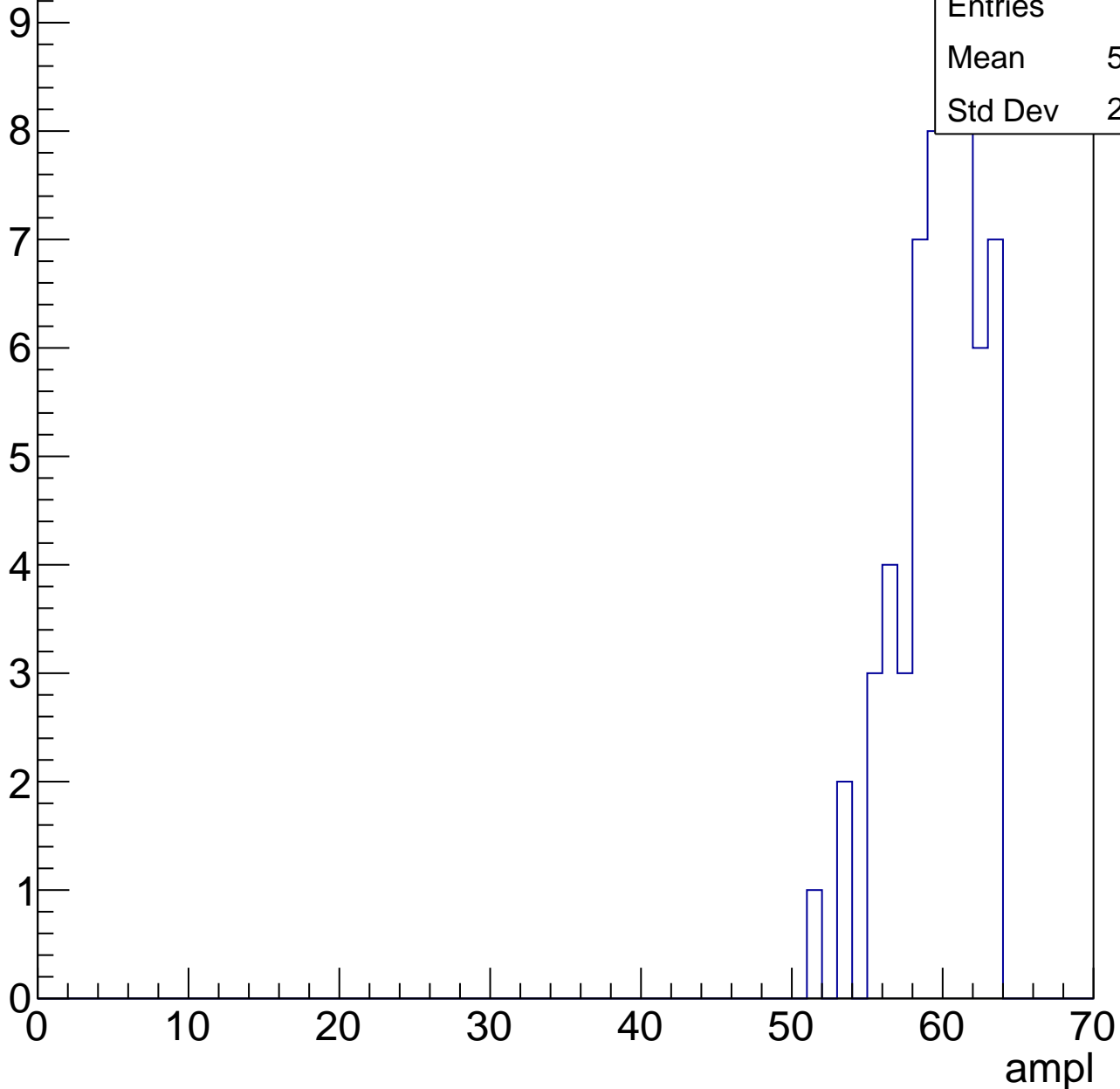


# B1L003S, U23-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

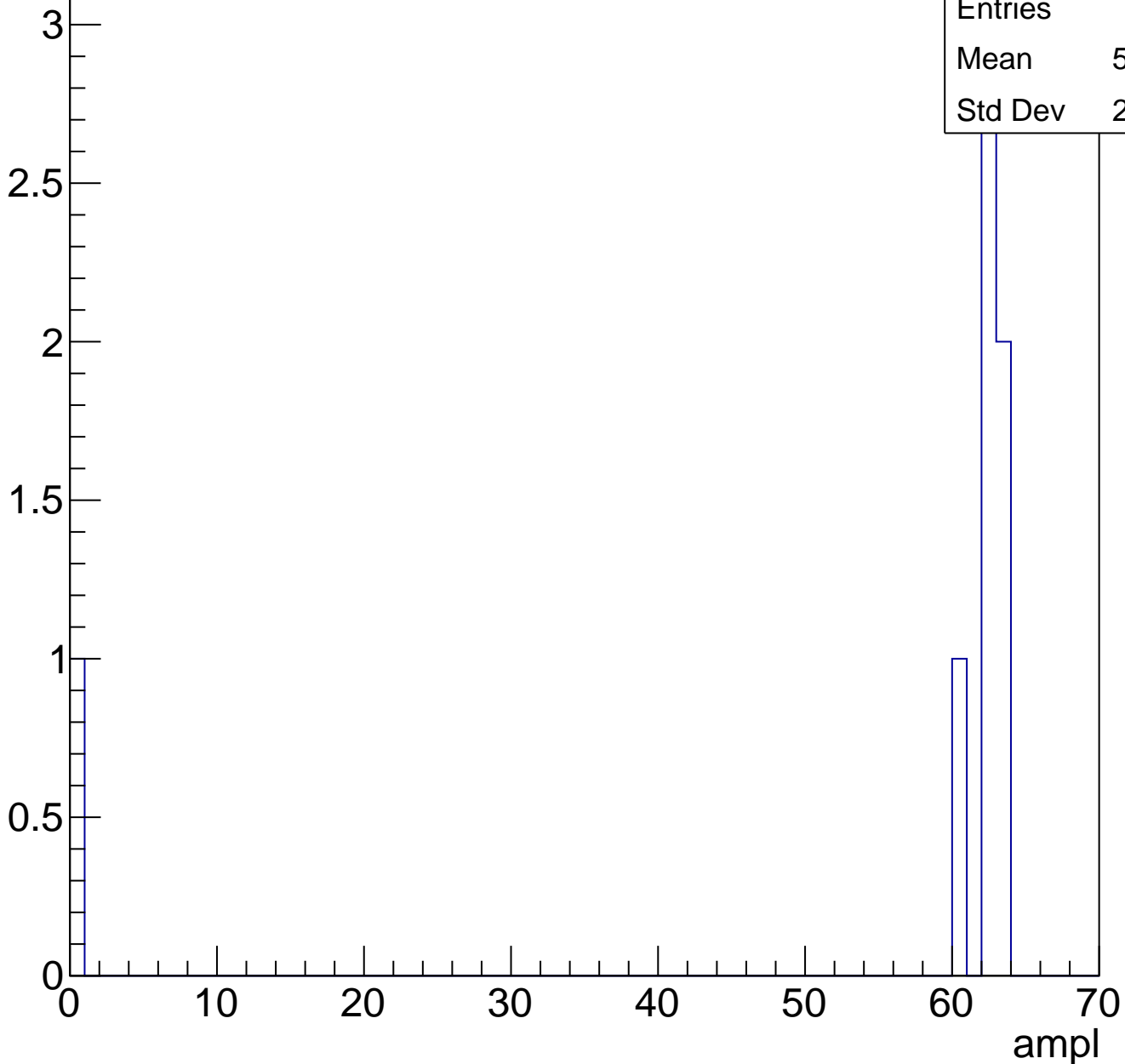
Entries	58
Mean	59.24
Std Dev	2.762



# B1L003S, U23-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch106, adc0

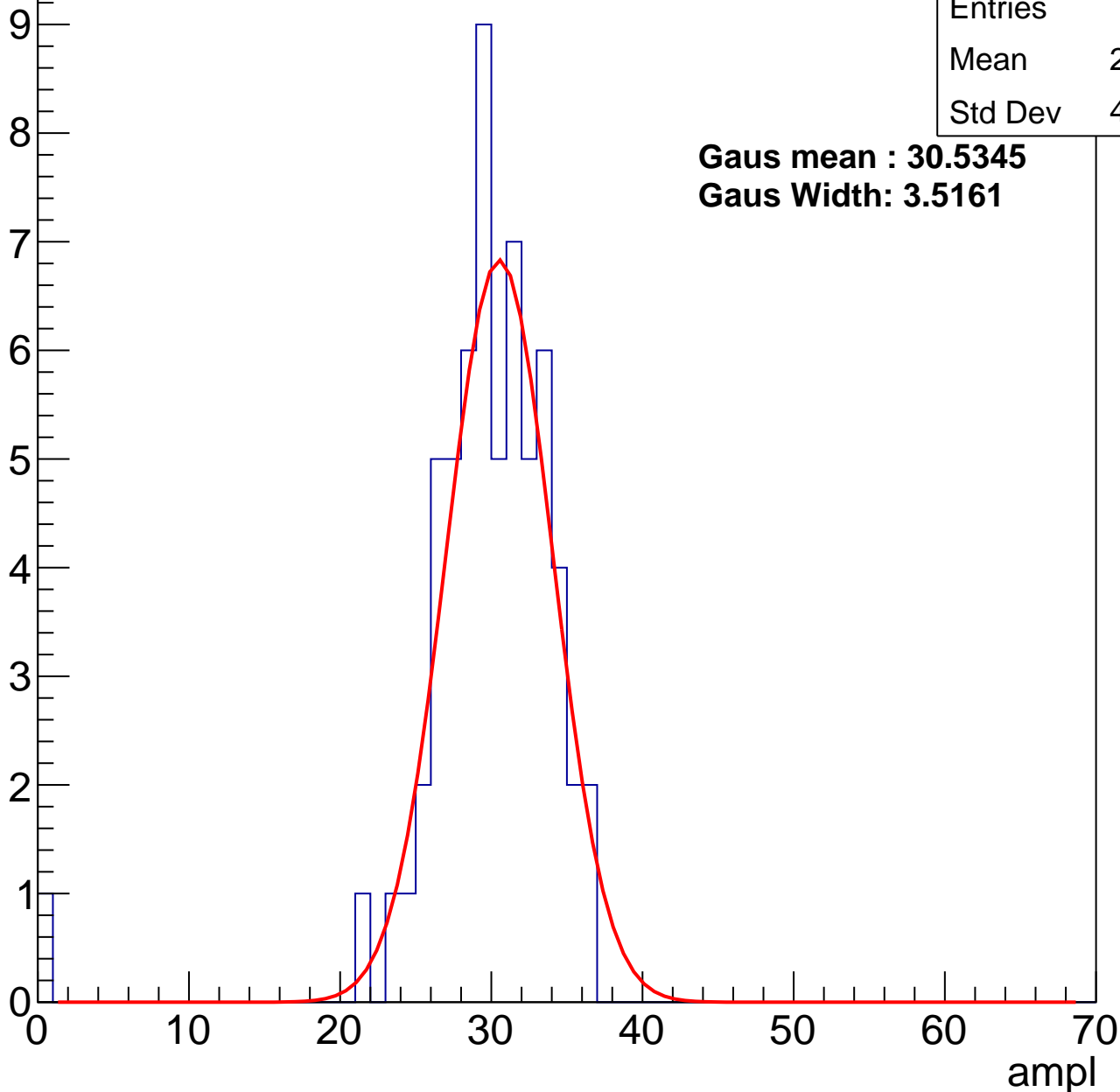
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.27
Std Dev	4.932

**Gaus mean : 30.5345**

**Gaus Width: 3.5161**



# B1L003S, U23-ch106, adc1

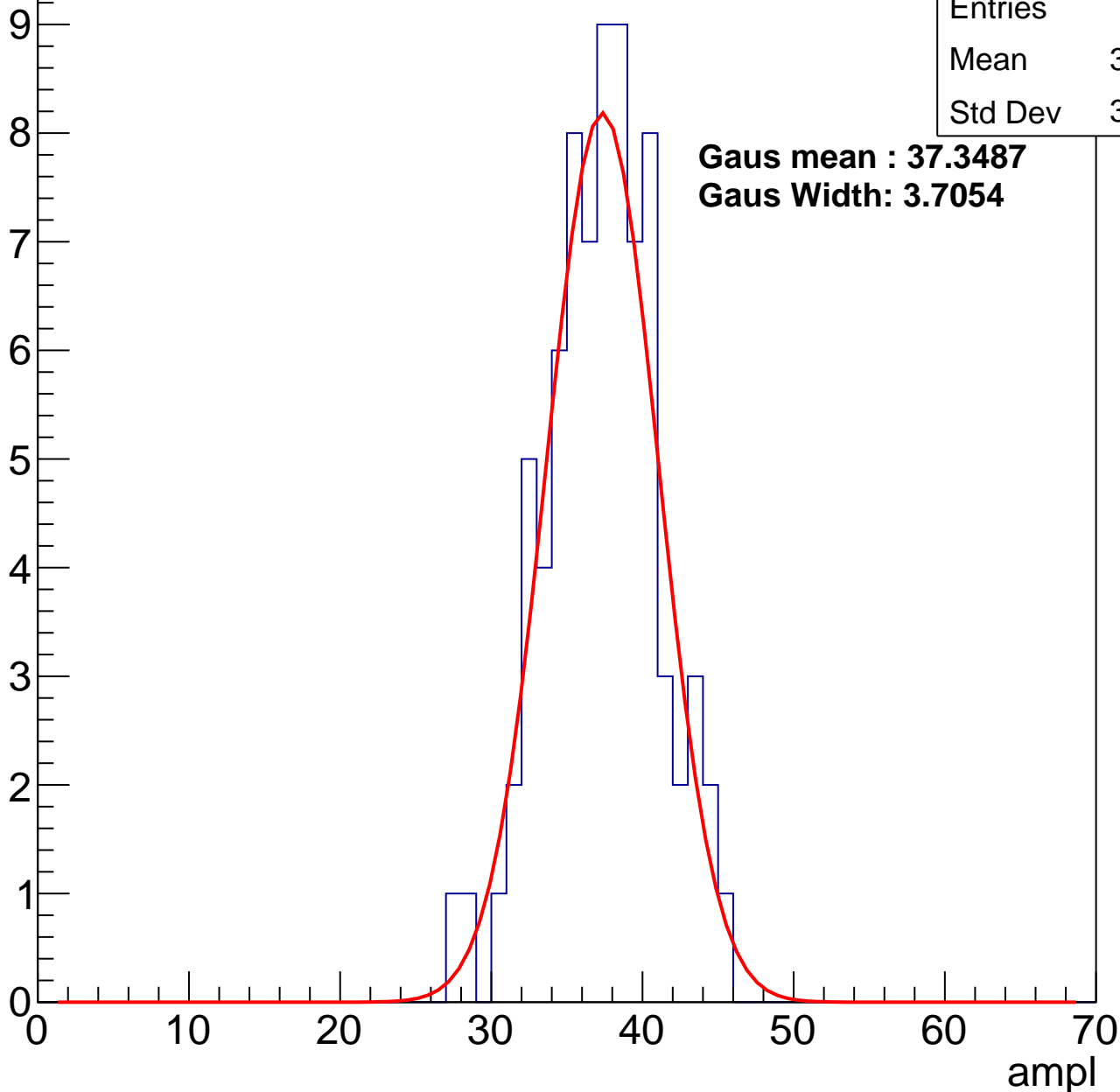
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	36.86
Std Dev	3.662

**Gaus mean : 37.3487**

**Gaus Width: 3.7054**



# B1L003S, U23-ch106, adc2

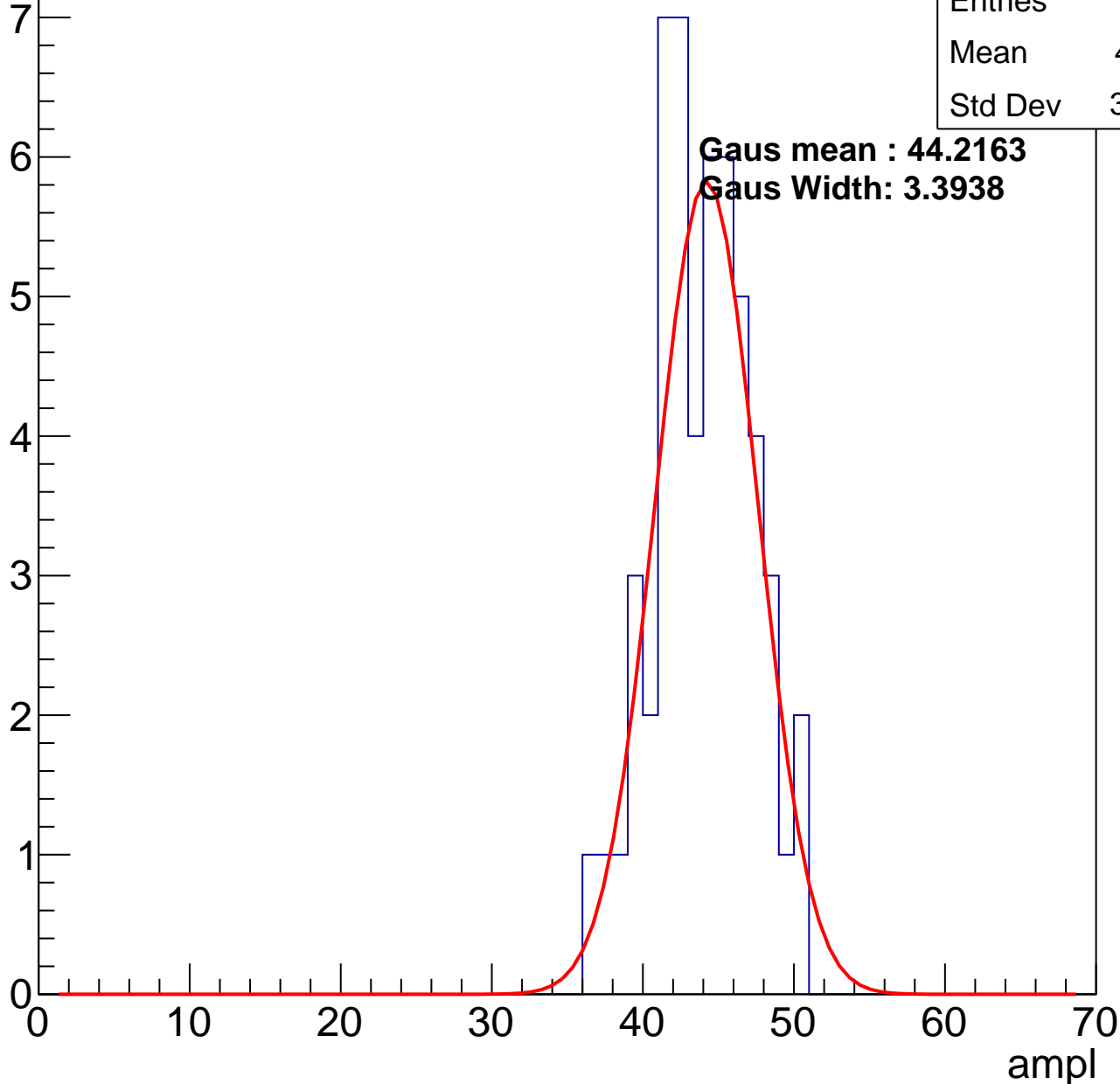
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.51
Std Dev	3.207

**Gaus mean : 44.2163**

**Gaus Width: 3.3938**

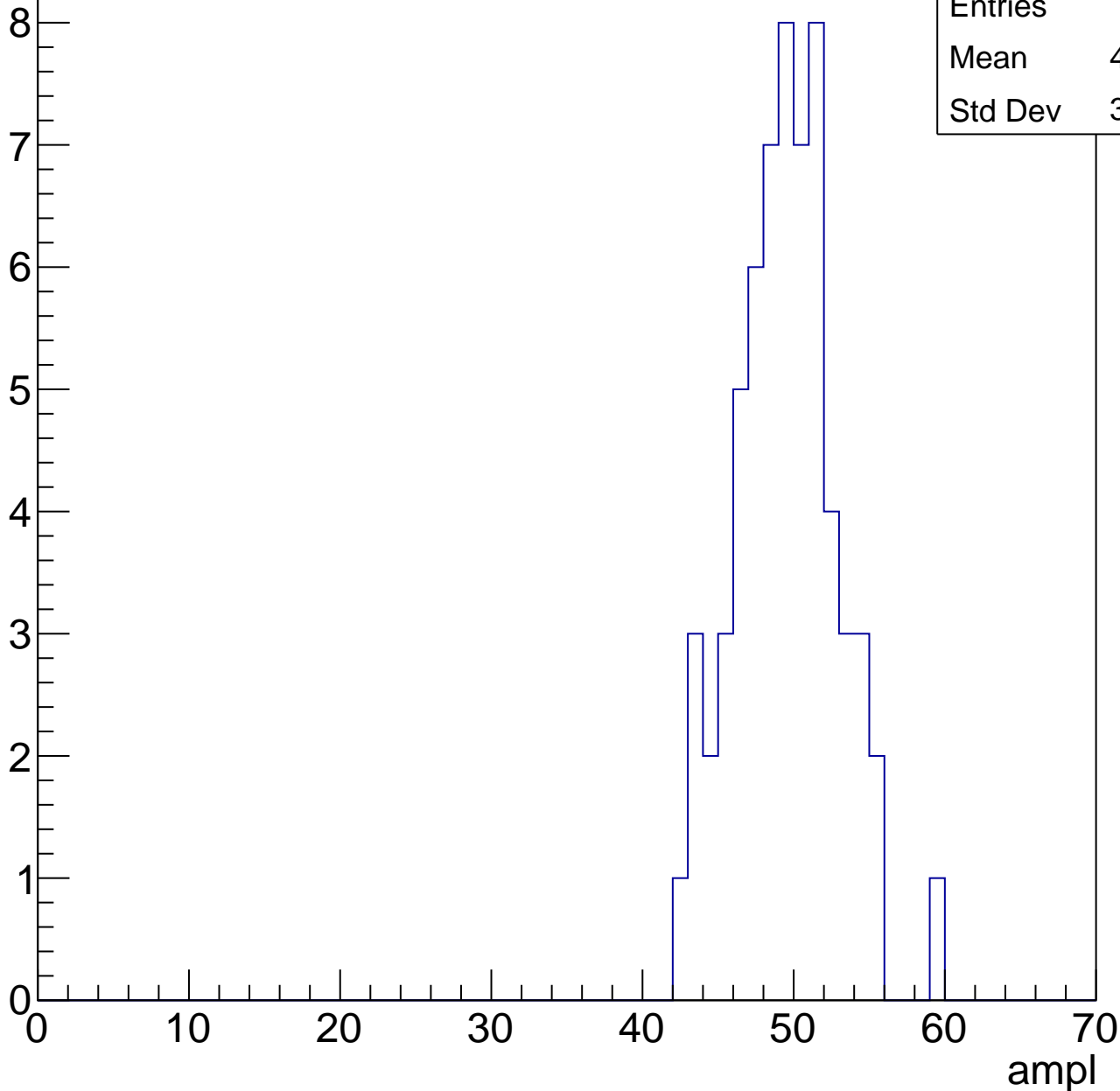


# B1L003S, U23-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	49.05
Std Dev	3.335

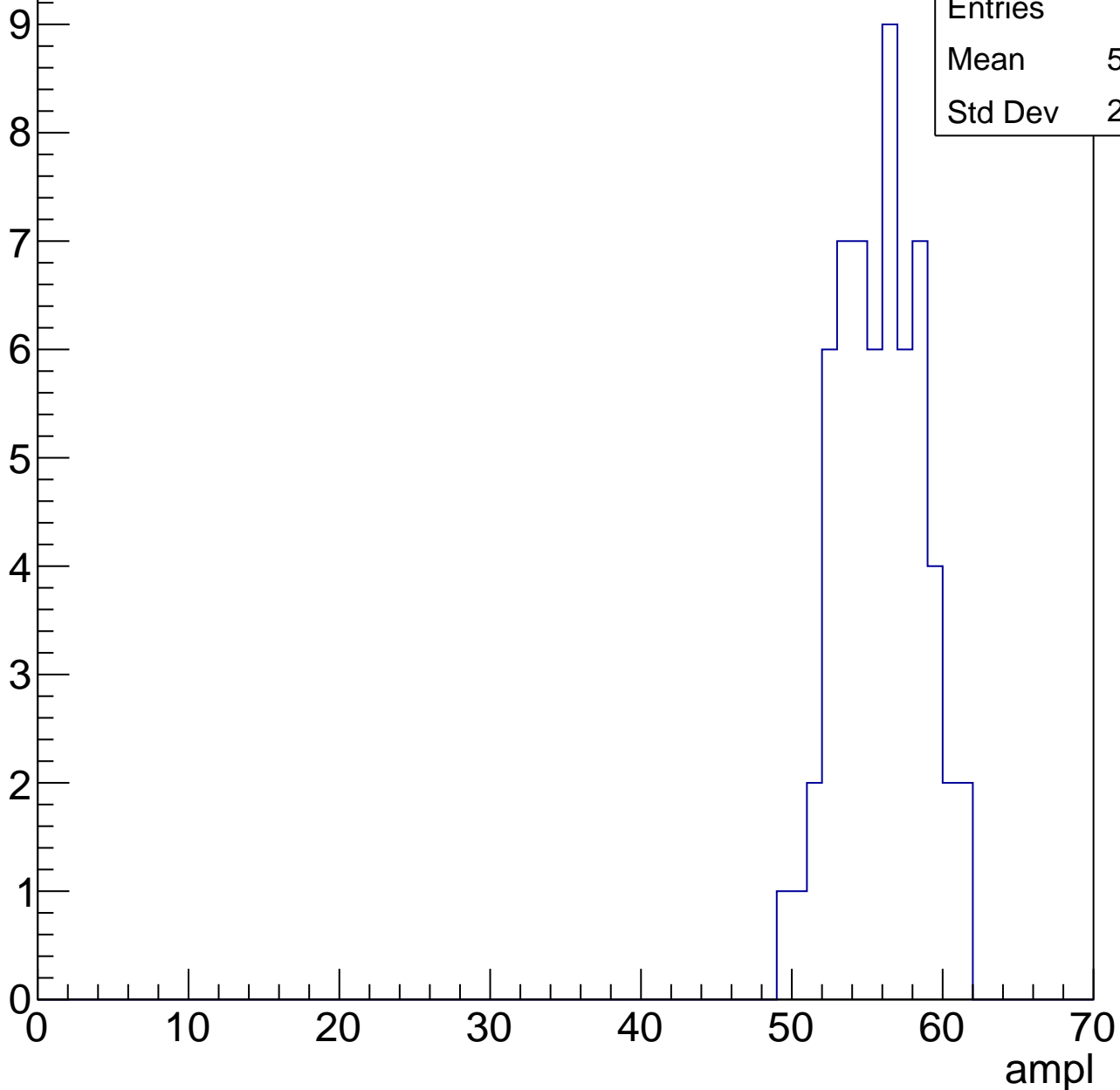


# B1L003S, U23-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	55.37
Std Dev	2.757

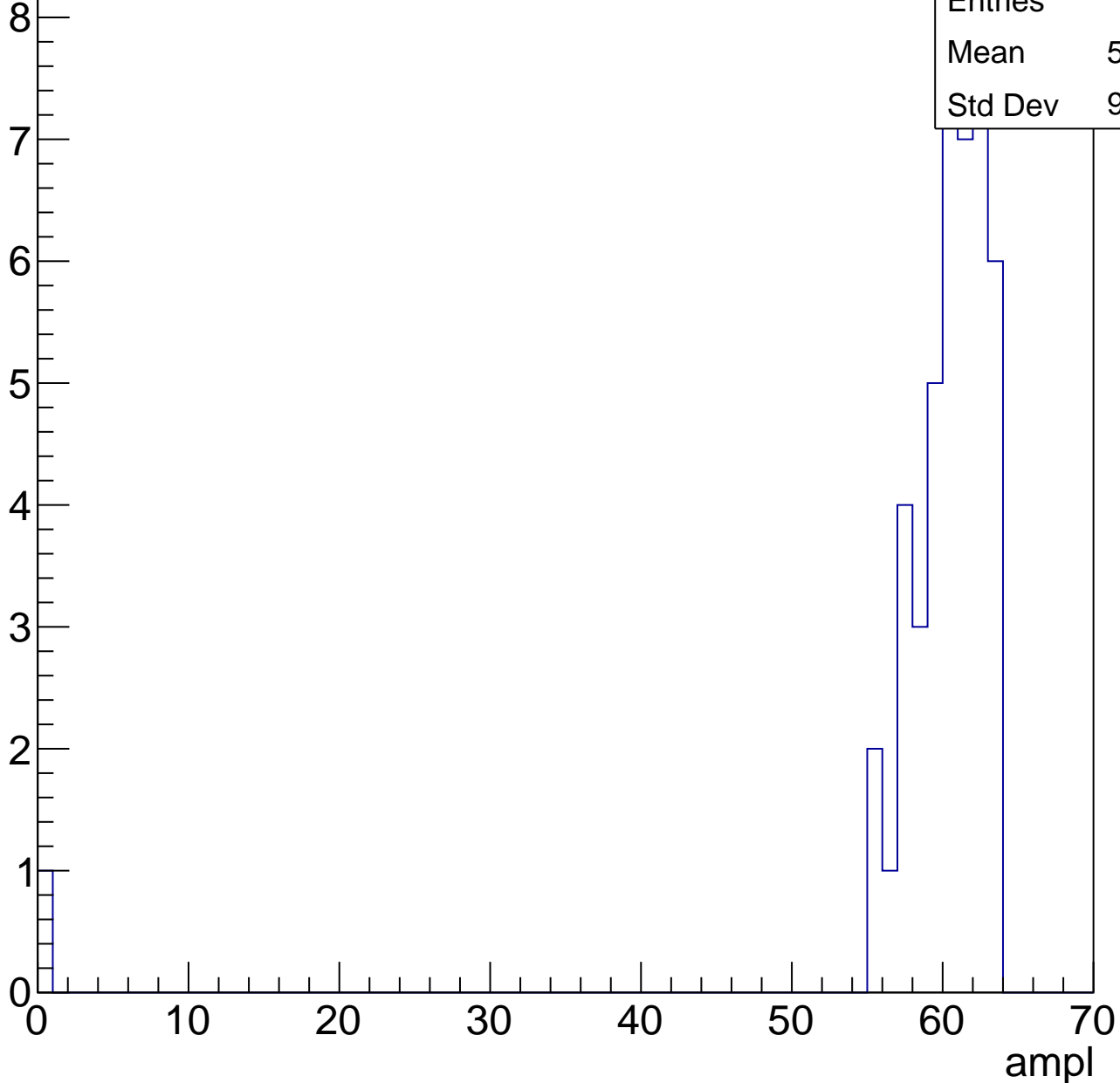


# B1L003S, U23-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.76
Std Dev	9.119



# B1L003S, U23-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U23-ch107, adc0

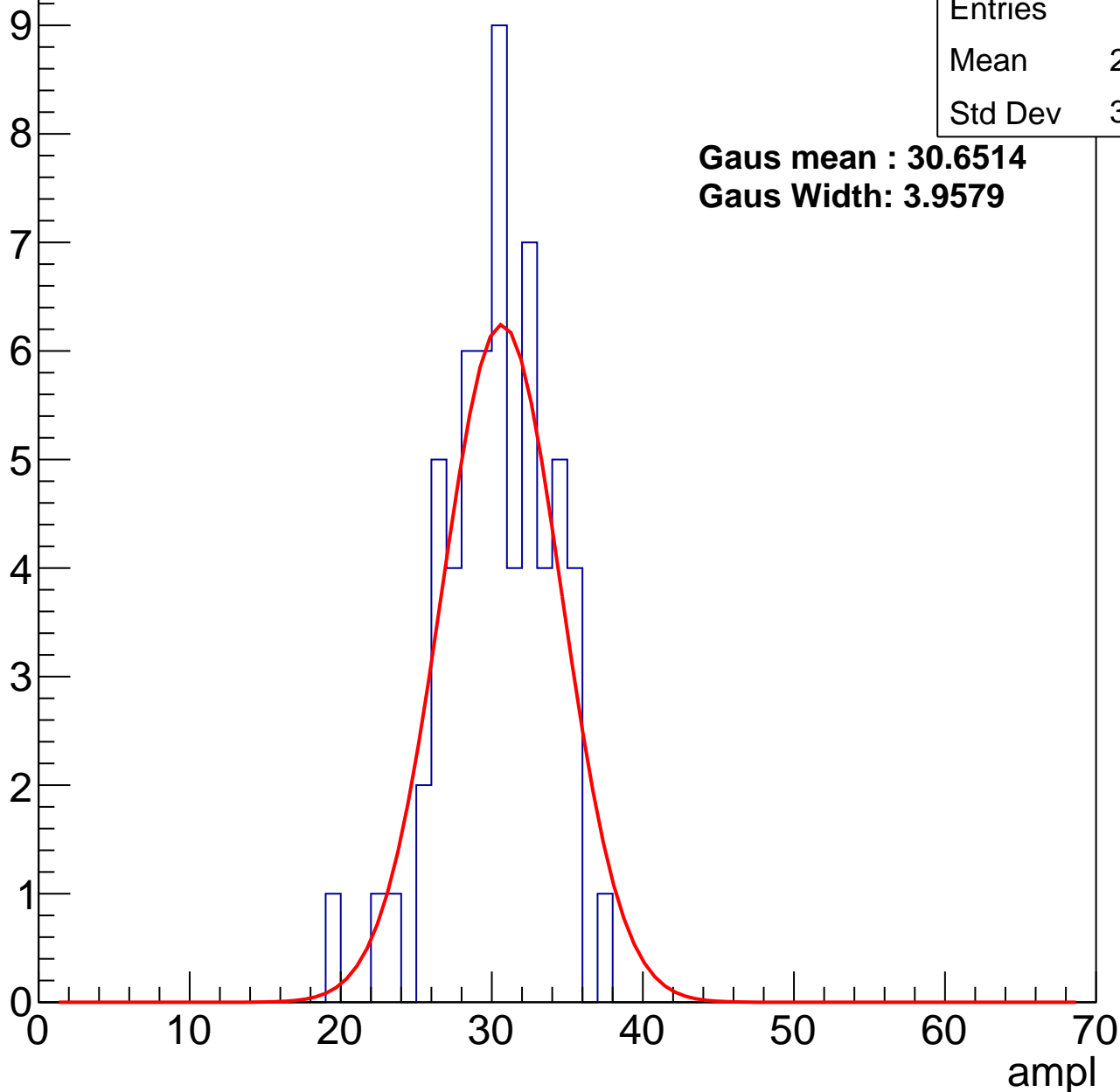
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29.85
Std Dev	3.478

**Gaus mean : 30.6514**

**Gaus Width: 3.9579**



# B1L003S, U23-ch107, adc1

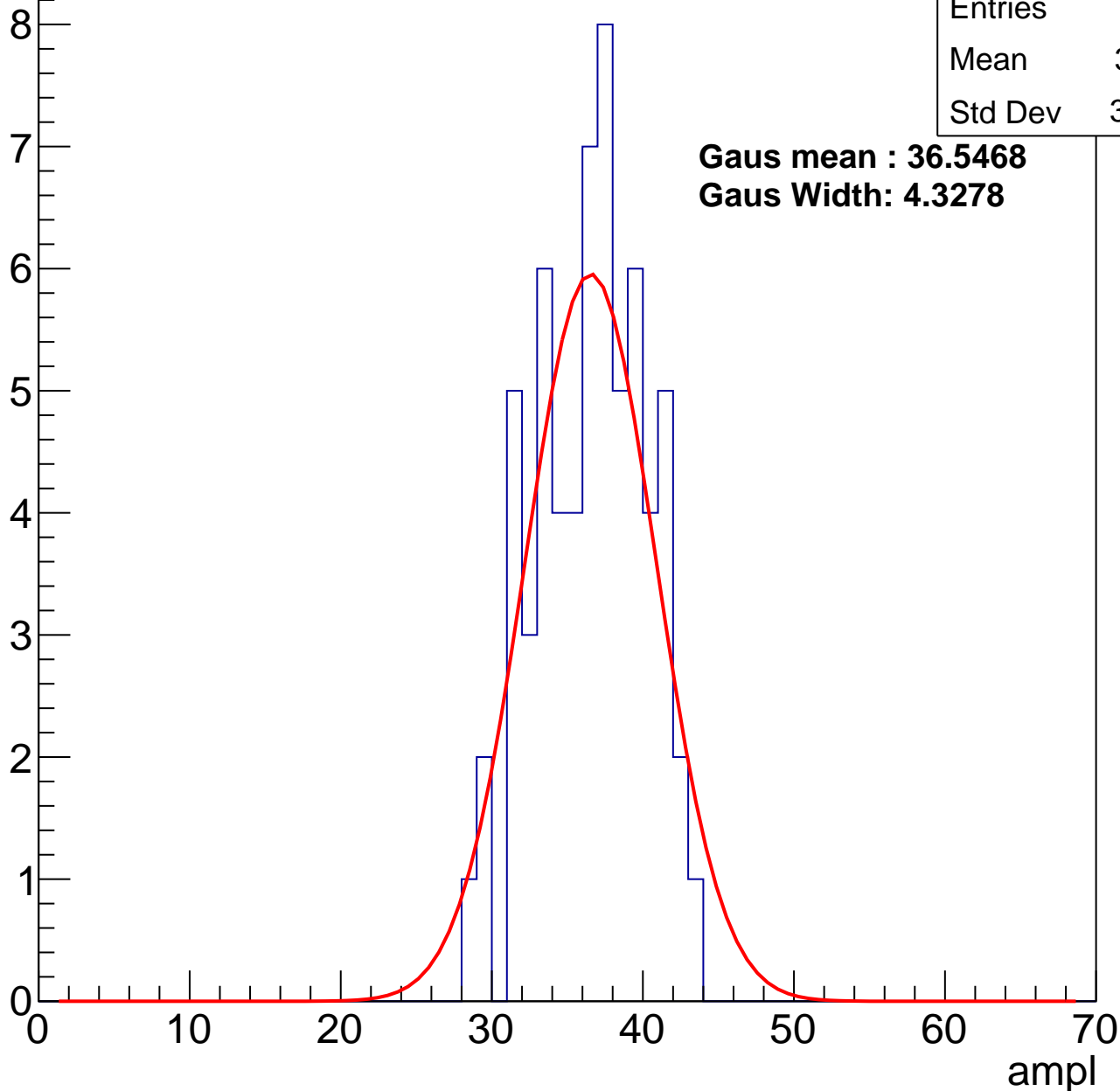
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	36.11
Std Dev	3.564

**Gaus mean : 36.5468**

**Gaus Width: 4.3278**



# B1L003S, U23-ch107, adc2

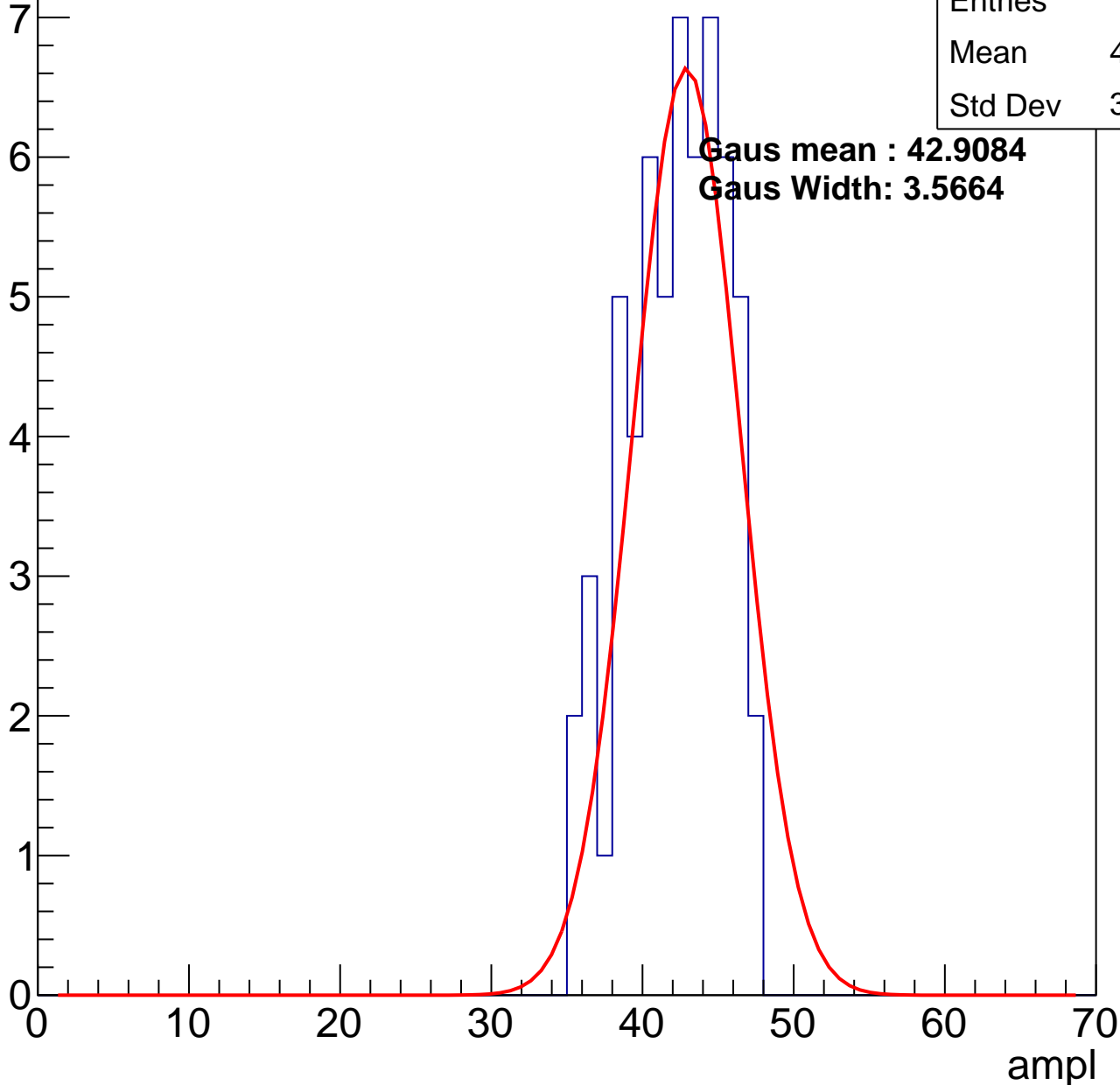
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.69
Std Dev	3.158

**Gaus mean : 42.9084**

**Gaus Width: 3.5664**

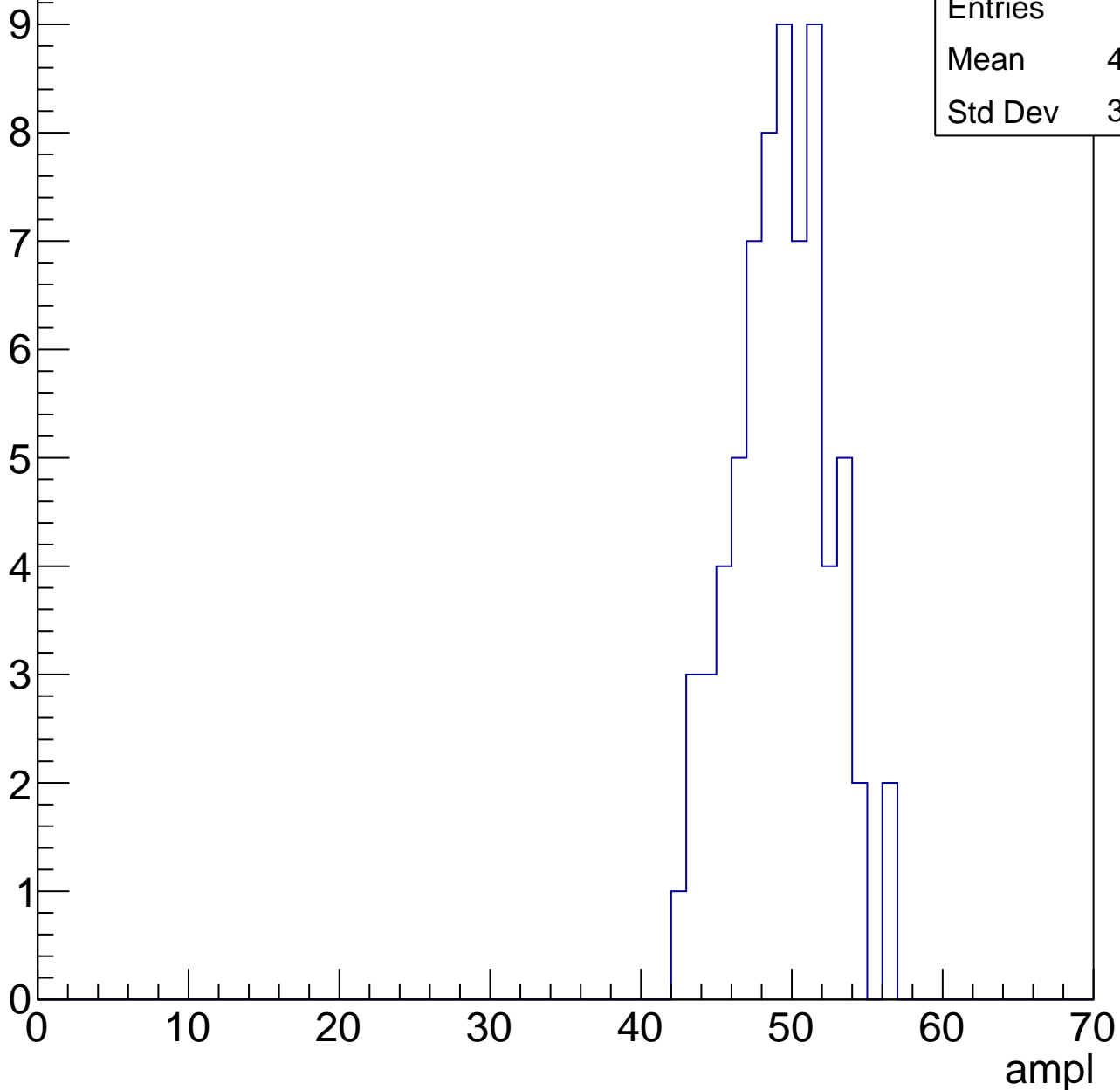


# B1L003S, U23-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

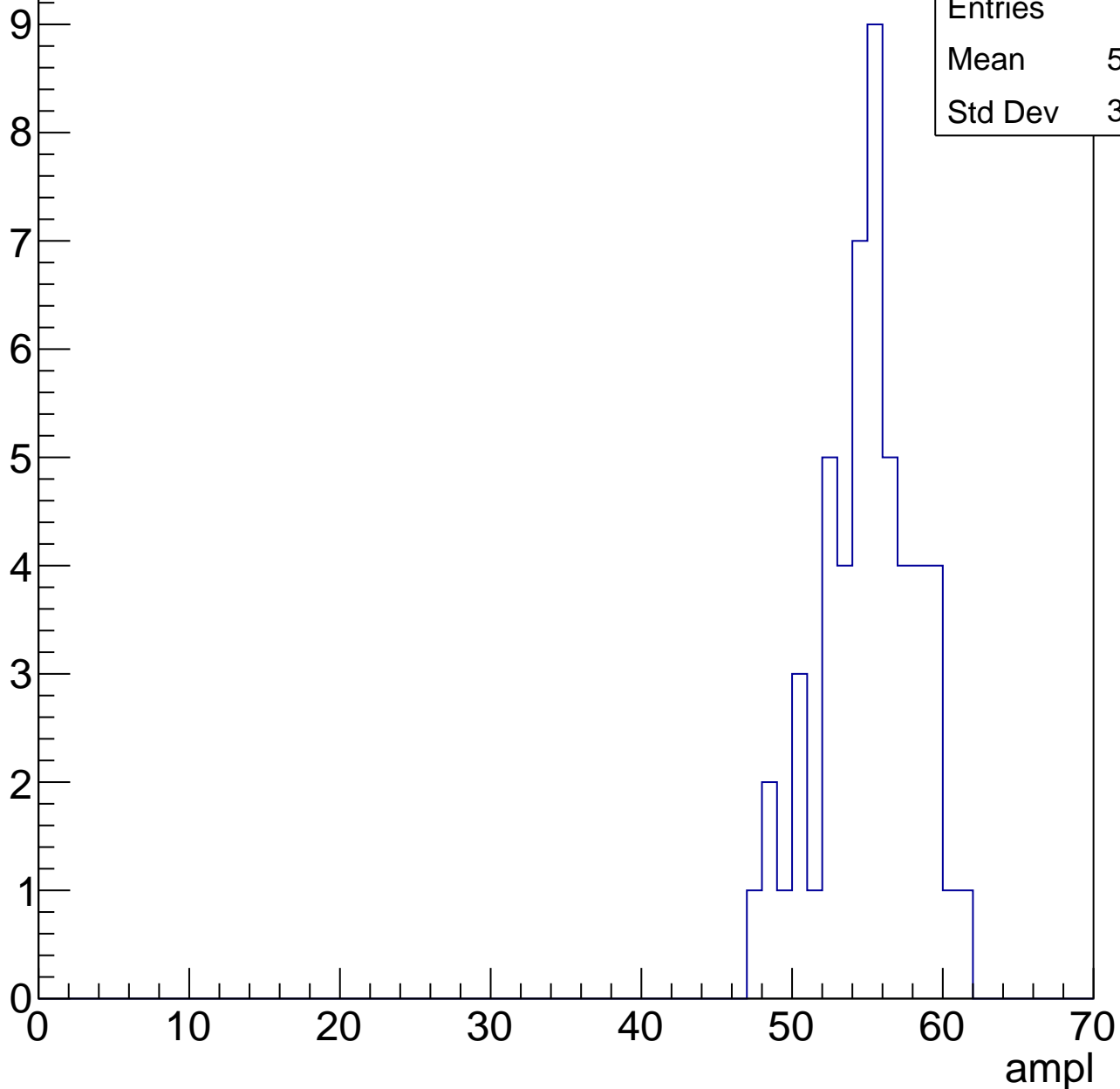
Entries	69
Mean	48.83
Std Dev	3.144



# B1L003S, U23-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

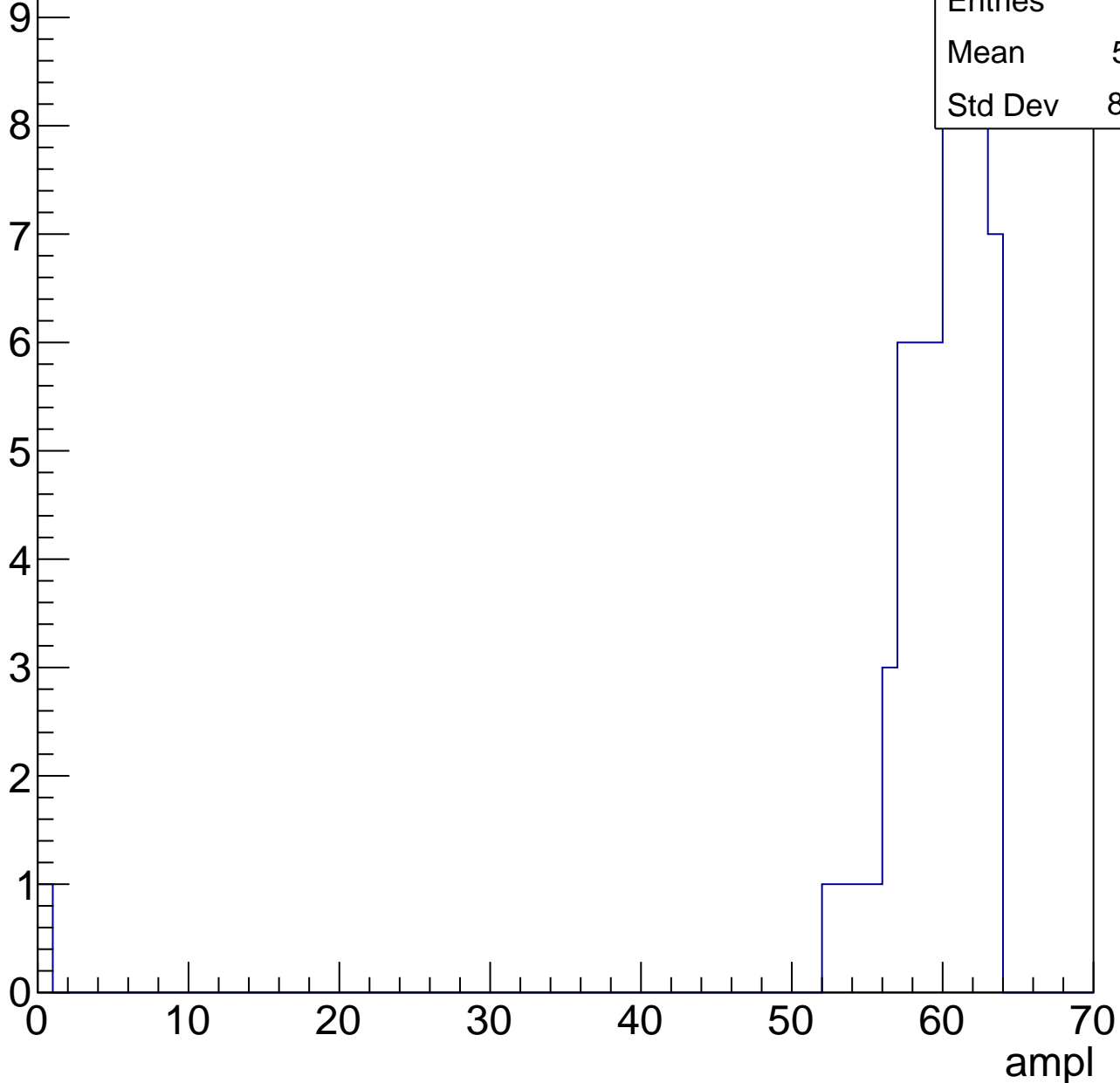


# B1L003S, U23-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

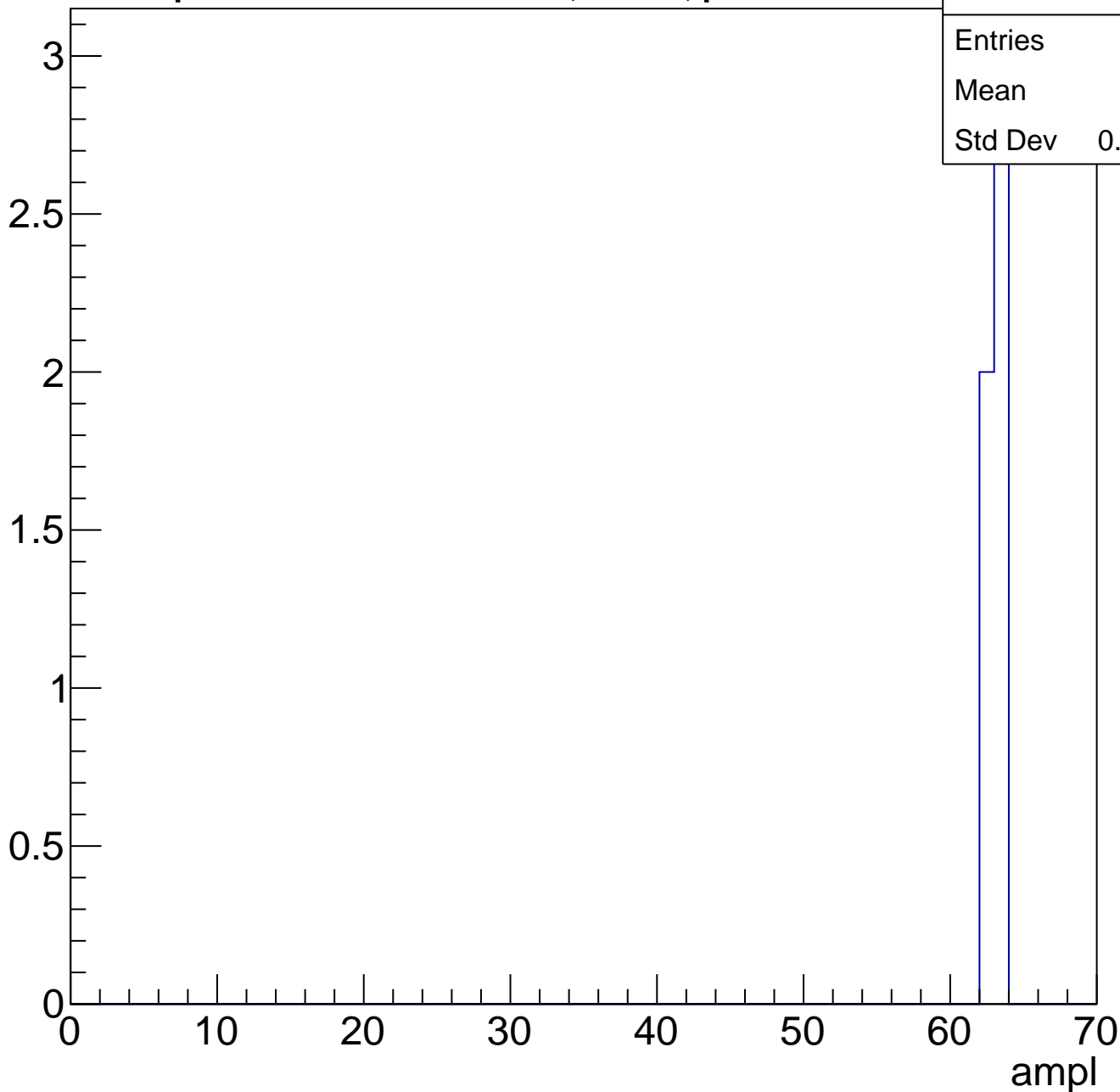
Entries	59
Mean	58.51
Std Dev	8.108



# B1L003S, U23-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch108, adc0

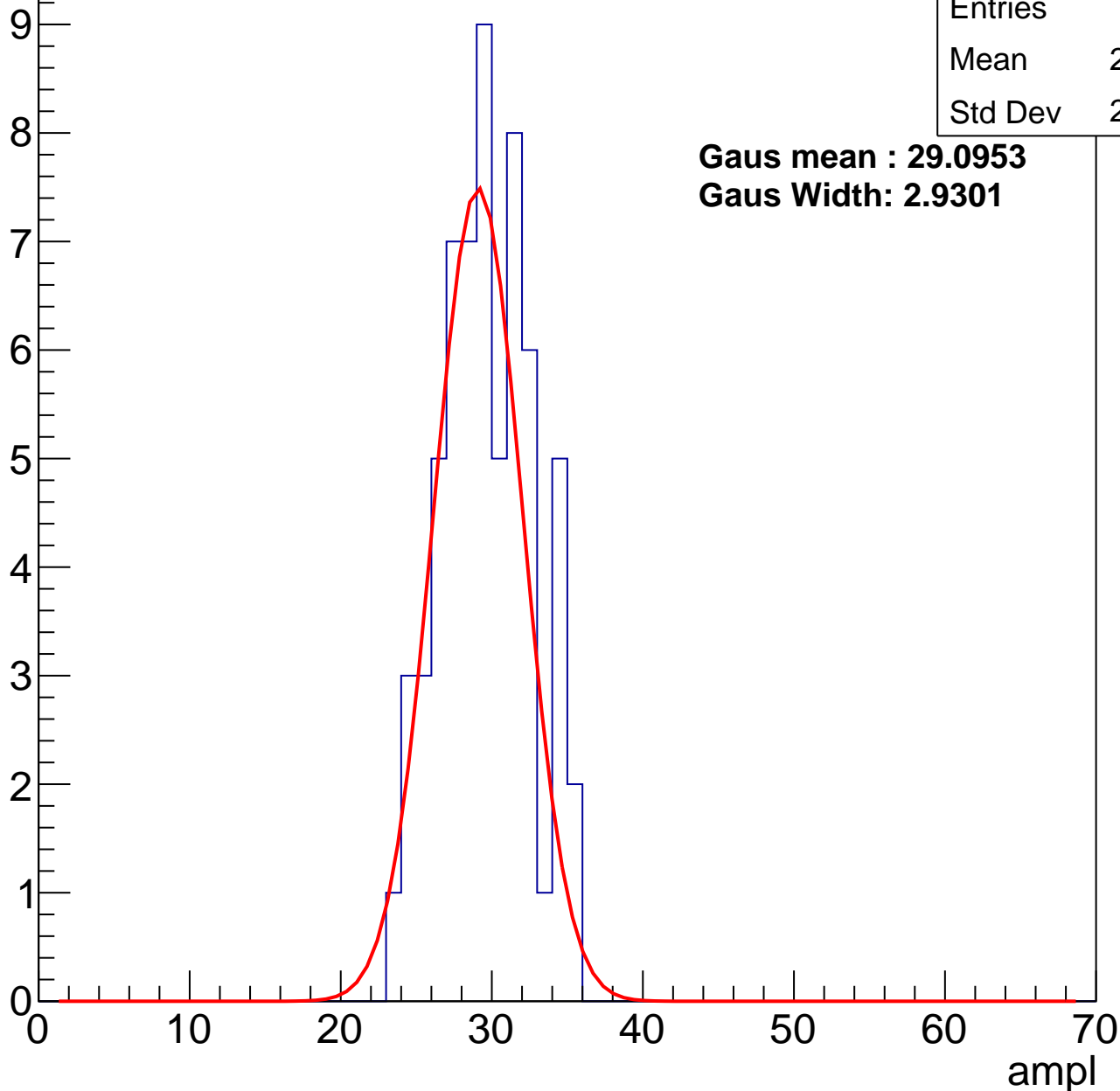
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.18
Std Dev	2.954

**Gaus mean : 29.0953**

**Gaus Width: 2.9301**



# B1L003S, U23-ch108, adc1

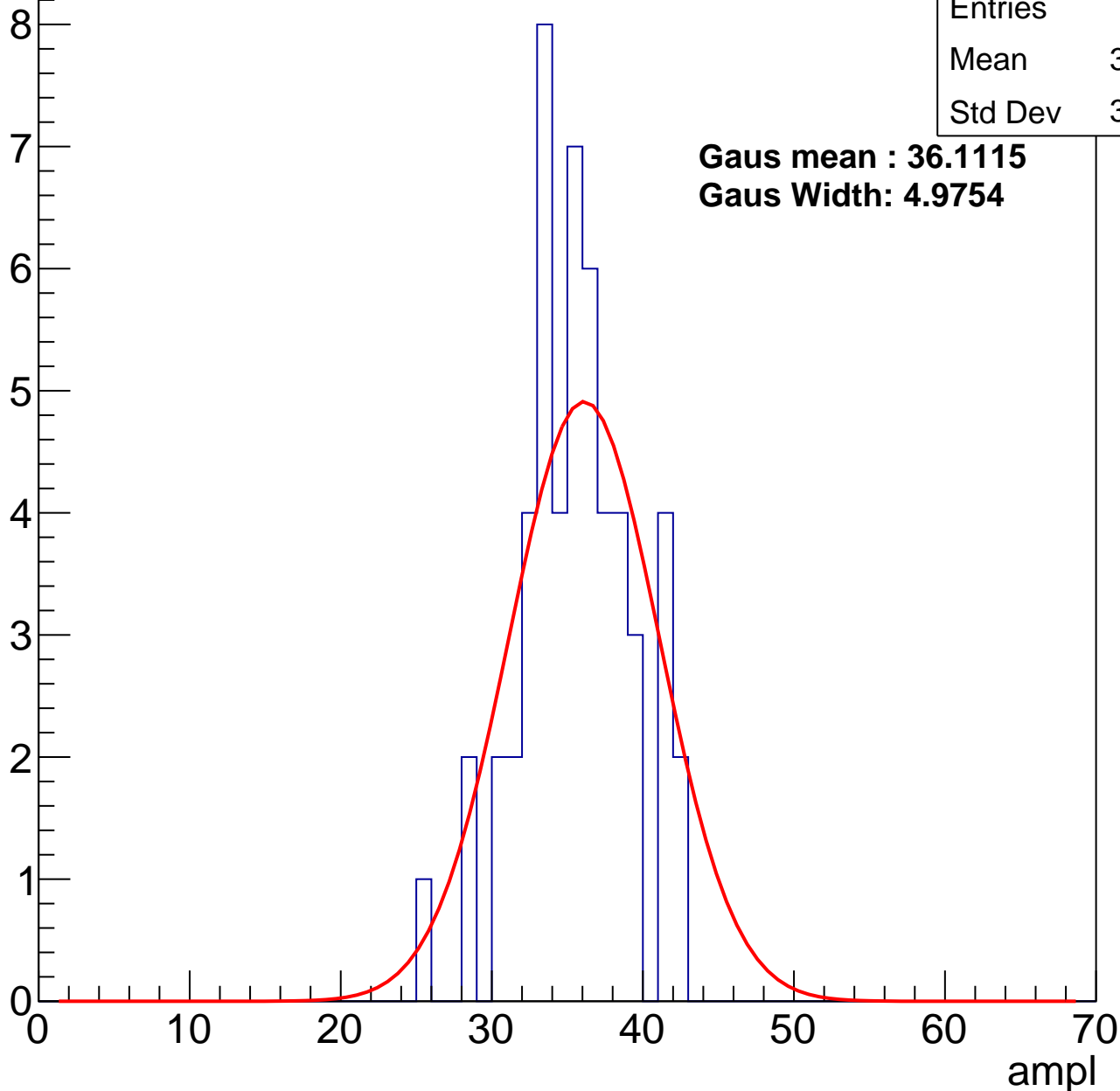
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.04
Std Dev	3.634

**Gaus mean : 36.1115**

**Gaus Width: 4.9754**



# B1L003S, U23-ch108, adc2

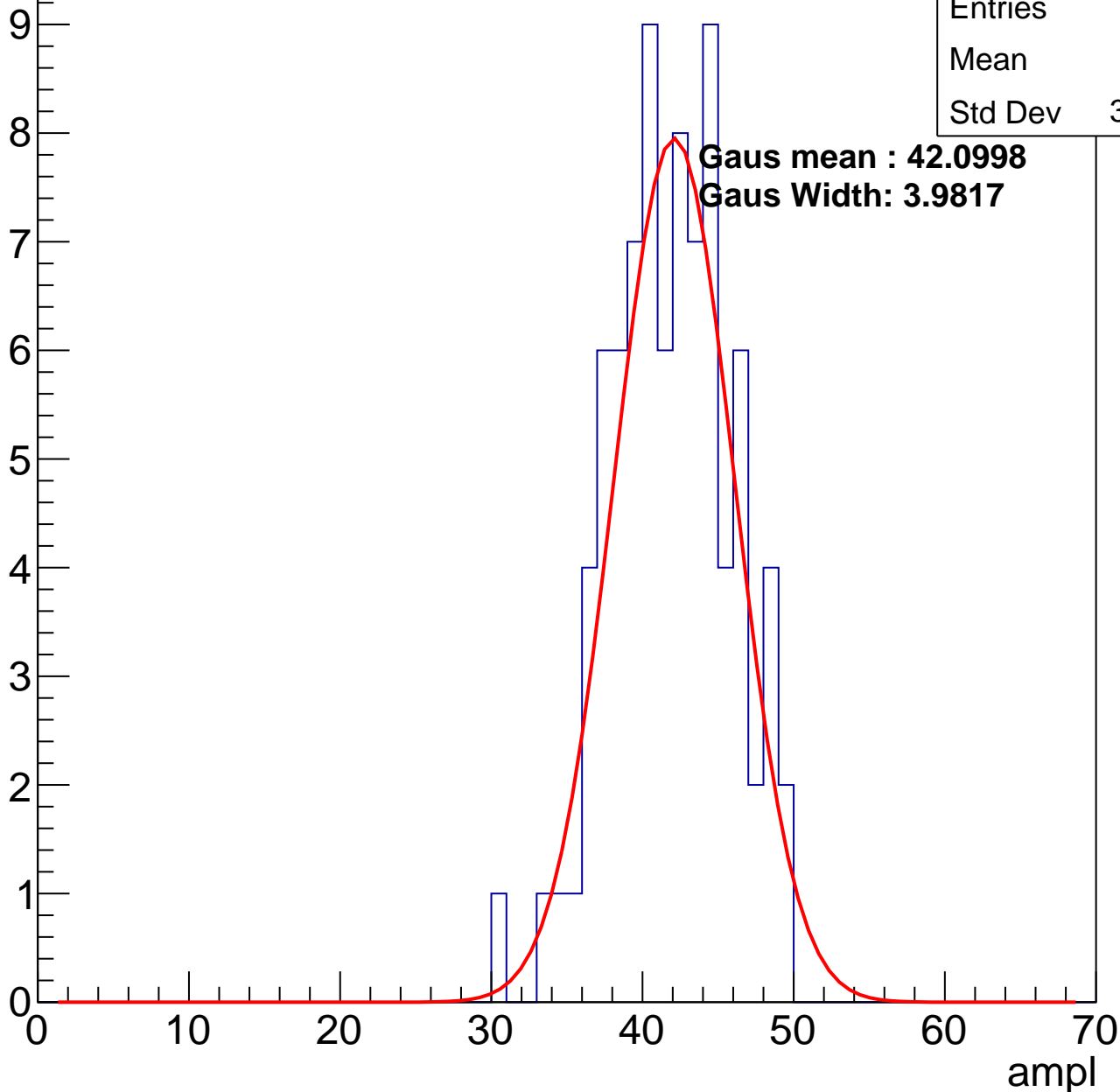
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	84
Mean	41.4
Std Dev	3.889

**Gaus mean : 42.0998**

**Gaus Width: 3.9817**

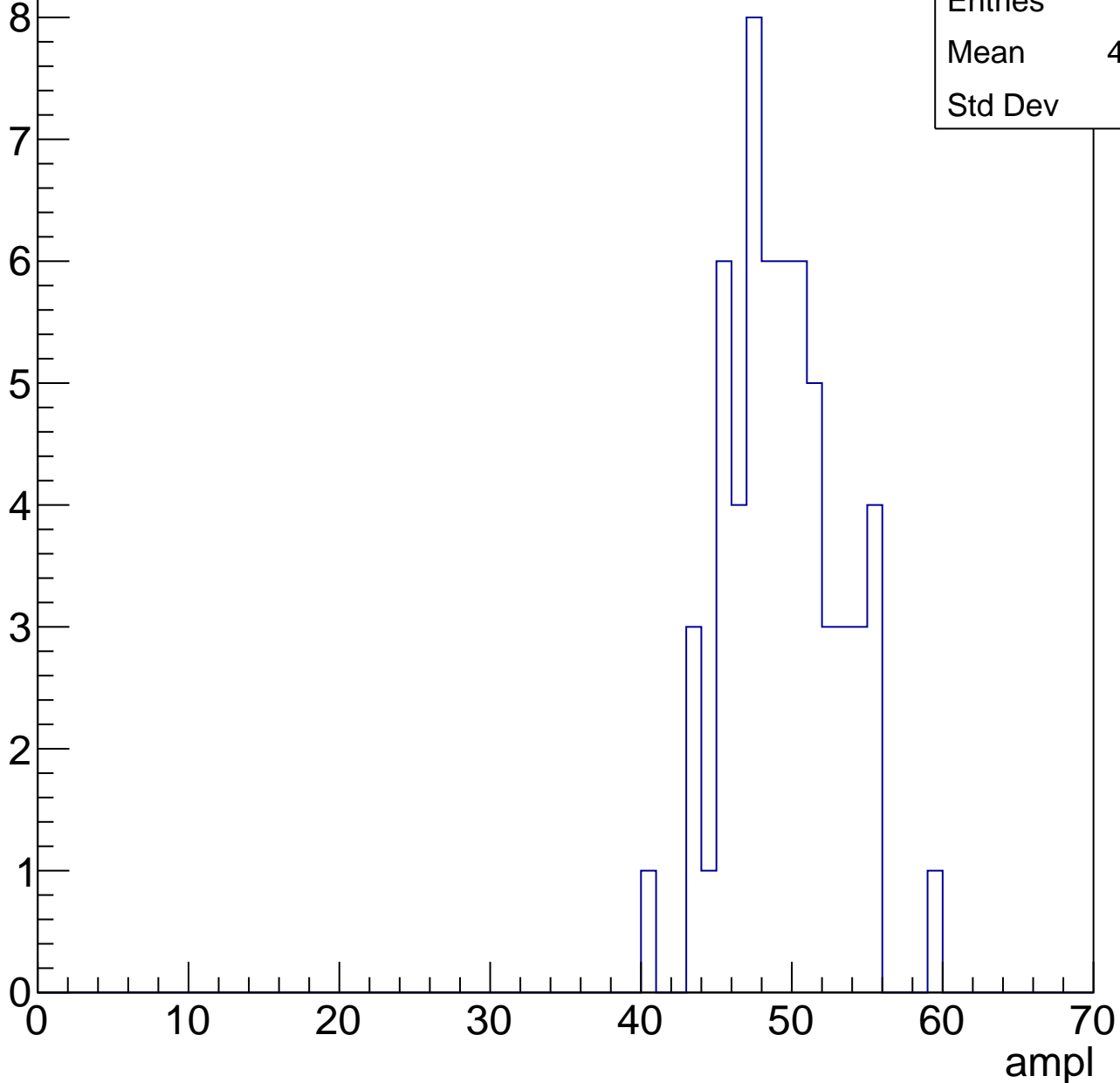


# B1L003S, U23-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	48.93
Std Dev	3.66

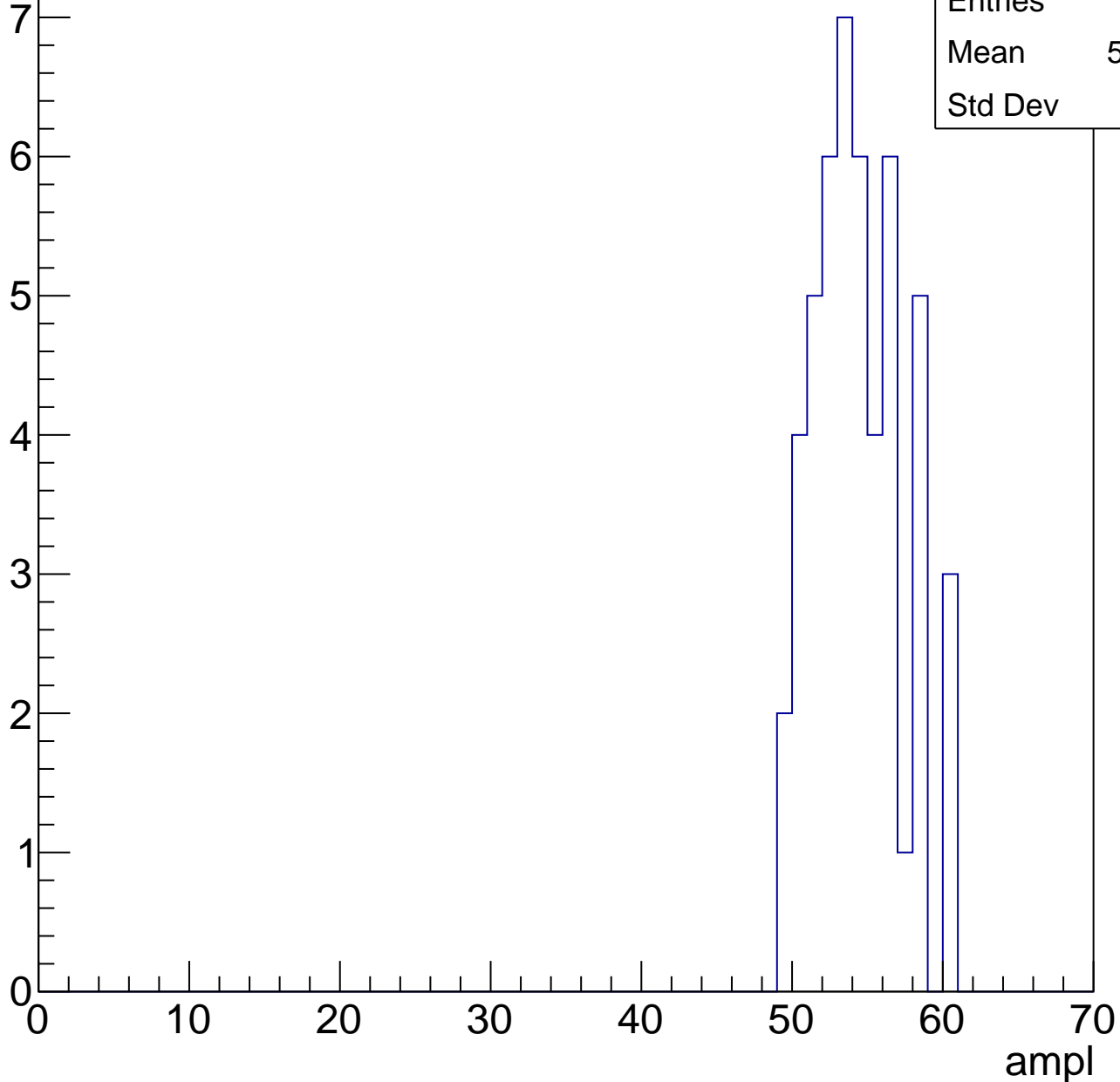


# B1L003S, U23-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	53.94
Std Dev	2.91



# B1L003S, U23-ch108, adc5

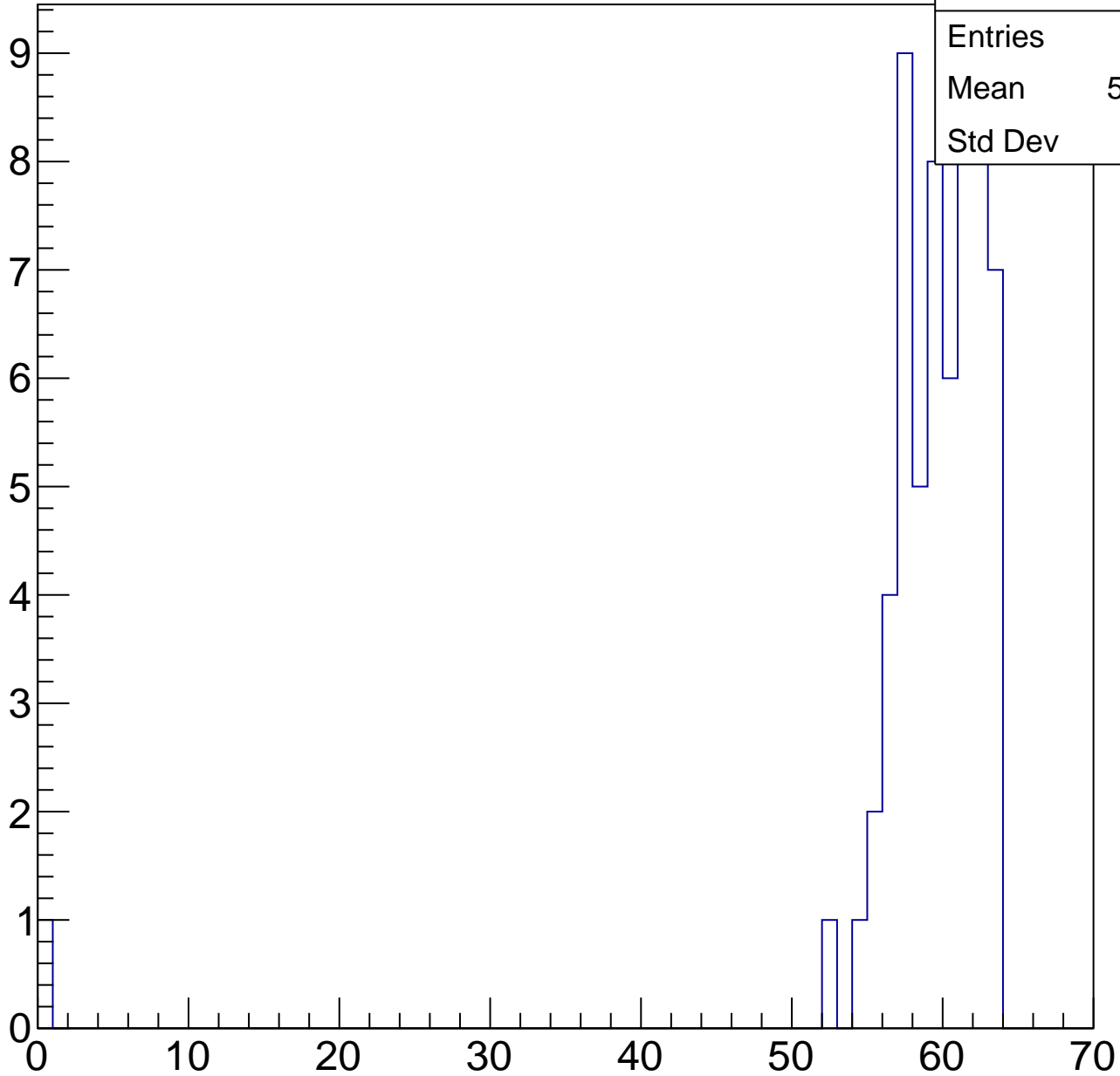
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	58.33
Std Dev	8.02

ampl



# B1L003S, U23-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

8

Mean

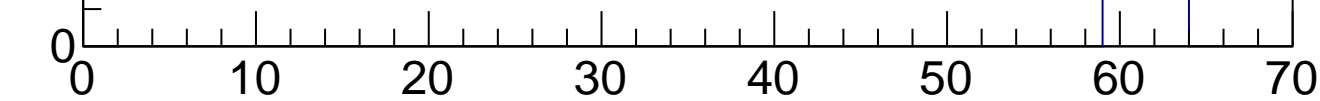
61.5

Std Dev

1.414

ampl

0 10 20 30 40 50 60 70

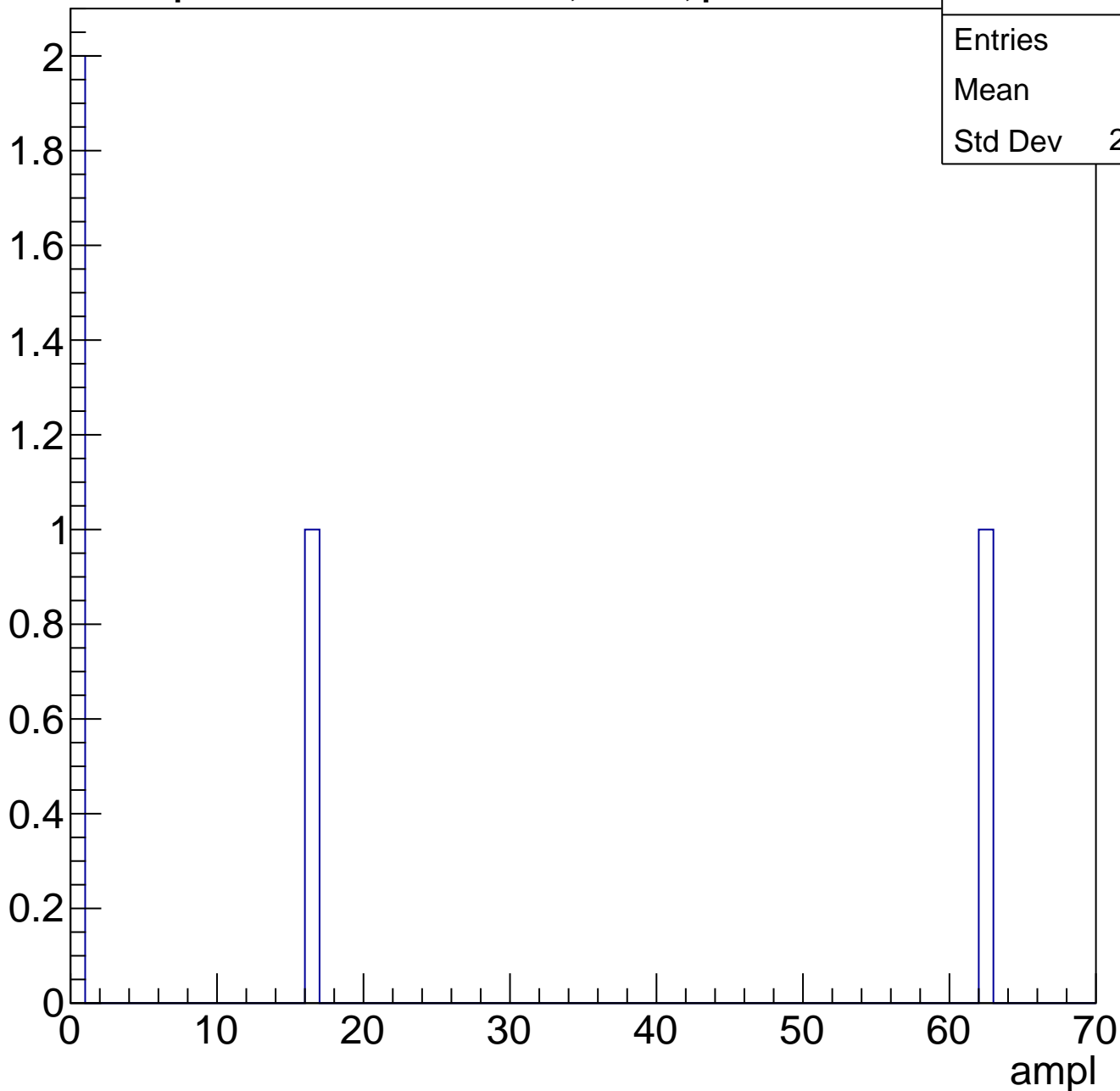




# B1L003S, U23-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	4
Mean	19.5
Std Dev	25.39

# B1L003S, U23-ch109, adc0

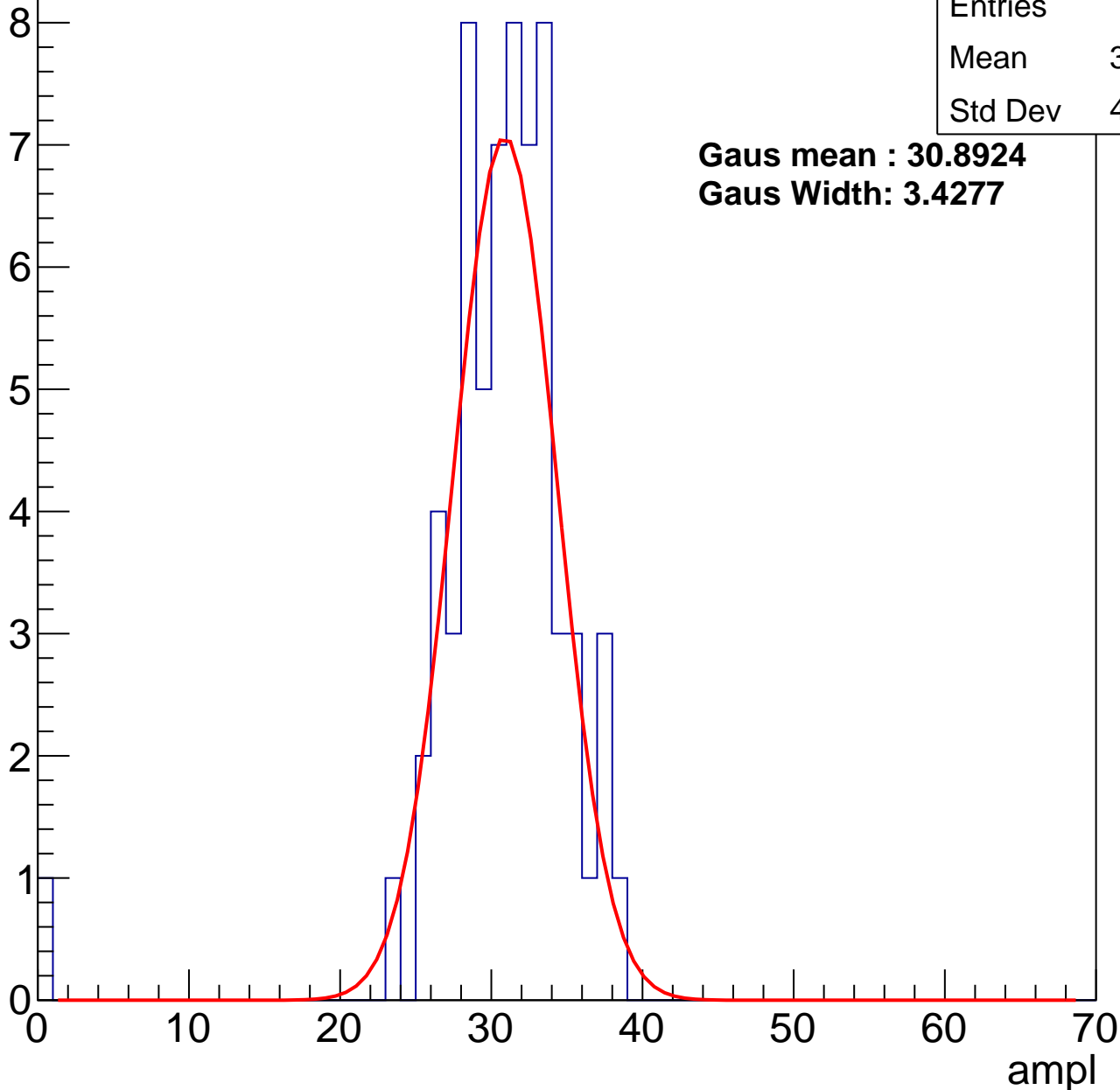
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.23
Std Dev	4.964

**Gaus mean : 30.8924**

**Gaus Width: 3.4277**



# B1L003S, U23-ch109, adc1

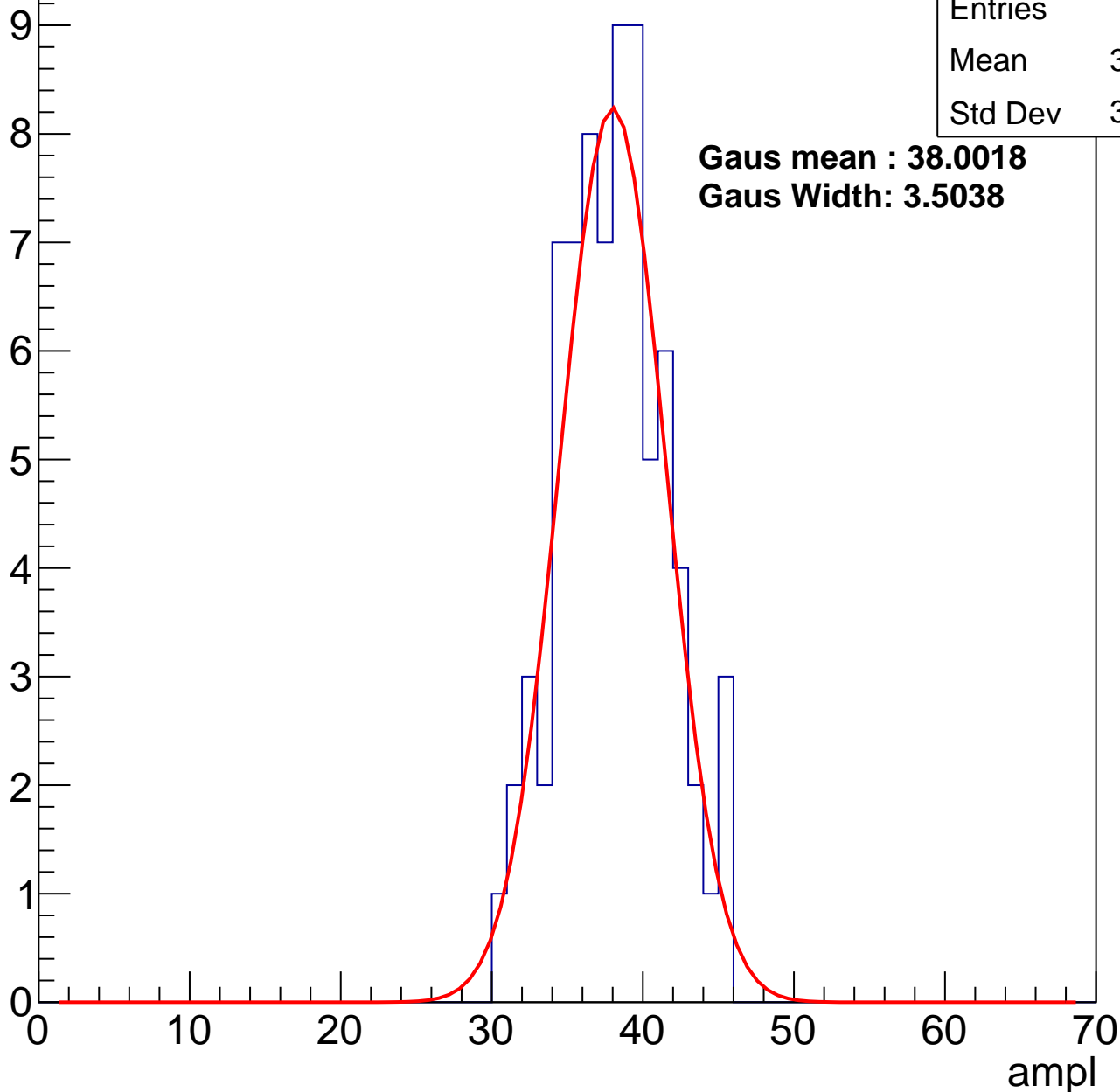
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	37.58
Std Dev	3.442

**Gaus mean : 38.0018**

**Gaus Width: 3.5038**



# B1L003S, U23-ch109, adc2

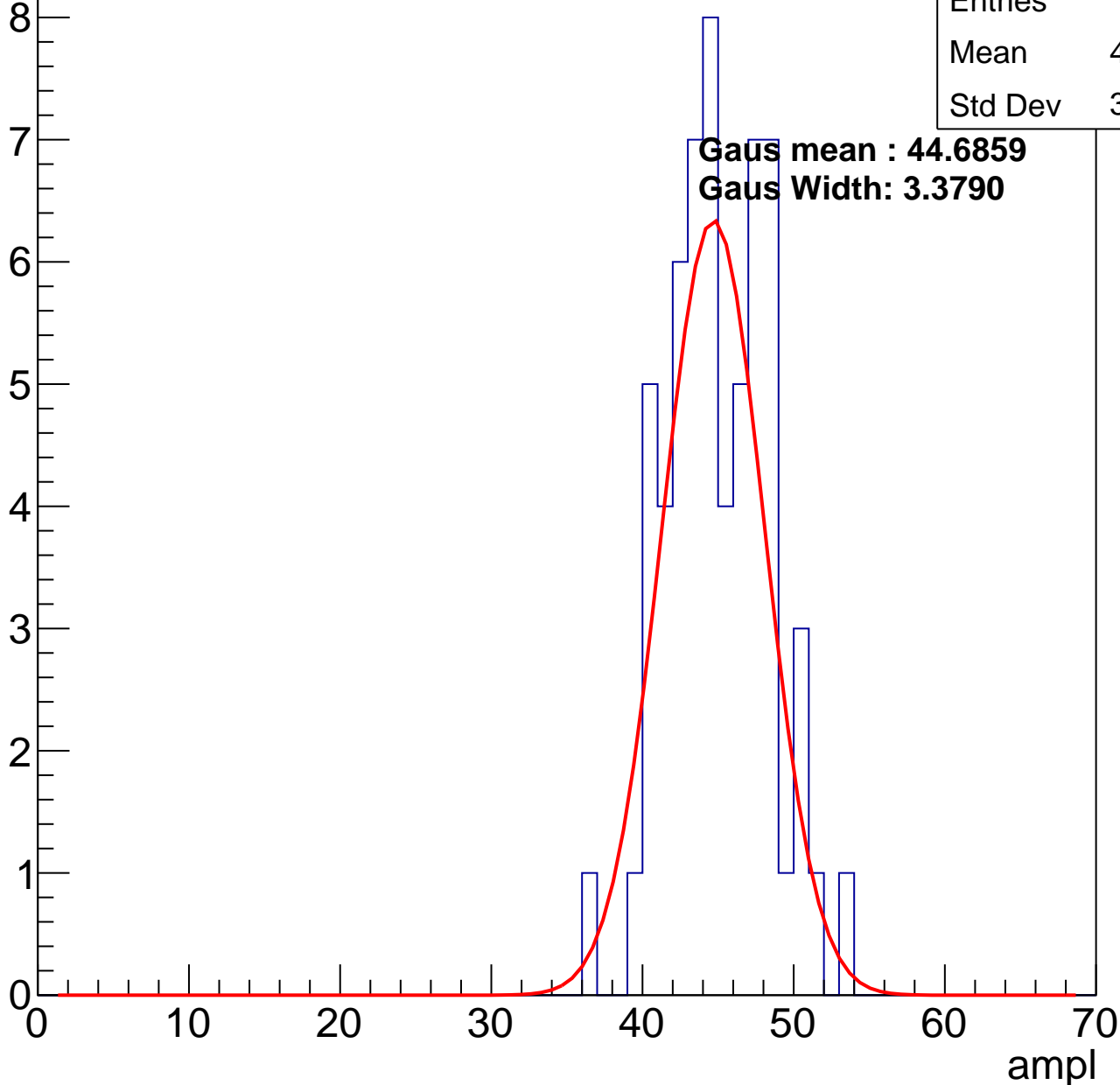
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	44.62
Std Dev	3.325

Gaus mean : 44.6859

Gaus Width: 3.3790

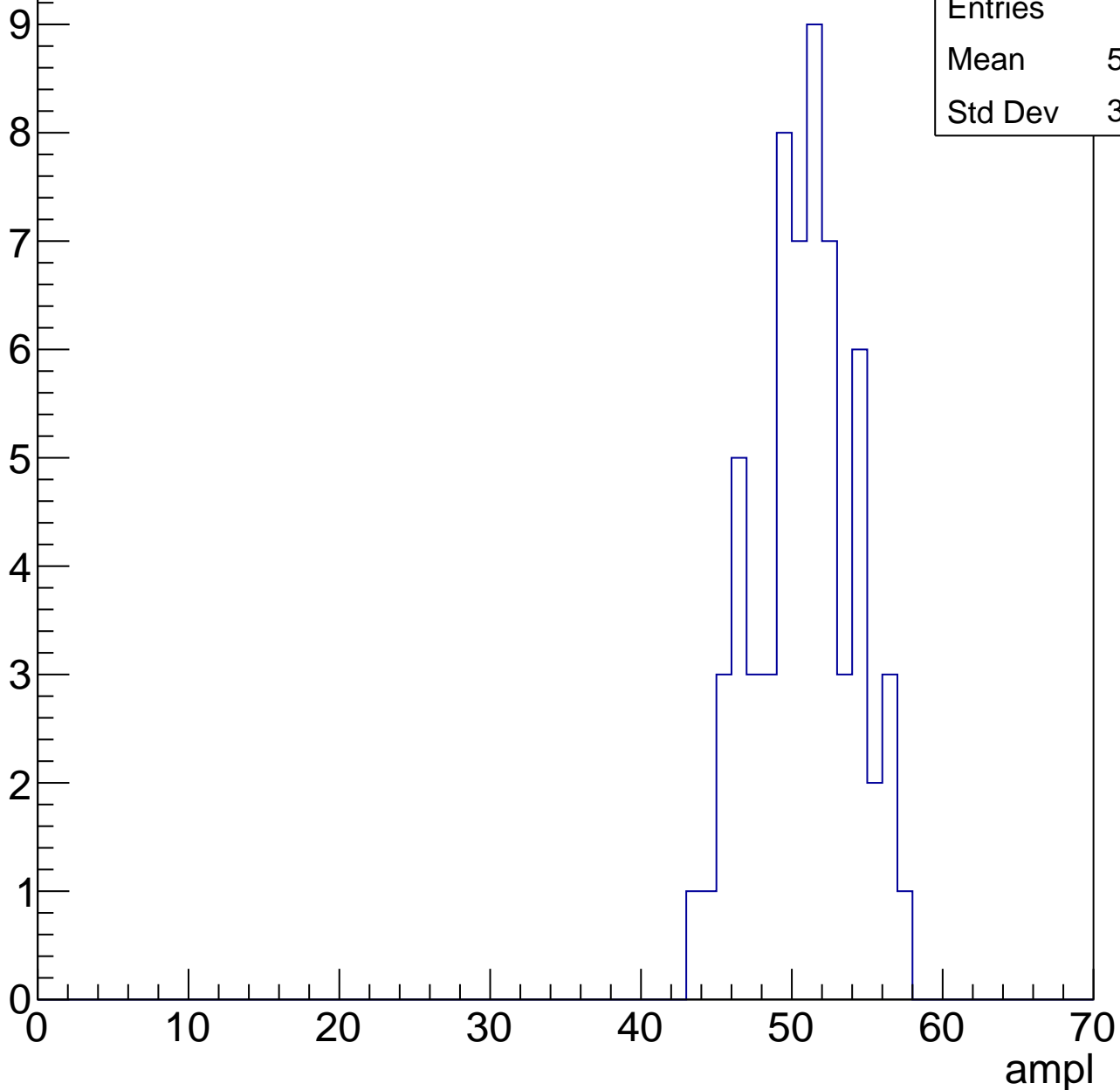


# B1L003S, U23-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	50.32
Std Dev	3.232

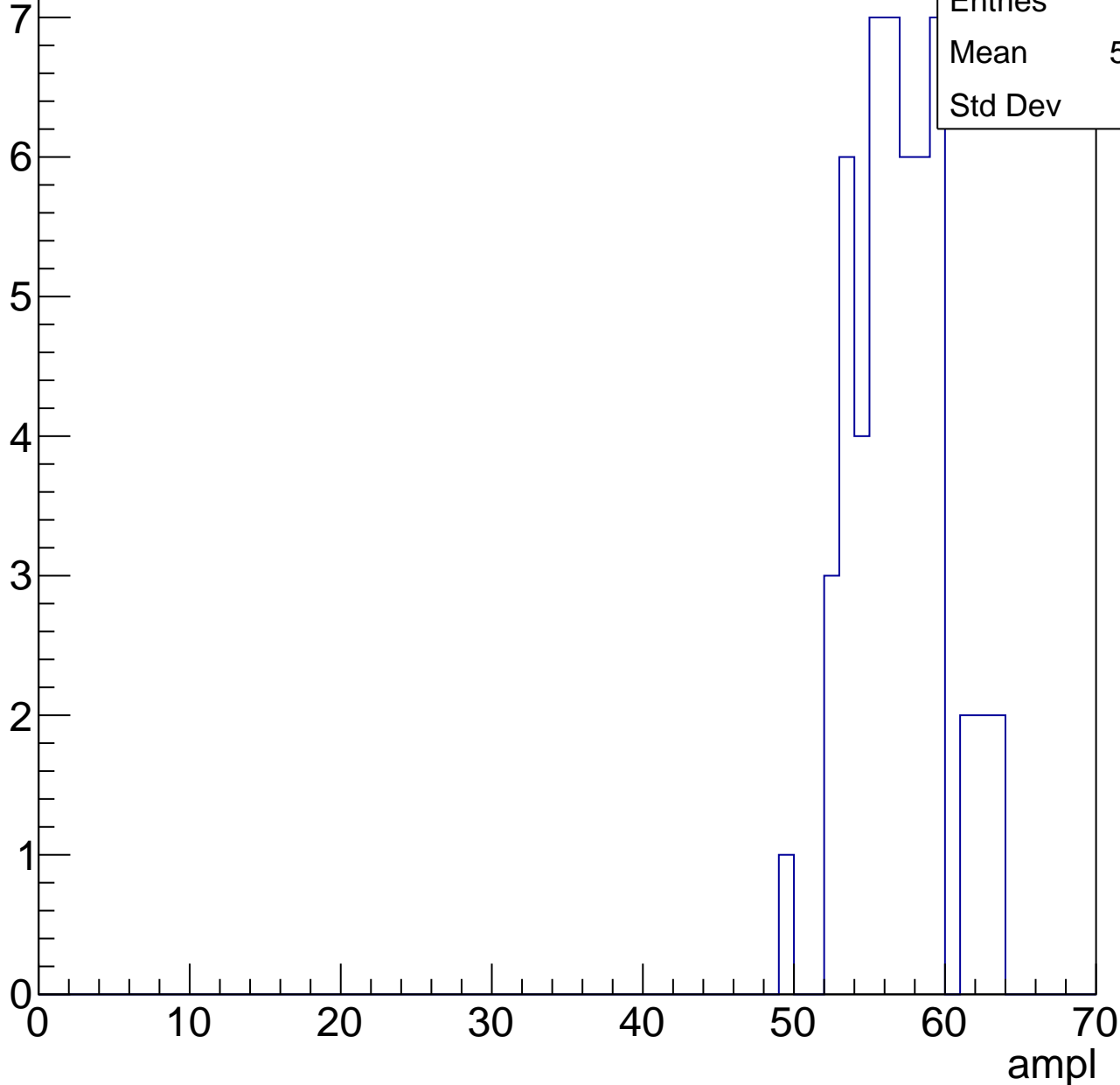


# B1L003S, U23-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	56.43
Std Dev	3

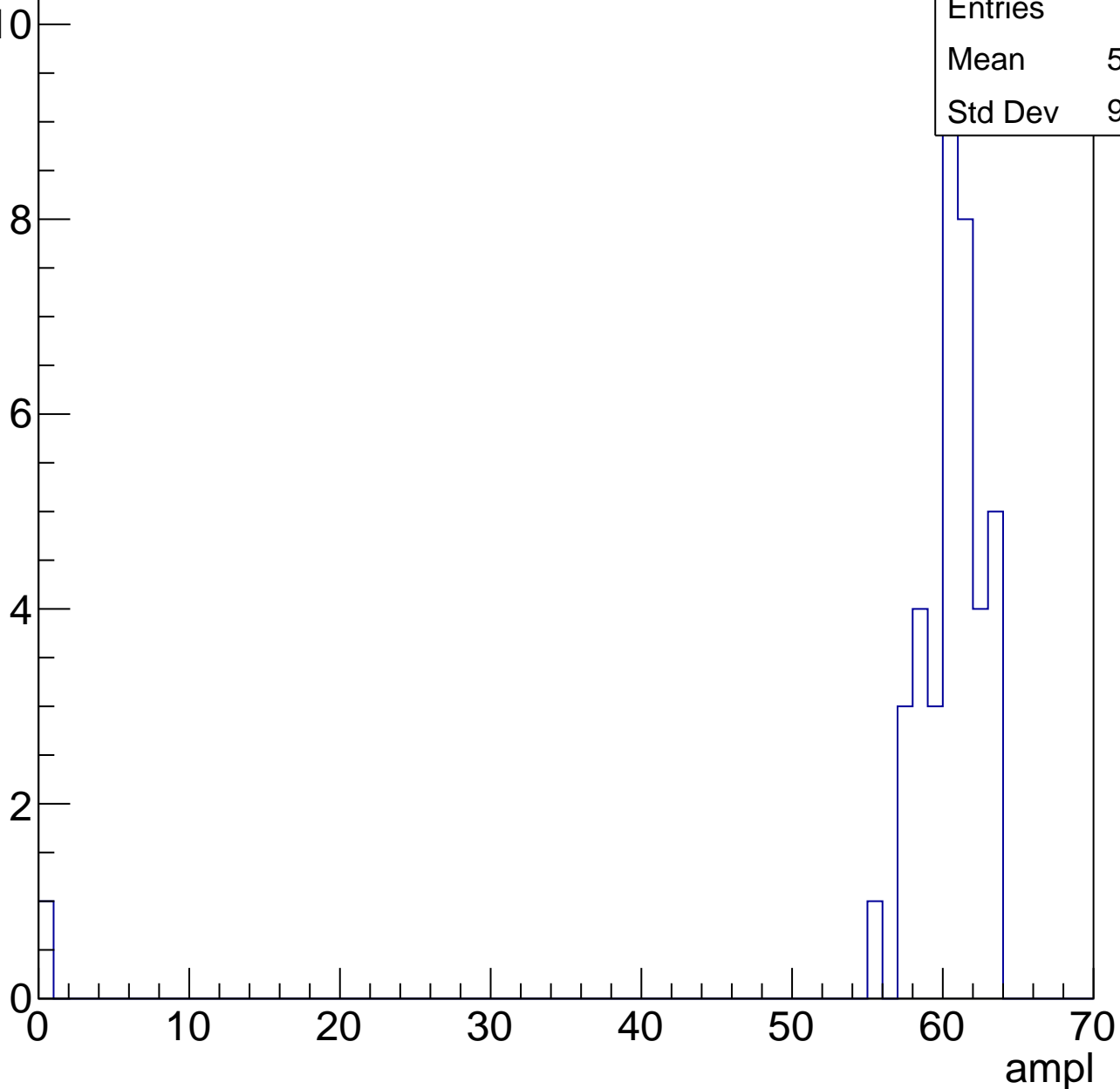


# B1L003S, U23-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

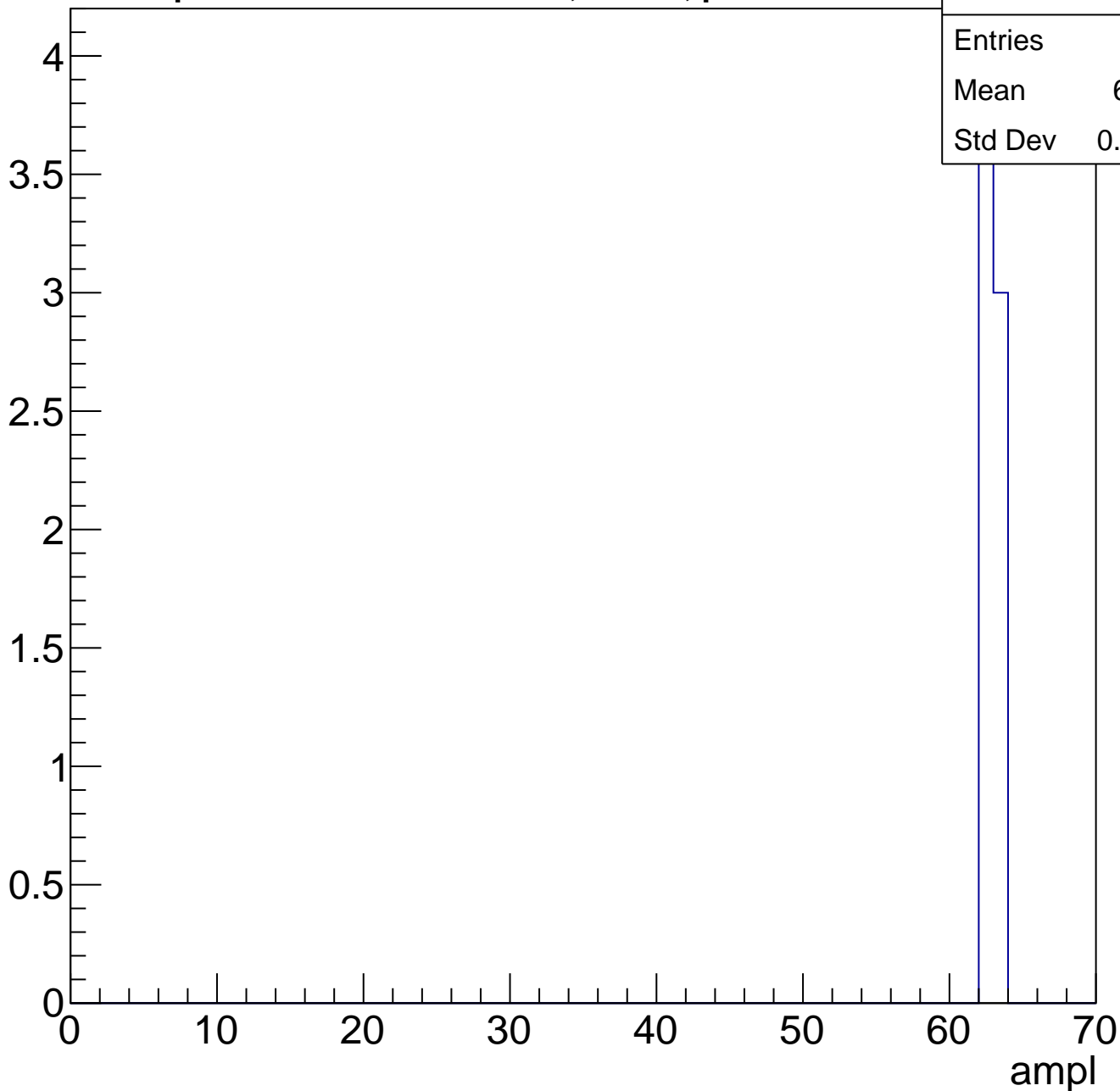
Entries	39
Mean	58.62
Std Dev	9.694



# B1L003S, U23-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	7
Mean	62.43
Std Dev	0.4949



# B1L003S, U23-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch110, adc0

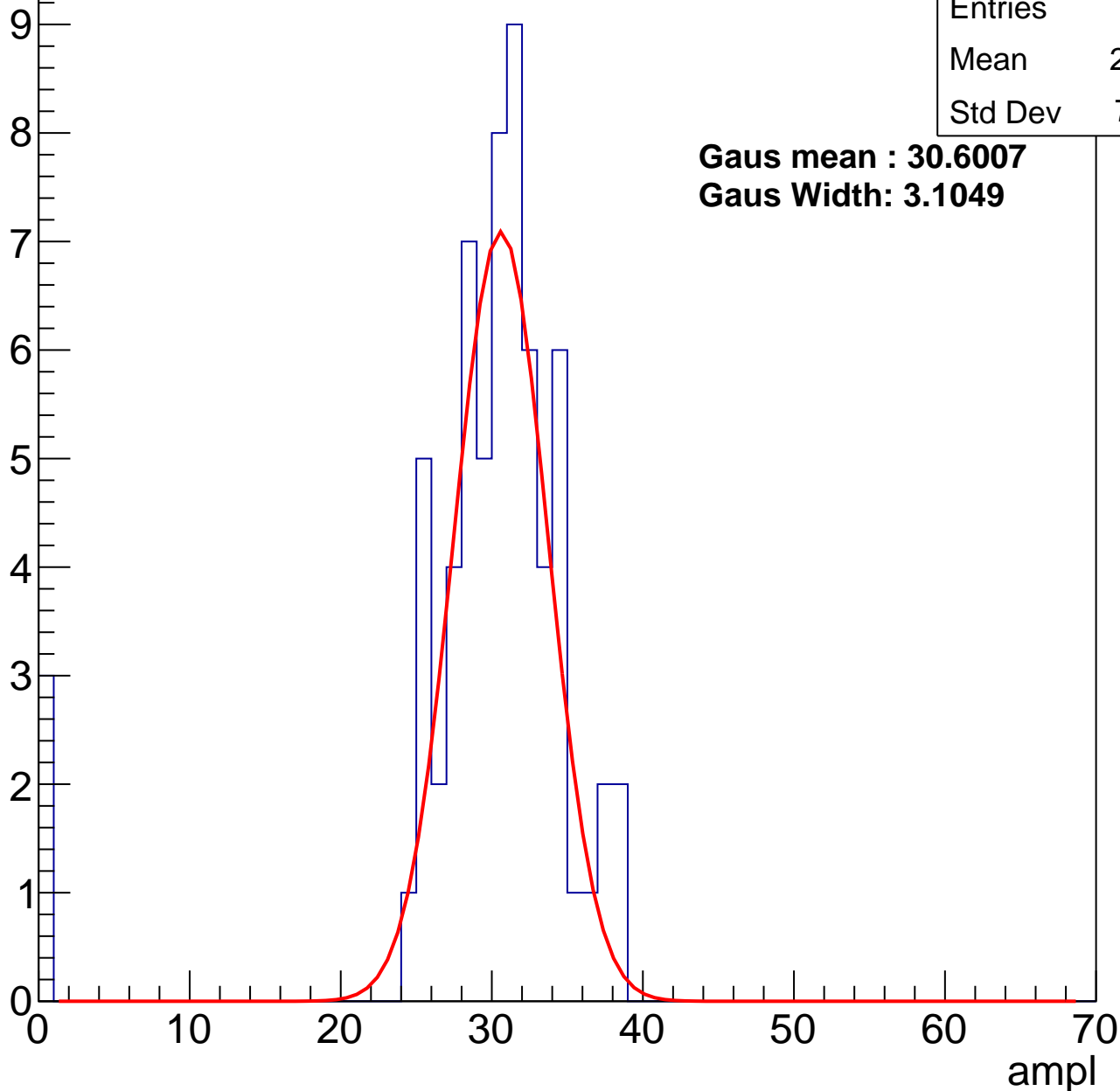
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.06
Std Dev	7.131

**Gaus mean : 30.6007**

**Gaus Width: 3.1049**



# B1L003S, U23-ch110, adc1

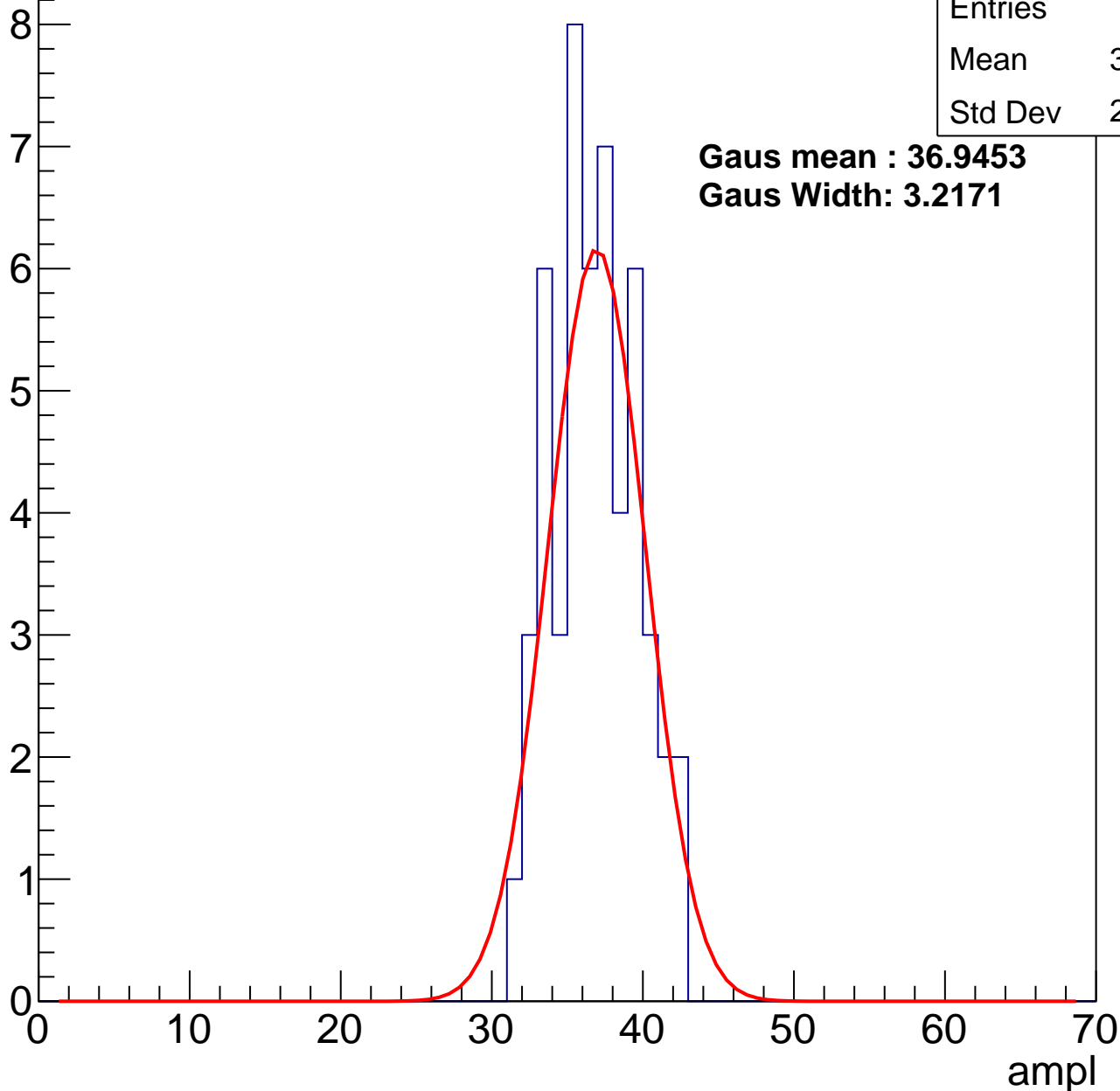
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	36.35
Std Dev	2.757

**Gaus mean : 36.9453**

**Gaus Width: 3.2171**



# B1L003S, U23-ch110, adc2

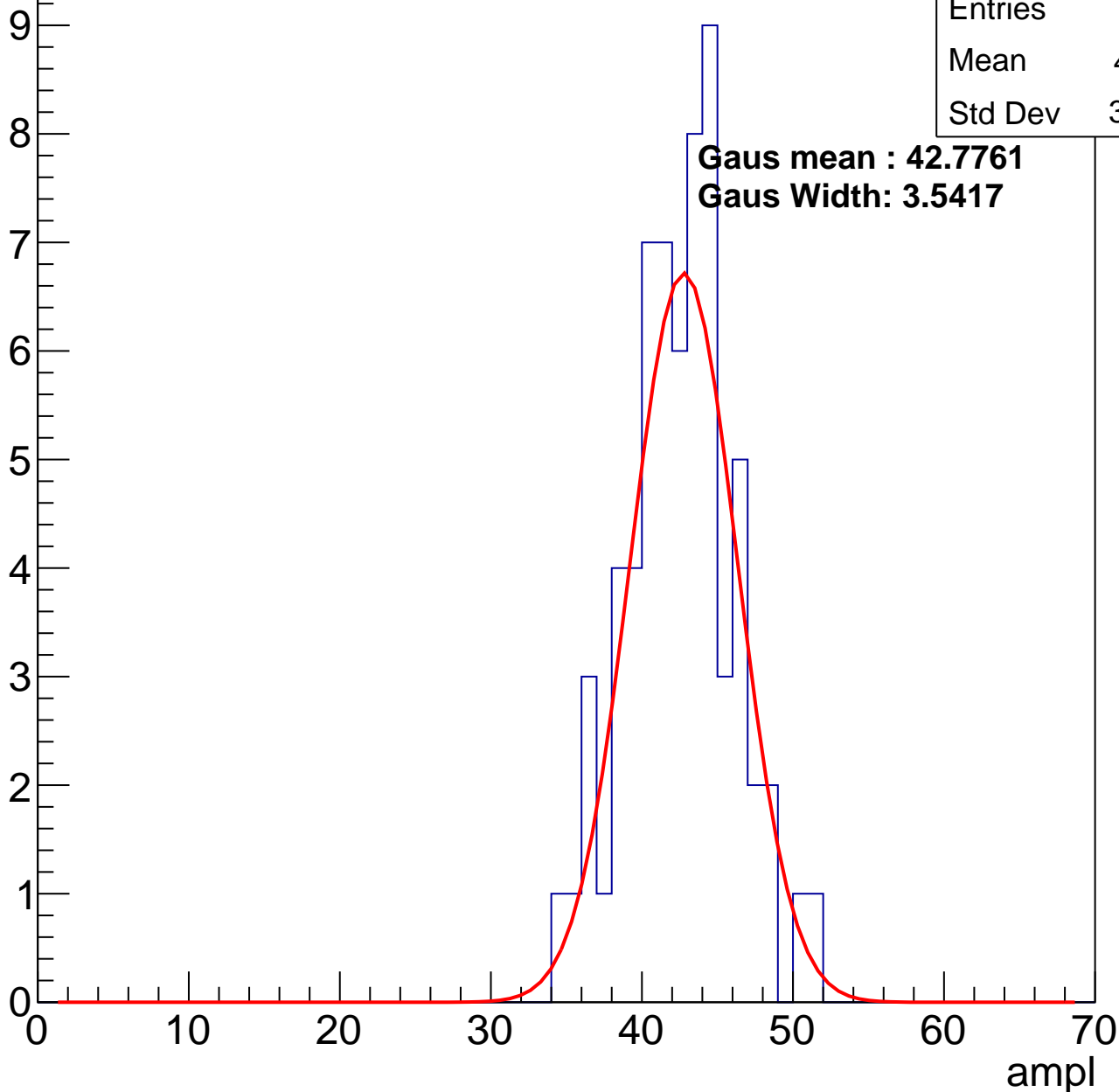
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.11
Std Dev	3.504

**Gaus mean : 42.7761**

**Gaus Width: 3.5417**

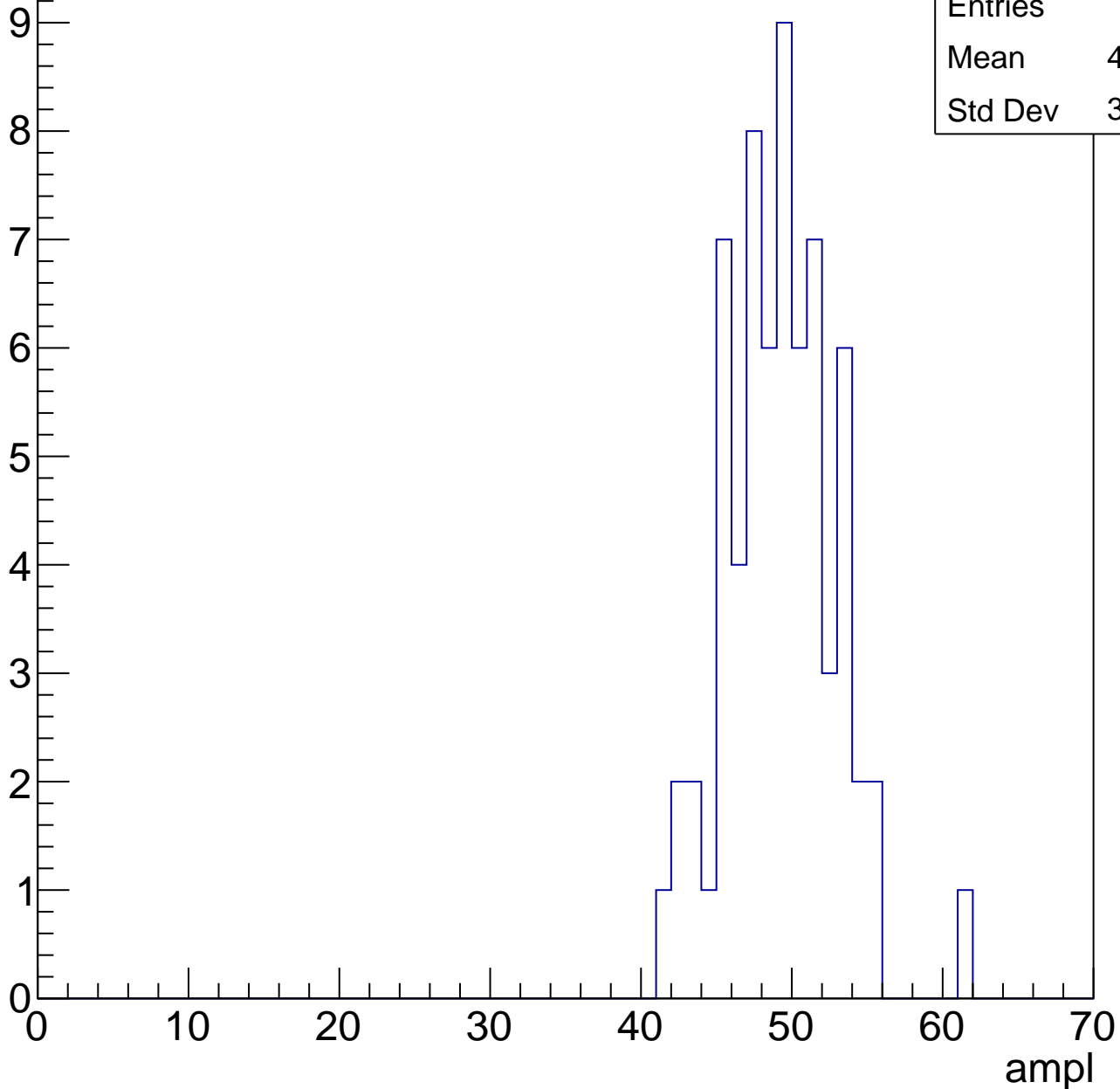


# B1L003S, U23-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	48.79
Std Dev	3.593

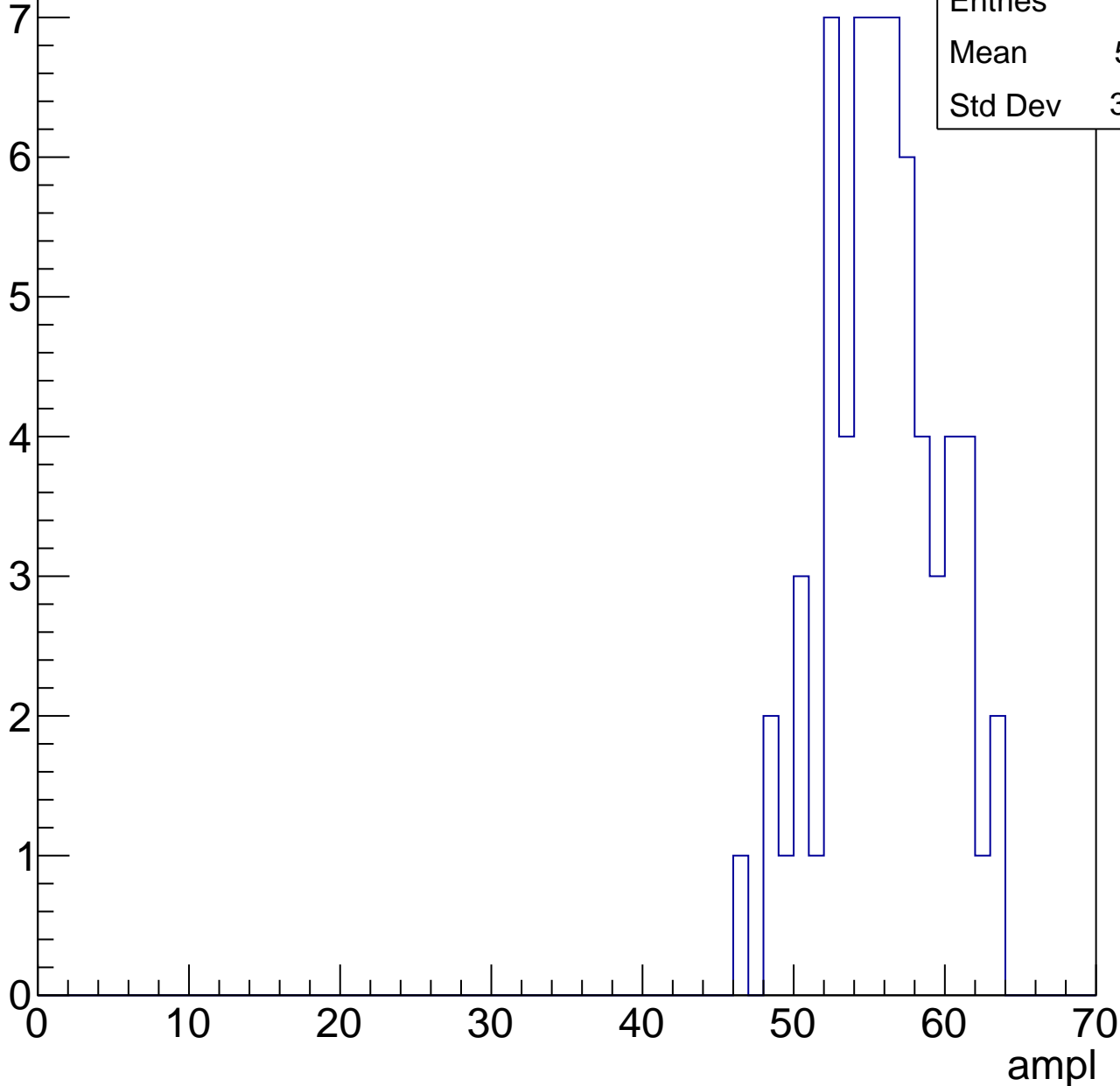


# B1L003S, U23-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.41
Std Dev	3.786

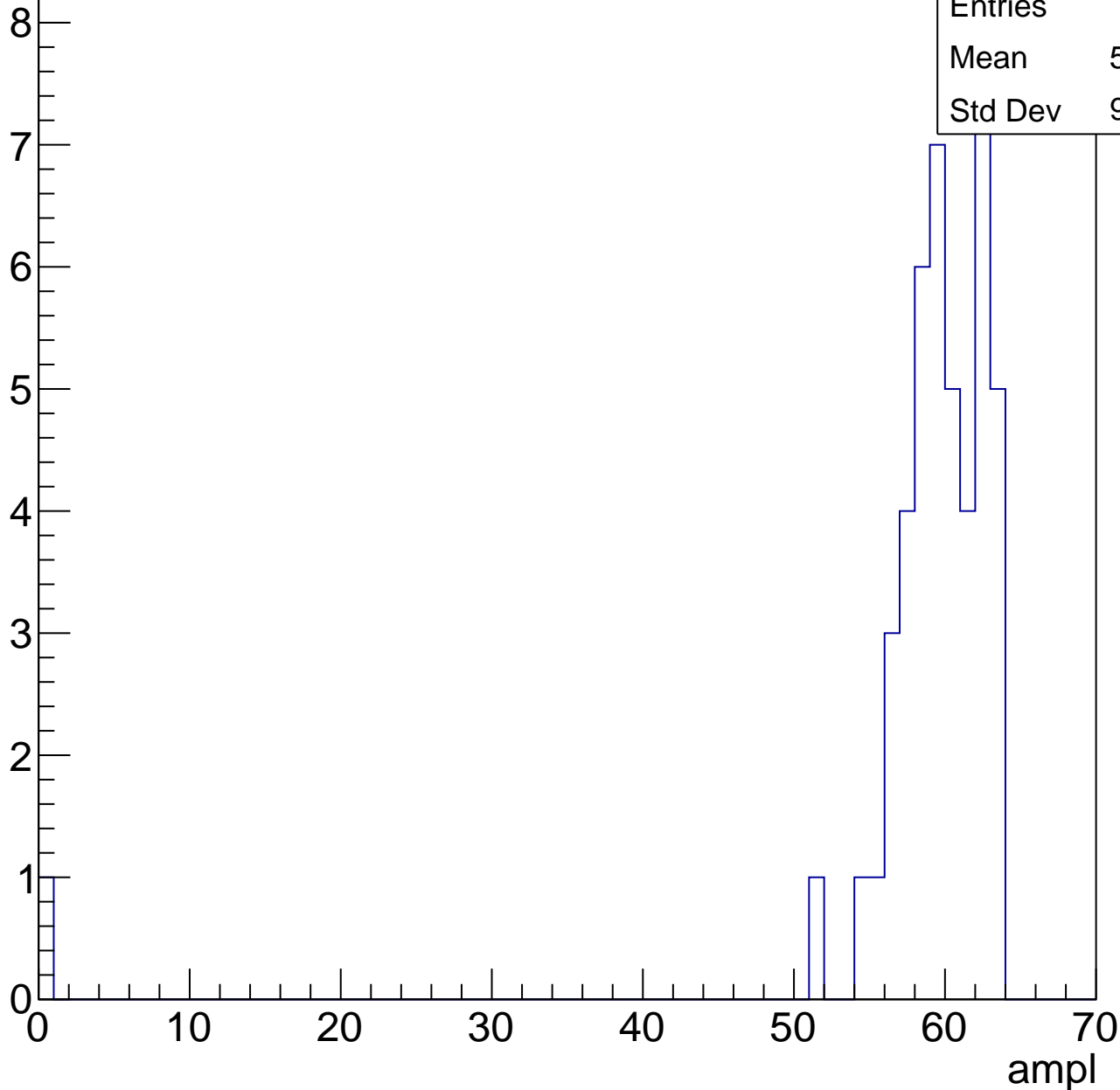


# B1L003S, U23-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.09
Std Dev	9.053



# B1L003S, U23-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch111, adc0

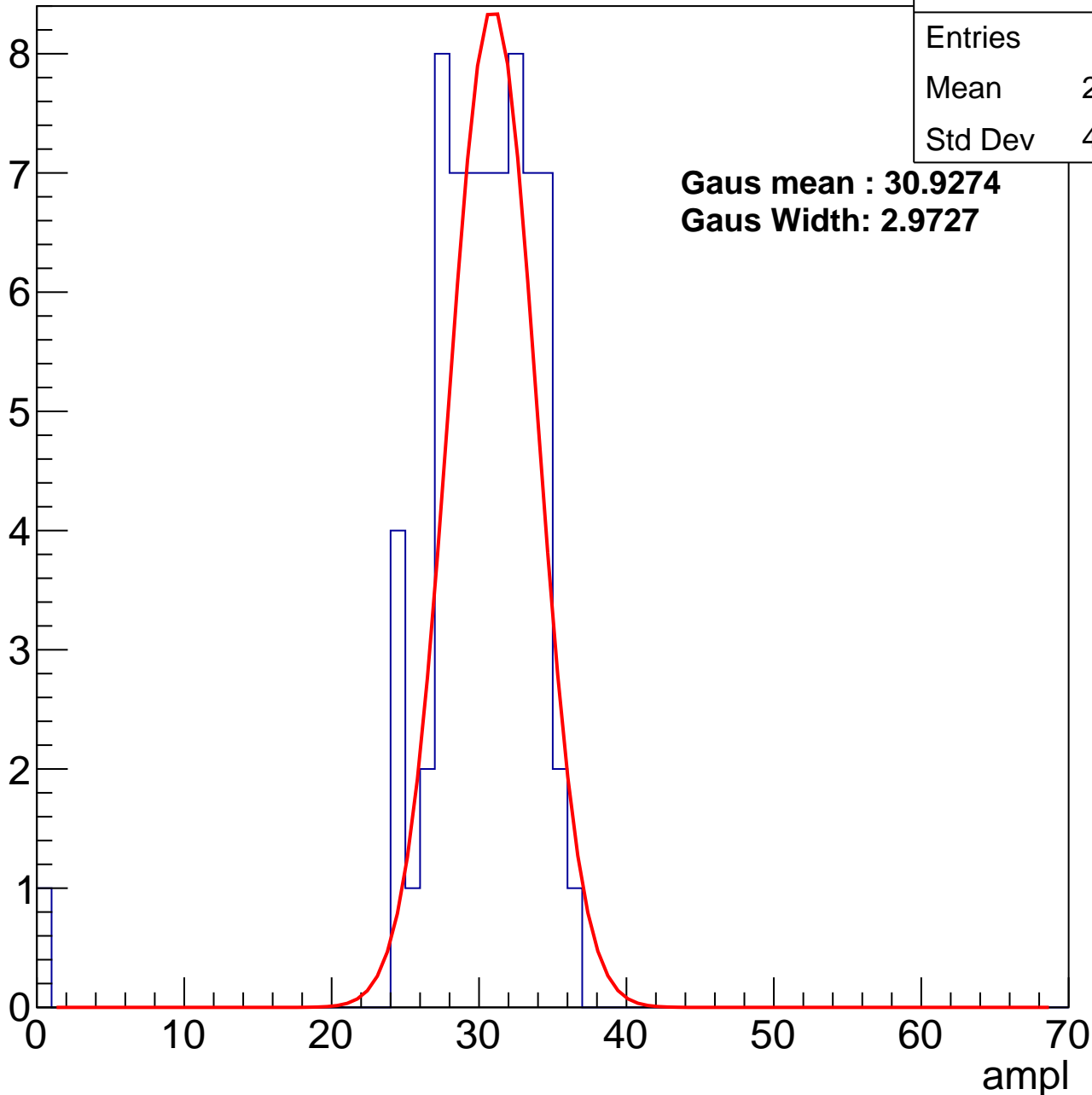
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.65
Std Dev	4.662

**Gaus mean : 30.9274**

**Gaus Width: 2.9727**



# B1L003S, U23-ch111, adc1

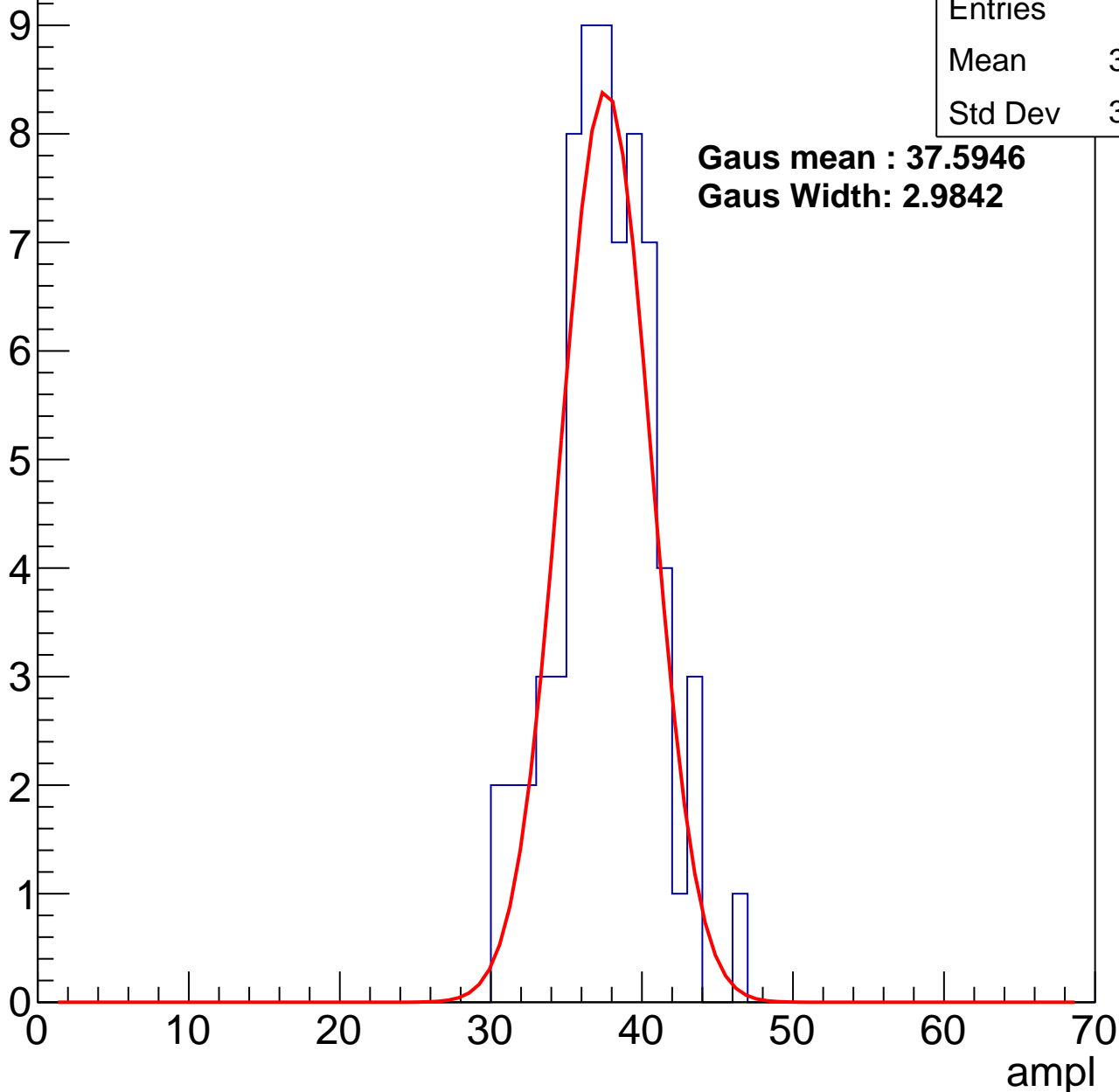
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	37.14
Std Dev	3.218

**Gaus mean : 37.5946**

**Gaus Width: 2.9842**



# B1L003S, U23-ch111, adc2

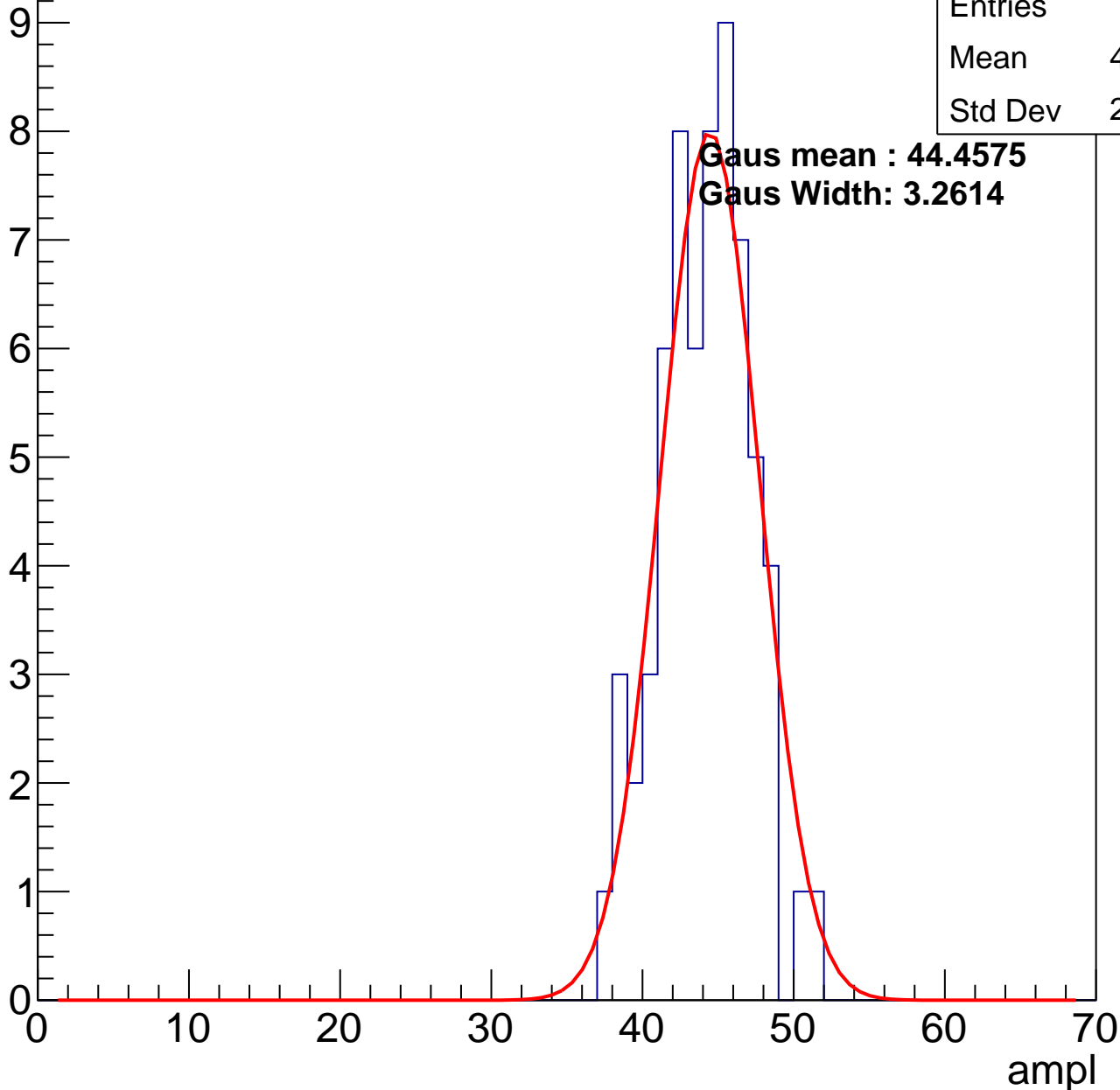
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.69
Std Dev	2.989

**Gaus mean : 44.4575**

**Gaus Width: 3.2614**



# B1L003S, U23-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

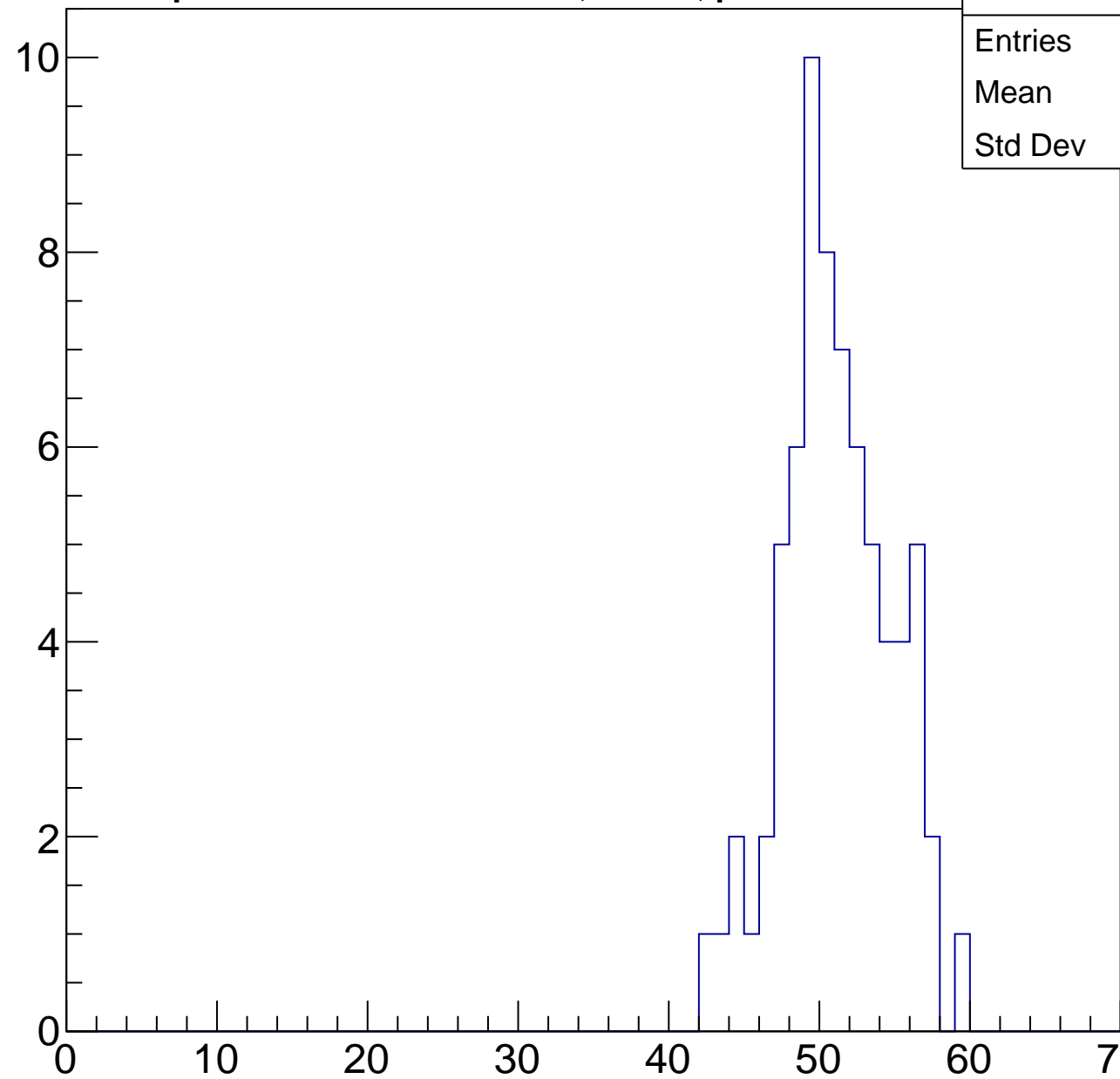
Entries	70
Mean	50.66
Std Dev	3.565

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

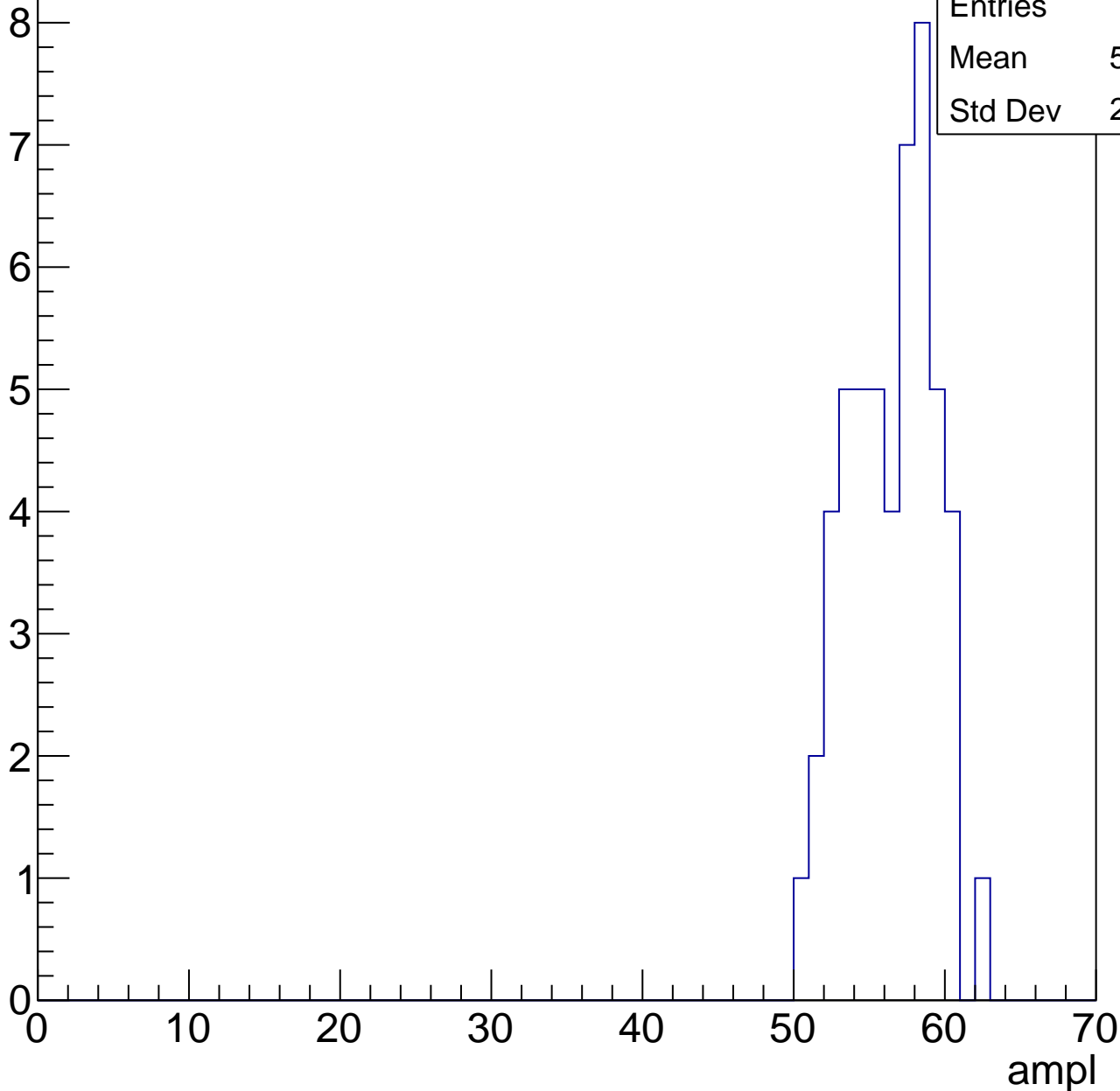


# B1L003S, U23-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	55.96
Std Dev	2.814

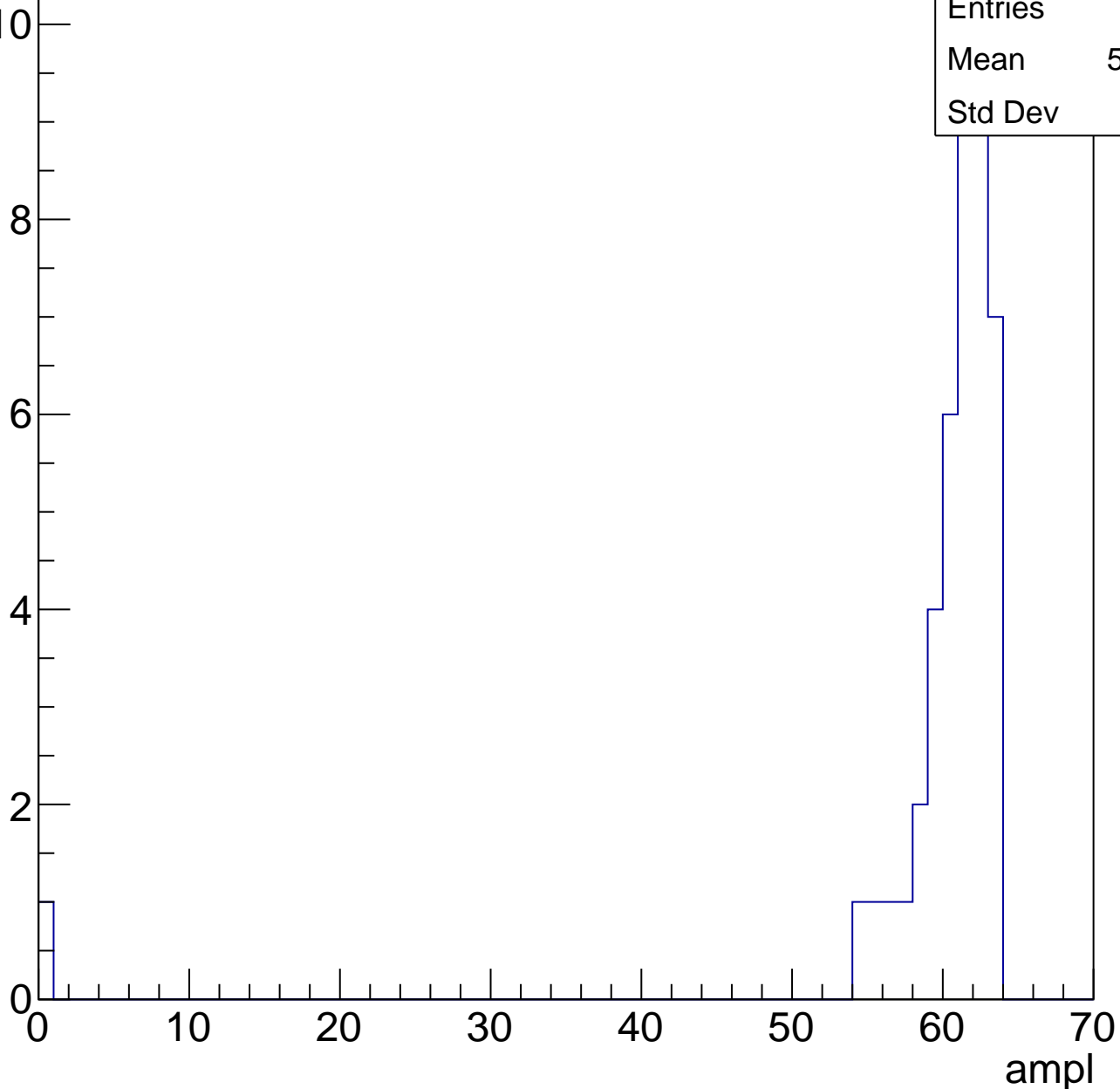


# B1L003S, U23-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	59.14
Std Dev	9.37



# B1L003S, U23-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch112, adc0

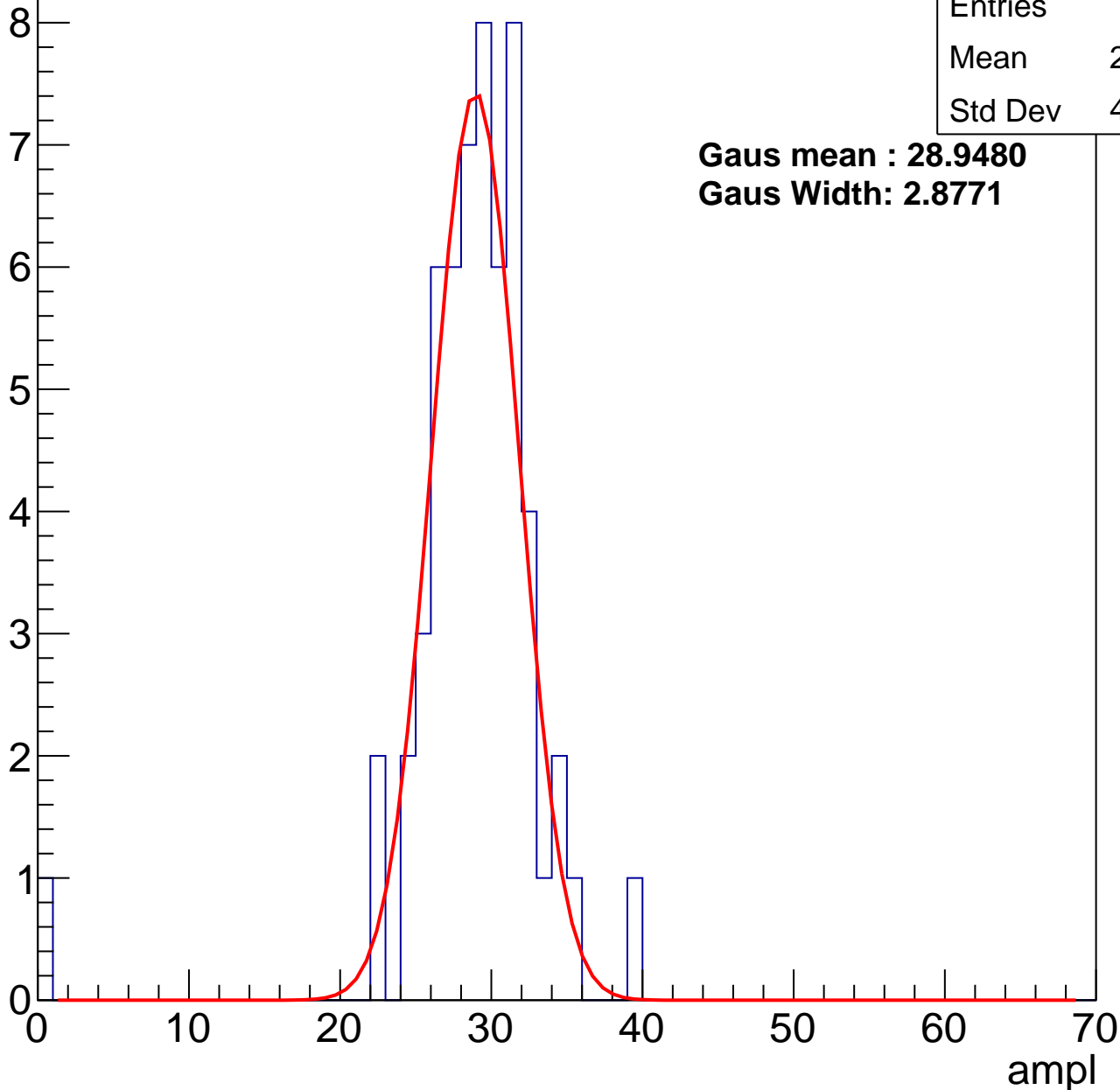
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.34
Std Dev	4.866

**Gaus mean : 28.9480**

**Gaus Width: 2.8771**



# B1L003S, U23-ch112, adc1

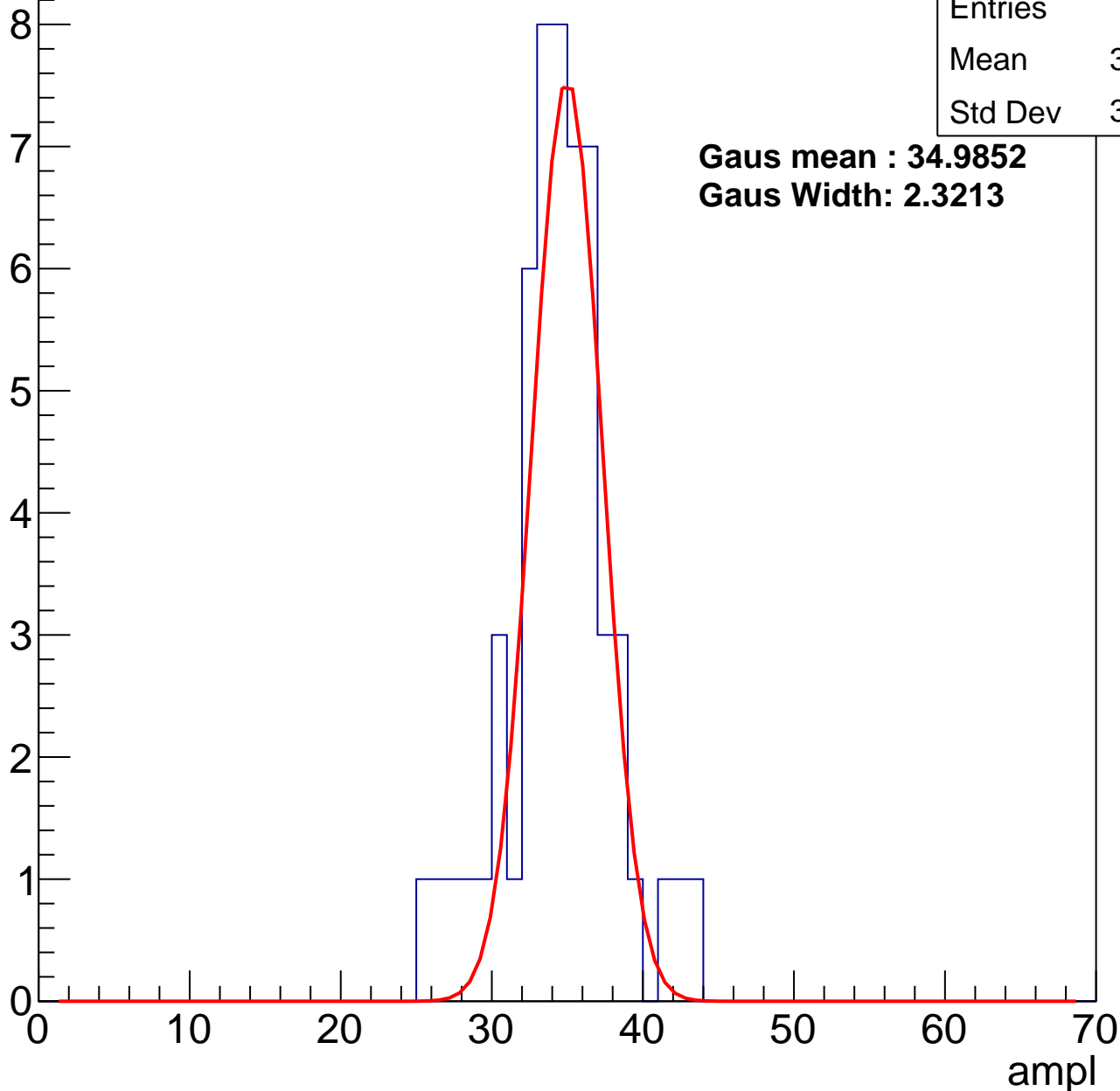
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	34.02
Std Dev	3.498

**Gaus mean : 34.9852**

**Gaus Width: 2.3213**



# B1L003S, U23-ch112, adc2

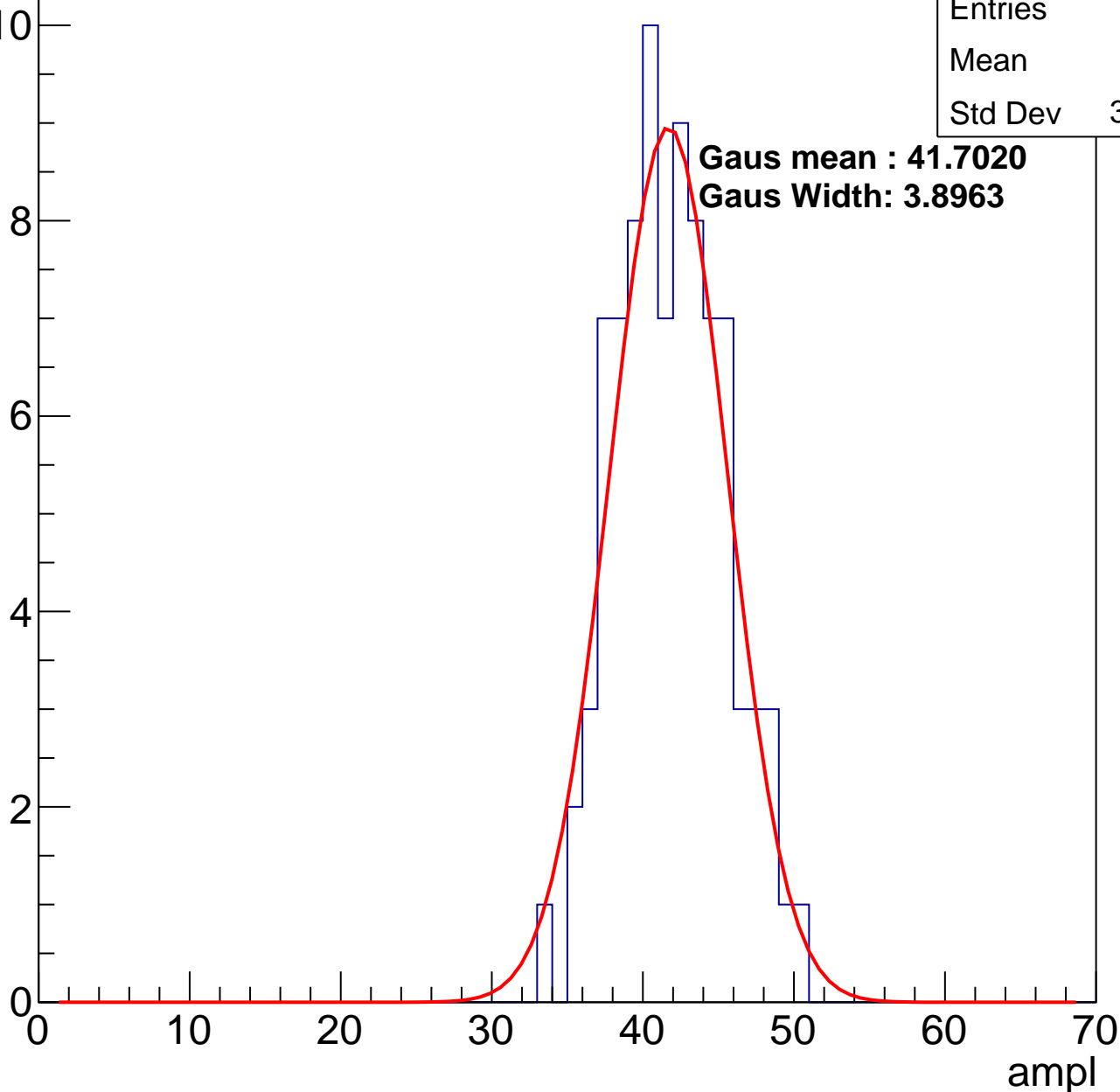
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	87
Mean	41.4
Std Dev	3.564

**Gaus mean : 41.7020**

**Gaus Width: 3.8963**

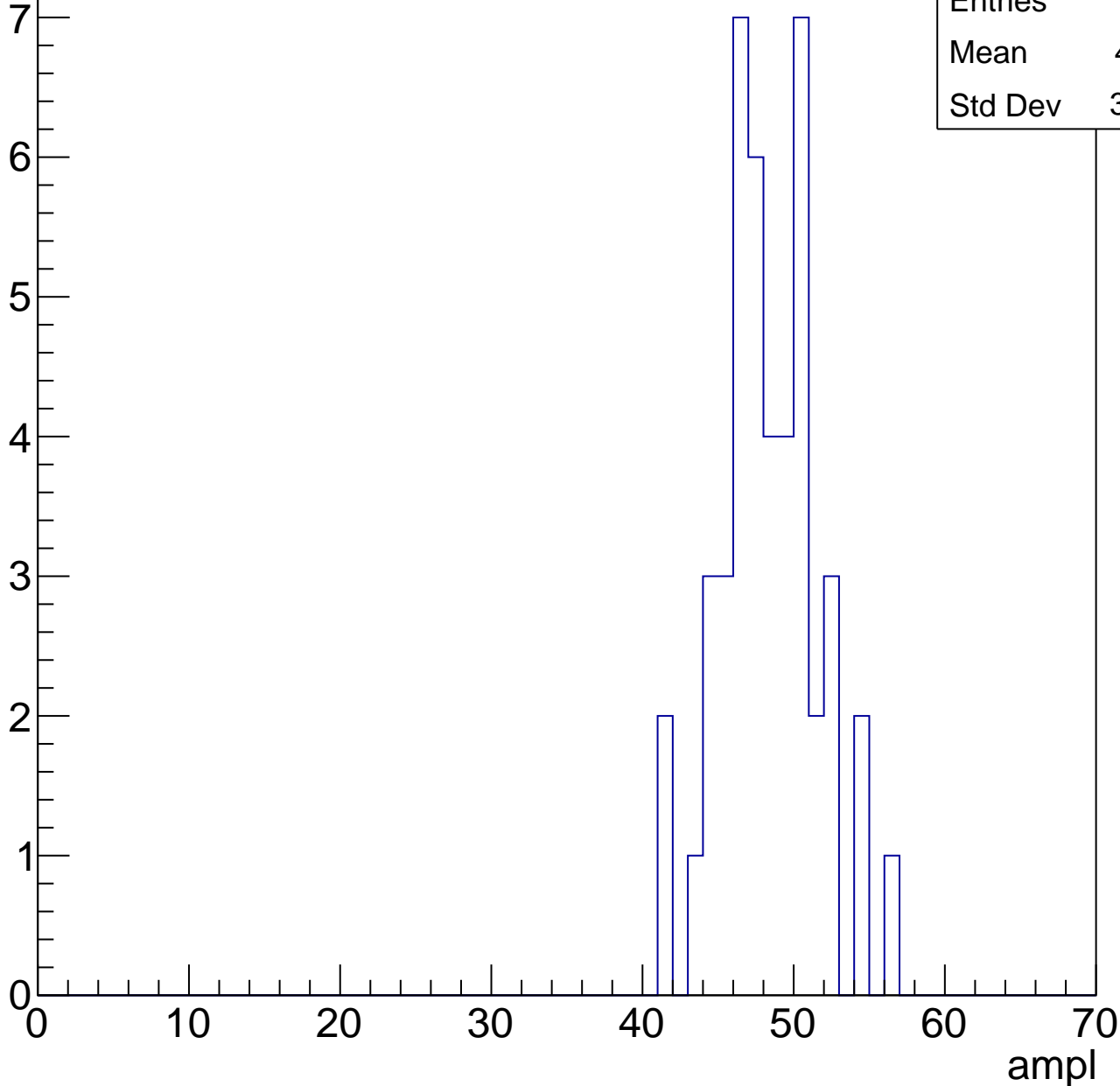


# B1L003S, U23-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	47.91
Std Dev	3.217

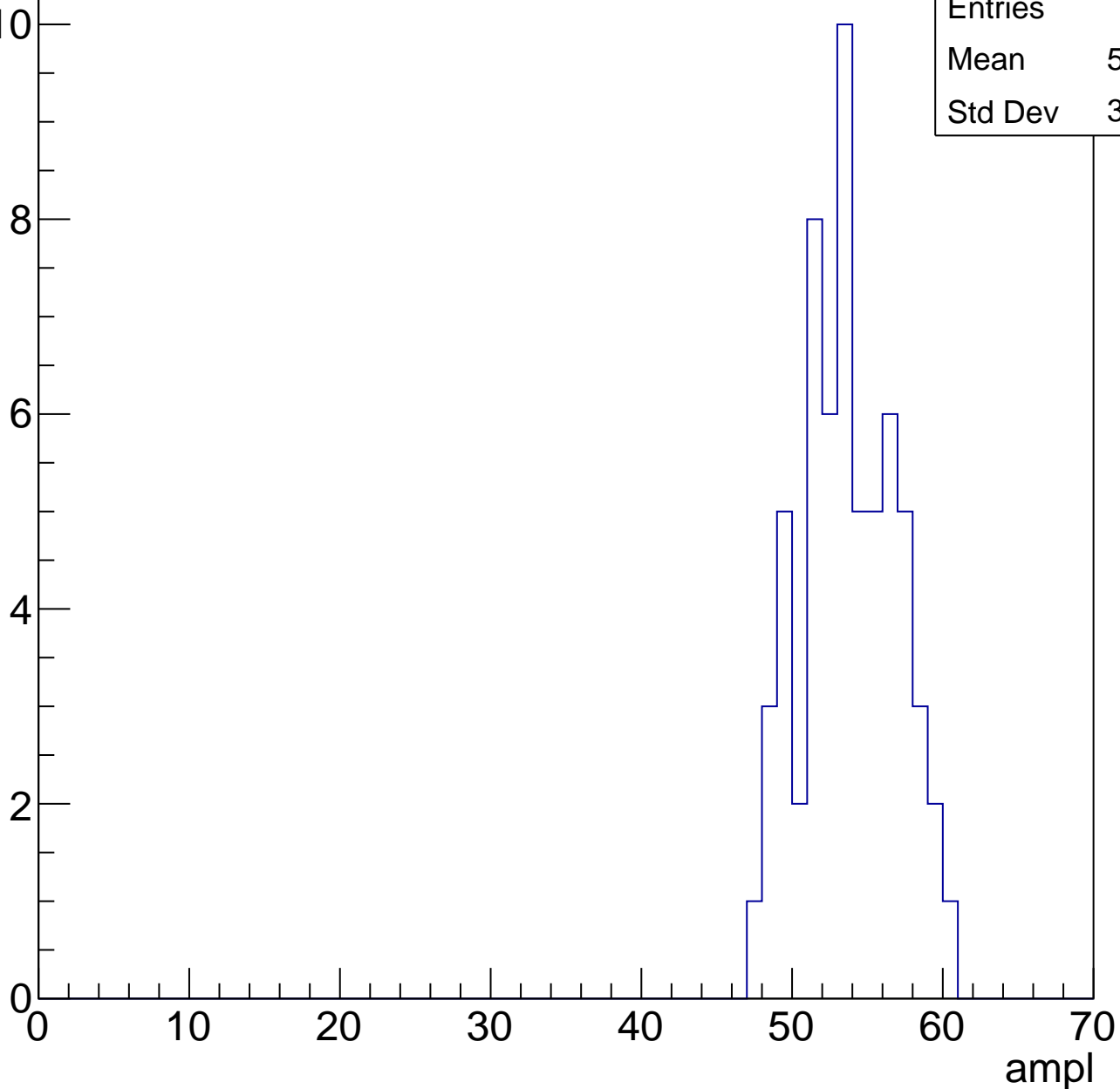


# B1L003S, U23-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	53.29
Std Dev	3.102

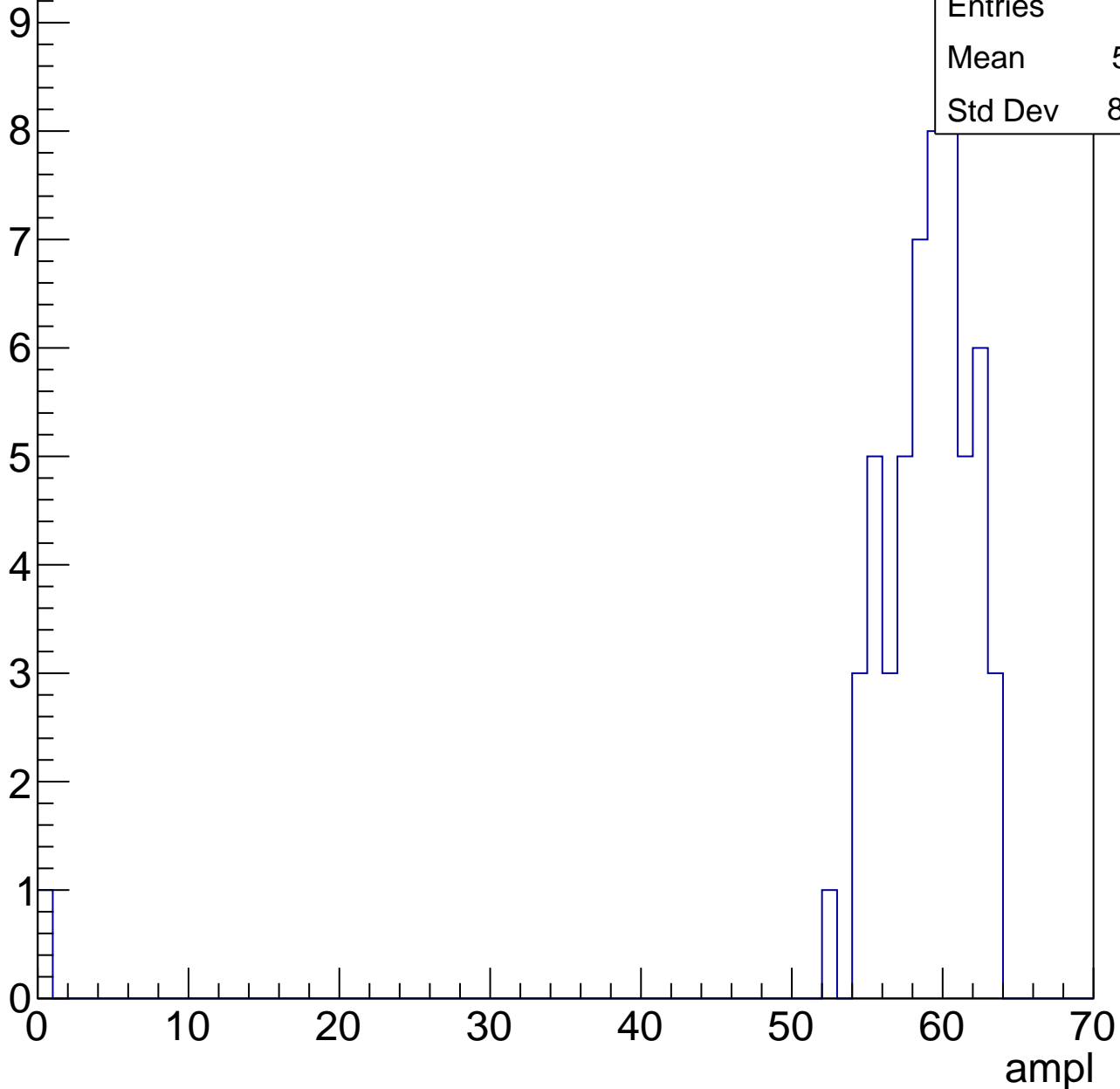


# B1L003S, U23-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	57.61
Std Dev	8.196

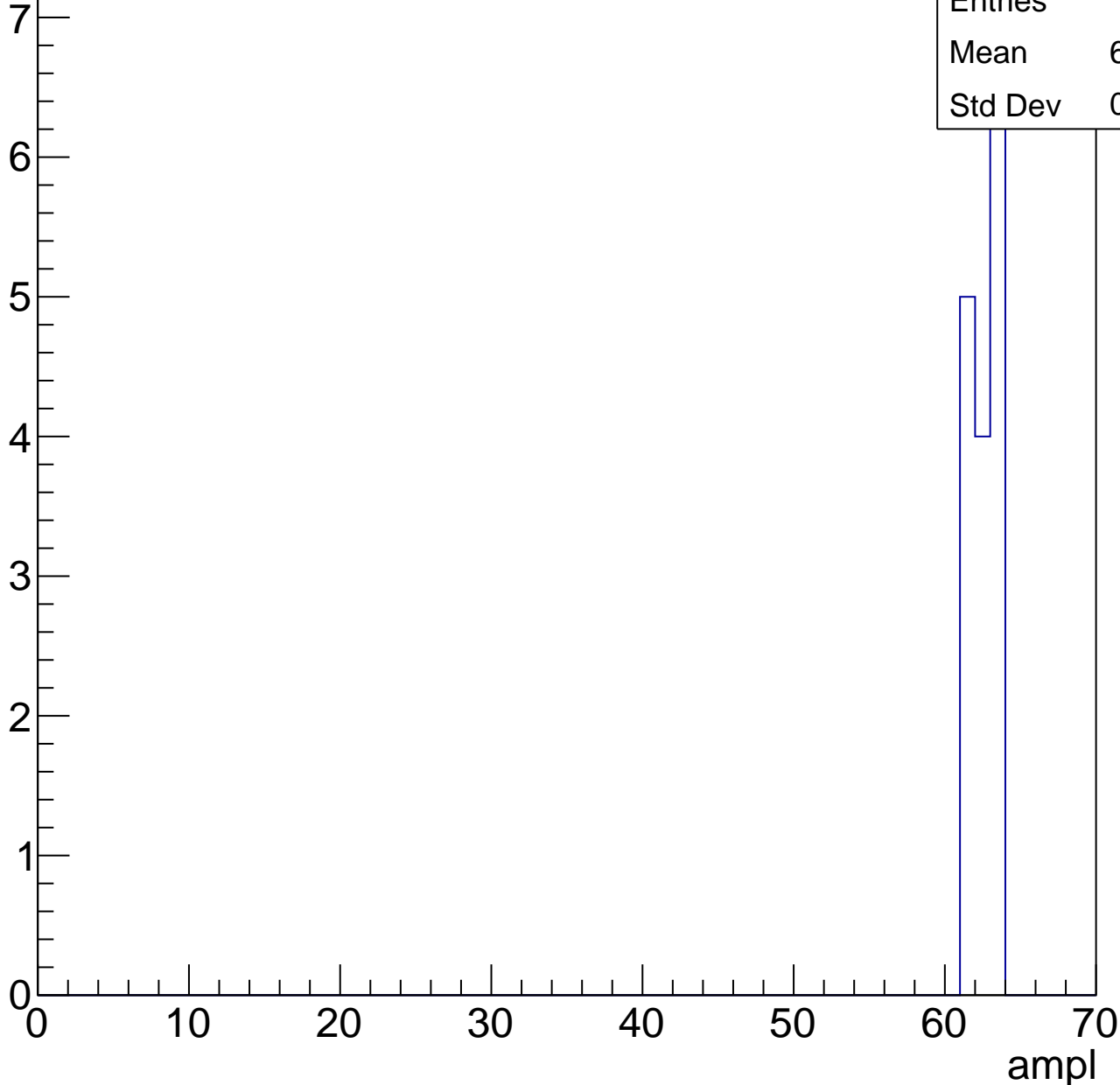


# B1L003S, U23-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	16
Mean	62.12
Std Dev	0.857





# B1L003S, U23-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch113, adc0

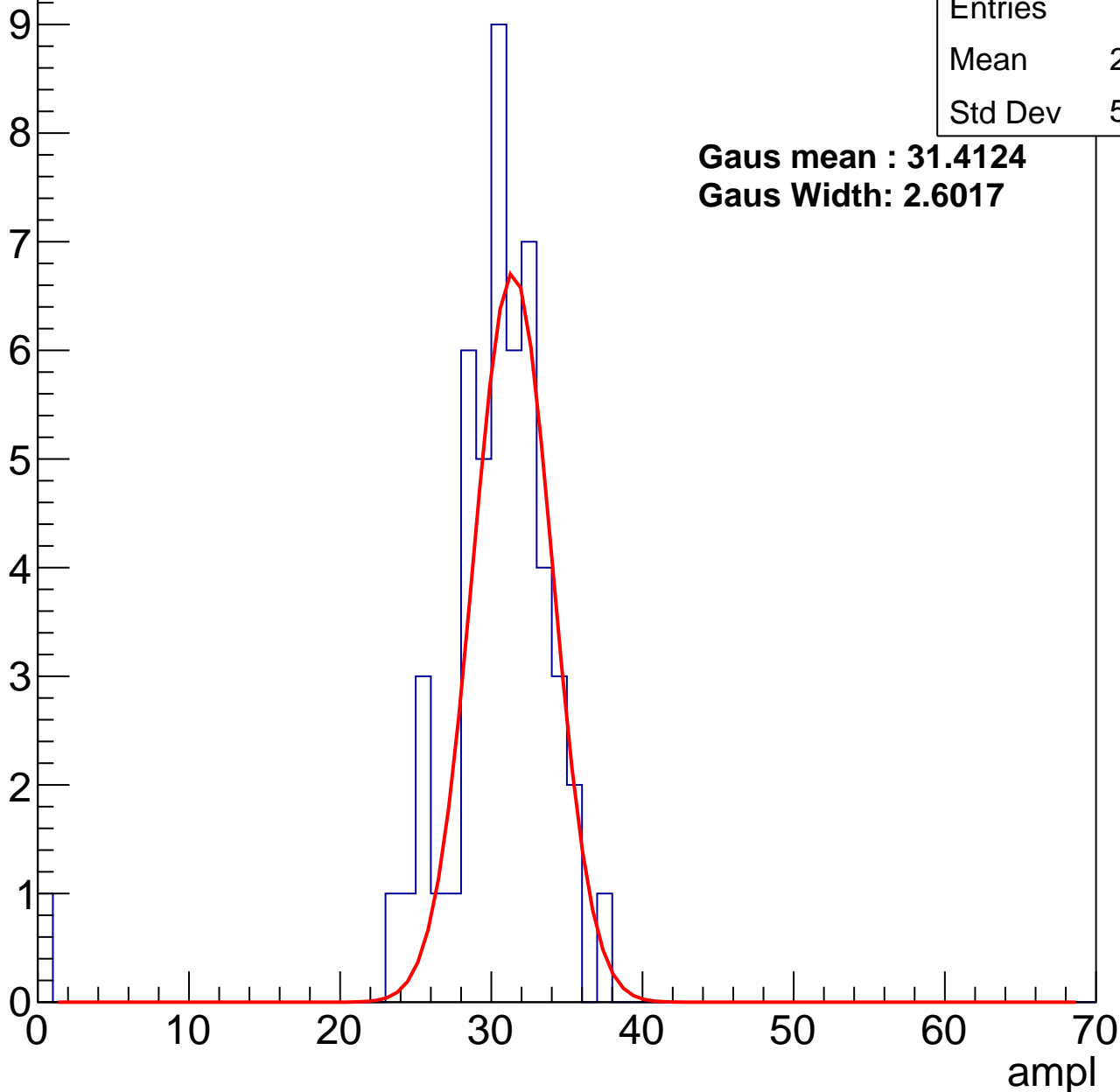
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	29.59
Std Dev	5.092

**Gaus mean : 31.4124**

**Gaus Width: 2.6017**



# B1L003S, U23-ch113, adc1

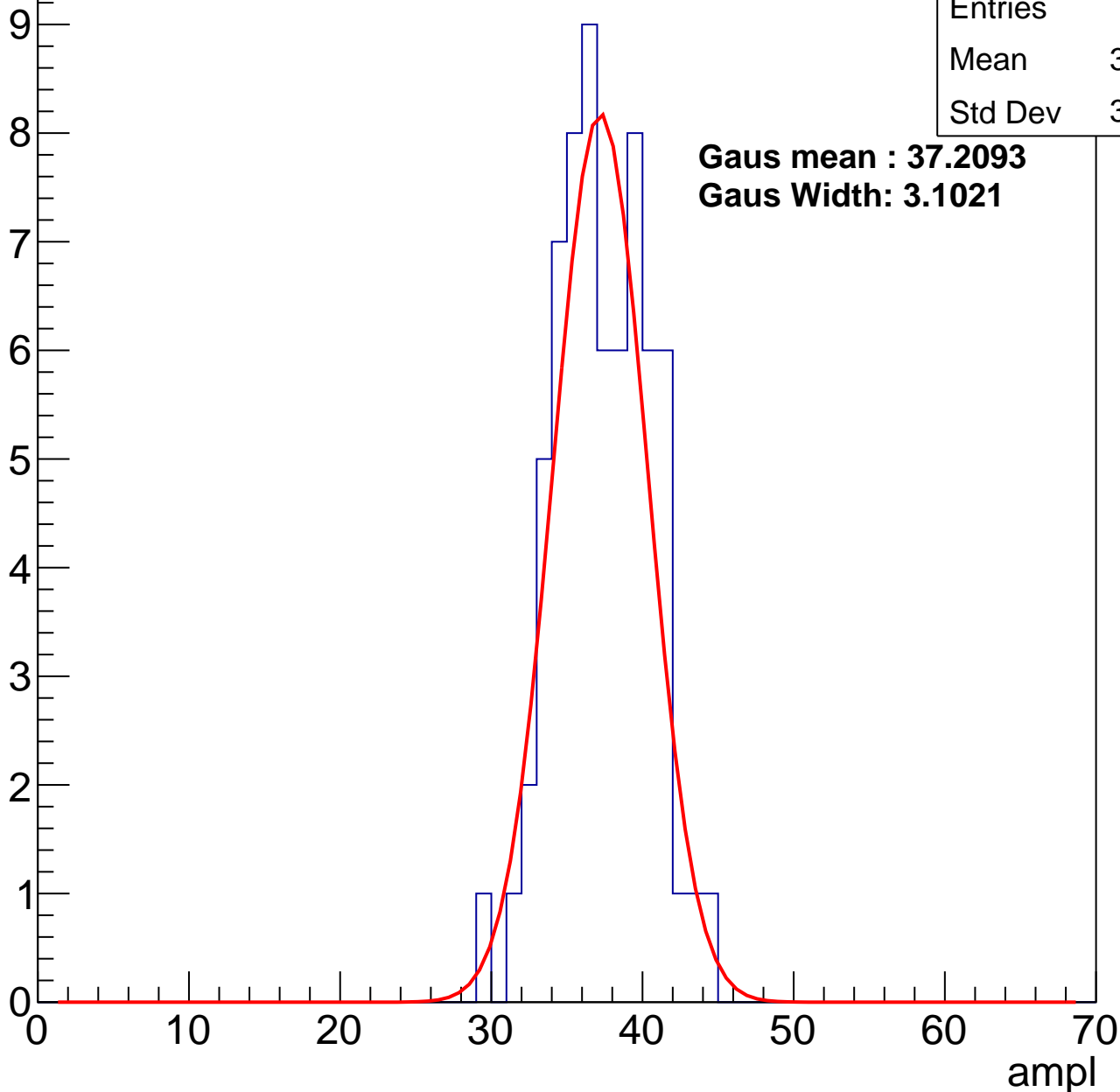
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	36.88
Std Dev	3.046

**Gaus mean : 37.2093**

**Gaus Width: 3.1021**



# B1L003S, U23-ch113, adc2

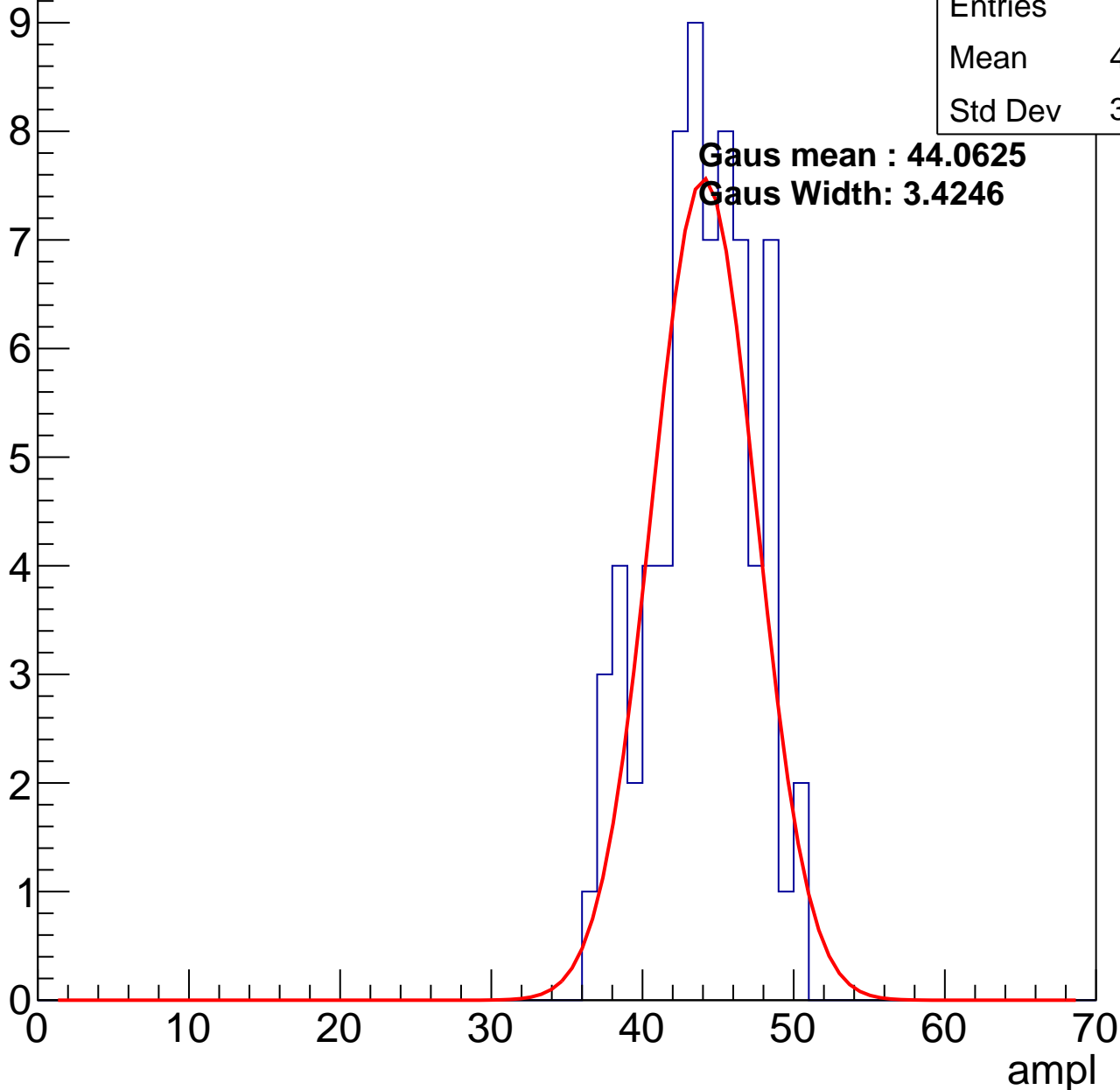
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	43.48
Std Dev	3.373

**Gaus mean : 44.0625**

**Gaus Width: 3.4246**

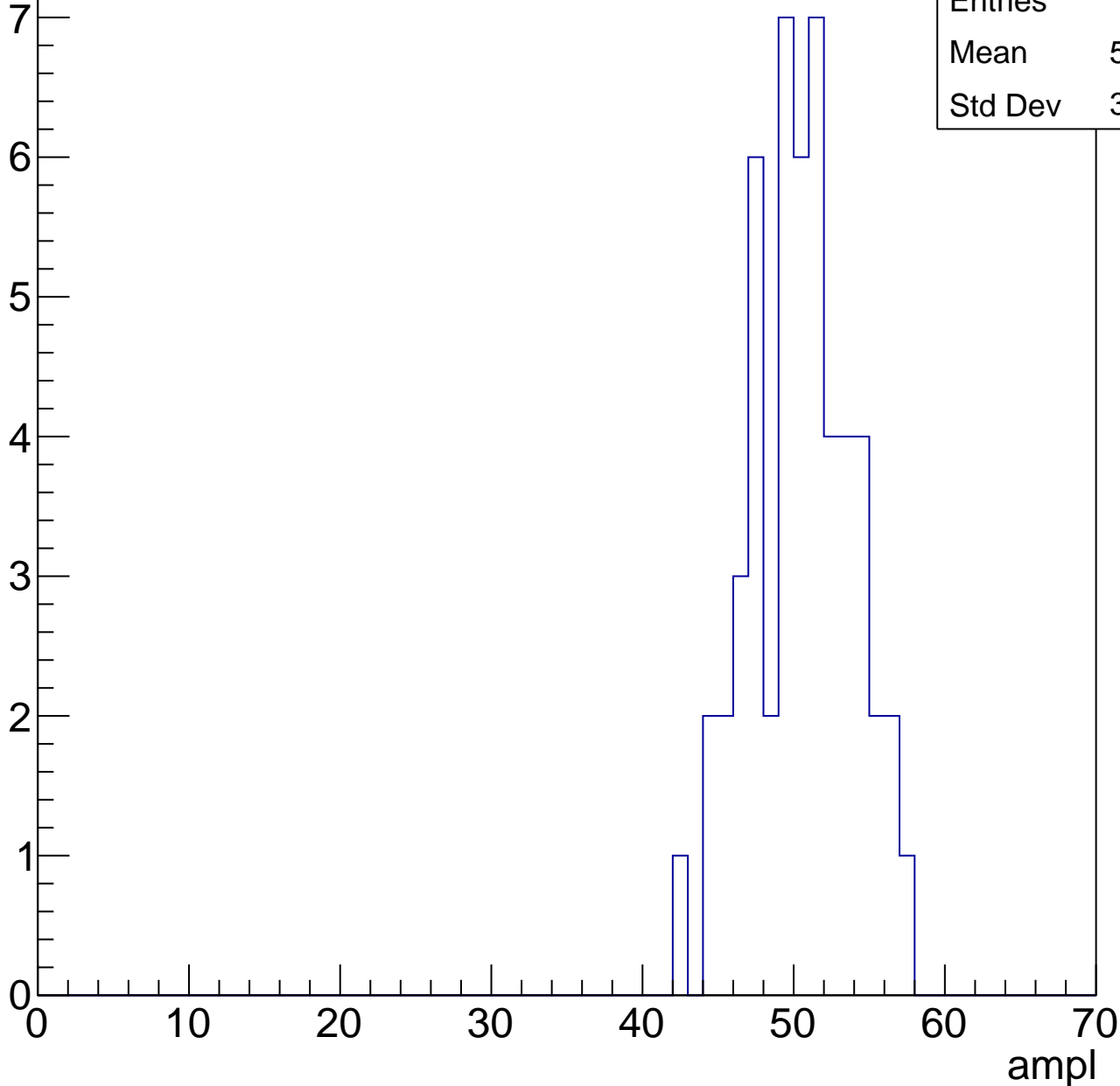


# B1L003S, U23-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	50.02
Std Dev	3.356

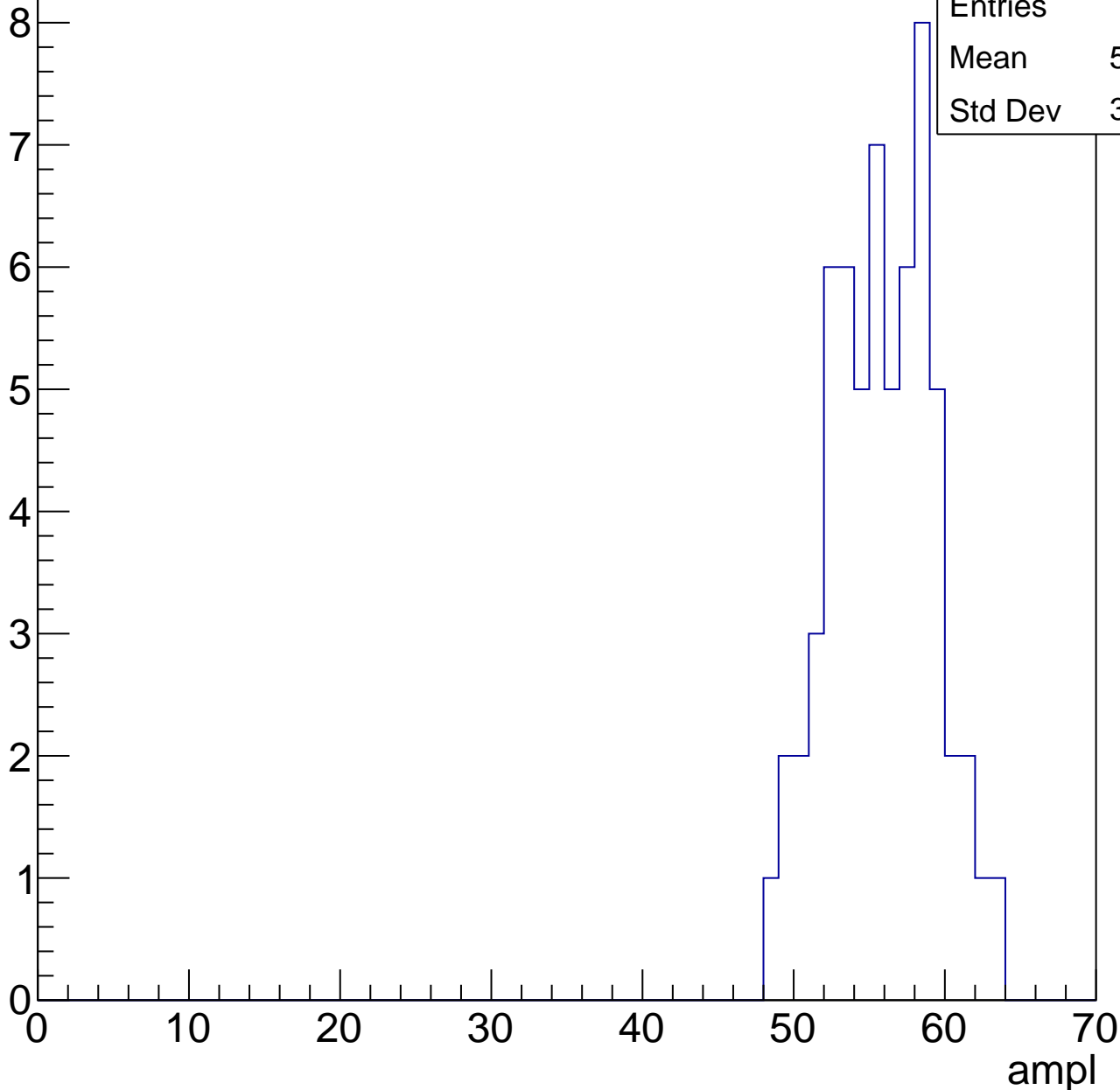


# B1L003S, U23-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	55.35
Std Dev	3.384

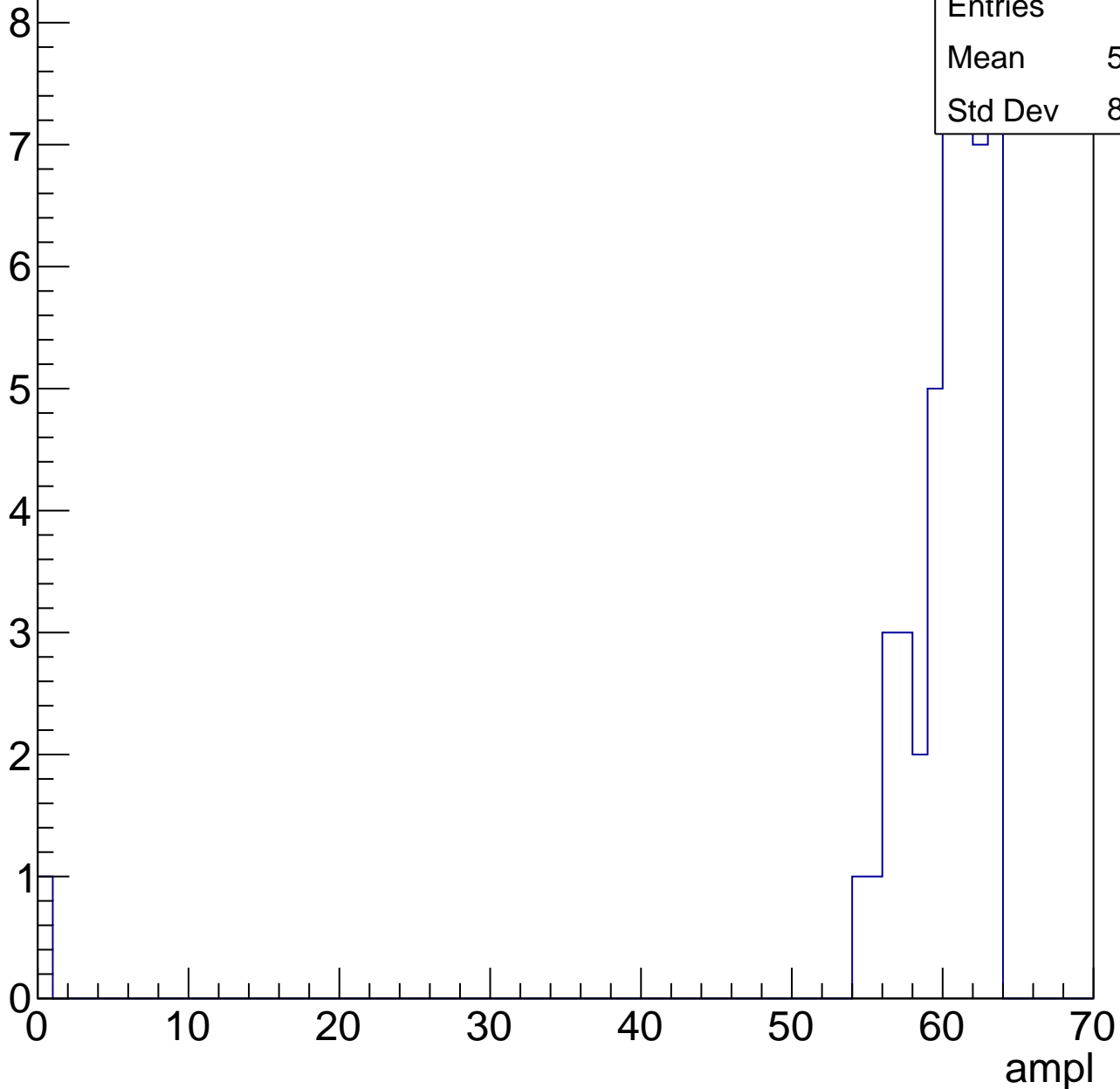


# B1L003S, U23-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

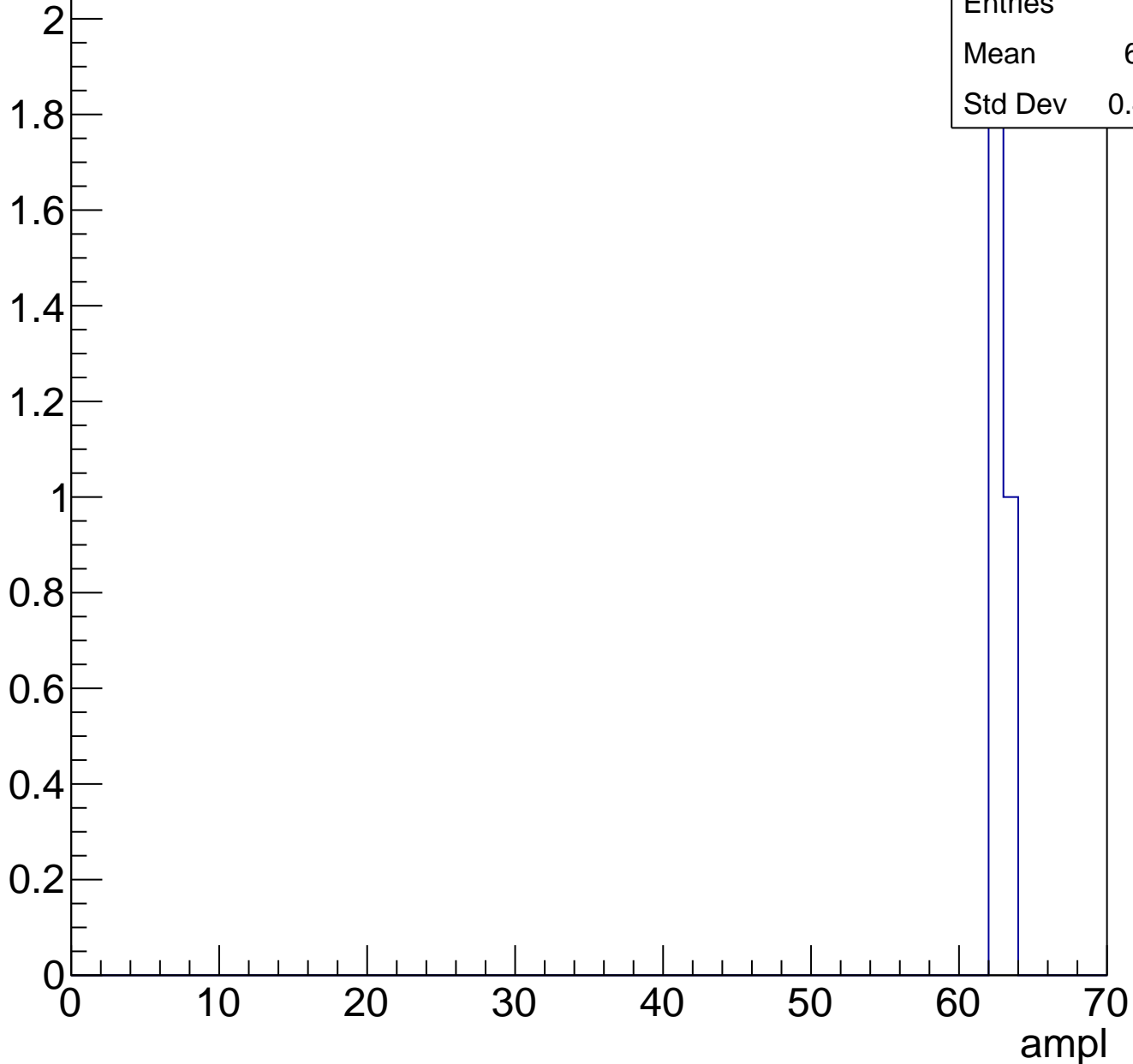
Entries	47
Mean	58.83
Std Dev	8.983



# B1L003S, U23-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch114, adc0

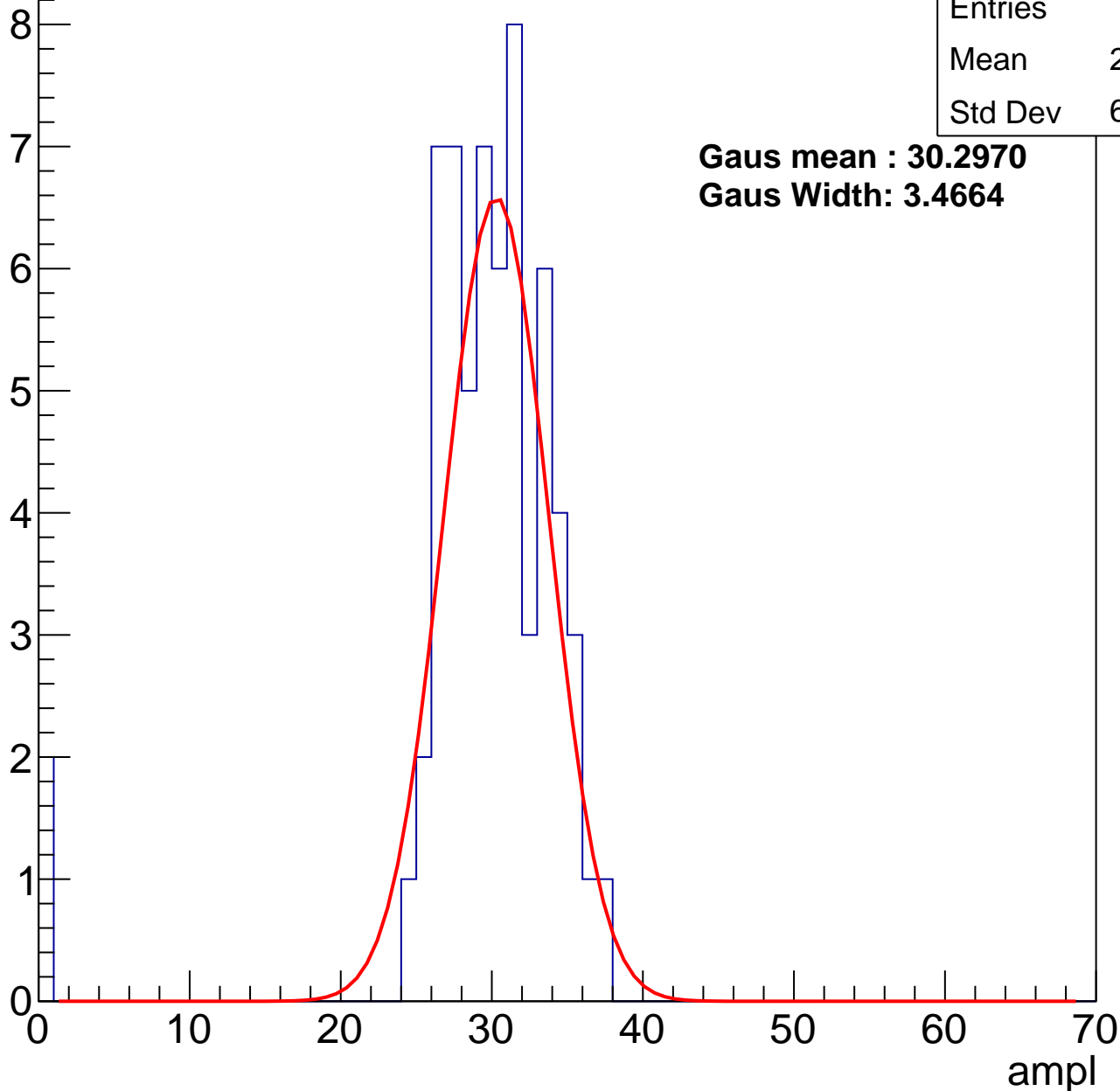
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	28.95
Std Dev	6.064

**Gaus mean : 30.2970**

**Gaus Width: 3.4664**



# B1L003S, U23-ch114, adc1

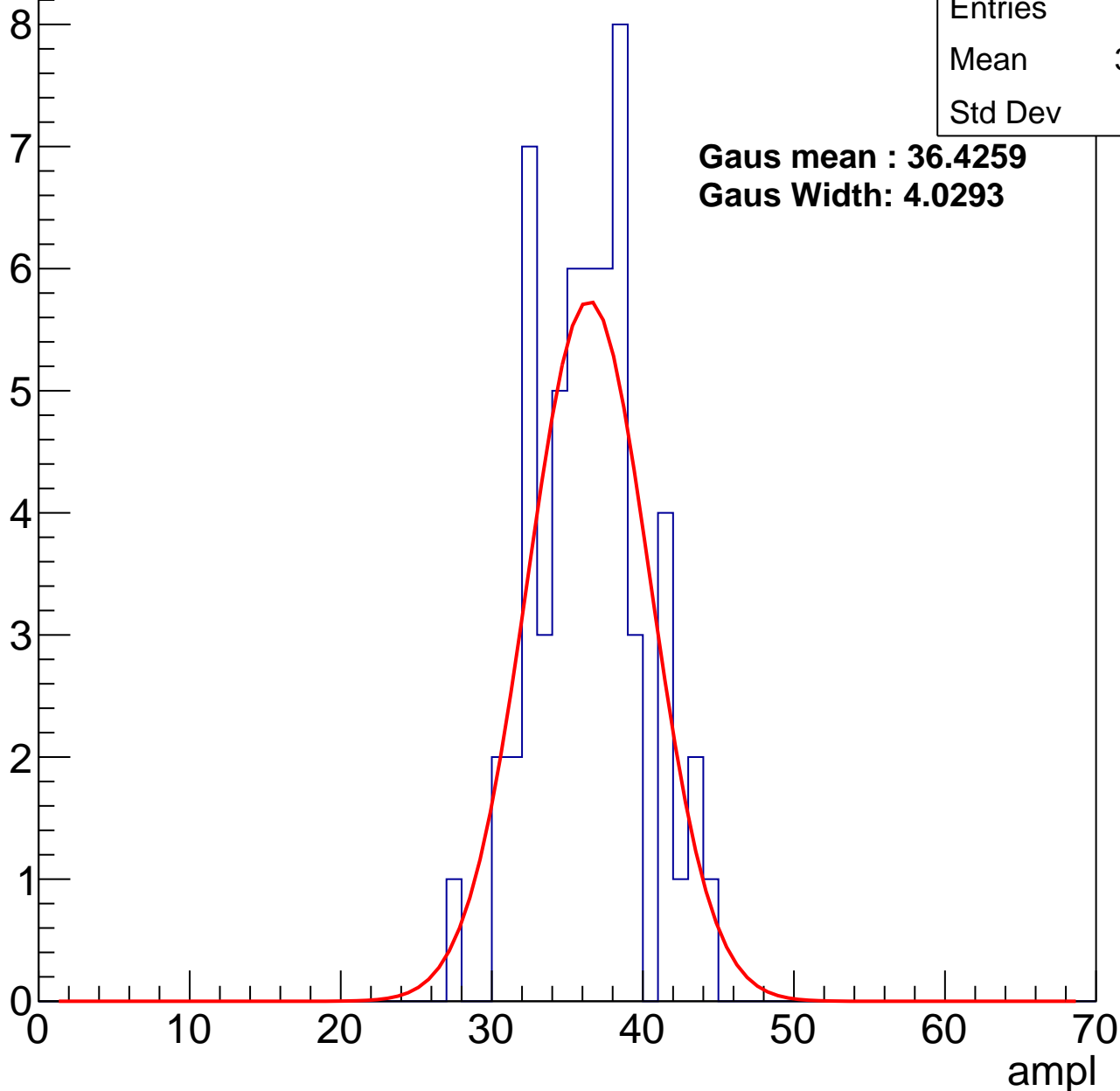
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.91
Std Dev	3.58

**Gaus mean : 36.4259**

**Gaus Width: 4.0293**



# B1L003S, U23-ch114, adc2

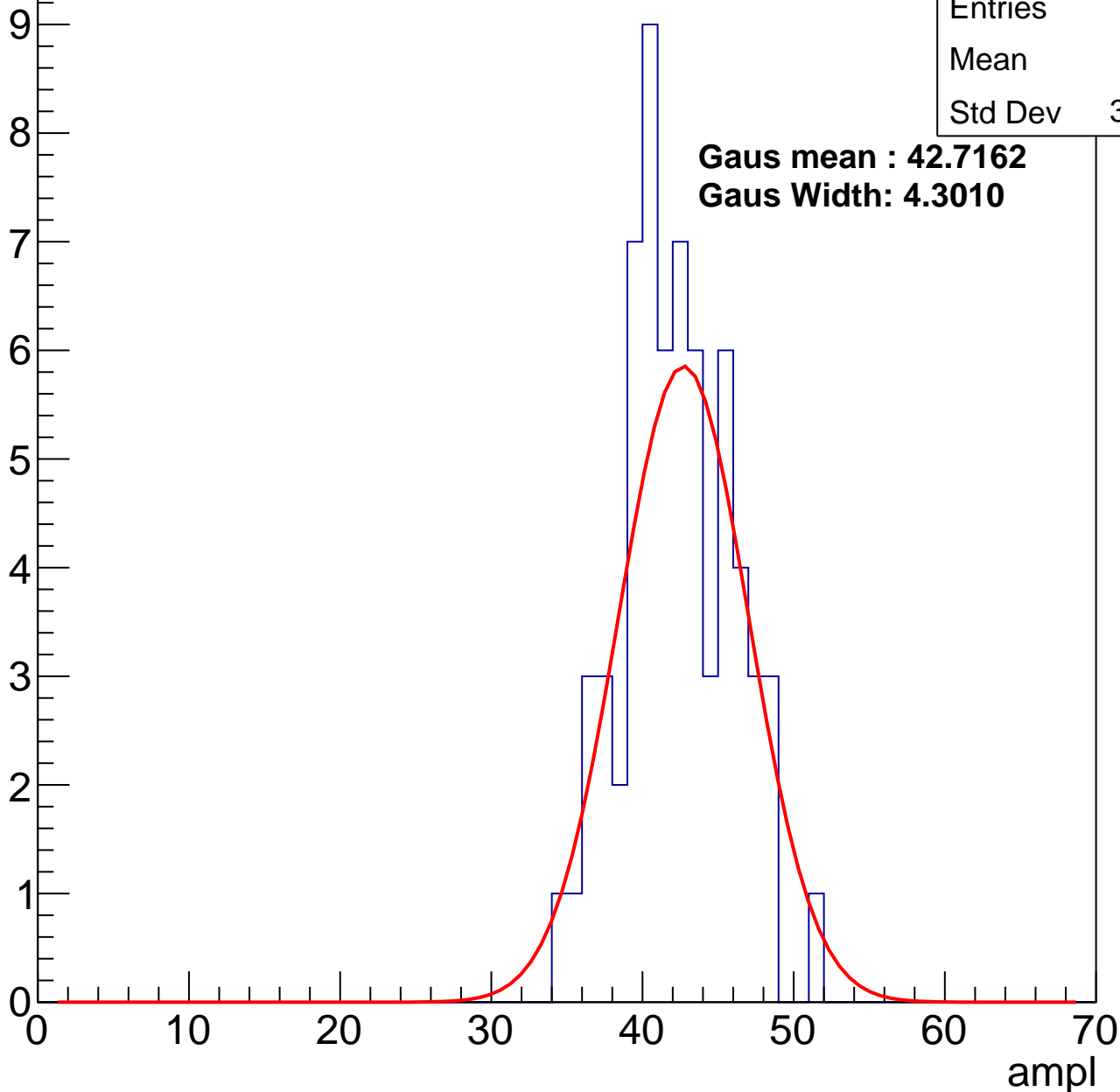
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	41.8
Std Dev	3.574

**Gaus mean : 42.7162**

**Gaus Width: 4.3010**

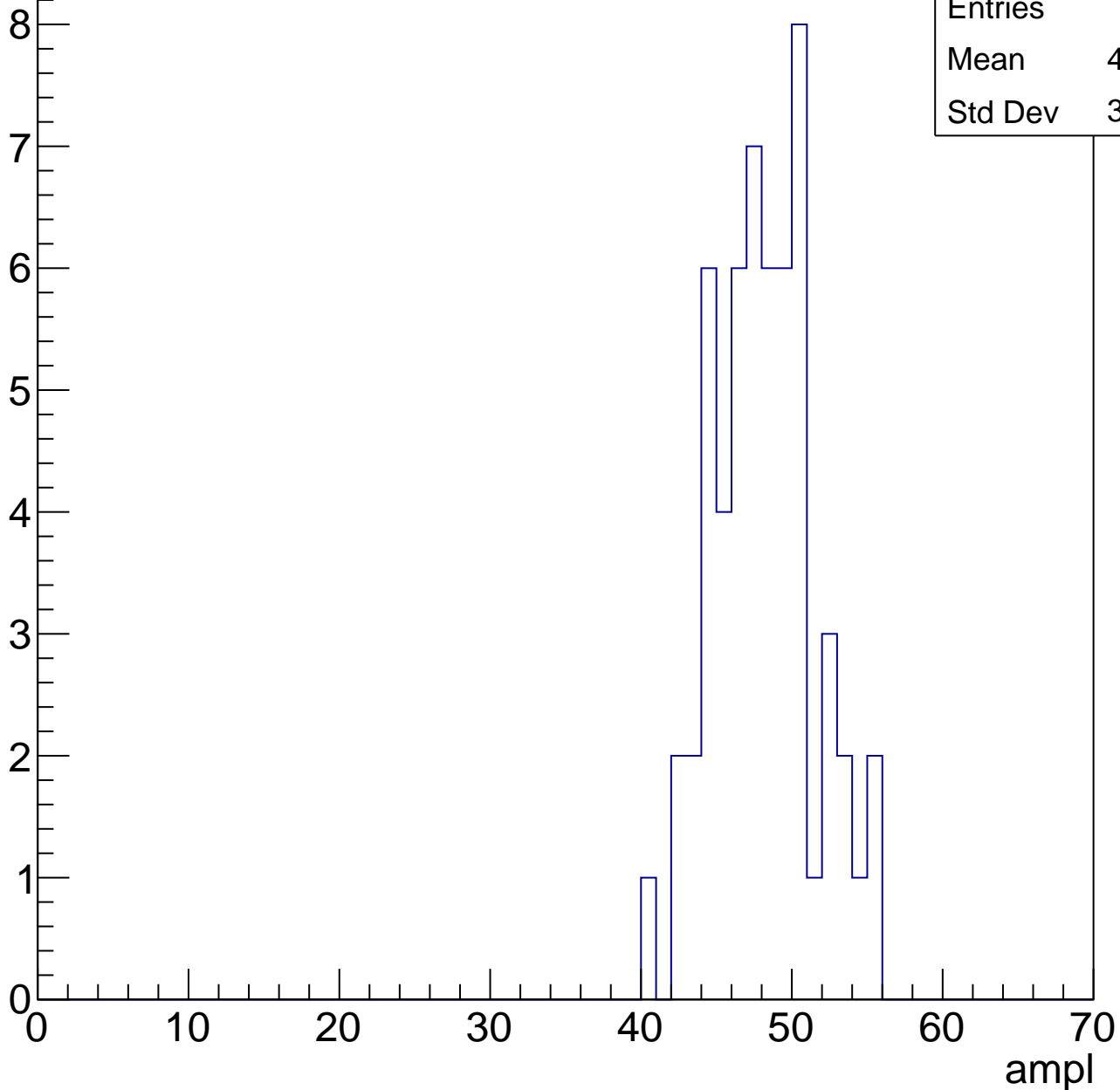


# B1L003S, U23-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	47.68
Std Dev	3.304

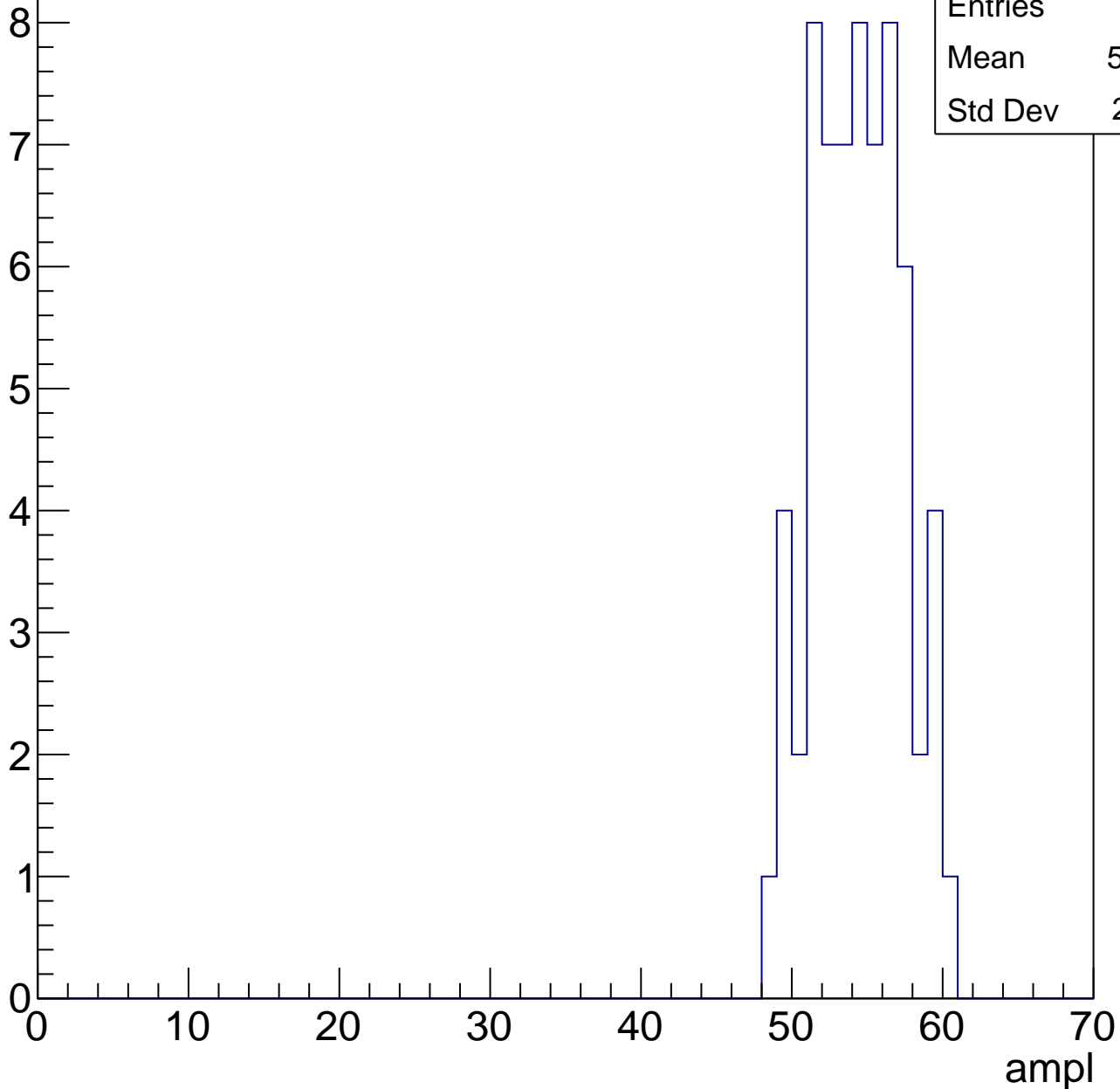


# B1L003S, U23-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	53.94
Std Dev	2.871

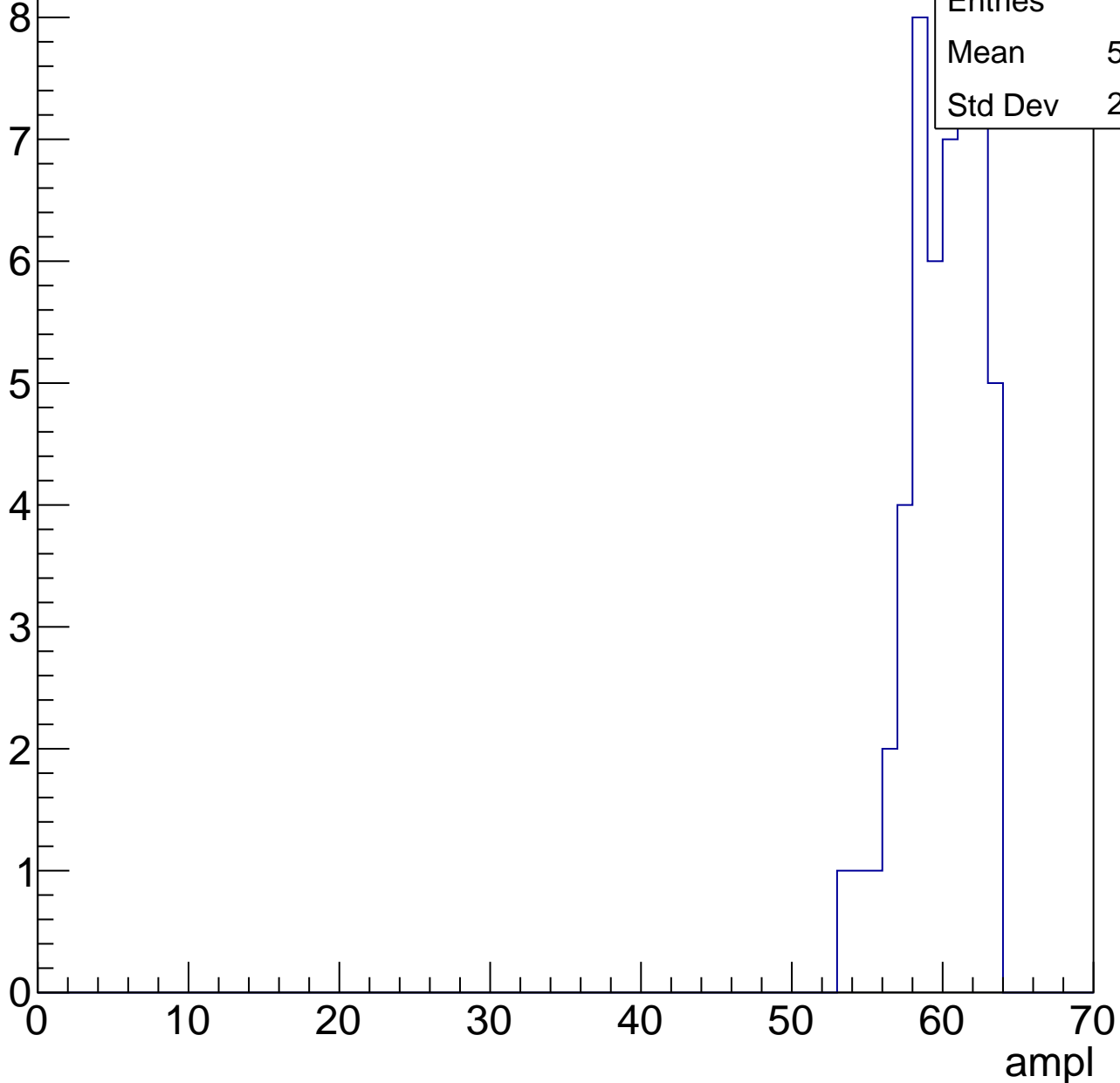


# B1L003S, U23-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	59.59
Std Dev	2.394

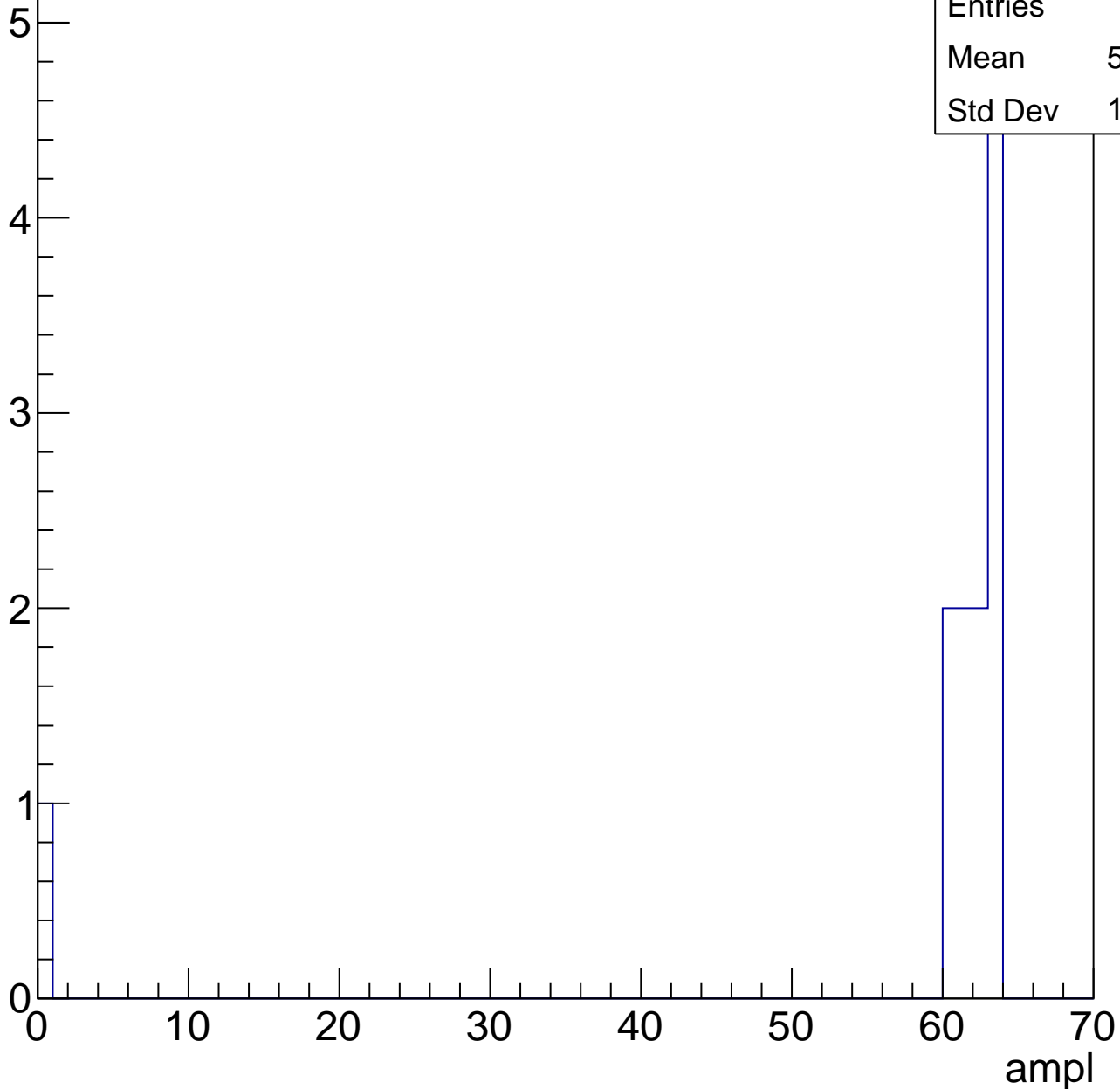


# B1L003S, U23-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	12
Mean	56.75
Std Dev	17.15





# B1L003S, U23-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch115, adc0

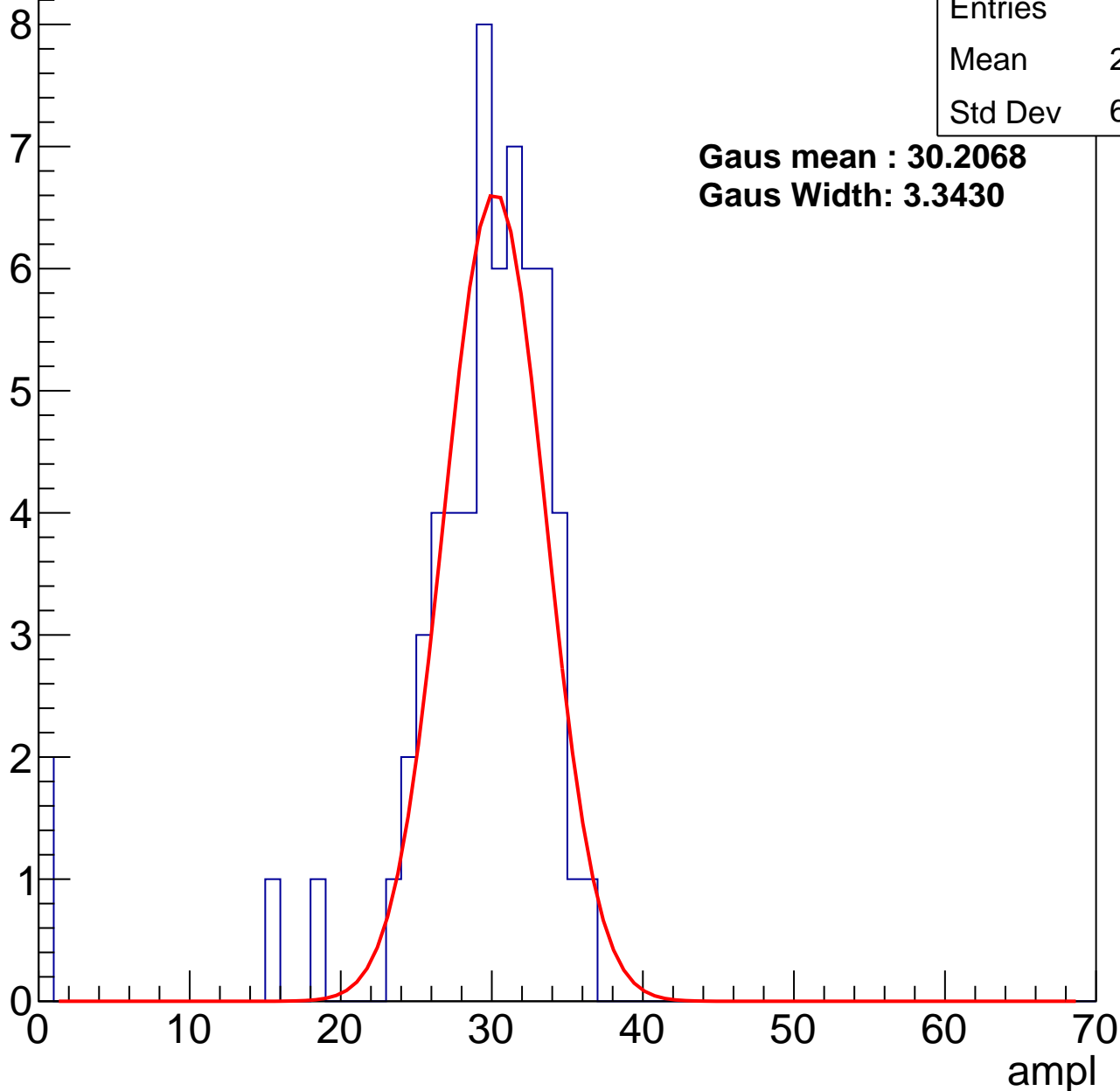
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.34
Std Dev	6.442

**Gaus mean : 30.2068**

**Gaus Width: 3.3430**



# B1L003S, U23-ch115, adc1

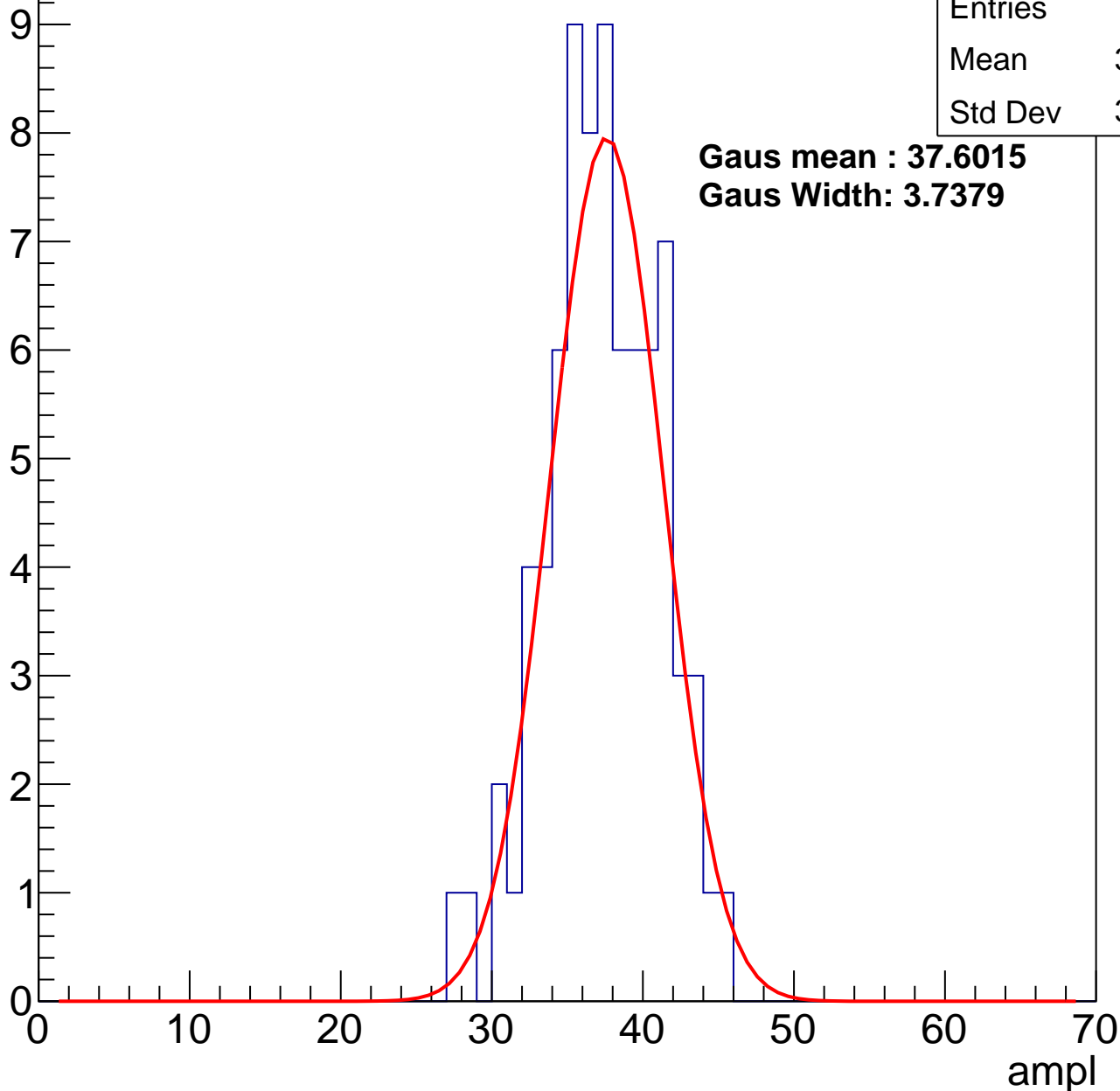
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	36.91
Std Dev	3.701

**Gaus mean : 37.6015**

**Gaus Width: 3.7379**



# B1L003S, U23-ch115, adc2

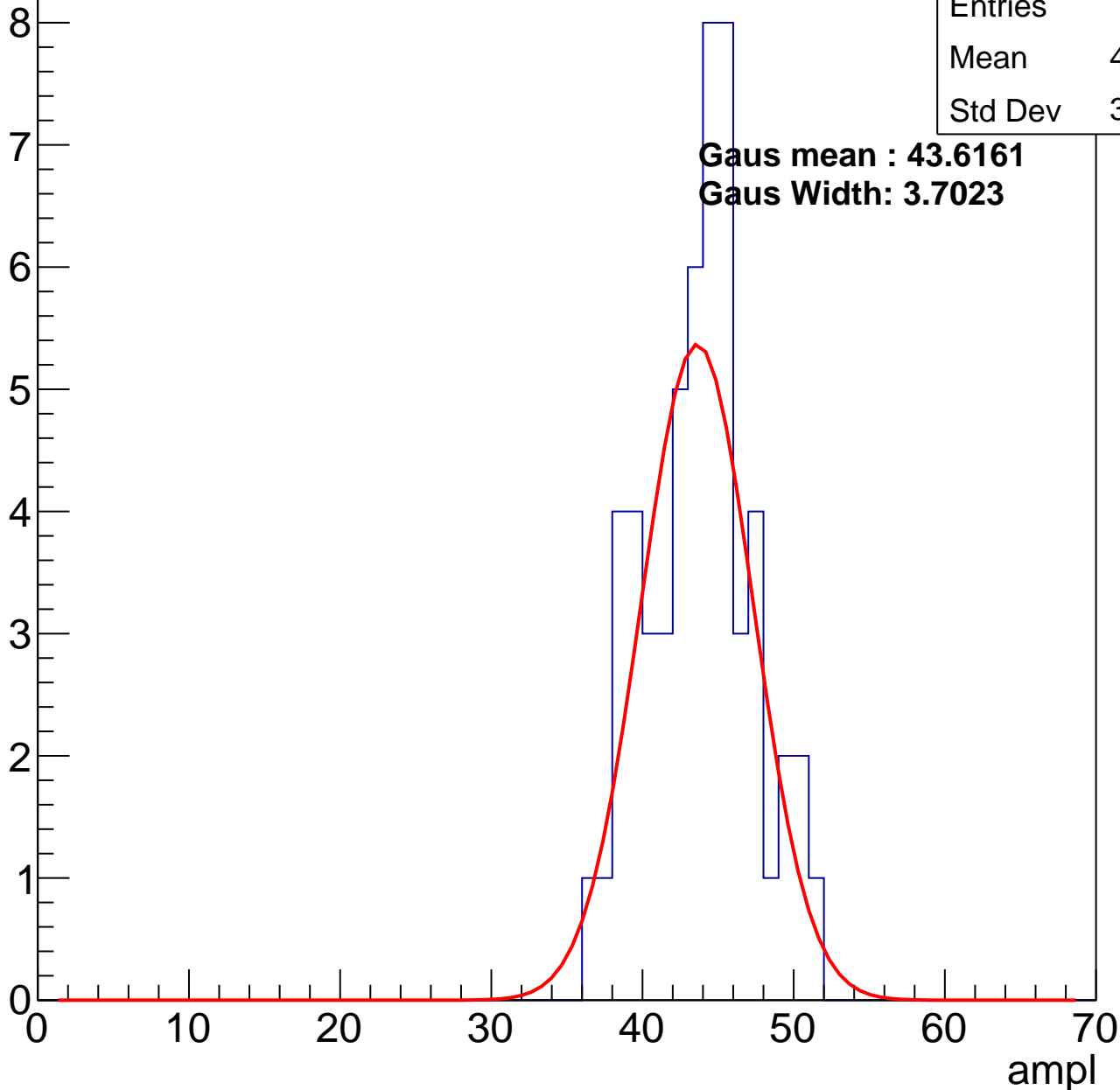
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.34
Std Dev	3.476

**Gaus mean : 43.6161**

**Gaus Width: 3.7023**

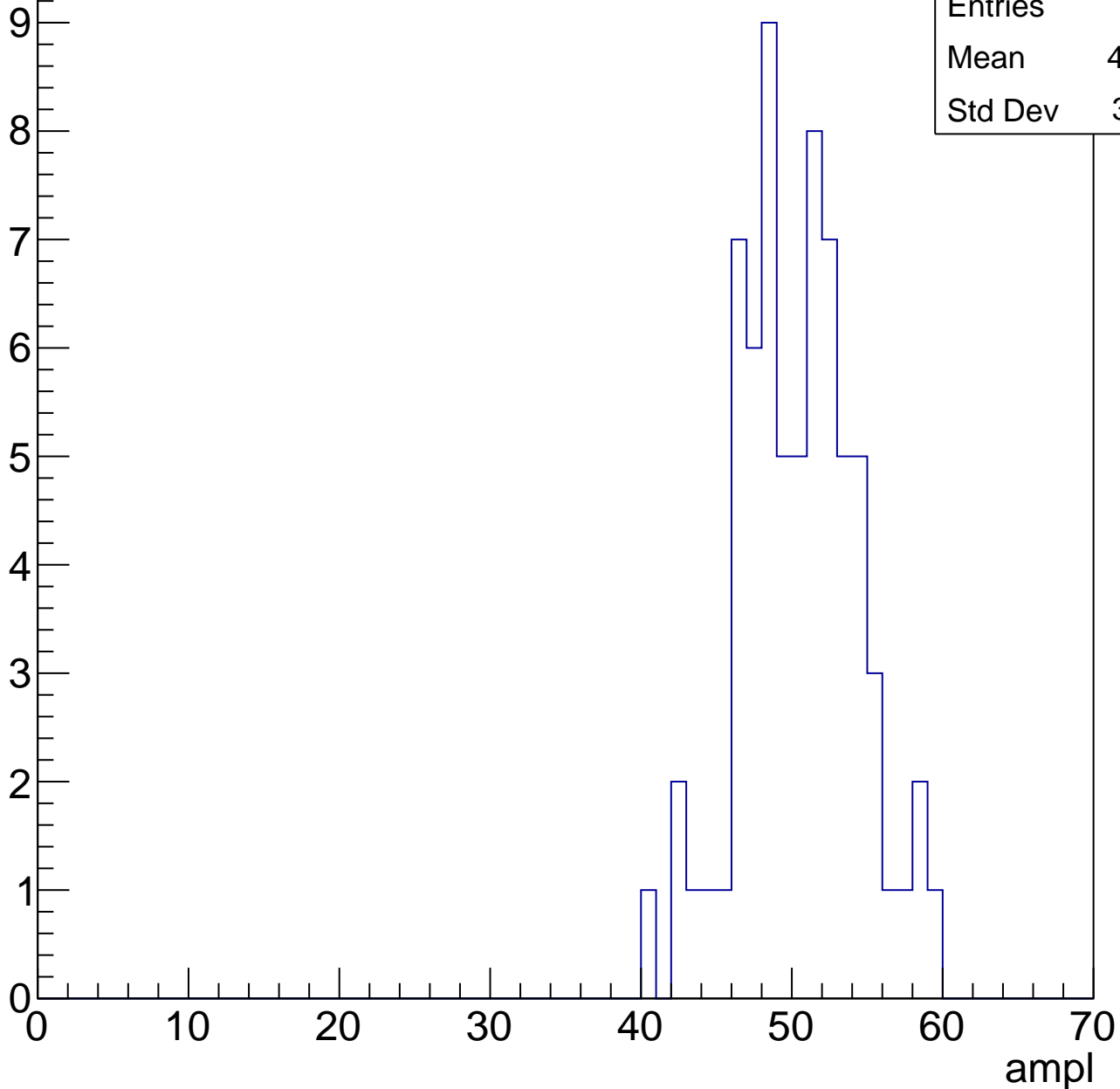


# B1L003S, U23-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	49.96
Std Dev	3.891

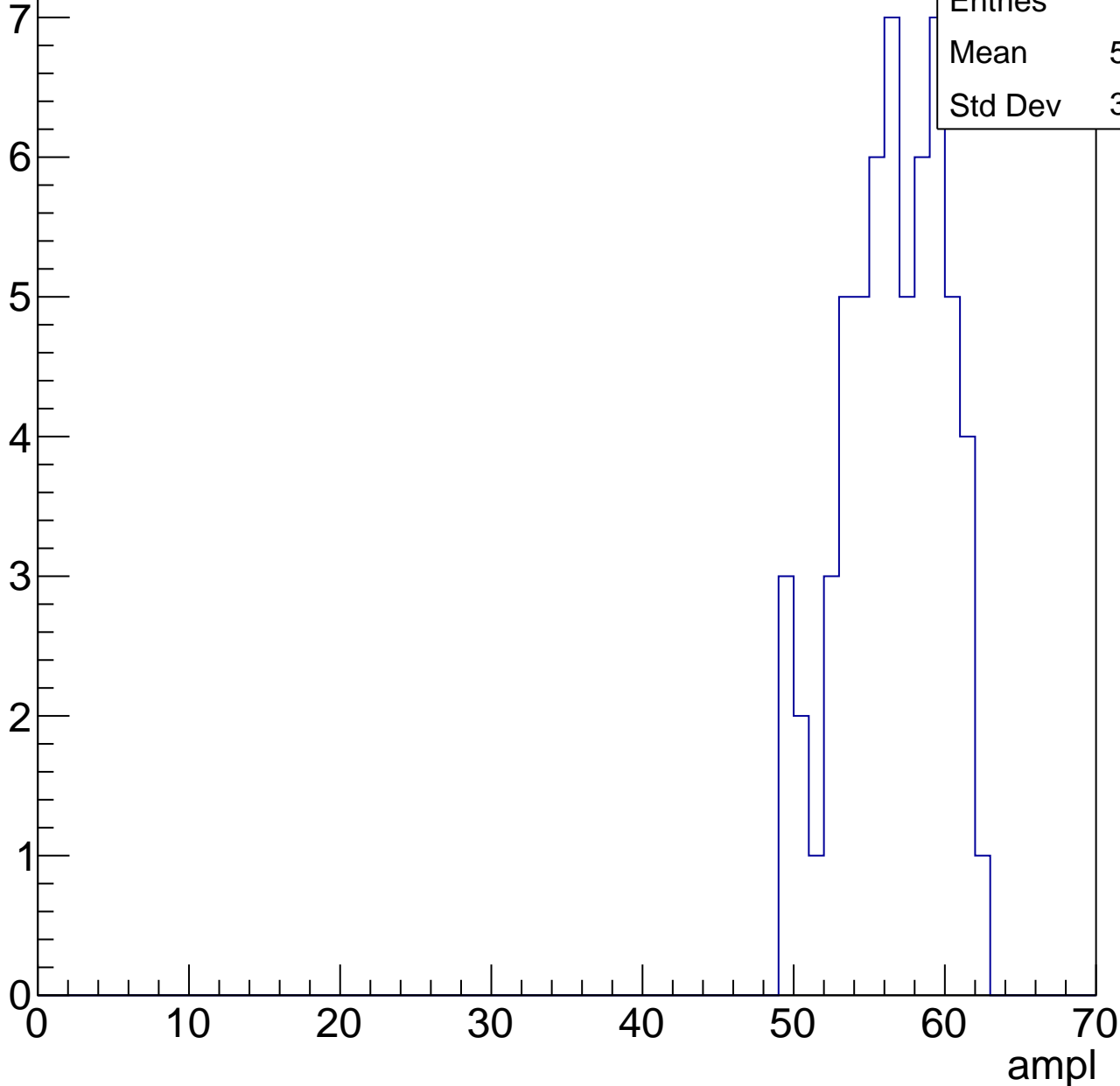


# B1L003S, U23-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	56.05
Std Dev	3.344

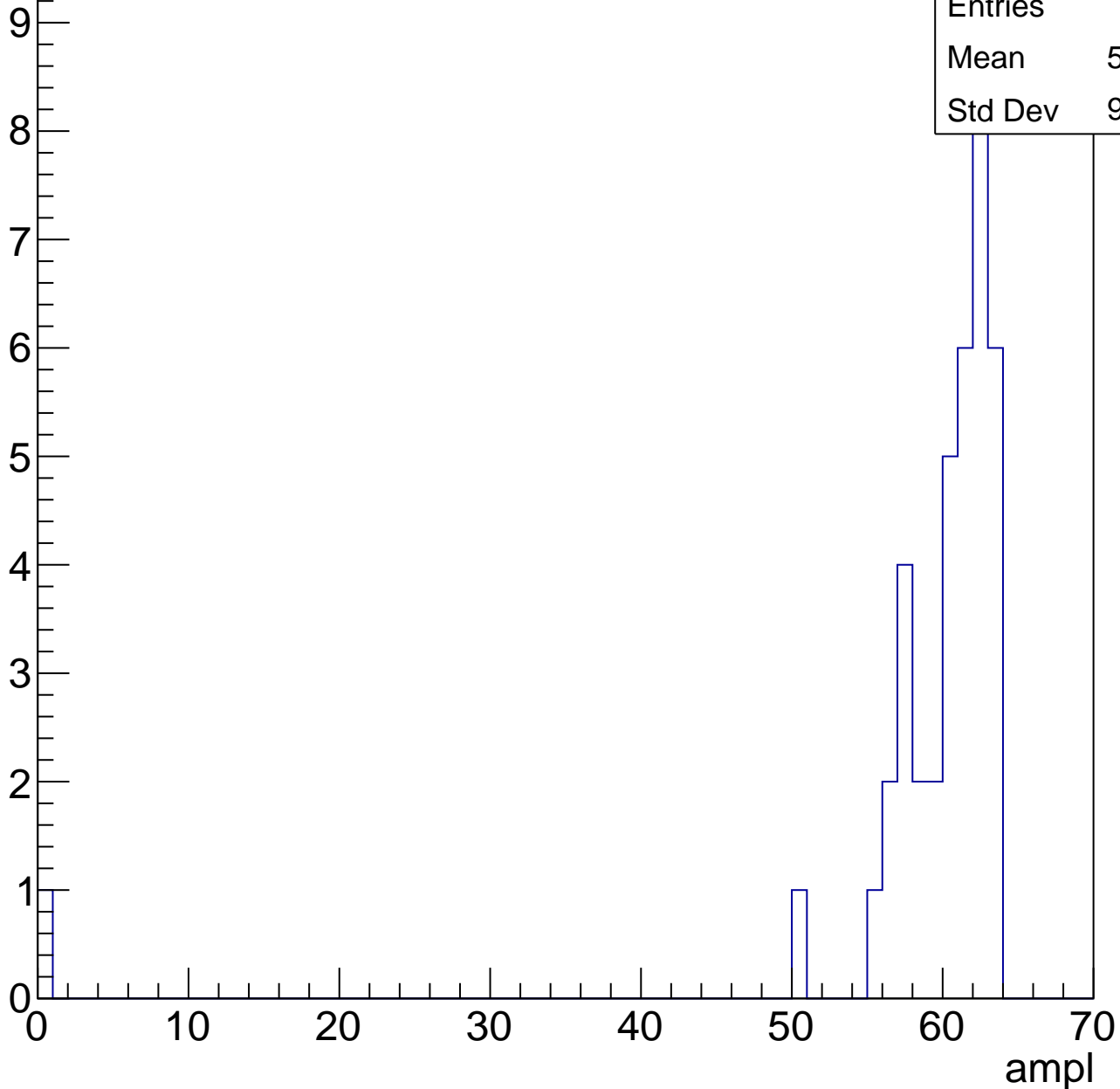


# B1L003S, U23-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.49
Std Dev	9.884



# B1L003S, U23-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	4
Mean	63
Std Dev	0

ampl



# B1L003S, U23-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch116, adc0

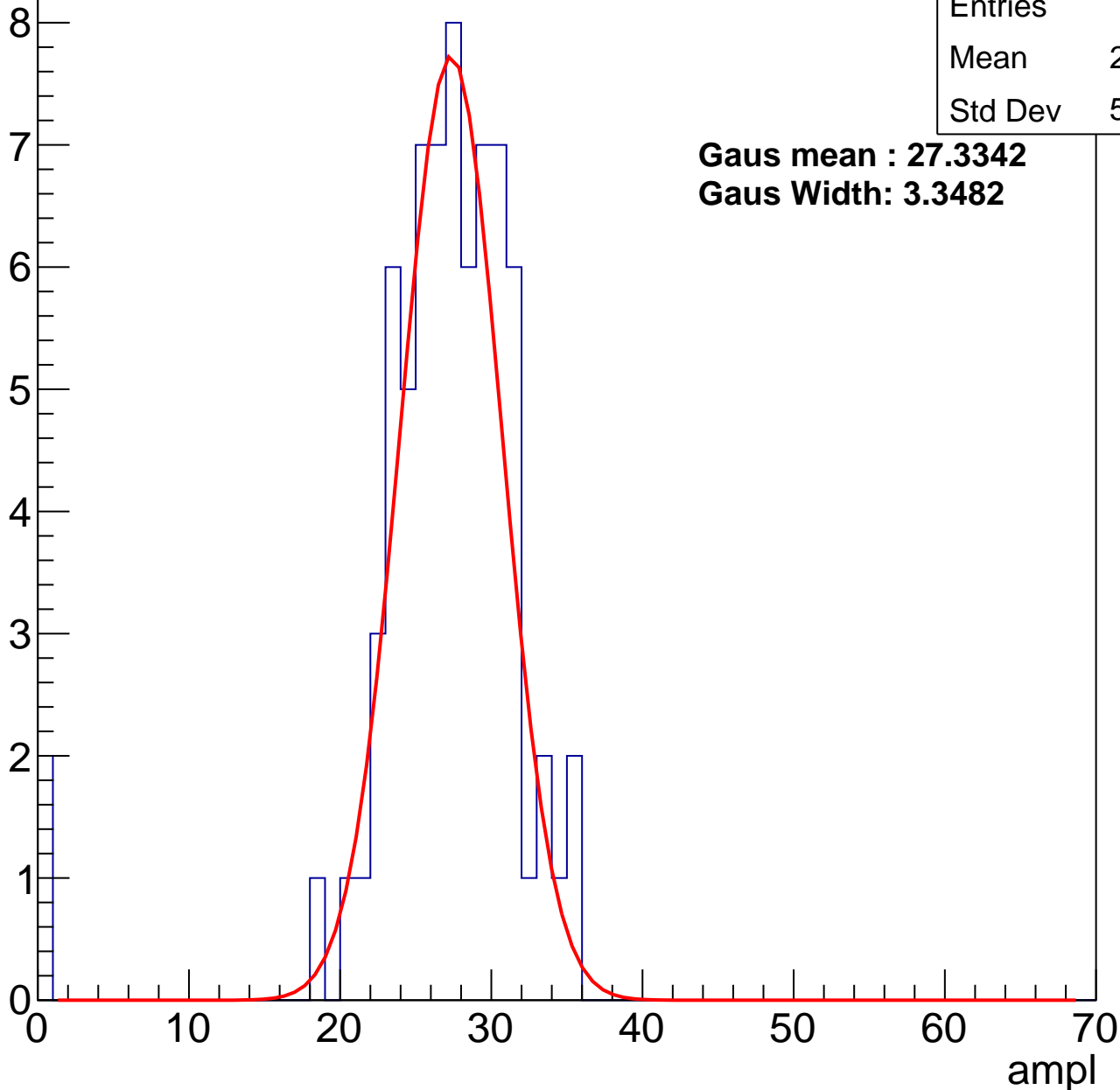
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	26.37
Std Dev	5.629

**Gaus mean : 27.3342**

**Gaus Width: 3.3482**



# B1L003S, U23-ch116, adc1

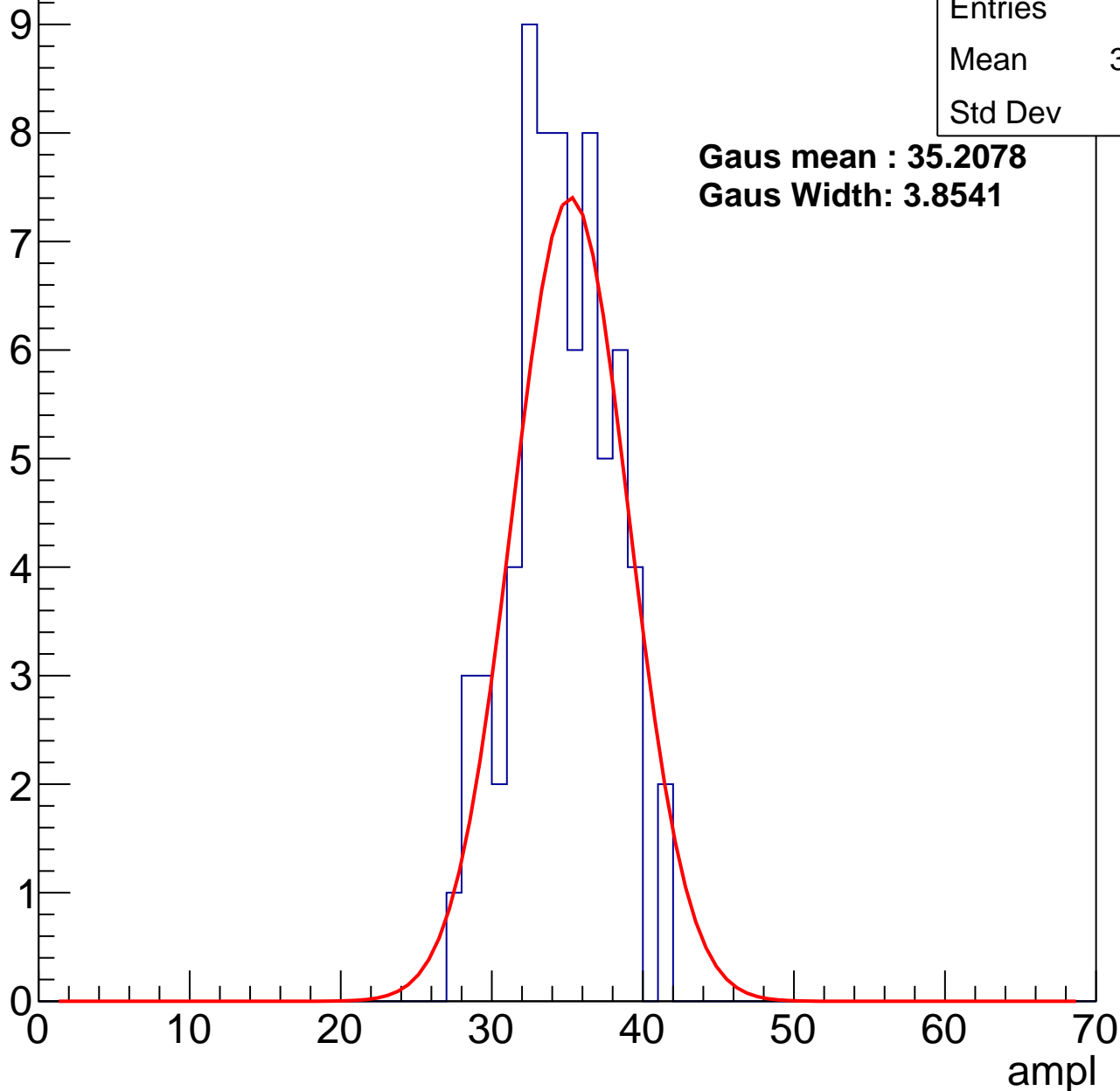
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	34.13
Std Dev	3.23

**Gaus mean : 35.2078**

**Gaus Width: 3.8541**



# B1L003S, U23-ch116, adc2

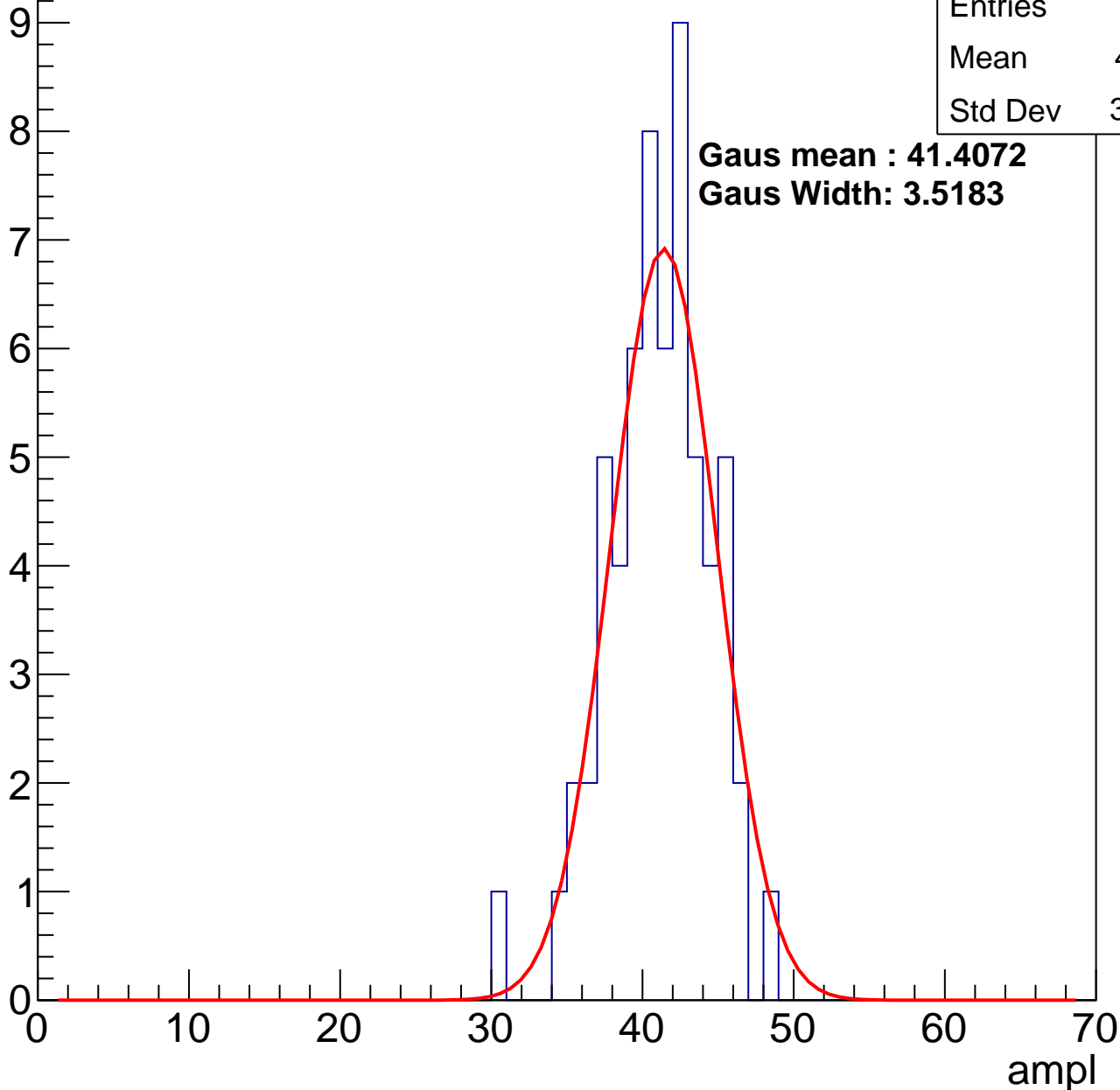
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	40.61
Std Dev	3.325

**Gaus mean : 41.4072**

**Gaus Width: 3.5183**

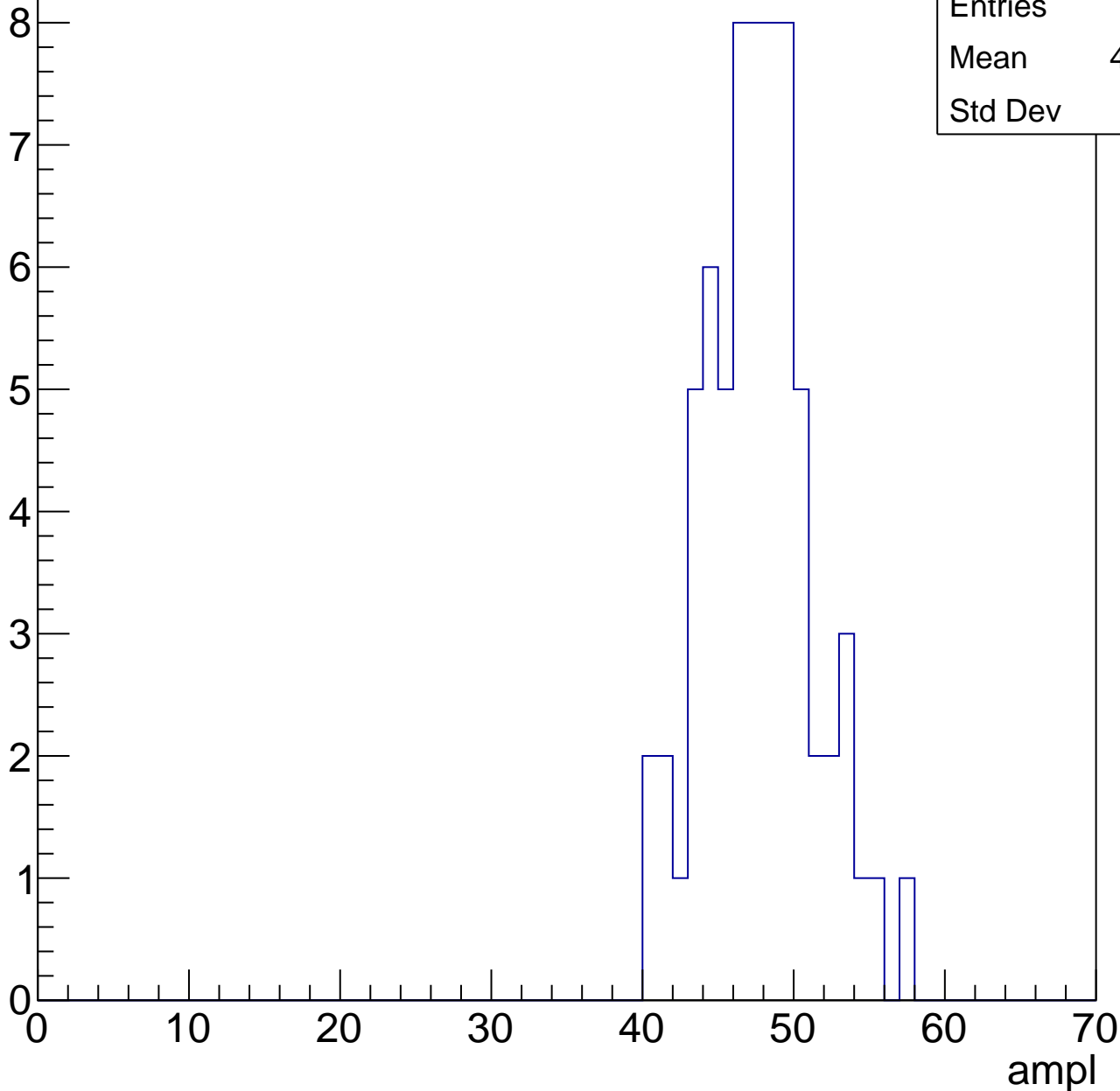


# B1L003S, U23-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

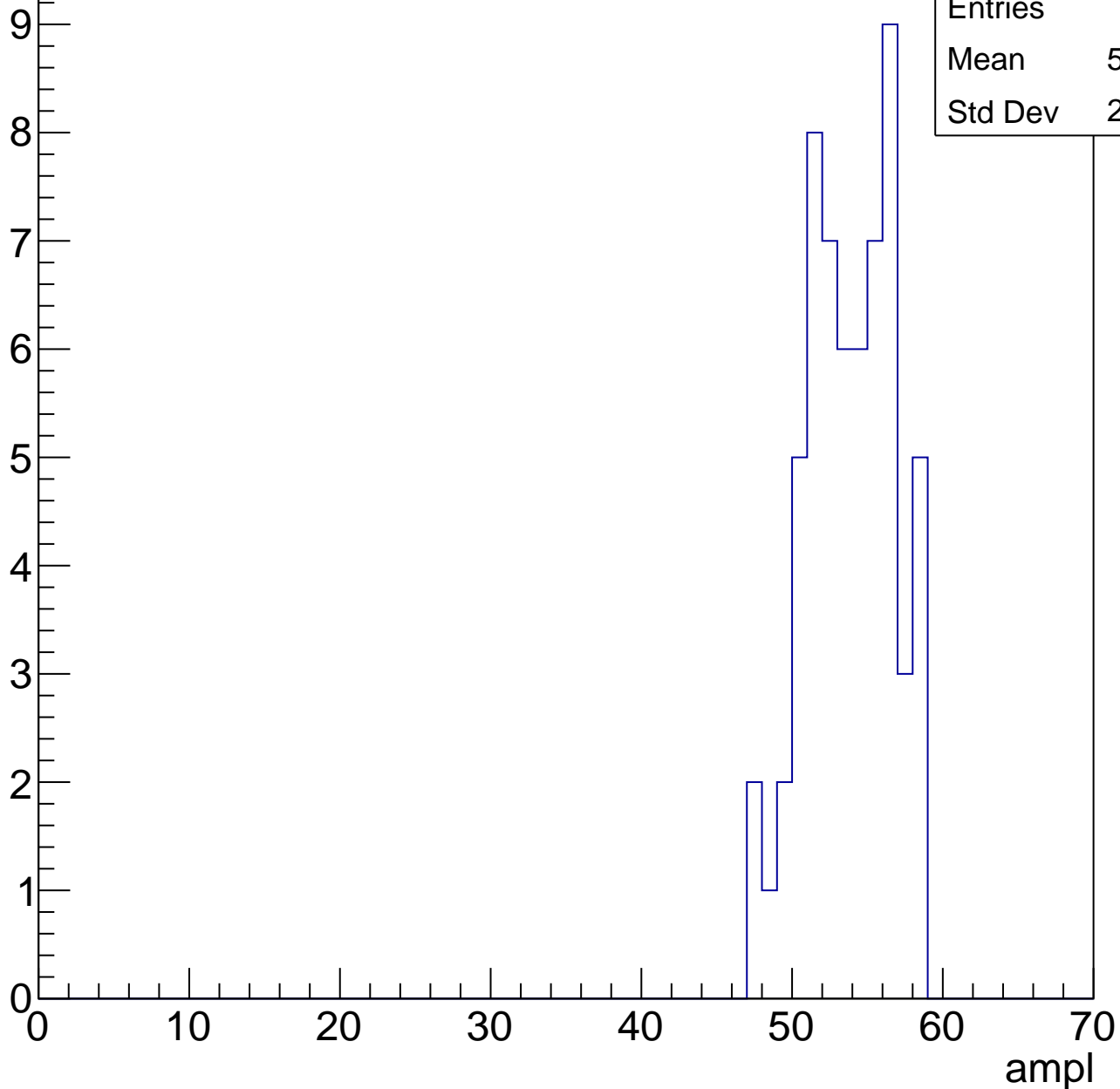
Entries	68
Mean	47.19
Std Dev	3.52



# B1L003S, U23-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

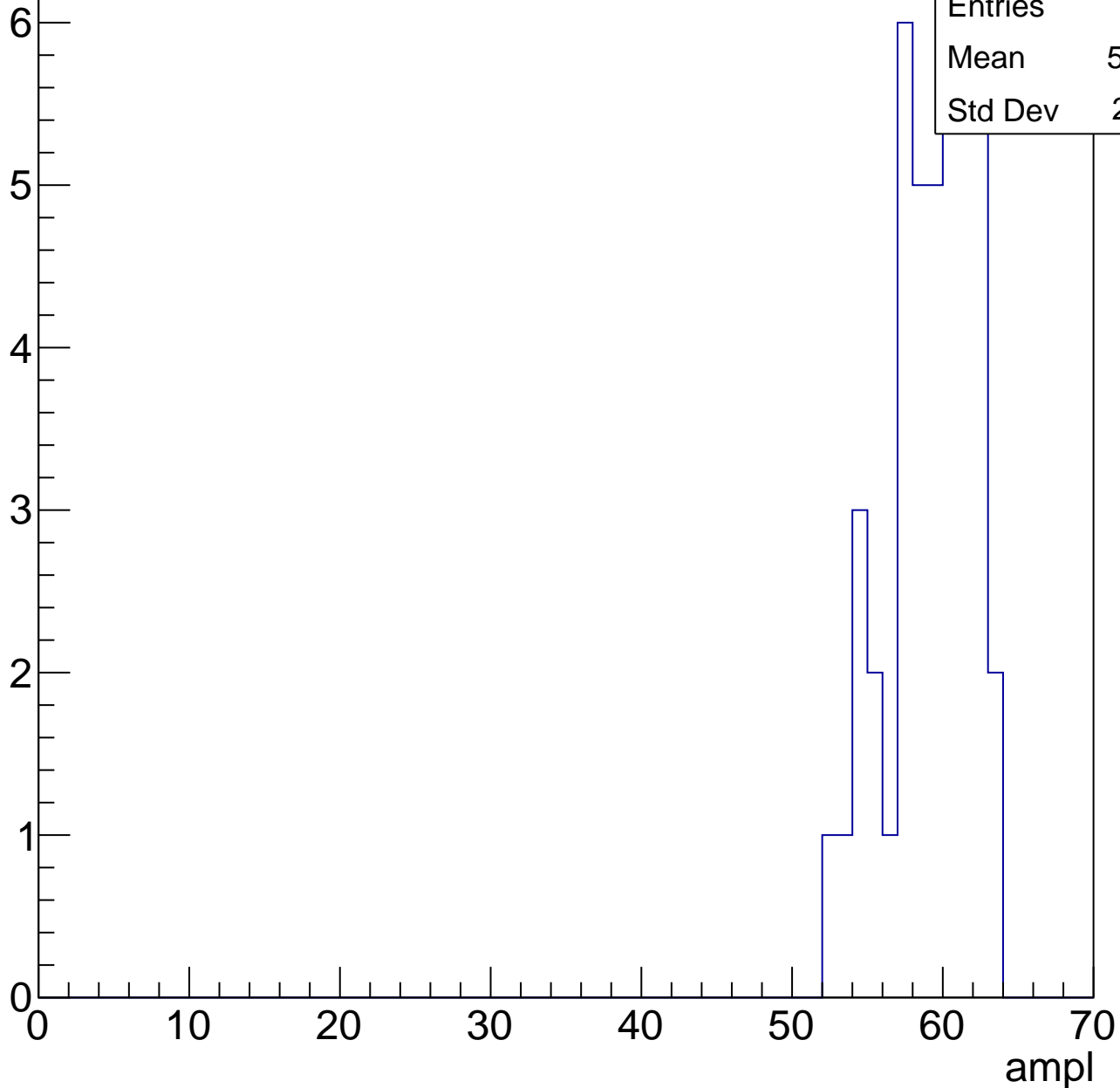


Entries	61
Mean	53.34
Std Dev	2.845

# B1L003S, U23-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

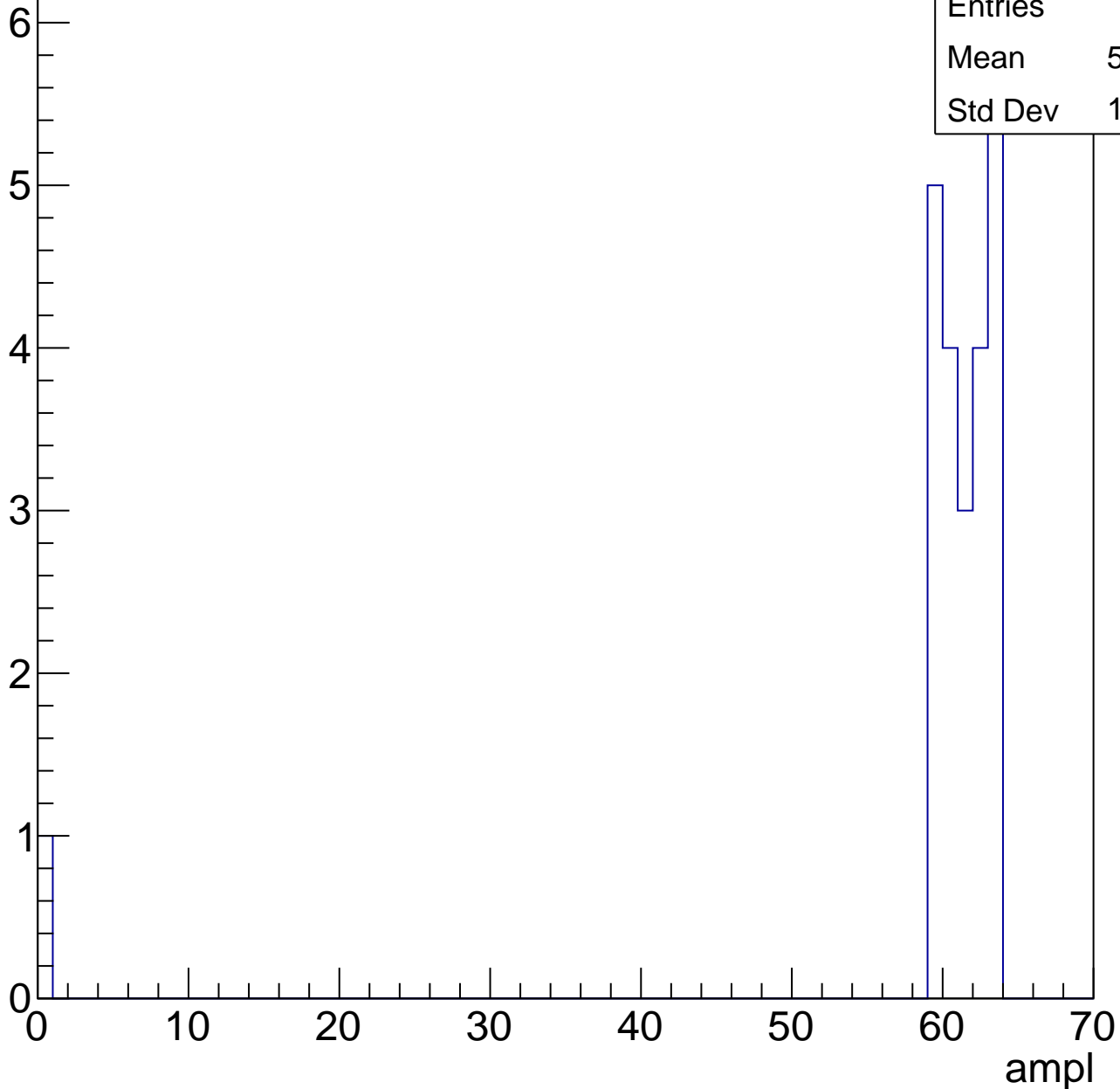


# B1L003S, U23-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	23
Mean	58.43
Std Dev	12.55





# B1L003S, U23-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

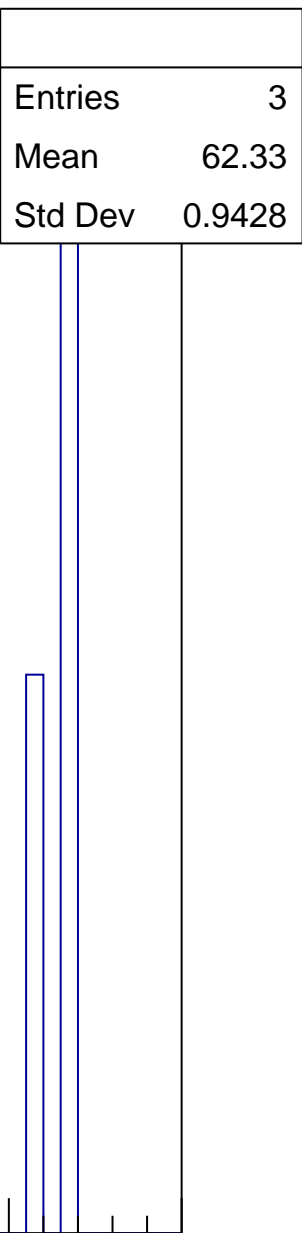
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



# B1L003S, U23-ch117, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	76
Mean	30.46
Std Dev	3.672

**Gaus mean : 30.6469**

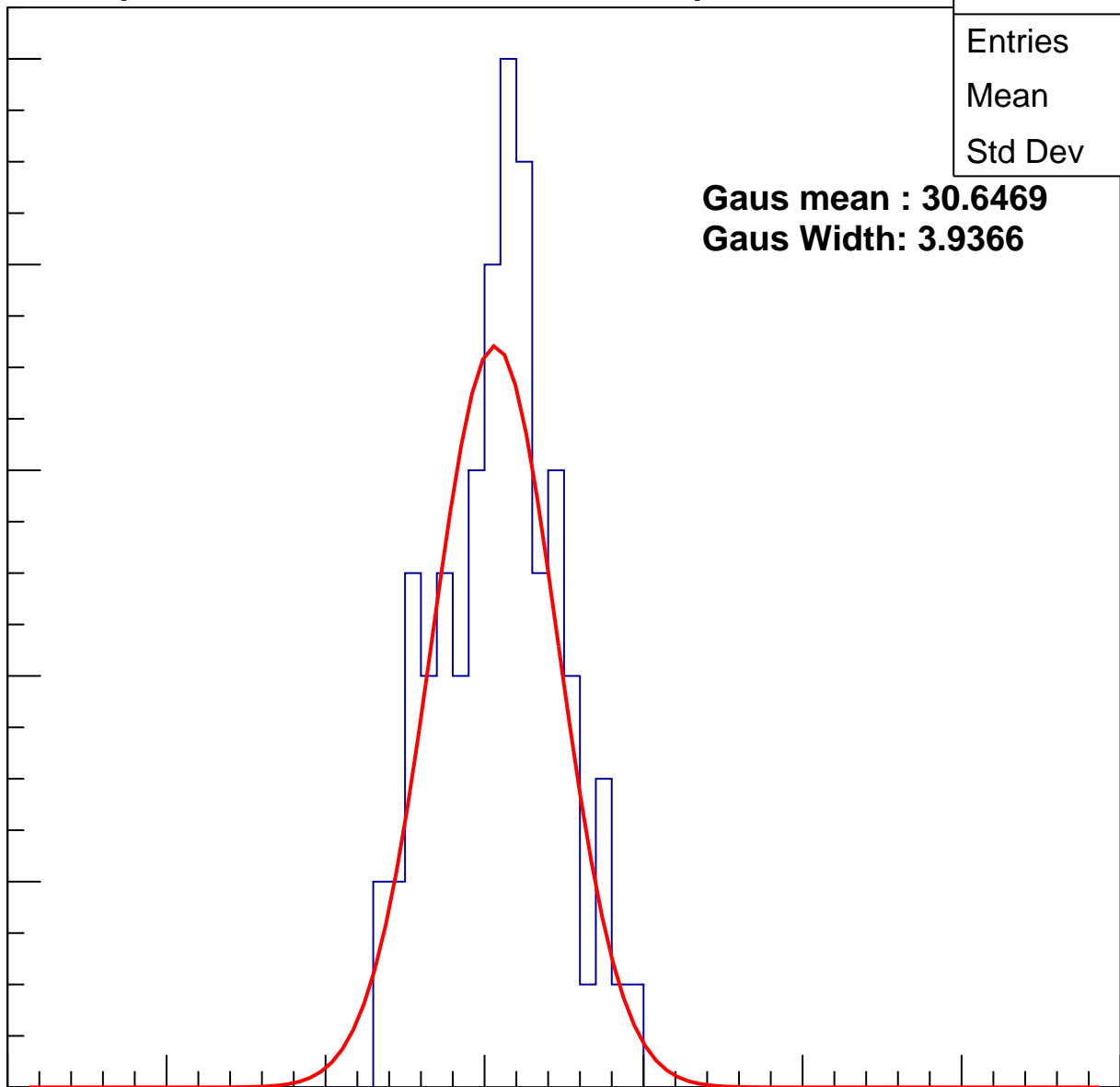
**Gaus Width: 3.9366**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch117, adc1

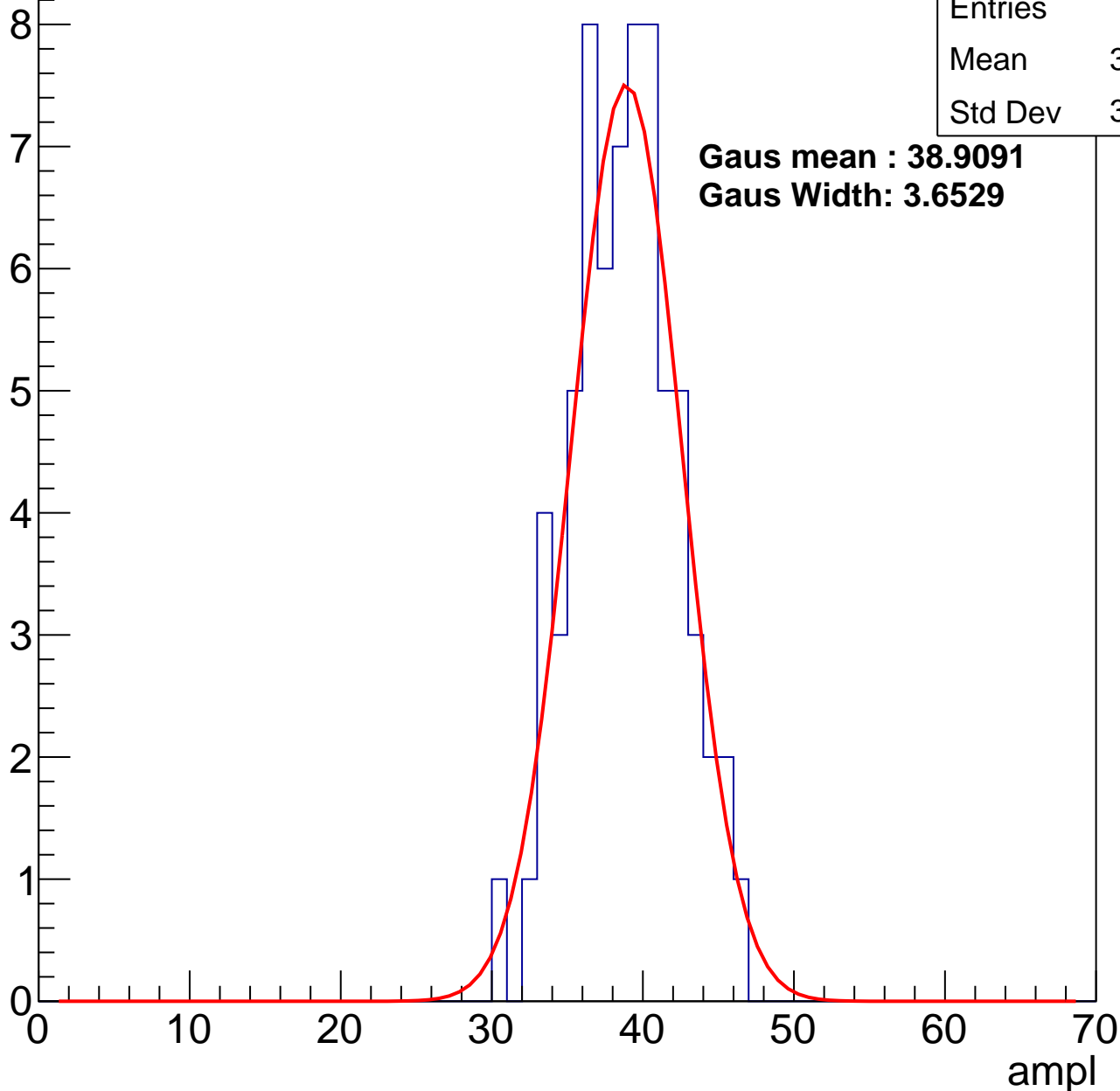
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	38.36
Std Dev	3.396

**Gaus mean : 38.9091**

**Gaus Width: 3.6529**



# B1L003S, U23-ch117, adc2

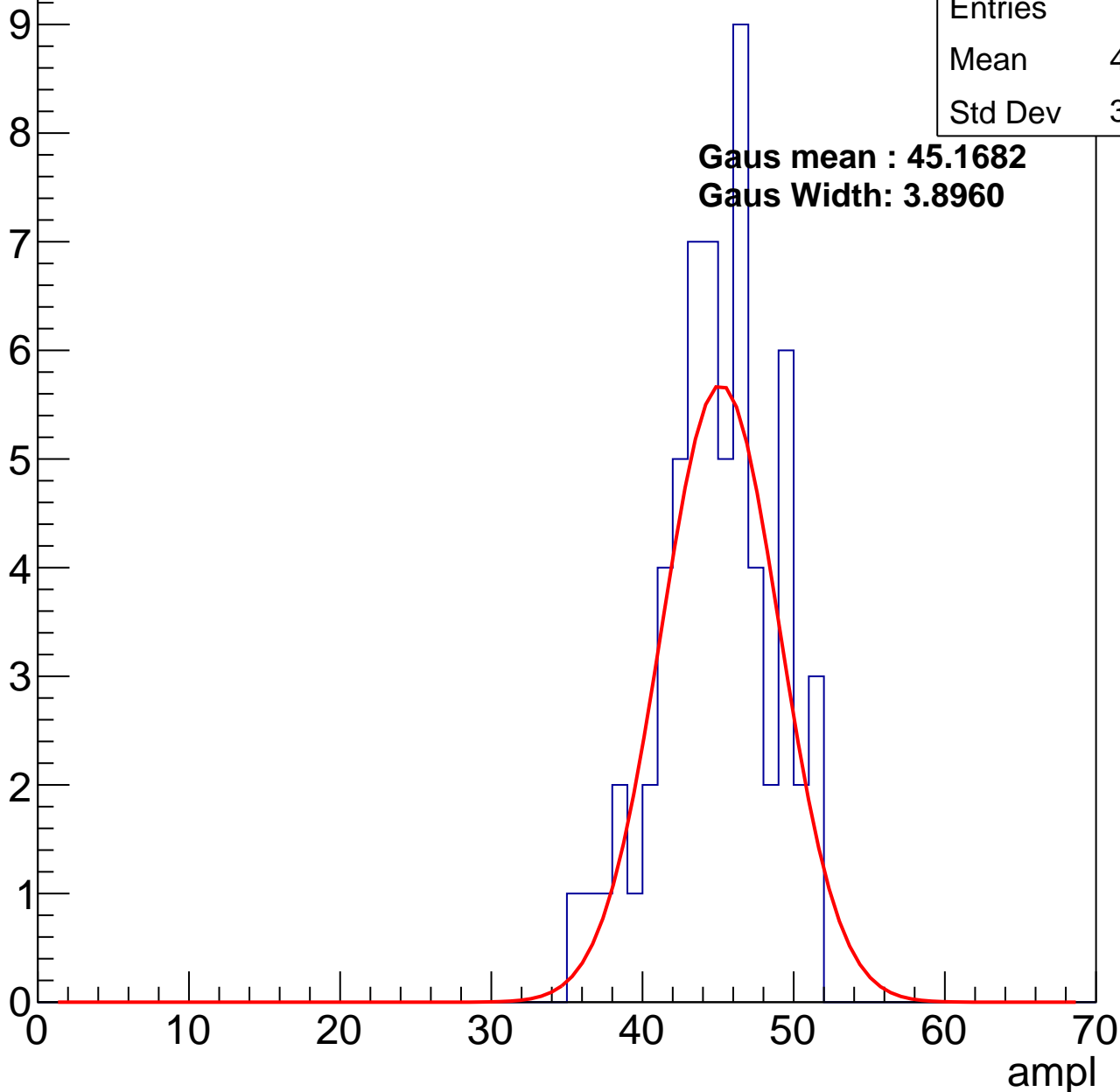
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	44.45
Std Dev	3.706

**Gaus mean : 45.1682**

**Gaus Width: 3.8960**

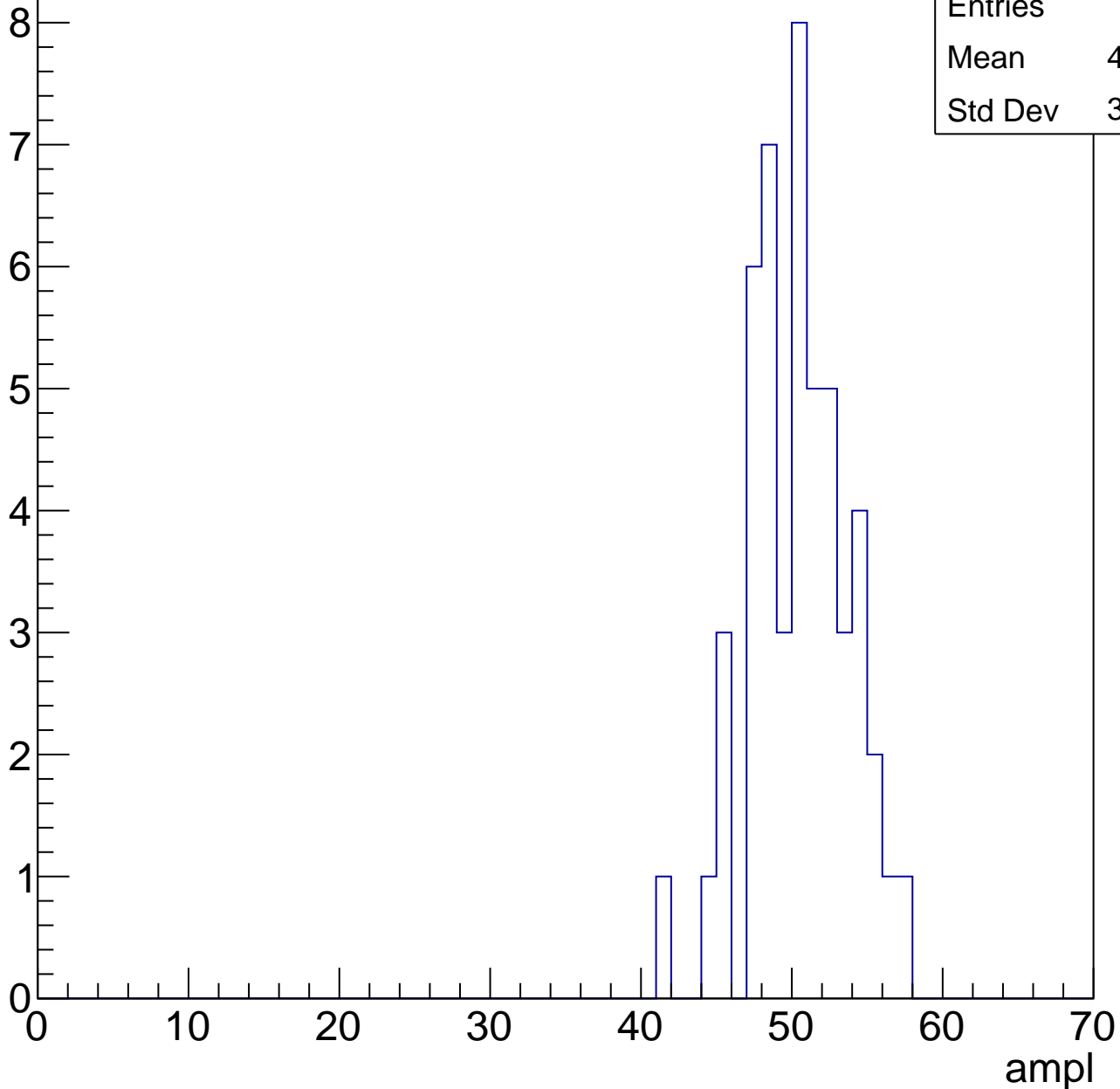


# B1L003S, U23-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	49.96
Std Dev	3.249

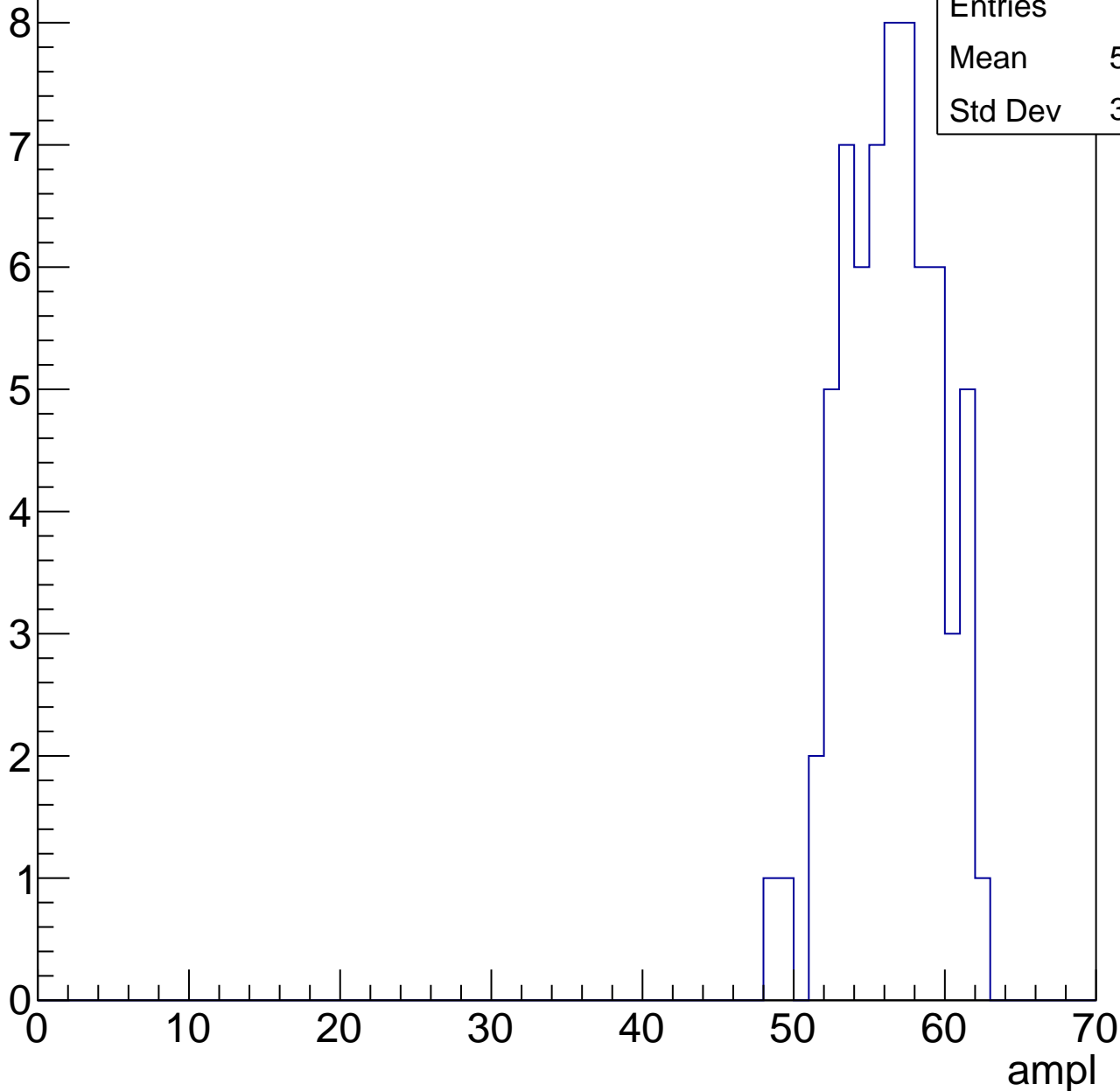


# B1L003S, U23-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	55.94
Std Dev	3.094

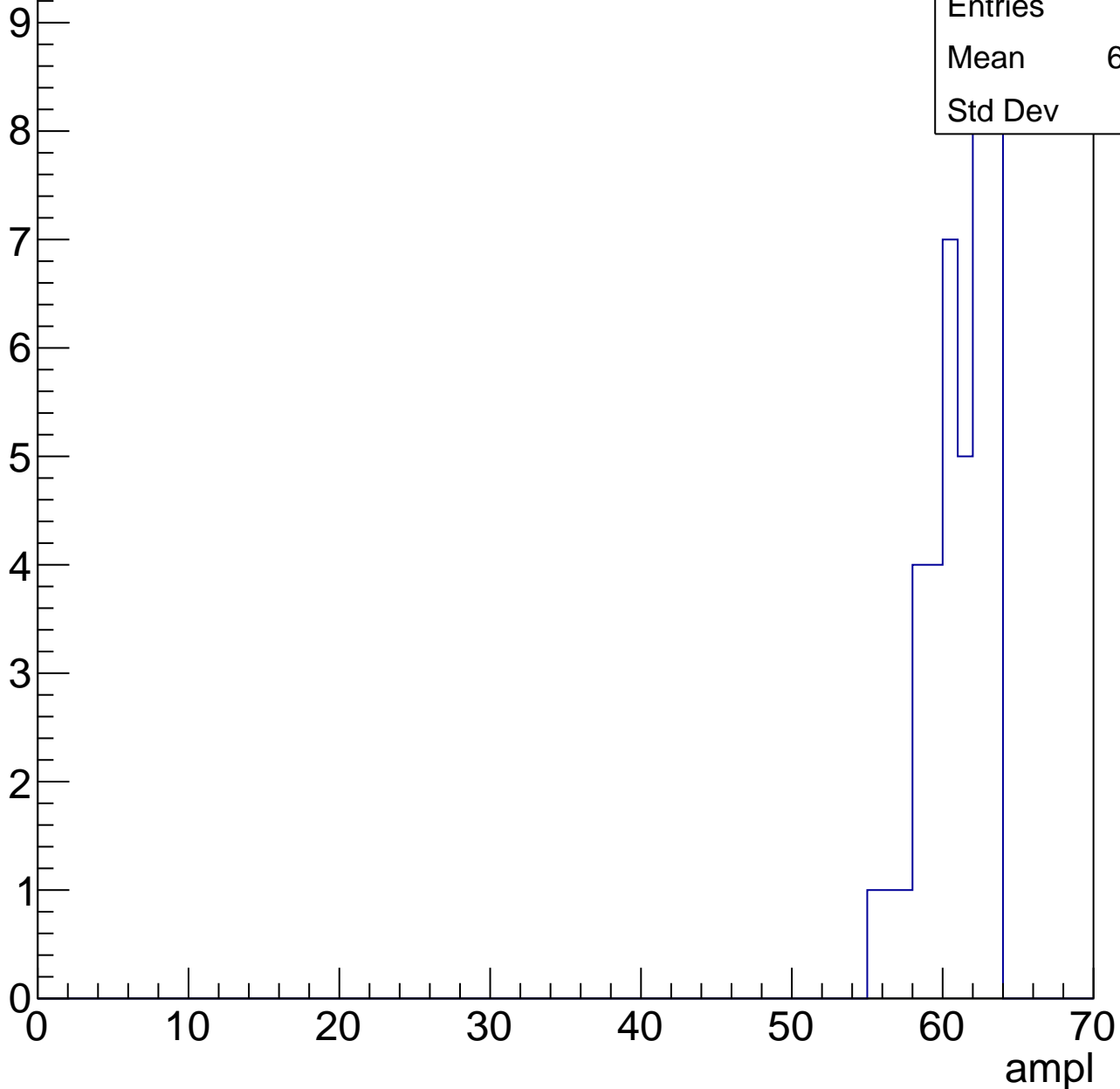


# B1L003S, U23-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	40
Mean	60.58
Std Dev	2.06



# B1L003S, U23-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	42
Std Dev	29.7

ampl



# B1L003S, U23-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch118, adc0

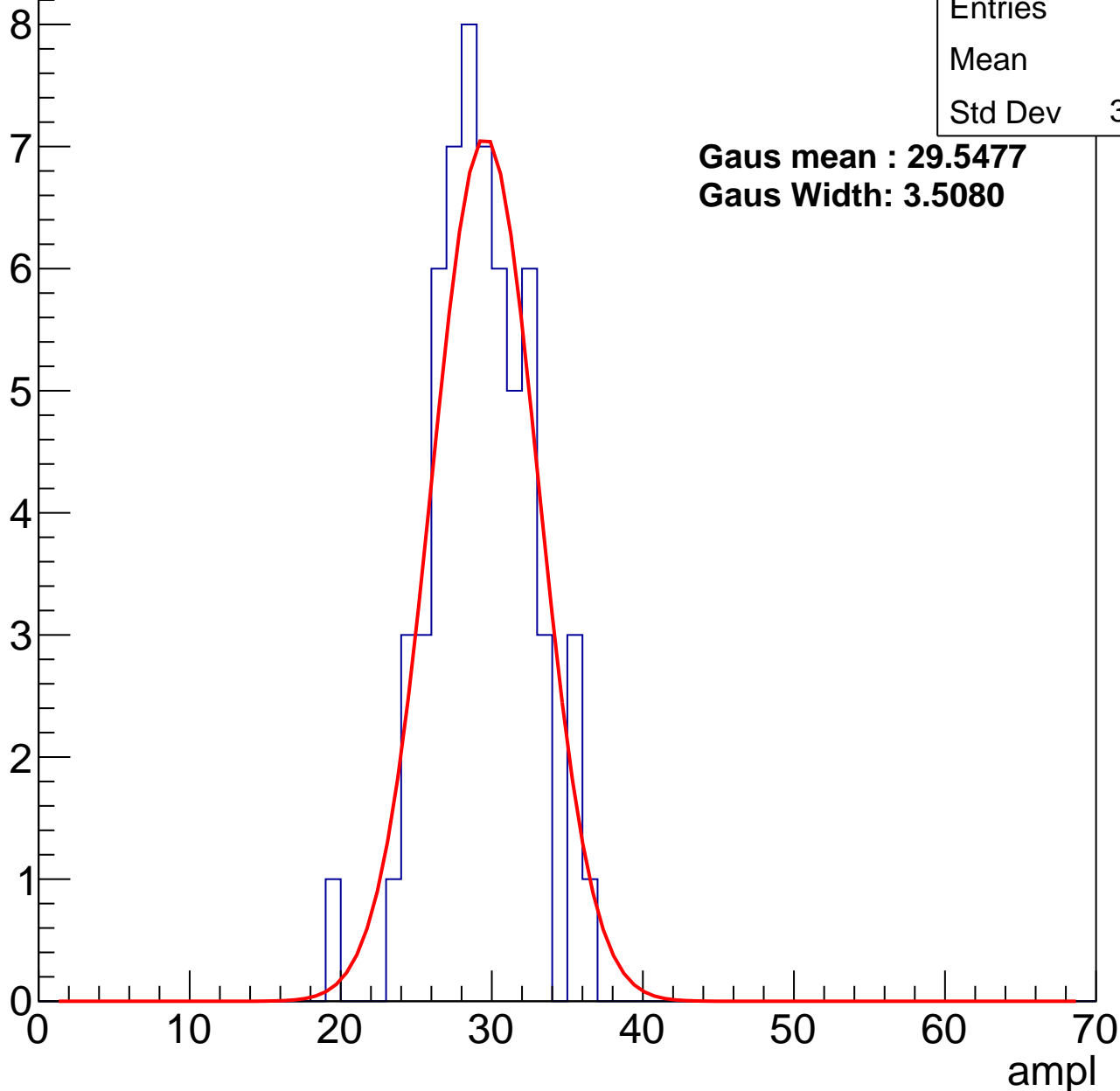
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	28.8
Std Dev	3.244

**Gaus mean : 29.5477**

**Gaus Width: 3.5080**



# B1L003S, U23-ch118, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	80
Mean	35.6
Std Dev	3.601

**Gaus mean : 36.0088**

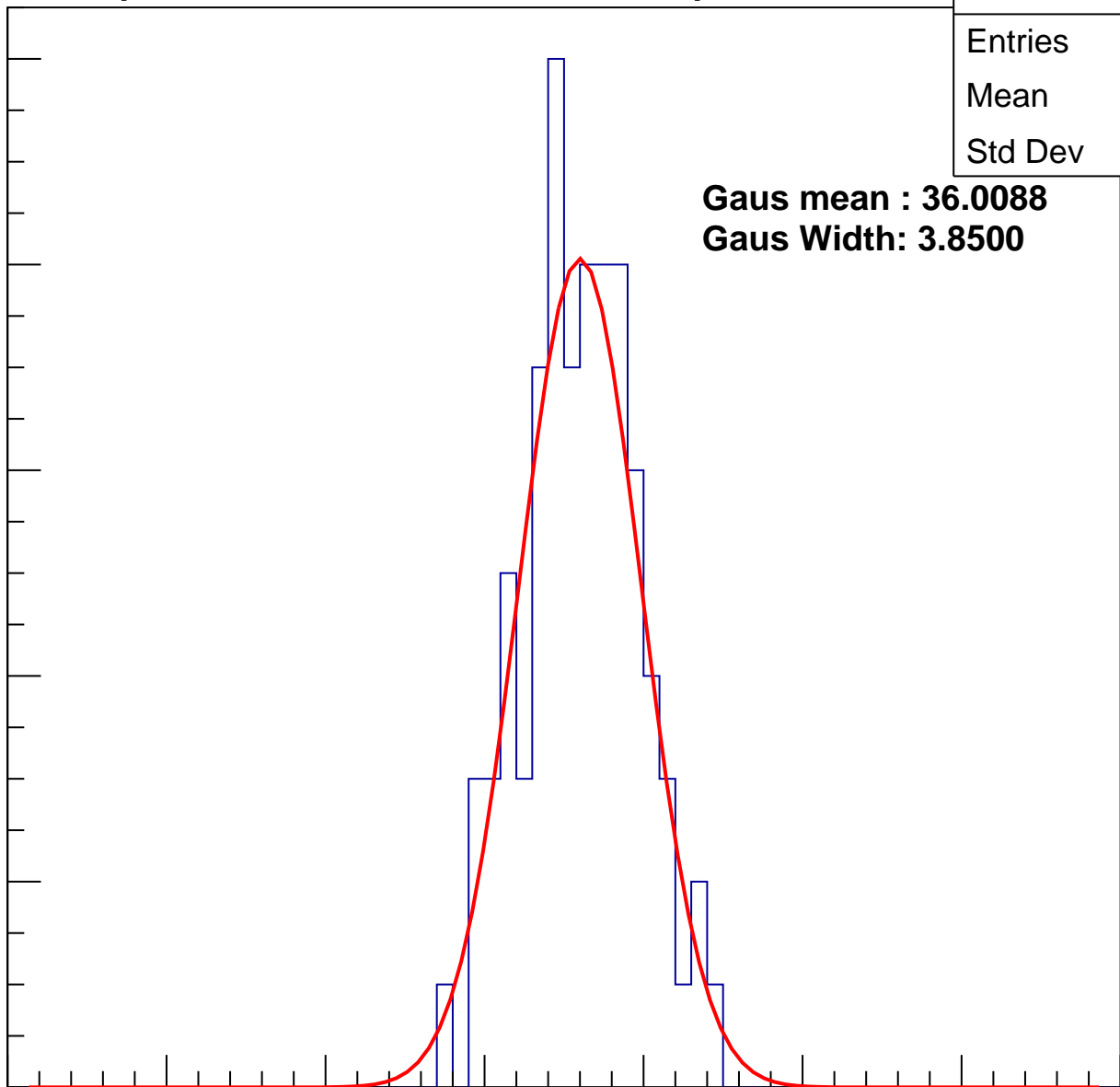
**Gaus Width: 3.8500**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U23-ch118, adc2

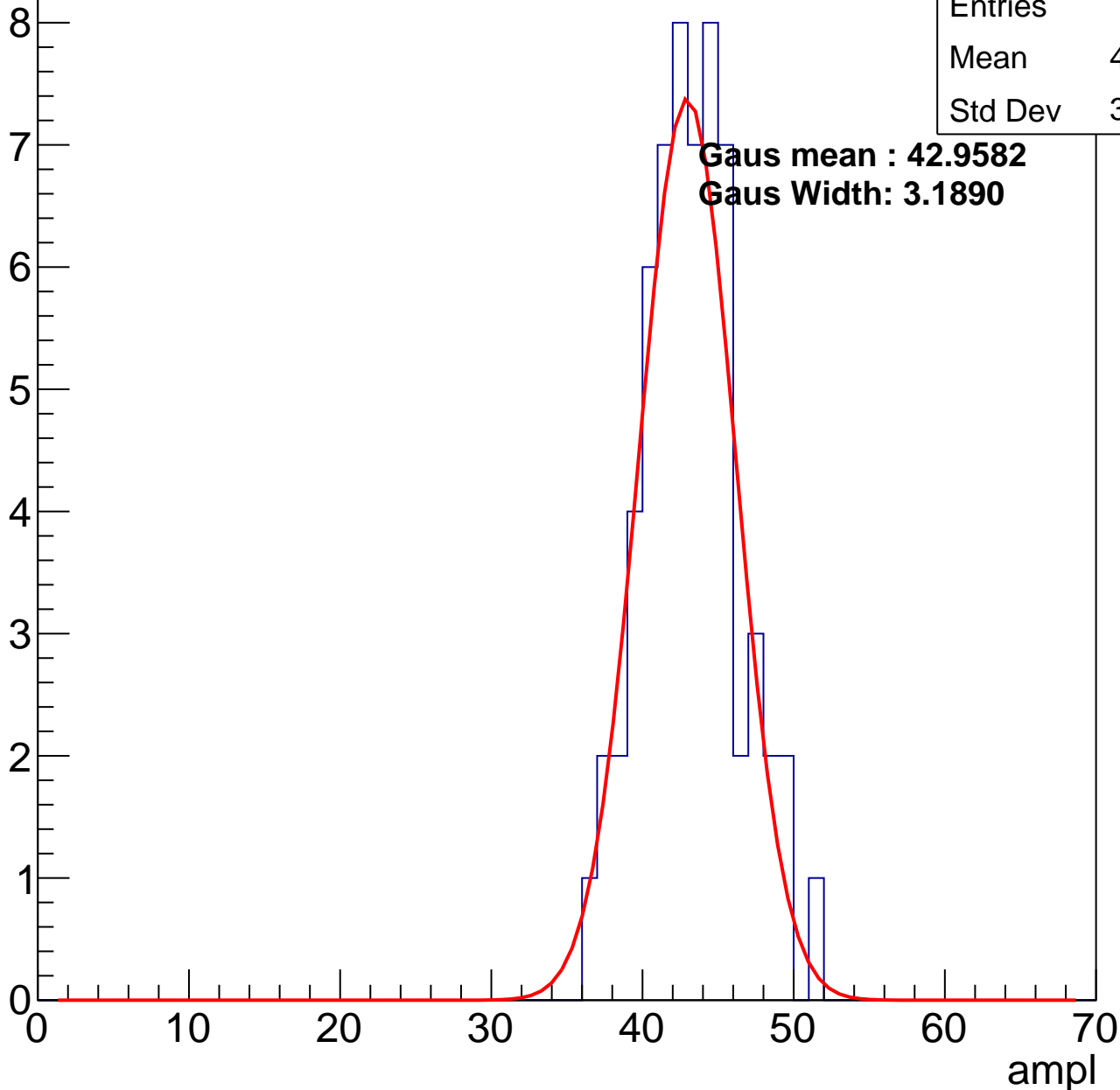
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.76
Std Dev	3.135

**Gaus mean : 42.9582**

**Gaus Width: 3.1890**

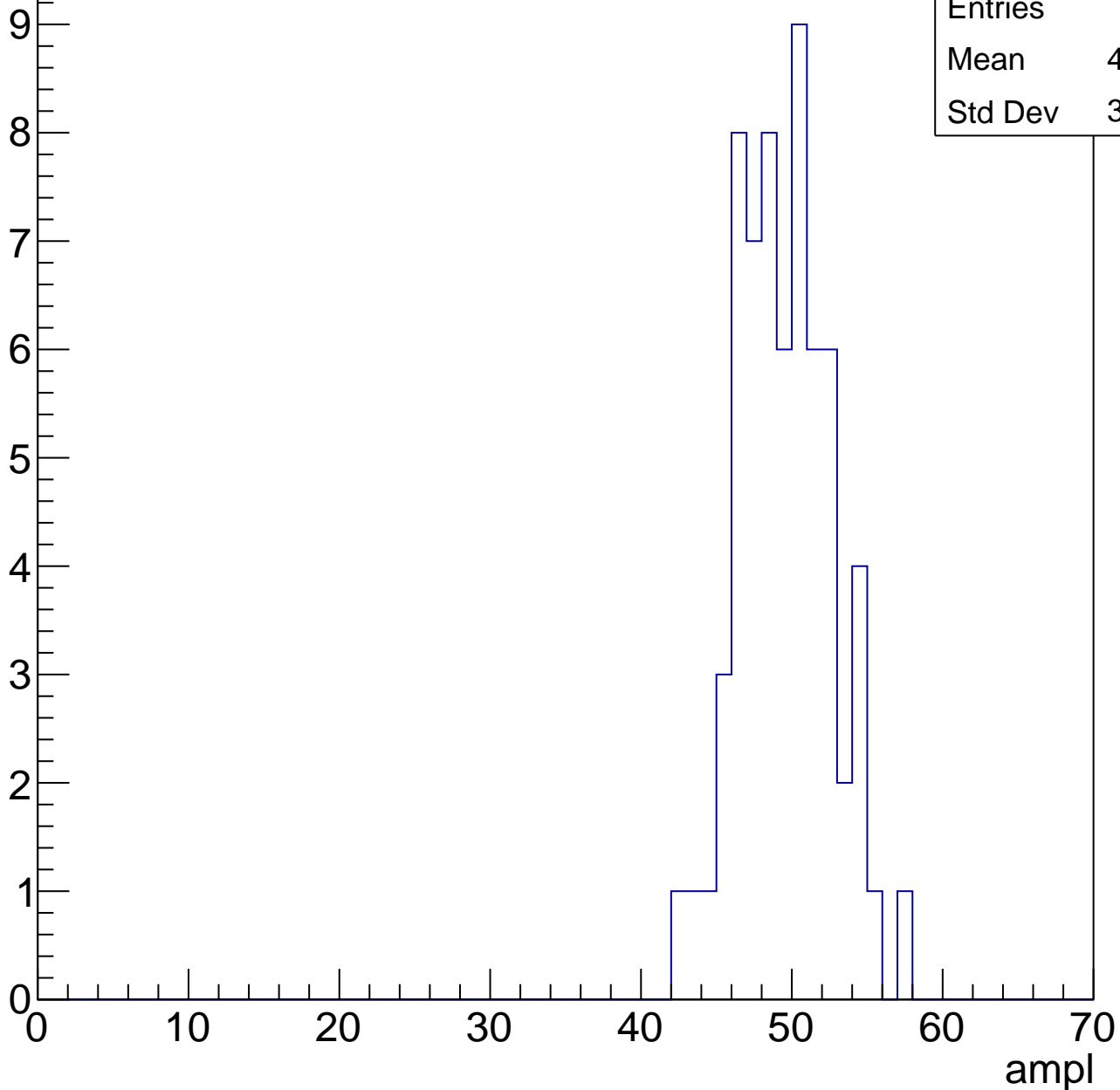


# B1L003S, U23-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.08
Std Dev	3.022

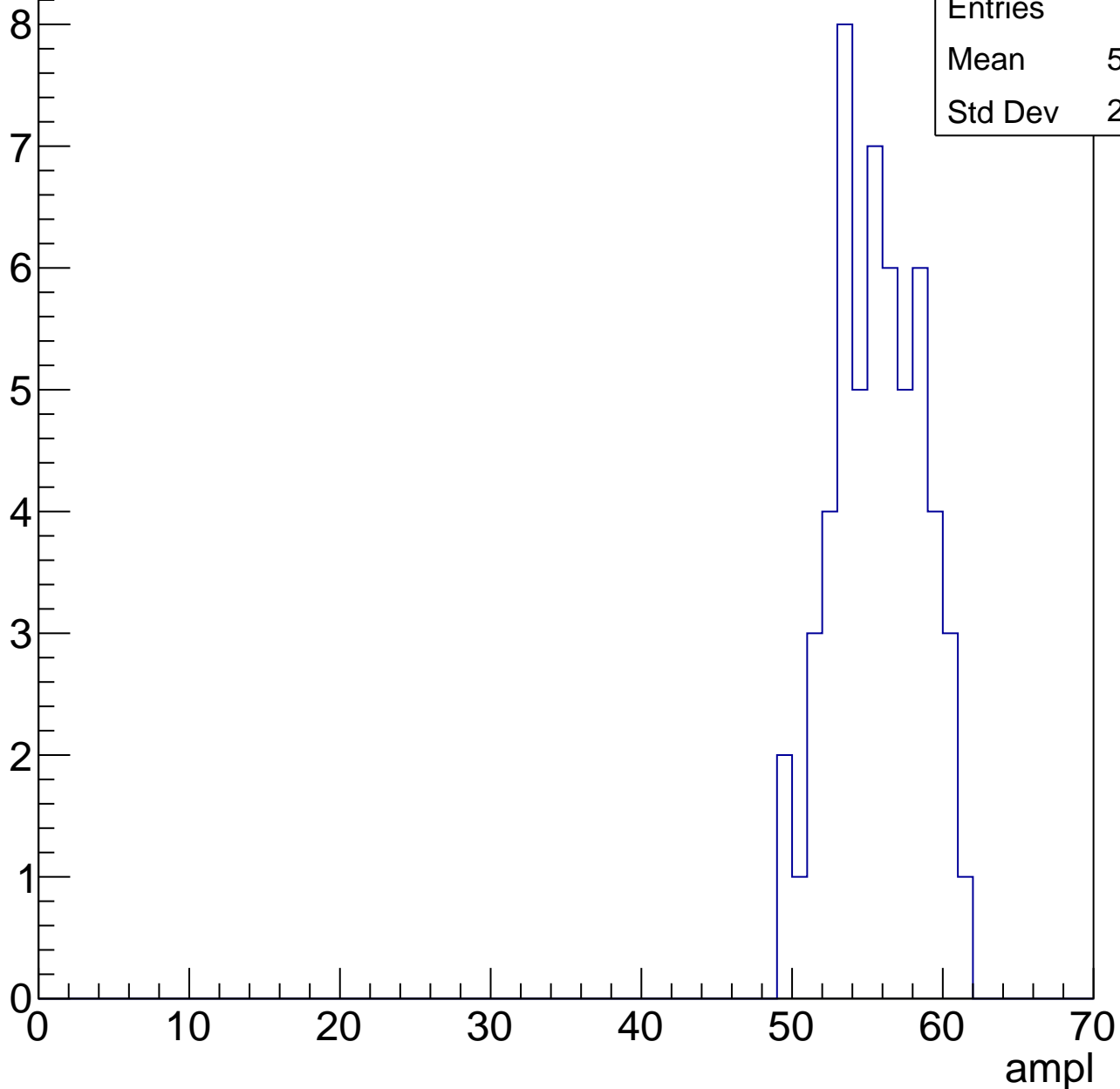


# B1L003S, U23-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.16
Std Dev	2.928

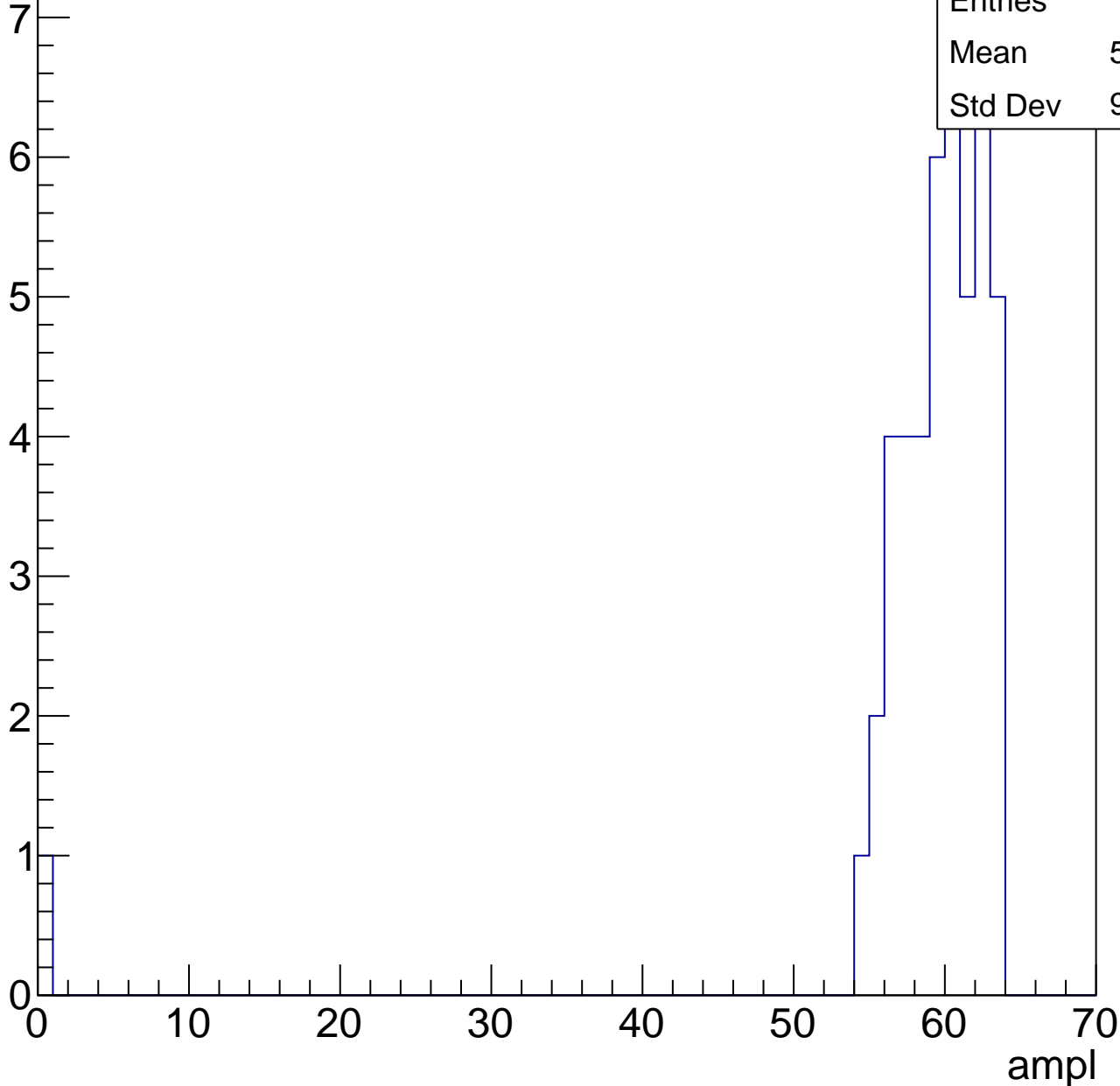


# B1L003S, U23-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.17
Std Dev	9.008

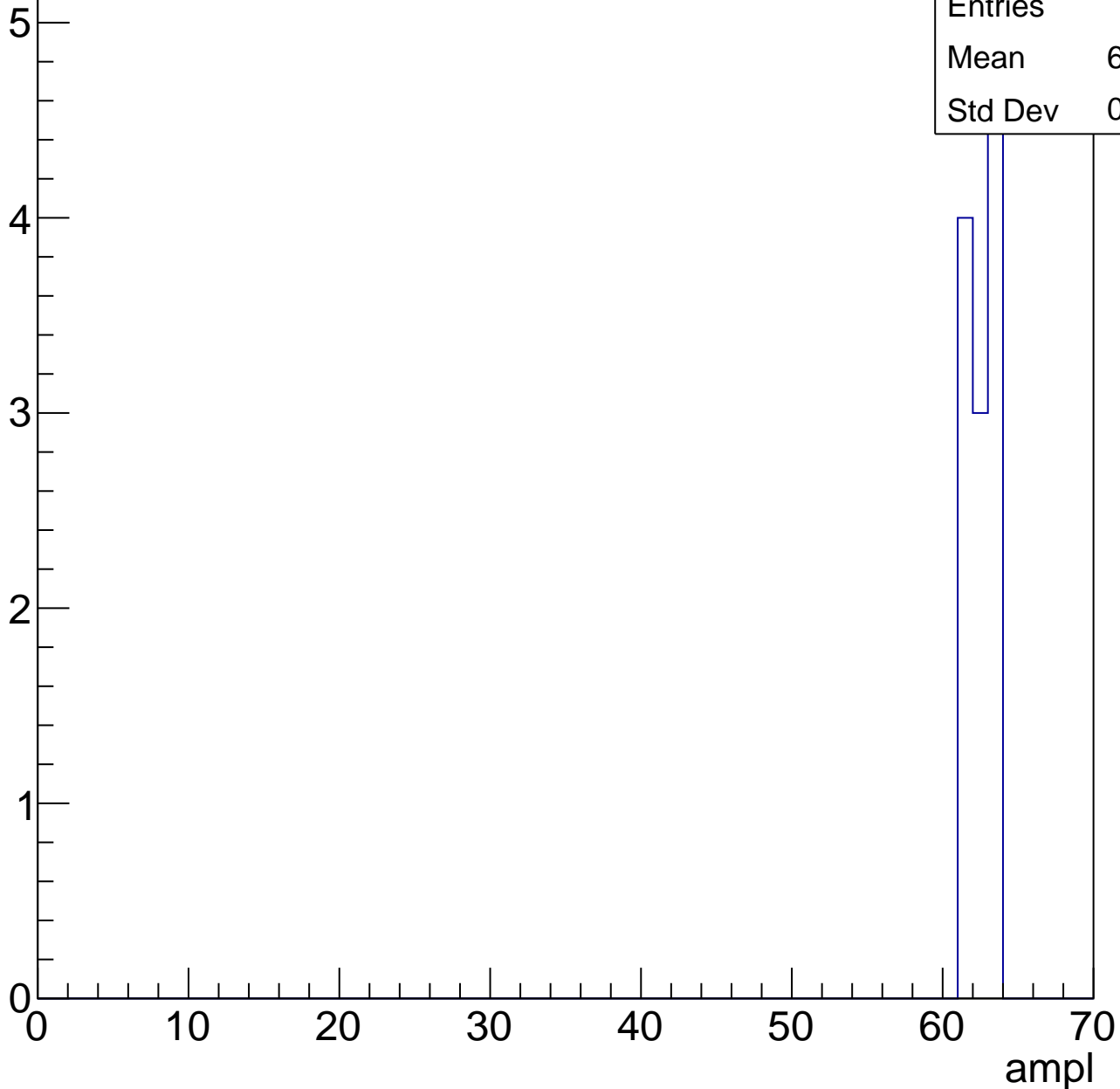


# B1L003S, U23-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	12
Mean	62.08
Std Dev	0.862





# B1L003S, U23-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch119, adc0

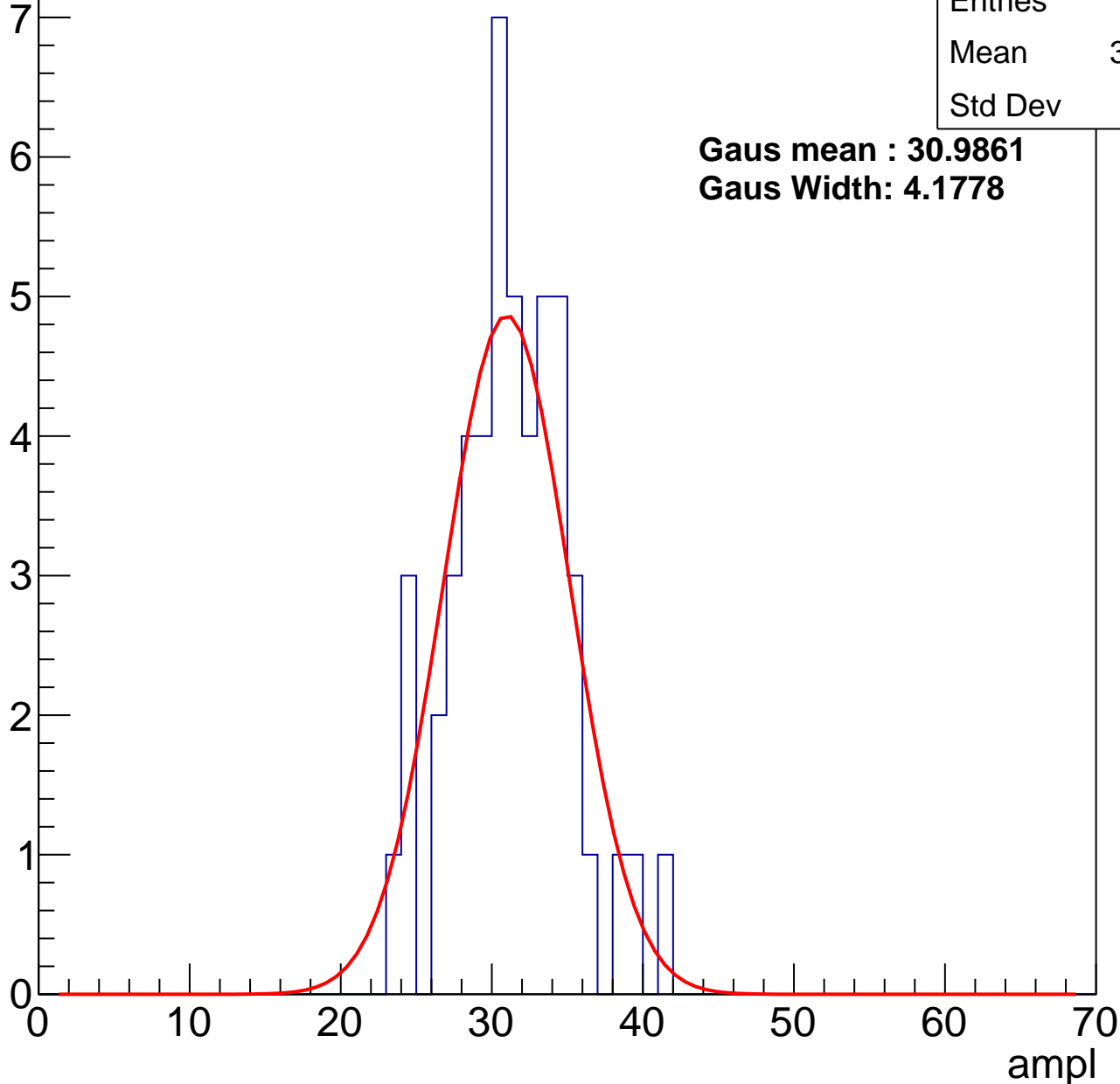
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	30.86
Std Dev	3.8

**Gaus mean : 30.9861**

**Gaus Width: 4.1778**



# B1L003S, U23-ch119, adc1

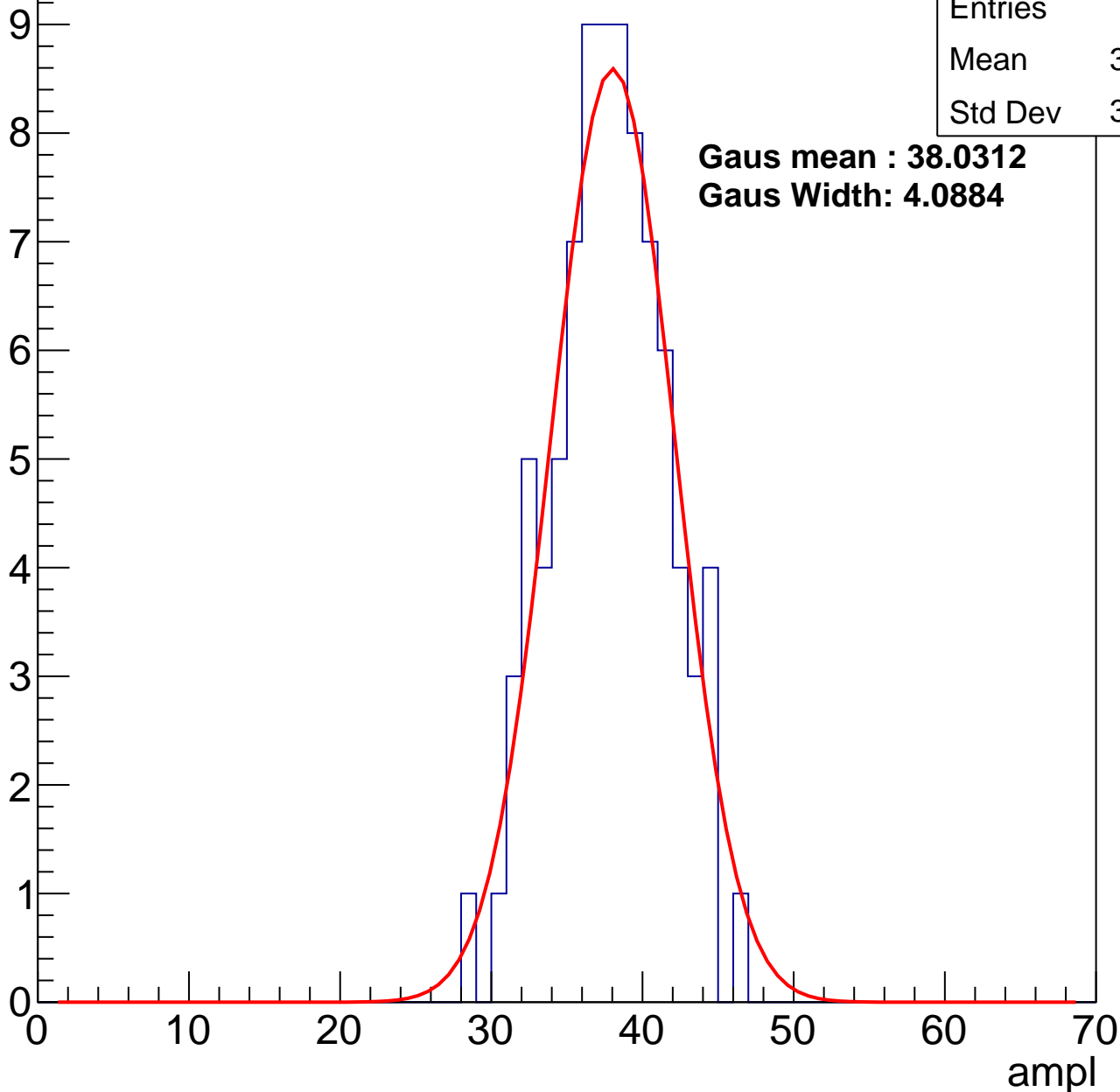
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	86
Mean	37.37
Std Dev	3.707

**Gaus mean : 38.0312**

**Gaus Width: 4.0884**



# B1L003S, U23-ch119, adc2

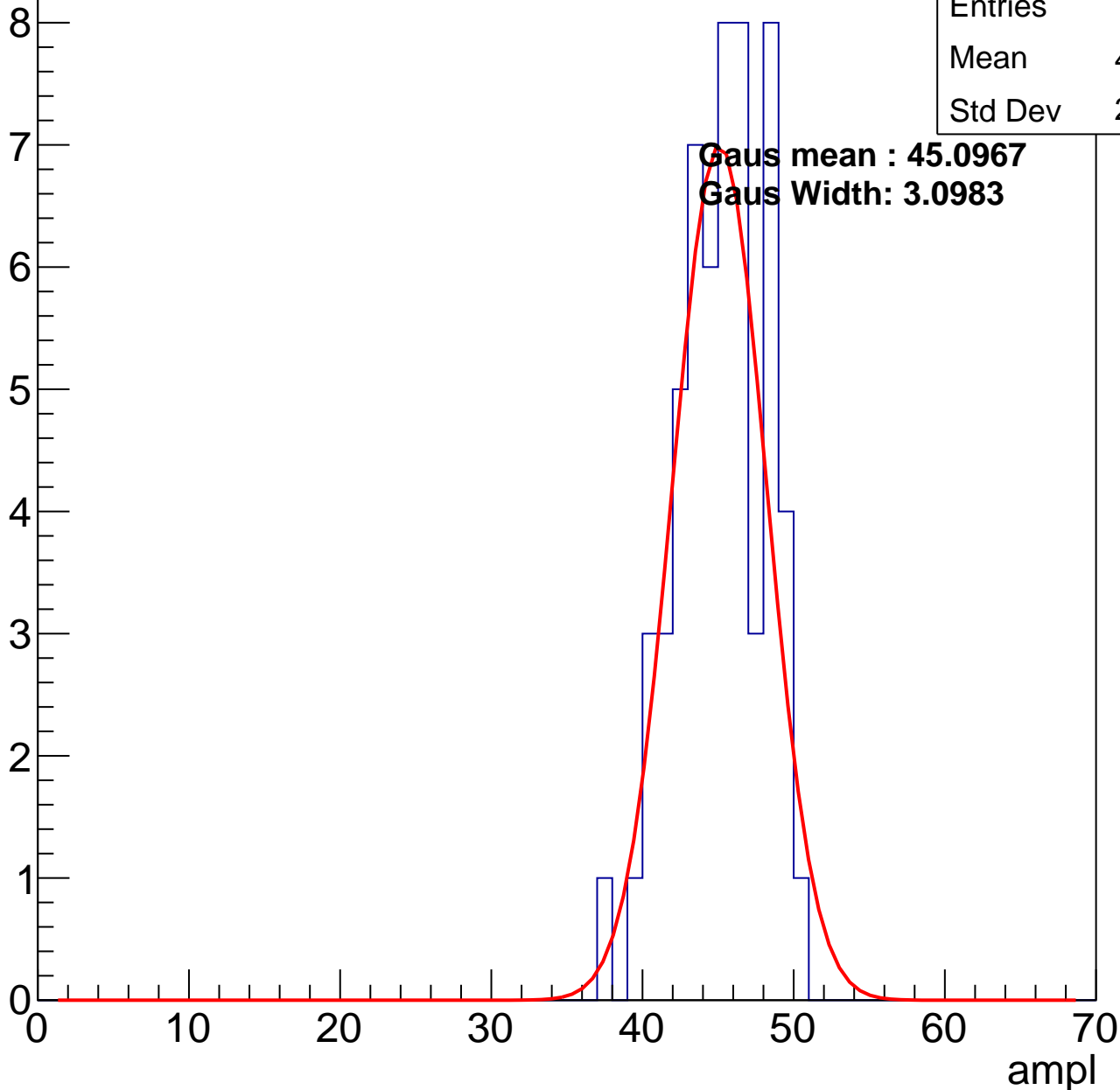
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.71
Std Dev	2.871

**Gaus mean : 45.0967**

**Gaus Width: 3.0983**

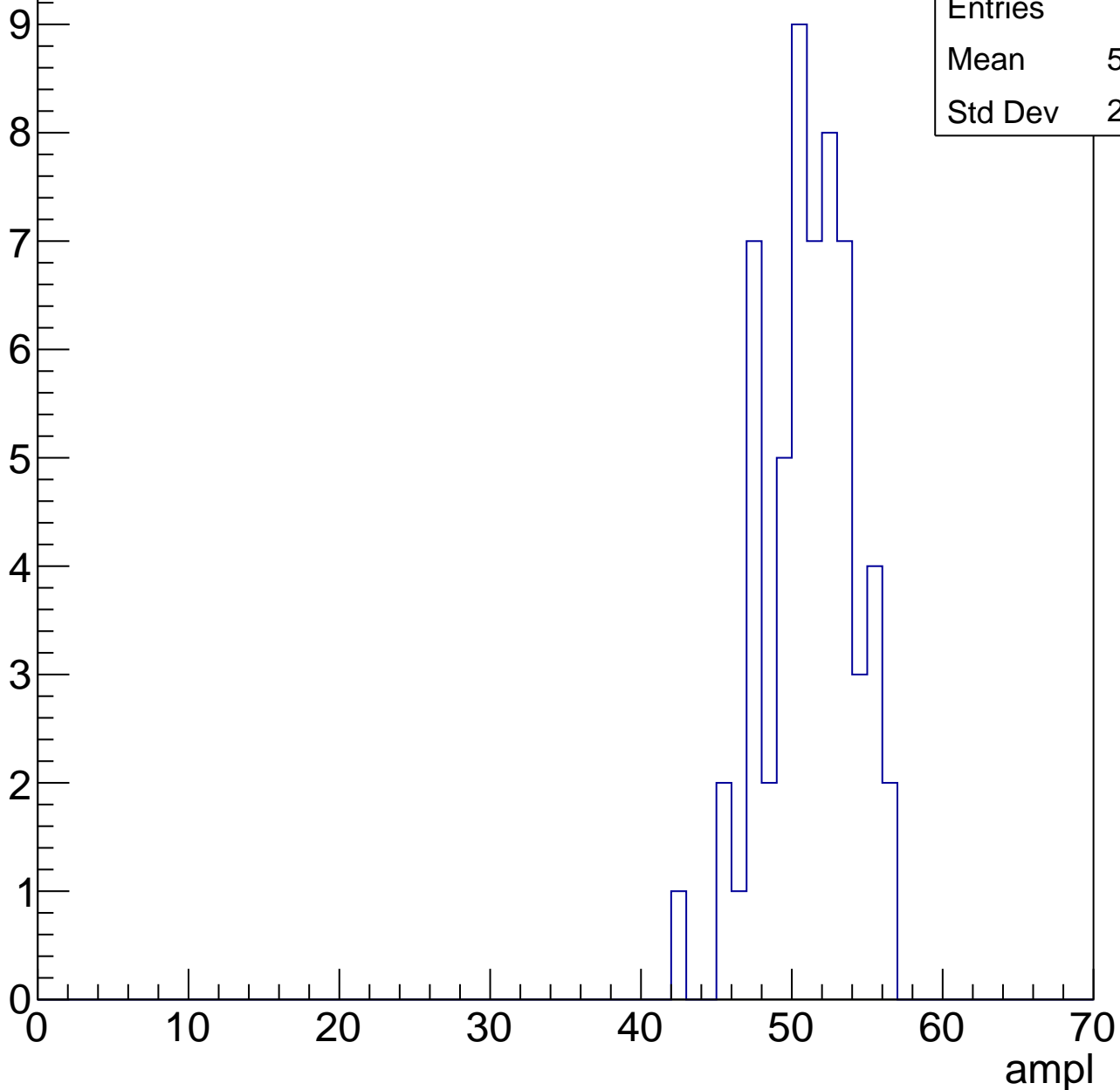


# B1L003S, U23-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	50.62
Std Dev	2.953

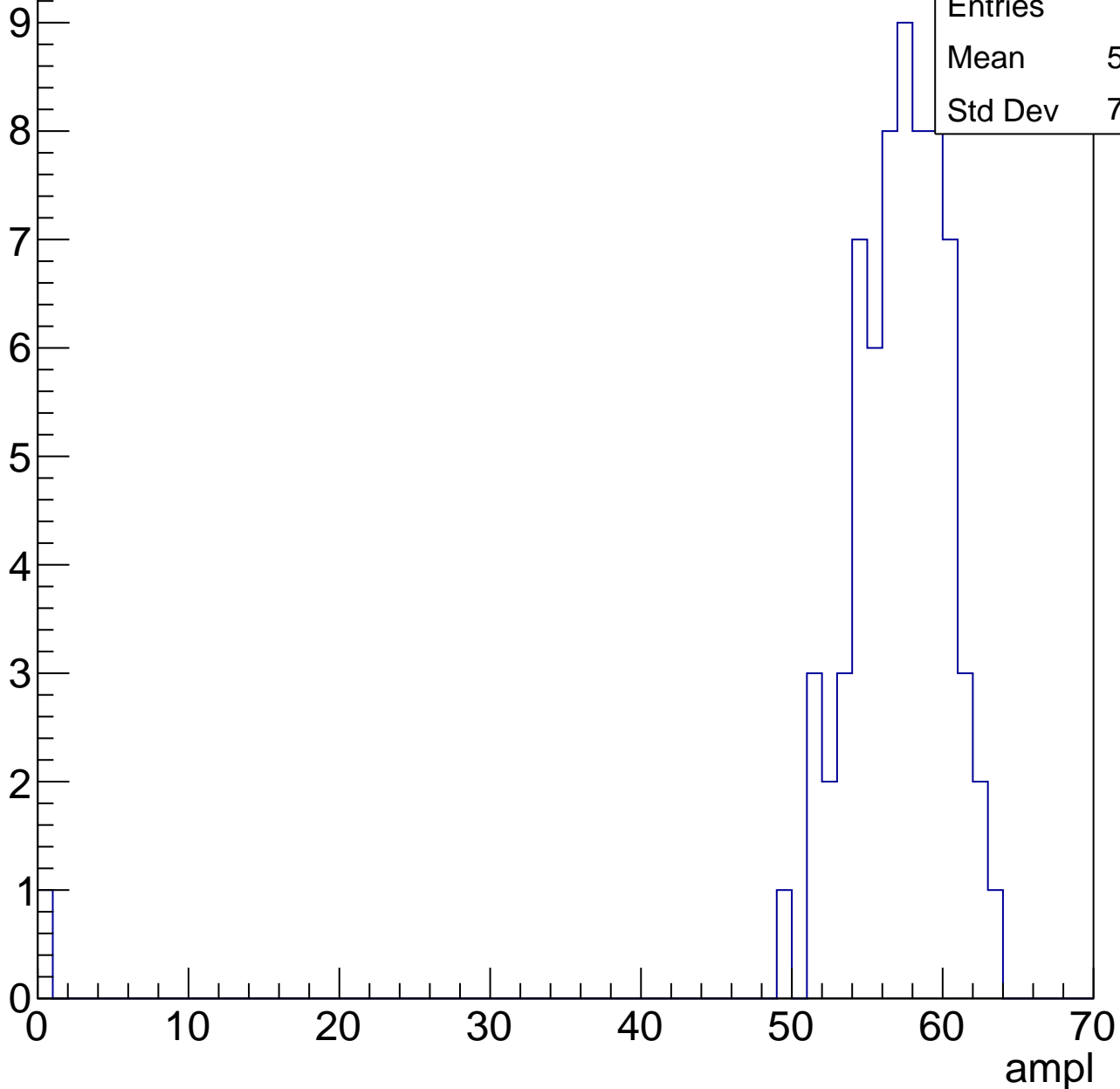


# B1L003S, U23-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	55.94
Std Dev	7.395

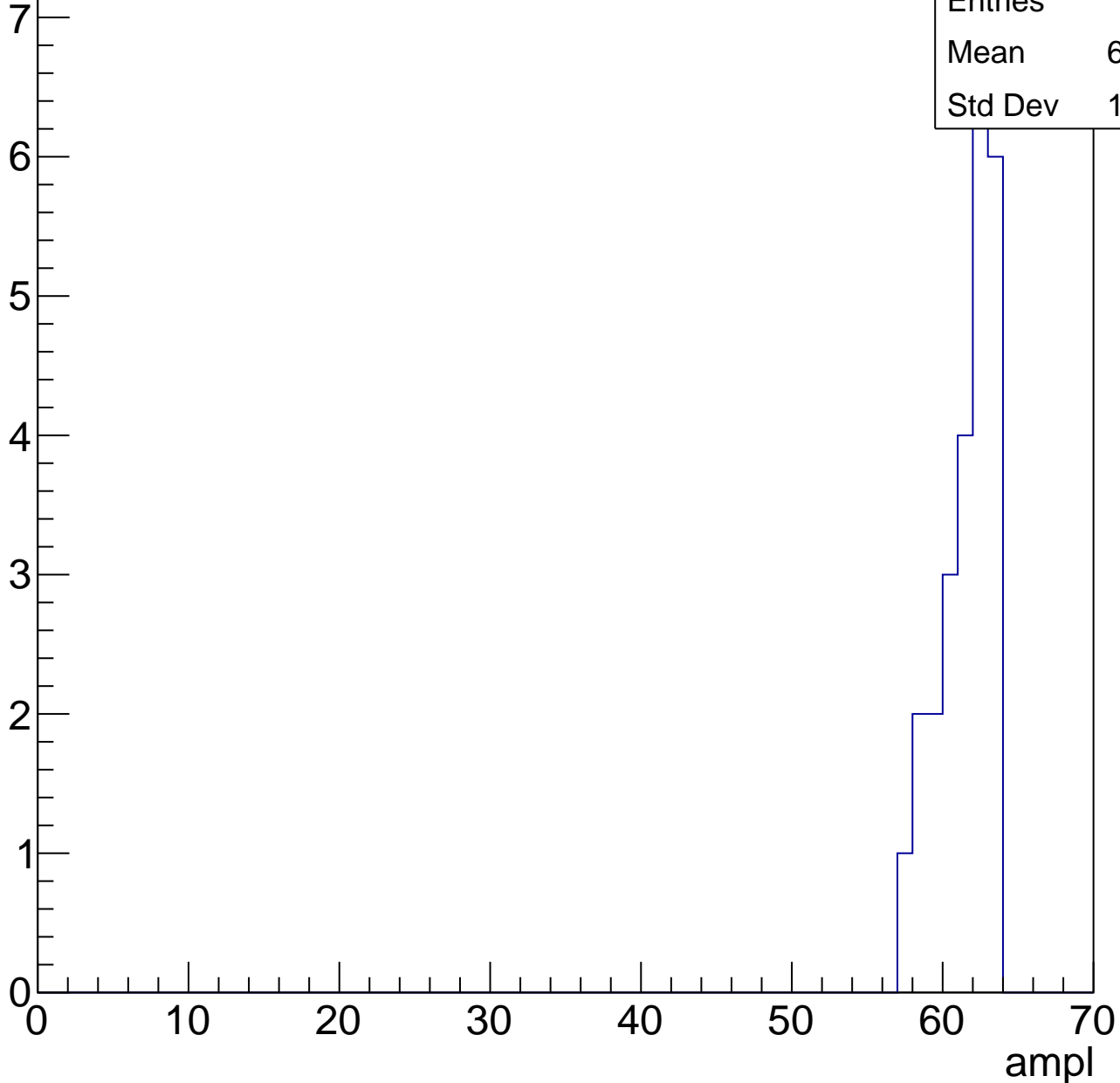


# B1L003S, U23-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

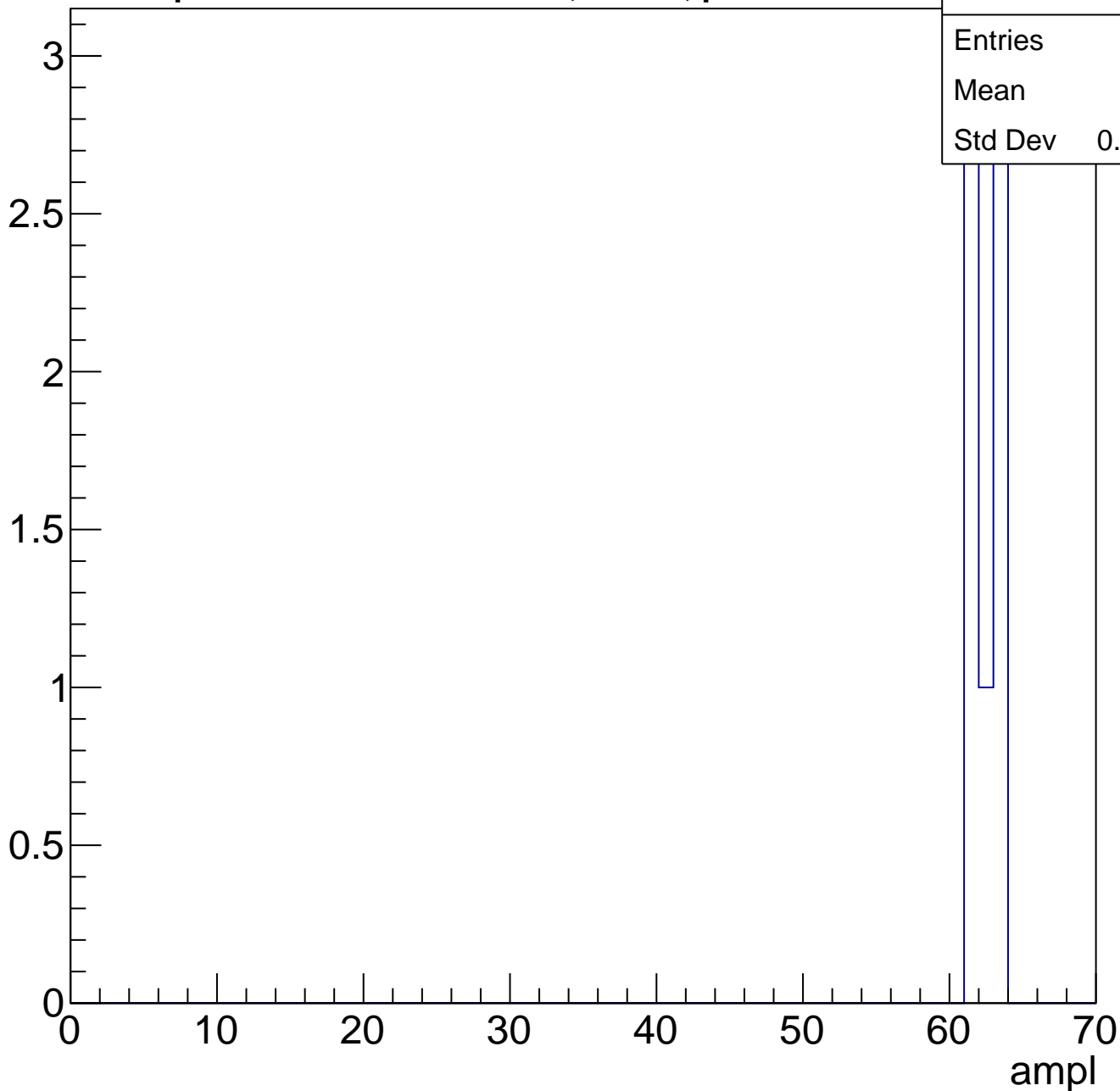
Entries	25
Mean	61.08
Std Dev	1.742



# B1L003S, U23-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch120, adc0

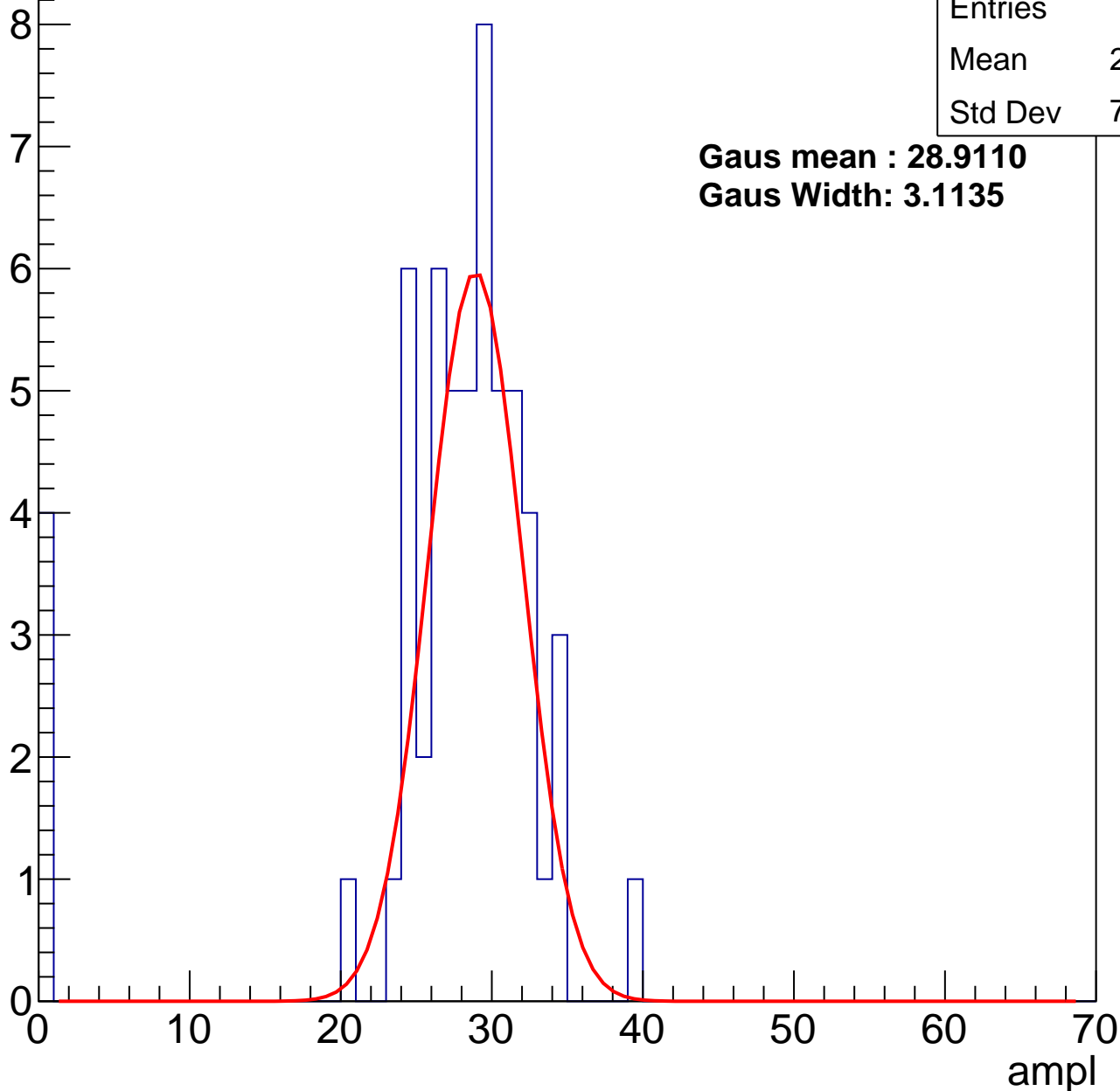
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	26.44
Std Dev	7.969

**Gaus mean : 28.9110**

**Gaus Width: 3.1135**



# B1L003S, U23-ch120, adc1

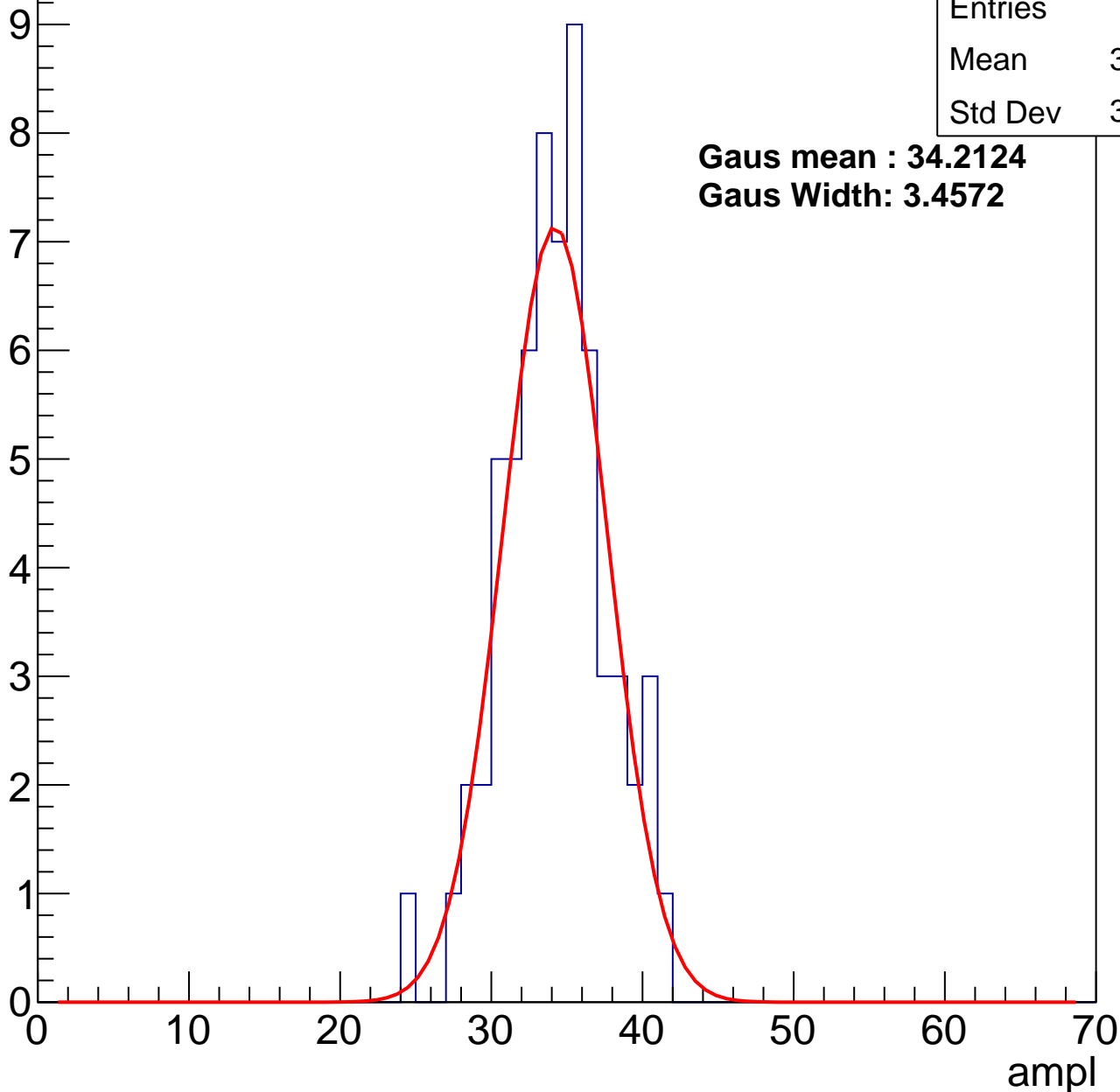
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	33.73
Std Dev	3.397

**Gaus mean : 34.2124**

**Gaus Width: 3.4572**



# B1L003S, U23-ch120, adc2

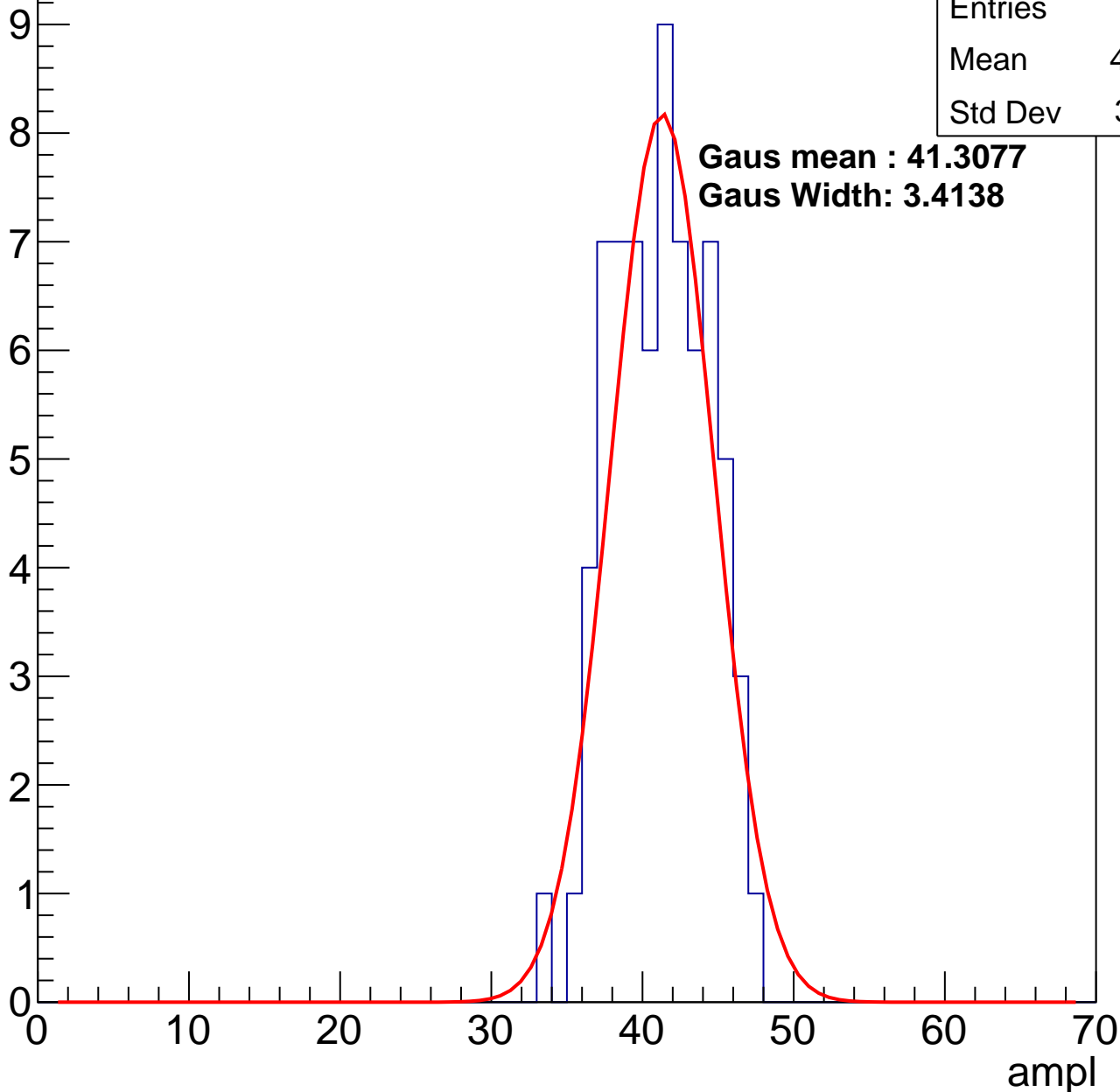
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	40.69
Std Dev	3.111

**Gaus mean : 41.3077**

**Gaus Width: 3.4138**

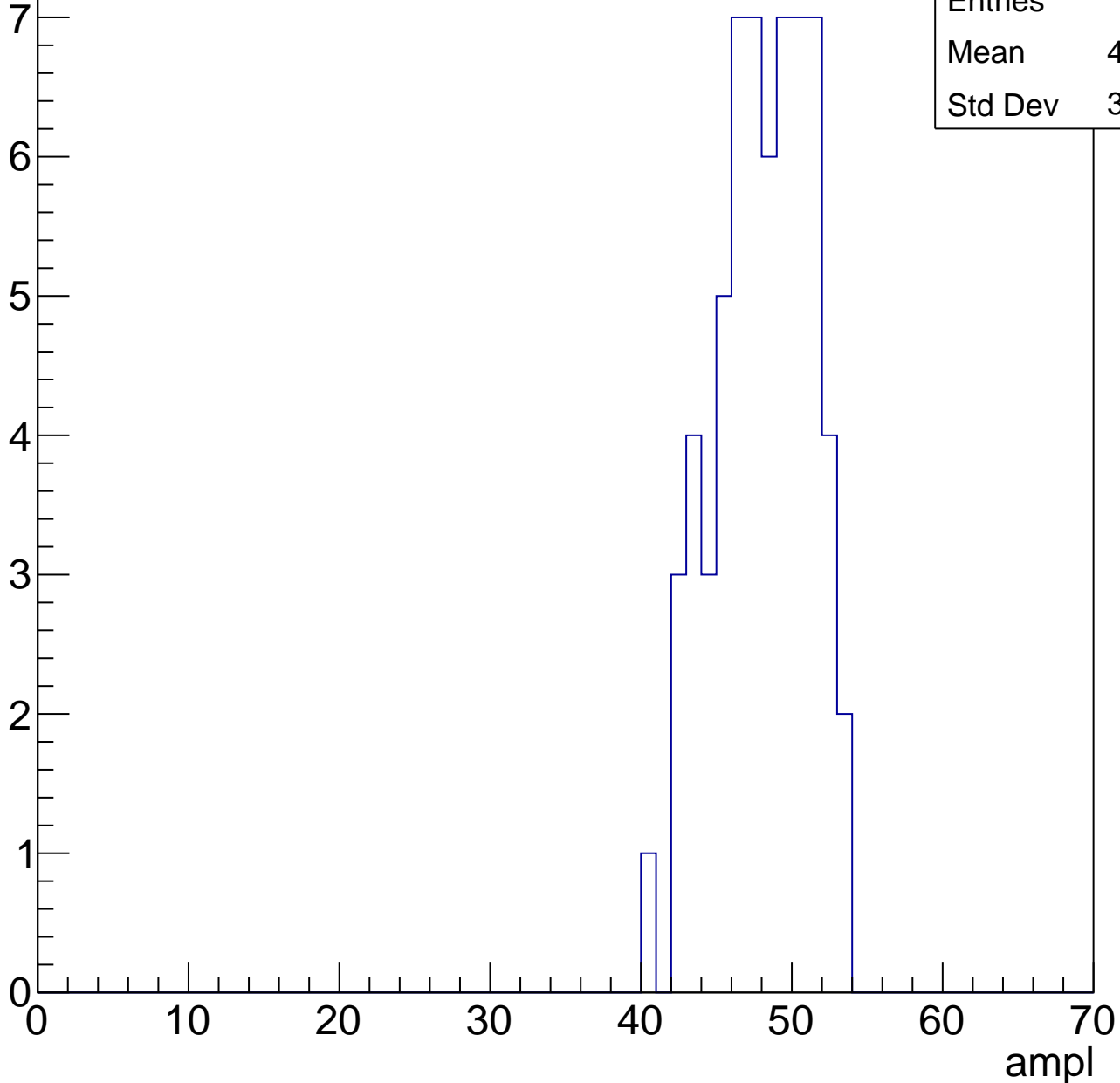


# B1L003S, U23-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	47.59
Std Dev	3.089

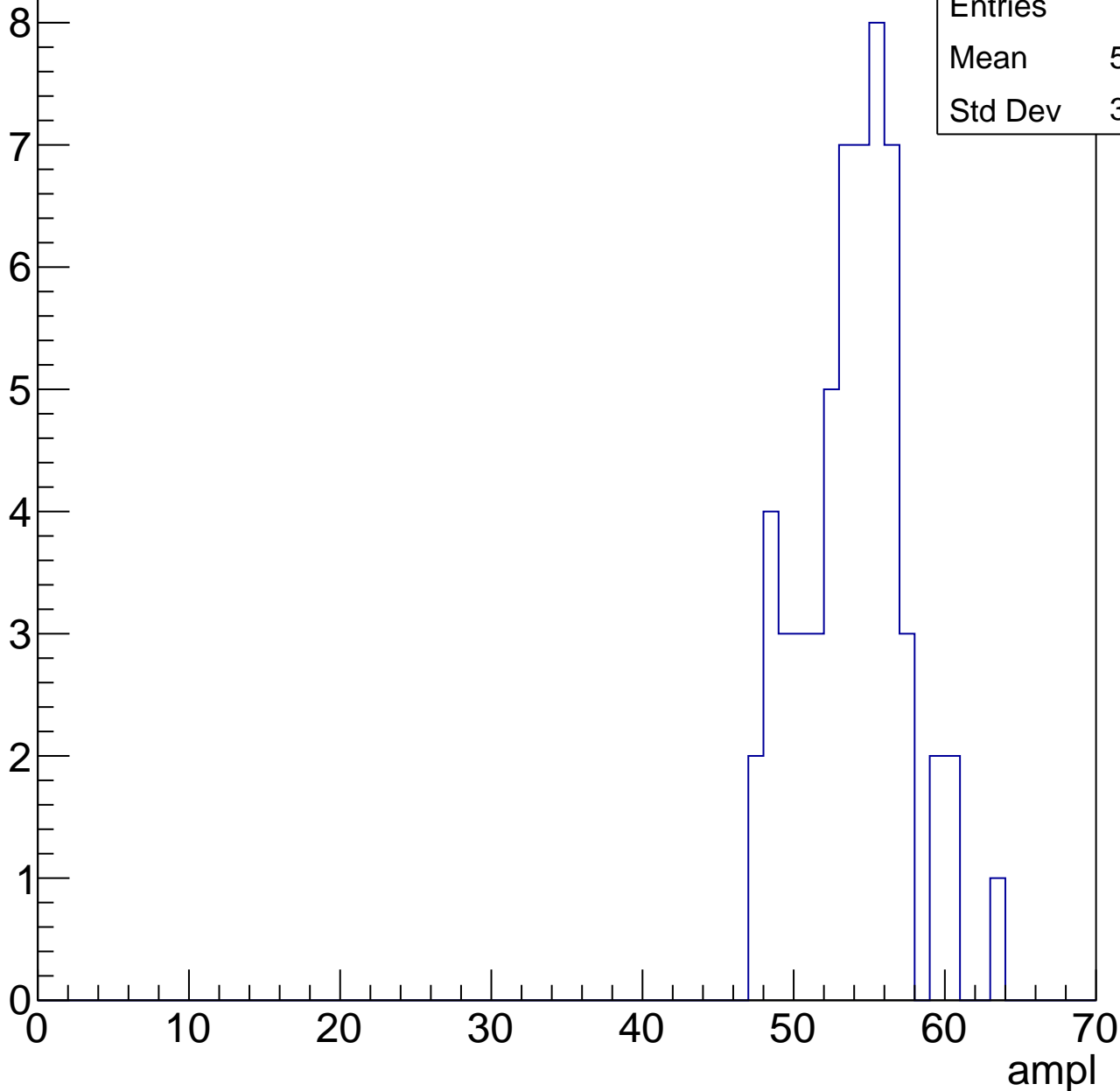


# B1L003S, U23-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

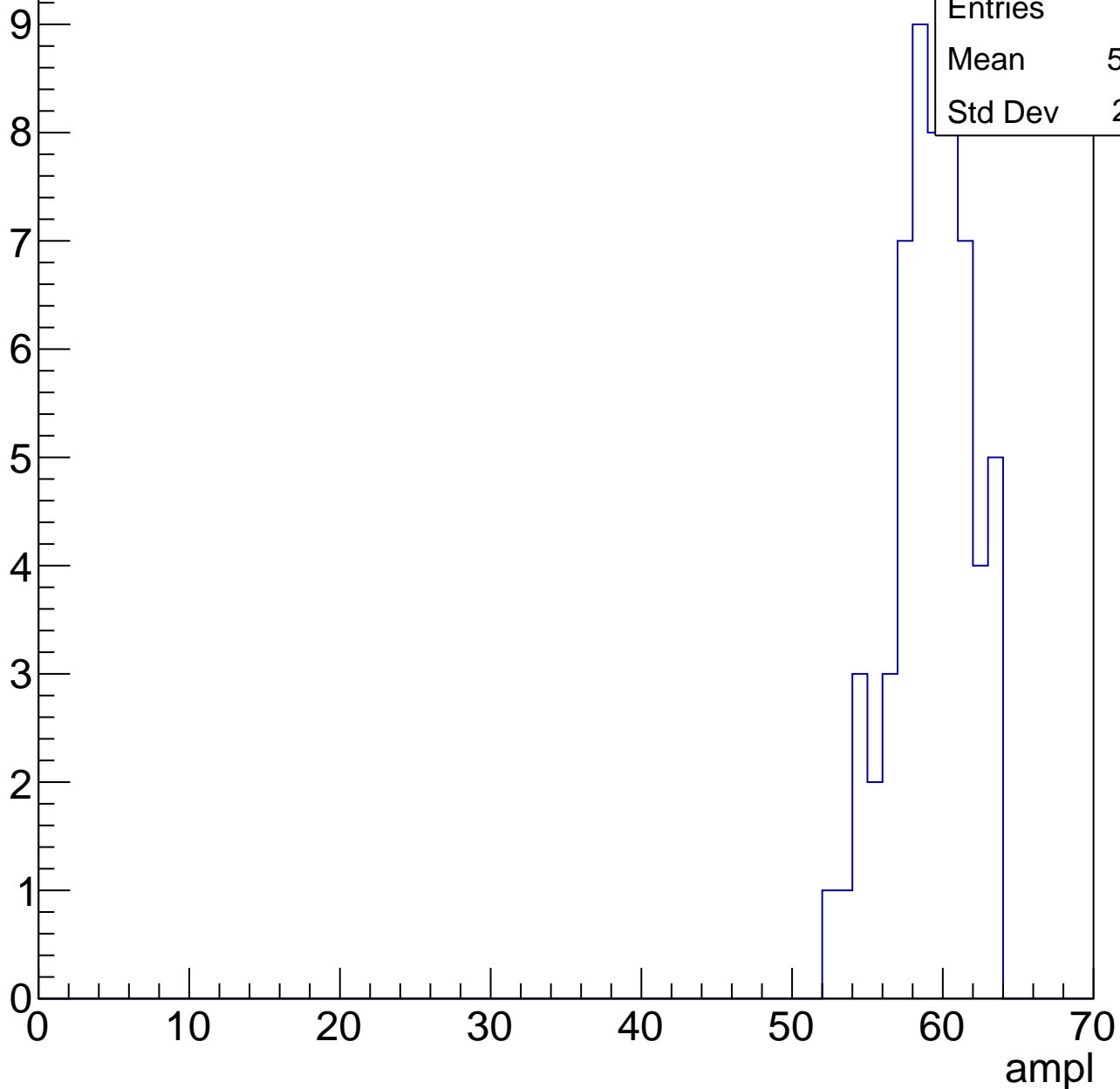
Entries	57
Mean	53.49
Std Dev	3.429



# B1L003S, U23-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



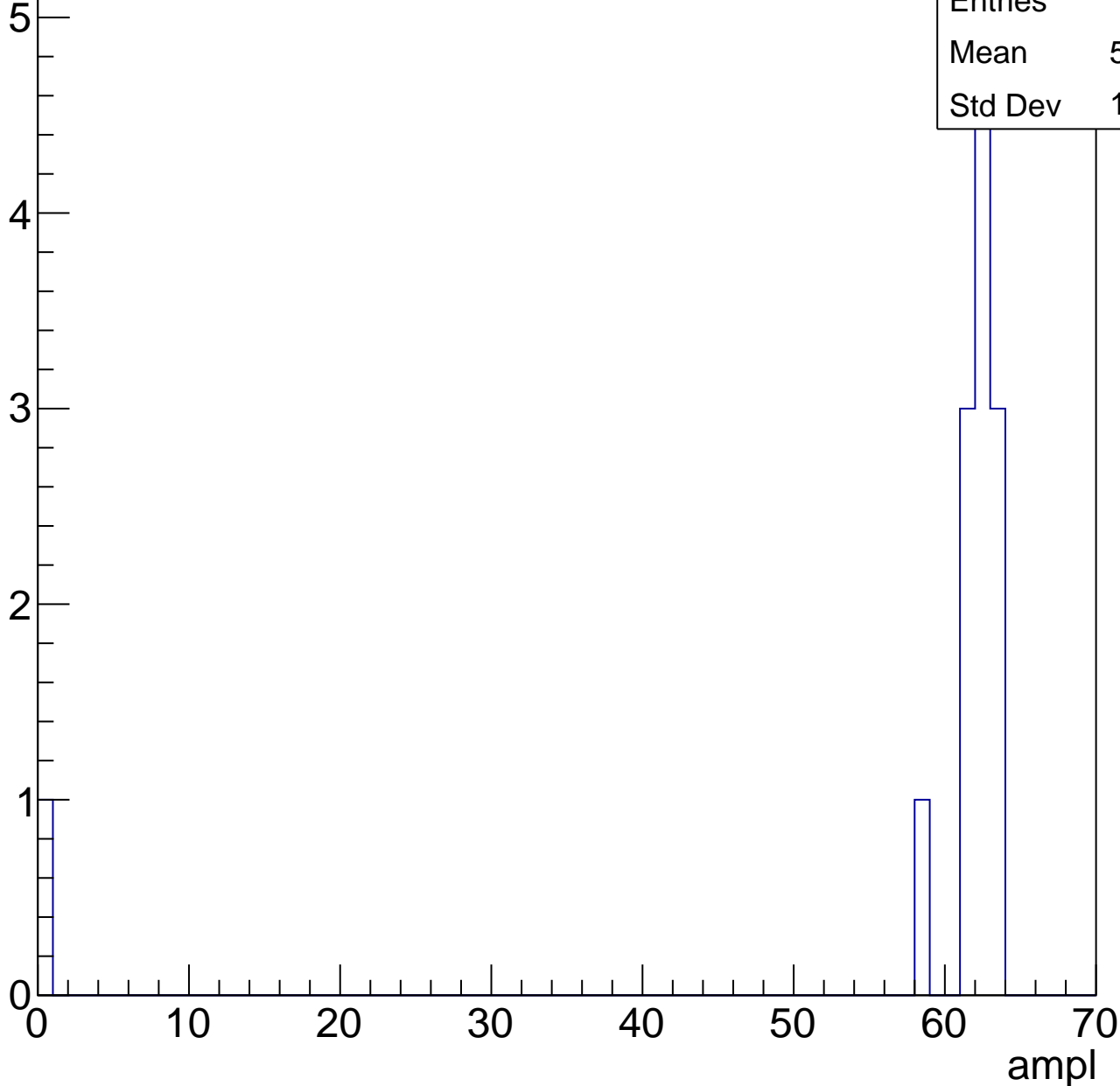
Entries	58
Mean	58.76
Std Dev	2.641

# B1L003S, U23-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	13
Mean	56.92
Std Dev	16.48

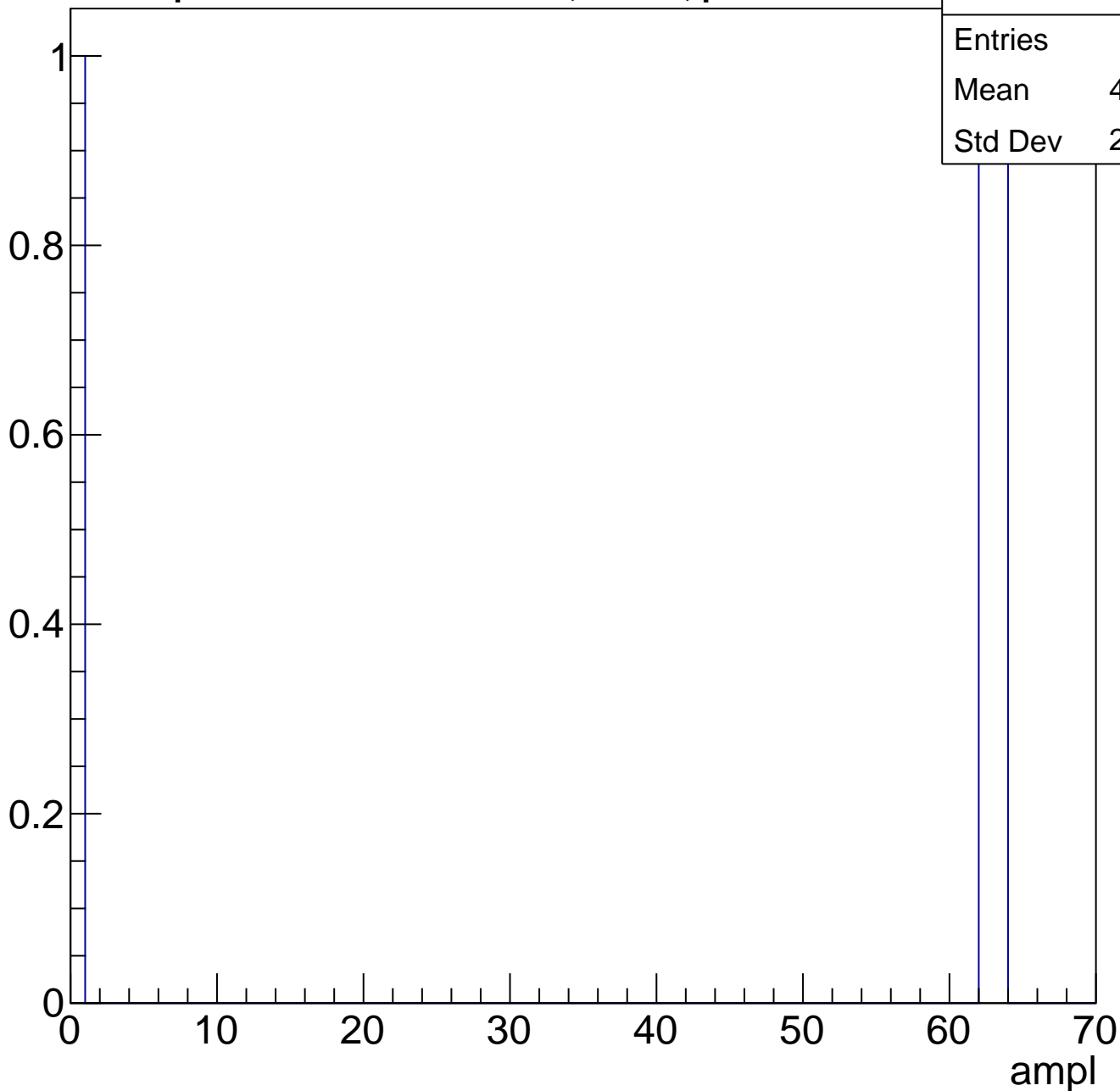




# B1L003S, U23-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	41.67
Std Dev	29.47

# B1L003S, U23-ch121, adc0

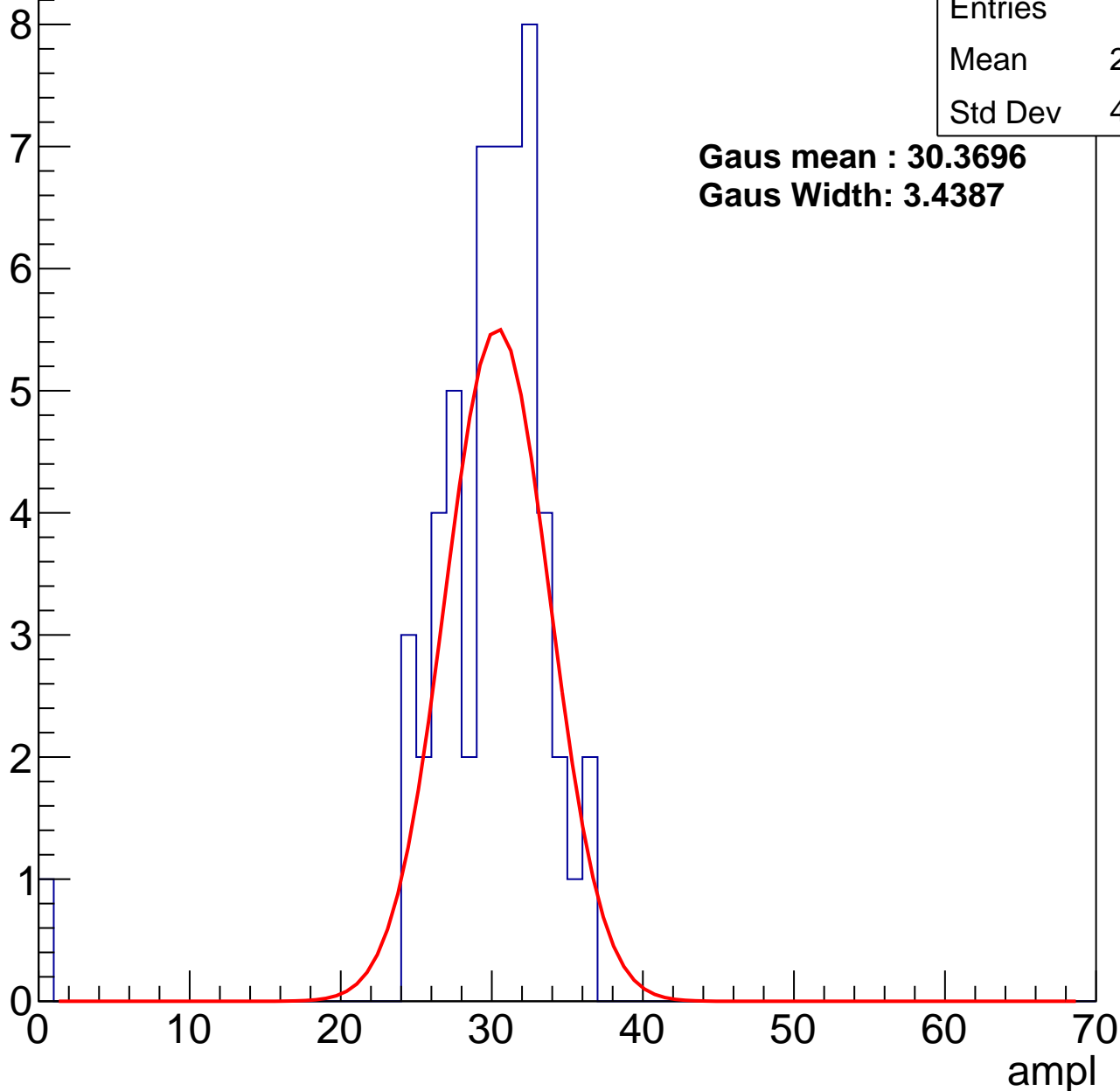
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.27
Std Dev	4.967

**Gaus mean : 30.3696**

**Gaus Width: 3.4387**



# B1L003S, U23-ch121, adc1

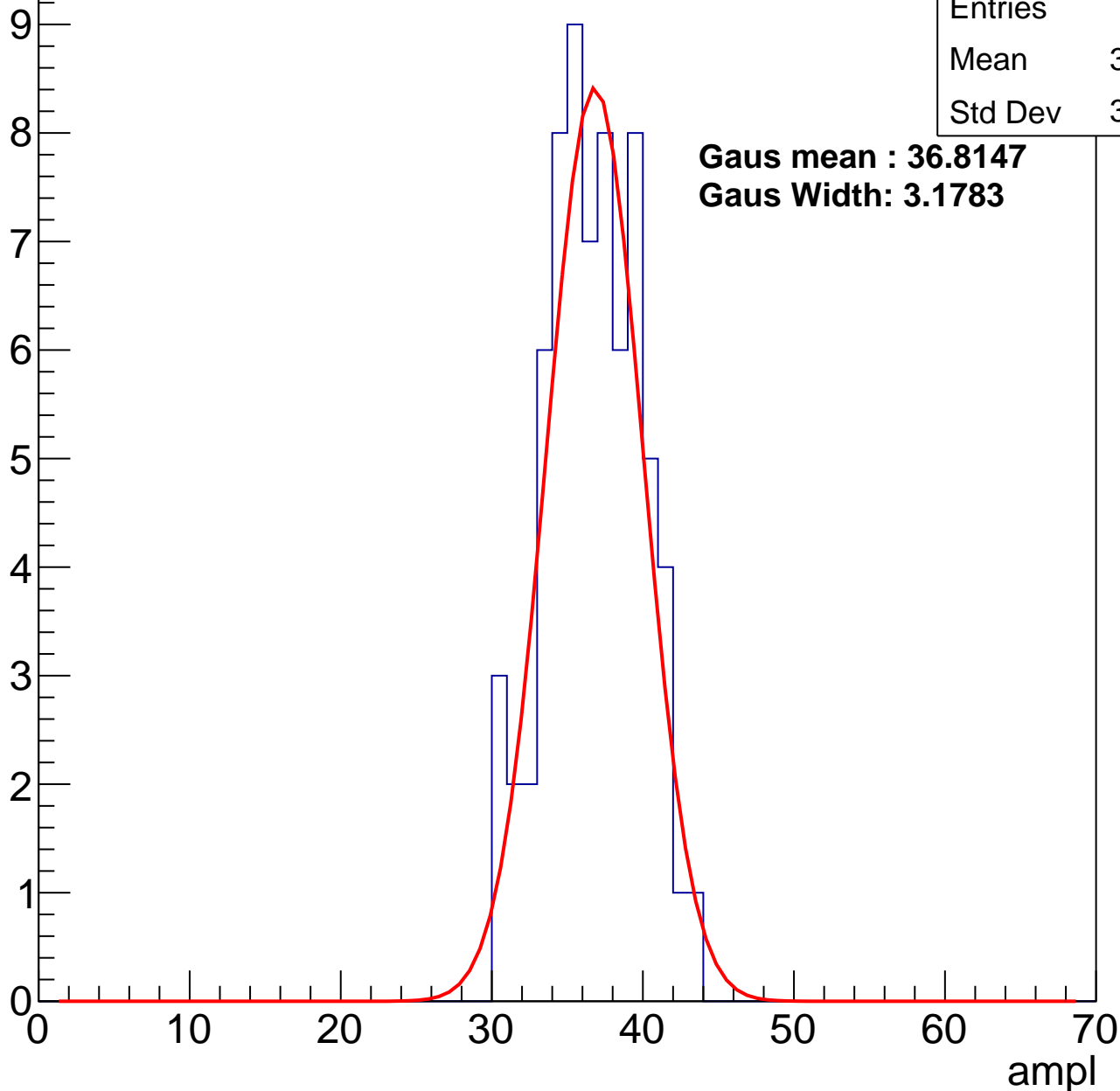
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	36.26
Std Dev	3.046

**Gaus mean : 36.8147**

**Gaus Width: 3.1783**



# B1L003S, U23-ch121, adc2

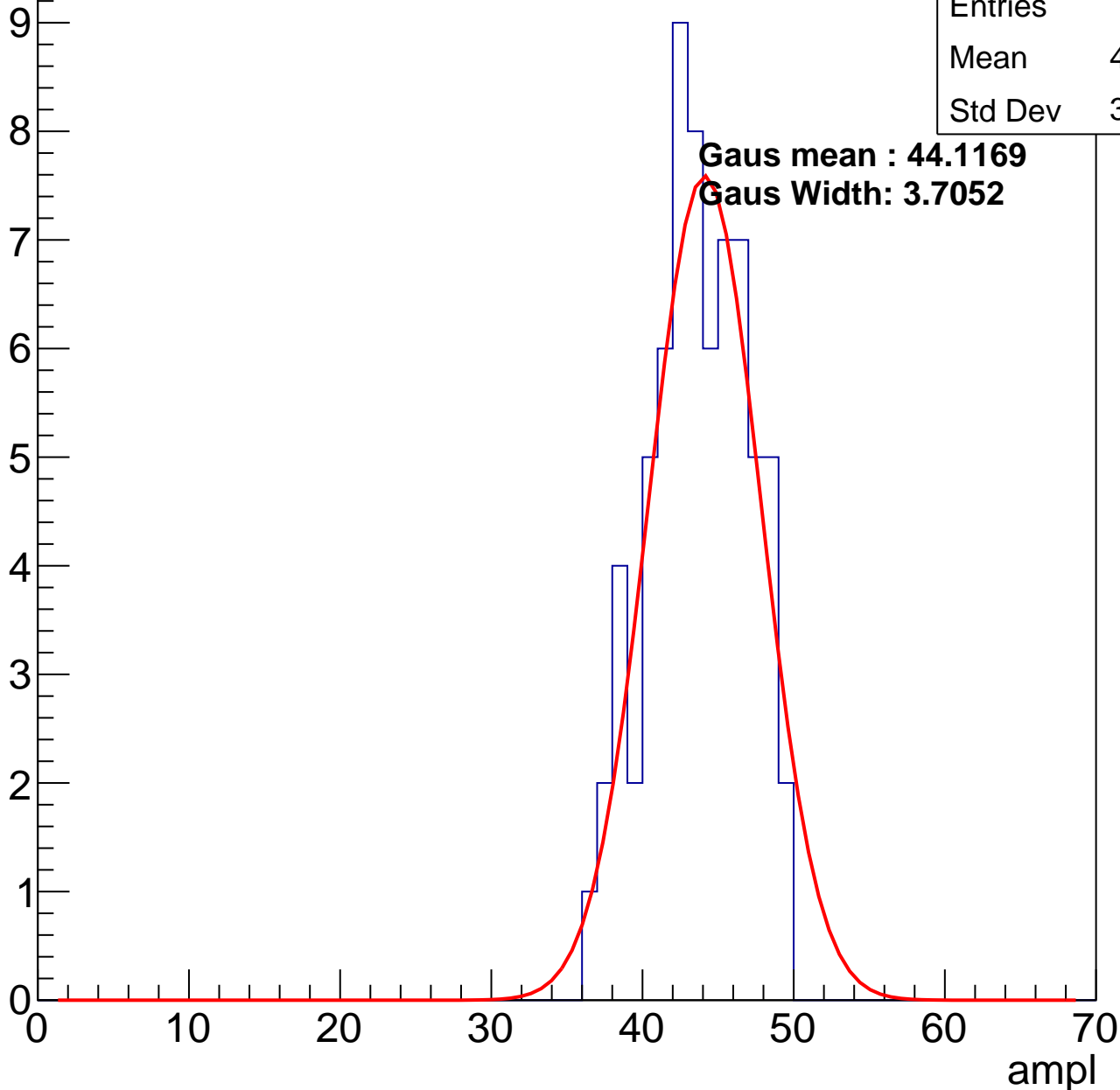
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.22
Std Dev	3.189

**Gaus mean : 44.1169**

**Gaus Width: 3.7052**

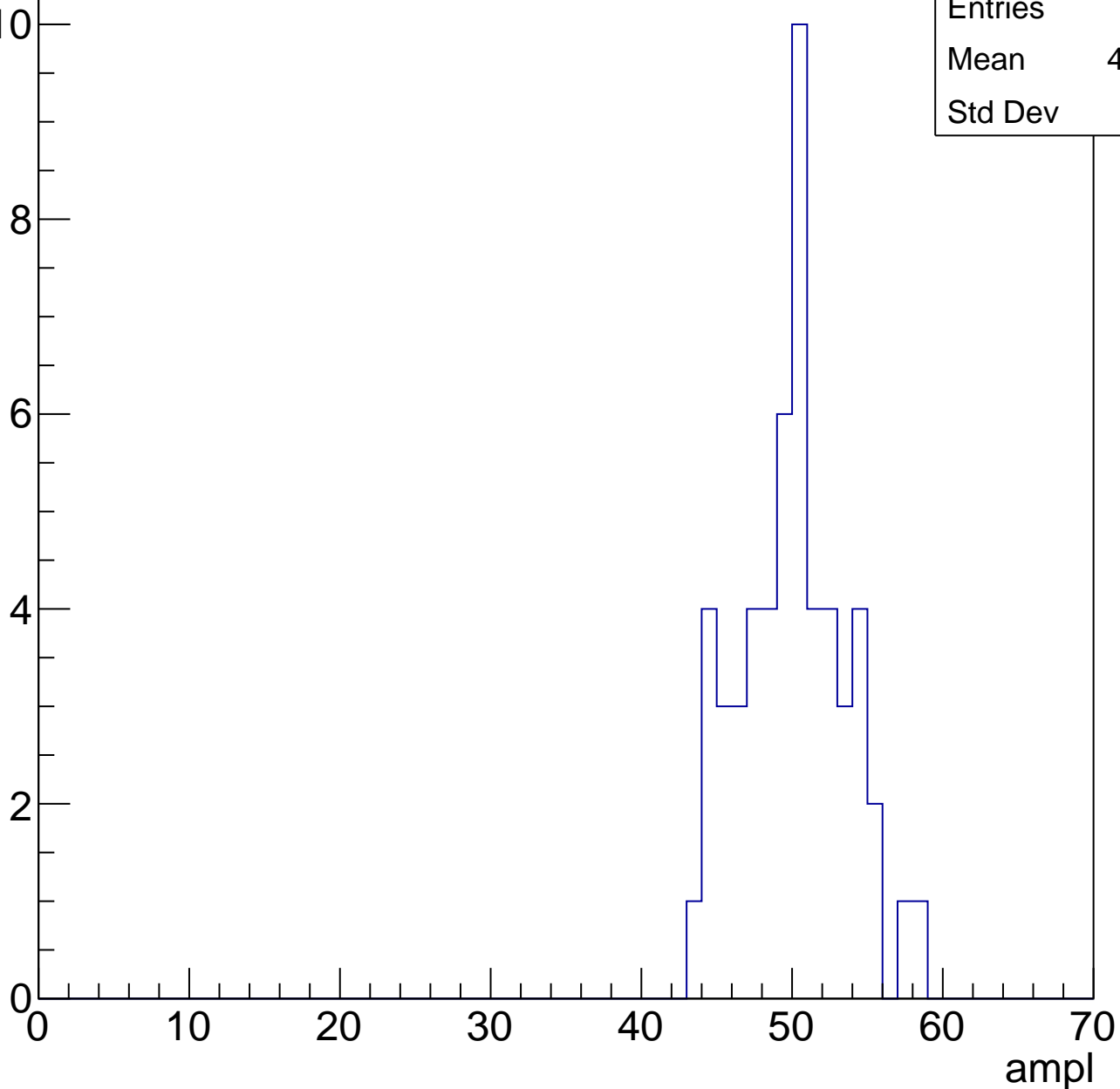


# B1L003S, U23-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	49.59
Std Dev	3.44

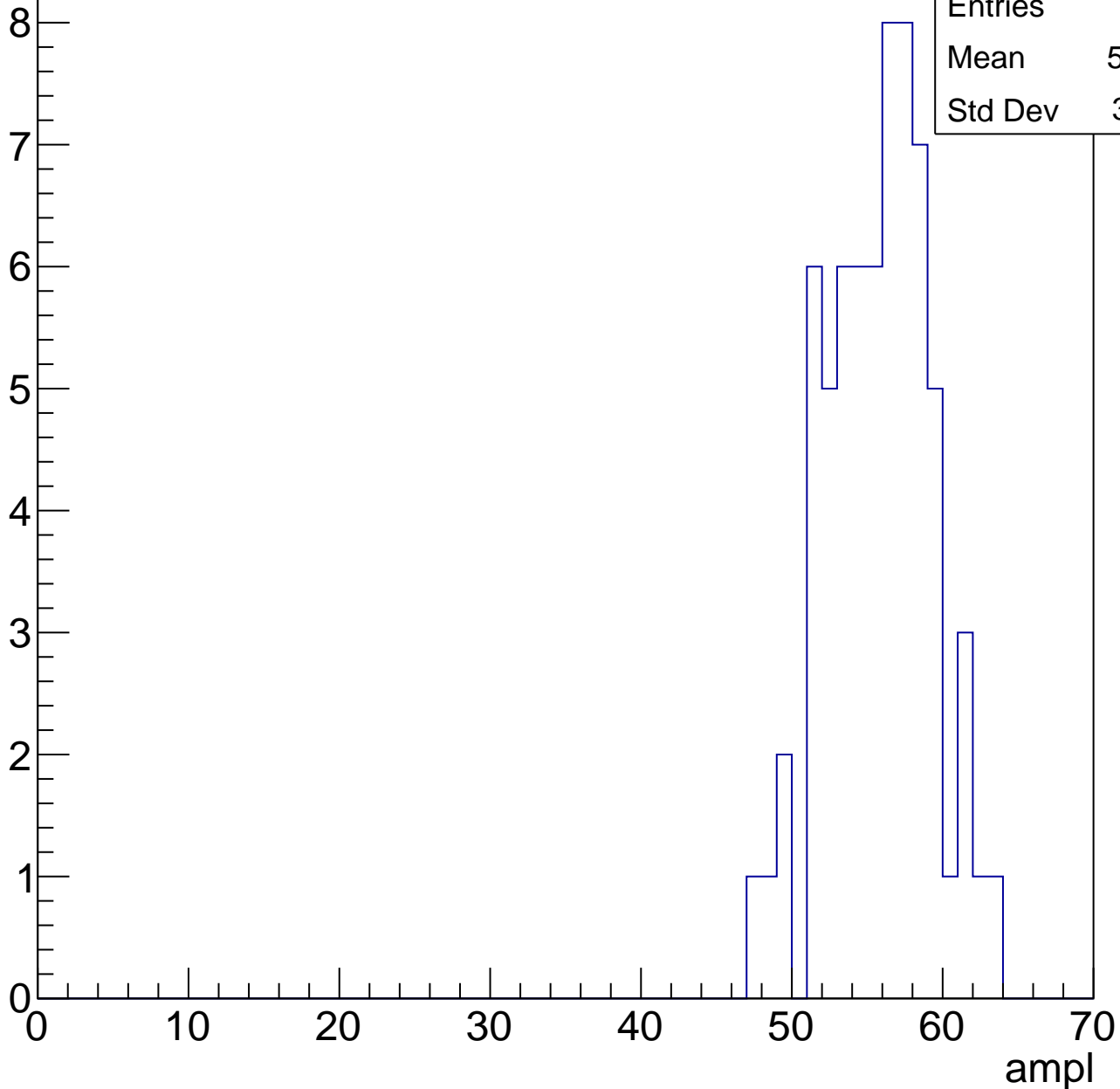


# B1L003S, U23-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.28
Std Dev	3.411

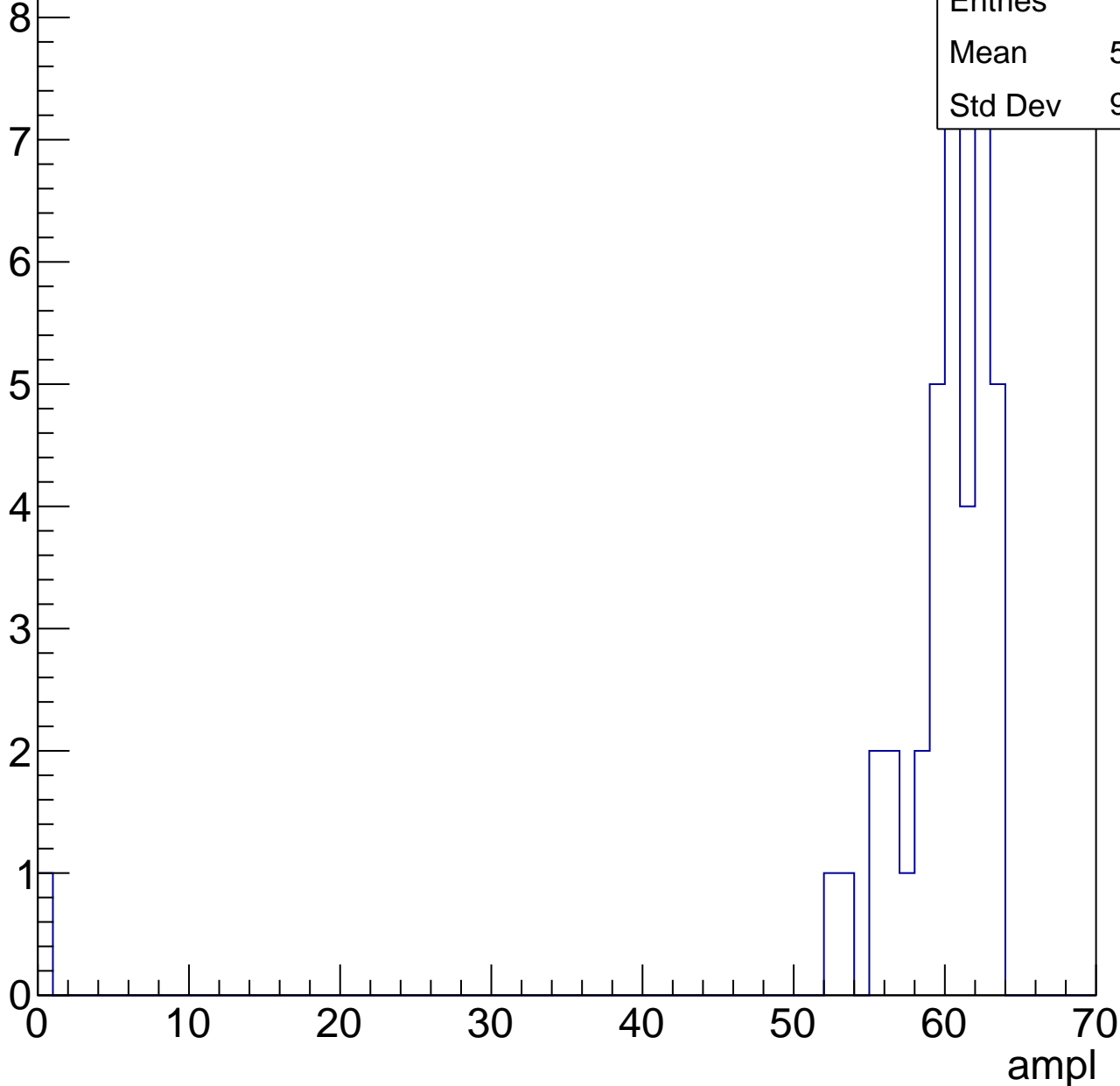


# B1L003S, U23-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

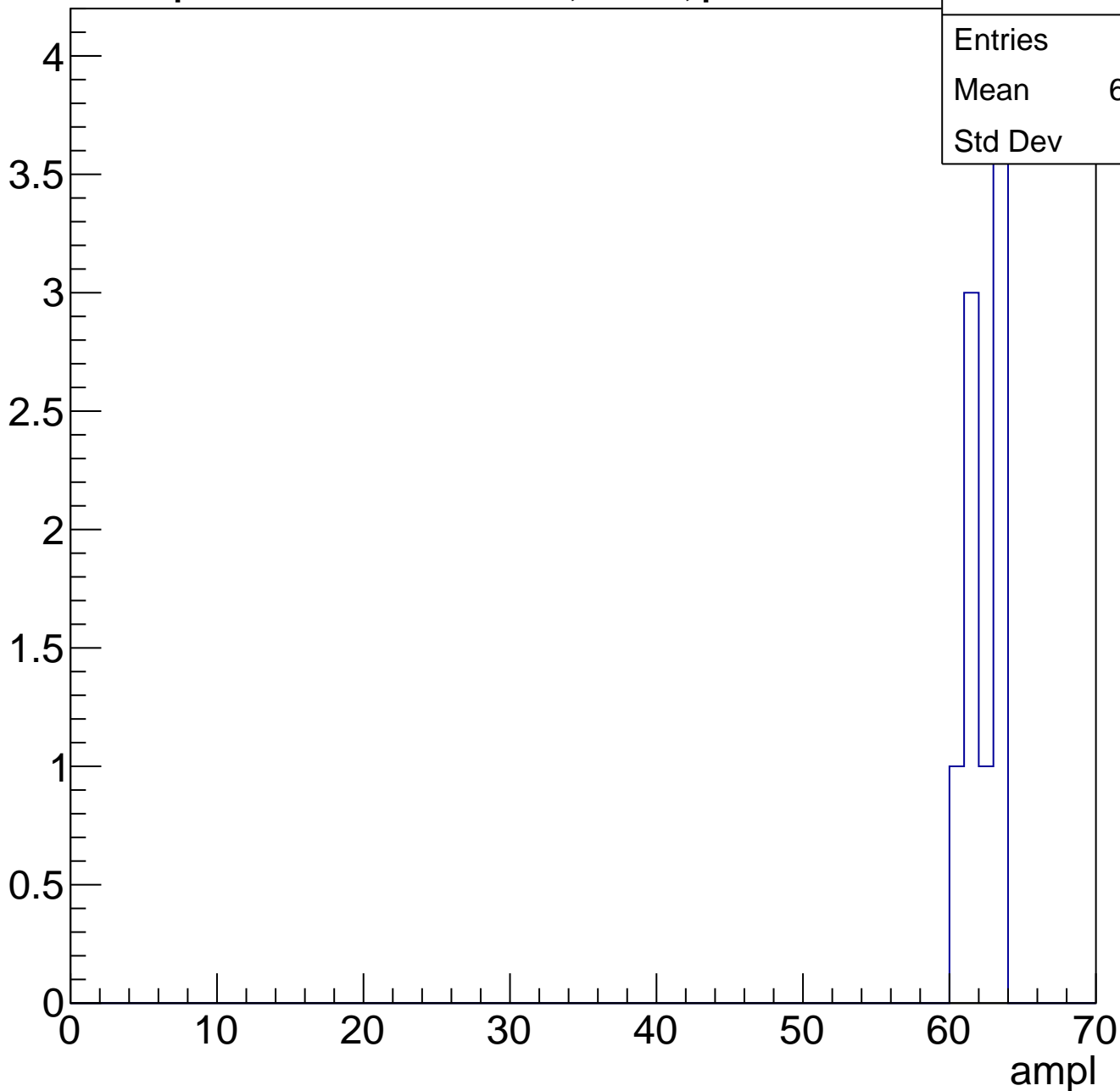
Entries	40
Mean	58.25
Std Dev	9.715



# B1L003S, U23-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch122, adc0

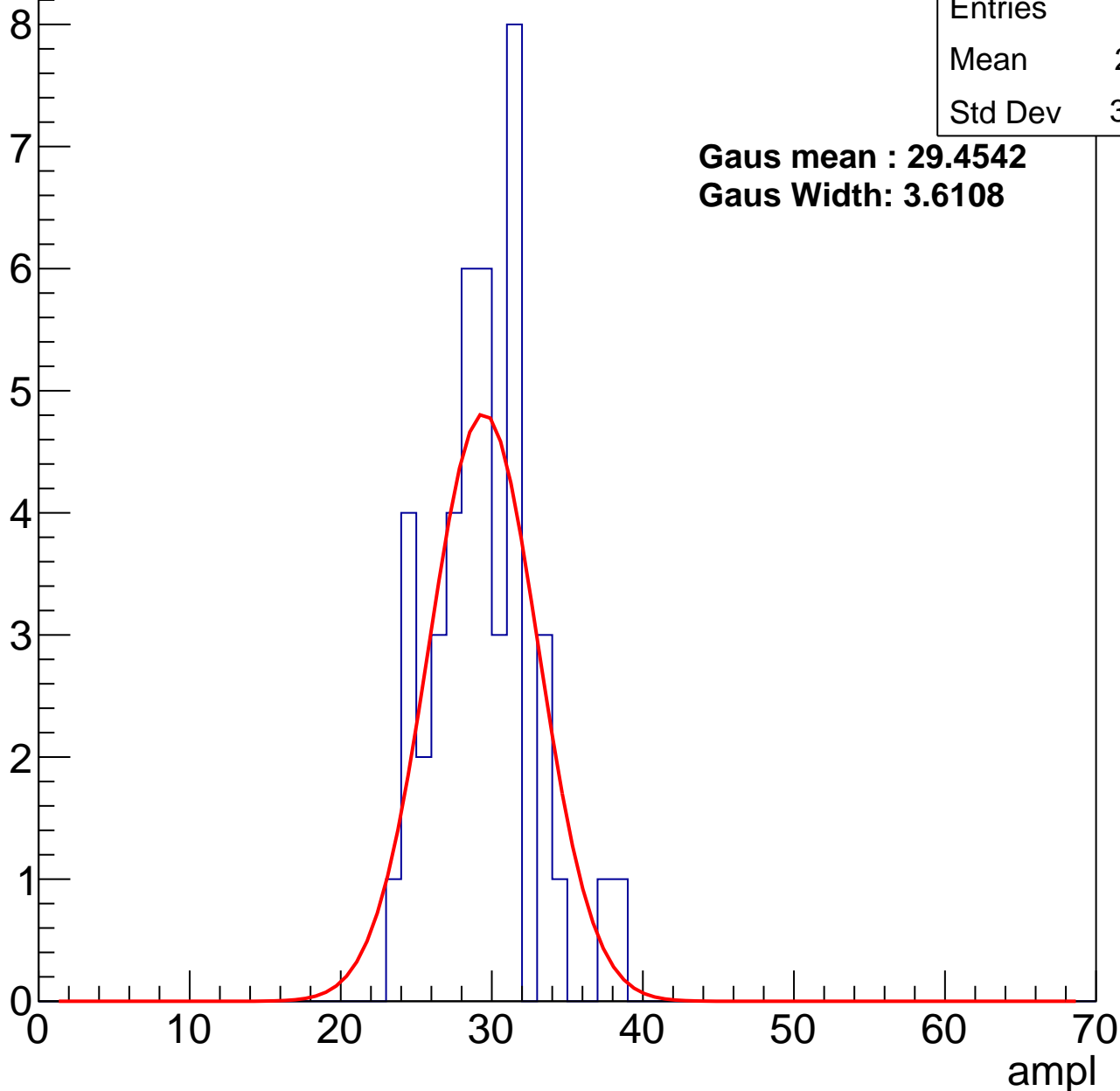
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	28.91
Std Dev	3.305

**Gaus mean : 29.4542**

**Gaus Width: 3.6108**



# B1L003S, U23-ch122, adc1

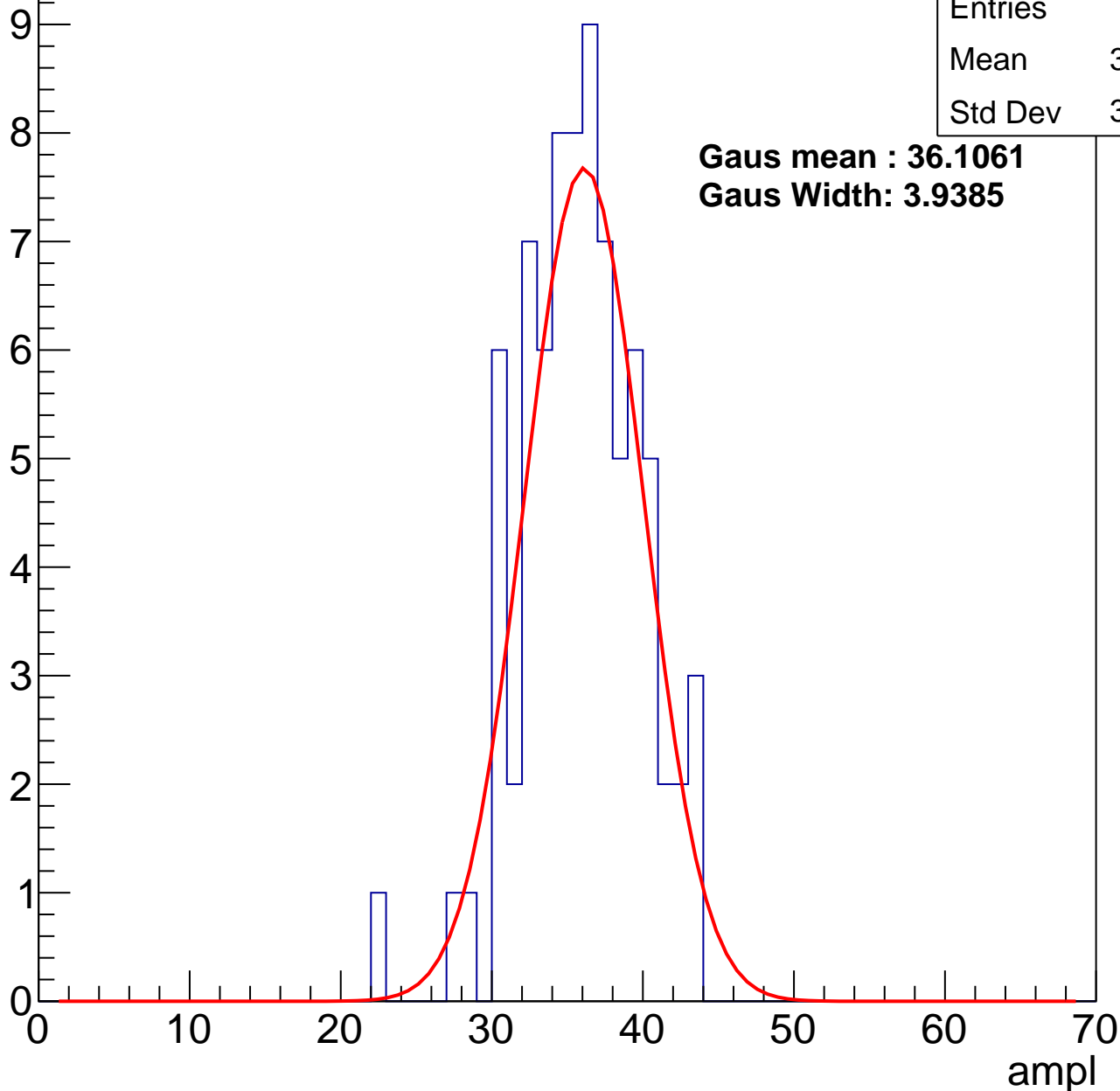
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	35.38
Std Dev	3.918

**Gaus mean : 36.1061**

**Gaus Width: 3.9385**



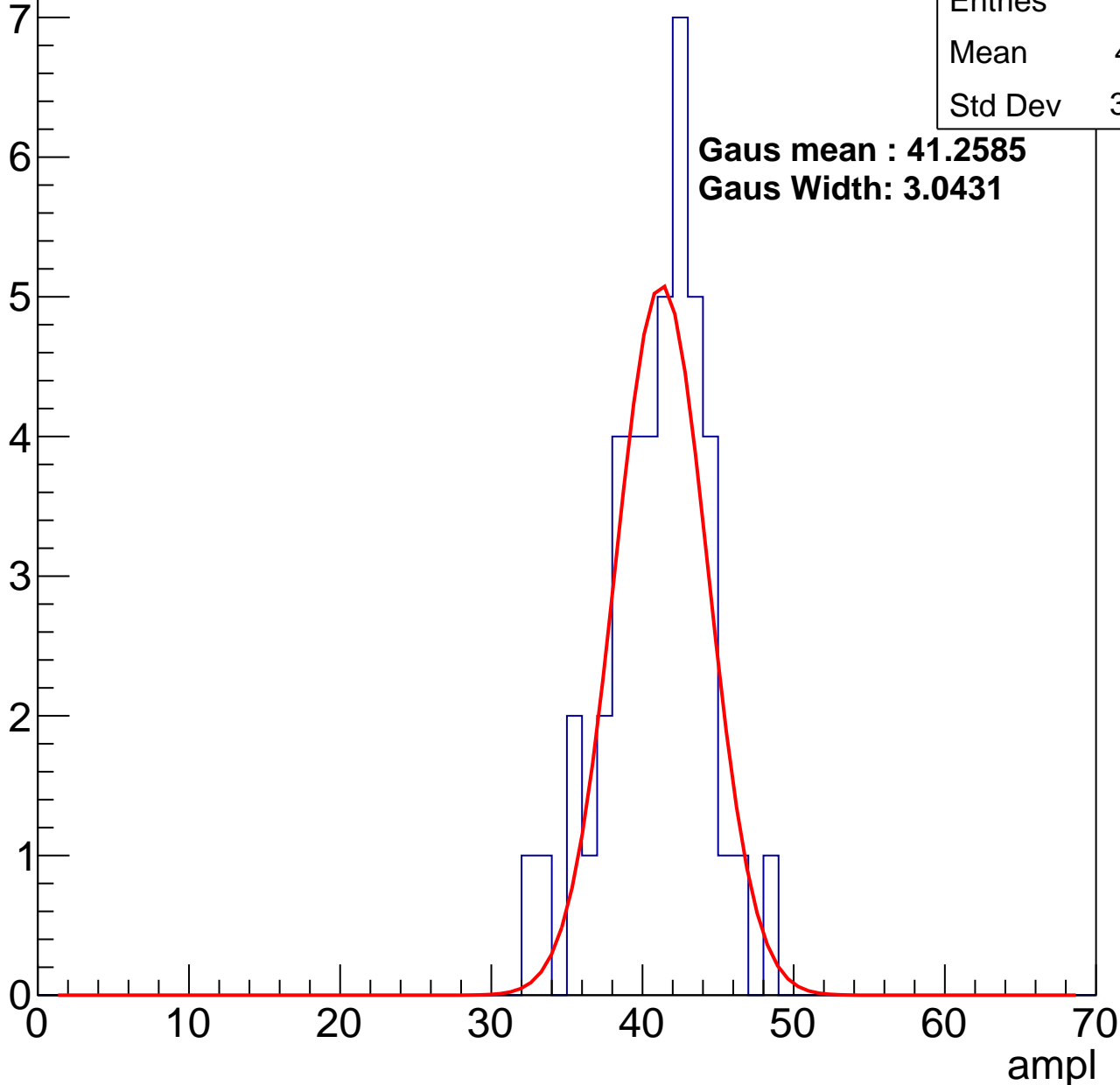
# B1L003S, U23-ch122, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	40.51
Std Dev	3.316

**Gaus mean : 41.2585**  
**Gaus Width: 3.0431**

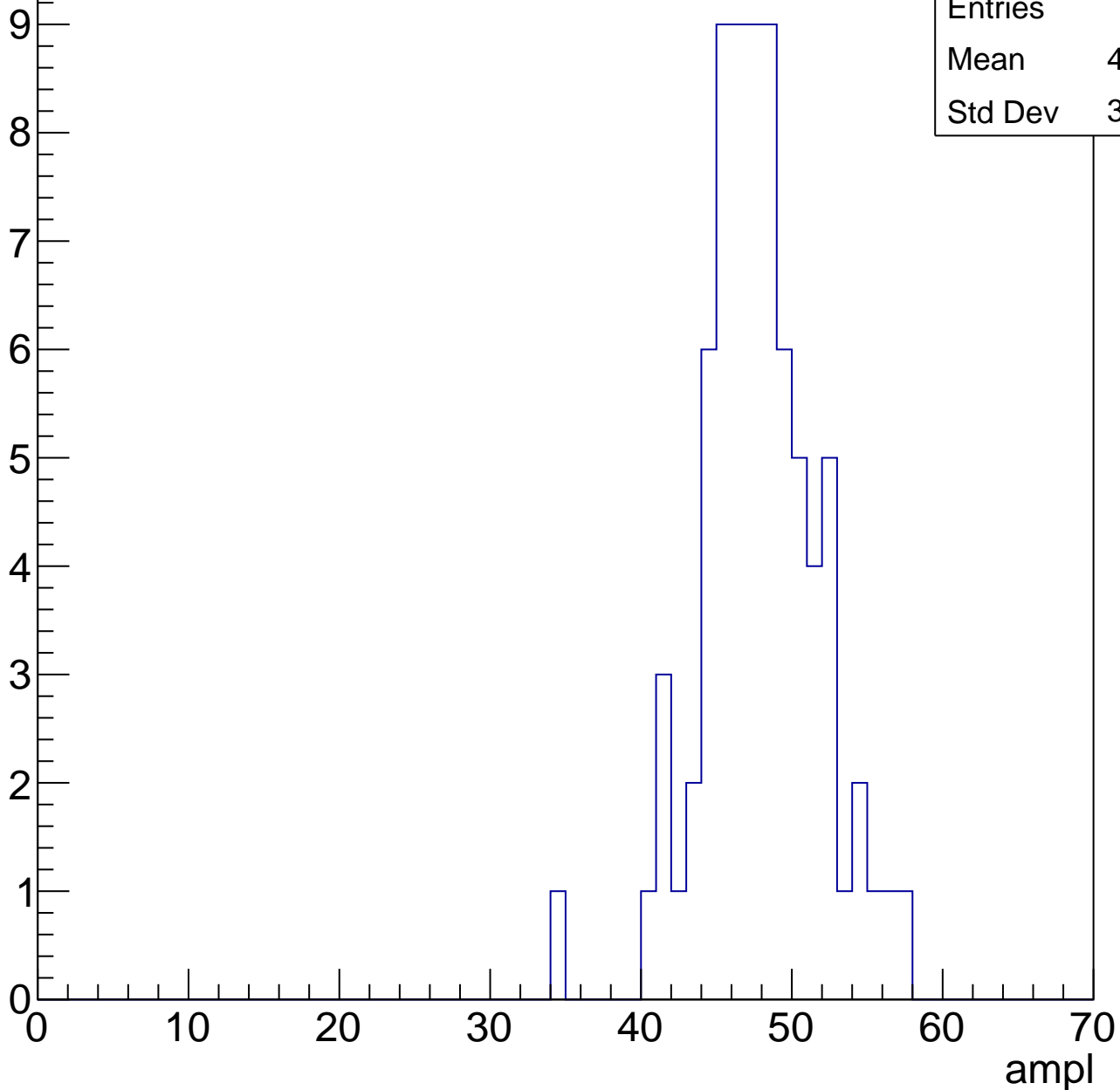


# B1L003S, U23-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

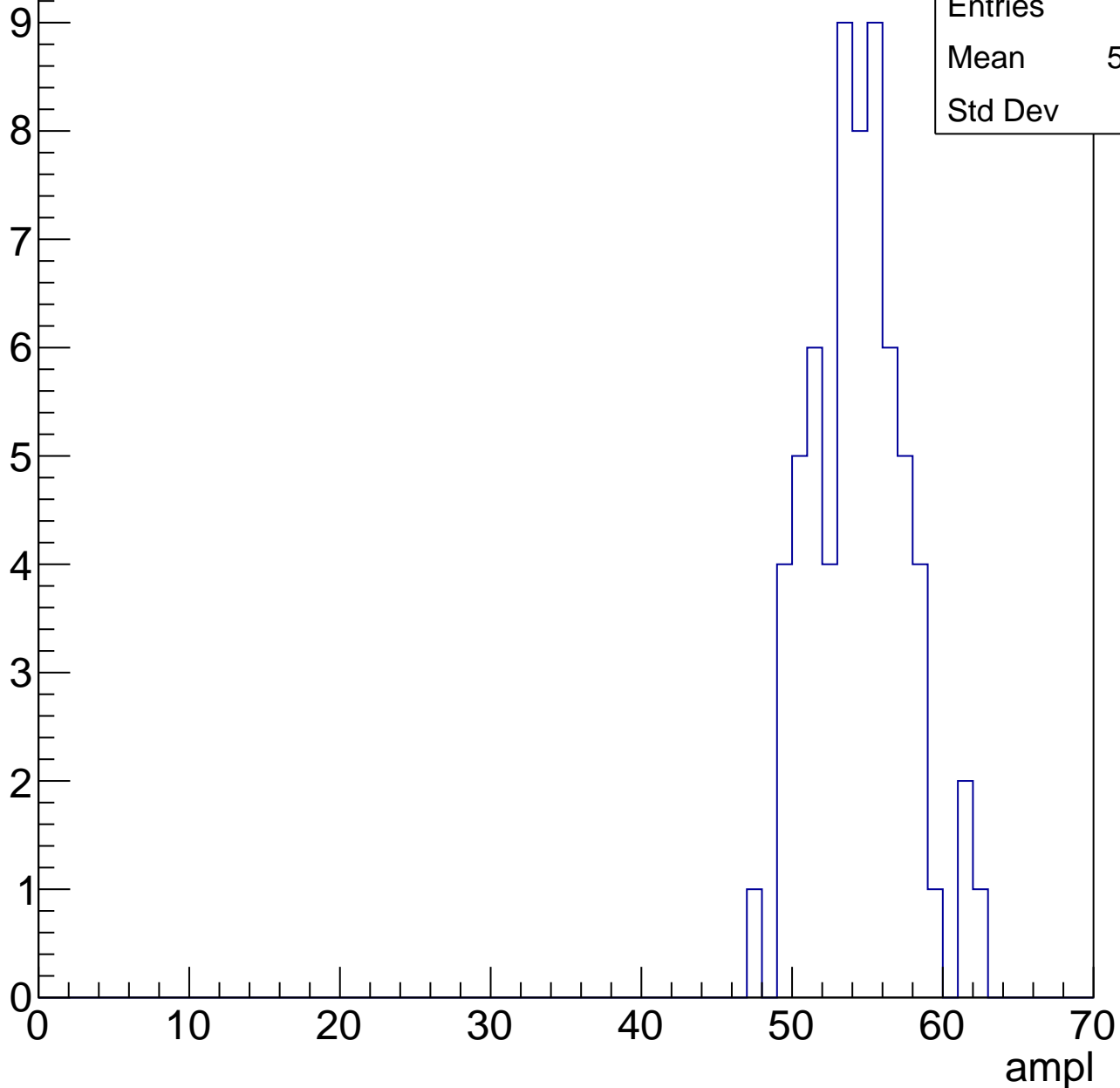
Entries	76
Mean	47.37
Std Dev	3.835



# B1L003S, U23-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

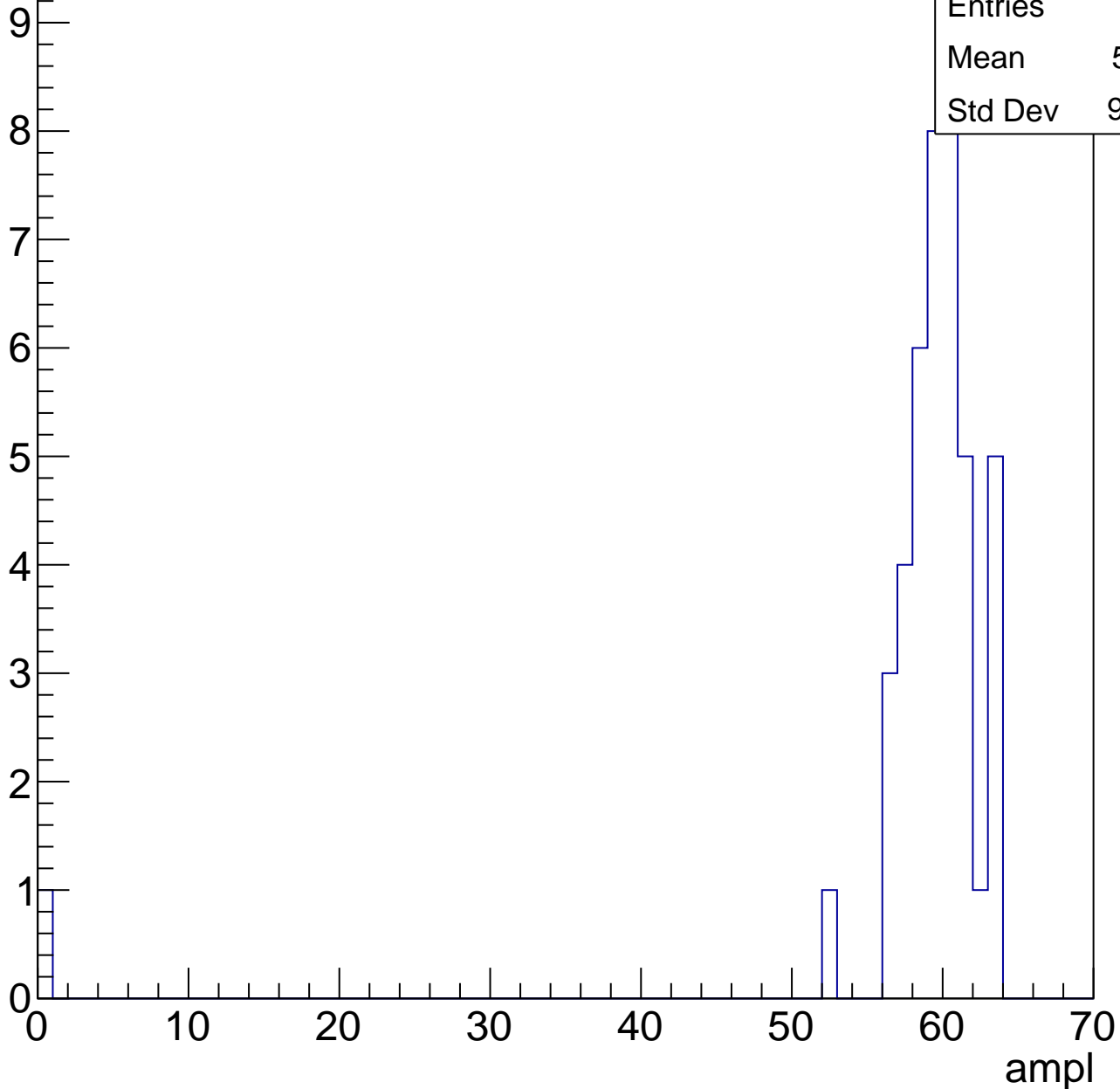


# B1L003S, U23-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	57.91
Std Dev	9.208

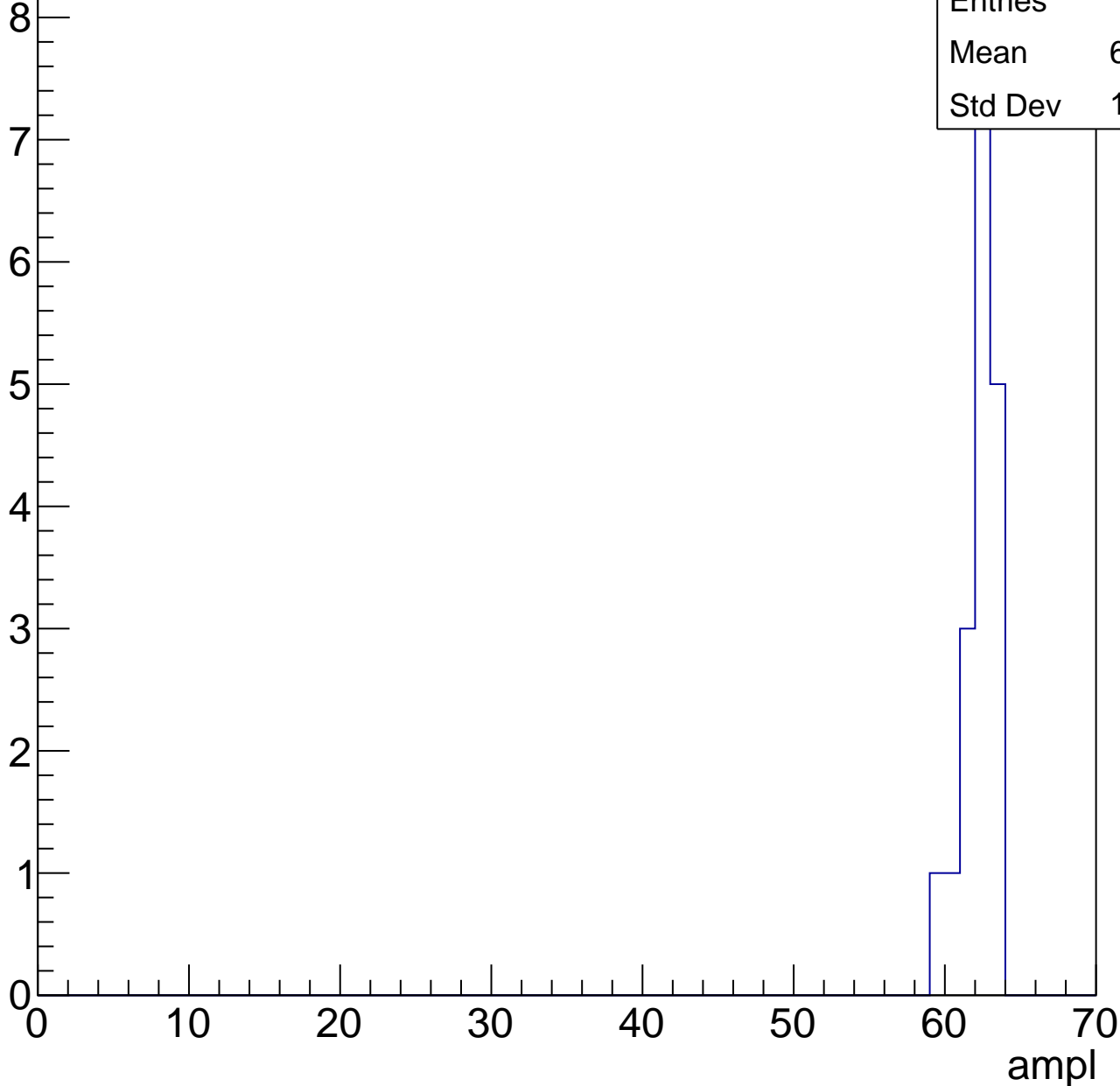


# B1L003S, U23-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	18
Mean	61.83
Std Dev	1.067





# B1L003S, U23-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch123, adc0

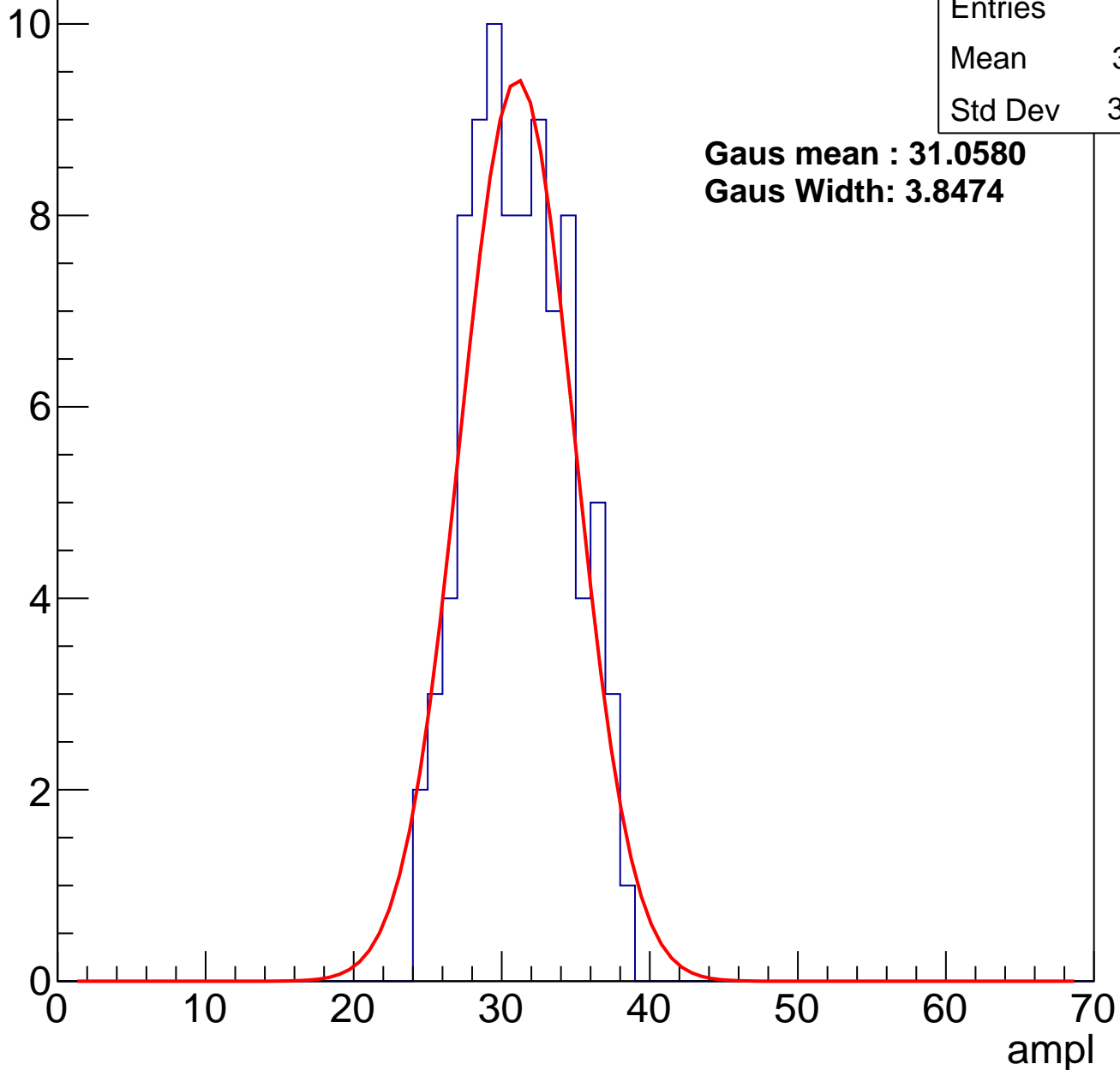
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	89
Mean	30.71
Std Dev	3.369

**Gaus mean : 31.0580**

**Gaus Width: 3.8474**

Entry



# B1L003S, U23-ch123, adc1

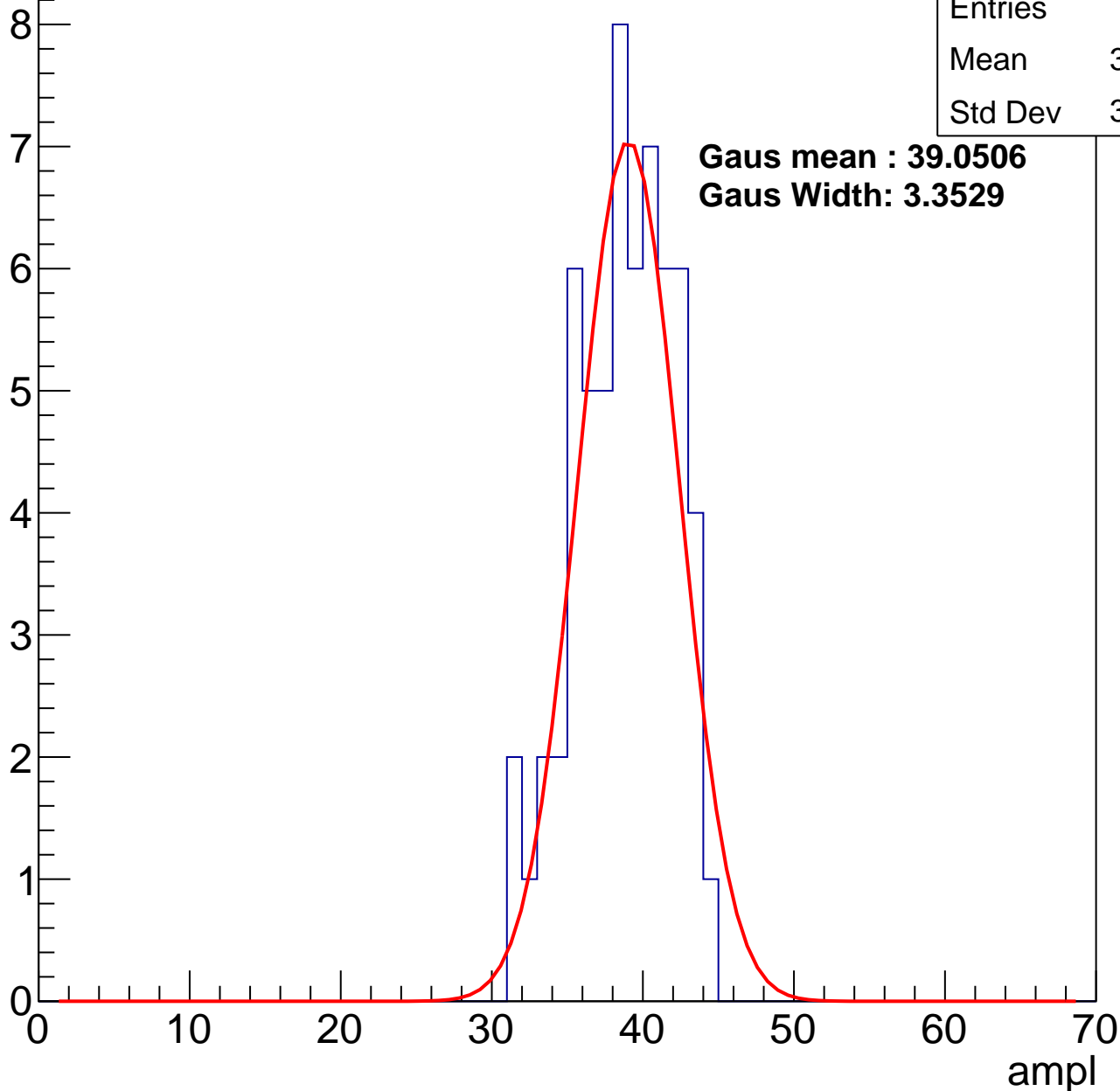
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	38.28
Std Dev	3.163

**Gaus mean : 39.0506**

**Gaus Width: 3.3529**



# B1L003S, U23-ch123, adc2

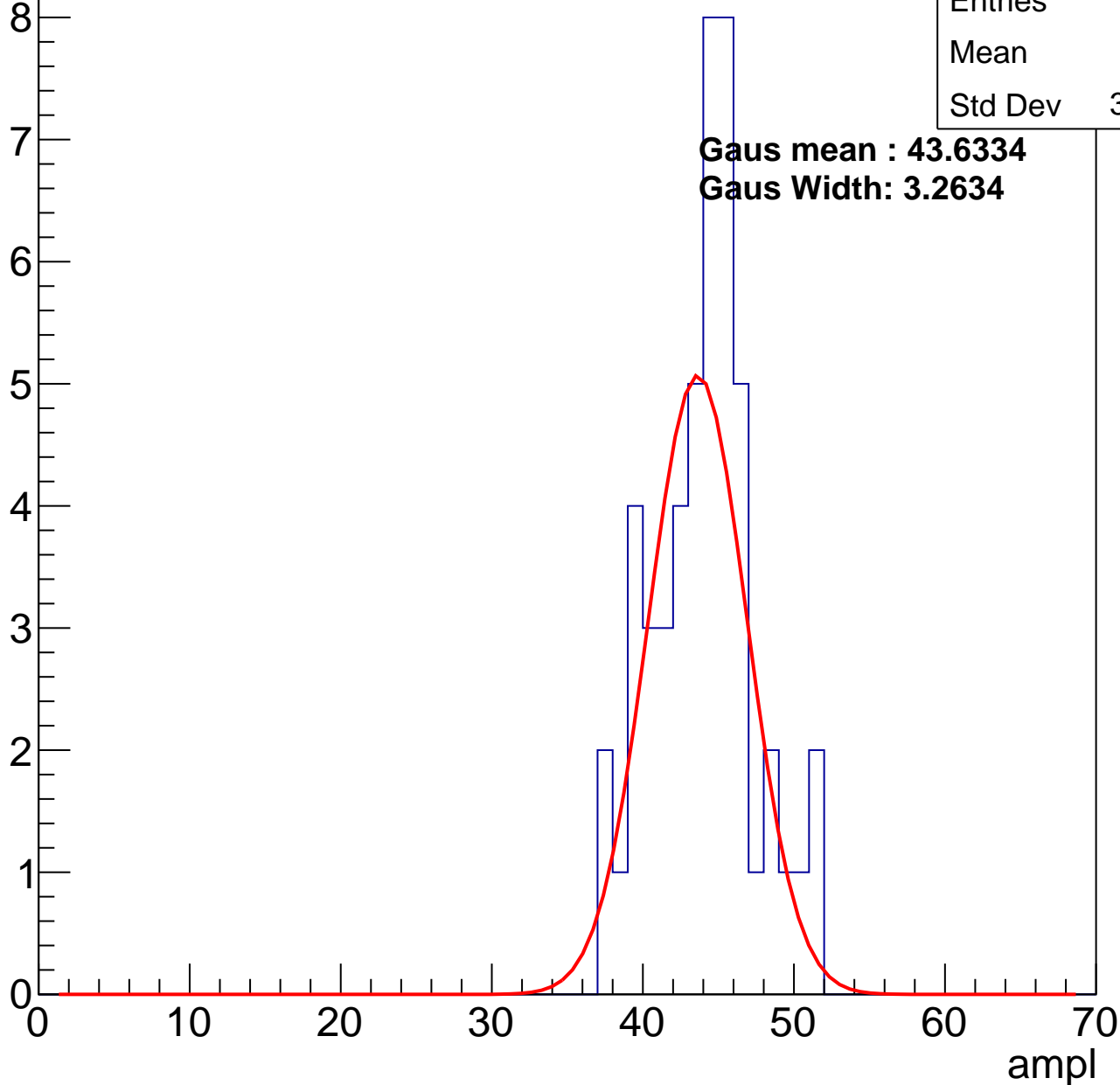
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.6
Std Dev	3.317

**Gaus mean : 43.6334**

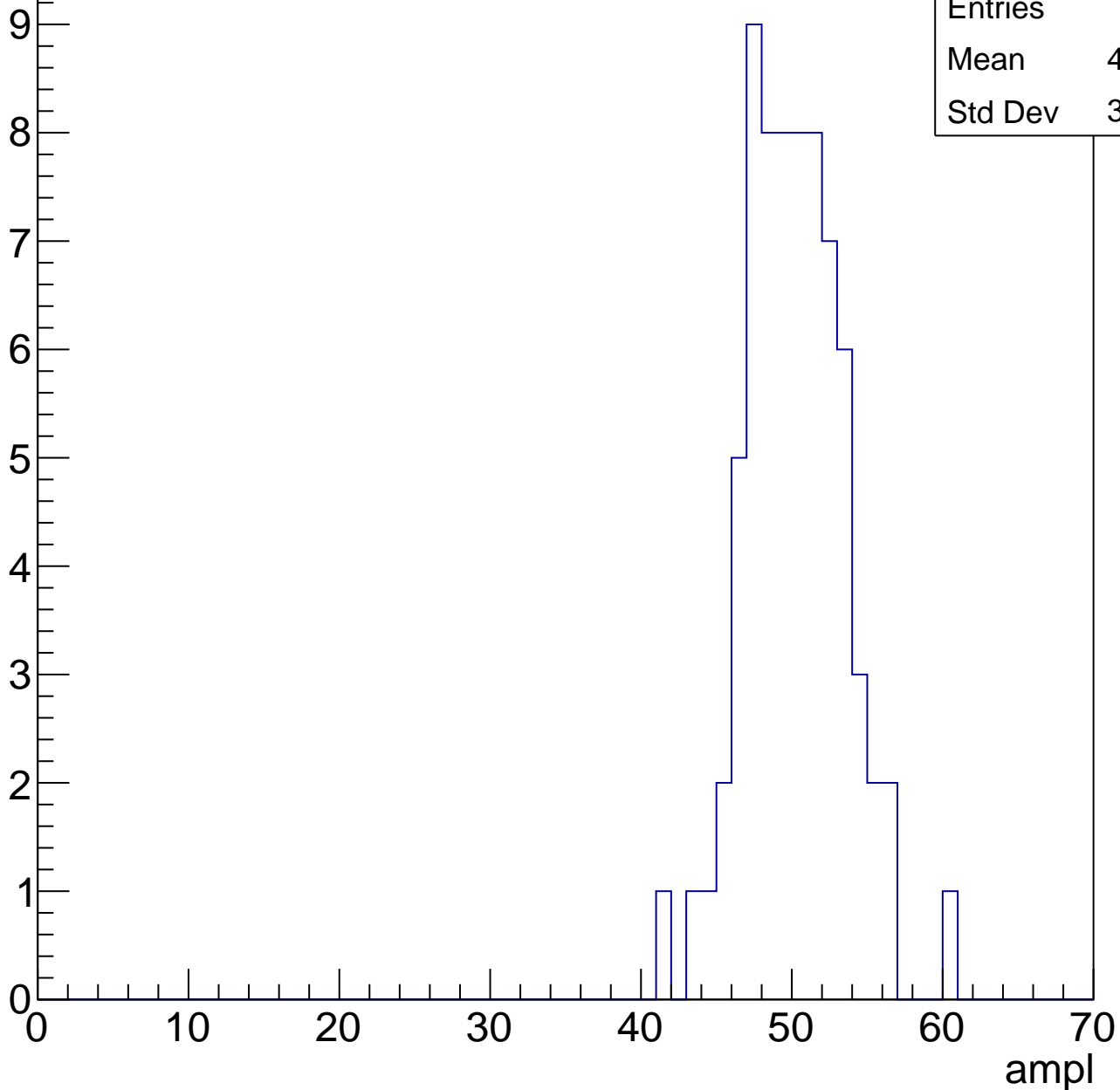
**Gaus Width: 3.2634**



# B1L003S, U23-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



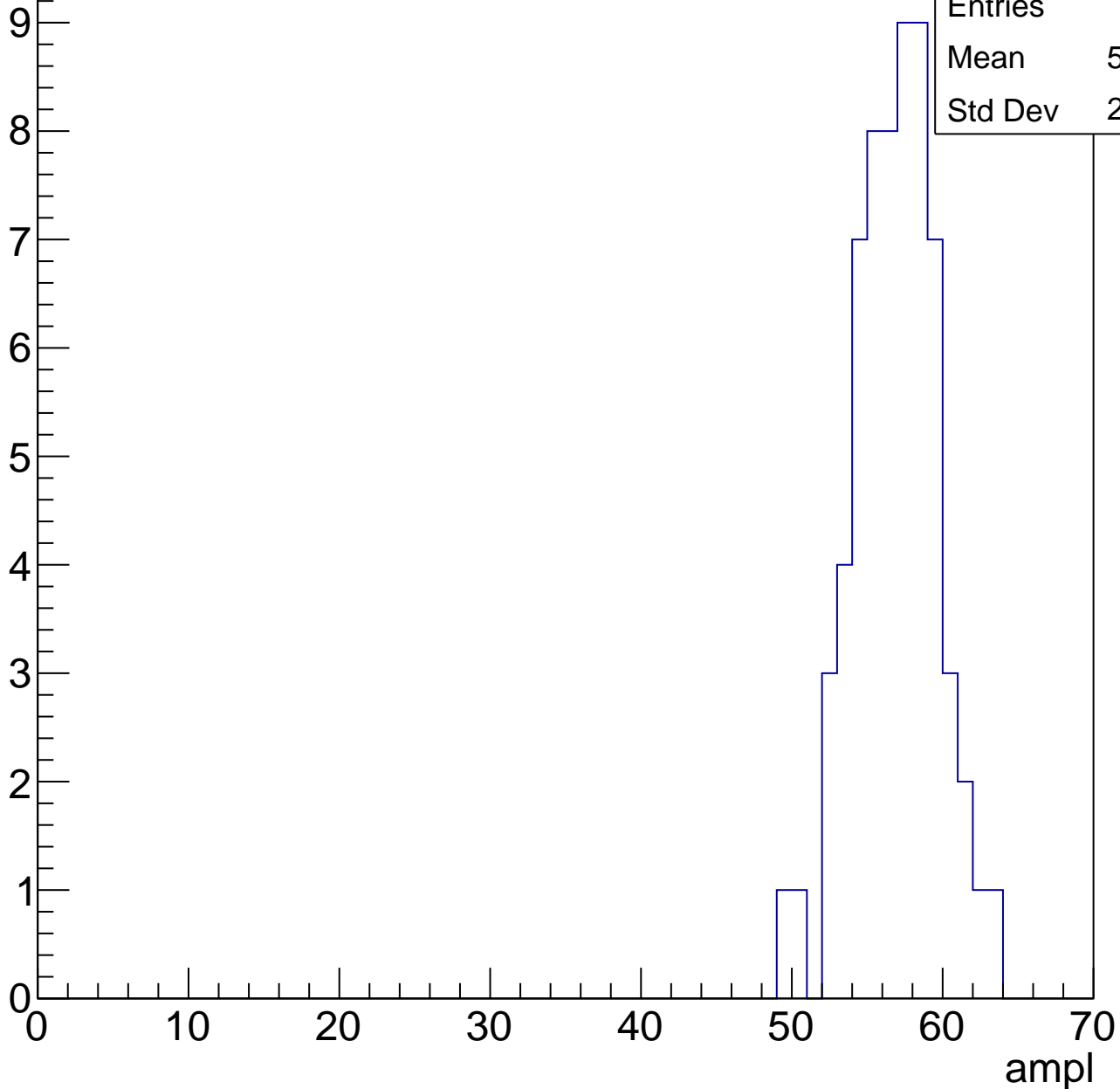
Entries	72
Mean	49.74
Std Dev	3.274

# B1L003S, U23-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	56.38
Std Dev	2.759

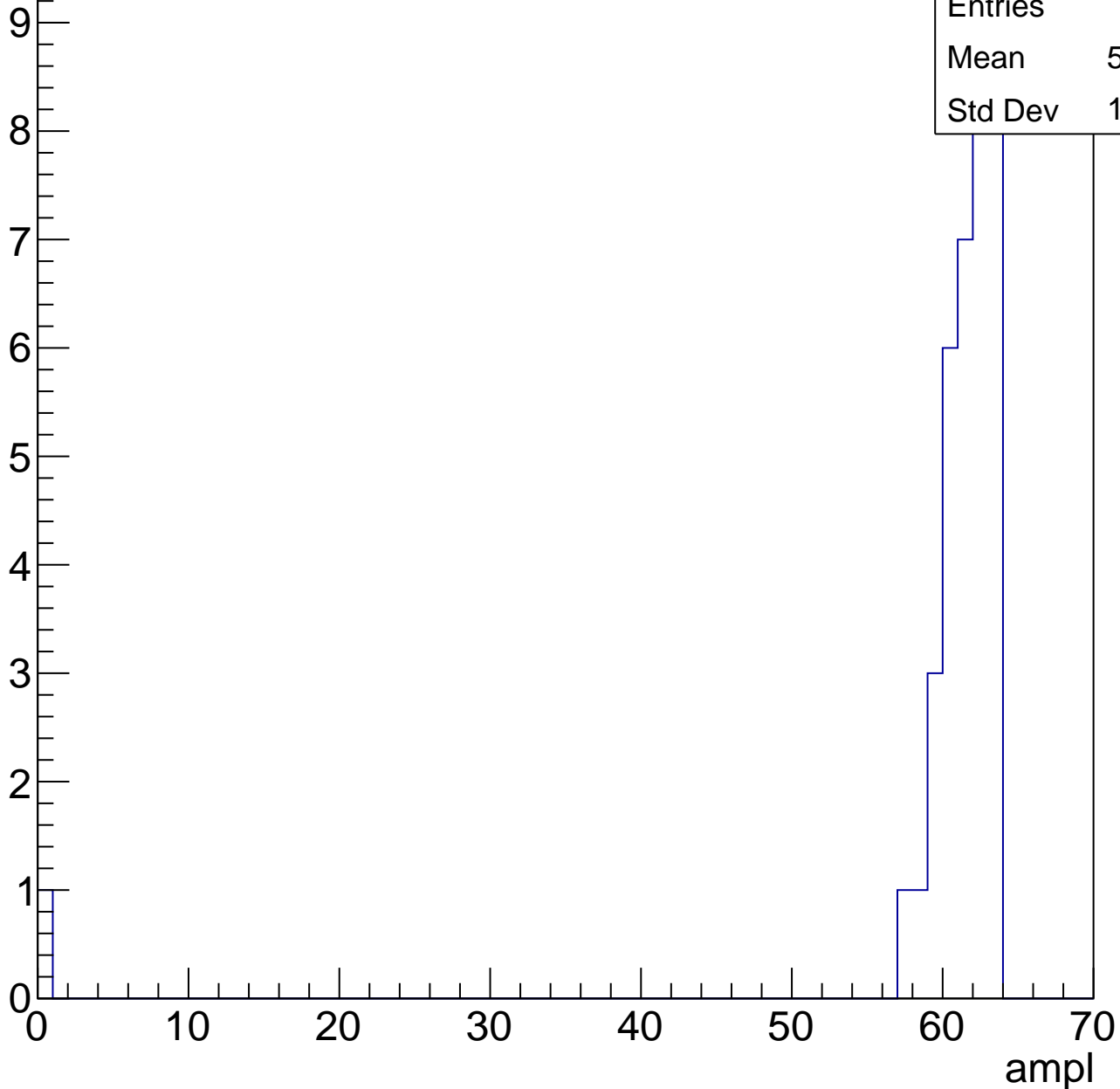


# B1L003S, U23-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.47
Std Dev	10.17



# B1L003S, U23-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch124, adc0

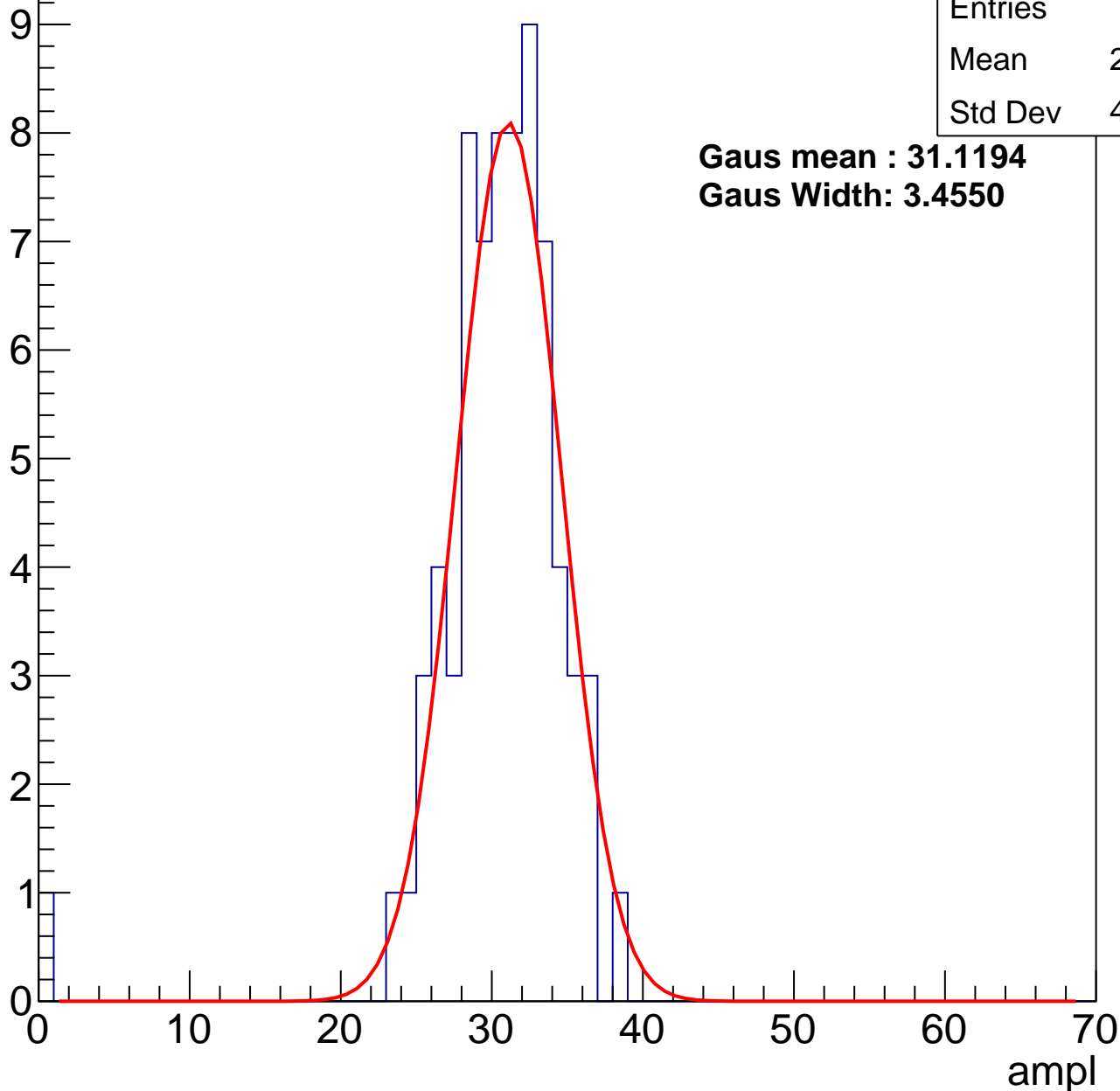
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	29.97
Std Dev	4.759

**Gaus mean : 31.1194**

**Gaus Width: 3.4550**



# B1L003S, U23-ch124, adc1

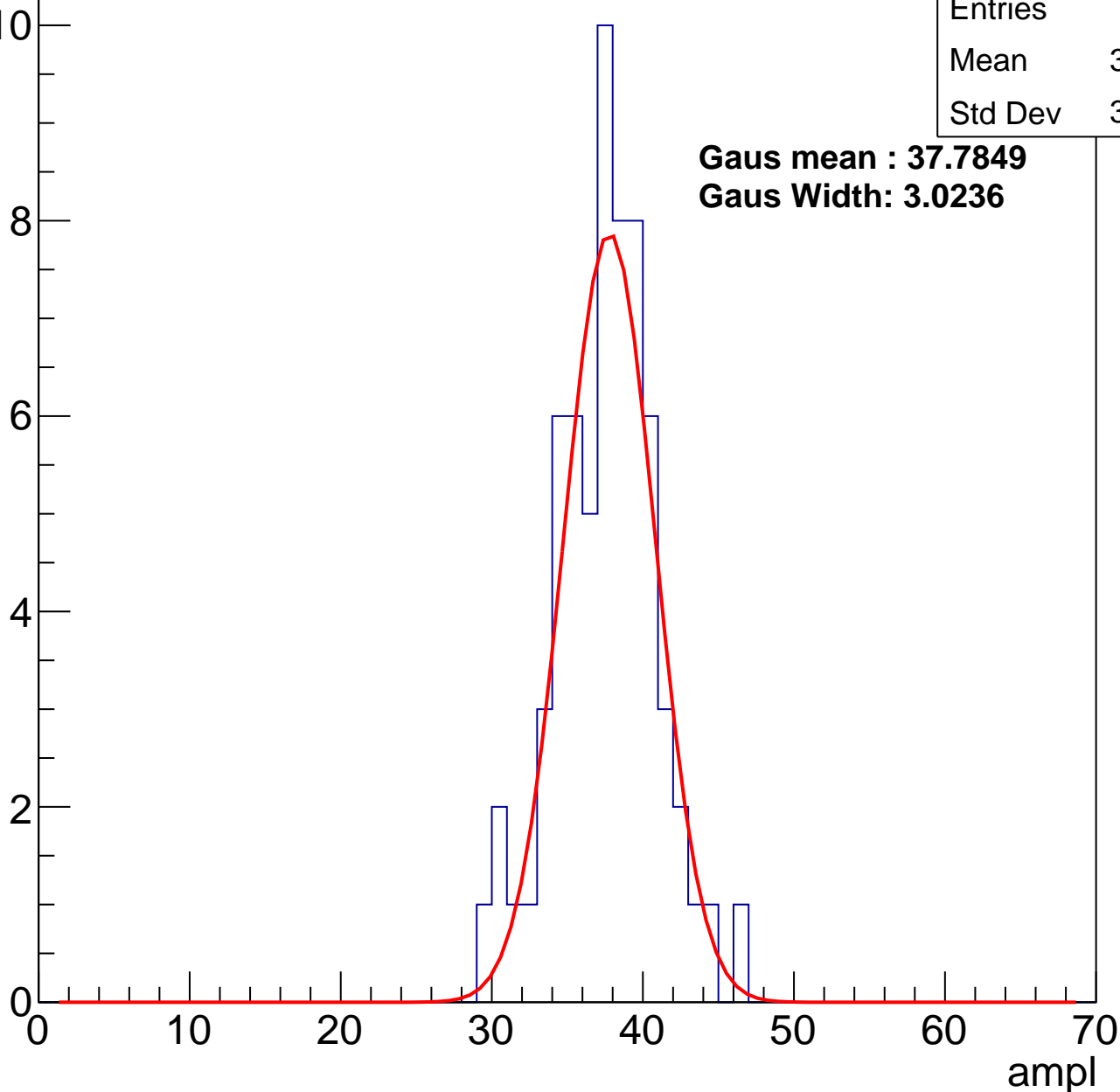
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	37.09
Std Dev	3.308

**Gaus mean : 37.7849**

**Gaus Width: 3.0236**



# B1L003S, U23-ch124, adc2

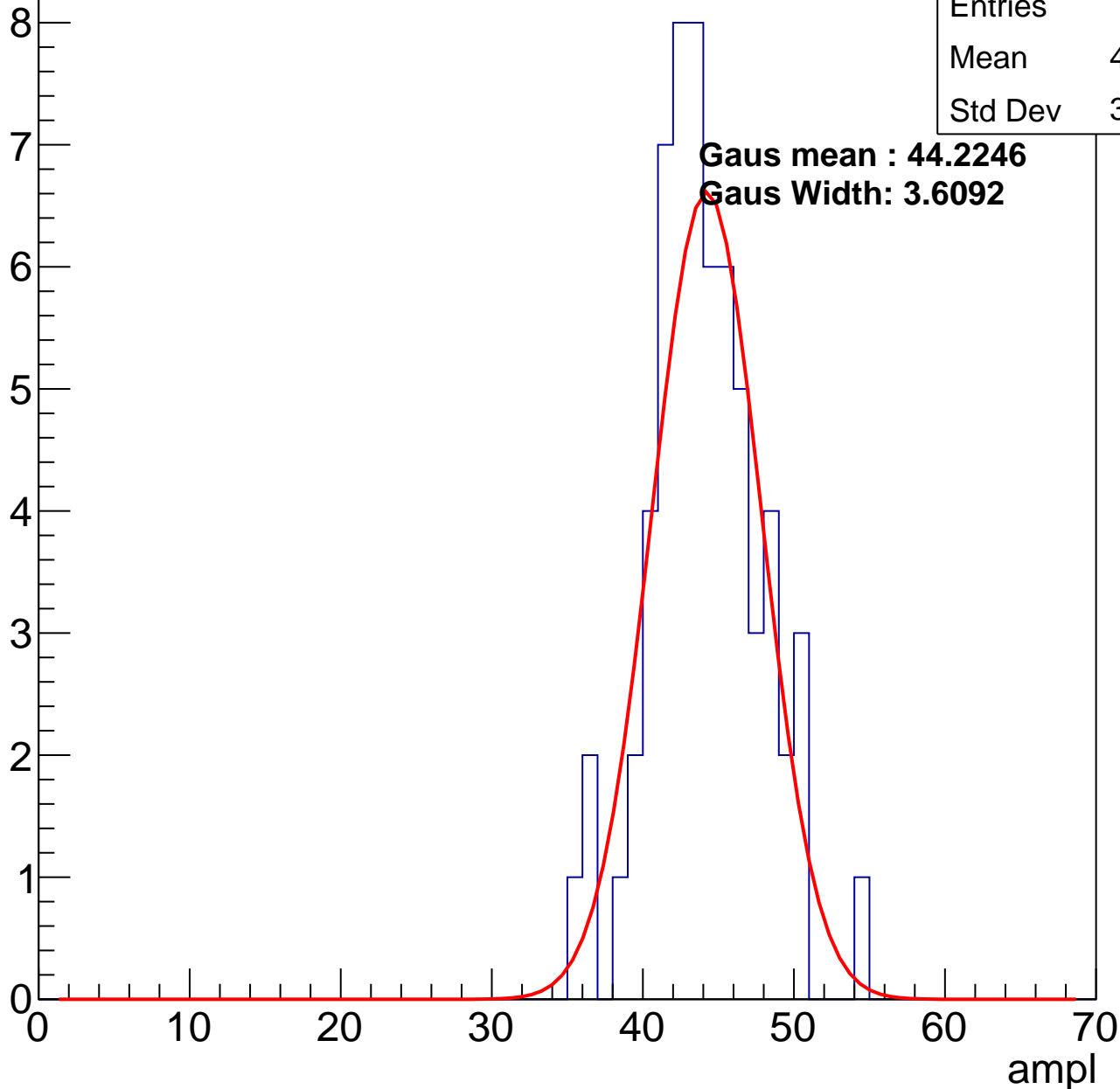
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	43.63
Std Dev	3.627

**Gaus mean : 44.2246**

**Gaus Width: 3.6092**

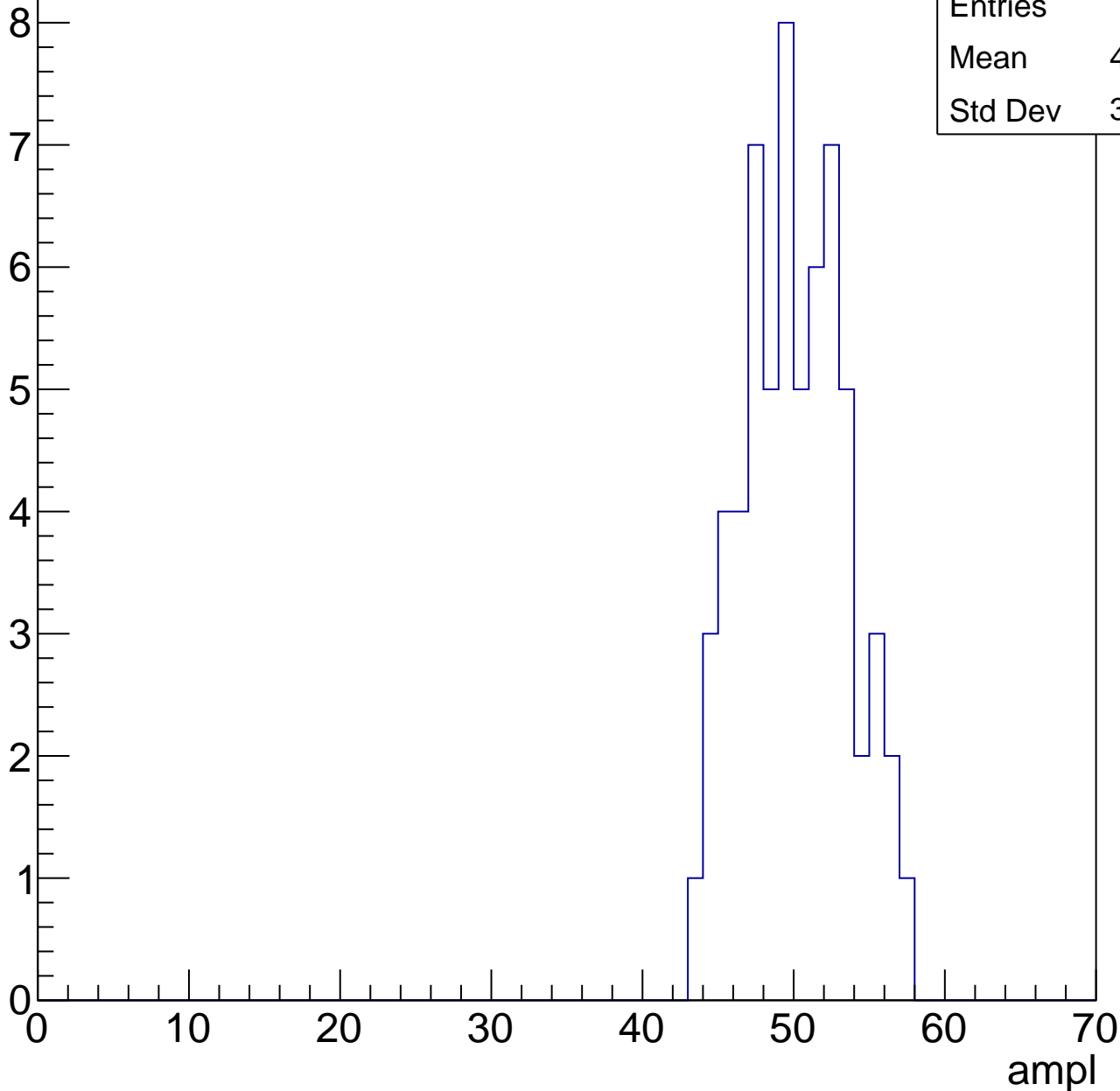


# B1L003S, U23-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	49.63
Std Dev	3.358

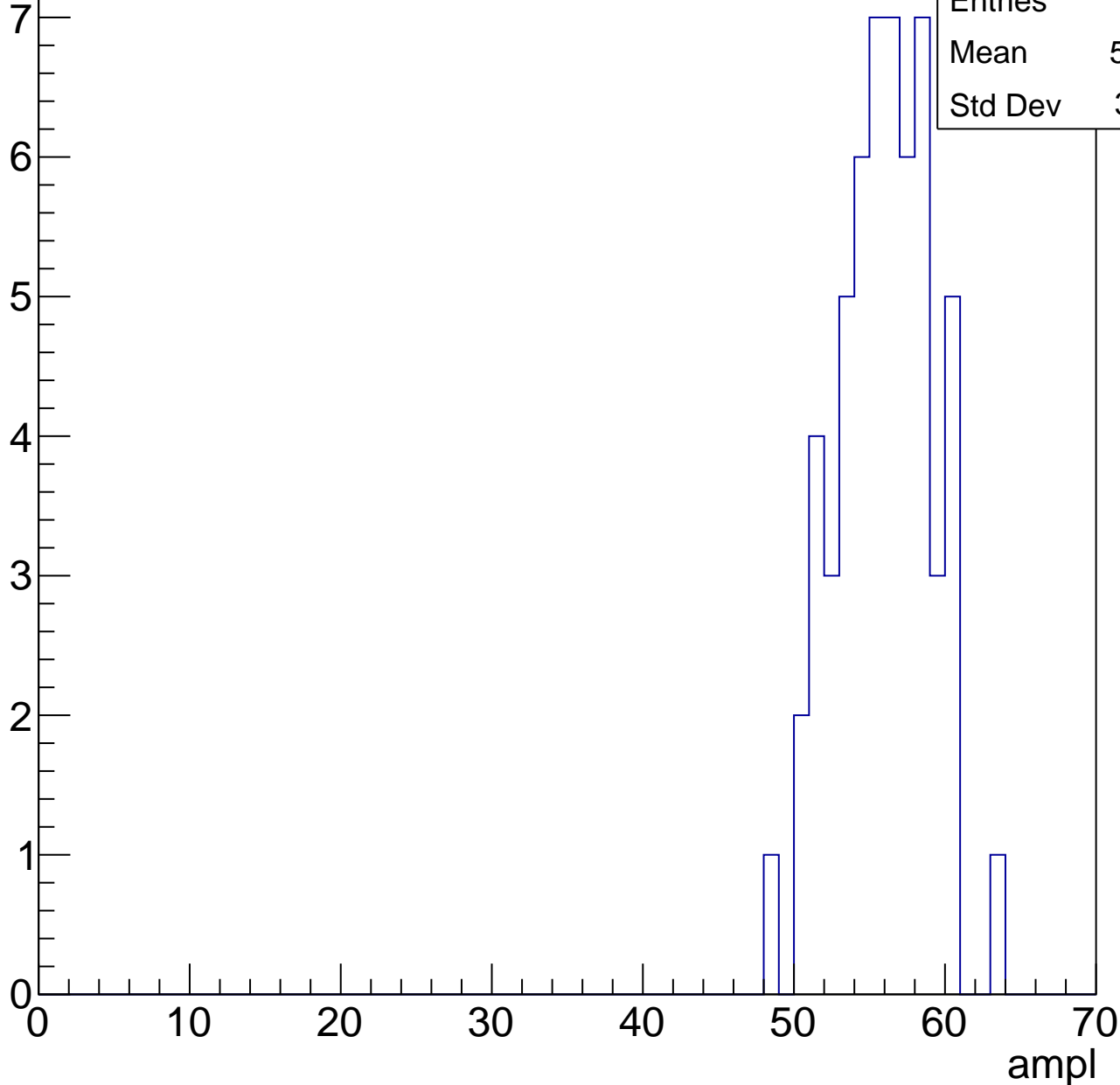


# B1L003S, U23-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	55.47
Std Dev	3.061

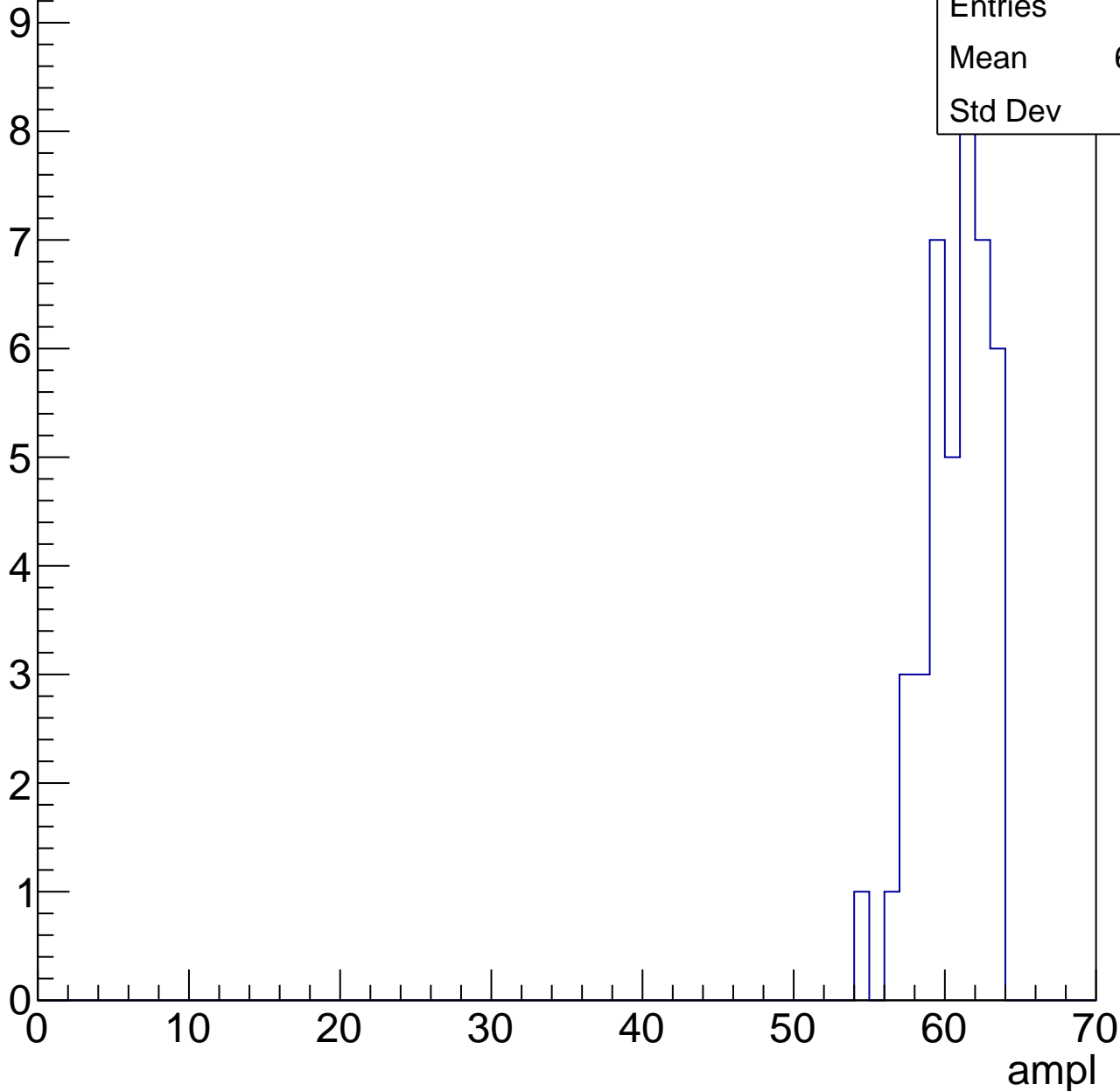


# B1L003S, U23-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

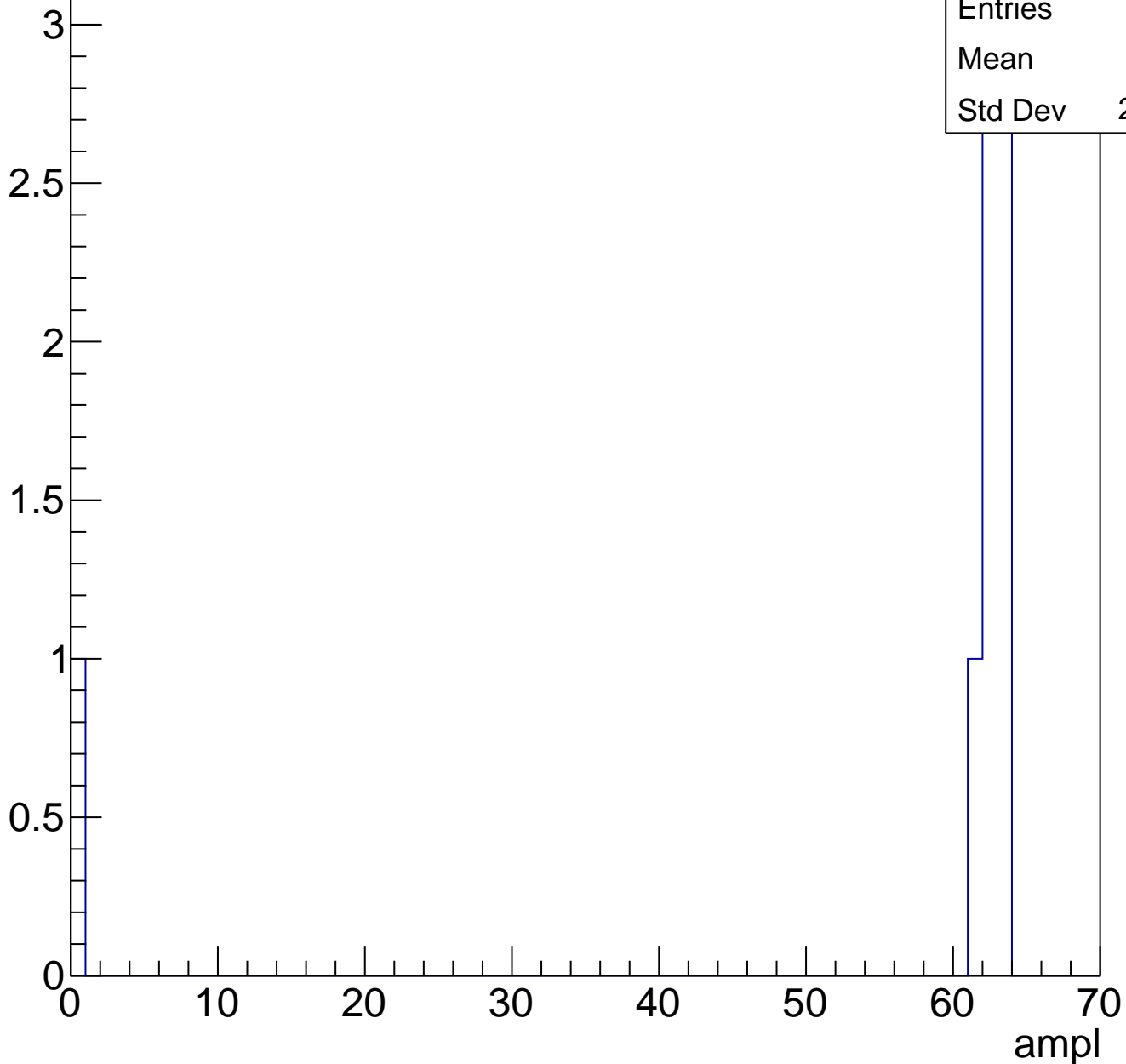
Entries	42
Mean	60.21
Std Dev	2.11



# B1L003S, U23-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U23-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch125, adc0

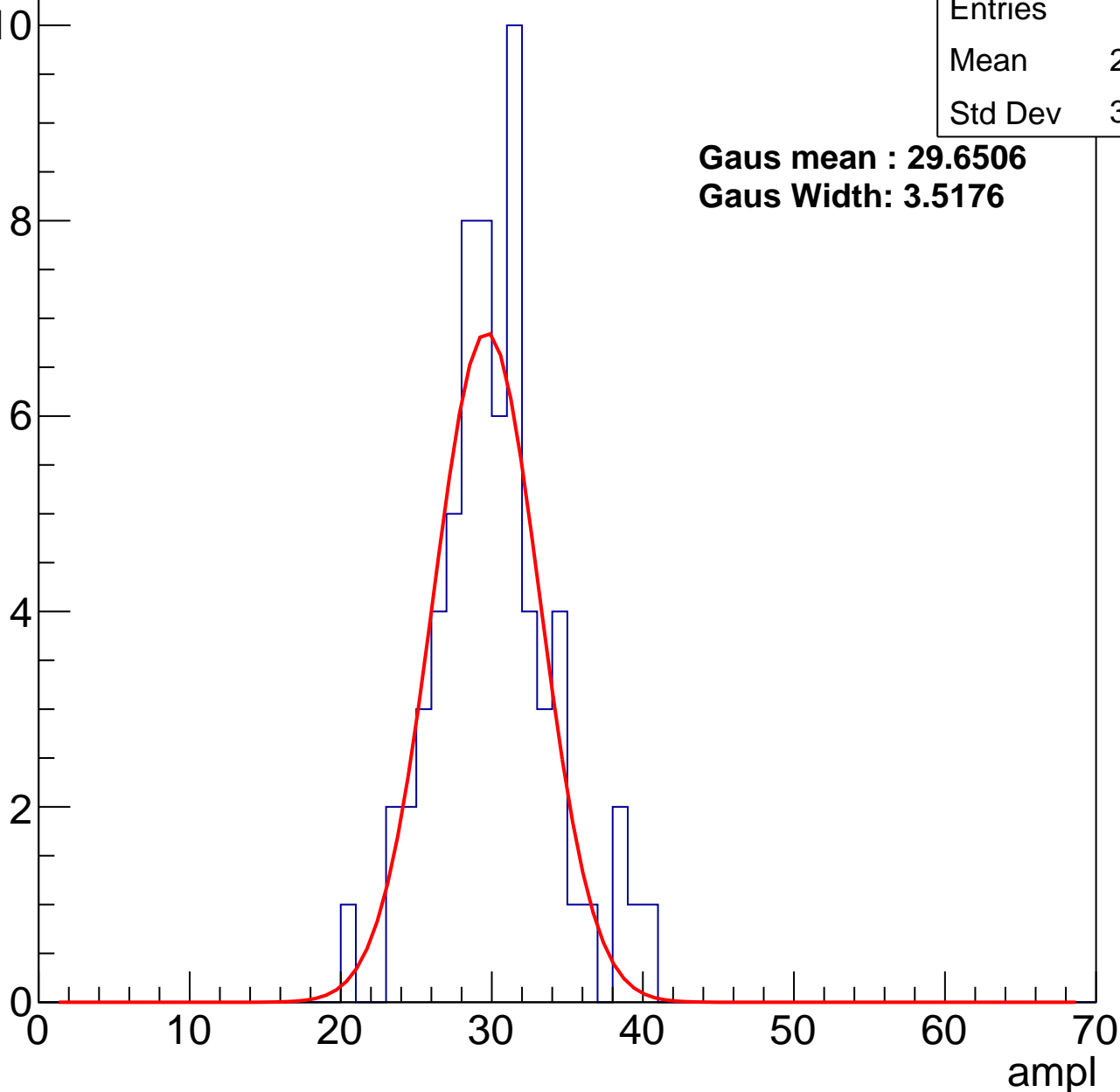
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.74
Std Dev	3.847

**Gaus mean : 29.6506**

**Gaus Width: 3.5176**



# B1L003S, U23-ch125, adc1

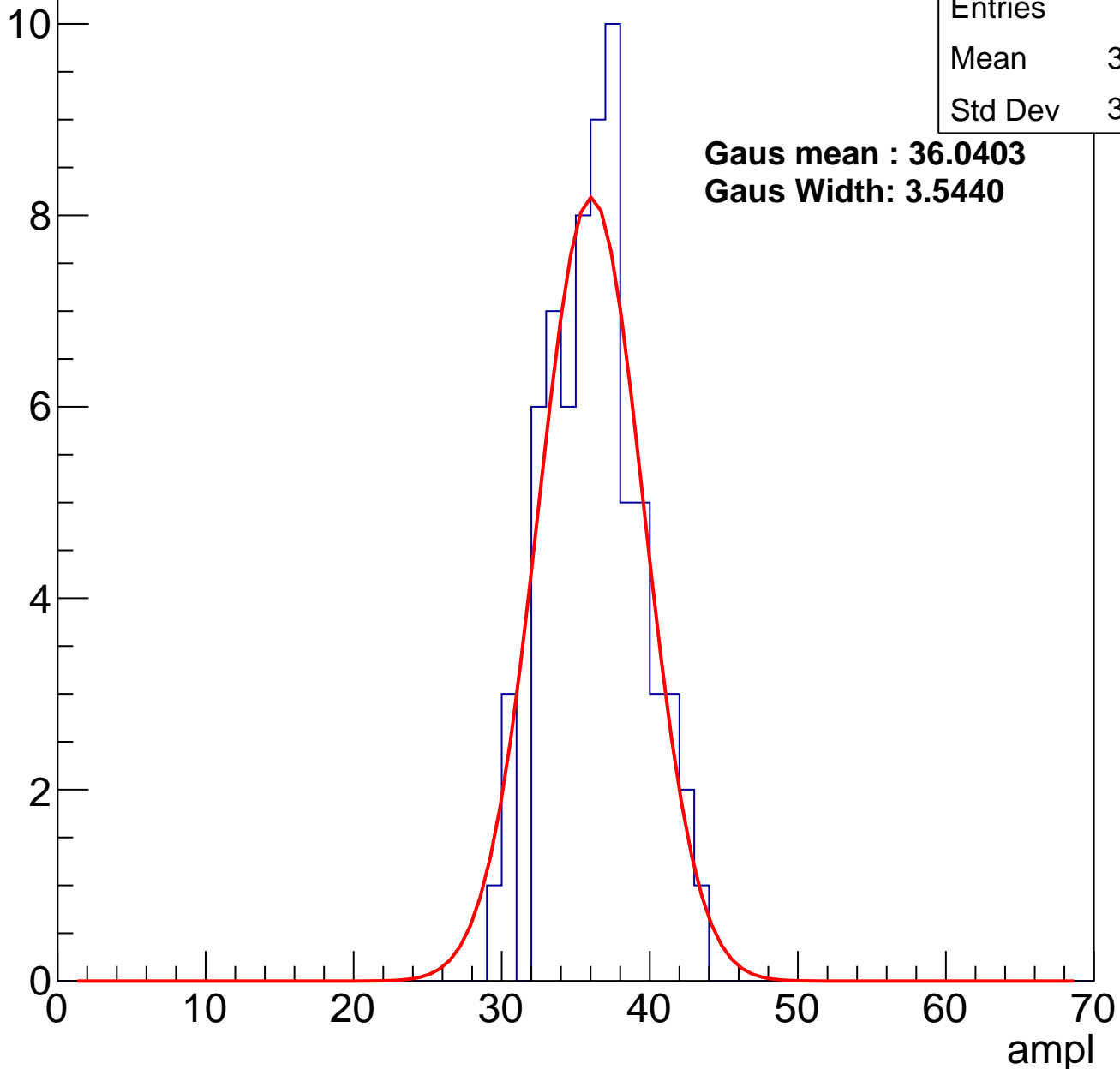
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	35.87
Std Dev	3.106

**Gaus mean : 36.0403**

**Gaus Width: 3.5440**

Entry



# B1L003S, U23-ch125, adc2

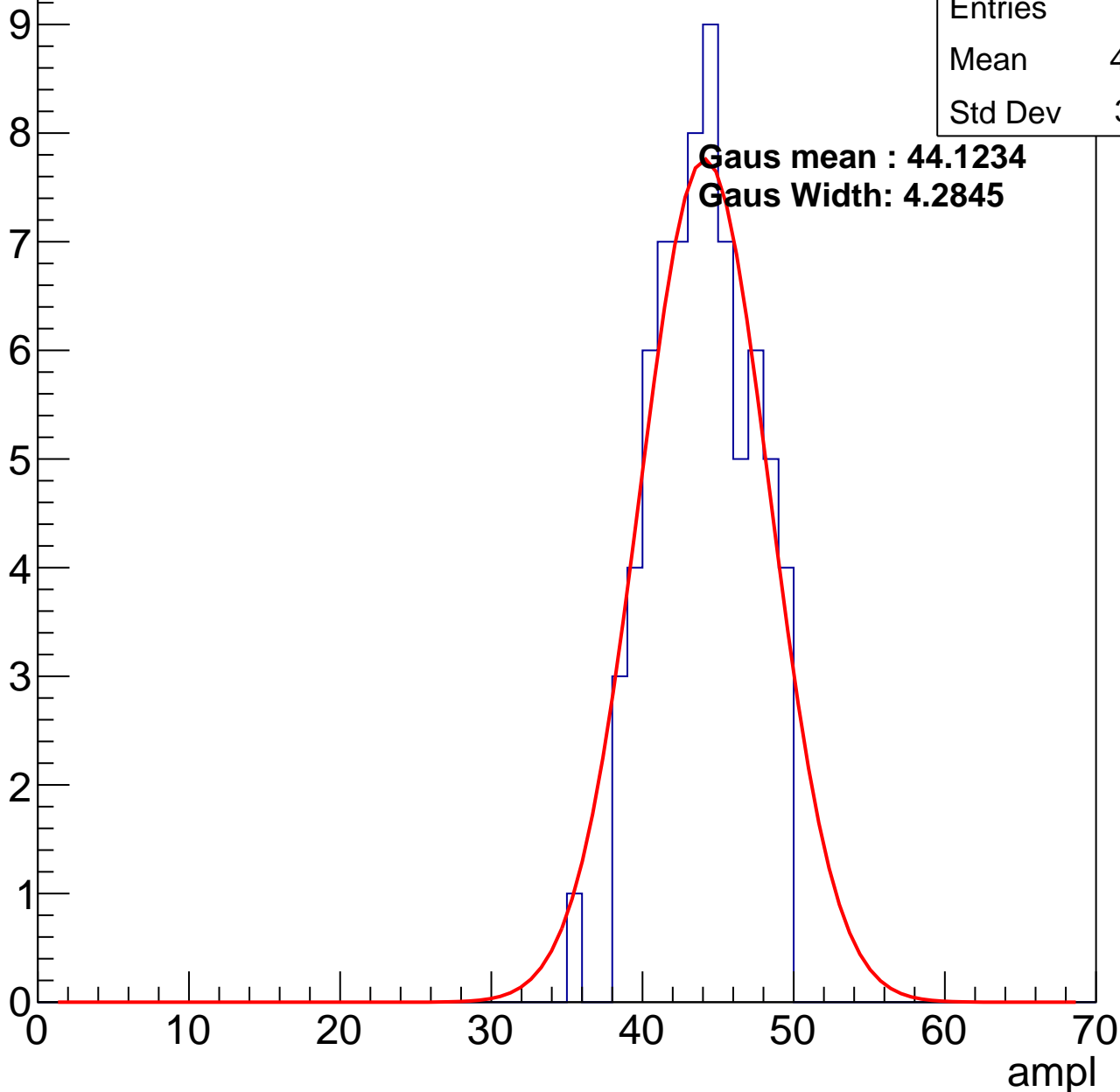
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	43.46
Std Dev	3.171

**Gaus mean : 44.1234**

**Gaus Width: 4.2845**

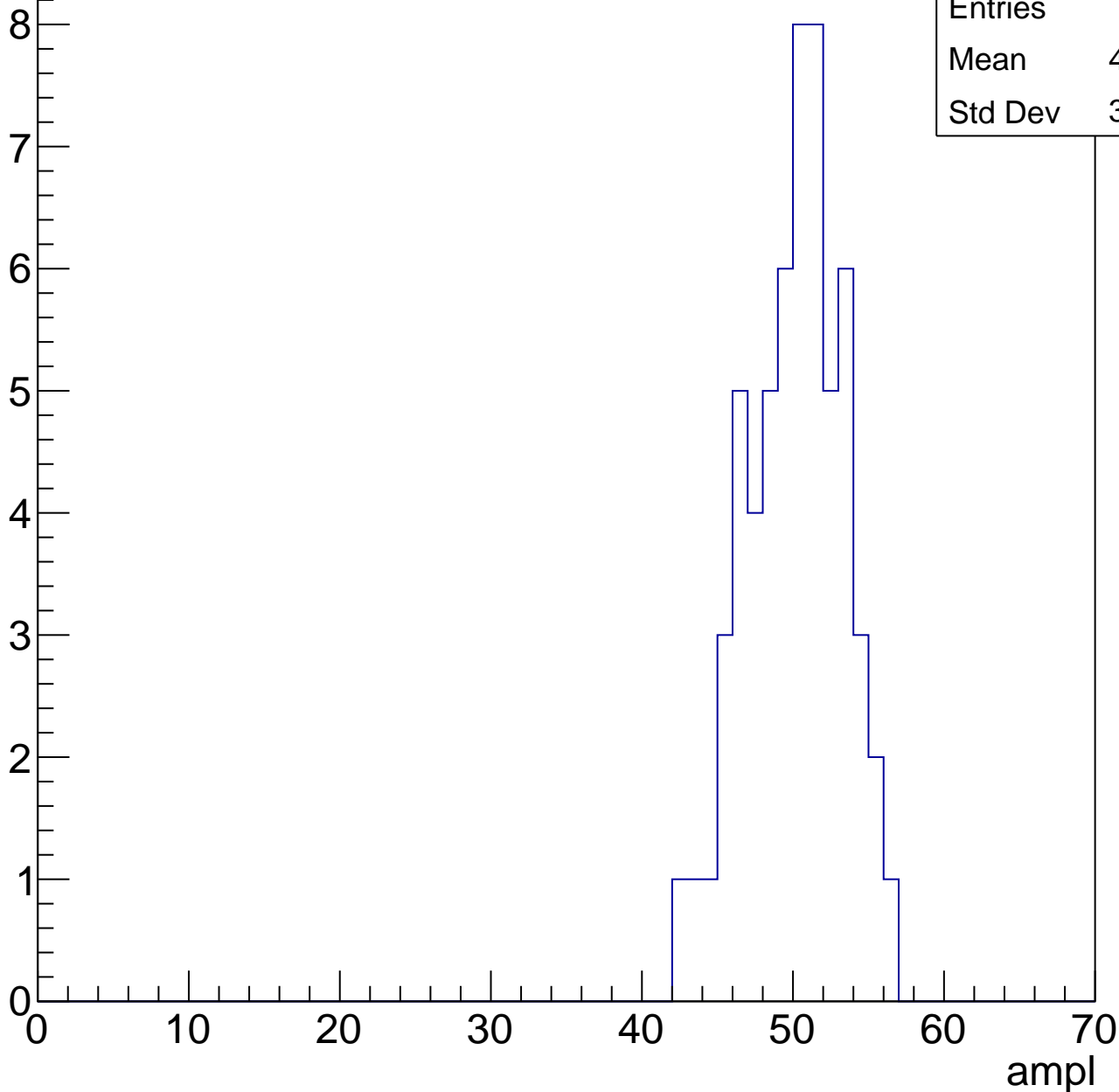


# B1L003S, U23-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	49.66
Std Dev	3.122



# B1L003S, U23-ch125, adc4

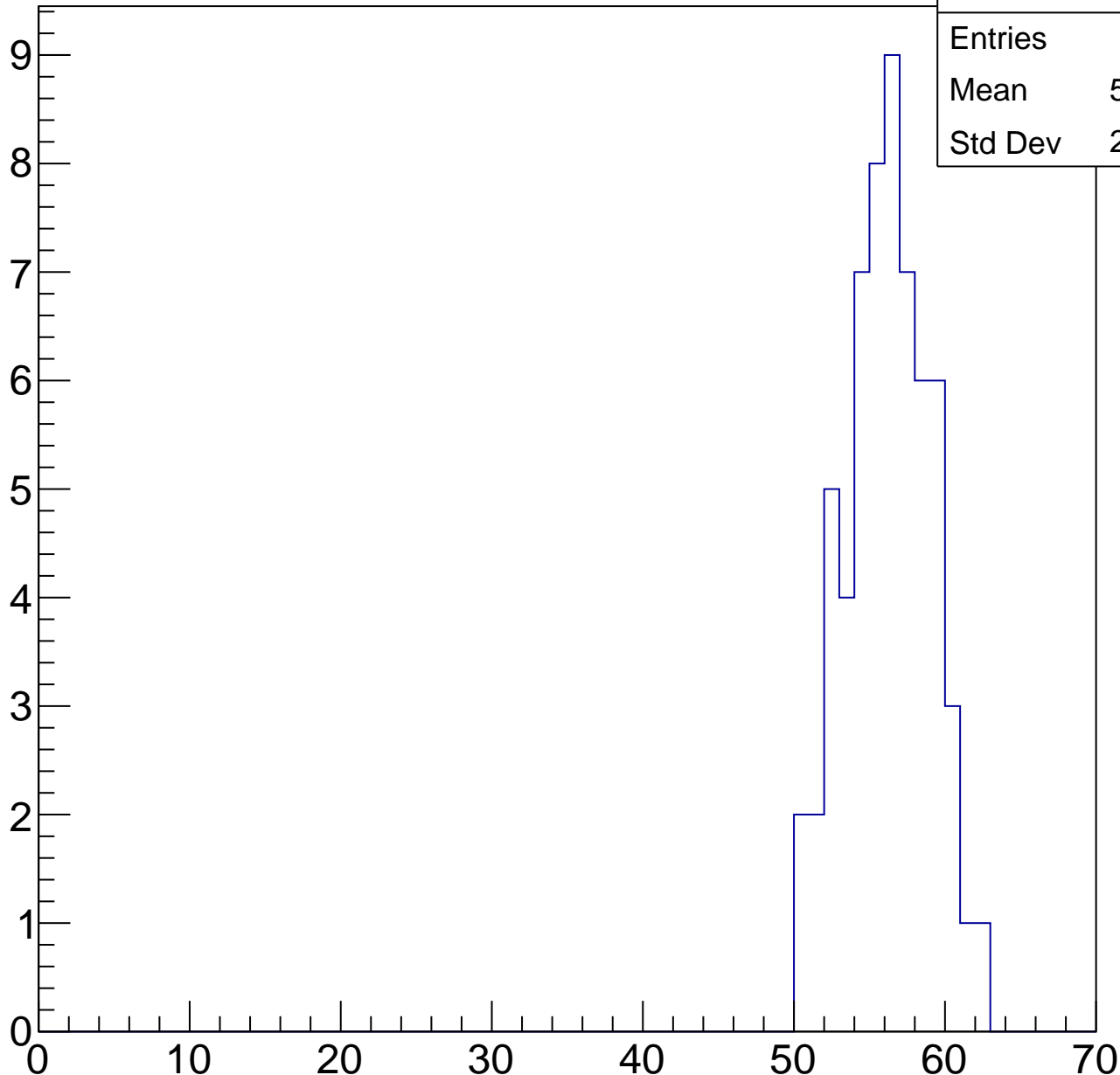
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	55.74
Std Dev	2.757

ampl

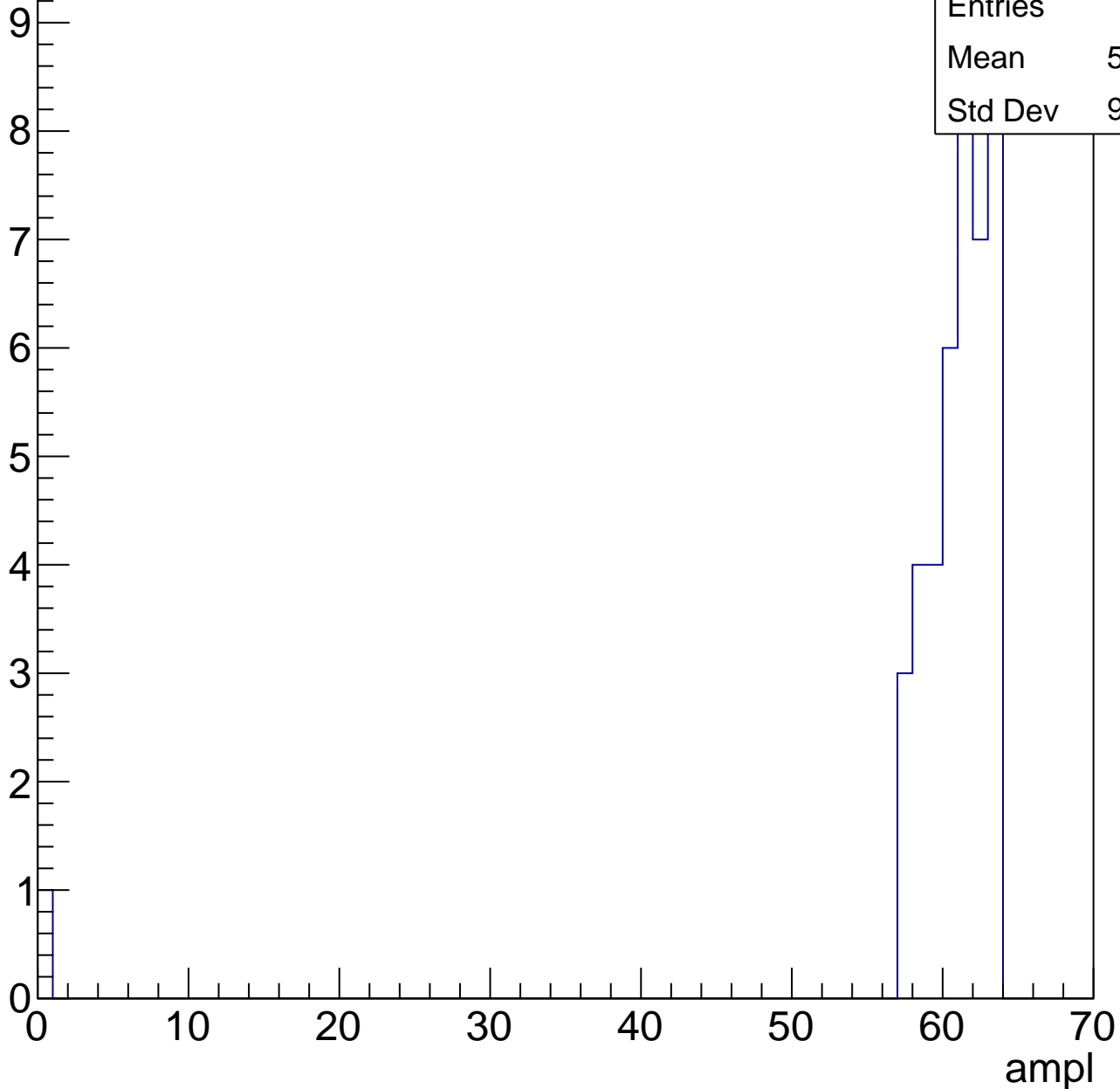


# B1L003S, U23-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	42
Mean	59.24
Std Dev	9.436



# B1L003S, U23-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

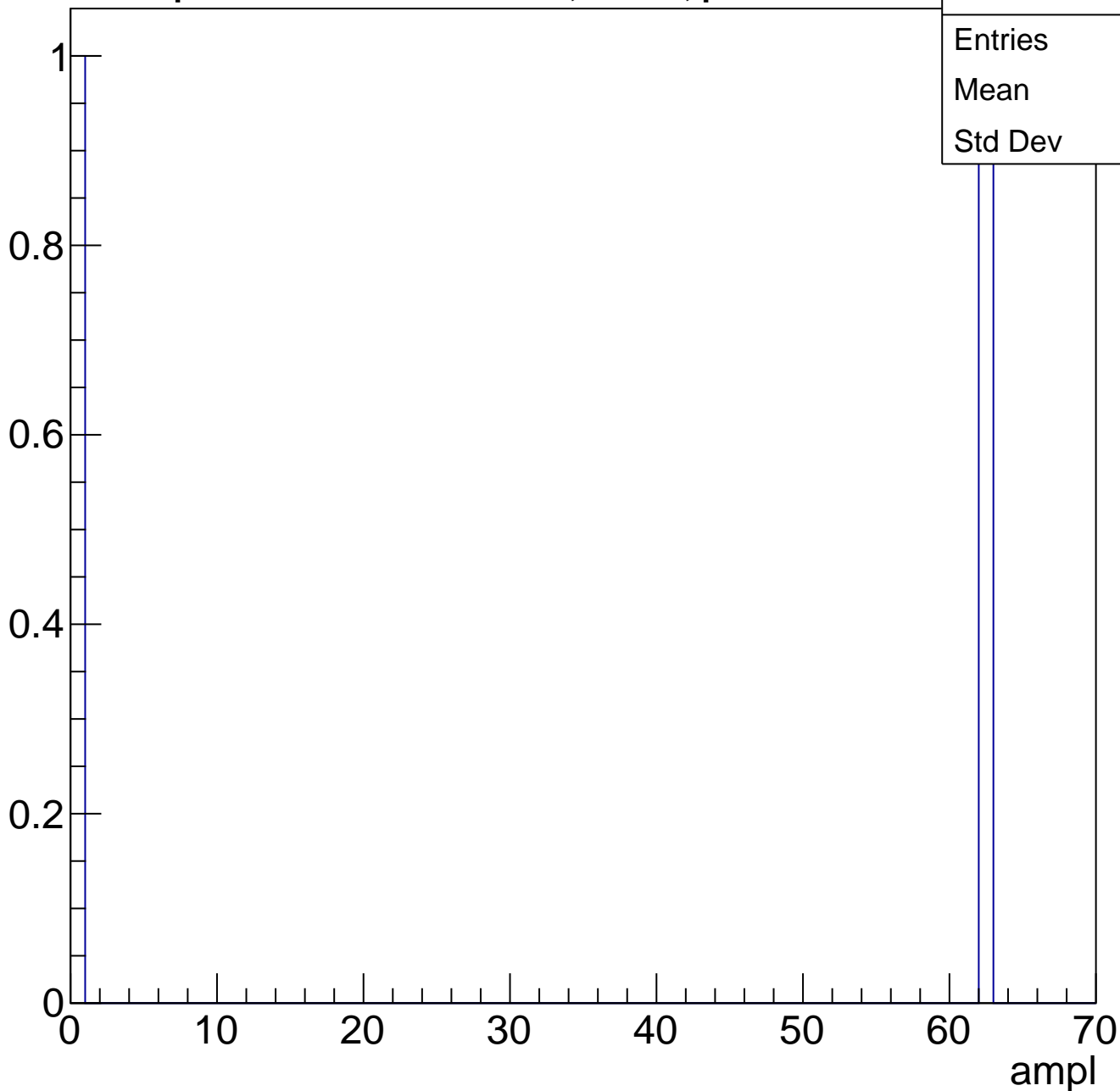




# B1L003S, U23-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U23-ch126, adc0

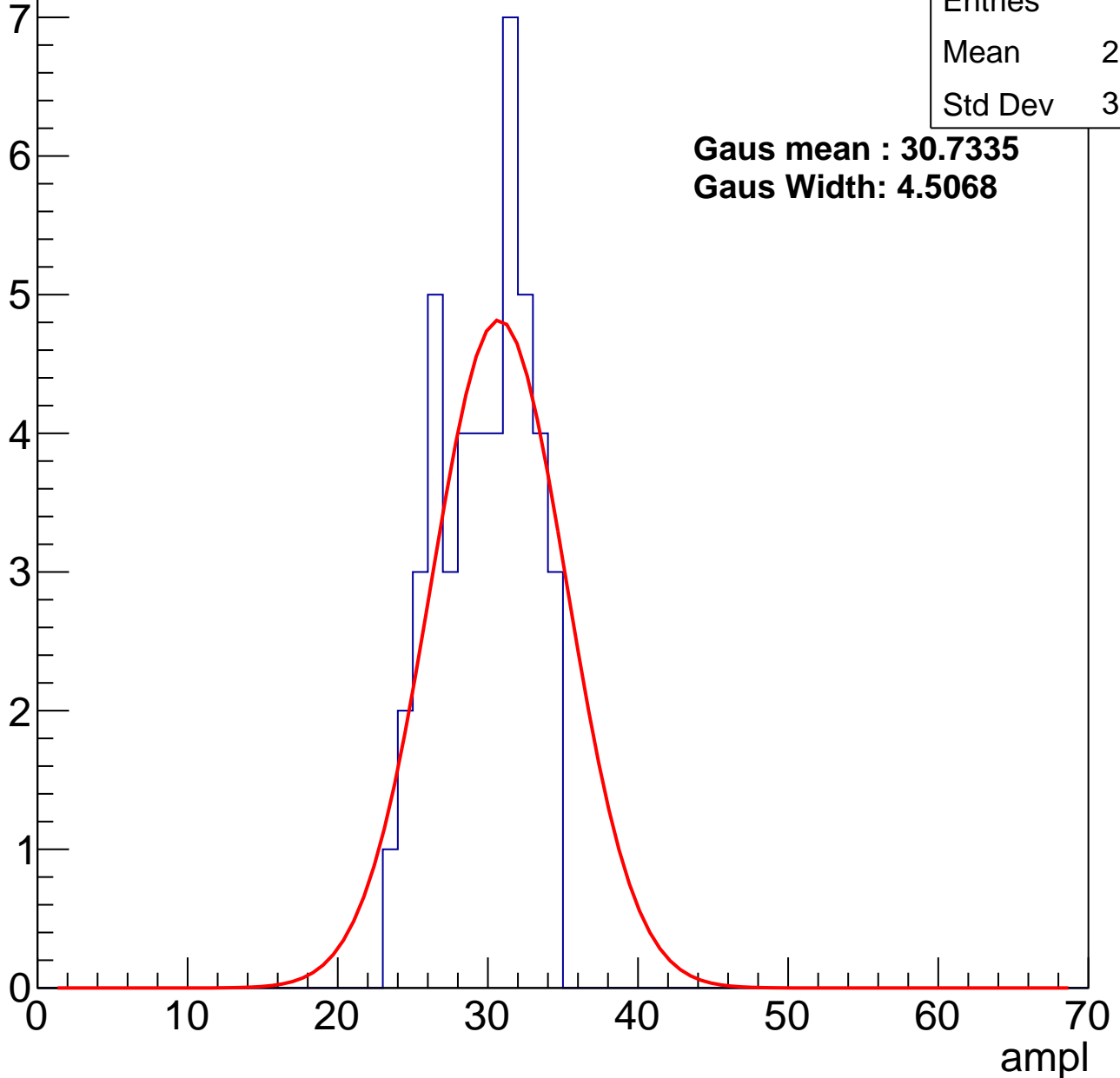
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	29.24
Std Dev	3.012

**Gaus mean : 30.7335**

**Gaus Width: 4.5068**



# B1L003S, U23-ch126, adc1

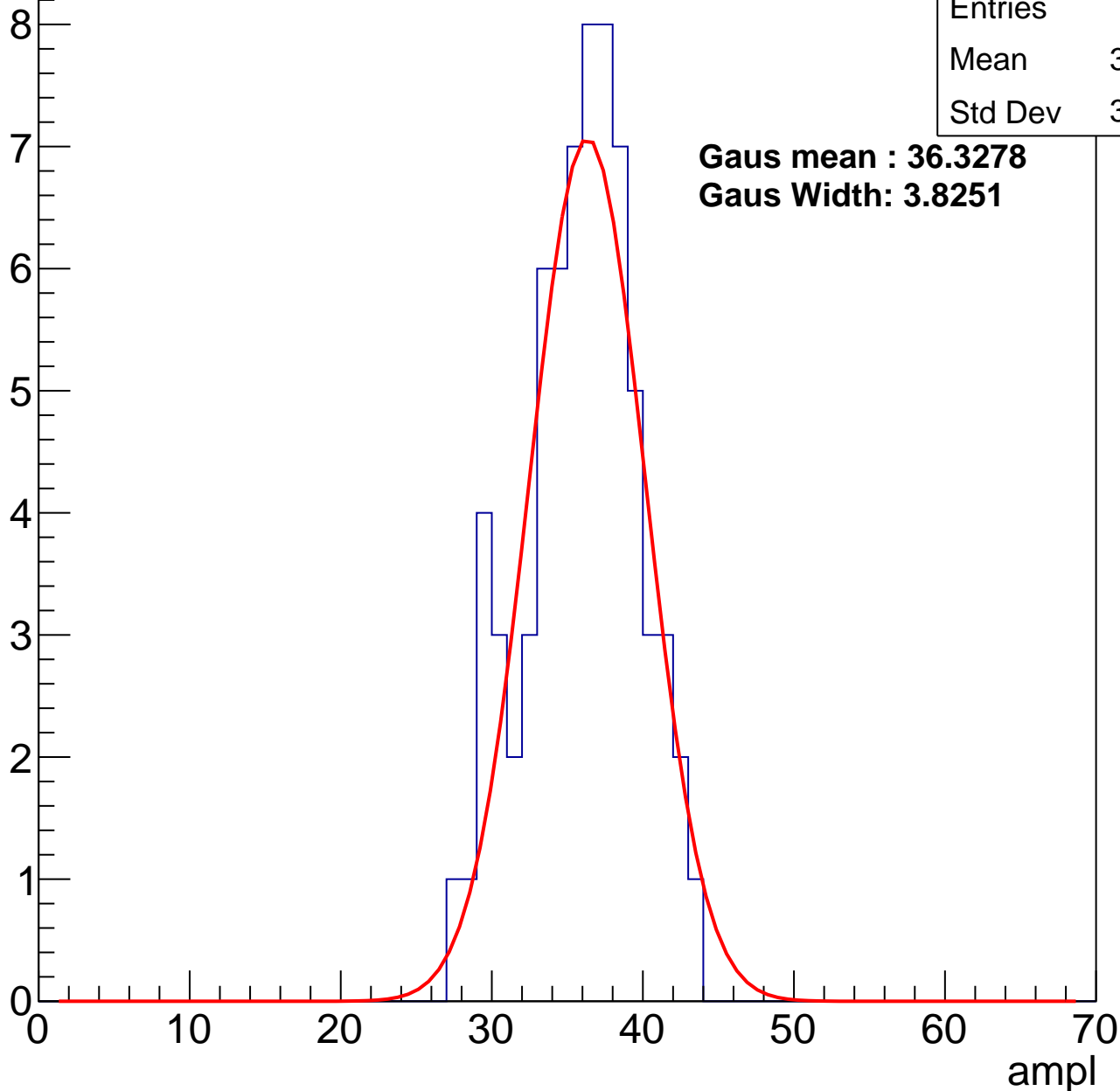
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	35.44
Std Dev	3.655

**Gaus mean : 36.3278**

**Gaus Width: 3.8251**



# B1L003S, U23-ch126, adc2

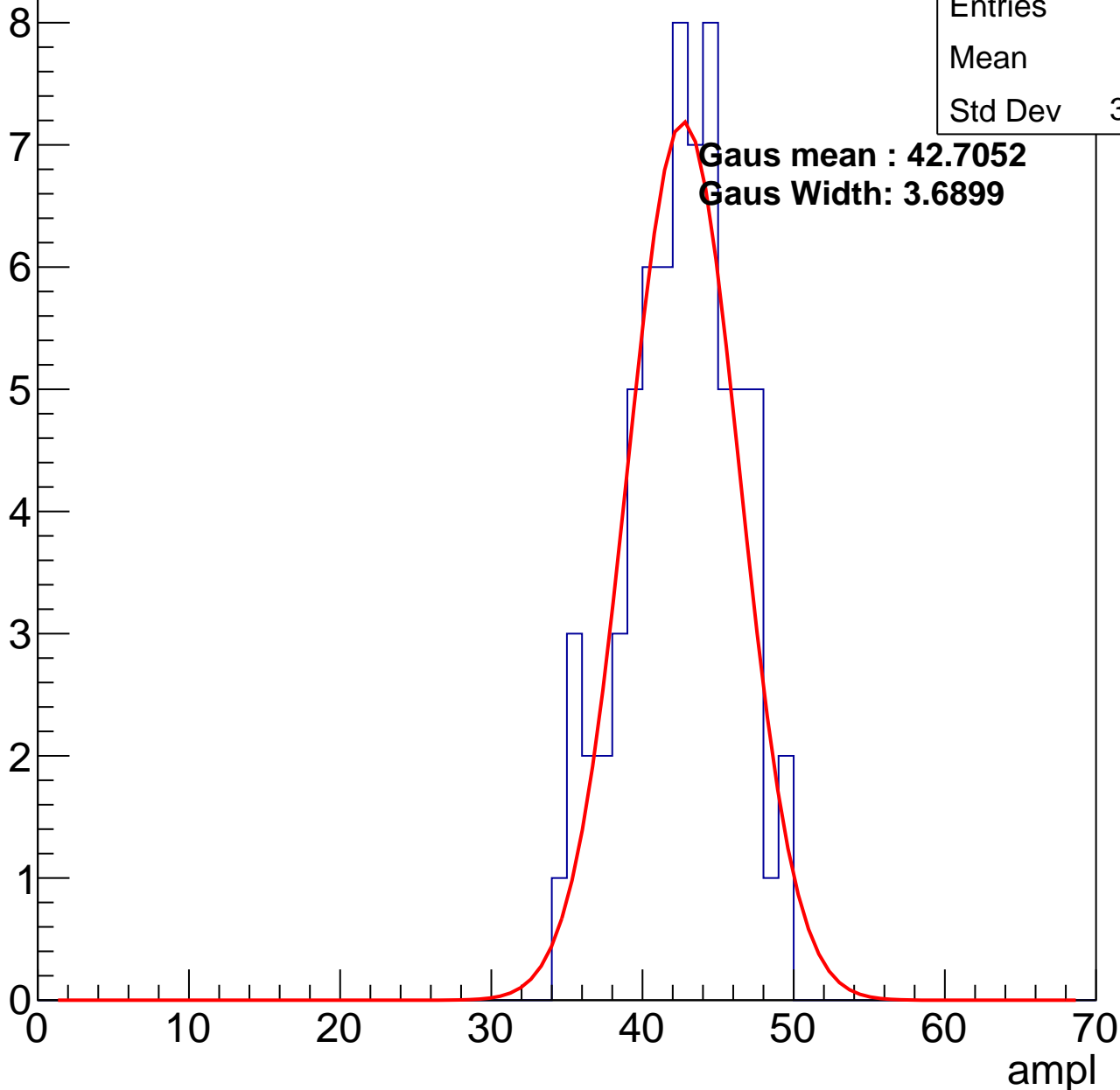
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.1
Std Dev	3.568

**Gaus mean : 42.7052**

**Gaus Width: 3.6899**

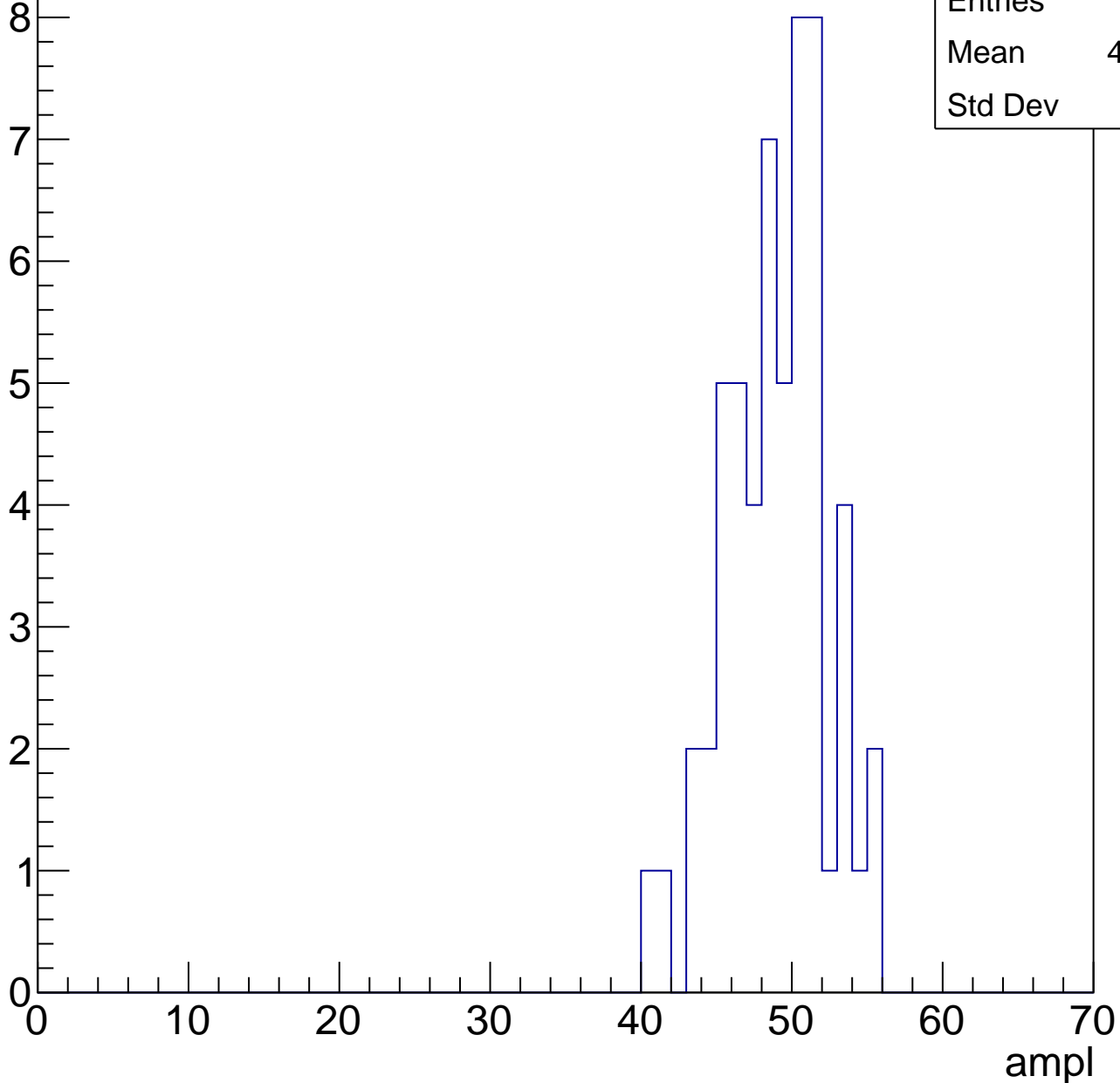


# B1L003S, U23-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

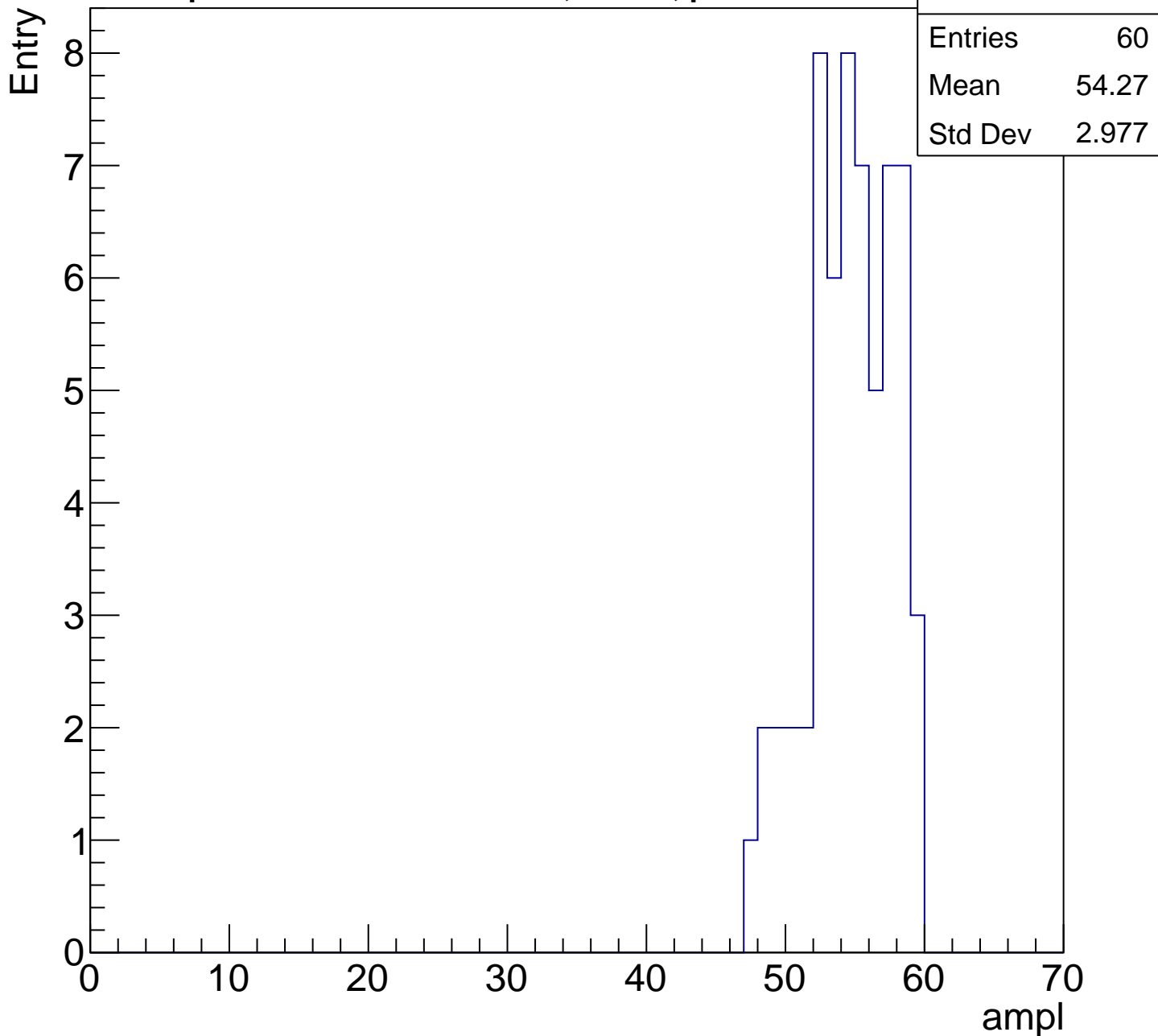
Entry

Entries	56
Mean	48.48
Std Dev	3.3



# B1L003S, U23-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

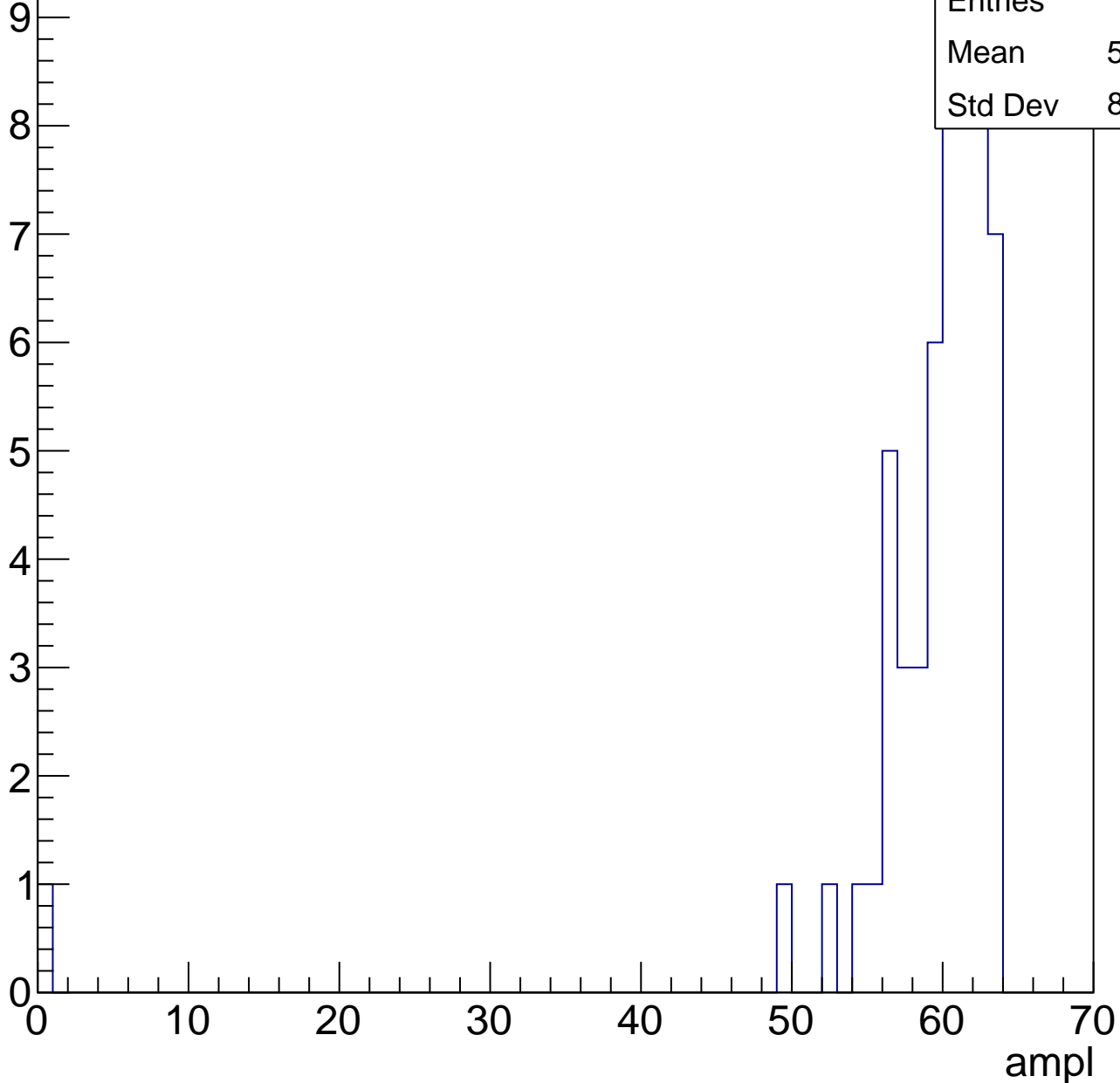


# B1L003S, U23-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	58.47
Std Dev	8.474



# B1L003S, U23-ch126, adc6

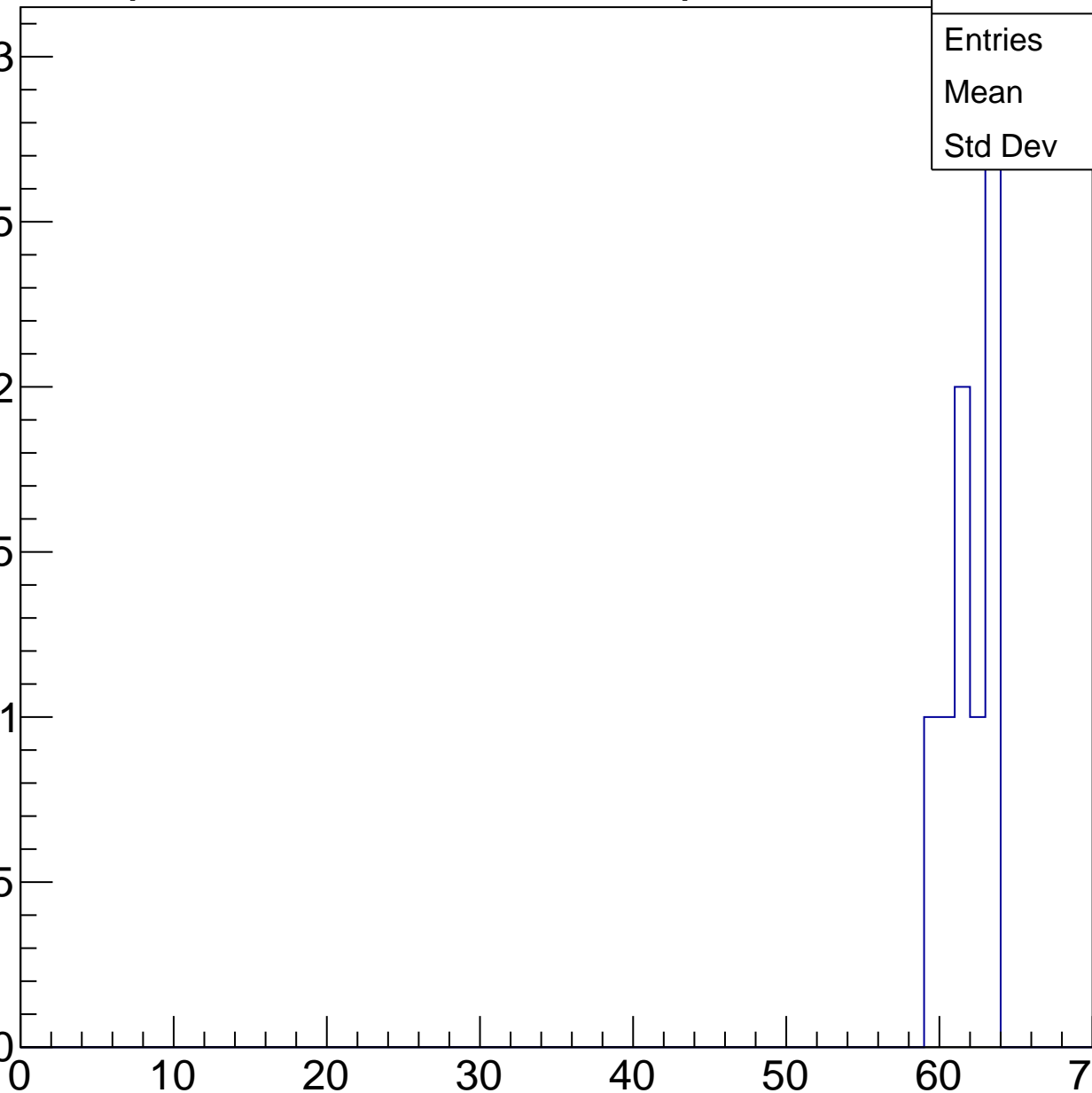
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	8
Mean	61.5
Std Dev	1.414

ampl





# B1L003S, U23-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U23-ch127, adc0

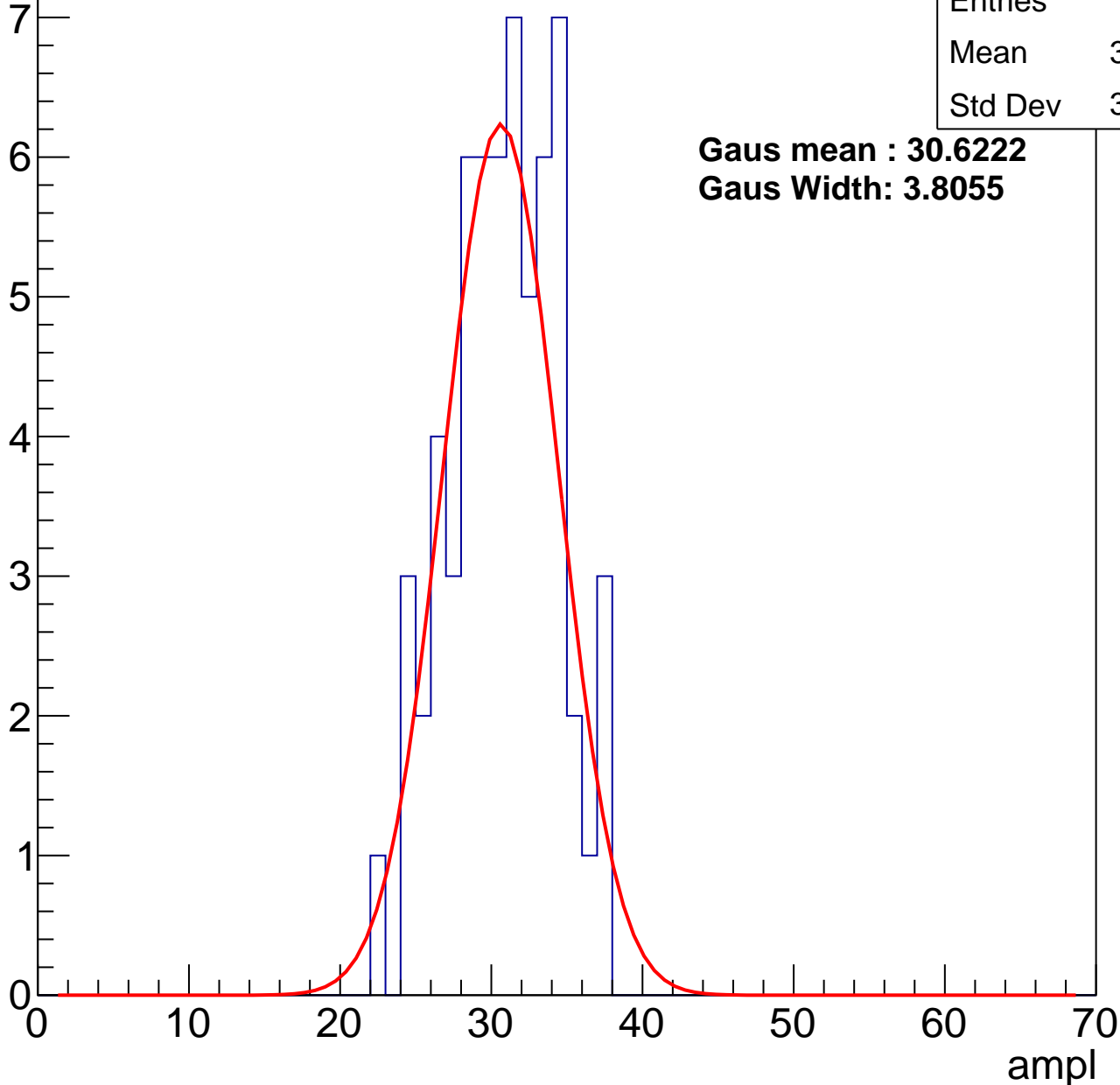
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	30.34
Std Dev	3.505

**Gaus mean : 30.6222**

**Gaus Width: 3.8055**



# B1L003S, U23-ch127, adc1

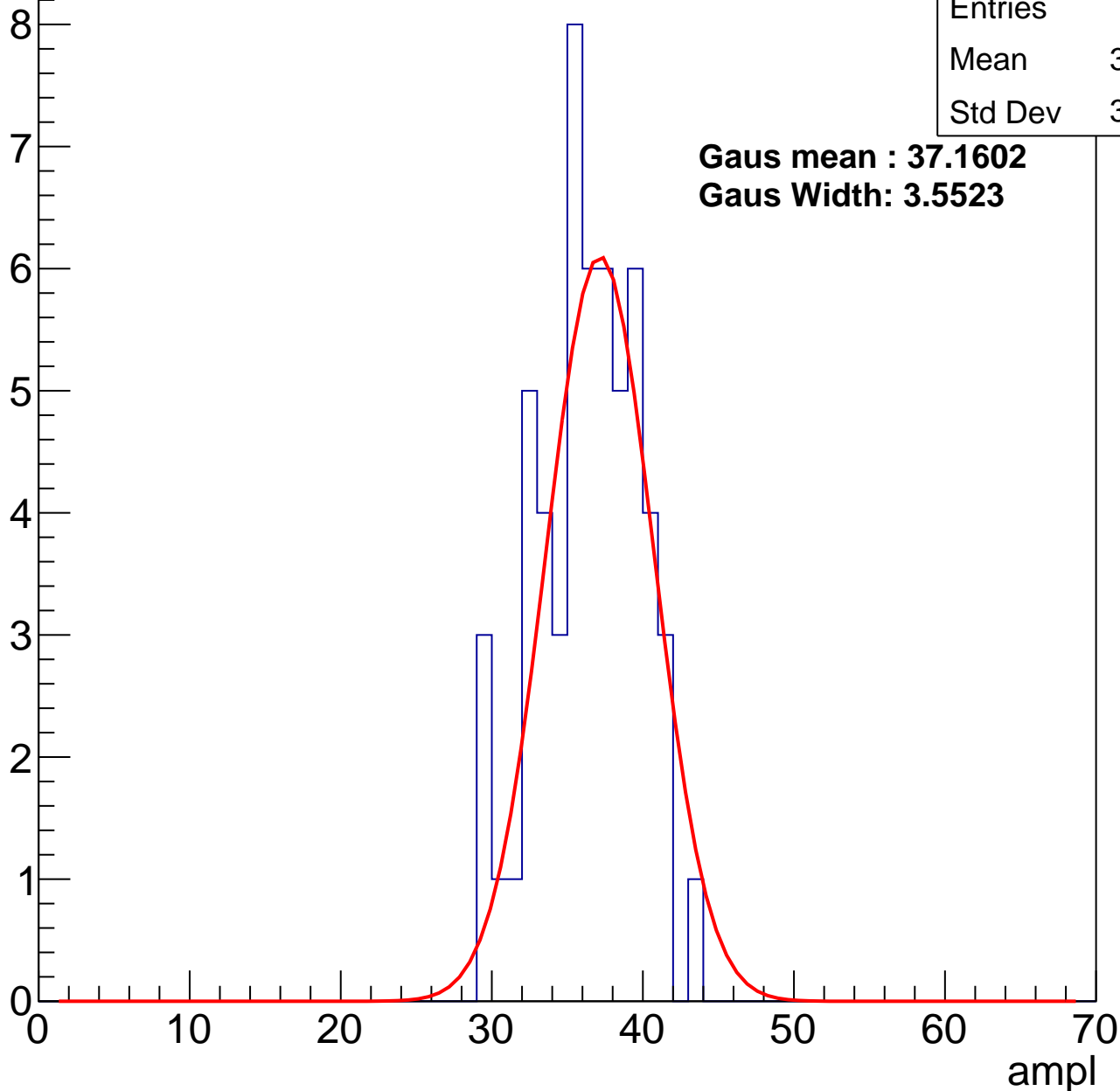
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	35.89
Std Dev	3.304

**Gaus mean : 37.1602**

**Gaus Width: 3.5523**



# B1L003S, U23-ch127, adc2

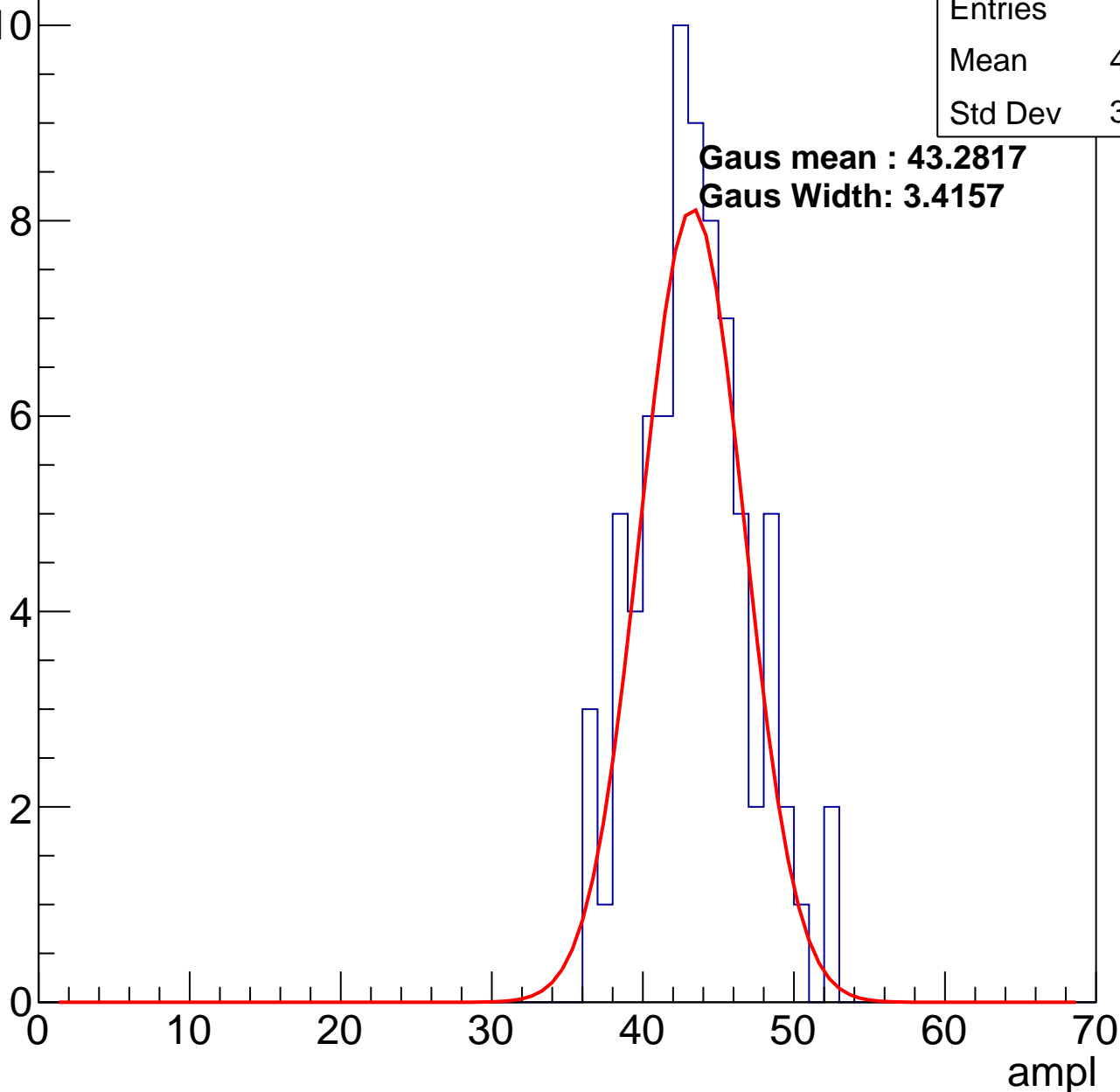
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	42.99
Std Dev	3.593

**Gaus mean : 43.2817**

**Gaus Width: 3.4157**

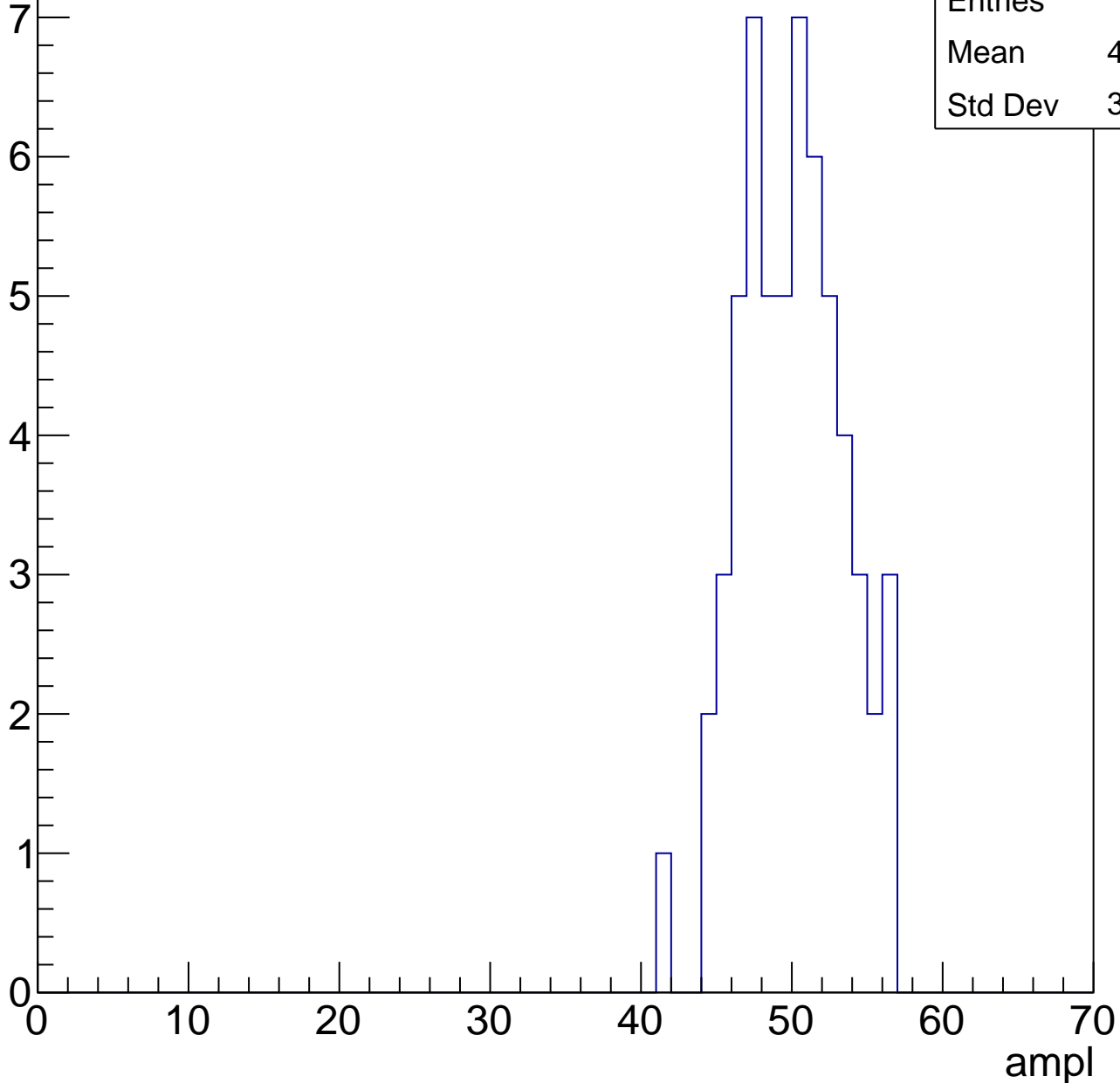


# B1L003S, U23-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	49.59
Std Dev	3.358

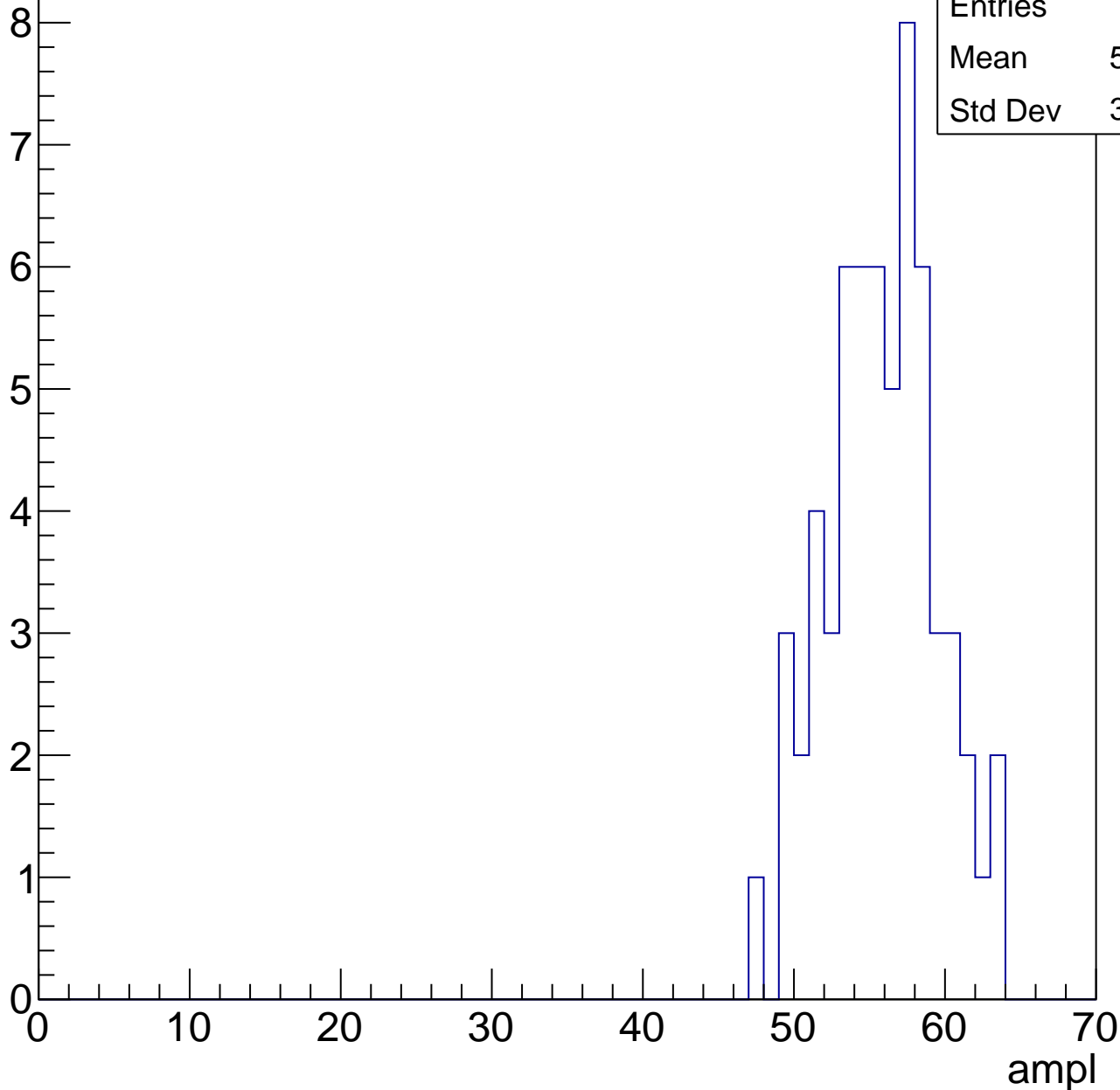


# B1L003S, U23-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	55.36
Std Dev	3.617



# B1L003S, U23-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

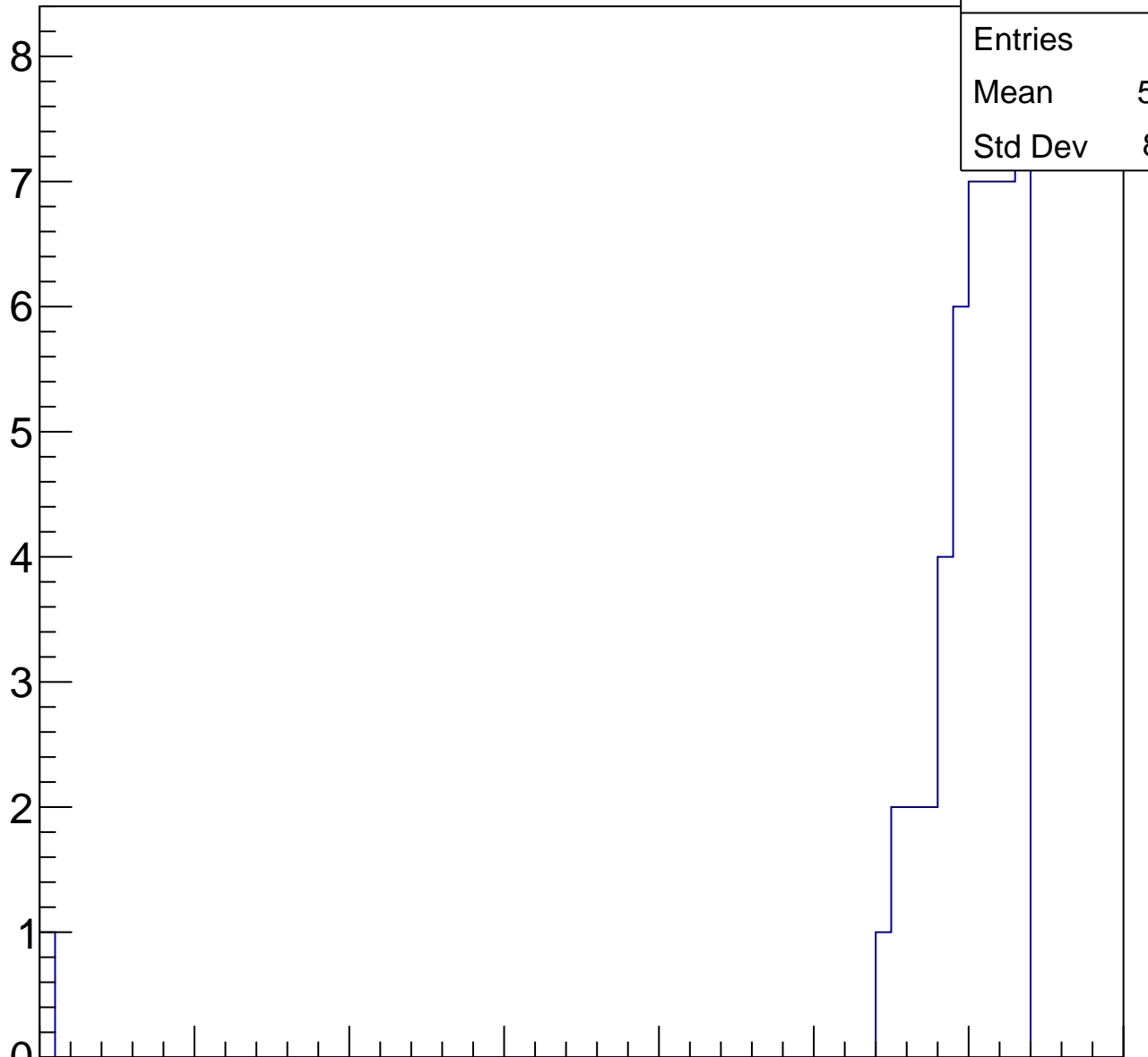
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	58.74
Std Dev	8.981

ampl

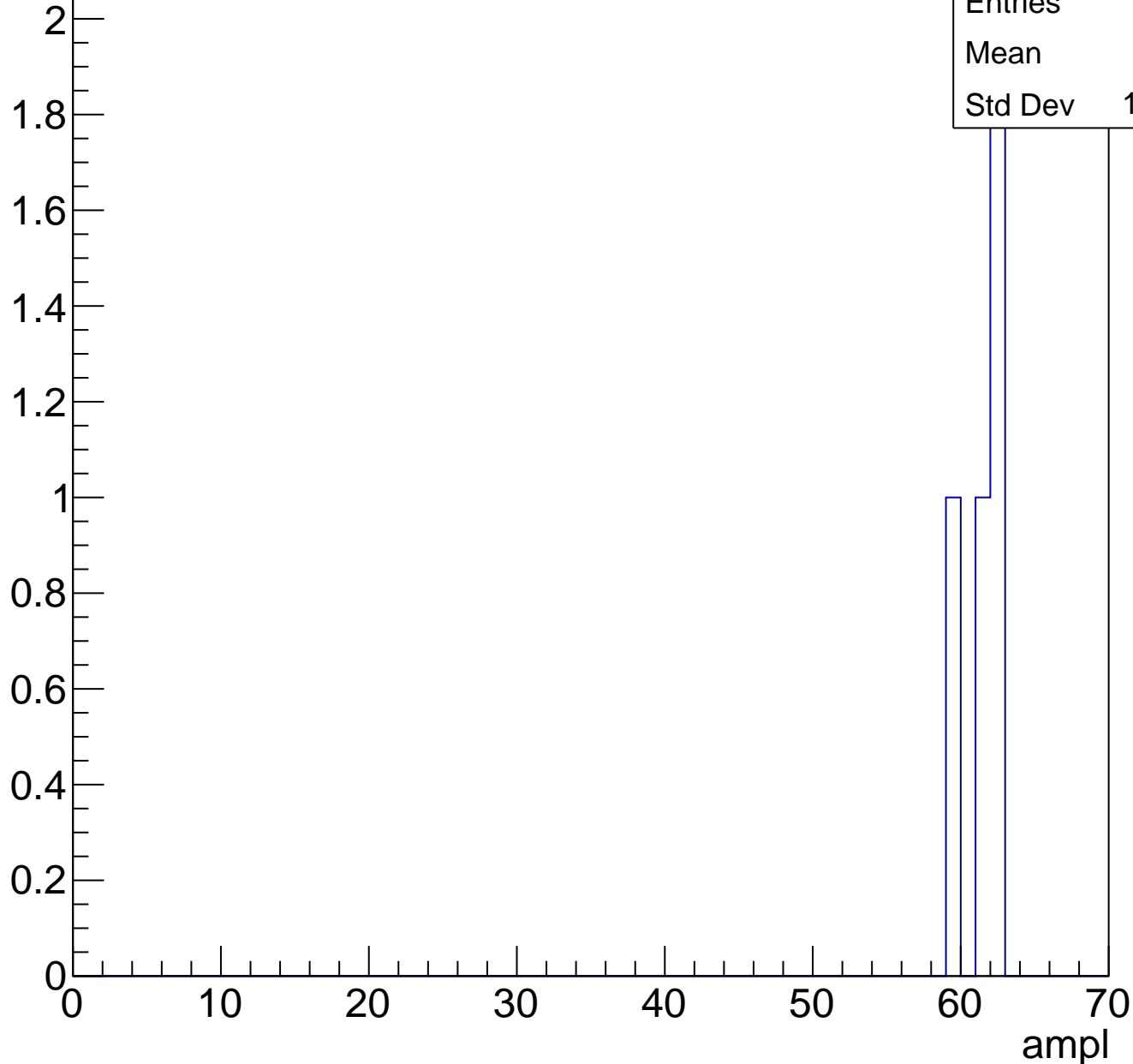
0 10 20 30 40 50 60 70



# B1L003S, U23-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

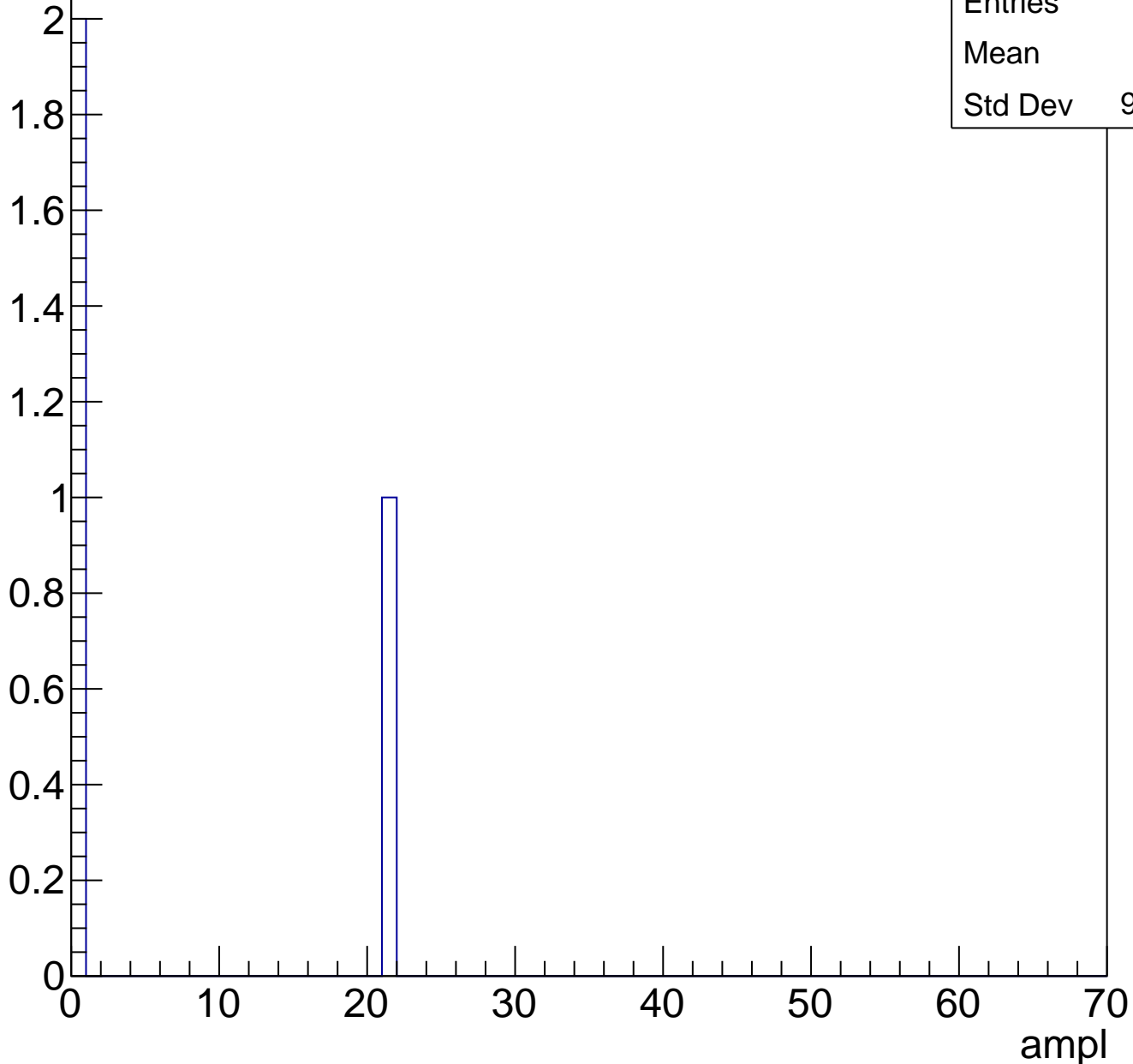




# B1L003S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

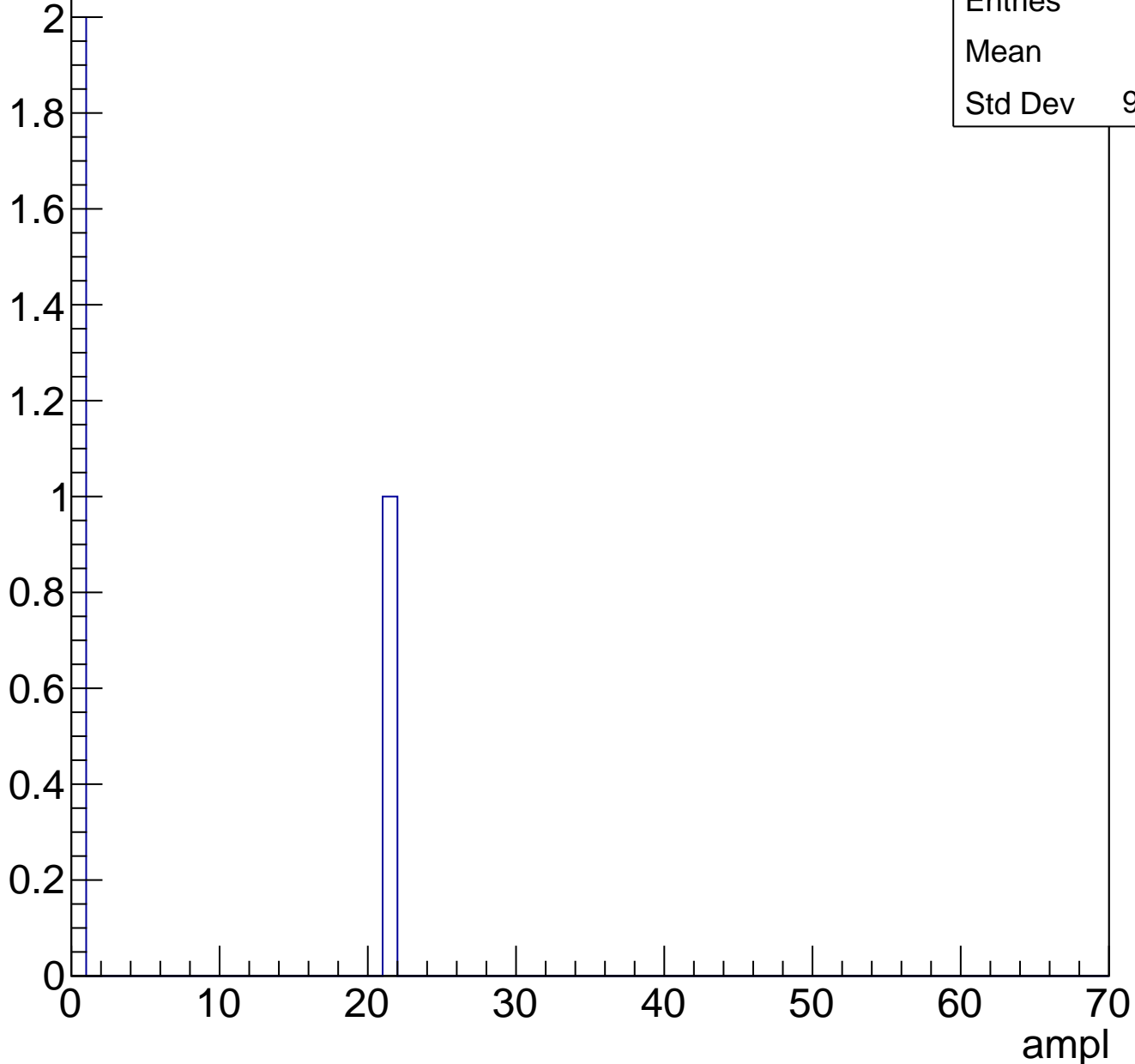


Entries	3
Mean	7
Std Dev	9.899

# B1L003S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899