

B0L001S, U14-ch0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.83
Std Dev	11.66

Turn on : 27.9408

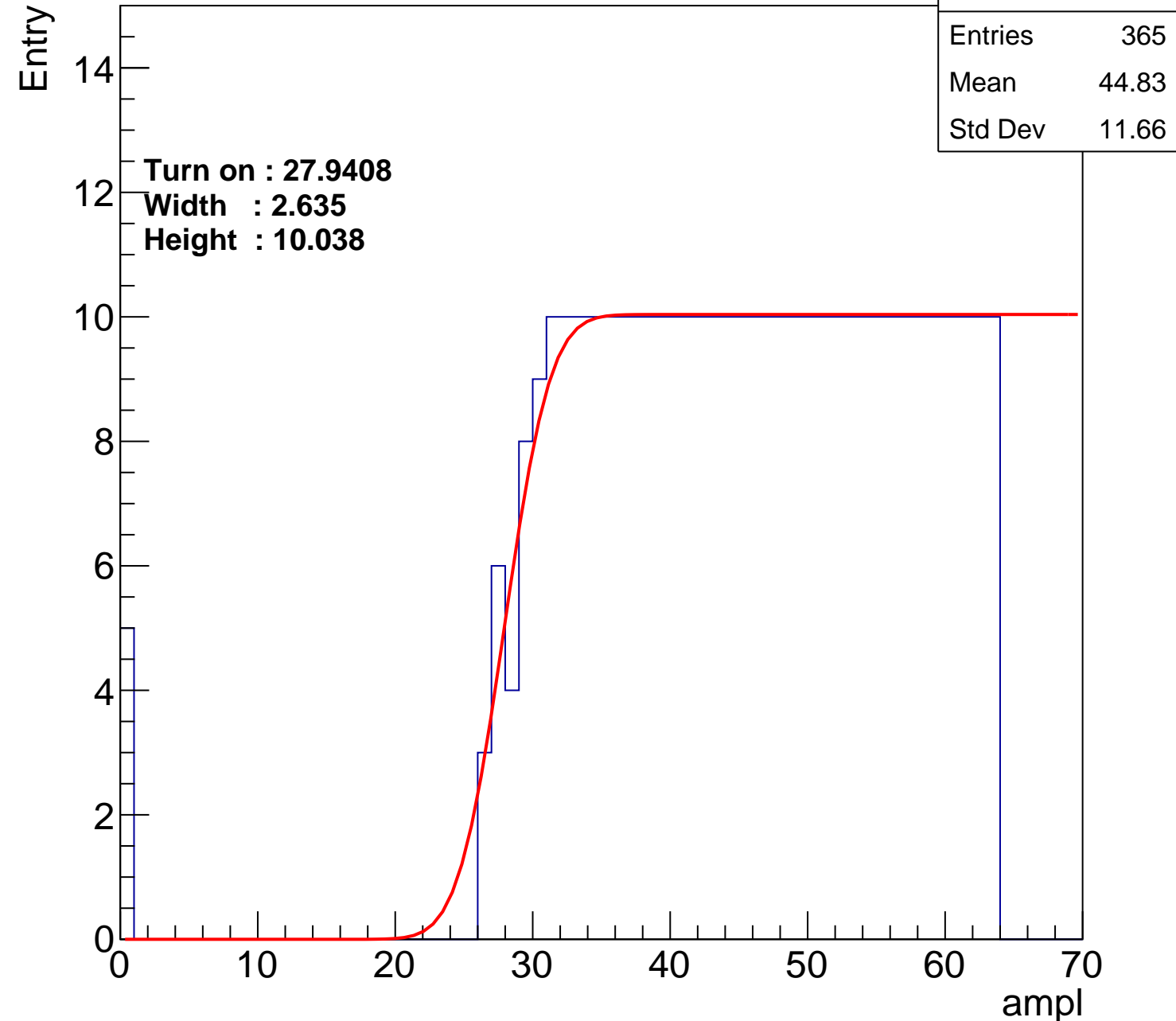
Width : 2.635

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.66
Std Dev	11.17

Turn on : 27.2487

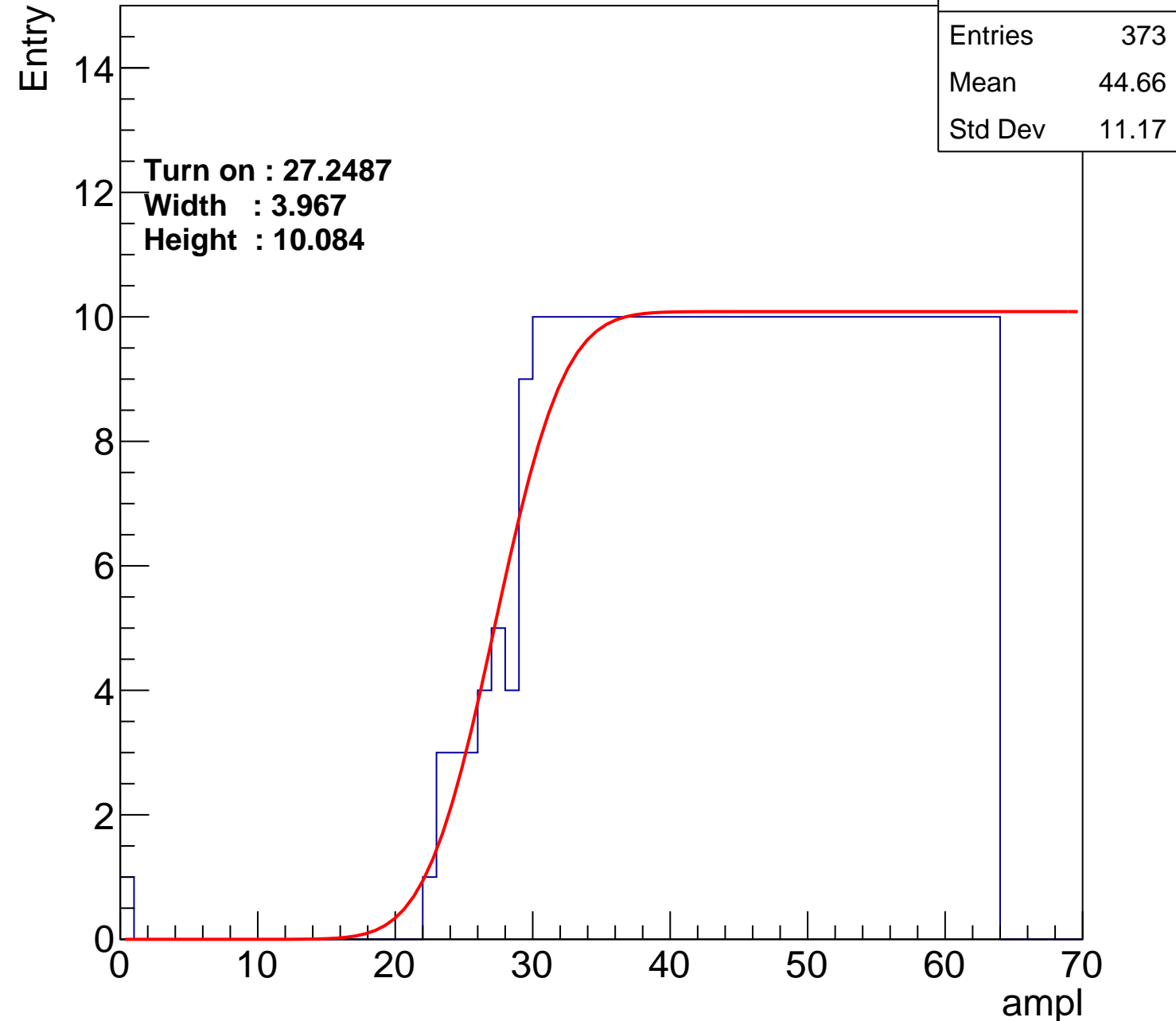
Width : 3.967

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.2334

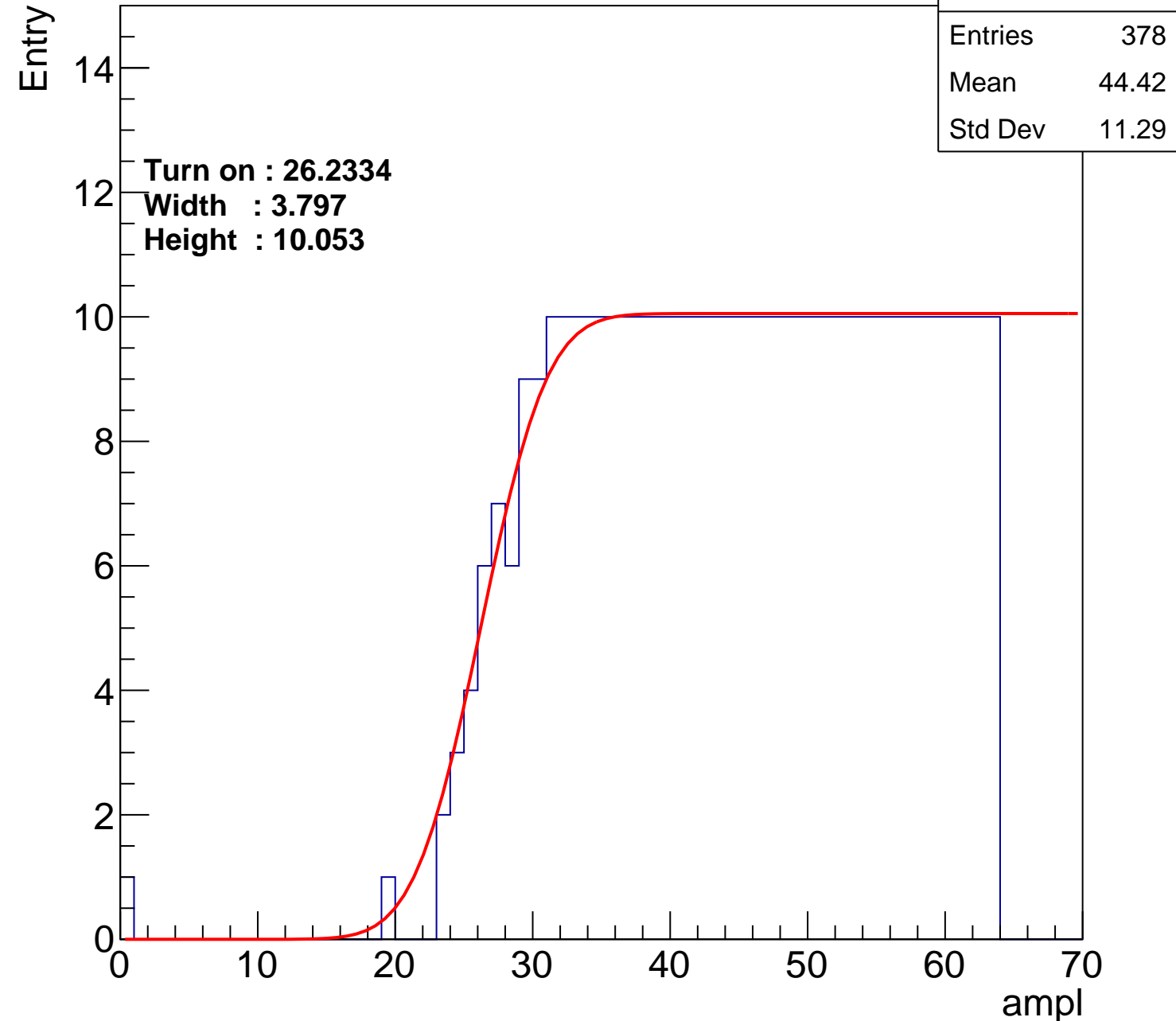
Width : 3.797

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	352
Mean	45.55
Std Dev	11.05

Turn on : 28.9659

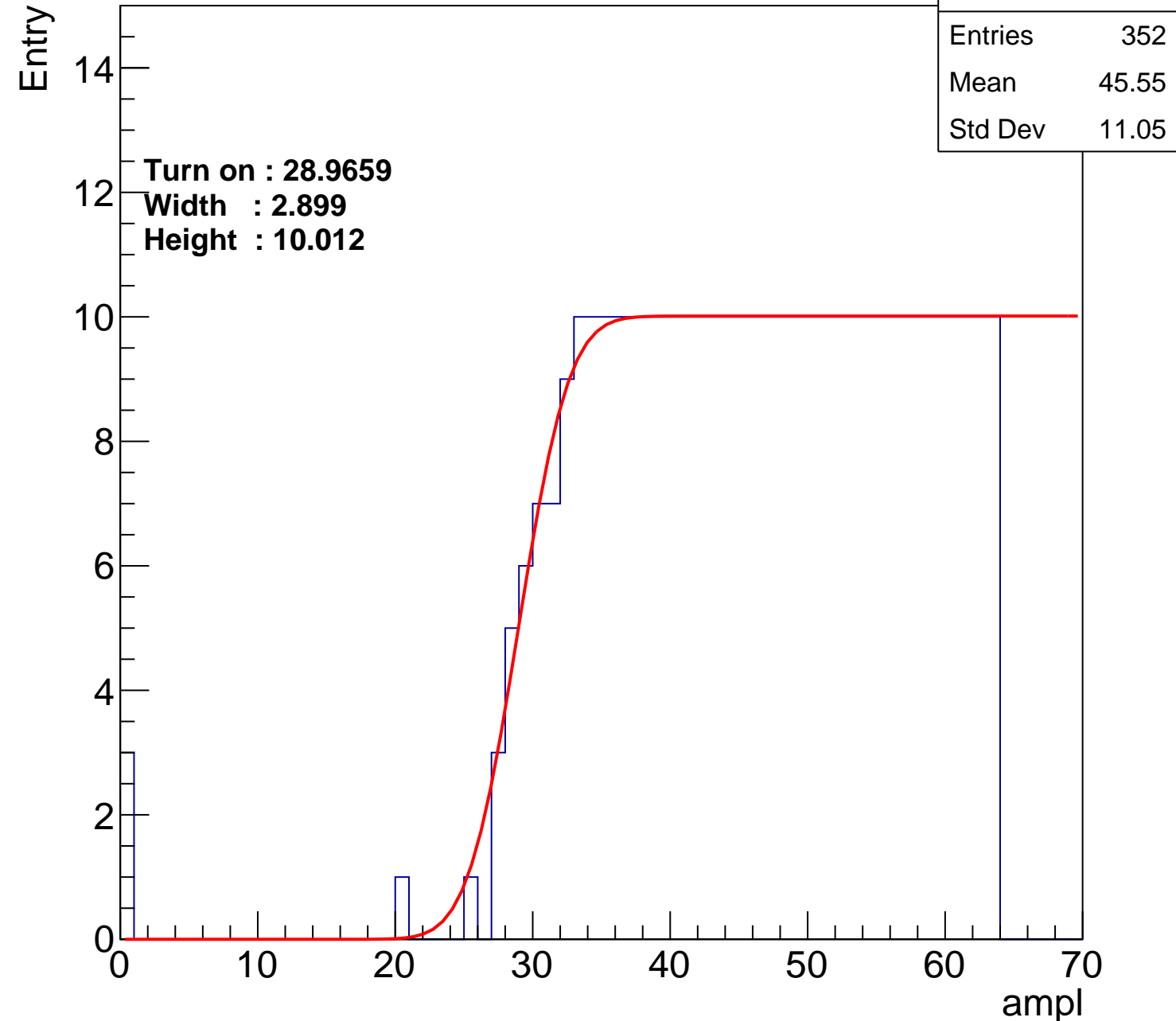
Width : 2.899

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.2310

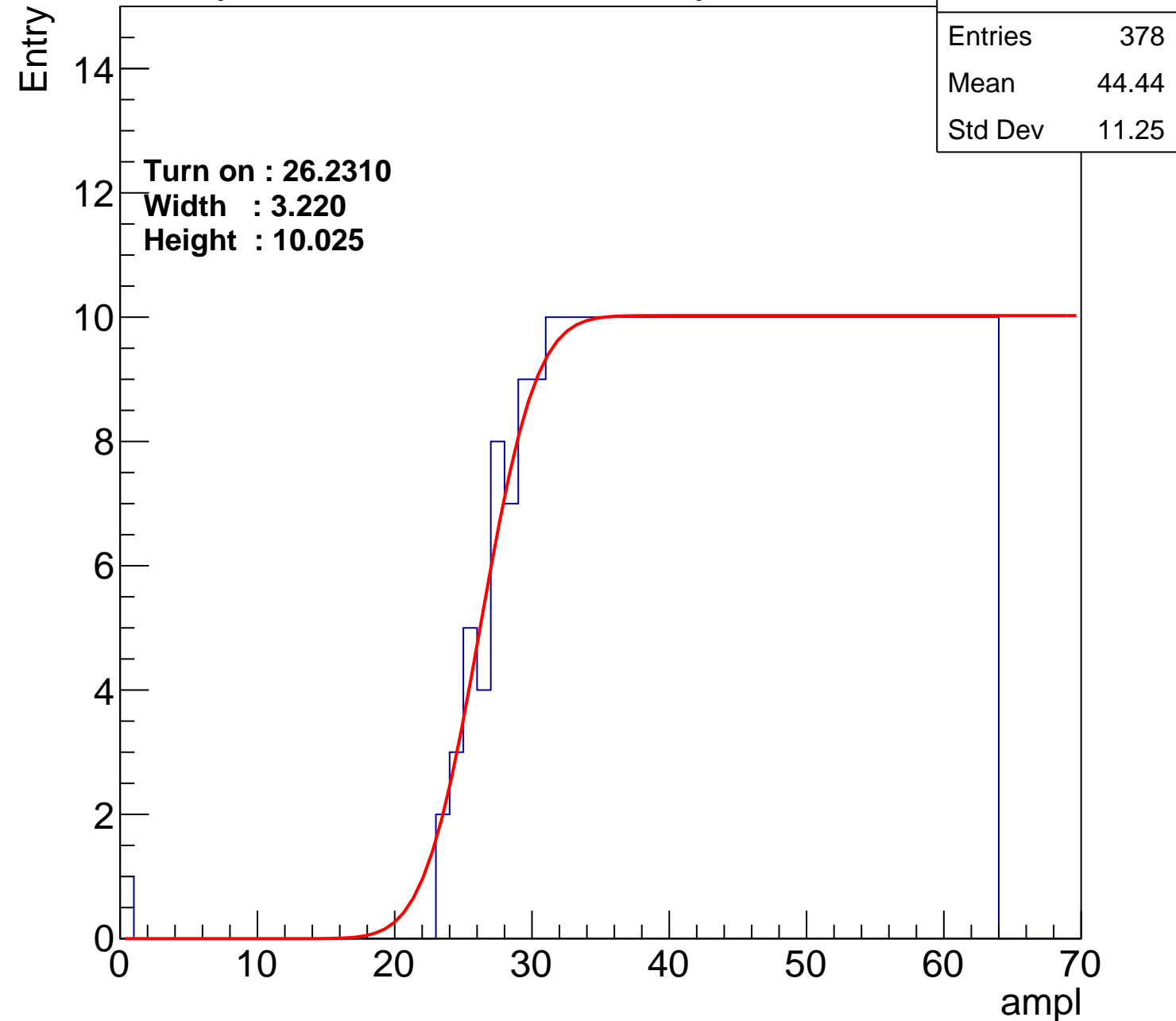
Width : 3.220

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch5

calib_packv5_042523_0143.root, FC#9, port A1

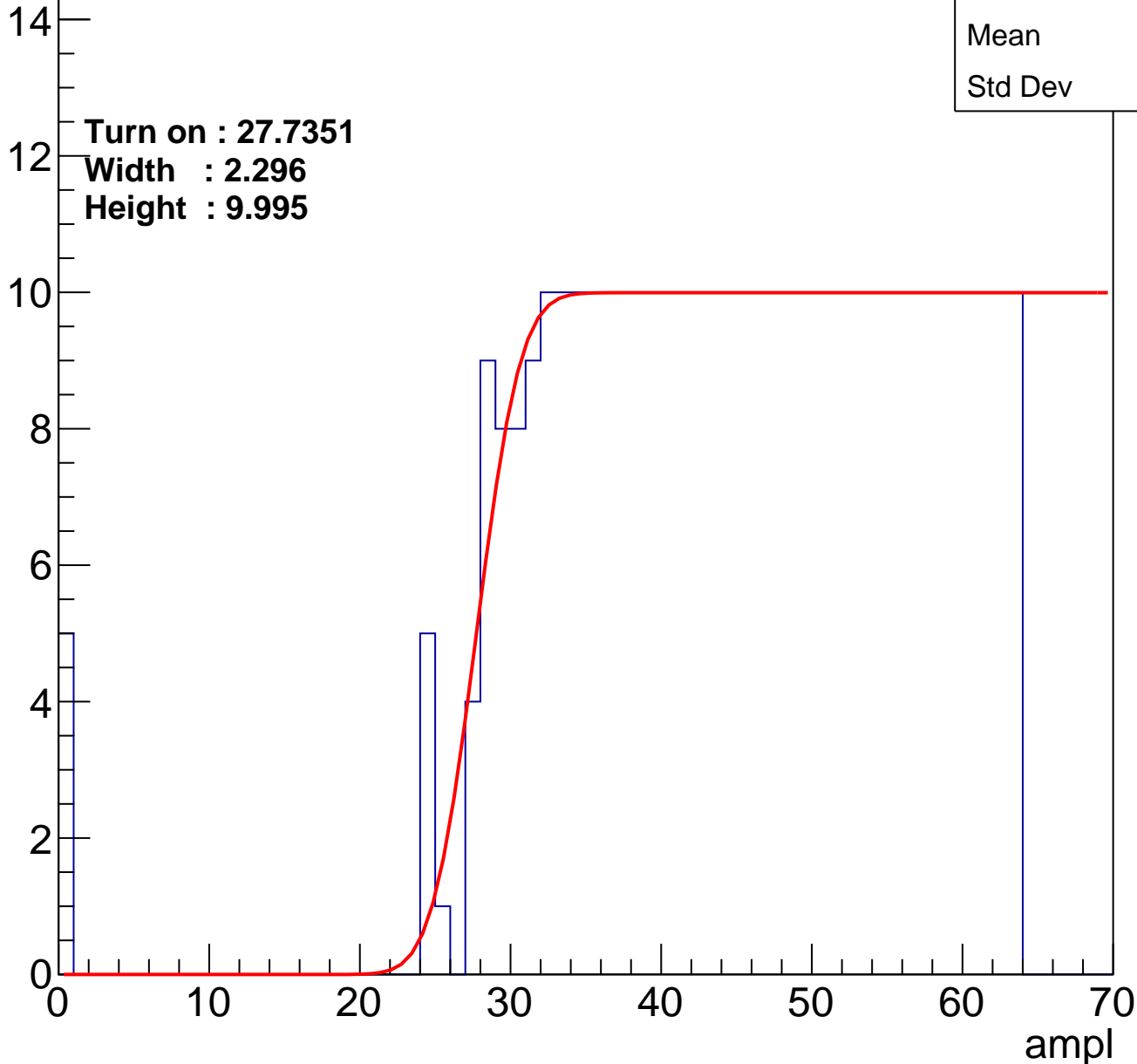
Entry

Entries	369
Mean	44.6
Std Dev	11.81

Turn on : 27.7351

Width : 2.296

Height : 9.995



B0L001S, U14-ch6

calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.67
Std Dev	11.48

Turn on : 27.4764

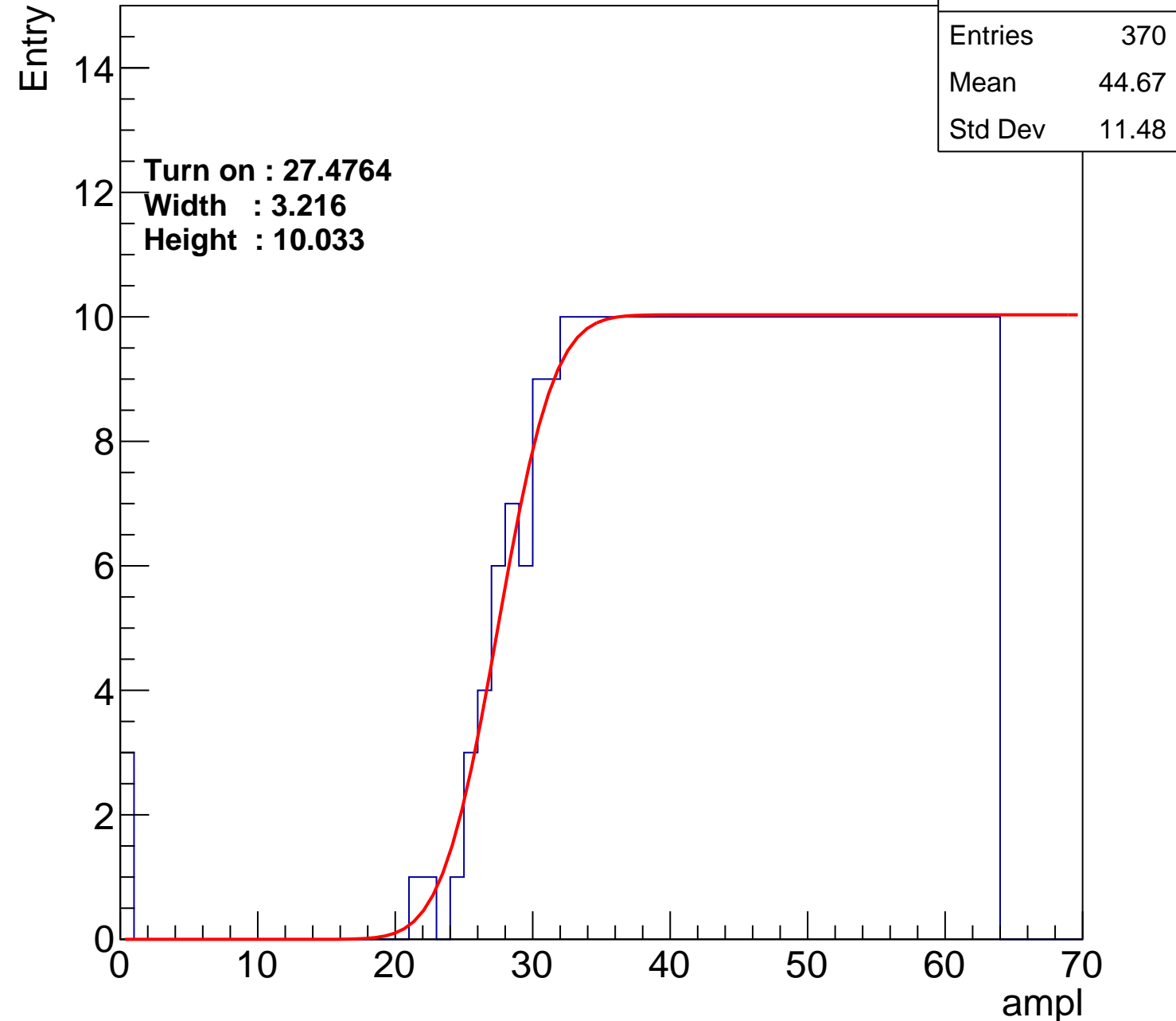
Width : 3.216

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch7

calib_packv5_042523_0143.root, FC#9, port A1

Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 26.3686

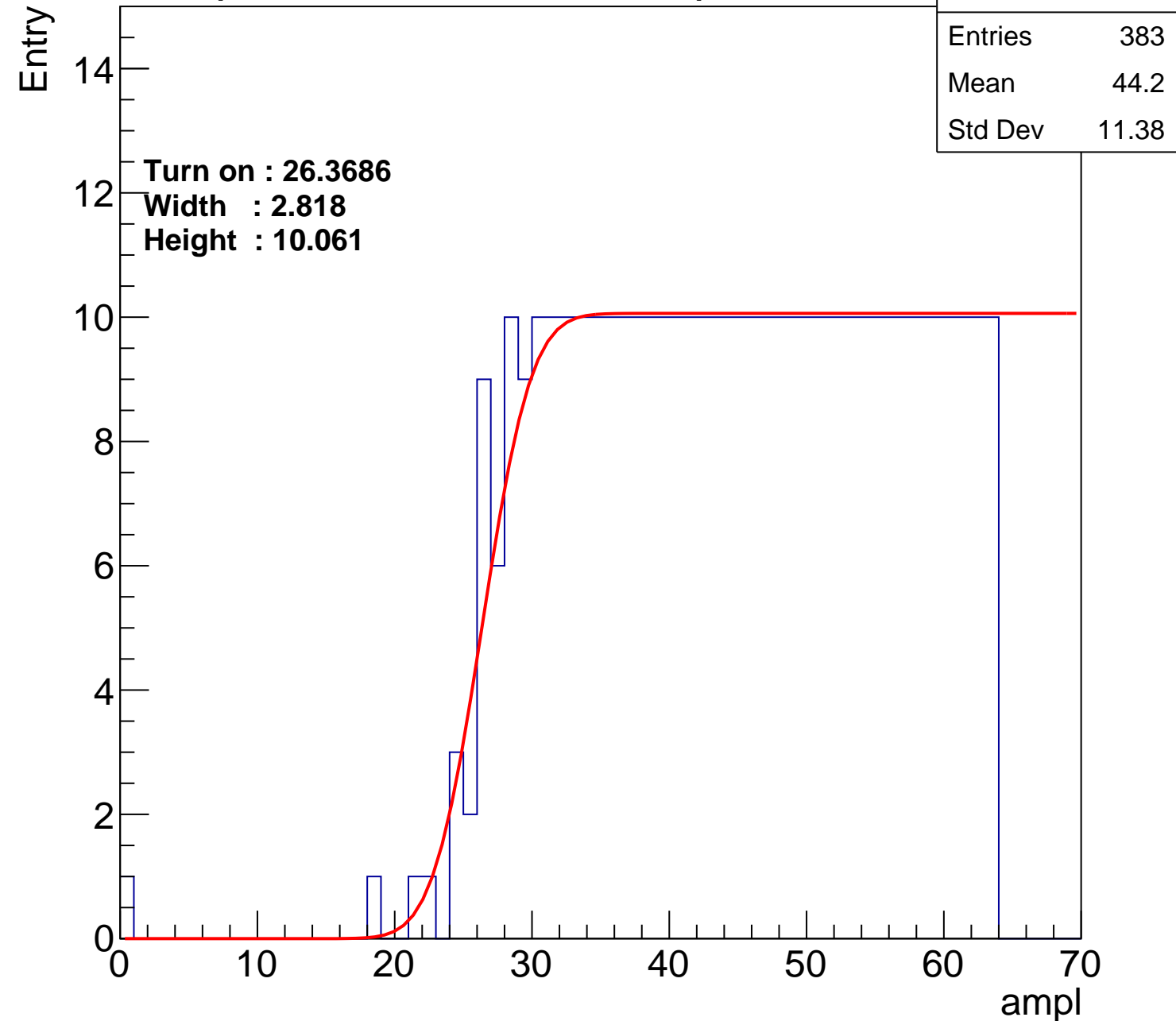
Width : 2.818

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch8

calib_packv5_042523_0143.root, FC#9, port A1

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 26.5373

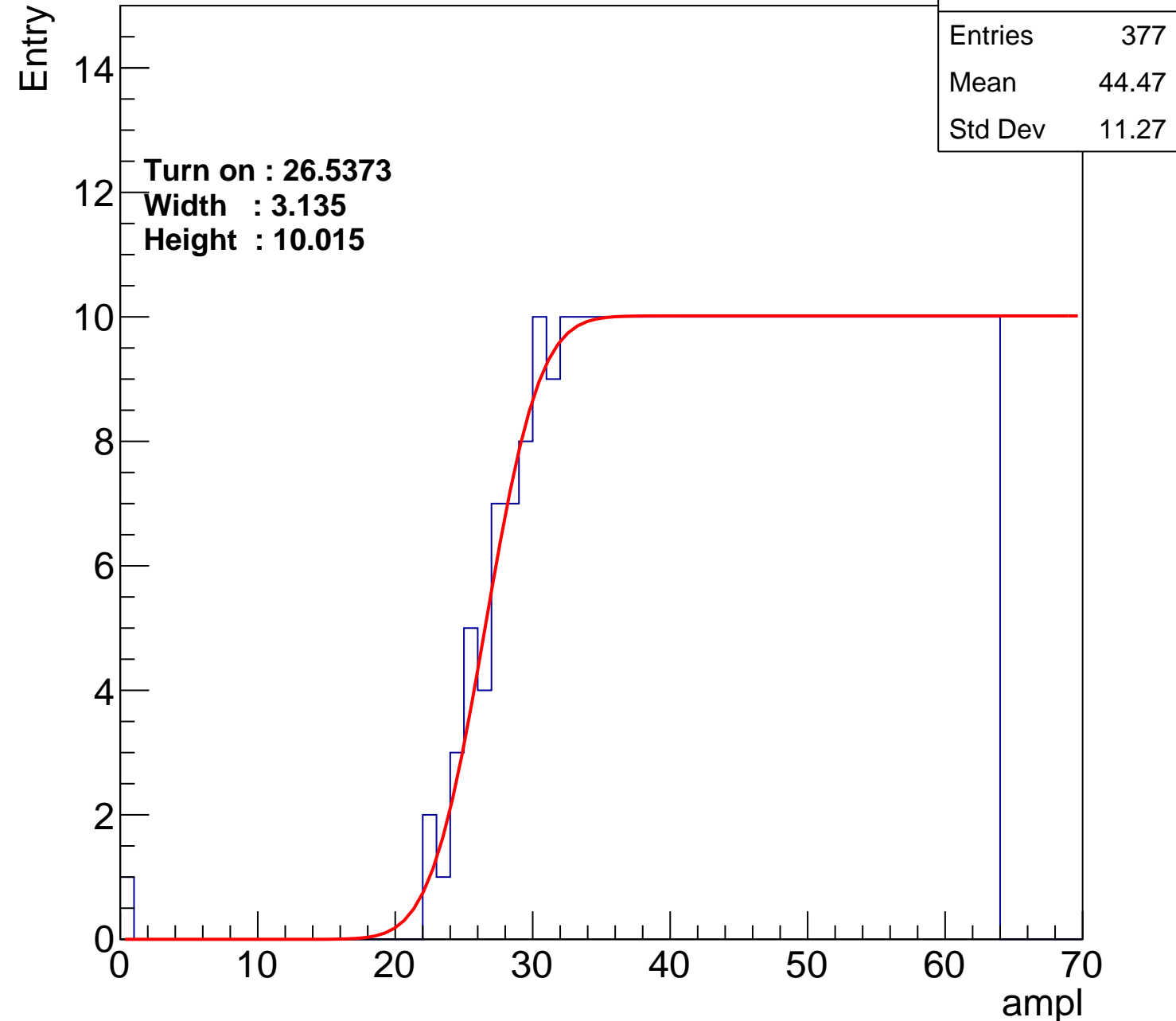
Width : 3.135

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch9

calib_packv5_042523_0143.root, FC#9, port A1

Entries	345
Mean	46
Std Dev	10.61

Turn on : 29.8853

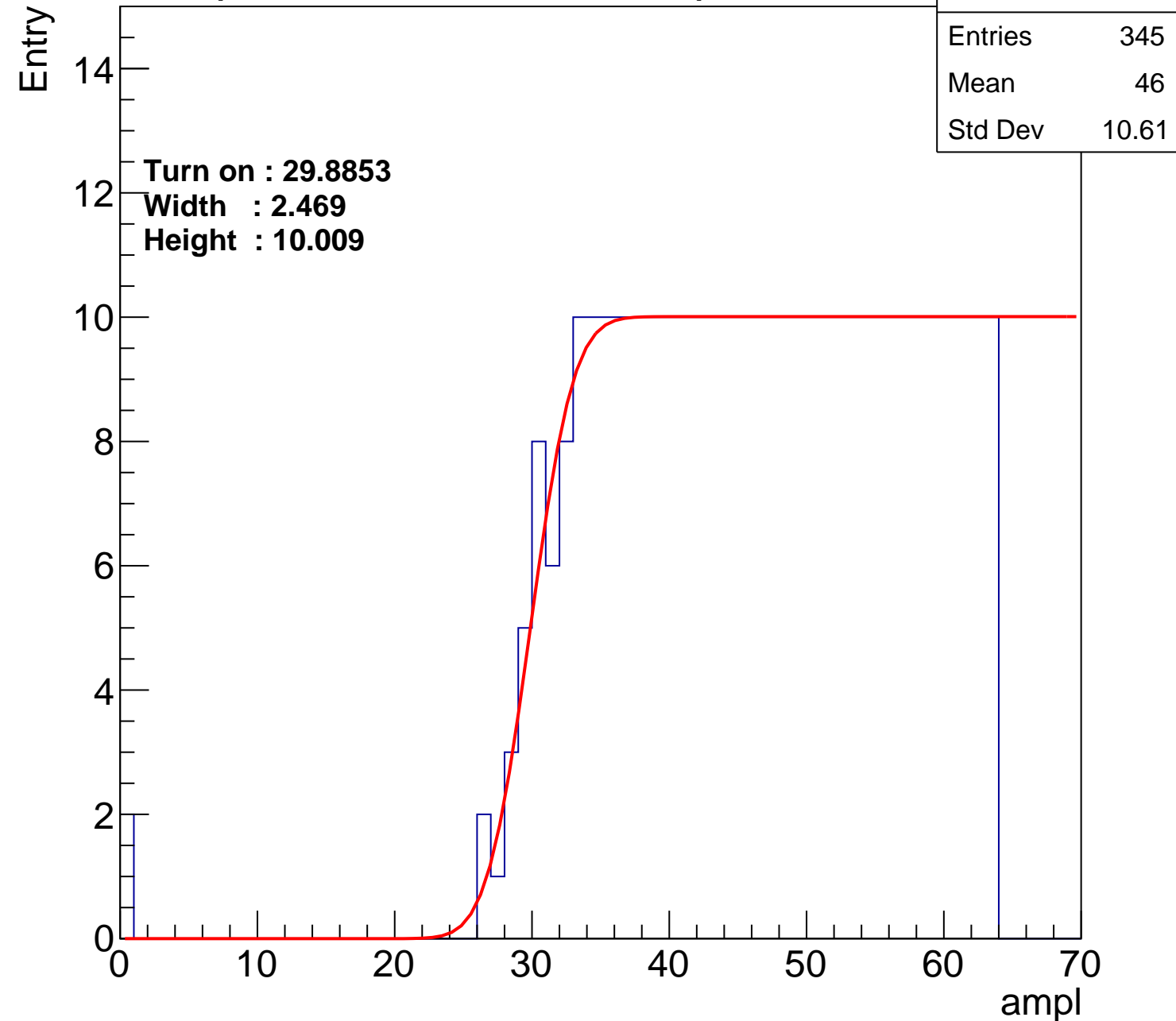
Width : 2.469

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl

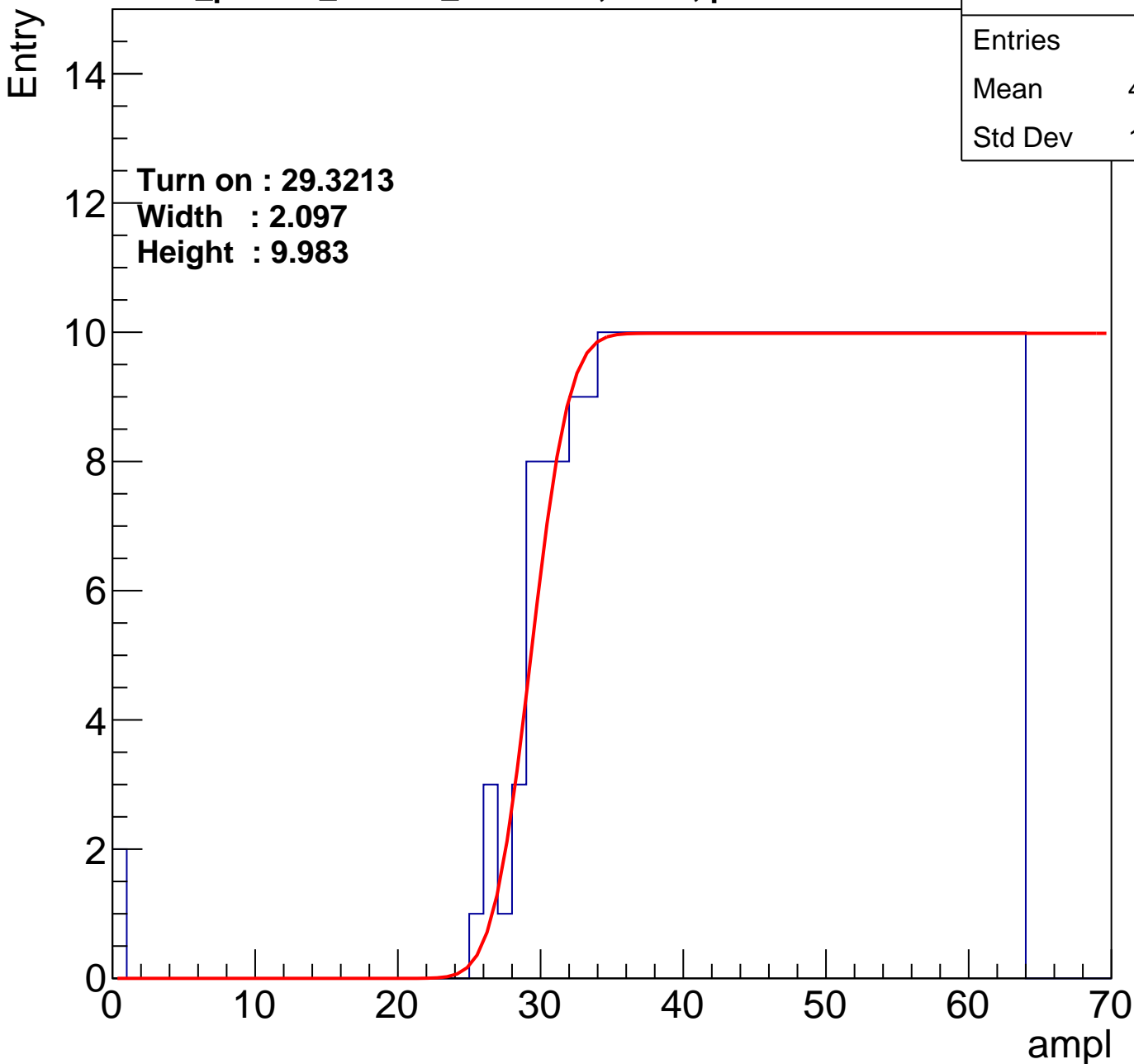


calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	352
Mean	45.65
Std Dev	10.79

Height : 9.983



B0L001S, U14-ch11

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 27.8296

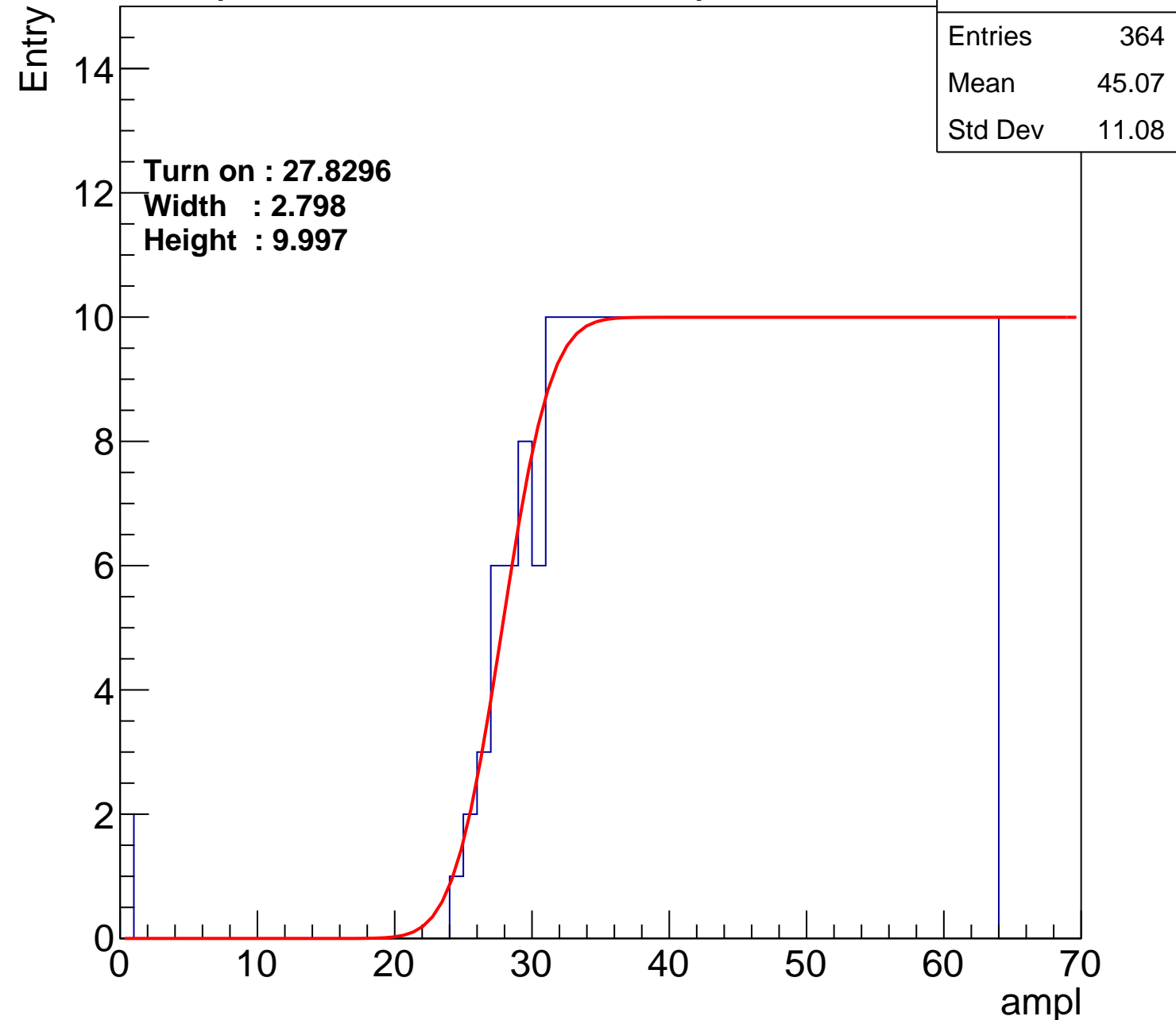
Width : 2.798

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch12

calib_packv5_042523_0143.root, FC#9, port A1

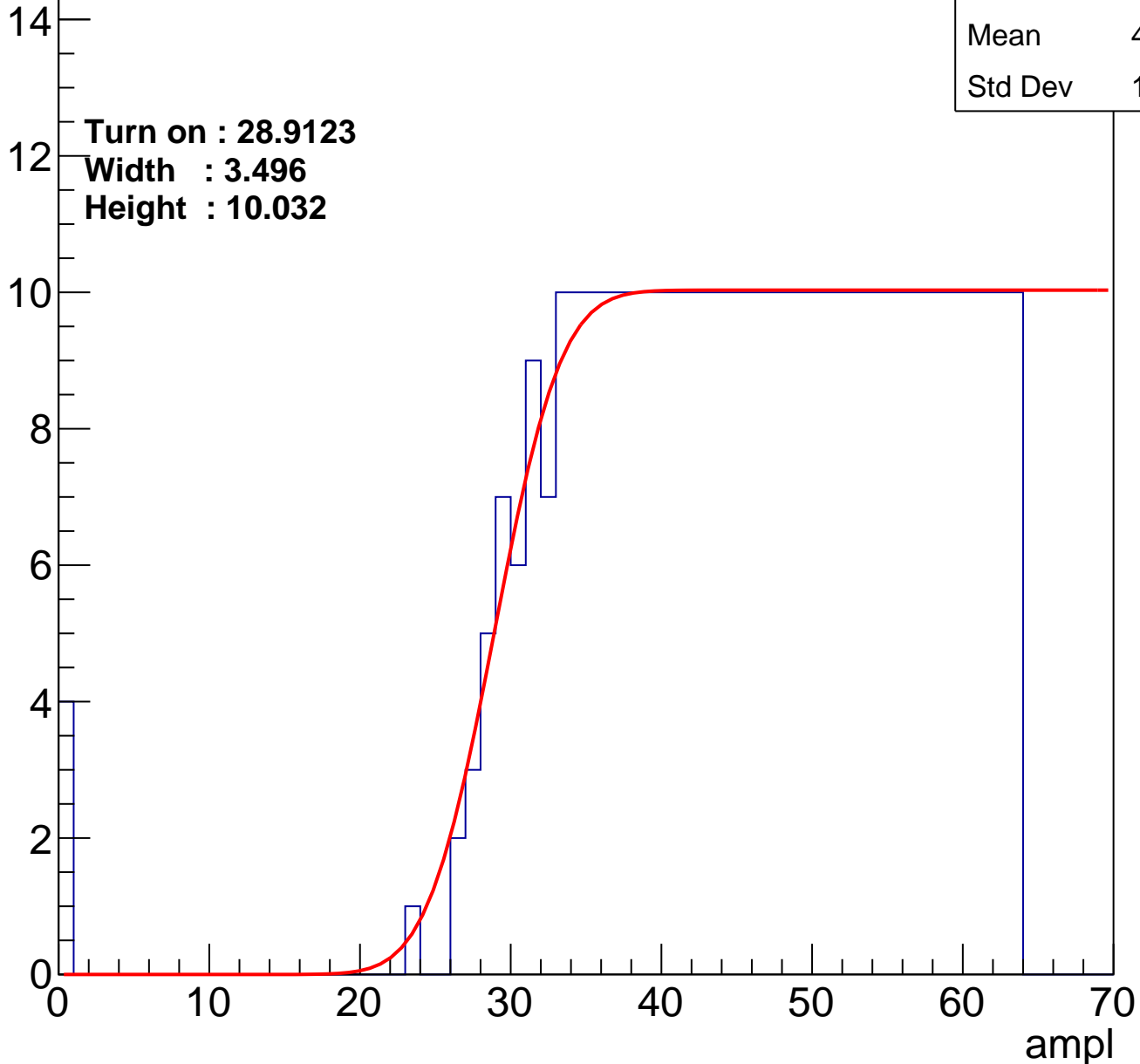
Entries	354
Mean	45.37
Std Dev	11.32

Turn on : 28.9123

Width : 3.496

Height : 10.032

Entry



B0L001S, U14-ch13

calib_packv5_042523_0143.root, FC#9, port A1

Entries	347
Mean	45.73
Std Dev	11.14

Turn on : 29.7370

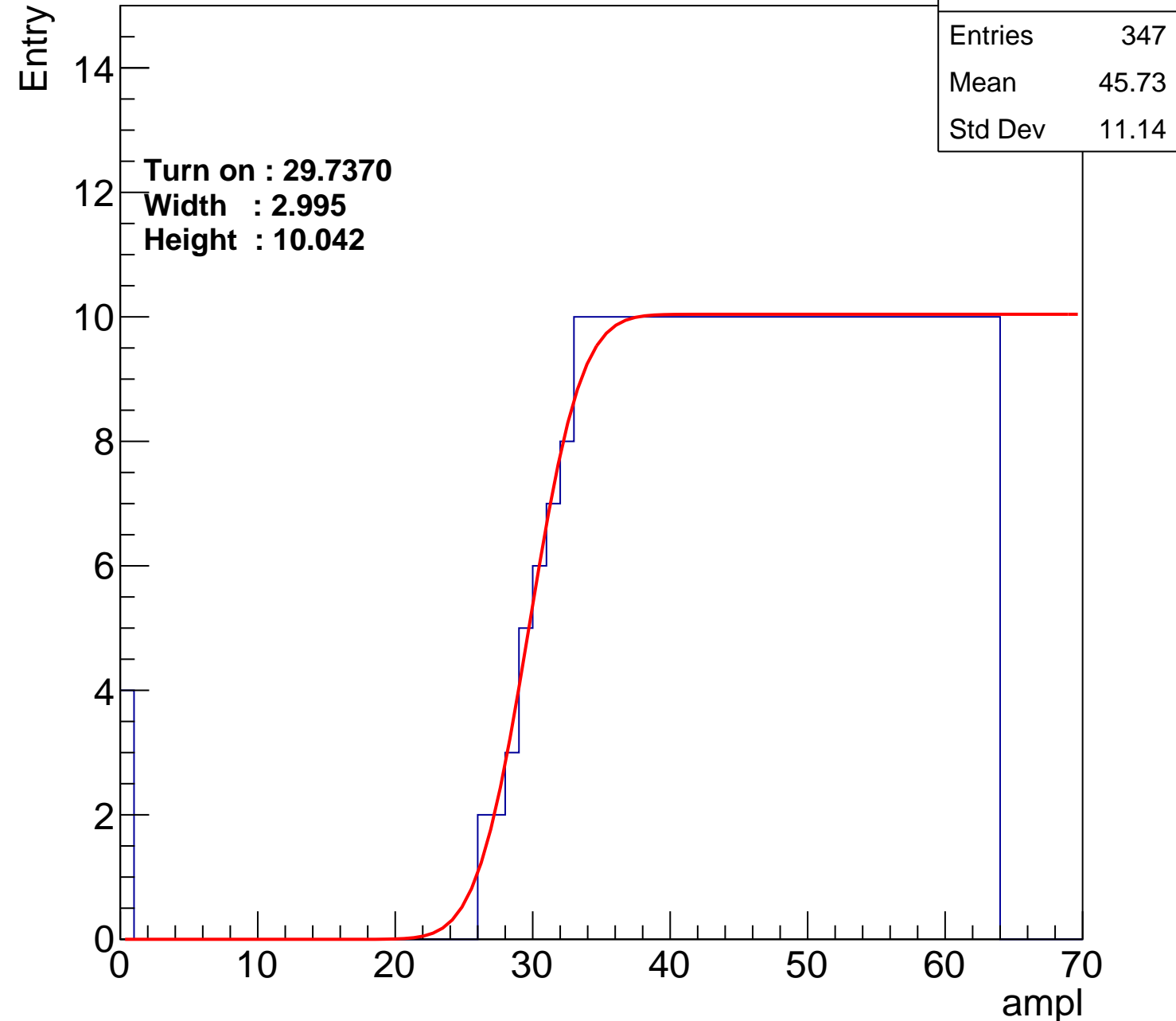
Width : 2.995

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch14

calib_packv5_042523_0143.root, FC#9, port A1

Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 26.0307

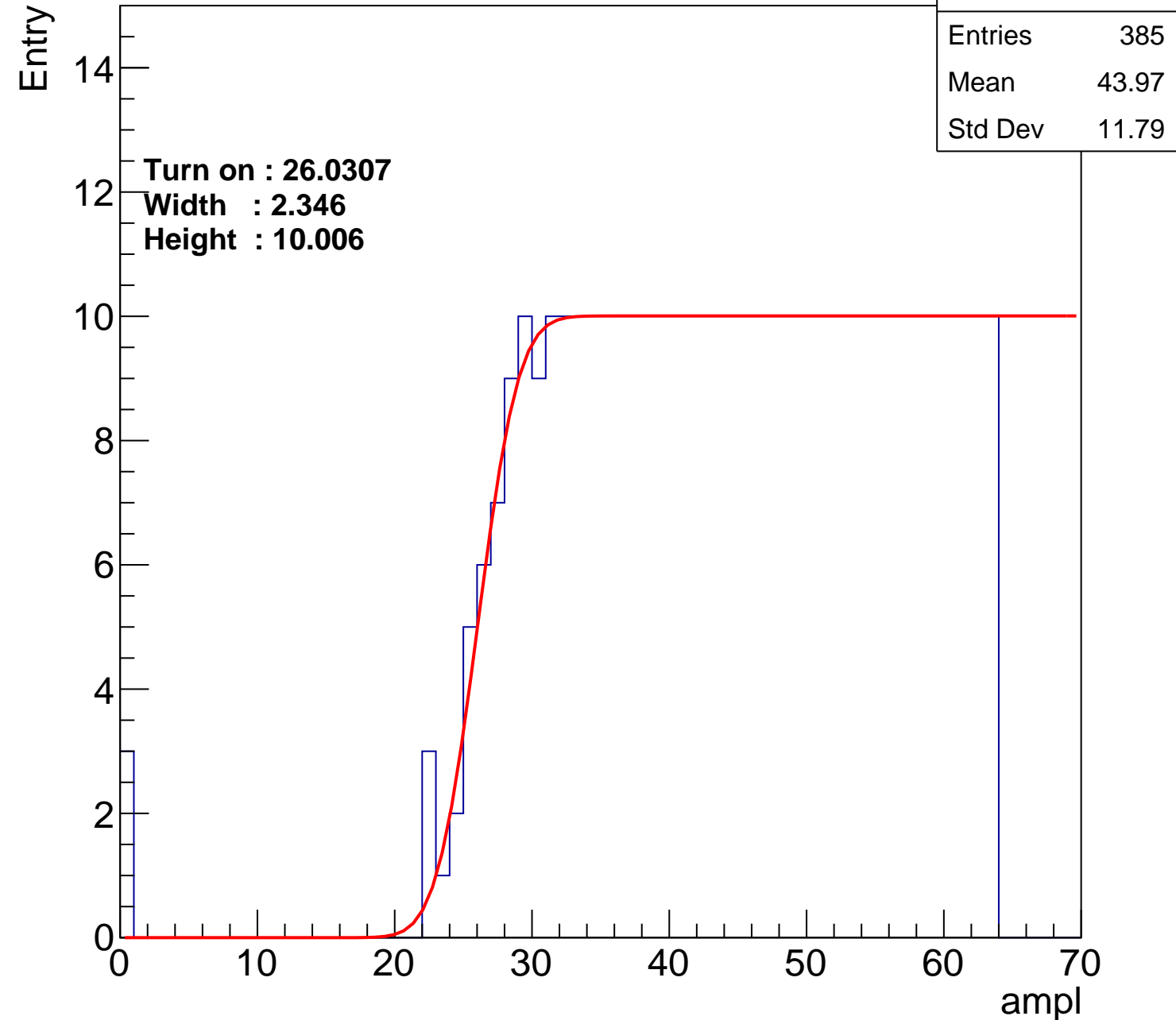
Width : 2.346

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch15

calib_packv5_042523_0143.root, FC#9, port A1

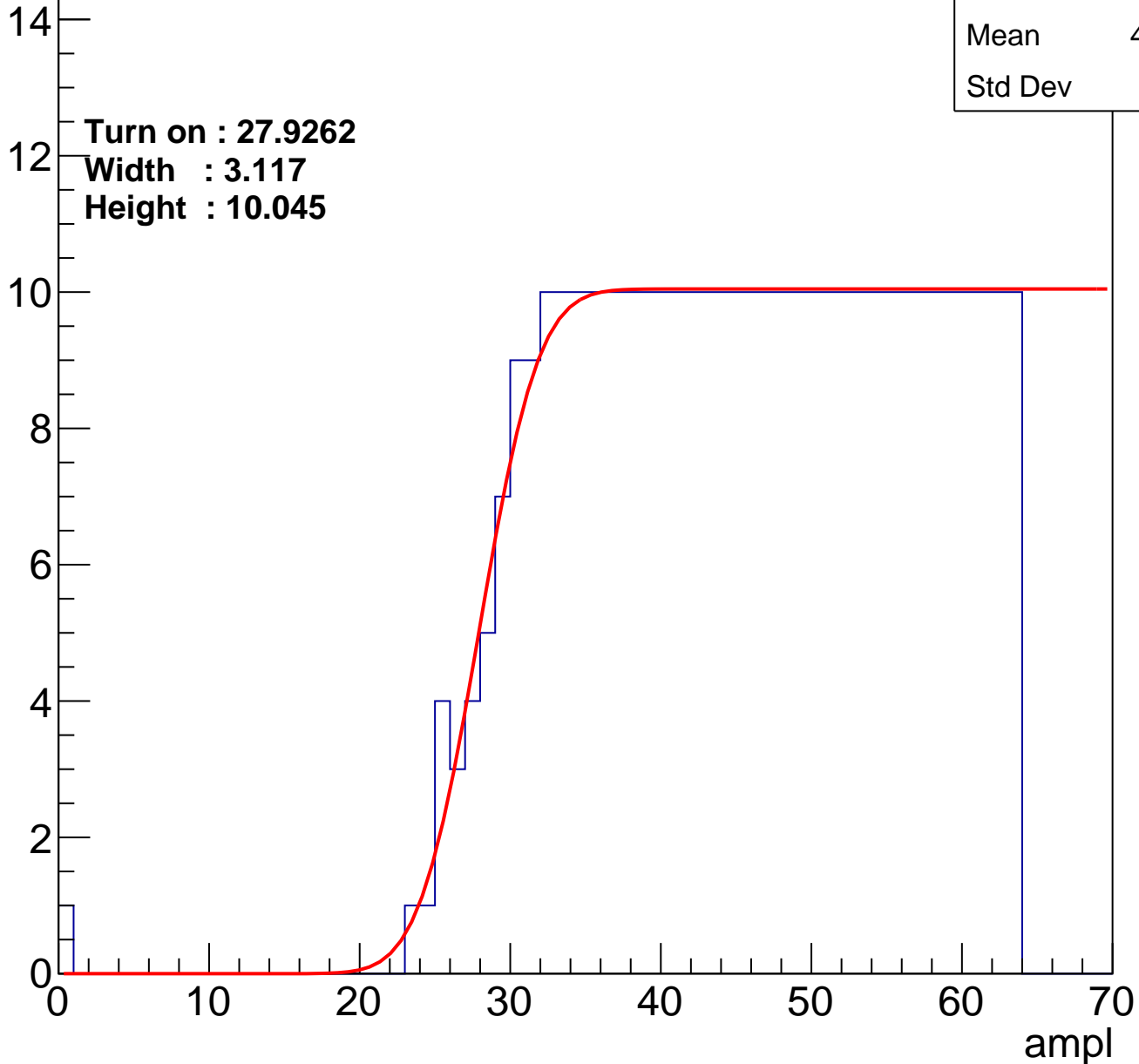
Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 27.9262

Width : 3.117

Height : 10.045

Entry



B0L001S, U14-ch16

calib_packv5_042523_0143.root, FC#9, port A1

Entries	352
Mean	45.53
Std Dev	11.09

Turn on : 28.8332

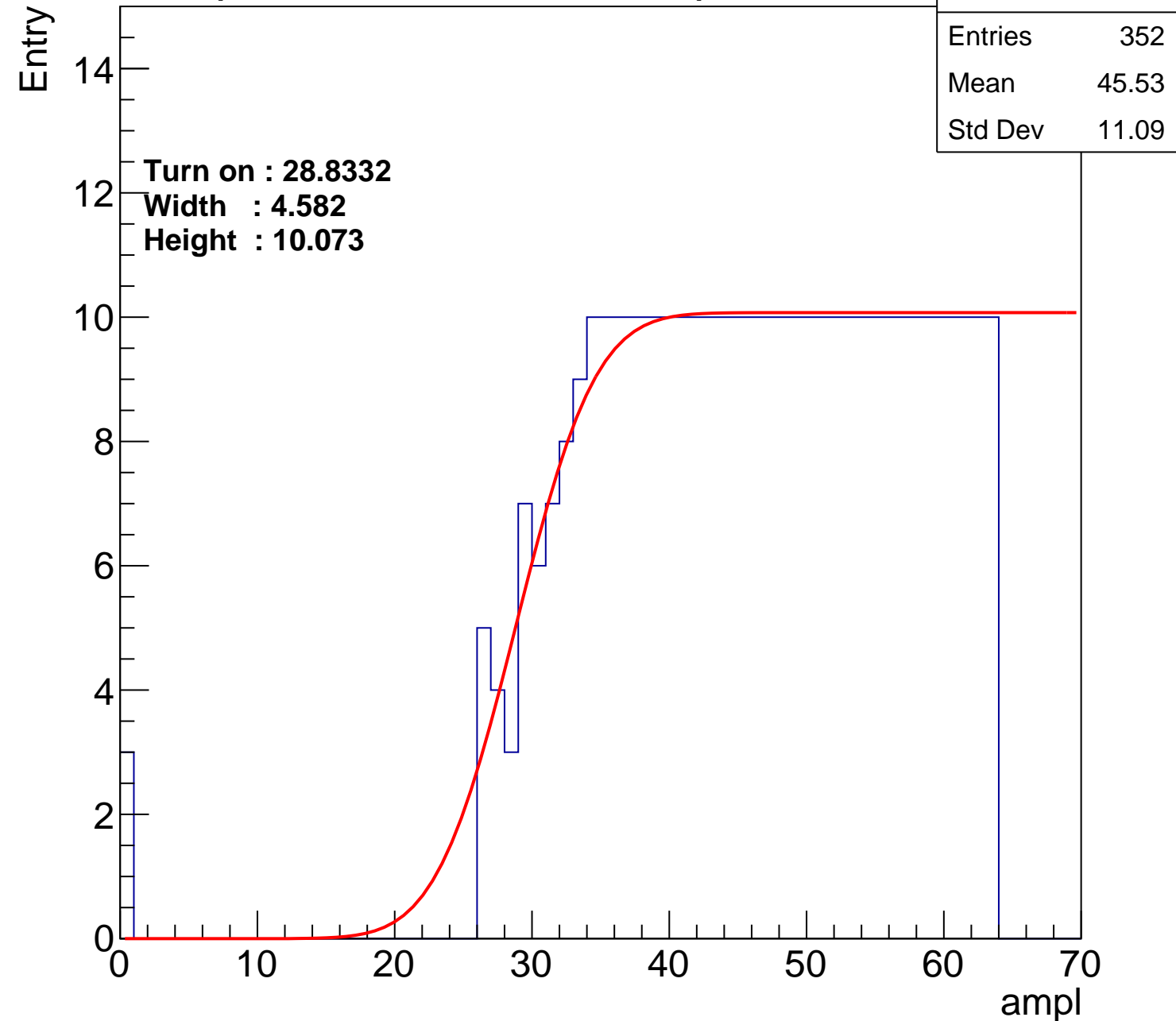
Width : 4.582

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch17

calib_packv5_042523_0143.root, FC#9, port A1

Entries	358
Mean	45.19
Std Dev	11.38

Turn on : 28.6245

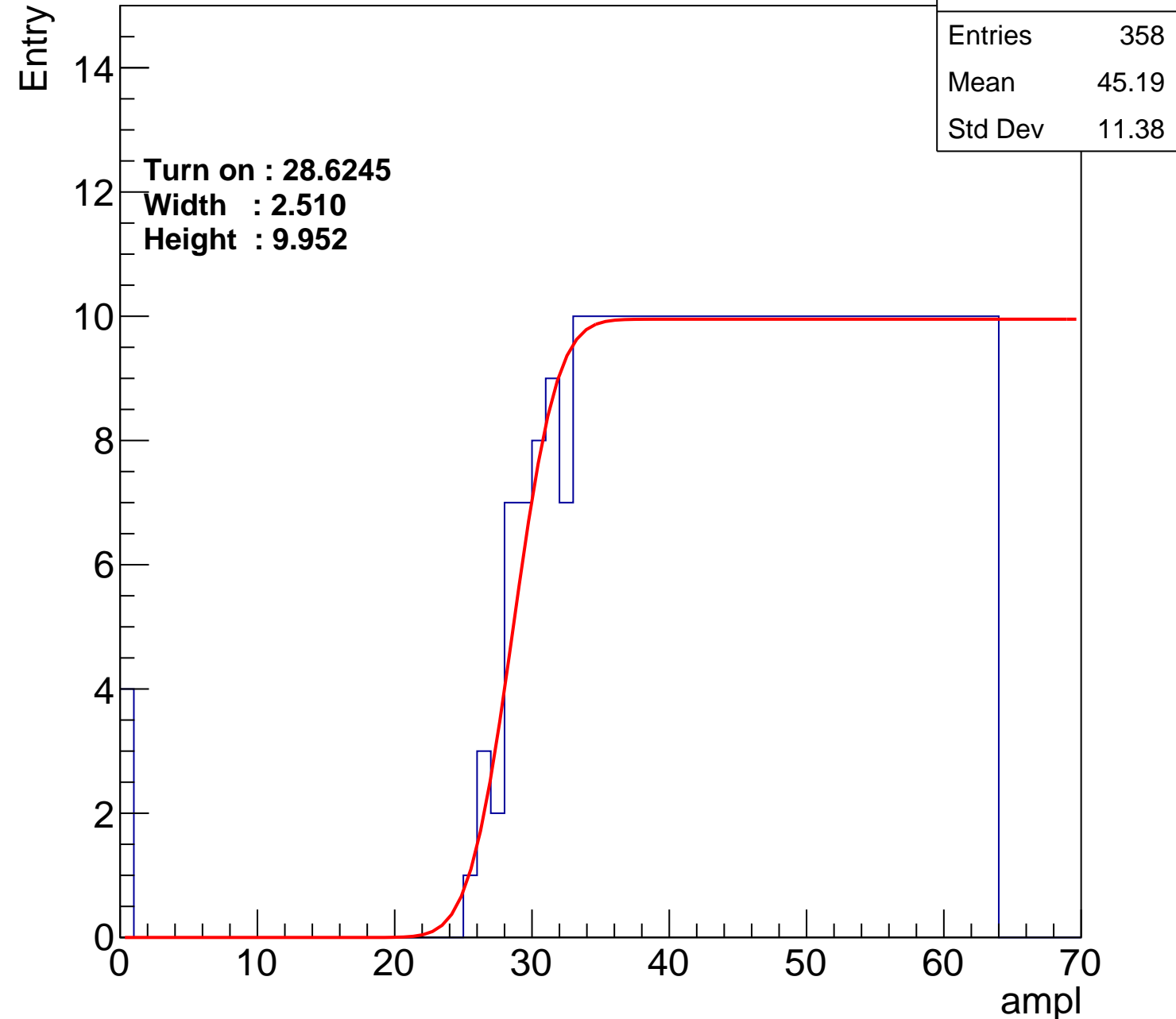
Width : 2.510

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch18

calib_packv5_042523_0143.root, FC#9, port A1

Entries	376
Mean	44.45
Std Dev	11.42

Turn on : 26.4375

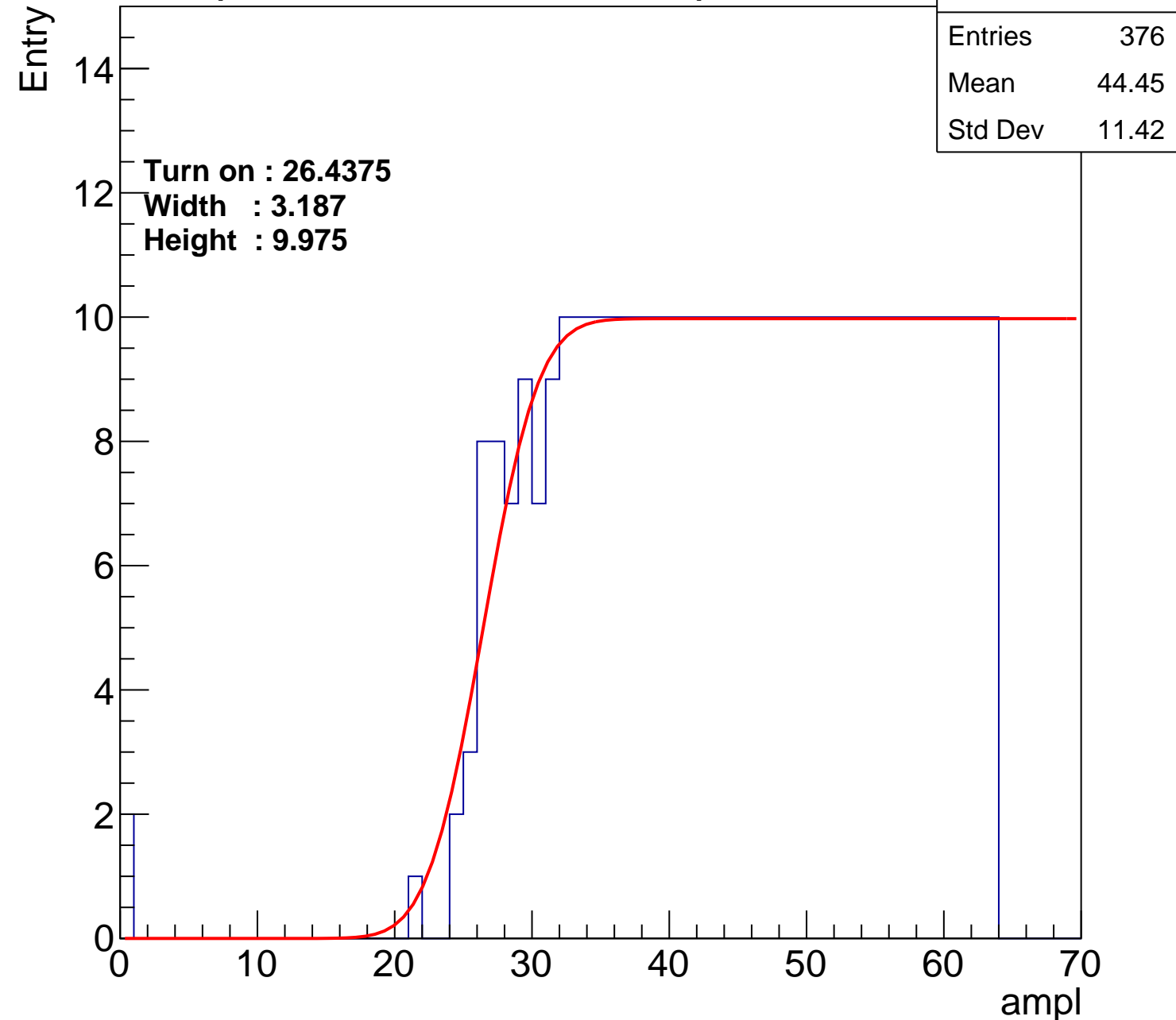
Width : 3.187

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch19

calib_packv5_042523_0143.root, FC#9, port A1

Entries	381
Mean	44.16
Std Dev	11.71

Turn on : 26.7542

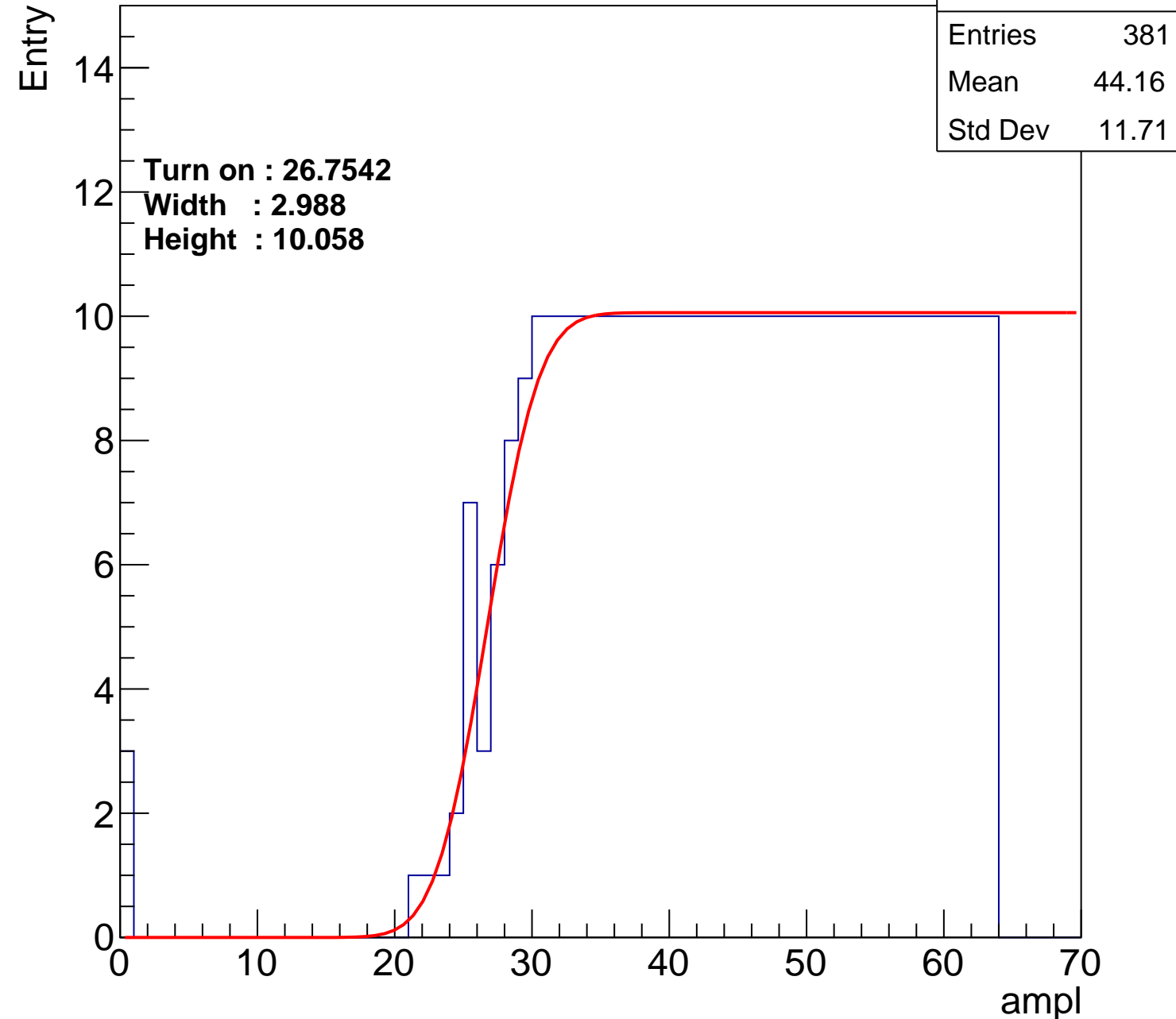
Width : 2.988

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch20

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.08
Std Dev	11.49

Turn on : 28.6942

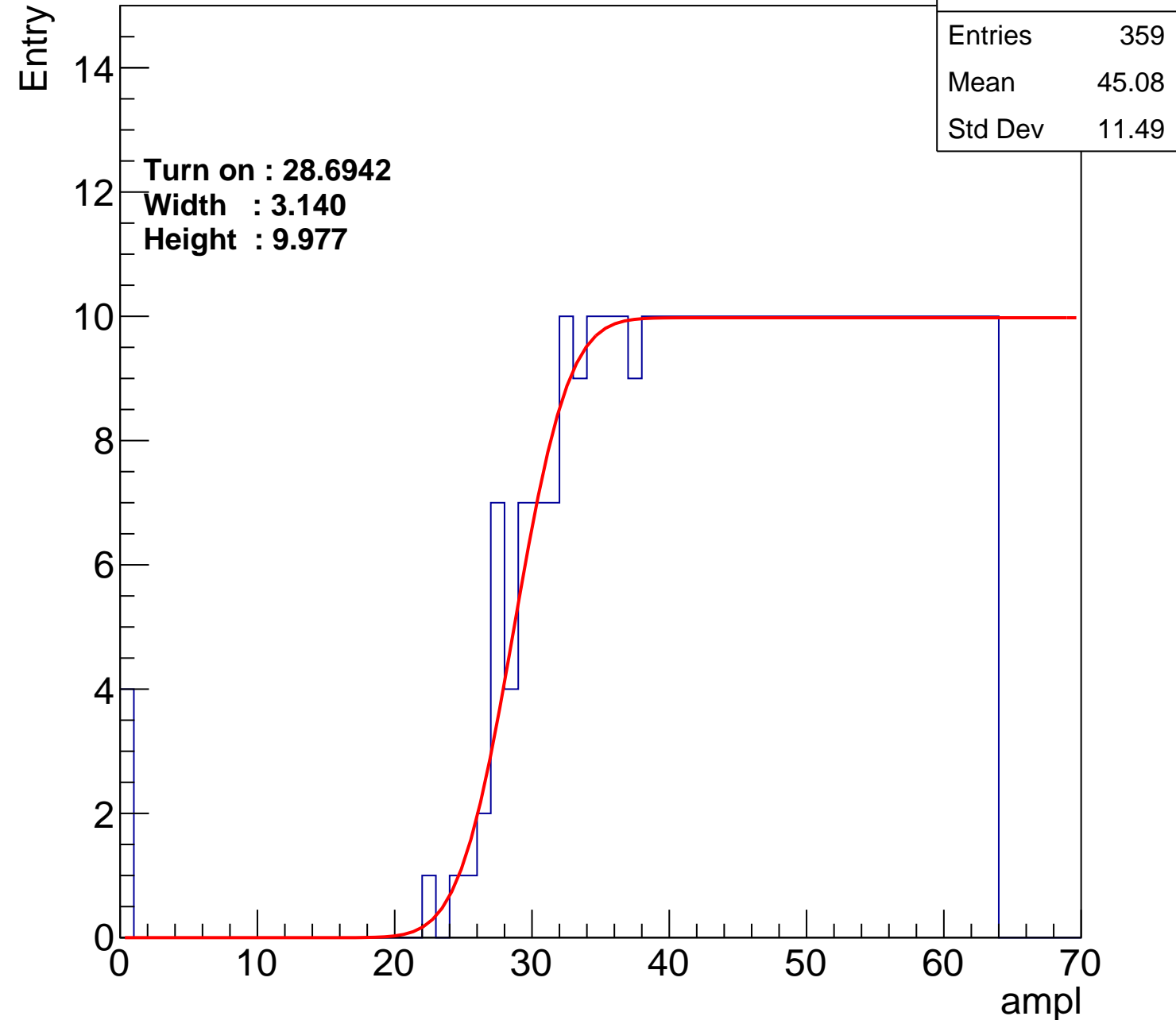
Width : 3.140

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch21

calib_packv5_042523_0143.root, FC#9, port A1

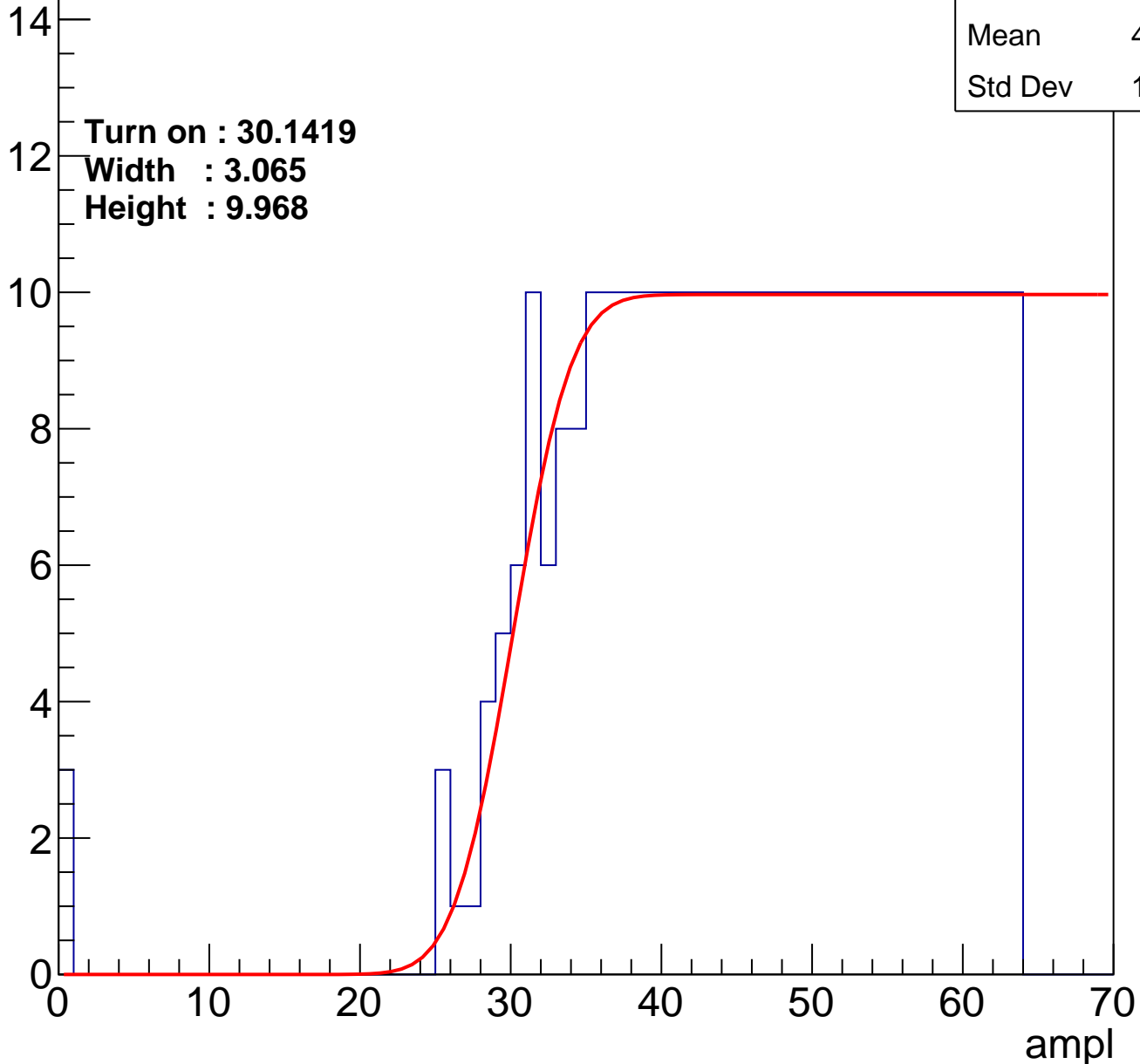
Entries	345
Mean	45.83
Std Dev	10.97

Turn on : 30.1419

Width : 3.065

Height : 9.968

Entry



B0L001S, U14-ch22

calib_packv5_042523_0143.root, FC#9, port A1

Entries	355
Mean	45.28
Std Dev	11.4

Turn on : 28.9553

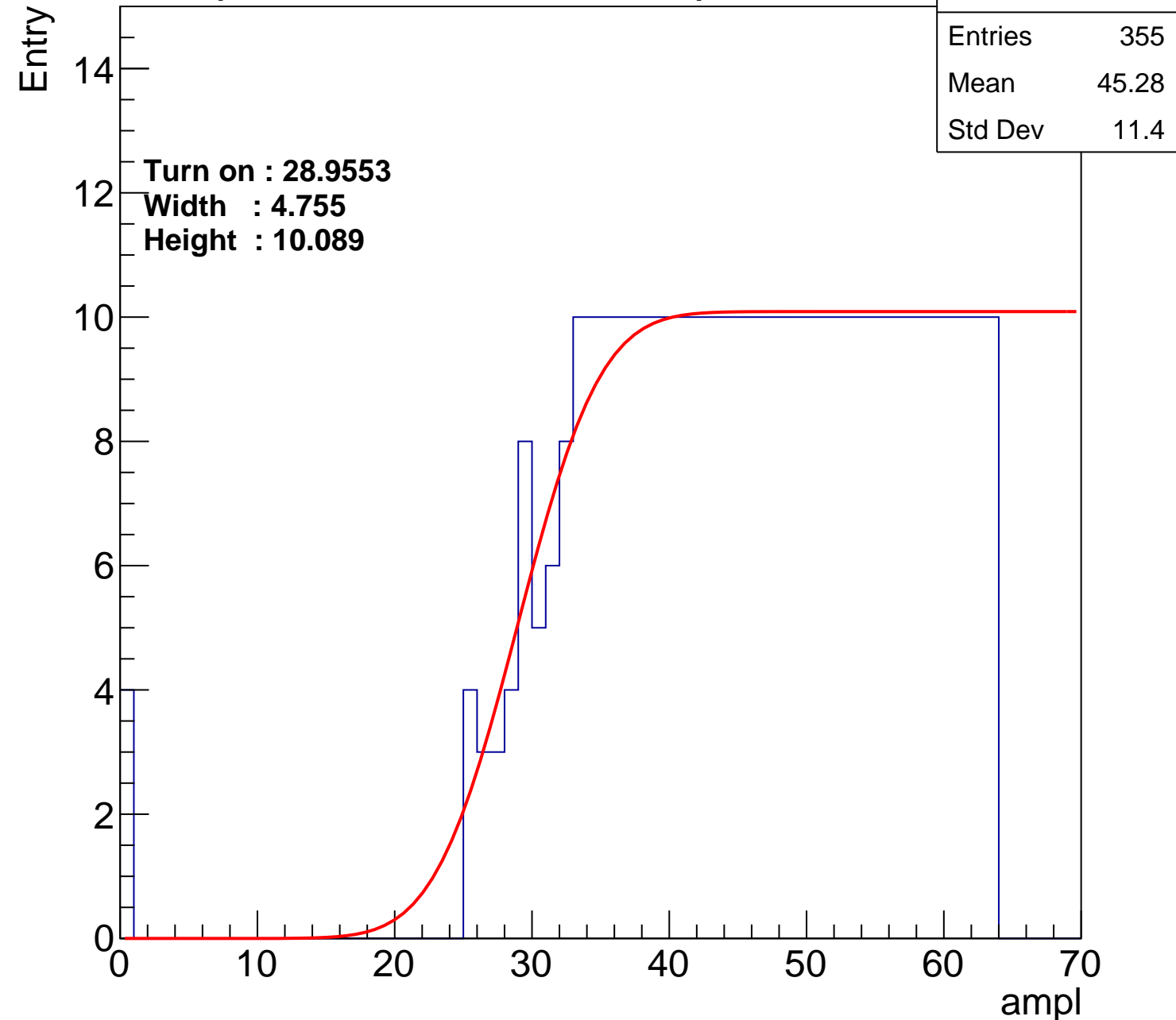
Width : 4.755

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch23

calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.1
Std Dev	11.22

Turn on : 28.4526

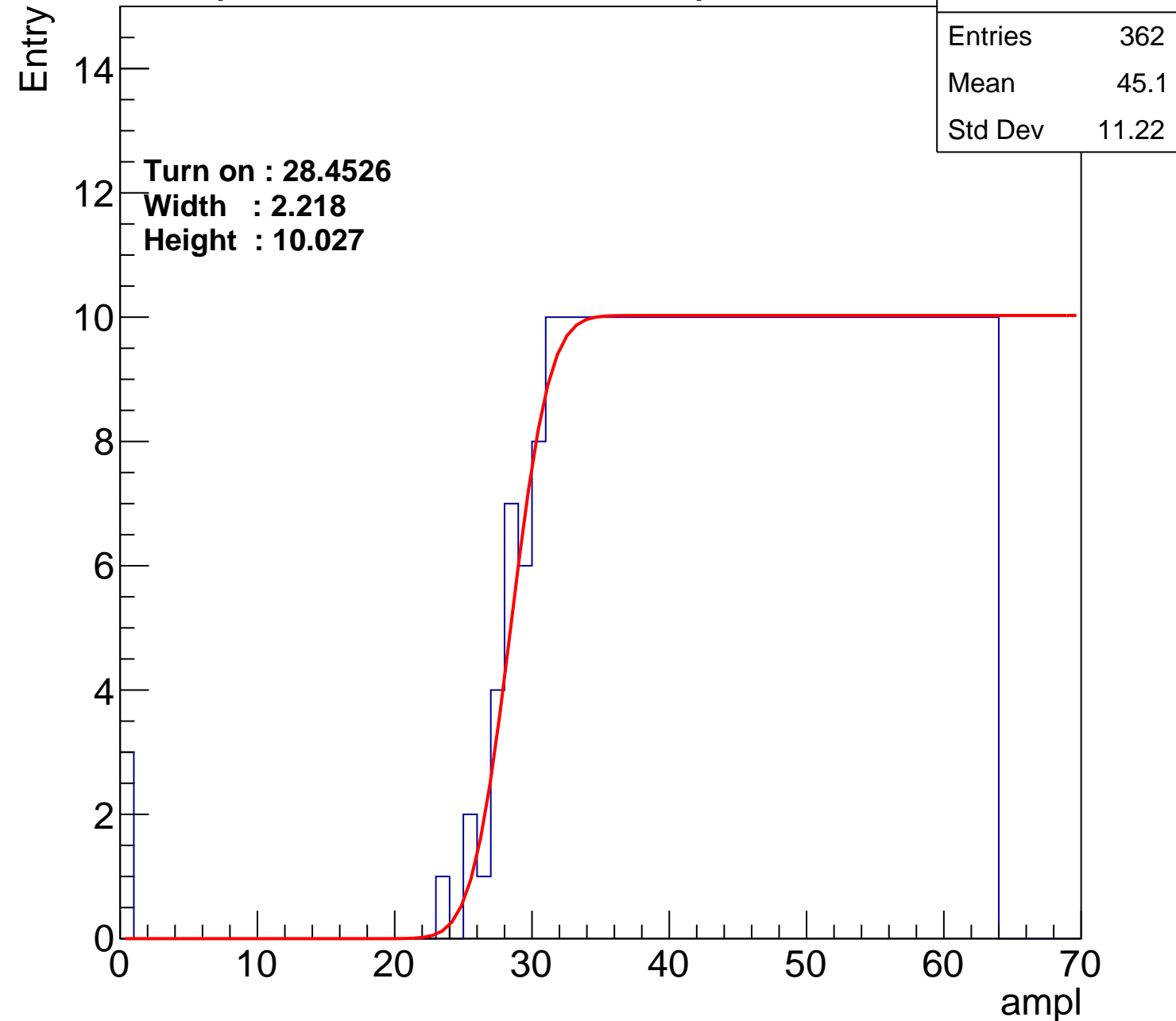
Width : 2.218

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch24

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.18
Std Dev	11.89

Turn on : 26.1047

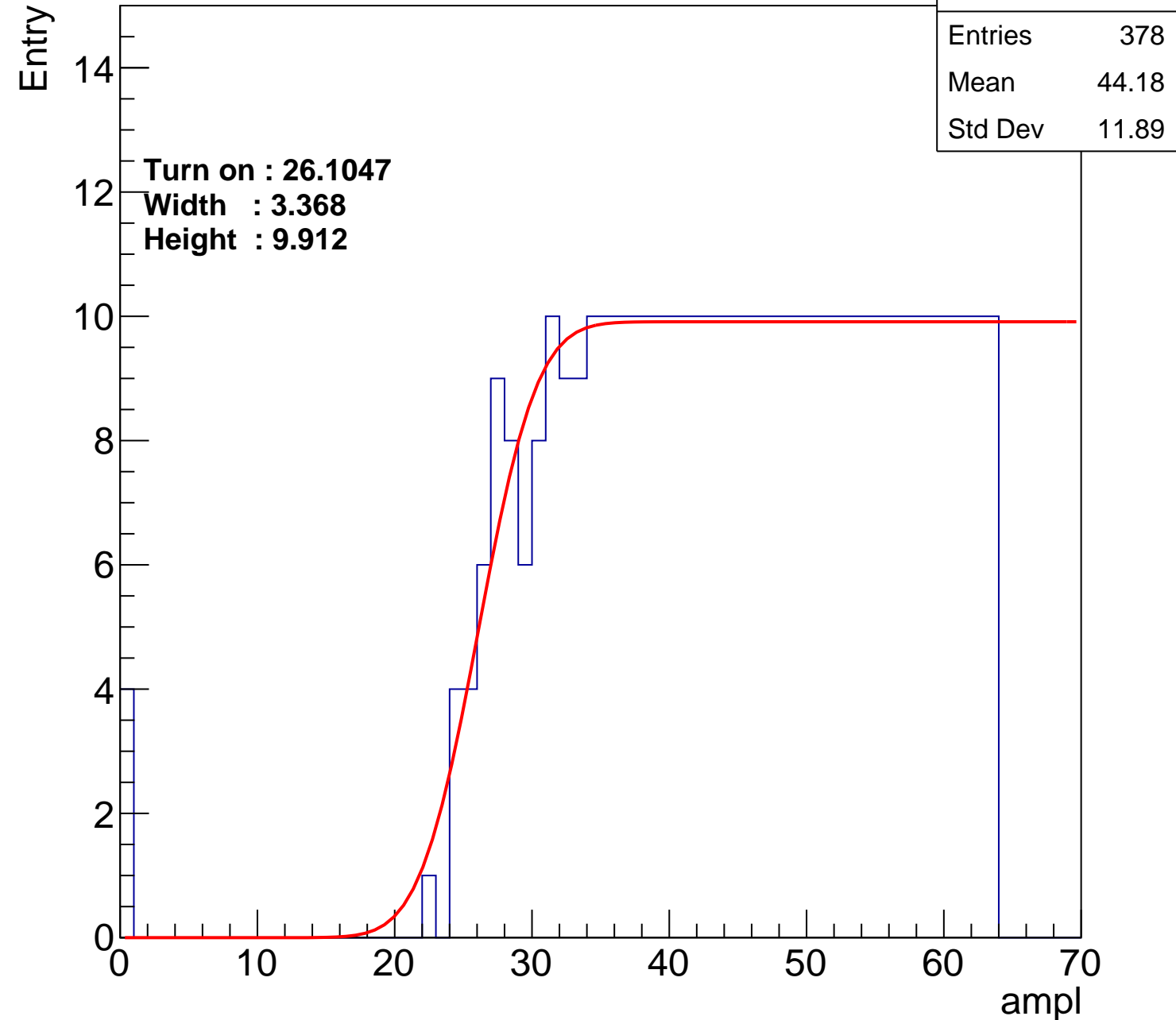
Width : 3.368

Height : 9.912

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch25

calib_packv5_042523_0143.root, FC#9, port A1

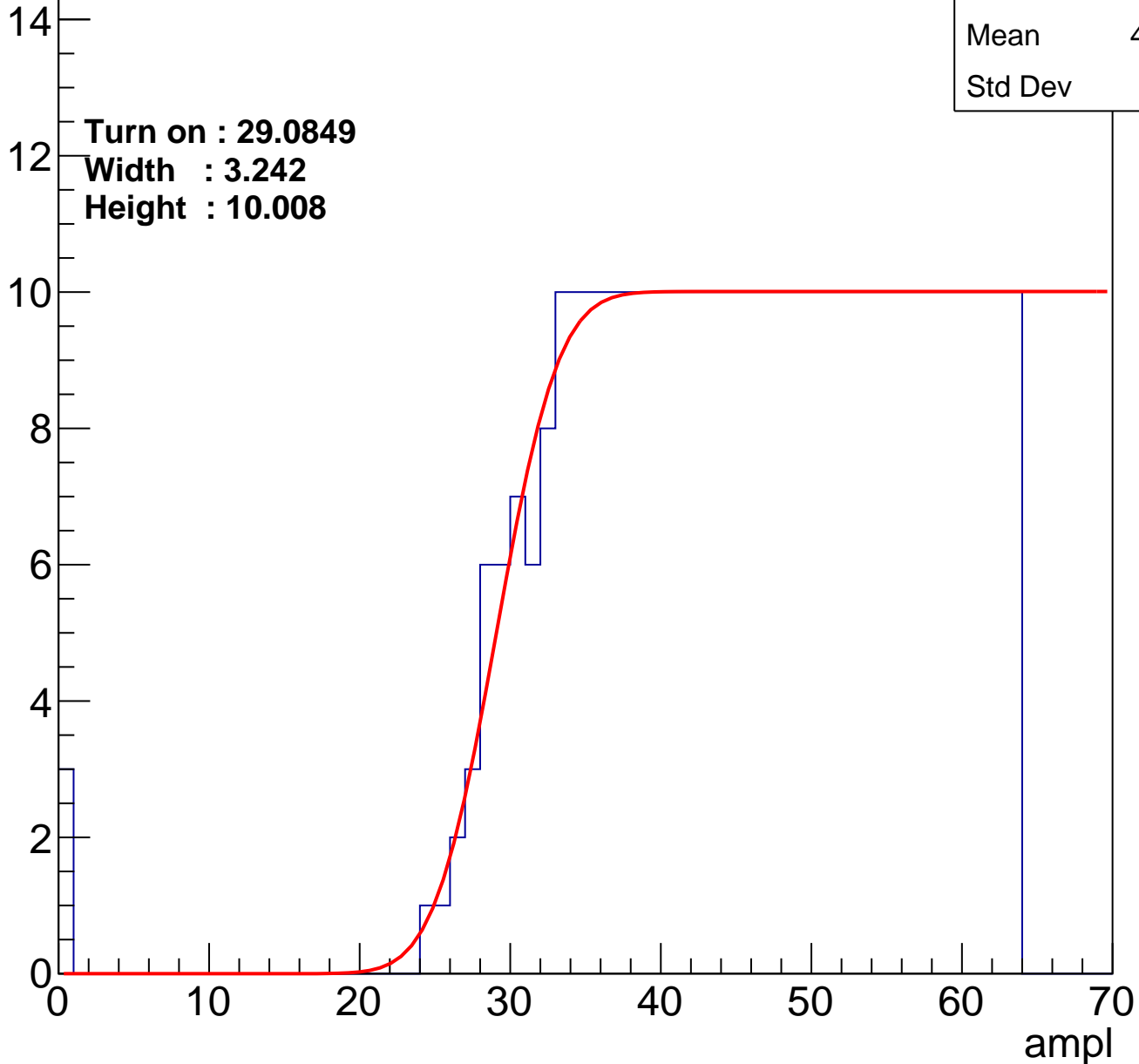
Entries	353
Mean	45.48
Std Dev	11.1

Turn on : 29.0849

Width : 3.242

Height : 10.008

Entry



B0L001S, U14-ch26

calib_packv5_042523_0143.root, FC#9, port A1

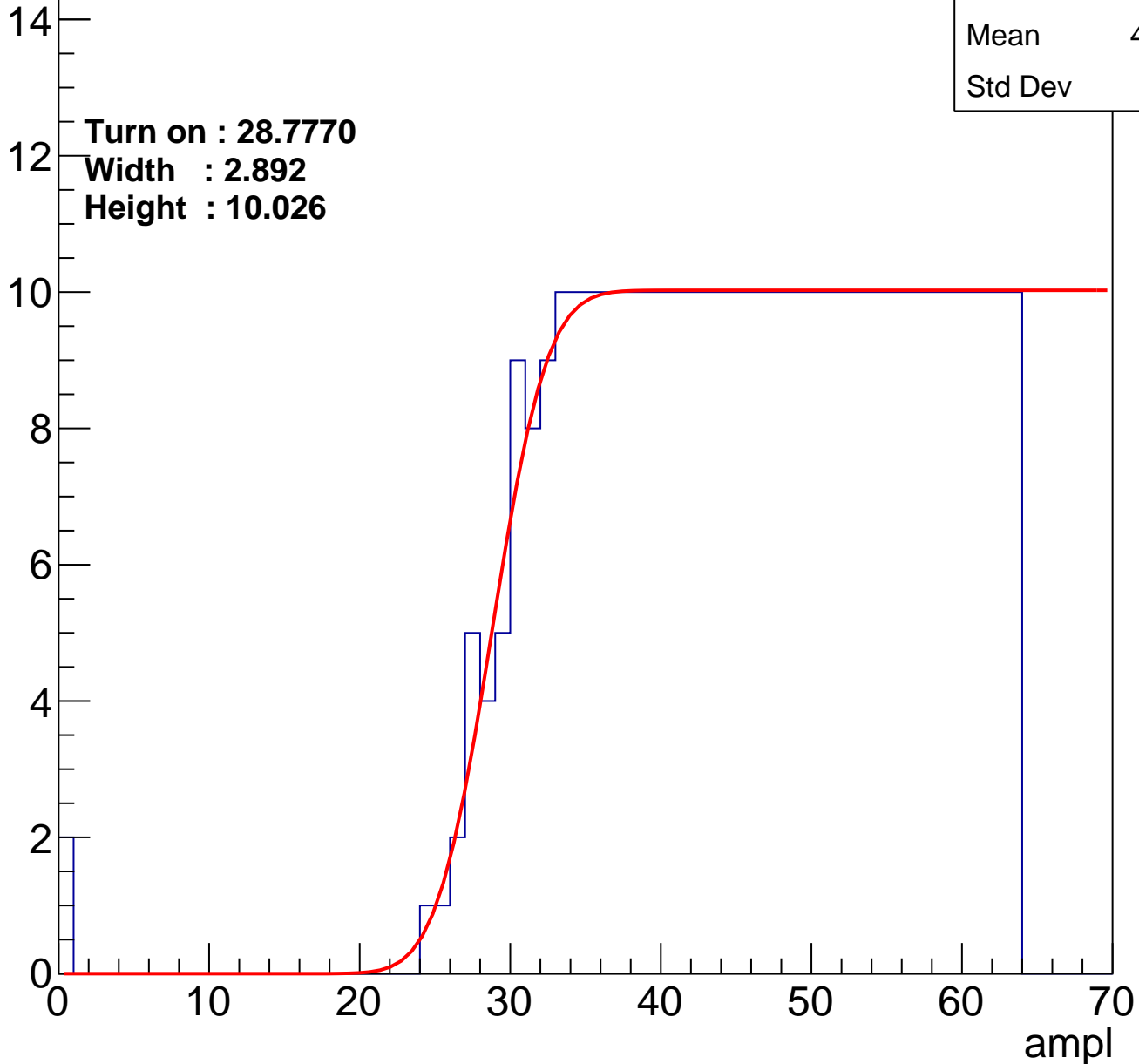
Entries	356
Mean	45.45
Std Dev	10.9

Turn on : 28.7770

Width : 2.892

Height : 10.026

Entry



B0L001S, U14-ch27

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.29
Std Dev	10.99

Turn on : 27.7406

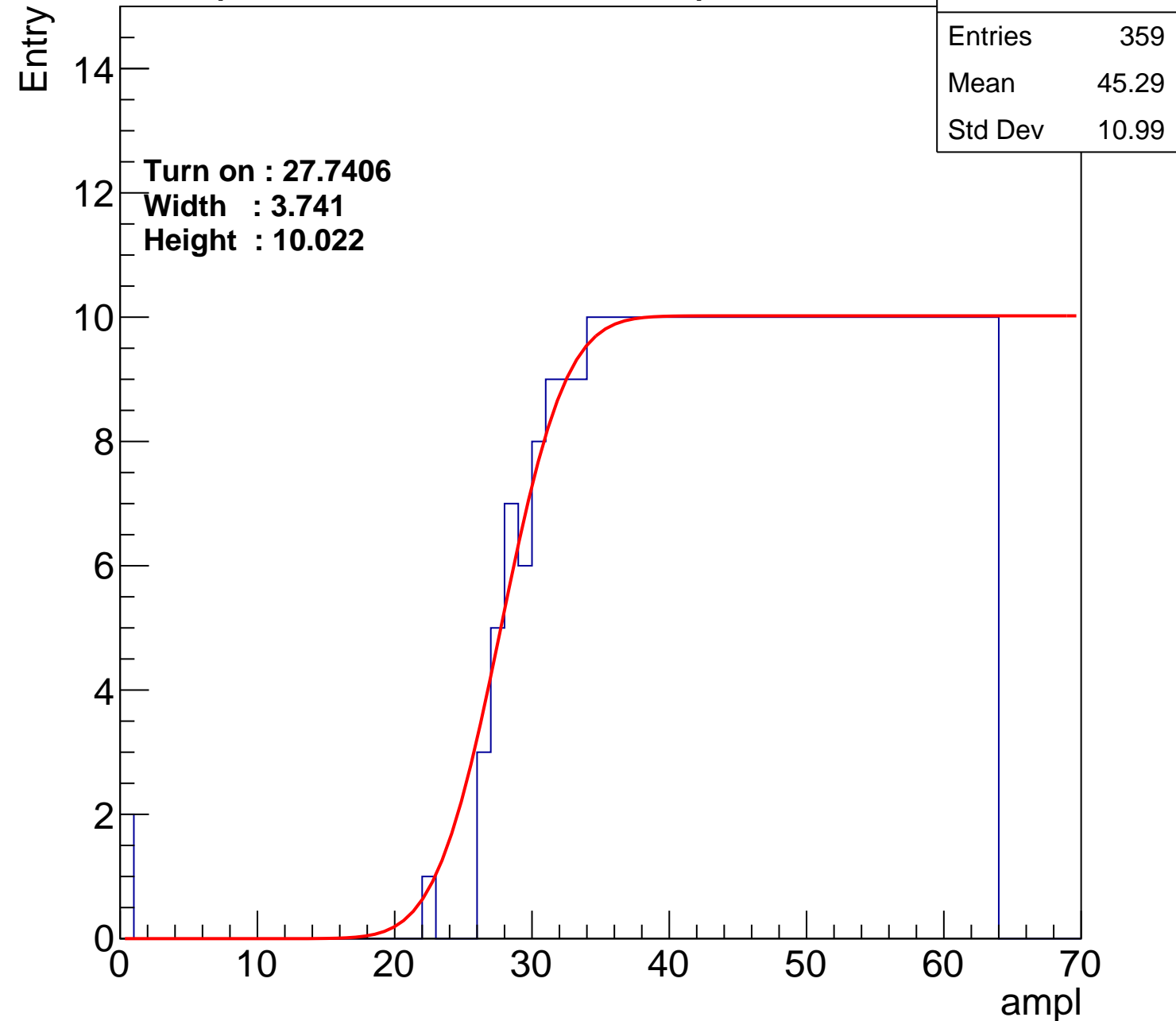
Width : 3.741

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch28

calib_packv5_042523_0143.root, FC#9, port A1

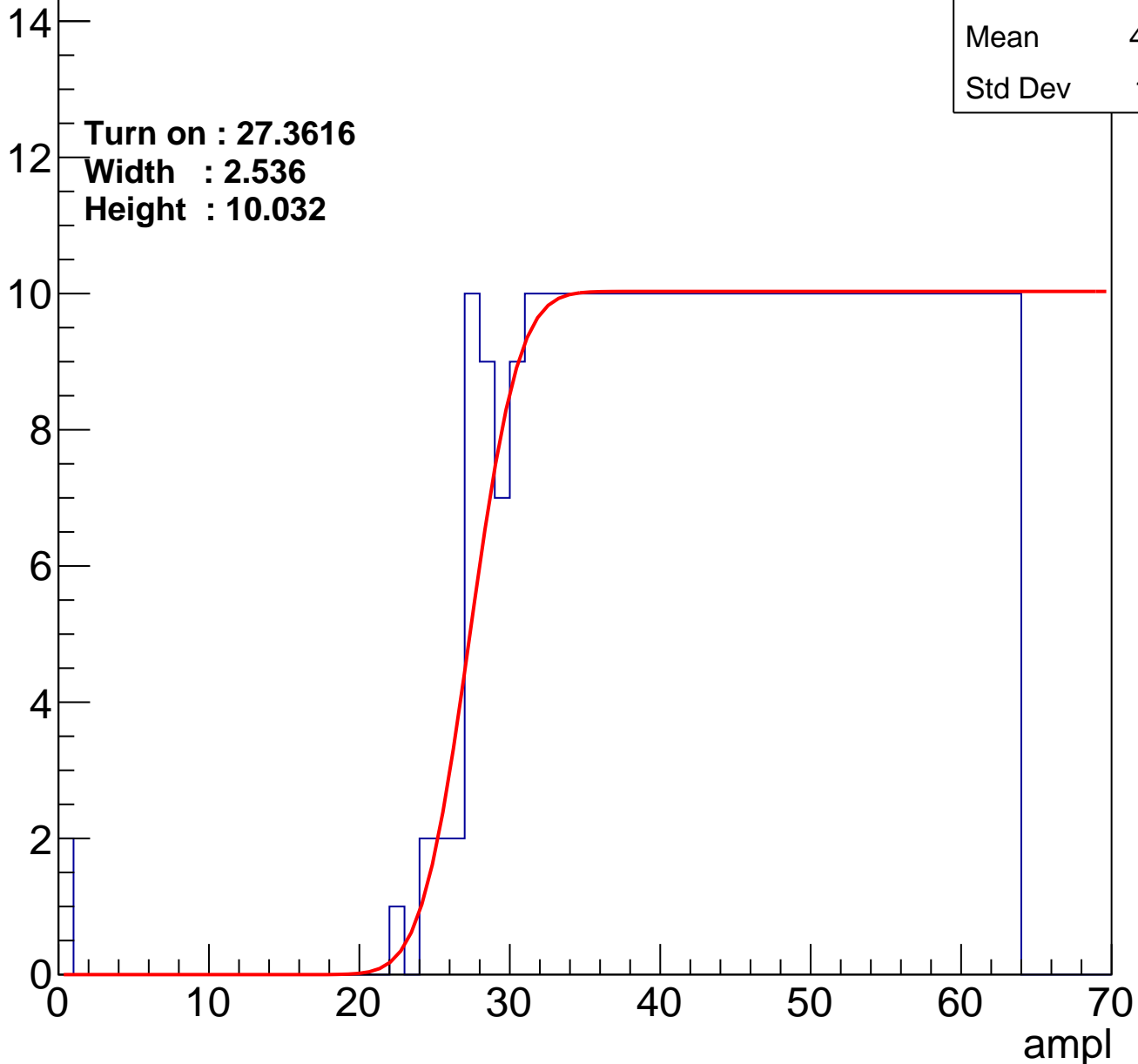
Entries	374
Mean	44.59
Std Dev	11.31

Turn on : 27.3616

Width : 2.536

Height : 10.032

Entry



B0L001S, U14-ch29

calib_packv5_042523_0143.root, FC#9, port A1

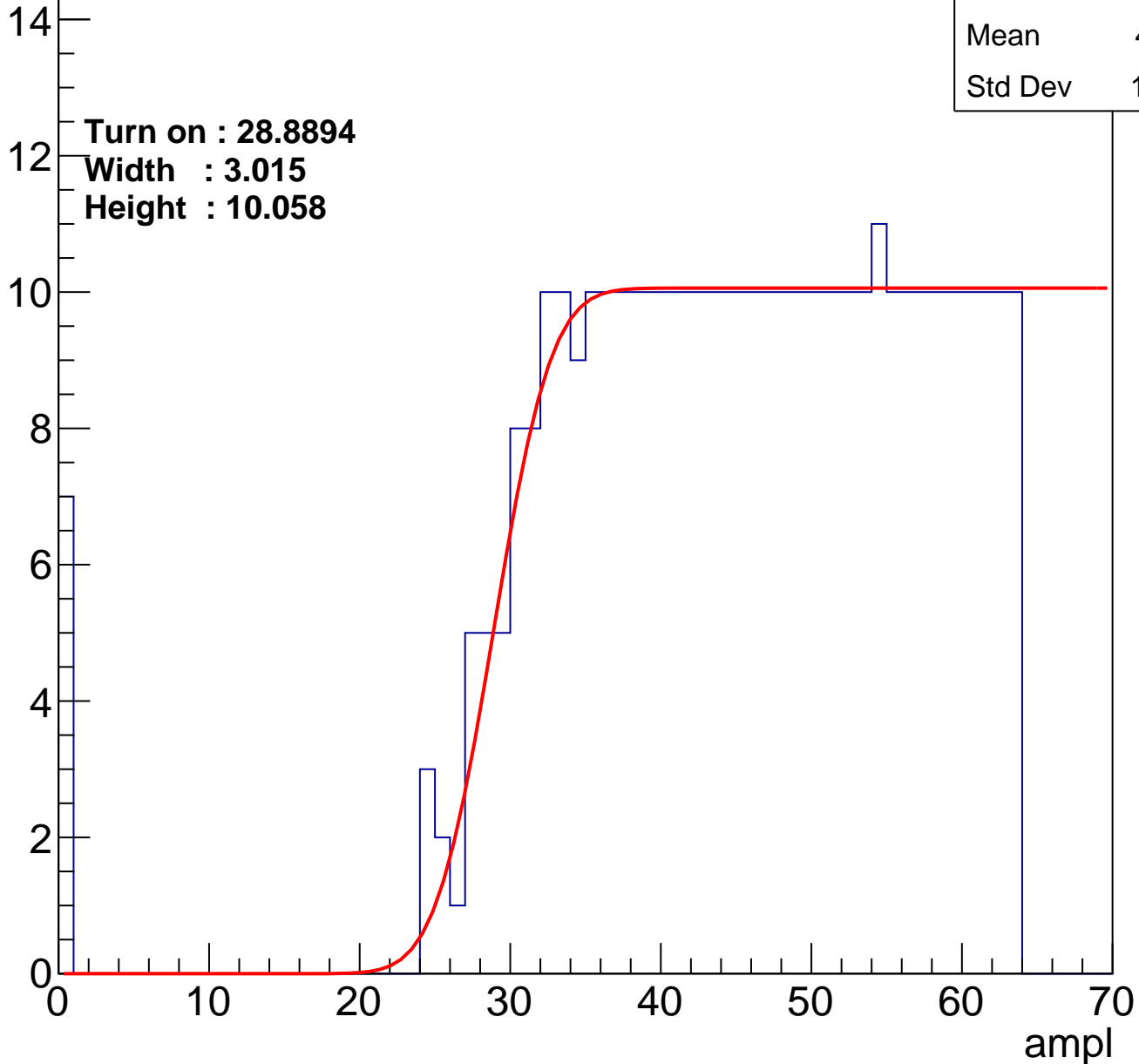
Entries	364
Mean	44.71
Std Dev	12.13

Turn on : 28.8894

Width : 3.015

Height : 10.058

Entry



B0L001S, U14-ch30

calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.29
Std Dev	11.24

Turn on : 28.9388

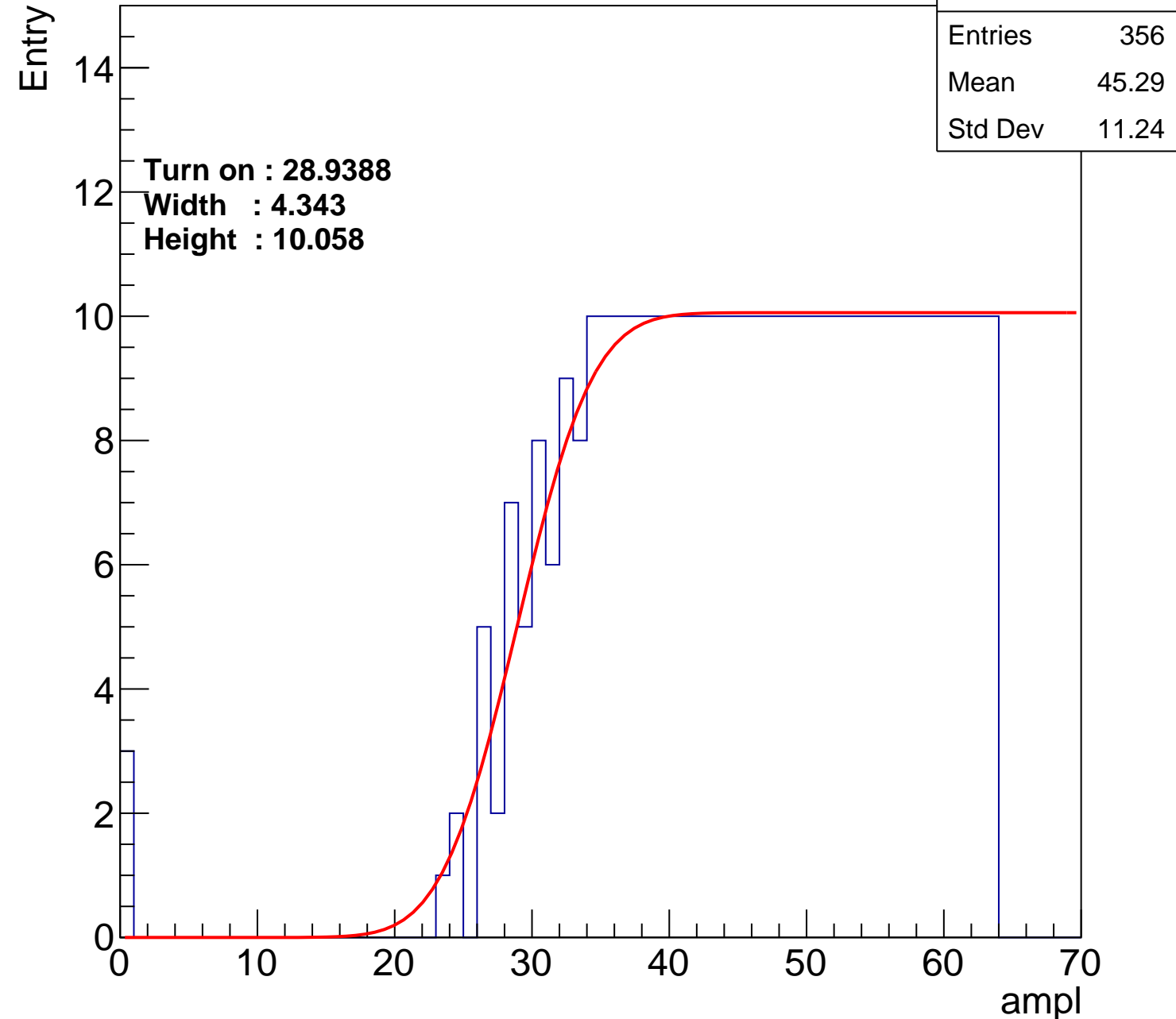
Width : 4.343

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch31

calib_packv5_042523_0143.root, FC#9, port A1

Entries	361
Mean	44.96
Std Dev	11.68

Turn on : 28.1776

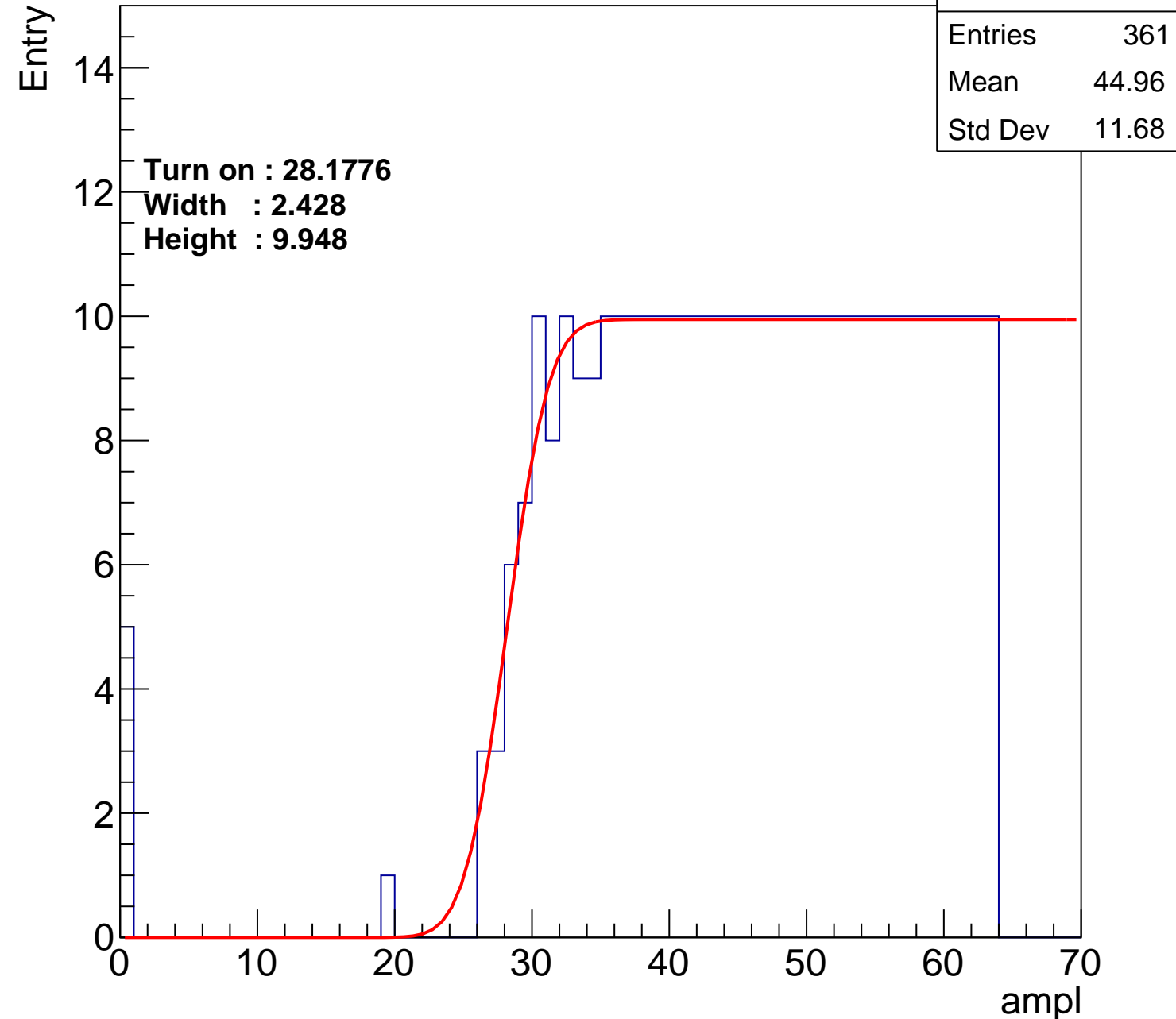
Width : 2.428

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch32

calib_packv5_042523_0143.root, FC#9, port A1

Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.1861

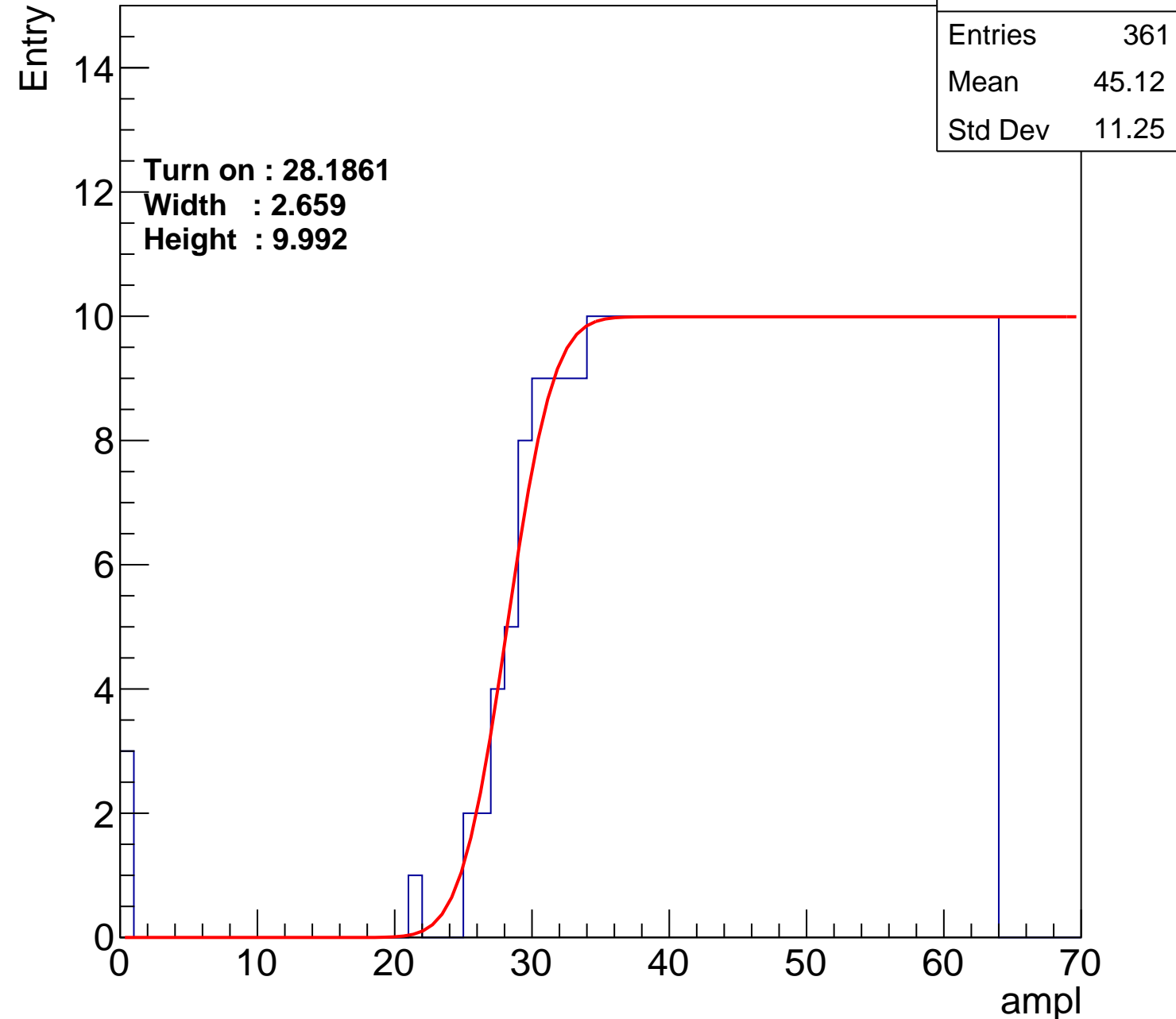
Width : 2.659

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch33

calib_packv5_042523_0143.root, FC#9, port A1

Entries	374
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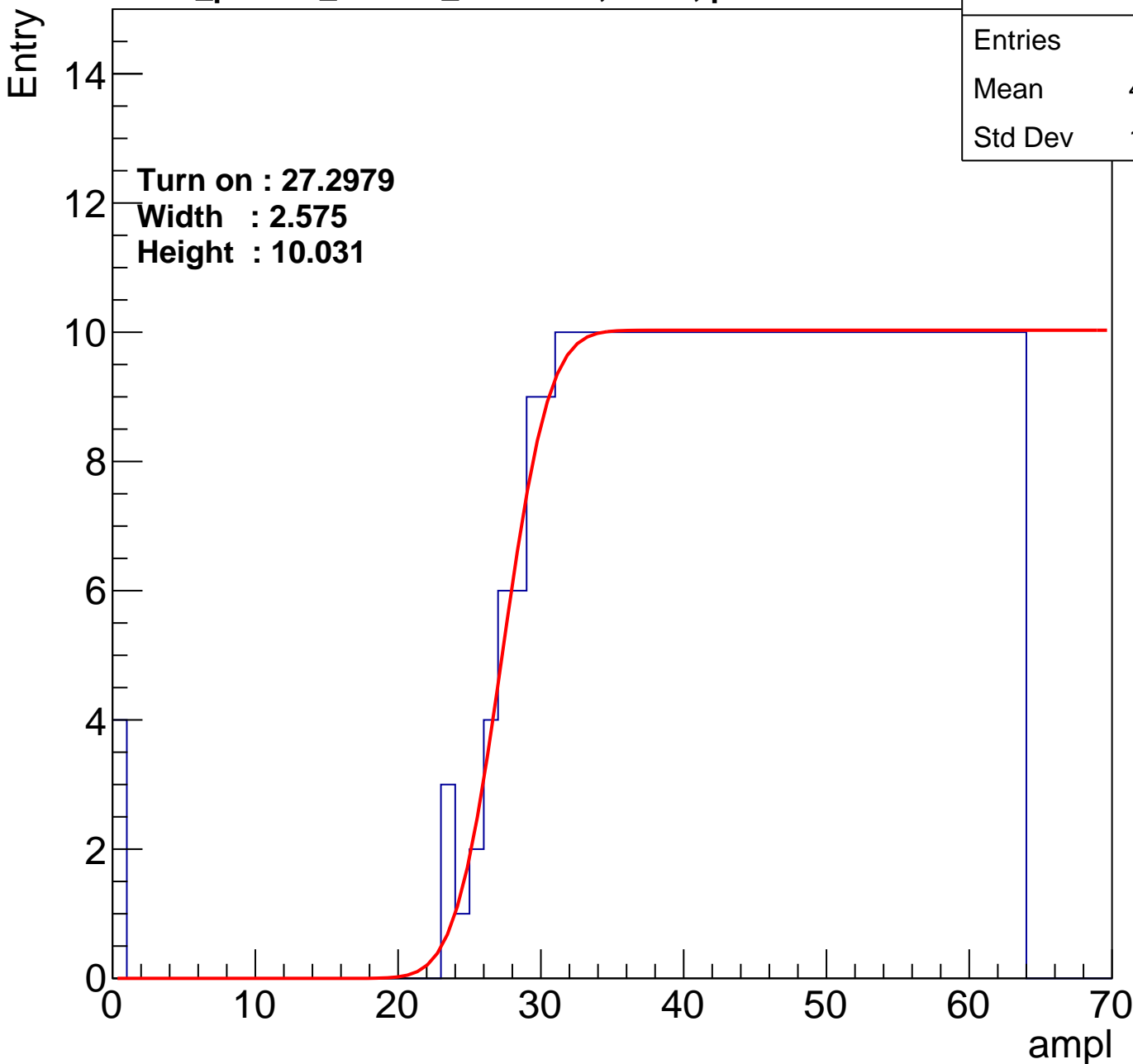
Mean	44.43
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Std Dev	11.72
---------	-------

Turn on : 27.2979

Width : 2.575

Height : 10.031



B0L001S, U14-ch34

calib_packv5_042523_0143.root, FC#9, port A1

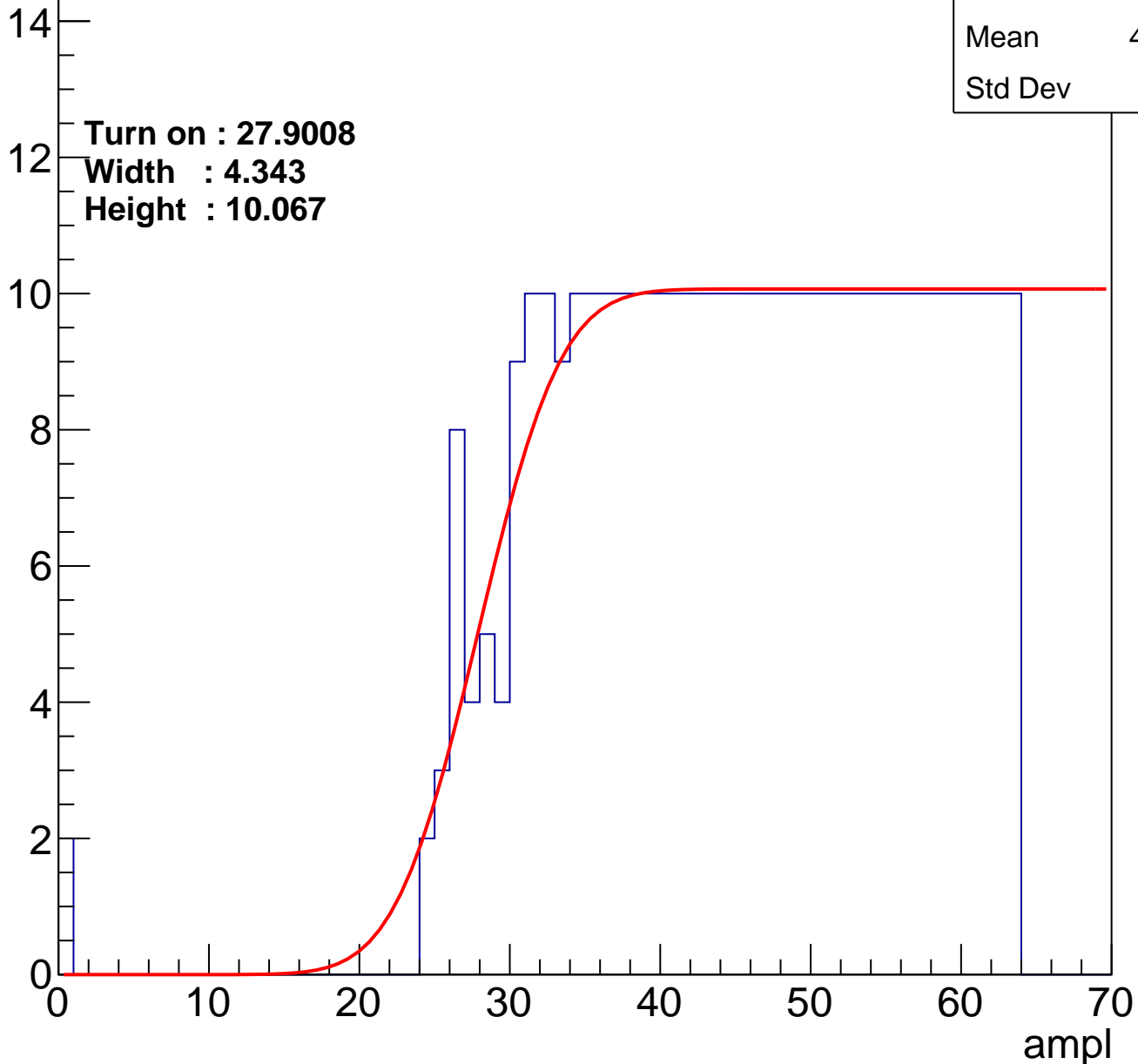
Entries	366
Mean	44.92
Std Dev	11.2

Turn on : 27.9008

Width : 4.343

Height : 10.067

Entry



B0L001S, U14-ch35

calib_packv5_042523_0143.root, FC#9, port A1

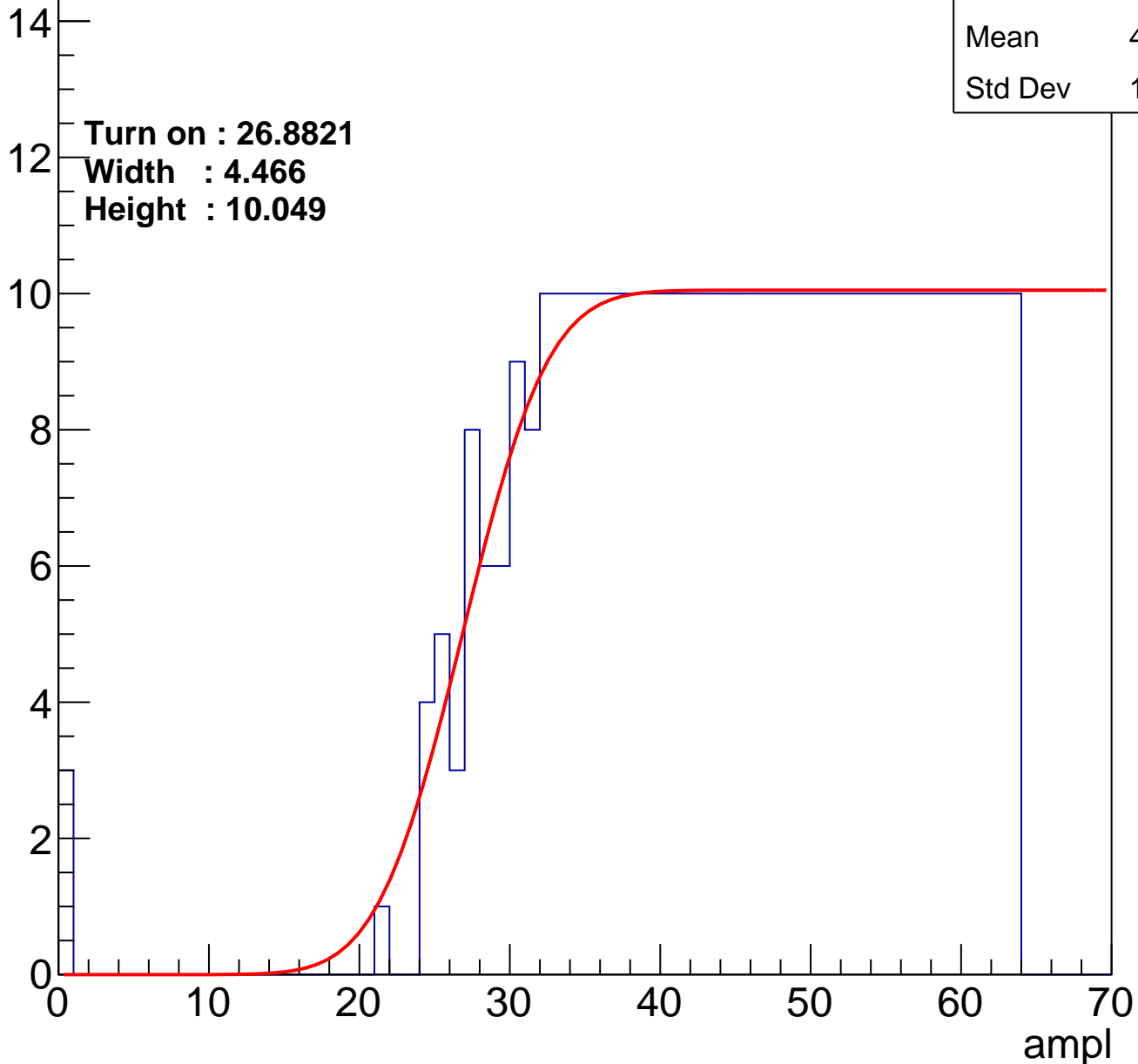
Entries	373
Mean	44.49
Std Dev	11.59

Turn on : 26.8821

Width : 4.466

Height : 10.049

Entry



B0L001S, U14-ch36

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	45.09
Std Dev	11.05

Turn on : 27.5143

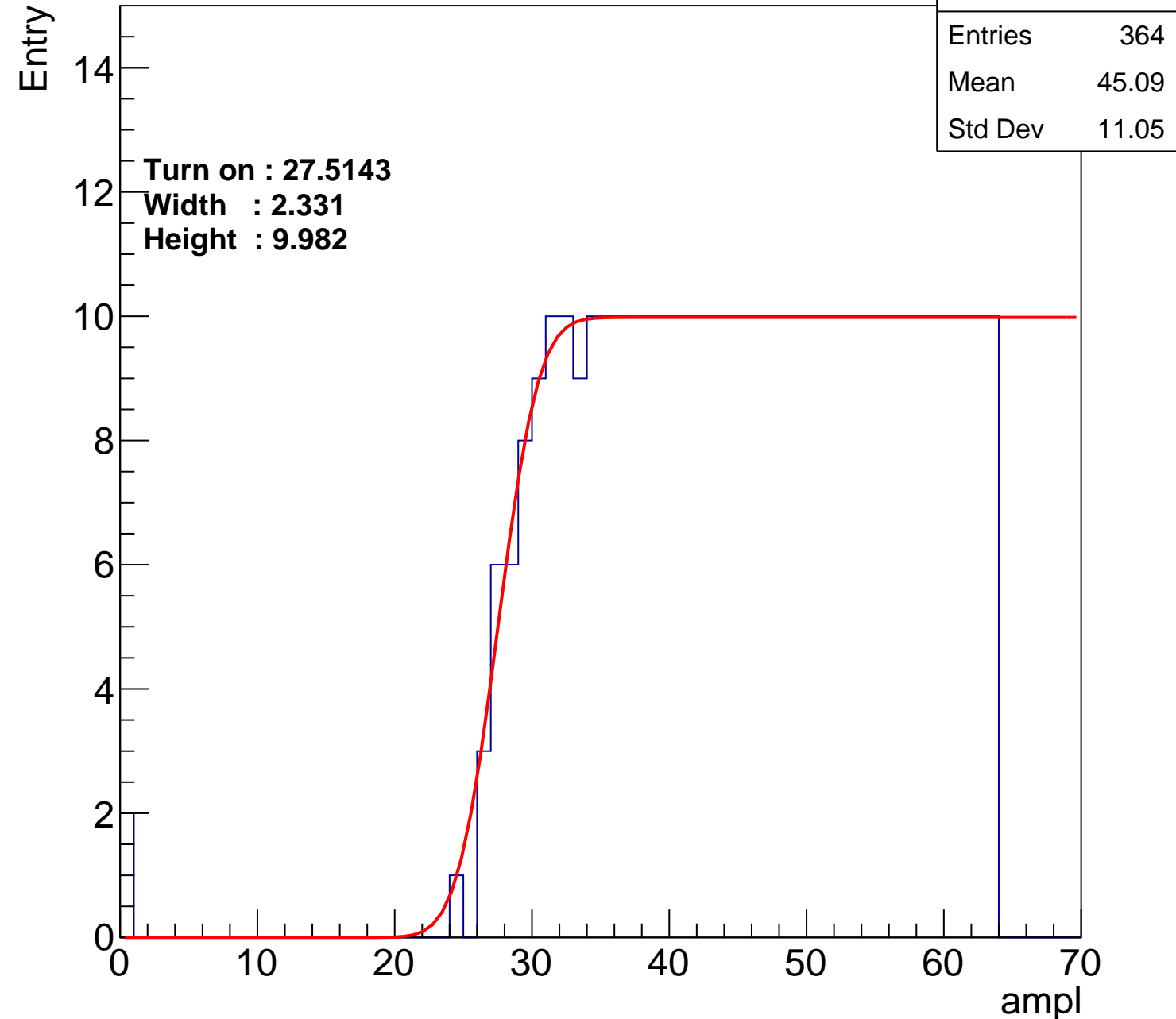
Width : 2.331

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch37

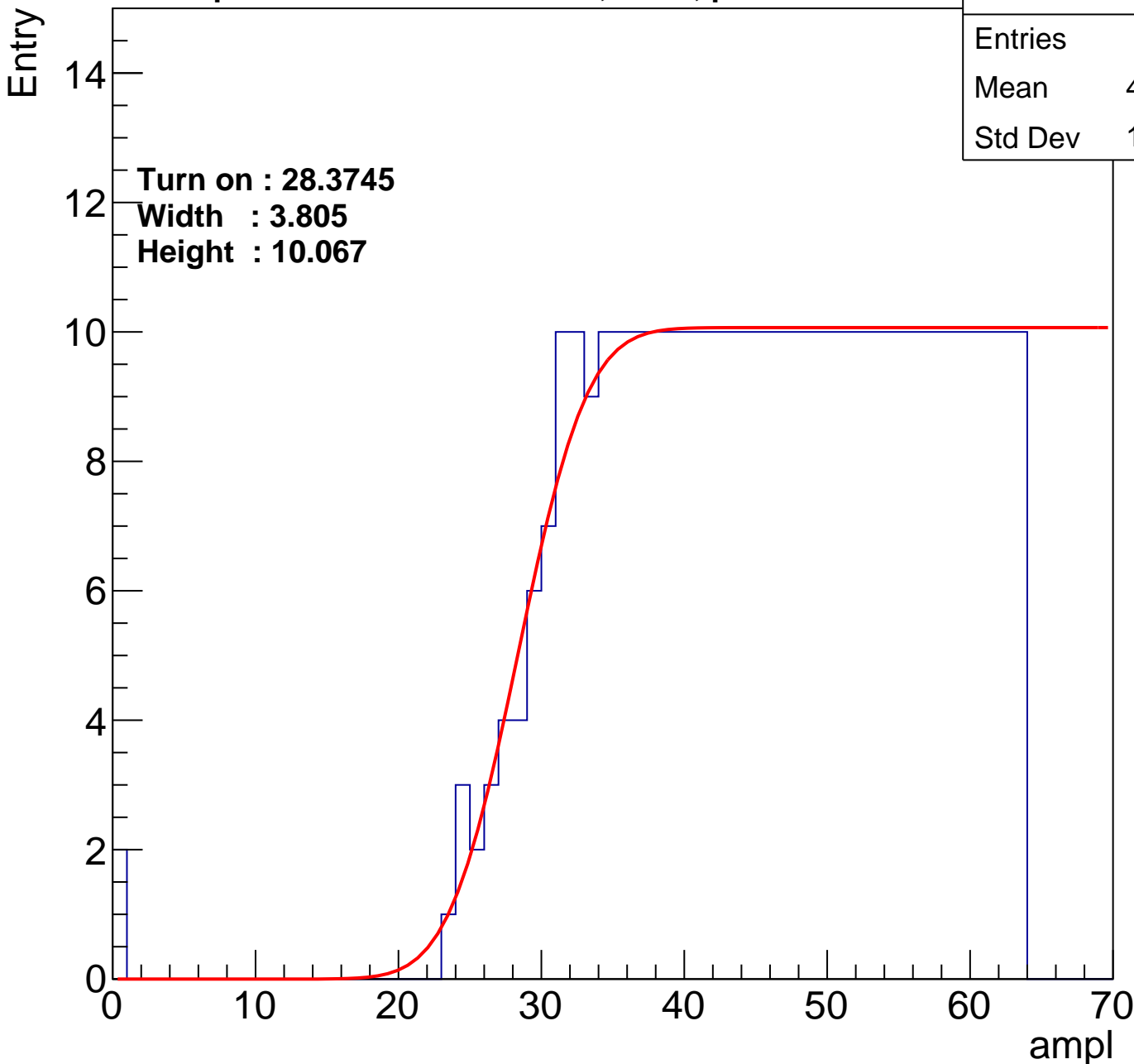
calib_packv5_042523_0143.root, FC#9, port A1

Entries	361
Mean	45.16
Std Dev	11.09

Turn on : 28.3745

Width : 3.805

Height : 10.067



B0L001S, U14-ch38

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.22
Std Dev	11.19

Turn on : 29.0437

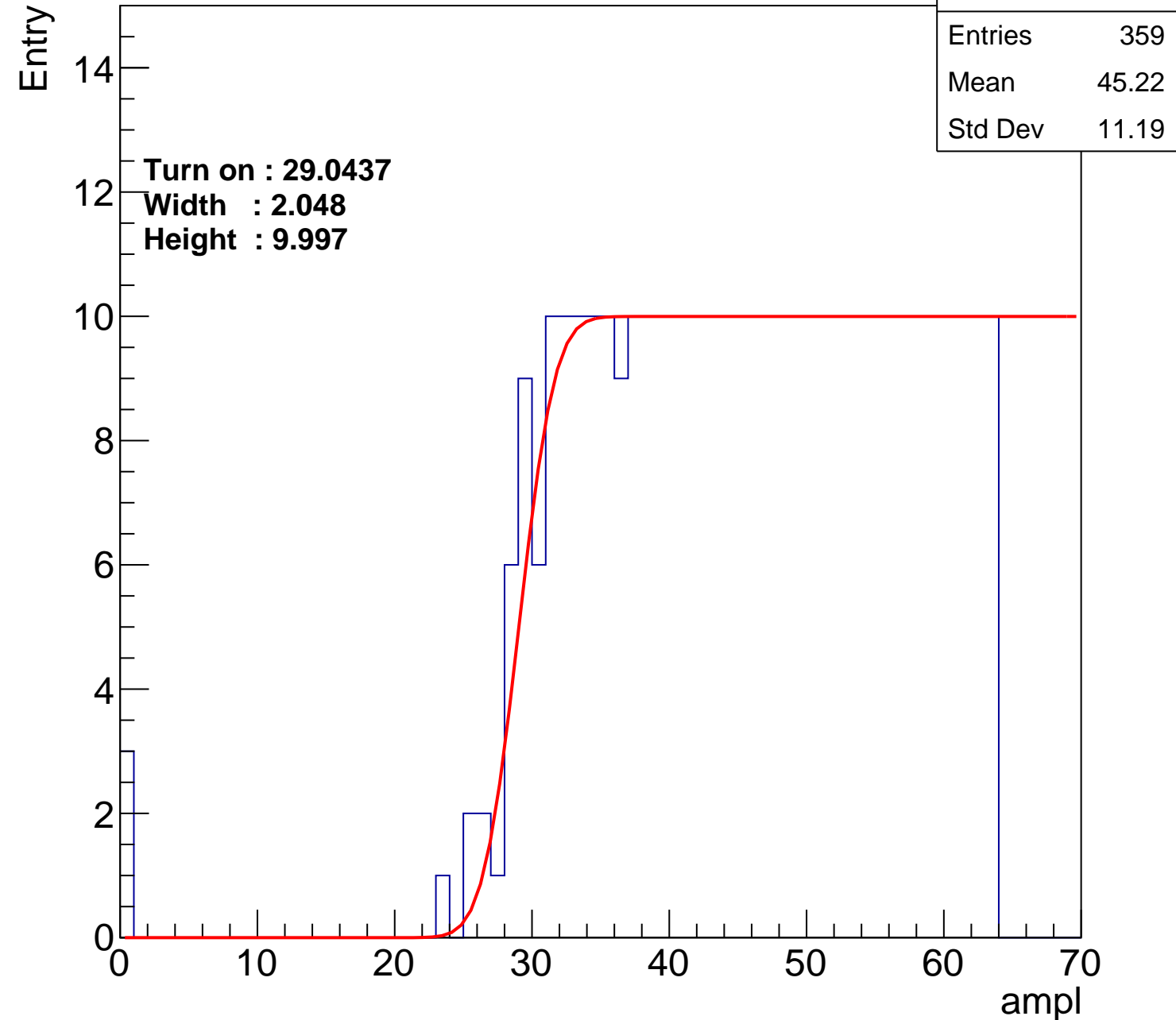
Width : 2.048

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch39

calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.02
Std Dev	11.43

Turn on : 27.8114

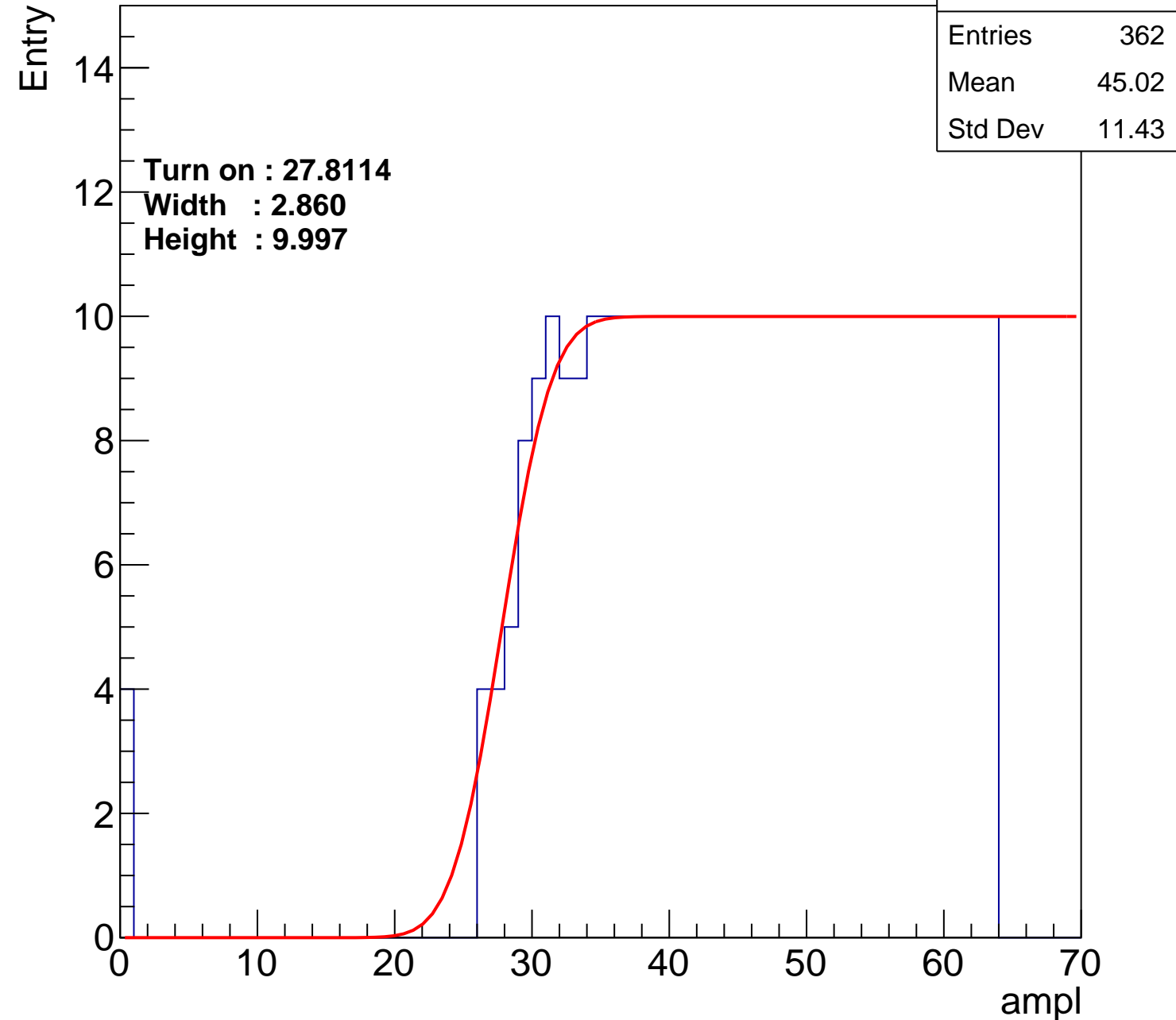
Width : 2.860

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch40

calib_packv5_042523_0143.root, FC#9, port A1

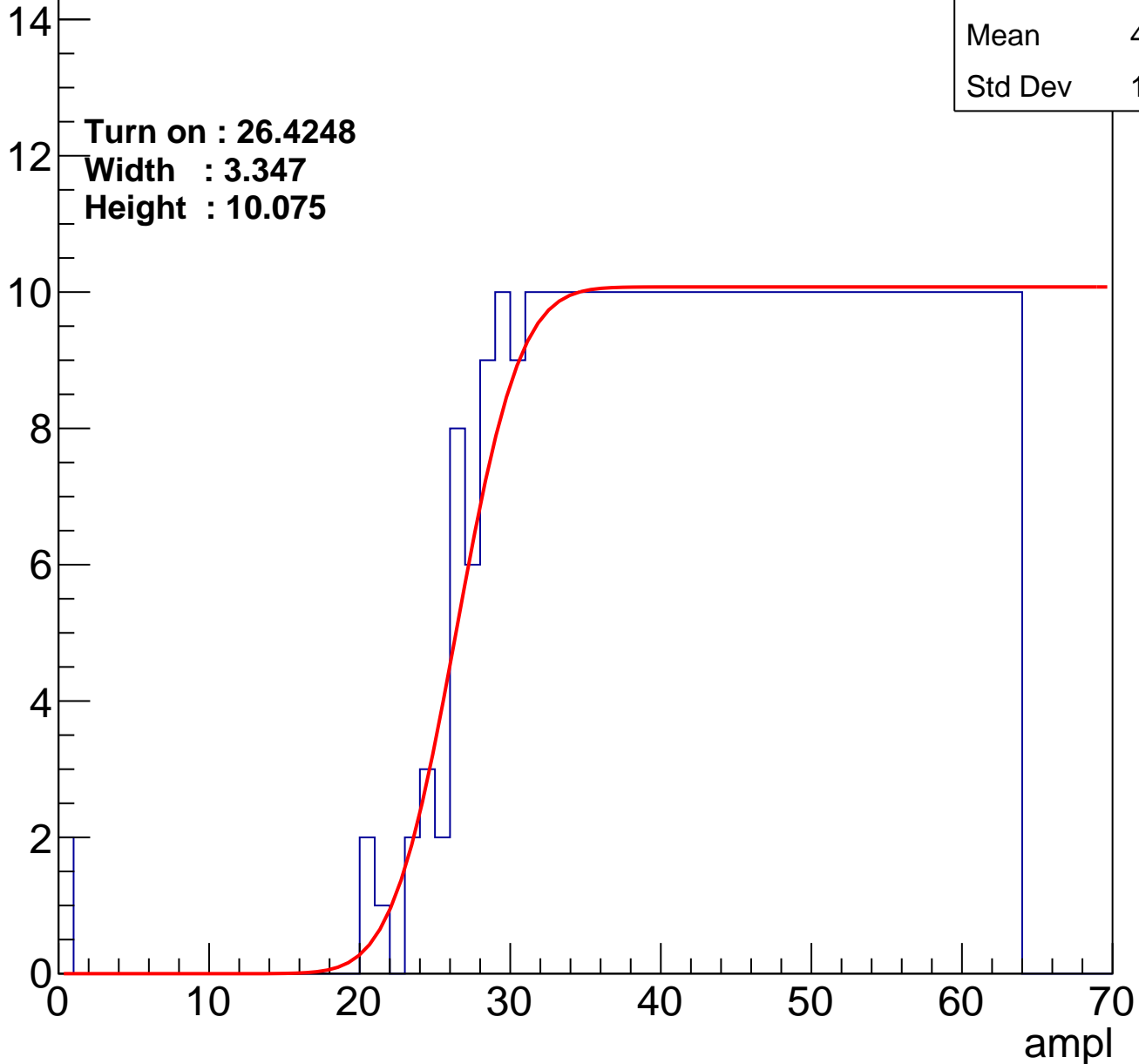
Entries	384
Mean	44.07
Std Dev	11.62

Turn on : 26.4248

Width : 3.347

Height : 10.075

Entry



B0L001S, U14-ch41

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.7055

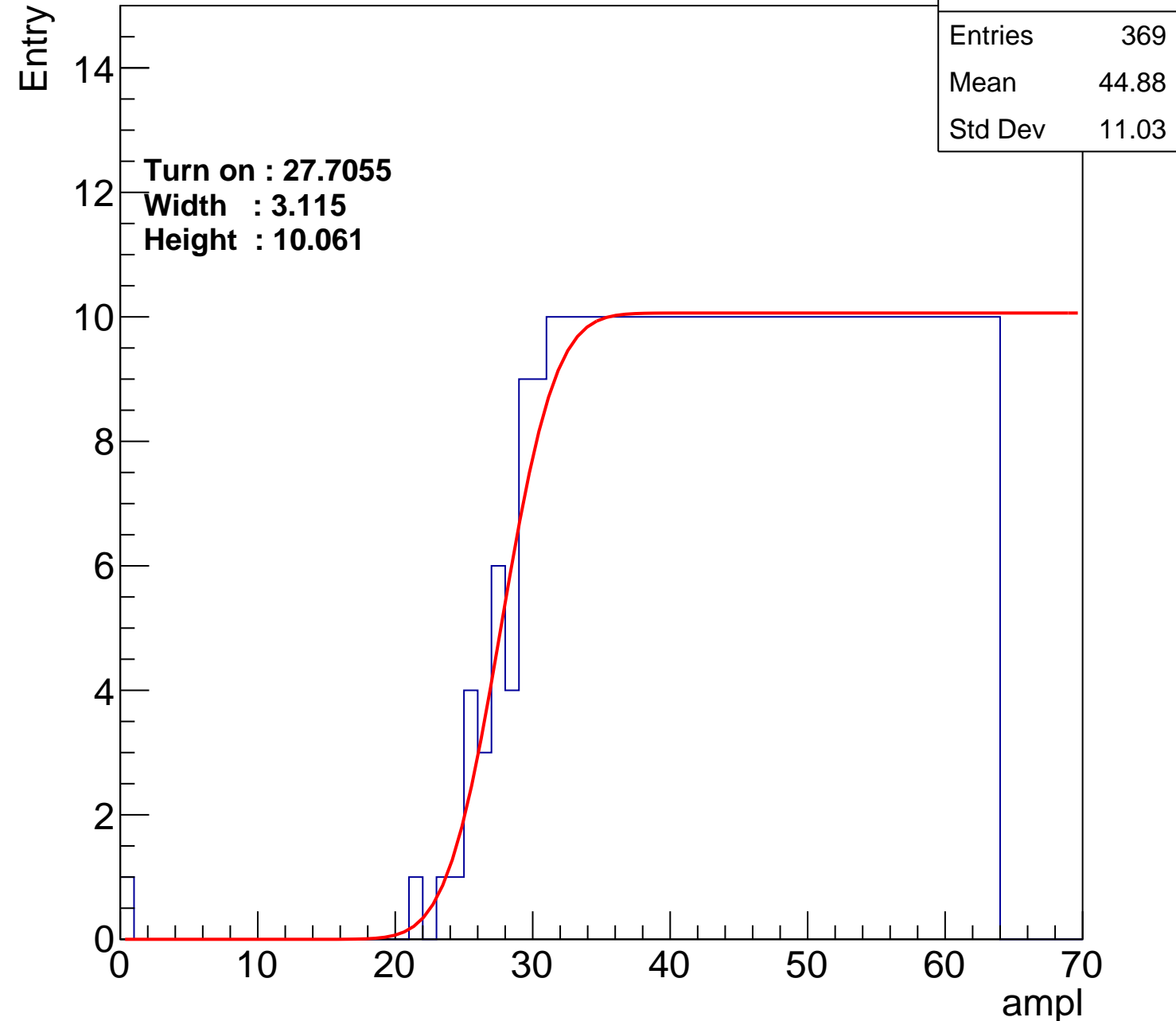
Width : 3.115

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch42

calib_packv5_042523_0143.root, FC#9, port A1

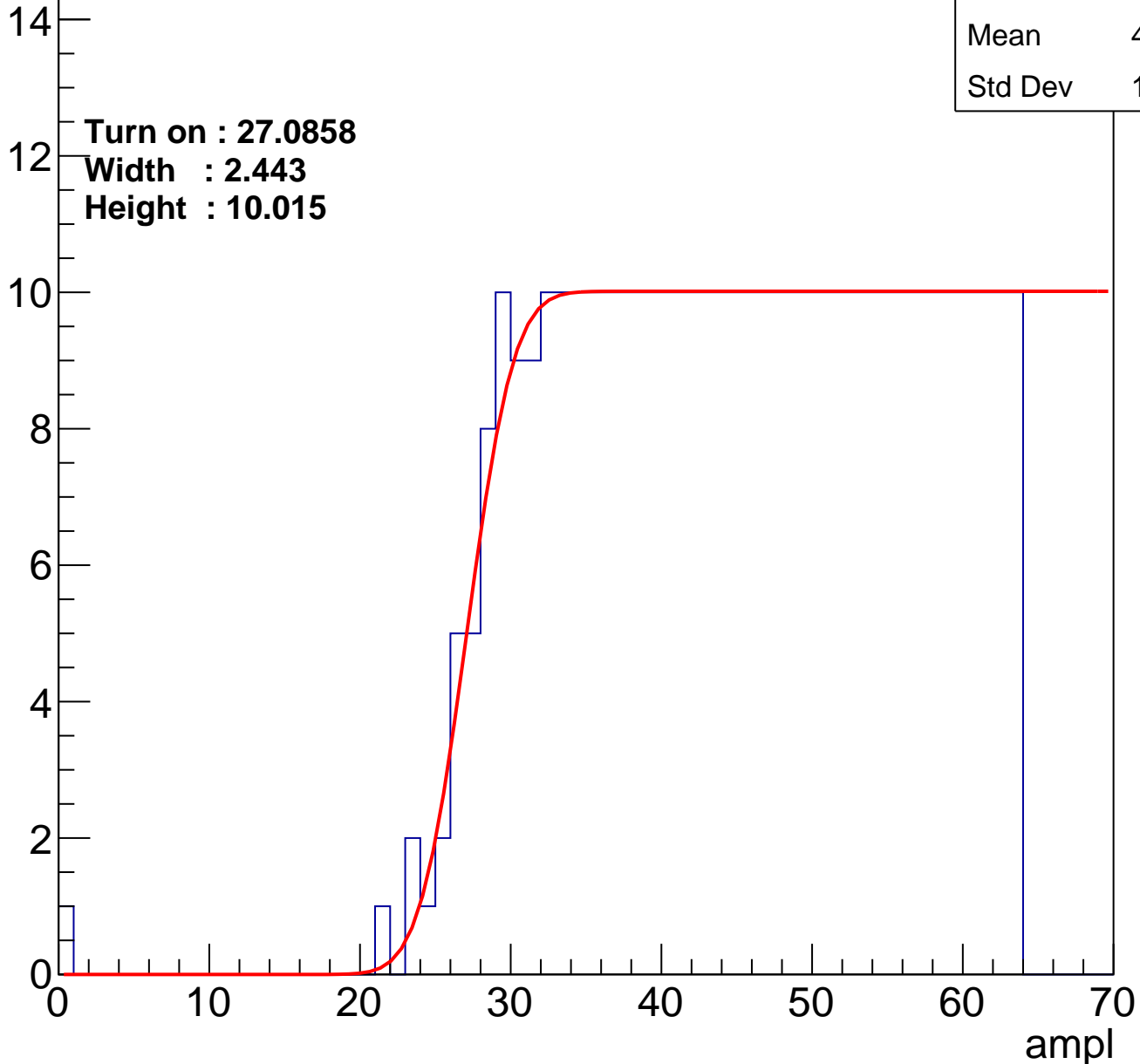
Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 27.0858

Width : 2.443

Height : 10.015

Entry



B0L001S, U14-ch43

calib_packv5_042523_0143.root, FC#9, port A1

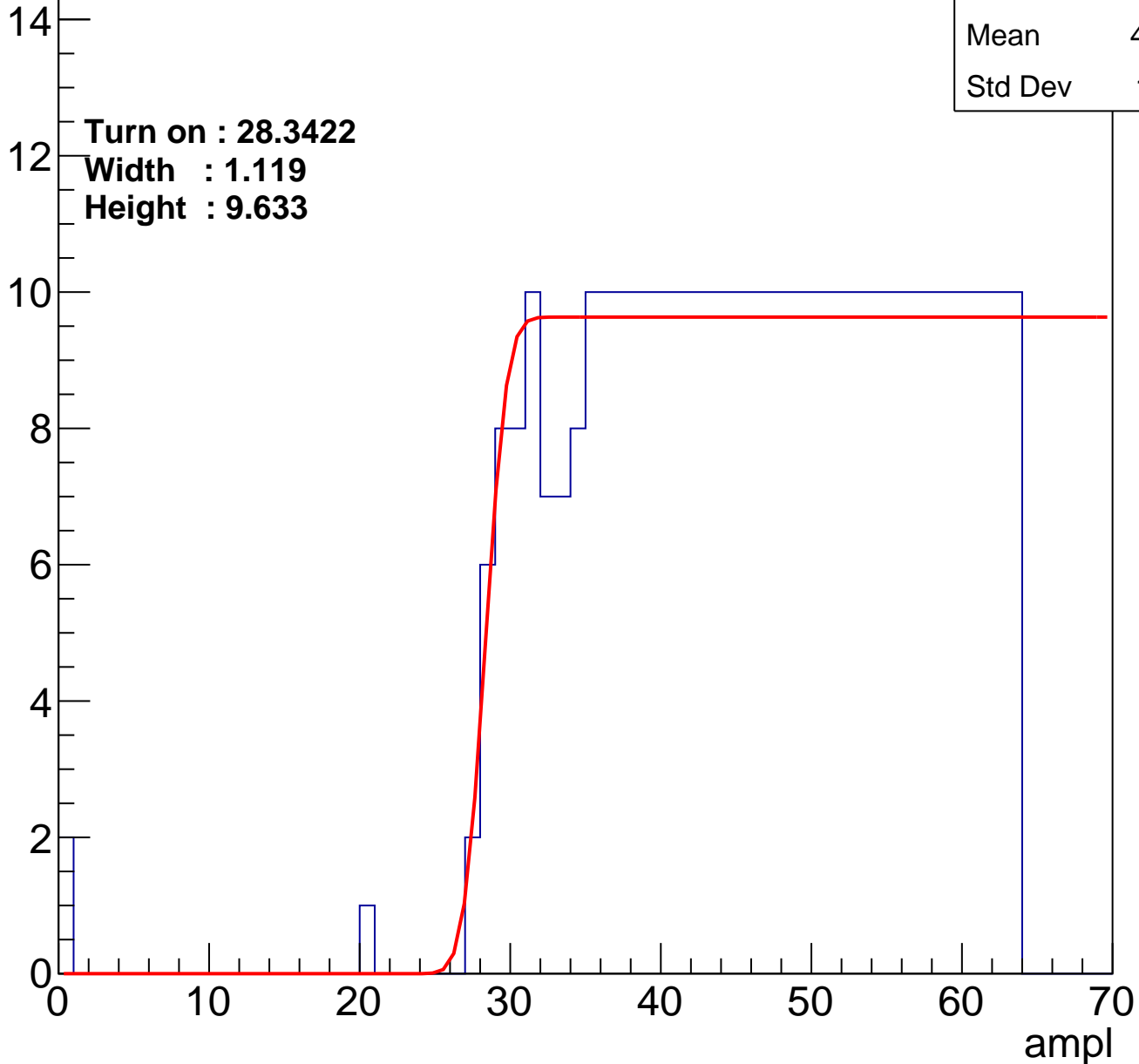
Entries	349
Mean	45.73
Std Dev	10.81

Turn on : 28.3422

Width : 1.119

Height : 9.633

Entry



B0L001S, U14-ch44

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.75
Std Dev	11.96

Turn on : 28.4178

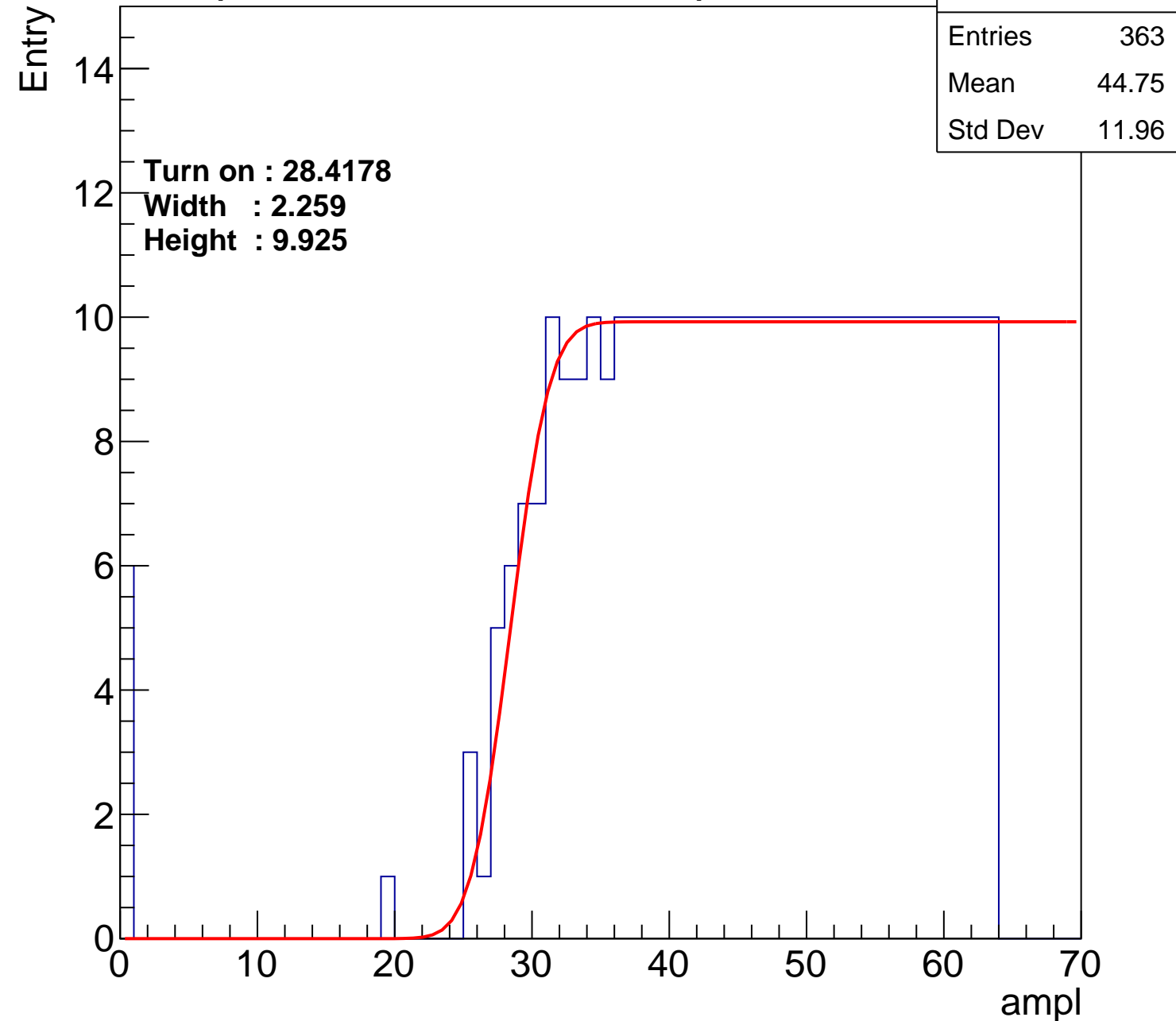
Width : 2.259

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch45

calib_packv5_042523_0143.root, FC#9, port A1

Entries	347
Mean	45.68
Std Dev	11.22

Turn on : 30.1547

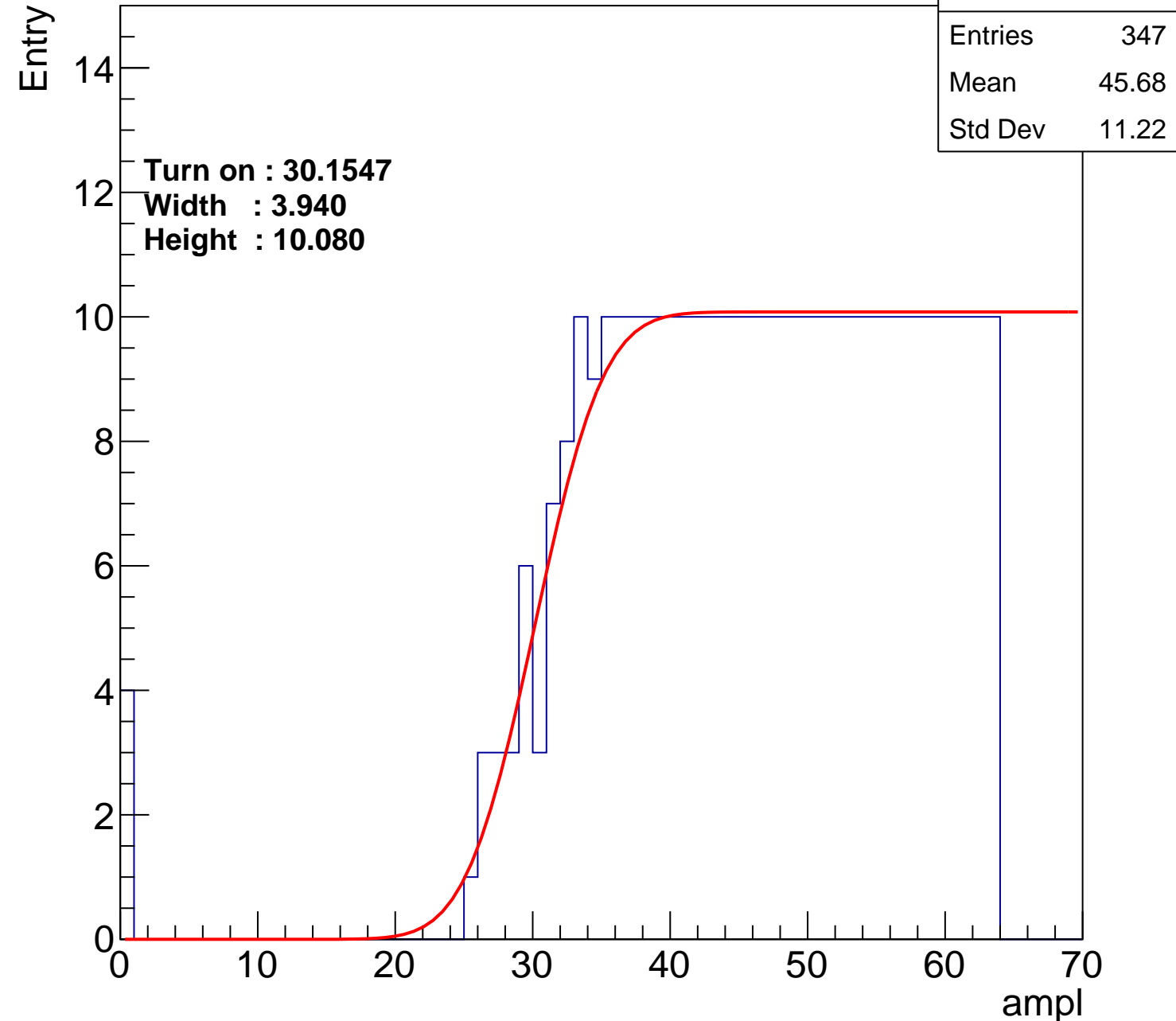
Width : 3.940

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch46

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.09
Std Dev	11.09

Turn on : 27.9222

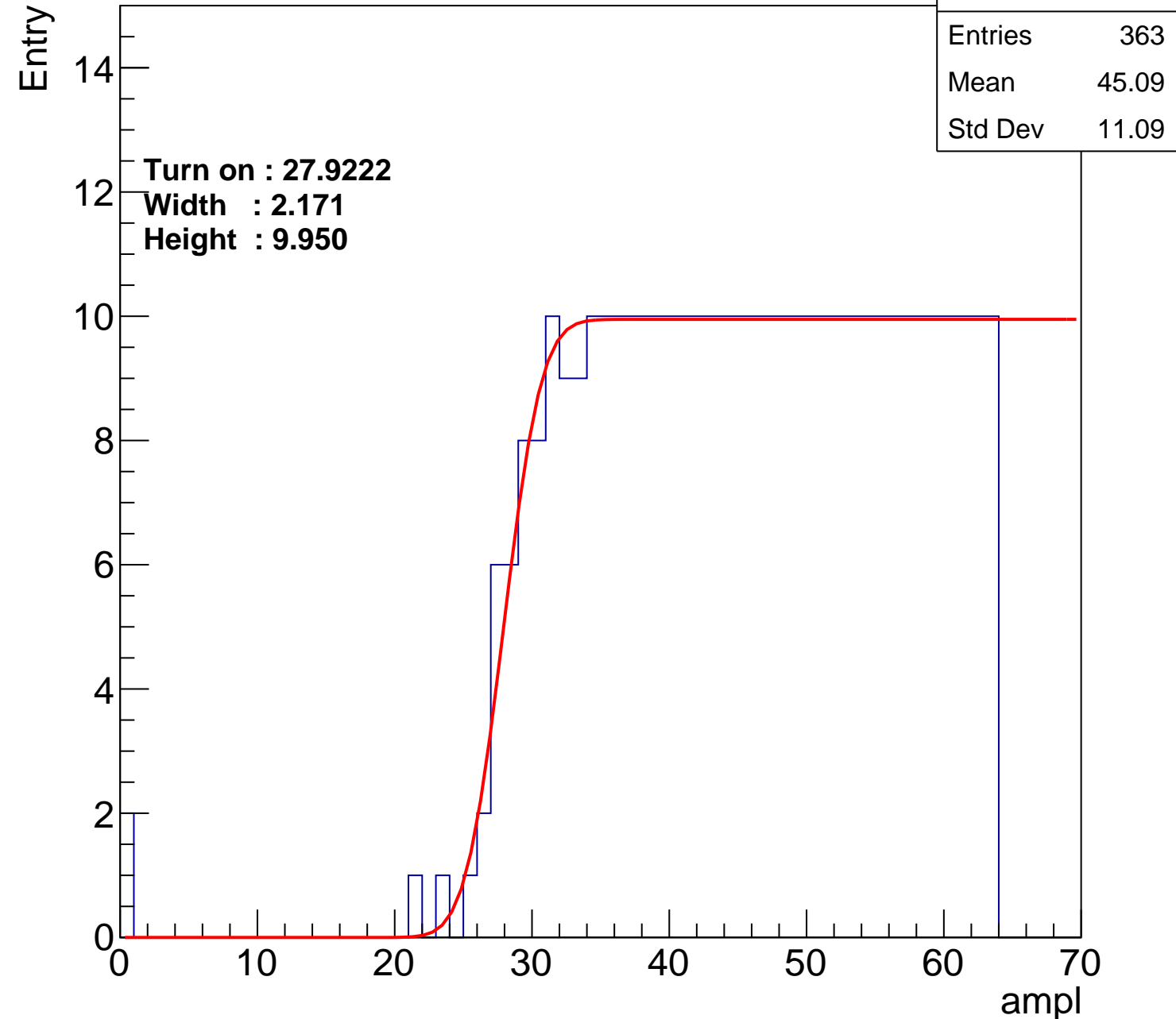
Width : 2.171

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch47

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.32
Std Dev	11.15

Turn on : 29.3111

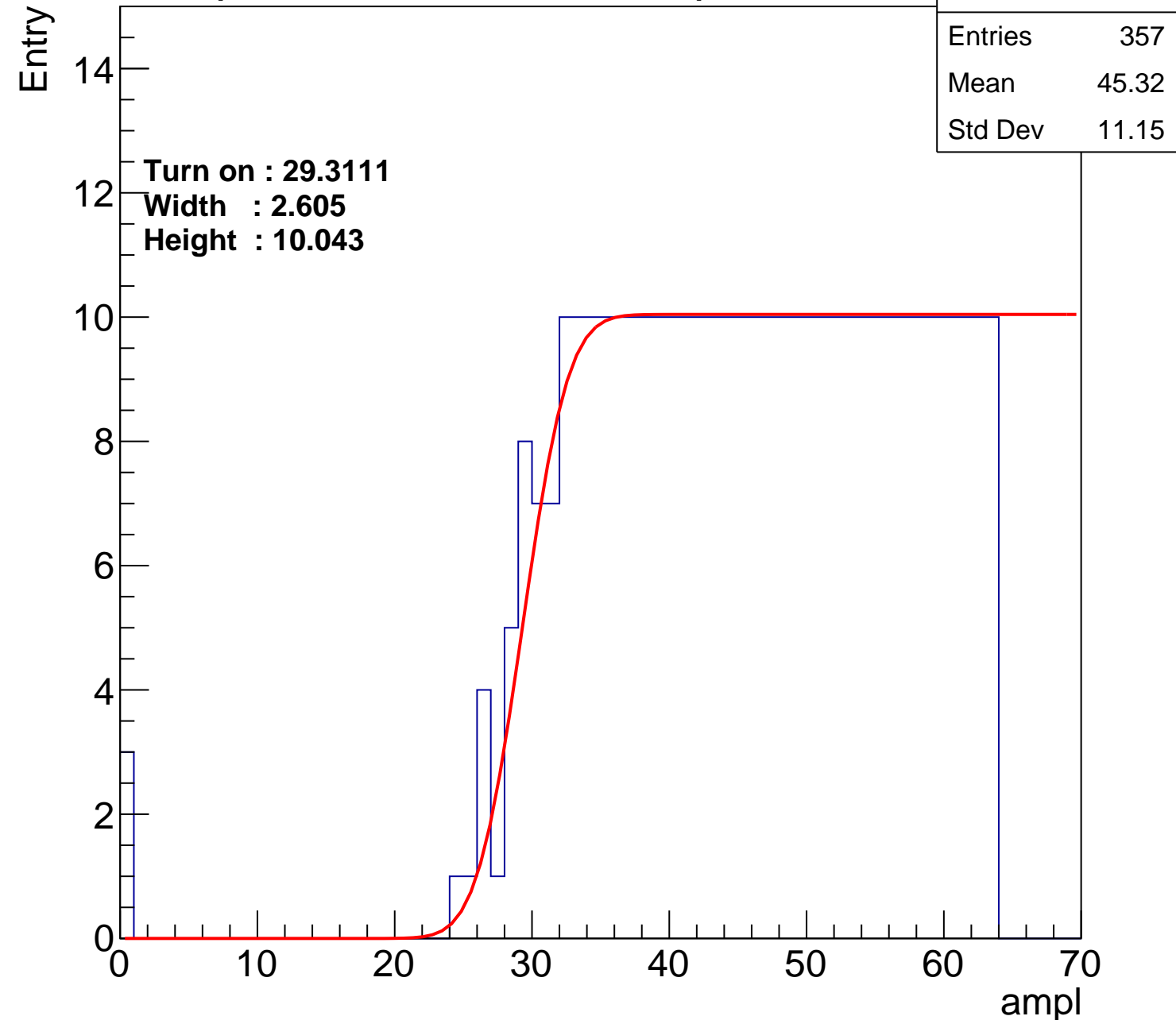
Width : 2.605

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch48

calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.45
Std Dev	11.65

Turn on : 27.7017

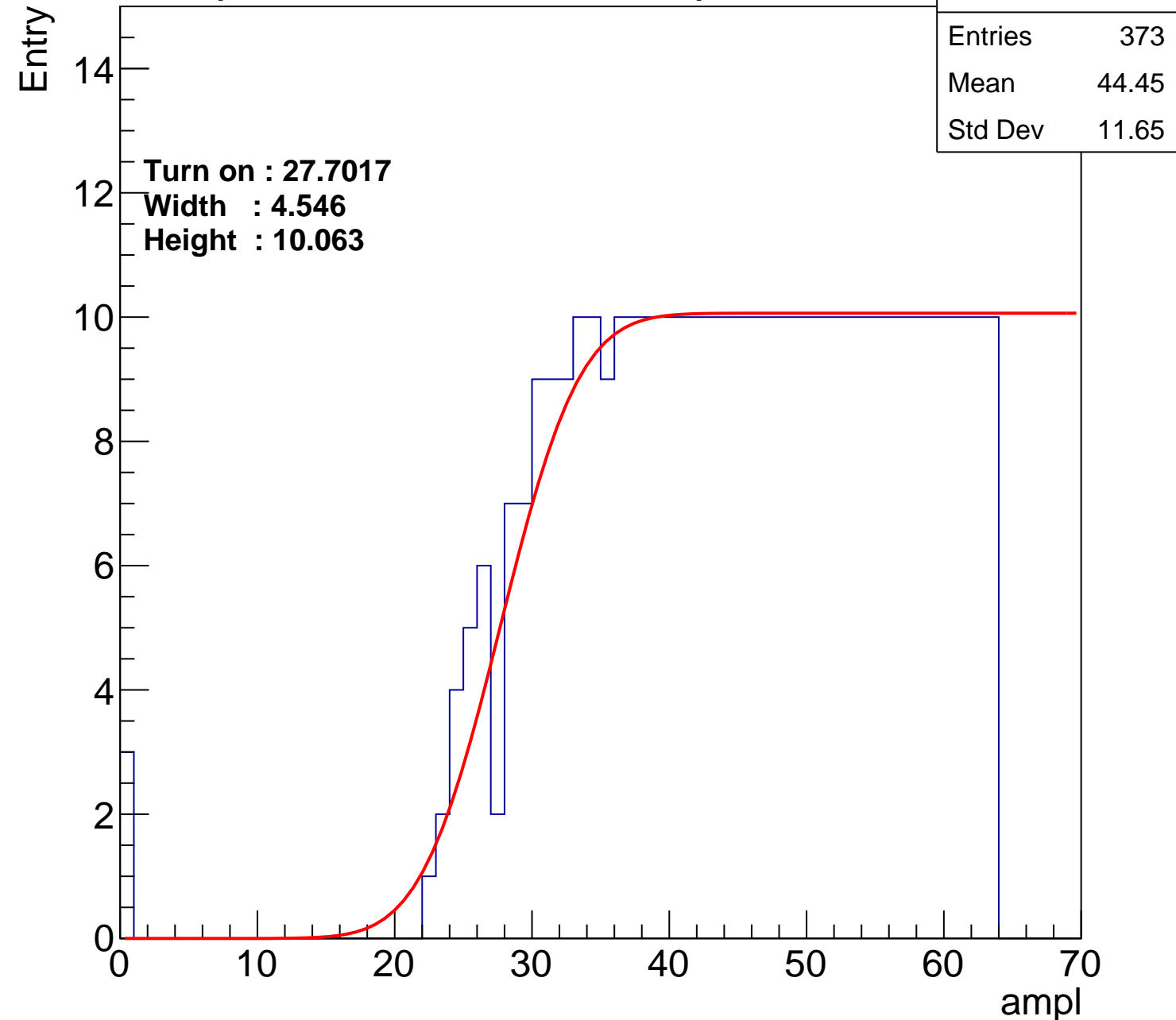
Width : 4.546

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch49

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.92
Std Dev	11.35

Turn on : 28.2494

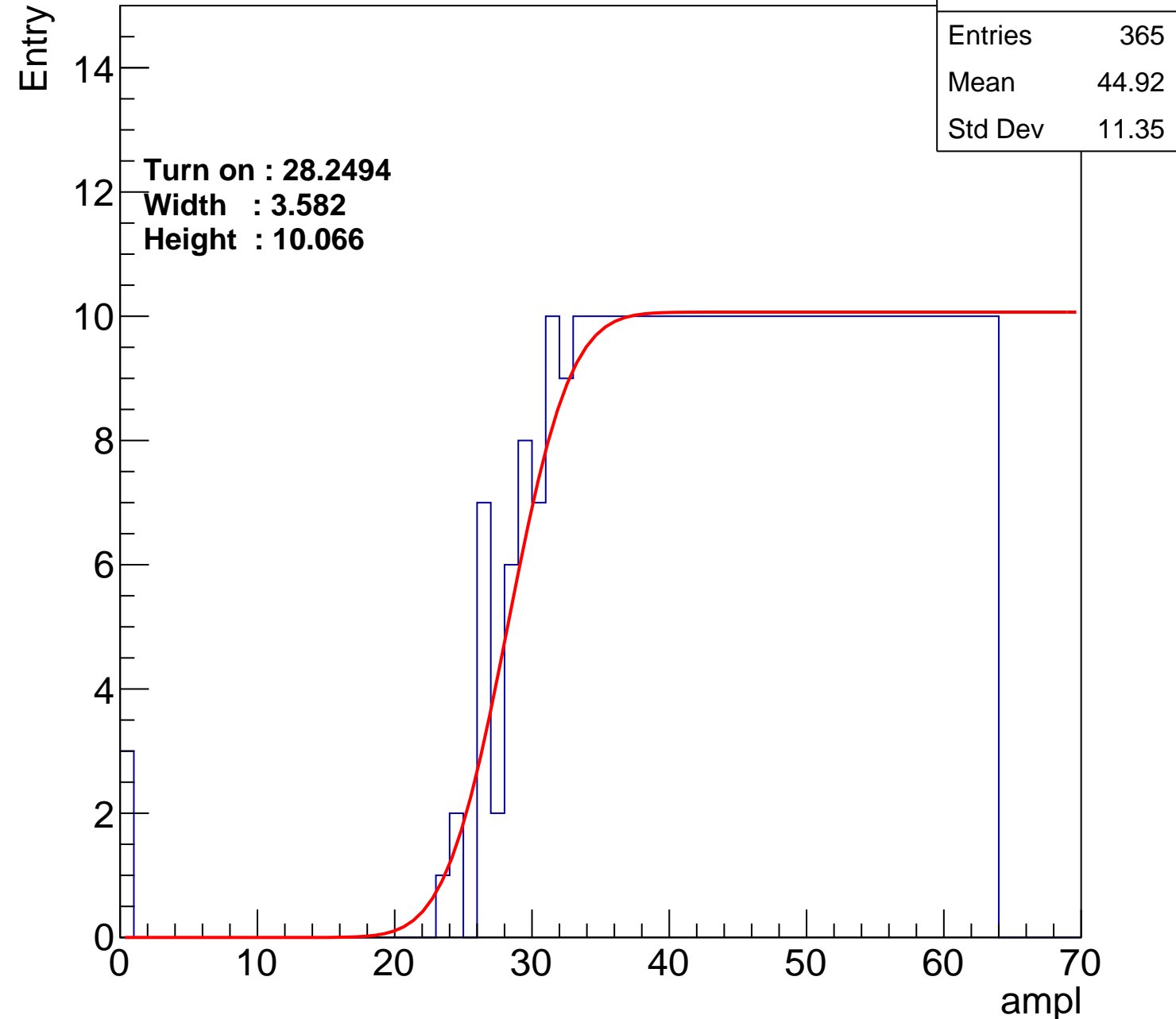
Width : 3.582

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch50

calib_packv5_042523_0143.root, FC#9, port A1

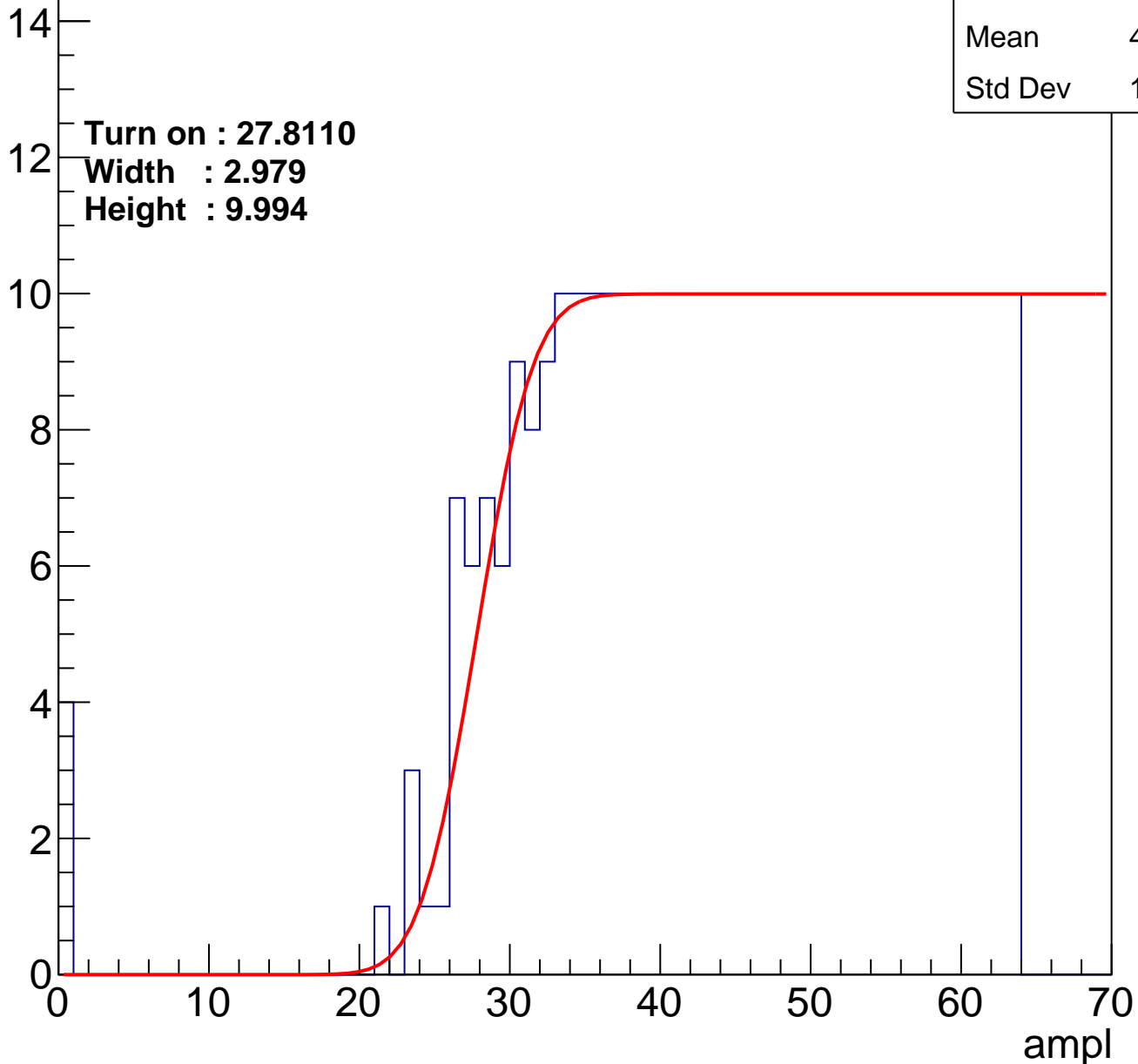
Entries	372
Mean	44.46
Std Dev	11.78

Turn on : 27.8110

Width : 2.979

Height : 9.994

Entry



B0L001S, U14-ch51

calib_packv5_042523_0143.root, FC#9, port A1

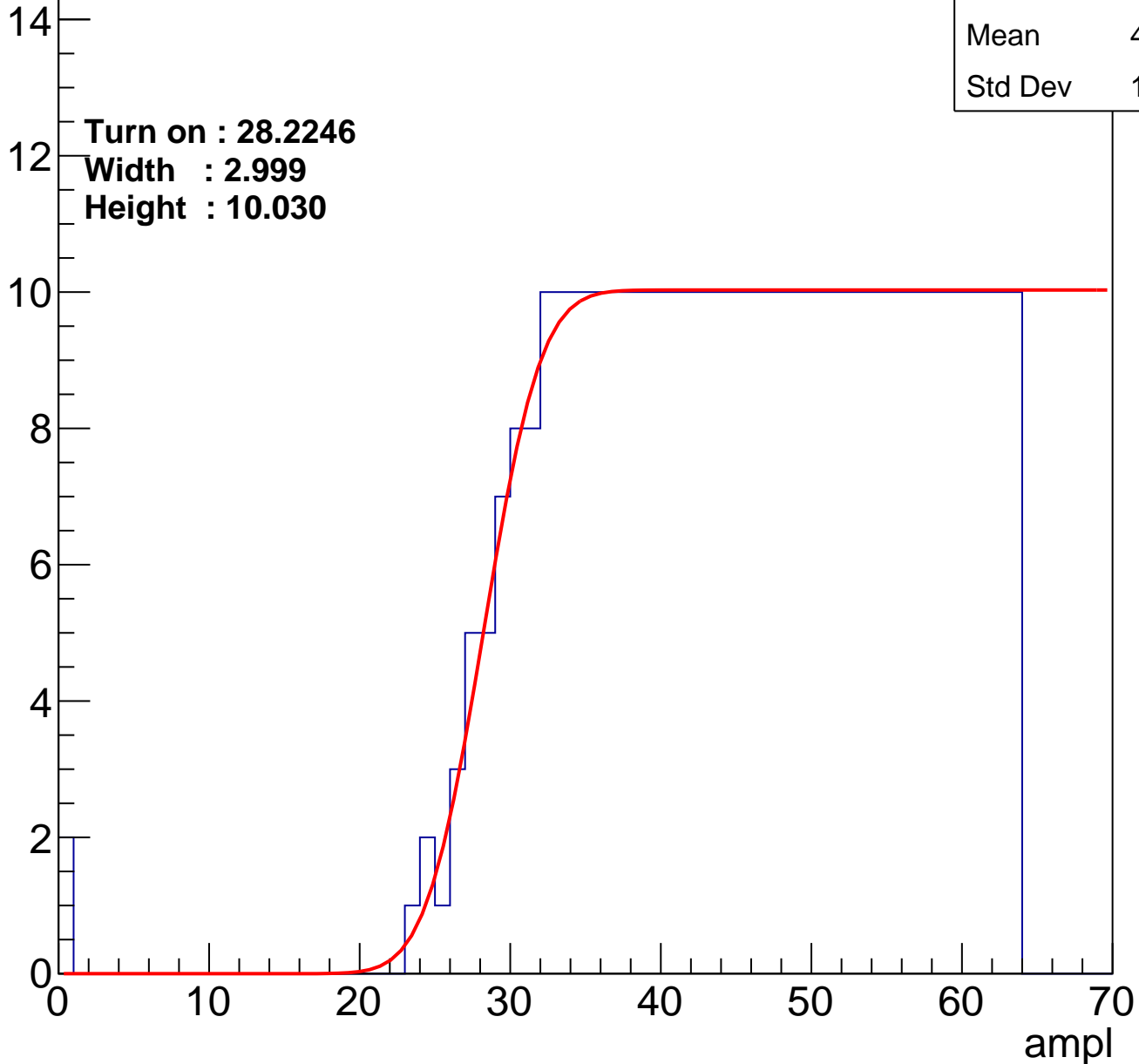
Entries	362
Mean	45.14
Std Dev	11.07

Turn on : 28.2246

Width : 2.999

Height : 10.030

Entry



B0L001S, U14-ch52

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 28.2481

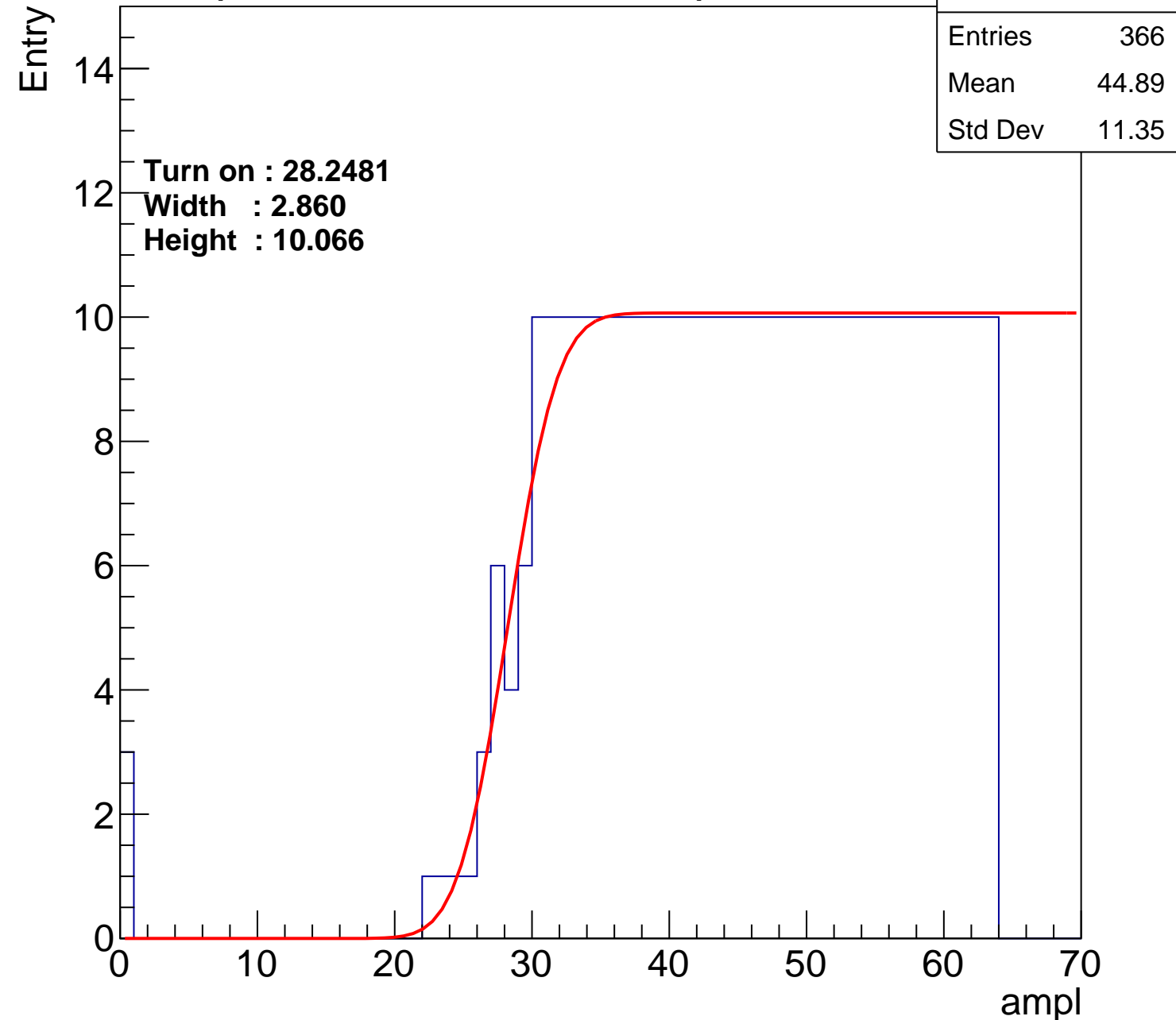
Width : 2.860

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch53

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.9
Std Dev	11.37

Turn on : 27.7765

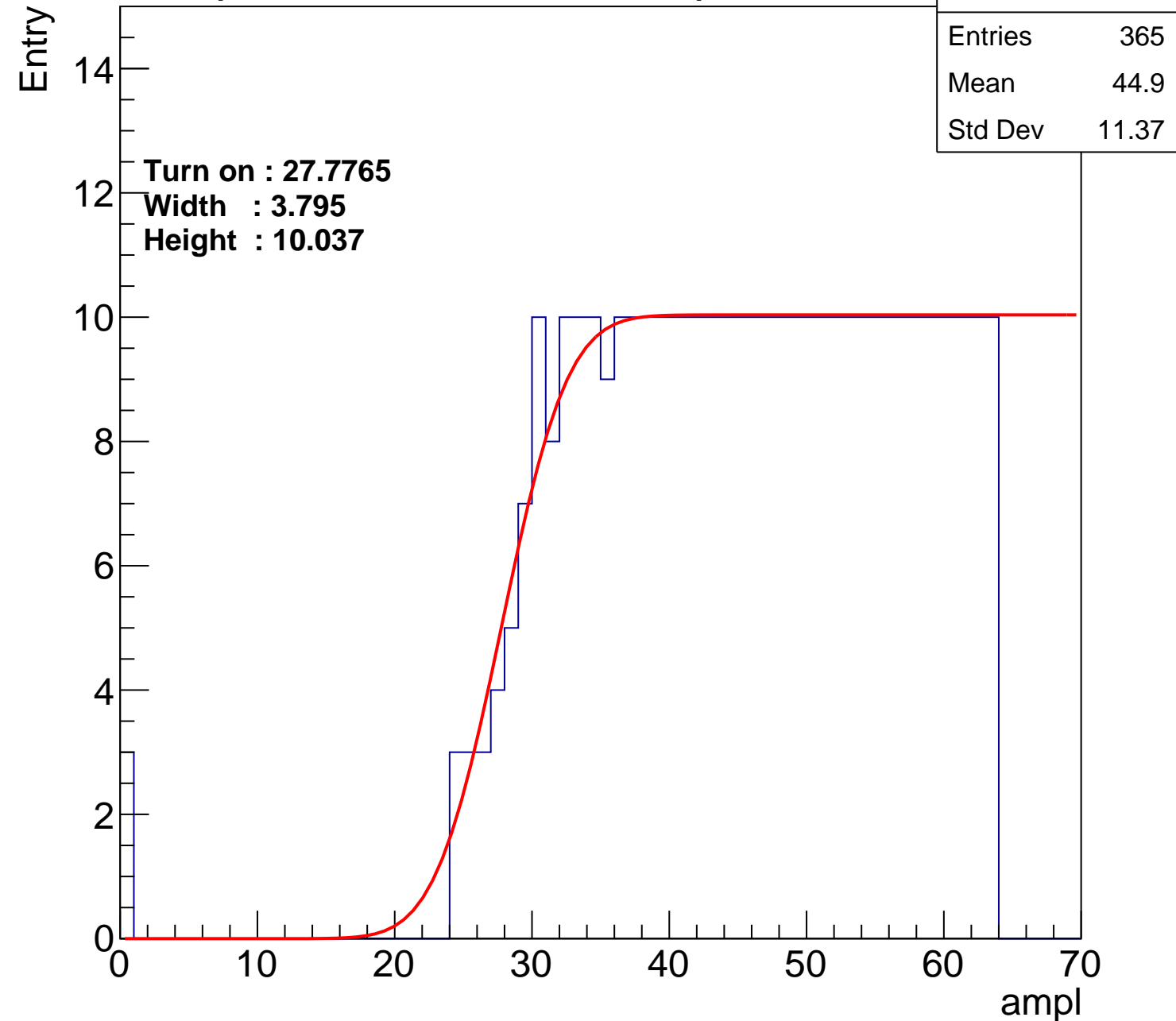
Width : 3.795

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch54

calib_packv5_042523_0143.root, FC#9, port A1

Entries	375
Mean	44.32
Std Dev	11.83

Turn on : 26.9818

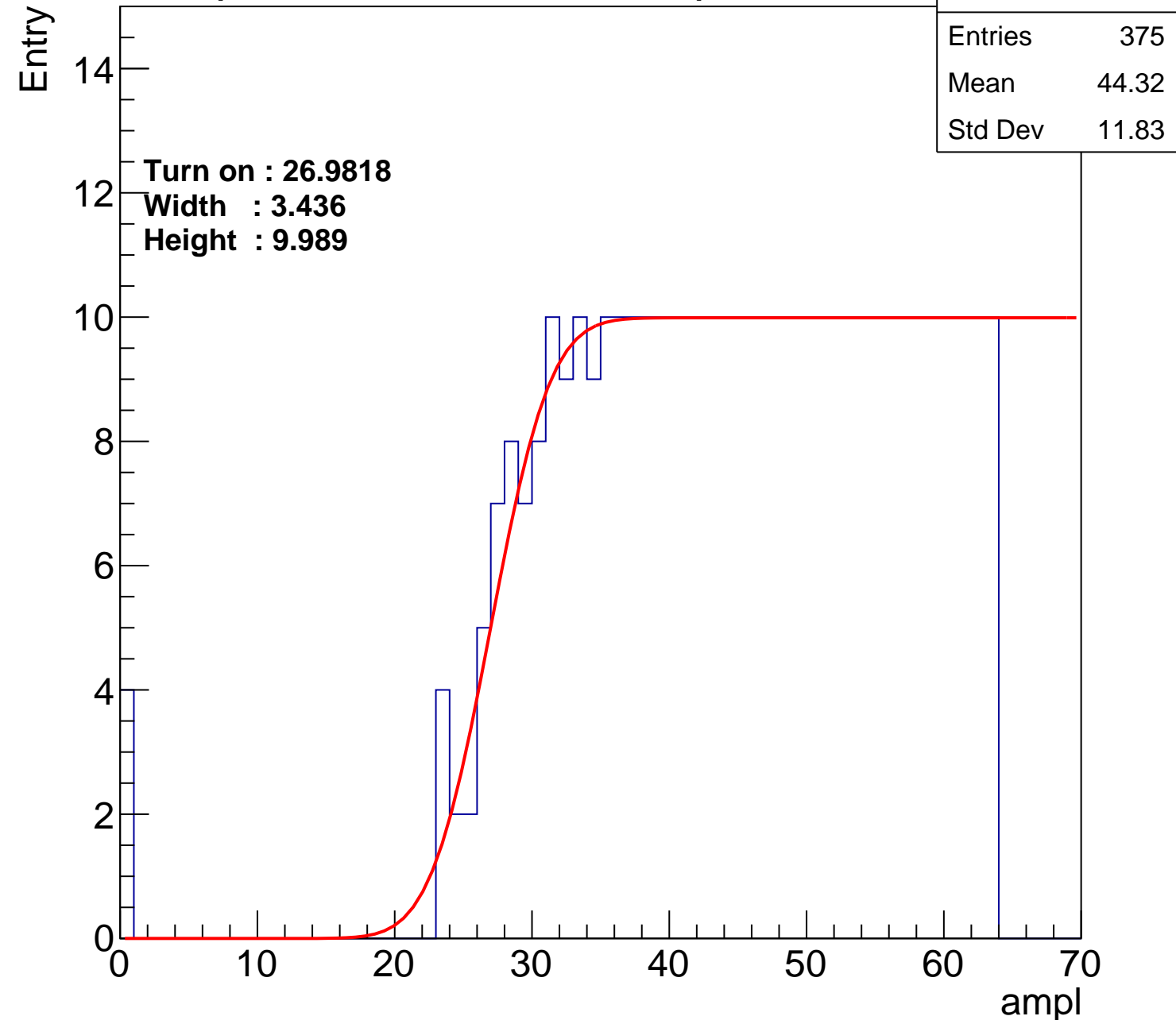
Width : 3.436

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch55

calib_packv5_042523_0143.root, FC#9, port A1

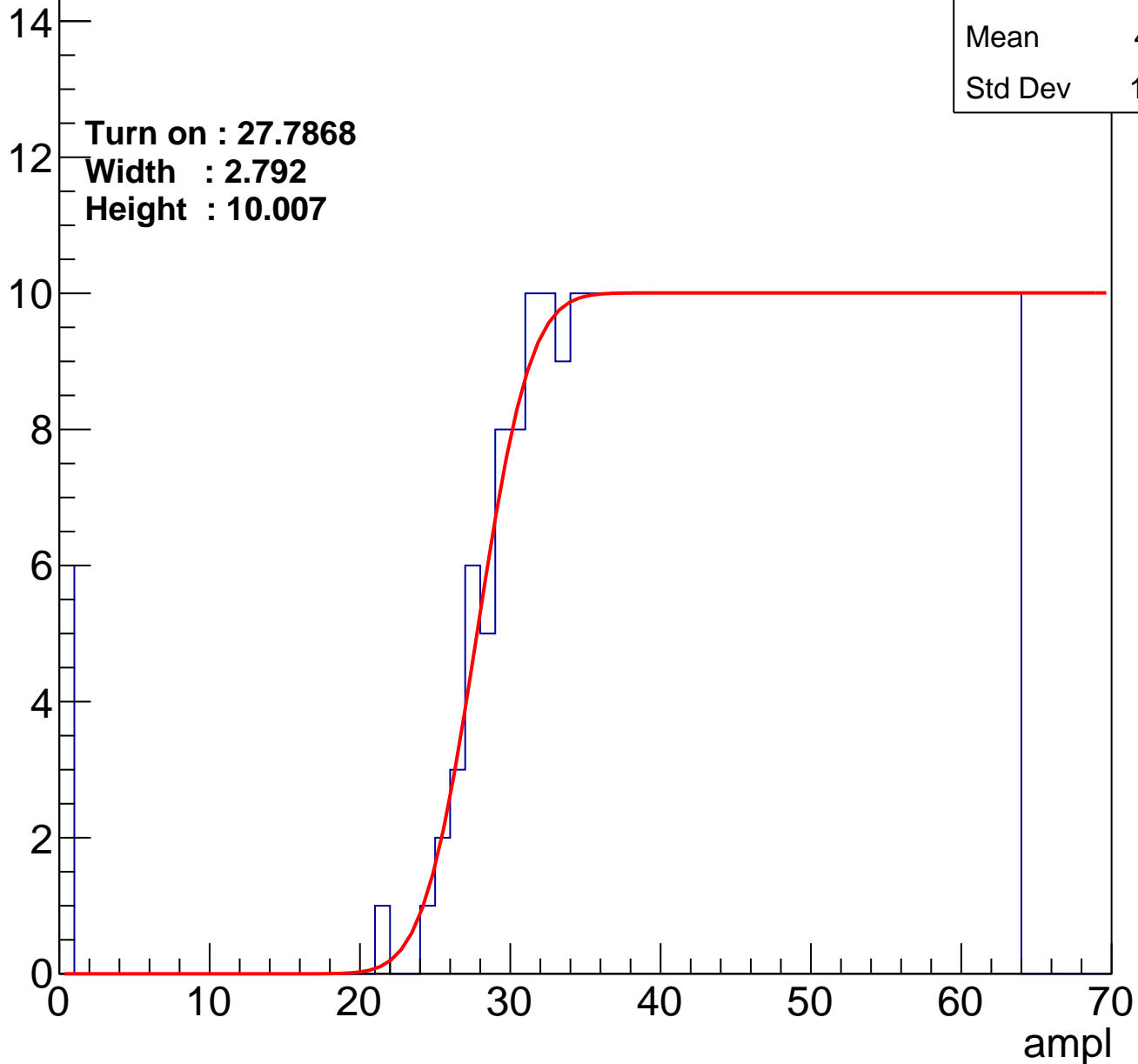
Entries	369
Mean	44.51
Std Dev	12.02

Turn on : 27.7868

Width : 2.792

Height : 10.007

Entry



B0L001S, U14-ch56

calib_packv5_042523_0143.root, FC#9, port A1

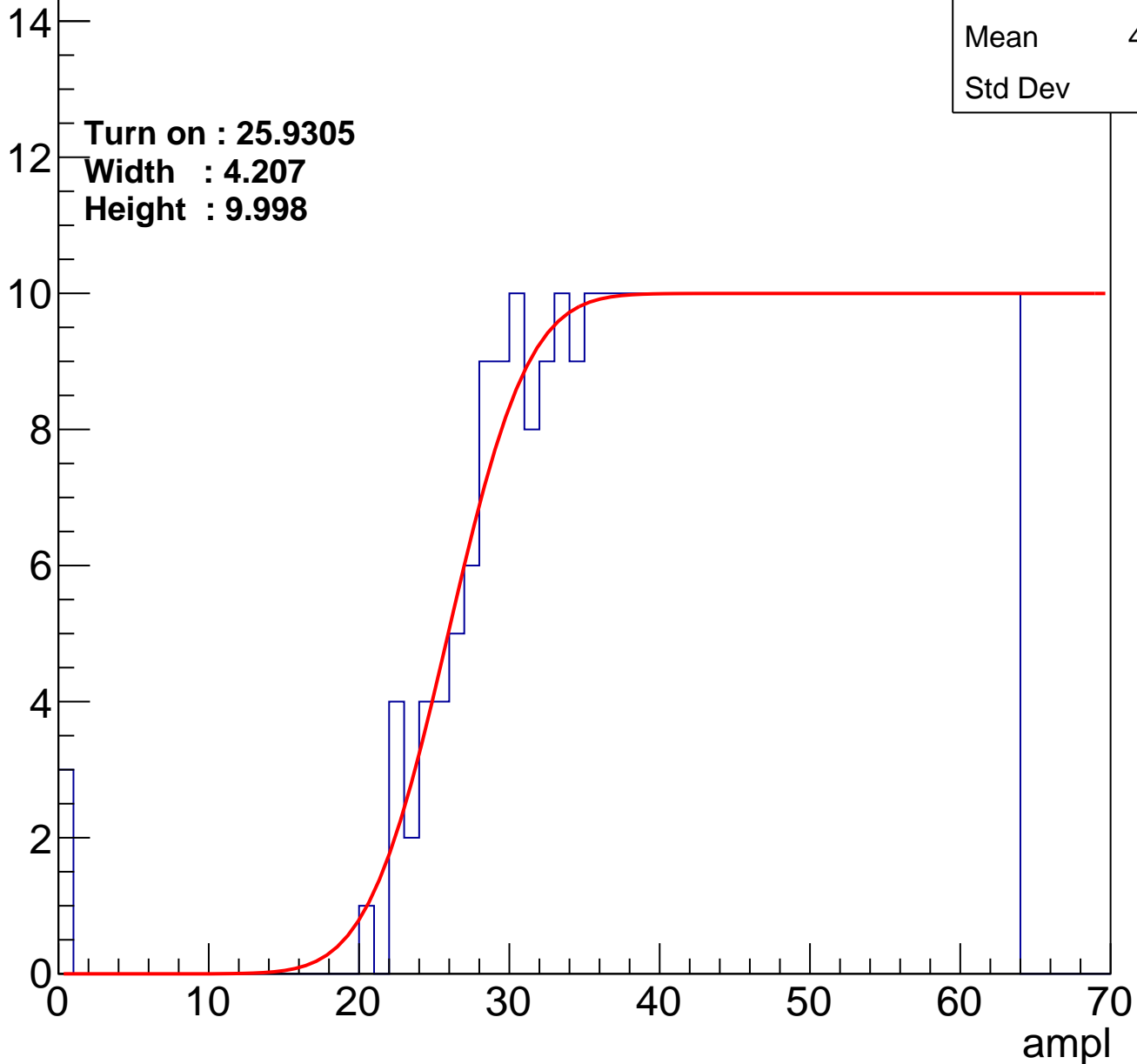
Entries	383
Mean	43.96
Std Dev	11.9

Turn on : 25.9305

Width : 4.207

Height : 9.998

Entry



B0L001S, U14-ch57

calib_packv5_042523_0143.root, FC#9, port A1

Entries	347
Mean	45.89
Std Dev	10.57

Turn on : 29.4394

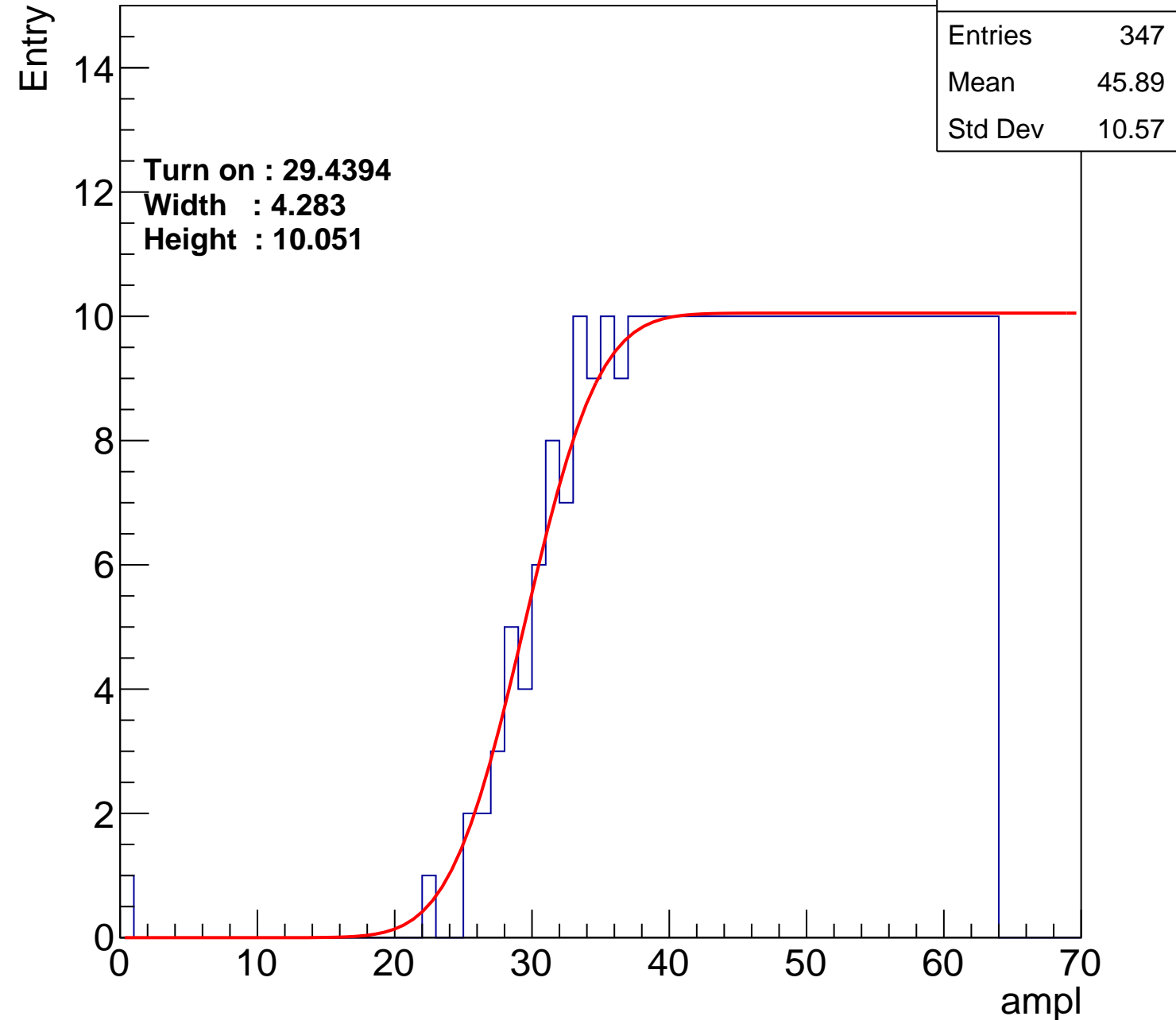
Width : 4.283

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch58

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.44
Std Dev	11.89

Turn on : 27.6390

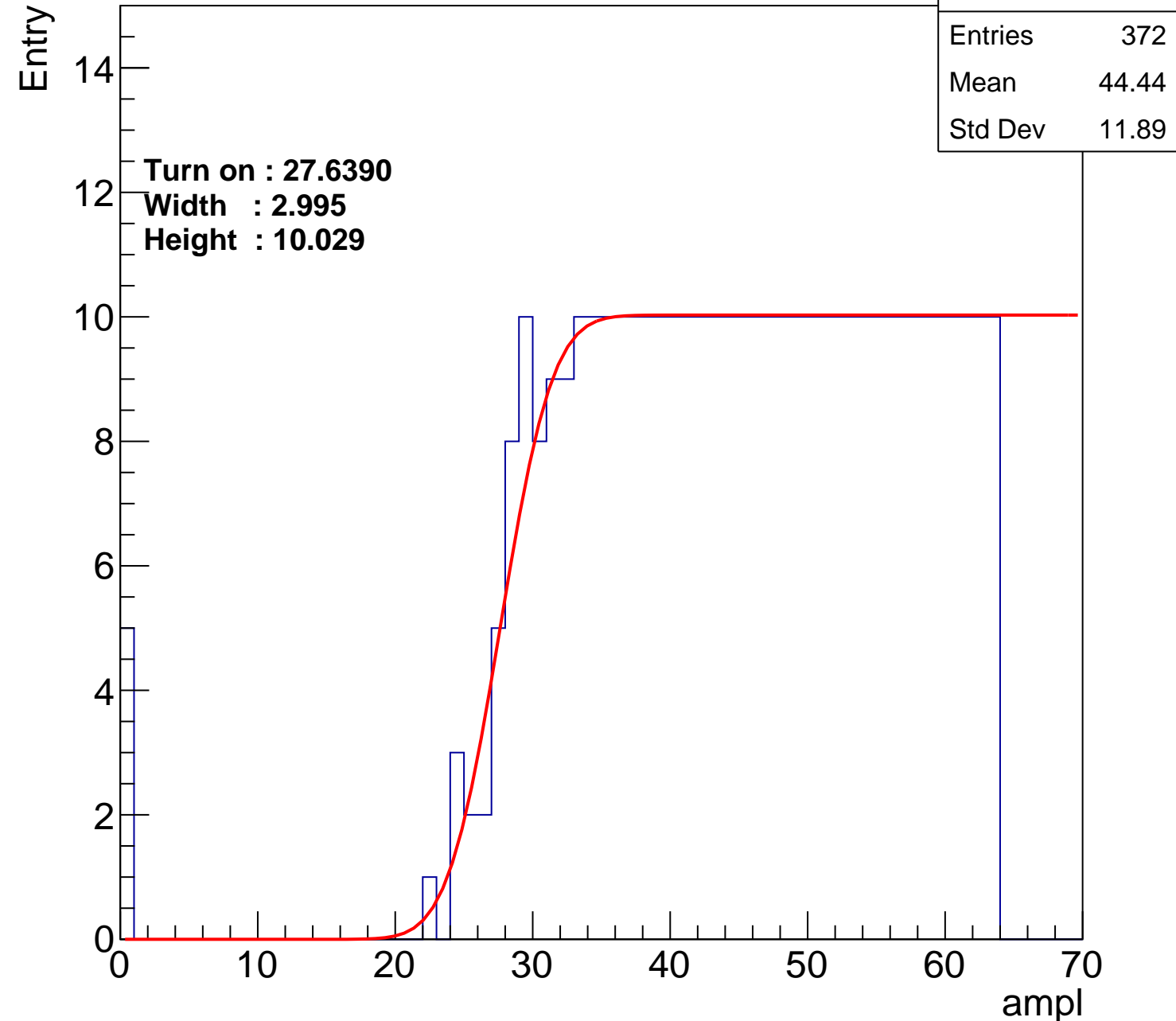
Width : 2.995

Height : 10.029

Entry

14
12
10
8
6
4
2
0

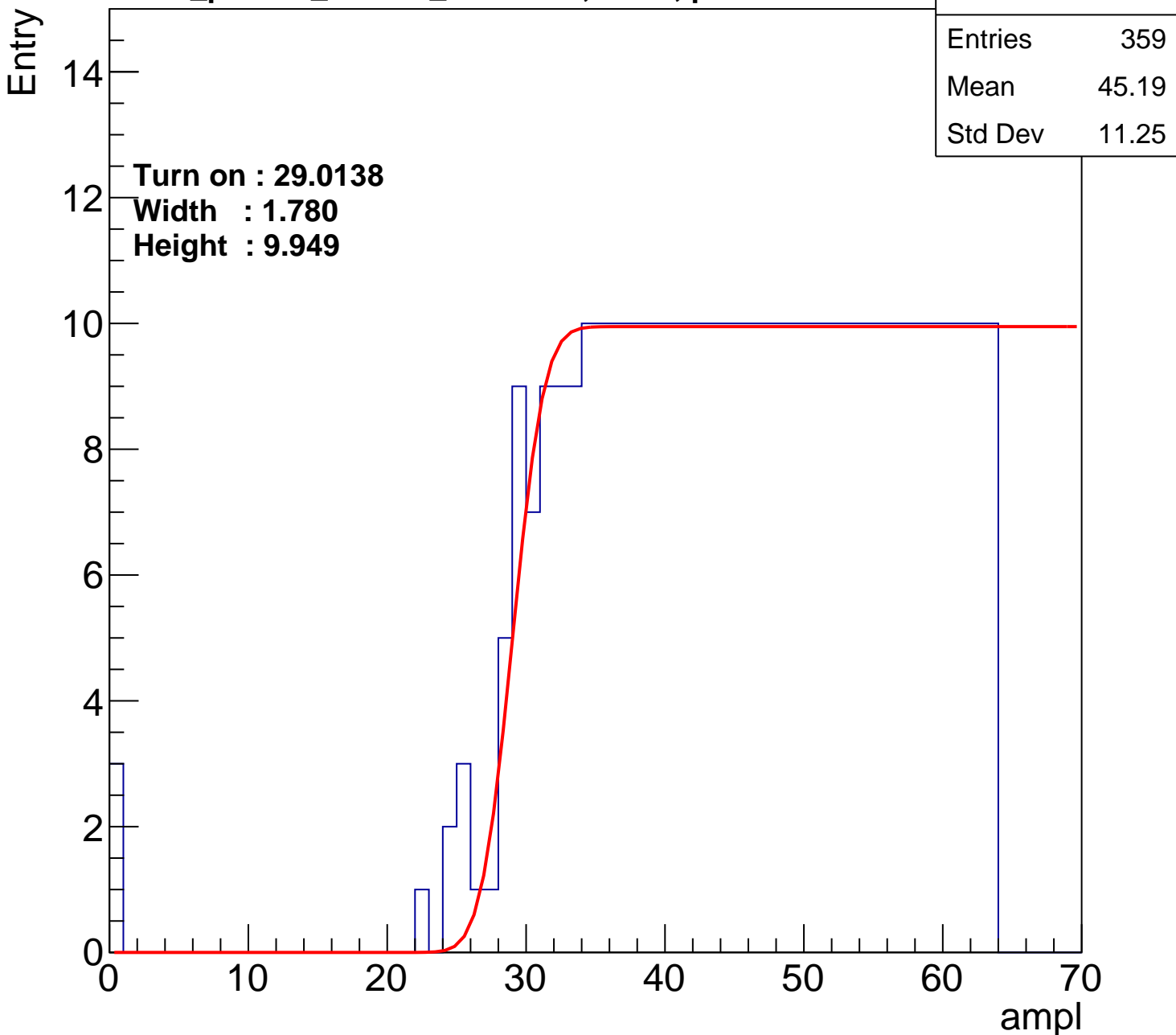
ampl



calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

Turn on : 29.0138
Width : 1.780
Height : 9.949



B0L001S, U14-ch60

calib_packv5_042523_0143.root, FC#9, port A1

Entries	343
Mean	45.97
Std Dev	10.88

Turn on : 30.5326

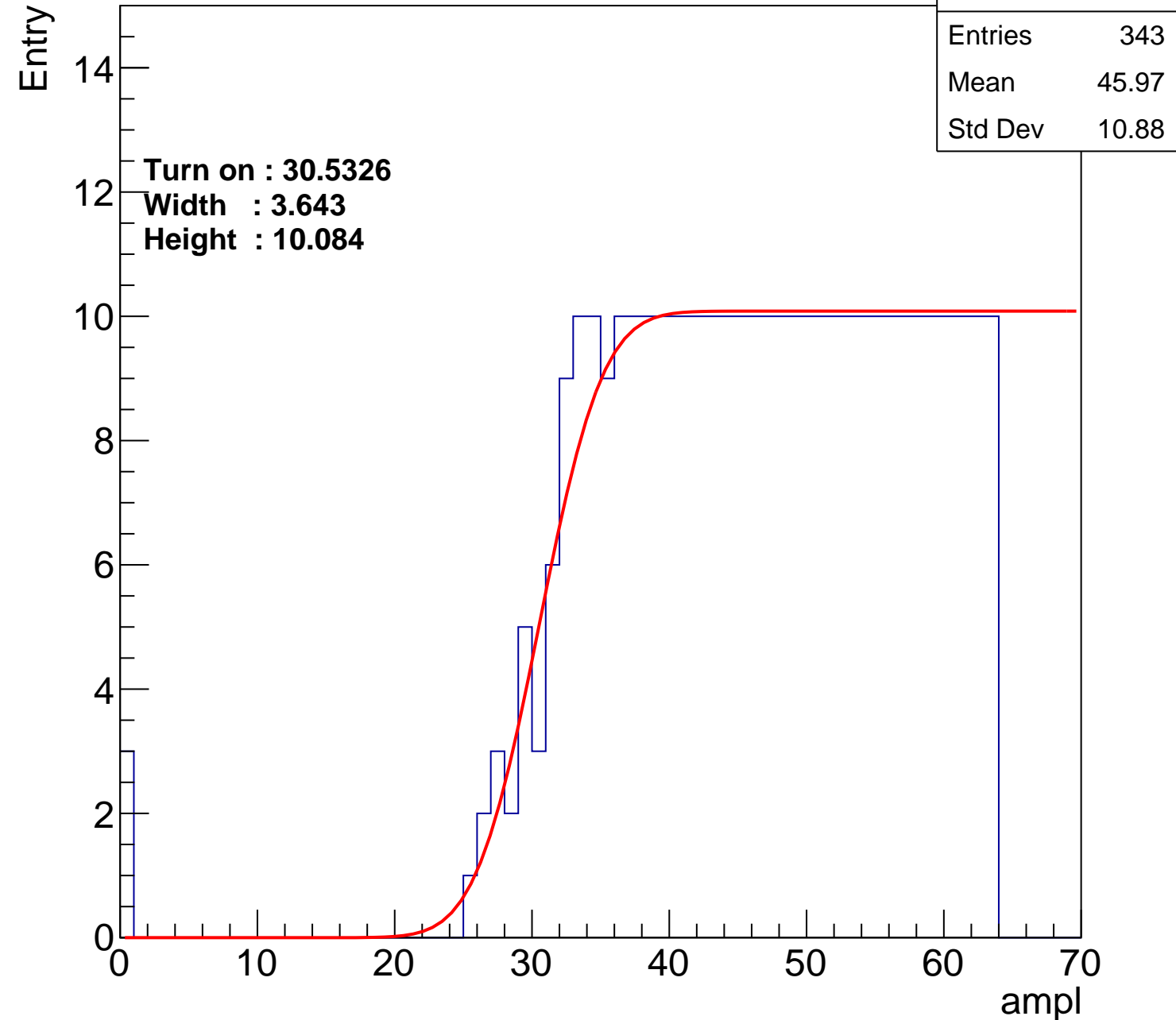
Width : 3.643

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch61

calib_packv5_042523_0143.root, FC#9, port A1

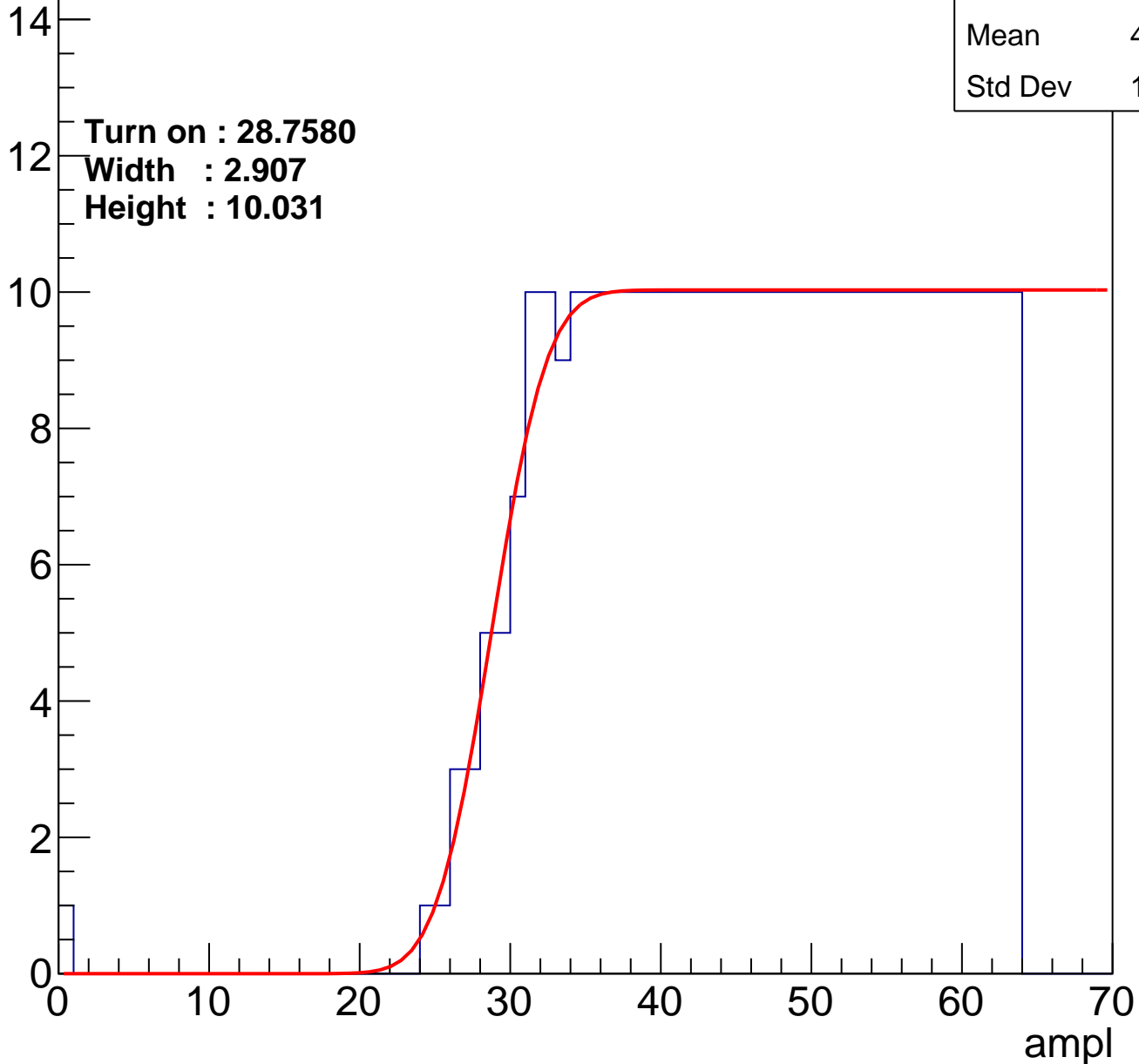
Entries	355
Mean	45.58
Std Dev	10.64

Turn on : 28.7580

Width : 2.907

Height : 10.031

Entry



B0L001S, U14-ch62

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.12
Std Dev	11.44

Turn on : 28.9341

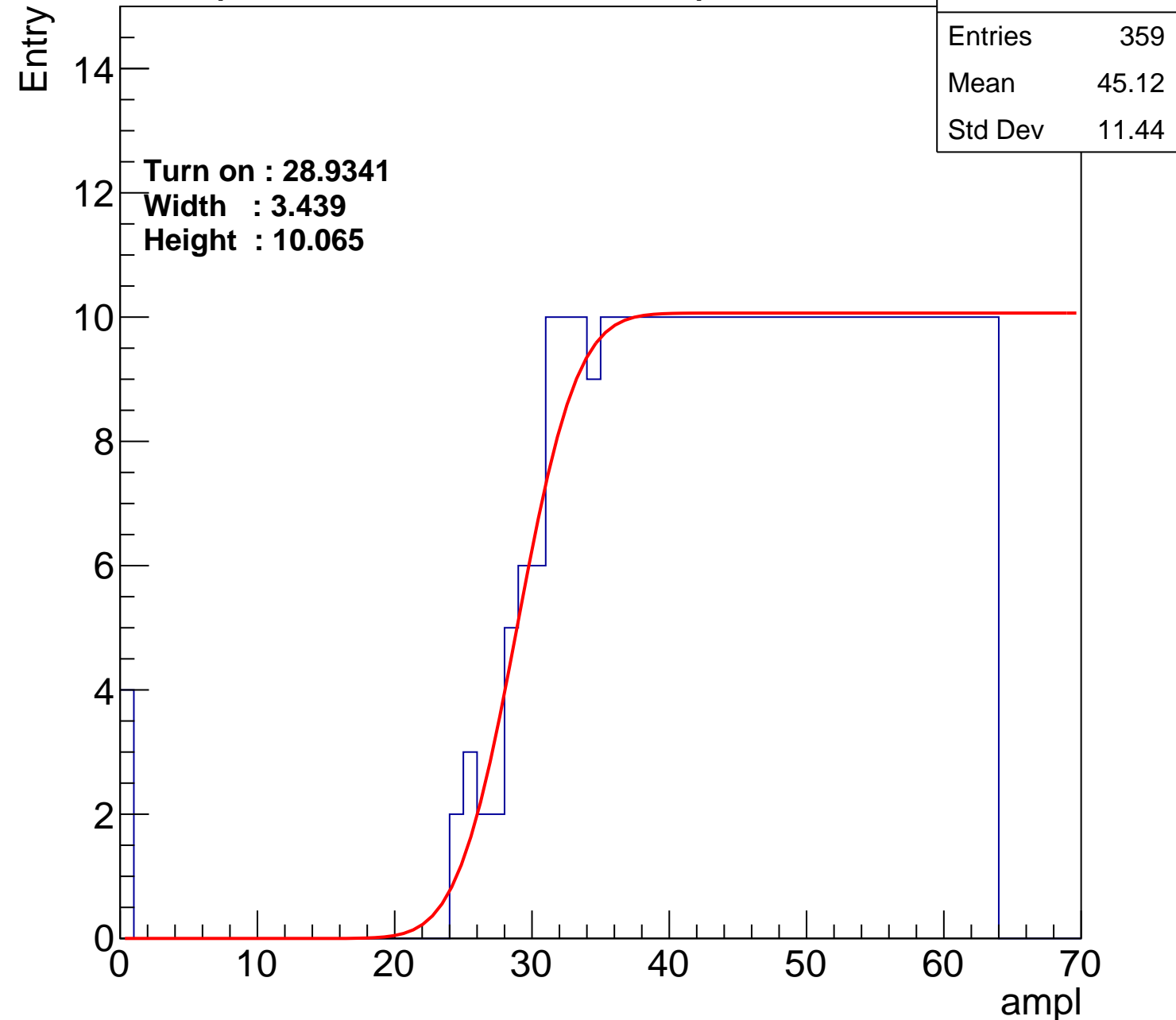
Width : 3.439

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch63

calib_packv5_042523_0143.root, FC#9, port A1

Entries	396
Mean	43.31
Std Dev	12.37

Turn on : 24.9727

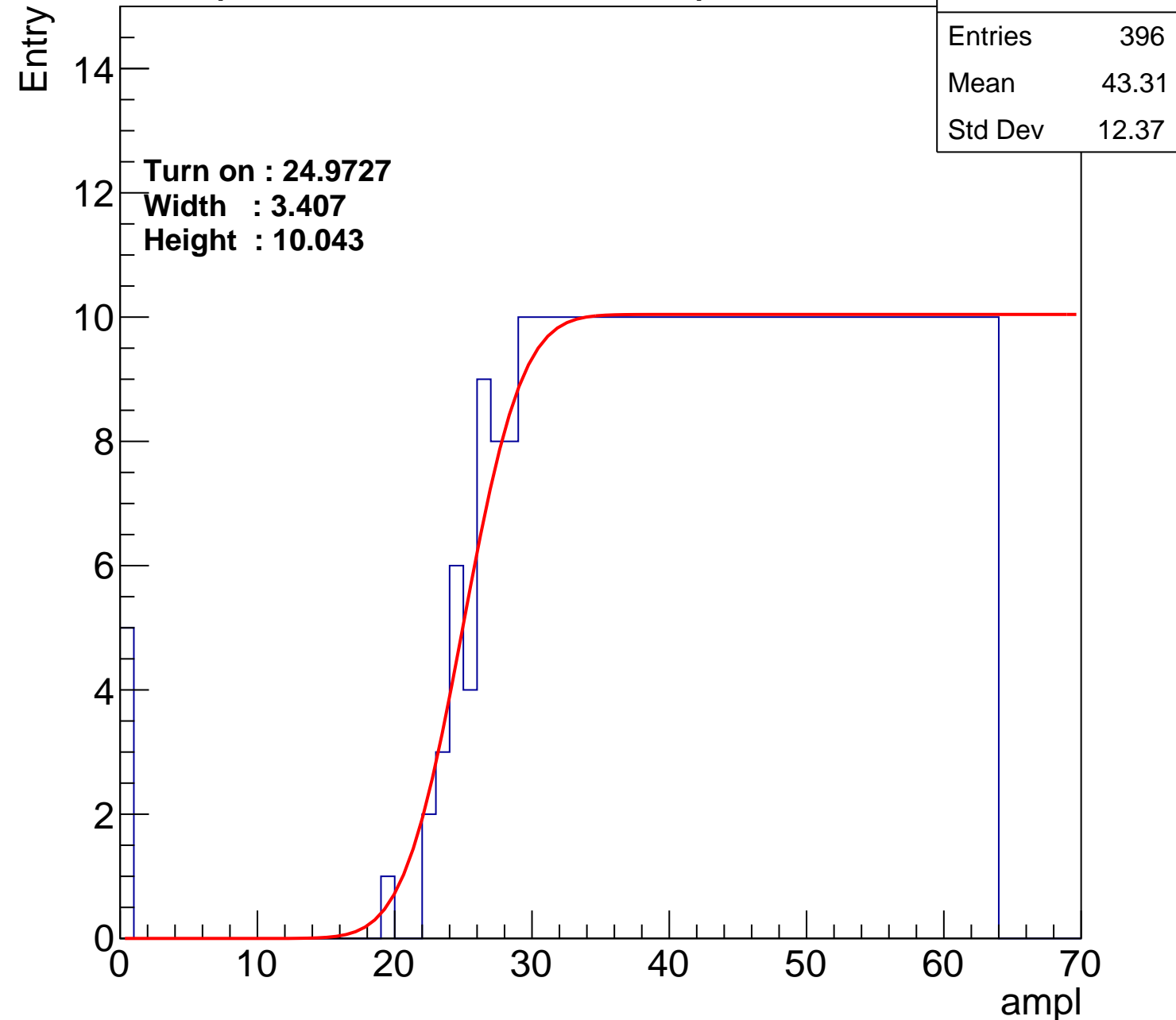
Width : 3.407

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch64

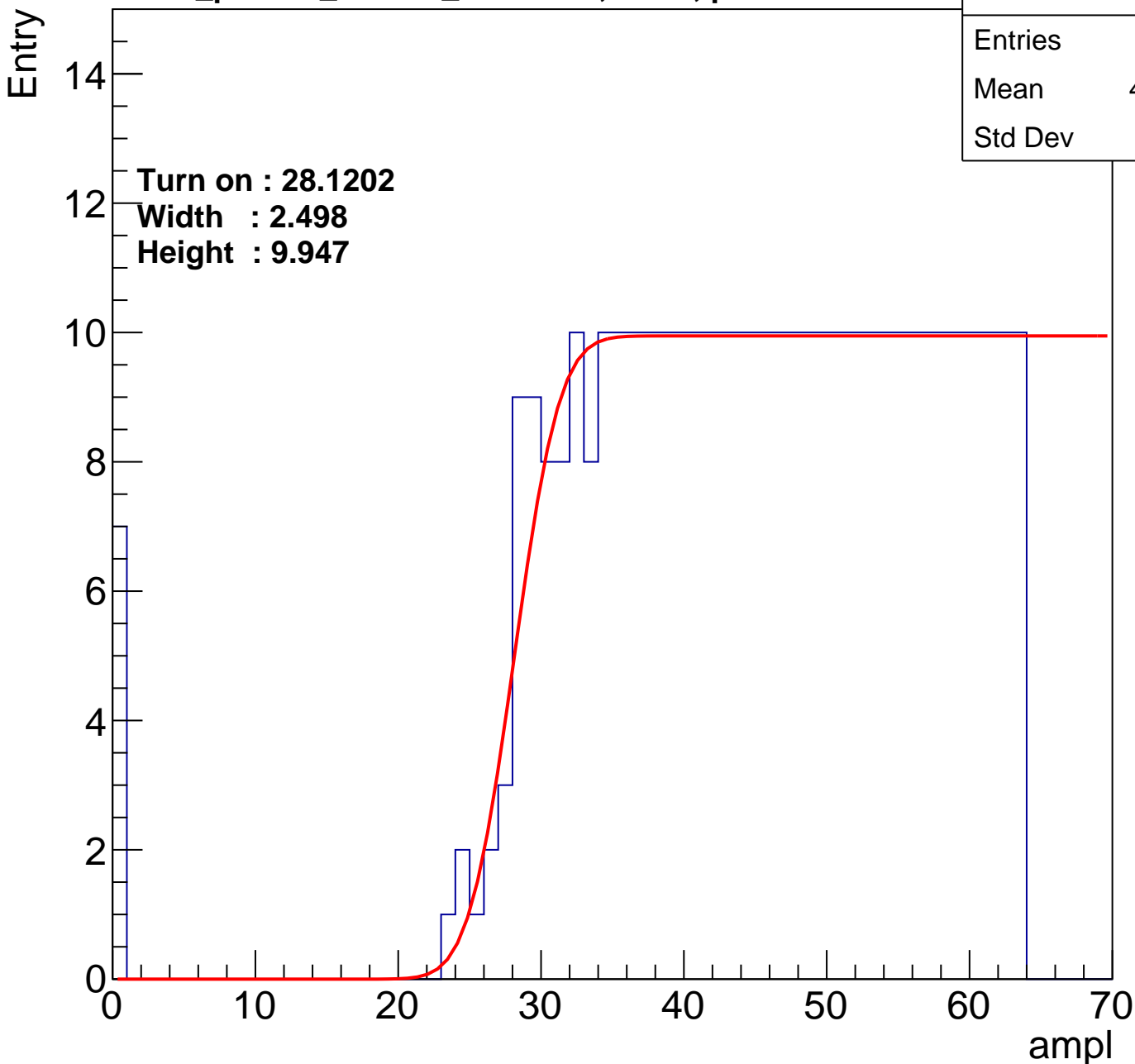
calib_packv5_042523_0143.root, FC#9, port A1

Entries	368
Mean	44.47
Std Dev	12.2

Turn on : 28.1202

Width : 2.498

Height : 9.947



B0L001S, U14-ch65

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.94
Std Dev	11.23

Turn on : 27.3924

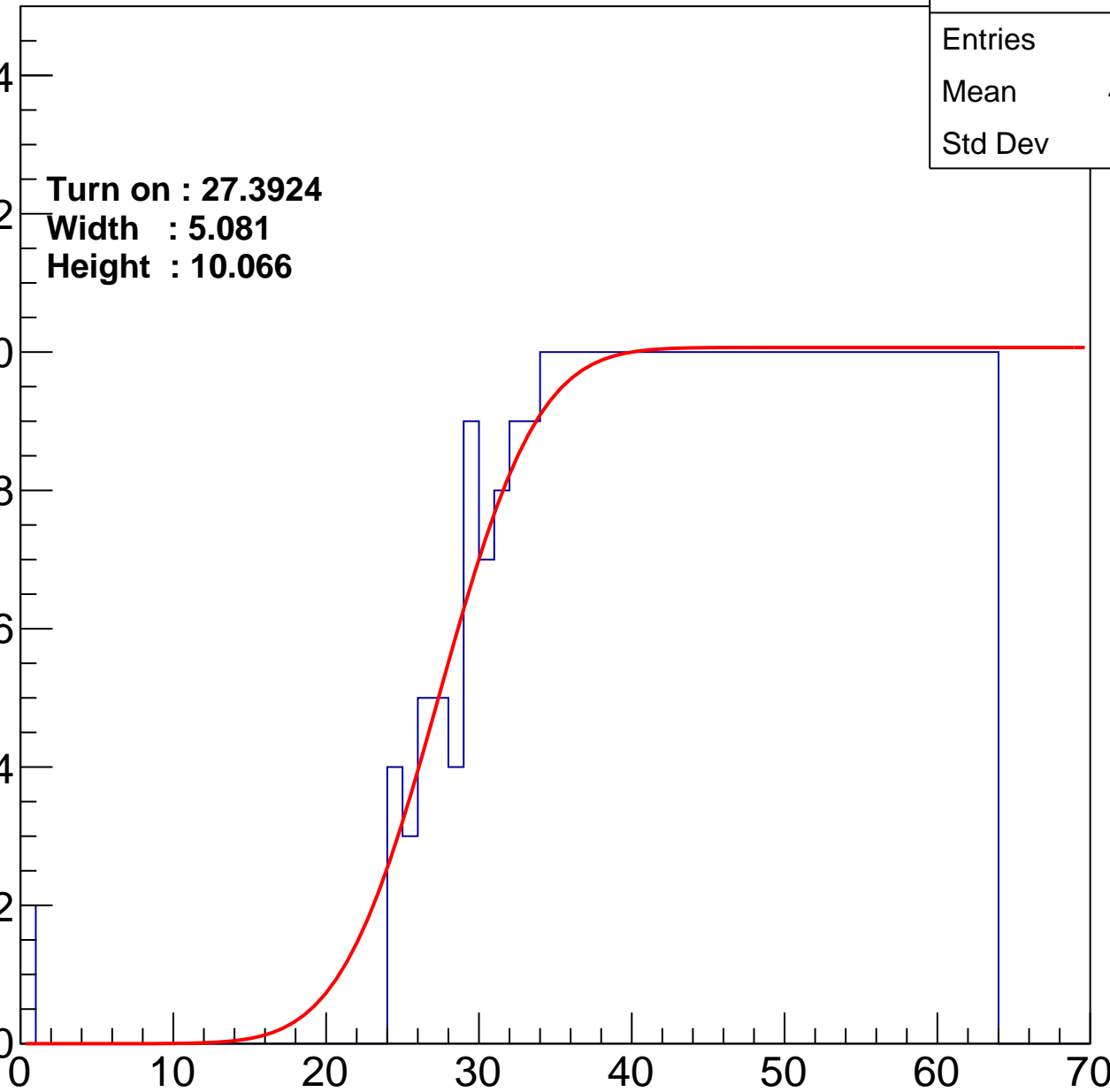
Width : 5.081

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch66

calib_packv5_042523_0143.root, FC#9, port A1

Entries	354
Mean	45.51
Std Dev	10.9

Turn on : 29.4626

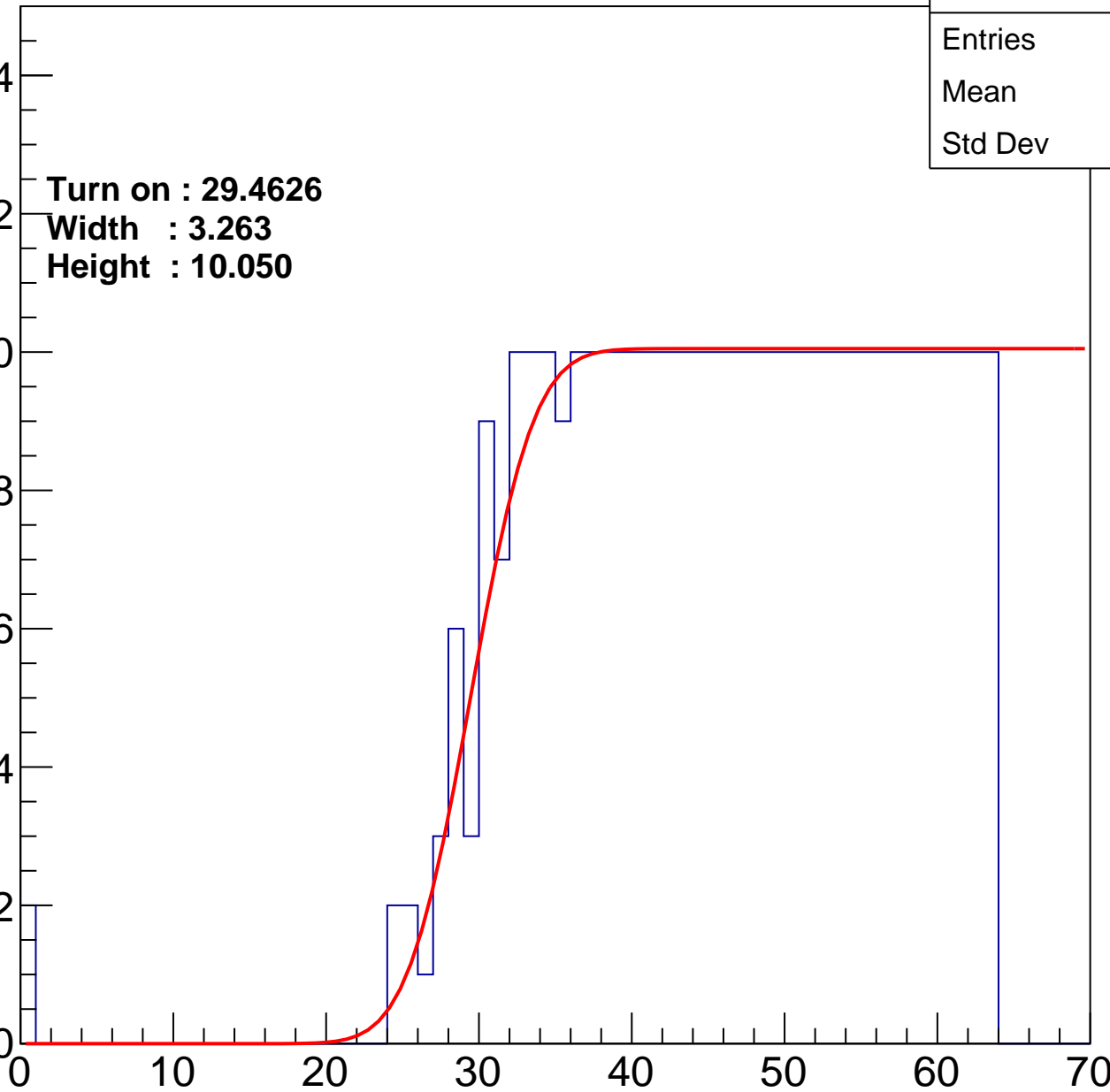
Width : 3.263

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch67

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.14
Std Dev	12.03

Turn on : 26.8825

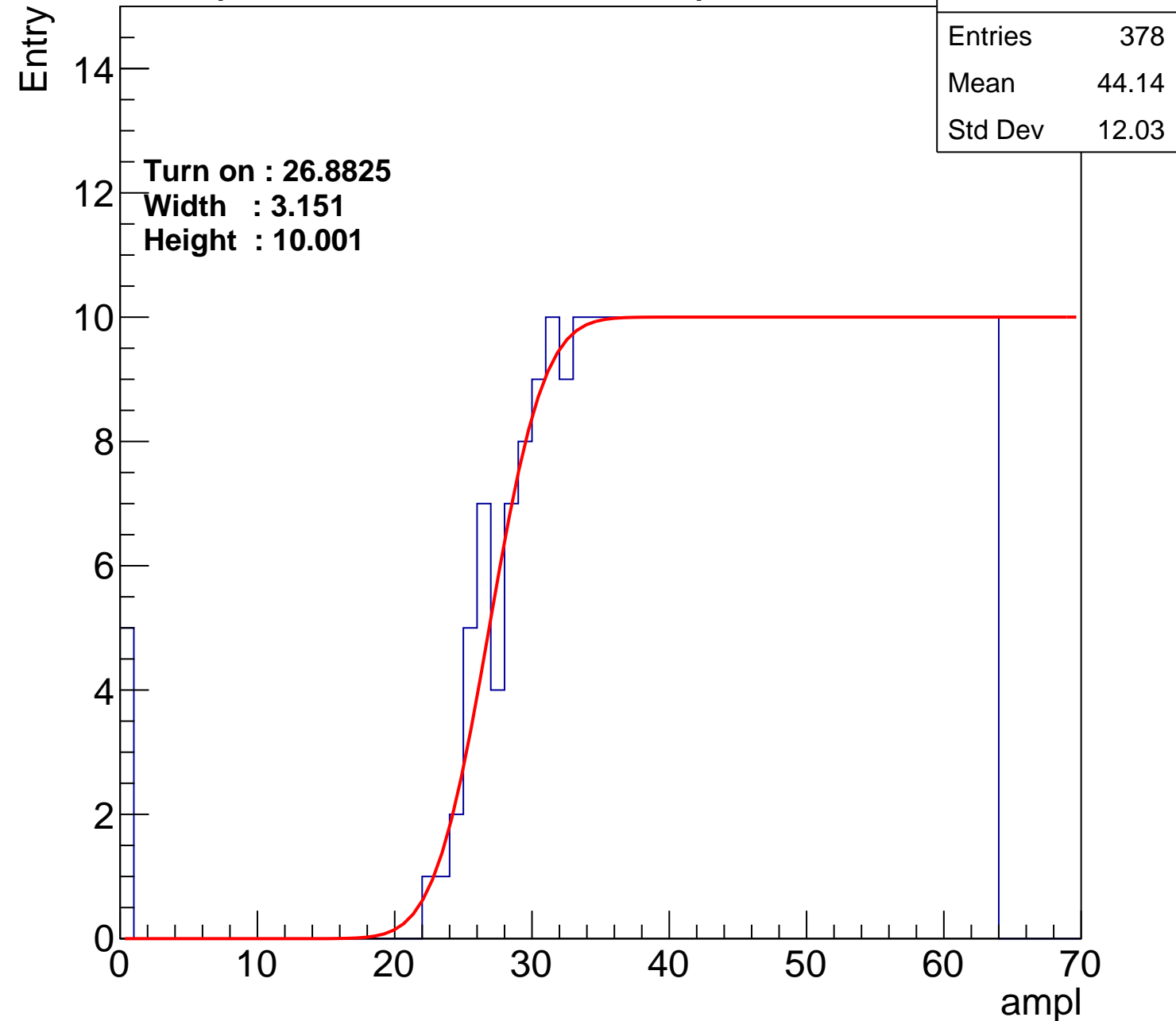
Width : 3.151

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch68

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.29
Std Dev	11.66

Turn on : 26.4915

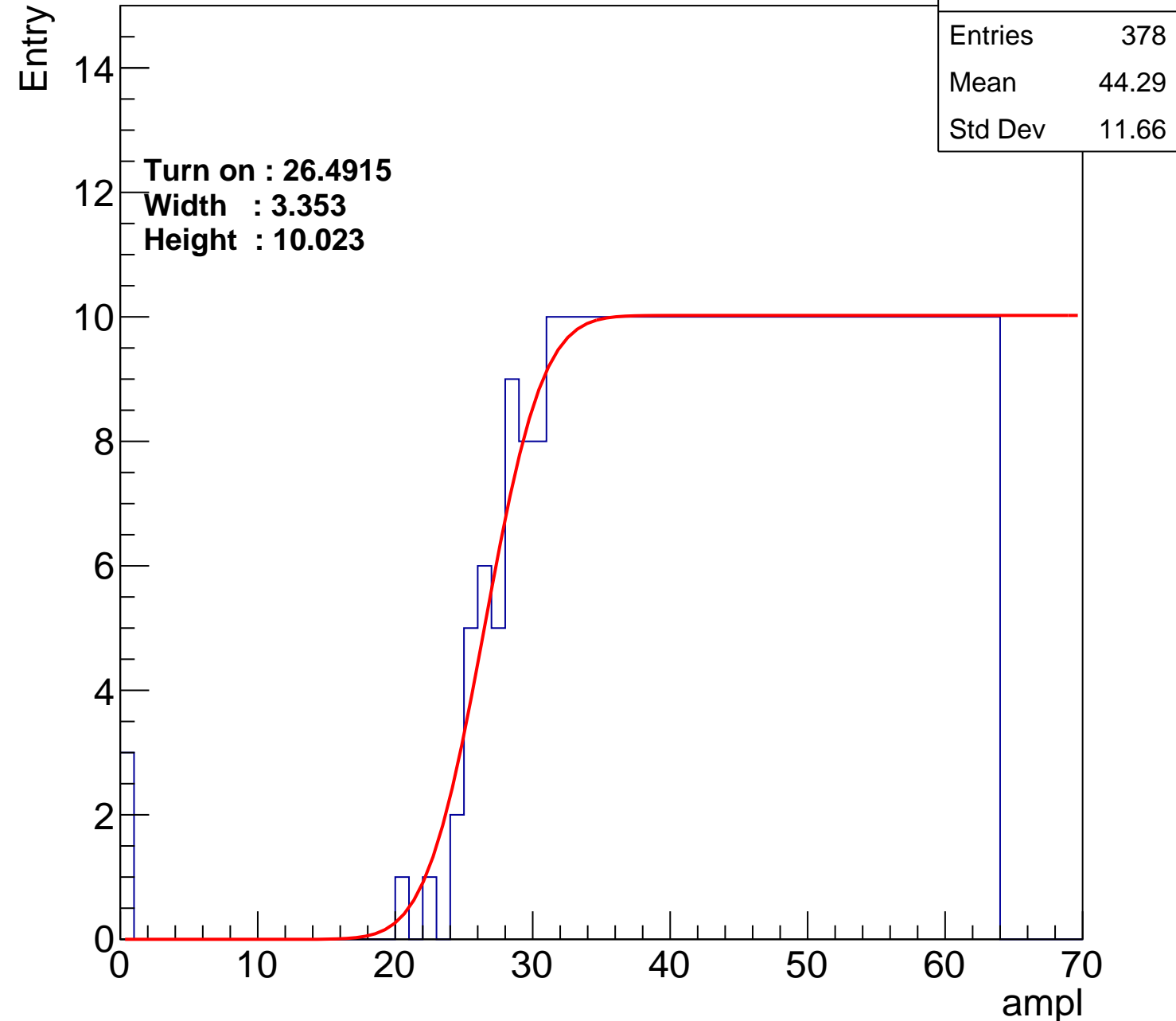
Width : 3.353

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch69

calib_packv5_042523_0143.root, FC#9, port A1

Entries	387
Mean	43.83
Std Dev	11.92

Turn on : 26.5647

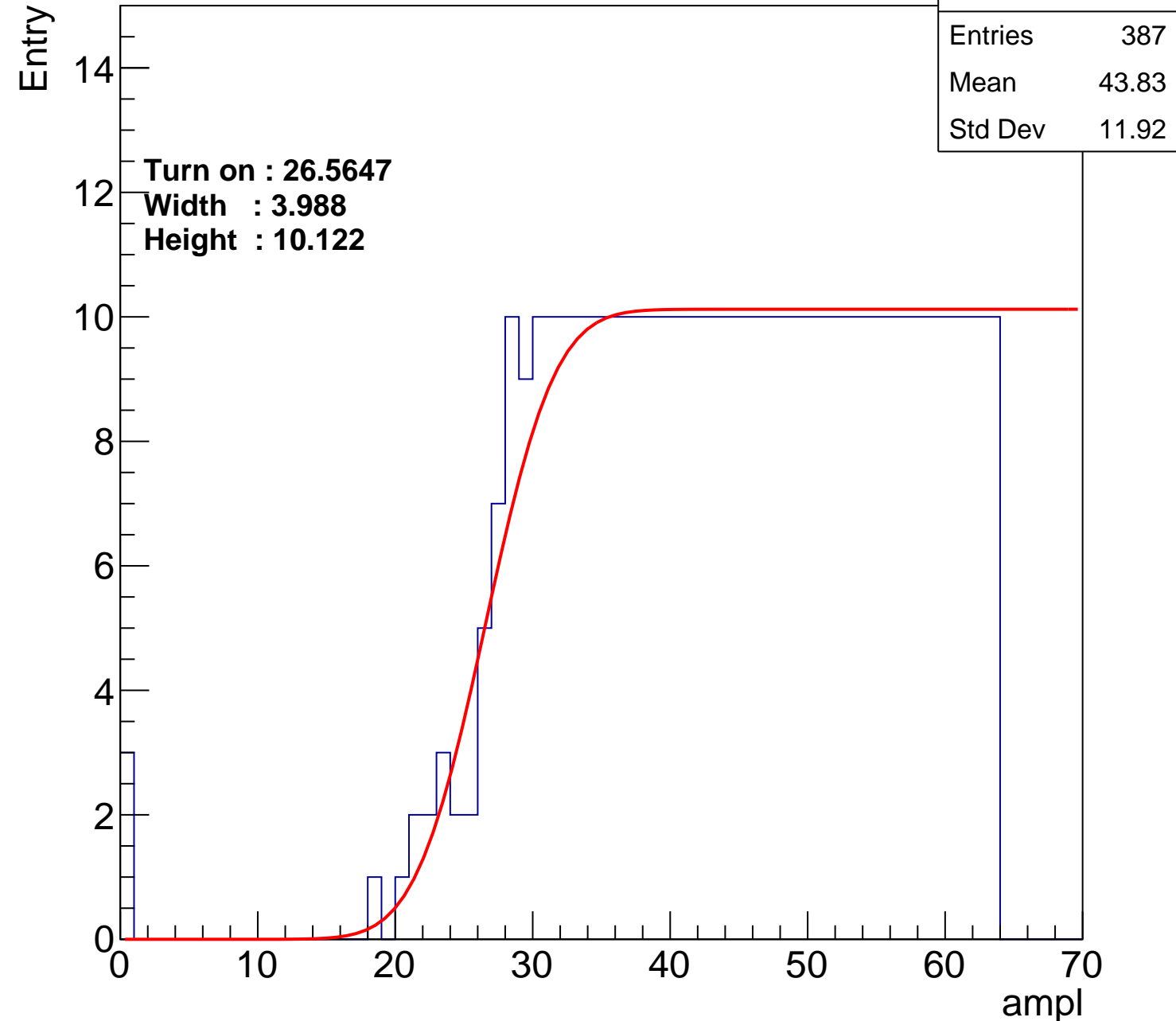
Width : 3.988

Height : 10.122

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch70

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.83
Std Dev	11.29

Turn on : 27.5292

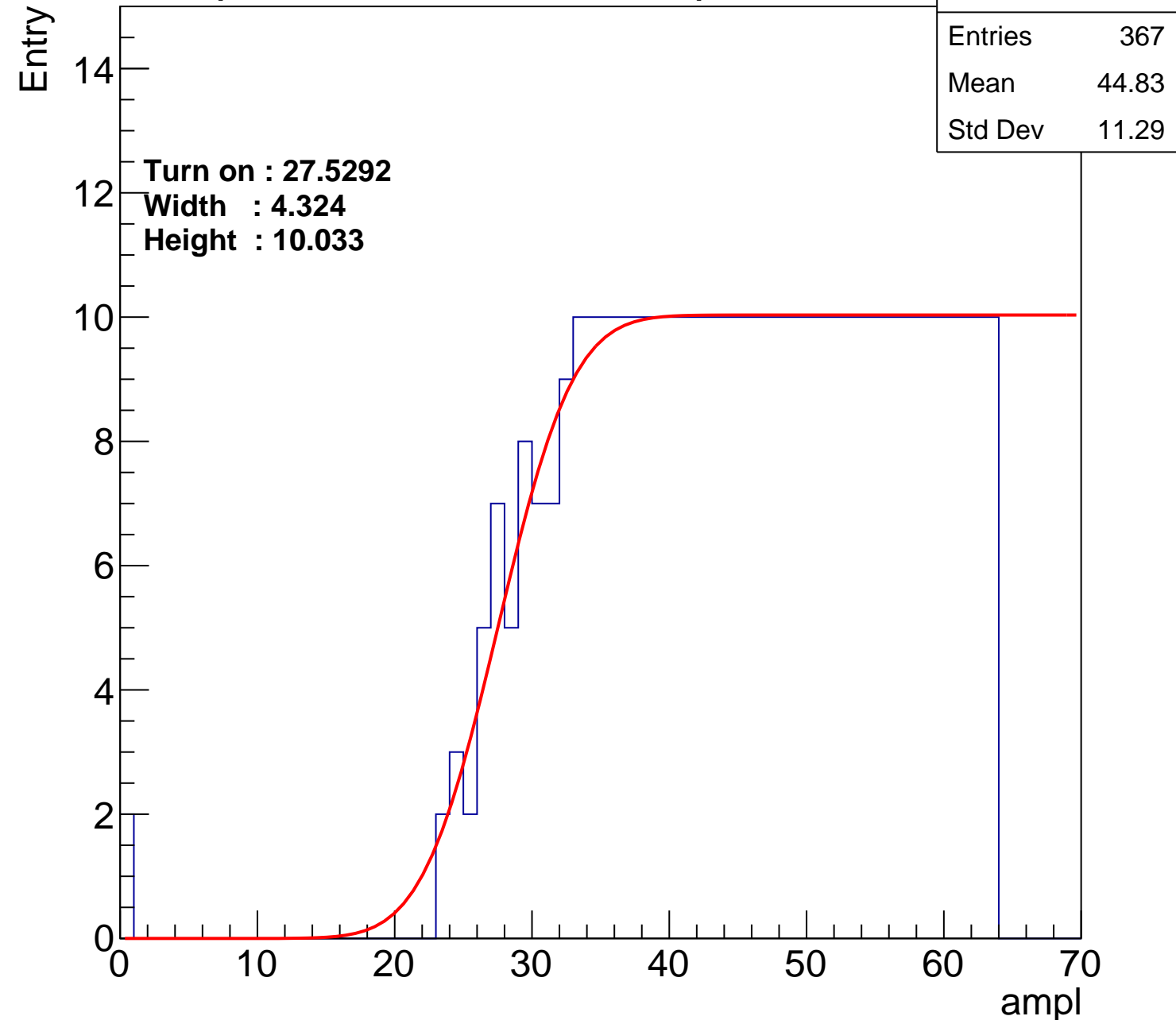
Width : 4.324

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch71

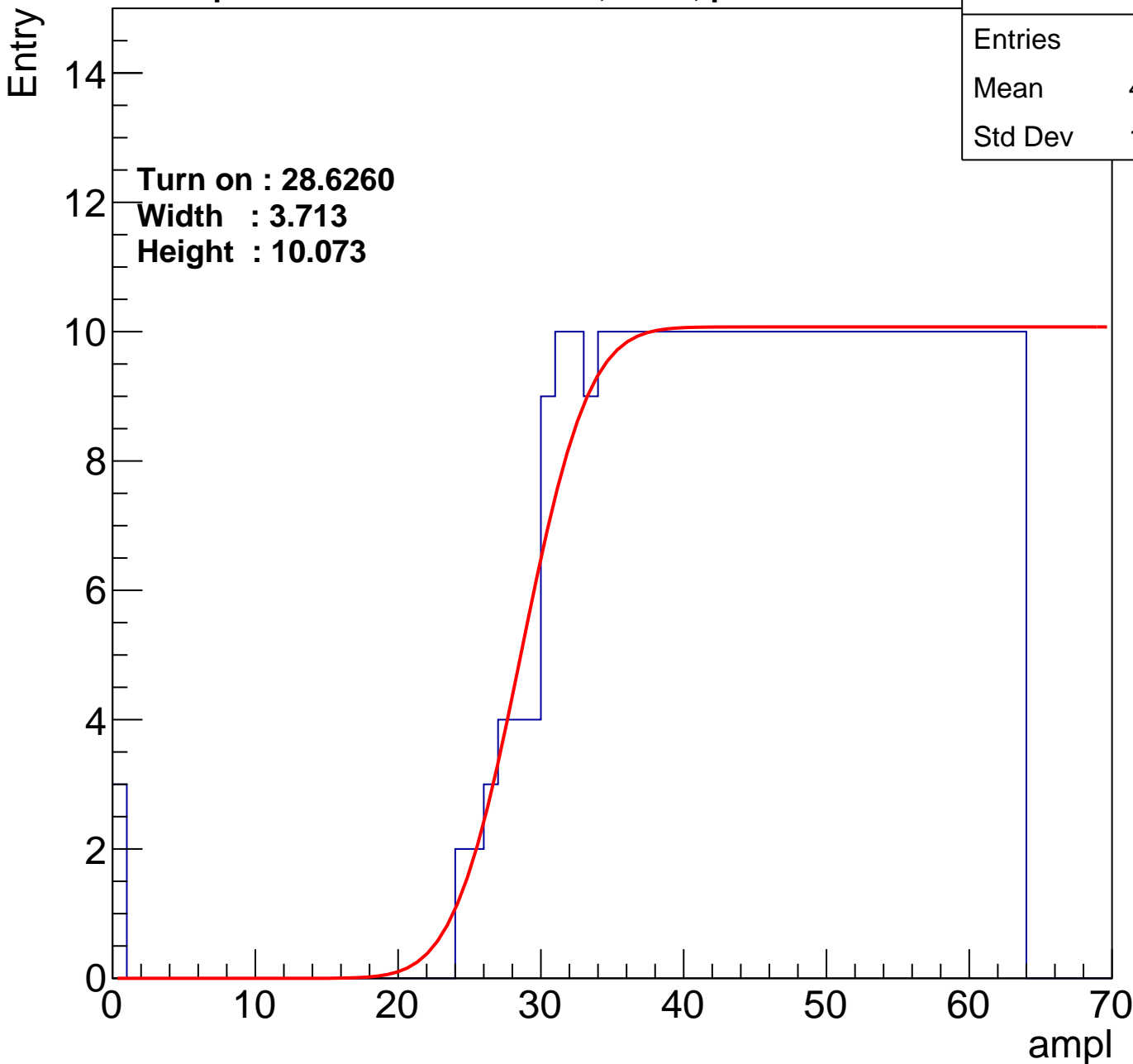
calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.16
Std Dev	11.23

Turn on : 28.6260

Width : 3.713

Height : 10.073



B0L001S, U14-ch72

calib_packv5_042523_0143.root, FC#9, port A1

Entries	383
Mean	43.95
Std Dev	11.99

Turn on : 26.3508

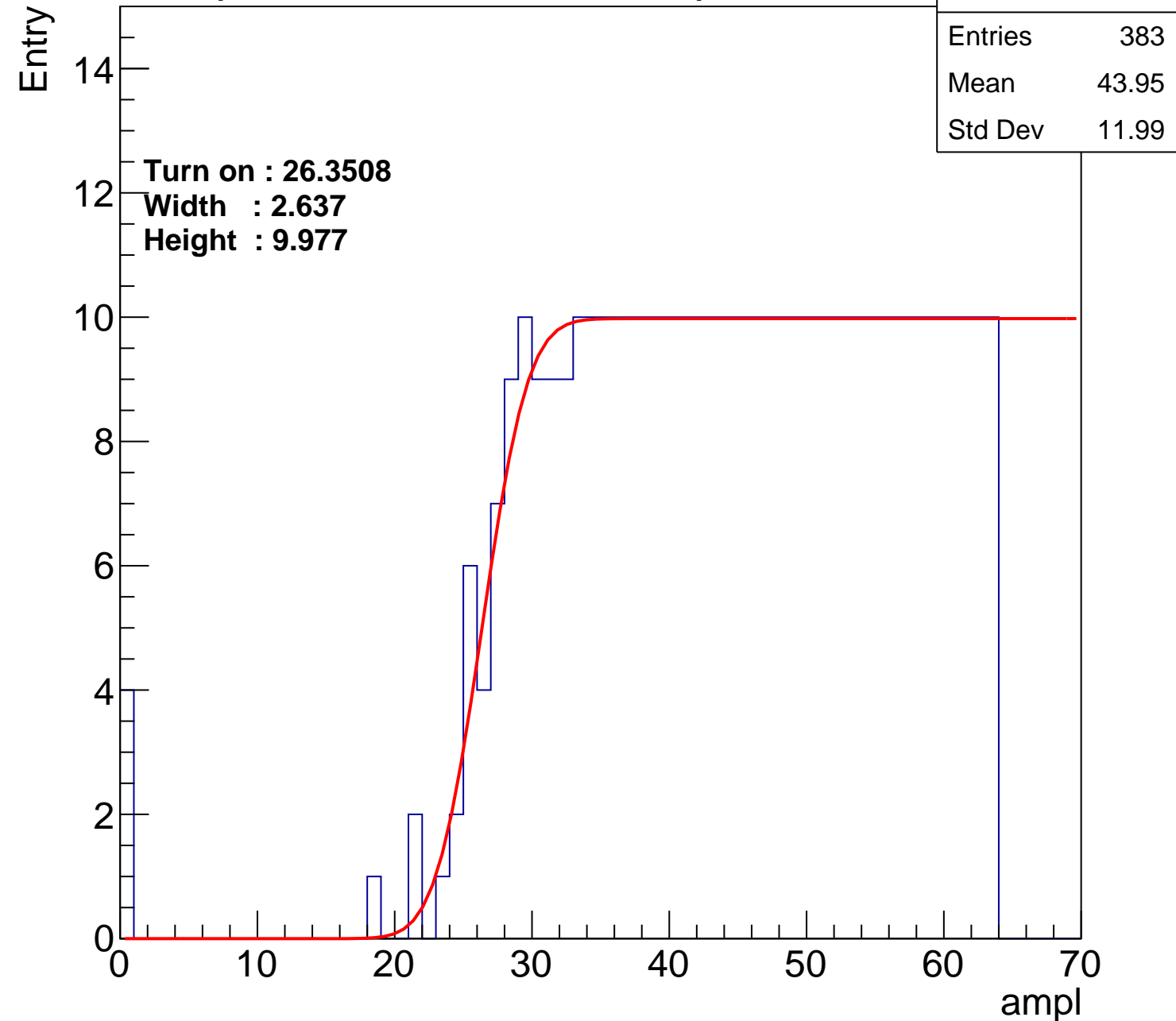
Width : 2.637

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch73

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.96
Std Dev	11.34

Turn on : 28.0809

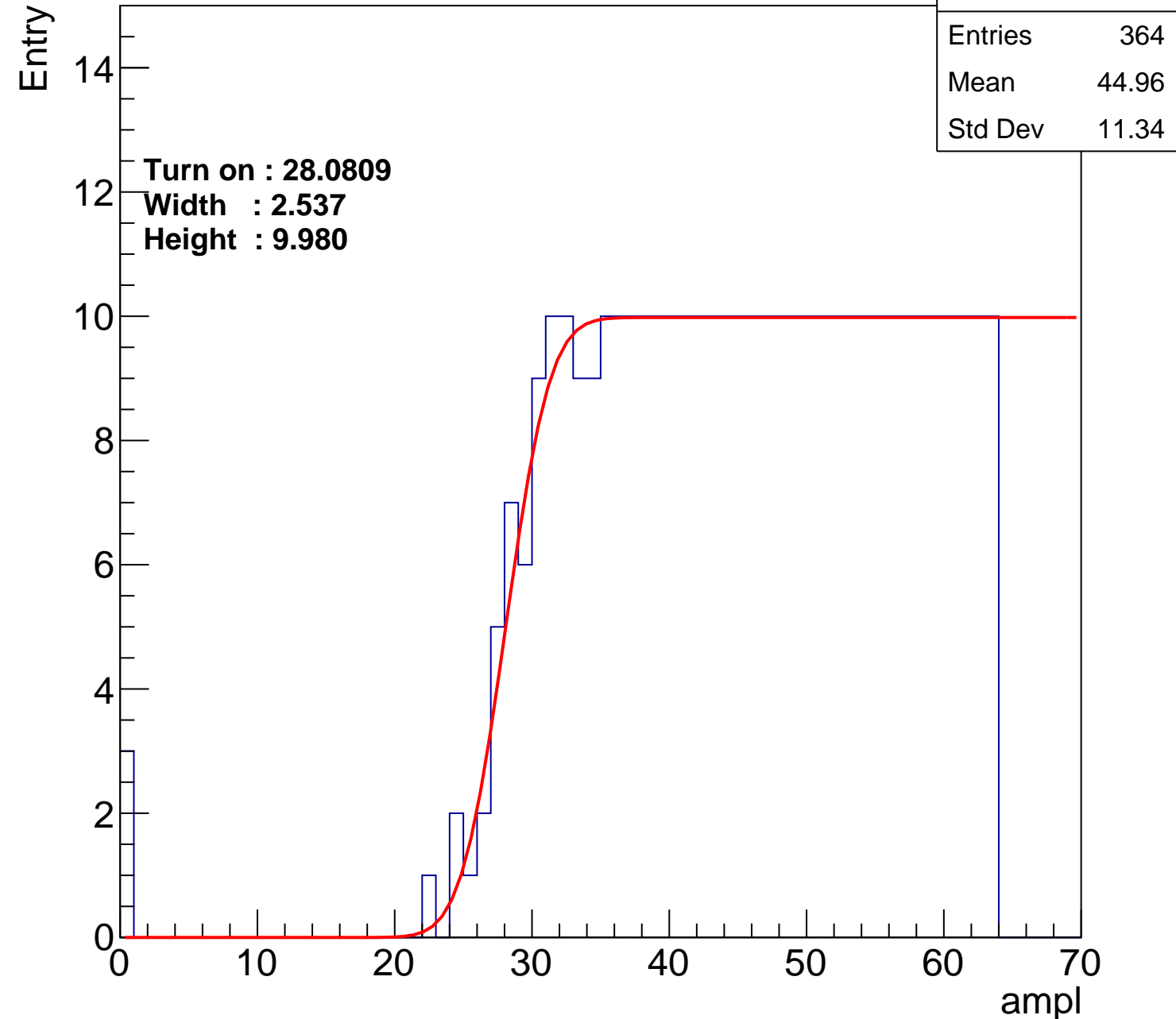
Width : 2.537

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch74

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.82
Std Dev	11.58

Turn on : 28.2118

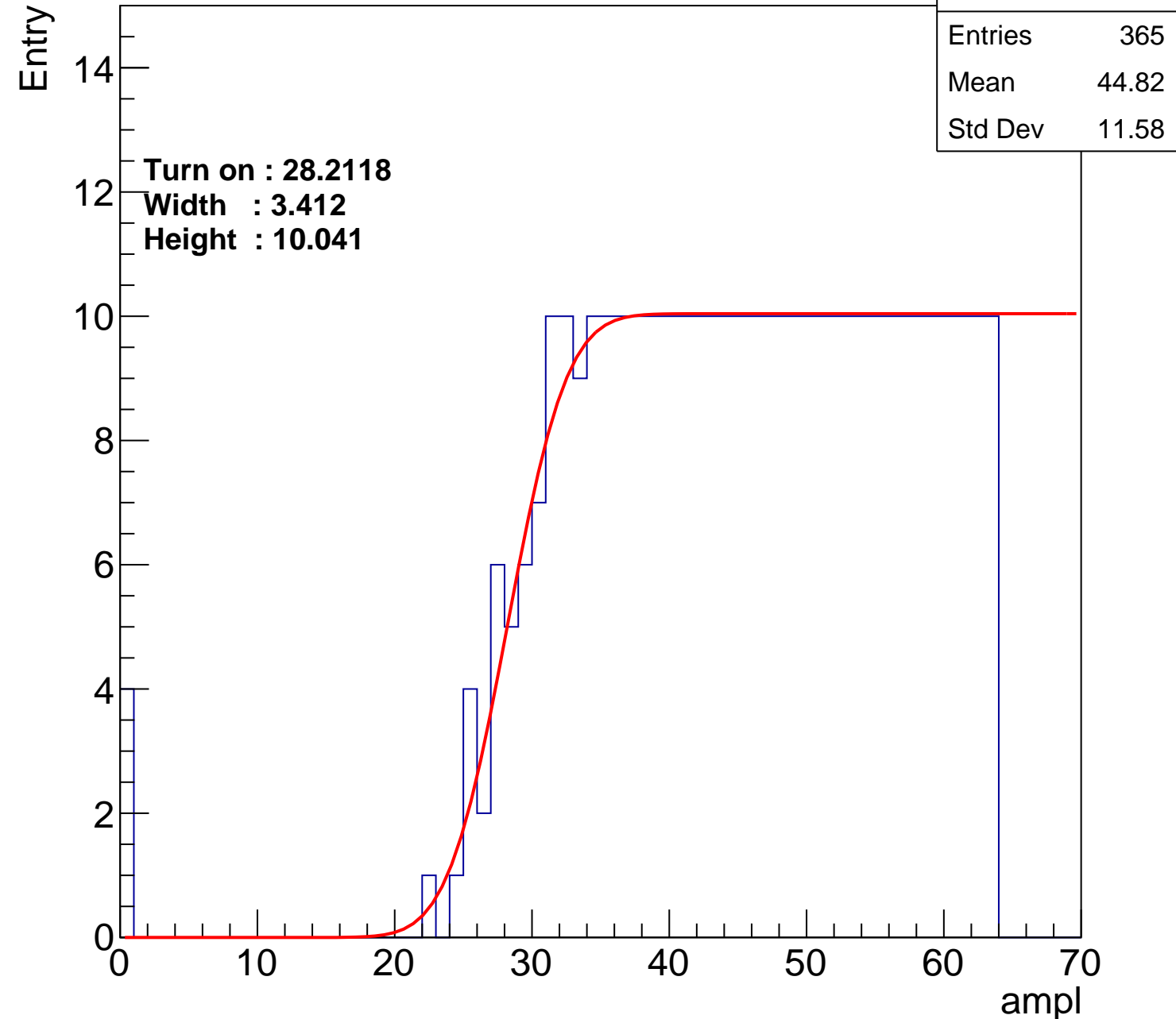
Width : 3.412

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch75

calib_packv5_042523_0143.root, FC#9, port A1

Entries	361
Mean	45.07
Std Dev	11.42

Turn on : 28.6061

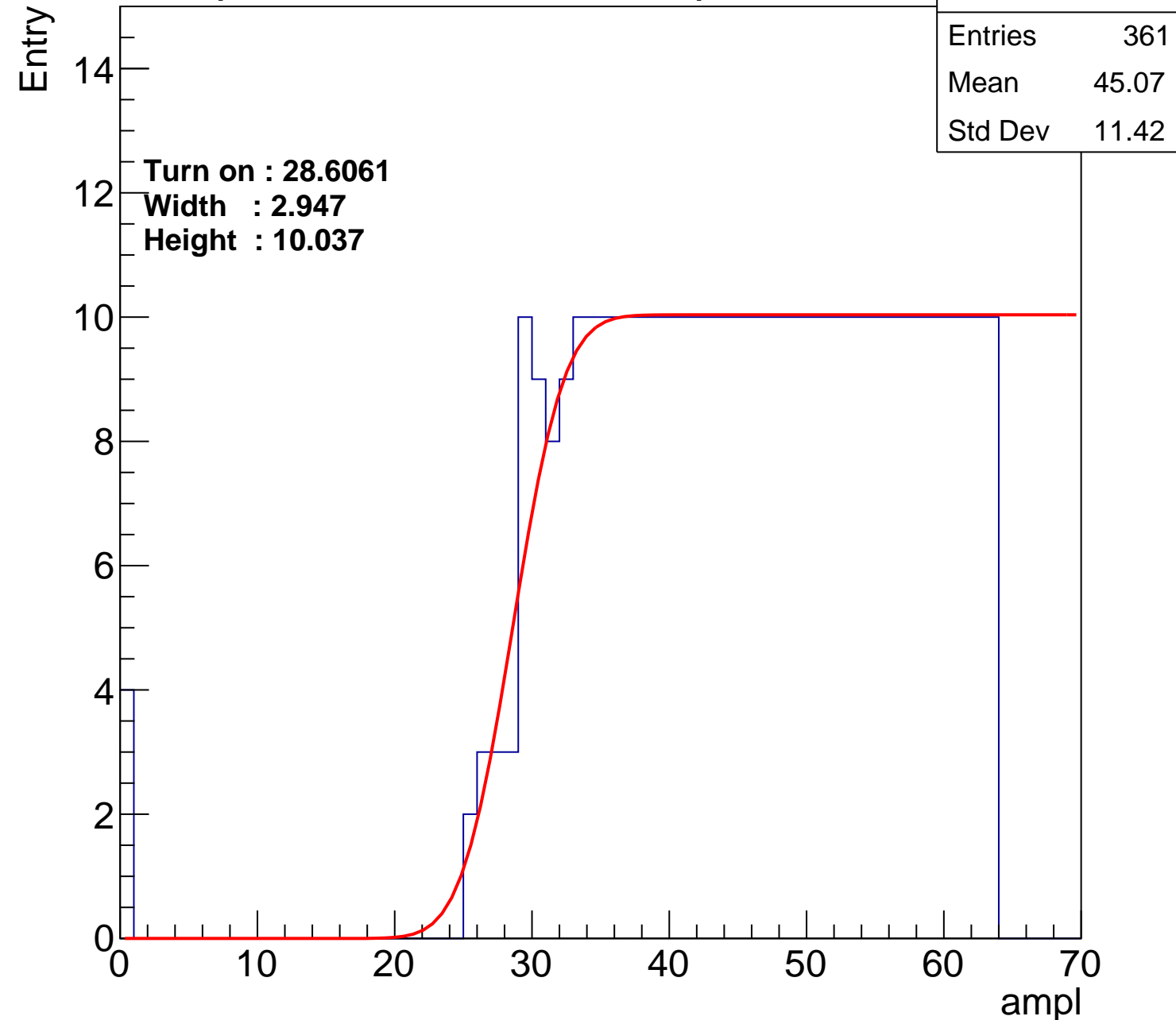
Width : 2.947

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch76

calib_packv5_042523_0143.root, FC#9, port A1

Entries	342
Mean	46.07
Std Dev	10.65

Turn on : 30.2422

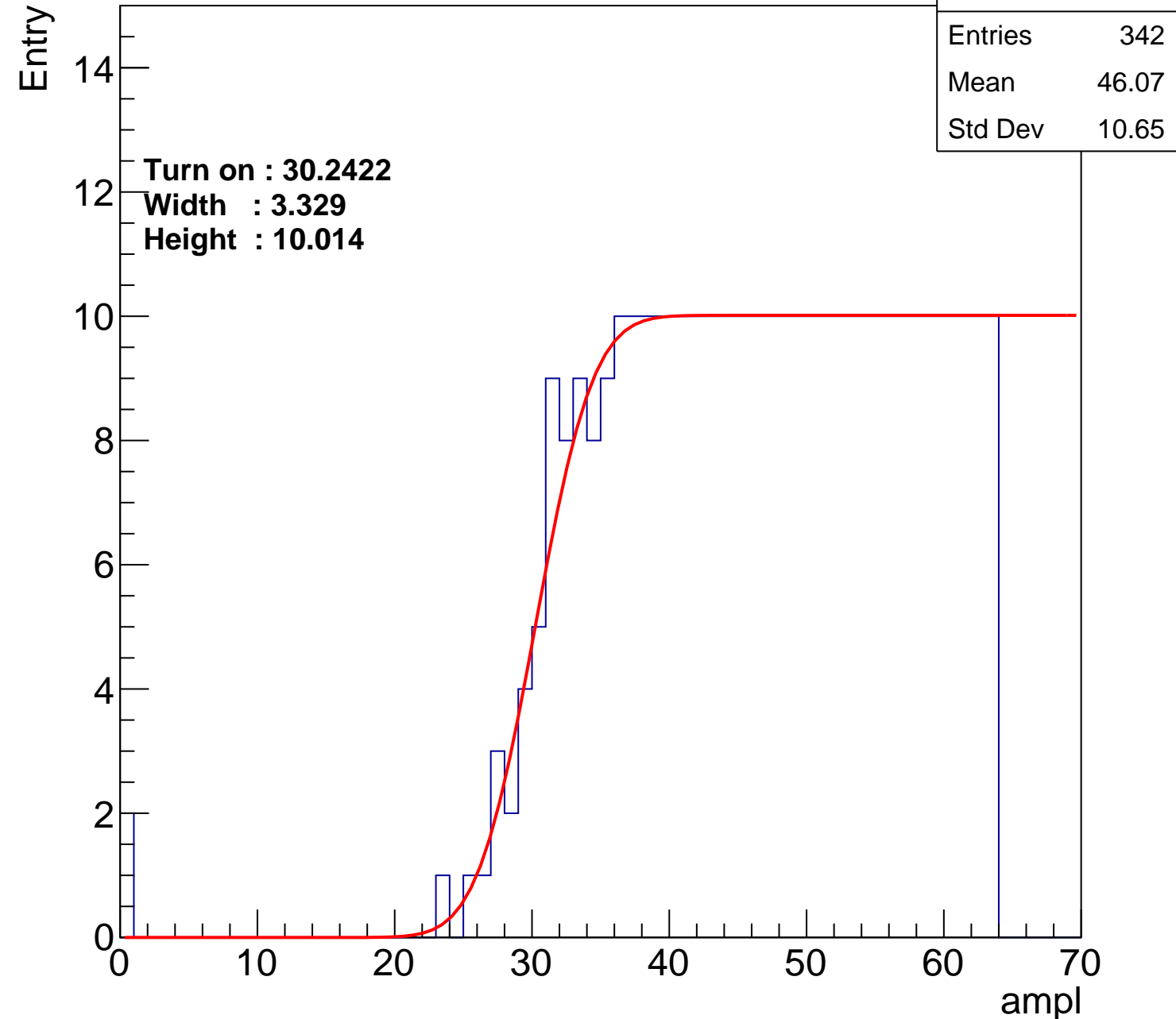
Width : 3.329

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch77

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.23
Std Dev	11.75

Turn on : 26.7417

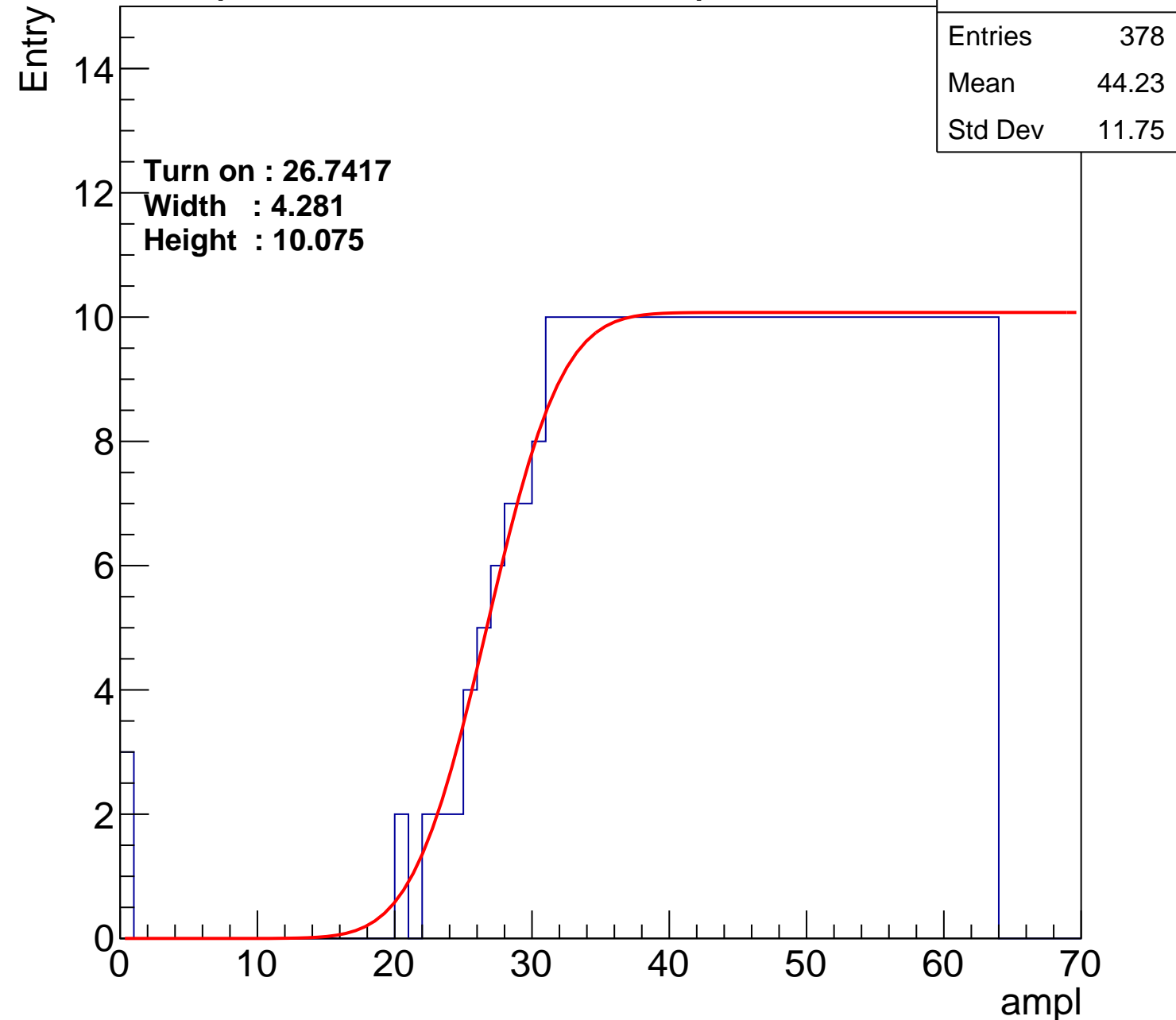
Width : 4.281

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch78

calib_packv5_042523_0143.root, FC#9, port A1

Entries	361
Mean	45.04
Std Dev	11.46

Turn on : 28.5549

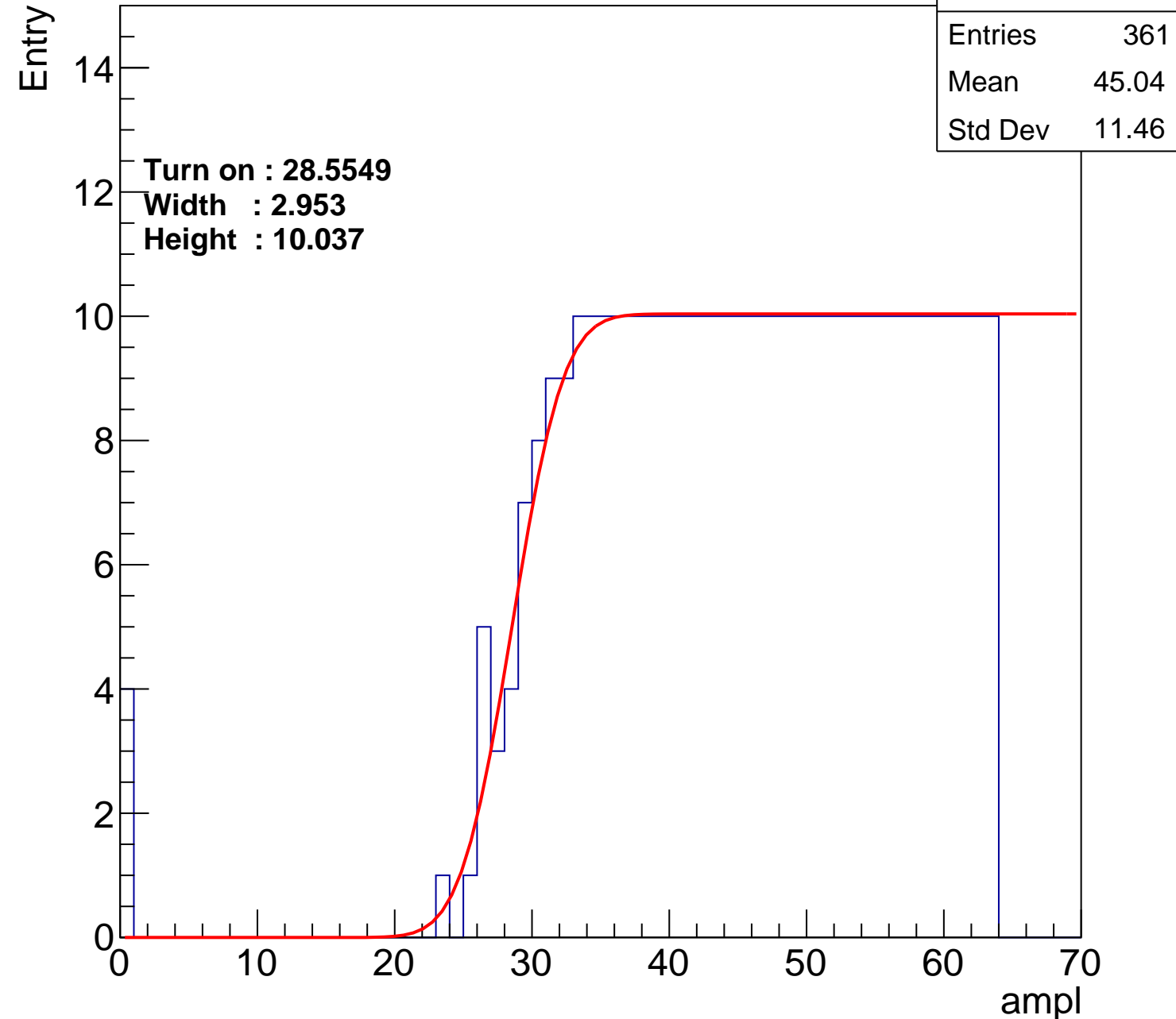
Width : 2.953

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch79

calib_packv5_042523_0143.root, FC#9, port A1

Entries	358
Mean	44.93
Std Dev	12.05

Turn on : 30.8709

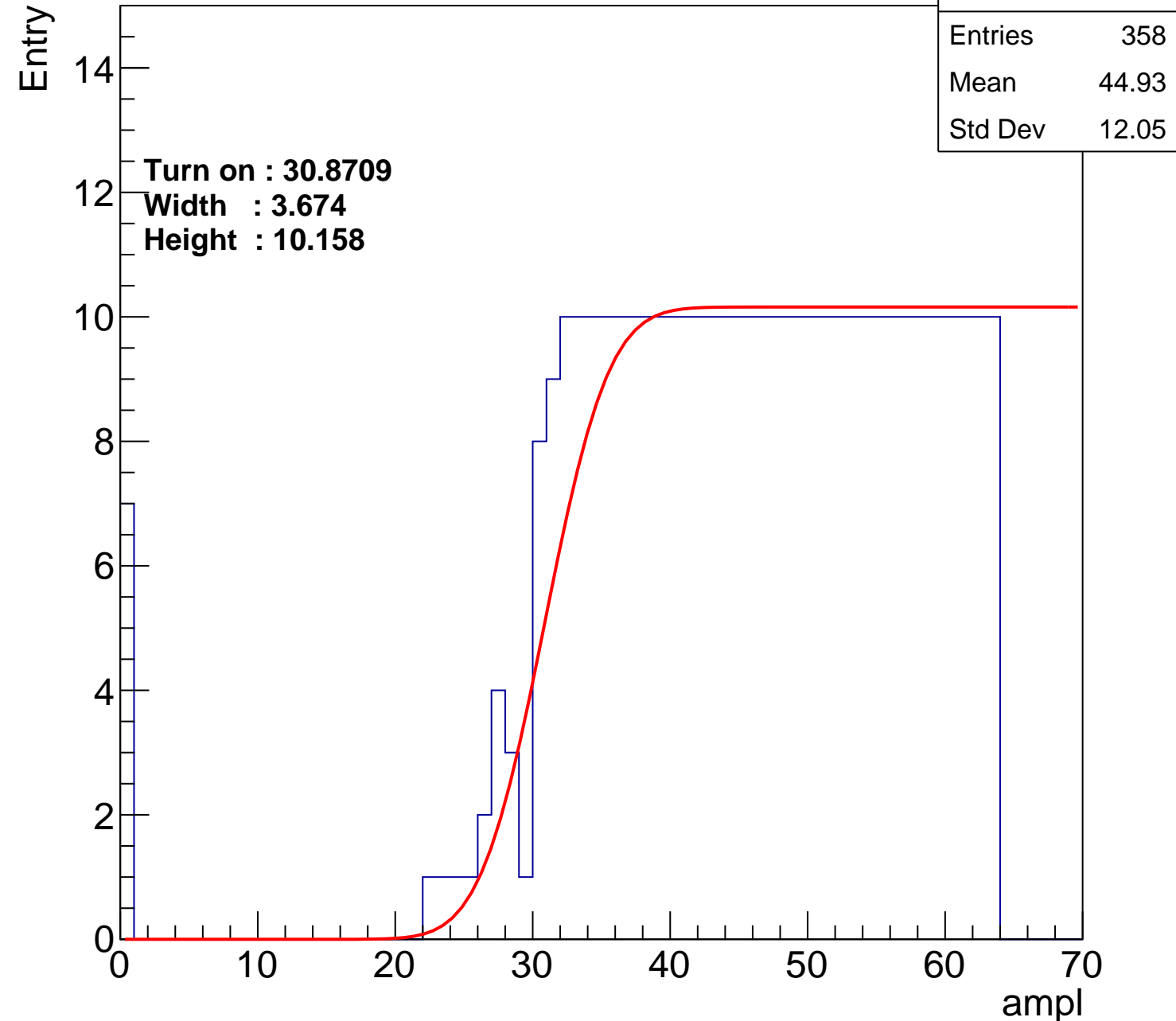
Width : 3.674

Height : 10.158

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch80

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	45.09
Std Dev	10.95

Turn on : 27.6165

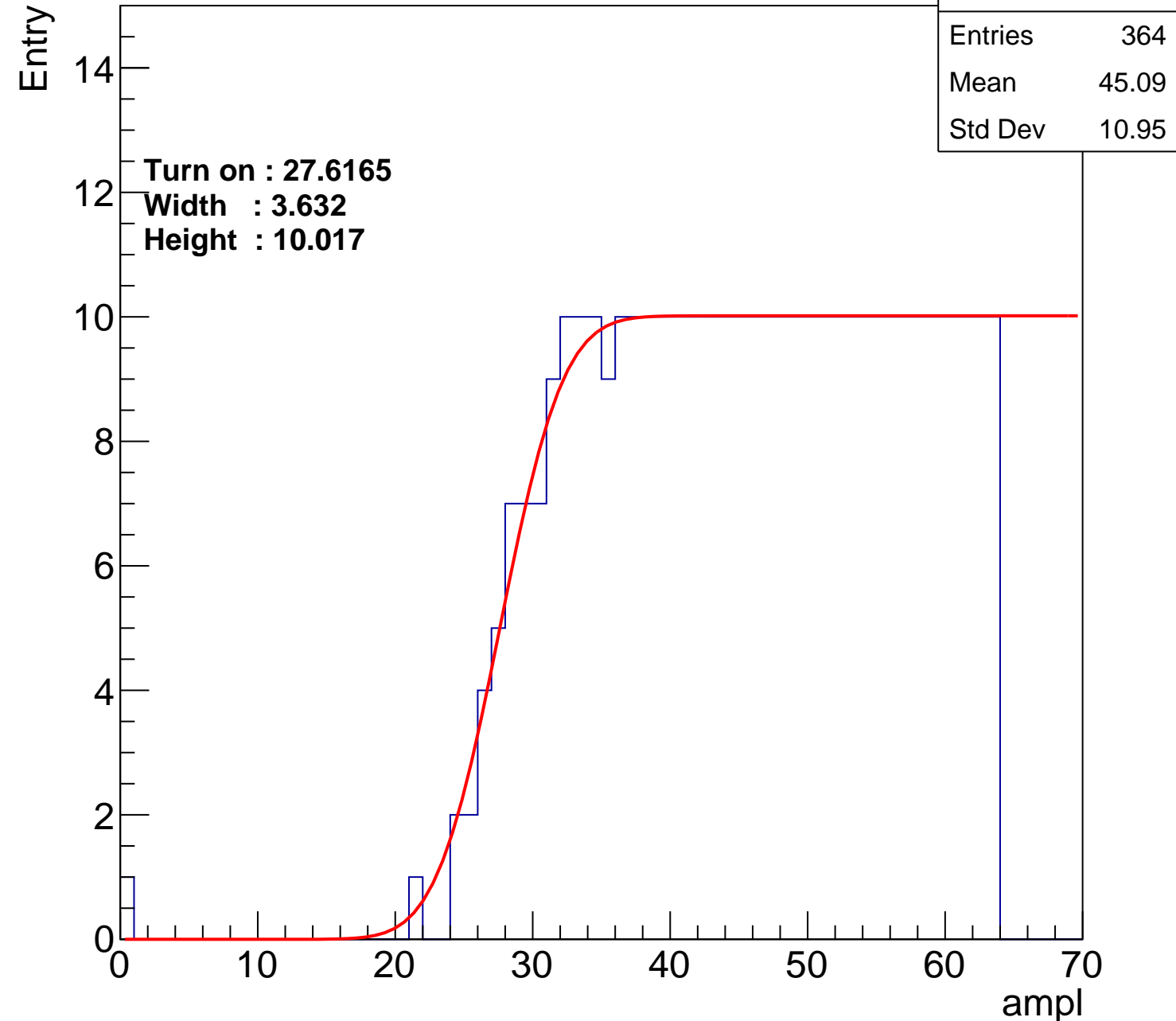
Width : 3.632

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch81

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 27.1691

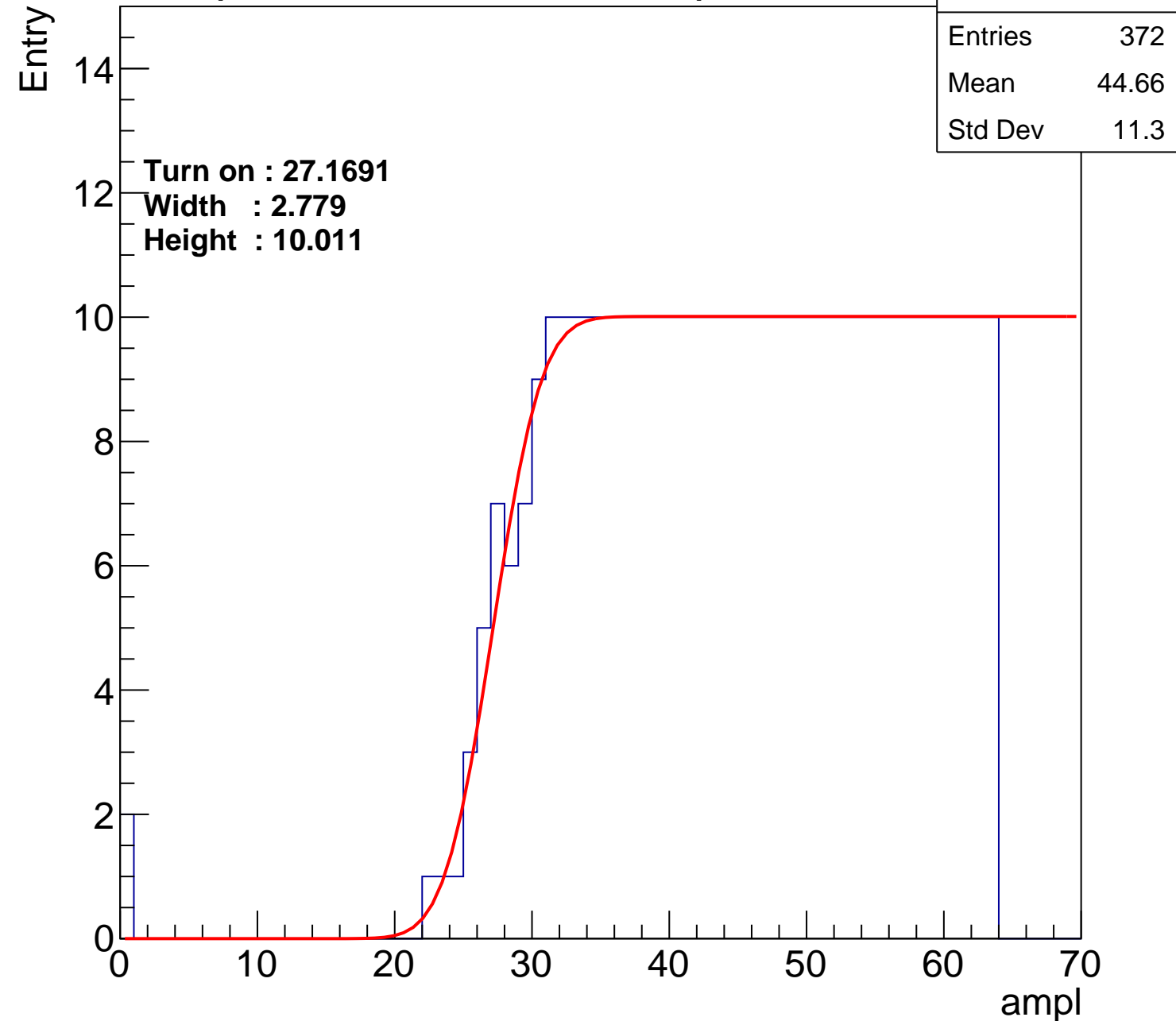
Width : 2.779

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch82

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.2
Std Dev	11.13

Turn on : 28.1403

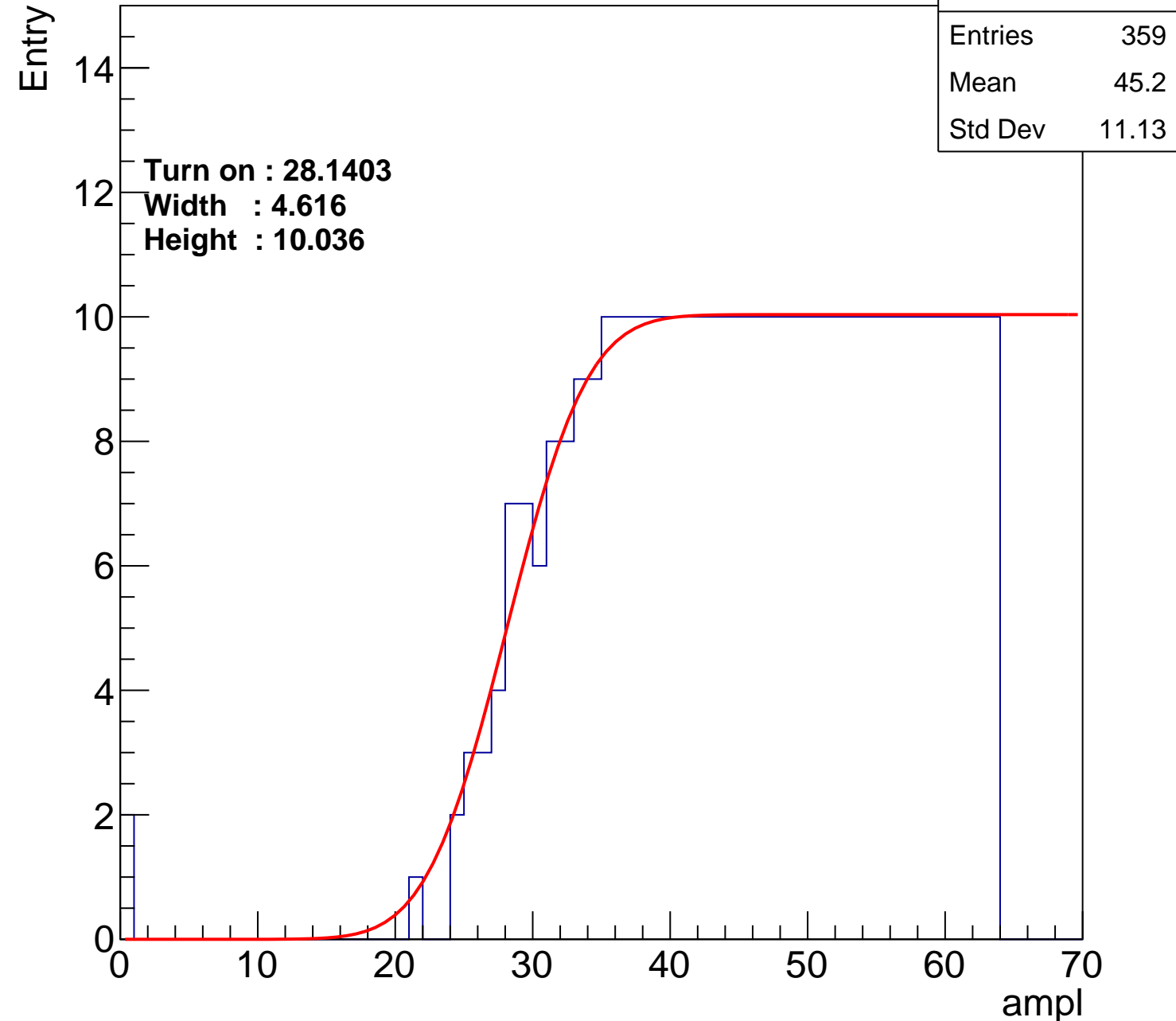
Width : 4.616

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch83

calib_packv5_042523_0143.root, FC#9, port A1

Entries	374
Mean	44.15
Std Dev	12.35

Turn on : 26.7122

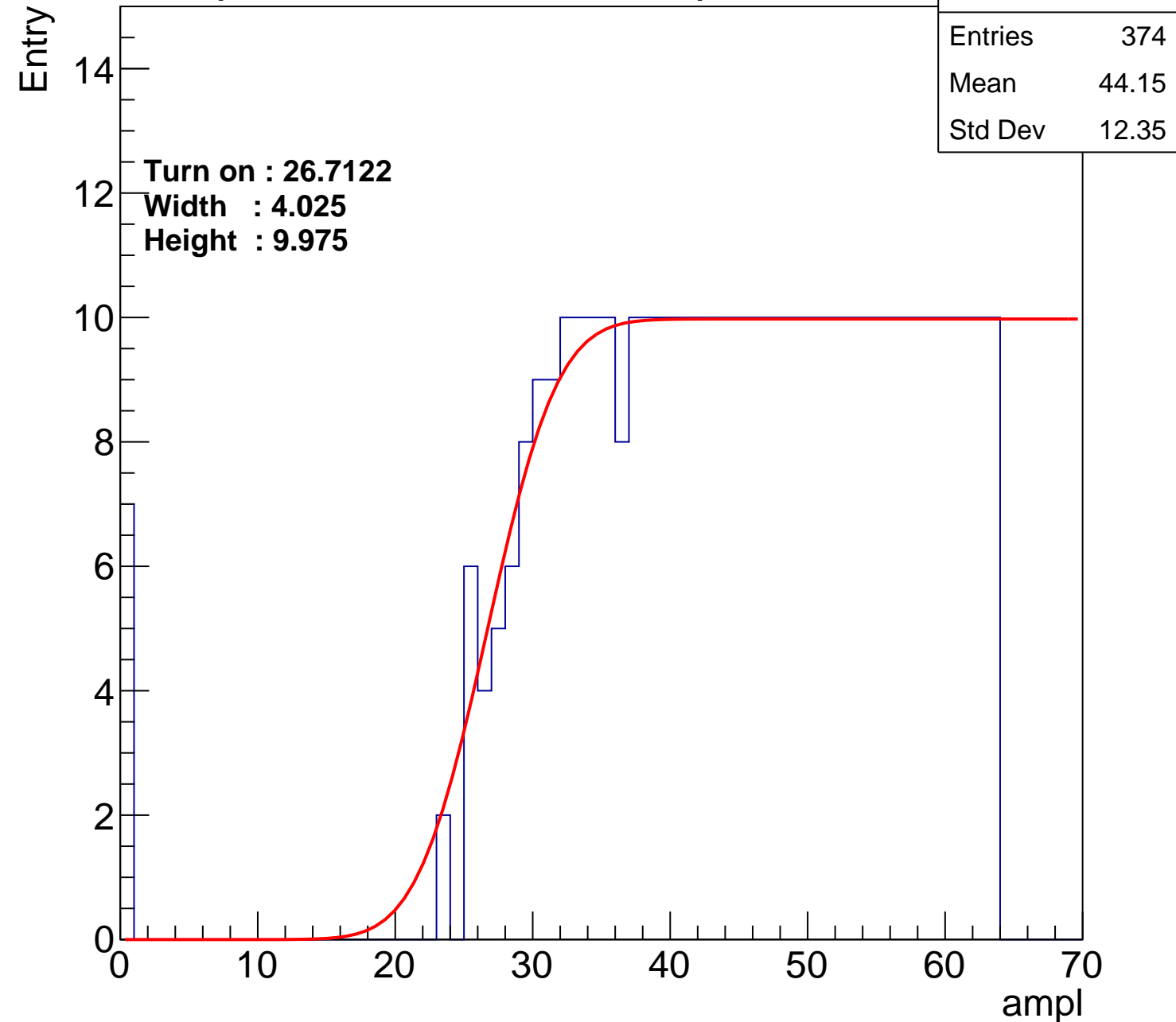
Width : 4.025

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch84

calib_packv5_042523_0143.root, FC#9, port A1

Entries	376
Mean	44.33
Std Dev	11.69

Turn on : 27.4279

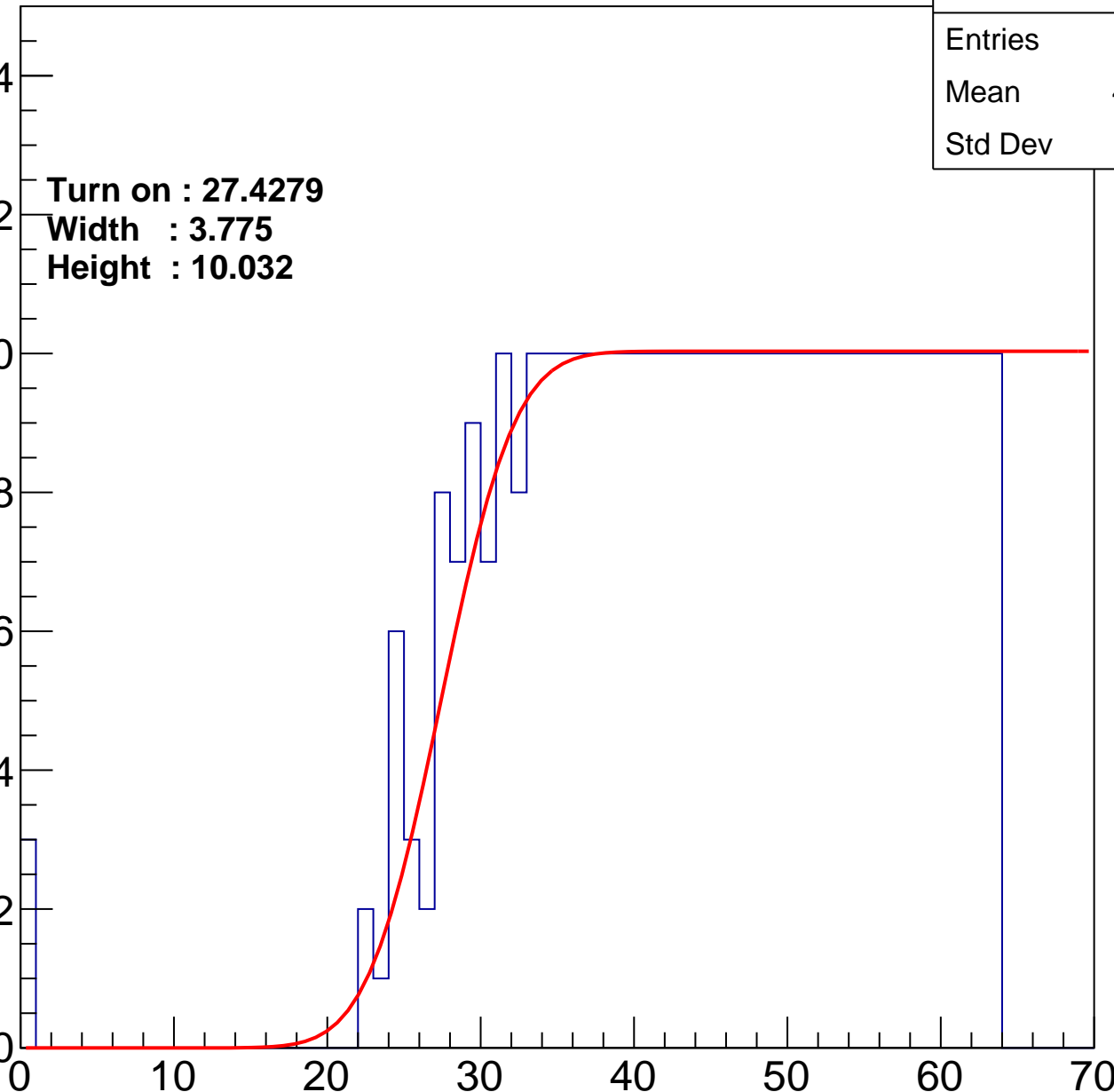
Width : 3.775

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch85

calib_packv5_042523_0143.root, FC#9, port A1

Entries	353
Mean	45.52
Std Dev	11.05

Turn on : 29.4632

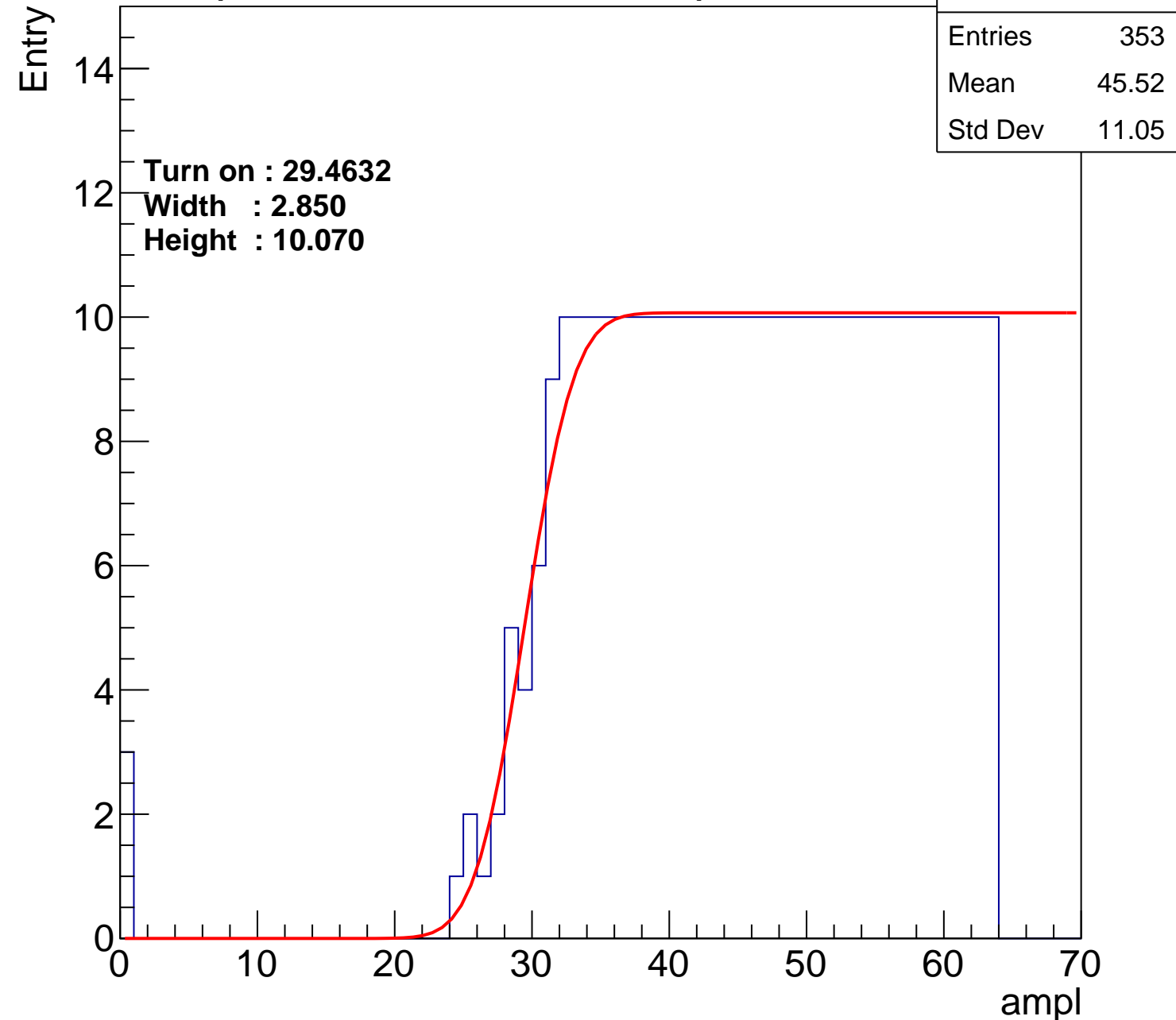
Width : 2.850

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch86

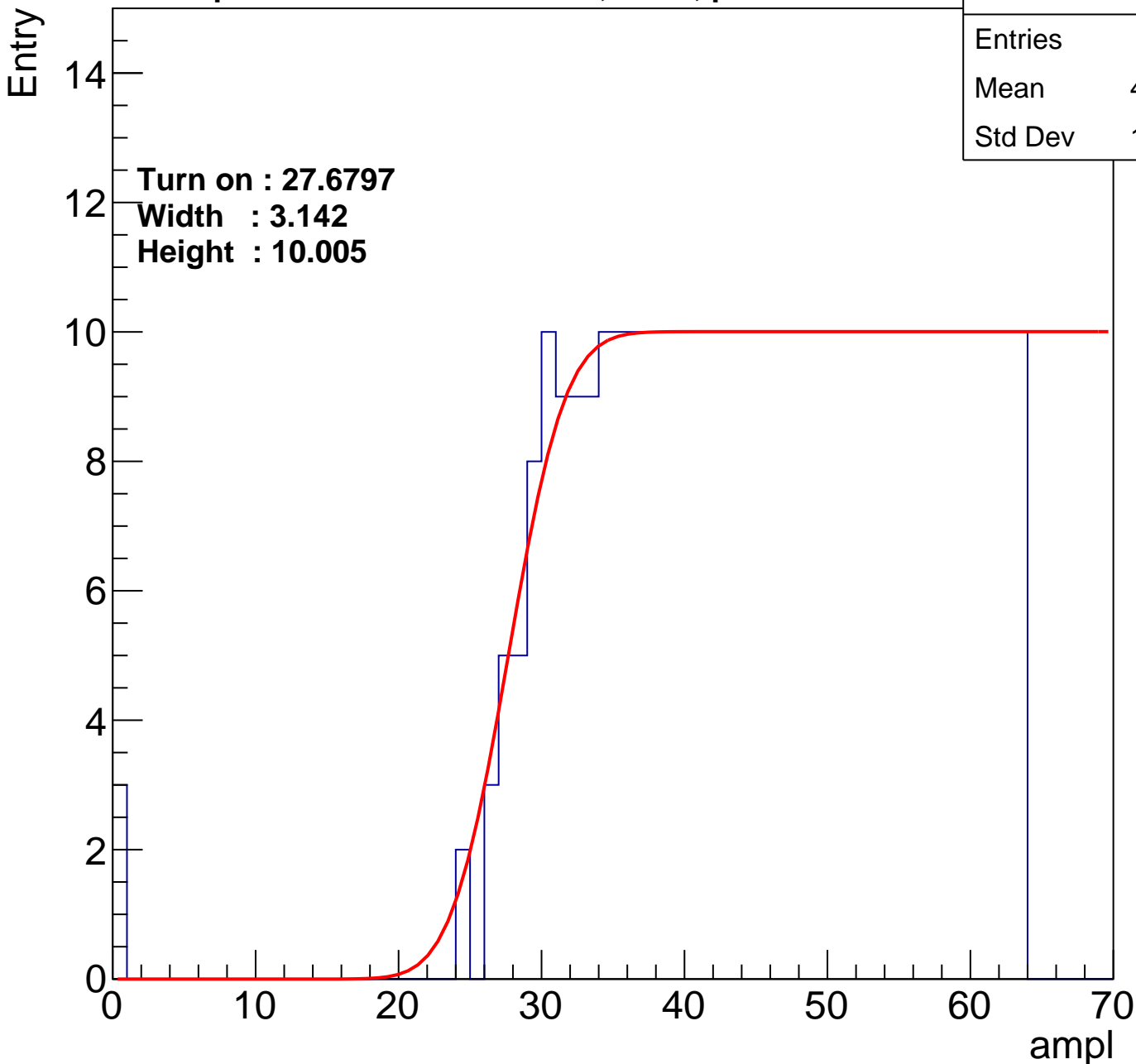
calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.03
Std Dev	11.27

Turn on : 27.6797

Width : 3.142

Height : 10.005



B0L001S, U14-ch87

calib_packv5_042523_0143.root, FC#9, port A1

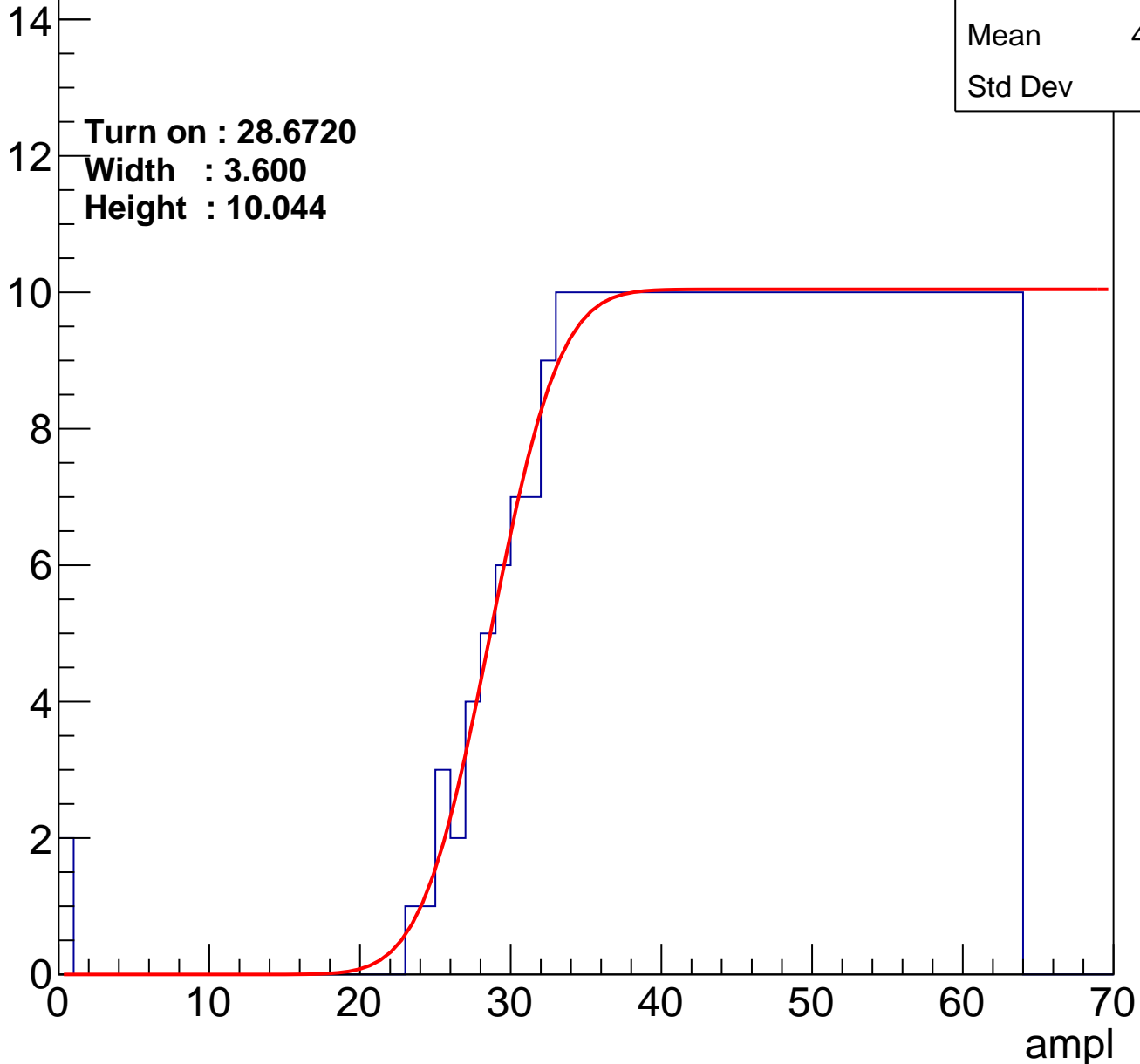
Entries	357
Mean	45.35
Std Dev	11

Turn on : 28.6720

Width : 3.600

Height : 10.044

Entry



B0L001S, U14-ch88

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.87
Std Dev	11.38

Turn on : 27.9843

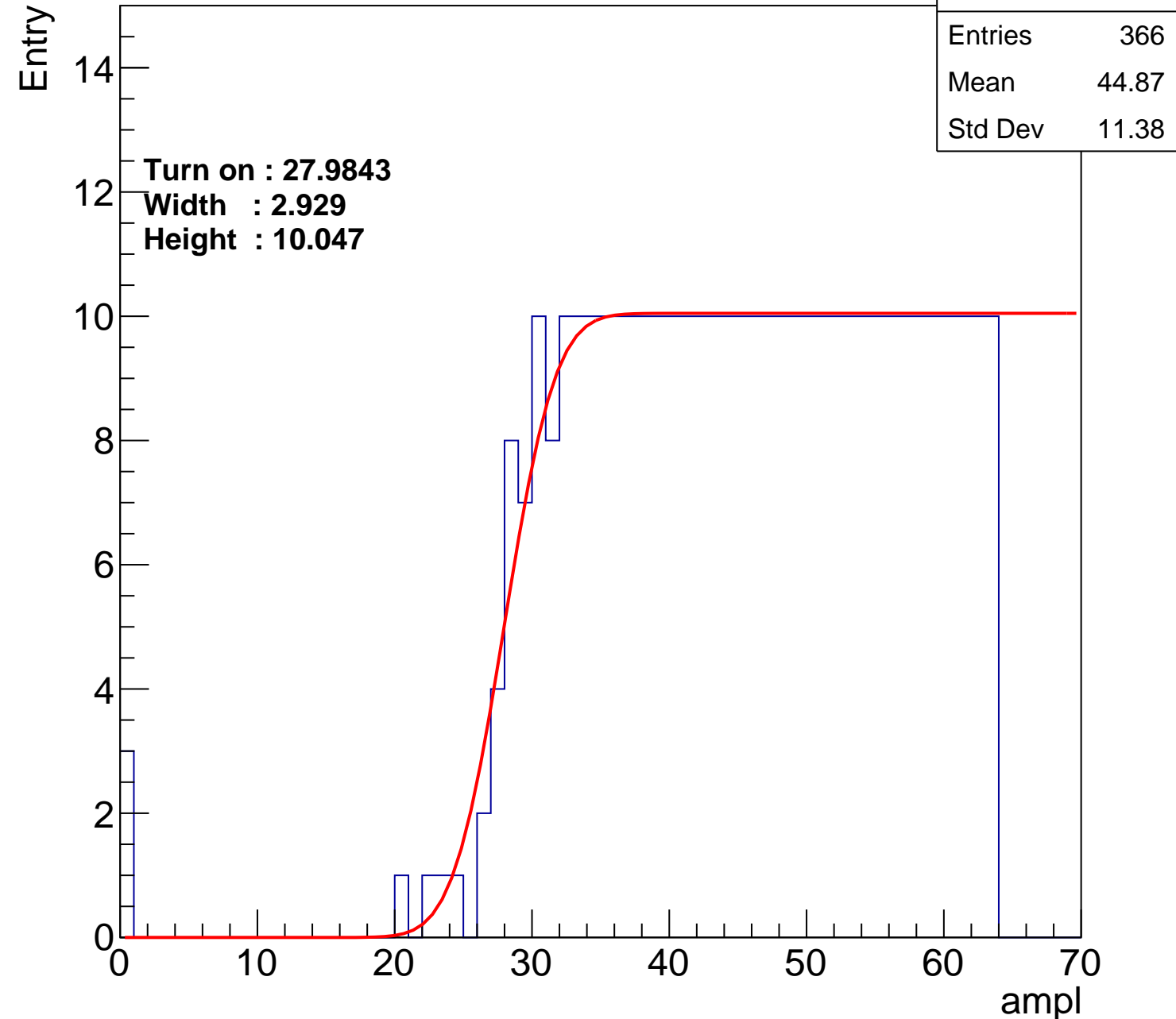
Width : 2.929

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch89

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.88
Std Dev	11.62

Turn on : 29.2265

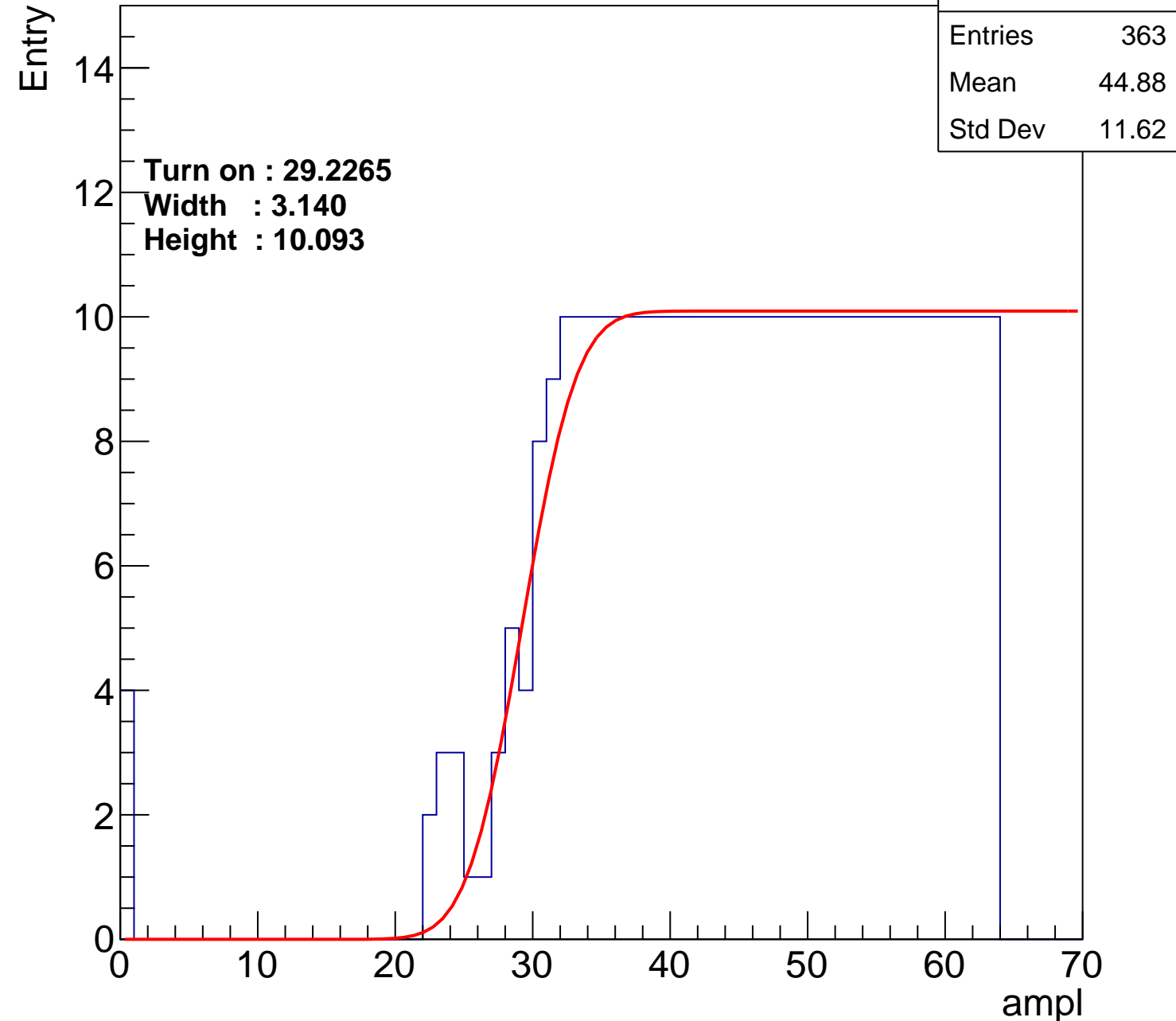
Width : 3.140

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch90

calib_packv5_042523_0143.root, FC#9, port A1

Entries	396
Mean	43.27
Std Dev	12.42

Turn on : 24.9339

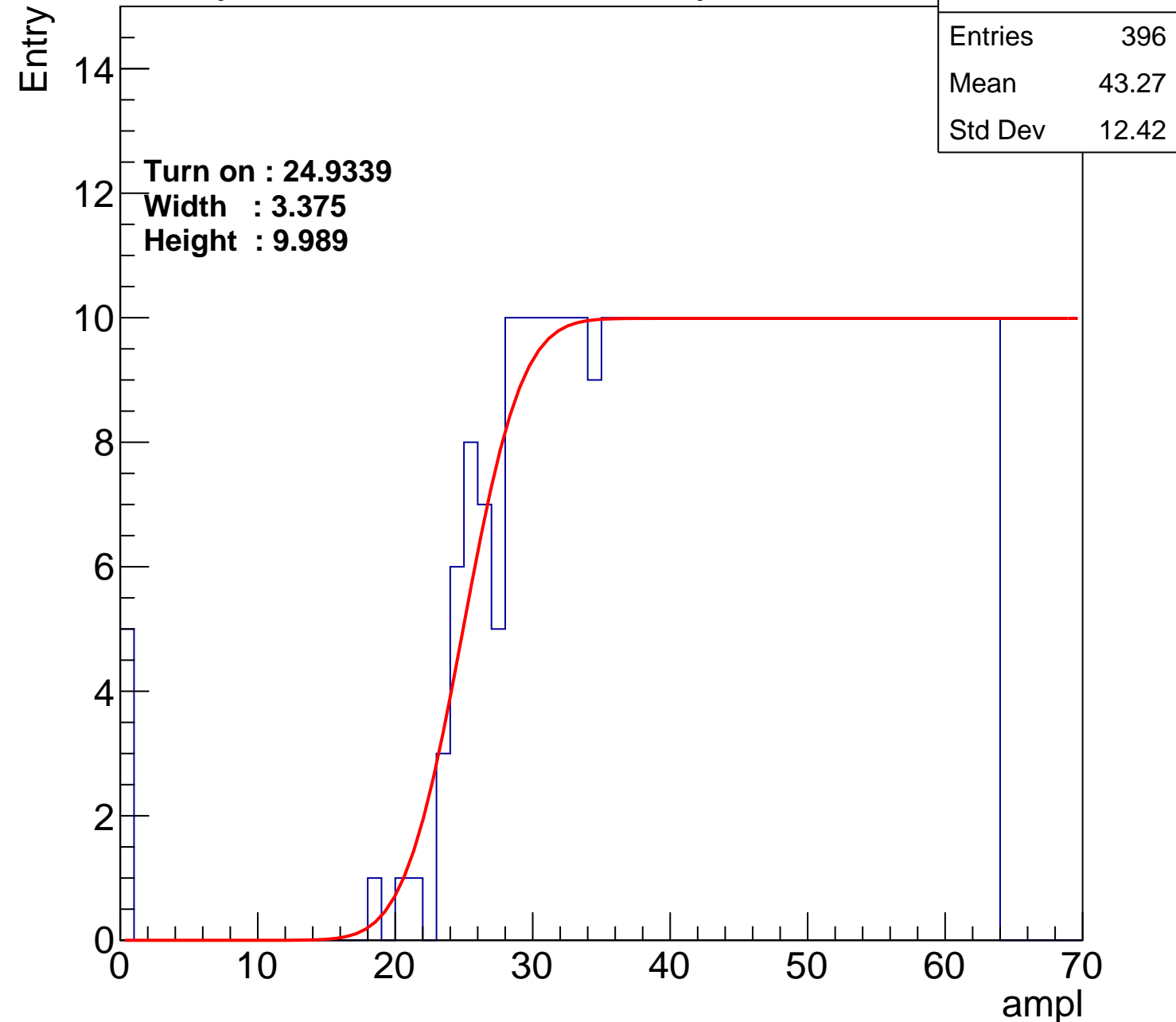
Width : 3.375

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch91

calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.18
Std Dev	12.44

Turn on : 27.4894

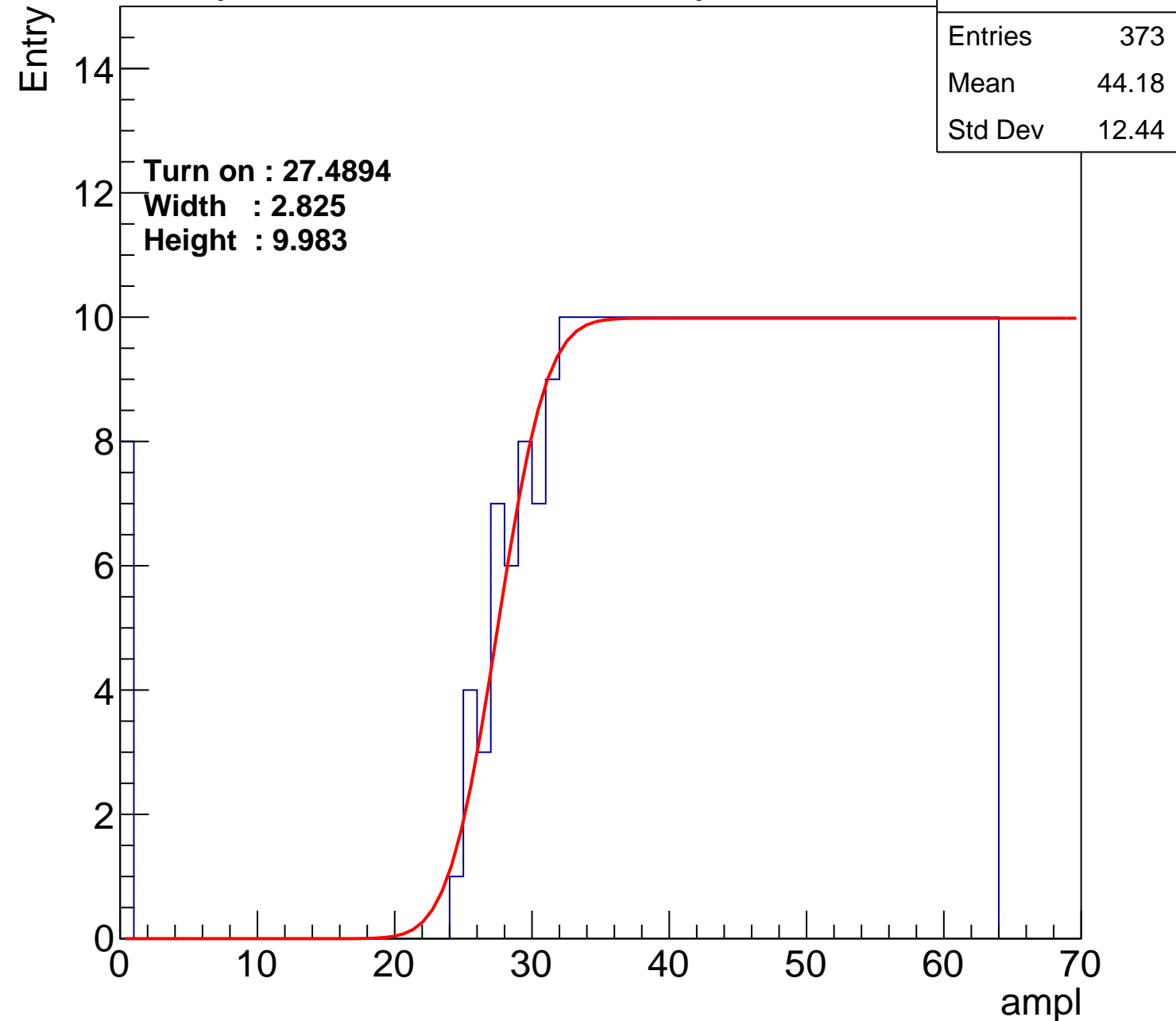
Width : 2.825

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch92

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	45.1
Std Dev	10.95

Turn on : 28.2030

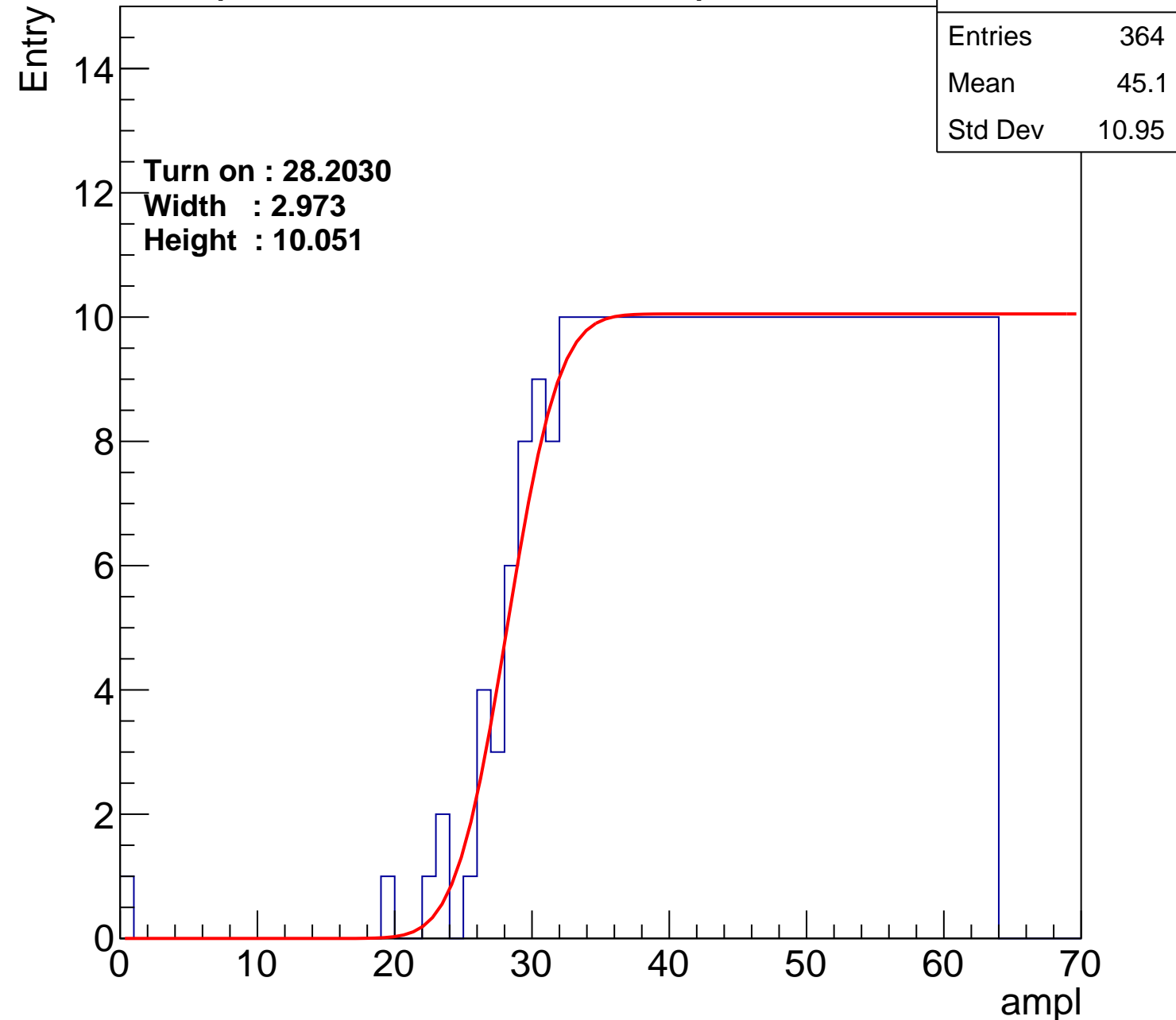
Width : 2.973

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch93

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.83
Std Dev	11.45

Turn on : 28.0051

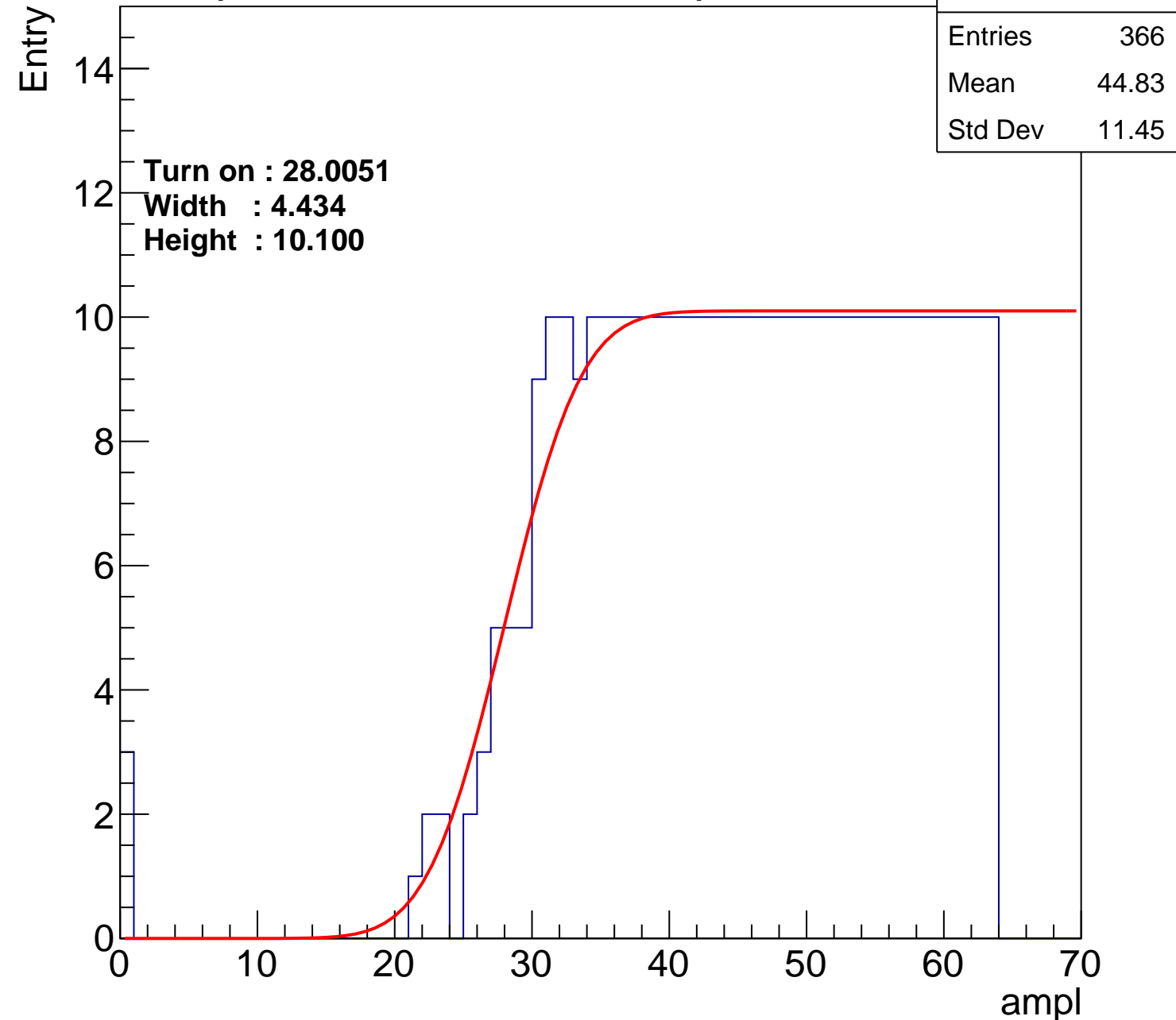
Width : 4.434

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch94

calib_packv5_042523_0143.root, FC#9, port A1

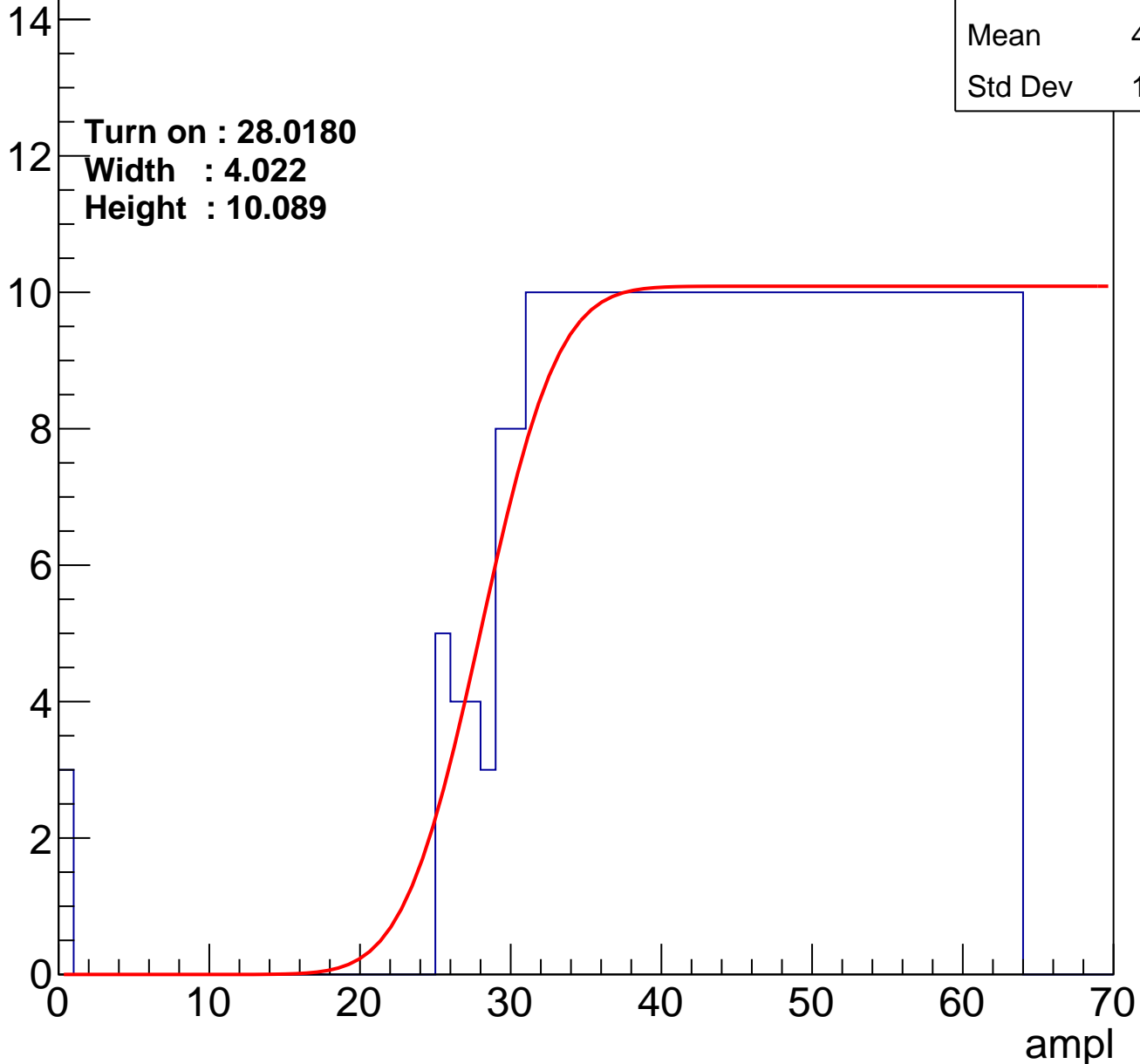
Entries	365
Mean	44.94
Std Dev	11.32

Turn on : 28.0180

Width : 4.022

Height : 10.089

Entry



B0L001S, U14-ch95

calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.48
Std Dev	10.75

Turn on : 29.5397

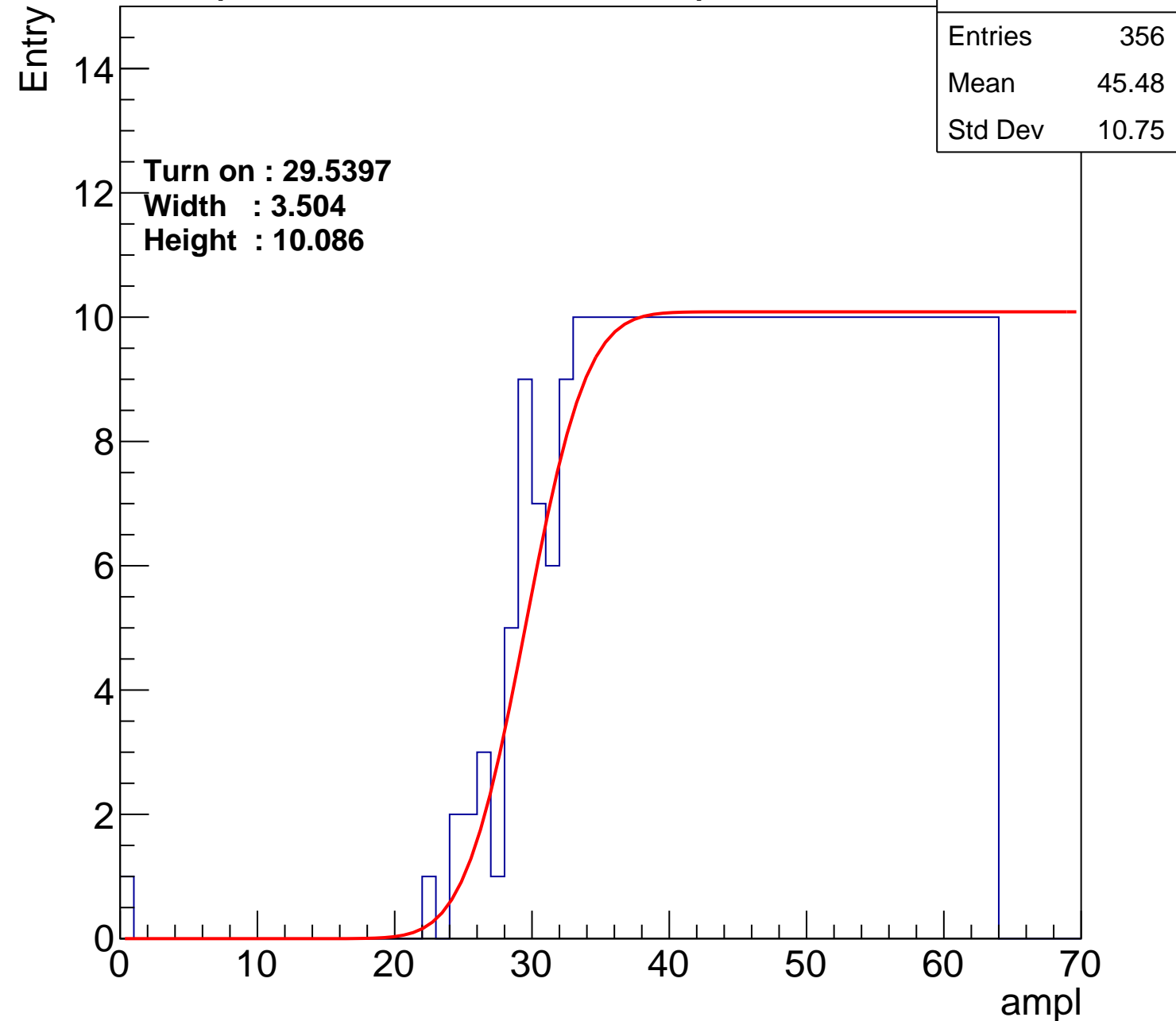
Width : 3.504

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch96

calib_packv5_042523_0143.root, FC#9, port A1

Entries	375
Mean	44.36
Std Dev	11.69

Turn on : 27.0819

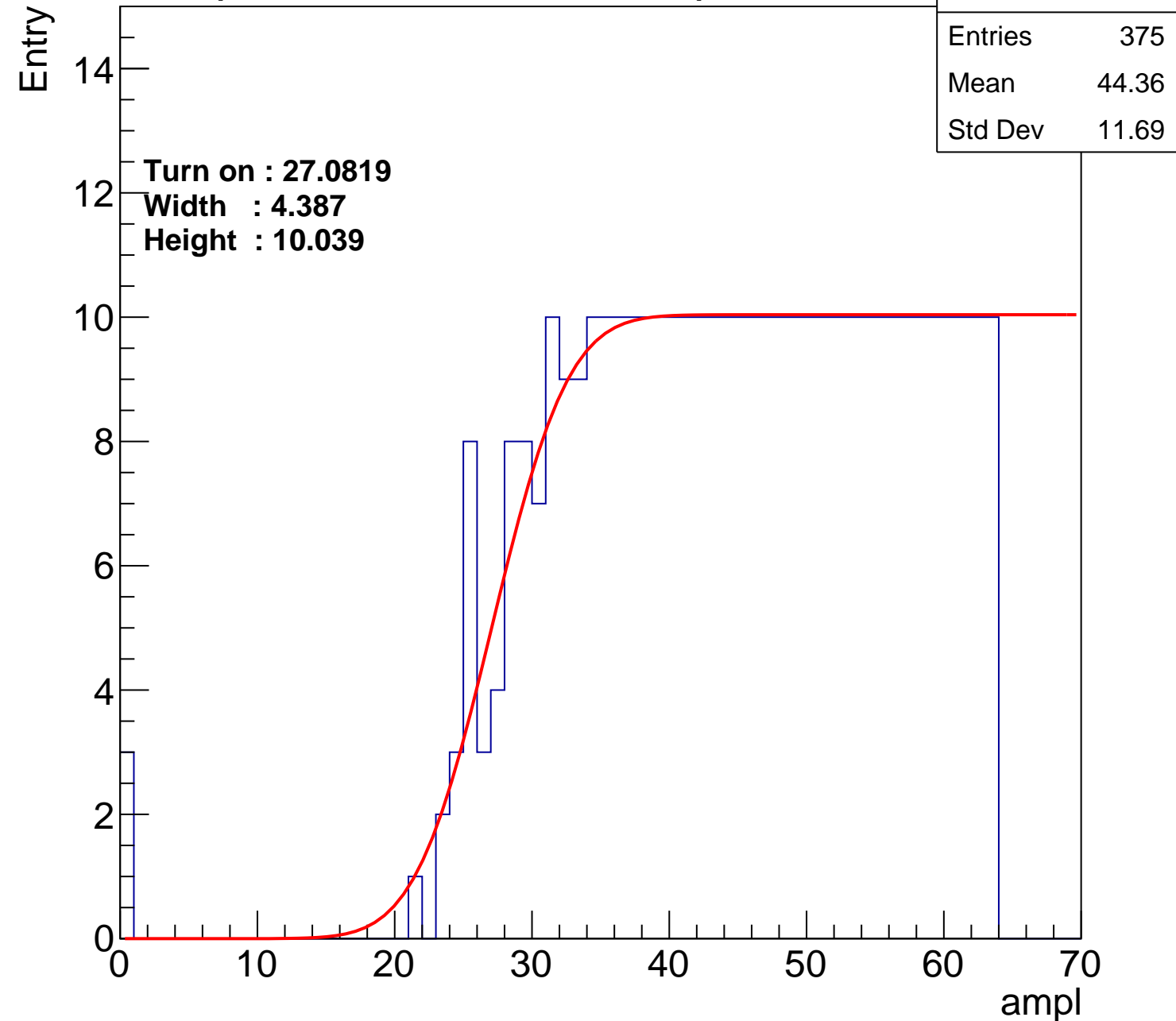
Width : 4.387

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch97

calib_packv5_042523_0143.root, FC#9, port A1

Entries	351
Mean	45.6
Std Dev	11.03

Turn on : 29.1915

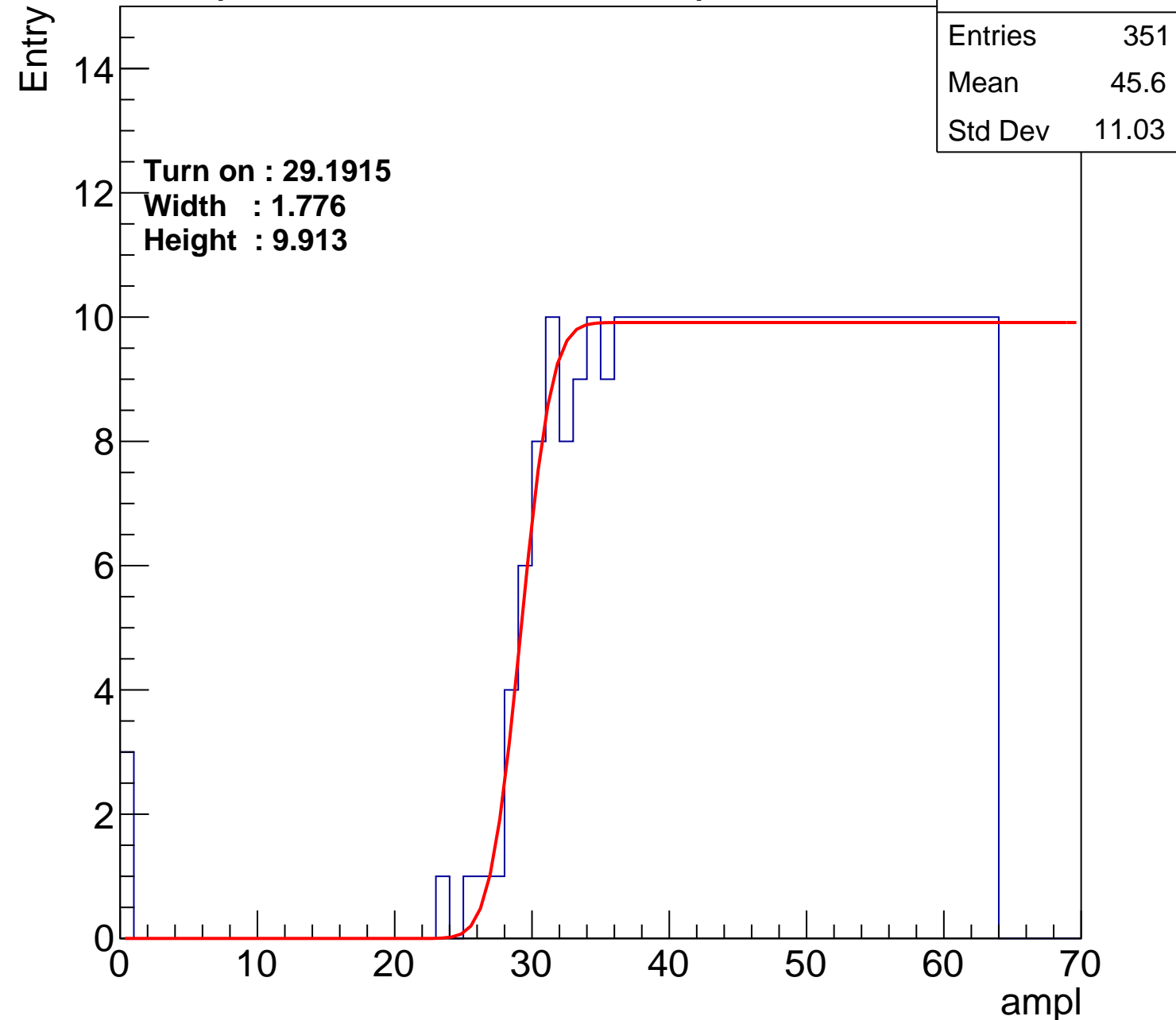
Width : 1.776

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch98

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.86
Std Dev	11.29

Turn on : 27.6862

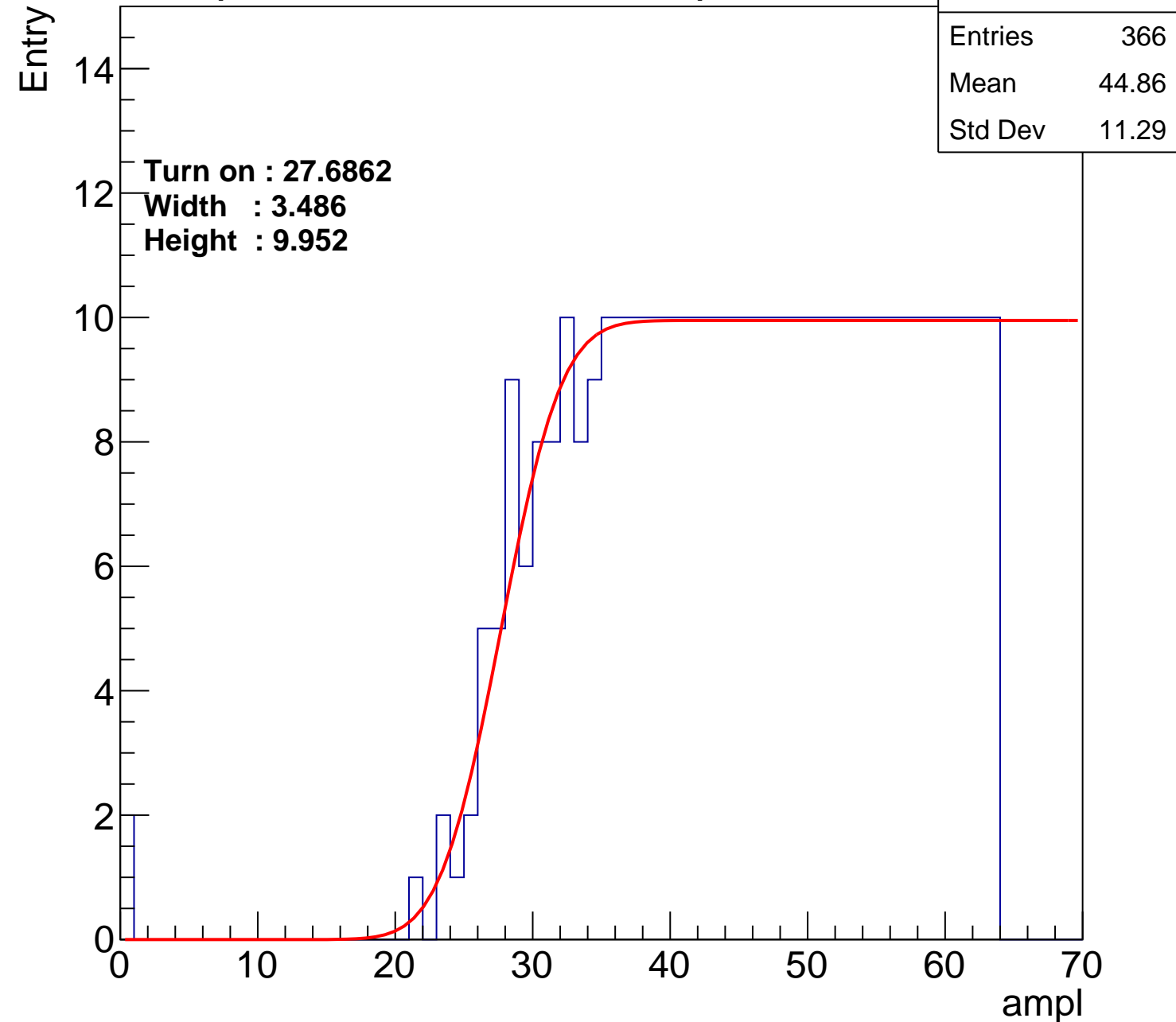
Width : 3.486

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch99

calib_packv5_042523_0143.root, FC#9, port A1

Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.7308

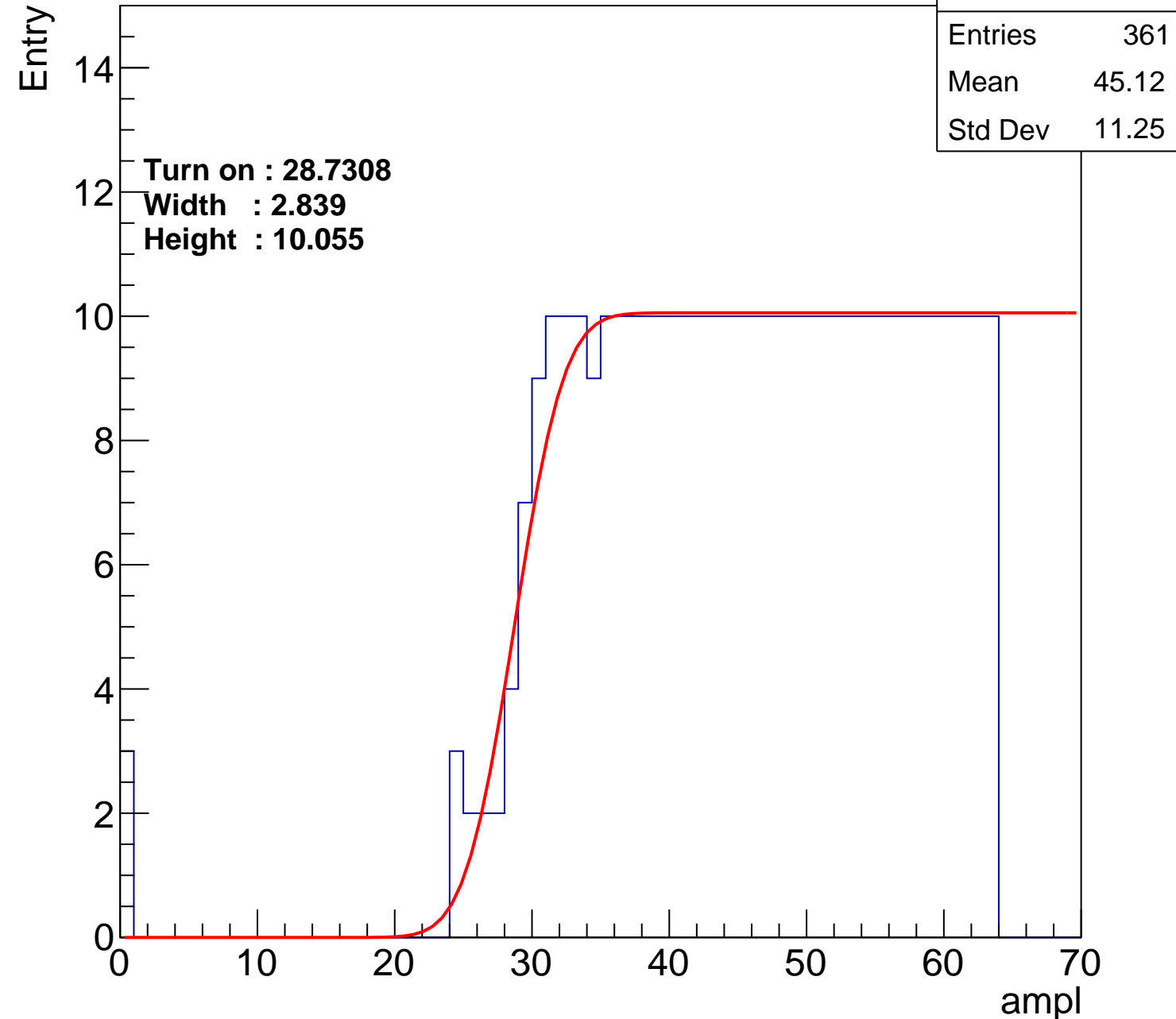
Width : 2.839

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch100

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.91
Std Dev	11.1

Turn on : 27.8337

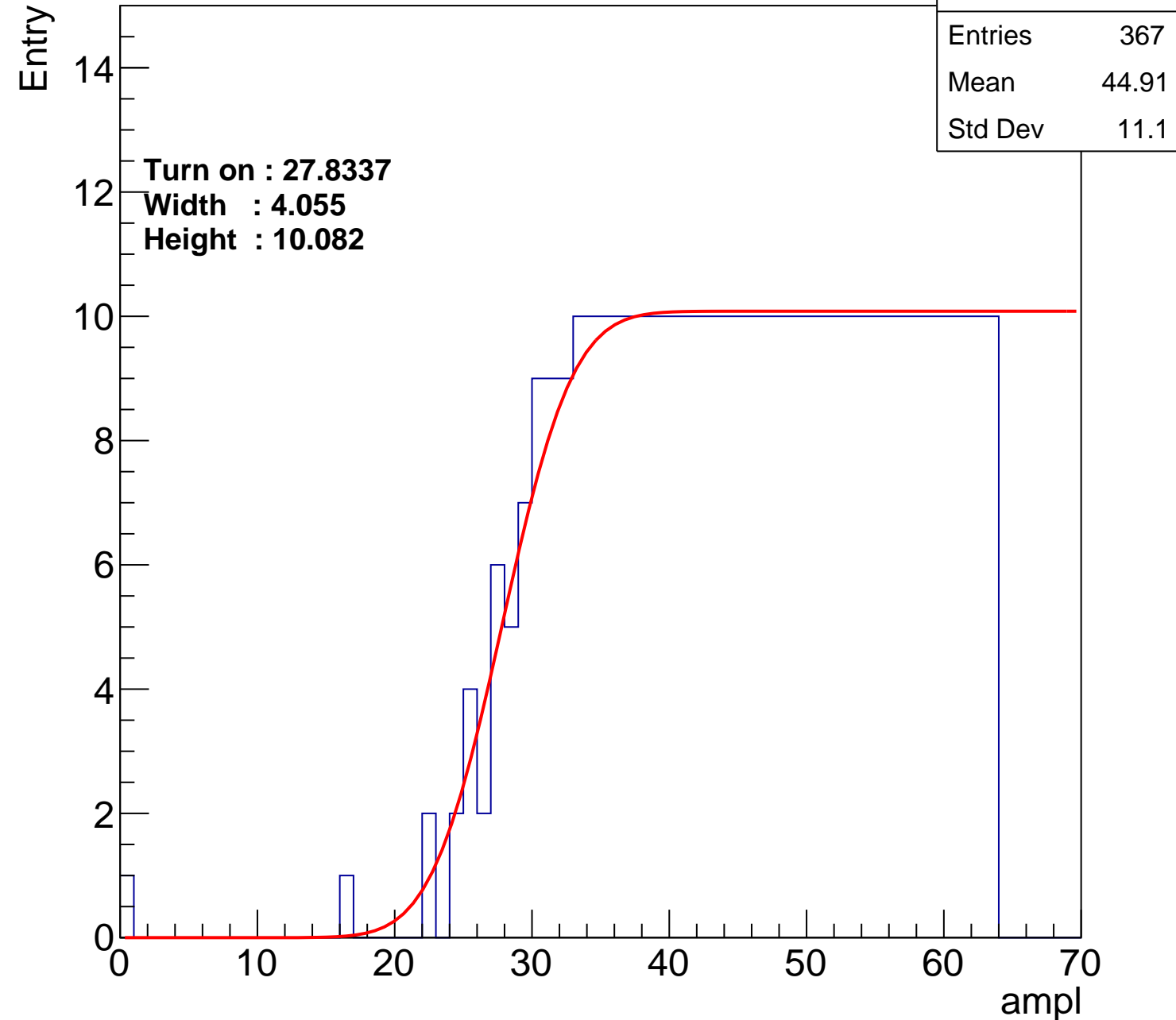
Width : 4.055

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch101

calib_packv5_042523_0143.root, FC#9, port A1

Entries	352
Mean	45.68
Std Dev	10.63

Turn on : 29.1867

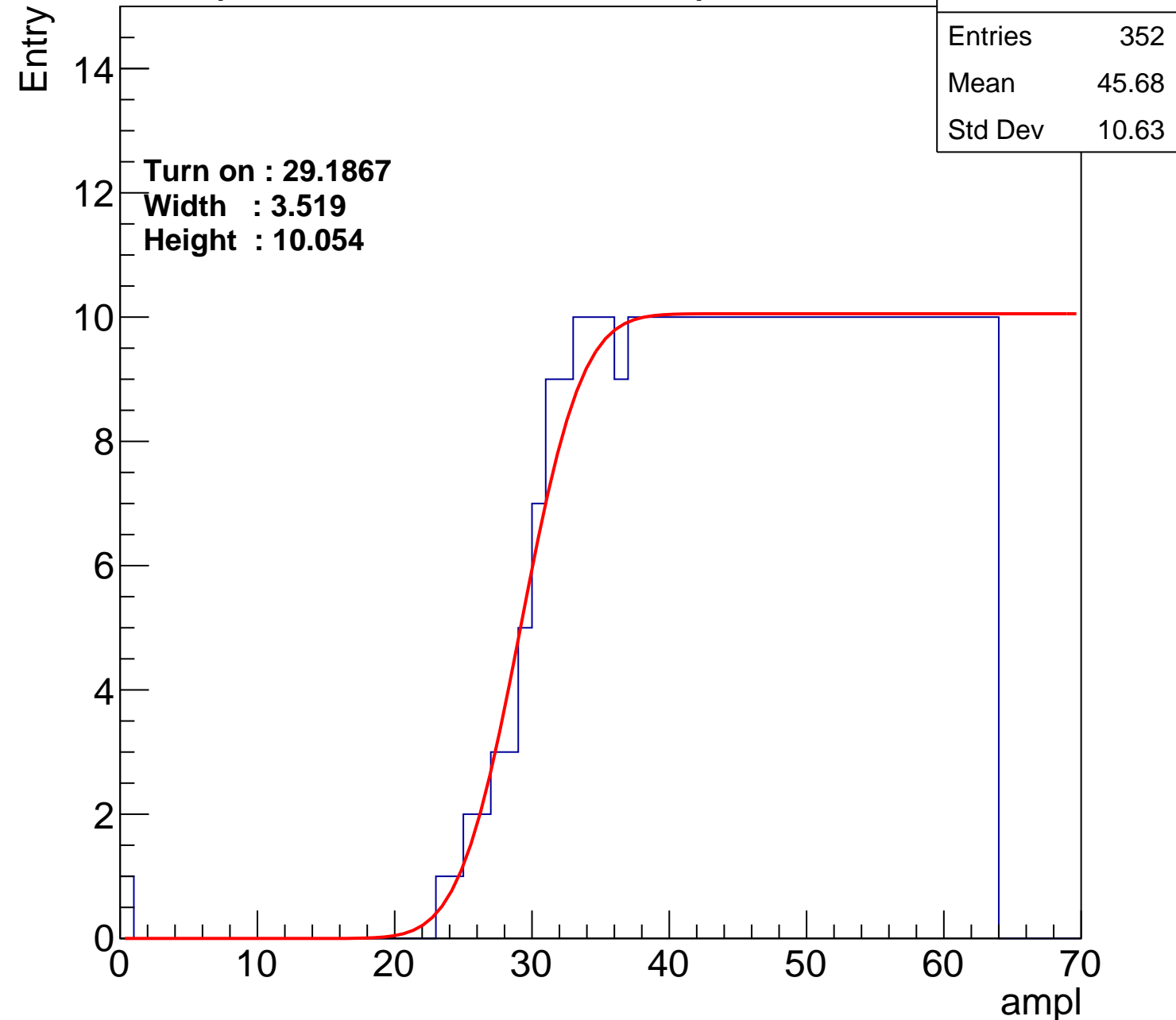
Width : 3.519

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch102

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 28.1382

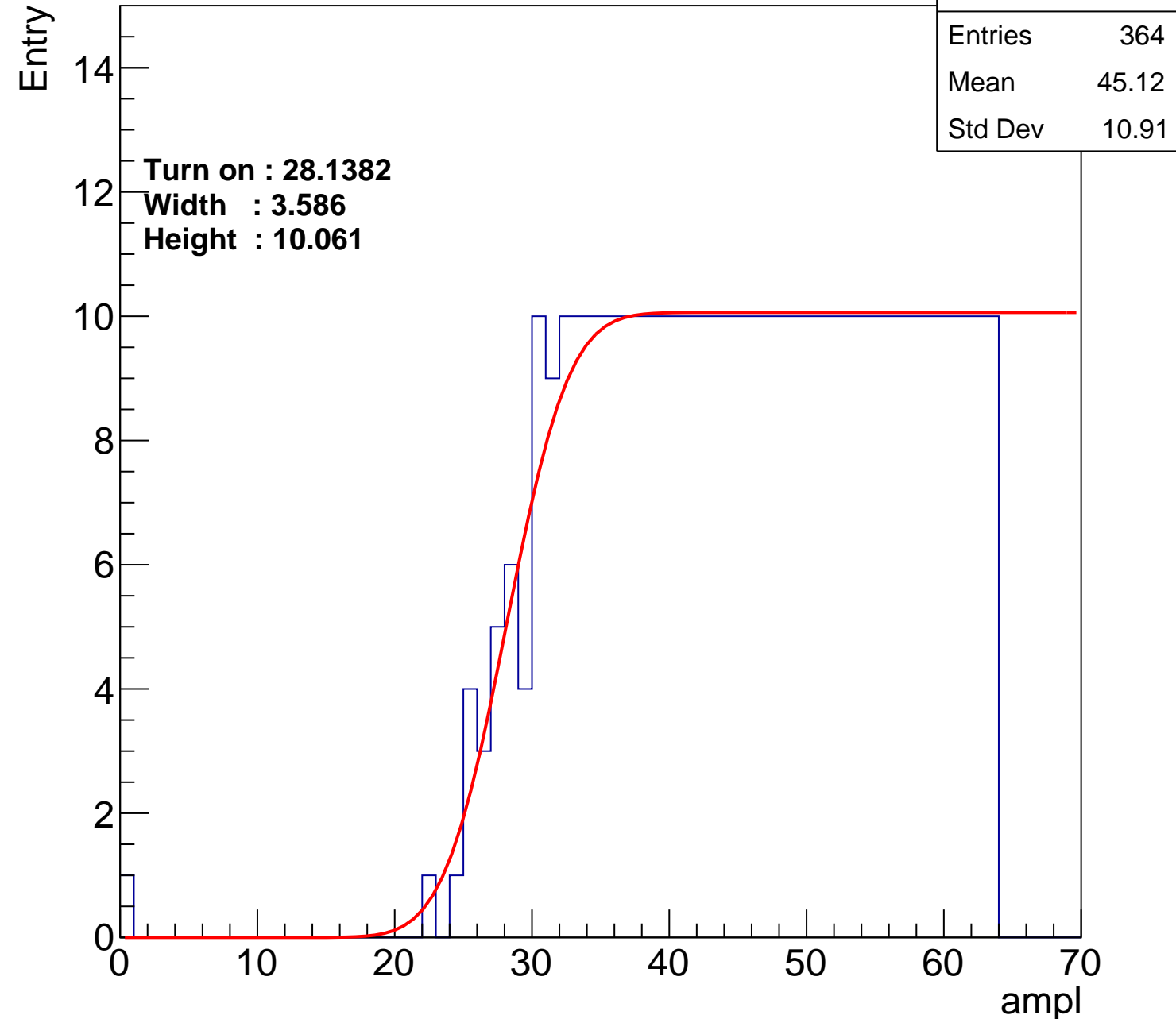
Width : 3.586

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch103

calib_packv5_042523_0143.root, FC#9, port A1

Entries	361
Mean	44.91
Std Dev	11.74

Turn on : 28.7146

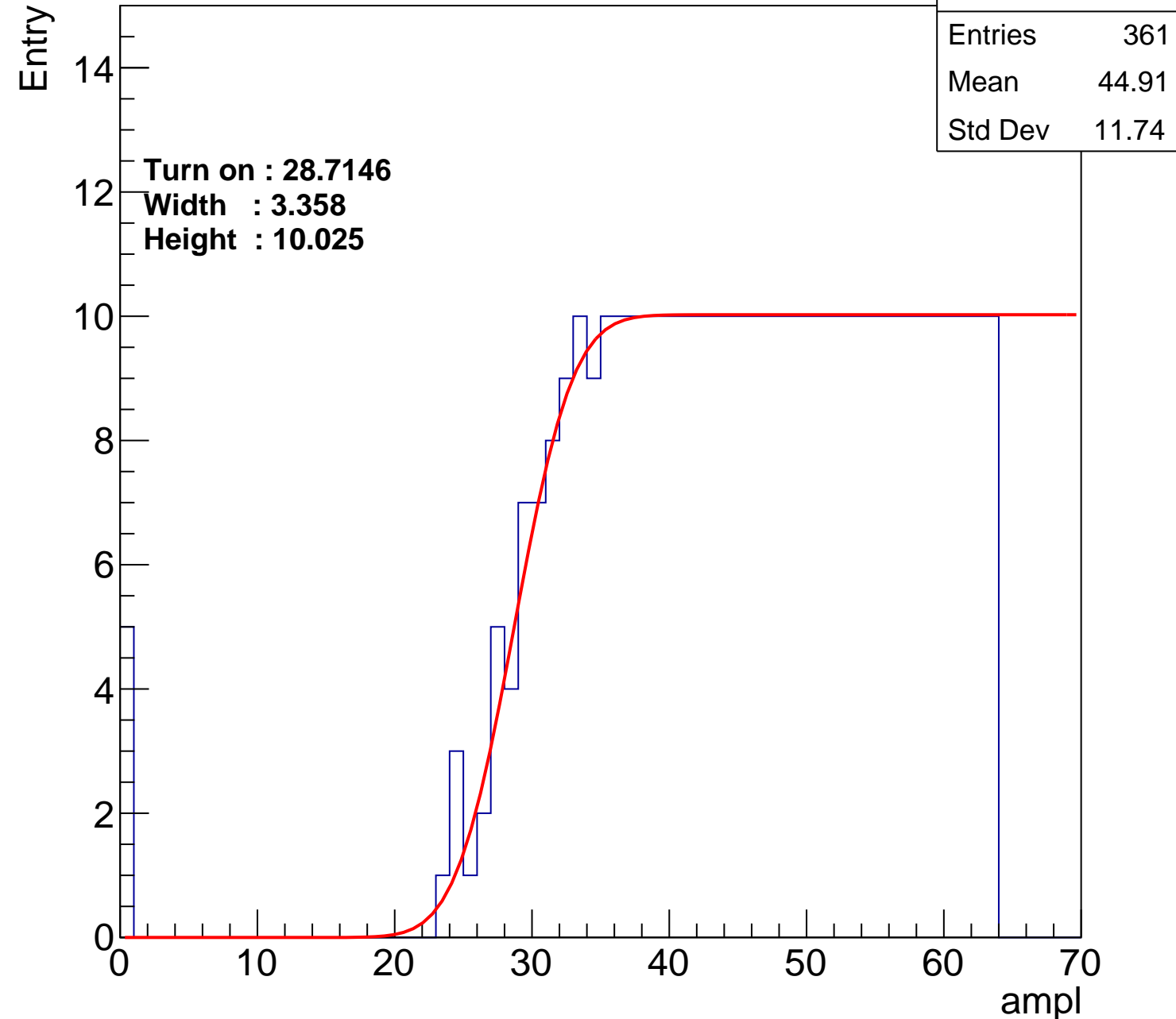
Width : 3.358

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch104

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.78
Std Dev	11.59

Turn on : 28.1540

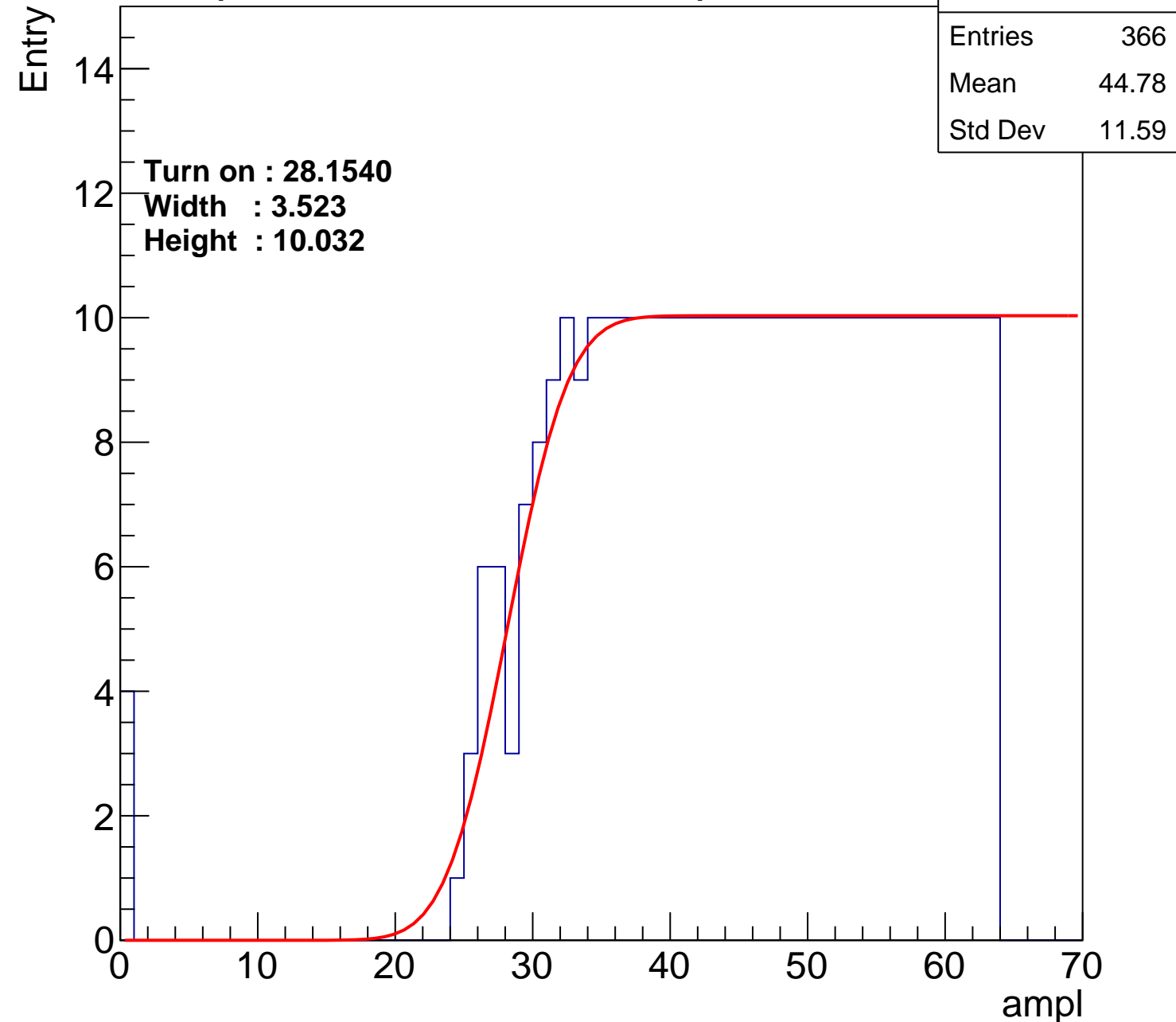
Width : 3.523

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch105

calib_packv5_042523_0143.root, FC#9, port A1

Entries	393
Mean	43.38
Std Dev	12.35

Turn on : 25.3617

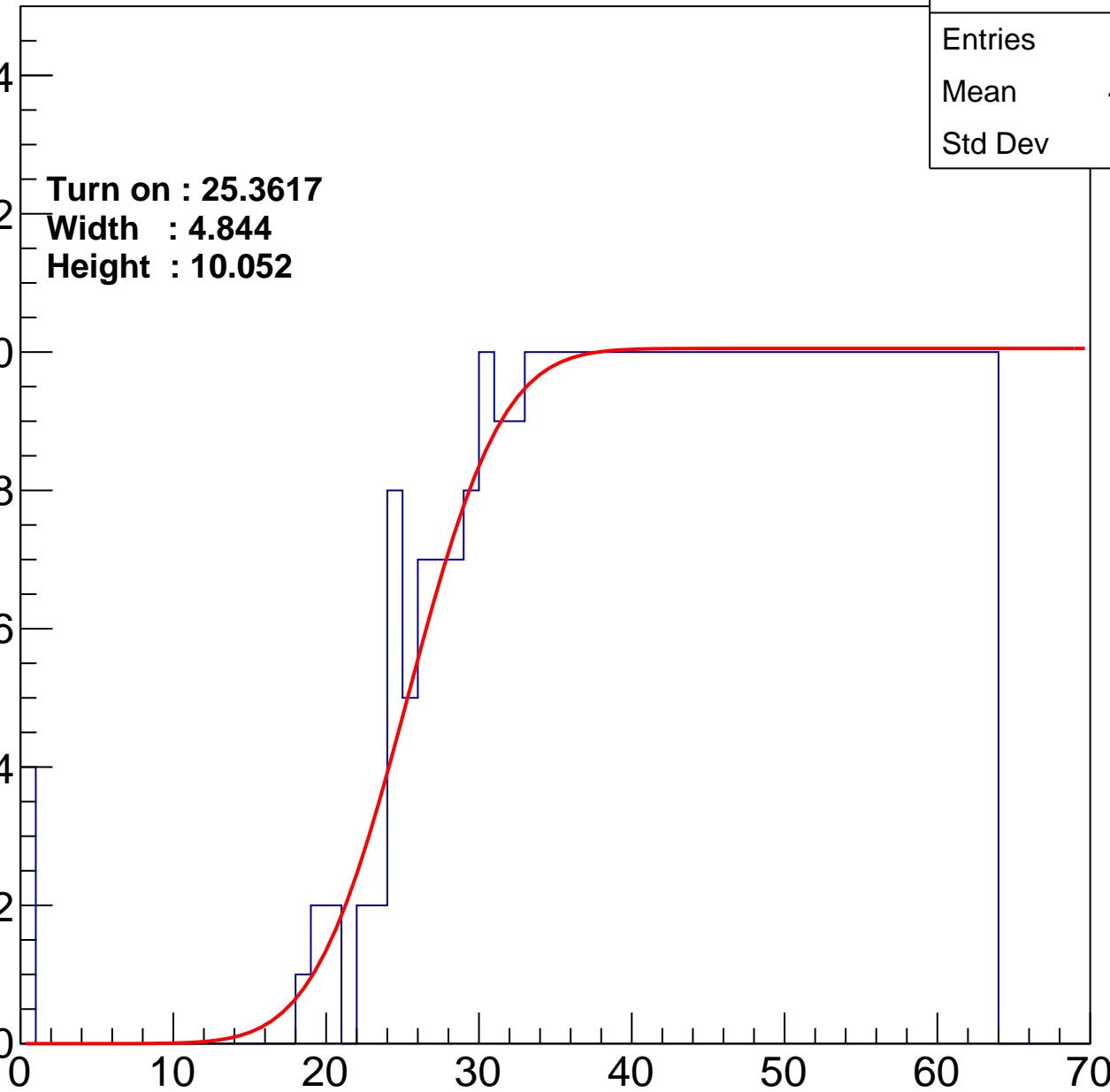
Width : 4.844

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch106

calib_packv5_042523_0143.root, FC#9, port A1

Entries	380
Mean	44.06
Std Dev	12.05

Turn on : 26.4690

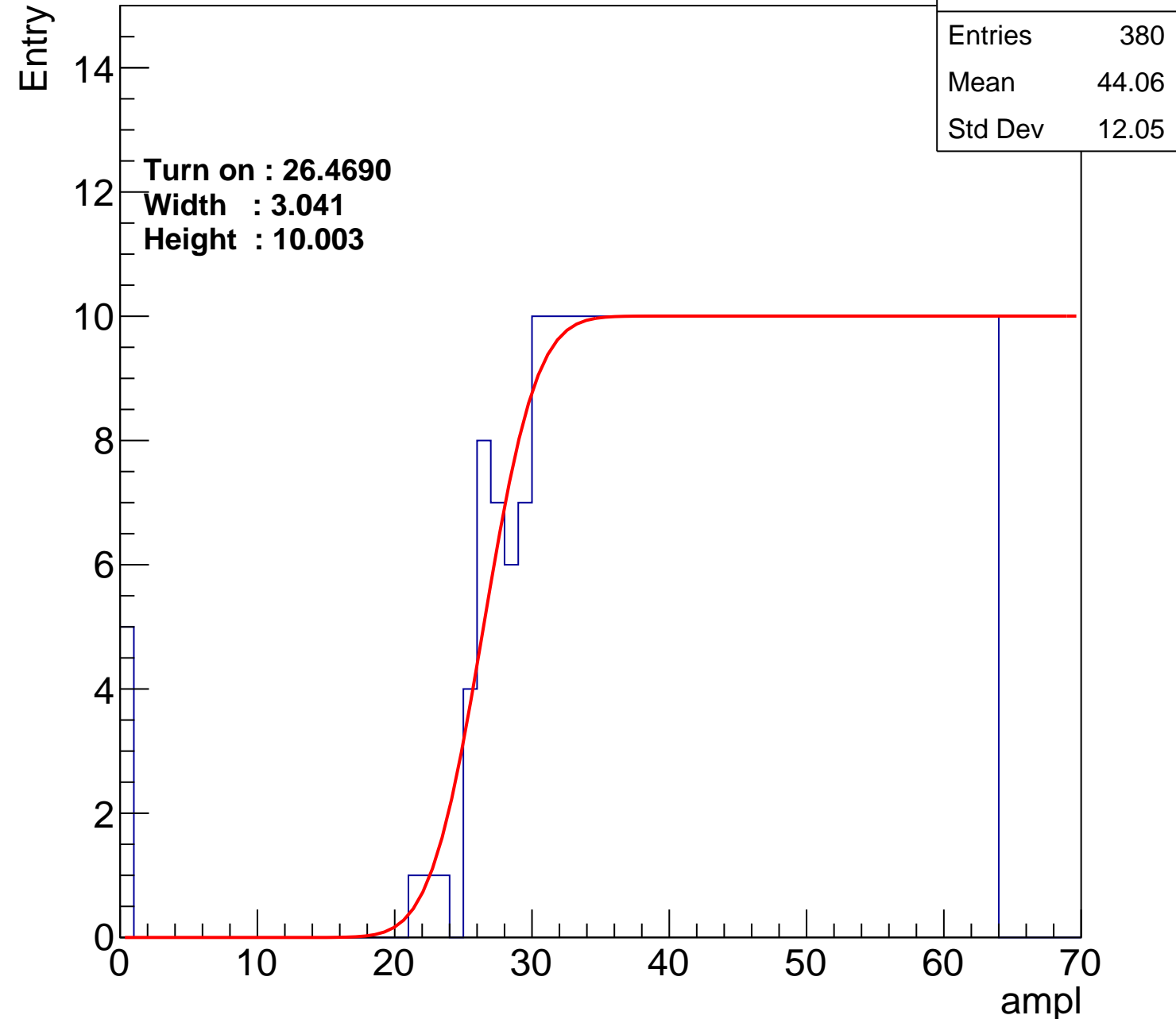
Width : 3.041

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch107

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.06
Std Dev	12.21

Turn on : 26.9624

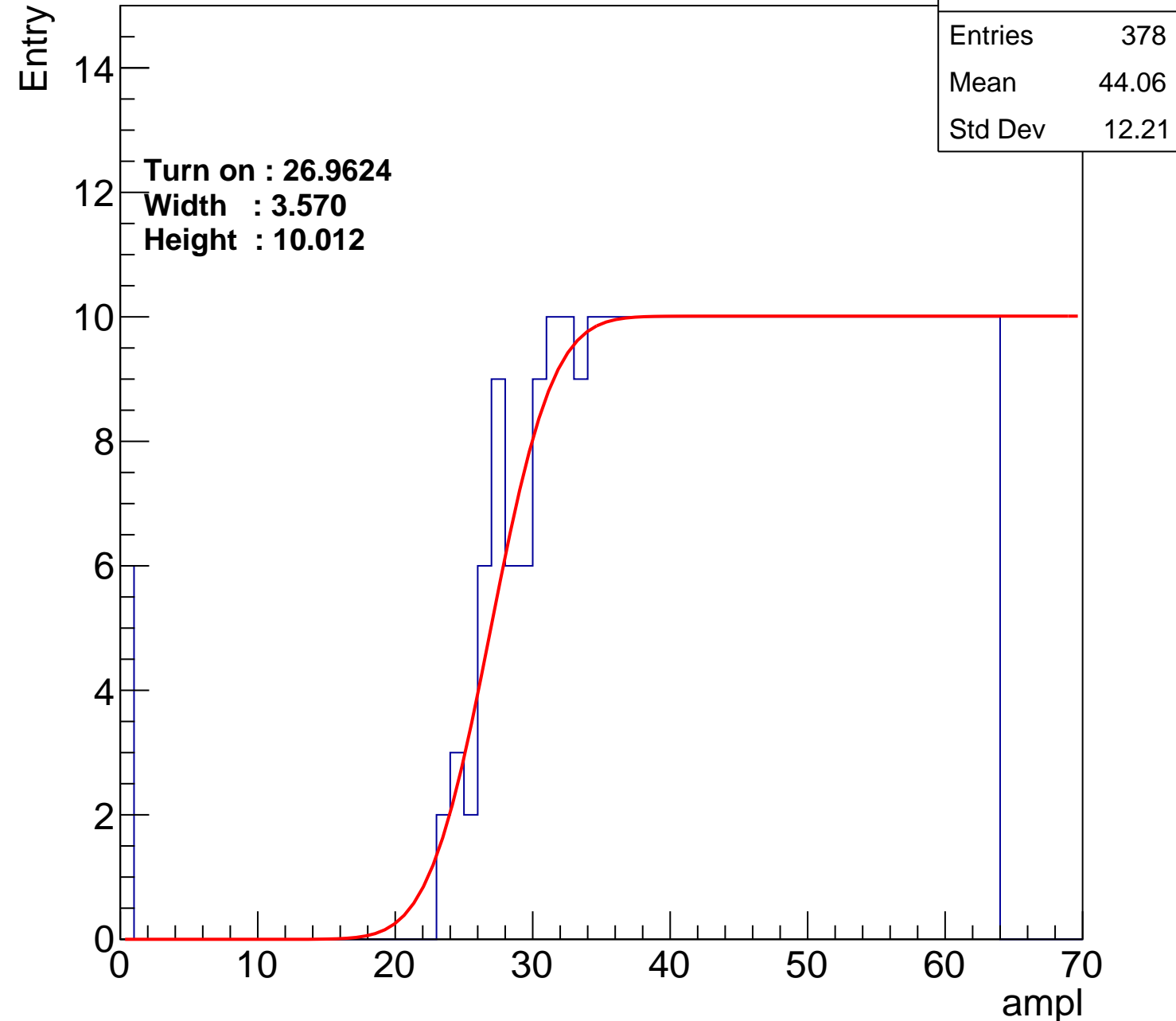
Width : 3.570

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch108

calib_packv5_042523_0143.root, FC#9, port A1

Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 26.4590

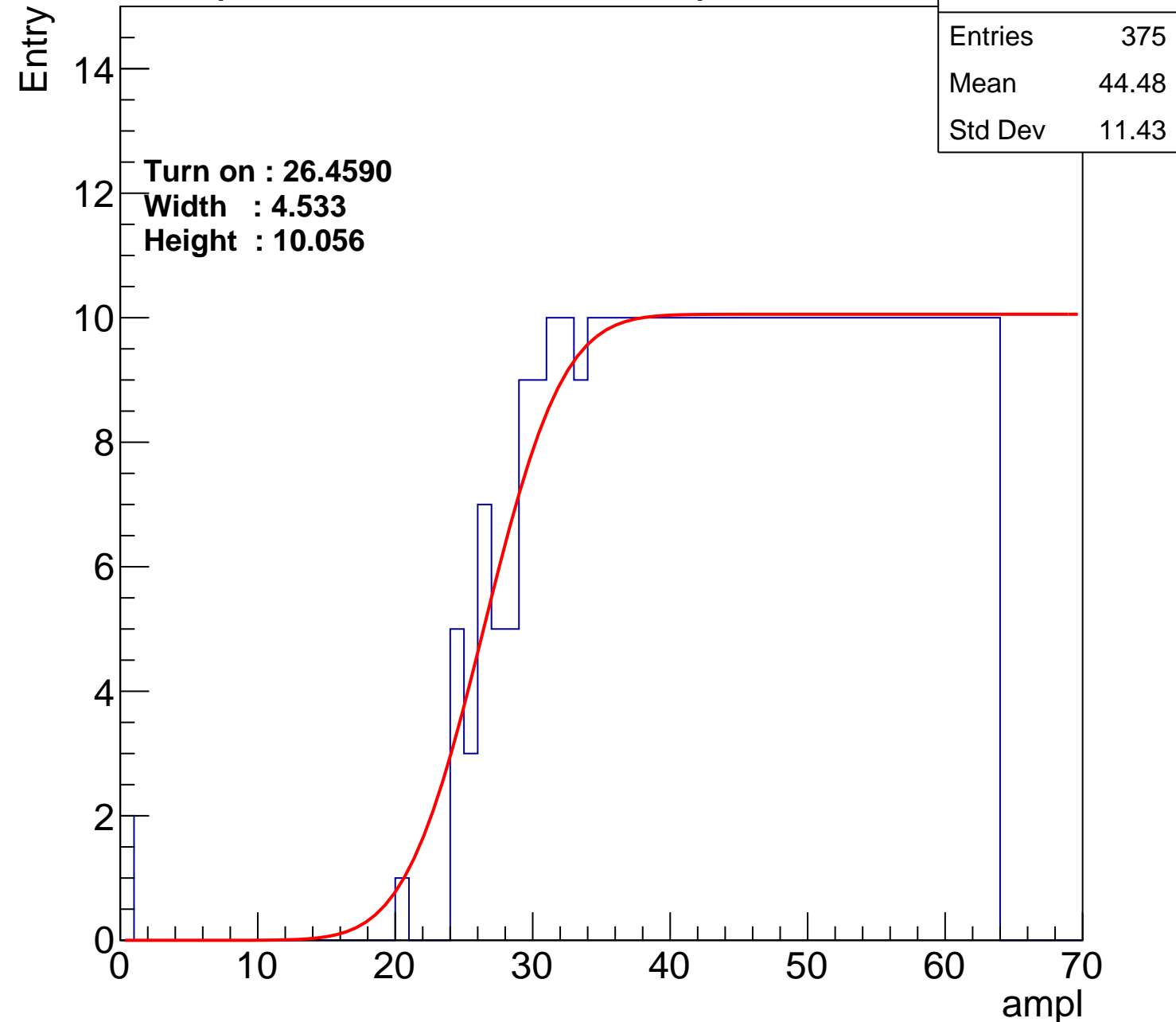
Width : 4.533

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch109

calib_packv5_042523_0143.root, FC#9, port A1

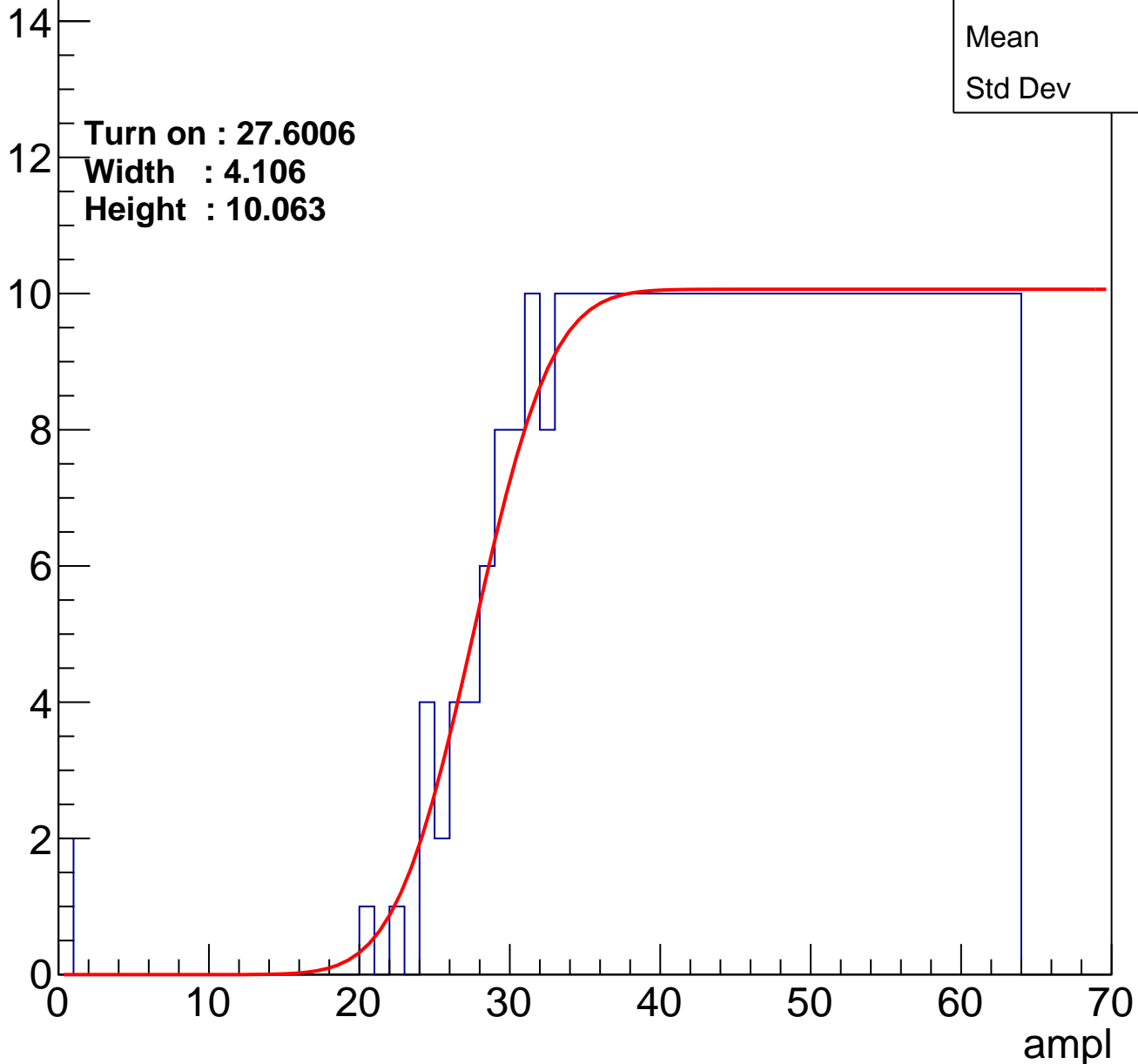
Entries	368
Mean	44.8
Std Dev	11.3

Turn on : 27.6006

Width : 4.106

Height : 10.063

Entry



B0L001S, U14-ch110

calib_packv5_042523_0143.root, FC#9, port A1

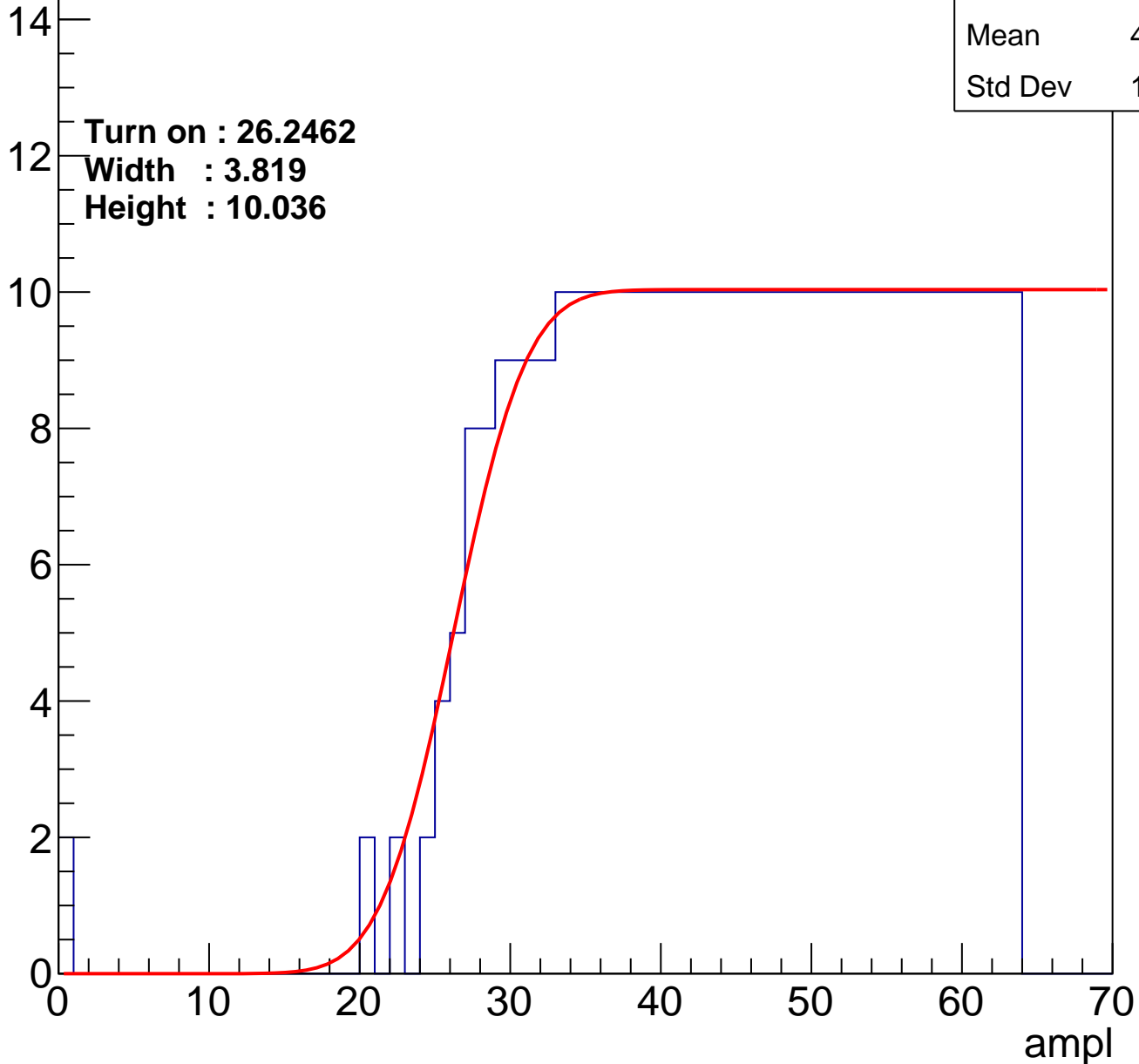
Entries	379
Mean	44.27
Std Dev	11.55

Turn on : 26.2462

Width : 3.819

Height : 10.036

Entry



B0L001S, U14-ch111

calib_packv5_042523_0143.root, FC#9, port A1

Entries	355
Mean	45.46
Std Dev	10.93

Turn on : 29.1131

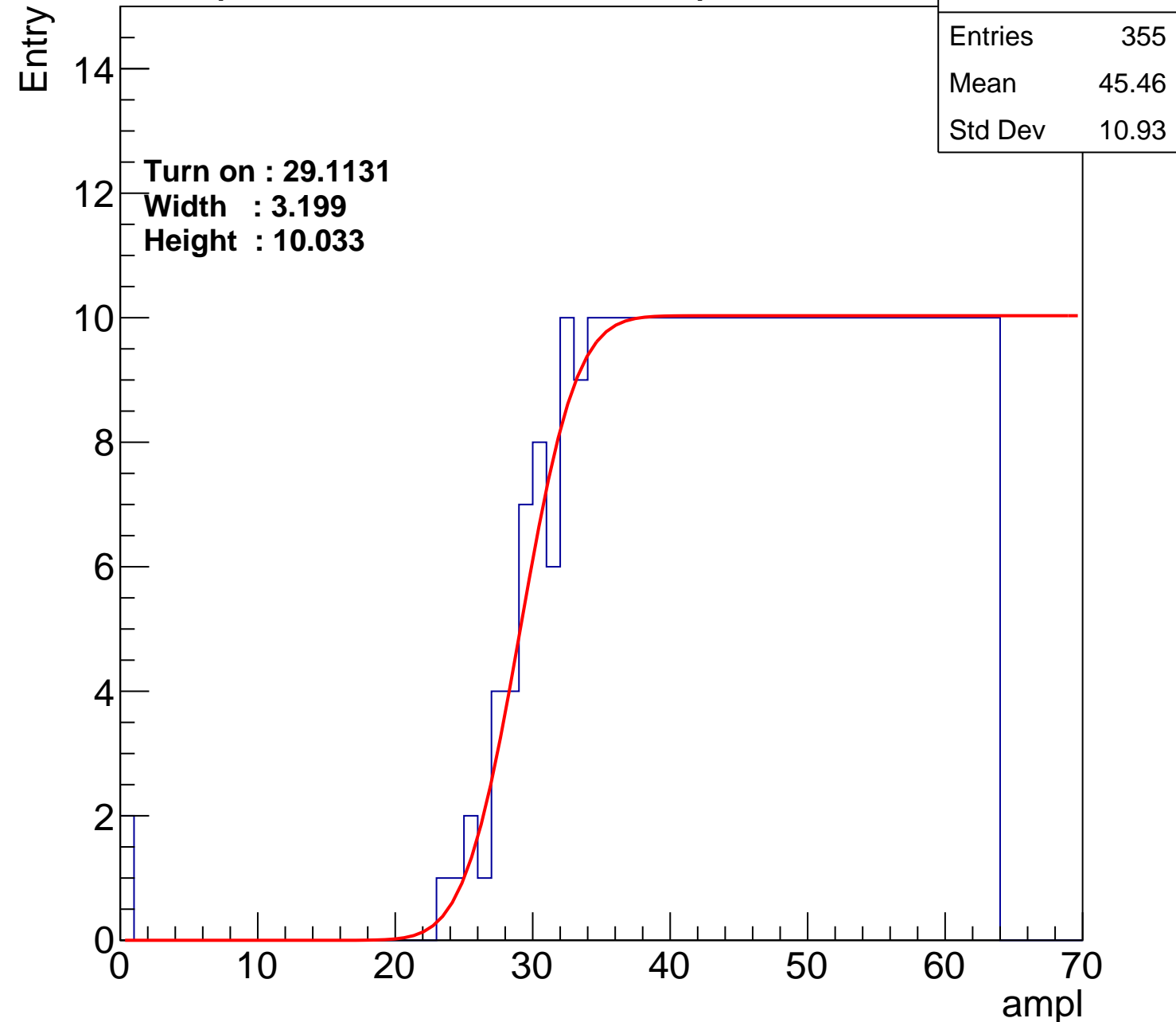
Width : 3.199

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch112

calib_packv5_042523_0143.root, FC#9, port A1

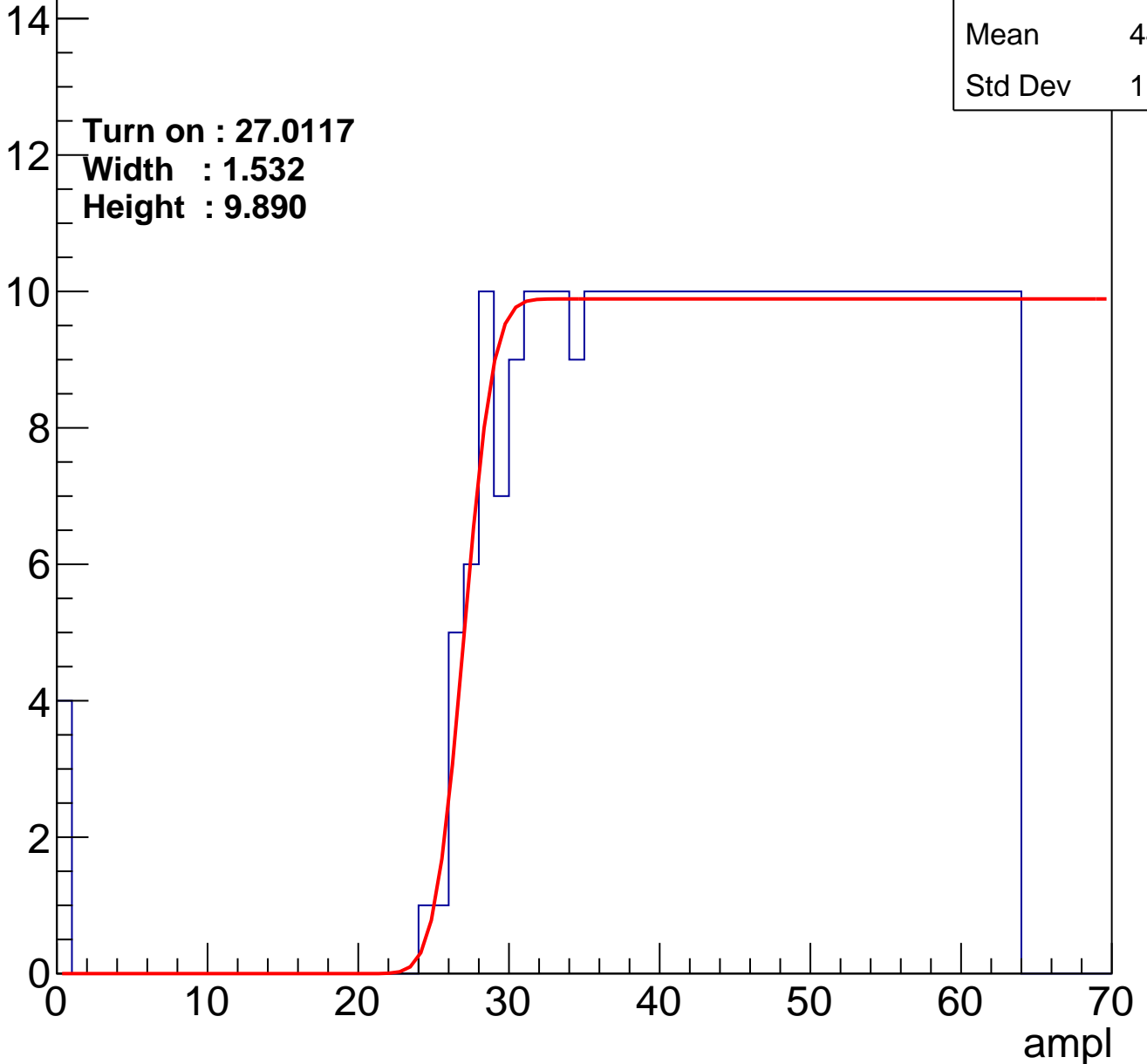
Entries	372
Mean	44.54
Std Dev	11.64

Turn on : 27.0117

Width : 1.532

Height : 9.890

Entry



B0L001S, U14-ch113

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.31
Std Dev	10.84

Turn on : 27.9470

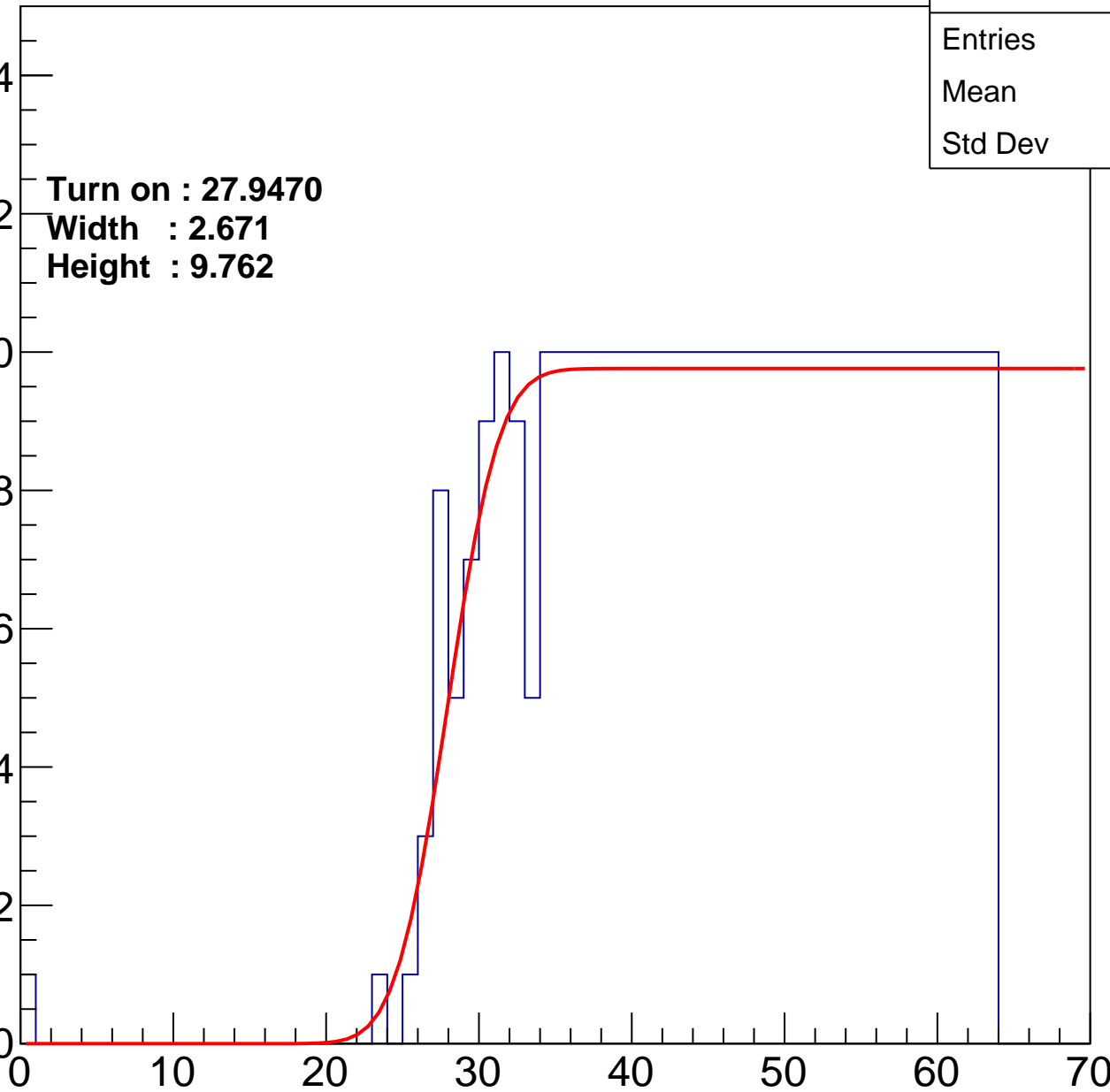
Width : 2.671

Height : 9.762

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch114

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
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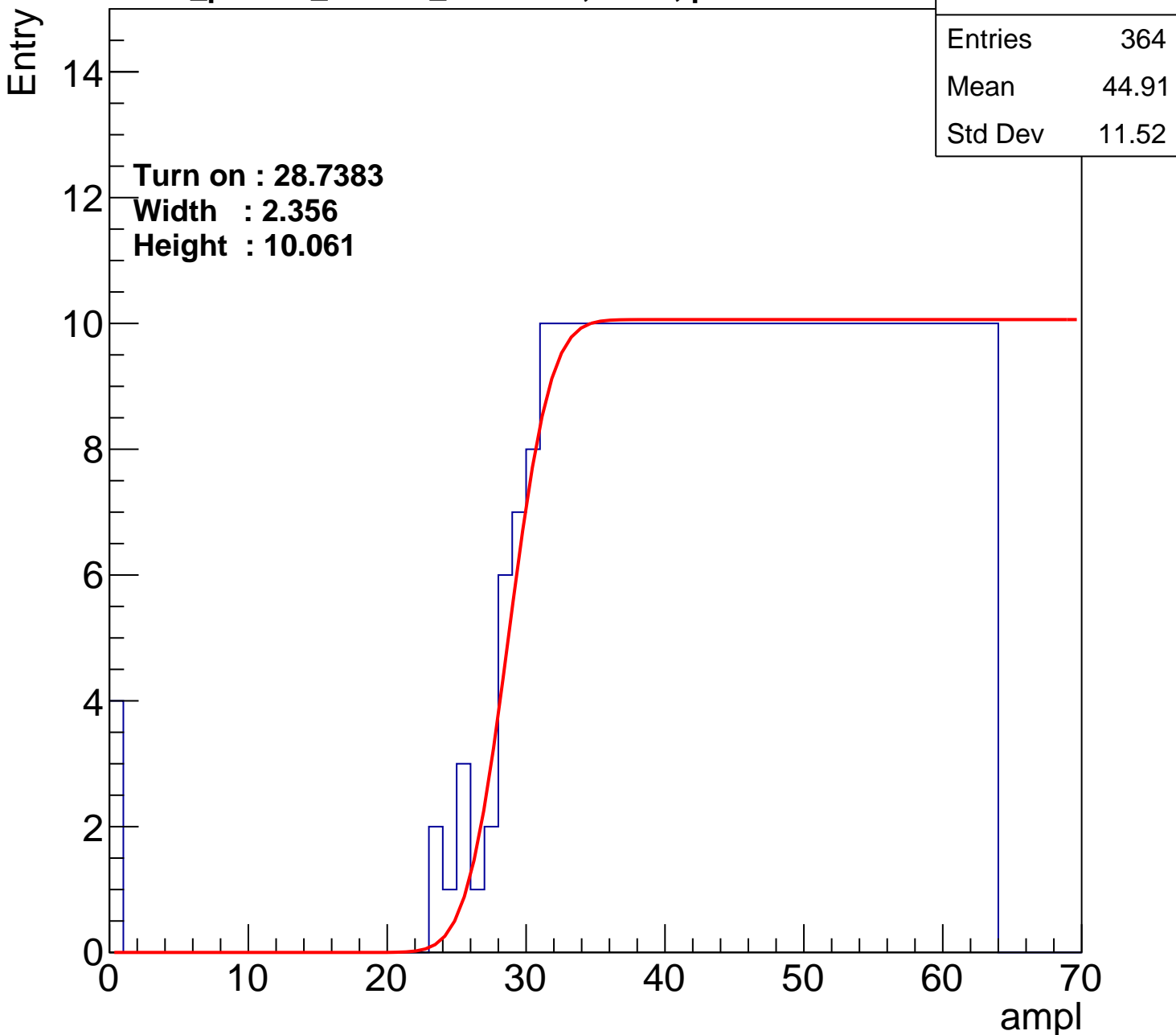
Mean	44.91
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Std Dev	11.52
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Turn on : 28.7383

Width : 2.356

Height : 10.061



B0L001S, U14-ch115

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	45.02
Std Dev	11

Turn on : 27.7320

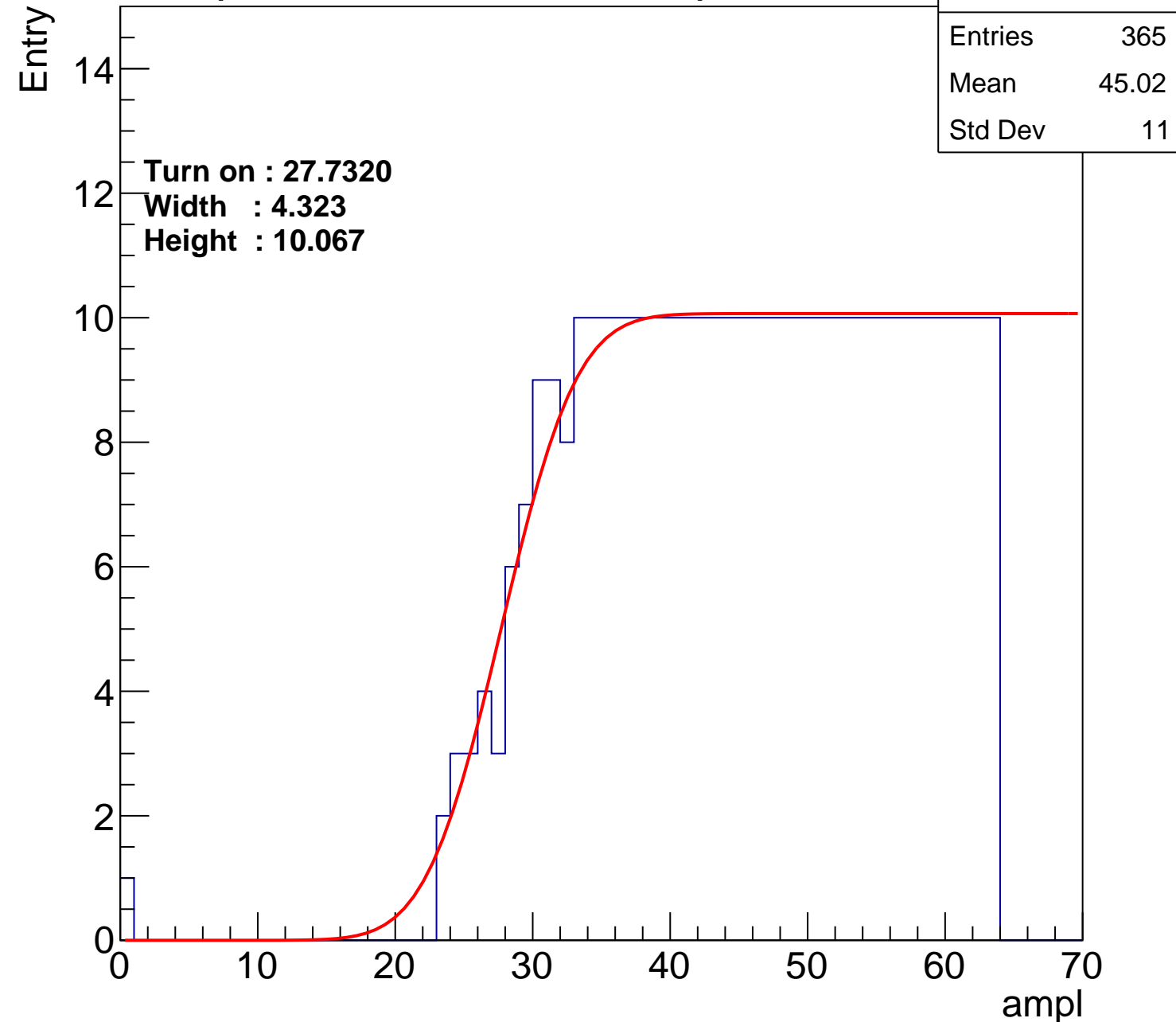
Width : 4.323

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch116

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.32
Std Dev	10.84

Turn on : 27.8452

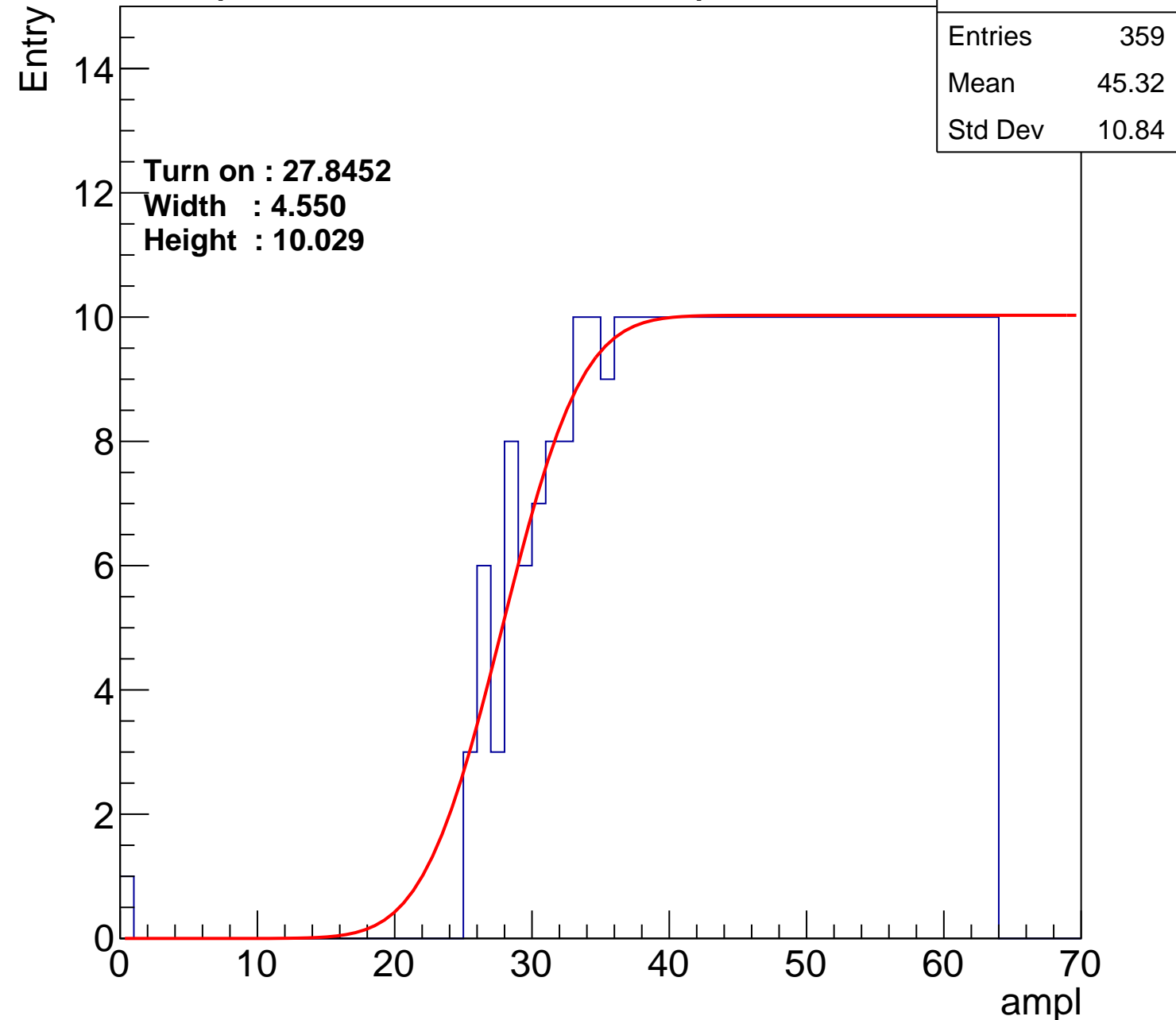
Width : 4.550

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch117

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.13
Std Dev	11.62

Turn on : 28.5569

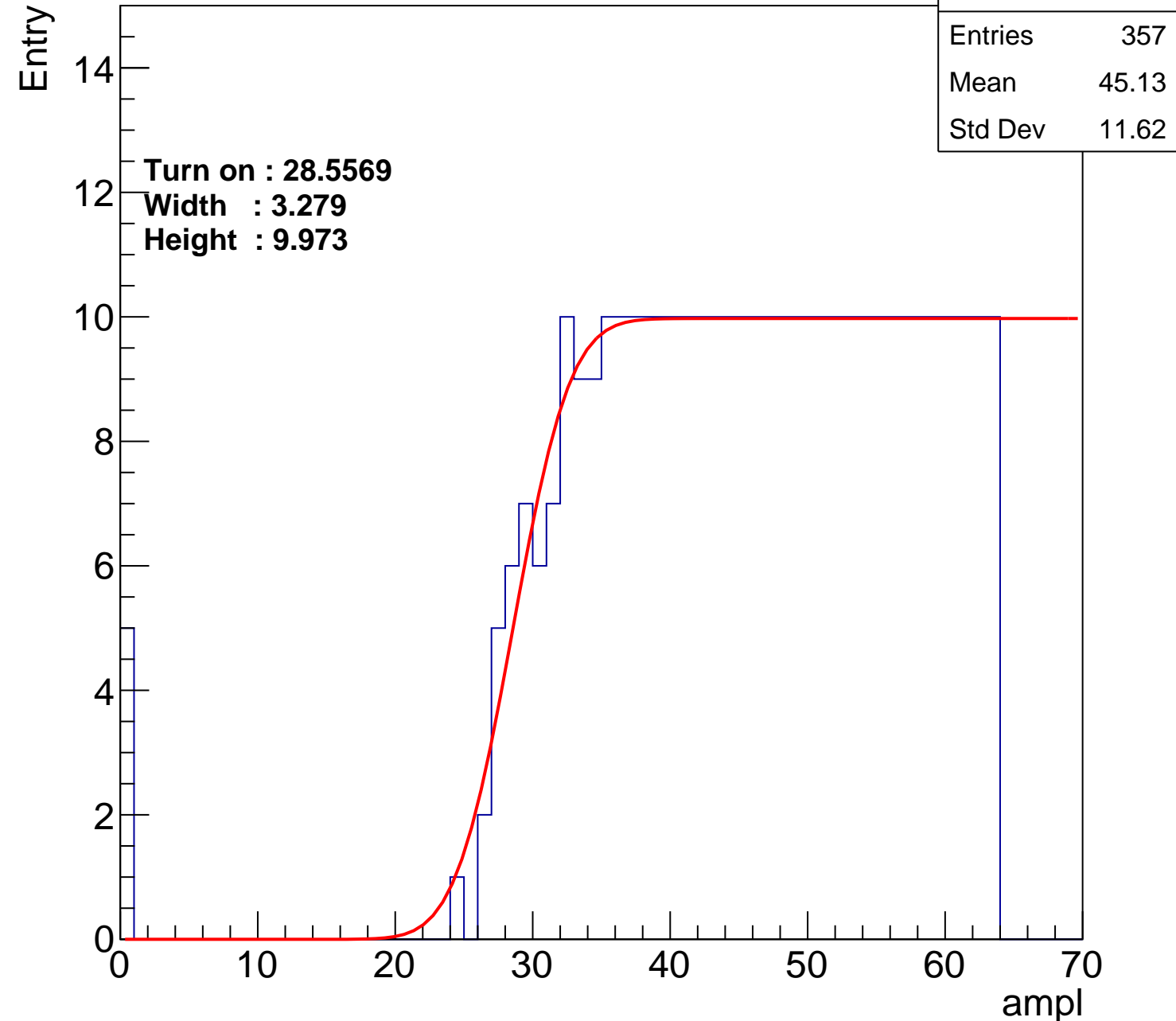
Width : 3.279

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch118

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.35
Std Dev	10.8

Turn on : 28.6550

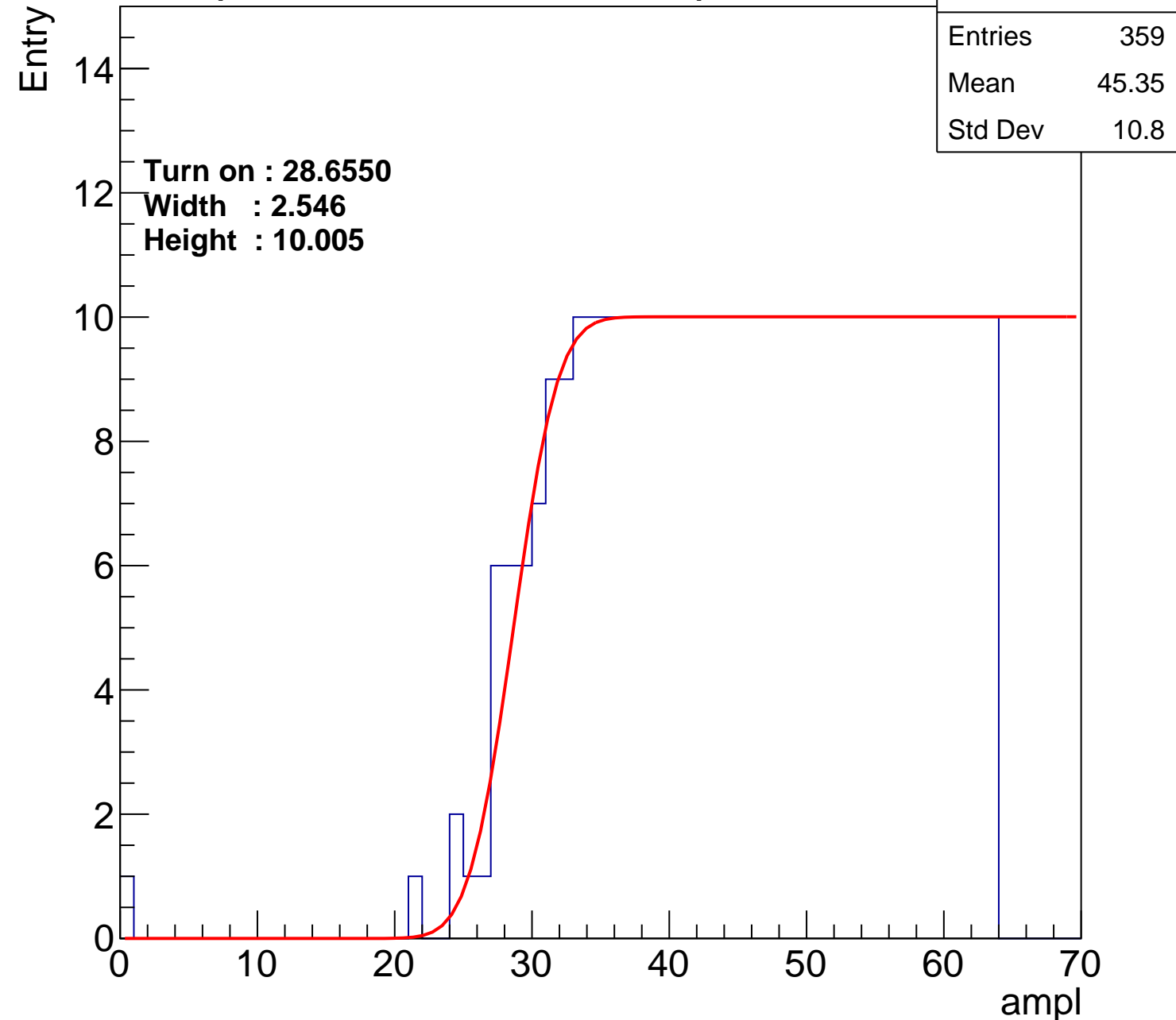
Width : 2.546

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch119

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.51
Std Dev	11.94

Turn on : 28.1512

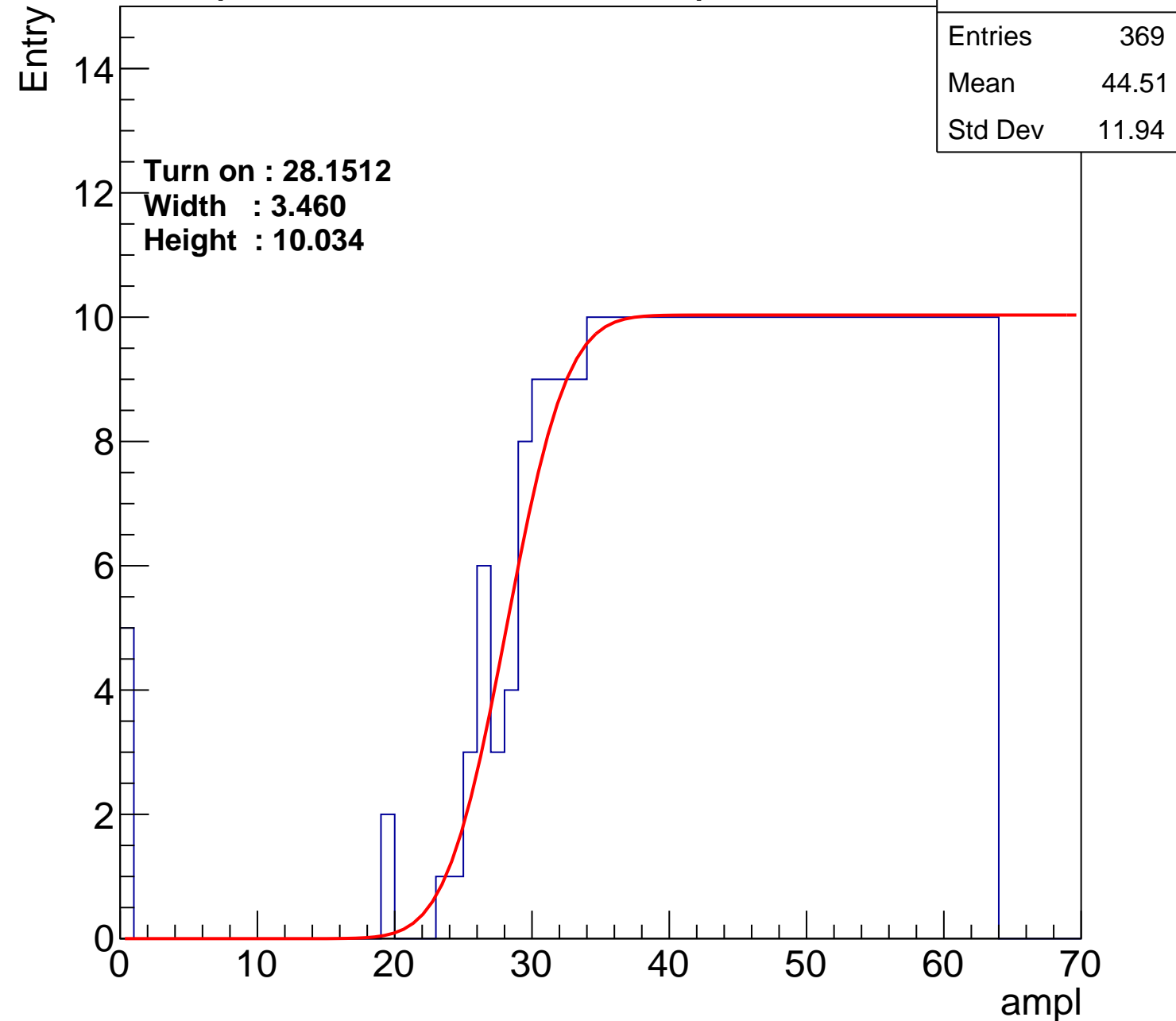
Width : 3.460

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch120

calib_packv5_042523_0143.root, FC#9, port A1

Entries	386
Mean	43.94
Std Dev	11.71

Turn on : 25.8650

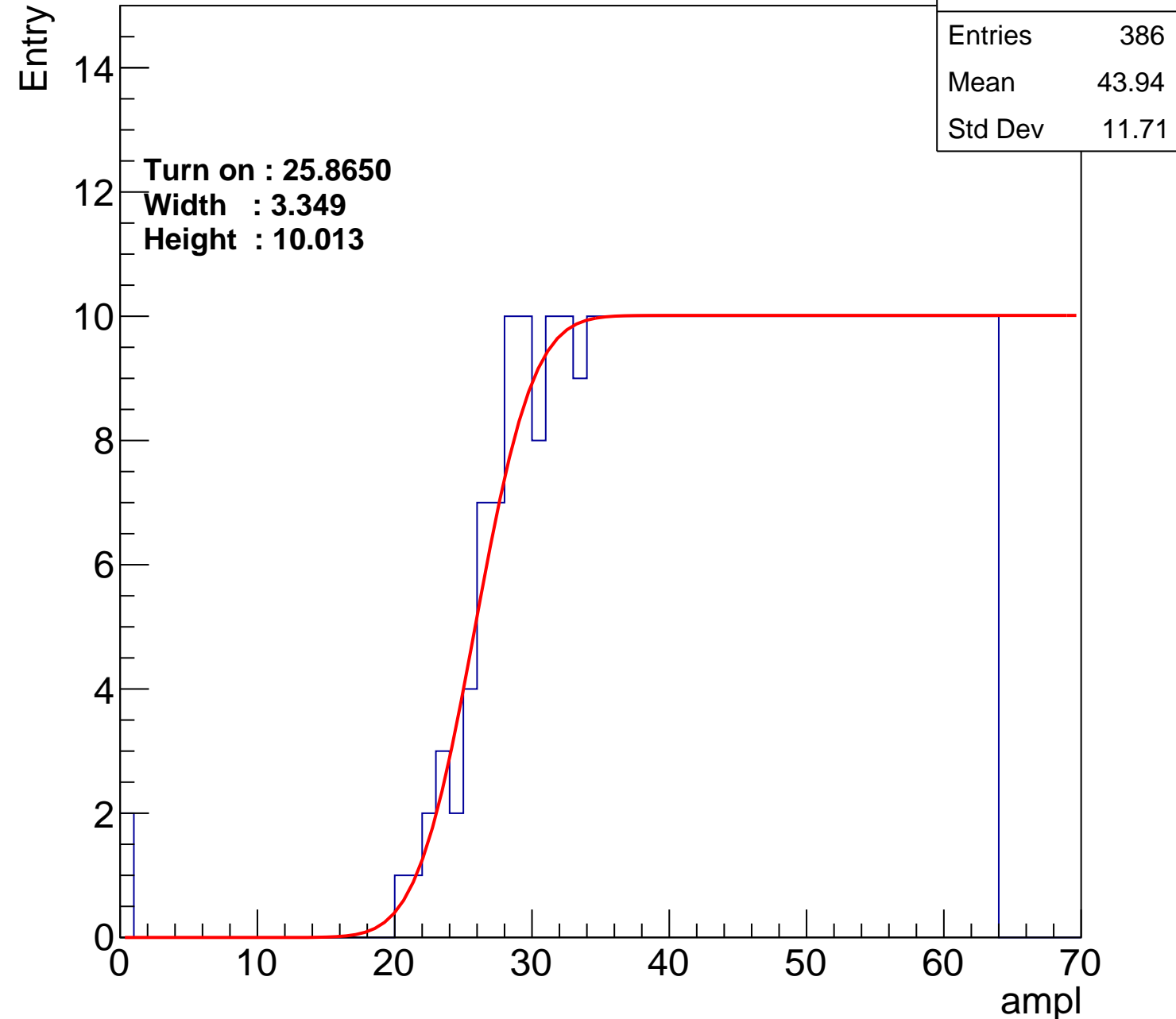
Width : 3.349

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch121

calib_packv5_042523_0143.root, FC#9, port A1

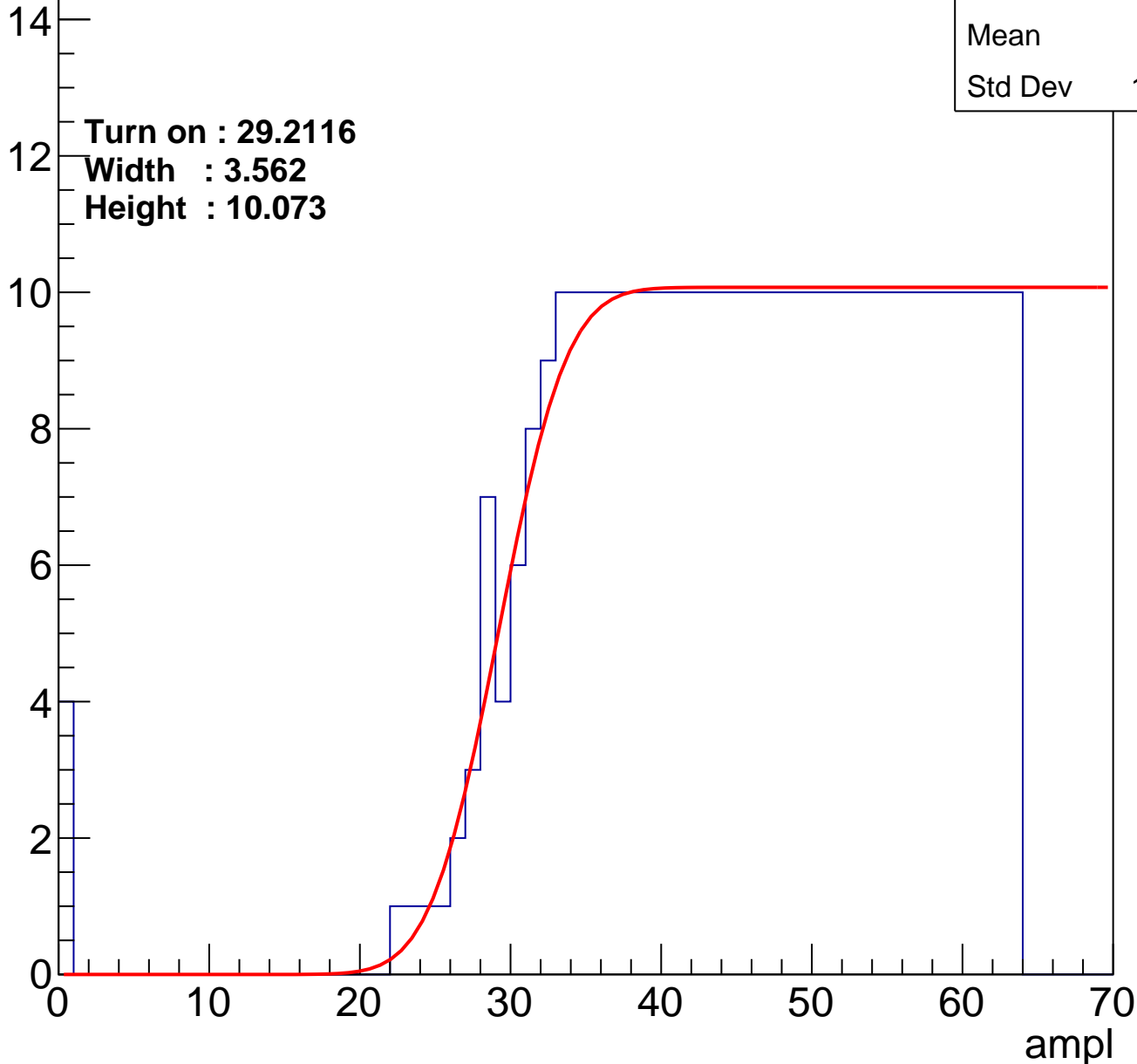
Entries	357
Mean	45.2
Std Dev	11.44

Turn on : 29.2116

Width : 3.562

Height : 10.073

Entry



B0L001S, U14-ch122

calib_packv5_042523_0143.root, FC#9, port A1

Entries	375
Mean	44.24
Std Dev	12.04

Turn on : 28.2970

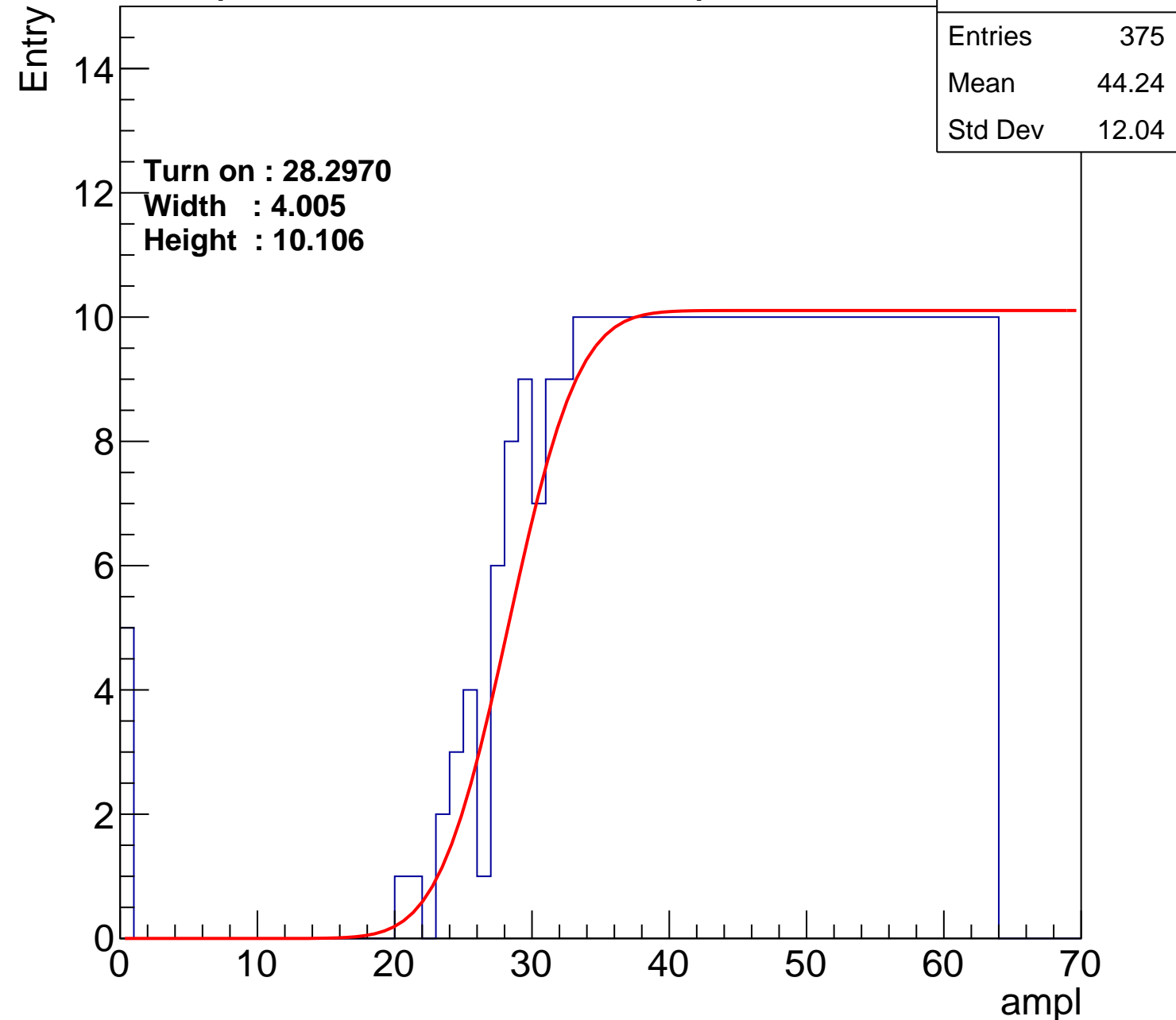
Width : 4.005

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch123

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.1369

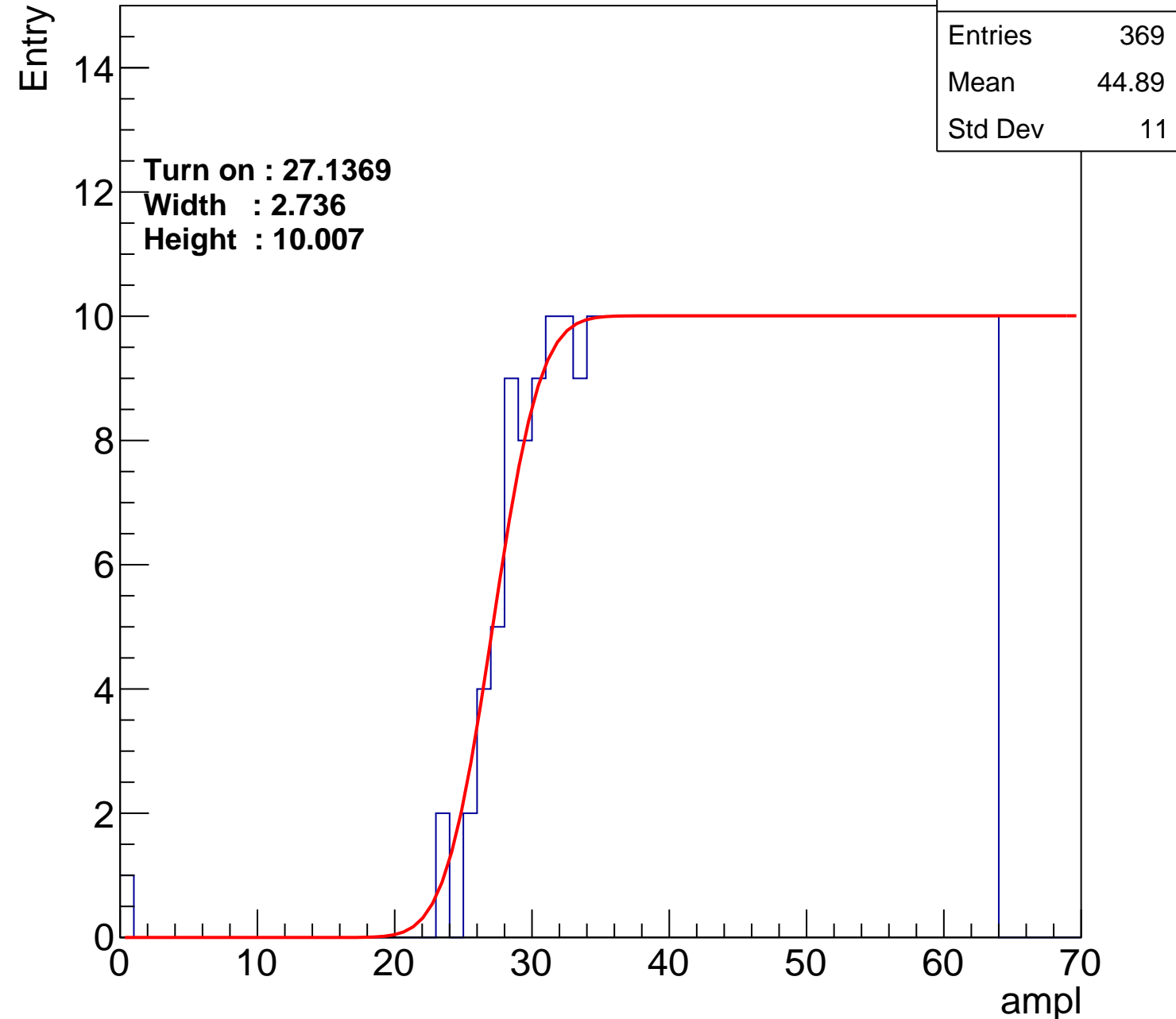
Width : 2.736

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch124

calib_packv5_042523_0143.root, FC#9, port A1

Entries	374
Mean	44.28
Std Dev	12.1

Turn on : 27.2790

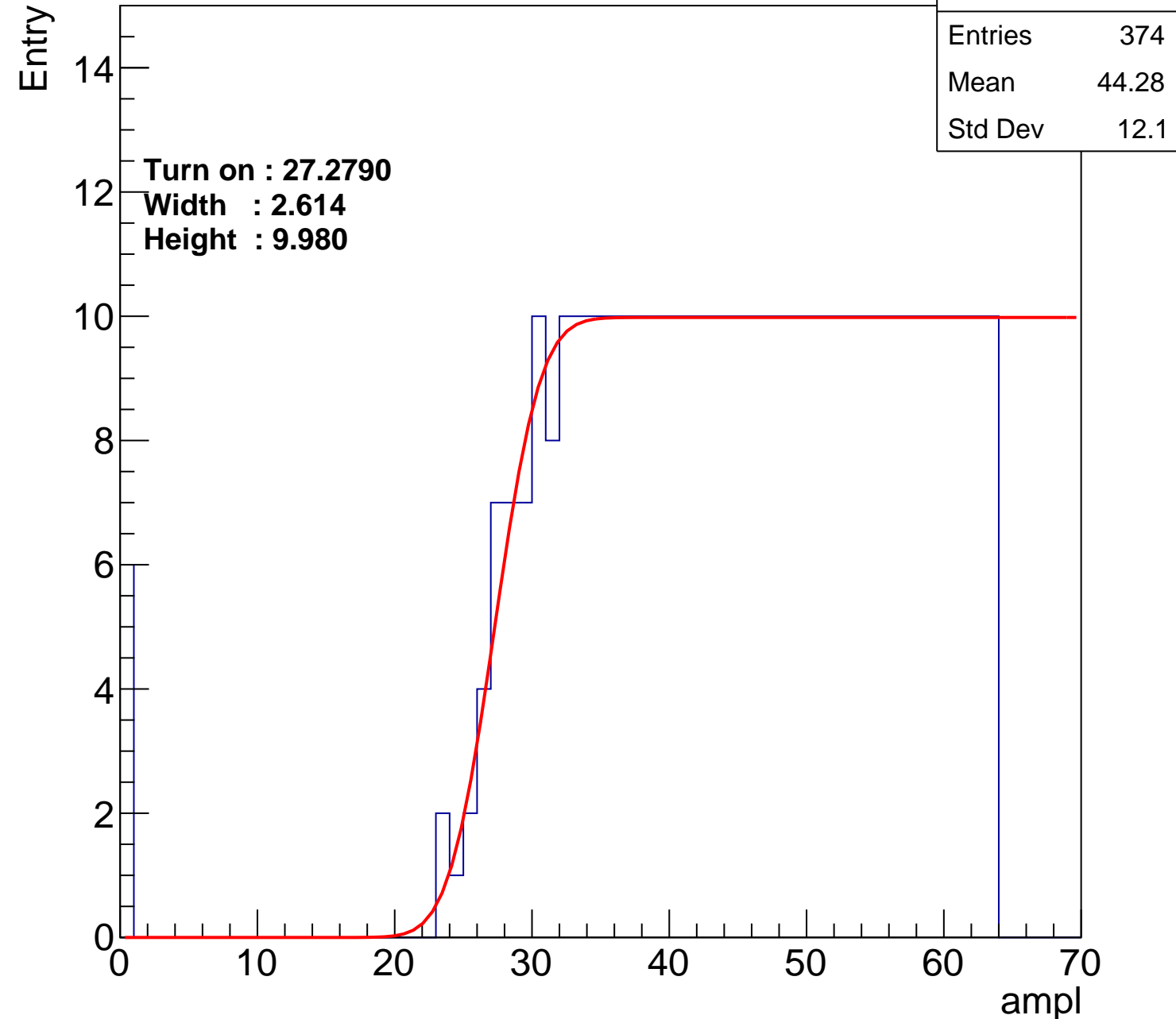
Width : 2.614

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch125

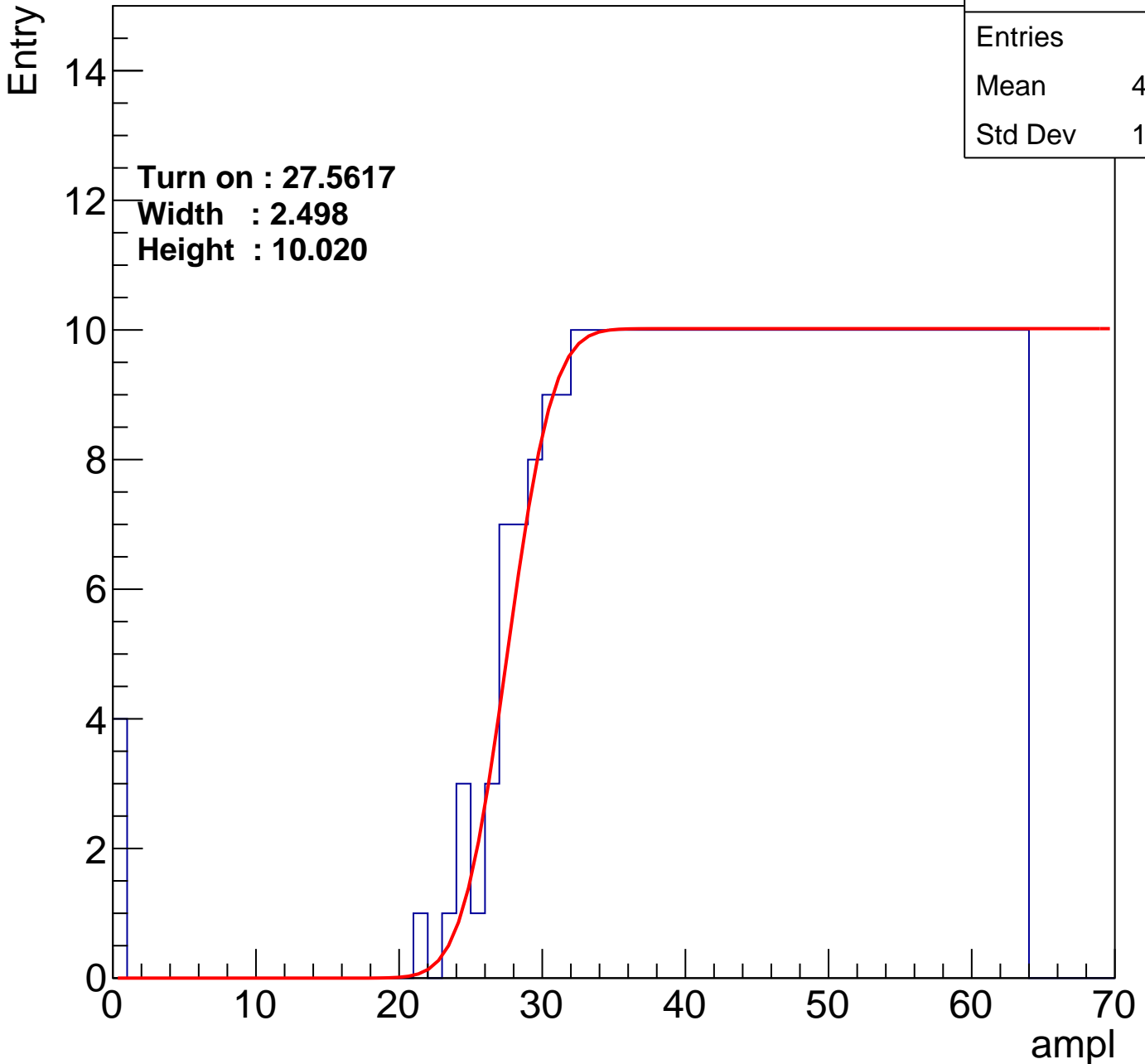
calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.46
Std Dev	11.72

Turn on : 27.5617

Width : 2.498

Height : 10.020



B0L001S, U14-ch126

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.79
Std Dev	11.82

Turn on : 28.3977

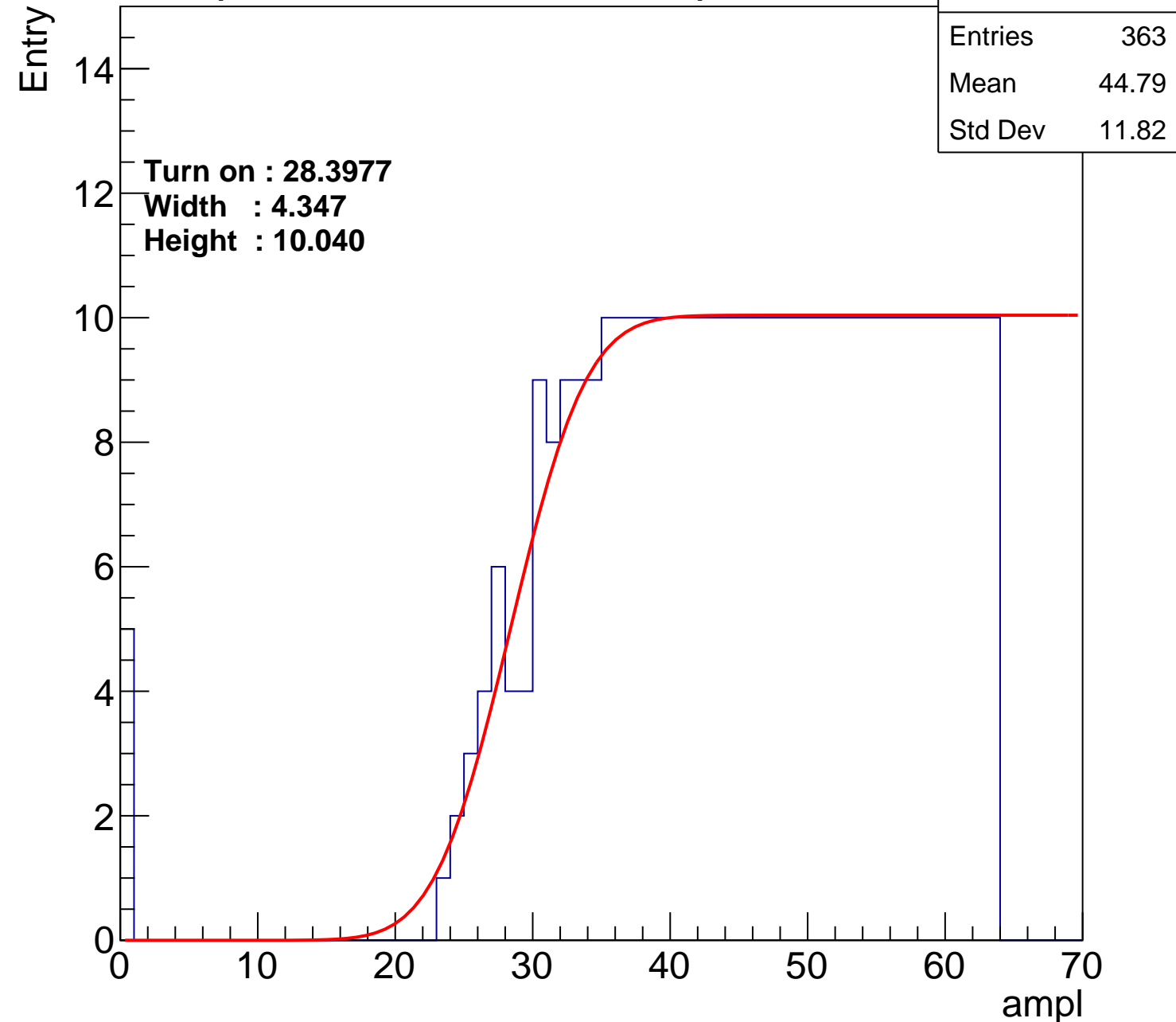
Width : 4.347

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U14-ch127

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
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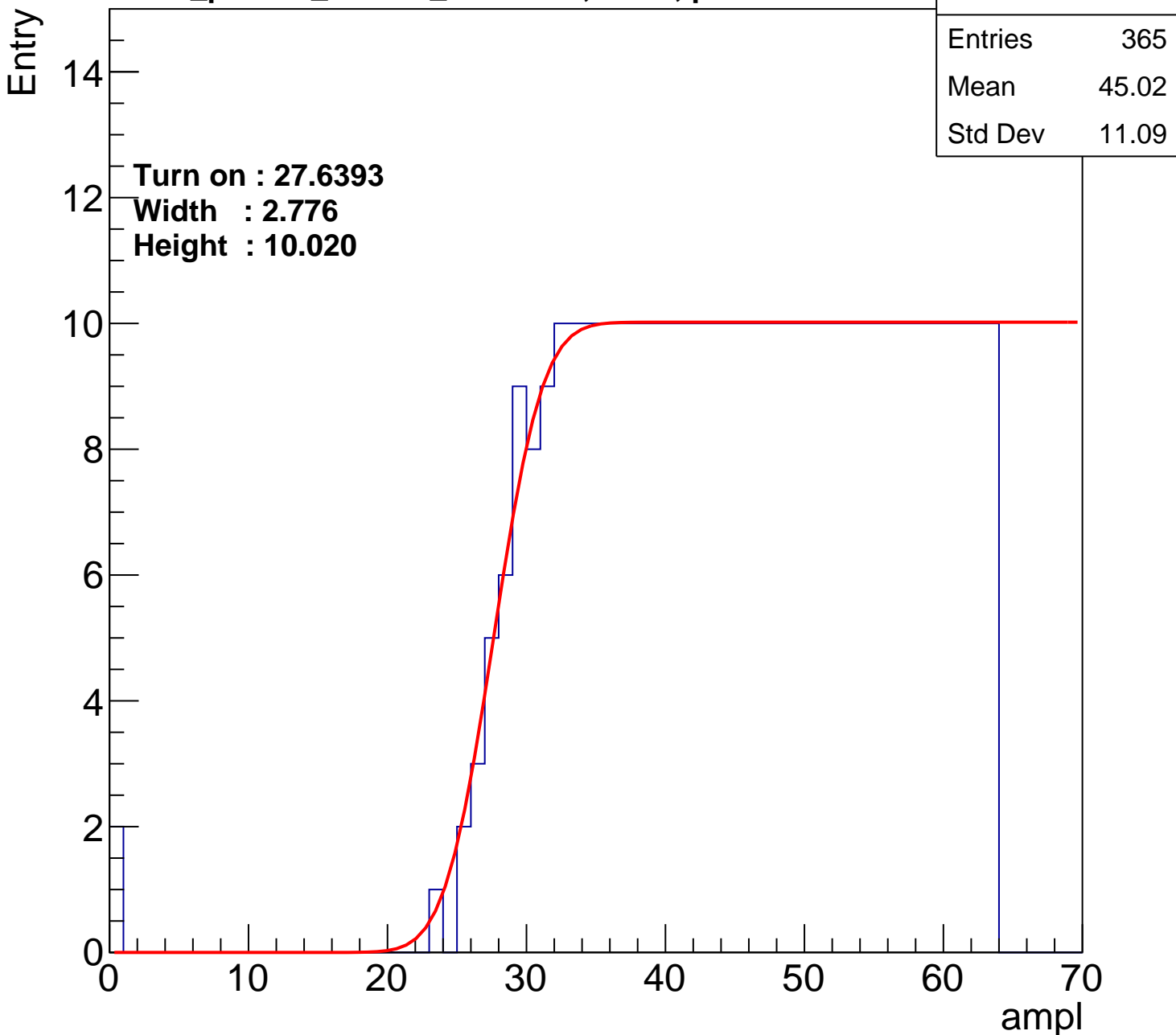
Mean	45.02
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Std Dev	11.09
---------	-------

Turn on : 27.6393

Width : 2.776

Height : 10.020



B0L001S, U14-ch127

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	45.02
Std Dev	11.09

Turn on : 27.6393

Width : 2.776

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl

