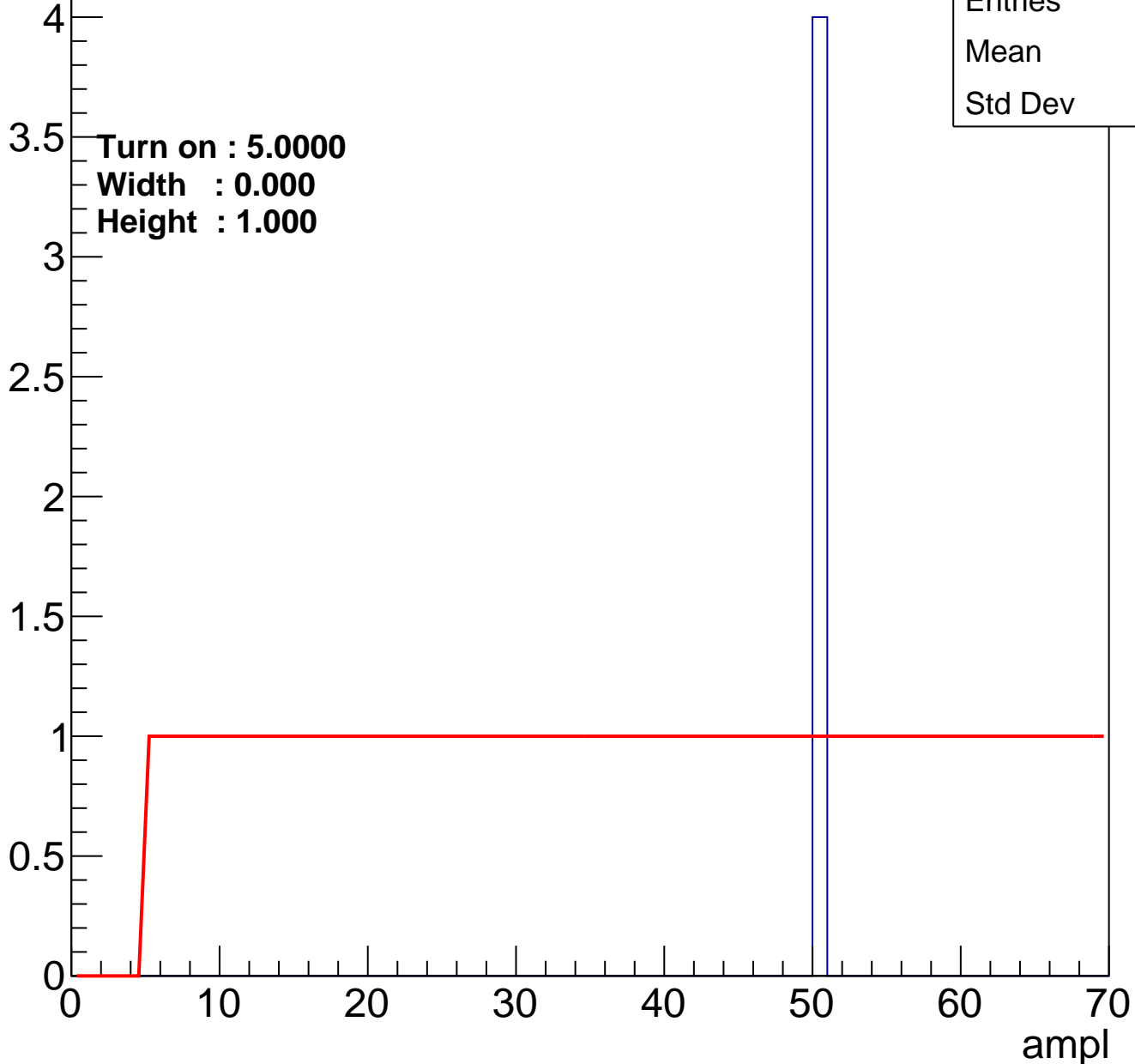


B0L100S, U13-ch0

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch1

calib_packv5_042523_0143.root, FC#6, port A1

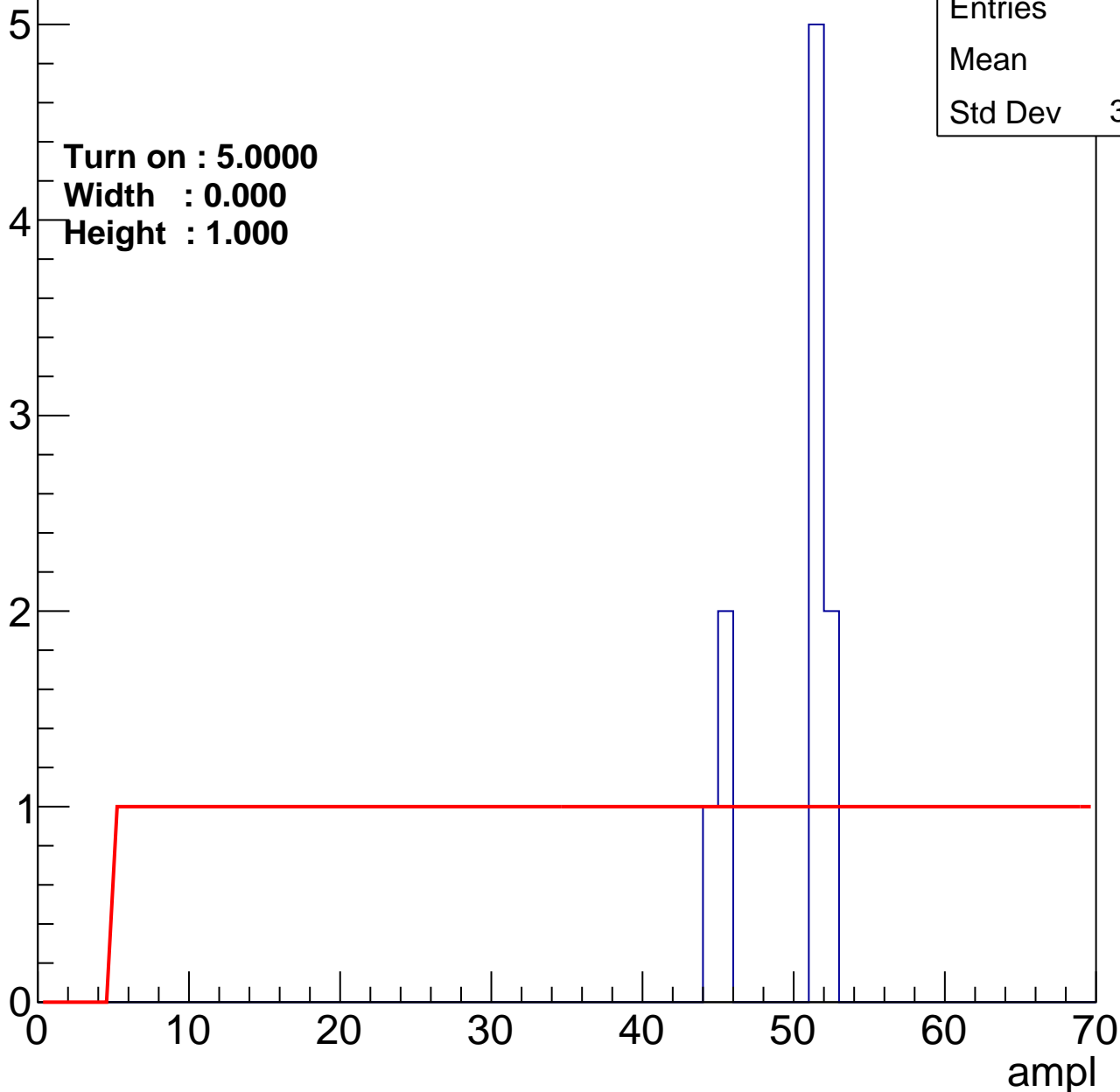
Entry

Entries	10
Mean	49.3
Std Dev	3.068

Turn on : 5.0000

Width : 0.000

Height : 1.000



B0L100S, U13-ch2

calib_packv5_042523_0143.root, FC#6, port A1

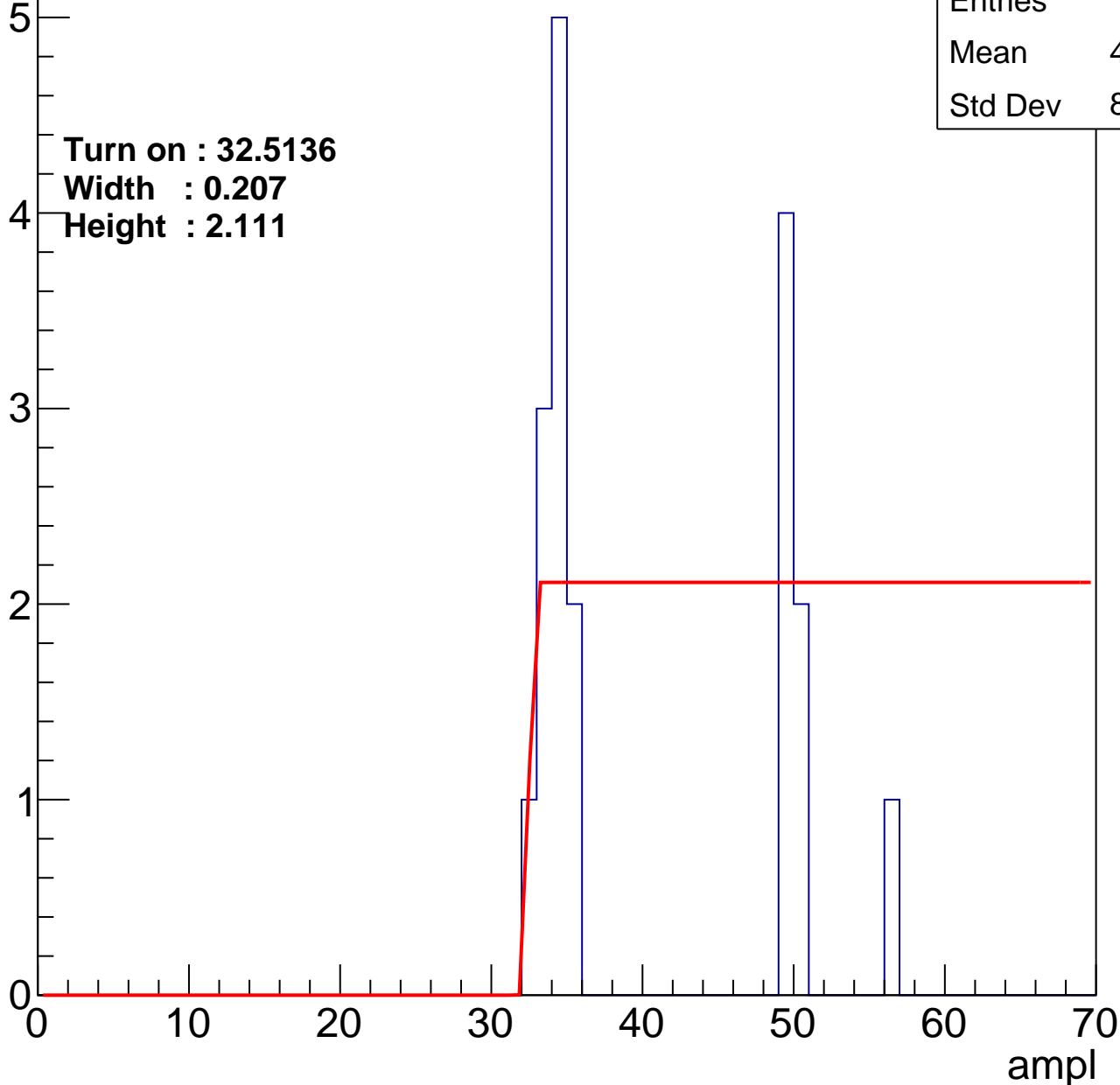
Entry

Entries	18
Mean	40.17
Std Dev	8.234

Turn on : 32.5136

Width : 0.207

Height : 2.111



B0L100S, U13-ch3

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch4

calib_packv5_042523_0143.root, FC#6, port A1

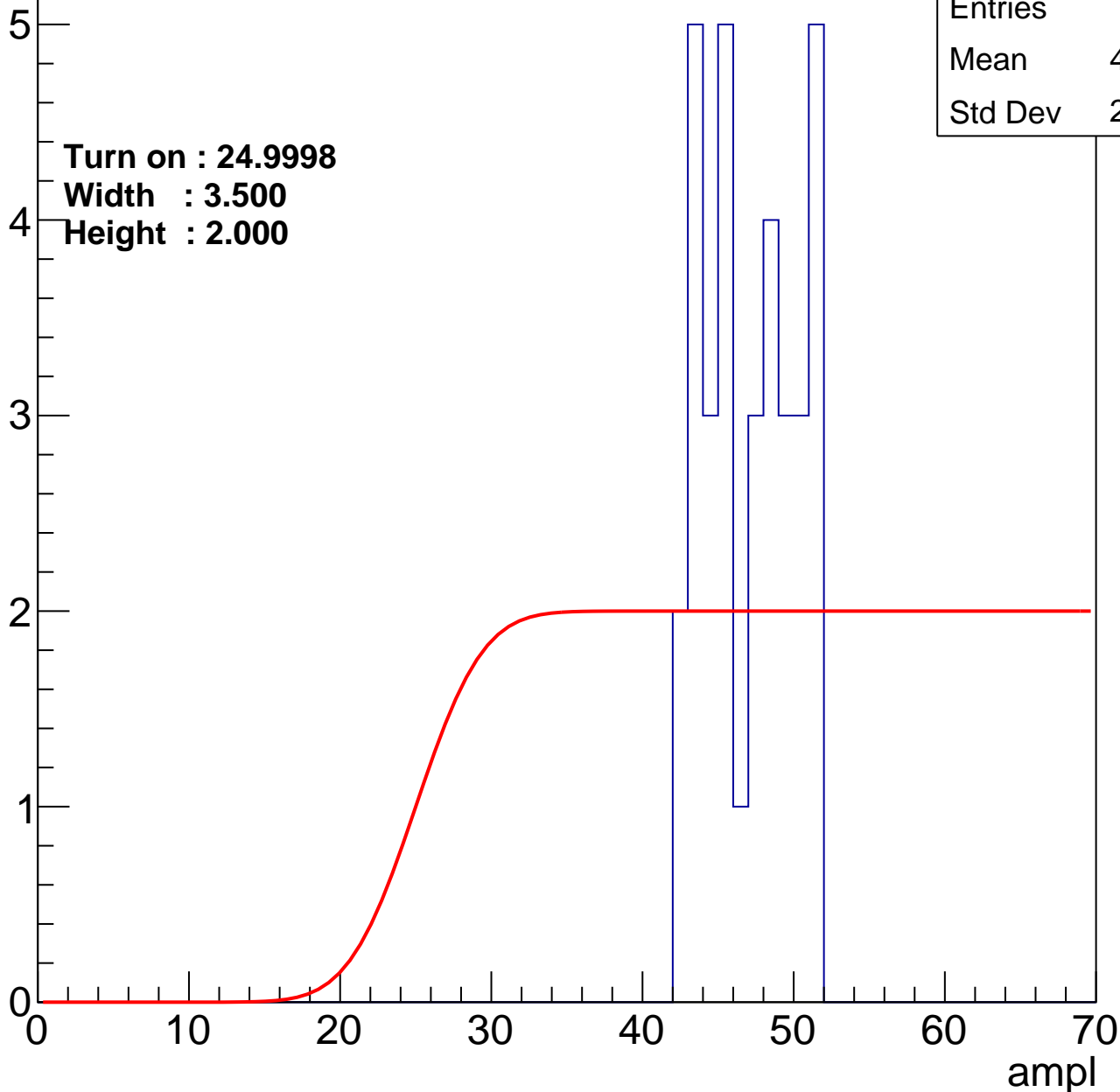
Entry

Entries	34
Mean	46.68
Std Dev	2.958

Turn on : 24.9998

Width : 3.500

Height : 2.000



B0L100S, U13-ch5

calib_packv5_042523_0143.root, FC#6, port A1

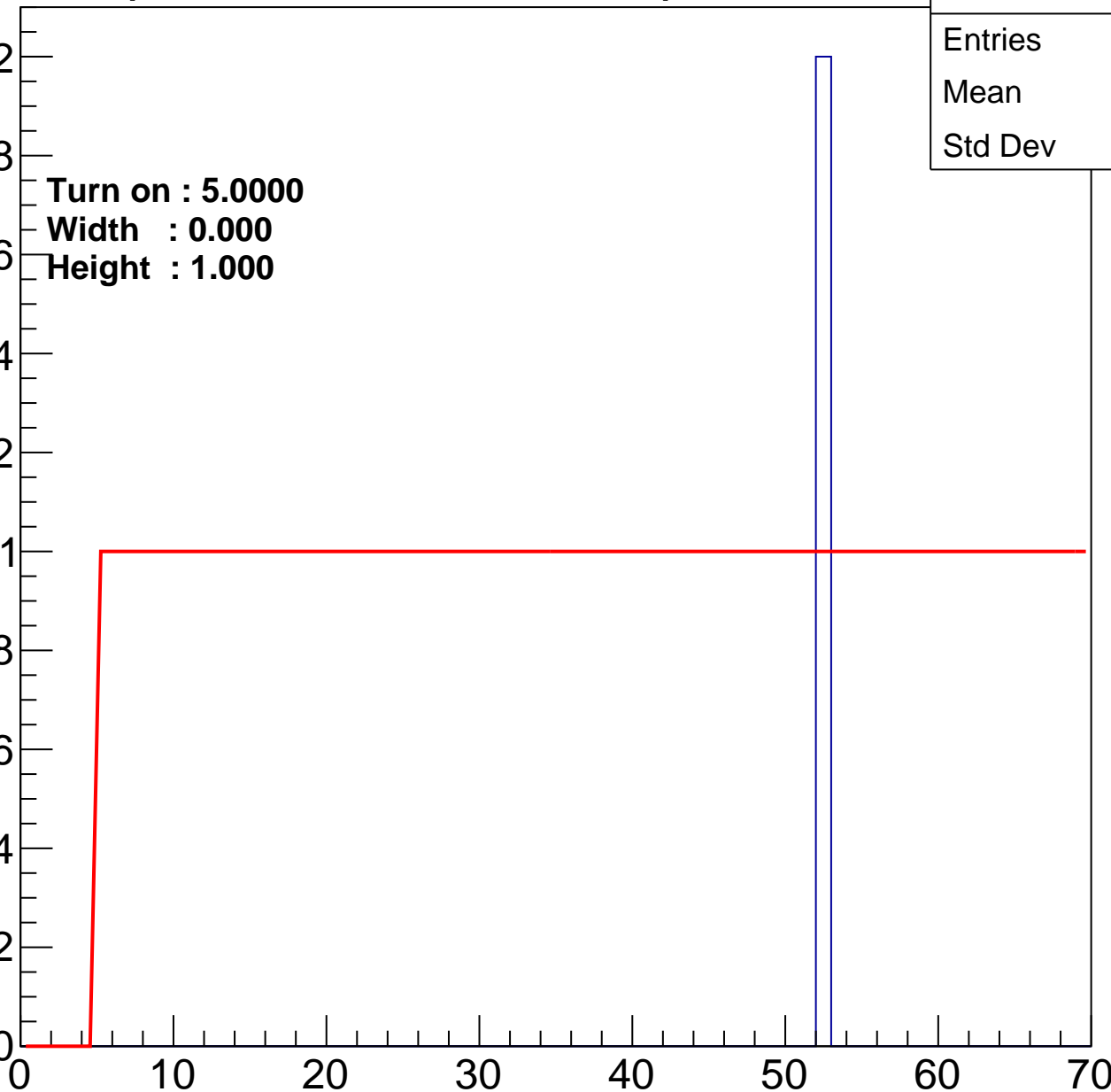
Entry

2
1.8
1.6
1.4
1.2
1.0
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	2
Mean	52
Std Dev	0

ampl



B0L100S, U13-ch6

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch7

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch8

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch9

calib_packv5_042523_0143.root, FC#6, port A1

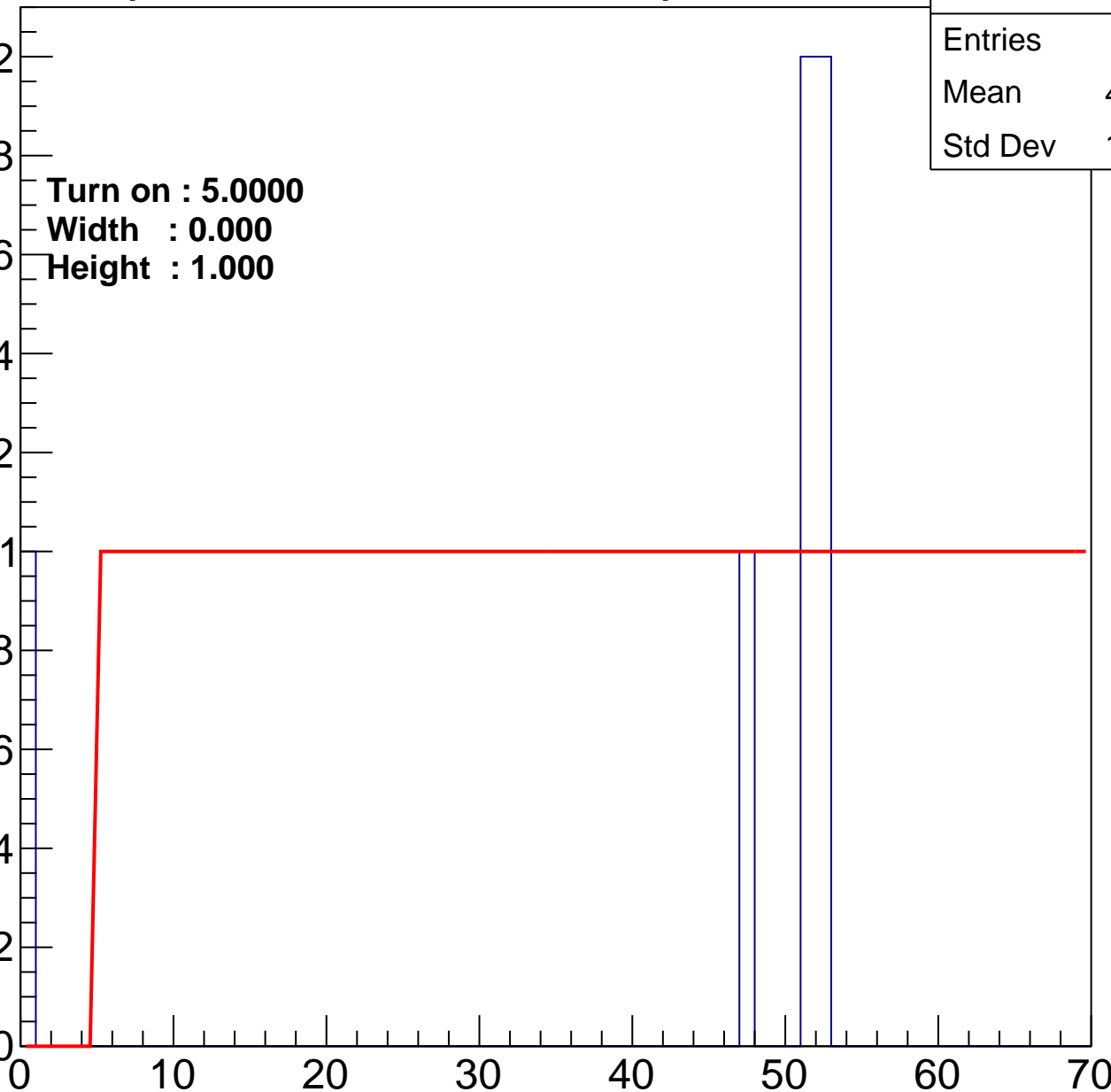
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	6
Mean	42.17
Std Dev	18.93

ampl



B0L100S, U13-ch10

calib_packv5_042523_0143.root, FC#6, port A1

Entry

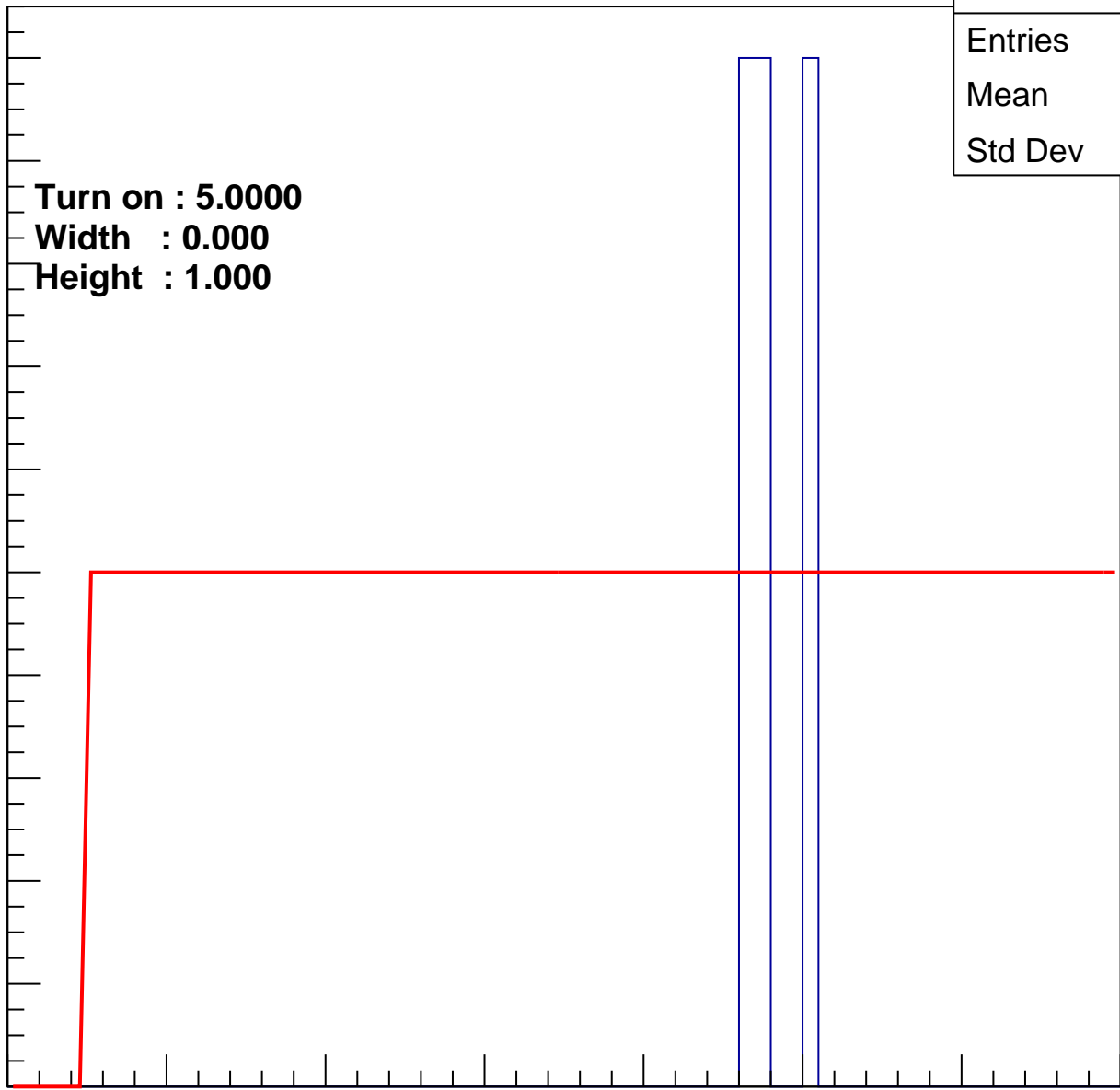
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	6
Mean	47.67
Std Dev	1.7

0 10 20 30 40 50 60 70

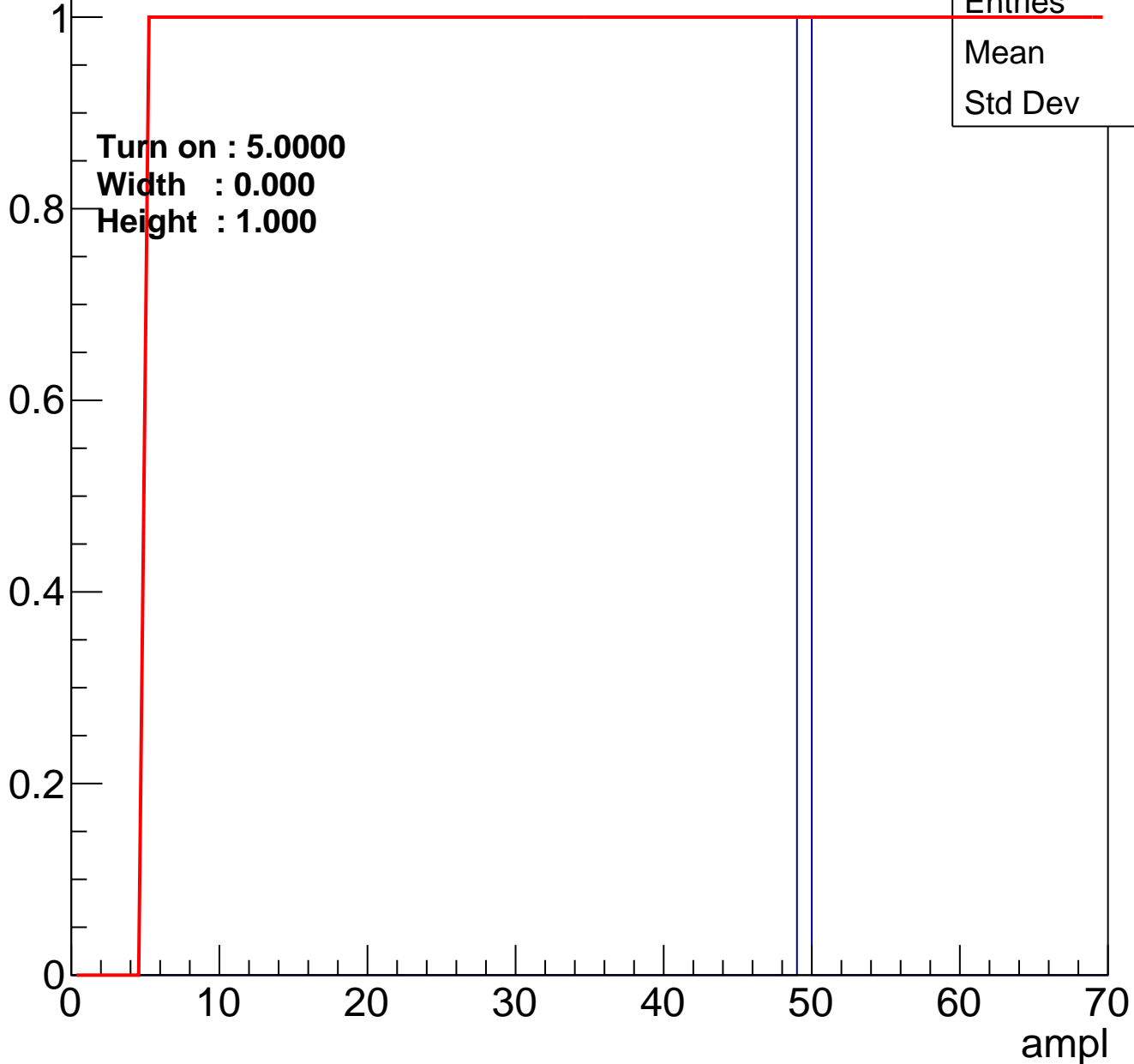
ampl



B0L100S, U13-ch11

calib_packv5_042523_0143.root, FC#6, port A1

Entry

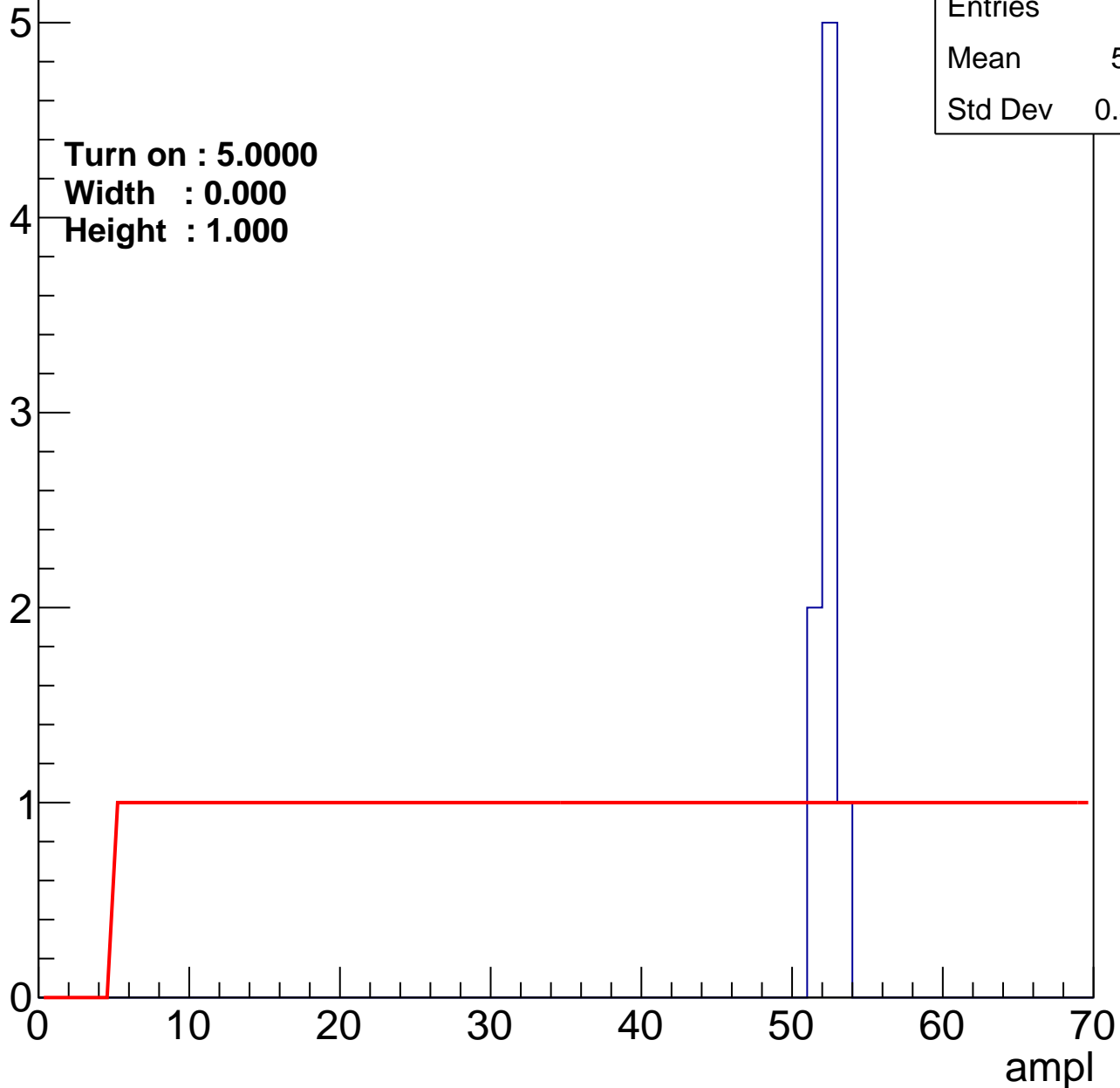


Entries	1
Mean	49
Std Dev	0

B0L100S, U13-ch12

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	8
Mean	51.88
Std Dev	0.5995

B0L100S, U13-ch13

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch14

calib_packv5_042523_0143.root, FC#6, port A1

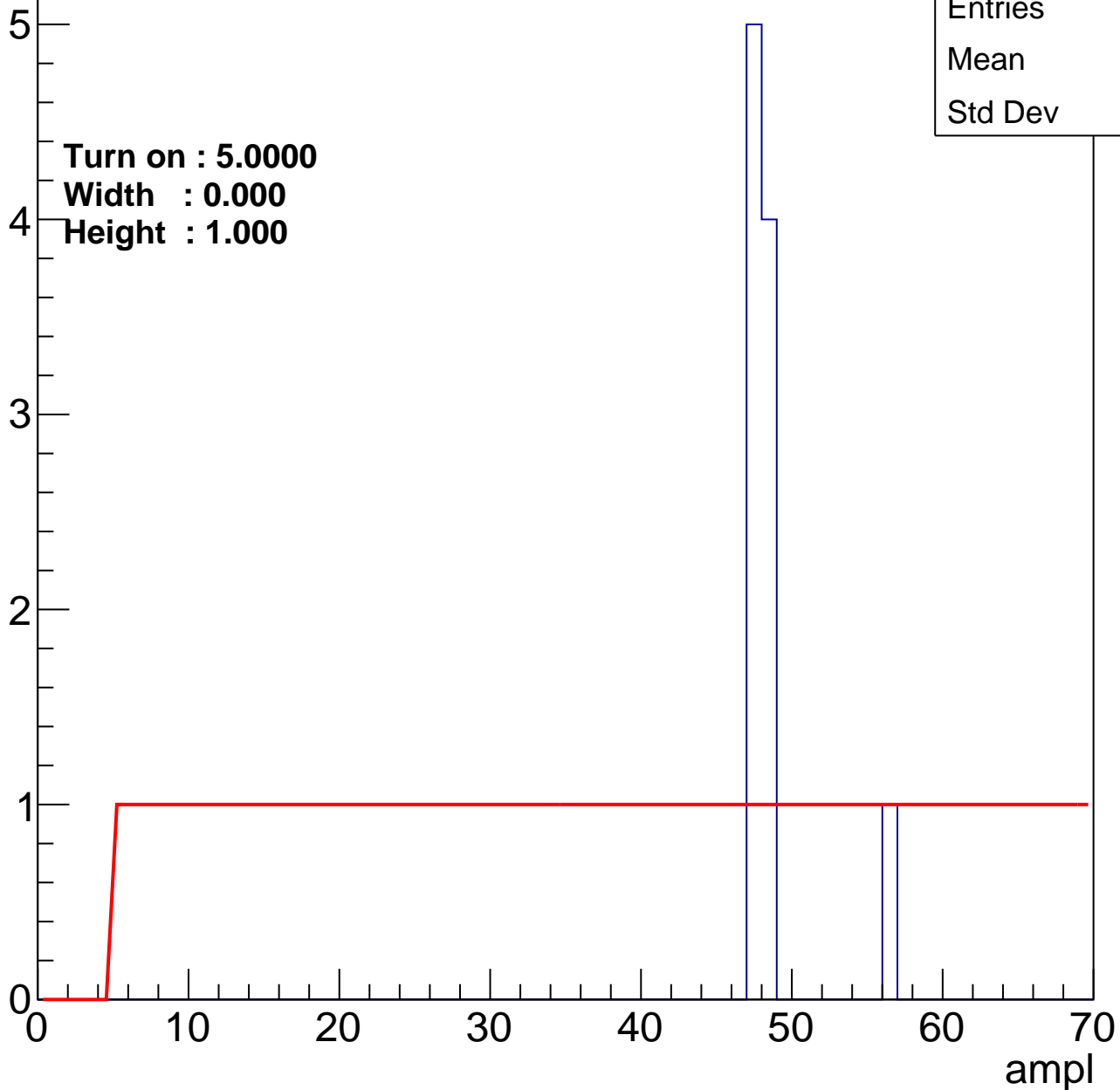
Entry

Entries	10
Mean	48.3
Std Dev	2.61

Turn on : 5.0000

Width : 0.000

Height : 1.000



B0L100S, U13-ch15

calib_packv5_042523_0143.root, FC#6, port A1

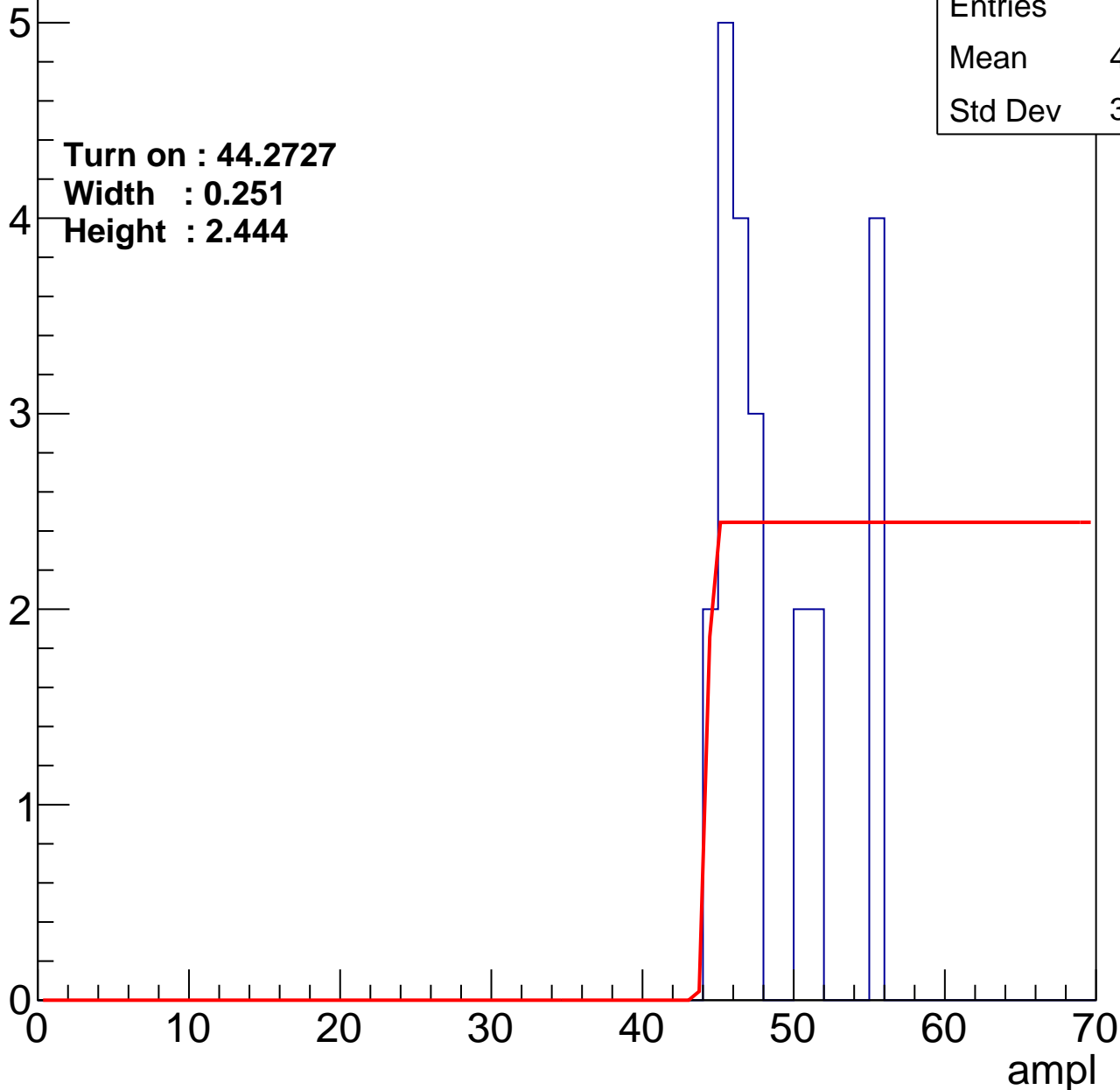
Entry

Entries	22
Mean	48.18
Std Dev	3.798

Turn on : 44.2727

Width : 0.251

Height : 2.444



B0L100S, U13-ch16

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch17

calib_packv5_042523_0143.root, FC#6, port A1

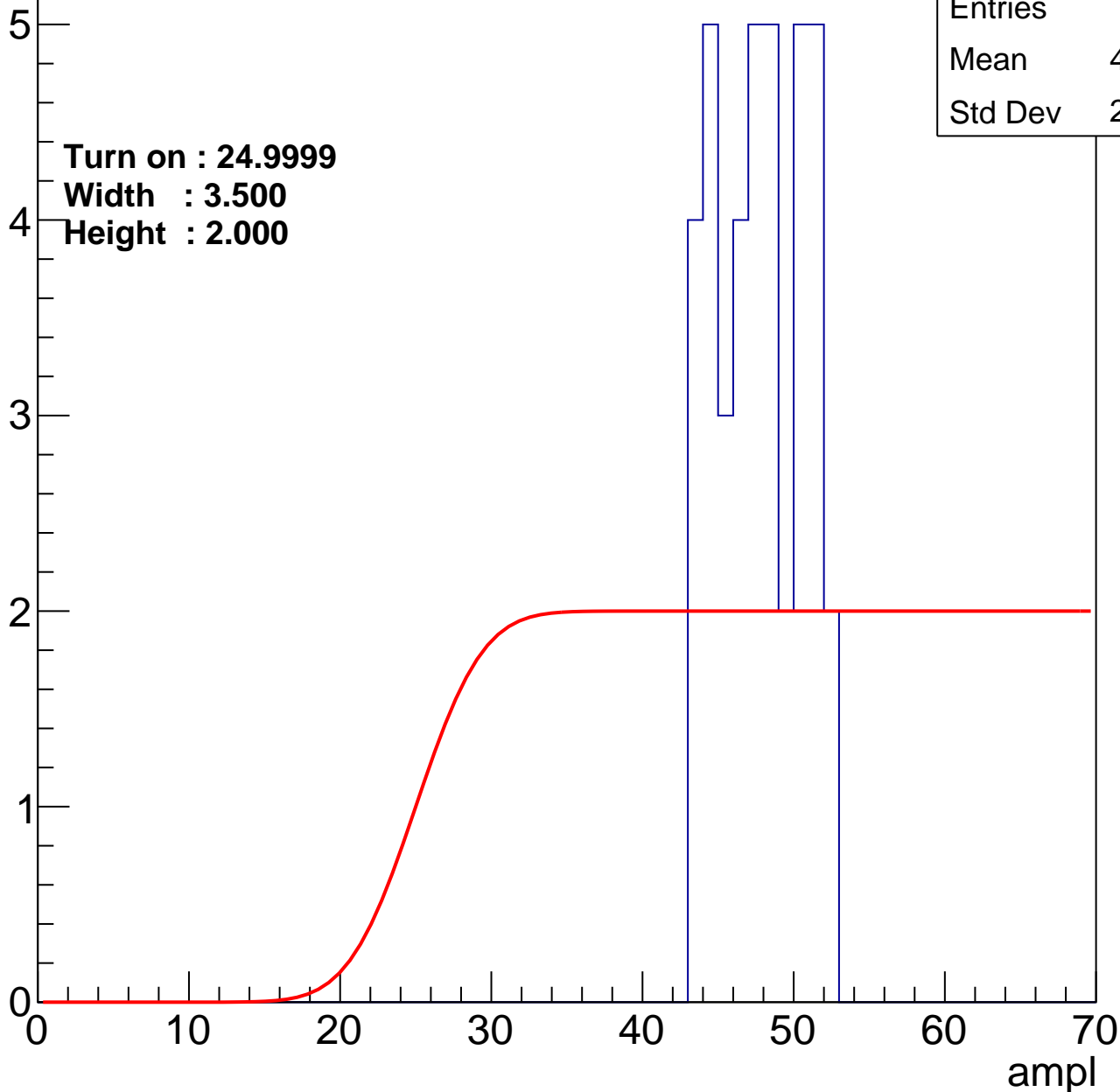
Entry

Entries	40
Mean	47.33
Std Dev	2.778

Turn on : 24.9999

Width : 3.500

Height : 2.000



B0L100S, U13-ch18

calib_packv5_042523_0143.root, FC#6, port A1

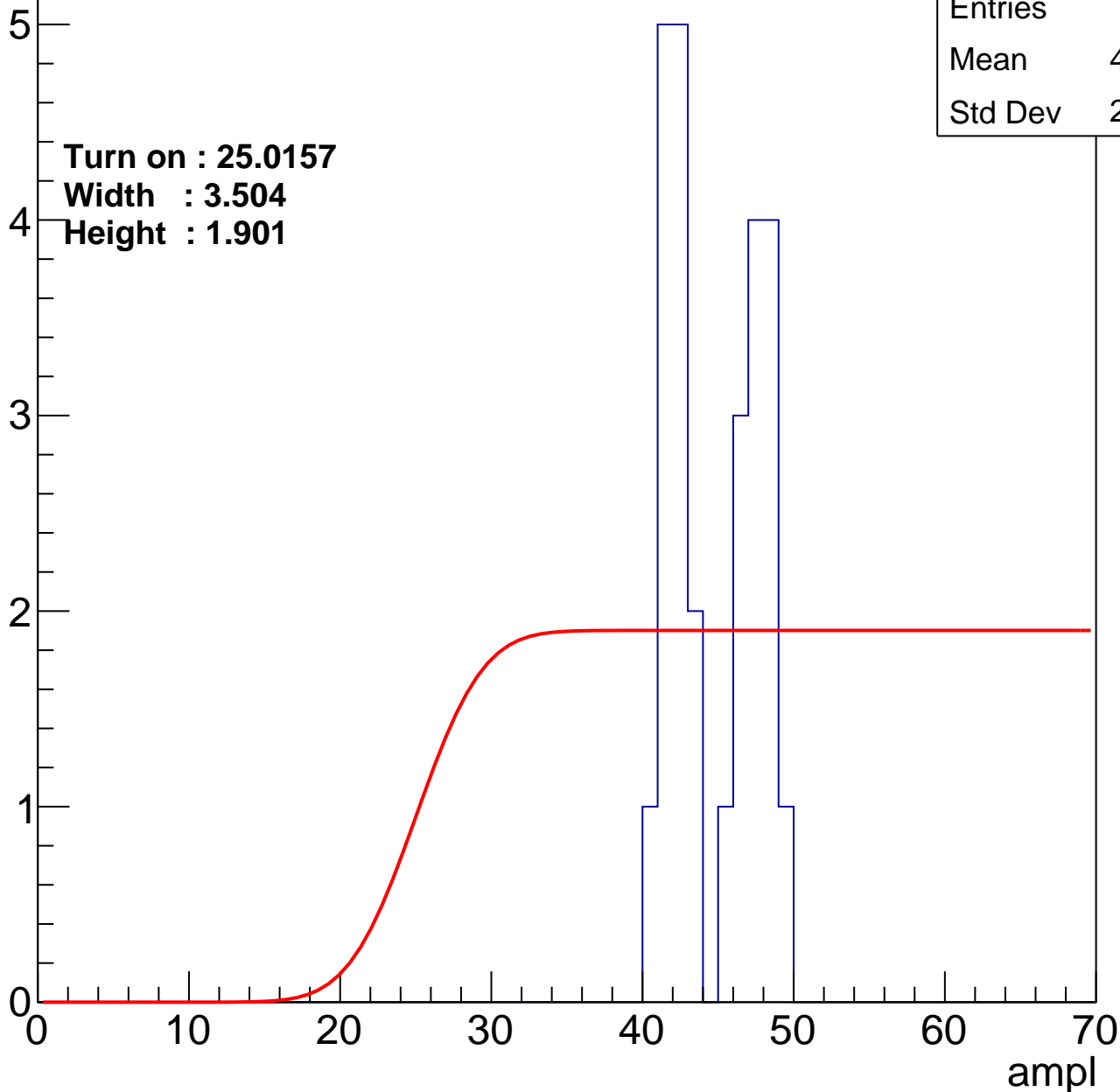
Entry

Entries	26
Mean	44.35
Std Dev	2.895

Turn on : 25.0157

Width : 3.504

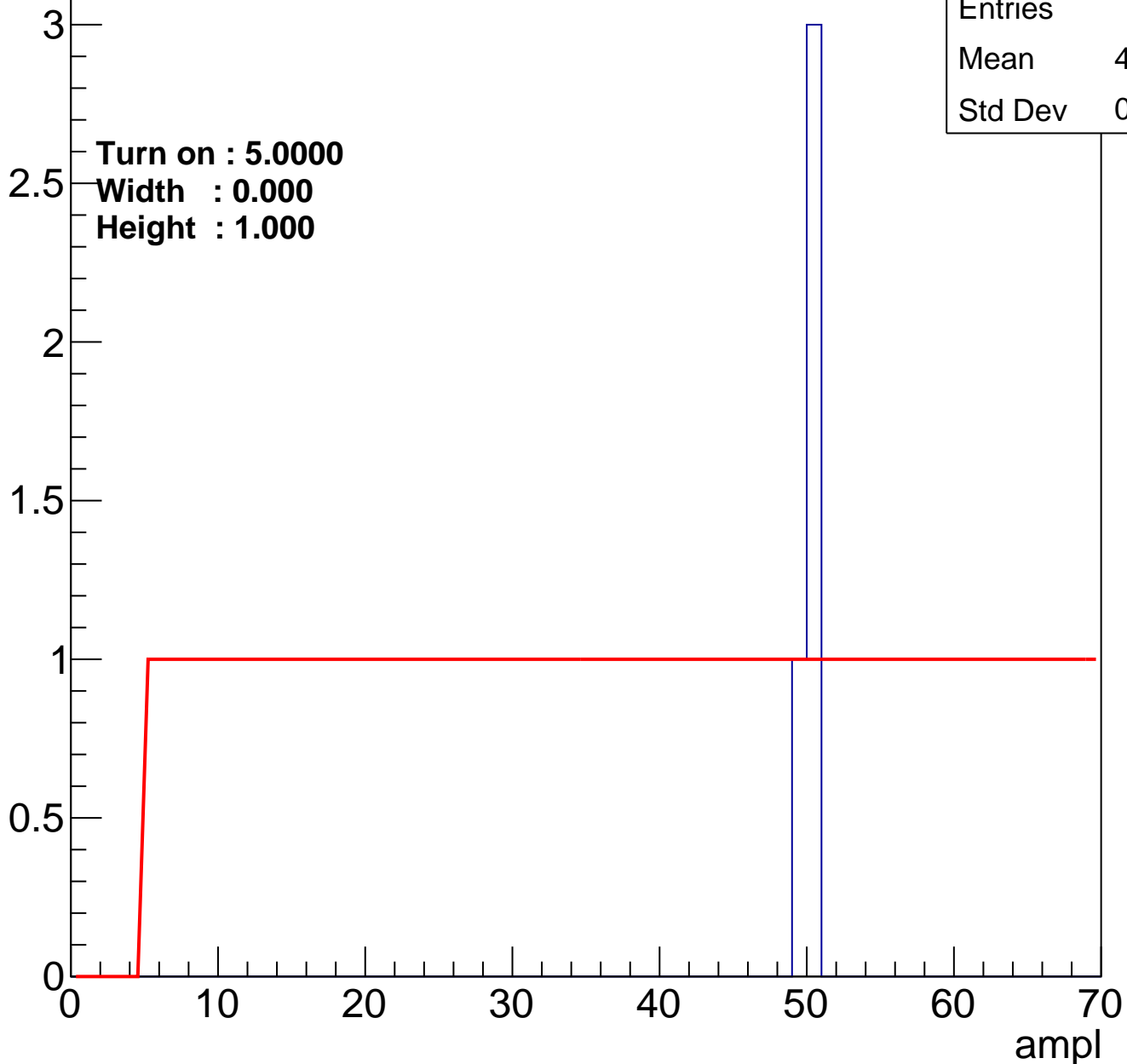
Height : 1.901



B0L100S, U13-ch19

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch20

calib_packv5_042523_0143.root, FC#6, port A1

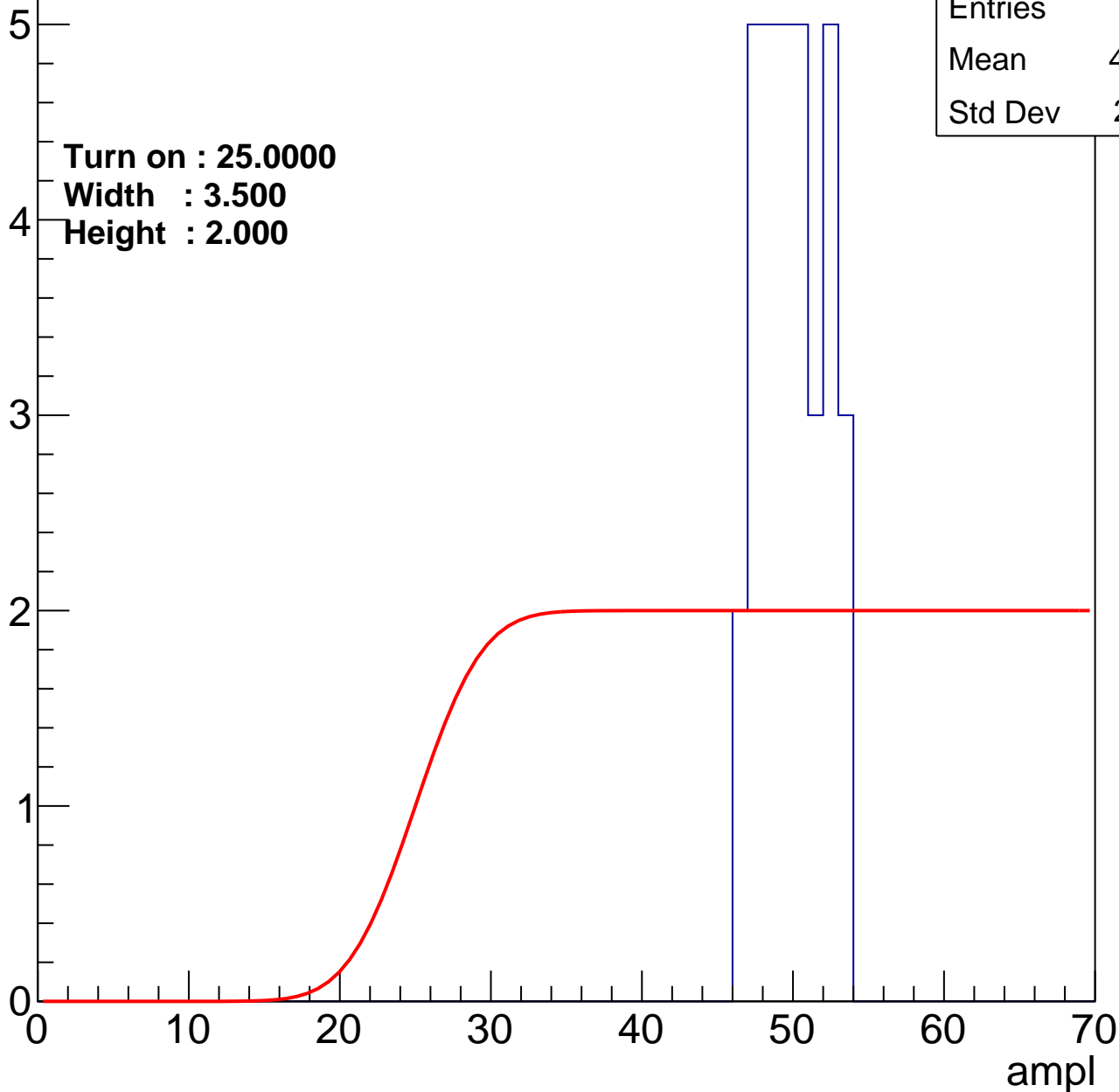
Entry

Entries	33
Mean	49.52
Std Dev	2.091

Turn on : 25.0000

Width : 3.500

Height : 2.000



B0L100S, U13-ch21

calib_packv5_042523_0143.root, FC#6, port A1

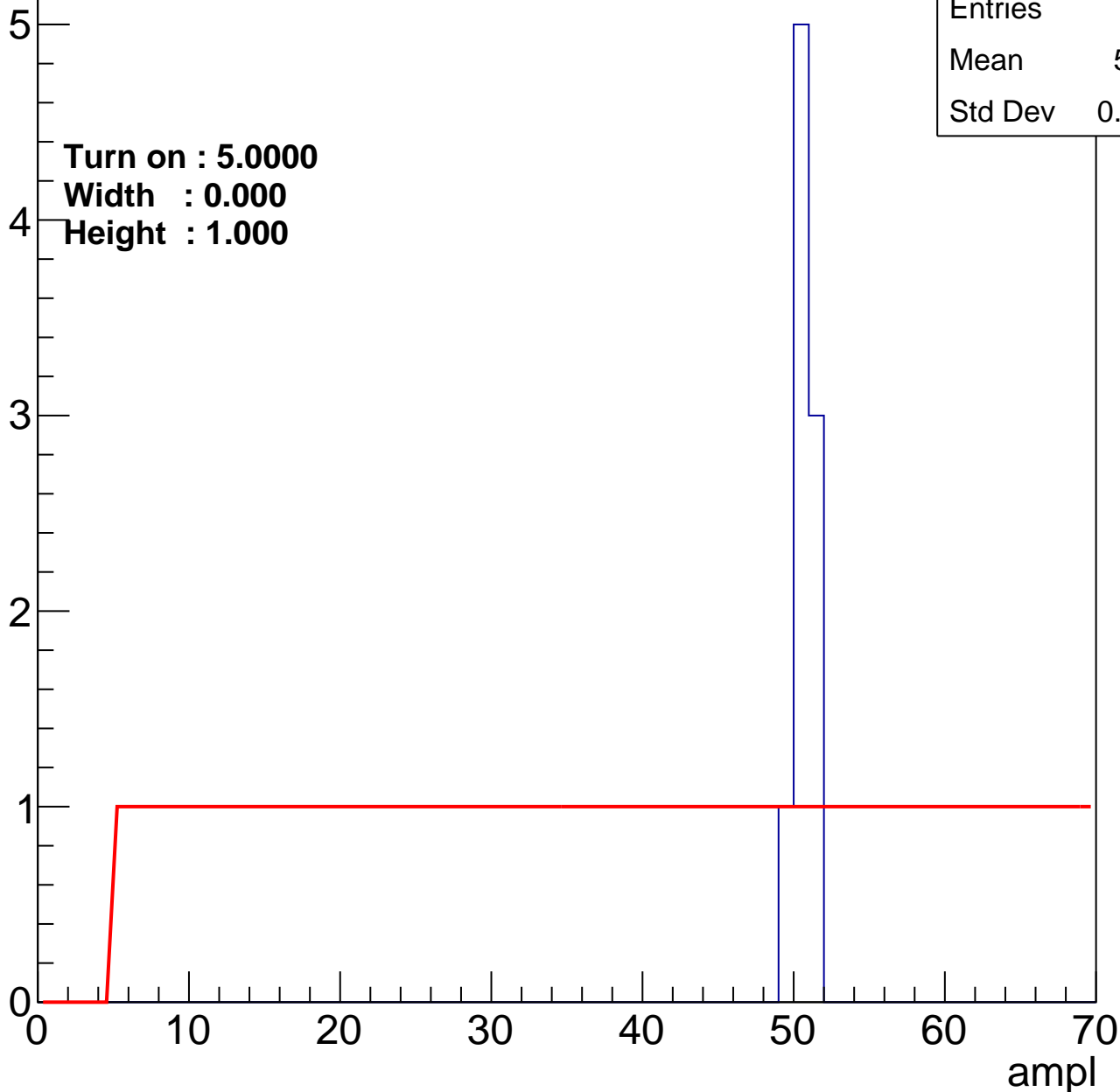
Entry

Entries	9
Mean	50.22
Std Dev	0.6285

Turn on : 5.0000

Width : 0.000

Height : 1.000



B0L100S, U13-ch22

calib_packv5_042523_0143.root, FC#6, port A1

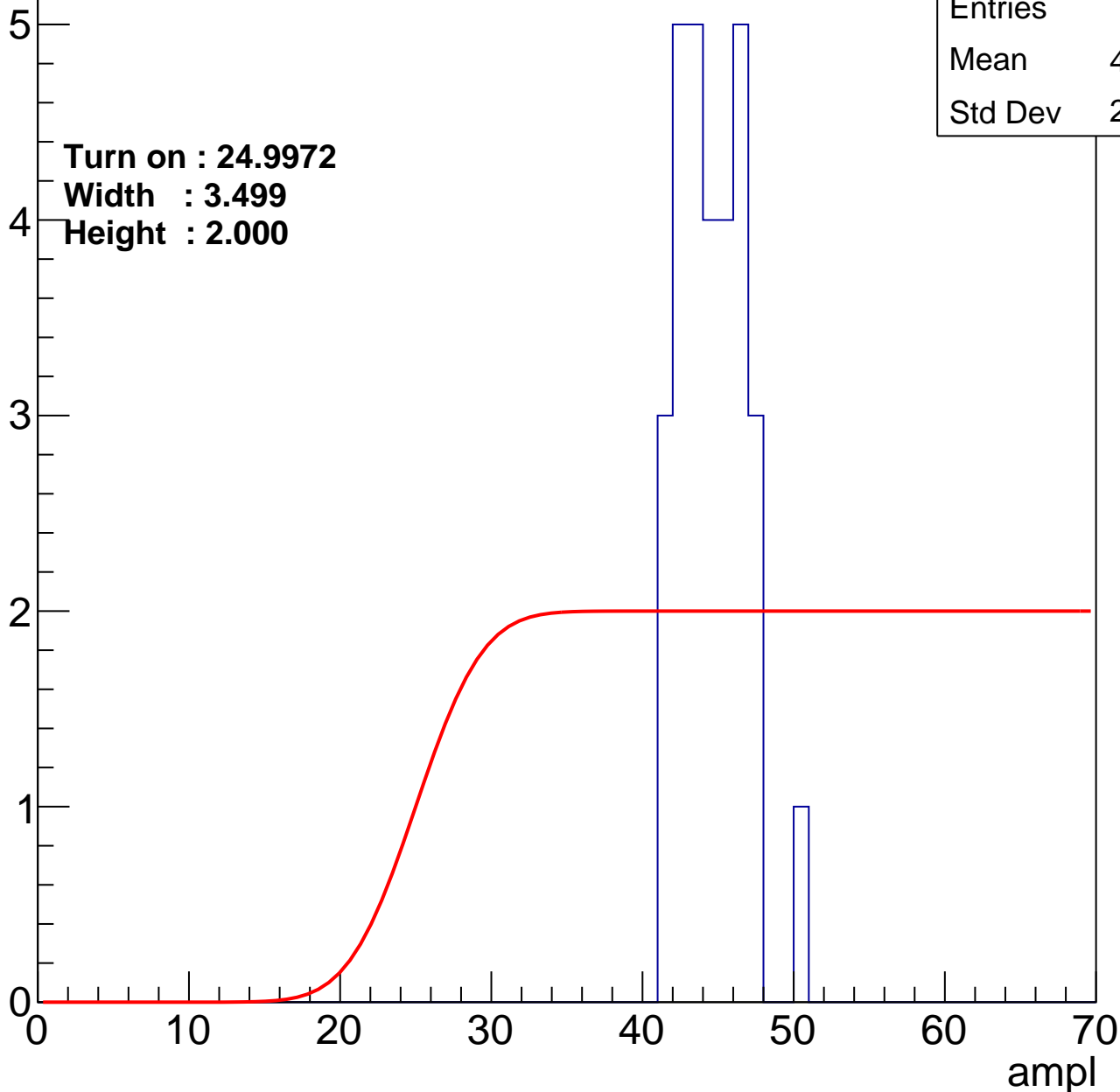
Entry

Entries	30
Mean	44.17
Std Dev	2.146

Turn on : 24.9972

Width : 3.499

Height : 2.000



B0L100S, U13-ch23

calib_packv5_042523_0143.root, FC#6, port A1

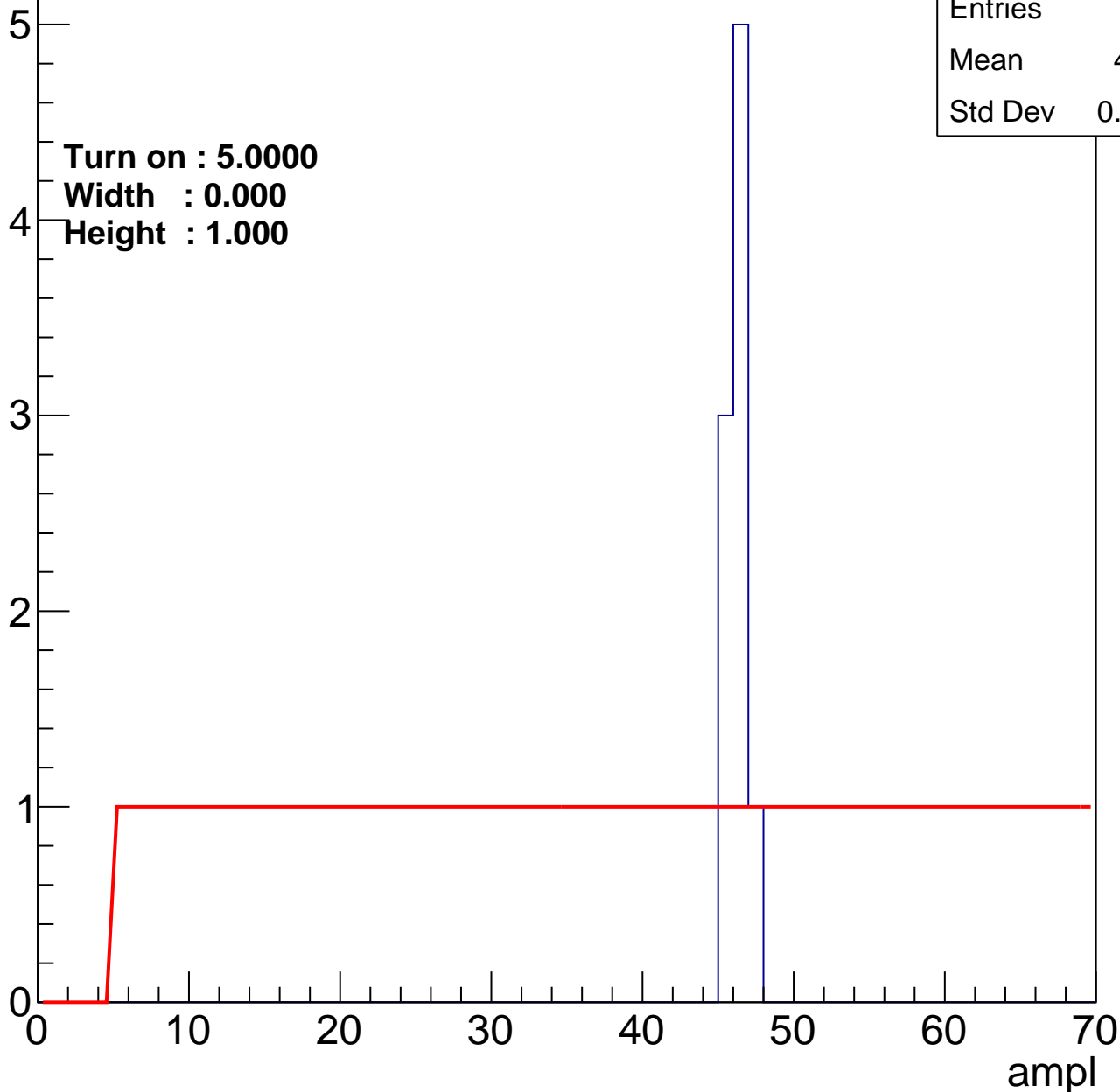
Entry

Entries	9
Mean	45.78
Std Dev	0.6285

Turn on : 5.0000

Width : 0.000

Height : 1.000



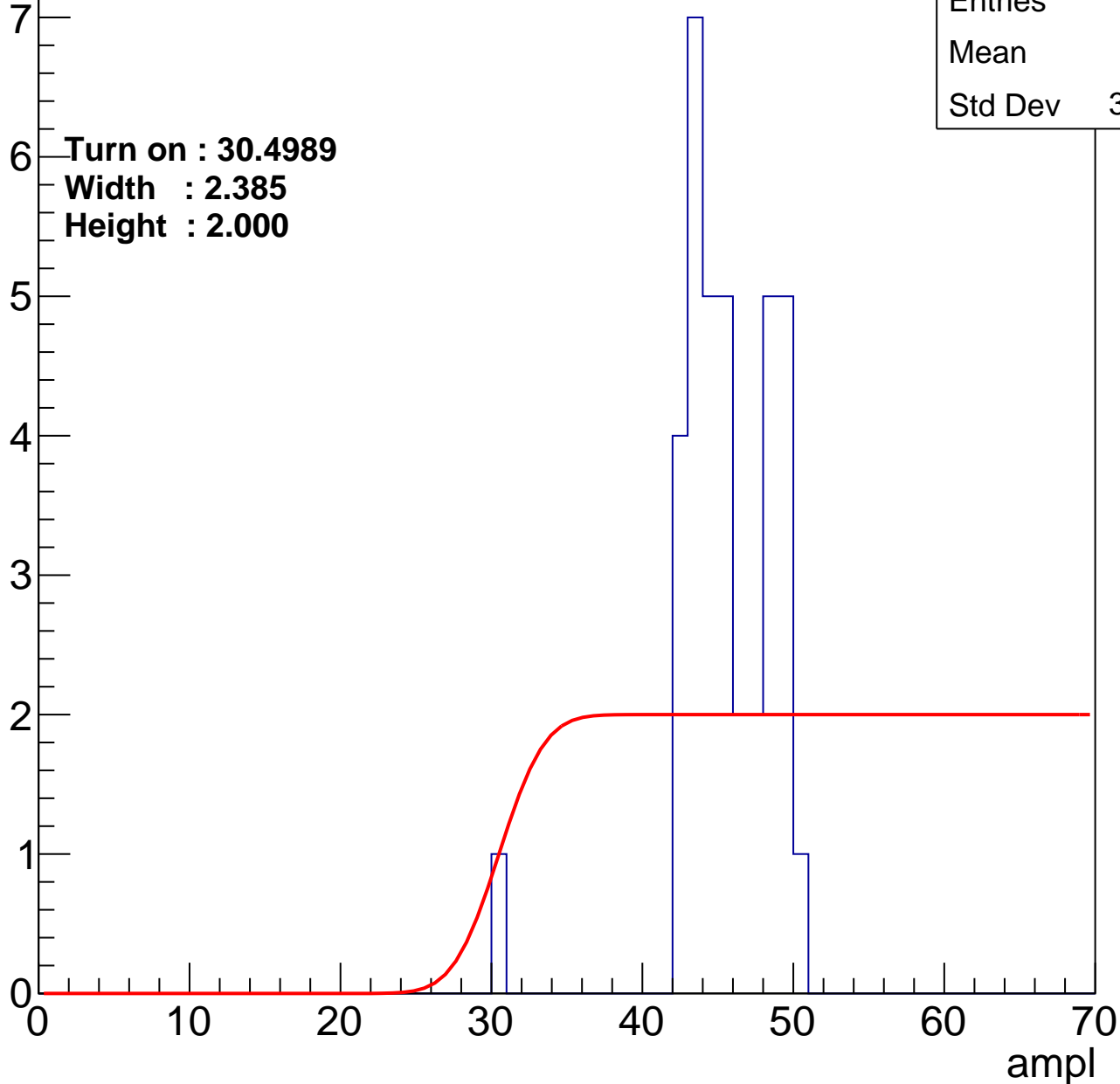
B0L100S, U13-ch24

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	37
Mean	45
Std Dev	3.503

Turn on : 30.4989
Width : 2.385
Height : 2.000



B0L100S, U13-ch25

calib_packv5_042523_0143.root, FC#6, port A1

Entry

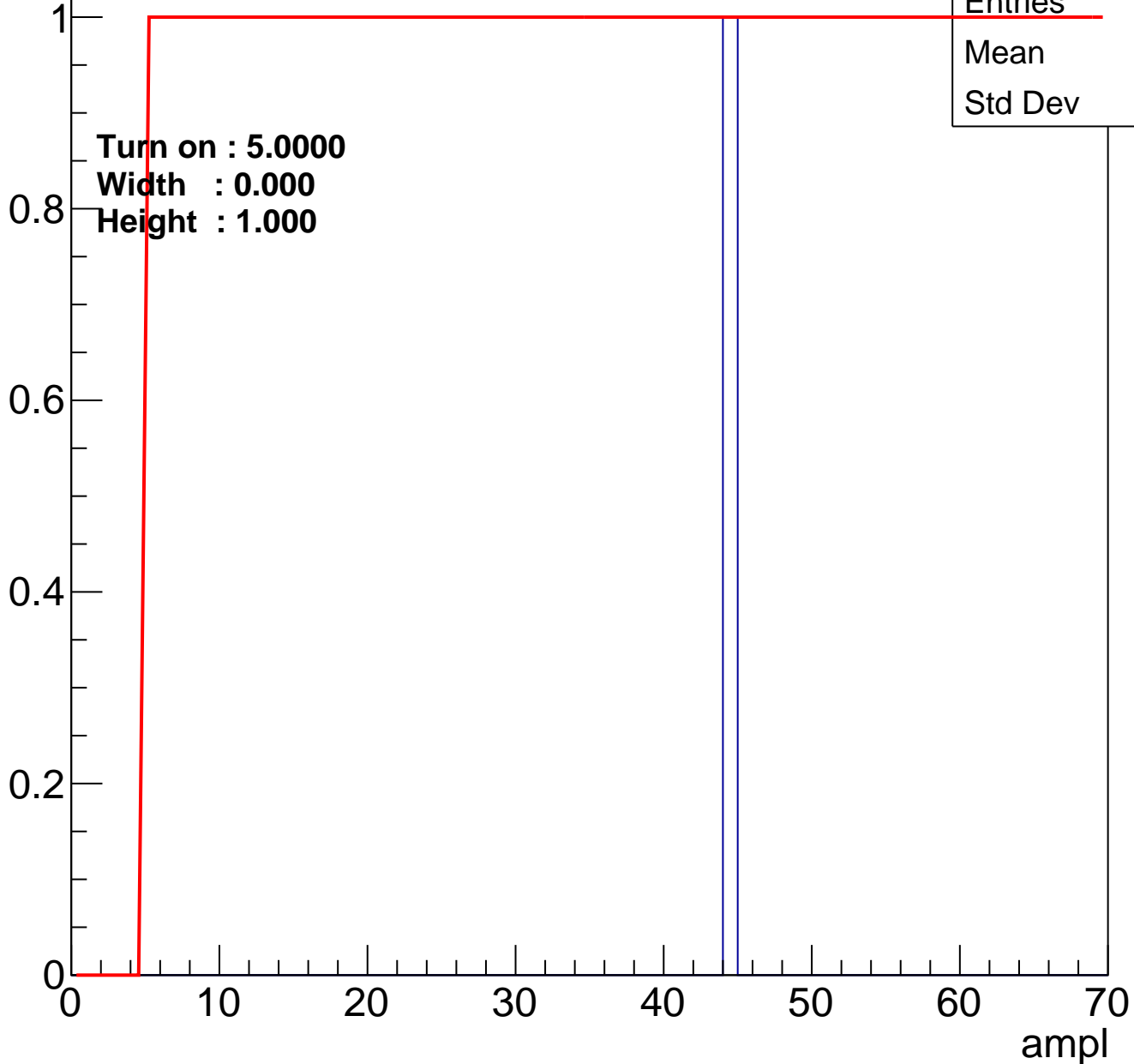


Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch26

calib_packv5_042523_0143.root, FC#6, port A1

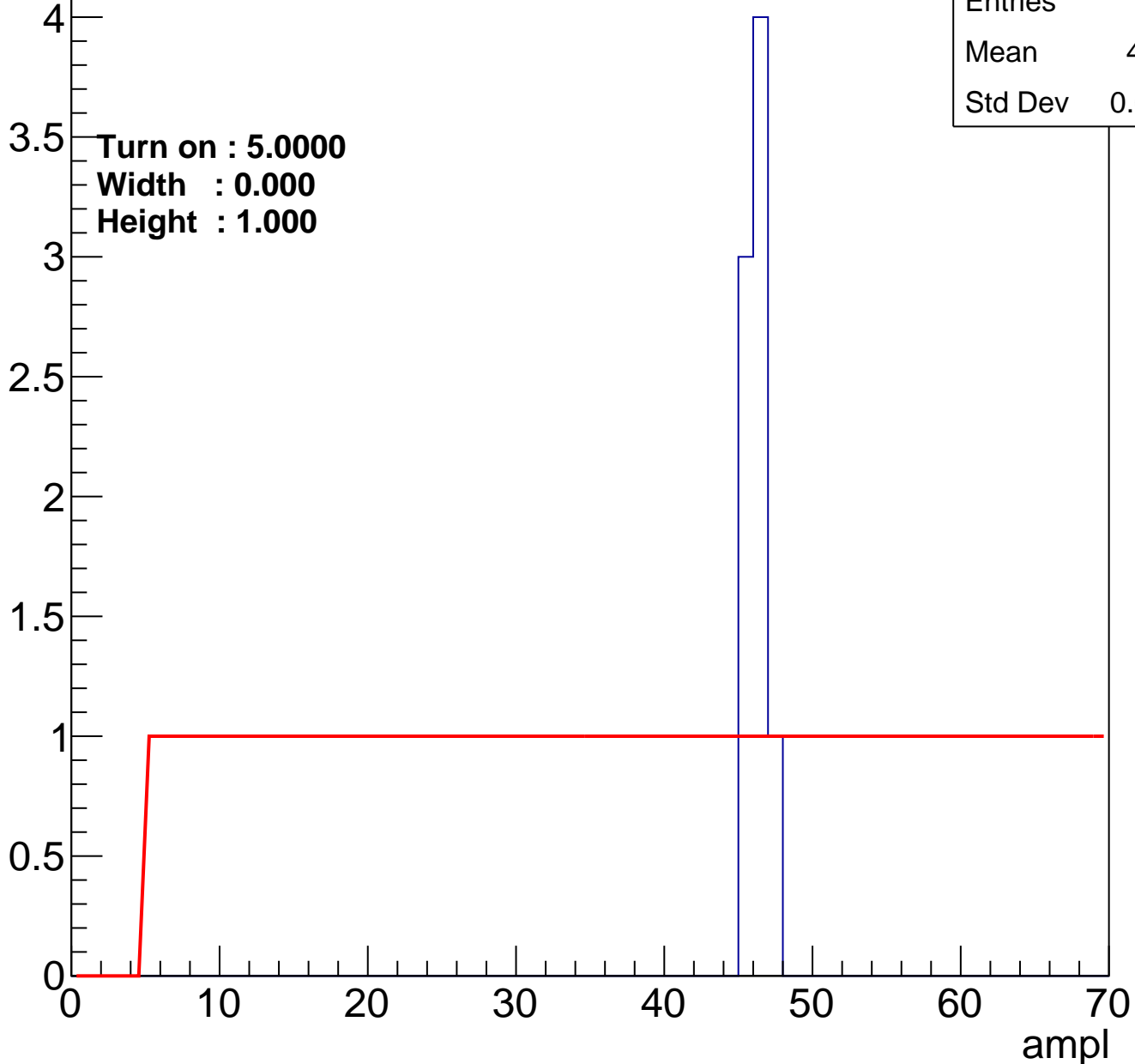
Entry



B0L100S, U13-ch27

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch28

calib_packv5_042523_0143.root, FC#6, port A1

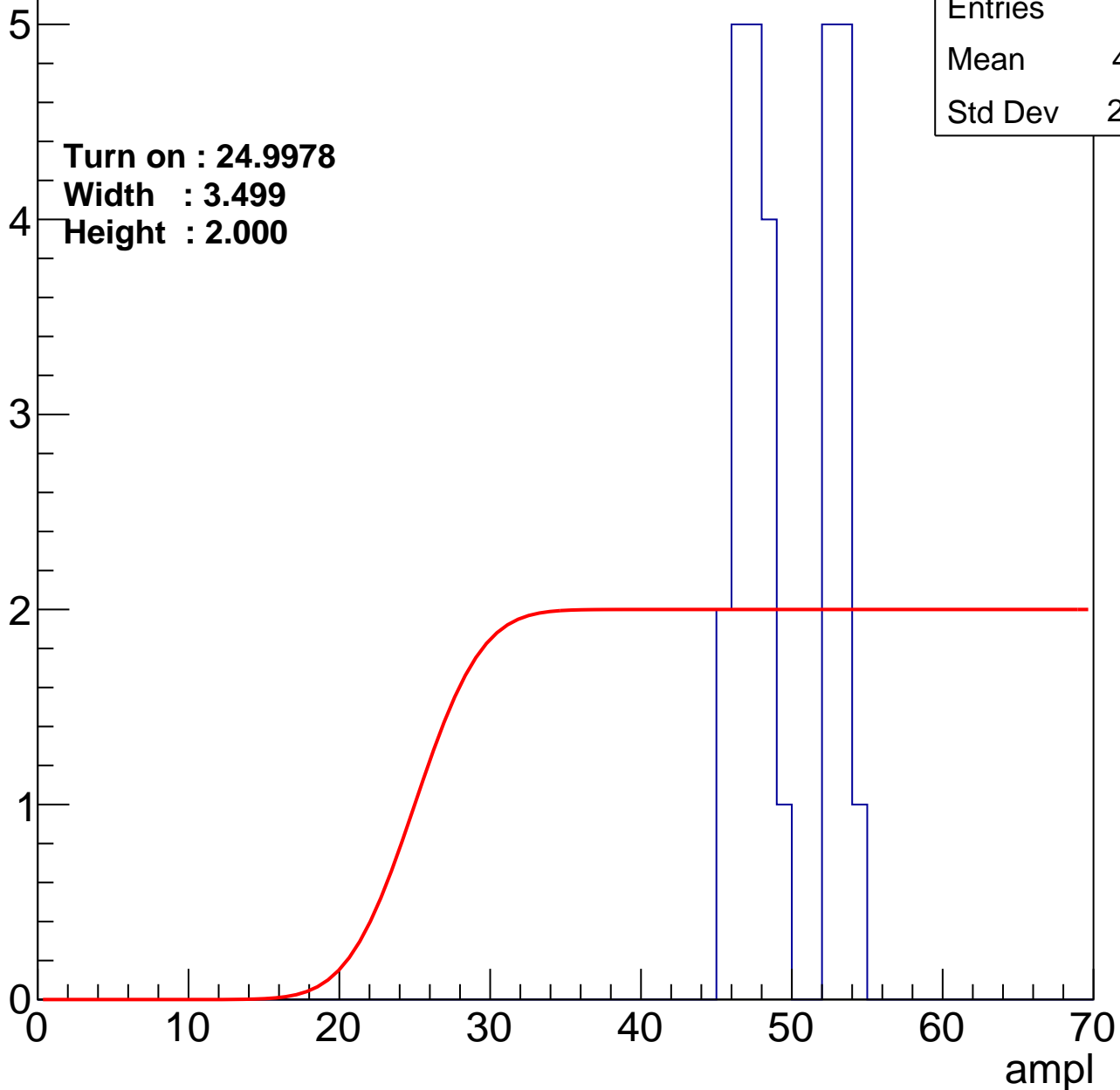
Entry

Entries	28
Mean	49.11
Std Dev	2.992

Turn on : 24.9978

Width : 3.499

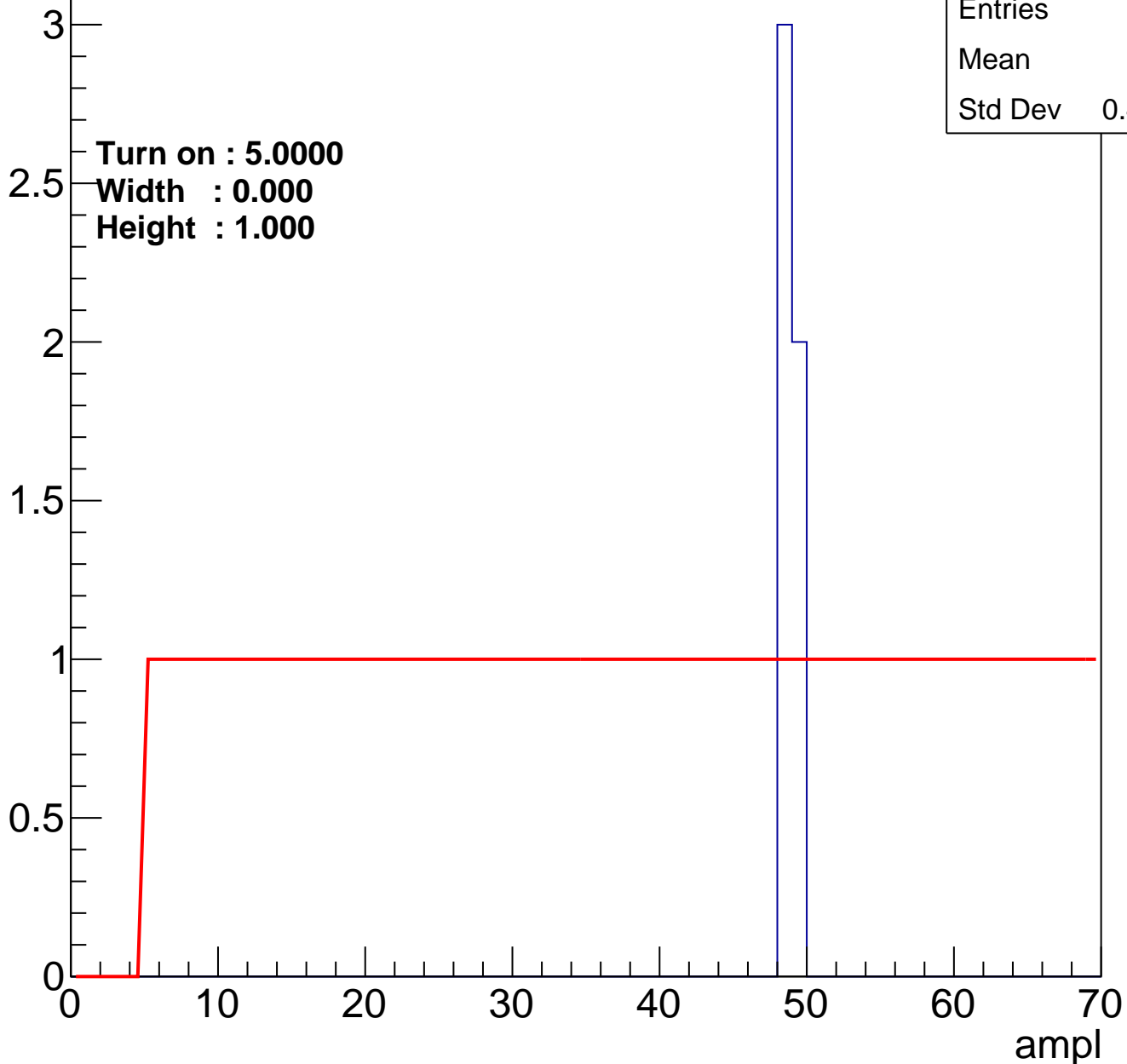
Height : 2.000



B0L100S, U13-ch29

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	5
Mean	48.4
Std Dev	0.4899

B0L100S, U13-ch30

calib_packv5_042523_0143.root, FC#6, port A1

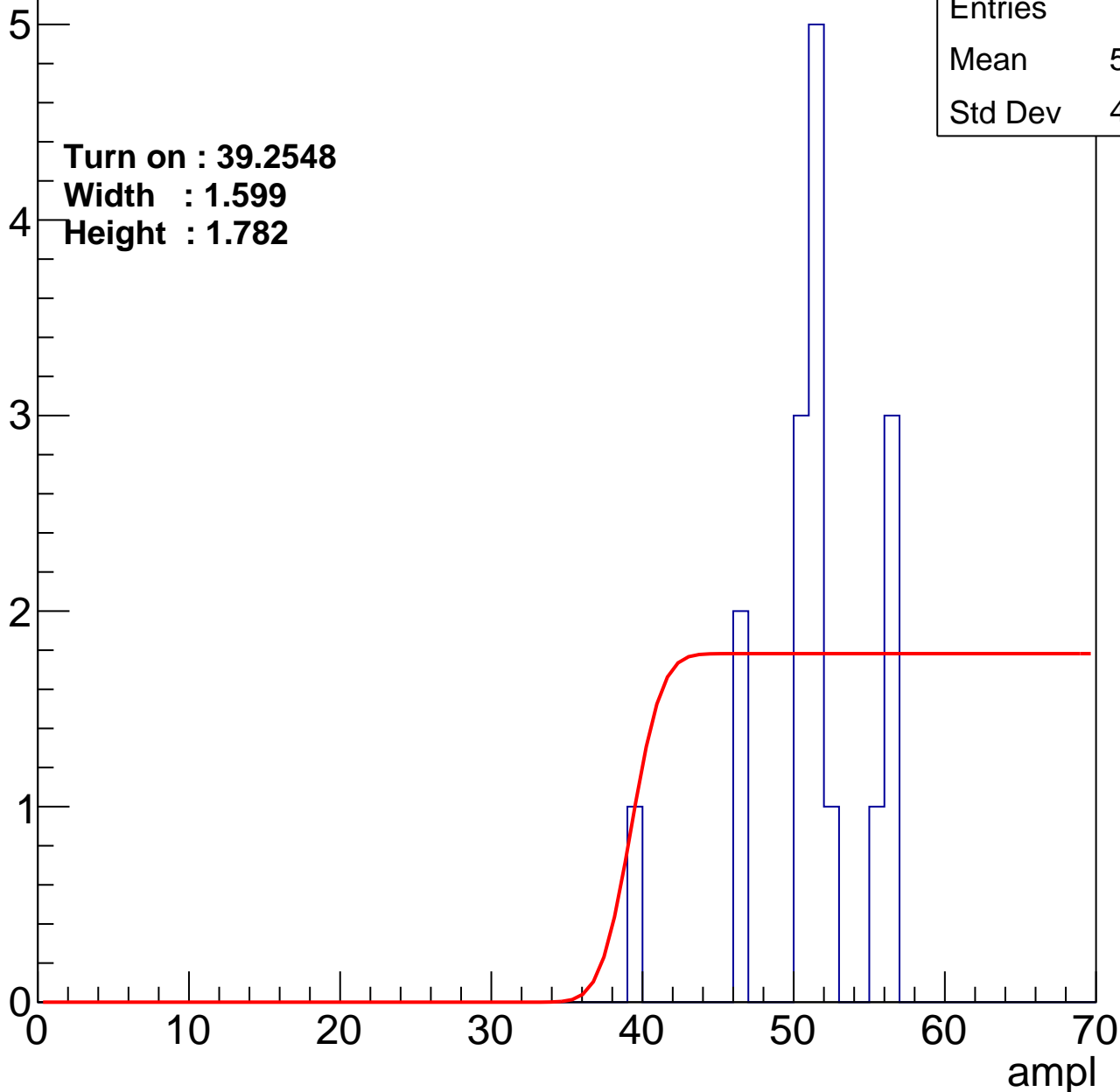
Entry

Entries	16
Mean	50.69
Std Dev	4.238

Turn on : 39.2548

Width : 1.599

Height : 1.782



B0L100S, U13-ch31

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch32

calib_packv5_042523_0143.root, FC#6, port A1

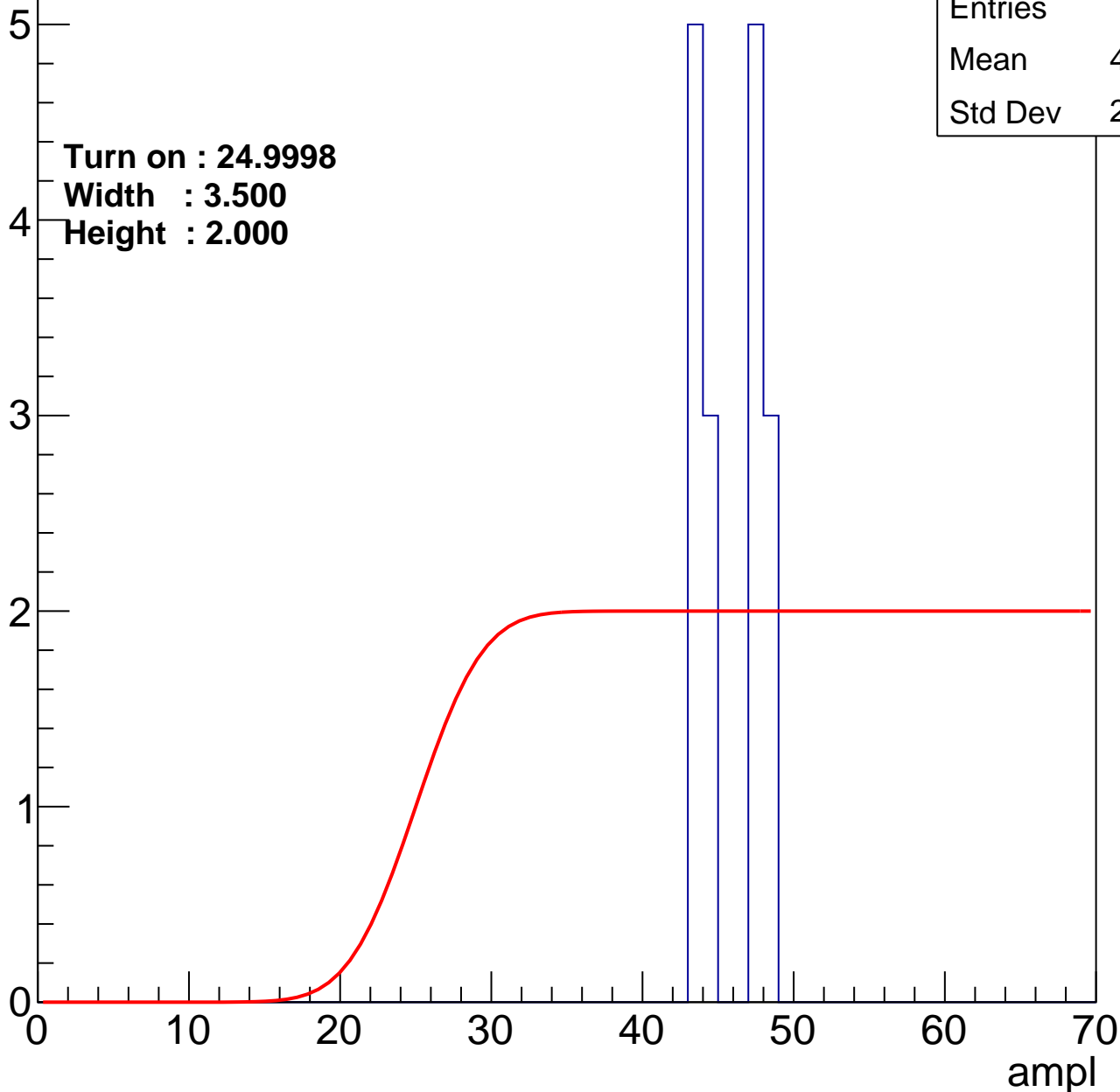
Entry

Entries	16
Mean	45.38
Std Dev	2.058

Turn on : 24.9998

Width : 3.500

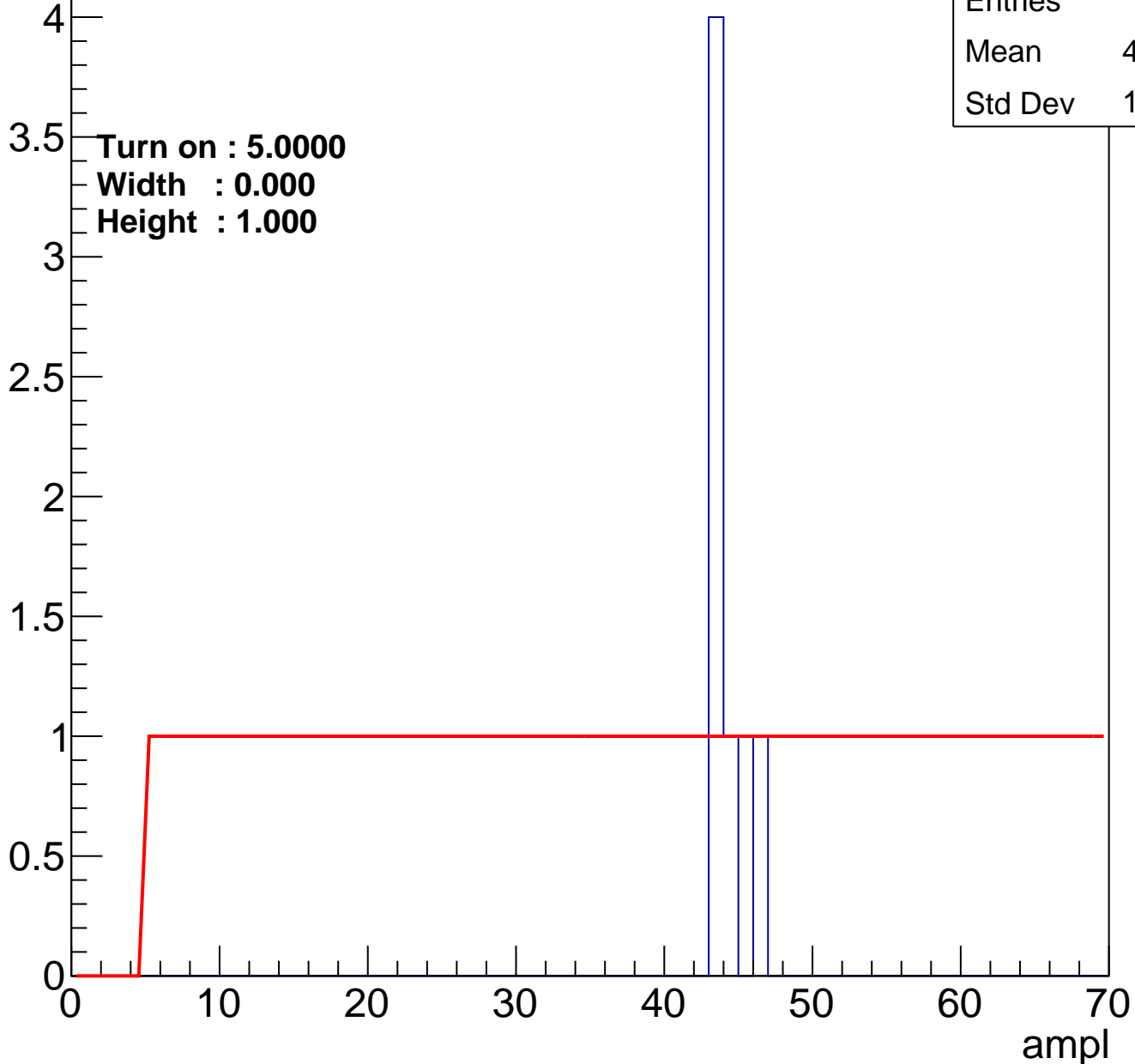
Height : 2.000



B0L100S, U13-ch33

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch34

calib_packv5_042523_0143.root, FC#6, port A1

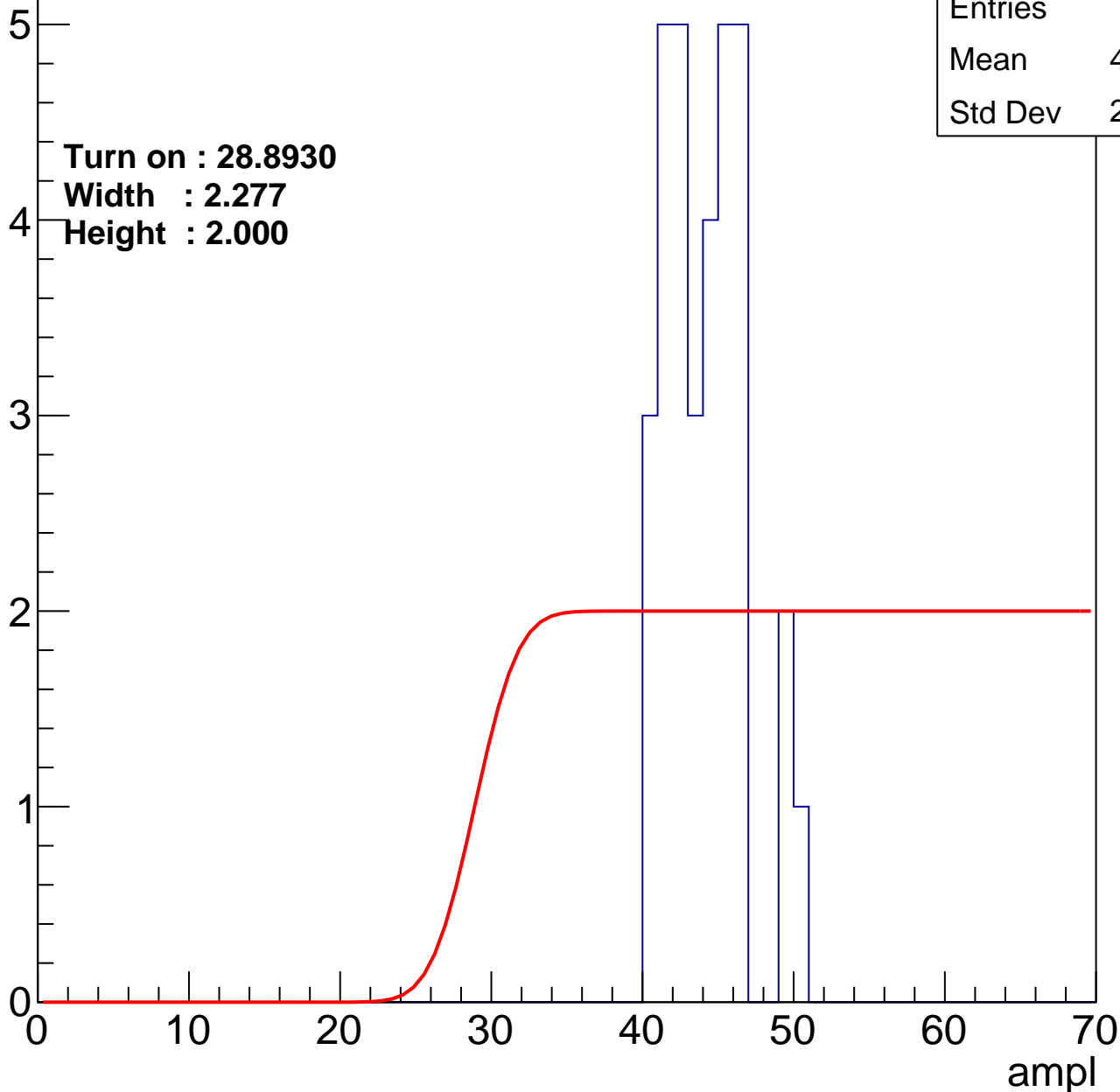
Entry

Entries	33
Mean	43.73
Std Dev	2.609

Turn on : 28.8930

Width : 2.277

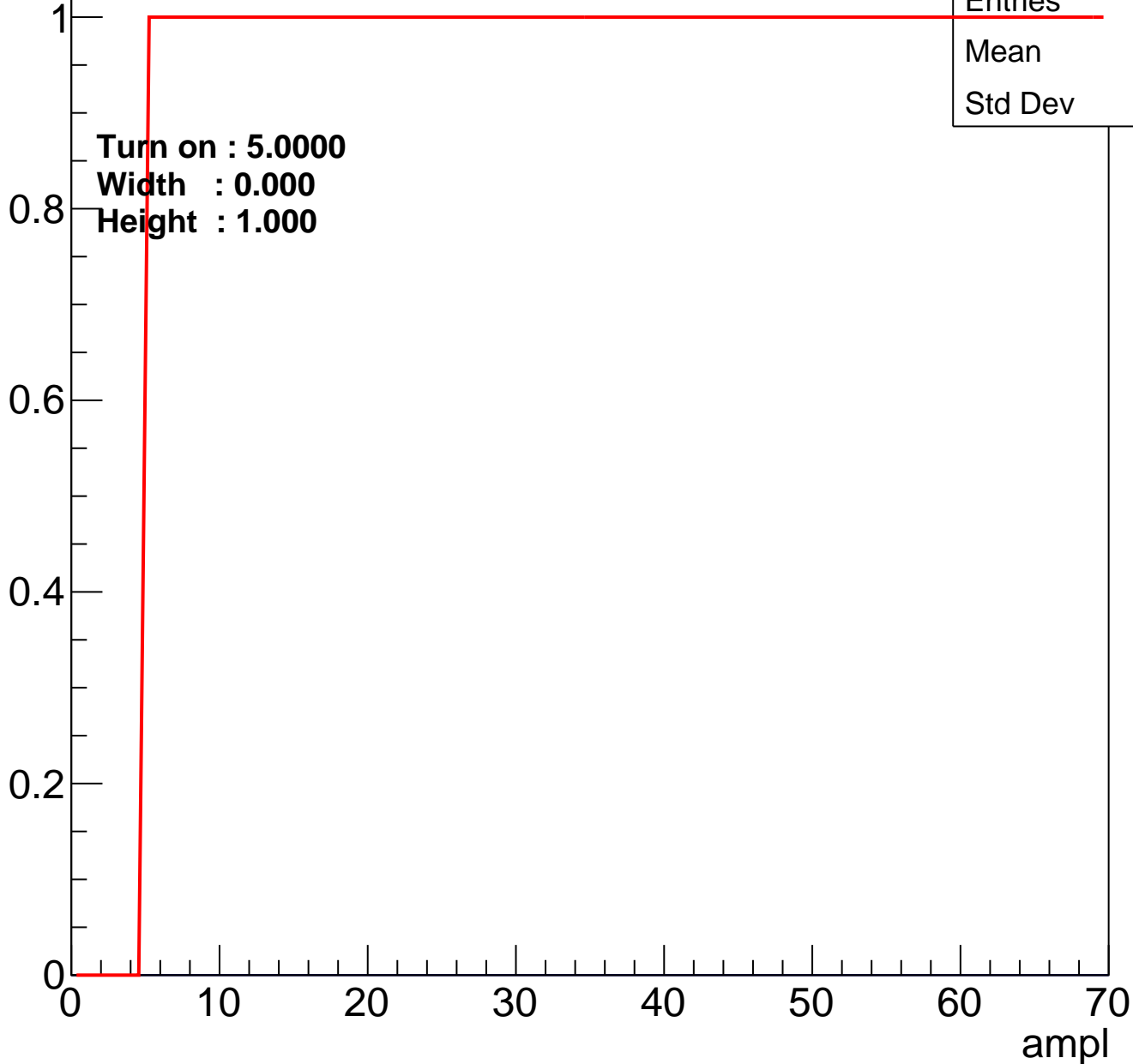
Height : 2.000



B0L100S, U13-ch35

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch36

calib_packv5_042523_0143.root, FC#6, port A1

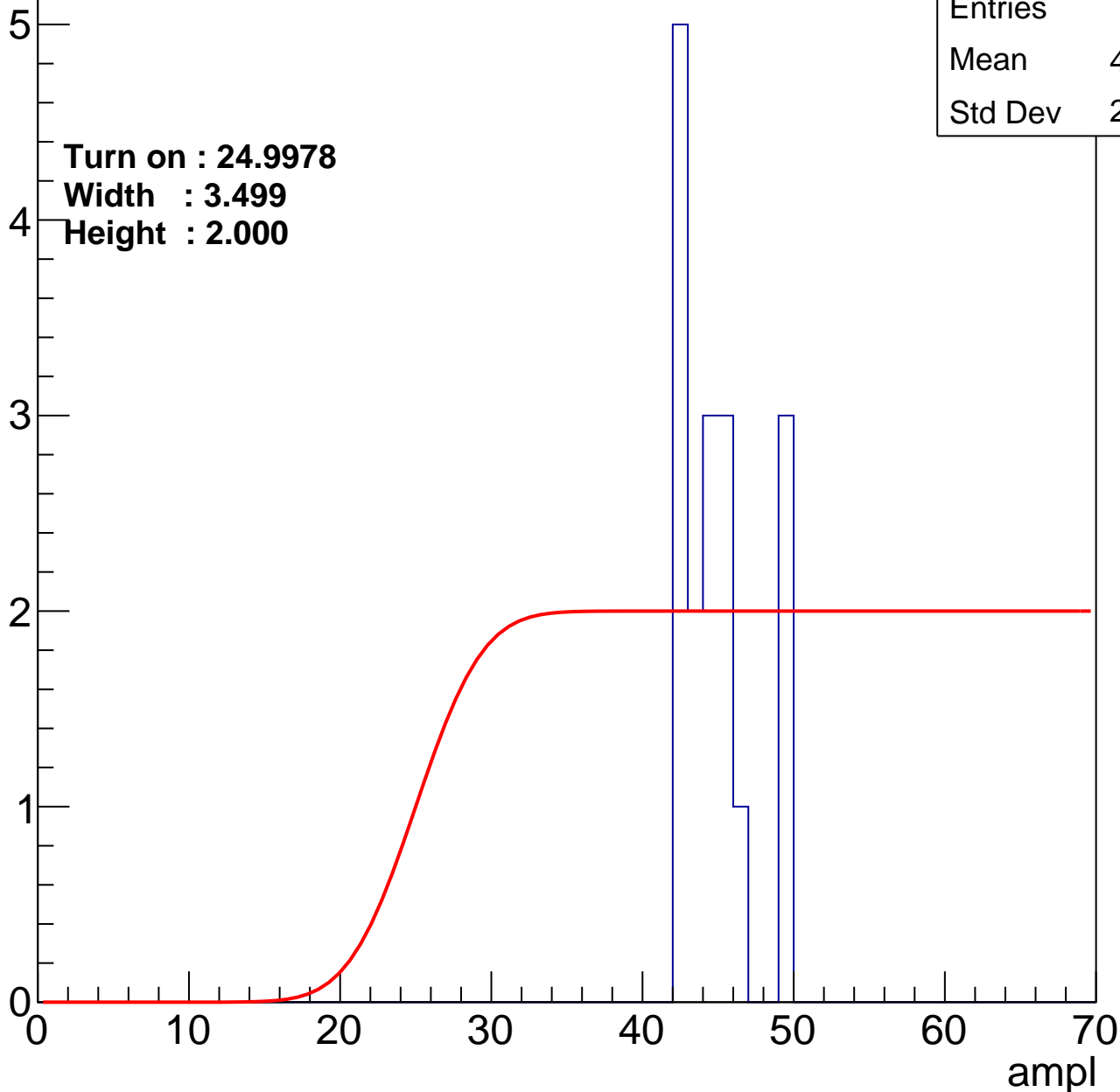
Entry

Entries	17
Mean	44.47
Std Dev	2.428

Turn on : 24.9978

Width : 3.499

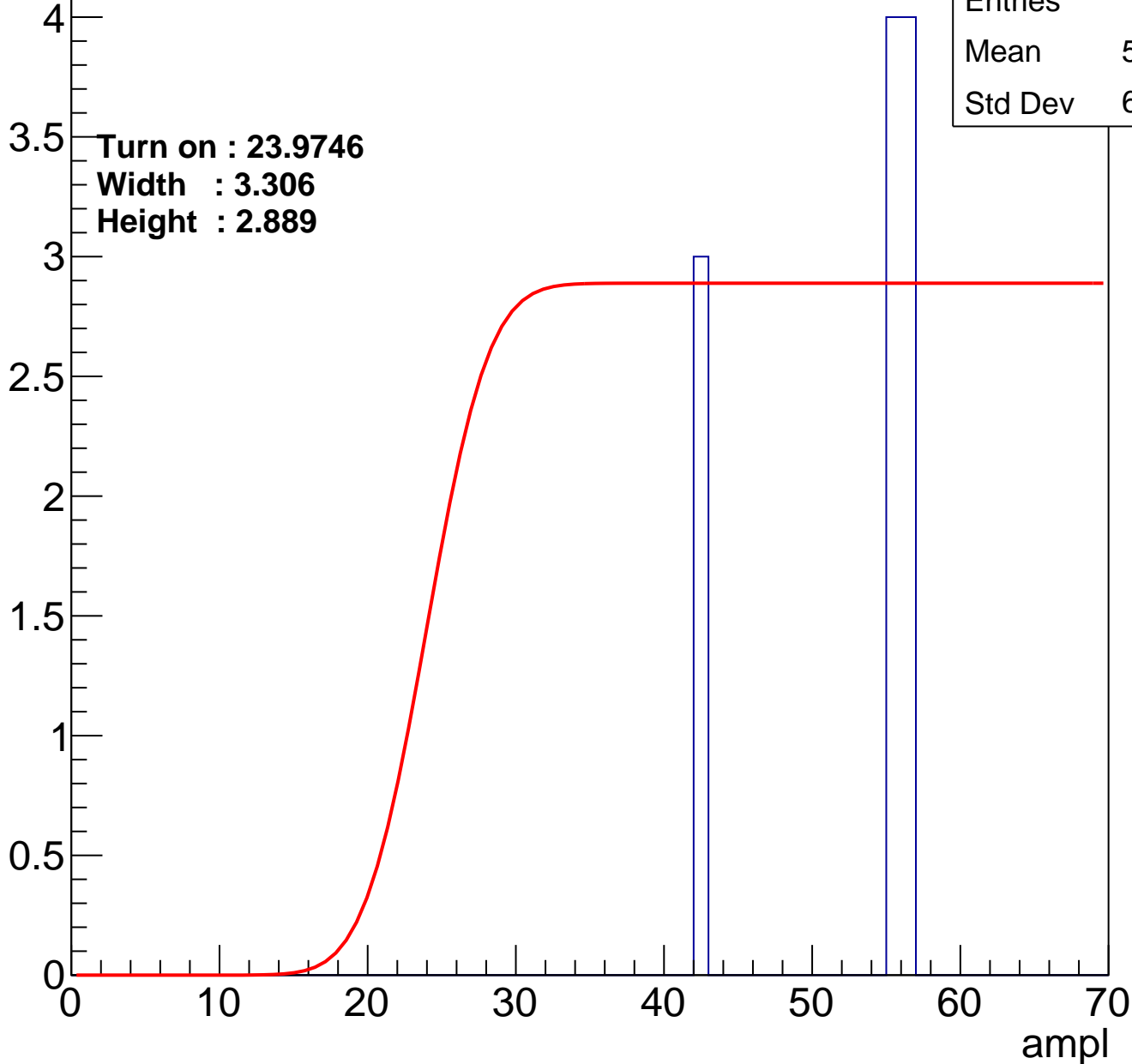
Height : 2.000



B0L100S, U13-ch37

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	11
Mean	51.82
Std Dev	6.027

B0L100S, U13-ch38

calib_packv5_042523_0143.root, FC#6, port A1

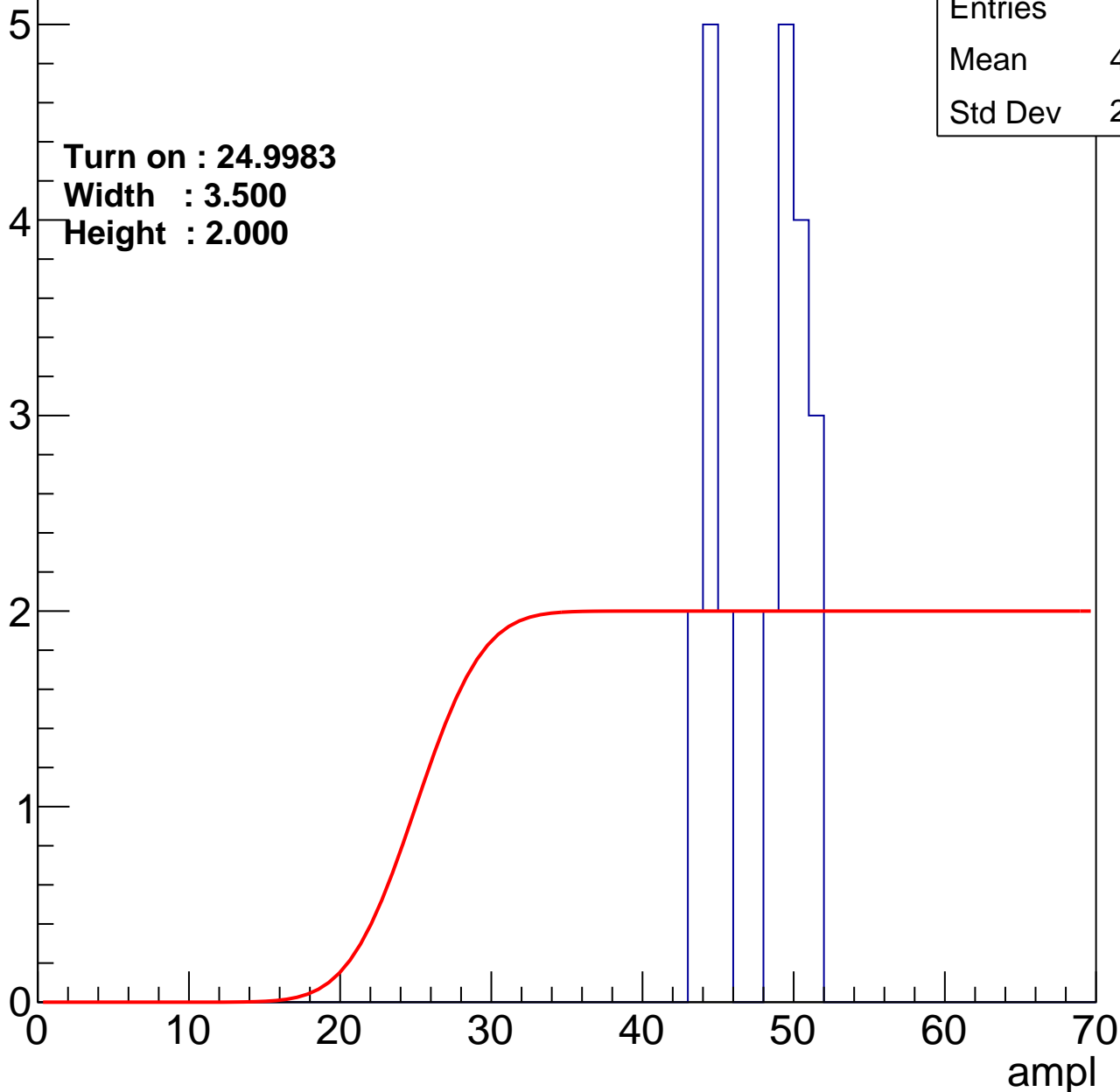
Entry

Entries	23
Mean	47.39
Std Dev	2.855

Turn on : 24.9983

Width : 3.500

Height : 2.000



B0L100S, U13-ch39

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch40

calib_packv5_042523_0143.root, FC#6, port A1

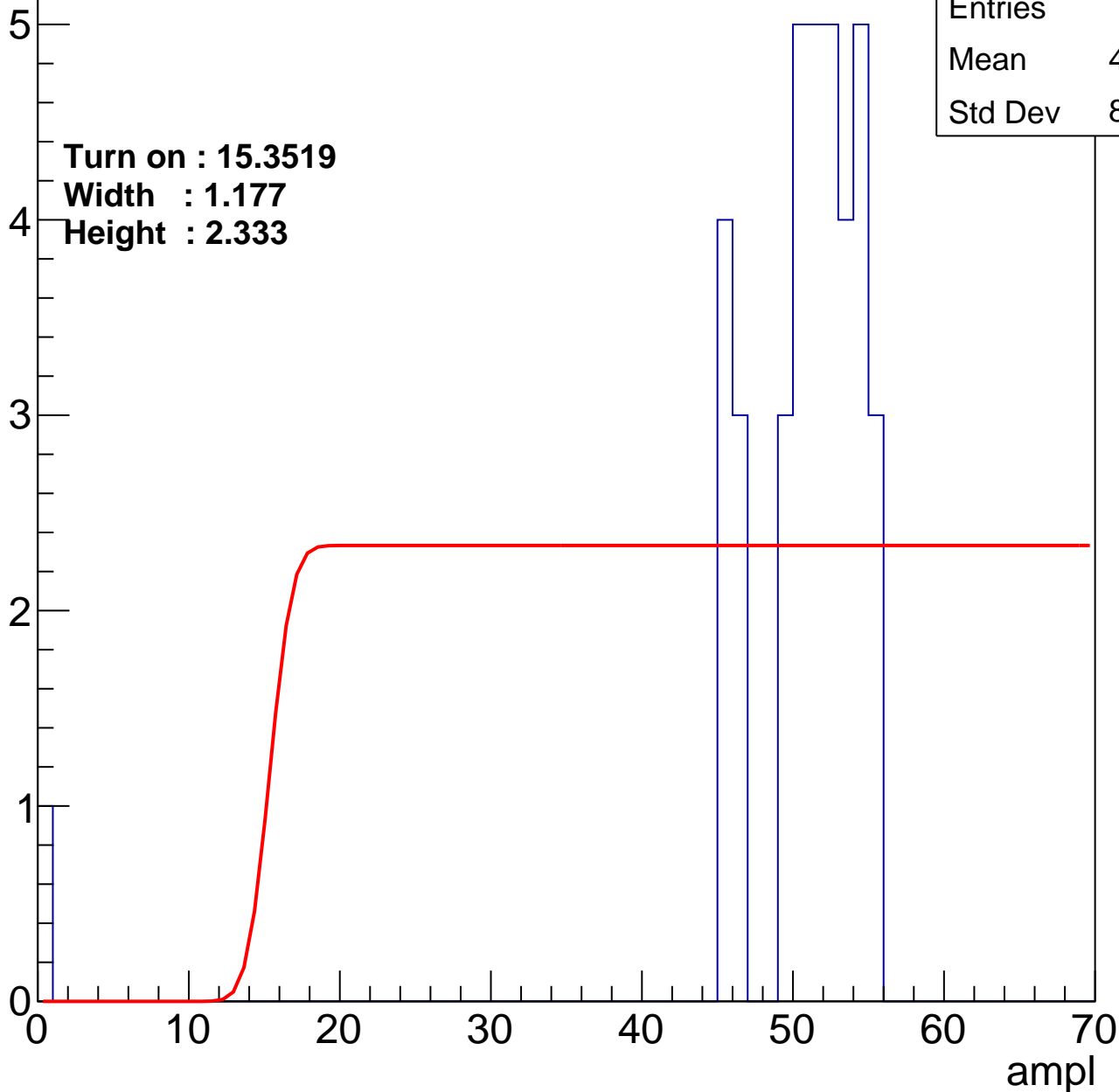
Entry

Entries	38
Mean	49.39
Std Dev	8.665

Turn on : 15.3519

Width : 1.177

Height : 2.333



B0L100S, U13-ch41

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch42

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch43

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch44

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch45

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch46

calib_packv5_042523_0143.root, FC#6, port A1

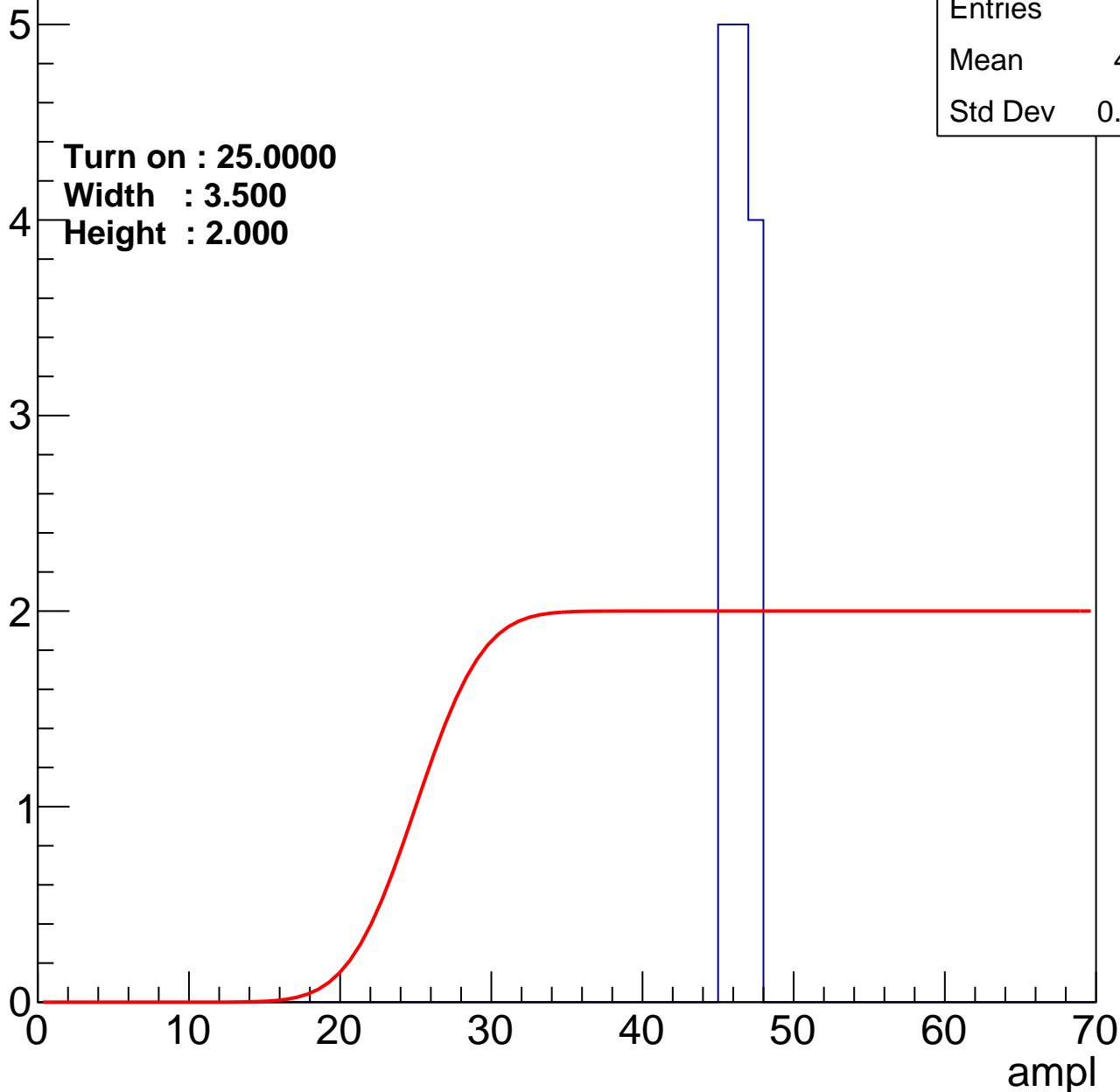
Entry

Entries	14
Mean	45.93
Std Dev	0.7986

Turn on : 25.0000

Width : 3.500

Height : 2.000



B0L100S, U13-ch47

calib_packv5_042523_0143.root, FC#6, port A1

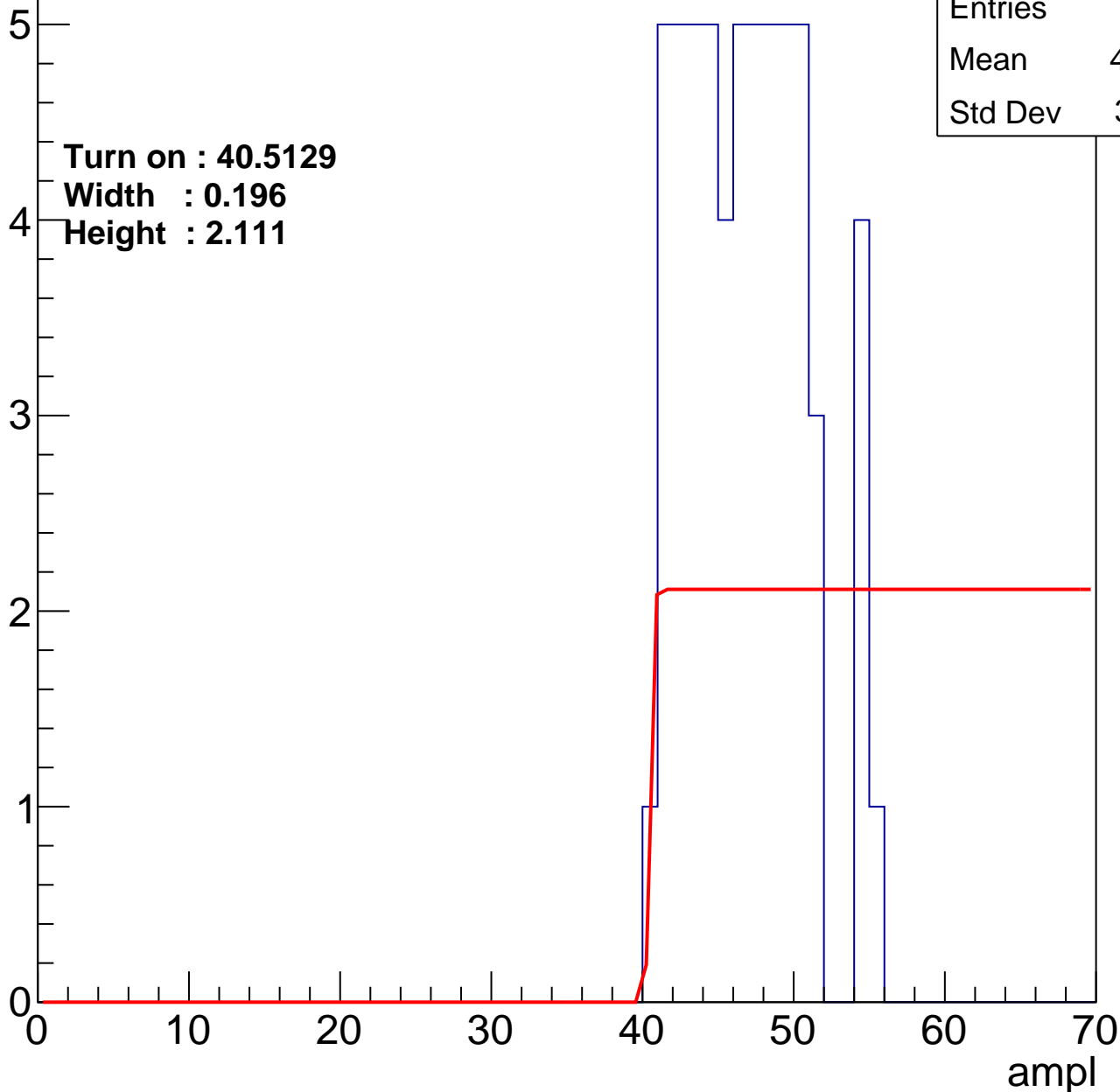
Entry

Entries	58
Mean	46.45
Std Dev	3.851

Turn on : 40.5129

Width : 0.196

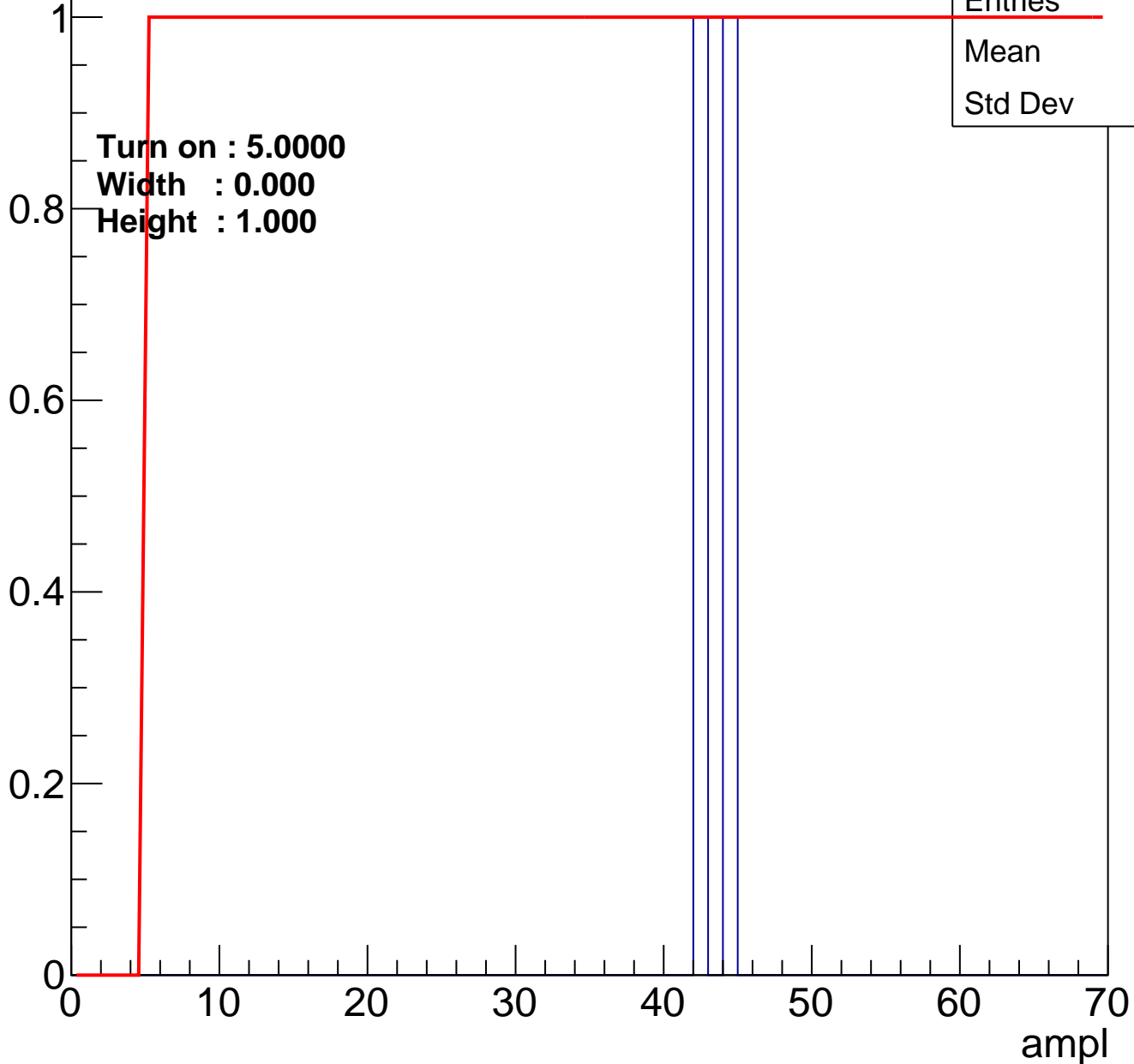
Height : 2.111



B0L100S, U13-ch48

calib_packv5_042523_0143.root, FC#6, port A1

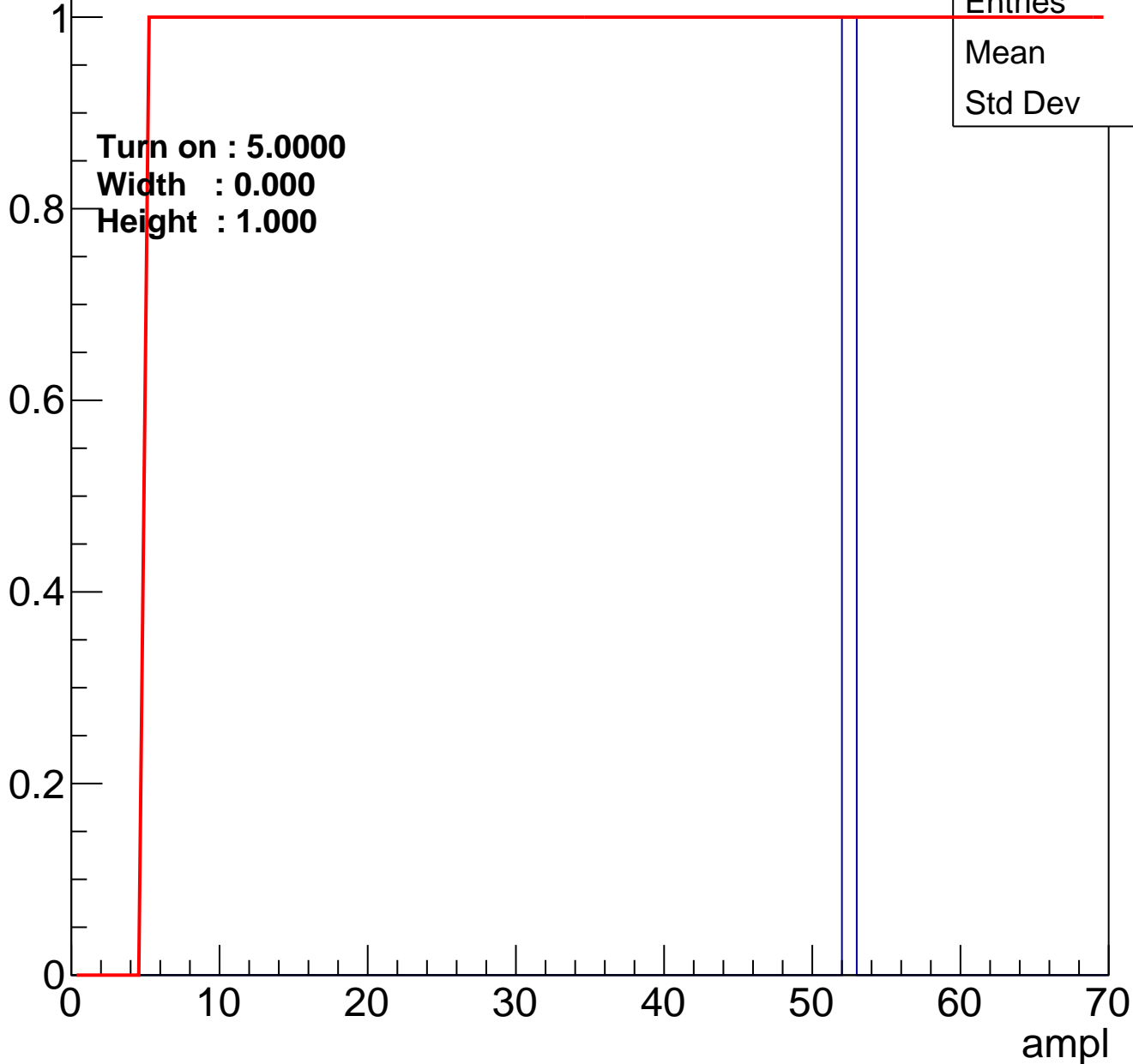
Entry



B0L100S, U13-ch49

calib_packv5_042523_0143.root, FC#6, port A1

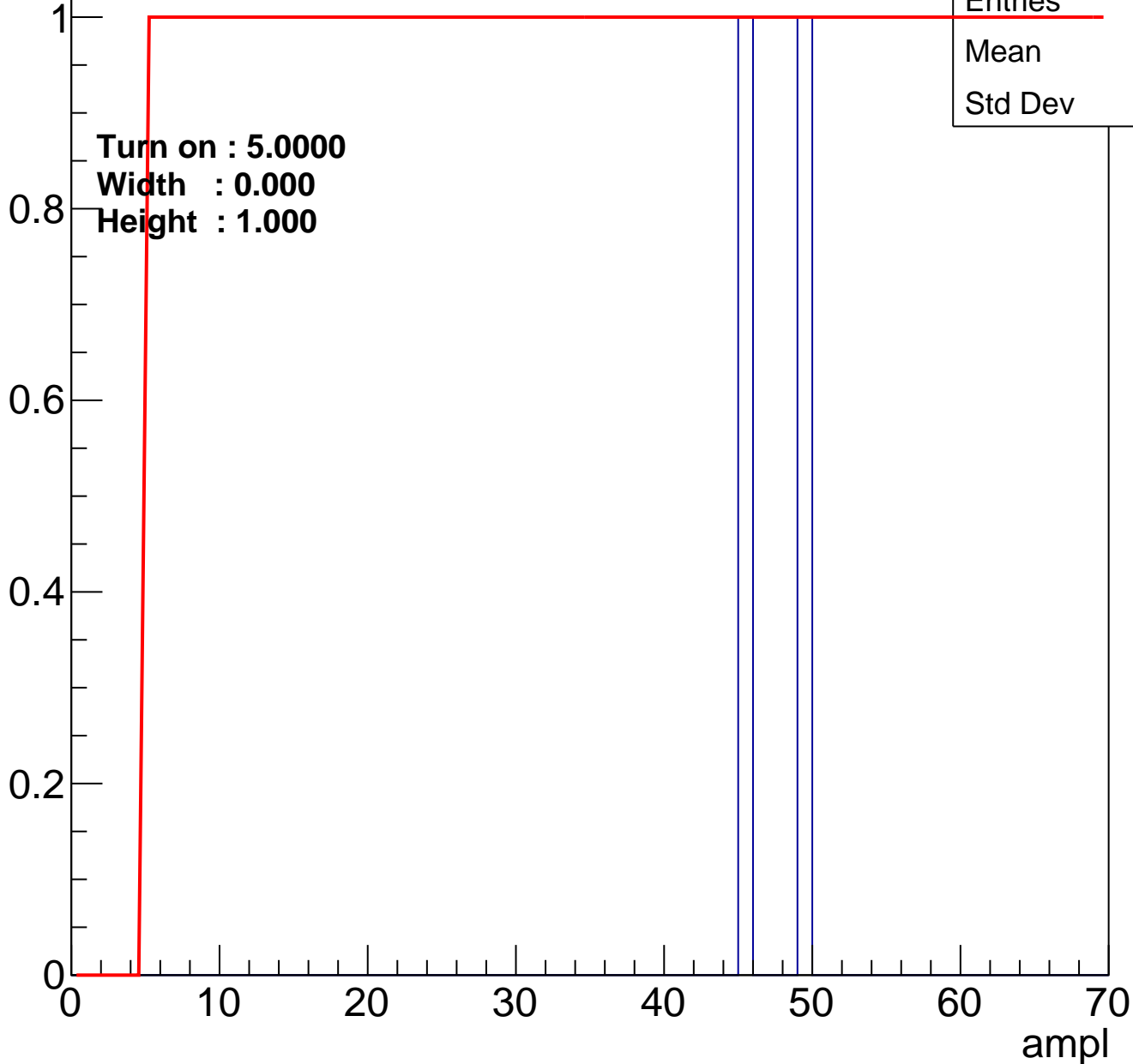
Entry



B0L100S, U13-ch50

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch51

calib_packv5_042523_0143.root, FC#6, port A1

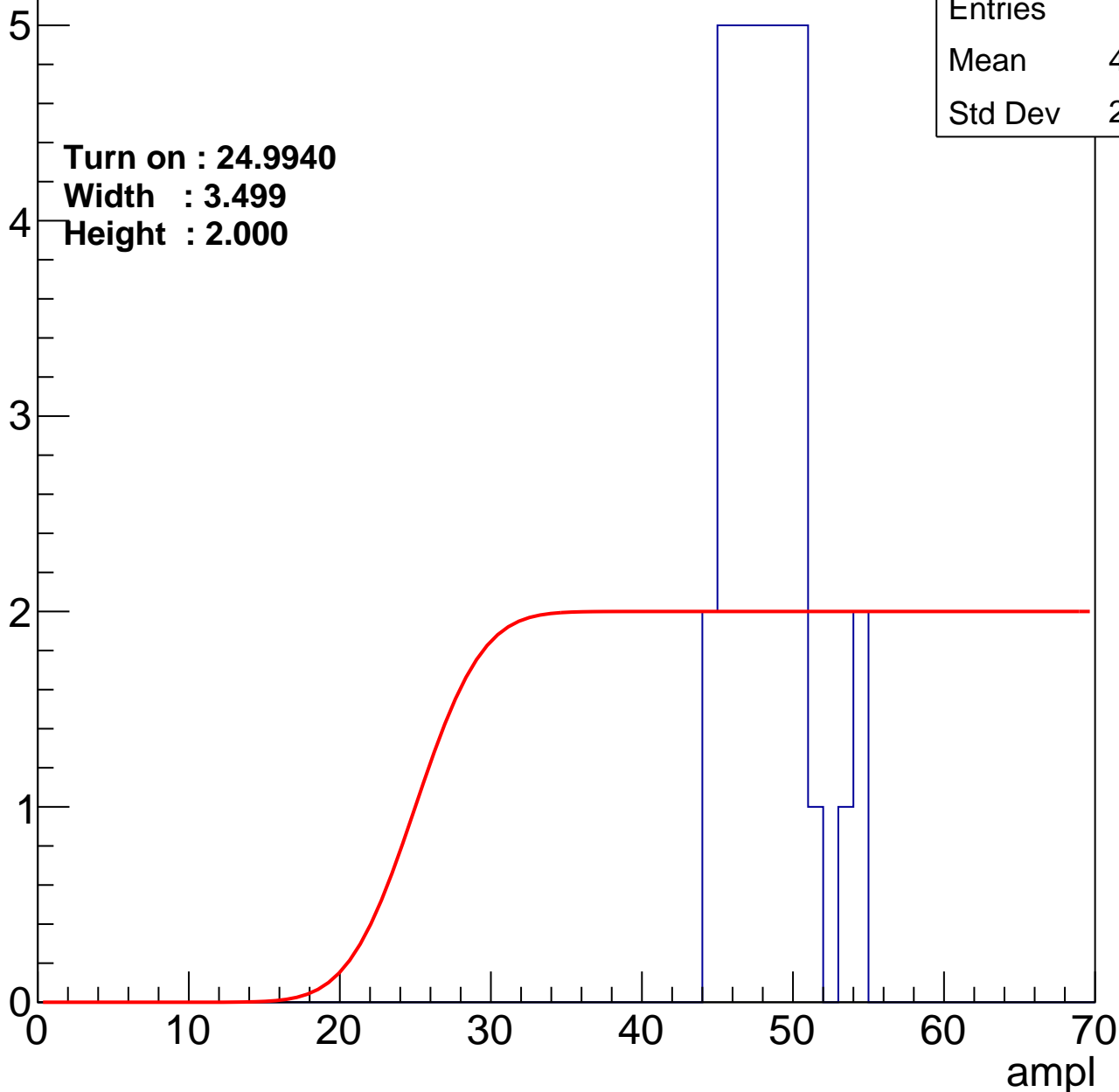
Entry

Entries	36
Mean	47.92
Std Dev	2.543

Turn on : 24.9940

Width : 3.499

Height : 2.000



B0L100S, U13-ch52

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch53

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch54

calib_packv5_042523_0143.root, FC#6, port A1

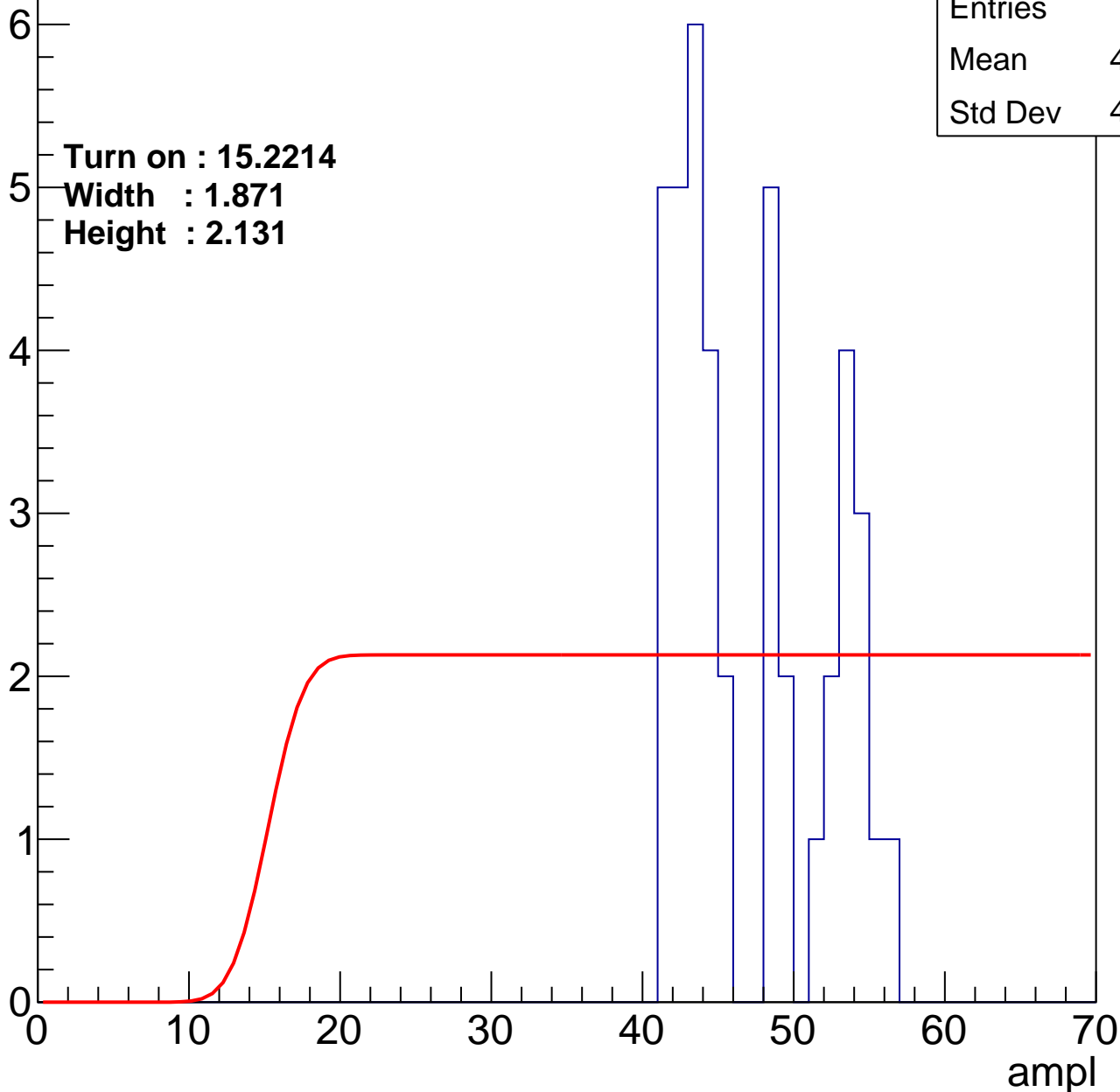
Entry

Entries	41
Mean	46.76
Std Dev	4.833

Turn on : 15.2214

Width : 1.871

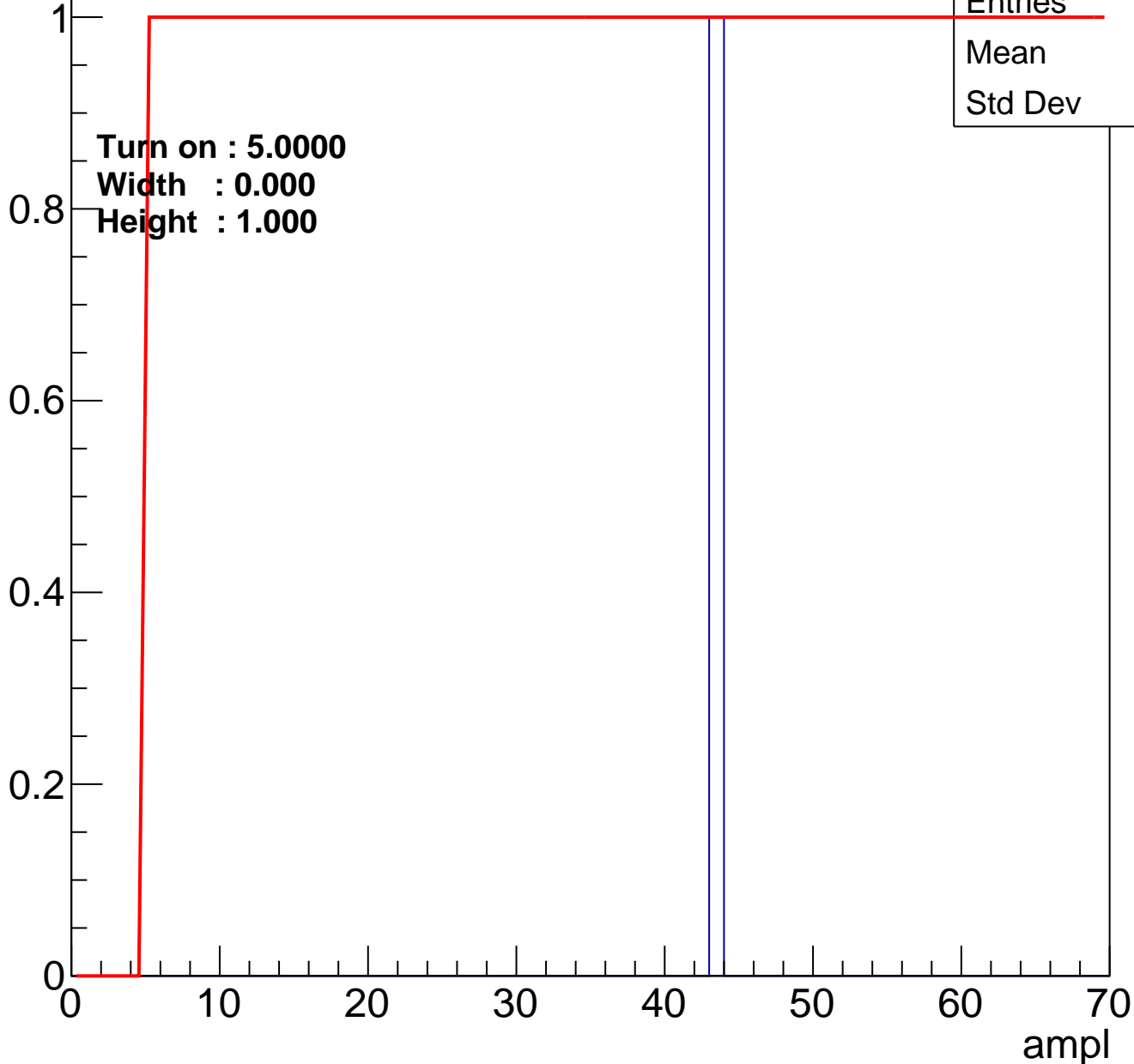
Height : 2.131



B0L100S, U13-ch55

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch56

calib_packv5_042523_0143.root, FC#6, port A1

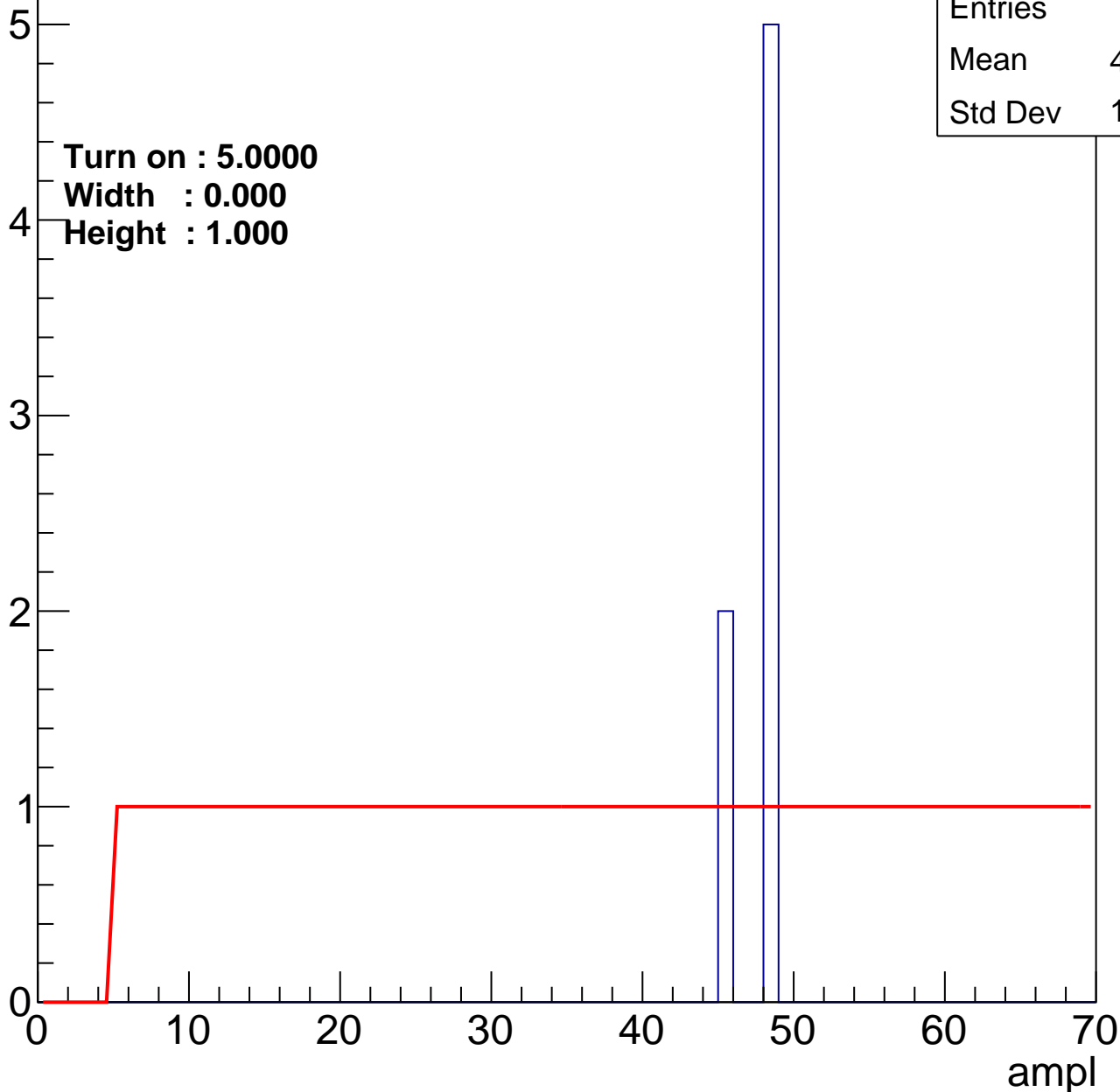
Entry

Entries	7
Mean	47.14
Std Dev	1.355

Turn on : 5.0000

Width : 0.000

Height : 1.000



B0L100S, U13-ch57

calib_packv5_042523_0143.root, FC#6, port A1

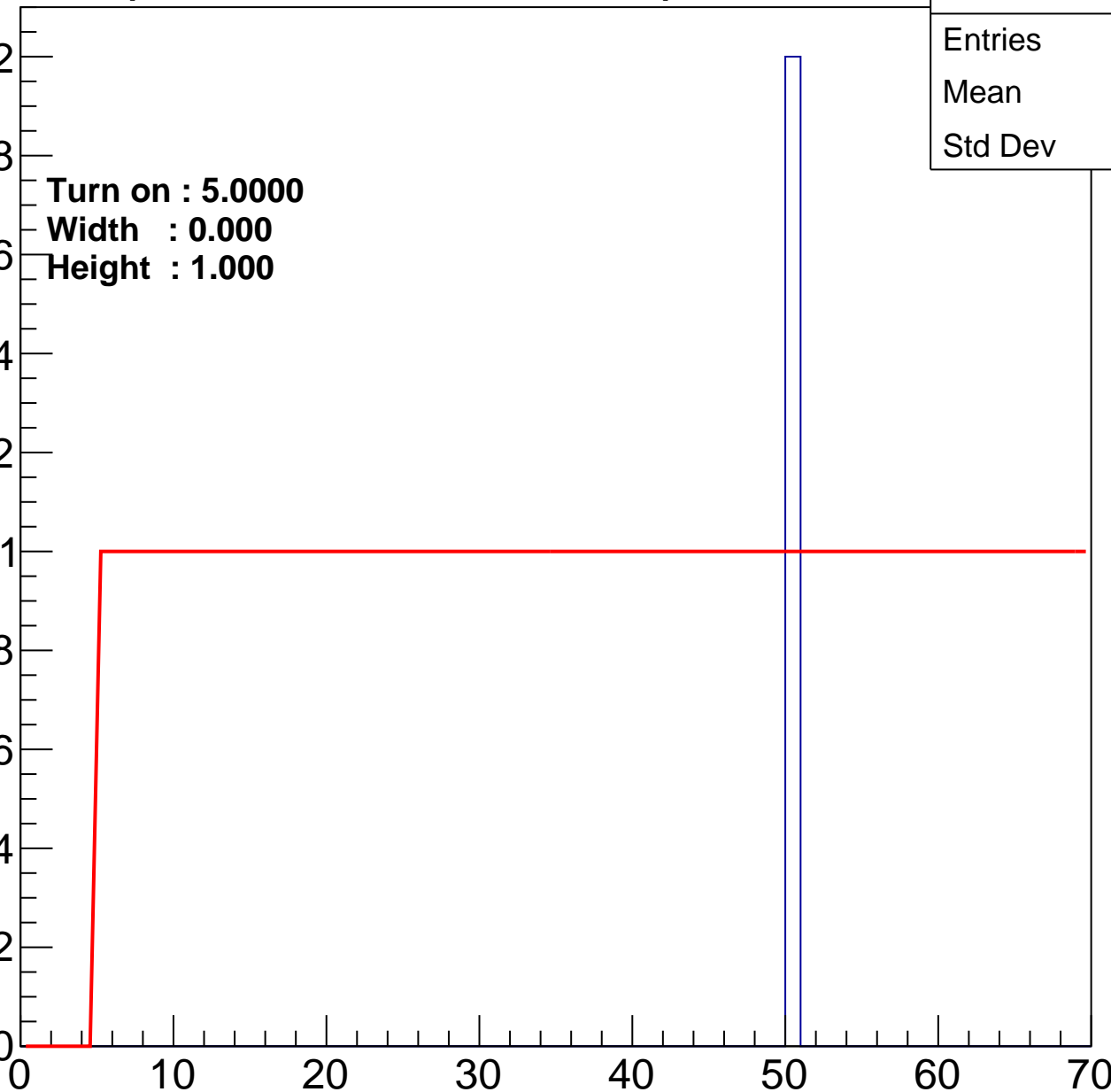
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	2
Mean	50
Std Dev	0

ampl



B0L100S, U13-ch58

calib_packv5_042523_0143.root, FC#6, port A1

Entry

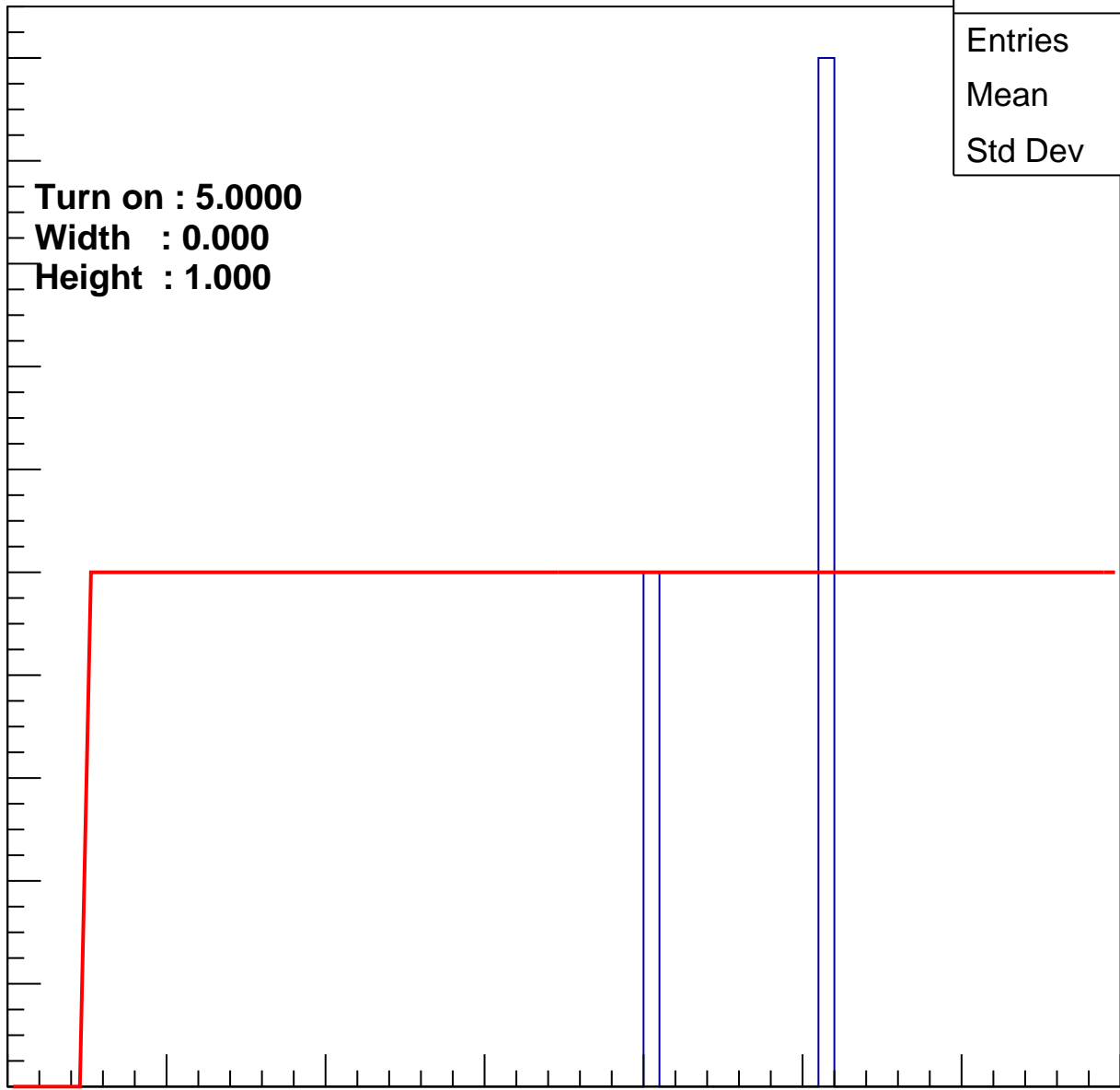
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	3
Mean	47.33
Std Dev	5.185

0 10 20 30 40 50 60 70

ampl



B0L100S, U13-ch59

calib_packv5_042523_0143.root, FC#6, port A1

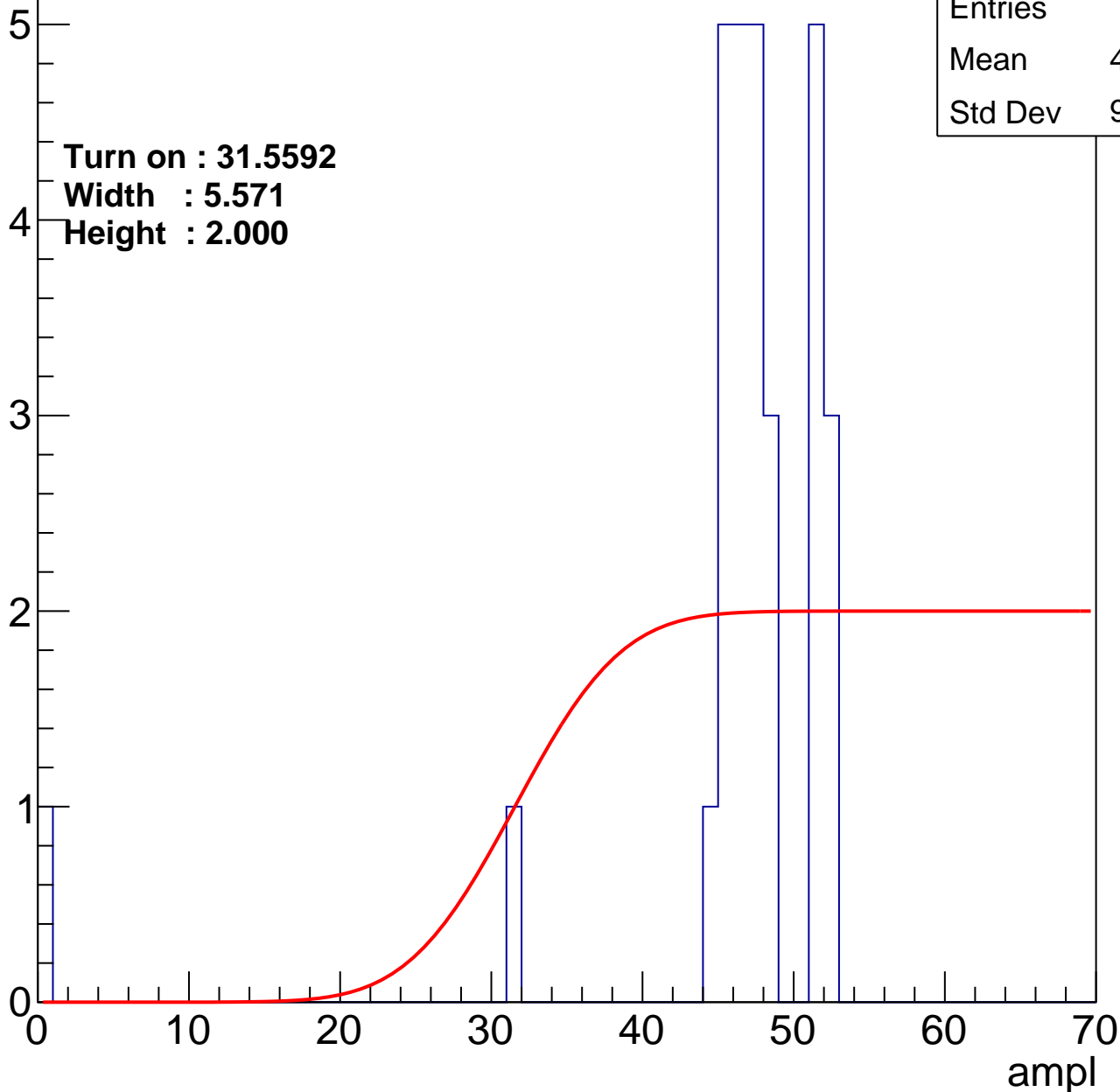
Entry

Entries	29
Mean	45.52
Std Dev	9.456

Turn on : 31.5592

Width : 5.571

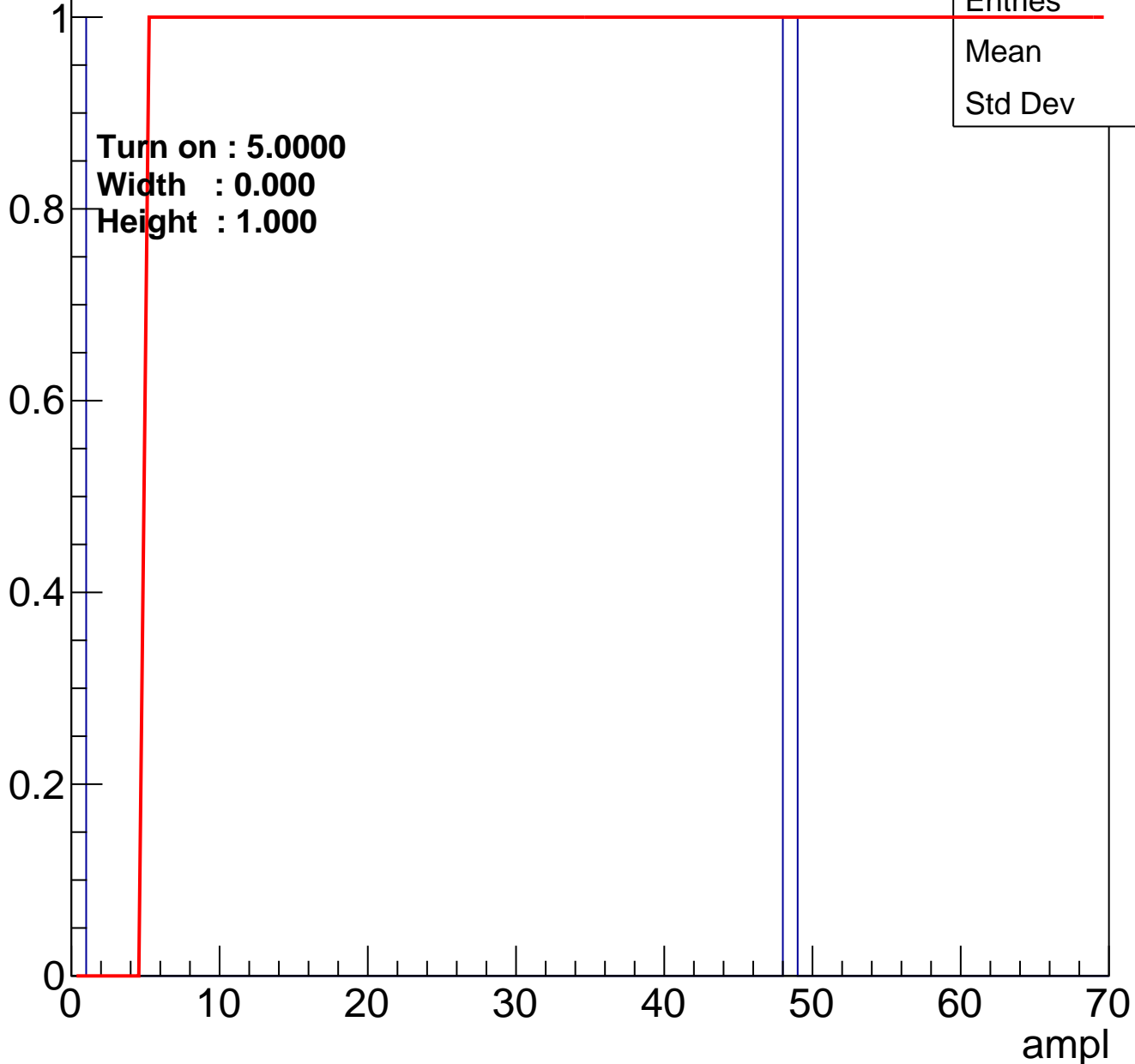
Height : 2.000



B0L100S, U13-ch60

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	2
Mean	24
Std Dev	24

B0L100S, U13-ch61

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch62

calib_packv5_042523_0143.root, FC#6, port A1

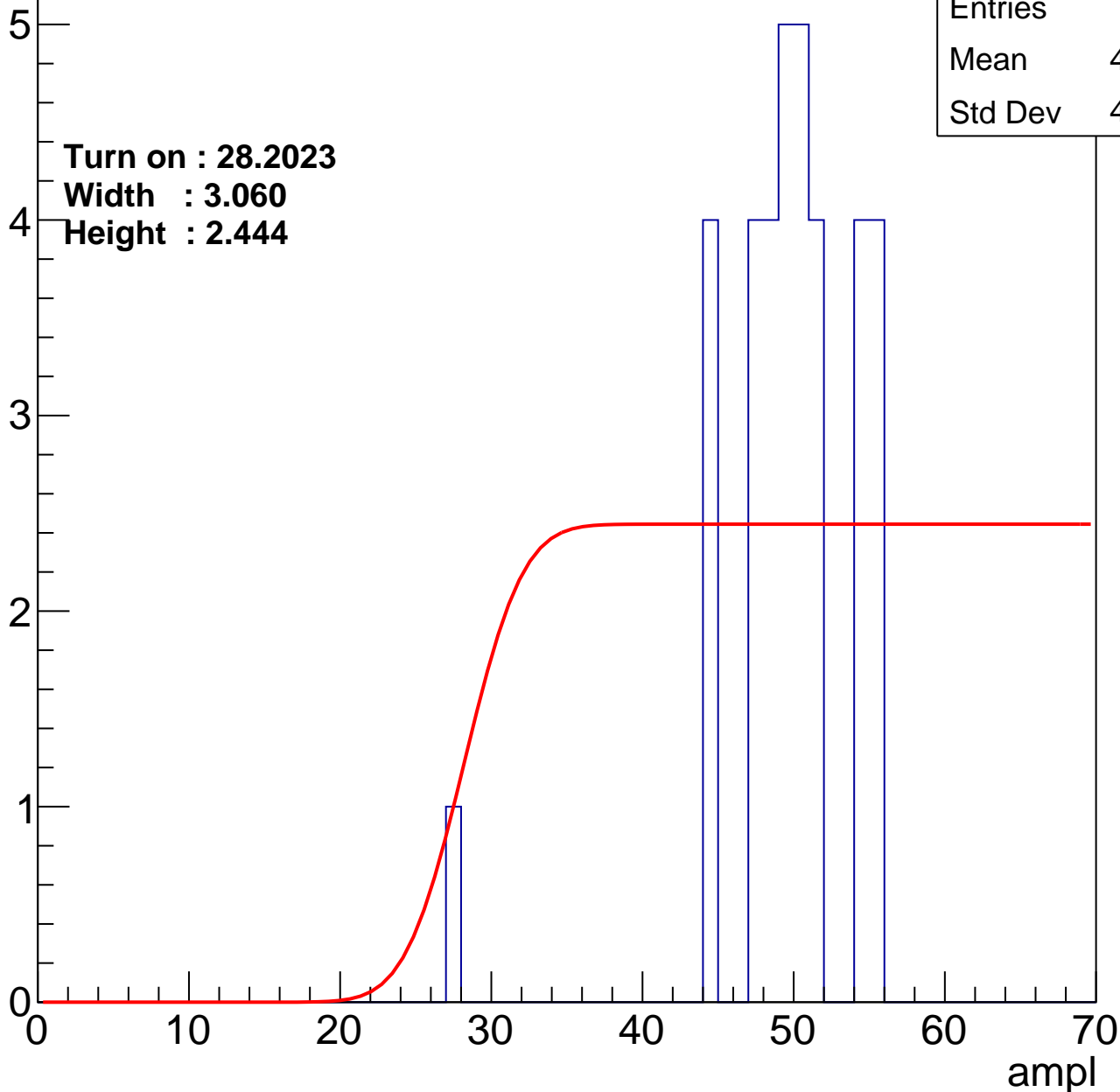
Entry

Entries	35
Mean	49.09
Std Dev	4.982

Turn on : 28.2023

Width : 3.060

Height : 2.444



B0L100S, U13-ch63

calib_packv5_042523_0143.root, FC#6, port A1

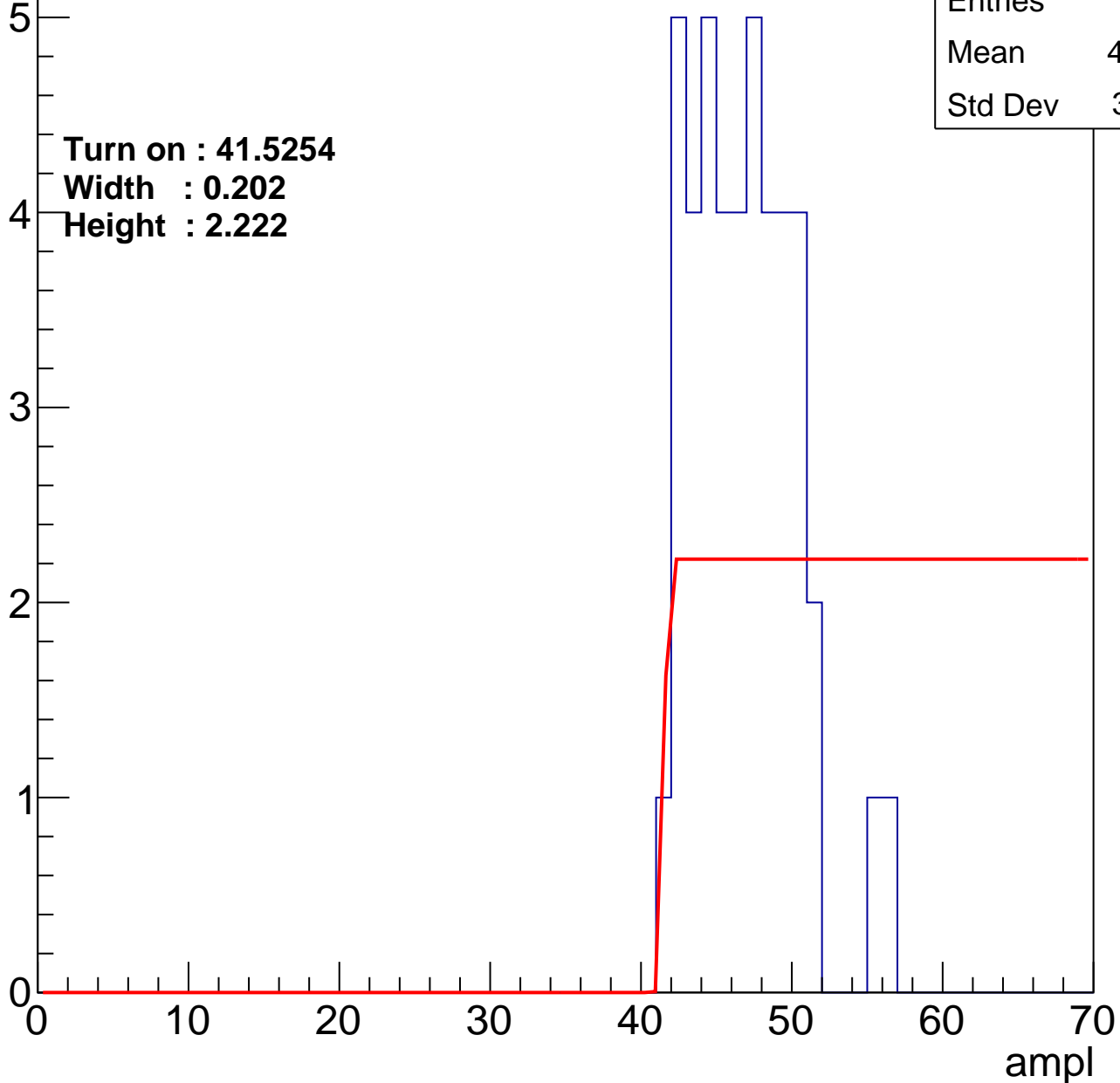
Entry

Entries	44
Mean	46.43
Std Dev	3.401

Turn on : 41.5254

Width : 0.202

Height : 2.222



B0L100S, U13-ch64

calib_packv5_042523_0143.root, FC#6, port A1

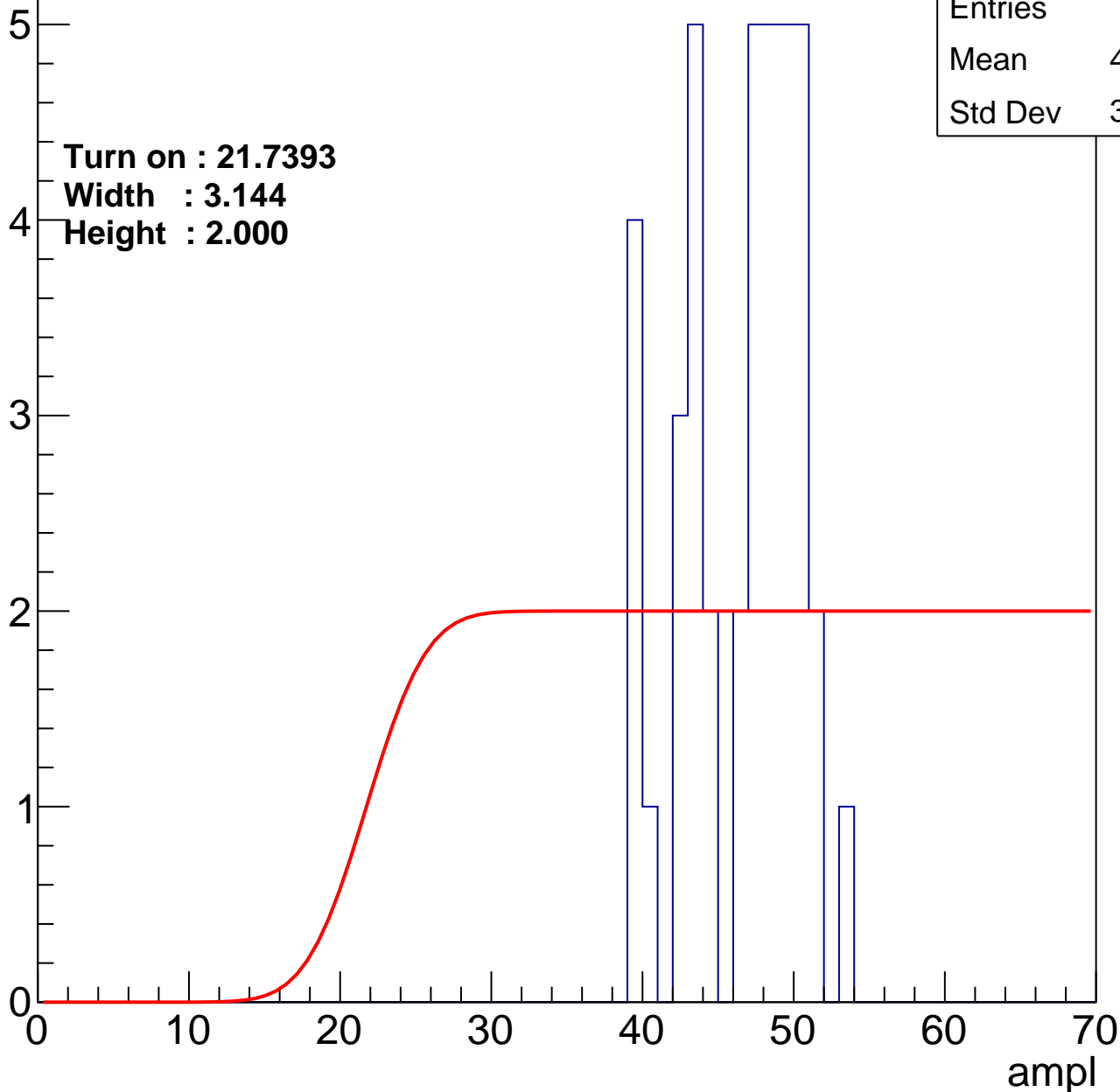
Entry

Entries	40
Mean	46.05
Std Dev	3.814

Turn on : 21.7393

Width : 3.144

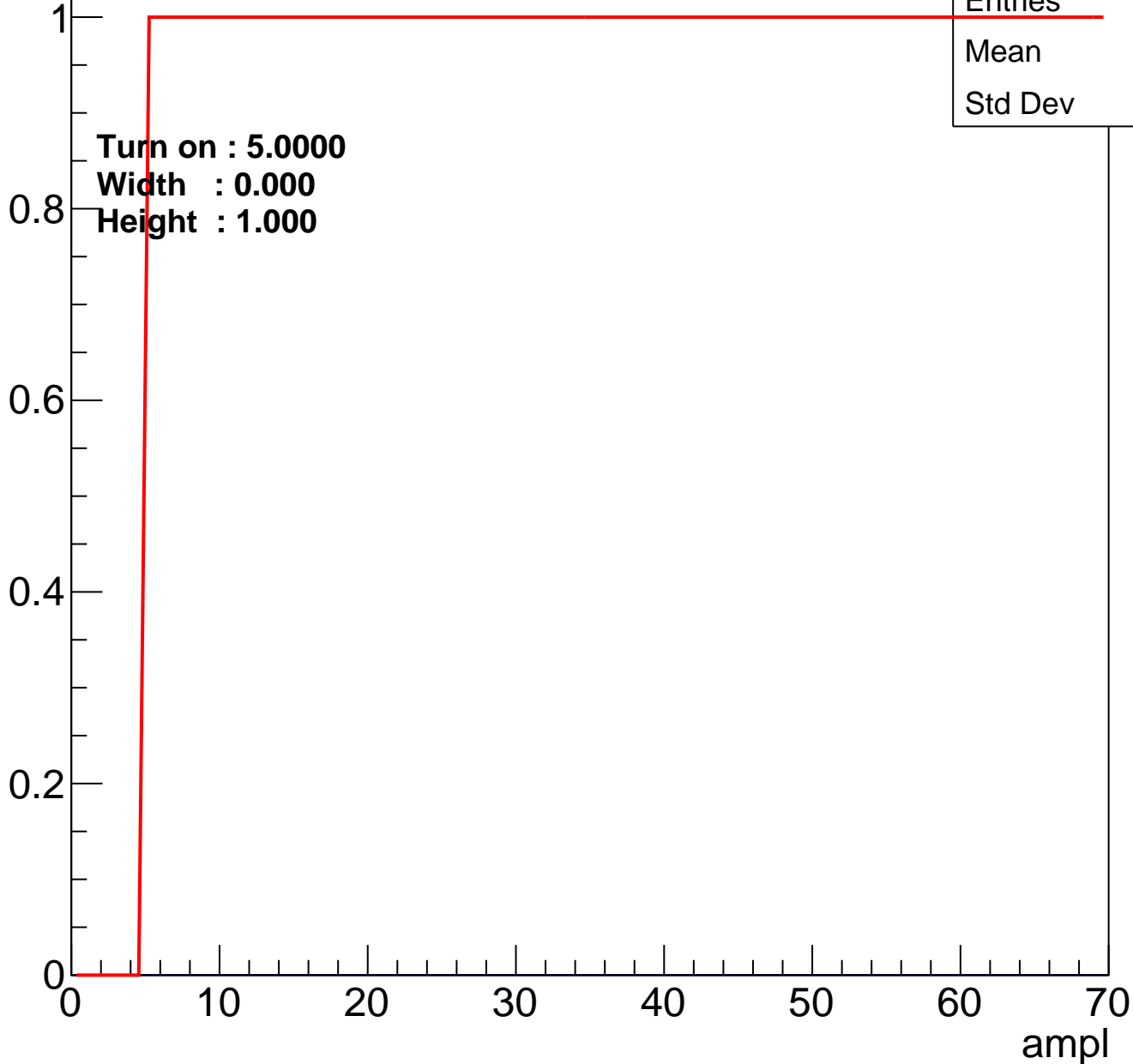
Height : 2.000



B0L100S, U13-ch65

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch66

calib_packv5_042523_0143.root, FC#6, port A1

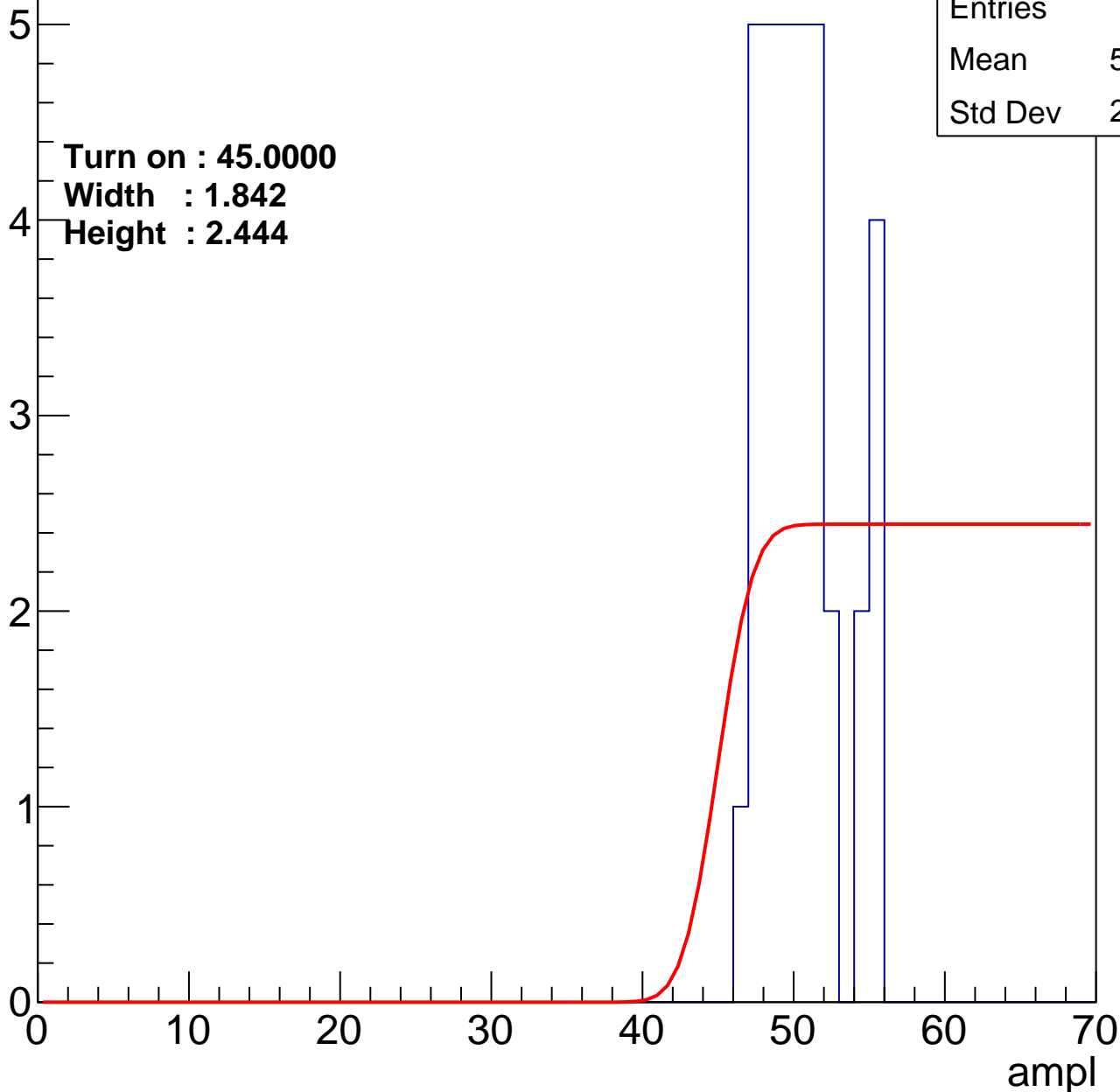
Entry

Entries	34
Mean	50.09
Std Dev	2.605

Turn on : 45.0000

Width : 1.842

Height : 2.444



B0L100S, U13-ch67

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch68

calib_packv5_042523_0143.root, FC#6, port A1

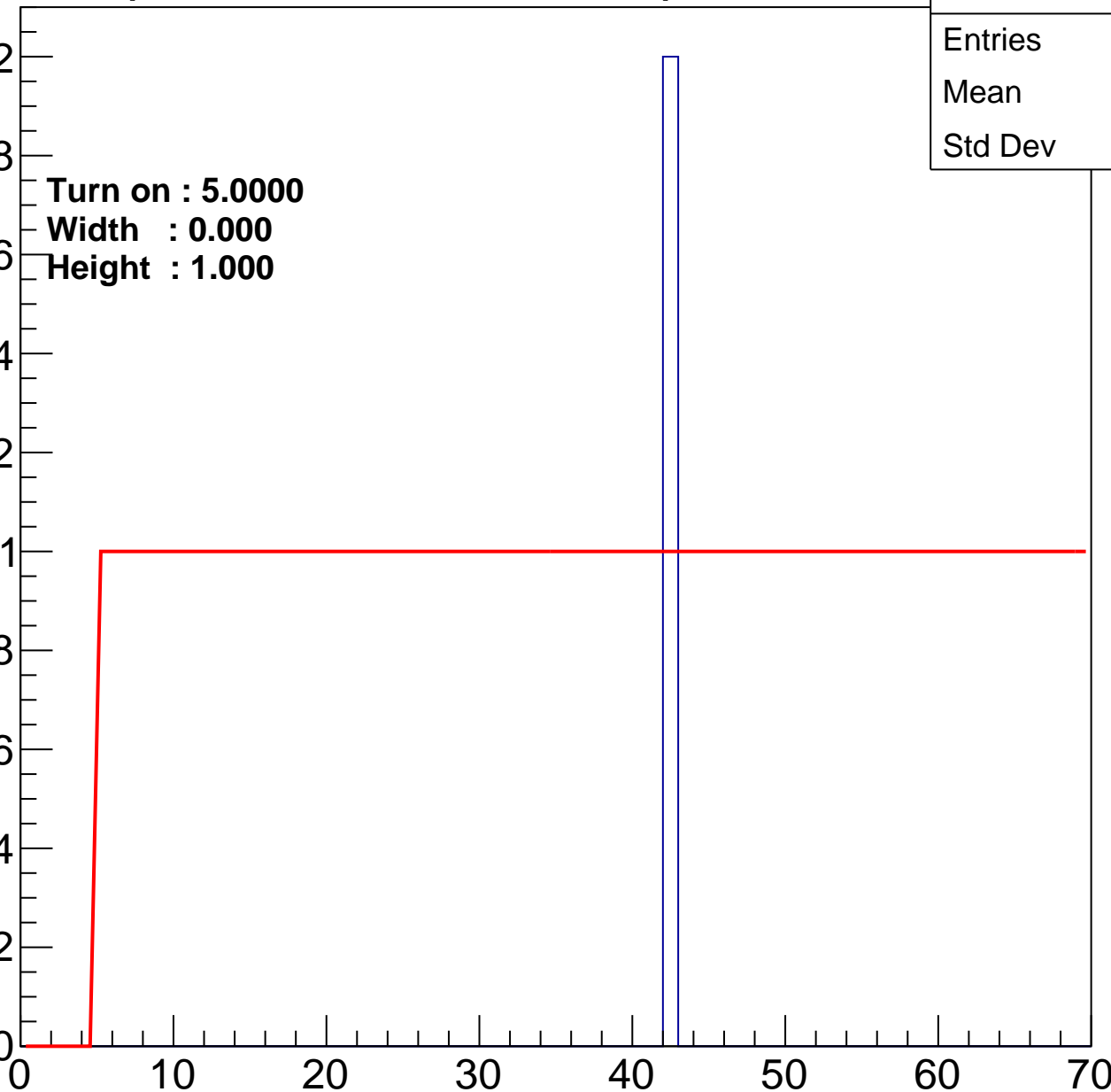
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	2
Mean	42
Std Dev	0

ampl



B0L100S, U13-ch69

calib_packv5_042523_0143.root, FC#6, port A1

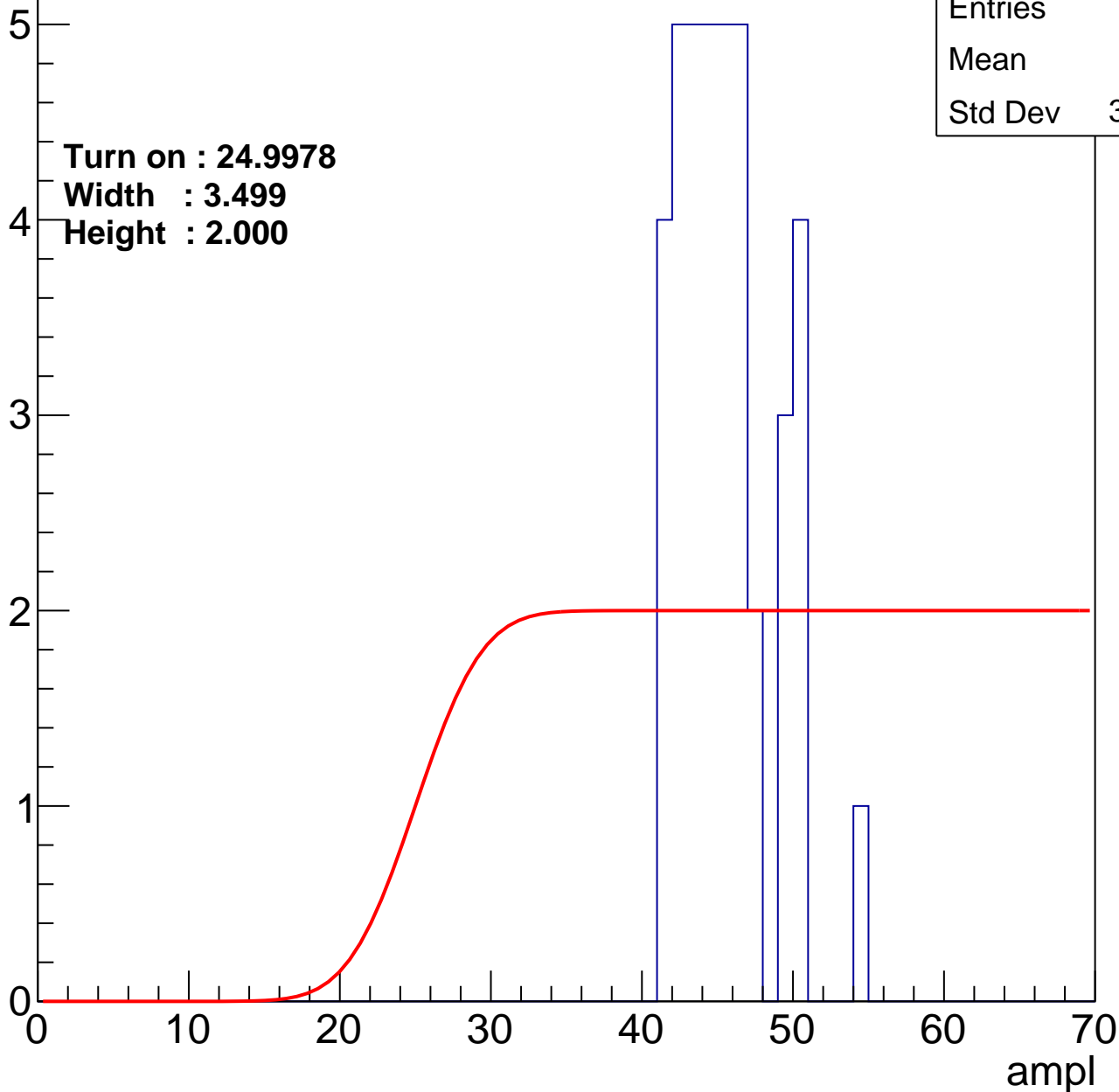
Entry

Entries	39
Mean	45.1
Std Dev	3.103

Turn on : 24.9978

Width : 3.499

Height : 2.000



B0L100S, U13-ch70

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch71

calib_packv5_042523_0143.root, FC#6, port A1

Entry

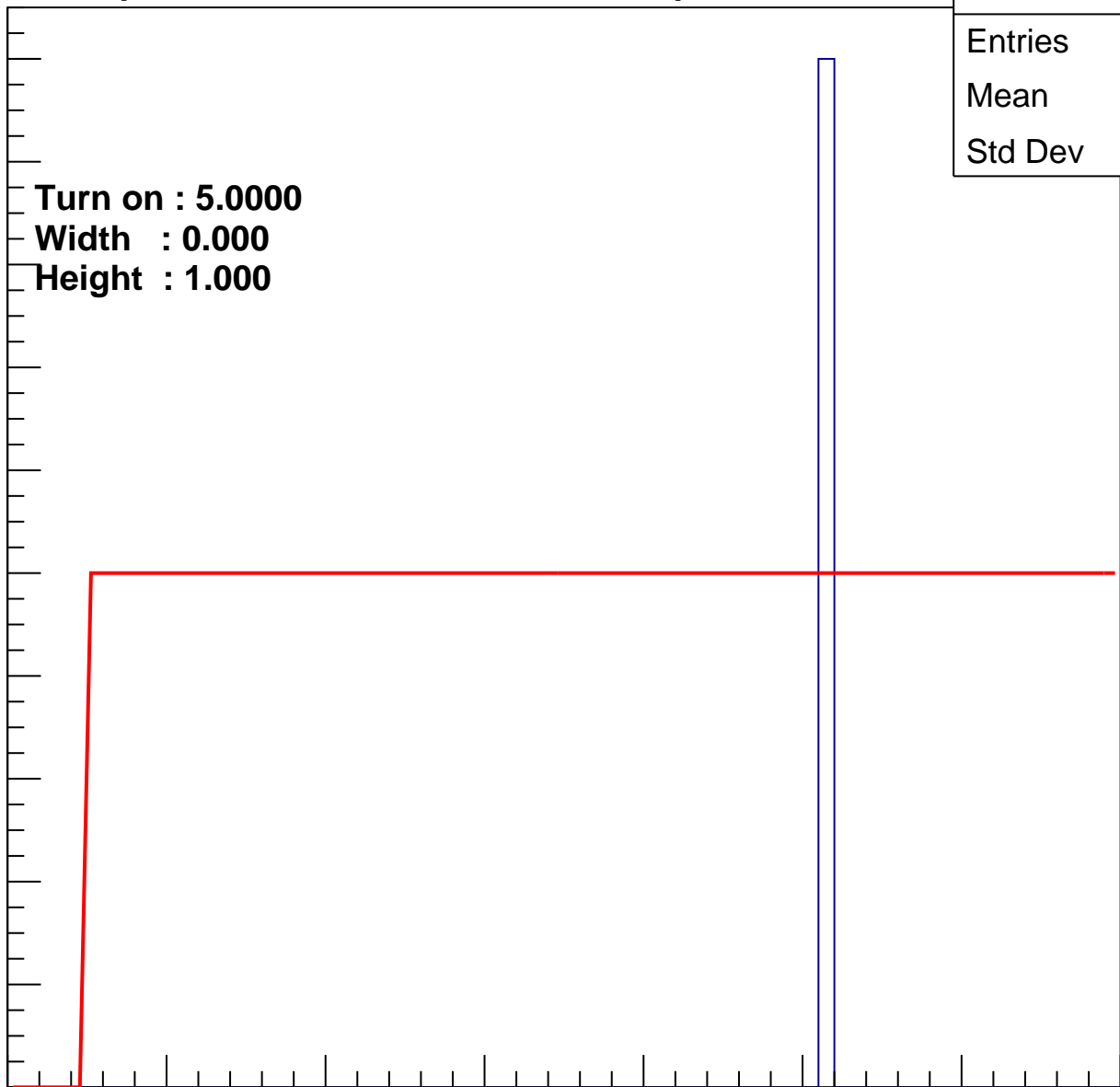
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	2
Mean	51
Std Dev	0

0 10 20 30 40 50 60 70

ampl



B0L100S, U13-ch72

calib_packv5_042523_0143.root, FC#6, port A1

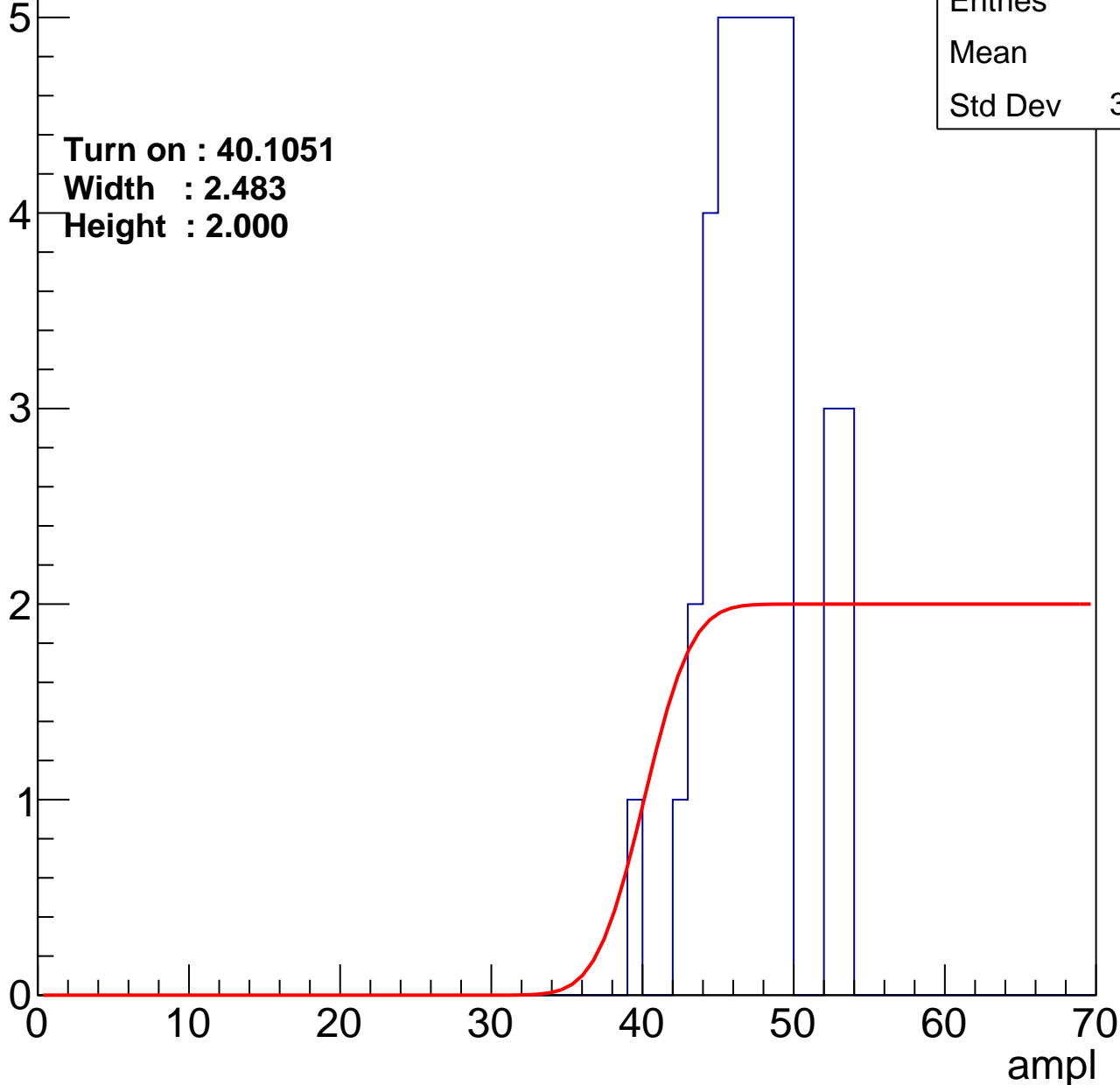
Entry

Entries	39
Mean	47
Std Dev	3.162

Turn on : 40.1051

Width : 2.483

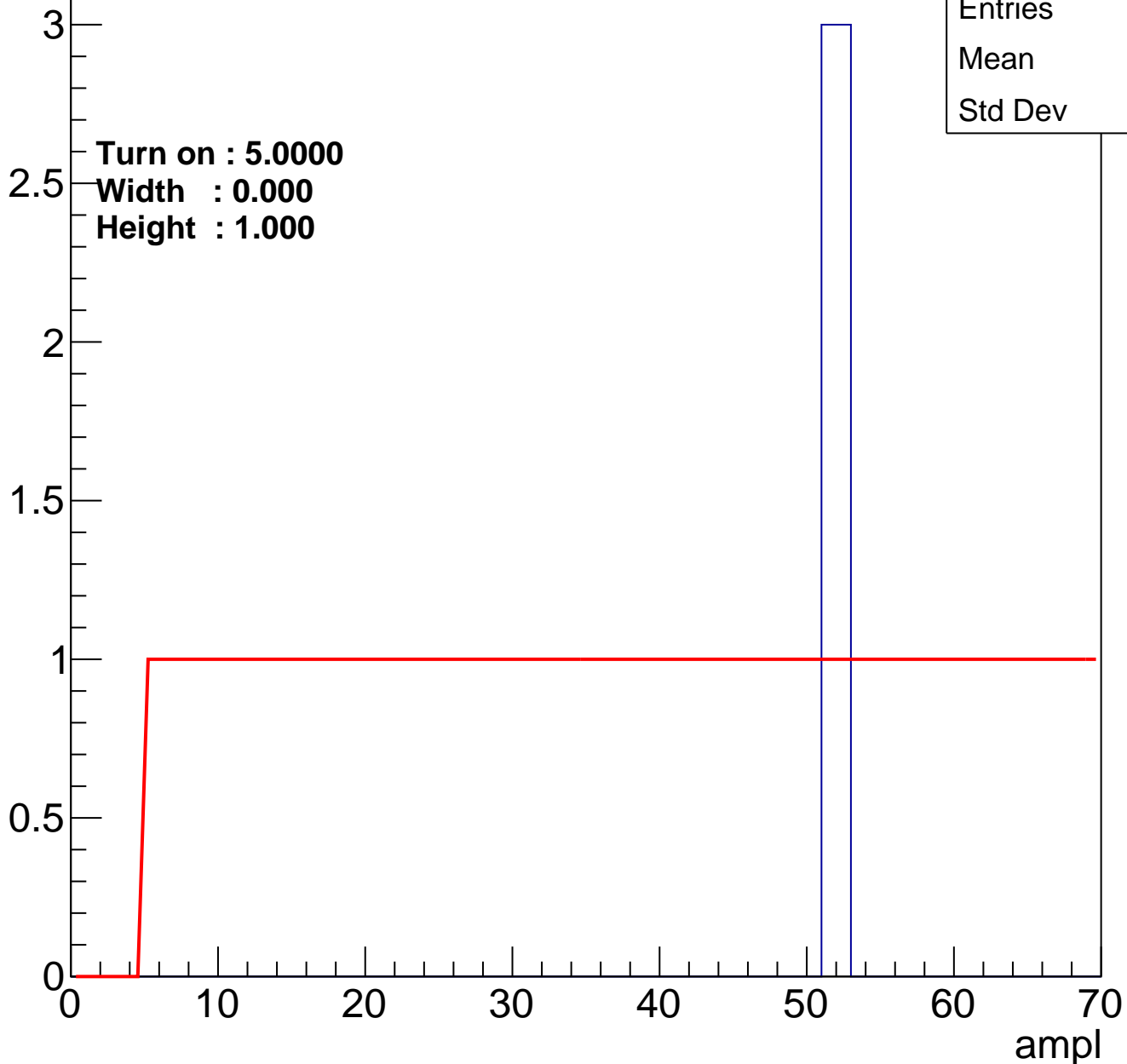
Height : 2.000



B0L100S, U13-ch73

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch74

calib_packv5_042523_0143.root, FC#6, port A1

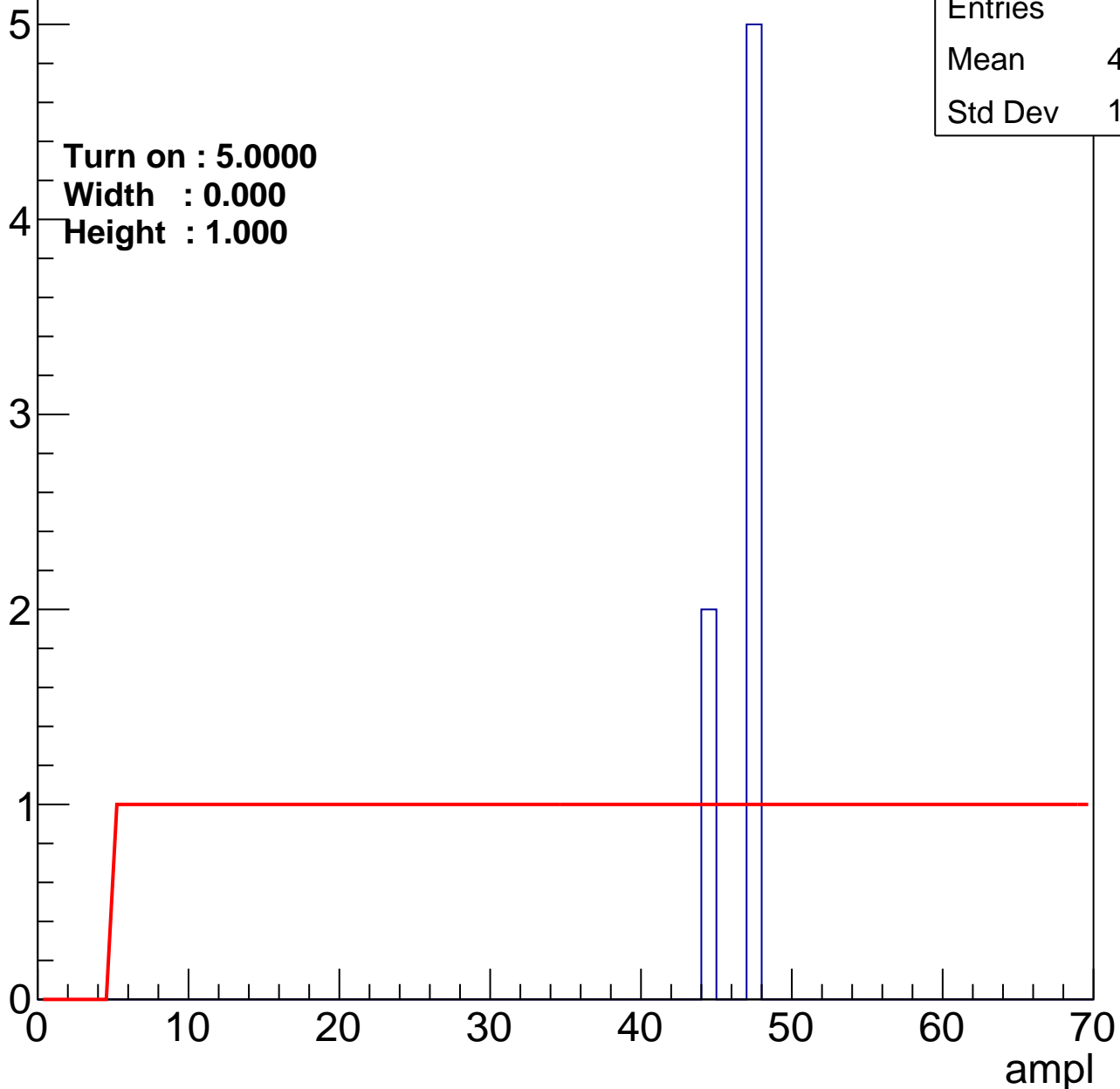
Entry

Entries	7
Mean	46.14
Std Dev	1.355

Turn on : 5.0000

Width : 0.000

Height : 1.000



B0L100S, U13-ch75

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch76

calib_packv5_042523_0143.root, FC#6, port A1

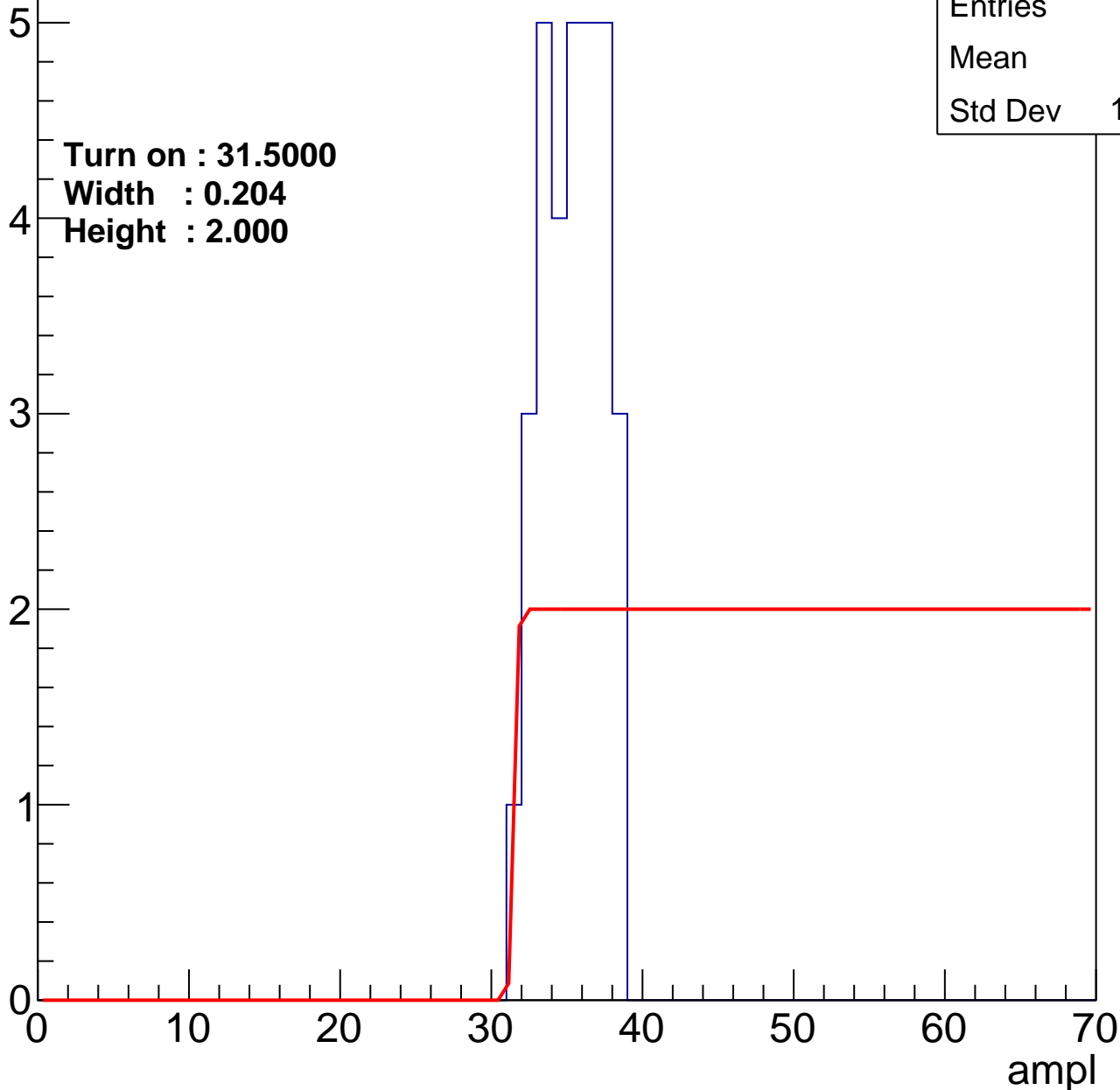
Entry

Entries	31
Mean	34.9
Std Dev	1.957

Turn on : 31.5000

Width : 0.204

Height : 2.000



B0L100S, U13-ch77

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch78

calib_packv5_042523_0143.root, FC#6, port A1

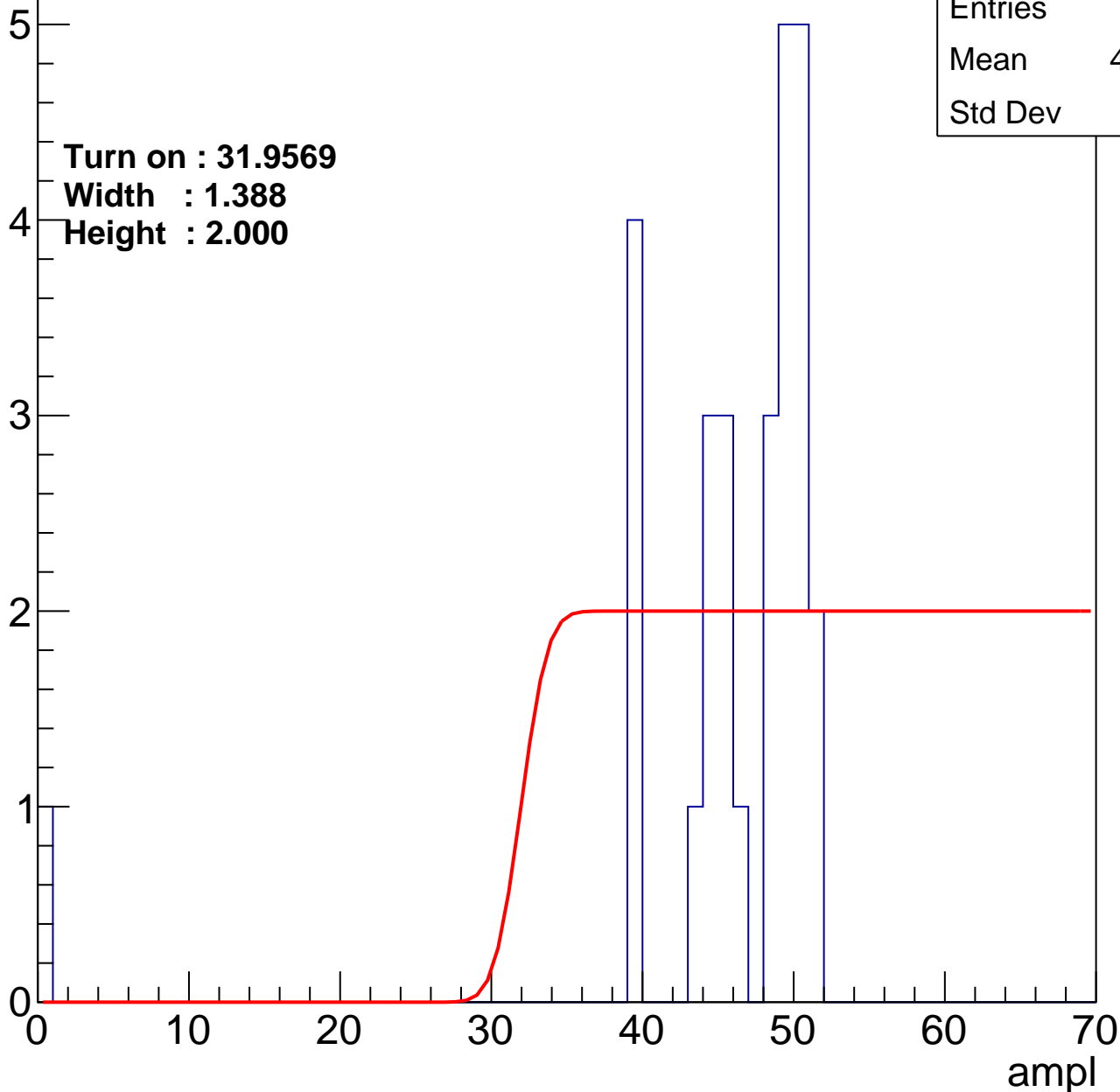
Entry

Entries	28
Mean	44.75
Std Dev	9.41

Turn on : 31.9569

Width : 1.388

Height : 2.000



B0L100S, U13-ch79

calib_packv5_042523_0143.root, FC#6, port A1

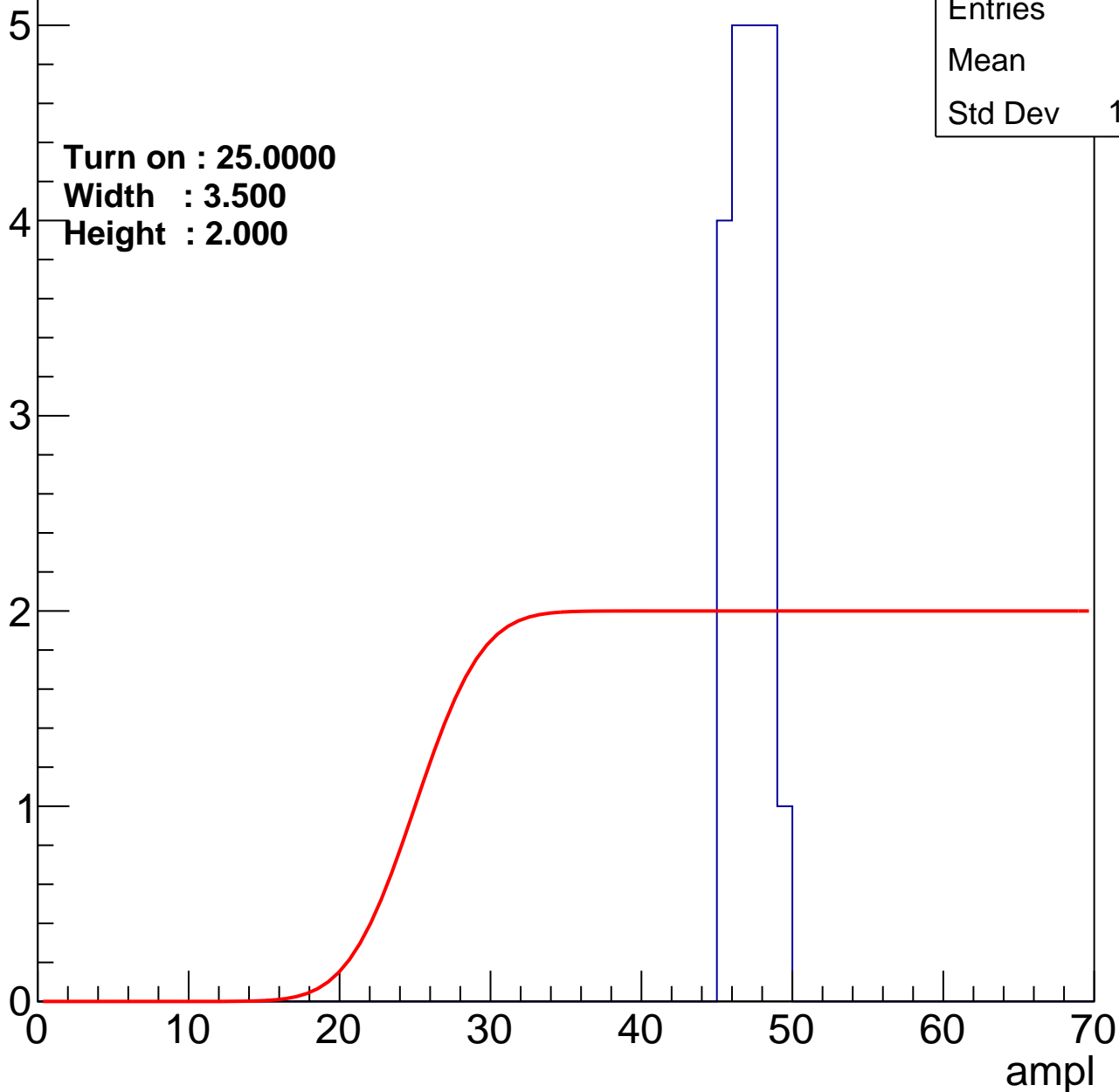
Entry

Entries	20
Mean	46.7
Std Dev	1.187

Turn on : 25.0000

Width : 3.500

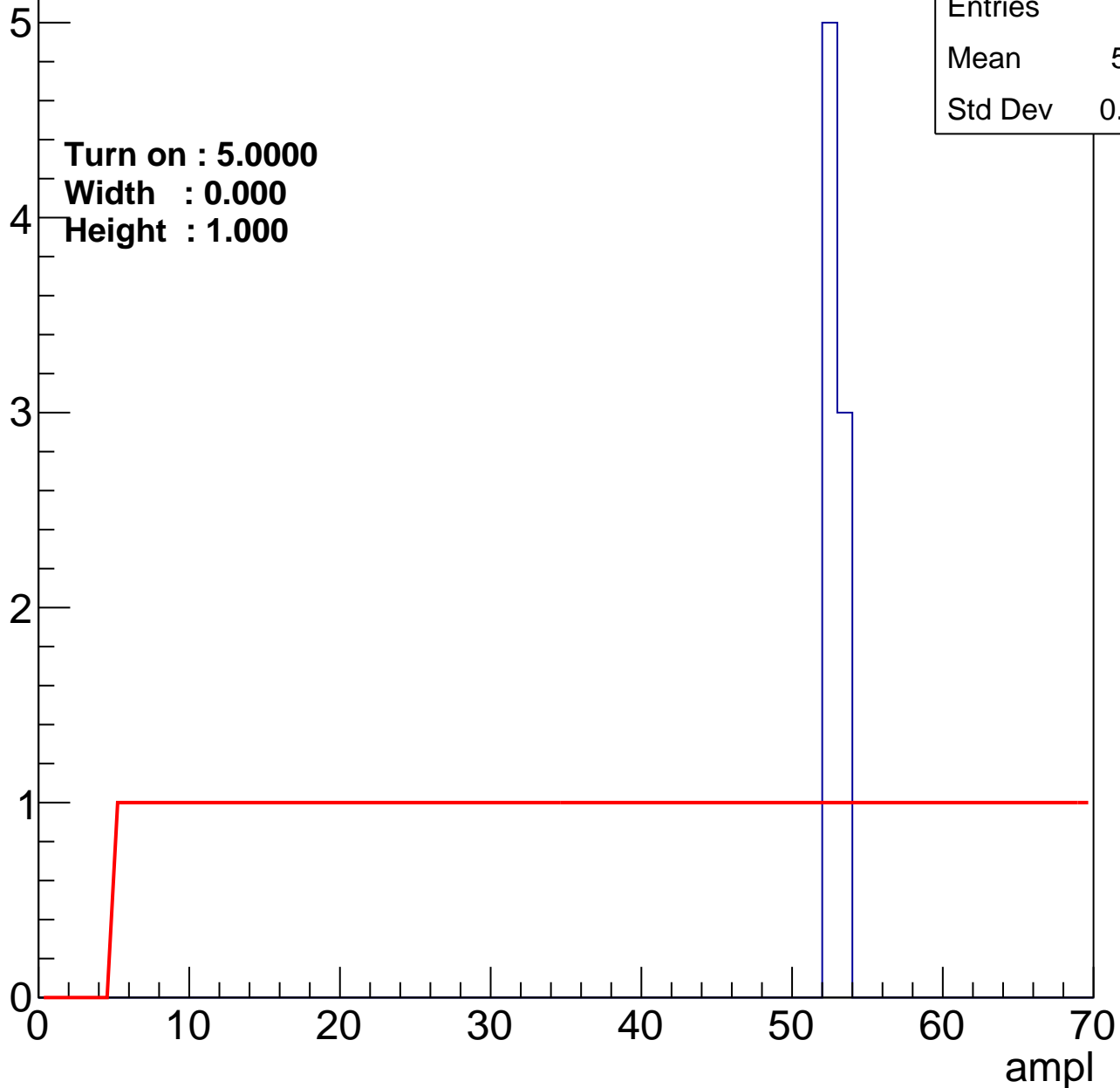
Height : 2.000



B0L100S, U13-ch80

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch81

calib_packv5_042523_0143.root, FC#6, port A1

Entry

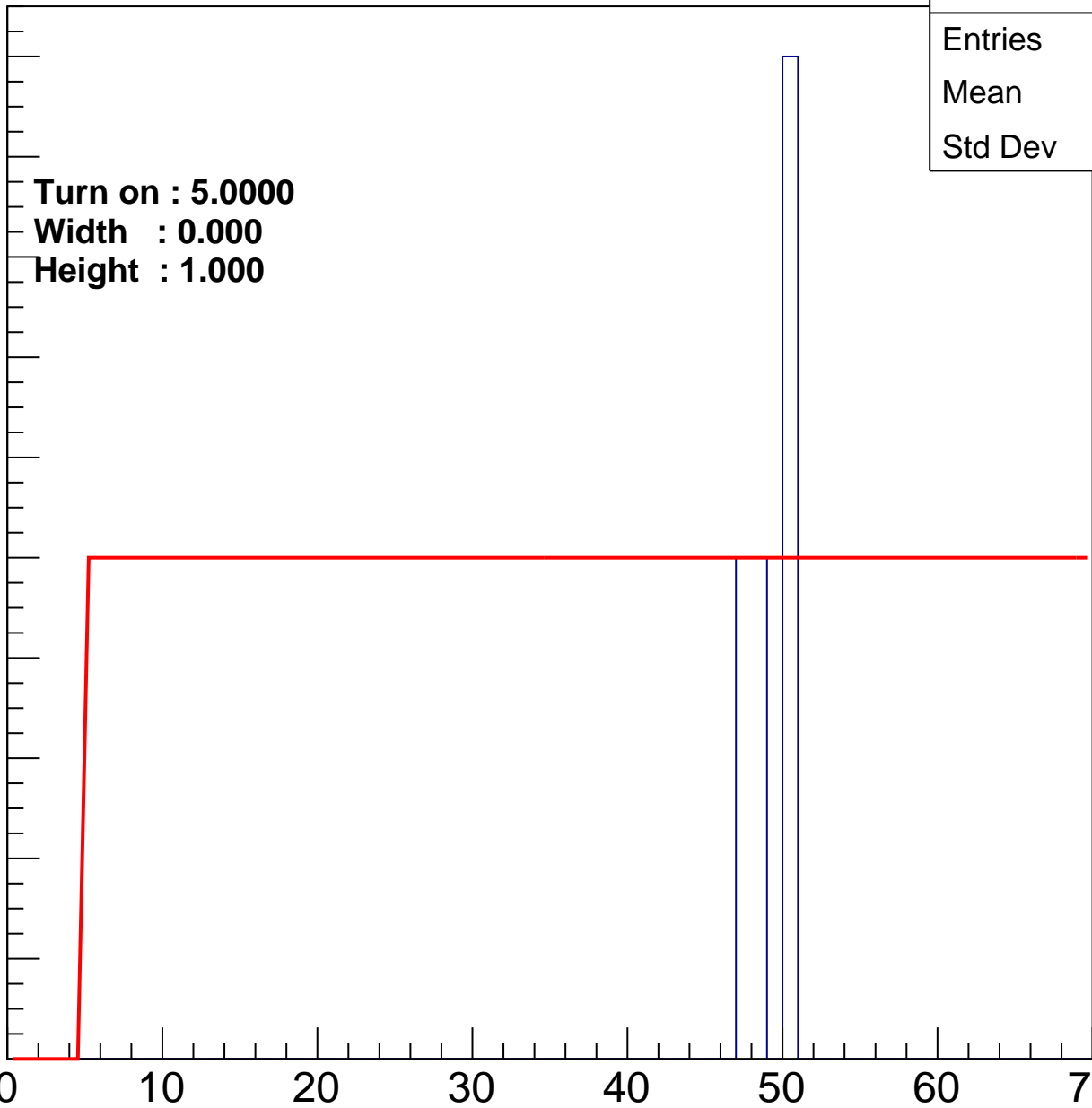
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	4
Mean	48.75
Std Dev	1.299

0 10 20 30 40 50 60 70

ampl



B0L100S, U13-ch82

calib_packv5_042523_0143.root, FC#6, port A1

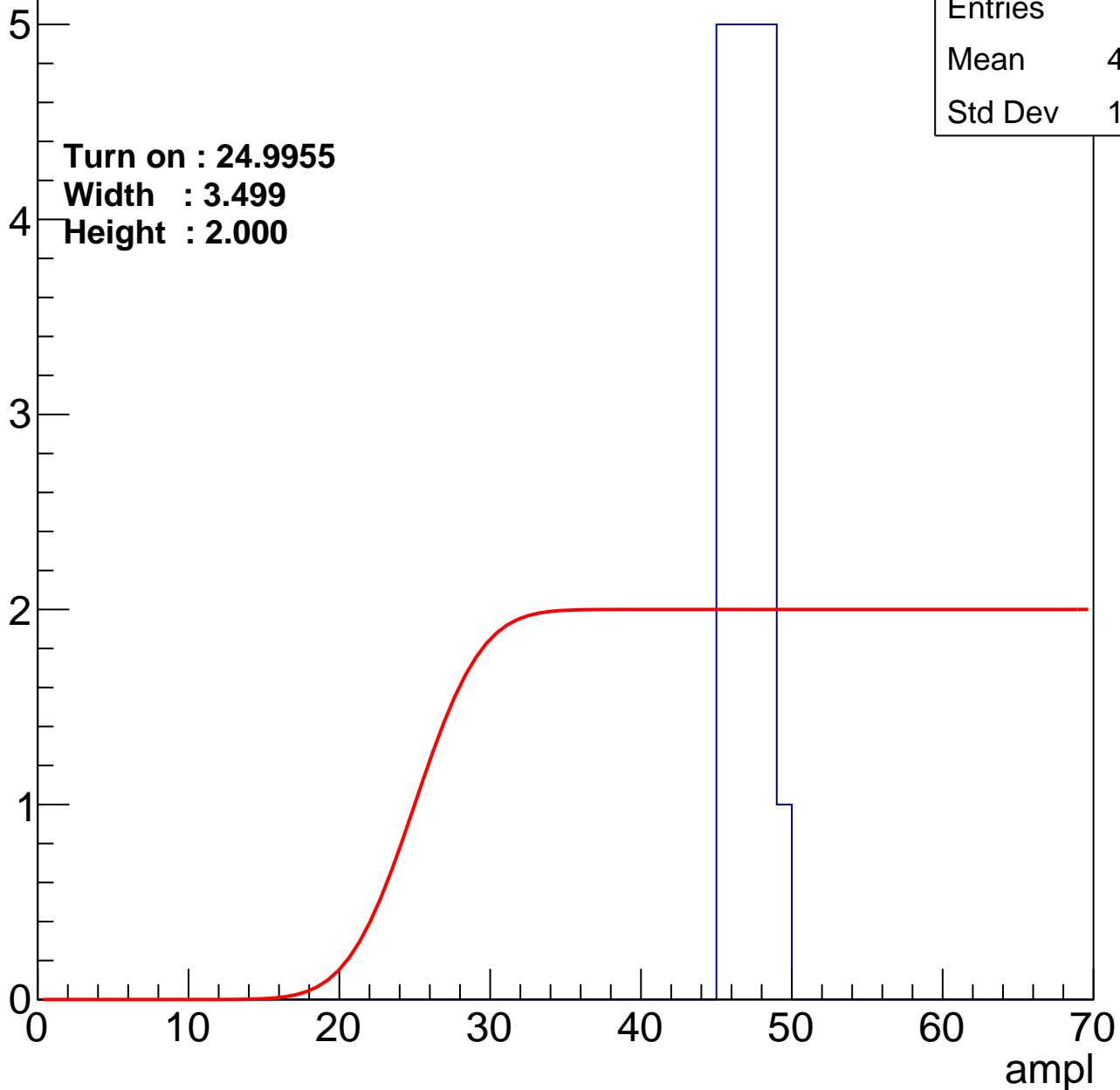
Entry

Entries	21
Mean	46.62
Std Dev	1.214

Turn on : 24.9955

Width : 3.499

Height : 2.000



B0L100S, U13-ch83

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch84

calib_packv5_042523_0143.root, FC#6, port A1

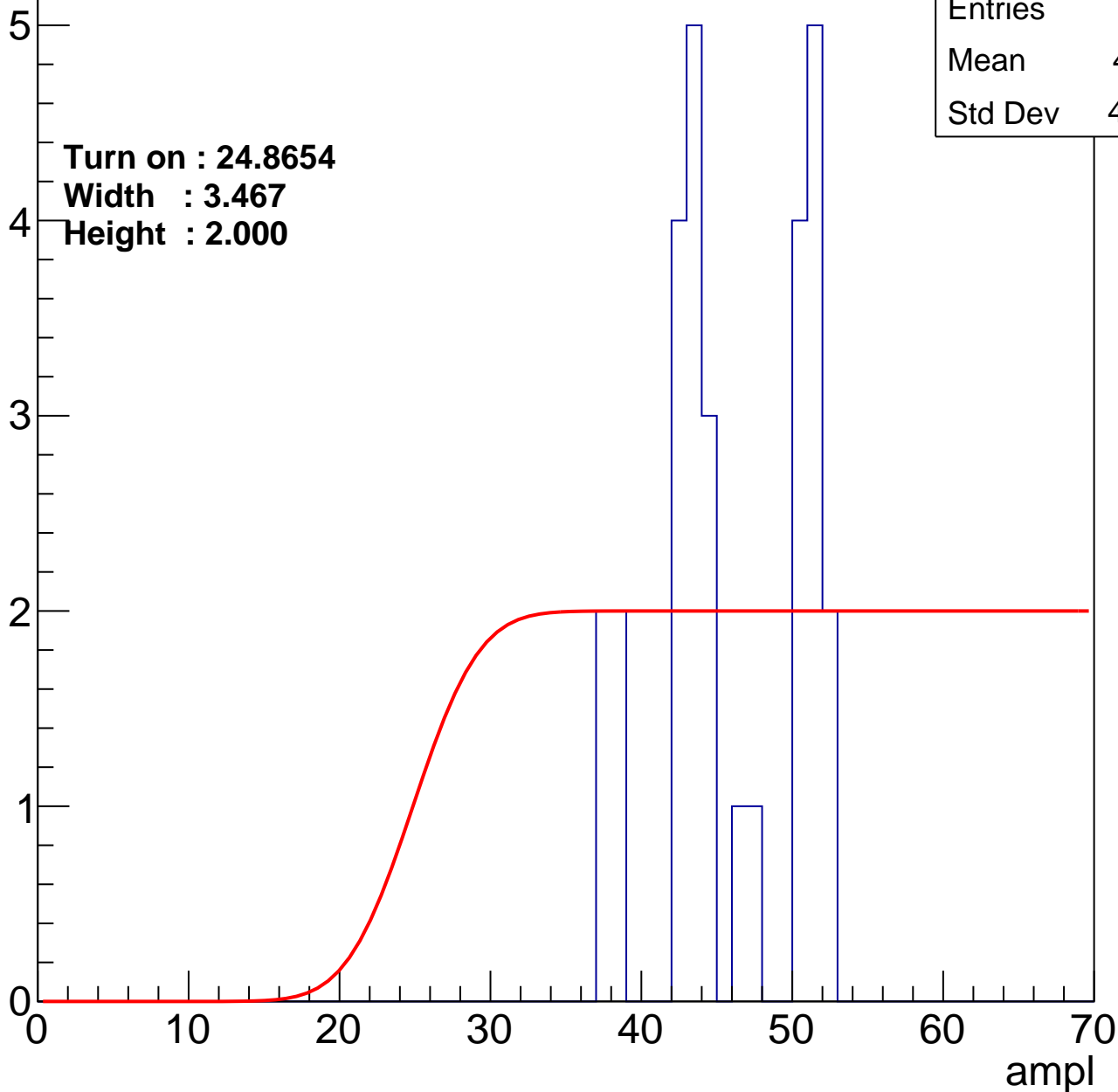
Entry

Entries	29
Mean	45.41
Std Dev	4.782

Turn on : 24.8654

Width : 3.467

Height : 2.000



B0L100S, U13-ch85

calib_packv5_042523_0143.root, FC#6, port A1

Entry

5

4

3

2

1

0

Turn on : 25.0000

Width : 3.500

Height : 2.000

Entries	41
Mean	49.39
Std Dev	2.367

ampl

0

10

20

30

40

50

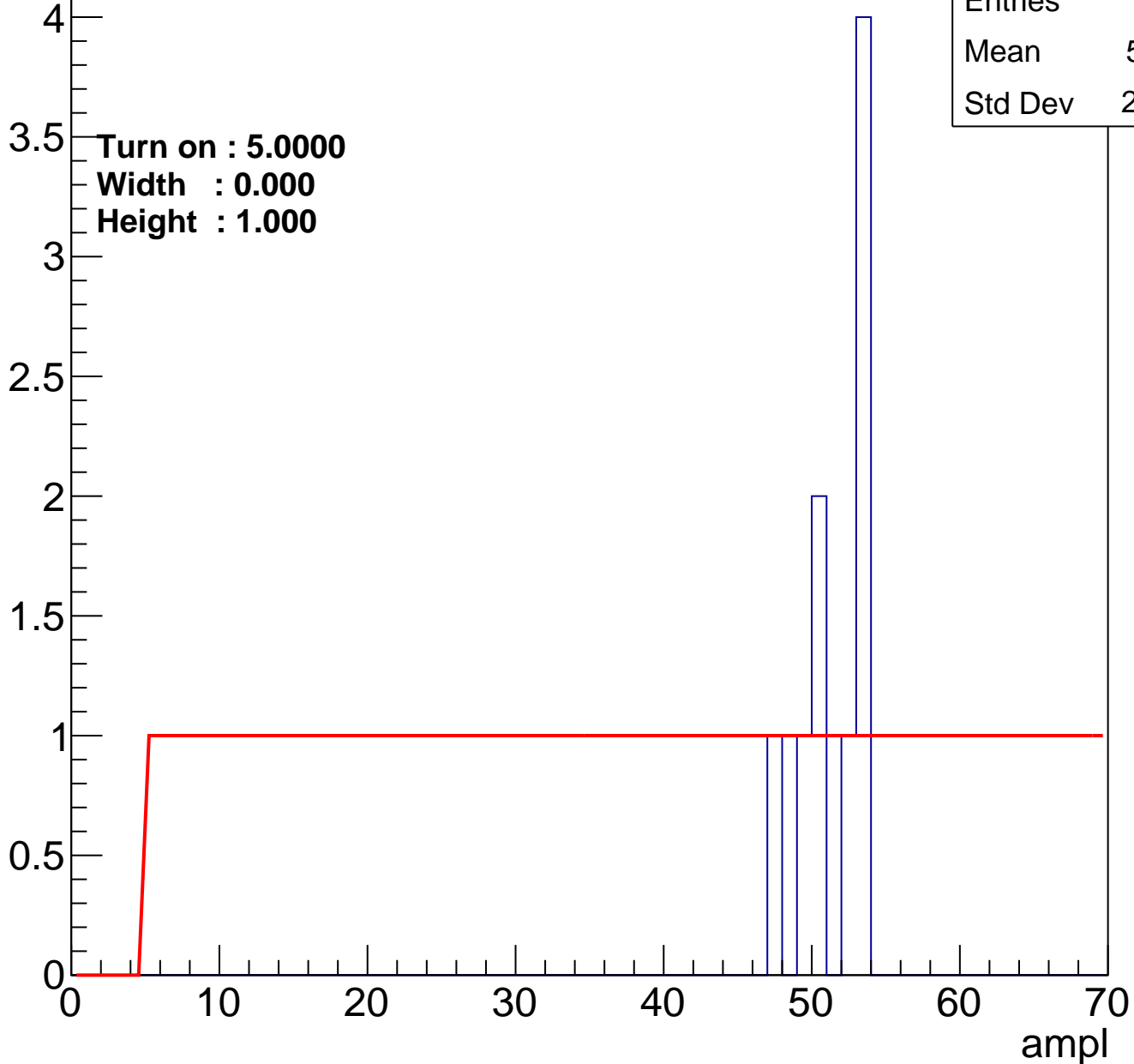
60

70

B0L100S, U13-ch86

calib_packv5_042523_0143.root, FC#6, port A1

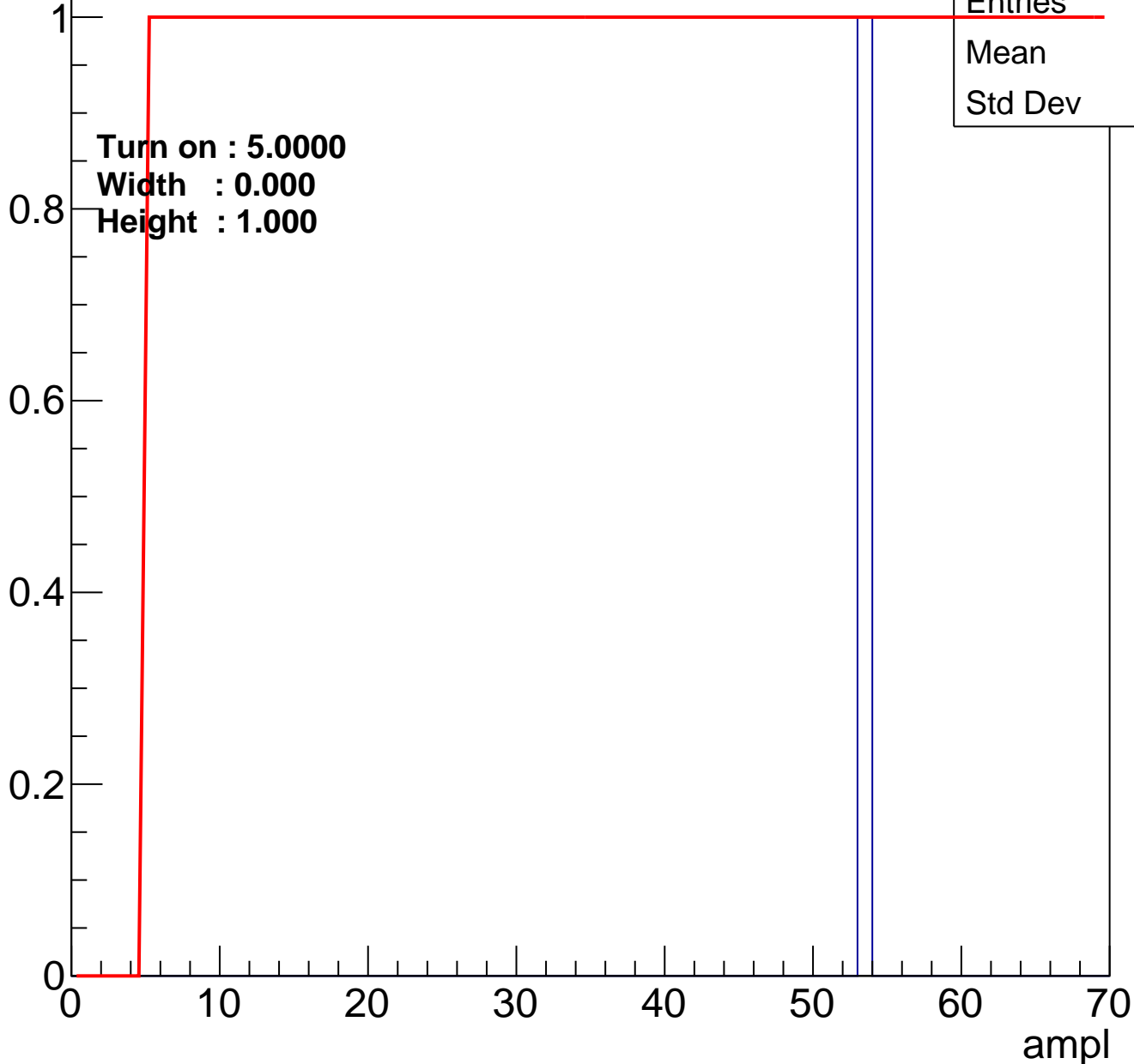
Entry



B0L100S, U13-ch87

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch88

calib_packv5_042523_0143.root, FC#6, port A1

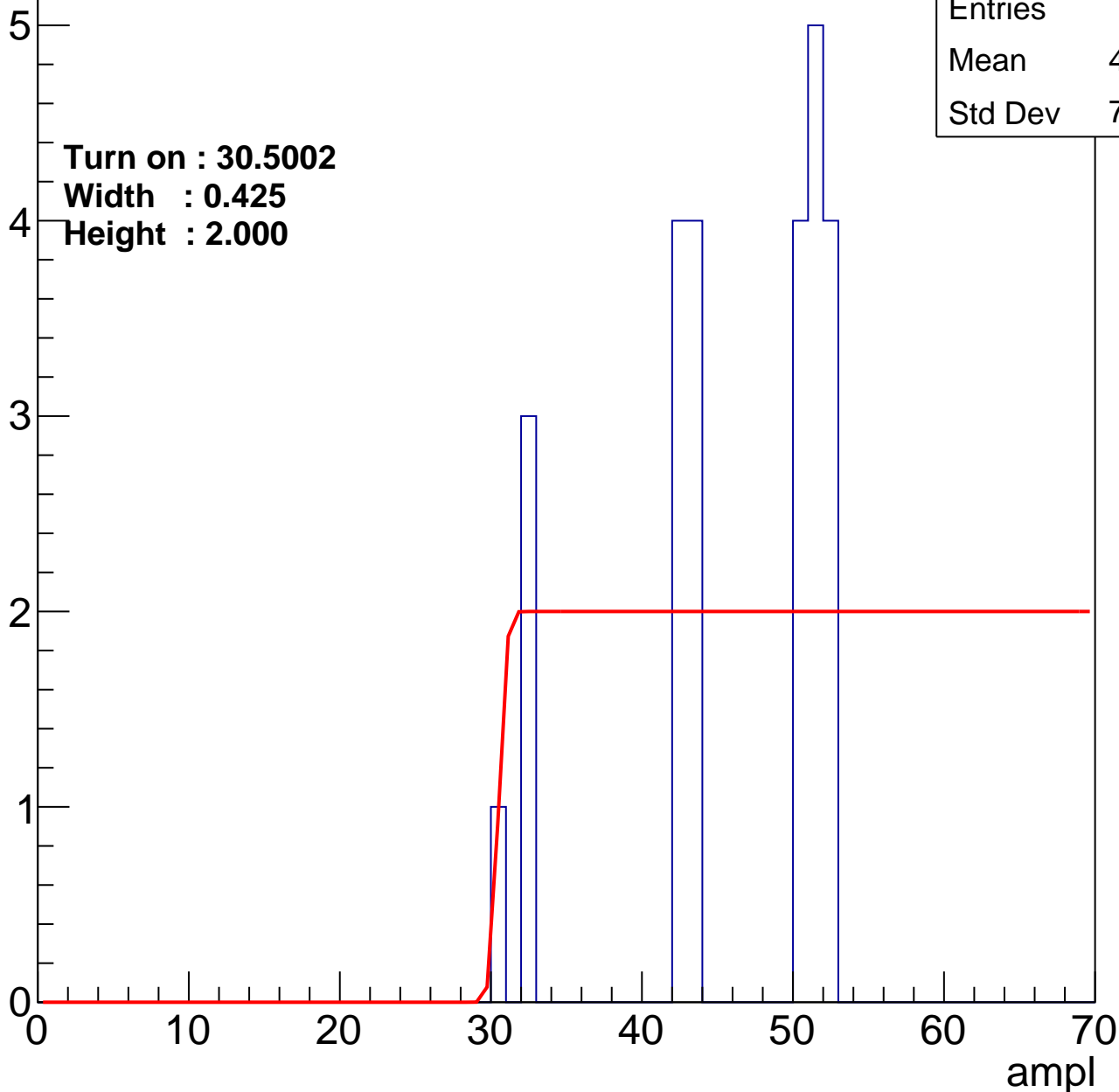
Entry

Entries	25
Mean	45.16
Std Dev	7.097

Turn on : 30.5002

Width : 0.425

Height : 2.000



B0L100S, U13-ch89

calib_packv5_042523_0143.root, FC#6, port A1

Entry

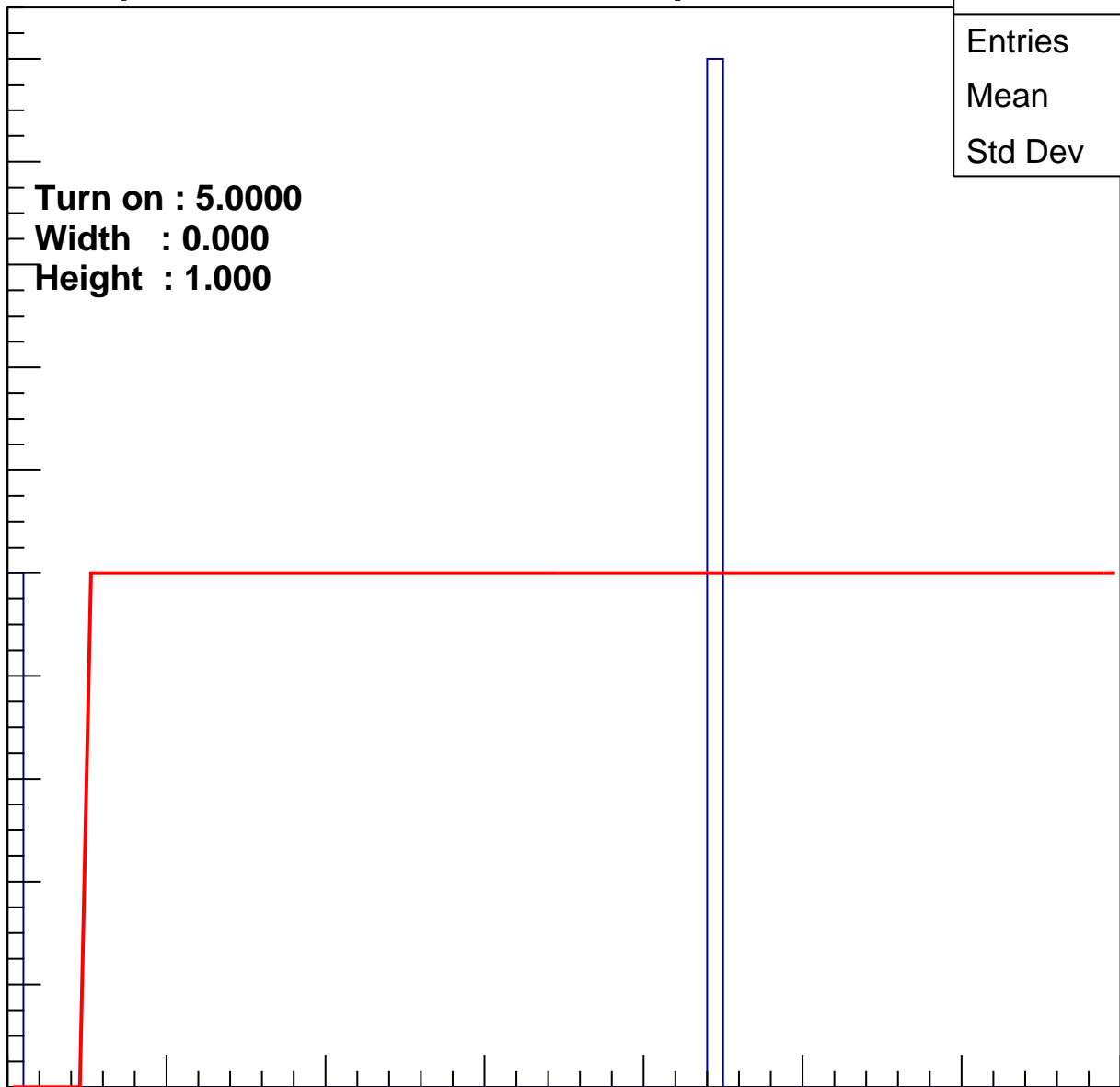
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	3
Mean	29.33
Std Dev	20.74

0 10 20 30 40 50 60 70

ampl



B0L100S, U13-ch90

calib_packv5_042523_0143.root, FC#6, port A1

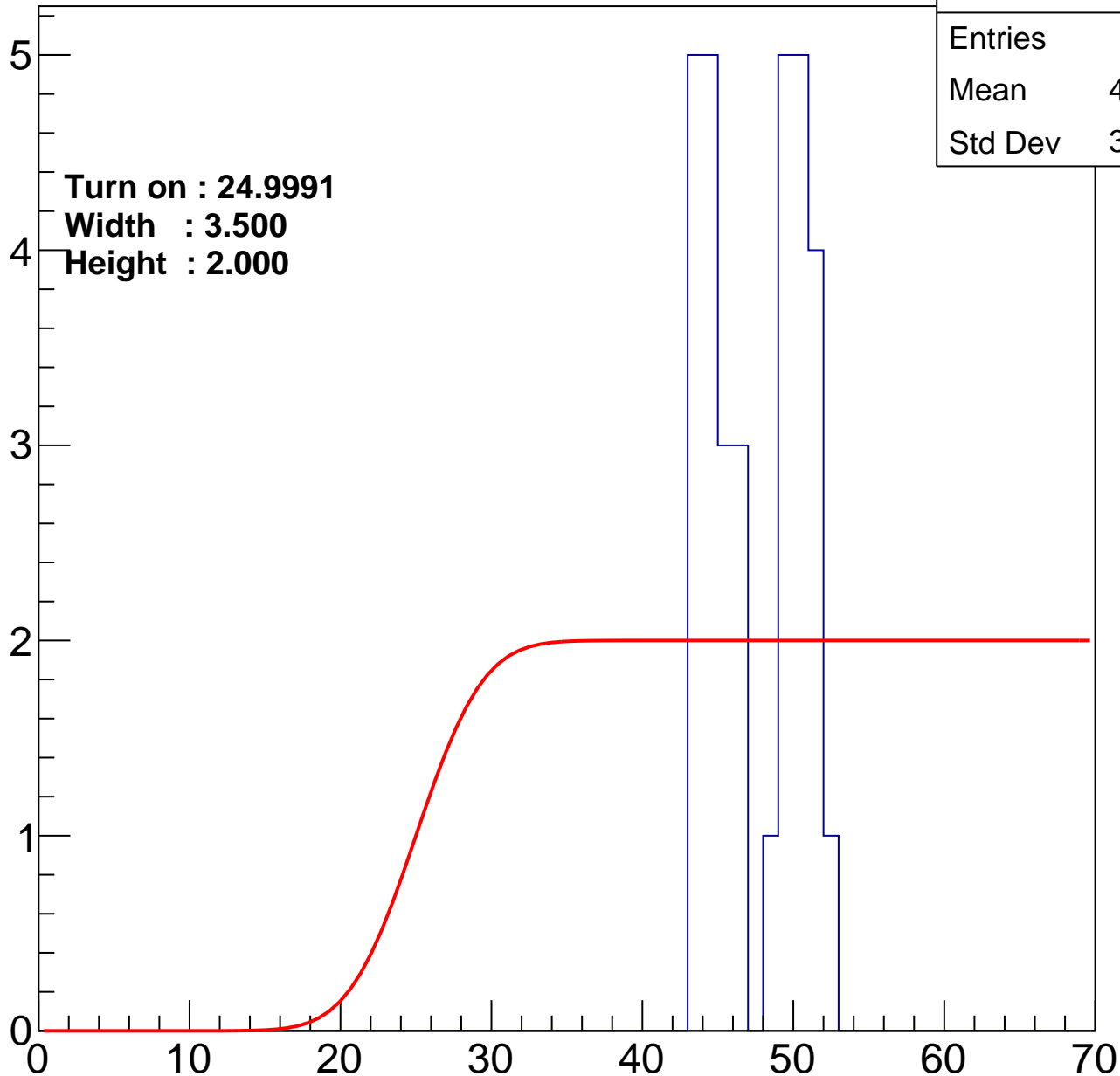
Entry

5
4
3
2
1
0

Turn on : 24.9991
Width : 3.500
Height : 2.000

Entries	32
Mean	47.09
Std Dev	3.035

ampl



B0L100S, U13-ch91

calib_packv5_042523_0143.root, FC#6, port A1

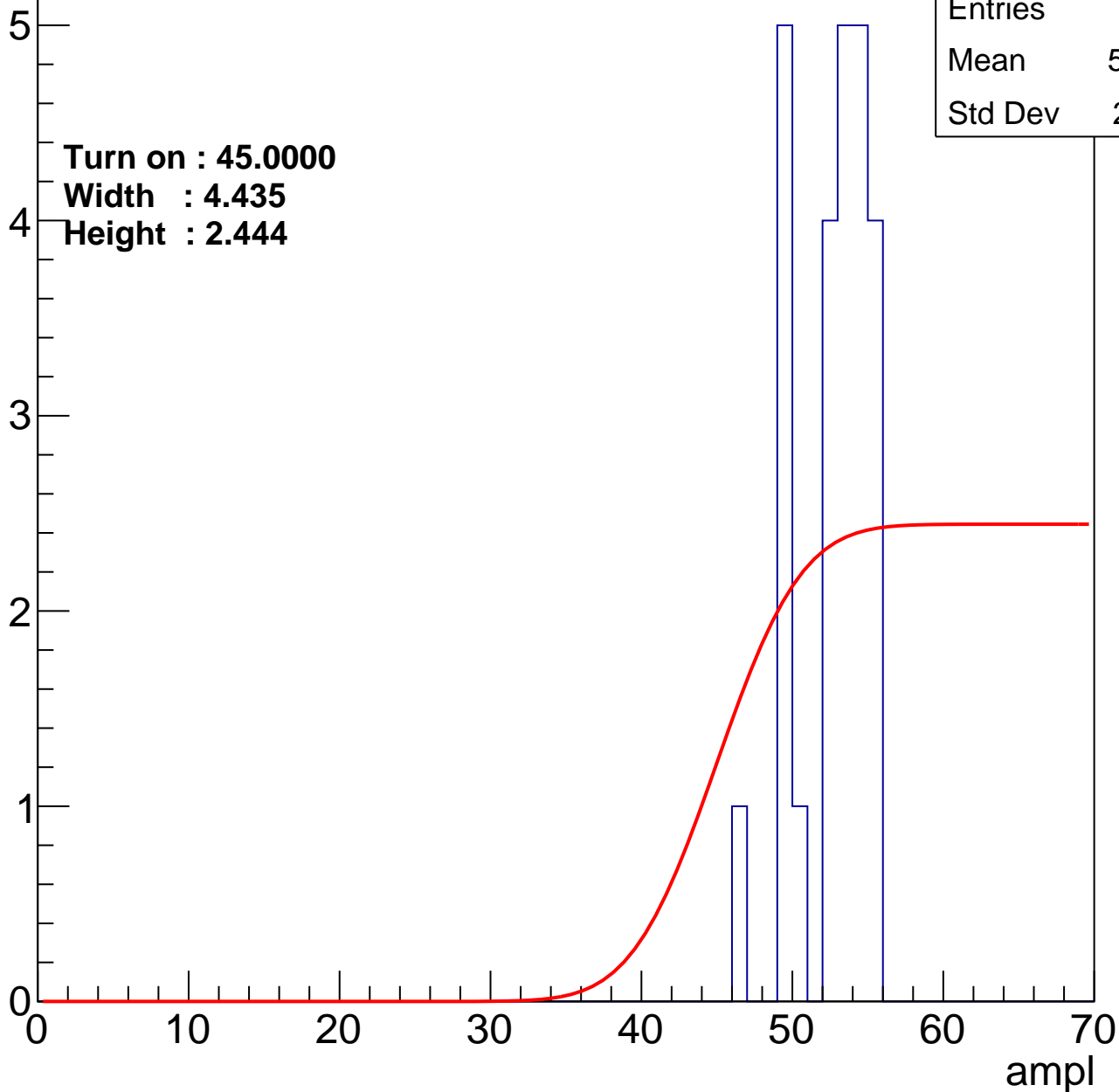
Entry

Entries	25
Mean	52.16
Std Dev	2.411

Turn on : 45.0000

Width : 4.435

Height : 2.444



B0L100S, U13-ch92

calib_packv5_042523_0143.root, FC#6, port A1

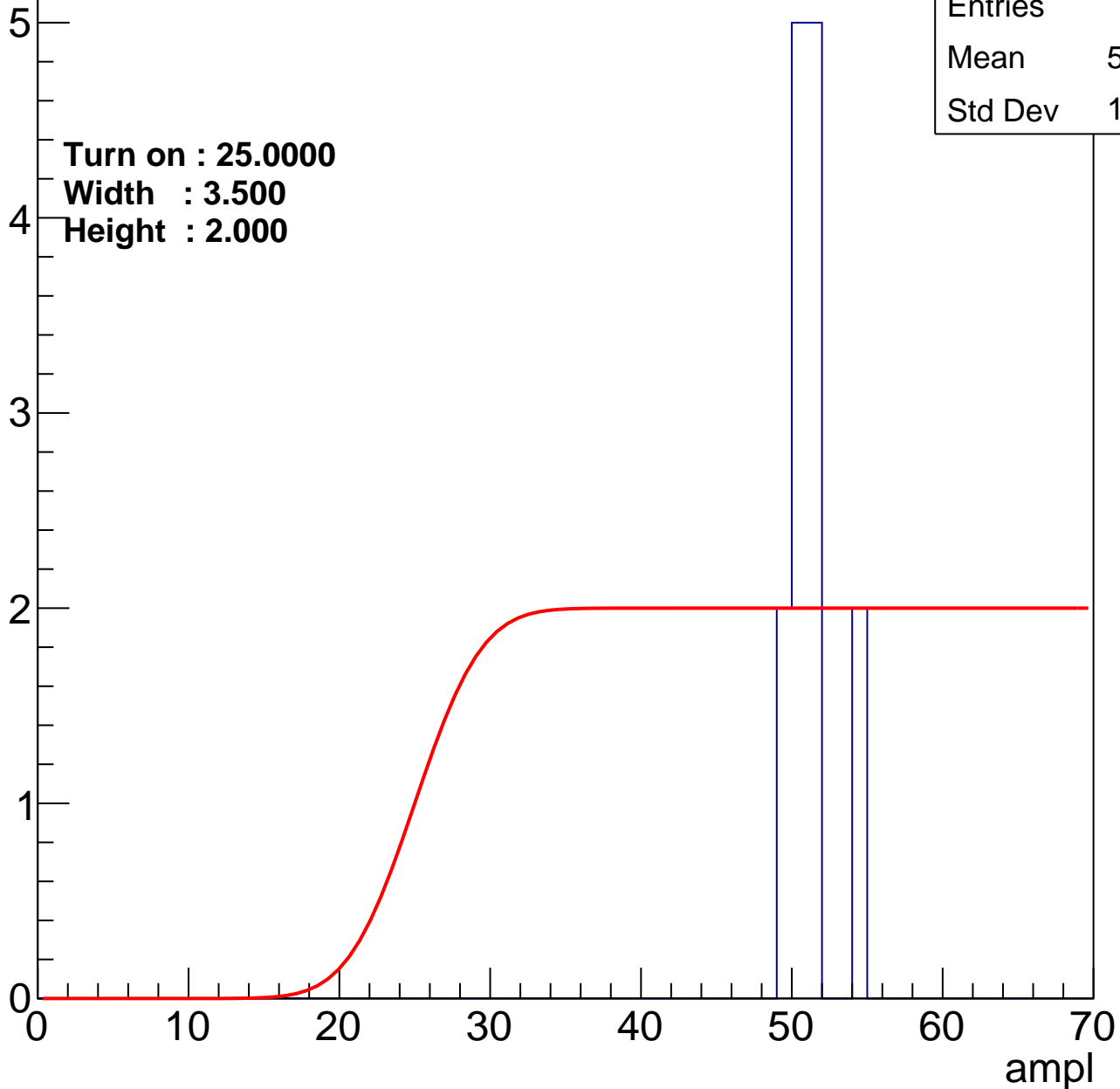
Entry

Entries	14
Mean	50.79
Std Dev	1.473

Turn on : 25.0000

Width : 3.500

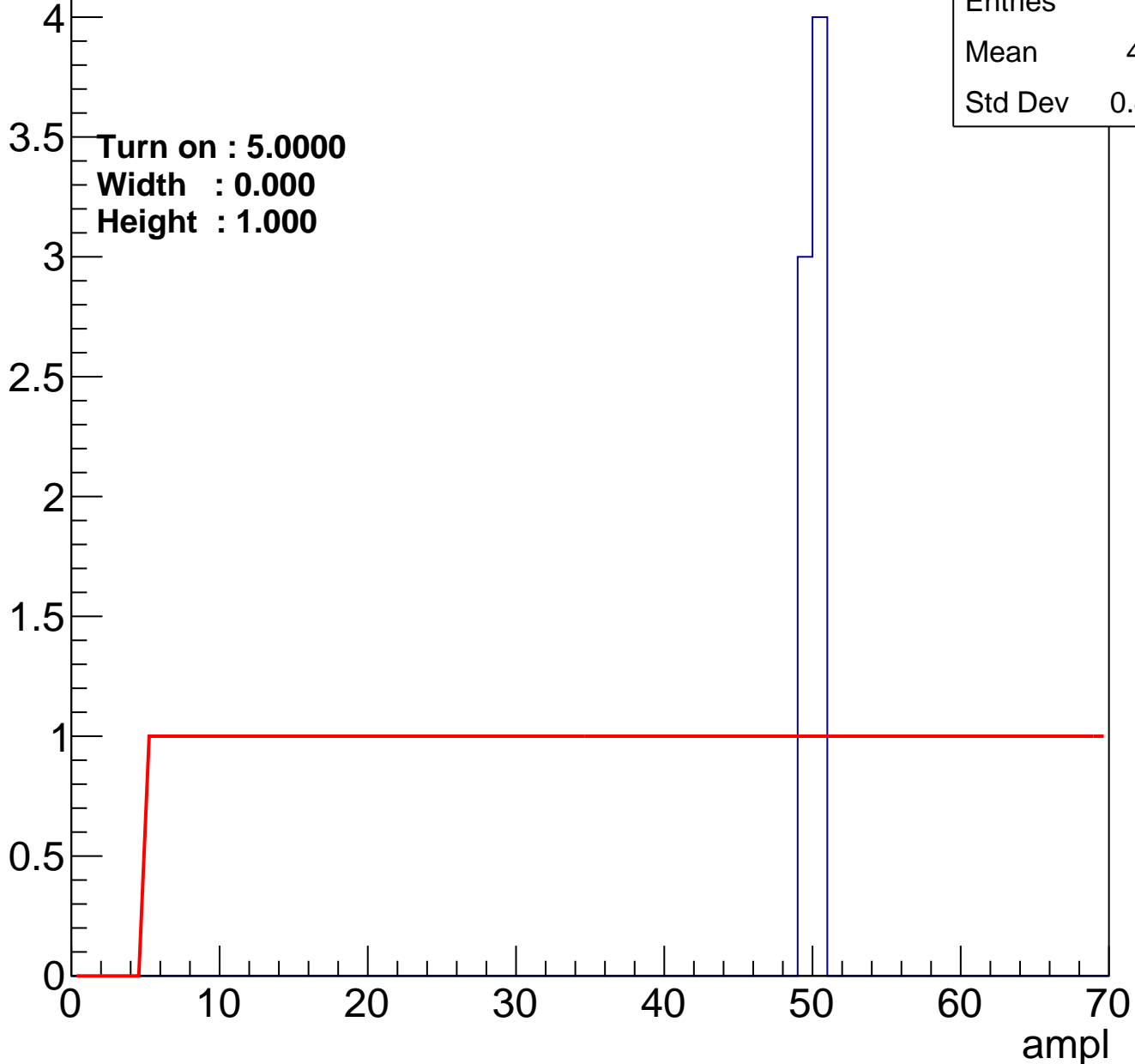
Height : 2.000



B0L100S, U13-ch93

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch94

calib_packv5_042523_0143.root, FC#6, port A1

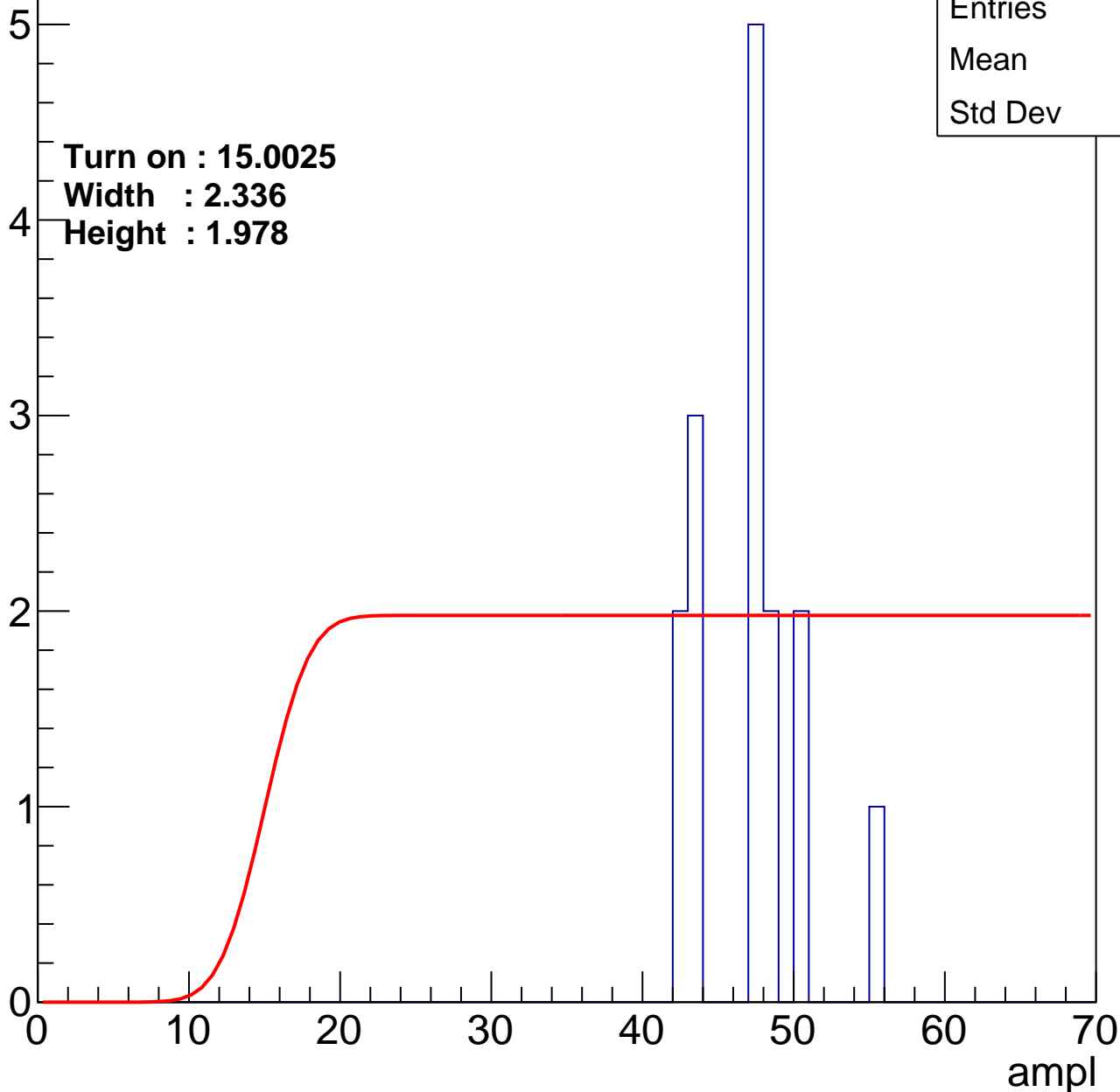
Entry

Entries	15
Mean	46.6
Std Dev	3.46

Turn on : 15.0025

Width : 2.336

Height : 1.978



B0L100S, U13-ch95

calib_packv5_042523_0143.root, FC#6, port A1

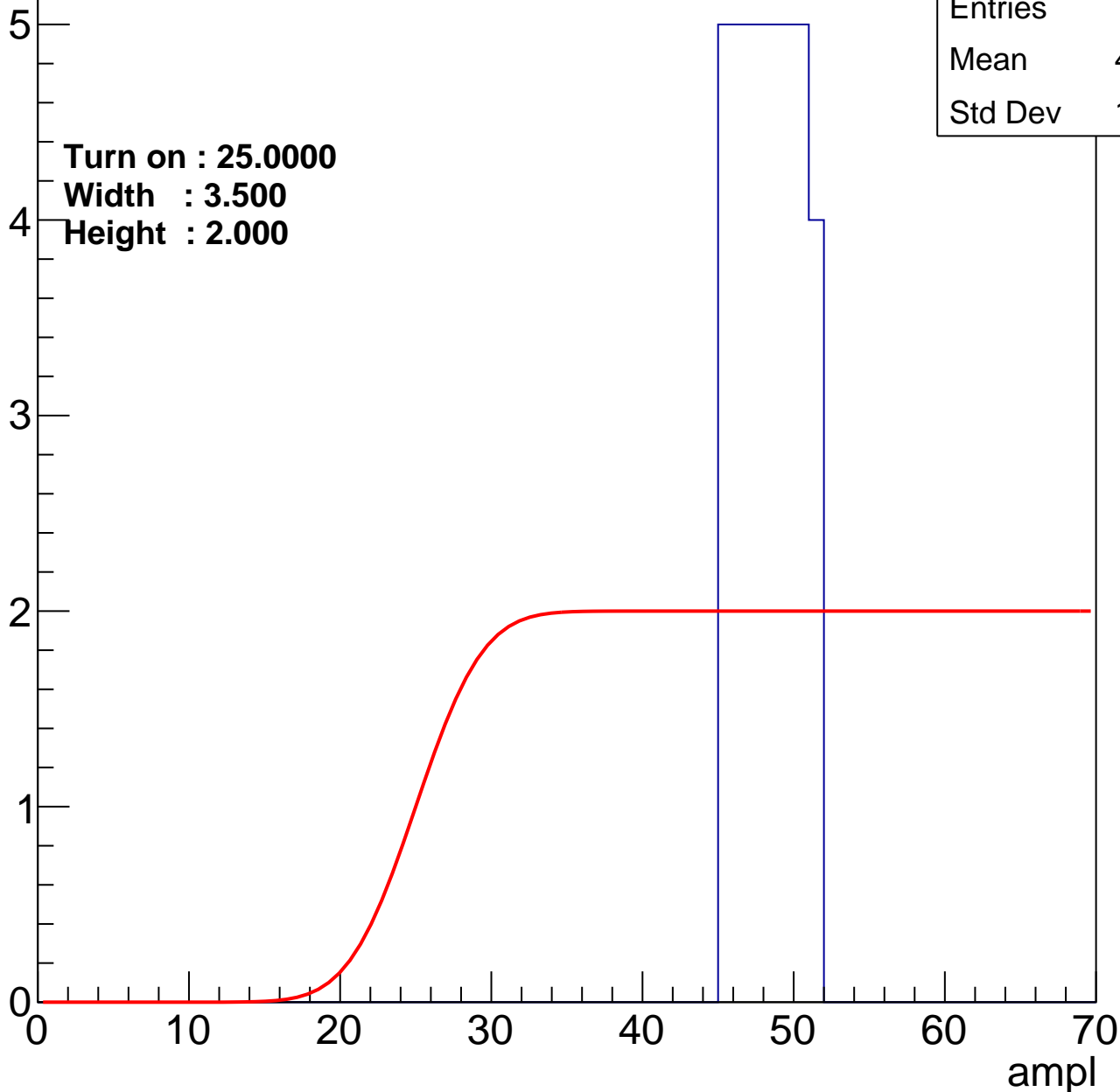
Entry

Entries	34
Mean	47.91
Std Dev	1.961

Turn on : 25.0000

Width : 3.500

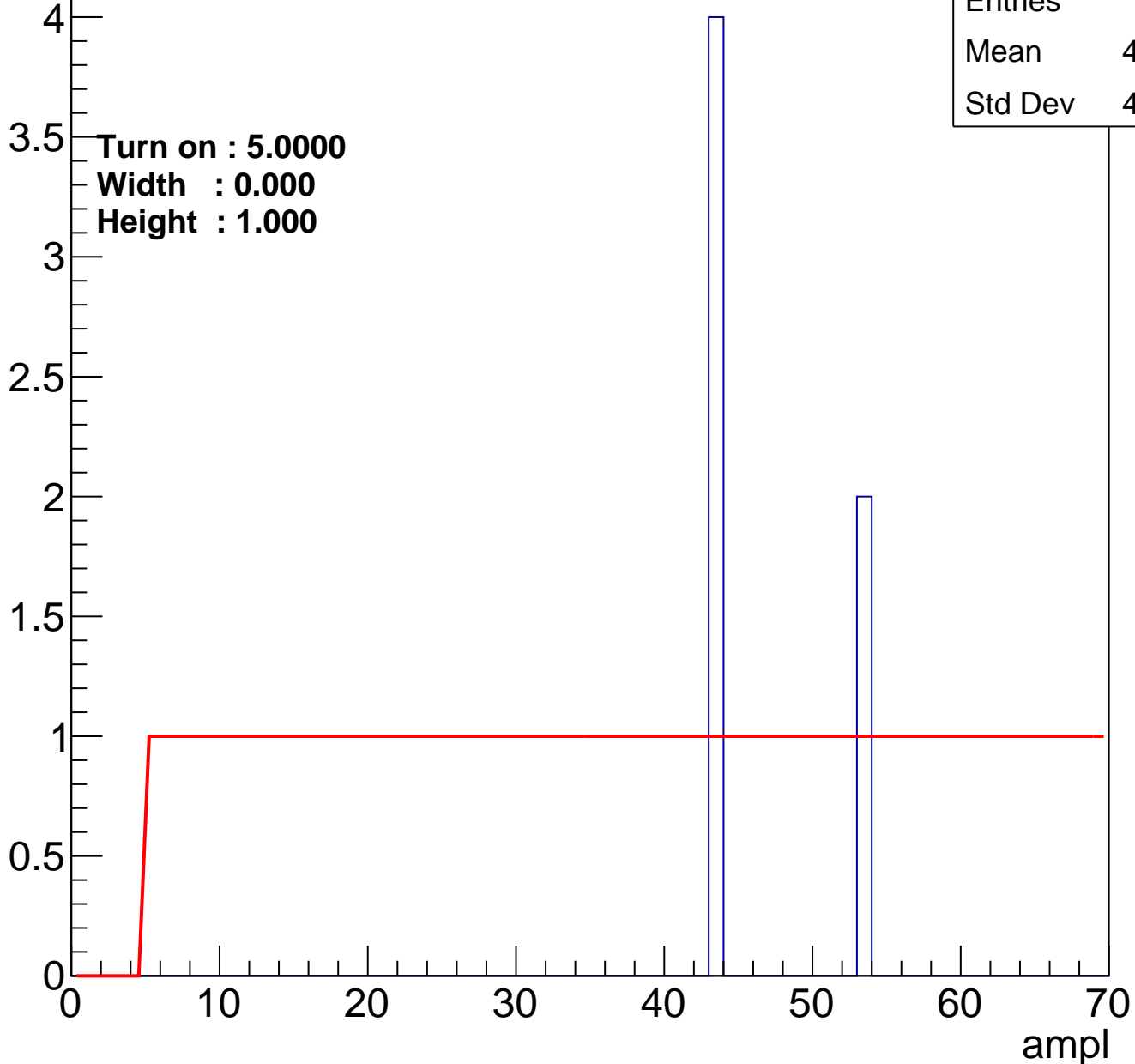
Height : 2.000



B0L100S, U13-ch96

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Turn on : 5.0000

Width : 0.000

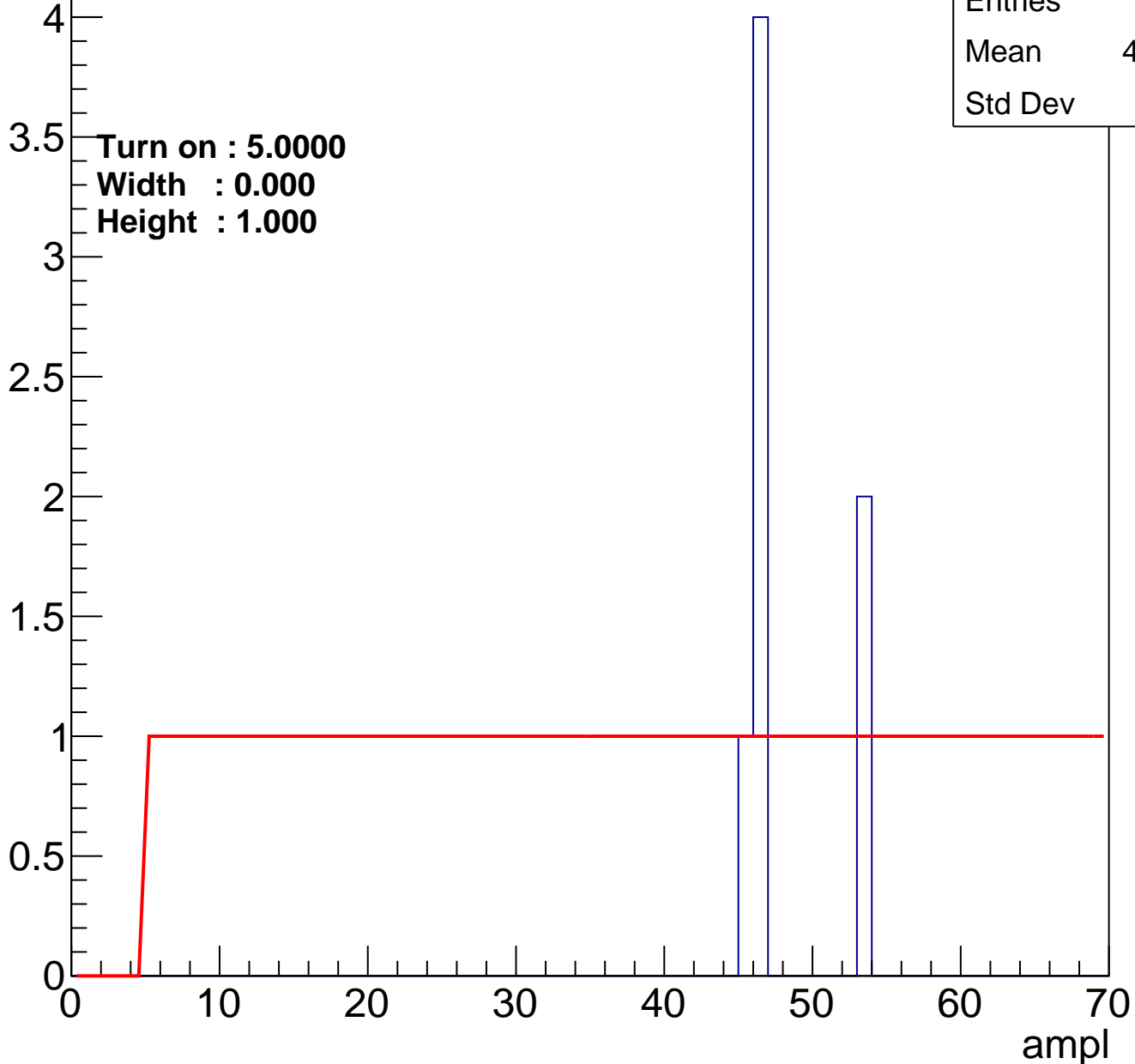
Height : 1.000

Entries	6
Mean	46.33
Std Dev	4.714

B0L100S, U13-ch97

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	7
Mean	47.86
Std Dev	3.27

B0L100S, U13-ch98

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch99

calib_packv5_042523_0143.root, FC#6, port A1

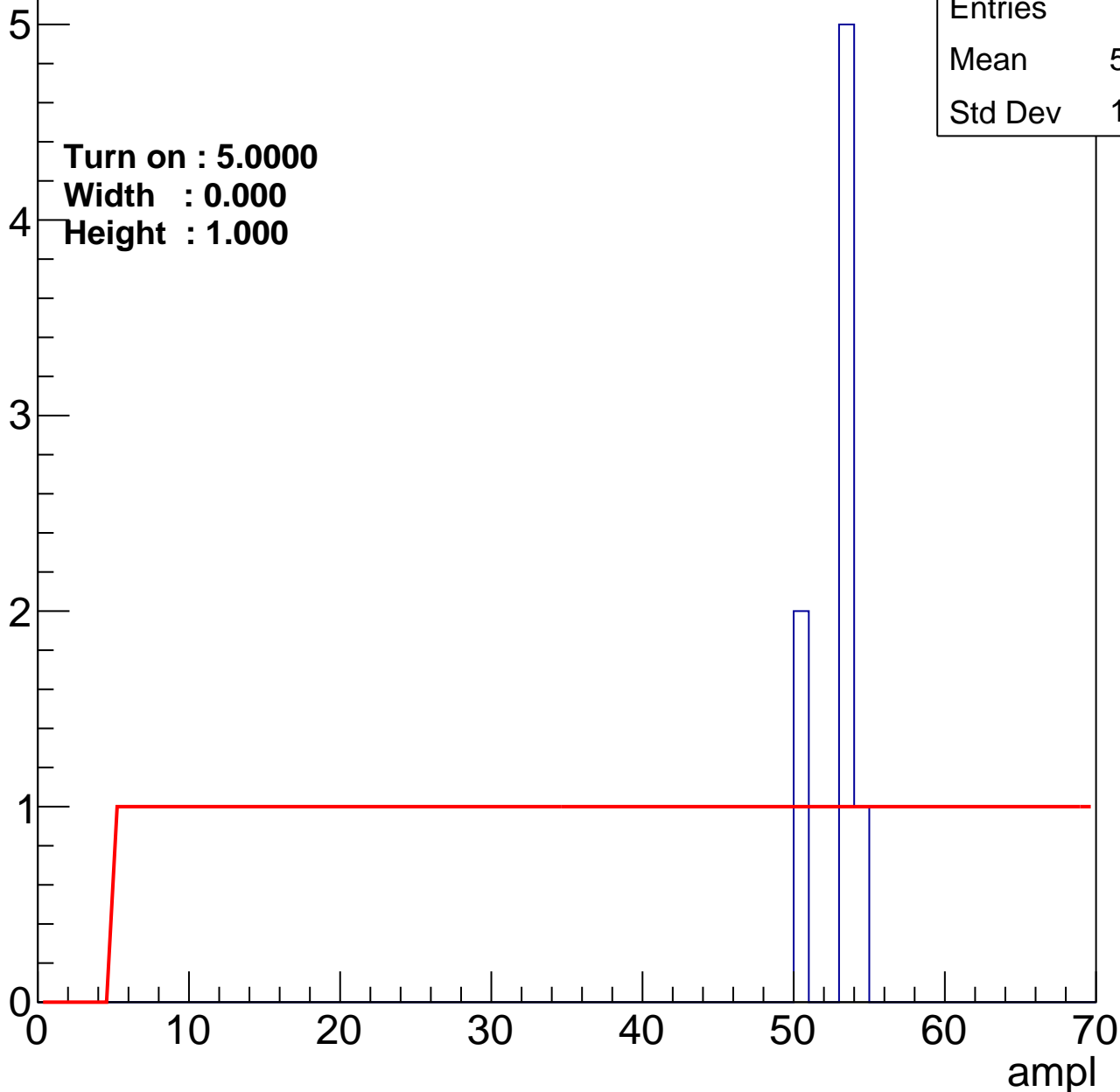
Entry

Entries	8
Mean	52.38
Std Dev	1.409

Turn on : 5.0000

Width : 0.000

Height : 1.000



B0L100S, U13-ch100

calib_packv5_042523_0143.root, FC#6, port A1

Entry

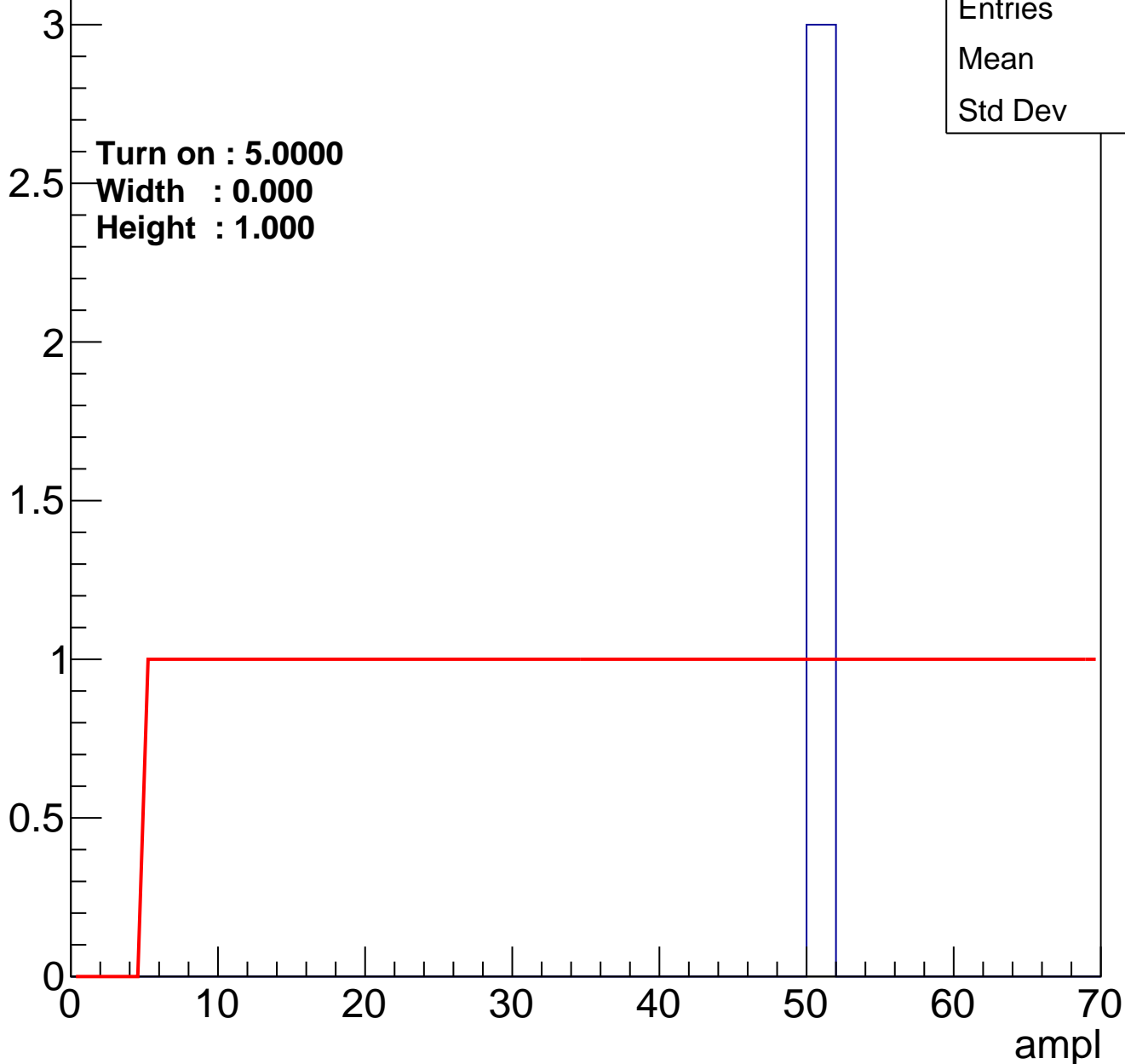


Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch101

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch102

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U13-ch103

calib_packv5_042523_0143.root, FC#6, port A1

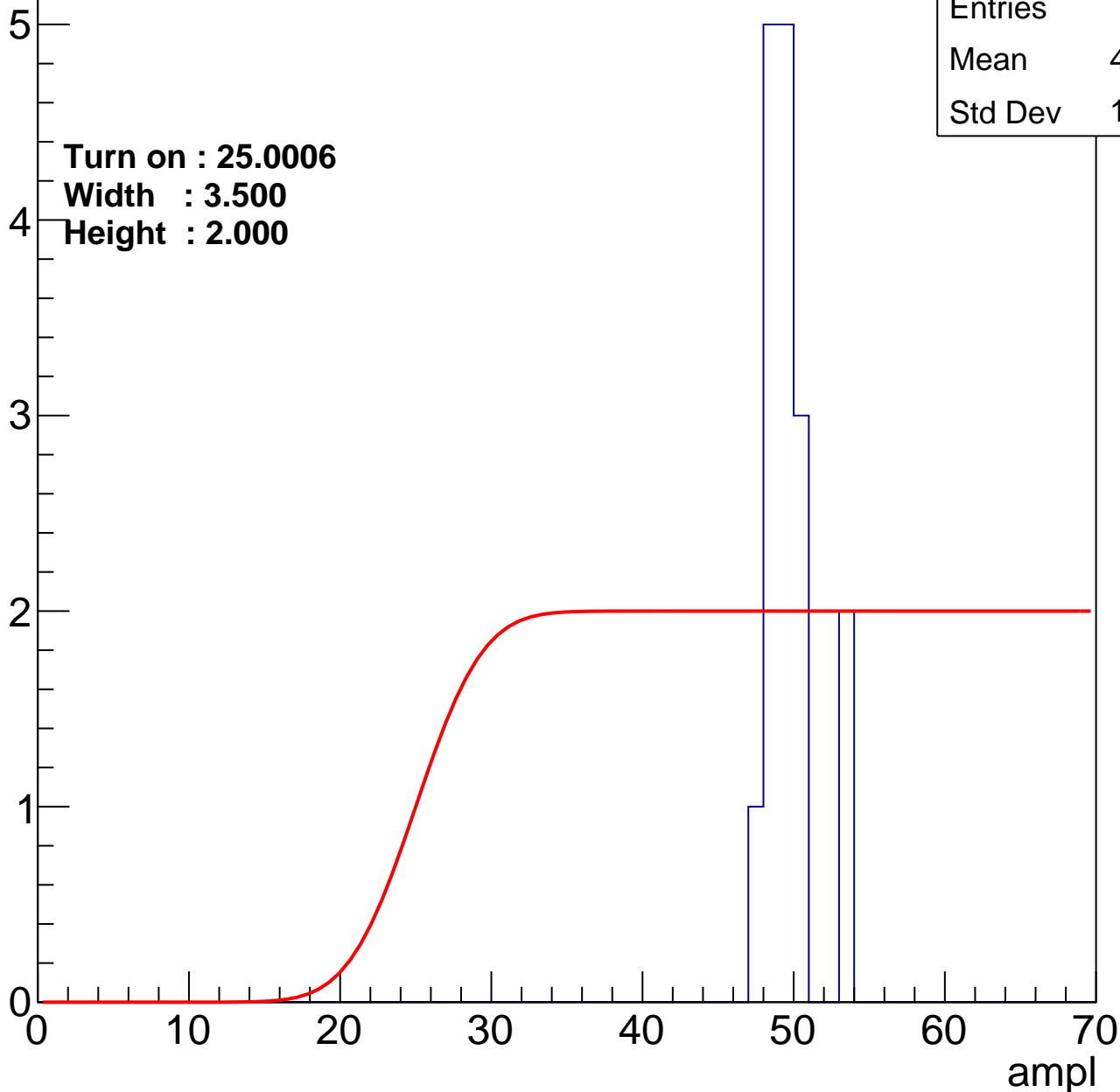
Entry

Entries	16
Mean	49.25
Std Dev	1.639

Turn on : 25.0006

Width : 3.500

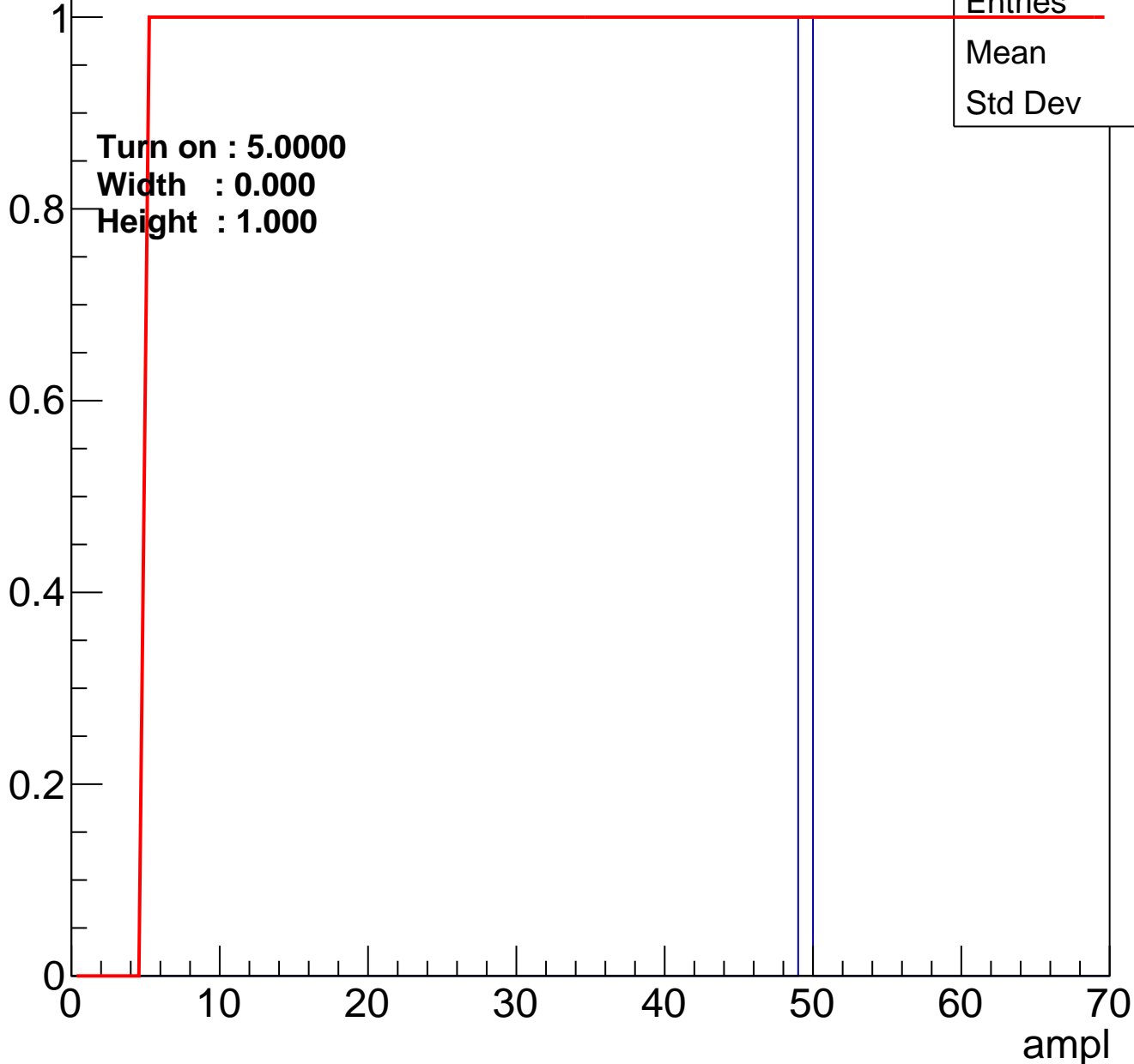
Height : 2.000



B0L100S, U13-ch104

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch105

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch106

calib_packv5_042523_0143.root, FC#6, port A1

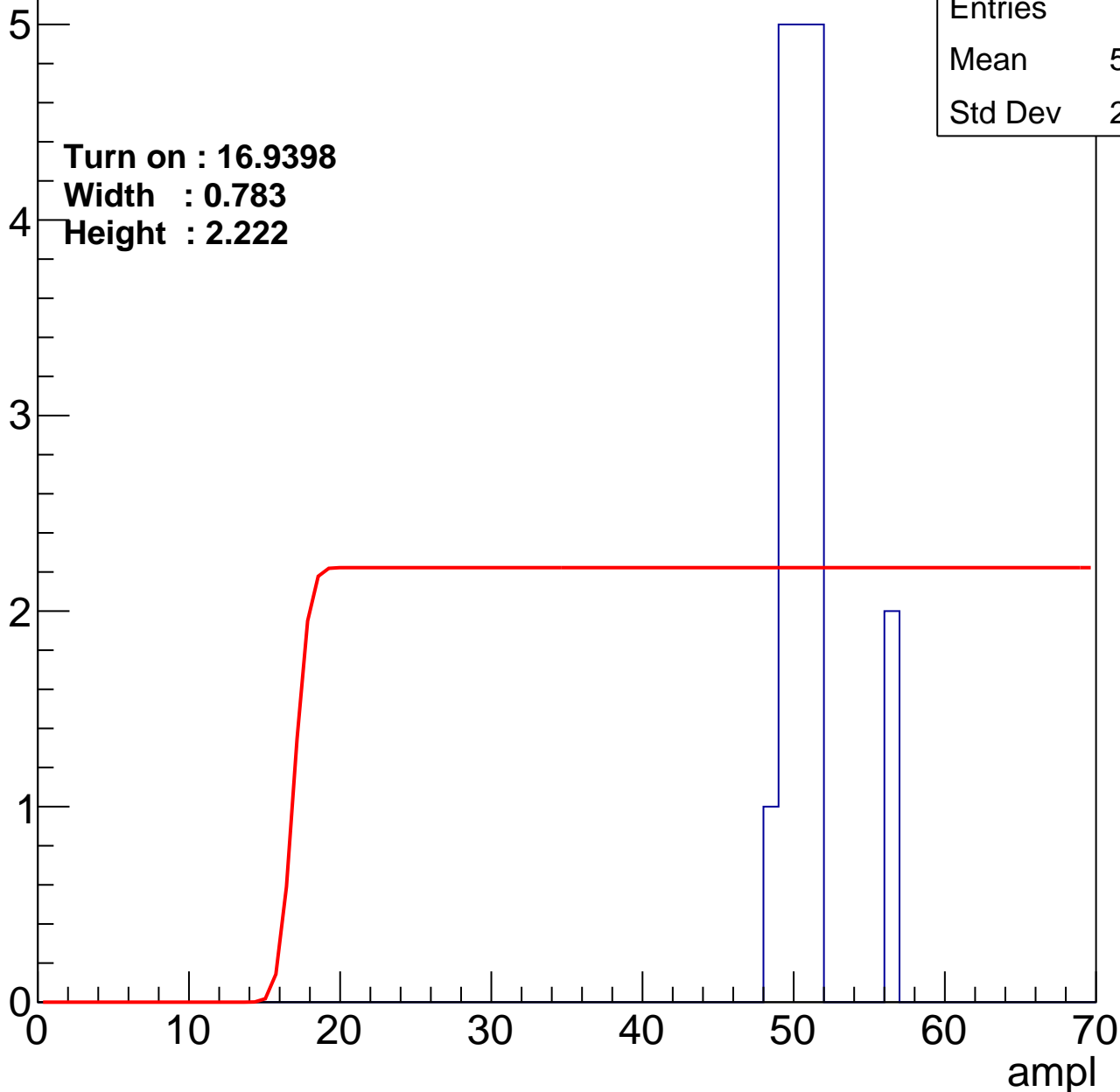
Entry

Entries	18
Mean	50.56
Std Dev	2.114

Turn on : 16.9398

Width : 0.783

Height : 2.222



B0L100S, U13-ch107

calib_packv5_042523_0143.root, FC#6, port A1

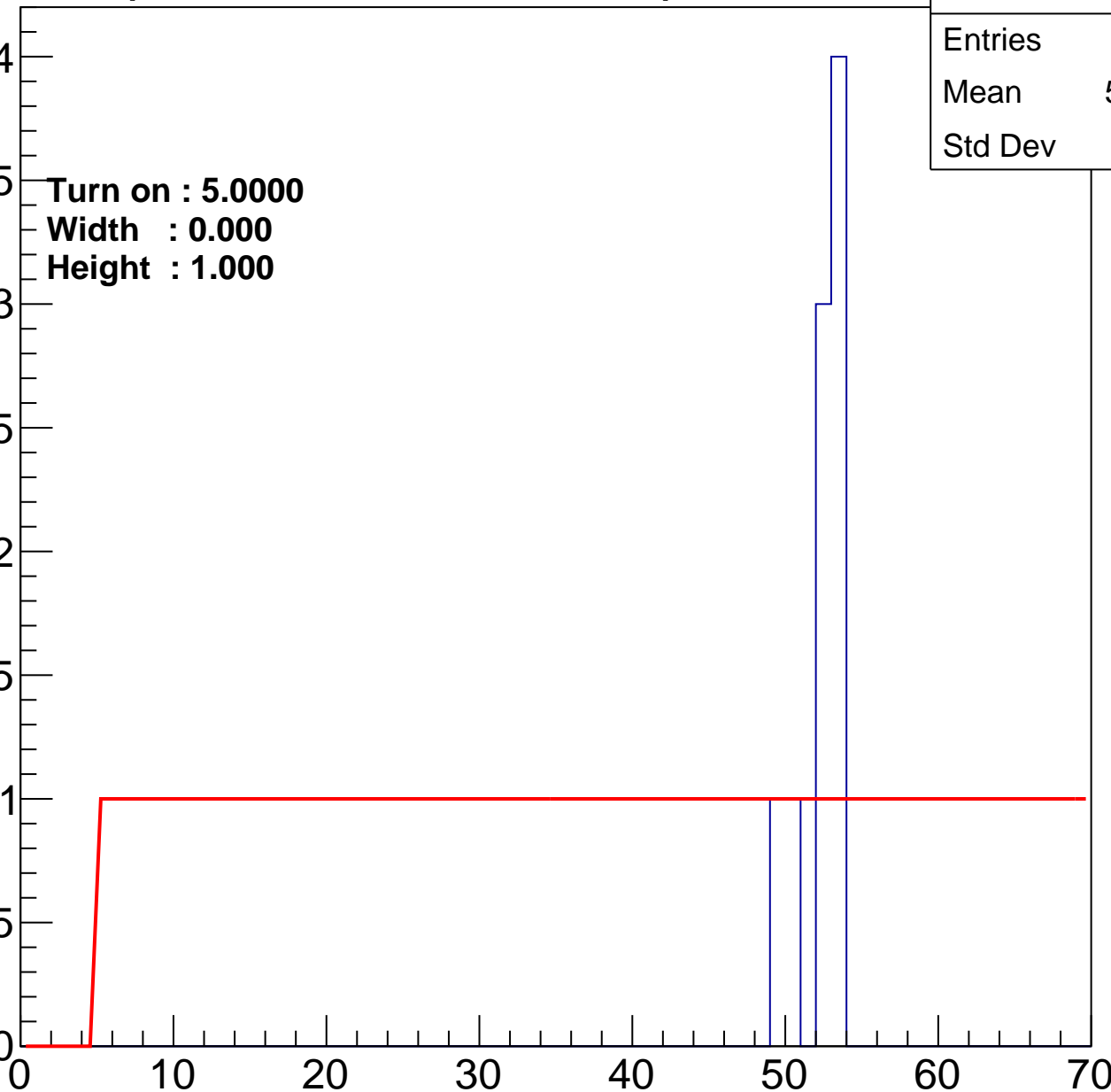
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	9
Mean	51.89
Std Dev	1.37

ampl



B0L100S, U13-ch108

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch109

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch110

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch111

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L100S, U13-ch112

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch113

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch114

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U13-ch115

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch116

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch117

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch118

calib_packv5_042523_0143.root, FC#6, port A1

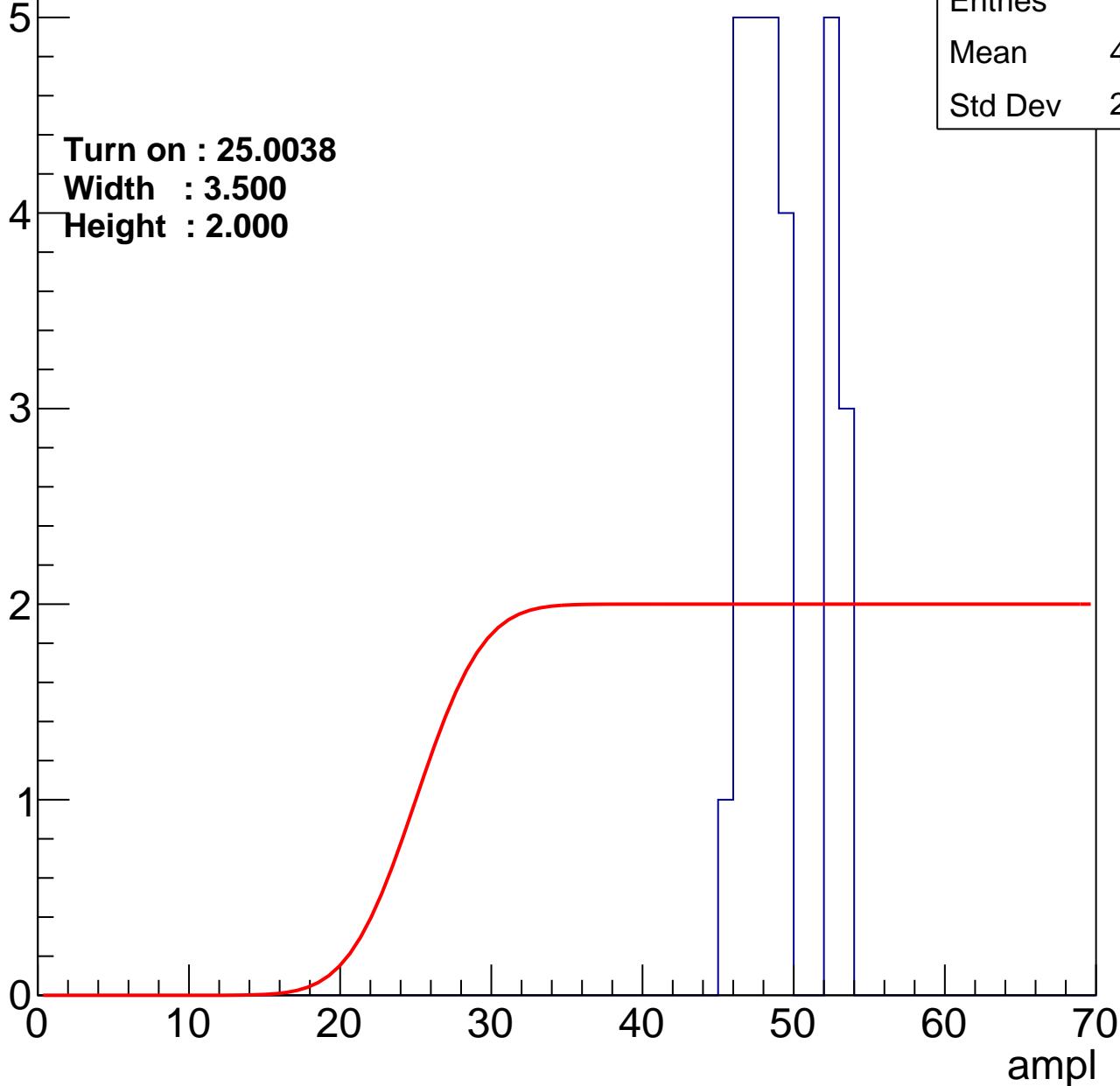
Entry

Entries	28
Mean	48.75
Std Dev	2.516

Turn on : 25.0038

Width : 3.500

Height : 2.000



B0L100S, U13-ch119

calib_packv5_042523_0143.root, FC#6, port A1

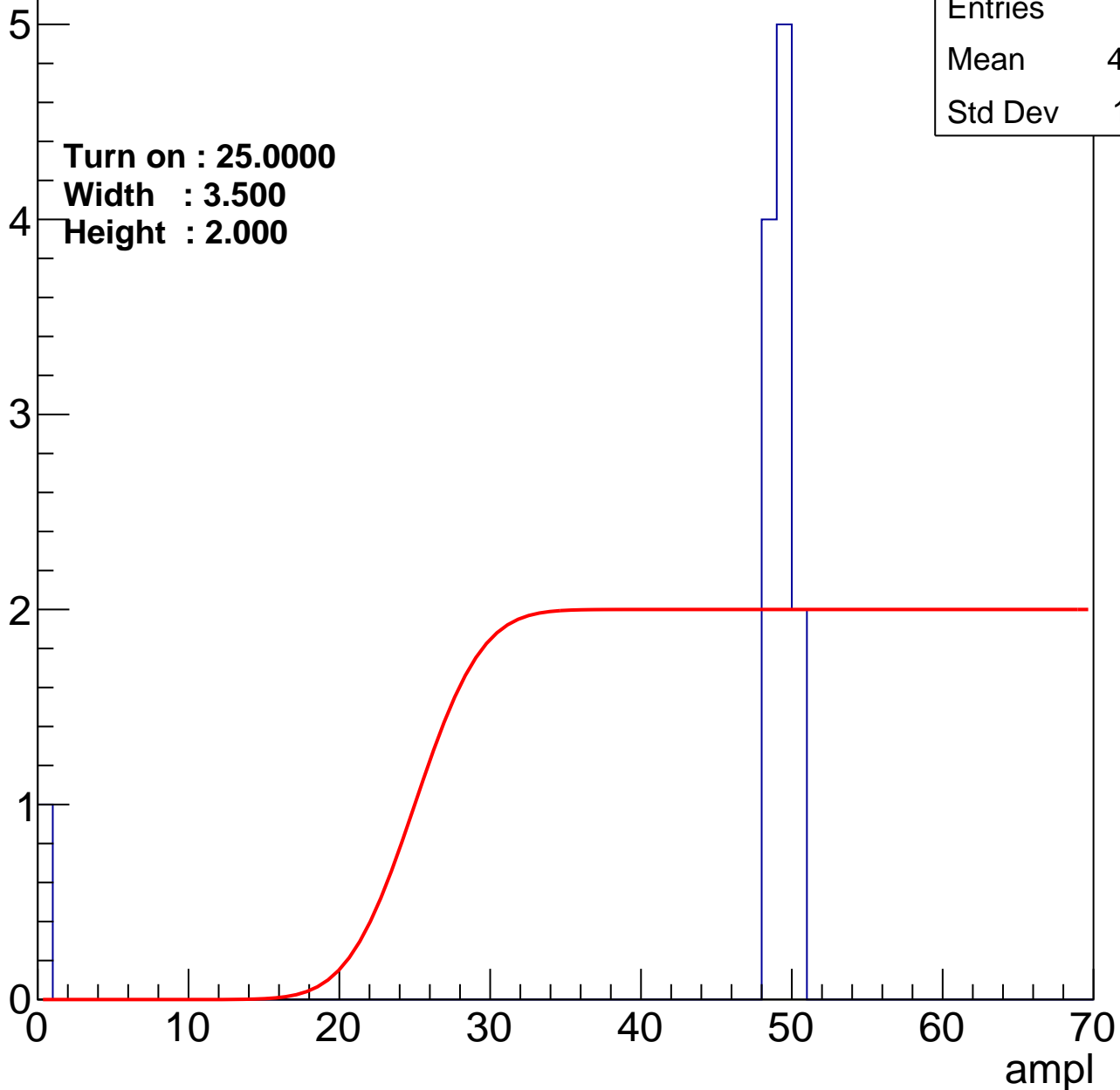
Entry

Entries	12
Mean	44.75
Std Dev	13.51

Turn on : 25.0000

Width : 3.500

Height : 2.000



B0L100S, U13-ch120

calib_packv5_042523_0143.root, FC#6, port A1

Entry

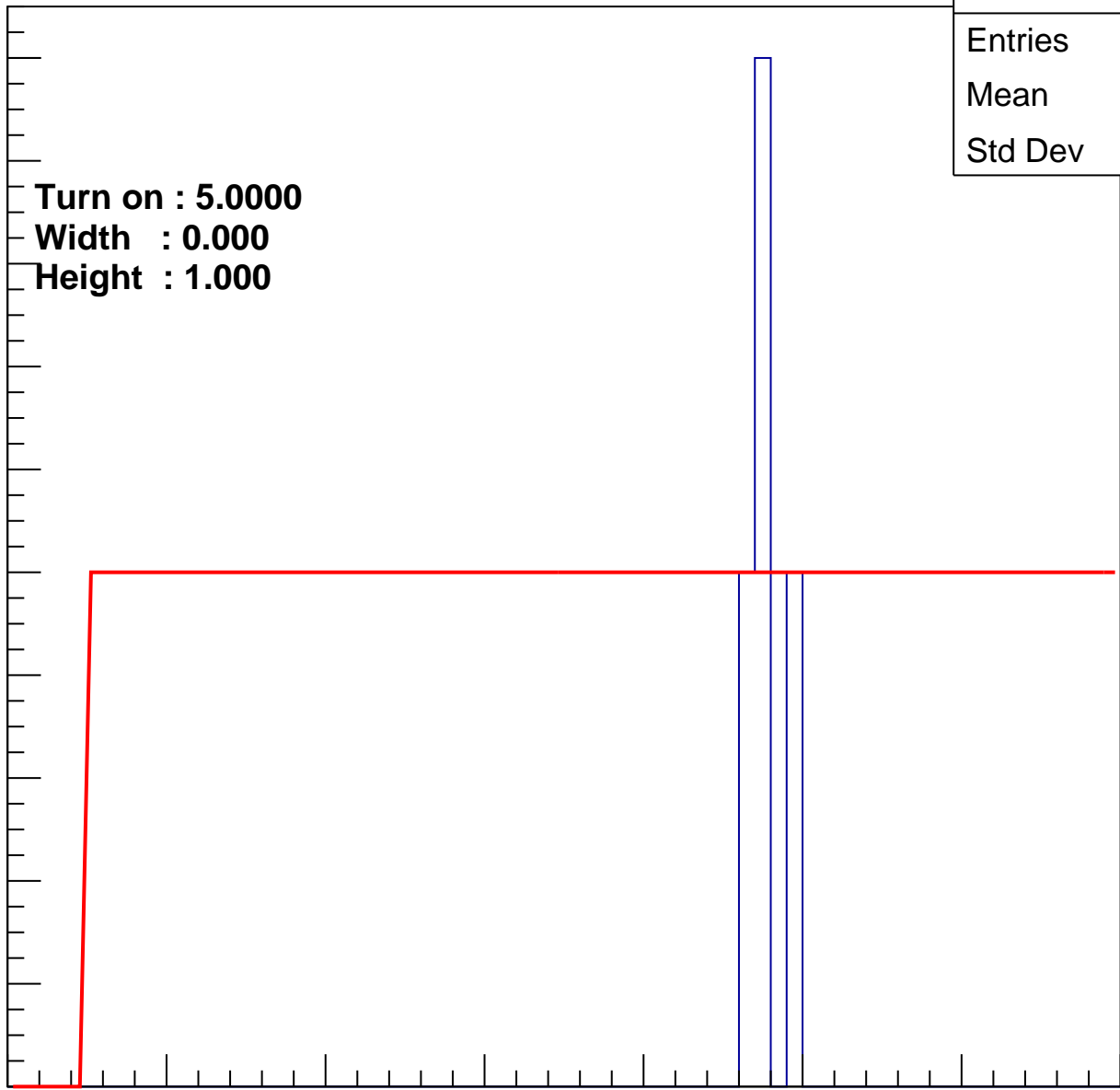
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	4
Mean	47.25
Std Dev	1.09

0 10 20 30 40 50 60 70

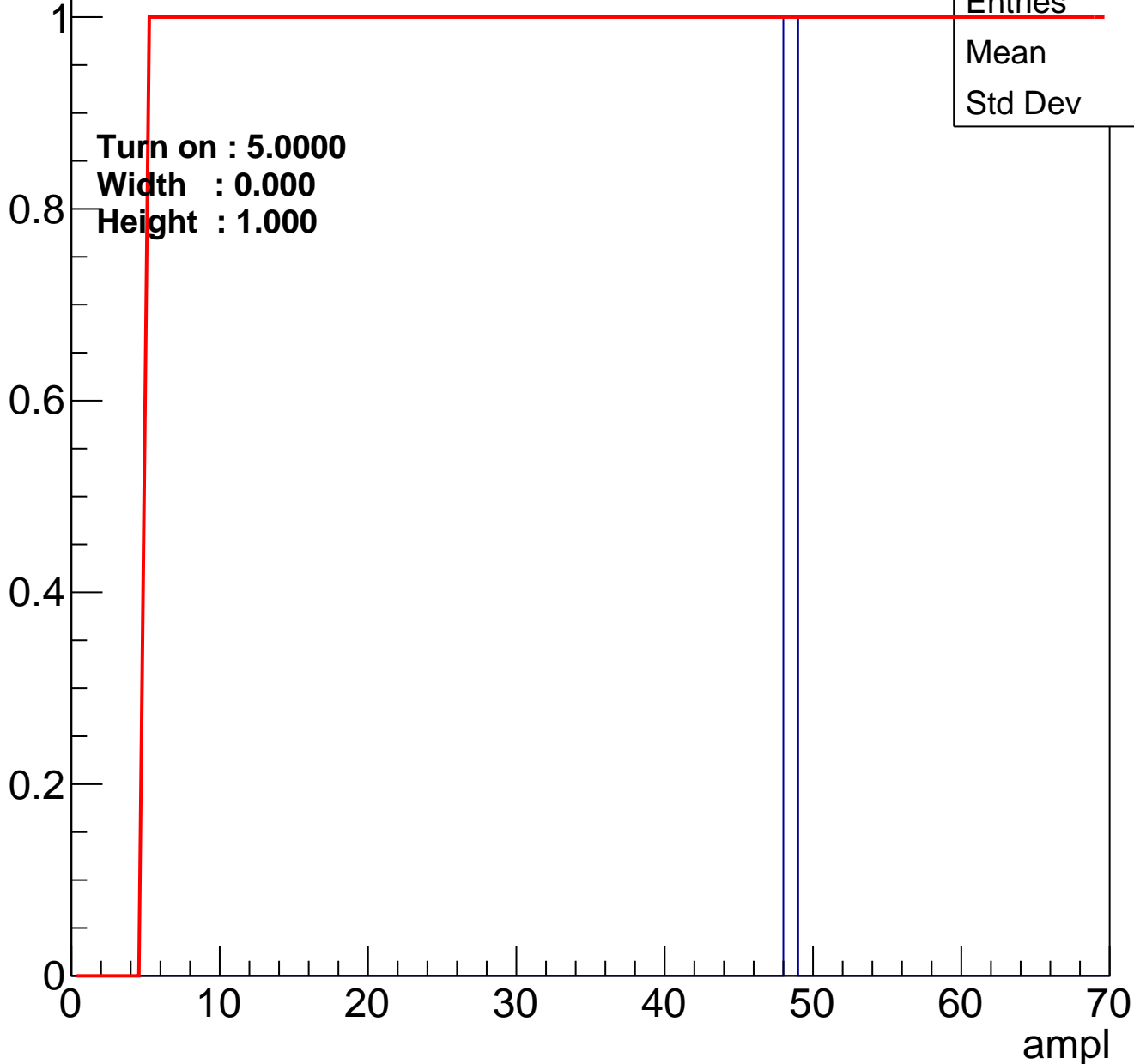
ampl



B0L100S, U13-ch121

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch122

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch123

calib_packv5_042523_0143.root, FC#6, port A1

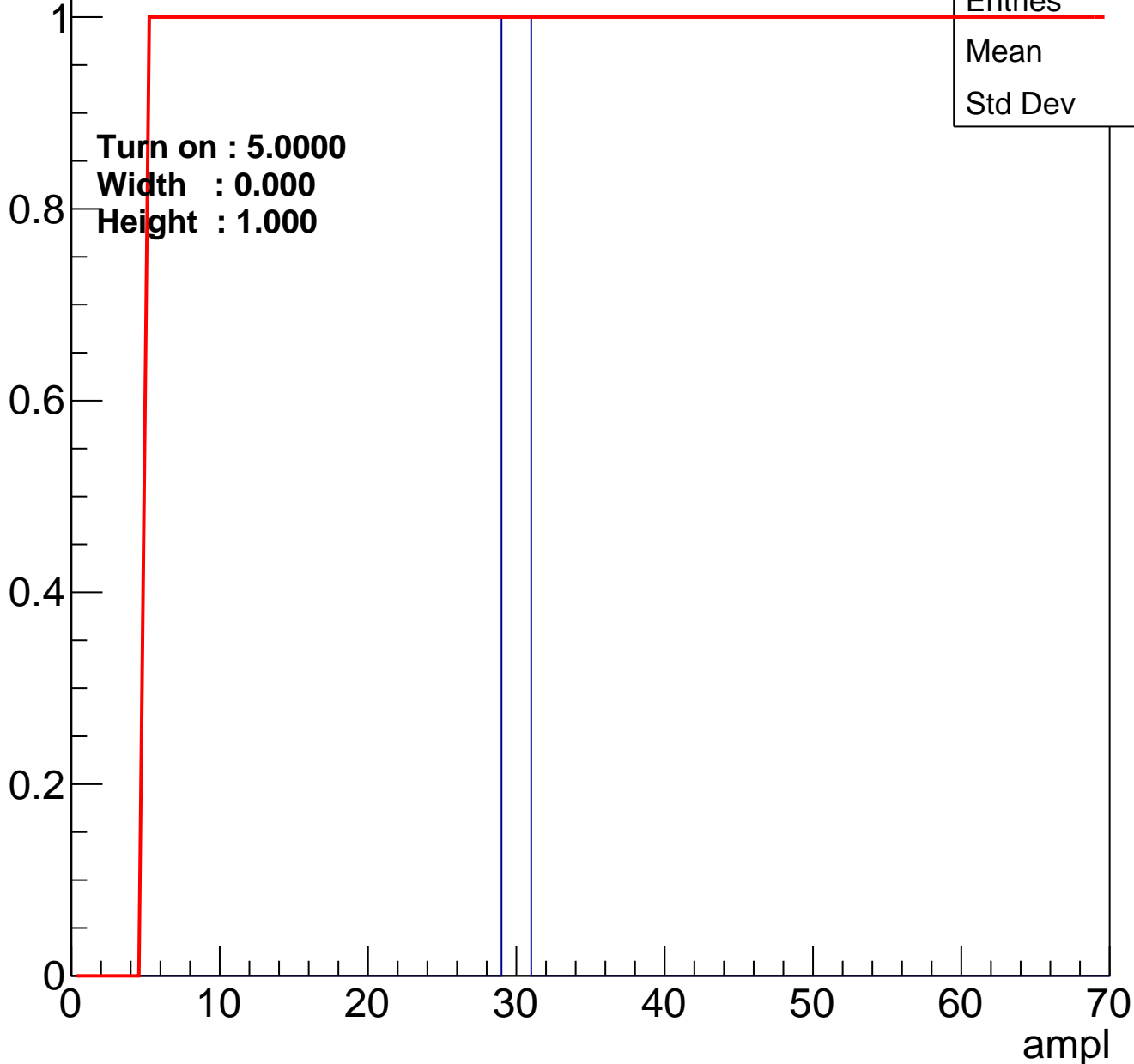
Entry



B0L100S, U13-ch124

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U13-ch125

calib_packv5_042523_0143.root, FC#6, port A1

Entry

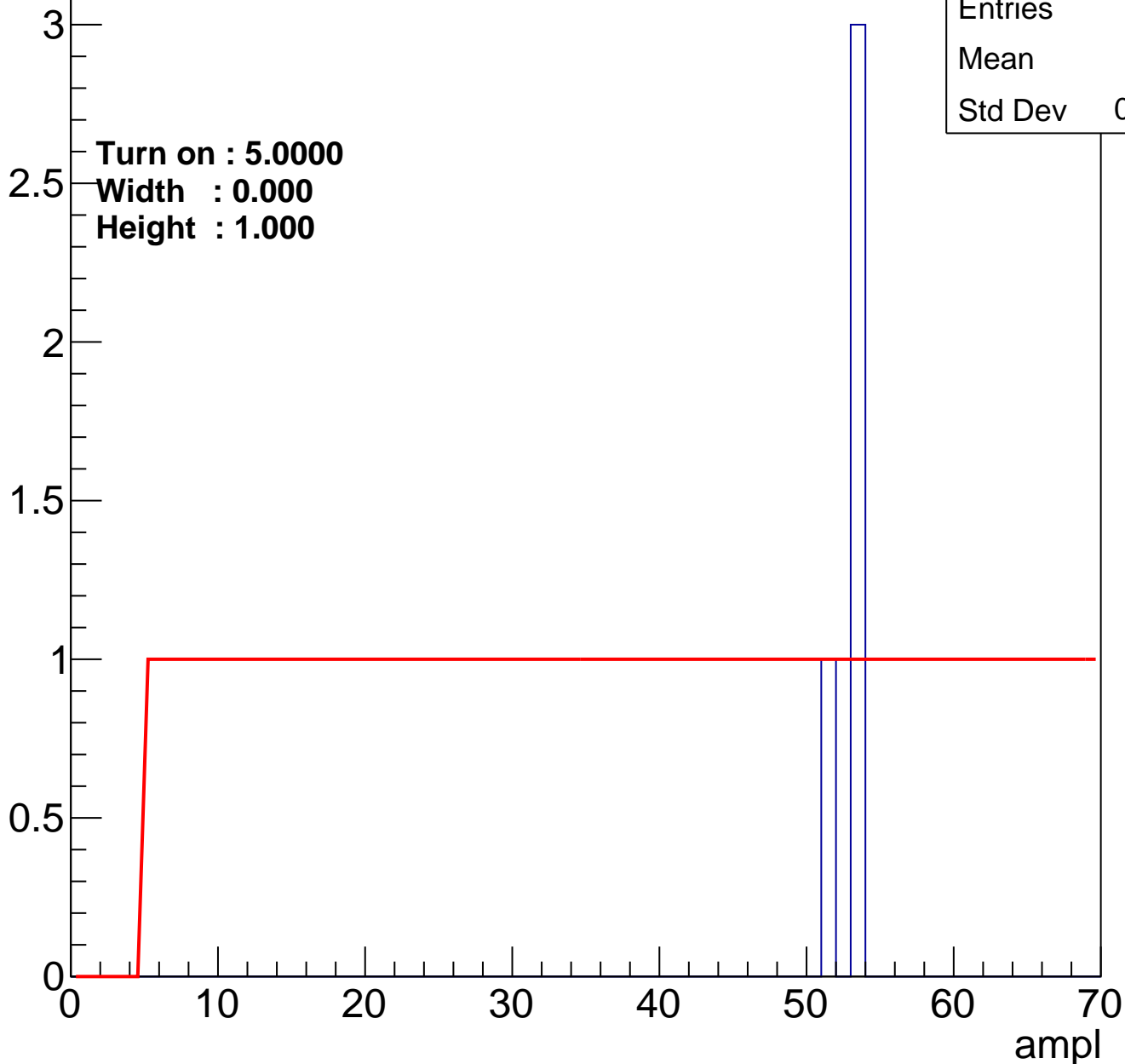


Entries	0
Mean	0
Std Dev	0

B0L100S, U13-ch126

calib_packv5_042523_0143.root, FC#6, port A1

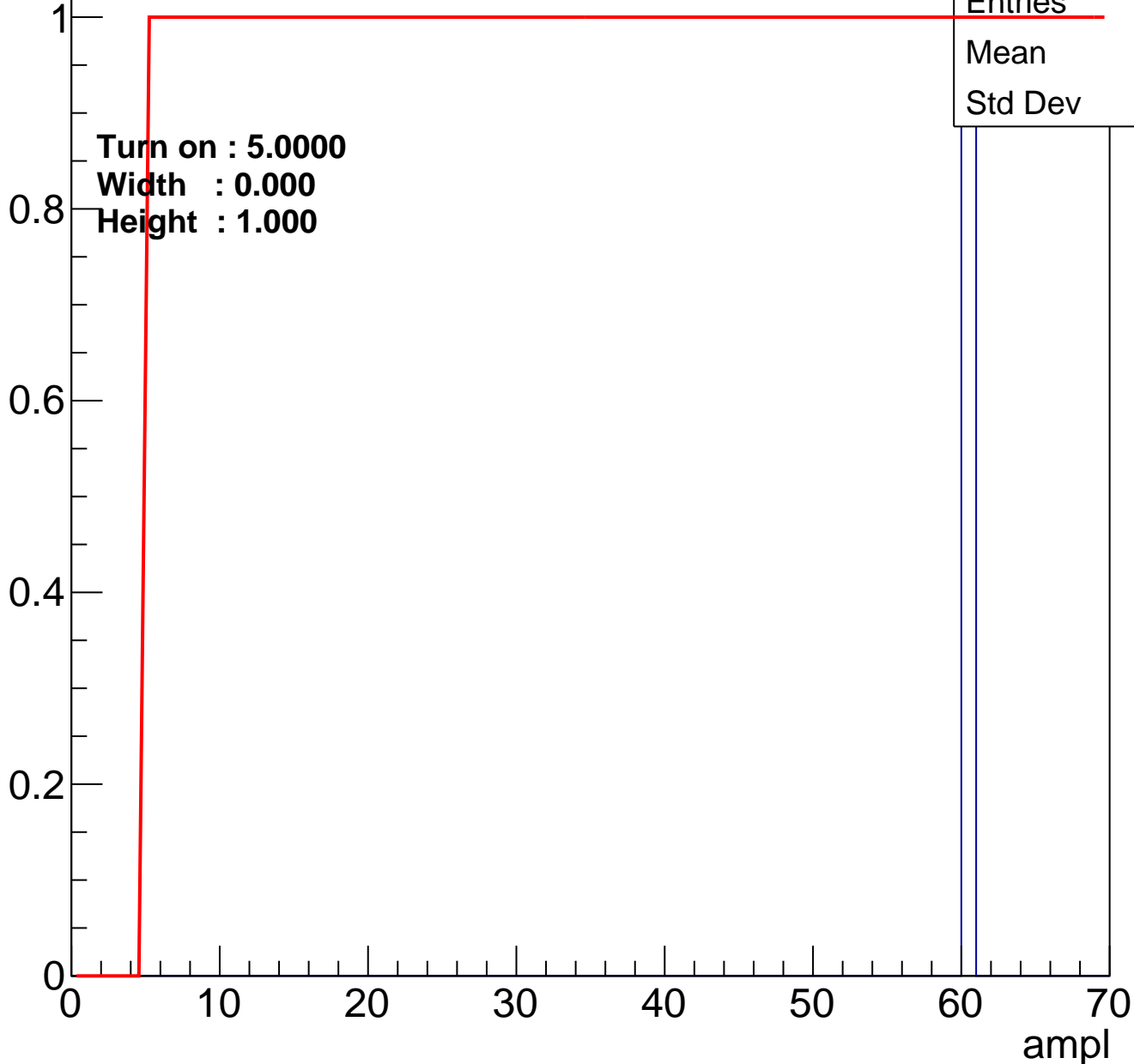
Entry



B0L100S, U13-ch127

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	60
Std Dev	0

B0L100S, U13-ch127

calib_packv5_042523_0143.root, FC#6, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	1
Mean	60
Std Dev	0

ampl

0 10 20 30 40 50 60 70