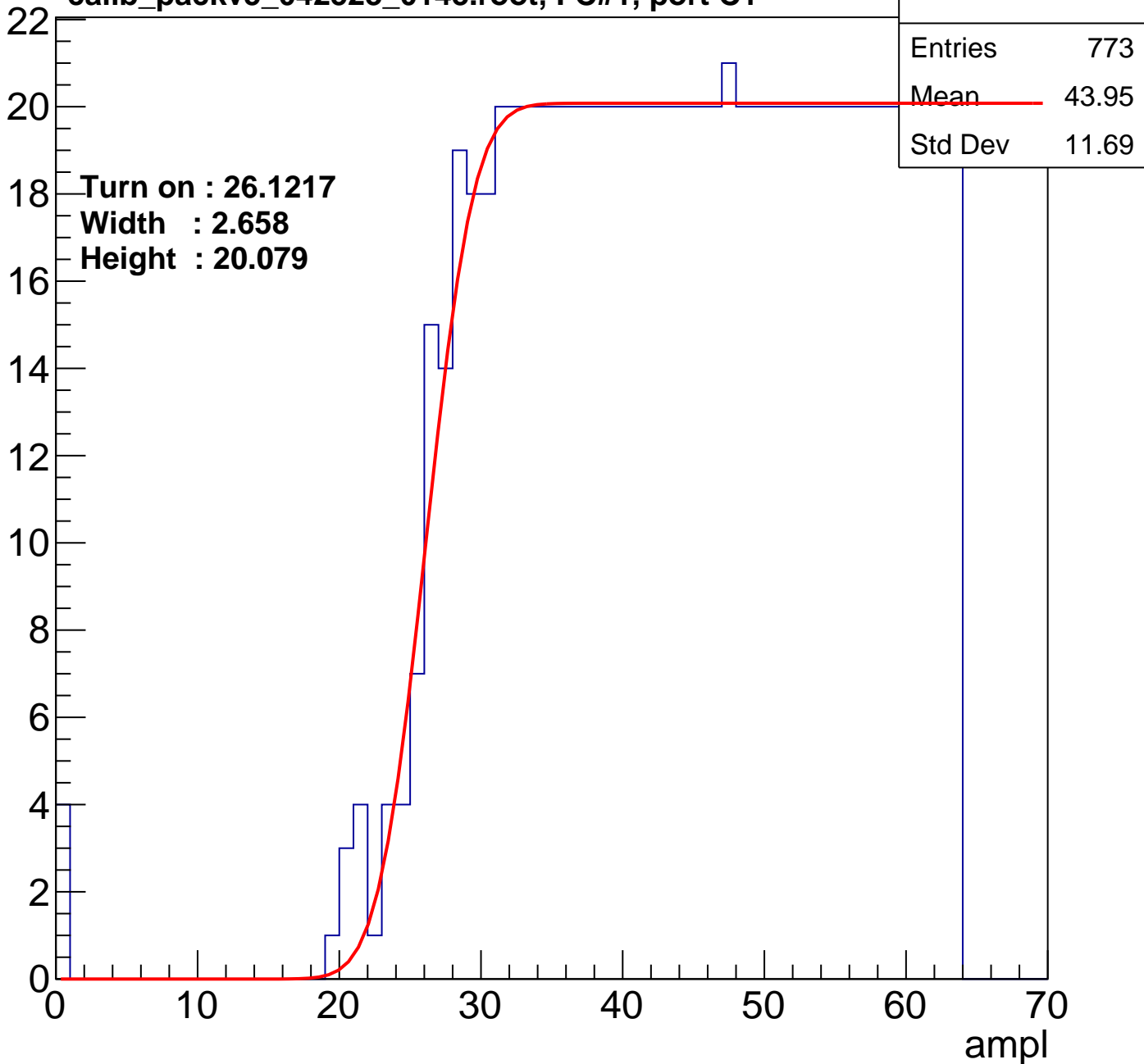


B0L101S, U21-ch0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch1

calib_packv5_042523_0143.root, FC#1, port C1

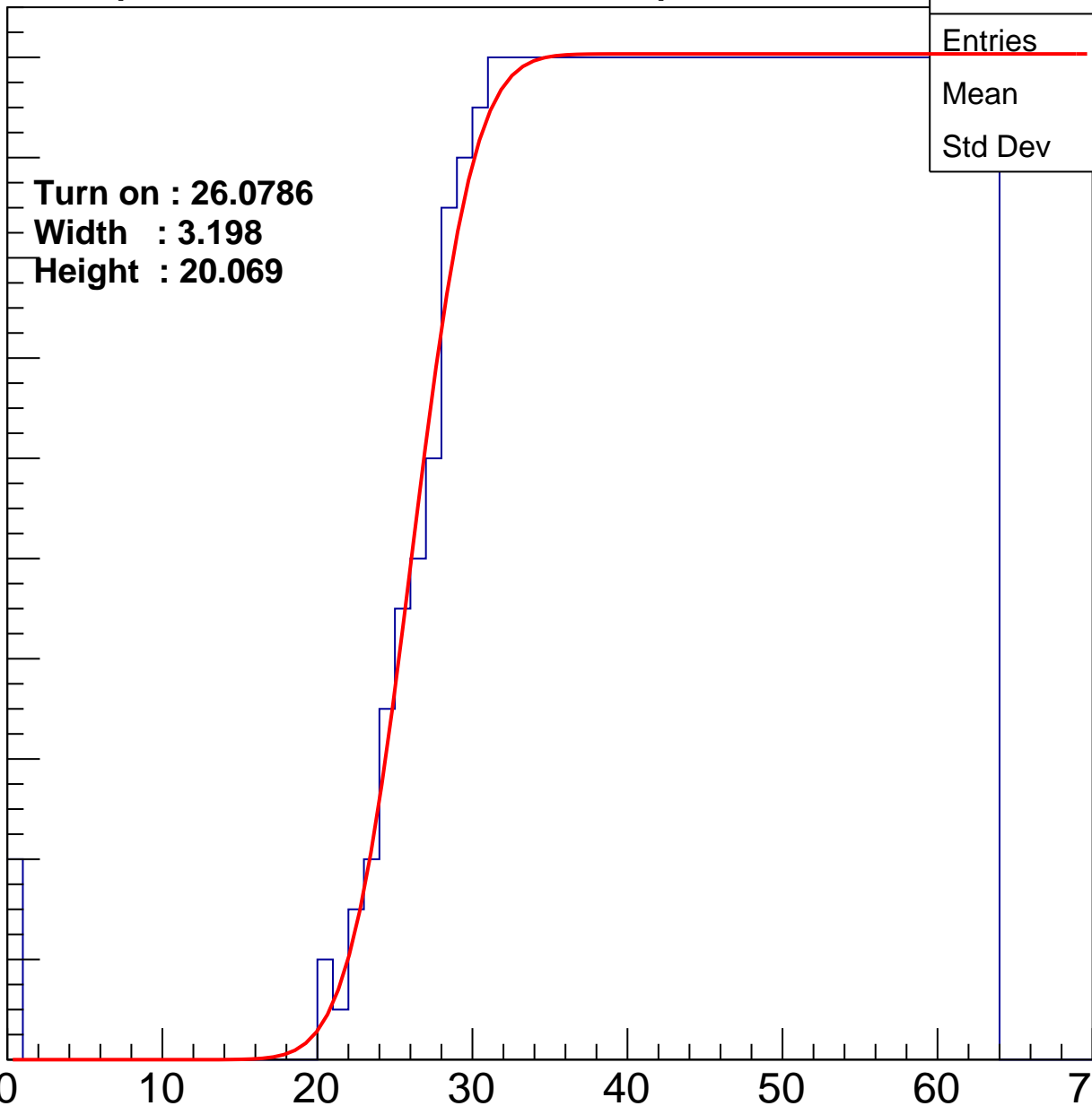
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0786
Width : 3.198
Height : 20.069

Entries	766
Mean	44.1
Std Dev	11.61

ampl



B0L101S, U21-ch2

calib_packv5_042523_0143.root, FC#1, port C1

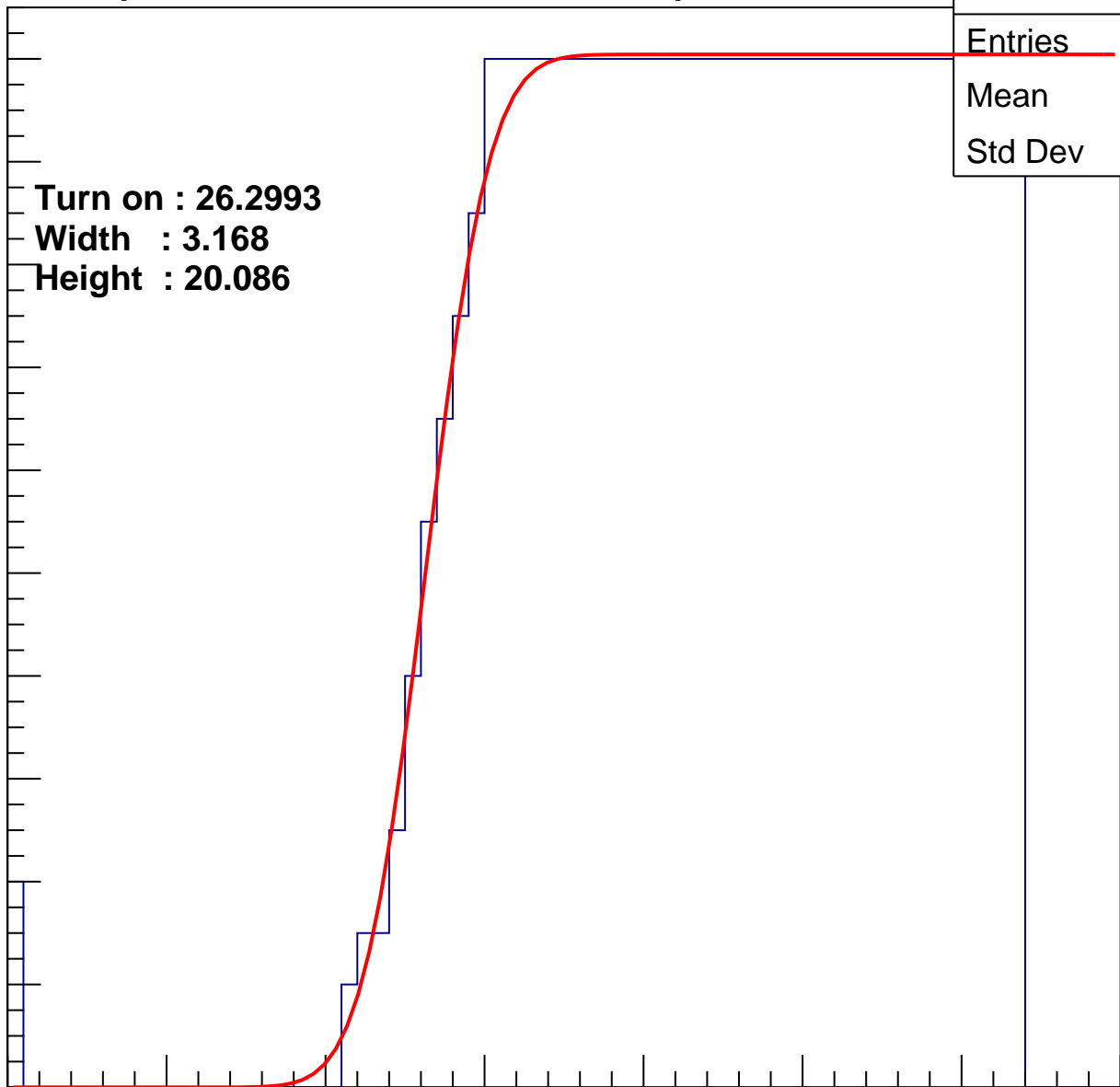
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2993
Width : 3.168
Height : 20.086

Entries	761
Mean	44.24
Std Dev	11.53

ampl



B0L101S, U21-ch3

calib_packv5_042523_0143.root, FC#1, port C1

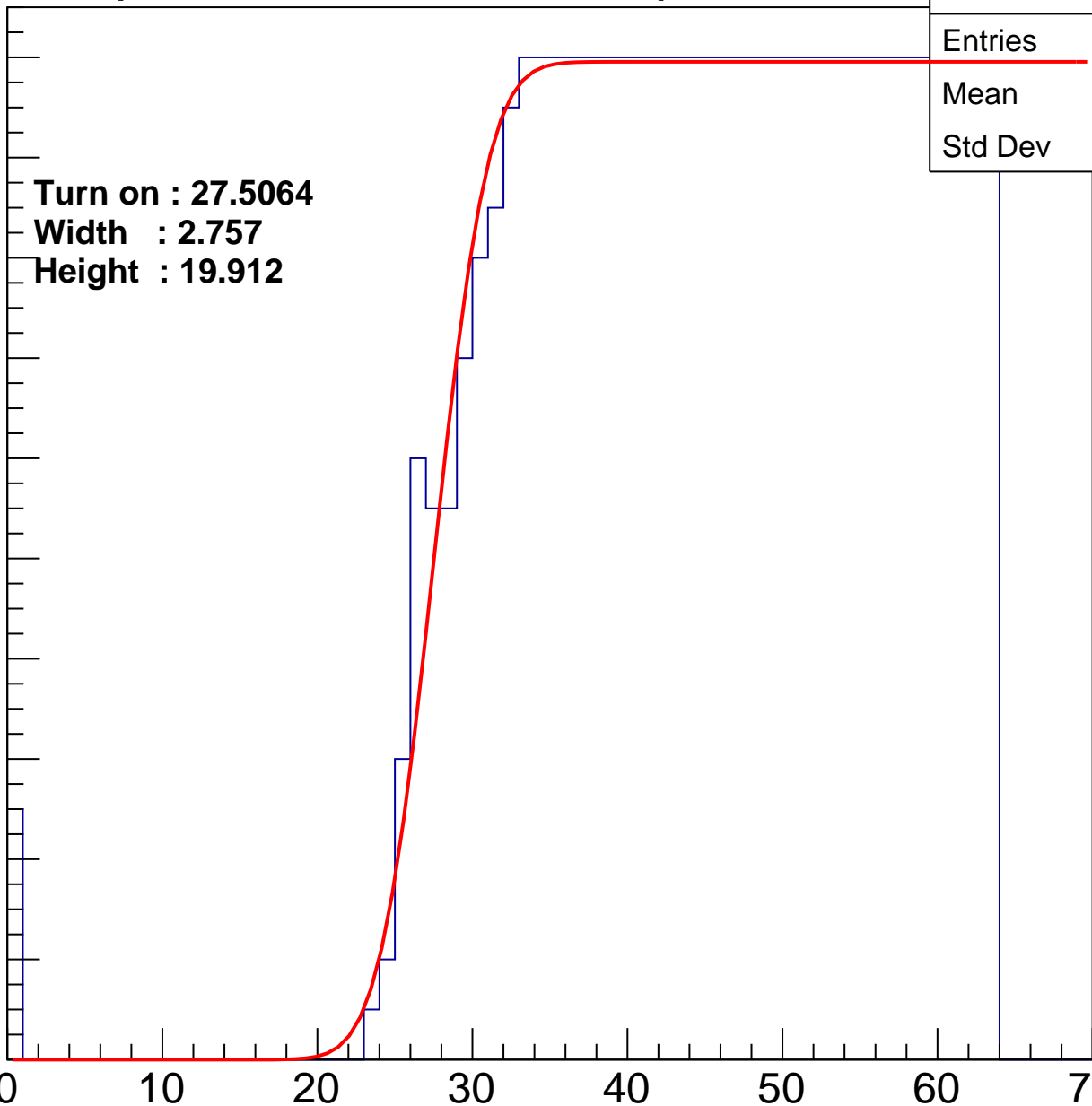
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5064
Width : 2.757
Height : 19.912

Entries	734
Mean	44.85
Std Dev	11.31

ampl



B0L101S, U21-ch4

calib_packv5_042523_0143.root, FC#1, port C1

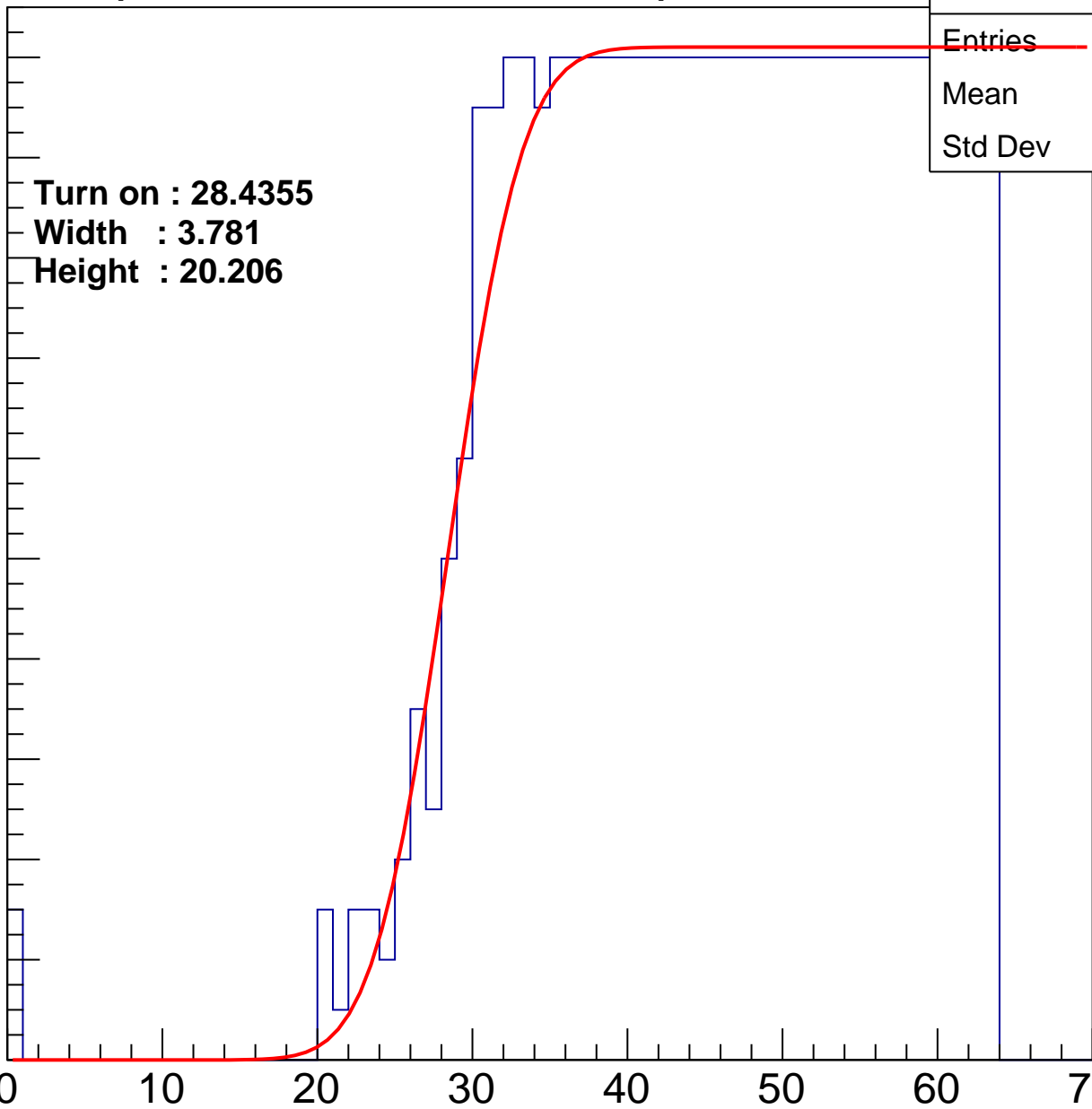
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4355
Width : 3.781
Height : 20.206

Entries	730
Mean	44.98
Std Dev	11.14

ampl



B0L101S, U21-ch5

calib_packv5_042523_0143.root, FC#1, port C1

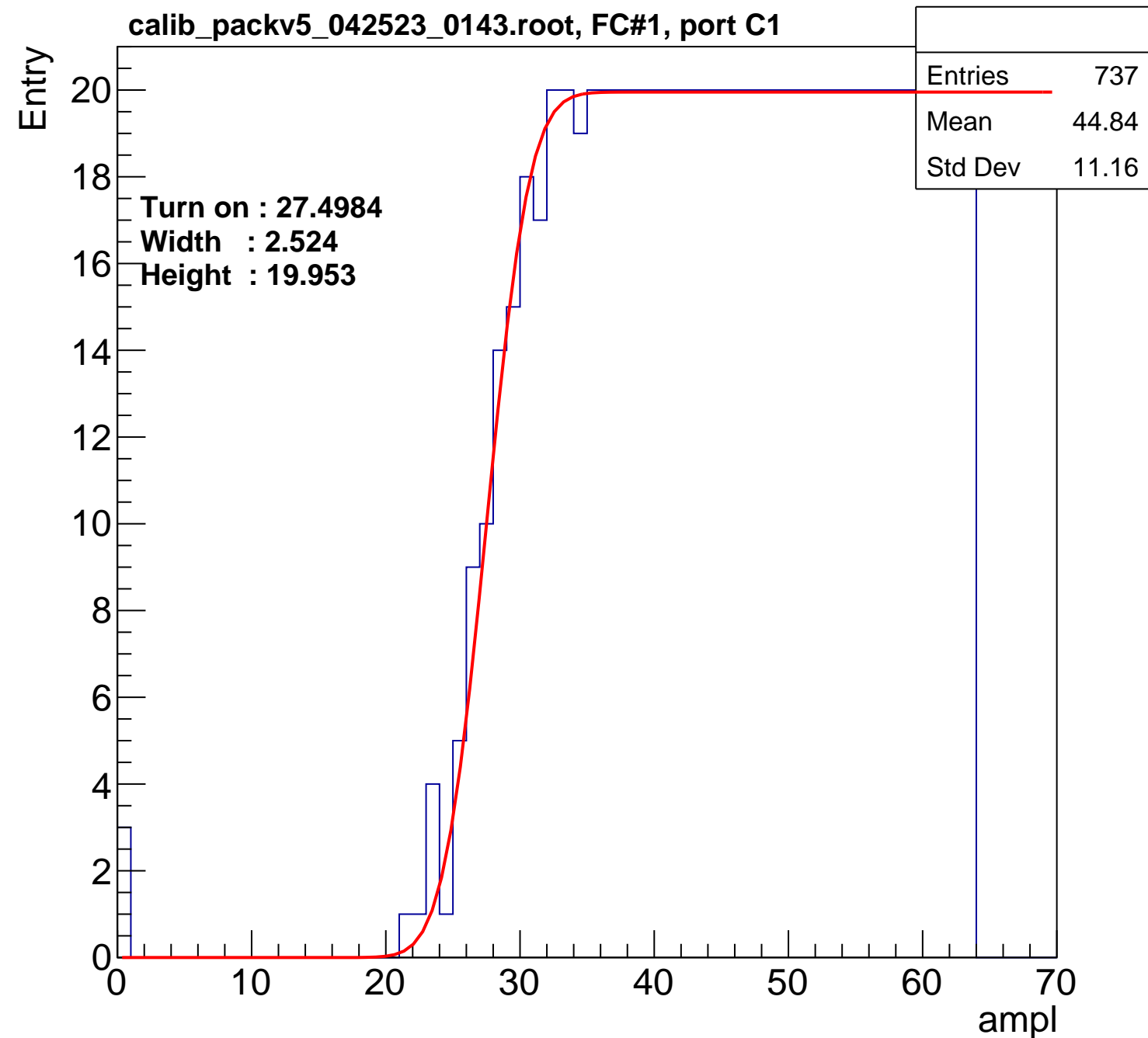
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4984
Width : 2.524
Height : 19.953

Entries	737
Mean	44.84
Std Dev	11.16

ampl



B0L101S, U21-ch6

calib_packv5_042523_0143.root, FC#1, port C1

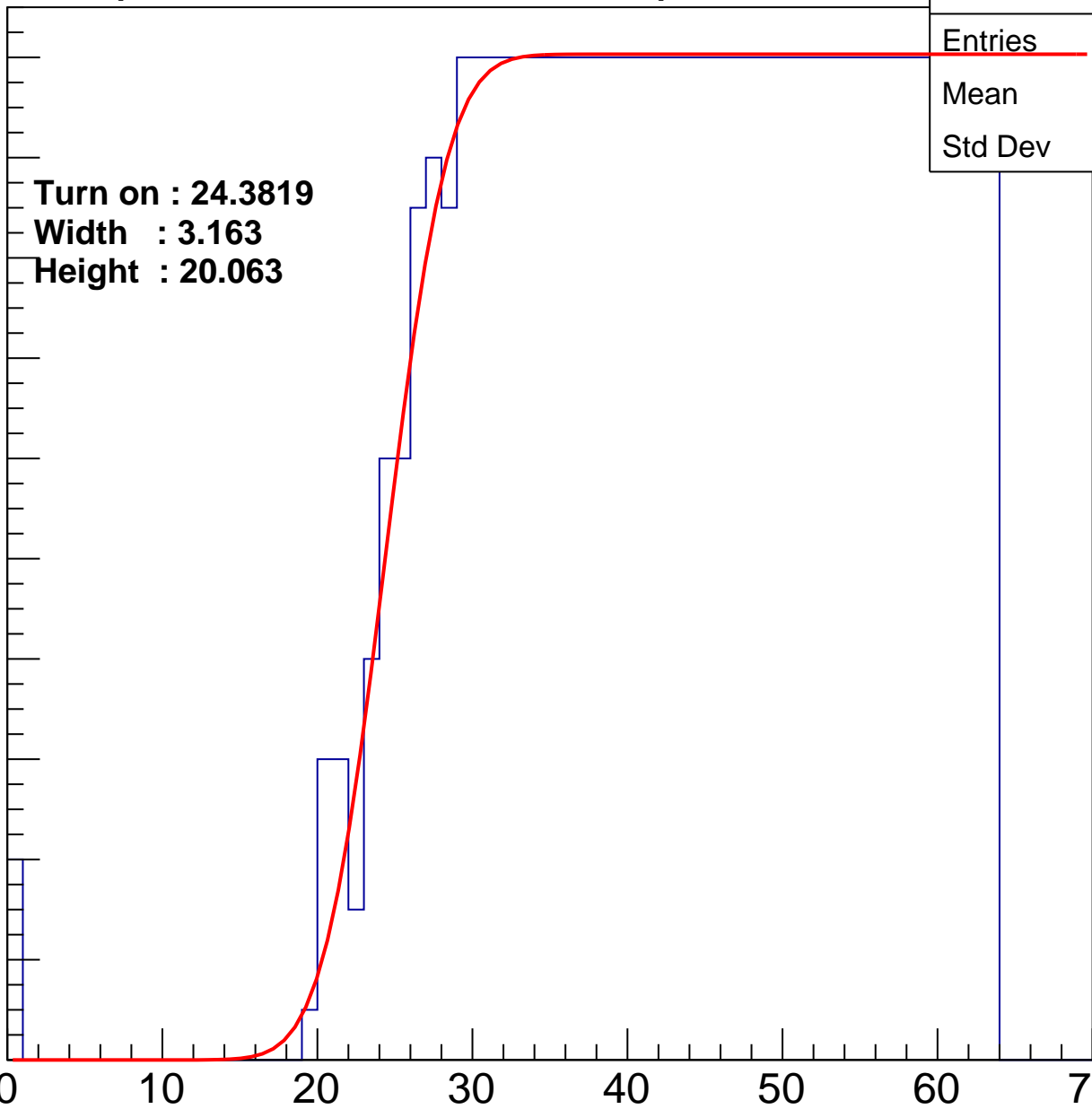
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.3819
Width : 3.163
Height : 20.063

Entries	804
Mean	43.17
Std Dev	12.11

ampl



B0L101S, U21-ch7

calib_packv5_042523_0143.root, FC#1, port C1

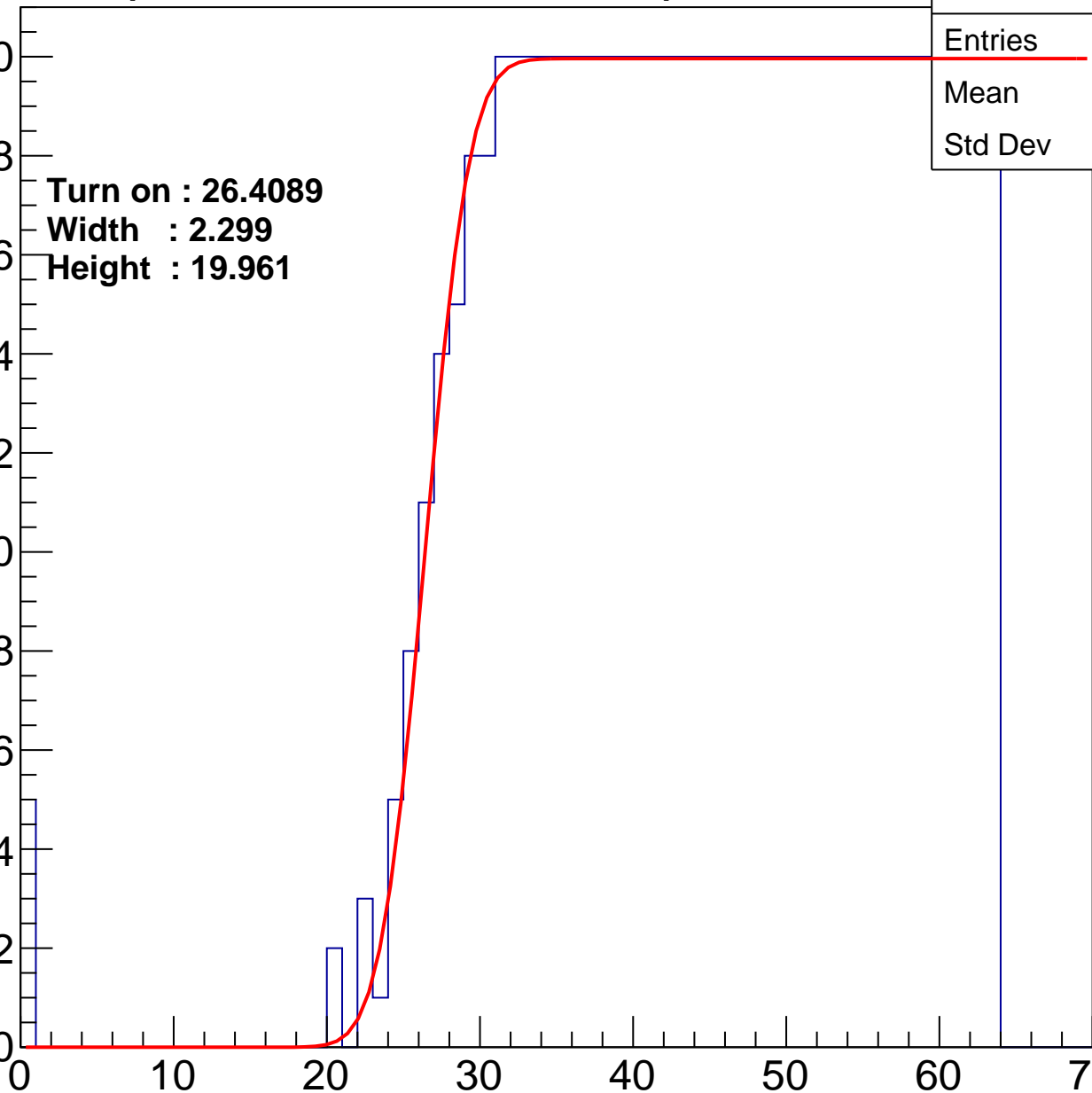
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4089
Width : 2.299
Height : 19.961

Entries	760
Mean	44.23
Std Dev	11.61

ampl



B0L101S, U21-ch8

calib_packv5_042523_0143.root, FC#1, port C1

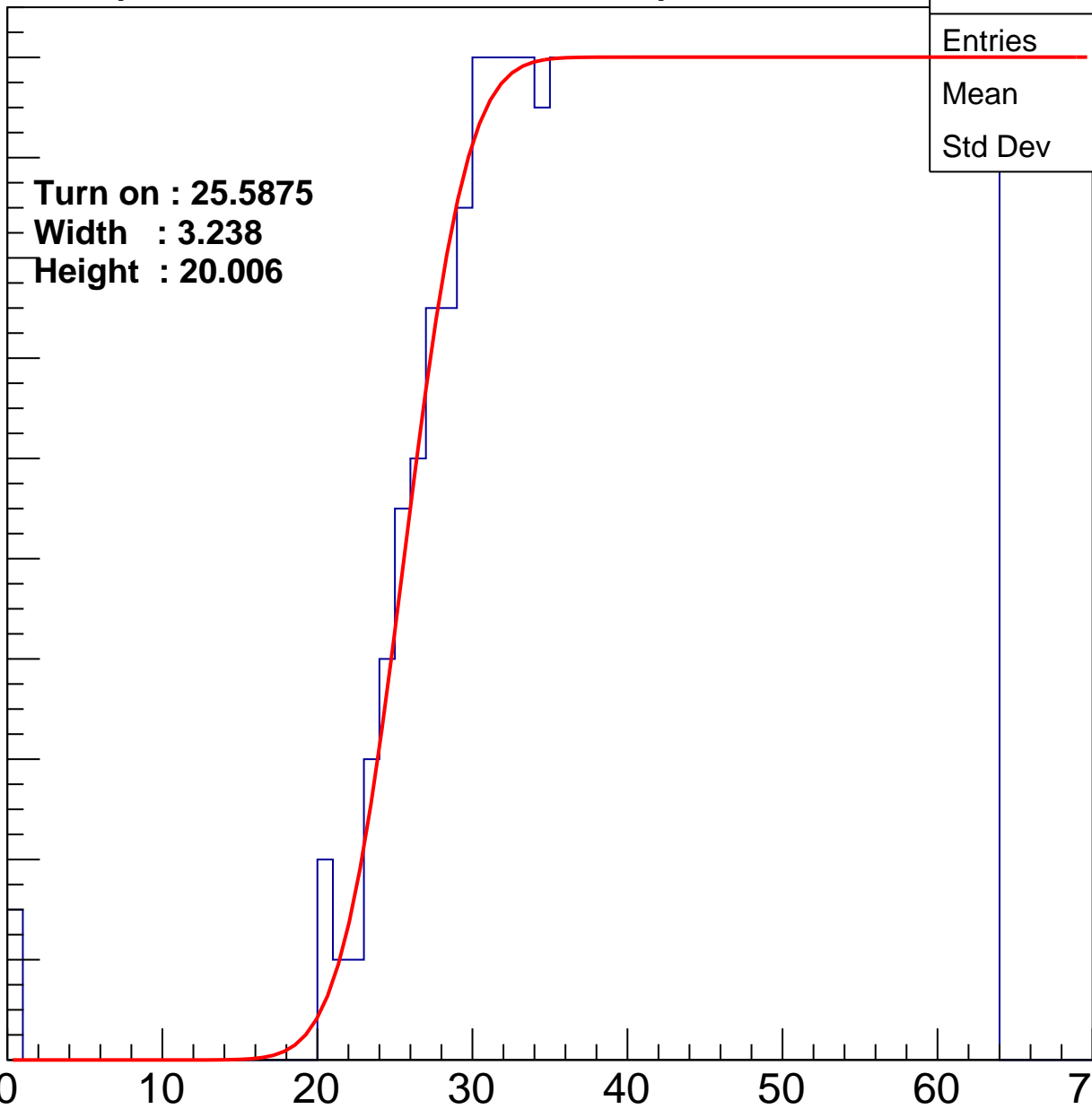
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5875
Width : 3.238
Height : 20.006

Entries	774
Mean	43.91
Std Dev	11.67

ampl



B0L101S, U21-ch9

calib_packv5_042523_0143.root, FC#1, port C1

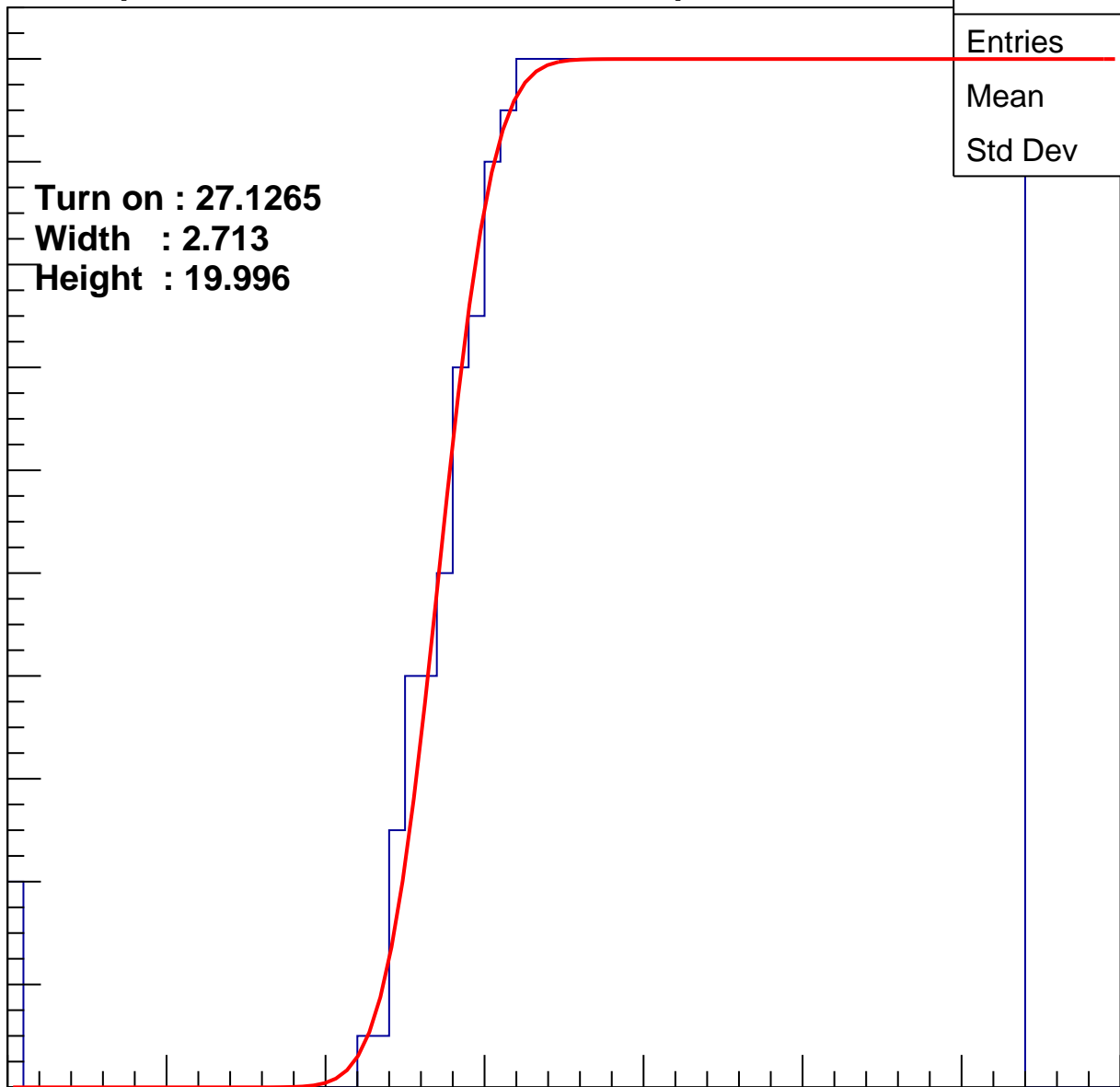
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1265
Width : 2.713
Height : 19.996

Entries	743
Mean	44.68
Std Dev	11.3

ampl



B0L101S, U21-ch10

calib_packv5_042523_0143.root, FC#1, port C1

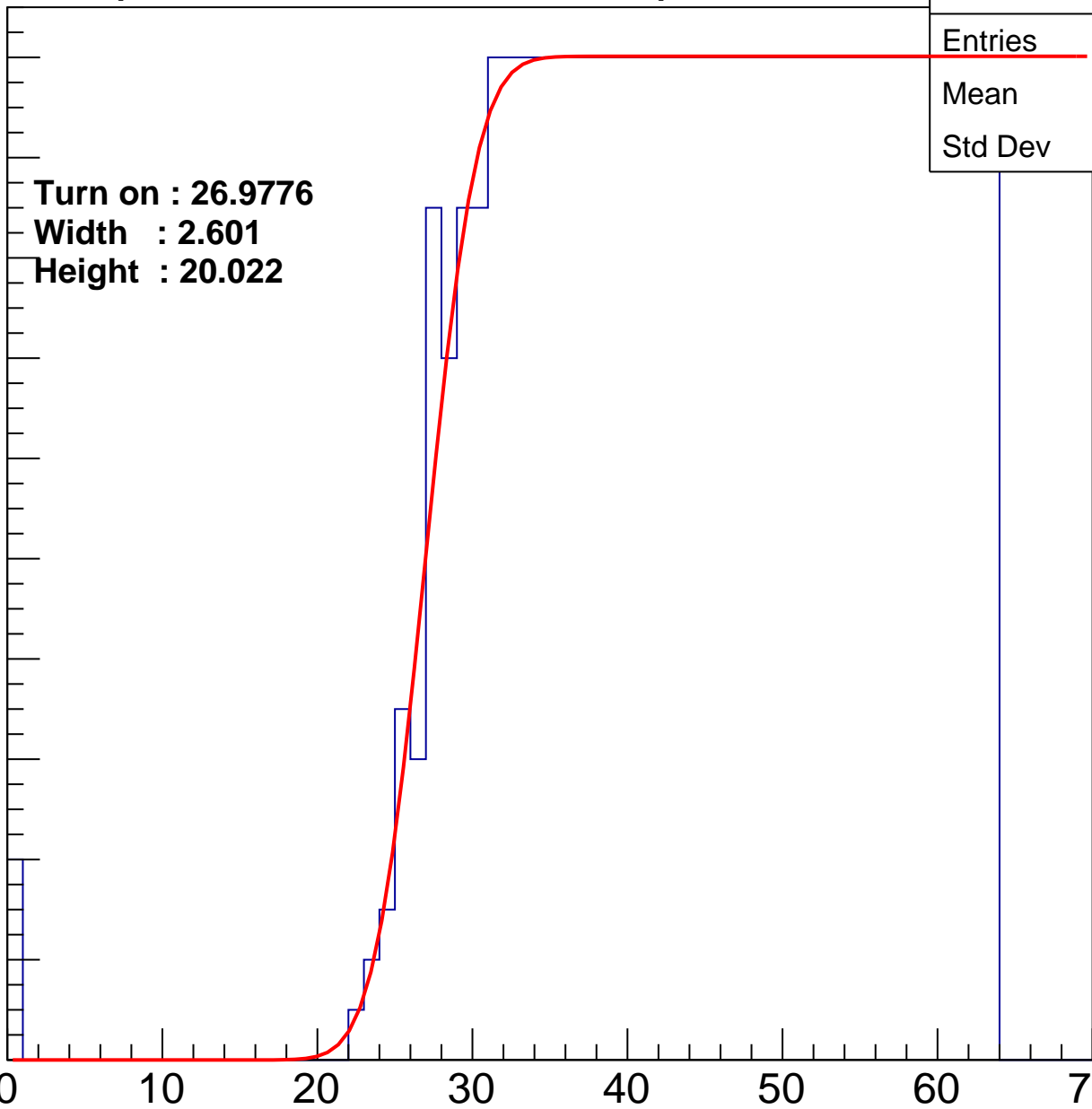
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9776
Width : 2.601
Height : 20.022

Entries	748
Mean	44.58
Std Dev	11.33

ampl



B0L101S, U21-ch11

calib_packv5_042523_0143.root, FC#1, port C1

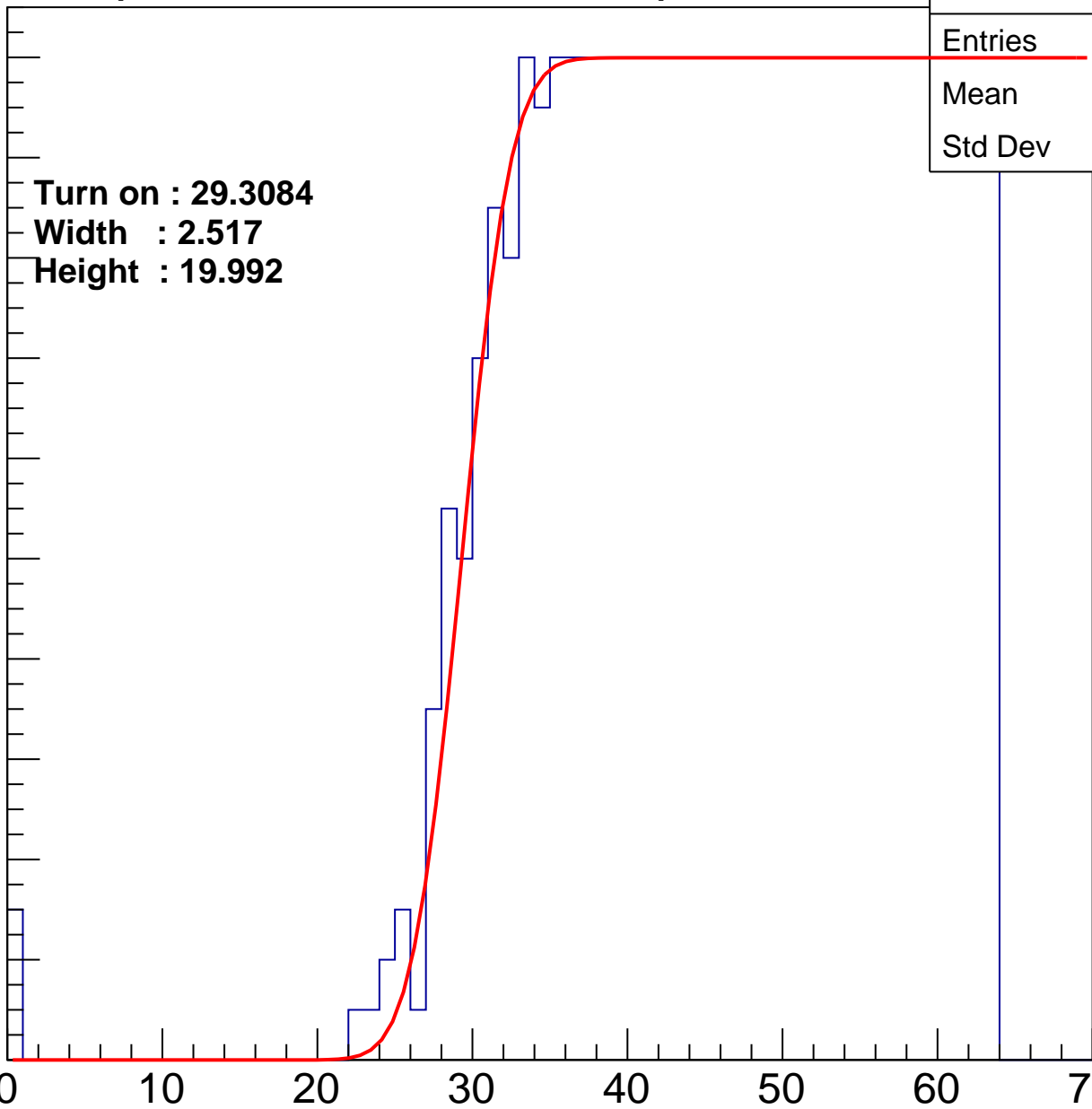
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.3084
Width : 2.517
Height : 19.992

Entries	705
Mean	45.63
Std Dev	10.75

ampl



B0L101S, U21-ch12

calib_packv5_042523_0143.root, FC#1, port C1

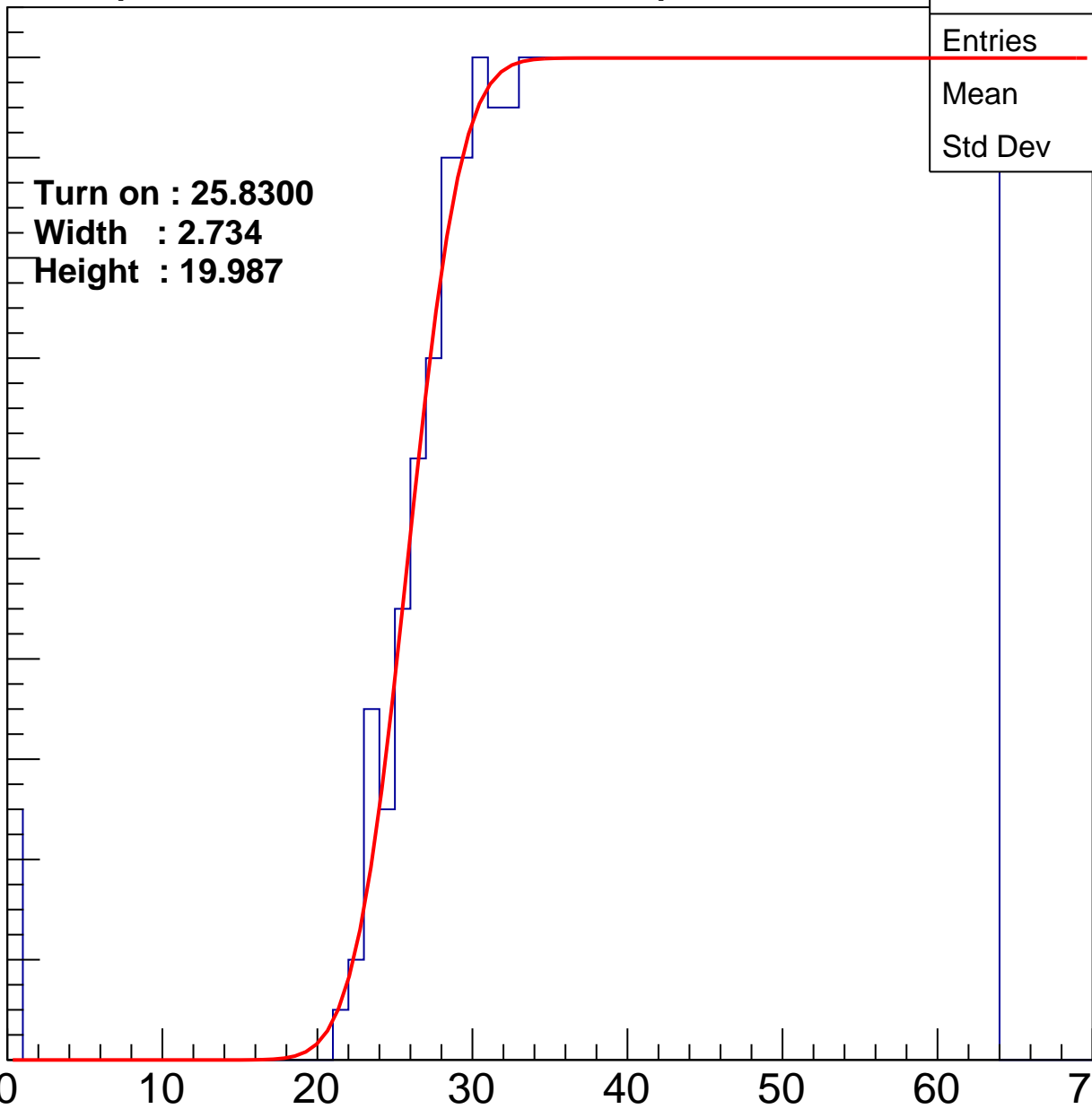
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8300
Width : 2.734
Height : 19.987

Entries	769
Mean	44.01
Std Dev	11.71

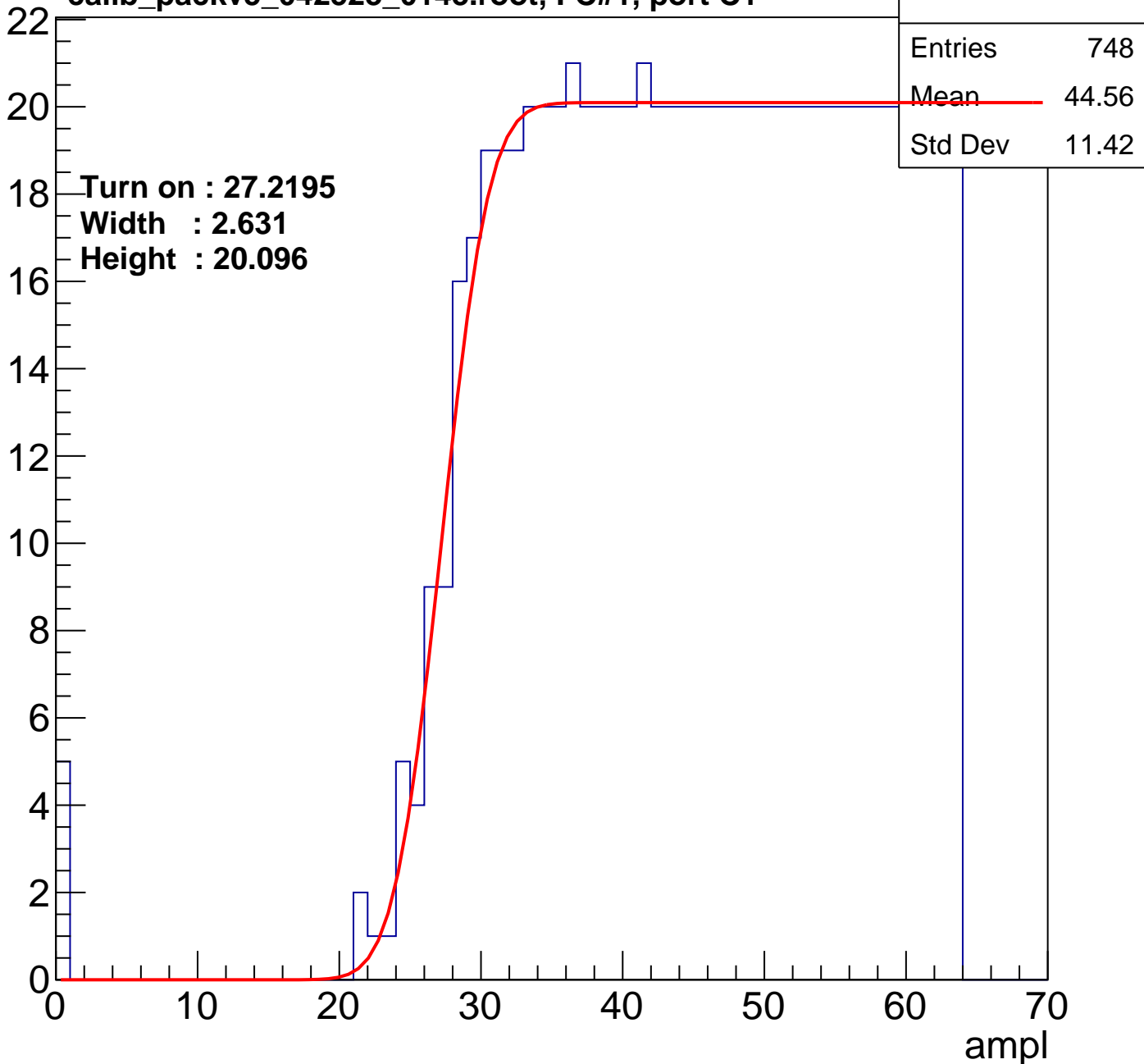
ampl



B0L101S, U21-ch13

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch14

calib_packv5_042523_0143.root, FC#1, port C1

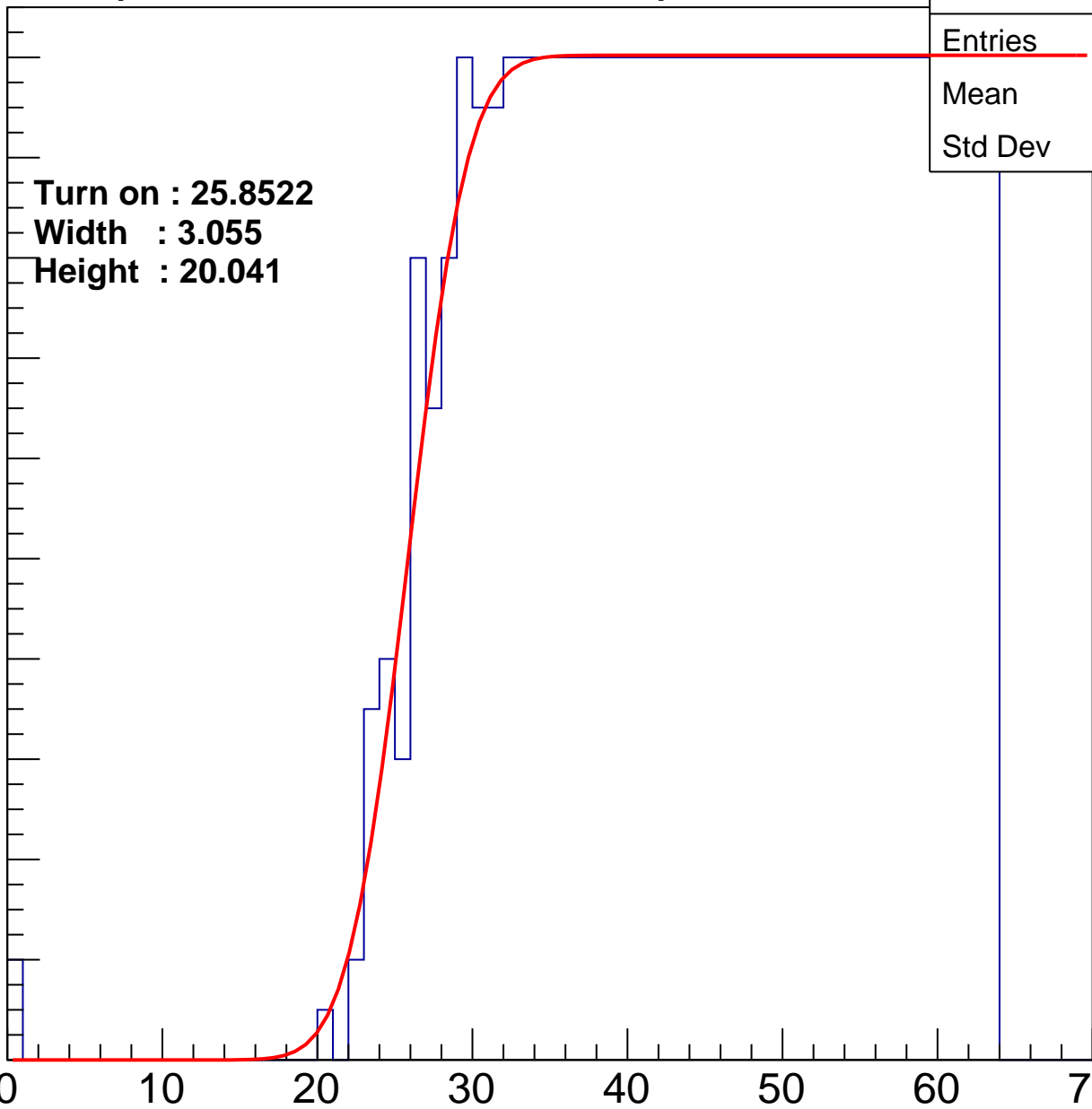
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8522
Width : 3.055
Height : 20.041

Entries	769
Mean	44.11
Std Dev	11.45

ampl



B0L101S, U21-ch15

calib_packv5_042523_0143.root, FC#1, port C1

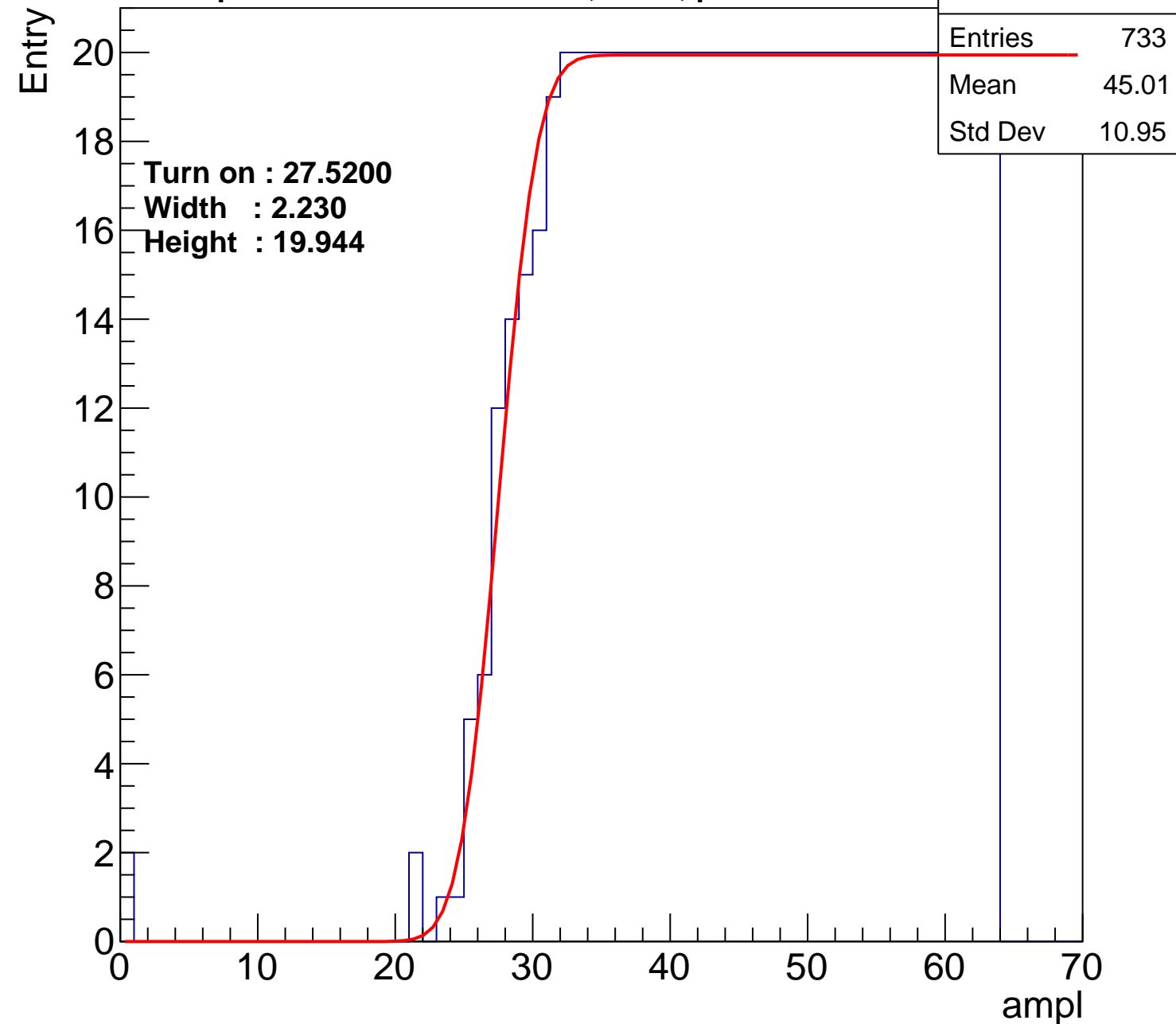
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5200
Width : 2.230
Height : 19.944

Entries	733
Mean	45.01
Std Dev	10.95

ampl



B0L101S, U21-ch16

calib_packv5_042523_0143.root, FC#1, port C1

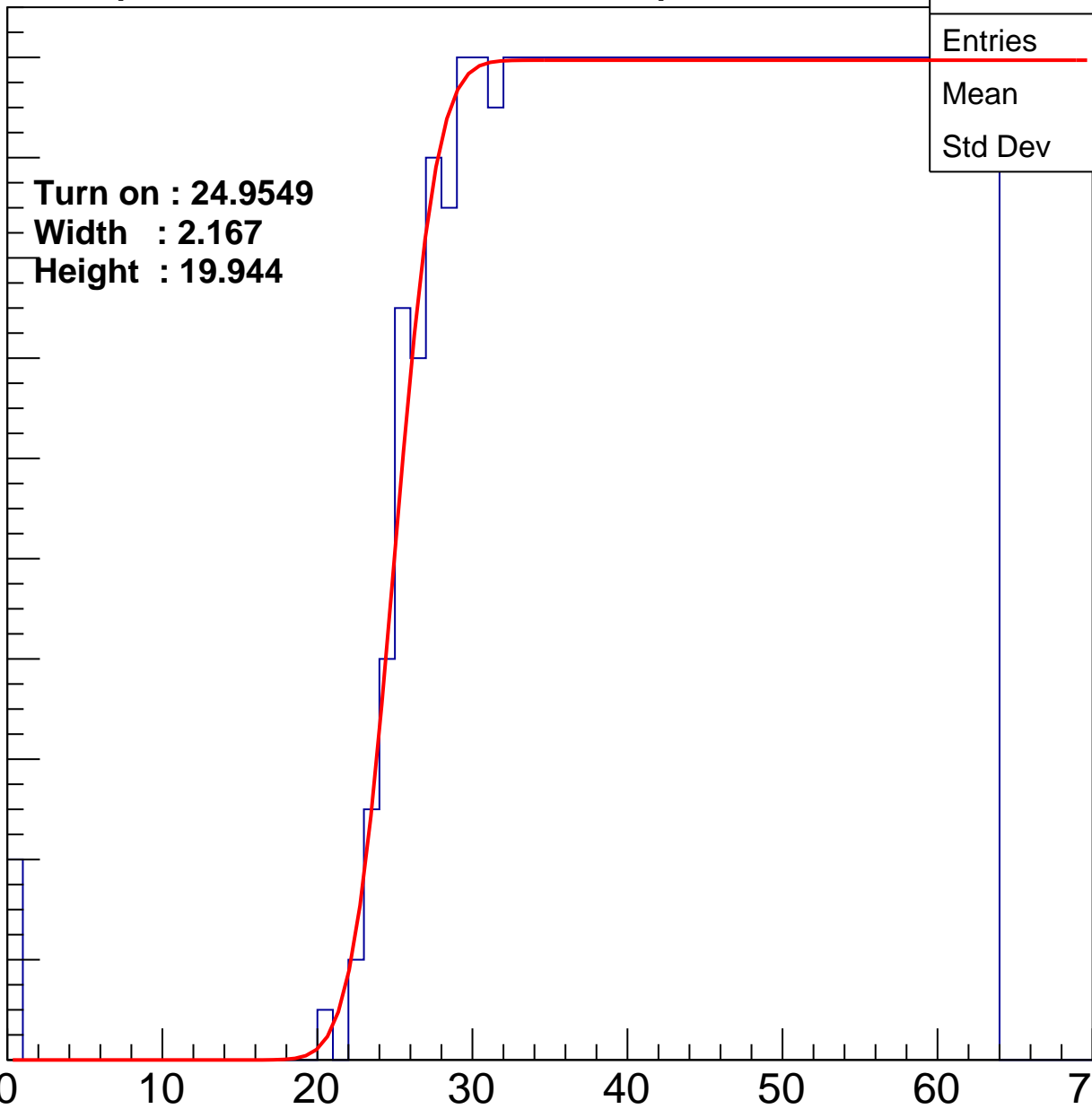
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.9549
Width : 2.167
Height : 19.944

Entries	783
Mean	43.73
Std Dev	11.75

ampl



B0L101S, U21-ch17

calib_packv5_042523_0143.root, FC#1, port C1

Entry

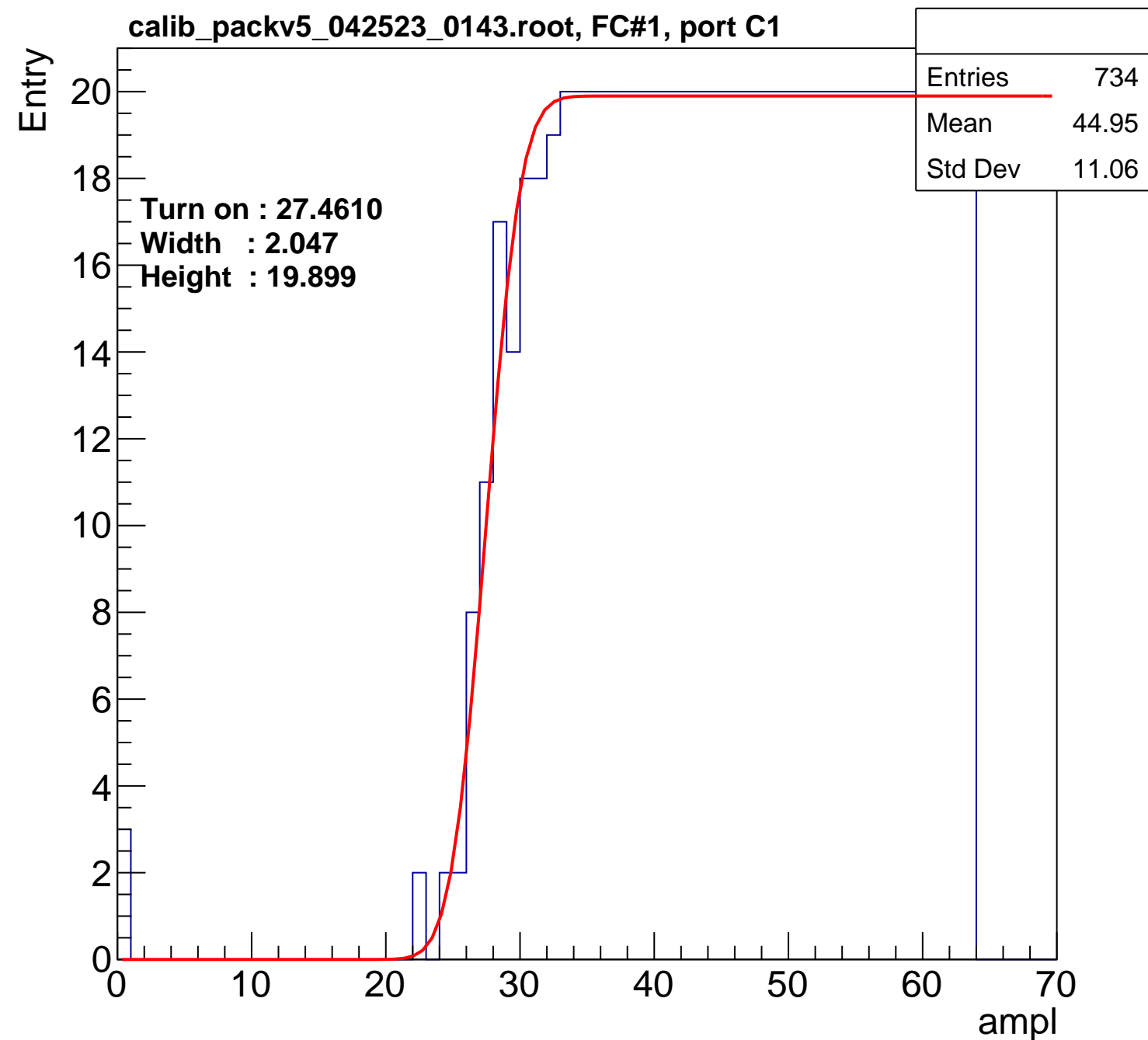
20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4610
Width : 2.047
Height : 19.899

Entries	734
Mean	44.95
Std Dev	11.06

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch18

calib_packv5_042523_0143.root, FC#1, port C1

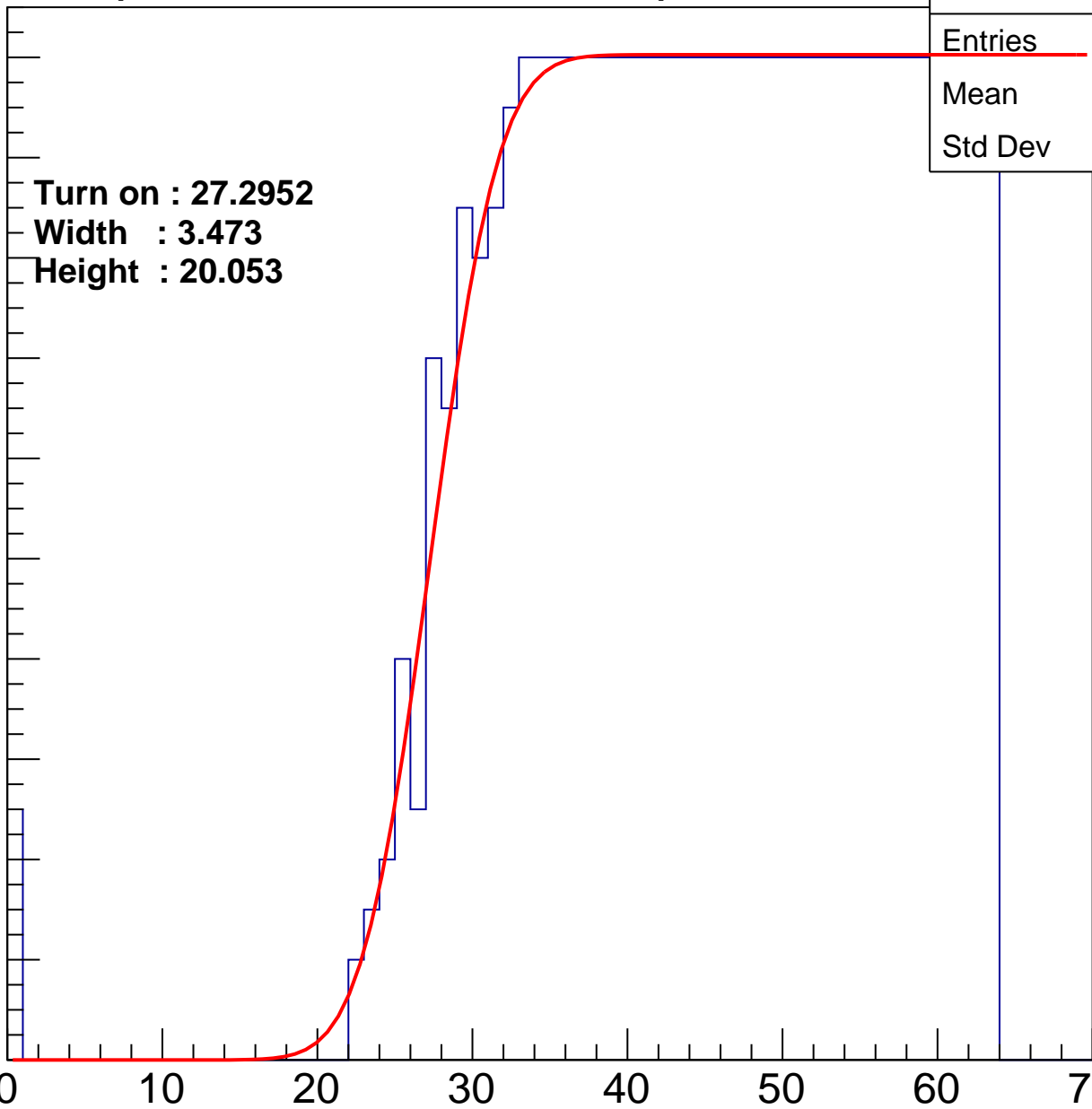
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2952
Width : 3.473
Height : 20.053

Entries	743
Mean	44.62
Std Dev	11.44

ampl



B0L101S, U21-ch19

calib_packv5_042523_0143.root, FC#1, port C1

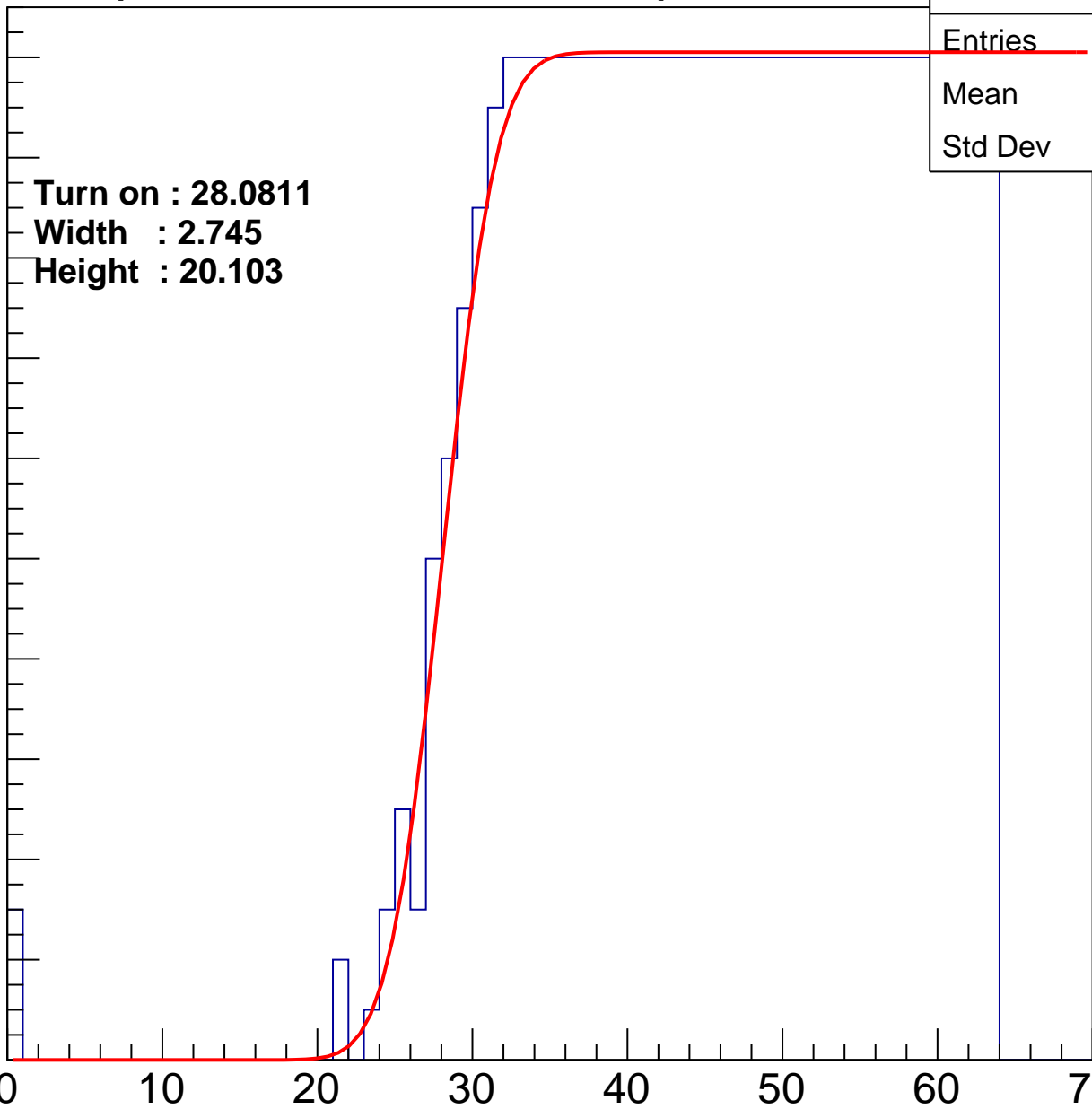
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0811
Width : 2.745
Height : 20.103

Entries	730
Mean	45.04
Std Dev	11.03

ampl



B0L101S, U21-ch20

calib_packv5_042523_0143.root, FC#1, port C1

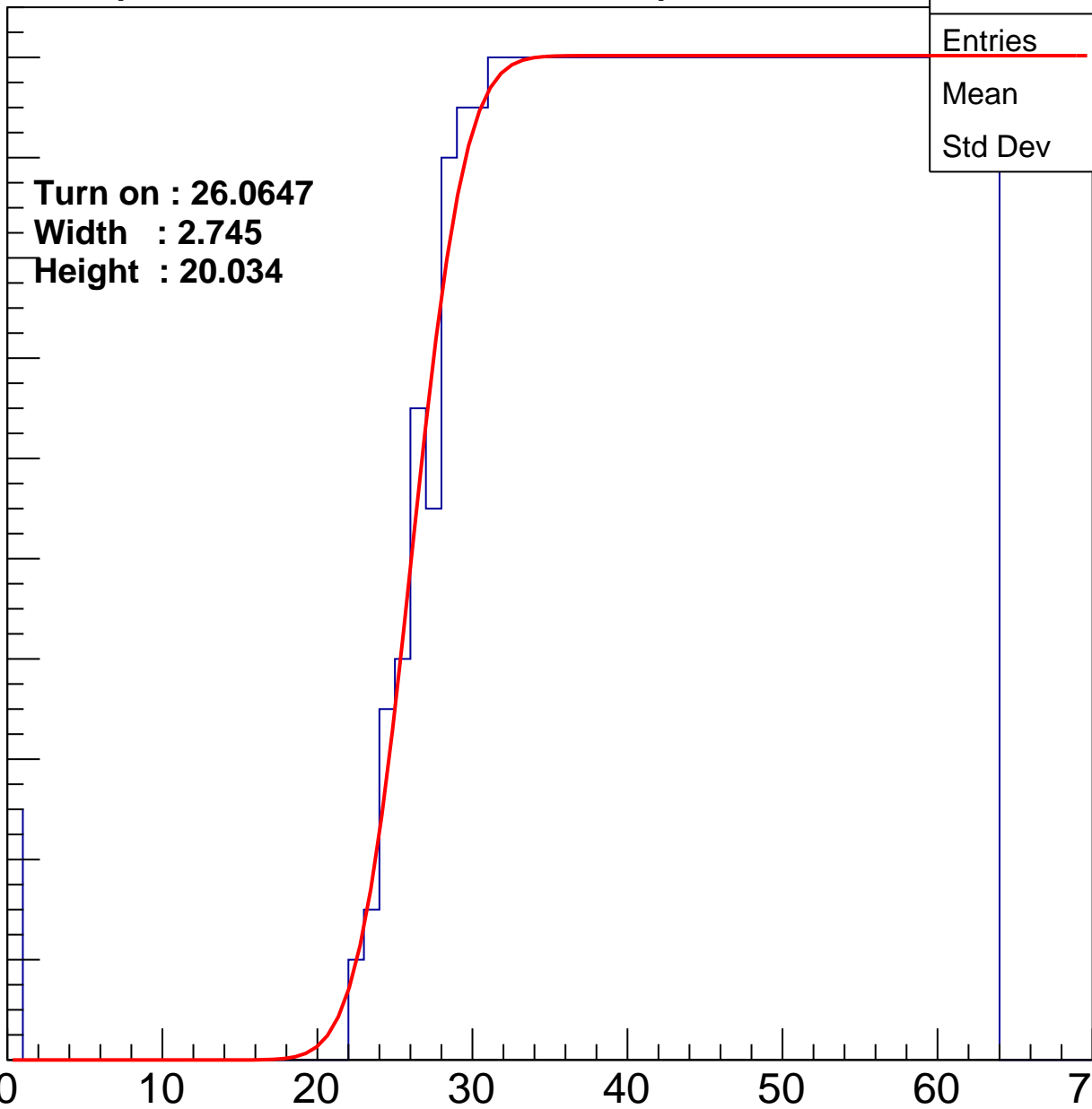
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0647
Width : 2.745
Height : 20.034

Entries	765
Mean	44.13
Std Dev	11.63

ampl



B0L101S, U21-ch21

calib_packv5_042523_0143.root, FC#1, port C1

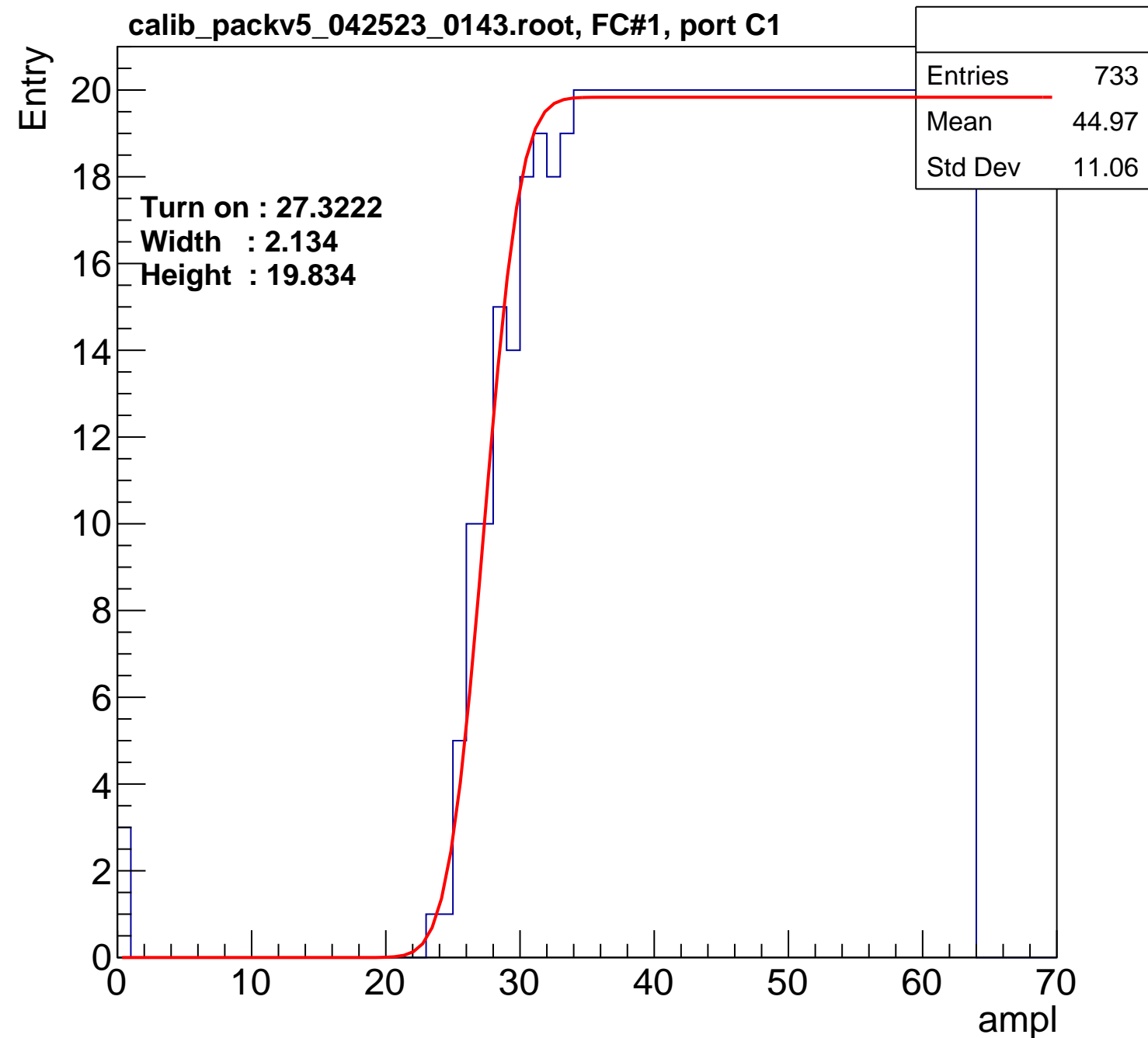
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3222
Width : 2.134
Height : 19.834

Entries	733
Mean	44.97
Std Dev	11.06

ampl



B0L101S, U21-ch22

calib_packv5_042523_0143.root, FC#1, port C1

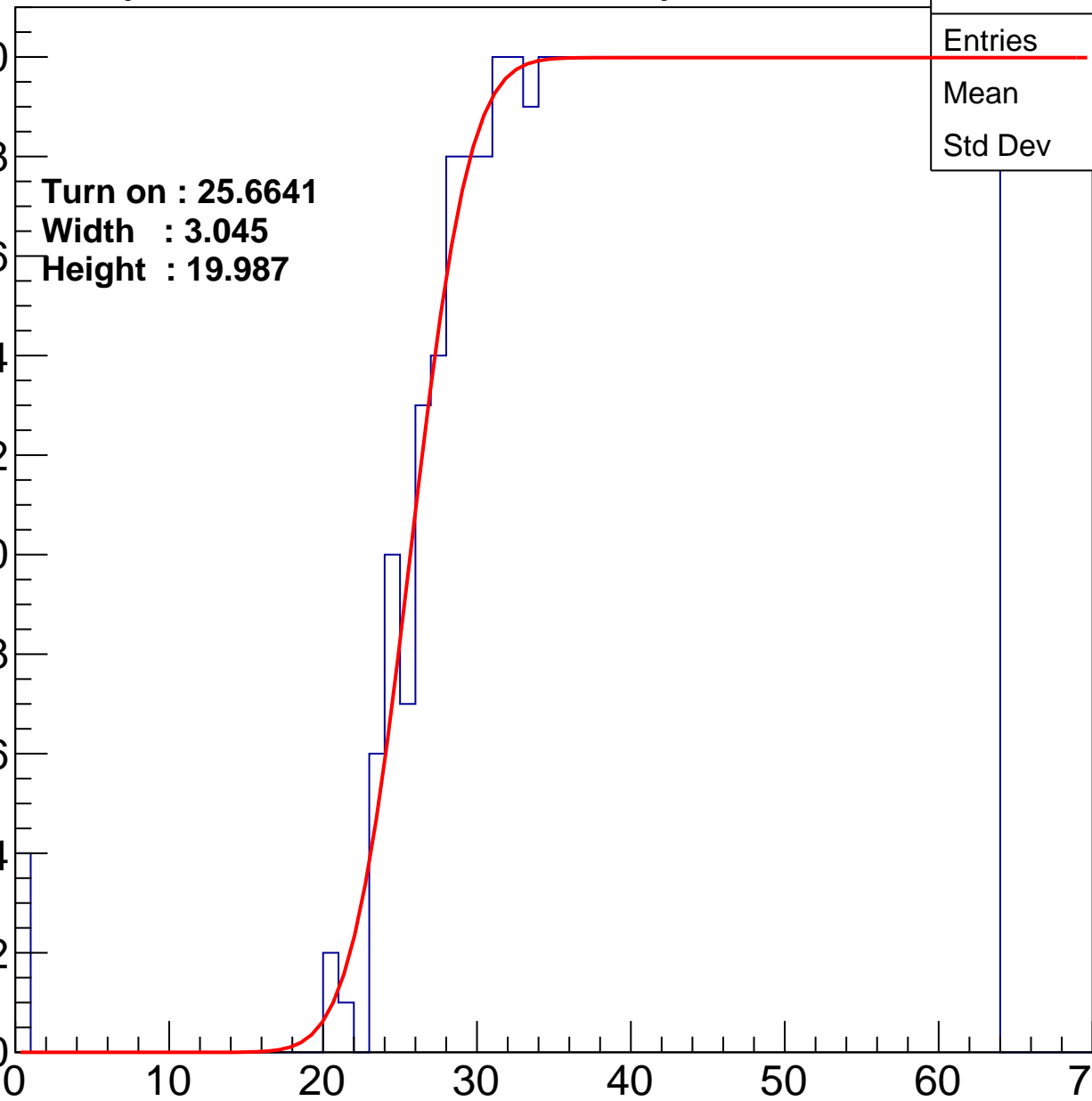
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6641
Width : 3.045
Height : 19.987

Entries	770
Mean	44
Std Dev	11.66

ampl



B0L101S, U21-ch23

calib_packv5_042523_0143.root, FC#1, port C1

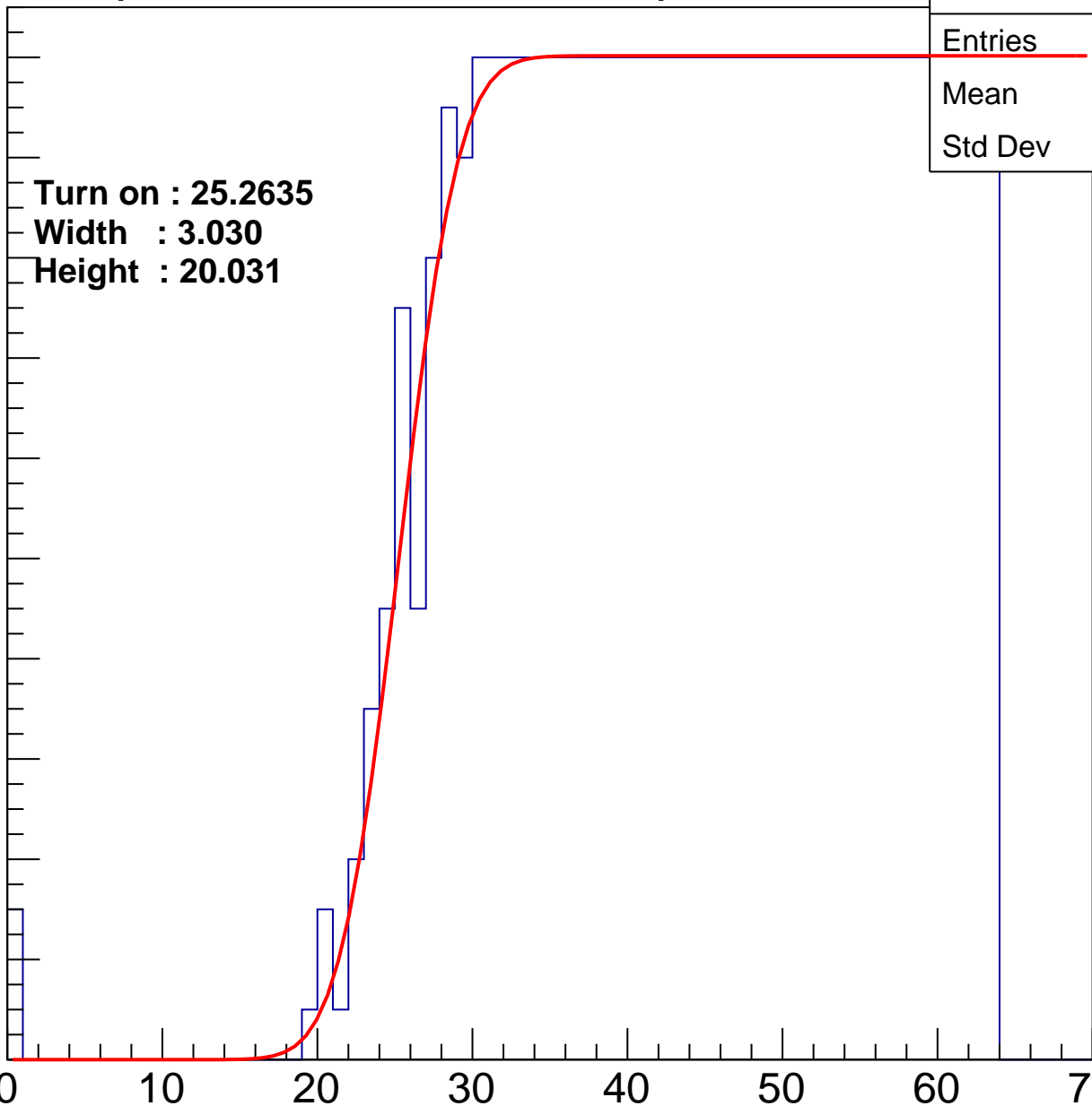
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2635
Width : 3.030
Height : 20.031

Entries	785
Mean	43.67
Std Dev	11.77

ampl



B0L101S, U21-ch24

calib_packv5_042523_0143.root, FC#1, port C1

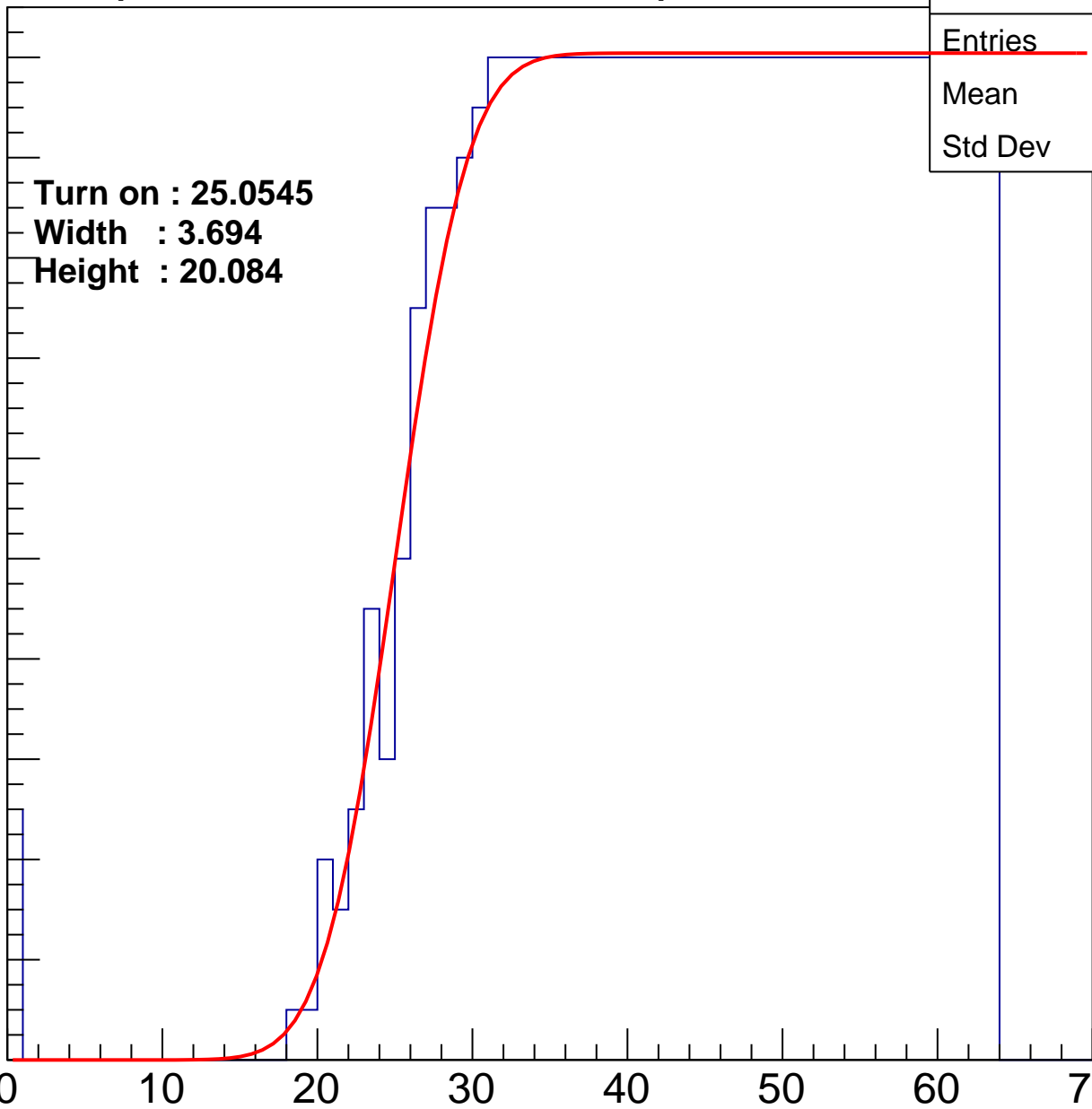
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0545
Width : 3.694
Height : 20.084

Entries	790
Mean	43.45
Std Dev	12.05

ampl



B0L101S, U21-ch25

calib_packv5_042523_0143.root, FC#1, port C1

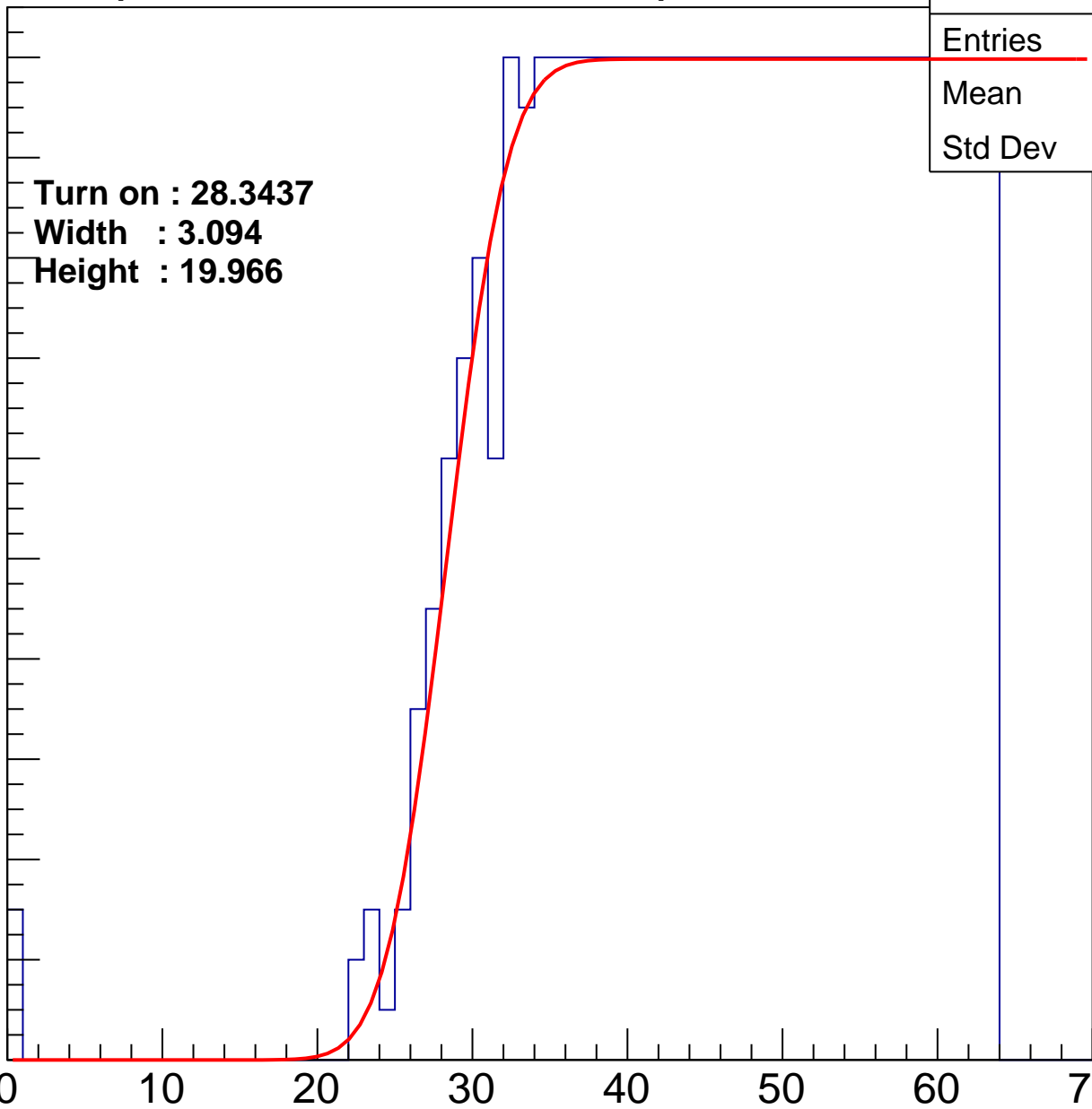
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3437
Width : 3.094
Height : 19.966

Entries	721
Mean	45.21
Std Dev	10.99

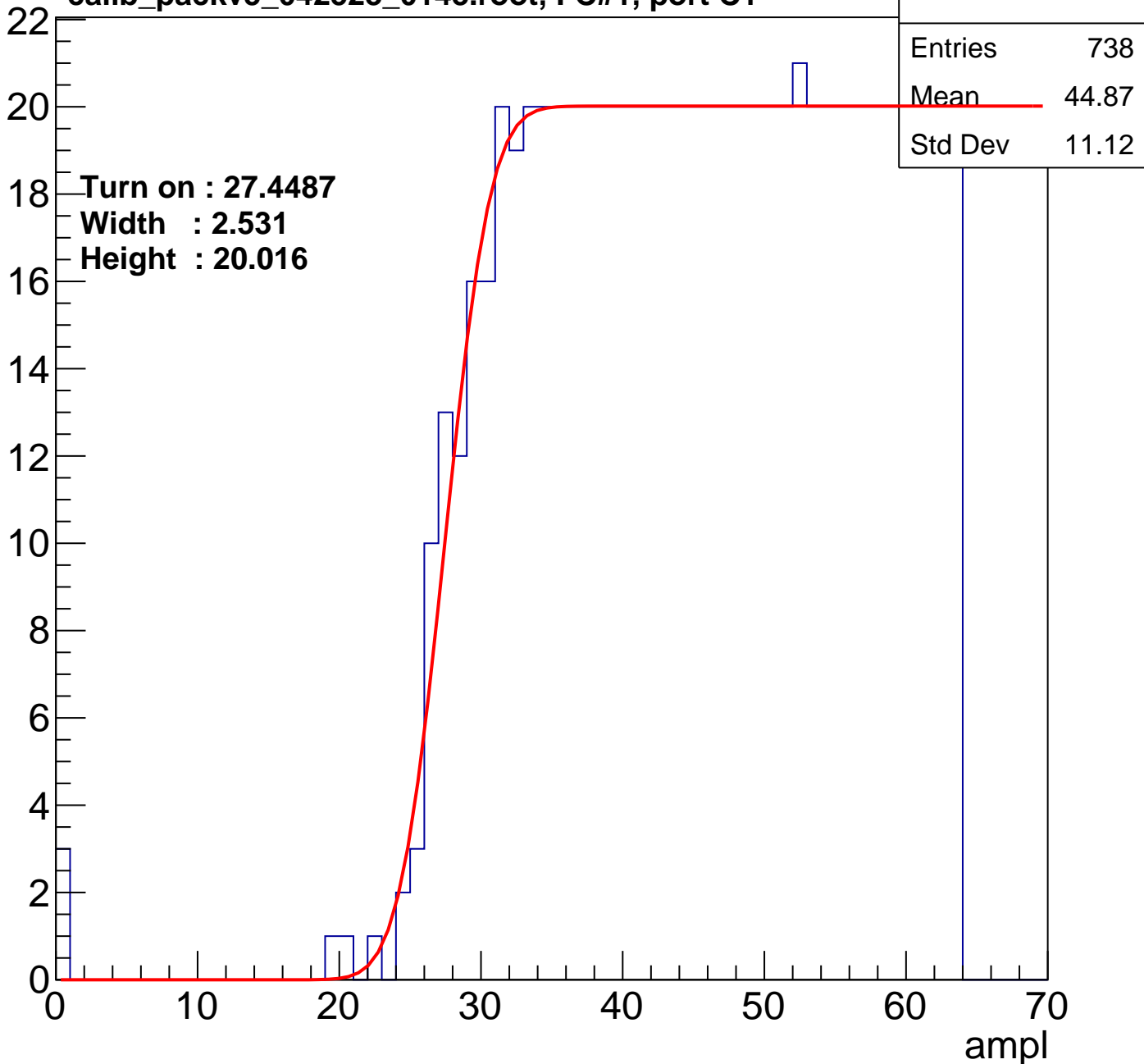
ampl



B0L101S, U21-ch26

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch27

calib_packv5_042523_0143.root, FC#1, port C1

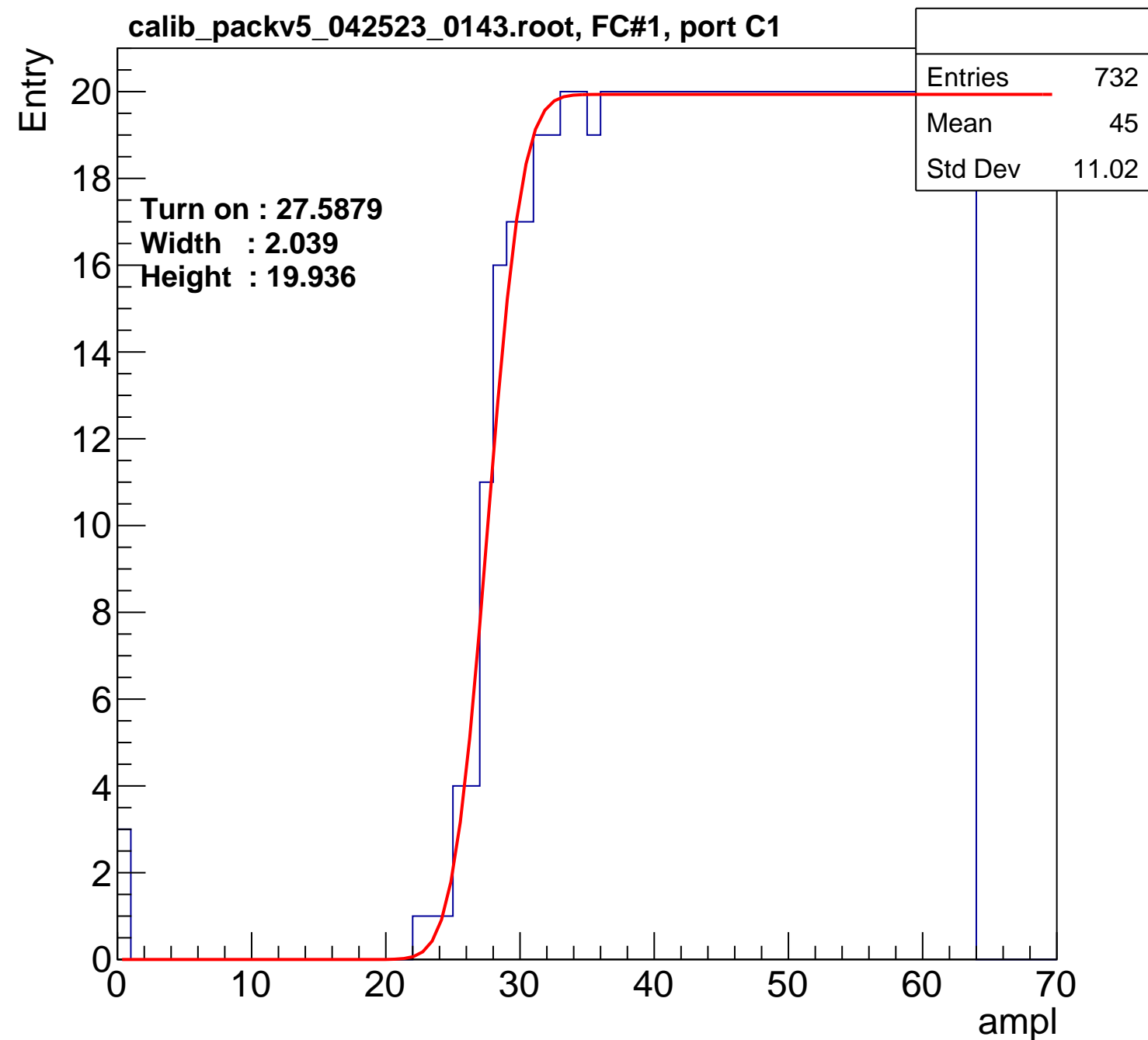
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5879
Width : 2.039
Height : 19.936

Entries	732
Mean	45
Std Dev	11.02

ampl



B0L101S, U21-ch28

calib_packv5_042523_0143.root, FC#1, port C1

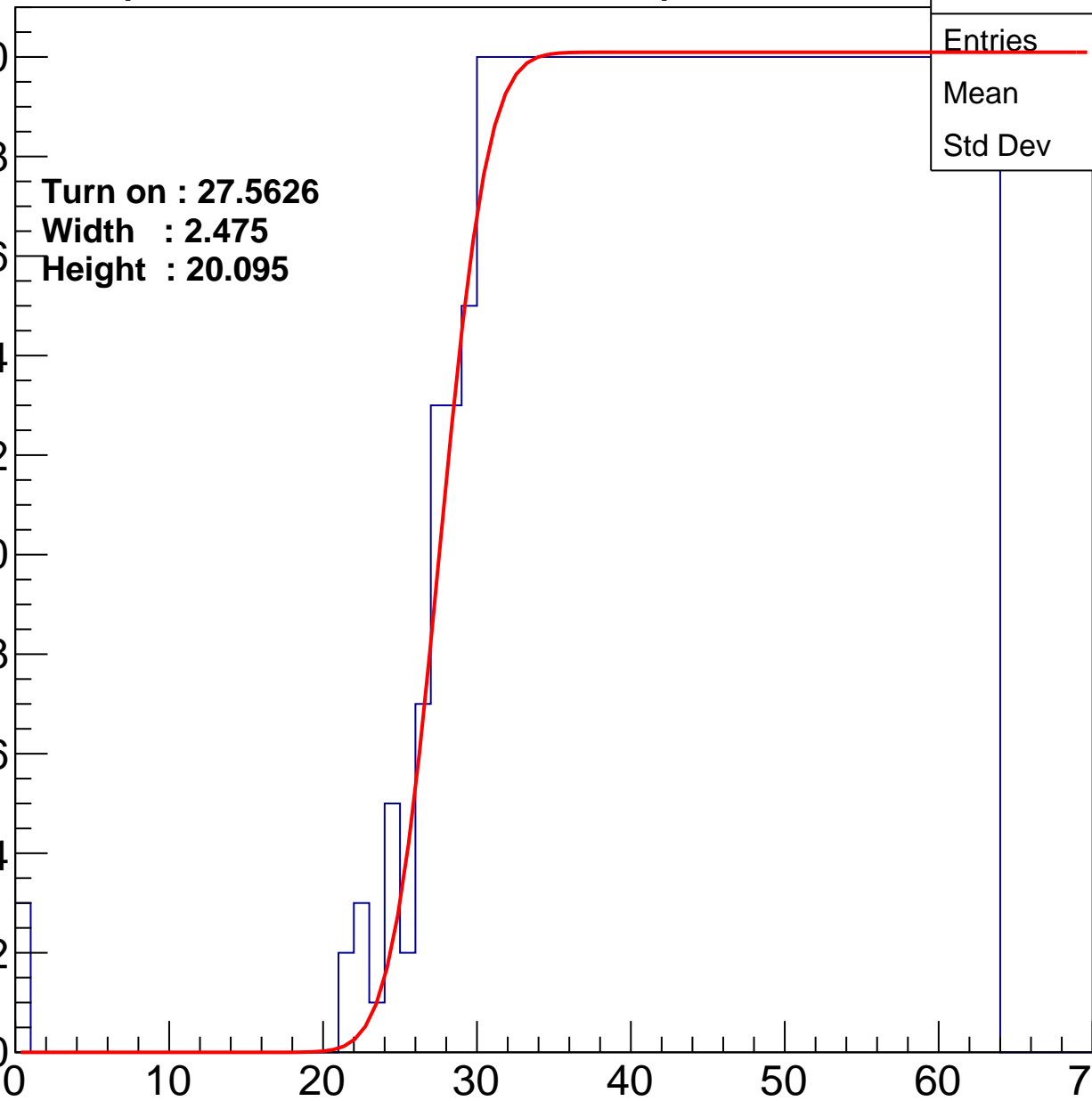
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5626
Width : 2.475
Height : 20.095

Entries	744
Mean	44.69
Std Dev	11.21

ampl



B0L101S, U21-ch29

calib_packv5_042523_0143.root, FC#1, port C1

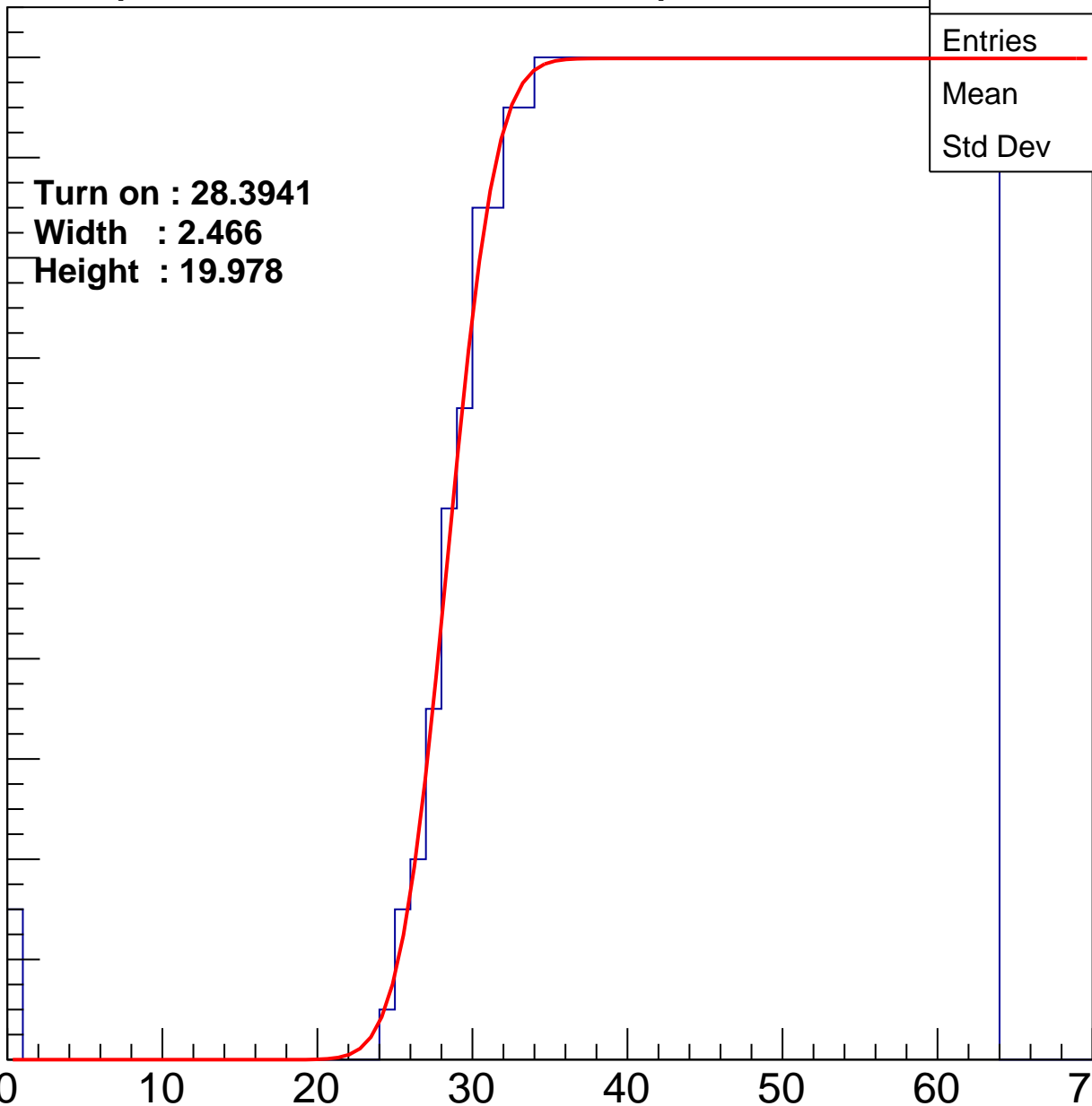
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3941
Width : 2.466
Height : 19.978

Entries	714
Mean	45.45
Std Dev	10.79

ampl



B0L101S, U21-ch30

calib_packv5_042523_0143.root, FC#1, port C1

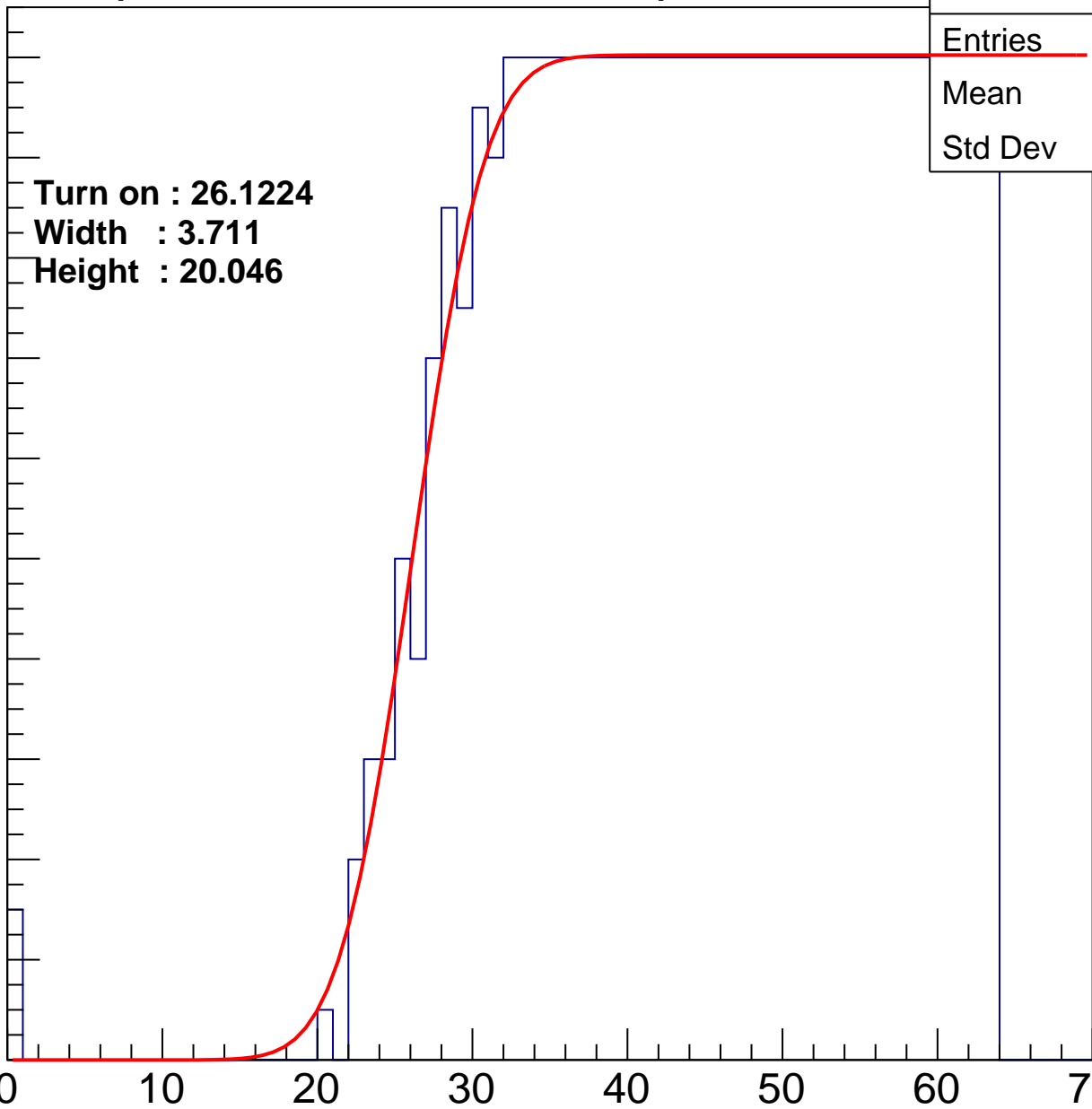
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1224
Width : 3.711
Height : 20.046

Entries	761
Mean	44.24
Std Dev	11.49

ampl



B0L101S, U21-ch31

calib_packv5_042523_0143.root, FC#1, port C1

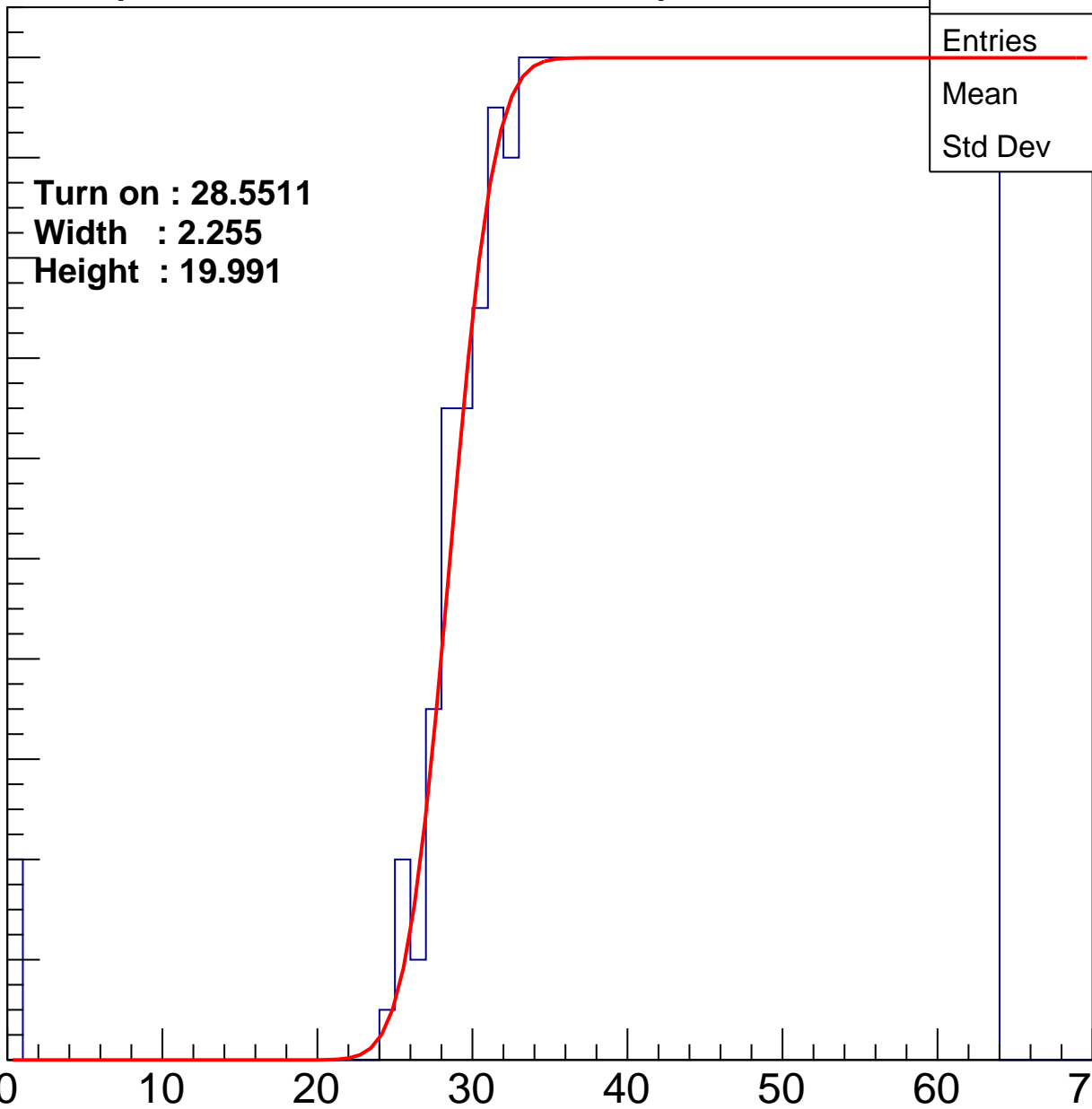
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5511
Width : 2.255
Height : 19.991

Entries	716
Mean	45.36
Std Dev	10.92

ampl



B0L101S, U21-ch32

calib_packv5_042523_0143.root, FC#1, port C1

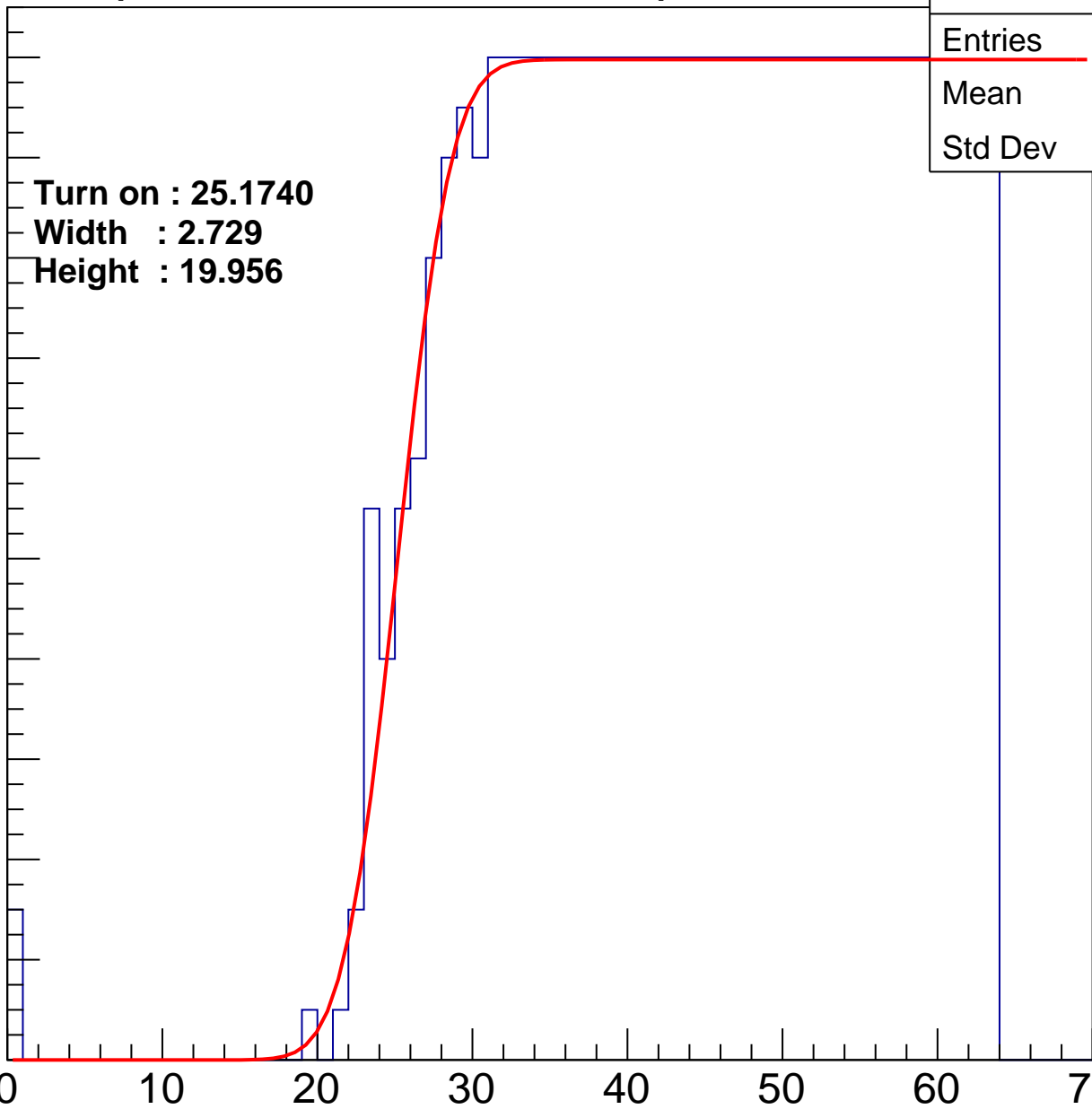
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1740
Width : 2.729
Height : 19.956

Entries	781
Mean	43.77
Std Dev	11.71

ampl



B0L101S, U21-ch33

calib_packv5_042523_0143.root, FC#1, port C1

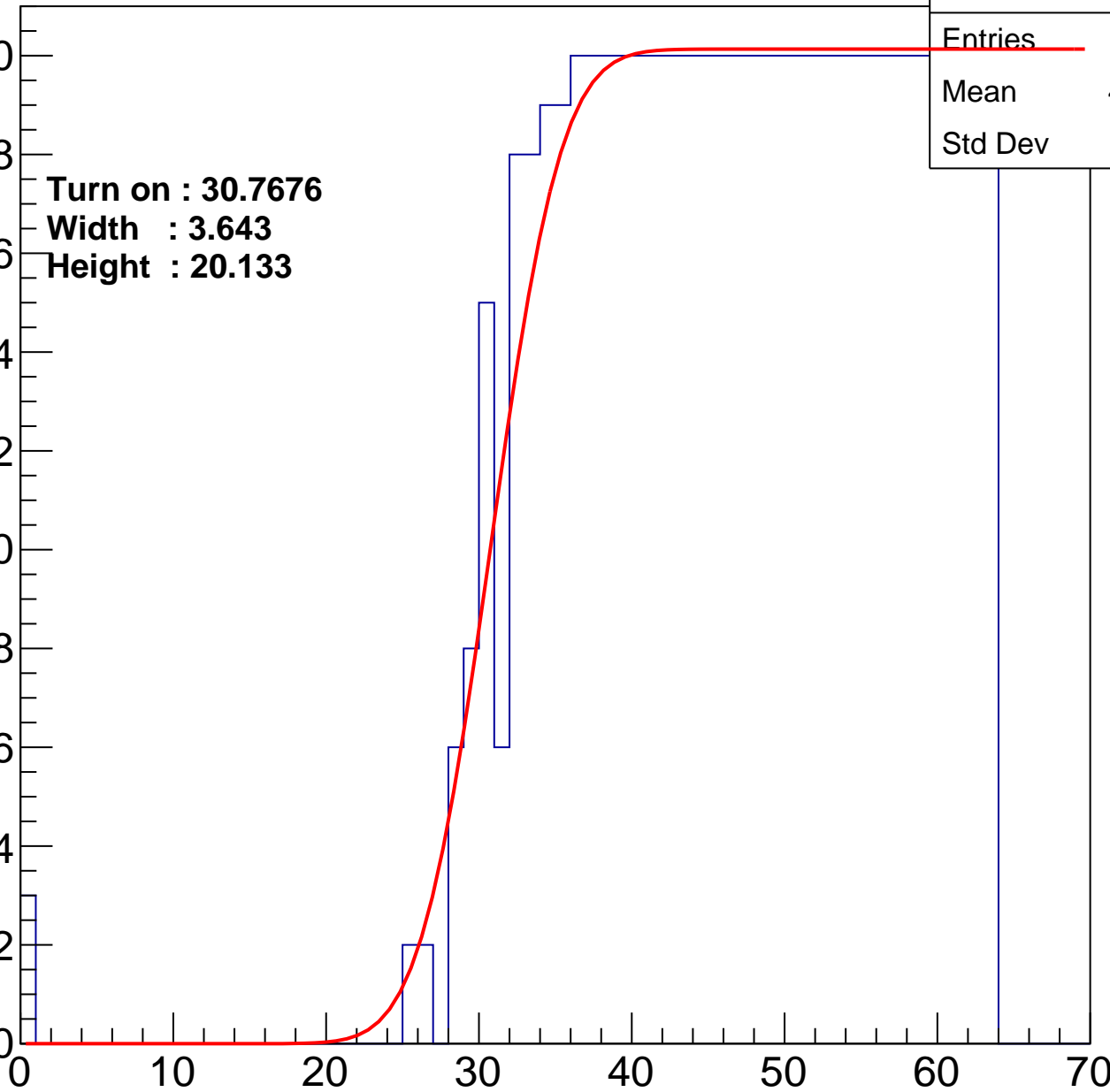
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 30.7676
Width : 3.643
Height : 20.133

Entries	676
Mean	46.36
Std Dev	10.35

ampl



B0L101S, U21-ch34

calib_packv5_042523_0143.root, FC#1, port C1

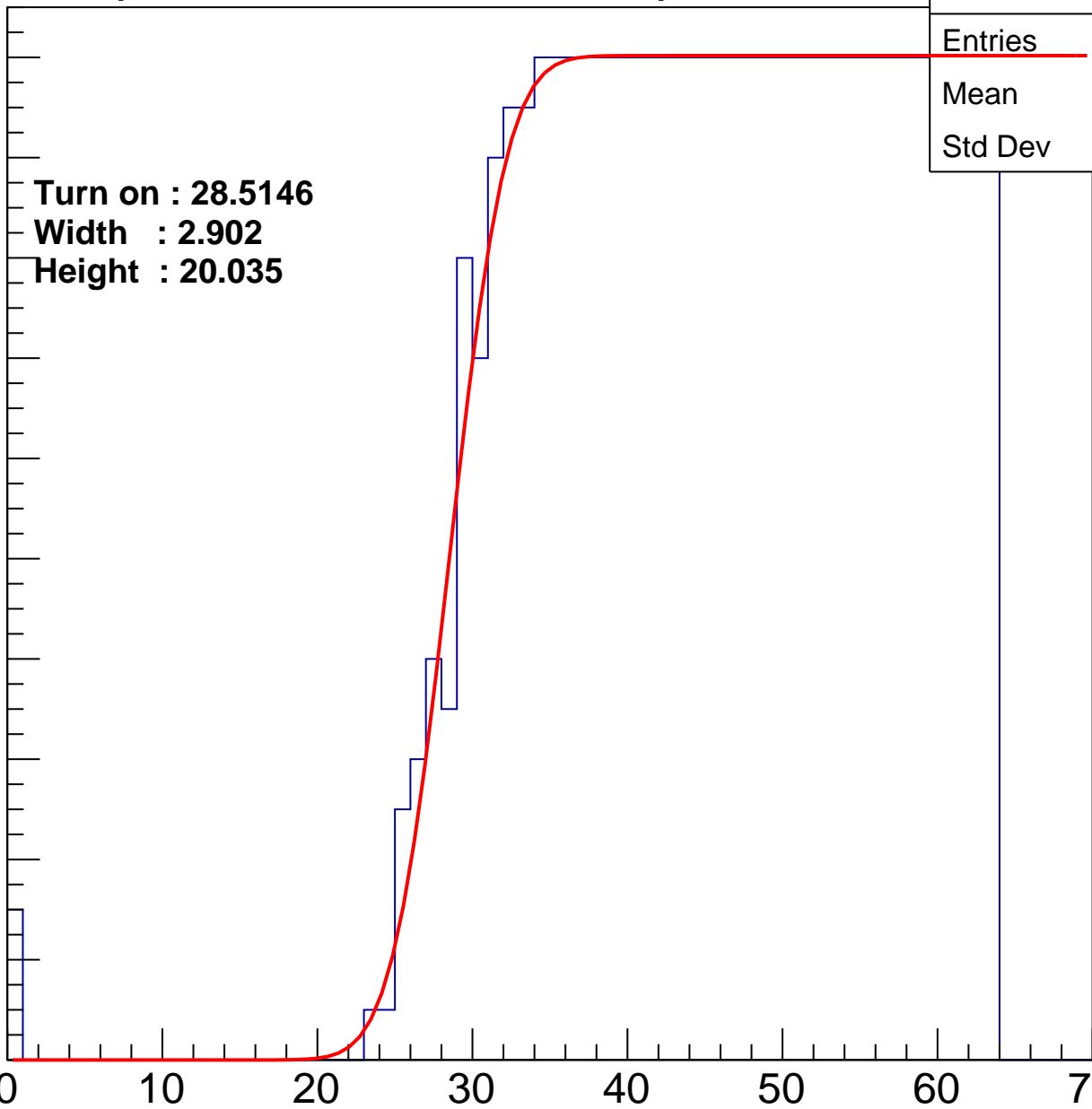
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5146
Width : 2.902
Height : 20.035

Entries	717
Mean	45.35
Std Dev	10.87

ampl



B0L101S, U21-ch35

calib_packv5_042523_0143.root, FC#1, port C1

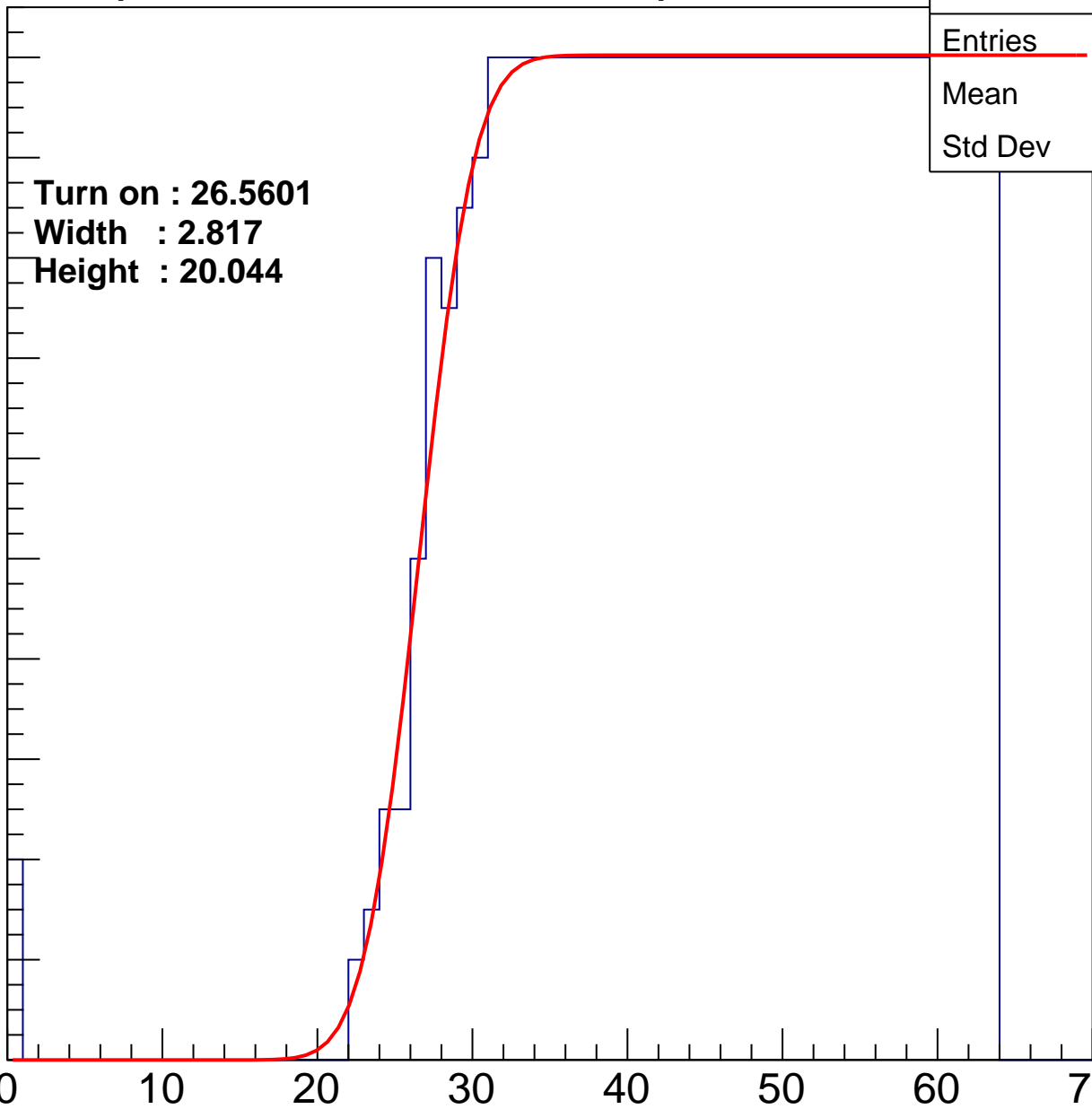
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5601
Width : 2.817
Height : 20.044

Entries	755
Mean	44.4
Std Dev	11.43

ampl



B0L101S, U21-ch36

calib_packv5_042523_0143.root, FC#1, port C1

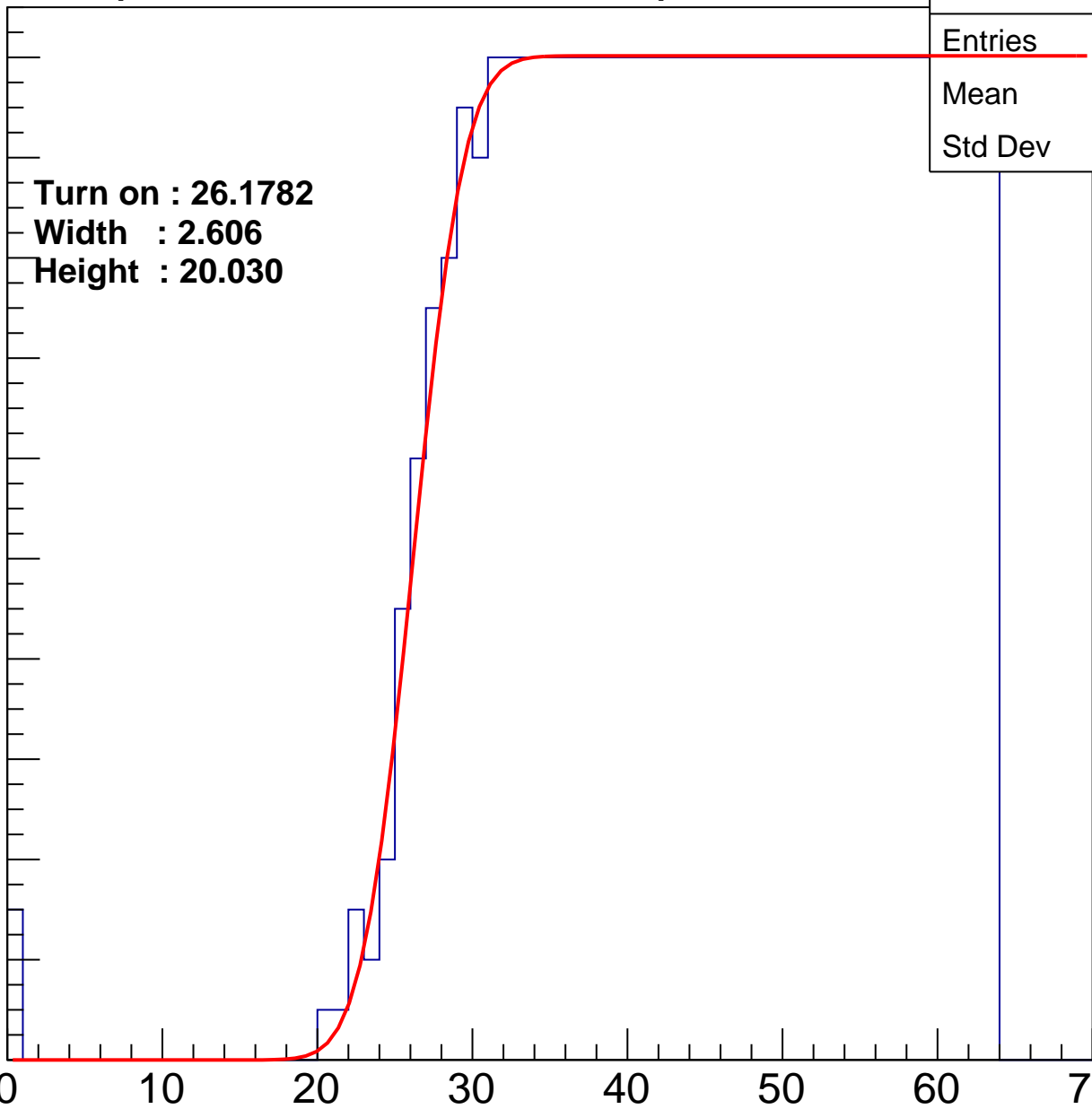
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1782
Width : 2.606
Height : 20.030

Entries	763
Mean	44.23
Std Dev	11.45

ampl



B0L101S, U21-ch37

calib_packv5_042523_0143.root, FC#1, port C1

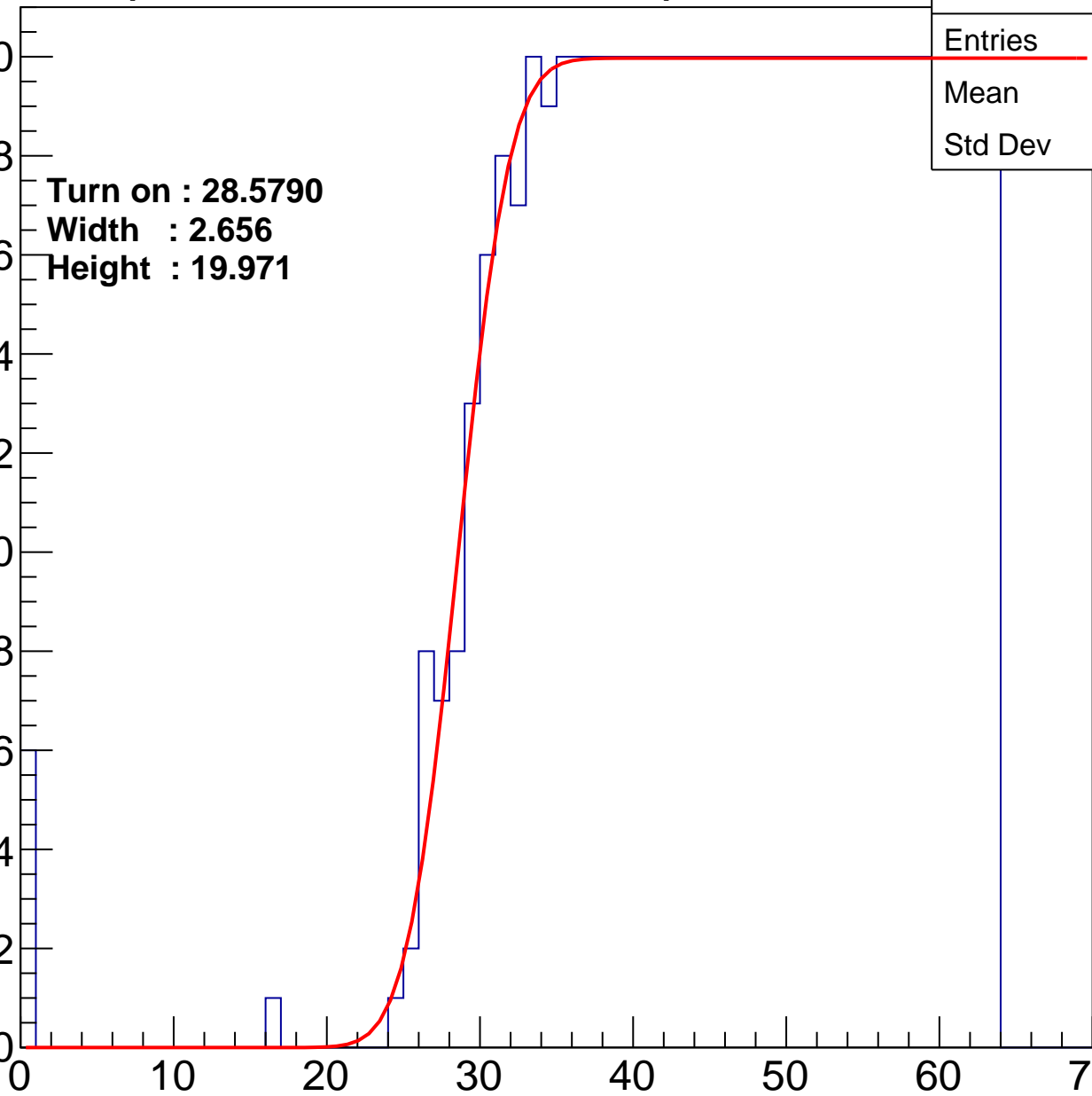
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5790
Width : 2.656
Height : 19.971

Entries	716
Mean	45.25
Std Dev	11.21

ampl



B0L101S, U21-ch38

calib_packv5_042523_0143.root, FC#1, port C1

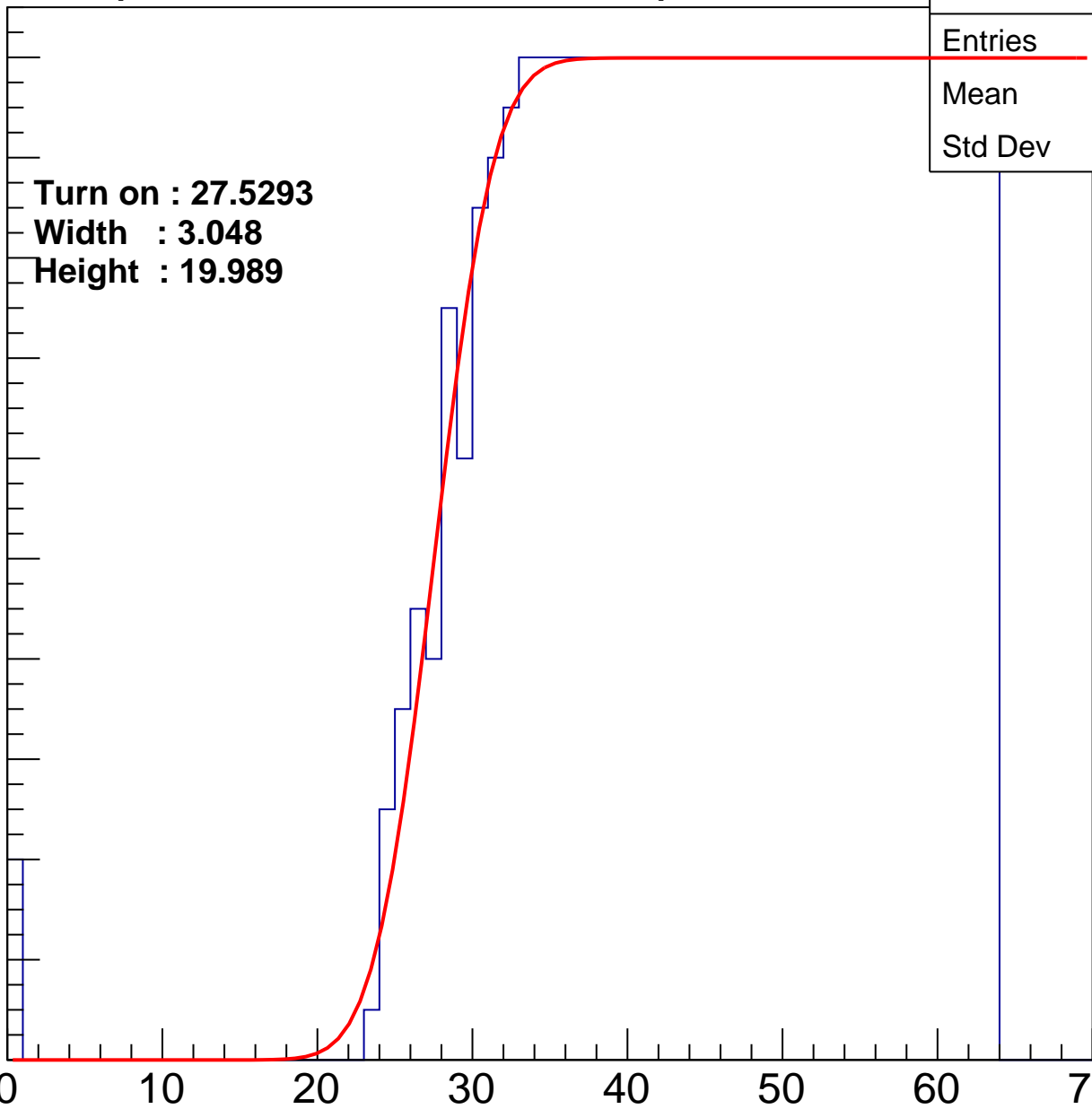
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5293
Width : 3.048
Height : 19.989

Entries	735
Mean	44.86
Std Dev	11.22

ampl



B0L101S, U21-ch39

calib_packv5_042523_0143.root, FC#1, port C1

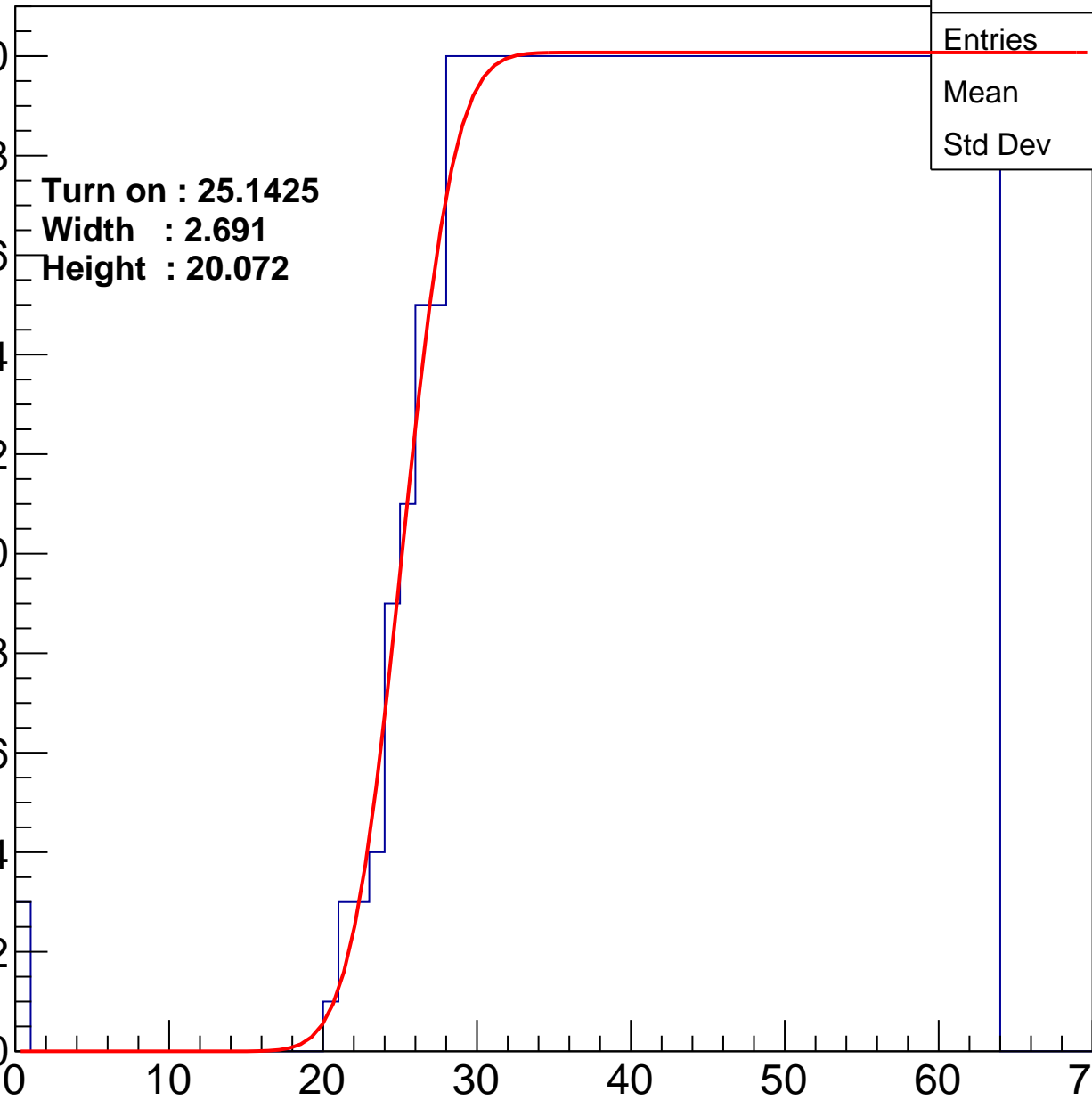
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1425
Width : 2.691
Height : 20.072

Entries	784
Mean	43.73
Std Dev	11.69

ampl



B0L101S, U21-ch40

calib_packv5_042523_0143.root, FC#1, port C1

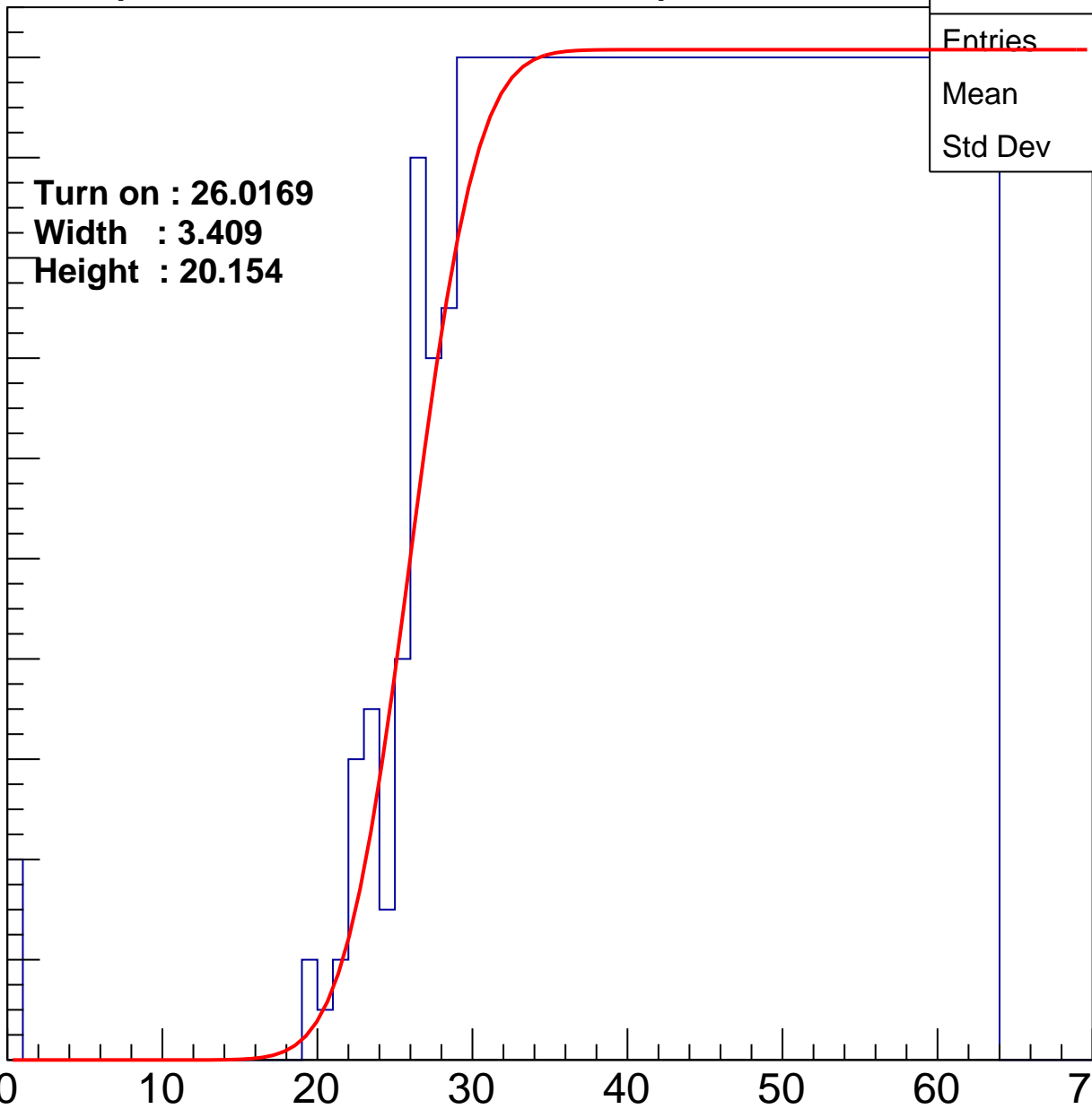
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0169
Width : 3.409
Height : 20.154

Entries	780
Mean	43.76
Std Dev	11.8

ampl



B0L101S, U21-ch41

calib_packv5_042523_0143.root, FC#1, port C1

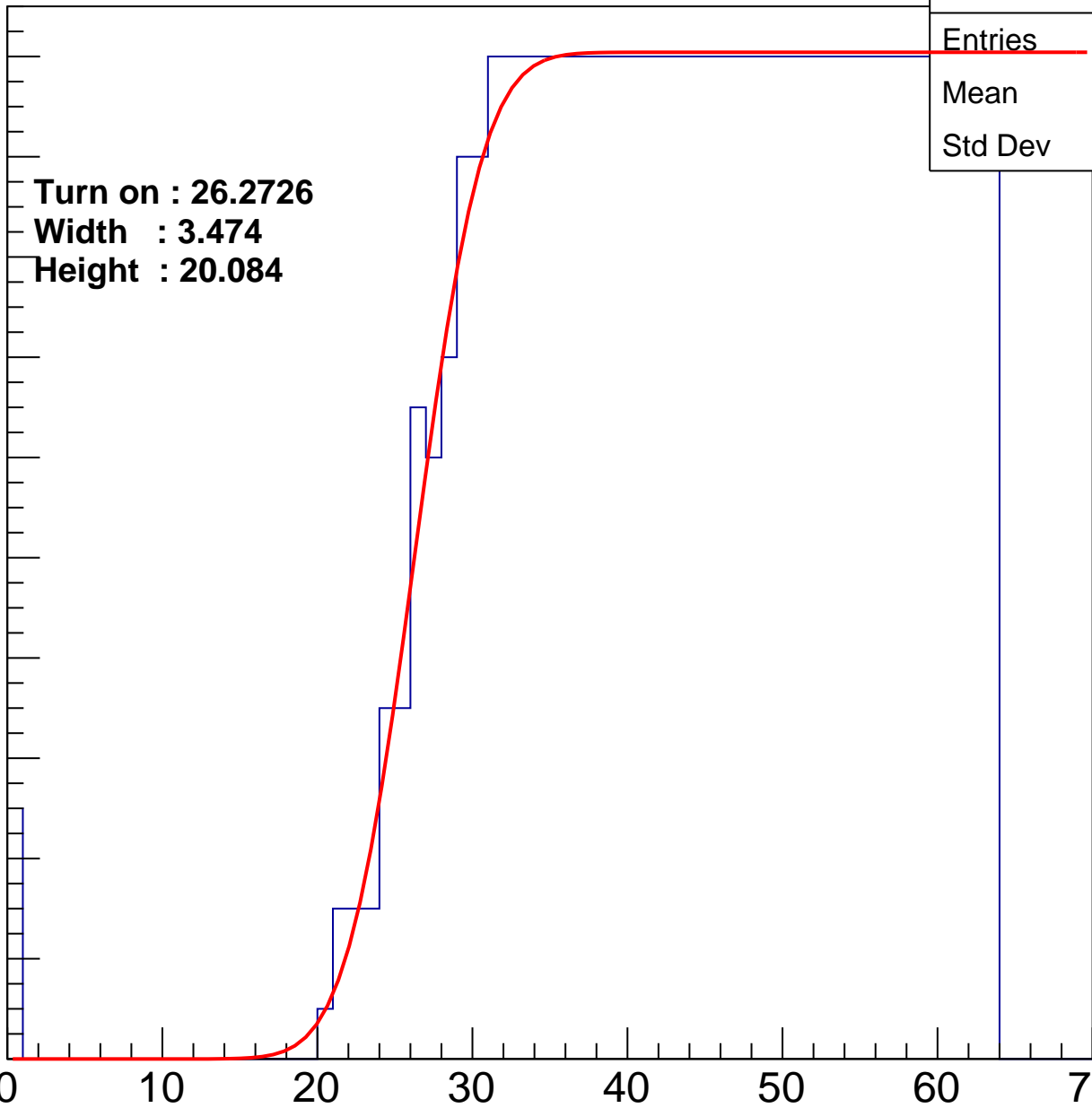
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2726
Width : 3.474
Height : 20.084

Entries	764
Mean	44.11
Std Dev	11.7

ampl



B0L101S, U21-ch42

calib_packv5_042523_0143.root, FC#1, port C1

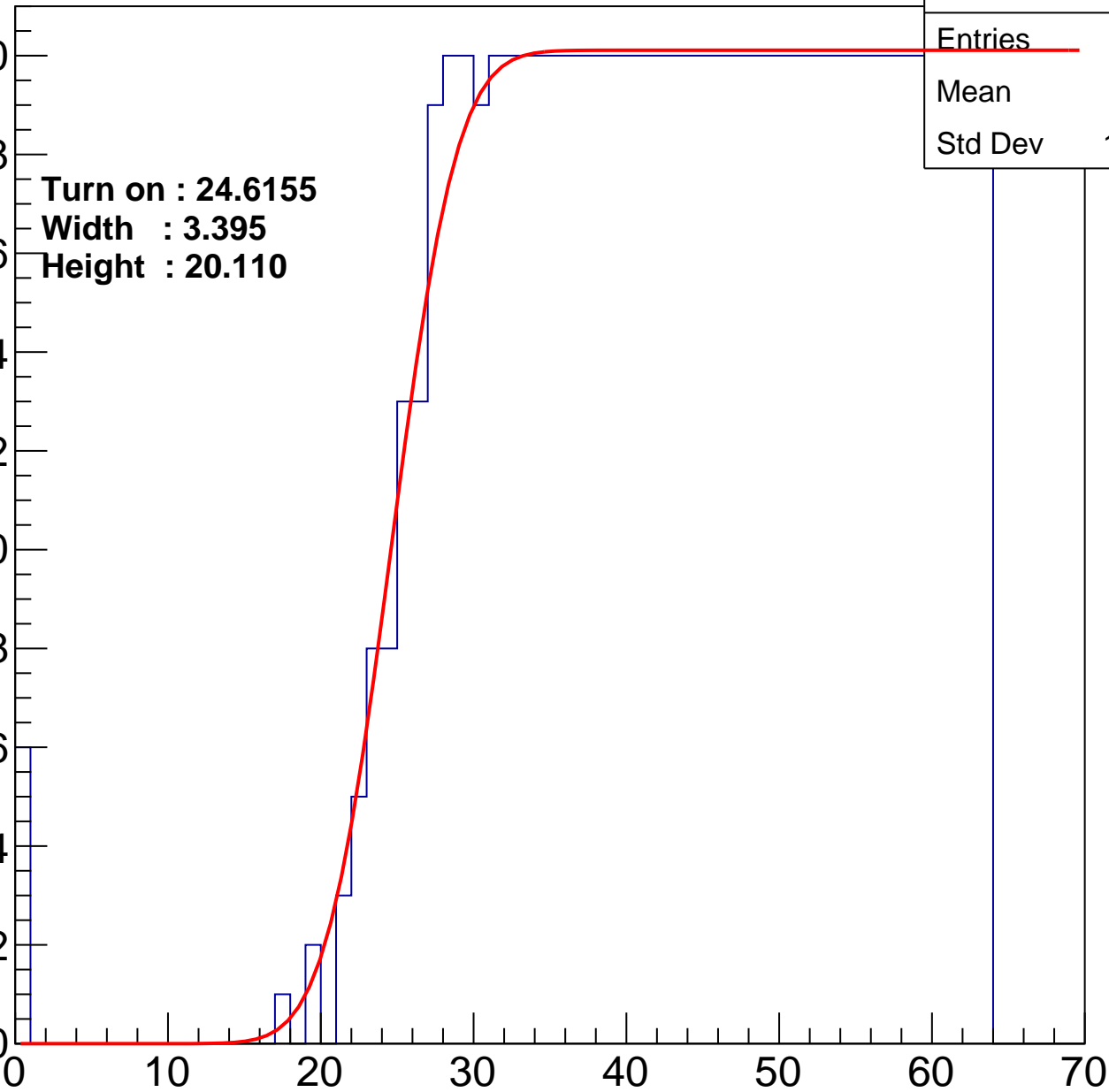
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6155
Width : 3.395
Height : 20.110

Entries	797
Mean	43.3
Std Dev	12.14

ampl



B0L101S, U21-ch43

calib_packv5_042523_0143.root, FC#1, port C1

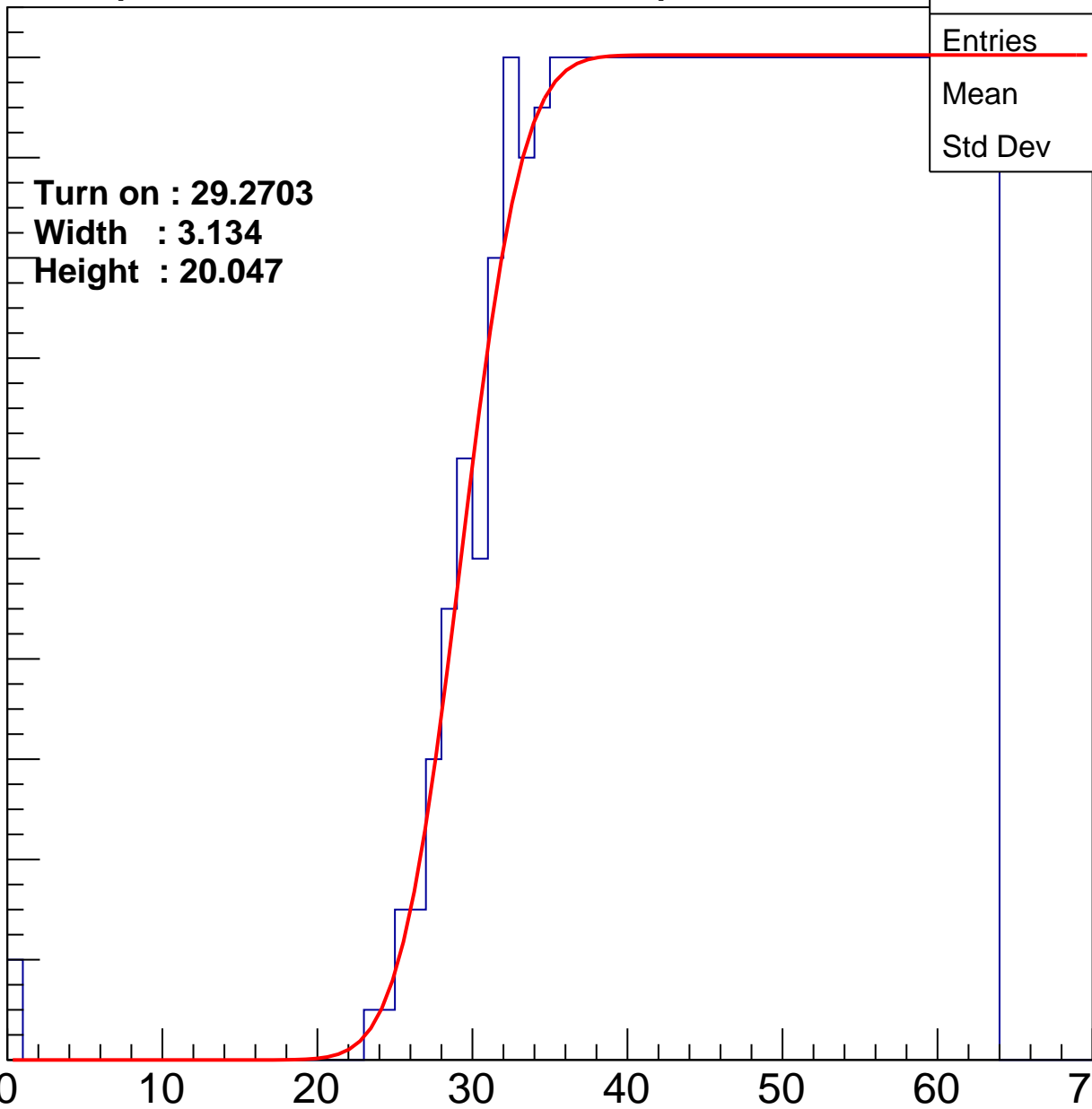
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.2703
Width : 3.134
Height : 20.047

Entries	700
Mean	45.8
Std Dev	10.55

ampl



B0L101S, U21-ch44

calib_packv5_042523_0143.root, FC#1, port C1

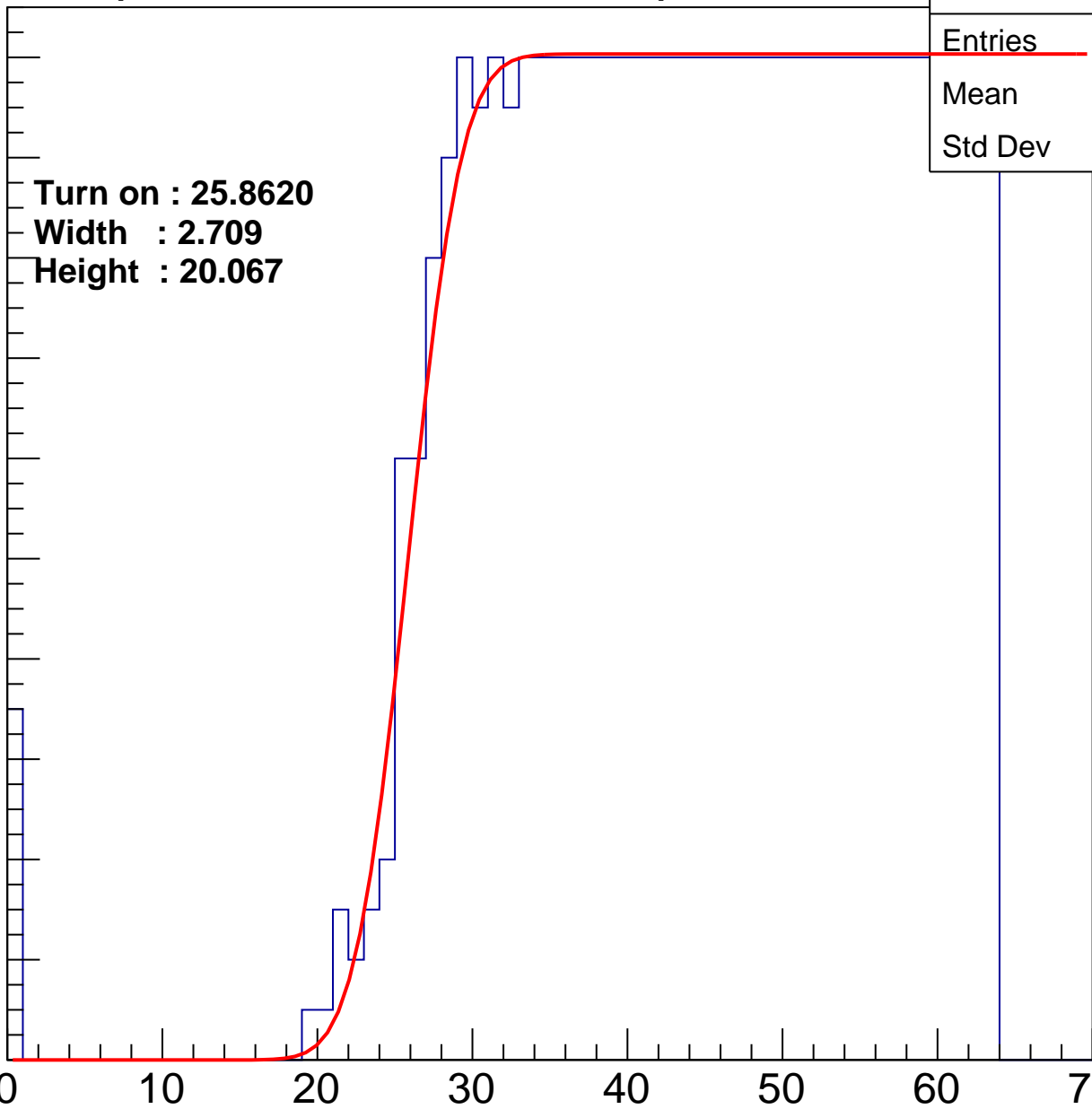
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8620
Width : 2.709
Height : 20.067

Entries	777
Mean	43.75
Std Dev	11.98

ampl



B0L101S, U21-ch45

calib_packv5_042523_0143.root, FC#1, port C1

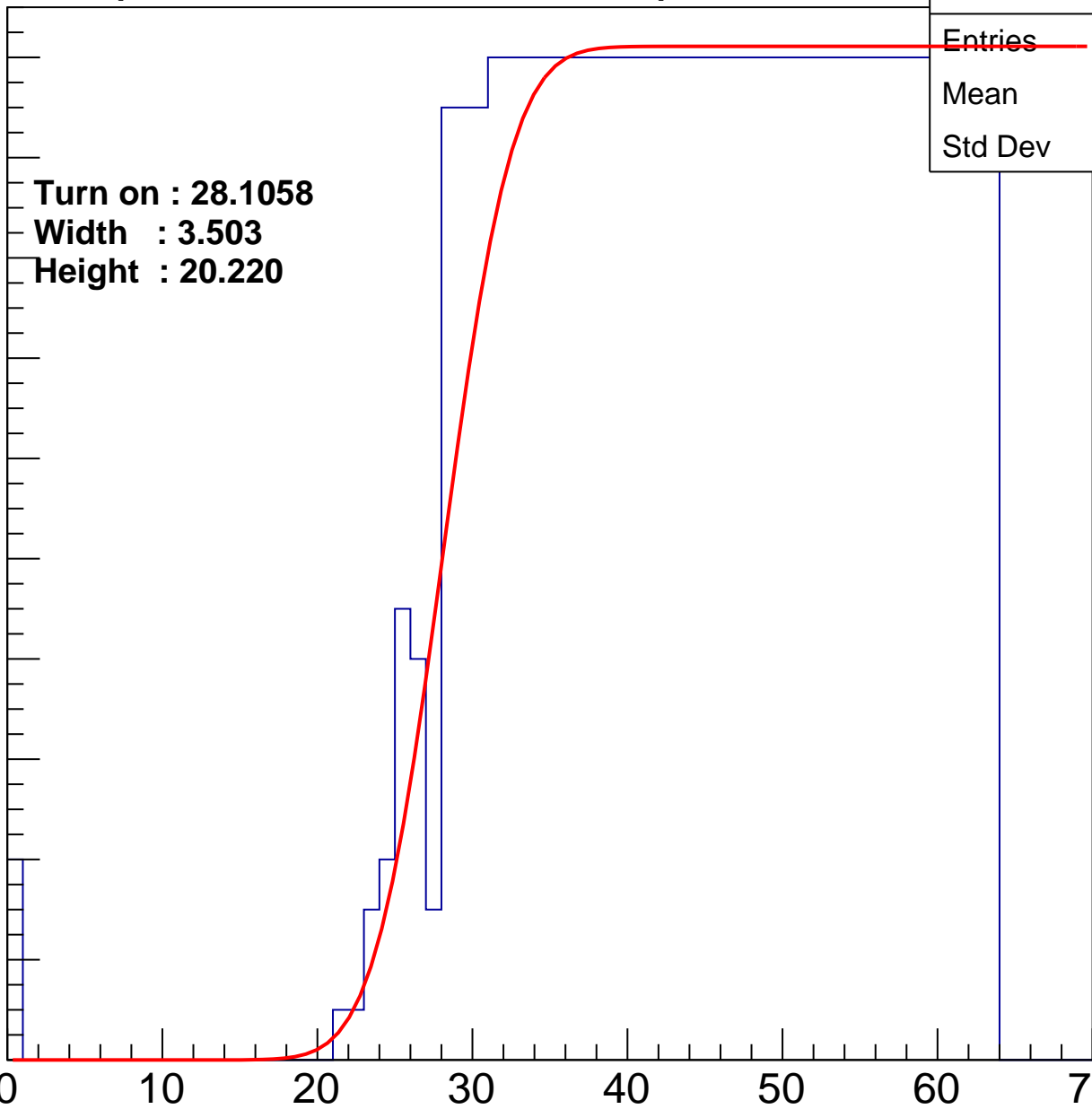
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1058
Width : 3.503
Height : 20.220

Entries	750
Mean	44.53
Std Dev	11.36

ampl



B0L101S, U21-ch46

calib_packv5_042523_0143.root, FC#1, port C1

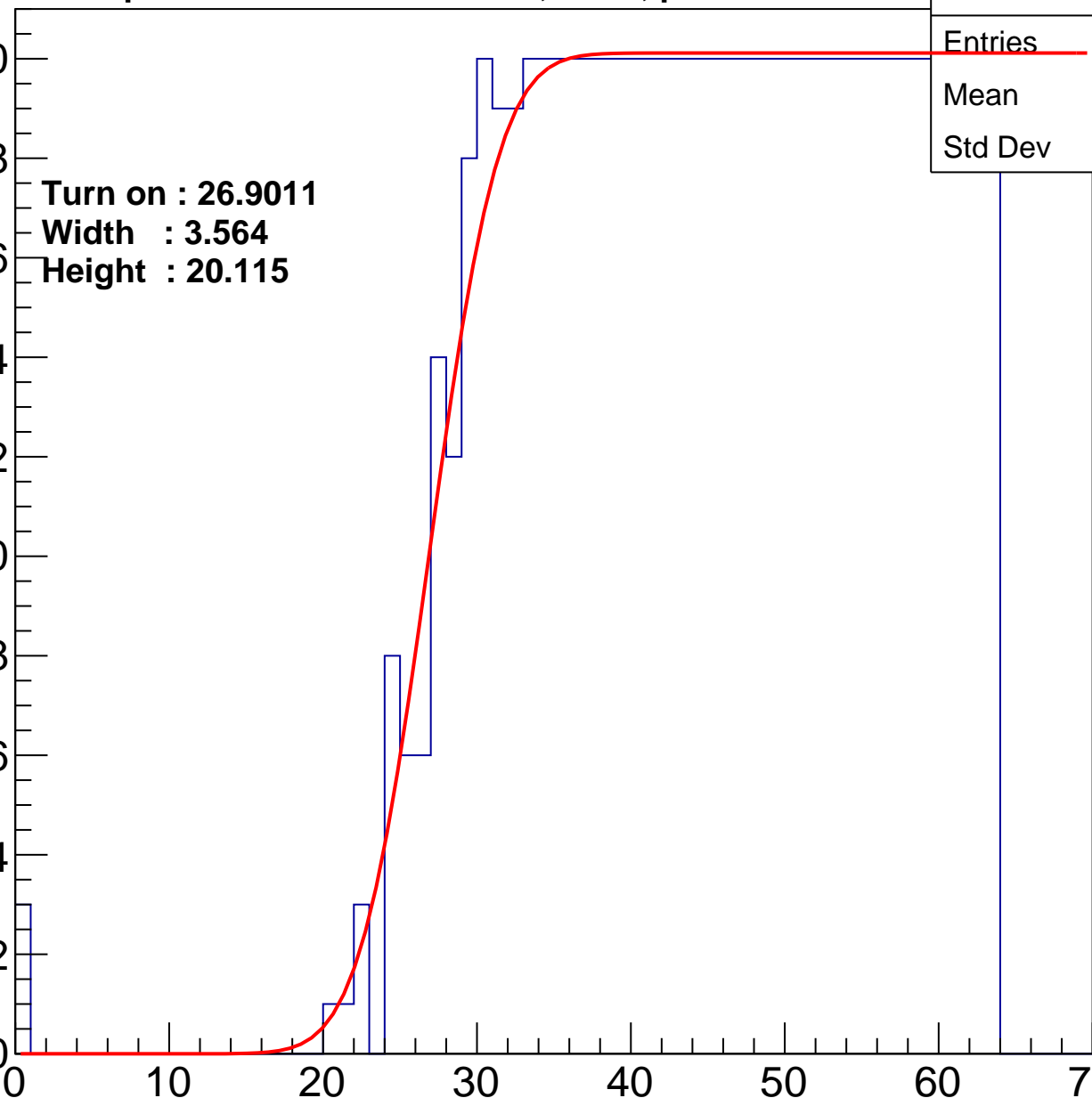
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9011
Width : 3.564
Height : 20.115

Entries	750
Mean	44.53
Std Dev	11.31

ampl



B0L101S, U21-ch47

calib_packv5_042523_0143.root, FC#1, port C1

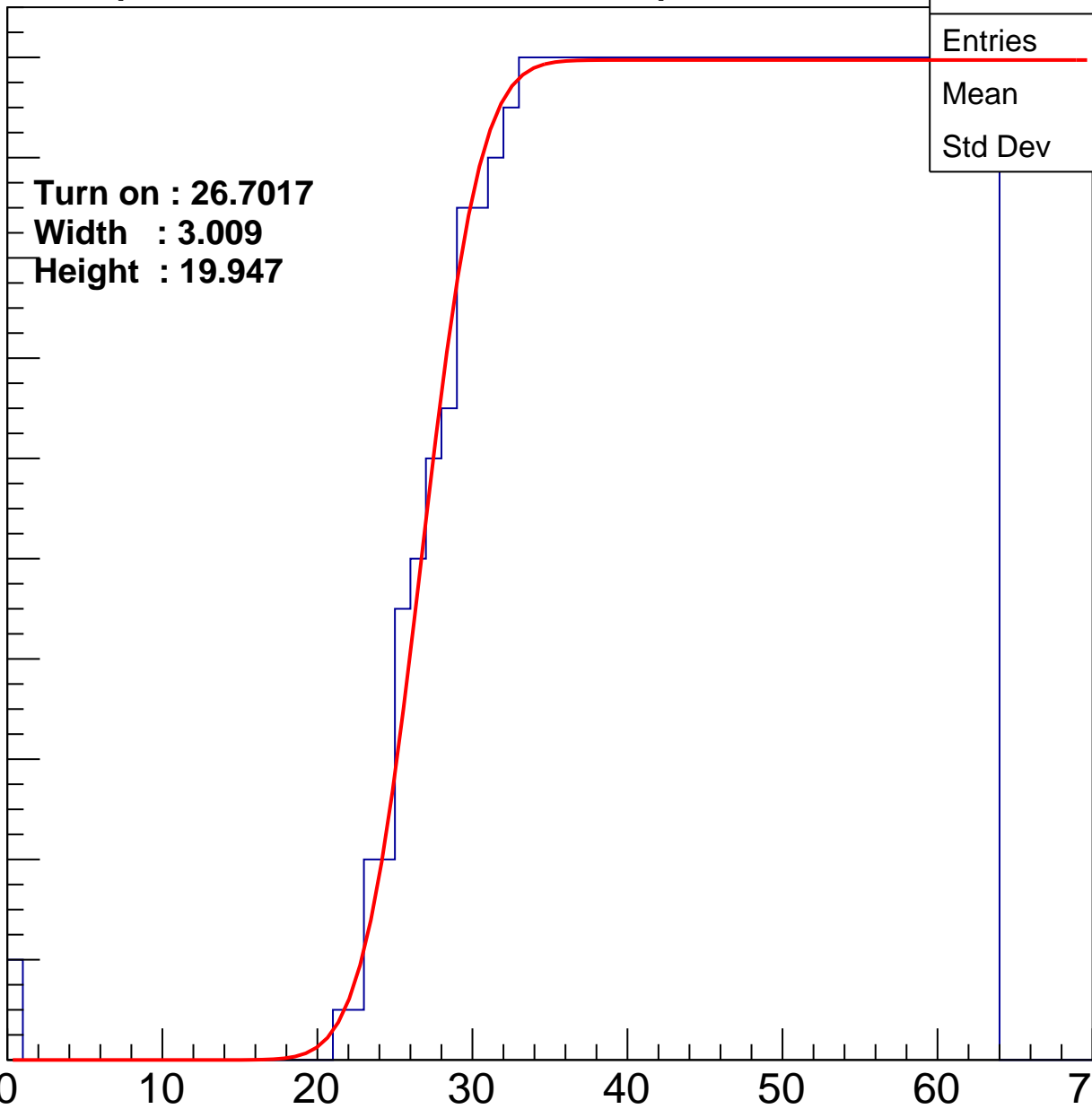
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7017
Width : 3.009
Height : 19.947

Entries	747
Mean	44.62
Std Dev	11.2

ampl



B0L101S, U21-ch48

calib_packv5_042523_0143.root, FC#1, port C1

Entry

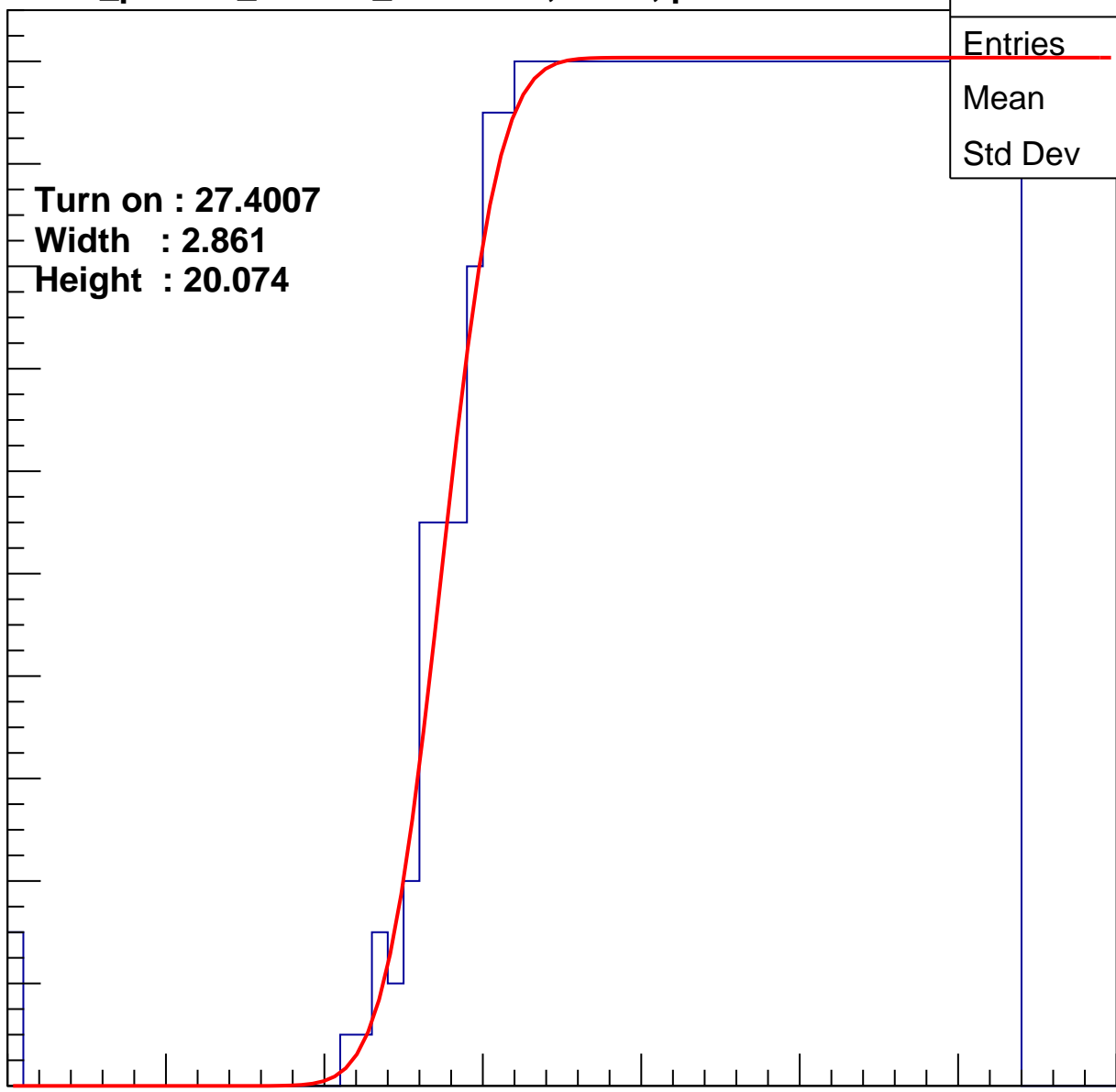
20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4007
Width : 2.861
Height : 20.074

Entries	741
Mean	44.77
Std Dev	11.17

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch49

calib_packv5_042523_0143.root, FC#1, port C1

Entry

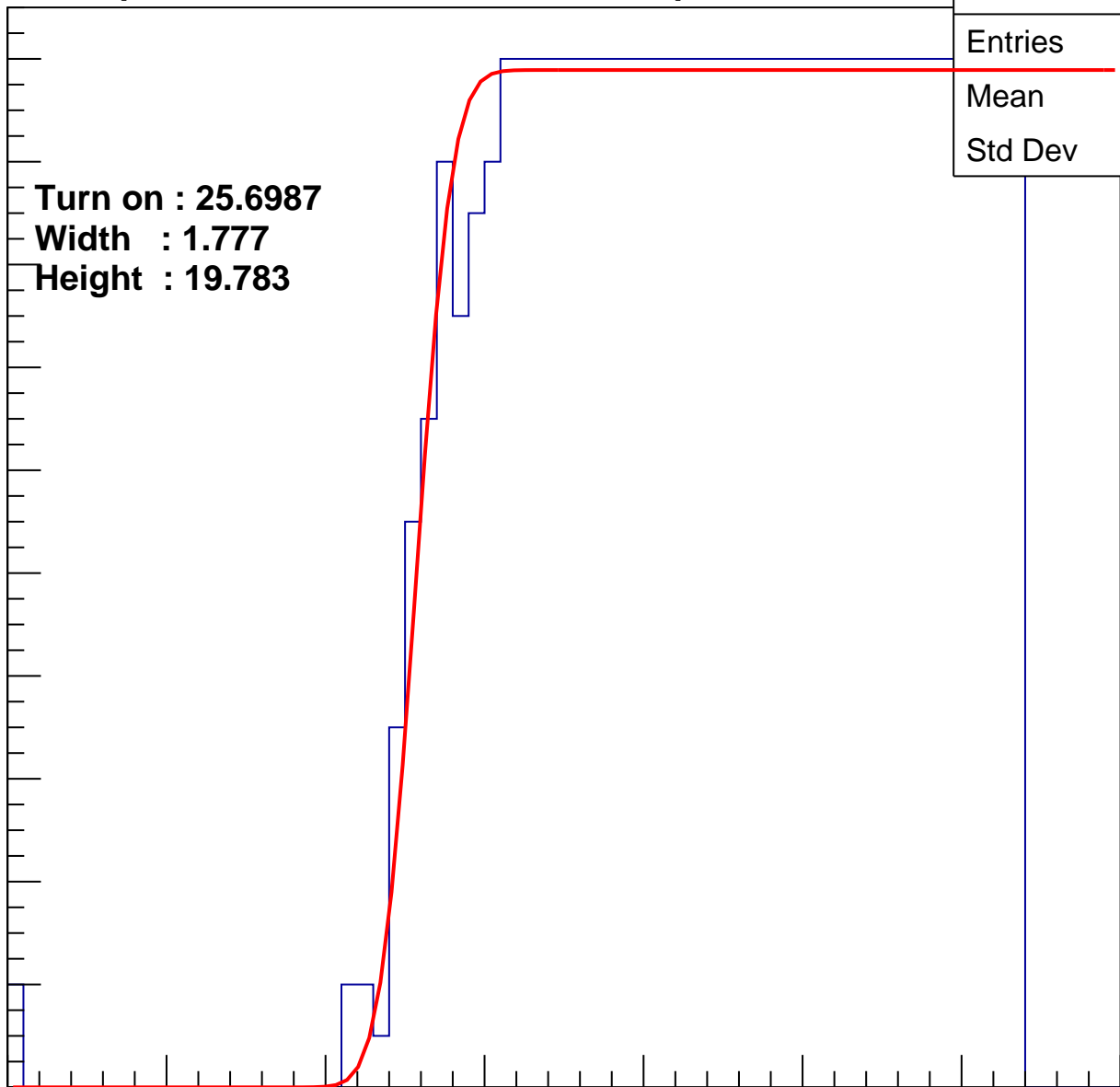
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6987
Width : 1.777
Height : 19.783

Entries	766
Mean	44.19
Std Dev	11.4

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch50

calib_packv5_042523_0143.root, FC#1, port C1

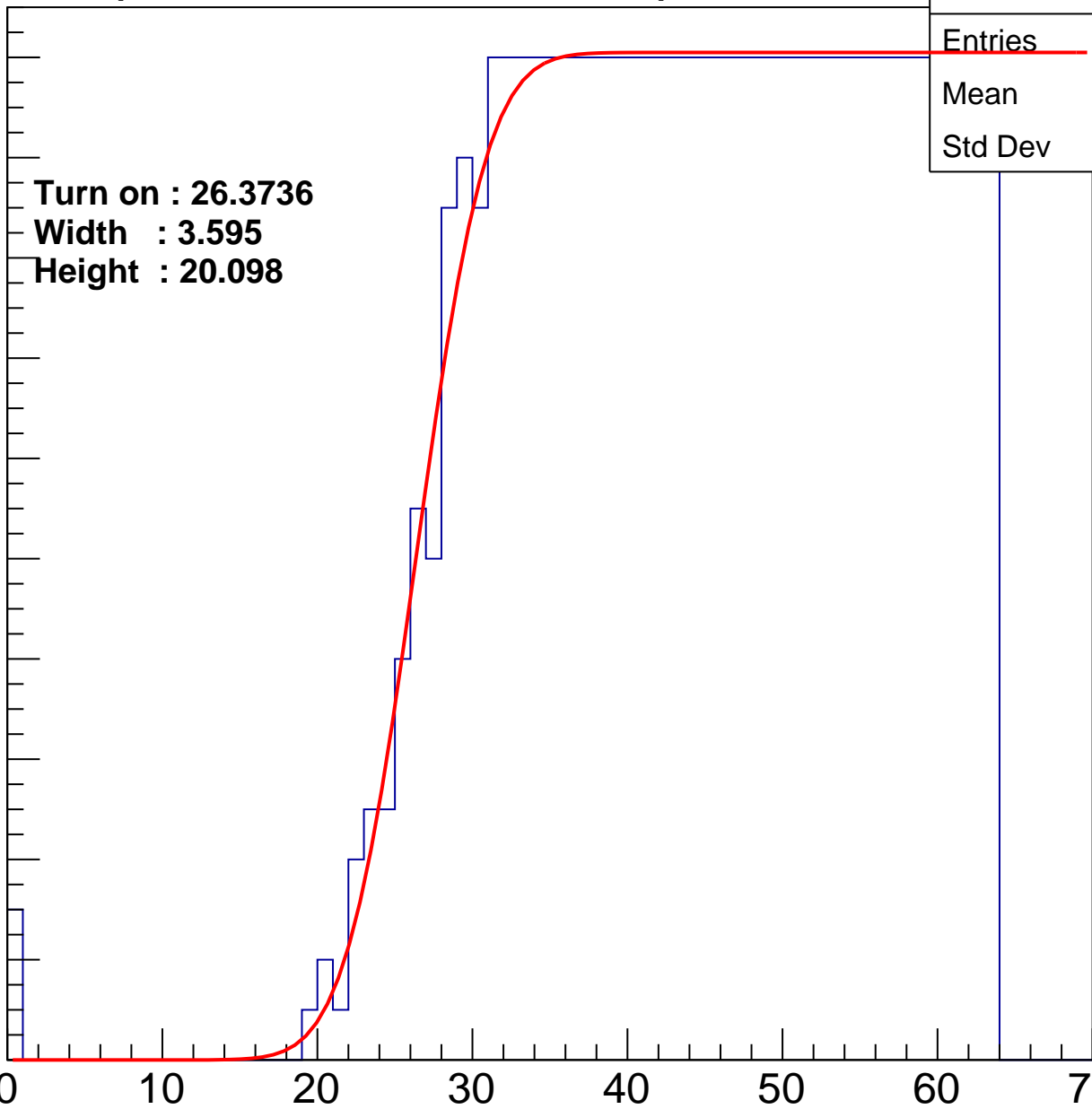
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3736
Width : 3.595
Height : 20.098

Entries	762
Mean	44.21
Std Dev	11.52

ampl



B0L101S, U21-ch51

calib_packv5_042523_0143.root, FC#1, port C1

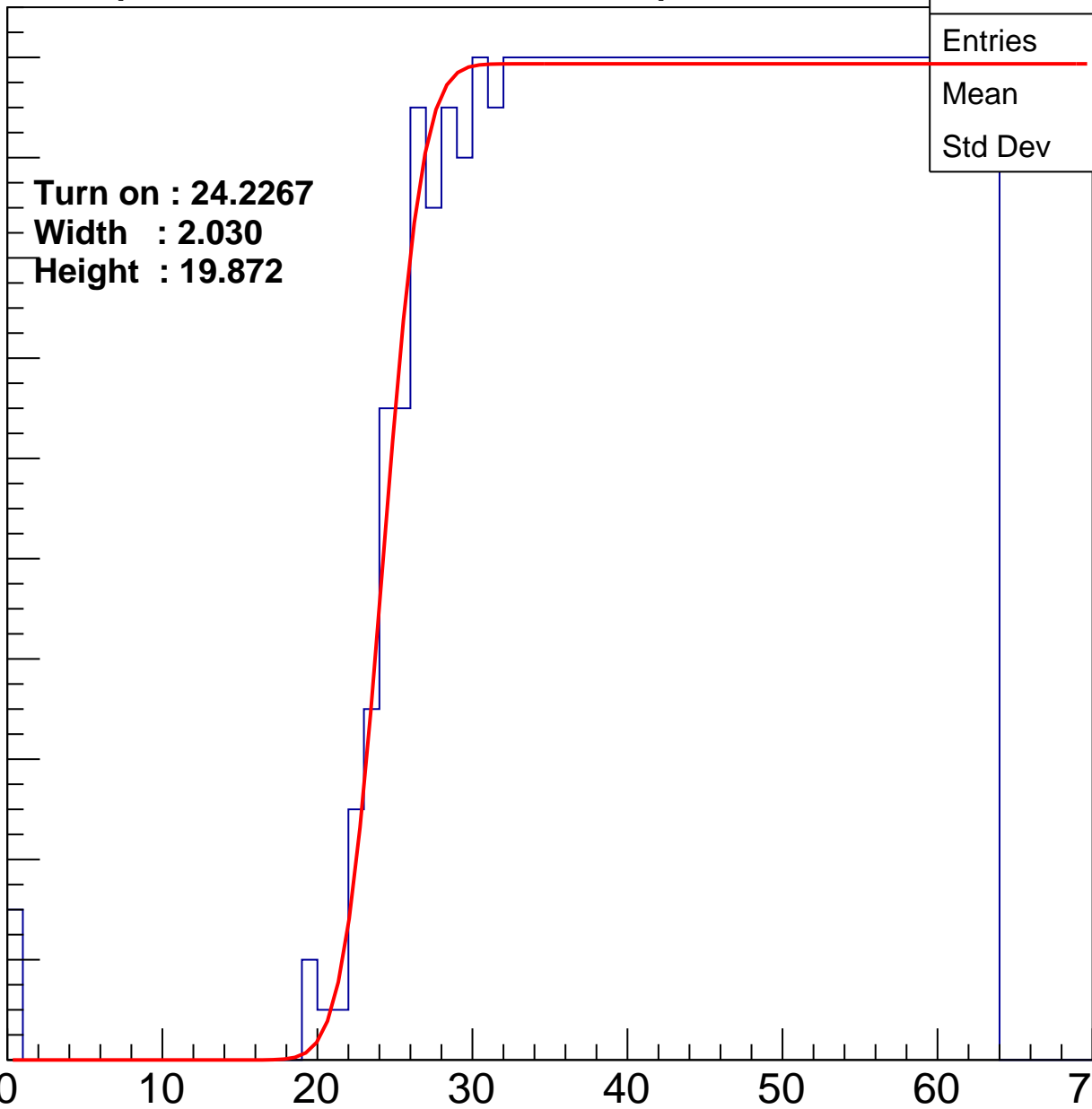
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.2267
Width : 2.030
Height : 19.872

Entries	797
Mean	43.39
Std Dev	11.9

ampl



B0L101S, U21-ch52

calib_packv5_042523_0143.root, FC#1, port C1

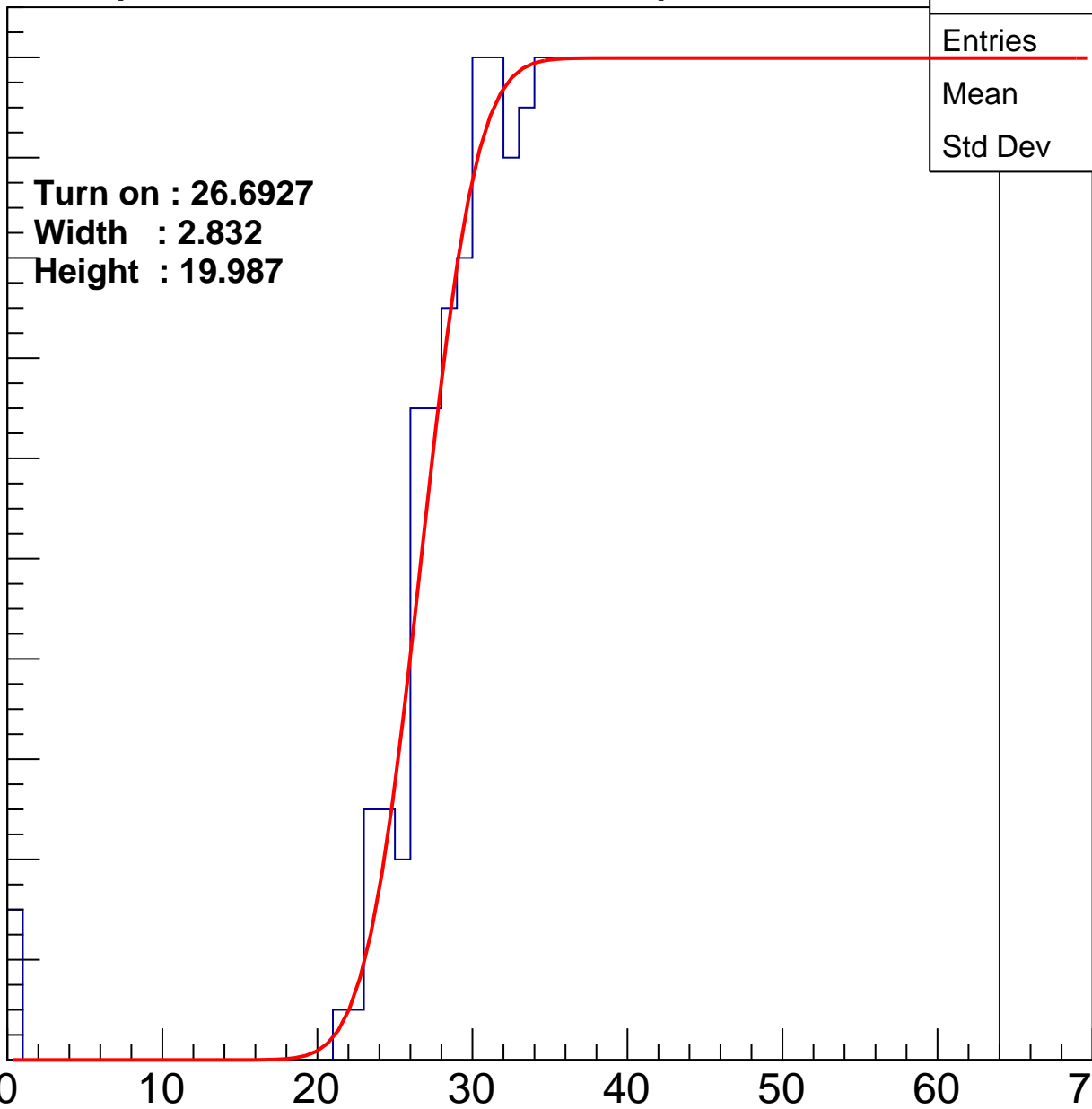
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6927
Width : 2.832
Height : 19.987

Entries	753
Mean	44.45
Std Dev	11.35

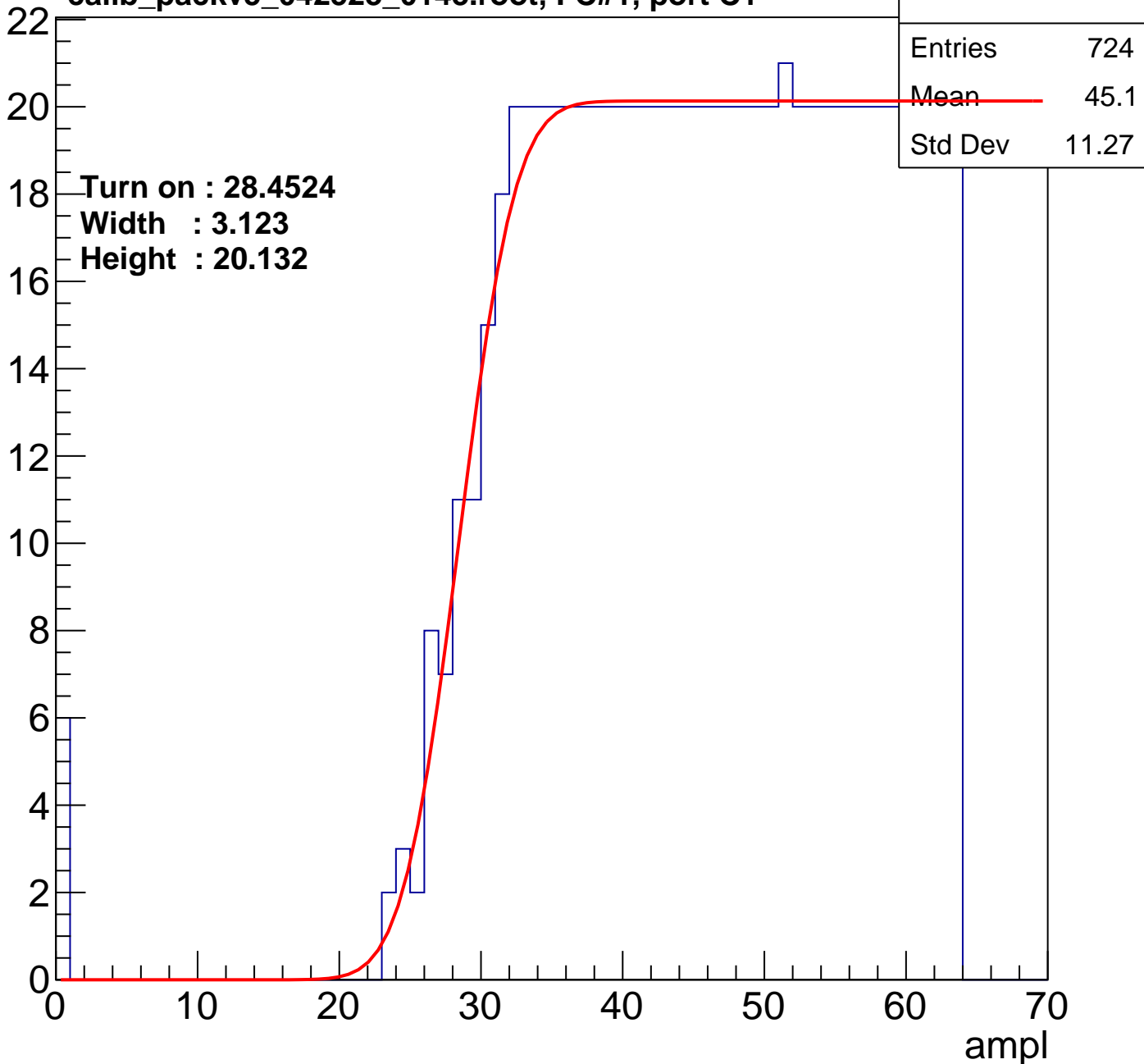
ampl



B0L101S, U21-ch53

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch54

calib_packv5_042523_0143.root, FC#1, port C1

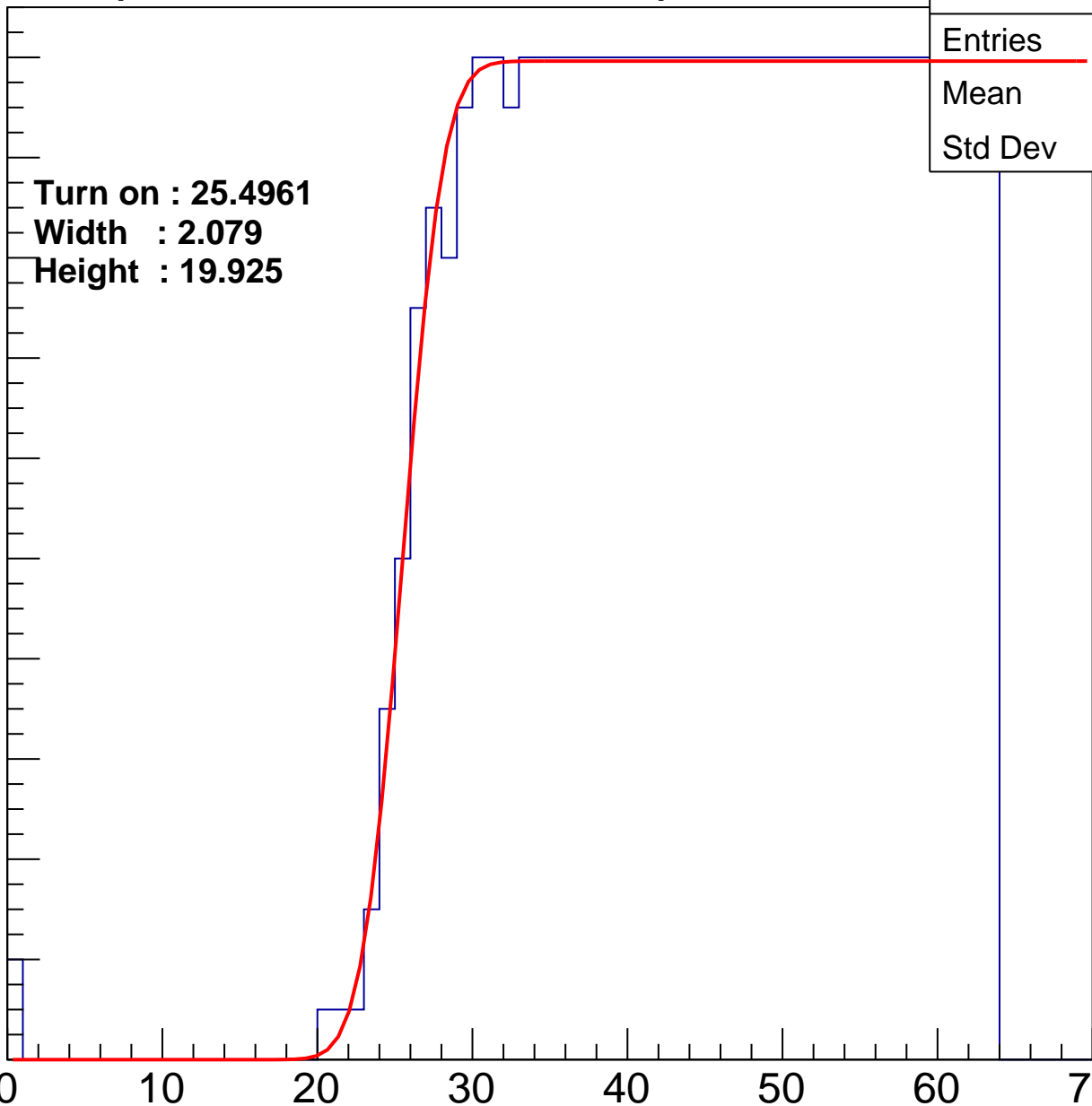
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4961
Width : 2.079
Height : 19.925

Entries	771
Mean	44.08
Std Dev	11.44

ampl



B0L101S, U21-ch55

calib_packv5_042523_0143.root, FC#1, port C1

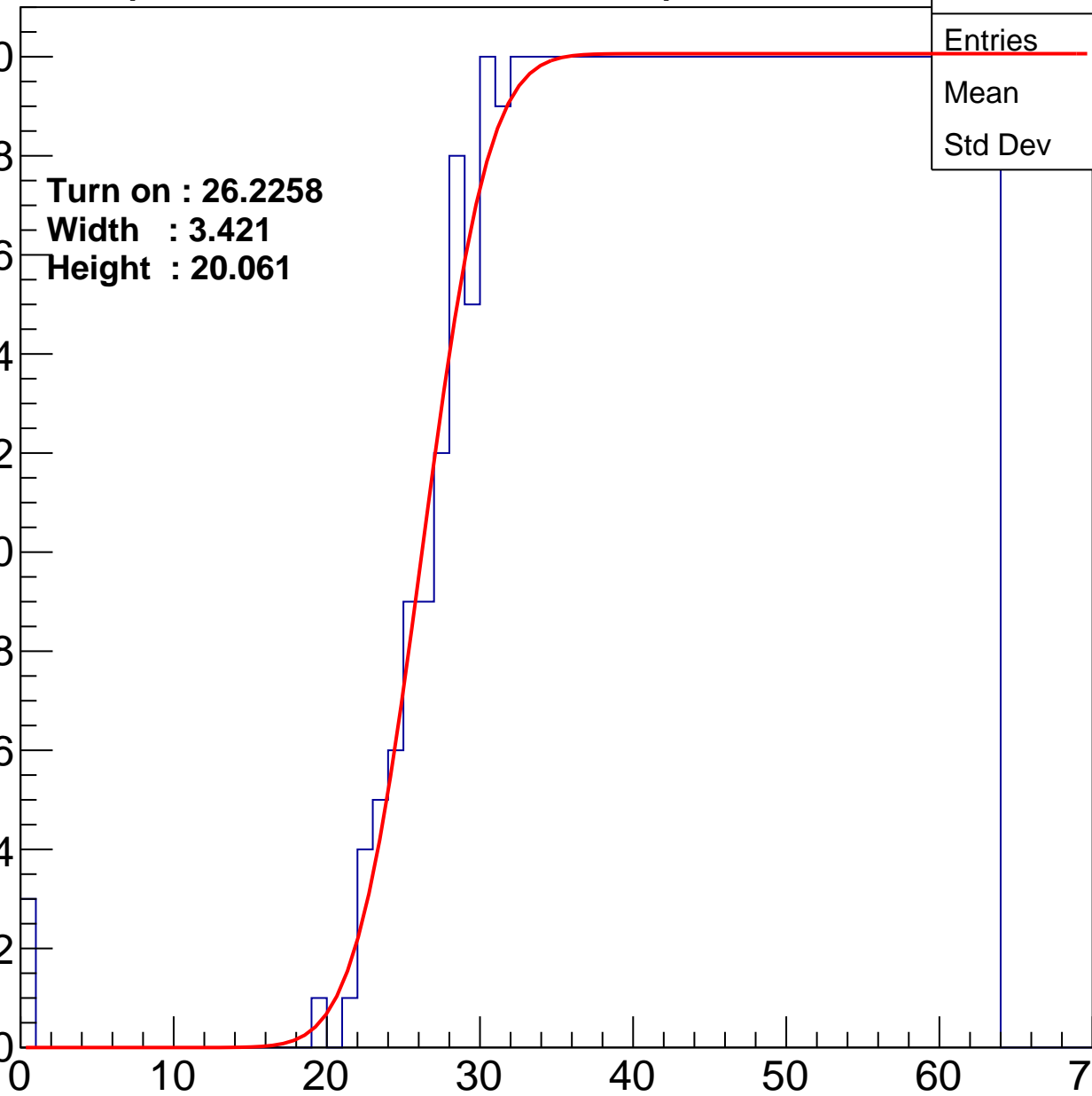
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2258
Width : 3.421
Height : 20.061

Entries	762
Mean	44.22
Std Dev	11.49

ampl



B0L101S, U21-ch56

calib_packv5_042523_0143.root, FC#1, port C1

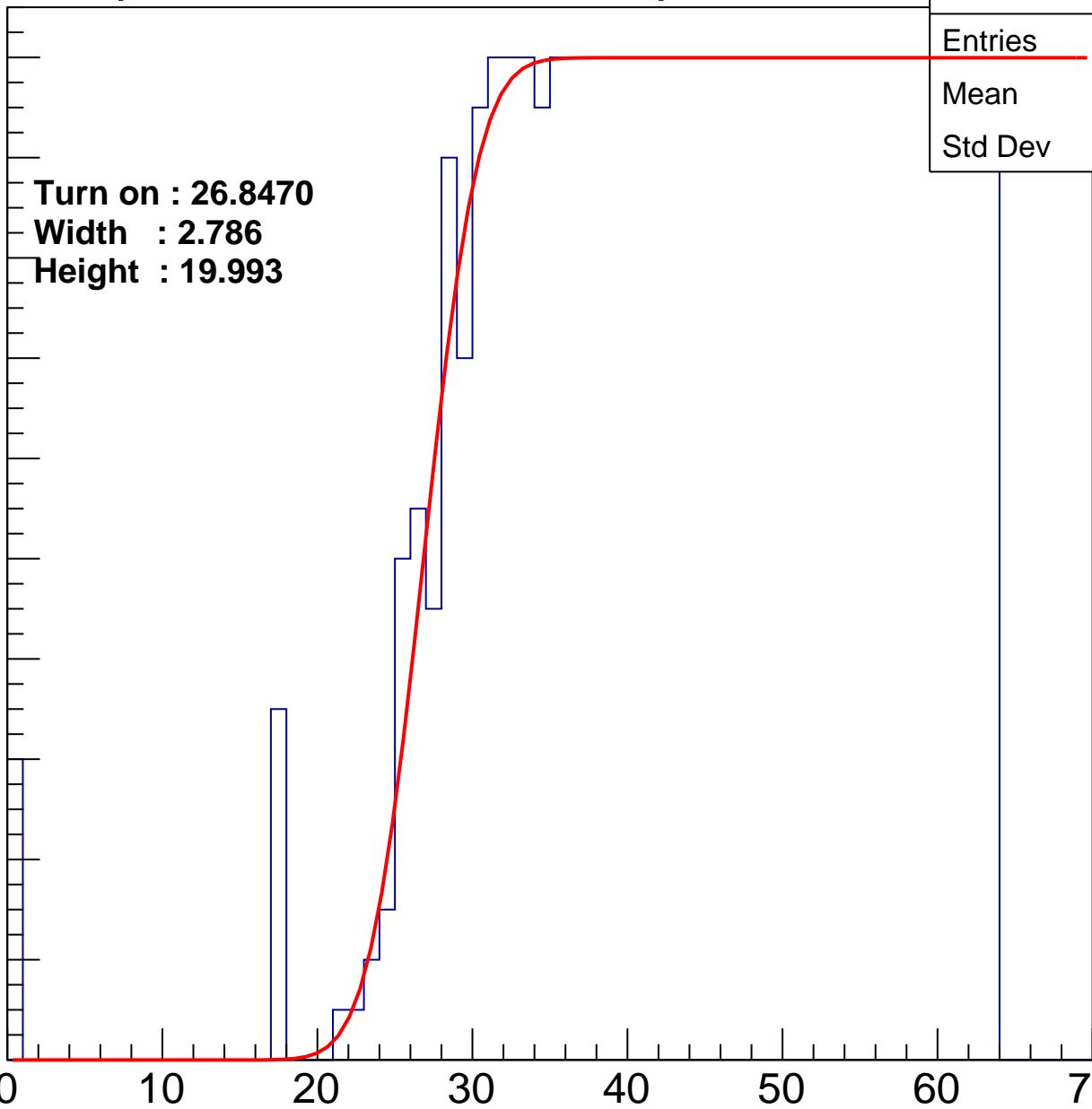
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8470
Width : 2.786
Height : 19.993

Entries	760
Mean	44.11
Std Dev	11.85

ampl



B0L101S, U21-ch57

calib_packv5_042523_0143.root, FC#1, port C1

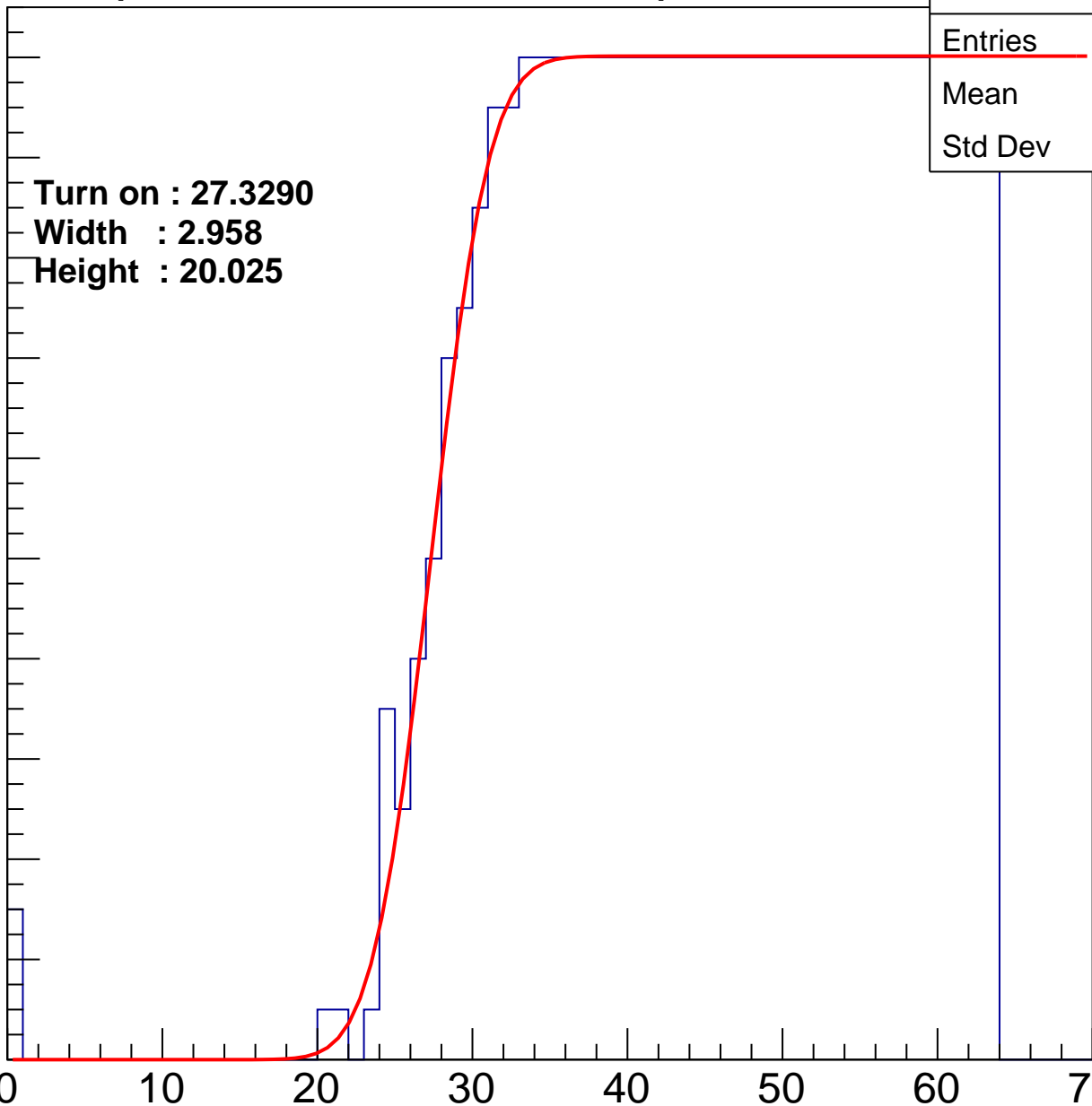
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3290
Width : 2.958
Height : 20.025

Entries	740
Mean	44.77
Std Dev	11.2

ampl



B0L101S, U21-ch58

calib_packv5_042523_0143.root, FC#1, port C1

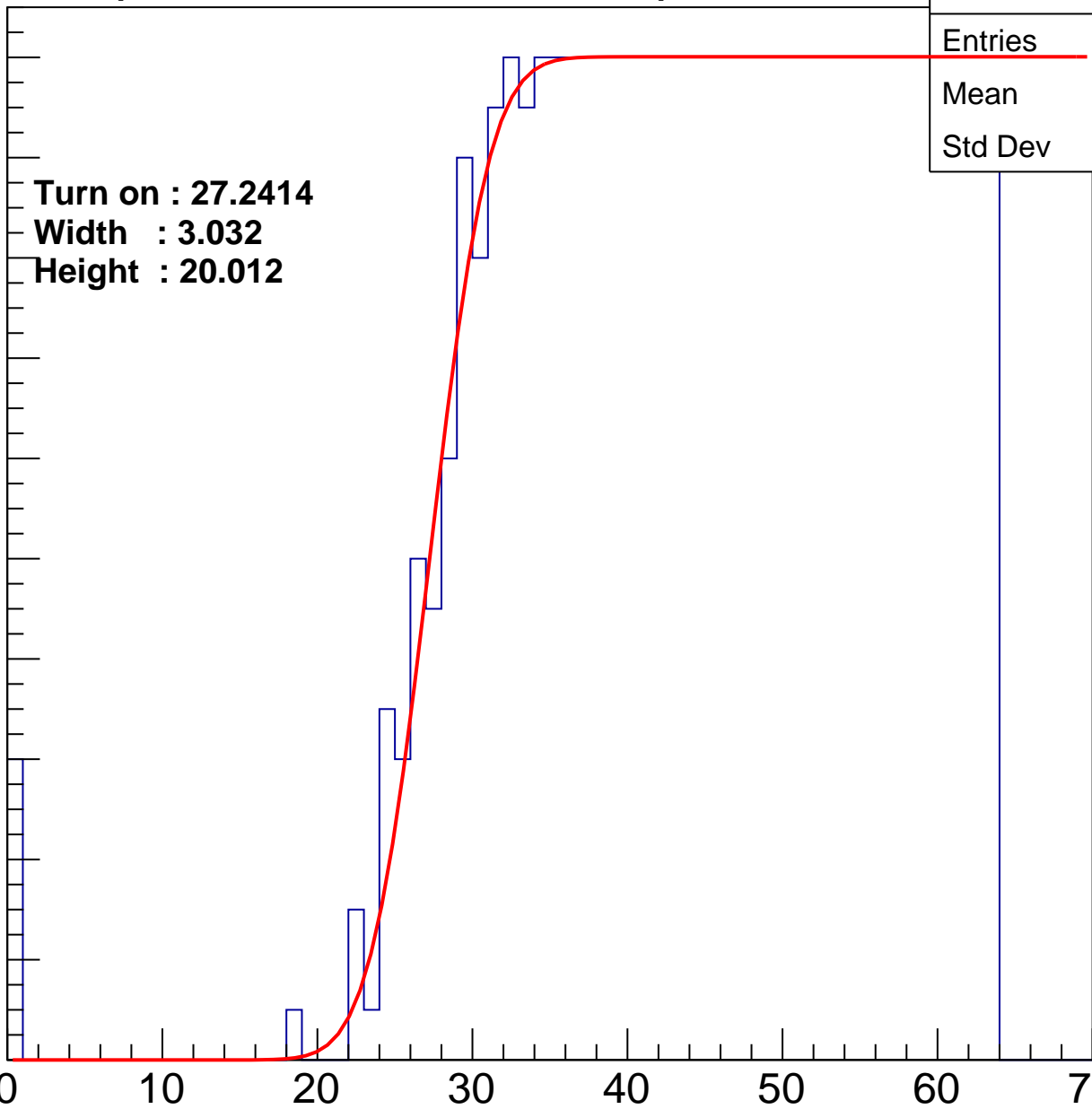
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2414
Width : 3.032
Height : 20.012

Entries	747
Mean	44.47
Std Dev	11.6

ampl



B0L101S, U21-ch59

calib_packv5_042523_0143.root, FC#1, port C1

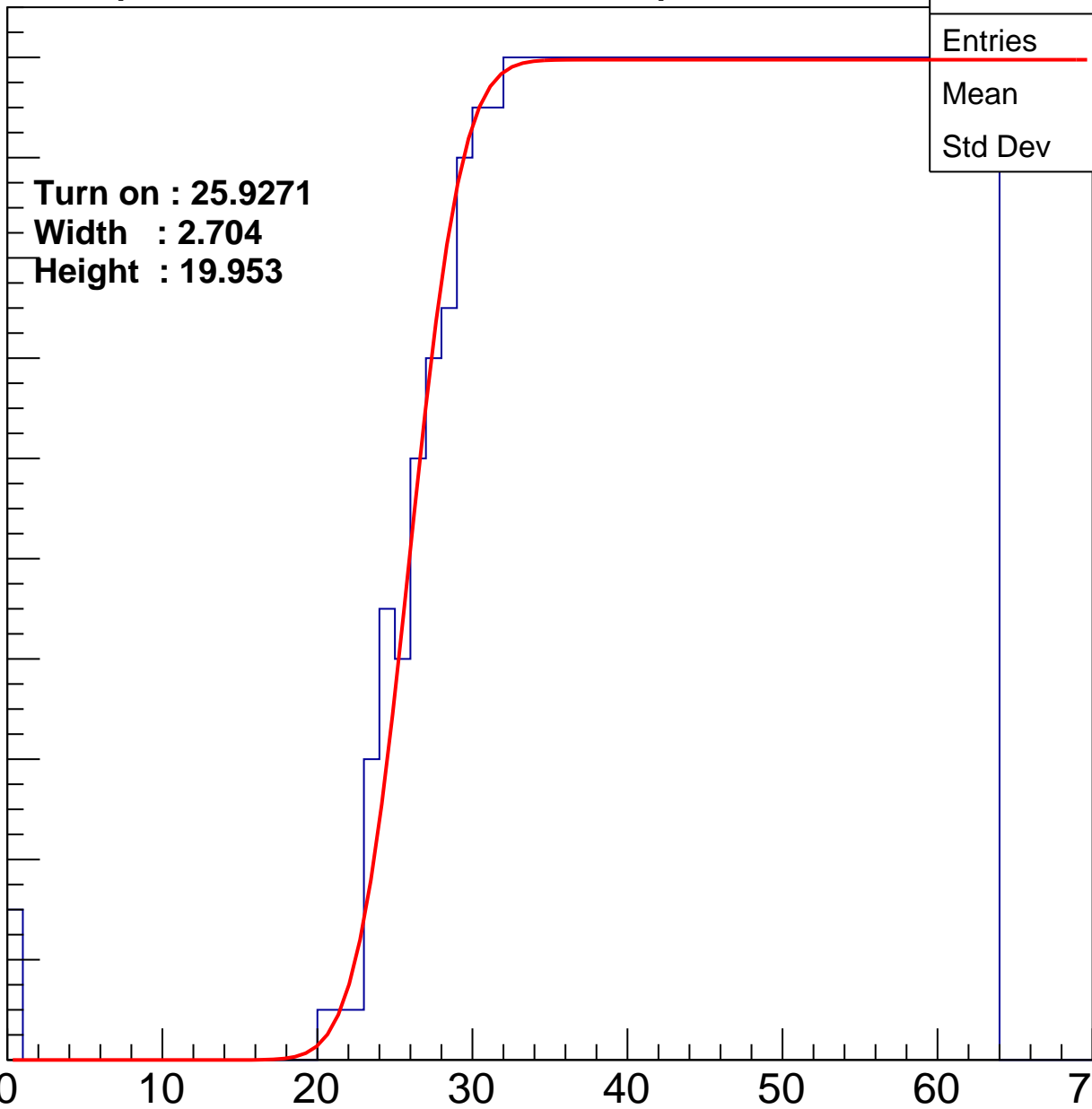
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9271
Width : 2.704
Height : 19.953

Entries	766
Mean	44.14
Std Dev	11.52

ampl



B0L101S, U21-ch60

calib_packv5_042523_0143.root, FC#1, port C1

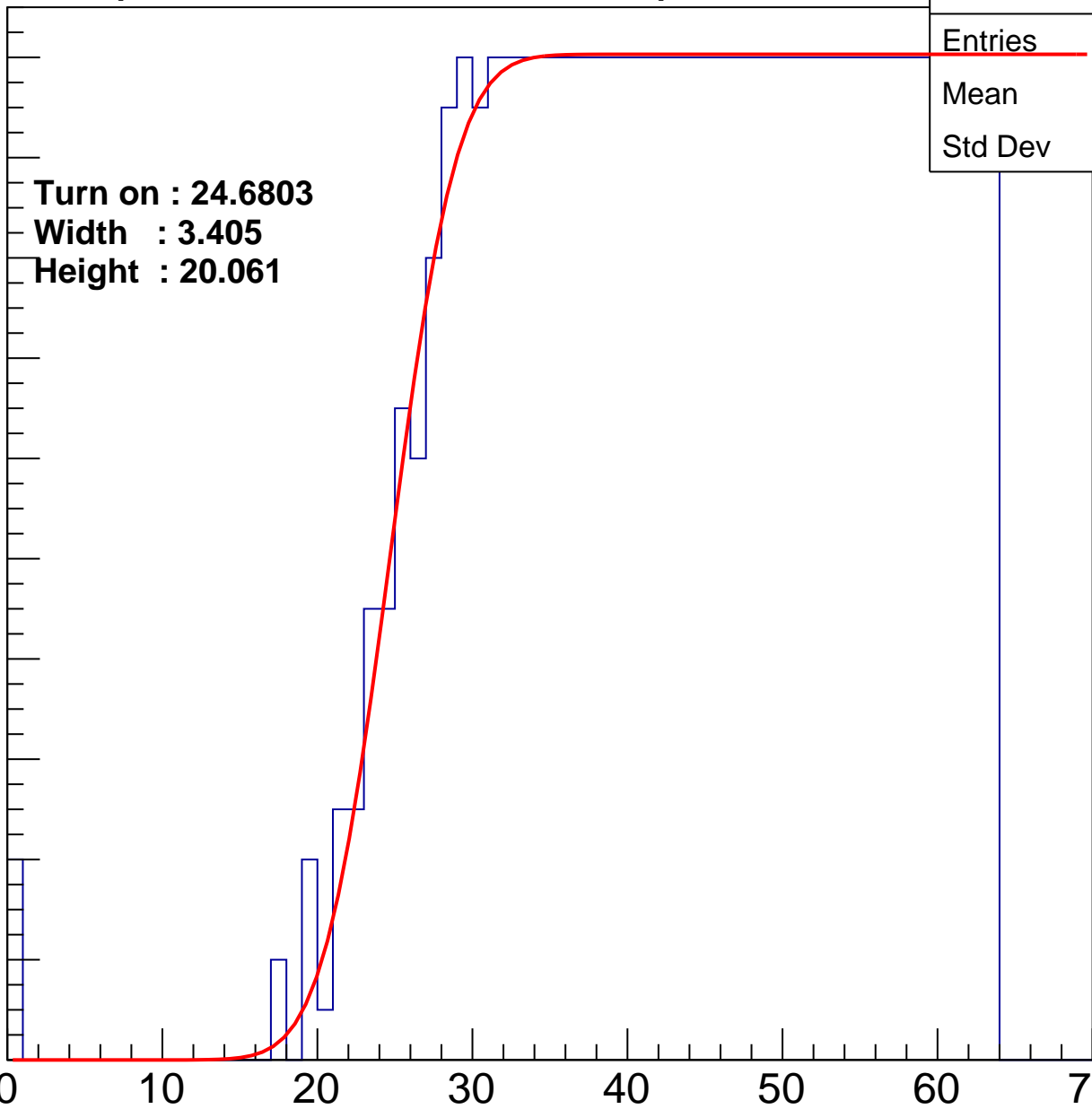
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6803
Width : 3.405
Height : 20.061

Entries	798
Mean	43.28
Std Dev	12.09

ampl



B0L101S, U21-ch61

calib_packv5_042523_0143.root, FC#1, port C1

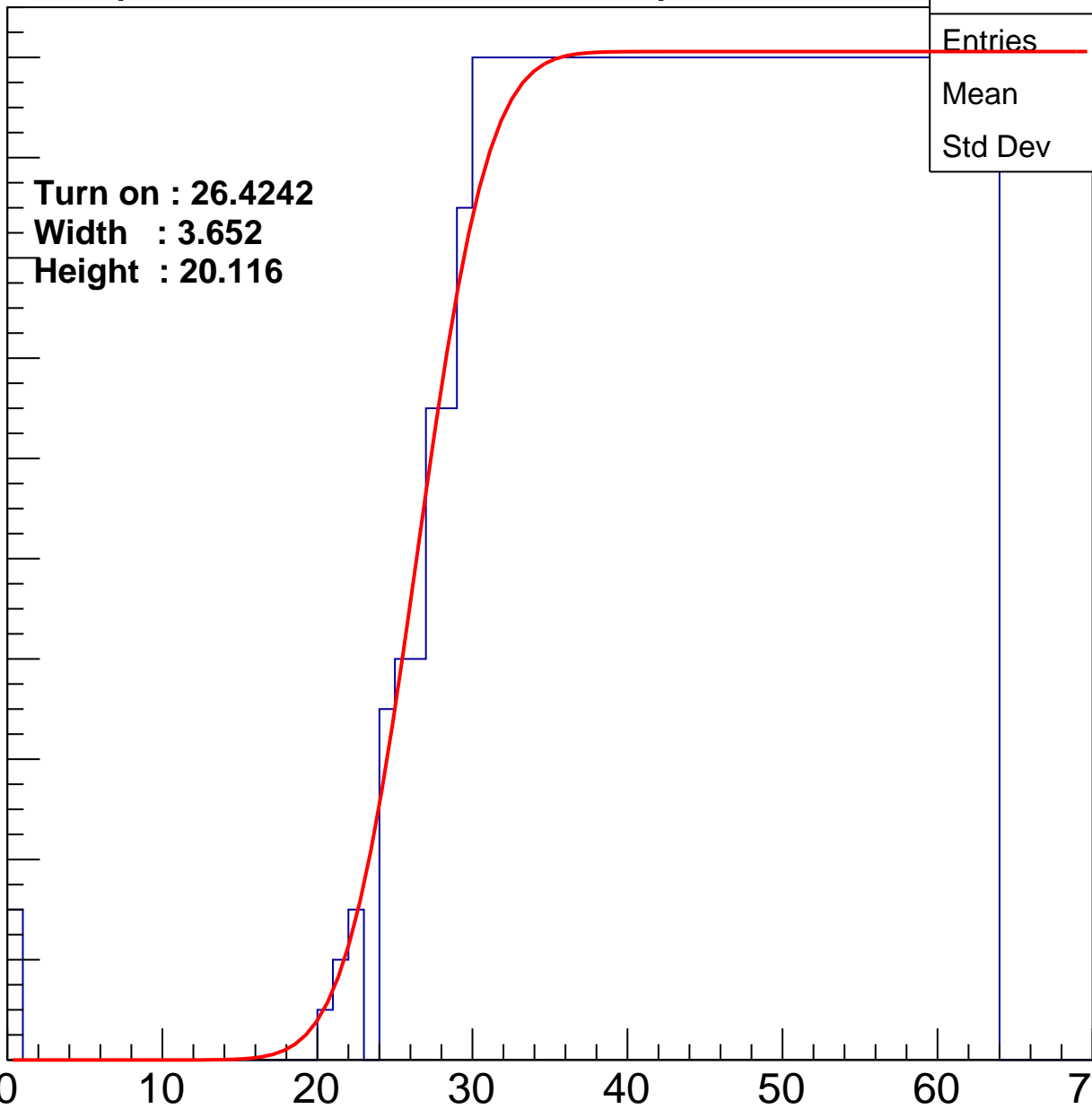
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4242
Width : 3.652
Height : 20.116

Entries	755
Mean	44.41
Std Dev	11.37

ampl



B0L101S, U21-ch62

calib_packv5_042523_0143.root, FC#1, port C1

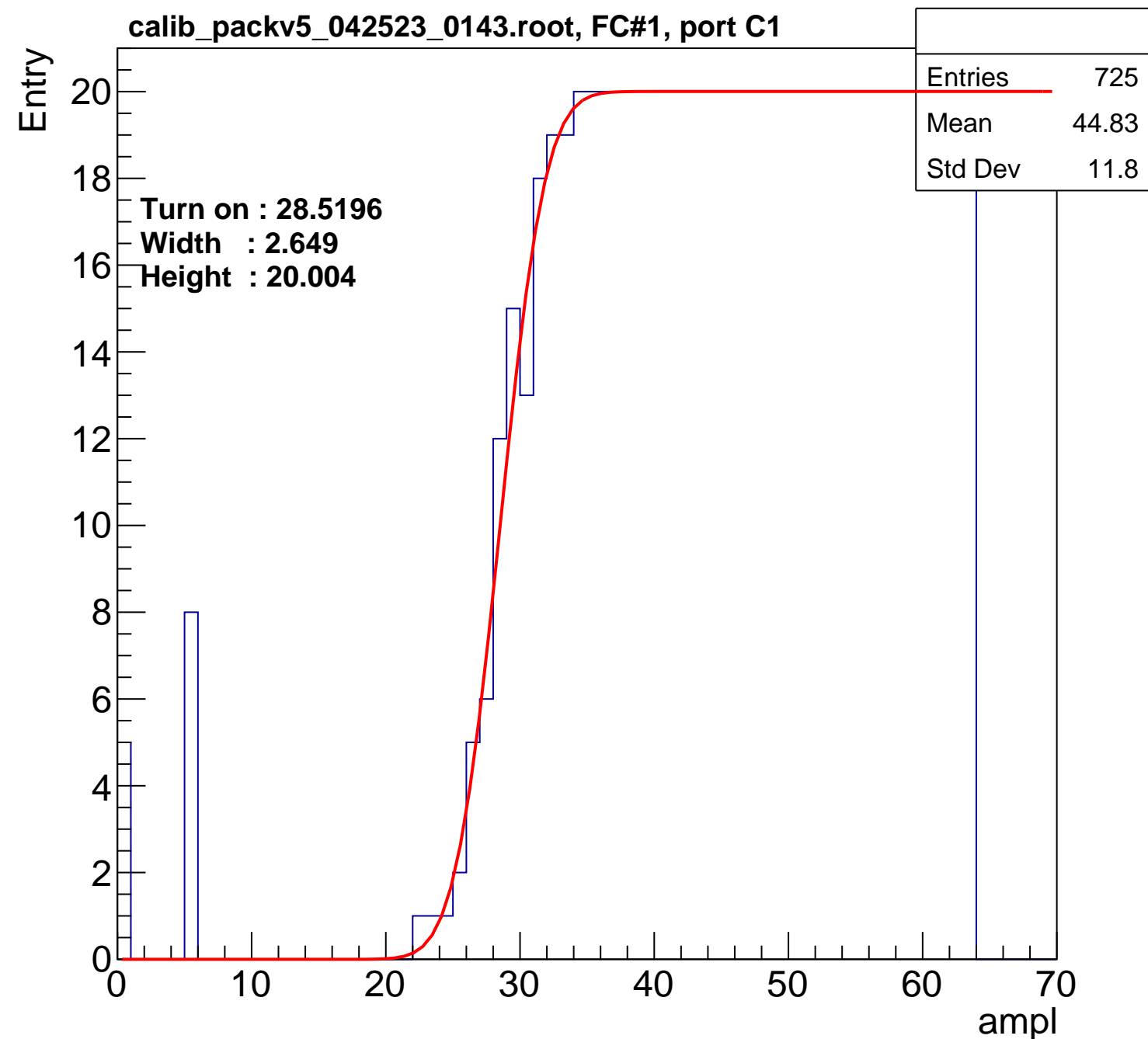
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5196
Width : 2.649
Height : 20.004

Entries	725
Mean	44.83
Std Dev	11.8

ampl



B0L101S, U21-ch63

calib_packv5_042523_0143.root, FC#1, port C1

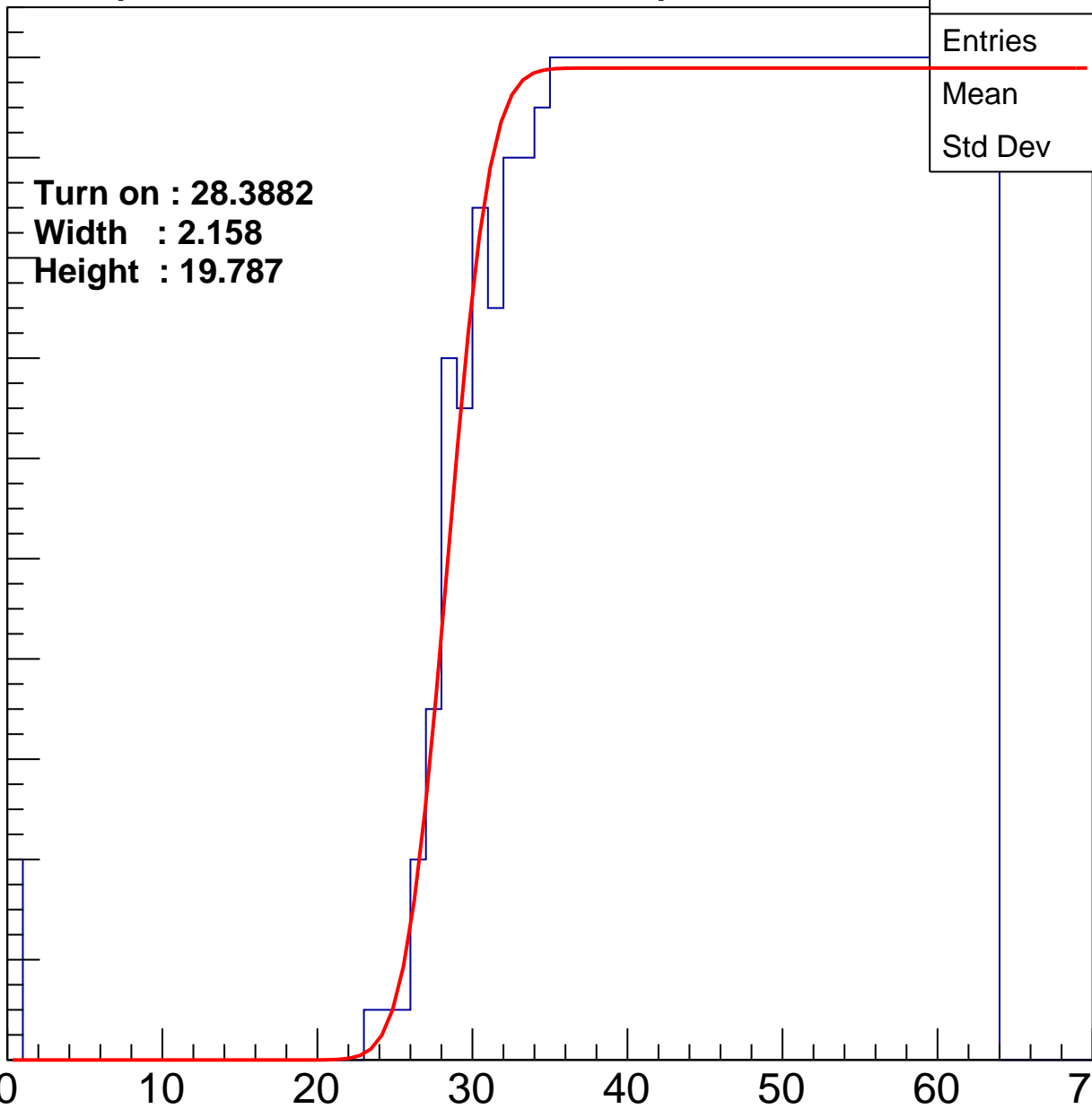
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3882
Width : 2.158
Height : 19.787

Entries	712
Mean	45.43
Std Dev	10.92

ampl



B0L101S, U21-ch64

calib_packv5_042523_0143.root, FC#1, port C1

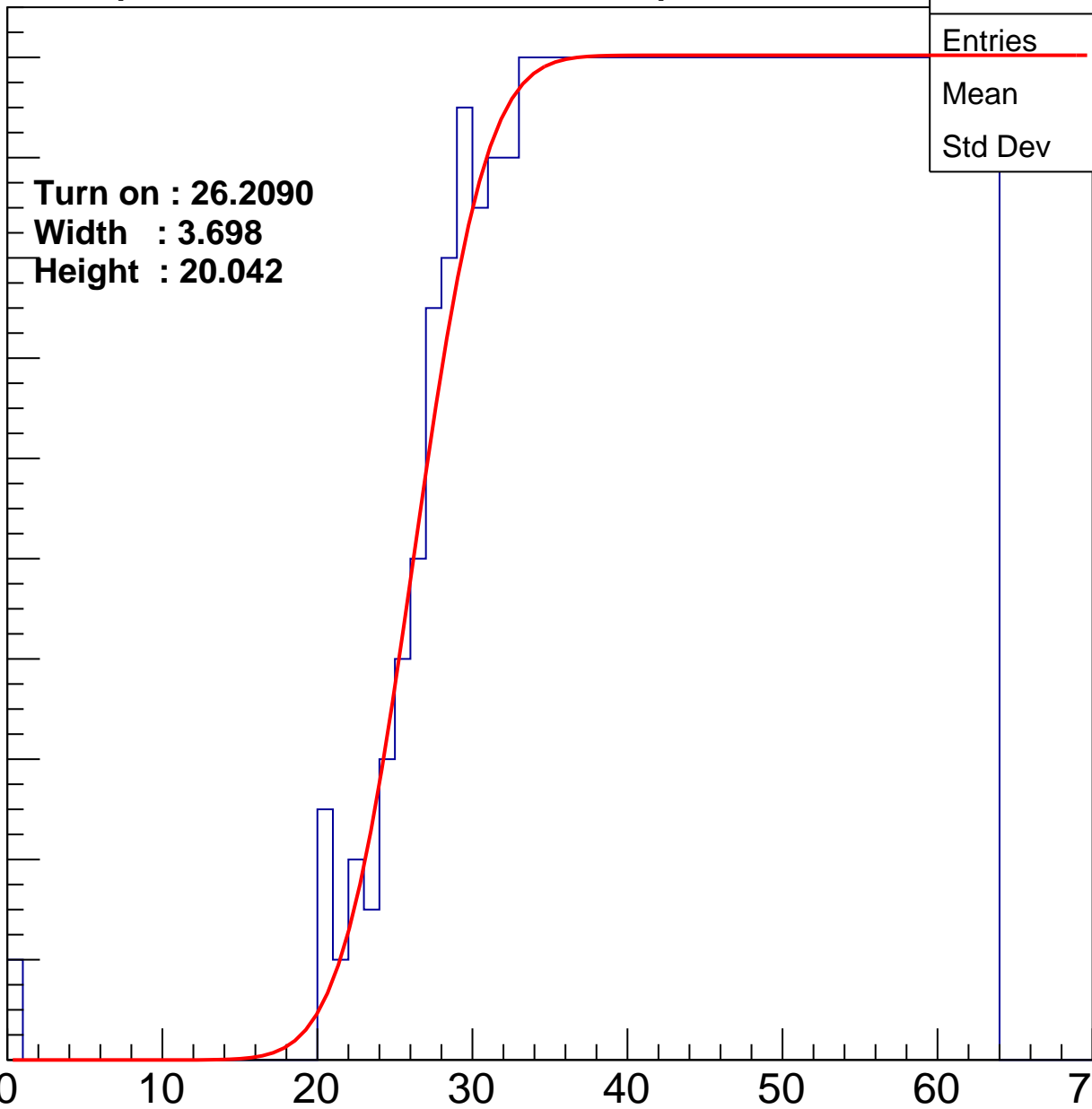
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2090
Width : 3.698
Height : 20.042

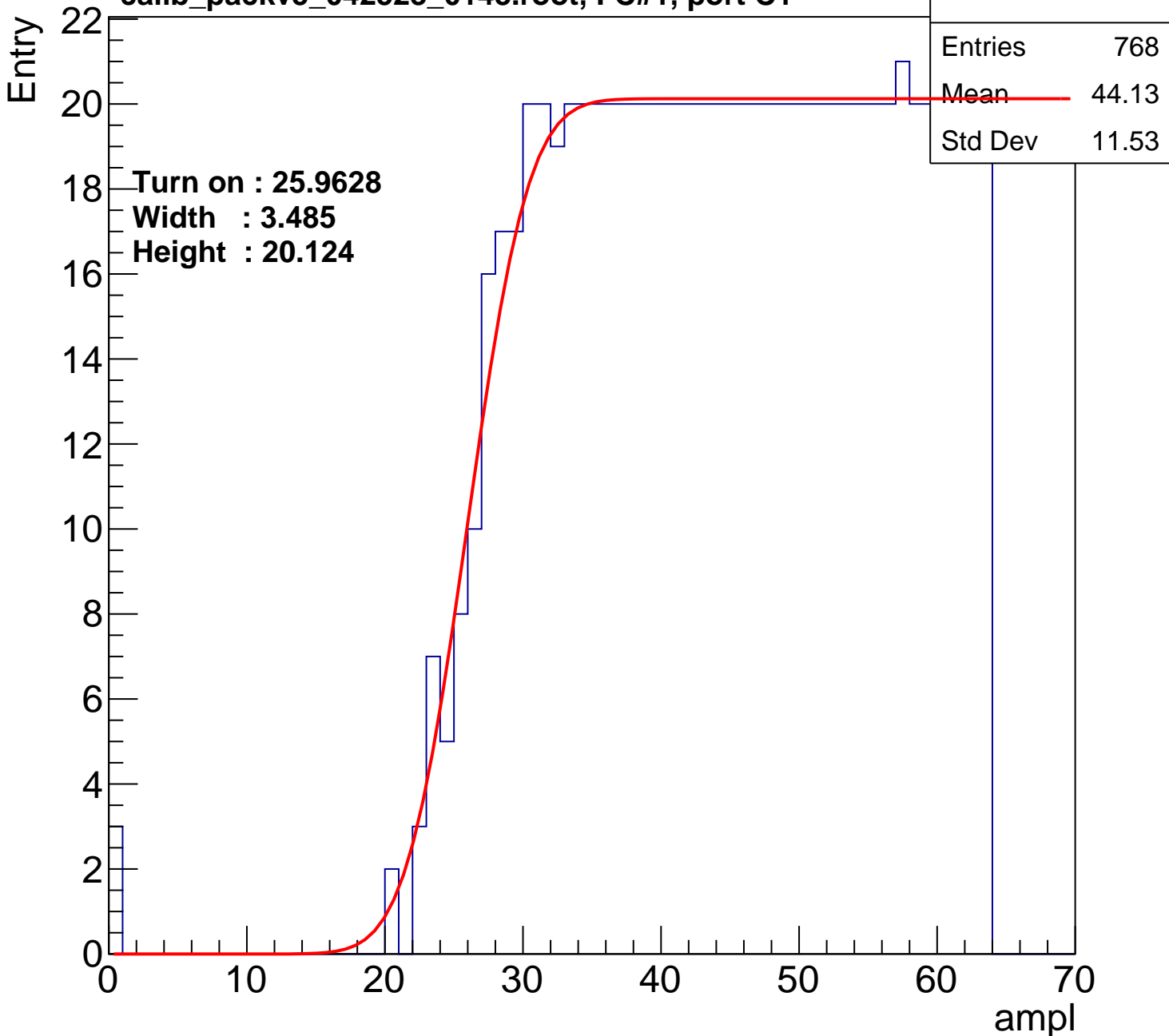
Entries	763
Mean	44.18
Std Dev	11.49

ampl



B0L101S, U21-ch65

calib_packv5_042523_0143.root, FC#1, port C1



B0L101S, U21-ch66

calib_packv5_042523_0143.root, FC#1, port C1

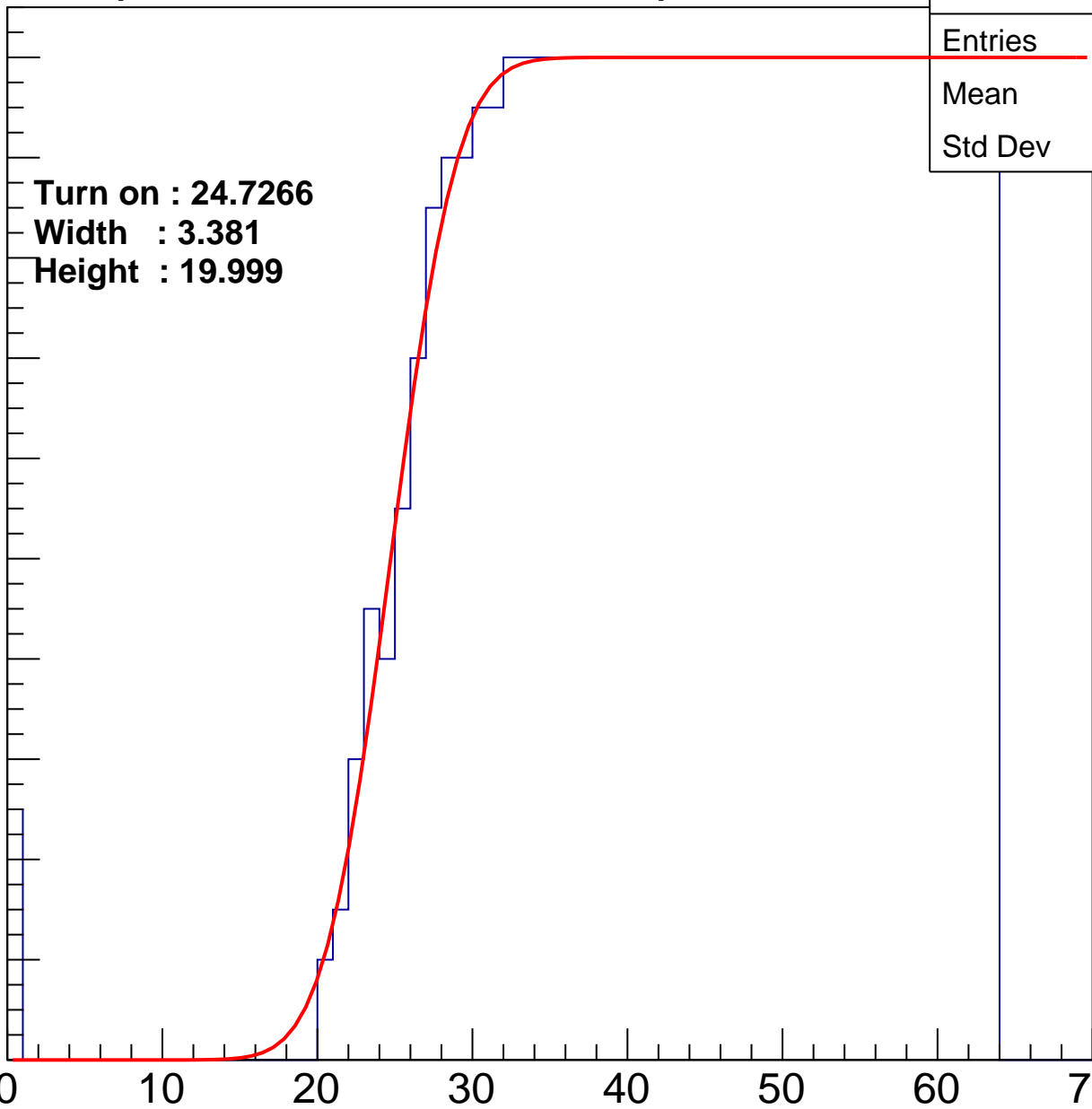
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7266
Width : 3.381
Height : 19.999

Entries	789
Mean	43.49
Std Dev	12

ampl



B0L101S, U21-ch67

calib_packv5_042523_0143.root, FC#1, port C1

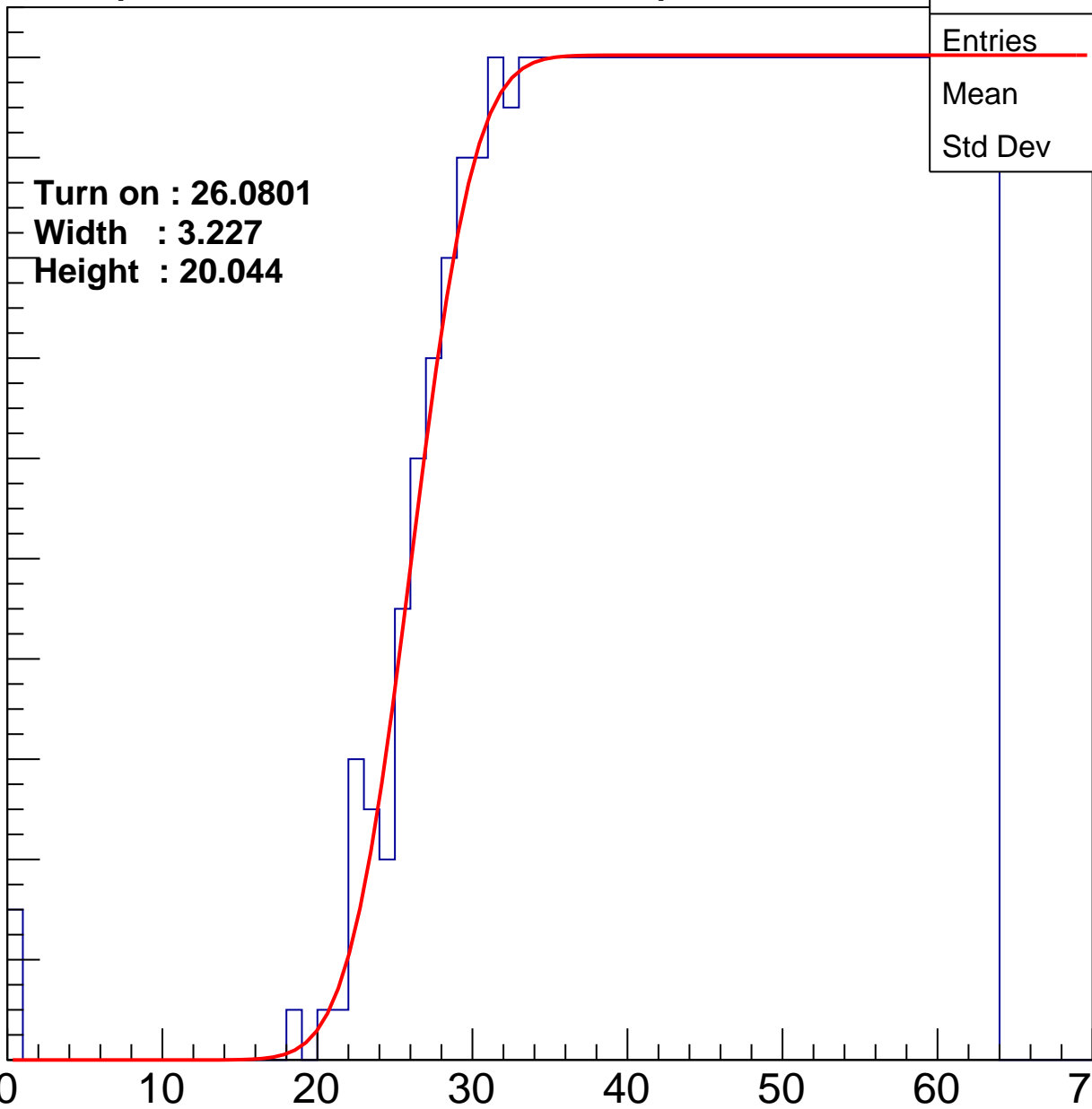
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0801
Width : 3.227
Height : 20.044

Entries	767
Mean	44.09
Std Dev	11.58

ampl



B0L101S, U21-ch68

calib_packv5_042523_0143.root, FC#1, port C1

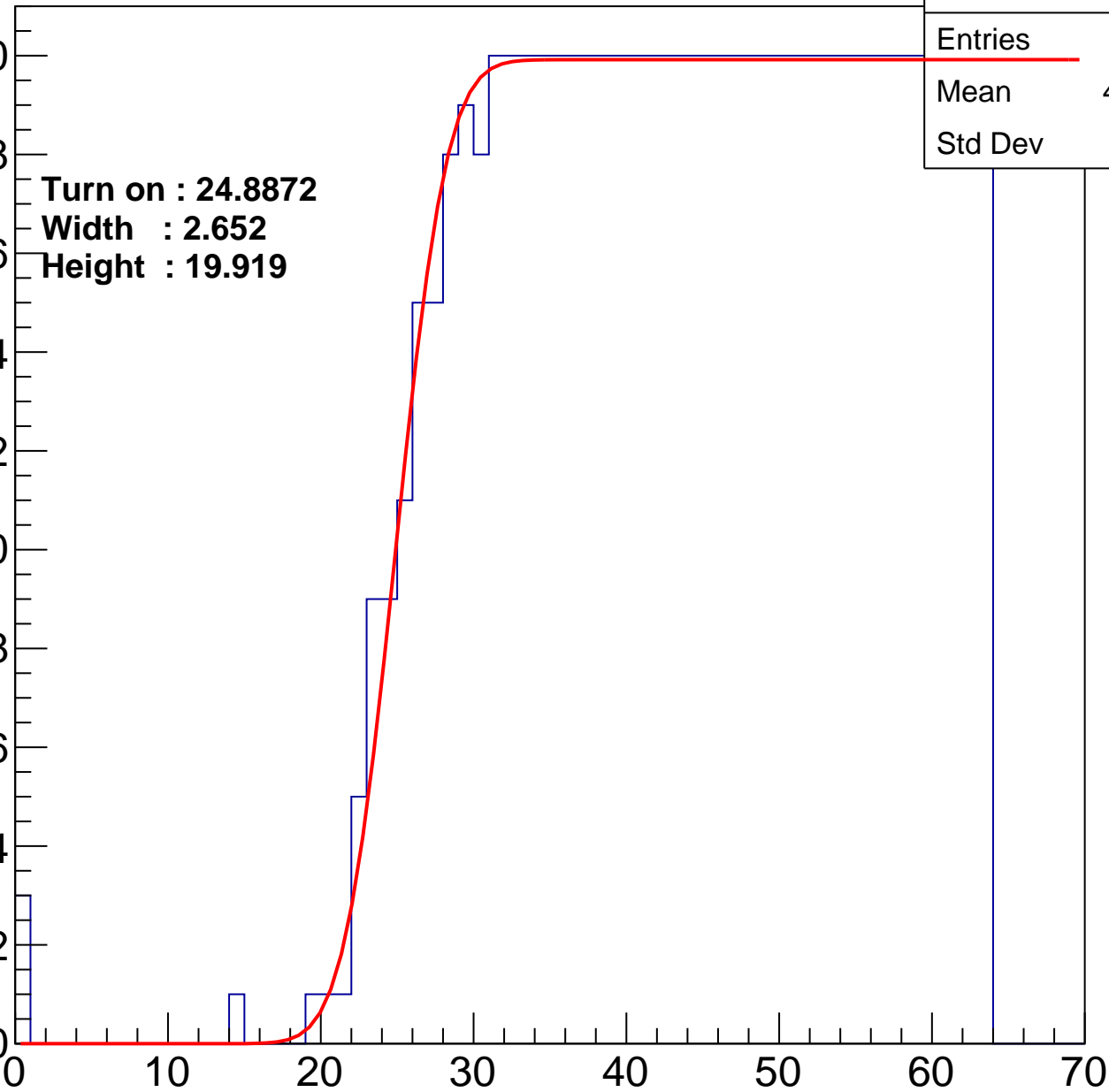
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8872
Width : 2.652
Height : 19.919

Entries	786
Mean	43.63
Std Dev	11.81

ampl



B0L101S, U21-ch69

calib_packv5_042523_0143.root, FC#1, port C1

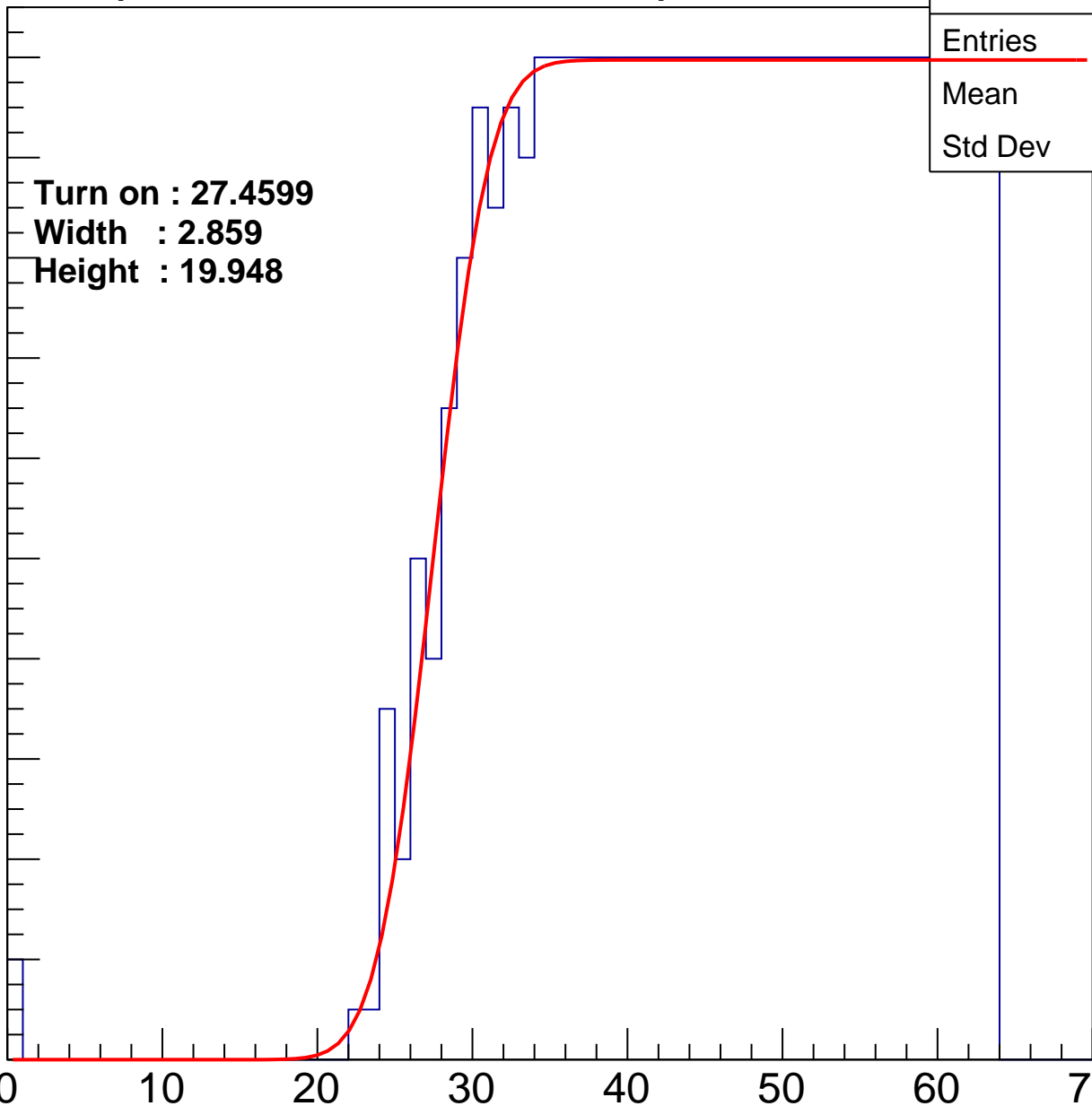
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4599
Width : 2.859
Height : 19.948

Entries	735
Mean	44.92
Std Dev	11.04

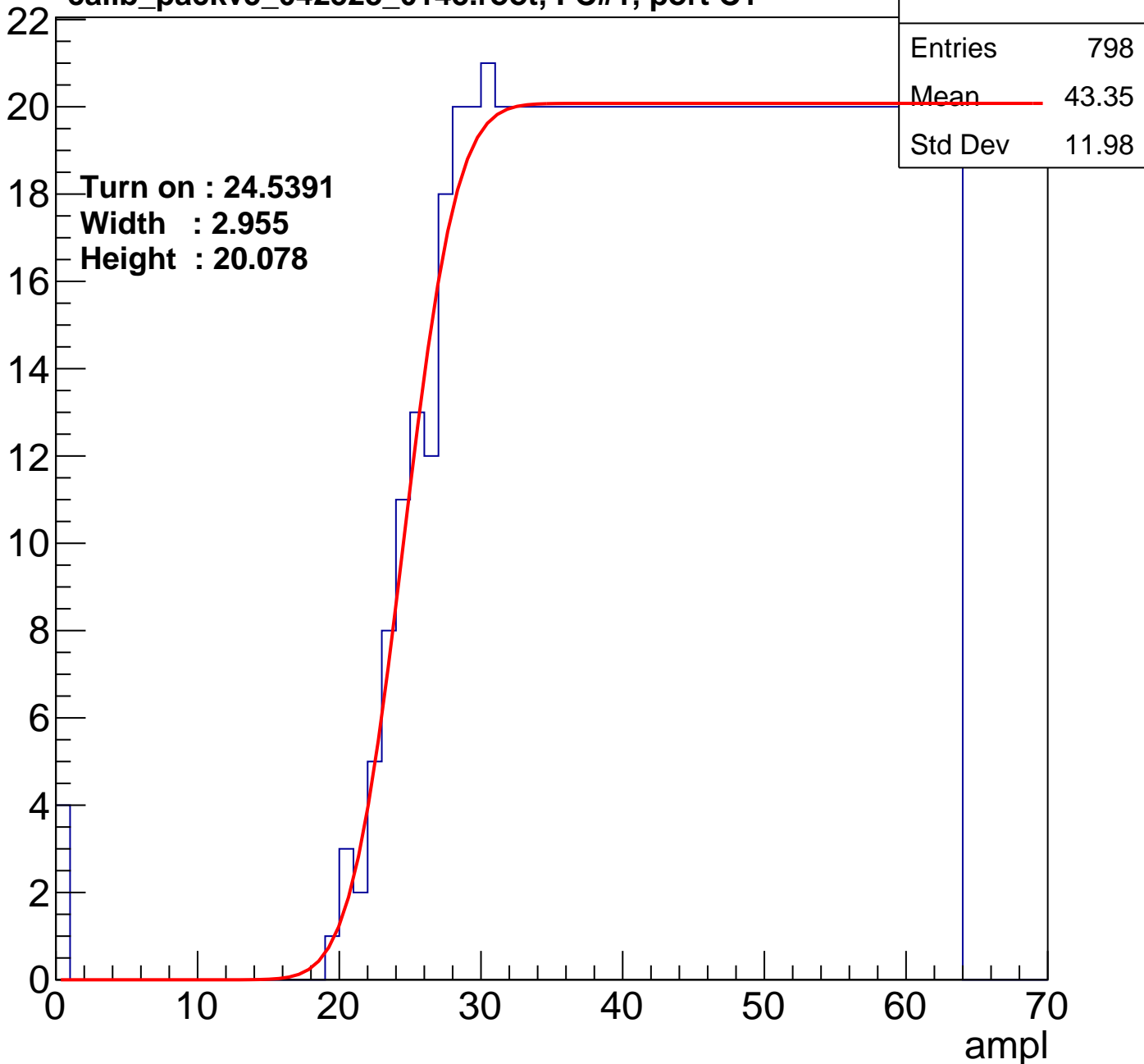
ampl



B0L101S, U21-ch70

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch71

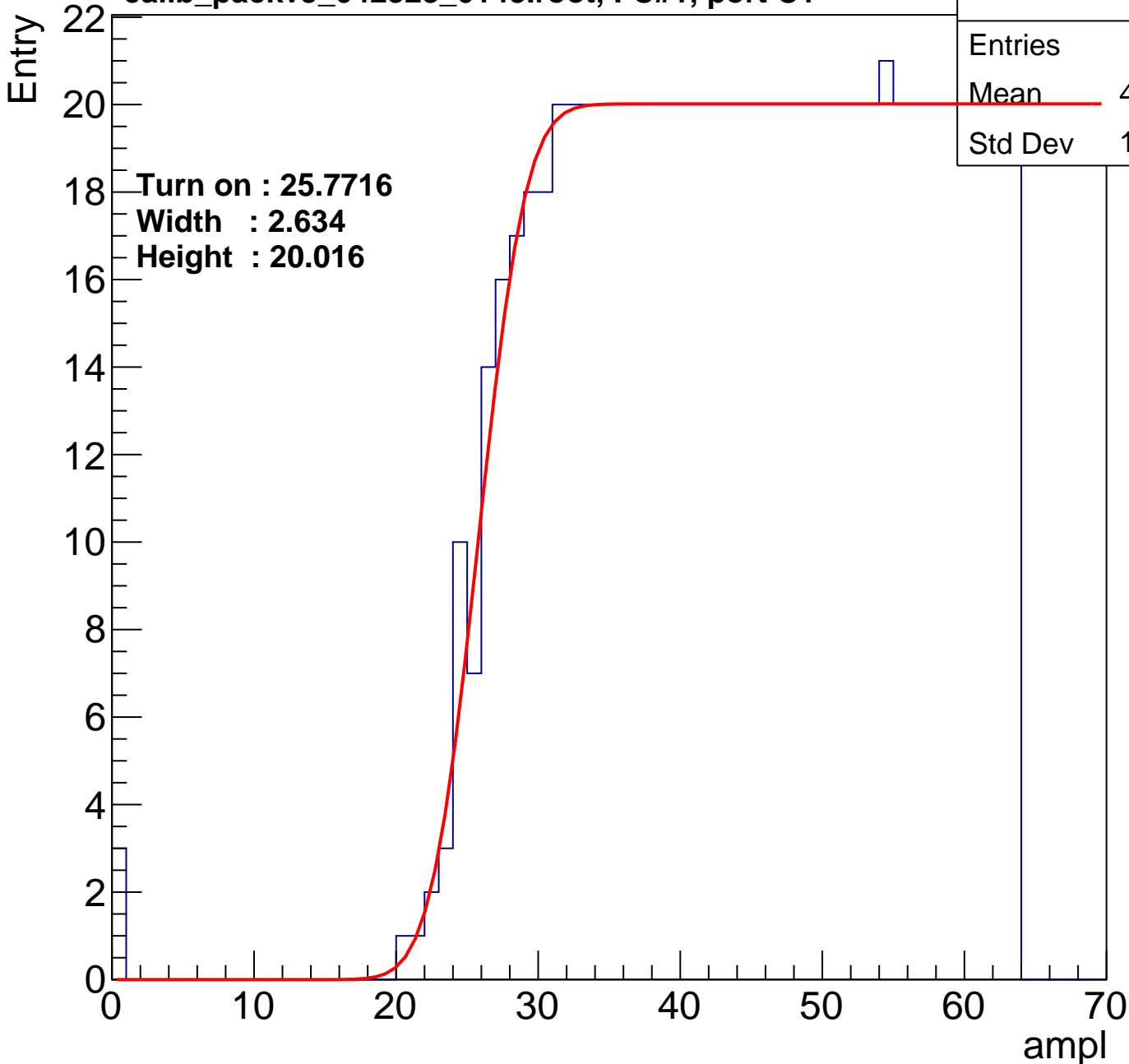
calib_packv5_042523_0143.root, FC#1, port C1

Entries	771
Mean	44.07
Std Dev	11.54

Turn on : 25.7716

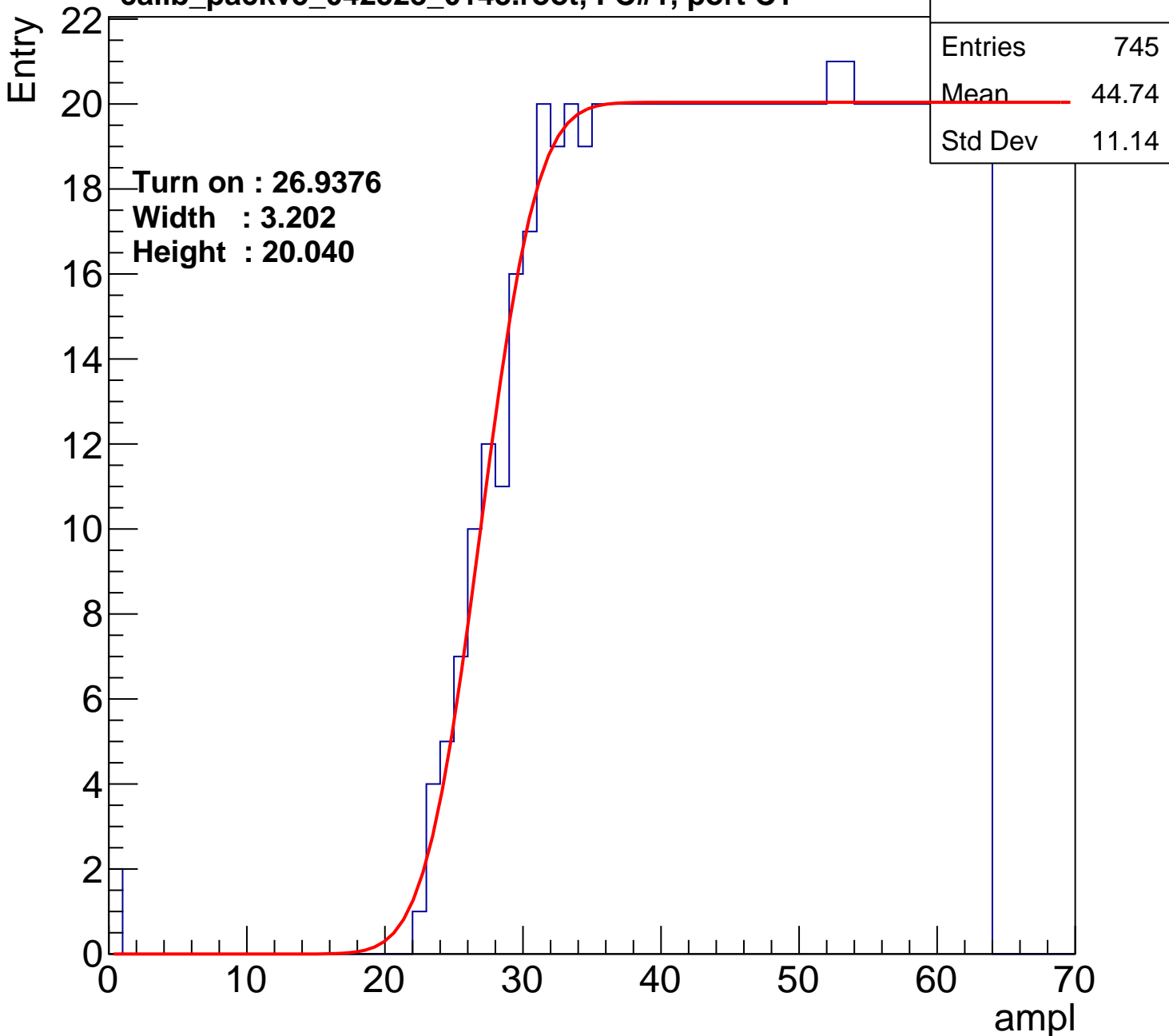
Width : 2.634

Height : 20.016



B0L101S, U21-ch72

calib_packv5_042523_0143.root, FC#1, port C1



B0L101S, U21-ch73

calib_packv5_042523_0143.root, FC#1, port C1

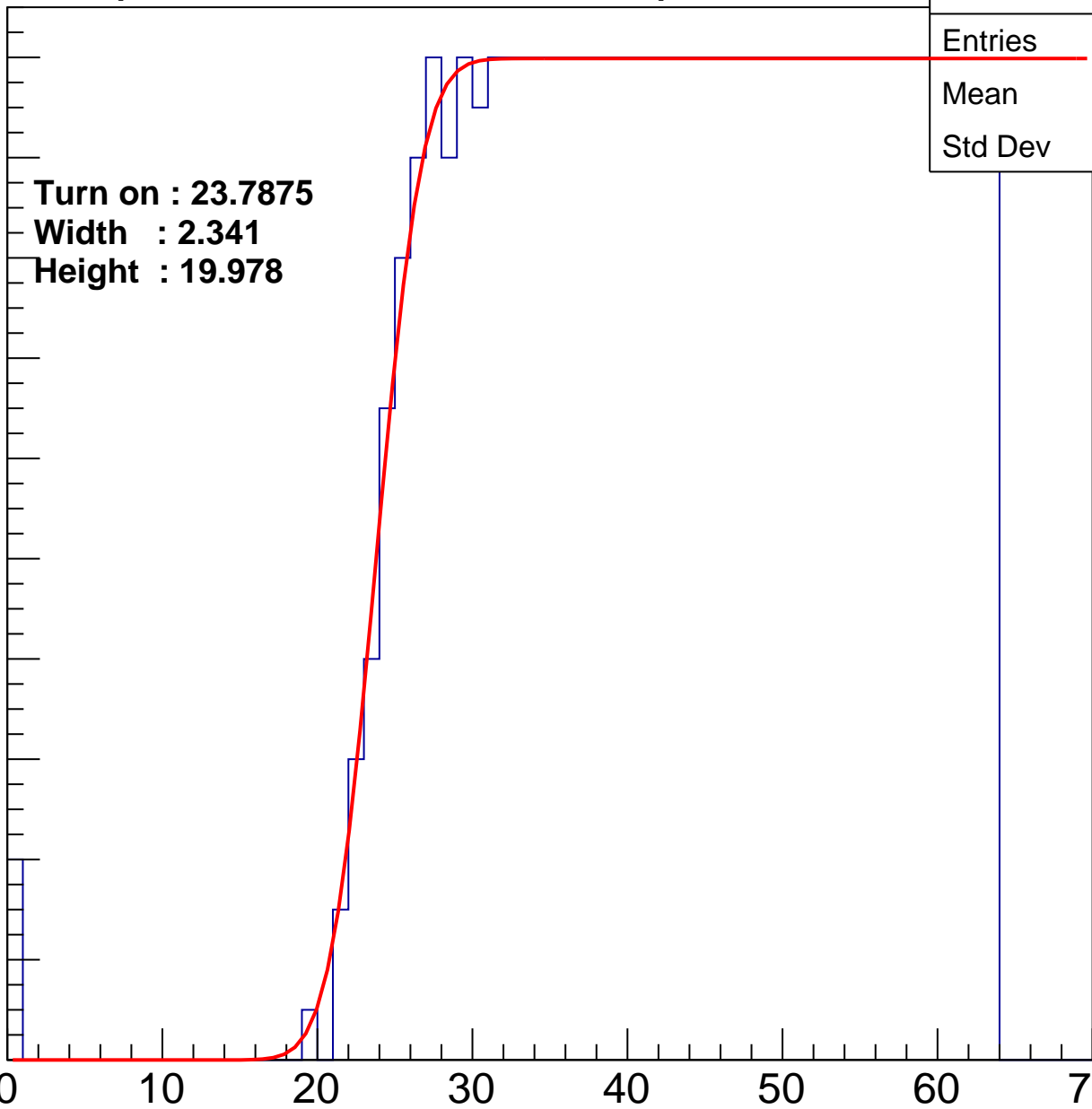
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 23.7875
Width : 2.341
Height : 19.978

Entries	806
Mean	43.17
Std Dev	12.05

ampl



B0L101S, U21-ch74

calib_packv5_042523_0143.root, FC#1, port C1

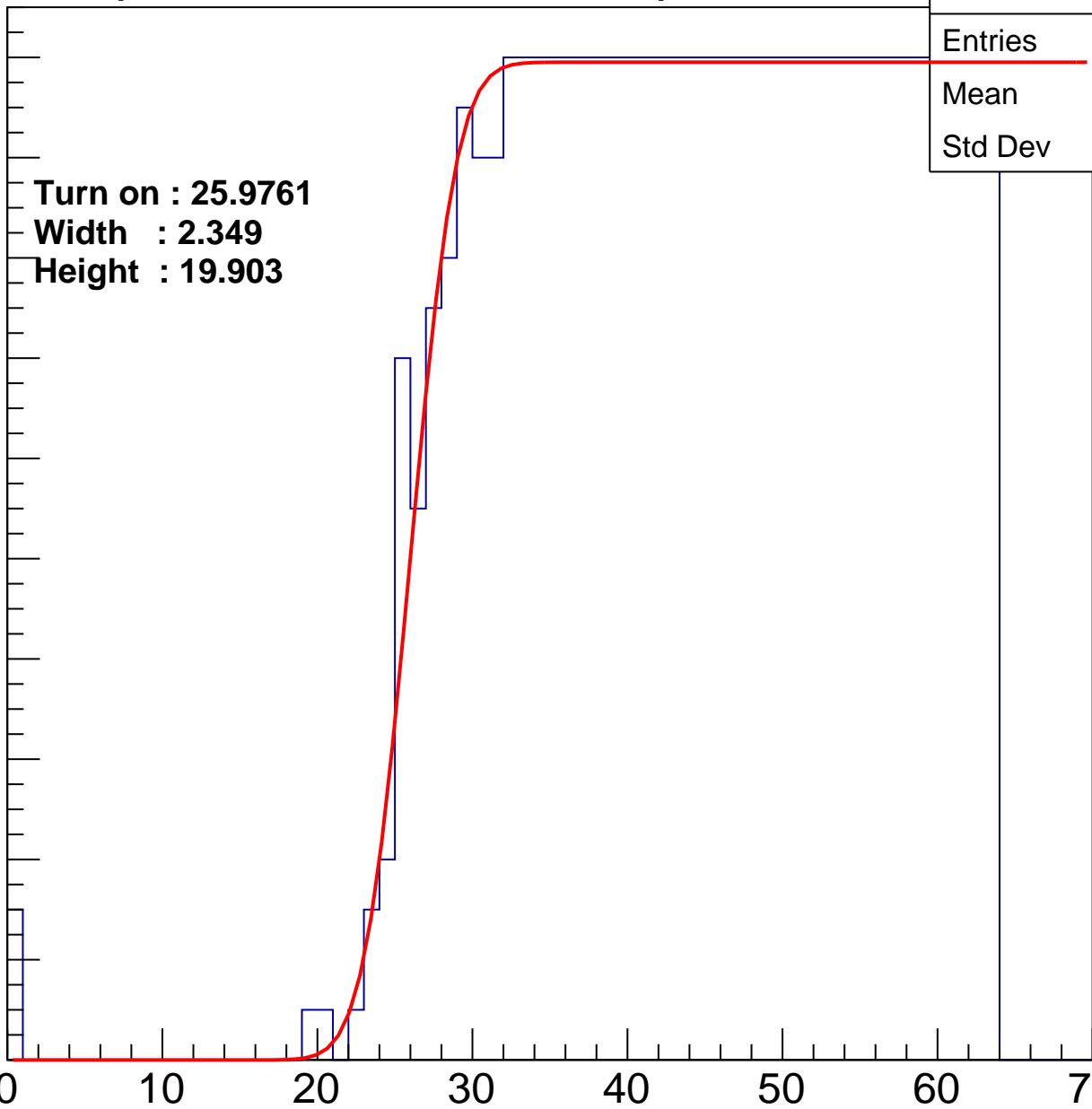
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9761
Width : 2.349
Height : 19.903

Entries	764
Mean	44.19
Std Dev	11.48

ampl



B0L101S, U21-ch75

calib_packv5_042523_0143.root, FC#1, port C1

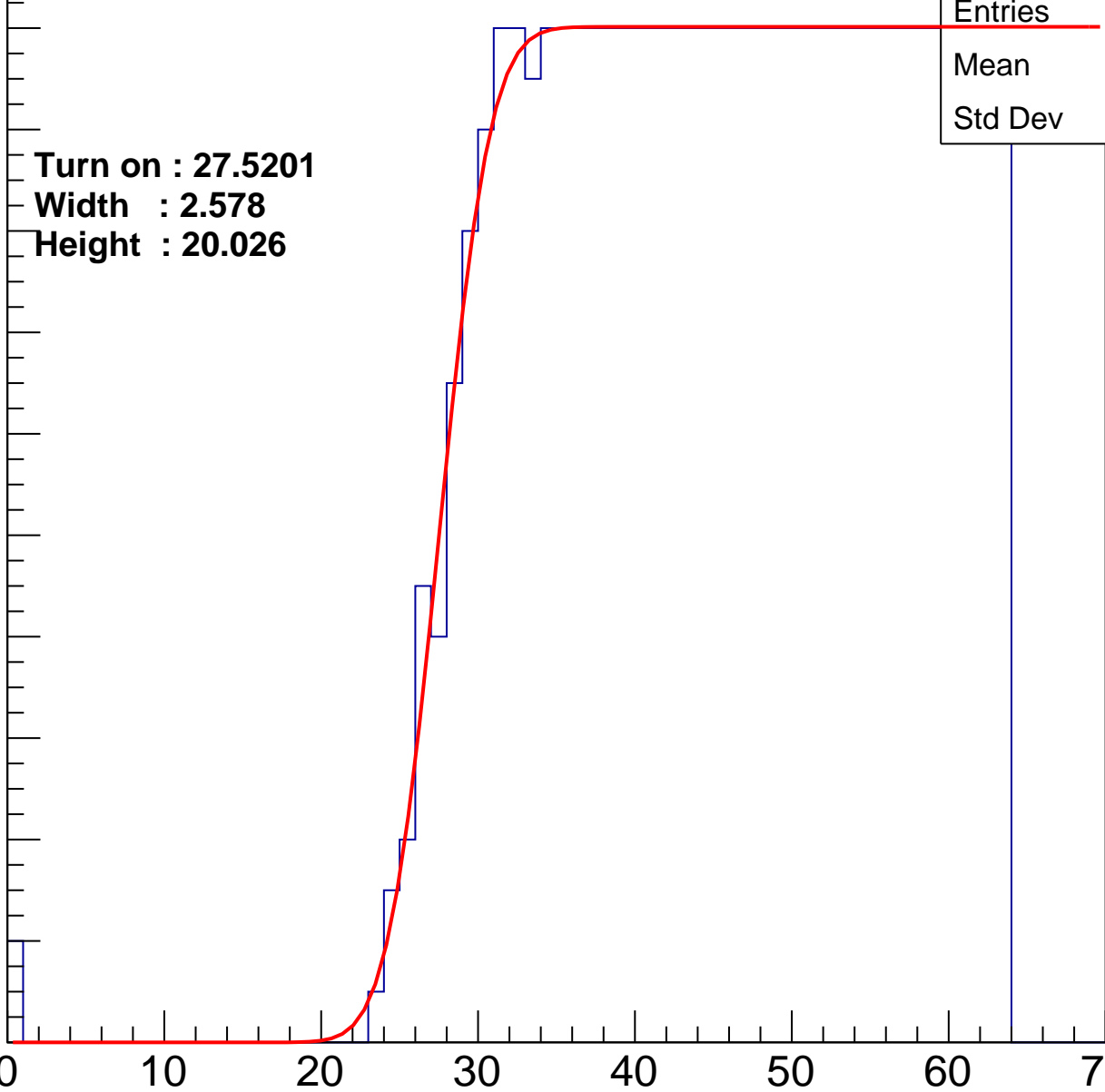
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5201
Width : 2.578
Height : 20.026

Entries	733
Mean	45.02
Std Dev	10.93

ampl



B0L101S, U21-ch76

calib_packv5_042523_0143.root, FC#1, port C1

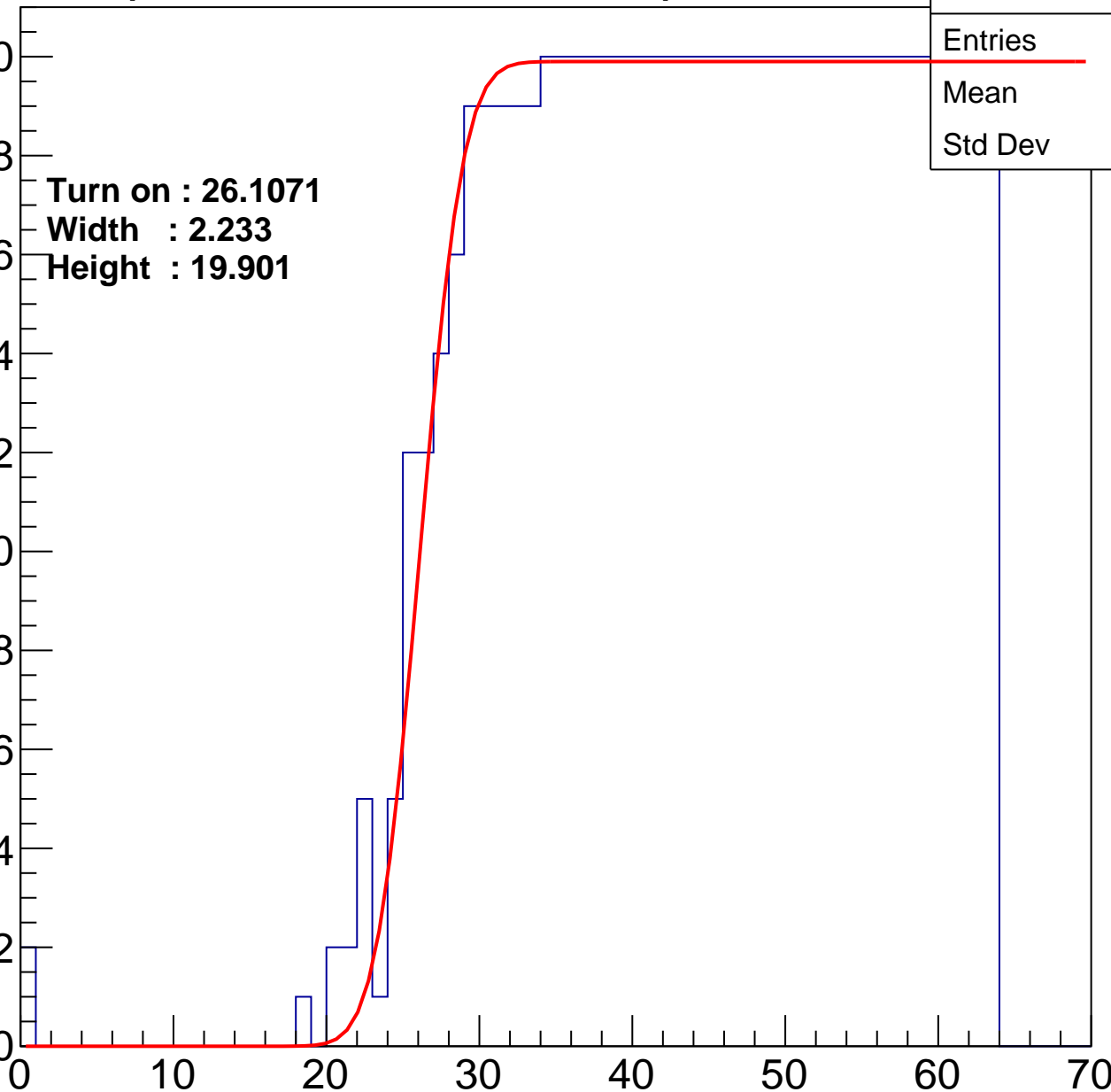
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1071
Width : 2.233
Height : 19.901

Entries	767
Mean	44.11
Std Dev	11.49

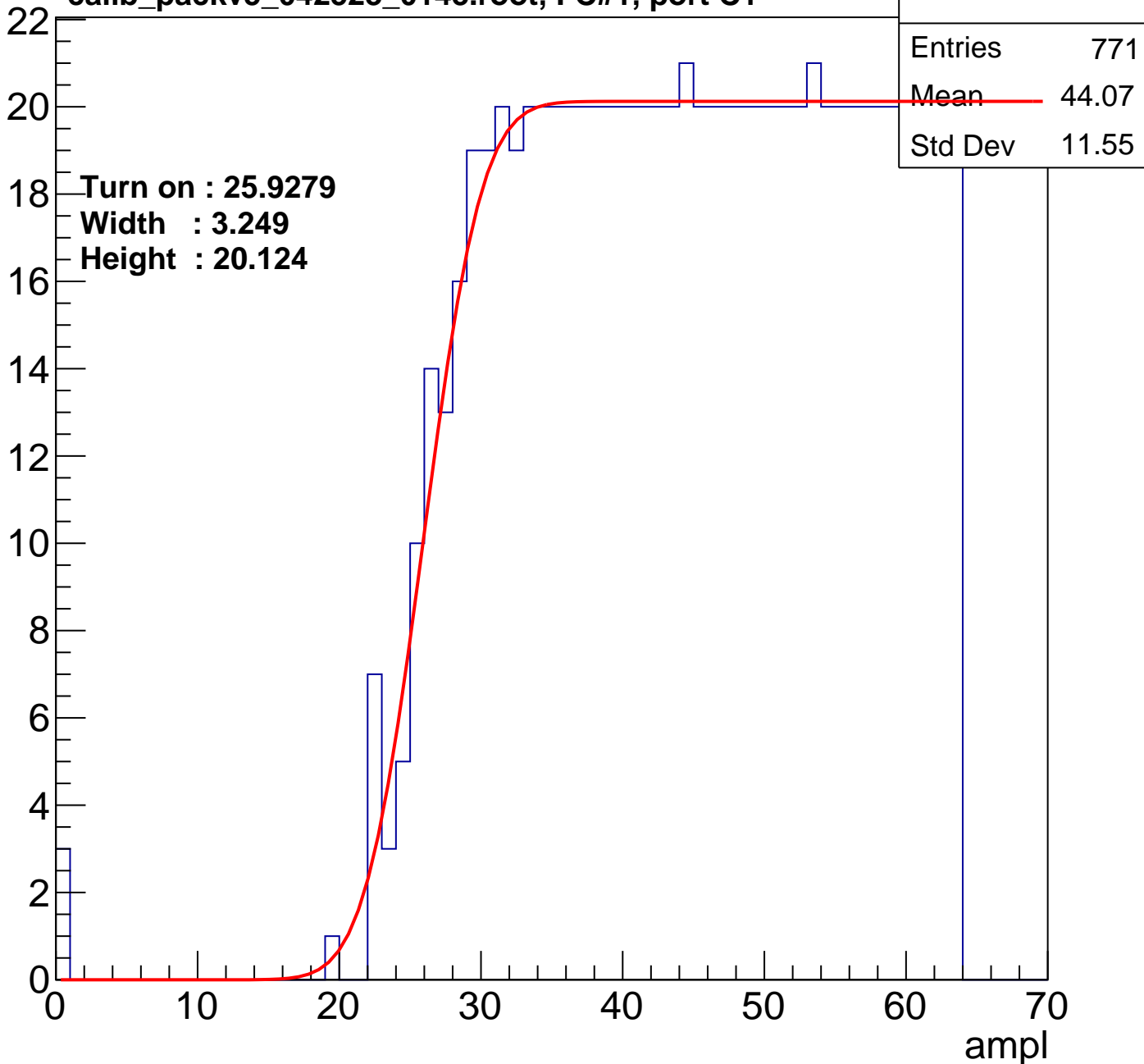
ampl



B0L101S, U21-ch77

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch78

calib_packv5_042523_0143.root, FC#1, port C1

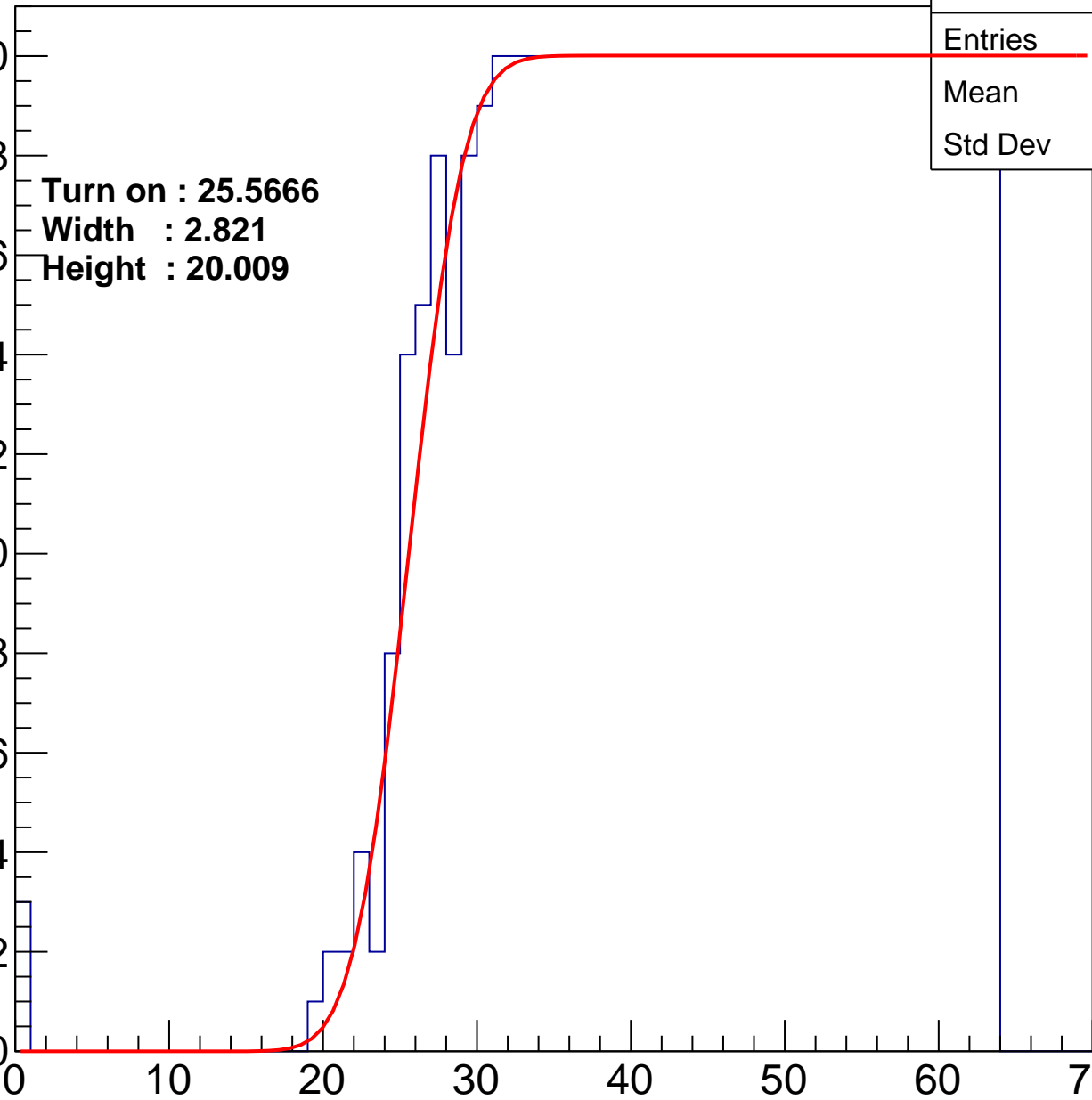
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5666
Width : 2.821
Height : 20.009

Entries	780
Mean	43.79
Std Dev	11.71

ampl



B0L101S, U21-ch79

calib_packv5_042523_0143.root, FC#1, port C1

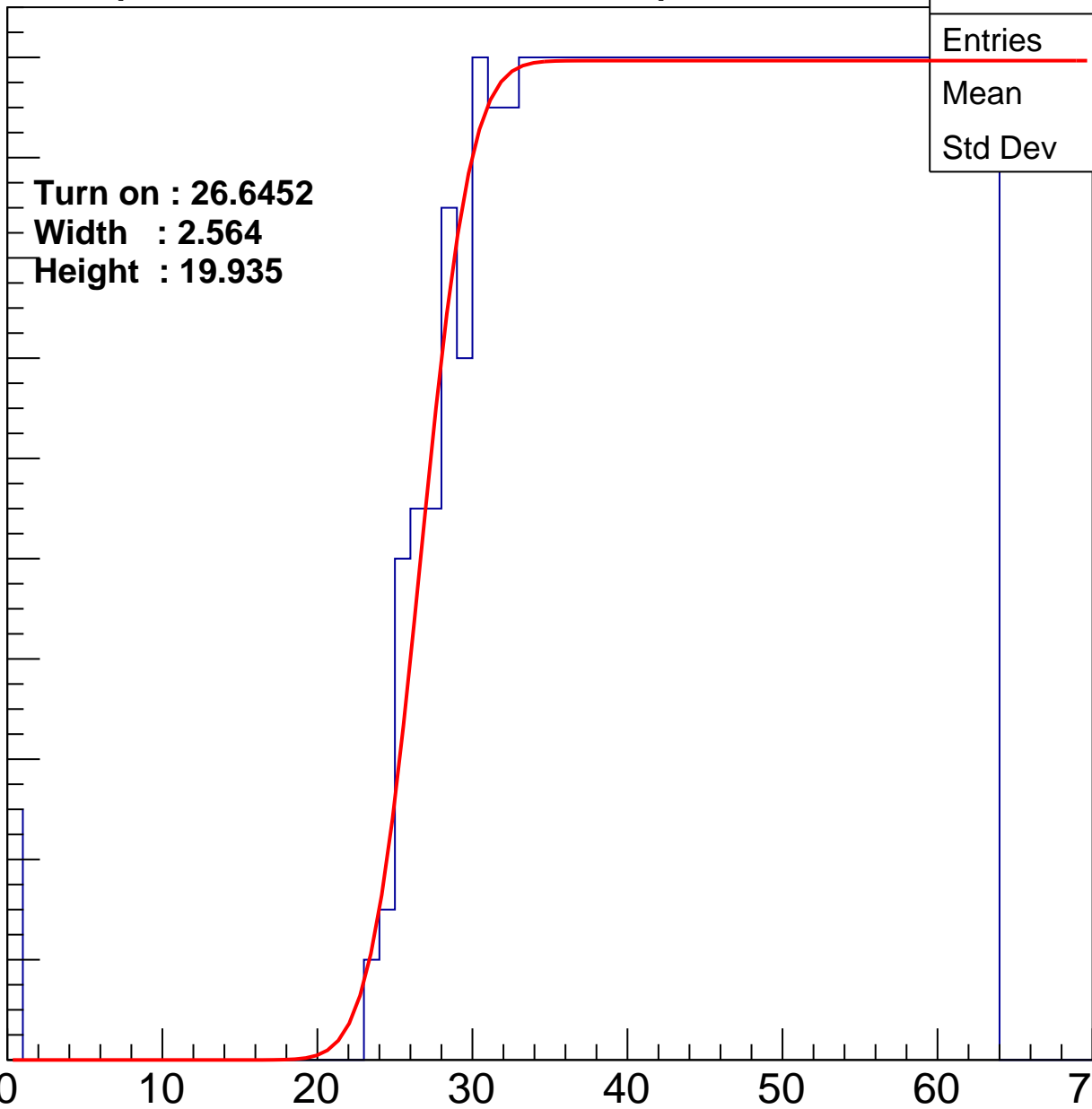
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6452
Width : 2.564
Height : 19.935

Entries	751
Mean	44.46
Std Dev	11.47

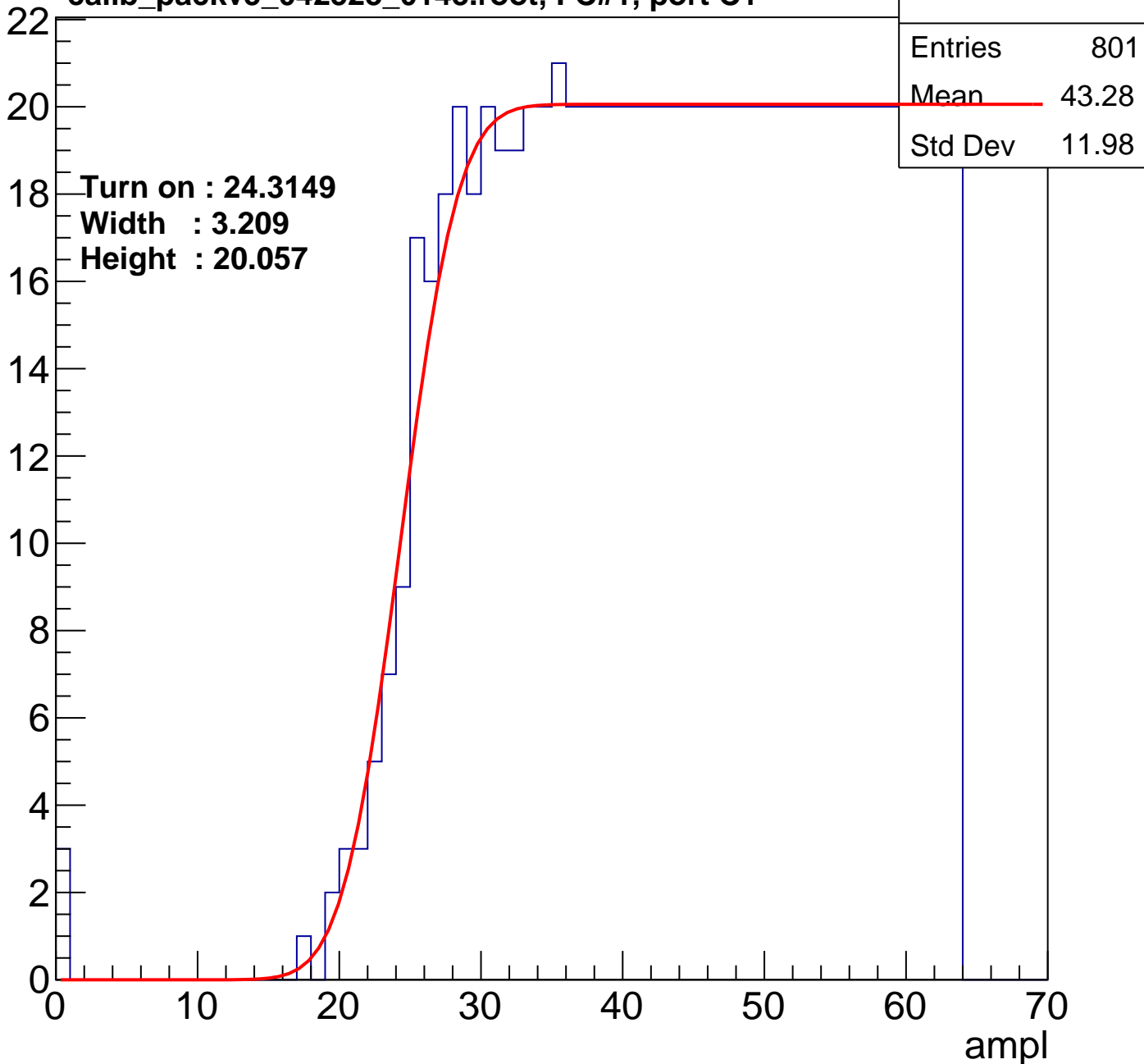
ampl



B0L101S, U21-ch80

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch81

calib_packv5_042523_0143.root, FC#1, port C1

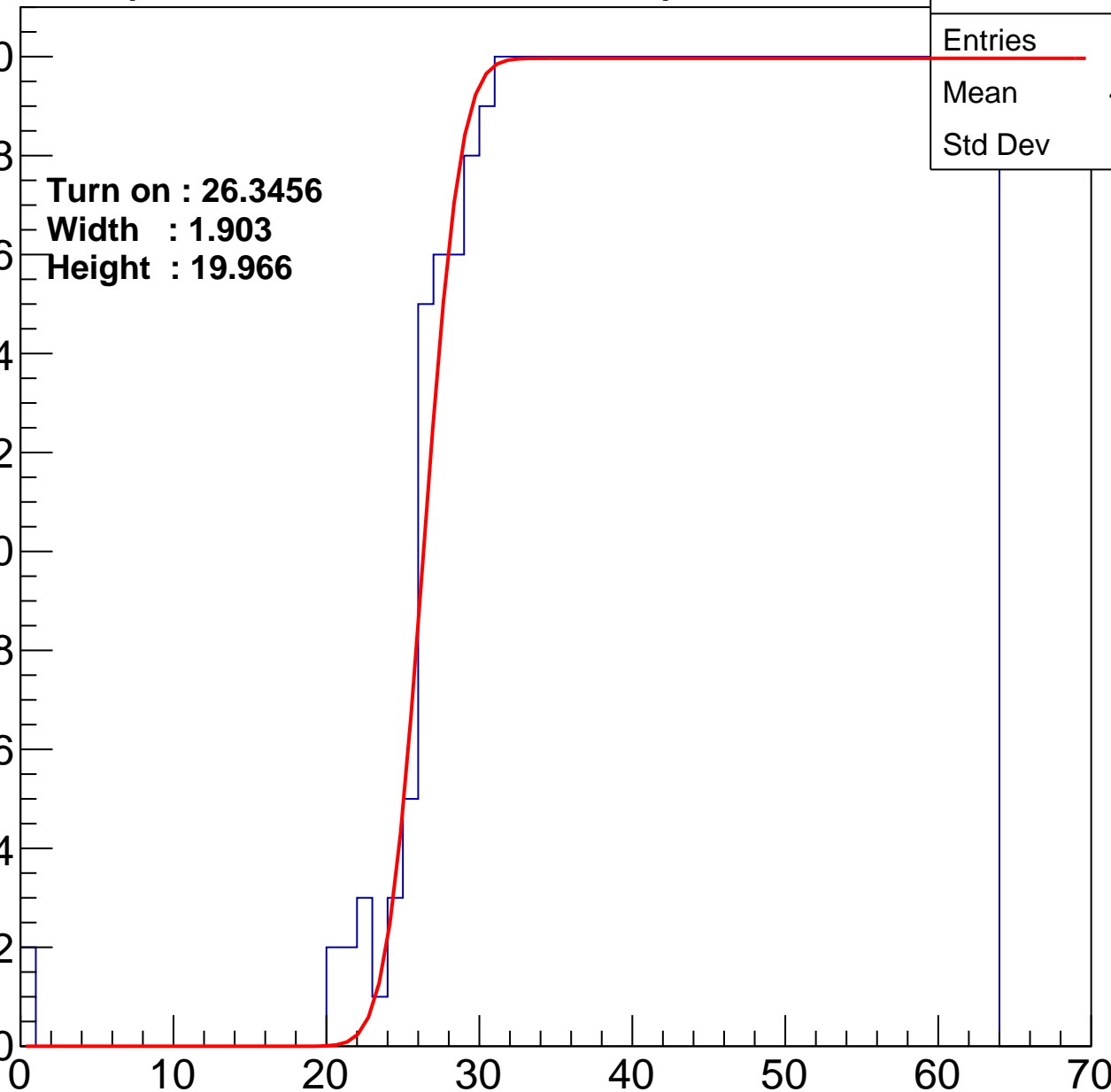
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3456
Width : 1.903
Height : 19.966

Entries	762
Mean	44.29
Std Dev	11.34

ampl



B0L101S, U21-ch82

calib_packv5_042523_0143.root, FC#1, port C1

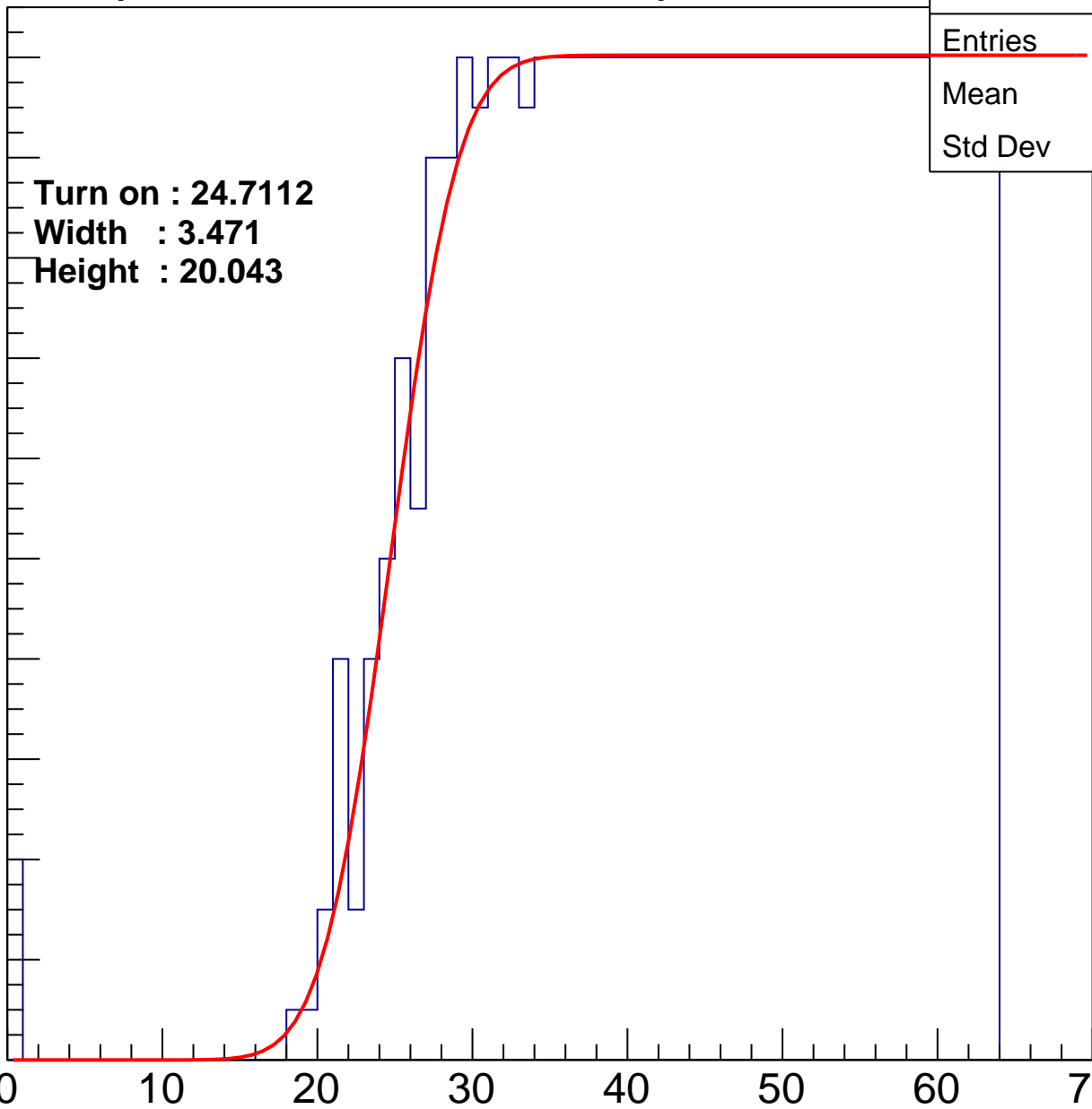
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7112
Width : 3.471
Height : 20.043

Entries	797
Mean	43.31
Std Dev	12.06

ampl



B0L101S, U21-ch83

calib_packv5_042523_0143.root, FC#1, port C1

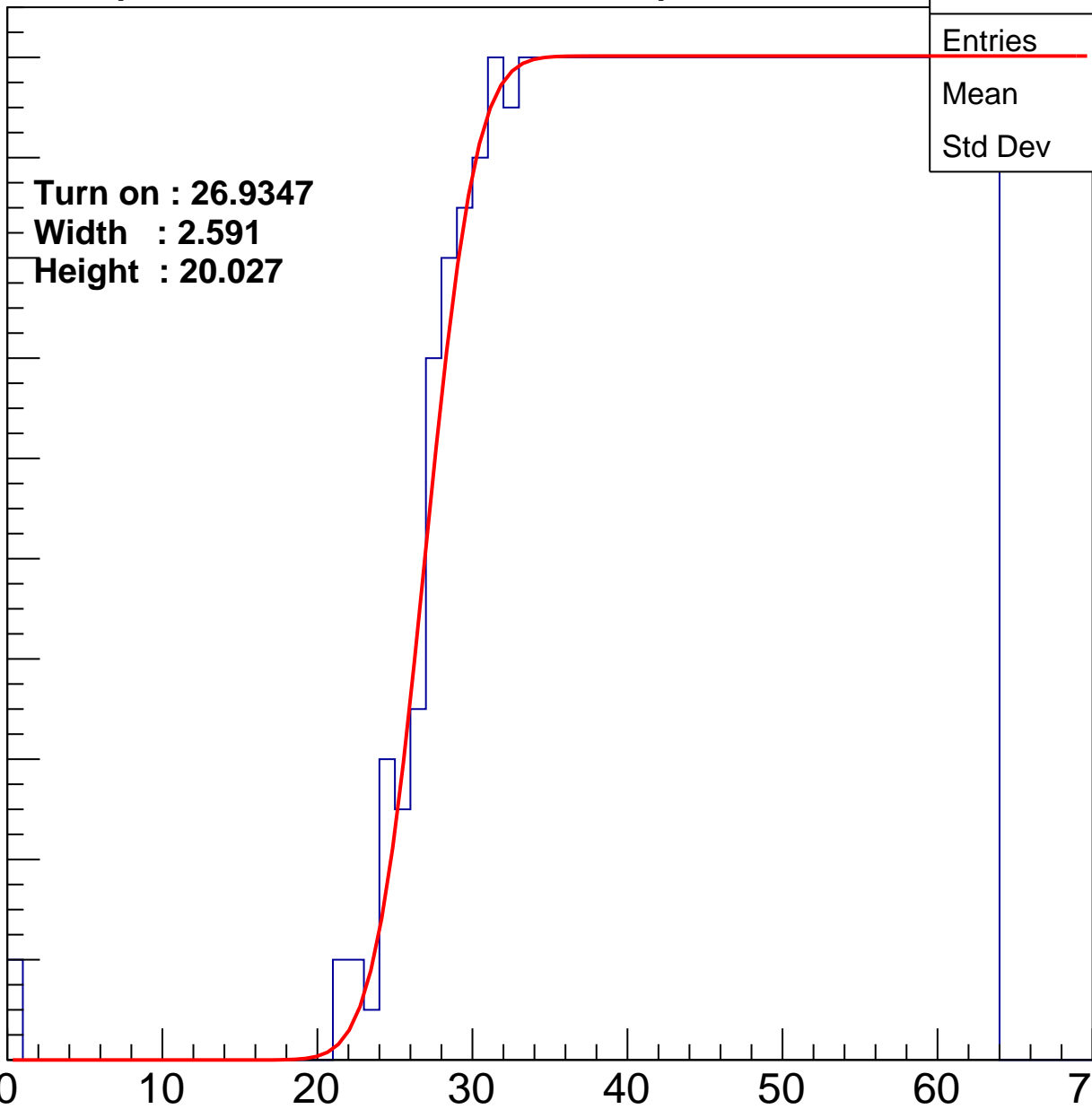
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9347
Width : 2.591
Height : 20.027

Entries	749
Mean	44.6
Std Dev	11.18

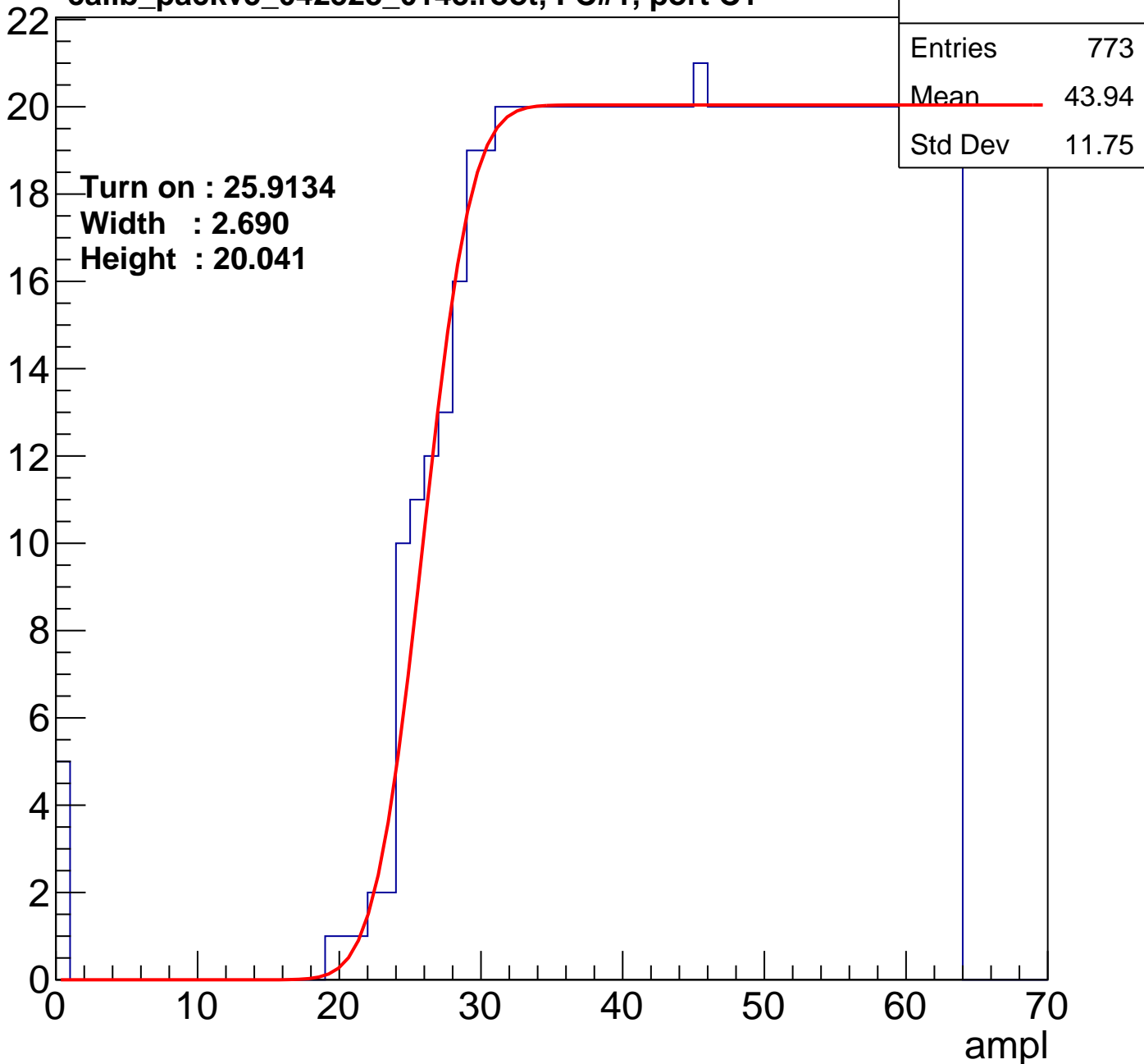
ampl



B0L101S, U21-ch84

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch85

calib_packv5_042523_0143.root, FC#1, port C1

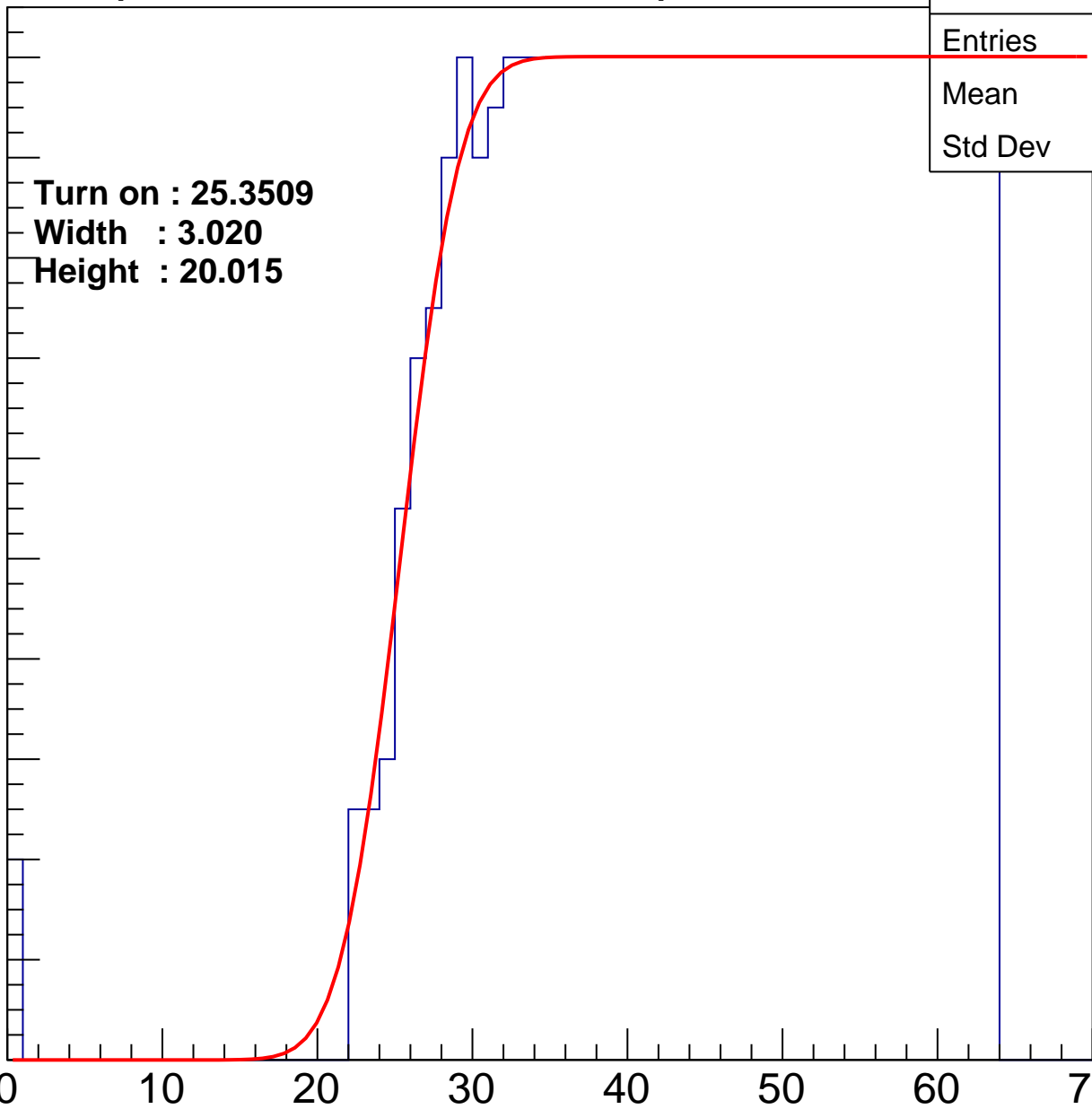
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3509
Width : 3.020
Height : 20.015

Entries	775
Mean	43.9
Std Dev	11.69

ampl



B0L101S, U21-ch86

calib_packv5_042523_0143.root, FC#1, port C1

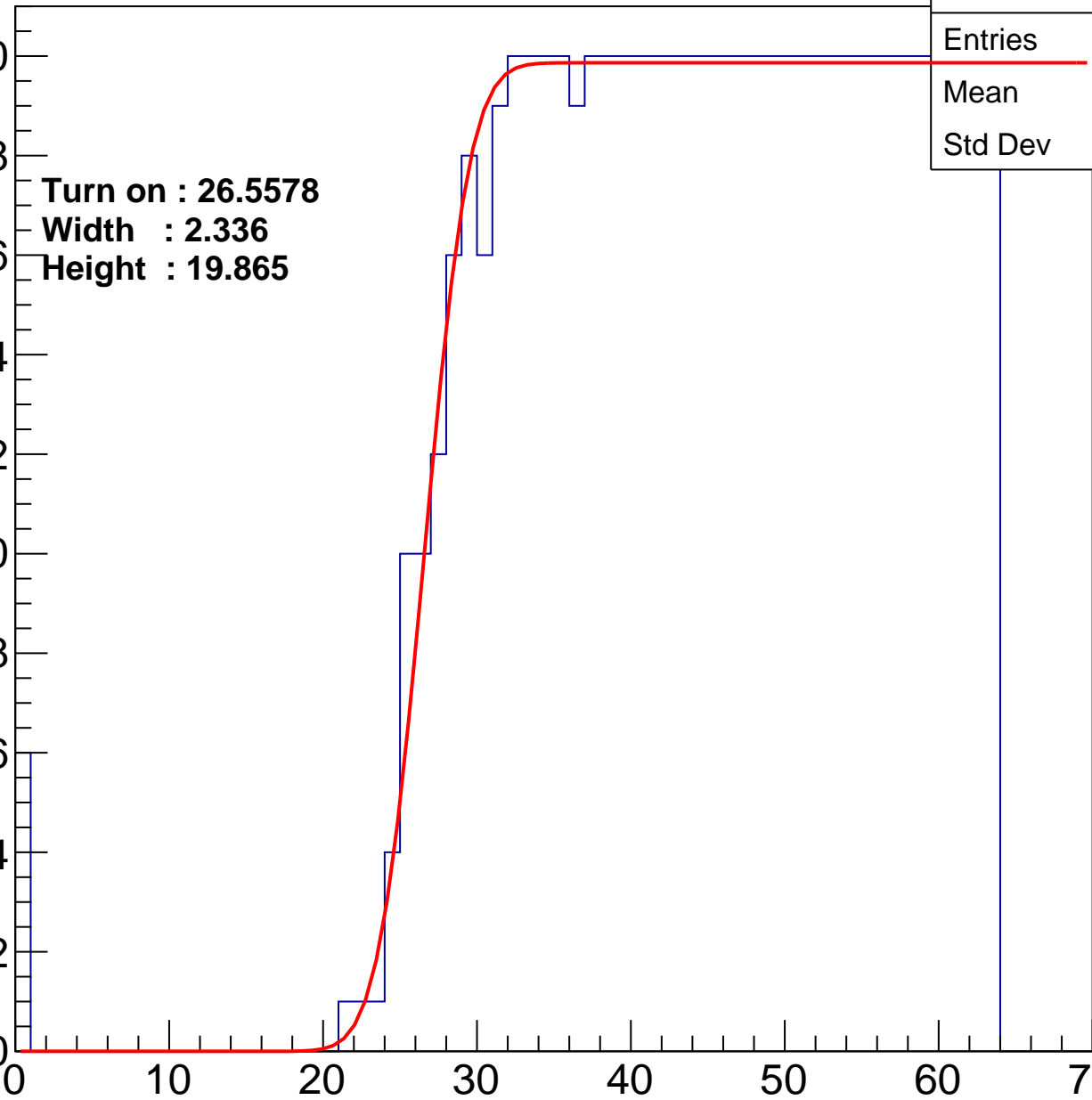
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5578
Width : 2.336
Height : 19.865

Entries	753
Mean	44.35
Std Dev	11.62

ampl



B0L101S, U21-ch87

calib_packv5_042523_0143.root, FC#1, port C1

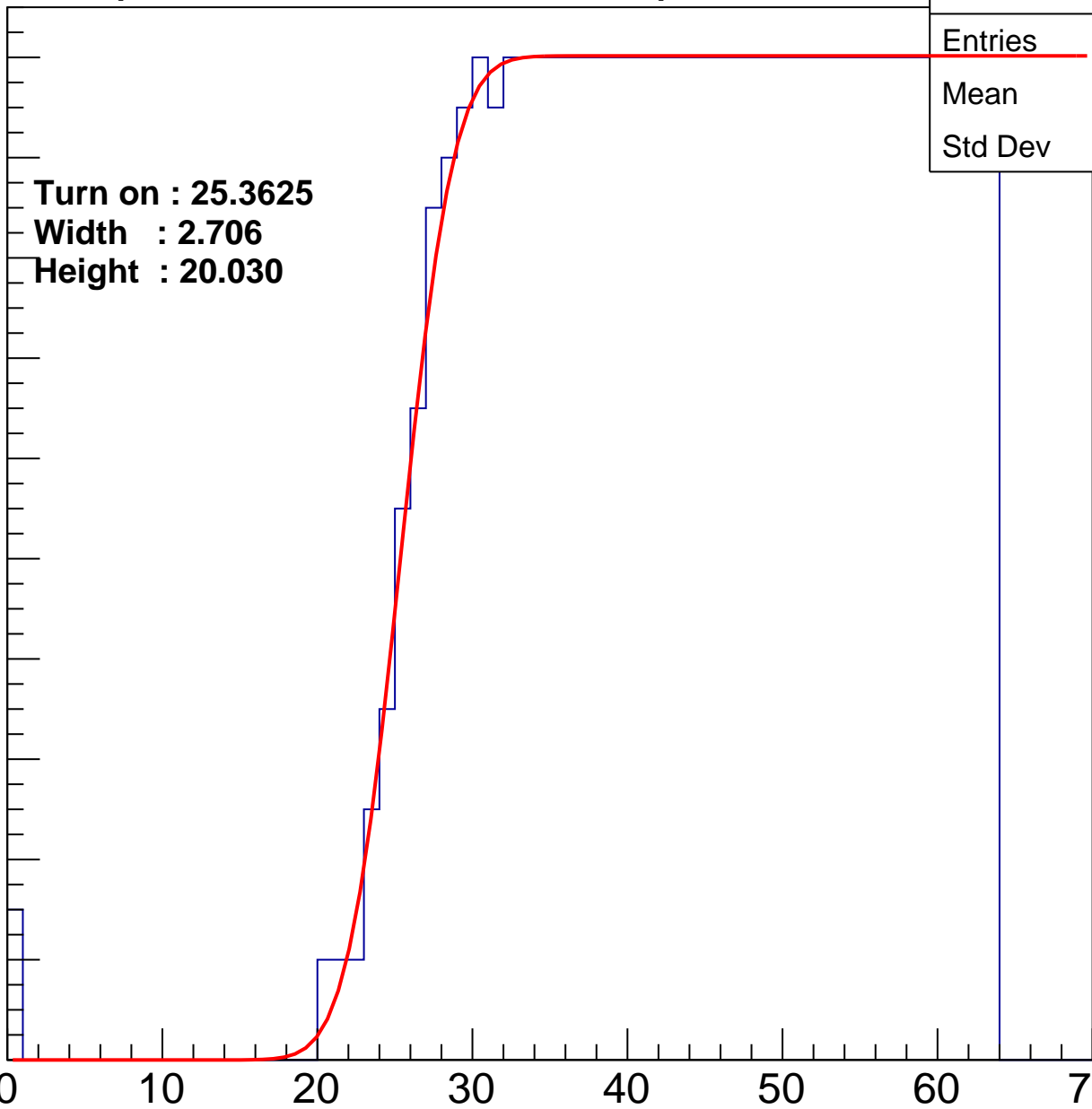
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3625
Width : 2.706
Height : 20.030

Entries	778
Mean	43.86
Std Dev	11.64

ampl



B0L101S, U21-ch88

calib_packv5_042523_0143.root, FC#1, port C1

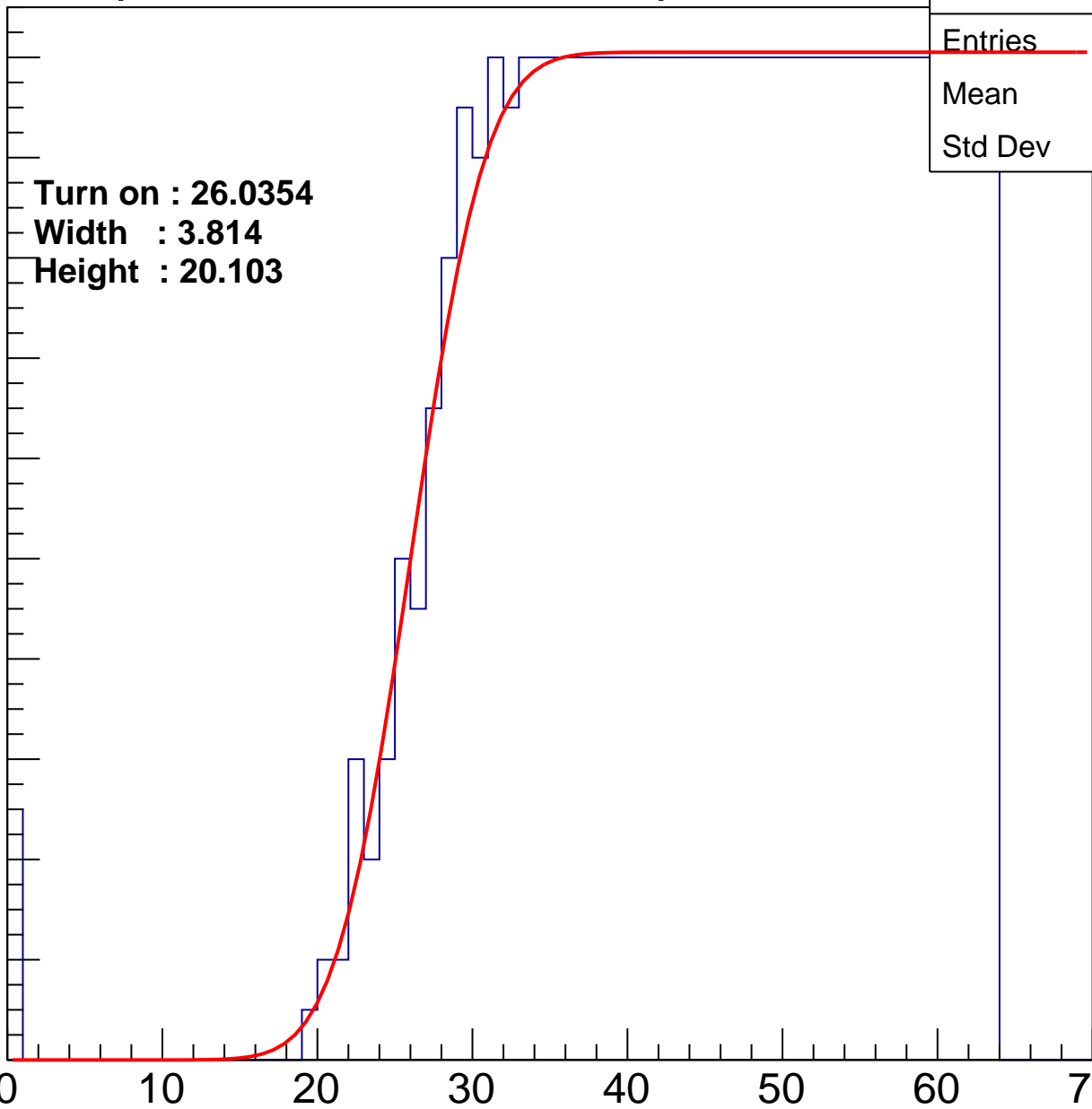
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0354
Width : 3.814
Height : 20.103

Entries	770
Mean	43.94
Std Dev	11.81

ampl



B0L101S, U21-ch89

calib_packv5_042523_0143.root, FC#1, port C1

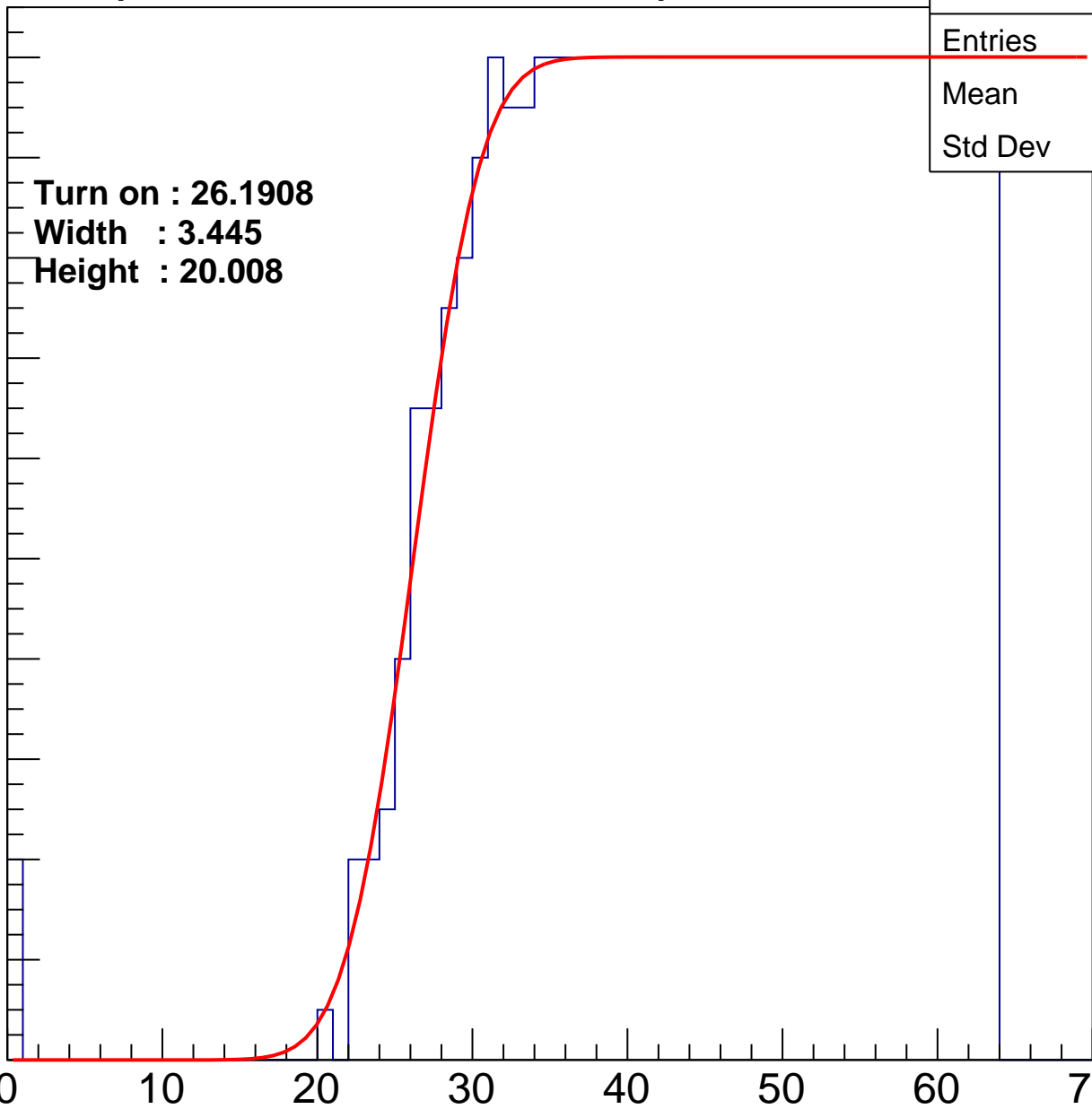
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1908
Width : 3.445
Height : 20.008

Entries	759
Mean	44.25
Std Dev	11.55

ampl



B0L101S, U21-ch90

calib_packv5_042523_0143.root, FC#1, port C1

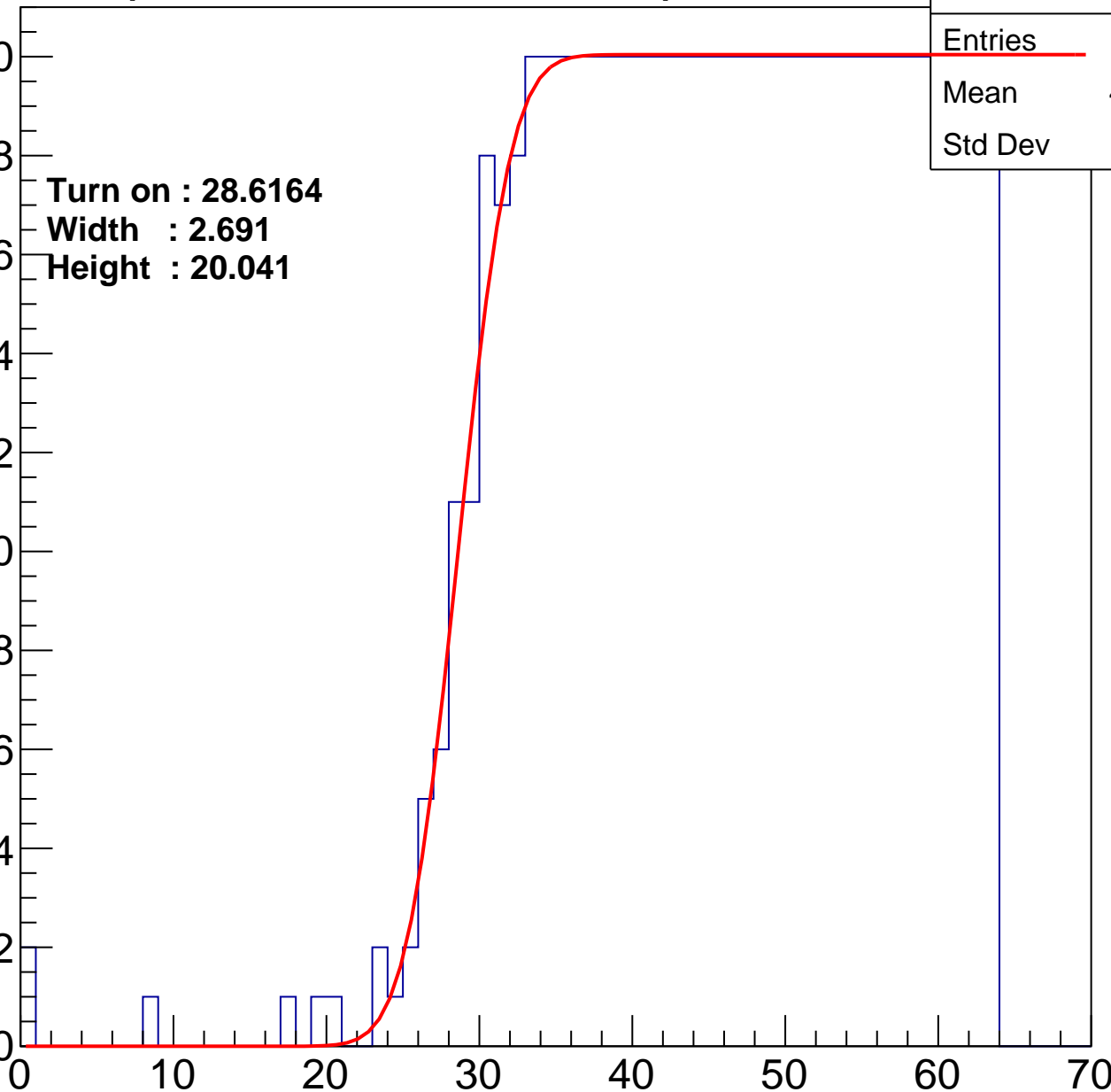
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6164
Width : 2.691
Height : 20.041

Entries	717
Mean	45.34
Std Dev	10.89

ampl



B0L101S, U21-ch91

calib_packv5_042523_0143.root, FC#1, port C1

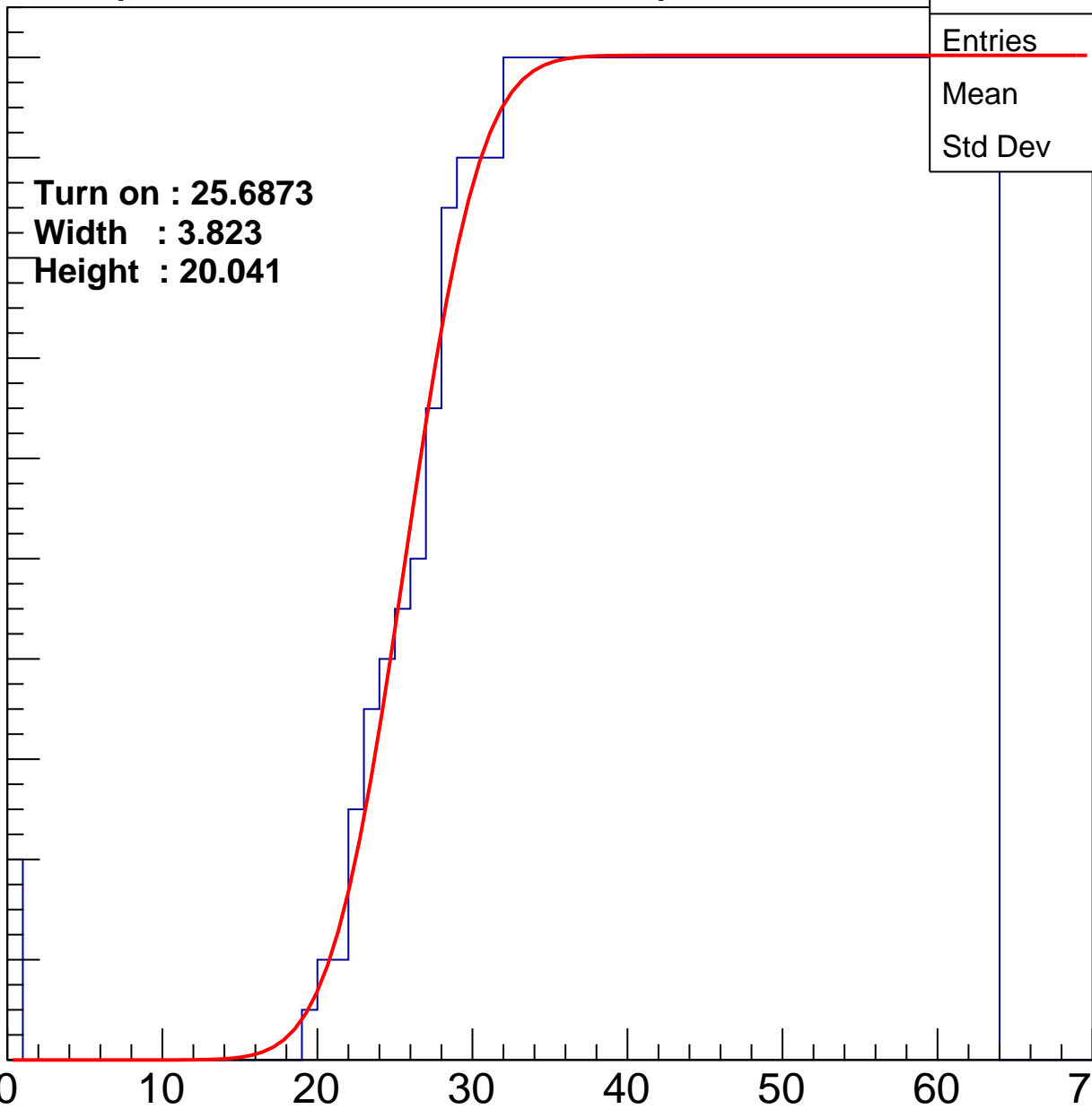
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6873
Width : 3.823
Height : 20.041

Entries	772
Mean	43.91
Std Dev	11.77

ampl



B0L101S, U21-ch92

calib_packv5_042523_0143.root, FC#1, port C1

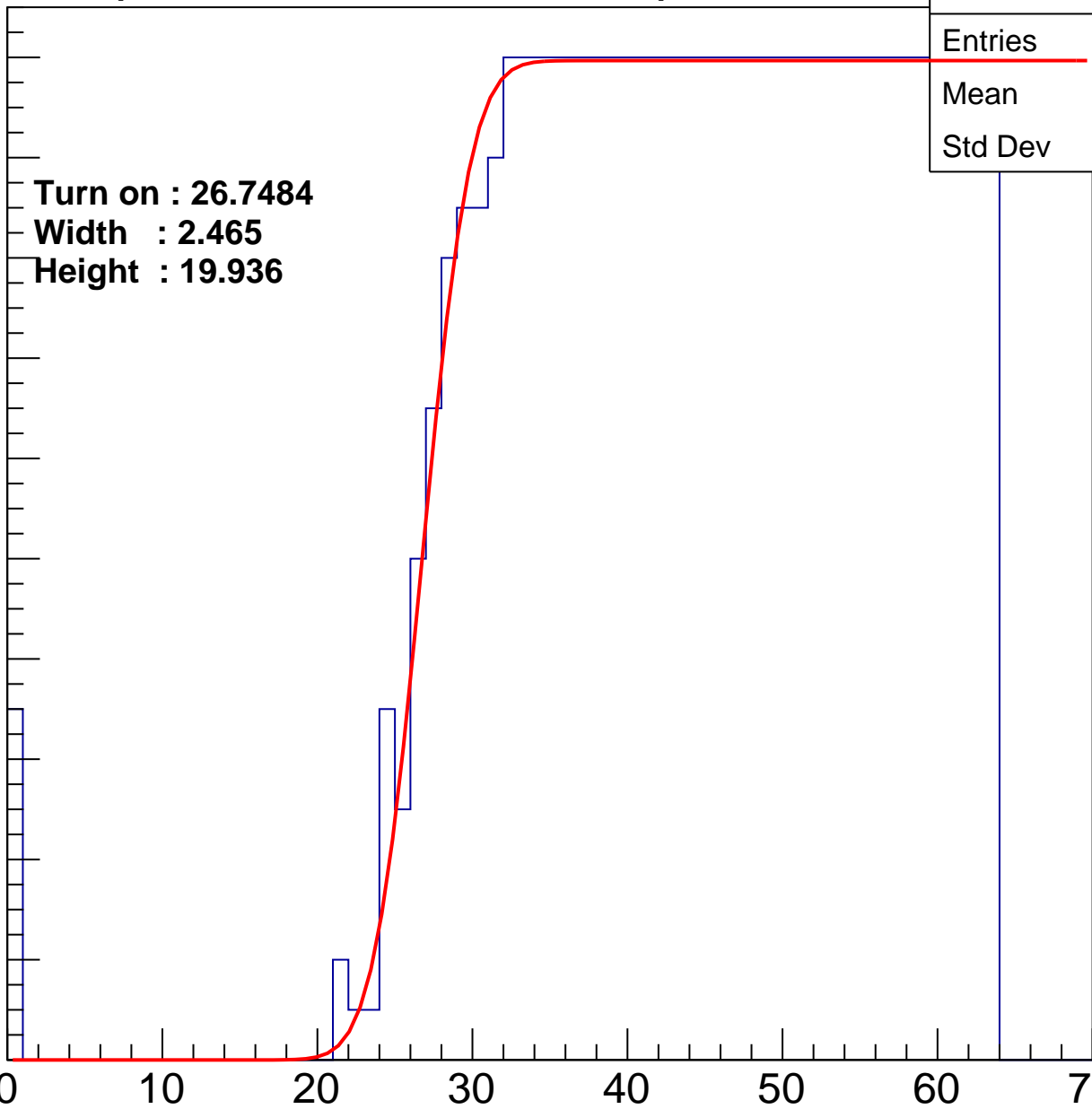
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7484
Width : 2.465
Height : 19.936

Entries	754
Mean	44.3
Std Dev	11.73

ampl



B0L101S, U21-ch93

calib_packv5_042523_0143.root, FC#1, port C1

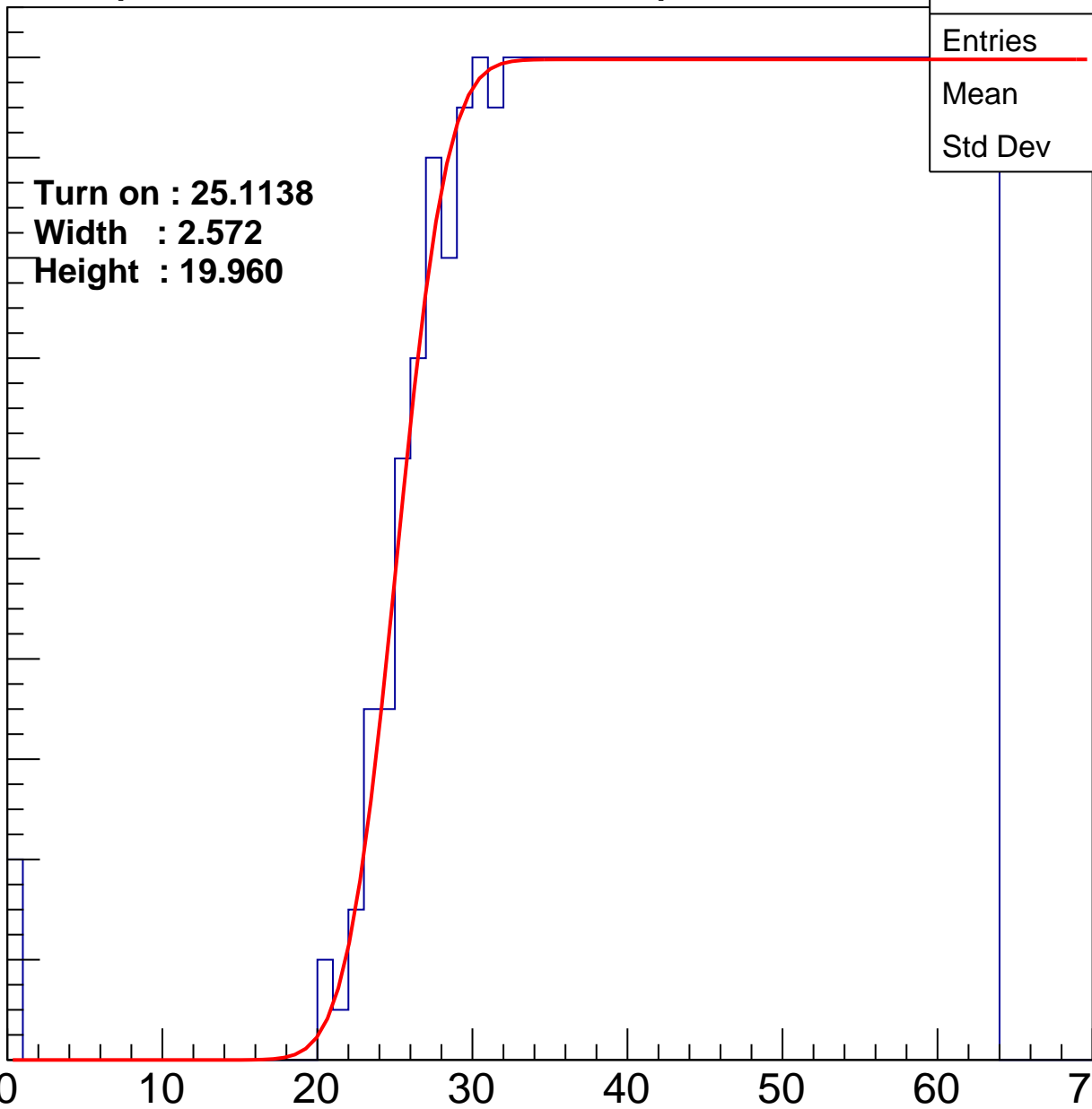
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1138
Width : 2.572
Height : 19.960

Entries	782
Mean	43.73
Std Dev	11.79

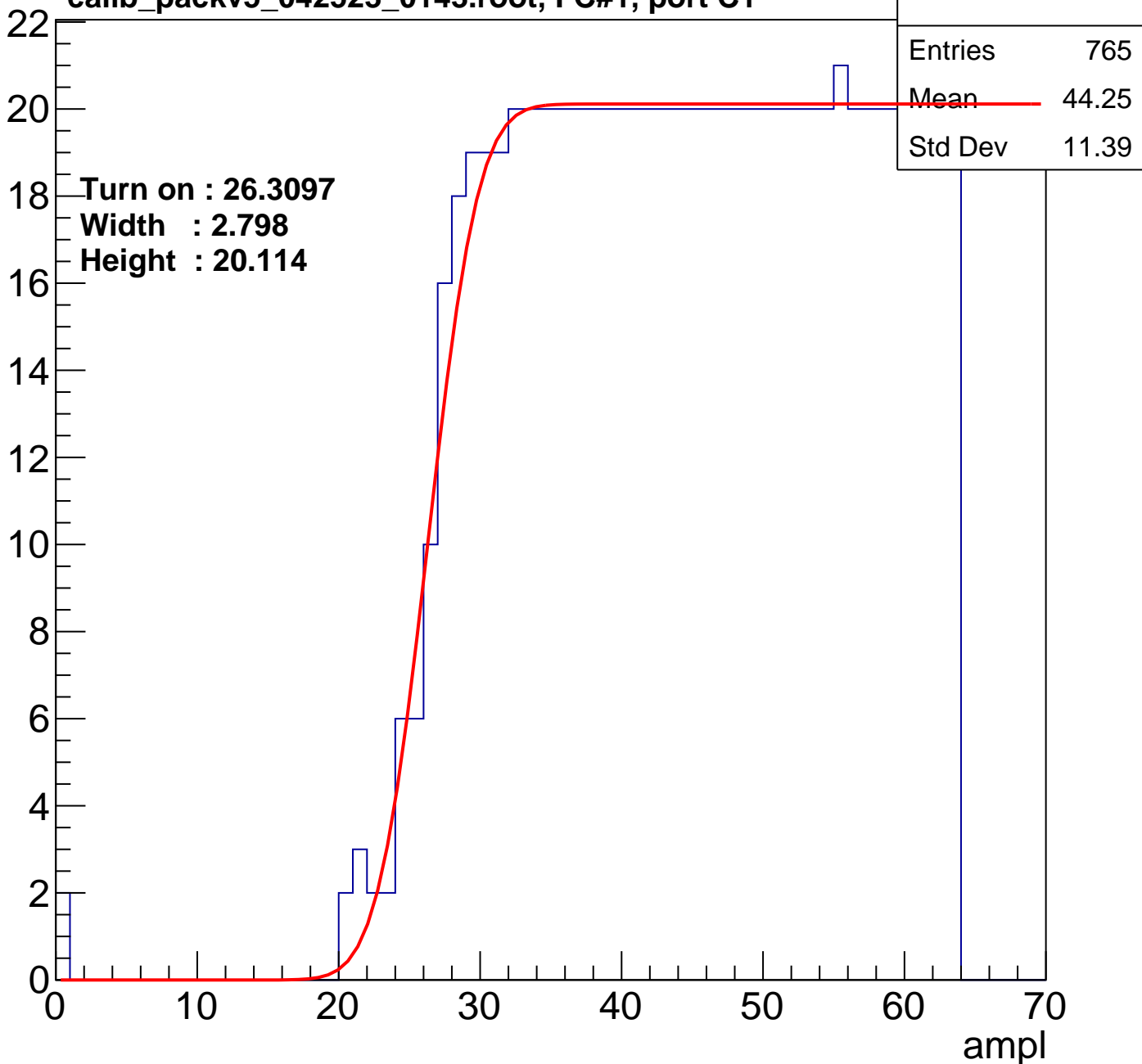
ampl



B0L101S, U21-ch94

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch95

calib_packv5_042523_0143.root, FC#1, port C1

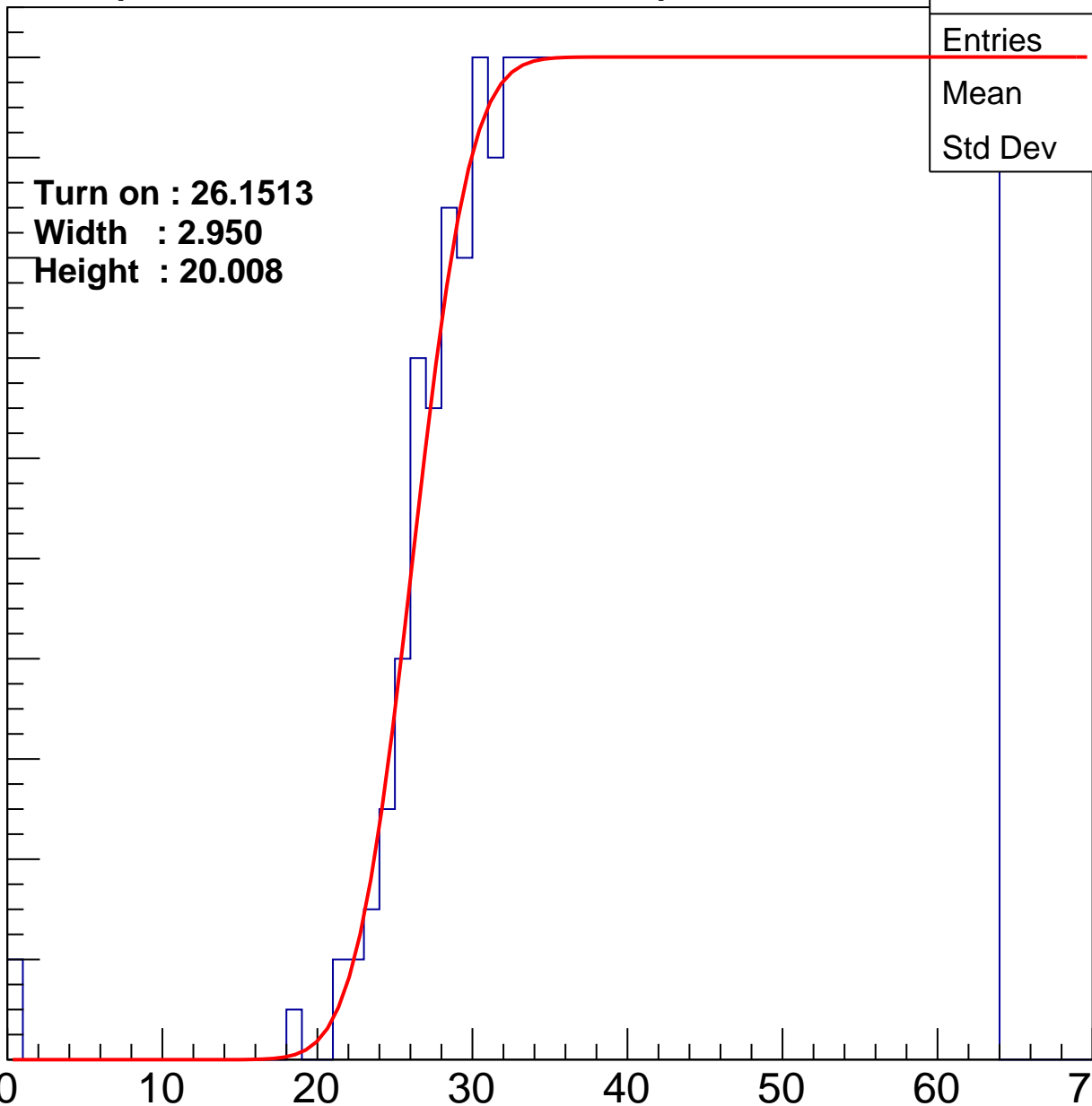
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1513
Width : 2.950
Height : 20.008

Entries	761
Mean	44.29
Std Dev	11.37

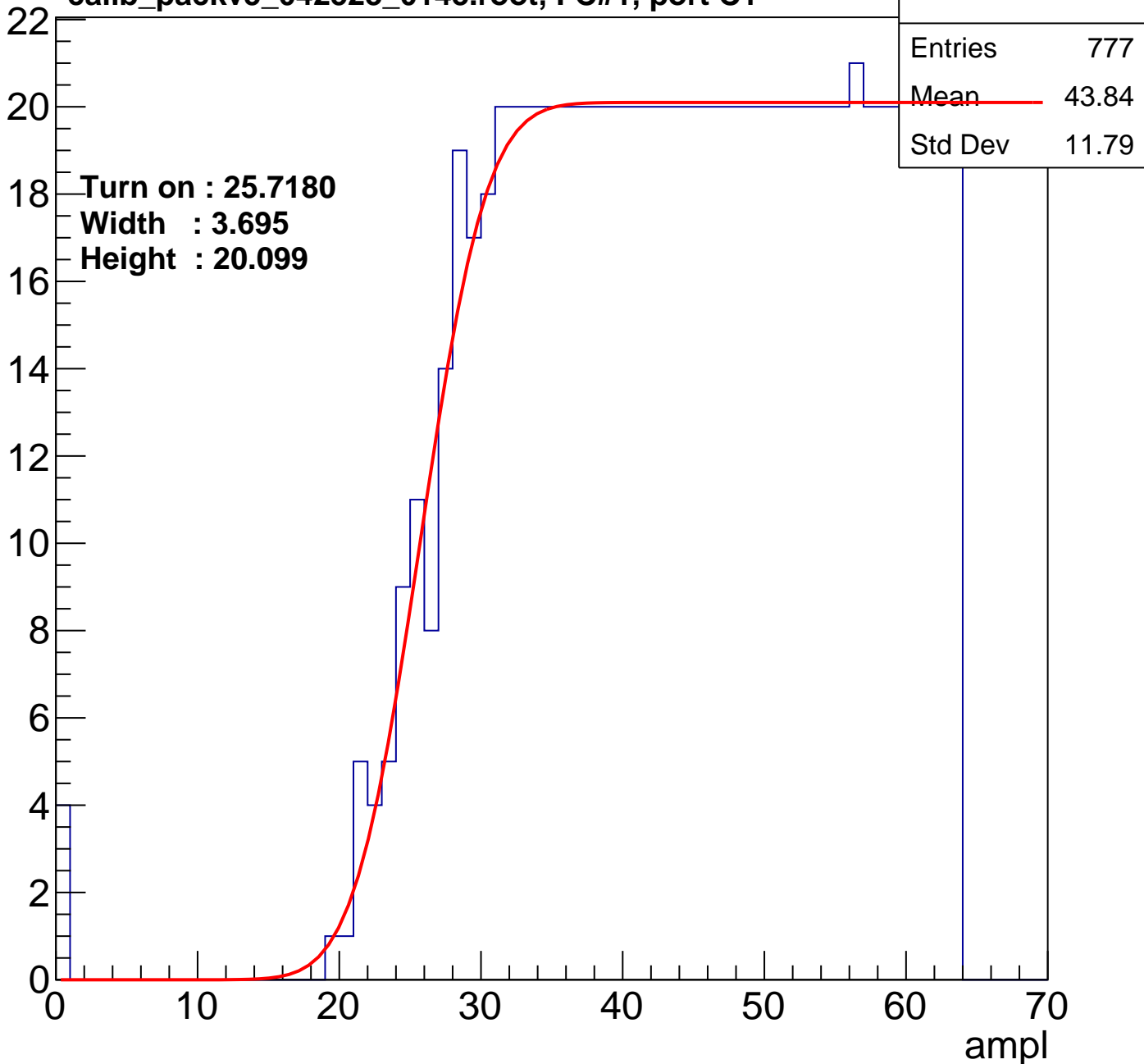
ampl



B0L101S, U21-ch96

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch97

calib_packv5_042523_0143.root, FC#1, port C1

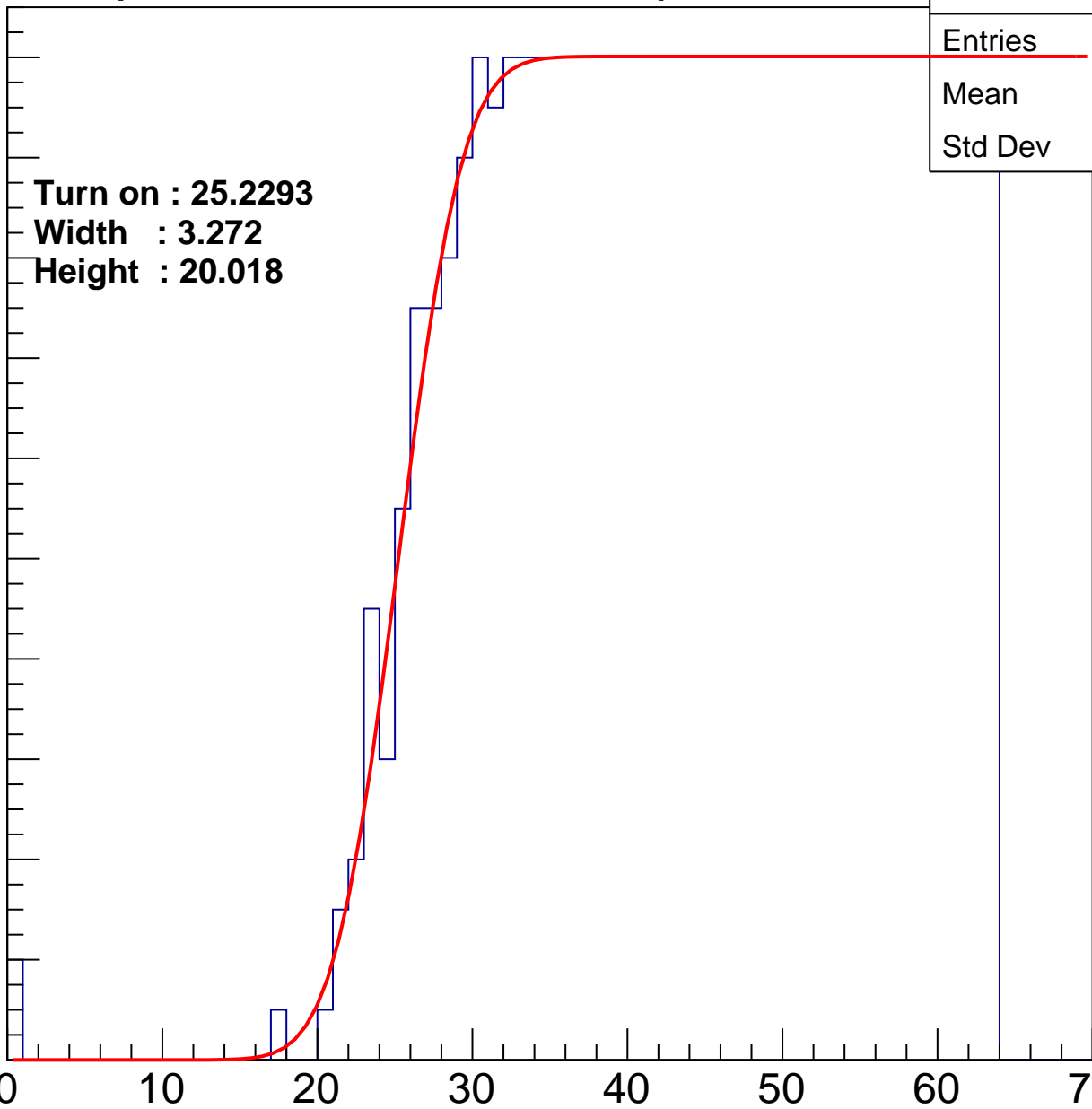
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2293
Width : 3.272
Height : 20.018

Entries	780
Mean	43.81
Std Dev	11.65

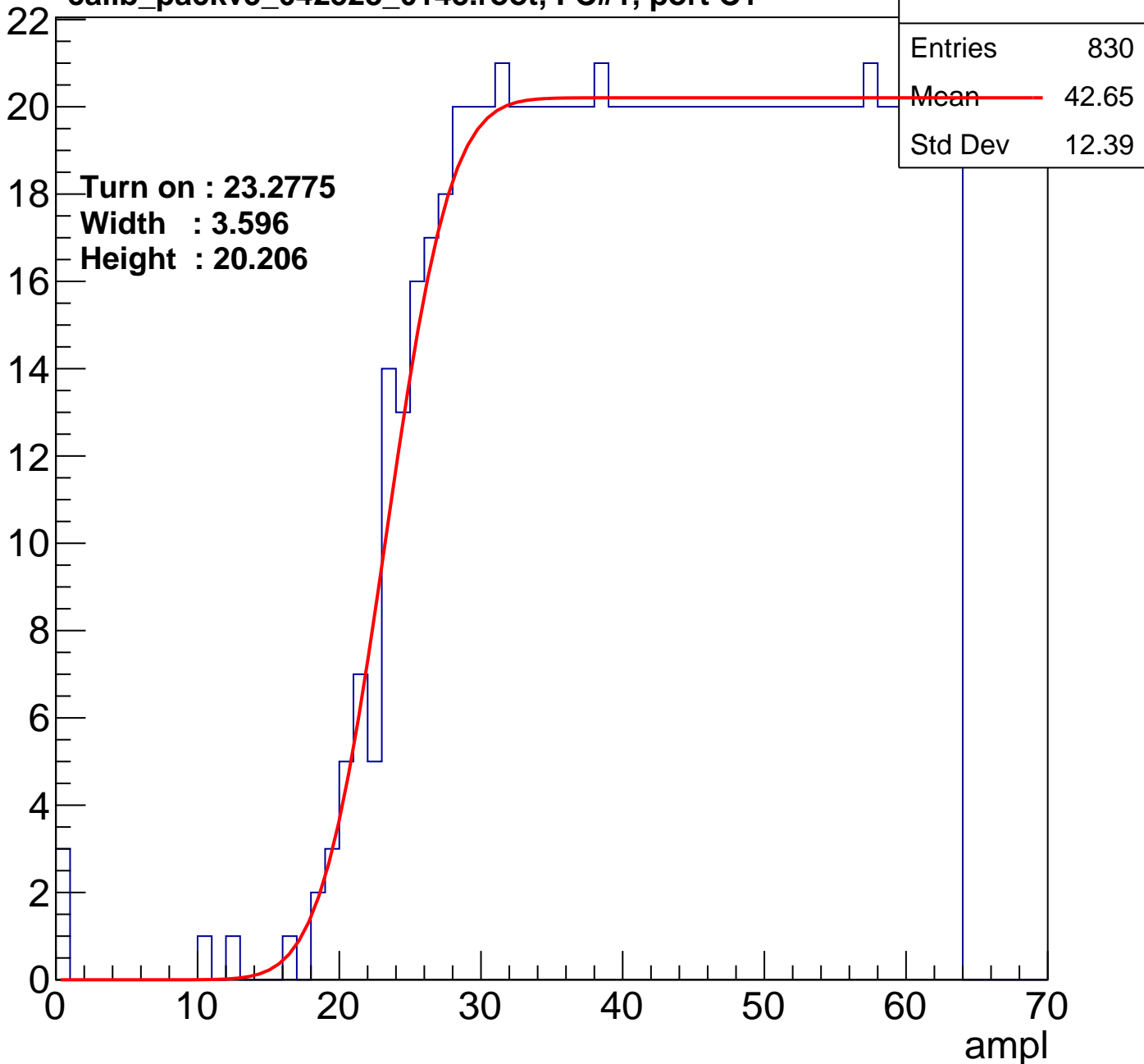
ampl



B0L101S, U21-ch98

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch99

calib_packv5_042523_0143.root, FC#1, port C1

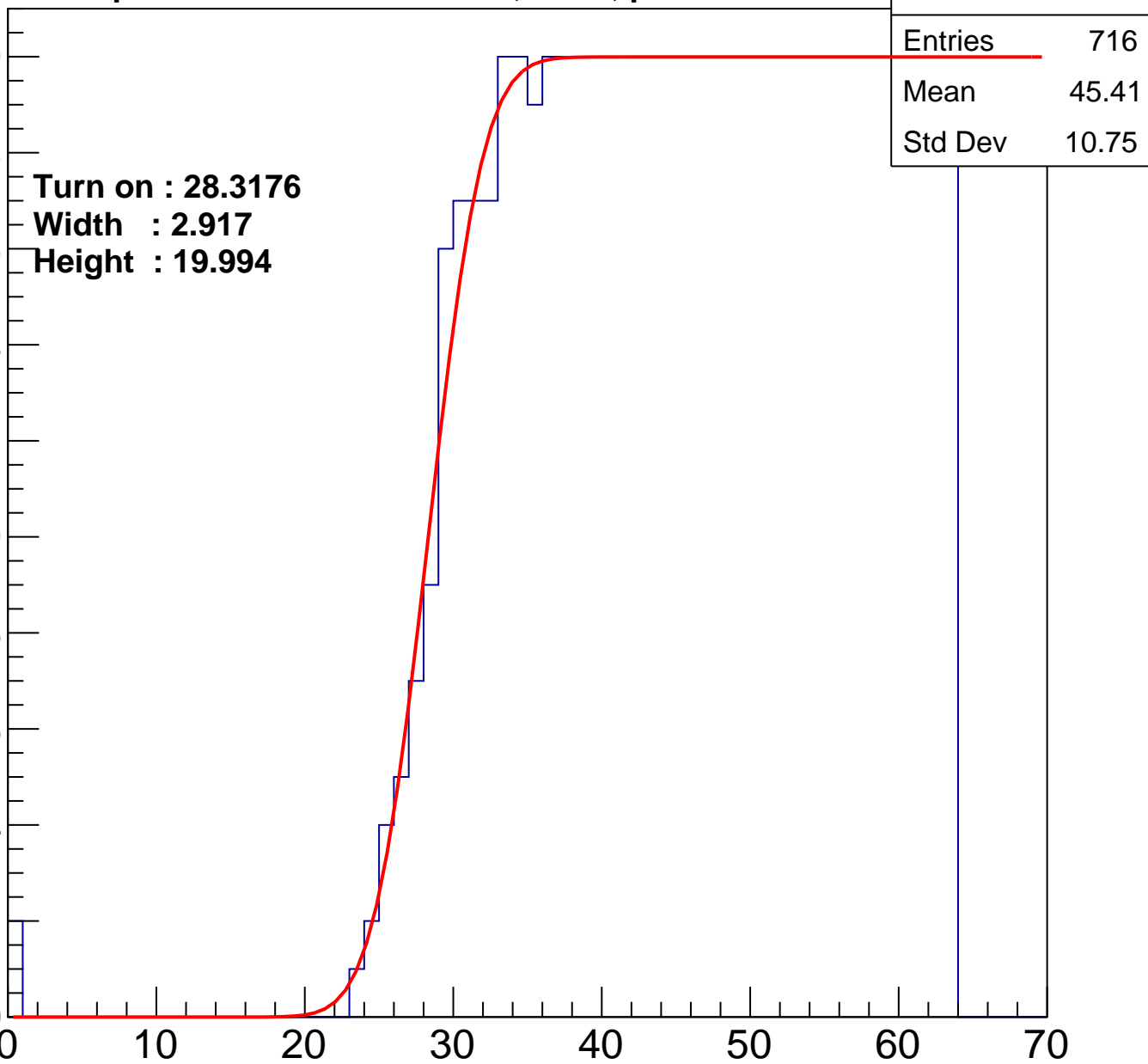
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3176
Width : 2.917
Height : 19.994

Entries	716
Mean	45.41
Std Dev	10.75

ampl



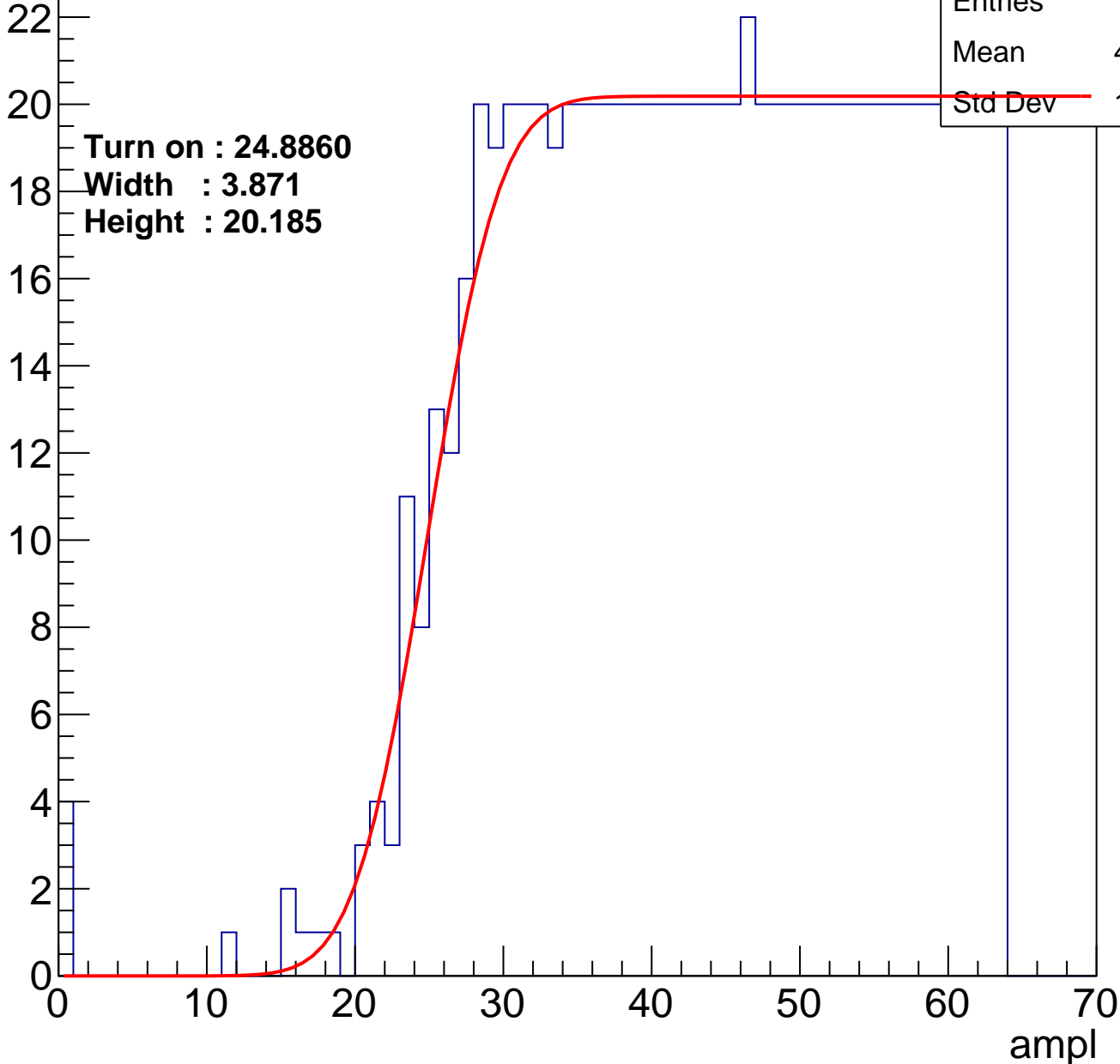
B0L101S, U21-ch100

calib_packv5_042523_0143.root, FC#1, port C1

Entries	800
Mean	43.26
Std Dev	12.13

Turn on : 24.8860
Width : 3.871
Height : 20.185

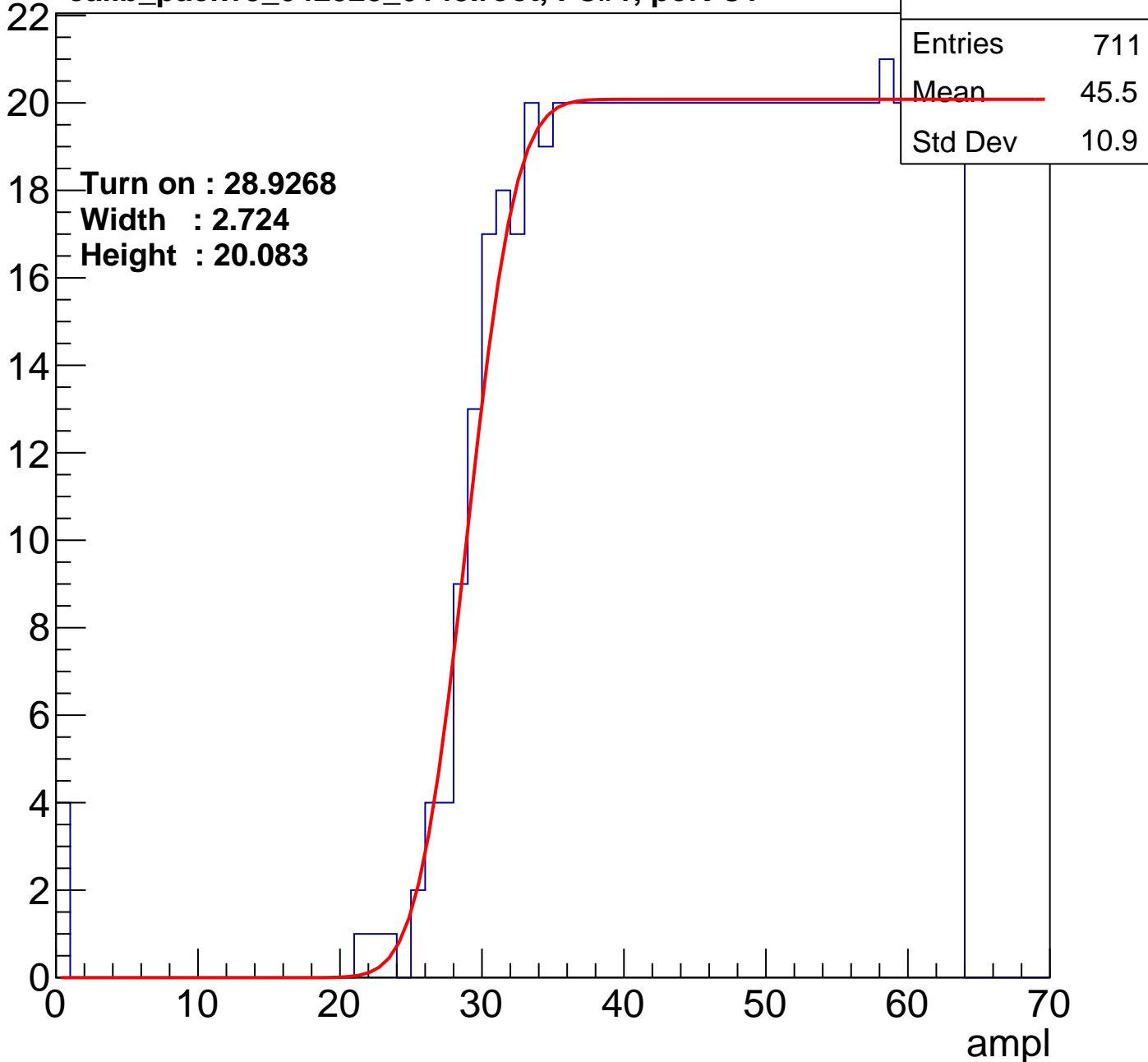
Entry



B0L101S, U21-ch101

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch102

calib_packv5_042523_0143.root, FC#1, port C1

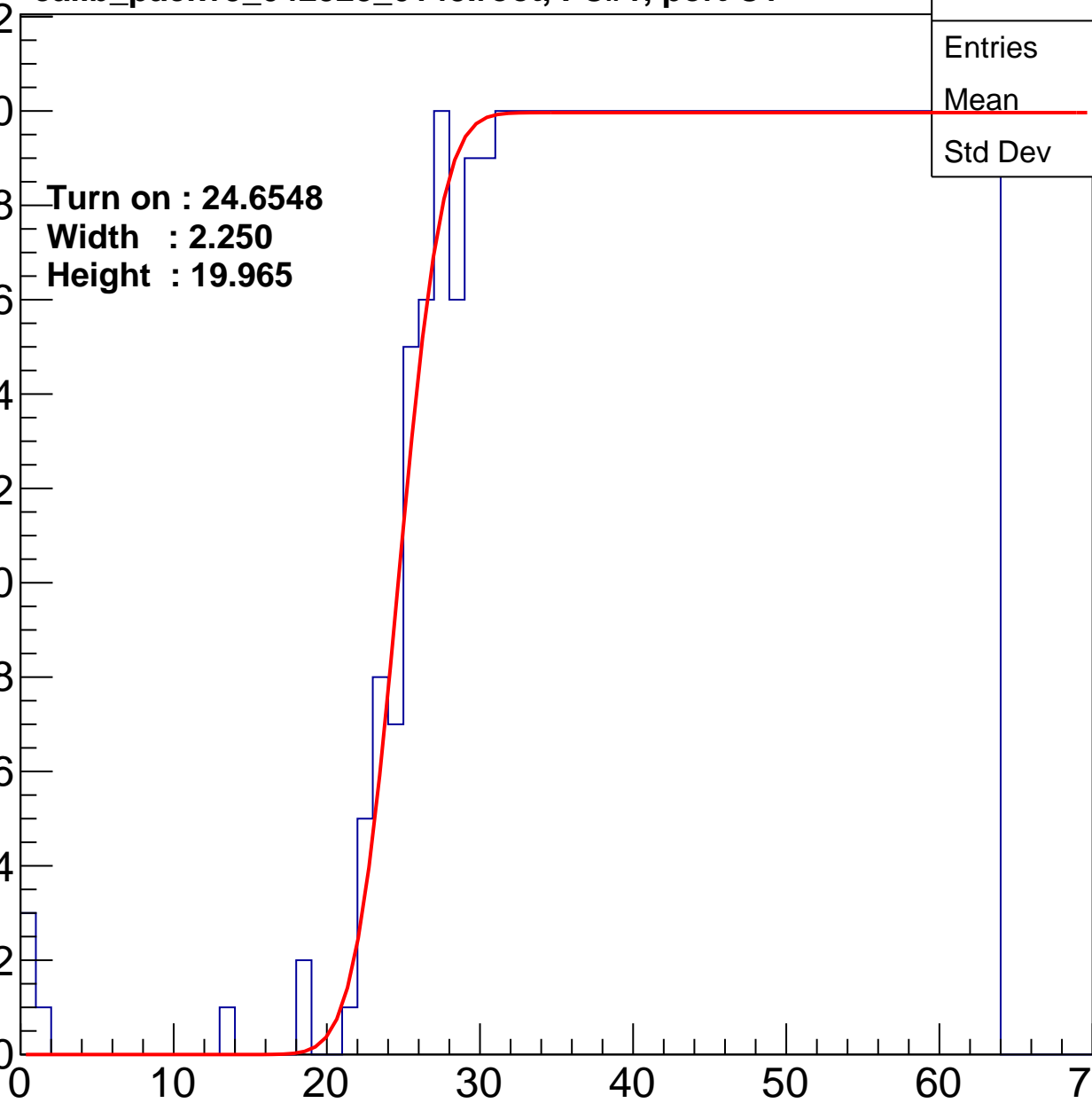
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6548
Width : 2.250
Height : 19.965

Entries	795
Mean	43.49
Std Dev	11.97

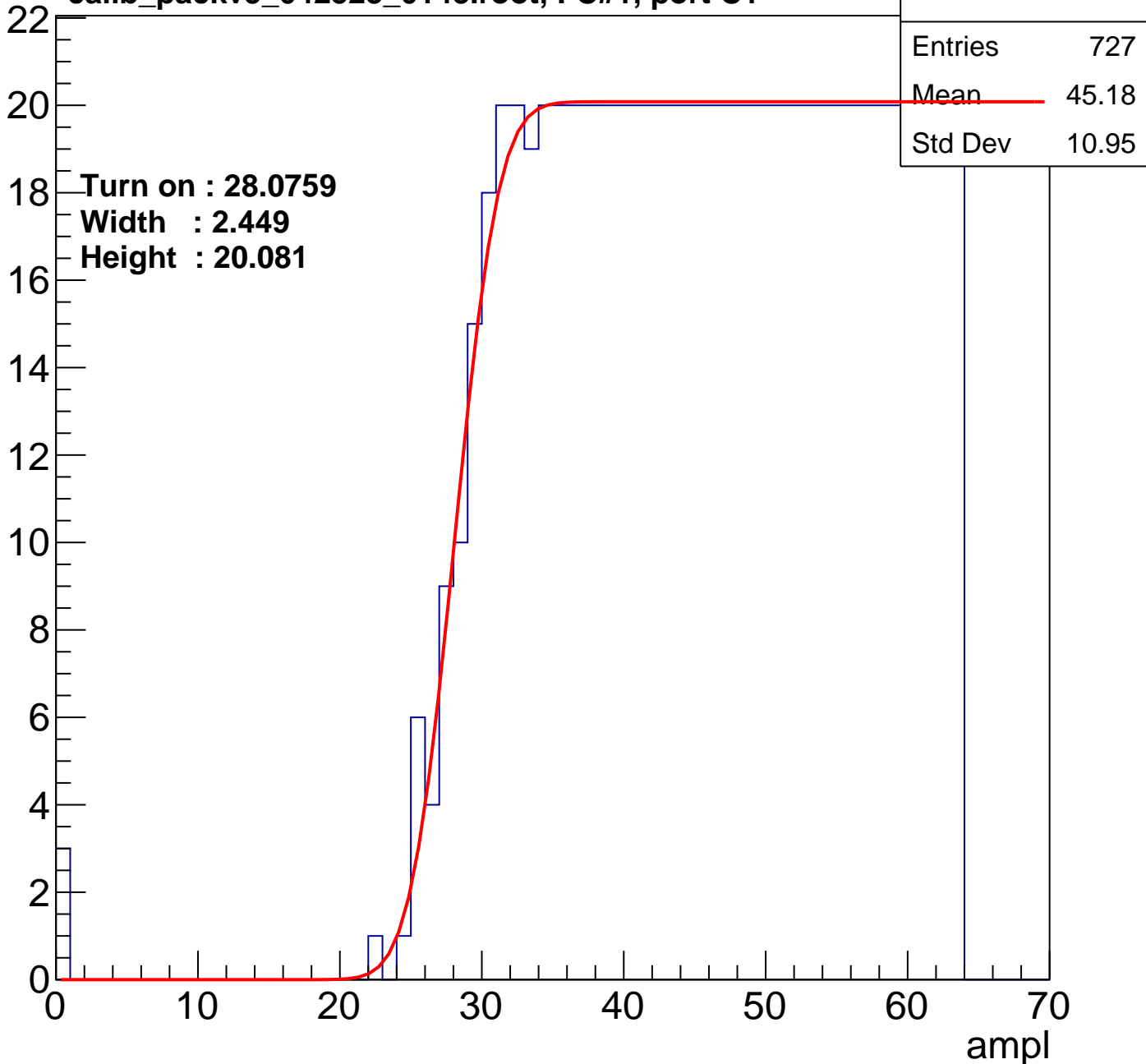
ampl



B0L101S, U21-ch103

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch104

calib_packv5_042523_0143.root, FC#1, port C1

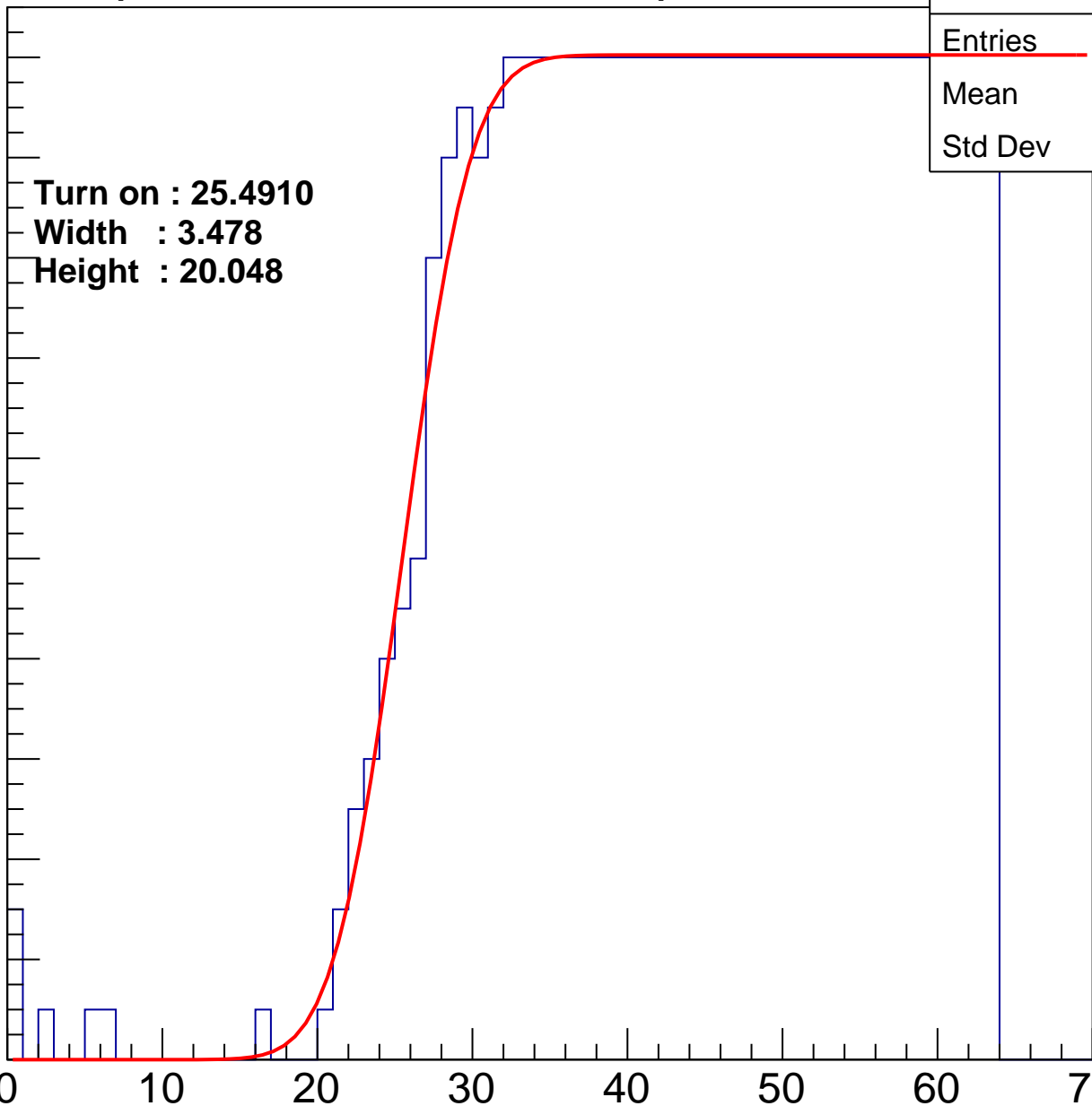
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4910
Width : 3.478
Height : 20.048

Entries	779
Mean	43.71
Std Dev	11.93

ampl



B0L101S, U21-ch105

calib_packv5_042523_0143.root, FC#1, port C1

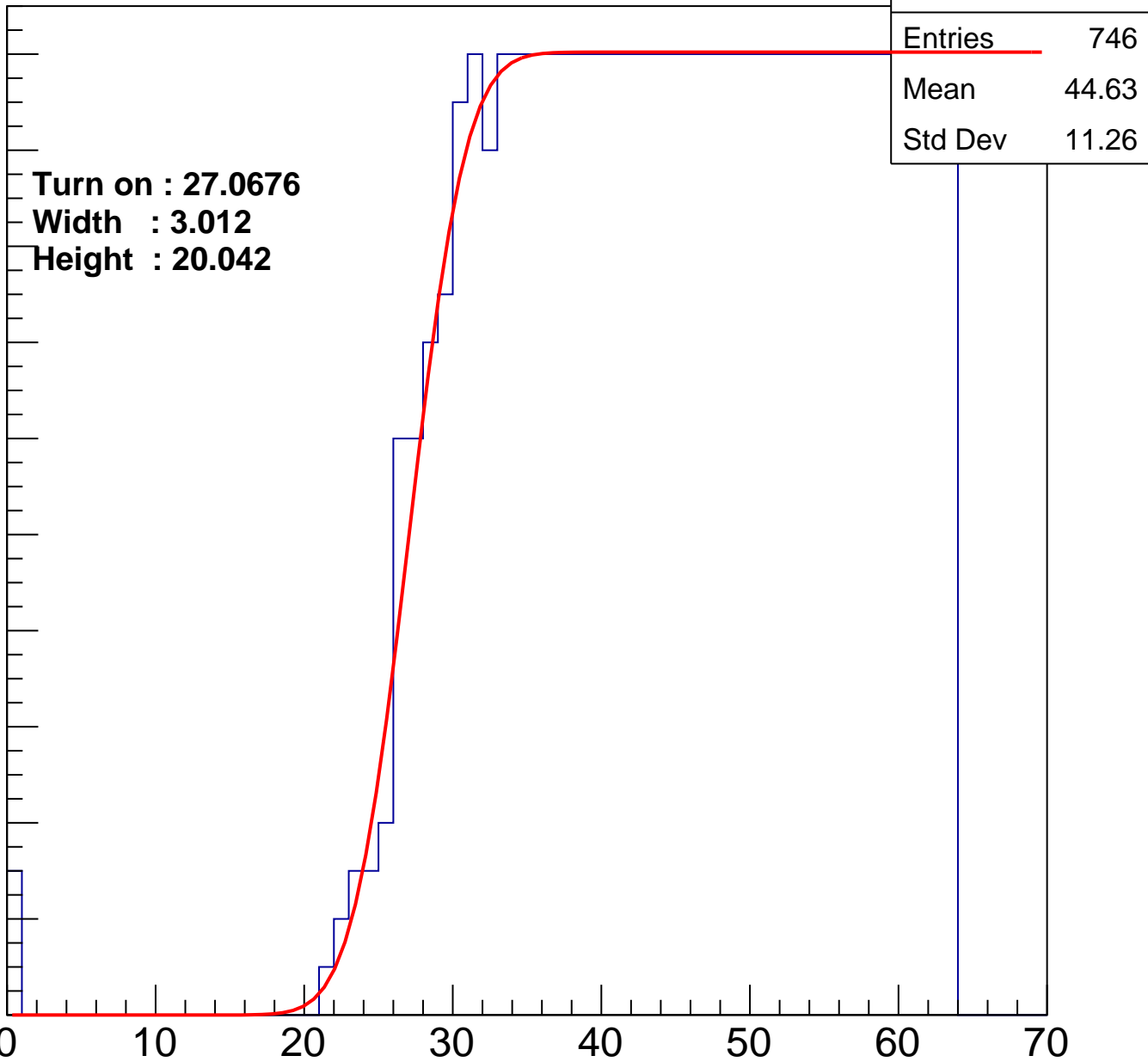
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0676
Width : 3.012
Height : 20.042

Entries	746
Mean	44.63
Std Dev	11.26

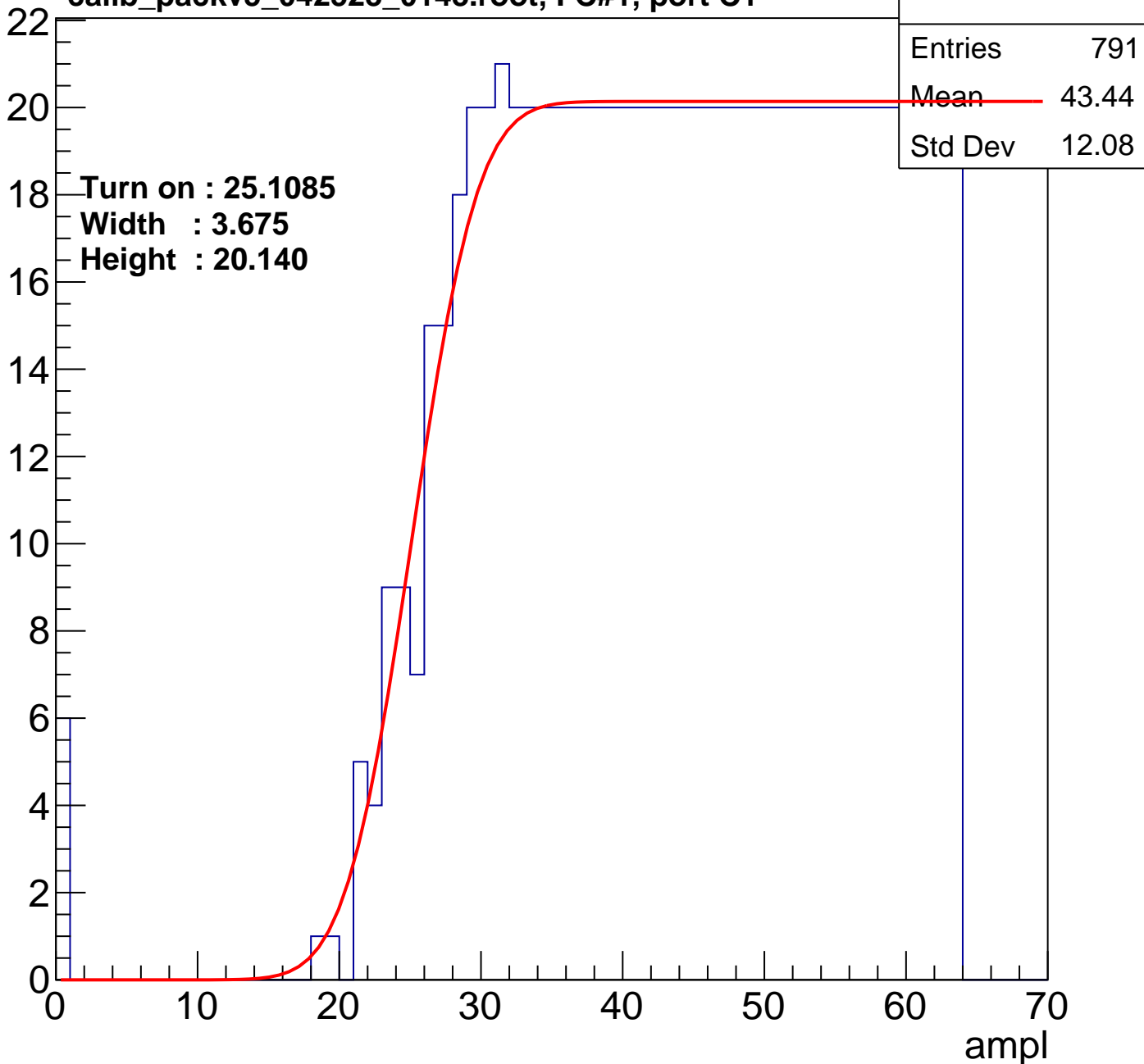
ampl



B0L101S, U21-ch106

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch107

calib_packv5_042523_0143.root, FC#1, port C1

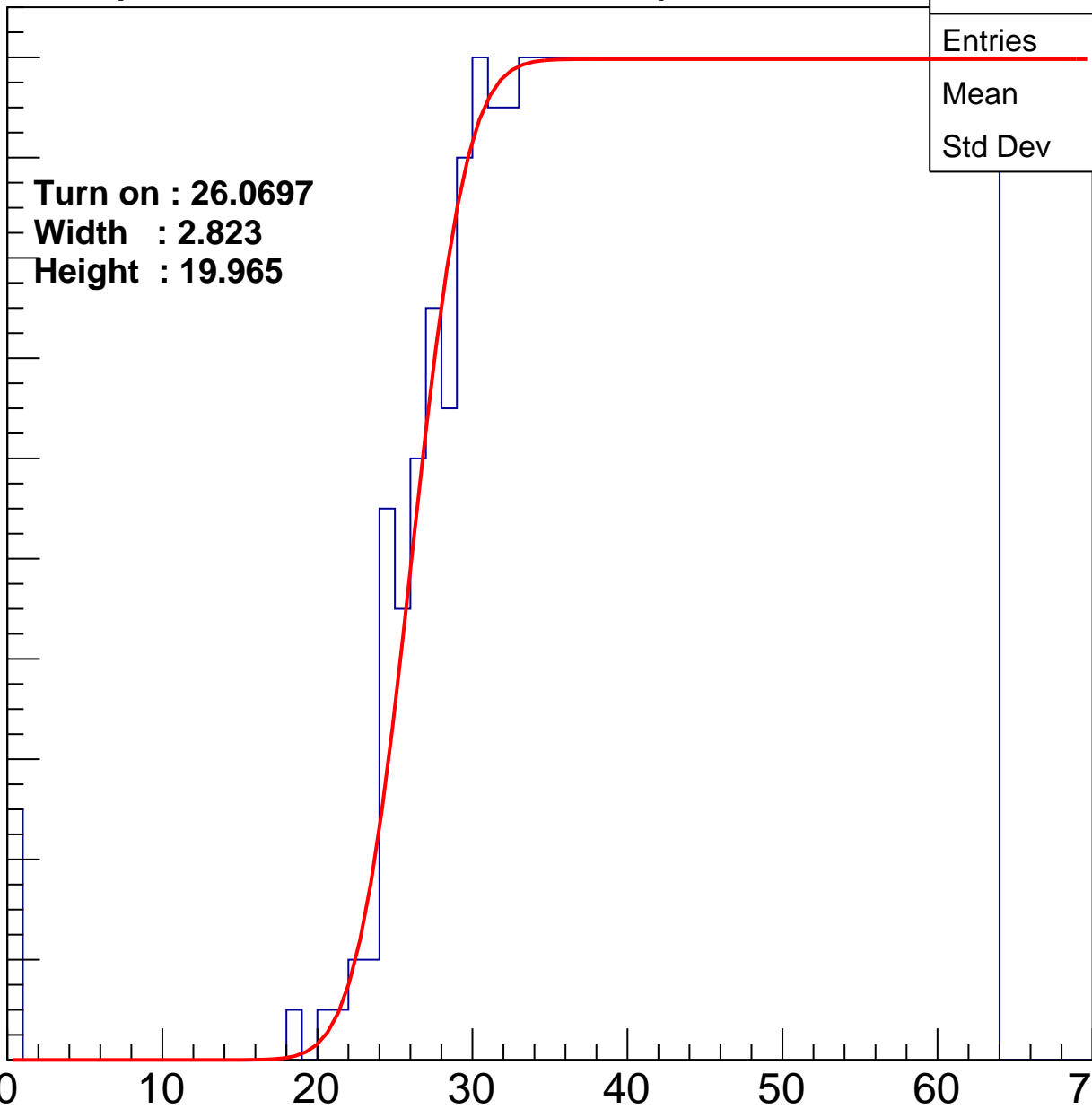
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0697
Width : 2.823
Height : 19.965

Entries	768
Mean	44.01
Std Dev	11.75

ampl

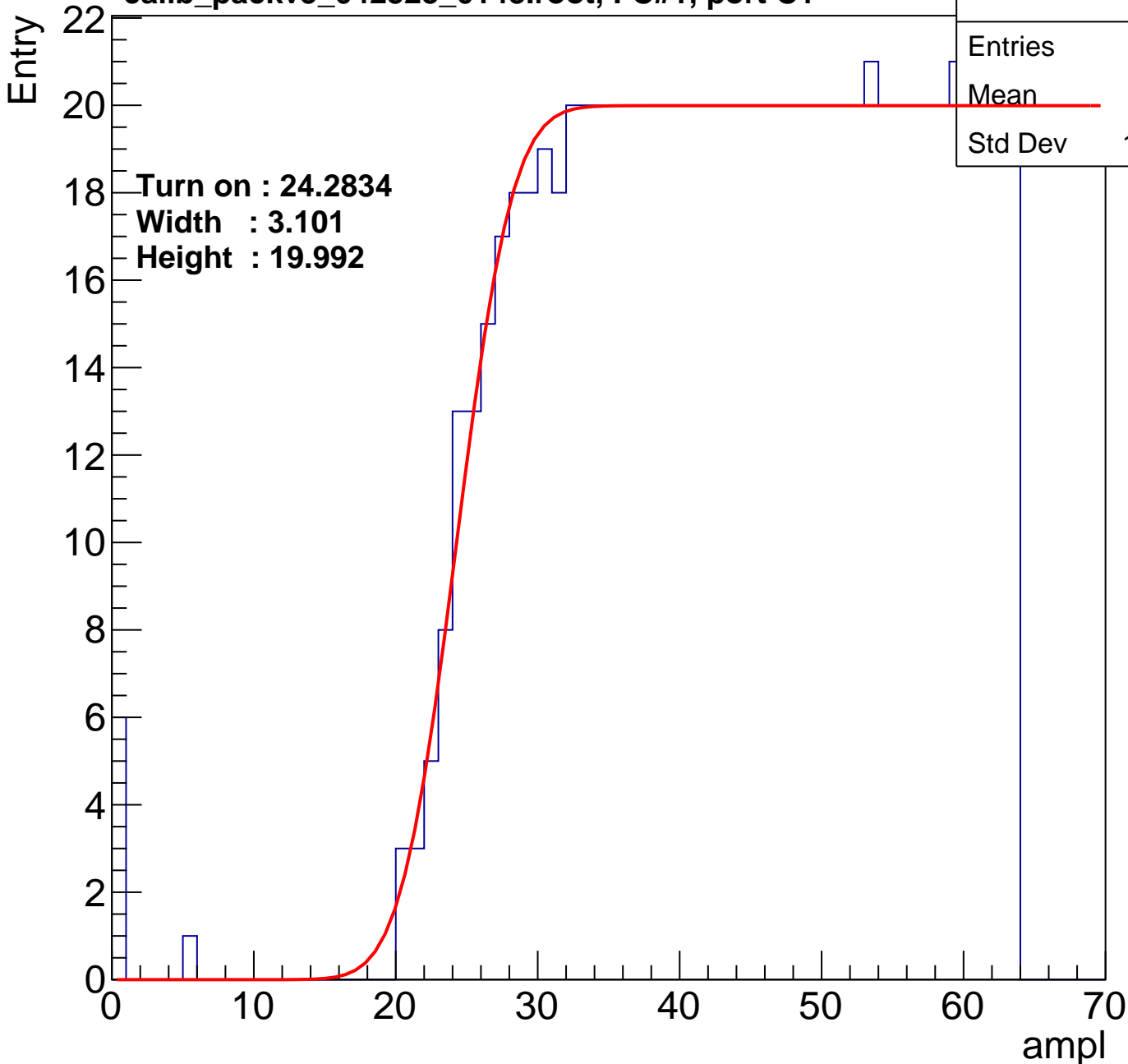


B0L101S, U21-ch108

calib_packv5_042523_0143.root, FC#1, port C1

Entries	800
Mean	43.3
Std Dev	12.26

Turn on : 24.2834
Width : 3.101
Height : 19.992



B0L101S, U21-ch109

calib_packv5_042523_0143.root, FC#1, port C1

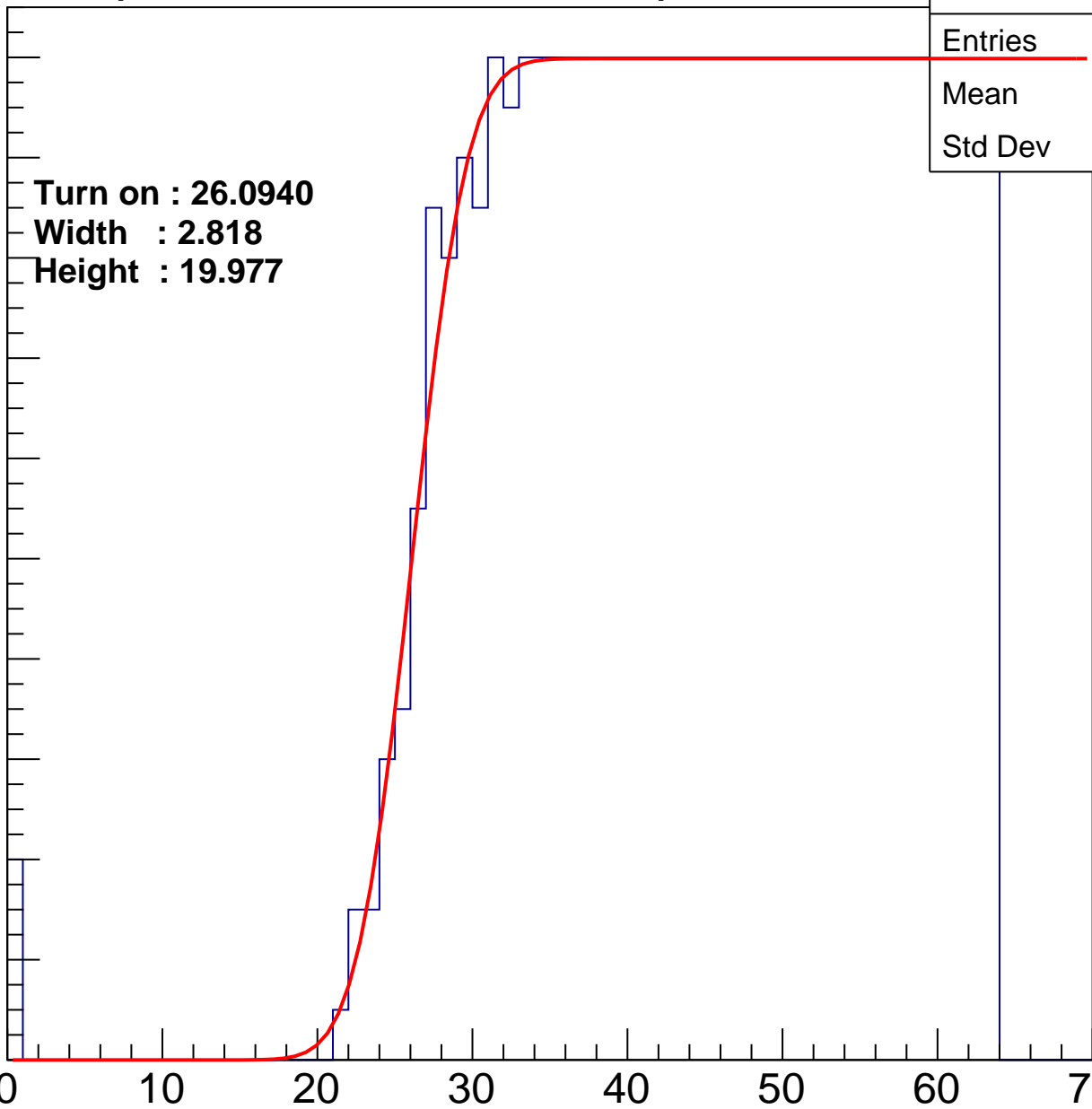
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0940
Width : 2.818
Height : 19.977

Entries	762
Mean	44.21
Std Dev	11.54

ampl



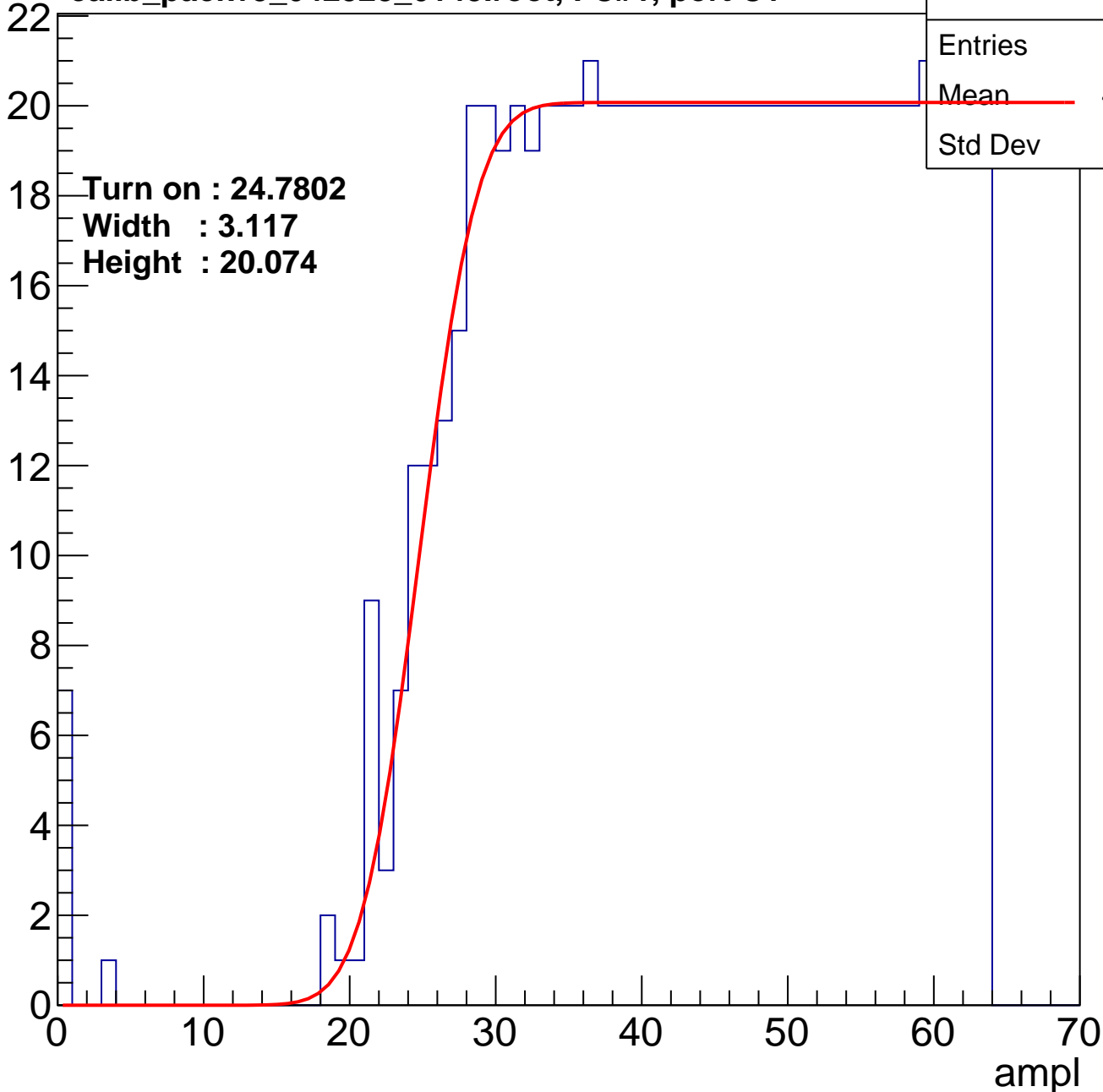
B0L101S, U21-ch110

calib_packv5_042523_0143.root, FC#1, port C1

Entries	803
Mean	43.11
Std Dev	12.4

Turn on : 24.7802
Width : 3.117
Height : 20.074

Entry



B0L101S, U21-ch111

calib_packv5_042523_0143.root, FC#1, port C1

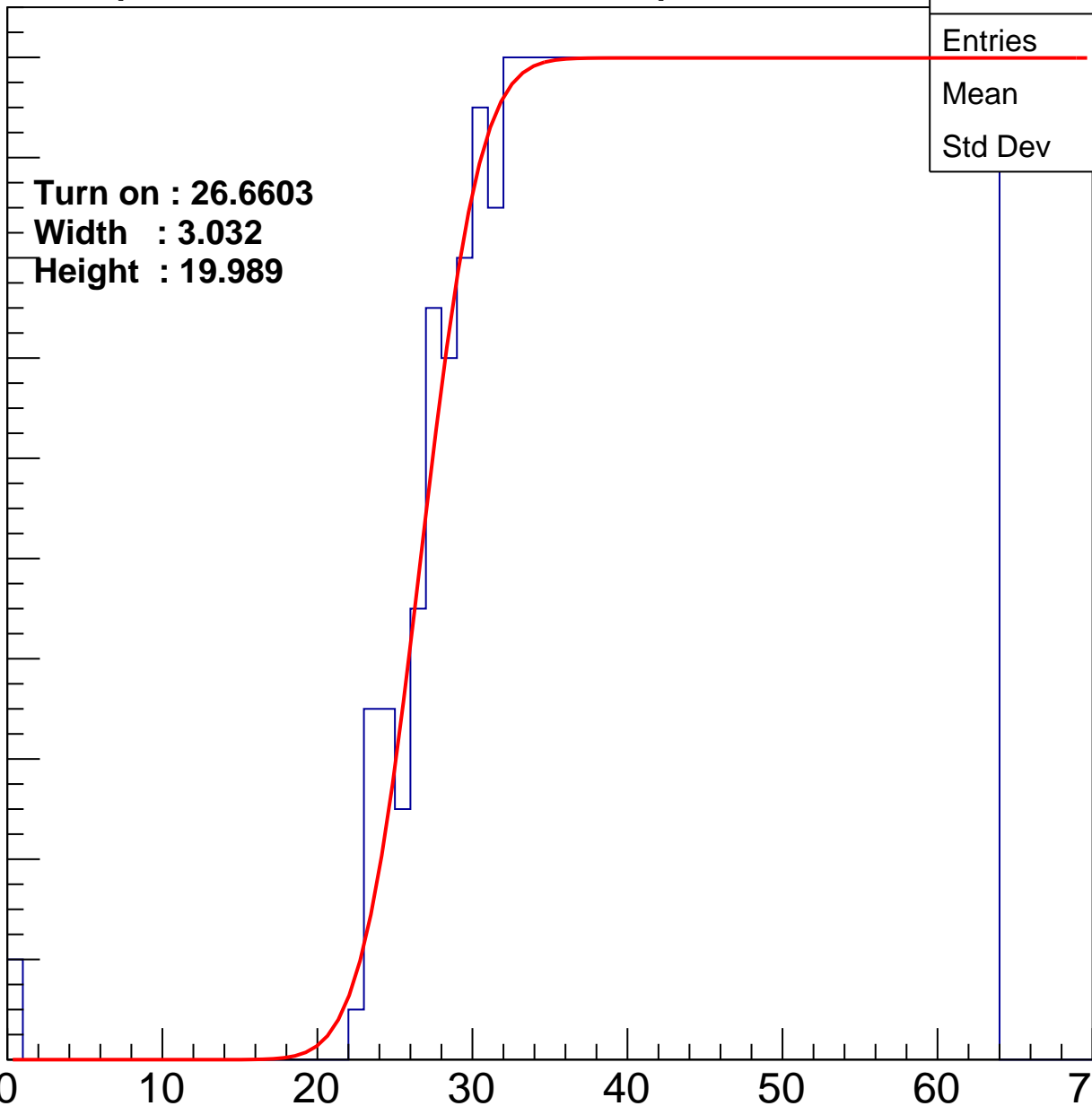
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6603
Width : 3.032
Height : 19.989

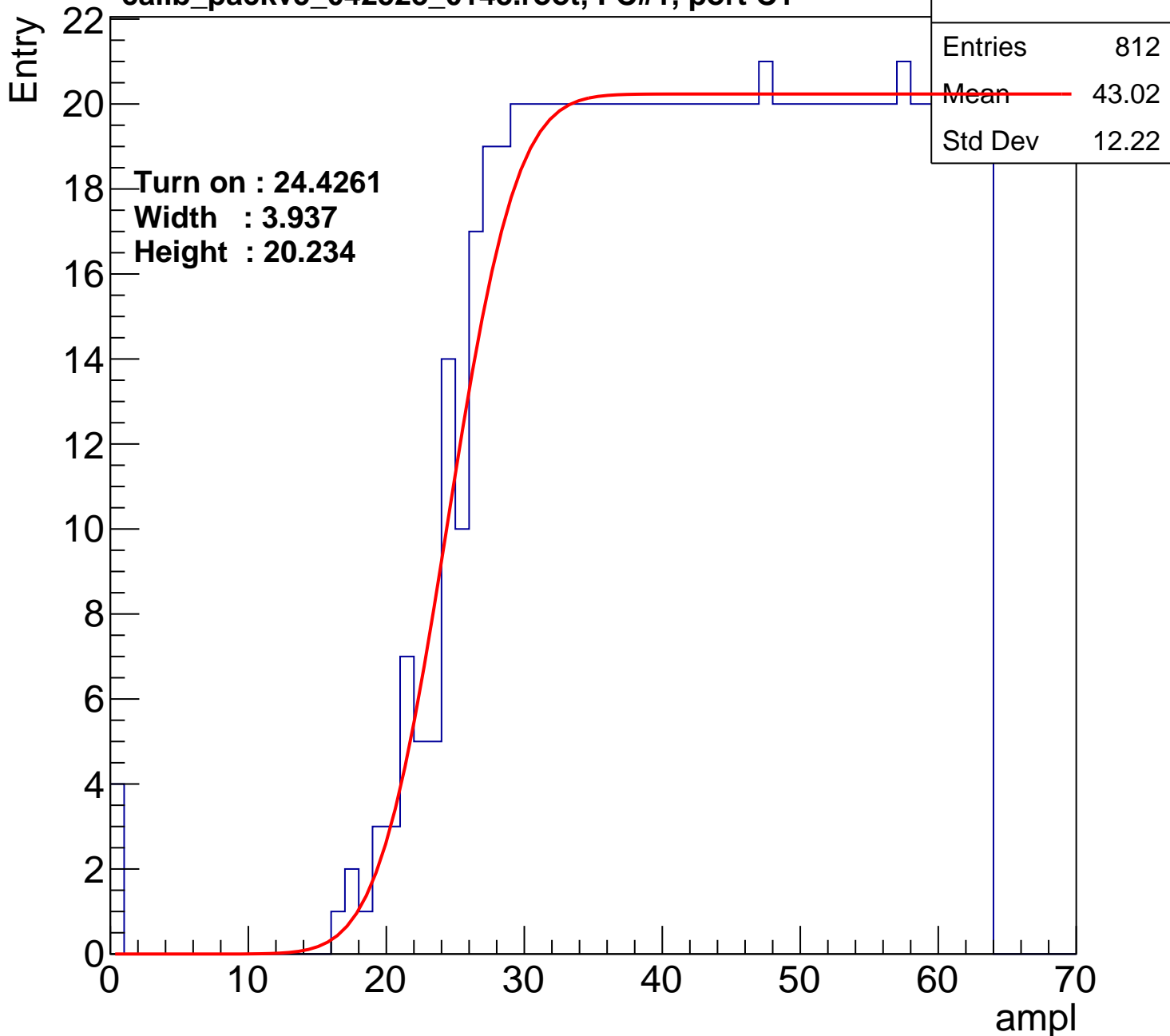
Entries	752
Mean	44.51
Std Dev	11.26

ampl



B0L101S, U21-ch112

calib_packv5_042523_0143.root, FC#1, port C1



B0L101S, U21-ch113

calib_packv5_042523_0143.root, FC#1, port C1

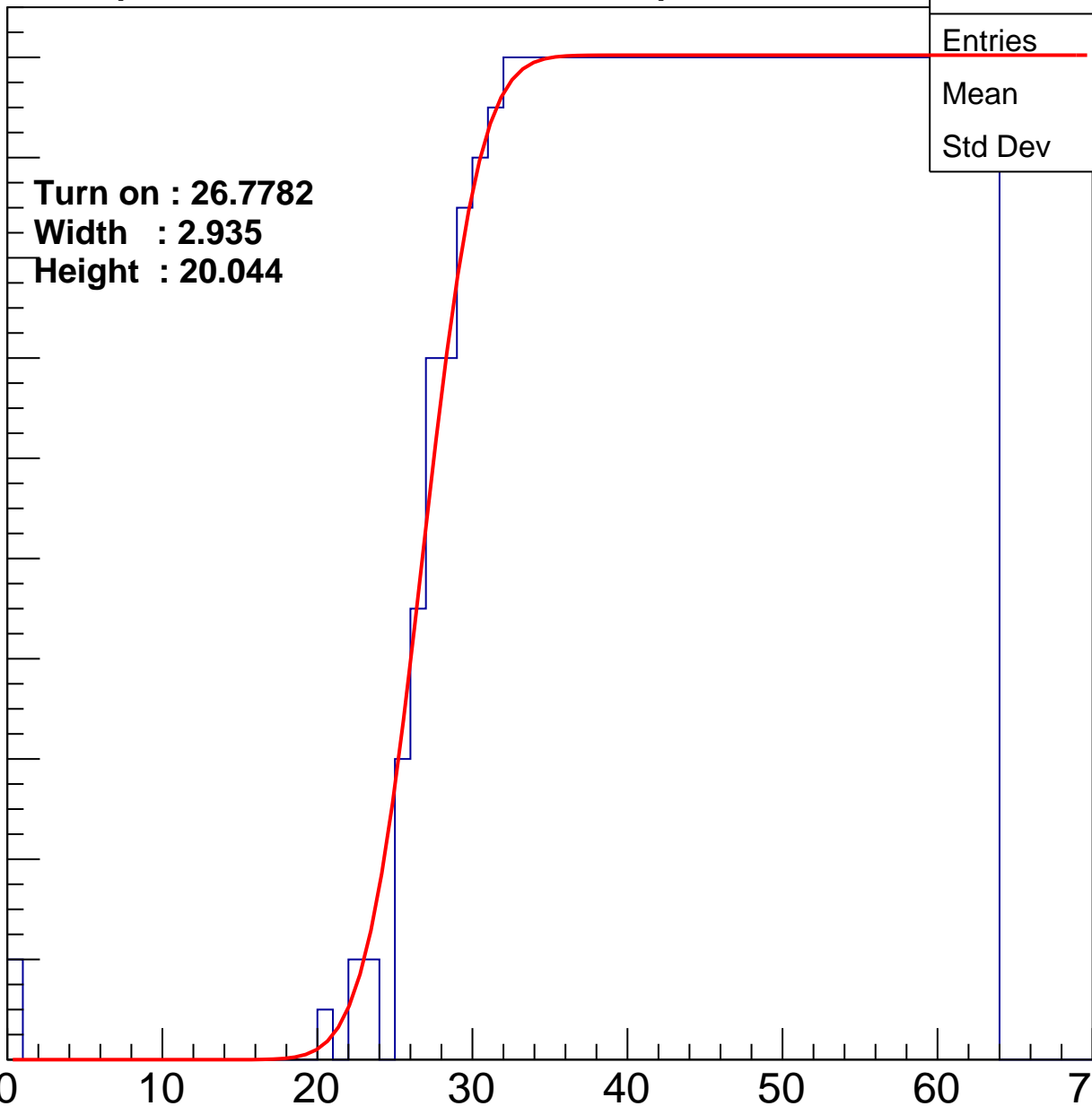
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7782
Width : 2.935
Height : 20.044

Entries	744
Mean	44.74
Std Dev	11.09

ampl



B0L101S, U21-ch114

calib_packv5_042523_0143.root, FC#1, port C1

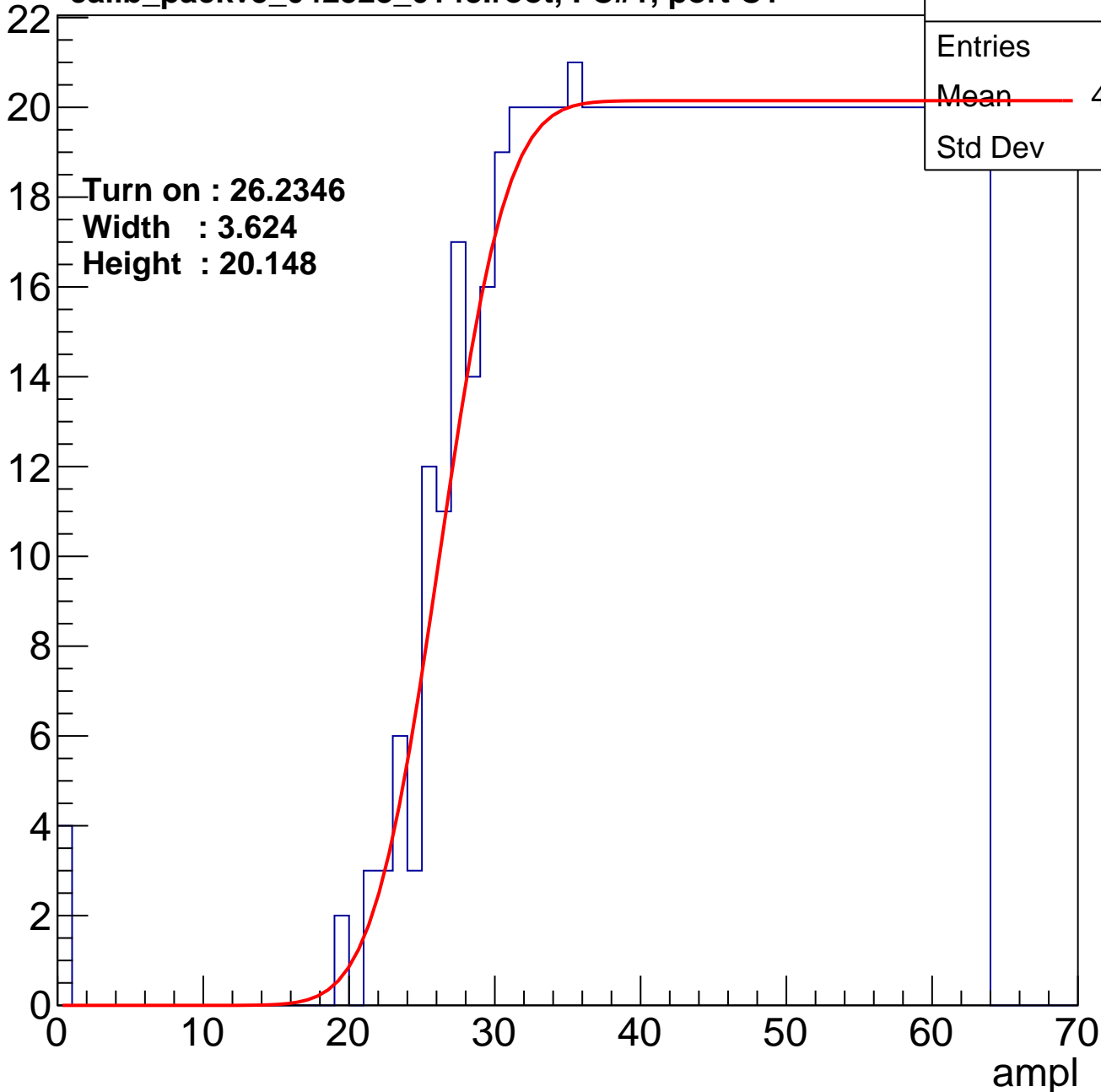
Entries	771
Mean	43.97
Std Dev	11.7

Turn on : 26.2346

Width : 3.624

Height : 20.148

Entry

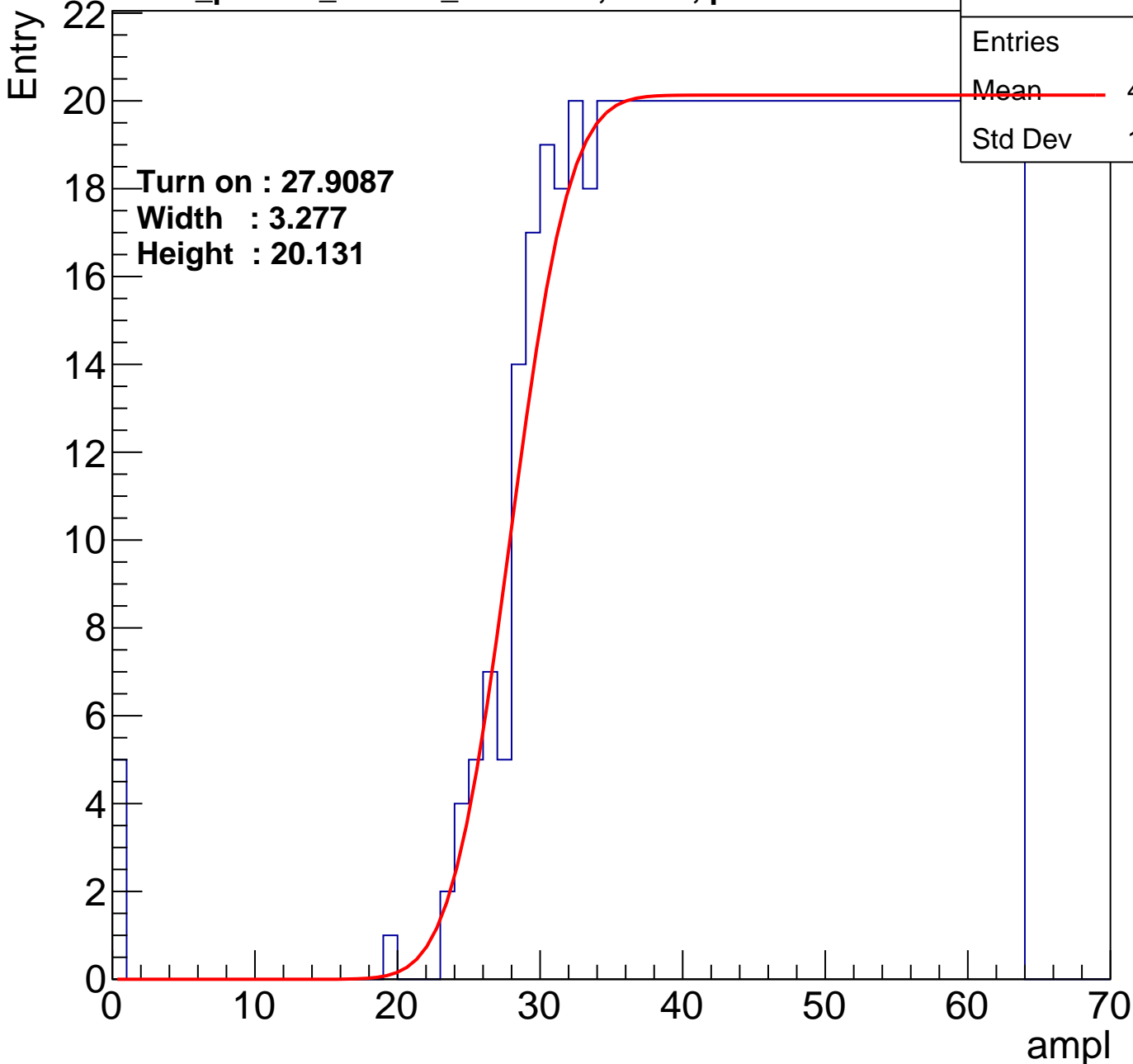


B0L101S, U21-ch115

calib_packv5_042523_0143.root, FC#1, port C1

Entries	736
Mean	44.85
Std Dev	11.32

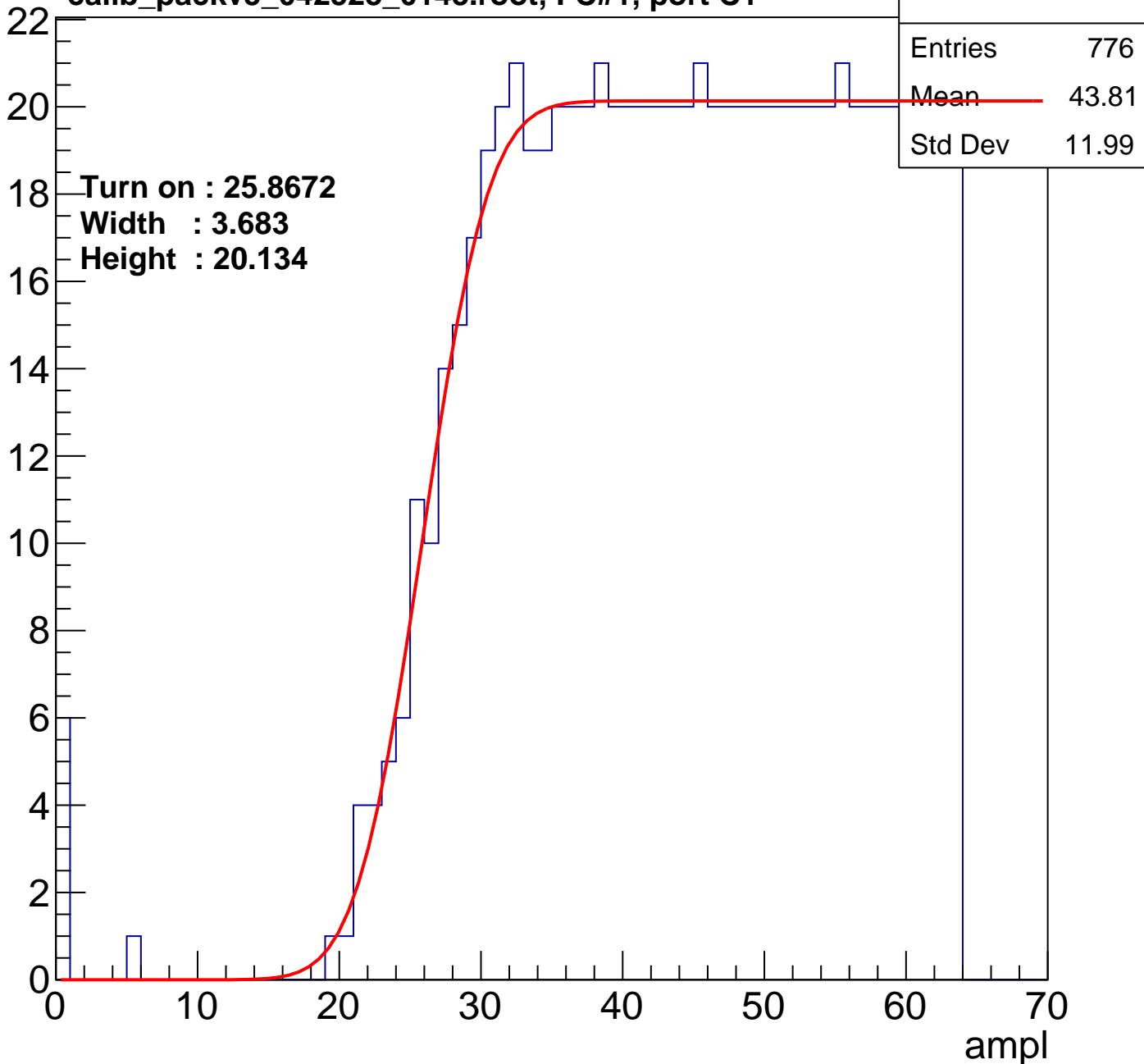
Turn on : 27.9087
Width : 3.277
Height : 20.131



B0L101S, U21-ch116

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch117

calib_packv5_042523_0143.root, FC#1, port C1

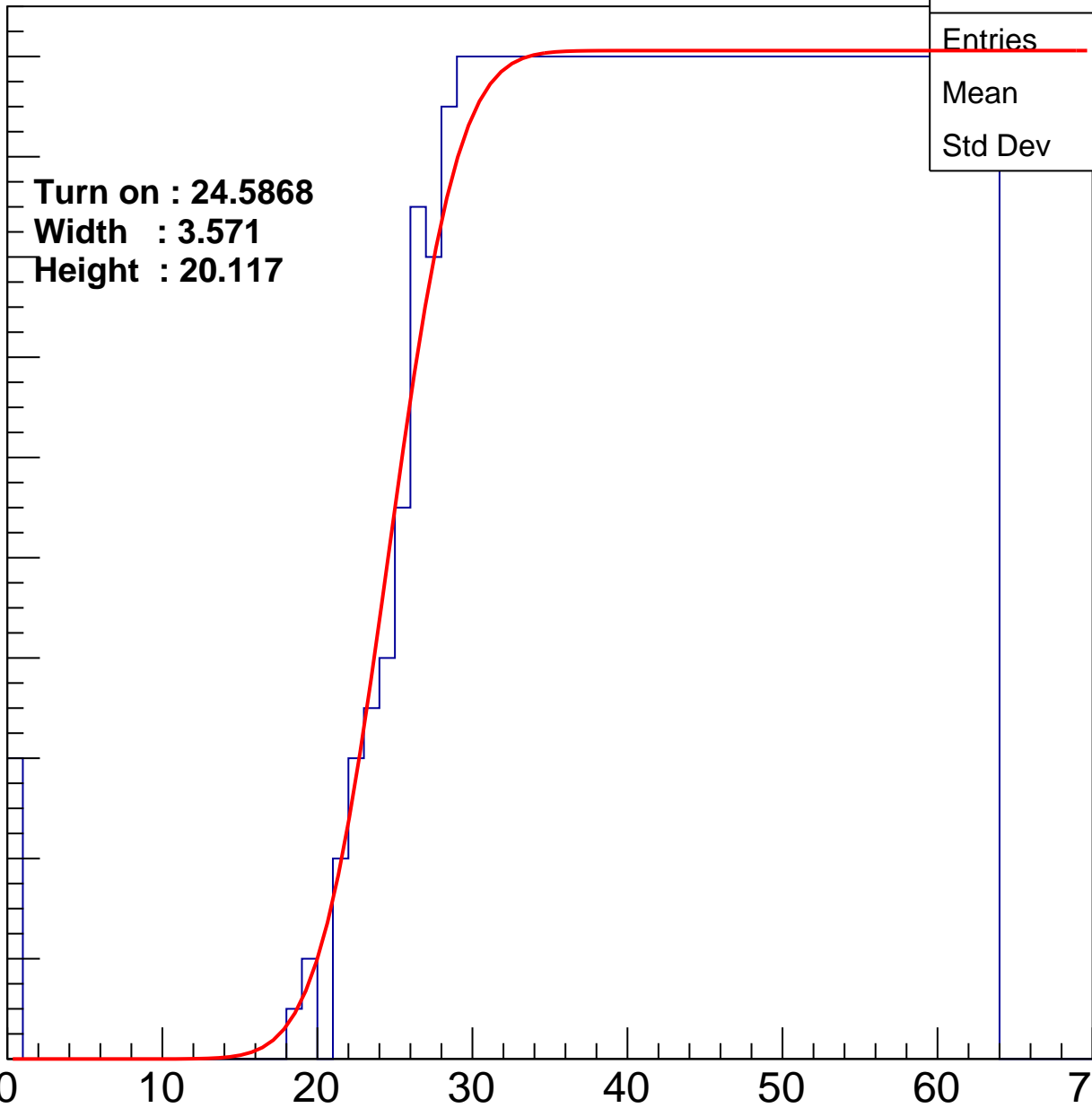
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.5868
Width : 3.571
Height : 20.117

Entries	797
Mean	43.29
Std Dev	12.15

ampl



B0L101S, U21-ch118

calib_packv5_042523_0143.root, FC#1, port C1

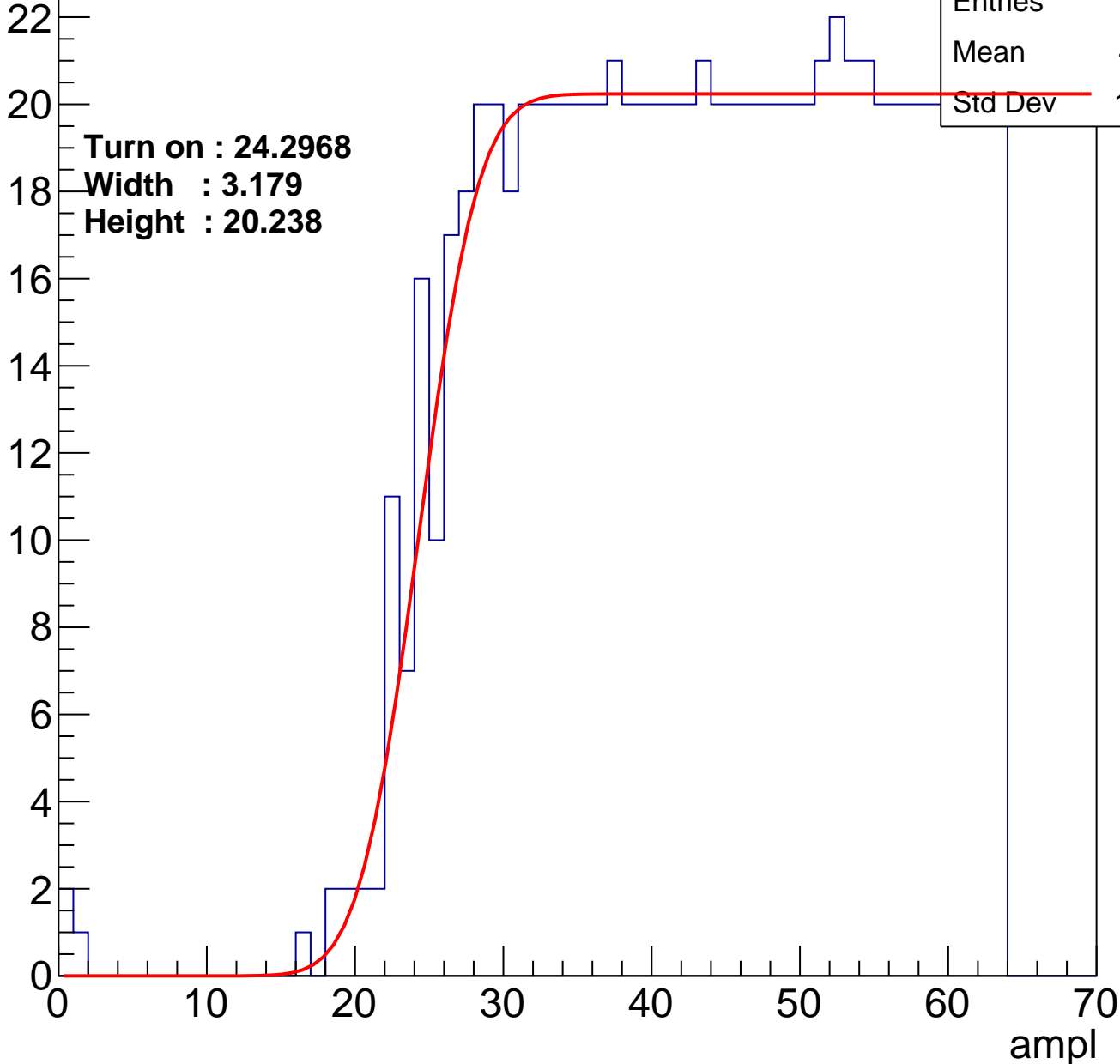
Entries	816
Mean	43.11
Std Dev	12.08

Turn on : 24.2968

Width : 3.179

Height : 20.238

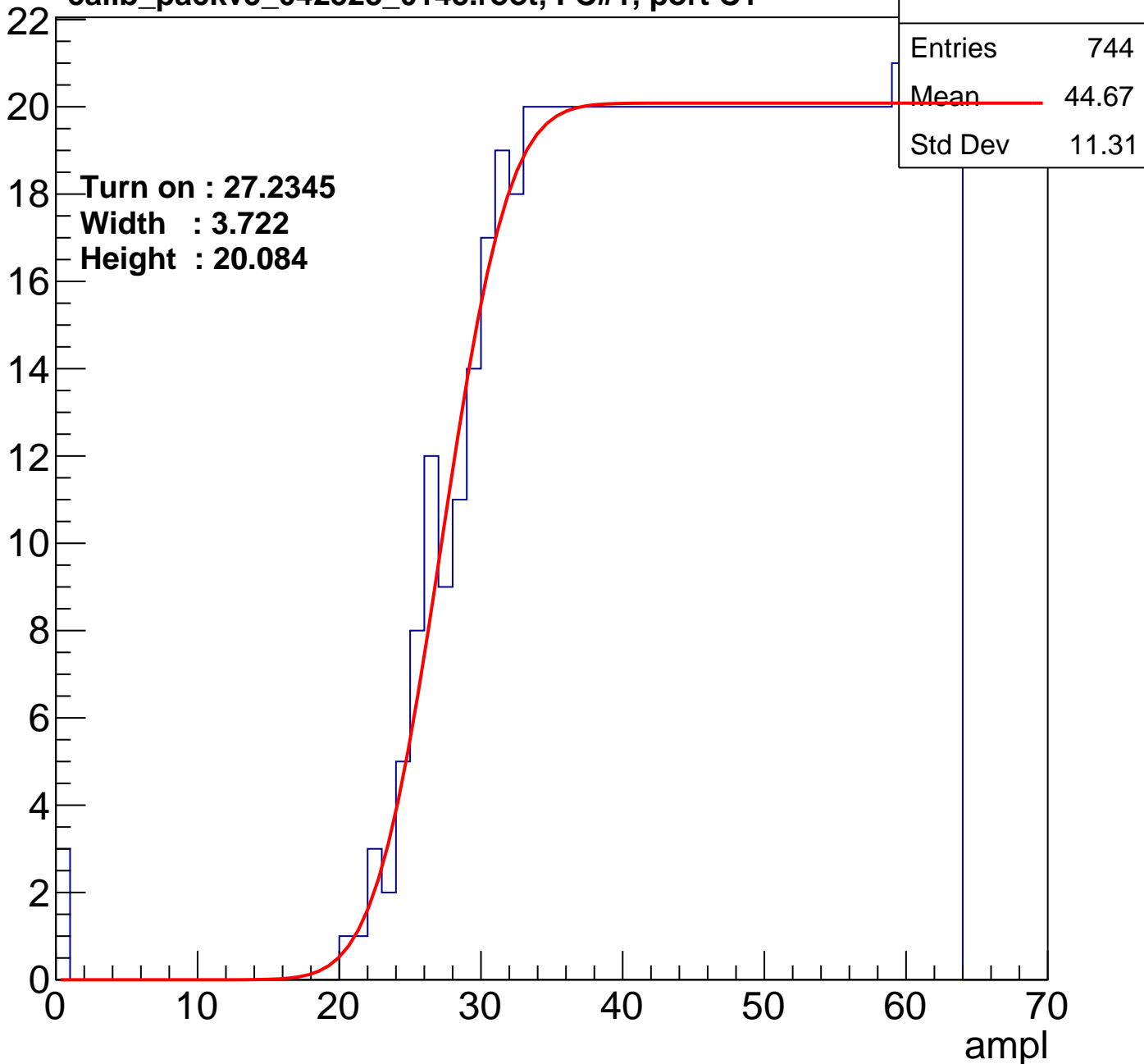
Entry



B0L101S, U21-ch119

calib_packv5_042523_0143.root, FC#1, port C1

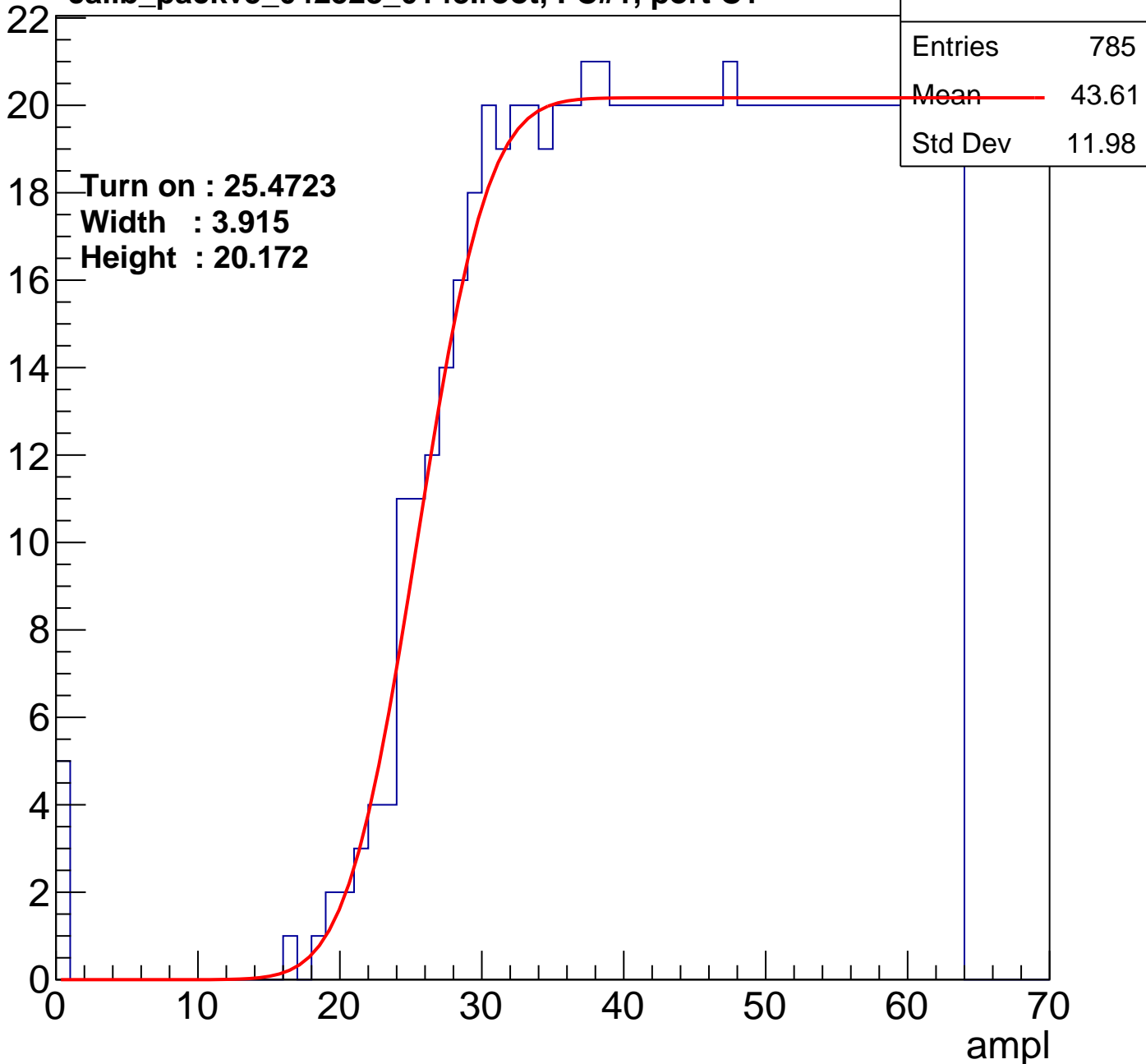
Entry



B0L101S, U21-ch120

calib_packv5_042523_0143.root, FC#1, port C1

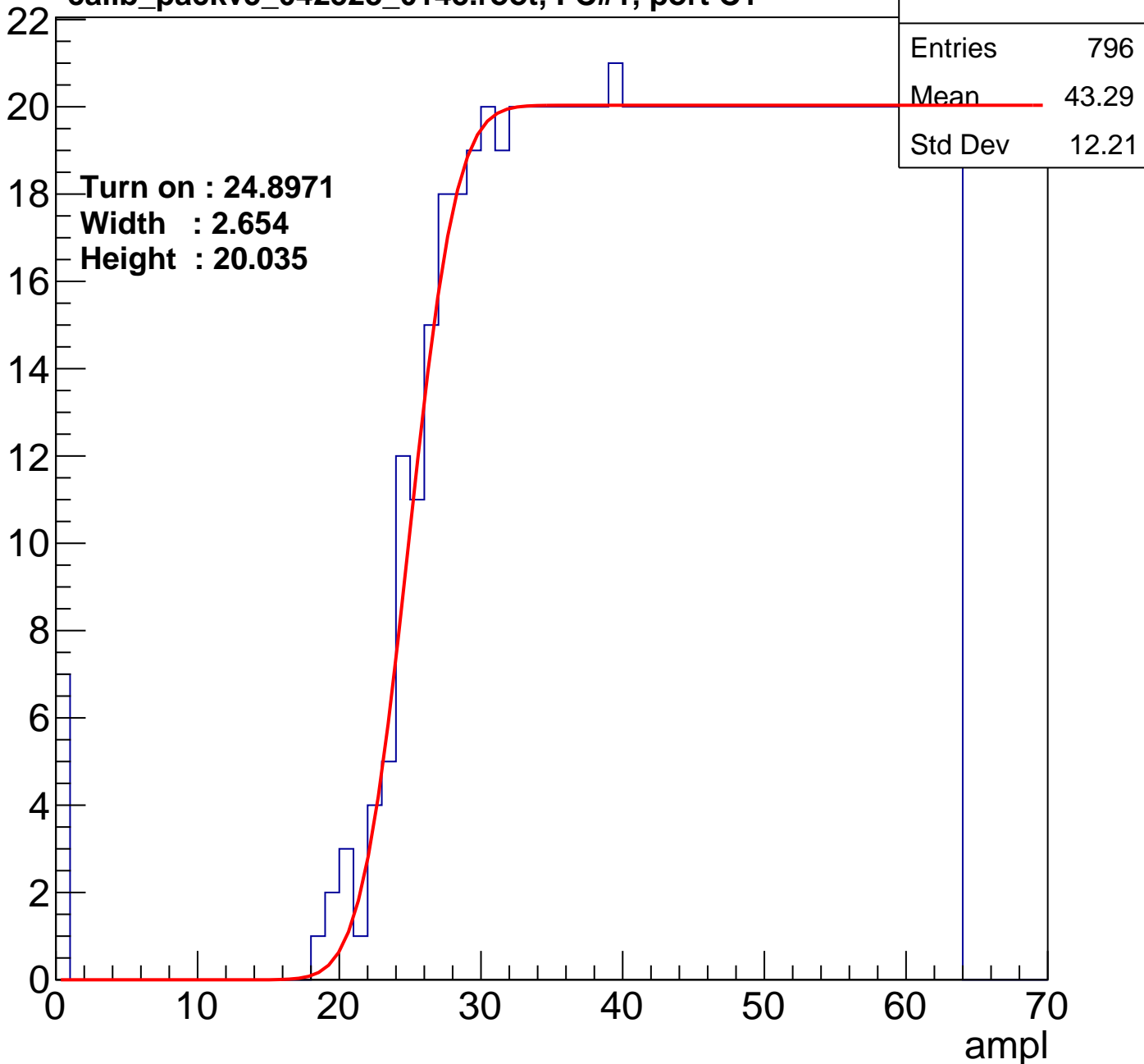
Entry



B0L101S, U21-ch121

calib_packv5_042523_0143.root, FC#1, port C1

Entry

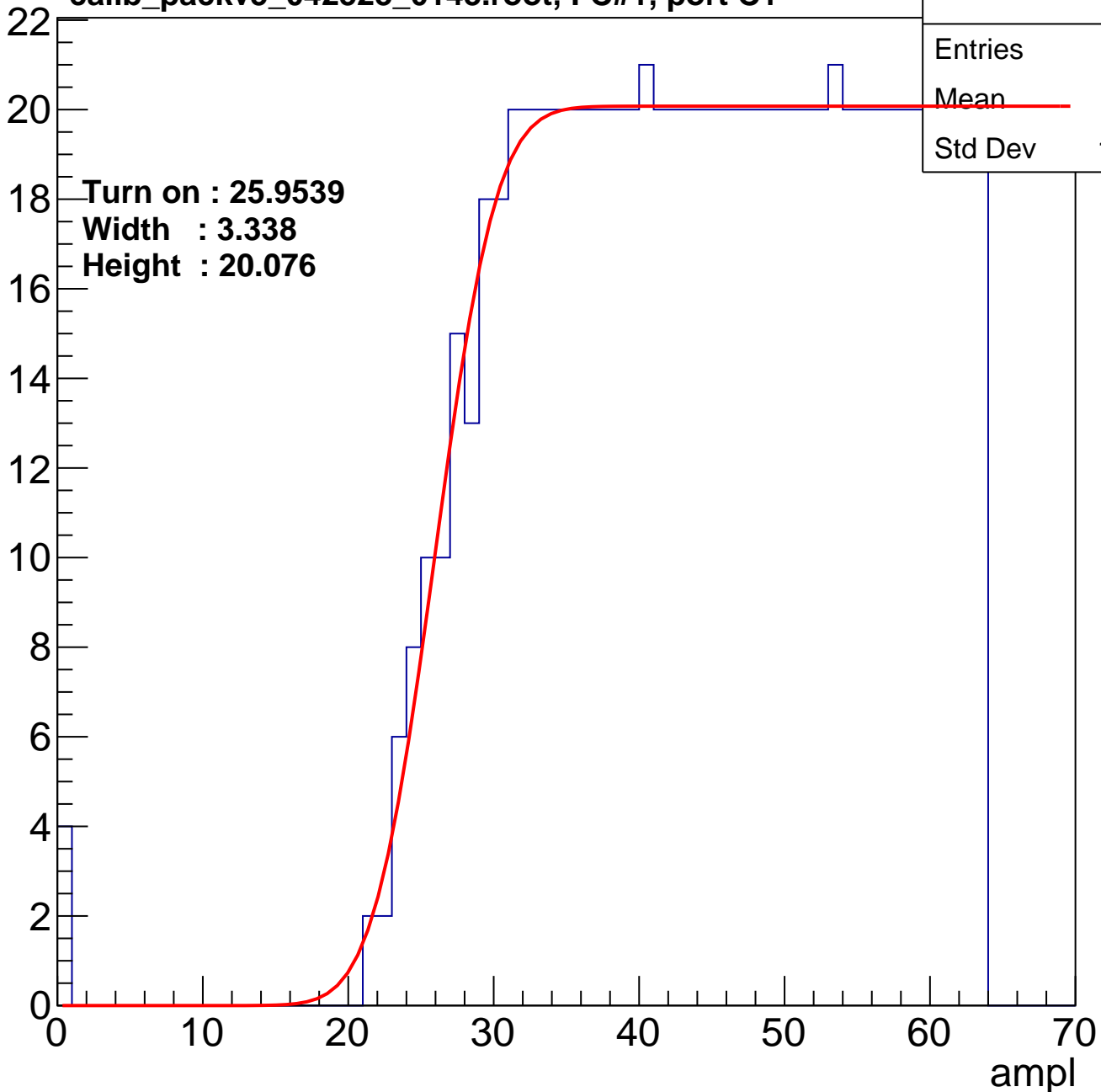


B0L101S, U21-ch122

calib_packv5_042523_0143.root, FC#1, port C1

Entries	768
Mean	44.1
Std Dev	11.61

Entry



B0L101S, U21-ch123

calib_packv5_042523_0143.root, FC#1, port C1

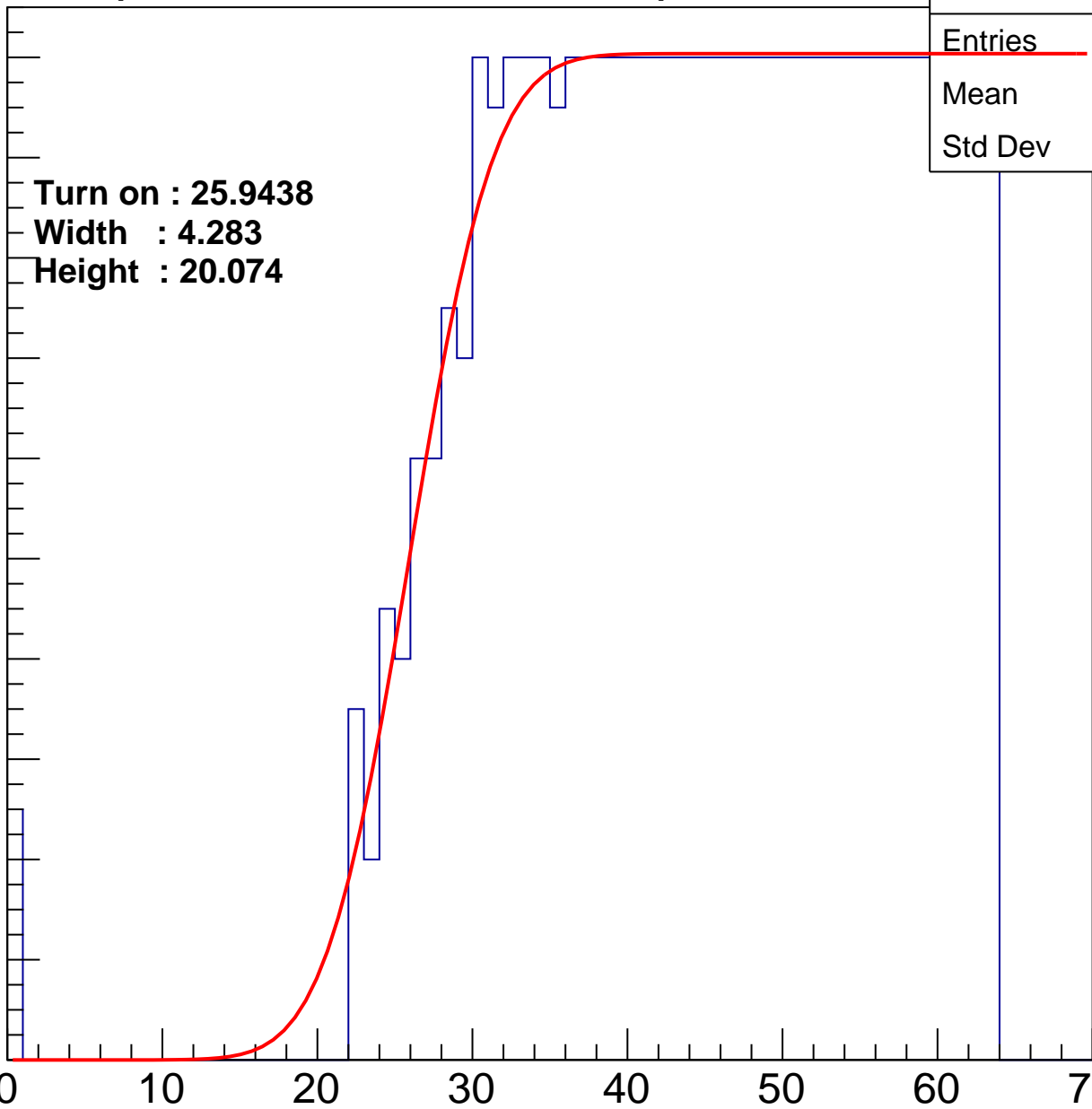
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9438
Width : 4.283
Height : 20.074

Entries	764
Mean	44.08
Std Dev	11.73

ampl

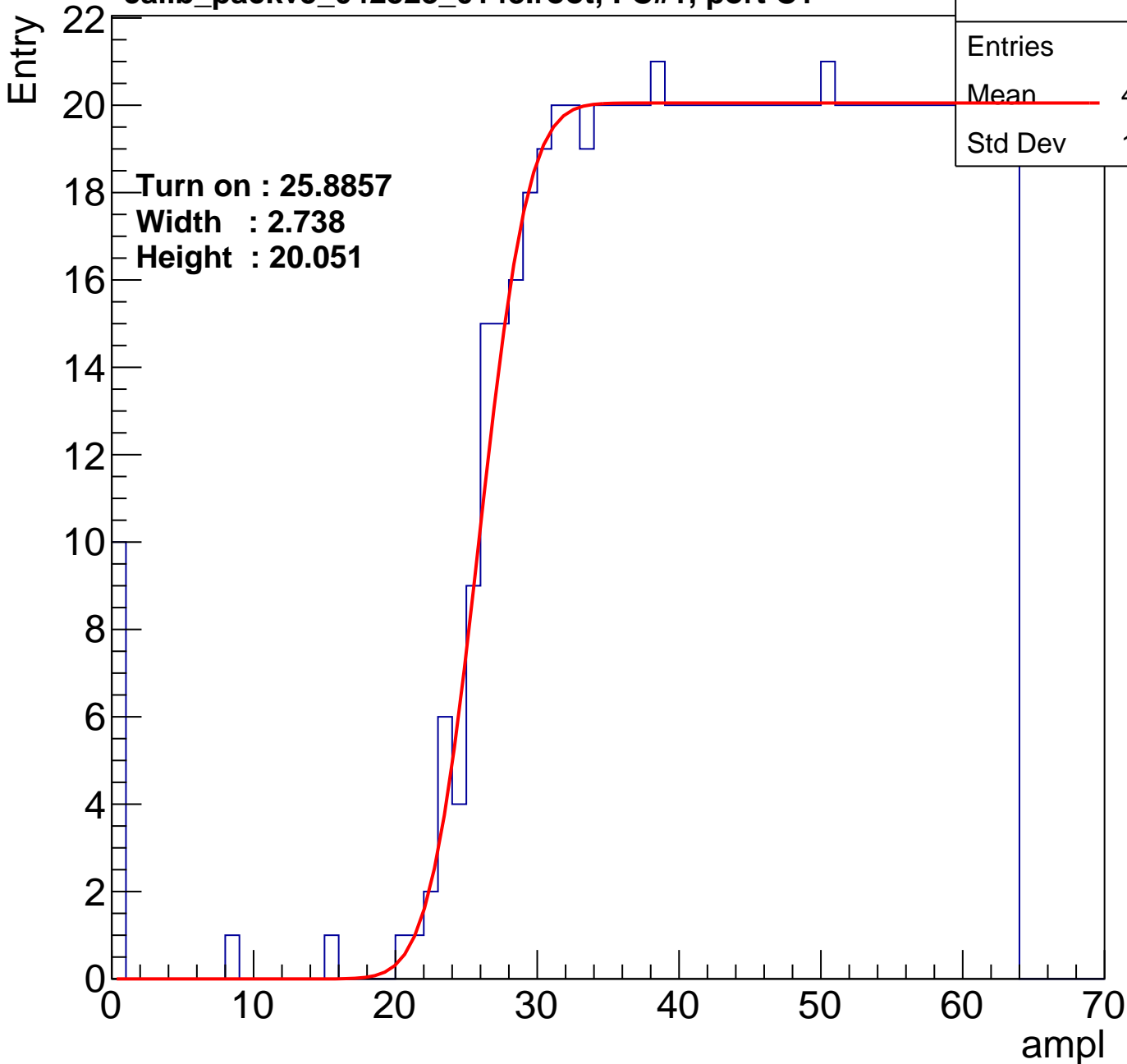


B0L101S, U21-ch124

calib_packv5_042523_0143.root, FC#1, port C1

Entries	779
Mean	43.62
Std Dev	12.29

Turn on : 25.8857
Width : 2.738
Height : 20.051



B0L101S, U21-ch125

calib_packv5_042523_0143.root, FC#1, port C1

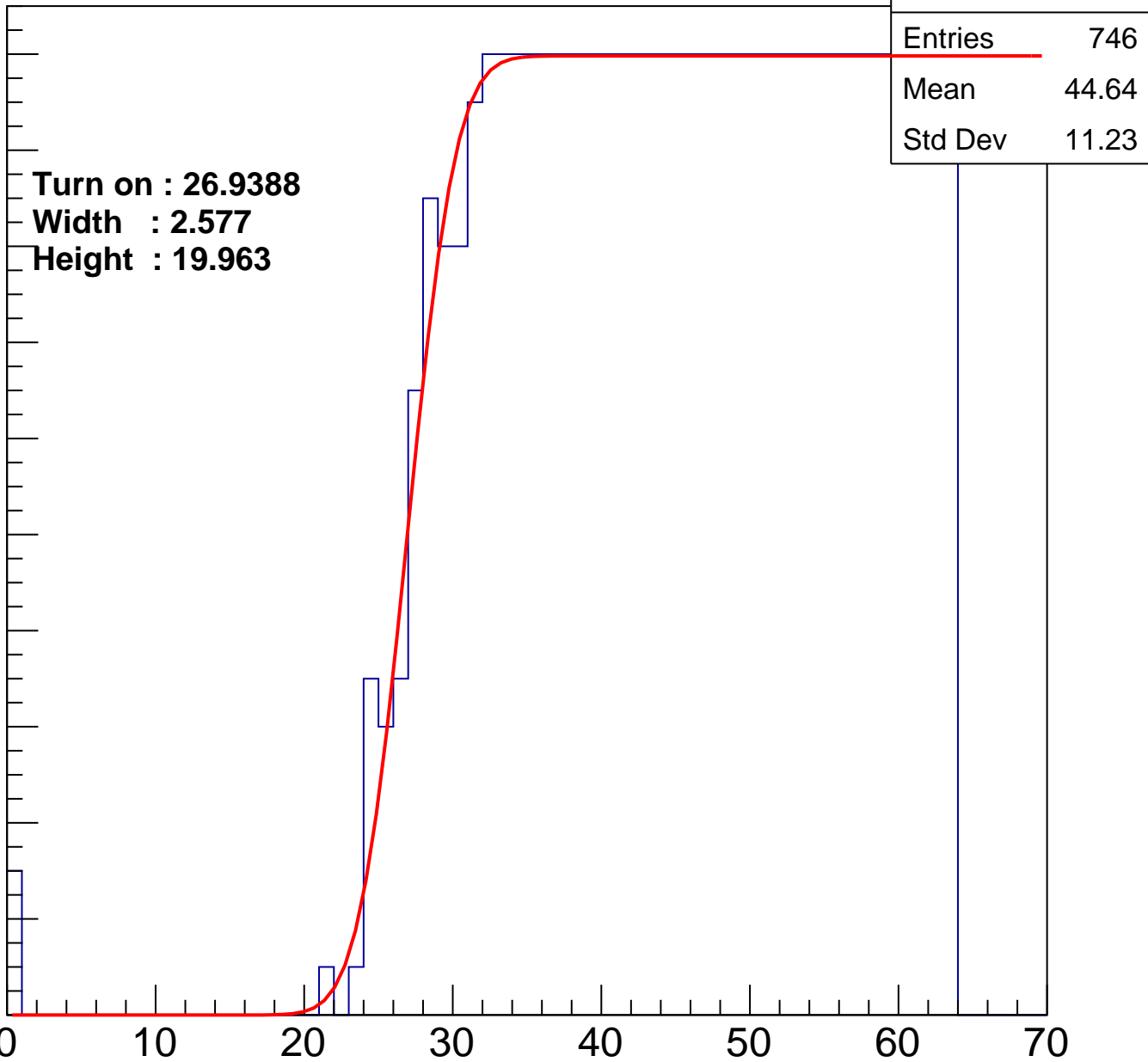
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9388
Width : 2.577
Height : 19.963

Entries	746
Mean	44.64
Std Dev	11.23

ampl

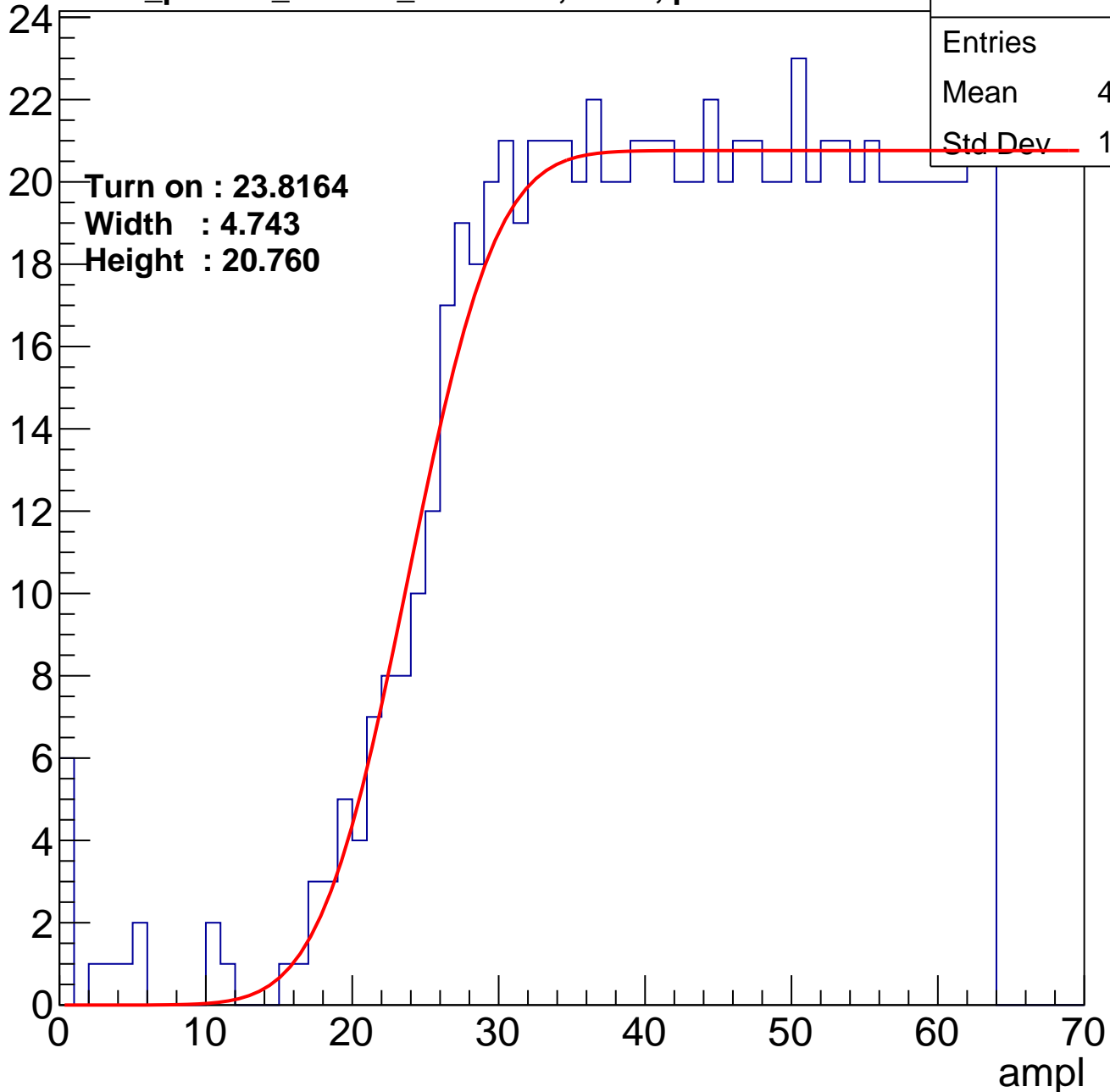


B0L101S, U21-ch126

calib_packv5_042523_0143.root, FC#1, port C1

Entries	851
Mean	42.35
Std Dev	12.98

Entry



B0L101S, U21-ch127

calib_packv5_042523_0143.root, FC#1, port C1

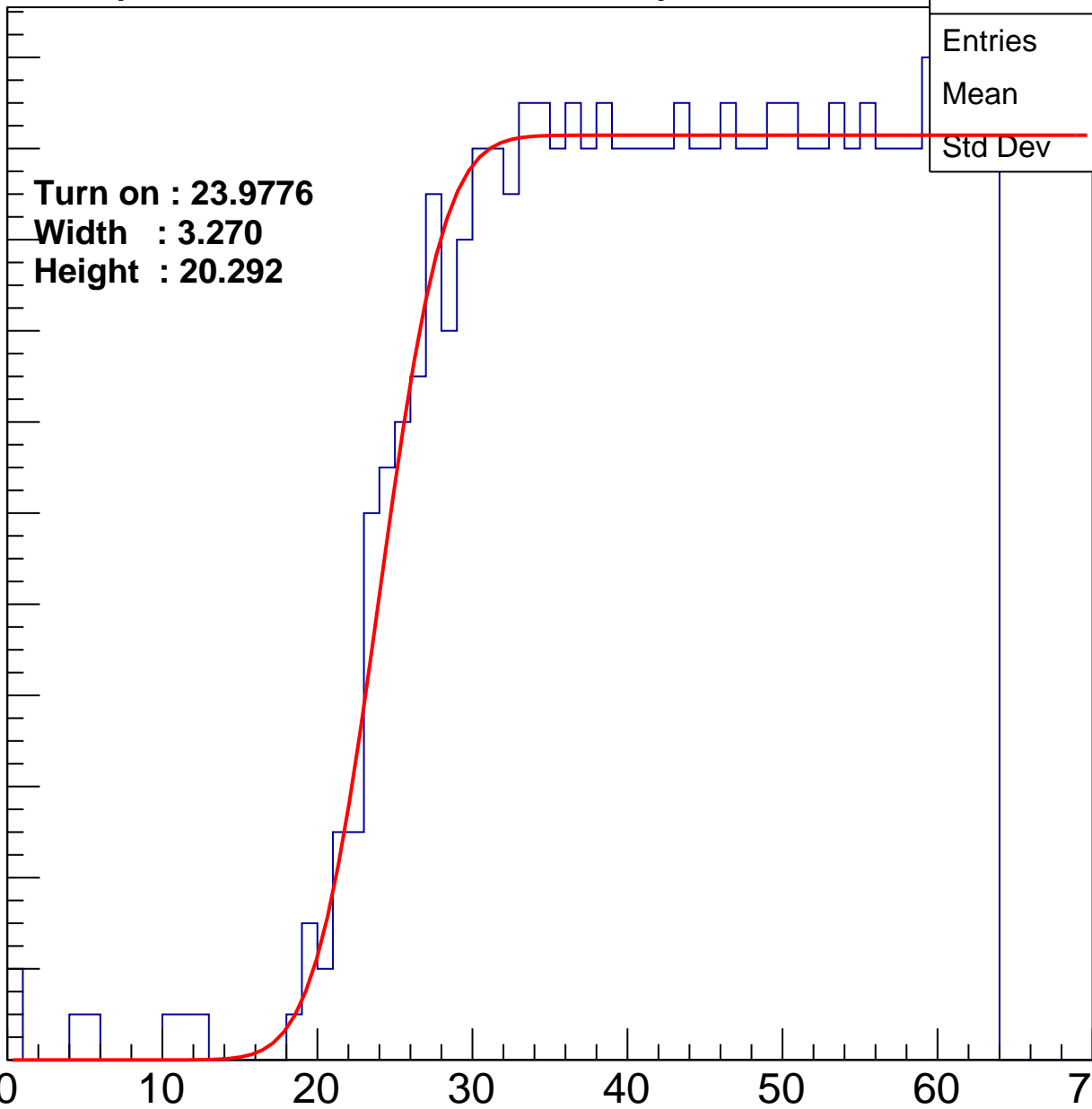
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 23.9776
Width : 3.270
Height : 20.292

Entries	822
Mean	43.05
Std Dev	12.25

ampl



B0L101S, U21-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 23.9776
Width : 3.270
Height : 20.292

Entries	822
Mean	43.05
Std Dev	12.25

ampl

