

B0L102S, U15-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.63
Std Dev	12.02

Turn on : 26.0527

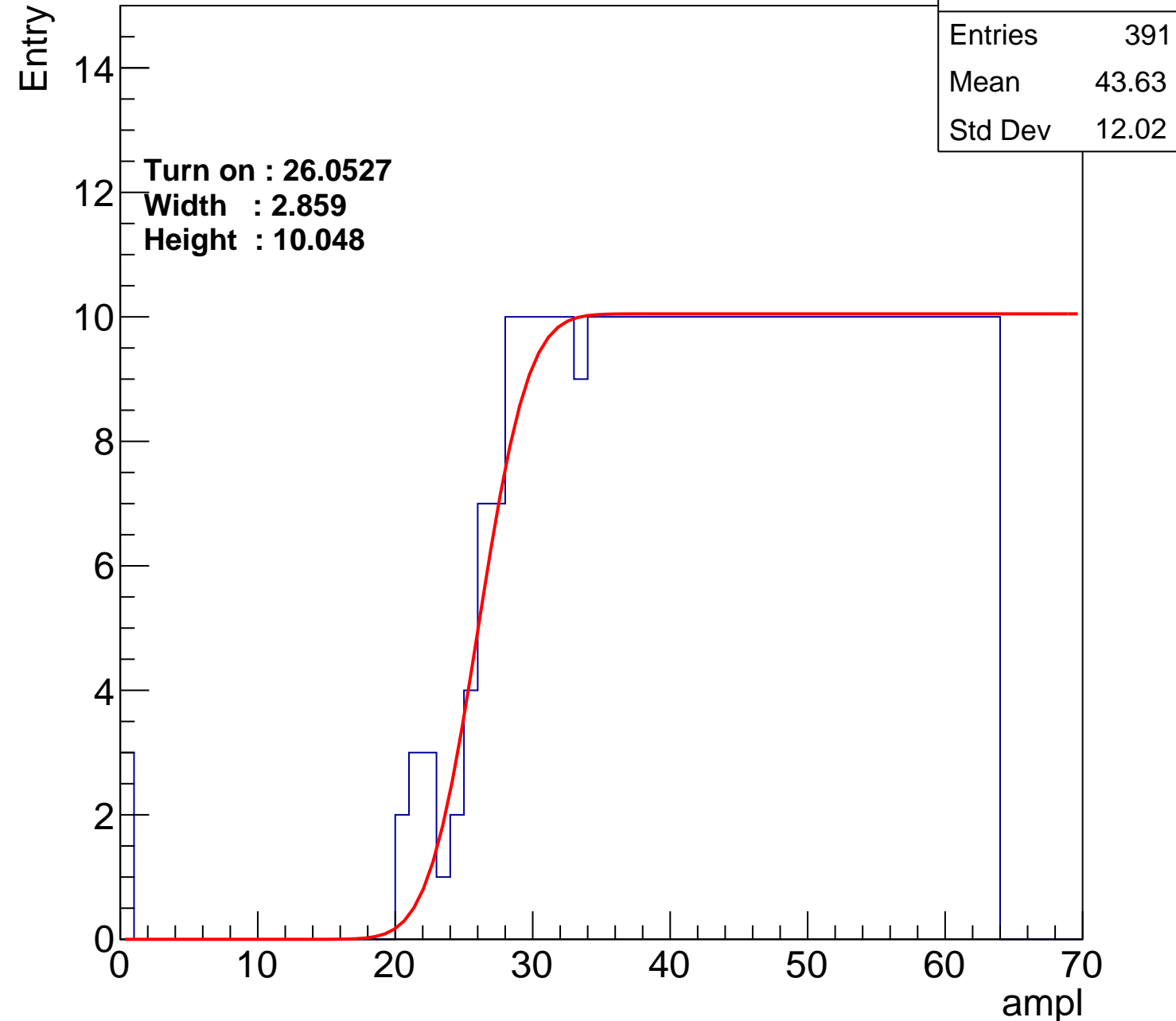
Width : 2.859

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.97
Std Dev	11.54

Turn on : 25.7388

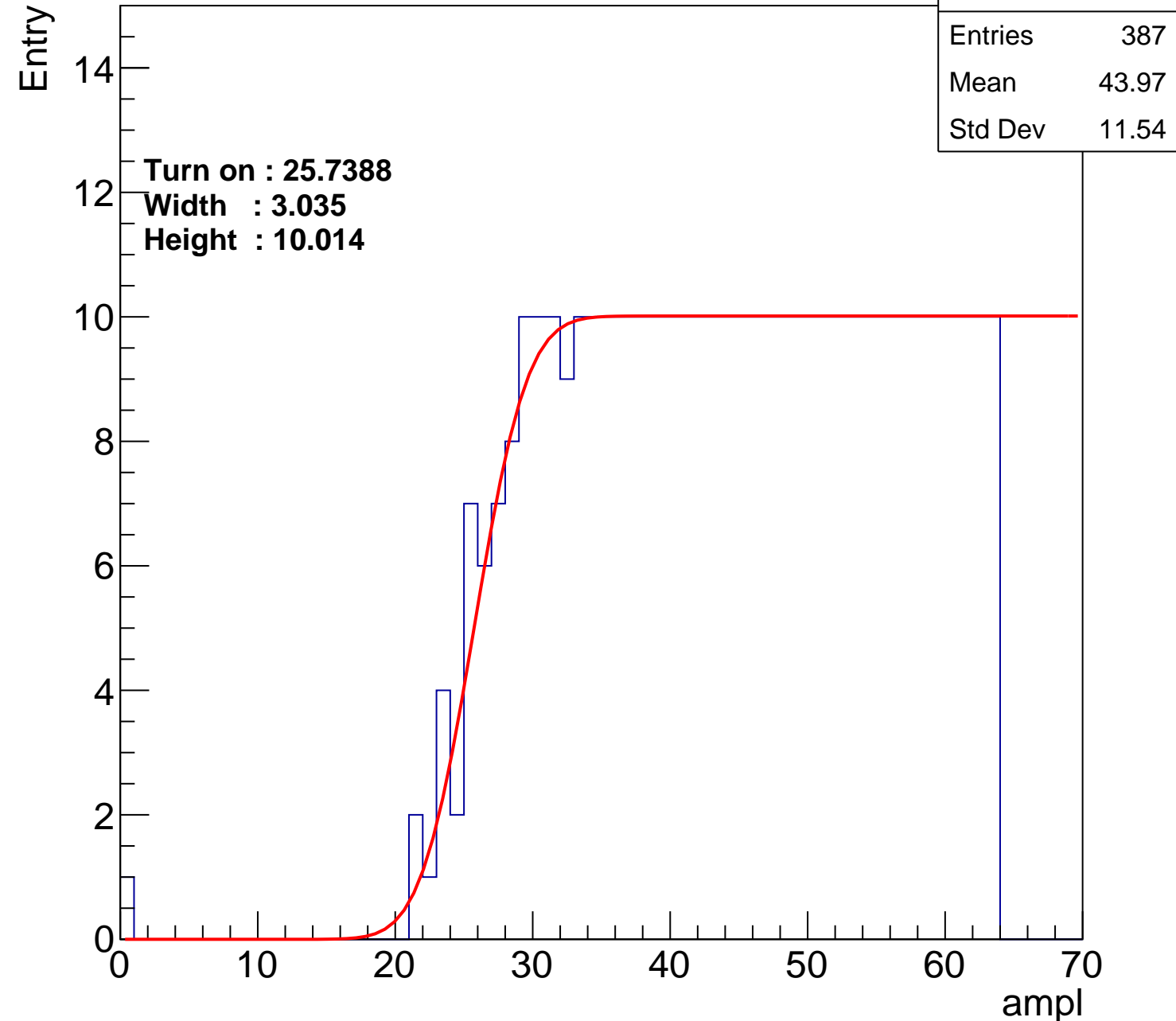
Width : 3.035

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.22
Std Dev	12.34

Turn on : 24.6428

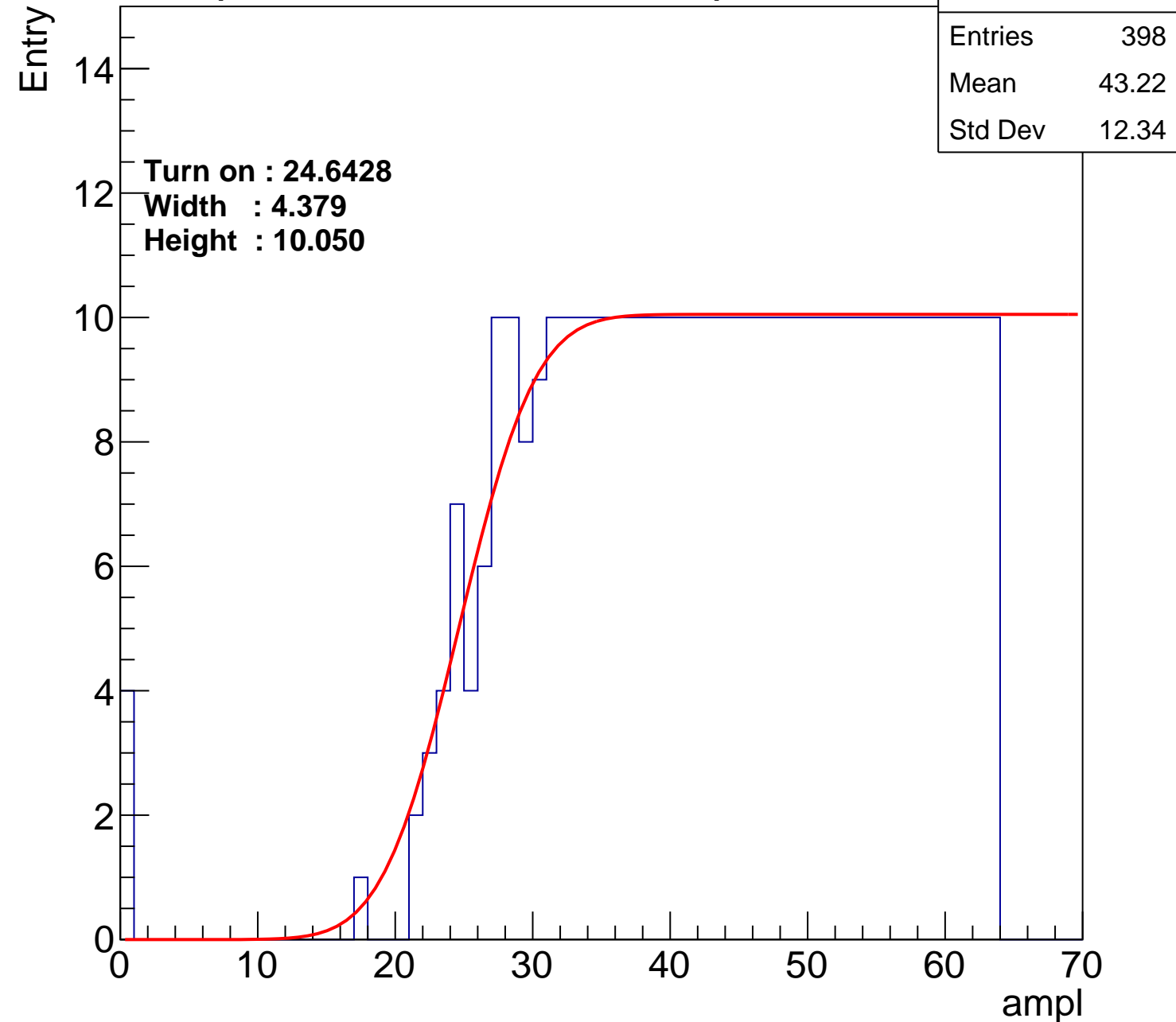
Width : 4.379

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.81
Std Dev	11.55

Turn on : 27.6027

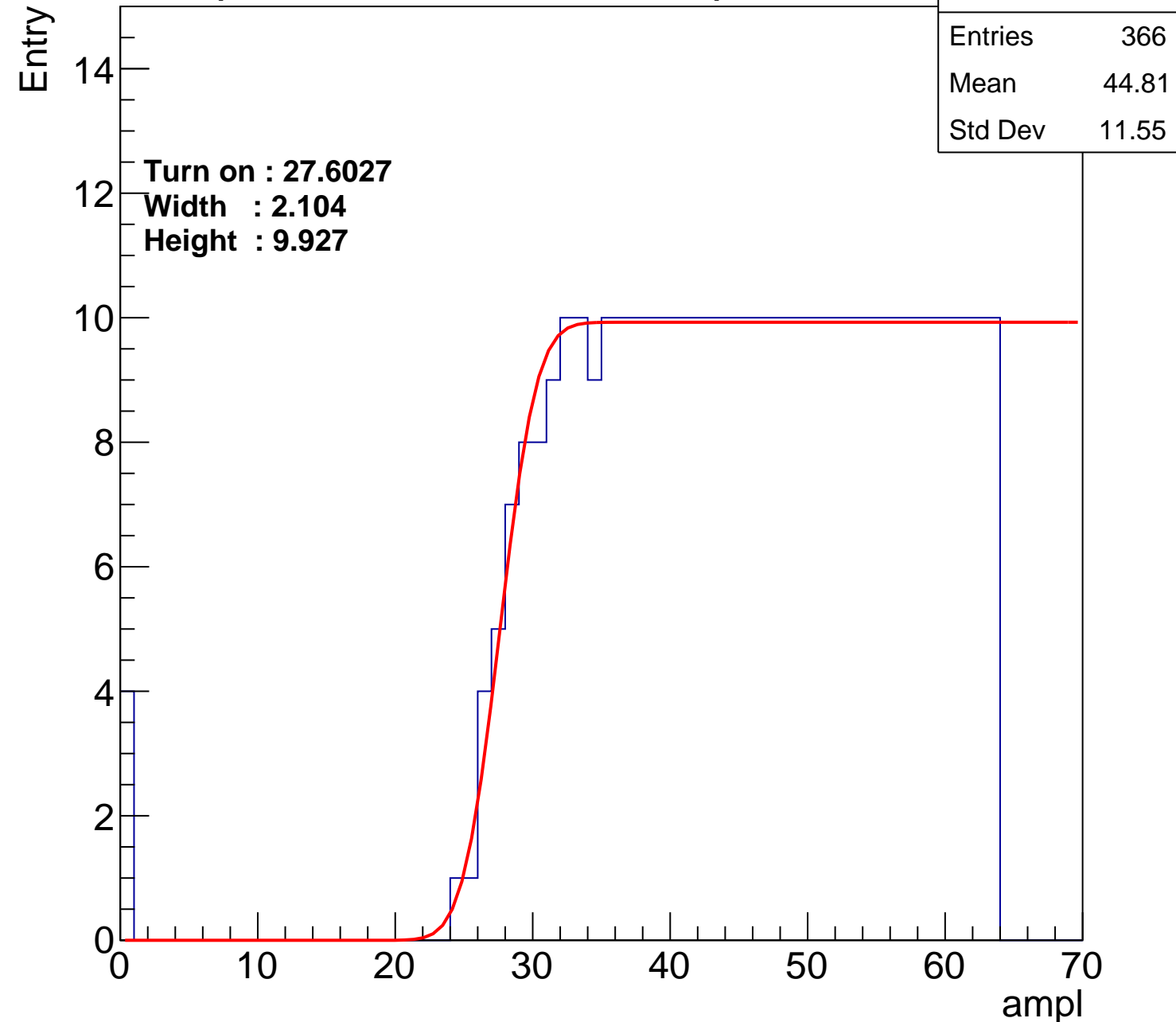
Width : 2.104

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch4

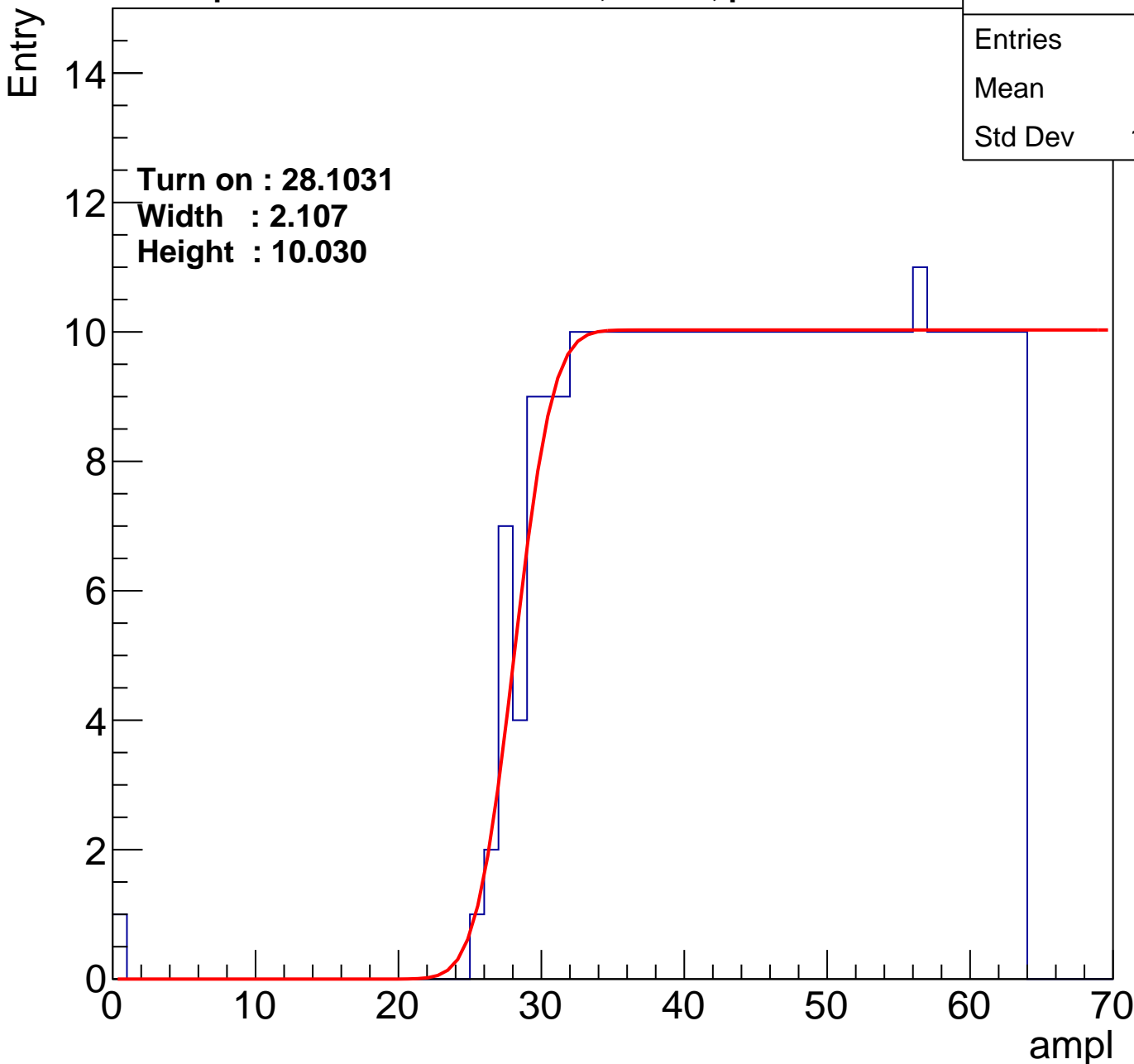
calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.3
Std Dev	10.76

Turn on : 28.1031

Width : 2.107

Height : 10.030



B0L102S, U15-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.07
Std Dev	11.14

Turn on : 28.8107

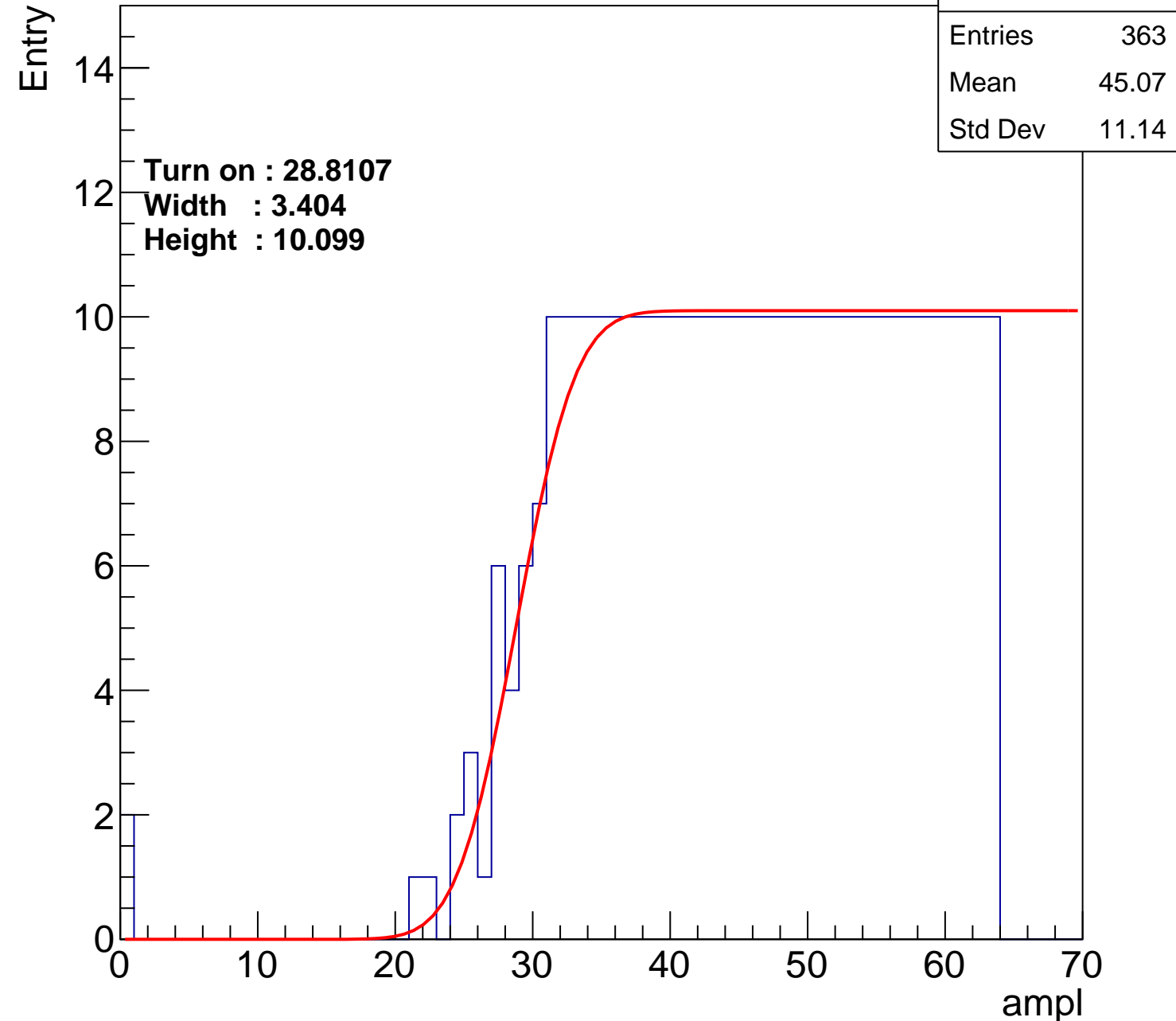
Width : 3.404

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch6

calib_packv5_042523_0143.root, FC#12, port B1

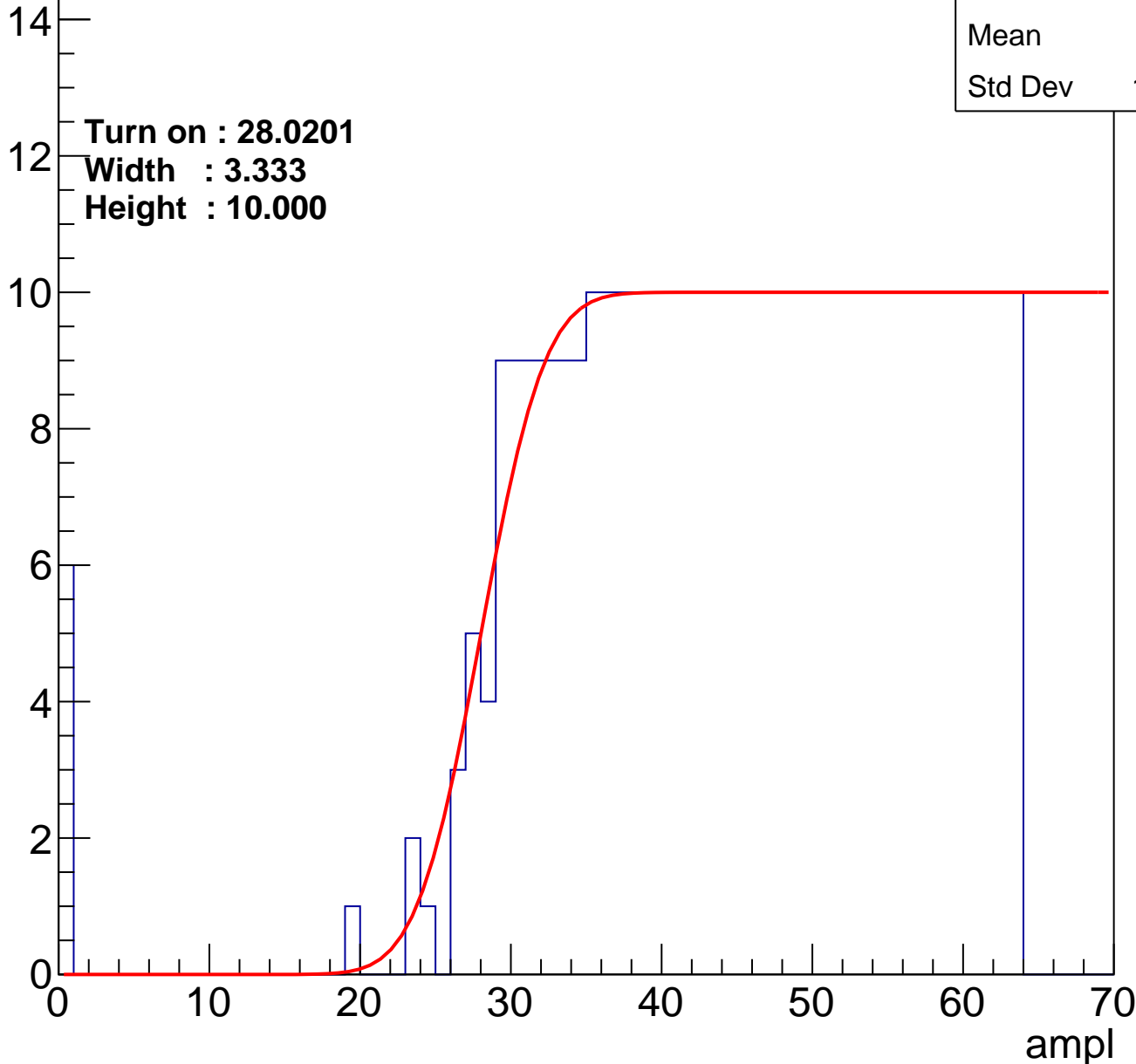
Entries	366
Mean	44.6
Std Dev	12.03

Turn on : 28.0201

Width : 3.333

Height : 10.000

Entry



B0L102S, U15-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.42
Std Dev	11.57

Turn on : 26.8522

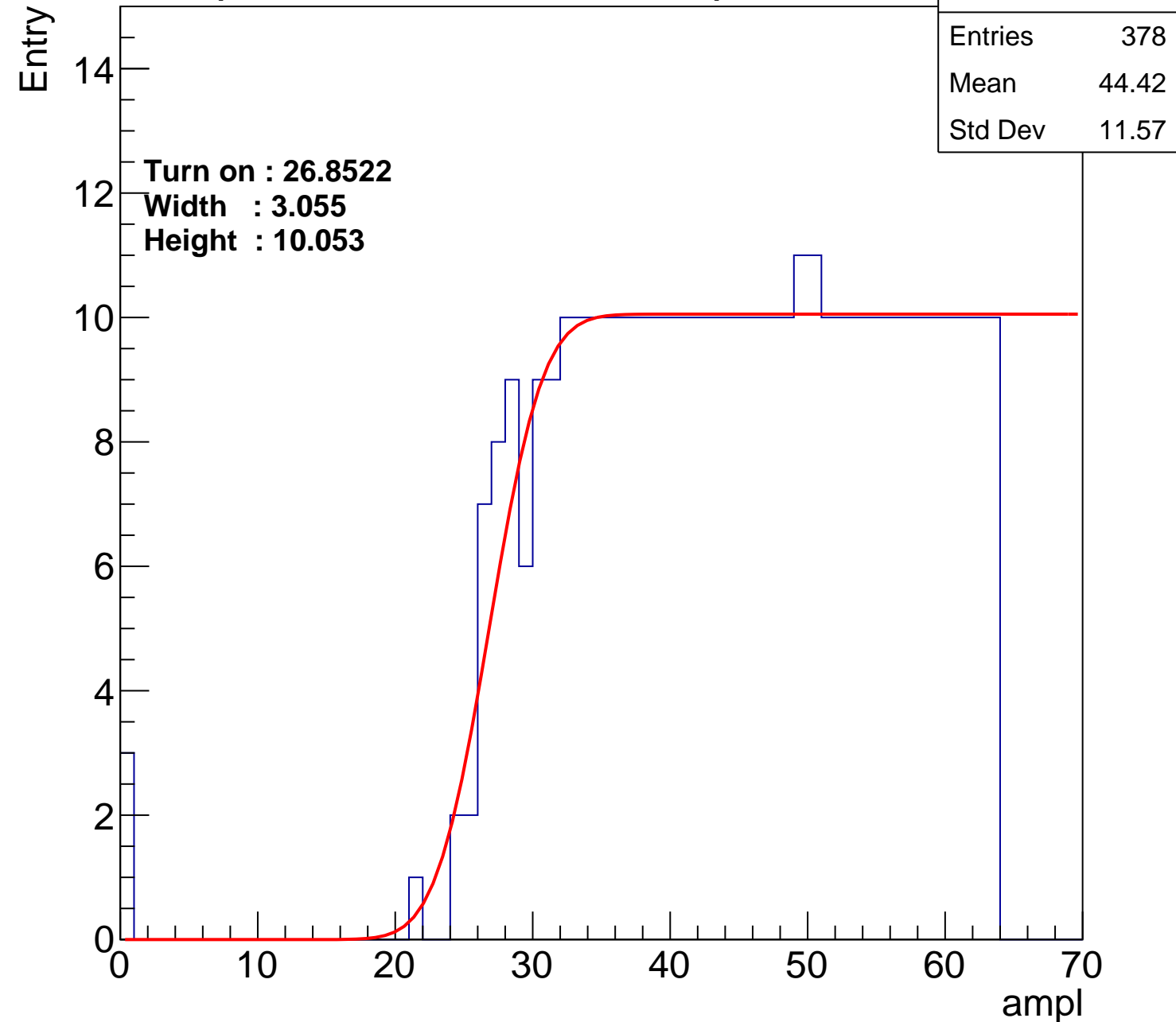
Width : 3.055

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 25.2591

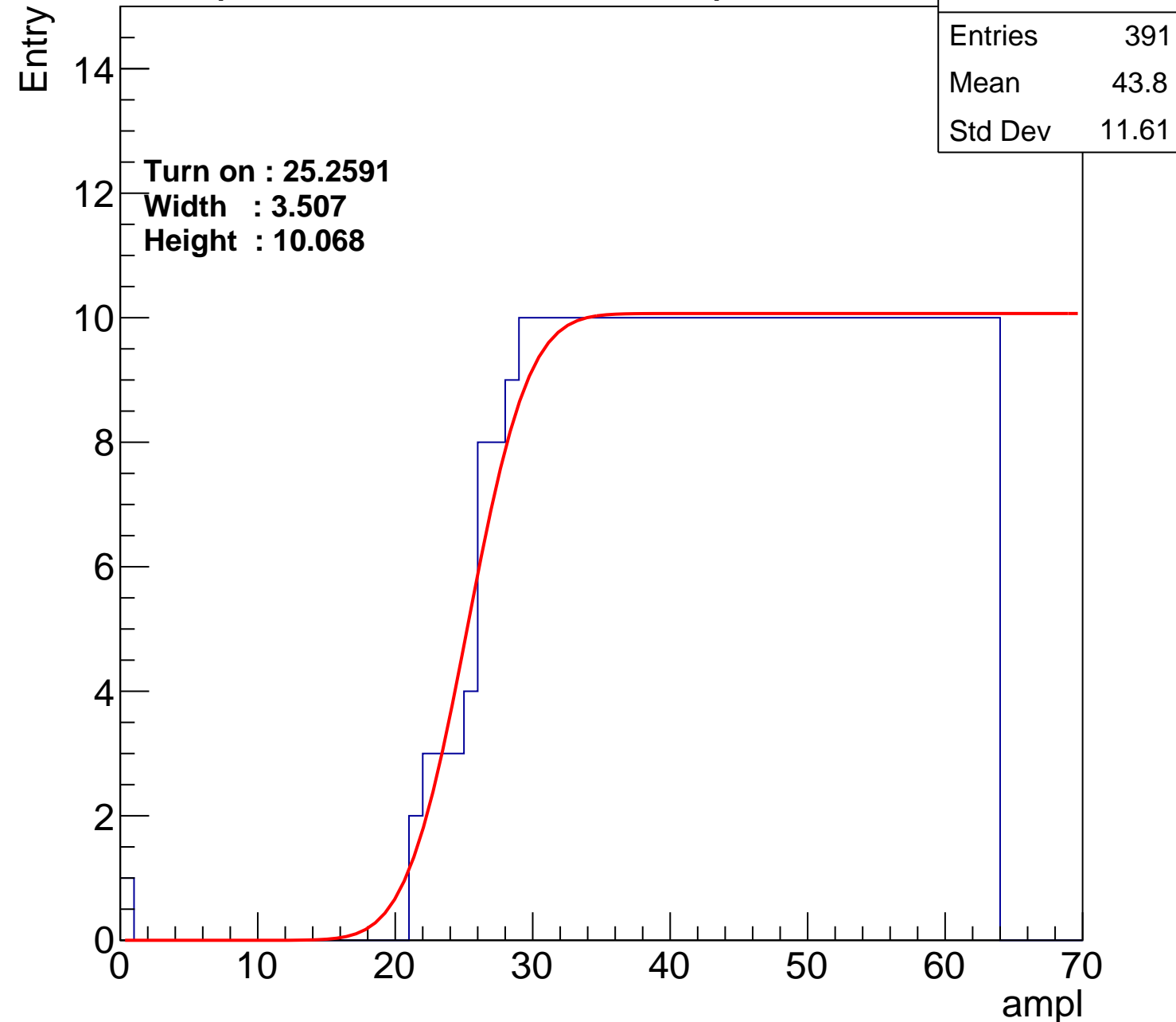
Width : 3.507

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.52
Std Dev	11.17

Turn on : 26.3906

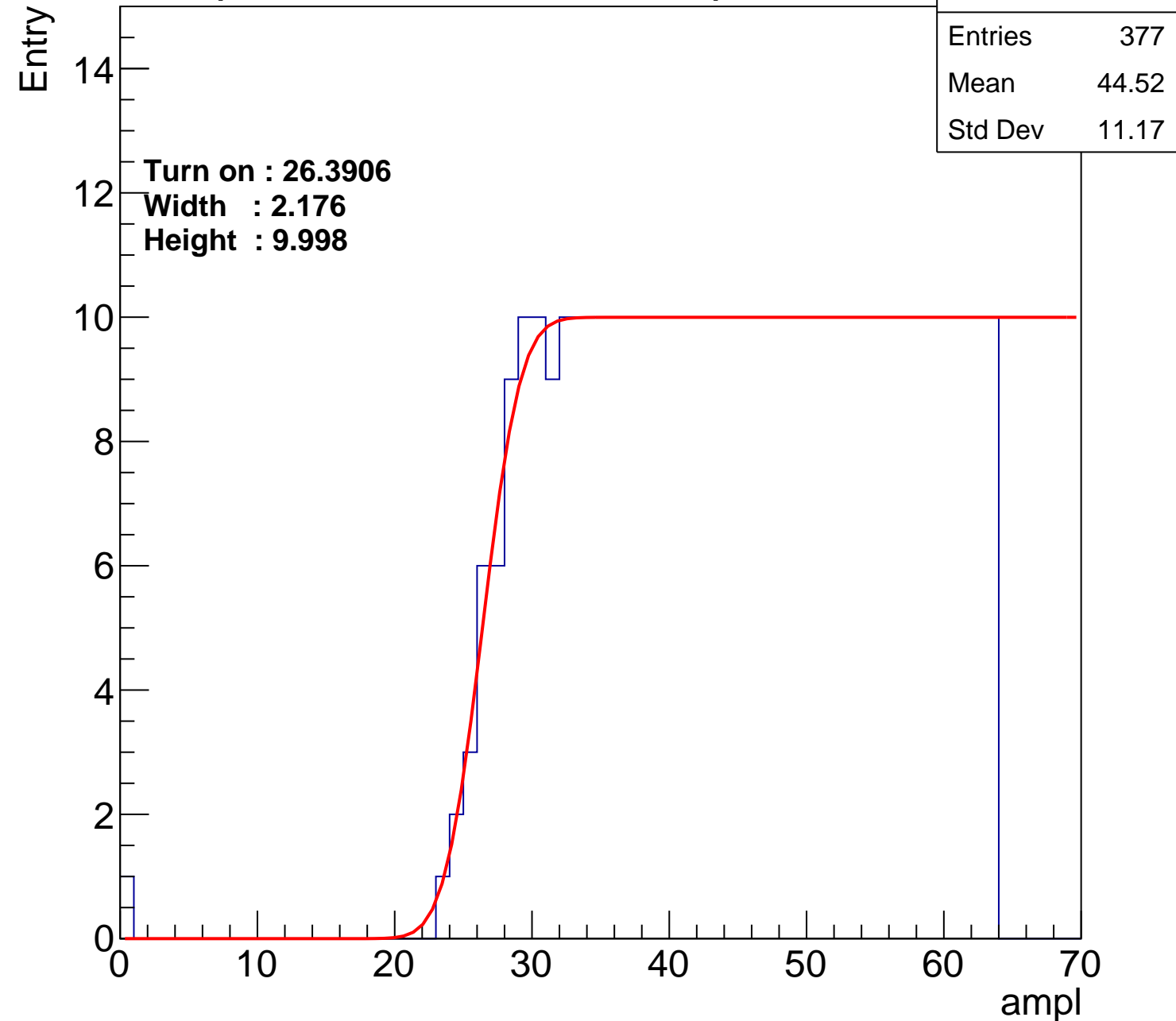
Width : 2.176

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	45.06
Std Dev	11.12

Turn on : 28.0616

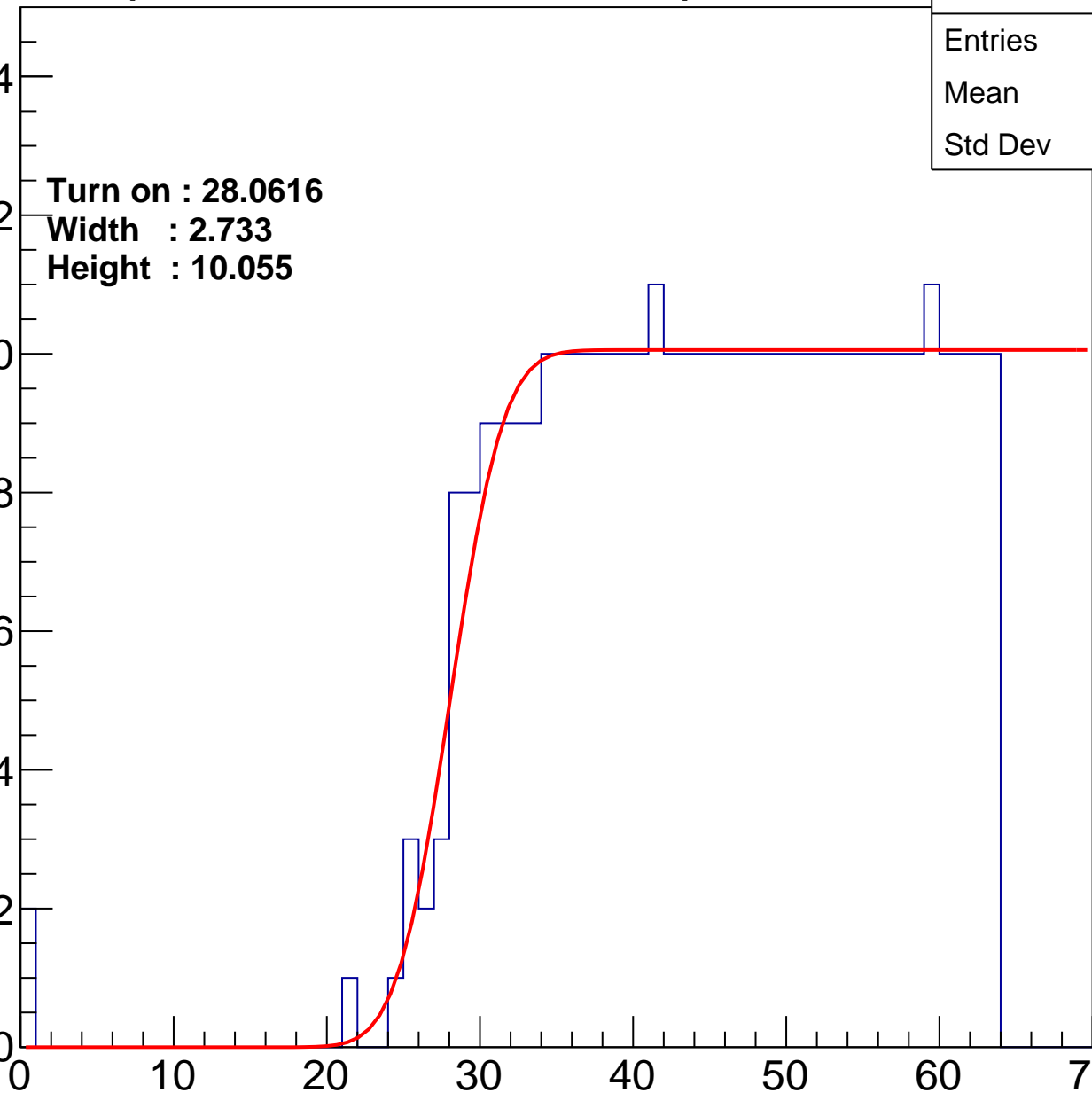
Width : 2.733

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.7
Std Dev	12.06

Turn on : 25.6106

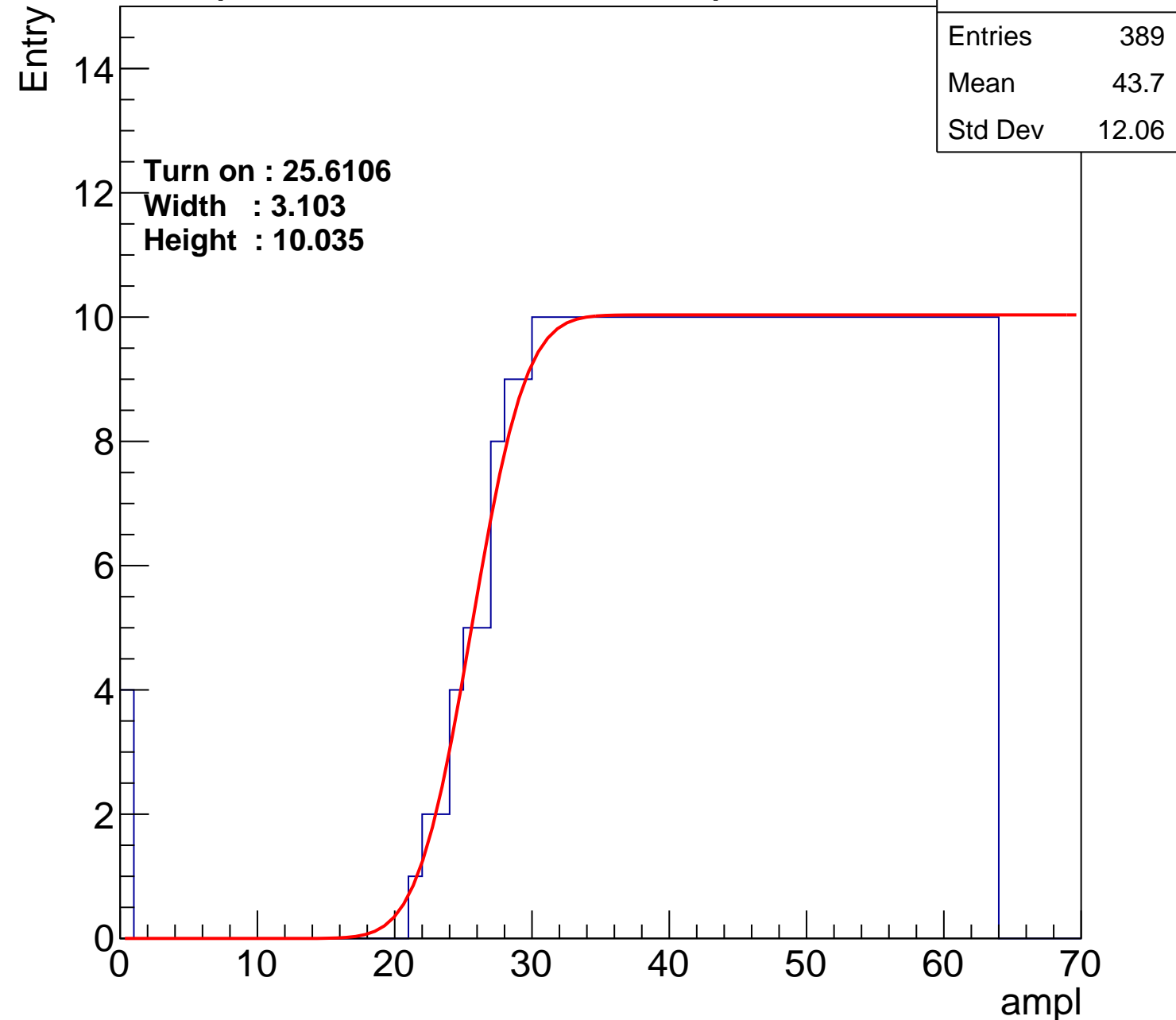
Width : 3.103

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.53
Std Dev	12

Turn on : 24.8883

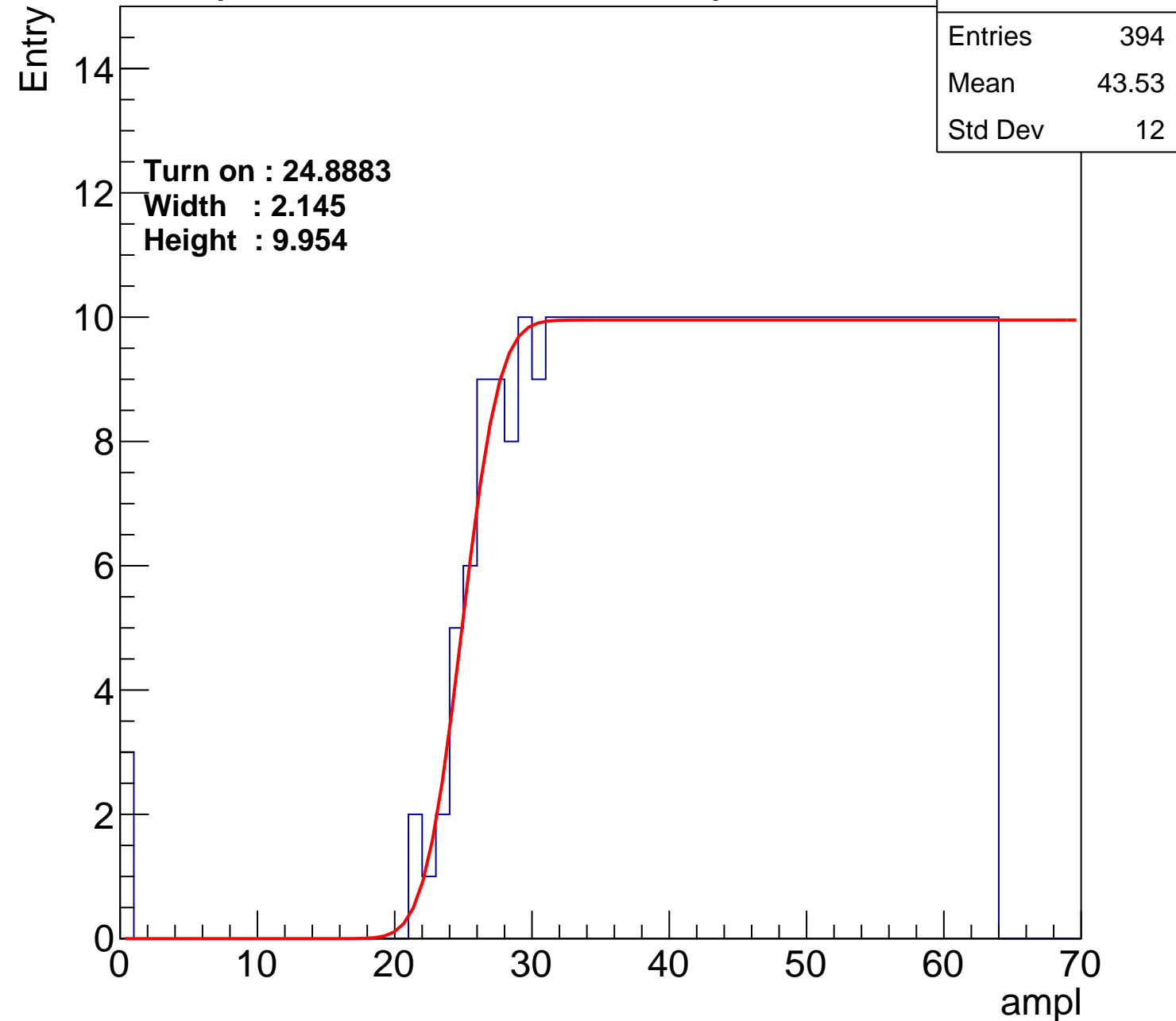
Width : 2.145

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.7
Std Dev	11.56

Turn on : 27.6137

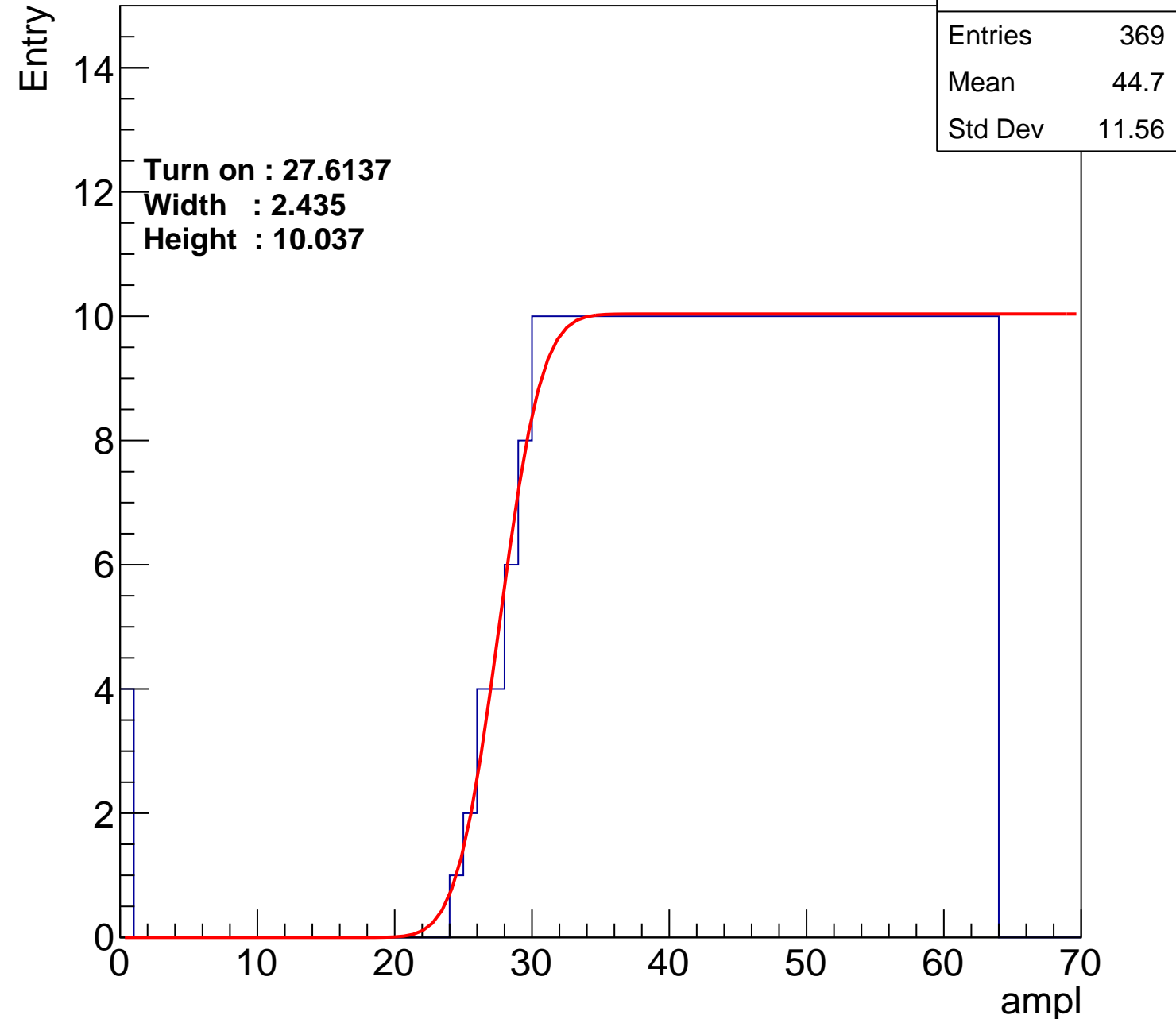
Width : 2.435

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.62
Std Dev	11.92

Turn on : 25.5799

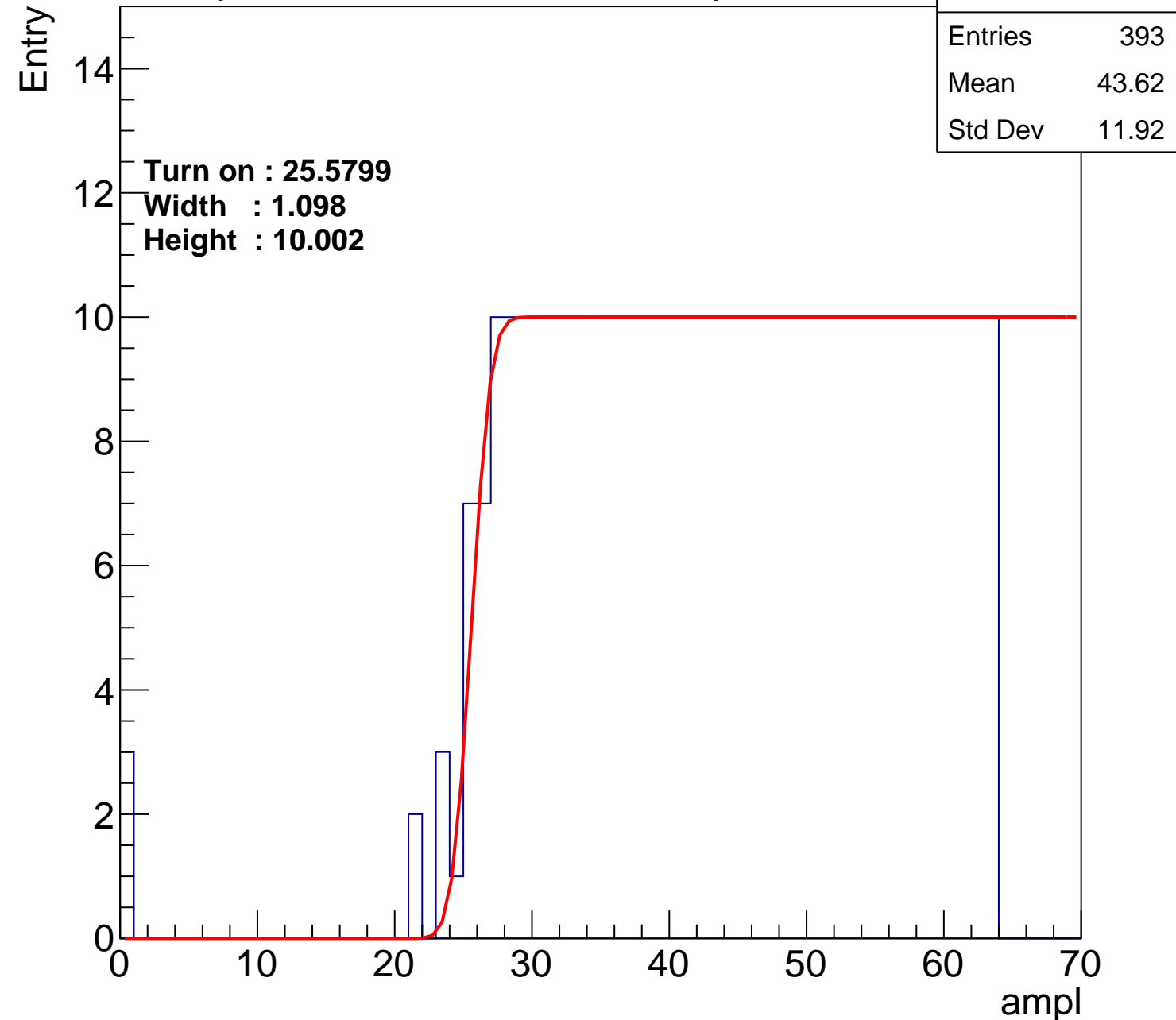
Width : 1.098

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.92
Std Dev	11.15

Turn on : 27.7145

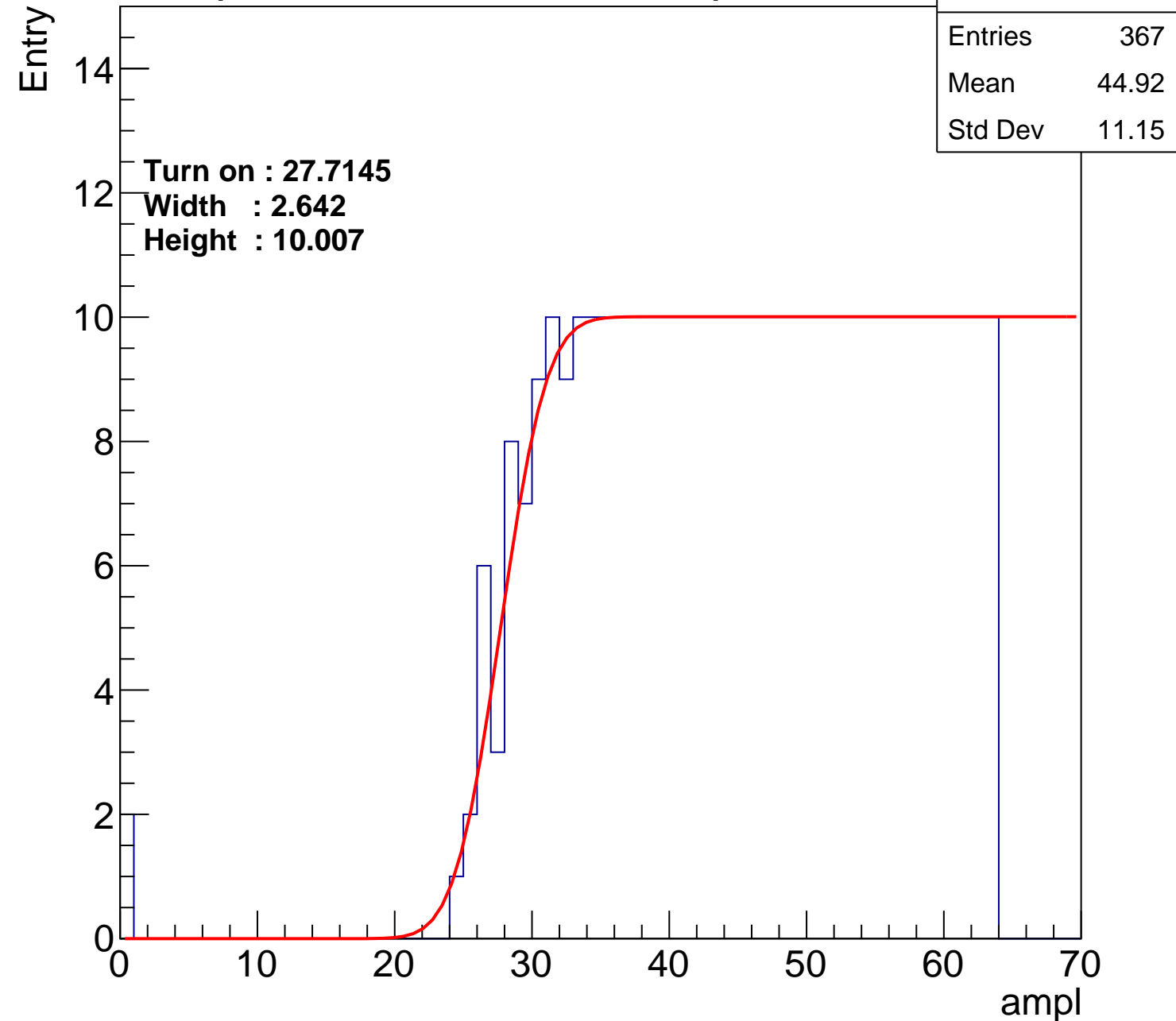
Width : 2.642

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.99
Std Dev	11.8

Turn on : 25.9000

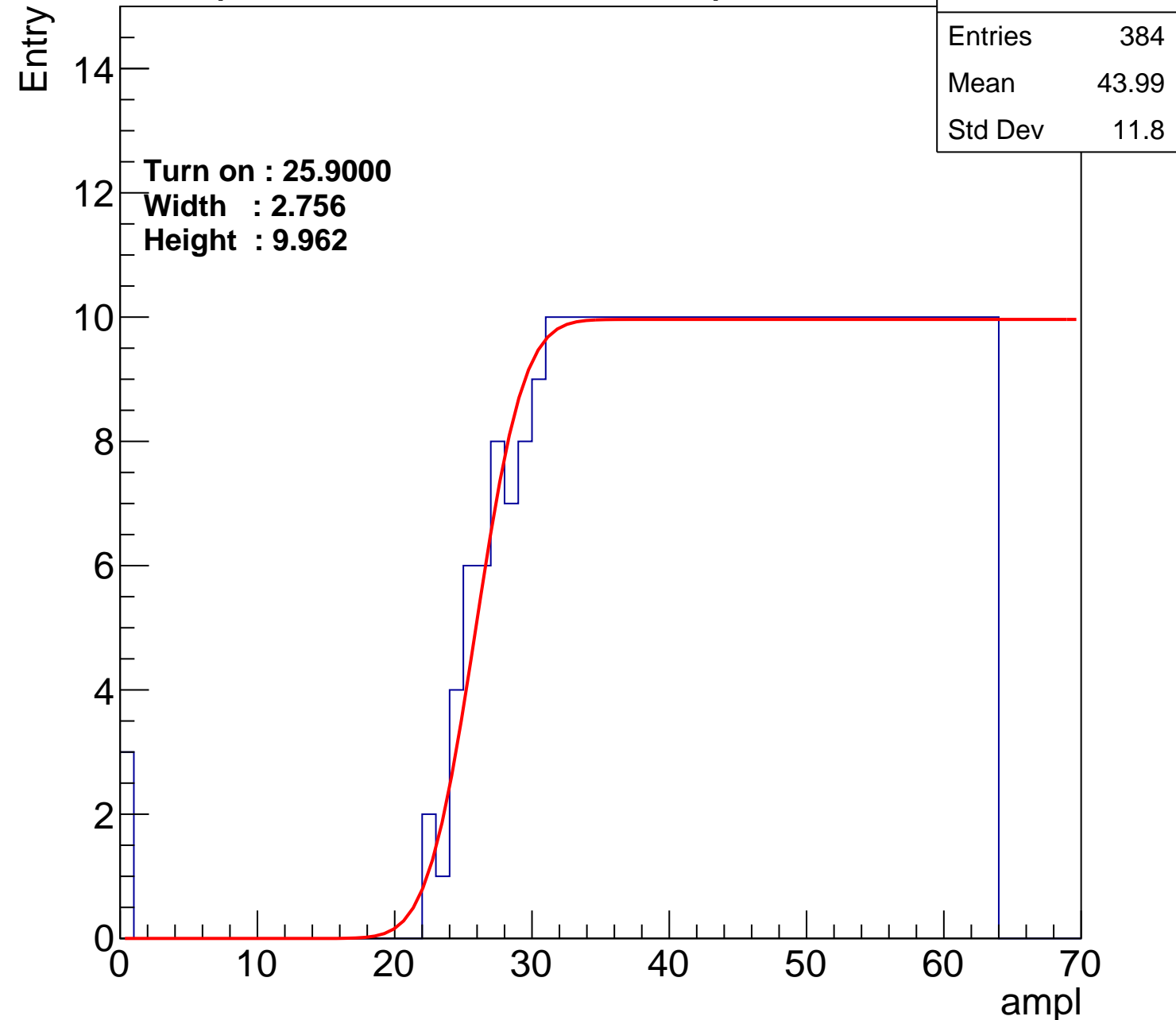
Width : 2.756

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.8786

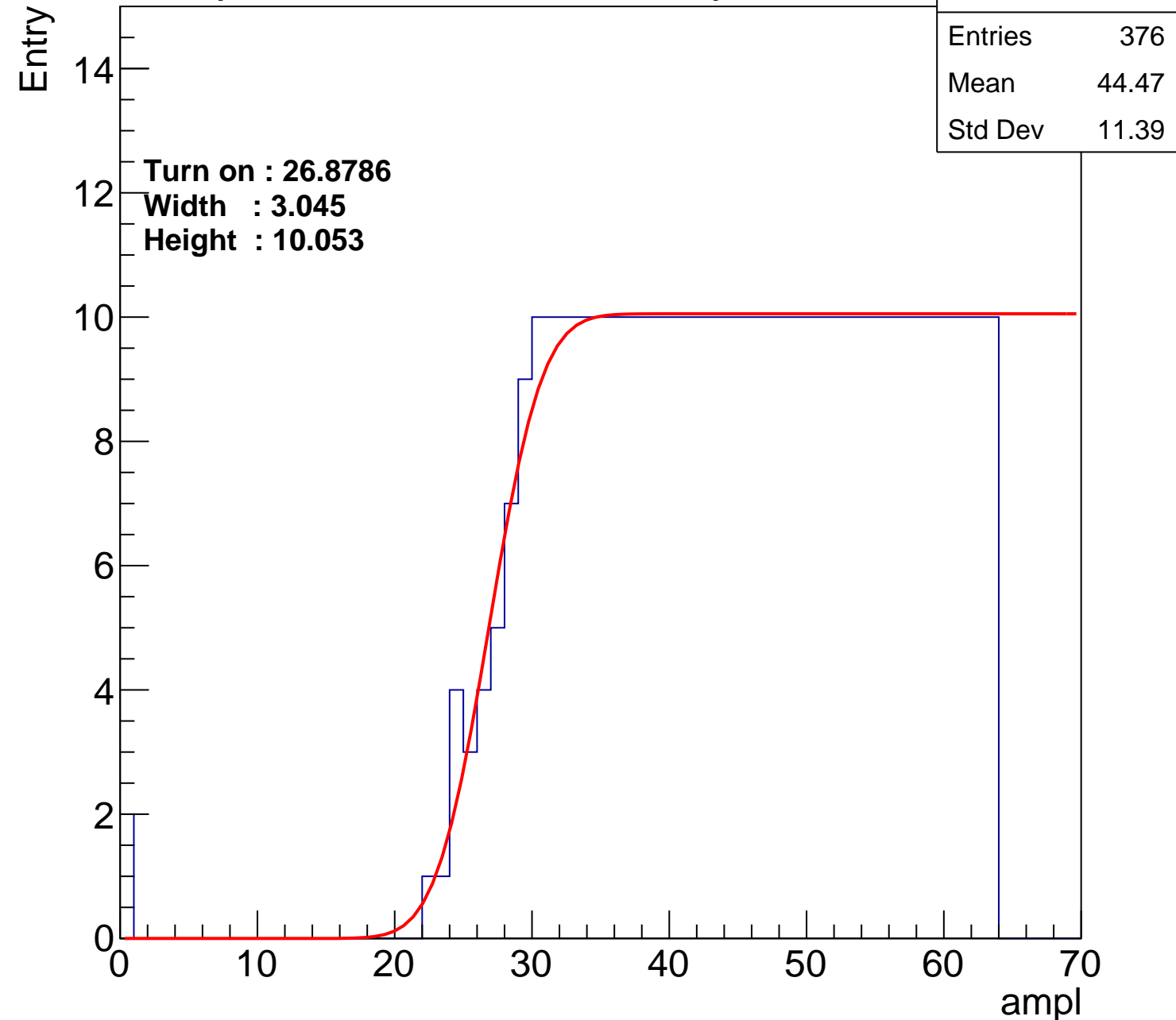
Width : 3.045

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.81
Std Dev	11.37

Turn on : 27.6766

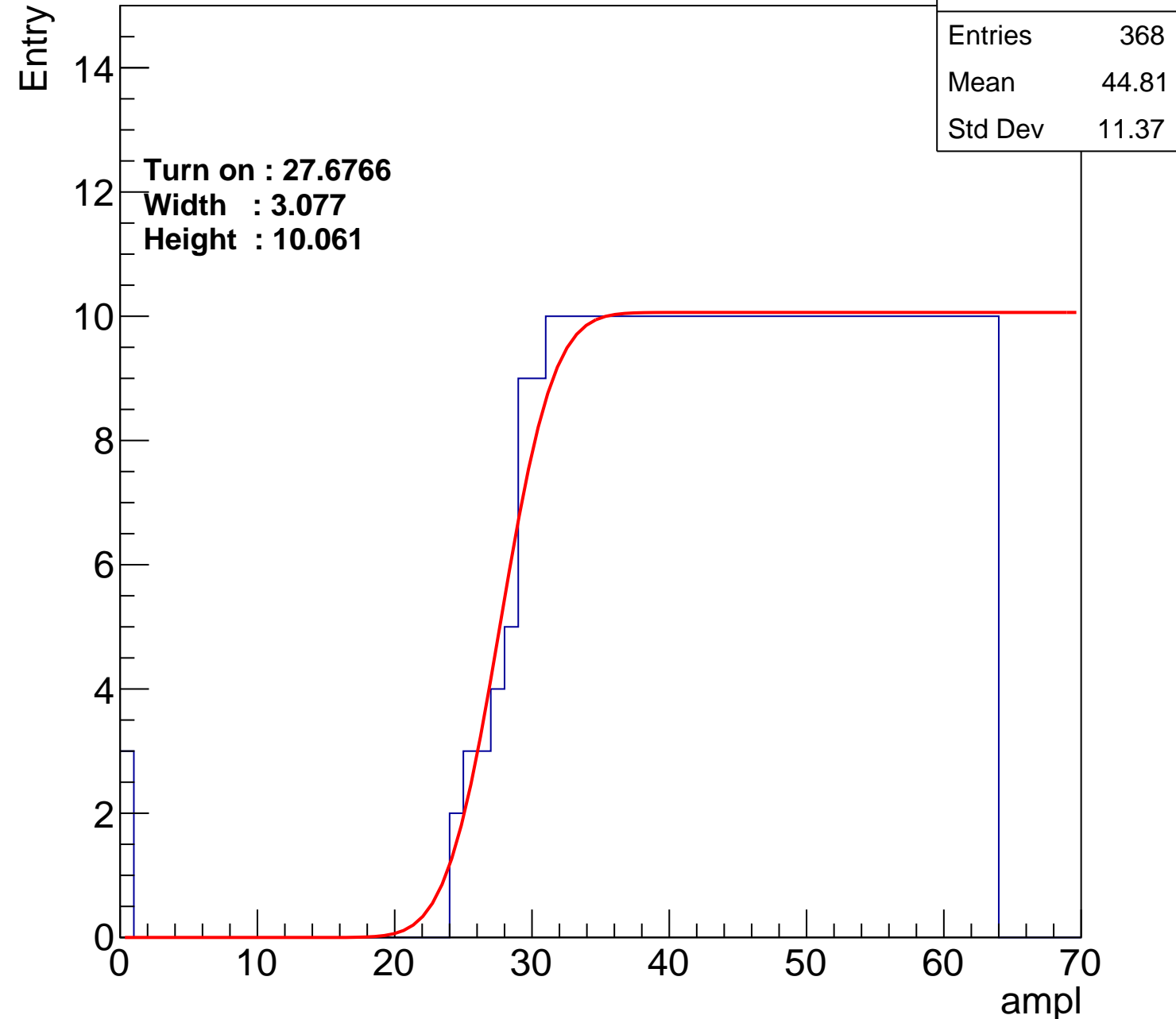
Width : 3.077

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.11
Std Dev	12.04

Turn on : 26.9215

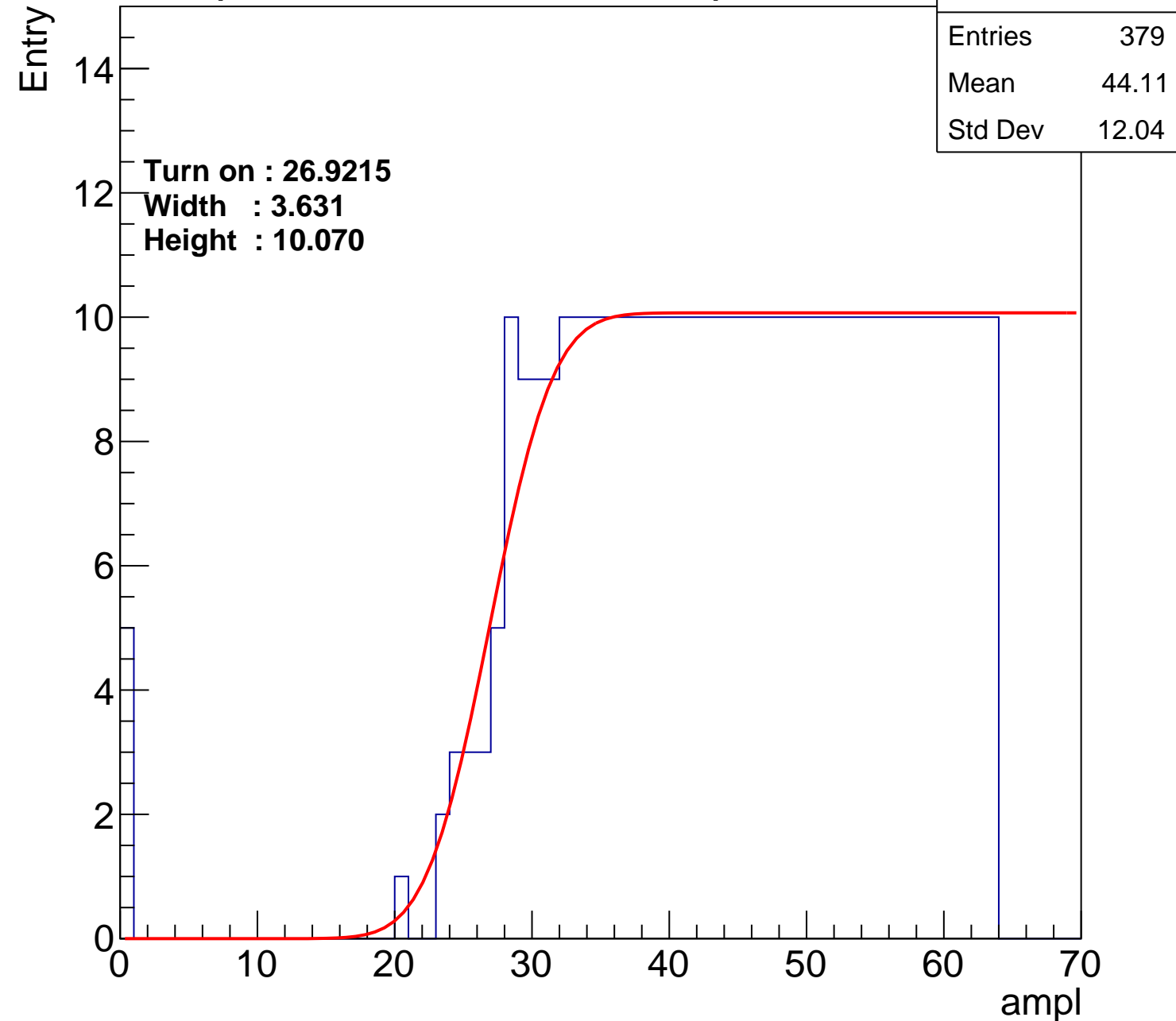
Width : 3.631

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch20

calib_packv5_042523_0143.root, FC#12, port B1

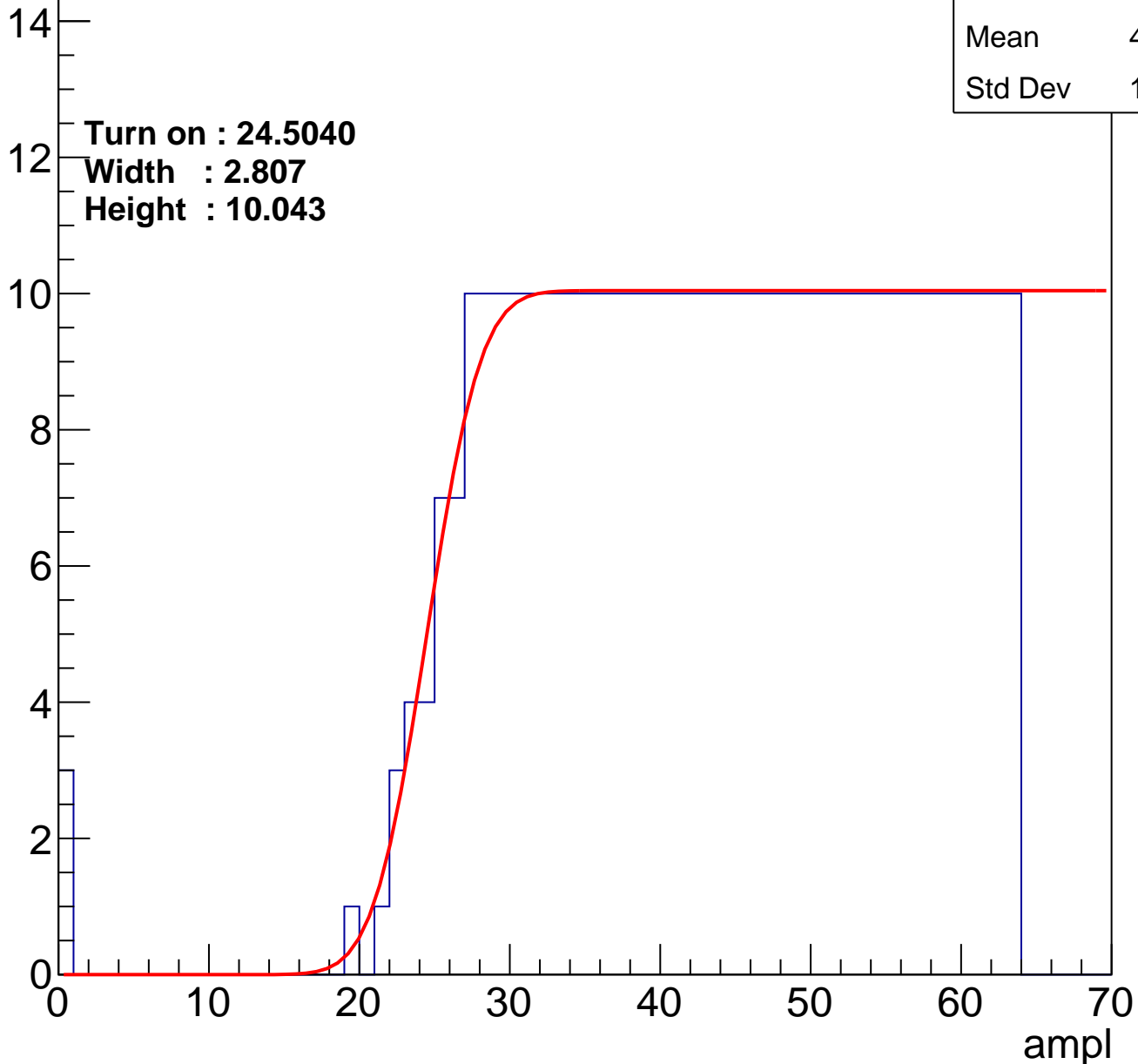
Entries	400
Mean	43.25
Std Dev	12.13

Turn on : 24.5040

Width : 2.807

Height : 10.043

Entry



B0L102S, U15-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 26.5396

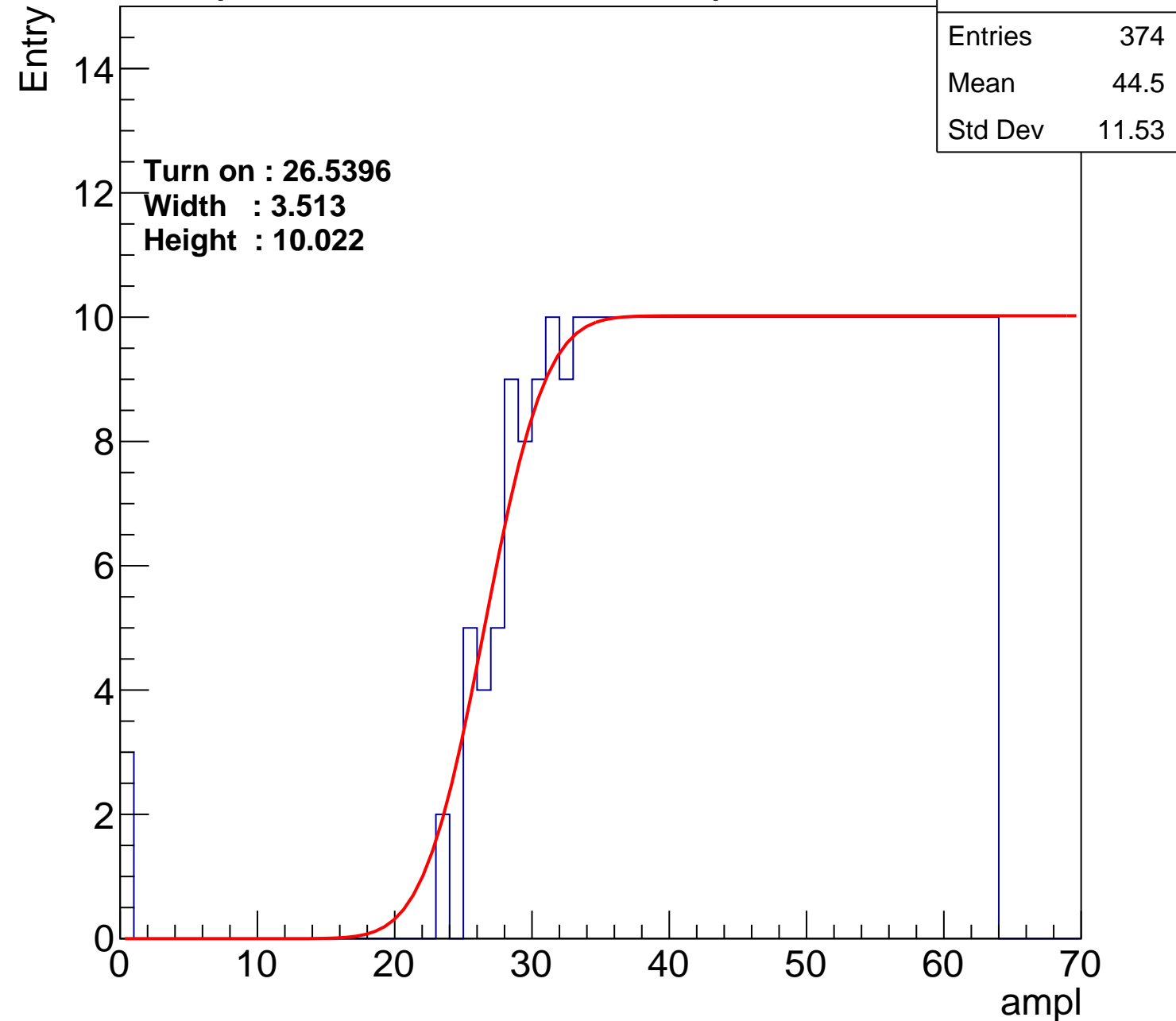
Width : 3.513

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.1356

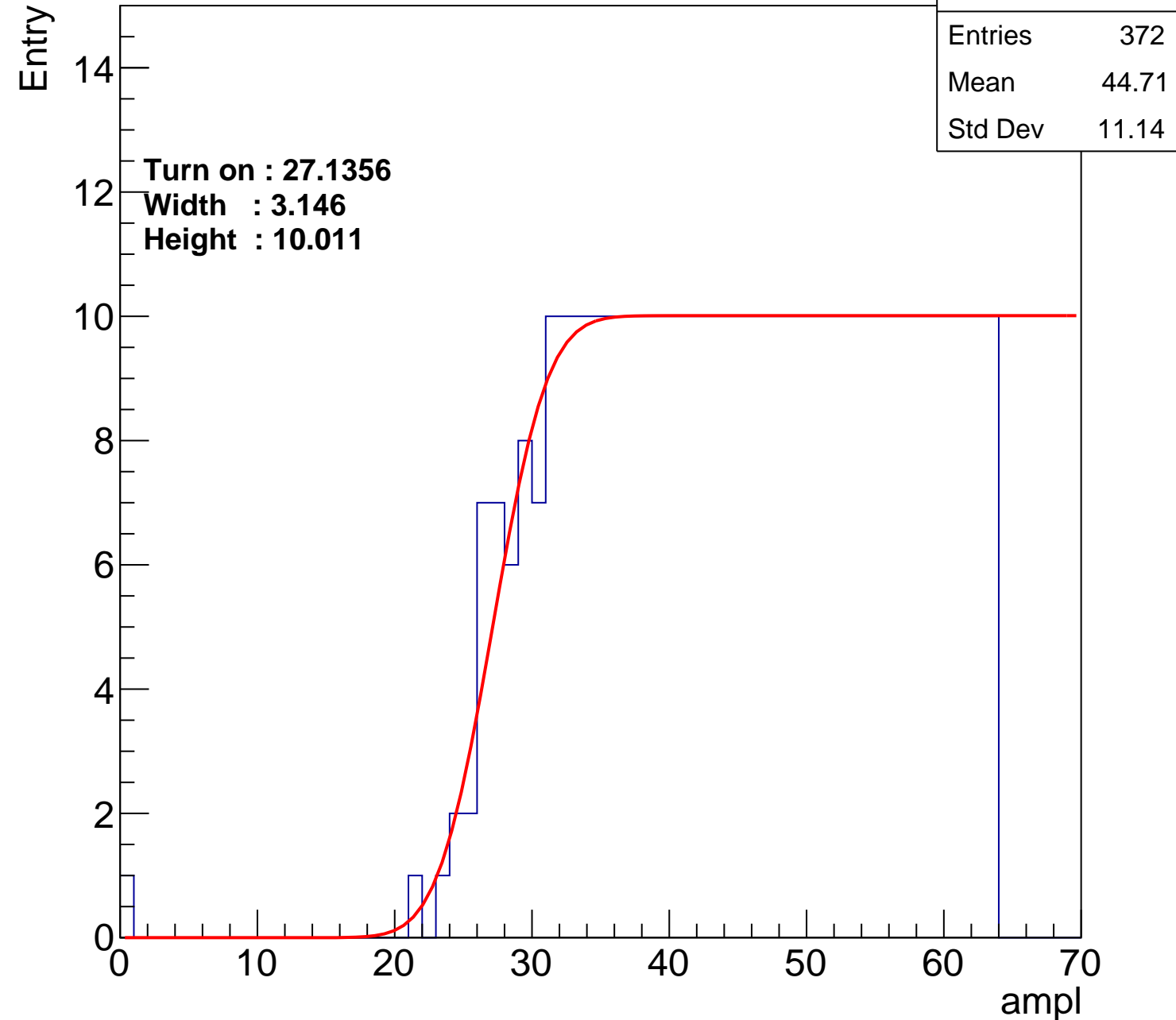
Width : 3.146

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.19
Std Dev	11.66

Turn on : 26.5424

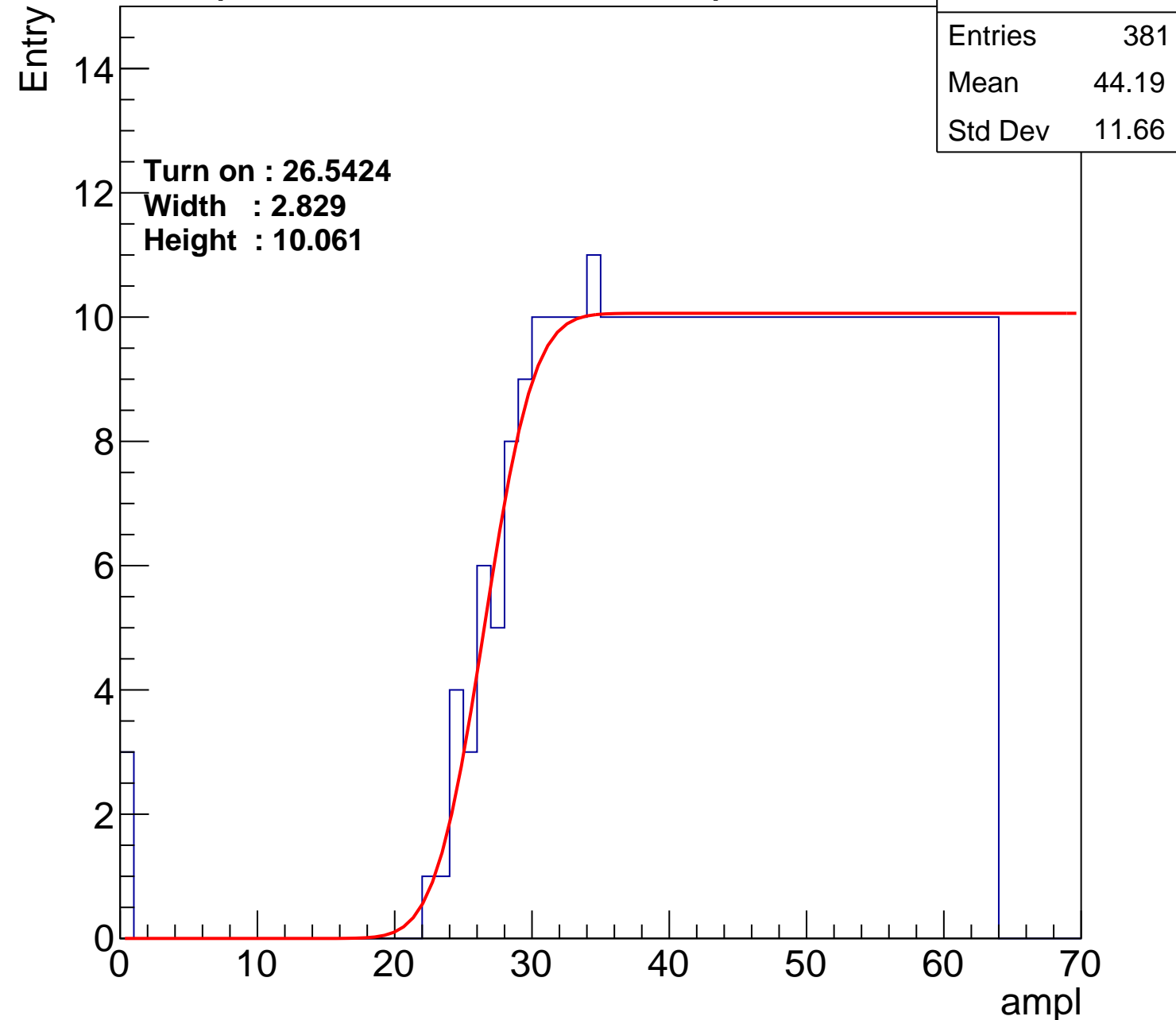
Width : 2.829

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch24

calib_packv5_042523_0143.root, FC#12, port B1

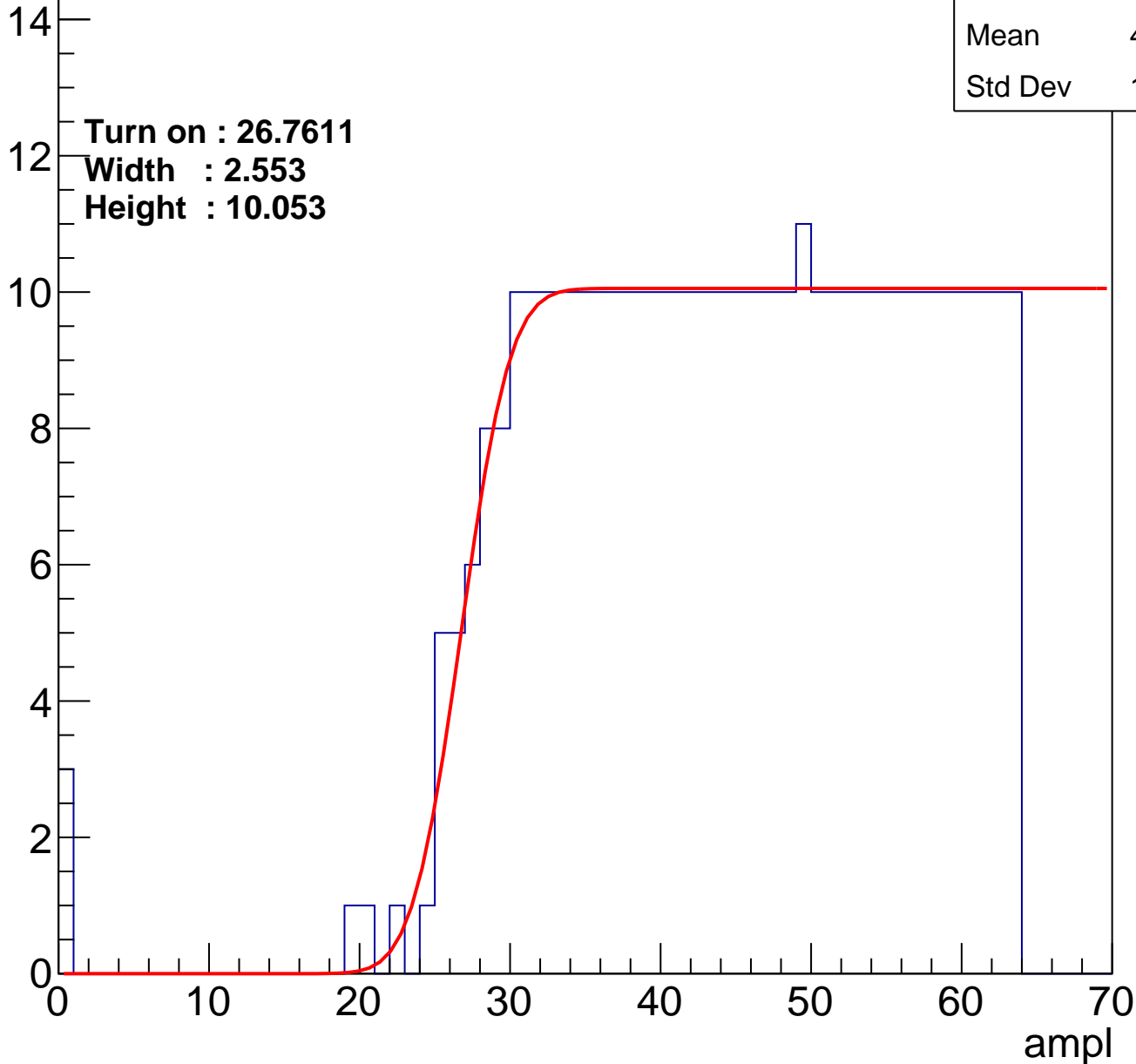
Entries	380
Mean	44.26
Std Dev	11.67

Turn on : 26.7611

Width : 2.553

Height : 10.053

Entry



B0L102S, U15-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.59
Std Dev	11.23

Turn on : 26.9839

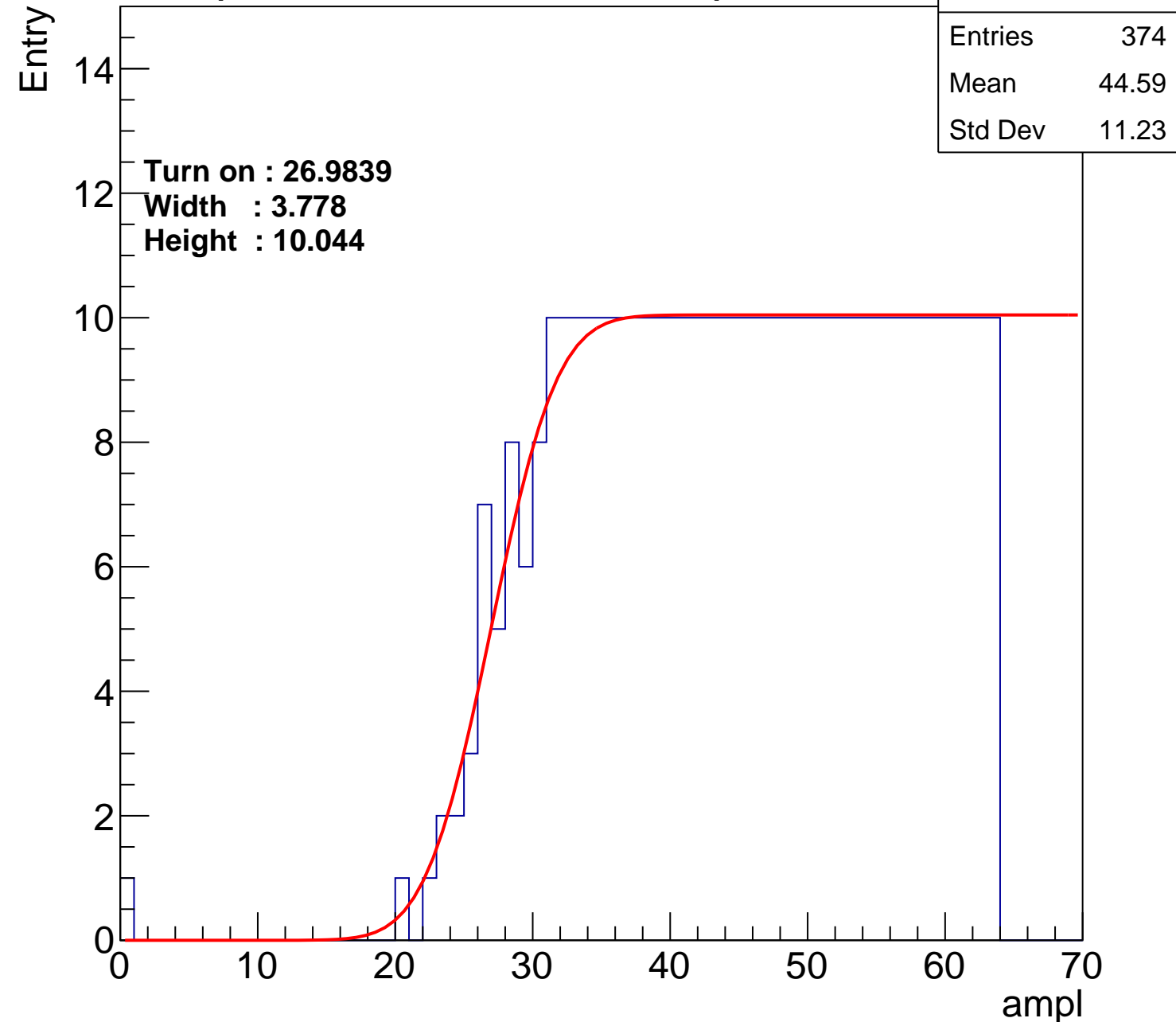
Width : 3.778

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch26

calib_packv5_042523_0143.root, FC#12, port B1

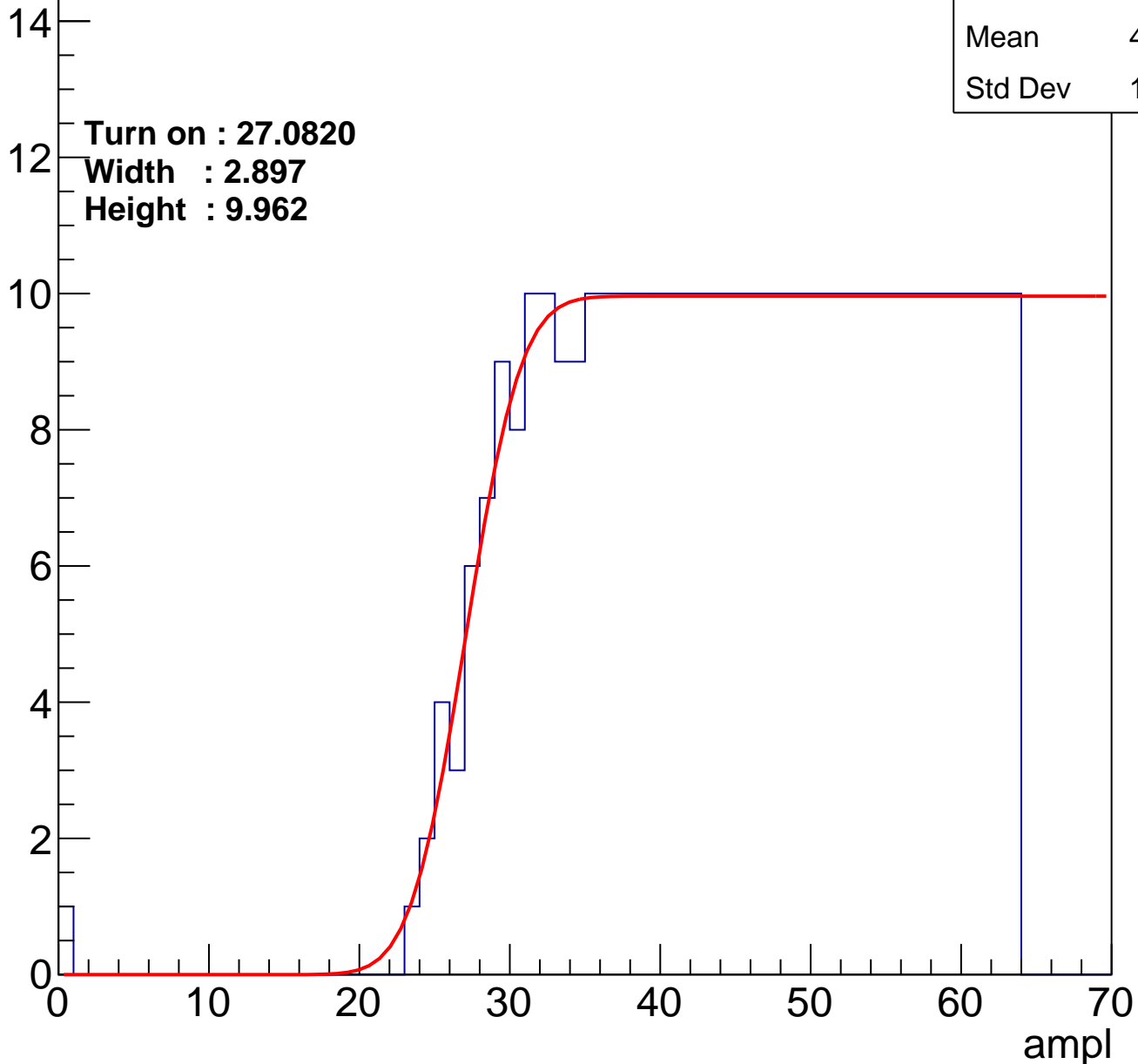
Entries	369
Mean	44.85
Std Dev	11.06

Turn on : 27.0820

Width : 2.897

Height : 9.962

Entry



B0L102S, U15-ch27

calib_packv5_042523_0143.root, FC#12, port B1

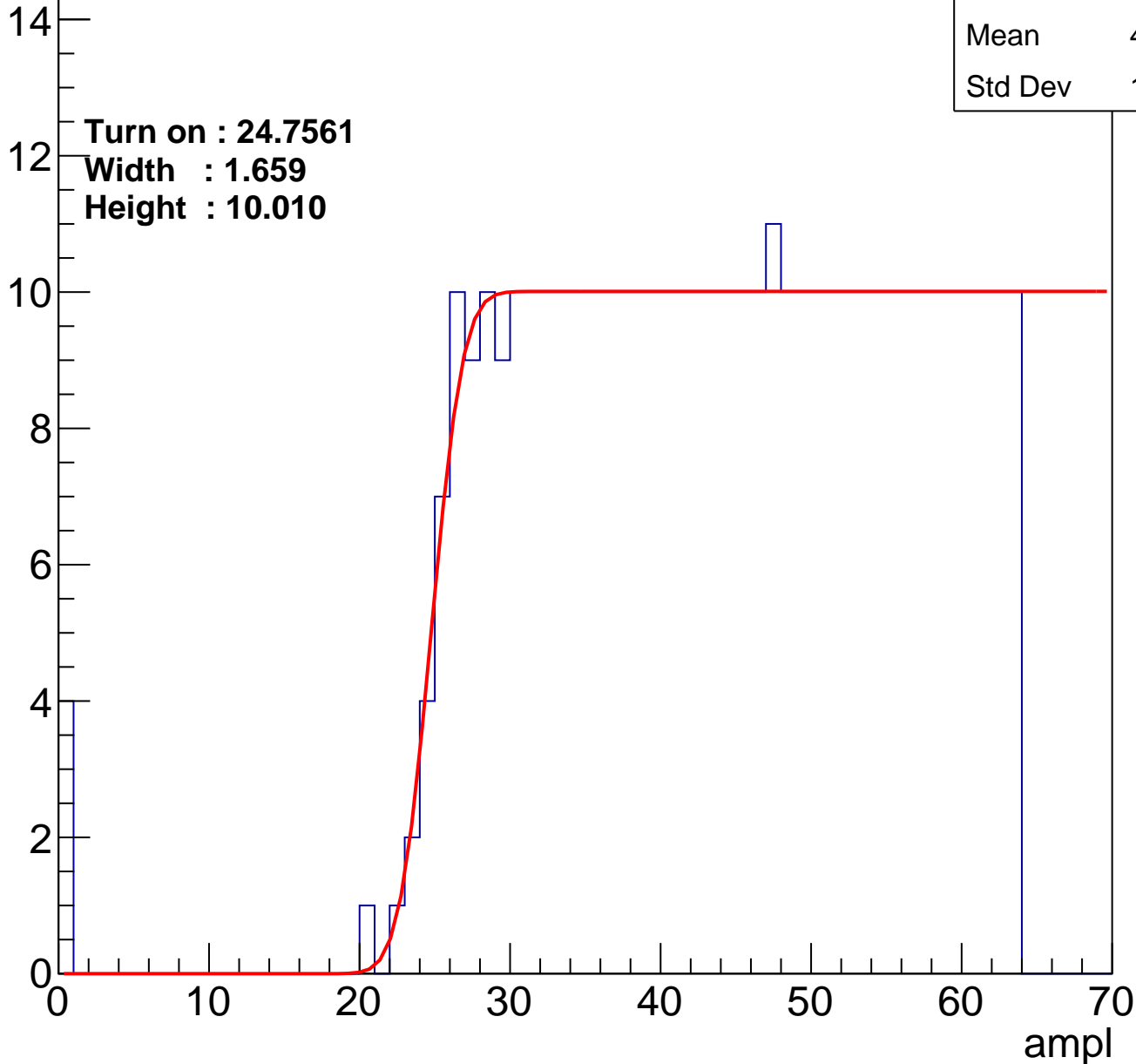
Entries	398
Mean	43.37
Std Dev	12.17

Turn on : 24.7561

Width : 1.659

Height : 10.010

Entry



B0L102S, U15-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.8244

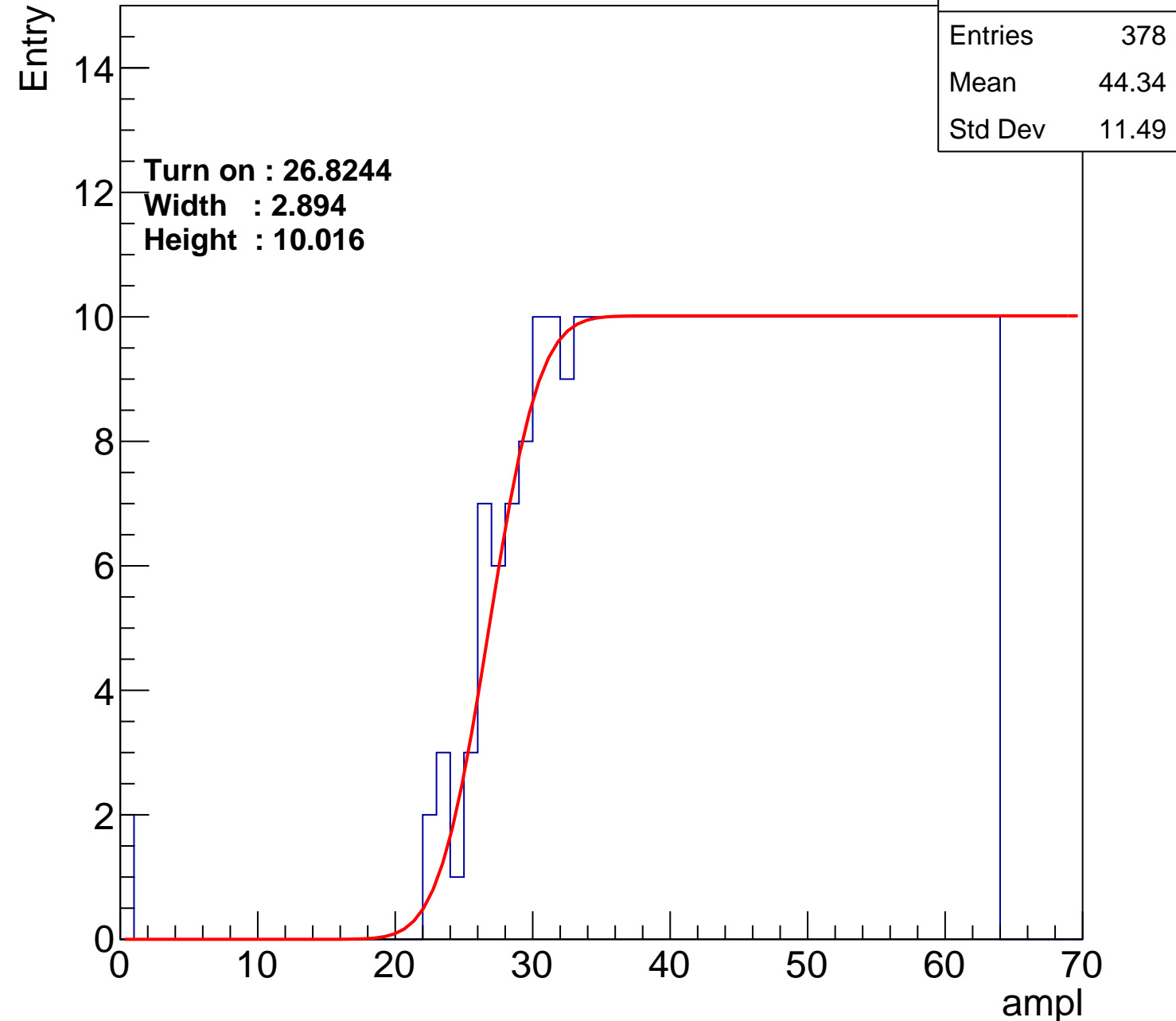
Width : 2.894

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.35
Std Dev	12.34

Turn on : 25.3012

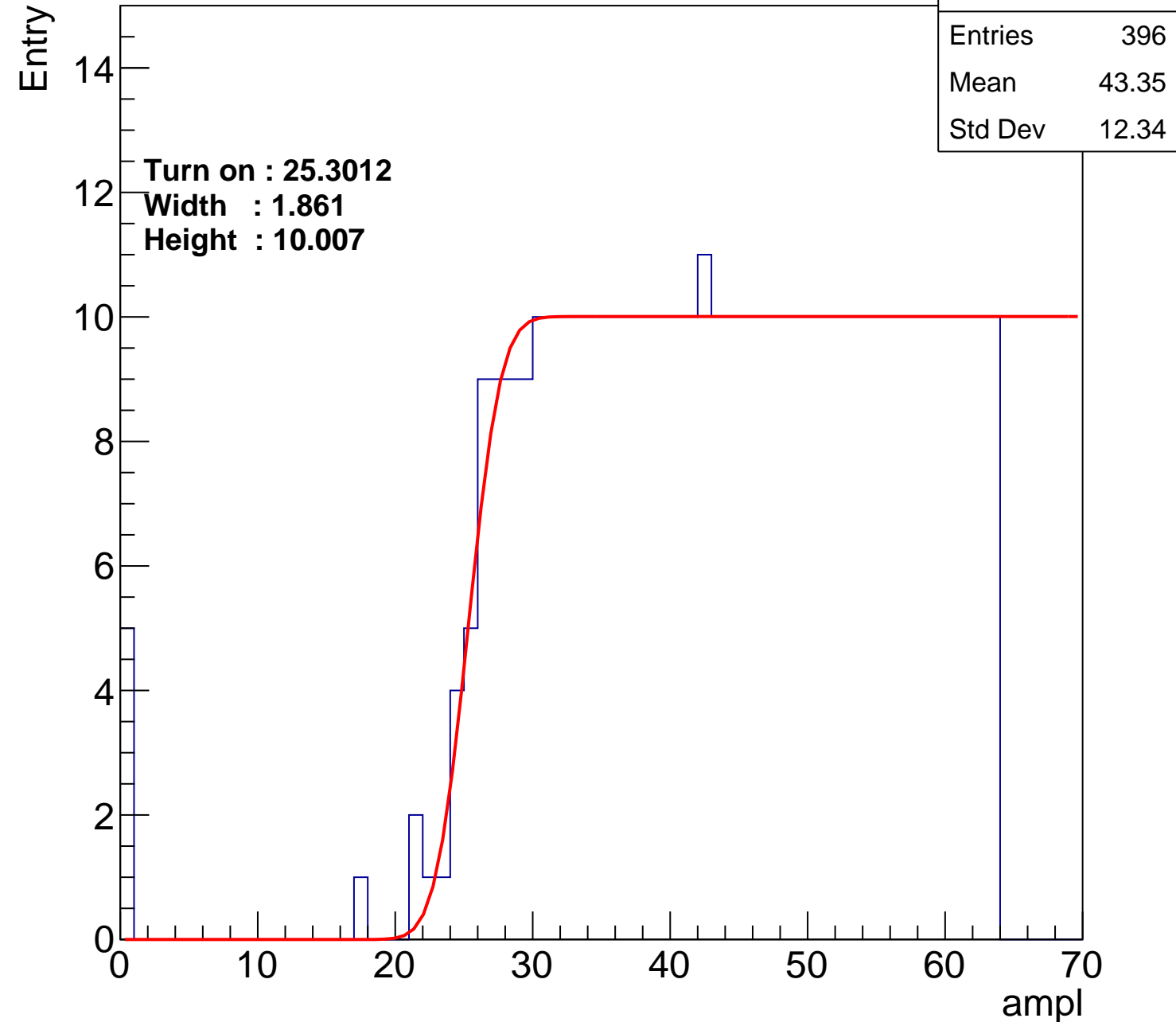
Width : 1.861

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch30

calib_packv5_042523_0143.root, FC#12, port B1

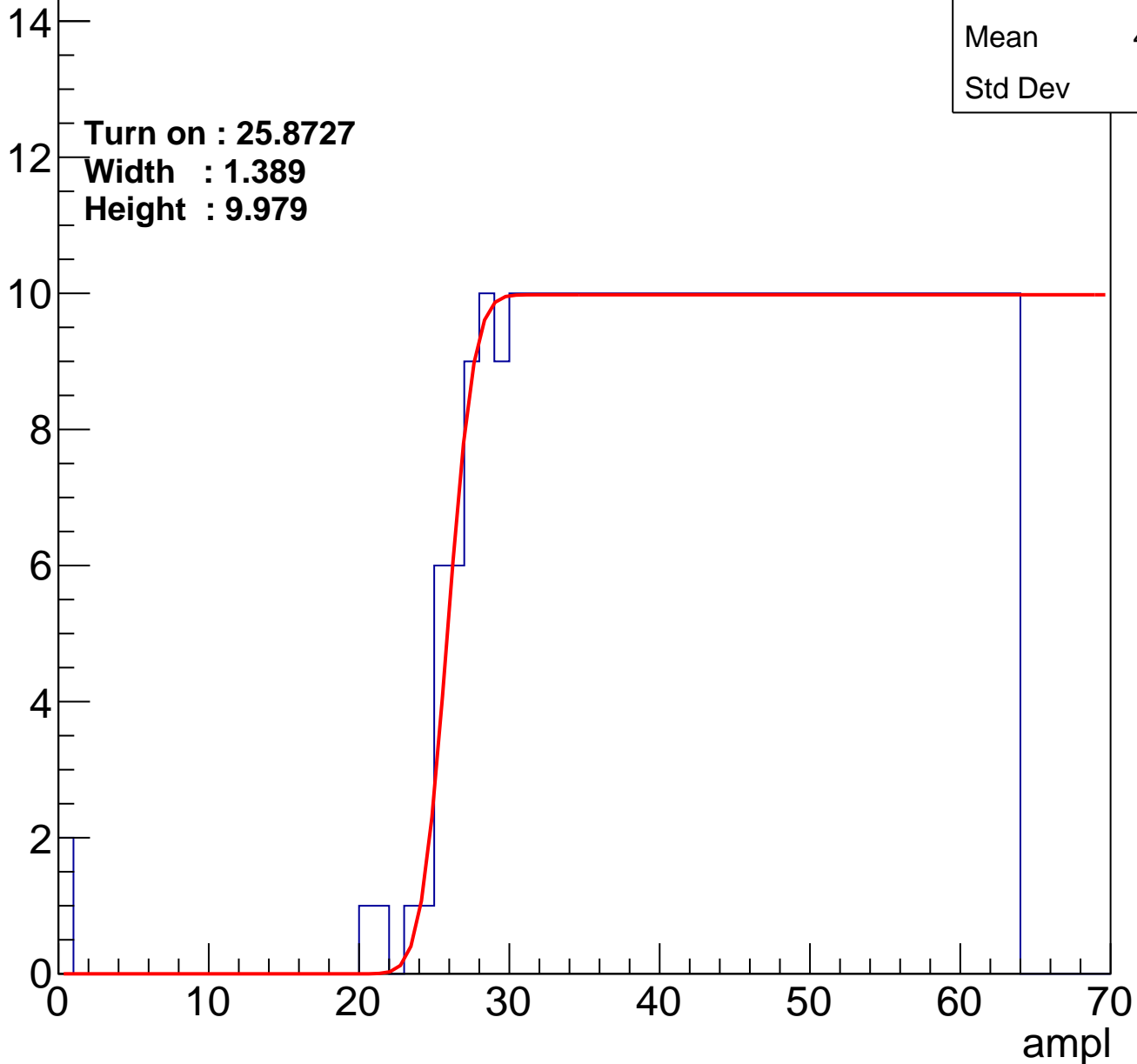
Entries	386
Mean	44.01
Std Dev	11.6

Turn on : 25.8727

Width : 1.389

Height : 9.979

Entry



B0L102S, U15-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.5
Std Dev	11.41

Turn on : 26.9134

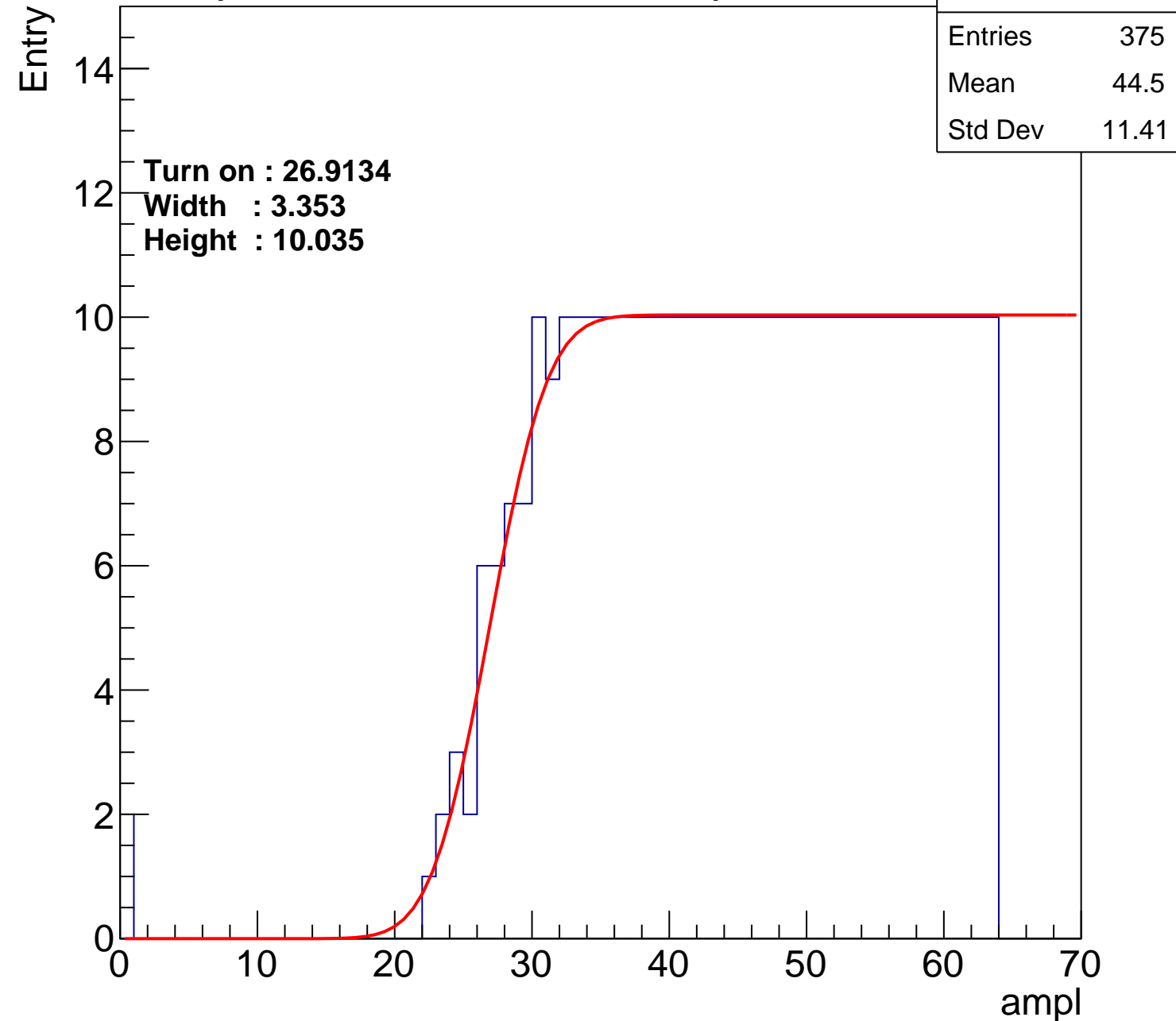
Width : 3.353

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.39
Std Dev	11.46

Turn on : 26.8054

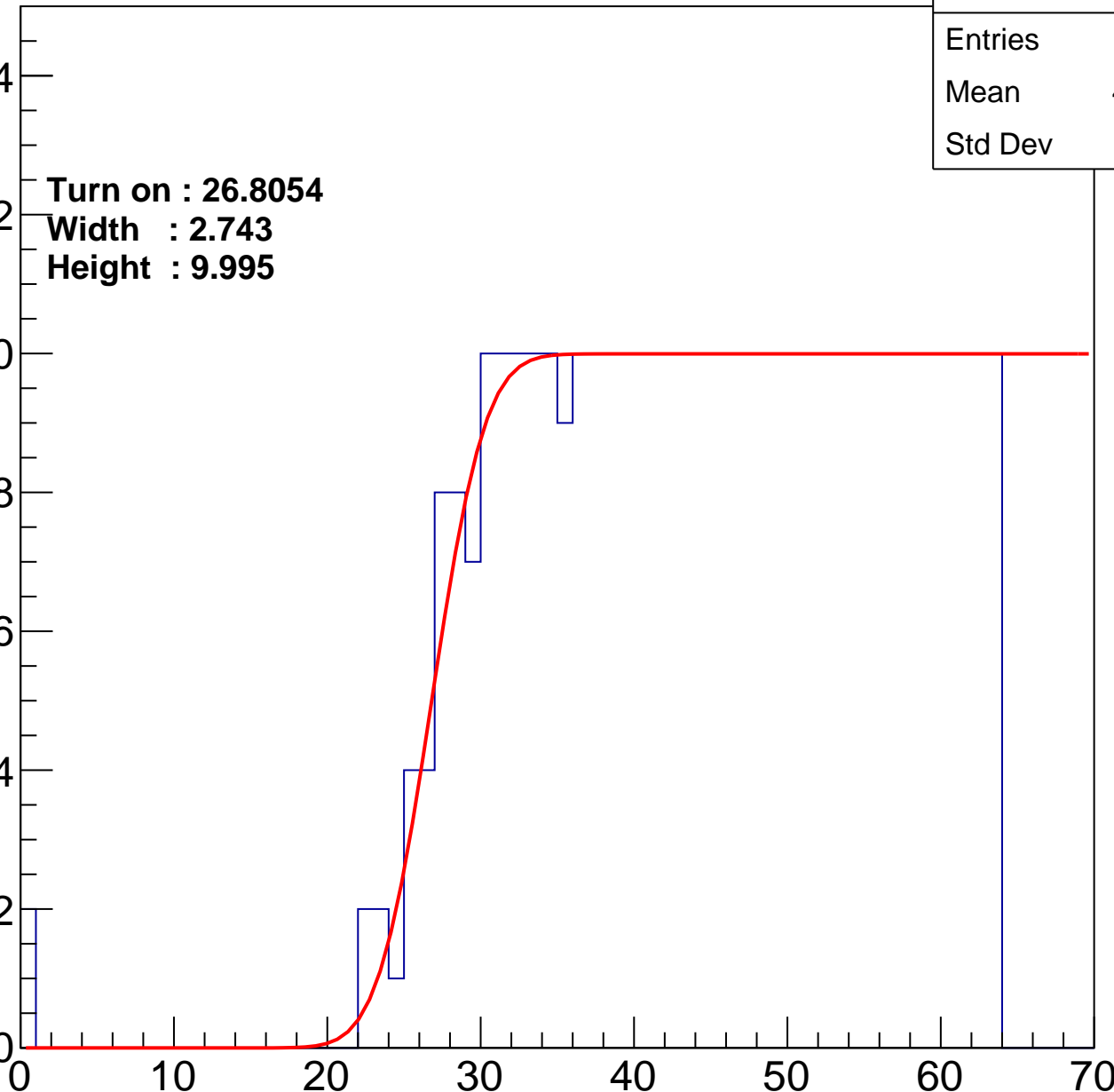
Width : 2.743

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.21
Std Dev	11.51

Turn on : 26.6353

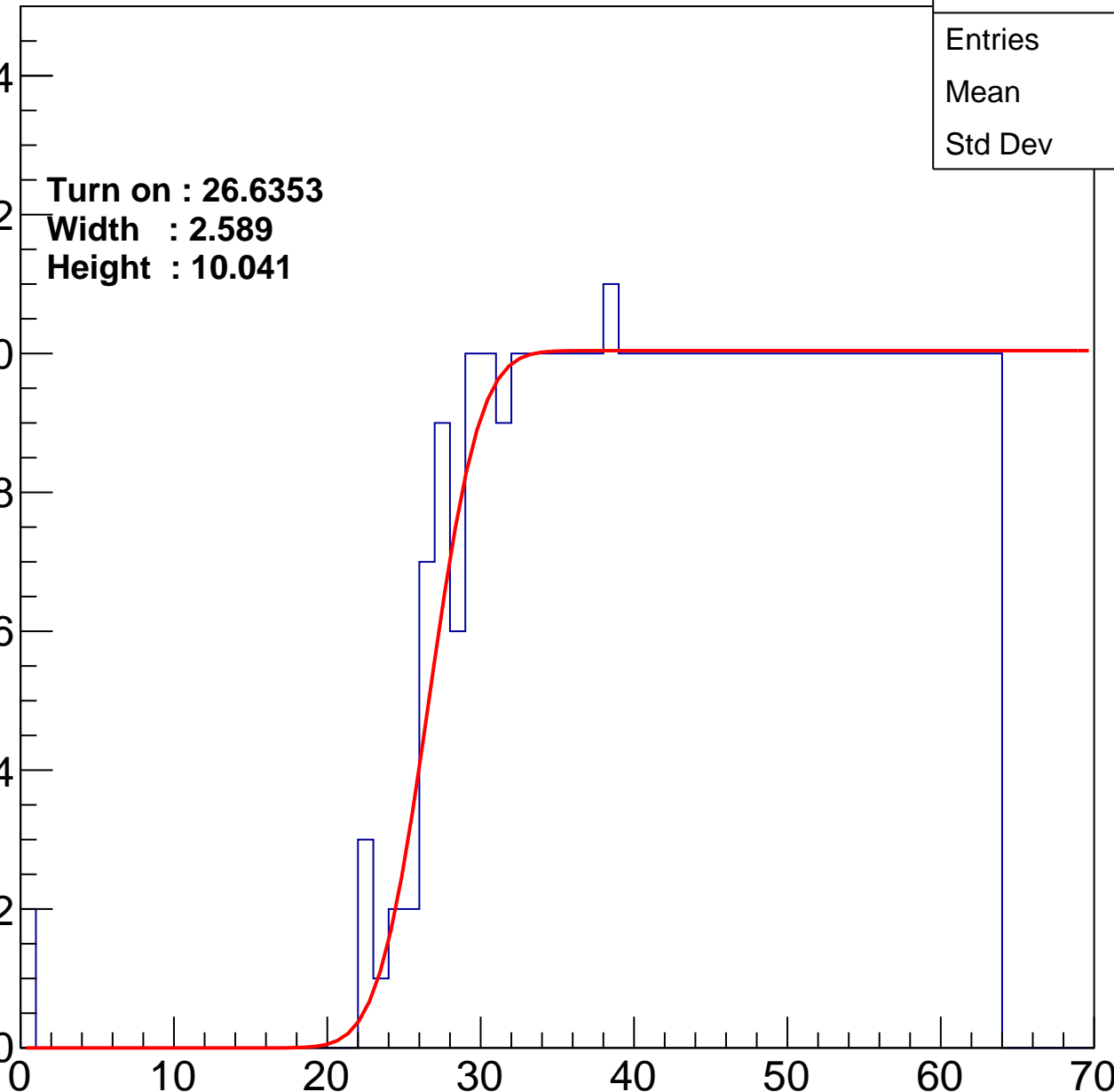
Width : 2.589

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 27.3728

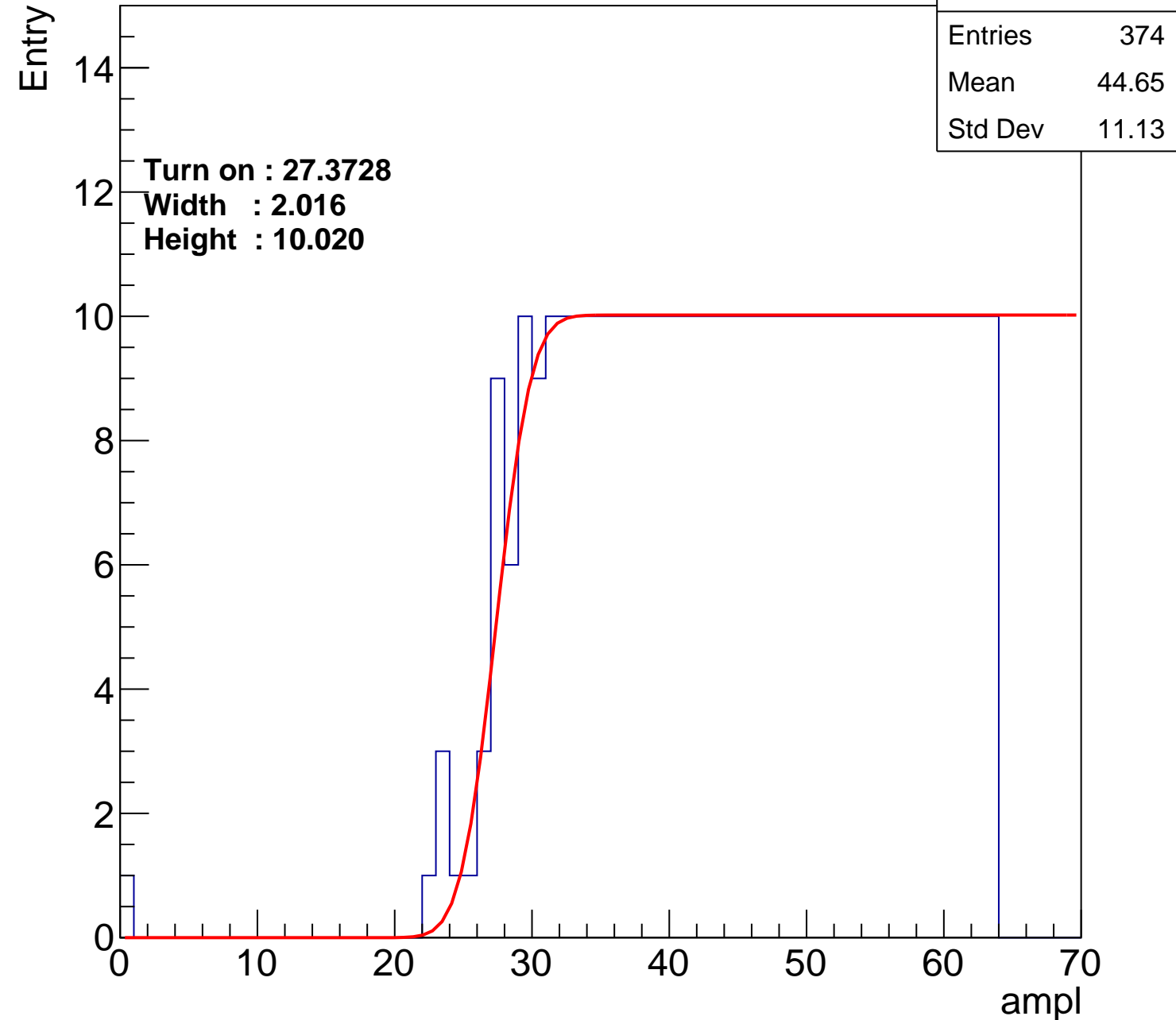
Width : 2.016

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch35

calib_packv5_042523_0143.root, FC#12, port B1

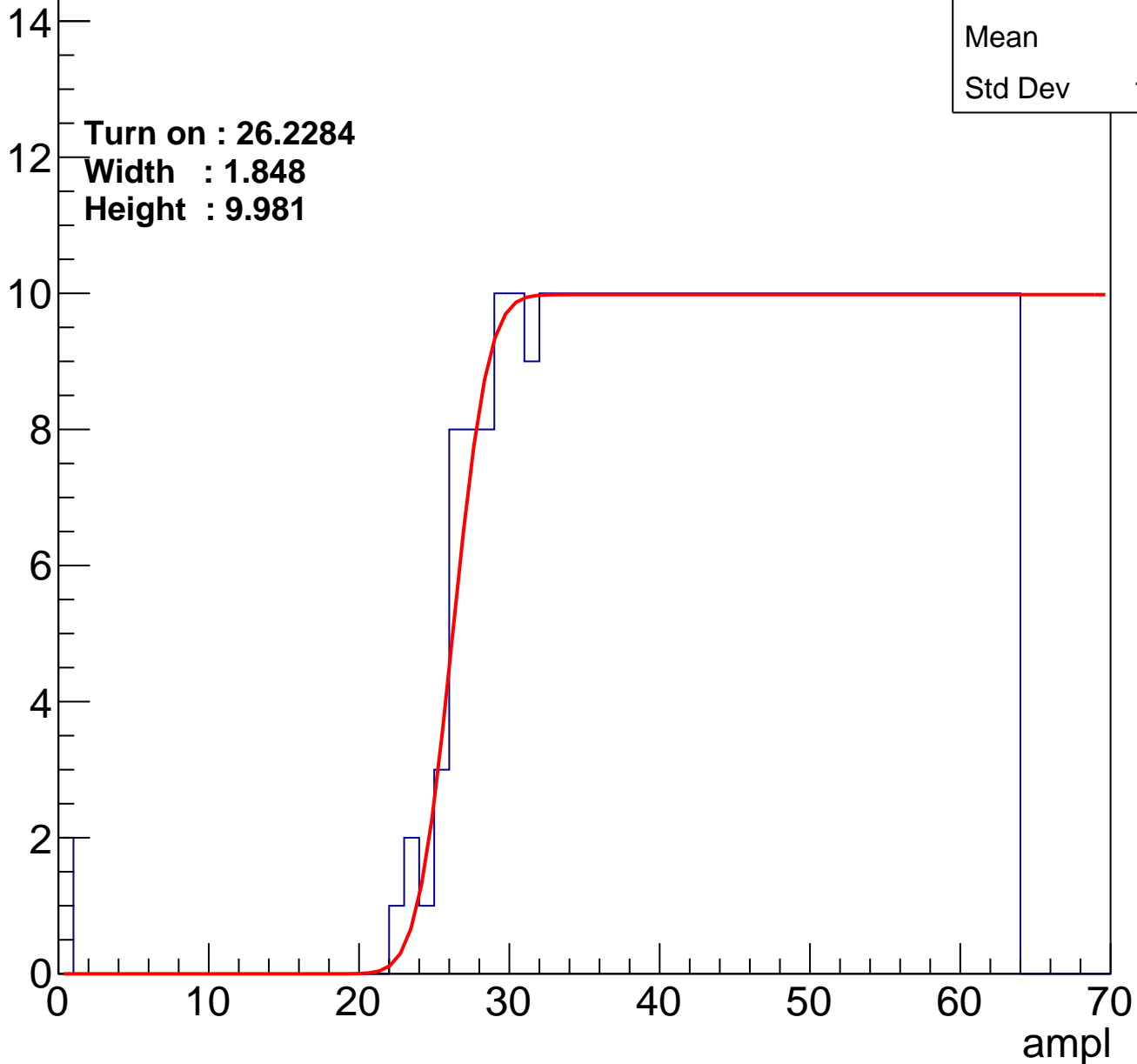
Entries	382
Mean	44.2
Std Dev	11.51

Turn on : 26.2284

Width : 1.848

Height : 9.981

Entry



B0L102S, U15-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.87
Std Dev	12.19

Turn on : 26.0467

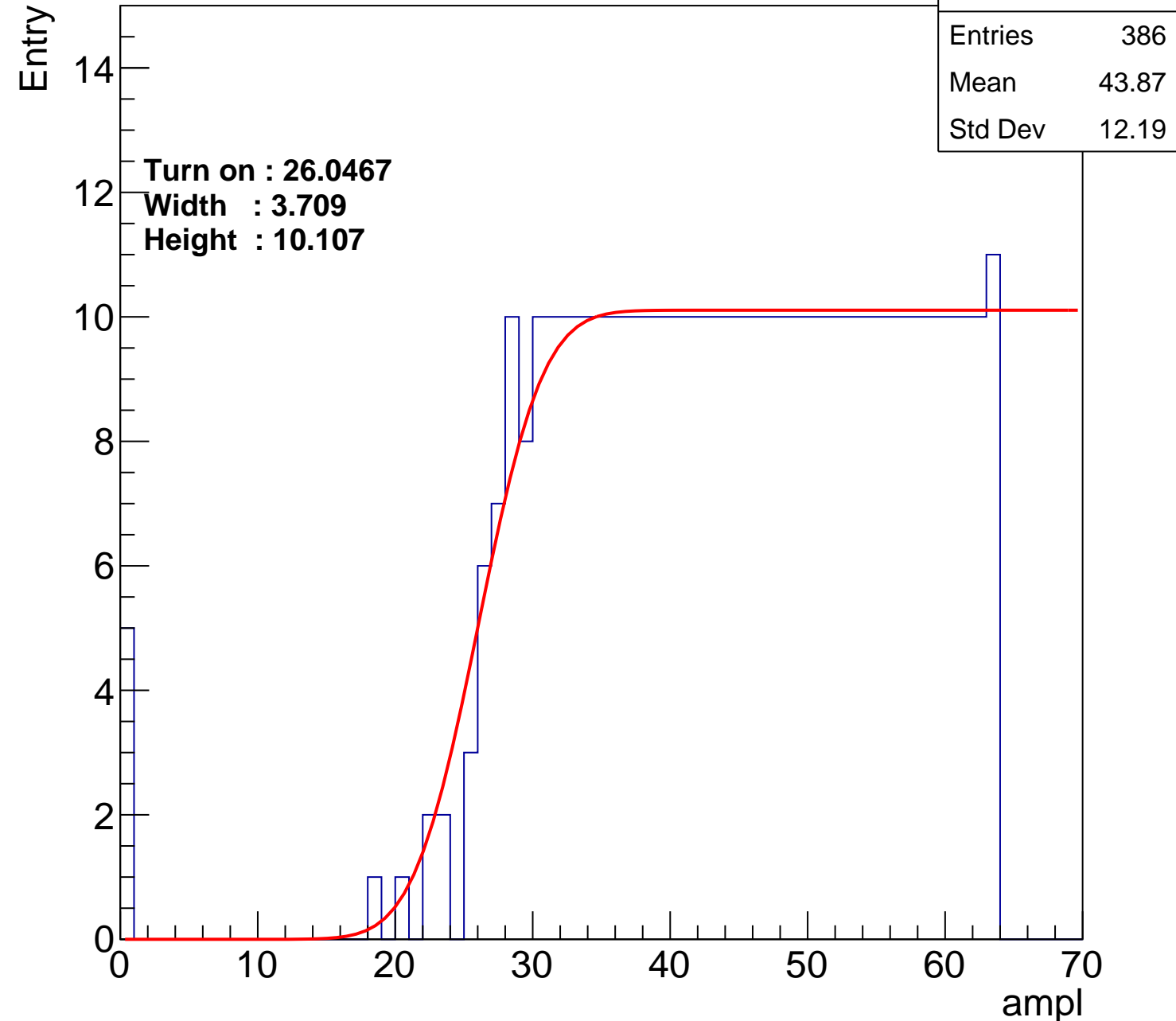
Width : 3.709

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.02
Std Dev	11.79

Turn on : 26.6378

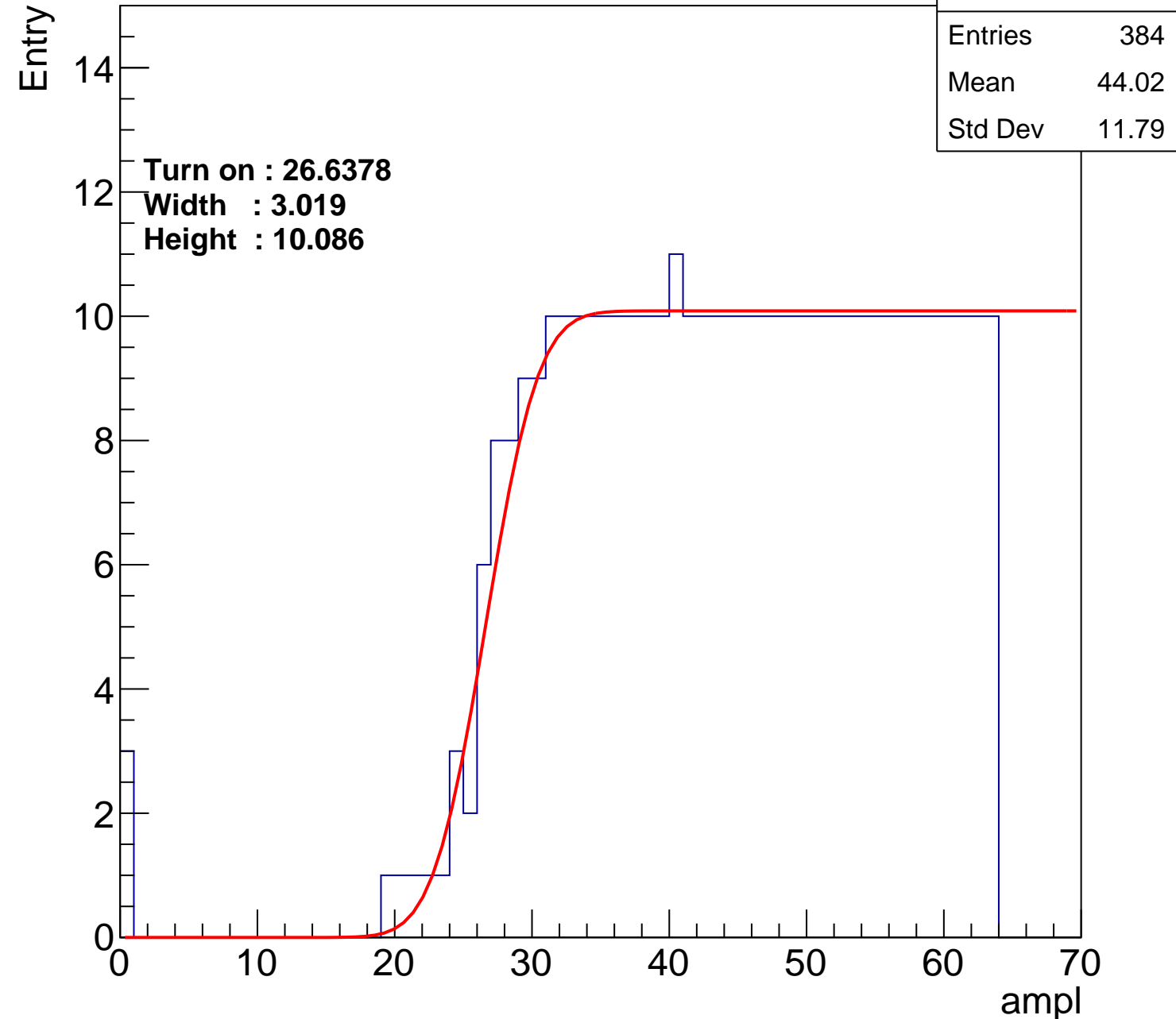
Width : 3.019

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch38

calib_packv5_042523_0143.root, FC#12, port B1

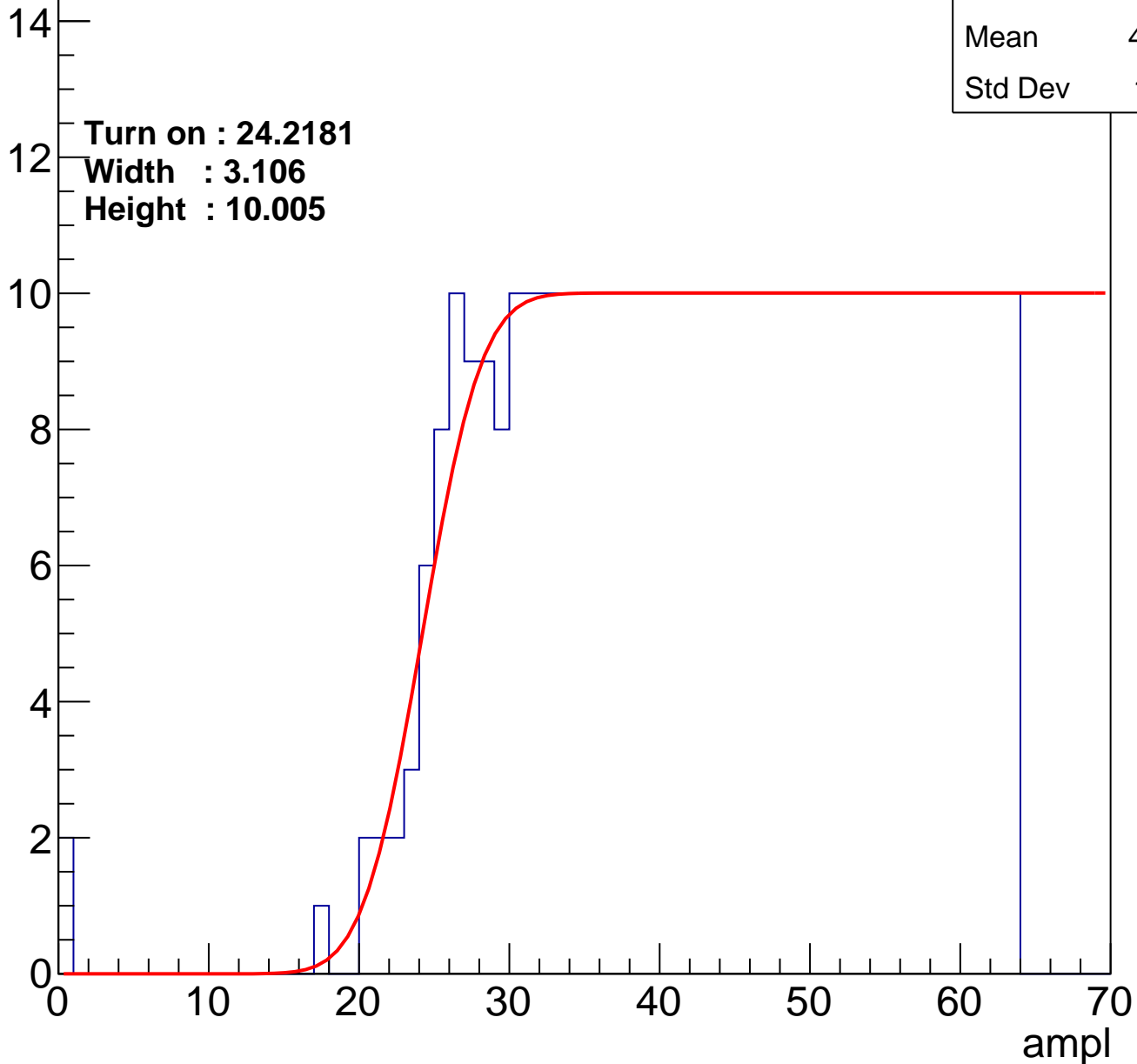
Entries	402
Mean	43.17
Std Dev	12.11

Turn on : 24.2181

Width : 3.106

Height : 10.005

Entry



B0L102S, U15-ch39

calib_packv5_042523_0143.root, FC#12, port B1

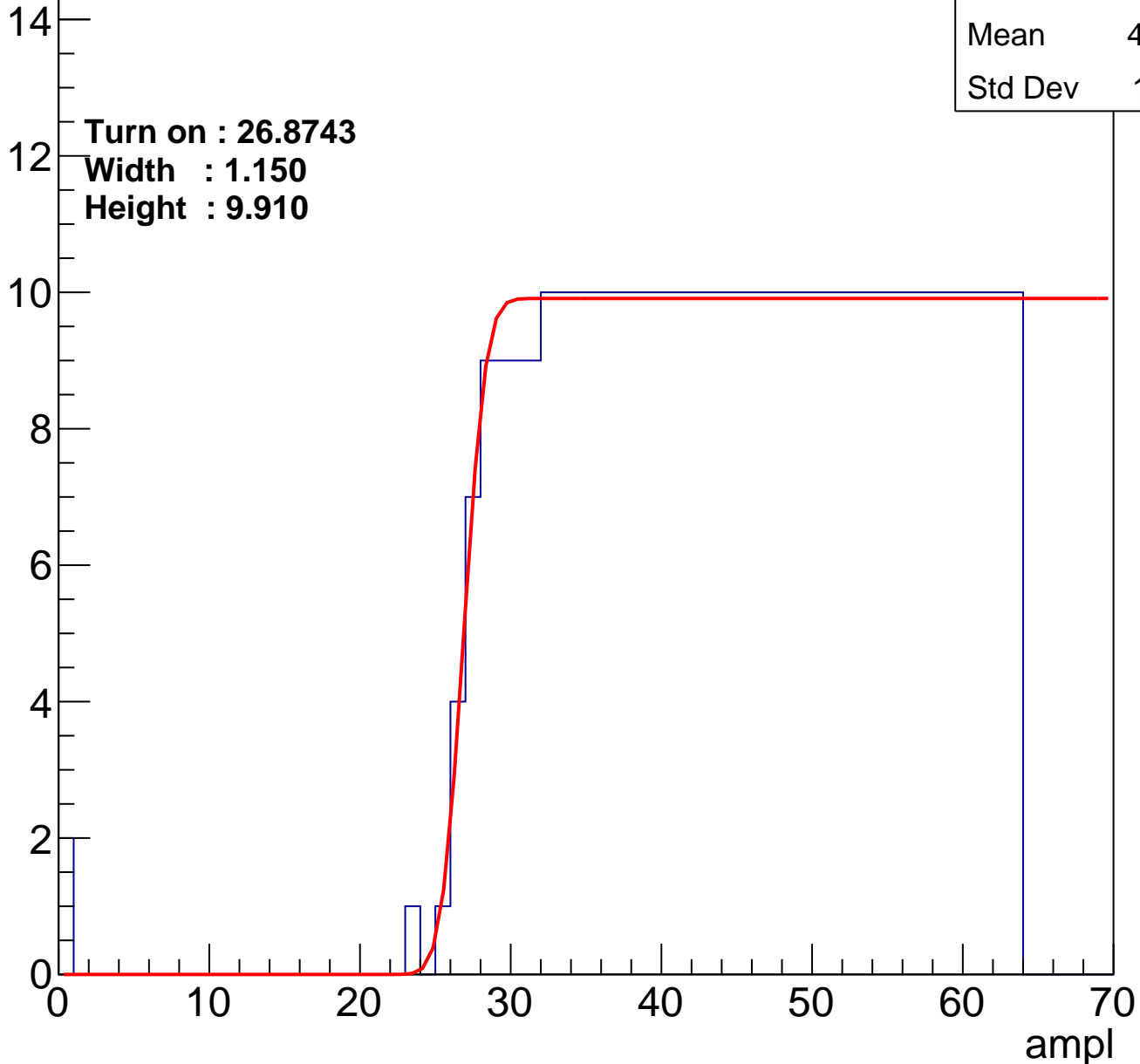
Entries	371
Mean	44.75
Std Dev	11.21

Turn on : 26.8743

Width : 1.150

Height : 9.910

Entry



B0L102S, U15-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.04
Std Dev	12.73

Turn on : 25.3806

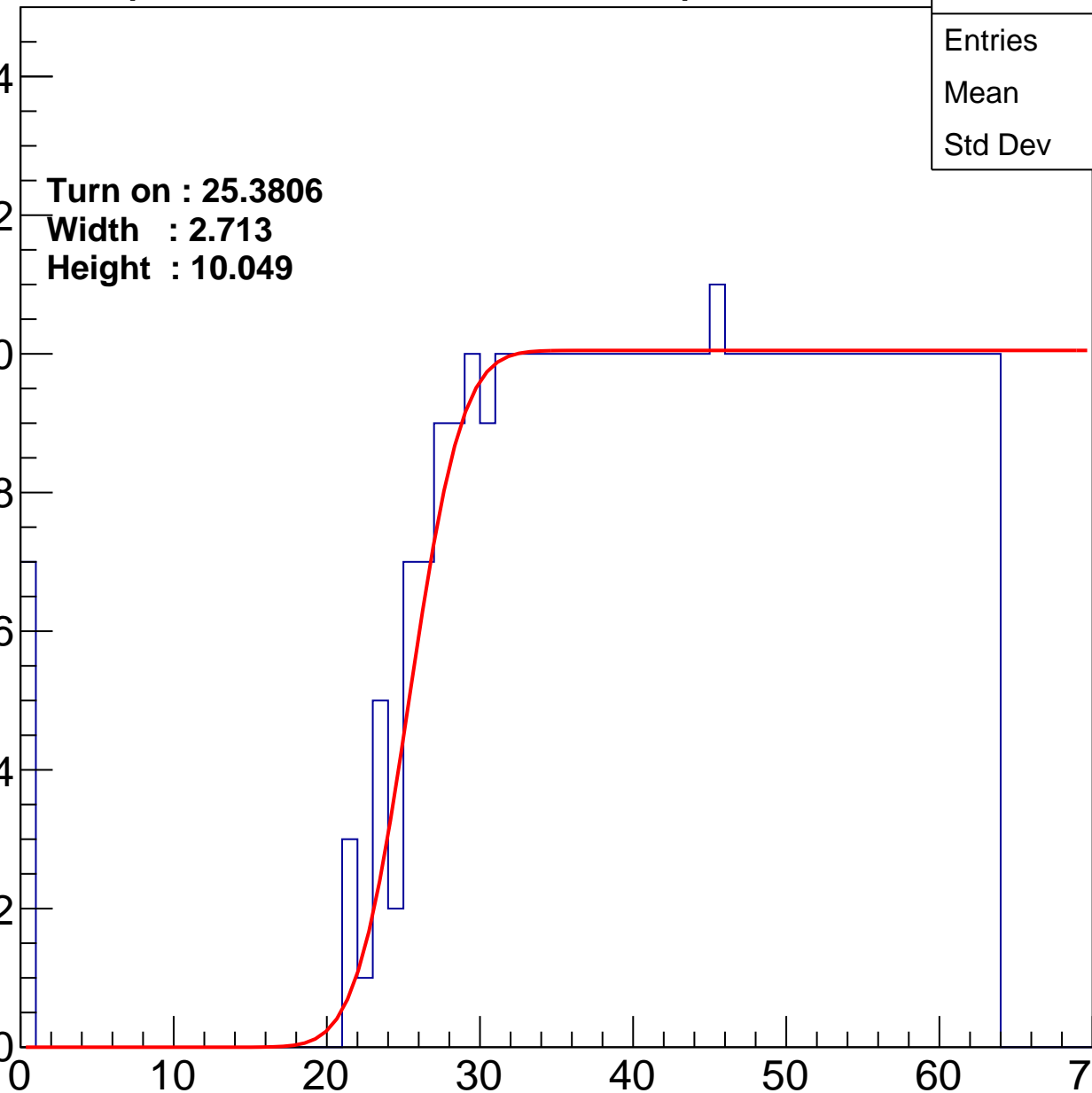
Width : 2.713

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch41

calib_packv5_042523_0143.root, FC#12, port B1

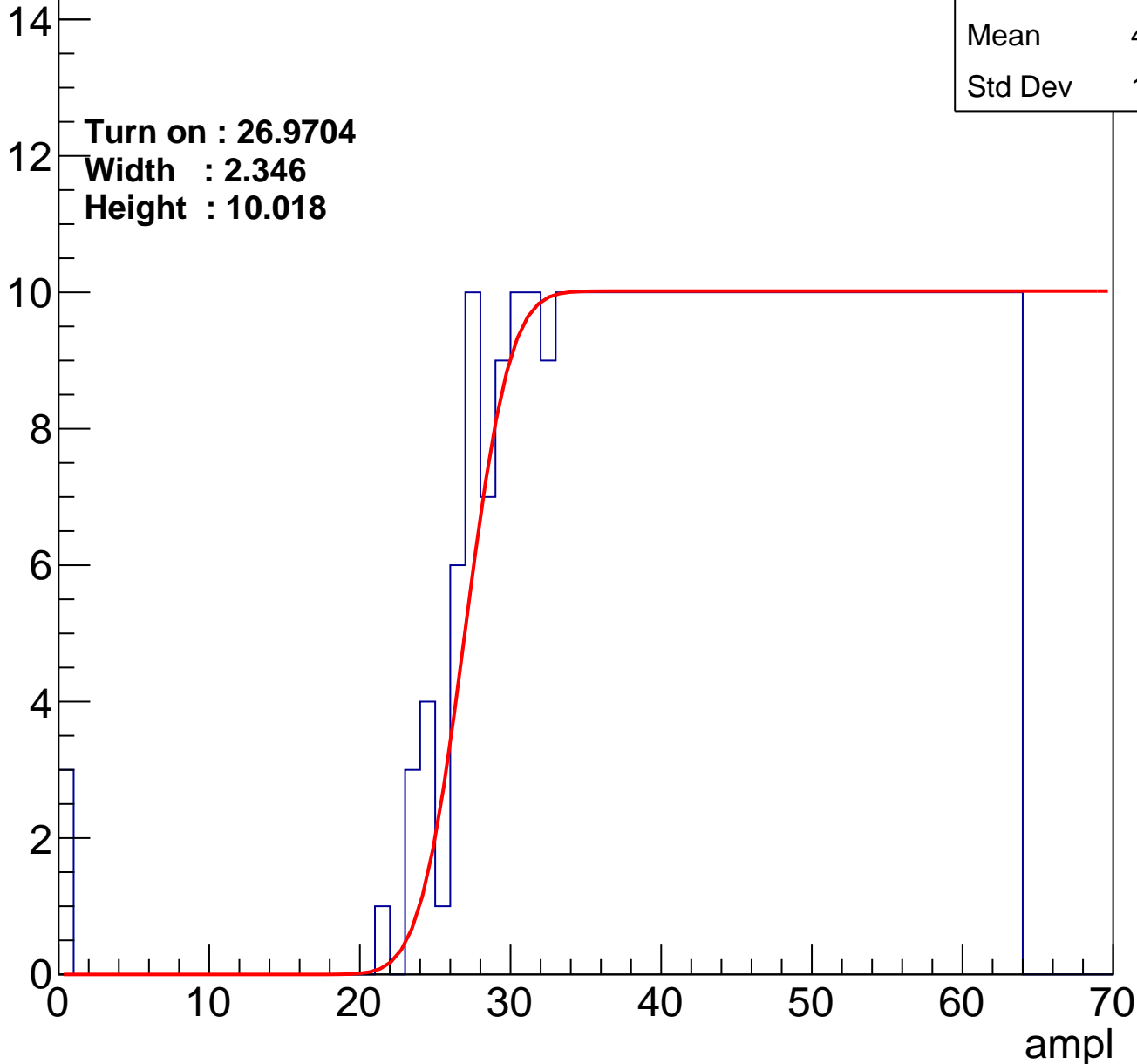
Entries	383
Mean	44.05
Std Dev	11.76

Turn on : 26.9704

Width : 2.346

Height : 10.018

Entry



B0L102S, U15-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.65
Std Dev	11.64

Turn on : 28.3697

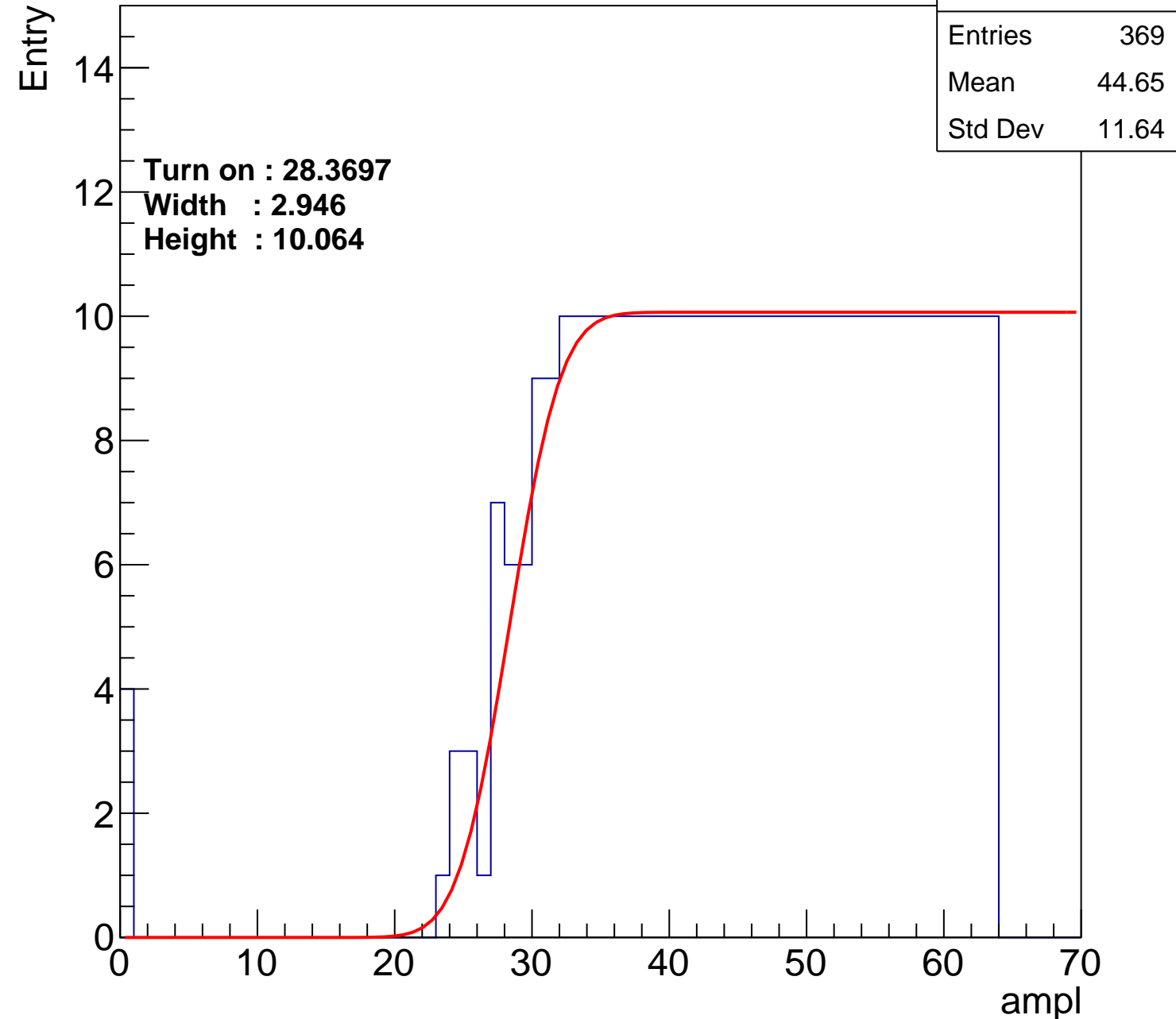
Width : 2.946

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.15
Std Dev	11.76

Turn on : 26.3218

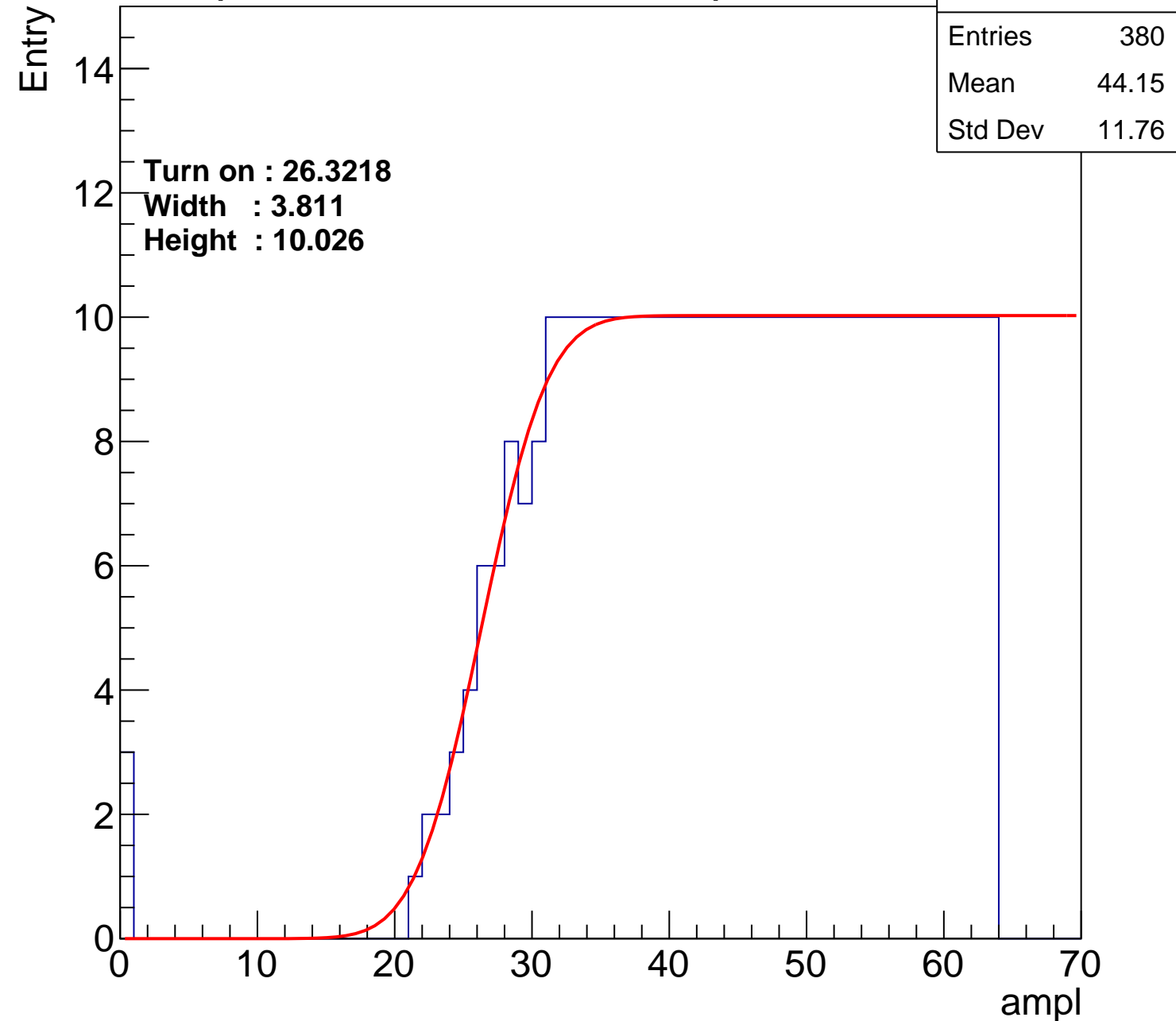
Width : 3.811

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch44

calib_packv5_042523_0143.root, FC#12, port B1

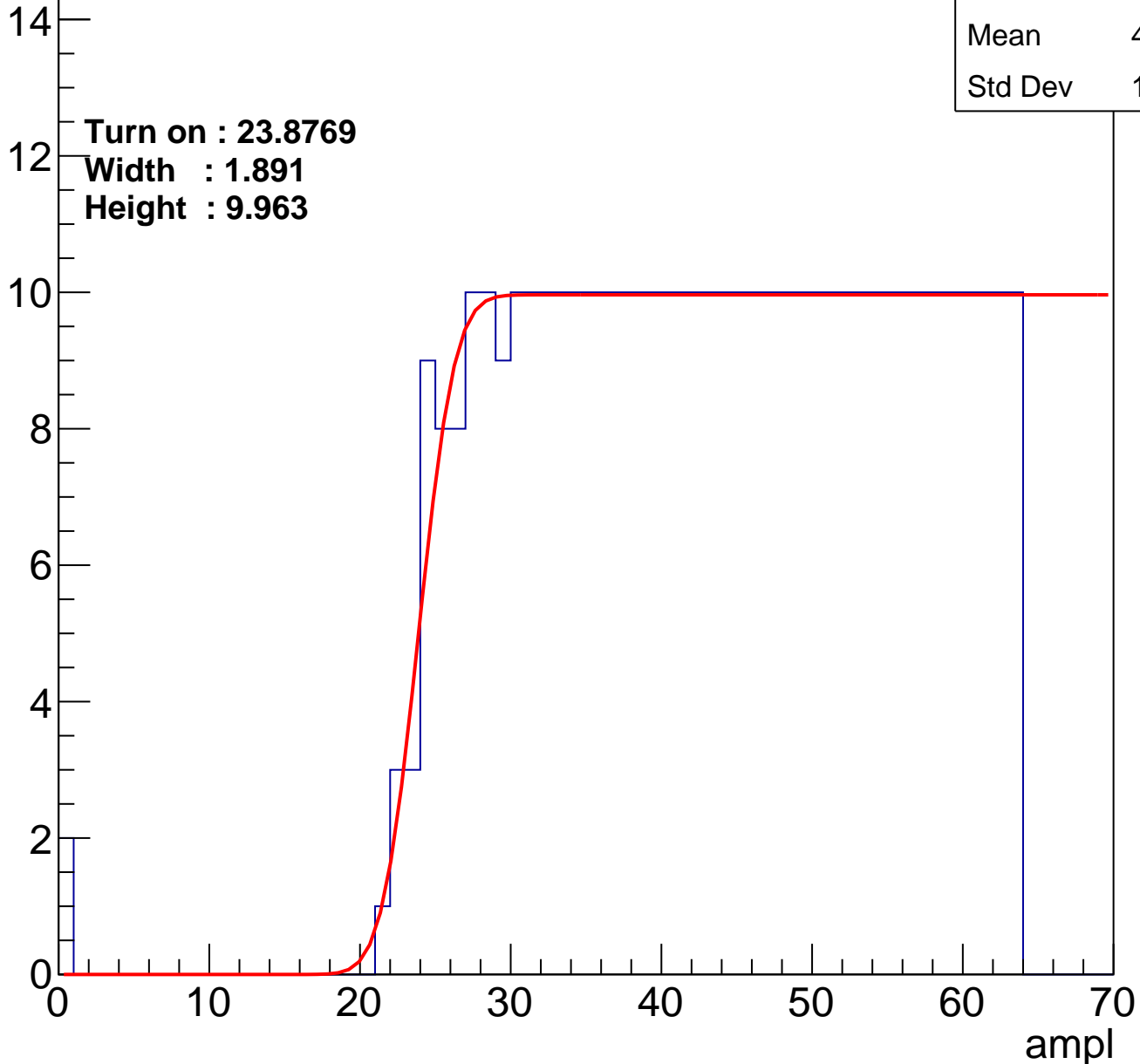
Entries	403
Mean	43.18
Std Dev	12.03

Turn on : 23.8769

Width : 1.891

Height : 9.963

Entry



B0L102S, U15-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.56
Std Dev	11.46

Turn on : 27.0482

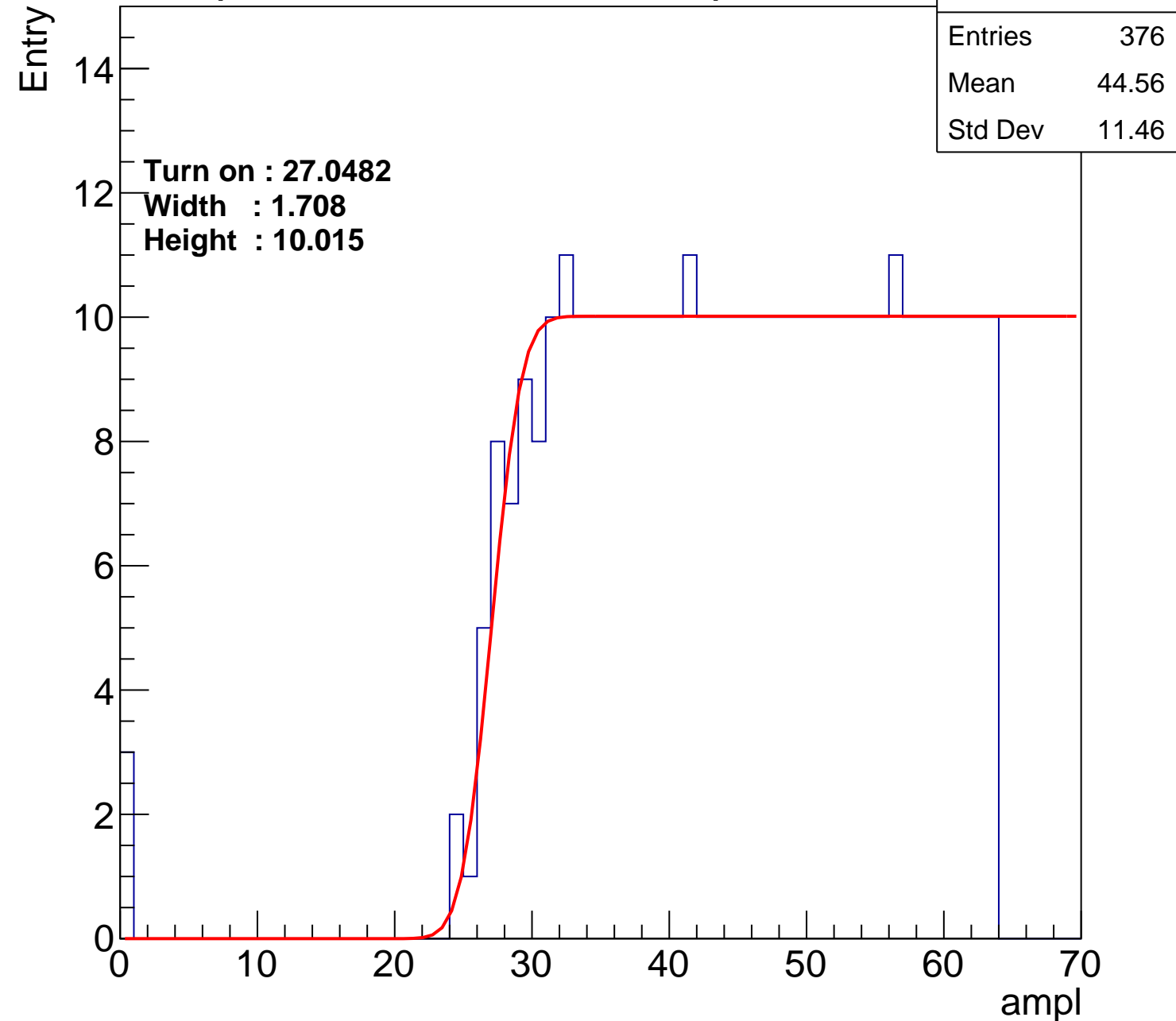
Width : 1.708

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.33
Std Dev	11.99

Turn on : 24.6049

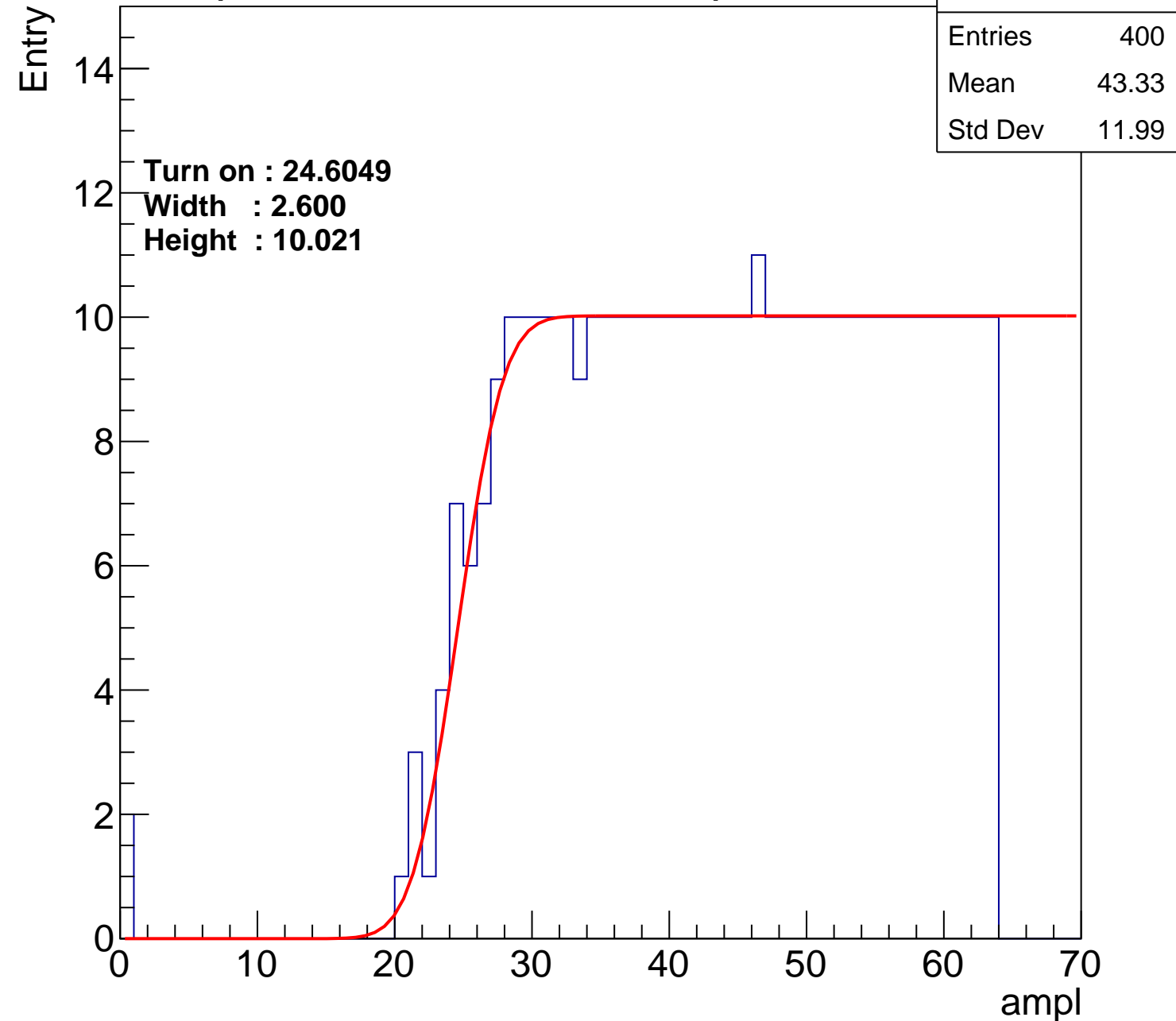
Width : 2.600

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch47

calib_packv5_042523_0143.root, FC#12, port B1

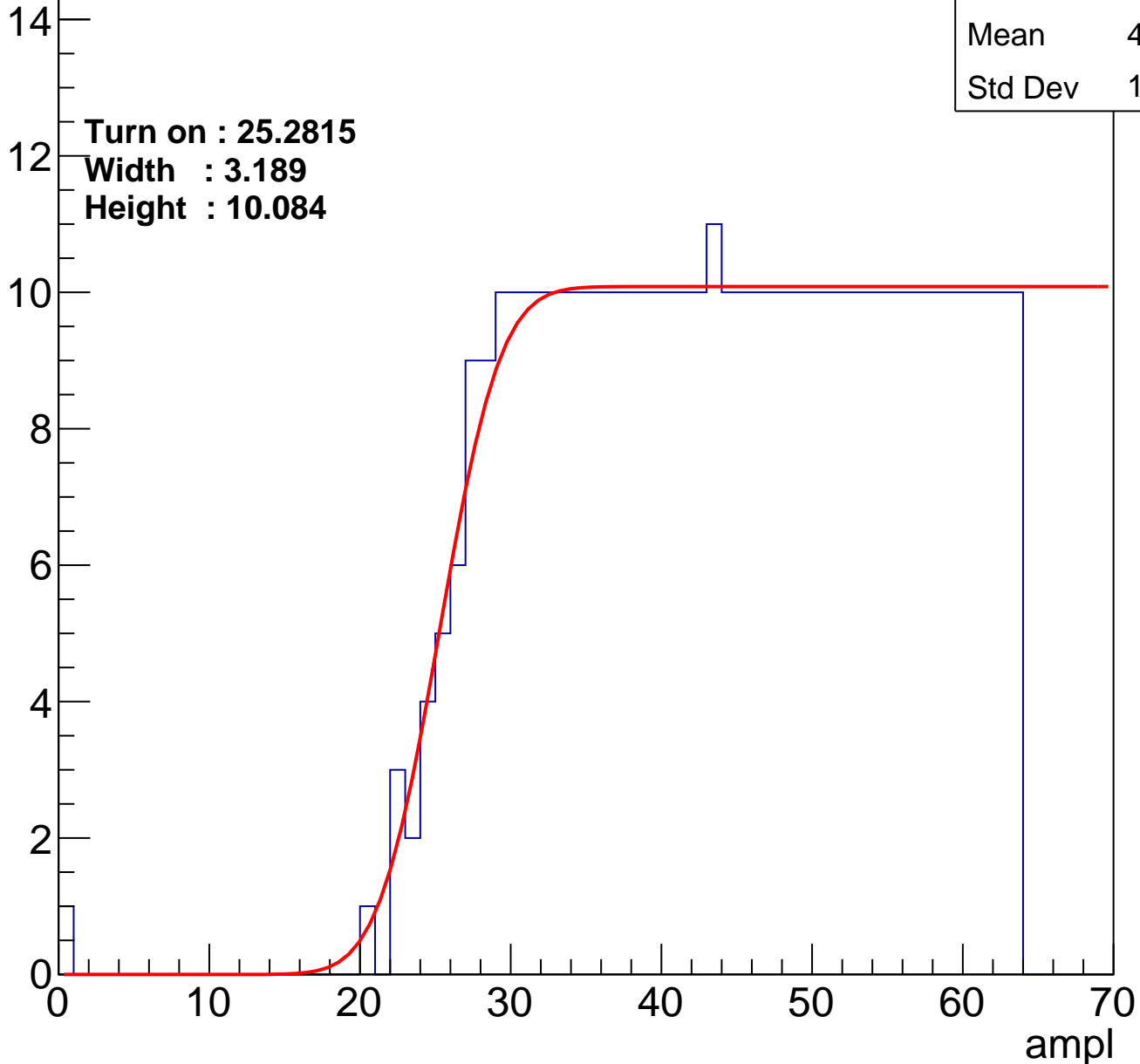
Entries	391
Mean	43.85
Std Dev	11.55

Turn on : 25.2815

Width : 3.189

Height : 10.084

Entry



B0L102S, U15-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 25.6769

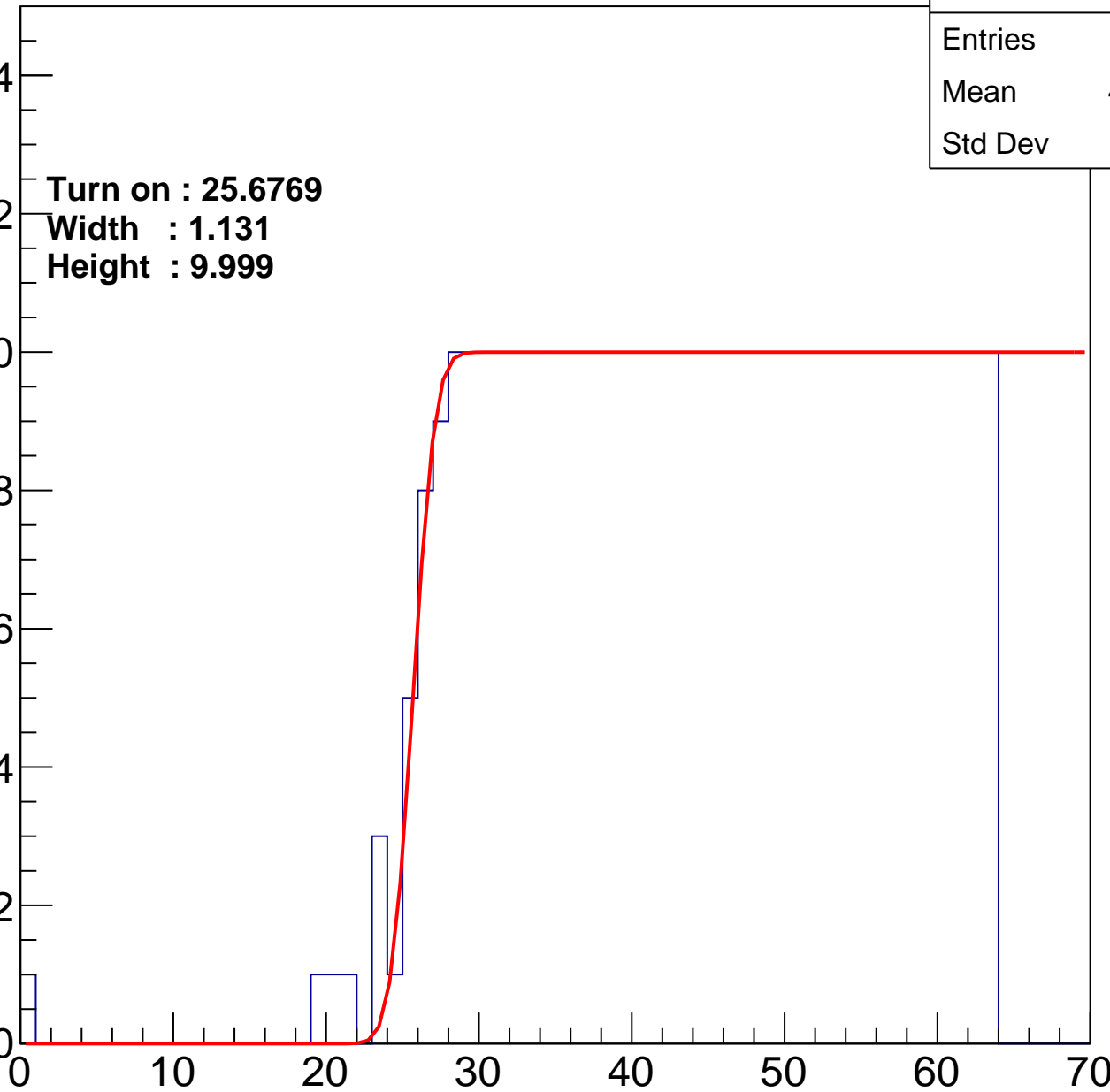
Width : 1.131

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.13
Std Dev	12.32

Turn on : 24.2727

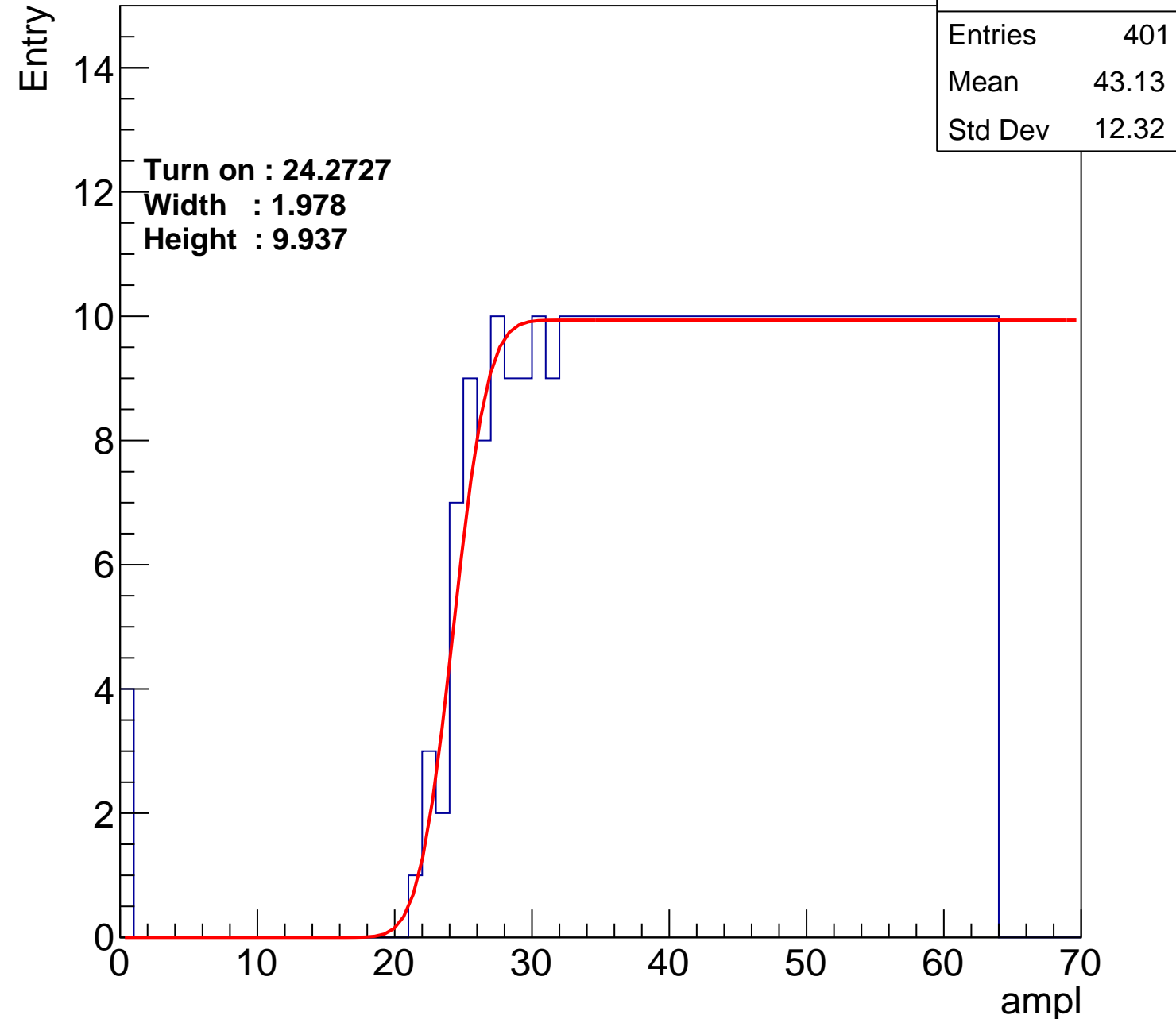
Width : 1.978

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 26.1038

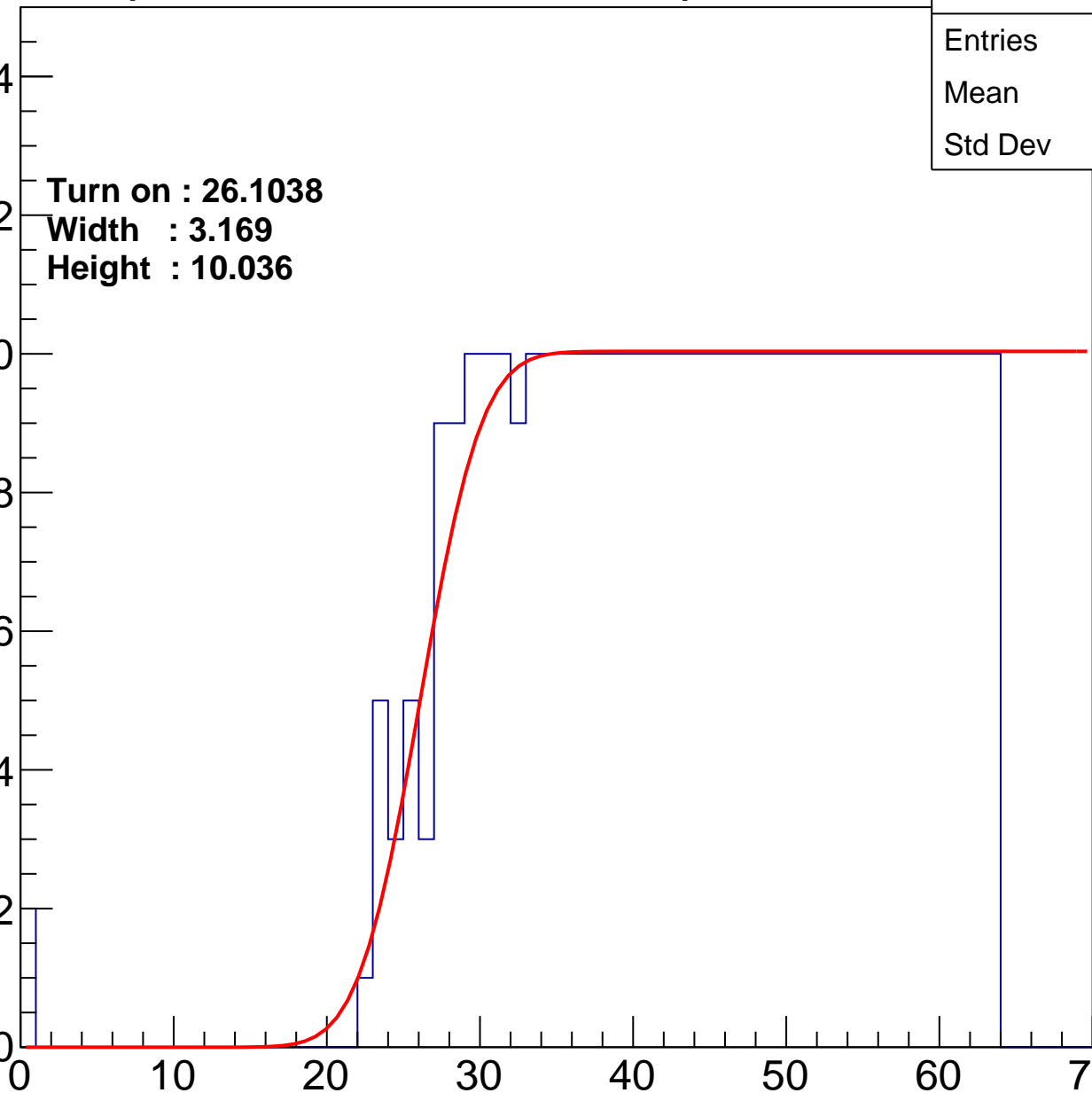
Width : 3.169

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.18
Std Dev	11.8

Turn on : 27.1188

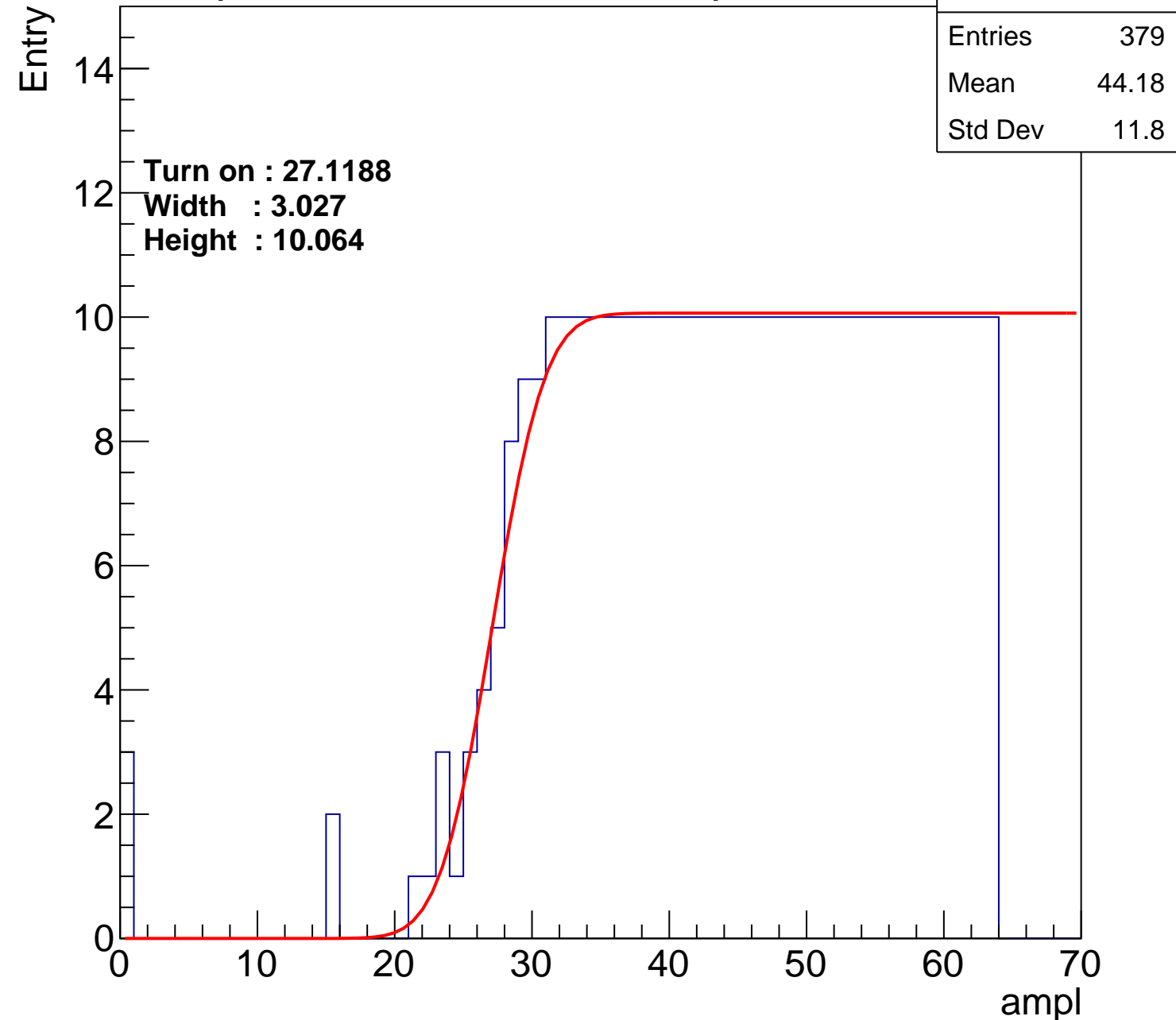
Width : 3.027

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 26.1573

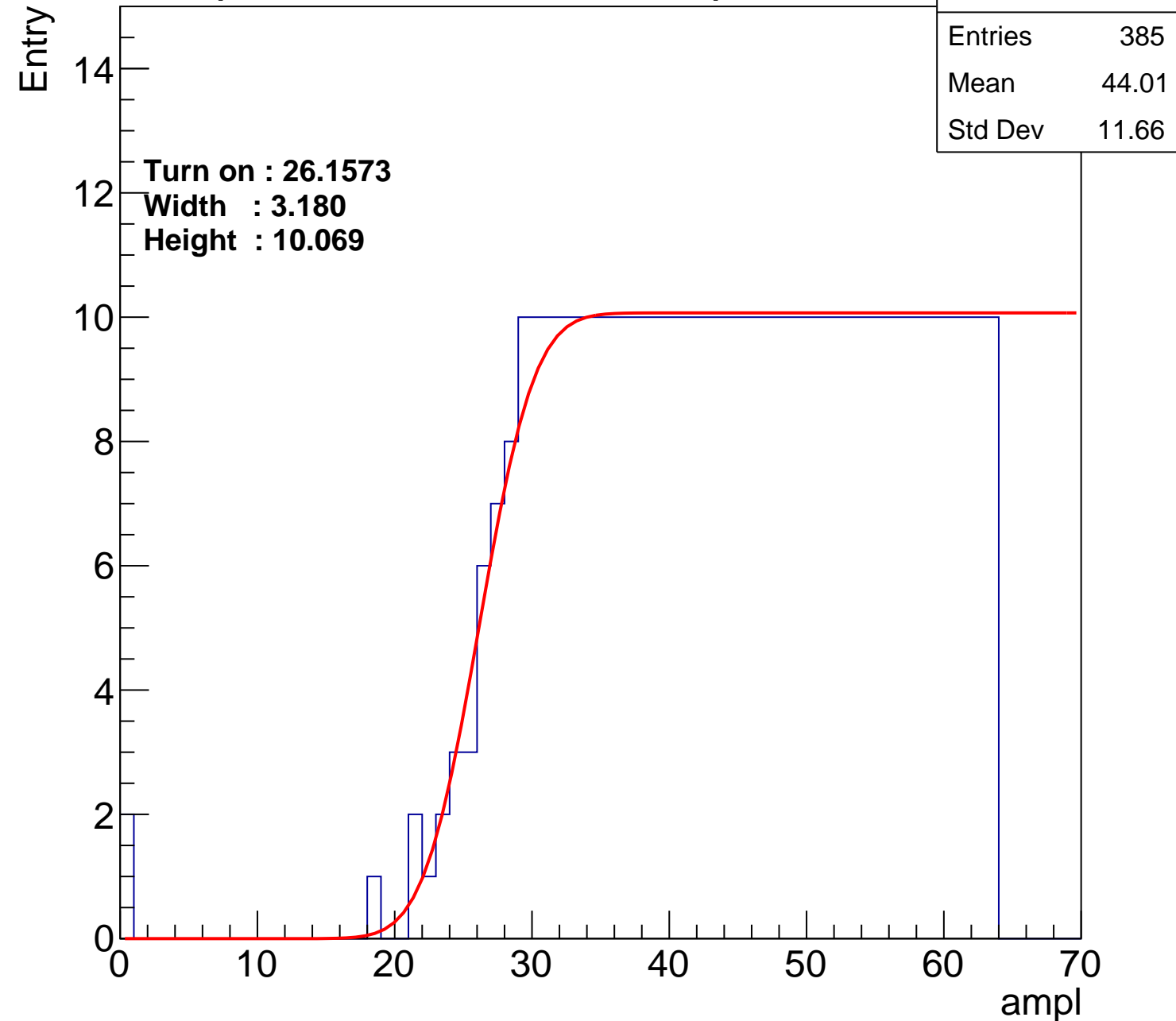
Width : 3.180

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.5
Std Dev	11.41

Turn on : 27.4784

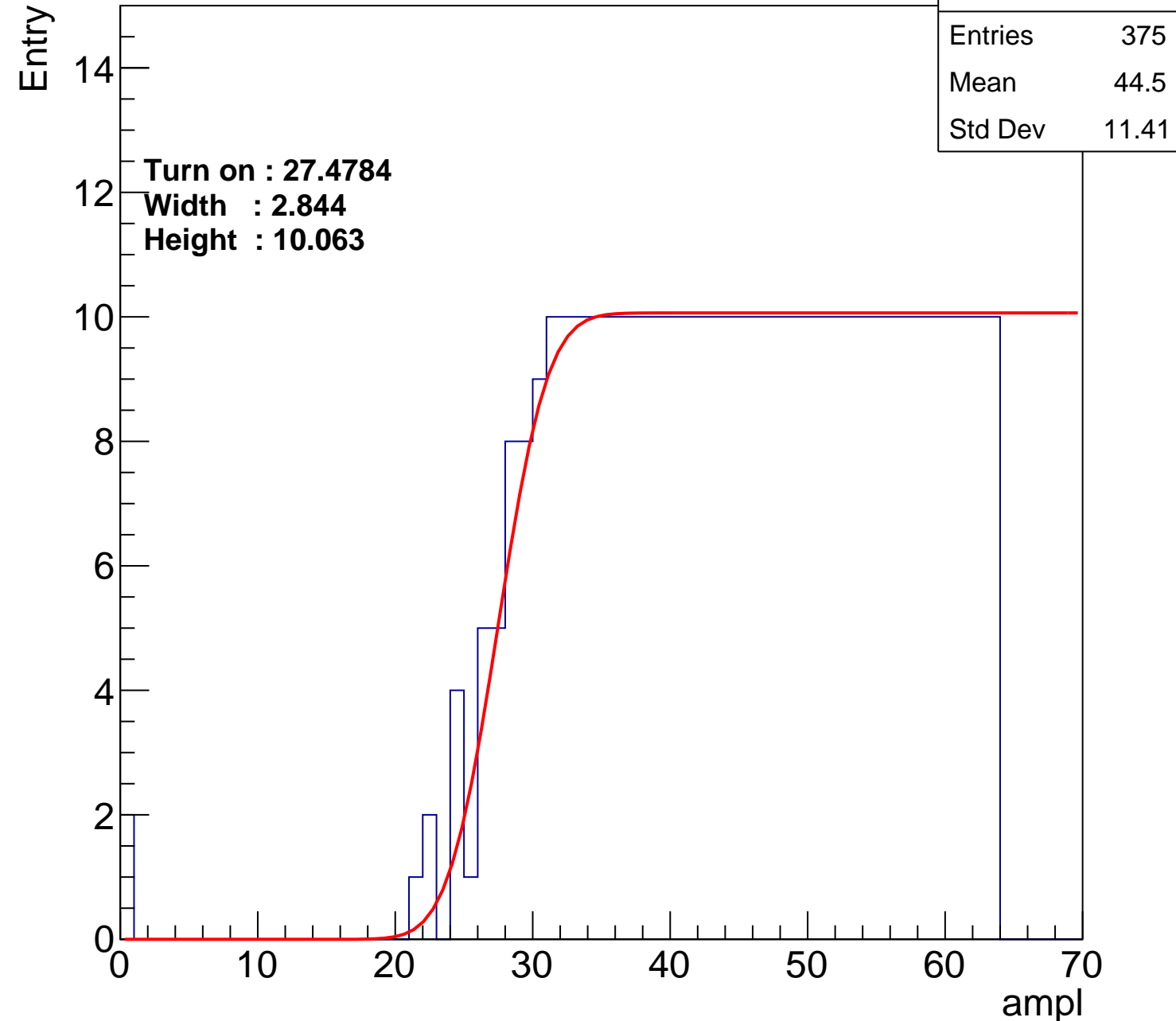
Width : 2.844

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.25
Std Dev	11.54

Turn on : 26.4440

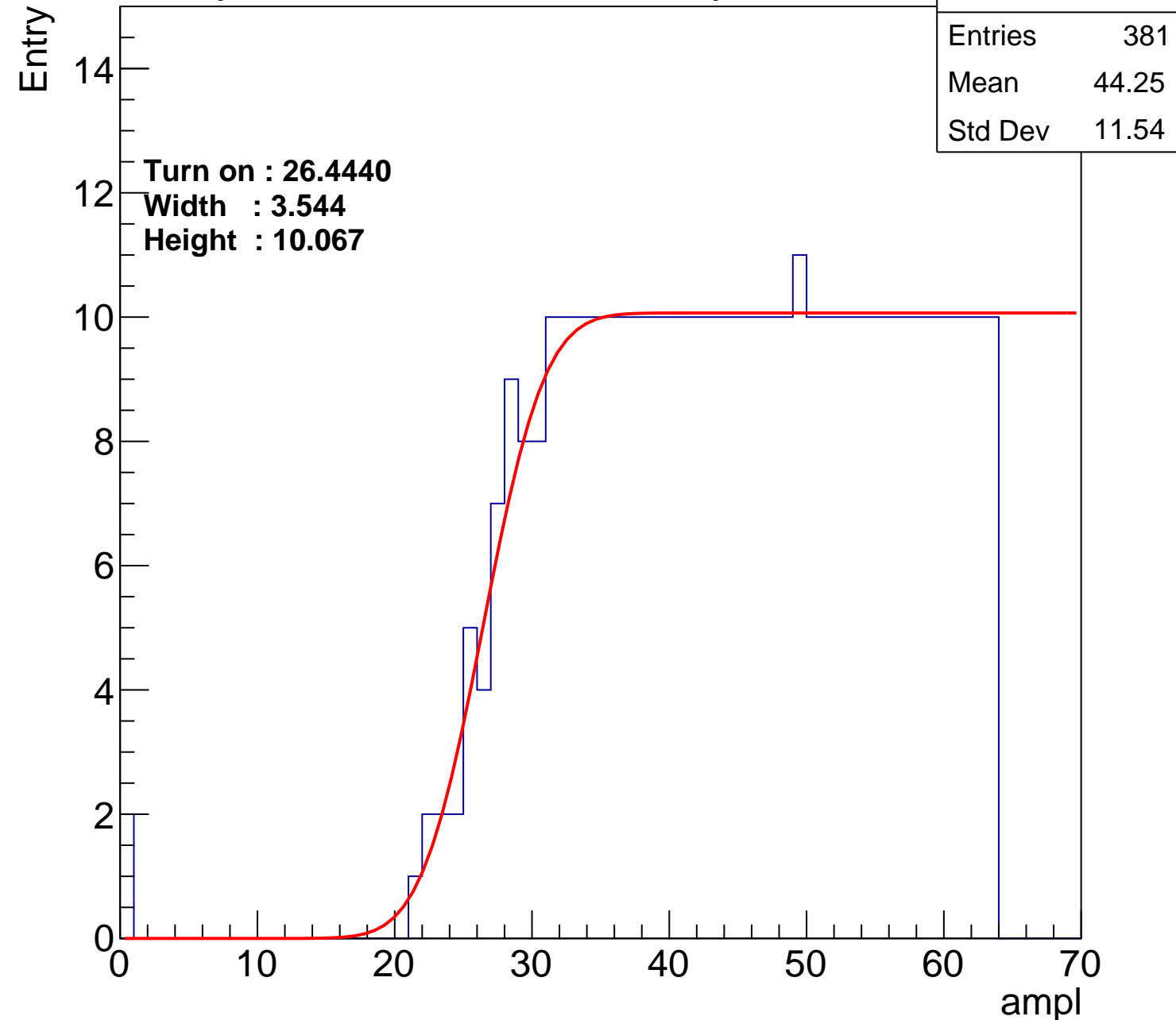
Width : 3.544

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entry

14

12

10

8

6

4

2

0

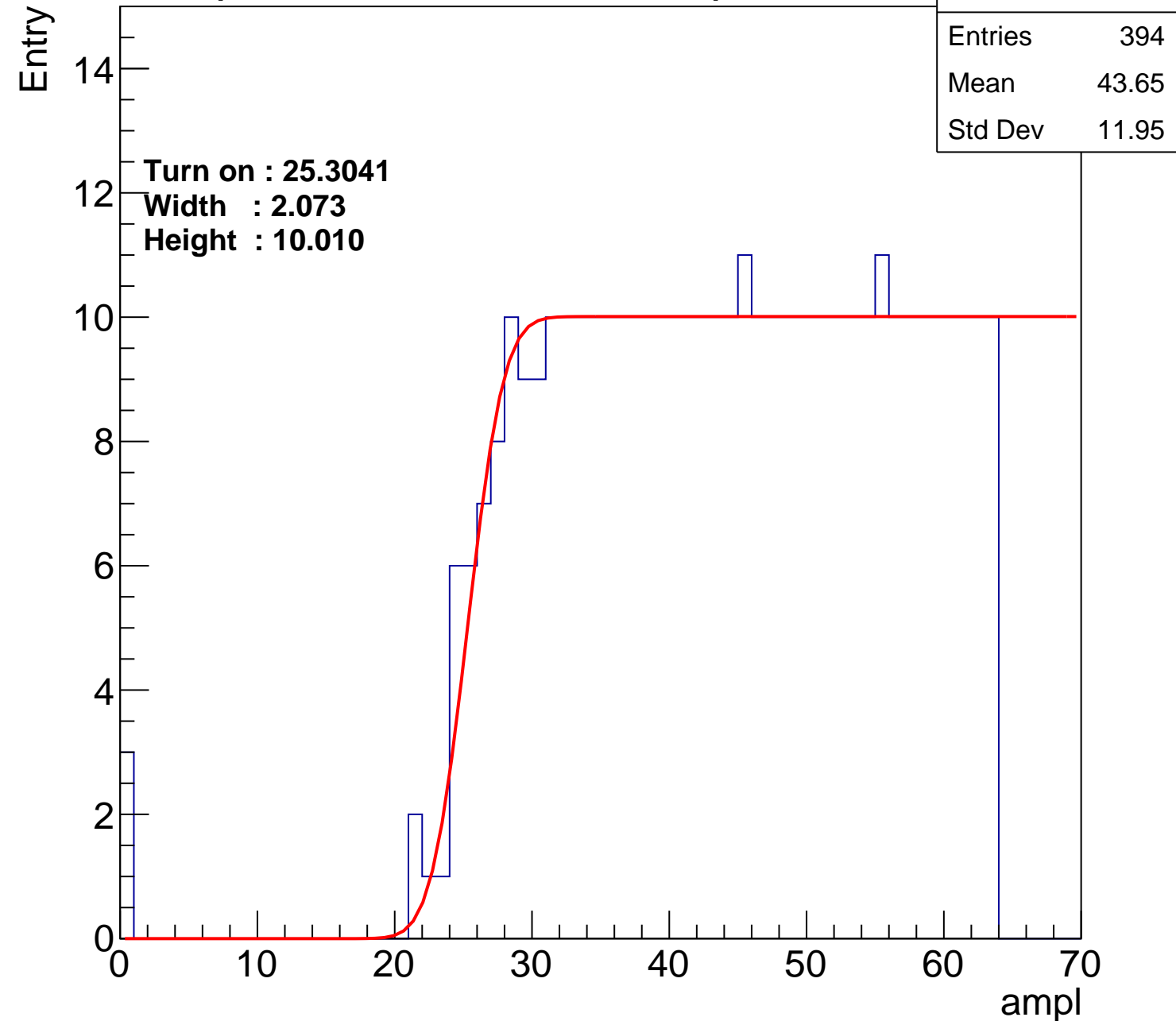
Turn on : 25.3041

Width : 2.073

Height : 10.010

Entries	394
Mean	43.65
Std Dev	11.95

ampl



B0L102S, U15-ch56

calib_packv5_042523_0143.root, FC#12, port B1

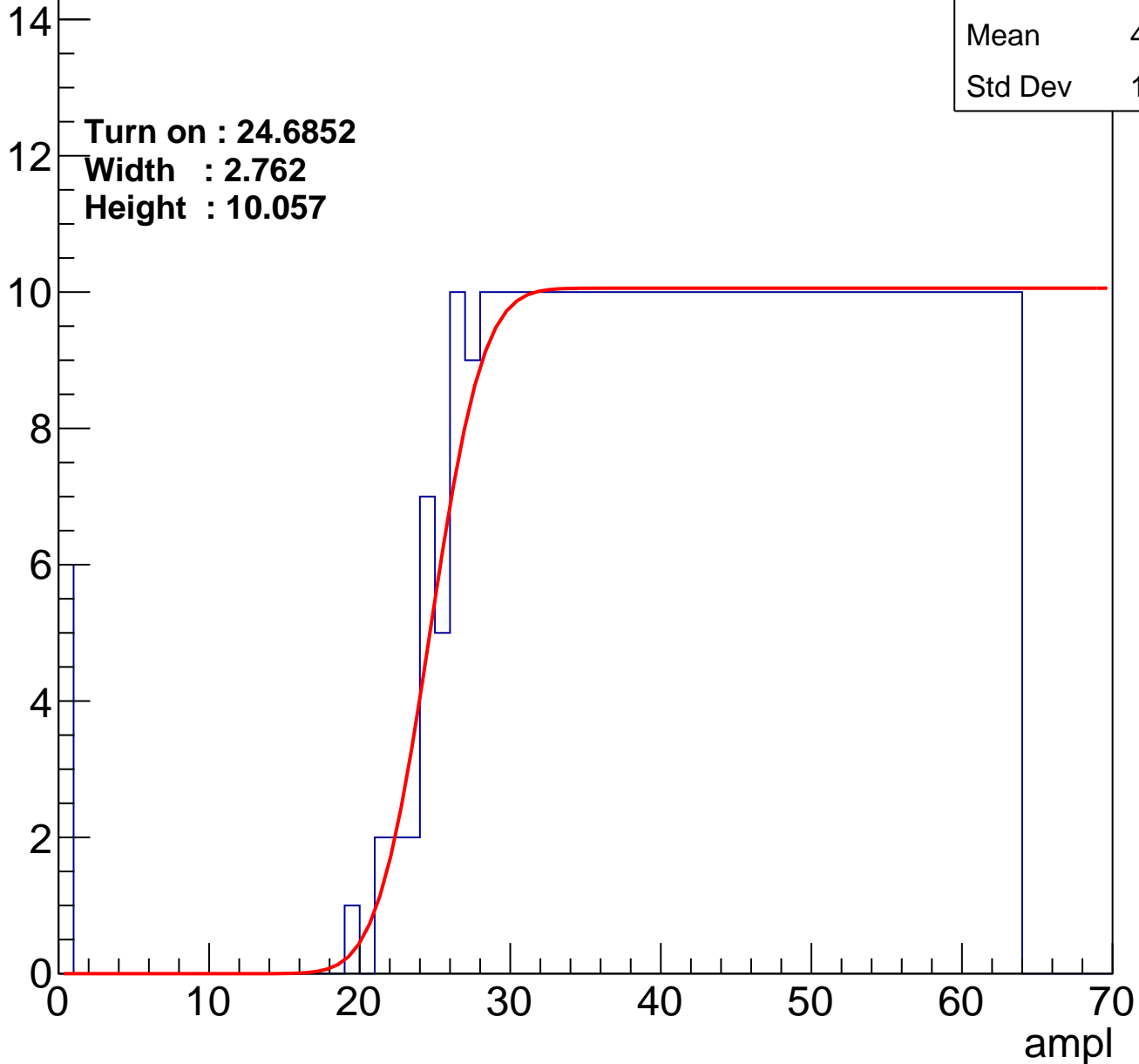
Entries	404
Mean	42.89
Std Dev	12.66

Turn on : 24.6852

Width : 2.762

Height : 10.057

Entry



B0L102S, U15-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	44.26
Std Dev	11.47

Turn on : 26.1893

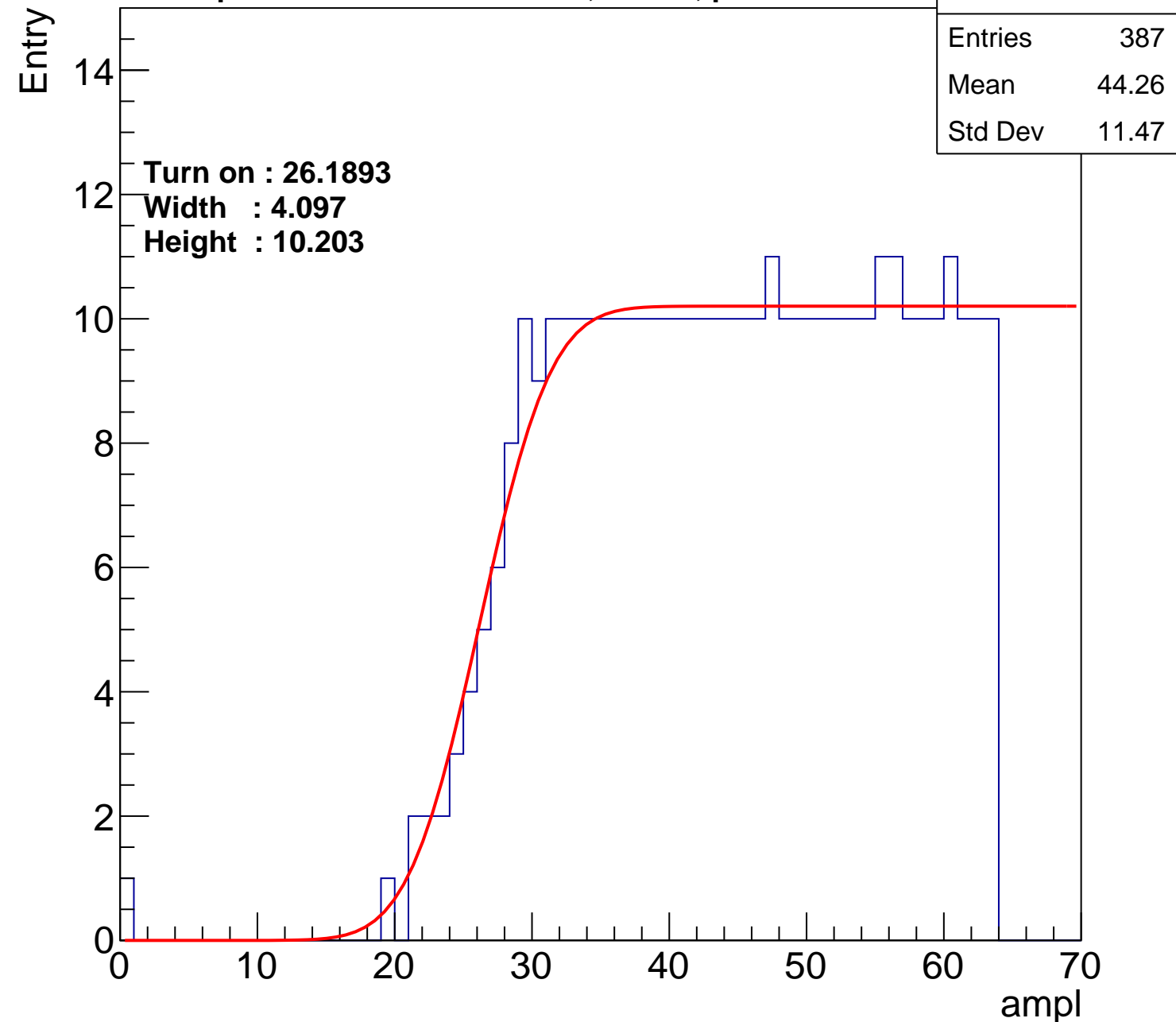
Width : 4.097

Height : 10.203

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.36
Std Dev	12.35

Turn on : 25.1267

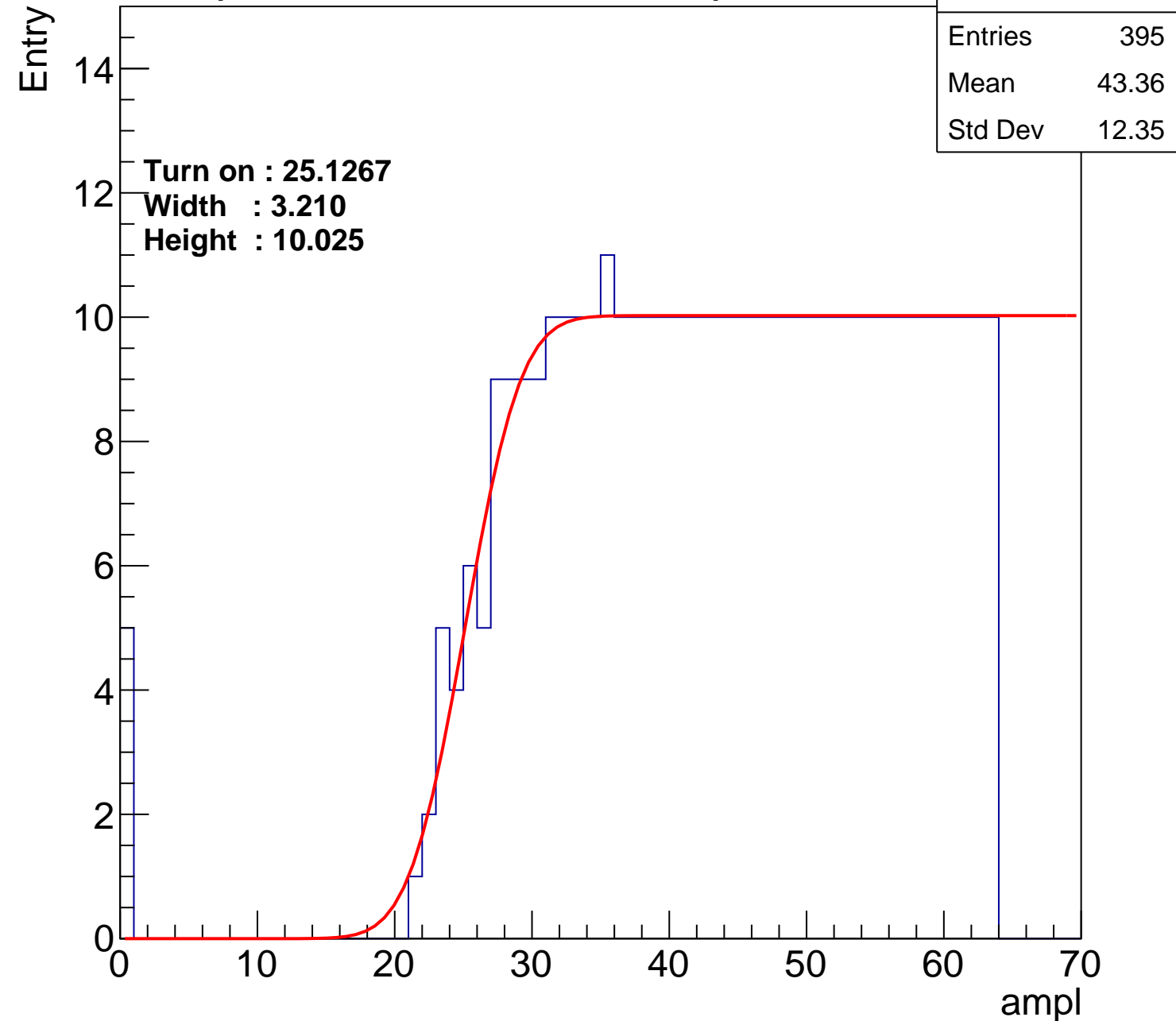
Width : 3.210

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch59

calib_packv5_042523_0143.root, FC#12, port B1

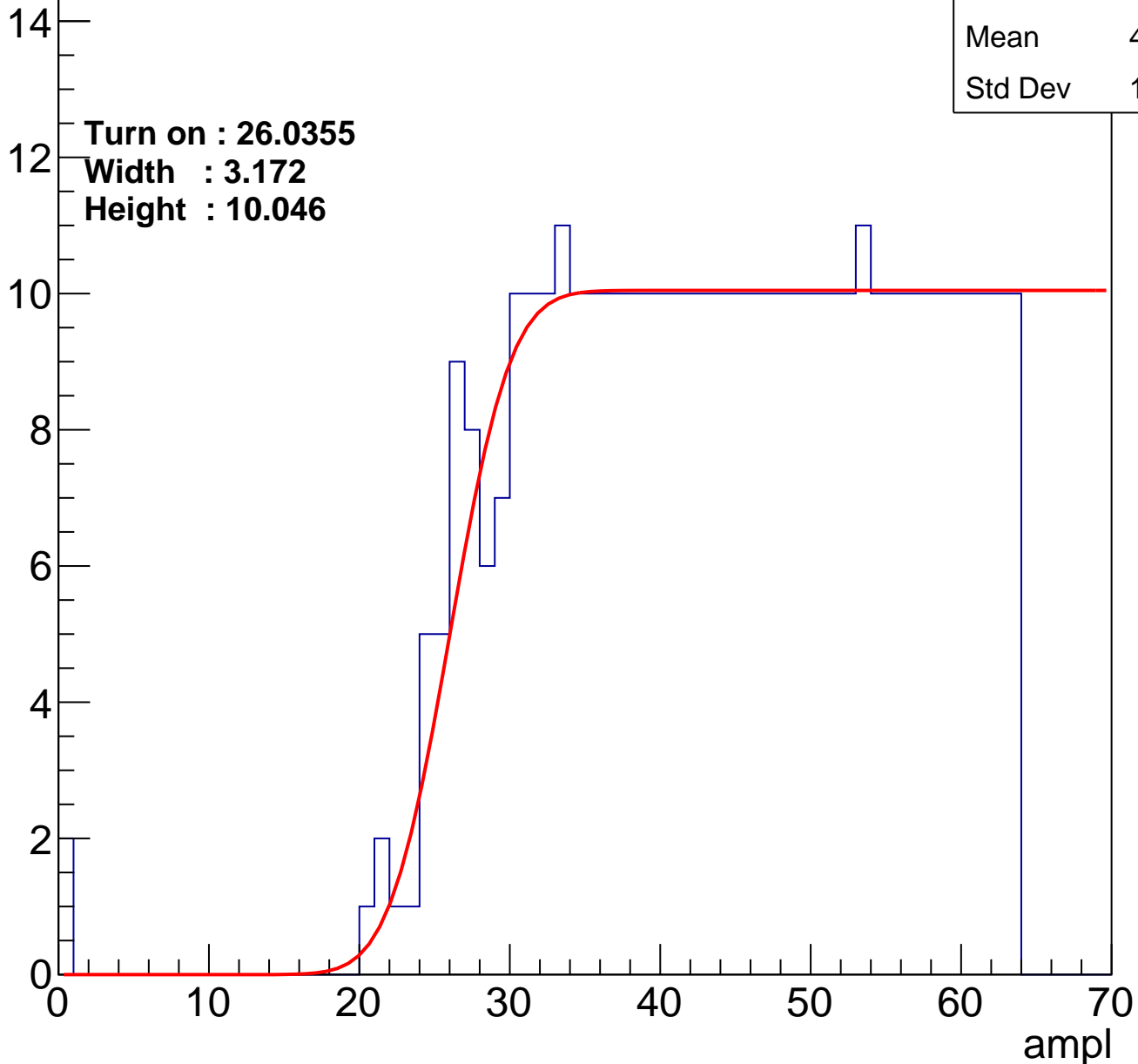
Entries	389
Mean	43.88
Std Dev	11.74

Turn on : 26.0355

Width : 3.172

Height : 10.046

Entry



B0L102S, U15-ch60

calib_packv5_042523_0143.root, FC#12, port B1

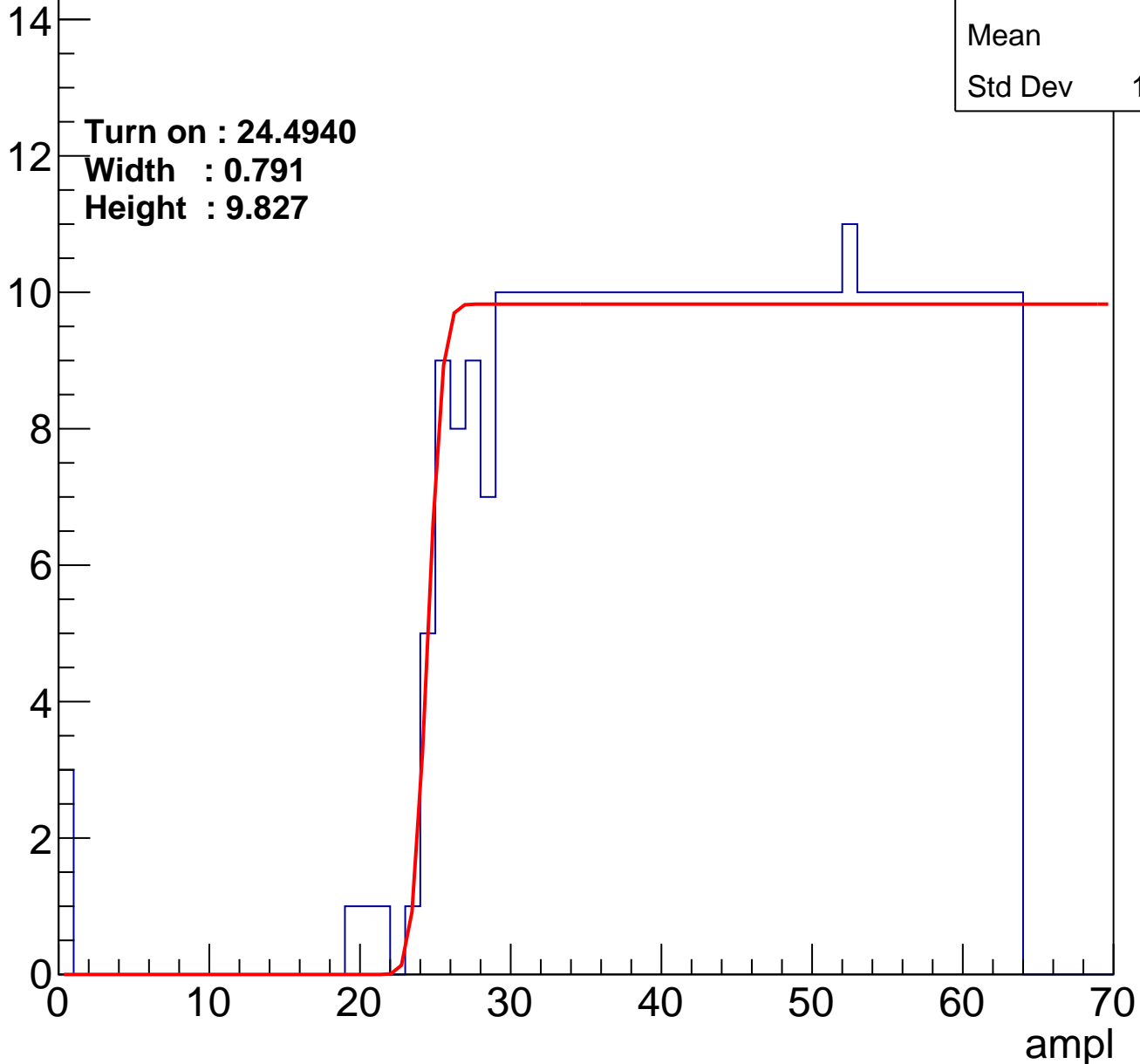
Entries	396
Mean	43.5
Std Dev	12.03

Turn on : 24.4940

Width : 0.791

Height : 9.827

Entry



B0L102S, U15-ch61

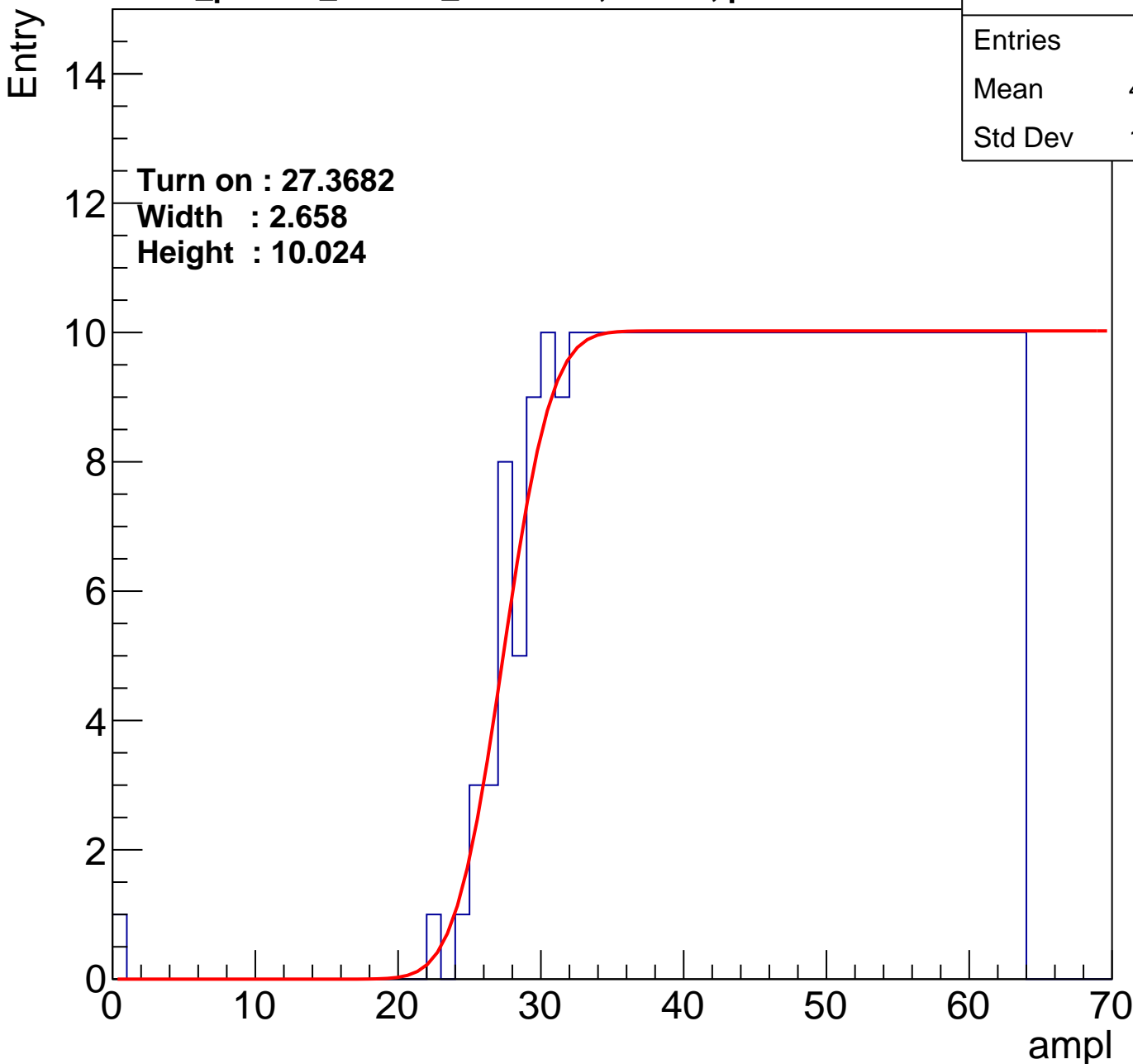
calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 27.3682

Width : 2.658

Height : 10.024



B0L102S, U15-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
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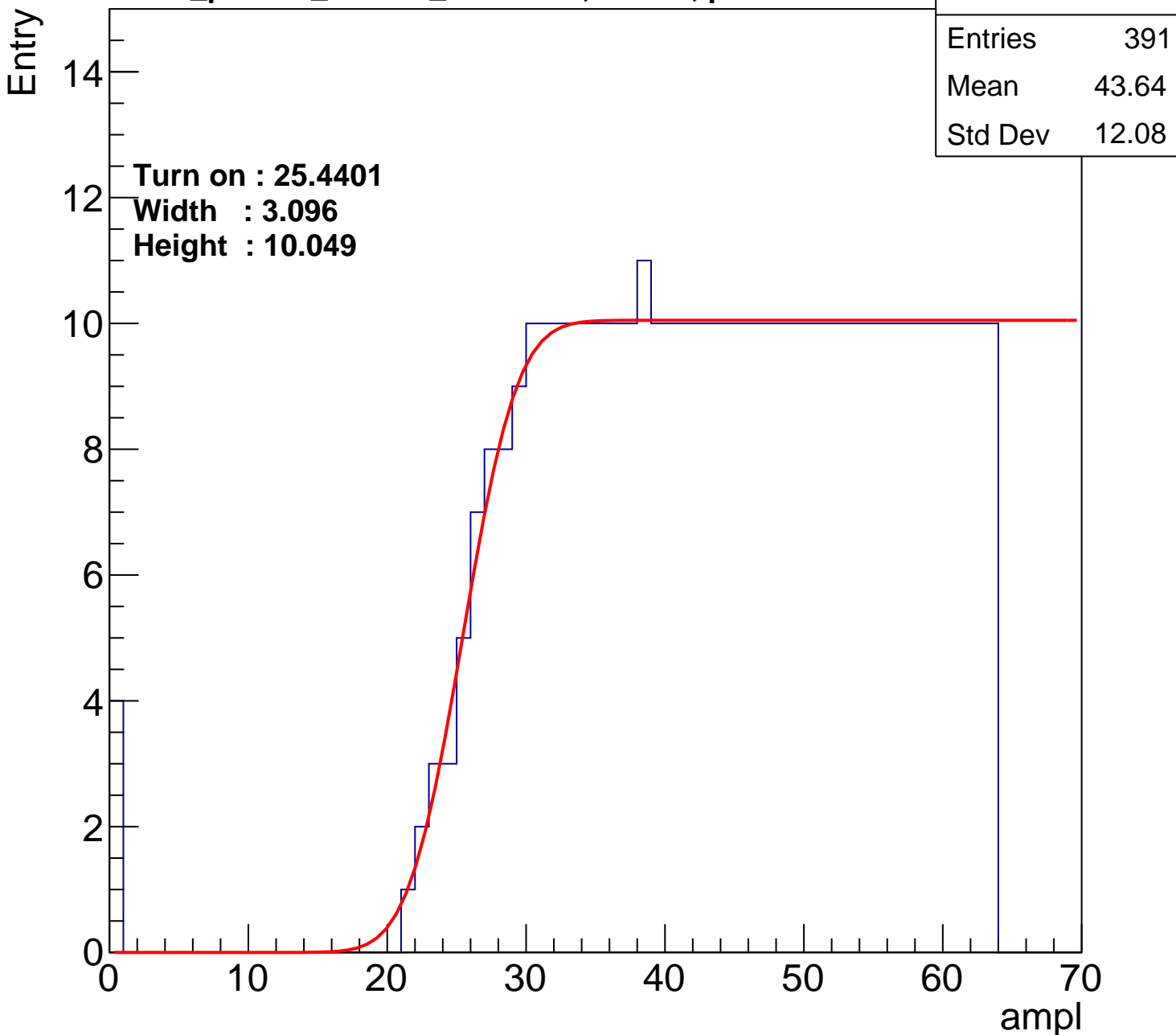
Mean	43.64
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Std Dev	12.08
---------	-------

Turn on : 25.4401

Width : 3.096

Height : 10.049



B0L102S, U15-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.78
Std Dev	11.81

Turn on : 25.5847

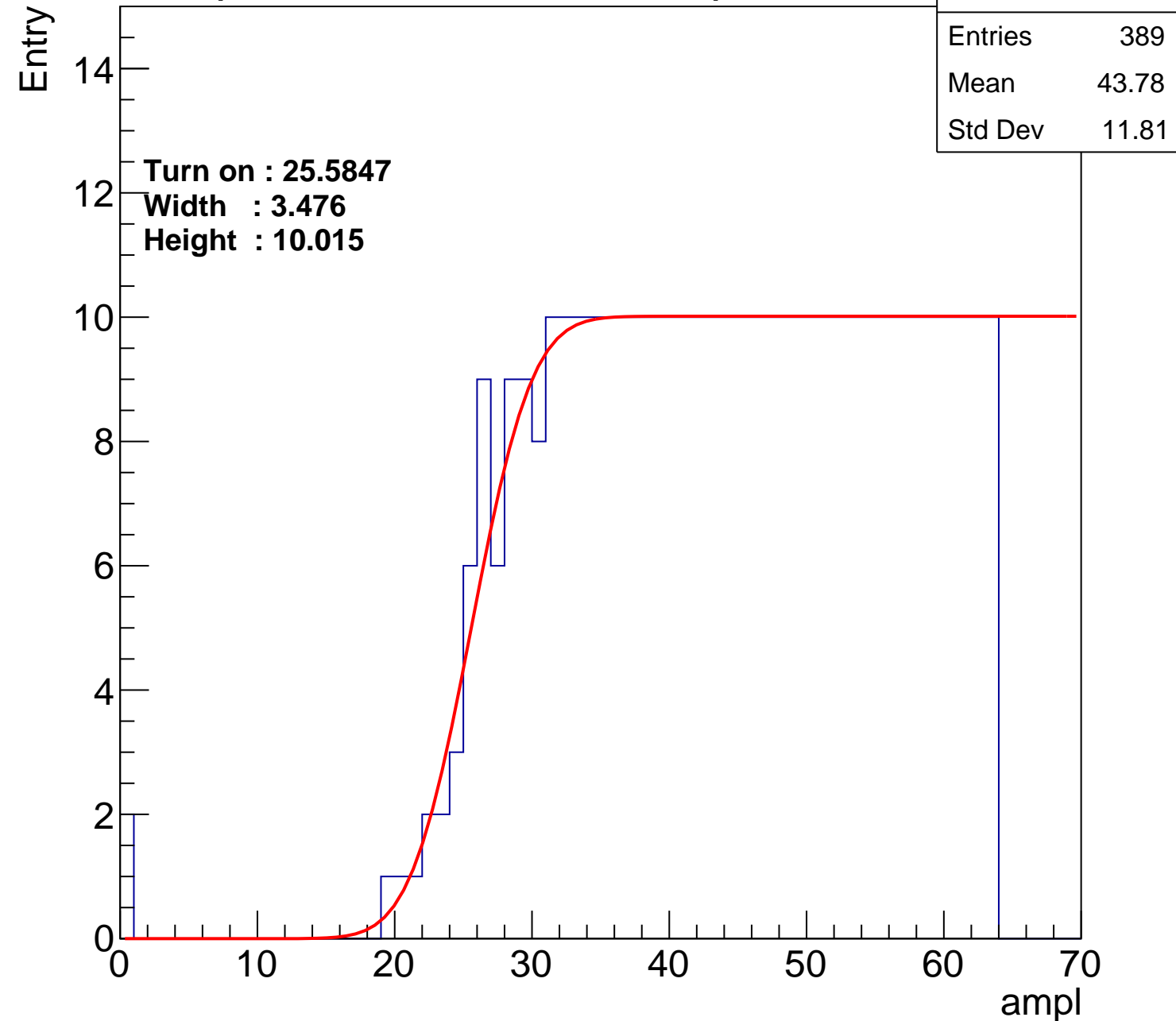
Width : 3.476

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch64

calib_packv5_042523_0143.root, FC#12, port B1

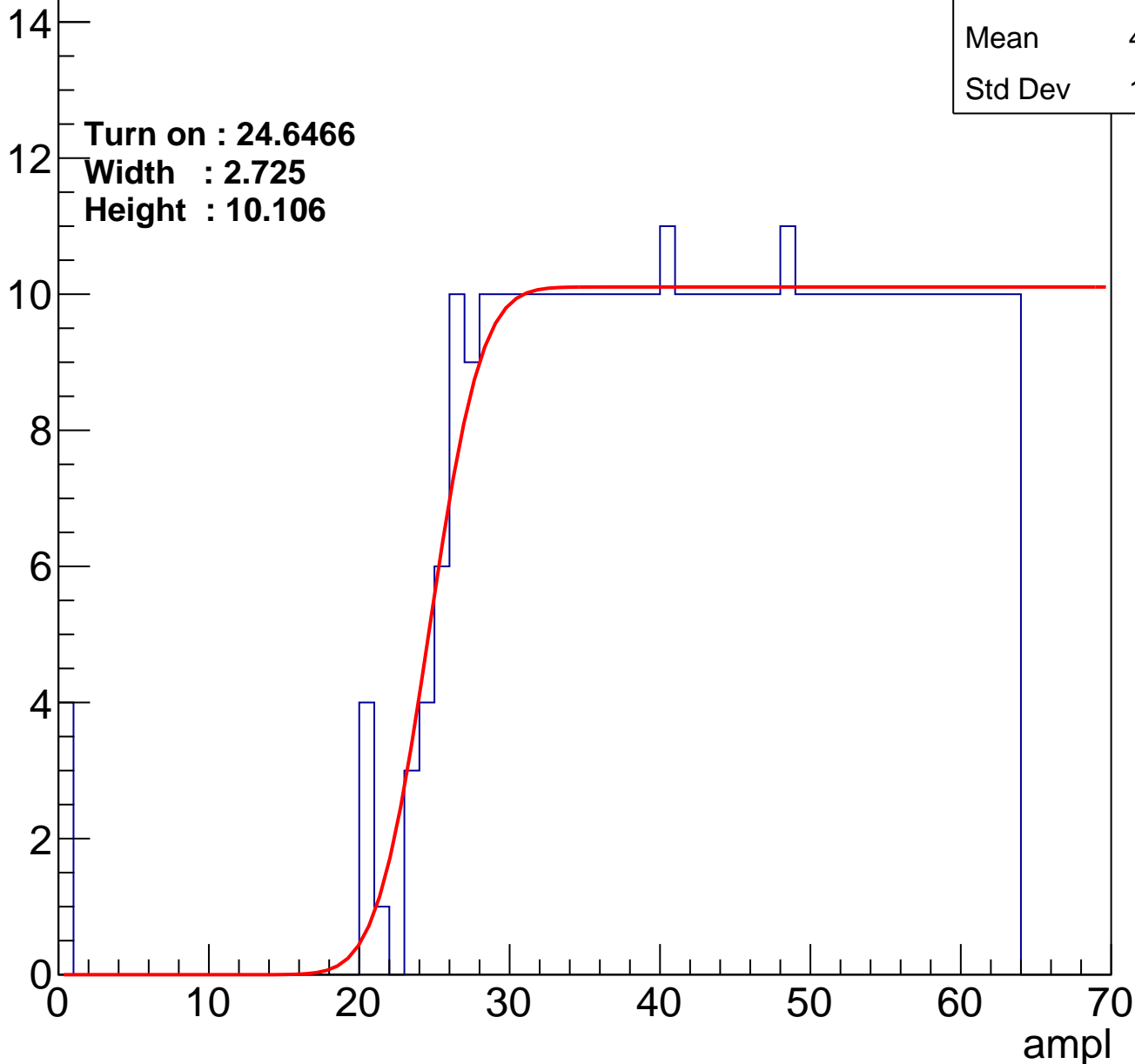
Entries	403
Mean	43.14
Std Dev	12.29

Turn on : 24.6466

Width : 2.725

Height : 10.106

Entry



B0L102S, U15-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.7411

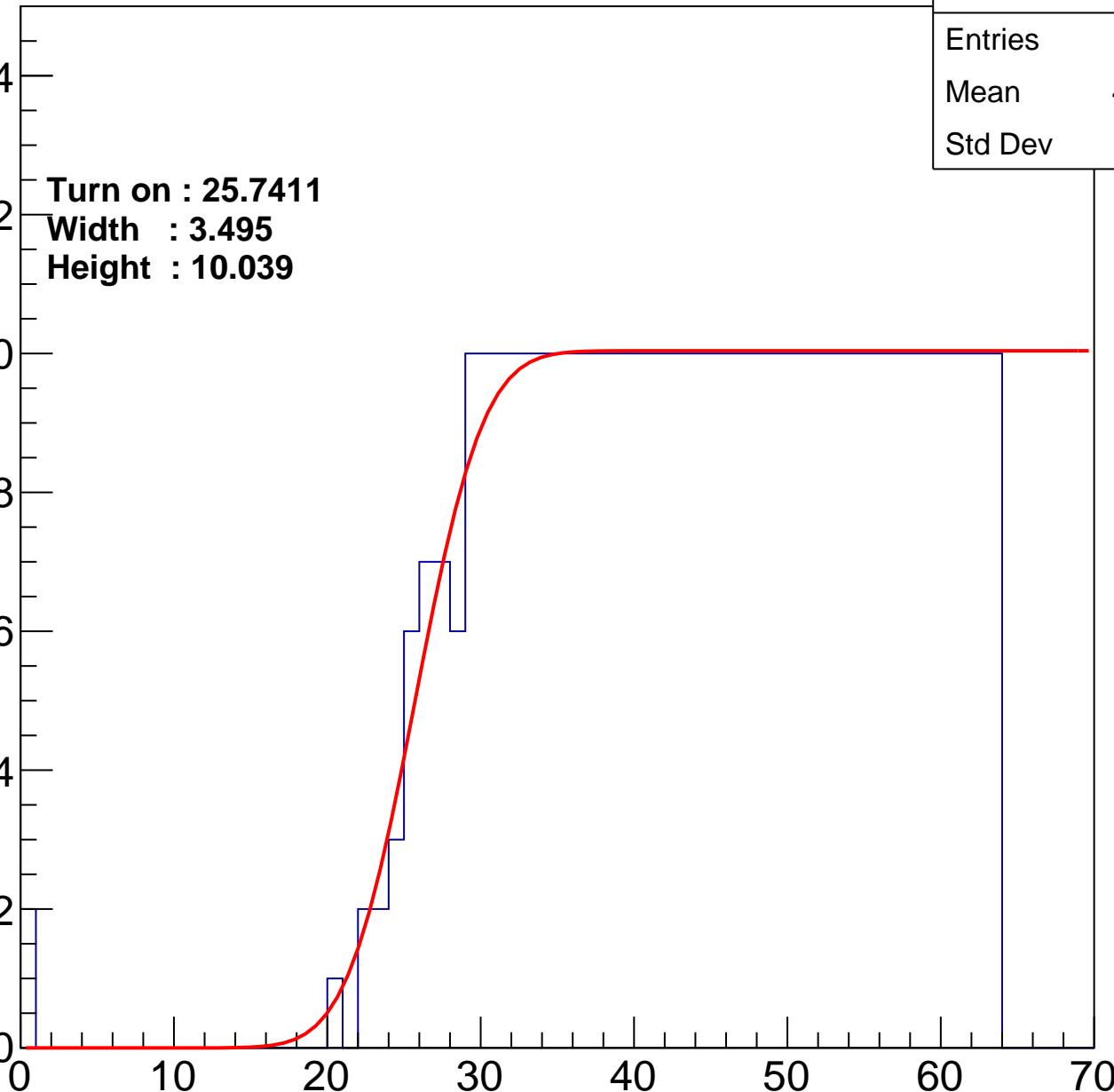
Width : 3.495

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 26.2729

Width : 1.659

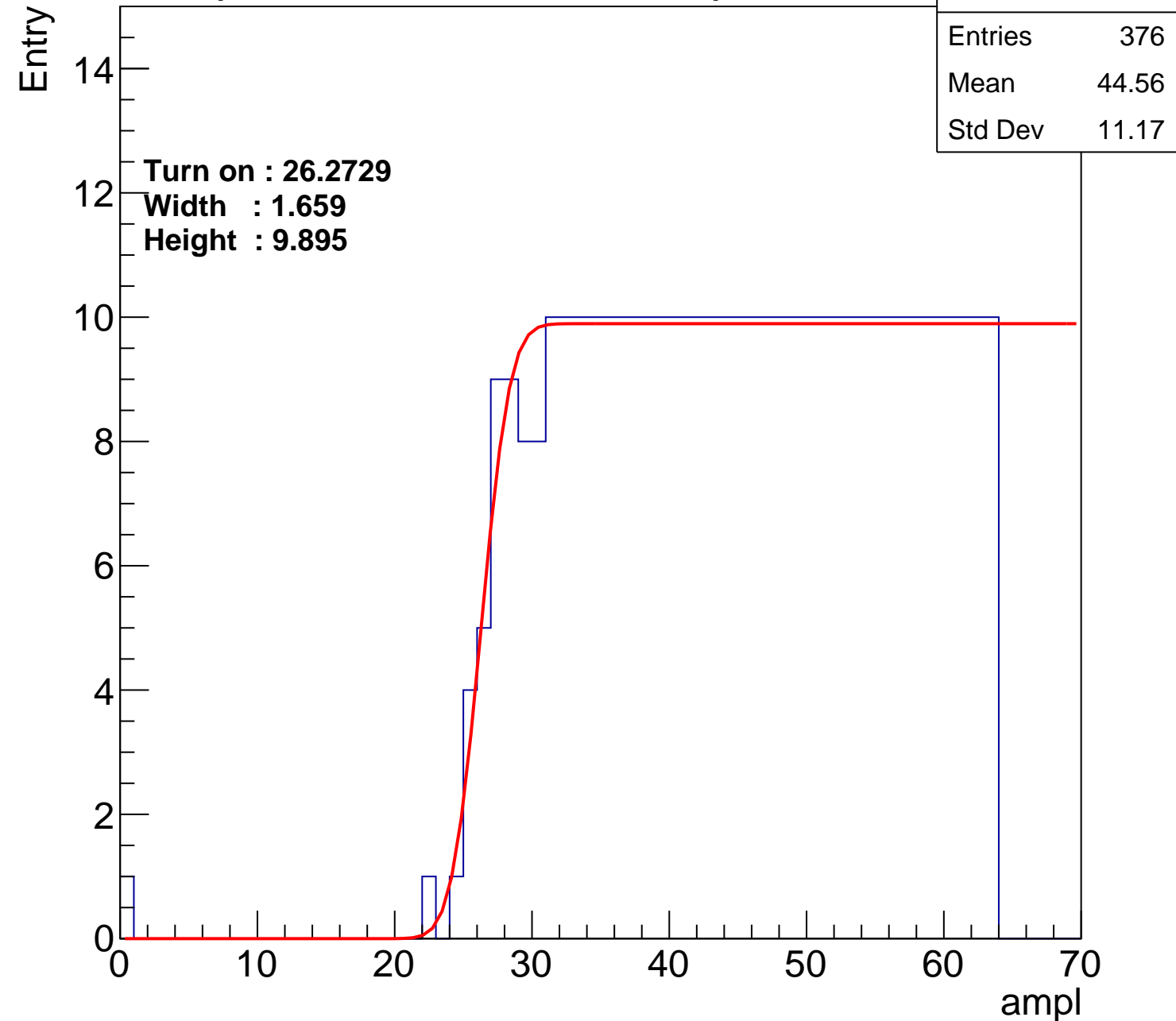
Height : 9.895

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U15-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.5730

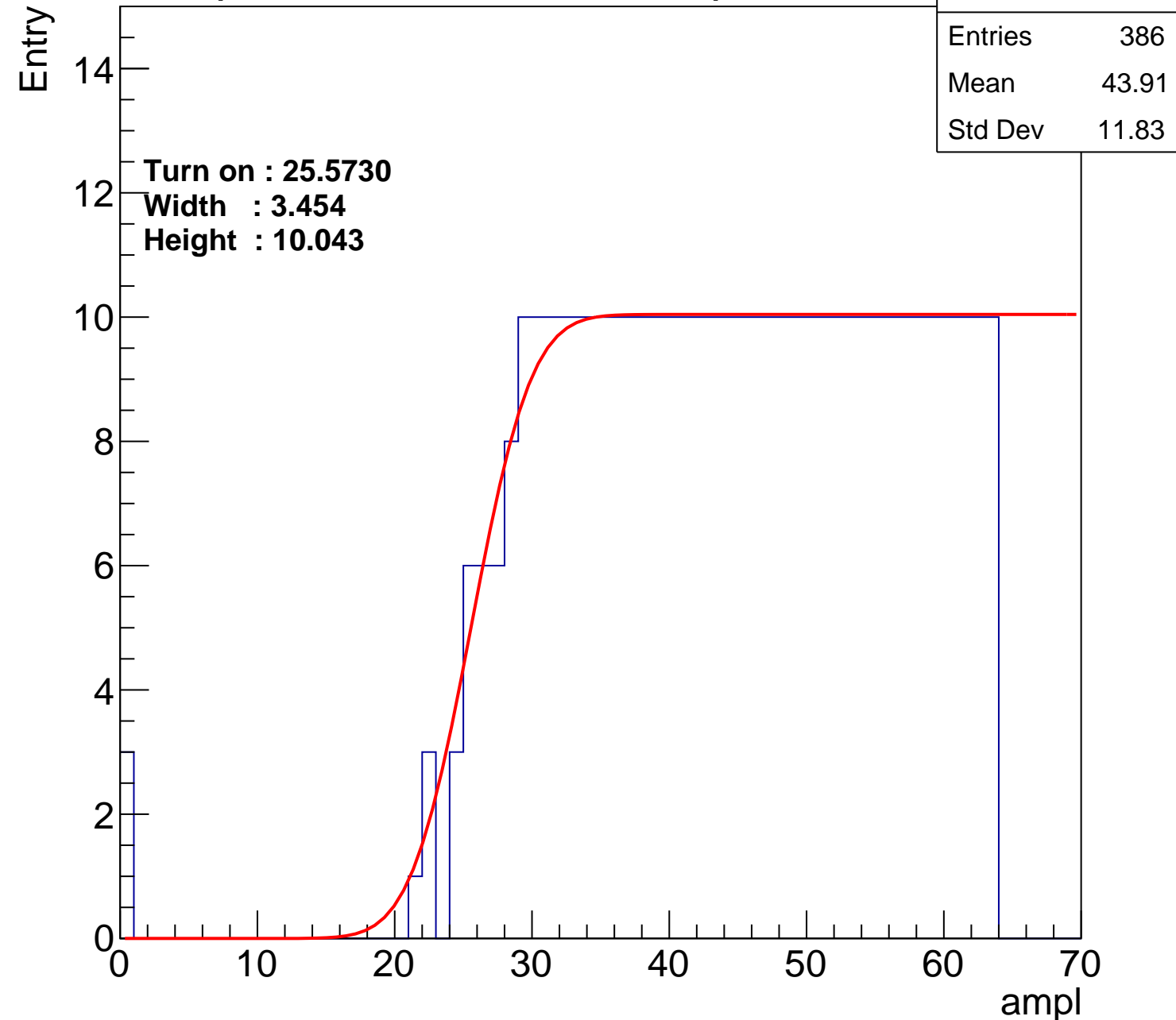
Width : 3.454

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.52
Std Dev	12.14

Turn on : 28.1879

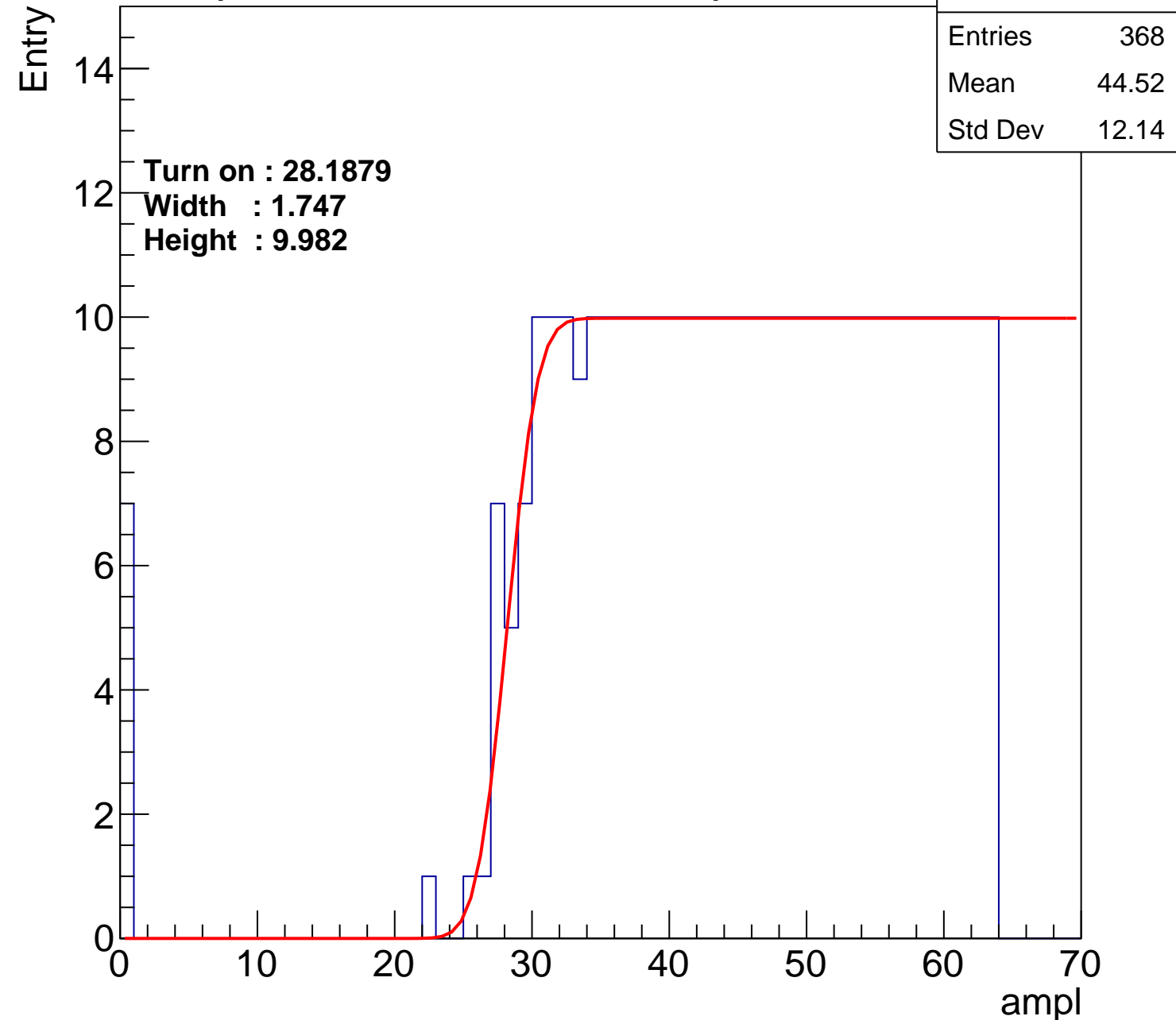
Width : 1.747

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.4981

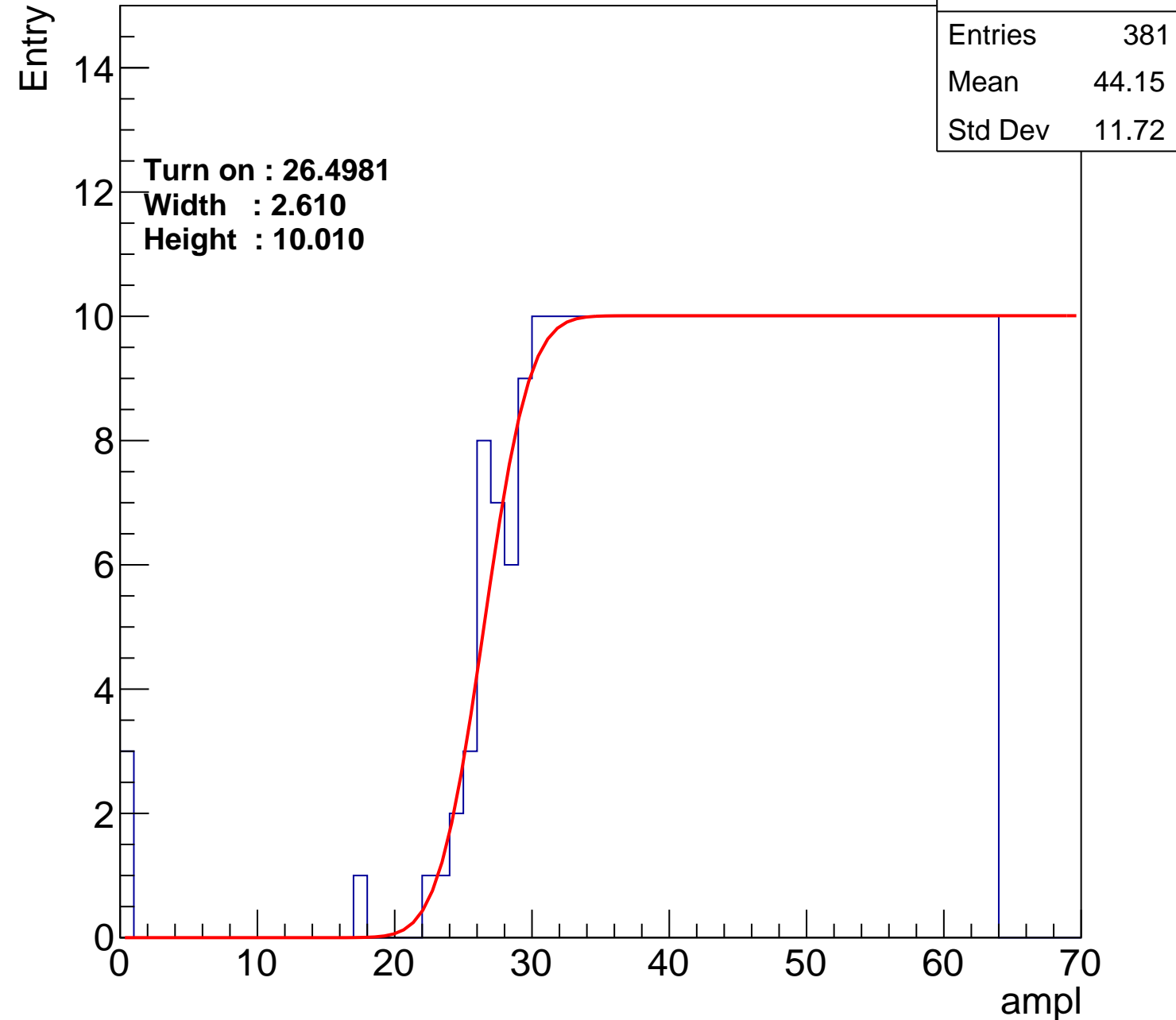
Width : 2.610

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch70

calib_packv5_042523_0143.root, FC#12, port B1

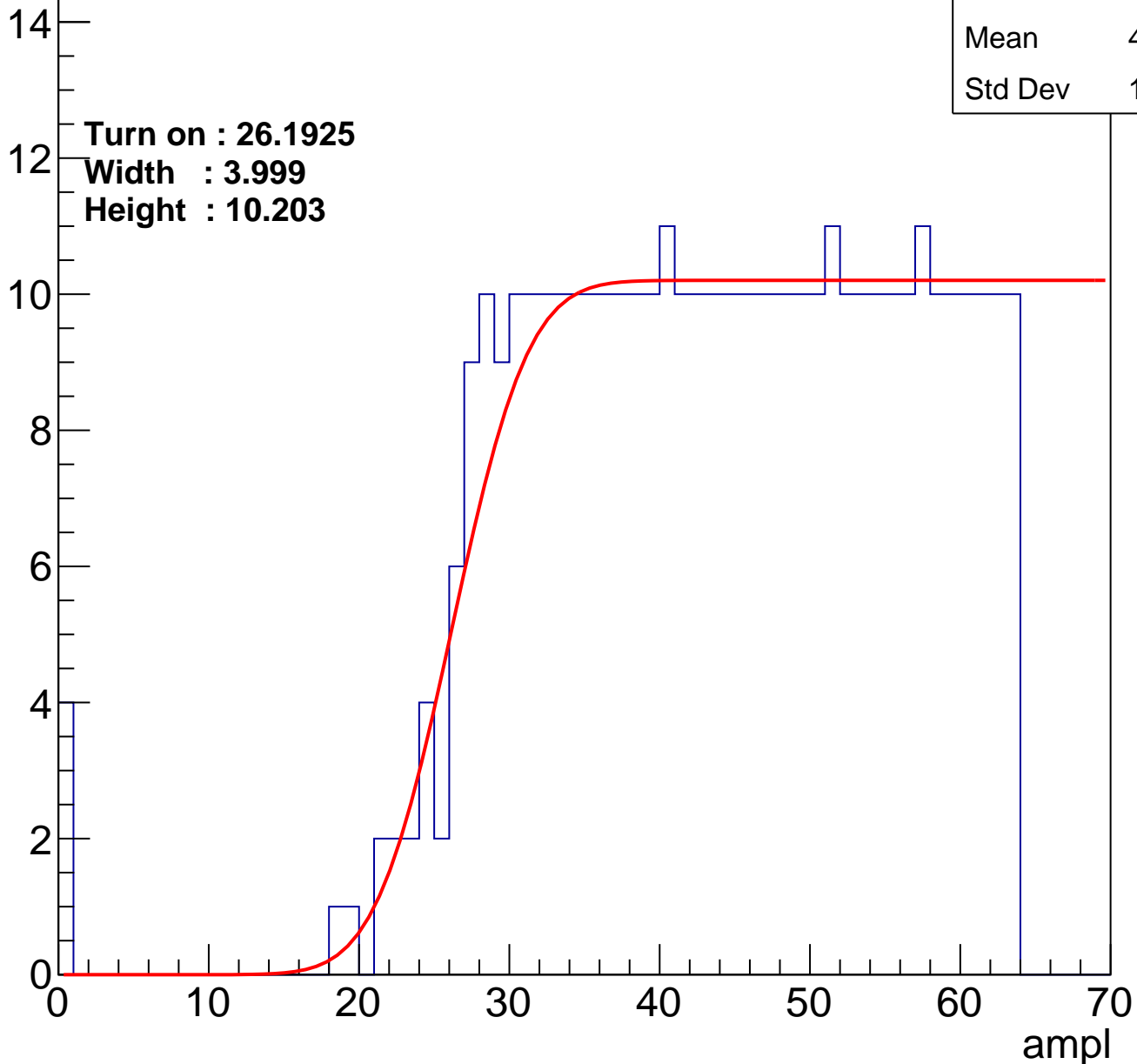
Entries	395
Mean	43.58
Std Dev	12.16

Turn on : 26.1925

Width : 3.999

Height : 10.203

Entry



B0L102S, U15-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.65
Std Dev	12.18

Turn on : 26.5329

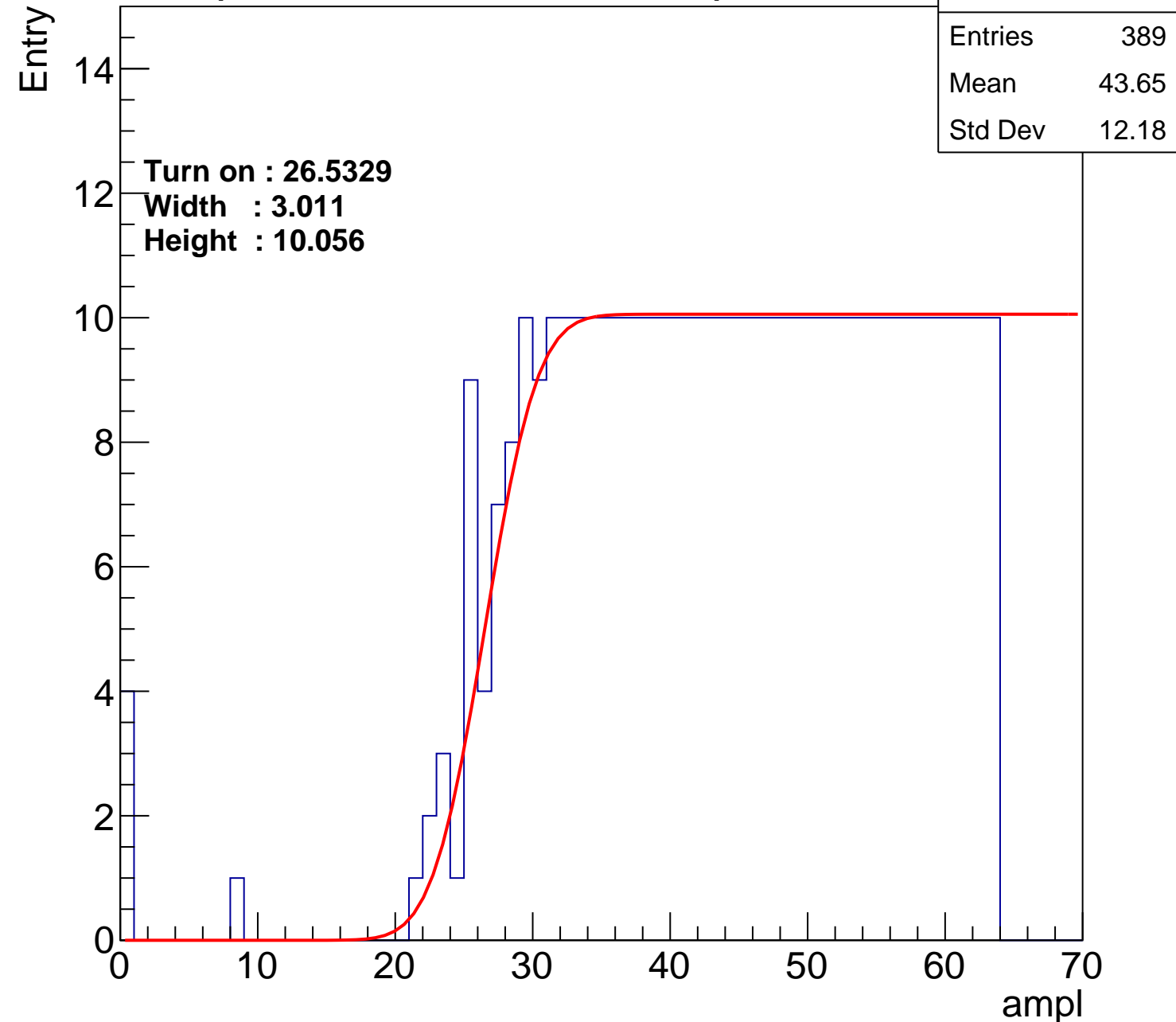
Width : 3.011

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch72

calib_packv5_042523_0143.root, FC#12, port B1

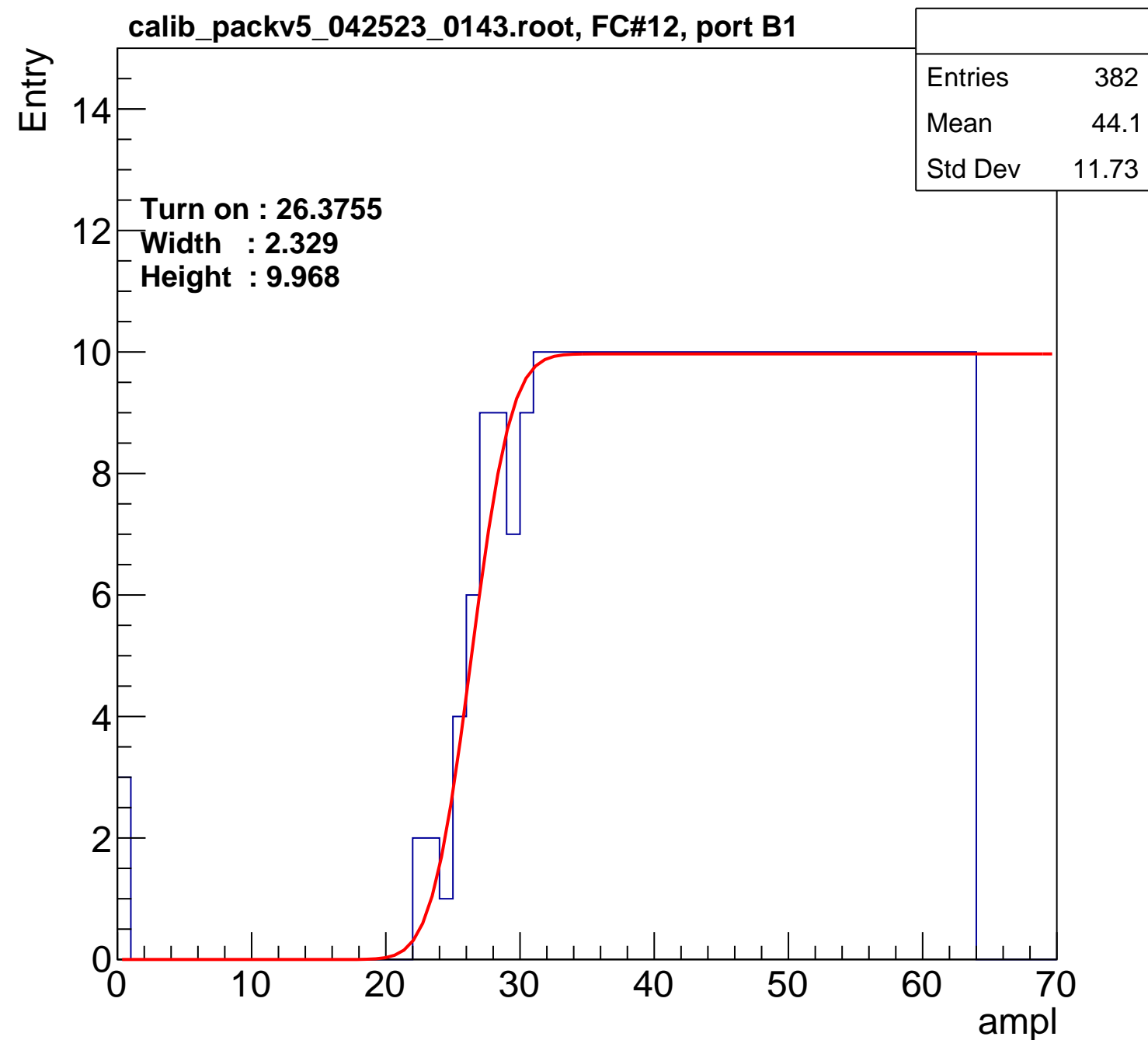
Entry

14
12
10
8
6
4
2
0

Turn on : 26.3755
Width : 2.329
Height : 9.968

Entries	382
Mean	44.1
Std Dev	11.73

ampl



B0L102S, U15-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.89
Std Dev	11.97

Turn on : 25.4983

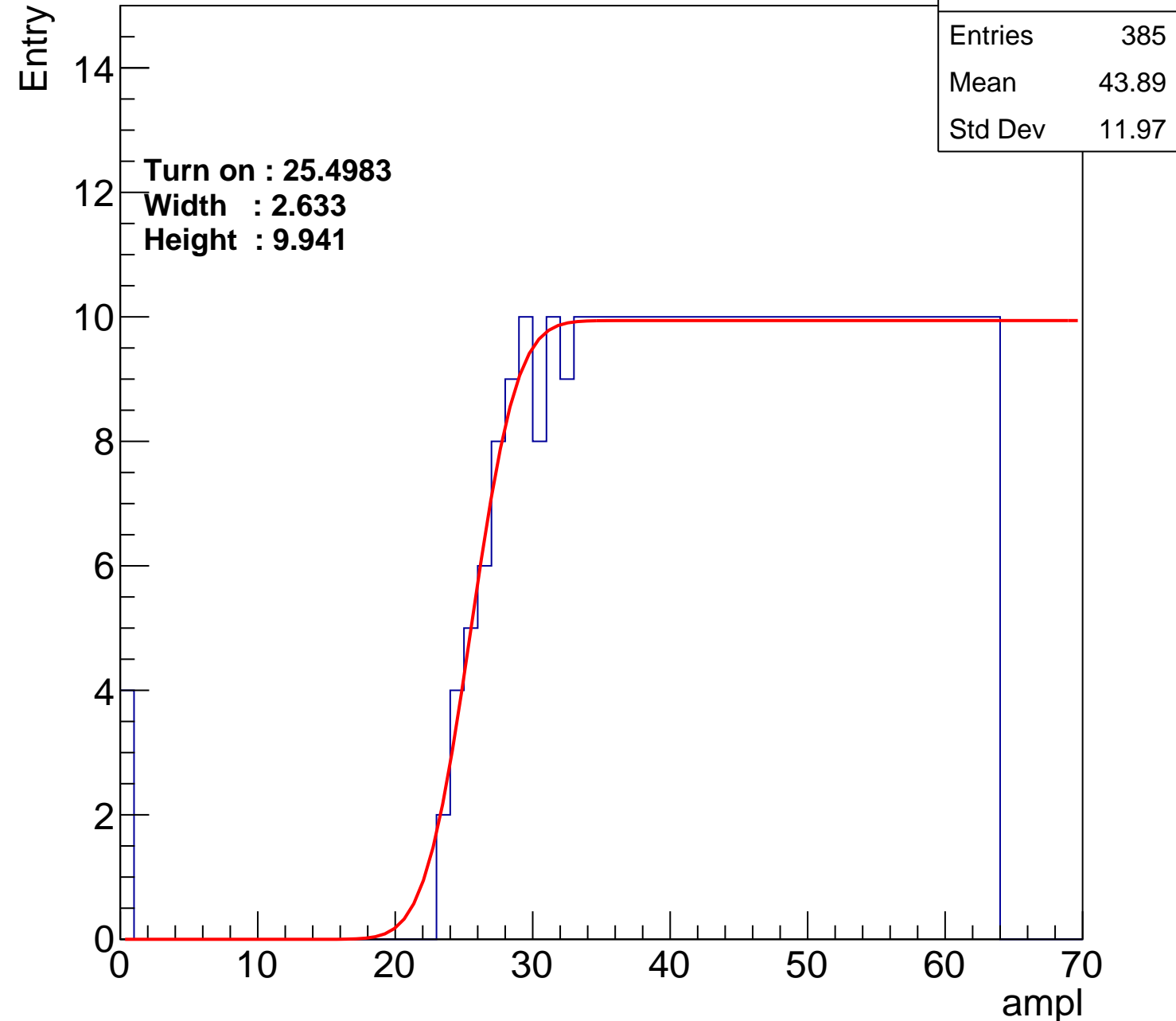
Width : 2.633

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.81
Std Dev	11.6

Turn on : 25.1622

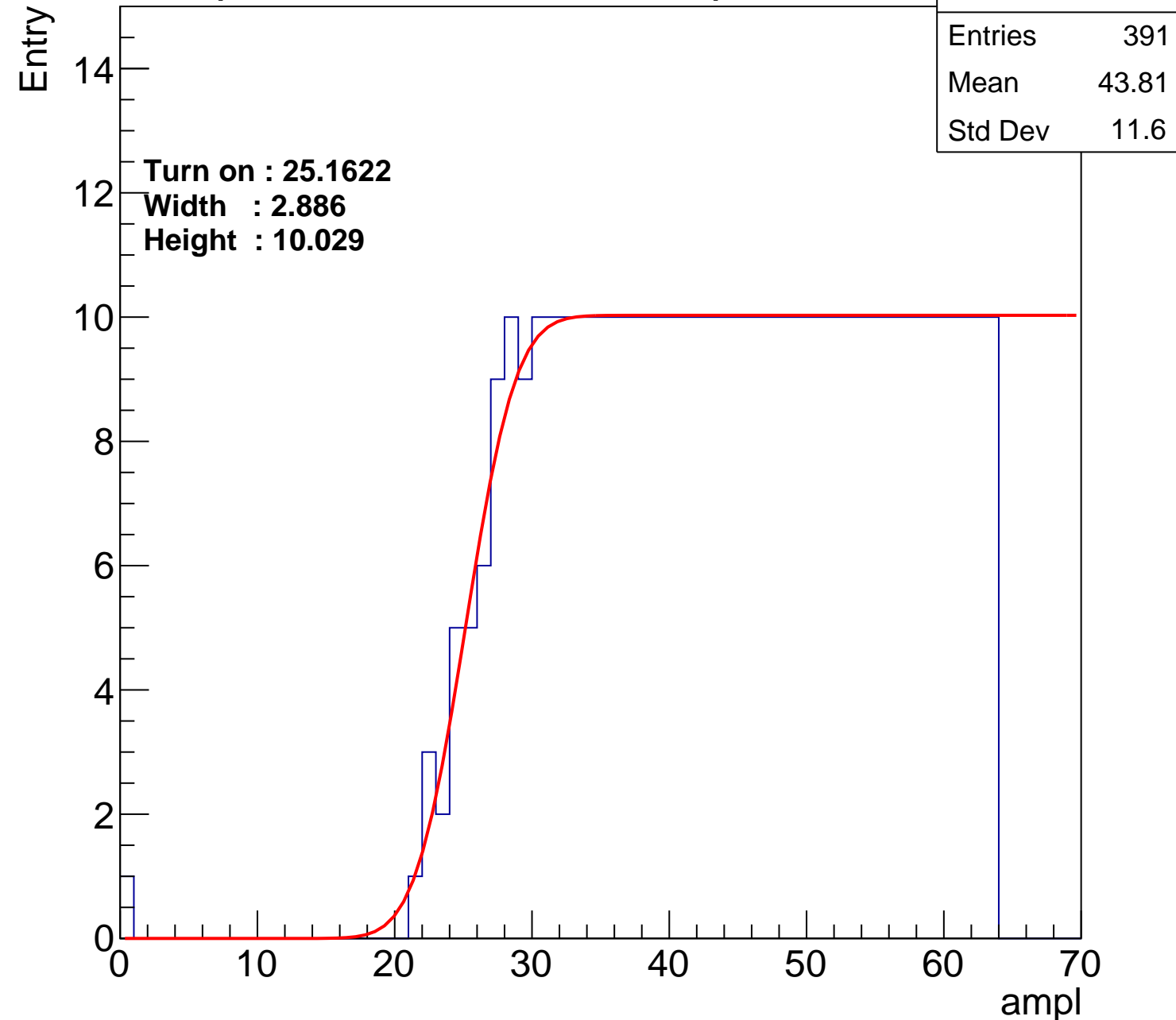
Width : 2.886

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.15
Std Dev	11.58

Turn on : 25.3352

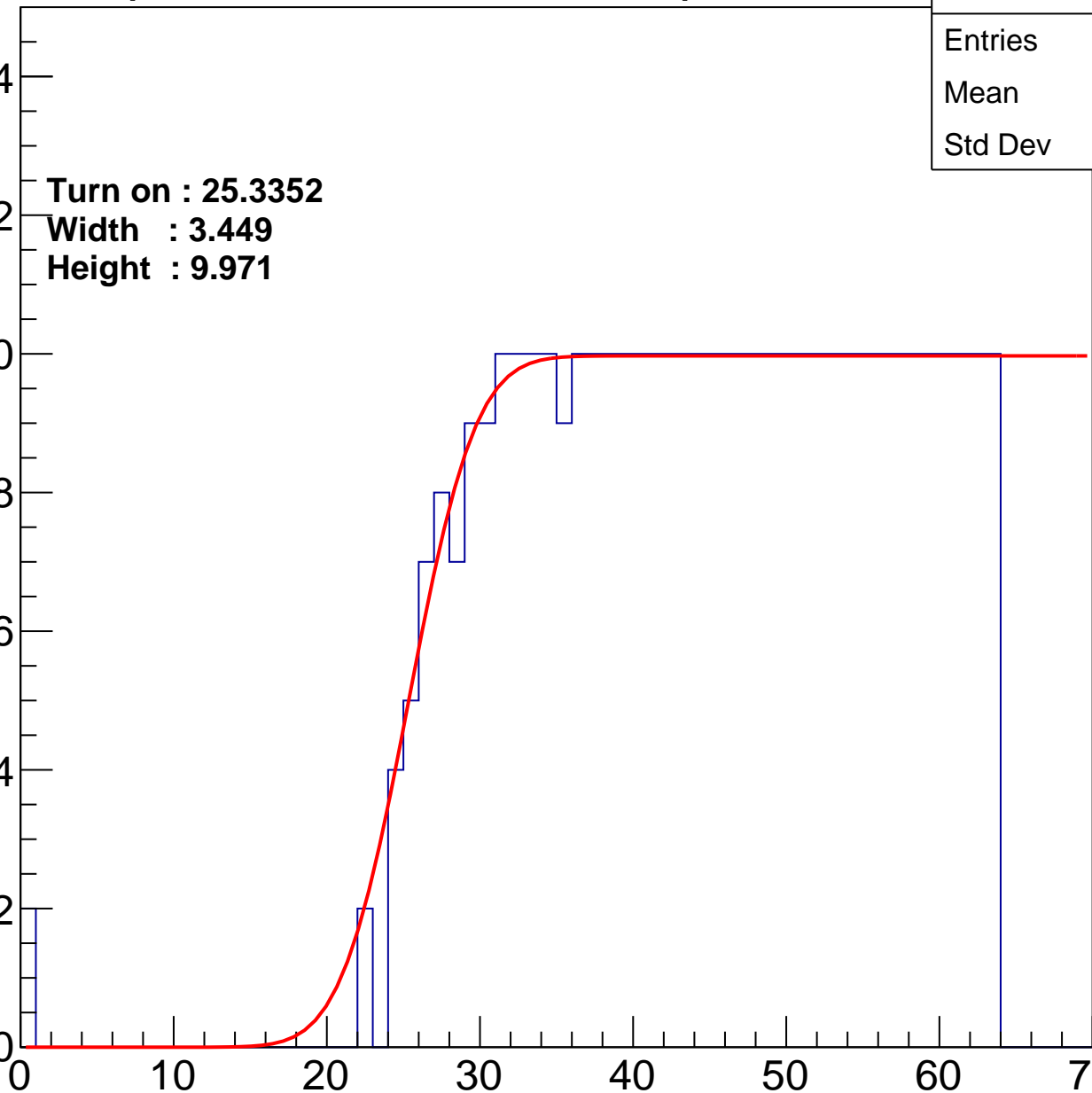
Width : 3.449

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.81
Std Dev	11.65

Turn on : 25.5938

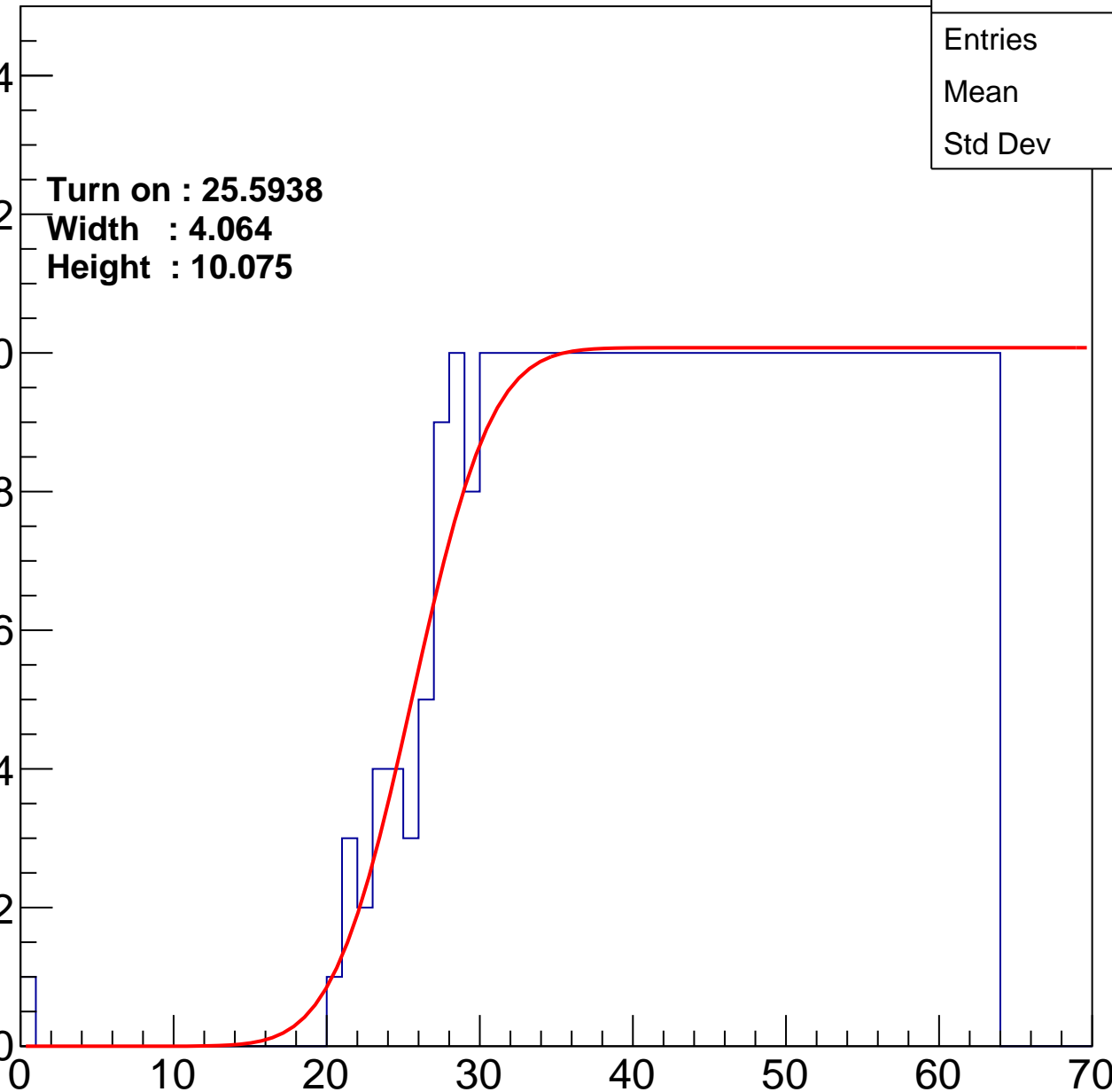
Width : 4.064

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.6768

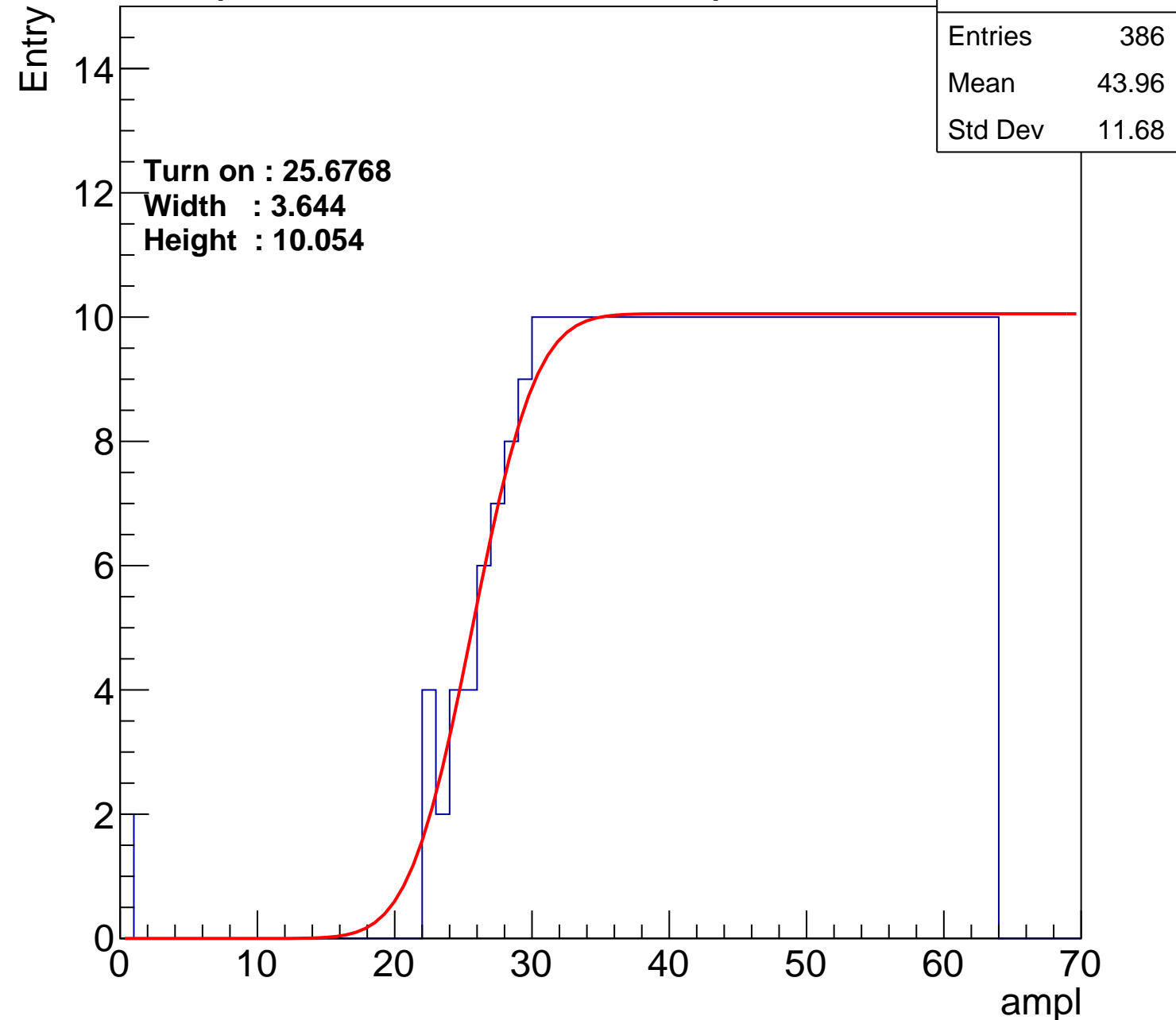
Width : 3.644

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.79
Std Dev	11.95

Turn on : 24.6824

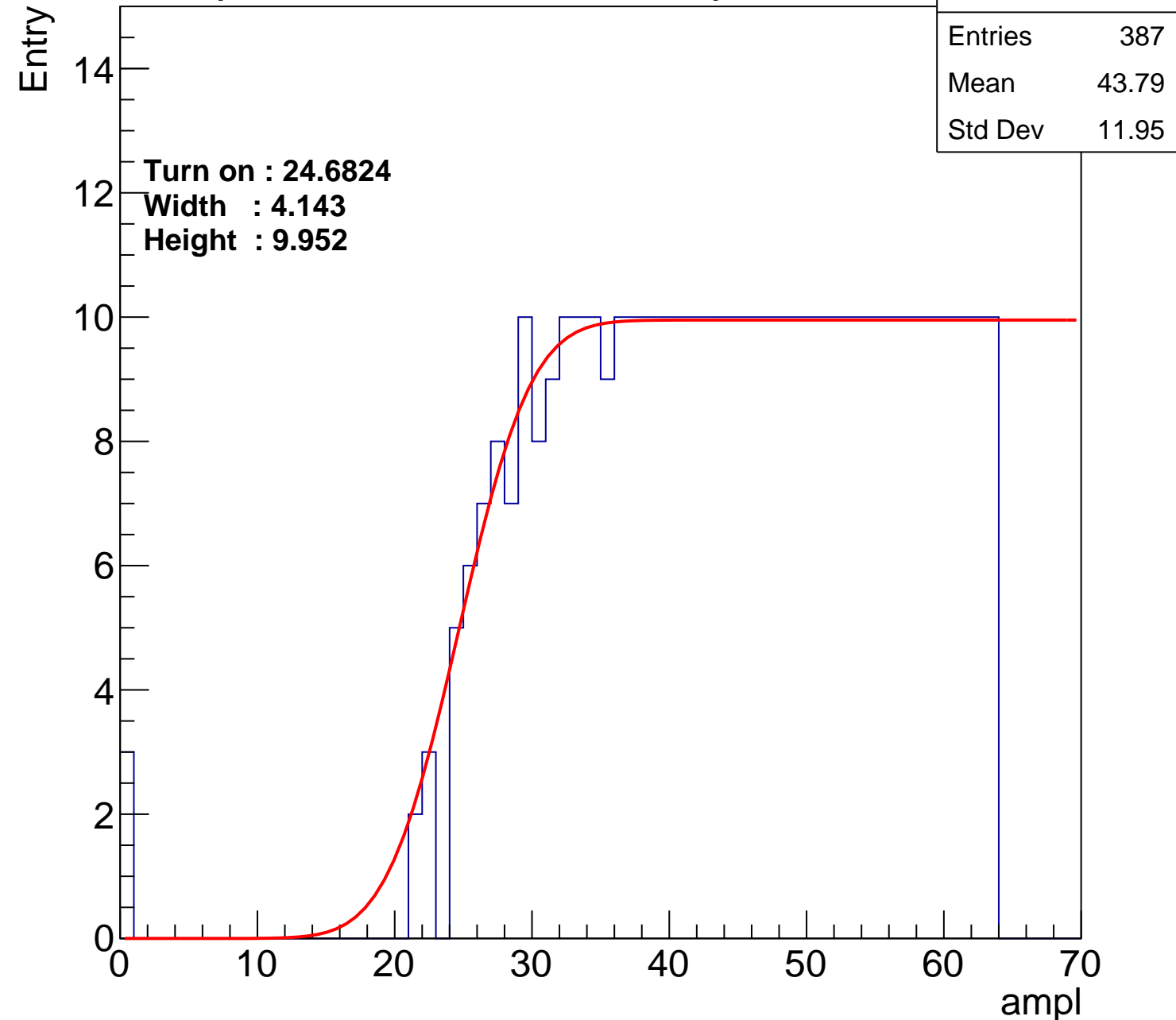
Width : 4.143

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.45
Std Dev	11.96

Turn on : 24.5993

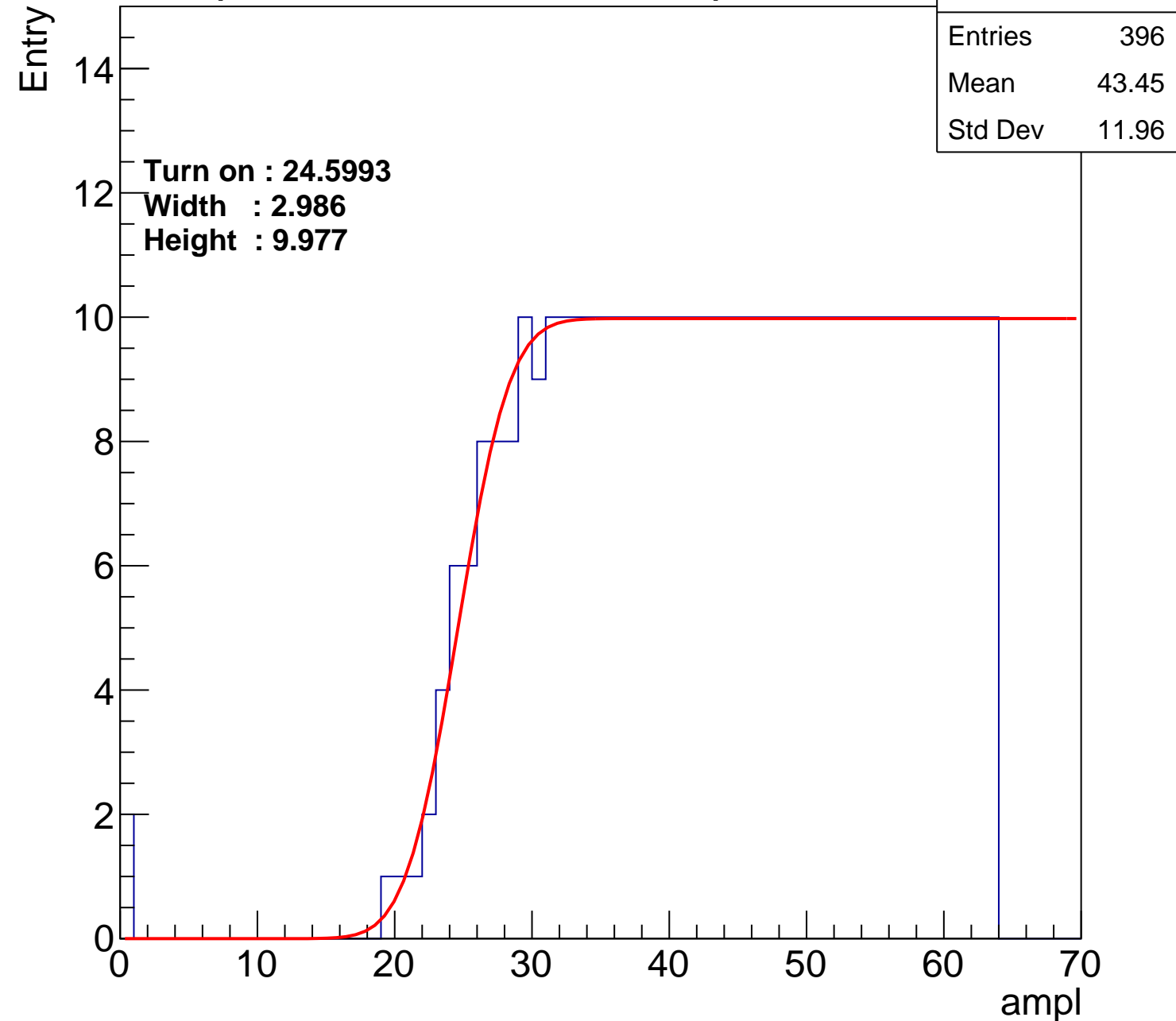
Width : 2.986

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.96
Std Dev	11.98

Turn on : 27.0730

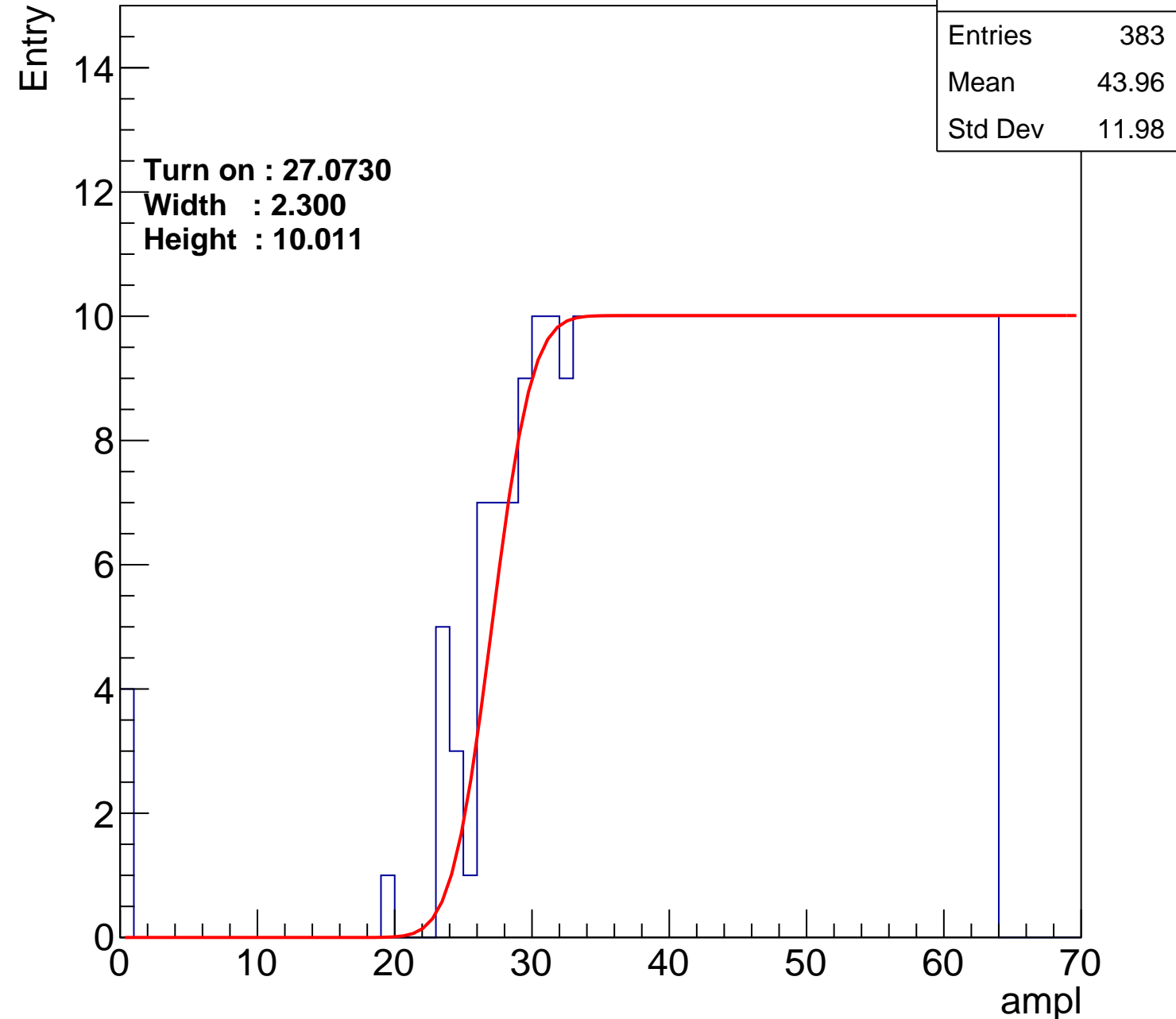
Width : 2.300

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.25
Std Dev	12.04

Turn on : 27.5387

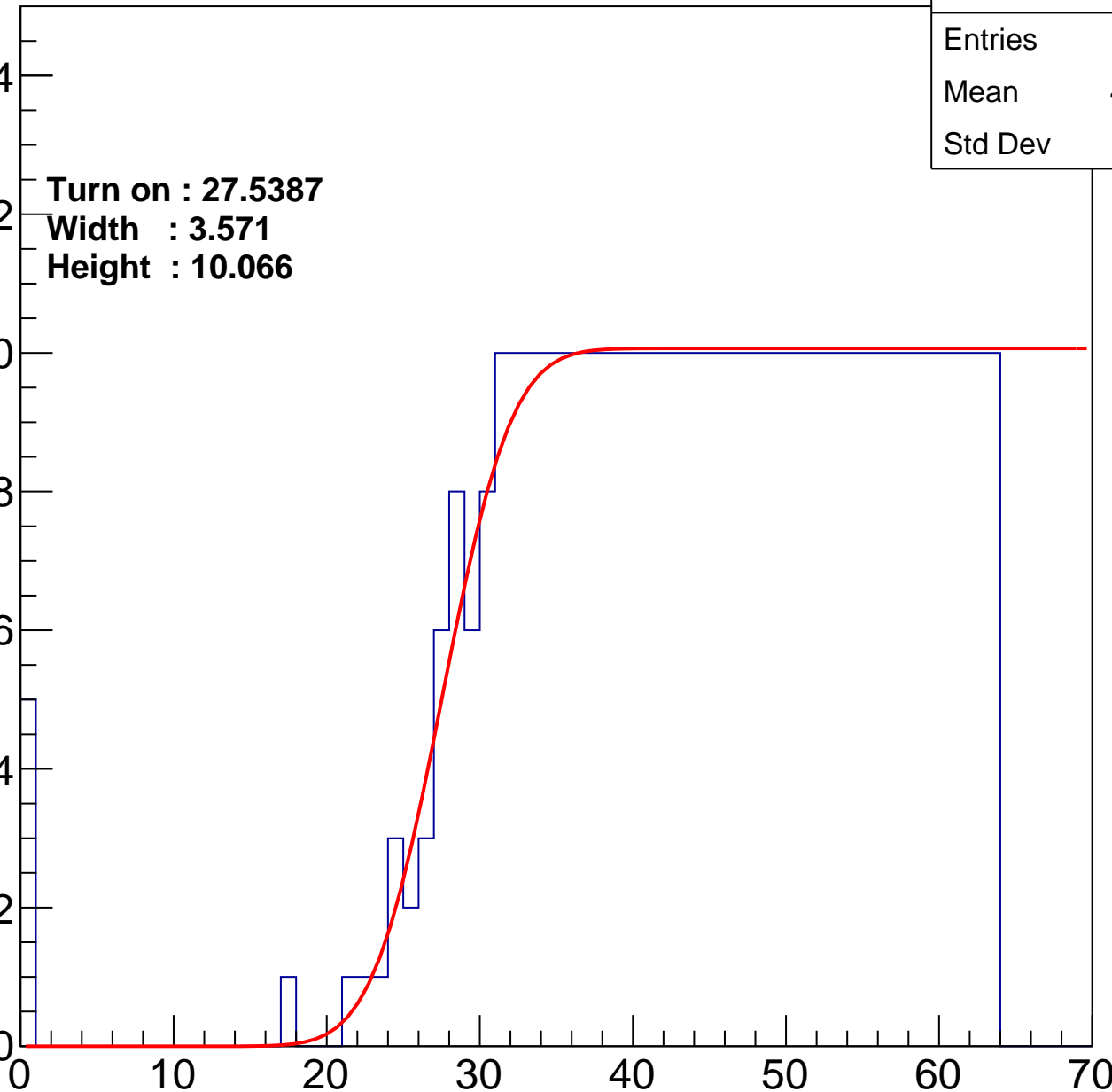
Width : 3.571

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch82

calib_packv5_042523_0143.root, FC#12, port B1

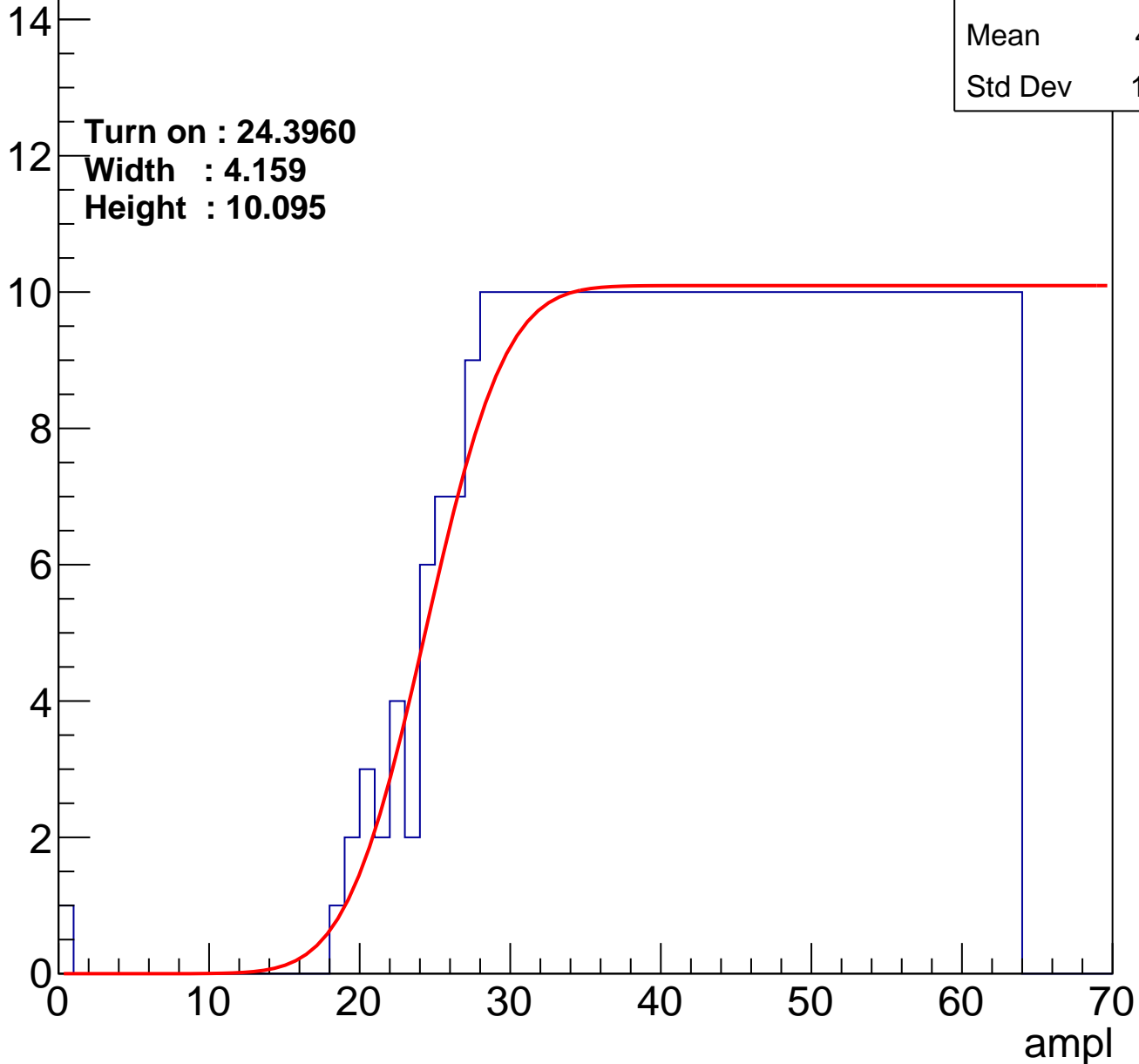
Entries	404
Mean	43.11
Std Dev	12.04

Turn on : 24.3960

Width : 4.159

Height : 10.095

Entry



B0L102S, U15-ch83

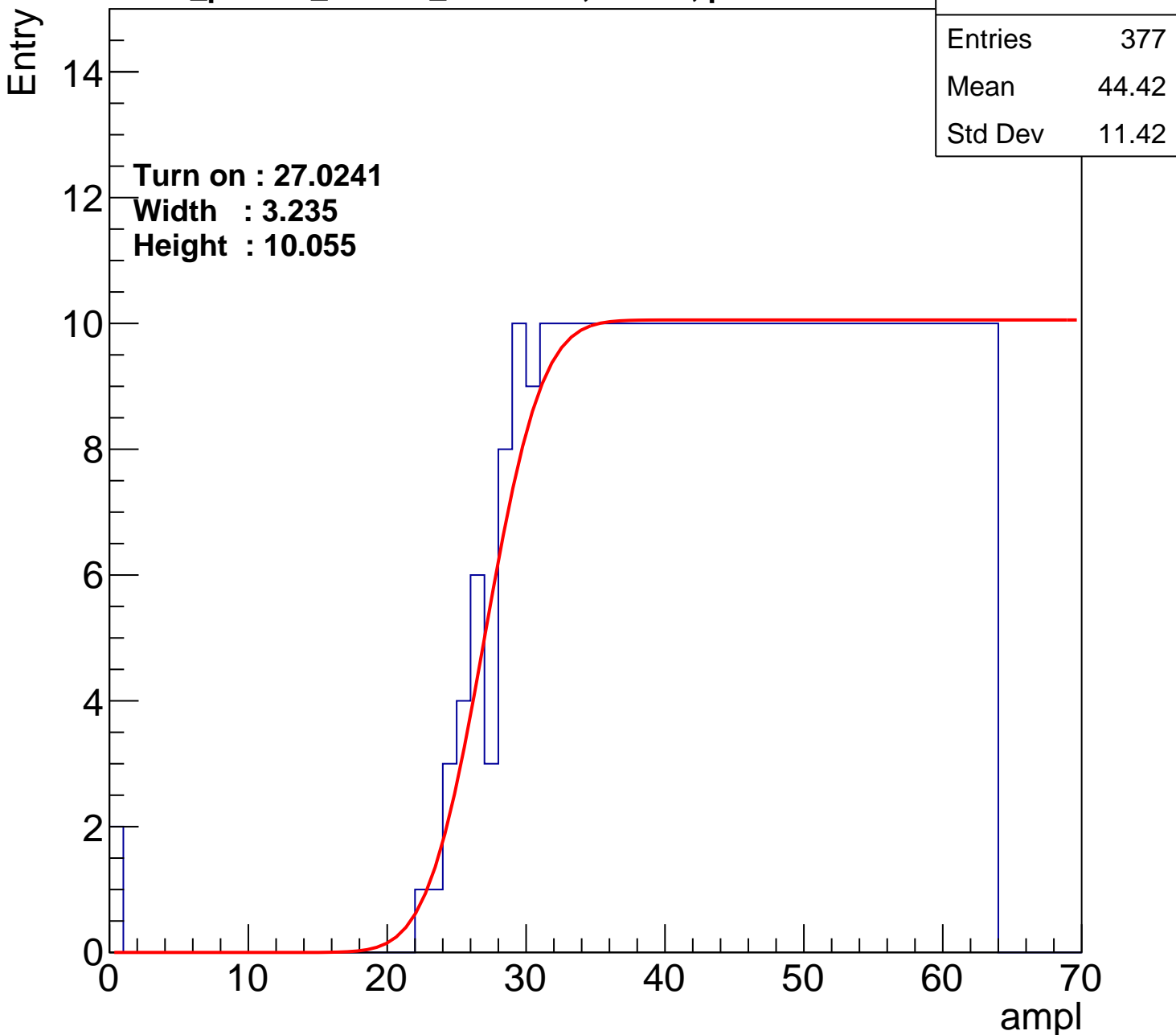
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 27.0241

Width : 3.235

Height : 10.055

Entries	377
Mean	44.42
Std Dev	11.42



B0L102S, U15-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.69
Std Dev	12.09

Turn on : 25.6445

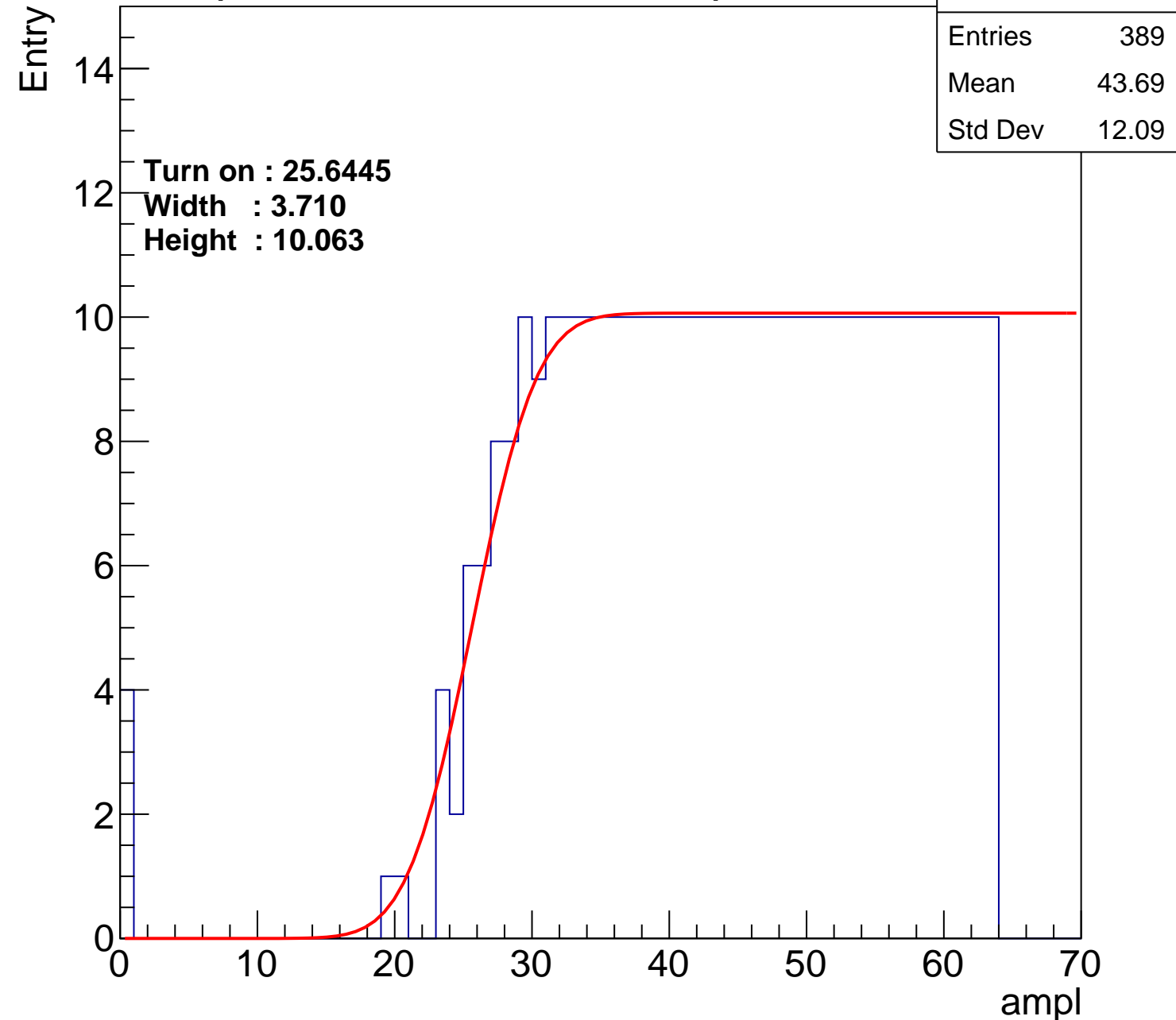
Width : 3.710

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	44.96
Std Dev	11.33

Turn on : 28.1842

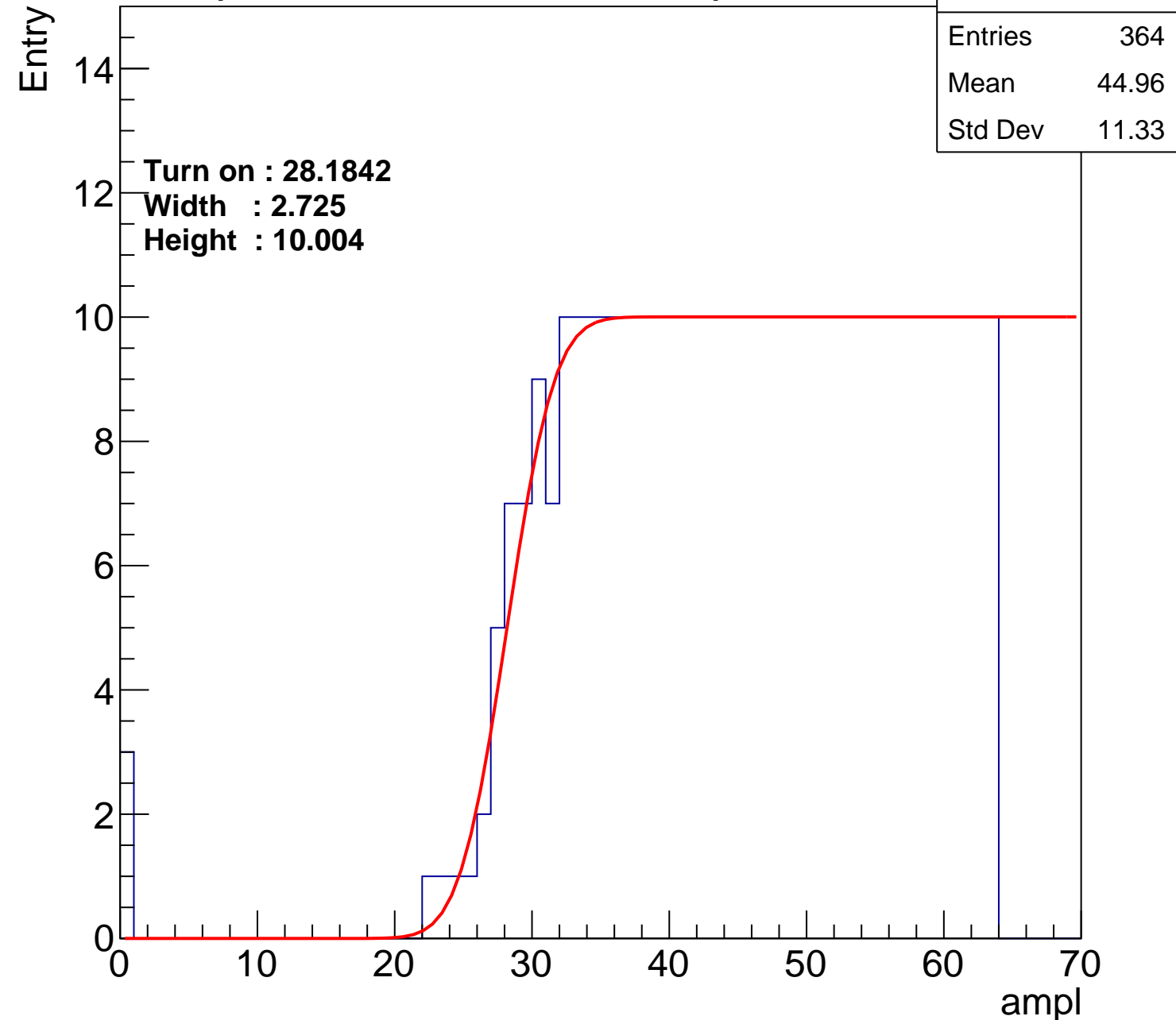
Width : 2.725

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.31
Std Dev	12.42

Turn on : 25.1428

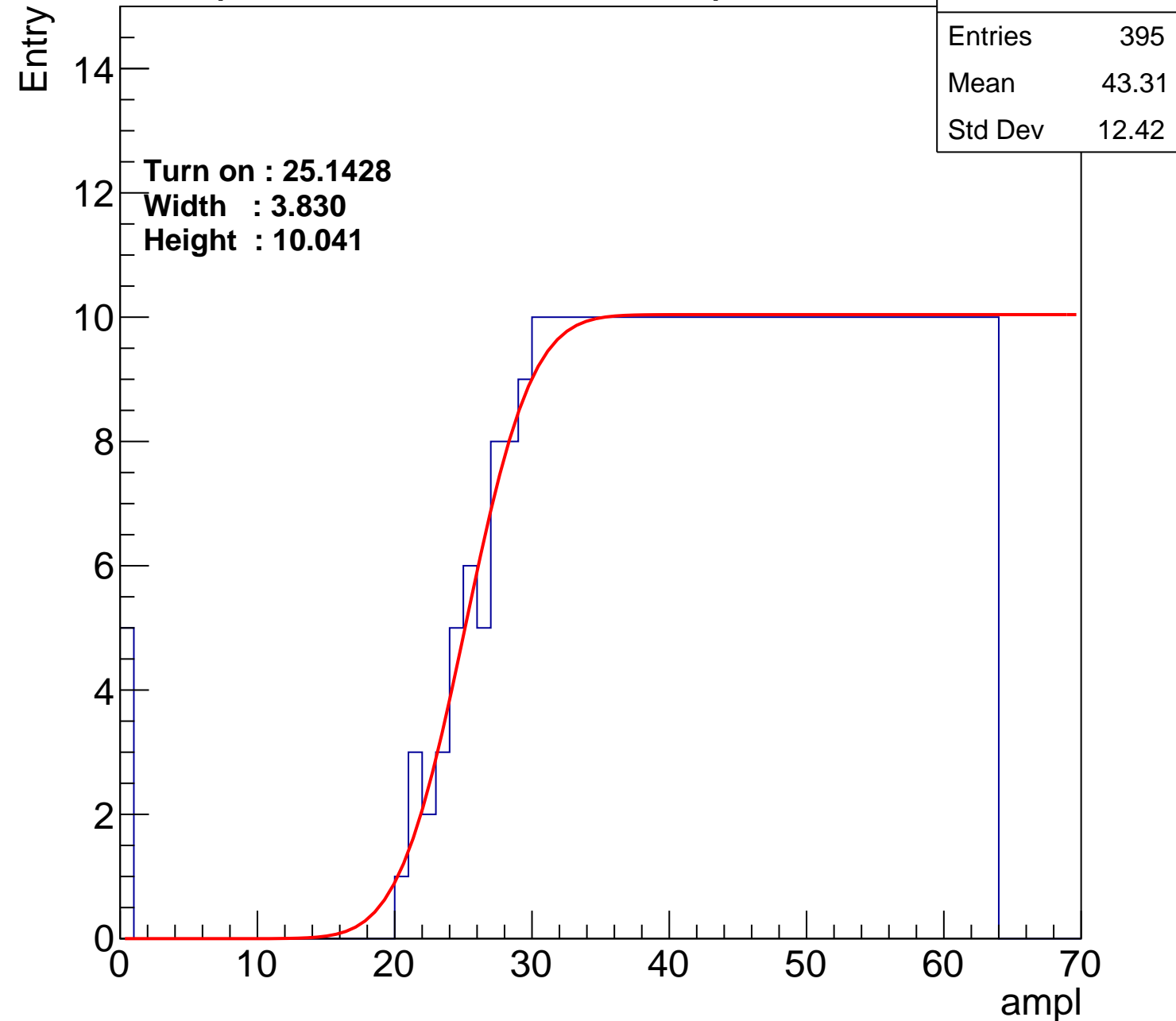
Width : 3.830

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.32
Std Dev	12.45

Turn on : 25.1017

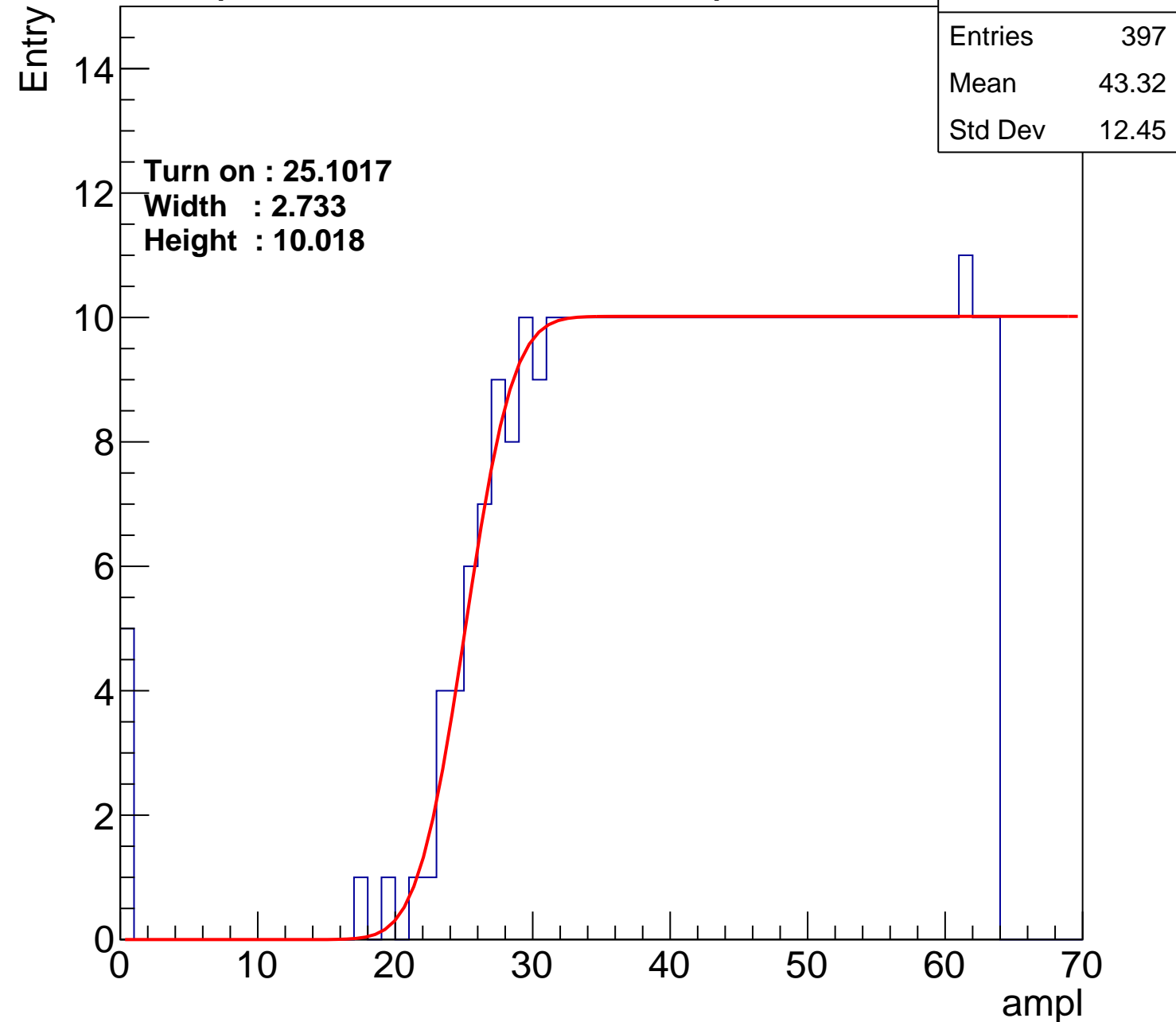
Width : 2.733

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.7
Std Dev	12.19

Turn on : 25.6050

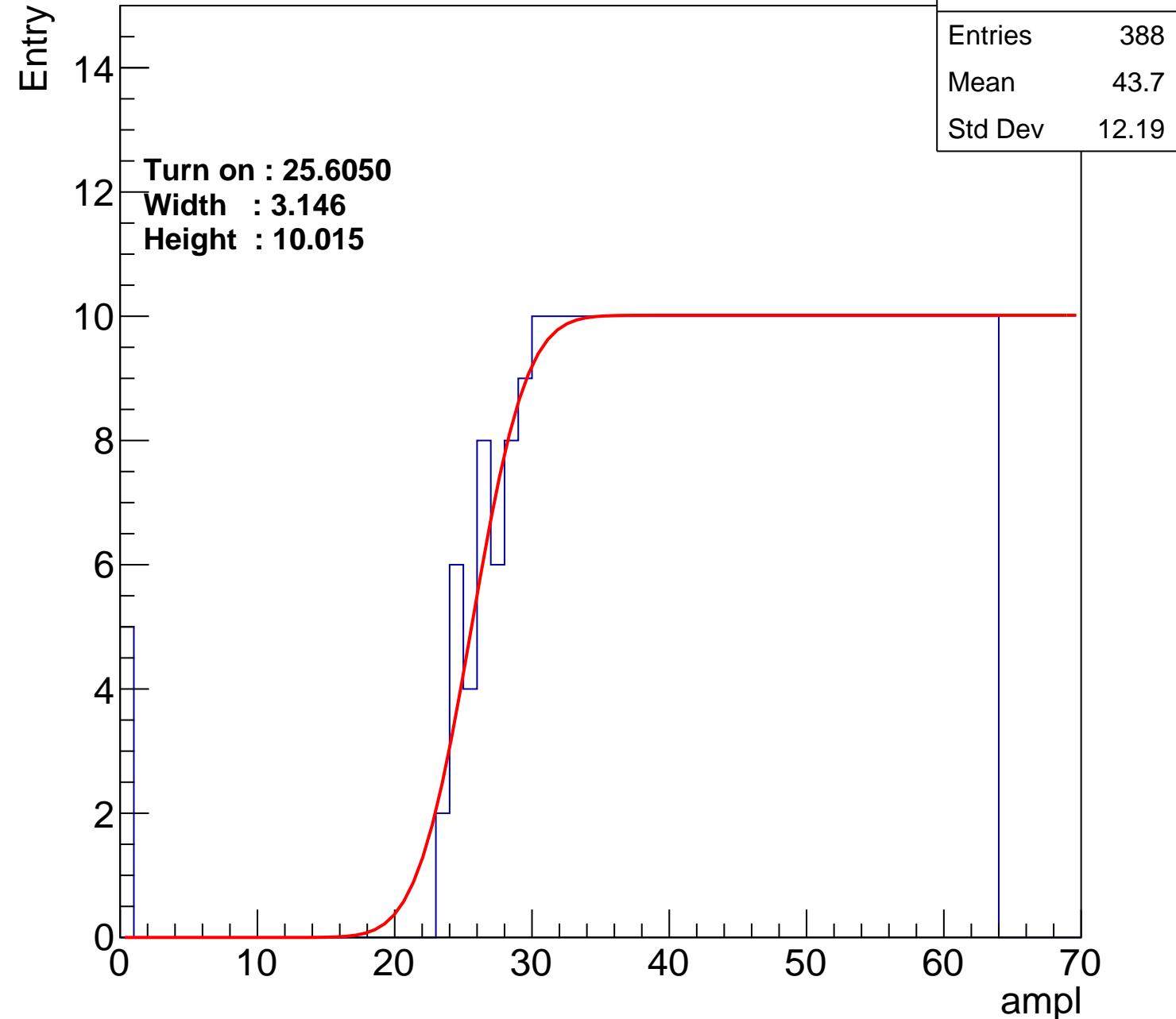
Width : 3.146

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch89

calib_packv5_042523_0143.root, FC#12, port B1

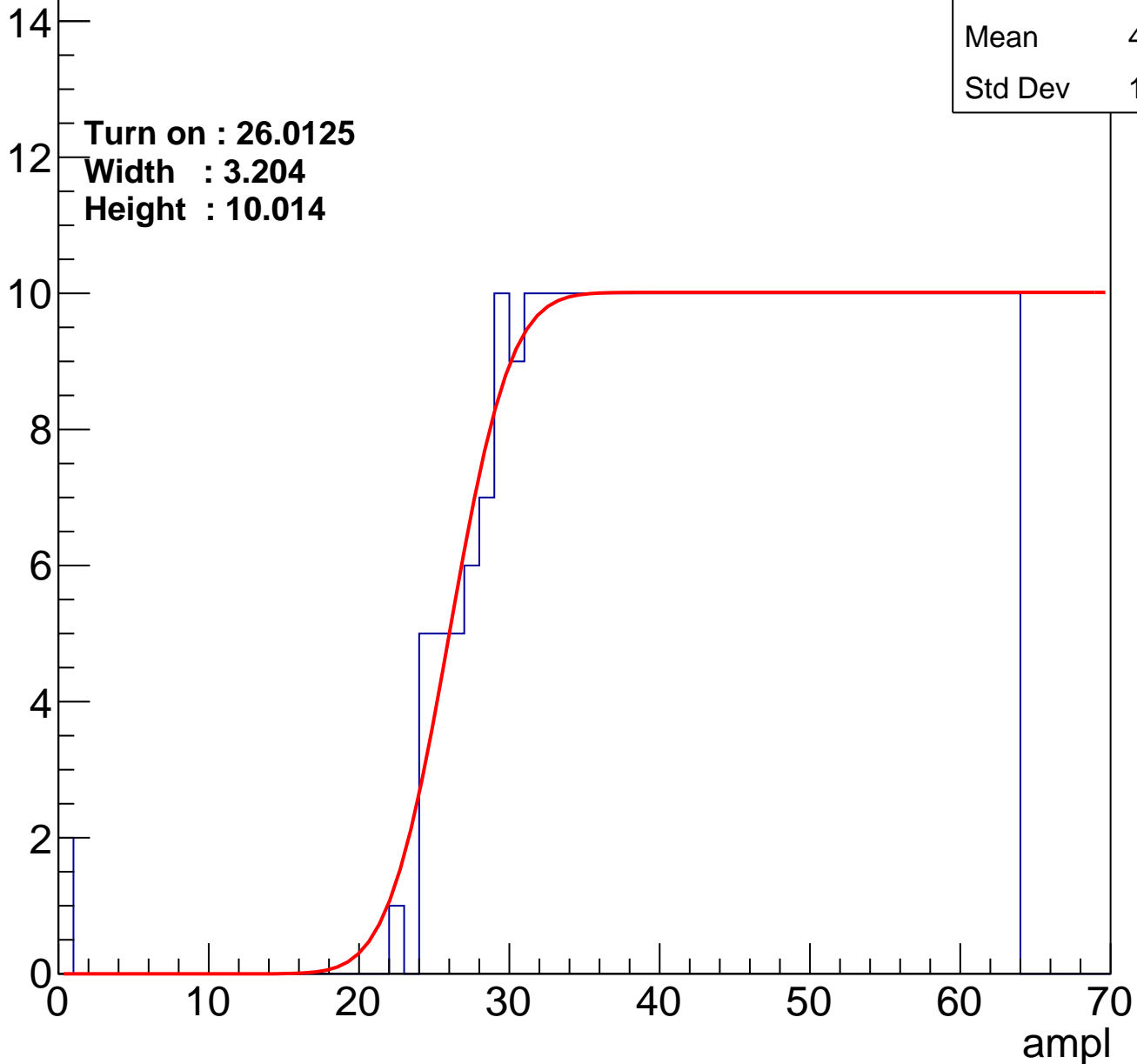
Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.0125

Width : 3.204

Height : 10.014

Entry



B0L102S, U15-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	42.97
Std Dev	12.75

Turn on : 24.8261

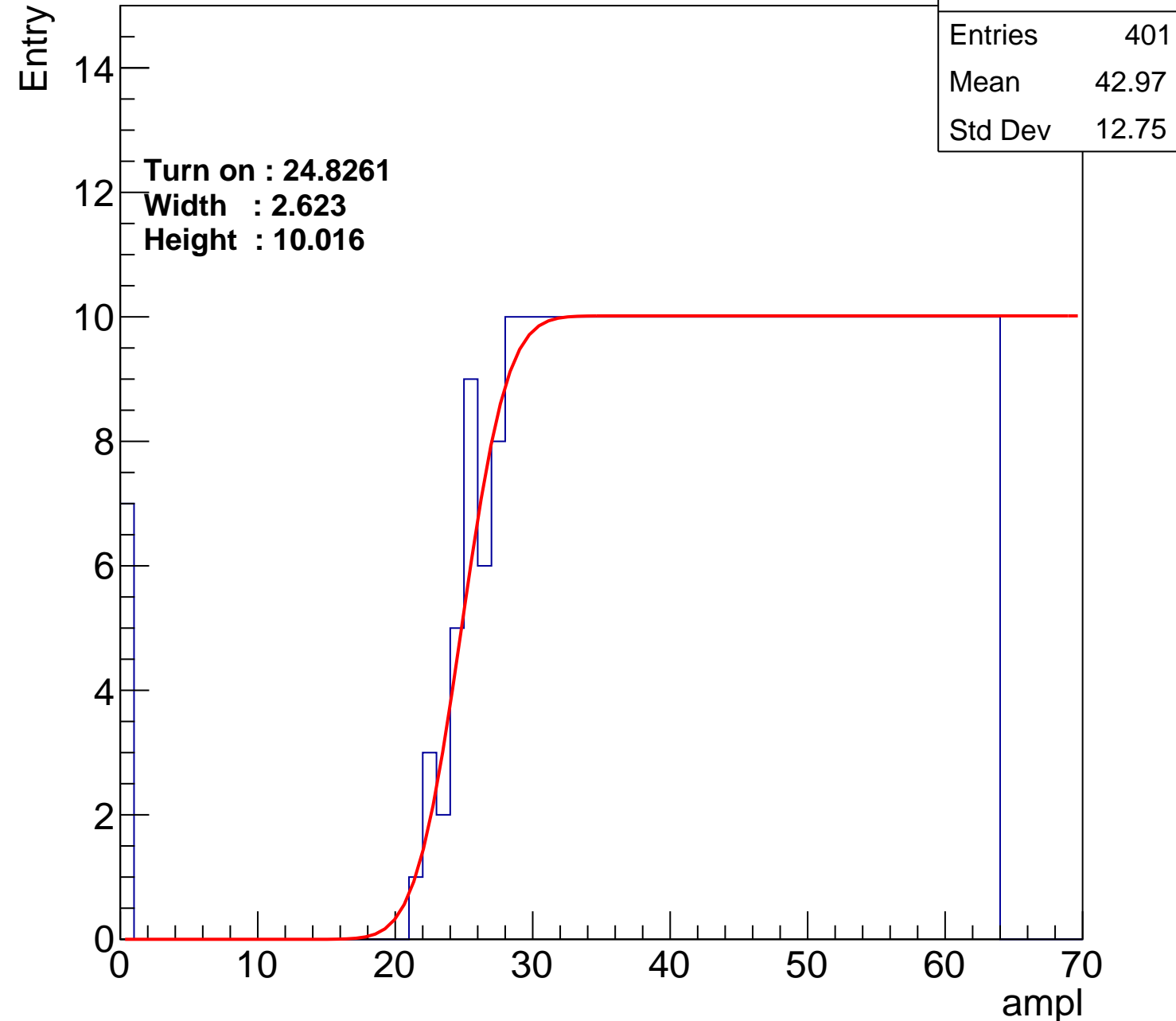
Width : 2.623

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 26.2123

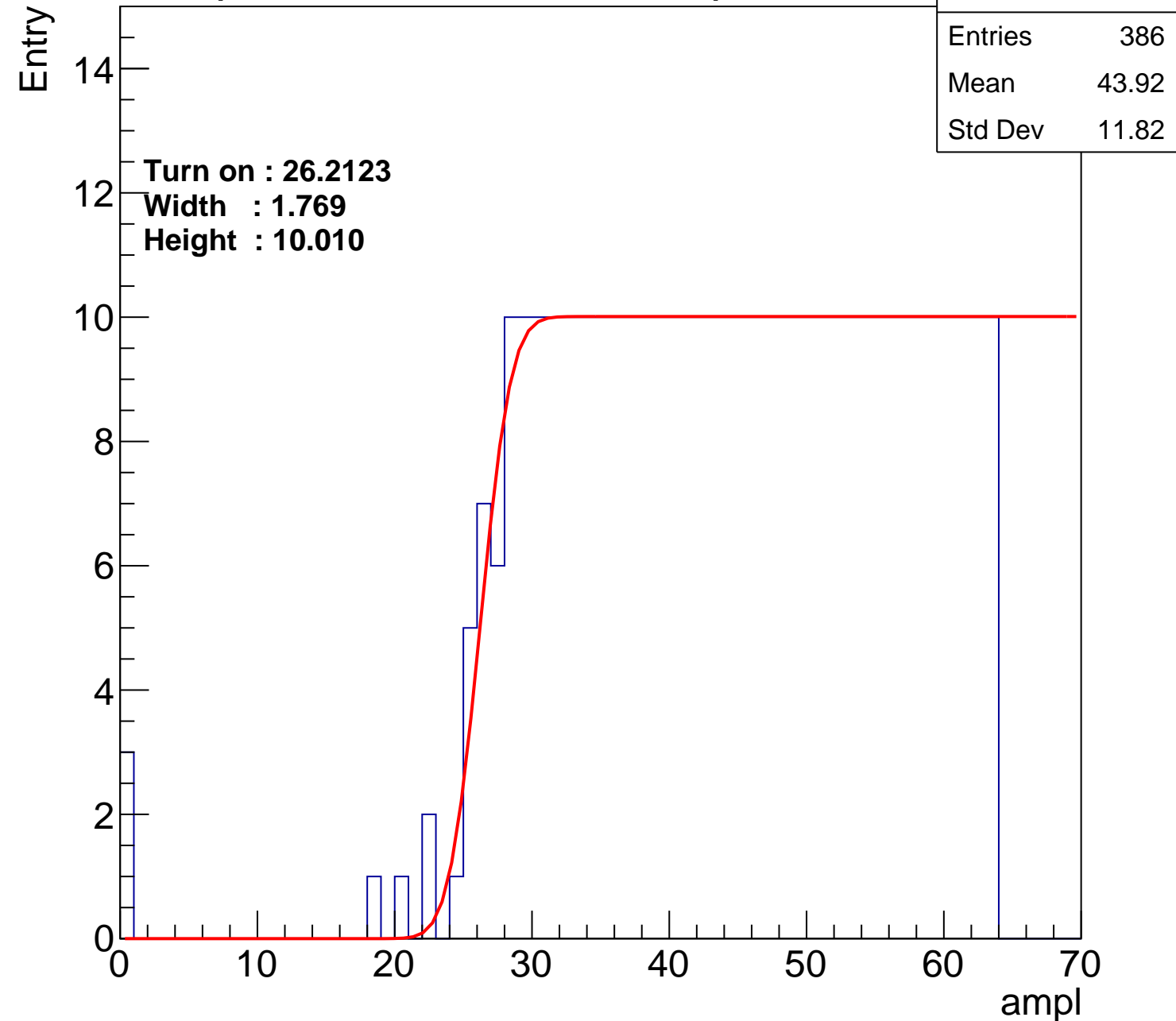
Width : 1.769

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.52
Std Dev	11.88

Turn on : 25.0731

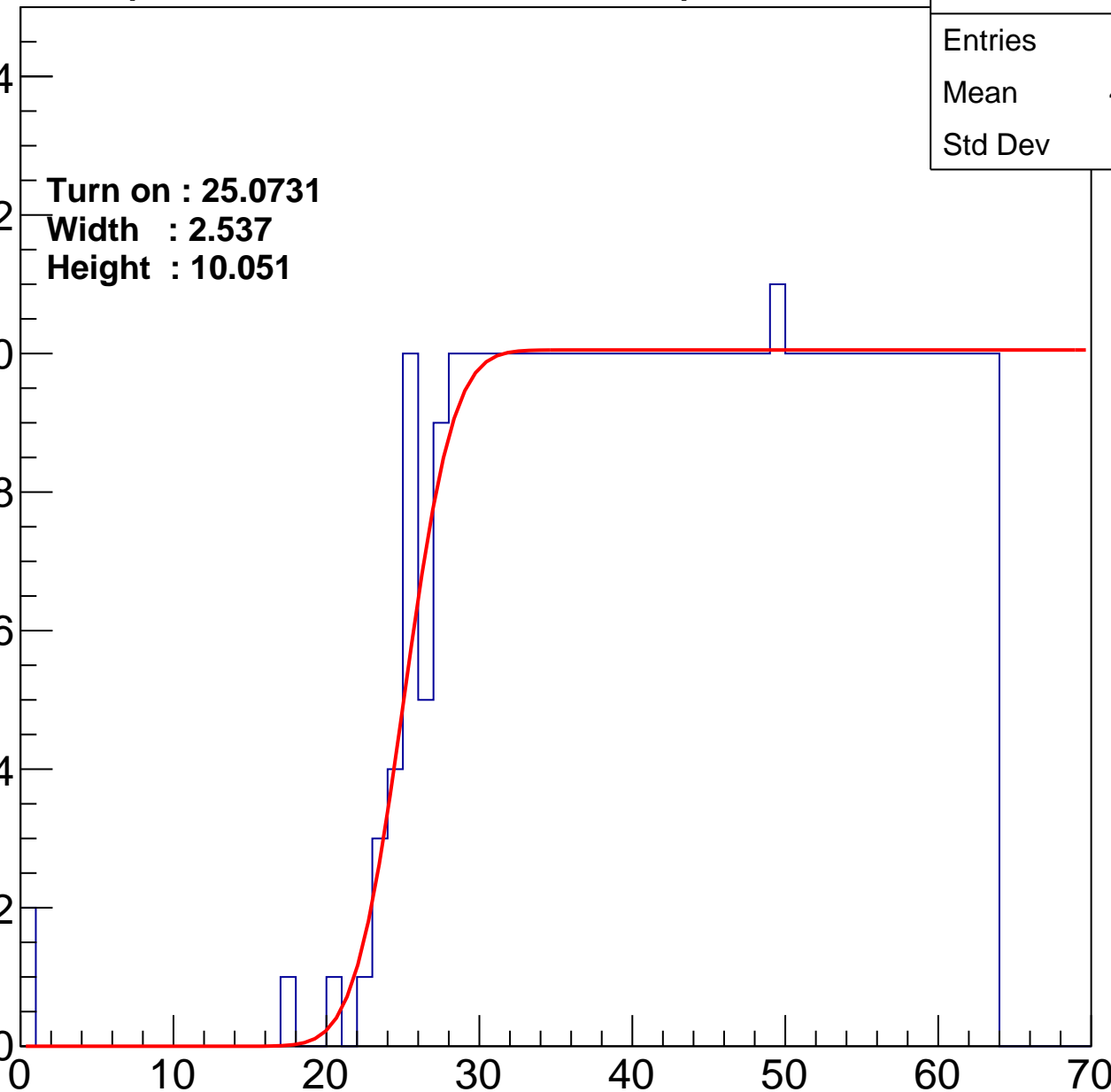
Width : 2.537

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	44
Std Dev	11.6

Turn on : 25.7228

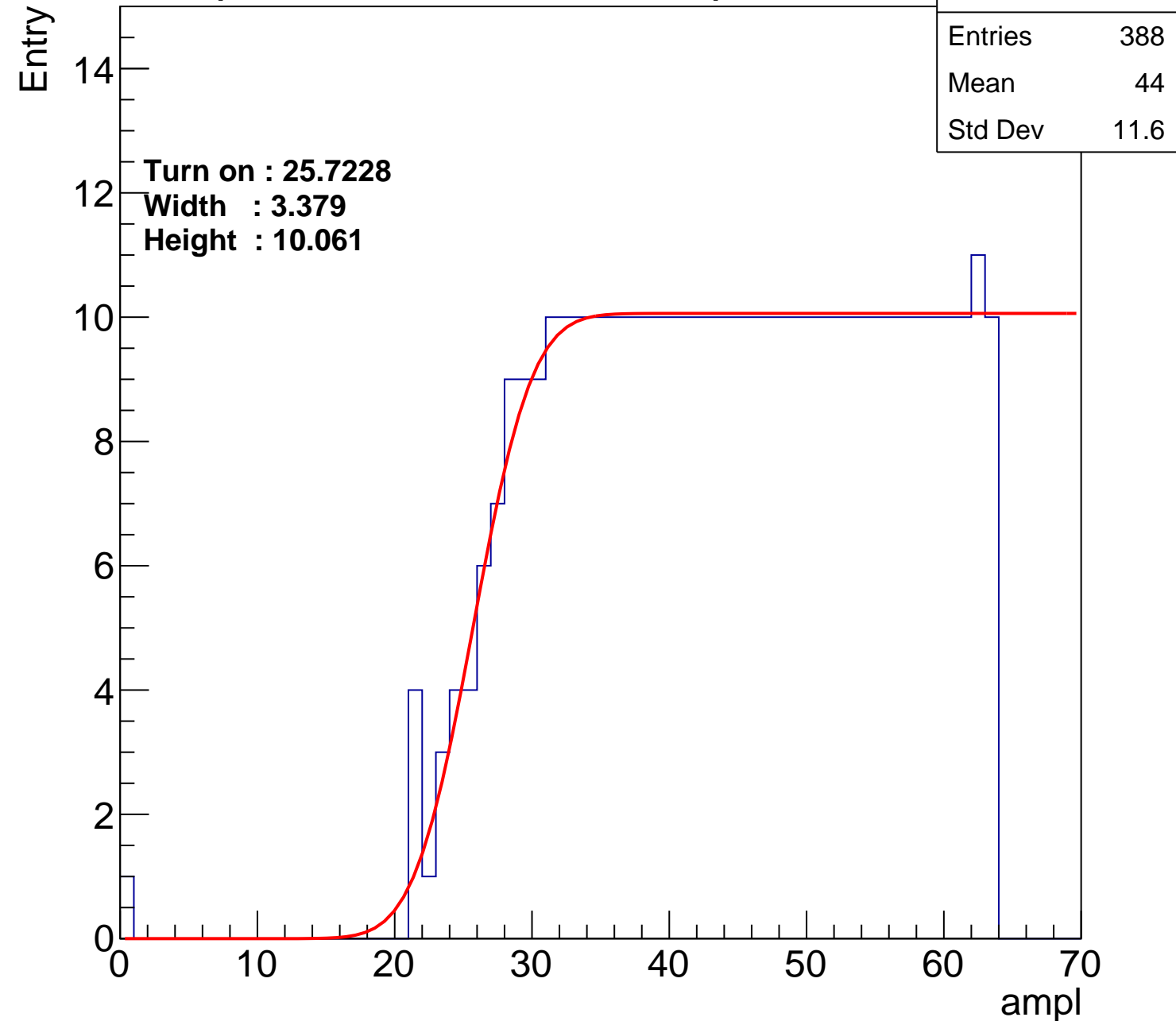
Width : 3.379

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.3
Std Dev	12.39

Turn on : 25.0539

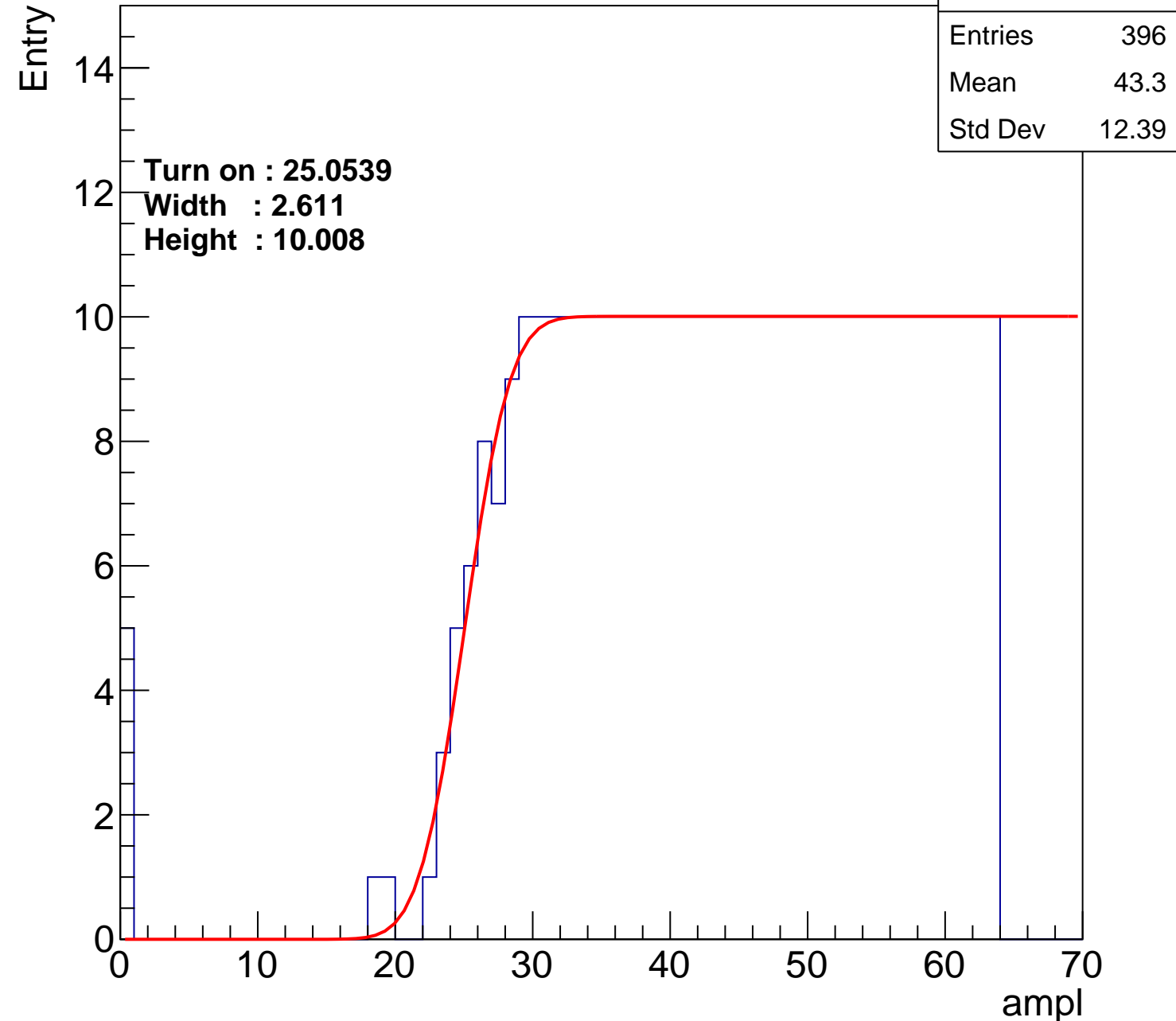
Width : 2.611

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch95

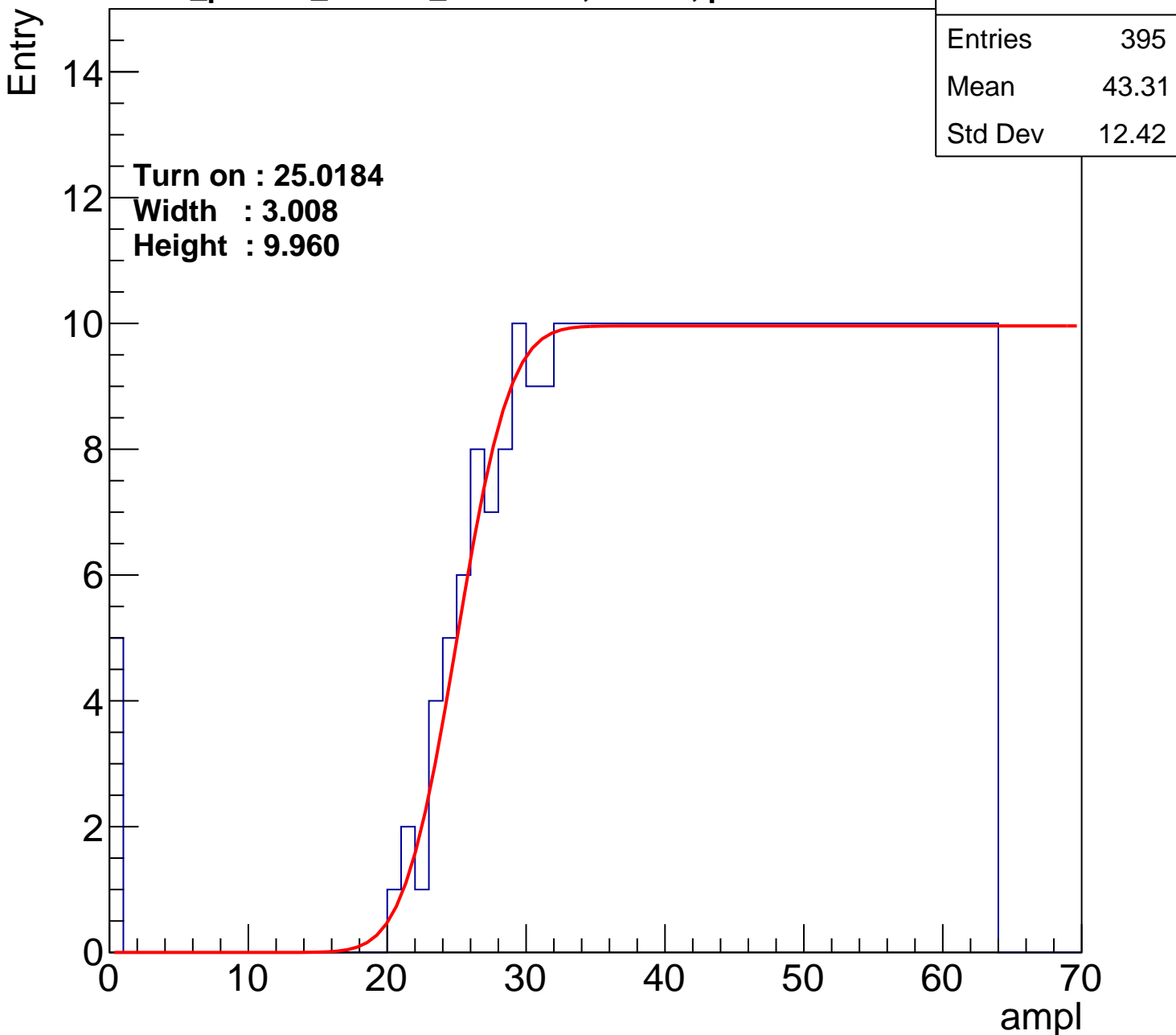
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.0184

Width : 3.008

Height : 9.960

Entries	395
Mean	43.31
Std Dev	12.42



B0L102S, U15-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.05
Std Dev	11.64

Turn on : 26.3363

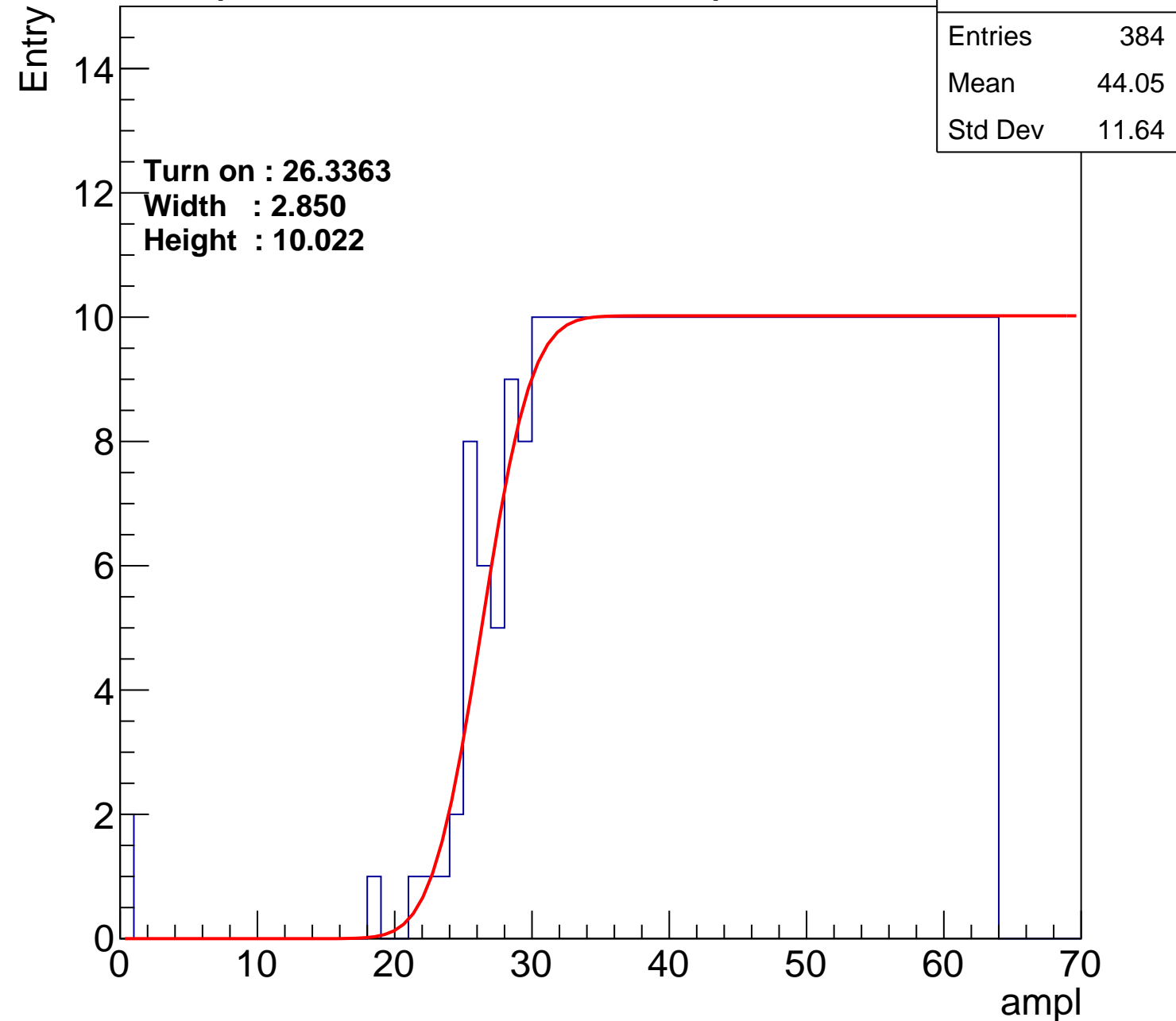
Width : 2.850

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.92
Std Dev	11.99

Turn on : 26.1062

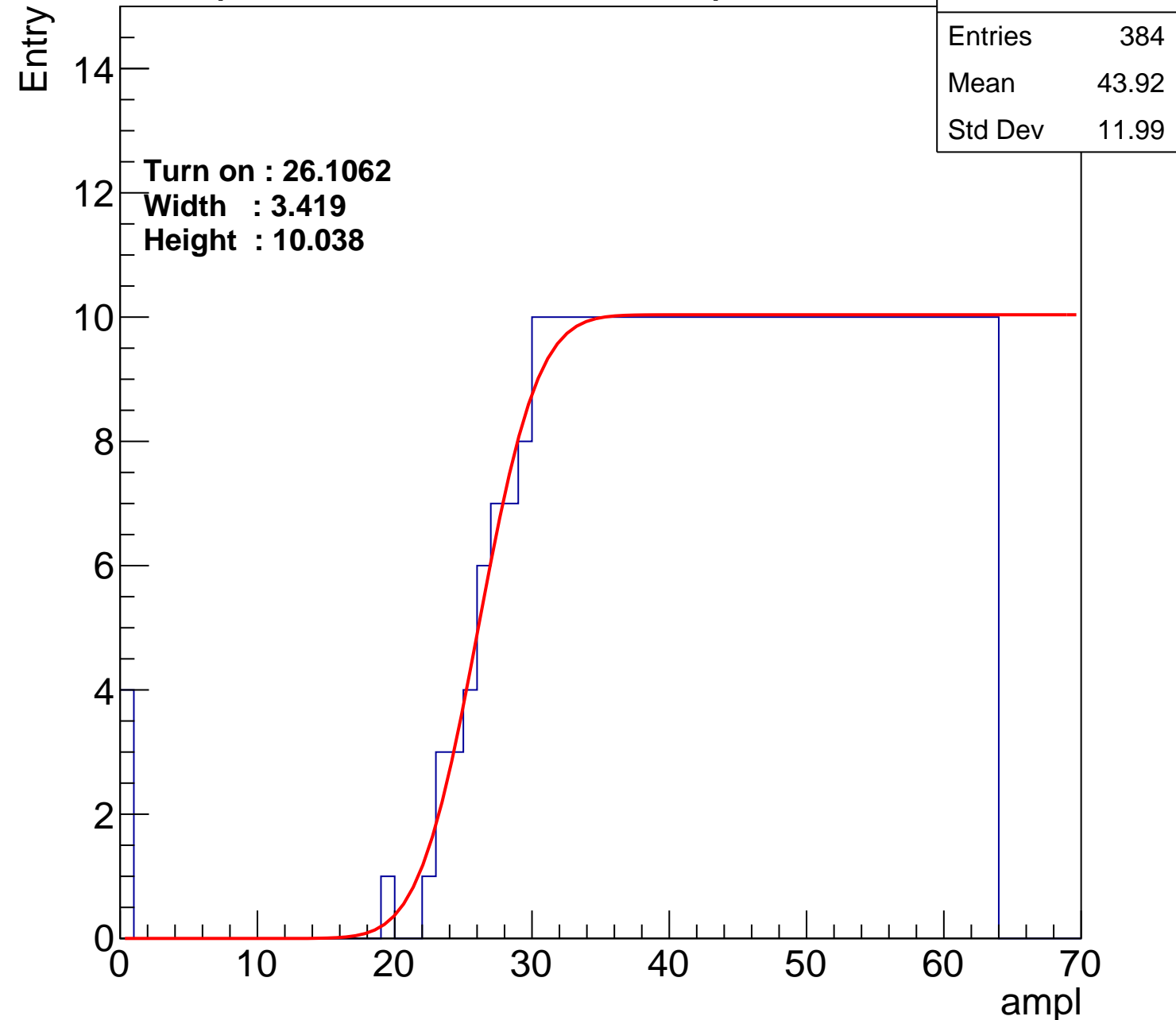
Width : 3.419

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch98

calib_packv5_042523_0143.root, FC#12, port B1

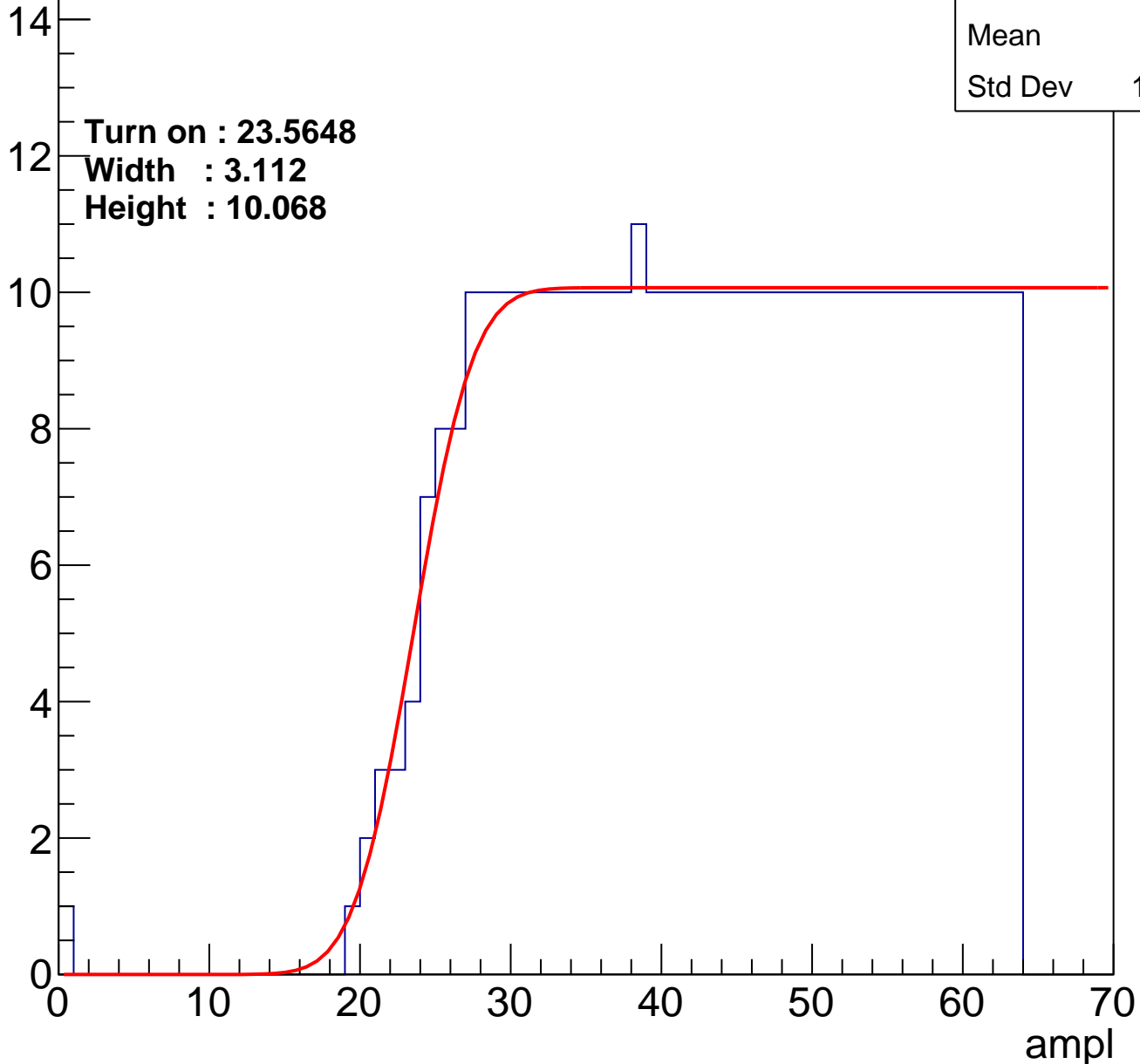
Entries	408
Mean	43
Std Dev	12.02

Turn on : 23.5648

Width : 3.112

Height : 10.068

Entry



B0L102S, U15-ch99

calib_packv5_042523_0143.root, FC#12, port B1

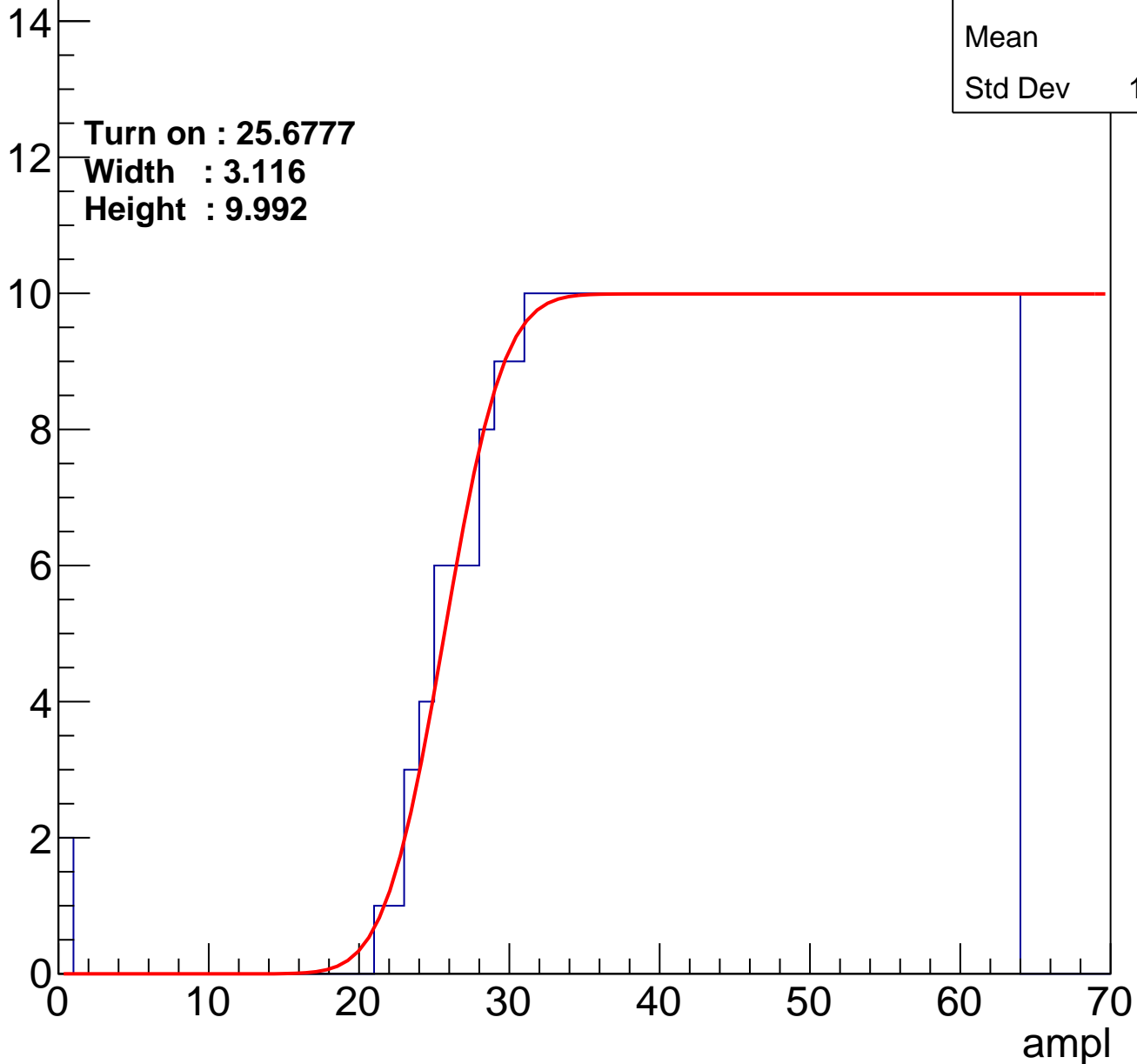
Entries	385
Mean	44
Std Dev	11.66

Turn on : 25.6777

Width : 3.116

Height : 9.992

Entry



B0L102S, U15-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.34
Std Dev	12.3

Turn on : 25.4987

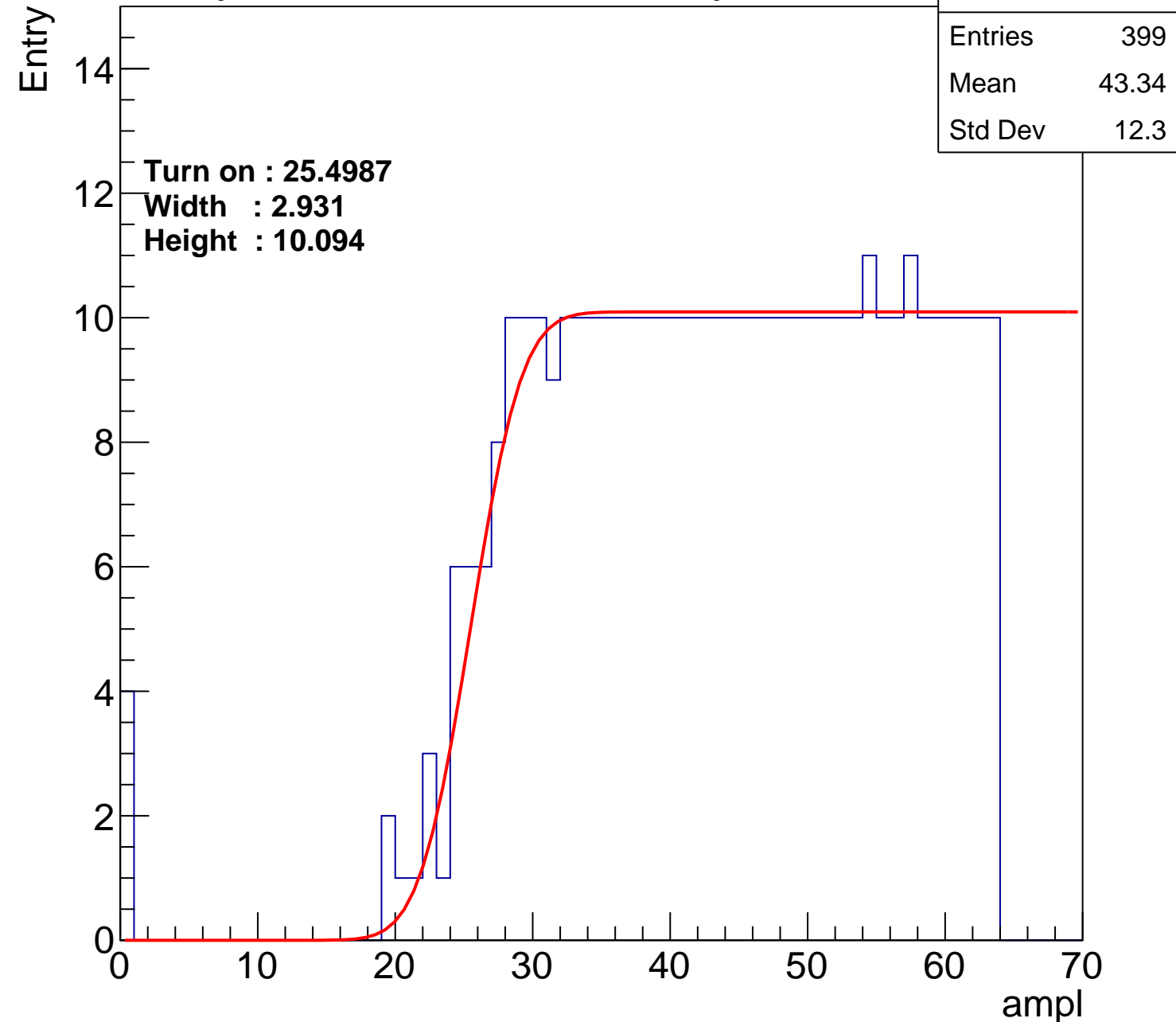
Width : 2.931

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch101

calib_packv5_042523_0143.root, FC#12, port B1

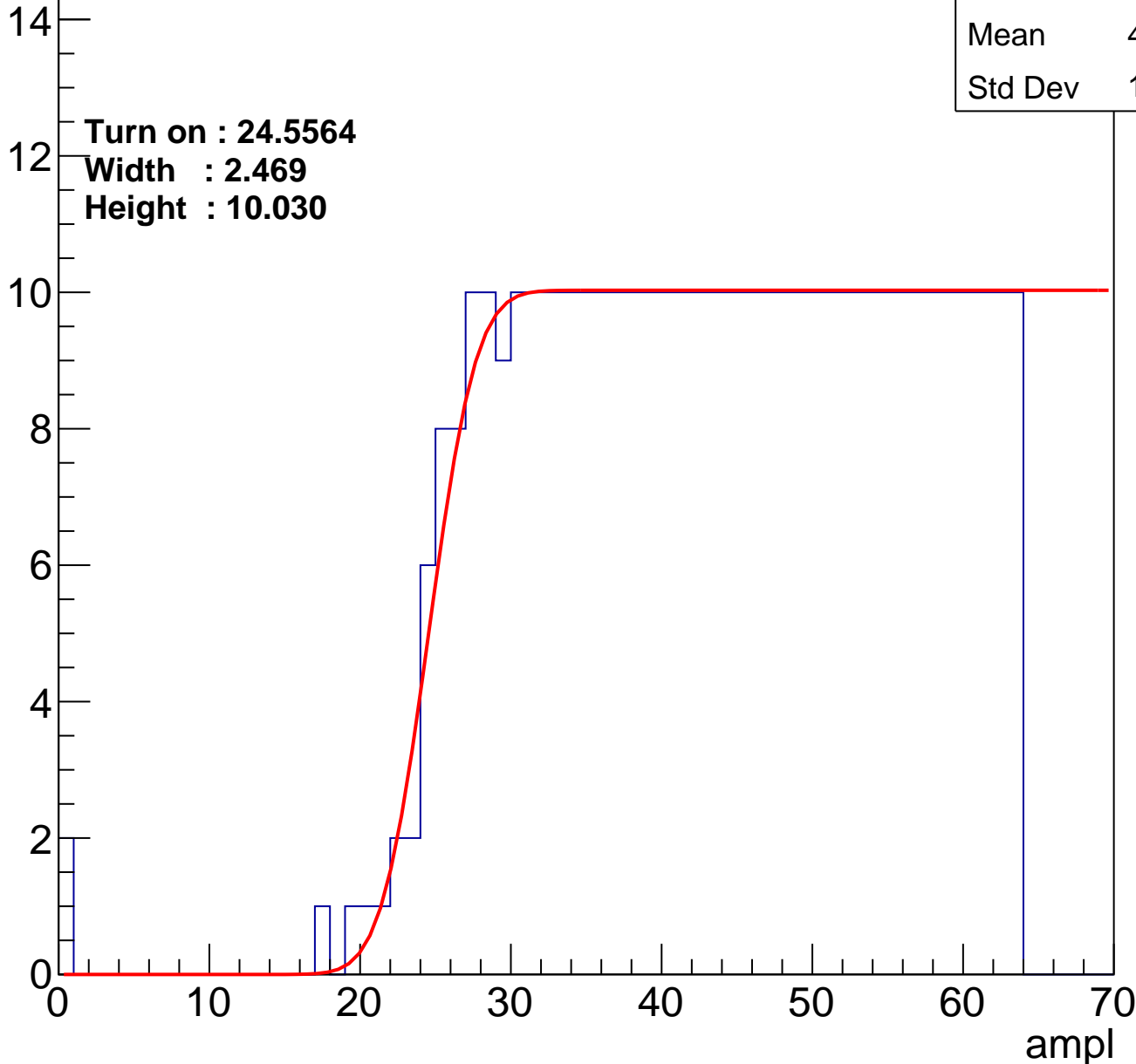
Entries	401
Mean	43.24
Std Dev	12.04

Turn on : 24.5564

Width : 2.469

Height : 10.030

Entry



B0L102S, U15-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.16
Std Dev	11.44

Turn on : 25.5534

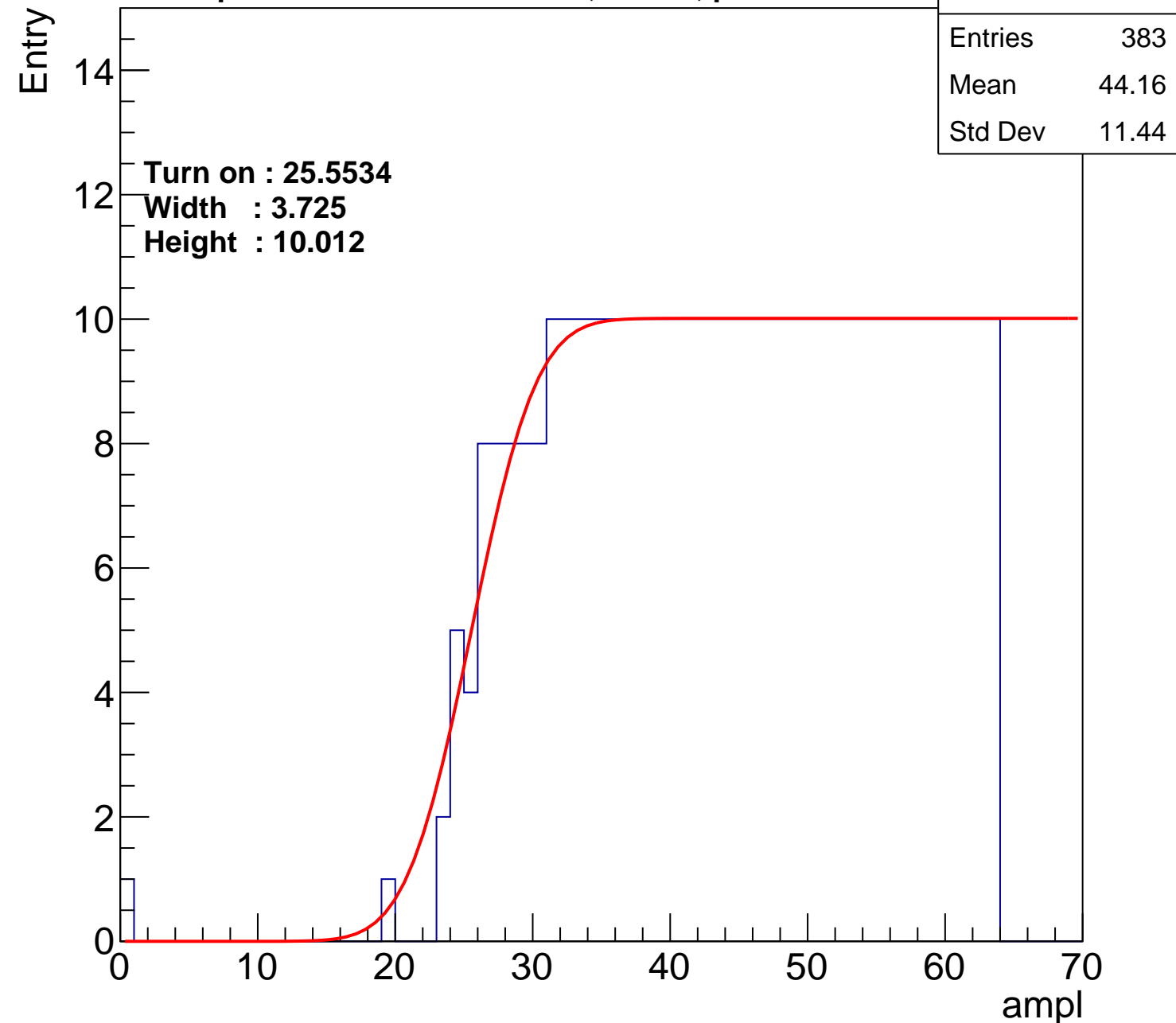
Width : 3.725

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.16
Std Dev	12.27

Turn on : 24.0821

Width : 3.715

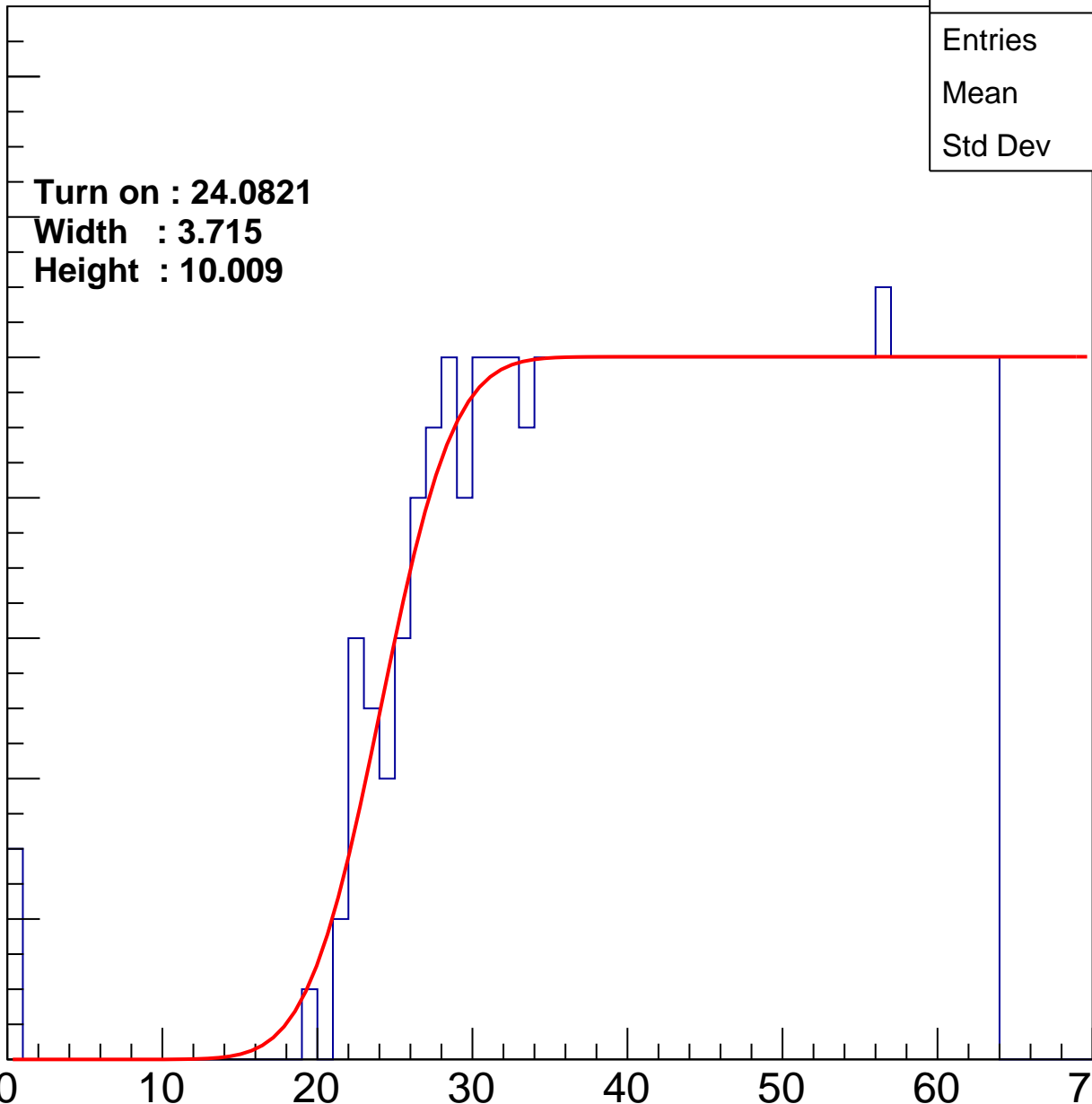
Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U15-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.87
Std Dev	11.96

Turn on : 25.9653

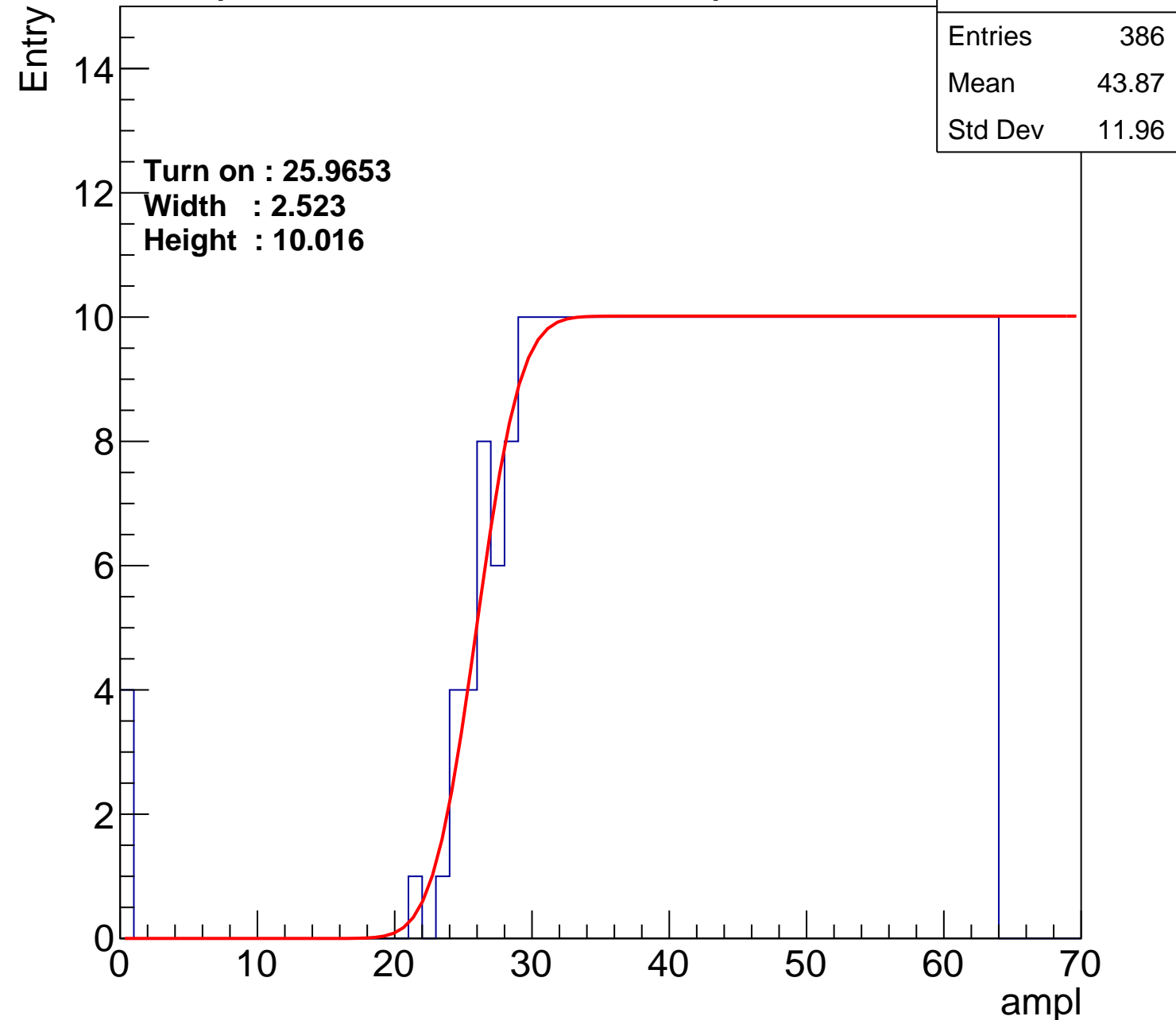
Width : 2.523

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch105

calib_packv5_042523_0143.root, FC#12, port B1

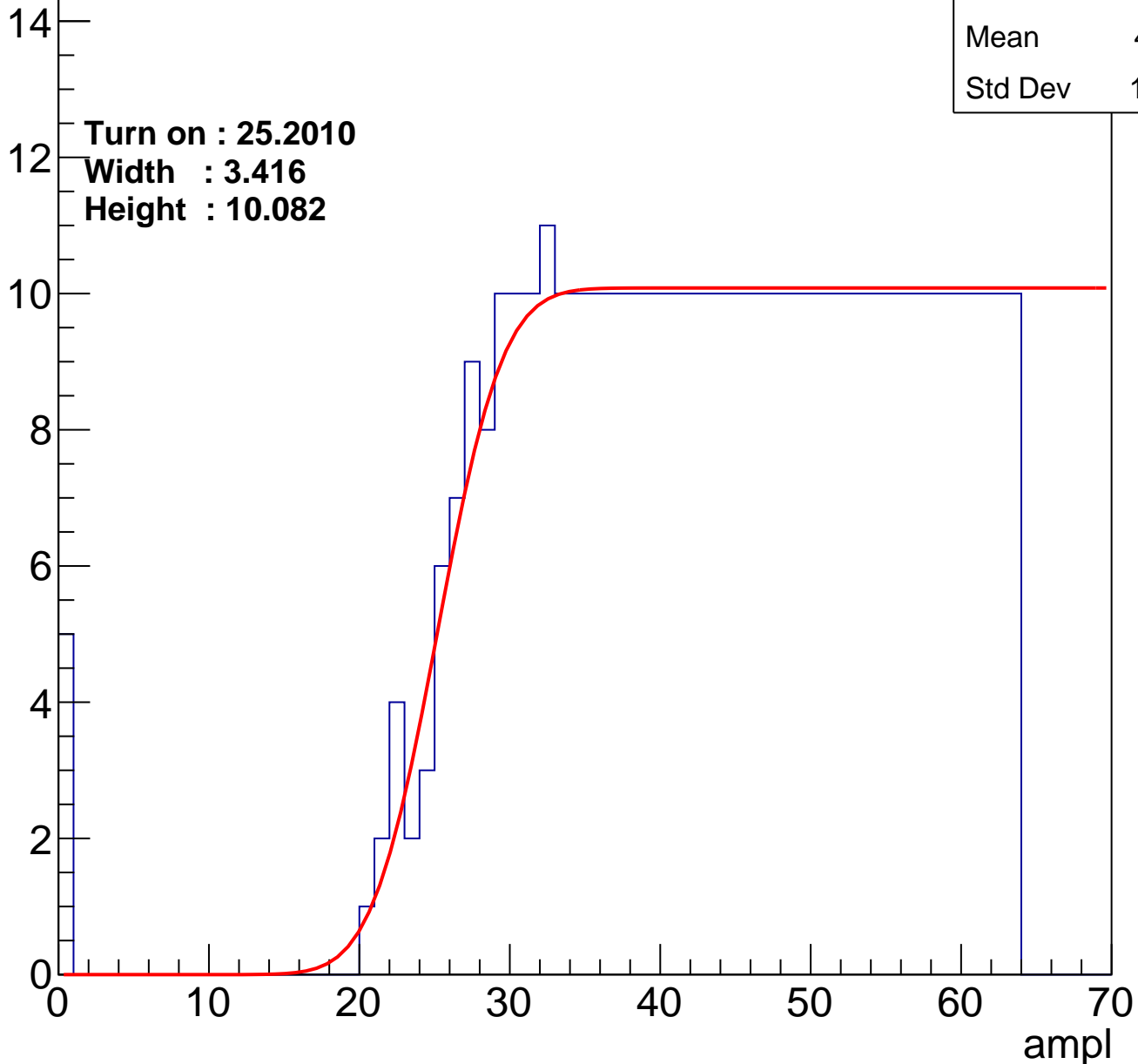
Entries	398
Mean	43.21
Std Dev	12.42

Turn on : 25.2010

Width : 3.416

Height : 10.082

Entry



B0L102S, U15-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.77
Std Dev	12.12

Turn on : 26.5506

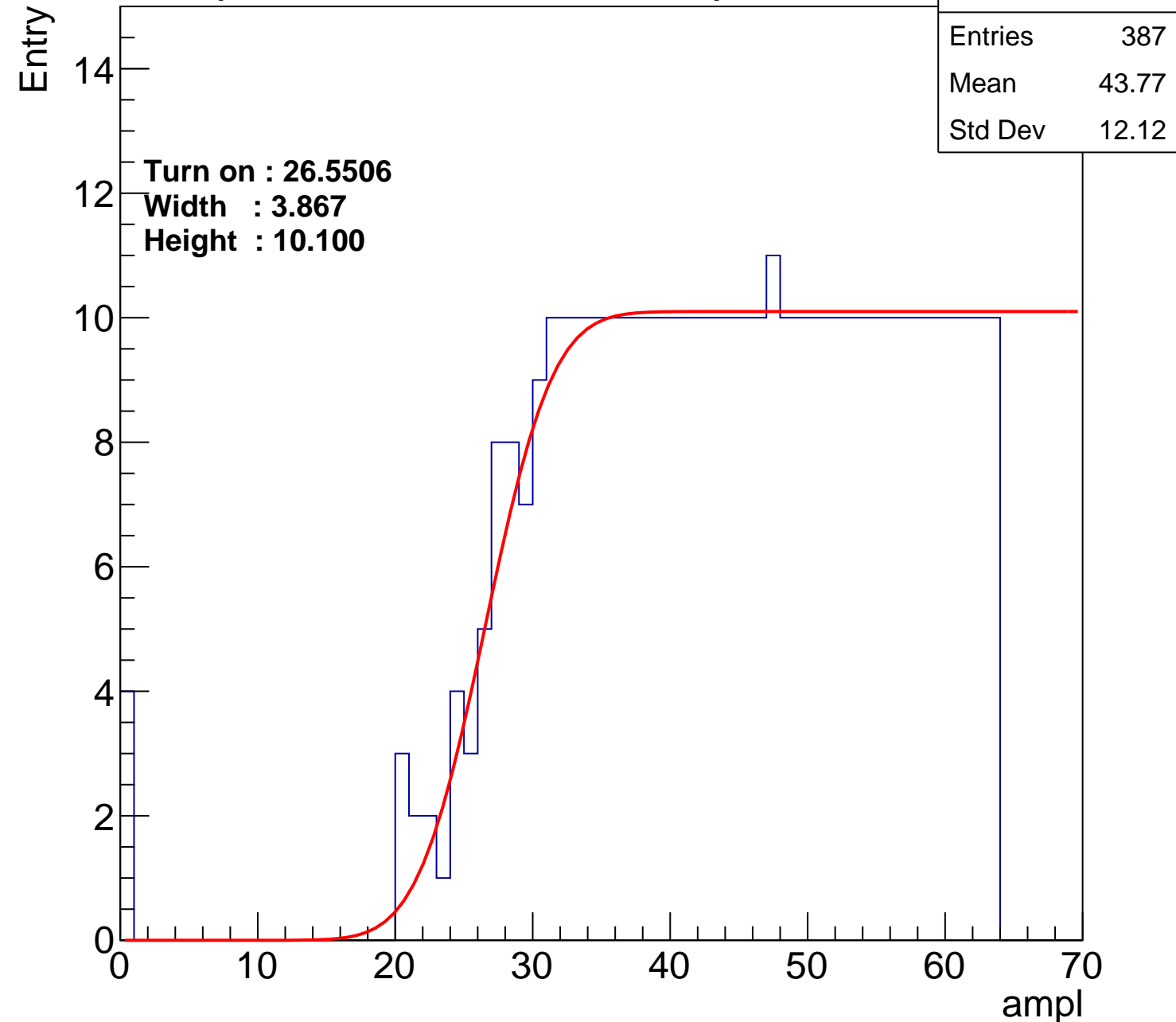
Width : 3.867

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.29
Std Dev	11.35

Turn on : 26.2079

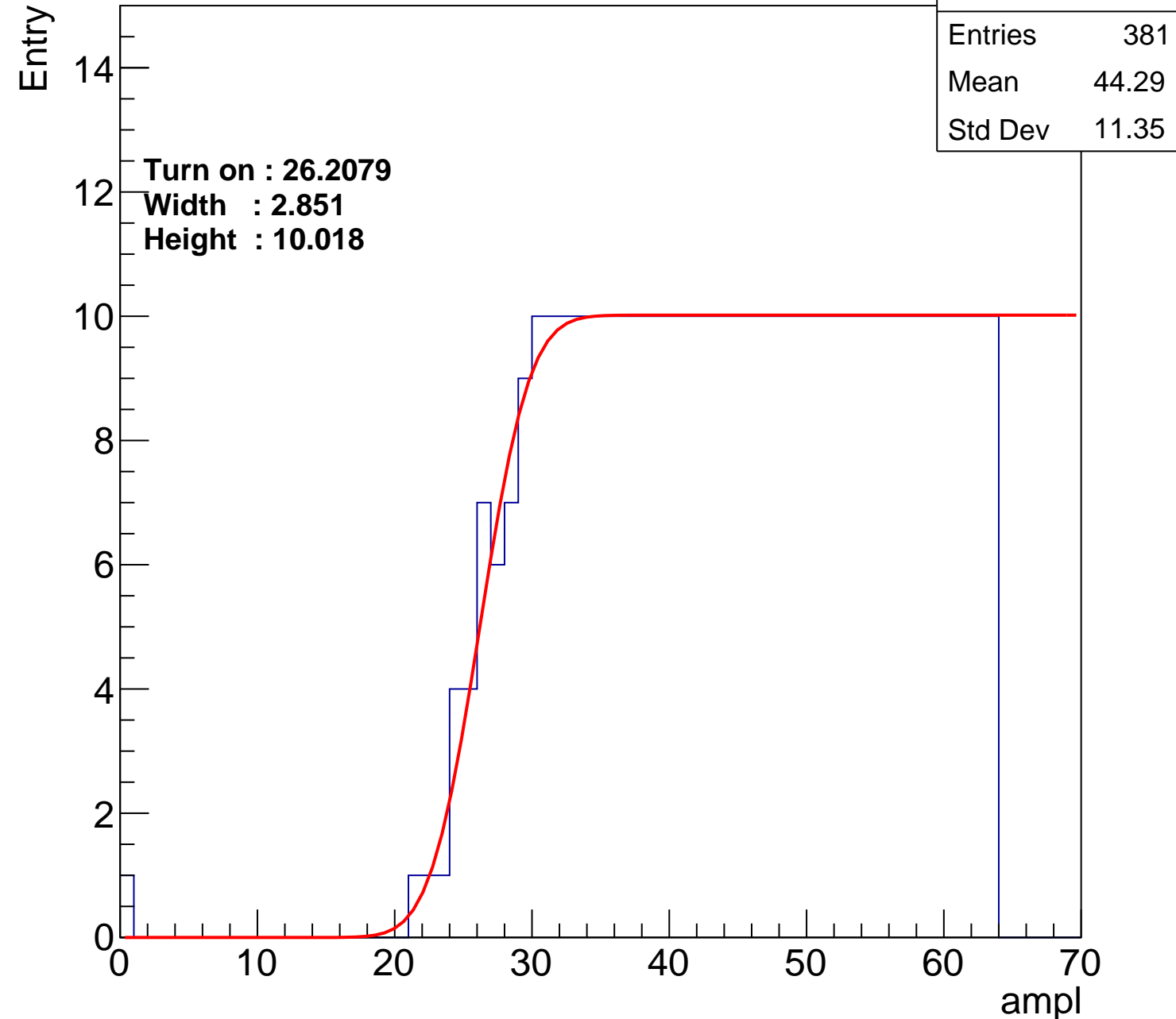
Width : 2.851

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.68
Std Dev	12.16

Turn on : 25.8874

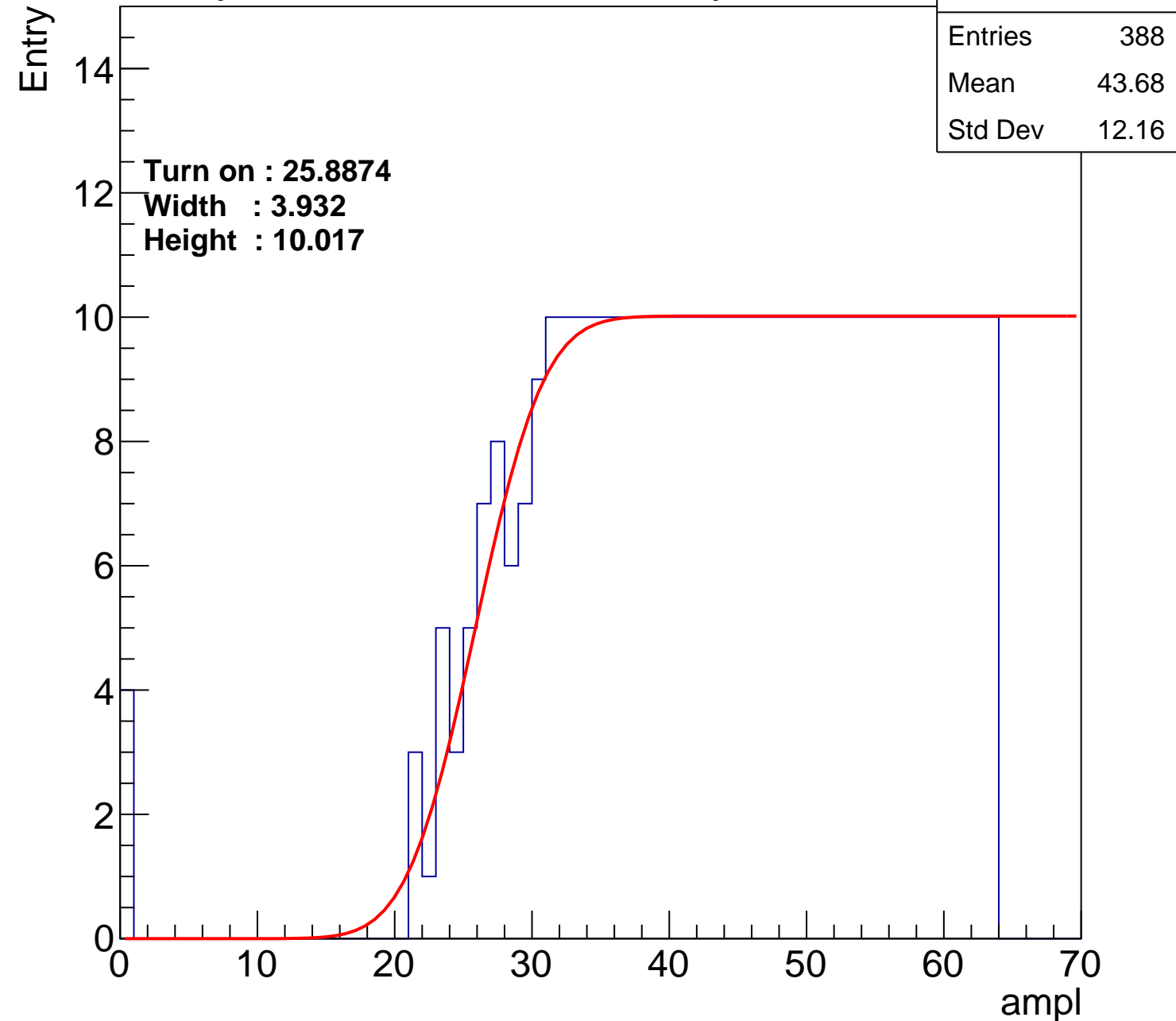
Width : 3.932

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.44
Std Dev	12.6

Turn on : 25.2810

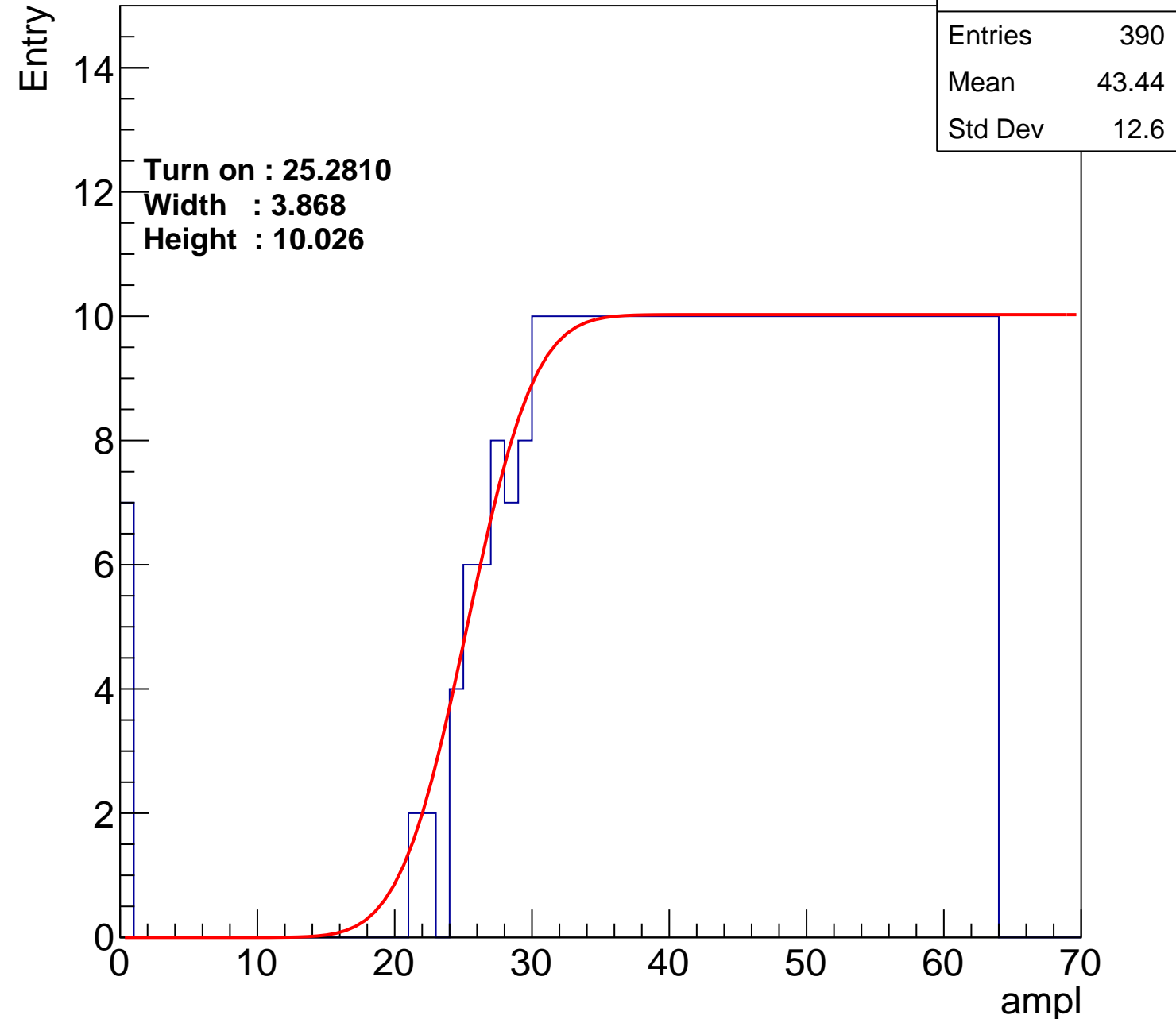
Width : 3.868

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.17
Std Dev	12.16

Turn on : 24.3347

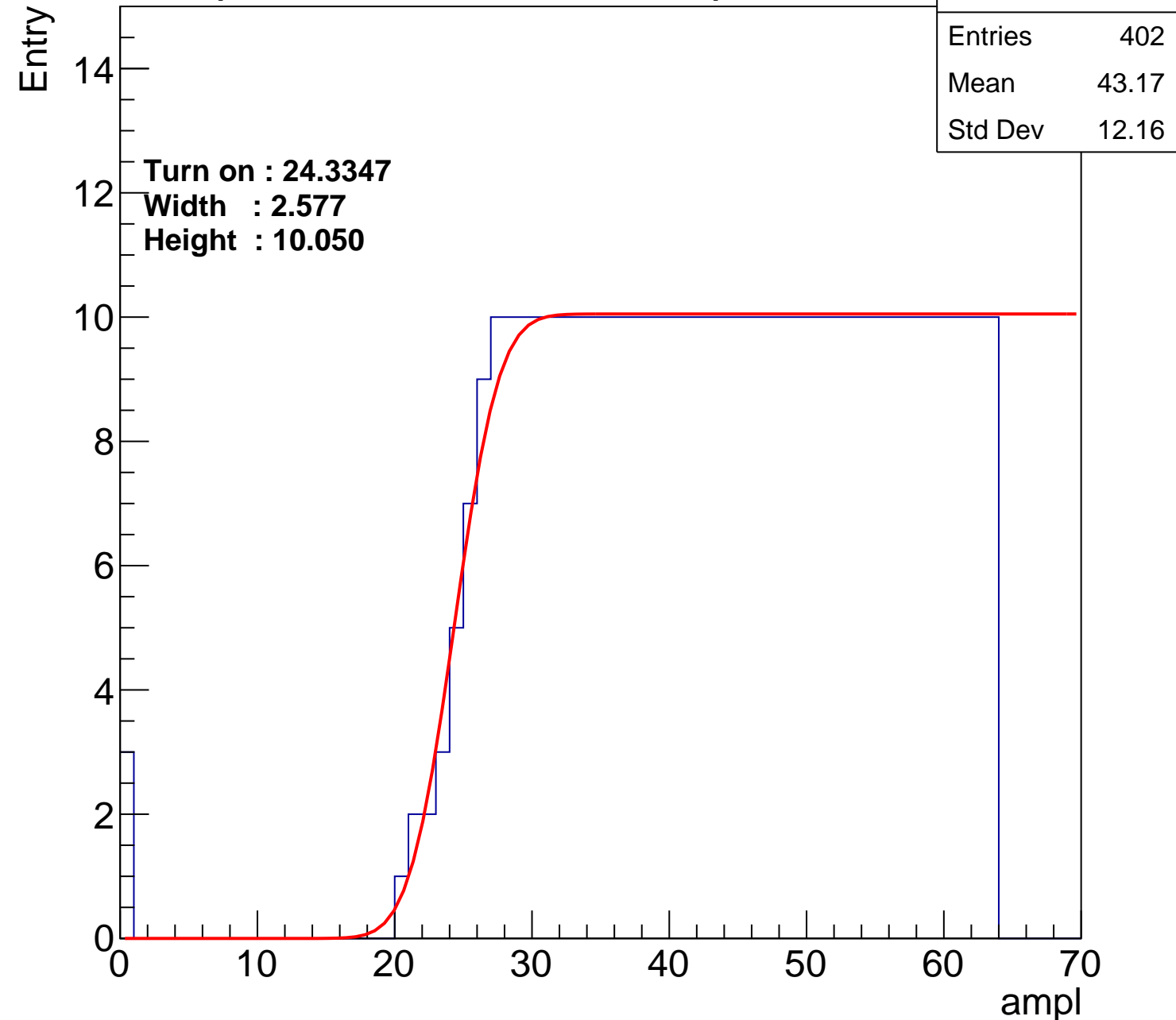
Width : 2.577

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.86
Std Dev	12.15

Turn on : 26.2755

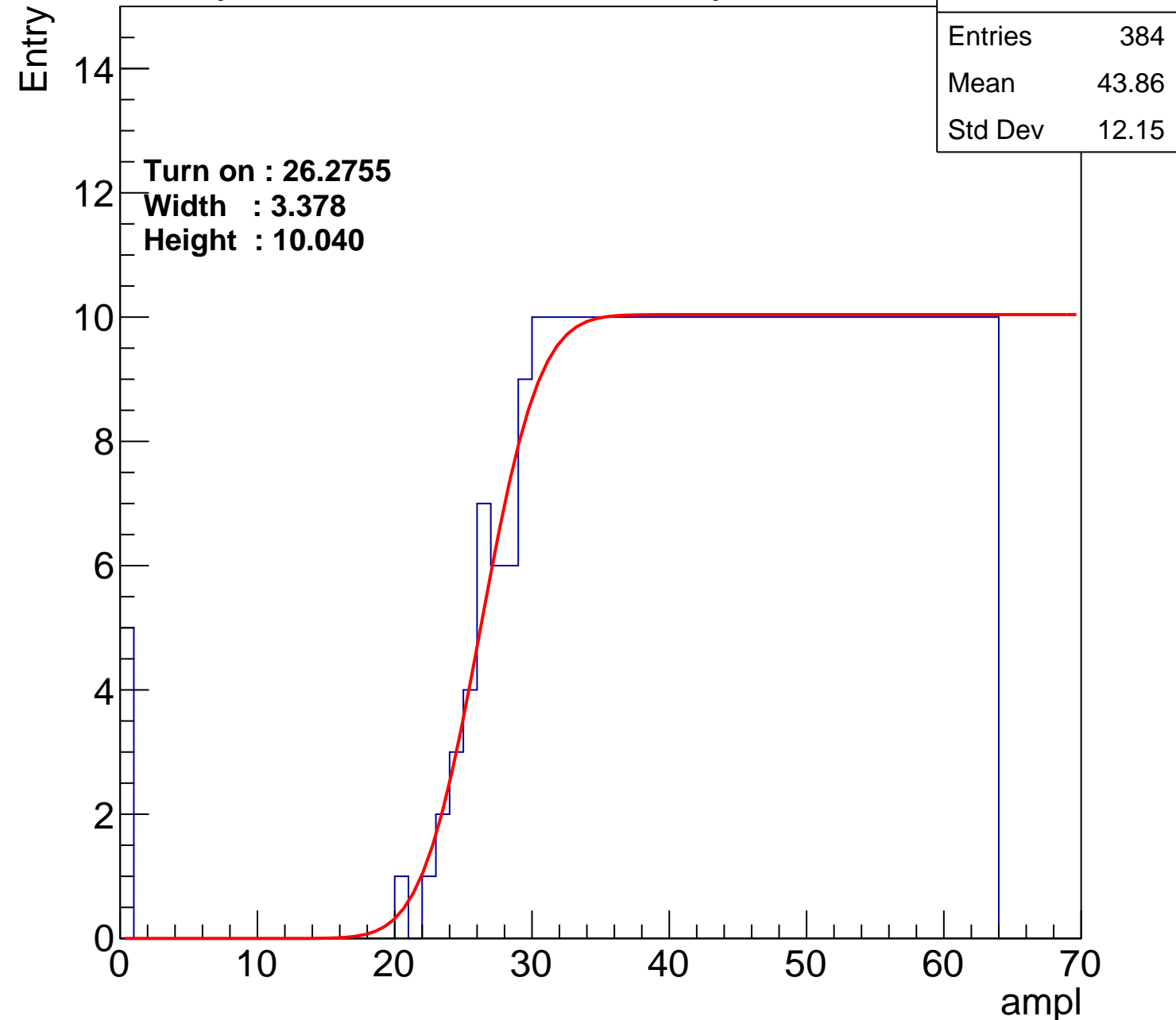
Width : 3.378

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch112

calib_packv5_042523_0143.root, FC#12, port B1

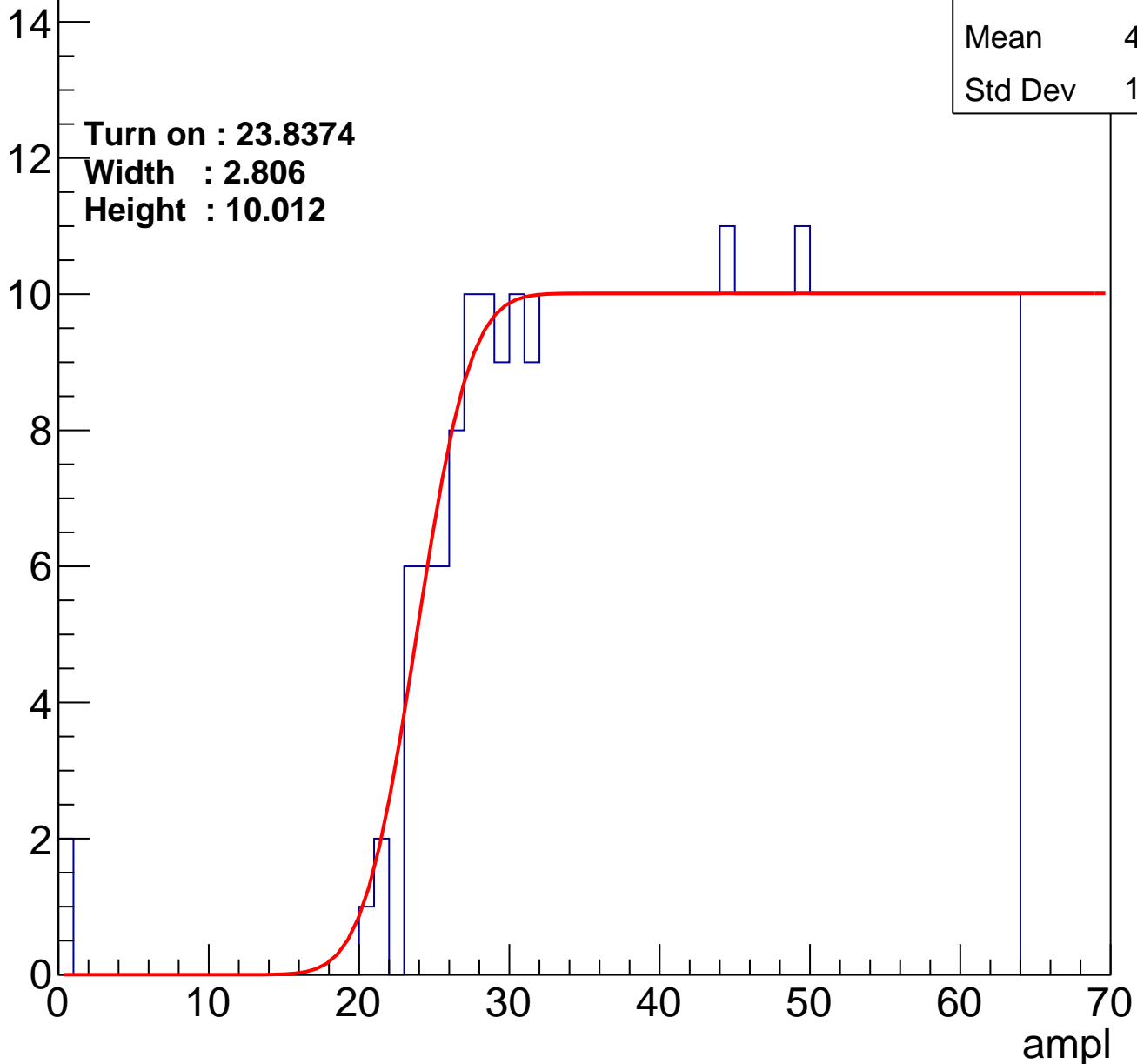
Entries	401
Mean	43.35
Std Dev	11.96

Turn on : 23.8374

Width : 2.806

Height : 10.012

Entry



B0L102S, U15-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.03
Std Dev	12.65

Turn on : 24.6261

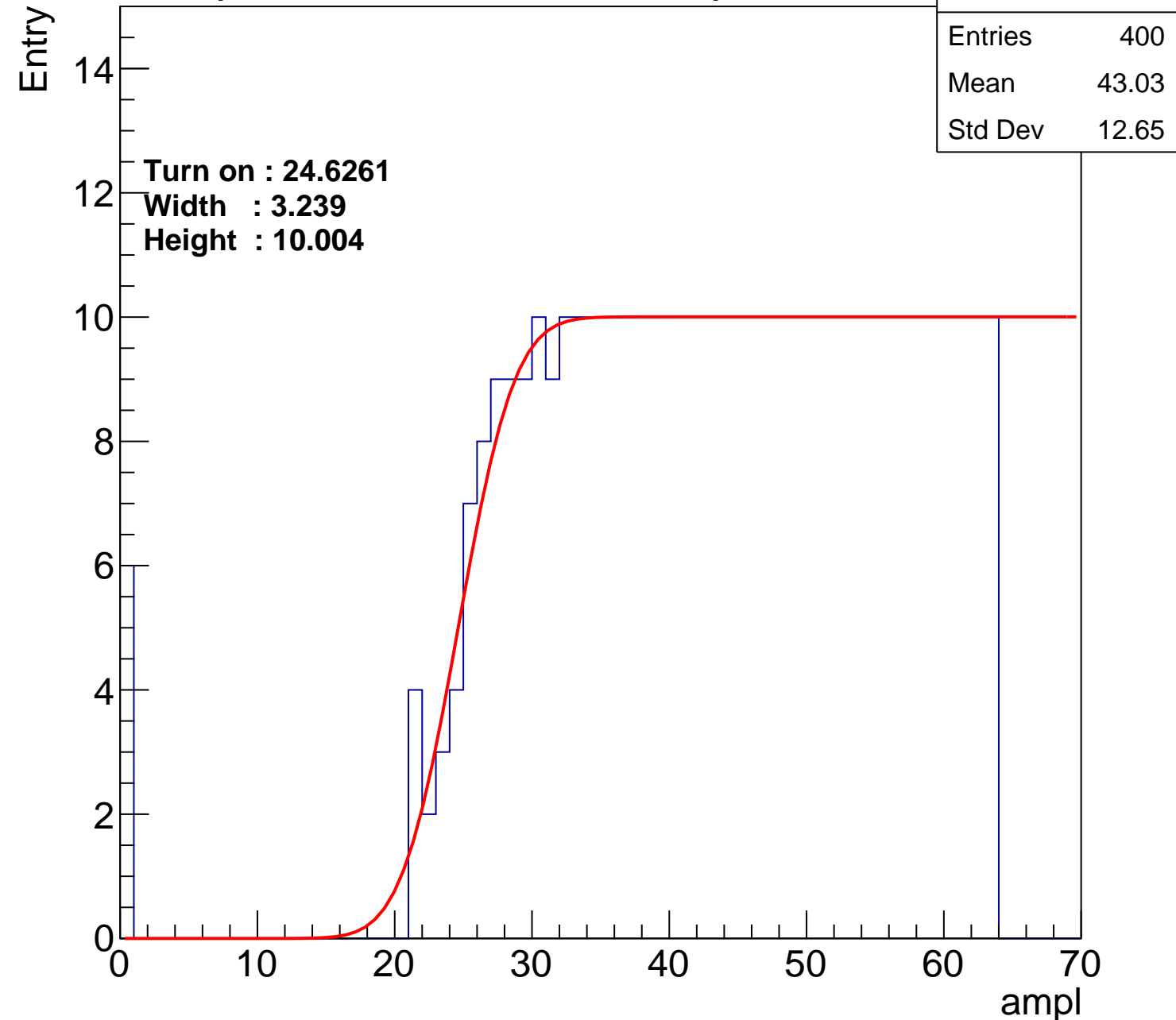
Width : 3.239

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.29
Std Dev	12.17

Turn on : 25.0529

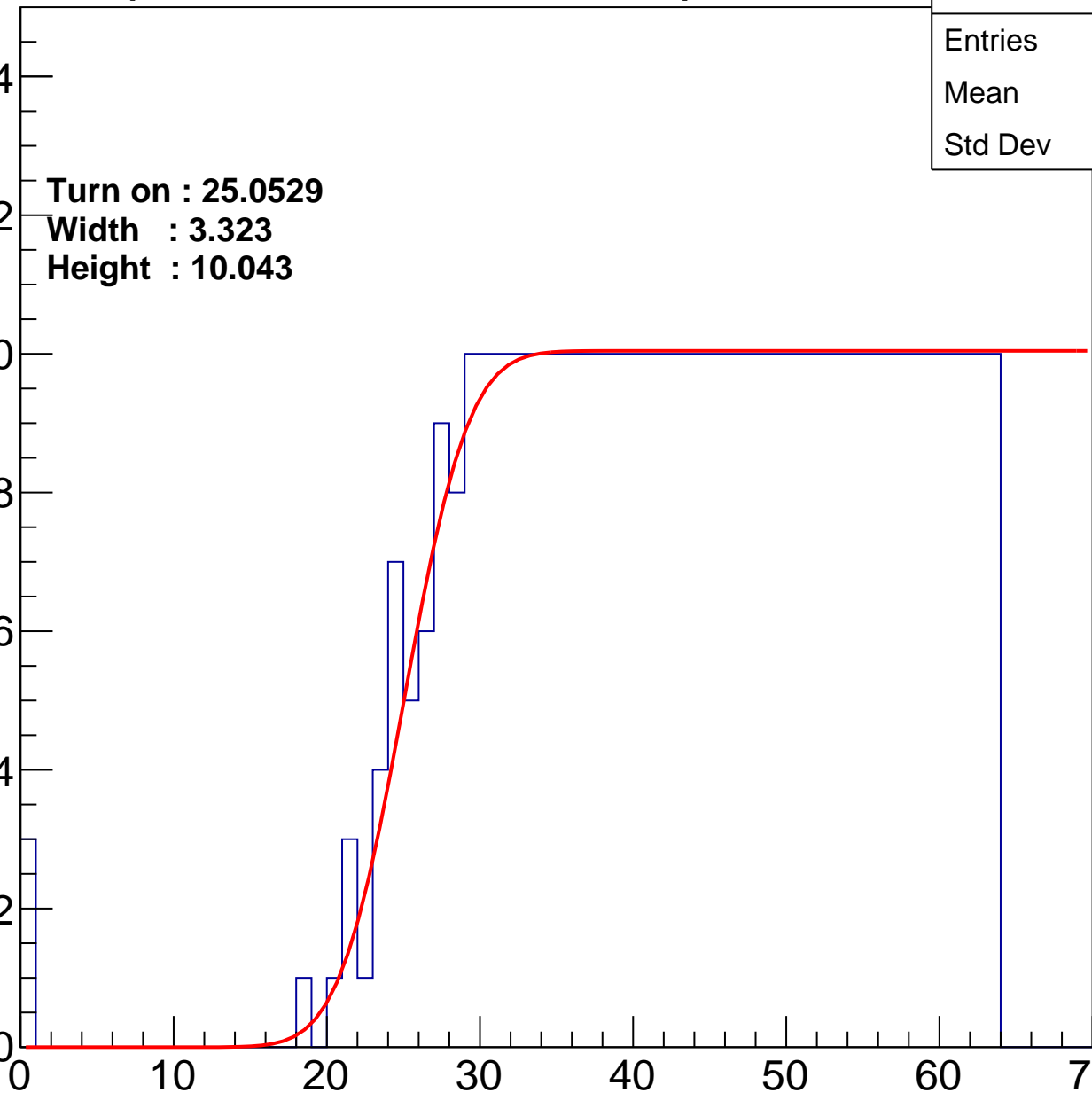
Width : 3.323

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.23
Std Dev	11.69

Turn on : 26.6821

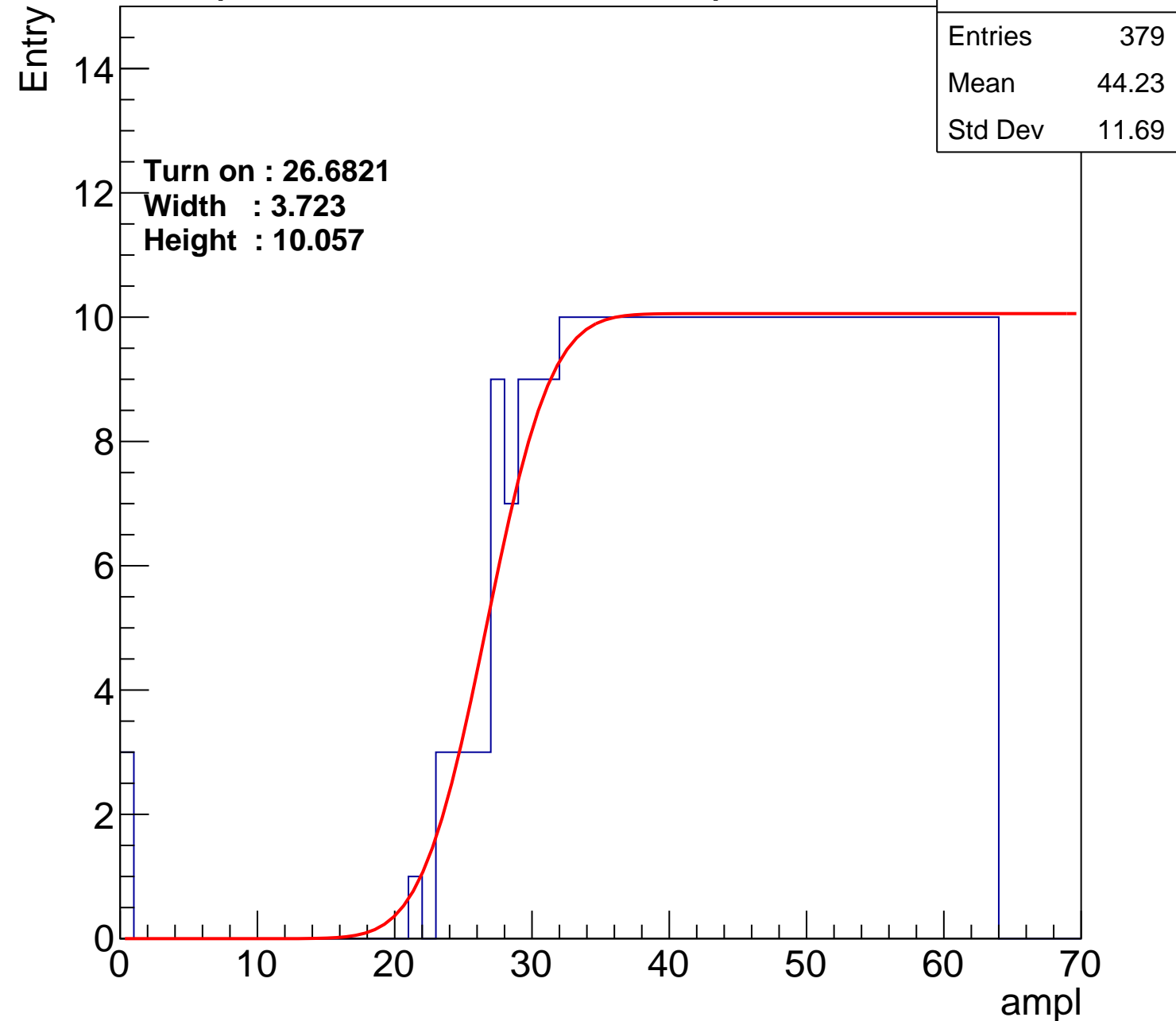
Width : 3.723

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.49
Std Dev	12.07

Turn on : 24.6074

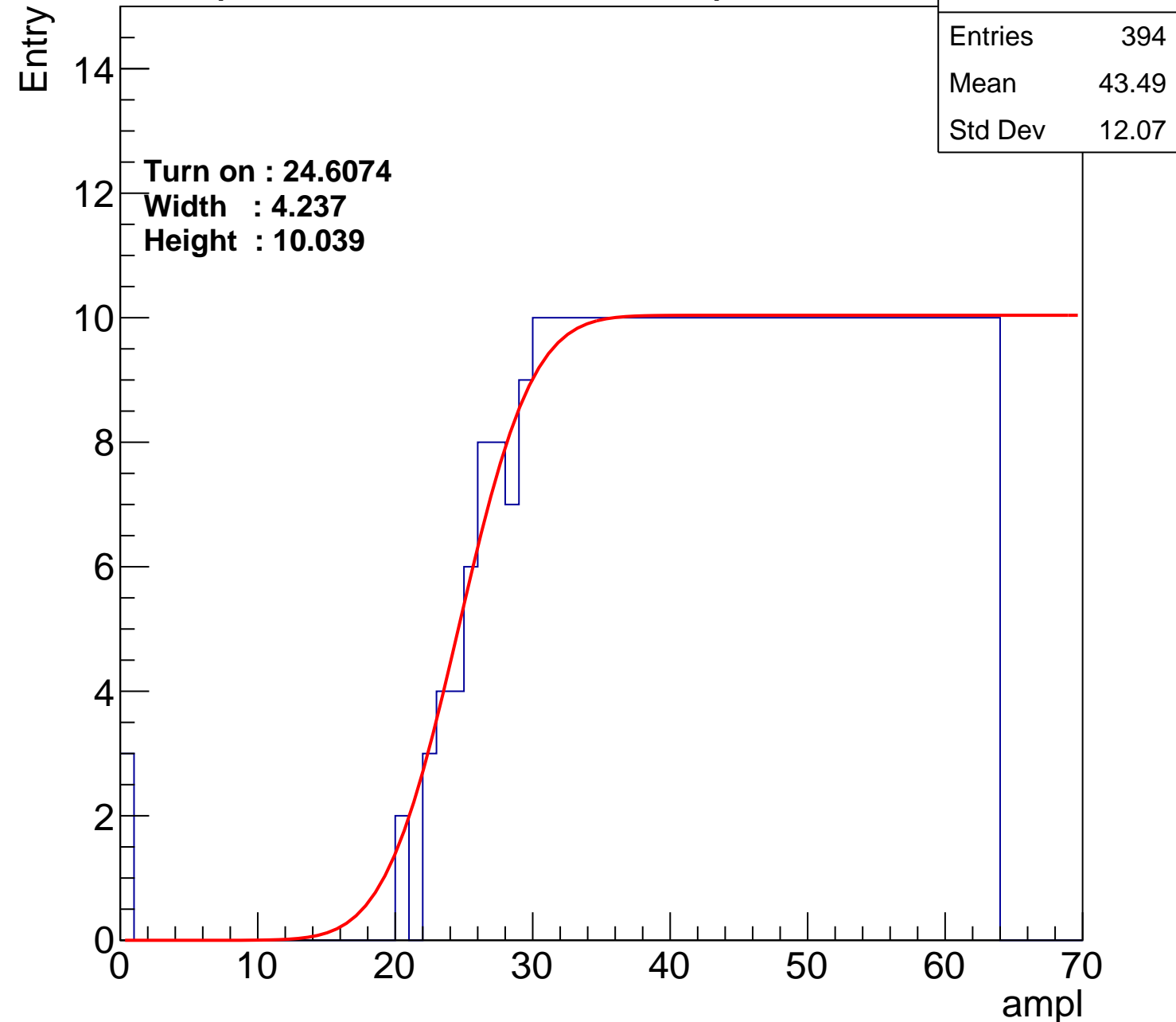
Width : 4.237

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	355
Mean	45.43
Std Dev	11.14

Turn on : 28.7810

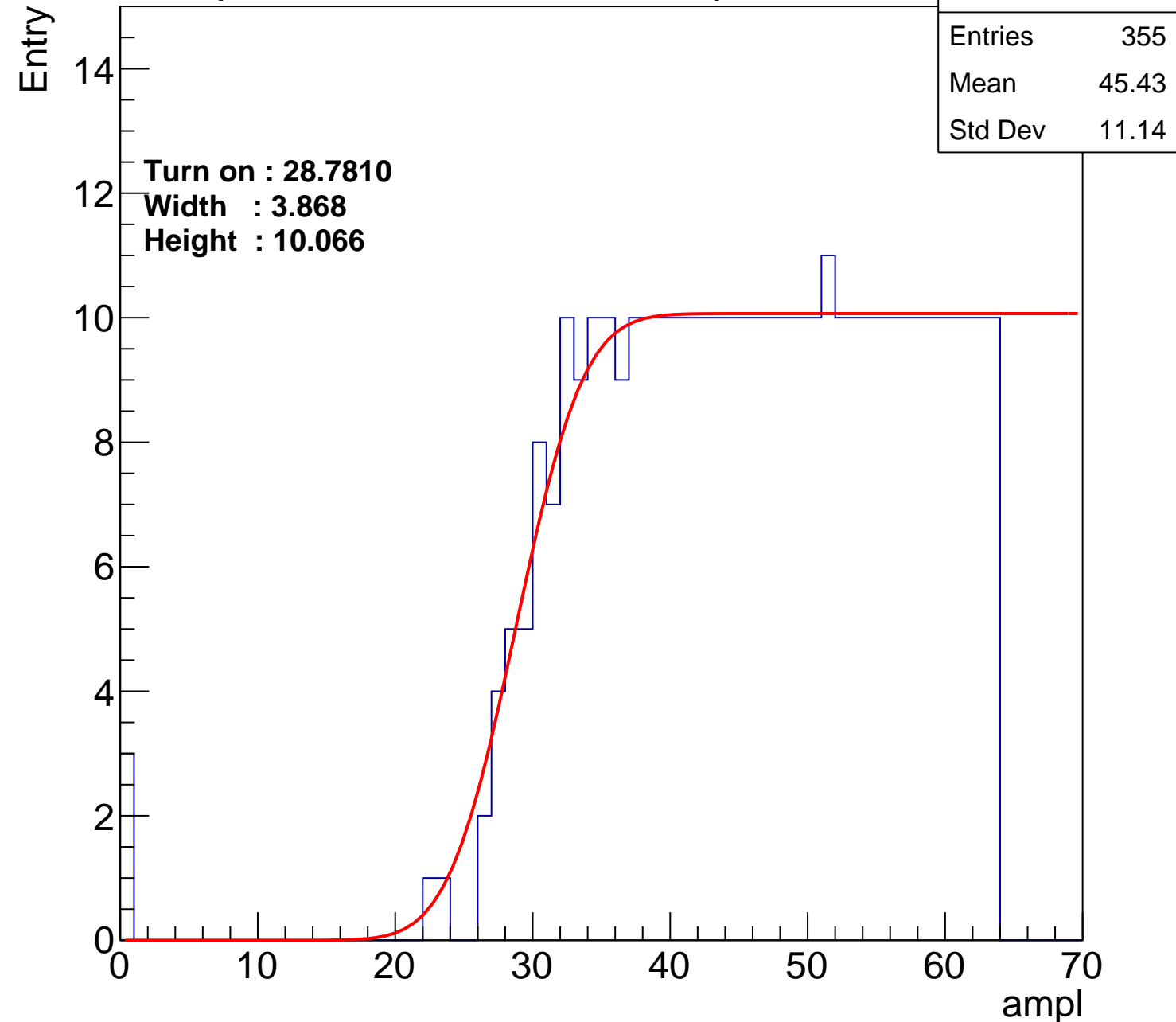
Width : 3.868

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	413
Mean	42.59
Std Dev	12.45

Turn on : 23.2449

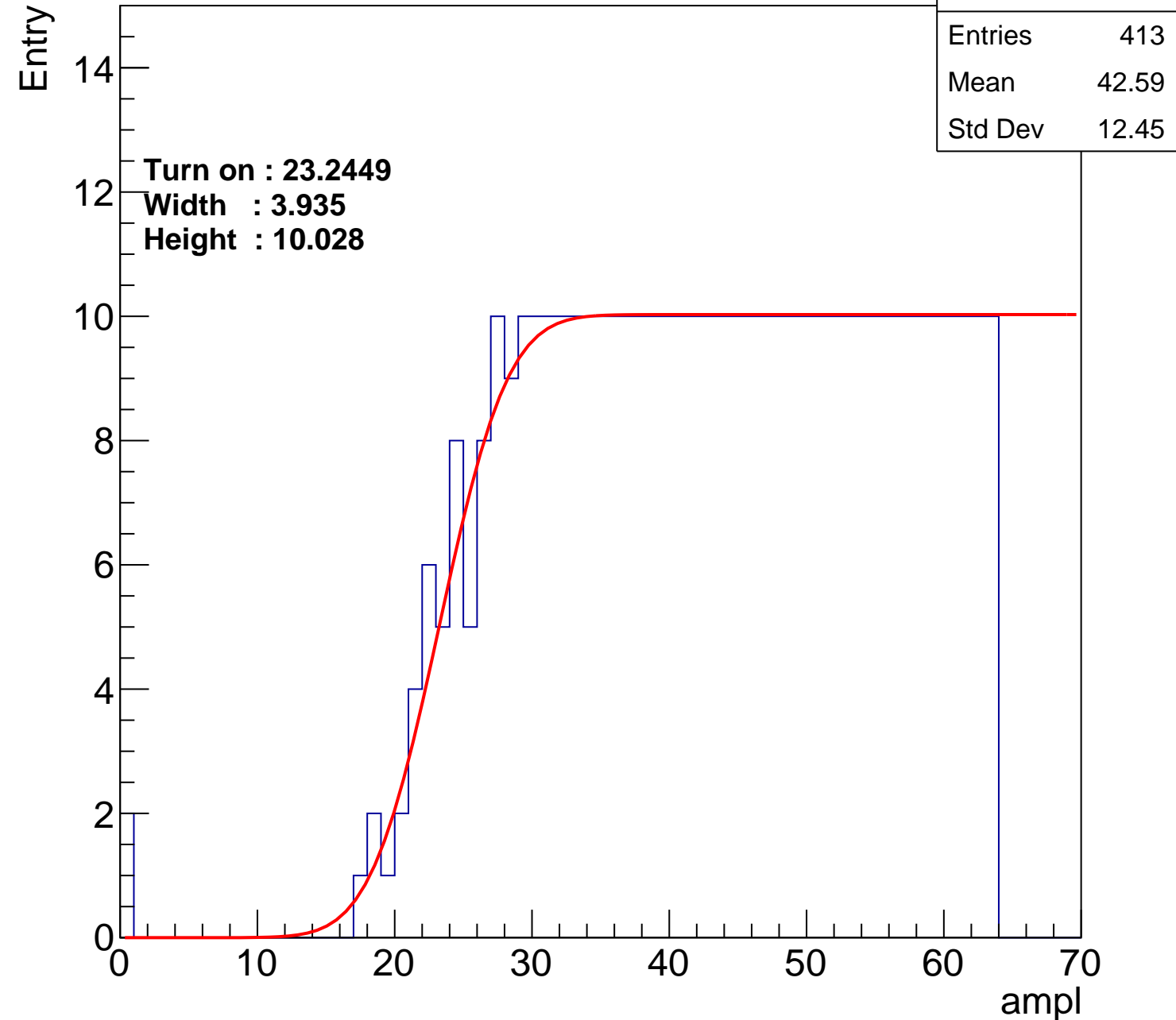
Width : 3.935

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.75
Std Dev	11.92

Turn on : 25.1339

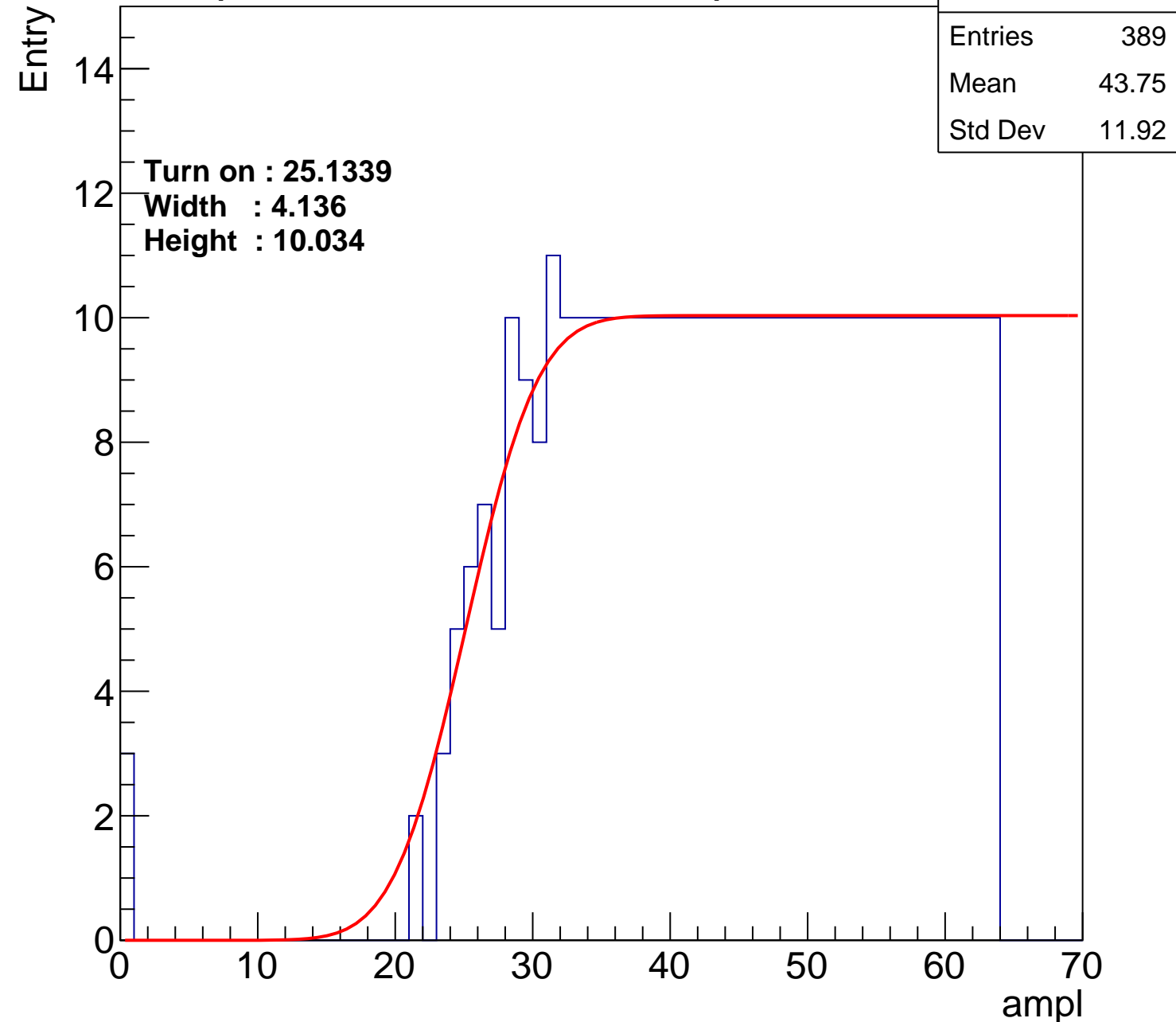
Width : 4.136

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.52
Std Dev	12.47

Turn on : 26.0219

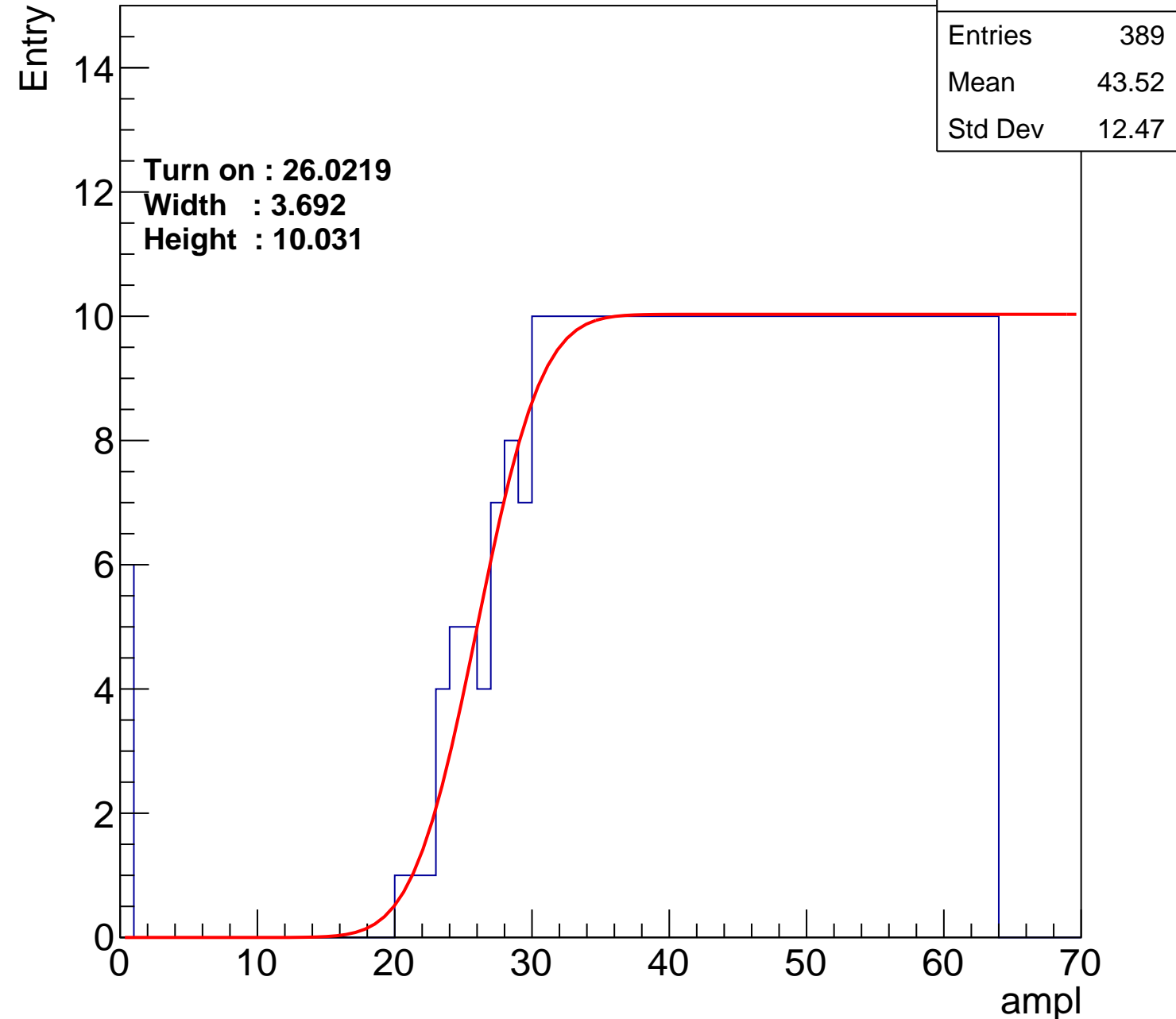
Width : 3.692

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.78
Std Dev	11.92

Turn on : 26.2784

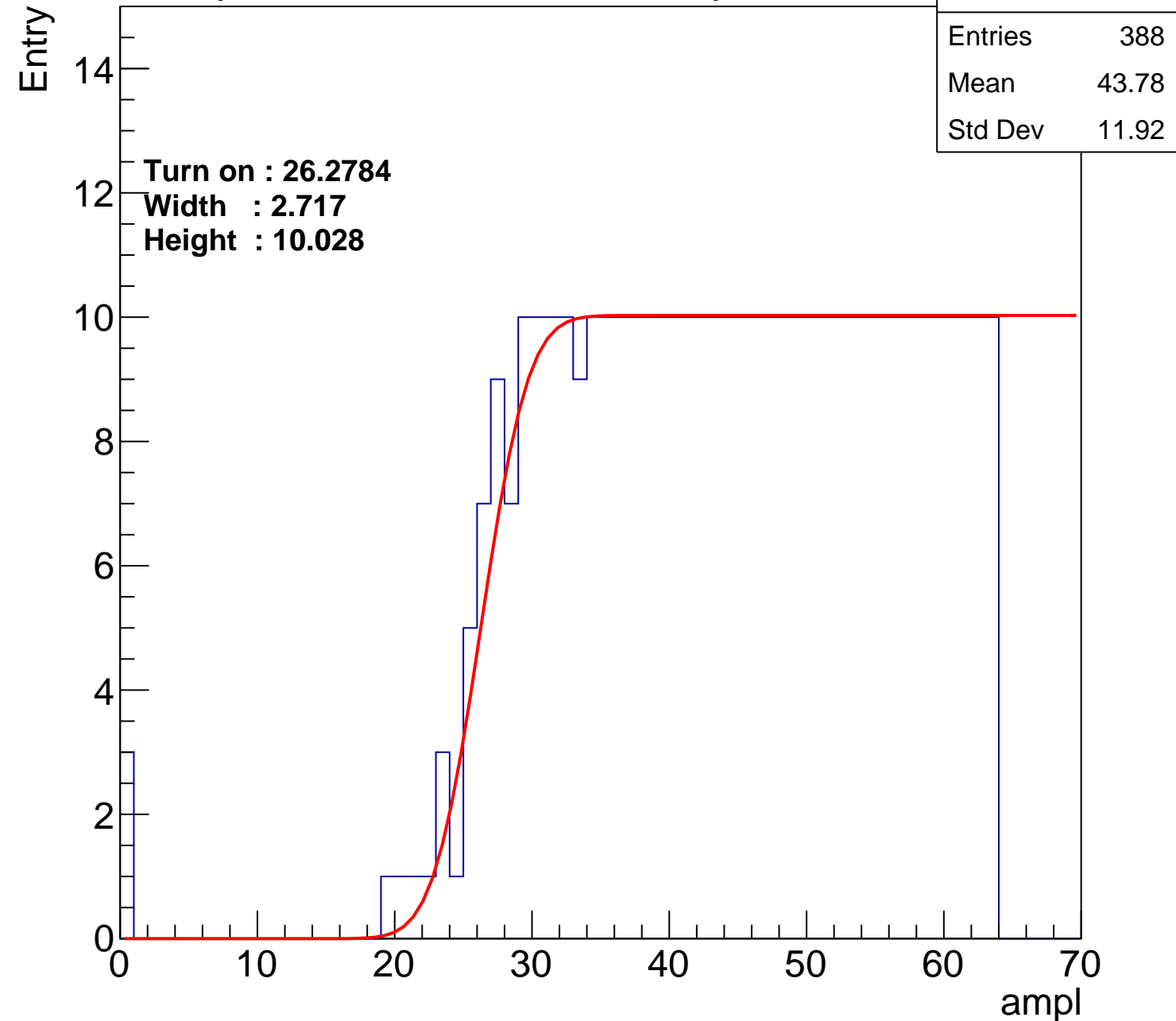
Width : 2.717

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.94
Std Dev	12.33

Turn on : 23.4023

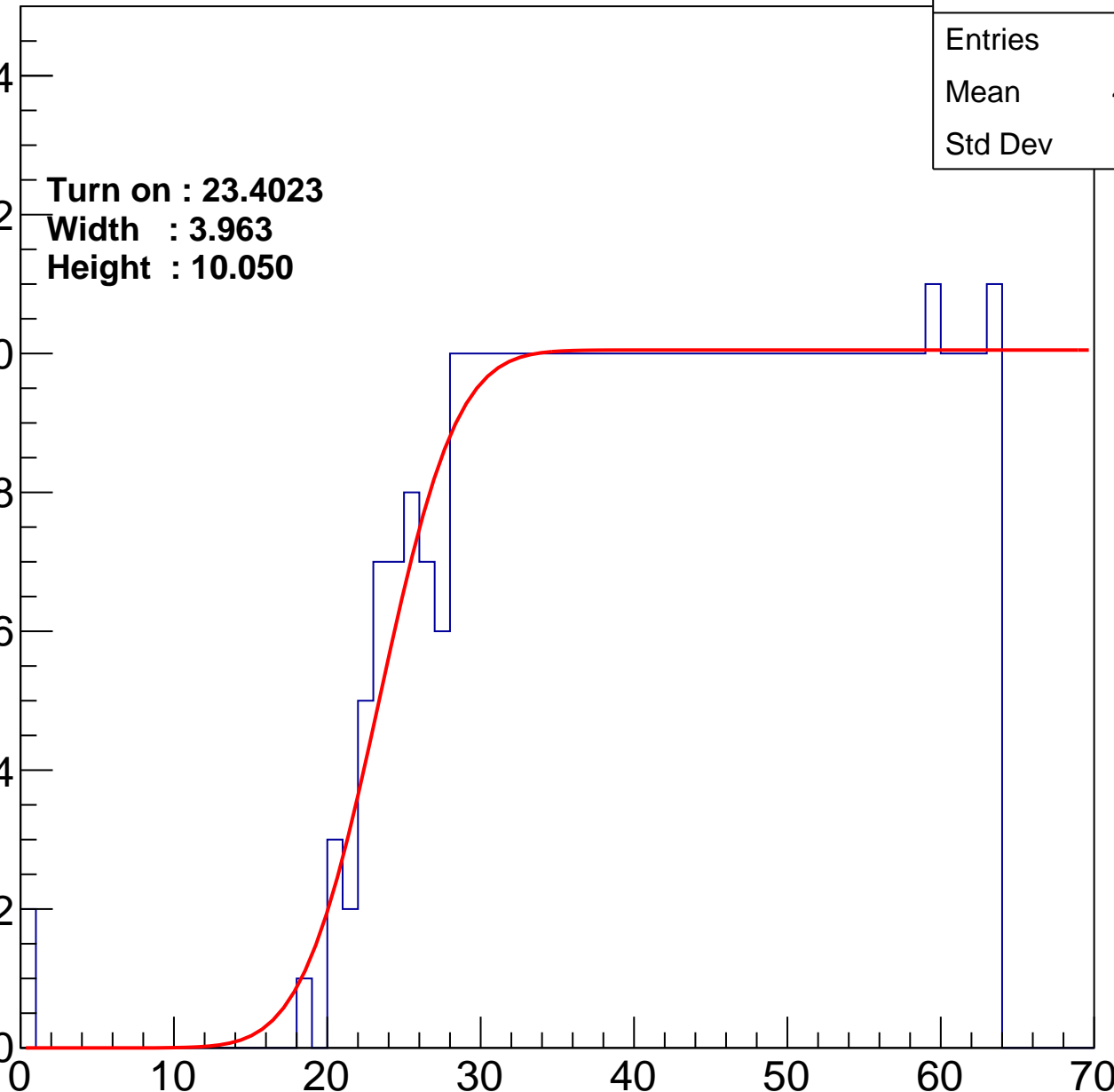
Width : 3.963

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.83
Std Dev	11.67

Turn on : 25.3908

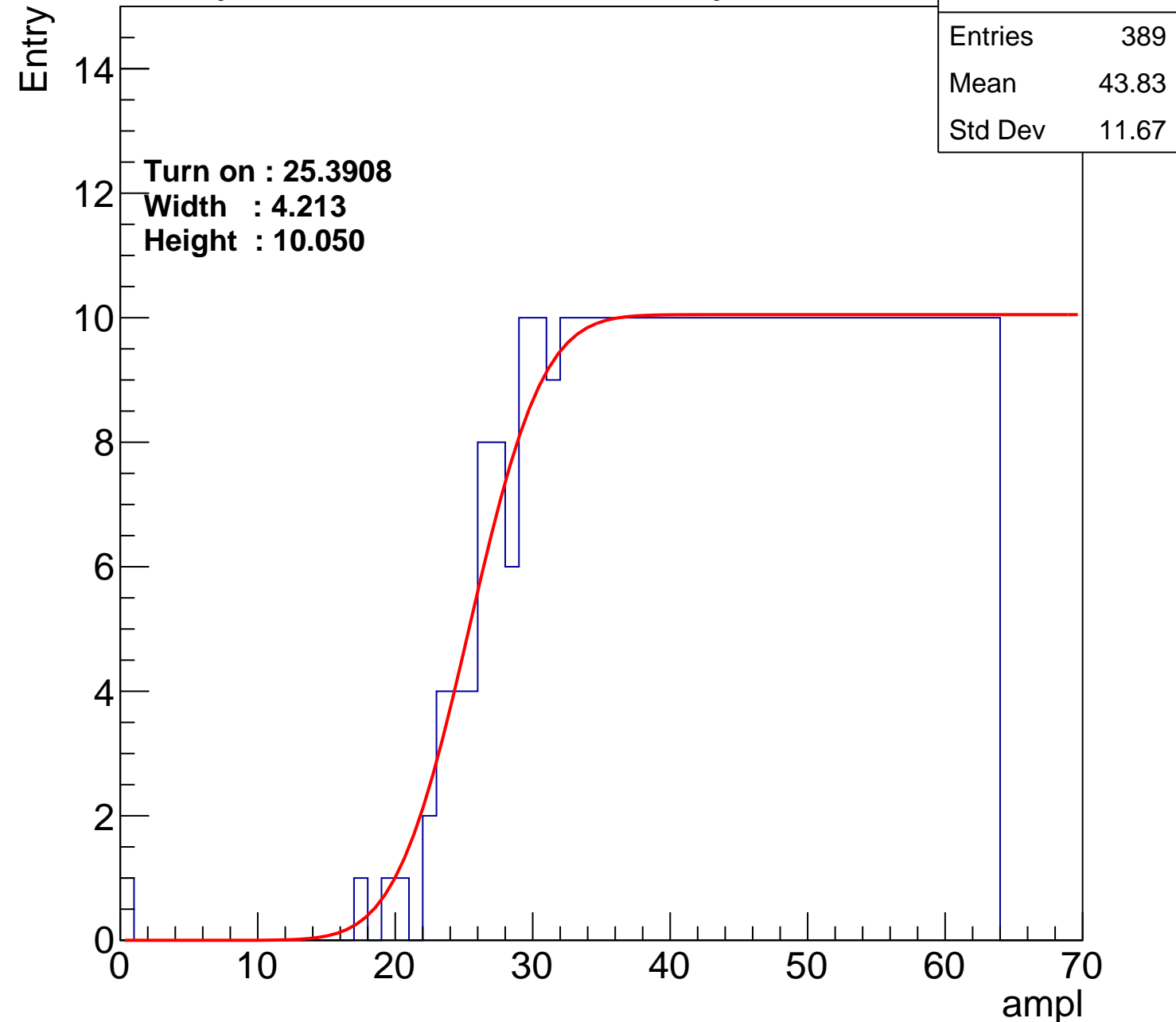
Width : 4.213

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.51
Std Dev	13.13

Turn on : 24.6252

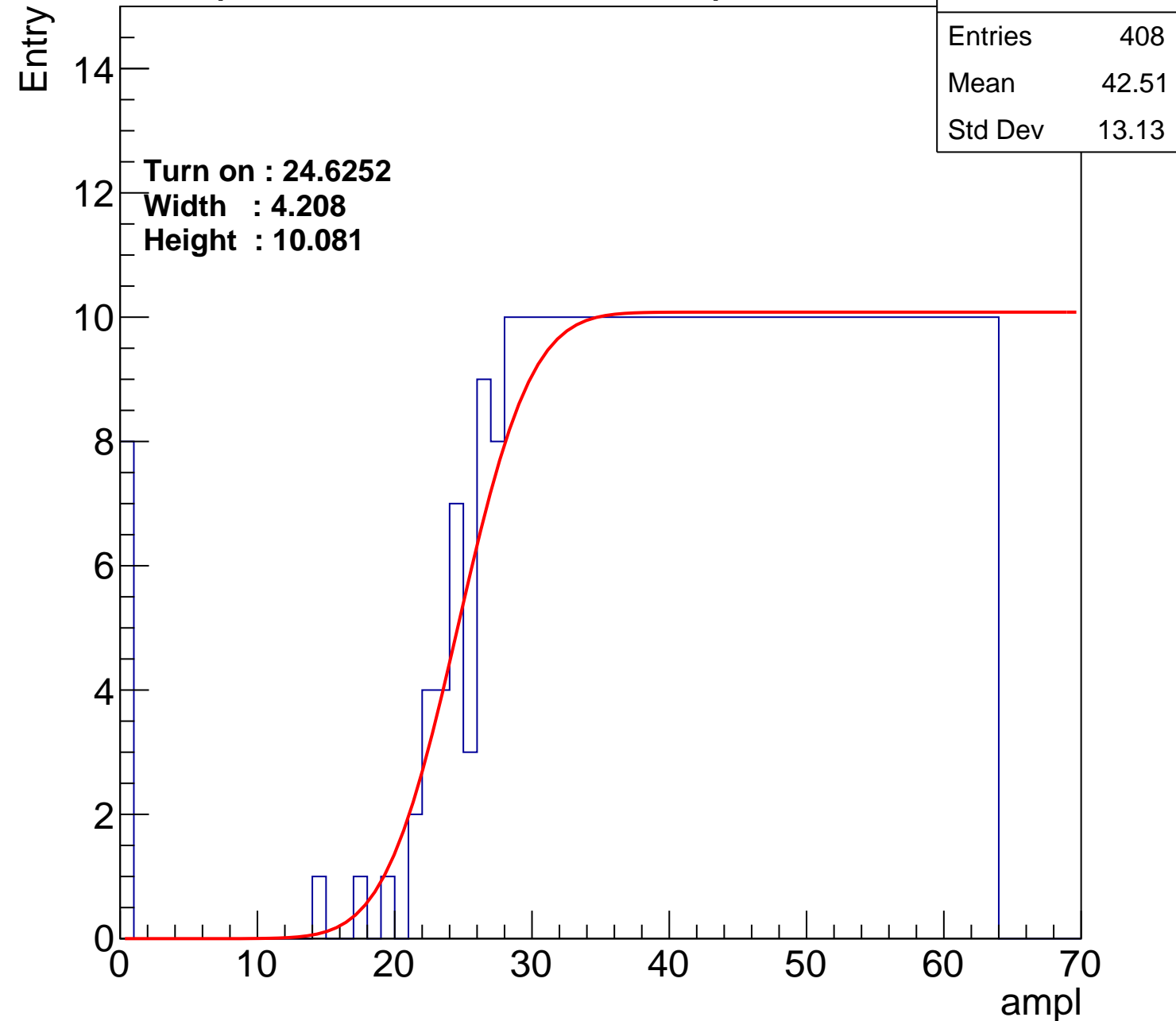
Width : 4.208

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.14
Std Dev	12.21

Turn on : 24.4262

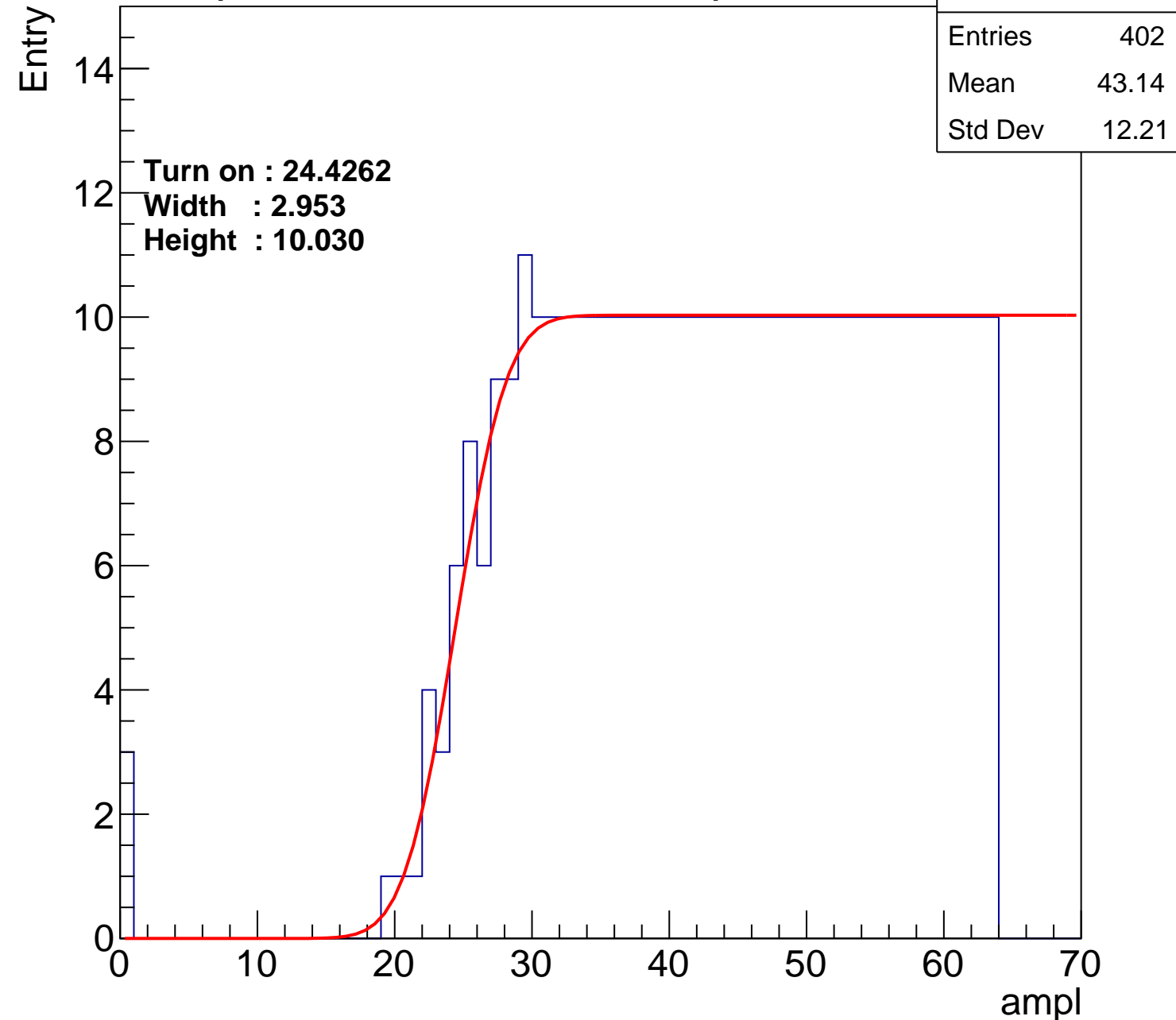
Width : 2.953

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.02
Std Dev	12.64

Turn on : 24.4729

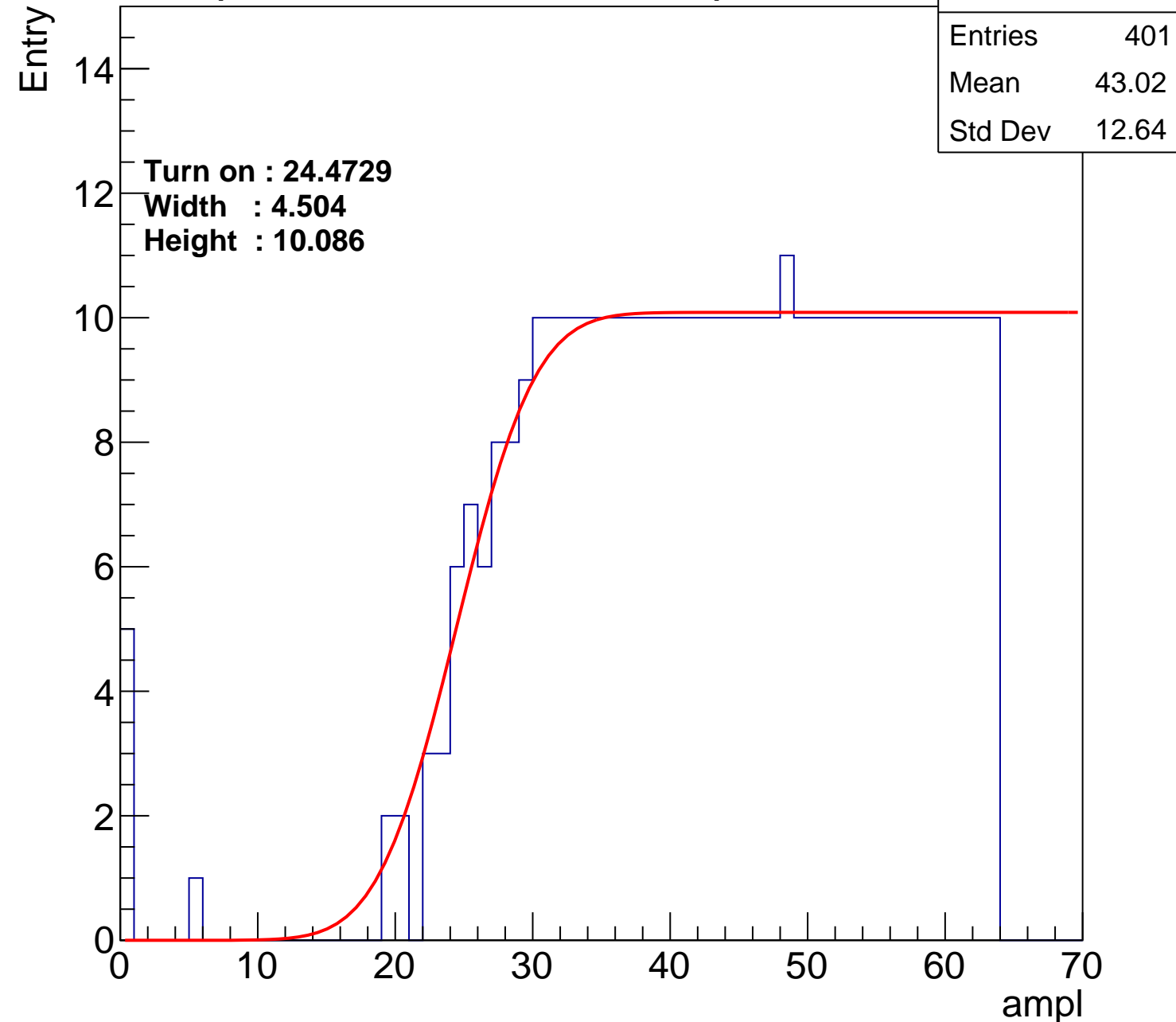
Width : 4.504

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.3061

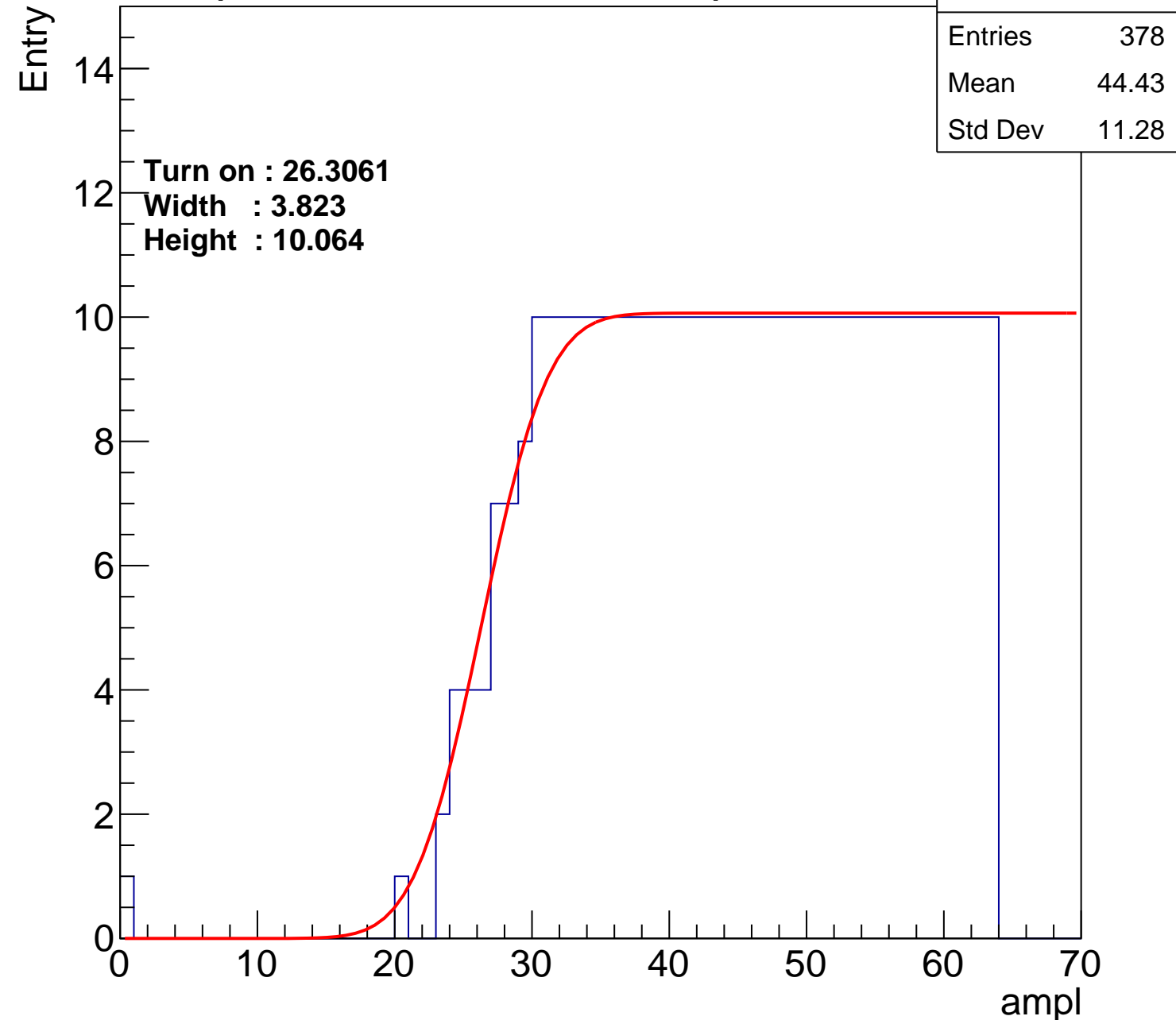
Width : 3.823

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U15-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.3061

Width : 3.823

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl

