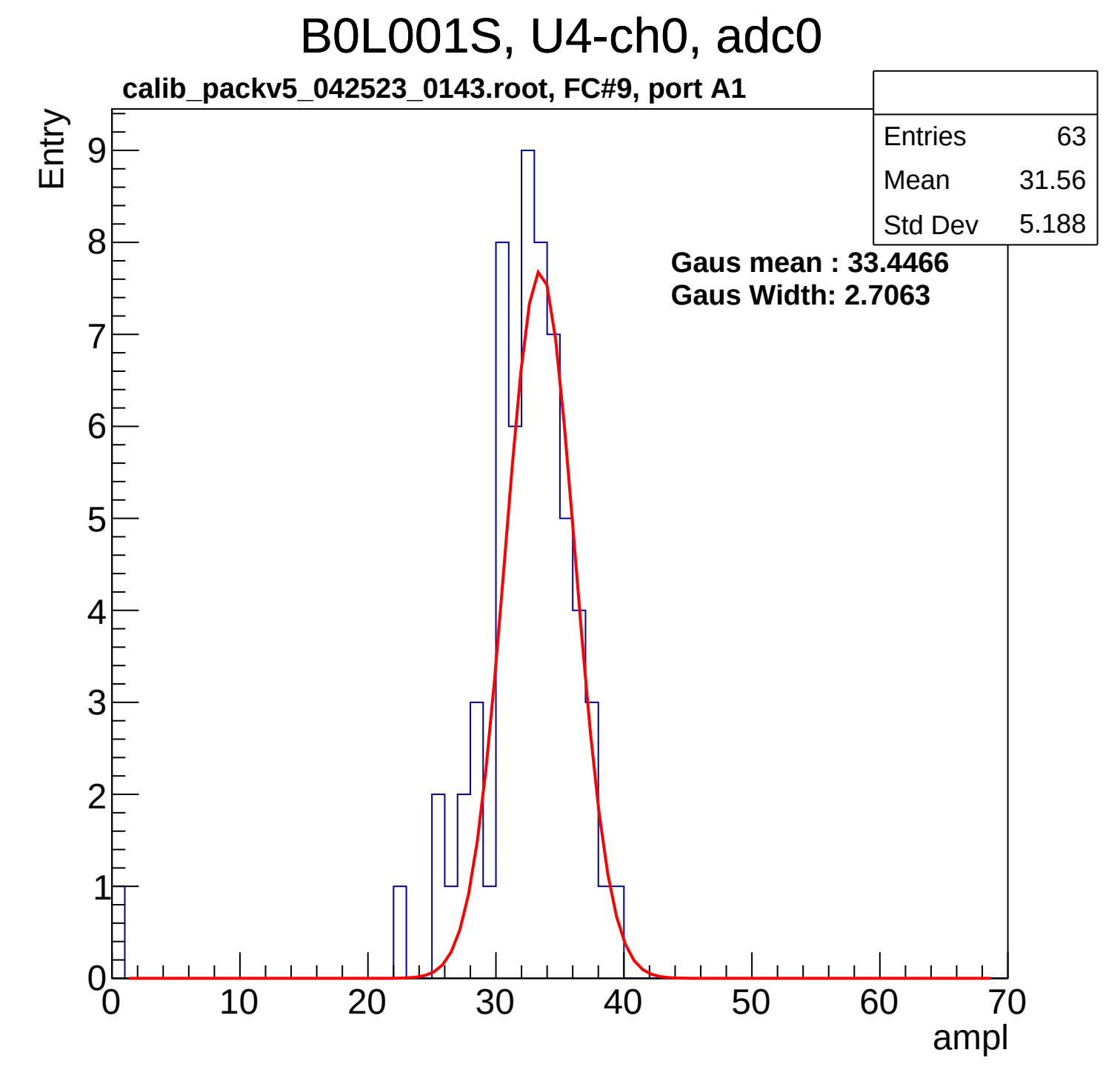
B0L001S, U4-ch0, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.56
Std Dev 5.188
Gaus mean : 33.4466
Gaus Width: 2.7063
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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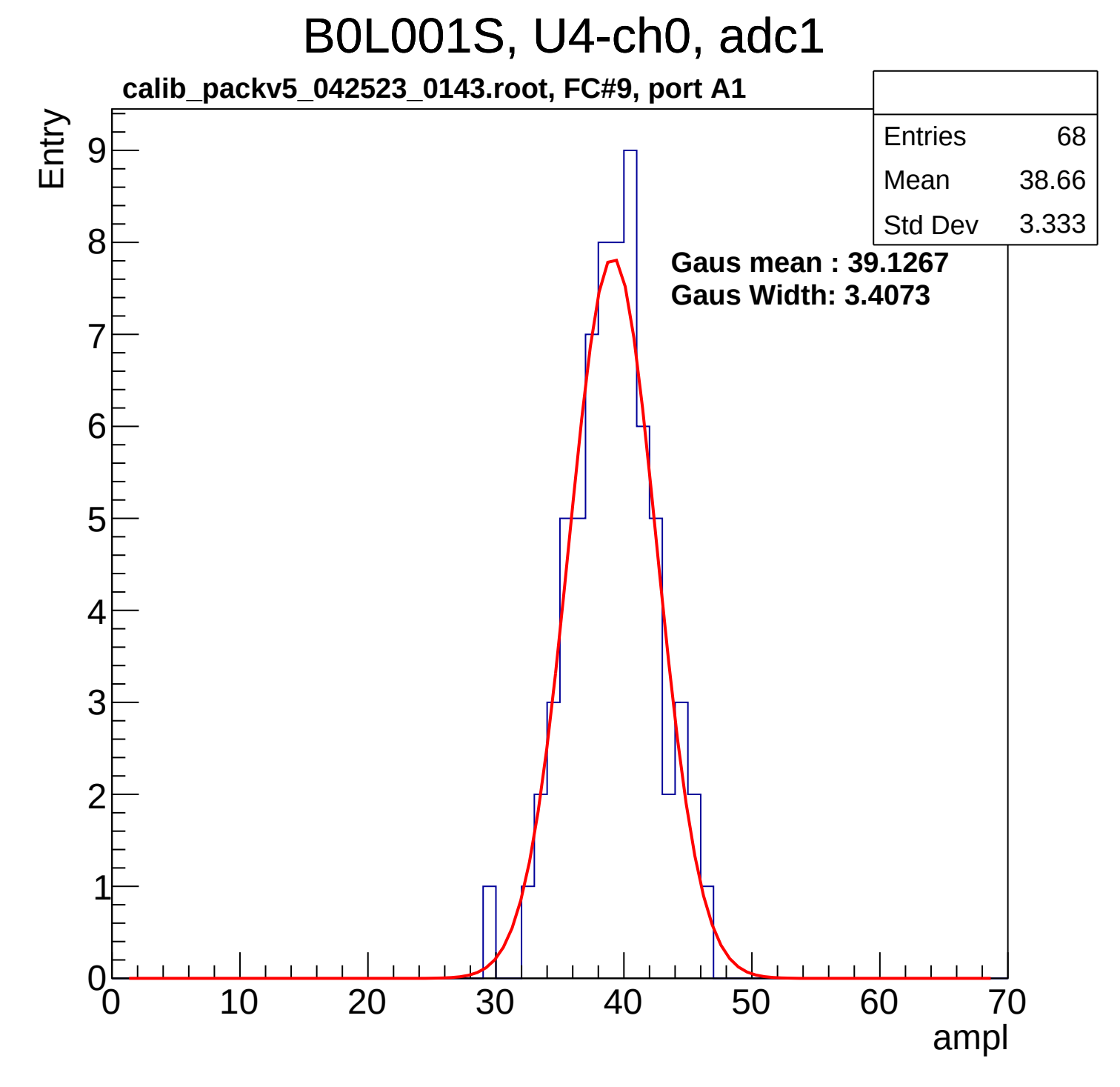
B0L001S, U4-ch0, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 38.66
Std Dev 3.333
Gaus mean : 39.1267
Gaus Width: 3.4073
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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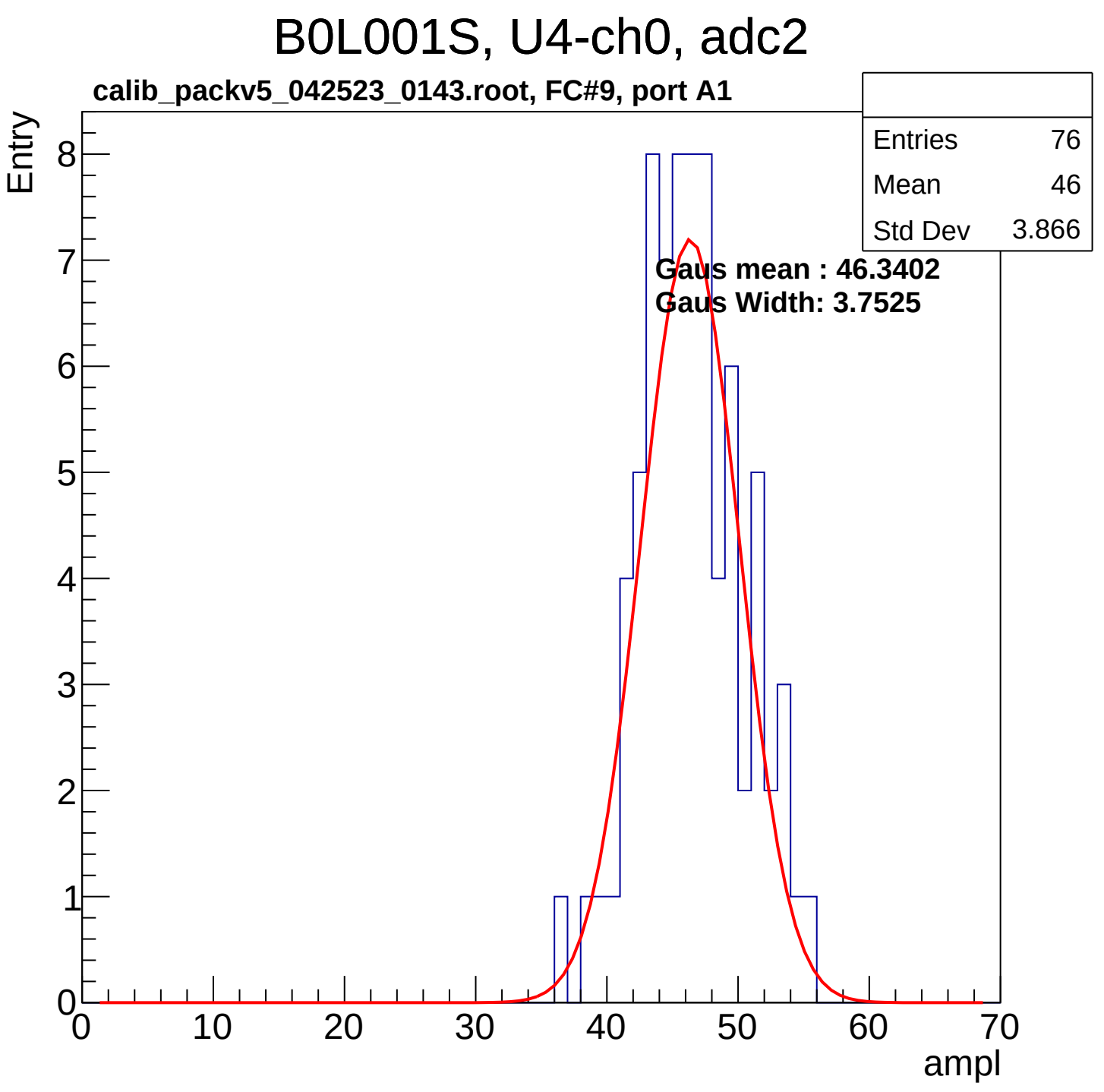
B0L001S, U4-ch0, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 46
Std Dev 3.866
Gaus mean : 46.3402
Gaus Width: 3.7525
Entry
ampl
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1
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4
5
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8
0
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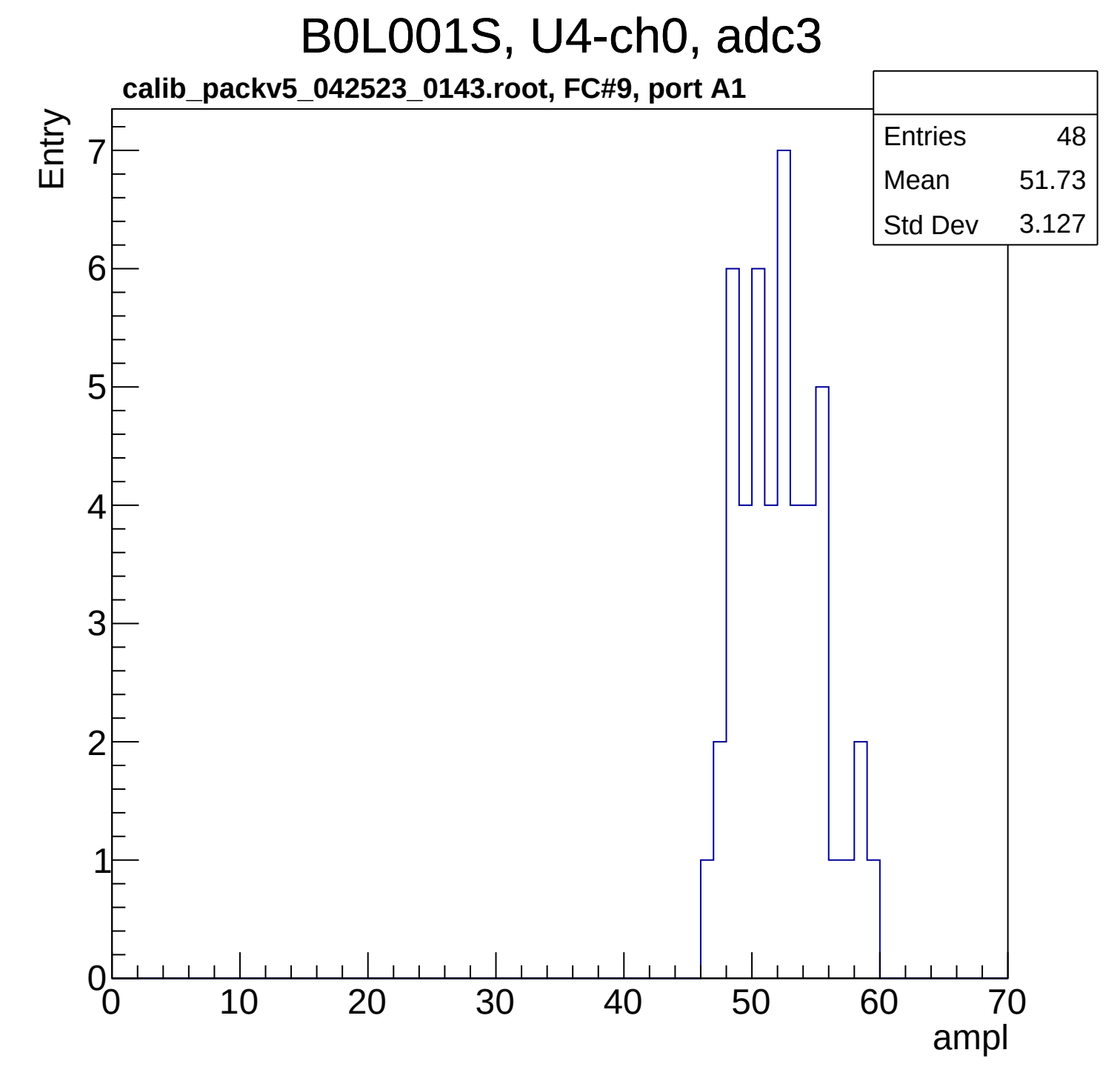
B0L001S, U4-ch0, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 51.73
Std Dev 3.127
Entry
ampl
0
1
2
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4
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6
7
0
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60
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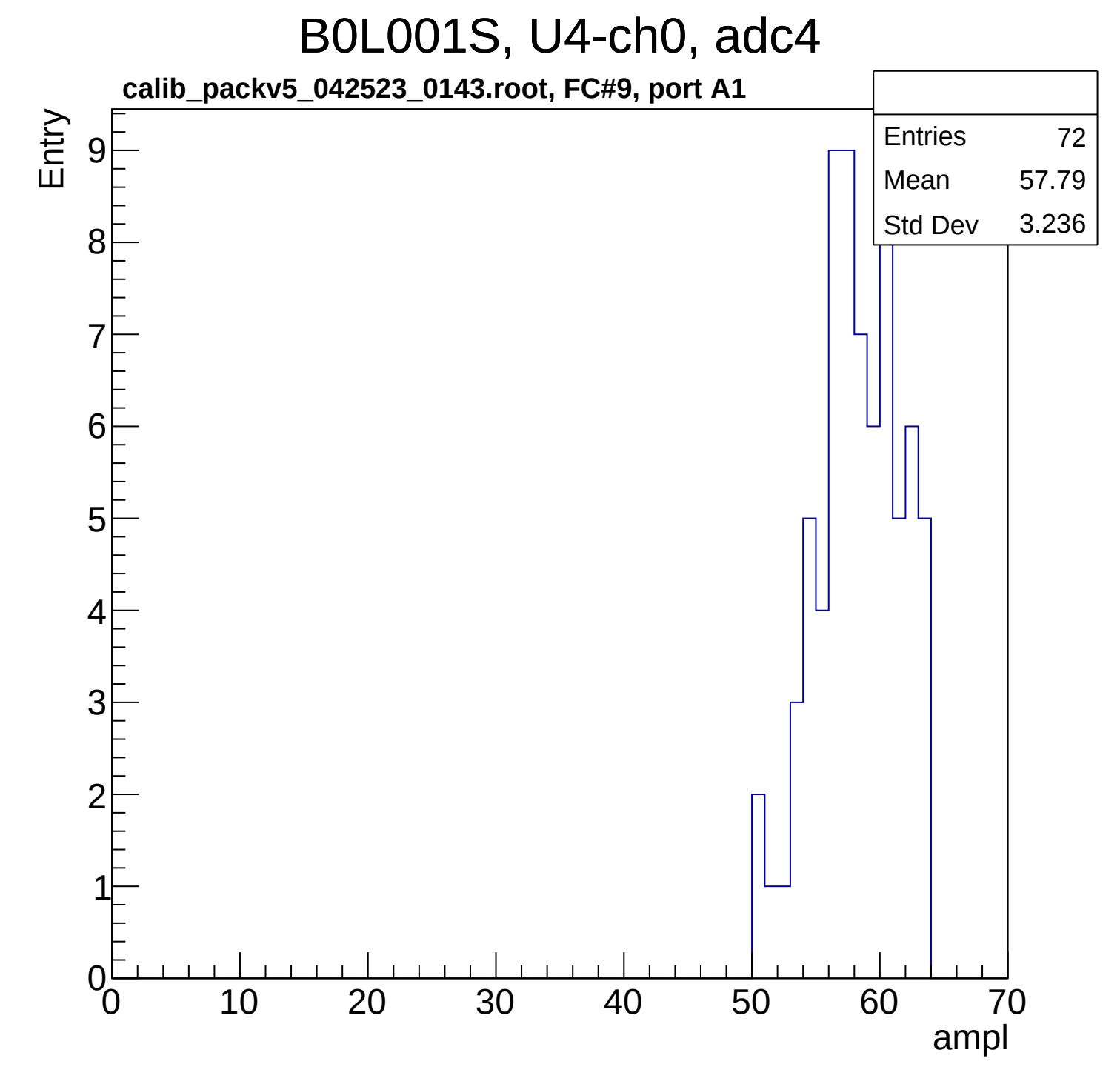
B0L001S, U4-ch0, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 57.79
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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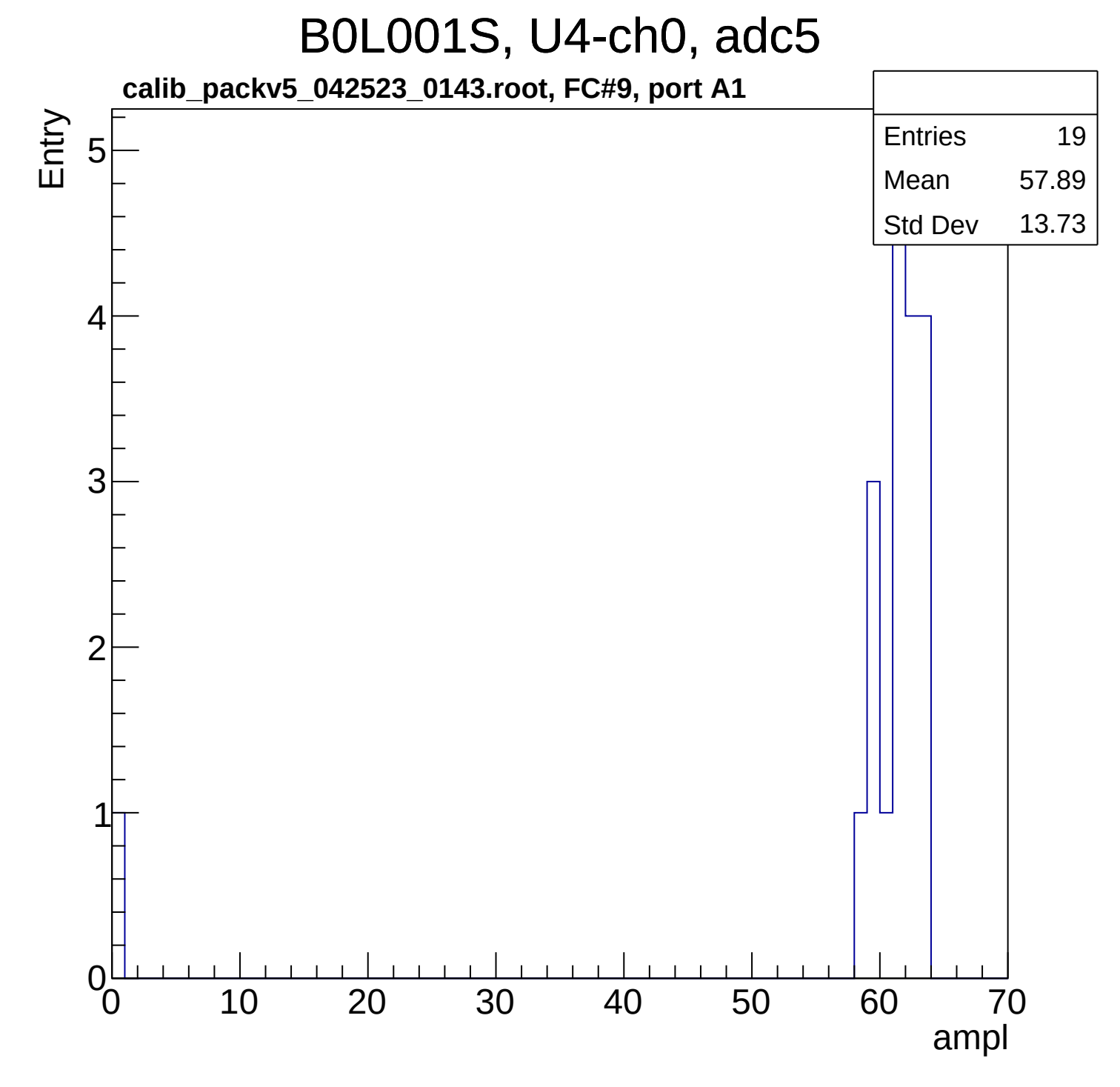
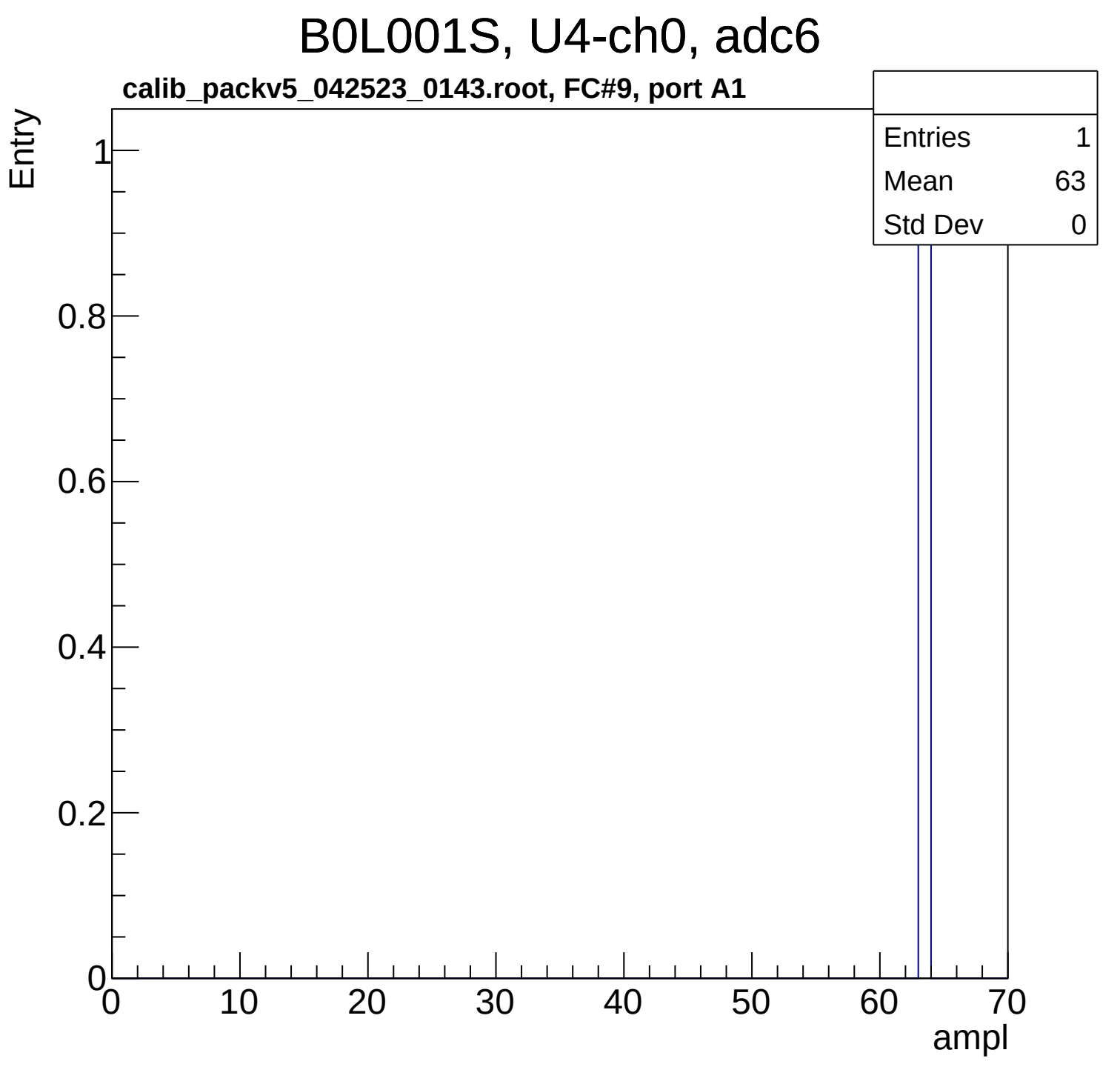
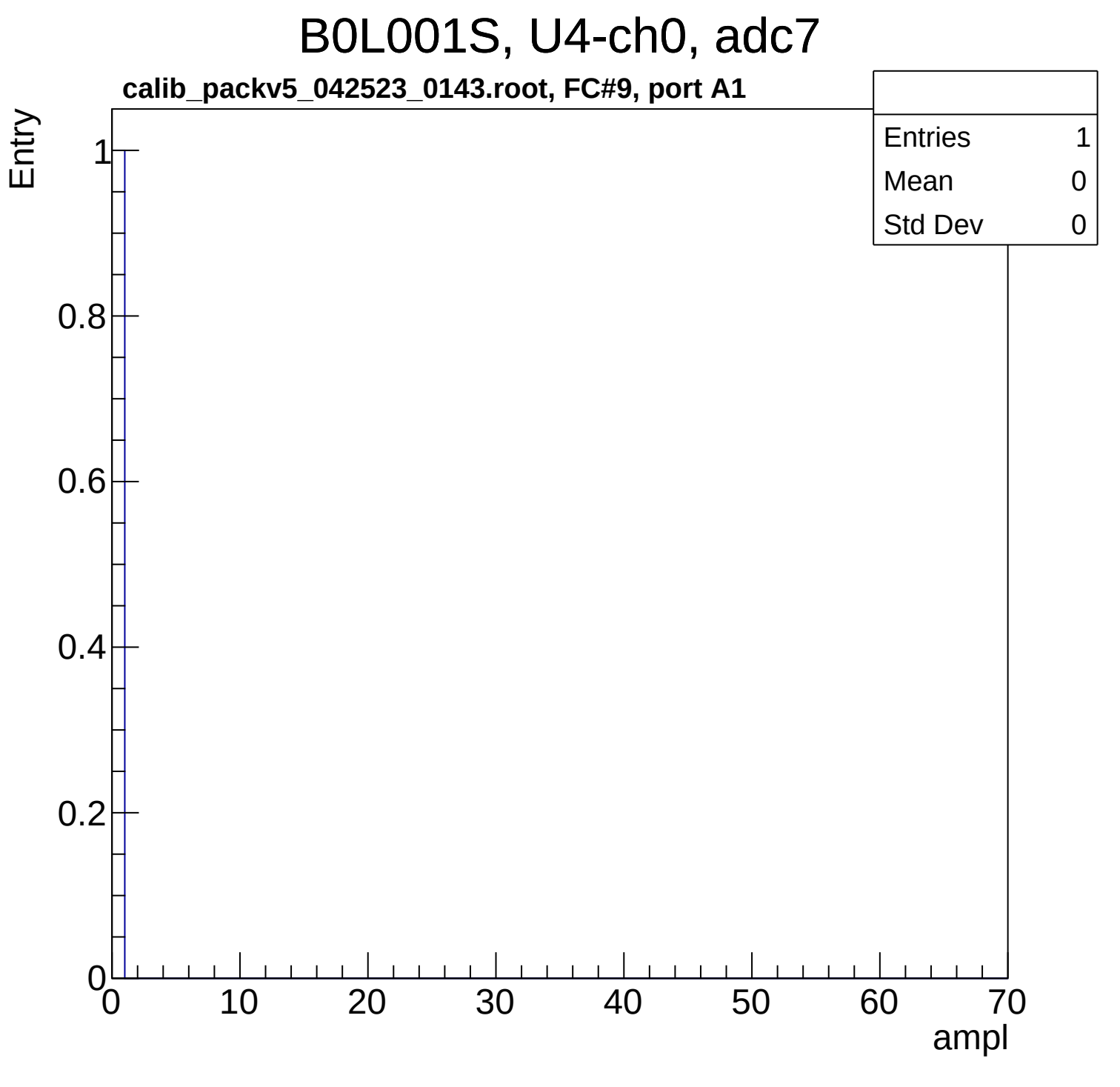

B0L001S, U4-ch0, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 57.89
Std Dev 13.73
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch0, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch0, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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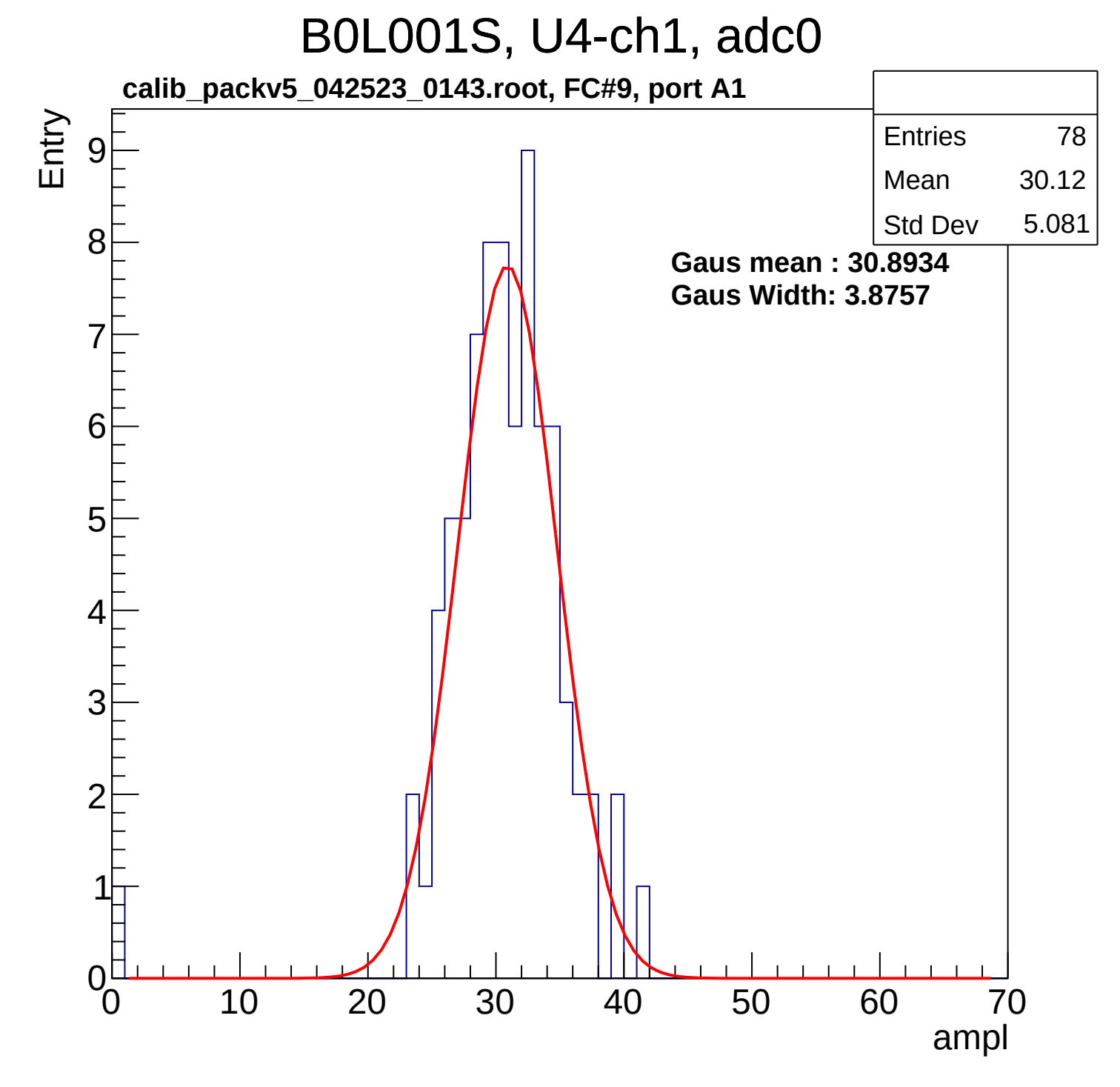
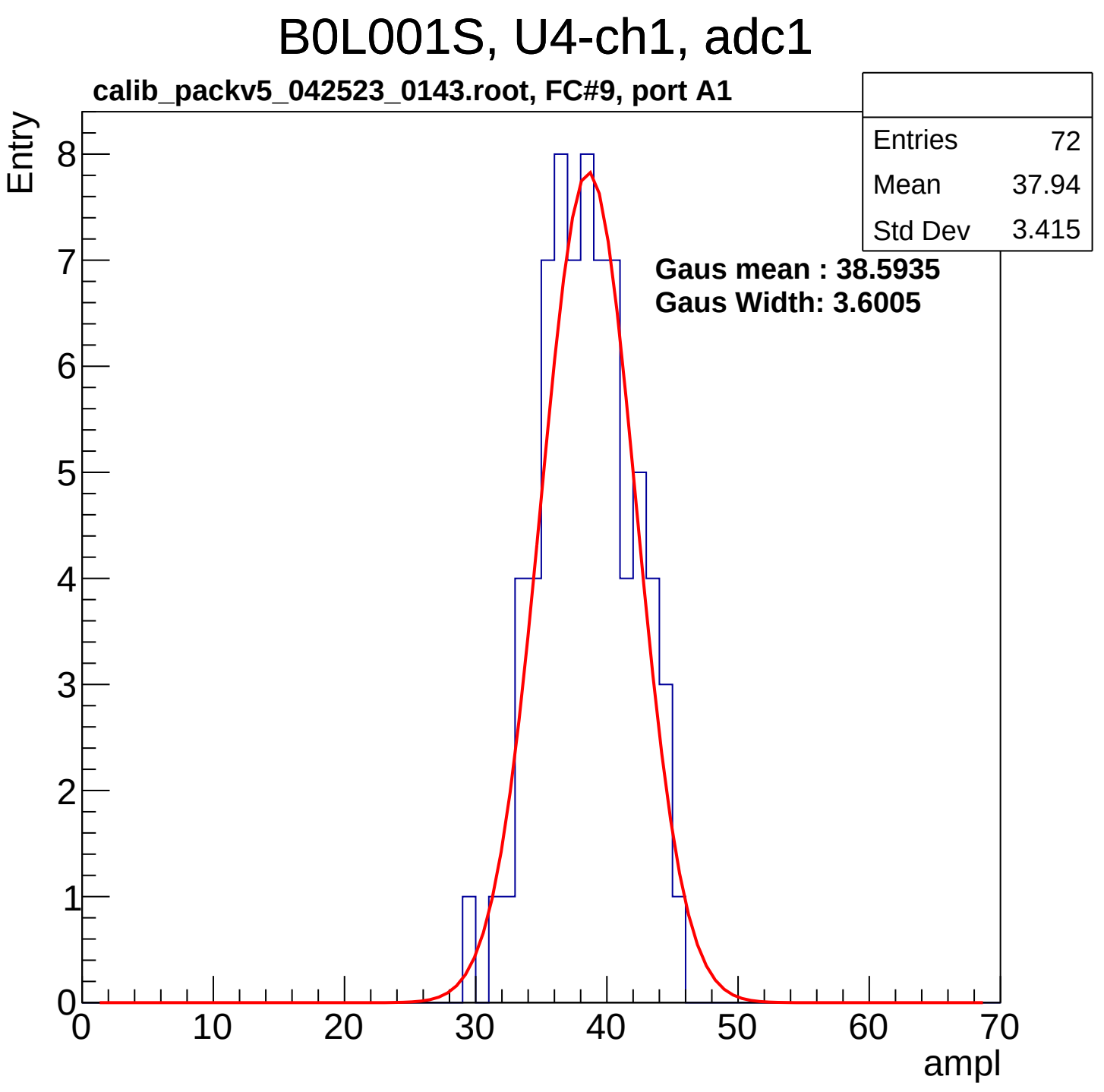
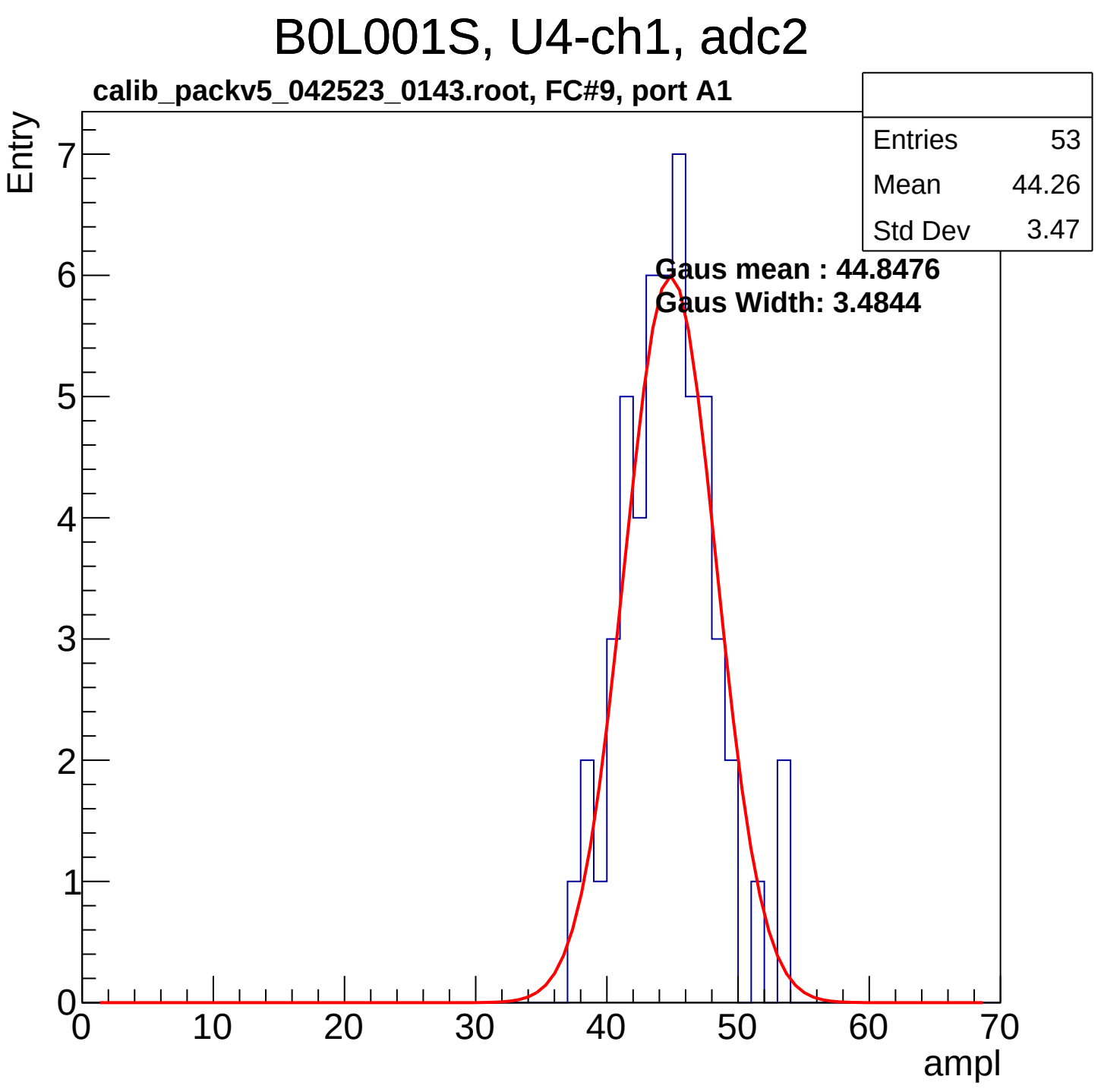
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch1, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.12
Std Dev 5.081
Gaus mean : 30.8934
Gaus Width: 3.8757
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch1, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.94
Std Dev 3.415
Gaus mean : 38.5935
Gaus Width: 3.6005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch1, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 44.26
Std Dev 3.47
Gaus mean : 44.8476
Gaus Width: 3.4844
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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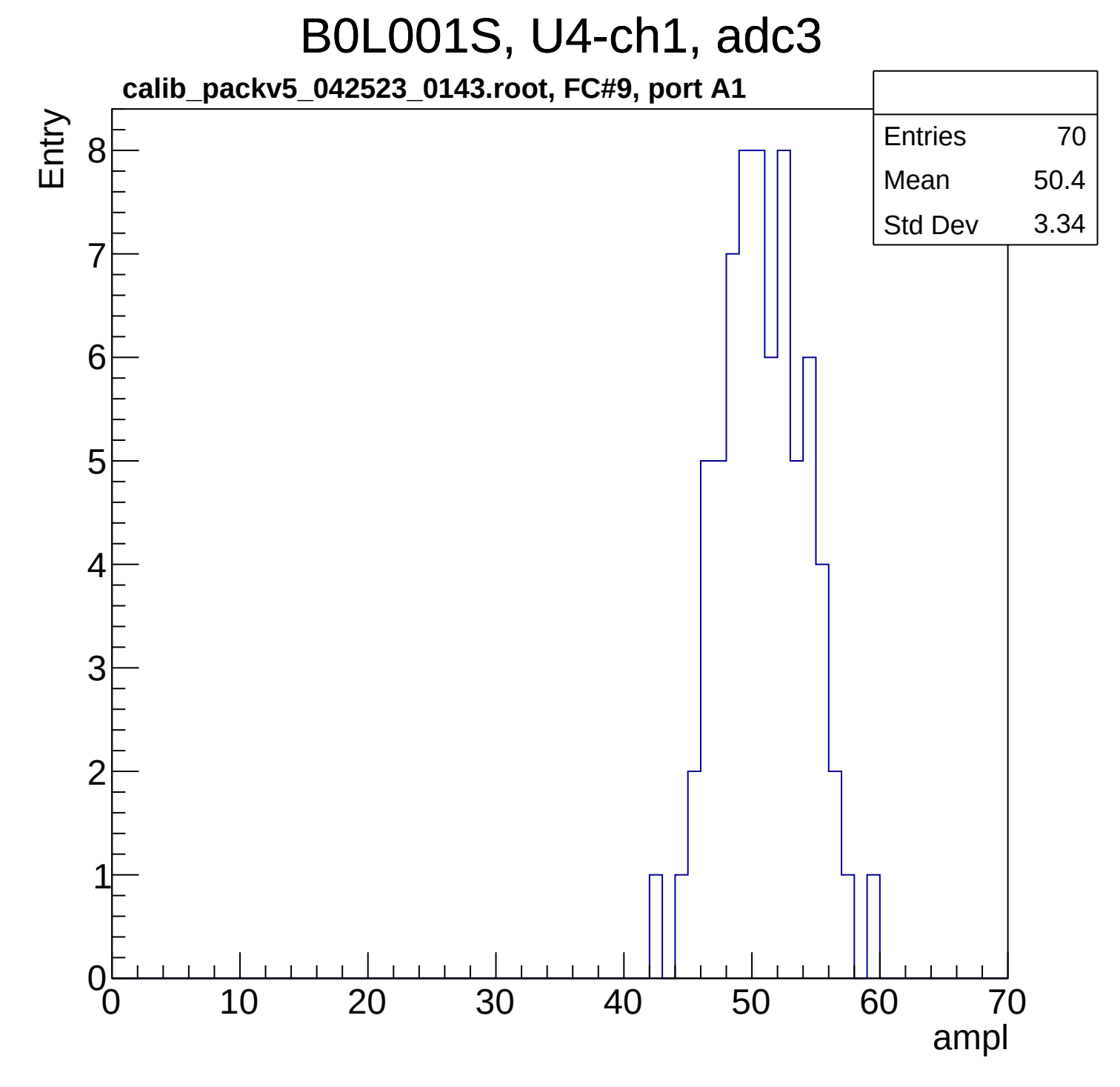
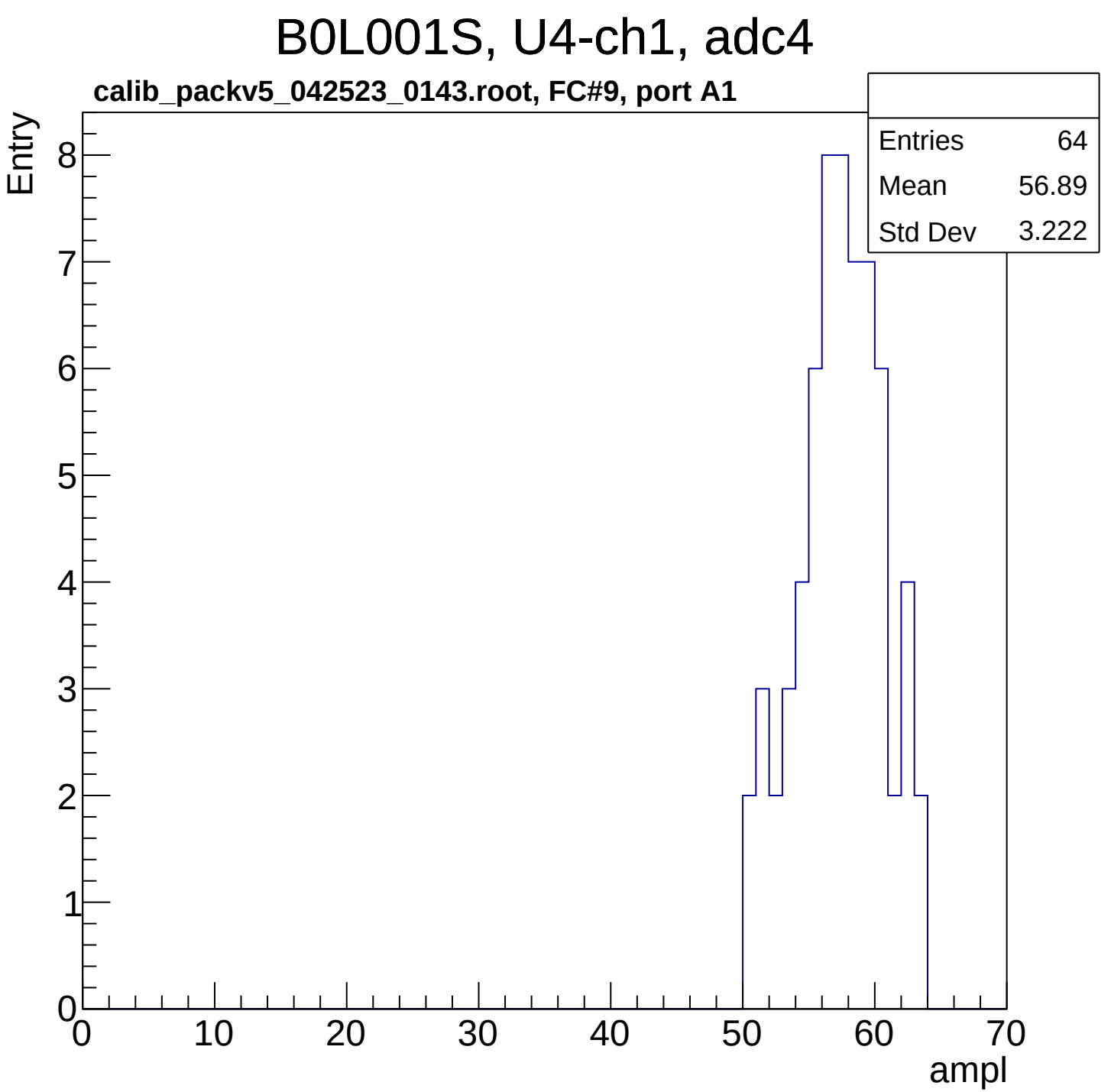
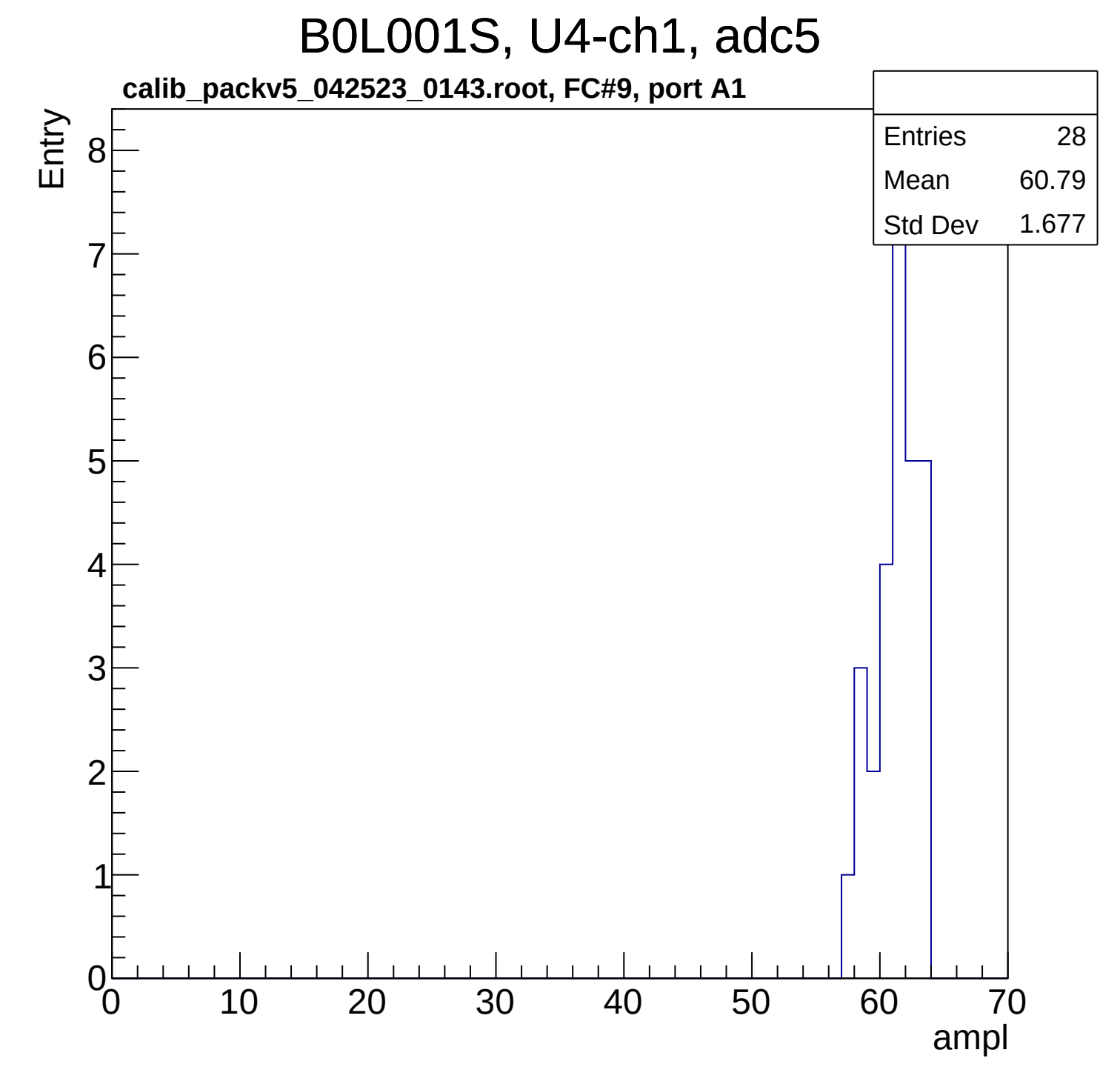

B0L001S, U4-ch1, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.4
Std Dev 3.34
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch1, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.89
Std Dev 3.222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch1, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 60.79
Std Dev 1.677
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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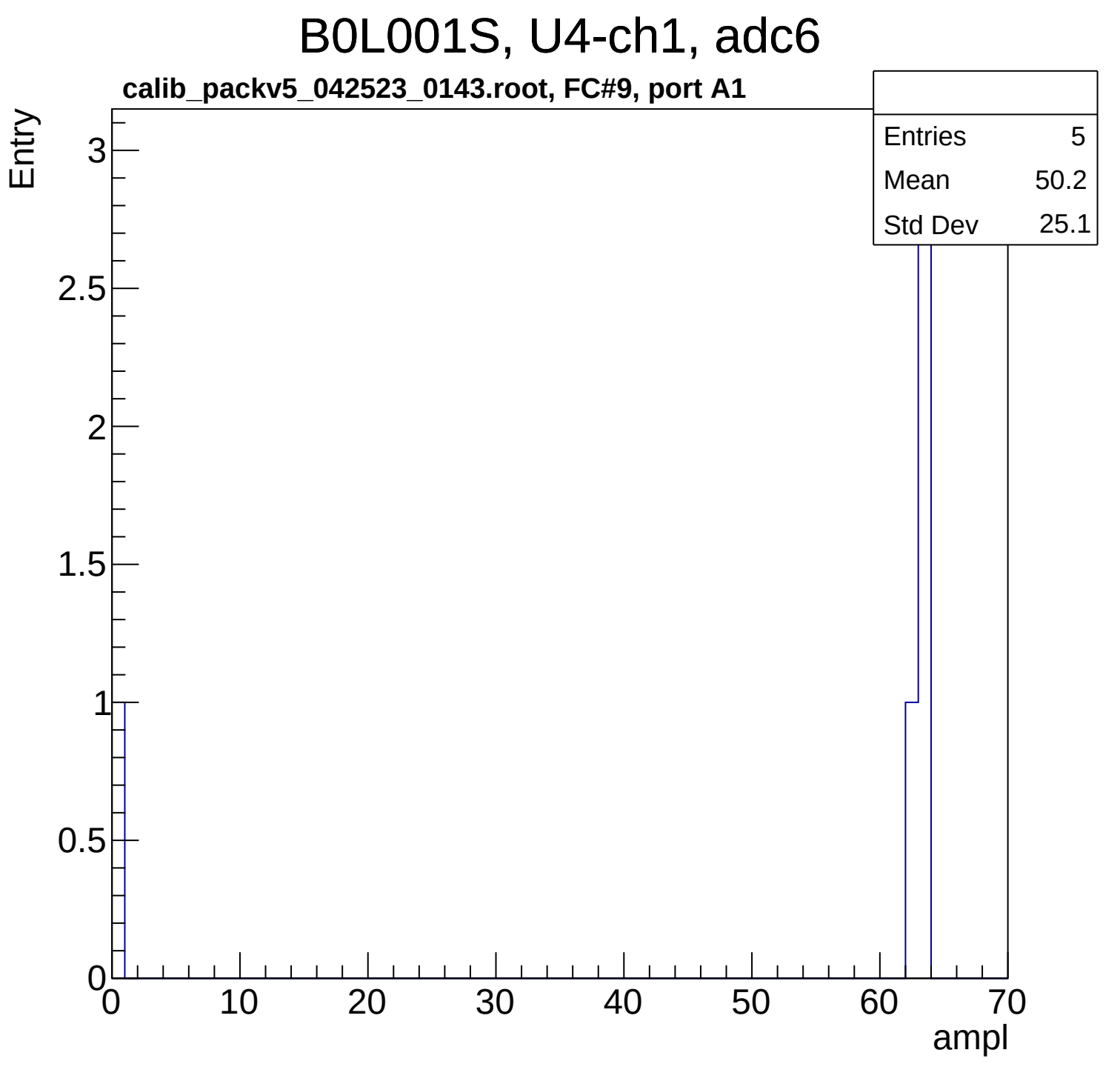
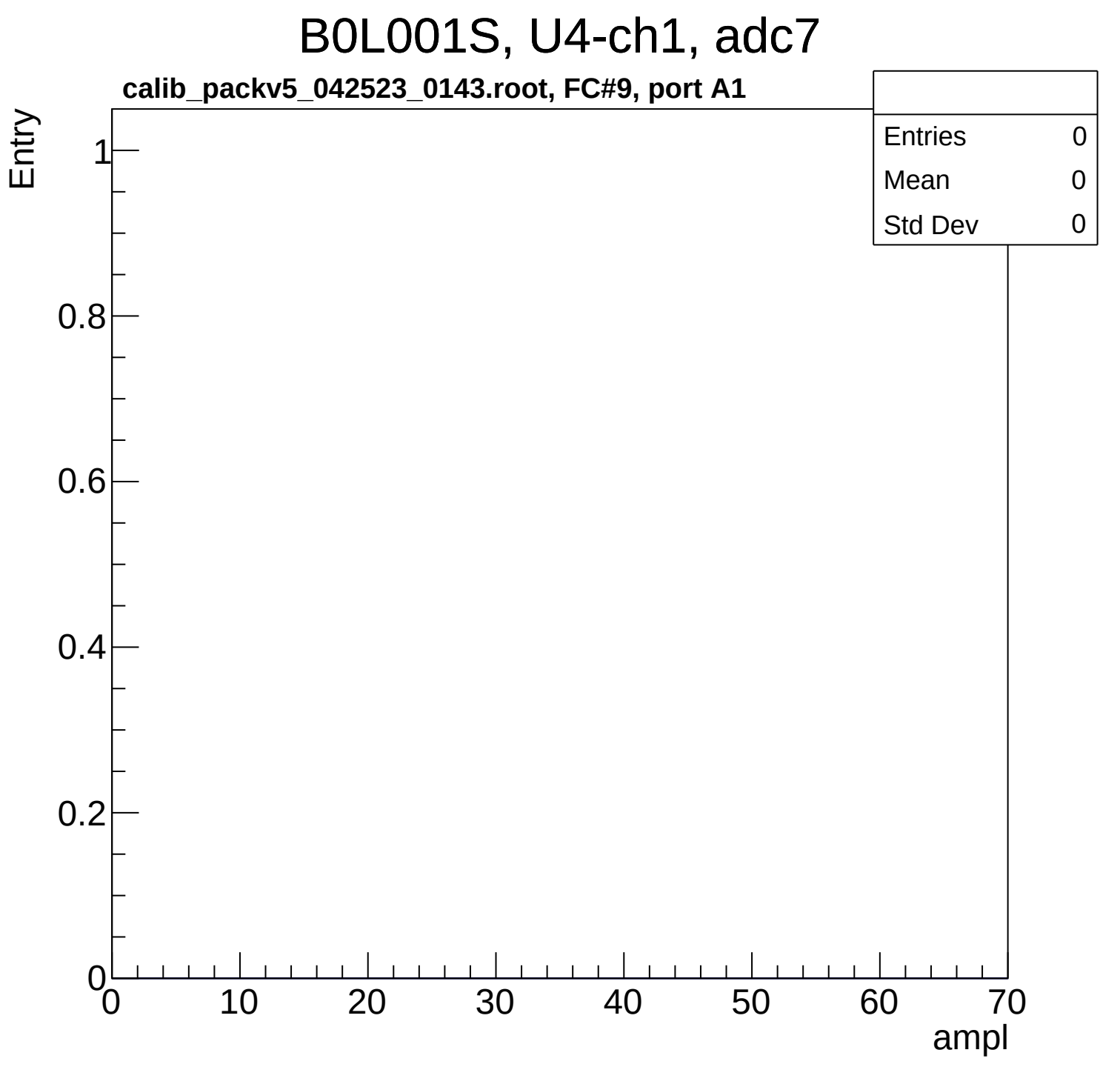
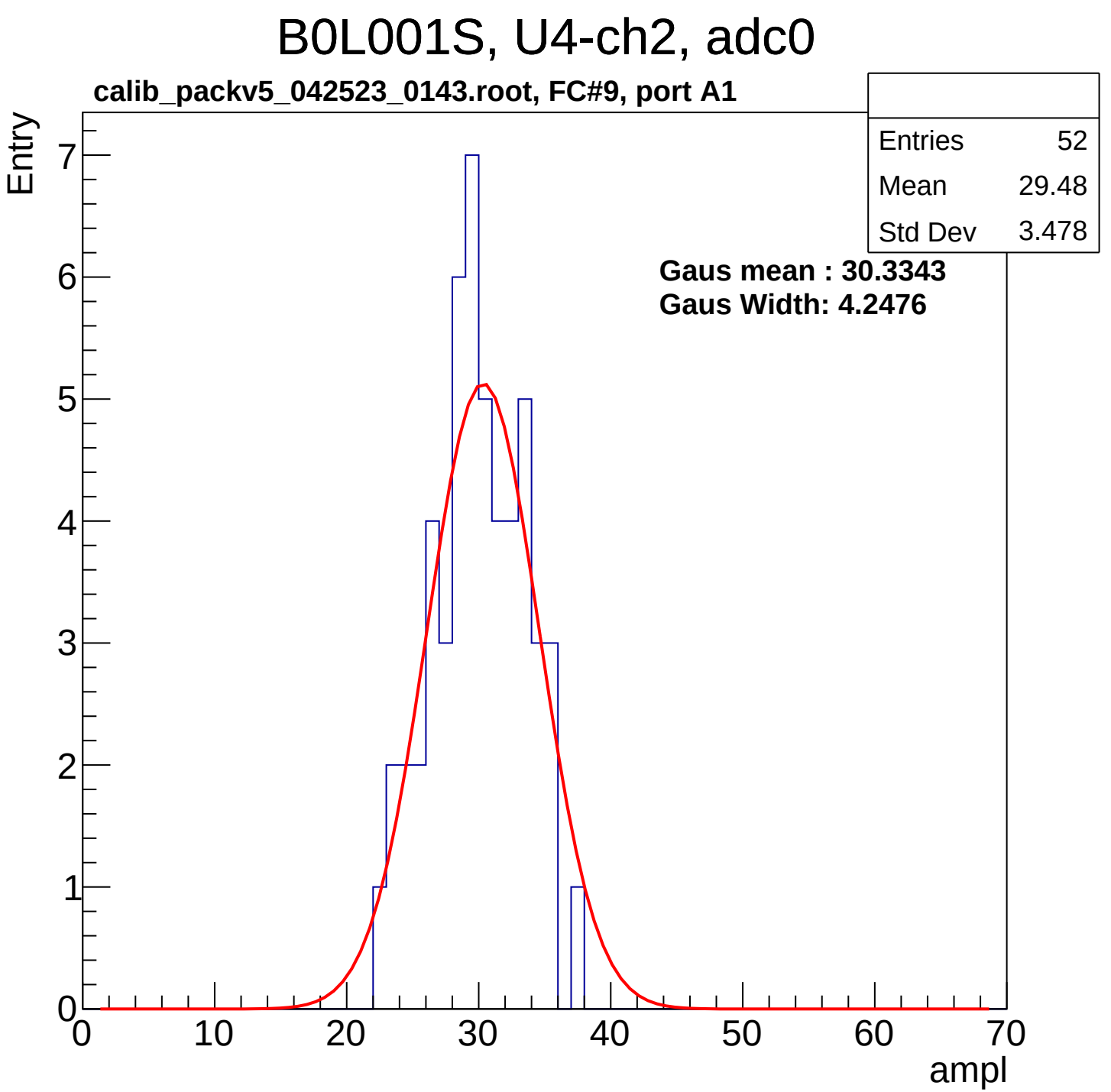

B0L001S, U4-ch1, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 50.2
Std Dev 25.1
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch1, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch2, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 29.48
Std Dev 3.478
Gaus mean : 30.3343
Gaus Width: 4.2476
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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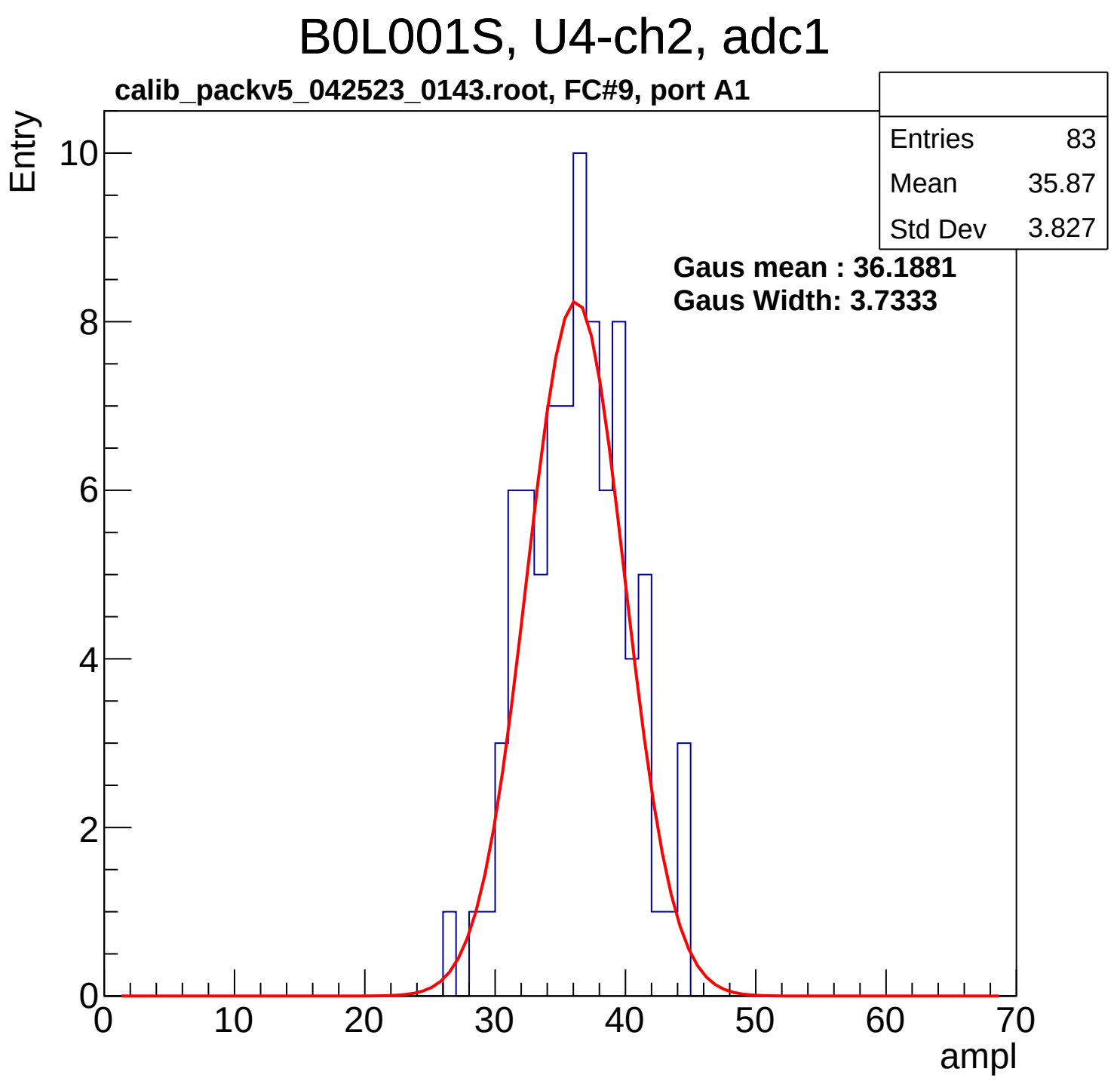
B0L001S, U4-ch2, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 35.87
Std Dev 3.827
Gaus mean : 36.1881
Gaus Width: 3.7333
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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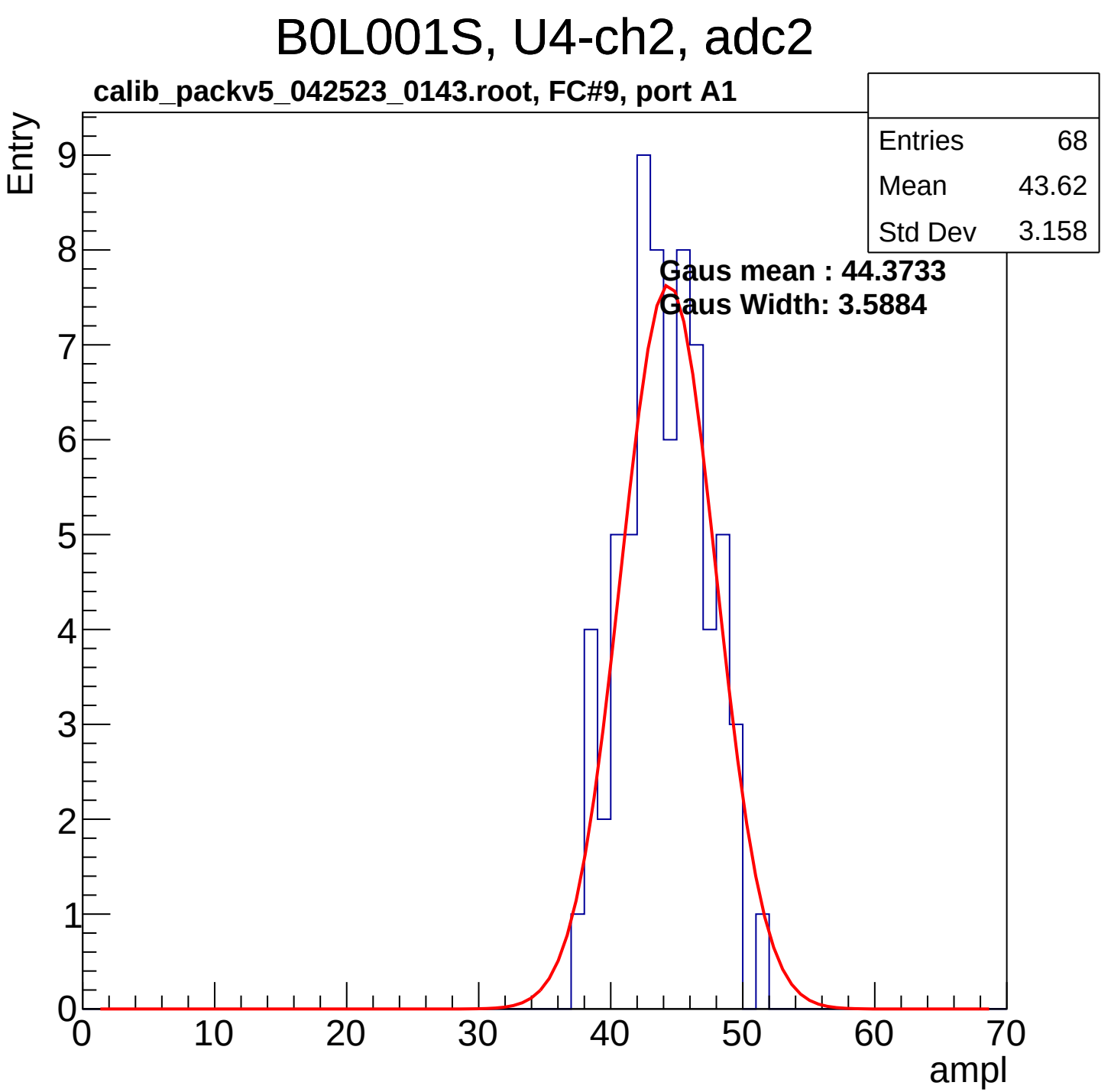
B0L001S, U4-ch2, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.62
Std Dev 3.158
Gaus mean : 44.3733
Gaus Width: 3.5884
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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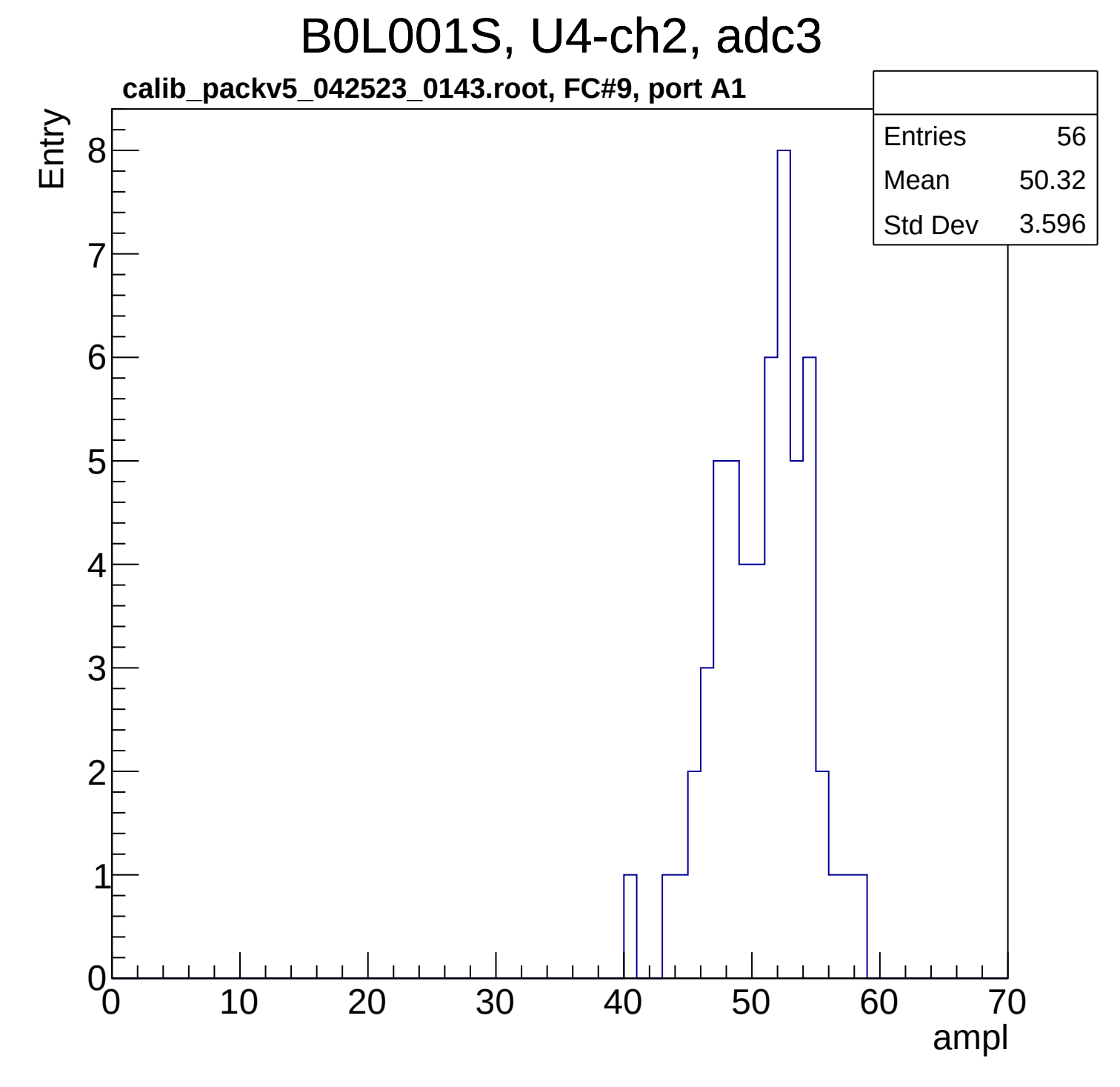
B0L001S, U4-ch2, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 50.32
Std Dev 3.596
Entry
ampl
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2
3
4
5
6
7
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0
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60
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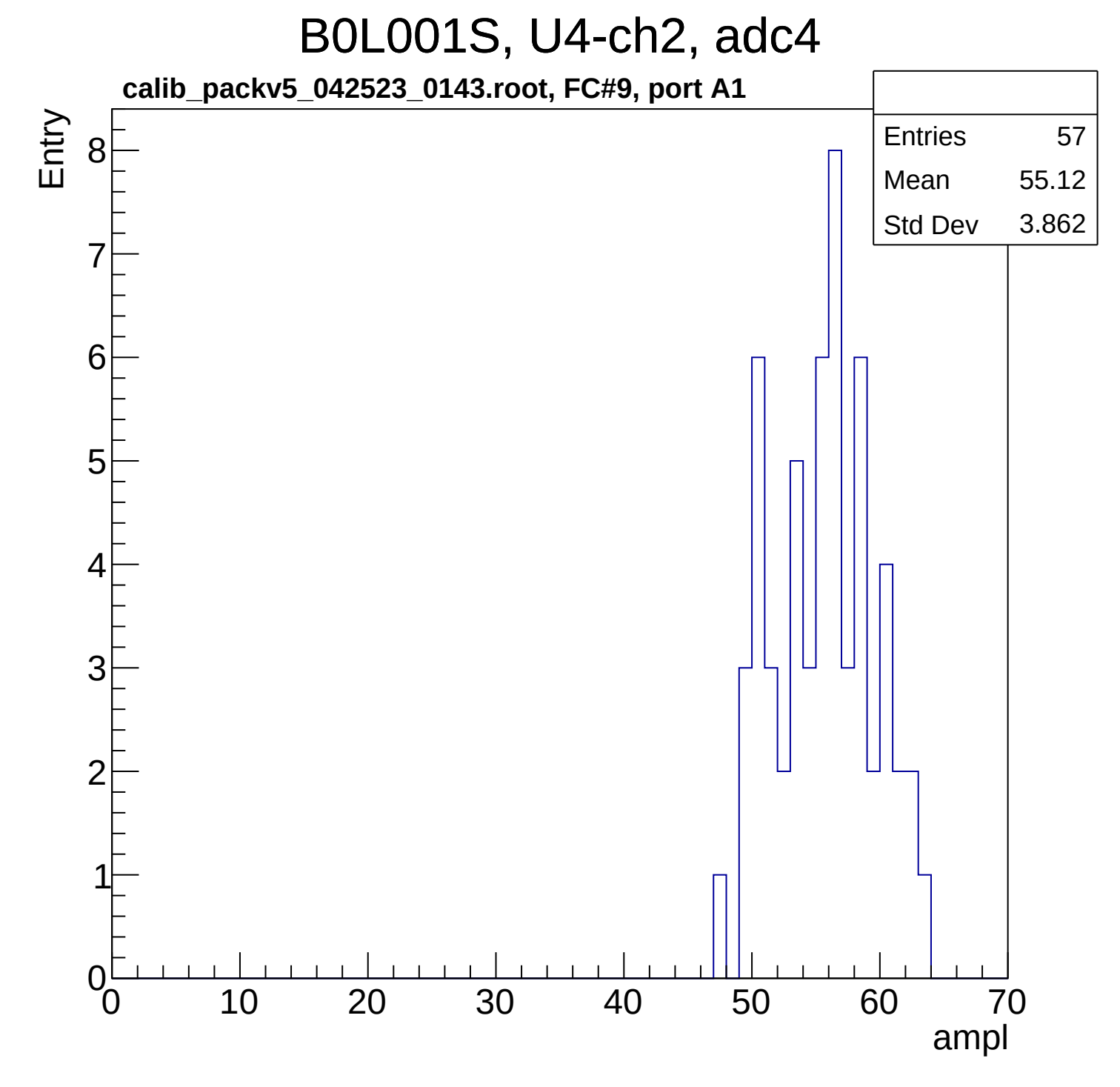
B0L001S, U4-ch2, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 55.12
Std Dev 3.862
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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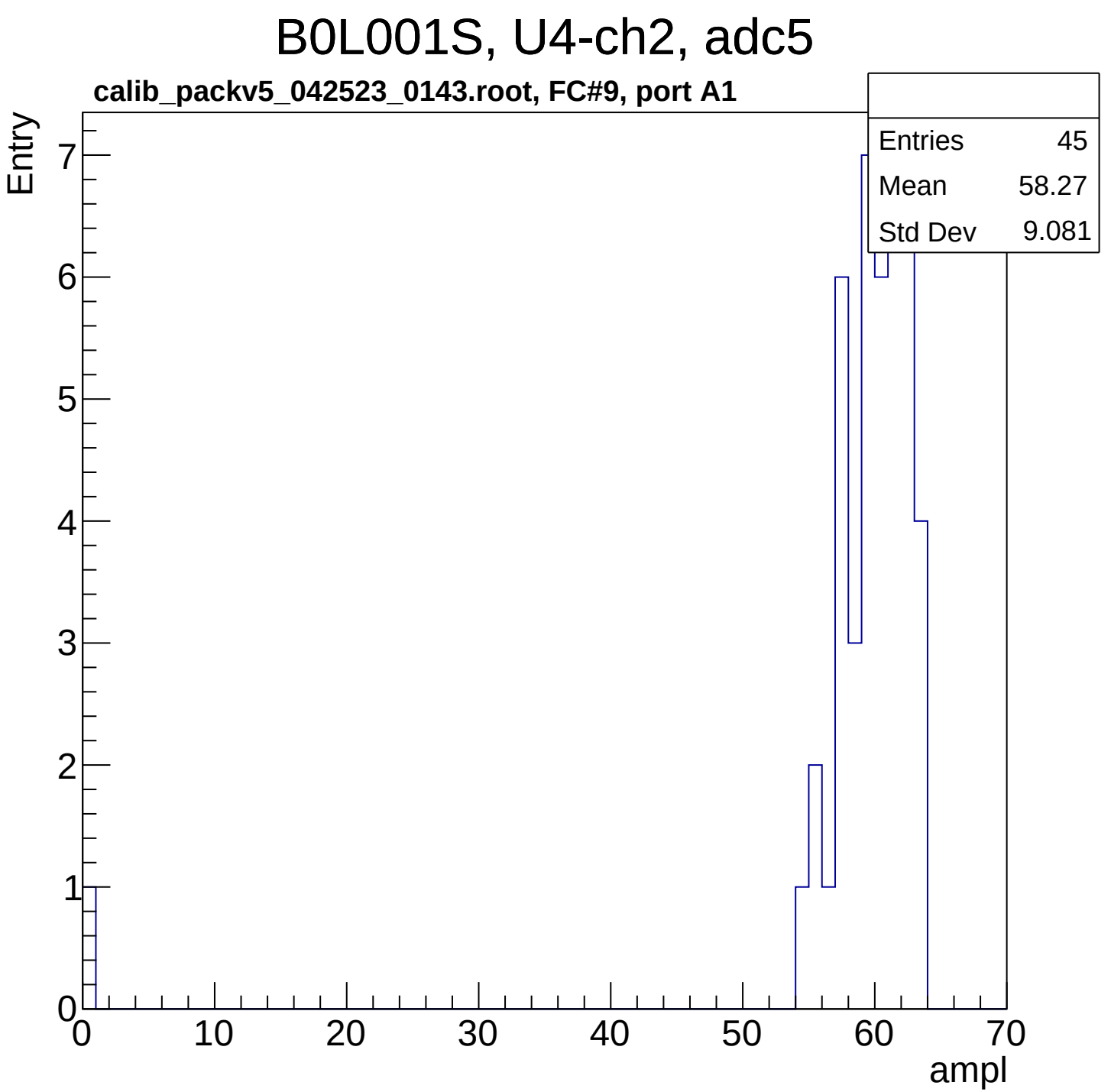
B0L001S, U4-ch2, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.27
Std Dev 9.081
Entry
ampl
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2
3
4
5
6
7
0
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20
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60
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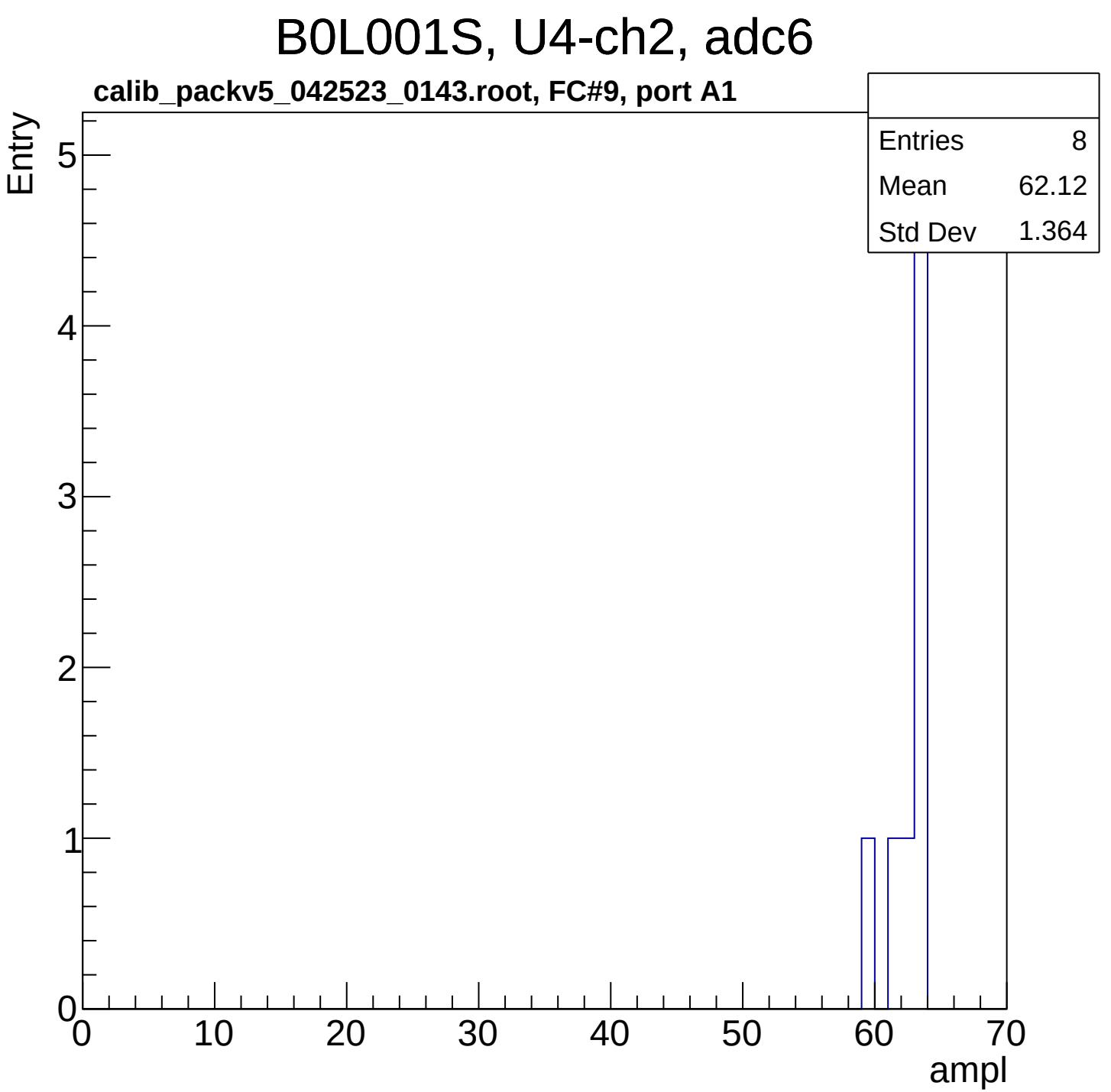
B0L001S, U4-ch2, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62.12
Std Dev 1.364
Entry
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2
3
4
5
0
10
20
30
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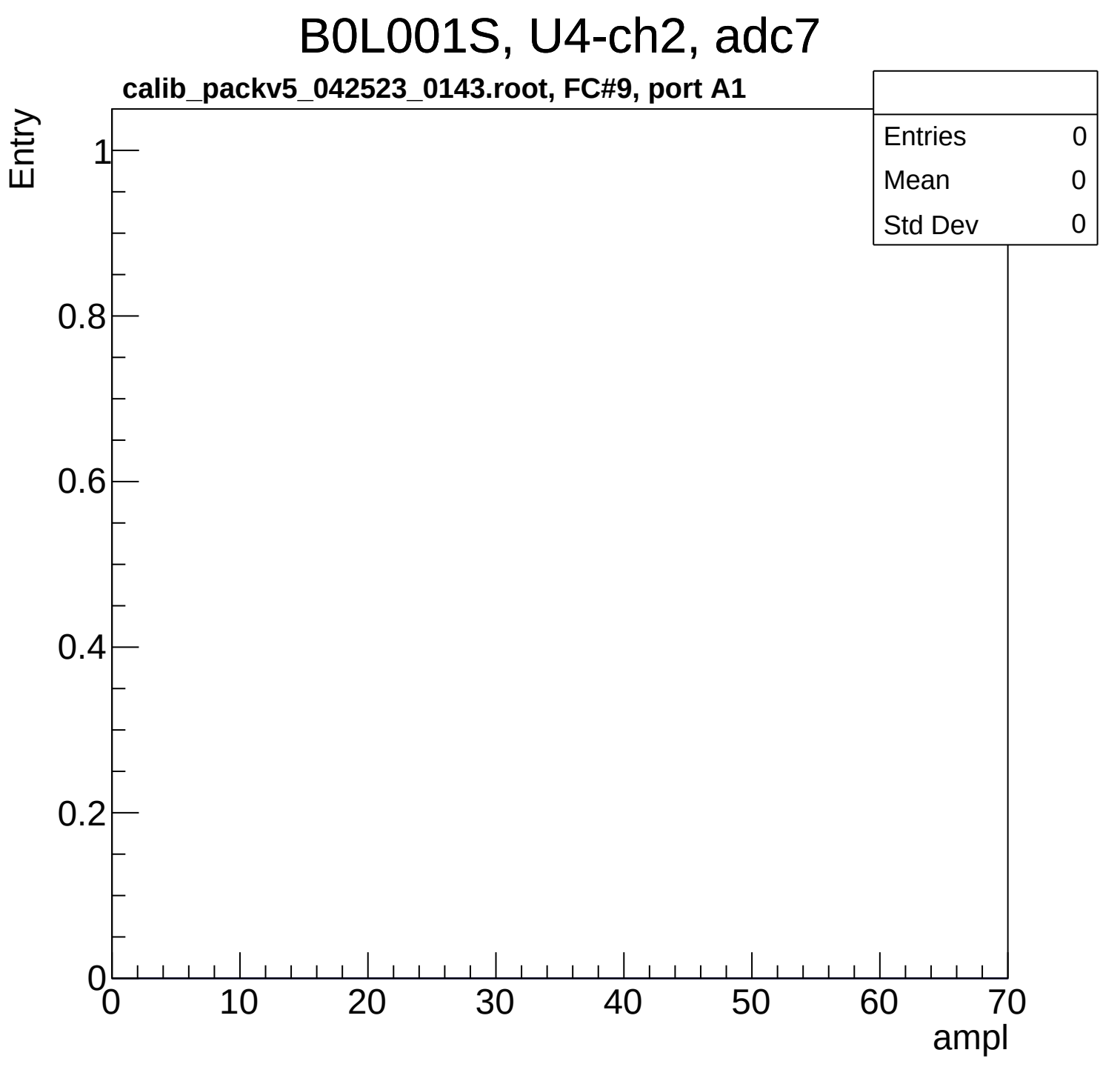
50
60
70
ampl


B0L001S, U4-ch2, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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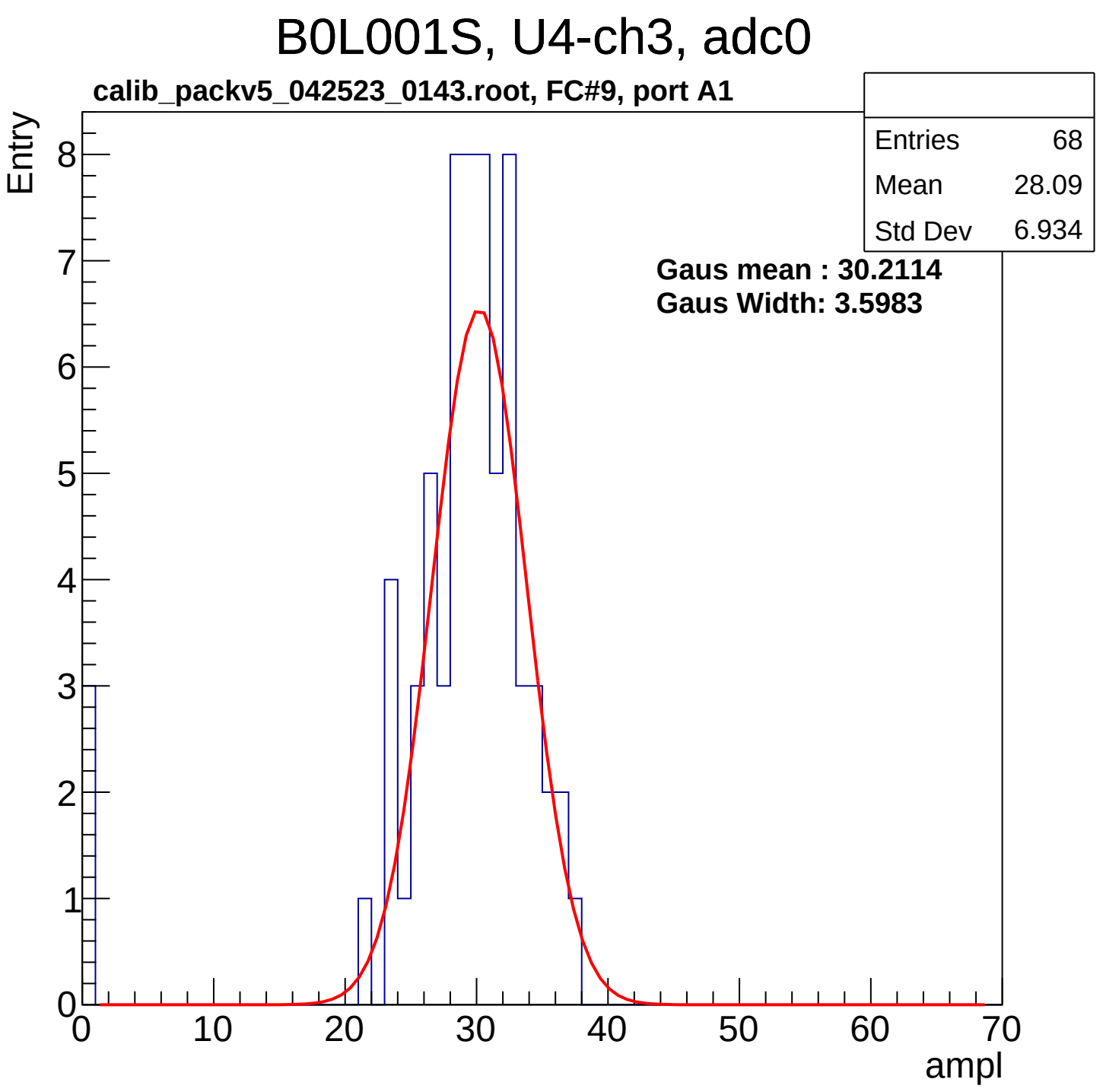
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch3, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 28.09
Std Dev 6.934
Gaus mean : 30.2114
Gaus Width: 3.5983
Entry
ampl
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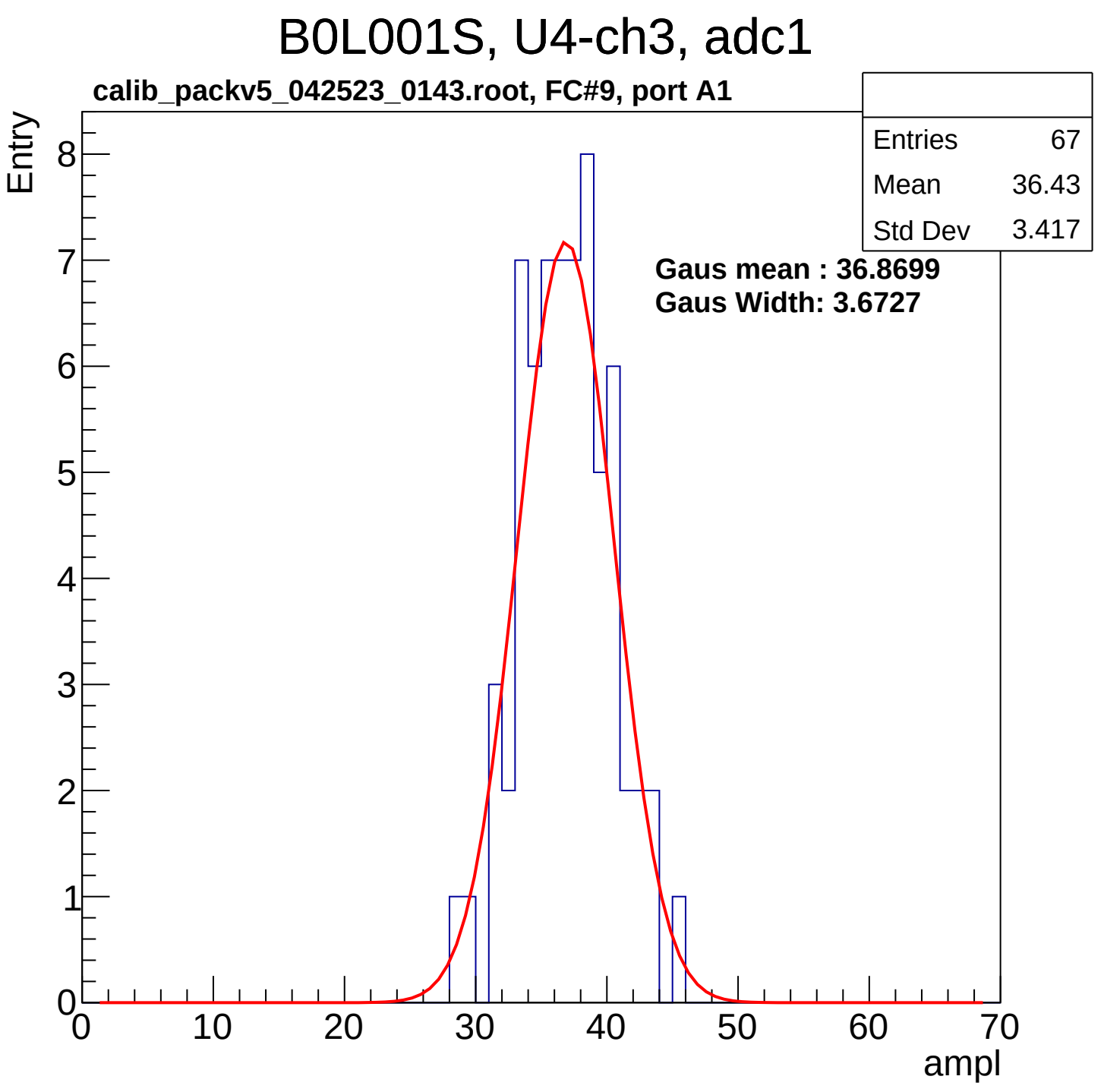
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch3, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.43
Std Dev 3.417
Gaus mean : 36.8699
Gaus Width: 3.6727
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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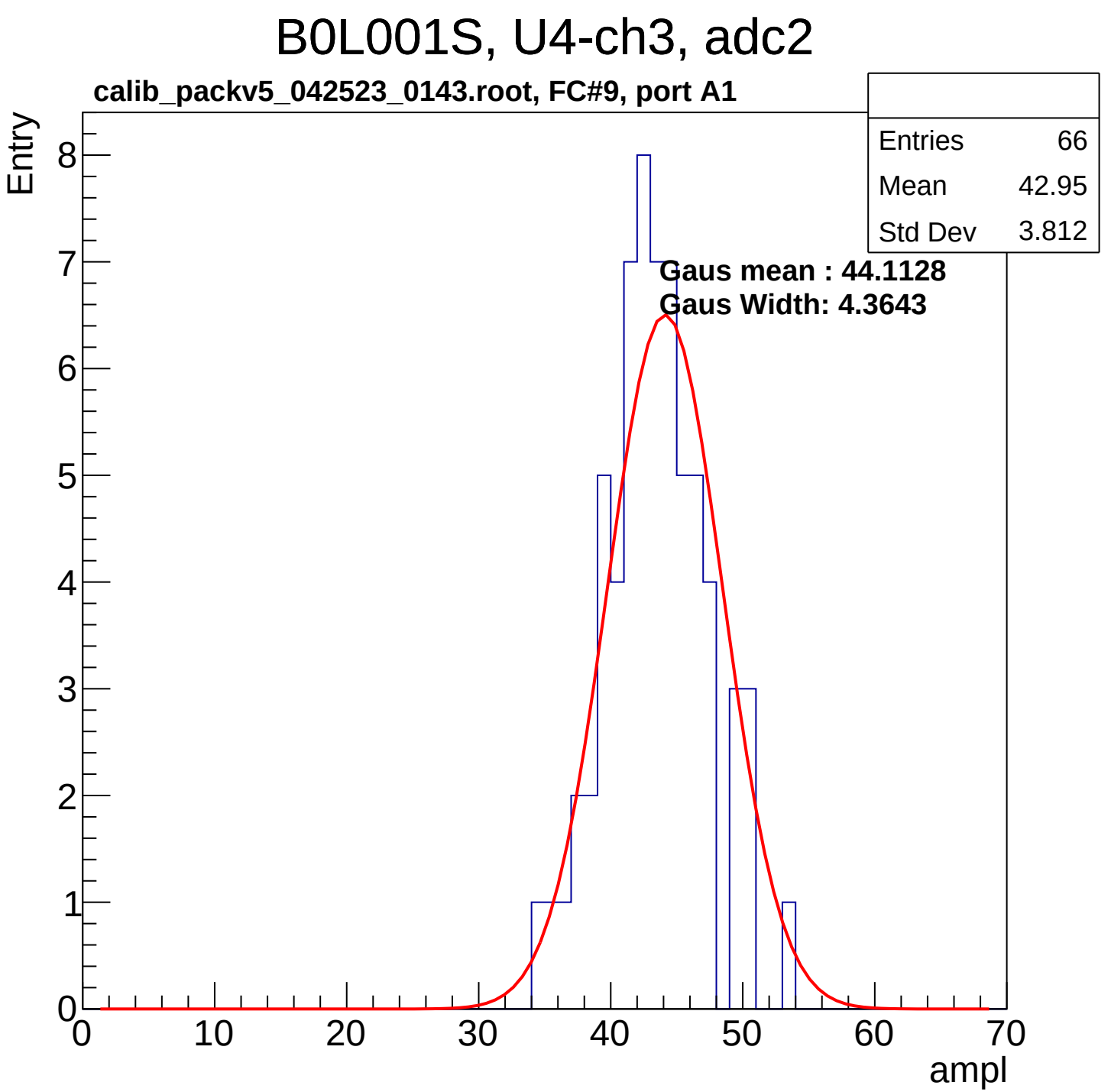
B0L001S, U4-ch3, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.95
Std Dev 3.812
Gaus mean : 44.1128
Gaus Width: 4.3643
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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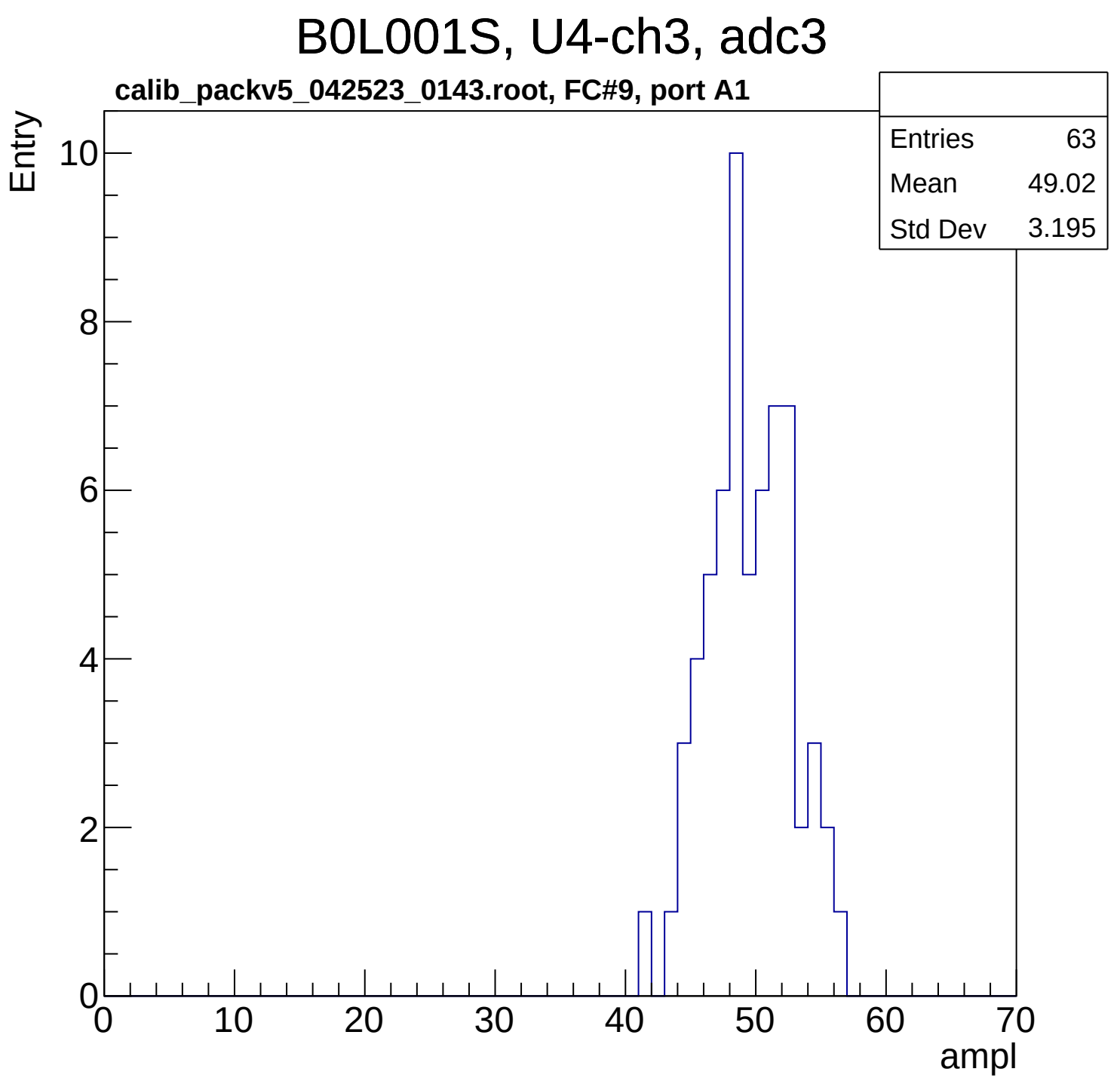
40
50
60
70


B0L001S, U4-ch3, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 49.02
Std Dev 3.195
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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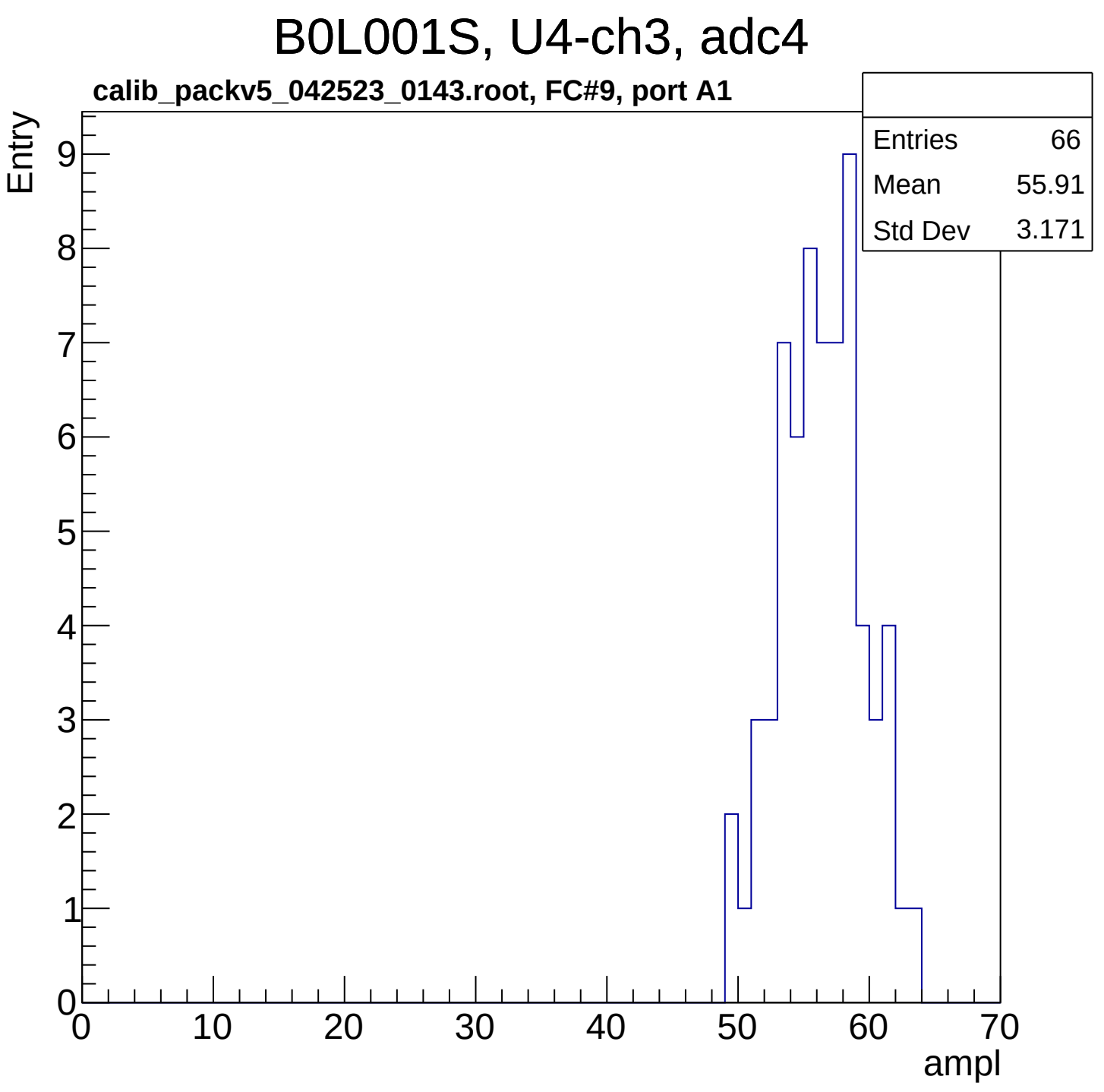
B0L001S, U4-ch3, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.91
Std Dev 3.171
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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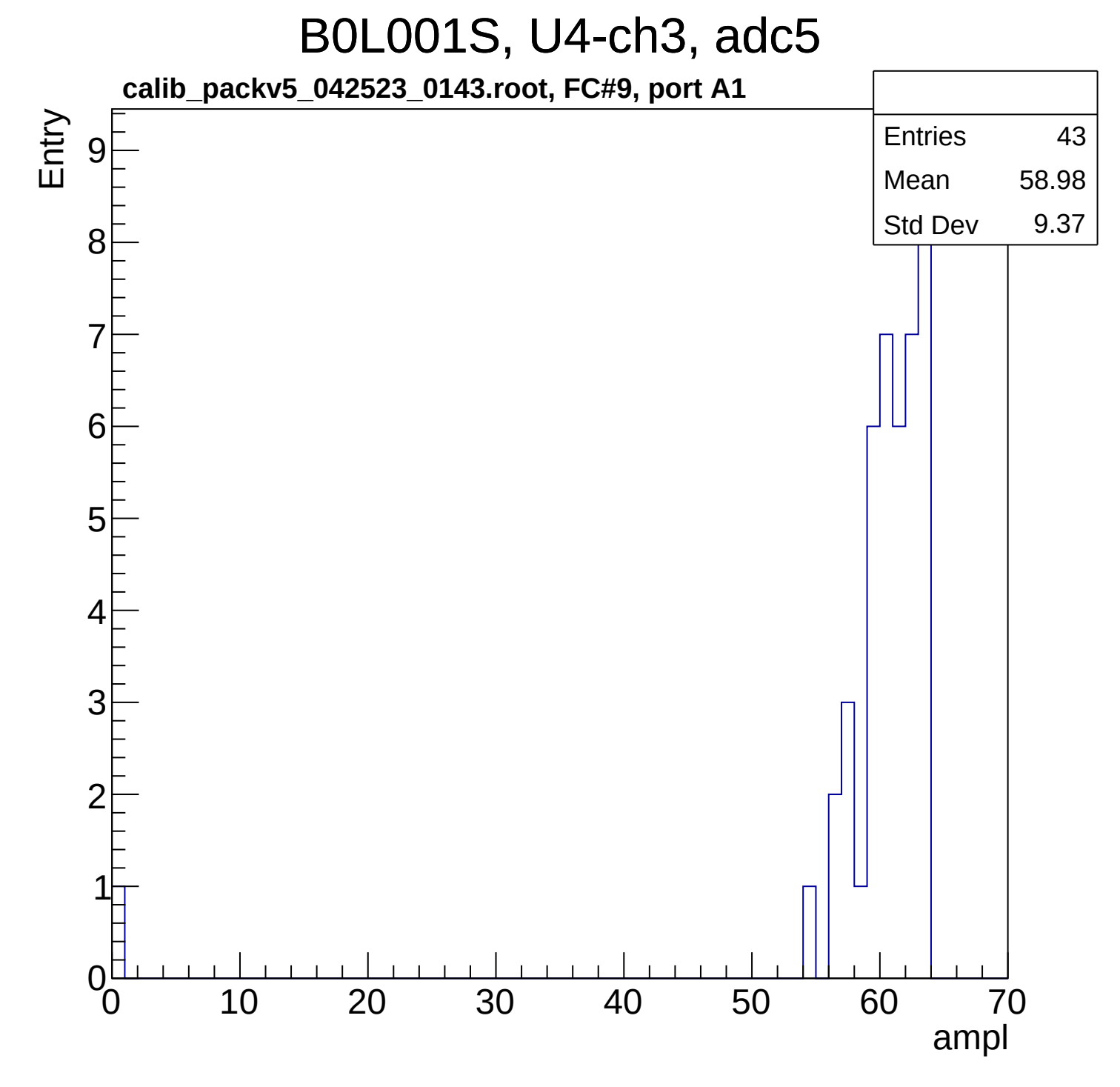
B0L001S, U4-ch3, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.98
Std Dev 9.37
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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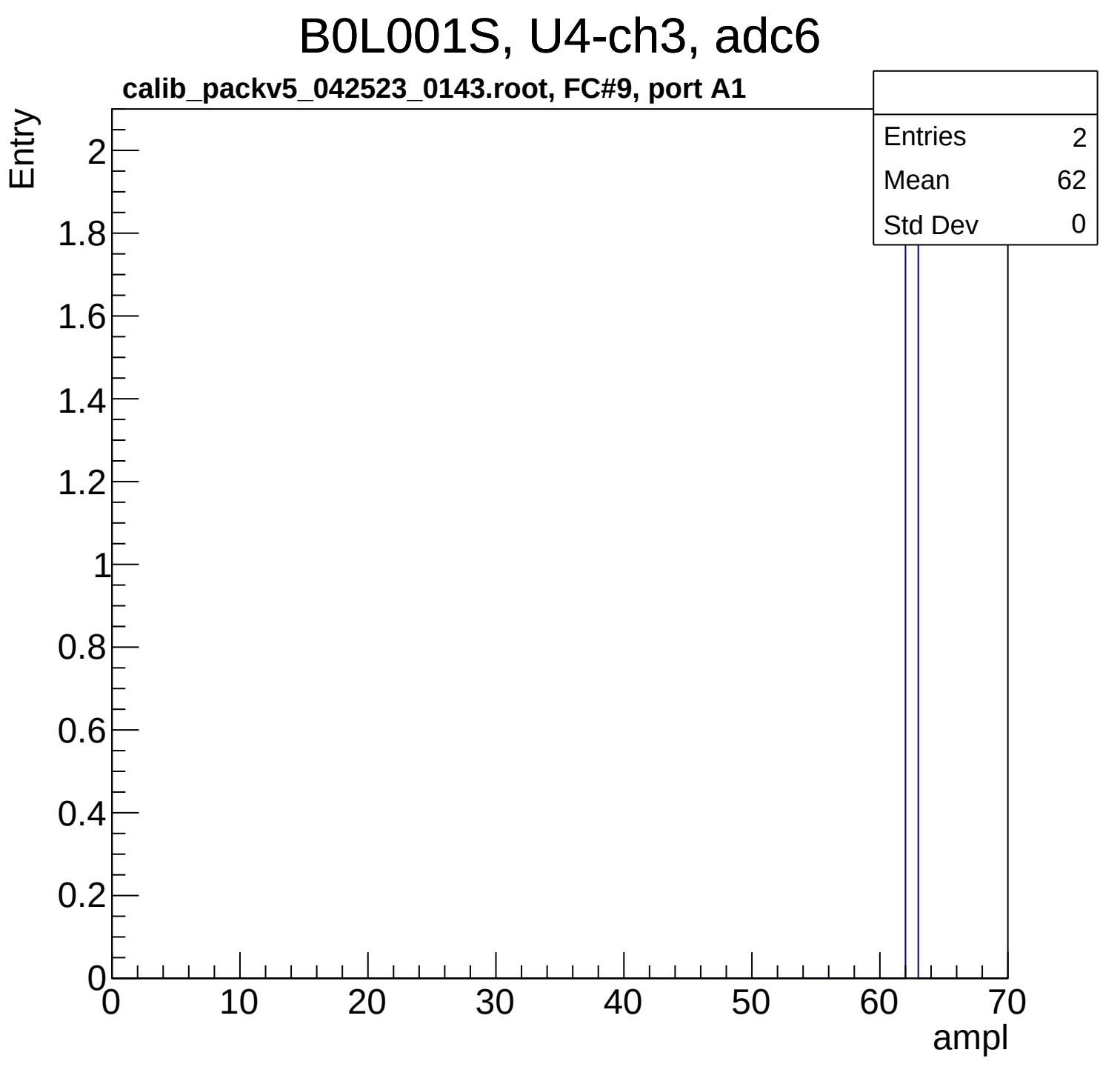
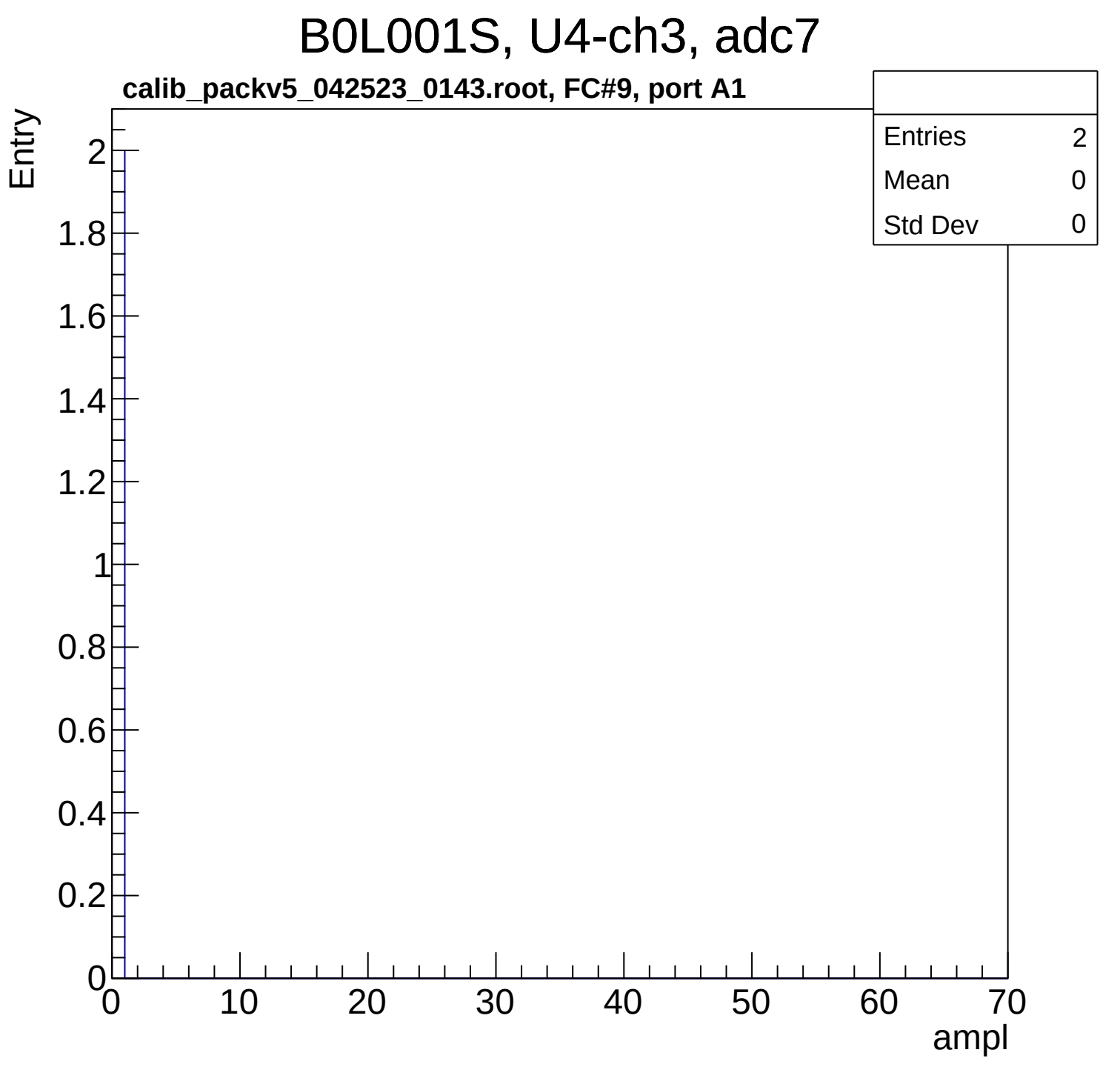

B0L001S, U4-ch3, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch3, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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60
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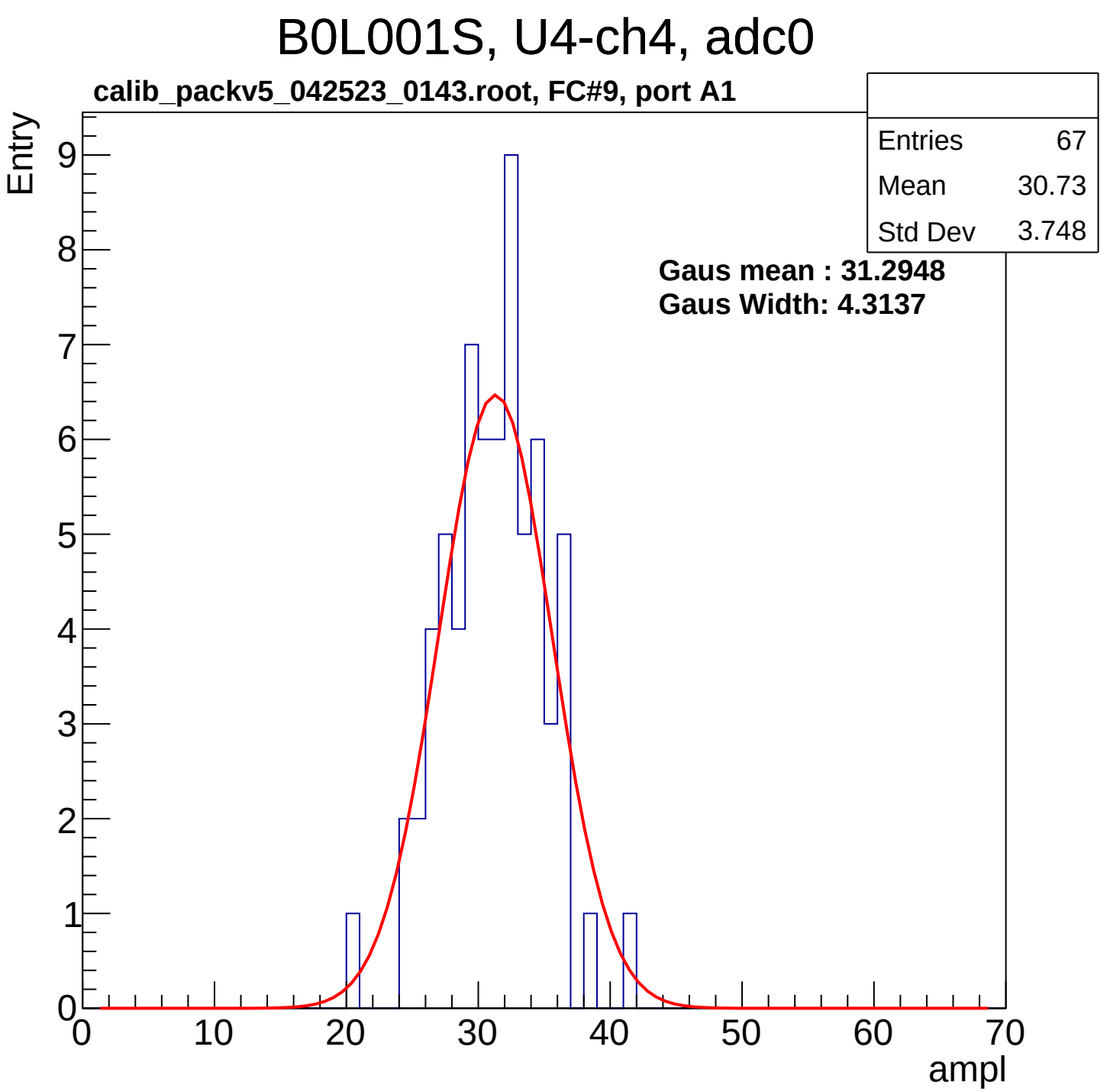
ampl


B0L001S, U4-ch4, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.73
Std Dev 3.748
Gaus mean : 31.2948
Gaus Width: 4.3137
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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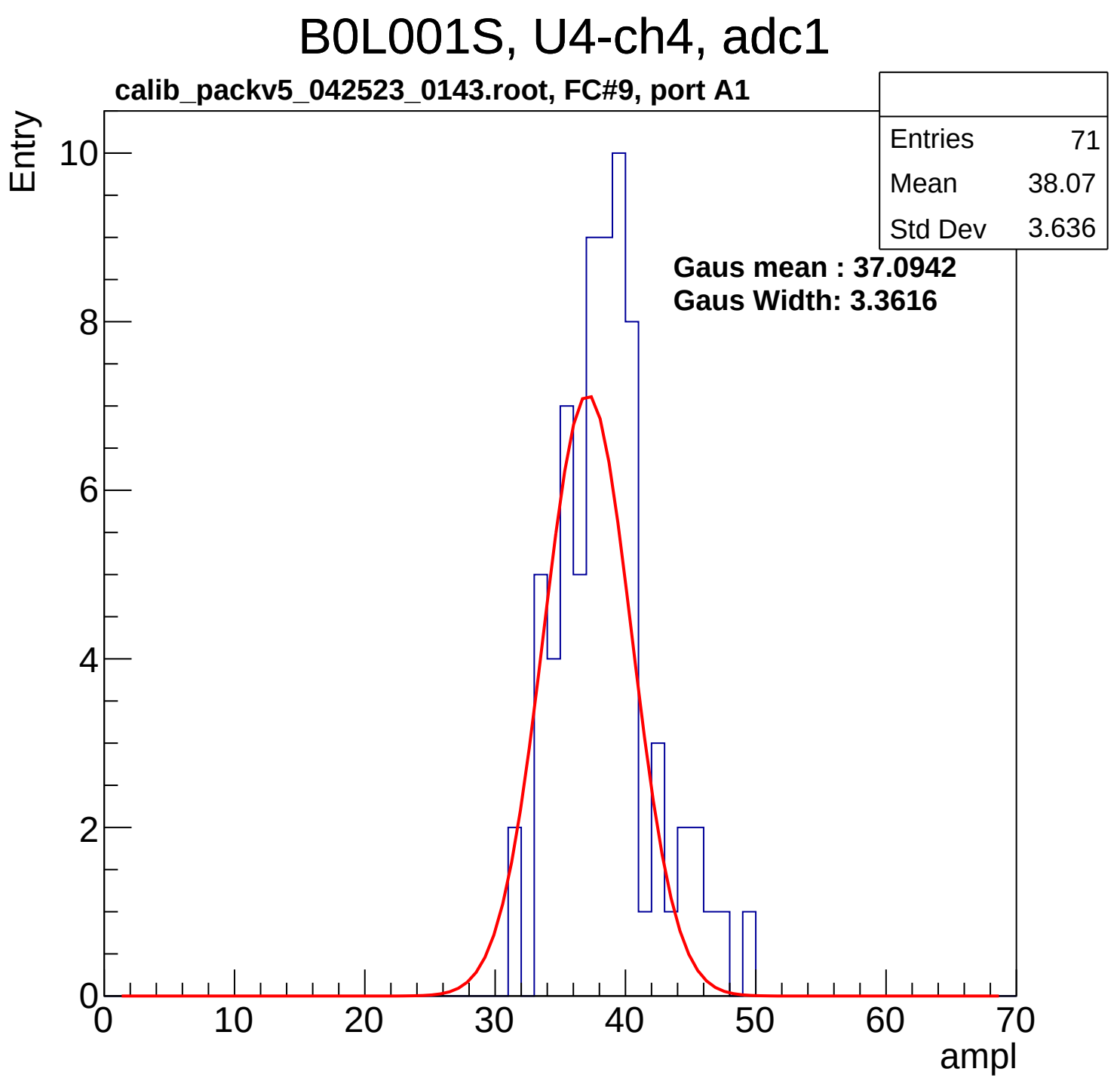
B0L001S, U4-ch4, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 38.07
Std Dev 3.636
Gaus mean : 37.0942
Gaus Width: 3.3616
Entry
ampl
0
2
4
6
8
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0
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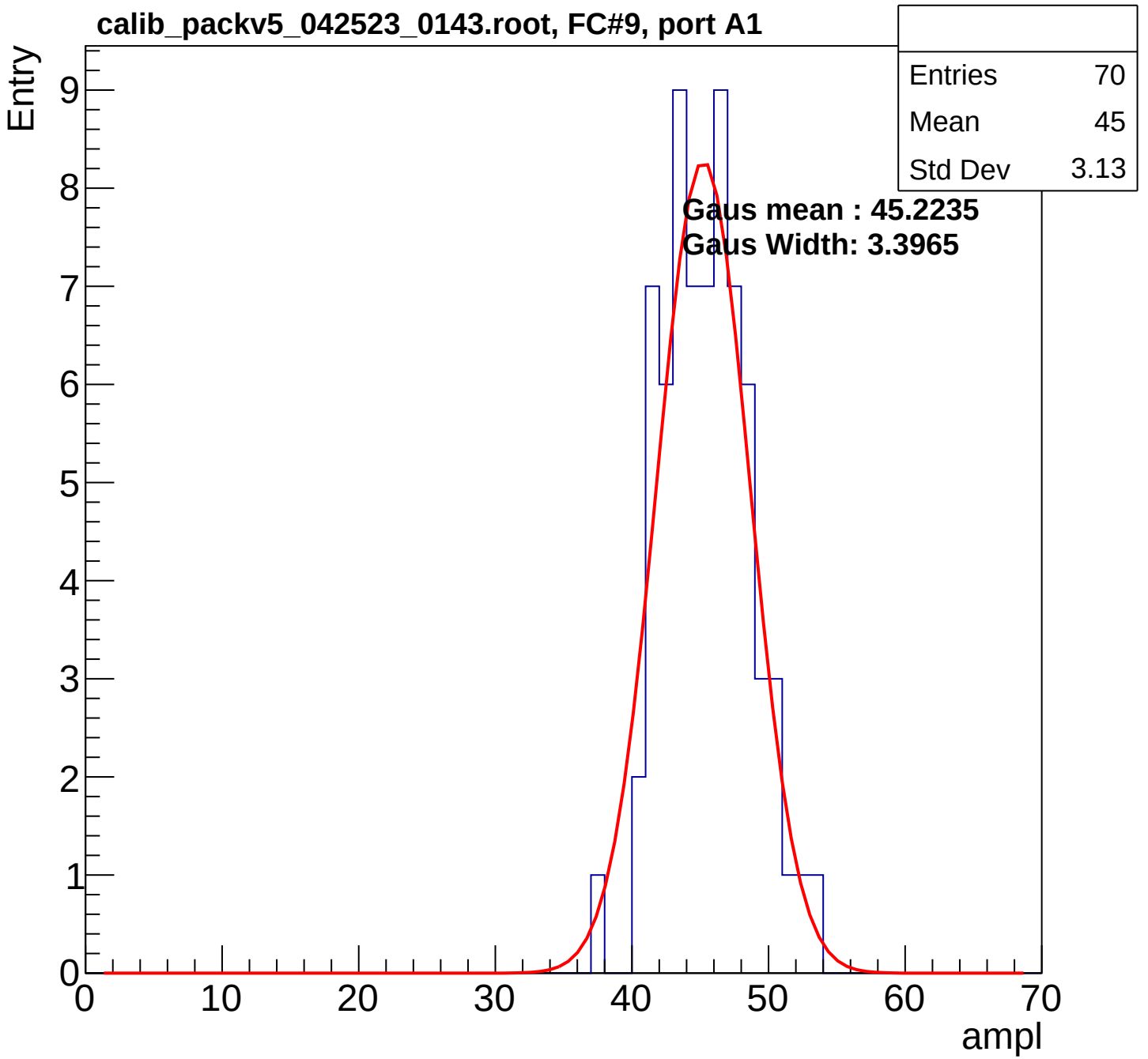
B0L001S, U4-ch4, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 45
Std Dev 3.13
Gaus mean : 45.2235
Gaus Width: 3.3965
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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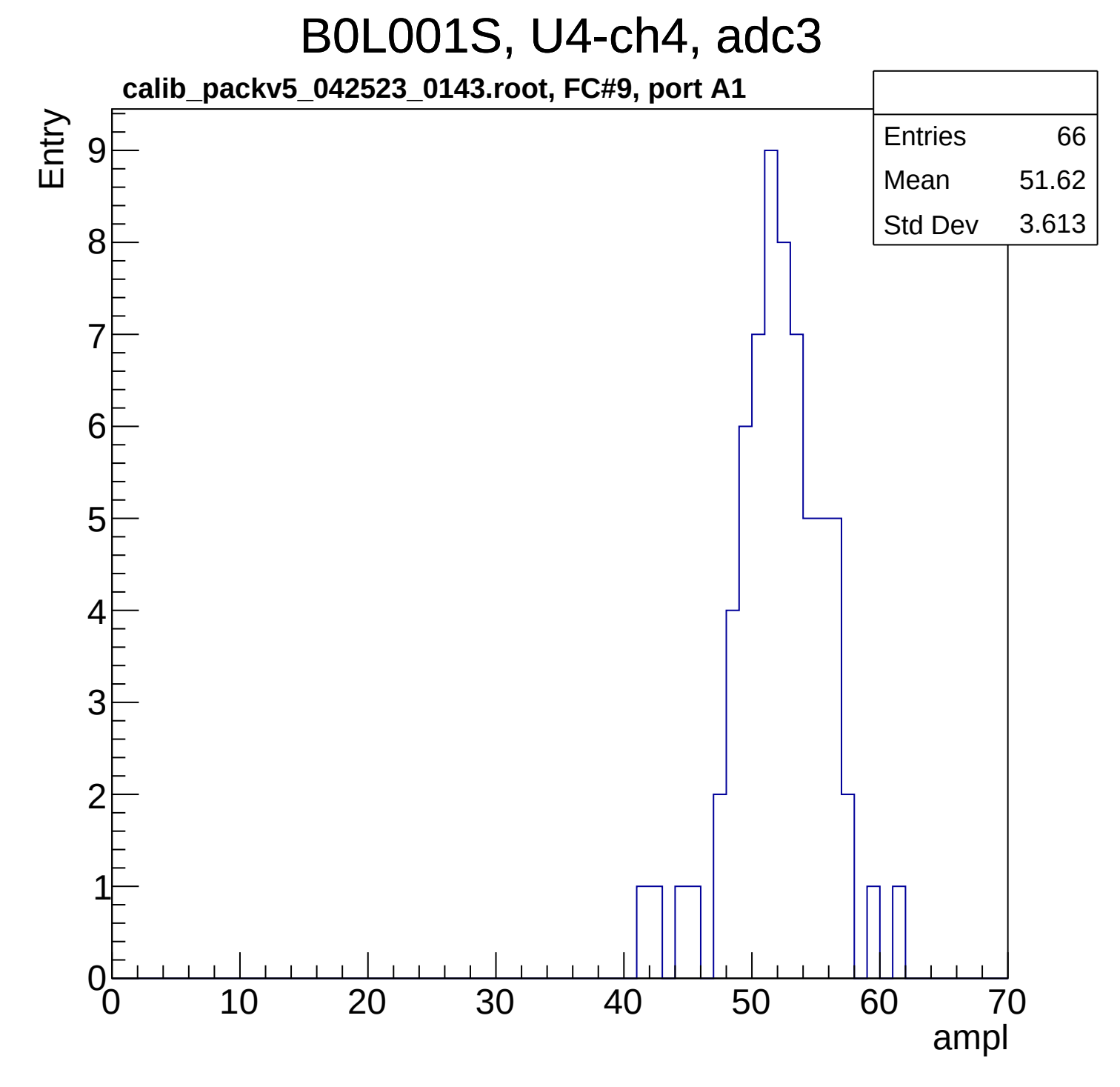
B0L001S, U4-ch4, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.62
Std Dev 3.613
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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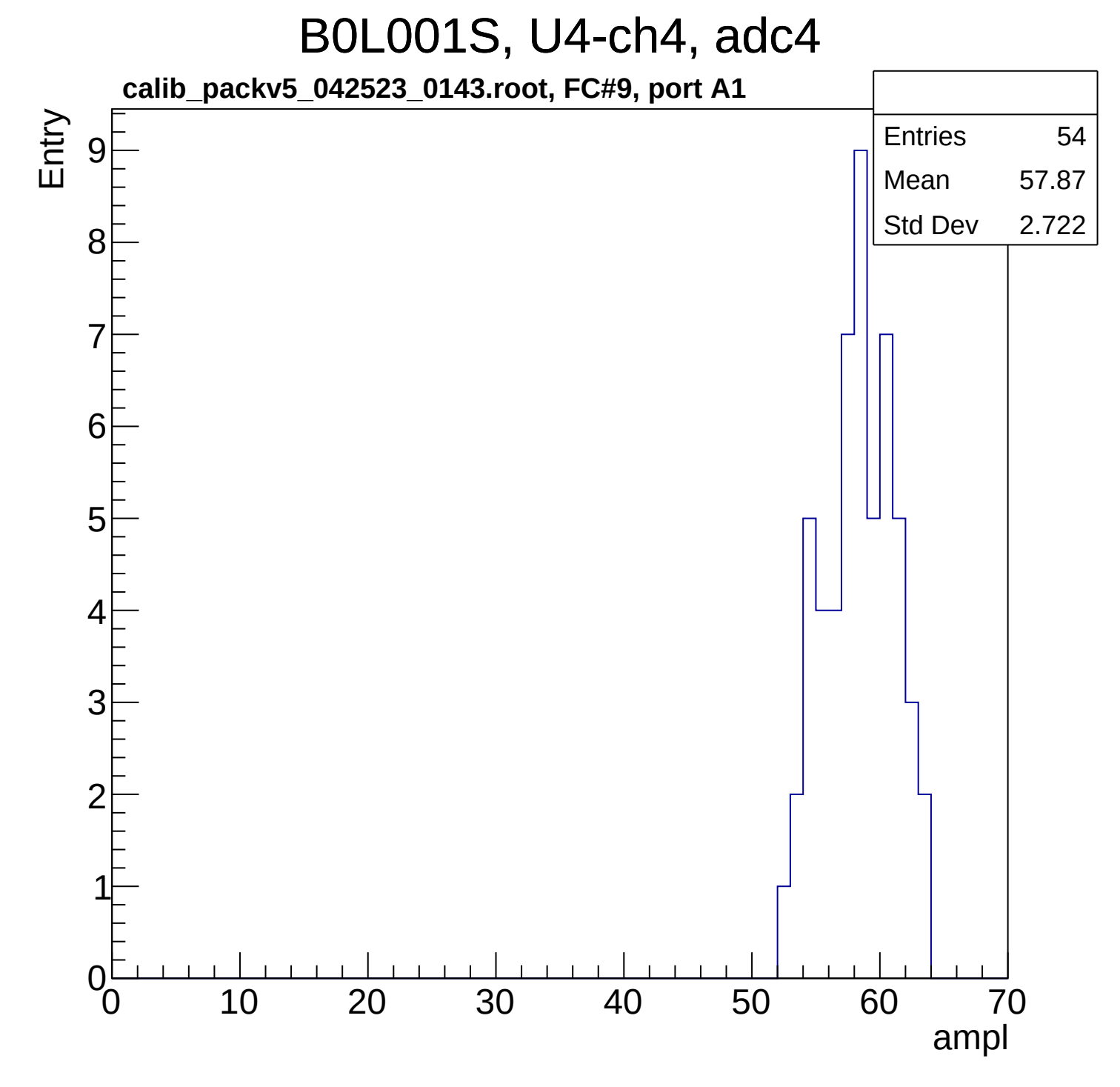
B0L001S, U4-ch4, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.87
Std Dev 2.722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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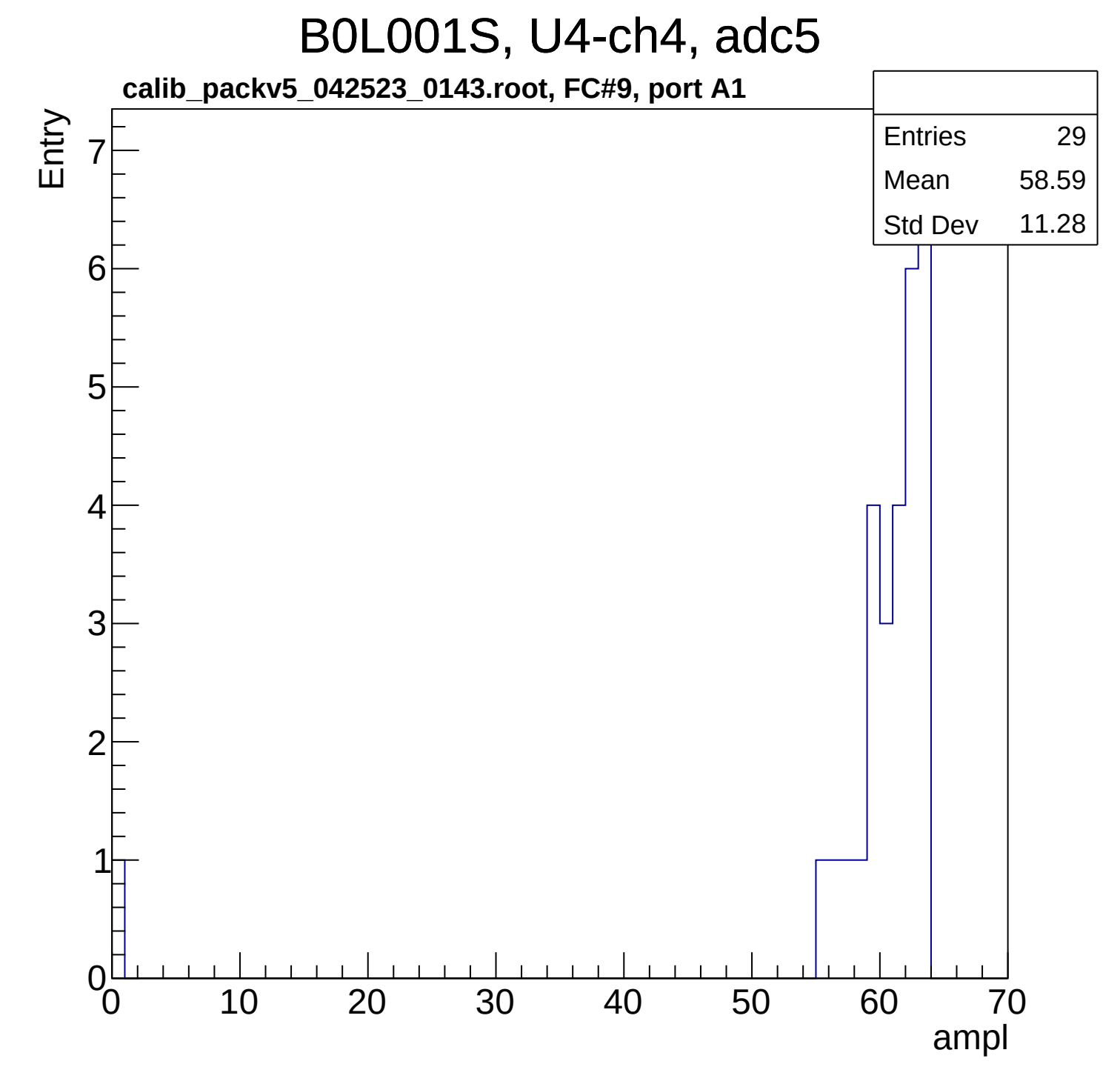
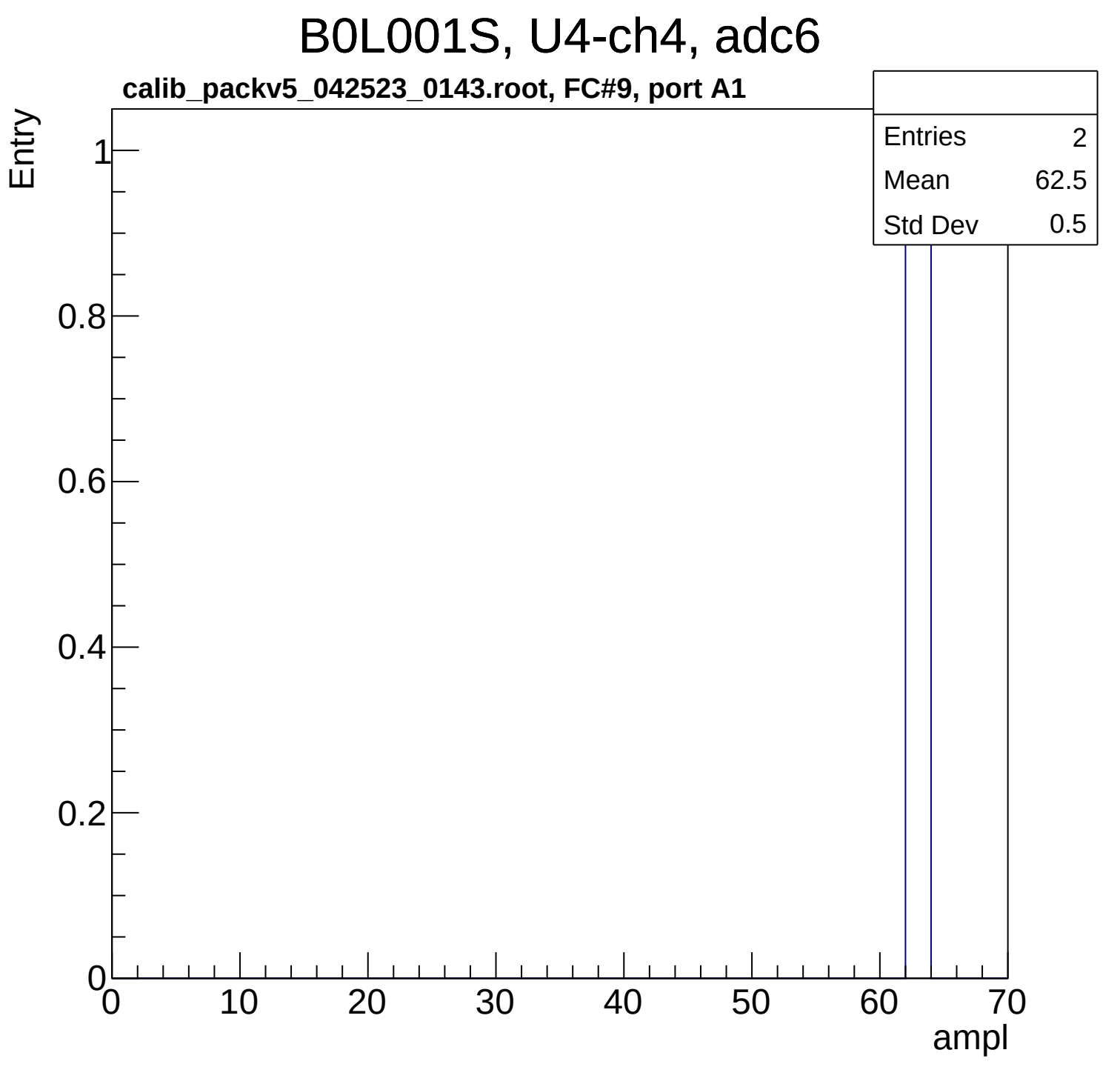
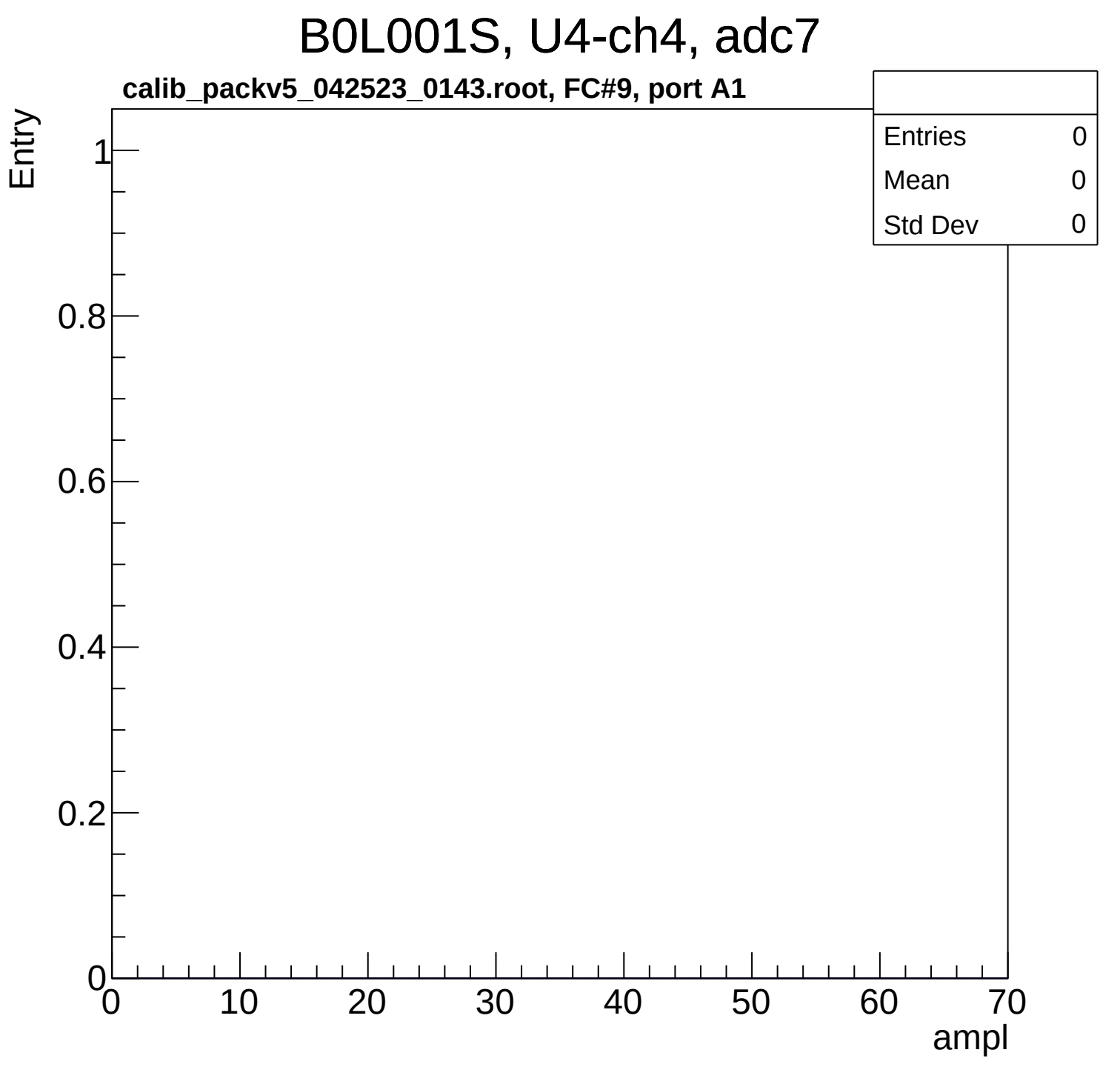
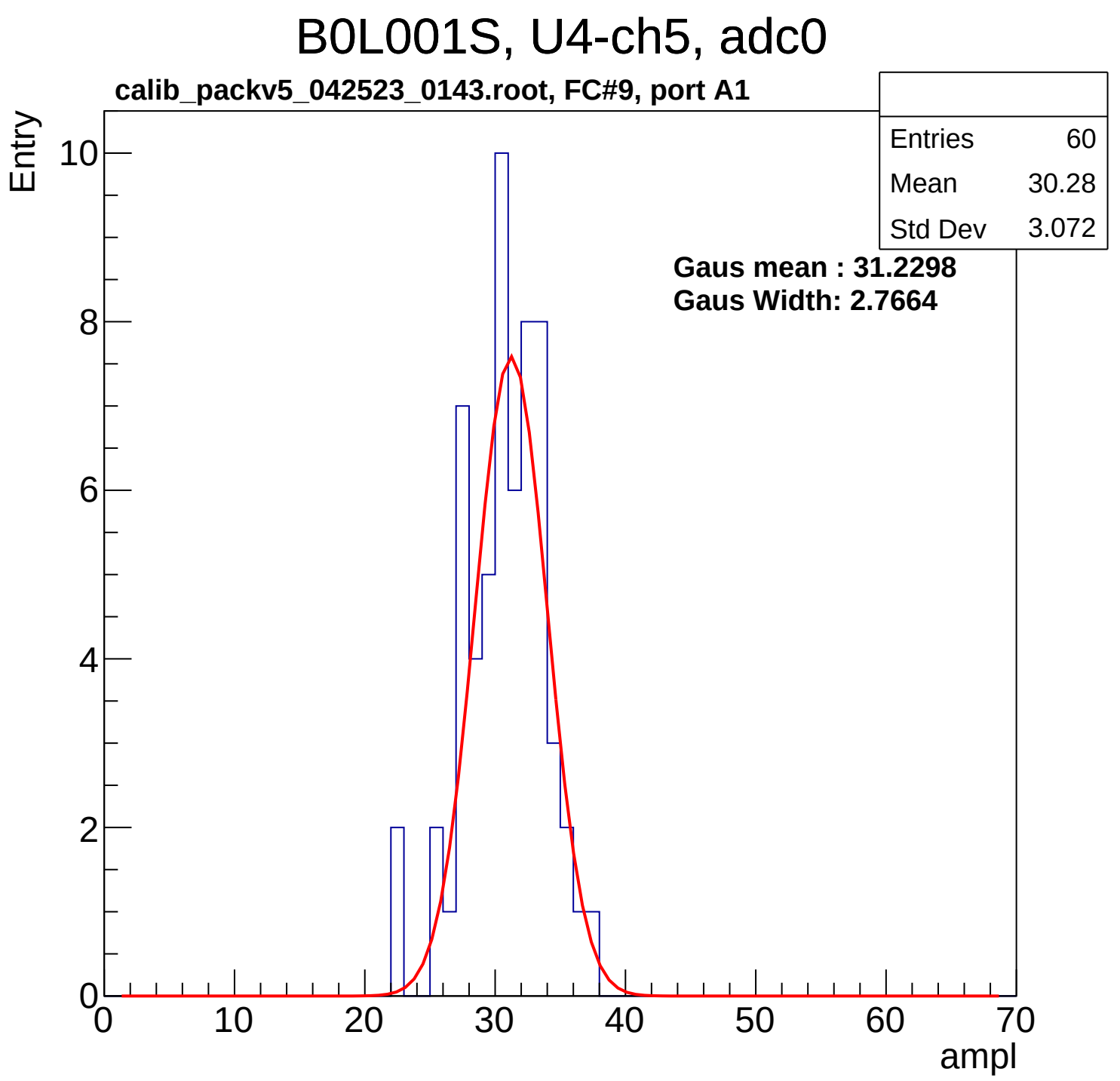
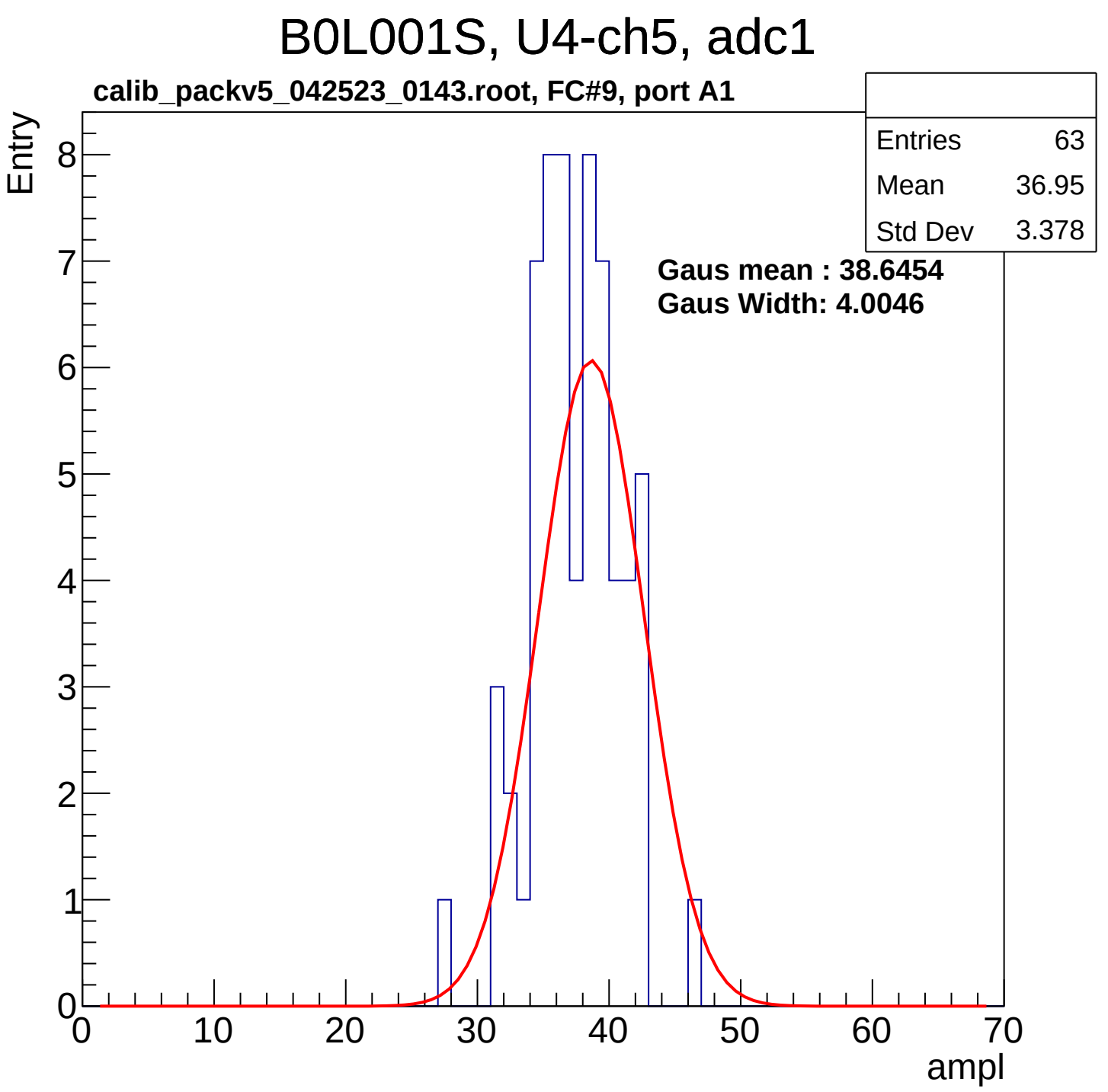

B0L001S, U4-ch4, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.59
Std Dev 11.28
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch4, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch4, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch5, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.28
Std Dev 3.072
Gaus mean : 31.2298
Gaus Width: 2.7664
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch5, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.95
Std Dev 3.378
Gaus mean : 38.6454
Gaus Width: 4.0046
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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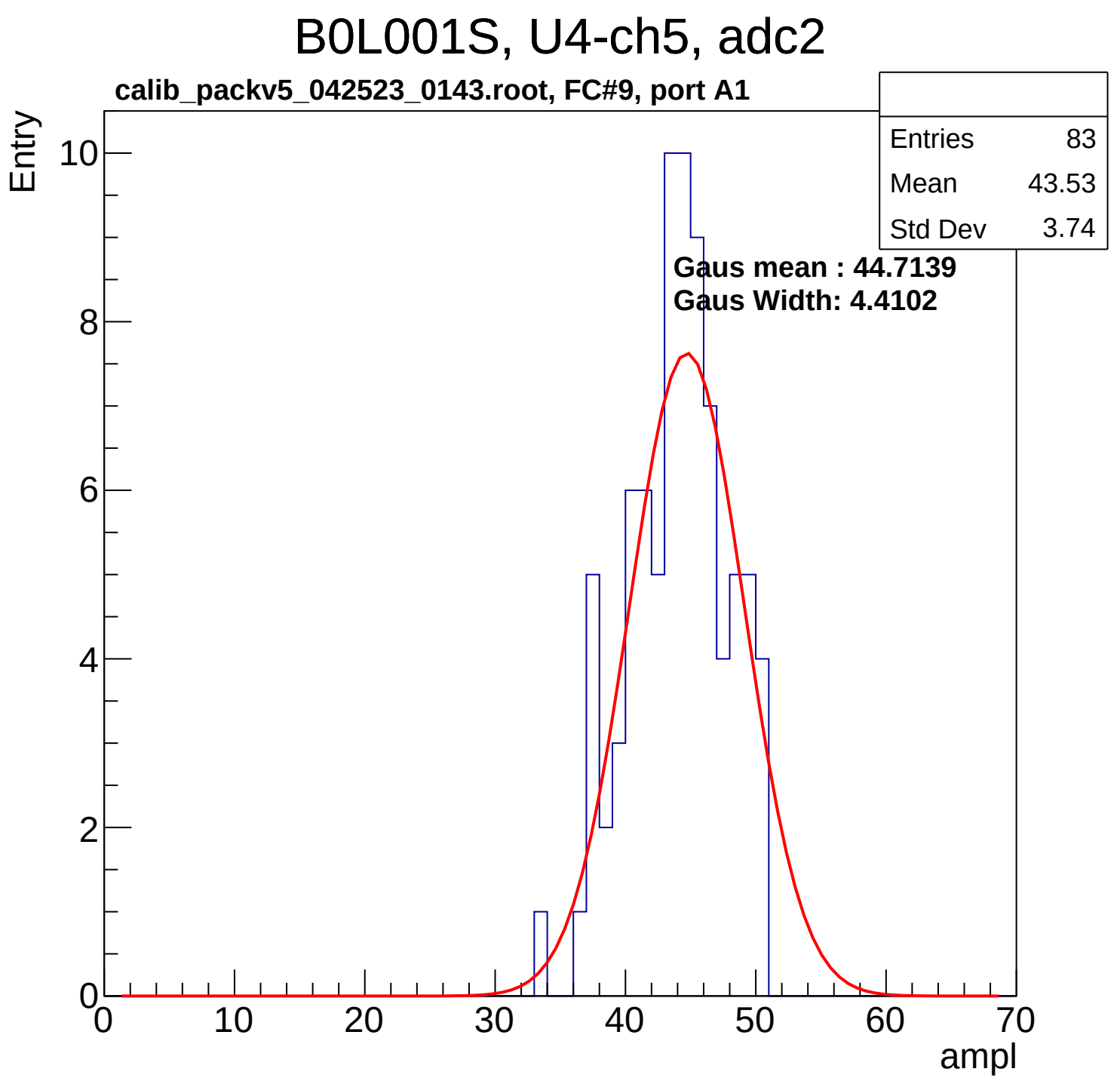
B0L001S, U4-ch5, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 43.53
Std Dev 3.74
Gaus mean : 44.7139
Gaus Width: 4.4102
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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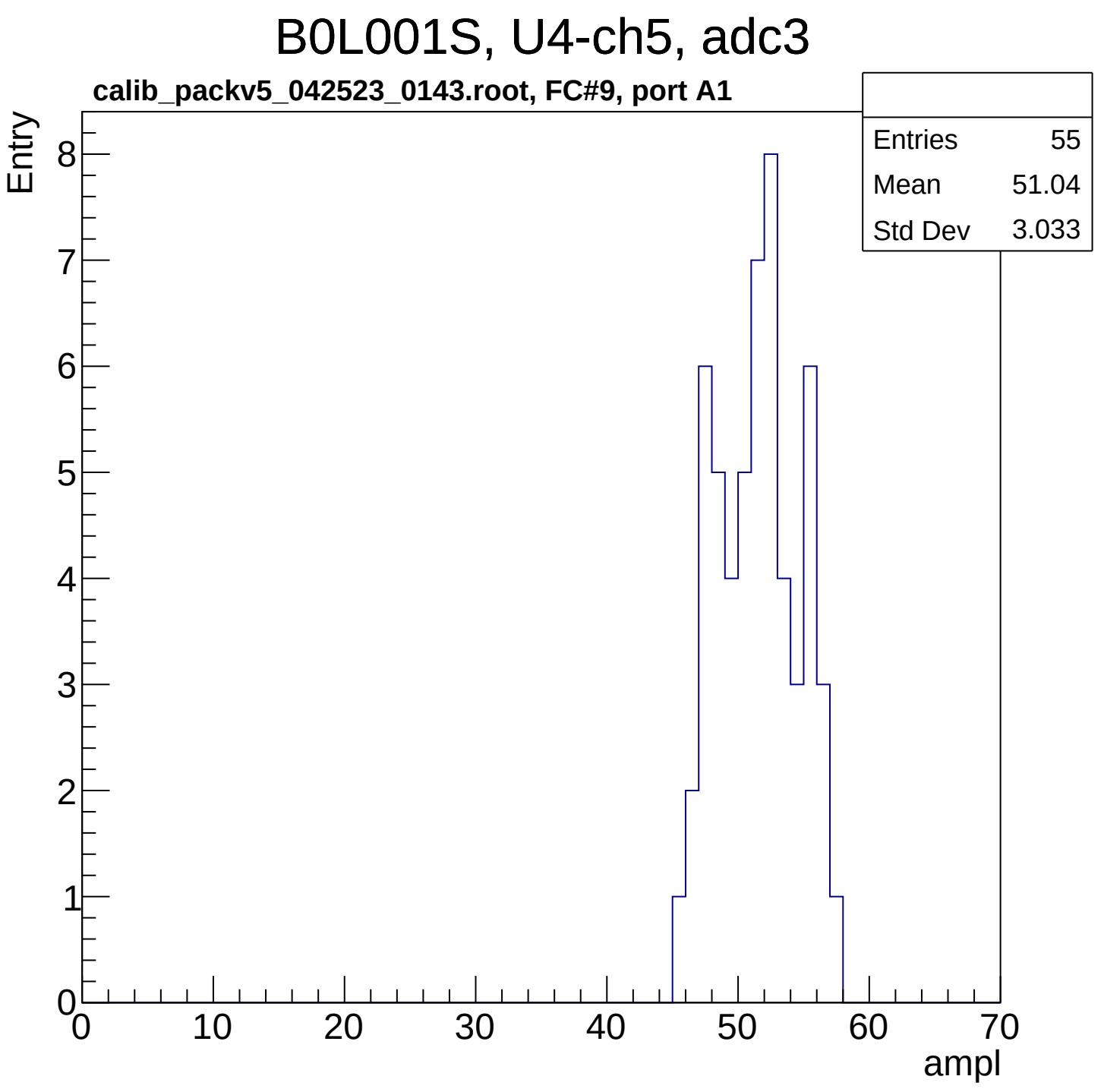
B0L001S, U4-ch5, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 51.04
Std Dev 3.033
Entry
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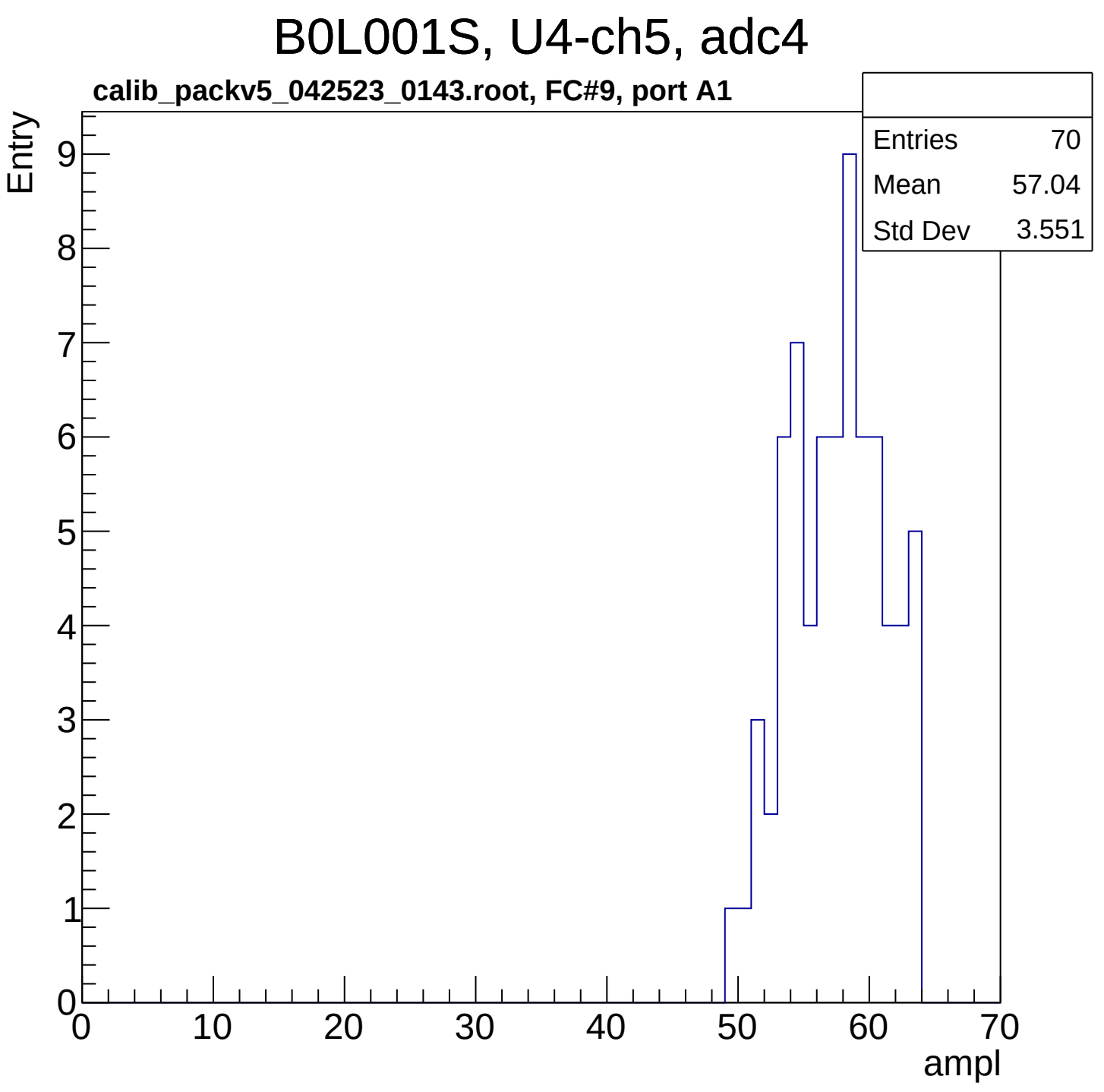
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch5, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 57.04
Std Dev 3.551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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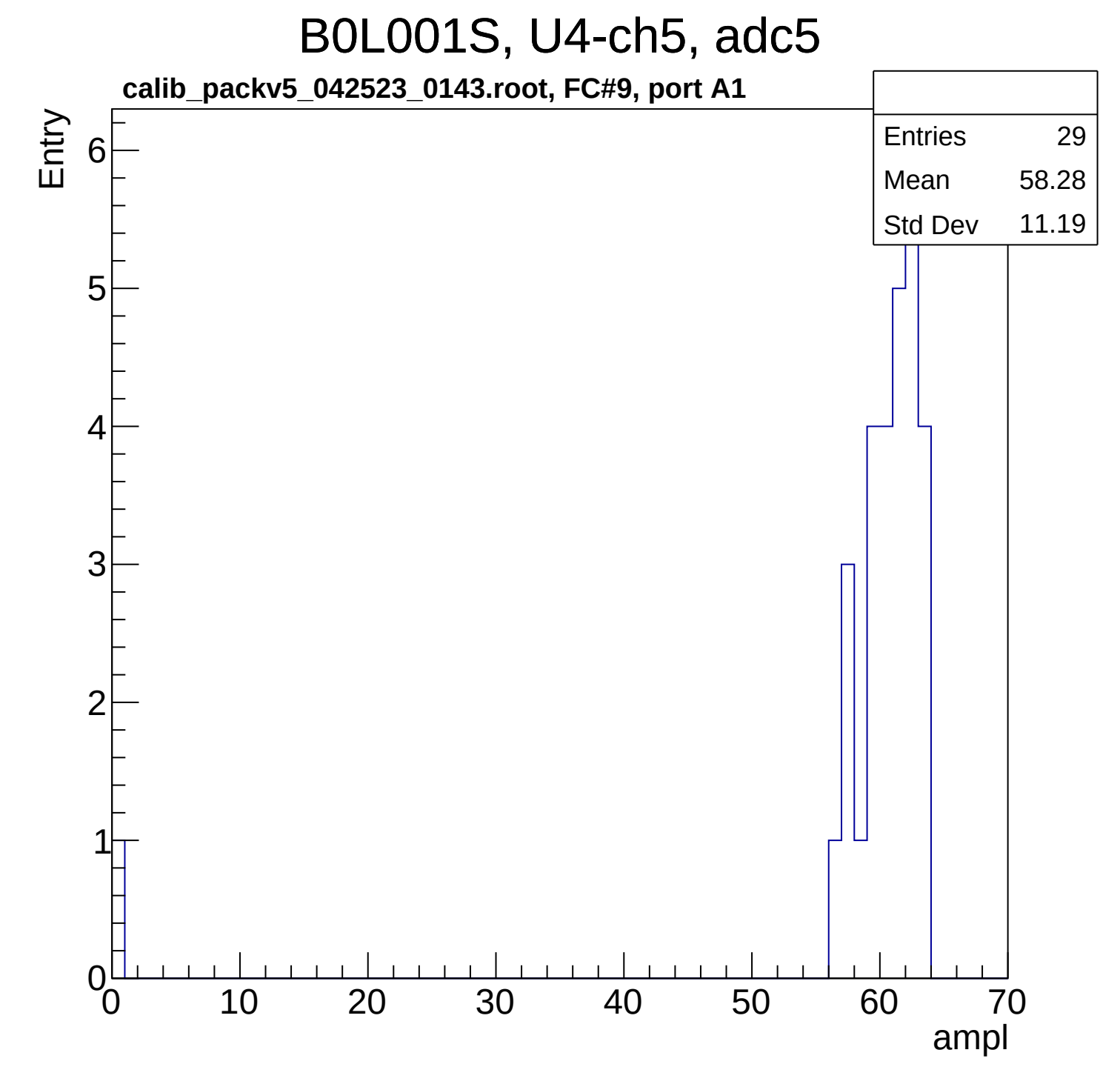
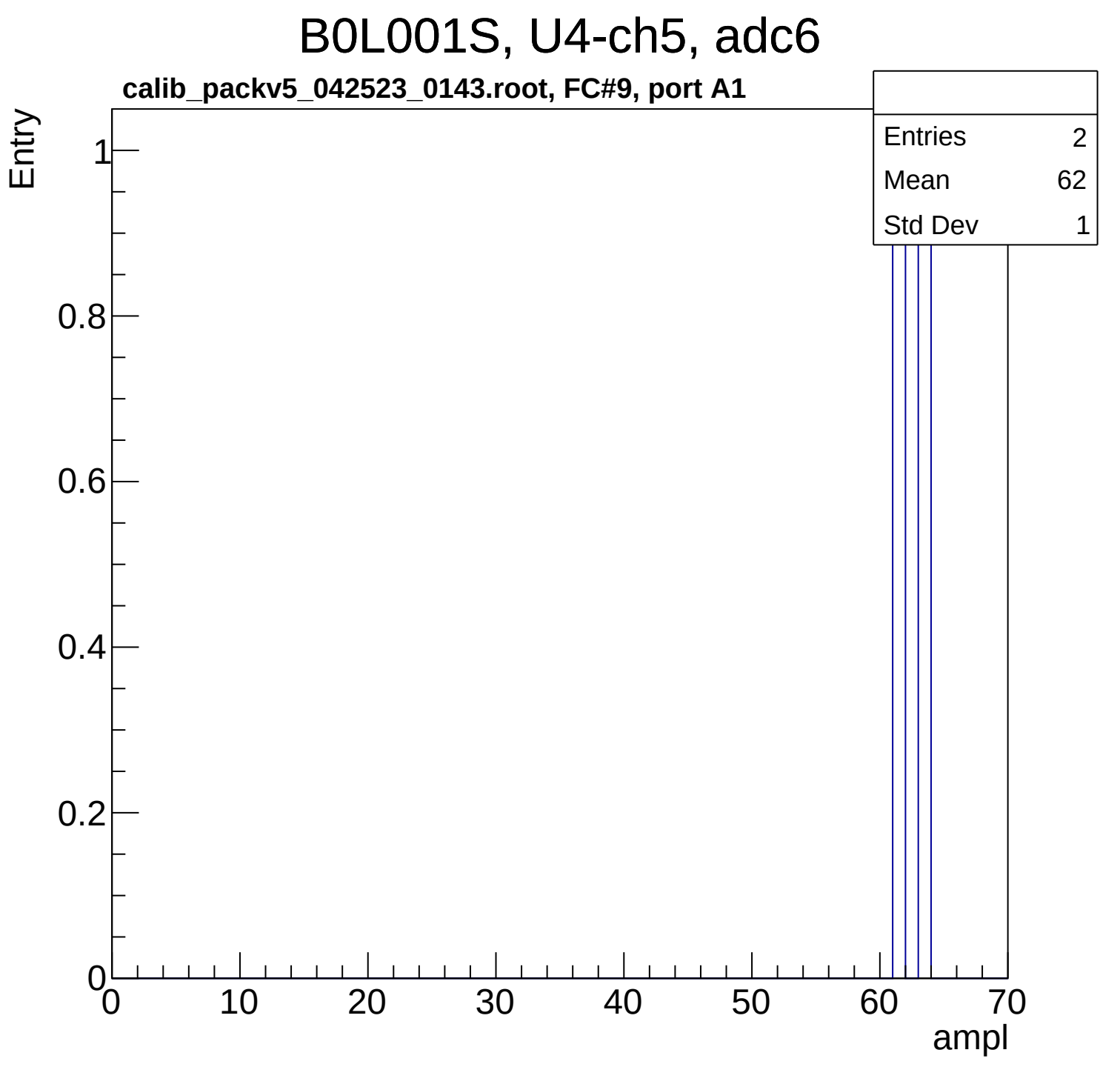
60
70


B0L001S, U4-ch5, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.28
Std Dev 11.19
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch5, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
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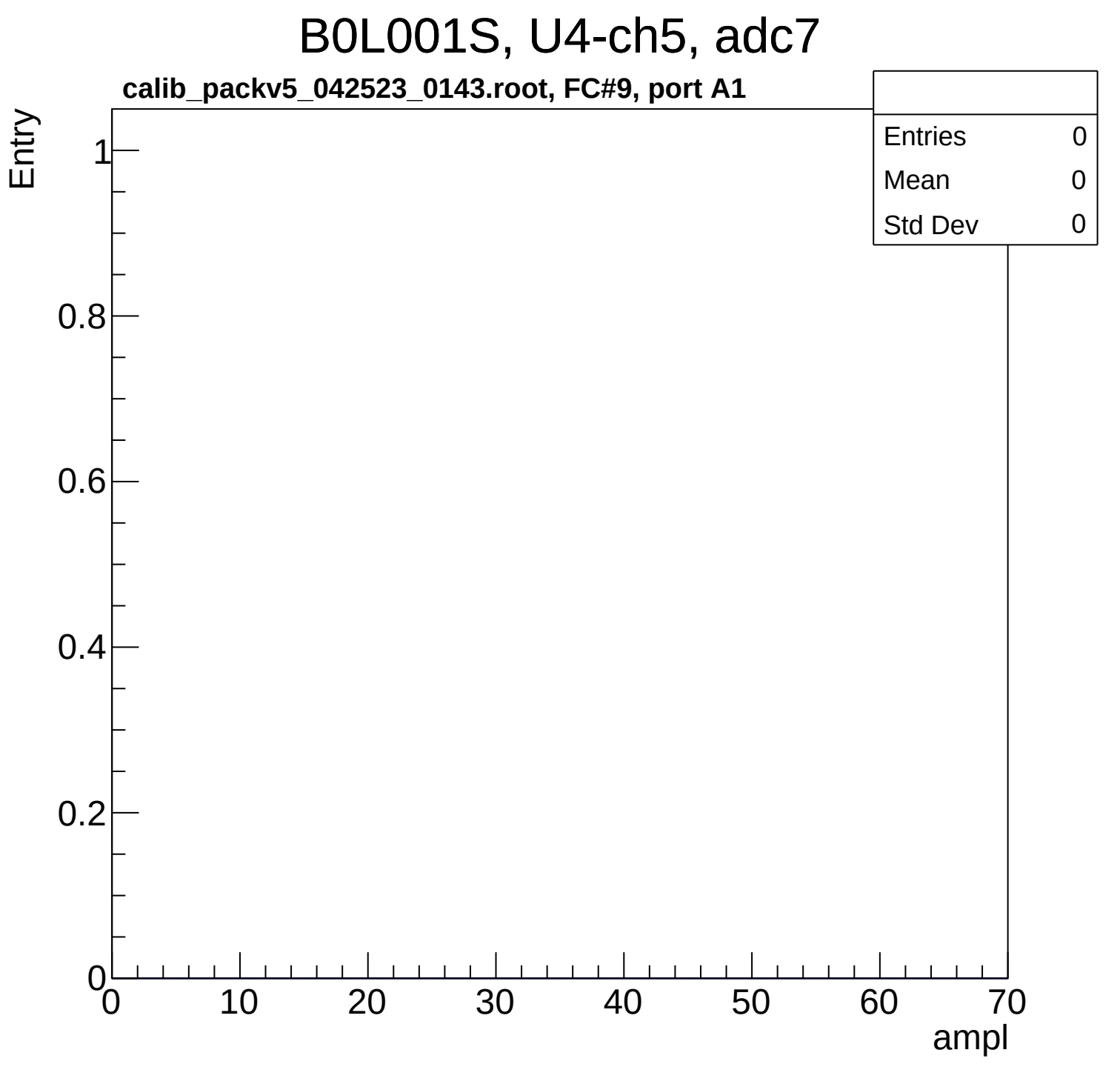
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch5, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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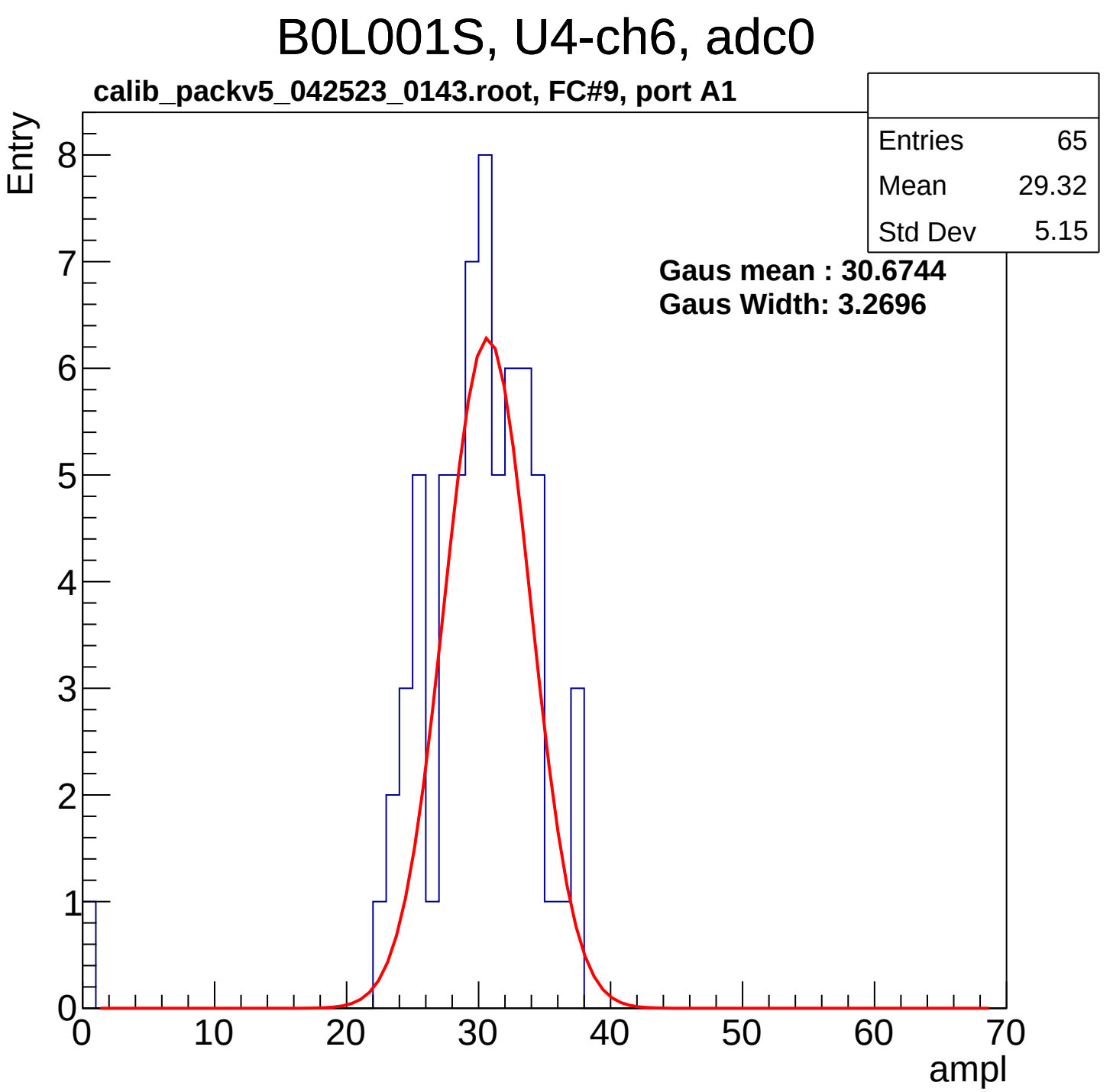
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch6, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.32
Std Dev 5.15
Gaus mean : 30.6744
Gaus Width: 3.2696
Entry
ampl
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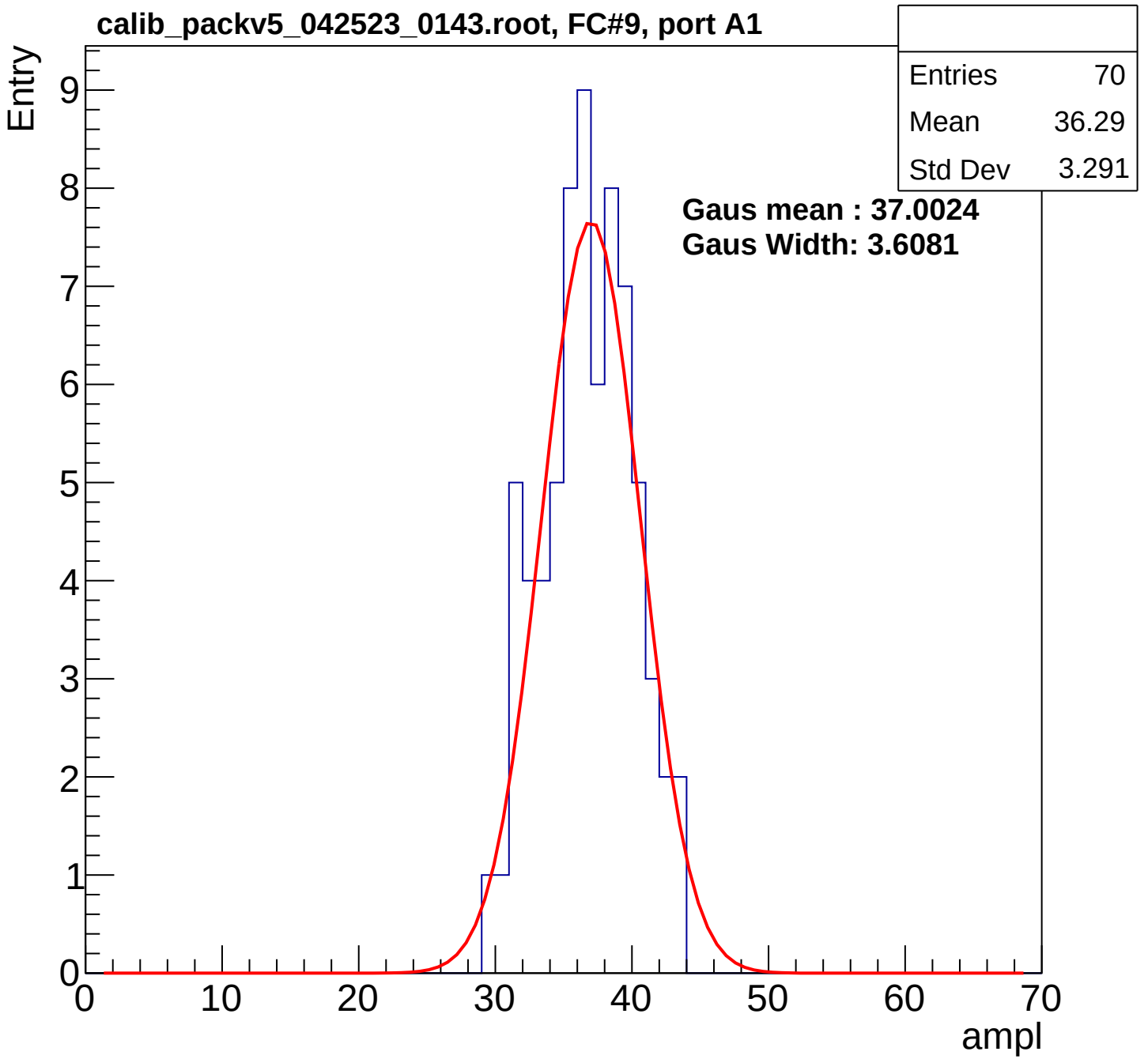
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch6, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.29
Std Dev 3.291
Gaus mean : 37.0024
Gaus Width: 3.6081
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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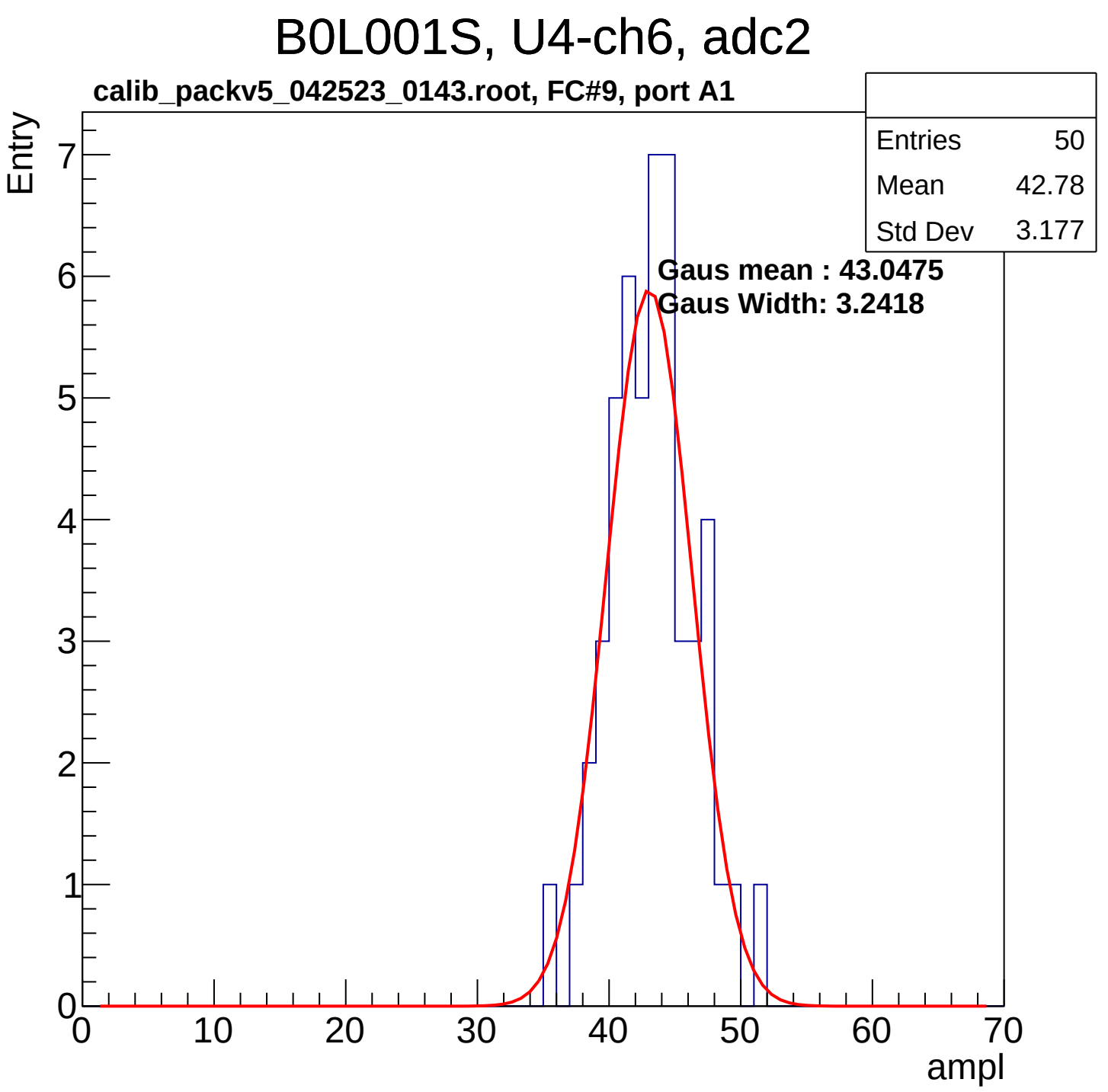
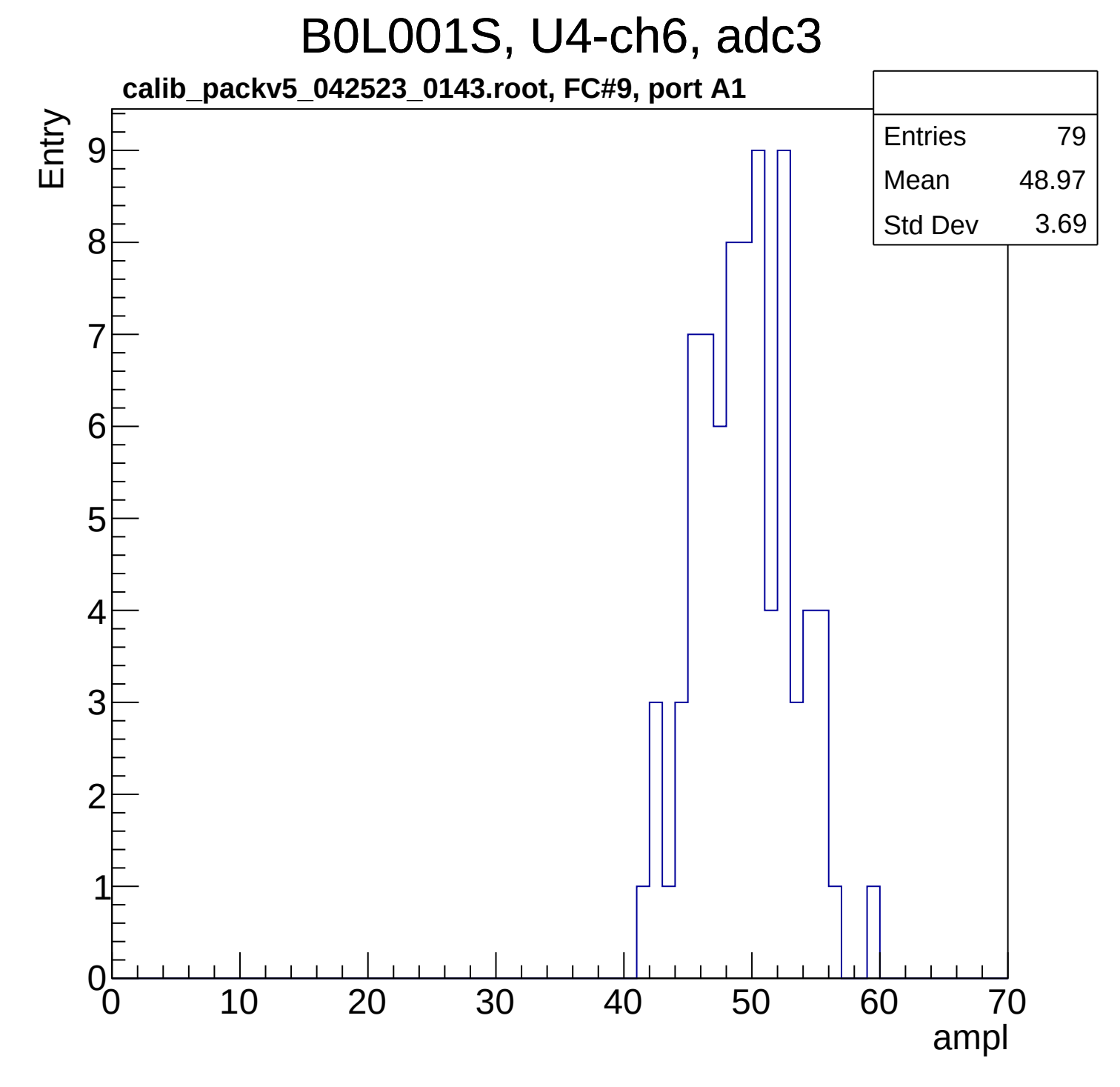

B0L001S, U4-ch6, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 42.78
Std Dev 3.177
Gaus mean : 43.0475
Gaus Width: 3.2418
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch6, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 48.97
Std Dev 3.69
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
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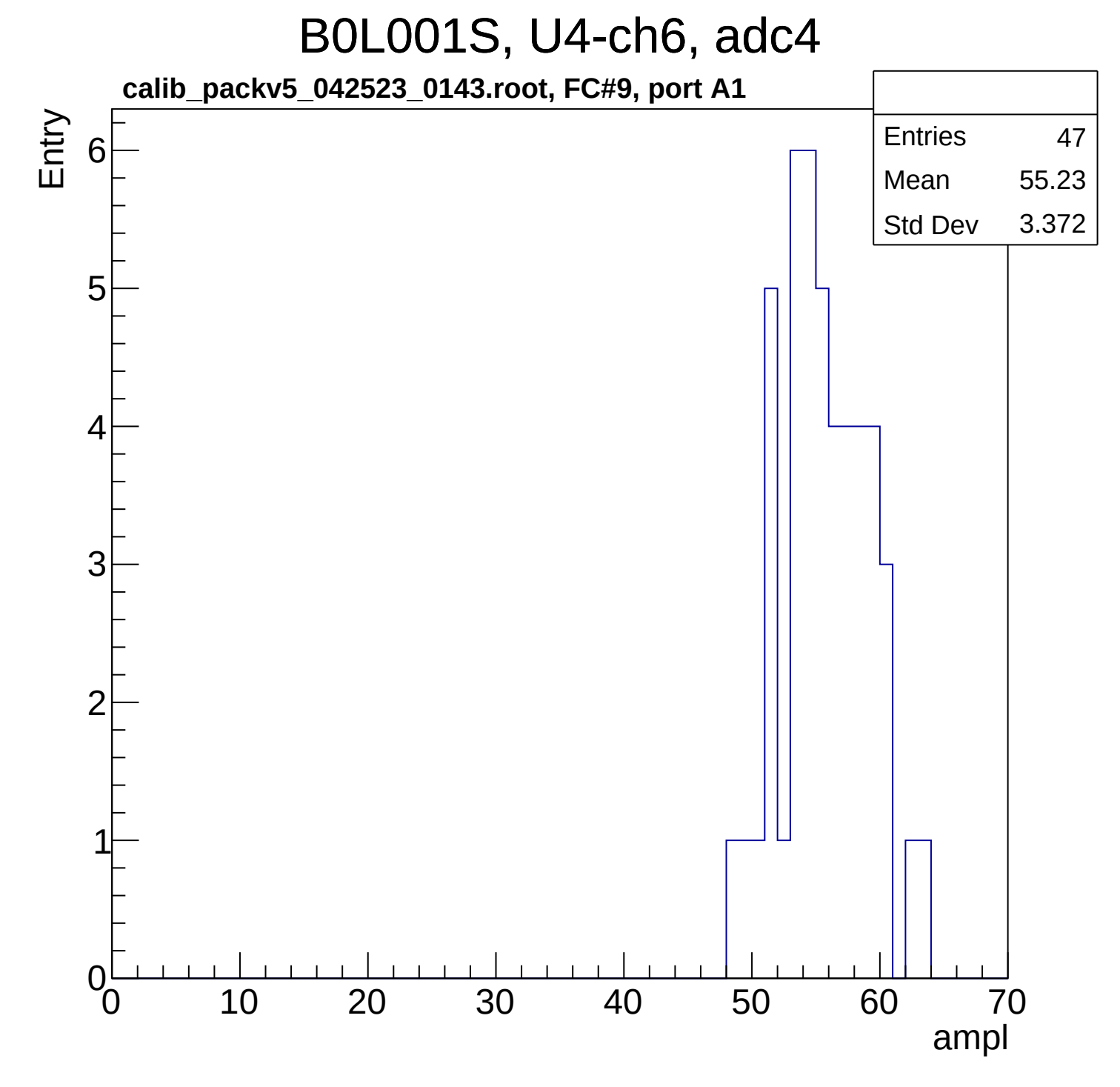
60
70


B0L001S, U4-ch6, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 55.23
Std Dev 3.372
Entry
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3
4
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6
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10
20
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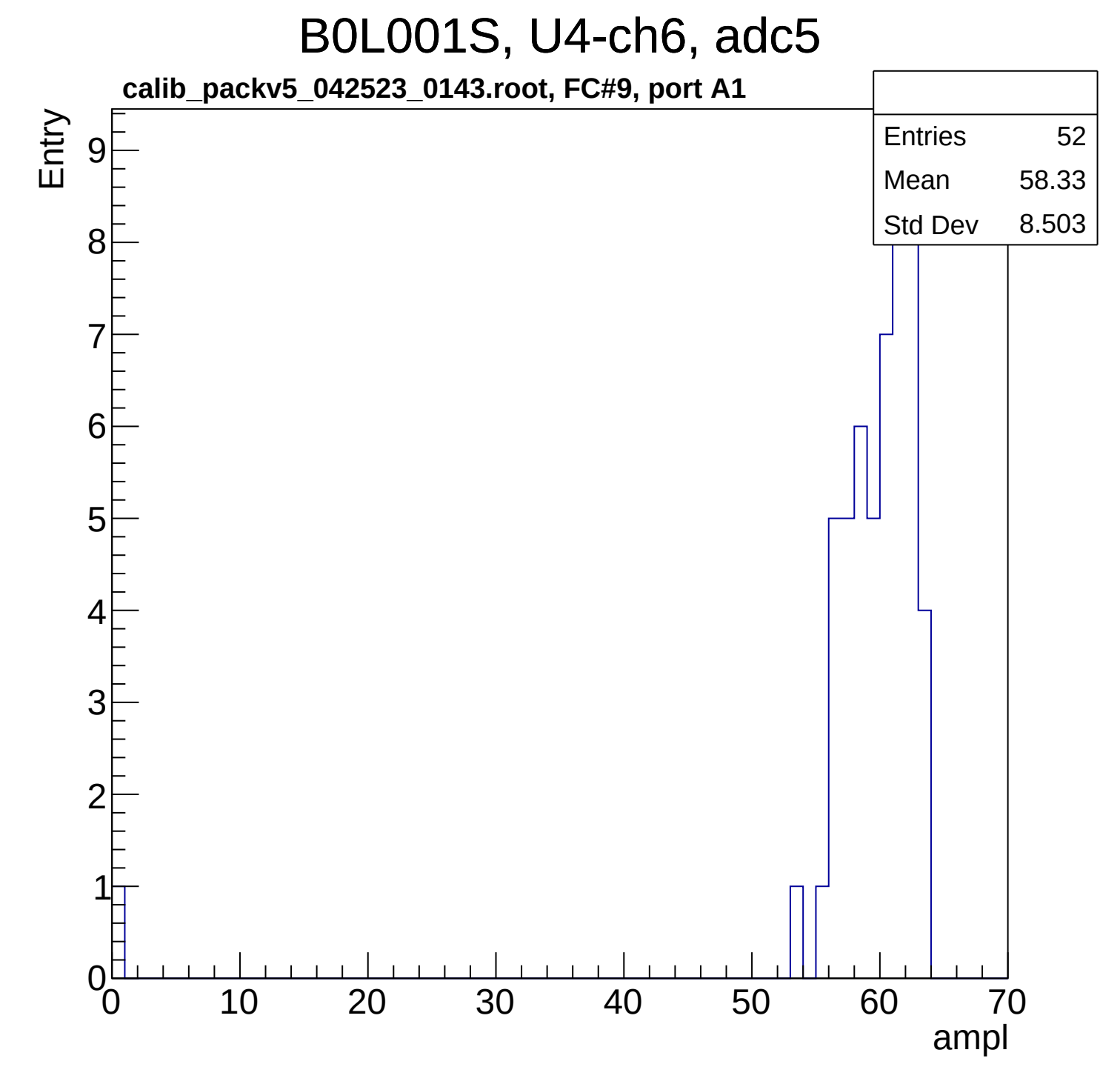
50
60
70
ampl


B0L001S, U4-ch6, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.33
Std Dev 8.503
Entry
ampl
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6
7
8
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0
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60
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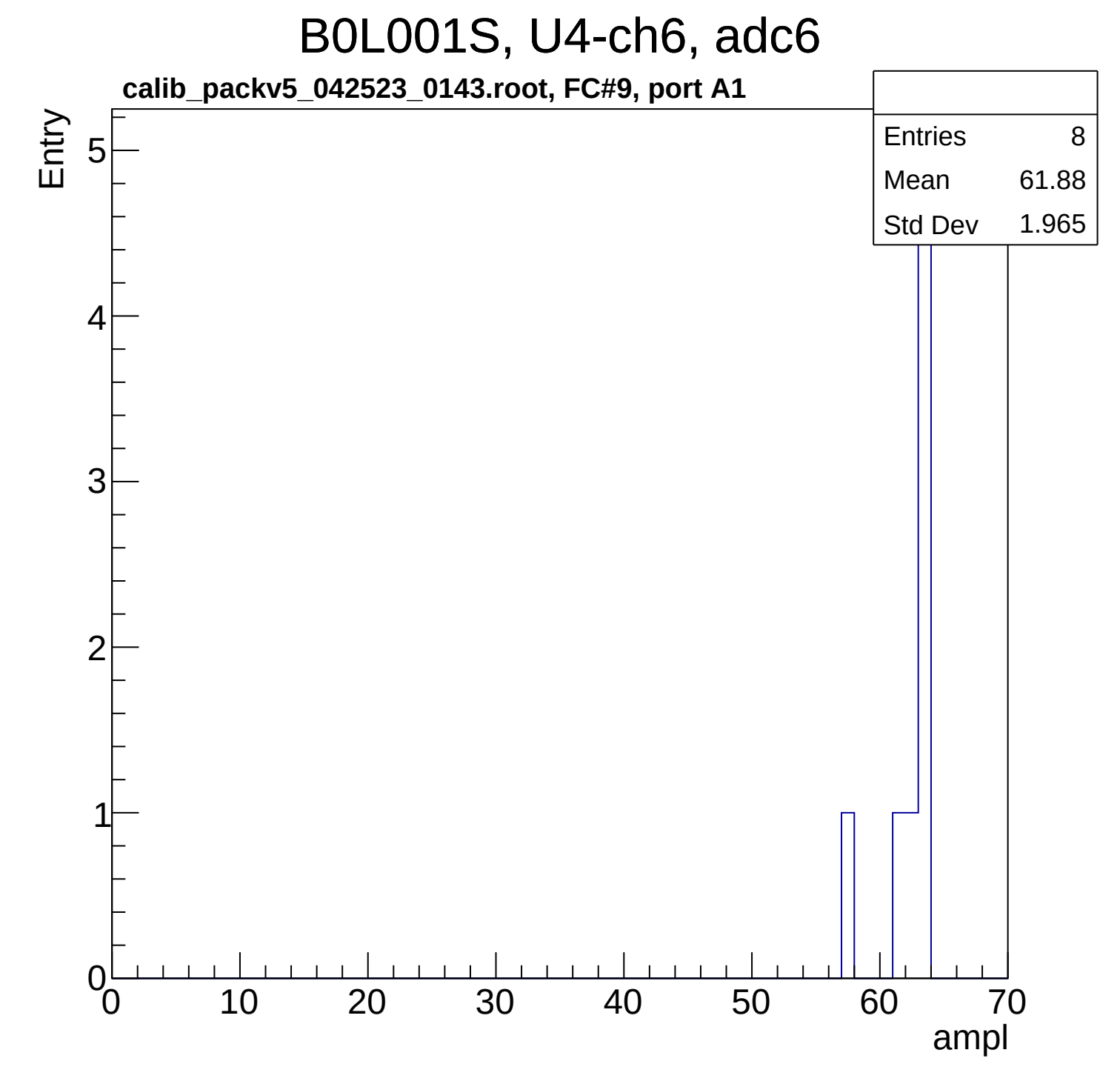
B0L001S, U4-ch6, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.88
Std Dev 1.965
Entry
ampl
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2
3
4
5
0
10
20
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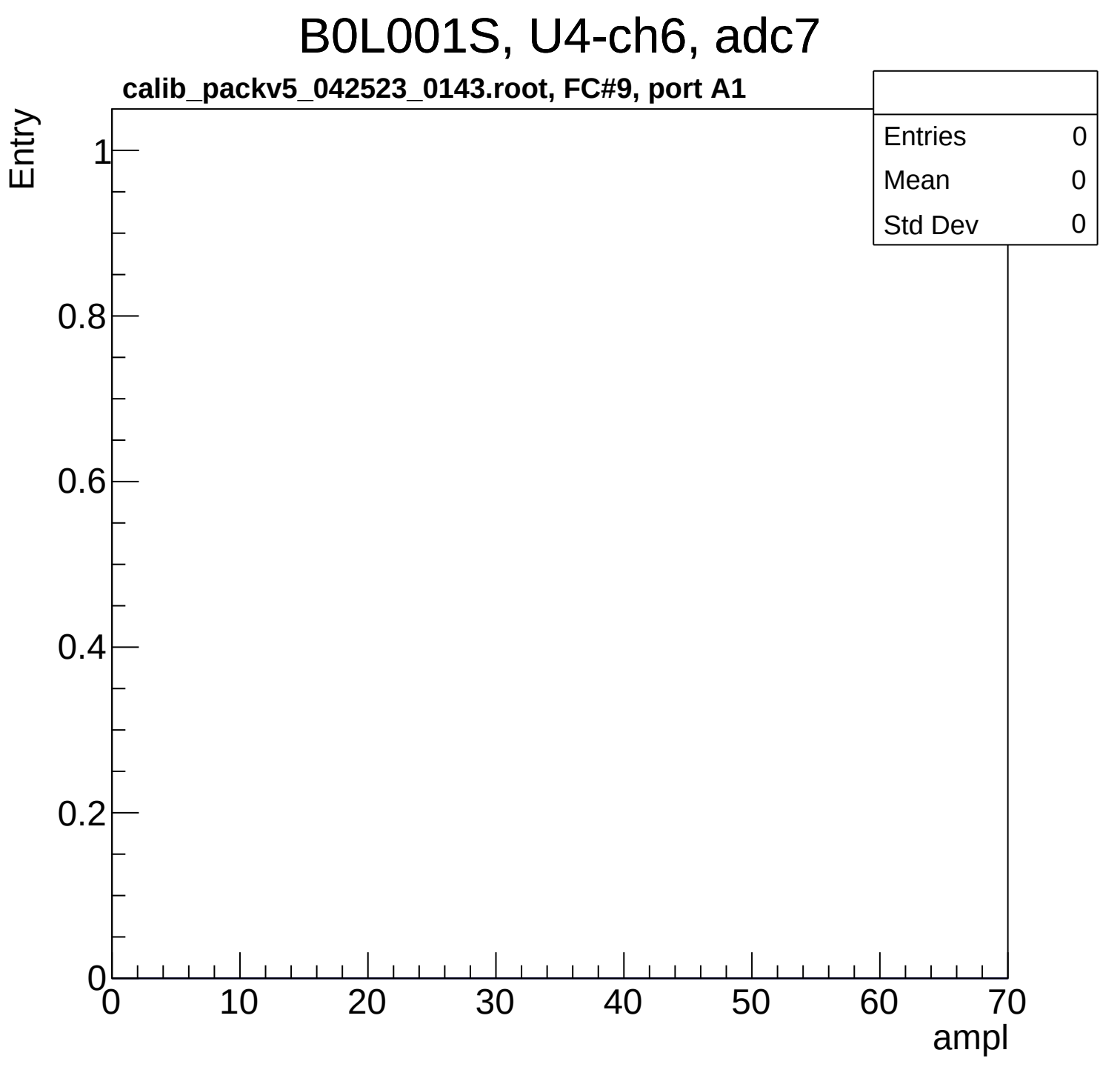
40
50
60
70


B0L001S, U4-ch6, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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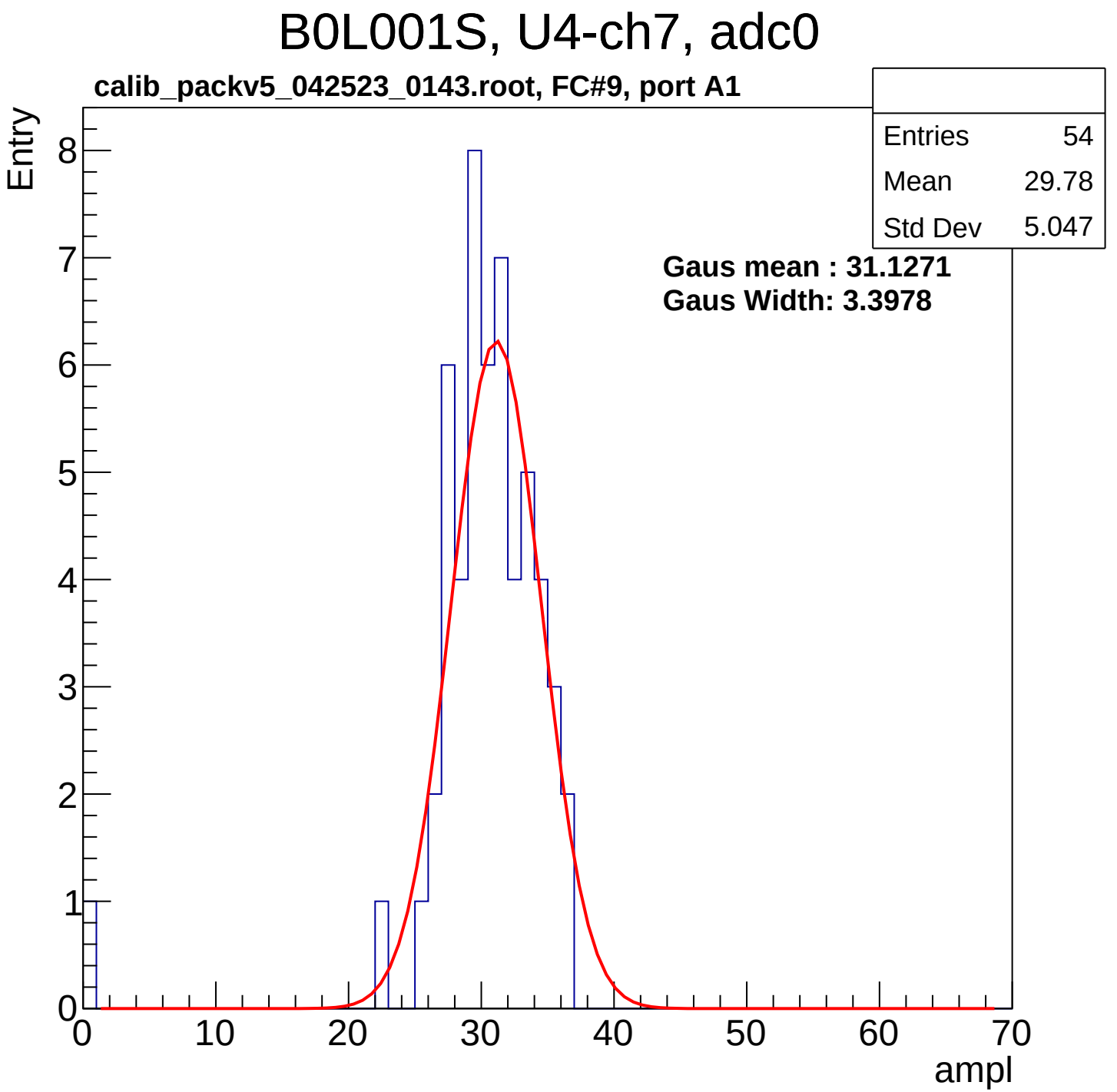
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch7, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 29.78
Std Dev 5.047
Gaus mean : 31.1271
Gaus Width: 3.3978
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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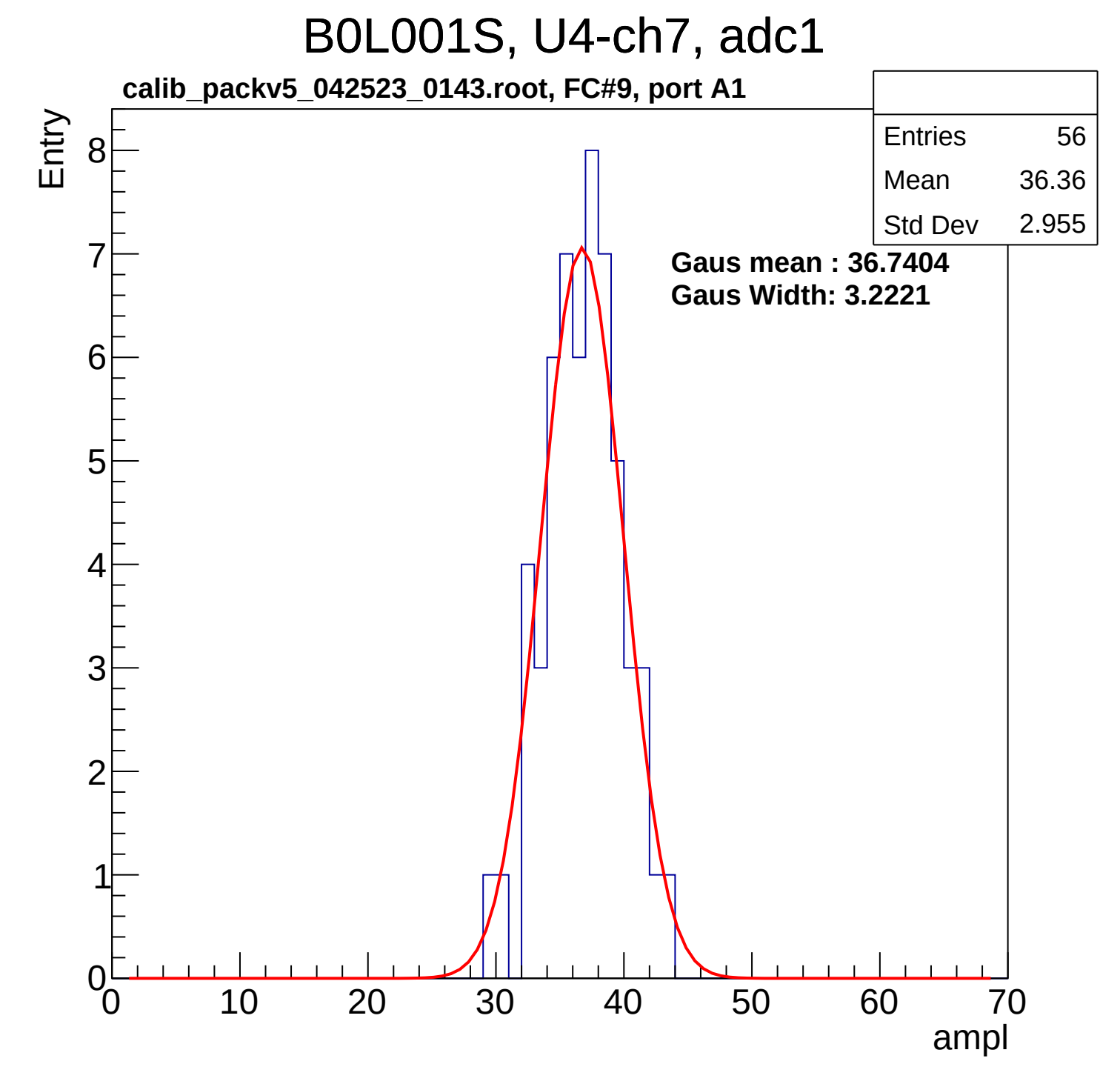
B0L001S, U4-ch7, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 36.36
Std Dev 2.955
Gaus mean : 36.7404
Gaus Width: 3.2221
Entry
ampl
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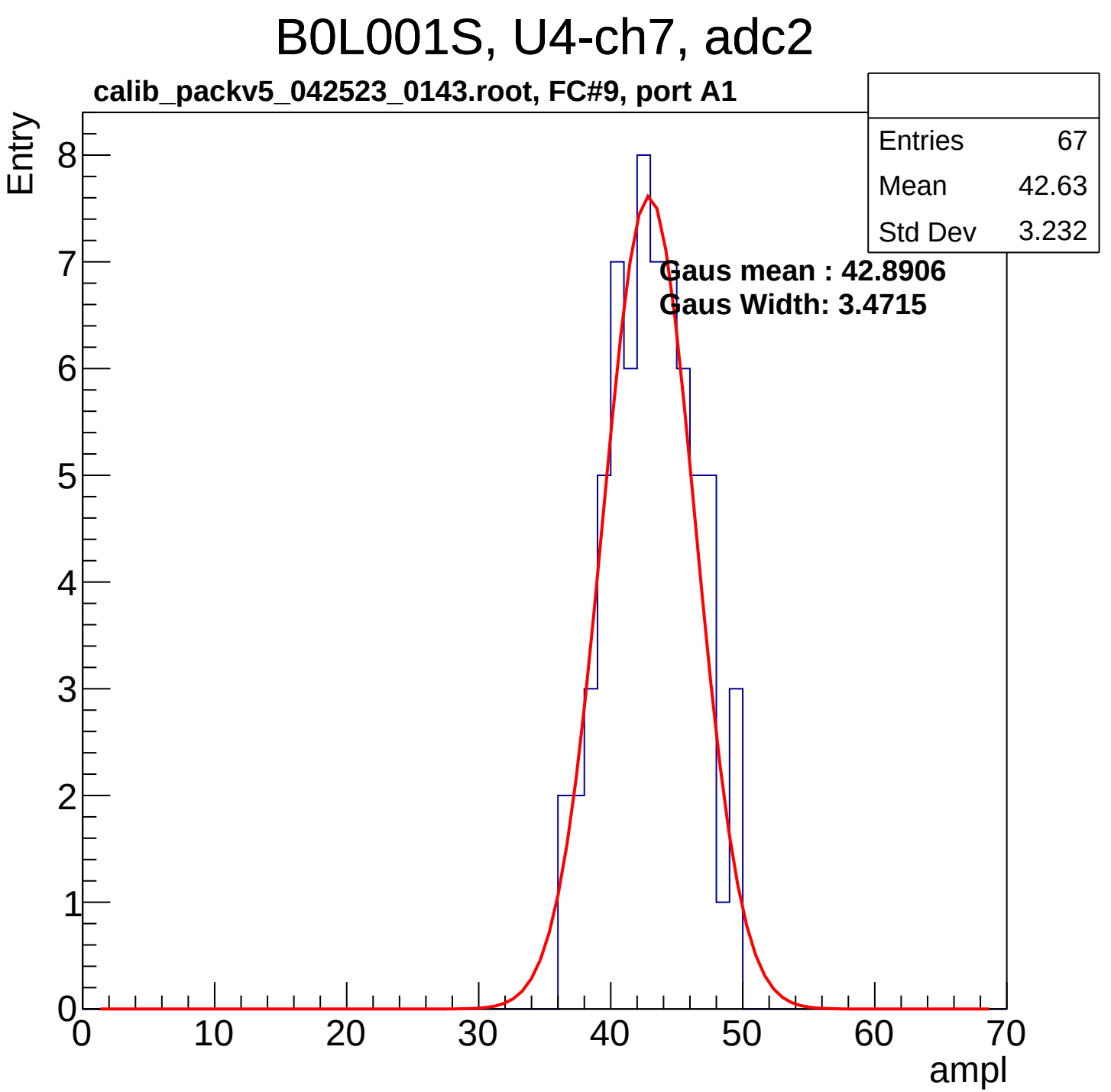
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch7, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 42.63
Std Dev 3.232
Gaus mean : 42.8906
Gaus Width: 3.4715
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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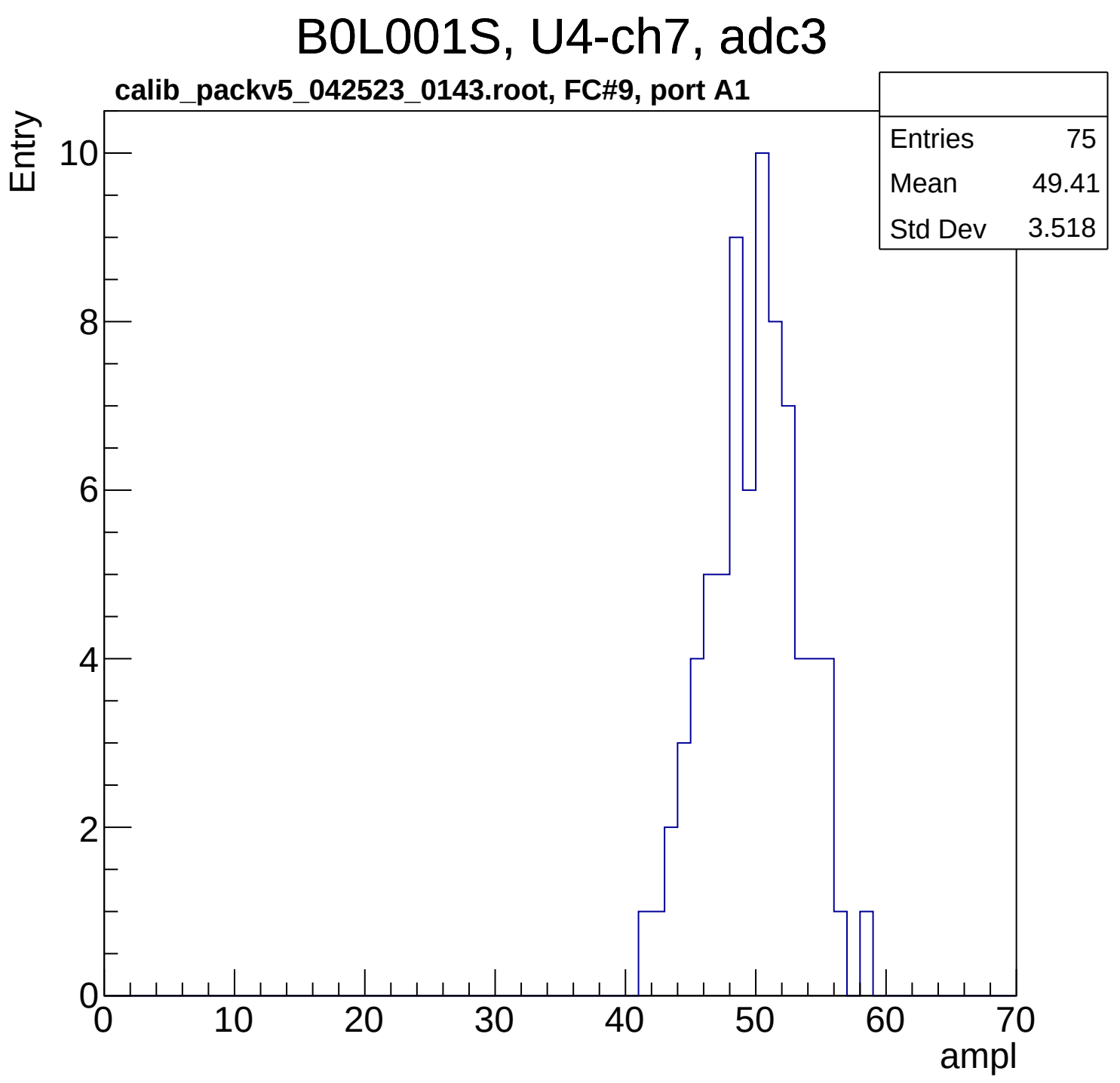
B0L001S, U4-ch7, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 49.41
Std Dev 3.518
Entry
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4
6
8
10
0
10
20
30
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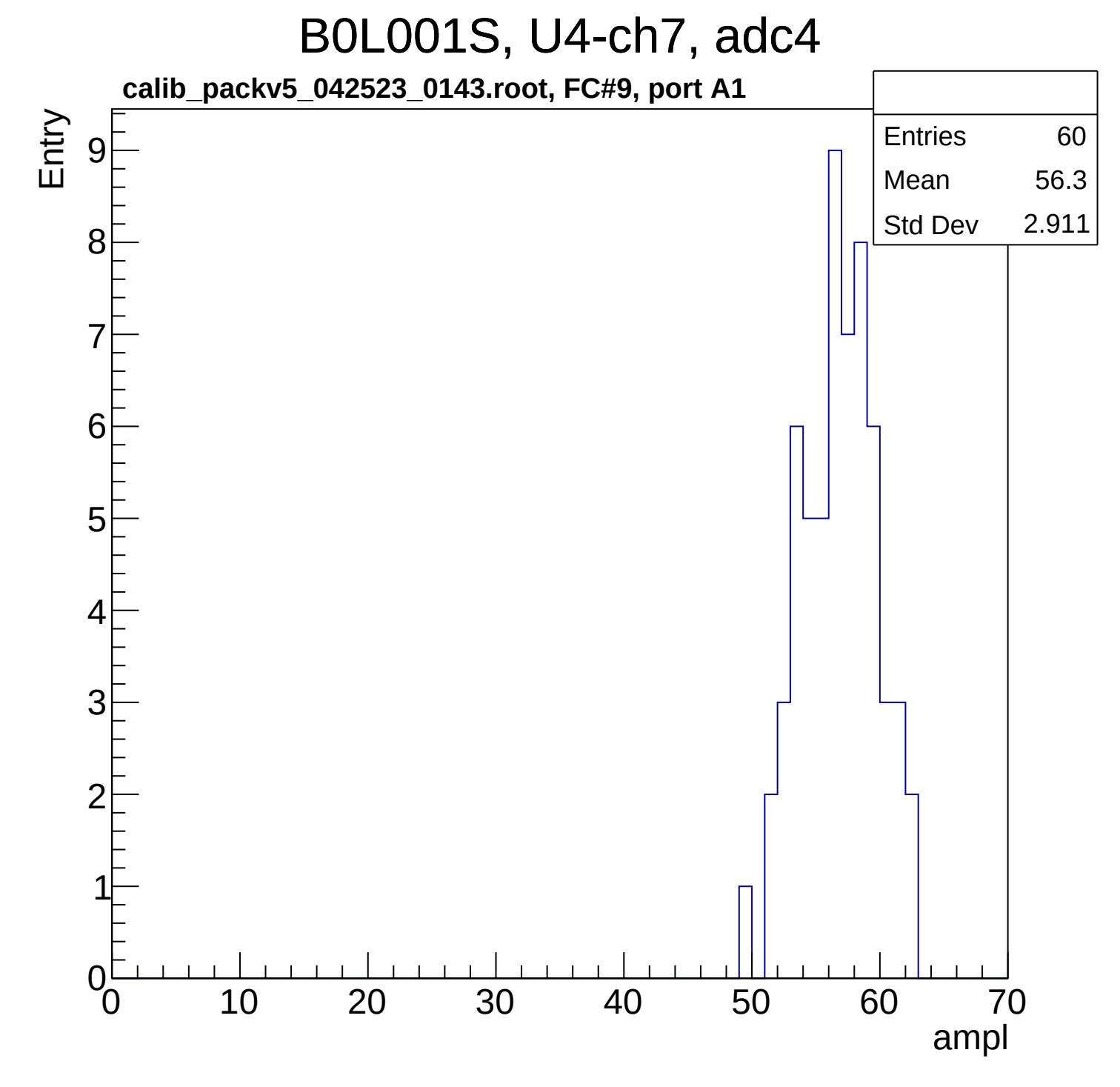
50
60
70
ampl


B0L001S, U4-ch7, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.3
Std Dev 2.911
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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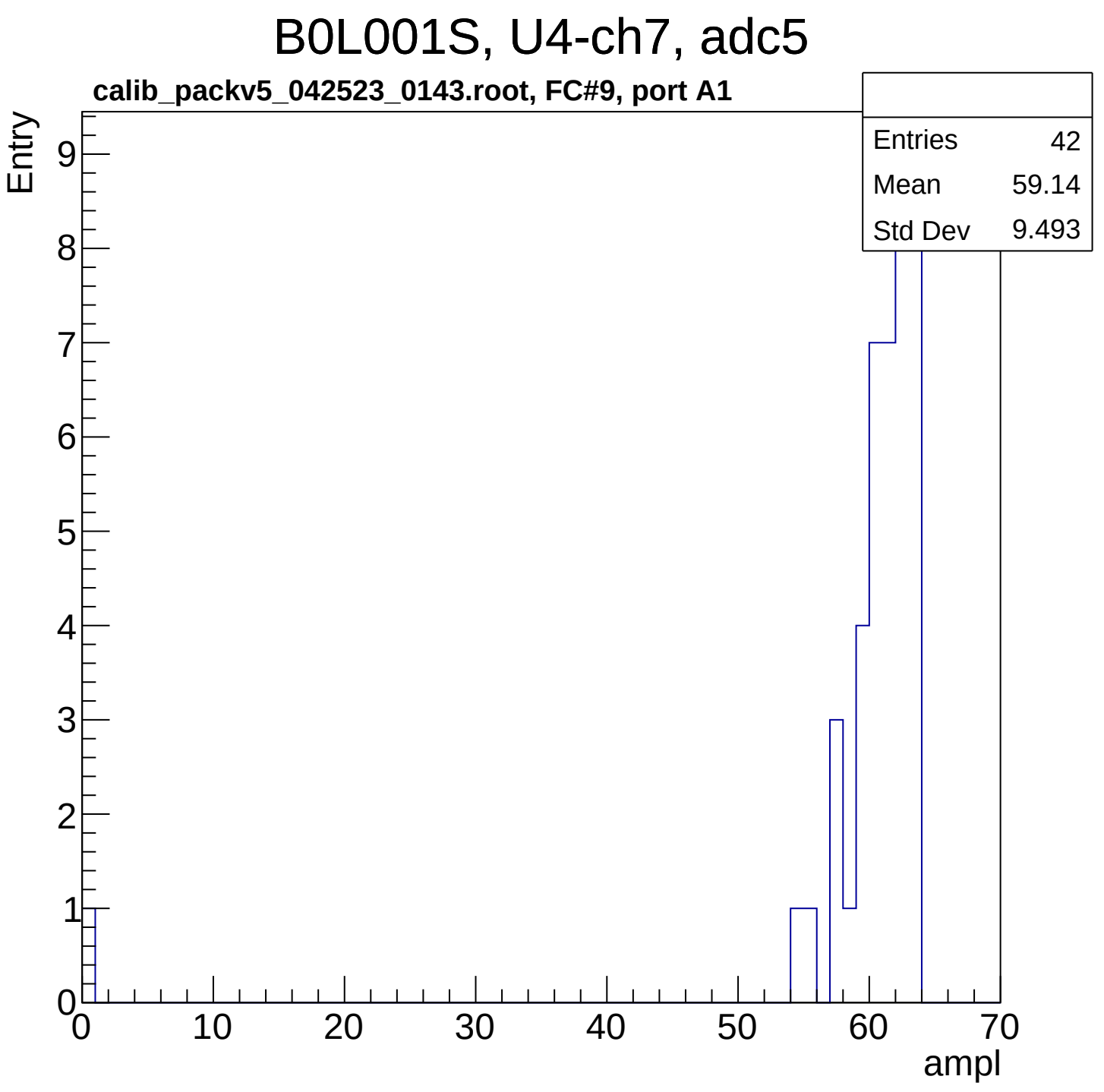
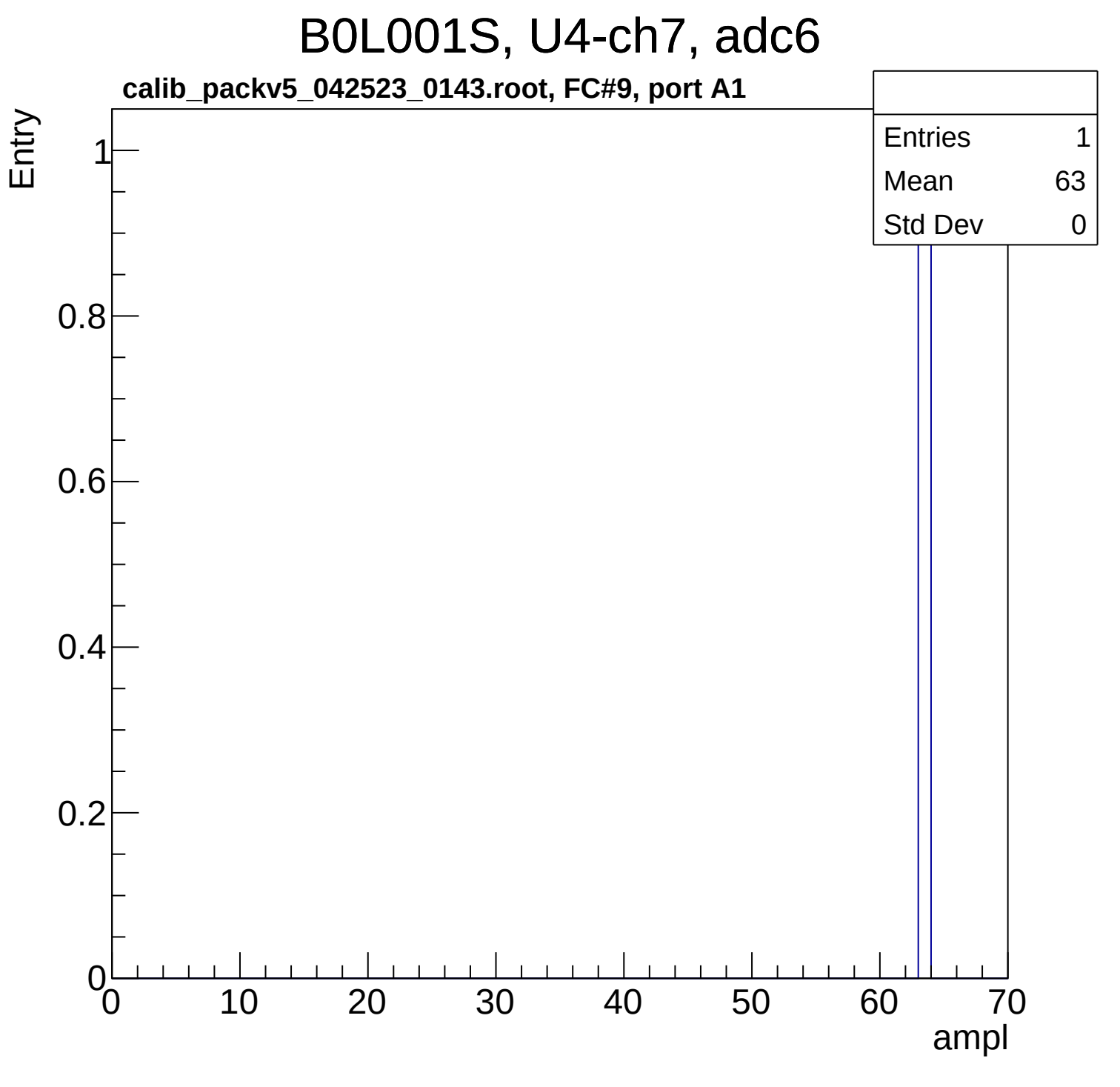

B0L001S, U4-ch7, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.14
Std Dev 9.493
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch7, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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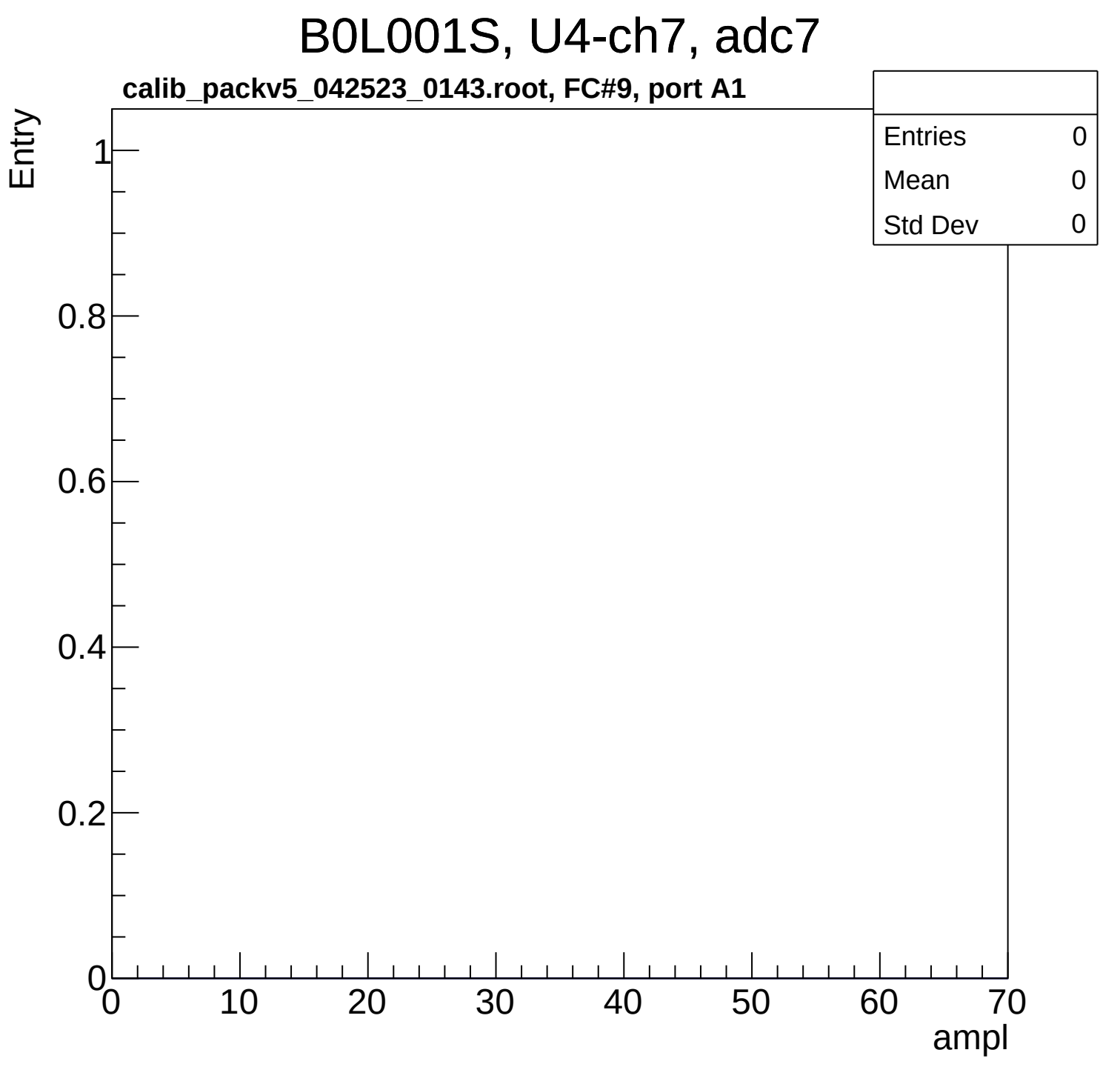
70
ampl


B0L001S, U4-ch7, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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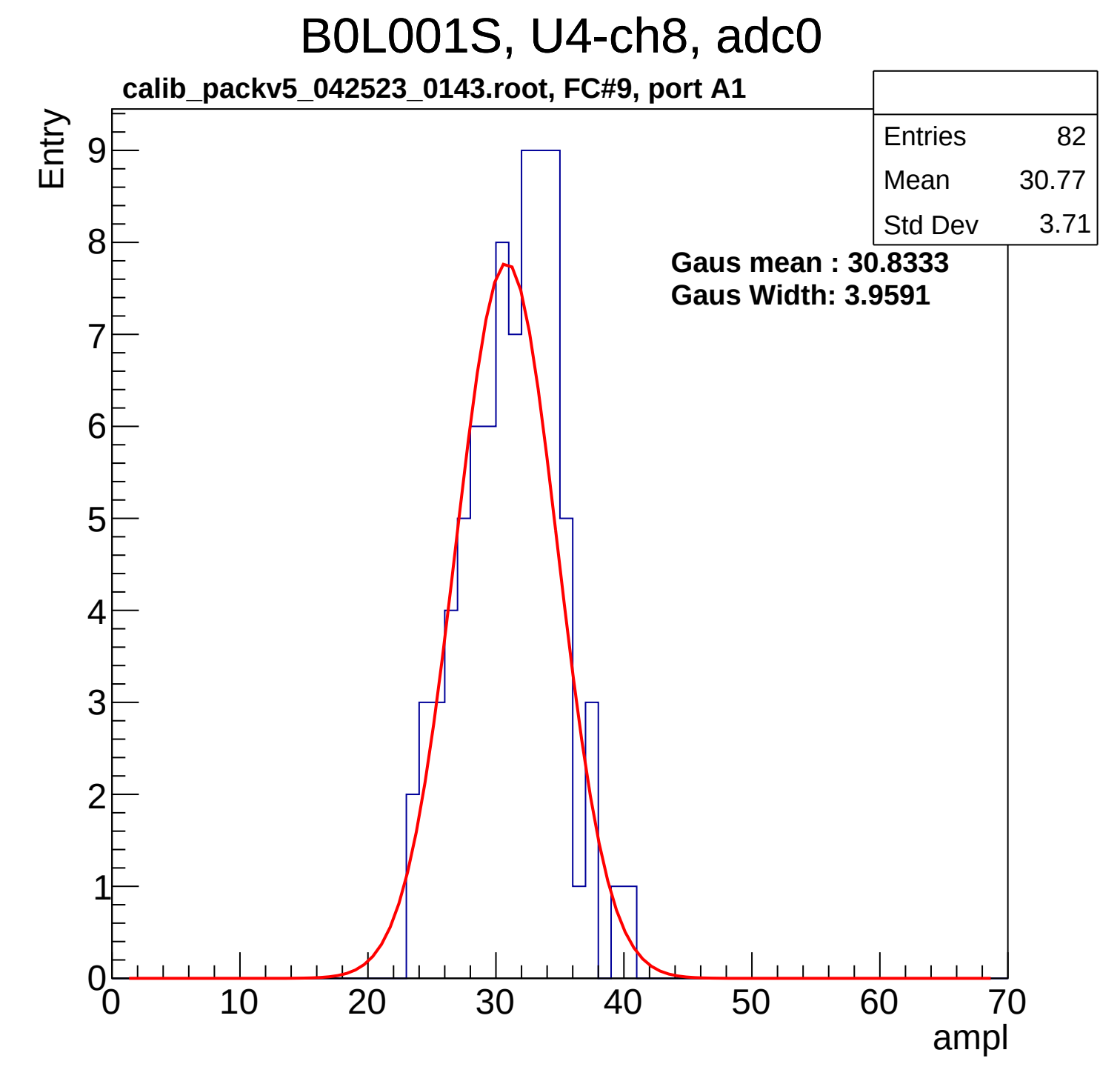
70
ampl


B0L001S, U4-ch8, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 30.77
Std Dev 3.71
Gaus mean : 30.8333
Gaus Width: 3.9591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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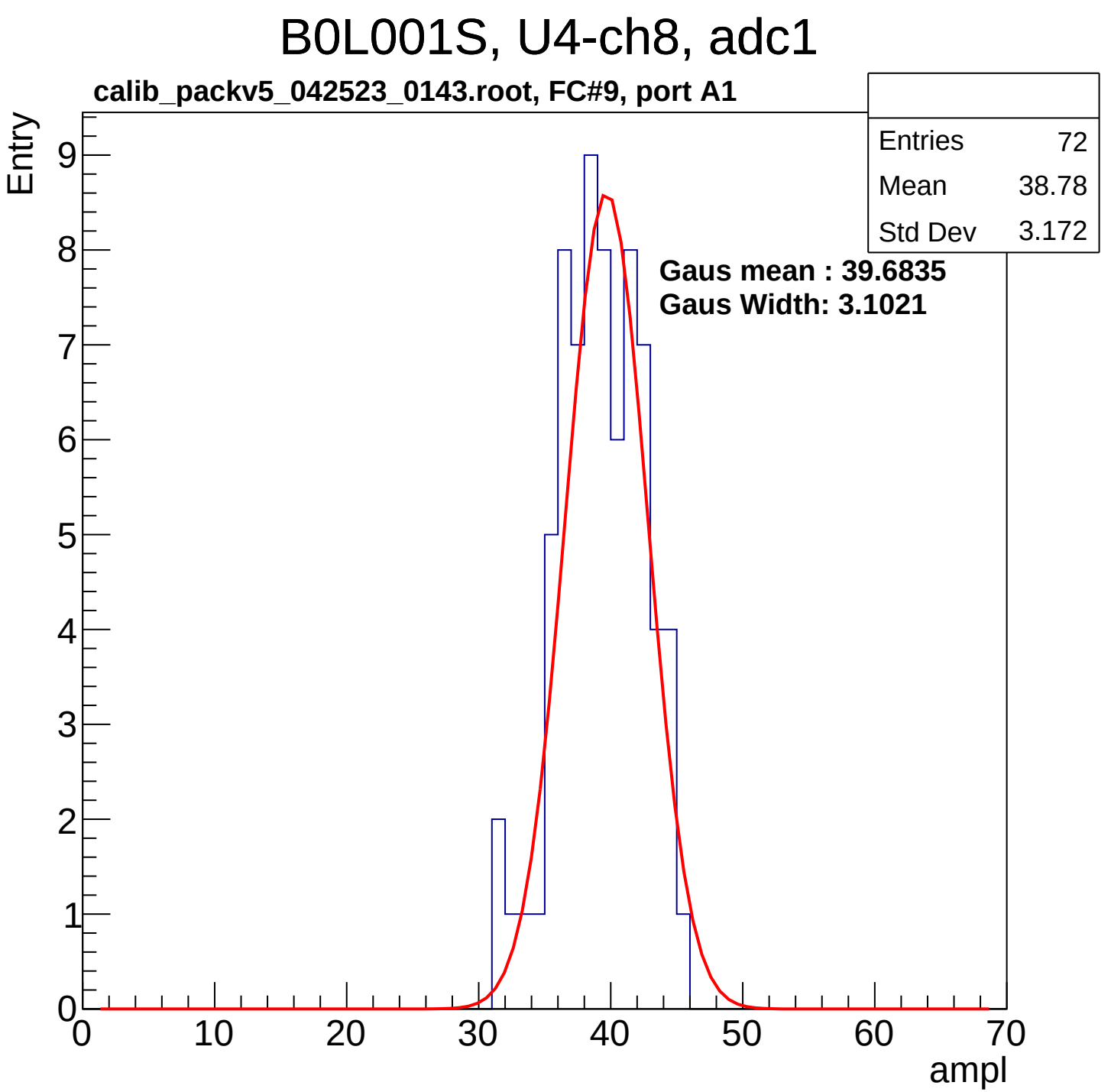
B0L001S, U4-ch8, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.78
Std Dev 3.172
Gaus mean : 39.6835
Gaus Width: 3.1021
Entry
ampl
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2
3
4
5
6
7
8
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0
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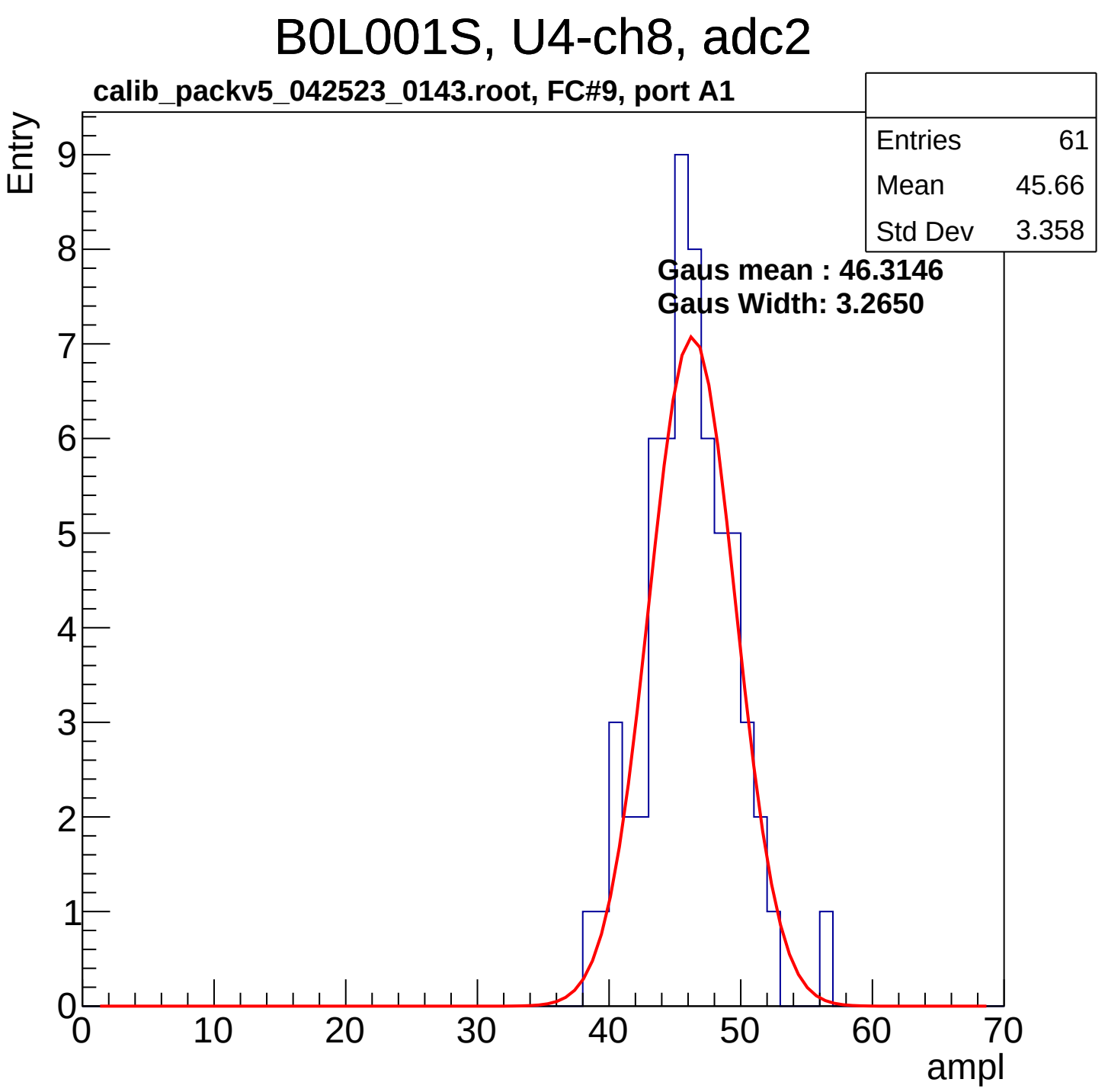
B0L001S, U4-ch8, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.66
Std Dev 3.358
Gaus mean : 46.3146
Gaus Width: 3.2650
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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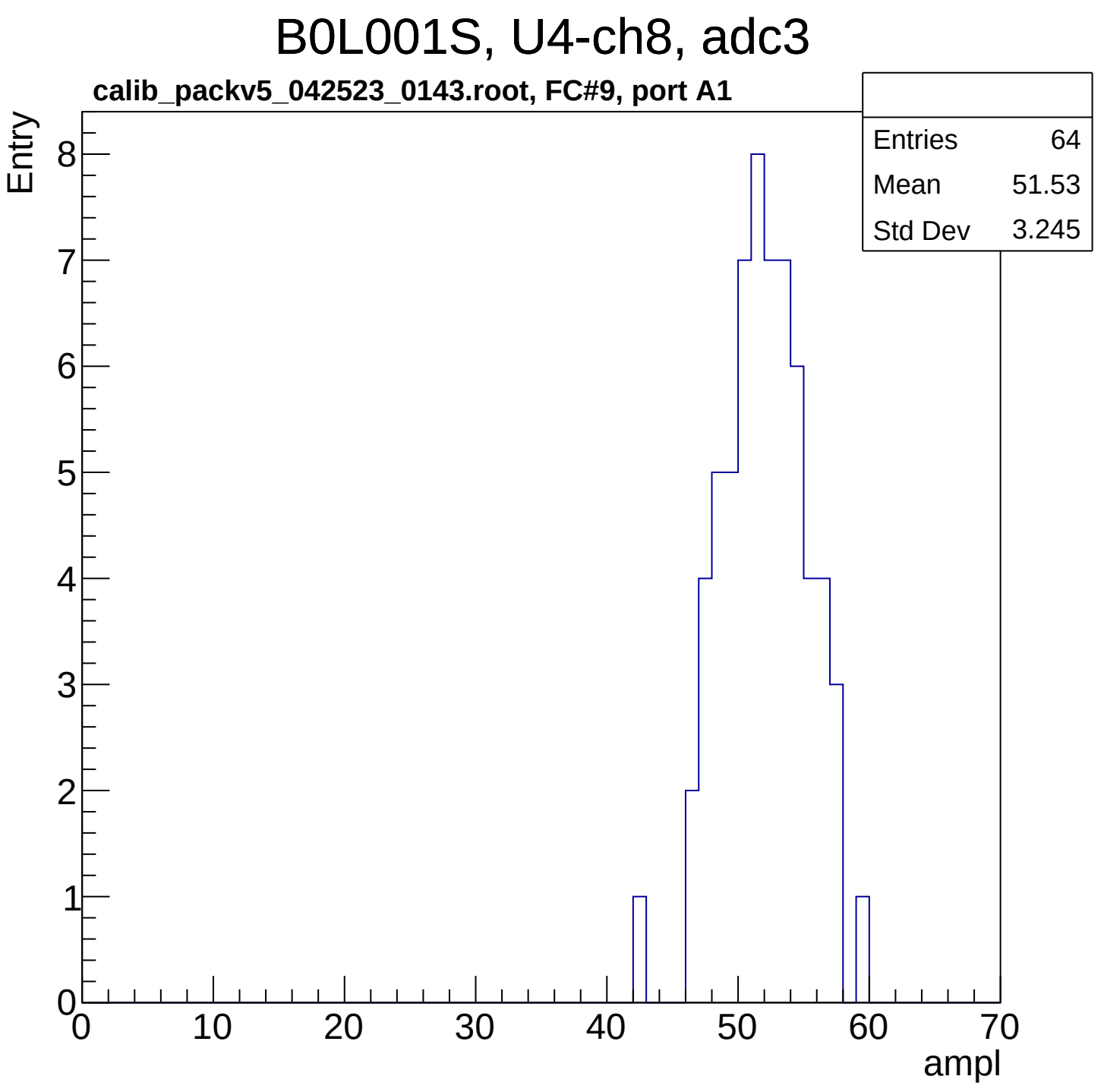
B0L001S, U4-ch8, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.53
Std Dev 3.245
Entry
ampl
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1
2
3
4
5
6
7
8
0
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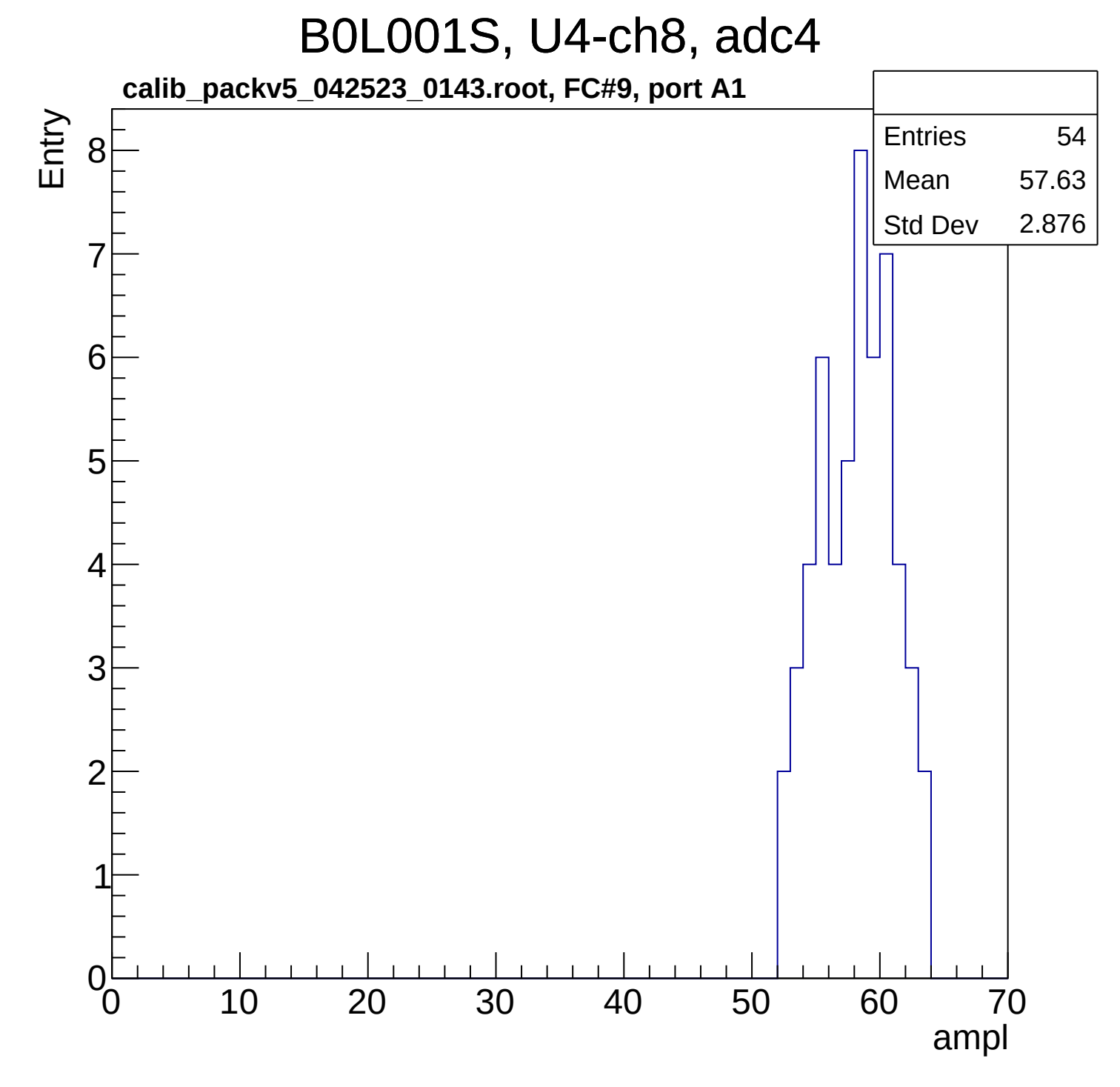
B0L001S, U4-ch8, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.63
Std Dev 2.876
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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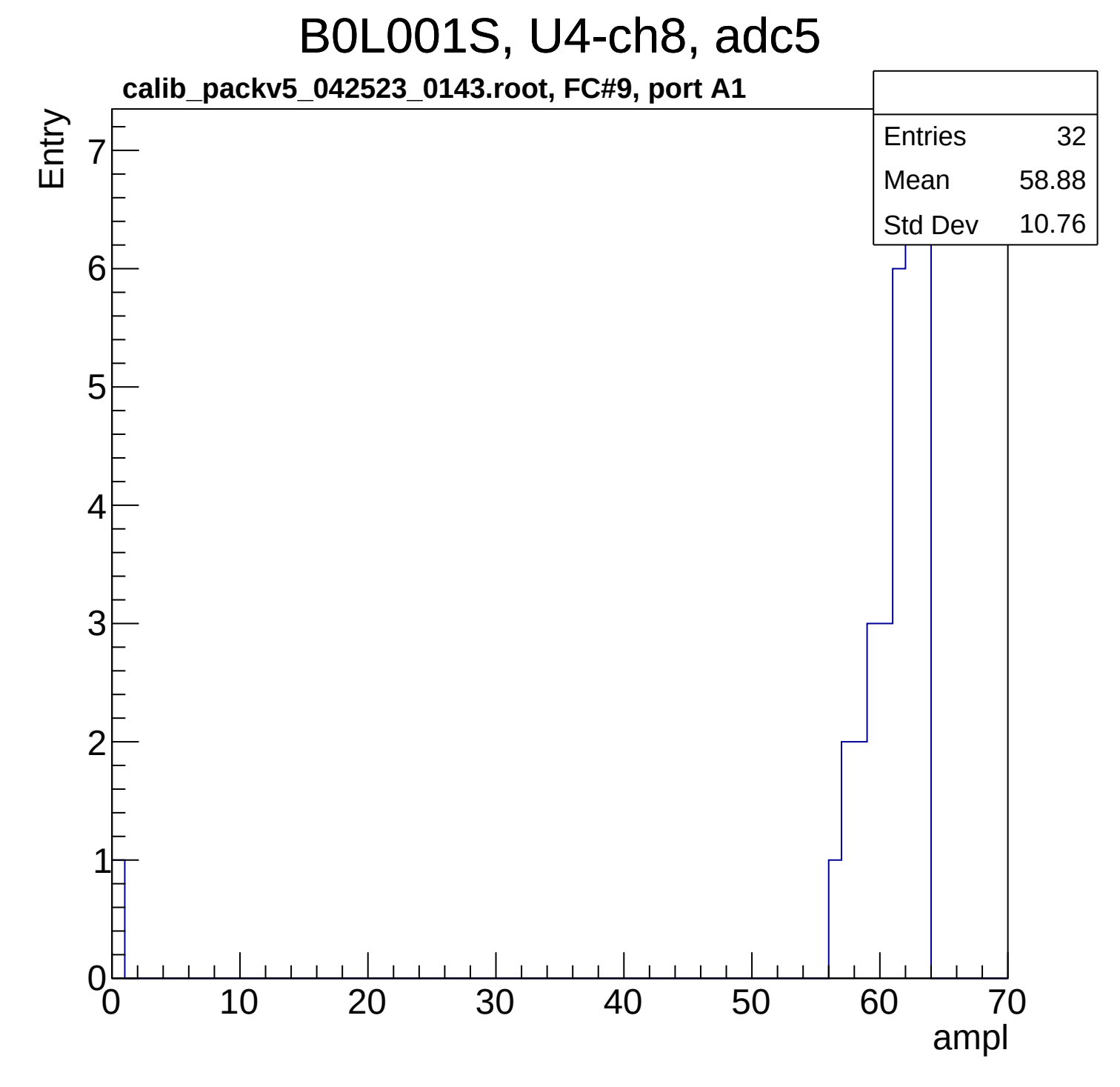
B0L001S, U4-ch8, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.88
Std Dev 10.76
Entry
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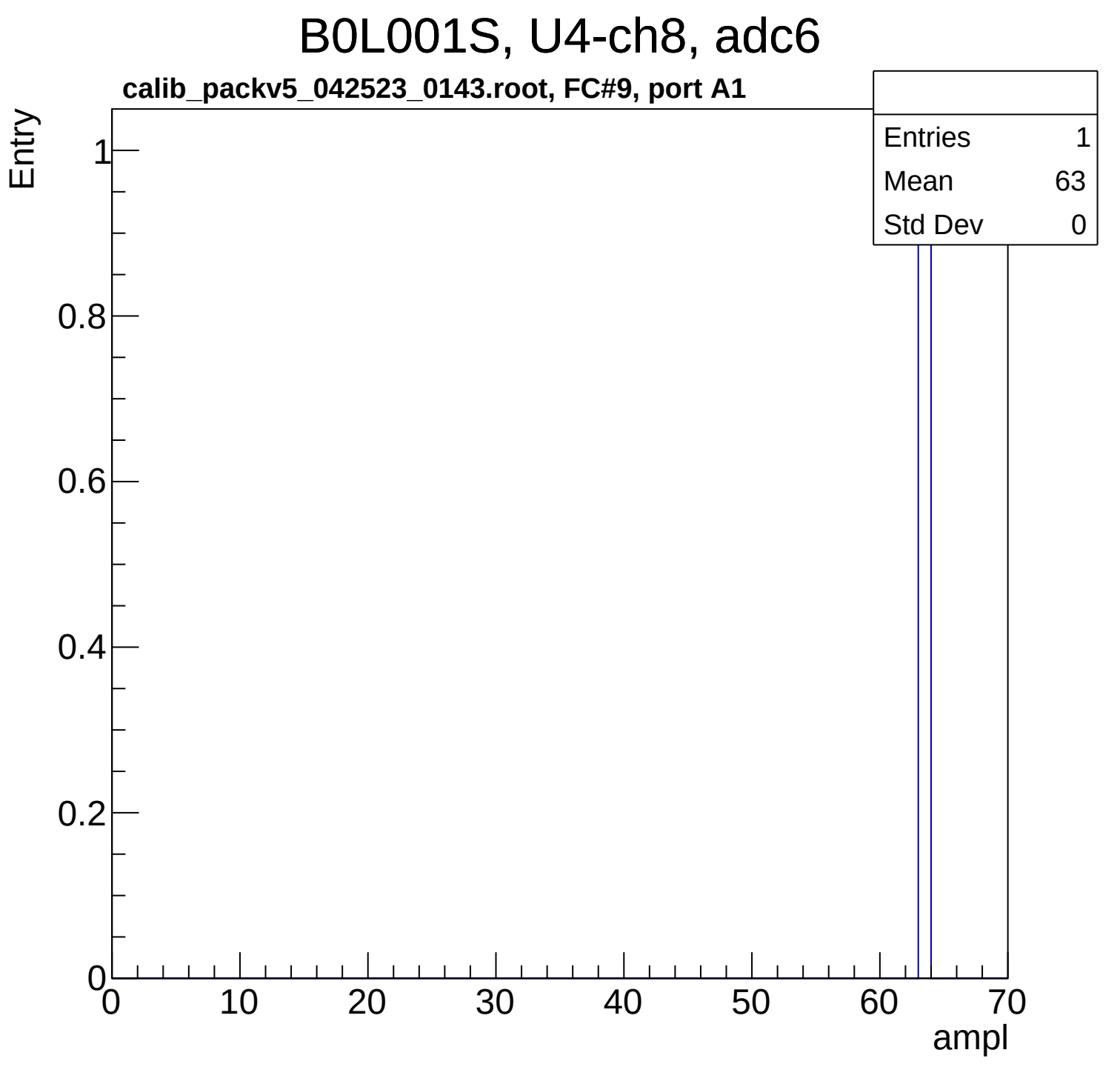
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch8, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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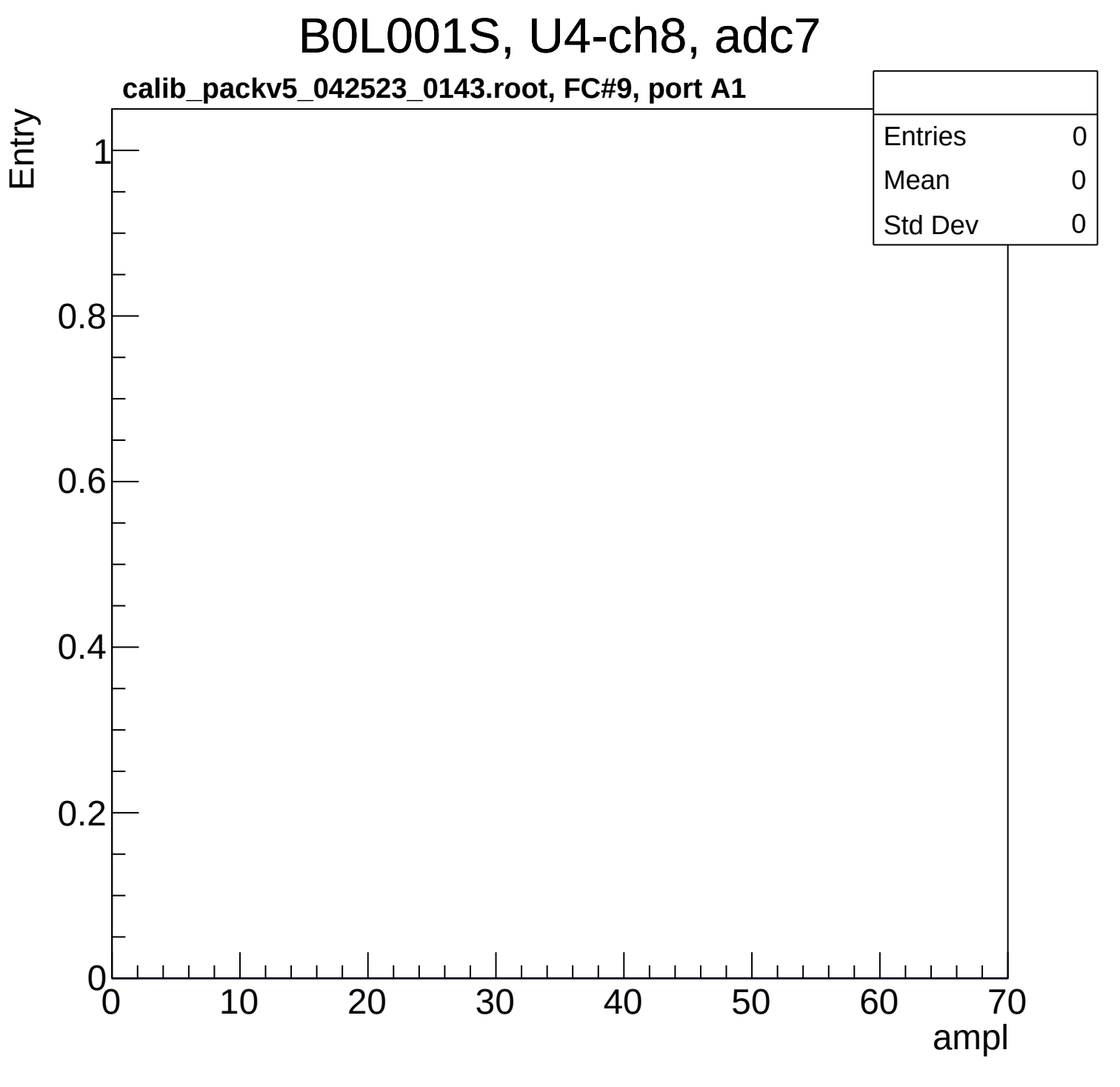
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch8, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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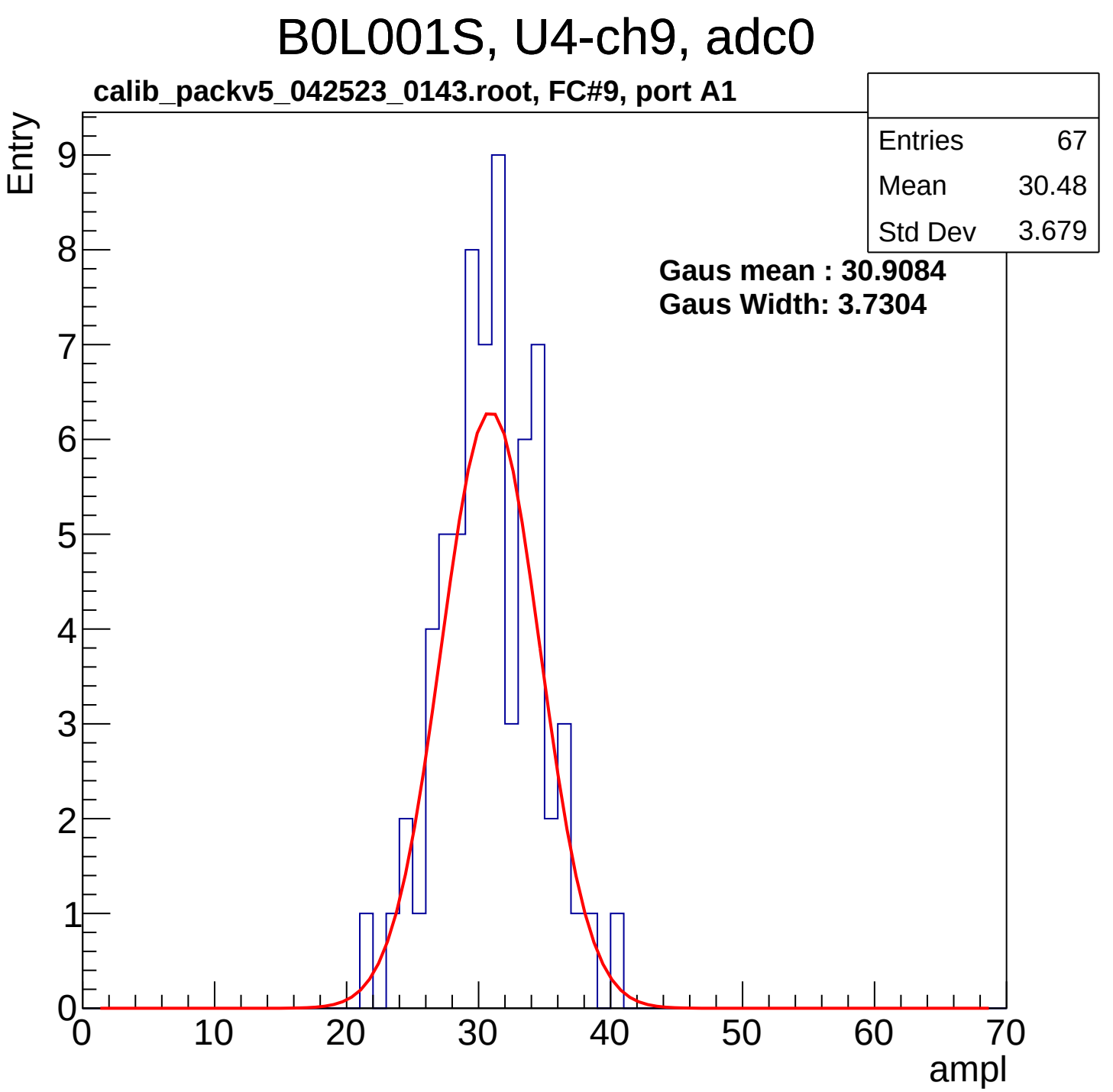
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch9, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.48
Std Dev 3.679
Gaus mean : 30.9084
Gaus Width: 3.7304
Entry
ampl
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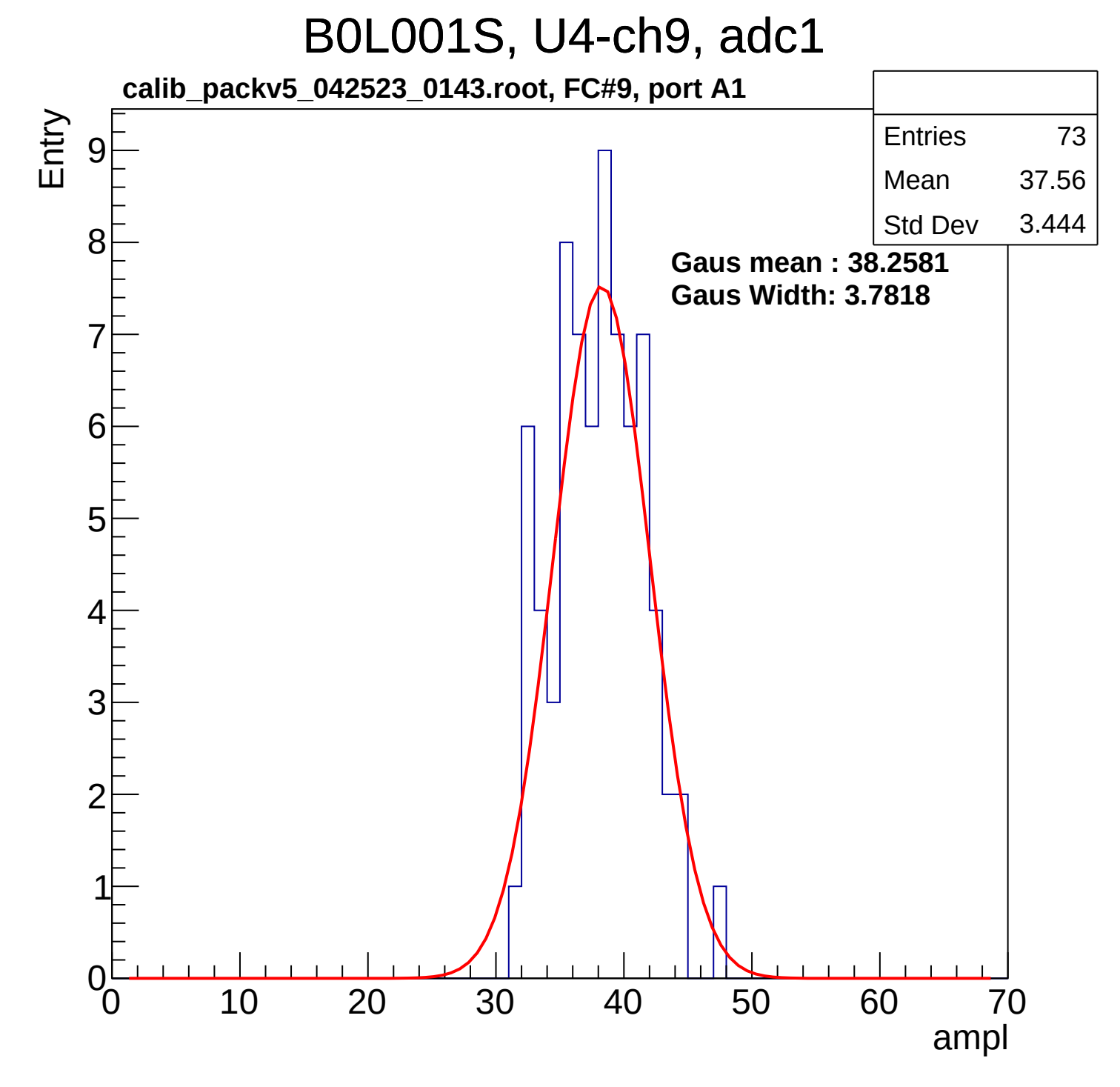
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch9, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.56
Std Dev 3.444
Gaus mean : 38.2581
Gaus Width: 3.7818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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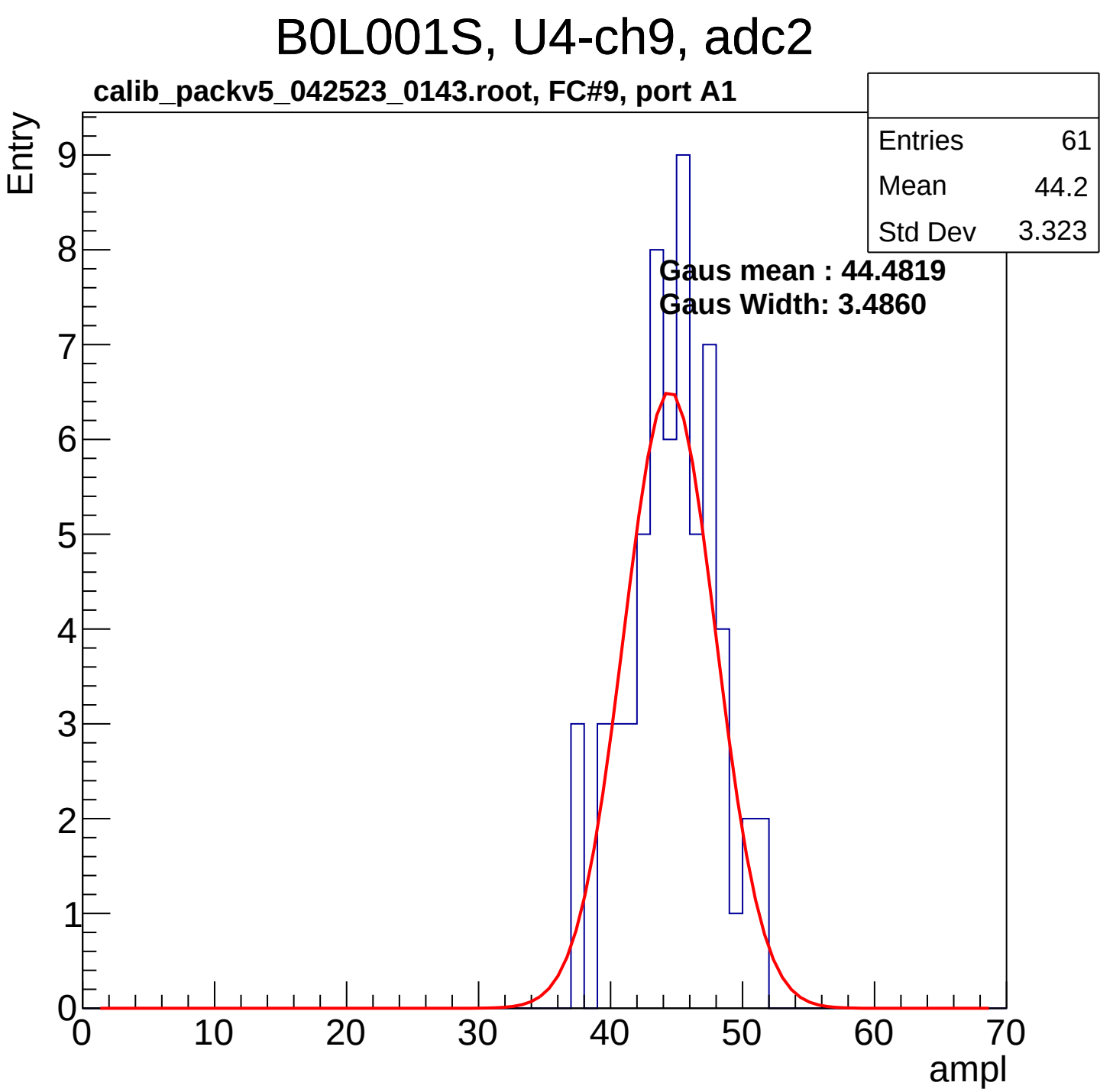
B0L001S, U4-ch9, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.2
Std Dev 3.323
Gaus mean : 44.4819
Gaus Width: 3.4860
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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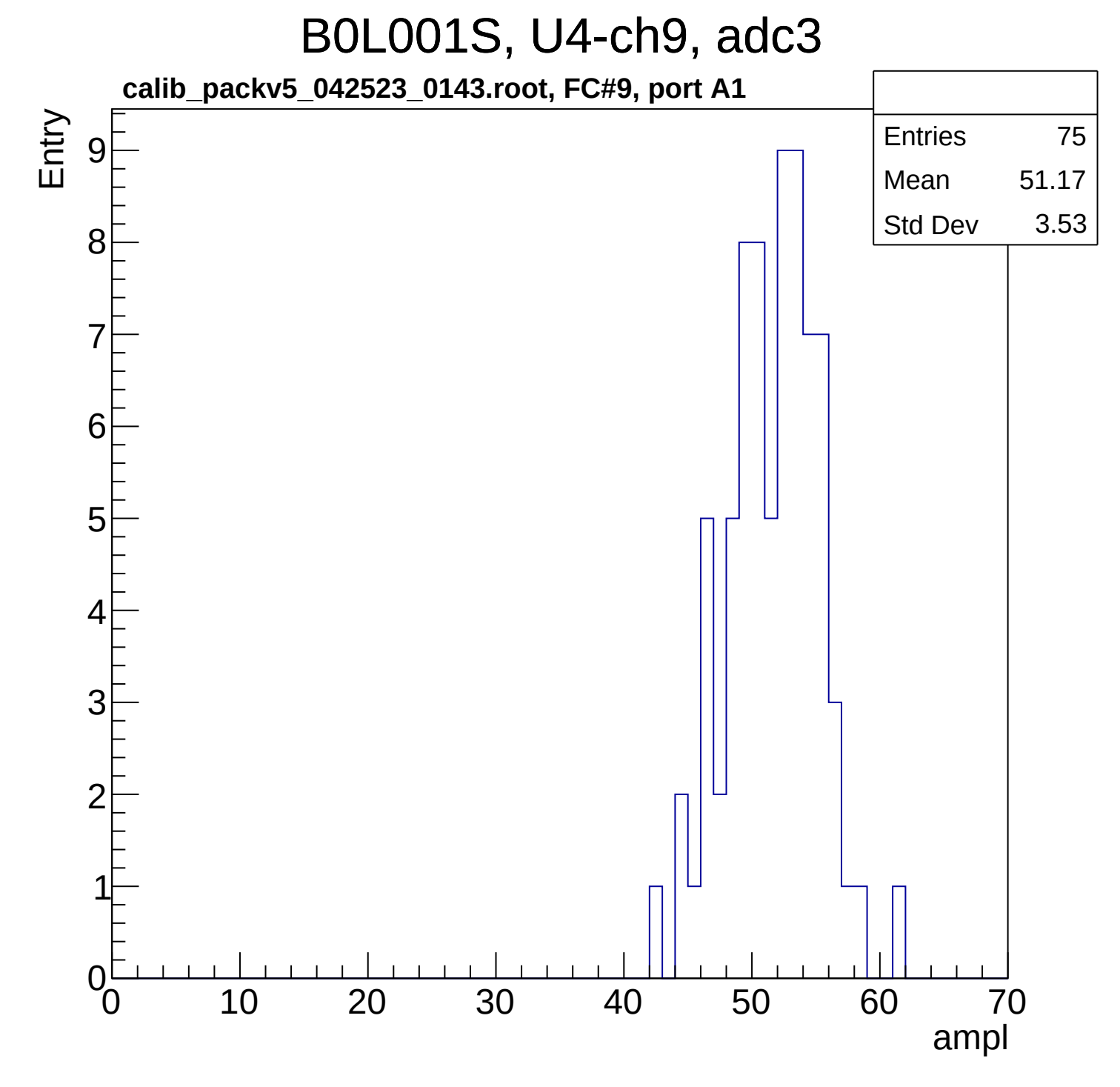
B0L001S, U4-ch9, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.17
Std Dev 3.53
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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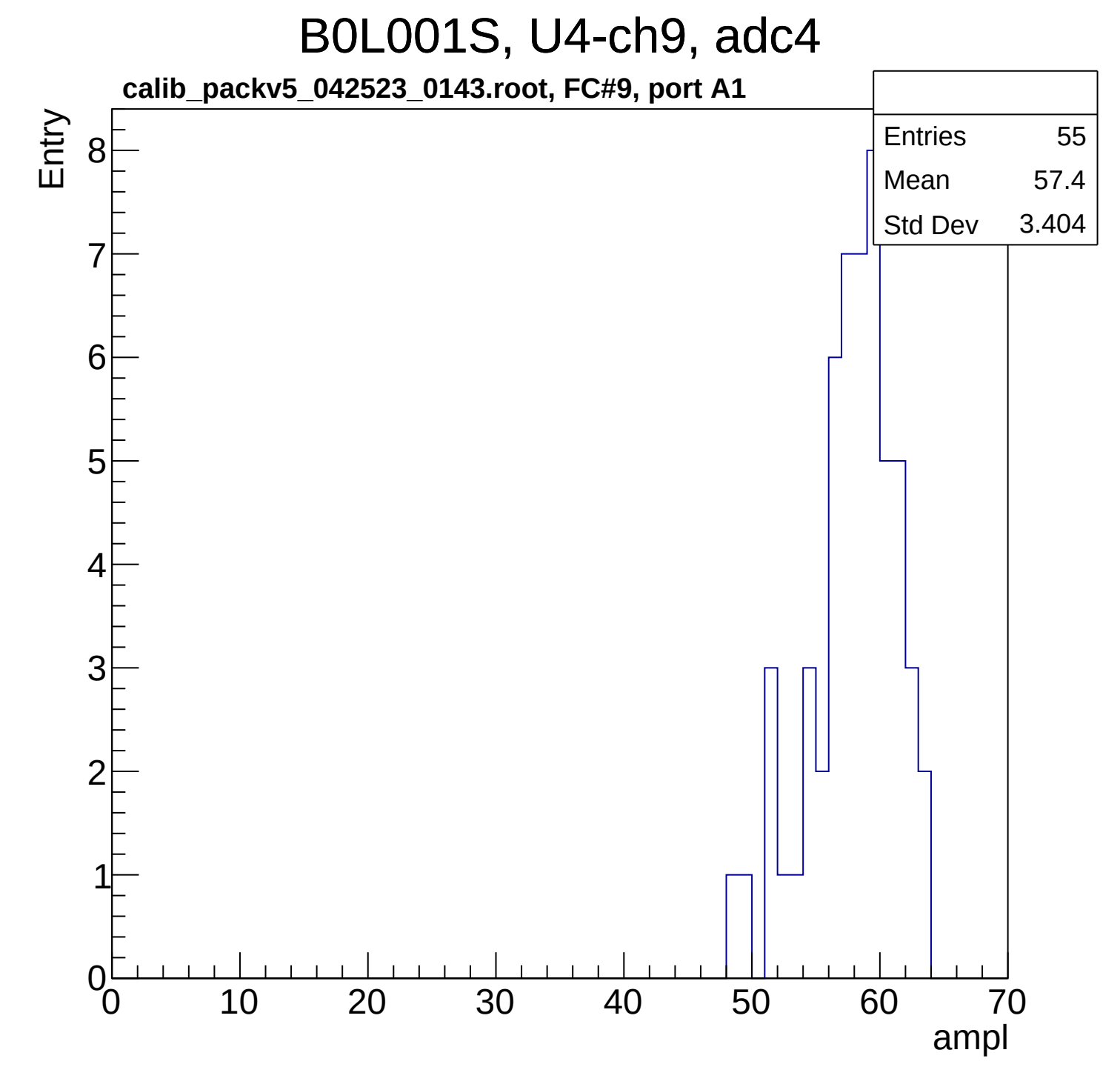
B0L001S, U4-ch9, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.4
Std Dev 3.404
Entry
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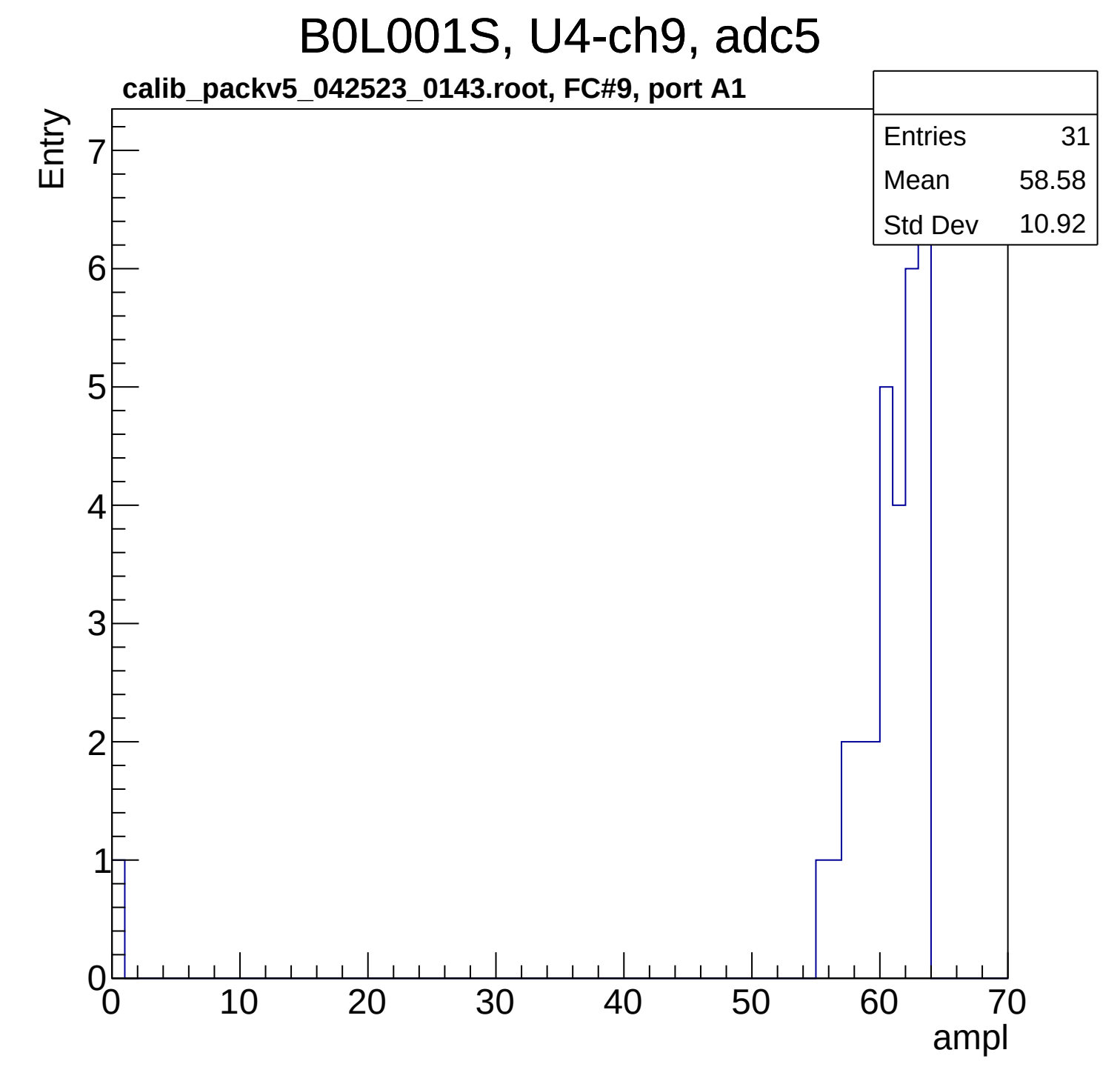
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch9, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.58
Std Dev 10.92
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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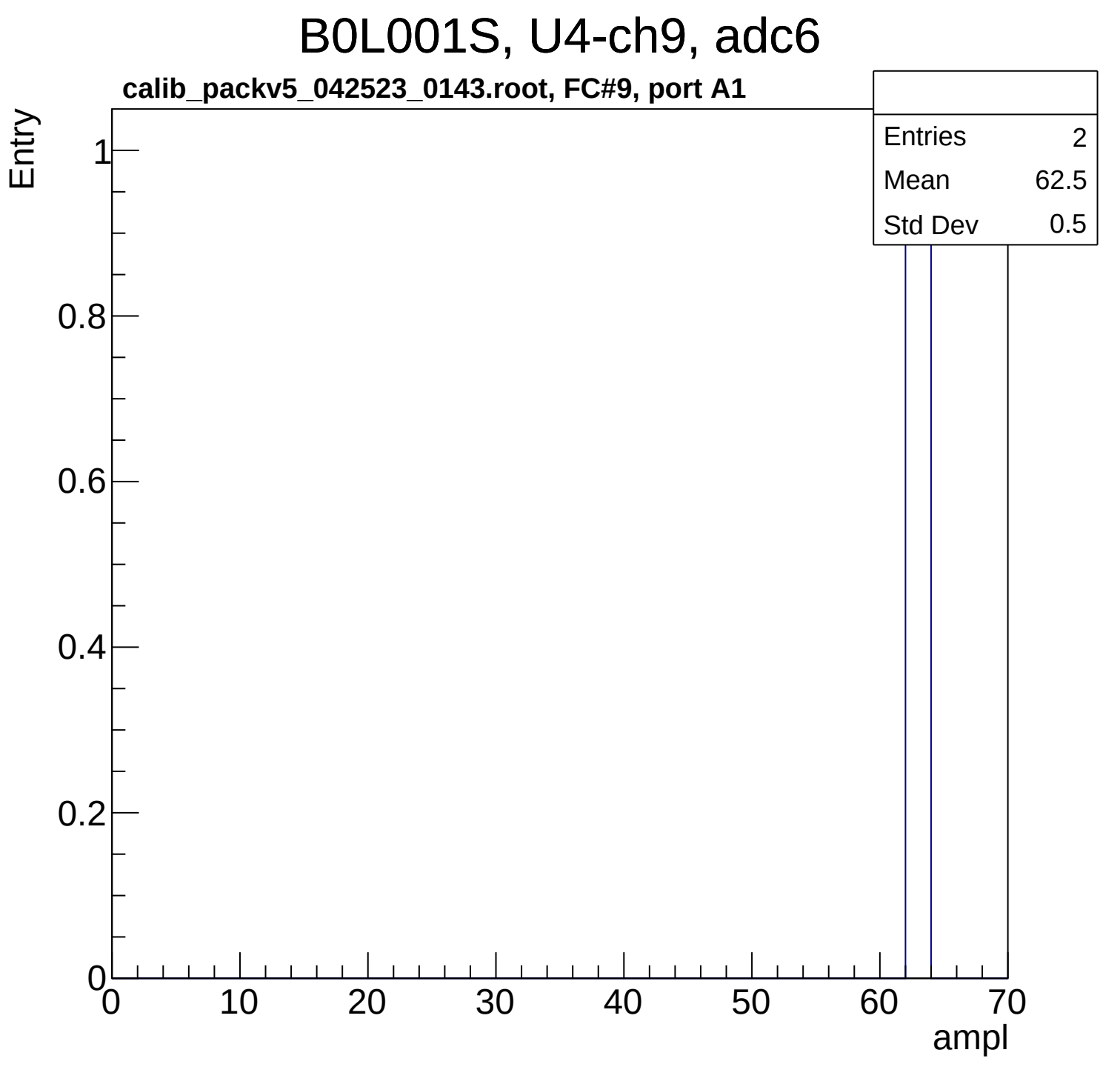
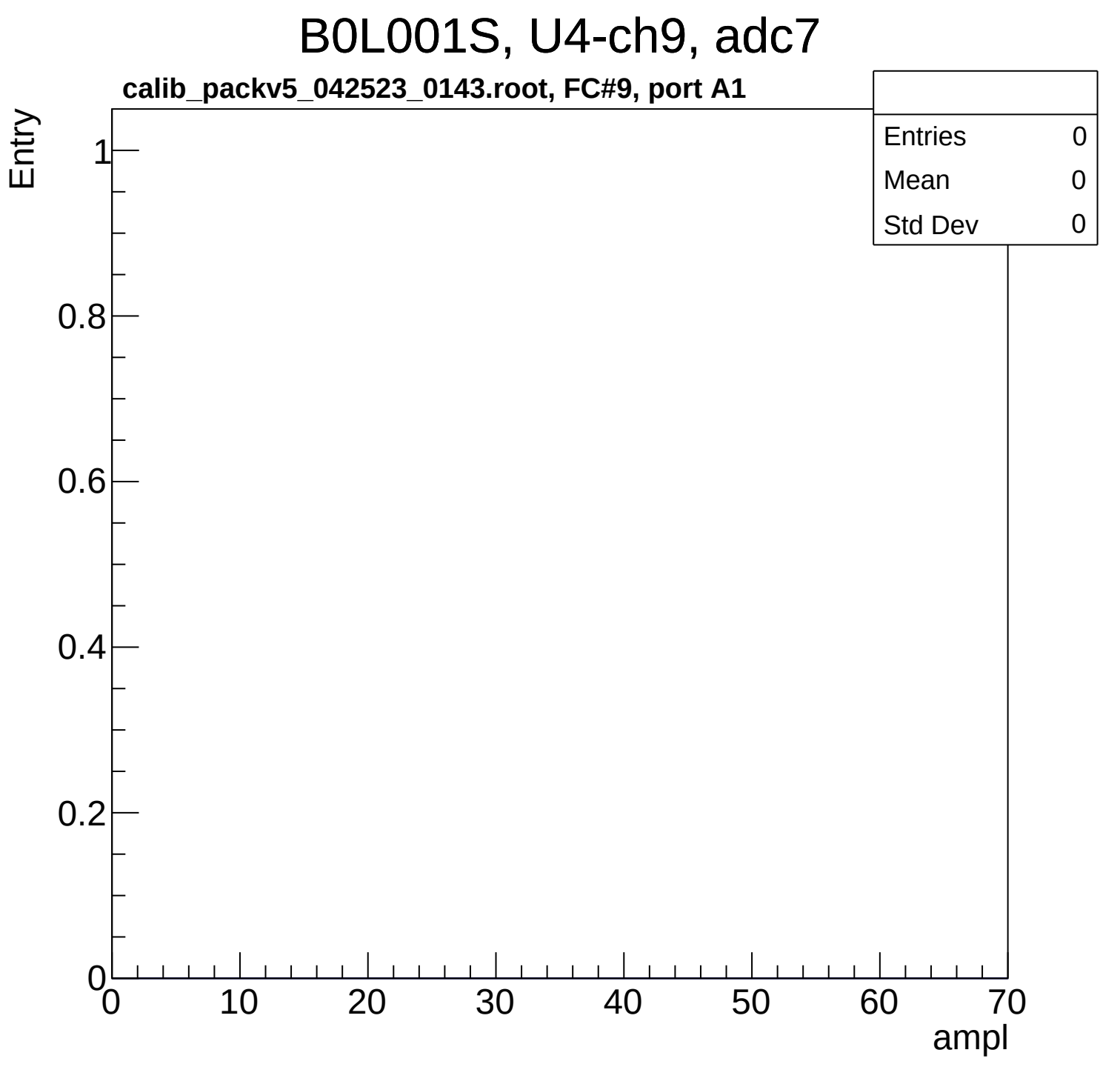

B0L001S, U4-ch9, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch9, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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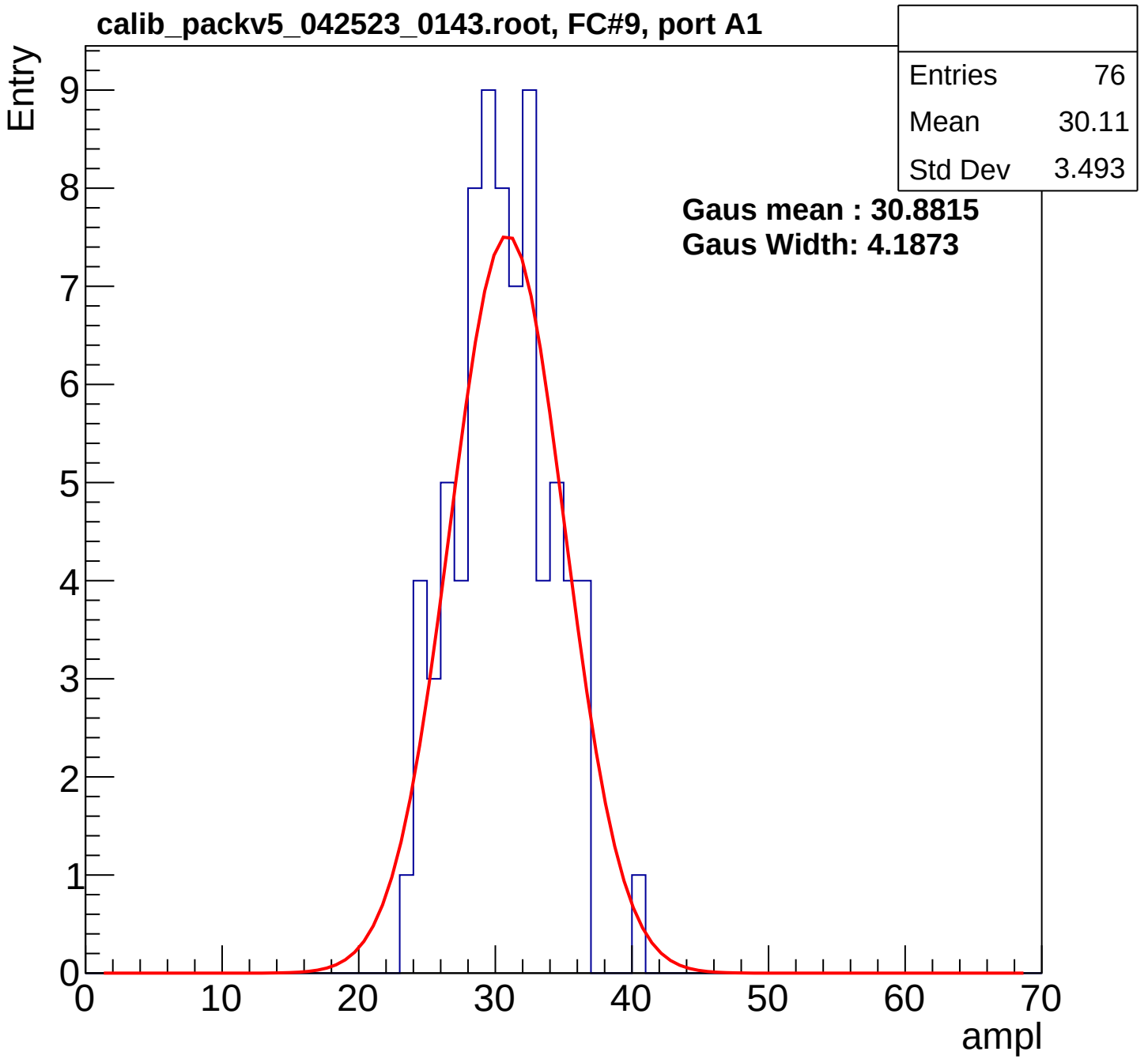
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch10, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 30.11
Std Dev 3.493
Gaus mean : 30.8815
Gaus Width: 4.1873
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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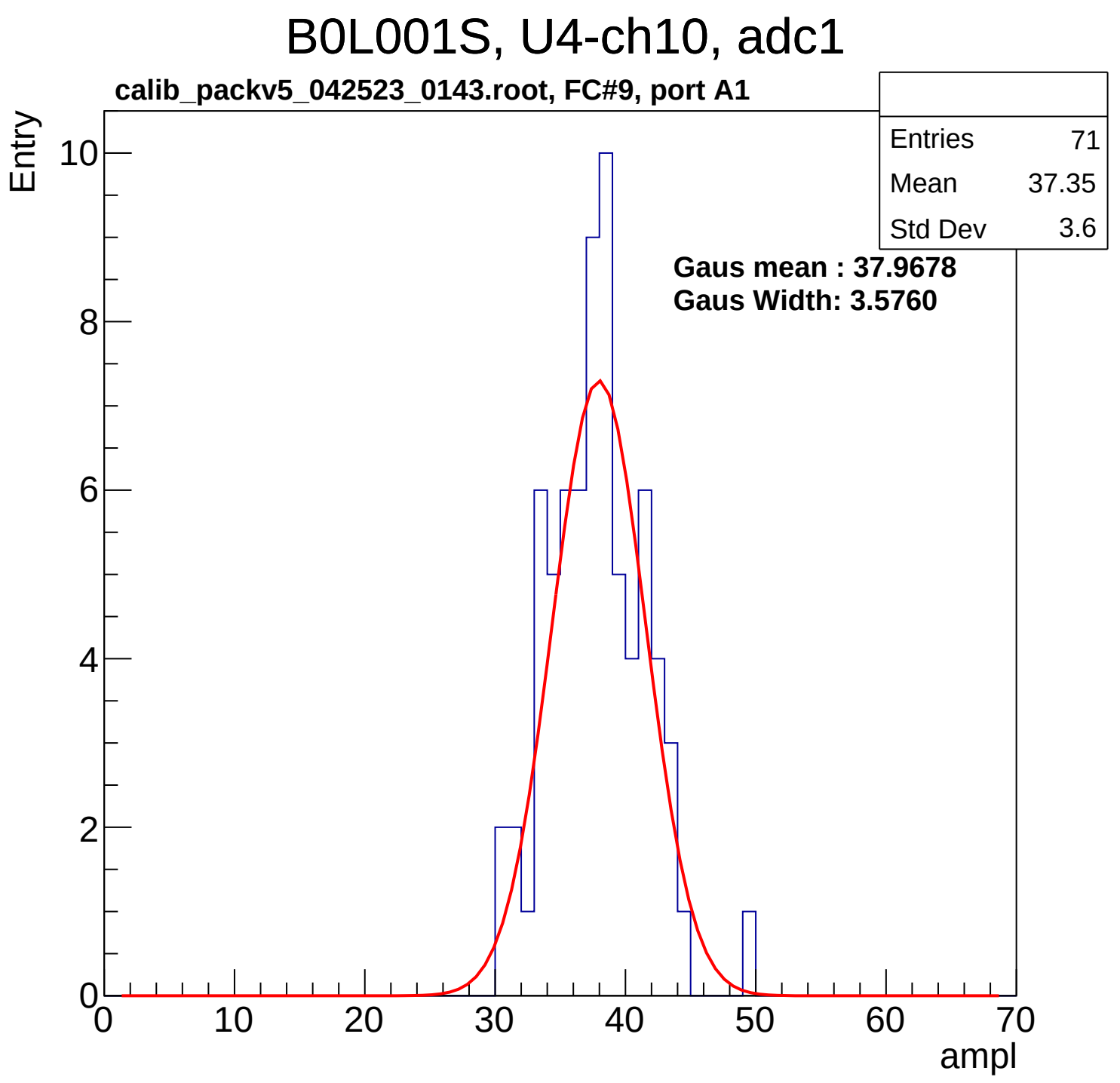
40
50
60
70


B0L001S, U4-ch10, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.35
Std Dev 3.6
Gaus mean : 37.9678
Gaus Width: 3.5760
Entry
ampl
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4
6
8
10
0
10
20
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40
50
60
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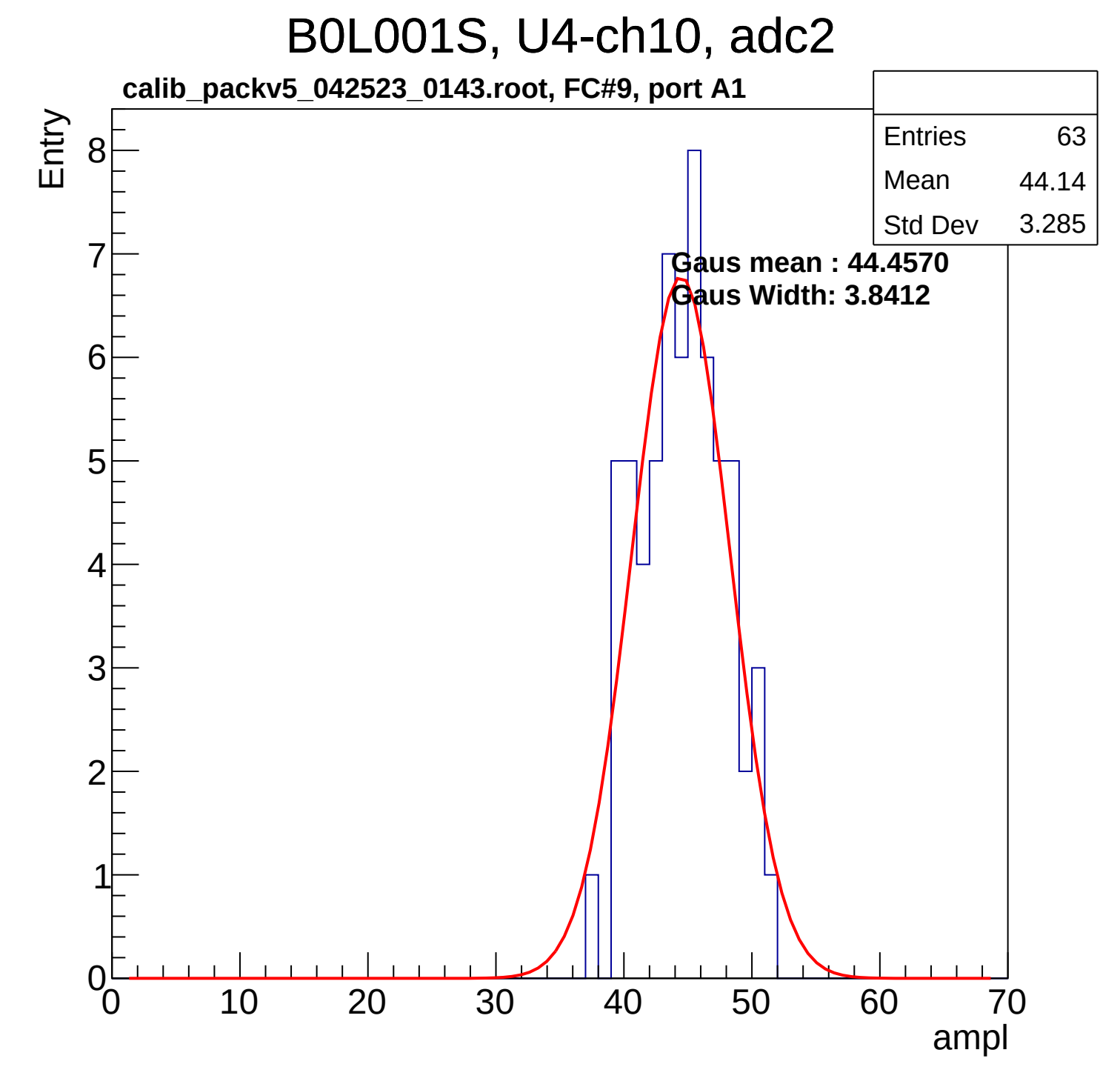
B0L001S, U4-ch10, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.14
Std Dev 3.285
Gaus mean : 44.4570
Gaus Width: 3.8412
Entry
ampl
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1
2
3
4
5
6
7
8
0
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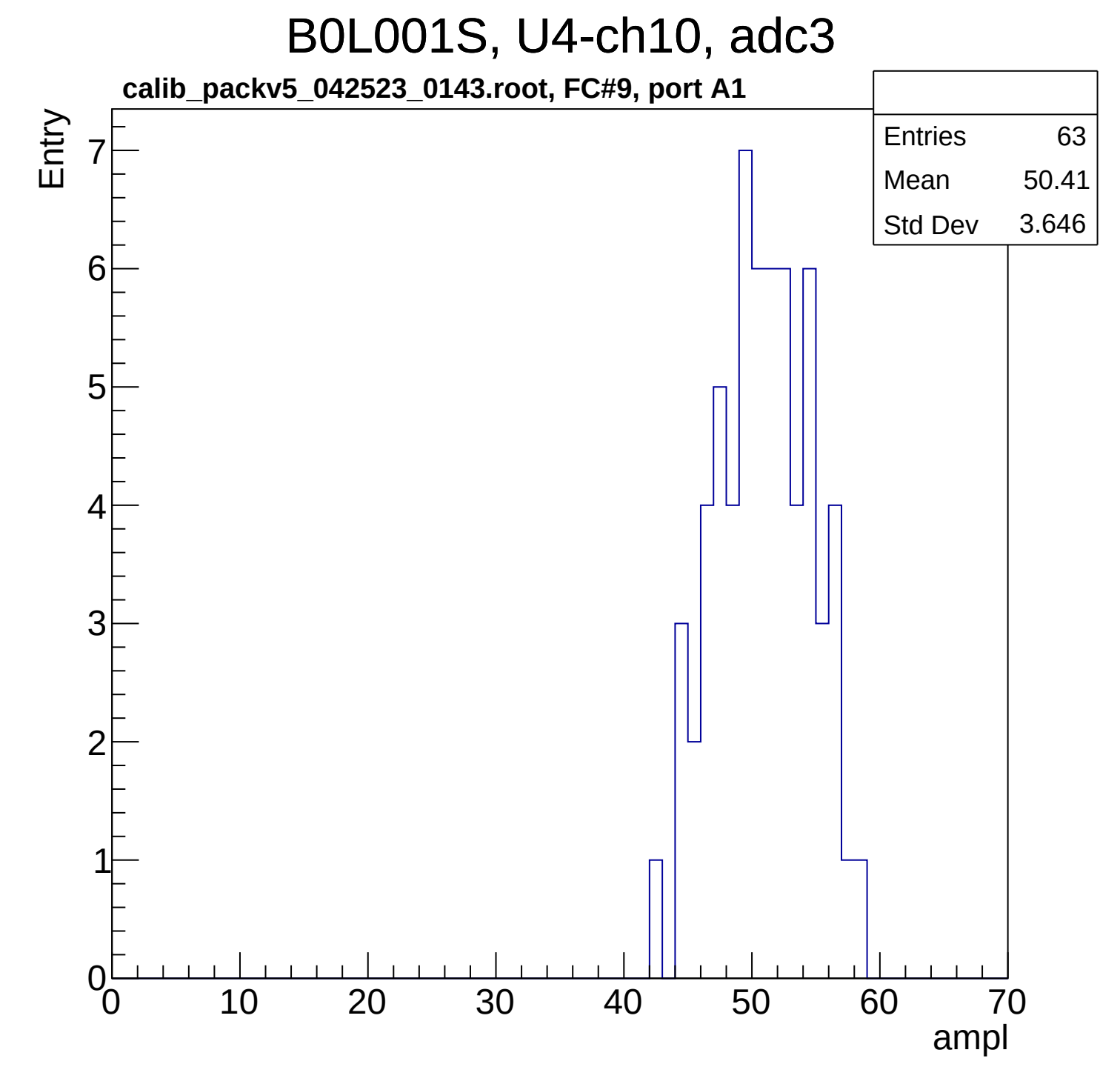
40
50
60
70


B0L001S, U4-ch10, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.41
Std Dev 3.646
Entry
ampl
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3
4
5
6
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0
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40
50
60
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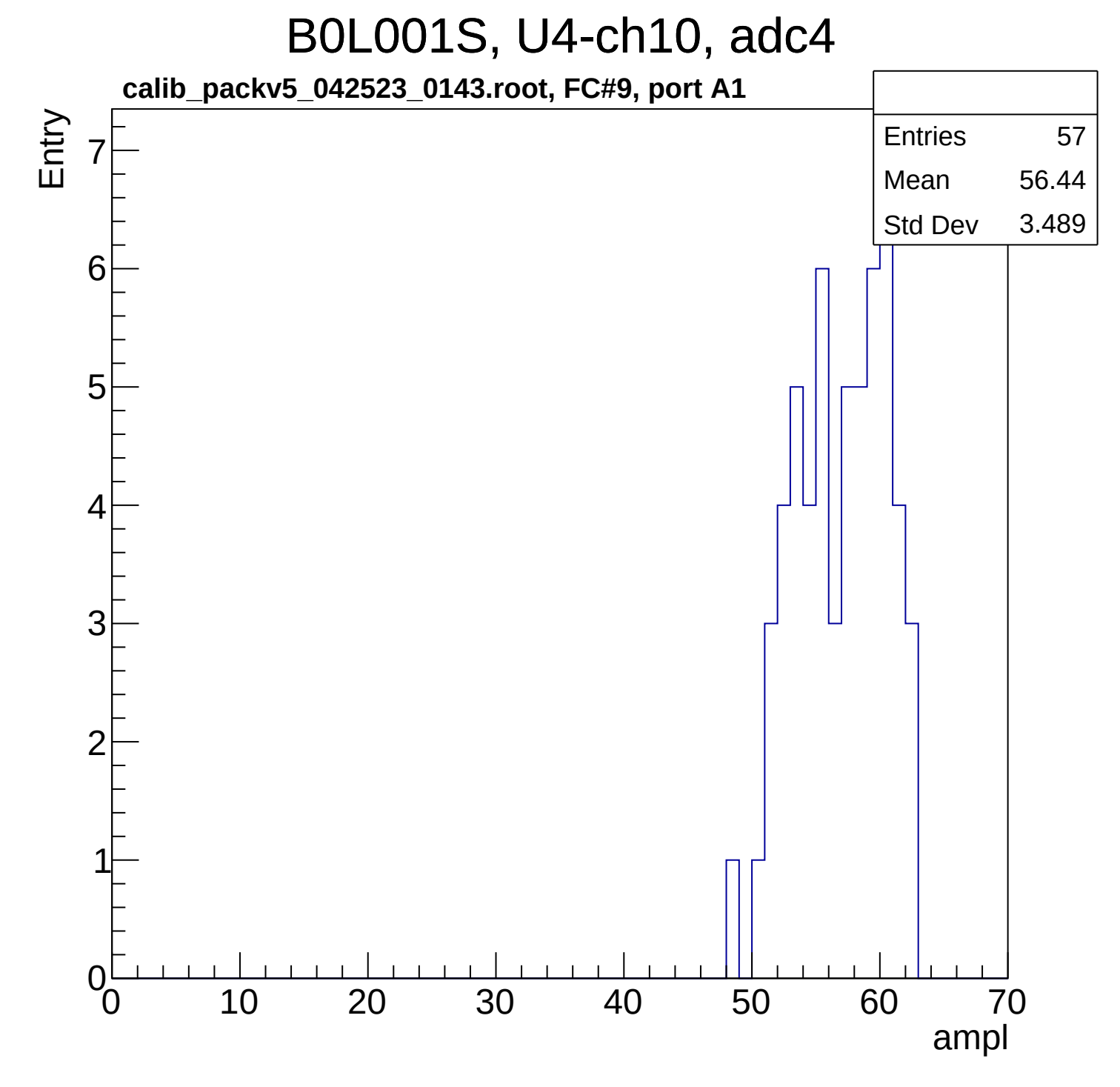
B0L001S, U4-ch10, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.44
Std Dev 3.489
Entry
ampl
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2
3
4
5
6
7
0
10
20
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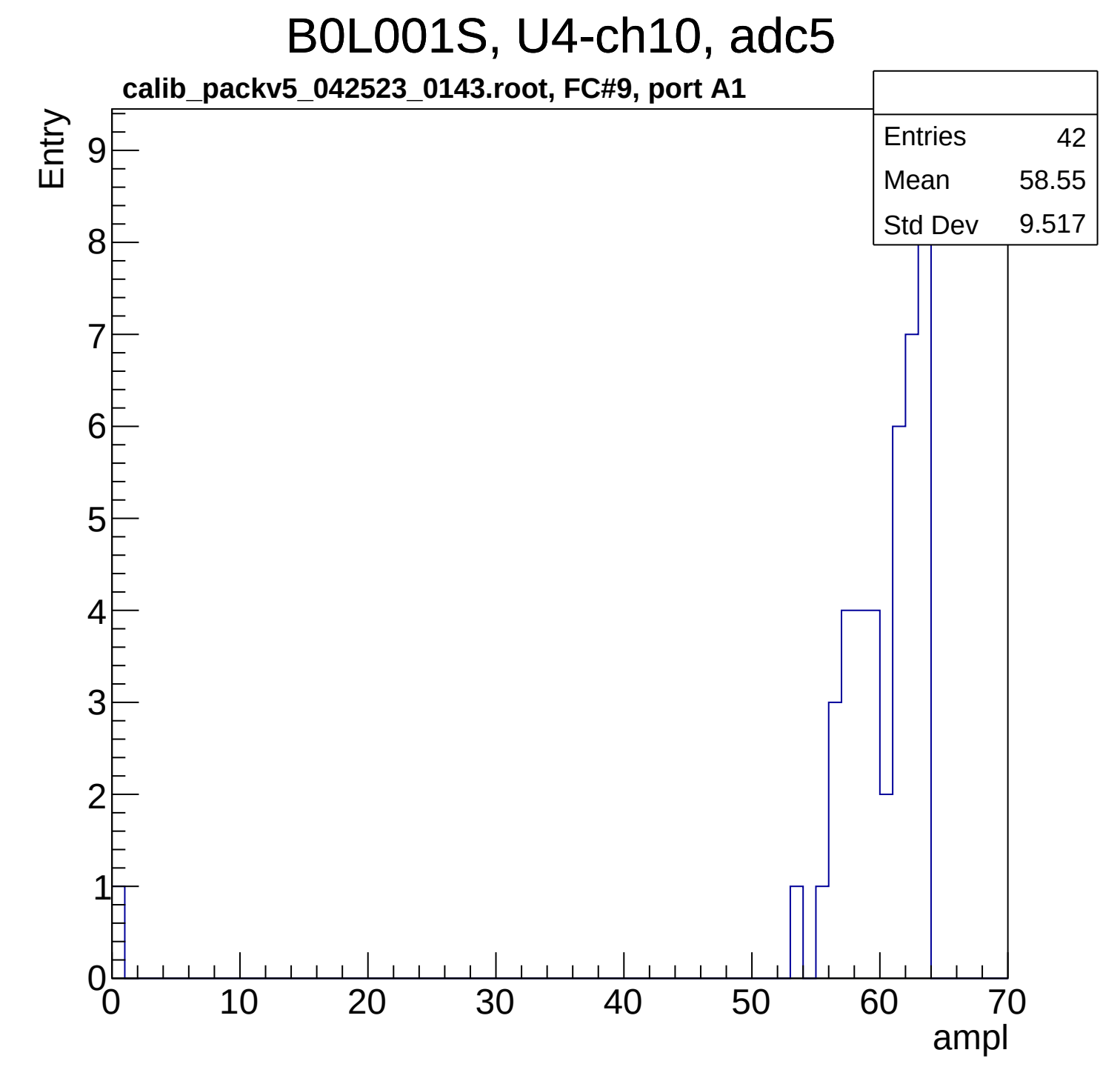
40
50
60
70


B0L001S, U4-ch10, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.55
Std Dev 9.517
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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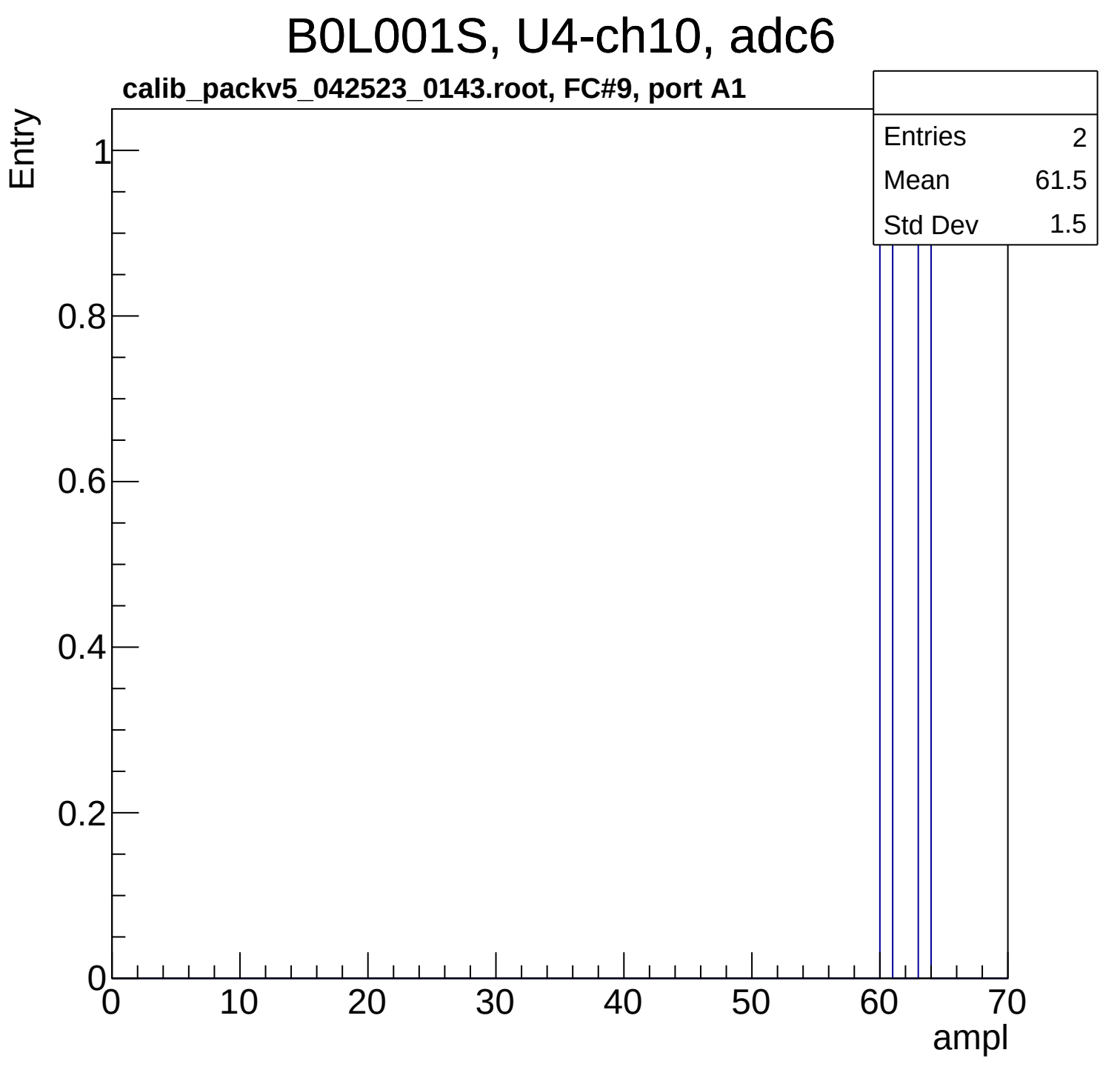
B0L001S, U4-ch10, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
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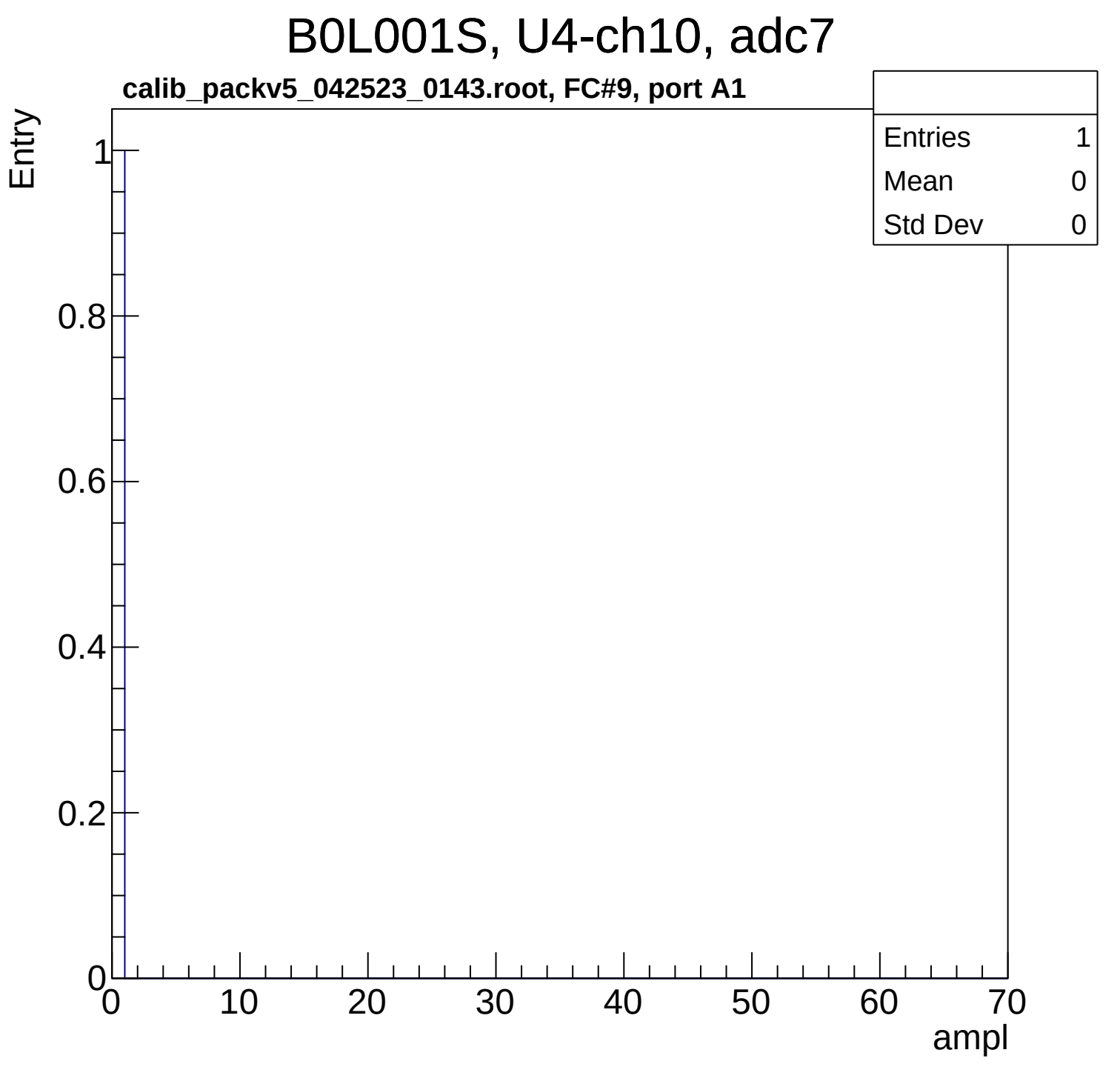
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch10, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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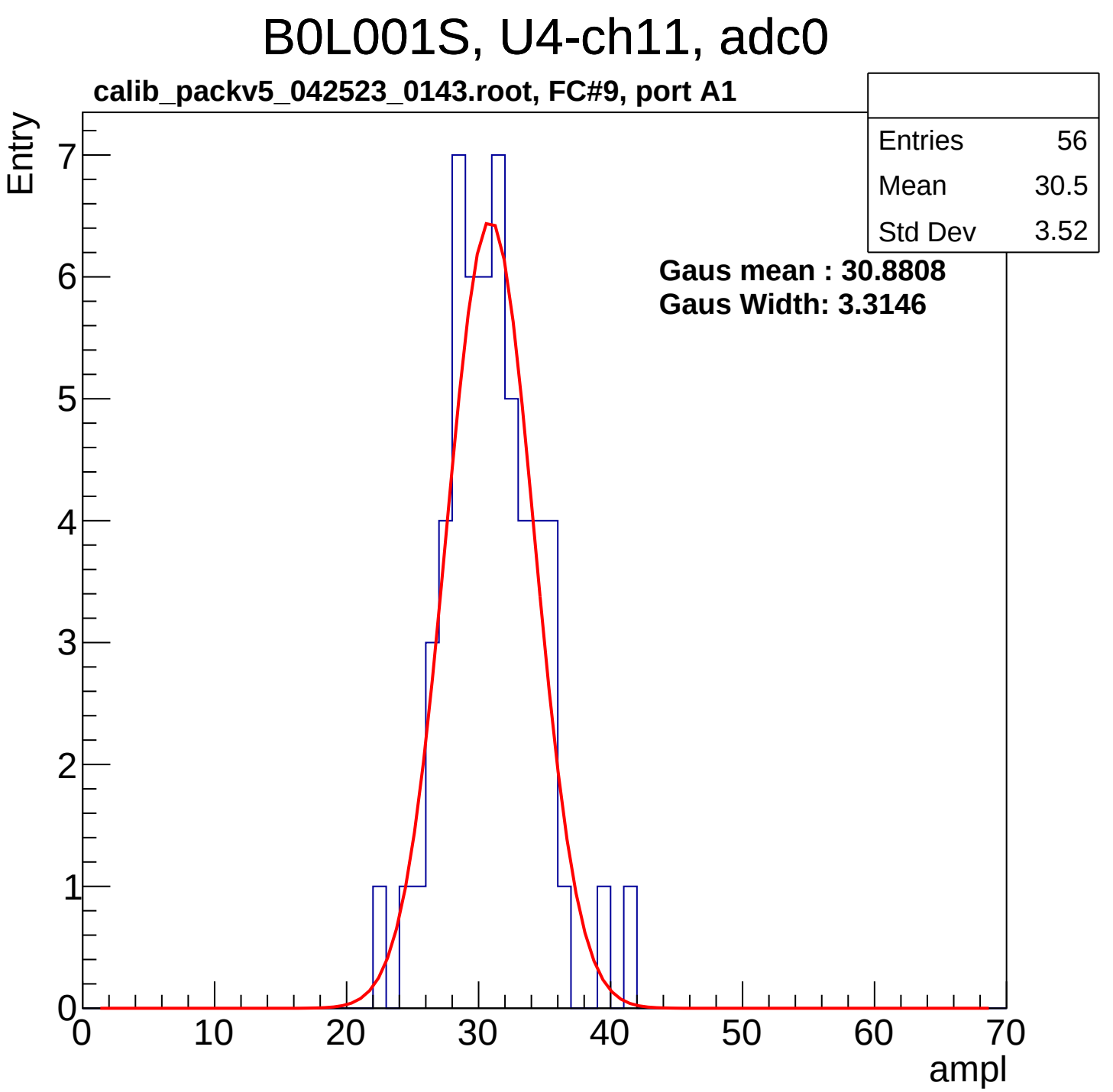
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch11, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.5
Std Dev 3.52
Gaus mean : 30.8808
Gaus Width: 3.3146
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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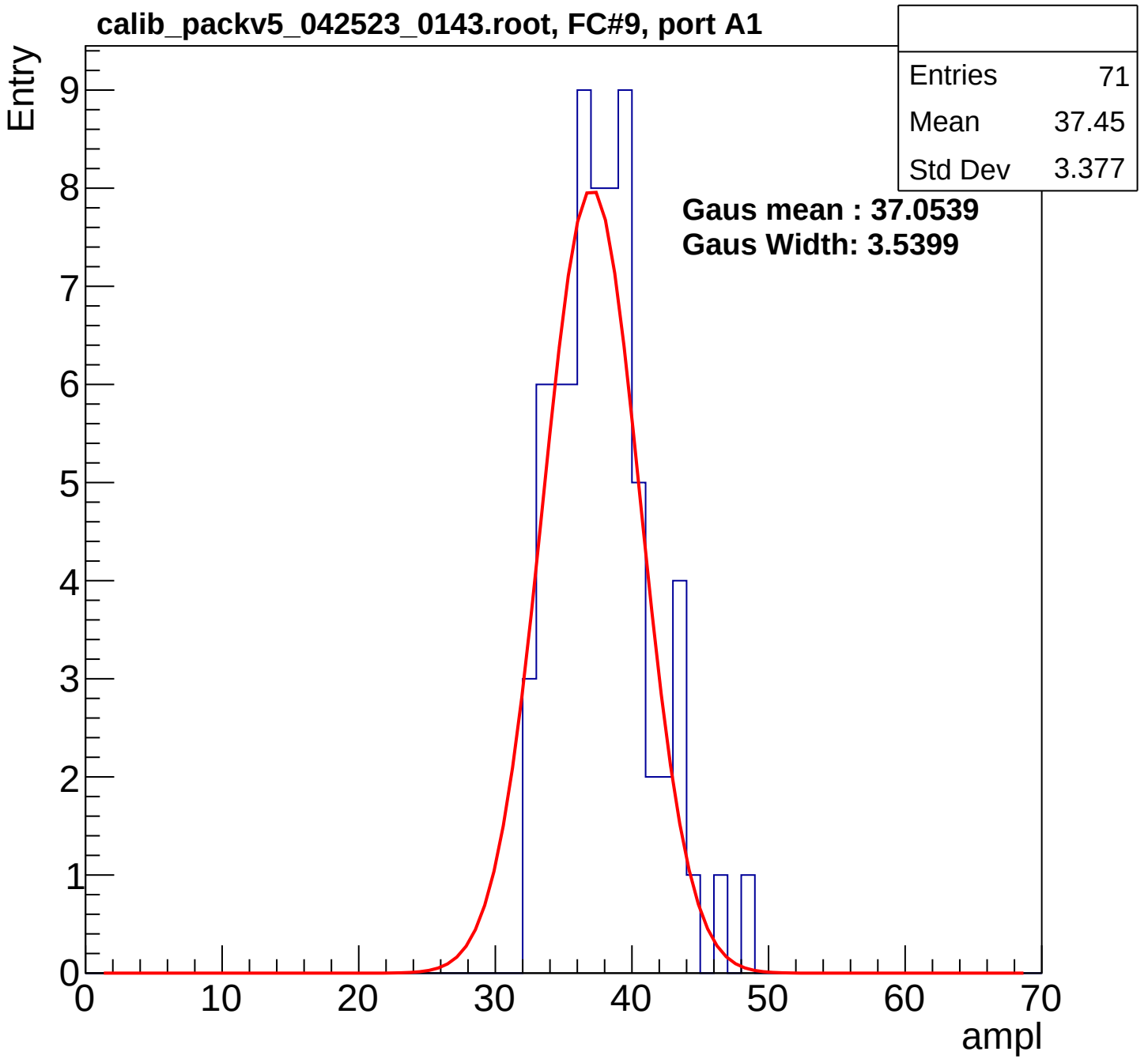
B0L001S, U4-ch11, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.45
Std Dev 3.377
Gaus mean : 37.0539
Gaus Width: 3.5399
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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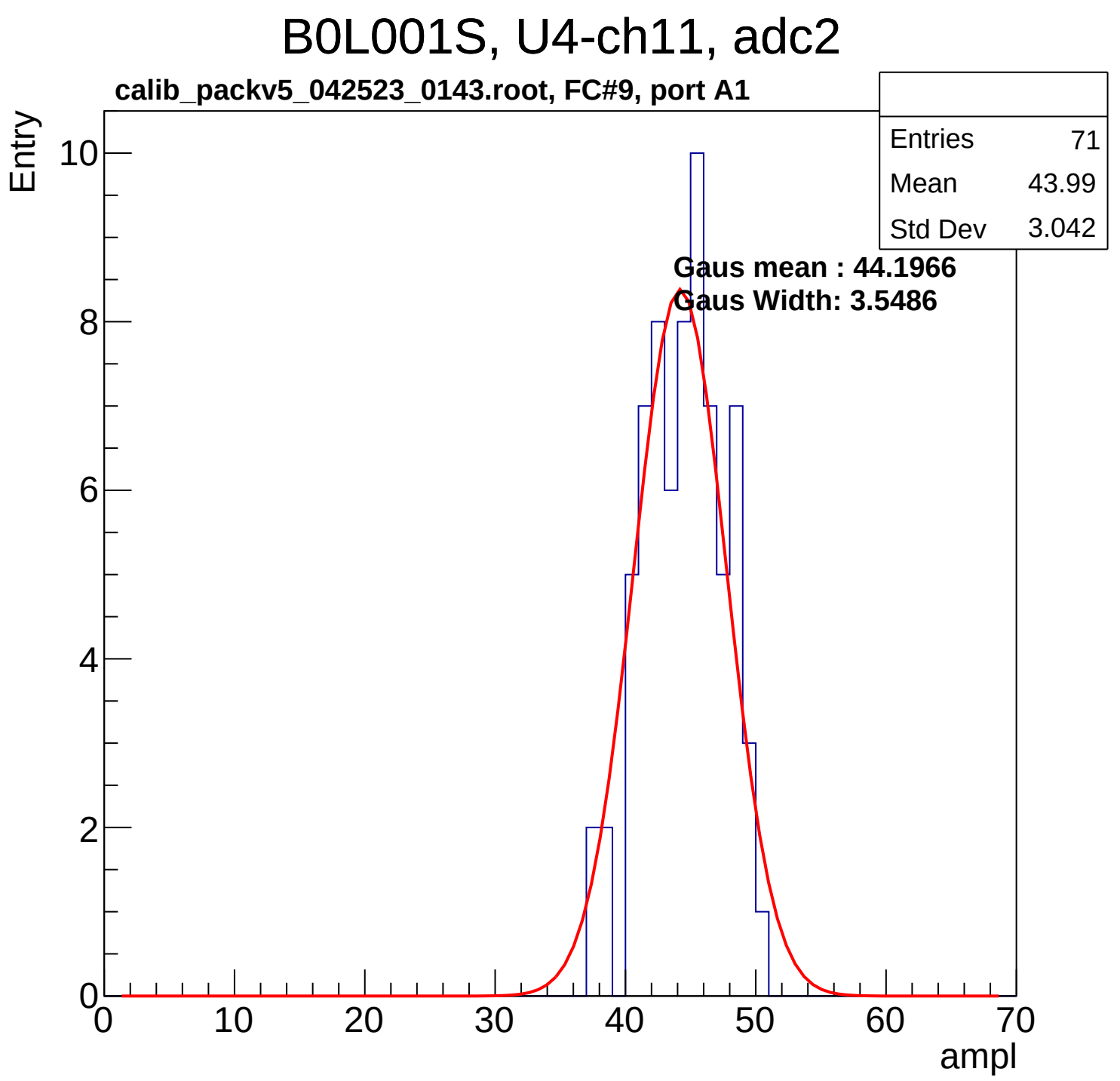
B0L001S, U4-ch11, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.99
Std Dev 3.042
Gaus mean : 44.1966
Gaus Width: 3.5486
Entry
ampl
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4
6
8
10
0
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60
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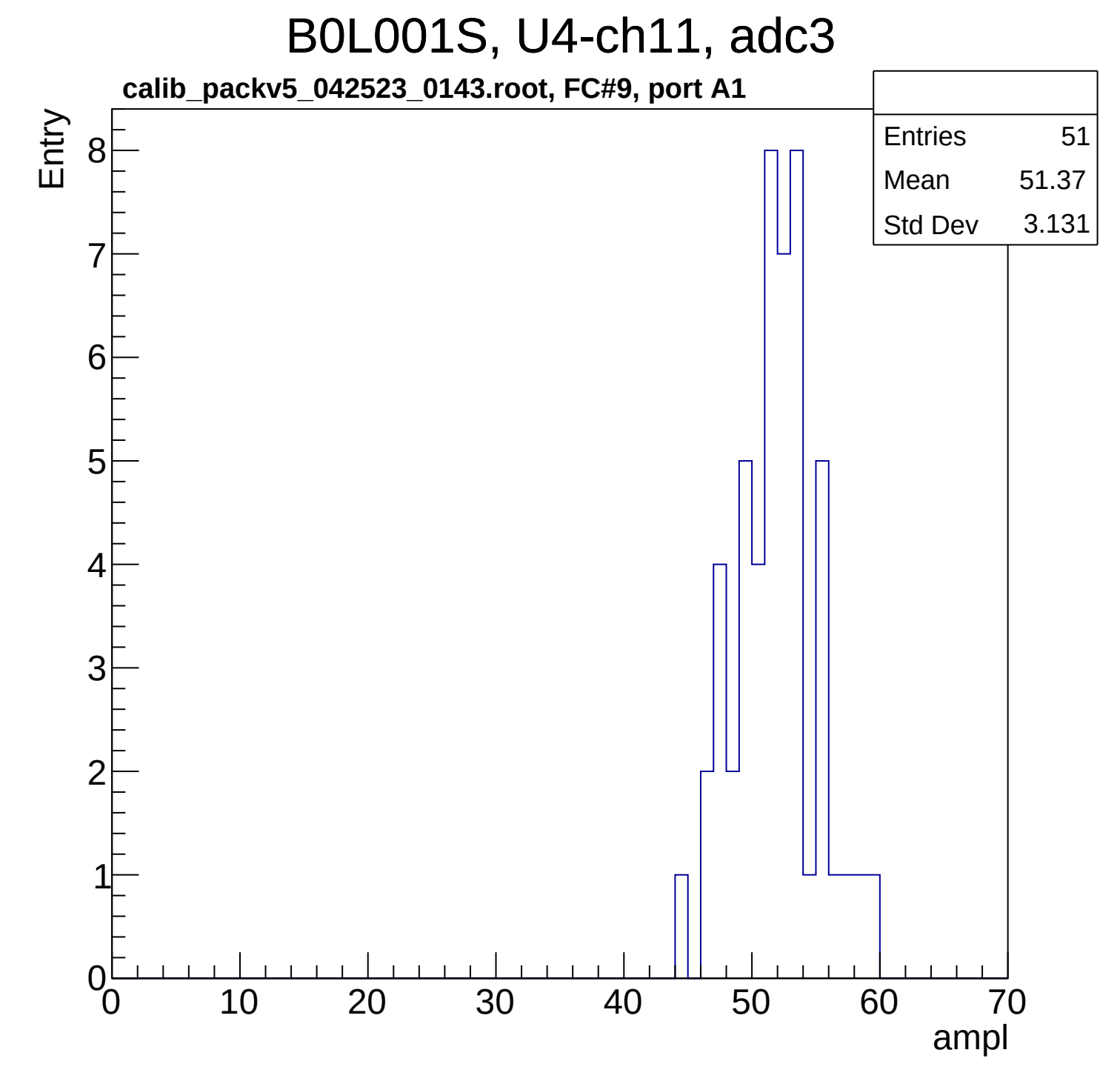
B0L001S, U4-ch11, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 51.37
Std Dev 3.131
Entry
ampl
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4
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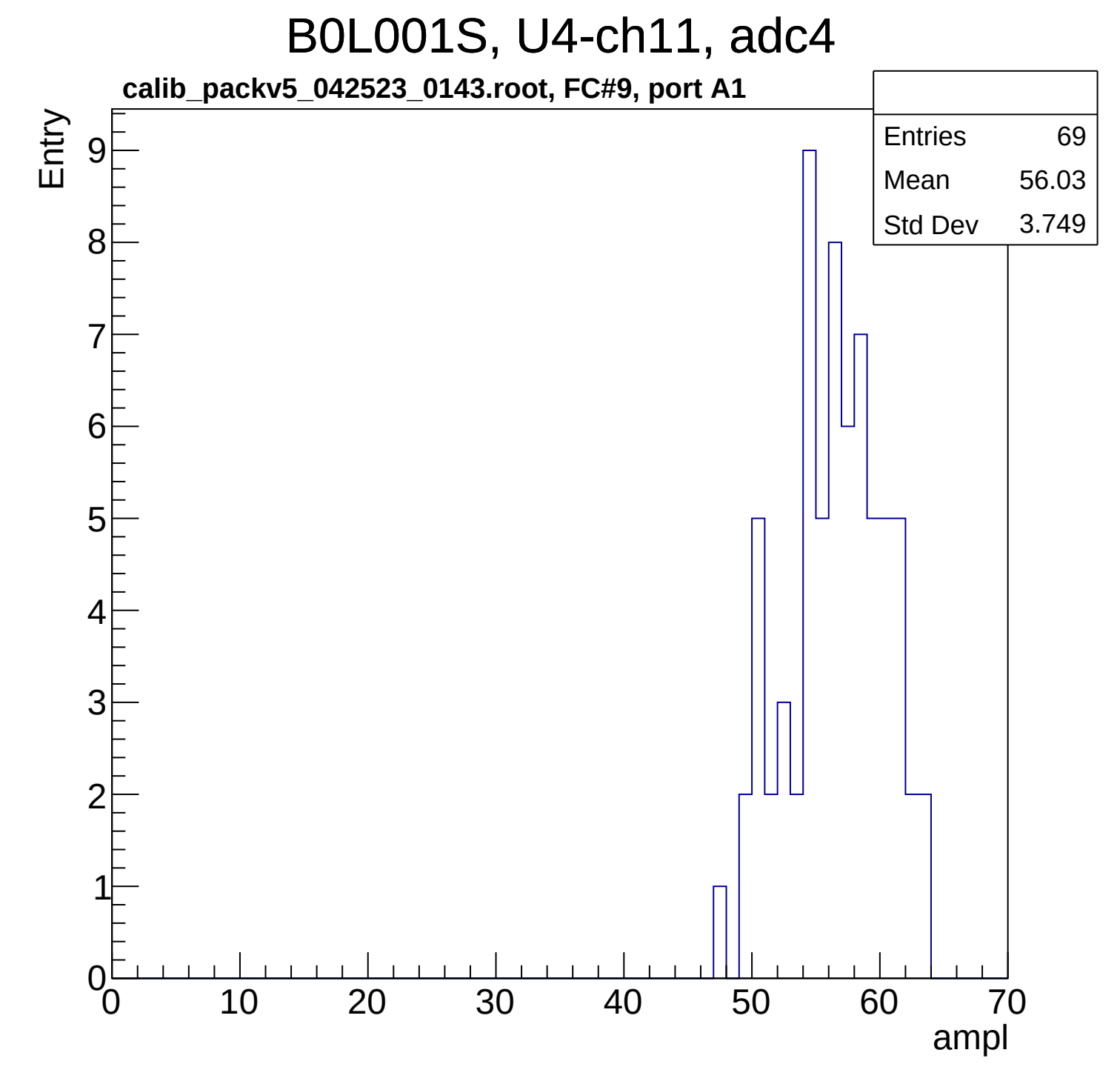
B0L001S, U4-ch11, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 56.03
Std Dev 3.749
Entry
ampl
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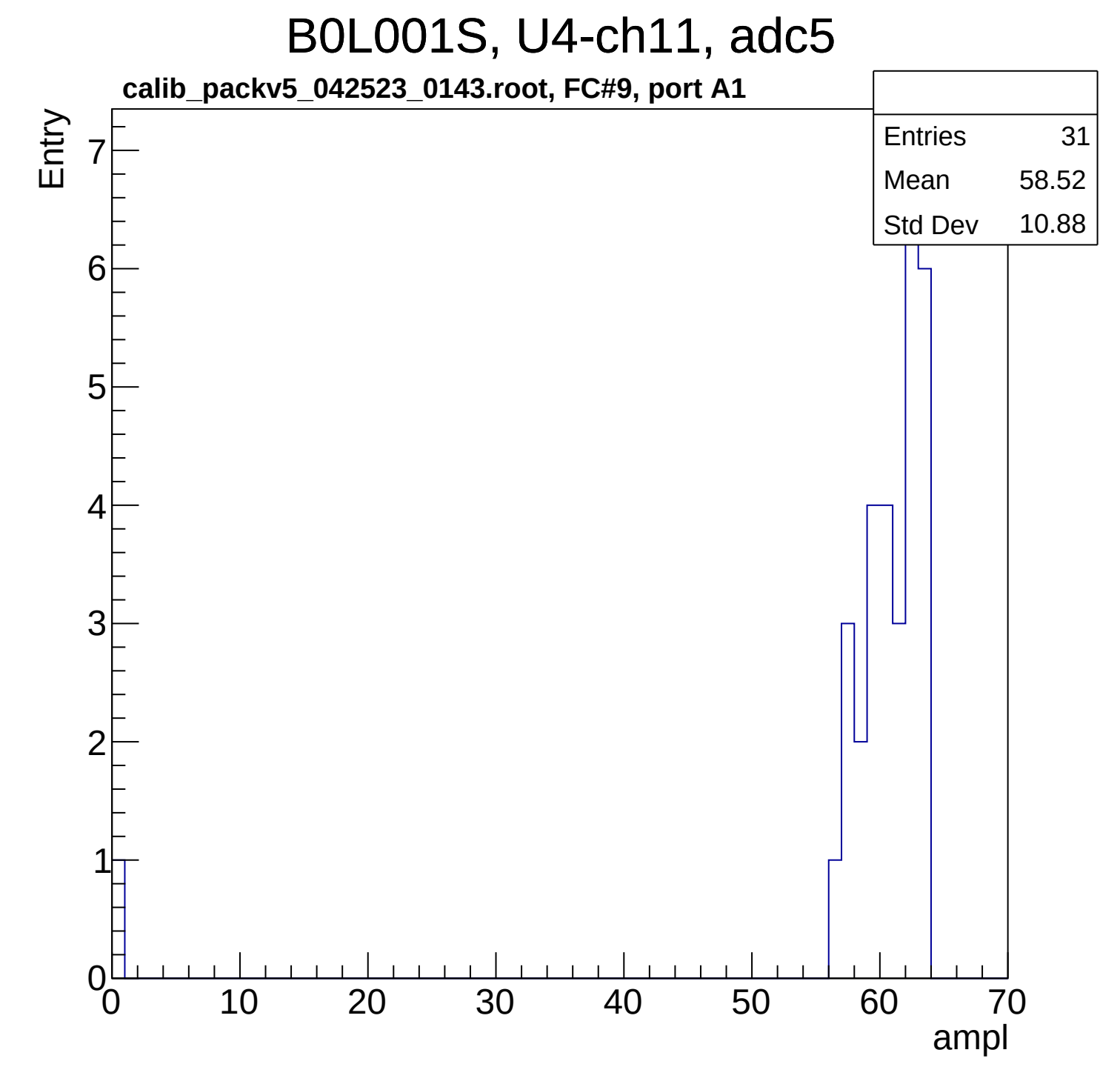
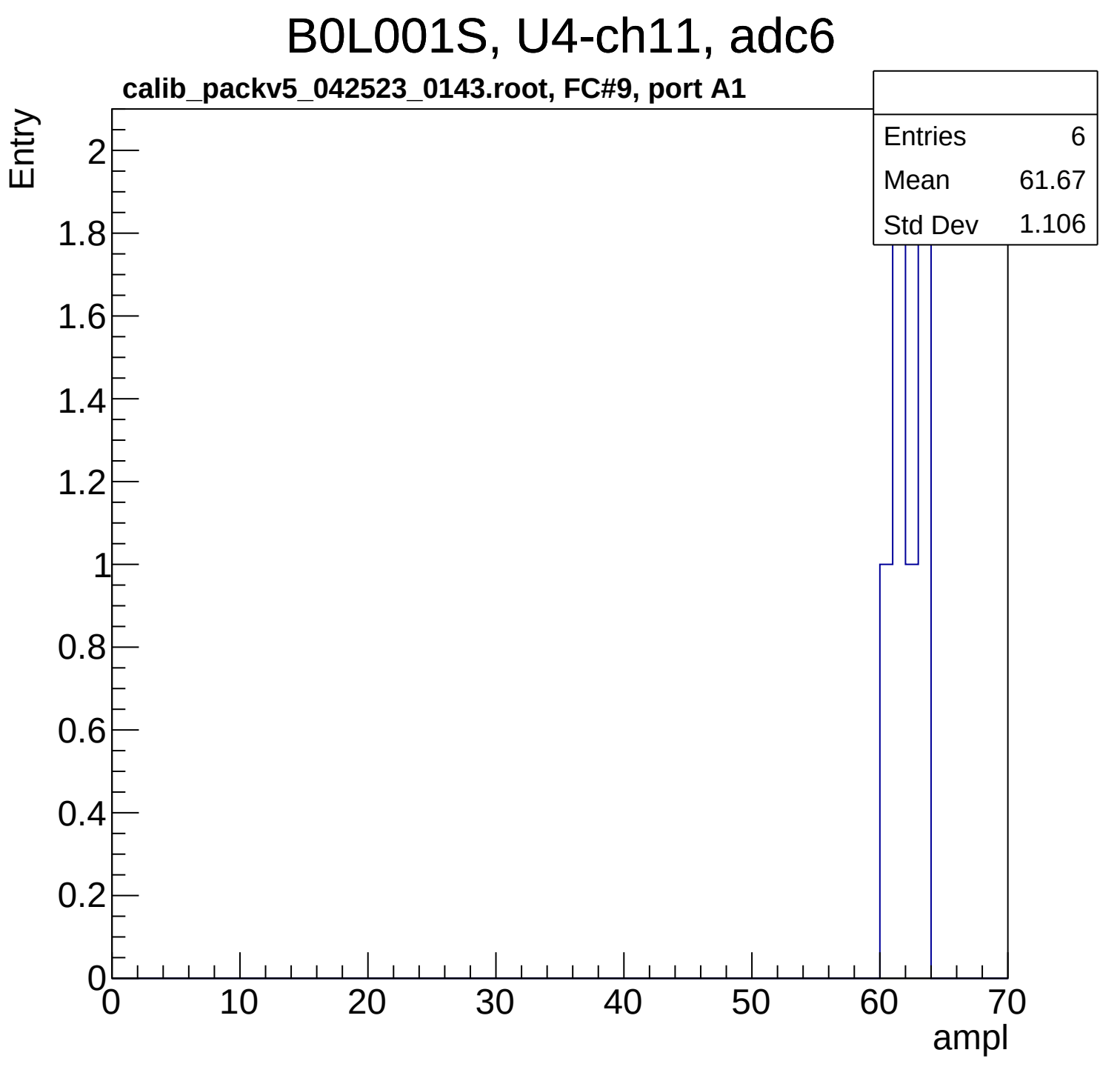
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch11, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.52
Std Dev 10.88
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch11, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.67
Std Dev 1.106
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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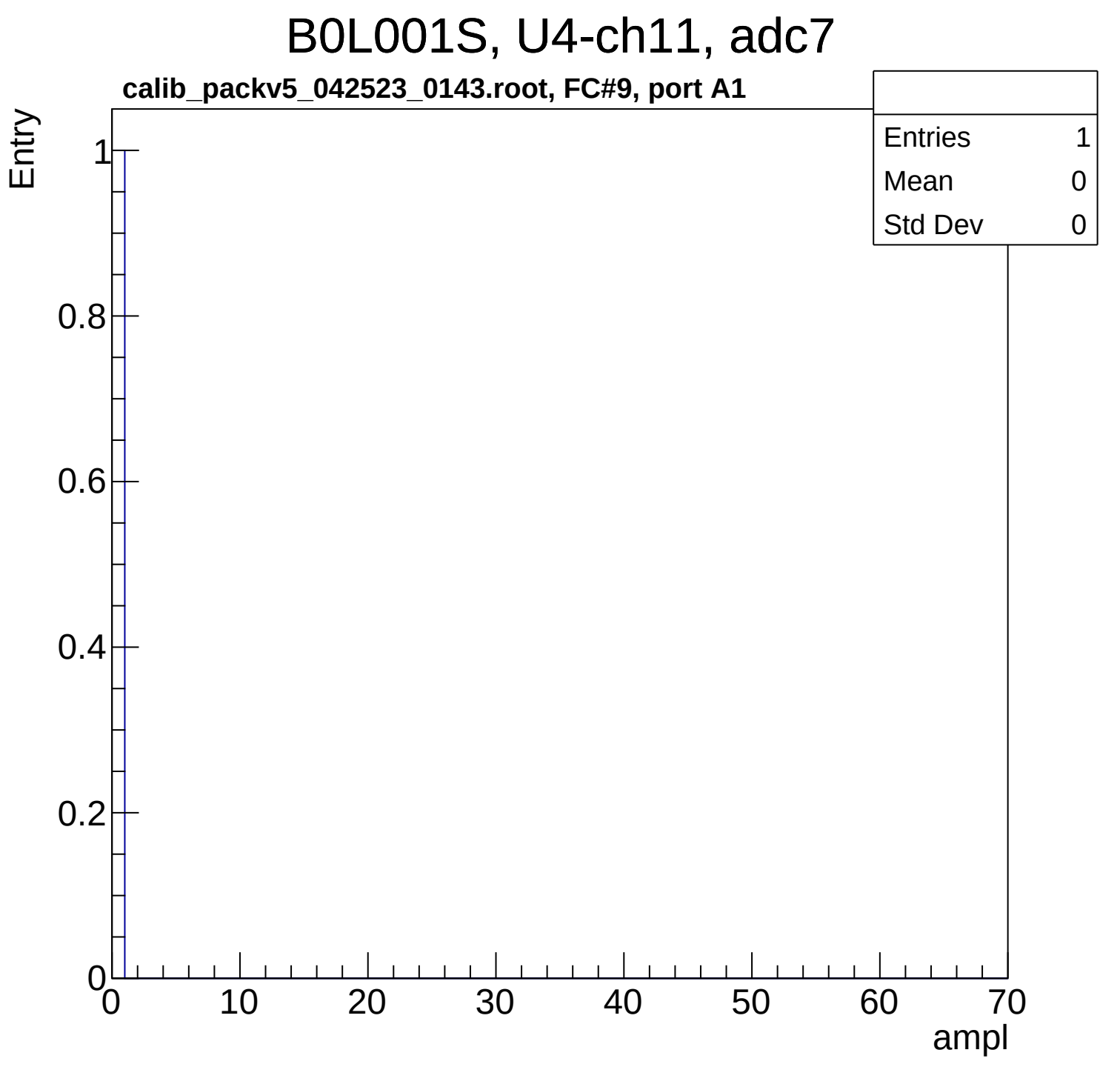
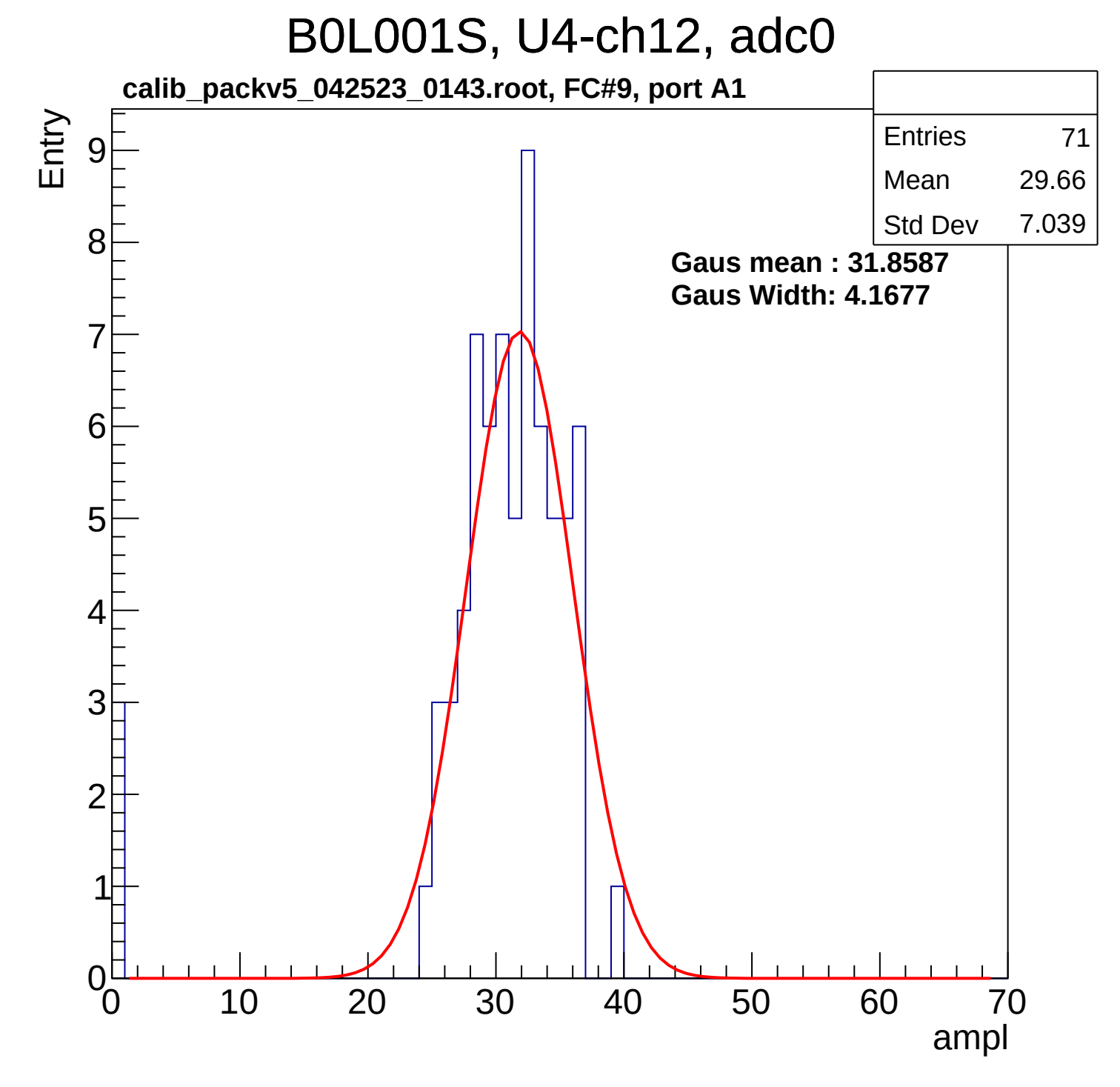

B0L001S, U4-ch11, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch12, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.66
Std Dev 7.039
Gaus mean : 31.8587
Gaus Width: 4.1677
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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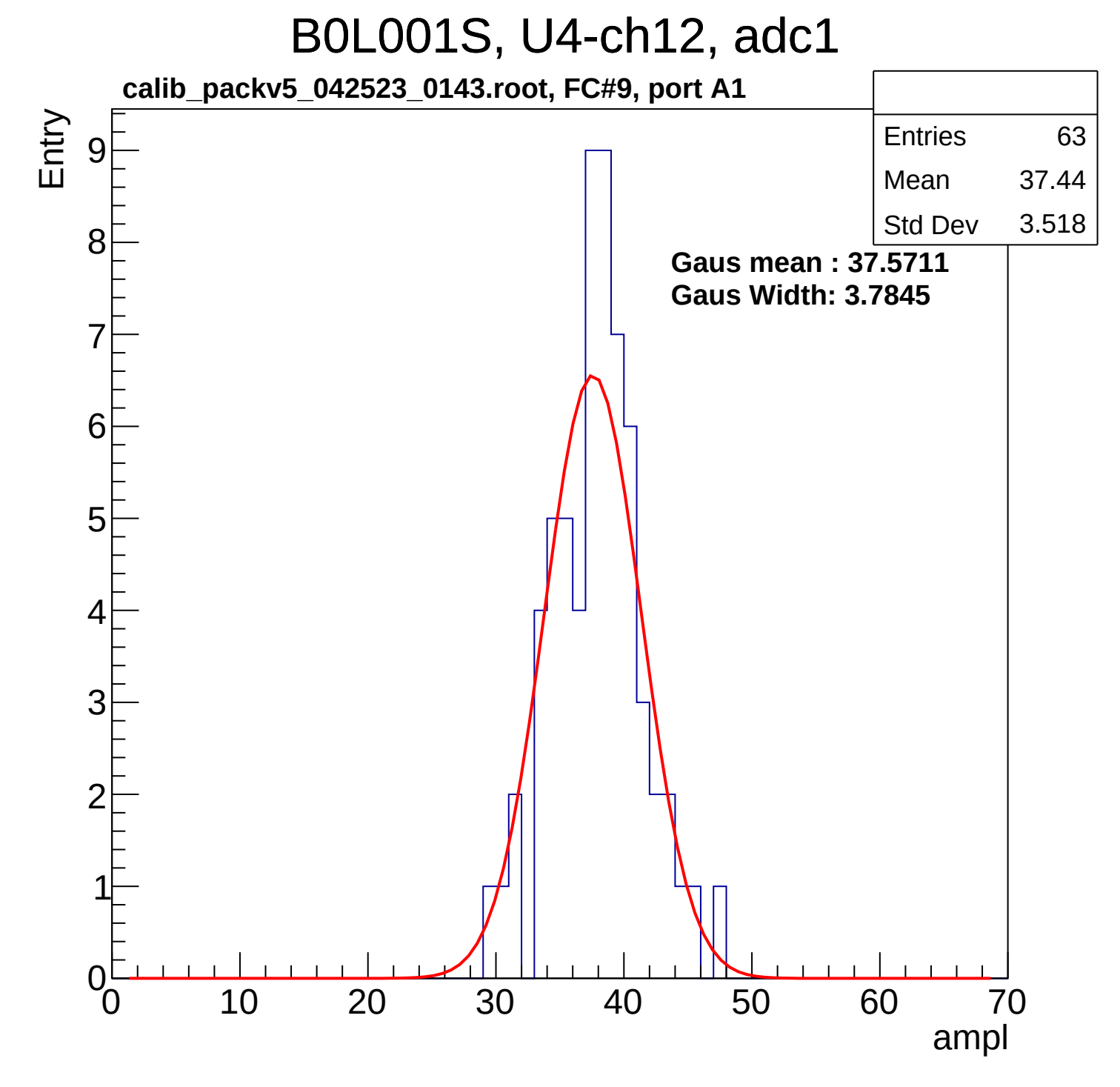
B0L001S, U4-ch12, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.44
Std Dev 3.518
Gaus mean : 37.5711
Gaus Width: 3.7845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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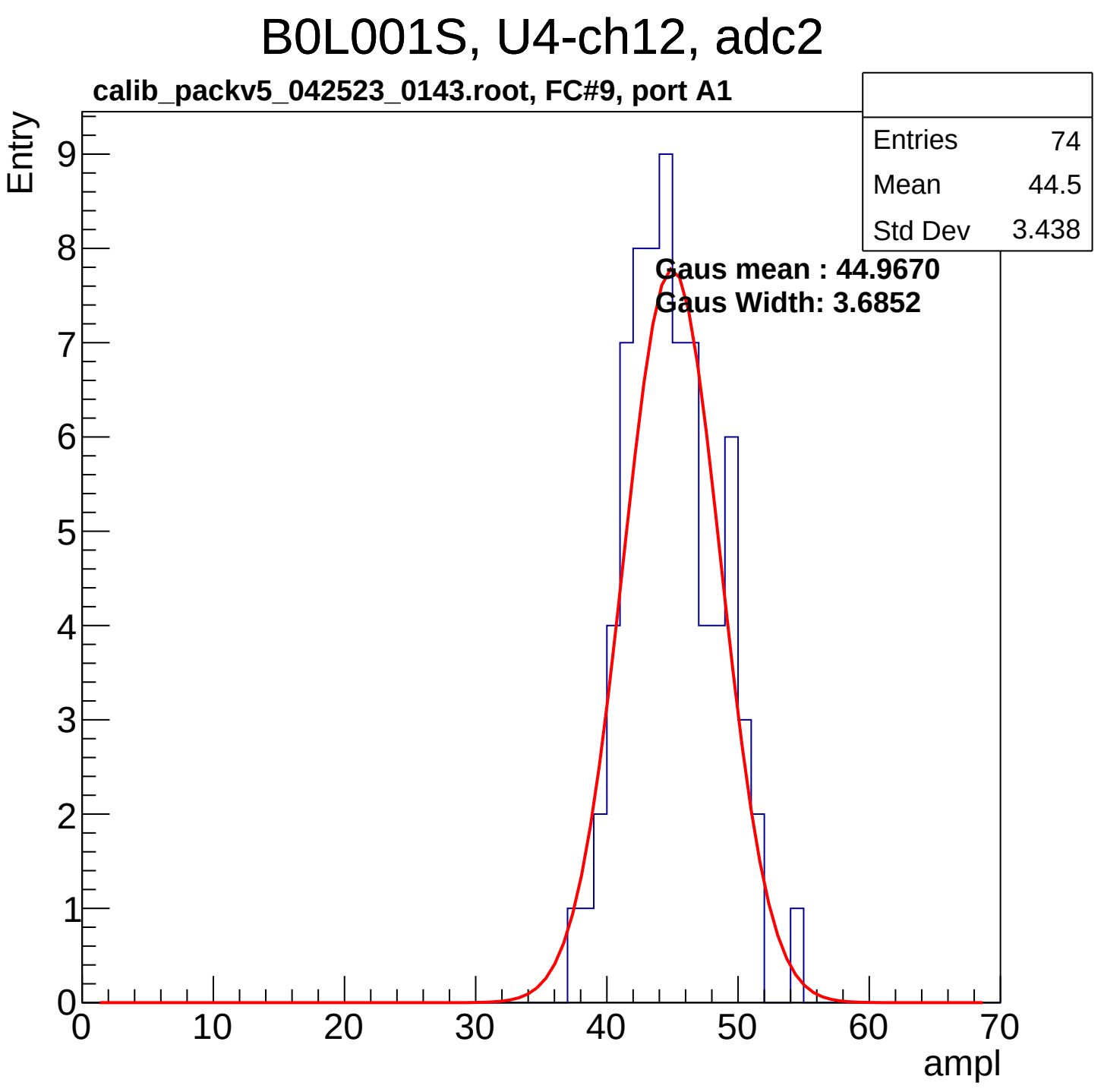
B0L001S, U4-ch12, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 44.5
Std Dev 3.438
Gaus mean : 44.9670
Gaus Width: 3.6852
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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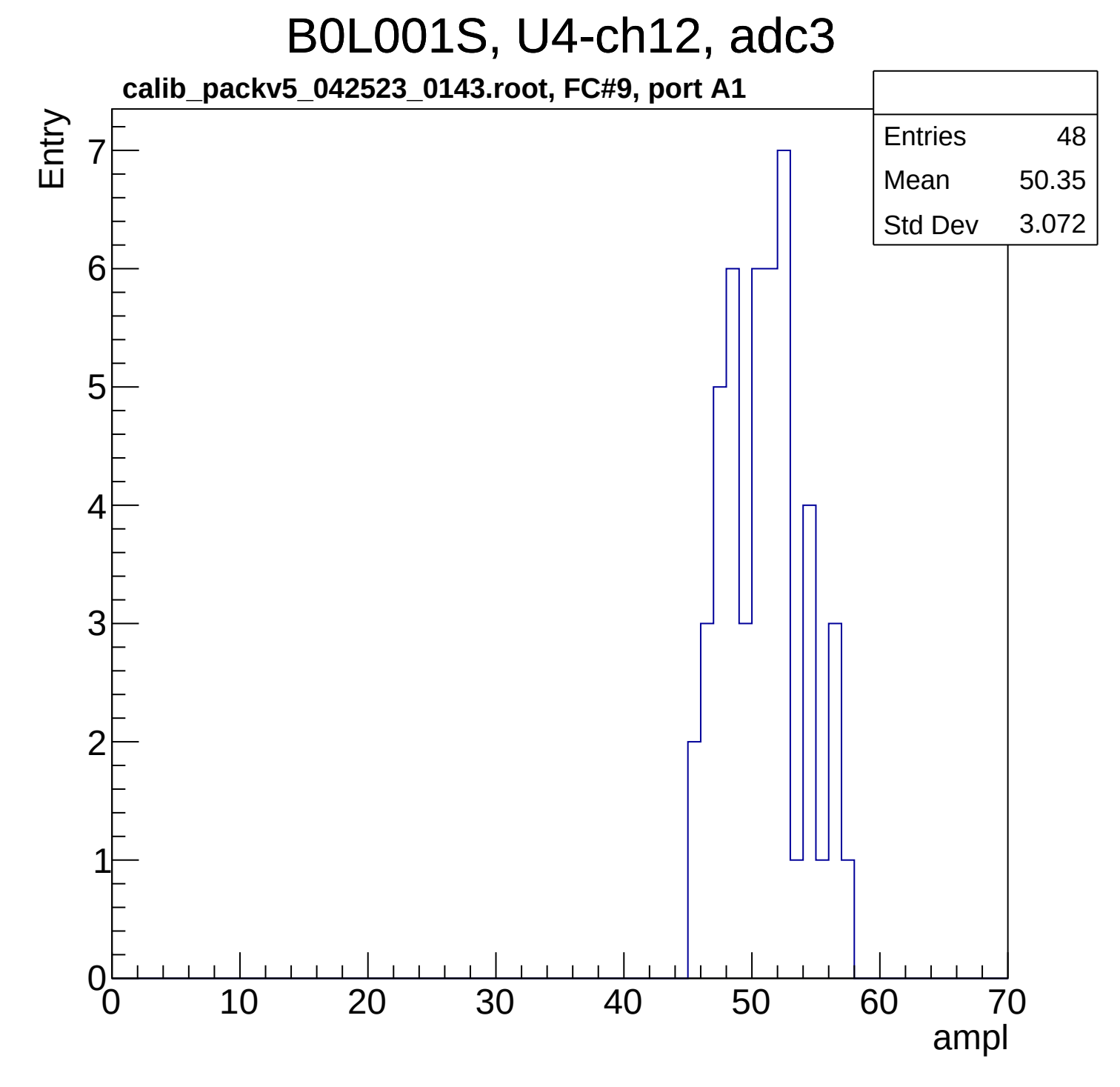
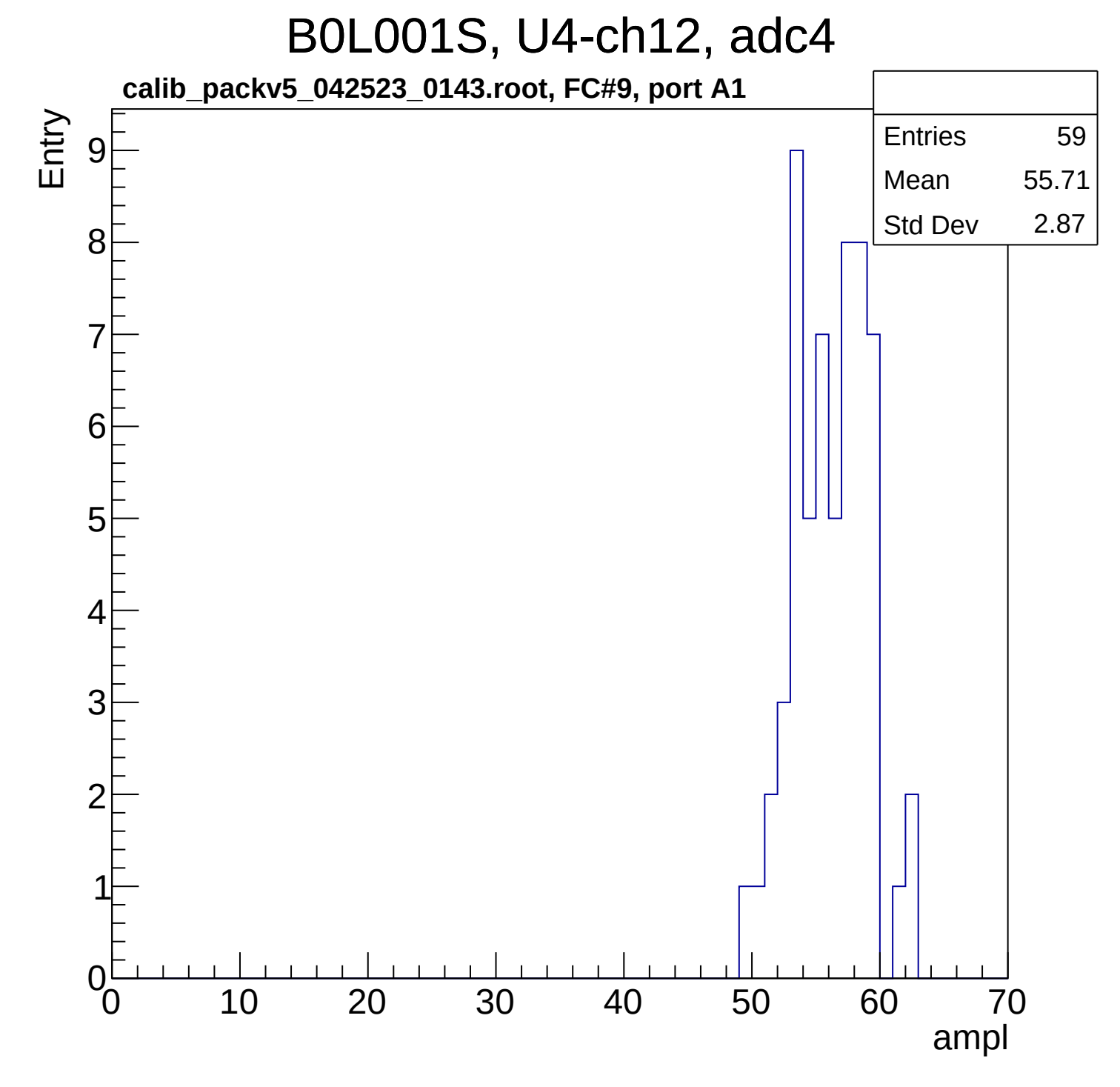
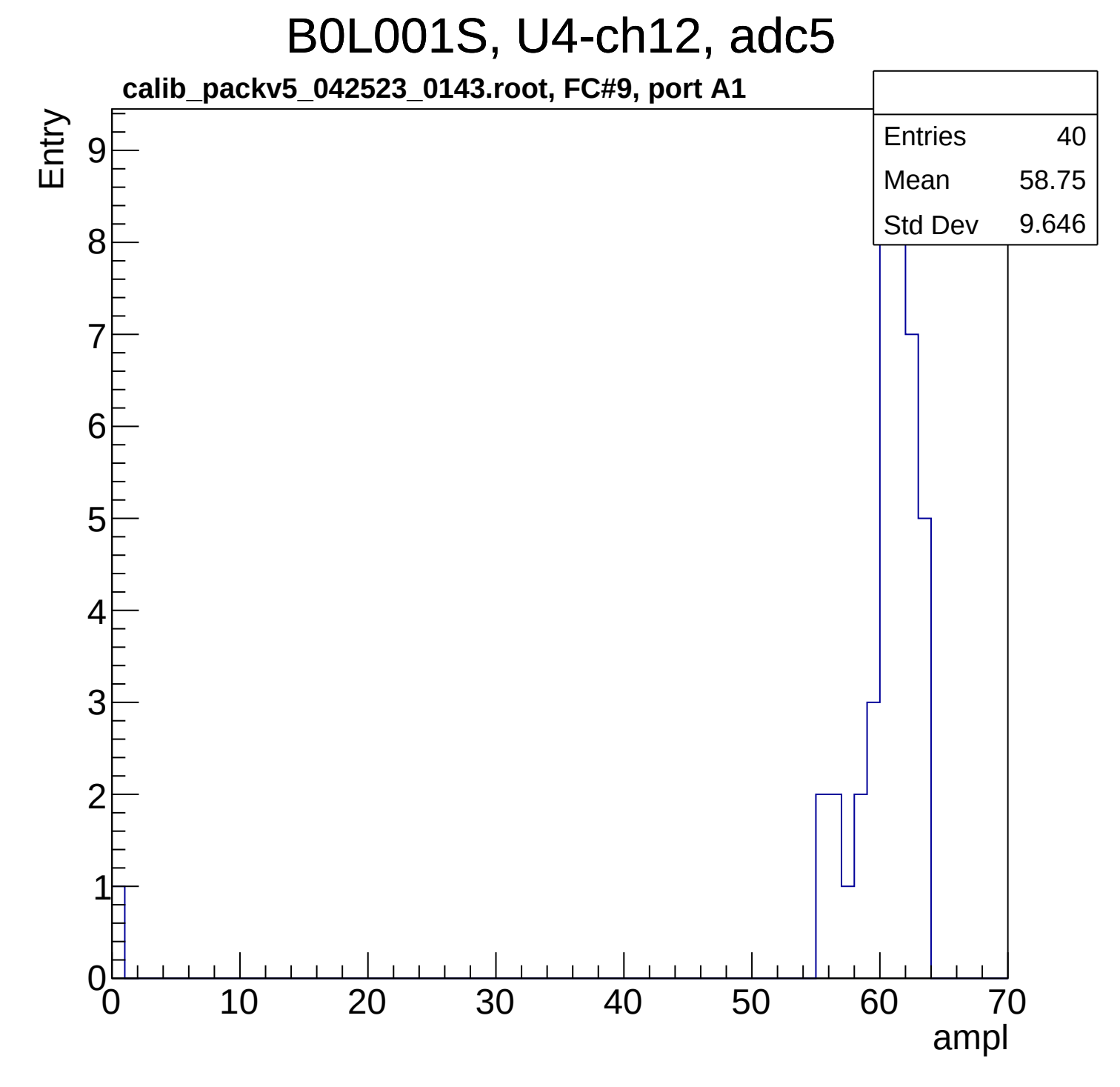
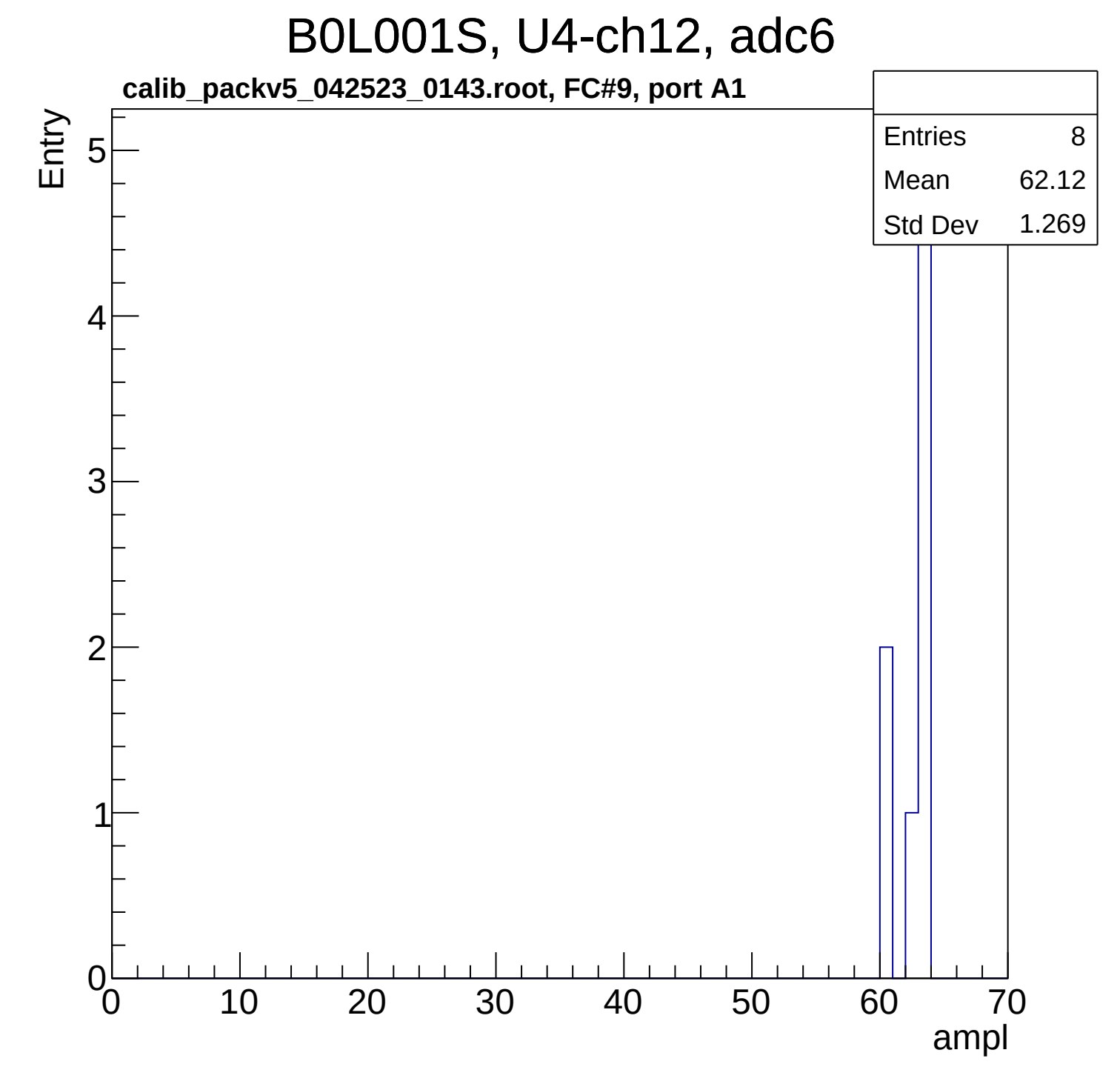
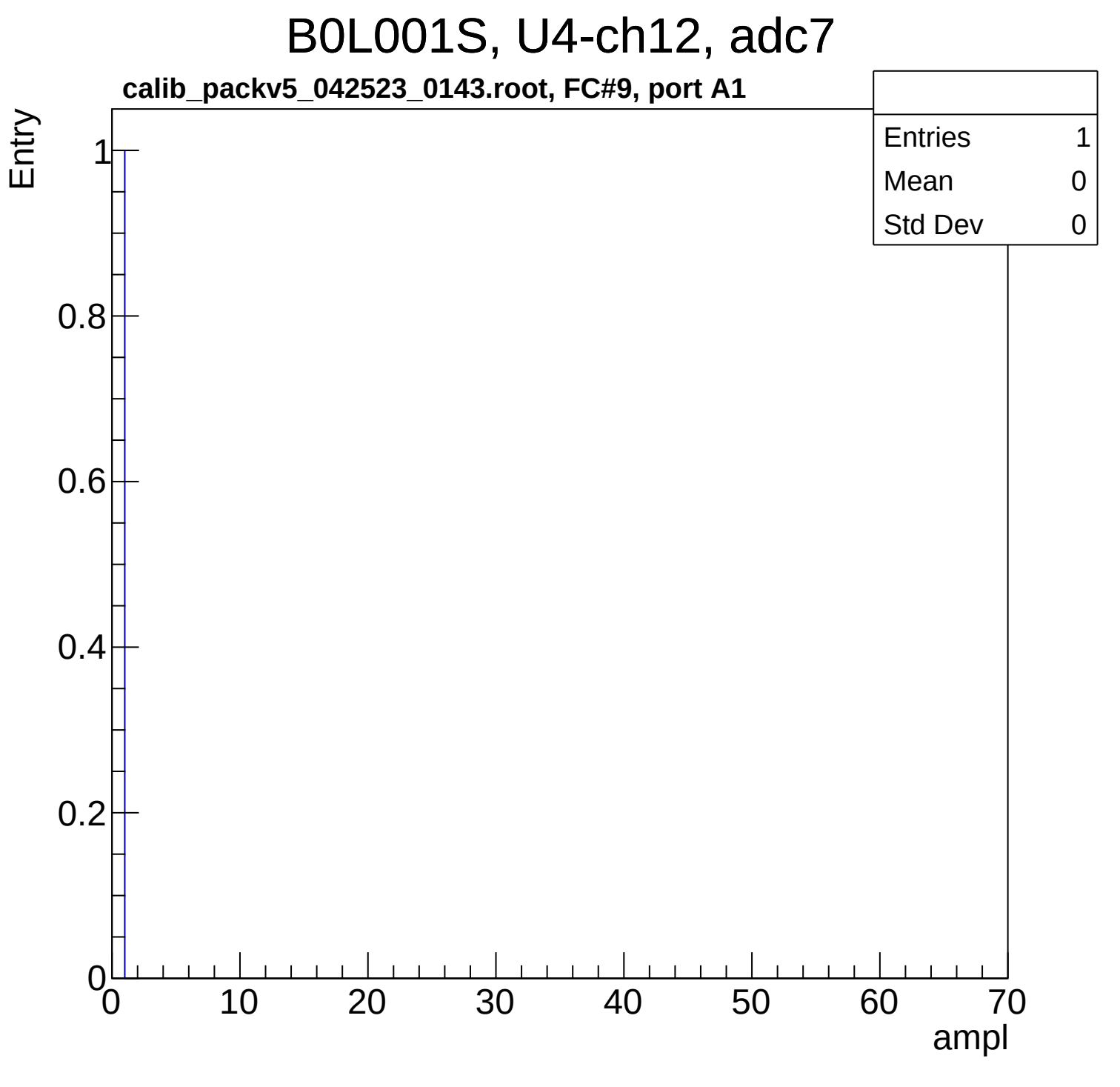
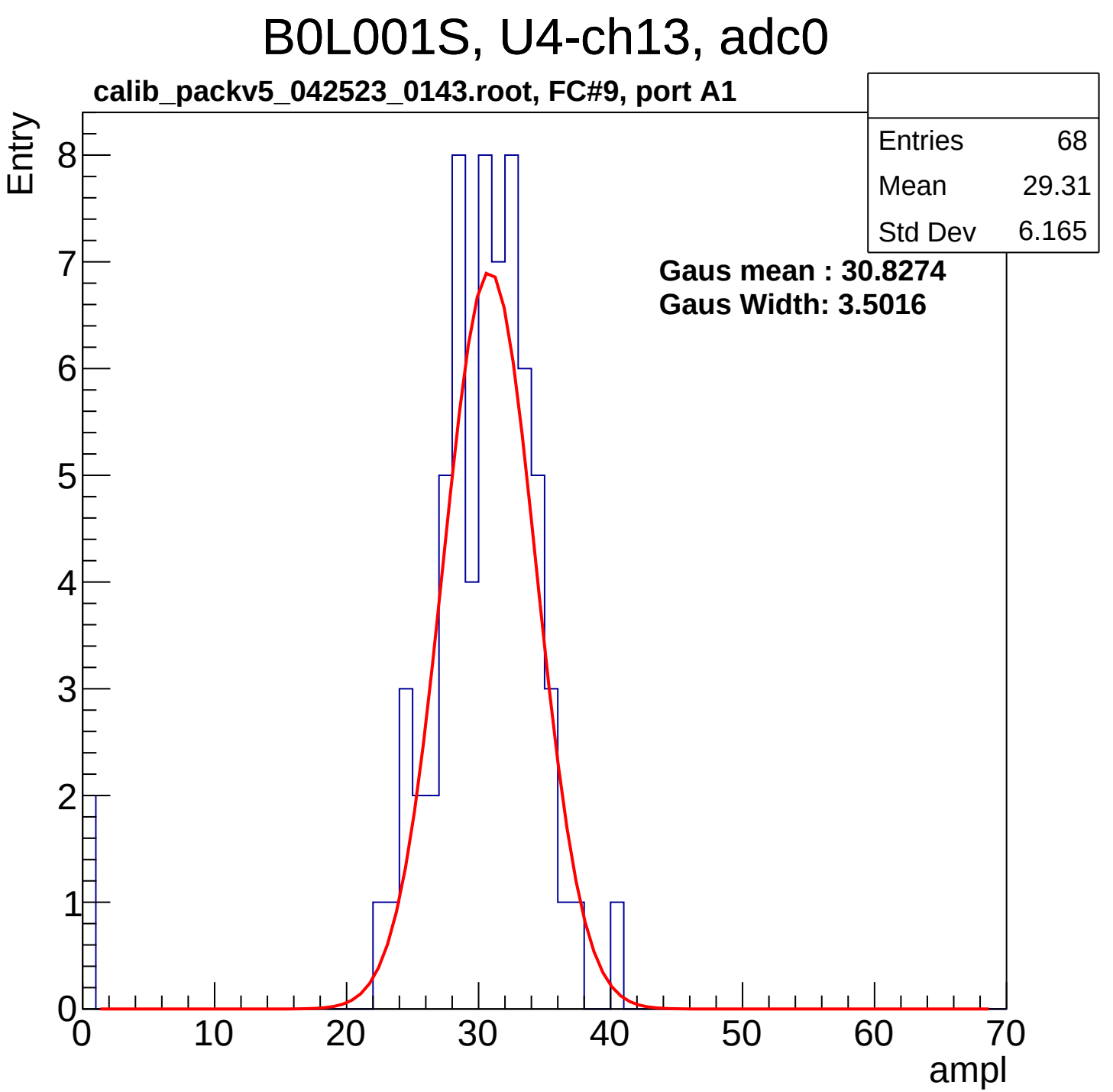

B0L001S, U4-ch12, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 50.35
Std Dev 3.072
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

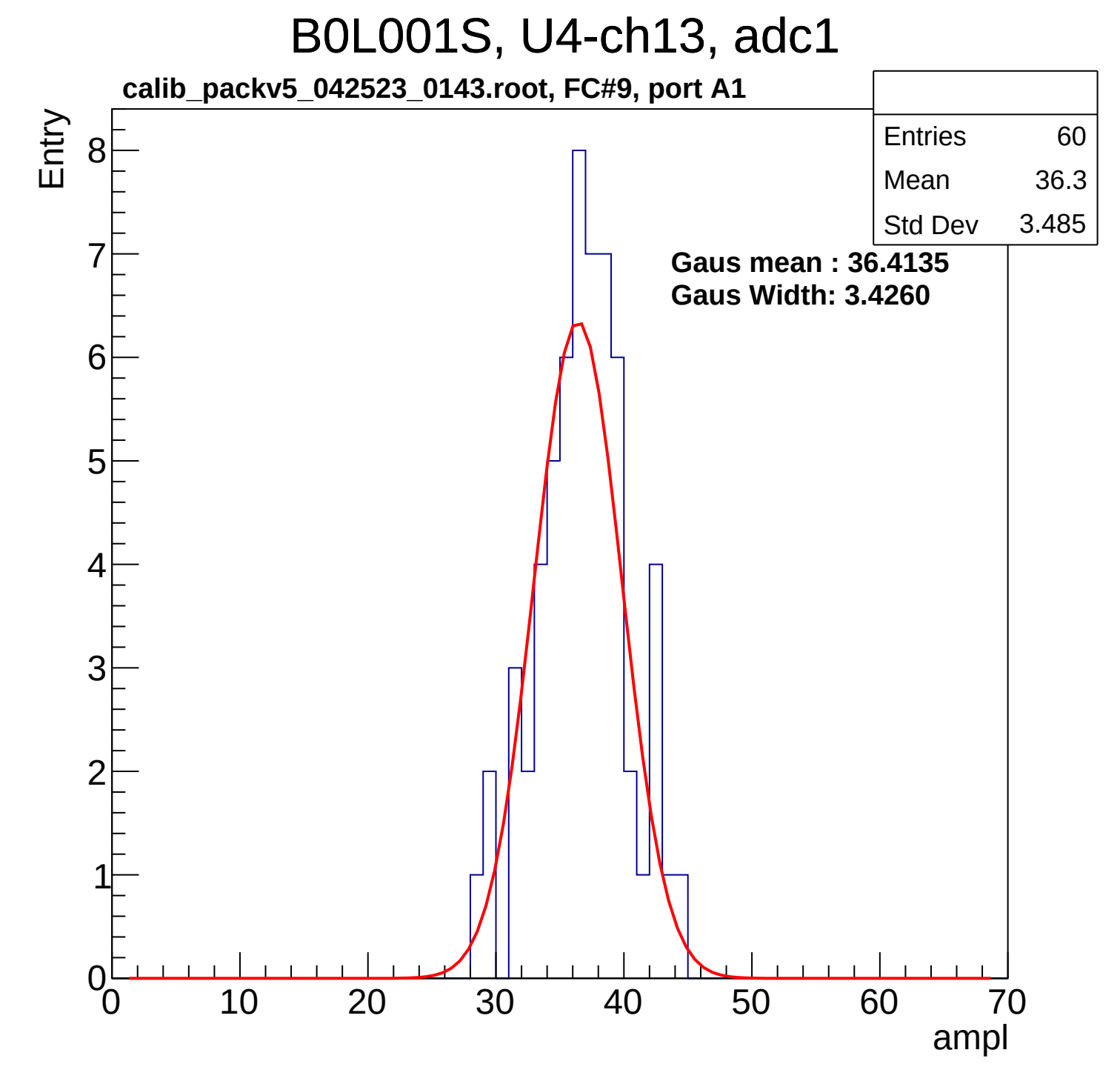

B0L001S, U4-ch12, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.71
Std Dev 2.87
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

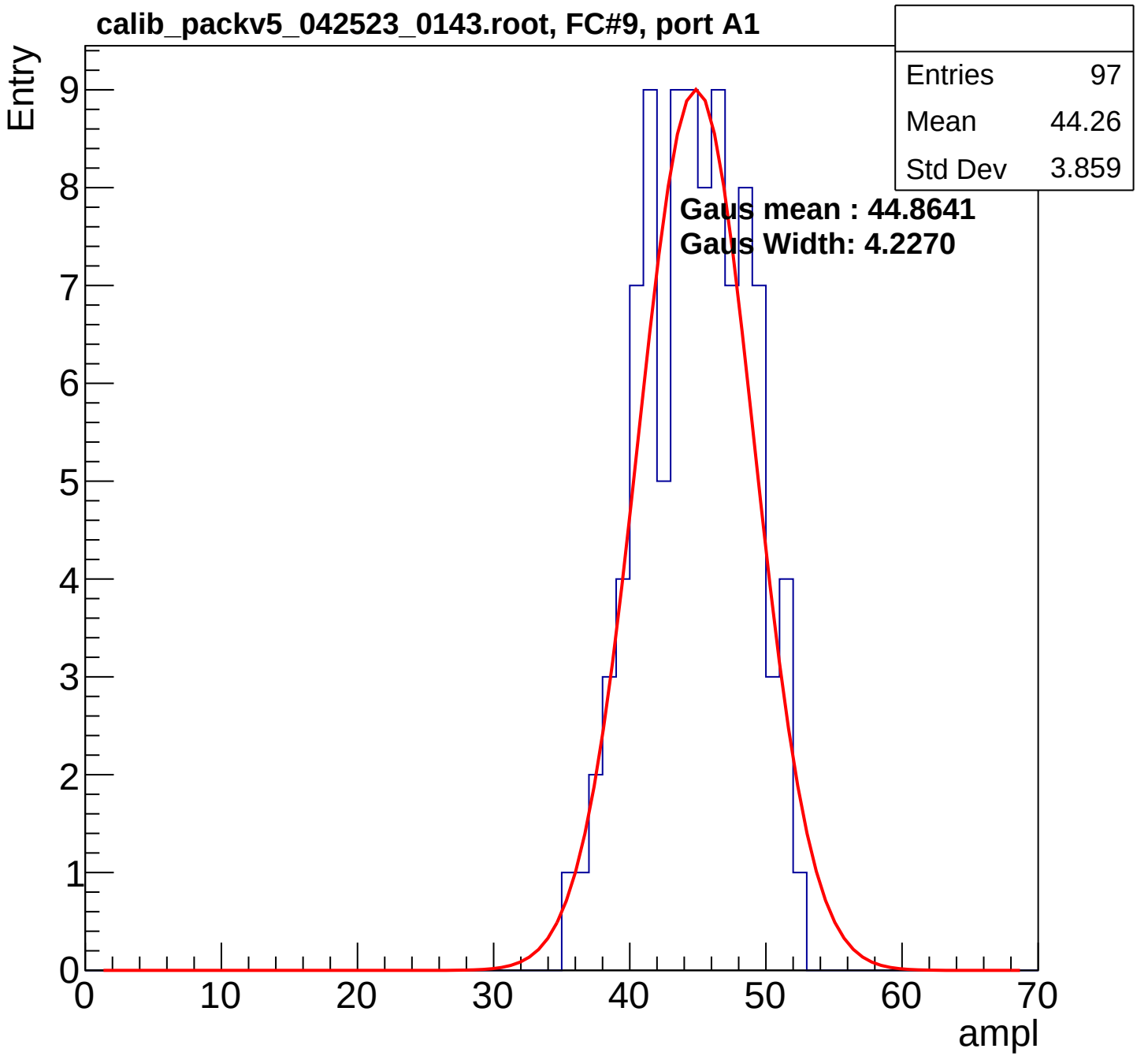

B0L001S, U4-ch12, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.75
Std Dev 9.646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

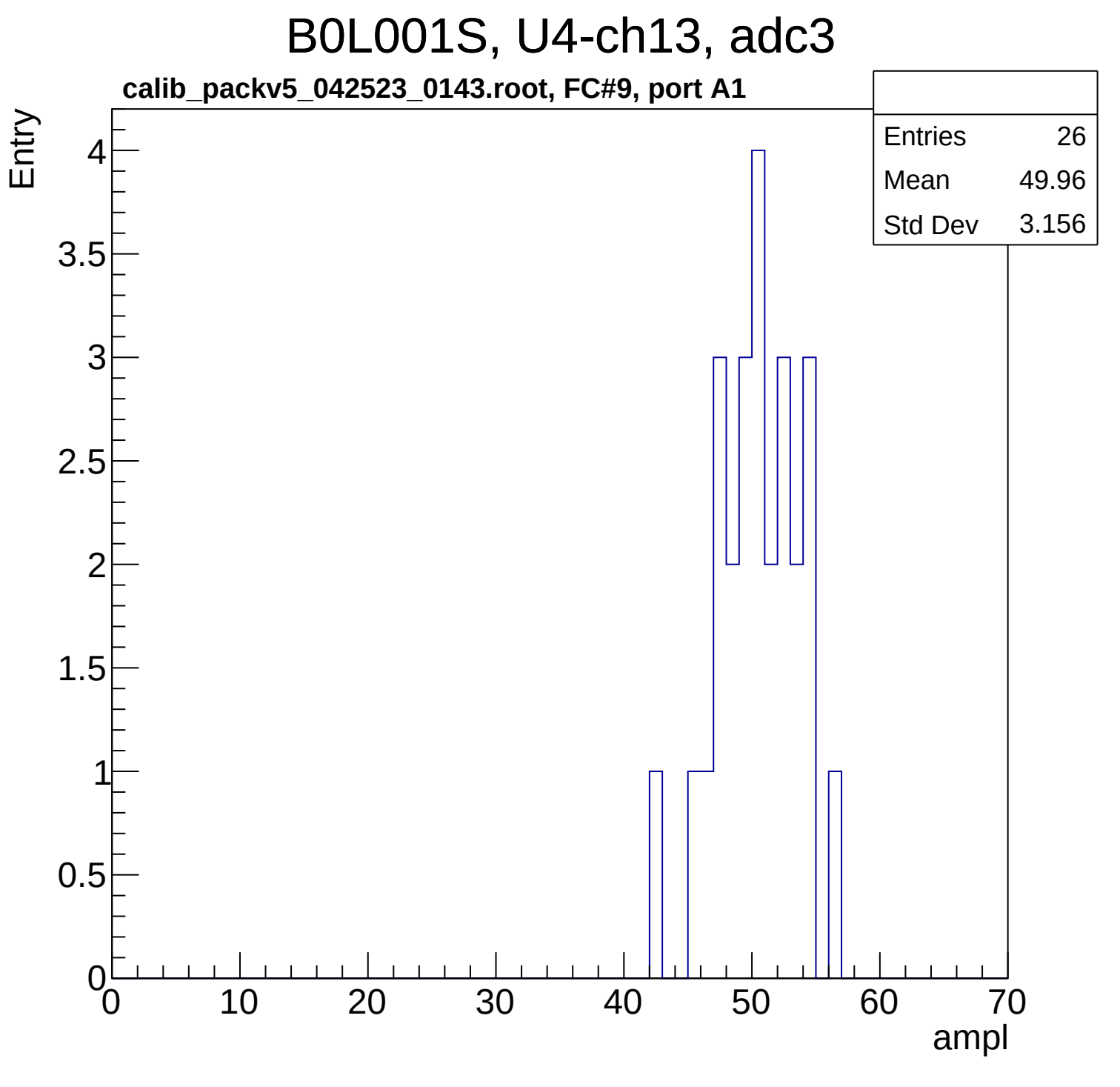

B0L001S, U4-ch12, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62.12
Std Dev 1.269
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch12, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch13, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 29.31
Std Dev 6.165
Gaus mean : 30.8274
Gaus Width: 3.5016
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch13, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 36.3
Std Dev 3.485
Gaus mean : 36.4135
Gaus Width: 3.4260
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch13, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 97
Mean 44.26
Std Dev 3.859
Gaus mean : 44.8641
Gaus Width: 4.2270
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch13, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 49.96
Std Dev 3.156
Entry
ampl
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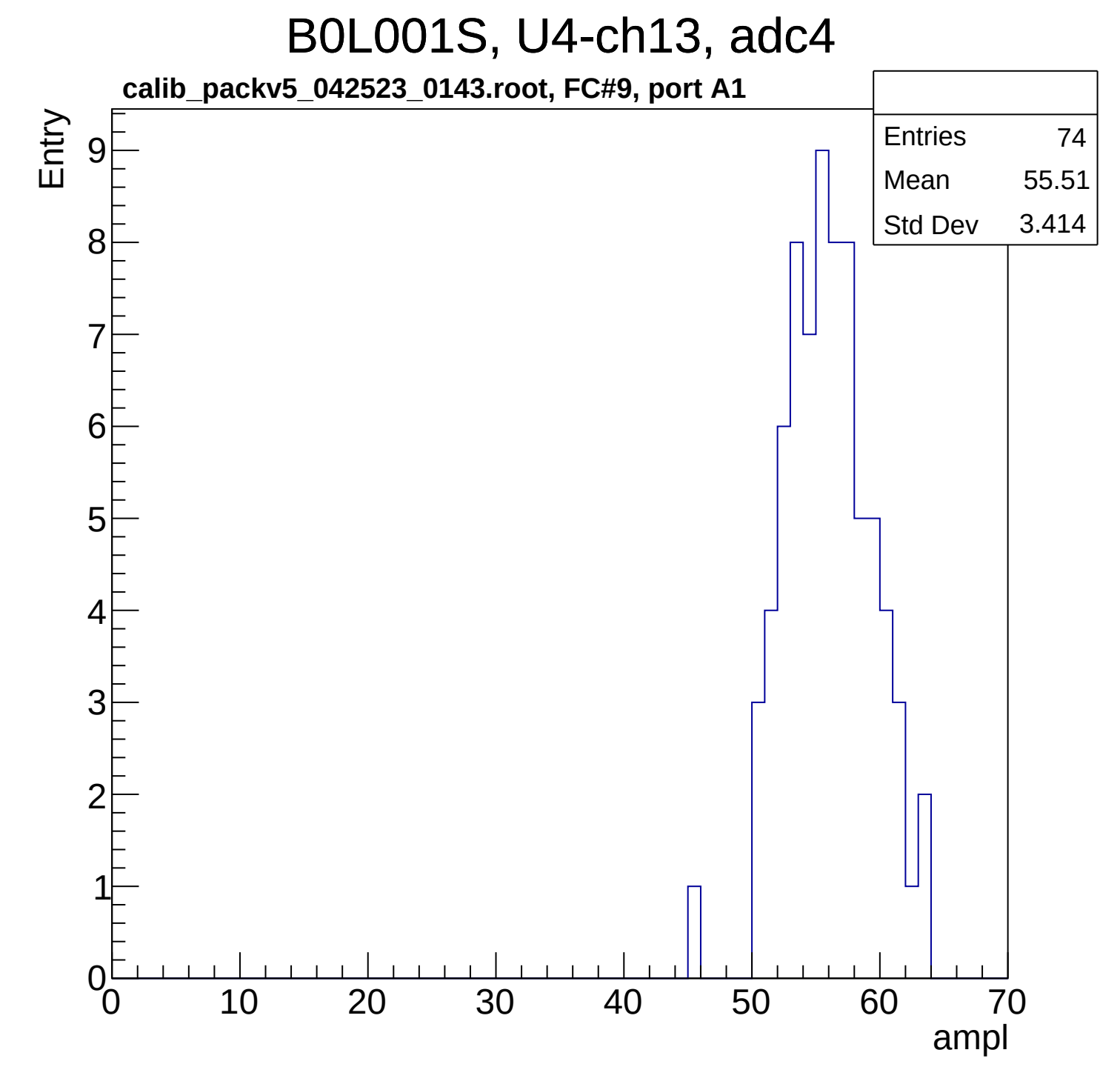
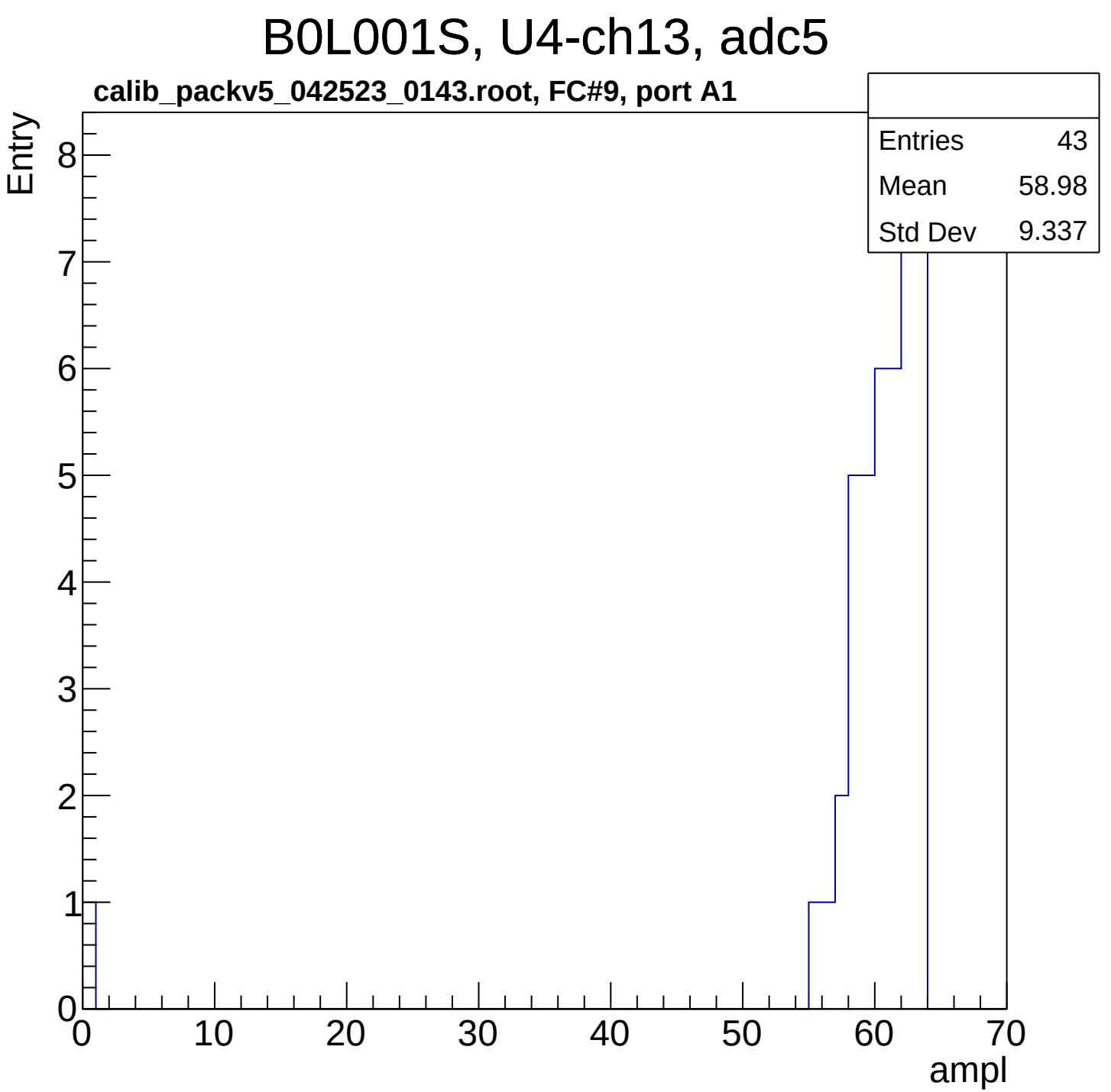
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch13, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 55.51
Std Dev 3.414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch13, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.98
Std Dev 9.337
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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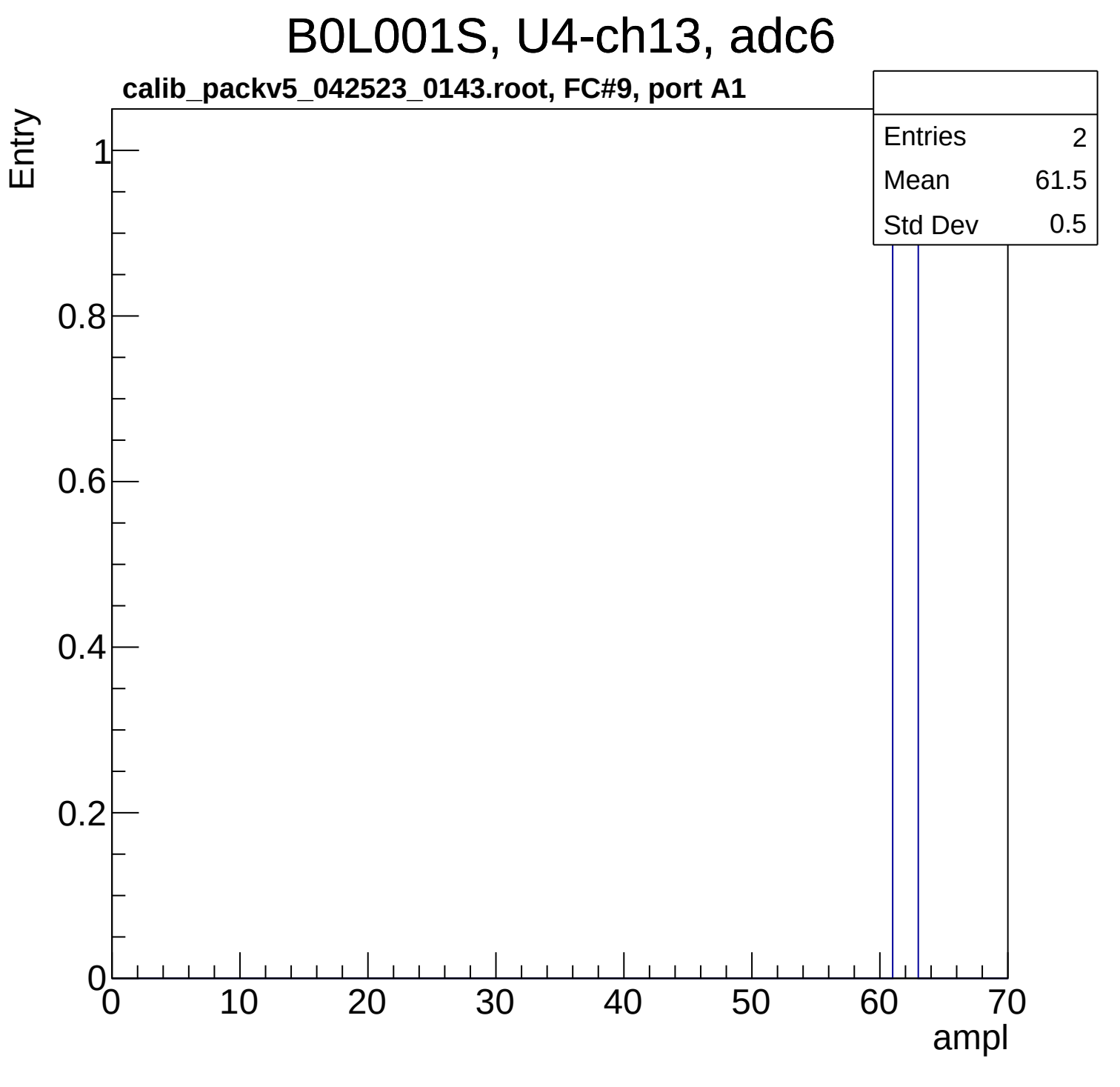
B0L001S, U4-ch13, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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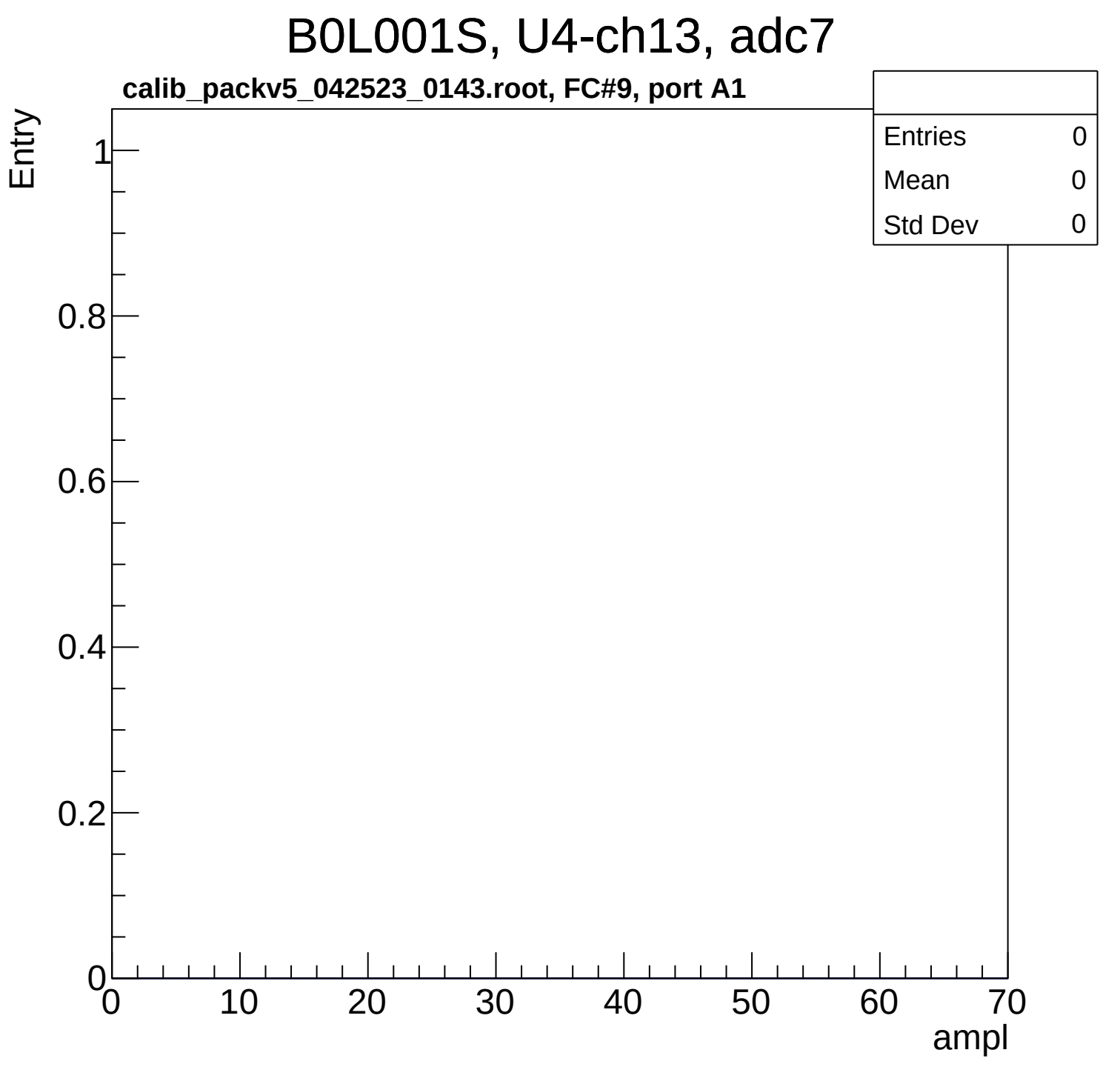
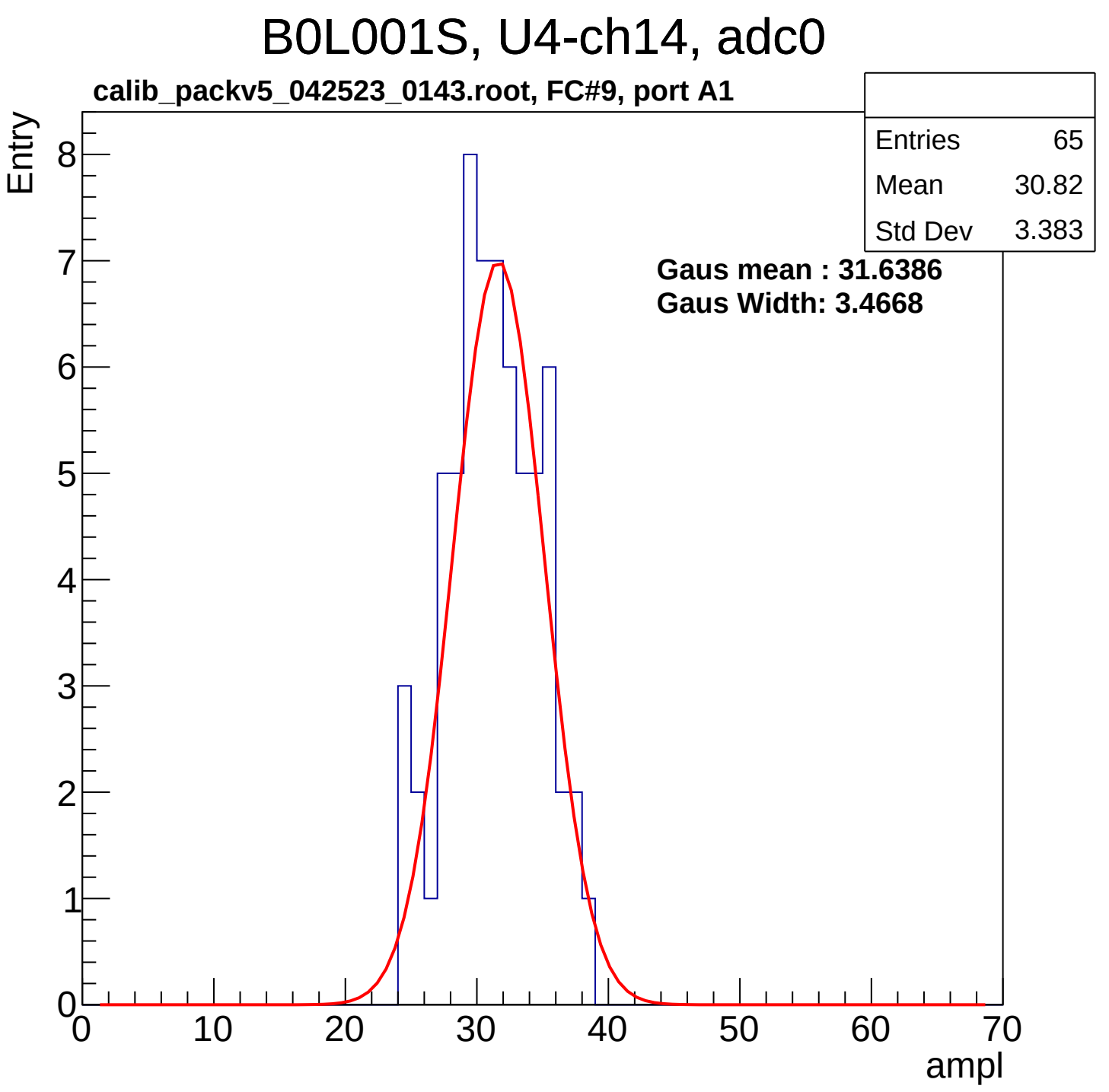
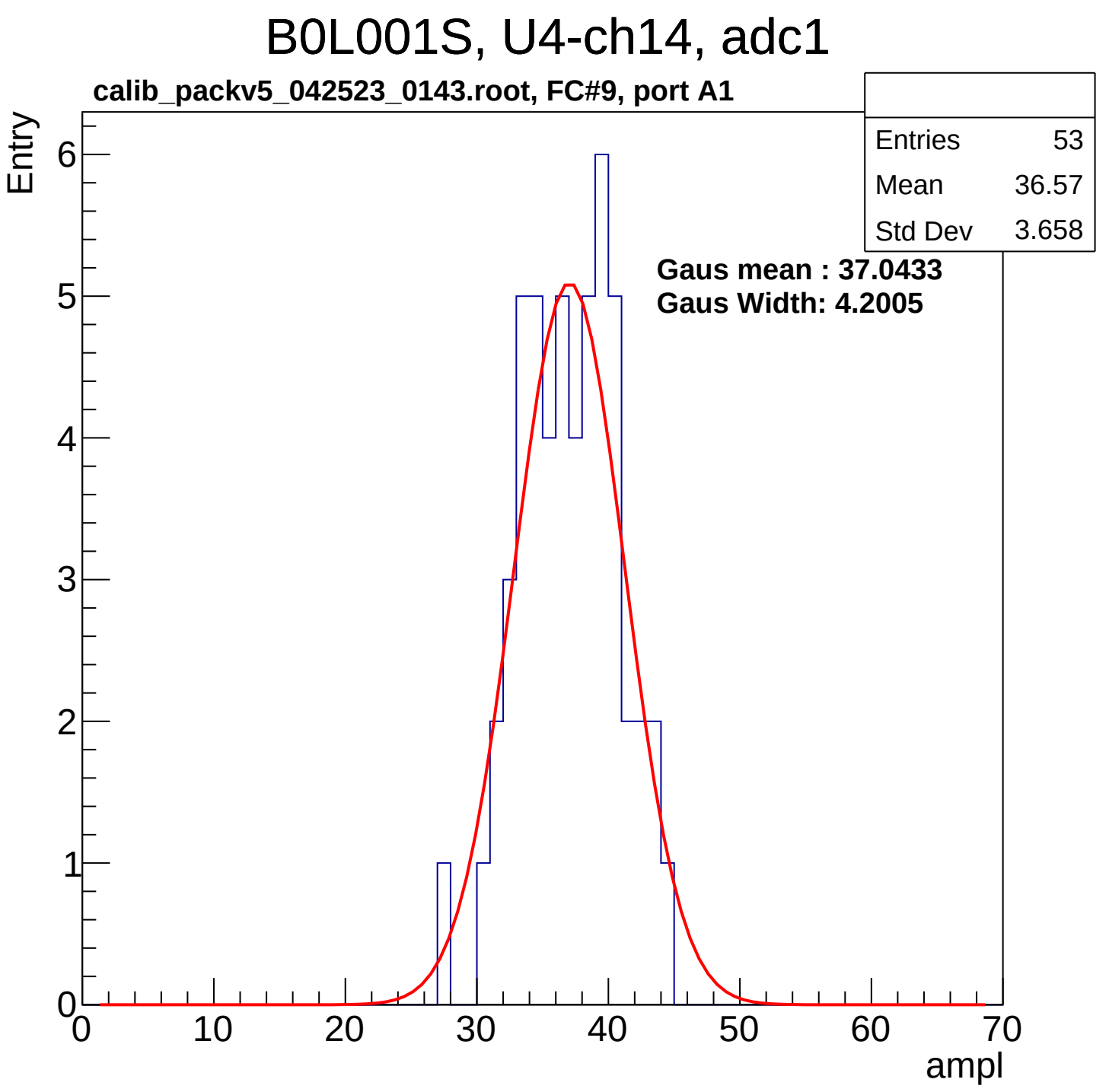
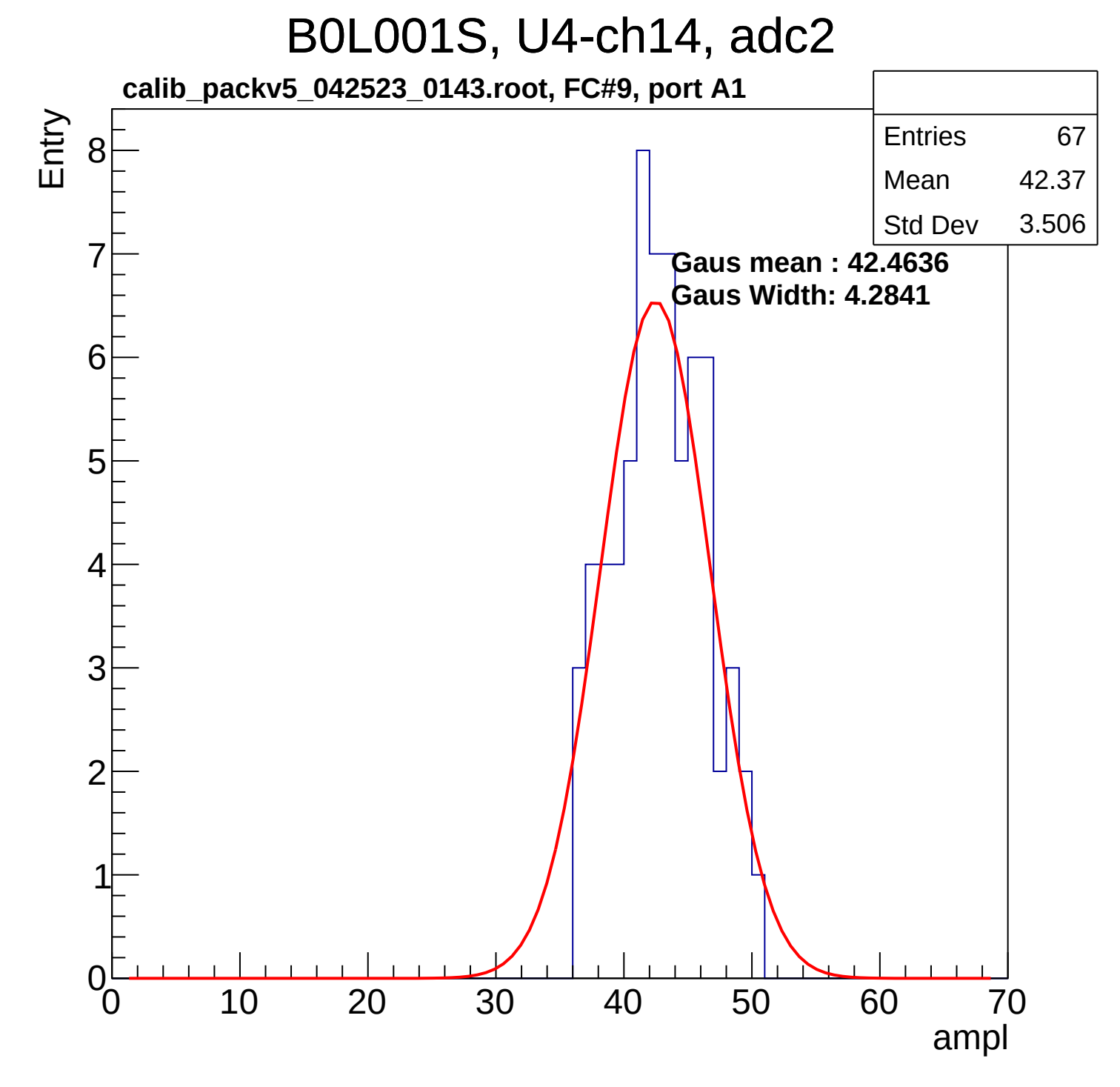
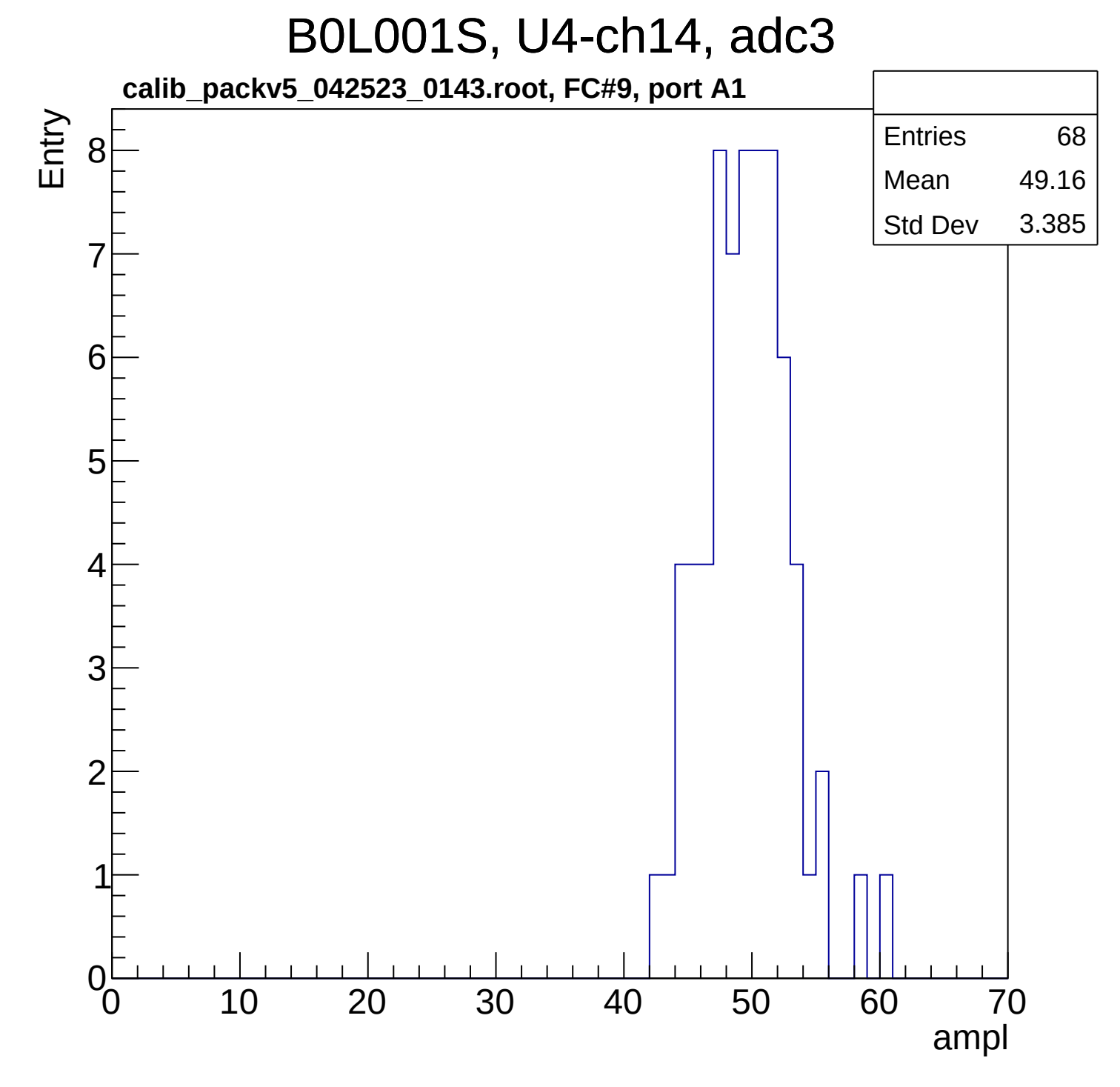
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch13, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch14, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.82
Std Dev 3.383
Gaus mean : 31.6386
Gaus Width: 3.4668
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch14, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 36.57
Std Dev 3.658
Gaus mean : 37.0433
Gaus Width: 4.2005
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U4-ch14, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 42.37
Std Dev 3.506
Gaus mean : 42.4636
Gaus Width: 4.2841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch14, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 49.16
Std Dev 3.385
Entry
ampl
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1
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3
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5
6
7
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0
10
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40
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60
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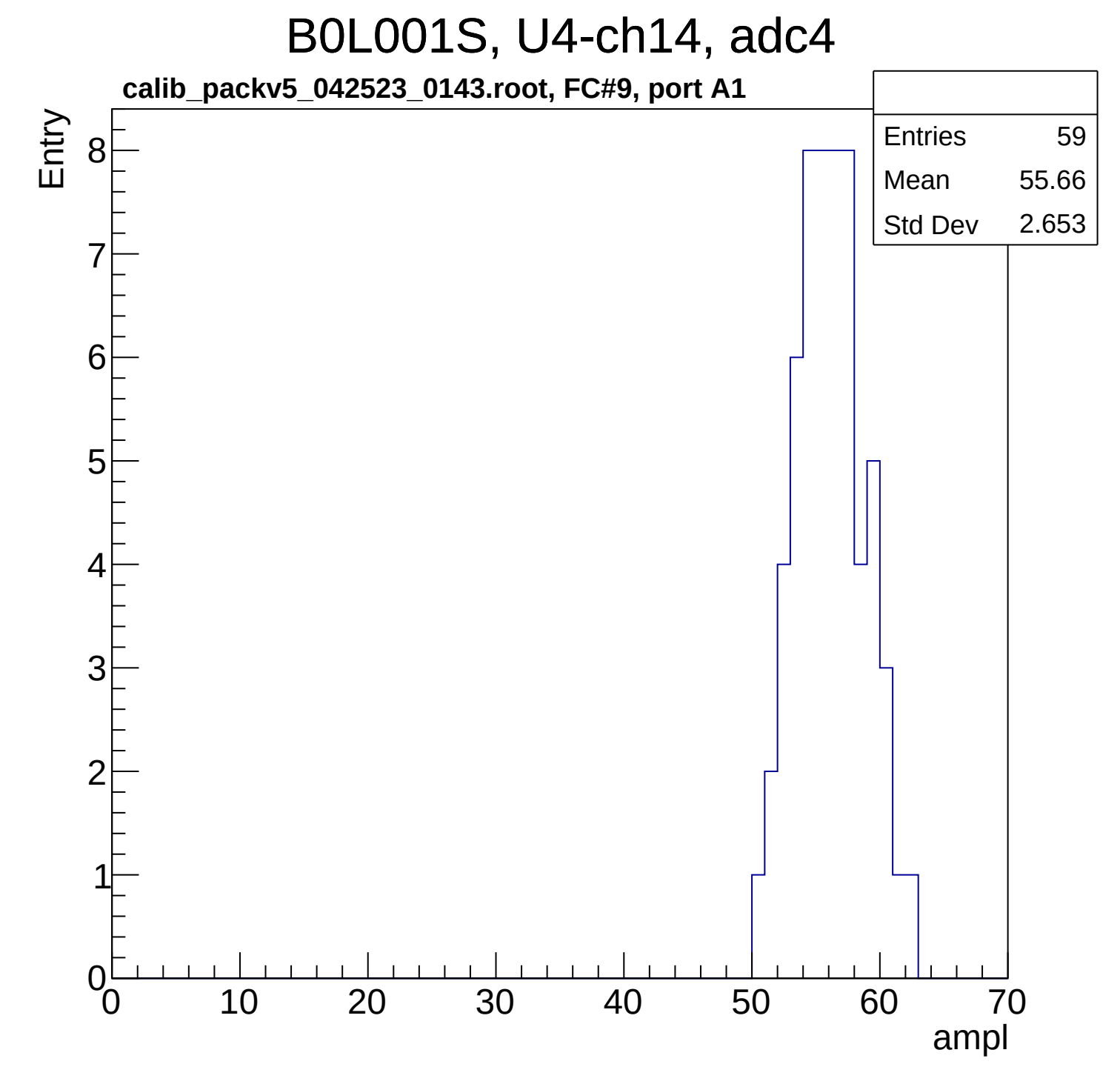
B0L001S, U4-ch14, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.66
Std Dev 2.653
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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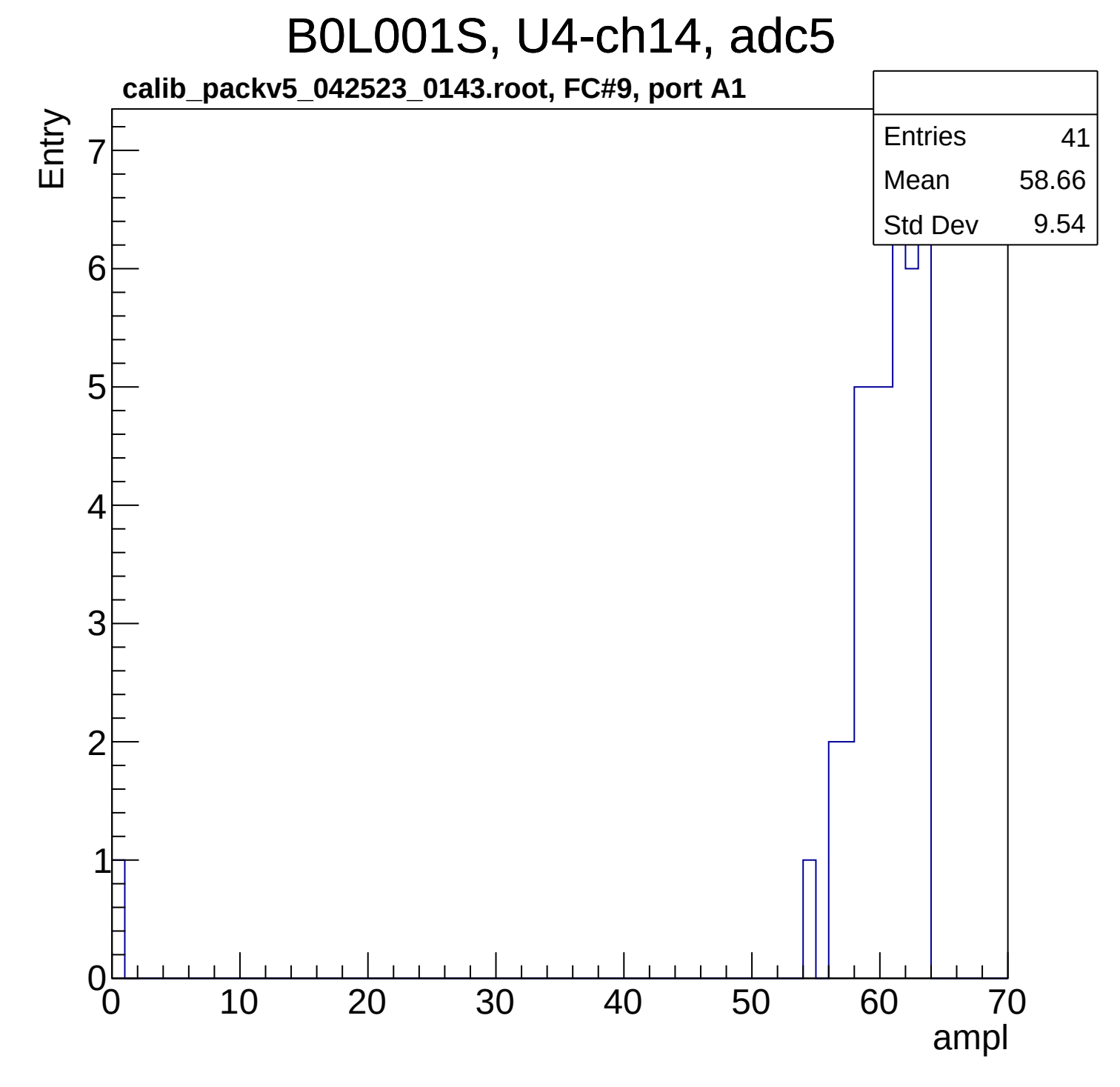
B0L001S, U4-ch14, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.66
Std Dev 9.54
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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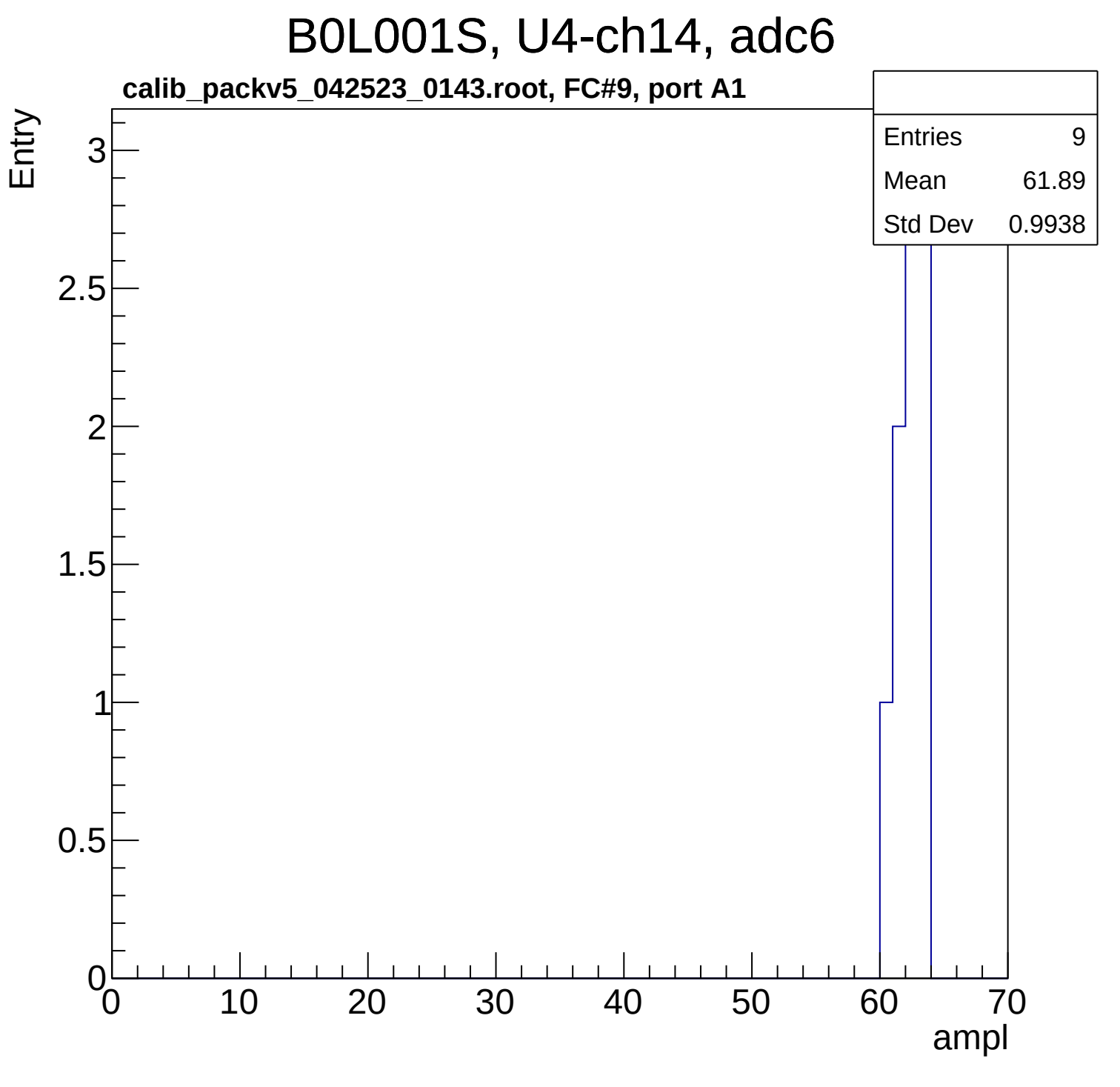
B0L001S, U4-ch14, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.89
Std Dev 0.9938
Entry
ampl
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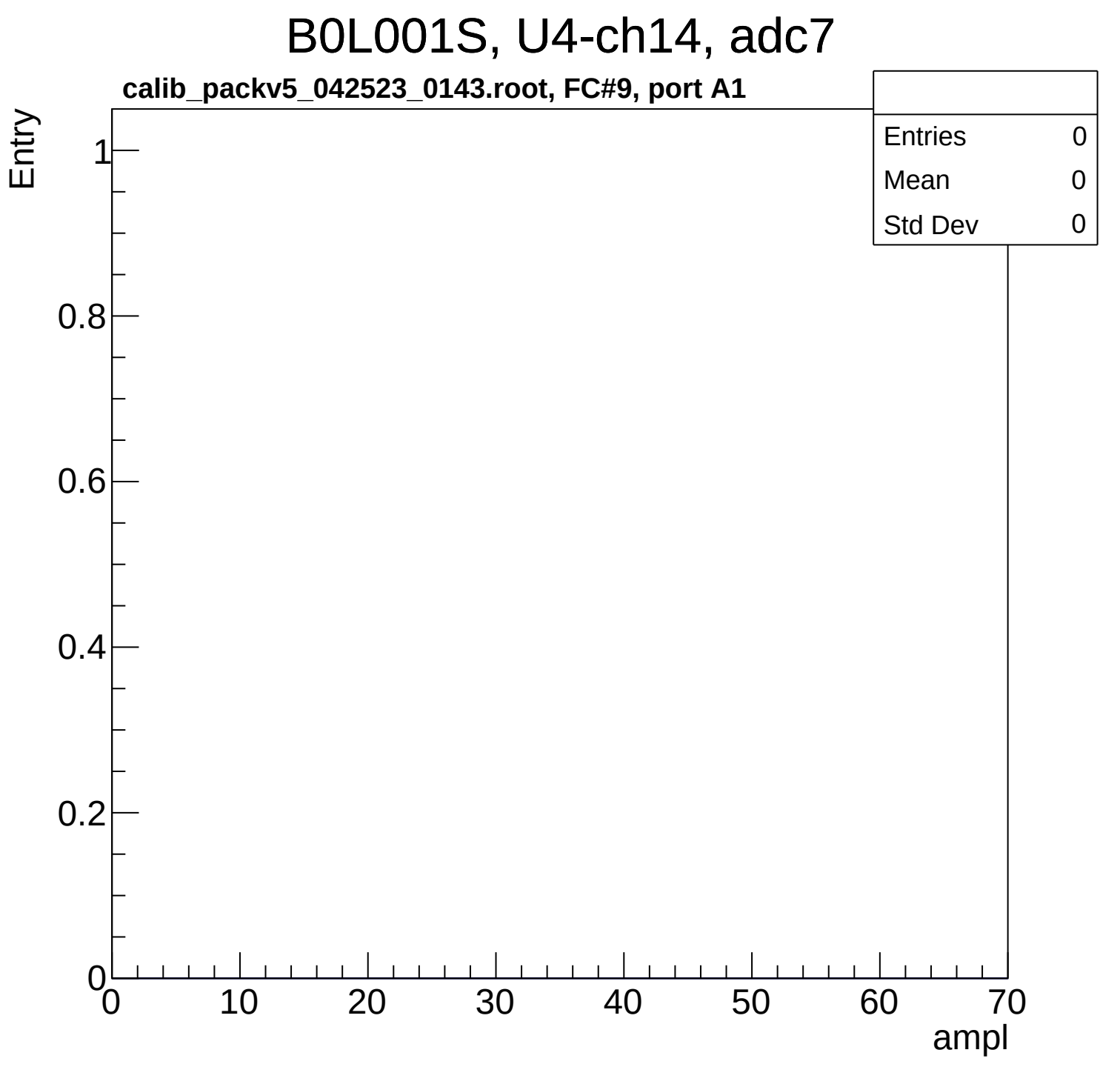
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U4-ch14, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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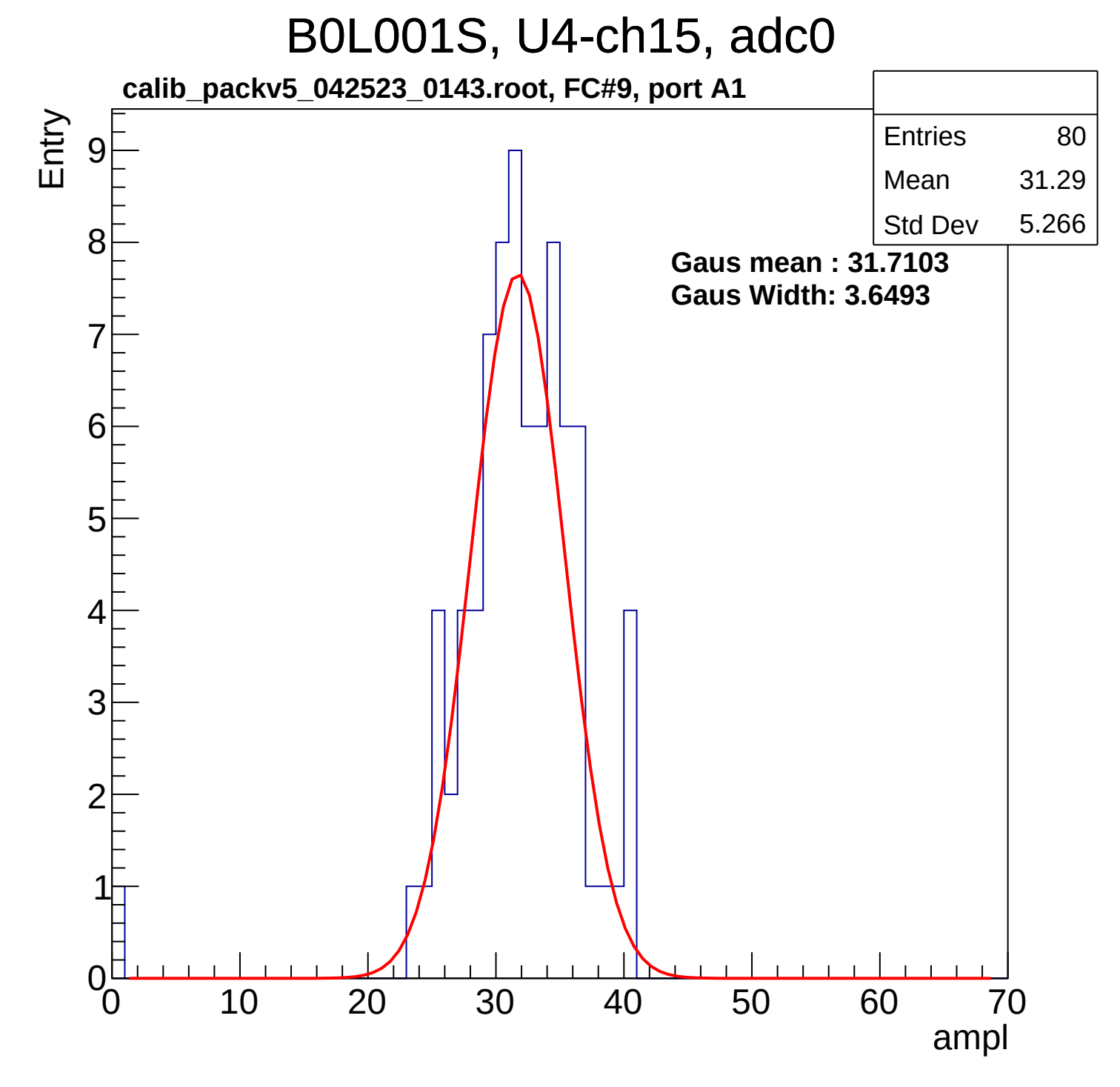
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch15, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 31.29
Std Dev 5.266
Gaus mean : 31.7103
Gaus Width: 3.6493
Entry
ampl
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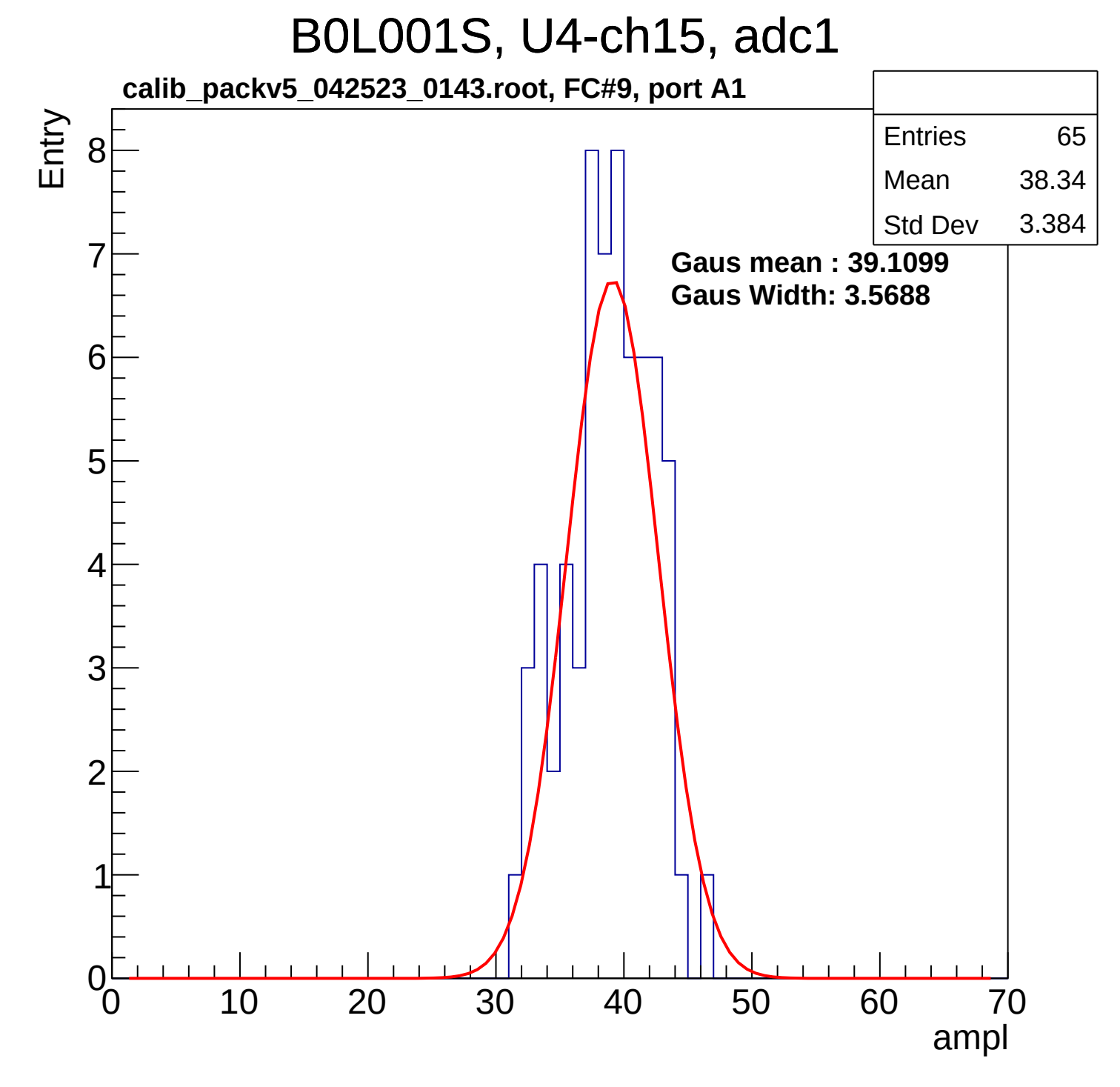
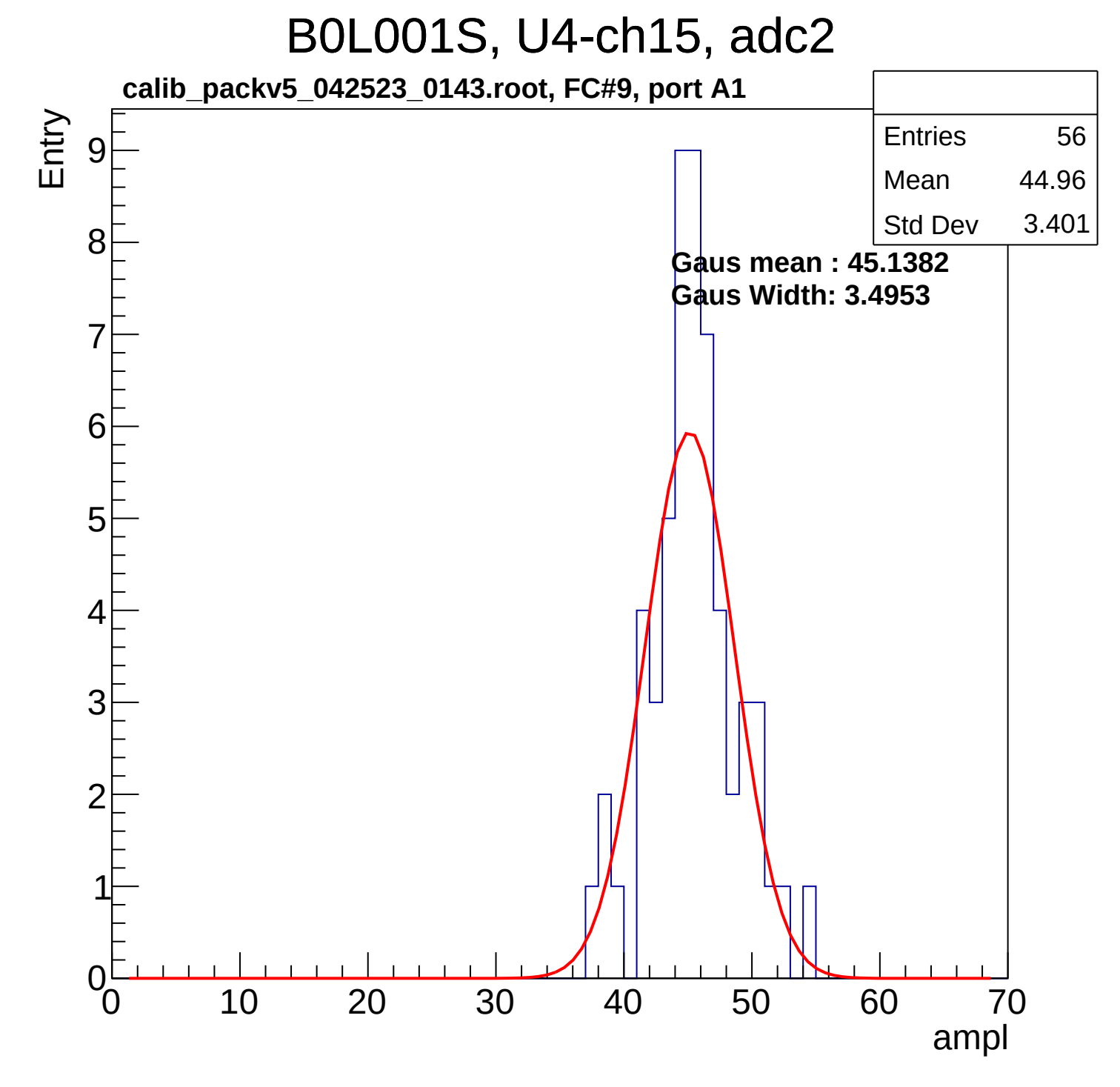
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch15, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.34
Std Dev 3.384
Gaus mean : 39.1099
Gaus Width: 3.5688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch15, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.96
Std Dev 3.401
Gaus mean : 45.1382
Gaus Width: 3.4953
Entry
ampl
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2
3
4
5
6
7
8
9
0
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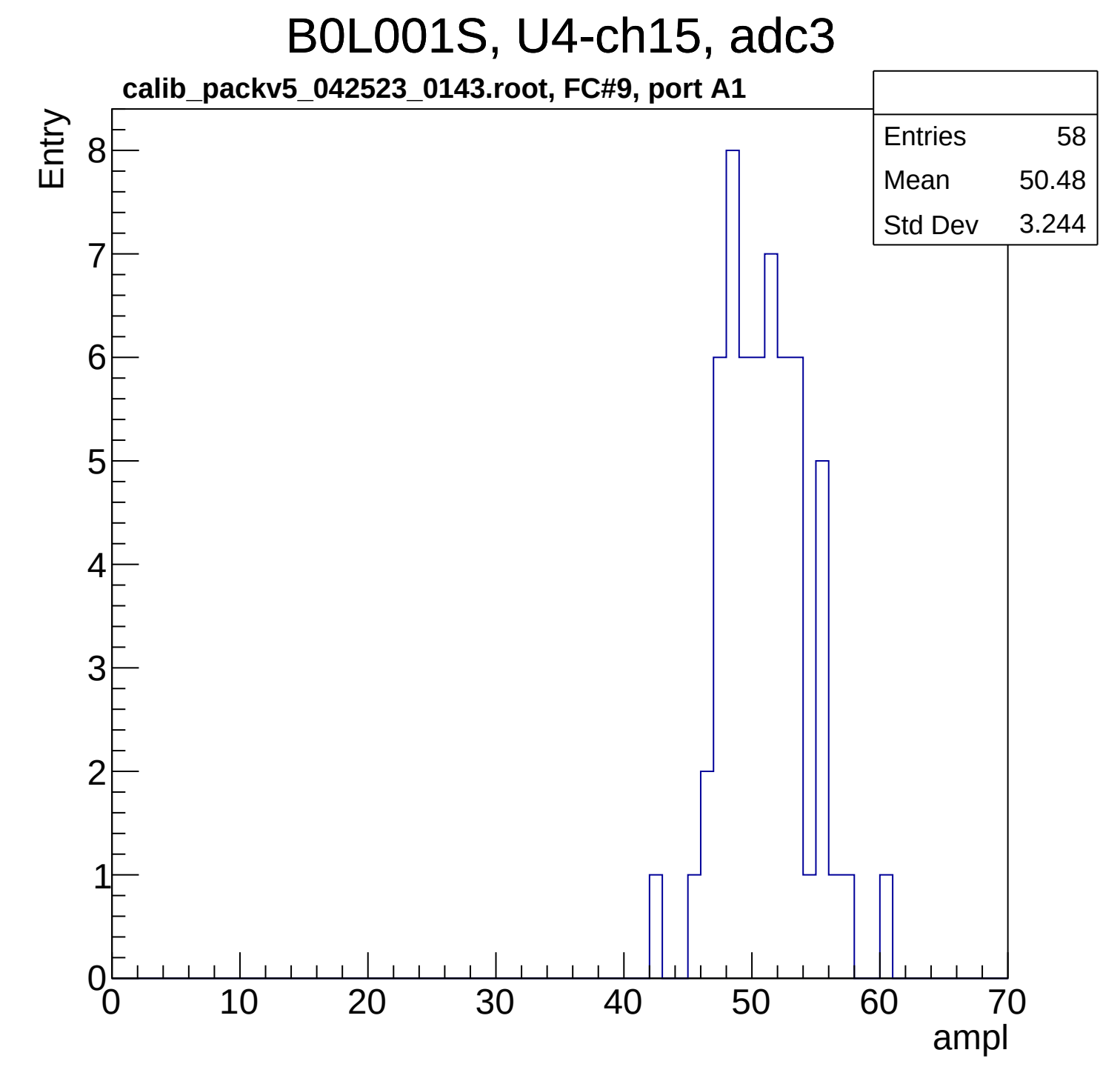
B0L001S, U4-ch15, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 50.48
Std Dev 3.244
Entry
ampl
0
1
2
3
4
5
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7
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0
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20
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40
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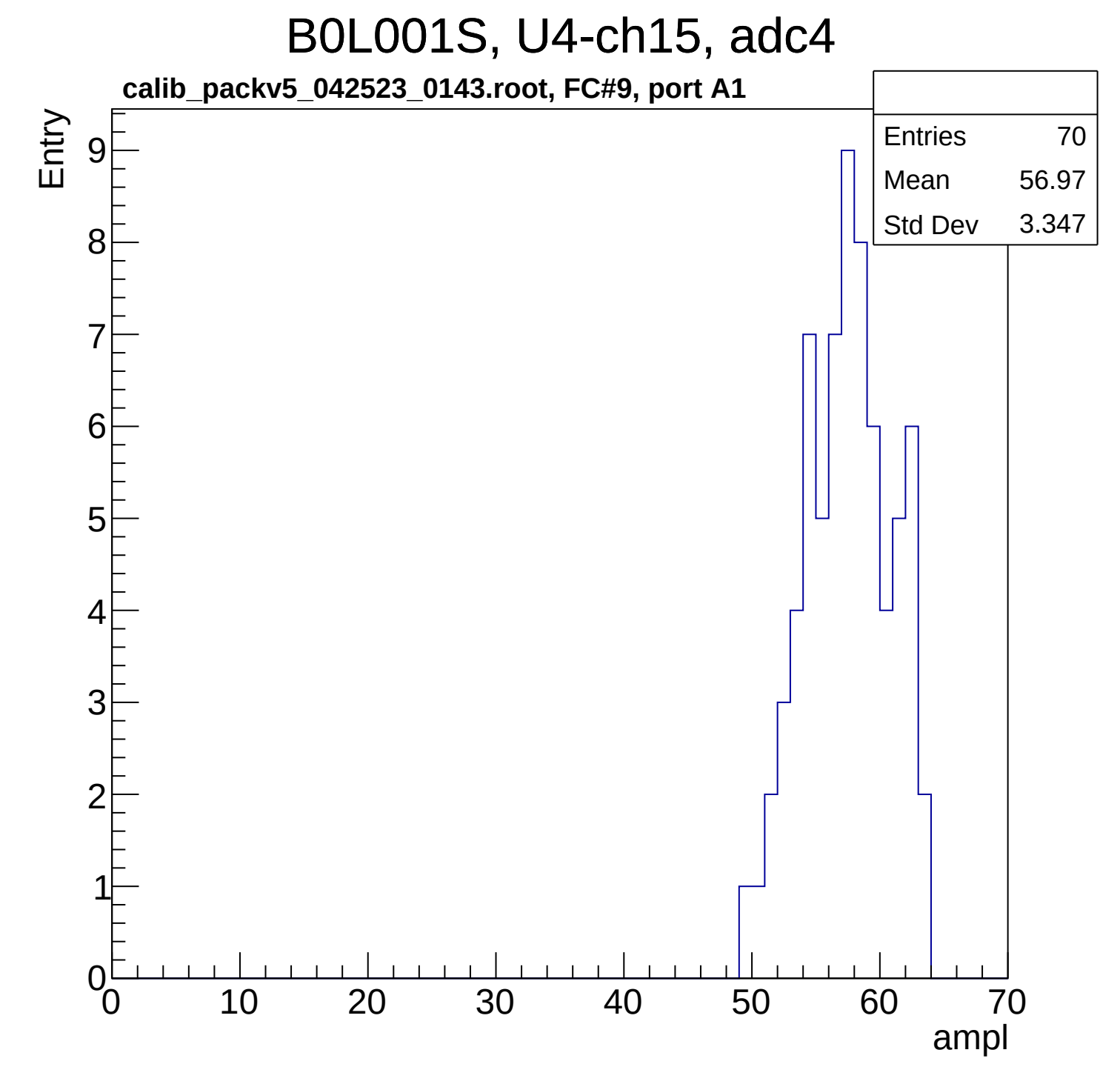
60
70


B0L001S, U4-ch15, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 56.97
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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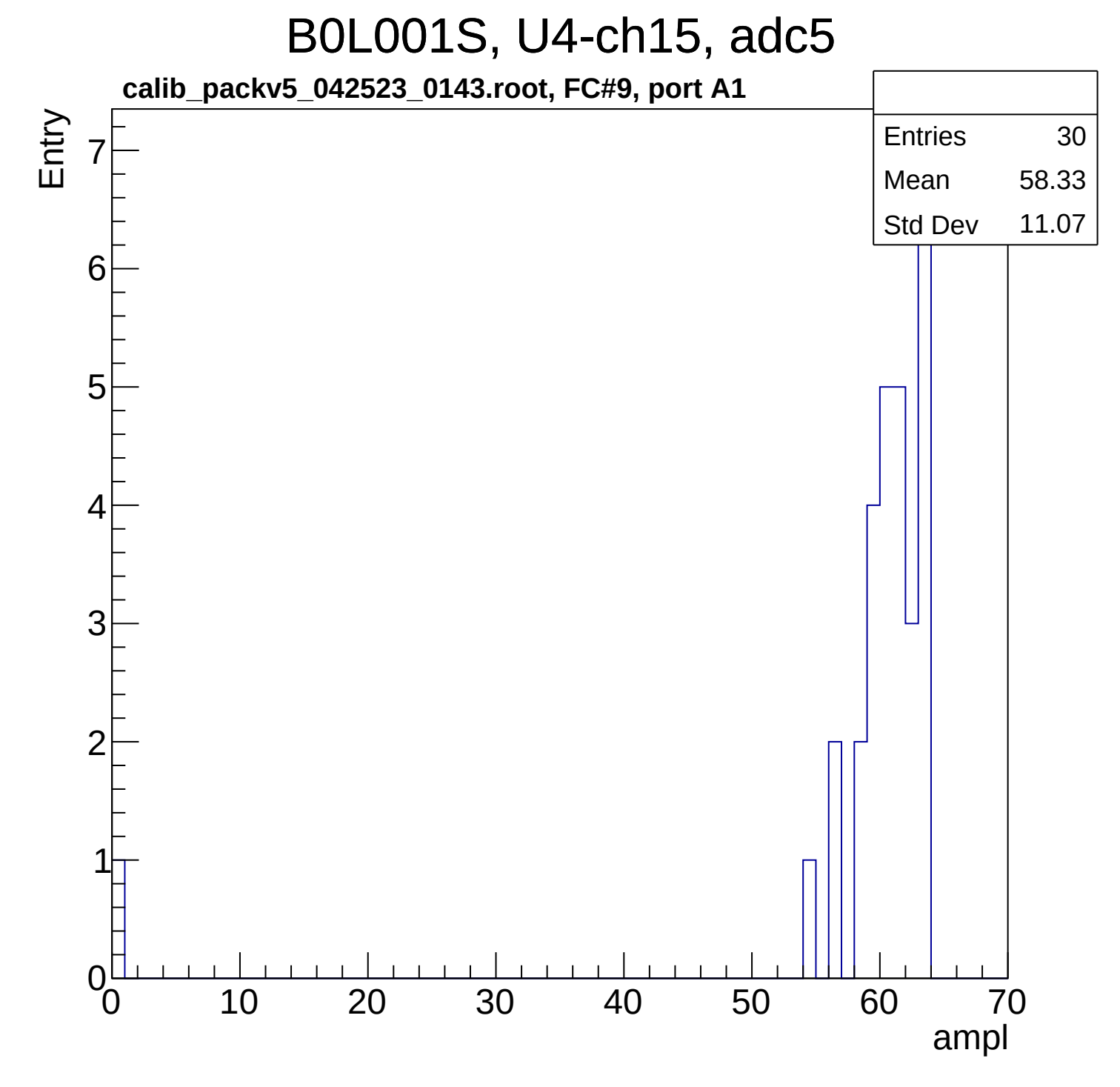
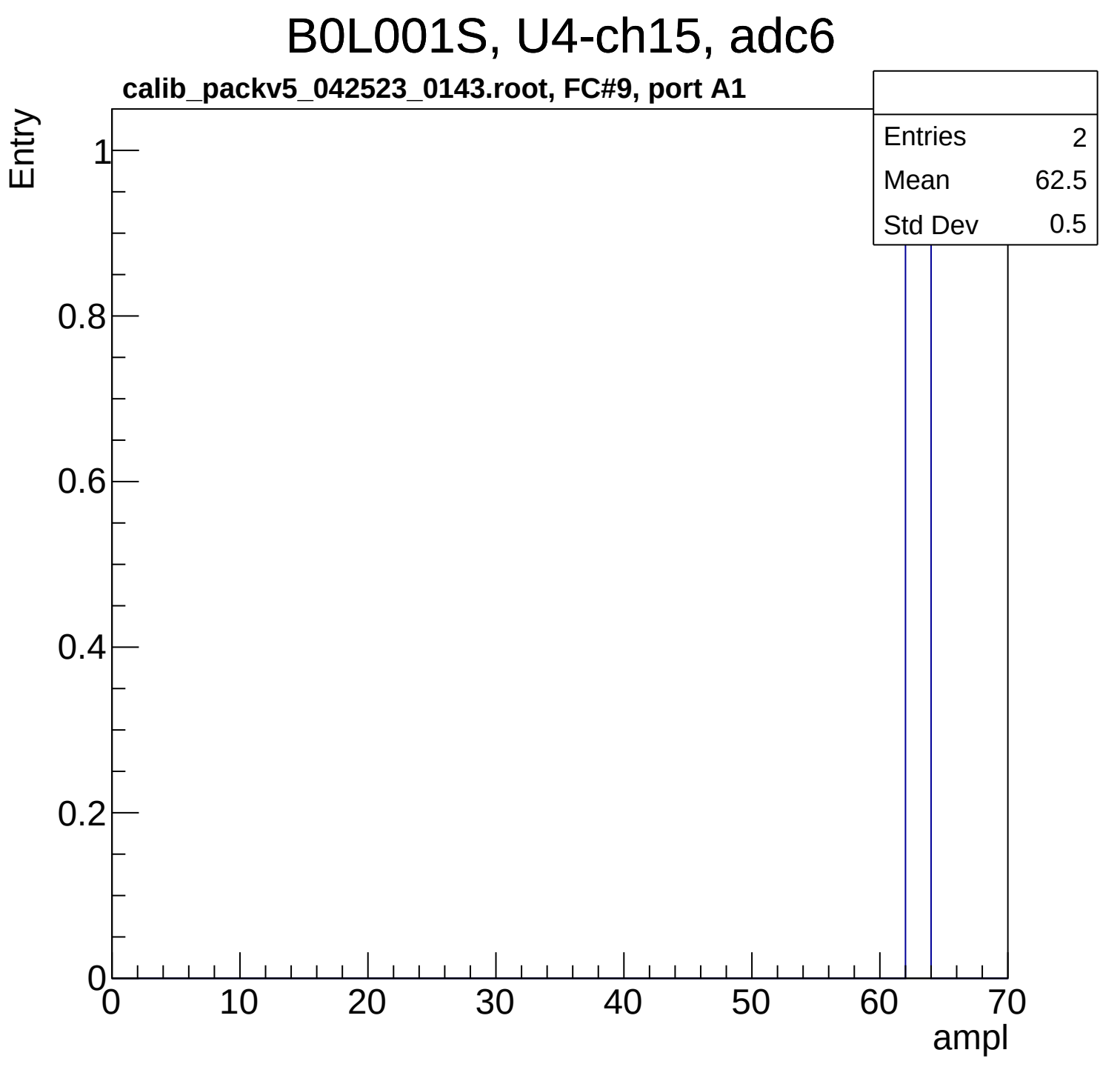
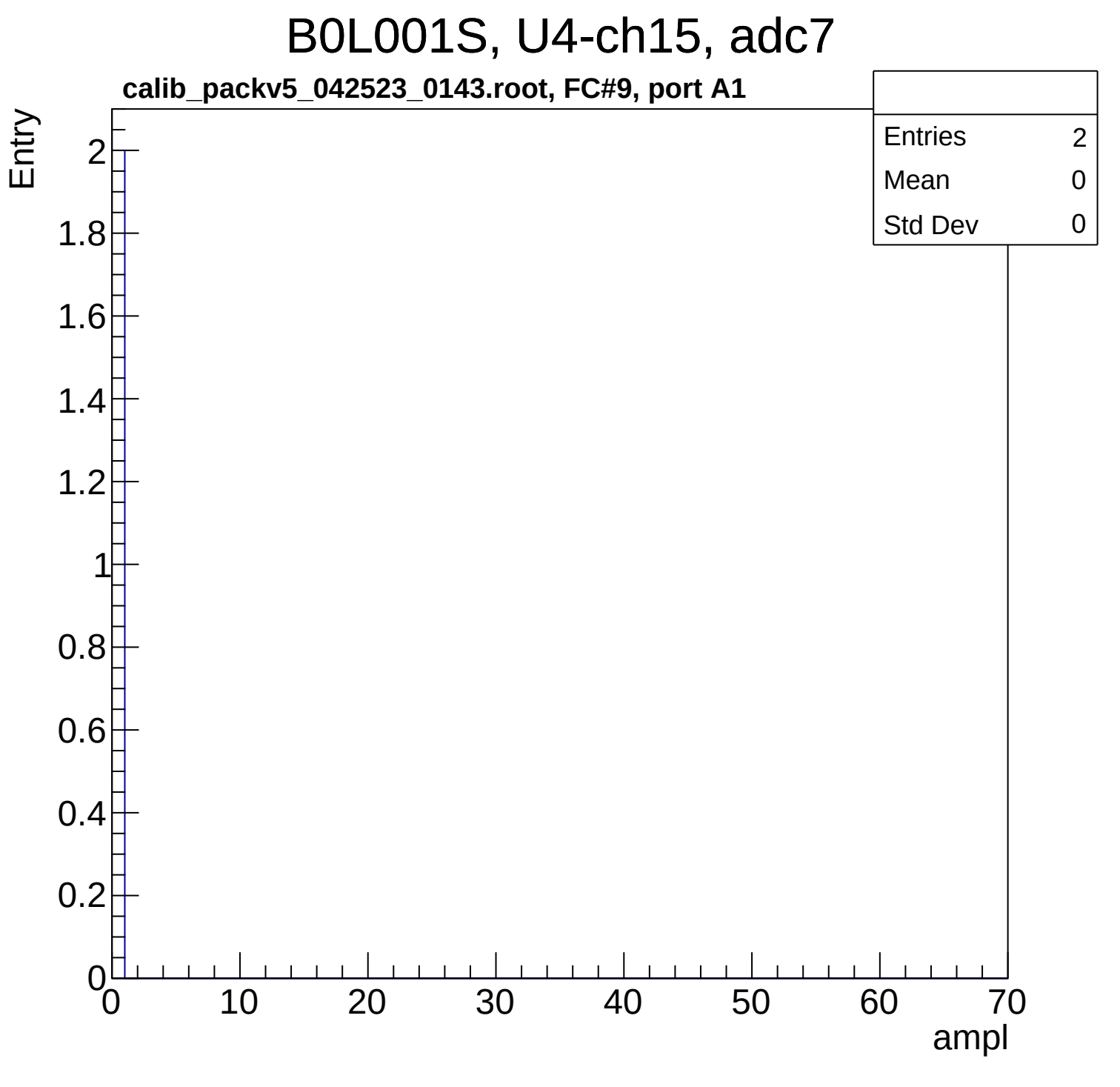
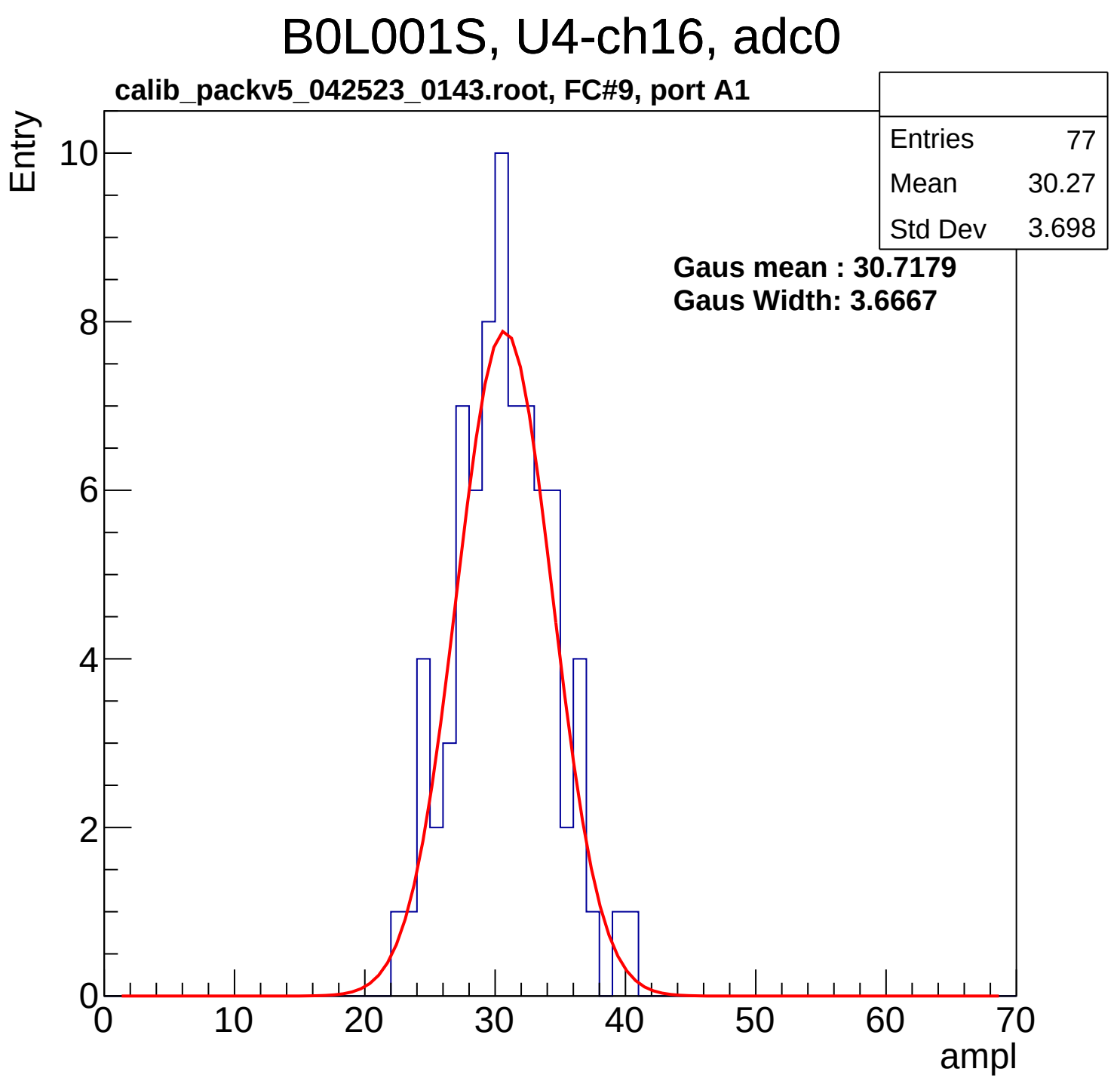
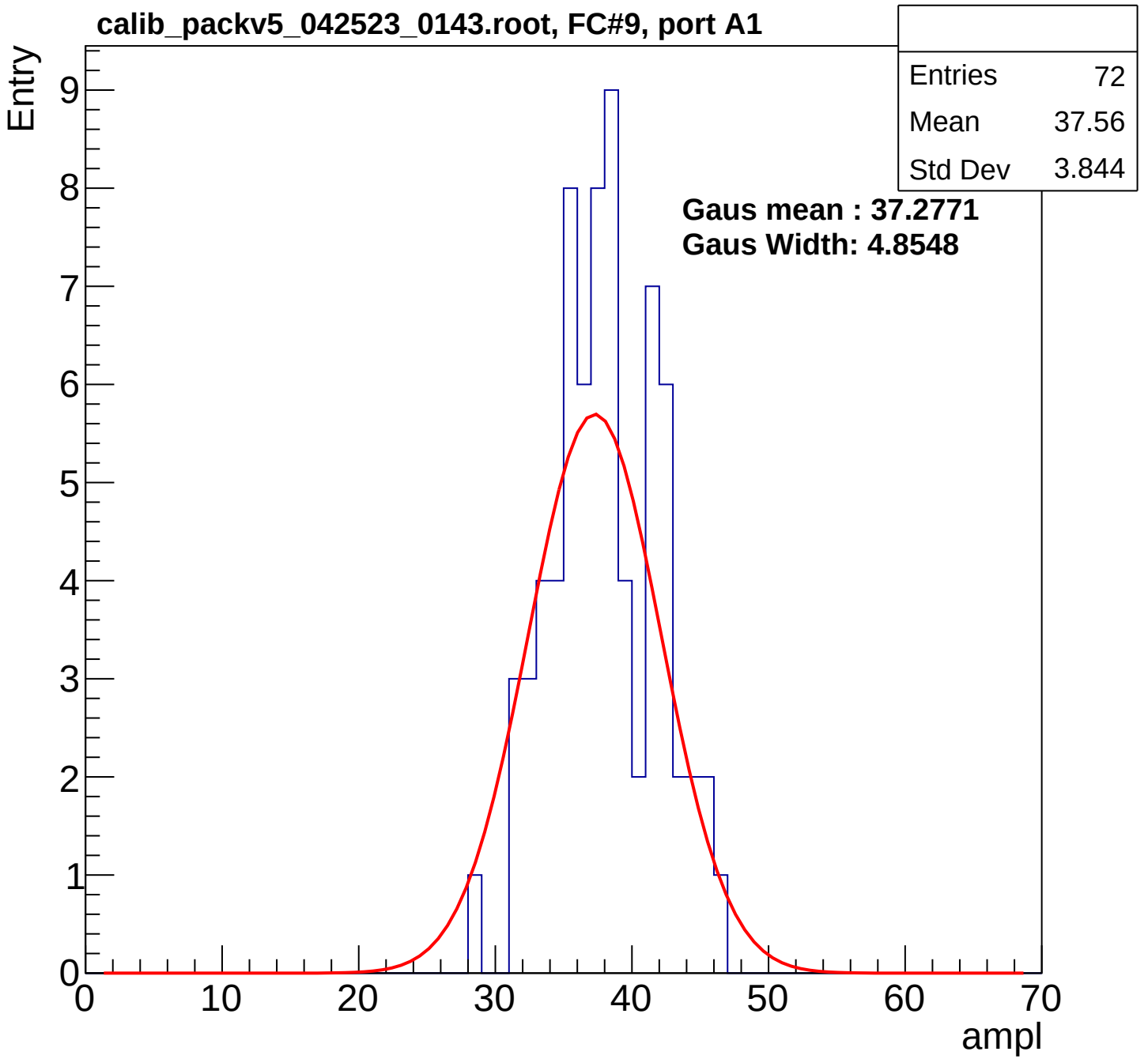

B0L001S, U4-ch15, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.33
Std Dev 11.07
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch15, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch15, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch16, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.27
Std Dev 3.698
Gaus mean : 30.7179
Gaus Width: 3.6667
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch16, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.56
Std Dev 3.844
Gaus mean : 37.2771
Gaus Width: 4.8548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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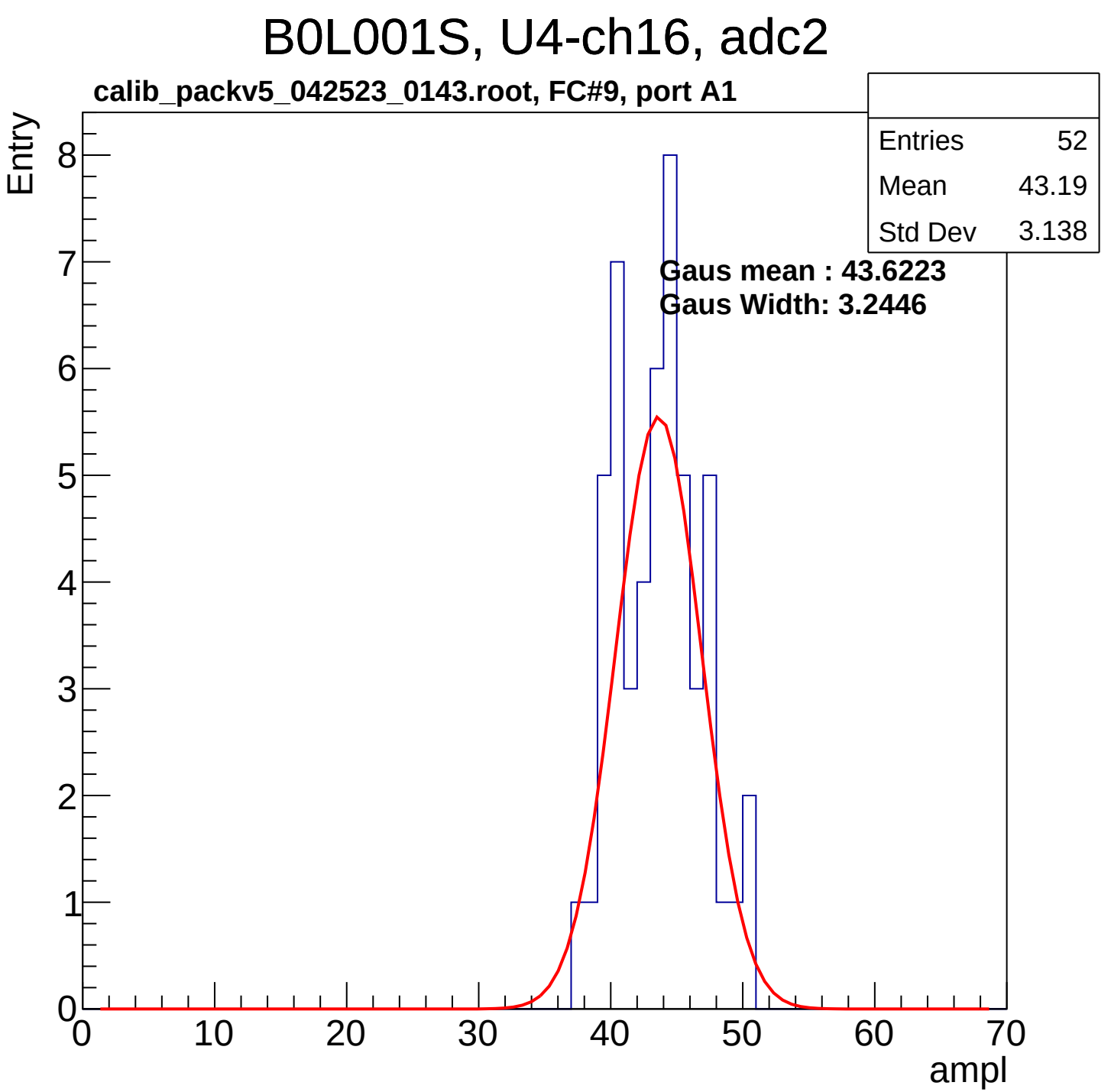
B0L001S, U4-ch16, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 43.19
Std Dev 3.138
Gaus mean : 43.6223
Gaus Width: 3.2446
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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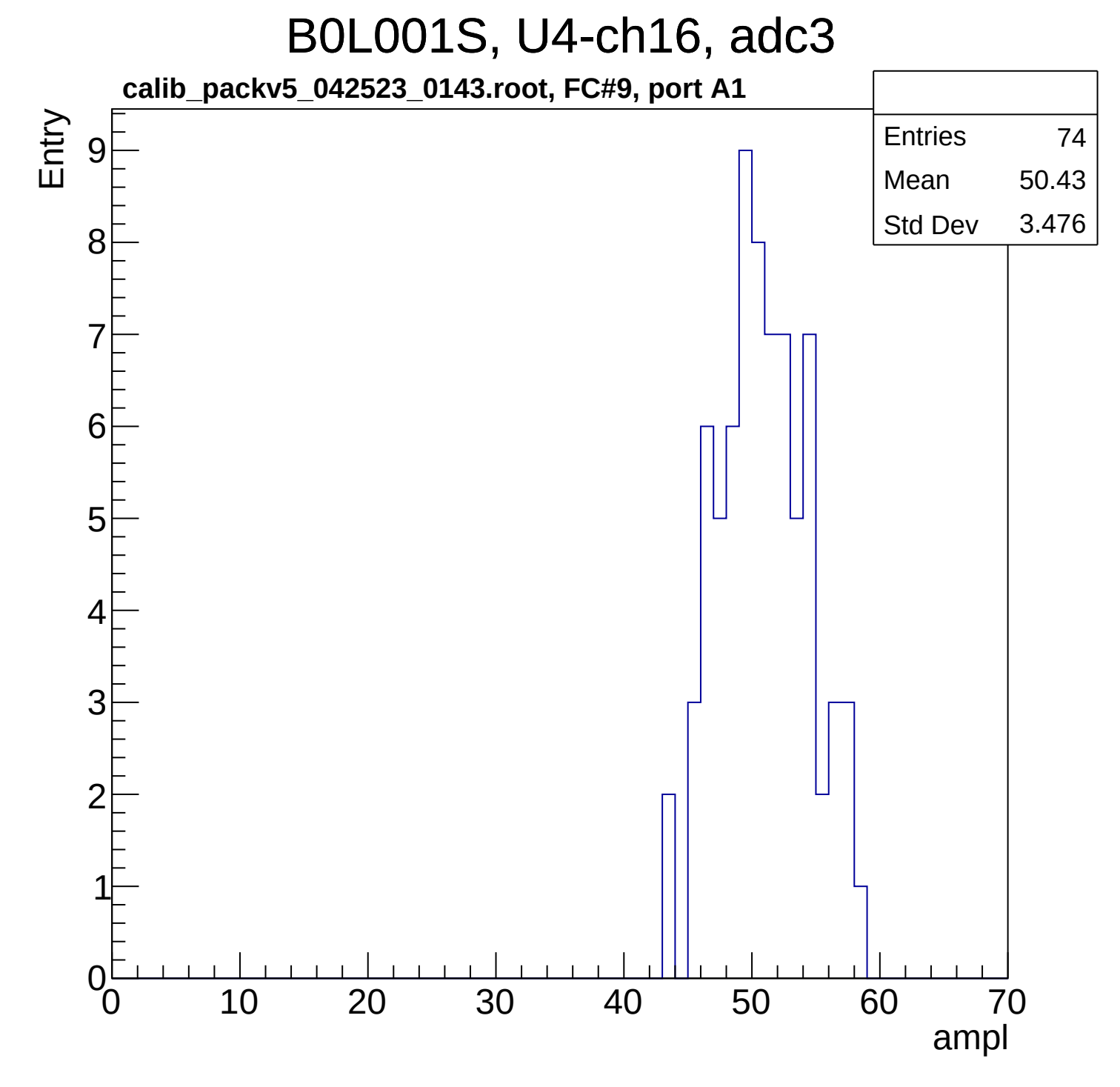
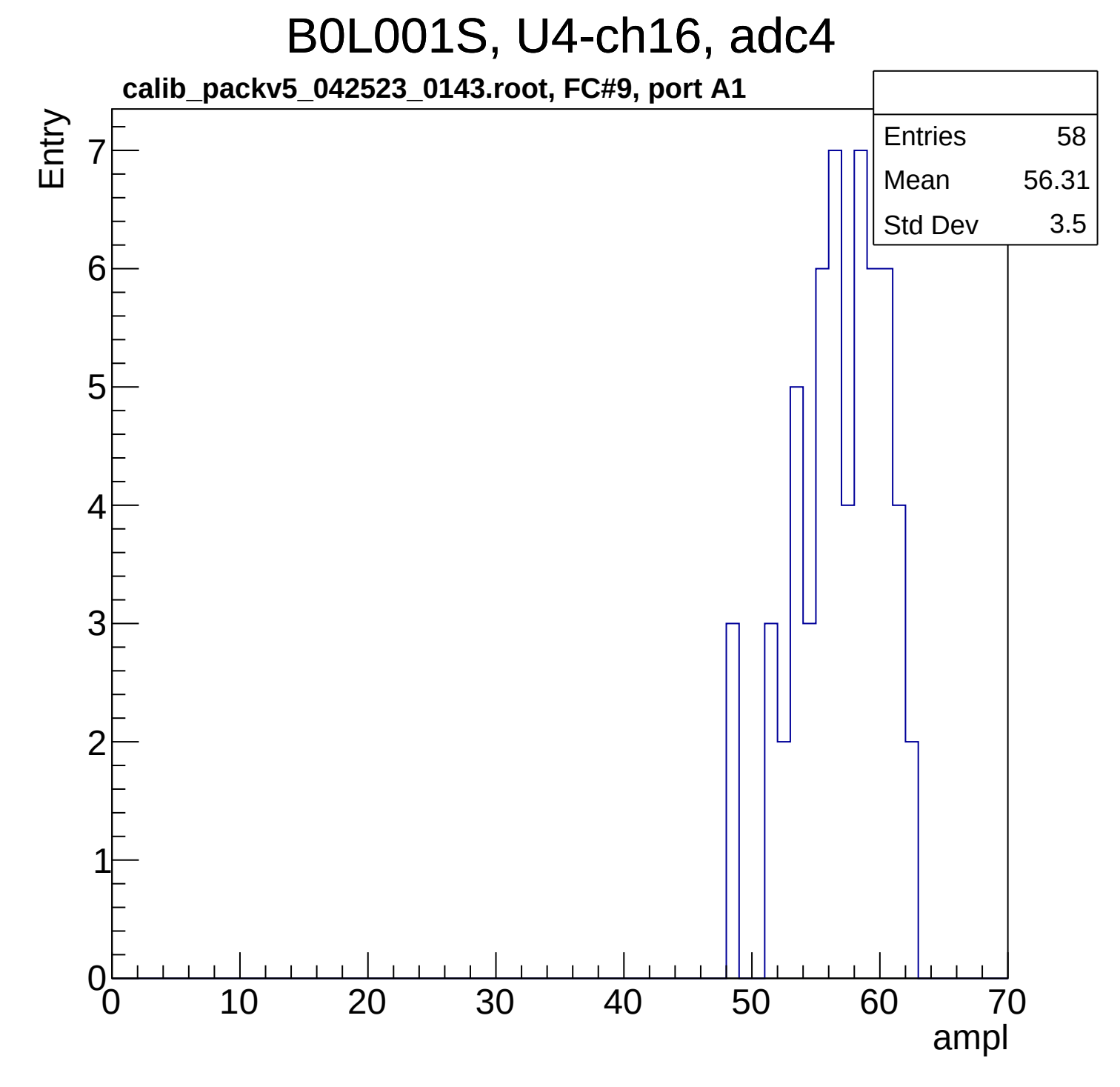
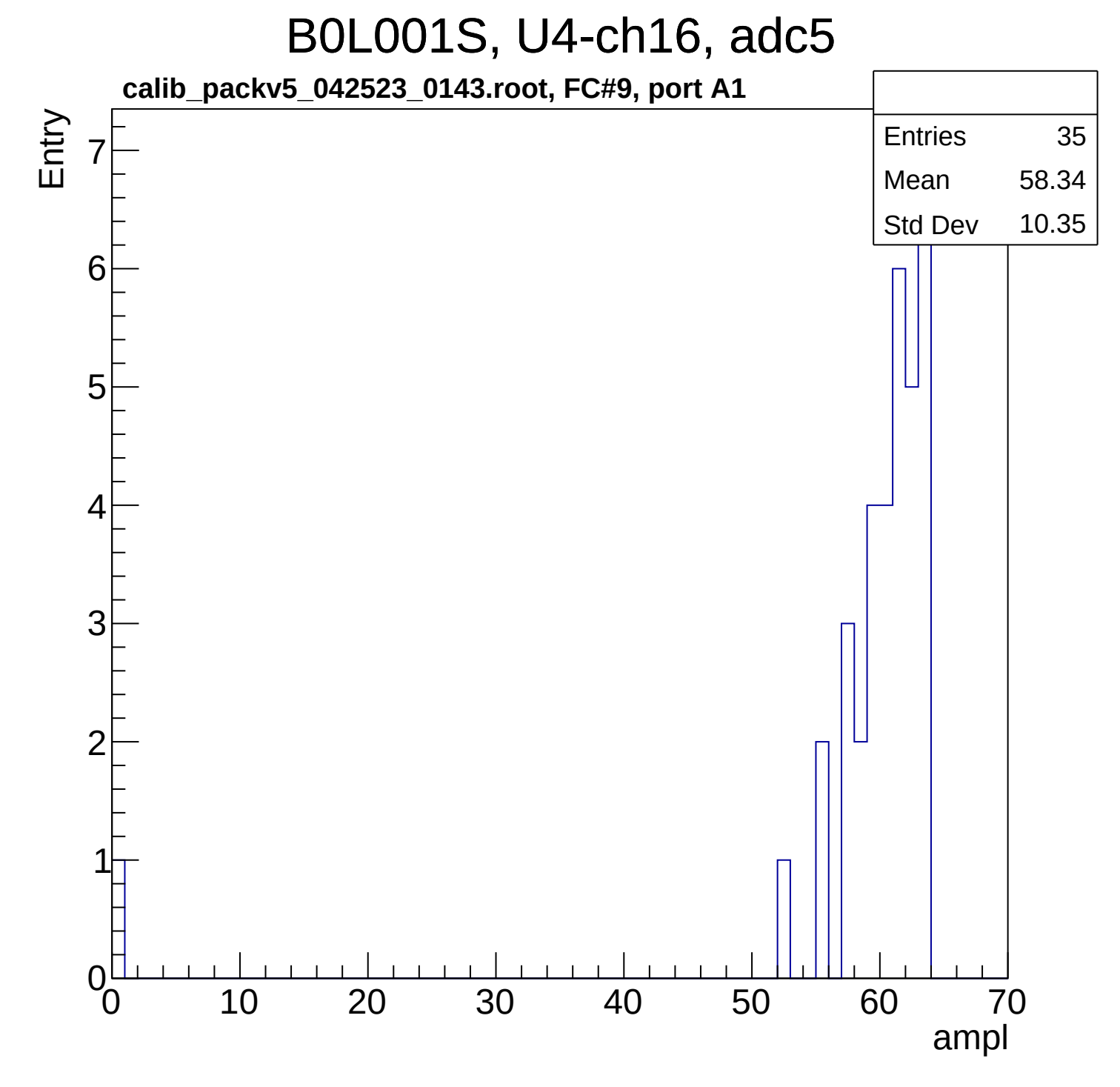
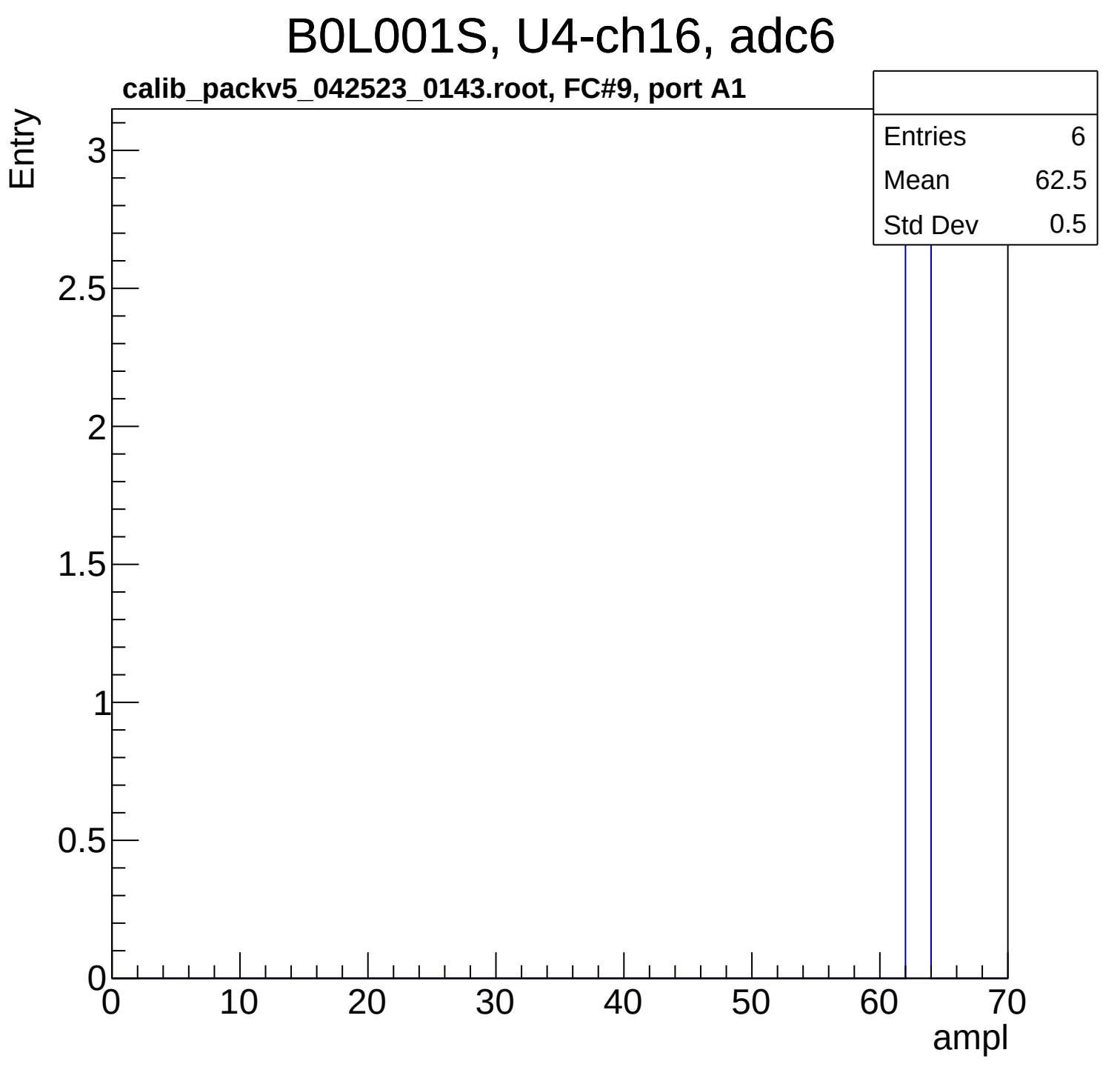
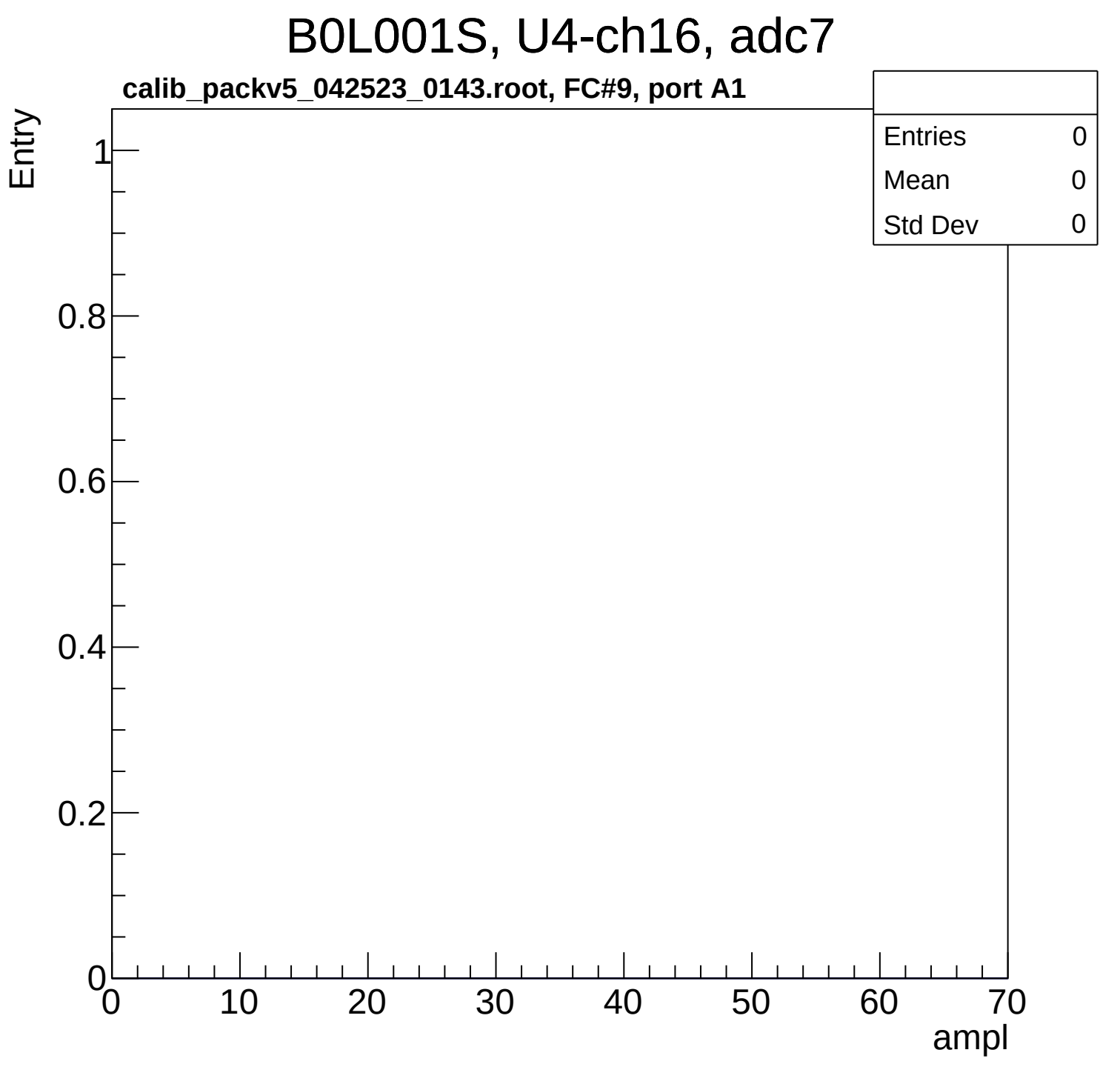
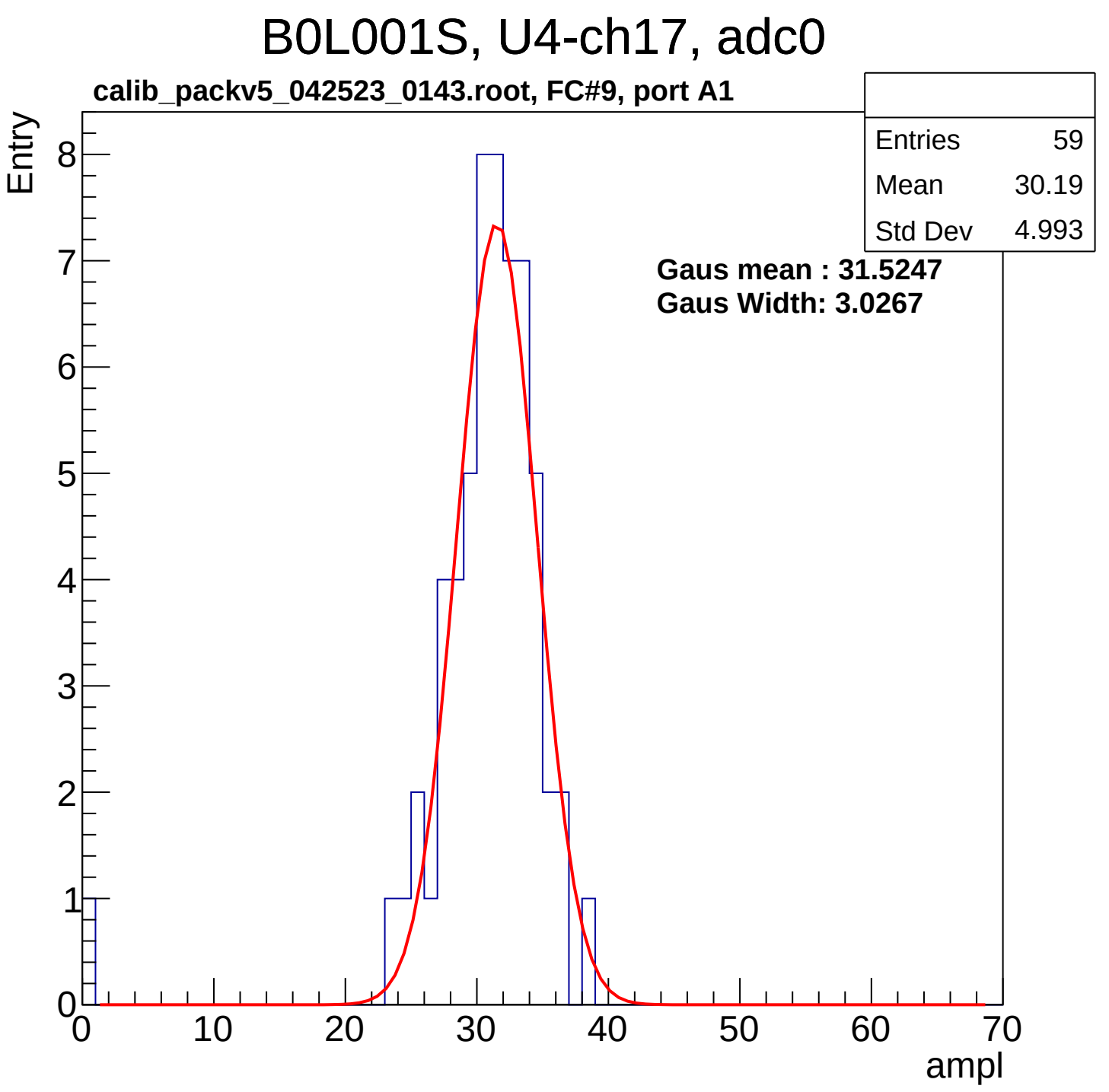

B0L001S, U4-ch16, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.43
Std Dev 3.476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

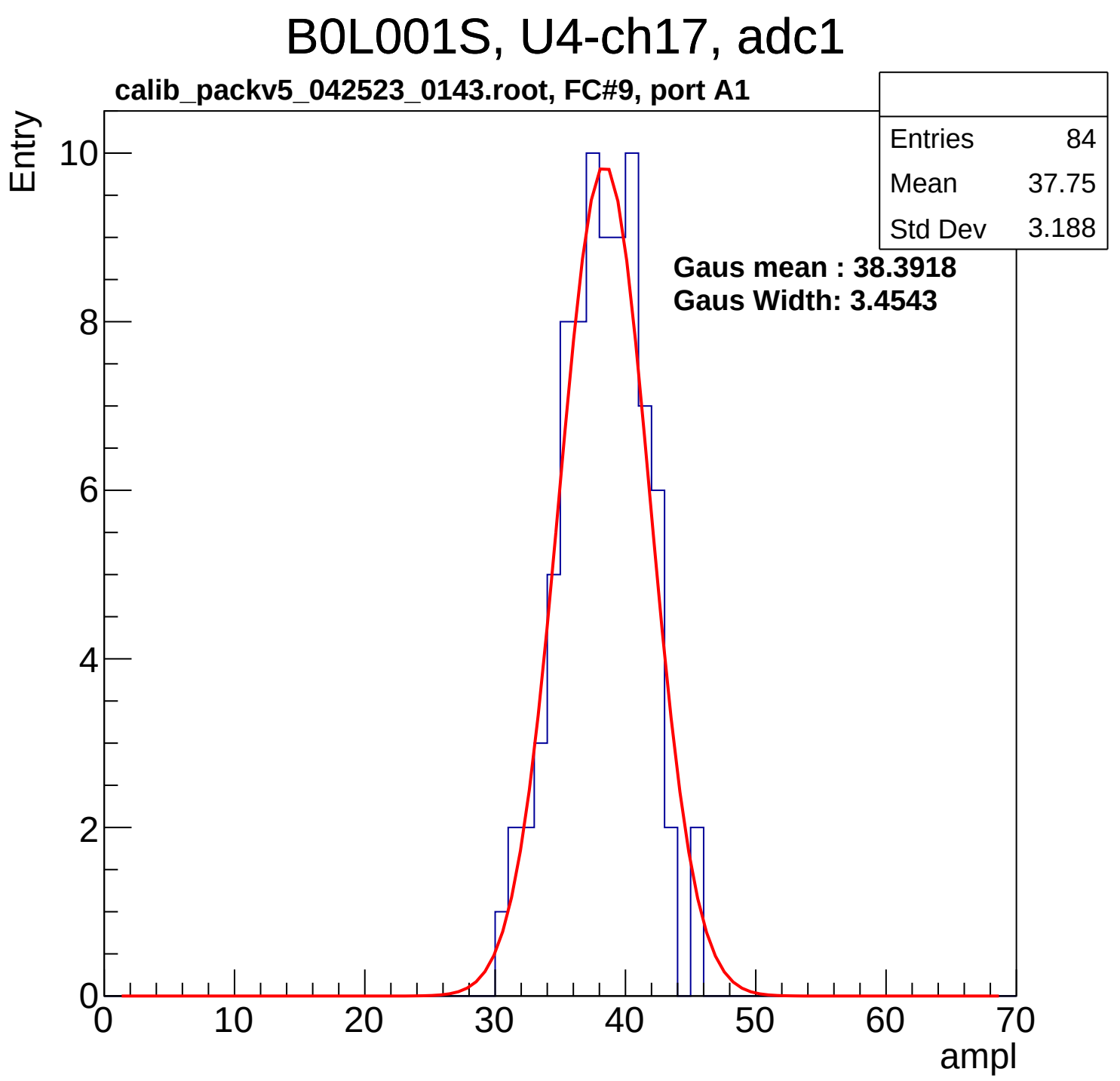

B0L001S, U4-ch16, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.31
Std Dev 3.5
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

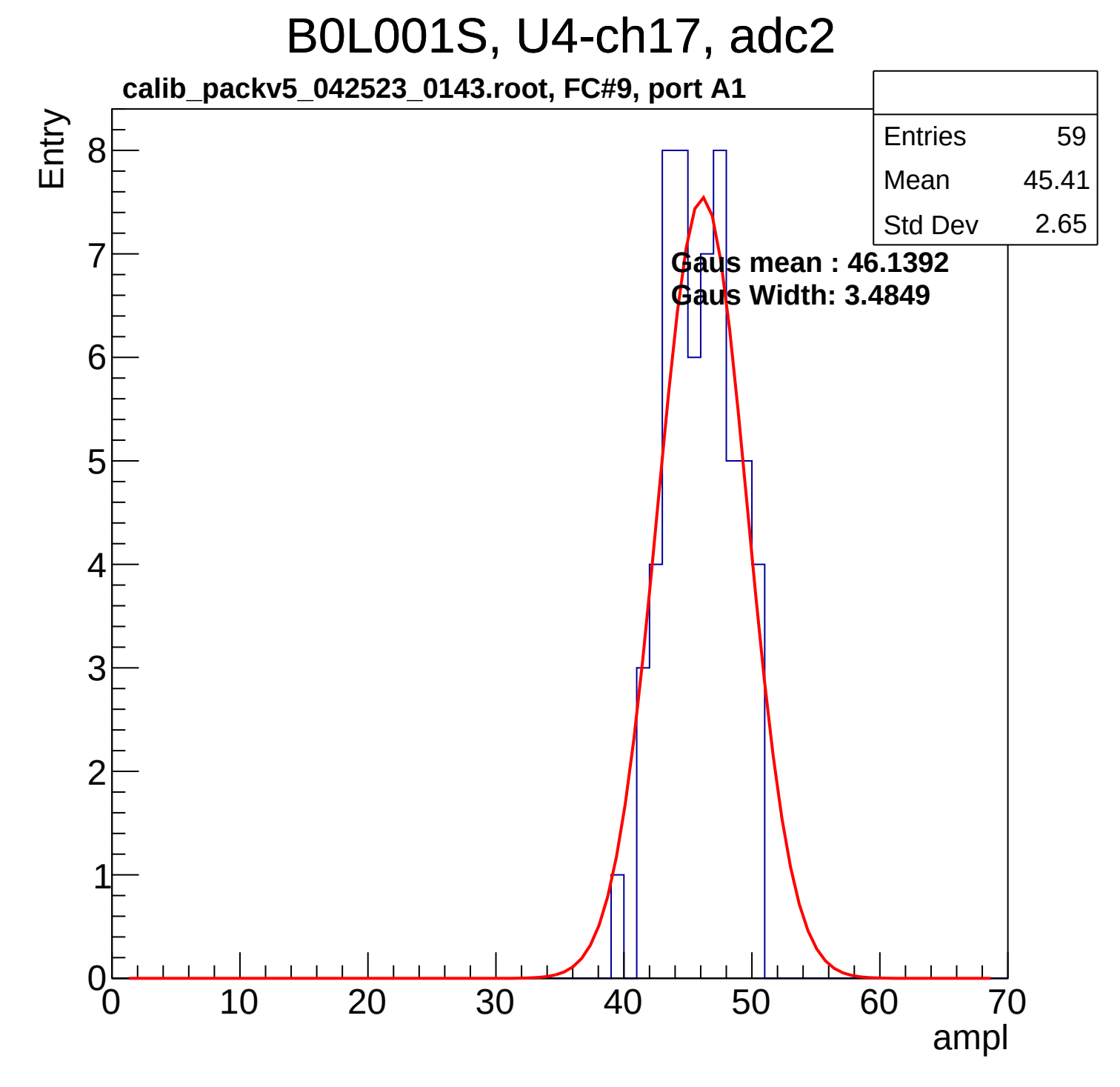

B0L001S, U4-ch16, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.34
Std Dev 10.35
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch16, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.5
Std Dev 0.5
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch16, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch17, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.19
Std Dev 4.993
Gaus mean : 31.5247
Gaus Width: 3.0267
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch17, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 37.75
Std Dev 3.188
Gaus mean : 38.3918
Gaus Width: 3.4543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch17, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.41
Std Dev 2.65
Gaus mean : 46.1392
Gaus Width: 3.4849
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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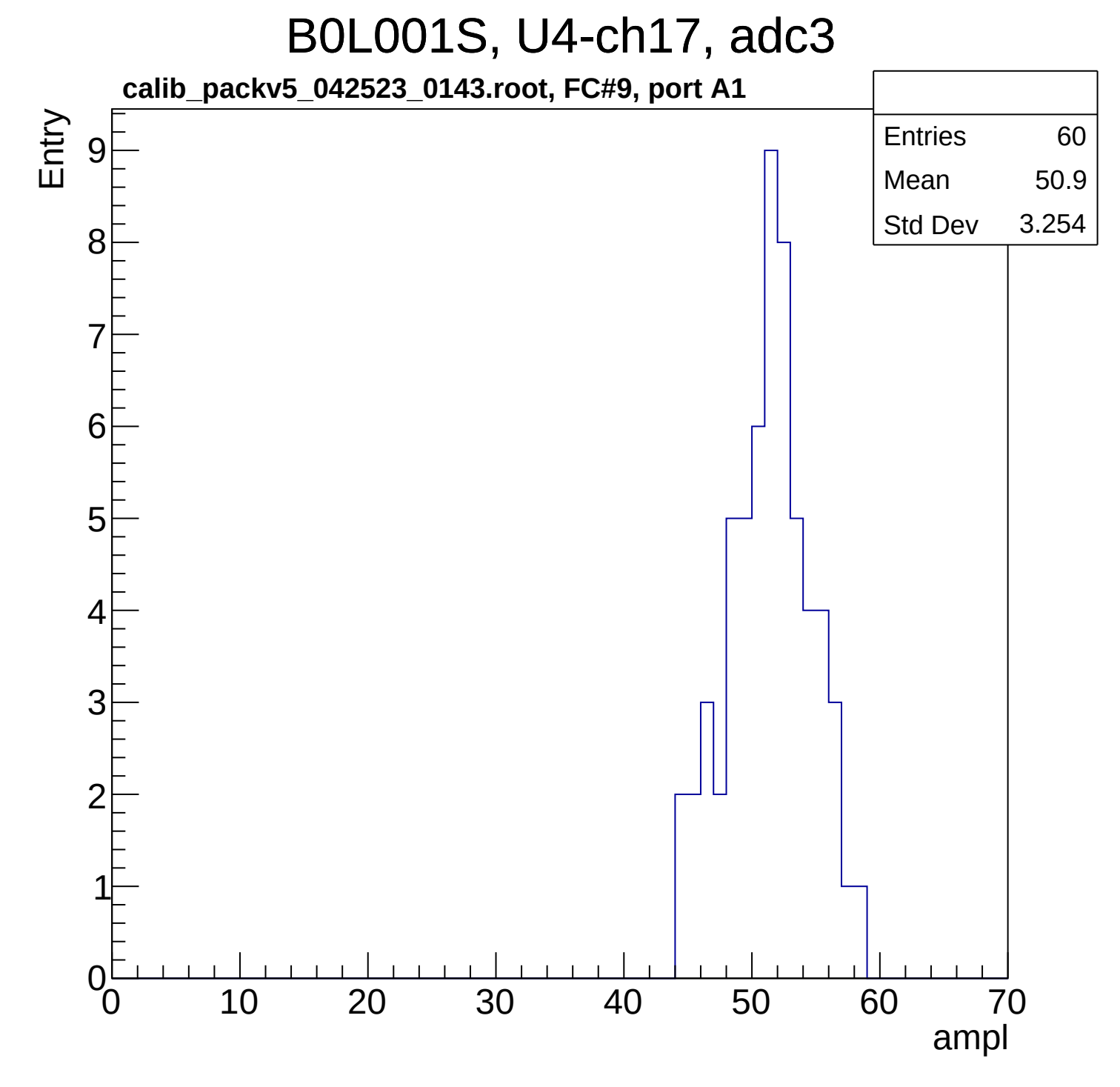
B0L001S, U4-ch17, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.9
Std Dev 3.254
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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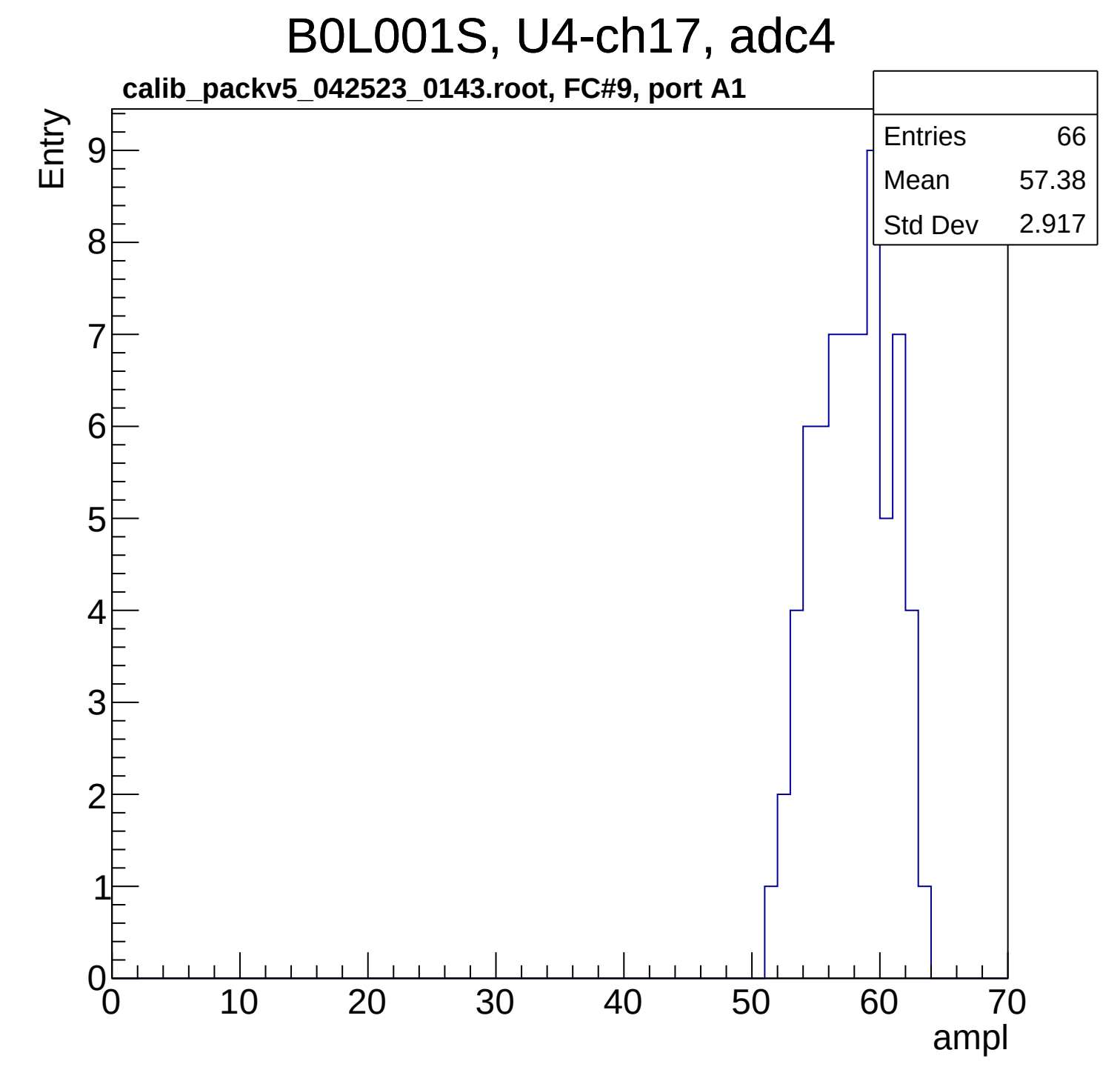
B0L001S, U4-ch17, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.38
Std Dev 2.917
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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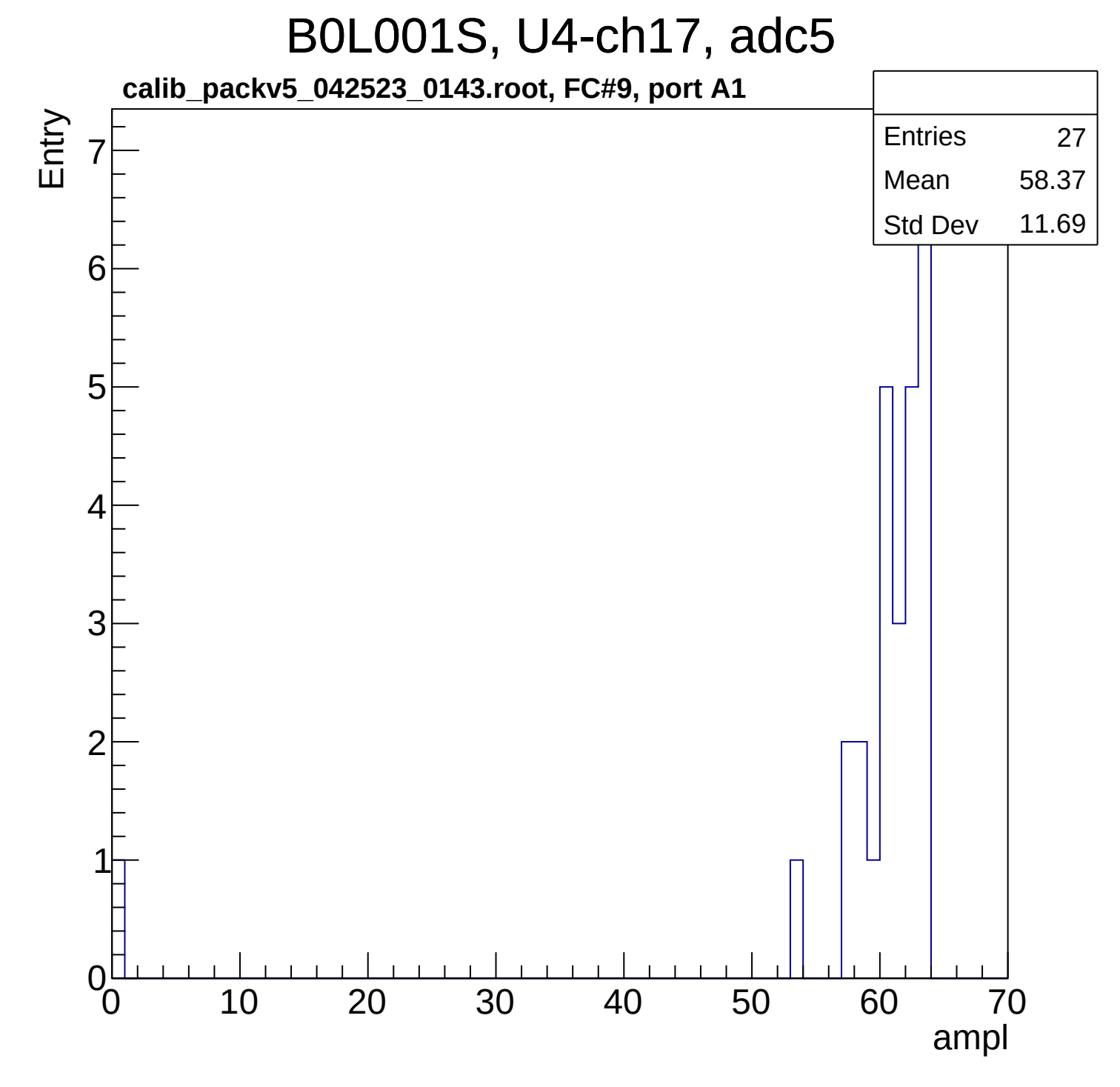
B0L001S, U4-ch17, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.37
Std Dev 11.69
Entry
0
1
2
3
4
5
6
7
0
10
20
30
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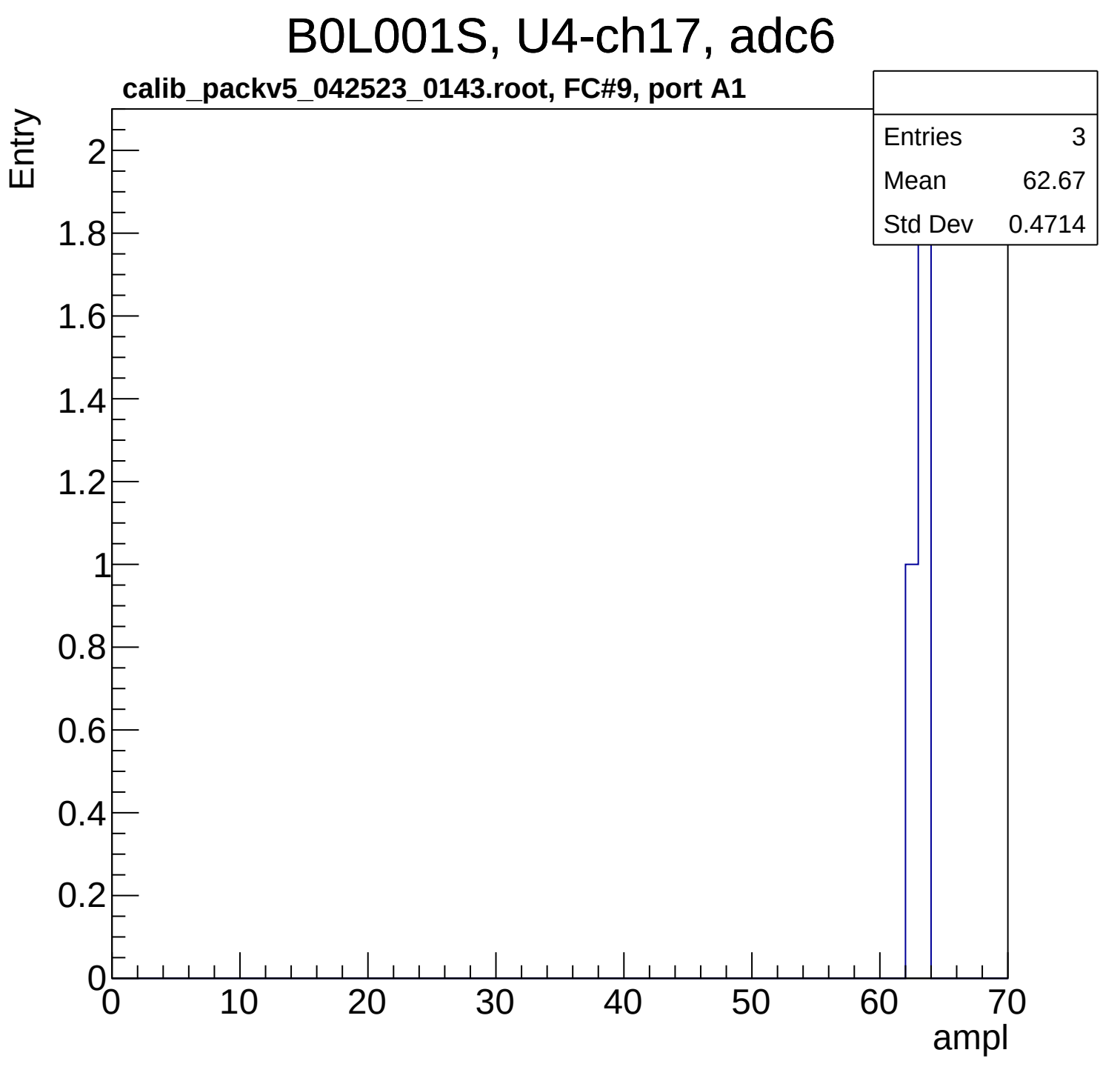
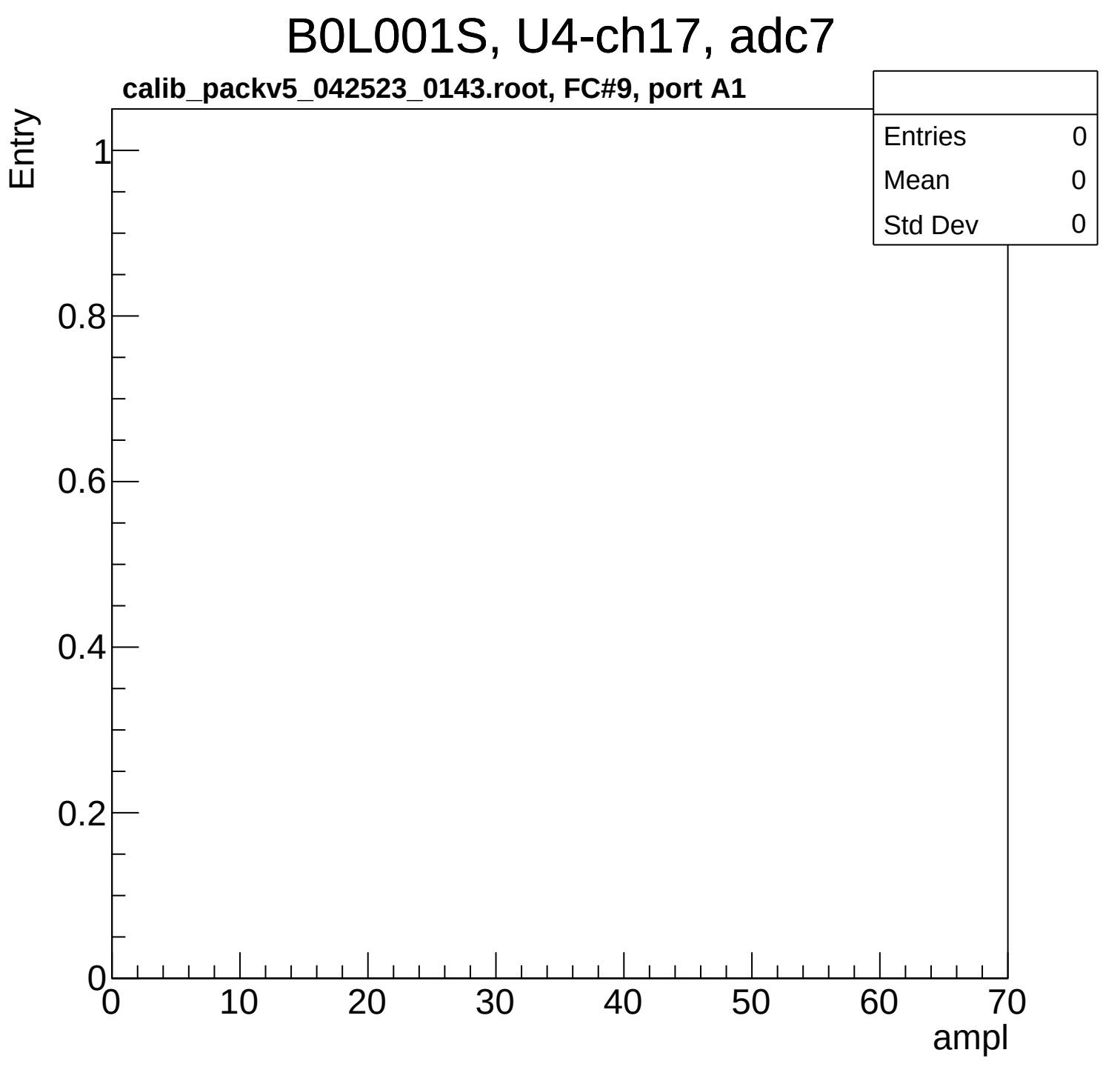
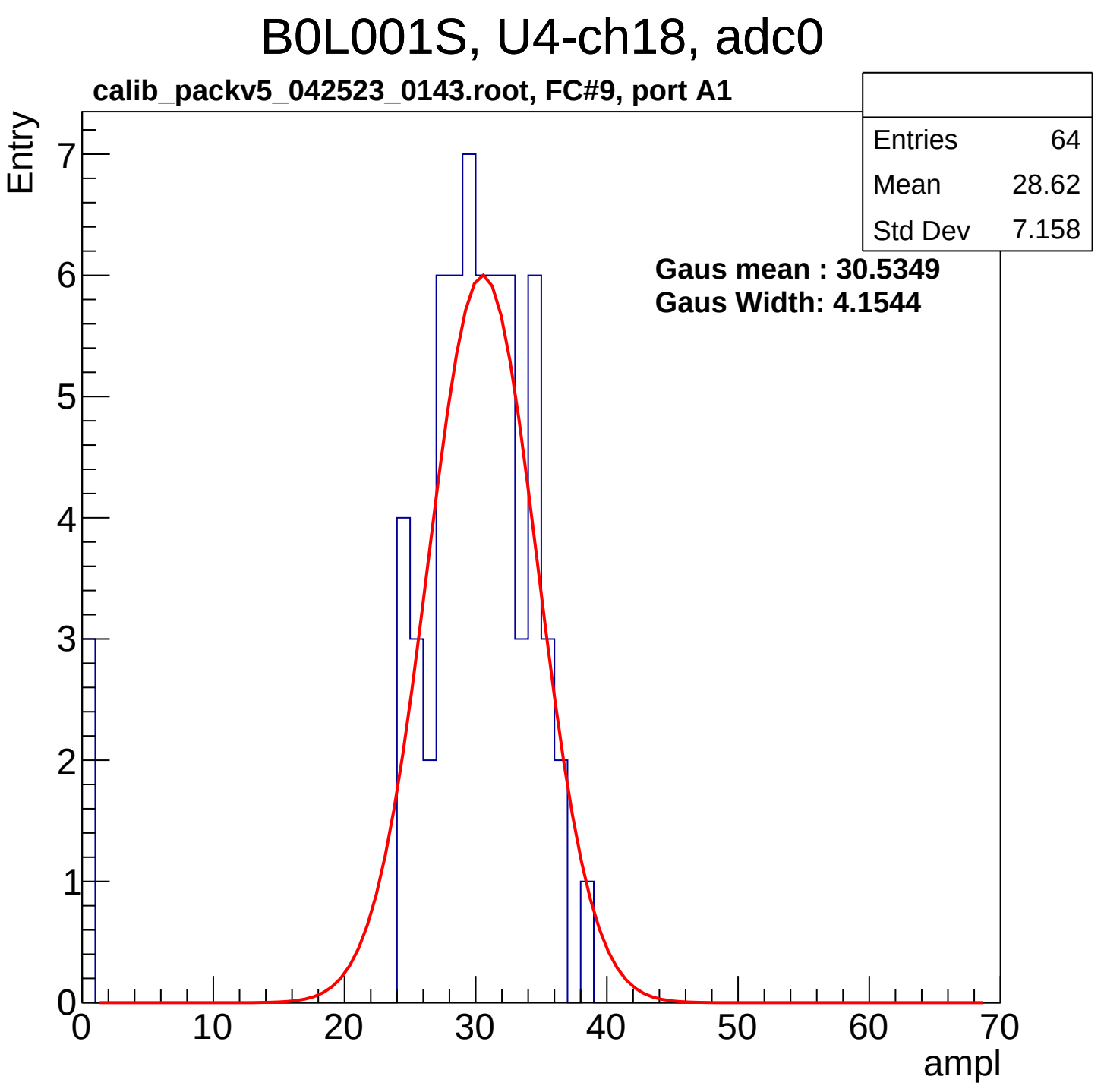
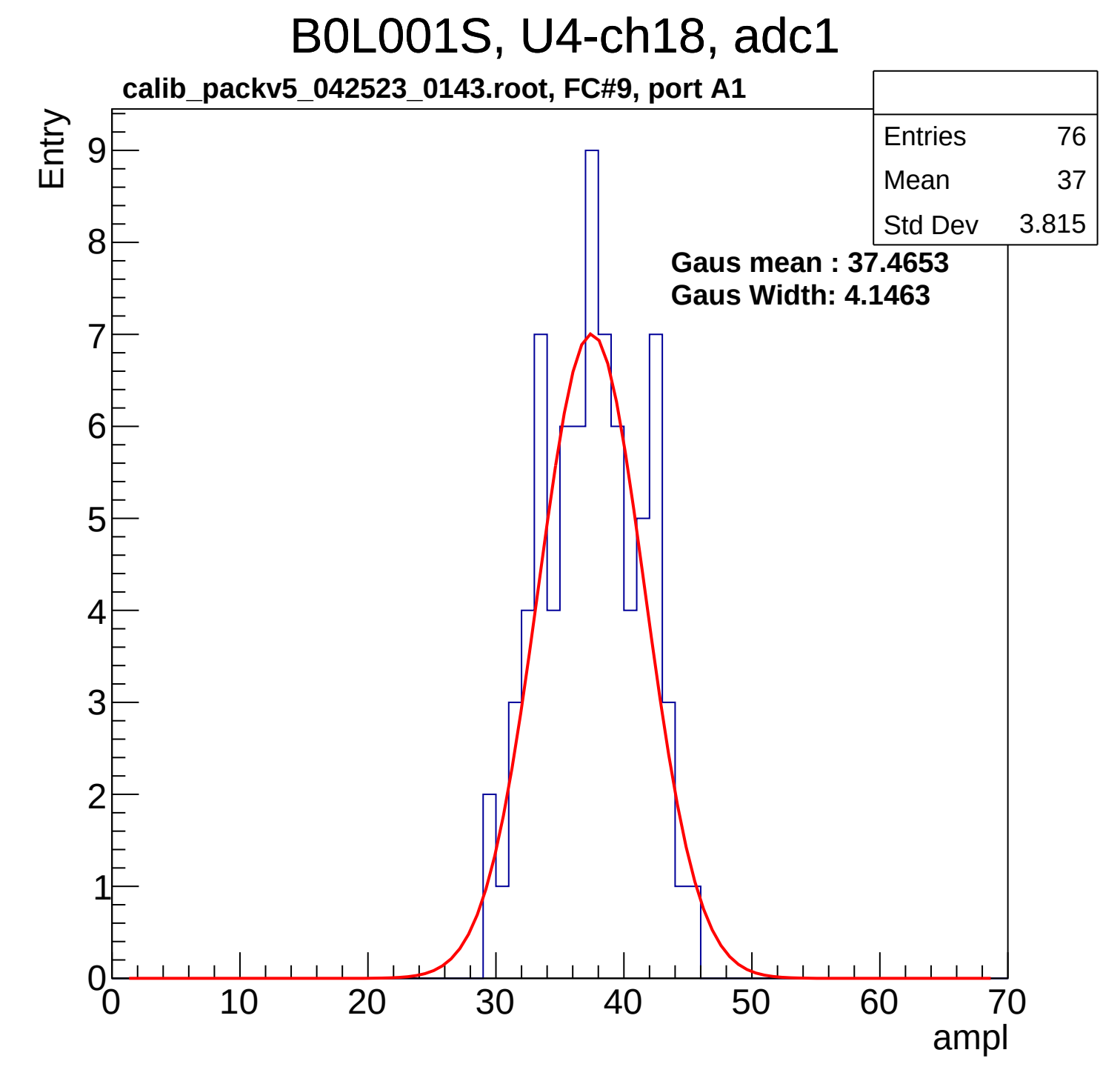
50
60
70
ampl


B0L001S, U4-ch17, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch17, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch18, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 28.62
Std Dev 7.158
Gaus mean : 30.5349
Gaus Width: 4.1544
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch18, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37
Std Dev 3.815
Gaus mean : 37.4653
Gaus Width: 4.1463
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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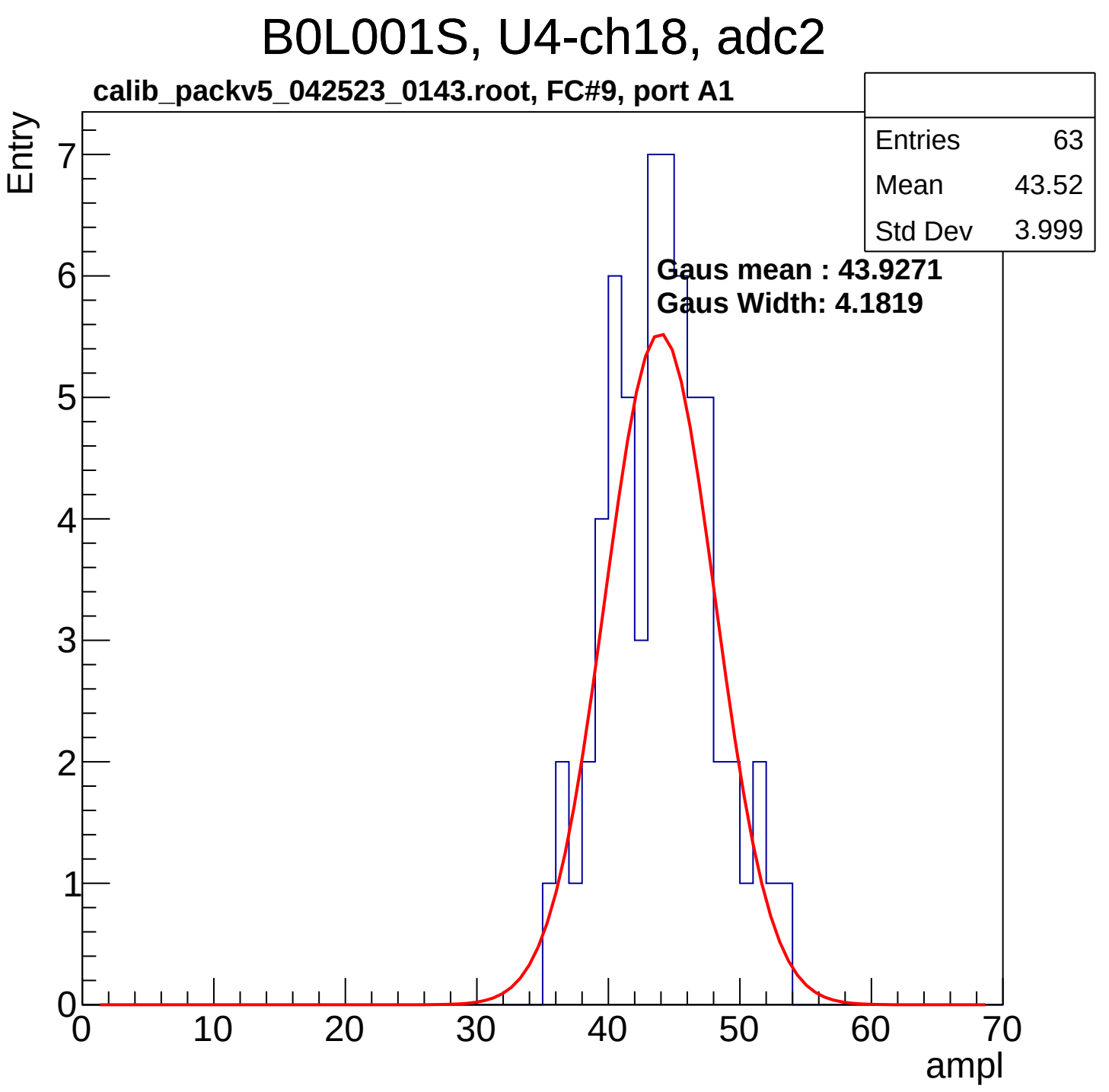
40
50
60
70


B0L001S, U4-ch18, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.52
Std Dev 3.999
Gaus mean : 43.9271
Gaus Width: 4.1819
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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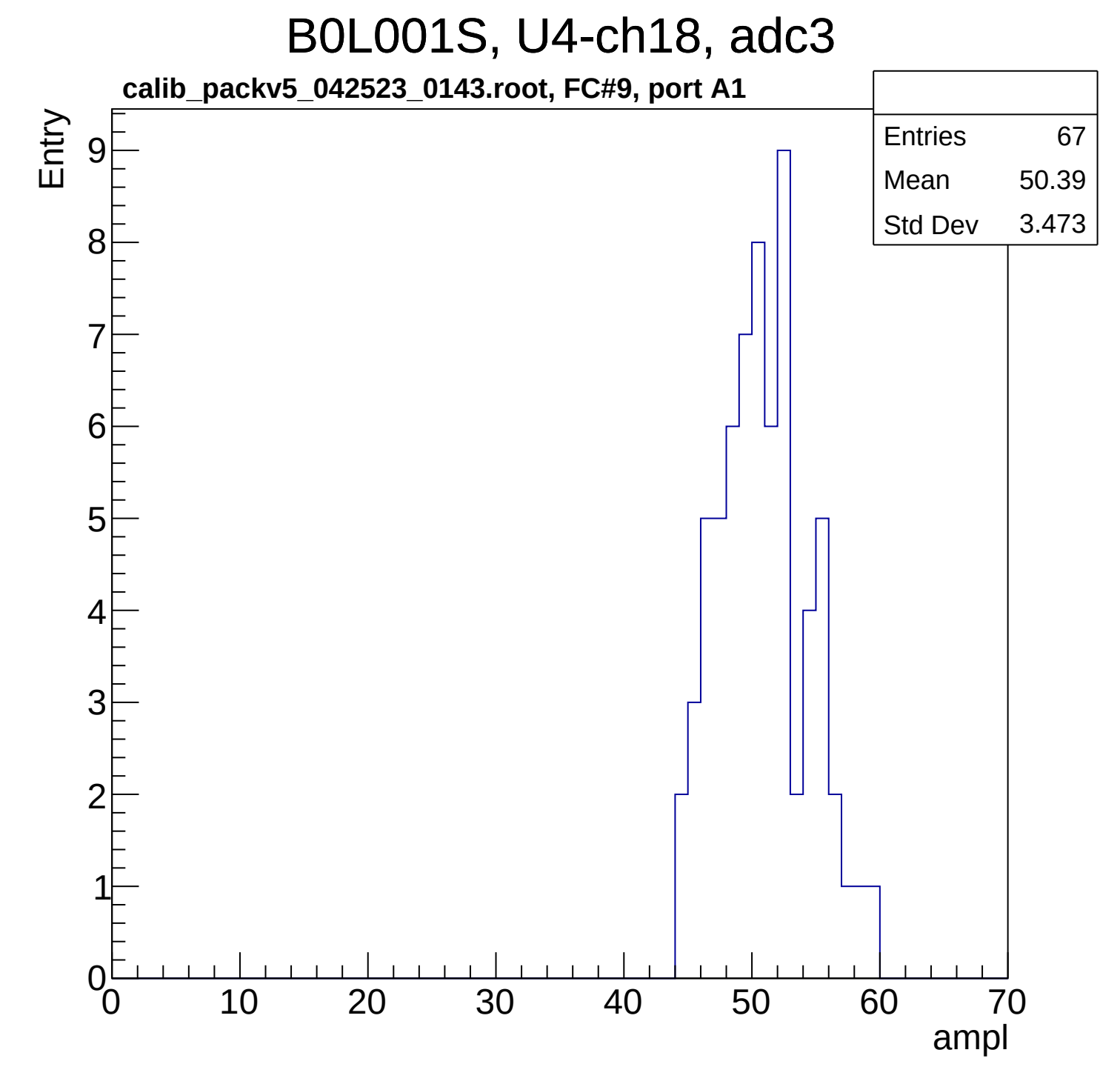
40
50
60
70


B0L001S, U4-ch18, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.39
Std Dev 3.473
Entry
ampl
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3
4
5
6
7
8
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0
10
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60
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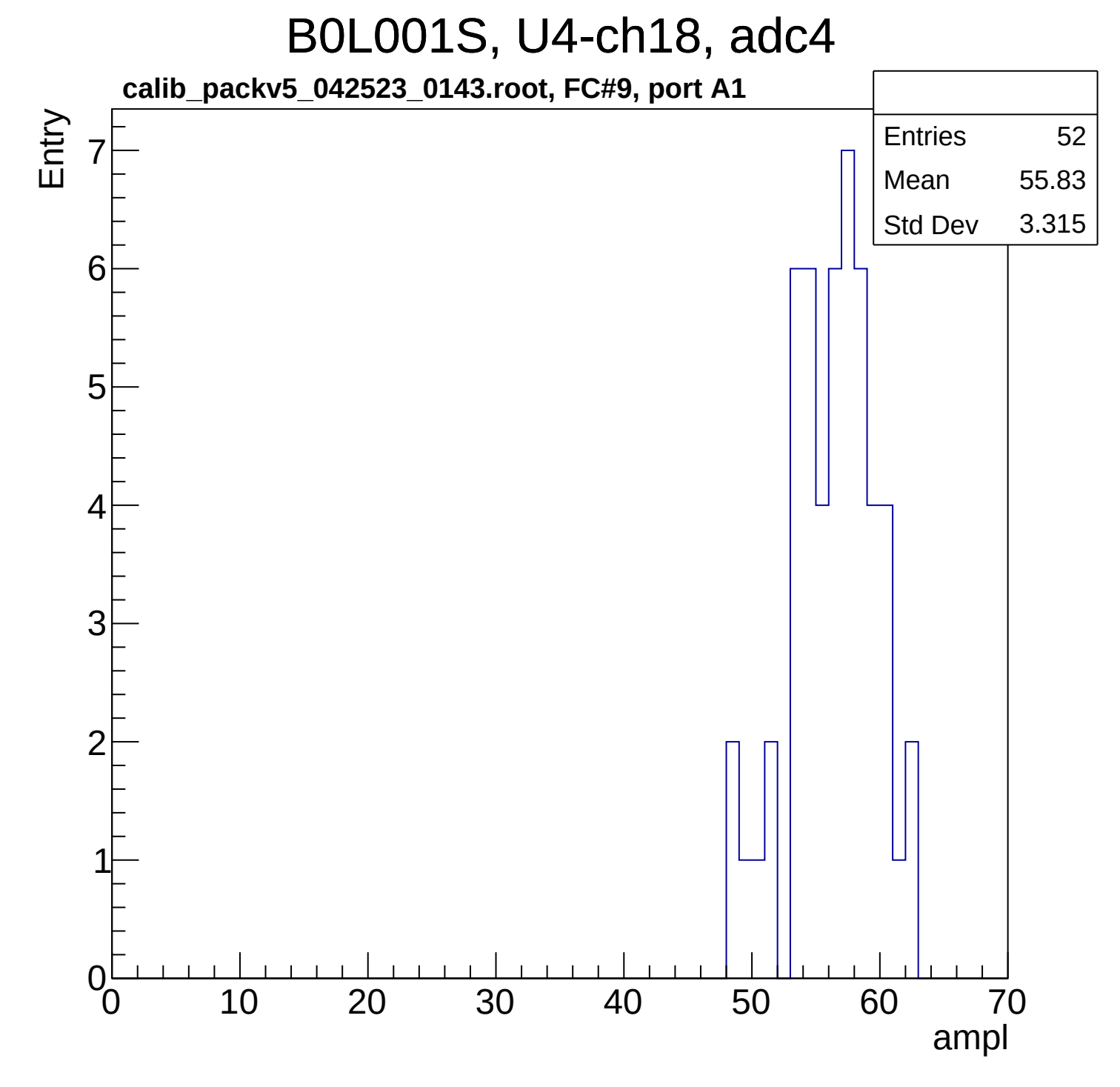
B0L001S, U4-ch18, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.83
Std Dev 3.315
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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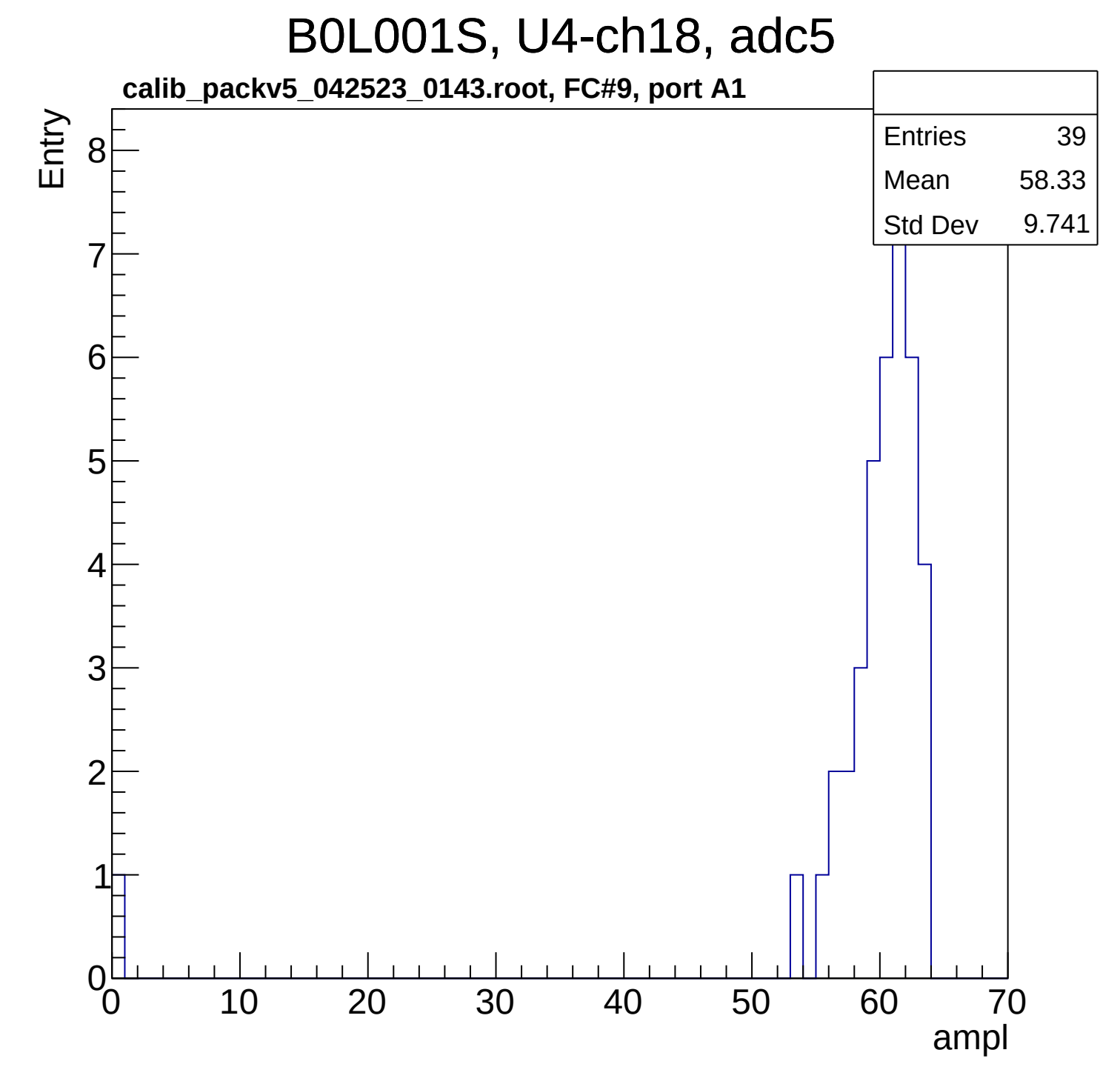
40
50
60
70


B0L001S, U4-ch18, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.33
Std Dev 9.741
Entry
ampl
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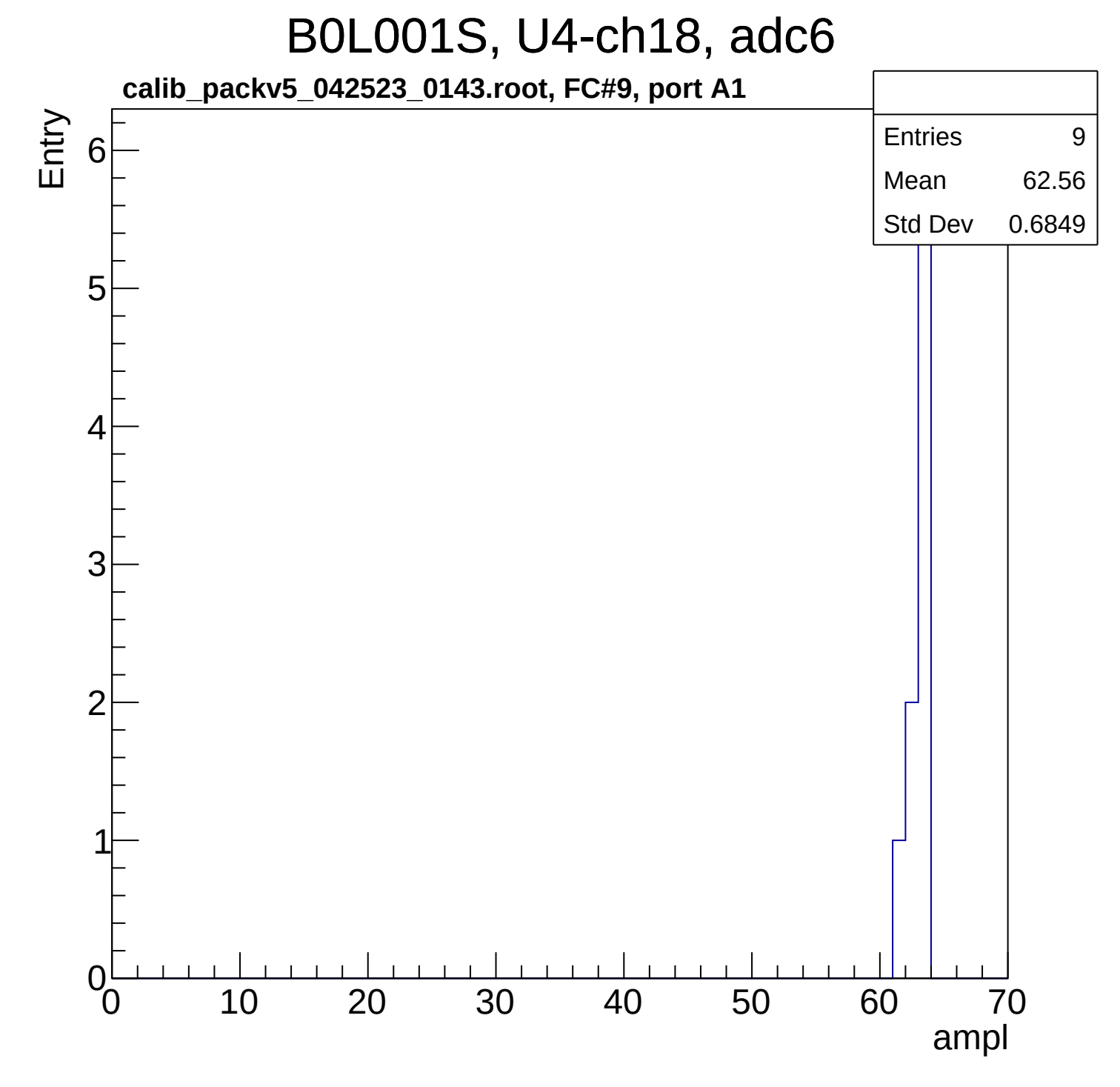
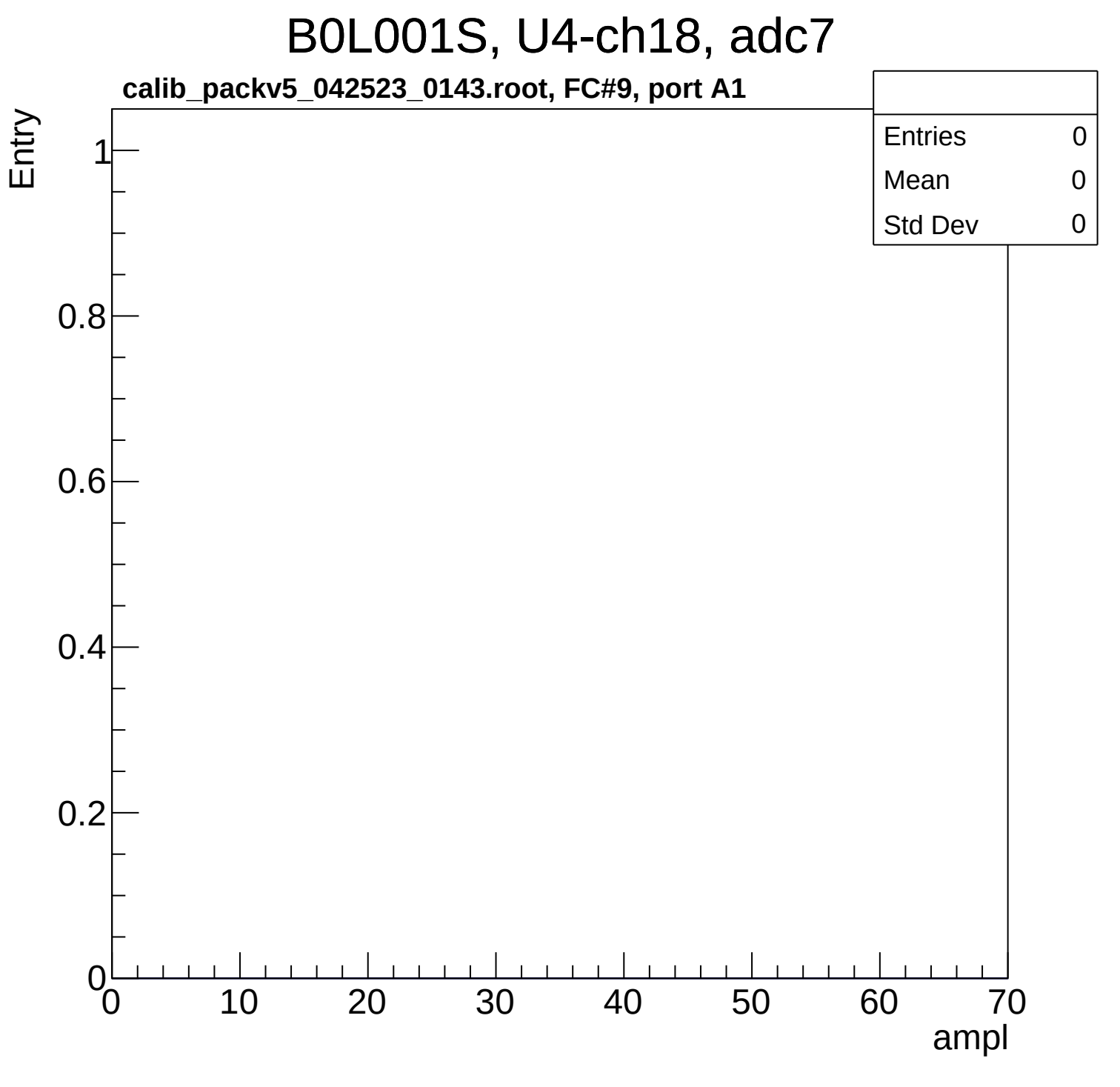
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch18, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 62.56
Std Dev 0.6849
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch18, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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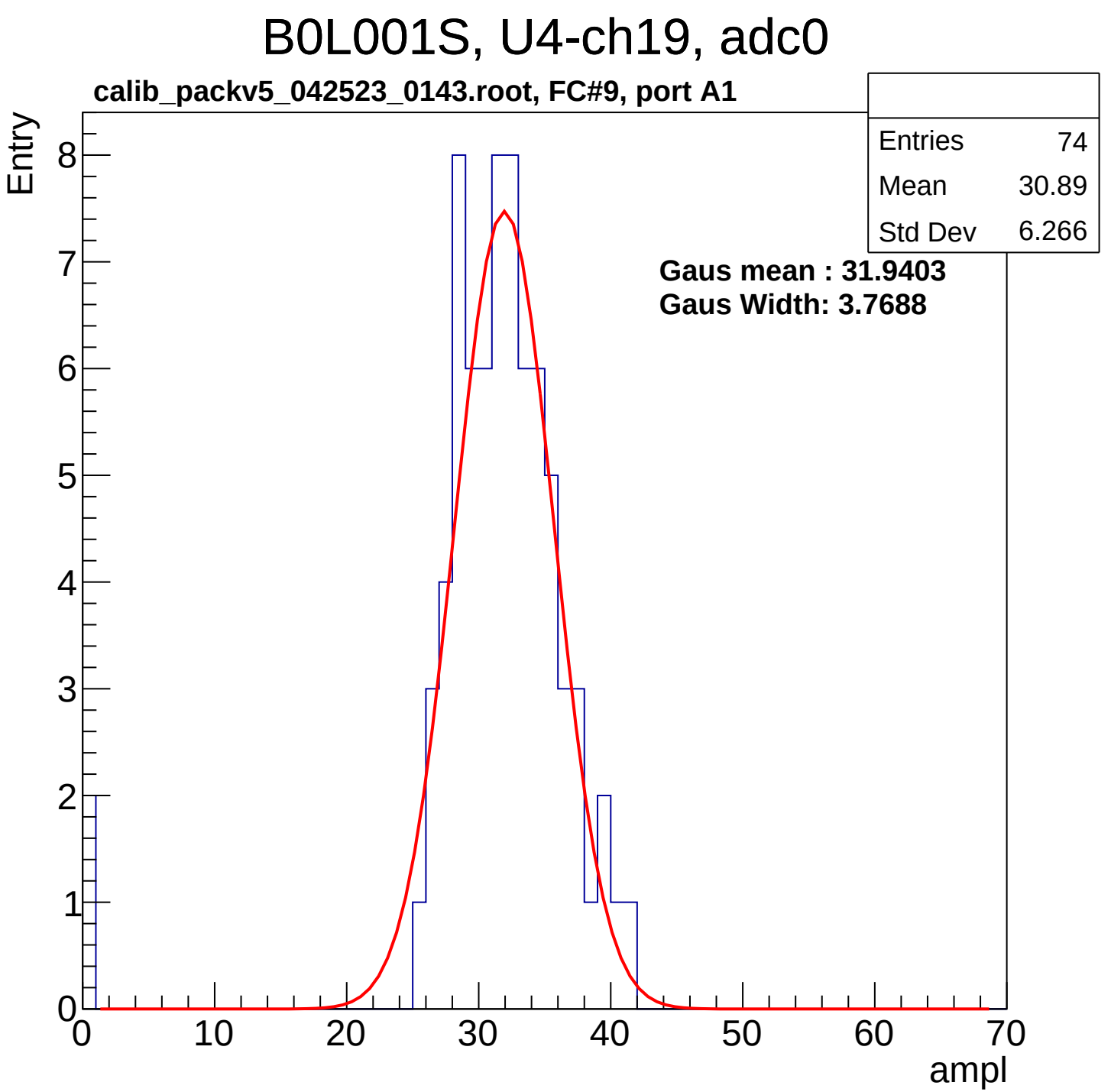
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch19, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.89
Std Dev 6.266
Gaus mean : 31.9403
Gaus Width: 3.7688
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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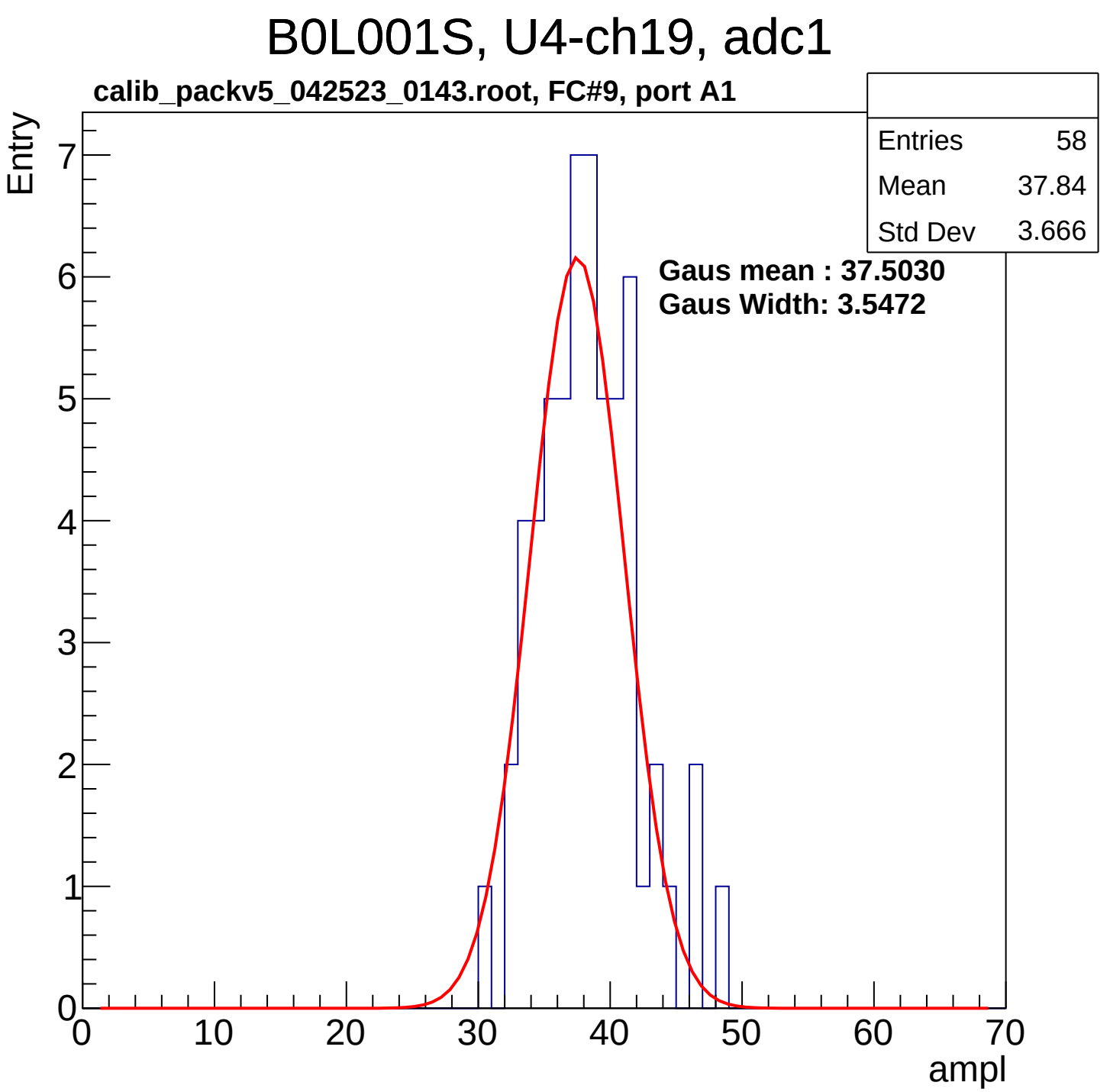
B0L001S, U4-ch19, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 37.84
Std Dev 3.666
Gaus mean : 37.5030
Gaus Width: 3.5472
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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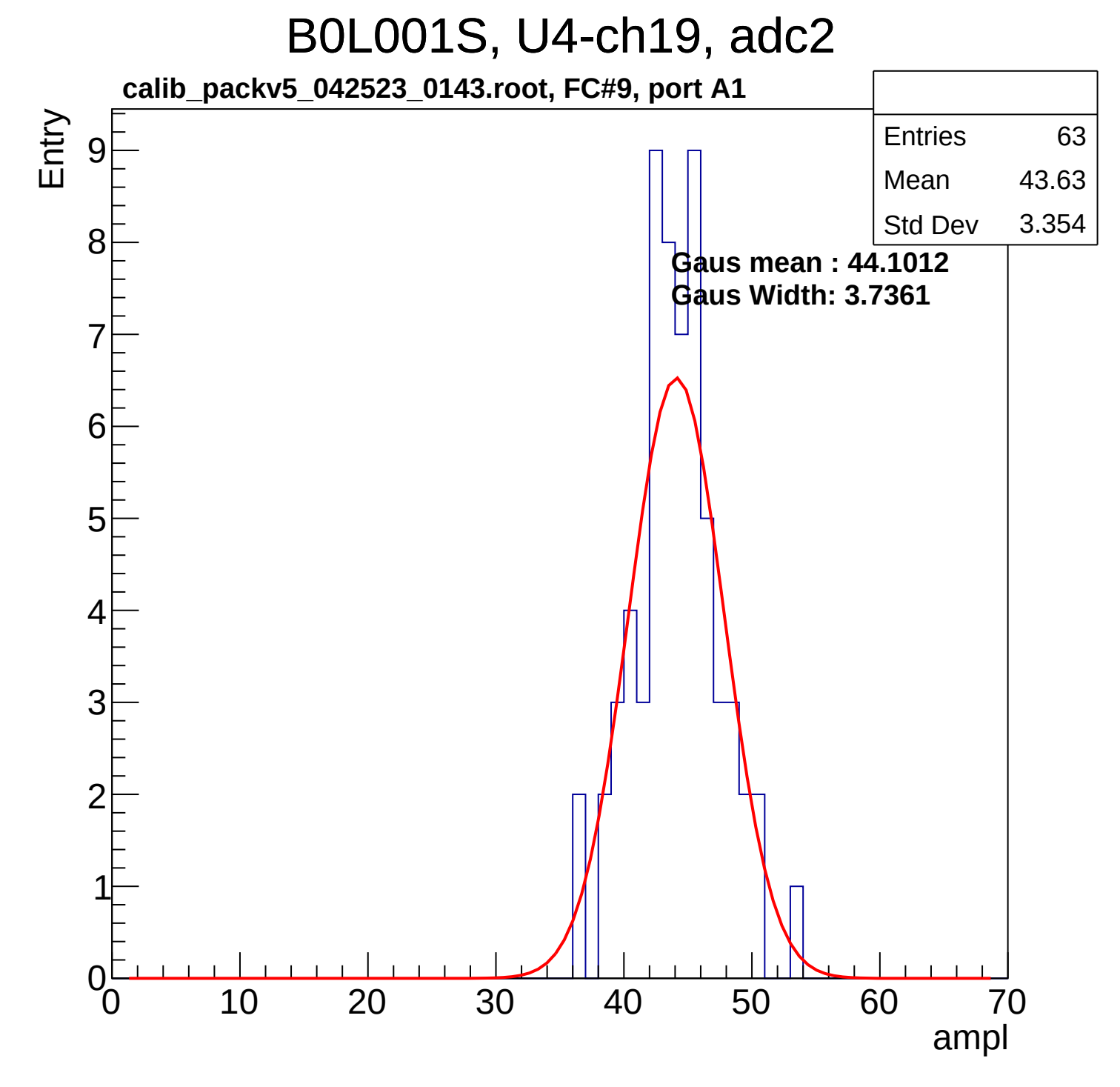
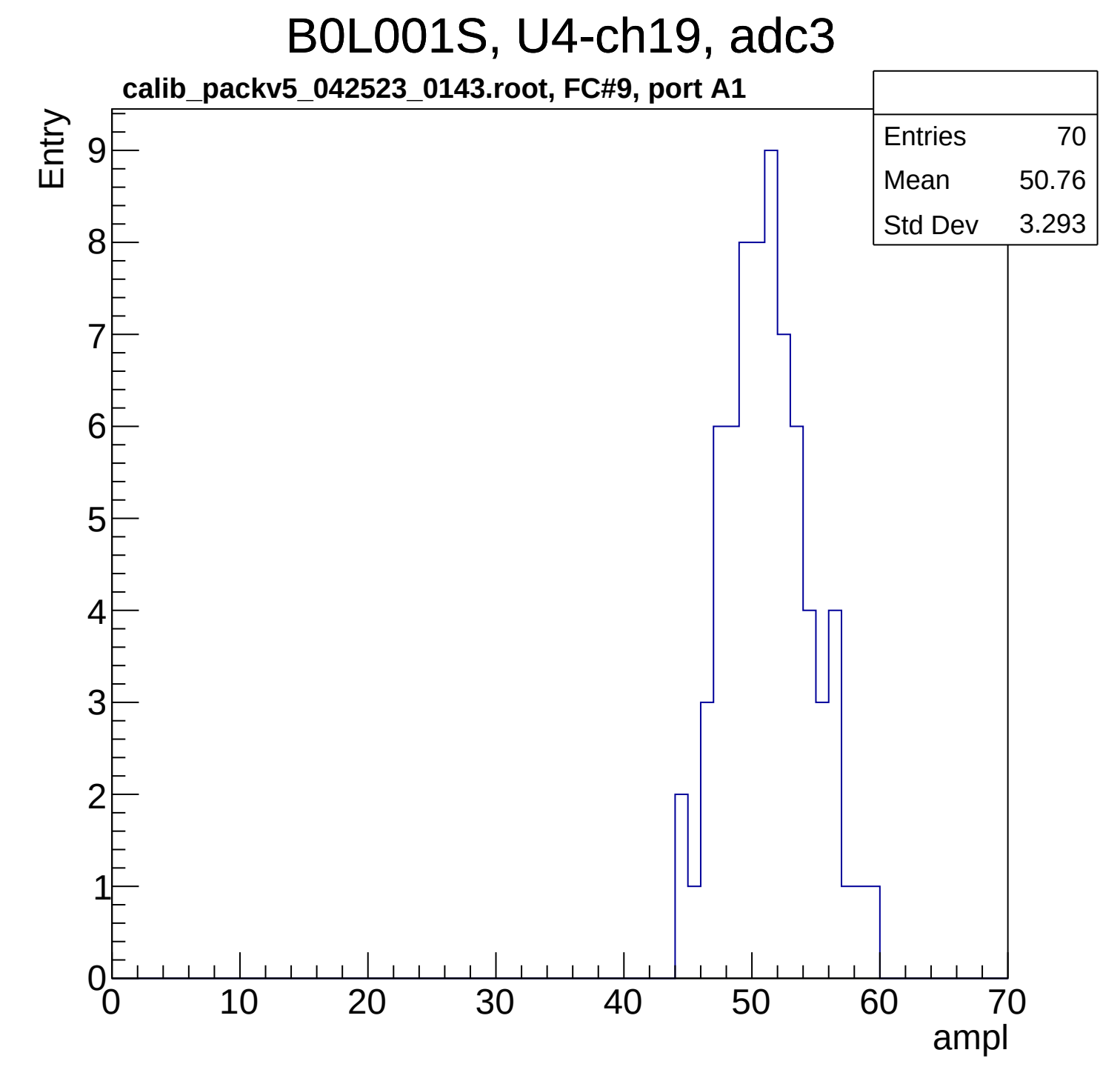
40
50
60
70


B0L001S, U4-ch19, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.63
Std Dev 3.354
Gaus mean : 44.1012
Gaus Width: 3.7361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch19, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.76
Std Dev 3.293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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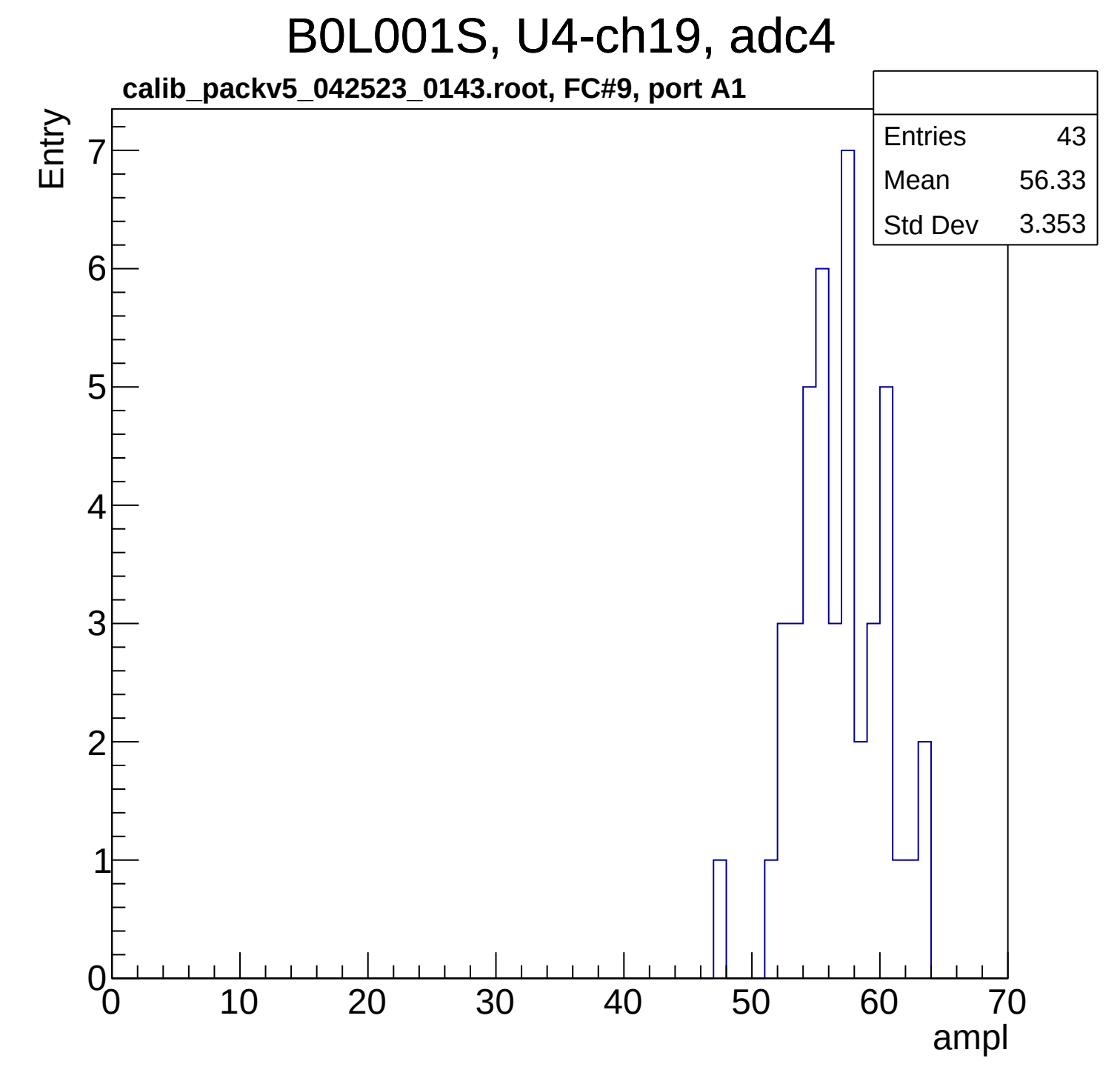
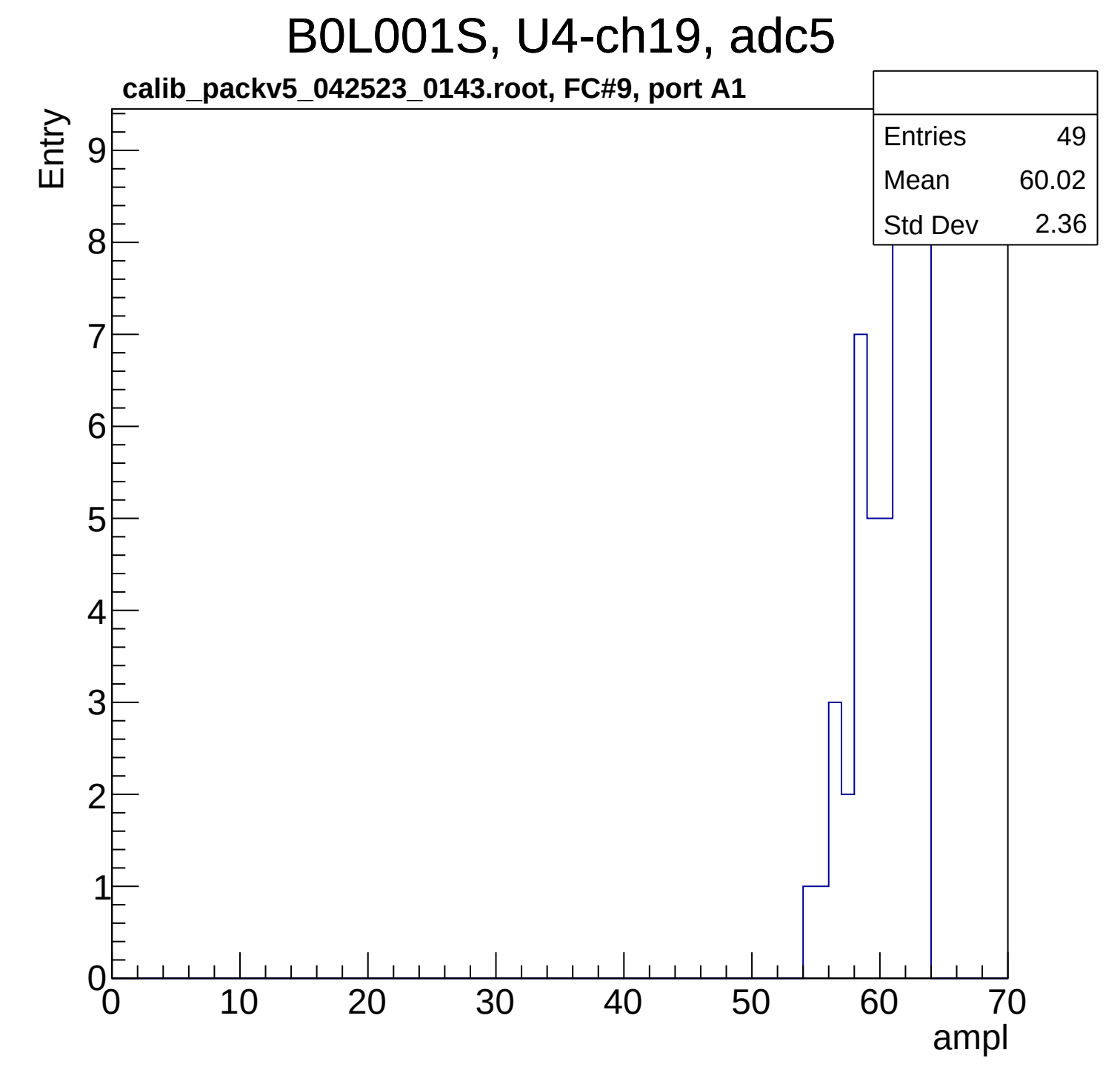
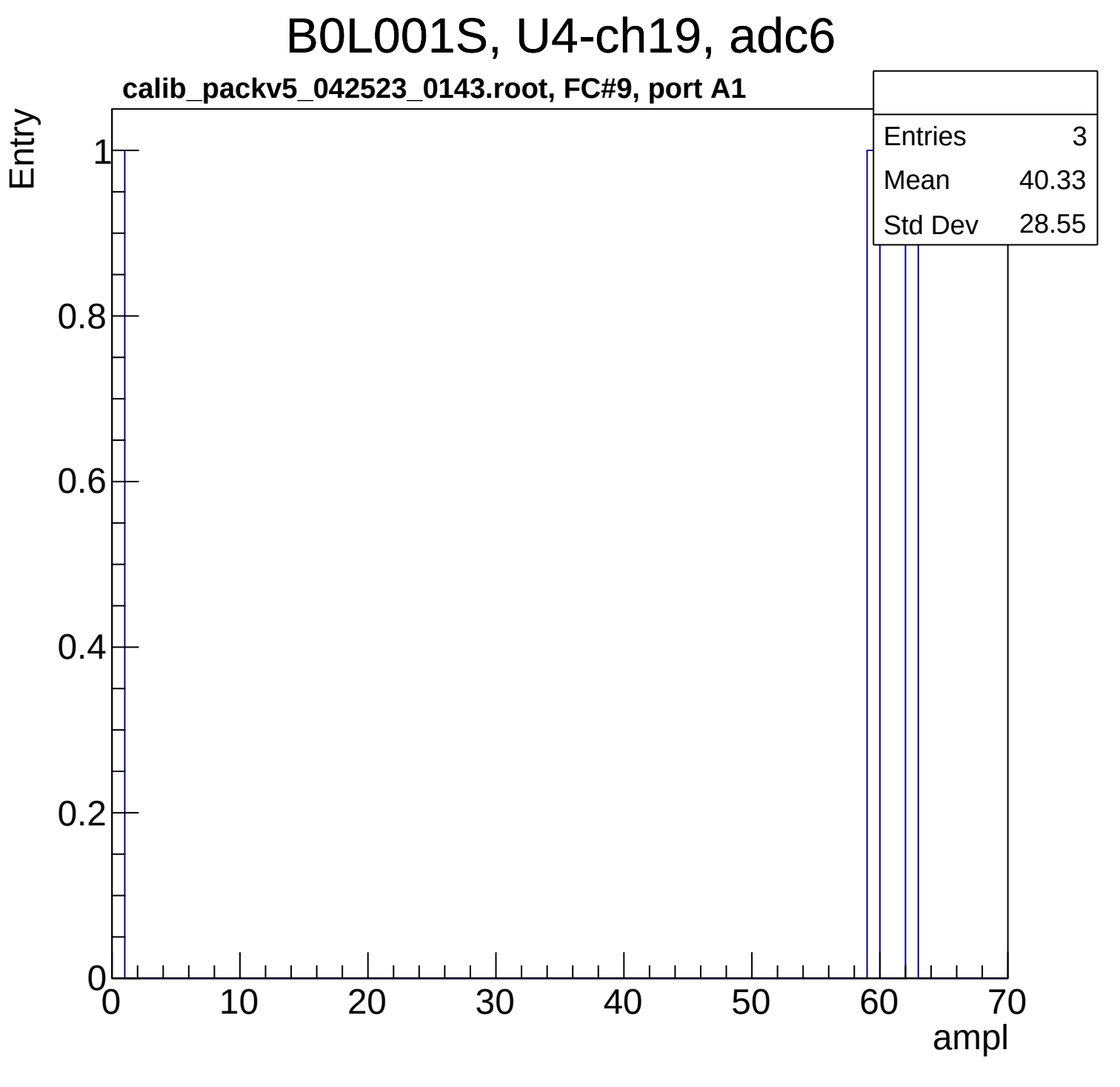
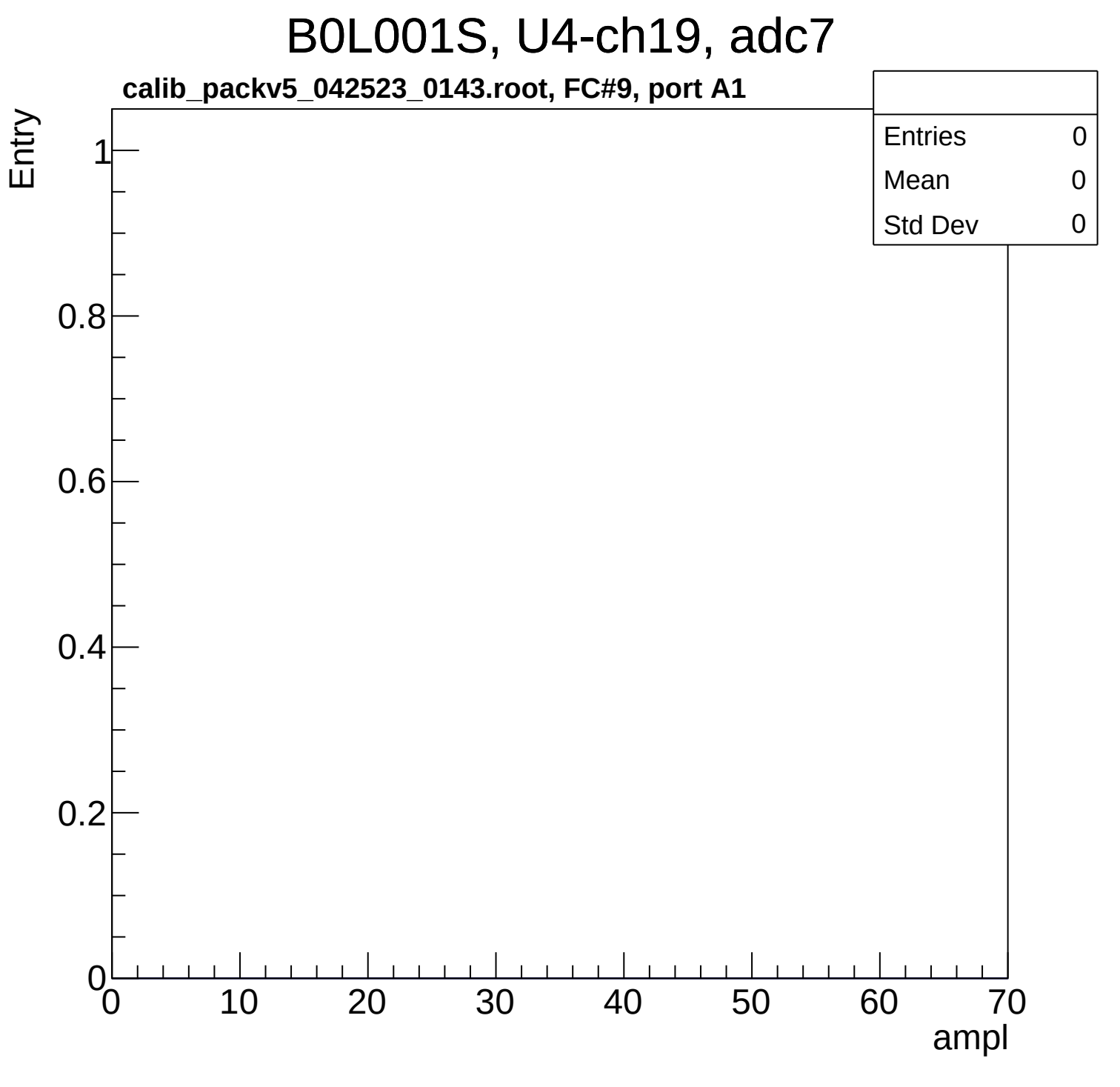
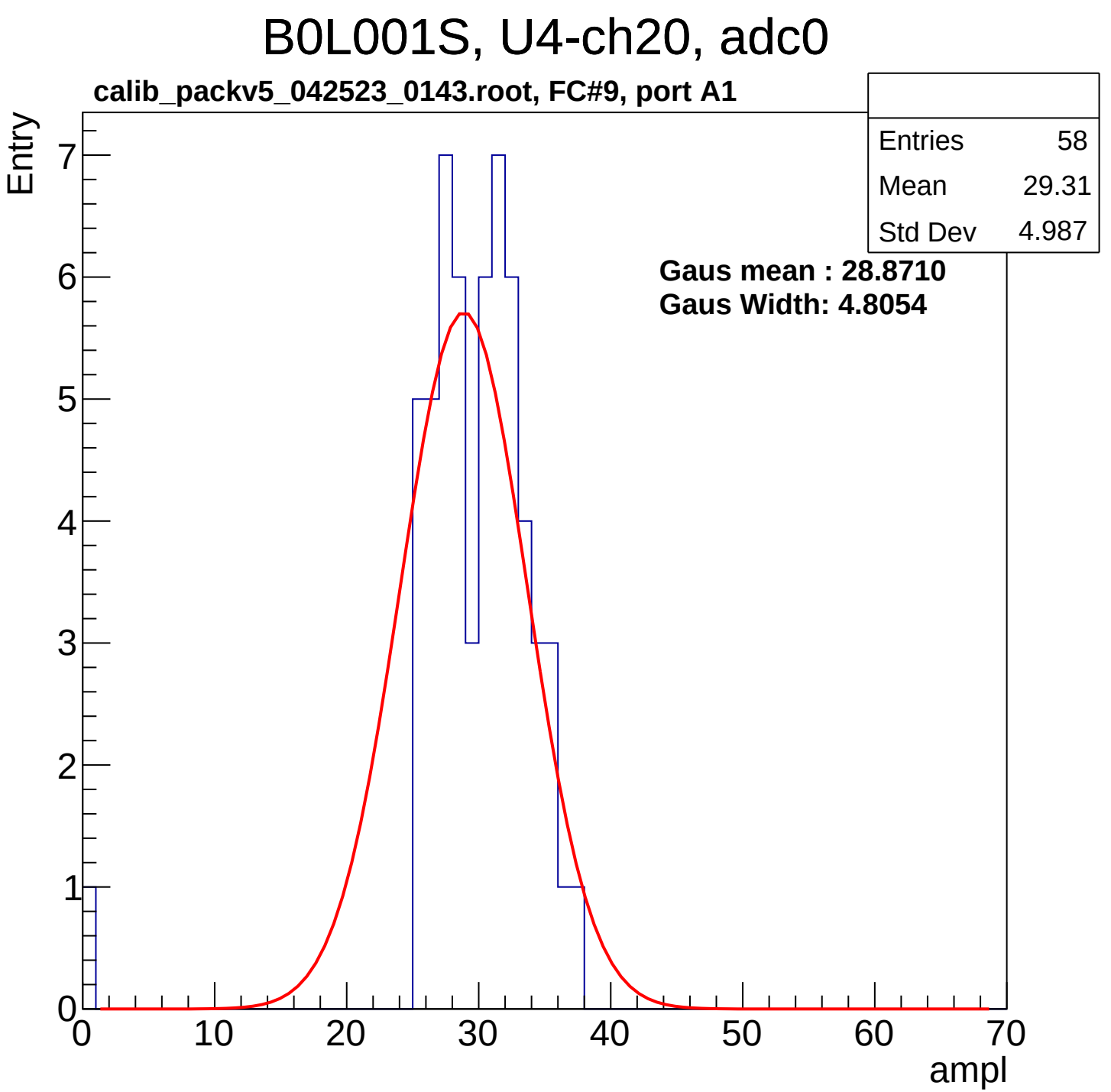
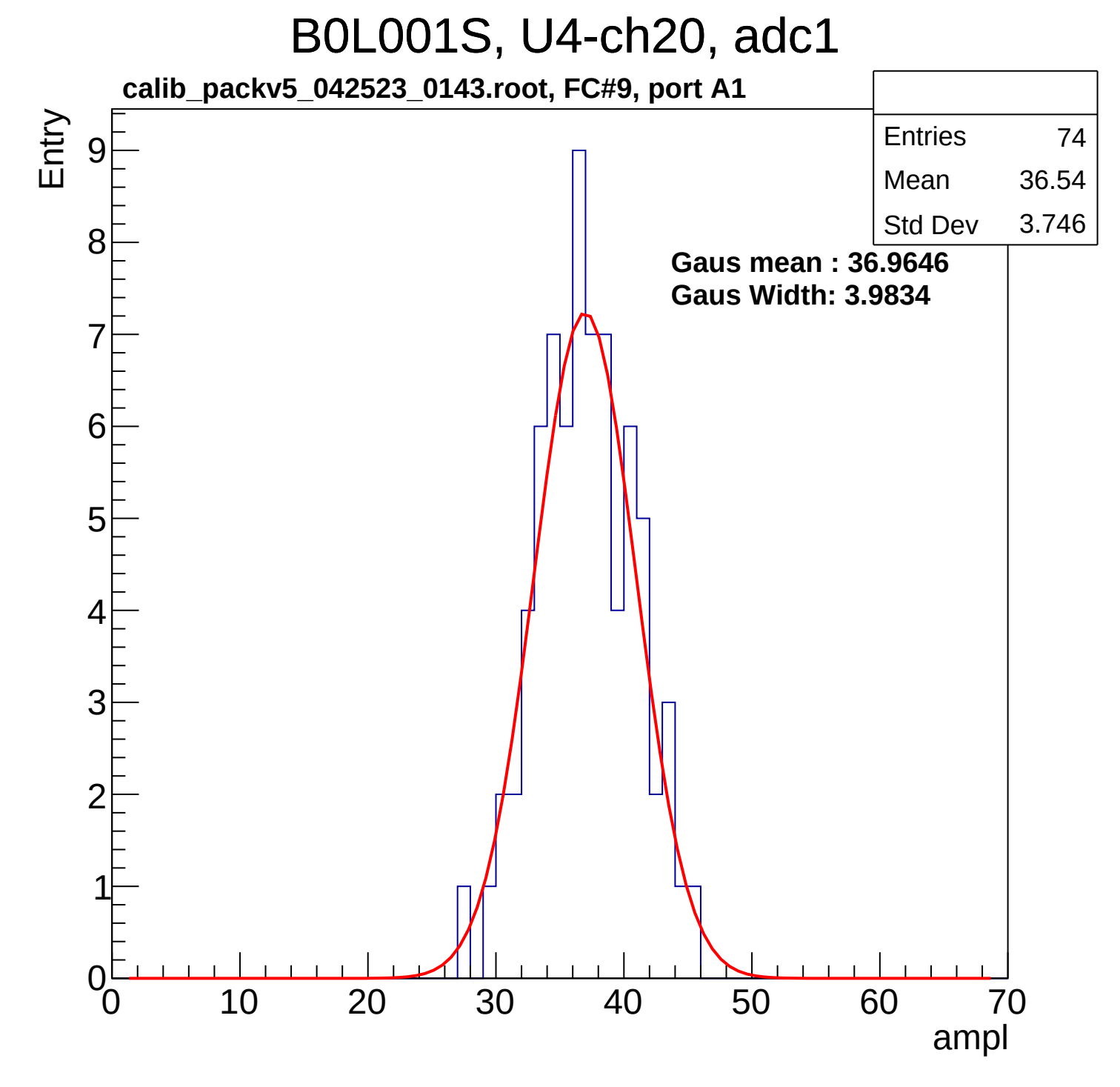

B0L001S, U4-ch19, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 56.33
Std Dev 3.353
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch19, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 60.02
Std Dev 2.36
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch19, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 40.33
Std Dev 28.55
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch19, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch20, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 29.31
Std Dev 4.987
Gaus mean : 28.8710
Gaus Width: 4.8054
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch20, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.54
Std Dev 3.746
Gaus mean : 36.9646
Gaus Width: 3.9834
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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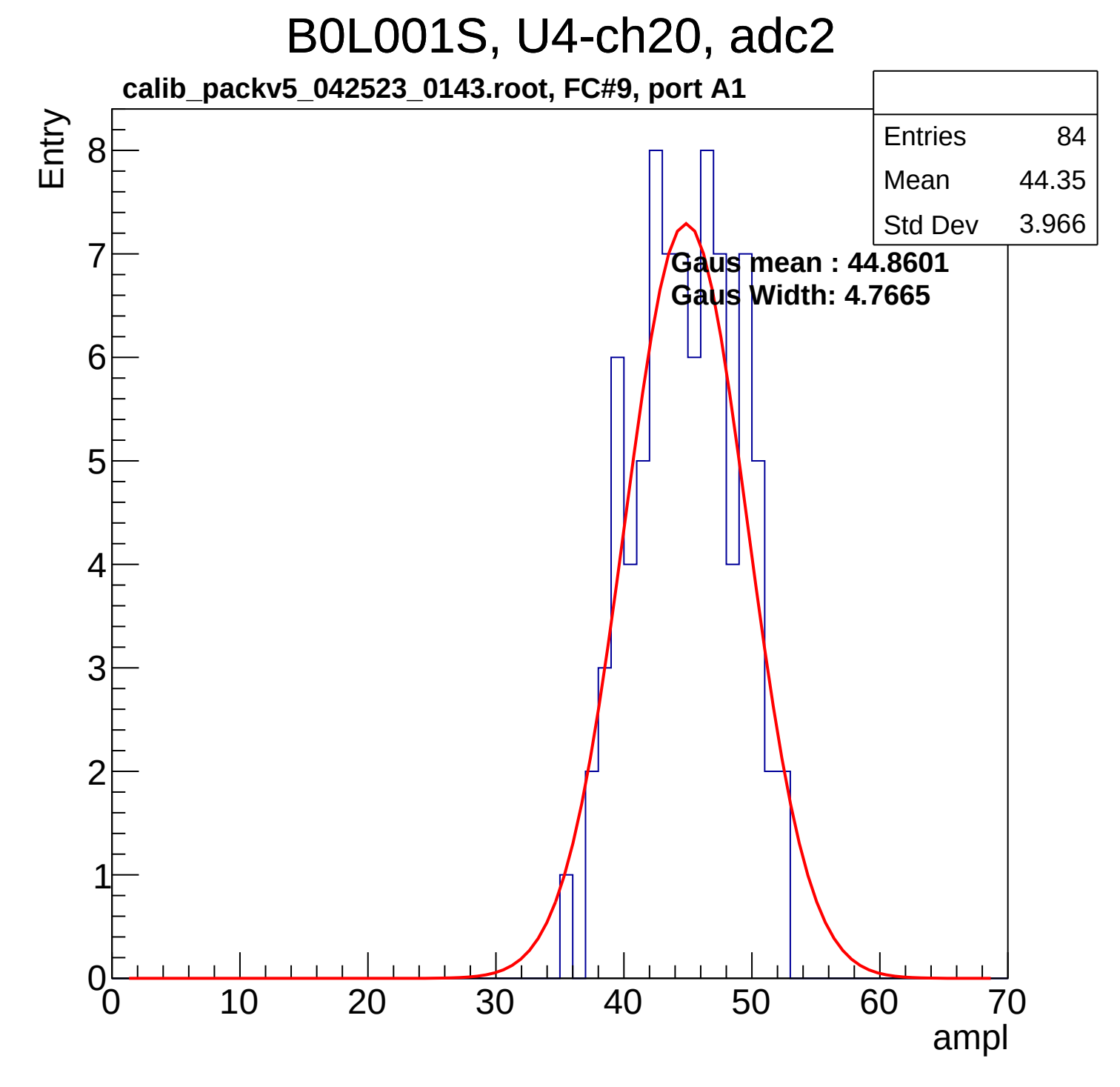
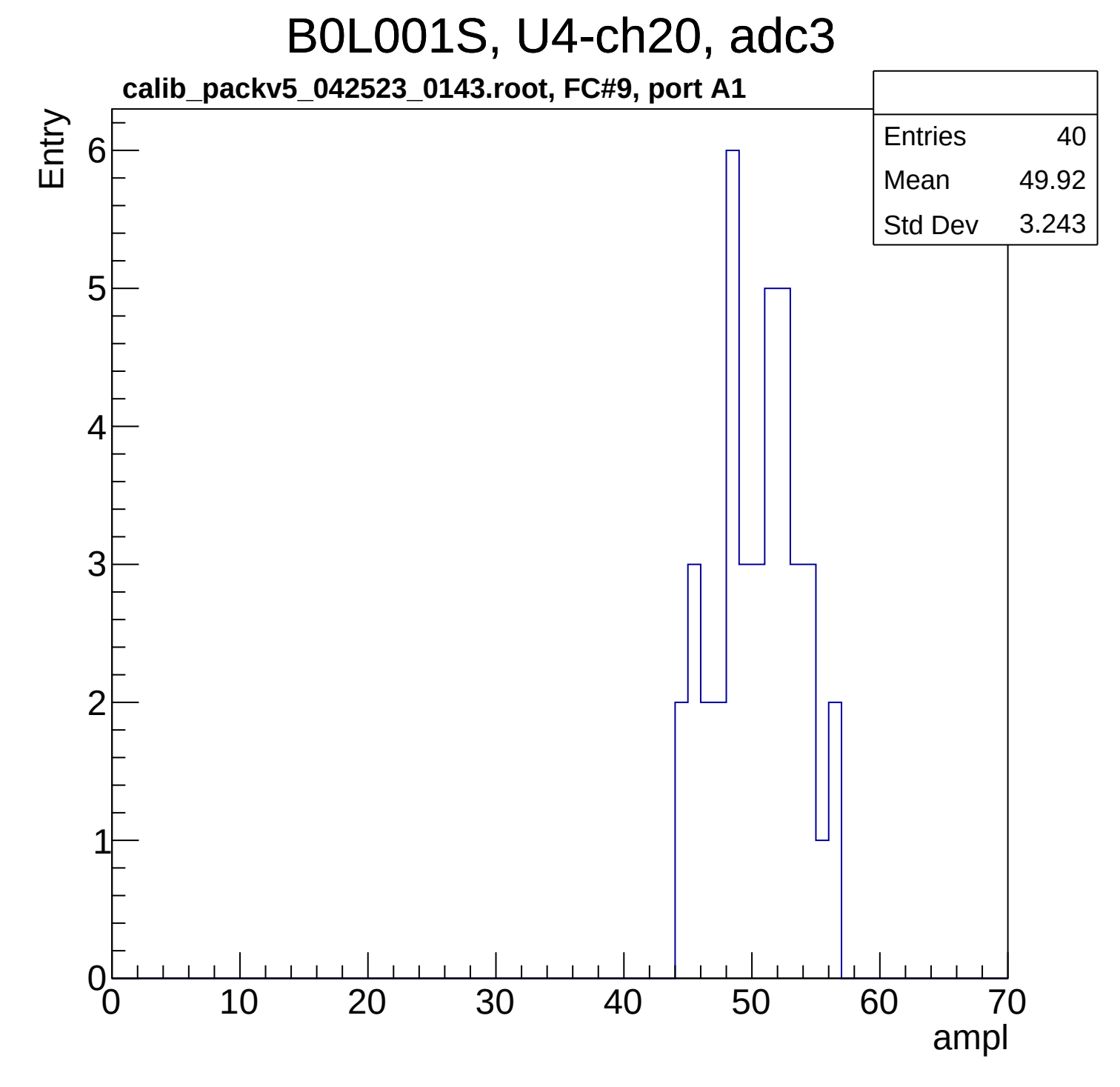
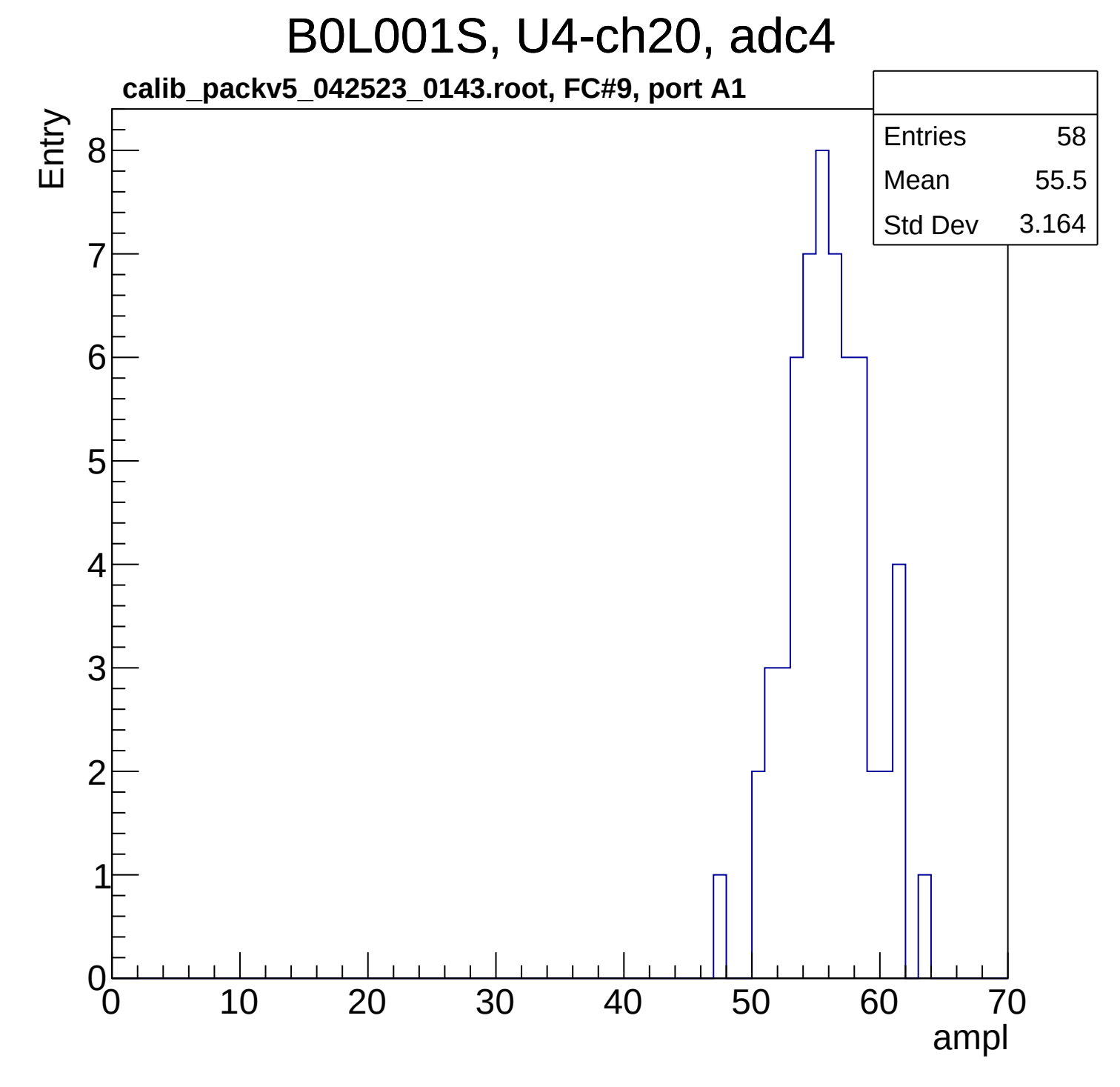
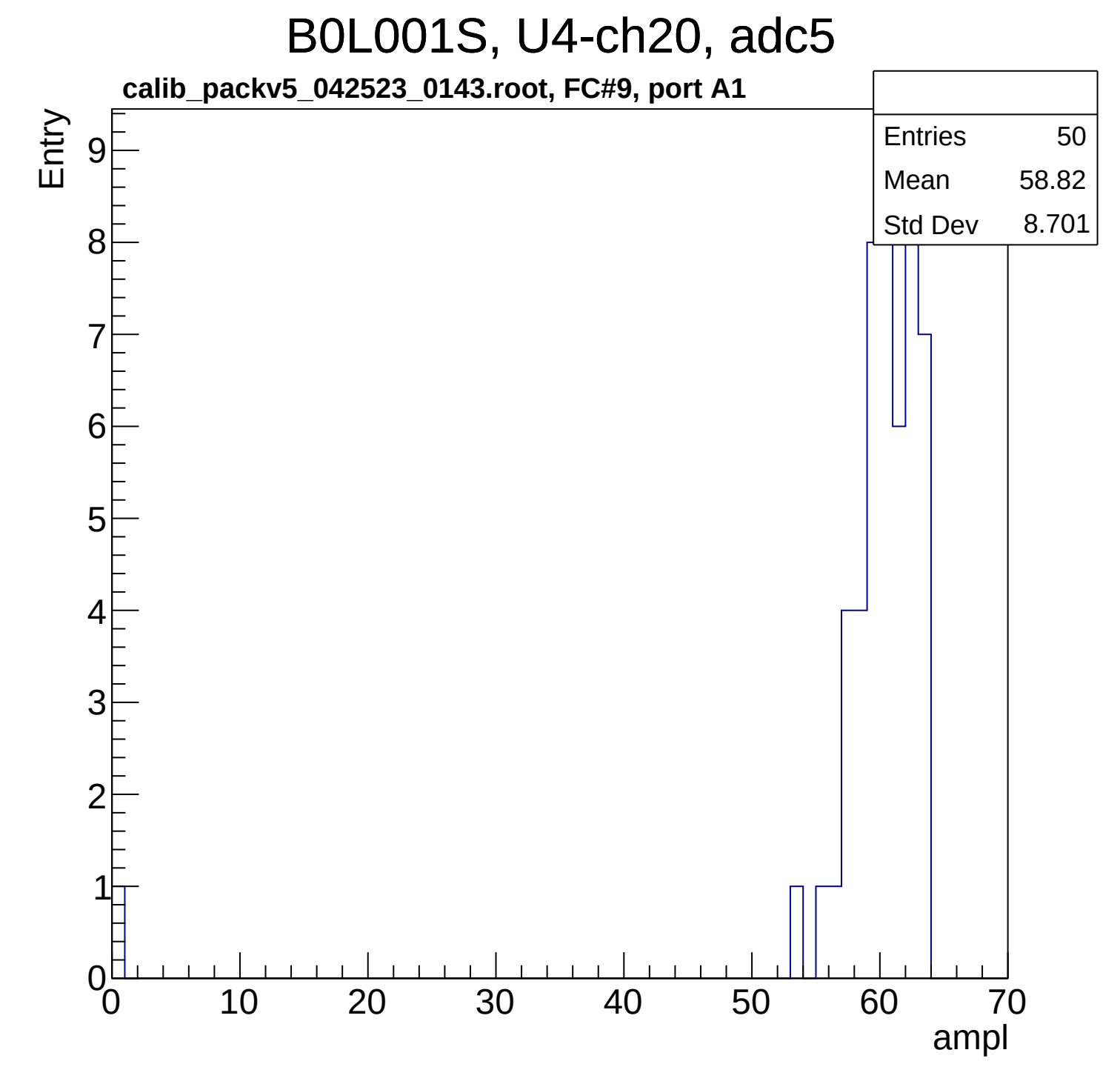
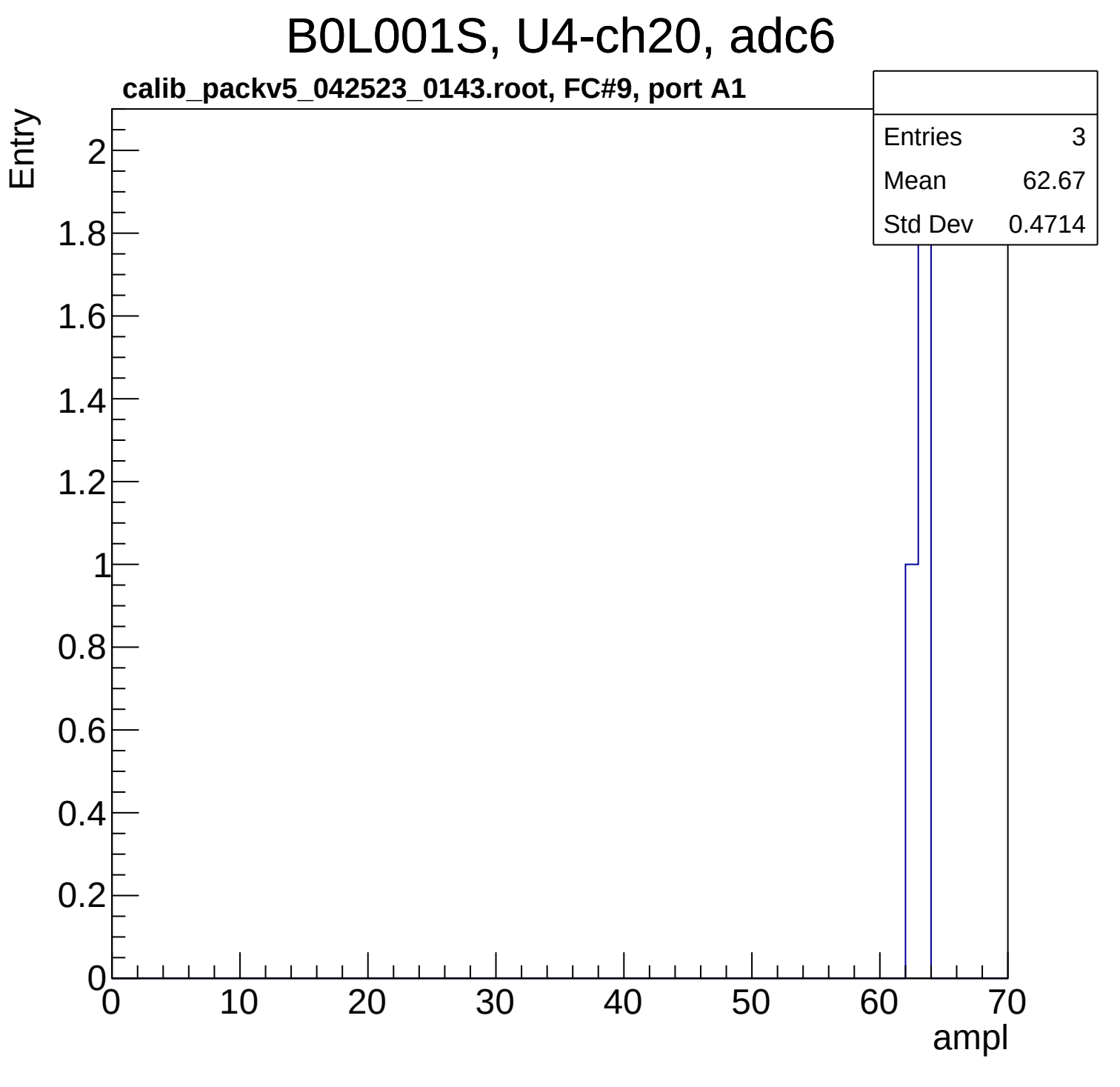
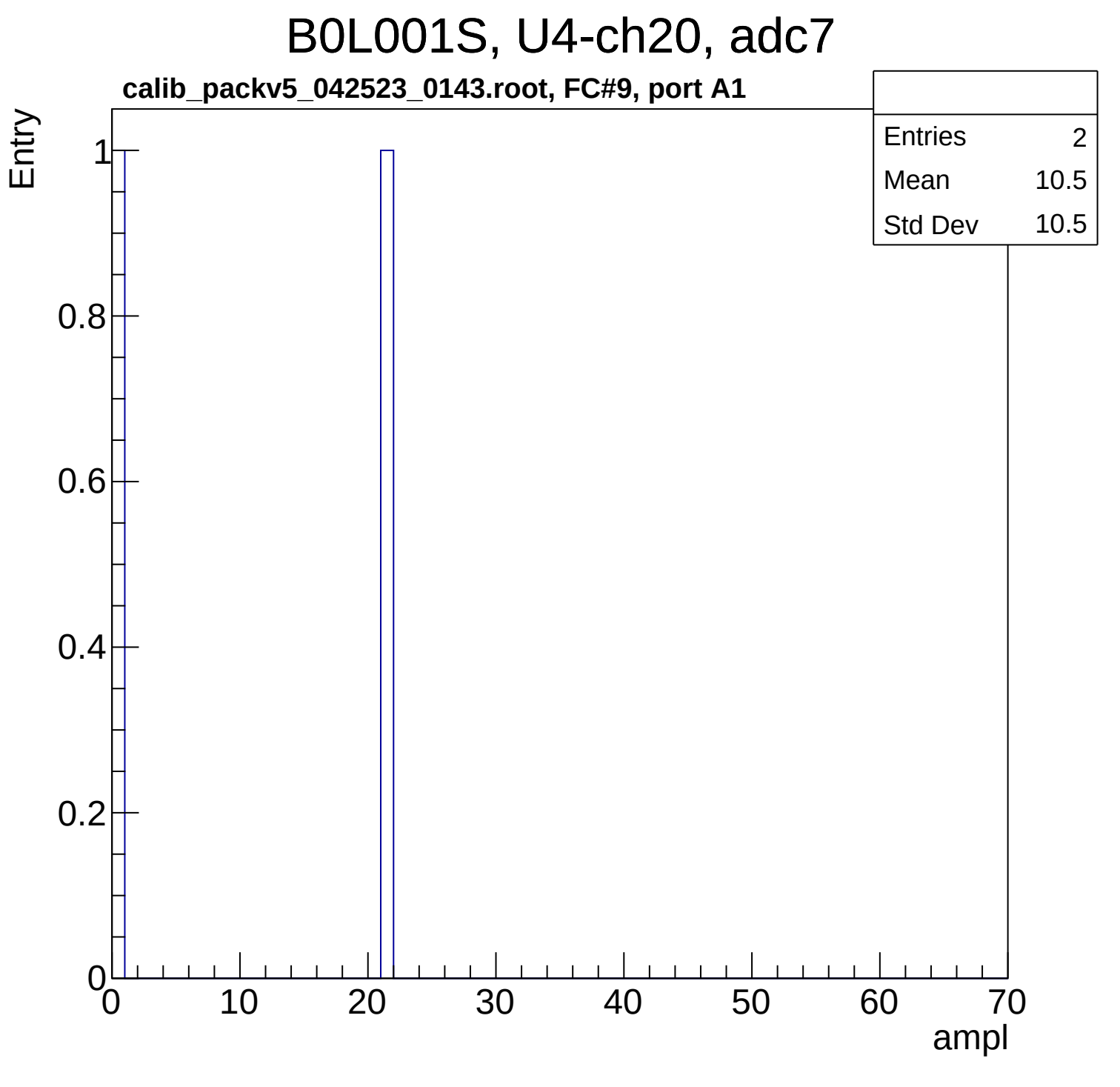

B0L001S, U4-ch20, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 44.35
Std Dev 3.966
Gaus mean : 44.8601
Gaus Width: 4.7665
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

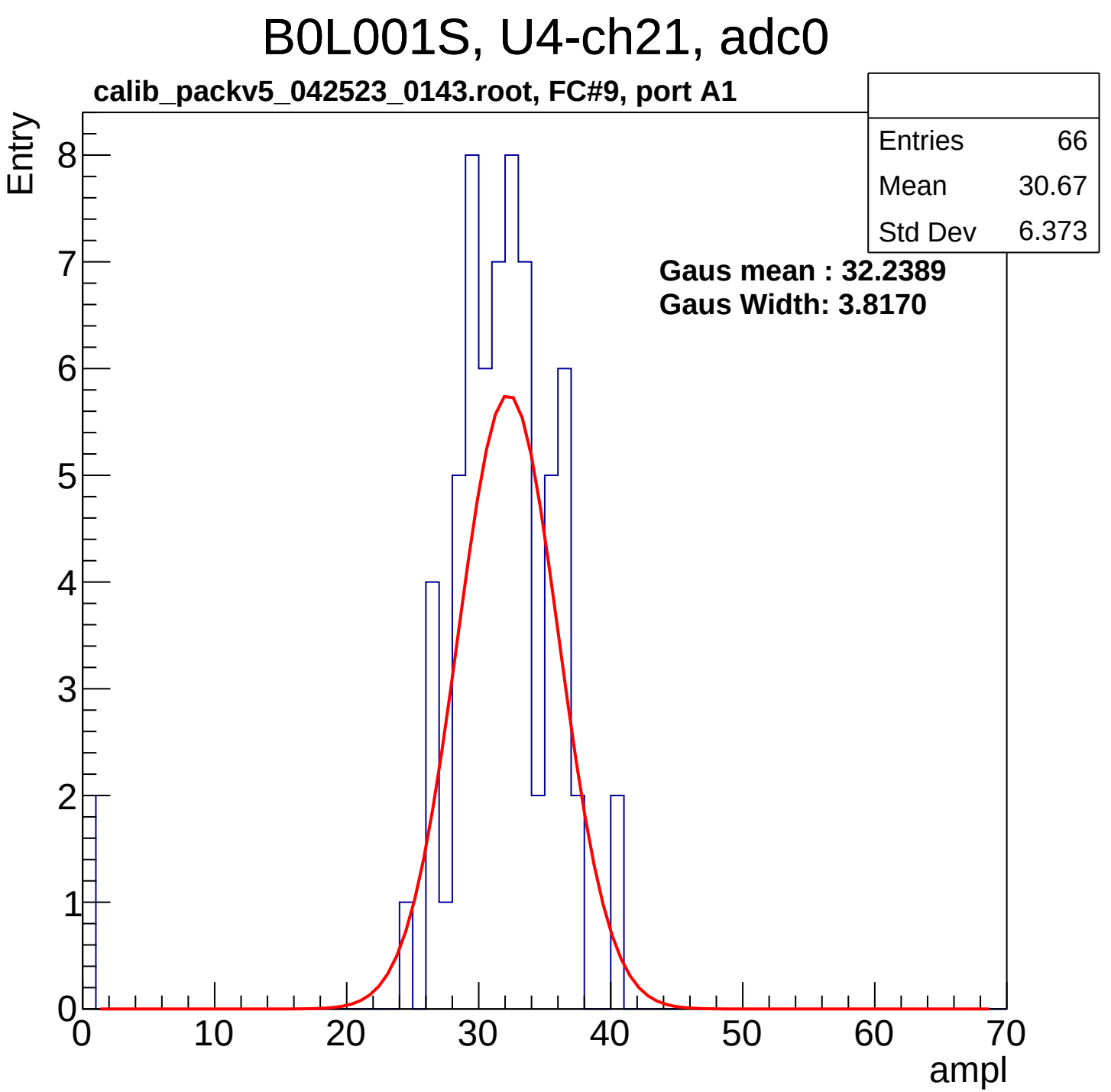

B0L001S, U4-ch20, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 49.92
Std Dev 3.243
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

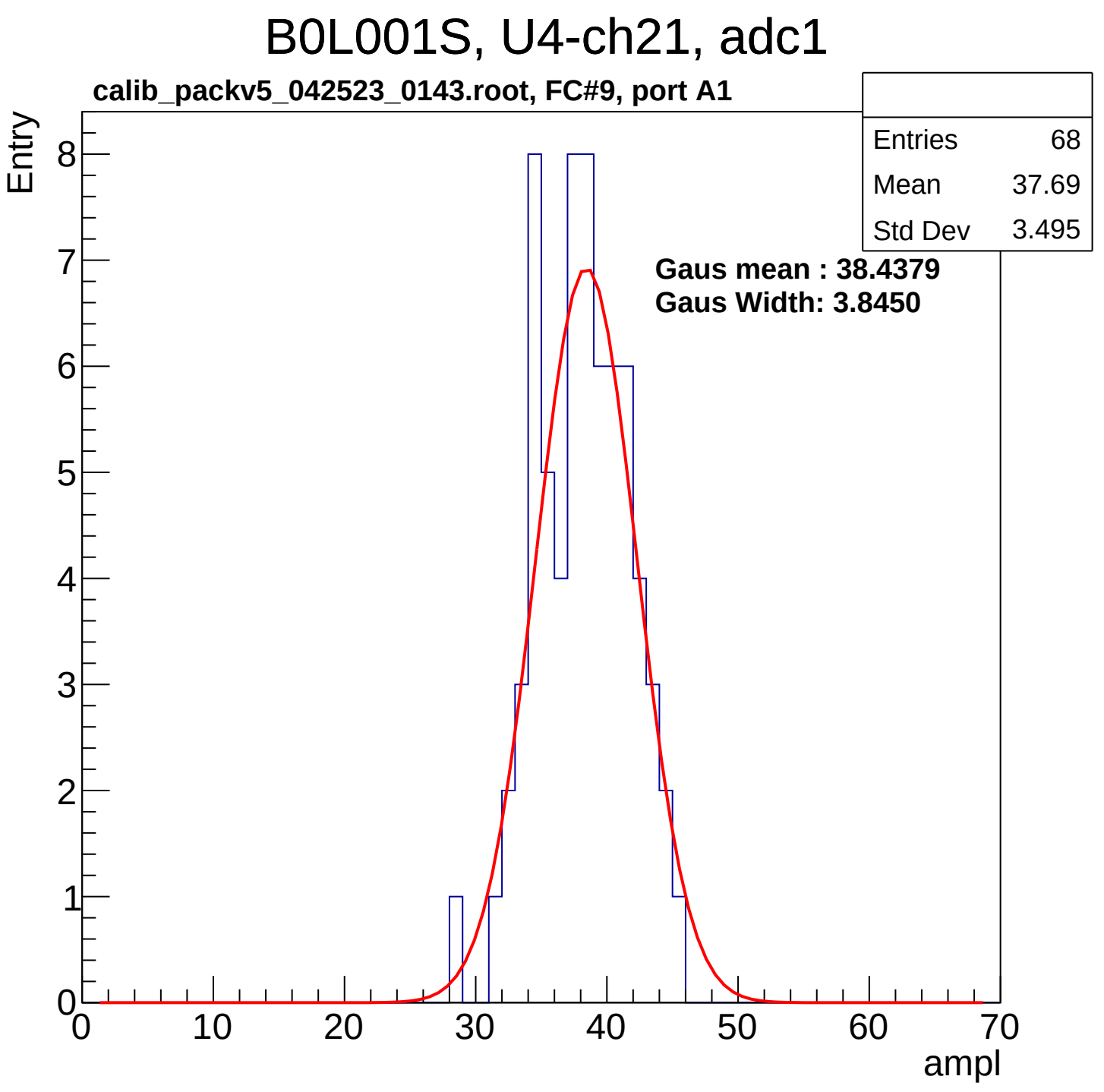

B0L001S, U4-ch20, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.5
Std Dev 3.164
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

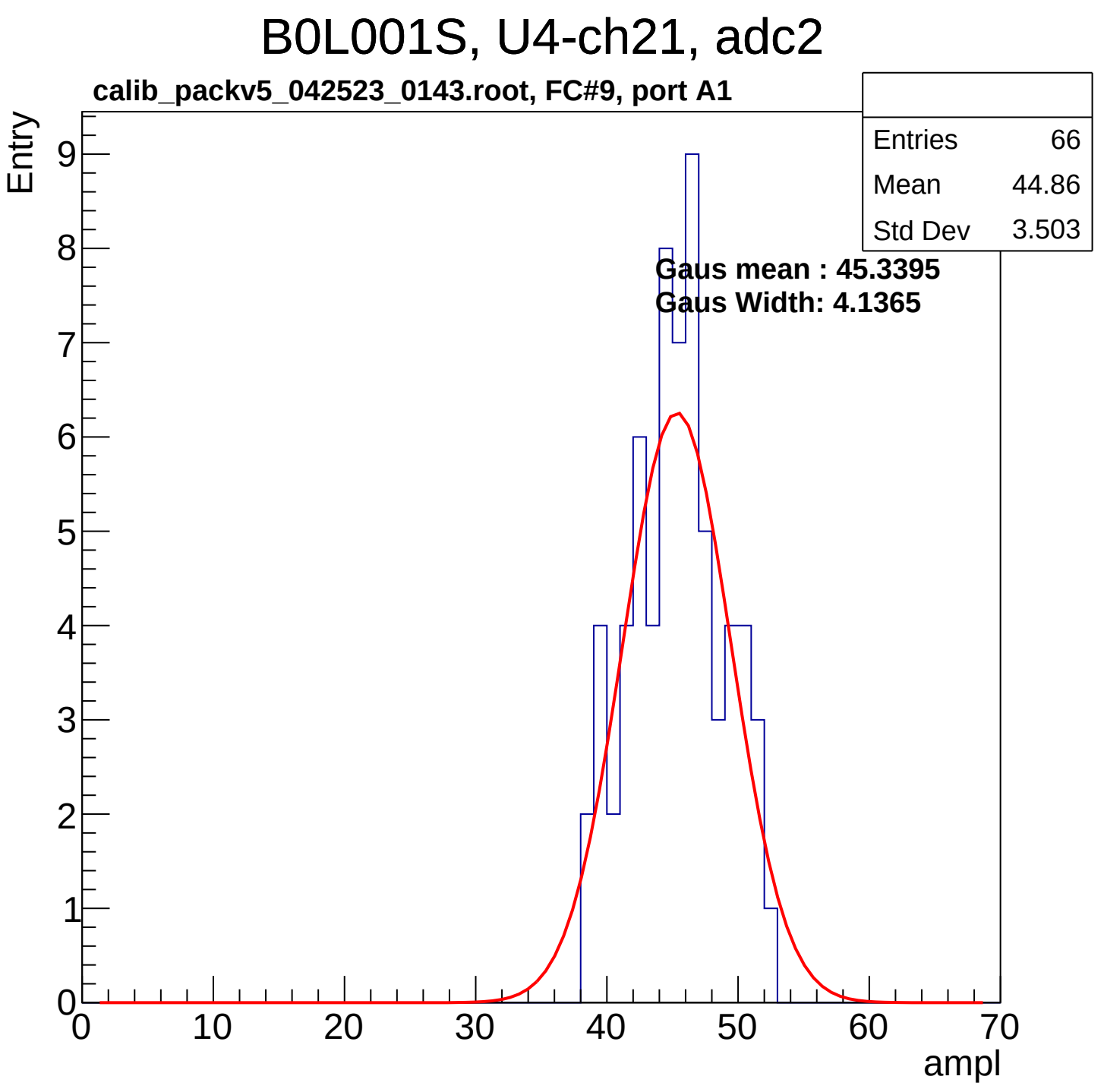

B0L001S, U4-ch20, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 58.82
Std Dev 8.701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

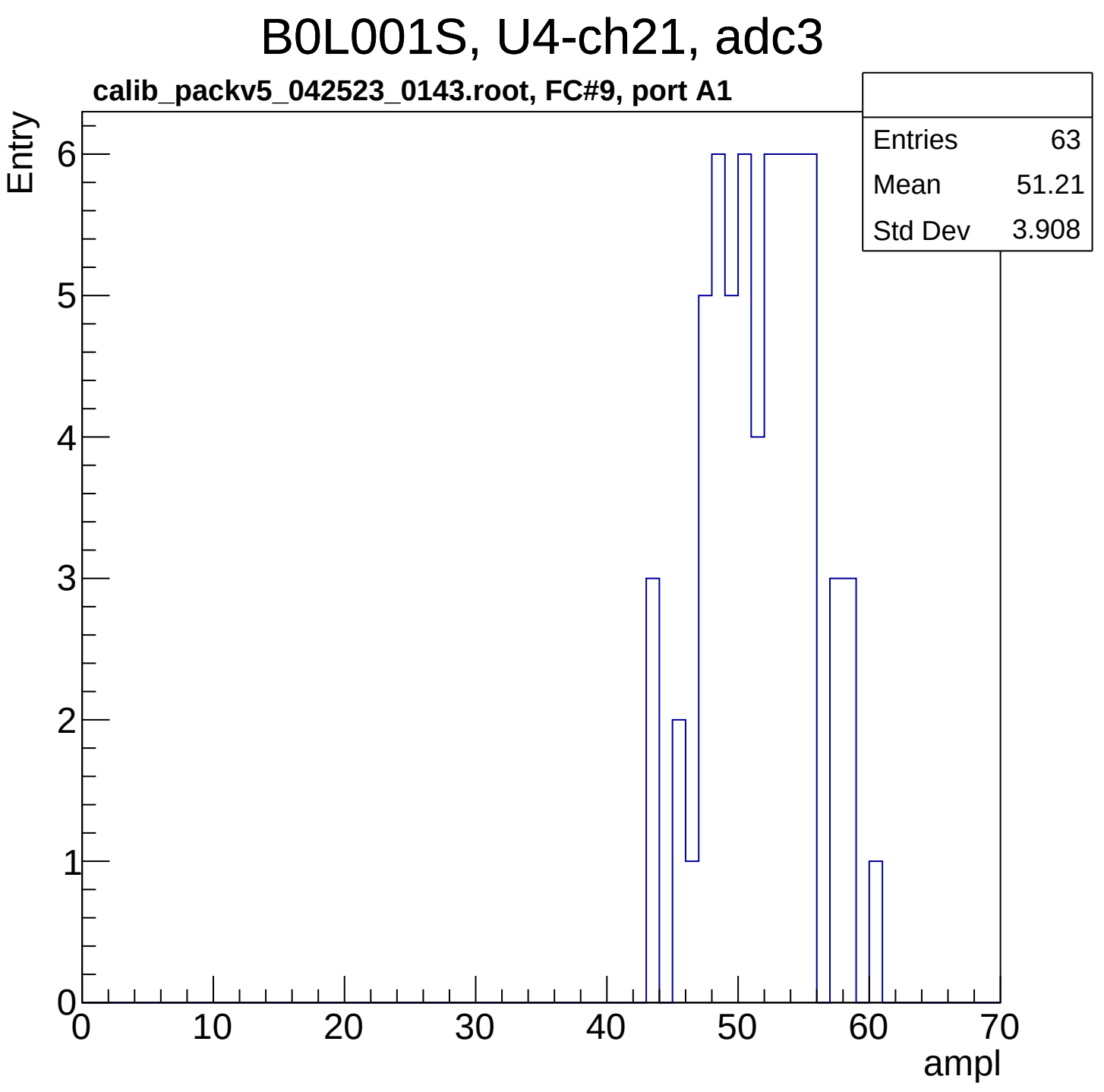

B0L001S, U4-ch20, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

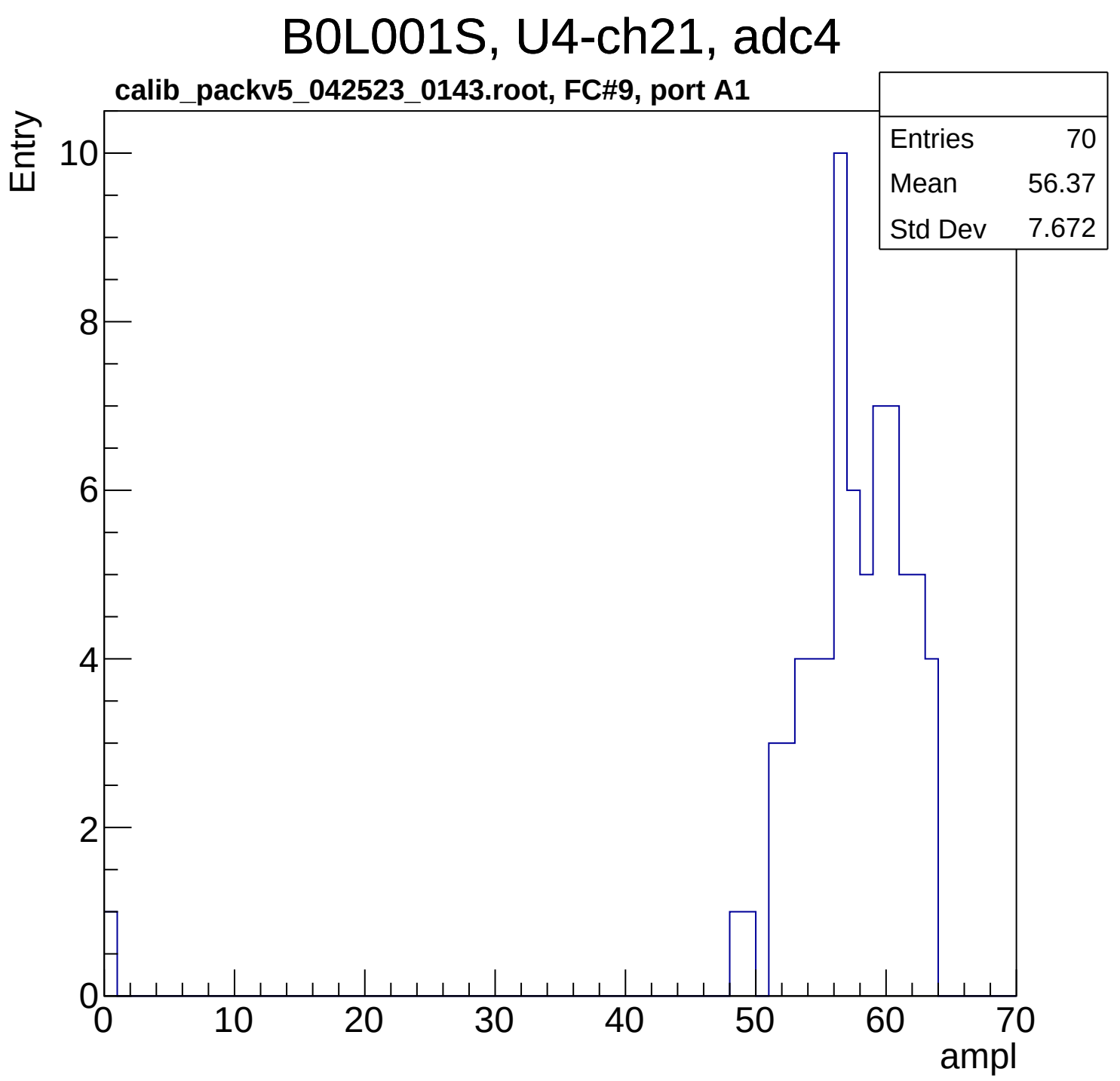

B0L001S, U4-ch20, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

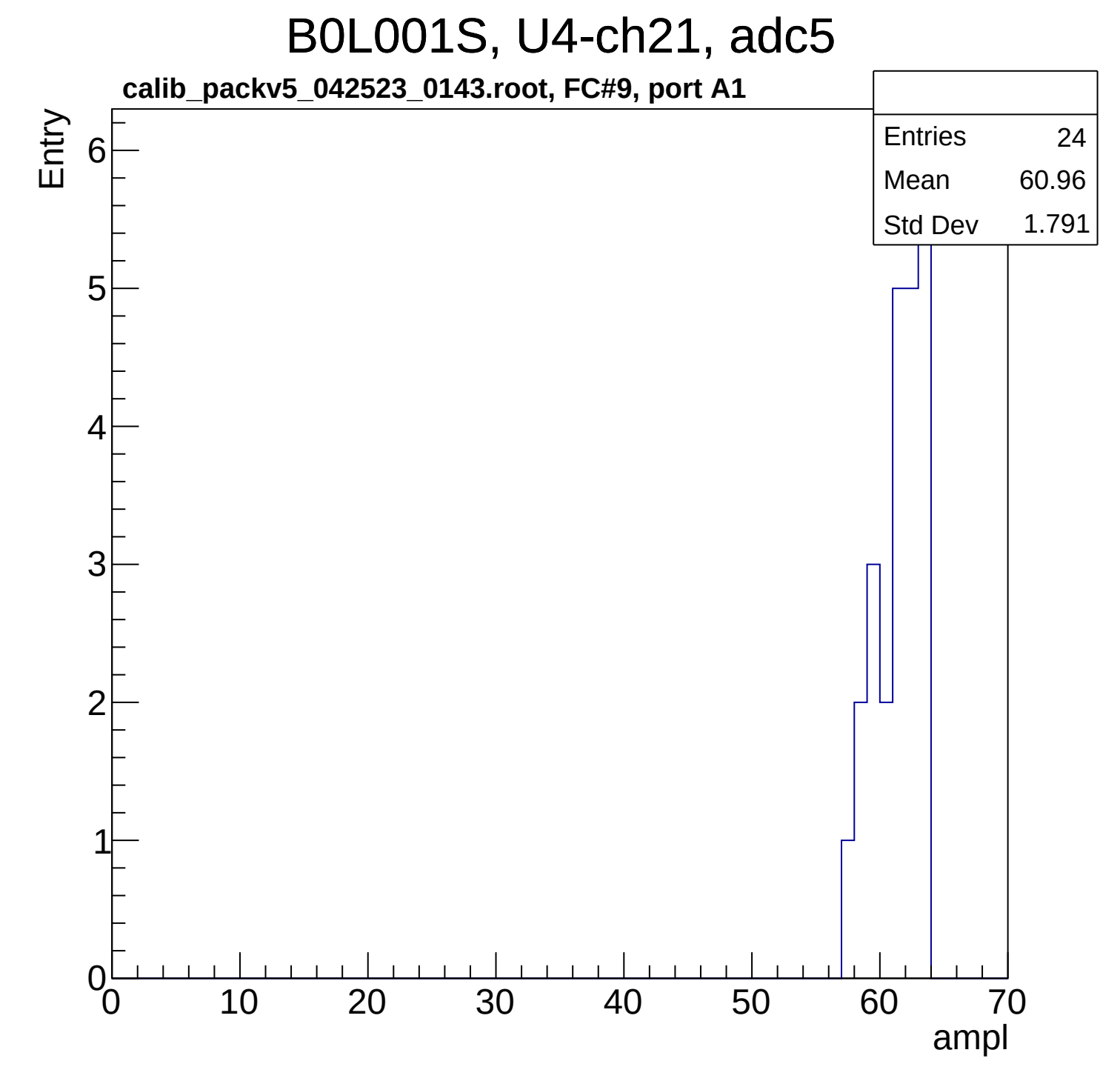

B0L001S, U4-ch21, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.67
Std Dev 6.373
Gaus mean : 32.2389
Gaus Width: 3.8170
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

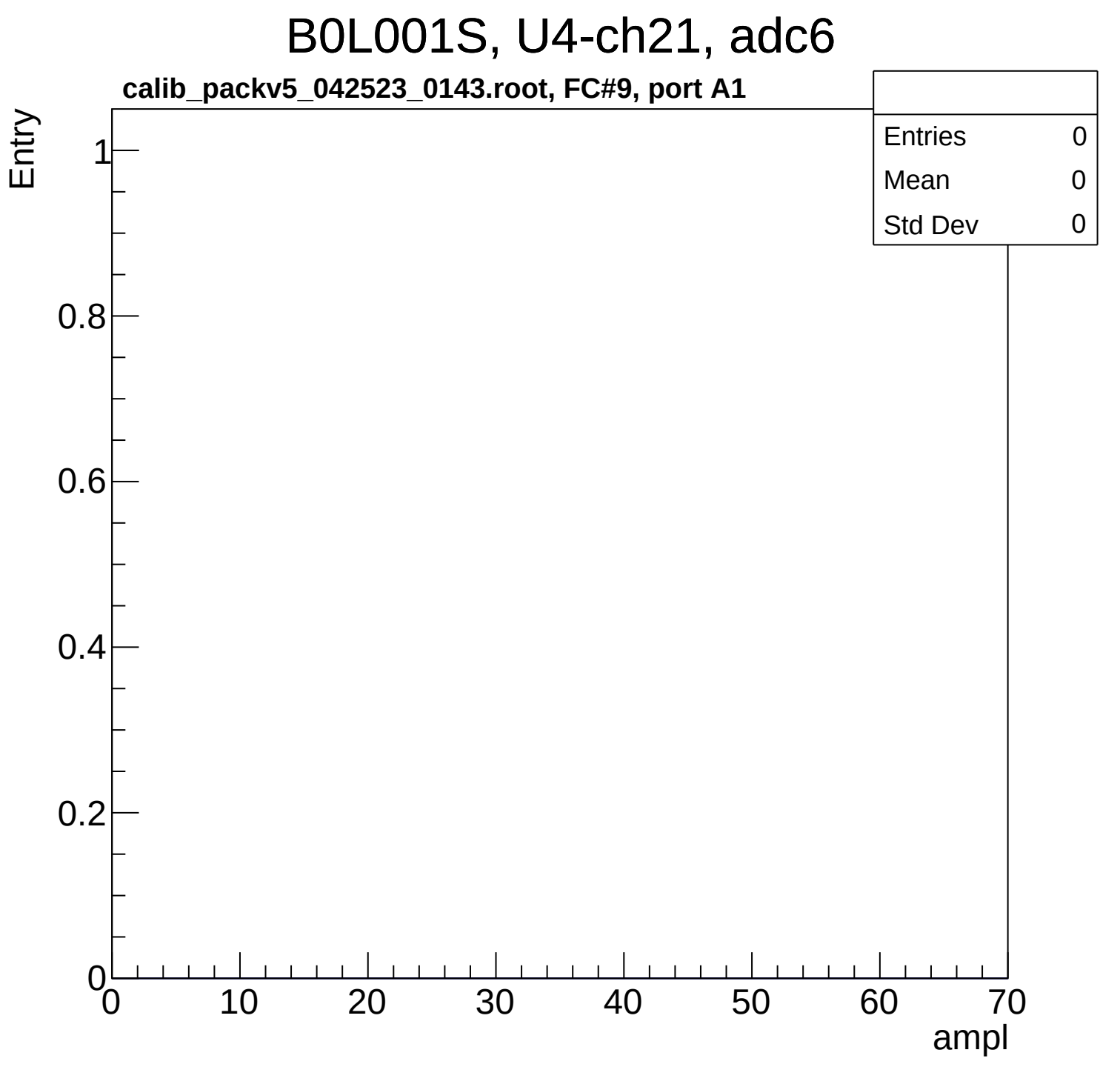

B0L001S, U4-ch21, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.69
Std Dev 3.495
Gaus mean : 38.4379
Gaus Width: 3.8450
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

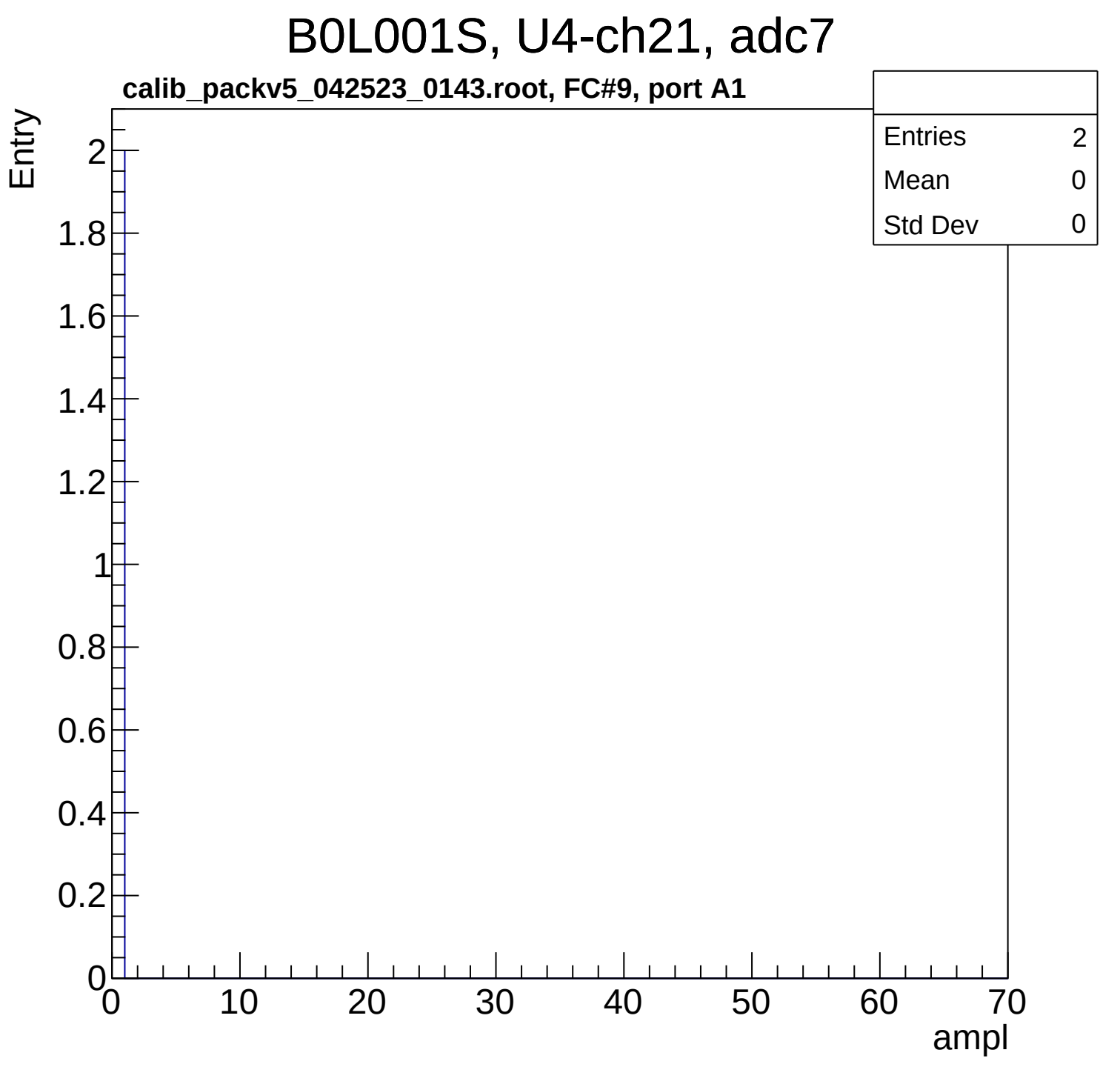

B0L001S, U4-ch21, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.86
Std Dev 3.503
Gaus mean : 45.3395
Gaus Width: 4.1365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

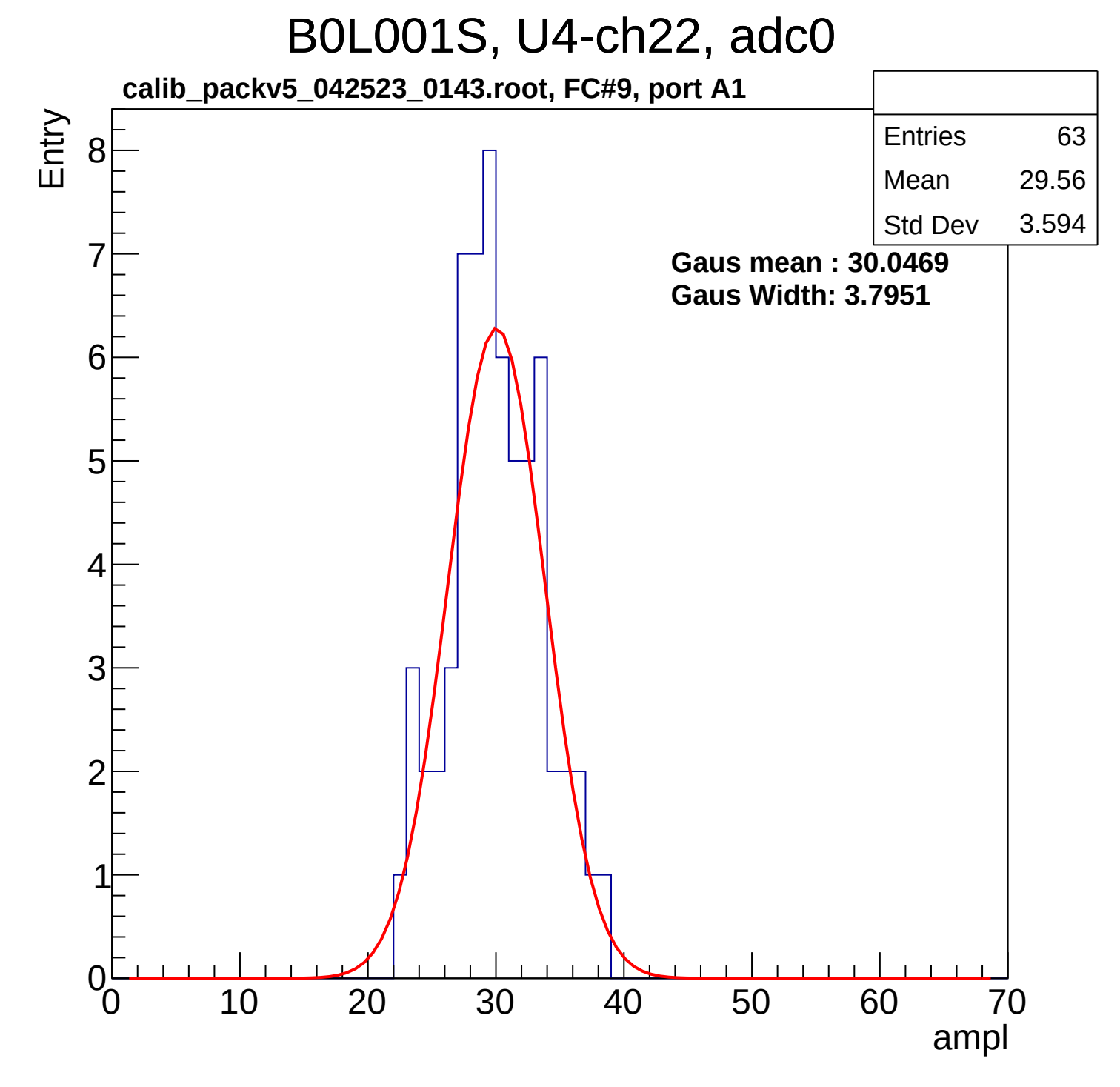

B0L001S, U4-ch21, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.21
Std Dev 3.908
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

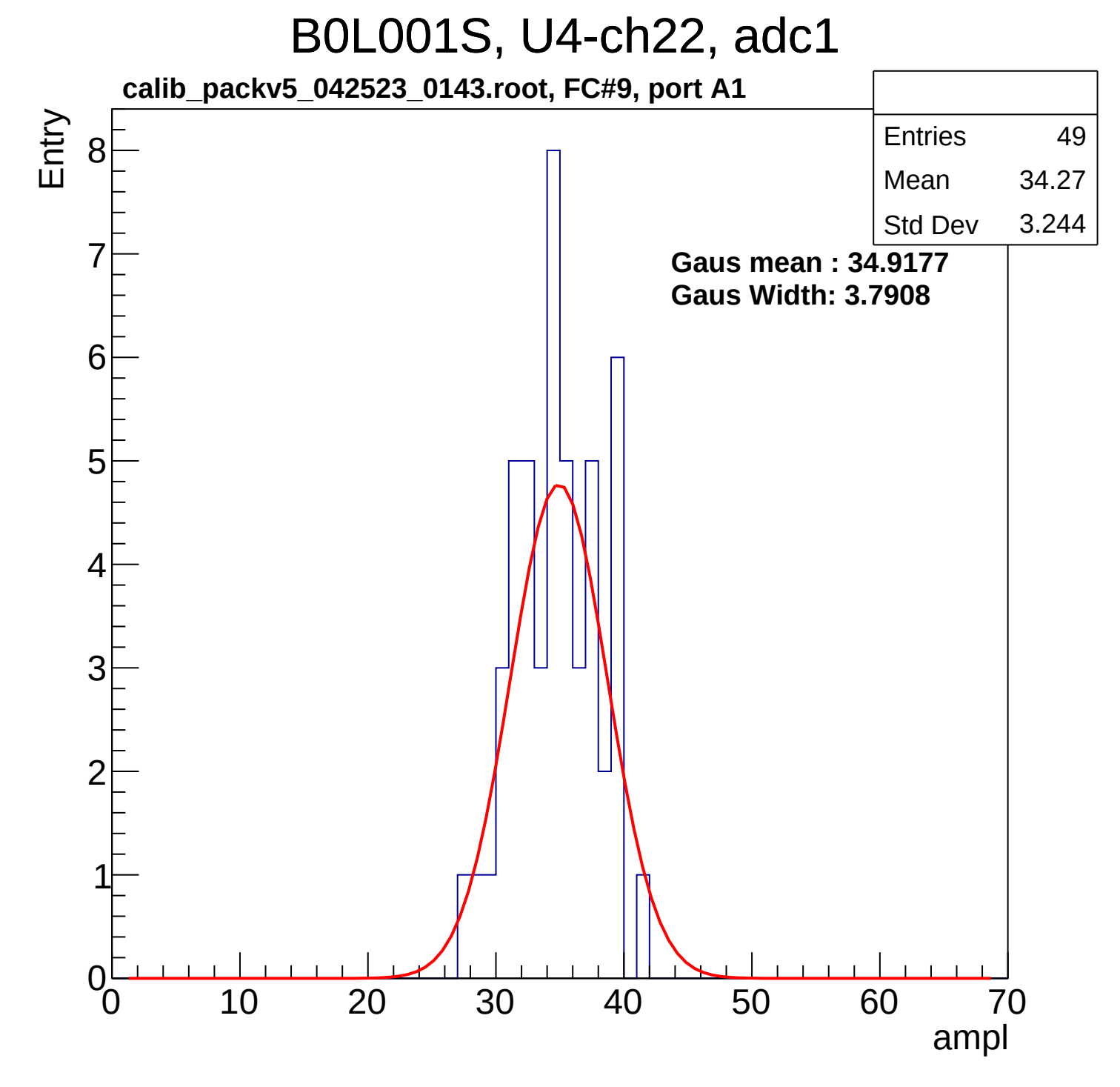

B0L001S, U4-ch21, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 56.37
Std Dev 7.672
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

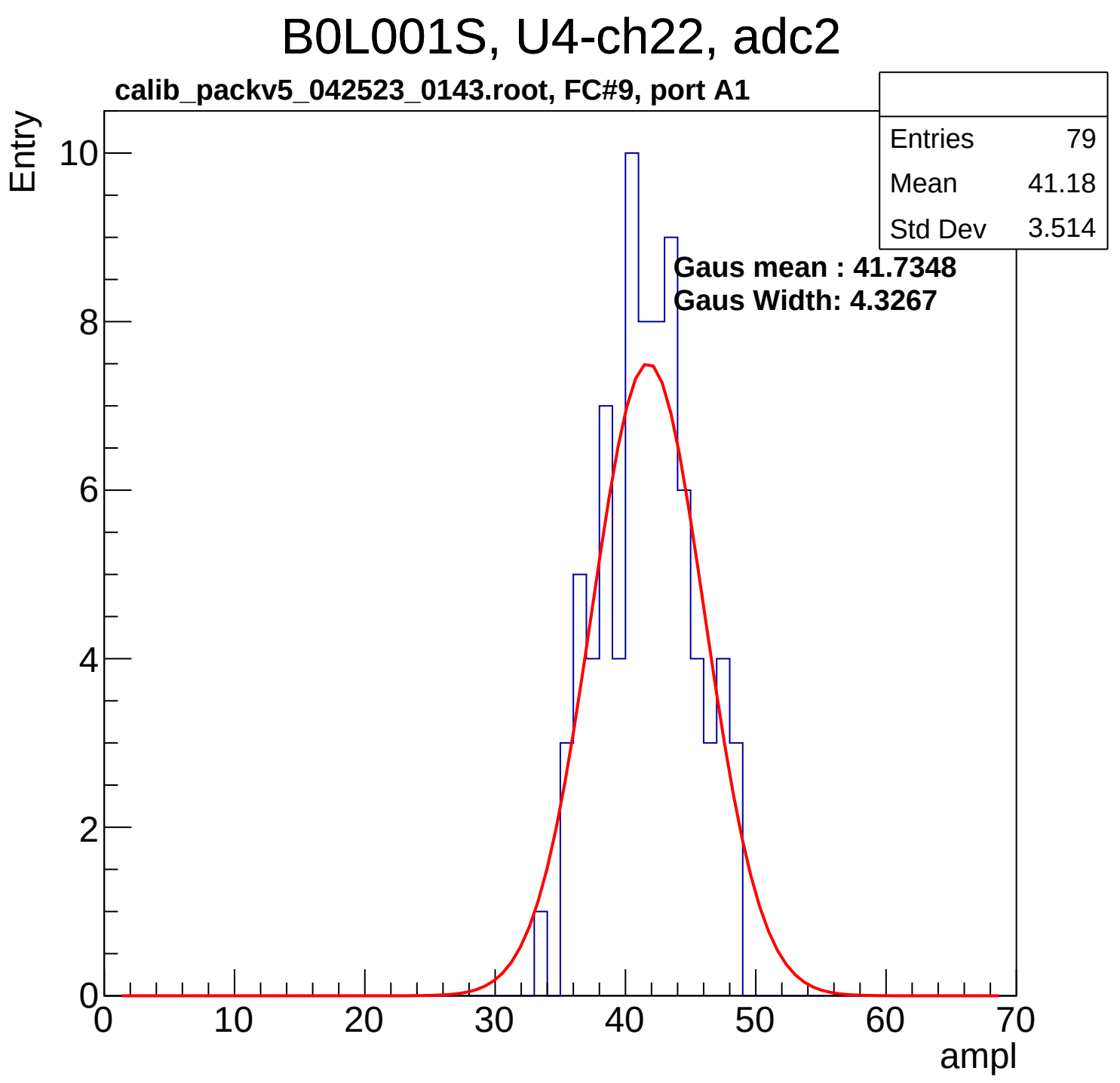

B0L001S, U4-ch21, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 60.96
Std Dev 1.791
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

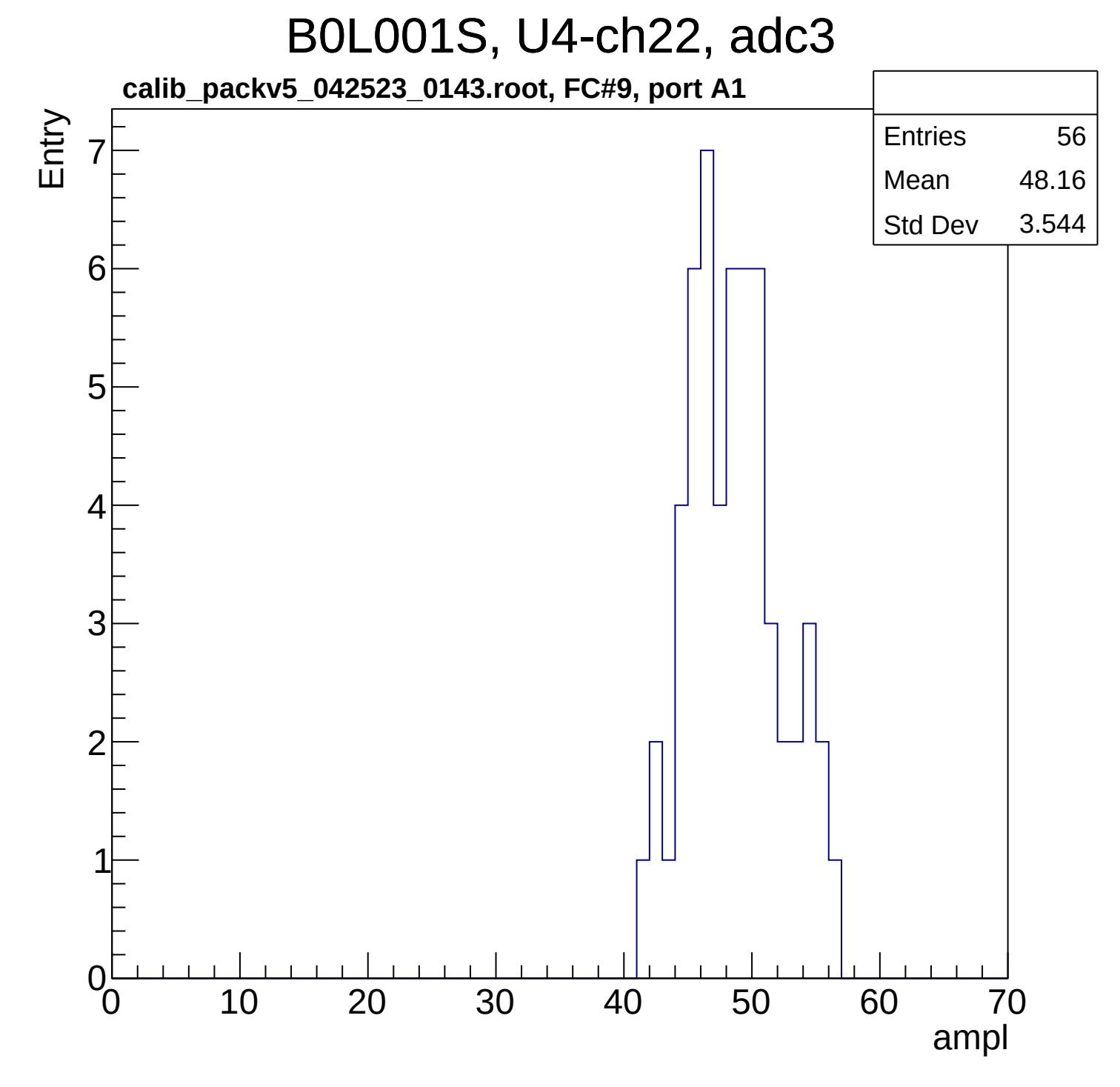

B0L001S, U4-ch21, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch21, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch22, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.56
Std Dev 3.594
Gaus mean : 30.0469
Gaus Width: 3.7951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch22, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 34.27
Std Dev 3.244
Gaus mean : 34.9177
Gaus Width: 3.7908
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch22, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 41.18
Std Dev 3.514
Gaus mean : 41.7348
Gaus Width: 4.3267
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch22, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 48.16
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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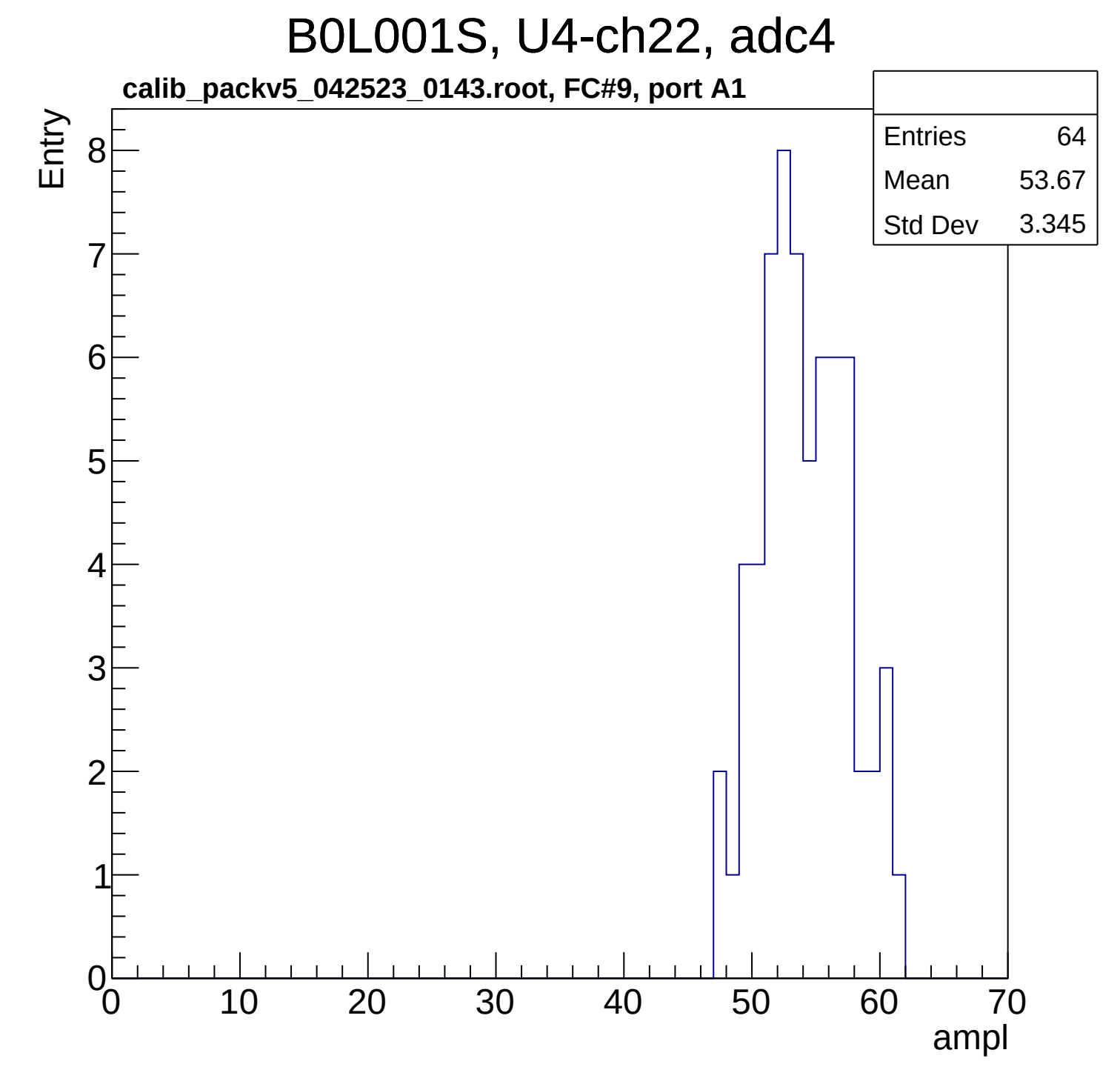
B0L001S, U4-ch22, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 53.67
Std Dev 3.345
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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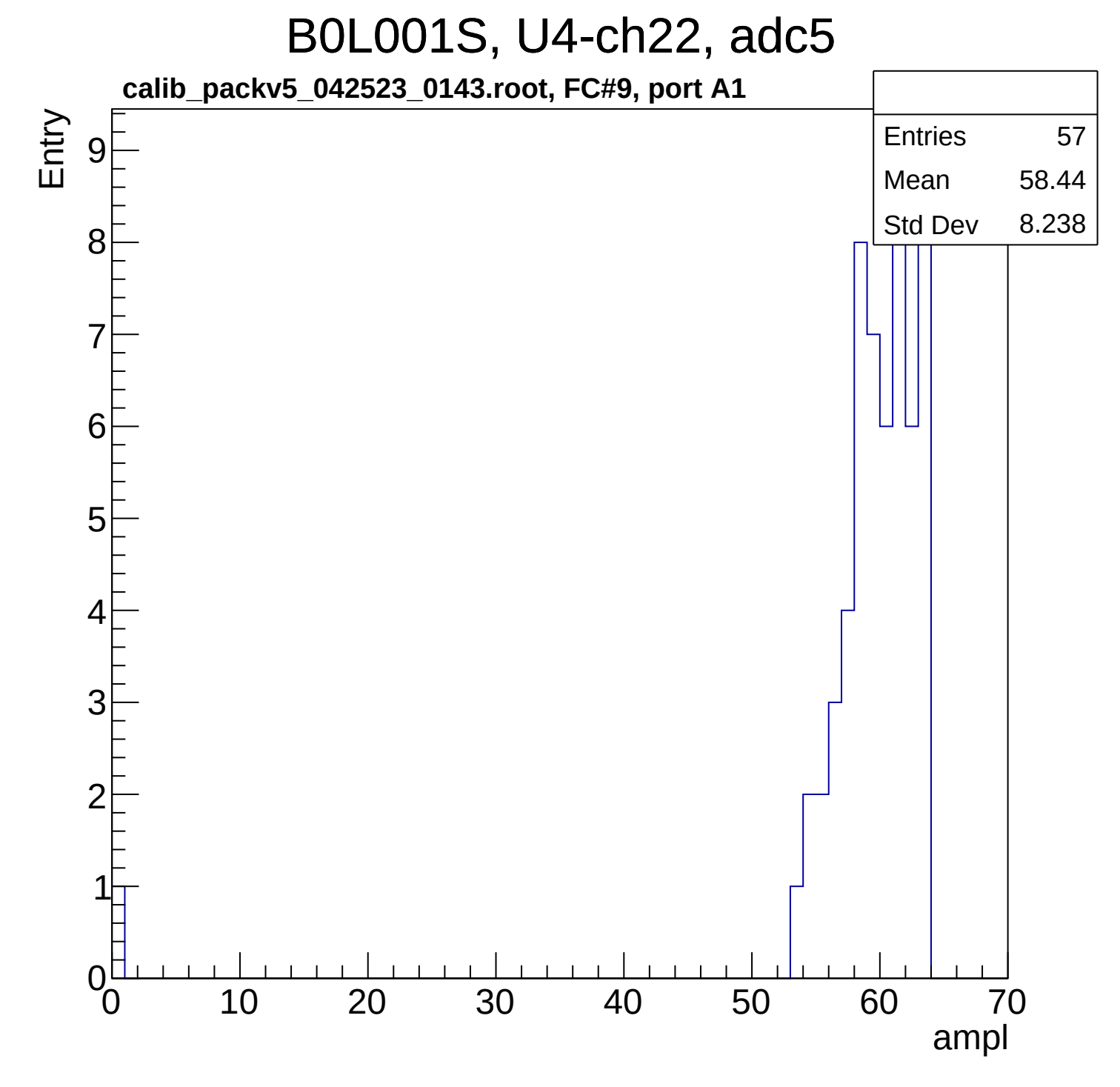
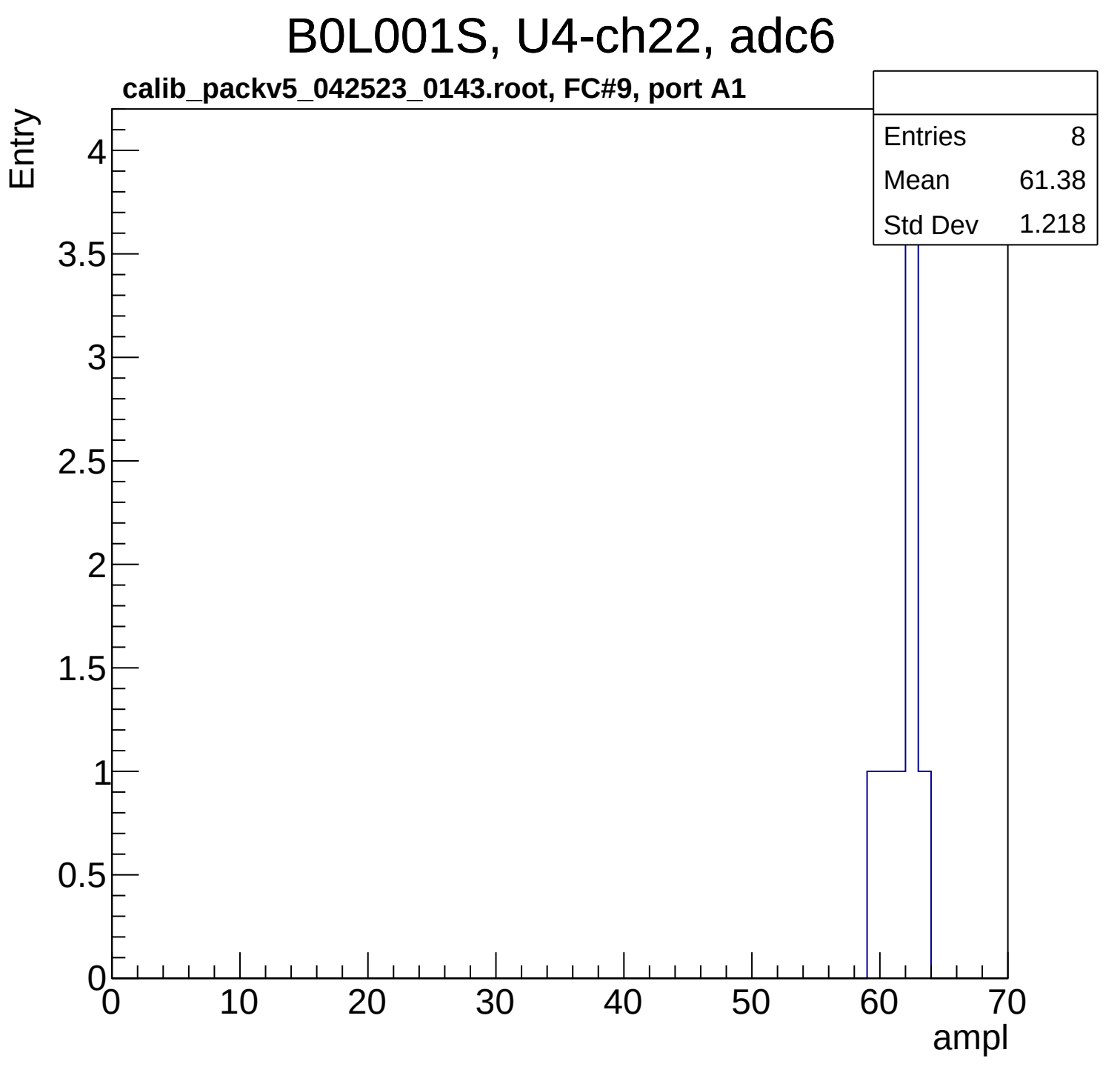

B0L001S, U4-ch22, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 58.44
Std Dev 8.238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch22, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.38
Std Dev 1.218
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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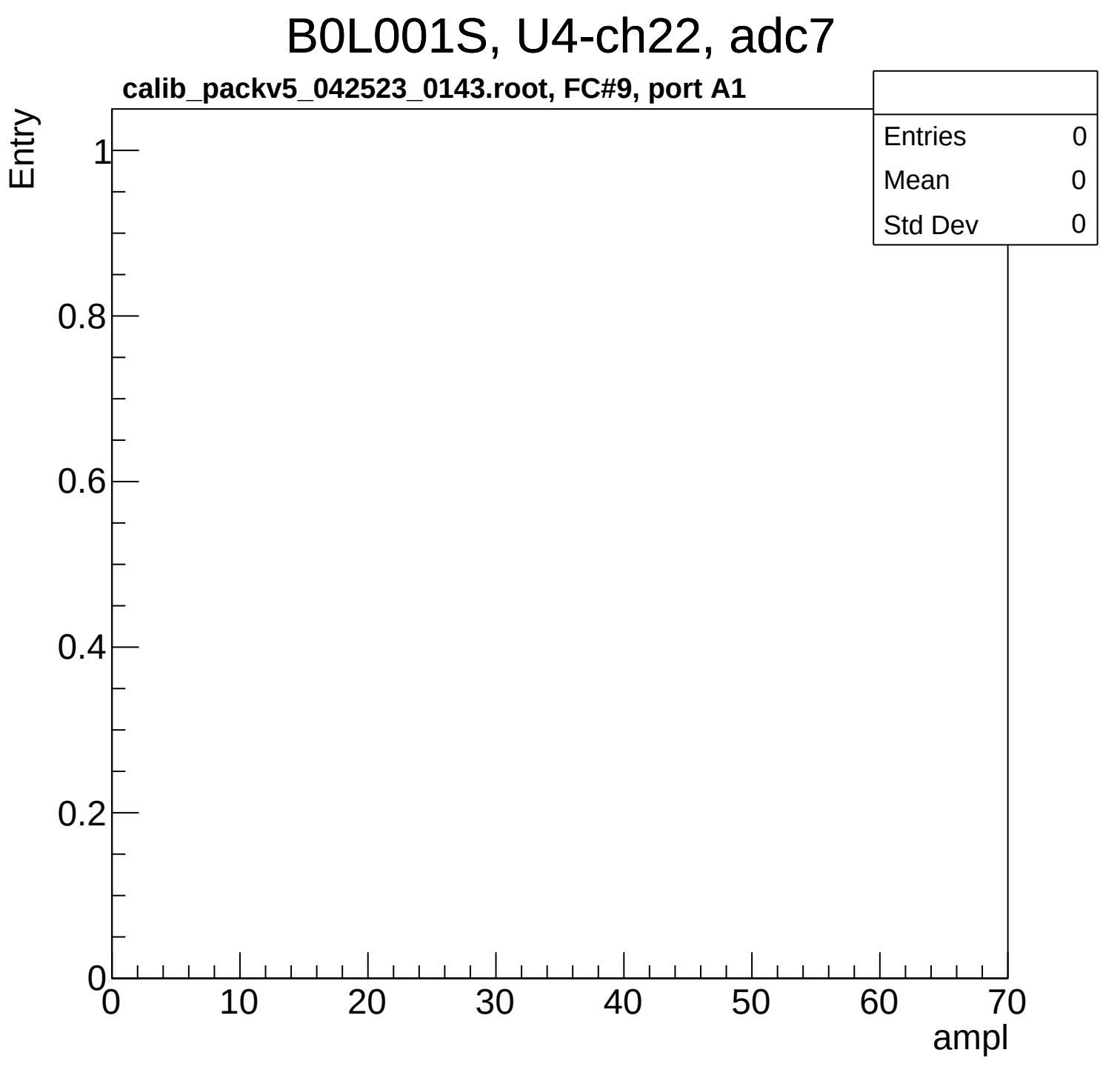
40
50
60
70


B0L001S, U4-ch22, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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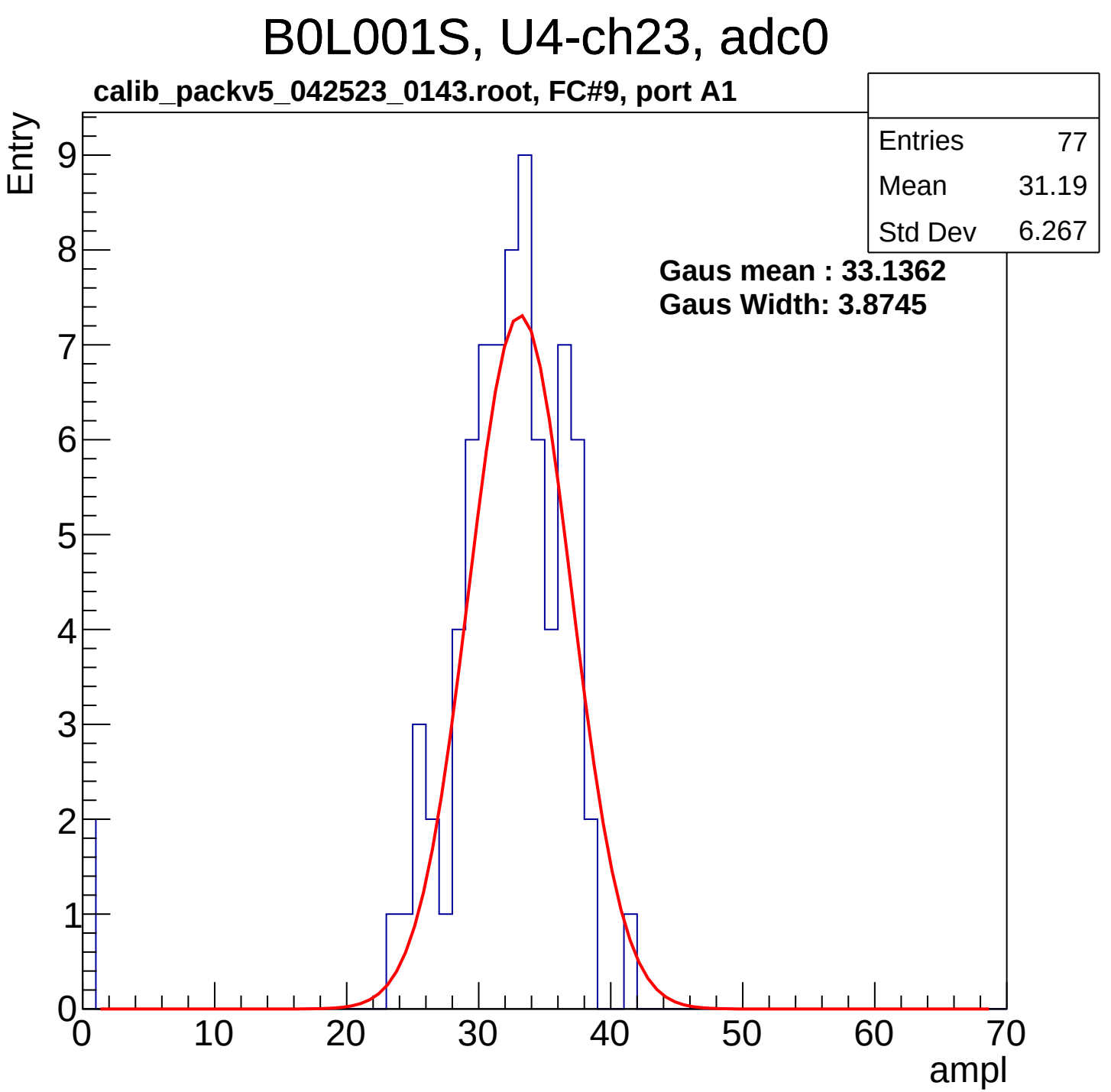
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch23, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.19
Std Dev 6.267
Gaus mean : 33.1362
Gaus Width: 3.8745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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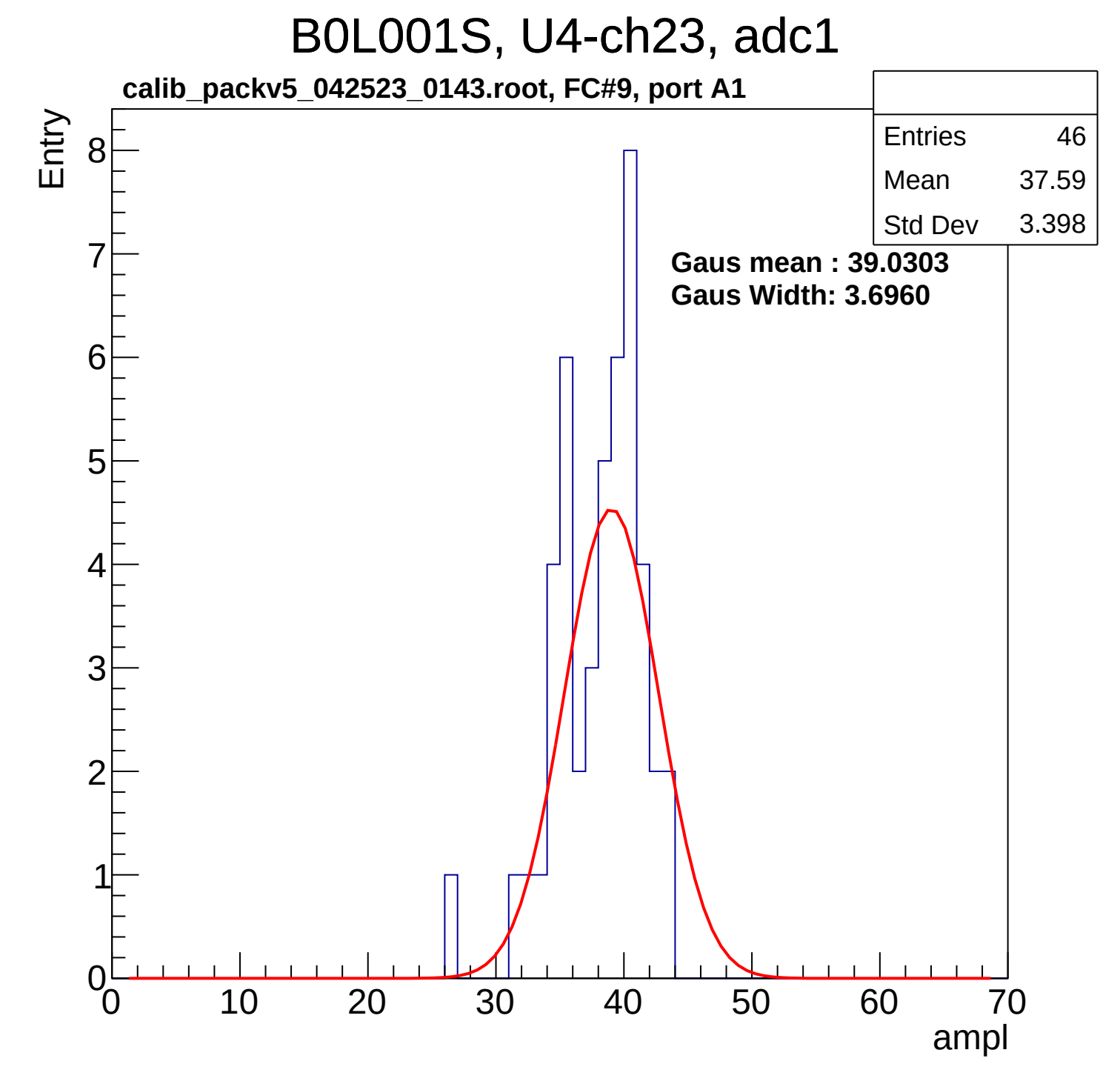
B0L001S, U4-ch23, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 37.59
Std Dev 3.398
Gaus mean : 39.0303
Gaus Width: 3.6960
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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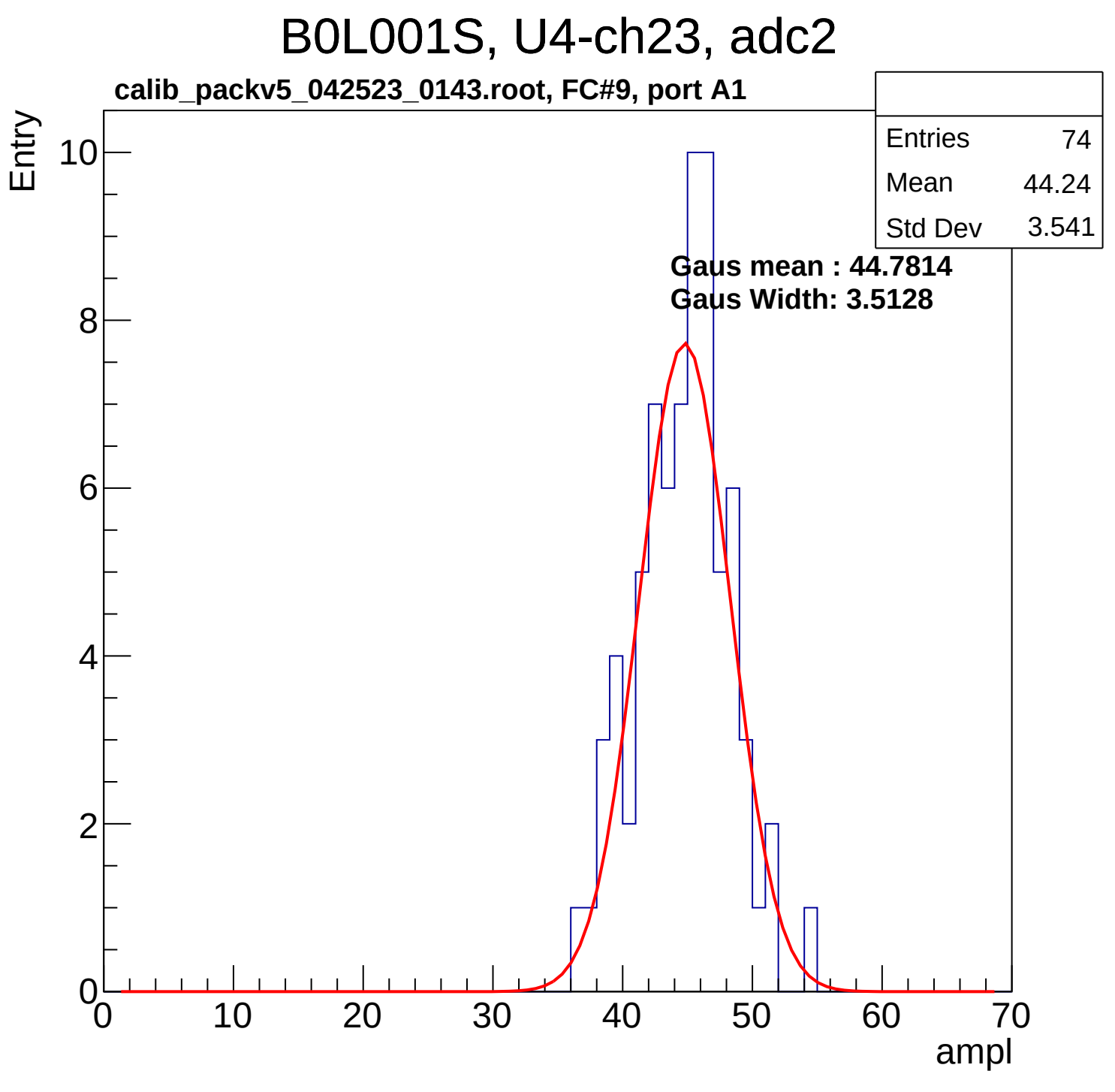
B0L001S, U4-ch23, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 44.24
Std Dev 3.541
Gaus mean : 44.7814
Gaus Width: 3.5128
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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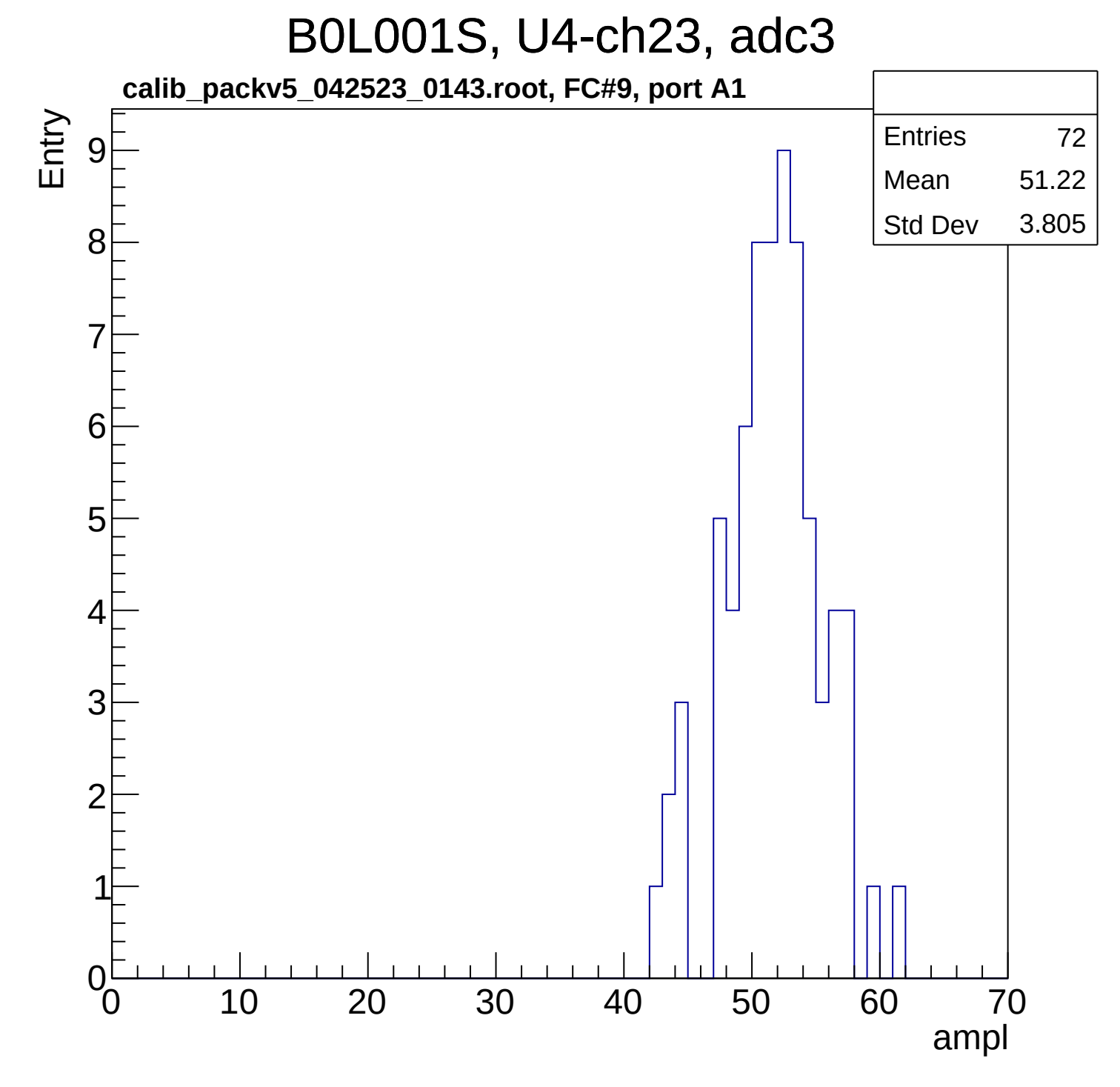
B0L001S, U4-ch23, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.22
Std Dev 3.805
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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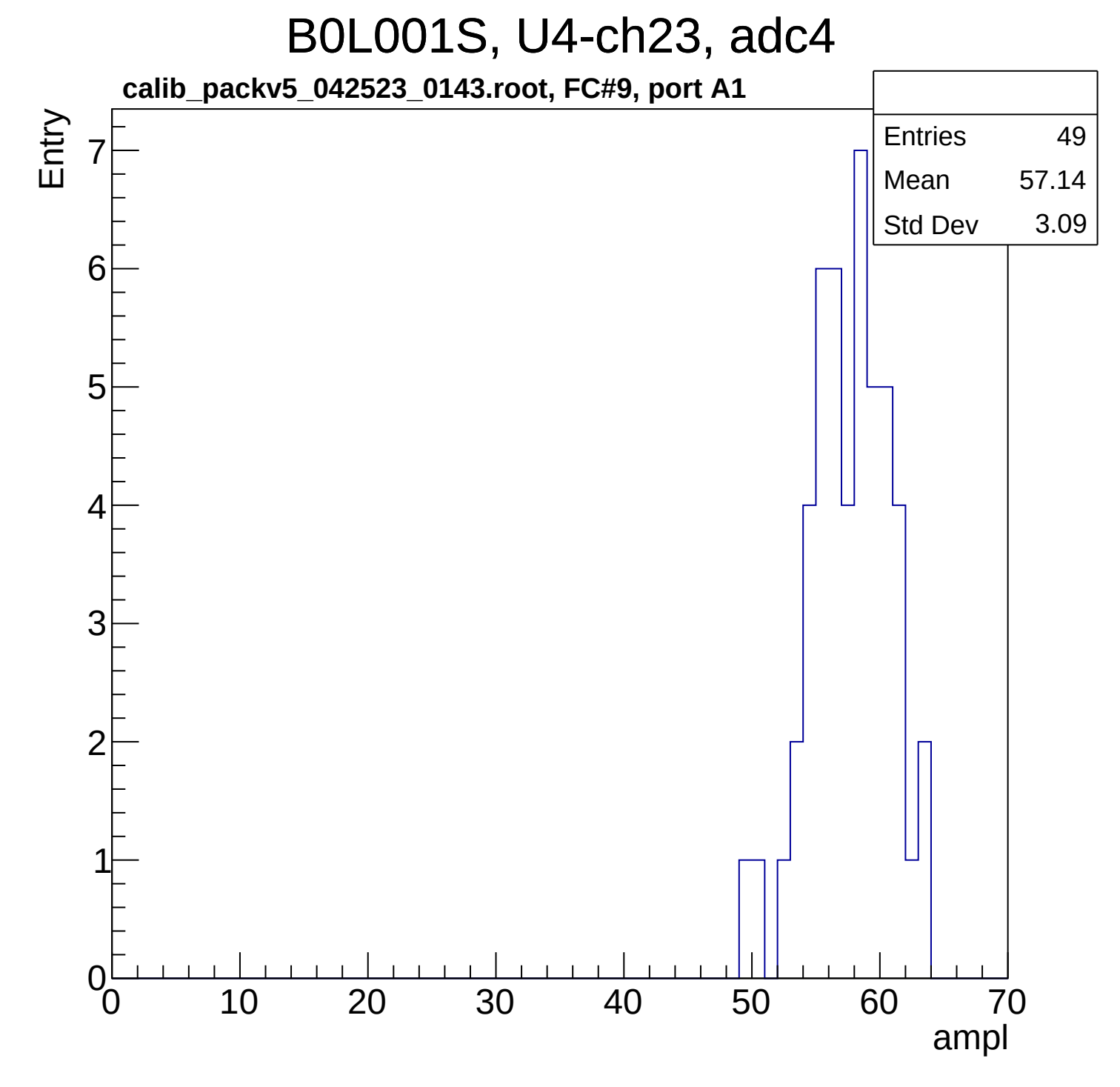
B0L001S, U4-ch23, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.14
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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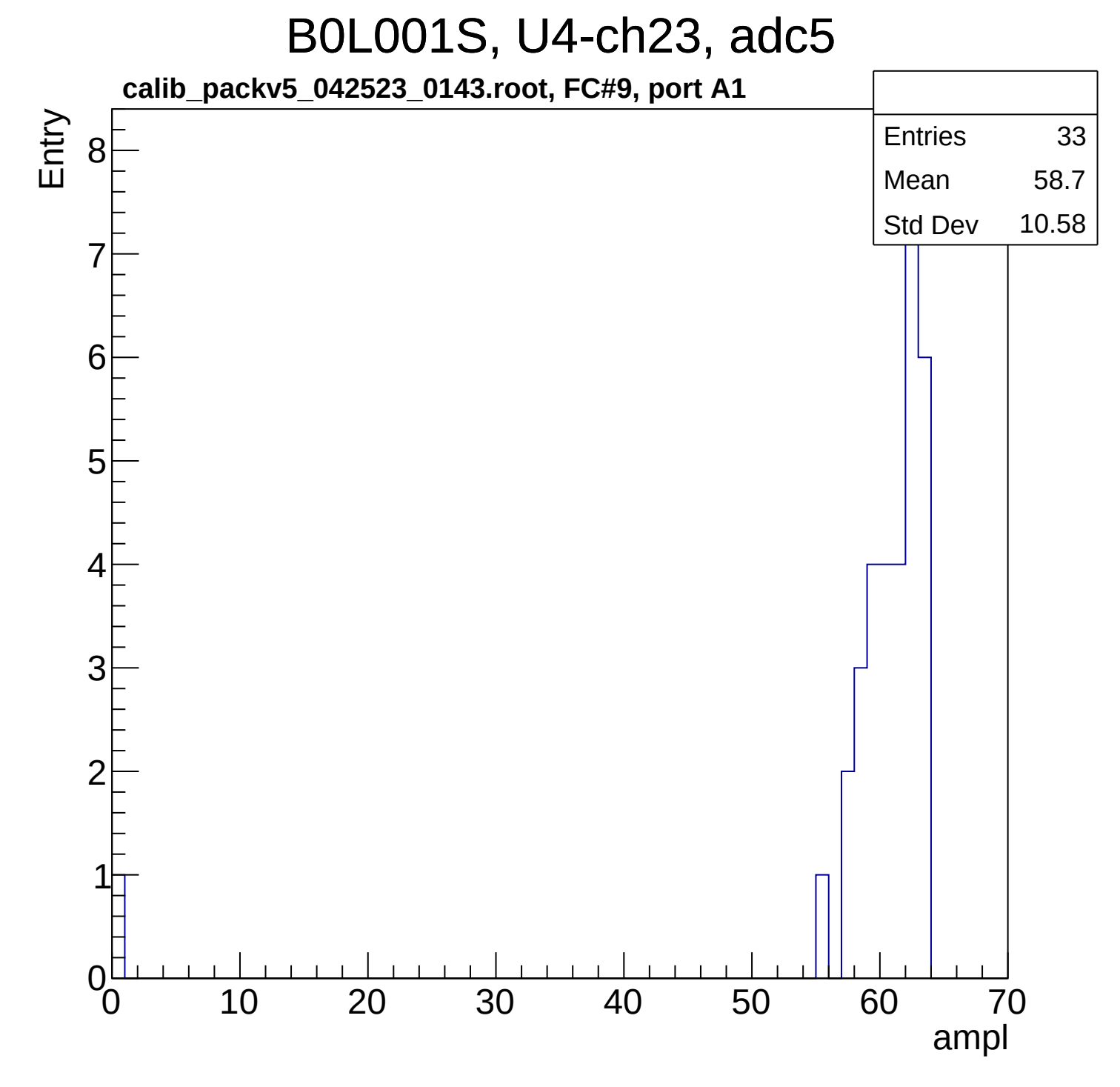
B0L001S, U4-ch23, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.7
Std Dev 10.58
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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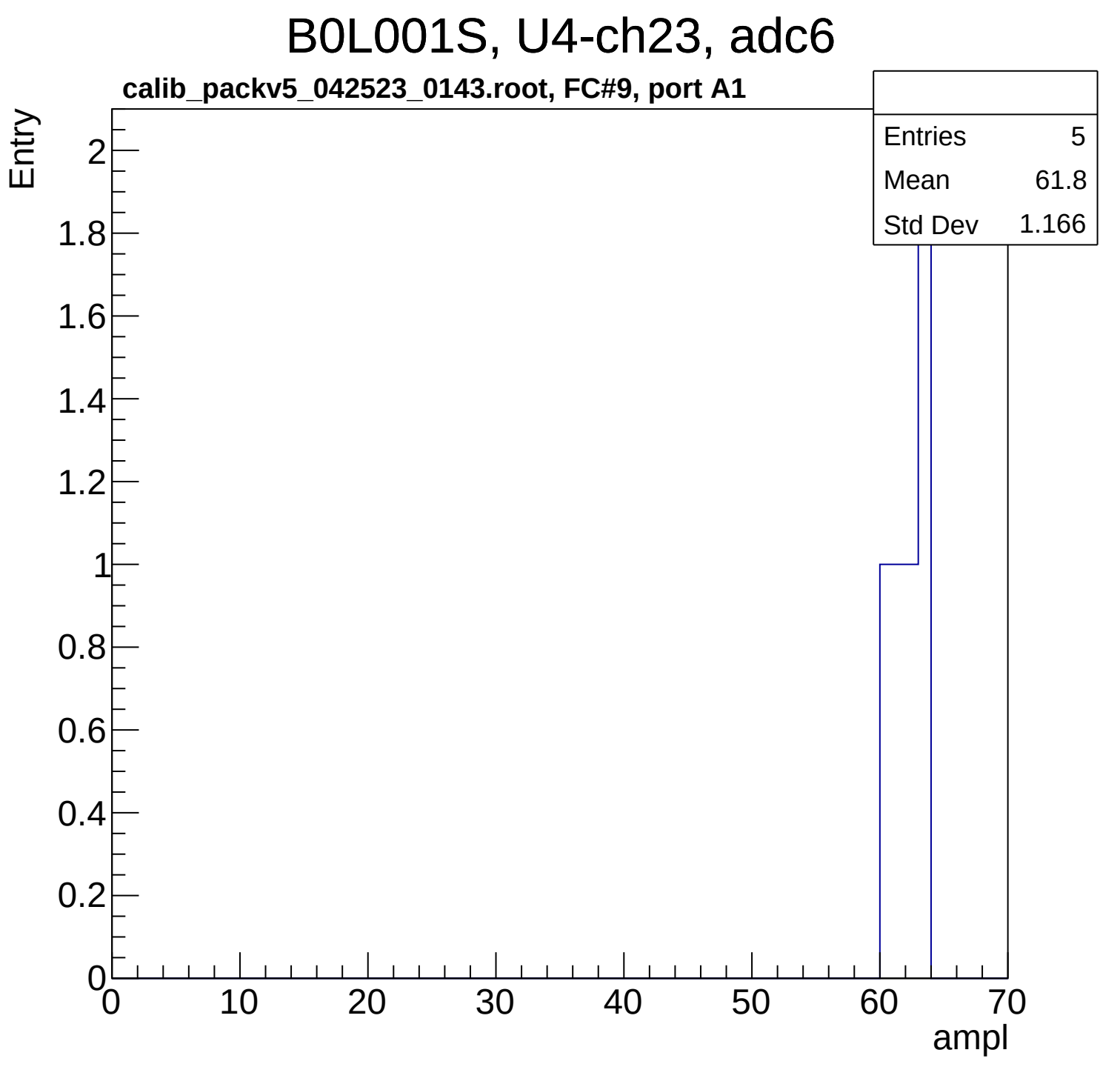
B0L001S, U4-ch23, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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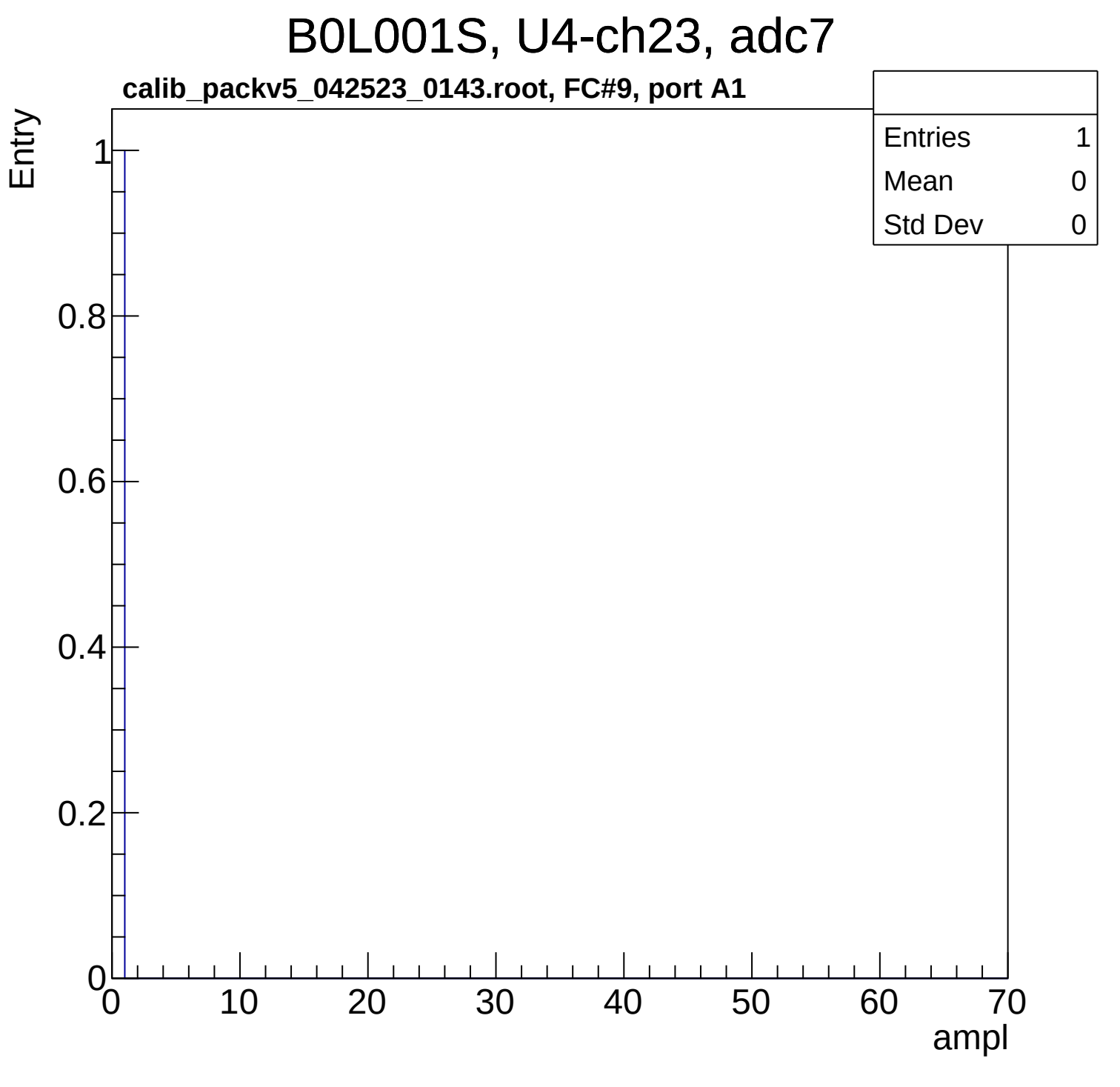
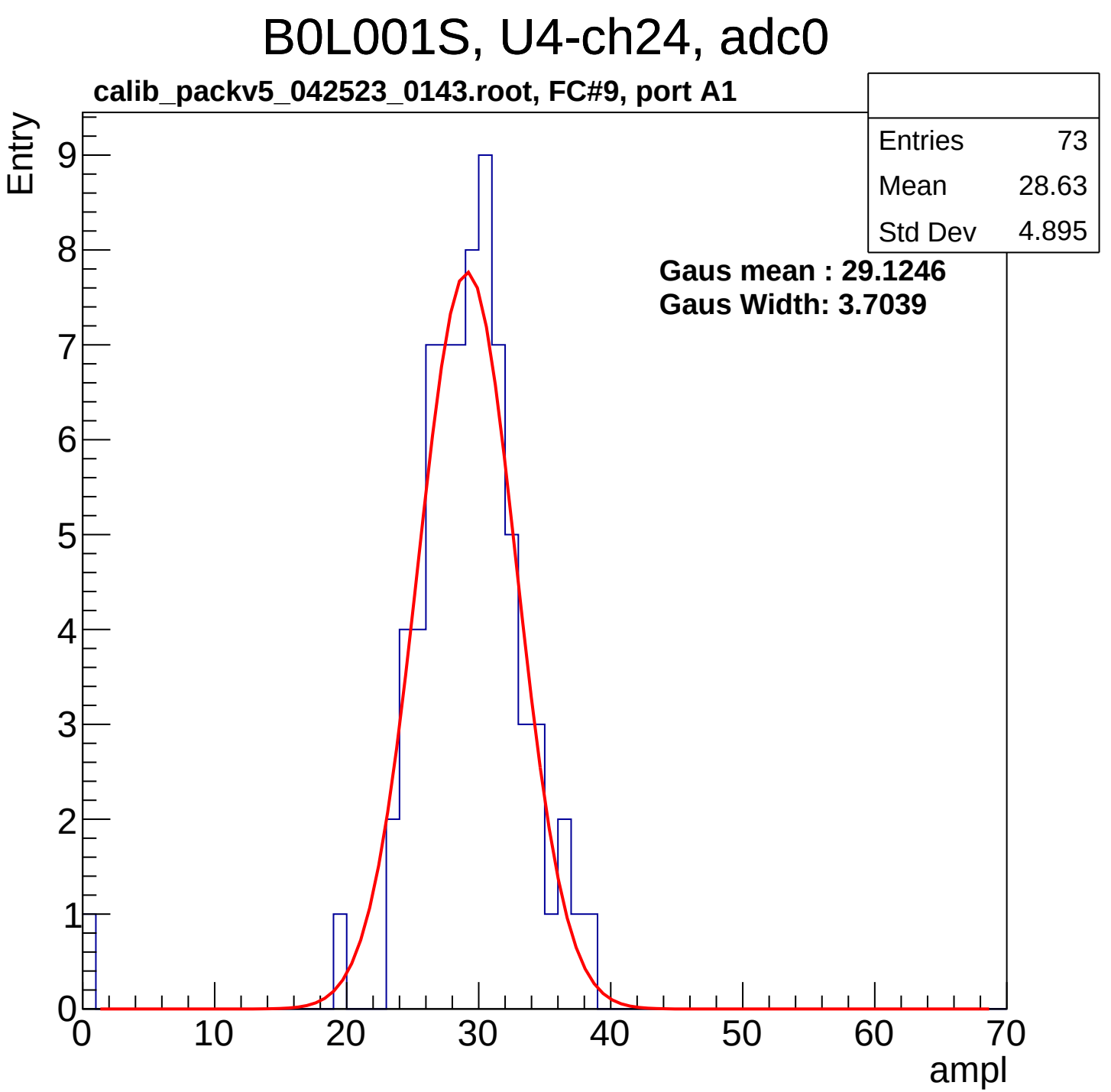

B0L001S, U4-ch23, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch24, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 28.63
Std Dev 4.895
Gaus mean : 29.1246
Gaus Width: 3.7039
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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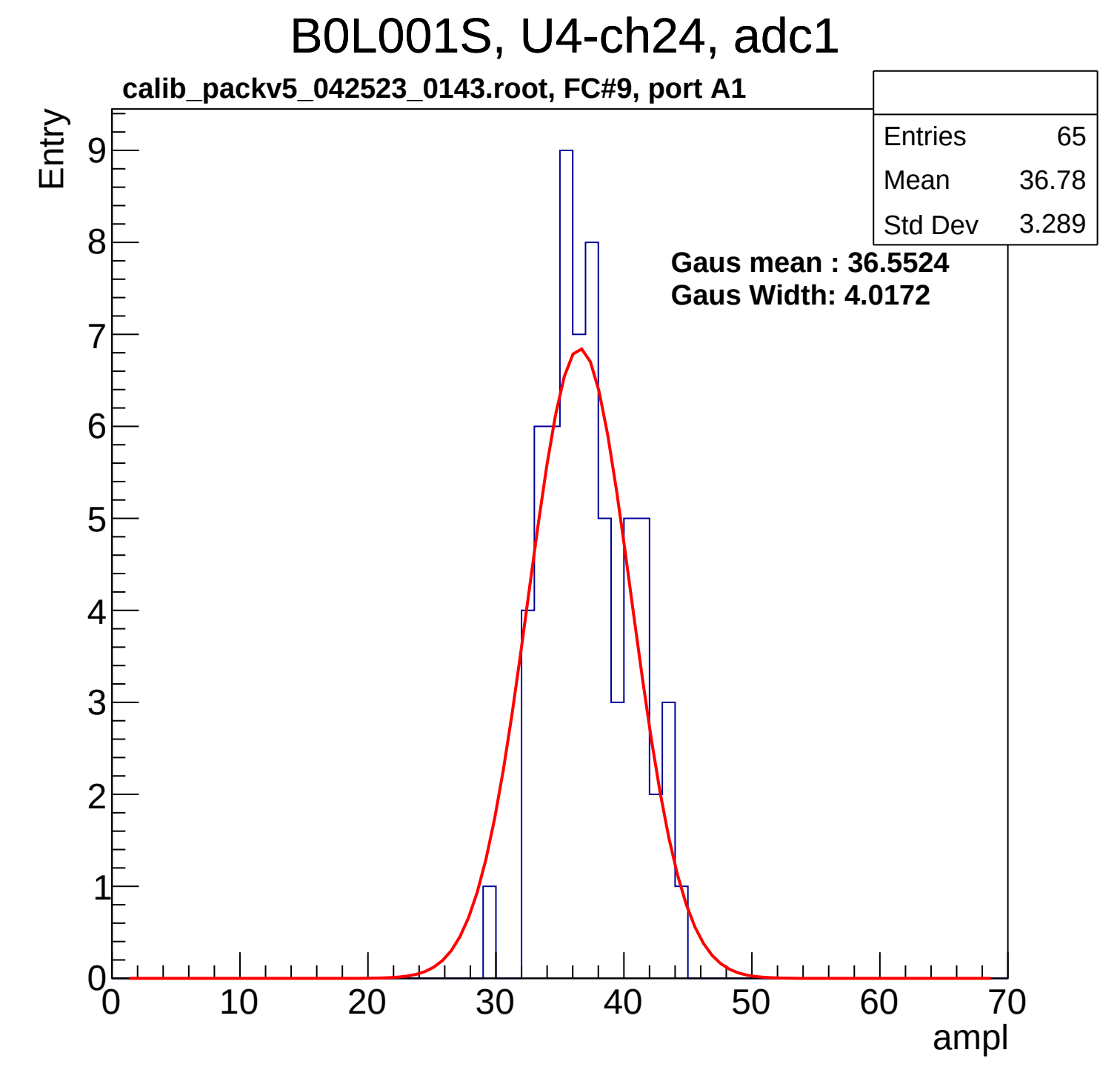
B0L001S, U4-ch24, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.78
Std Dev 3.289
Gaus mean : 36.5524
Gaus Width: 4.0172
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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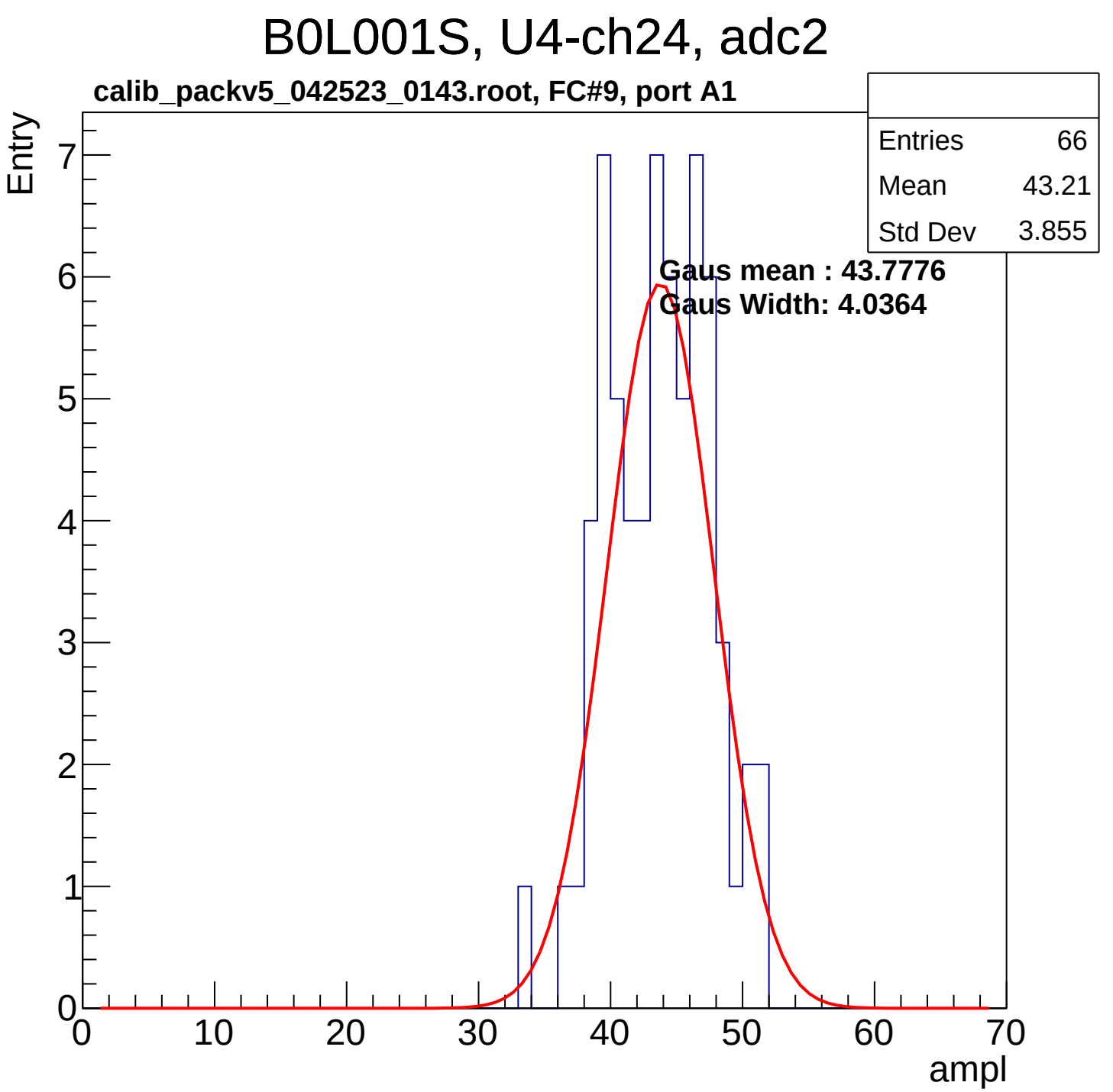
B0L001S, U4-ch24, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.21
Std Dev 3.855
Gaus mean : 43.7776
Gaus Width: 4.0364
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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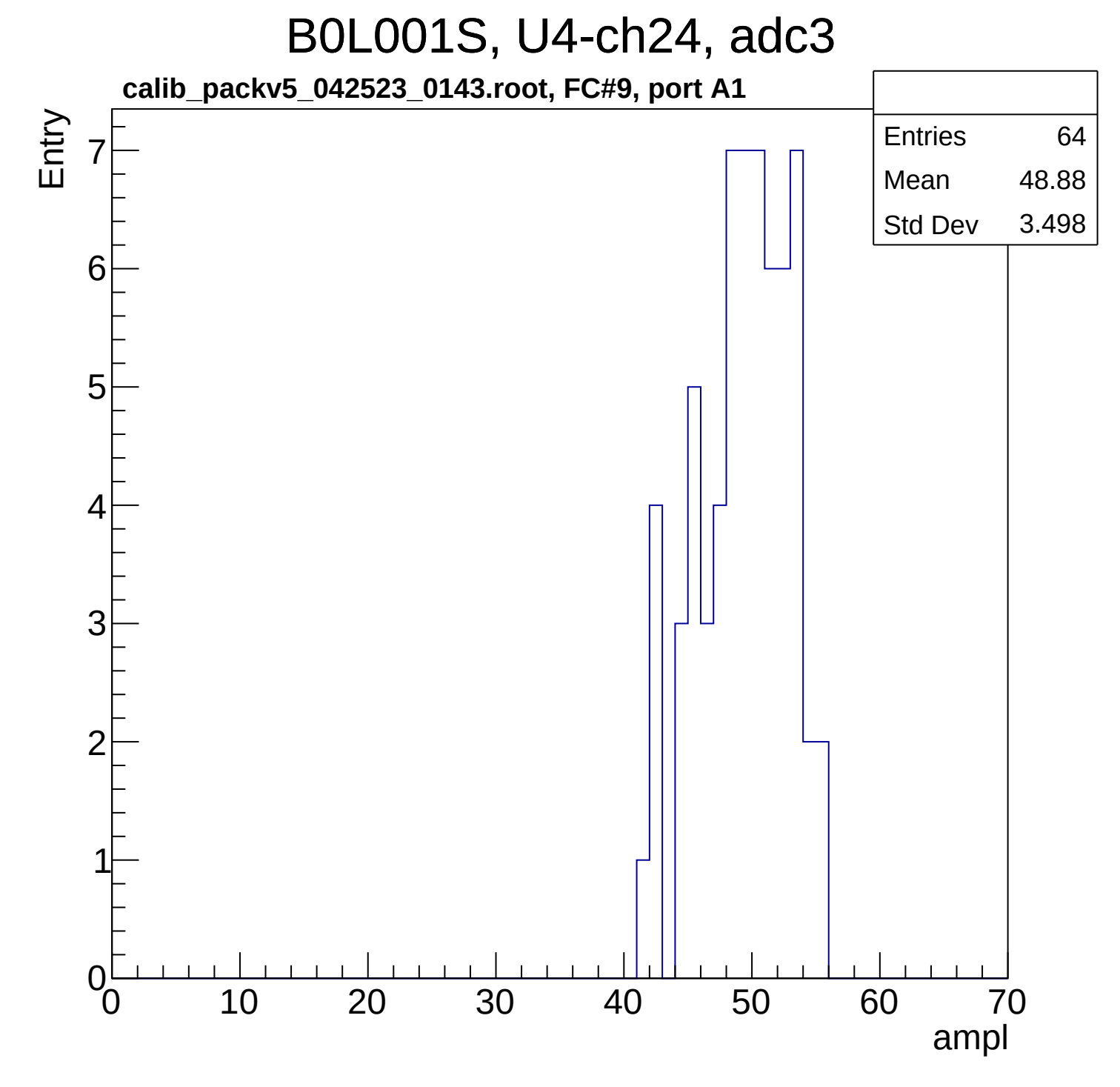
B0L001S, U4-ch24, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 48.88
Std Dev 3.498
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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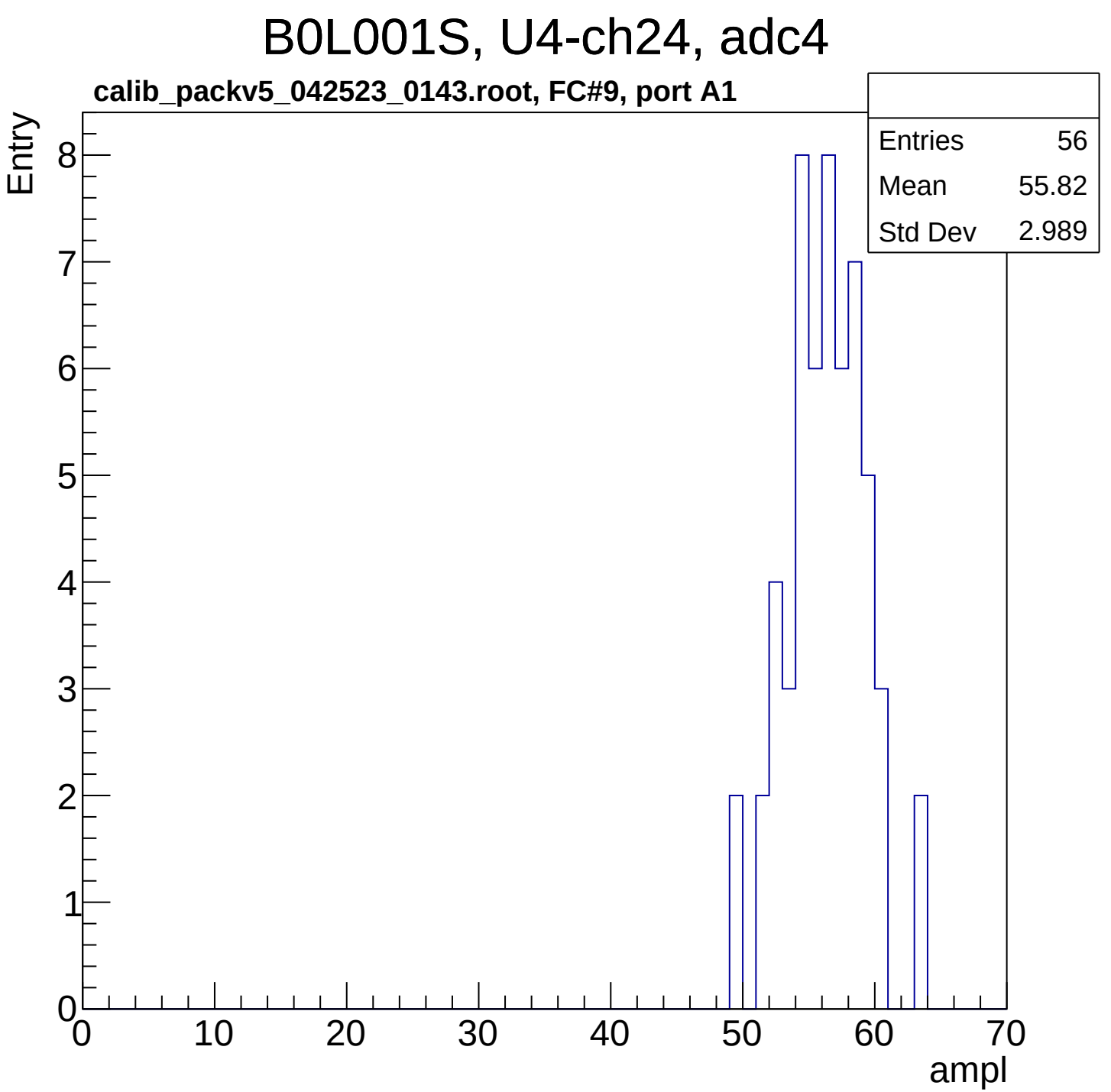
B0L001S, U4-ch24, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.82
Std Dev 2.989
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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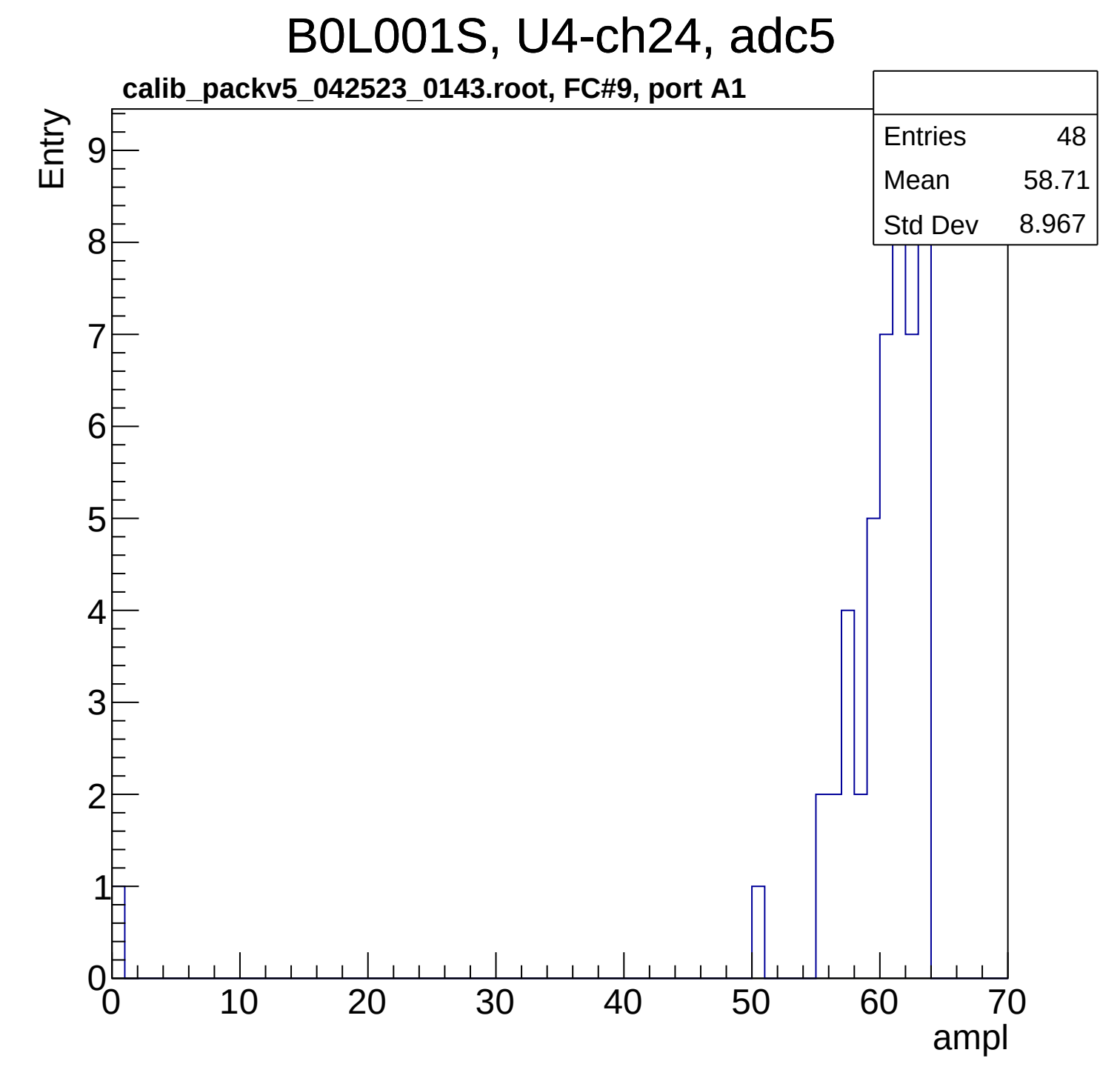
B0L001S, U4-ch24, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.71
Std Dev 8.967
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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50
60
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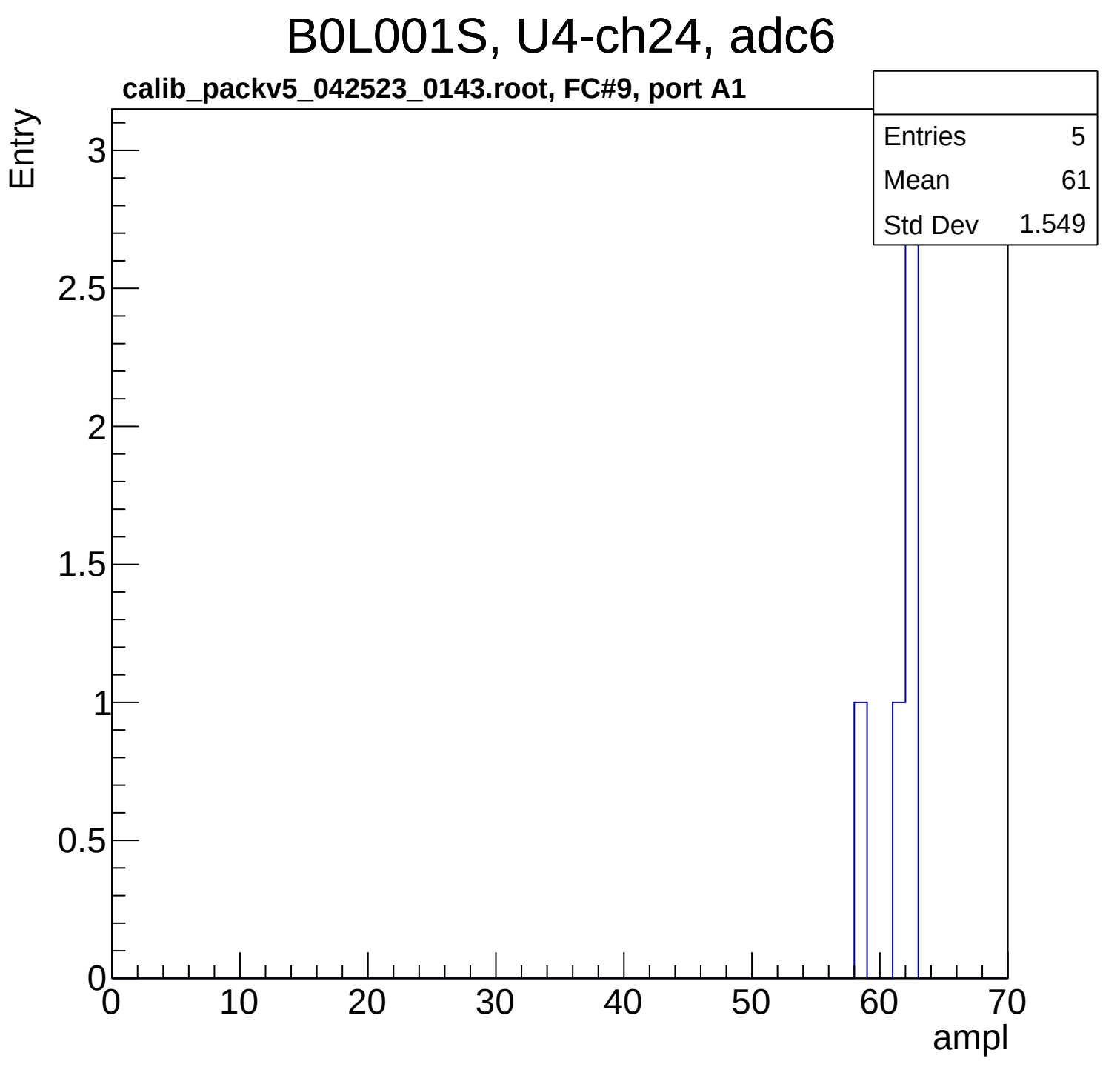
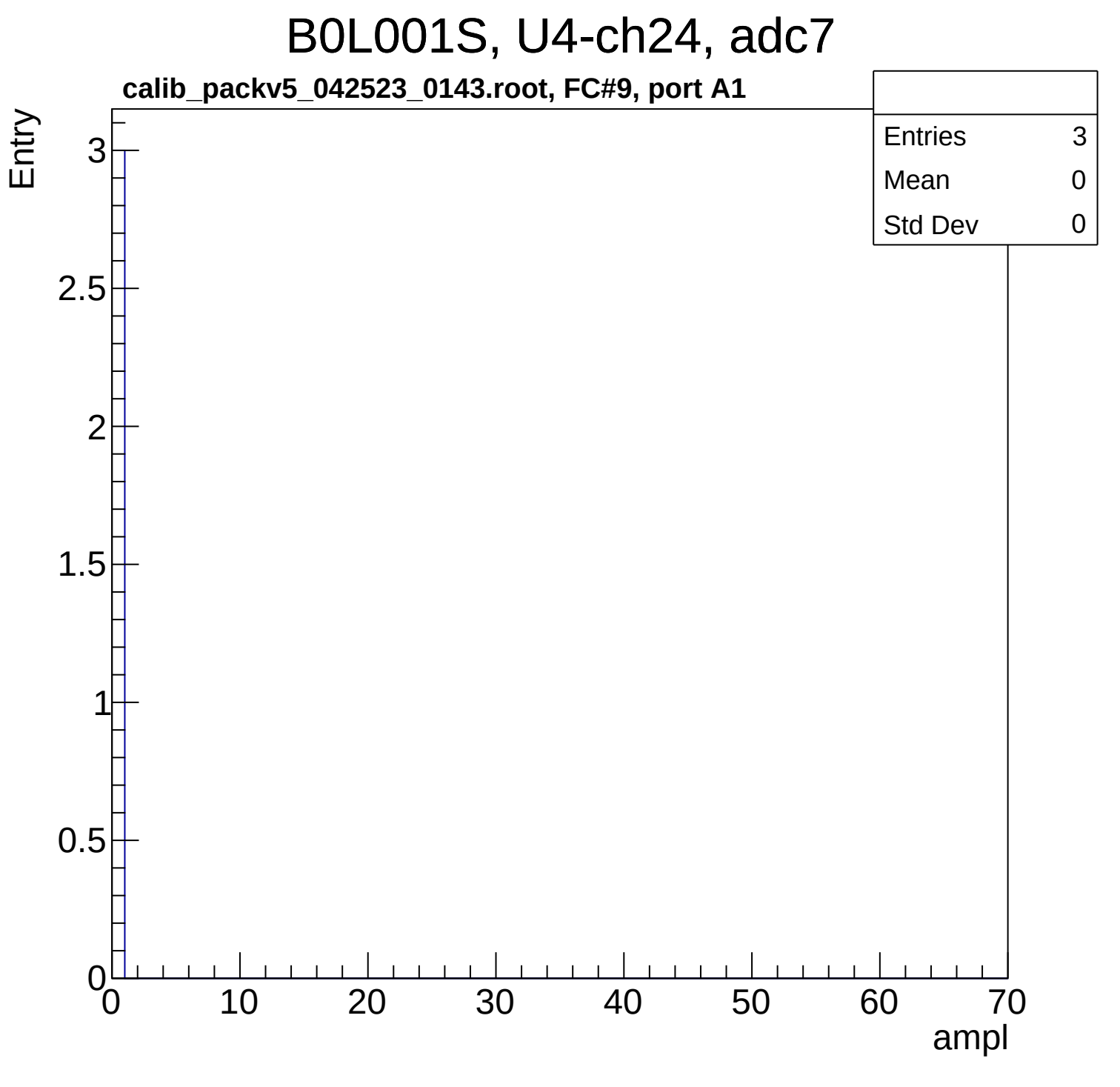
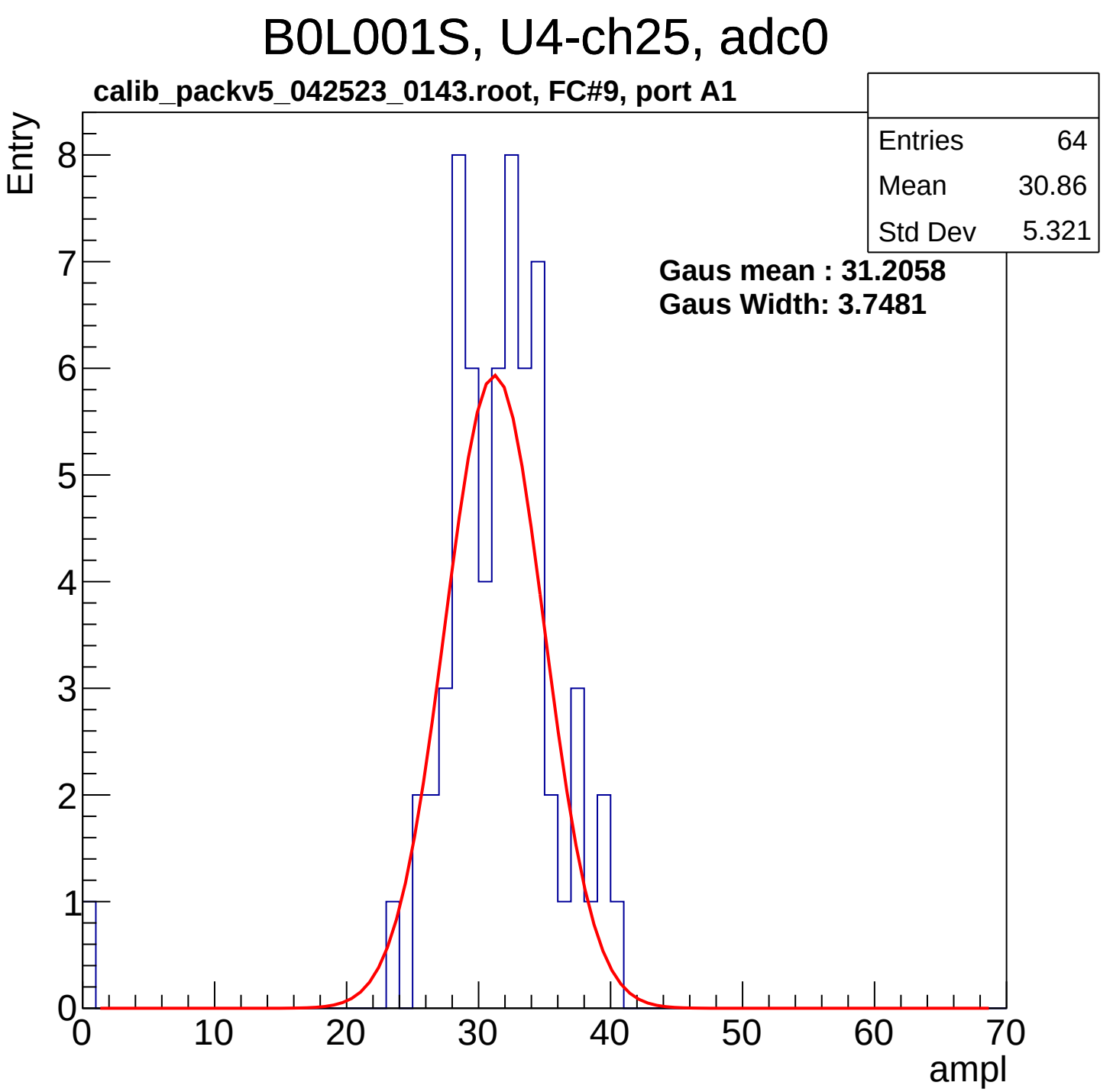

B0L001S, U4-ch24, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61
Std Dev 1.549
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch24, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch25, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.86
Std Dev 5.321
Gaus mean : 31.2058
Gaus Width: 3.7481
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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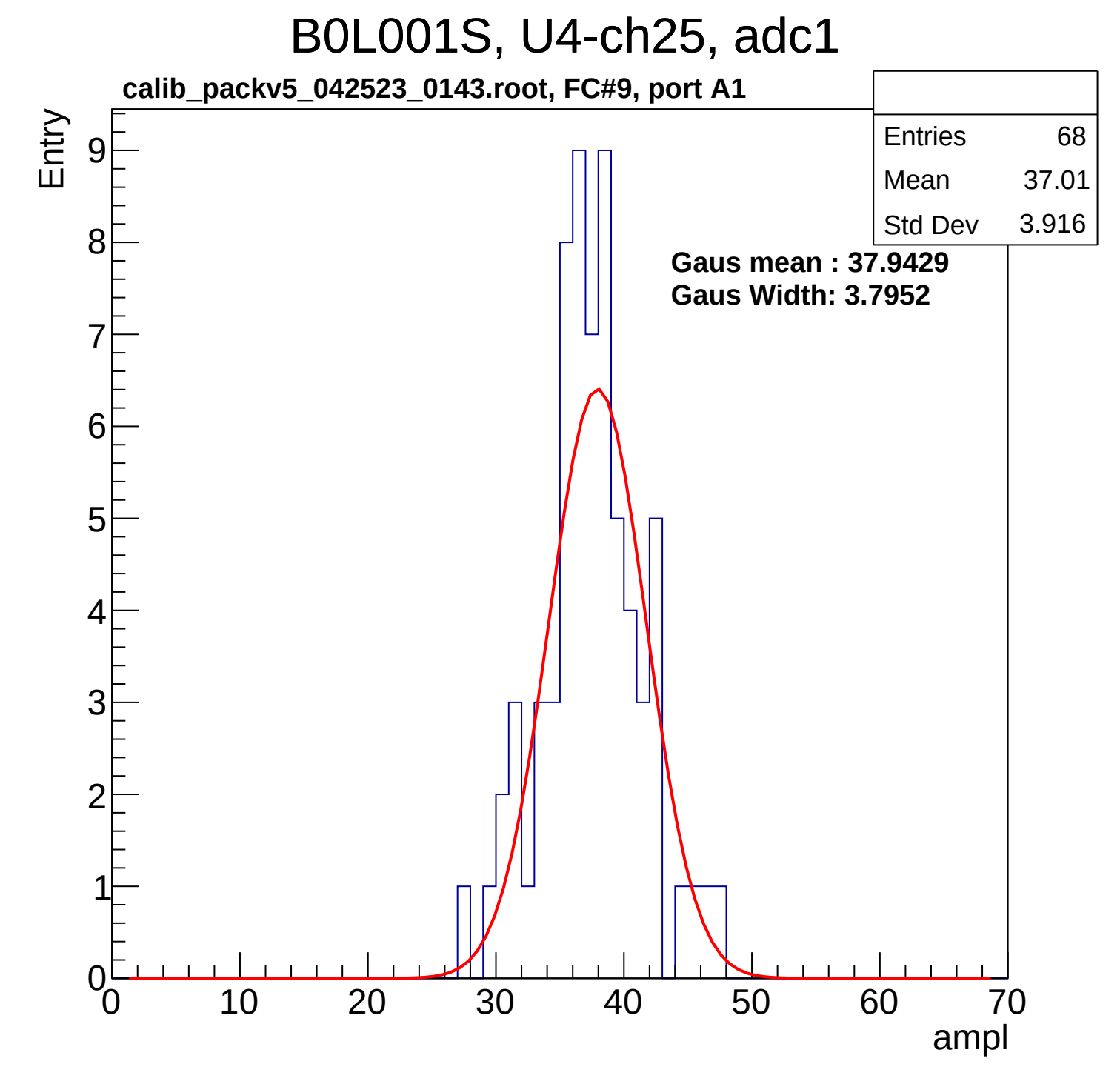
B0L001S, U4-ch25, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.01
Std Dev 3.916
Gaus mean : 37.9429
Gaus Width: 3.7952
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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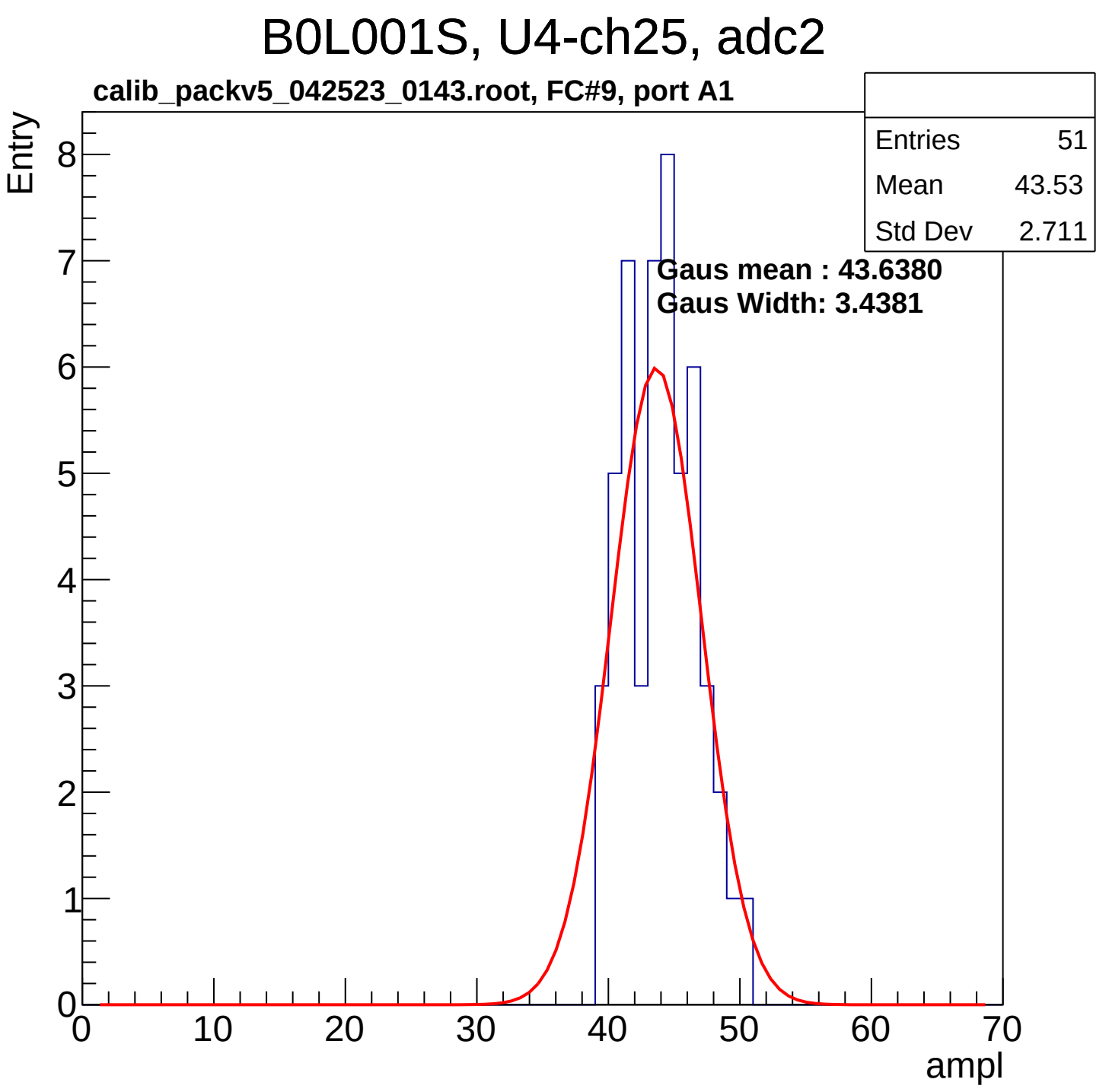
B0L001S, U4-ch25, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 43.53
Std Dev 2.711
Gaus mean : 43.6380
Gaus Width: 3.4381
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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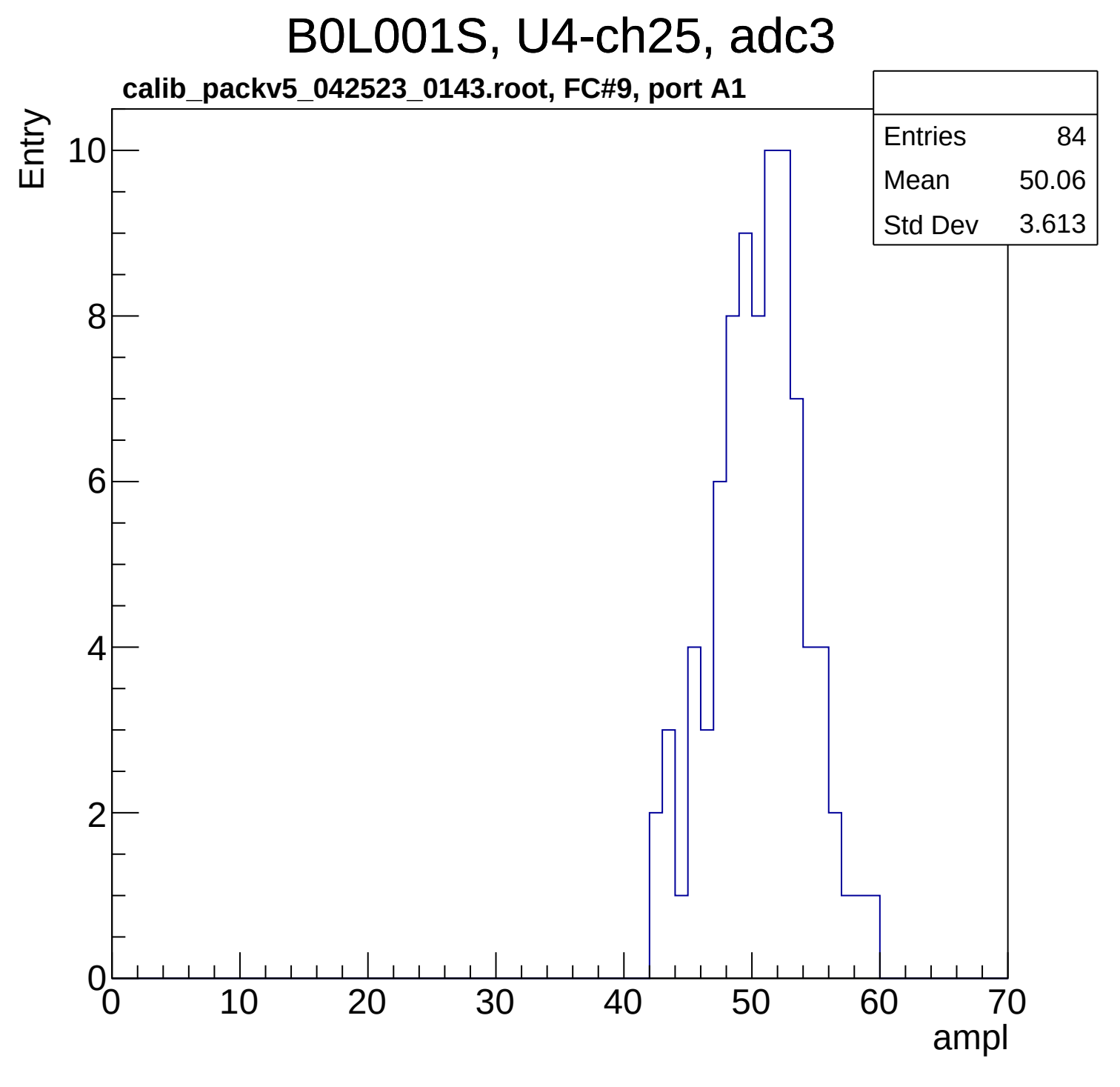
B0L001S, U4-ch25, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 50.06
Std Dev 3.613
Entry
ampl
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2
4
6
8
10
0
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20
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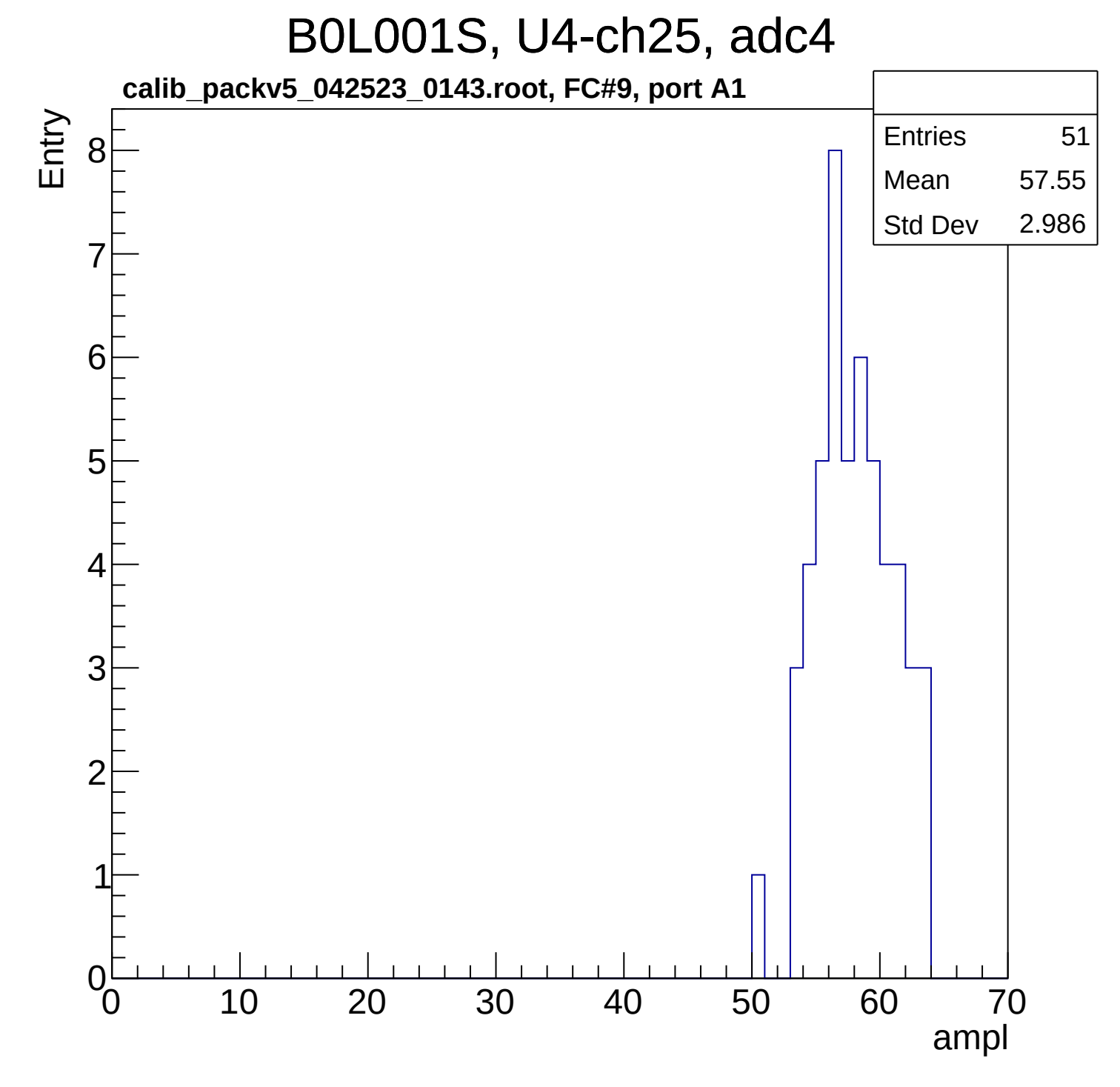
40
50
60
70


B0L001S, U4-ch25, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.55
Std Dev 2.986
Entry
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4
5
6
7
8
0
10
20
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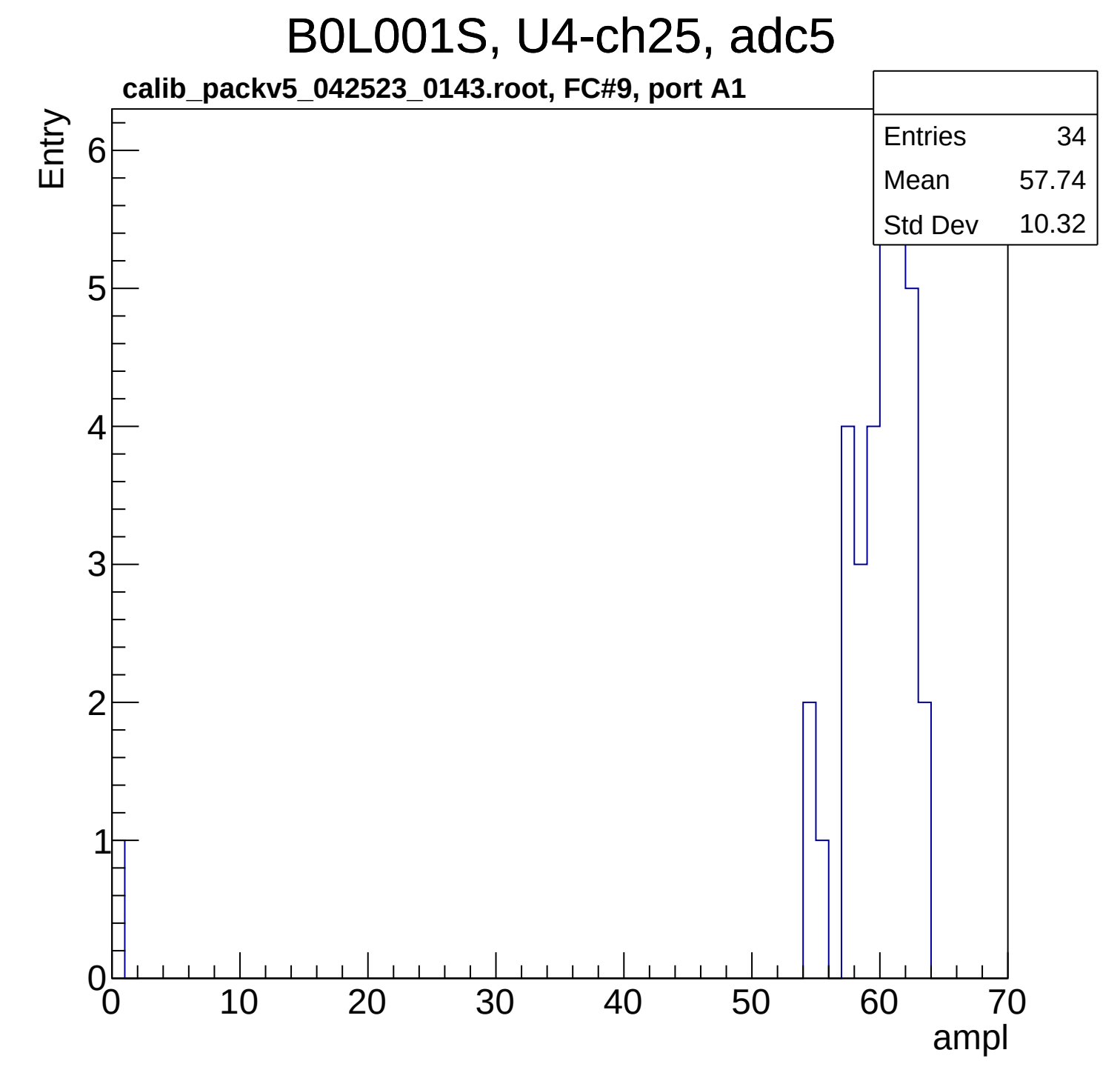
50
60
70
ampl


B0L001S, U4-ch25, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 57.74
Std Dev 10.32
Entry
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2
3
4
5
6
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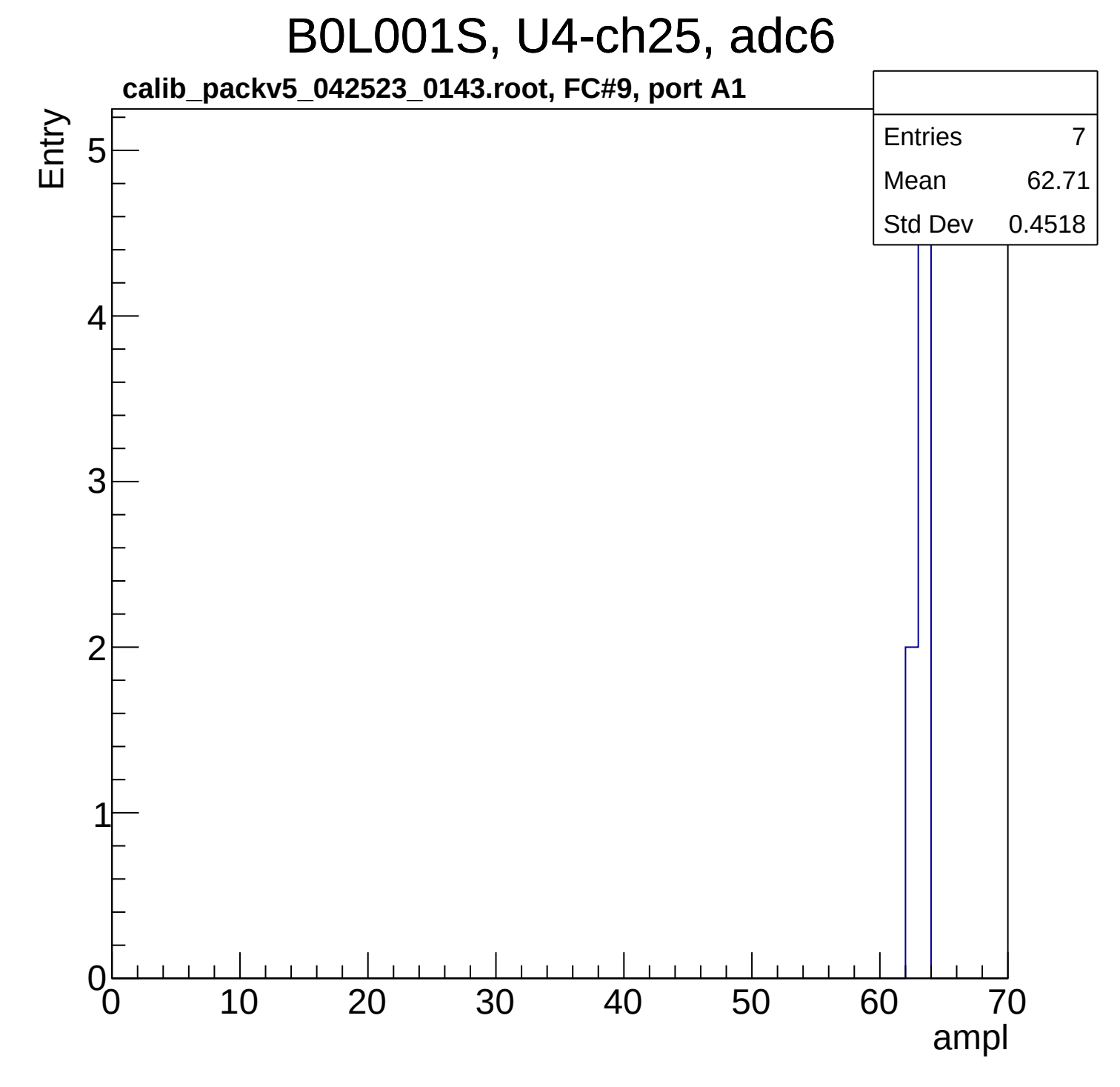
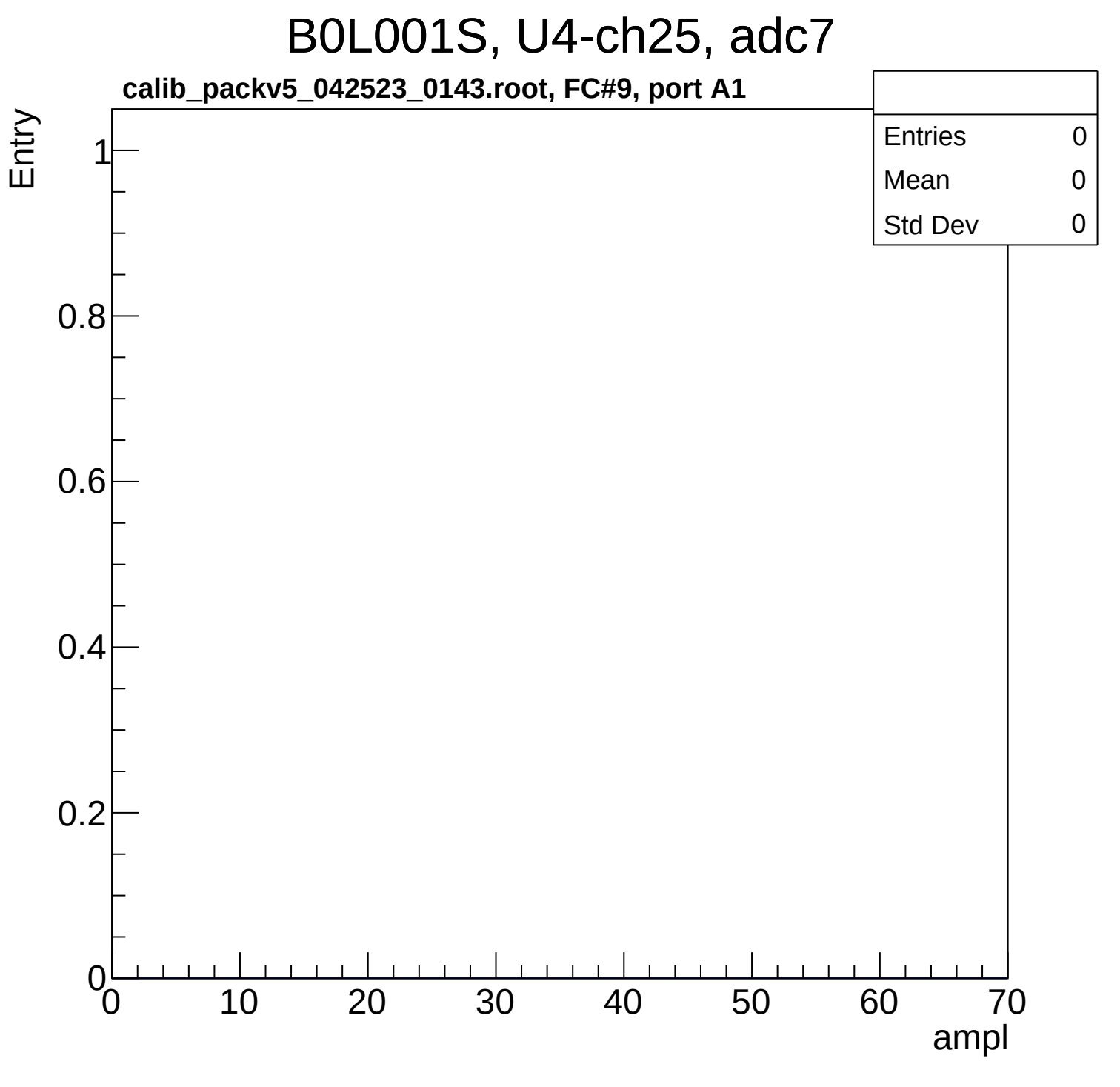
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch25, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.71
Std Dev 0.4518
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L001S, U4-ch25, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
10
20
30
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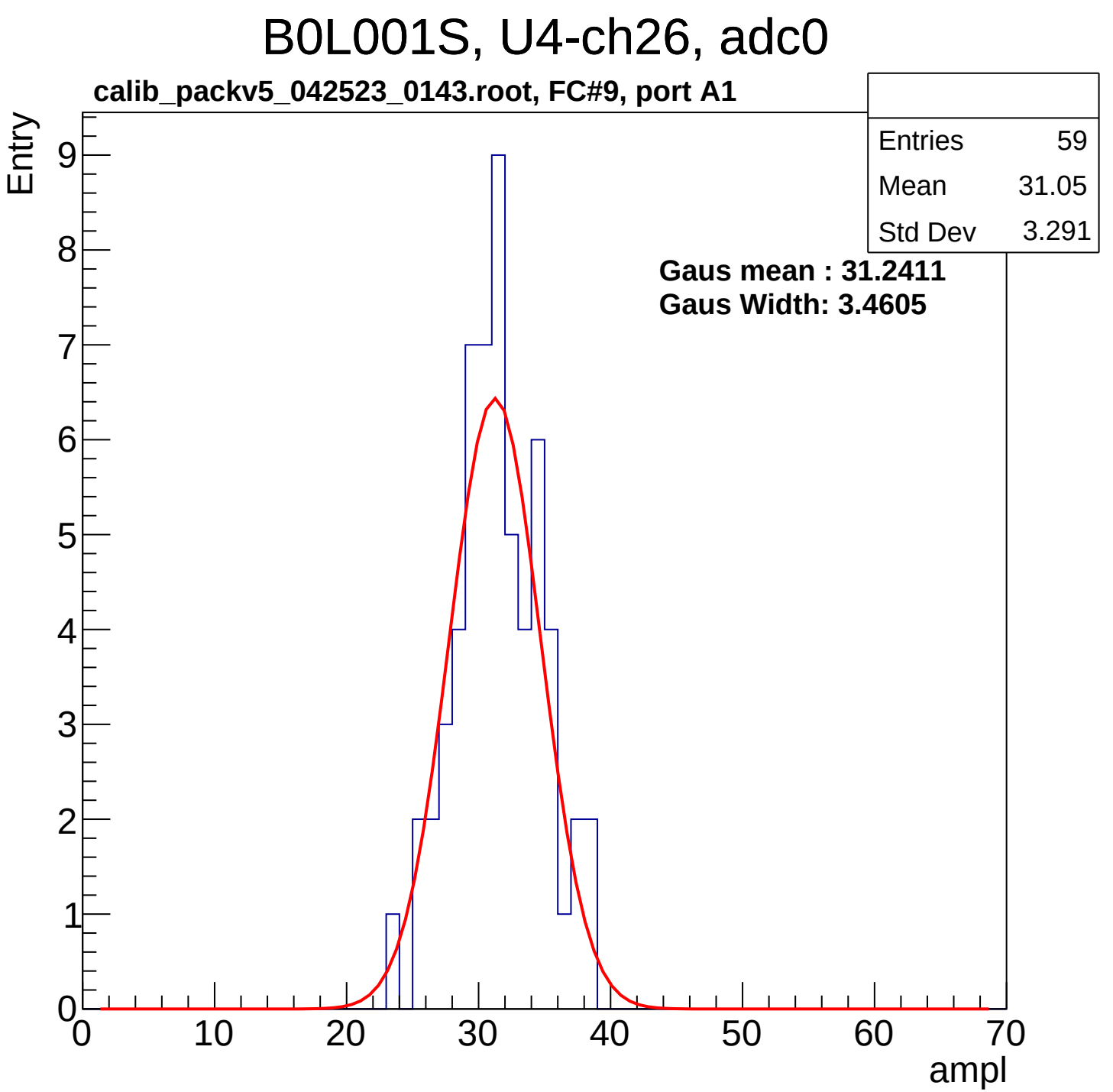
50
60
70
ampl


B0L001S, U4-ch26, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 31.05
Std Dev 3.291
Gaus mean : 31.2411
Gaus Width: 3.4605
Entry
ampl
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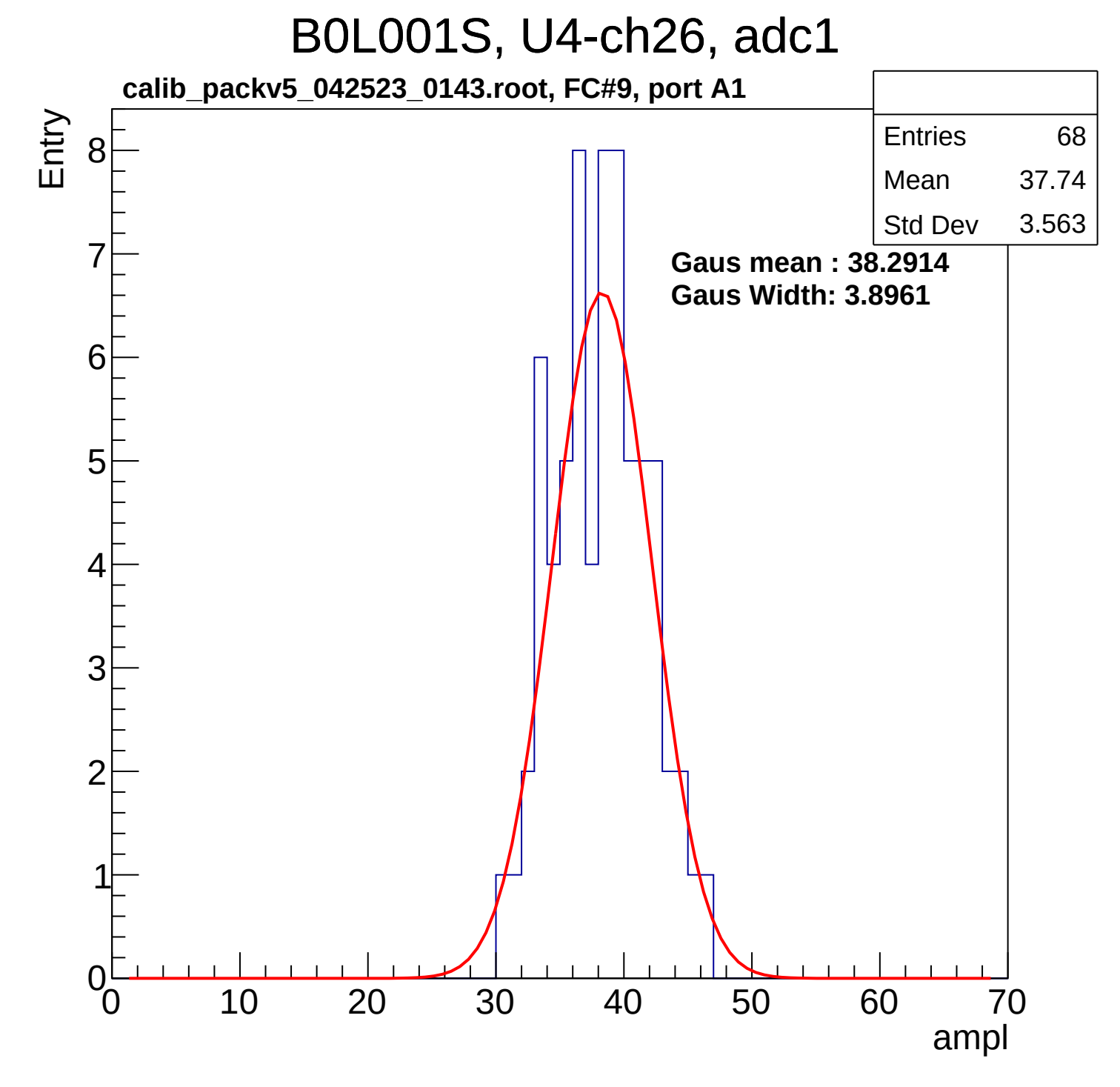
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch26, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.74
Std Dev 3.563
Gaus mean : 38.2914
Gaus Width: 3.8961
Entry
ampl
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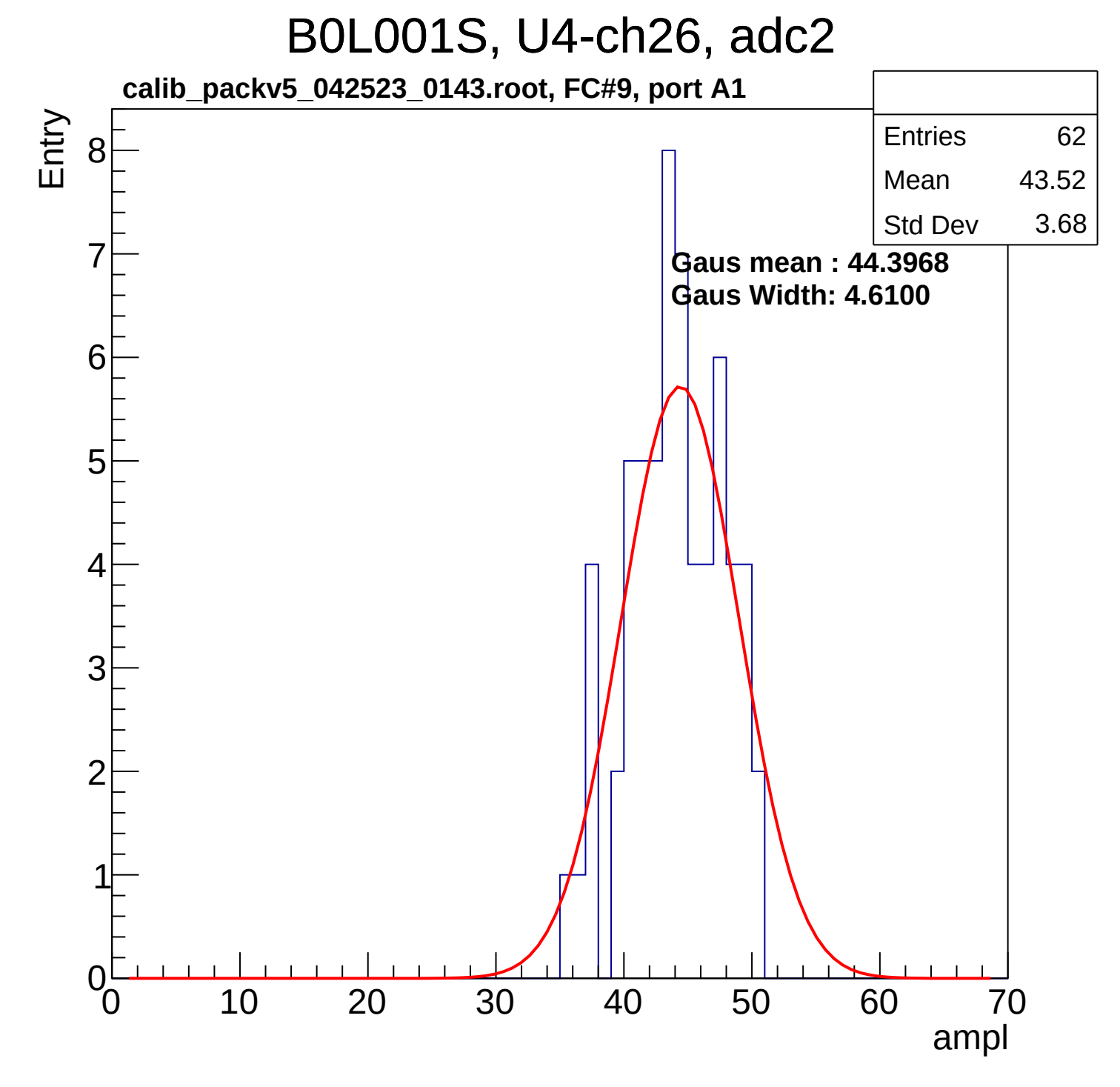
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch26, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 43.52
Std Dev 3.68
Gaus mean : 44.3968
Gaus Width: 4.6100
Entry
ampl
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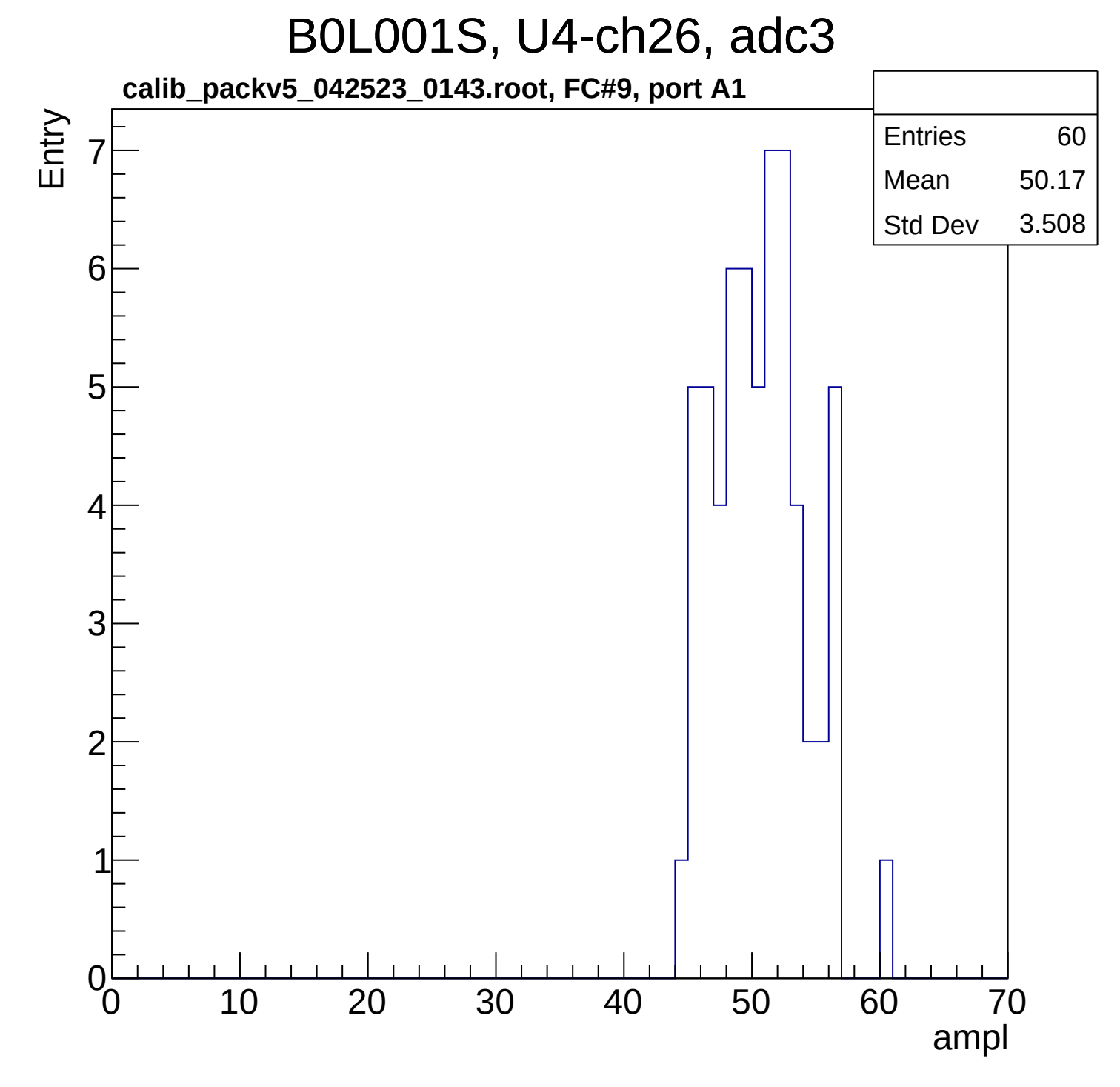
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch26, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.17
Std Dev 3.508
Entry
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2
3
4
5
6
7
0
10
20
30
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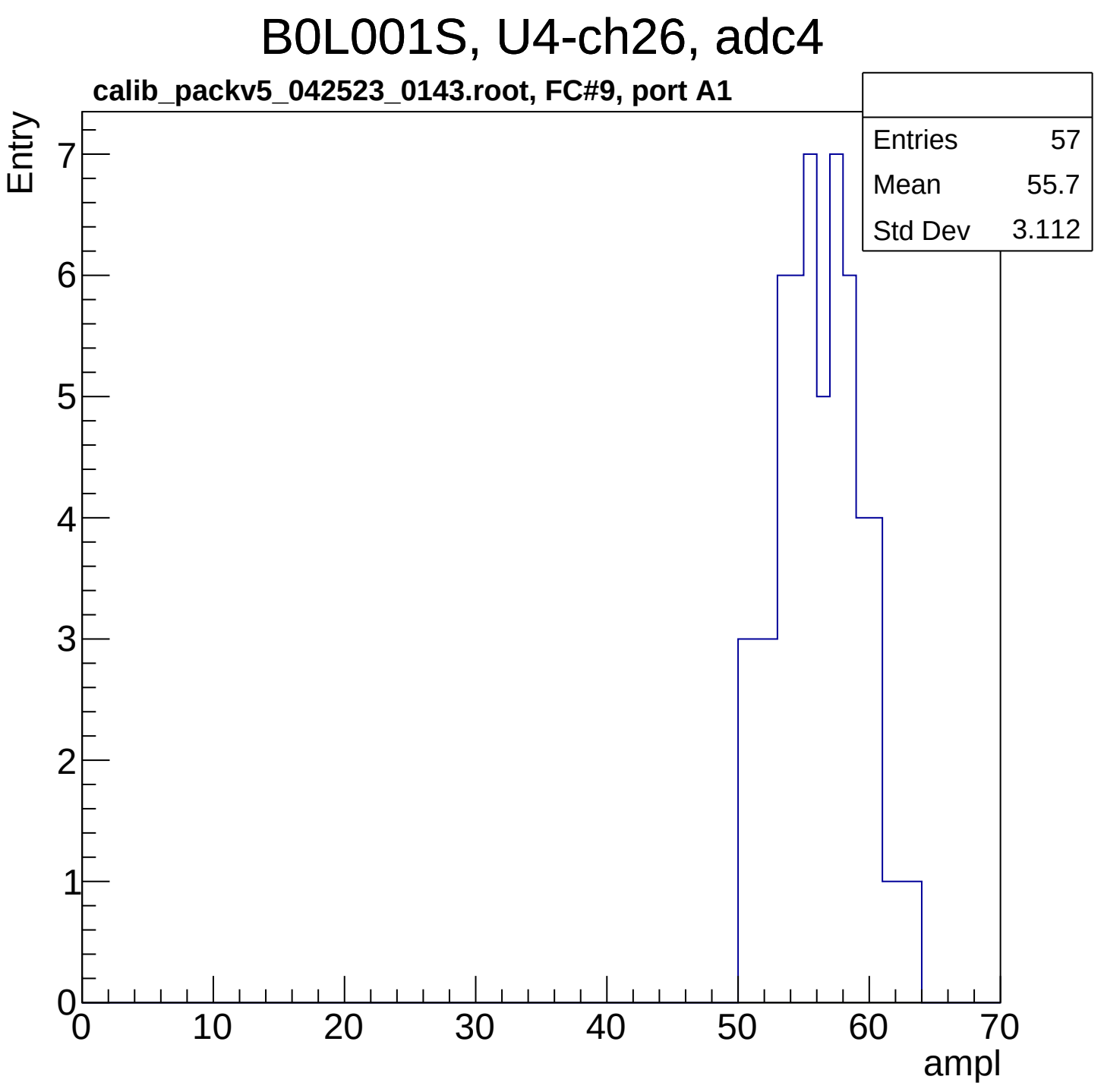
50
60
70
ampl


B0L001S, U4-ch26, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 55.7
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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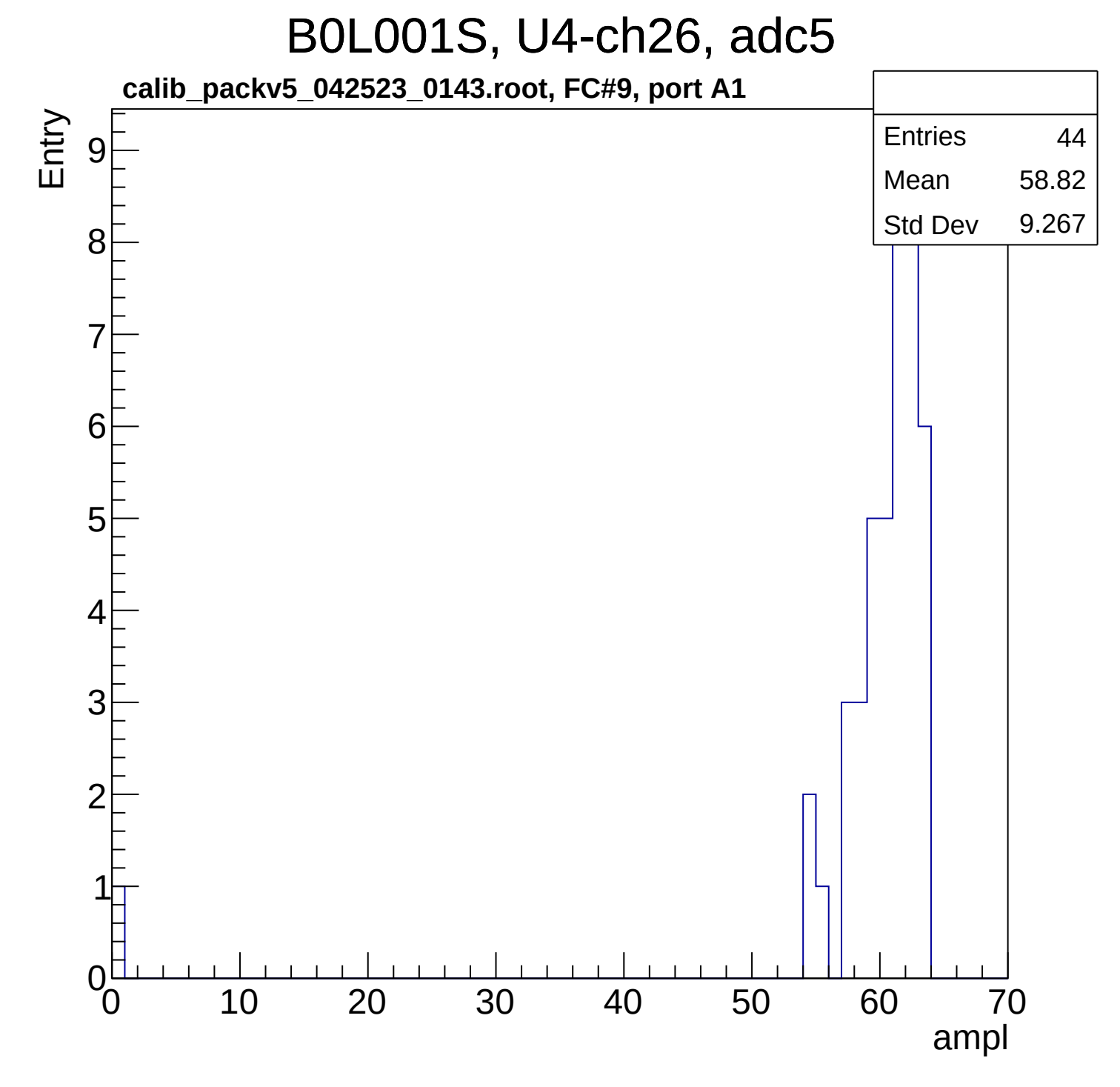
B0L001S, U4-ch26, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.82
Std Dev 9.267
Entry
ampl
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2
3
4
5
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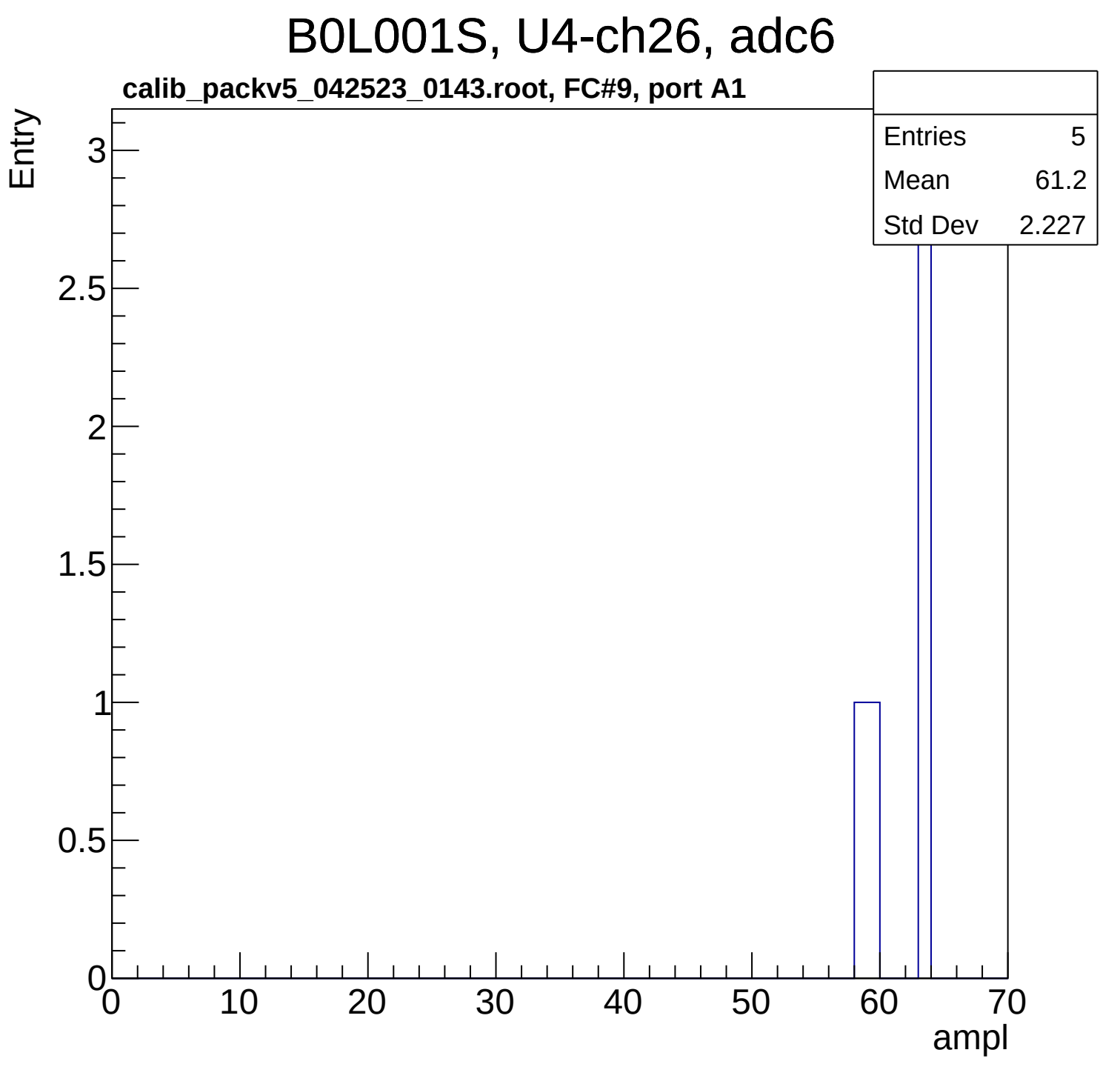
B0L001S, U4-ch26, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.2
Std Dev 2.227
Entry
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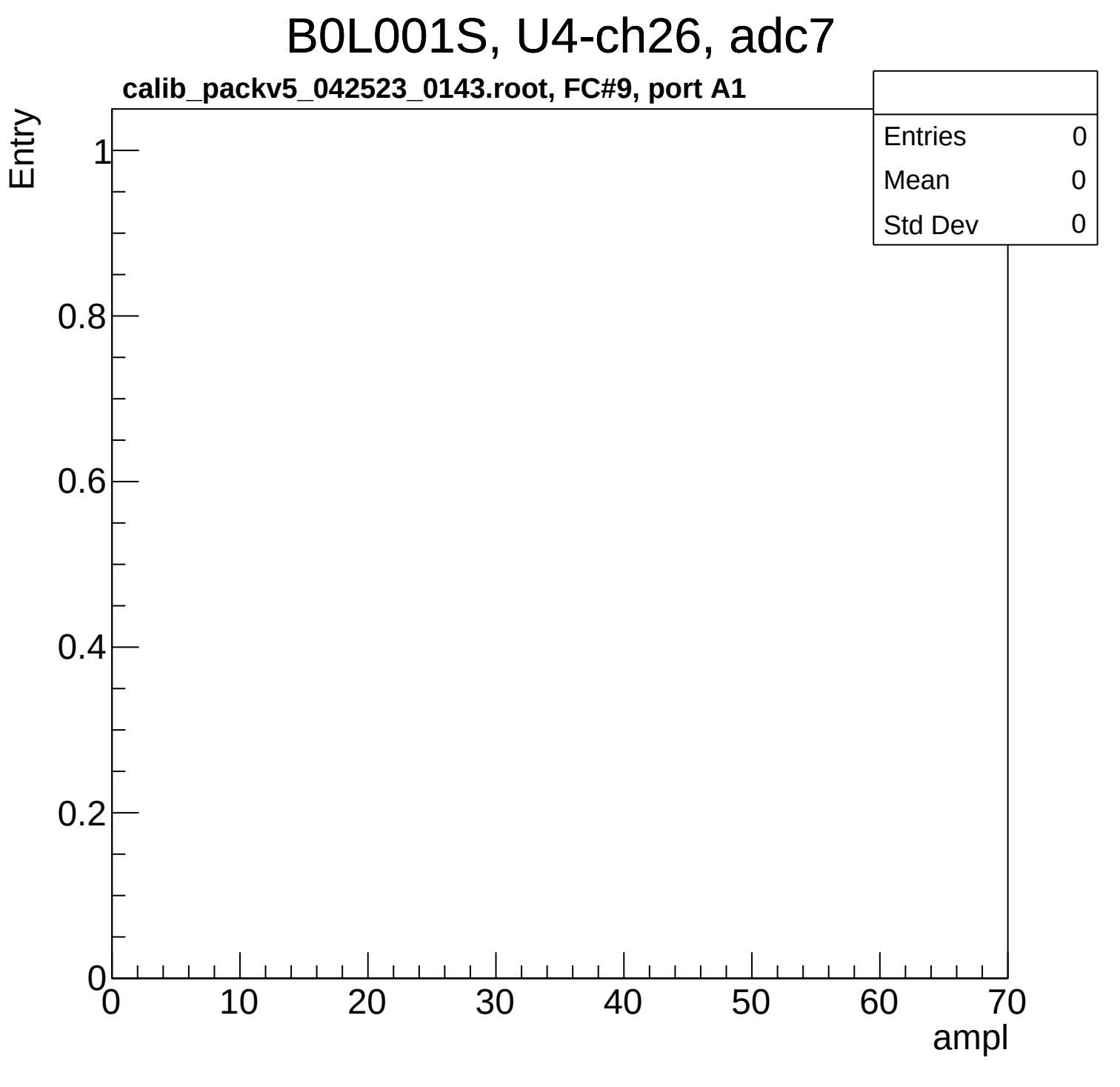
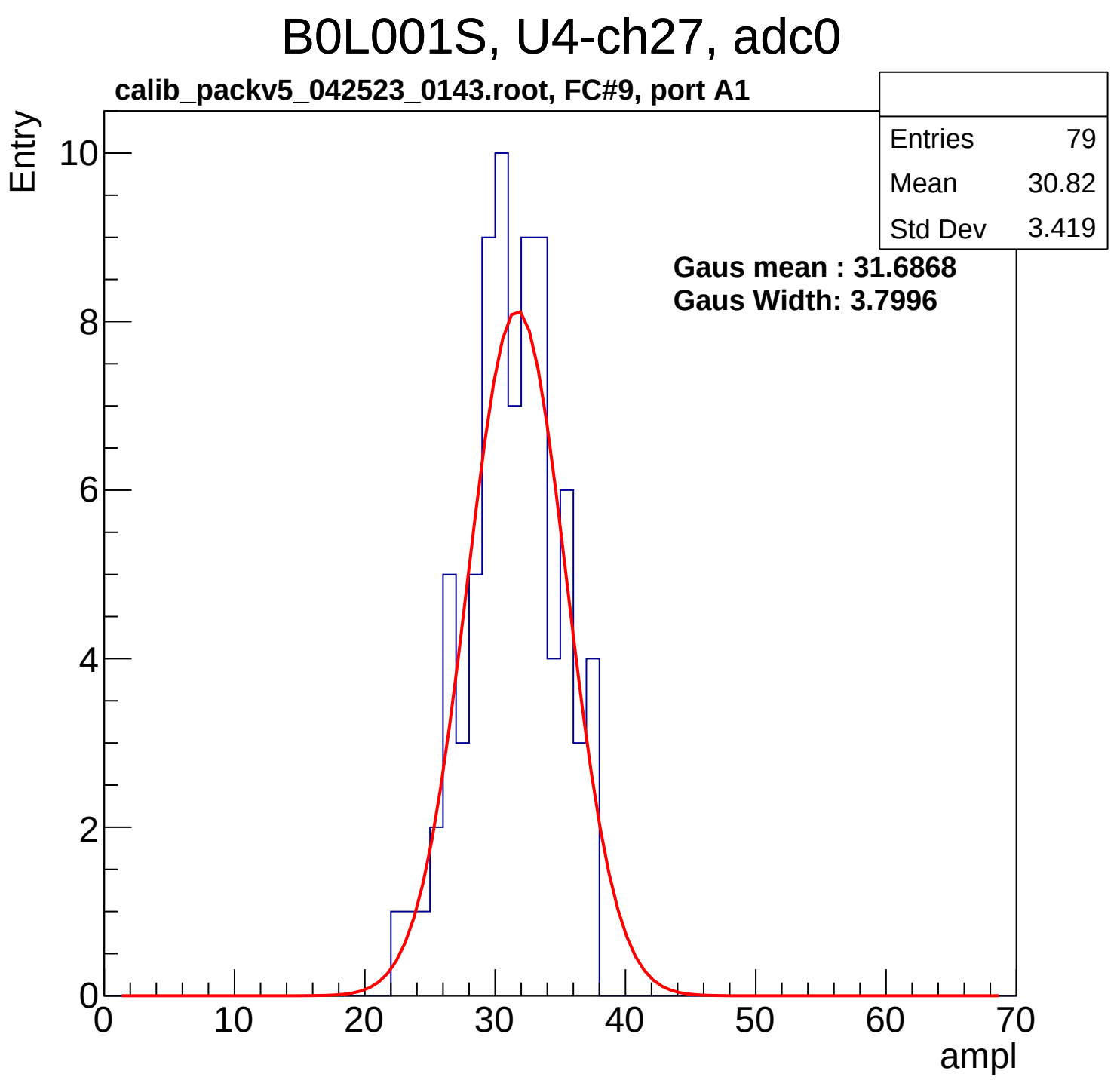
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch26, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch27, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.82
Std Dev 3.419
Gaus mean : 31.6868
Gaus Width: 3.7996
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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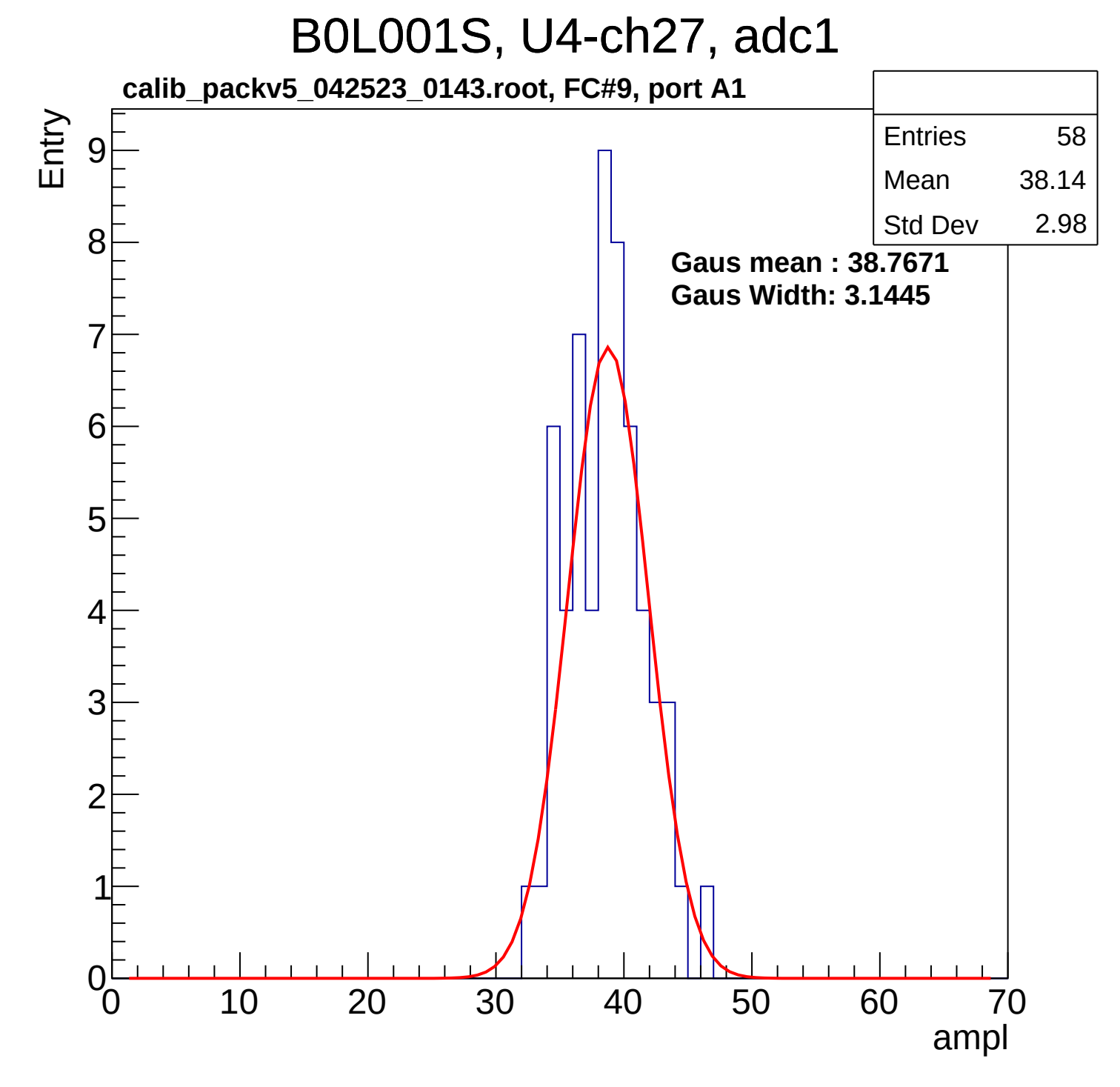
B0L001S, U4-ch27, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 38.14
Std Dev 2.98
Gaus mean : 38.7671
Gaus Width: 3.1445
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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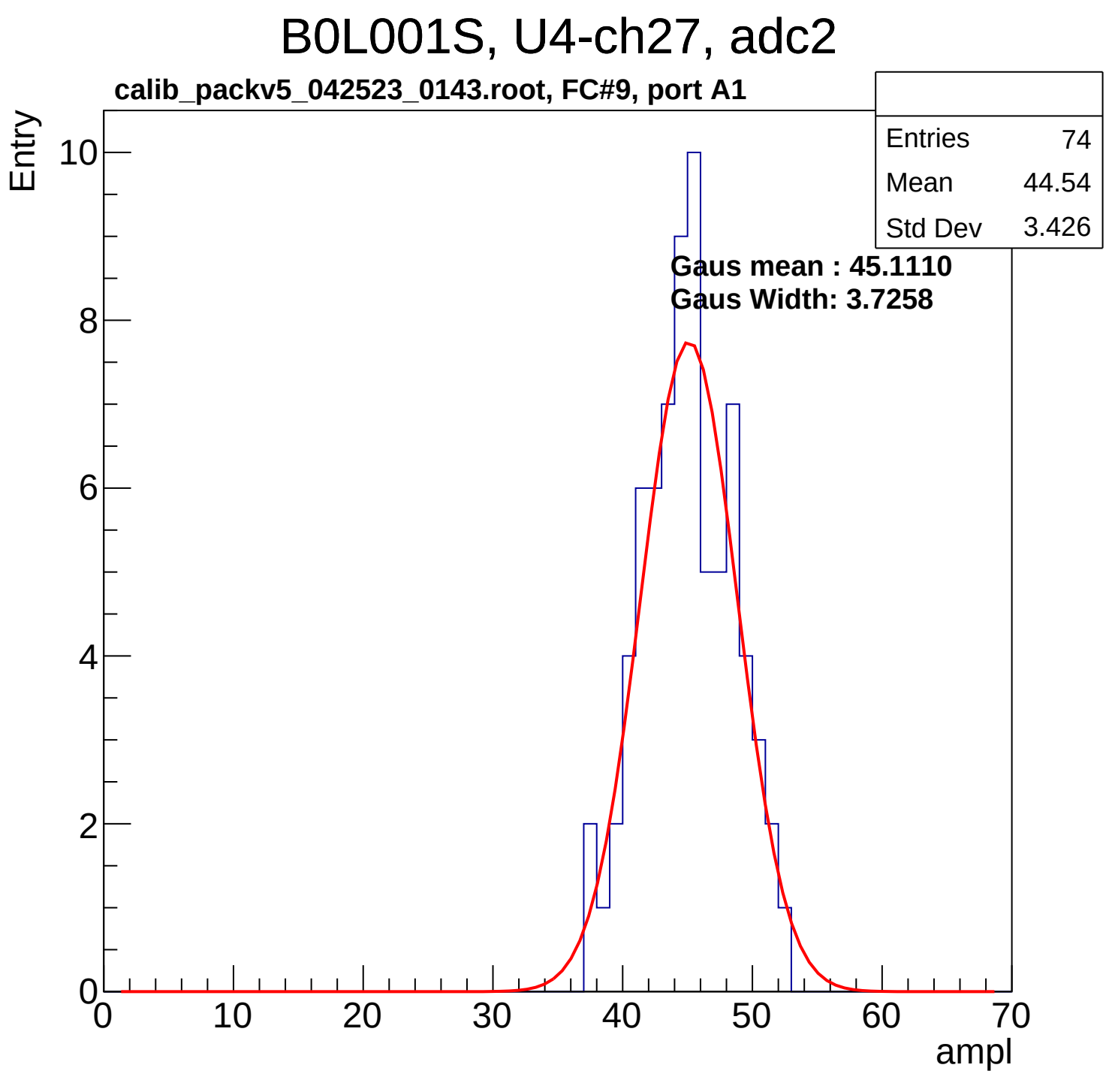
B0L001S, U4-ch27, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 44.54
Std Dev 3.426
Gaus mean : 45.1110
Gaus Width: 3.7258
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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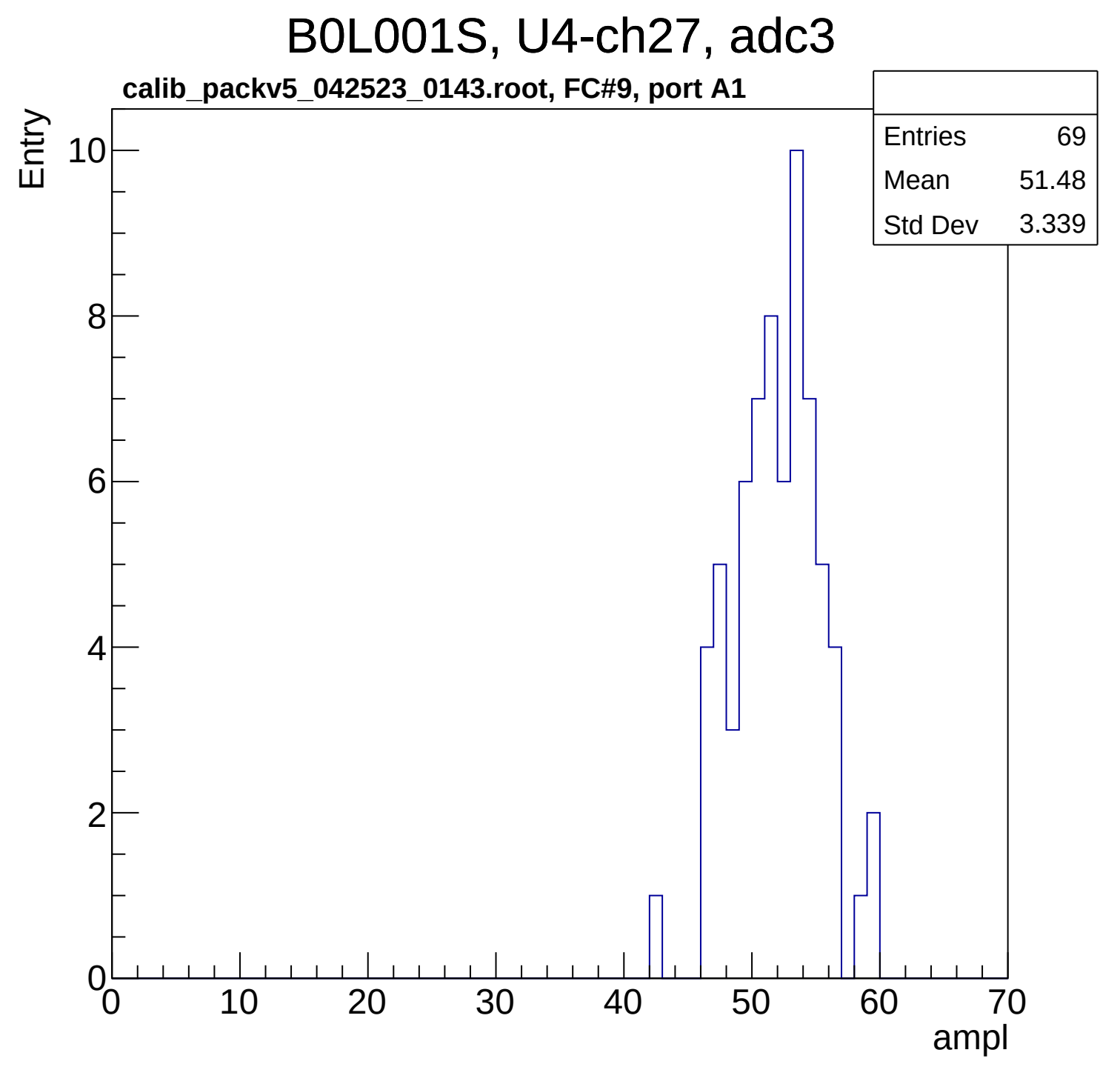
B0L001S, U4-ch27, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.48
Std Dev 3.339
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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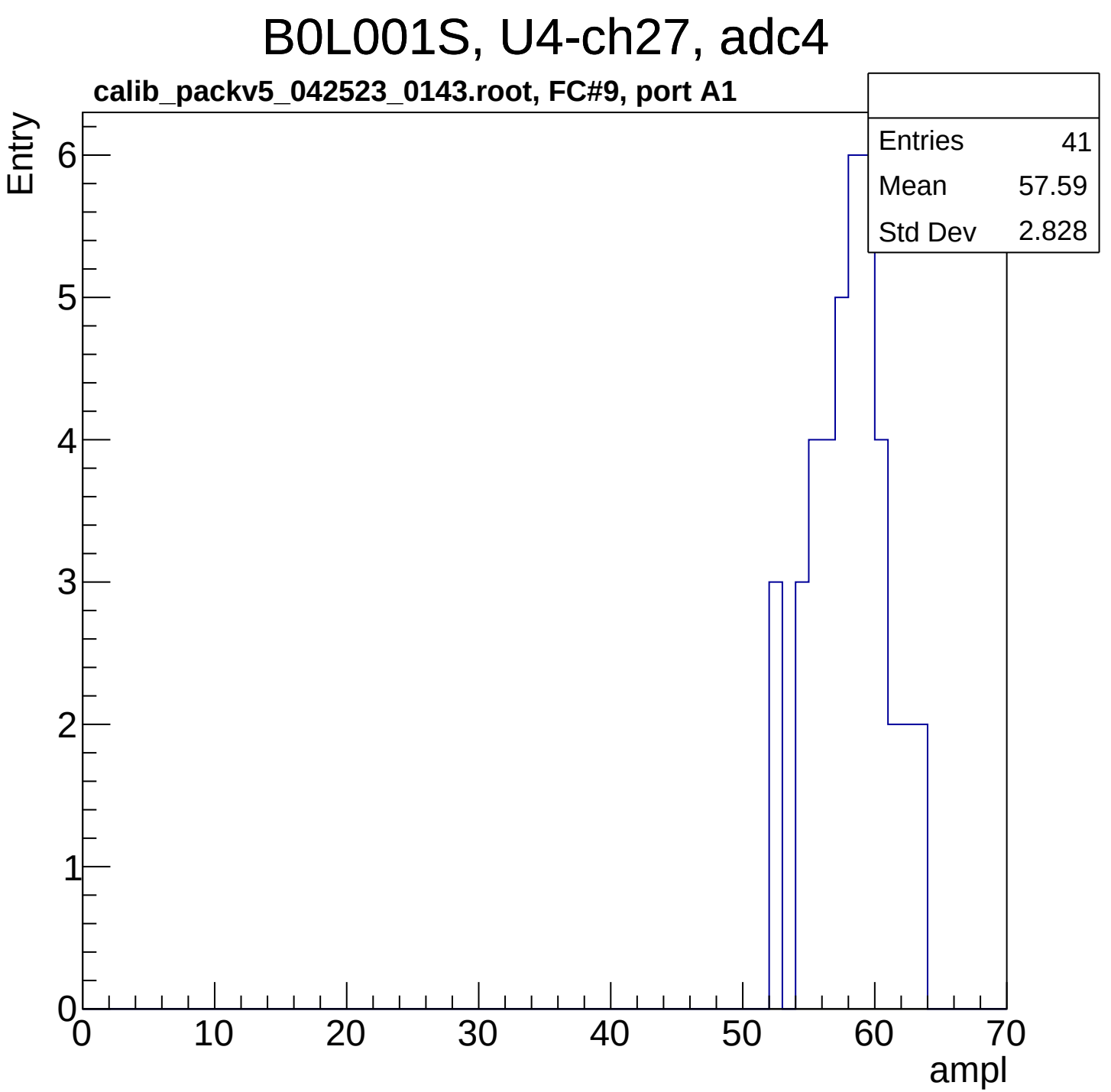
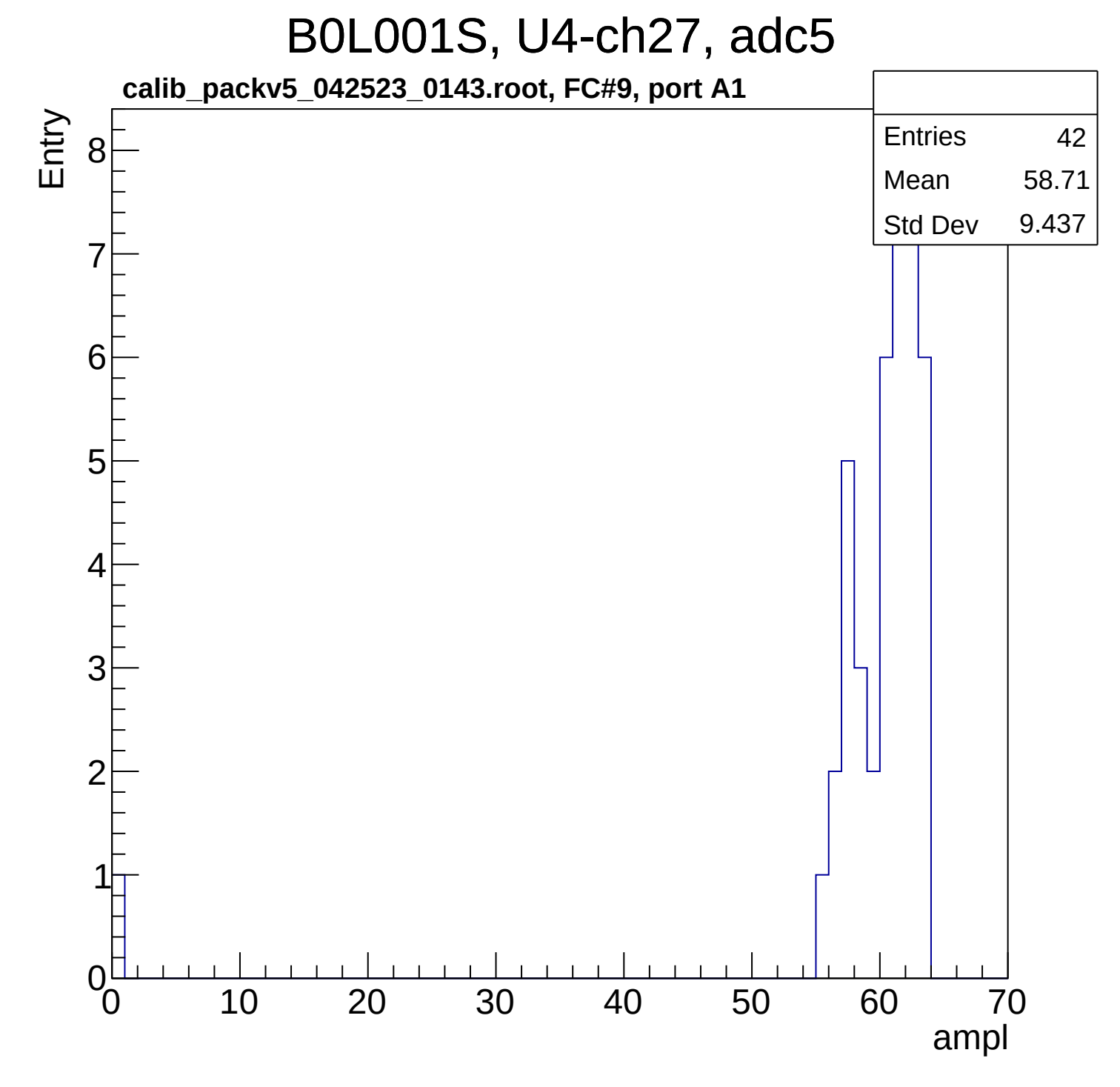

B0L001S, U4-ch27, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 57.59
Std Dev 2.828
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch27, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.71
Std Dev 9.437
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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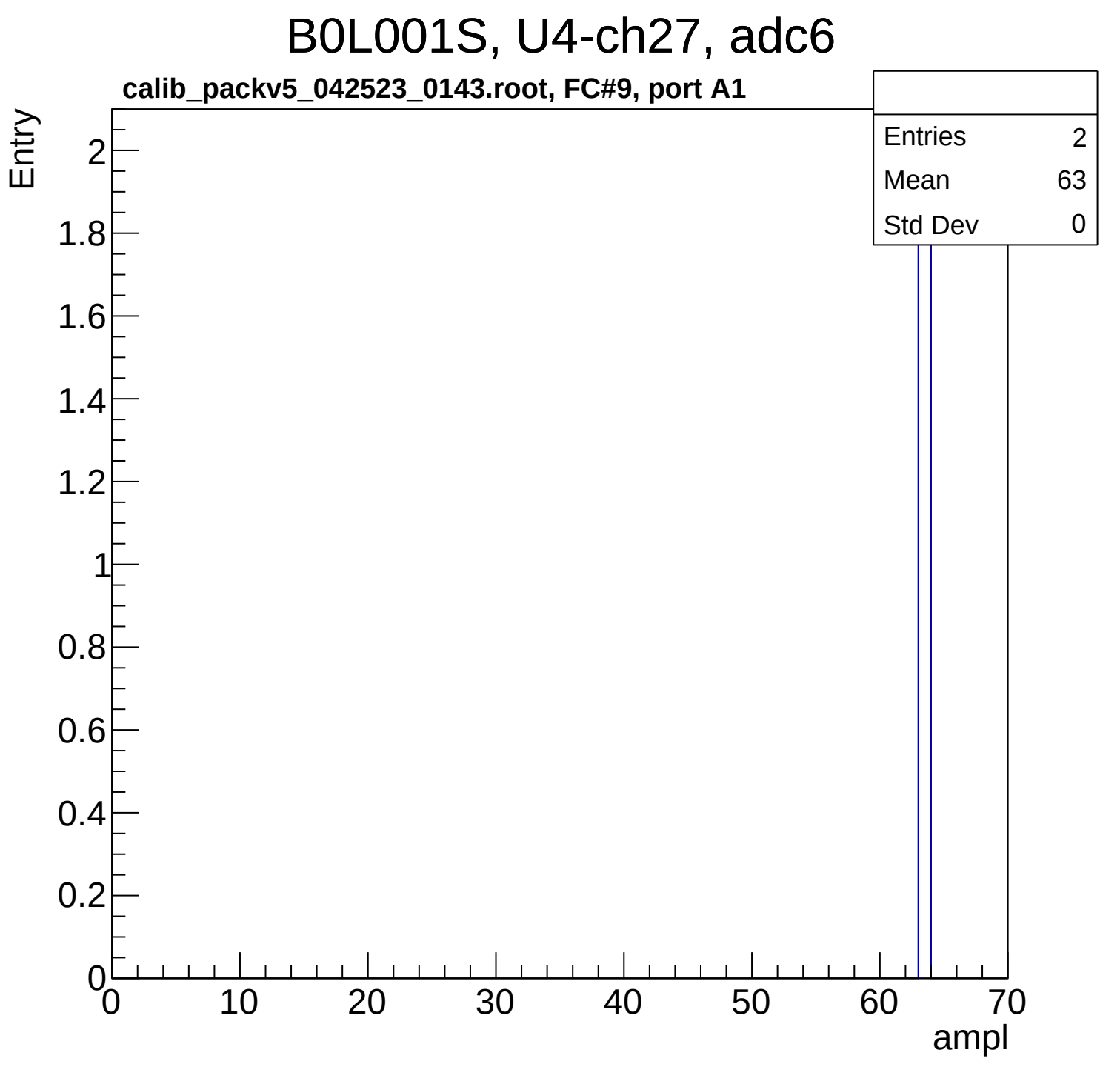
B0L001S, U4-ch27, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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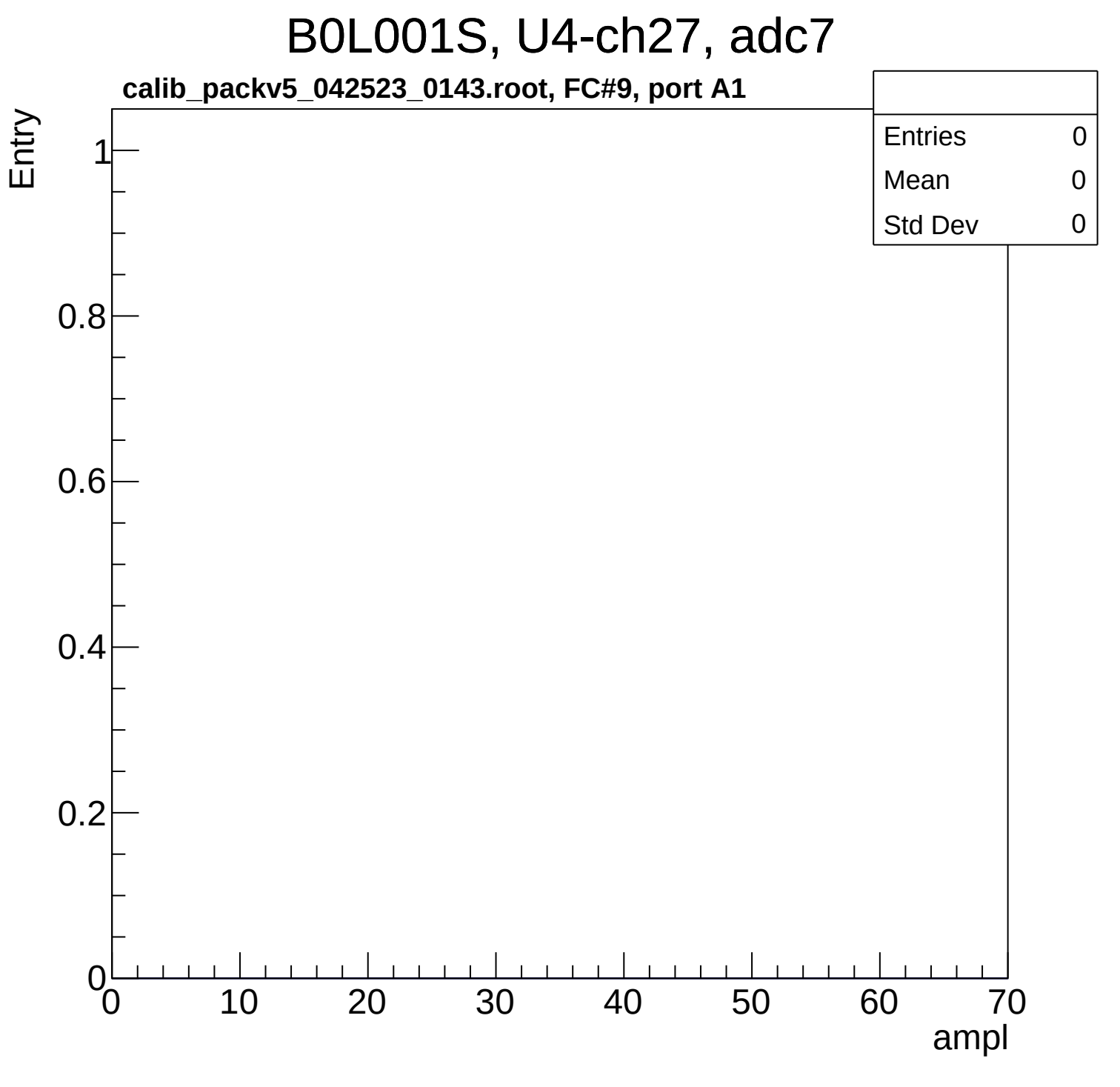
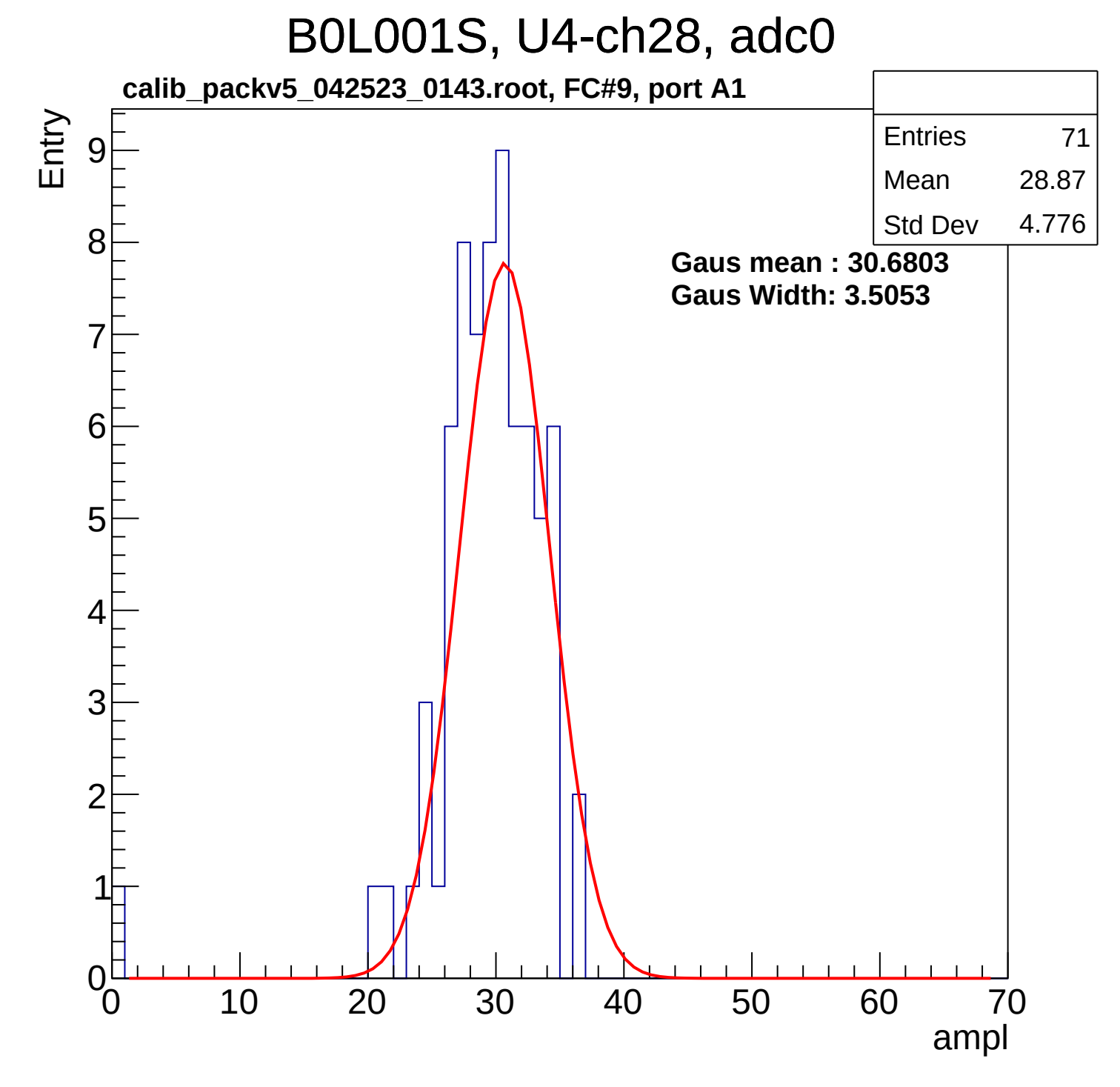
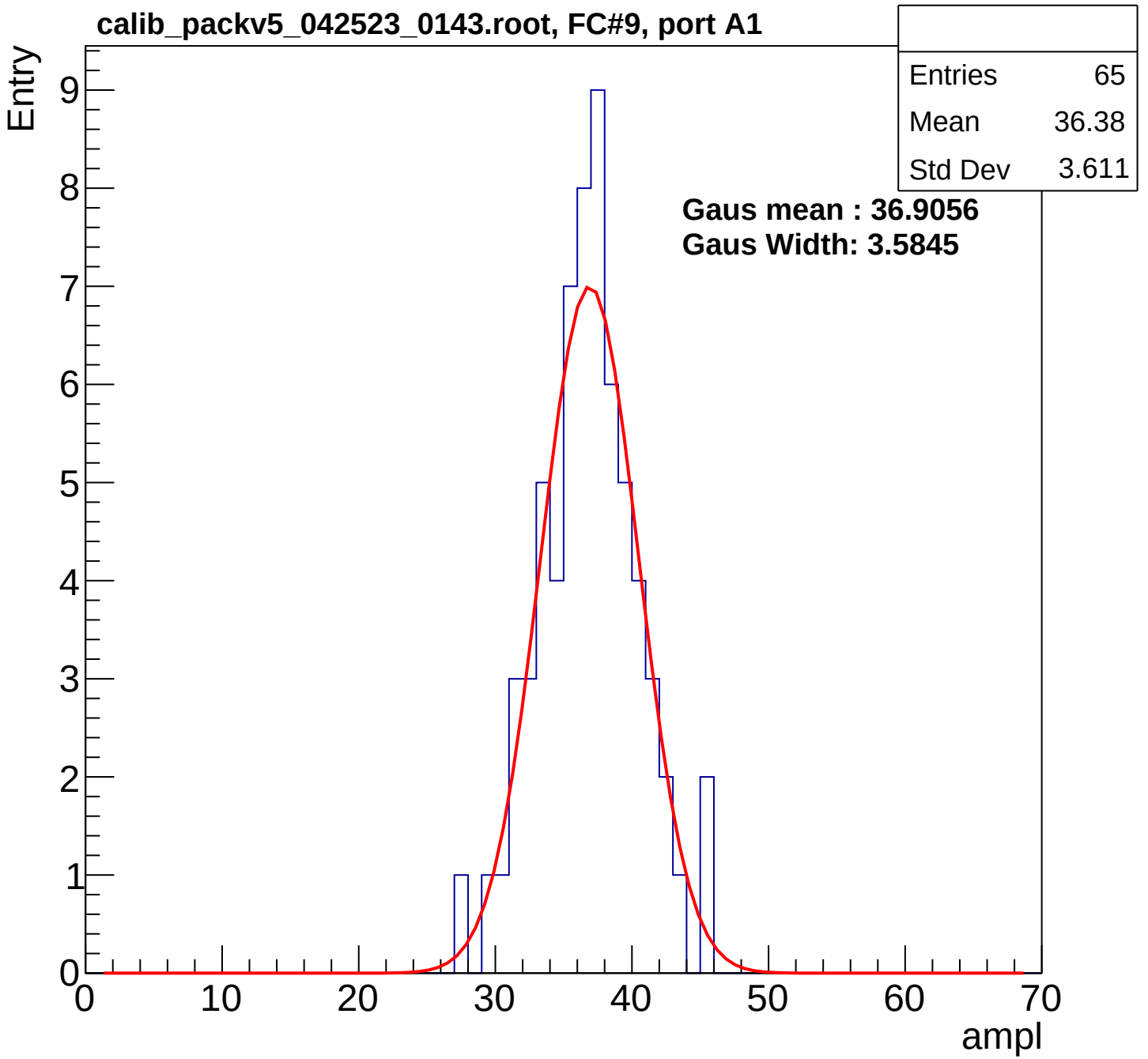
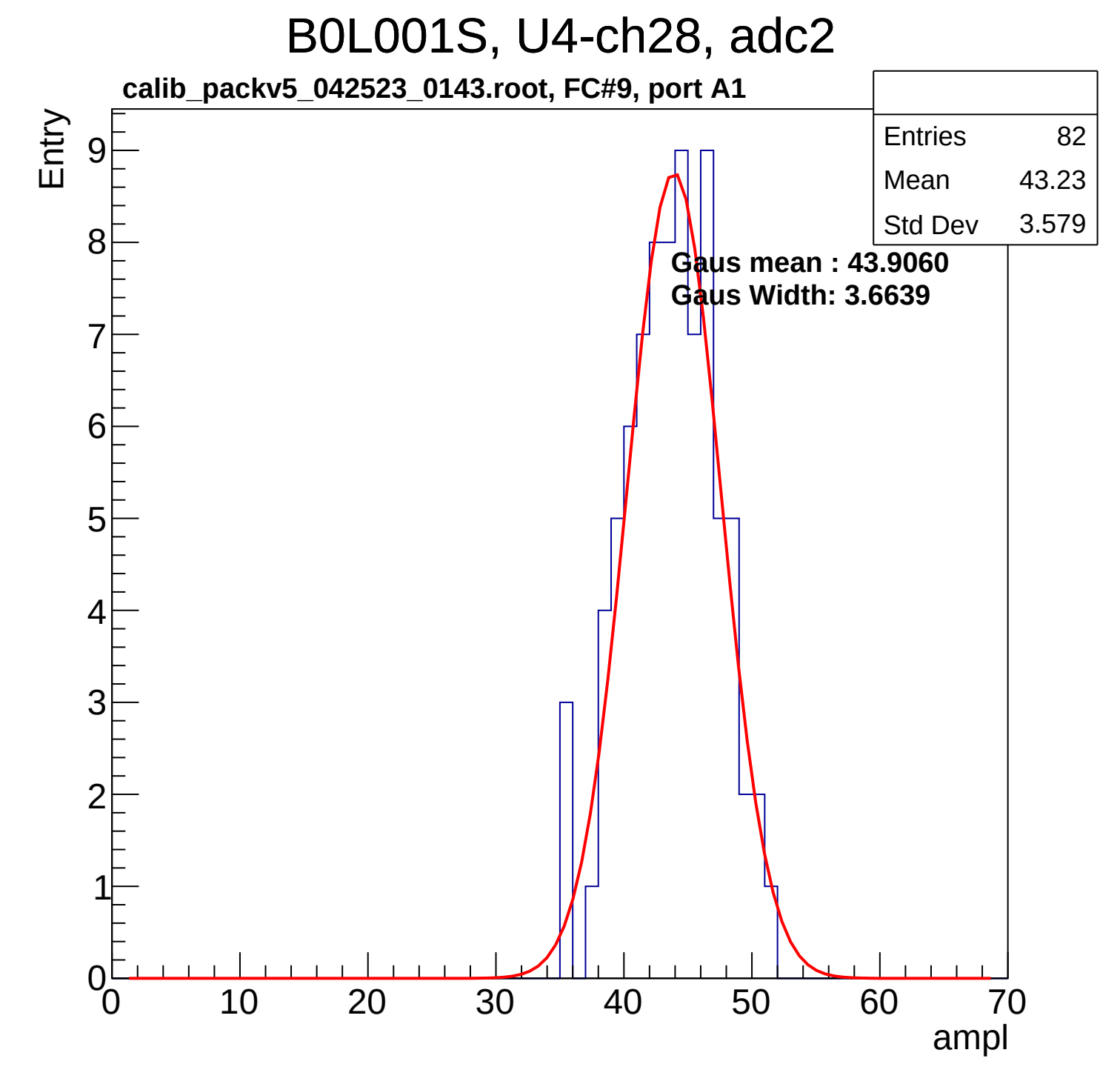

B0L001S, U4-ch27, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch28, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.87
Std Dev 4.776
Gaus mean : 30.6803
Gaus Width: 3.5053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch28, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.38
Std Dev 3.611
Gaus mean : 36.9056
Gaus Width: 3.5845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch28, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 43.23
Std Dev 3.579
Gaus mean : 43.9060
Gaus Width: 3.6639
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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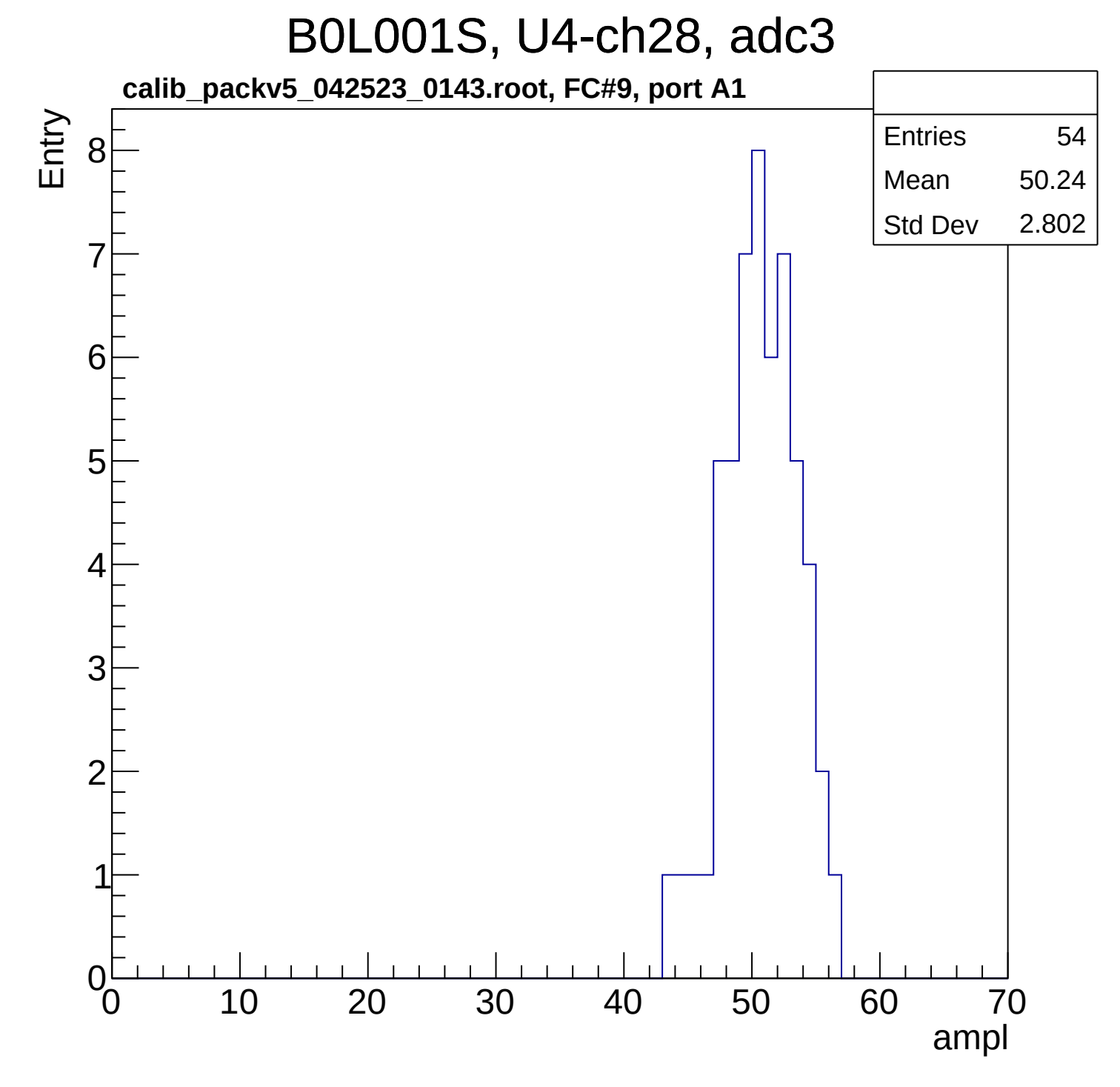
B0L001S, U4-ch28, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 50.24
Std Dev 2.802
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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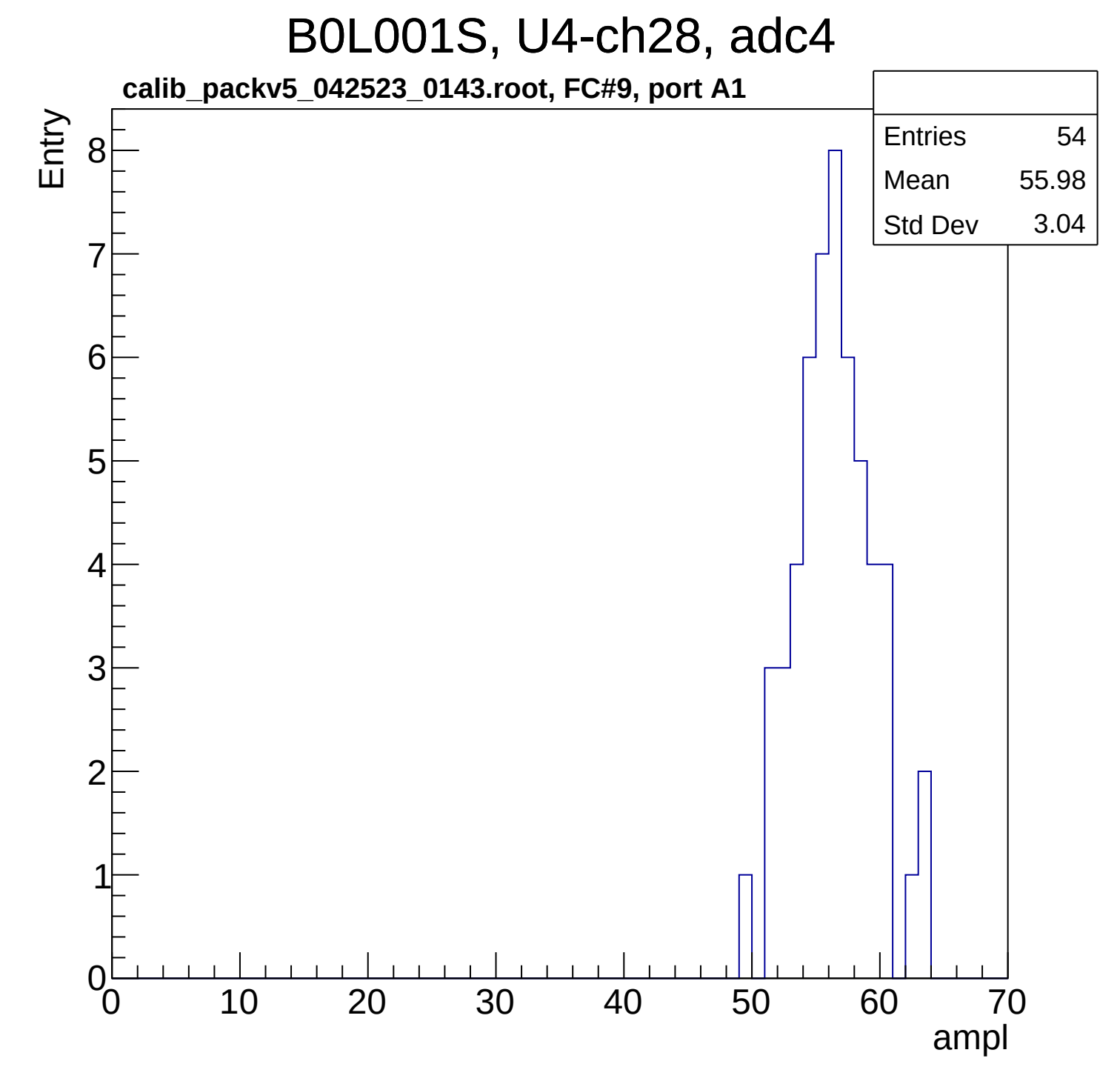
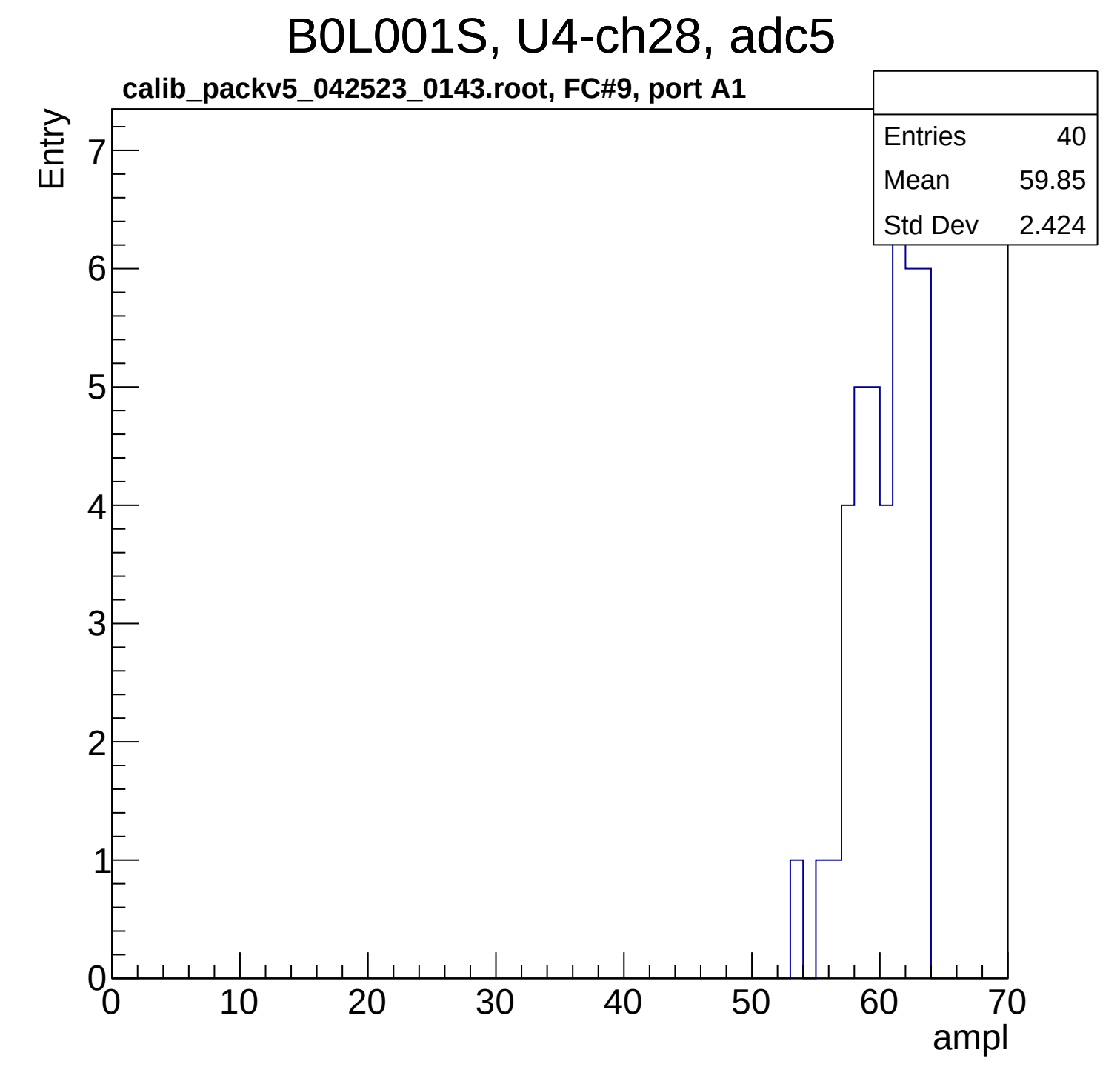
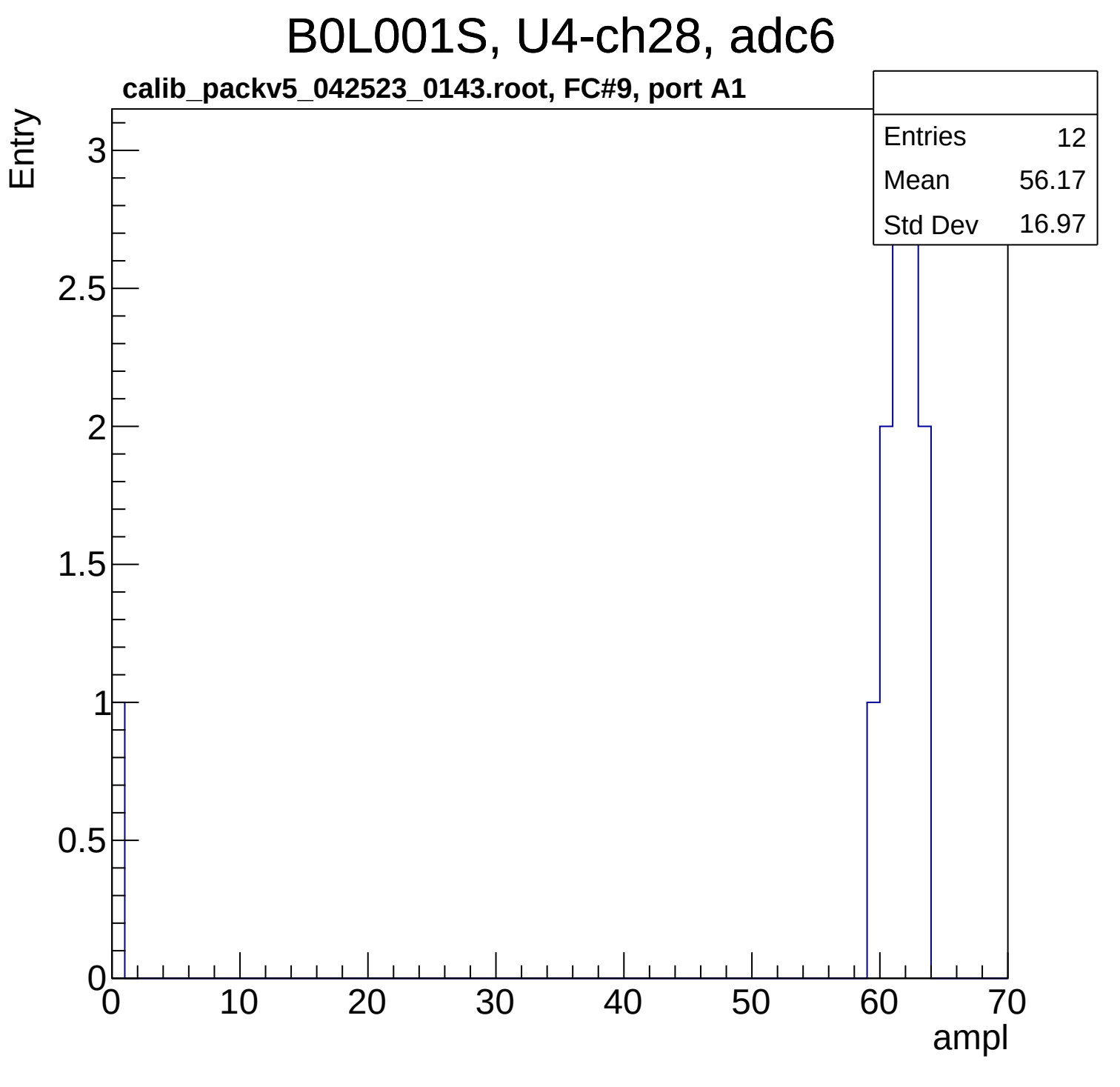
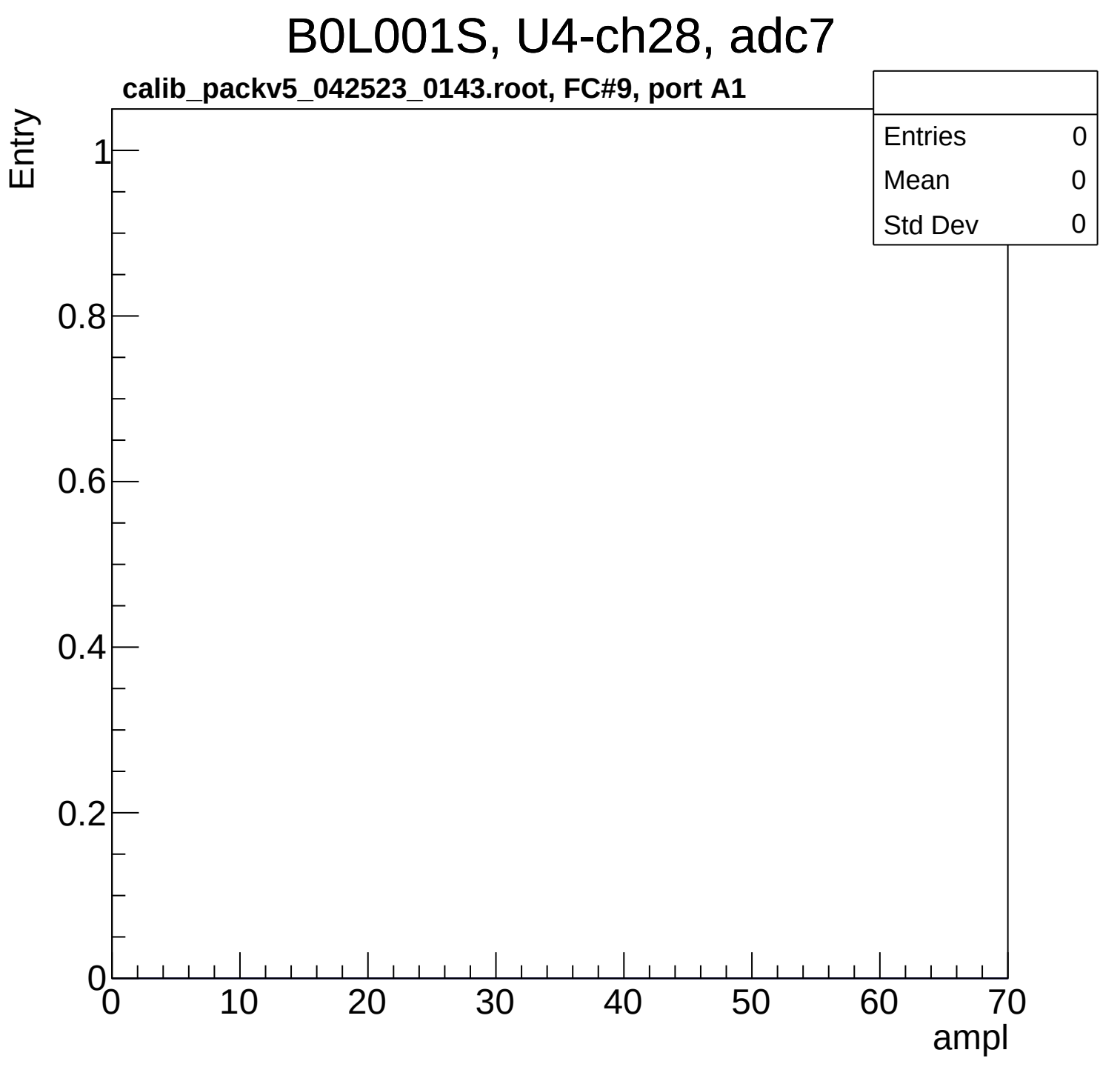
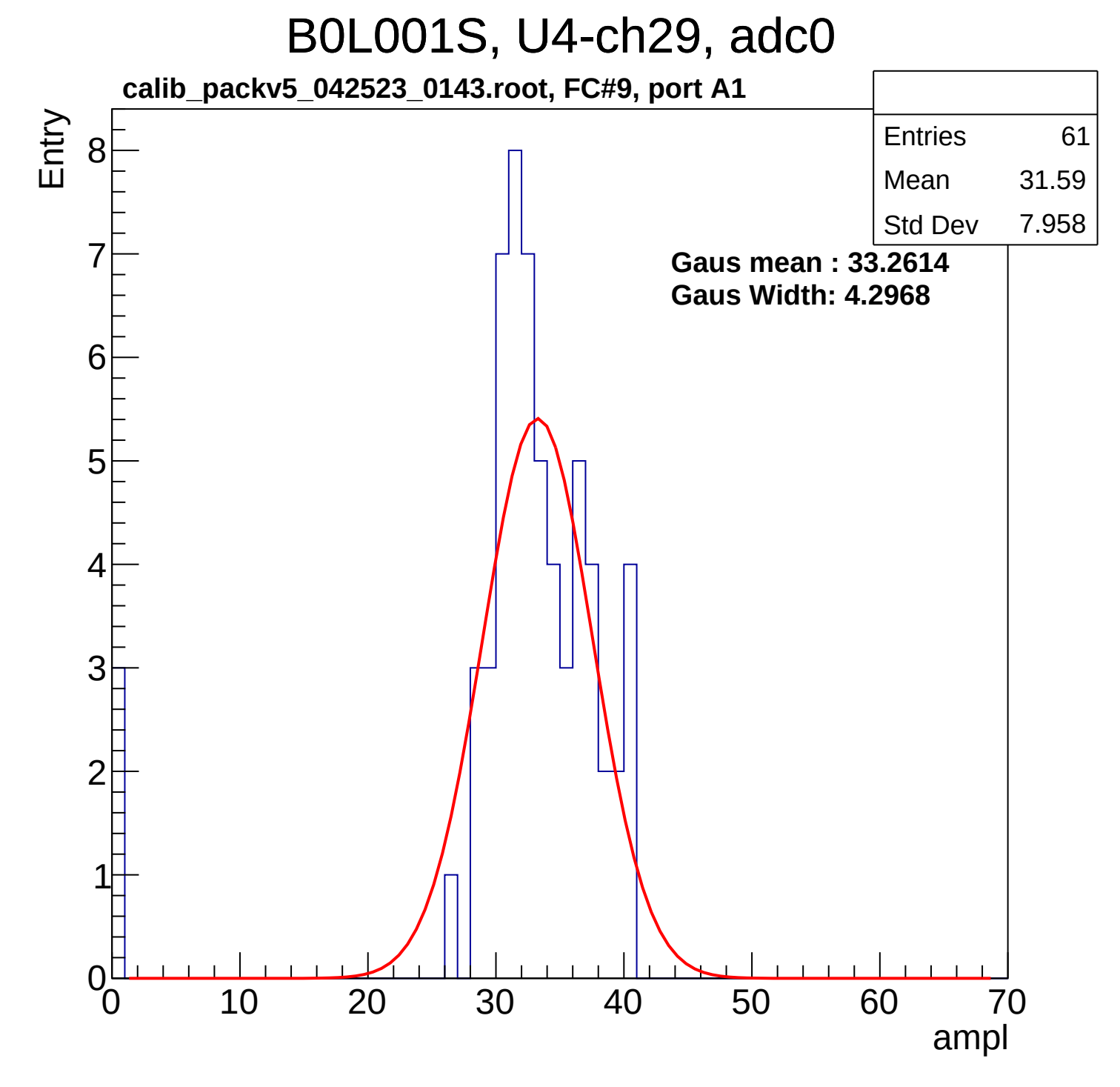

B0L001S, U4-ch28, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.98
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch28, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.85
Std Dev 2.424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch28, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.17
Std Dev 16.97
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch28, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch29, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.59
Std Dev 7.958
Gaus mean : 33.2614
Gaus Width: 4.2968
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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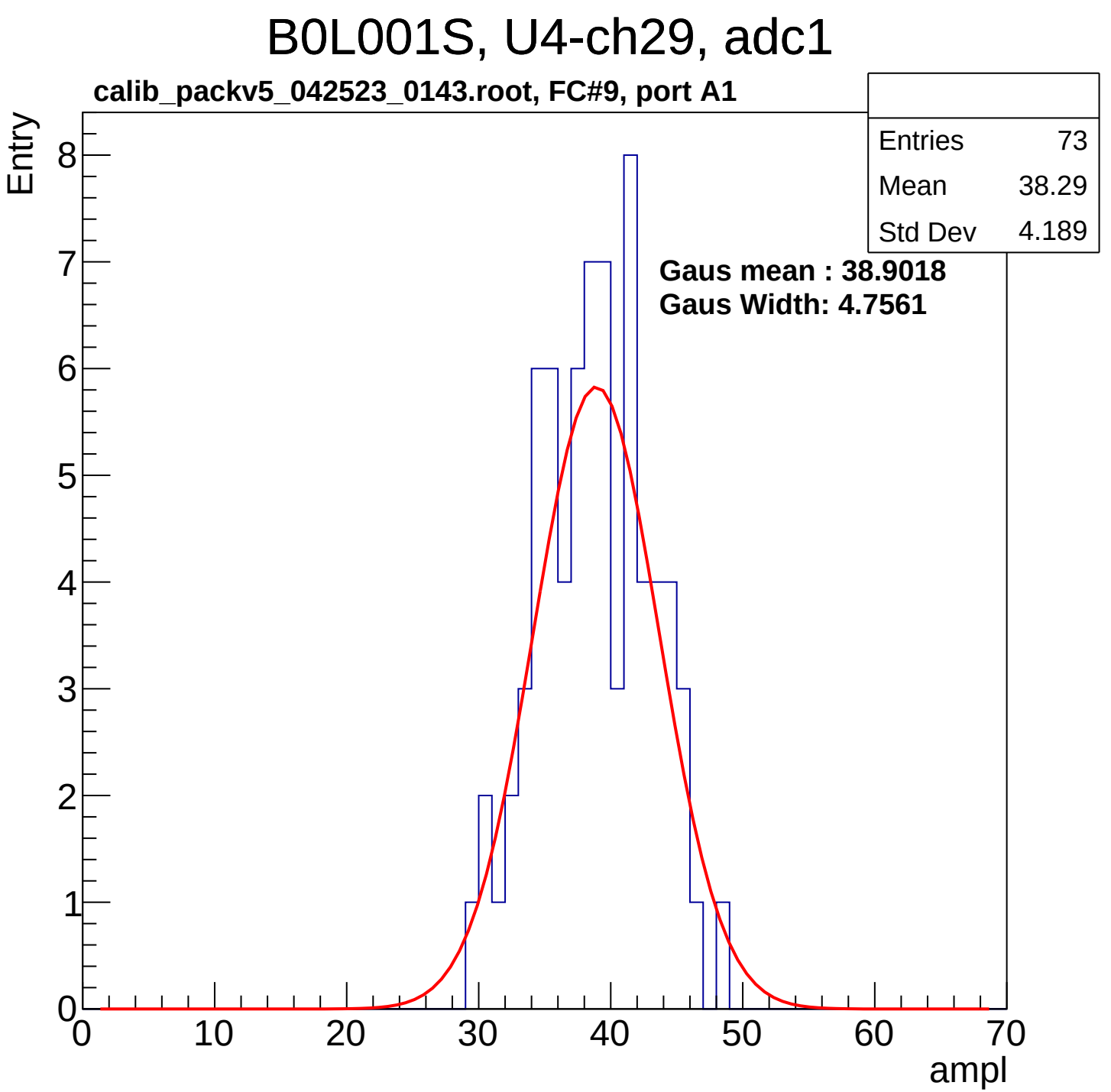
B0L001S, U4-ch29, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 38.29
Std Dev 4.189
Gaus mean : 38.9018
Gaus Width: 4.7561
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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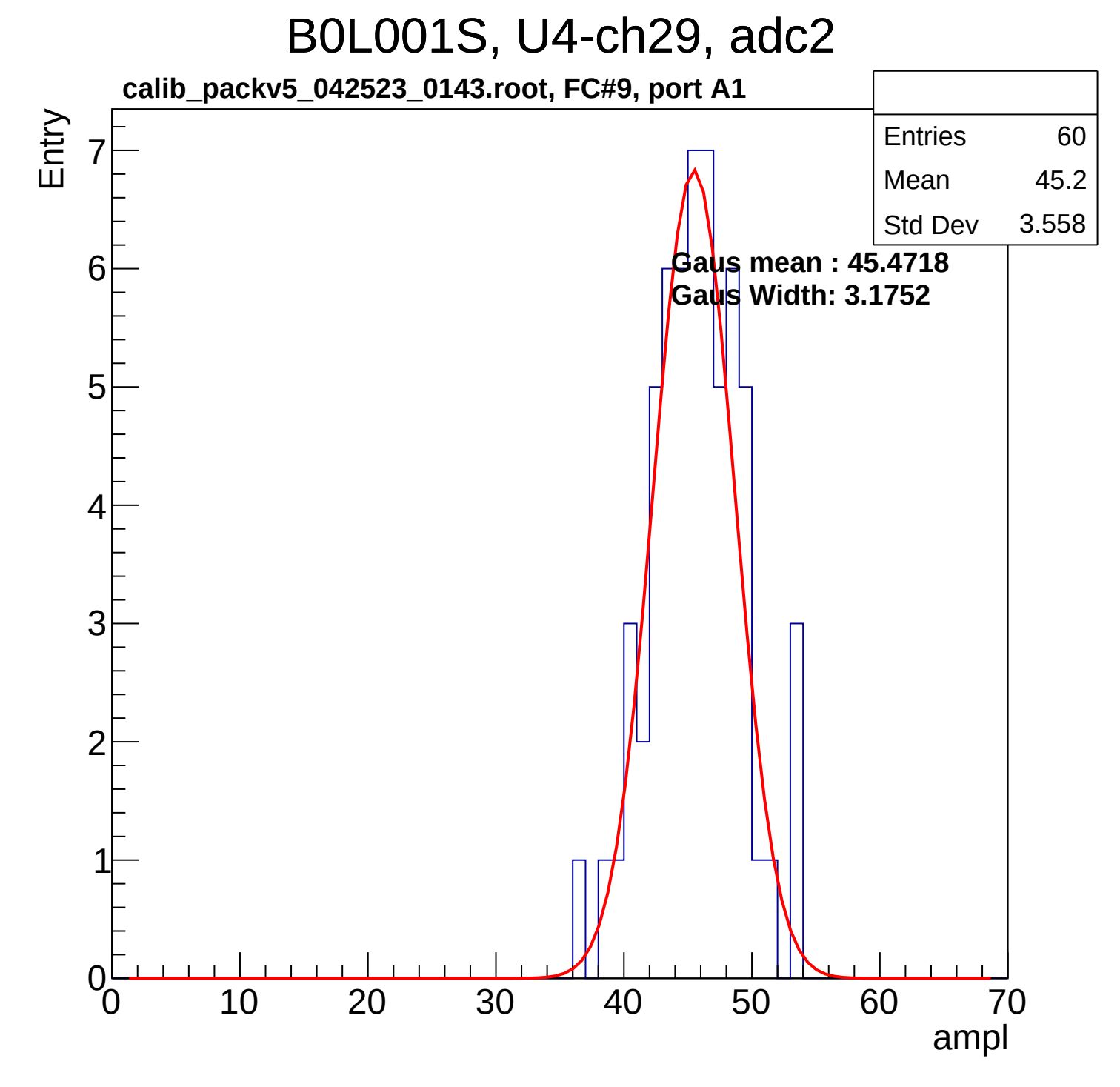
B0L001S, U4-ch29, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.2
Std Dev 3.558
Gaus mean : 45.4718
Gaus Width: 3.1752
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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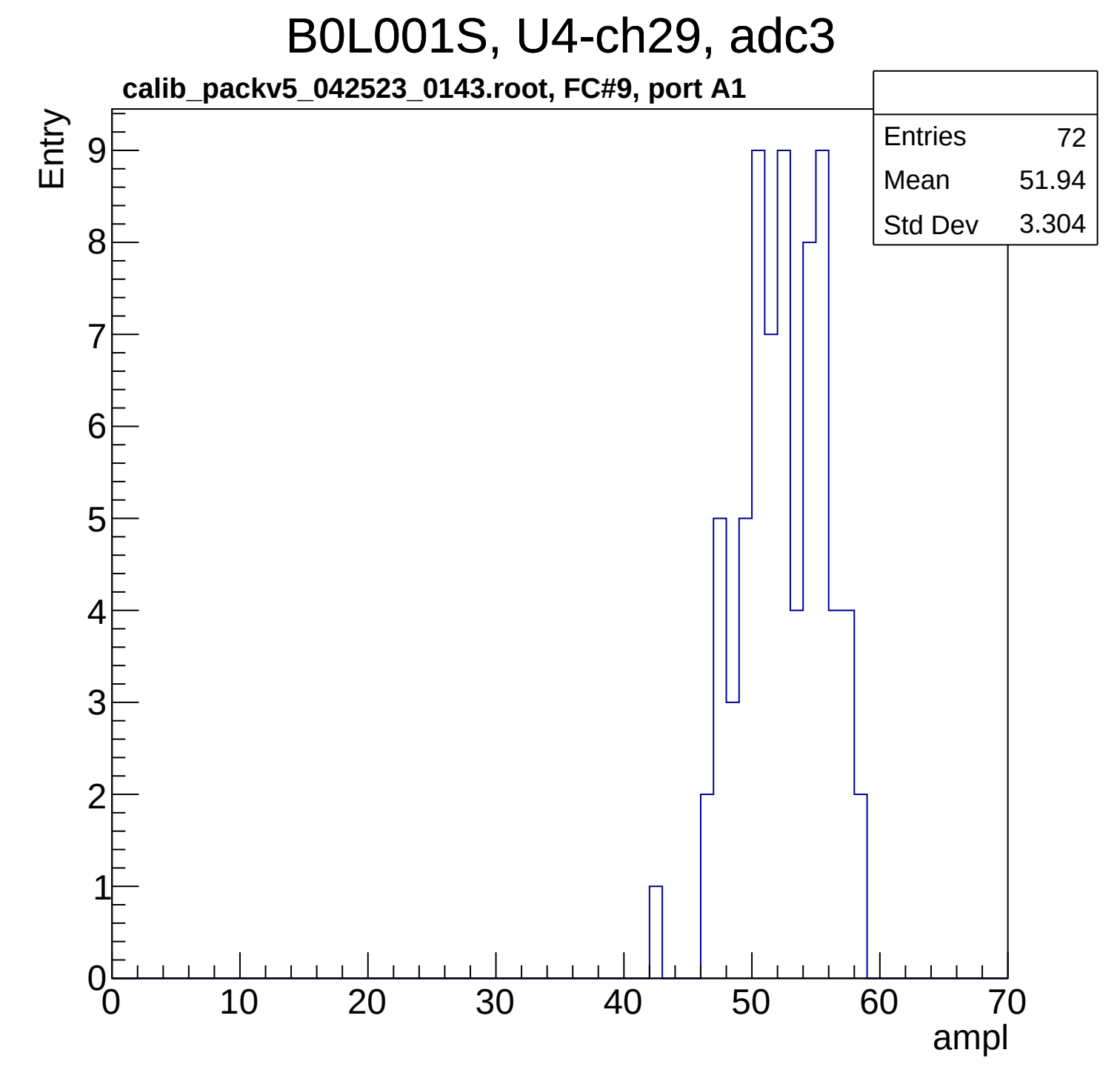
B0L001S, U4-ch29, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.94
Std Dev 3.304
Entry
ampl
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3
4
5
6
7
8
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0
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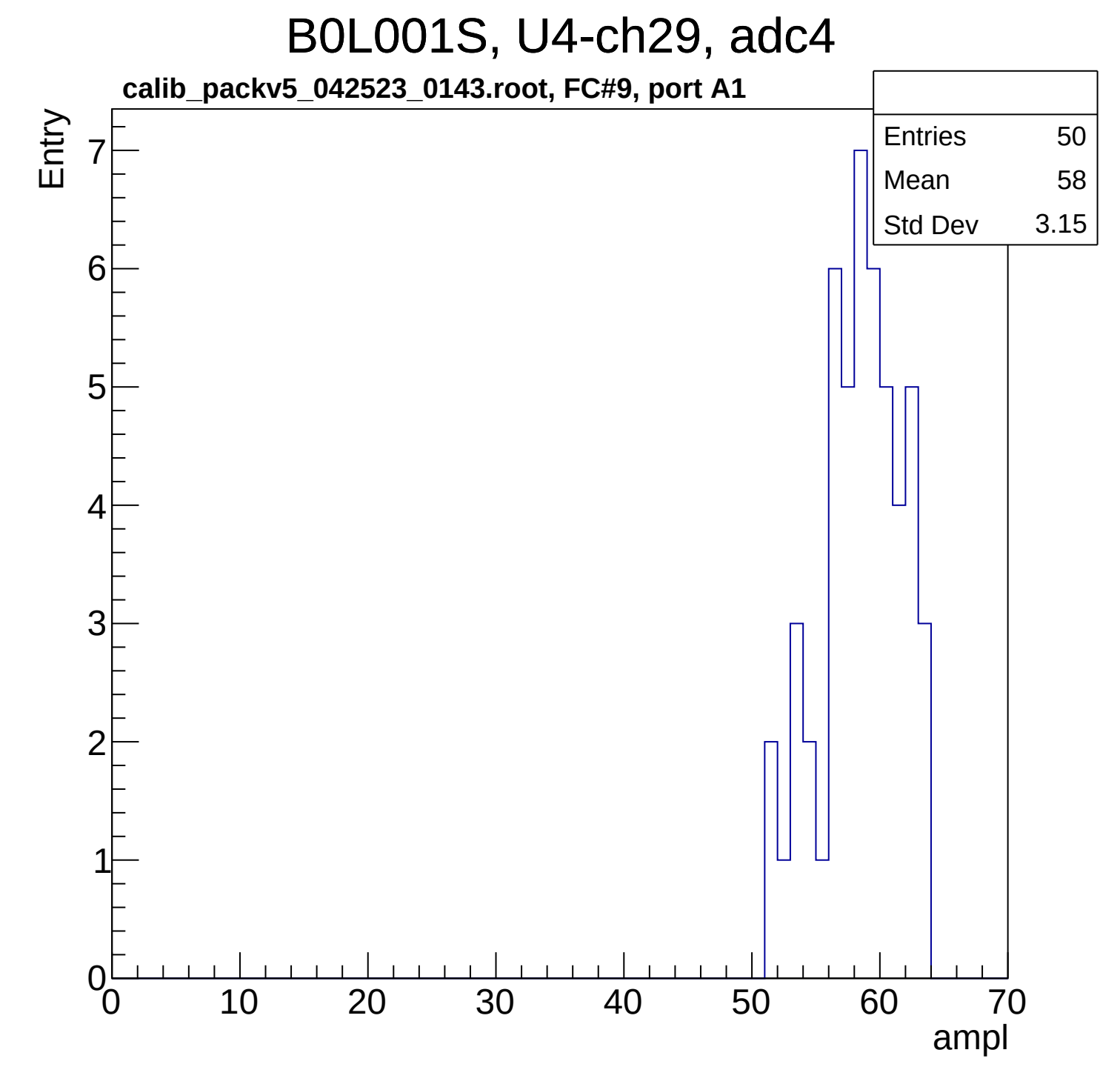
B0L001S, U4-ch29, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 58
Std Dev 3.15
Entry
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1
2
3
4
5
6
7
0
10
20
30
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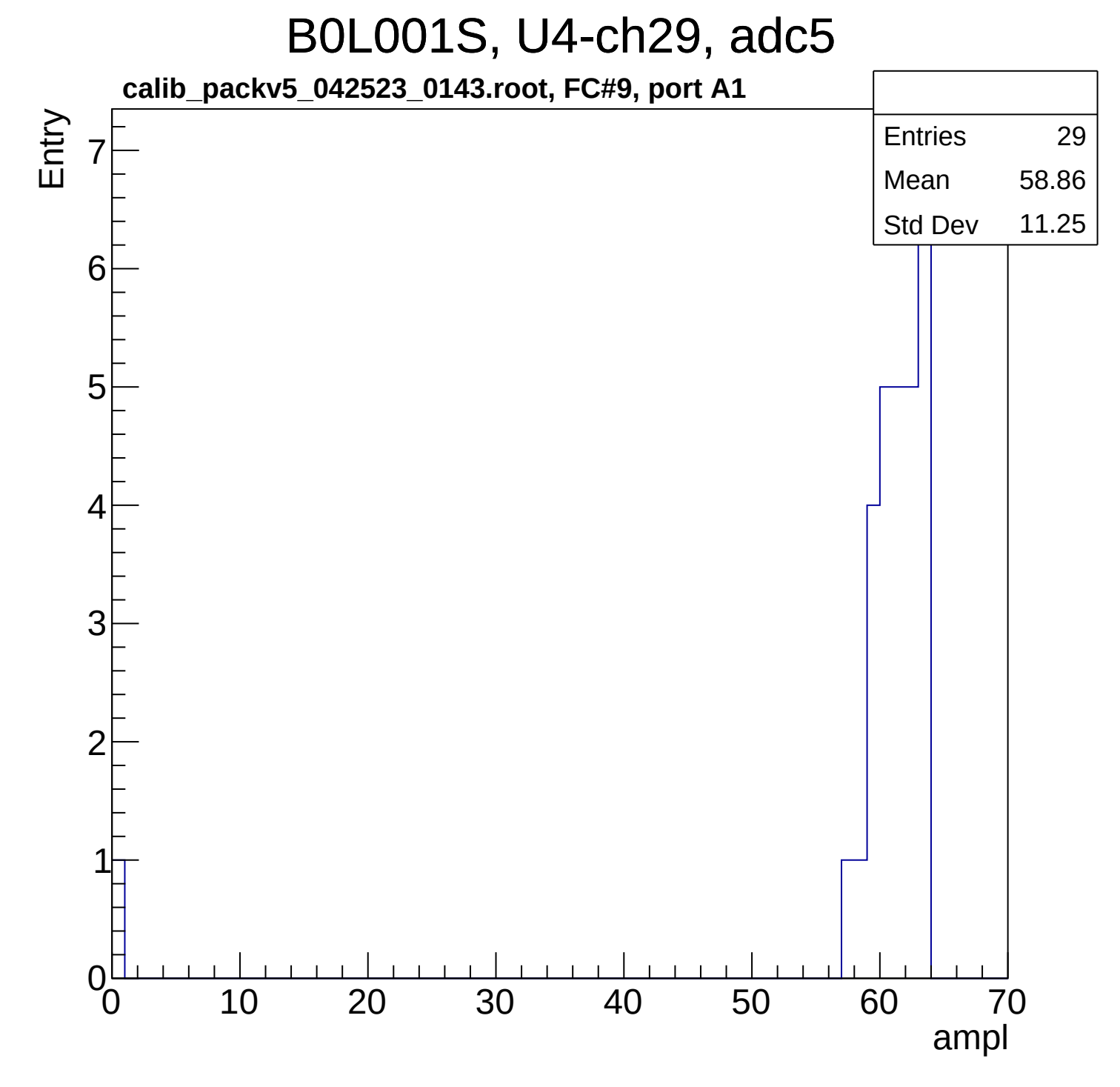
50
60
70
ampl


B0L001S, U4-ch29, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.86
Std Dev 11.25
Entry
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2
3
4
5
6
7
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10
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30
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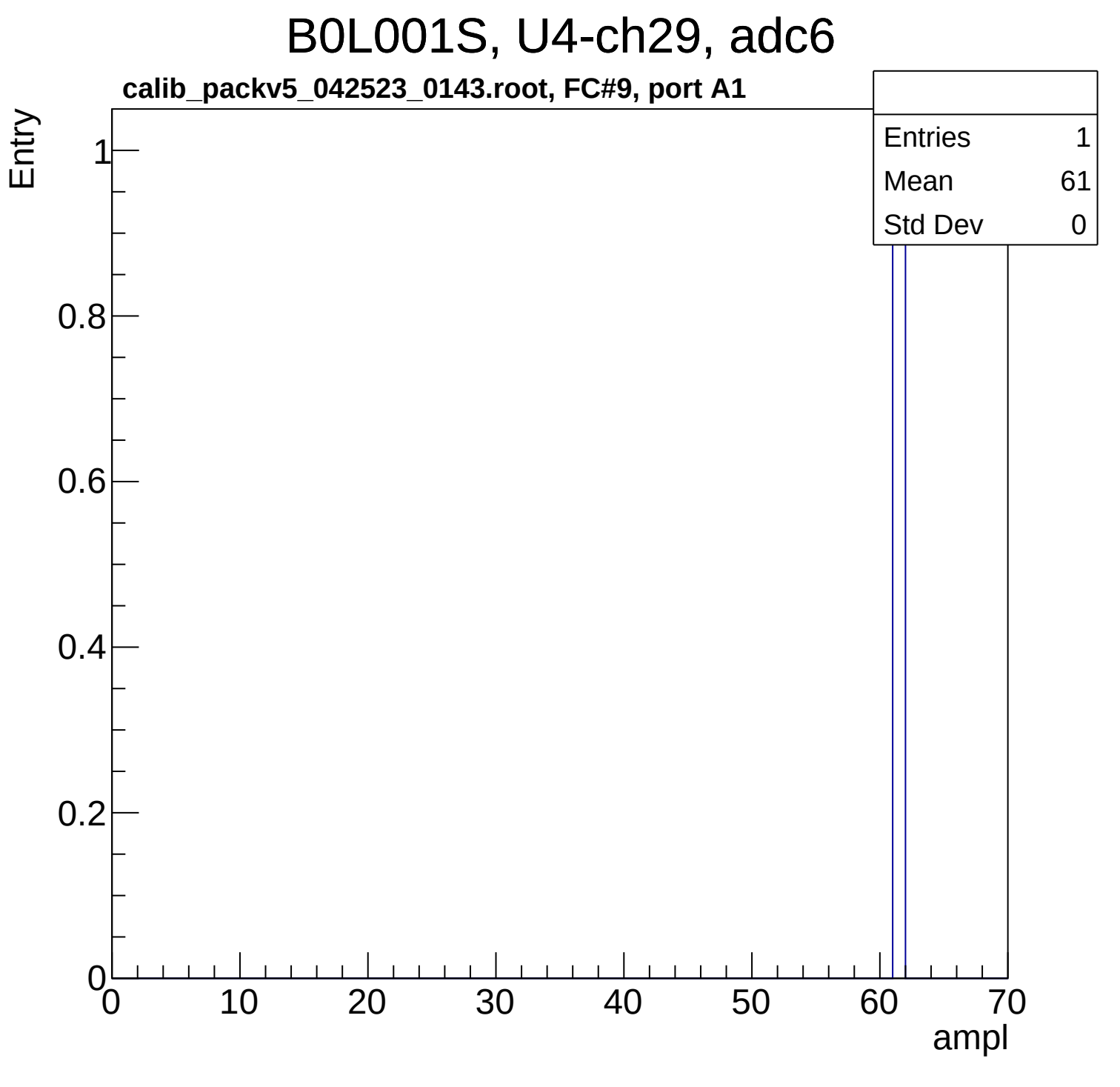
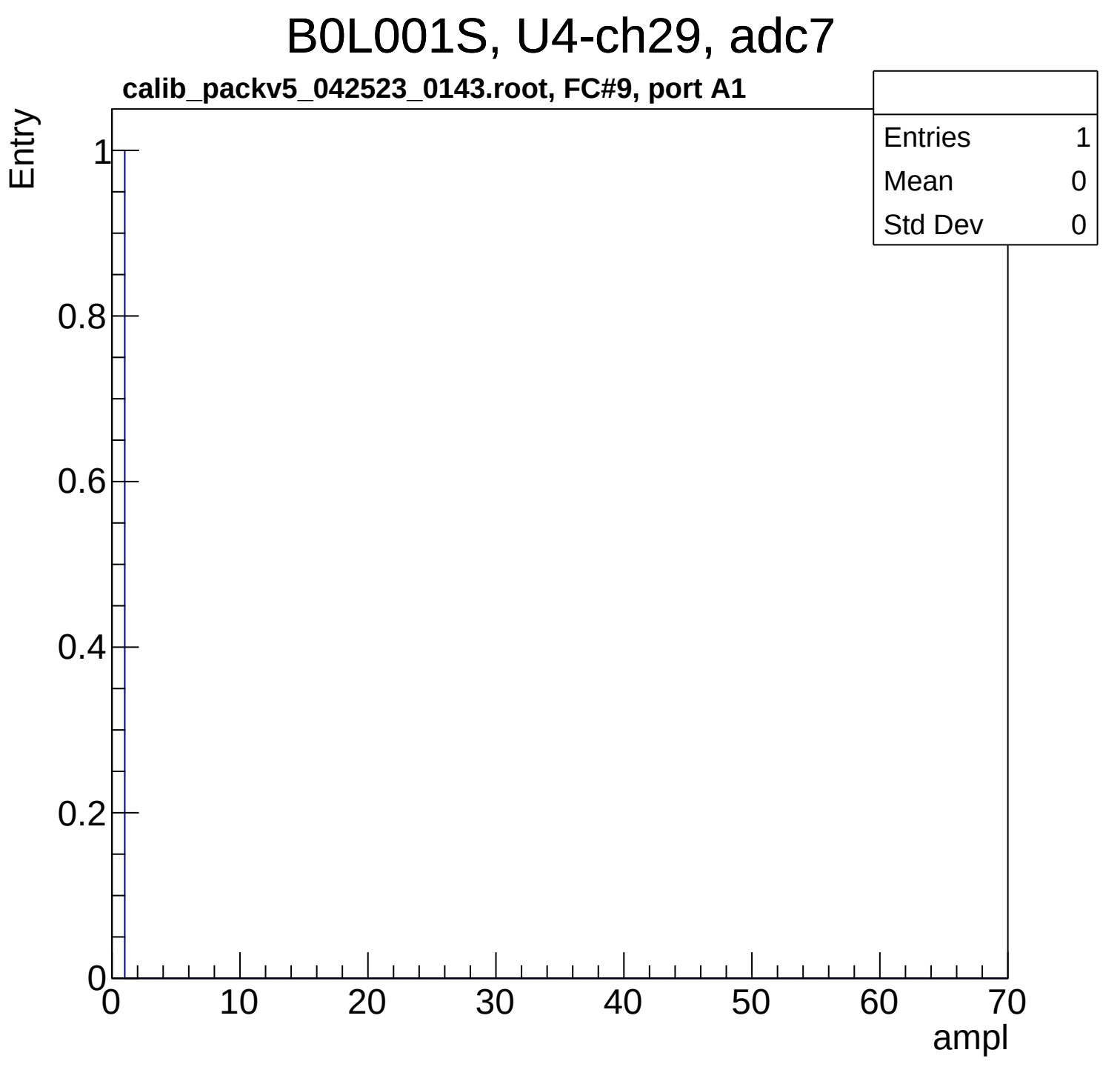
50
60
70
ampl


B0L001S, U4-ch29, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch29, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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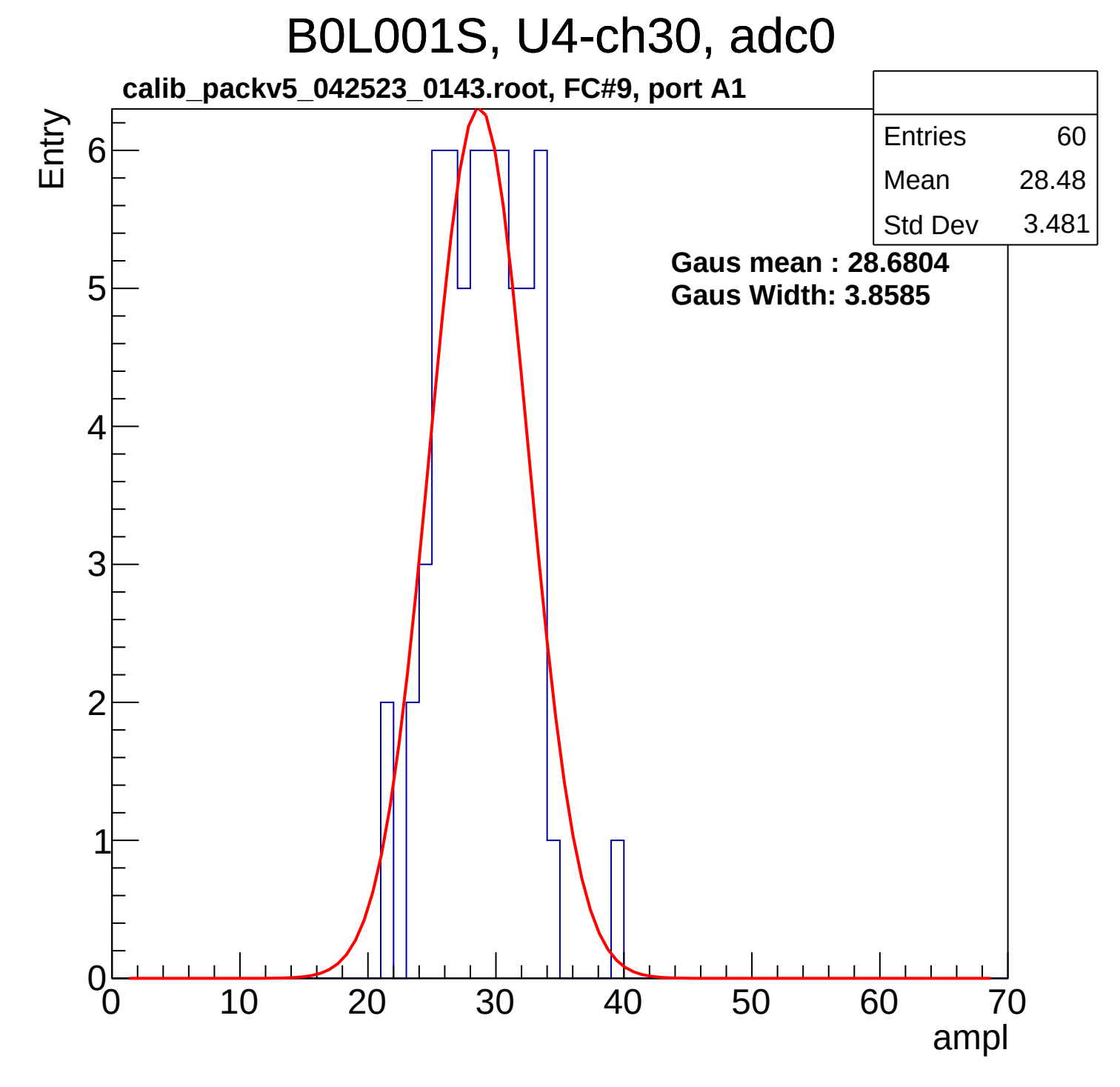
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch30, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 28.48
Std Dev 3.481
Gaus mean : 28.6804
Gaus Width: 3.8585
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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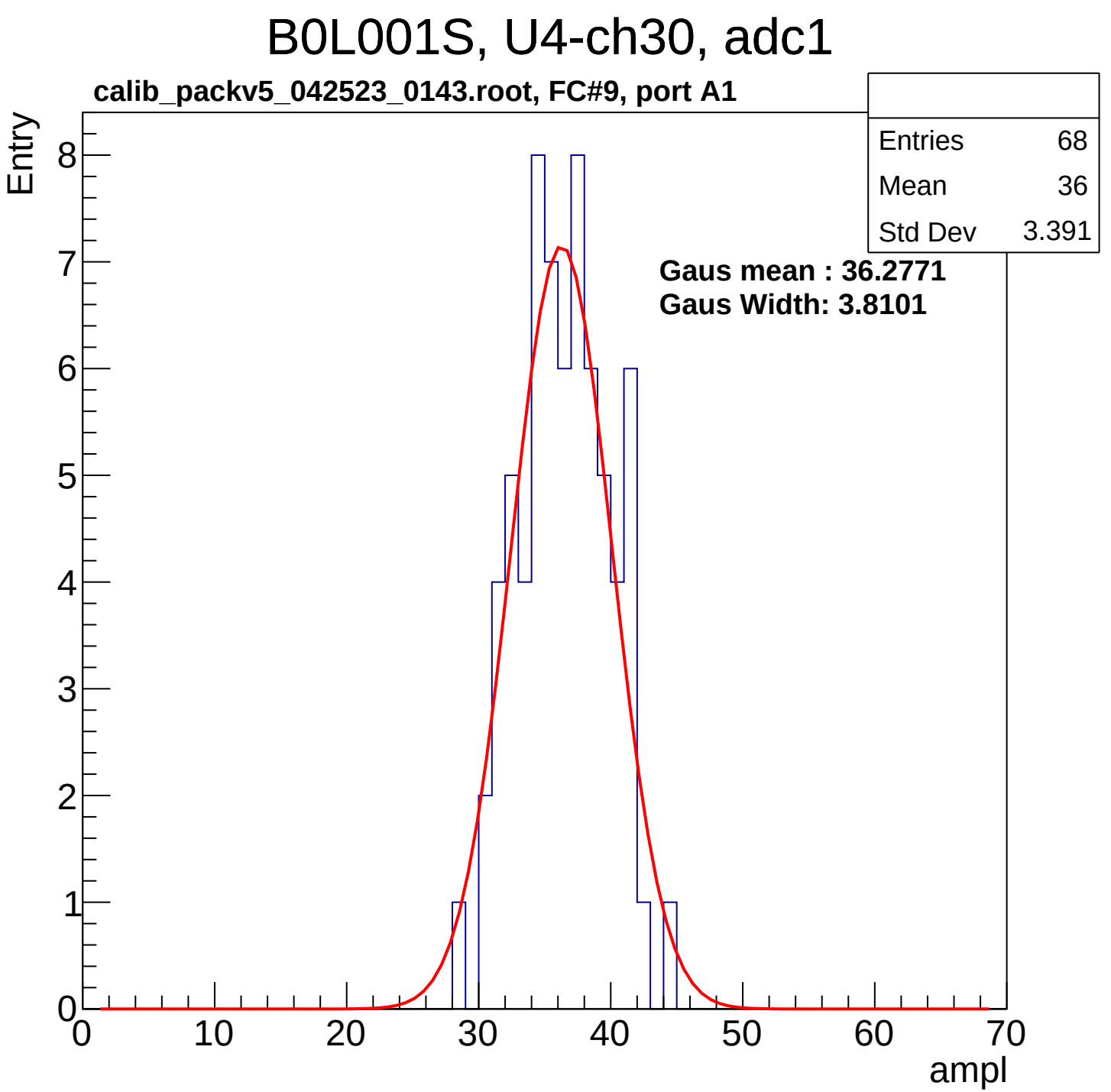
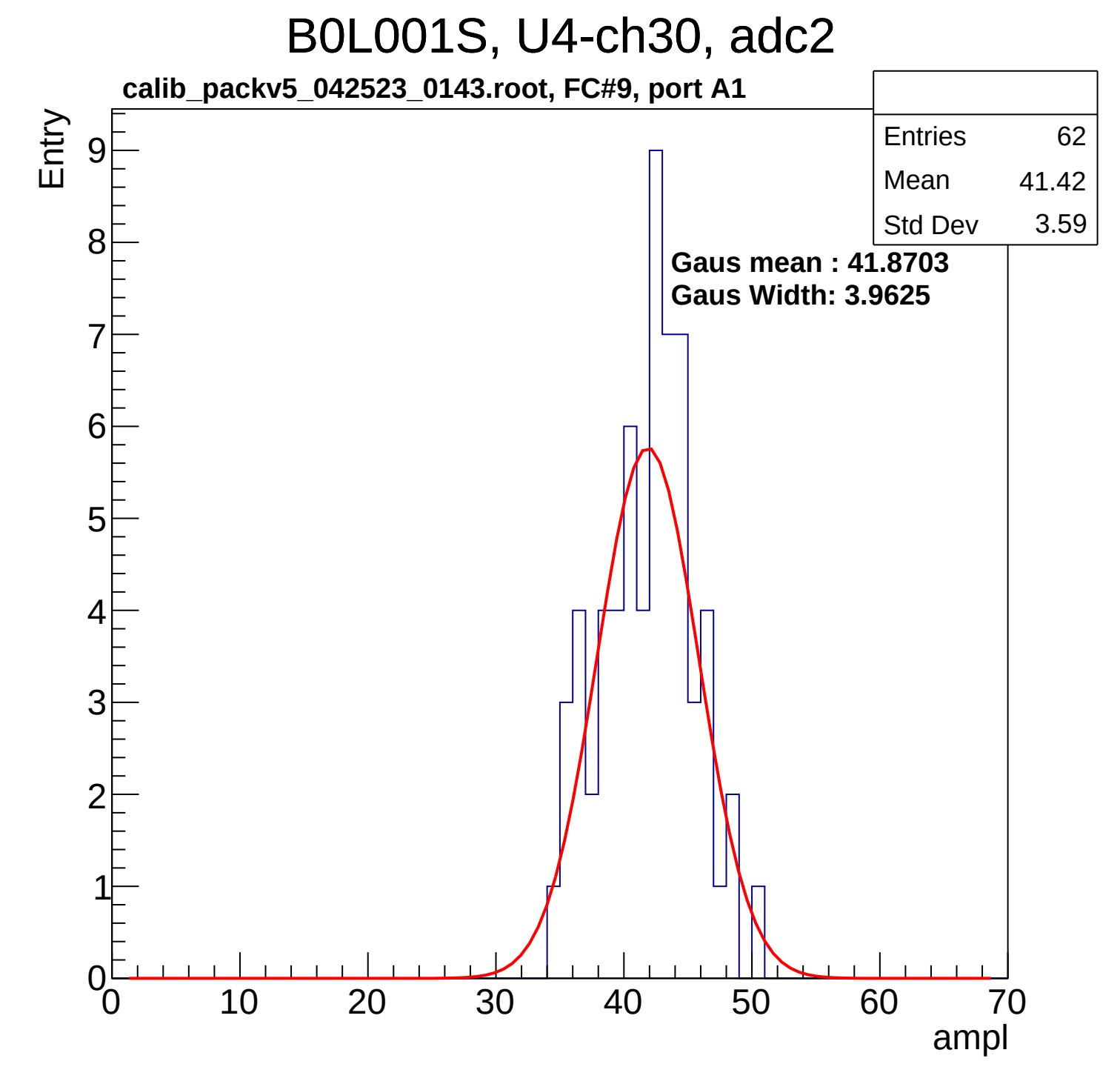

B0L001S, U4-ch30, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36
Std Dev 3.391
Gaus mean : 36.2771
Gaus Width: 3.8101
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch30, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 41.42
Std Dev 3.59
Gaus mean : 41.8703
Gaus Width: 3.9625
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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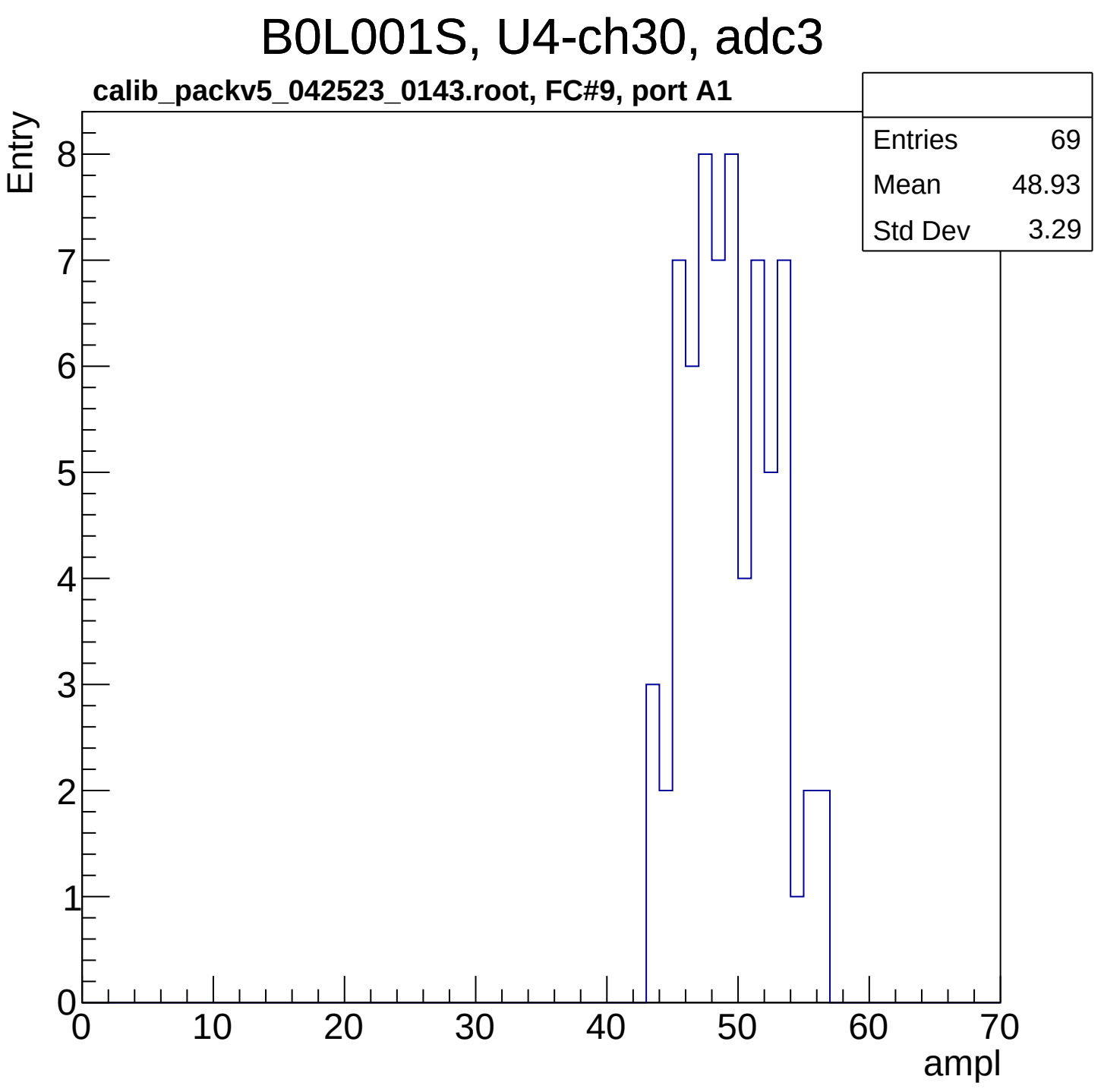
B0L001S, U4-ch30, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 48.93
Std Dev 3.29
Entry
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2
3
4
5
6
7
8
0
10
20
30
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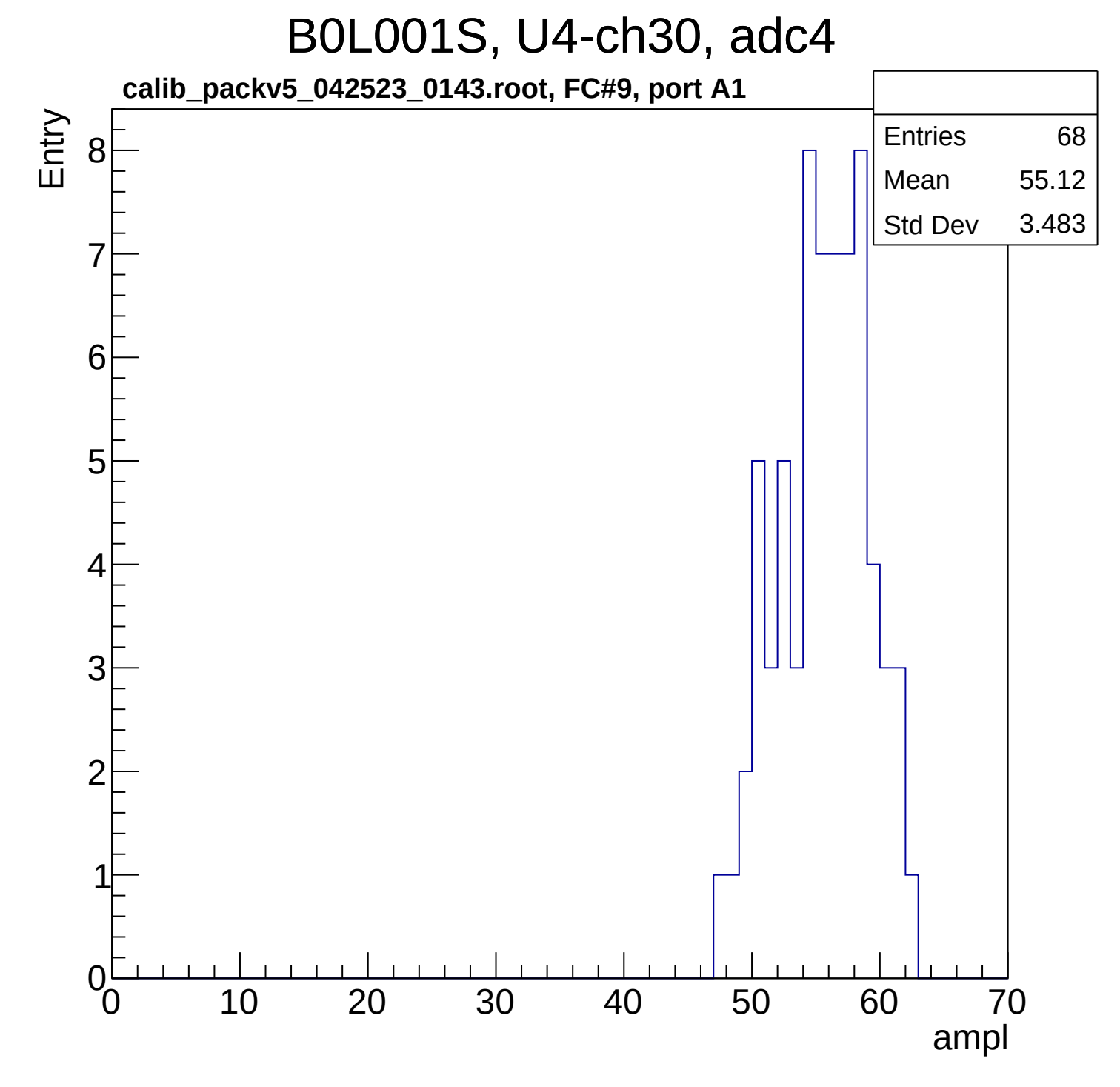
50
60
70
ampl


B0L001S, U4-ch30, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 55.12
Std Dev 3.483
Entry
ampl
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2
3
4
5
6
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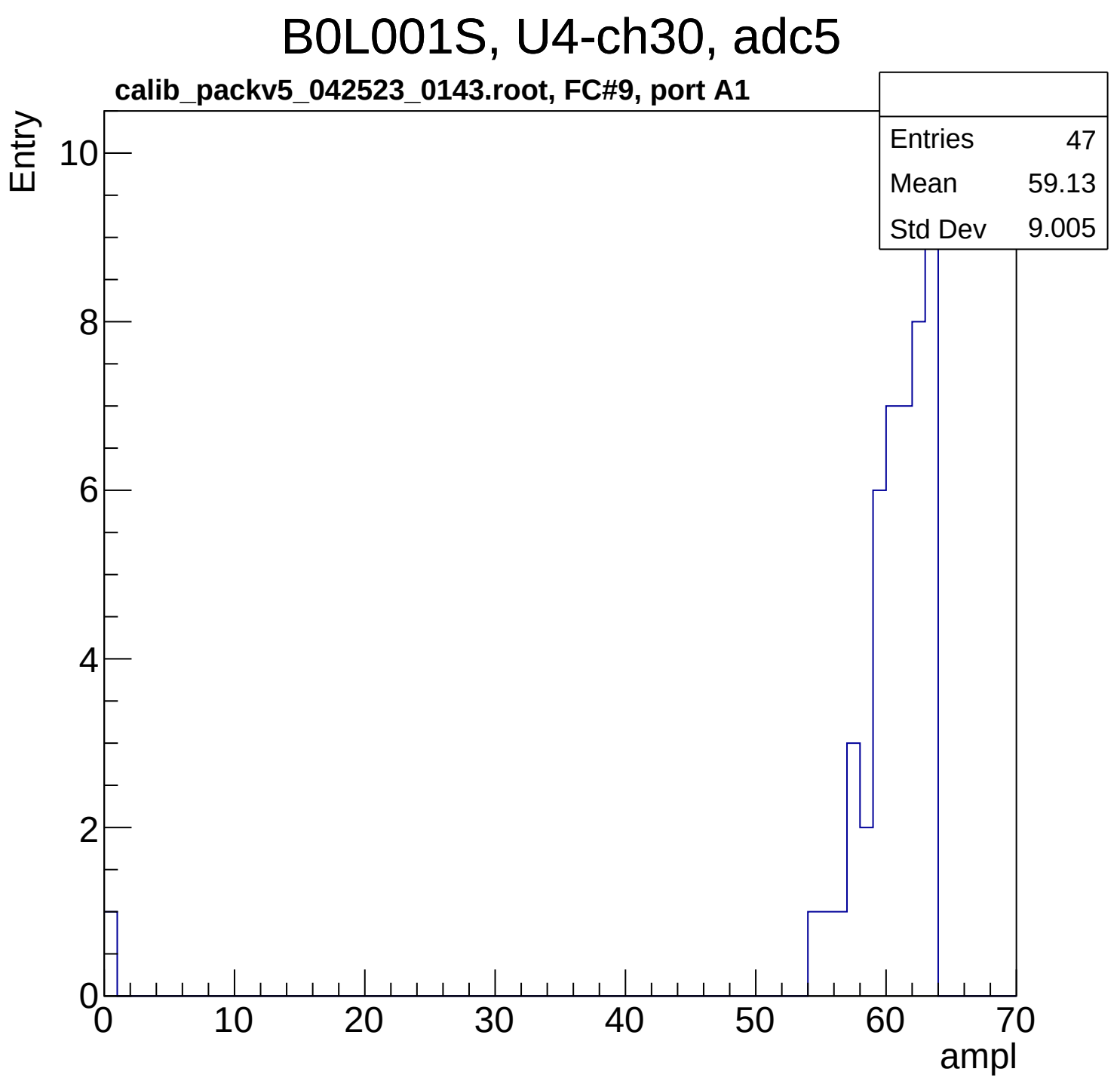
B0L001S, U4-ch30, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 59.13
Std Dev 9.005
Entry
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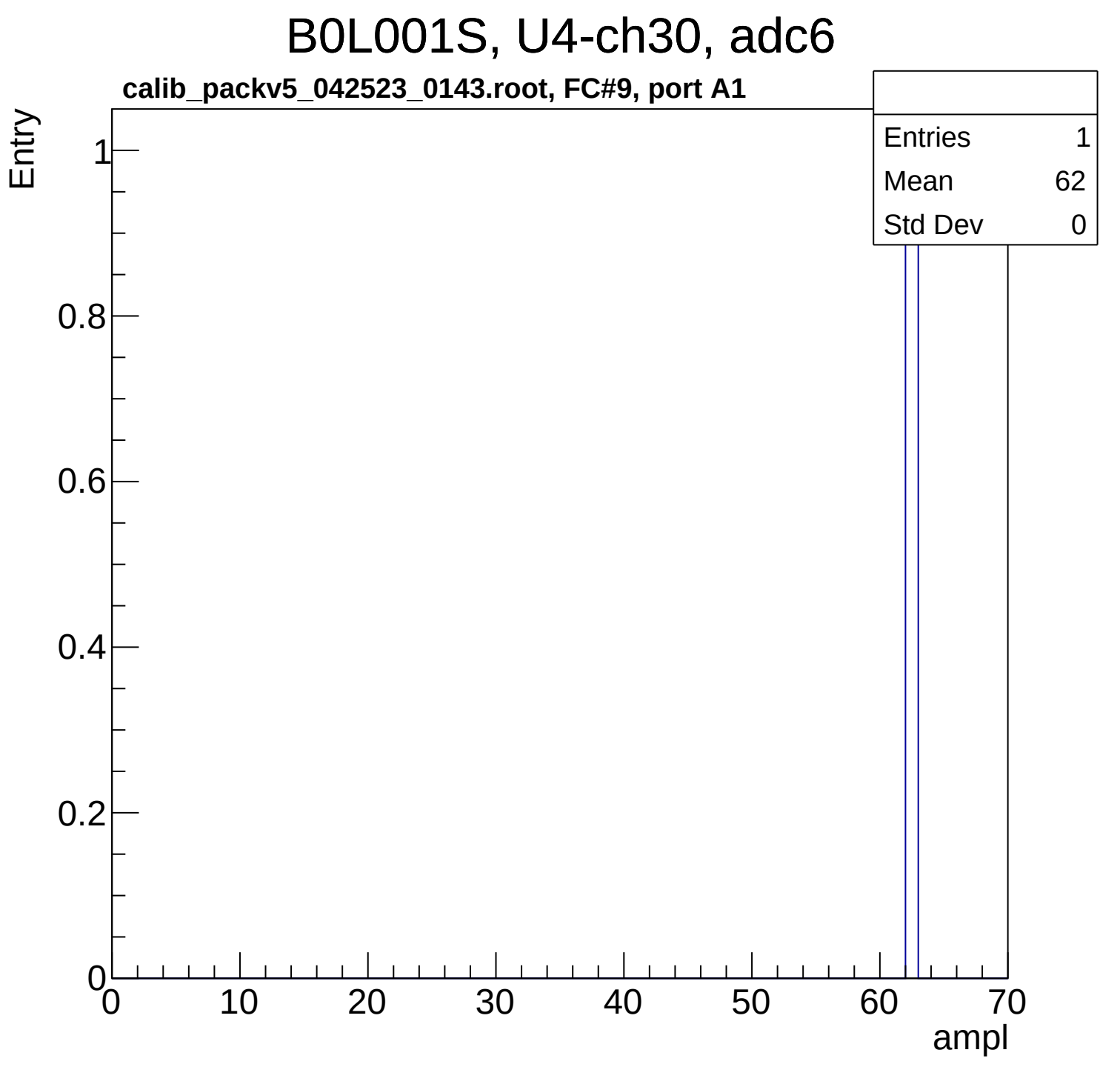
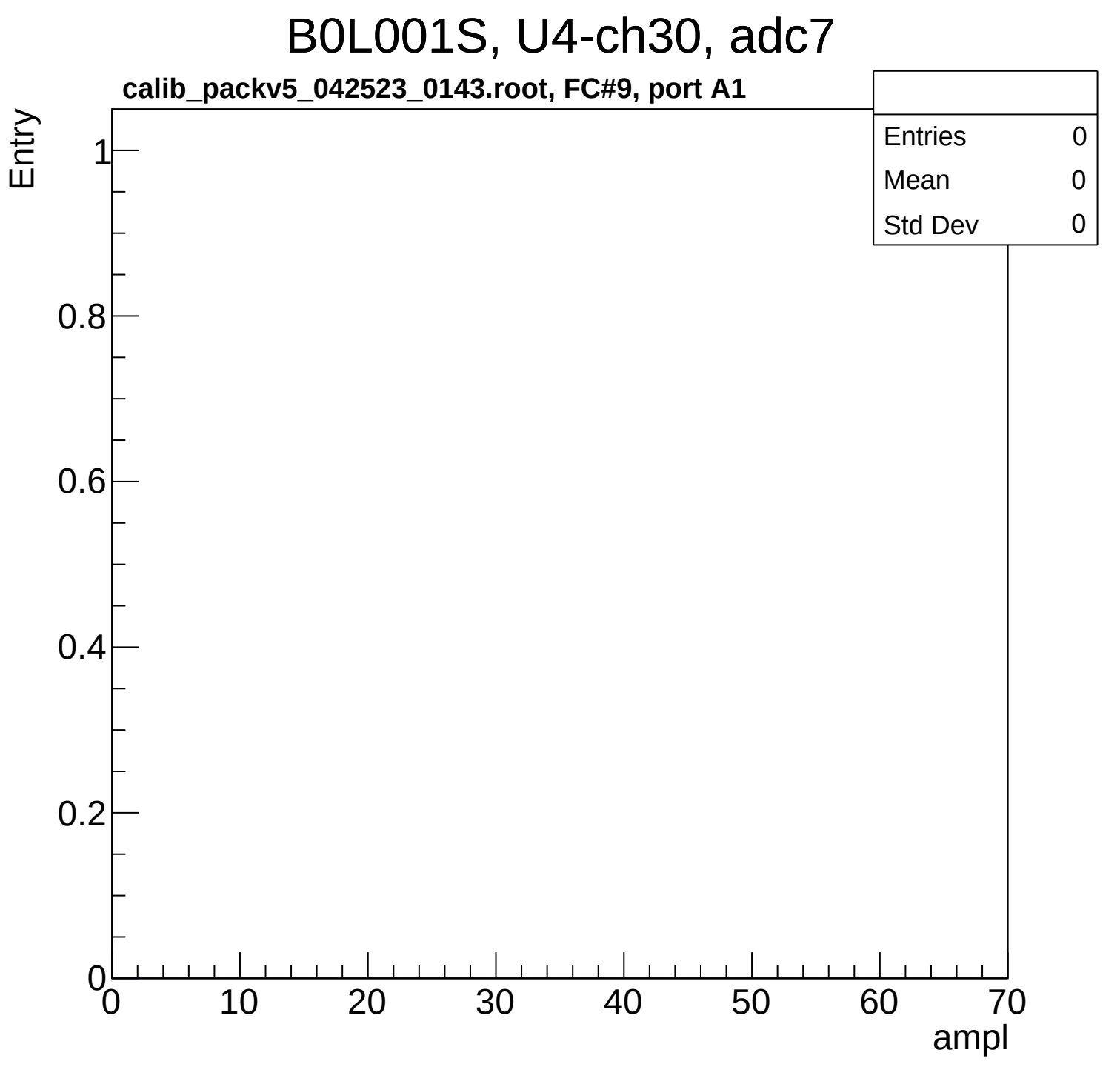
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch30, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch30, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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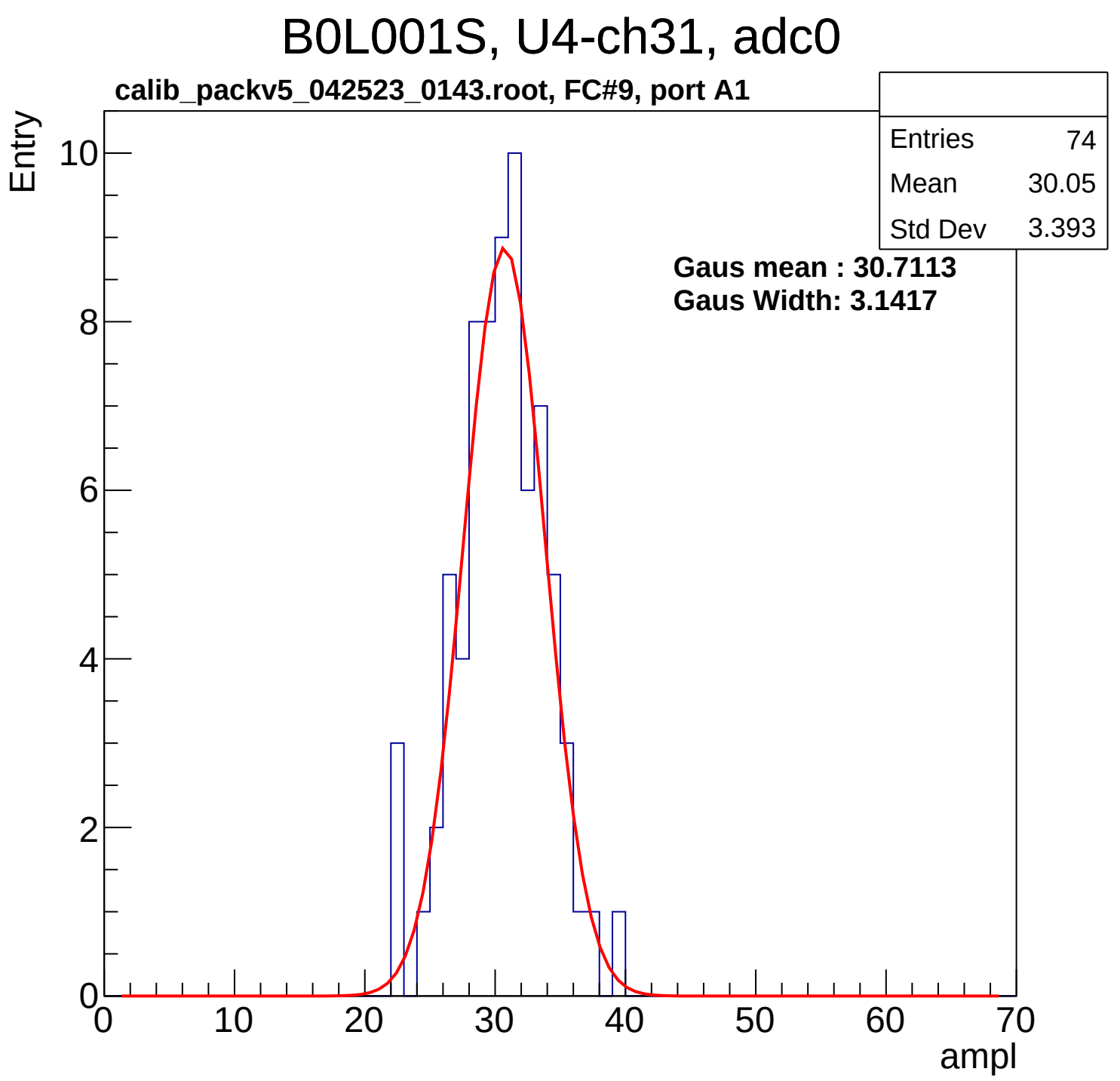
50
60
70
ampl


B0L001S, U4-ch31, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.05
Std Dev 3.393
Gaus mean : 30.7113
Gaus Width: 3.1417
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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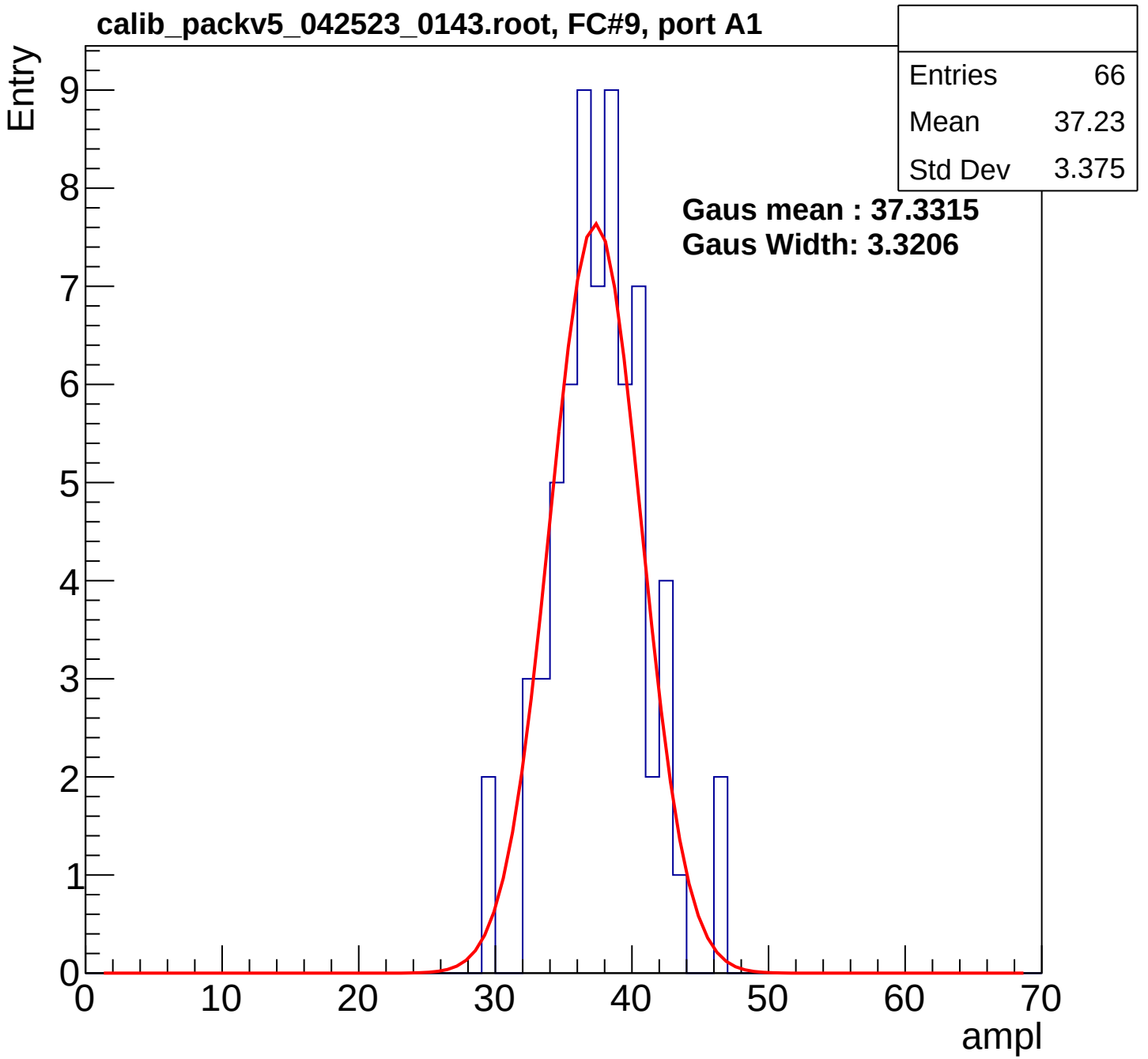
B0L001S, U4-ch31, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.23
Std Dev 3.375
Gaus mean : 37.3315
Gaus Width: 3.3206
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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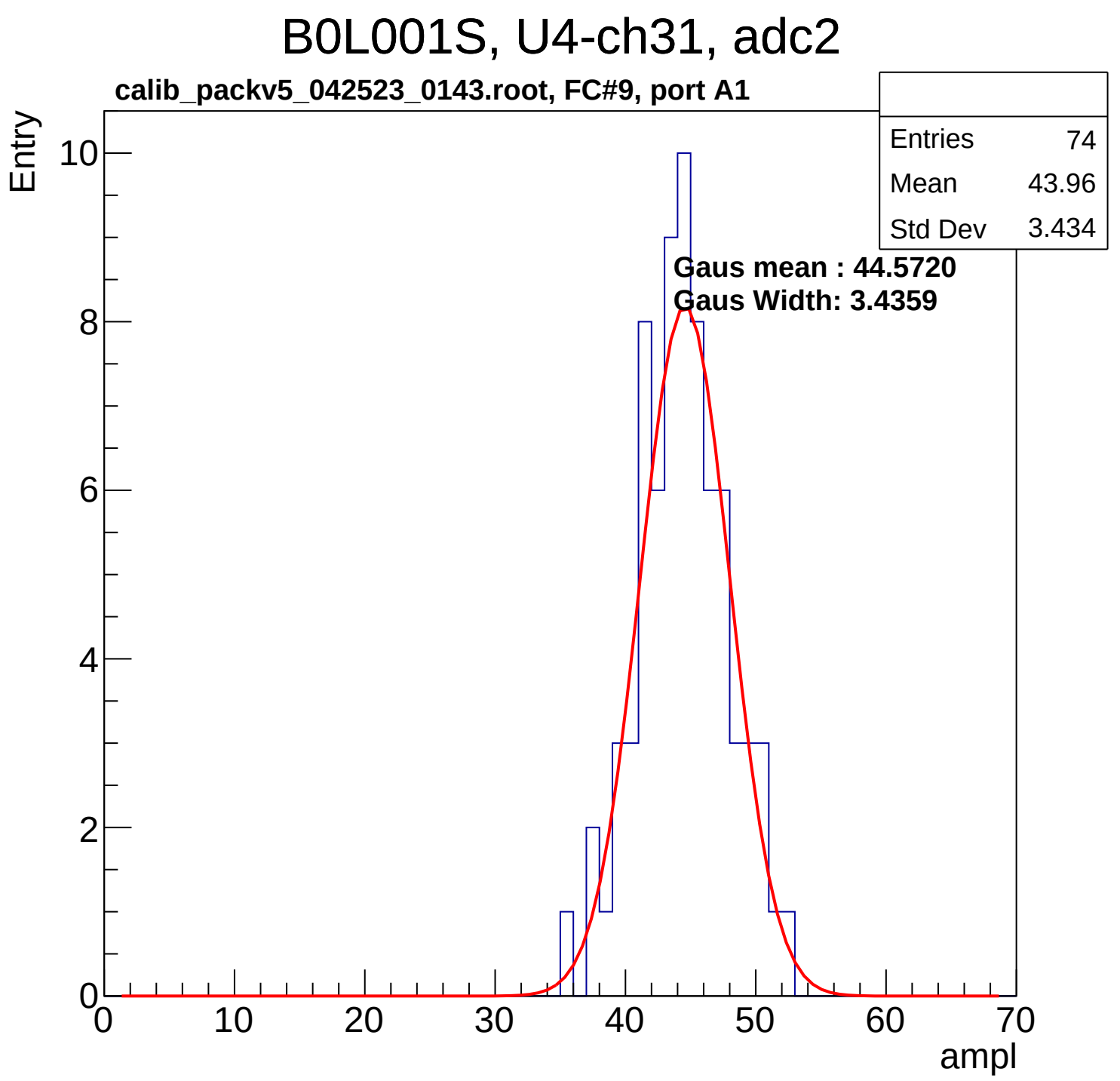
B0L001S, U4-ch31, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.96
Std Dev 3.434
Gaus mean : 44.5720
Gaus Width: 3.4359
Entry
ampl
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4
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8
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0
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60
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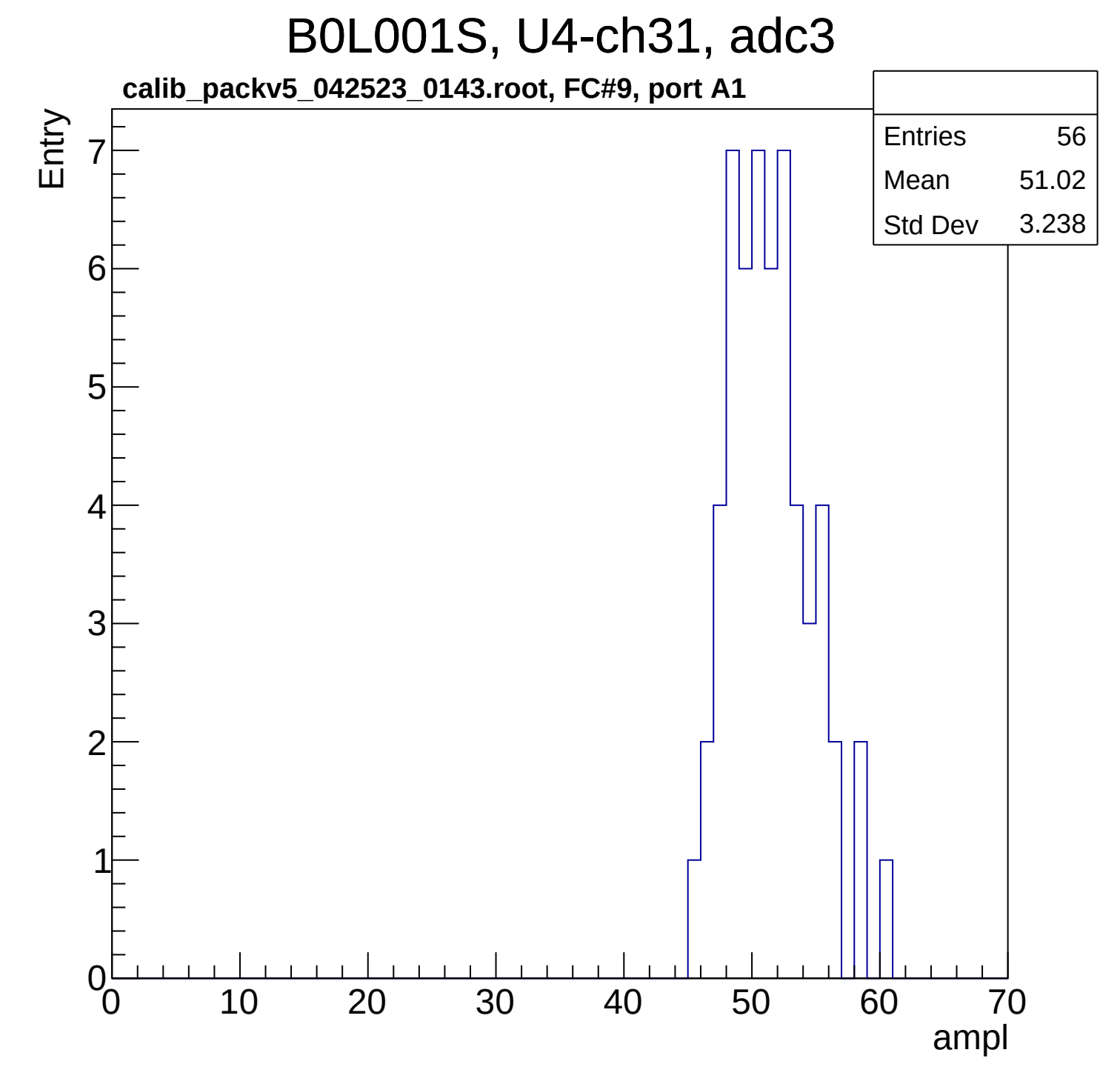
B0L001S, U4-ch31, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 51.02
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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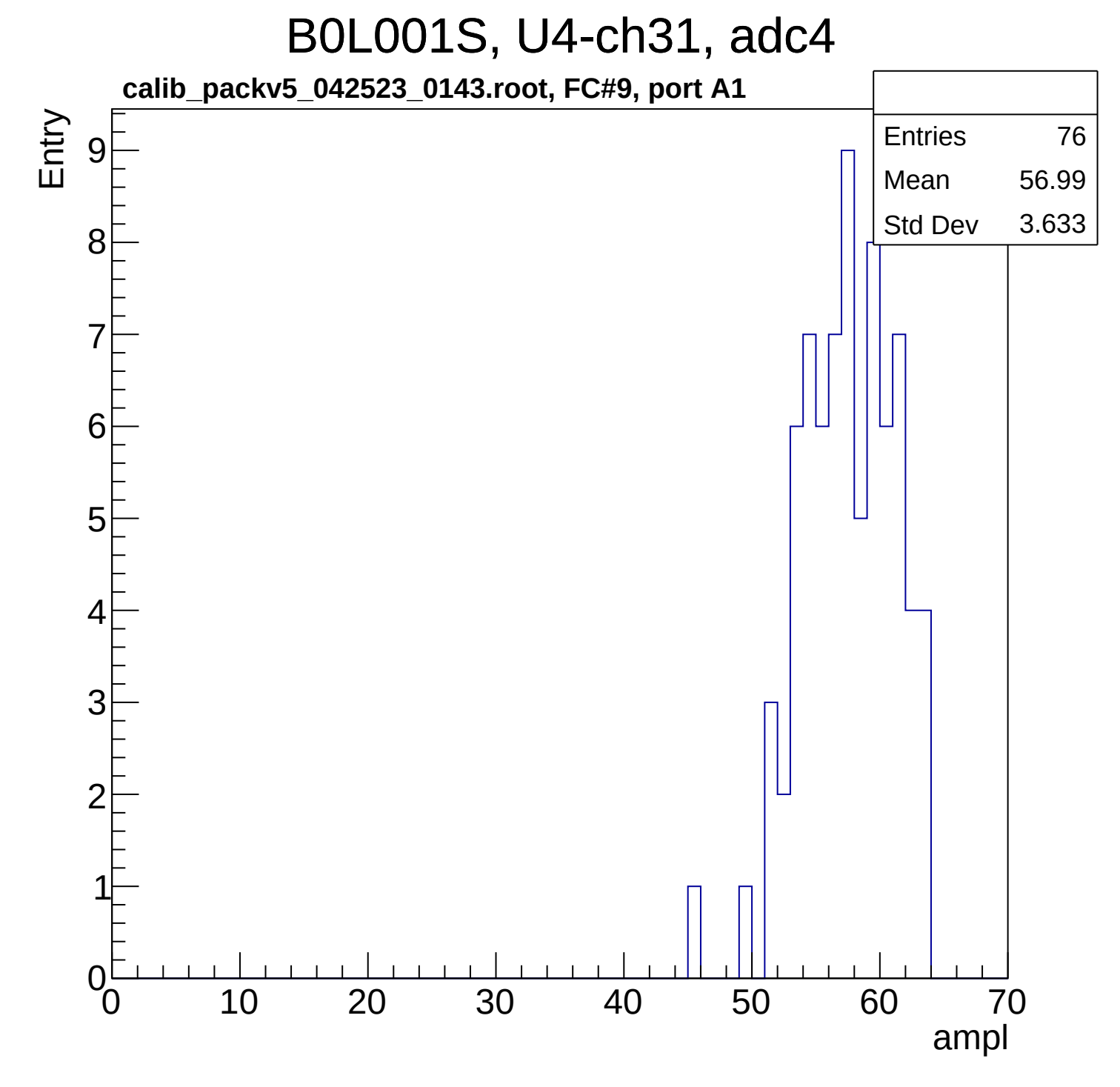
B0L001S, U4-ch31, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 56.99
Std Dev 3.633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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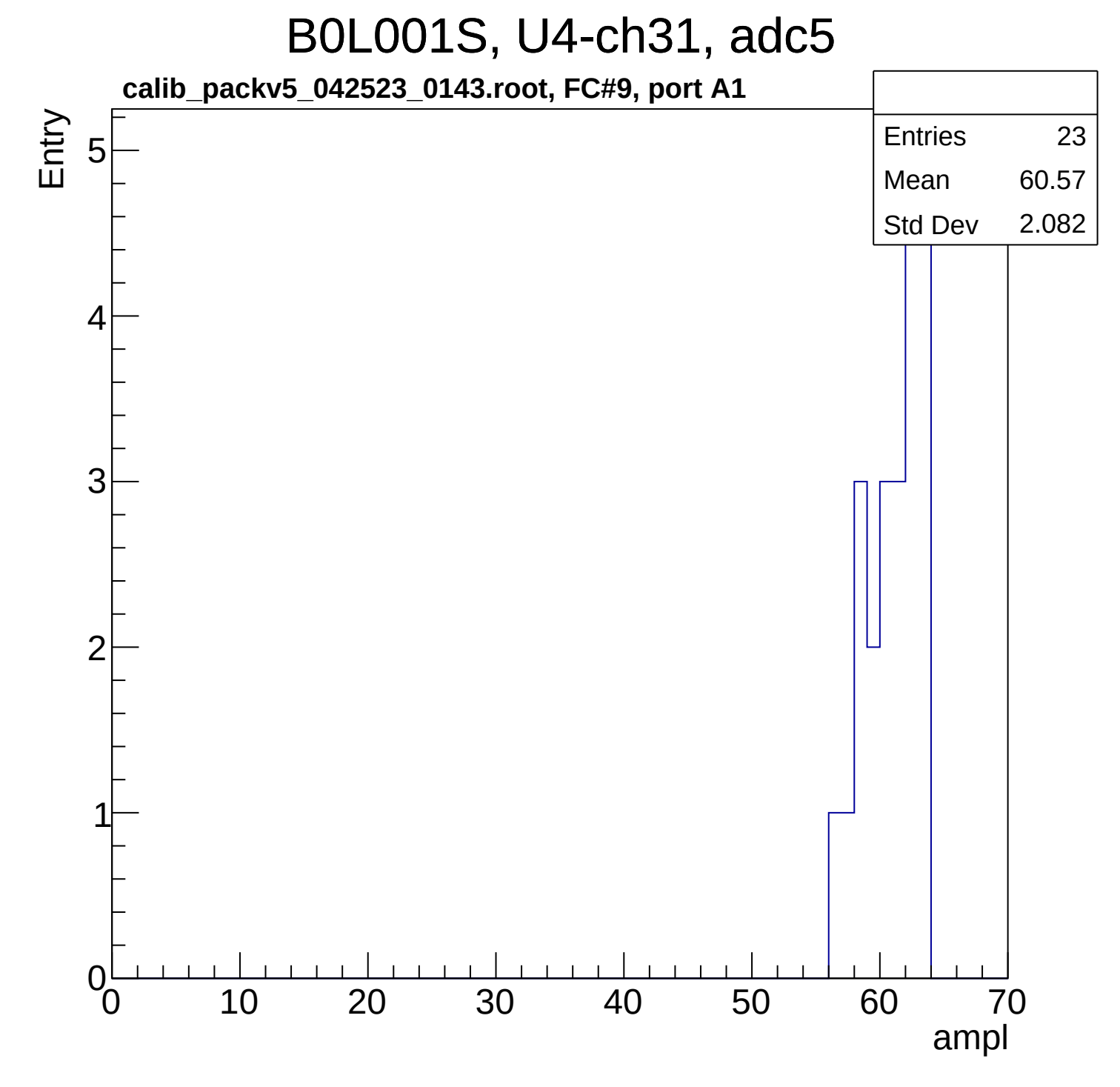
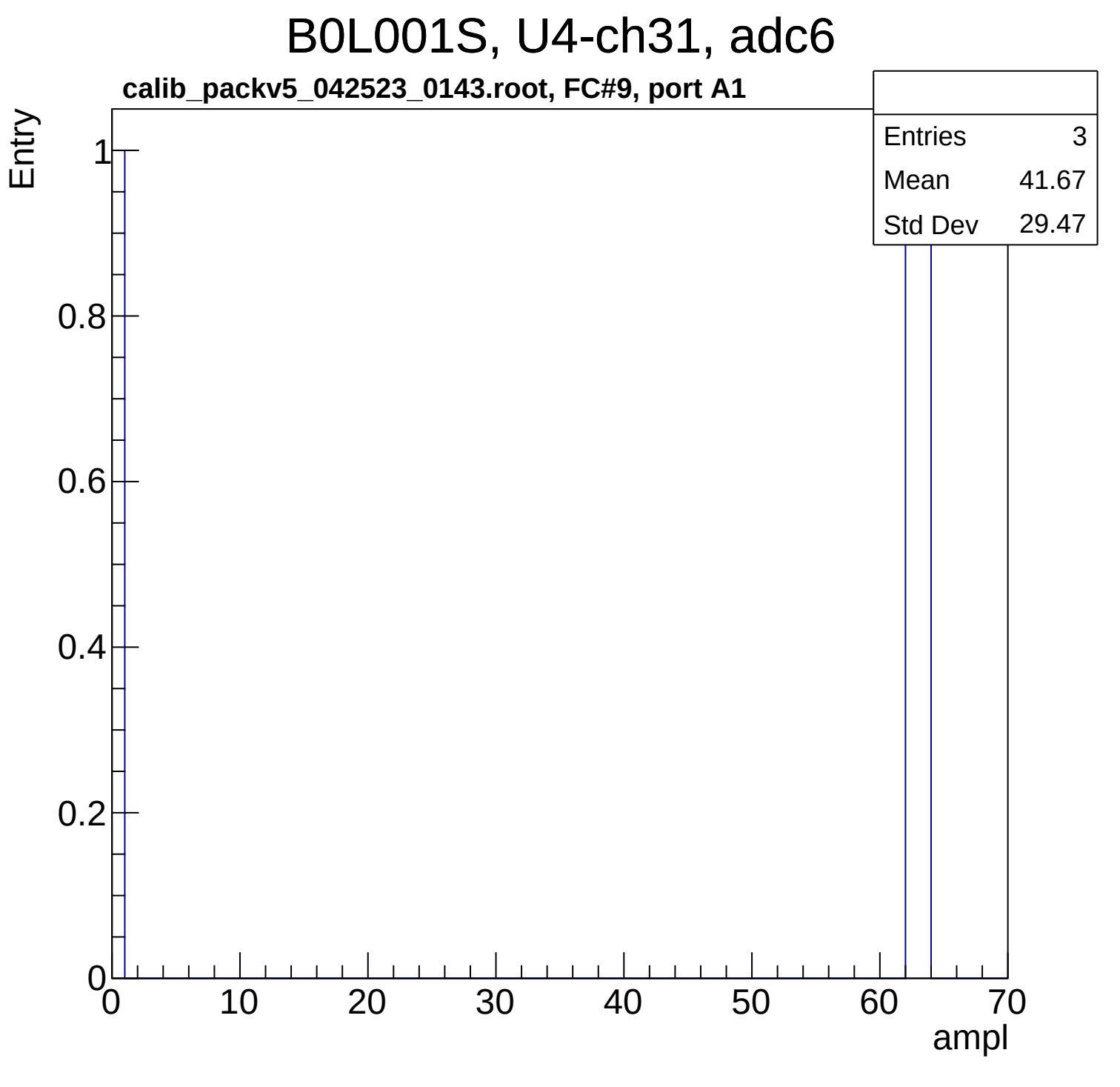

B0L001S, U4-ch31, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 60.57
Std Dev 2.082
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch31, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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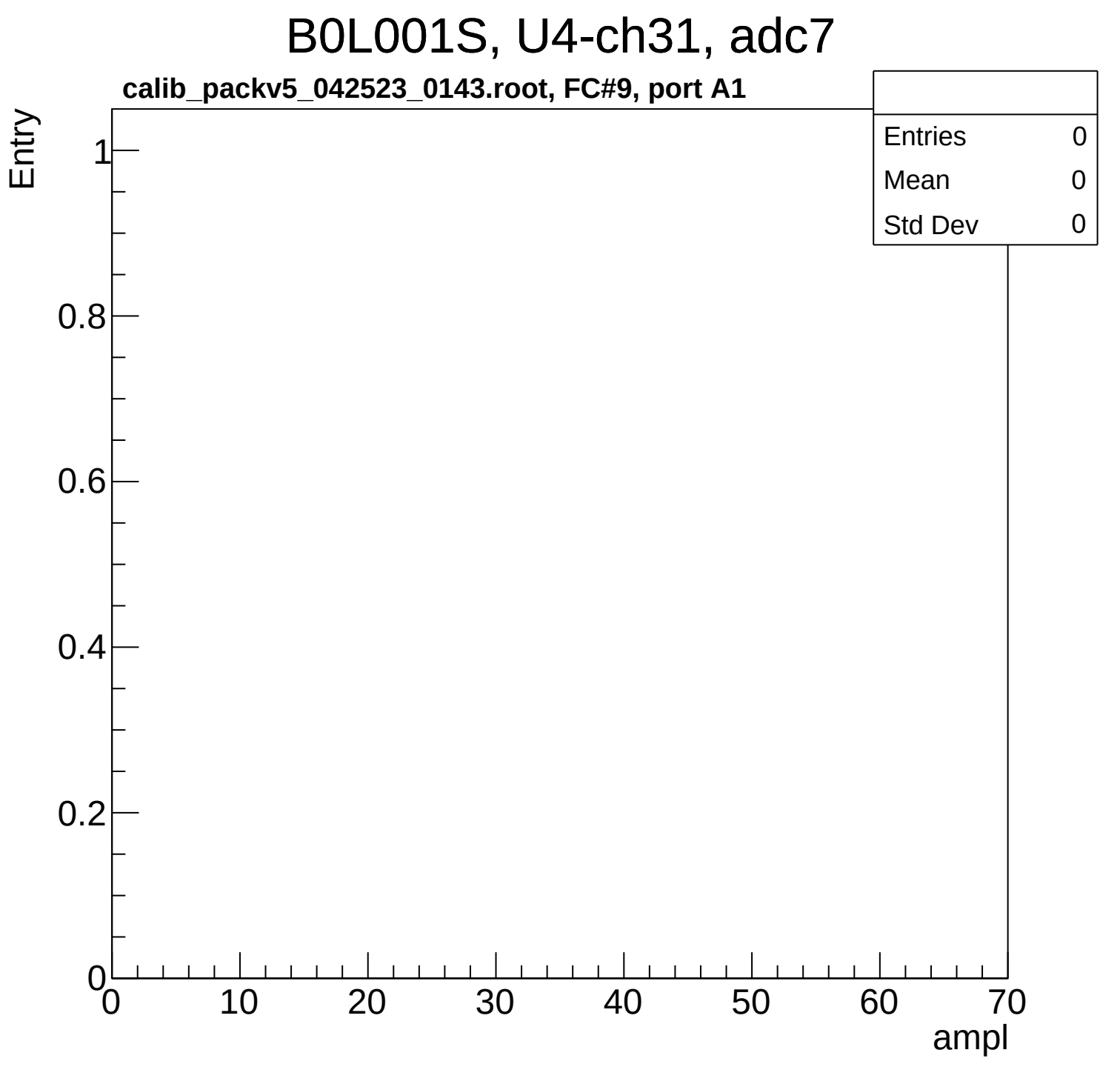
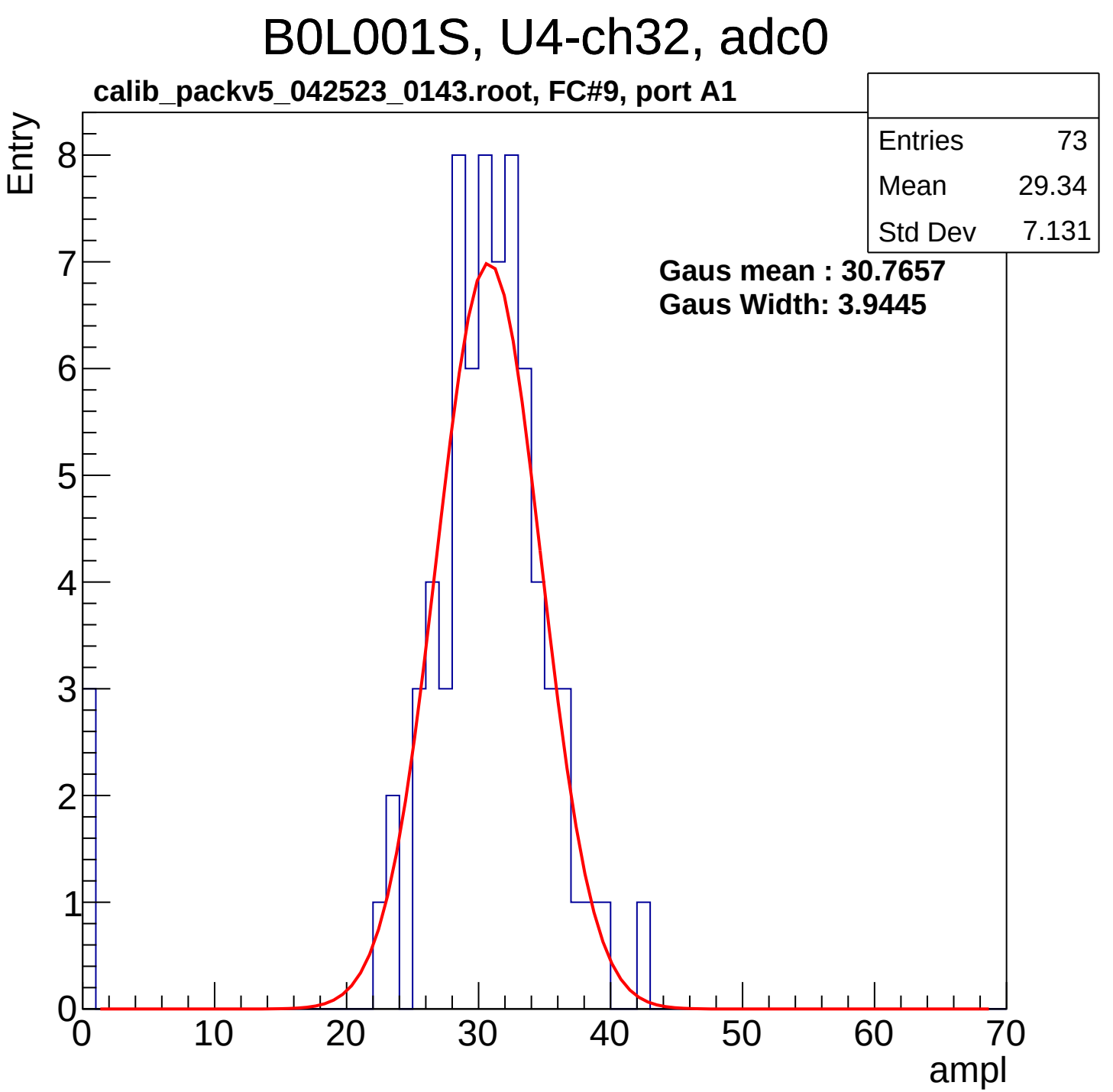
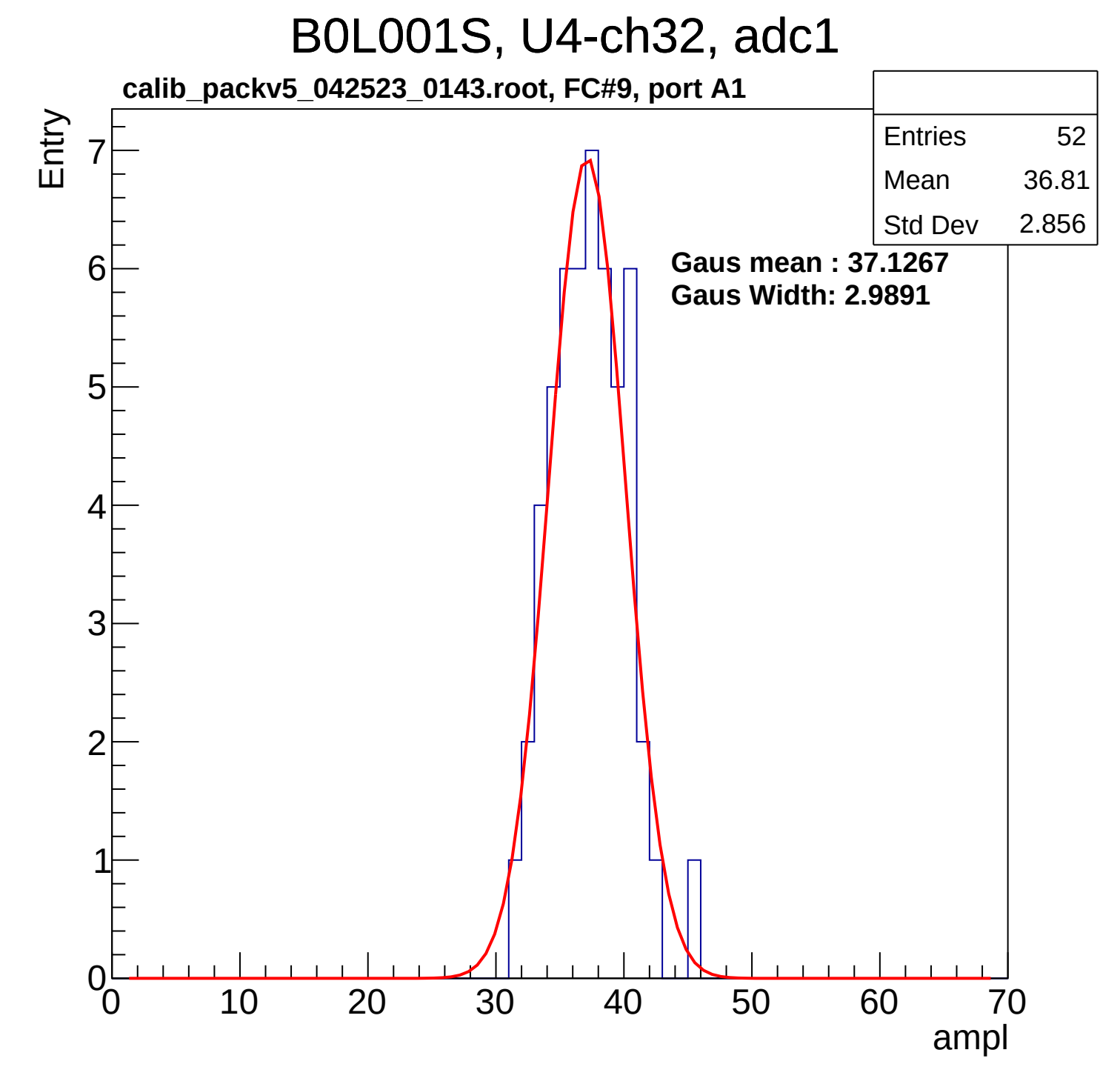
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch31, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch32, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.34
Std Dev 7.131
Gaus mean : 30.7657
Gaus Width: 3.9445
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch32, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 36.81
Std Dev 2.856
Gaus mean : 37.1267
Gaus Width: 2.9891
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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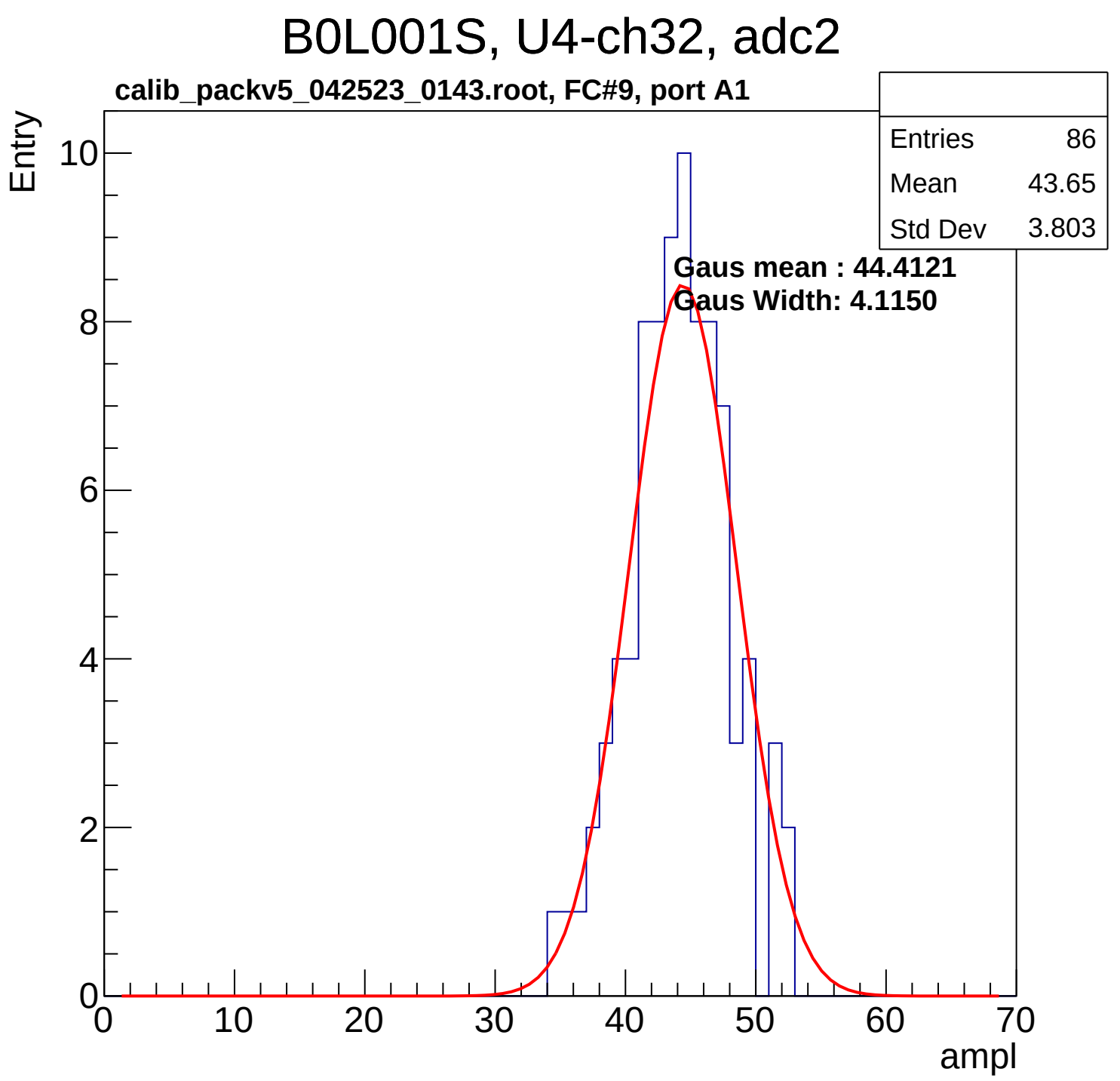
B0L001S, U4-ch32, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 43.65
Std Dev 3.803
Gaus mean : 44.4121
Gaus Width: 4.1150
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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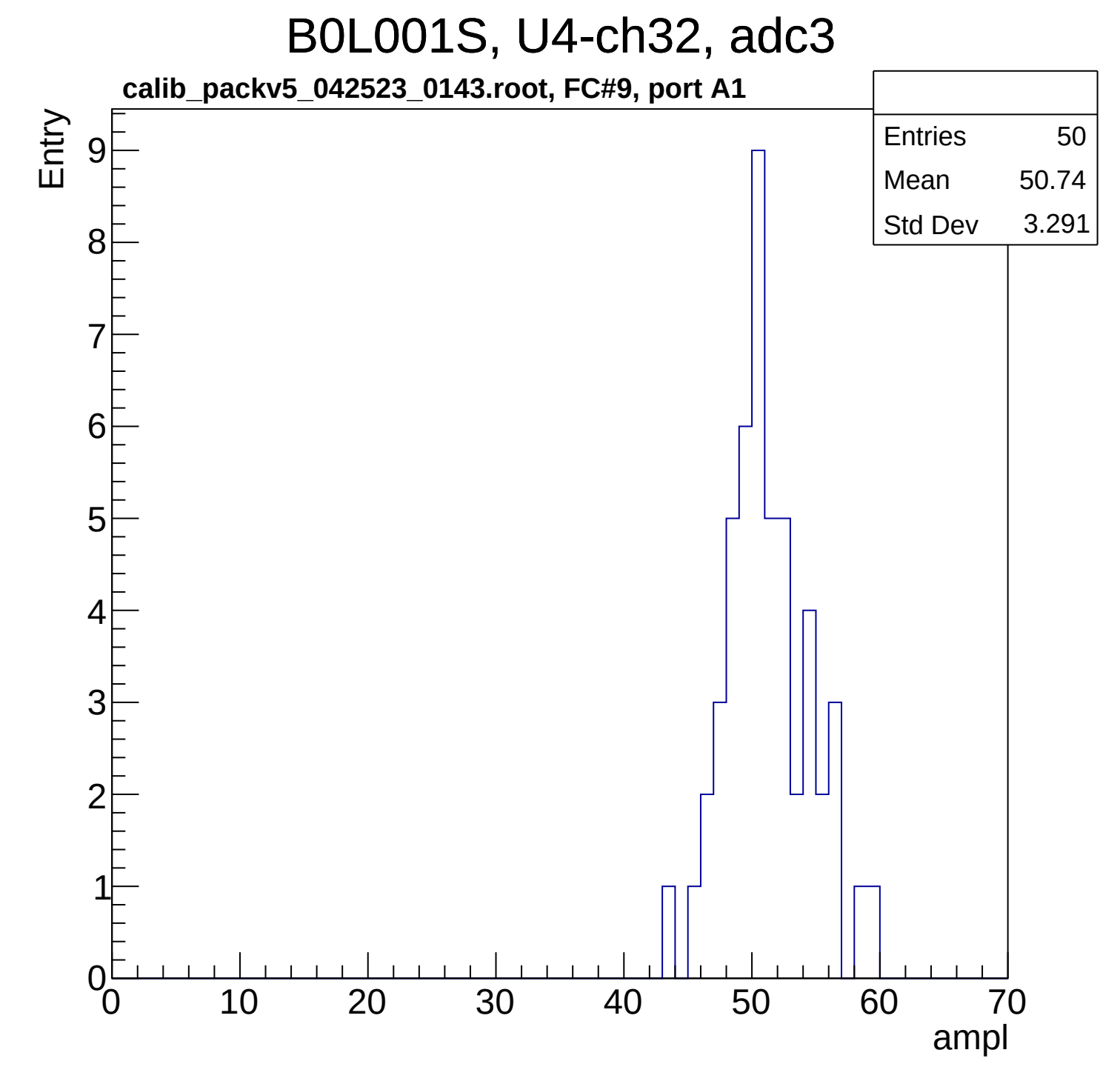
B0L001S, U4-ch32, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 50.74
Std Dev 3.291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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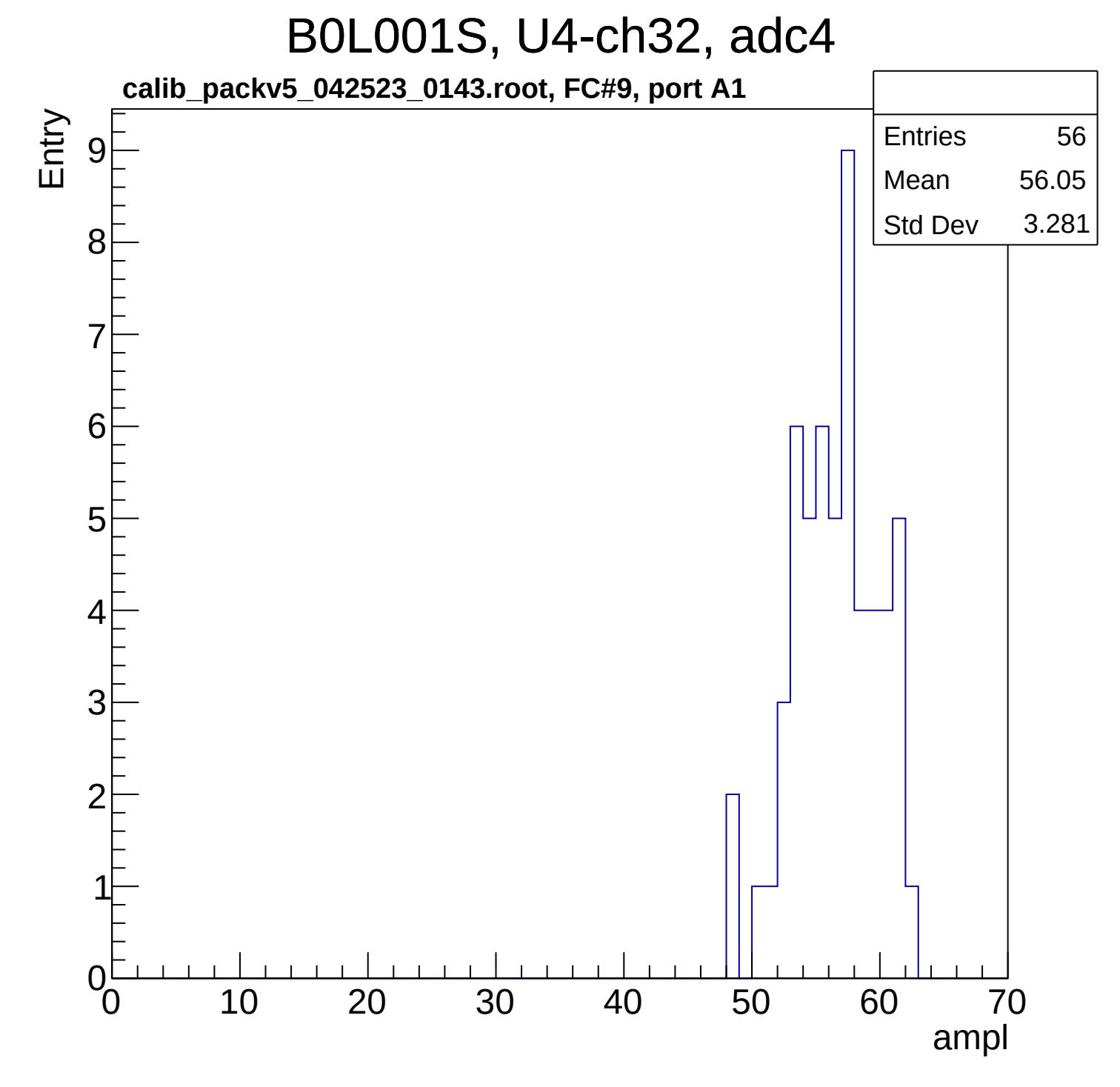
B0L001S, U4-ch32, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.05
Std Dev 3.281
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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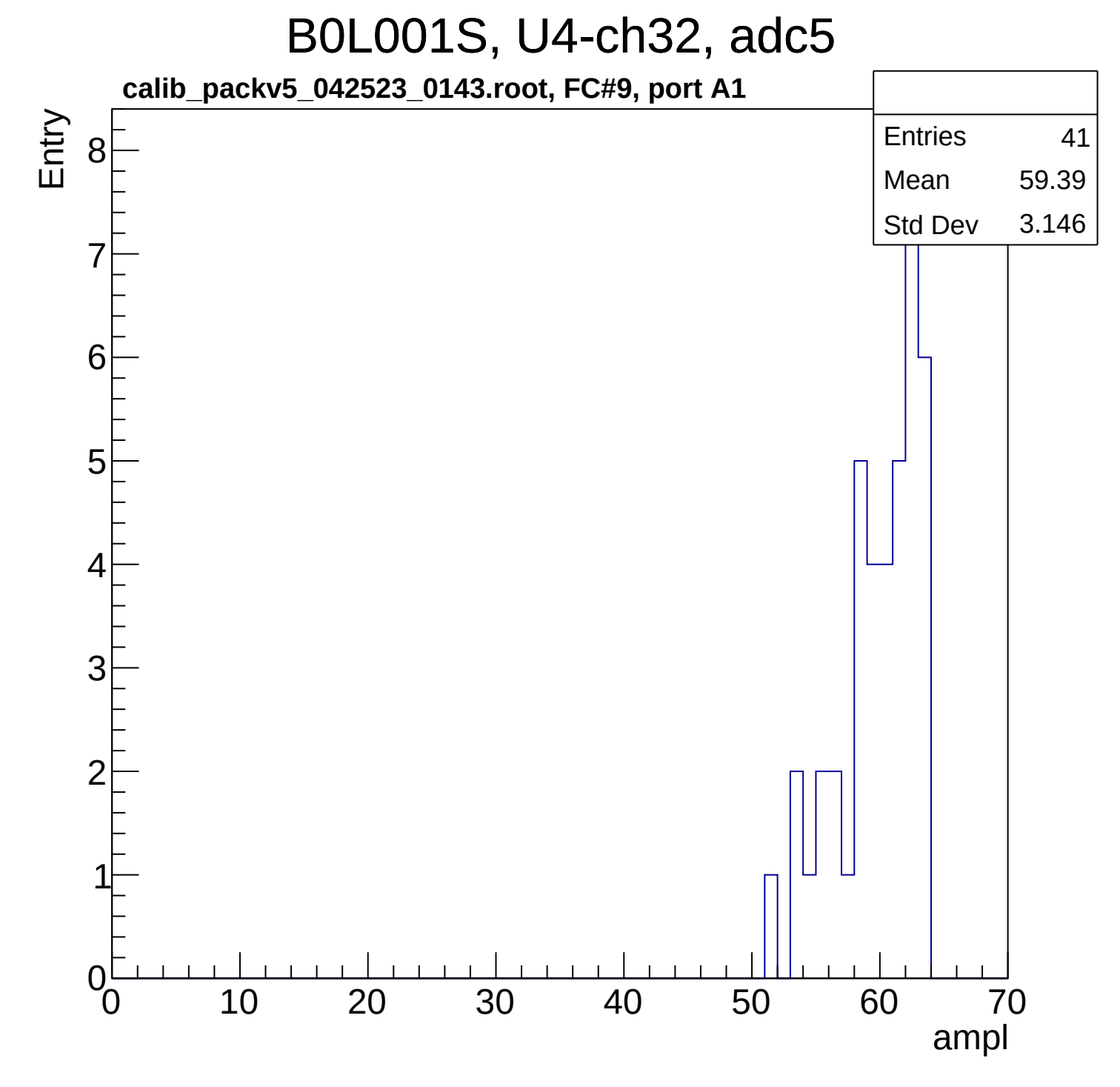
B0L001S, U4-ch32, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59.39
Std Dev 3.146
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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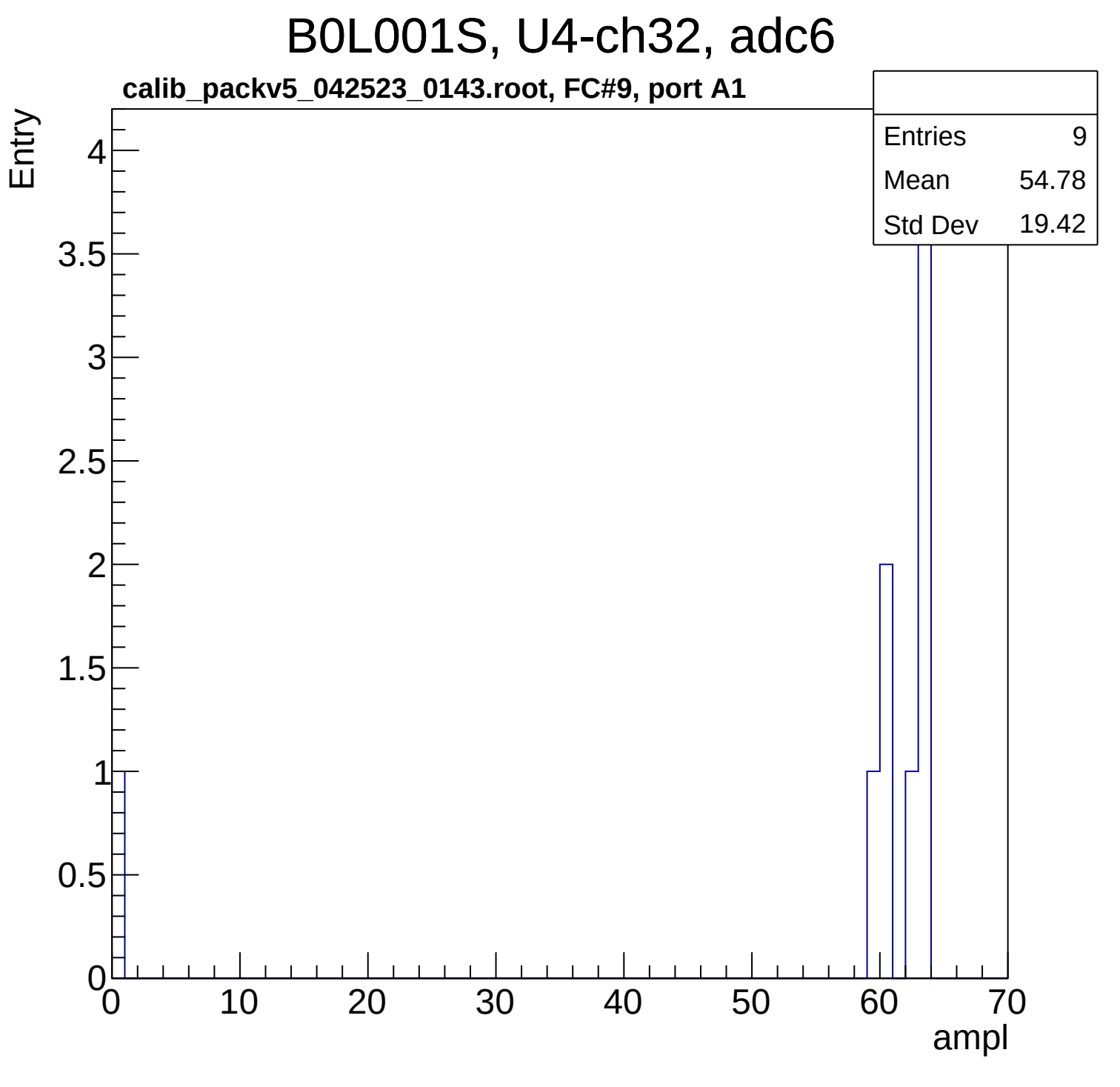
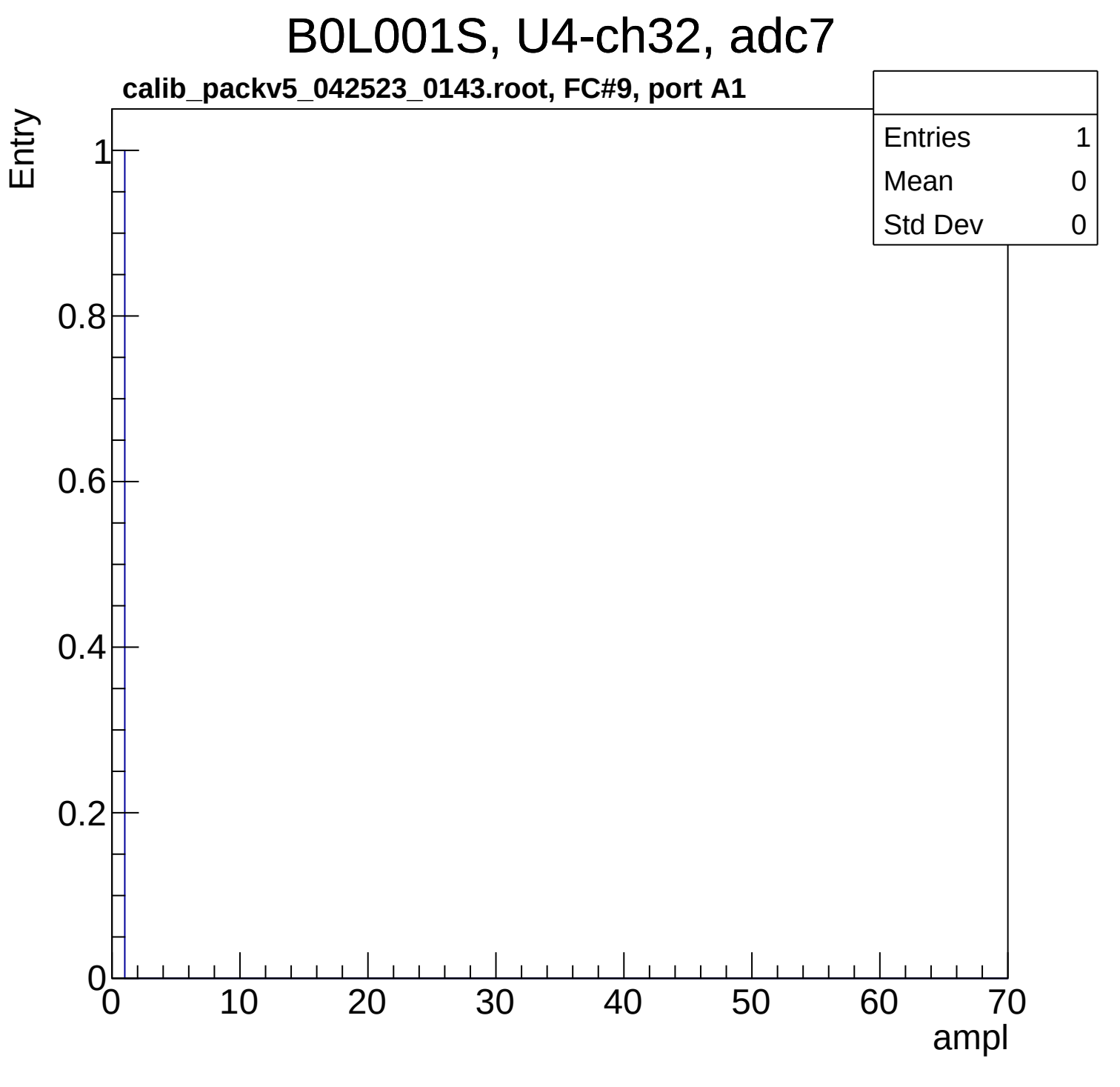
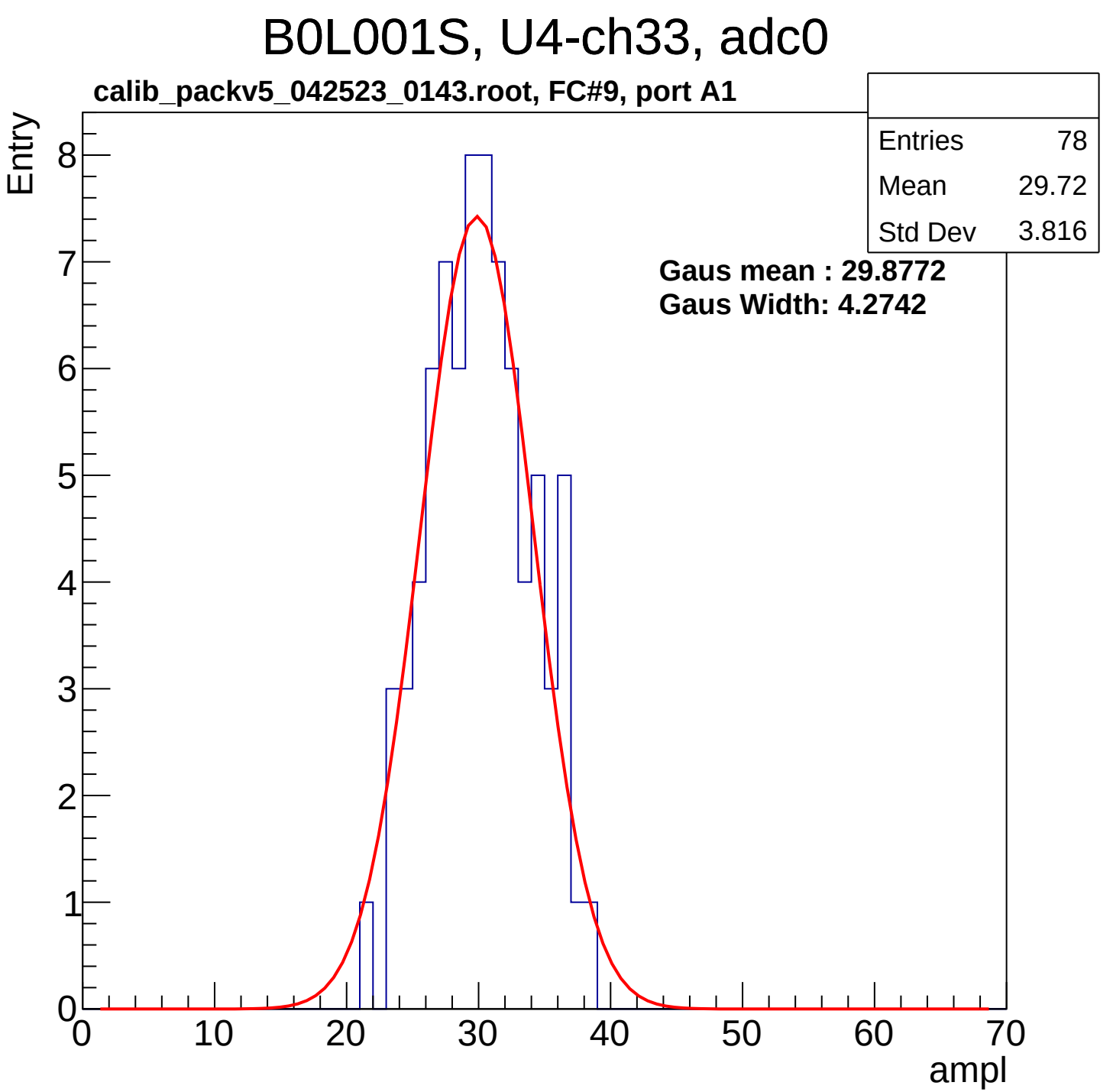

B0L001S, U4-ch32, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 54.78
Std Dev 19.42
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch32, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch33, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.72
Std Dev 3.816
Gaus mean : 29.8772
Gaus Width: 4.2742
Entry
ampl
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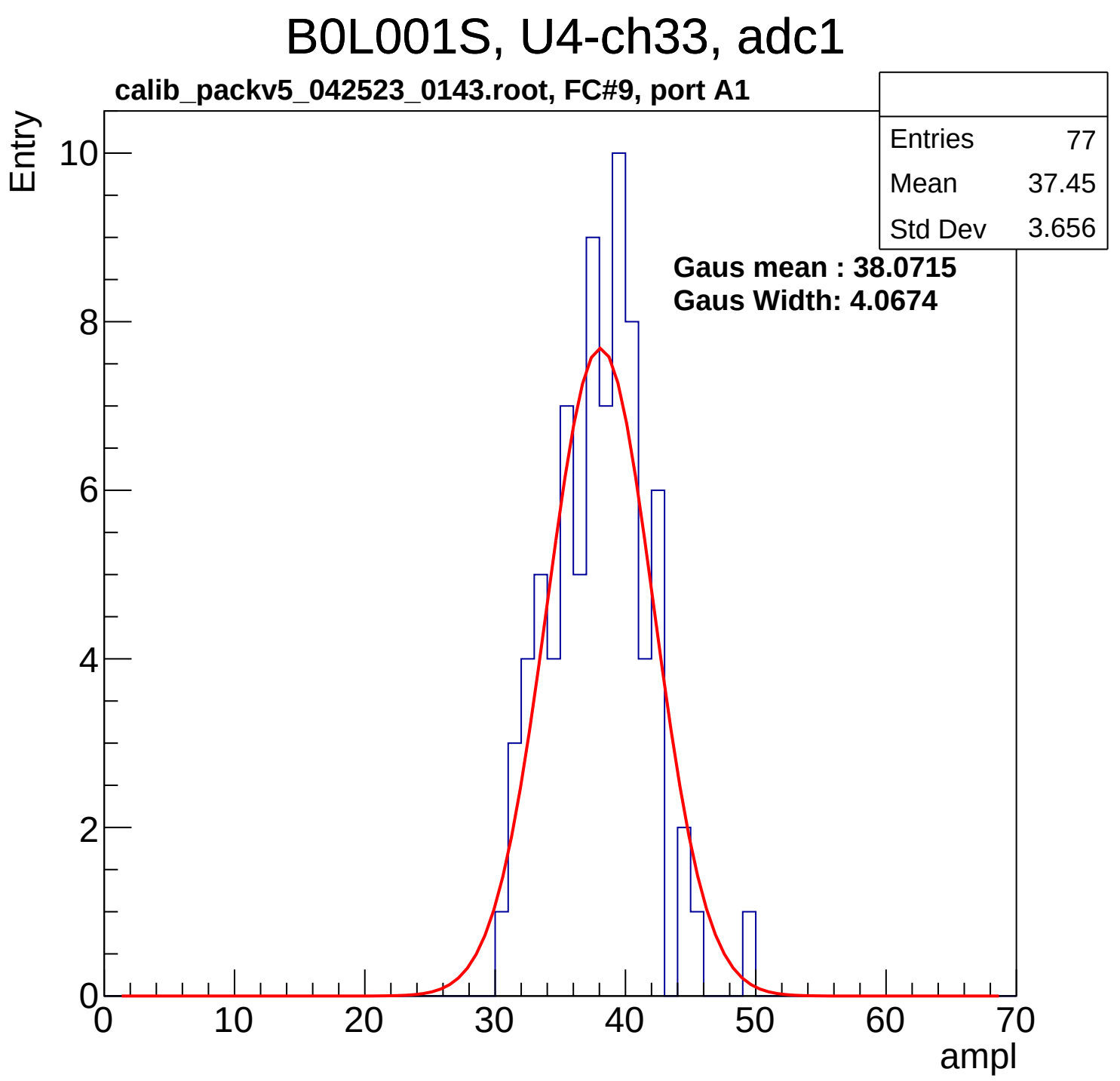
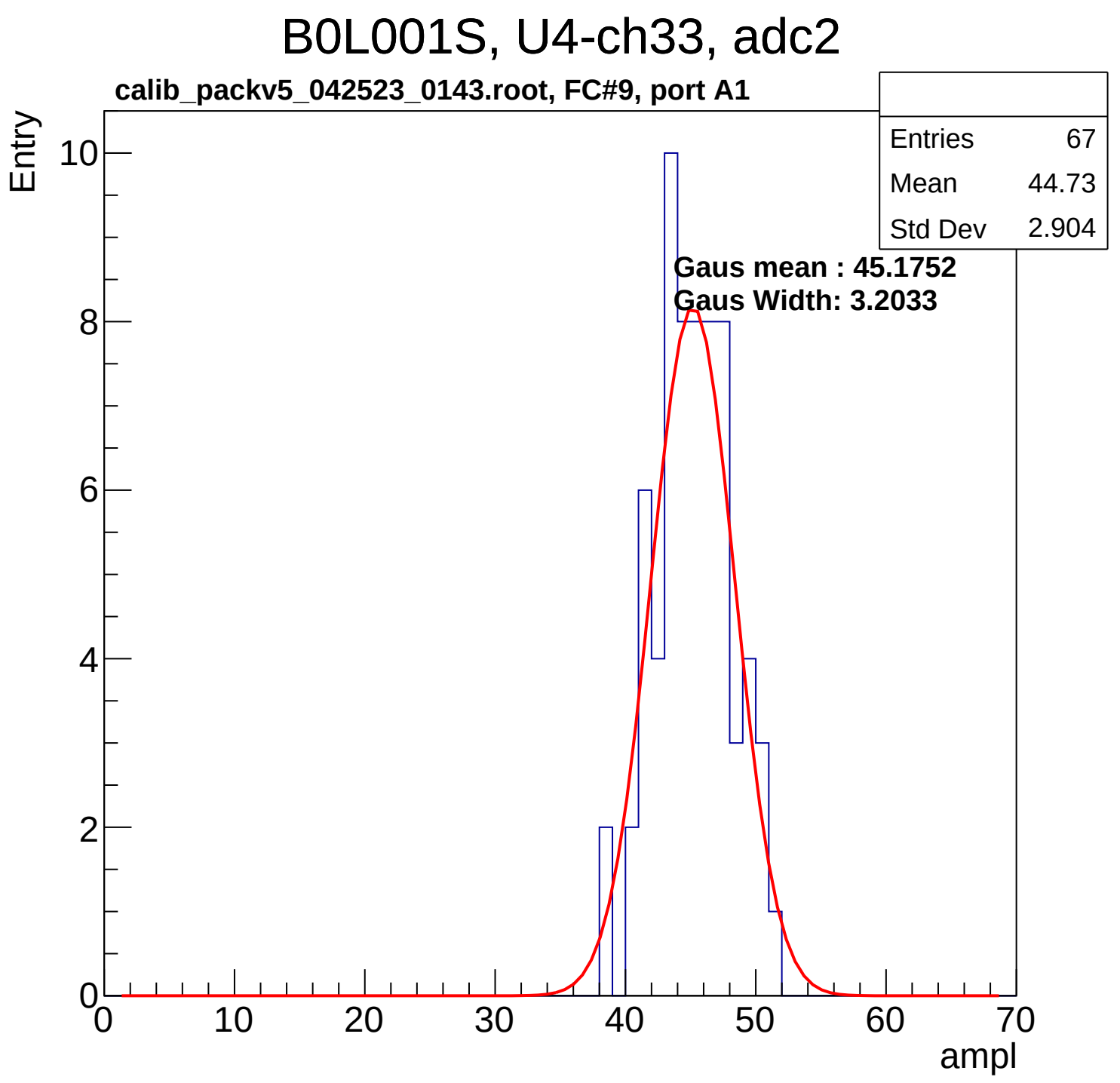
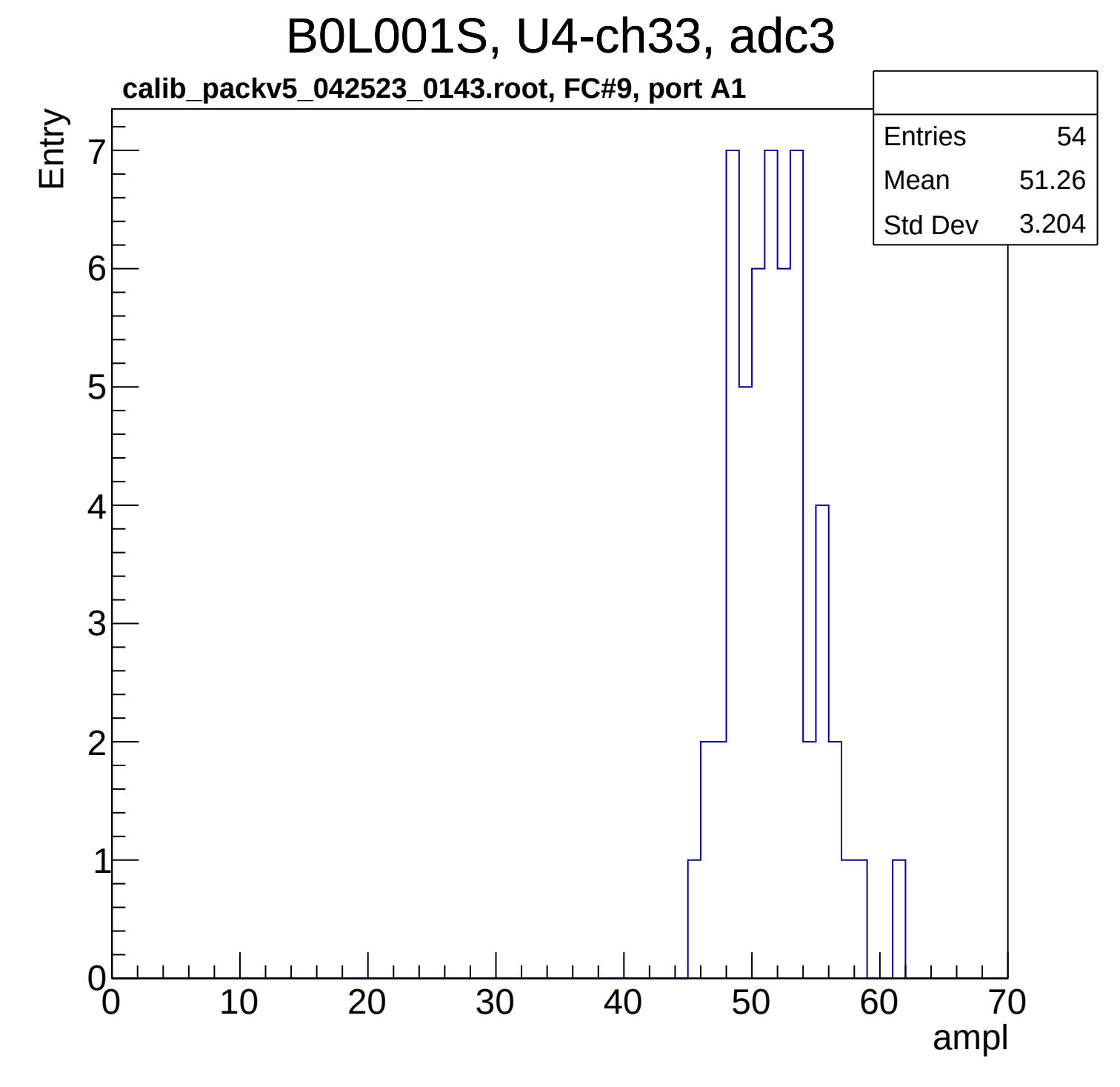
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch33, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.45
Std Dev 3.656
Gaus mean : 38.0715
Gaus Width: 4.0674
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch33, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.73
Std Dev 2.904
Gaus mean : 45.1752
Gaus Width: 3.2033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch33, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 51.26
Std Dev 3.204
Entry
ampl
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1
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3
4
5
6
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0
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60
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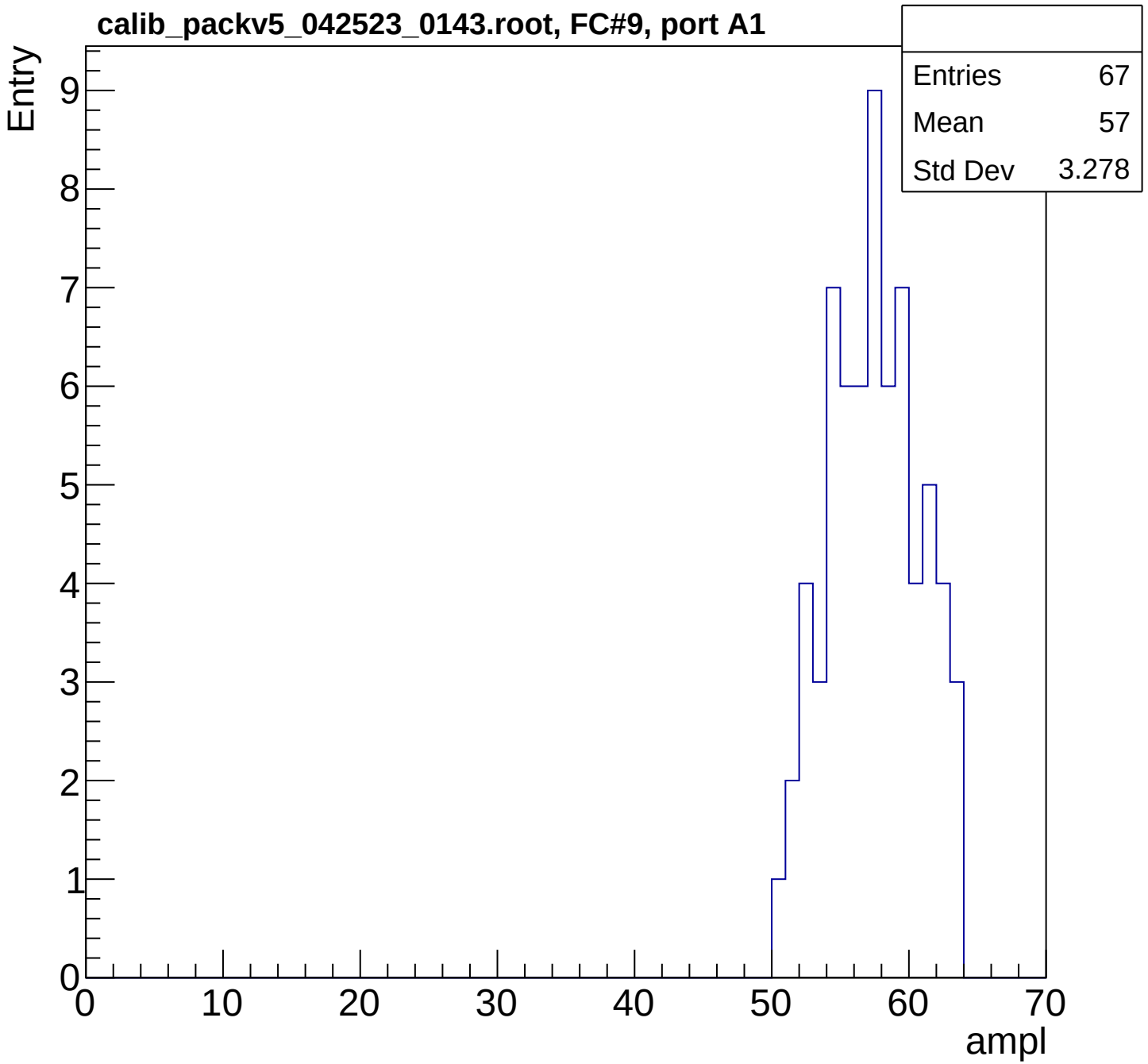
B0L001S, U4-ch33, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 57
Std Dev 3.278
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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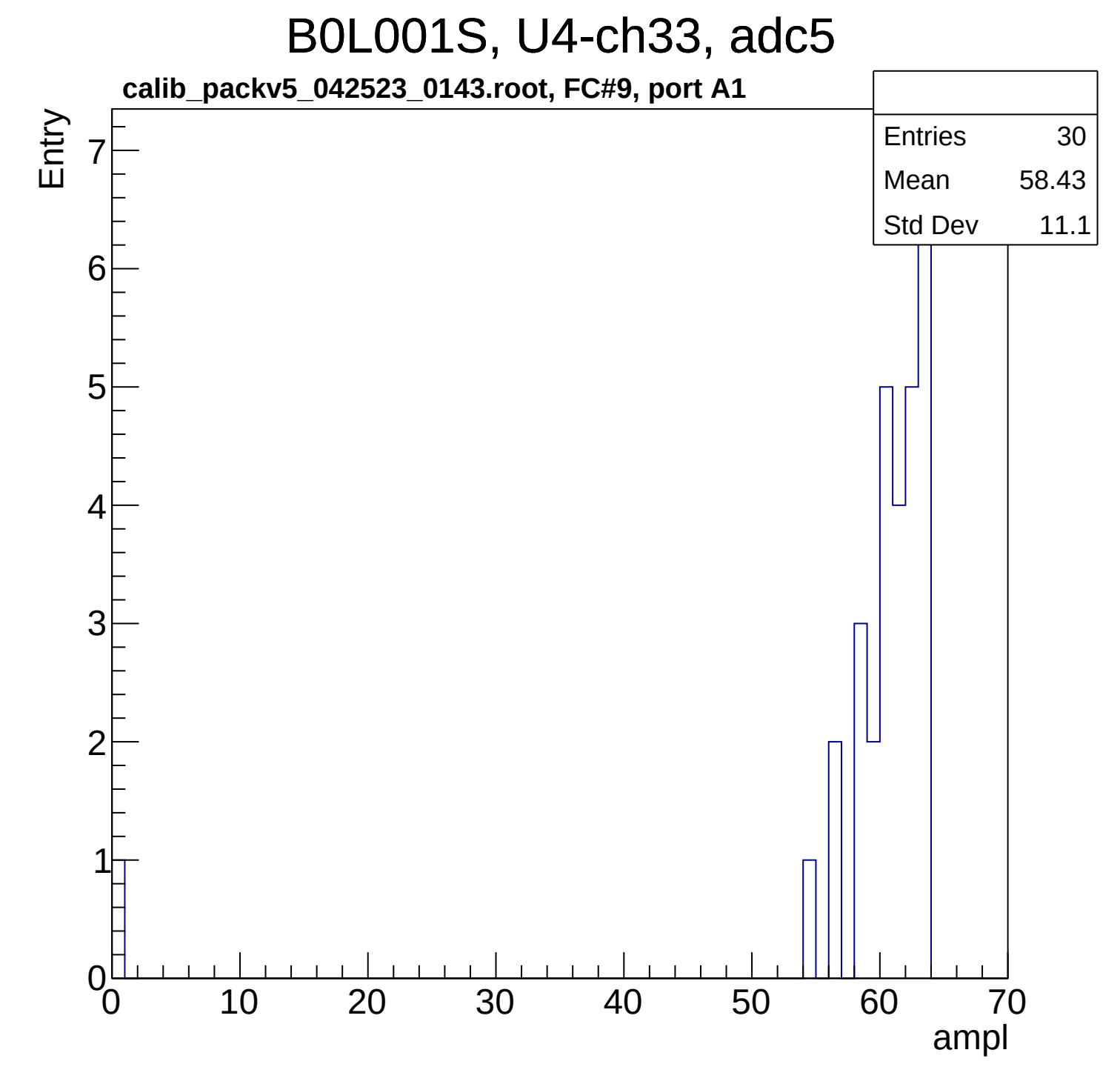
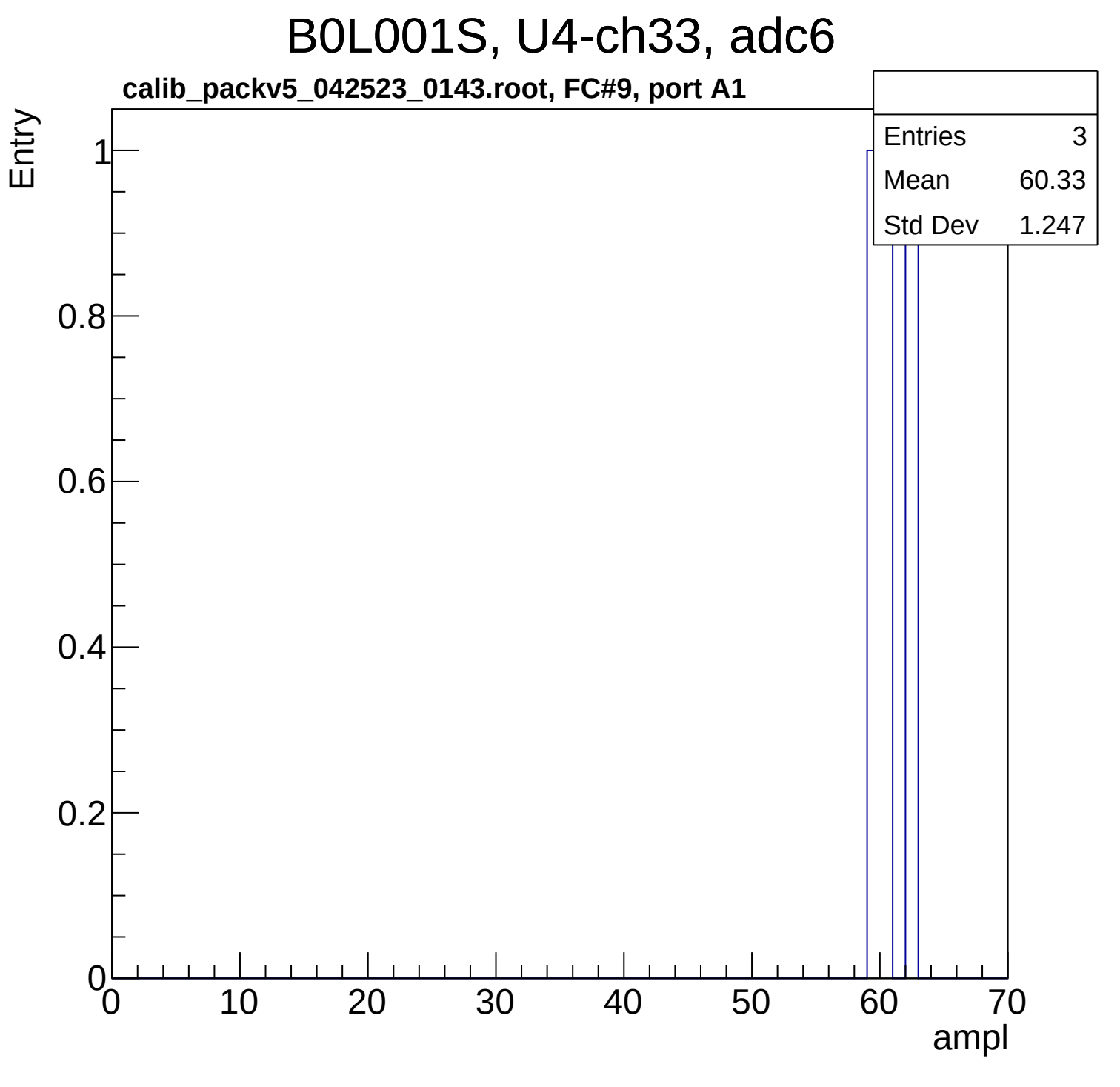

B0L001S, U4-ch33, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.43
Std Dev 11.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch33, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 60.33
Std Dev 1.247
Entry
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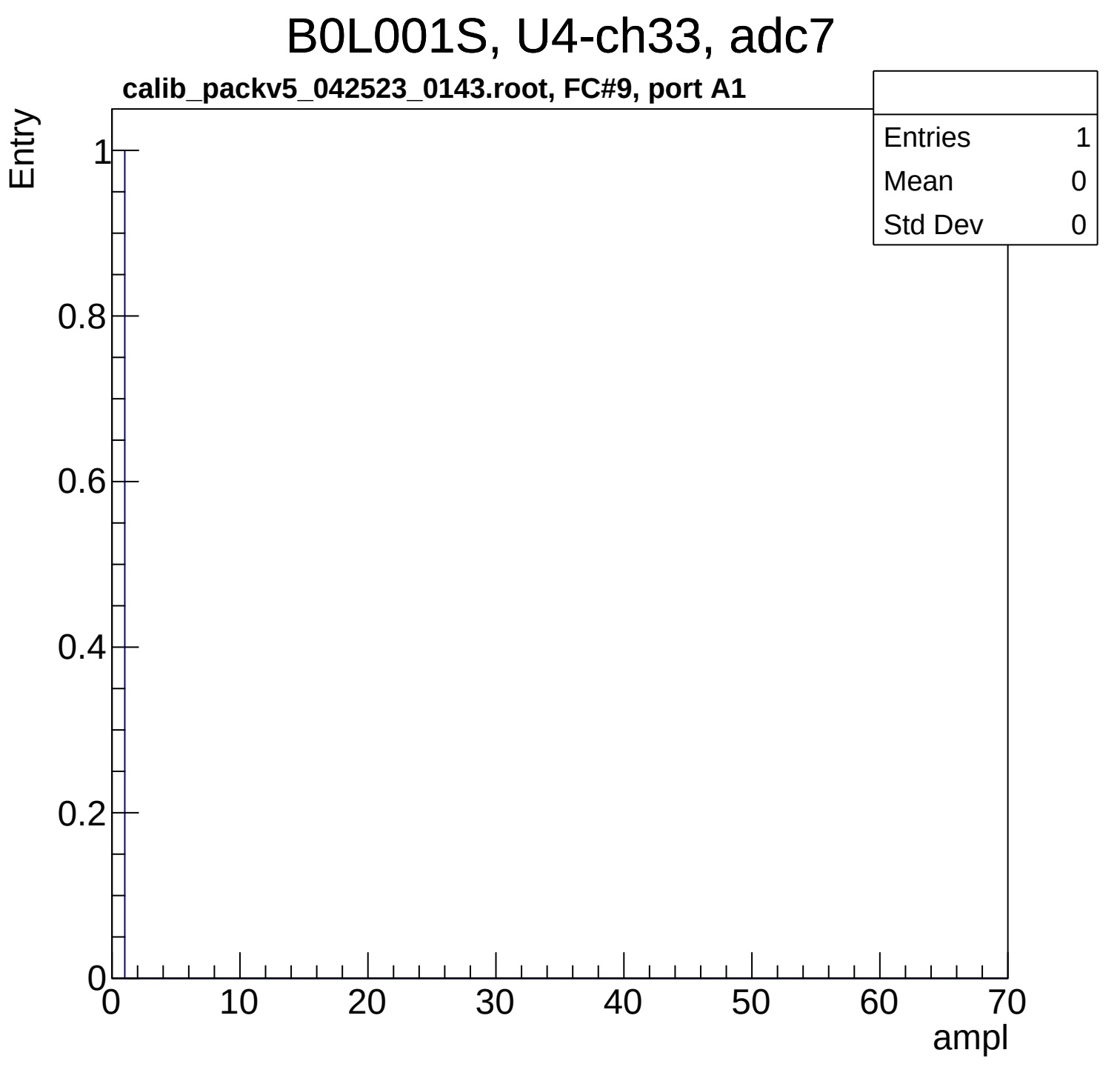
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch33, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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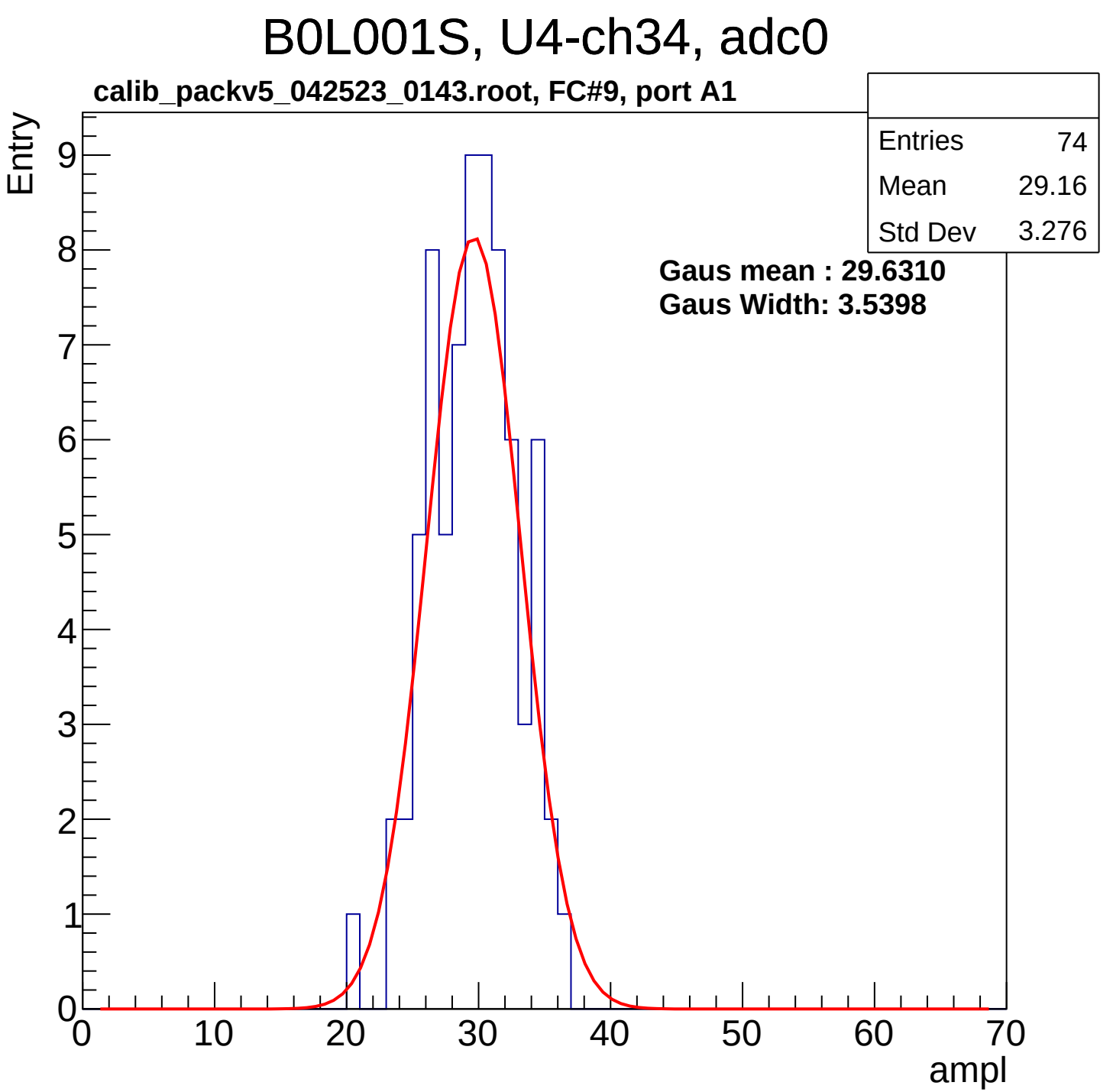
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch34, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 29.16
Std Dev 3.276
Gaus mean : 29.6310
Gaus Width: 3.5398
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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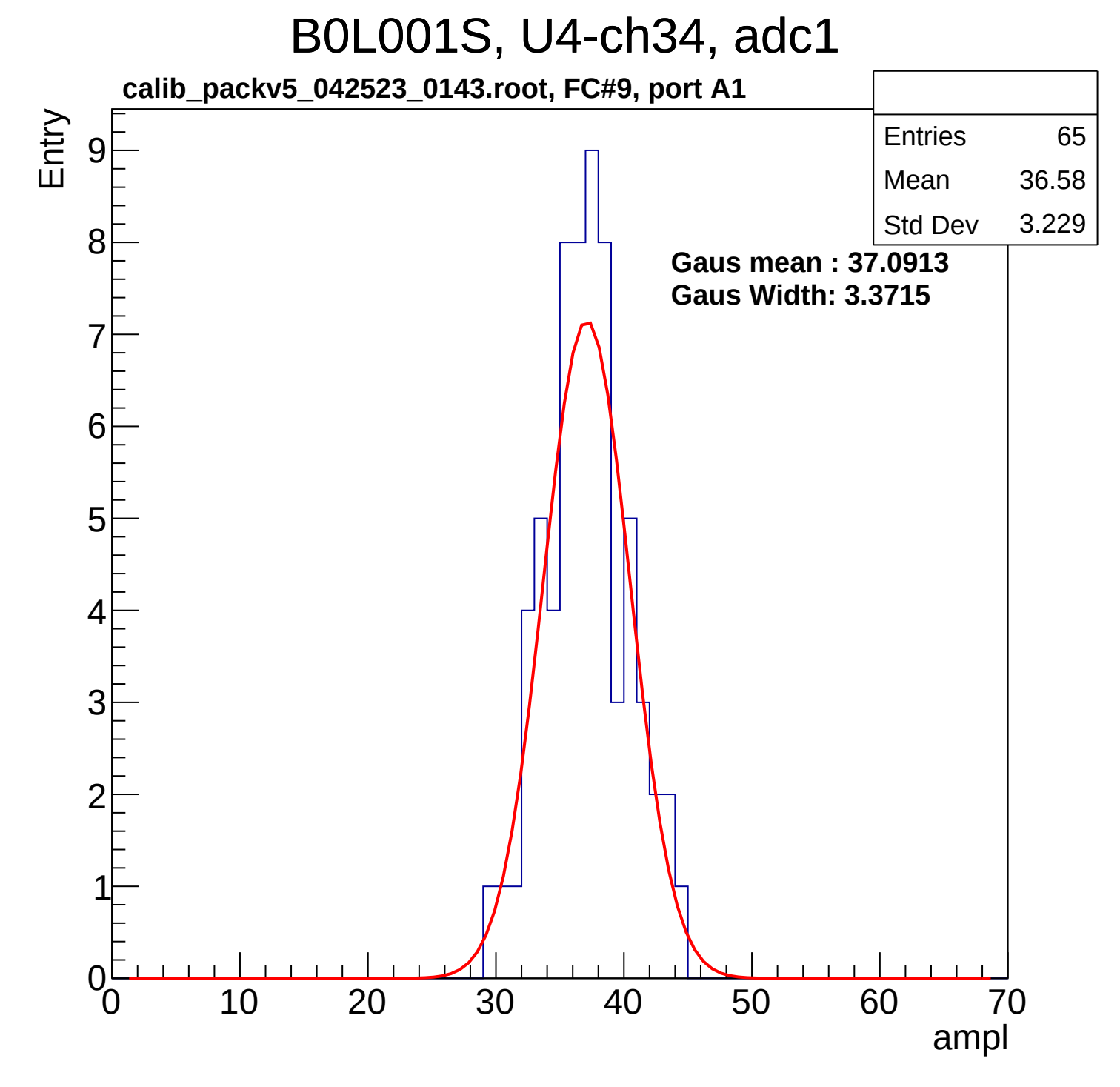
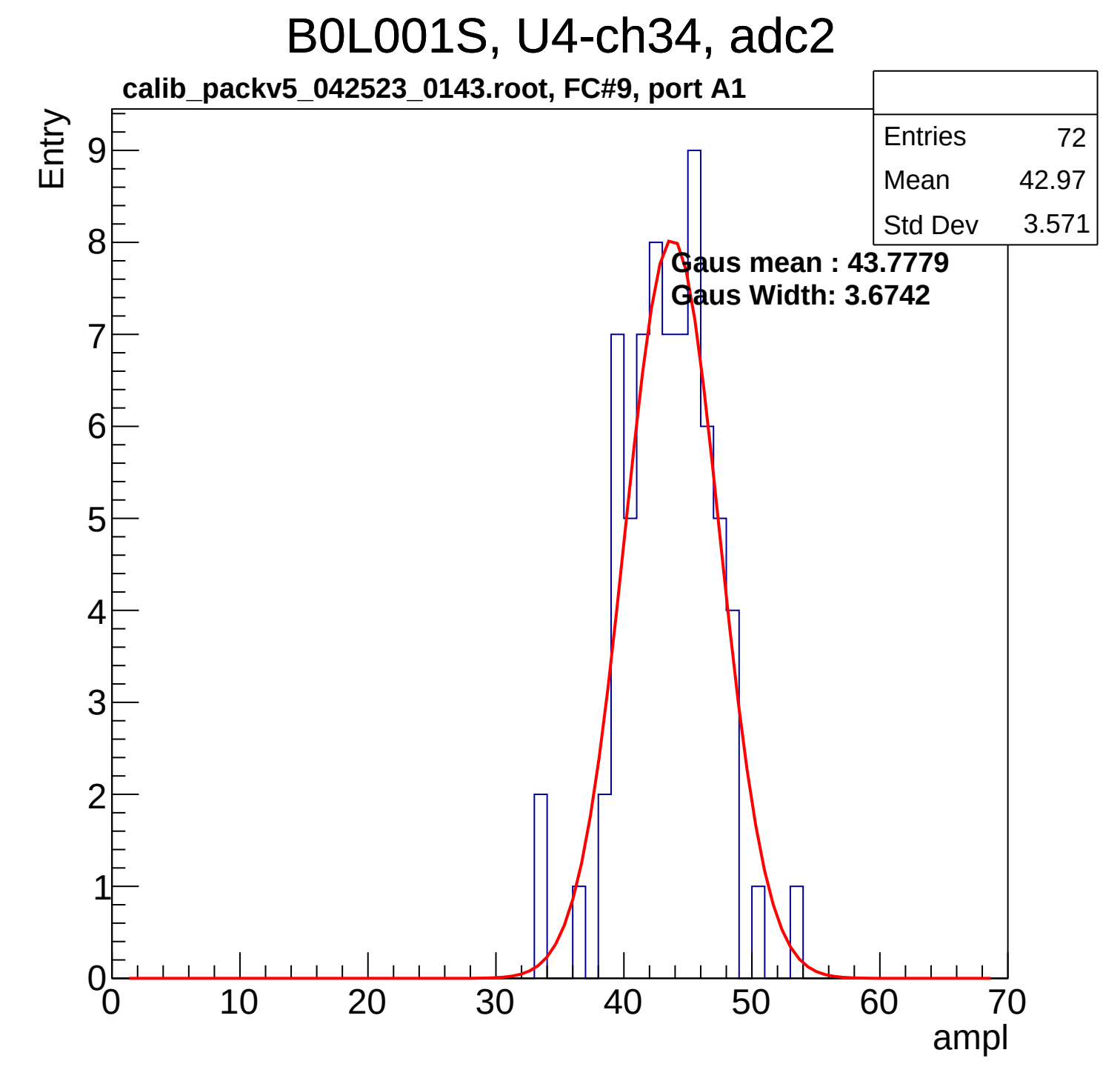

B0L001S, U4-ch34, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.58
Std Dev 3.229
Gaus mean : 37.0913
Gaus Width: 3.3715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch34, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 42.97
Std Dev 3.571
Gaus mean : 43.7779
Gaus Width: 3.6742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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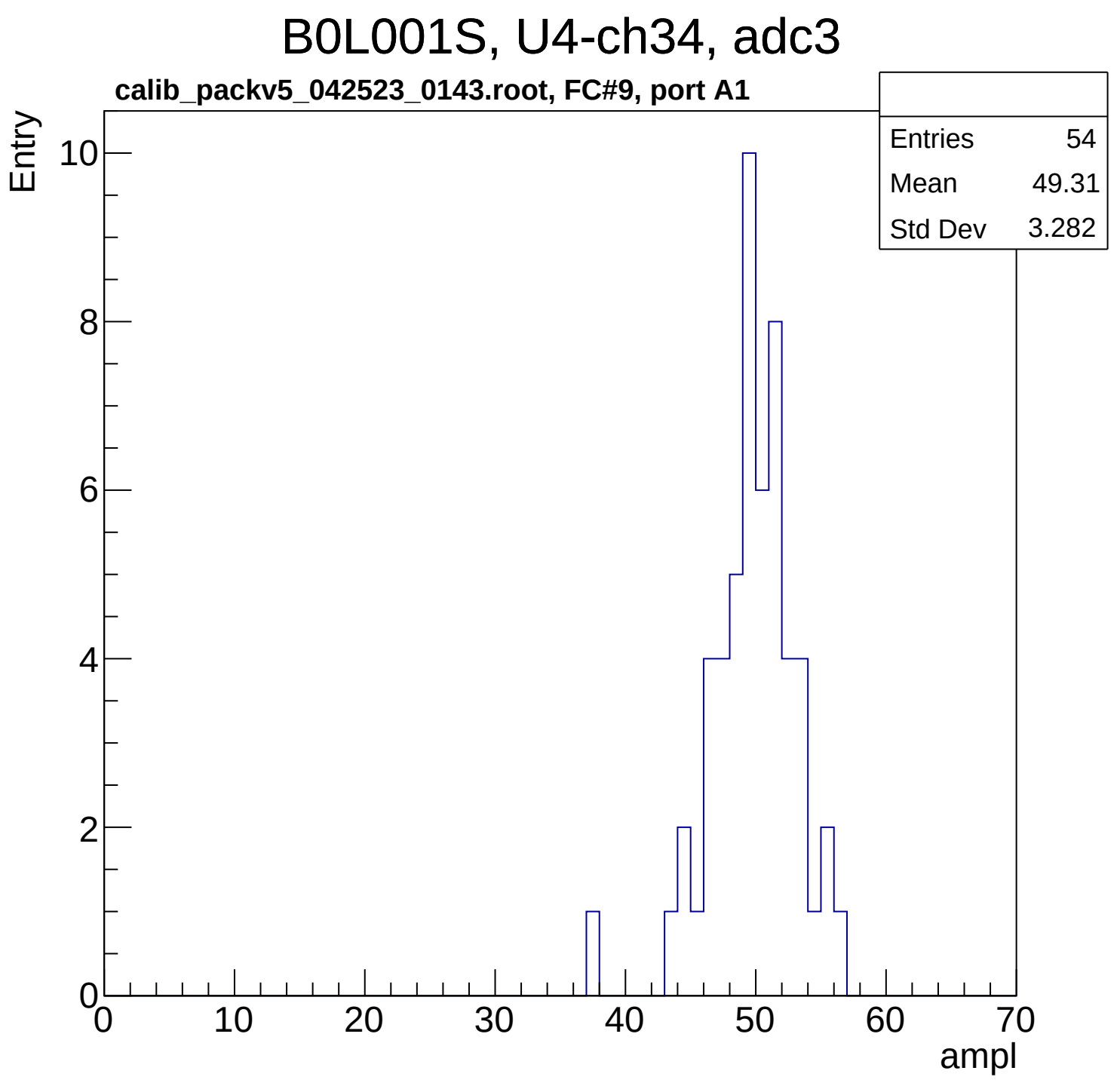
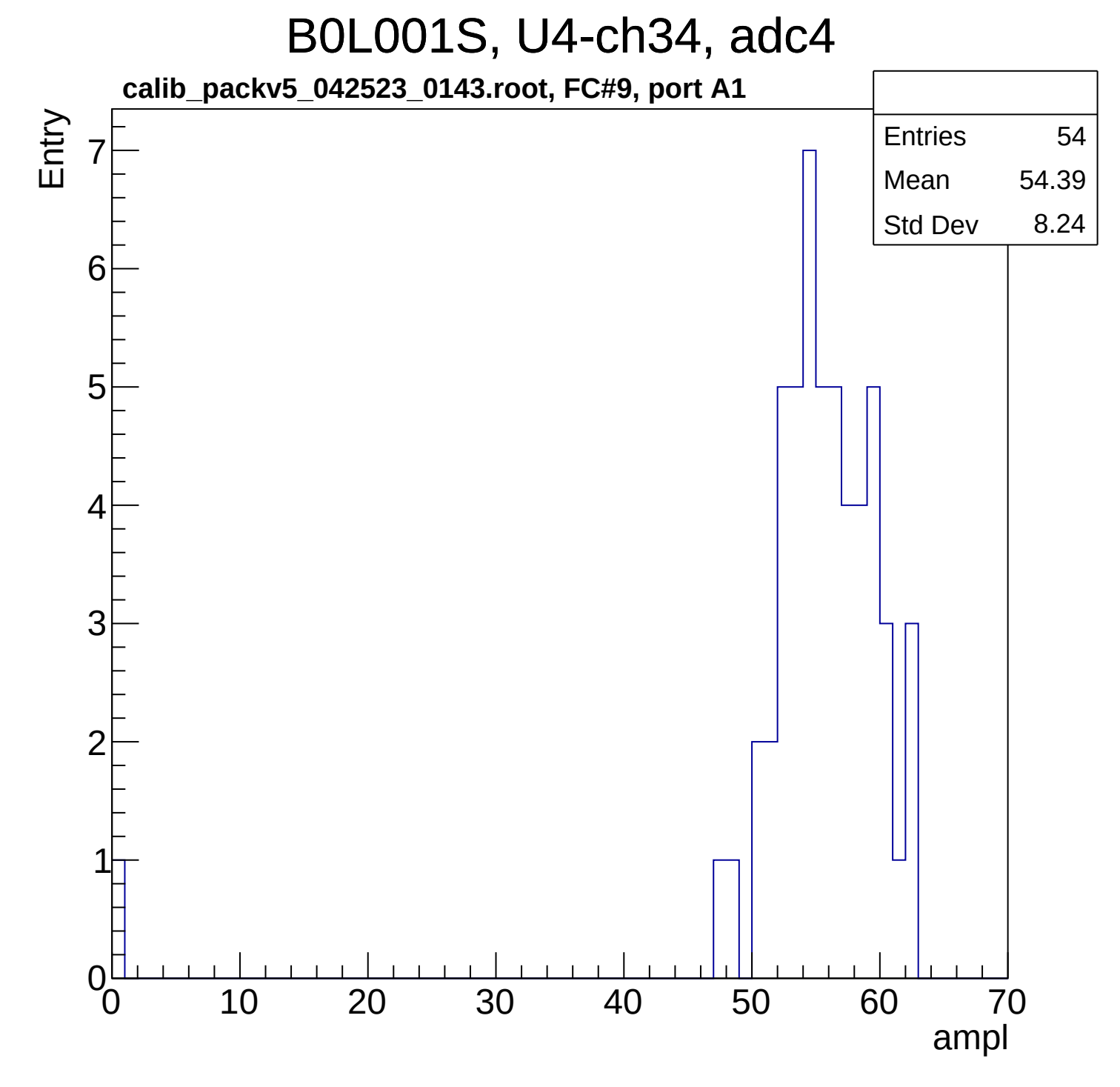

B0L001S, U4-ch34, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 49.31
Std Dev 3.282
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch34, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 54.39
Std Dev 8.24
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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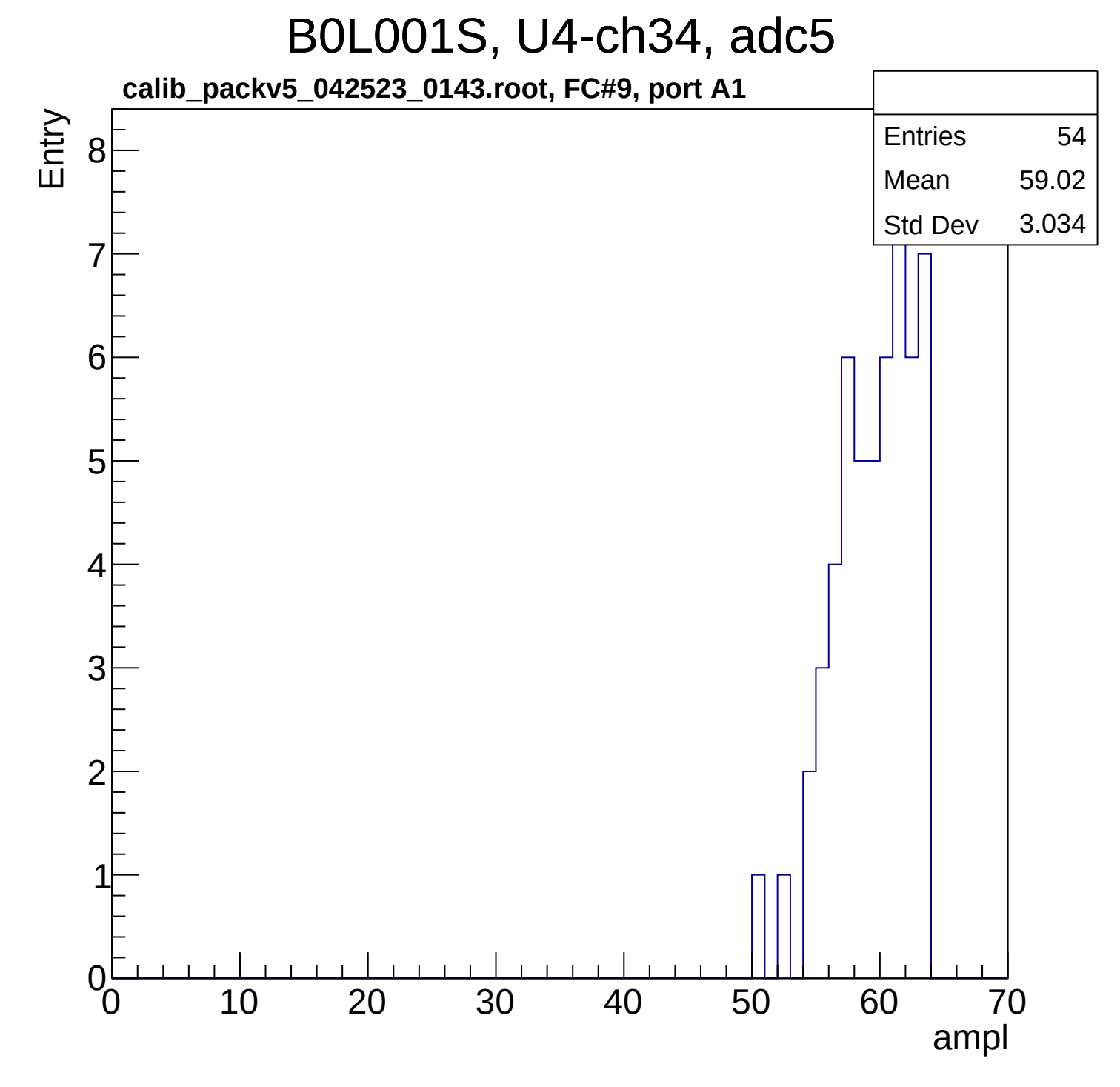
B0L001S, U4-ch34, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 59.02
Std Dev 3.034
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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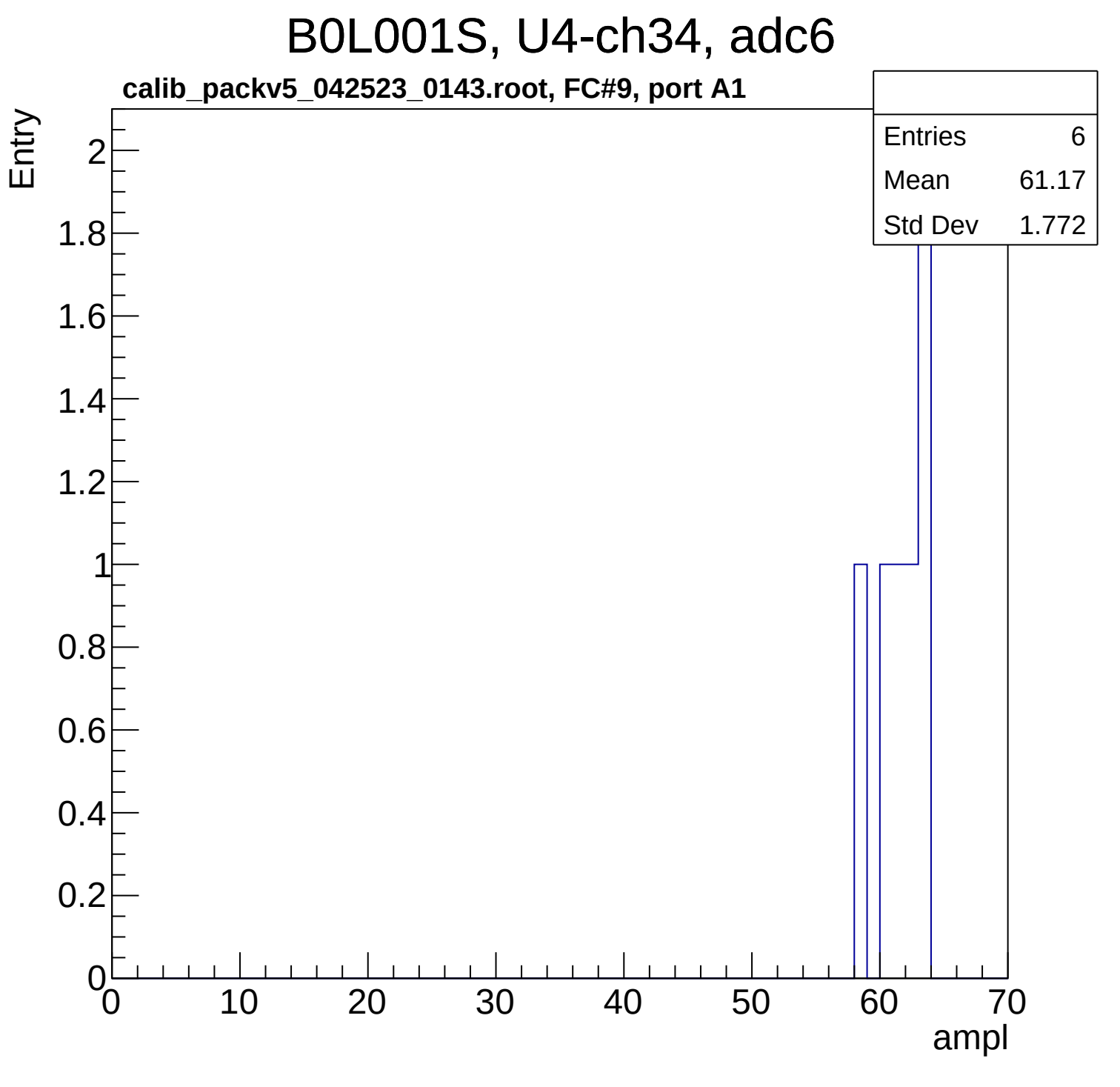
40
50
60
70


B0L001S, U4-ch34, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.17
Std Dev 1.772
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
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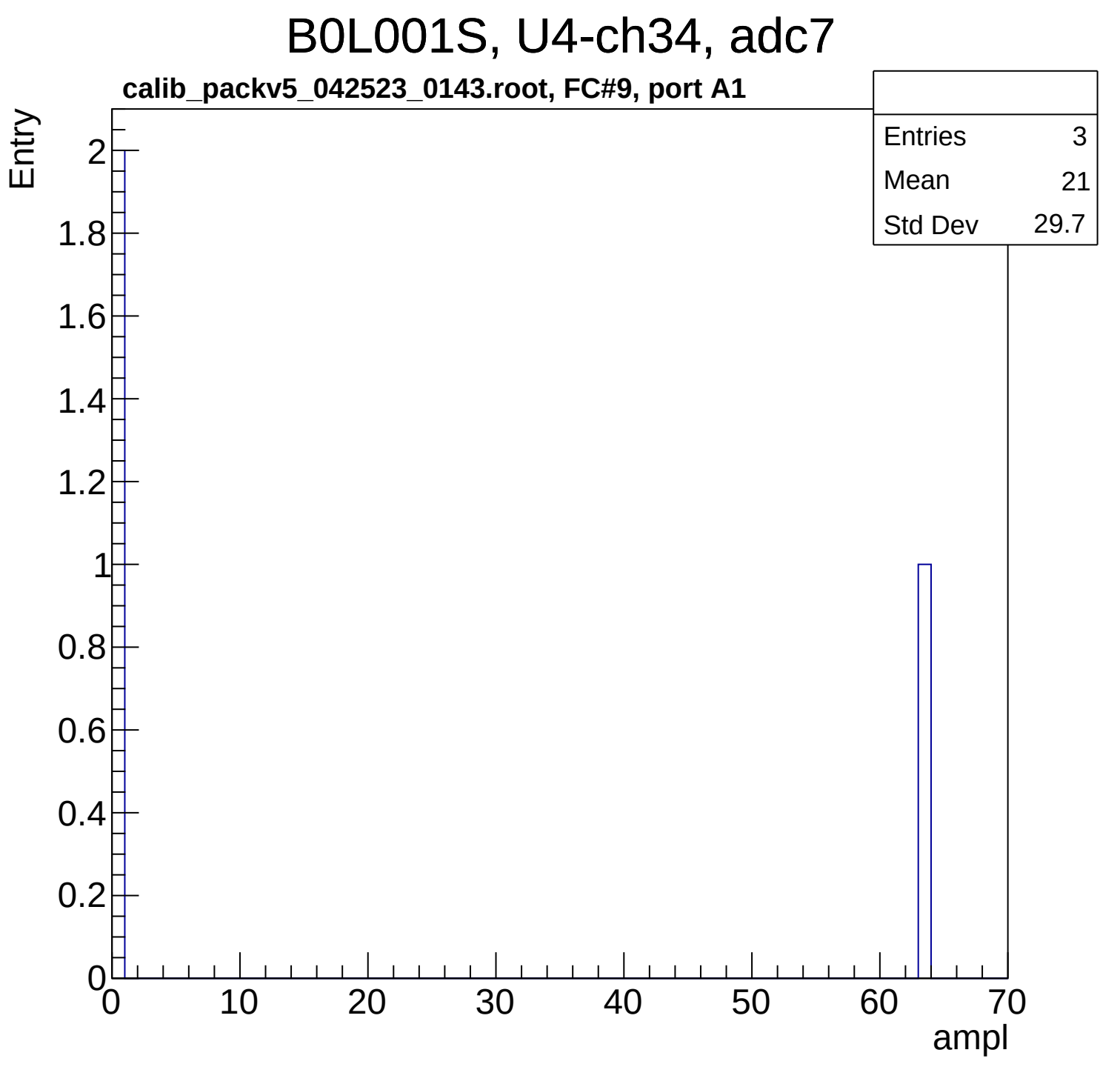
B0L001S, U4-ch34, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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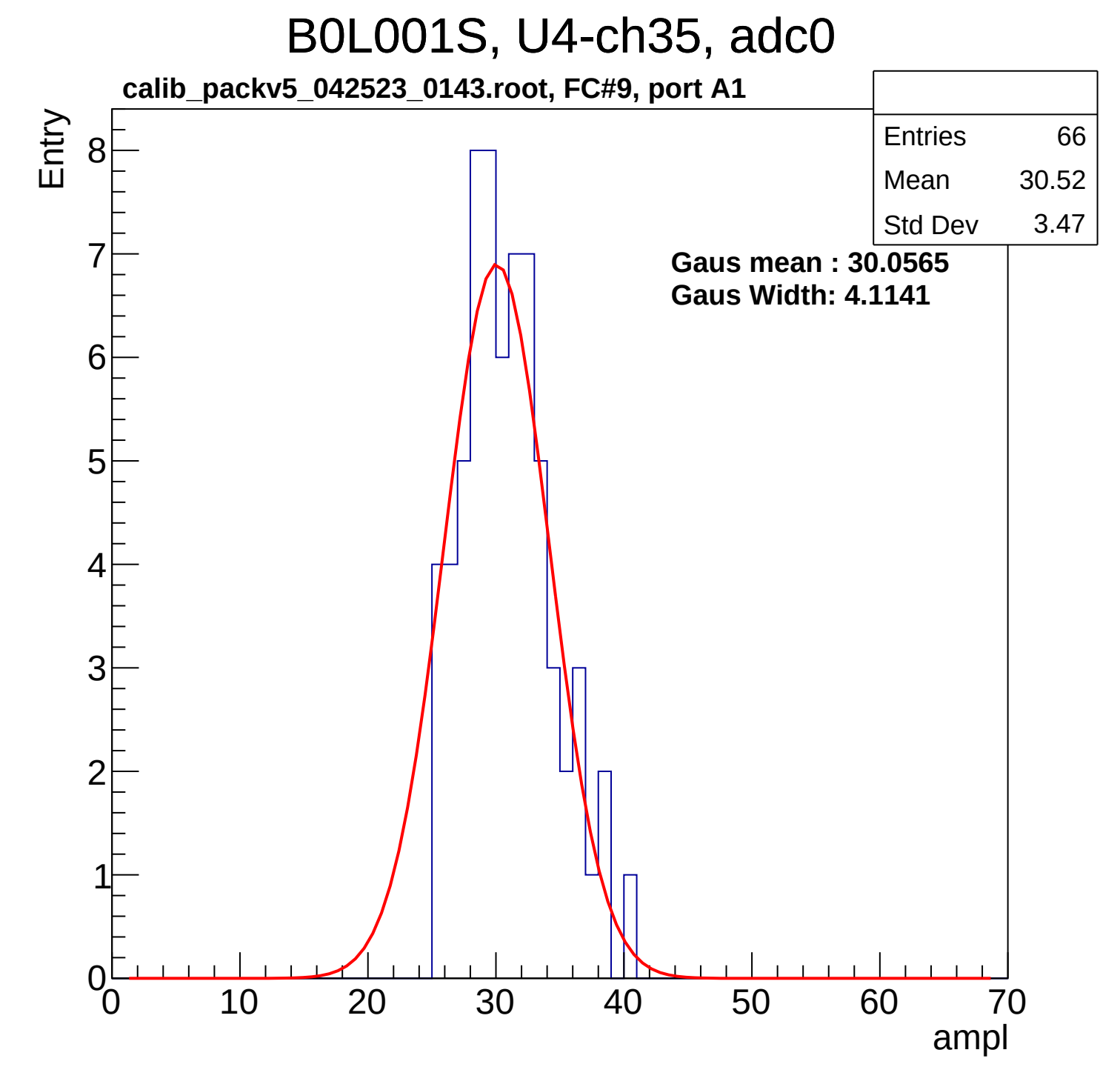
B0L001S, U4-ch35, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.52
Std Dev 3.47
Gaus mean : 30.0565
Gaus Width: 4.1141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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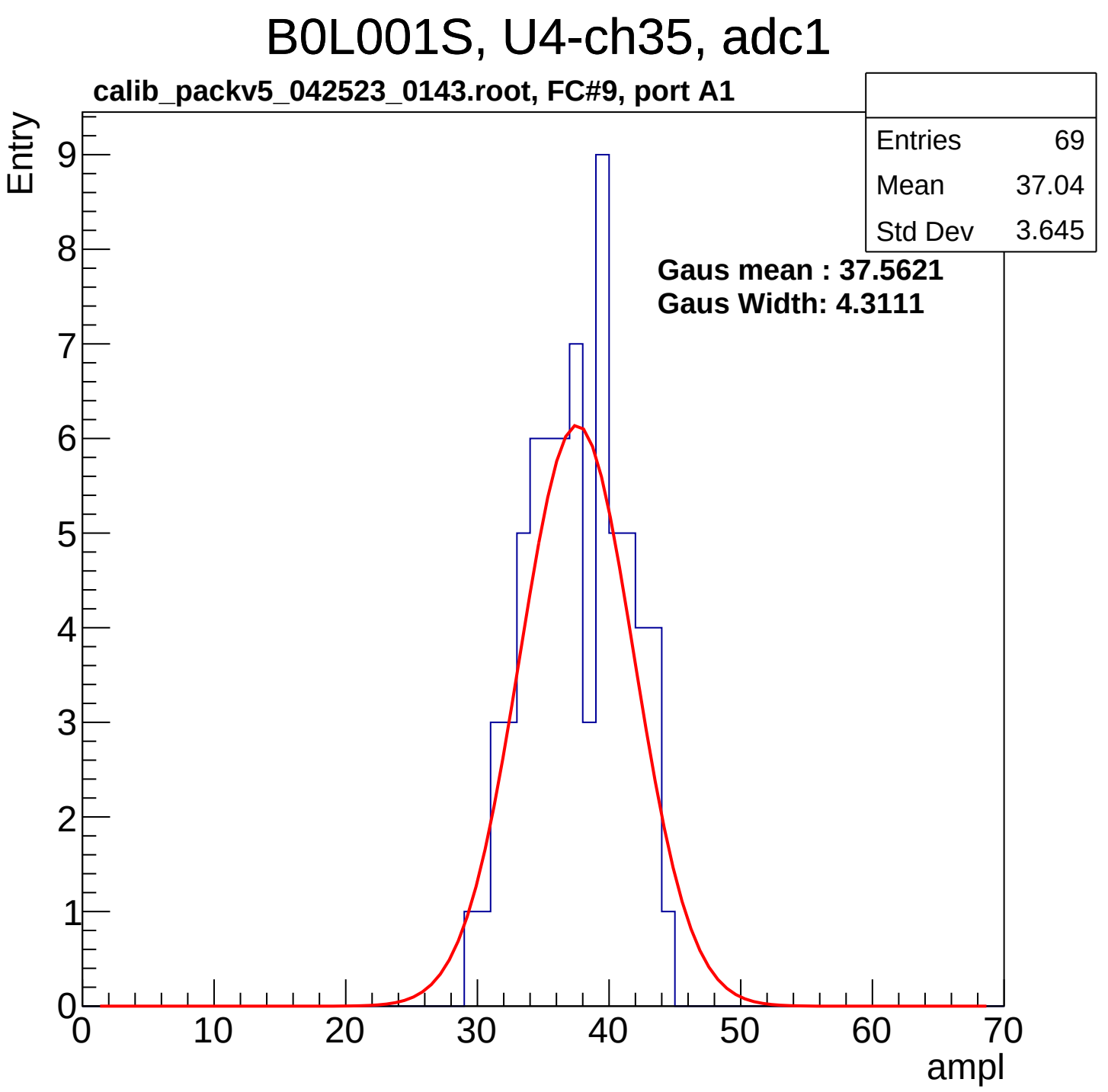
B0L001S, U4-ch35, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.04
Std Dev 3.645
Gaus mean : 37.5621
Gaus Width: 4.3111
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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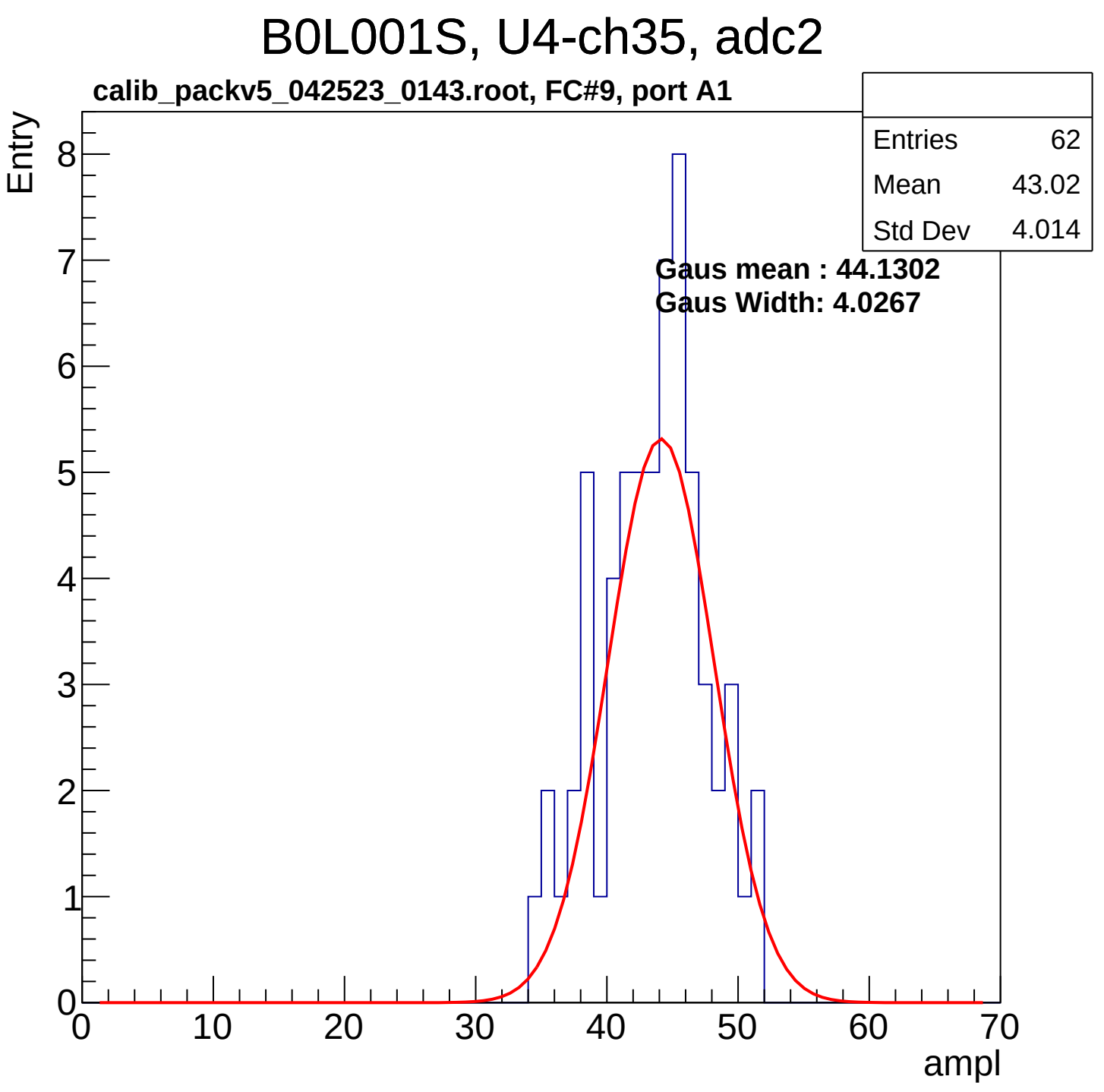
B0L001S, U4-ch35, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 43.02
Std Dev 4.014
Gaus mean : 44.1302
Gaus Width: 4.0267
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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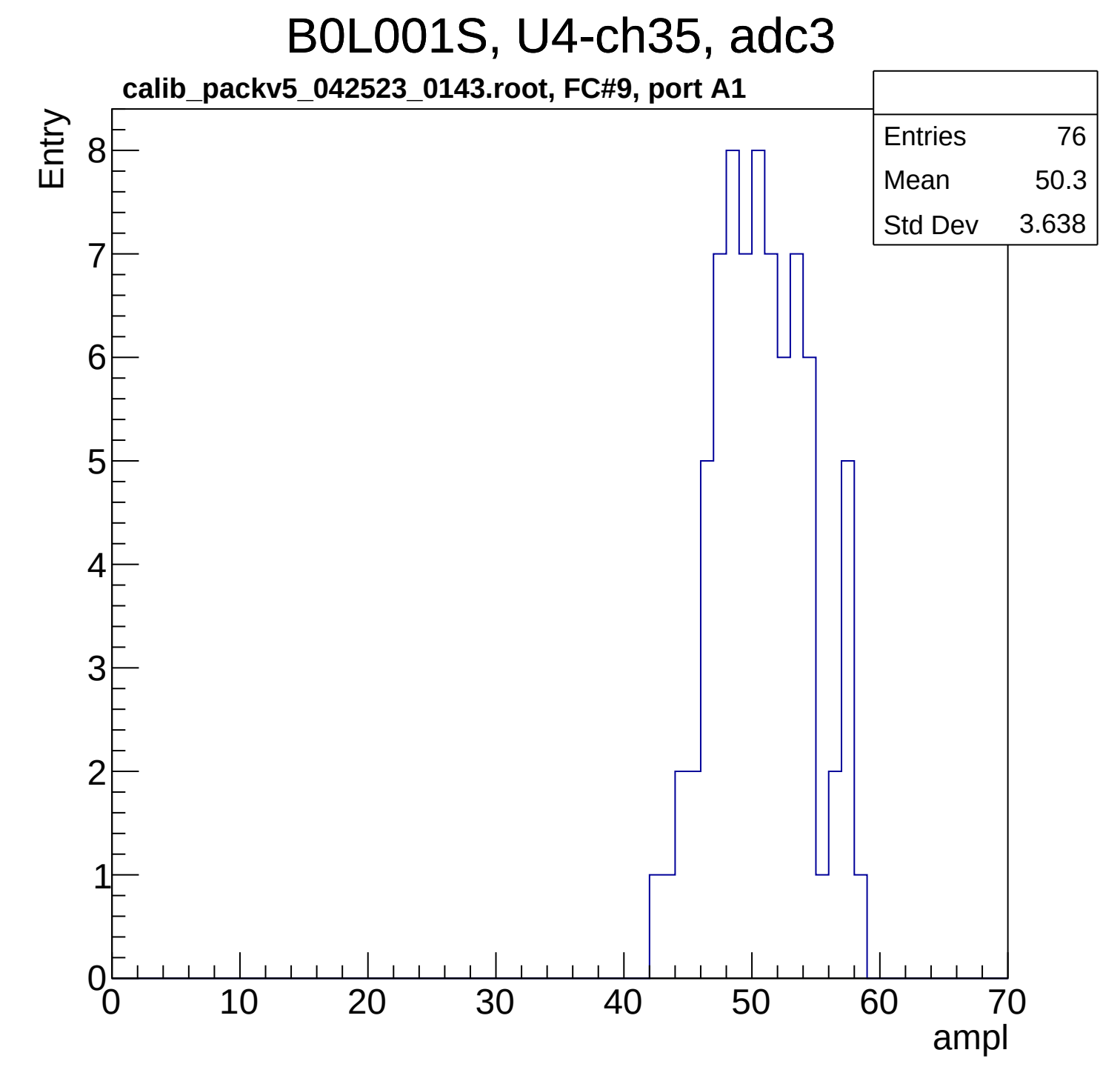
B0L001S, U4-ch35, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 50.3
Std Dev 3.638
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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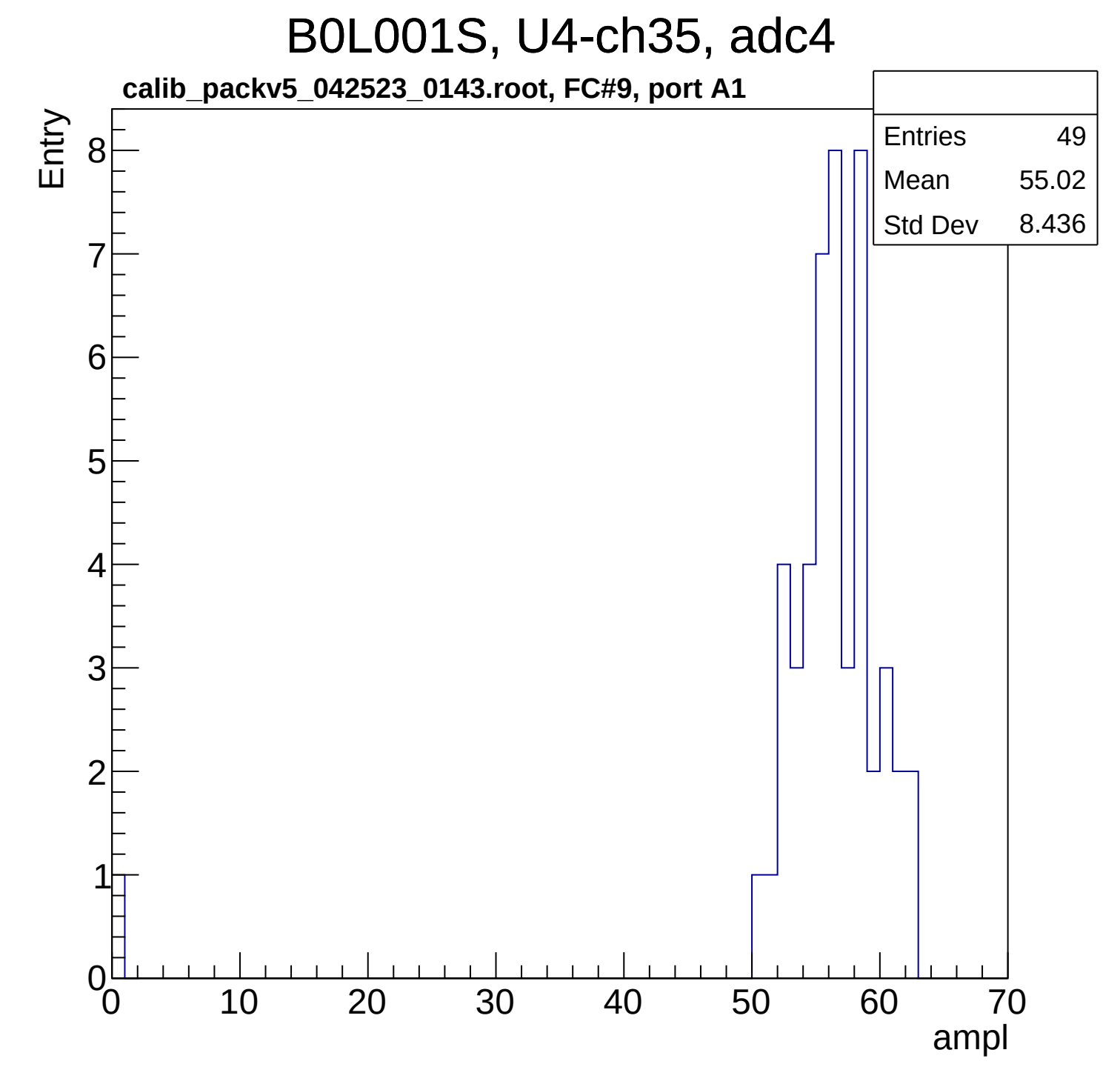
B0L001S, U4-ch35, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 55.02
Std Dev 8.436
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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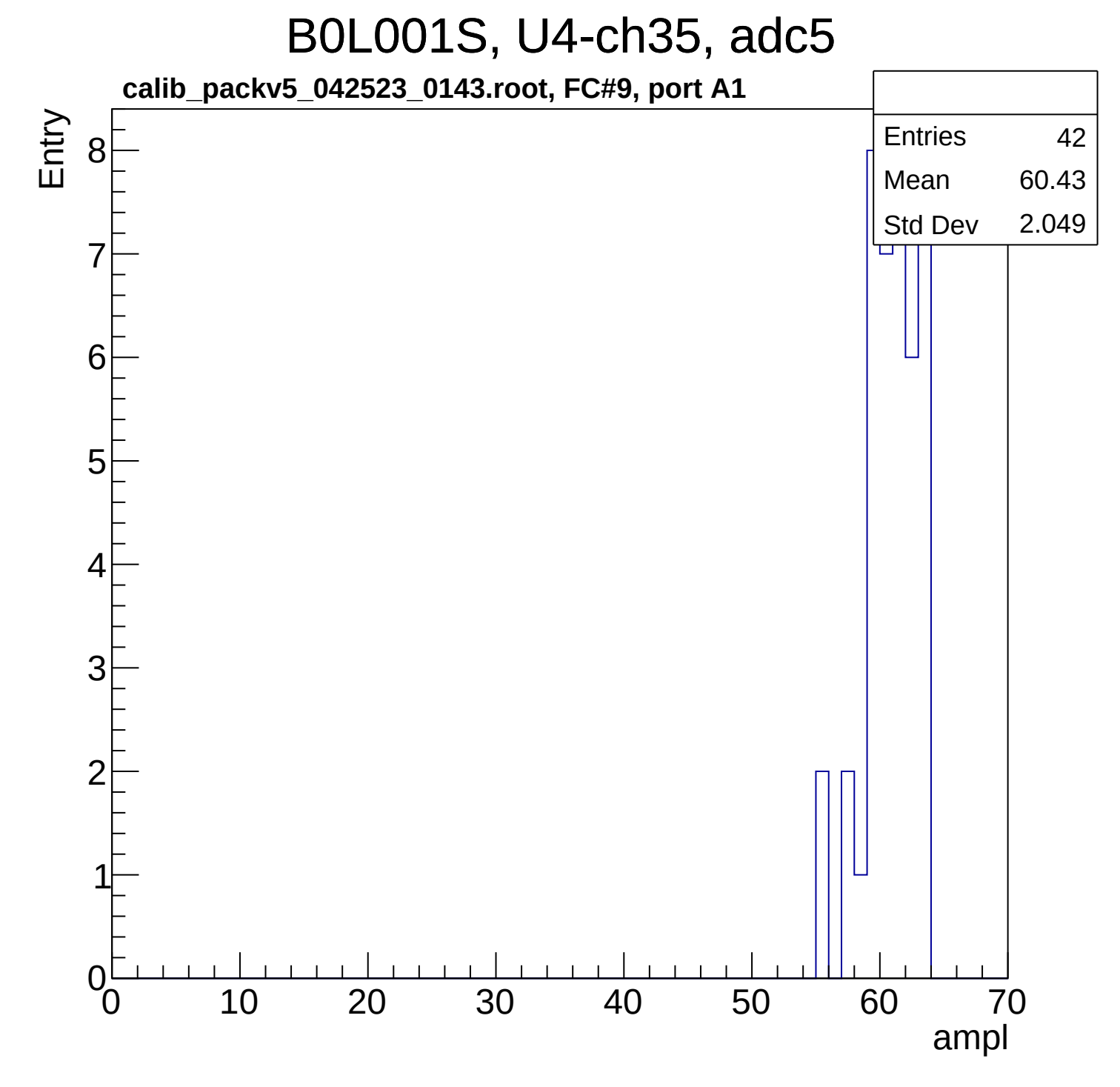
40
50
60
70


B0L001S, U4-ch35, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.43
Std Dev 2.049
Entry
ampl
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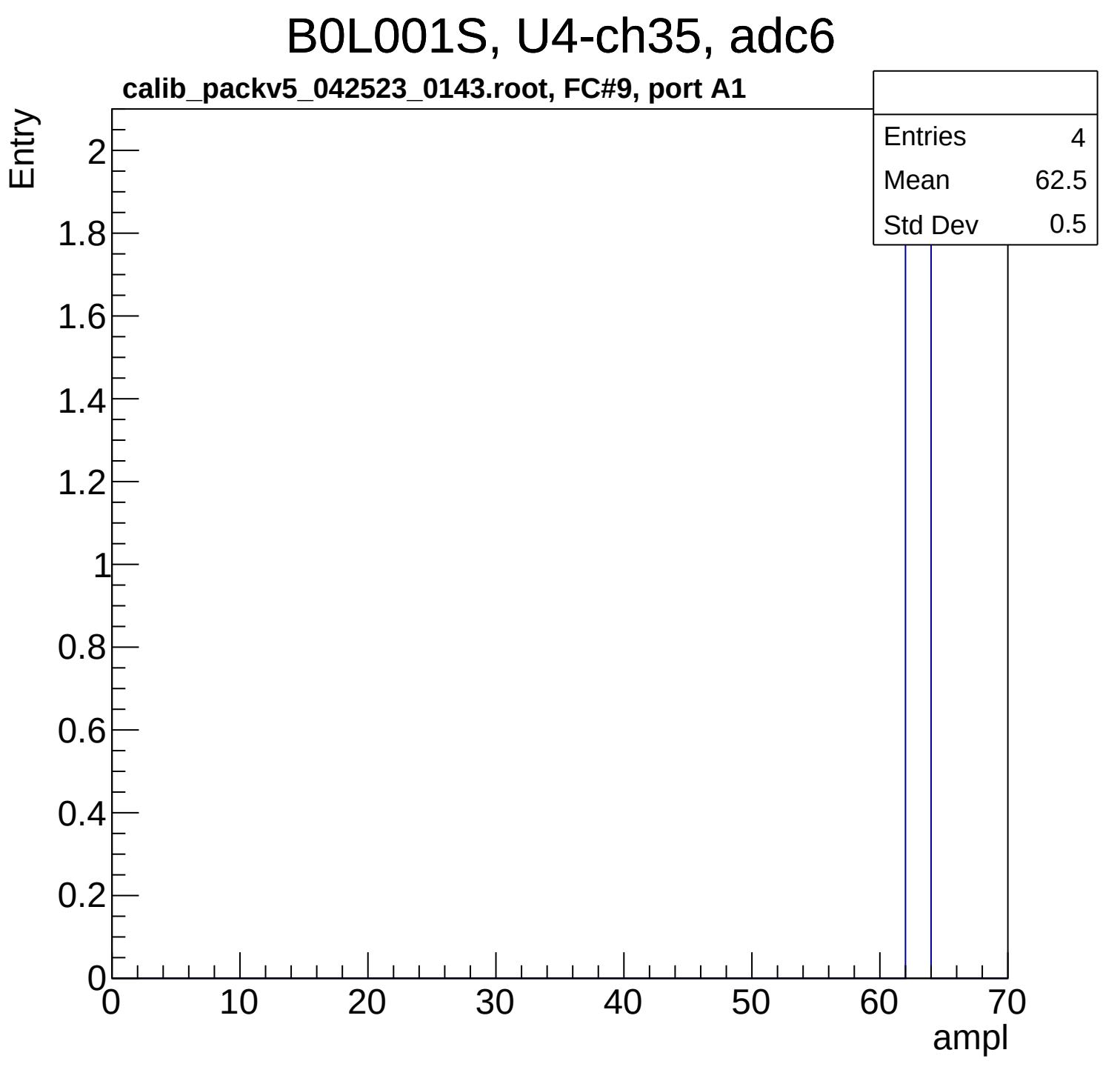
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch35, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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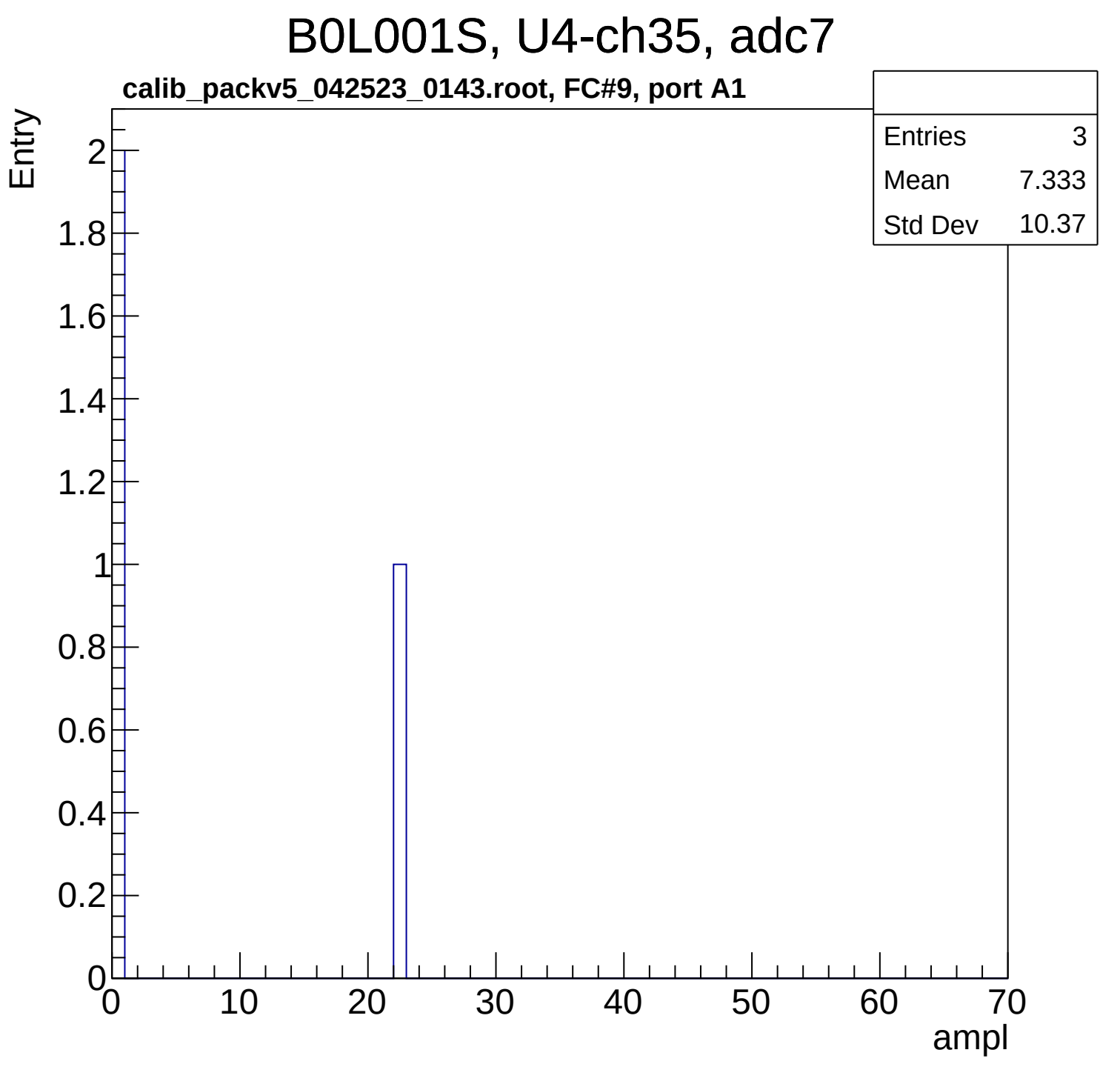
B0L001S, U4-ch35, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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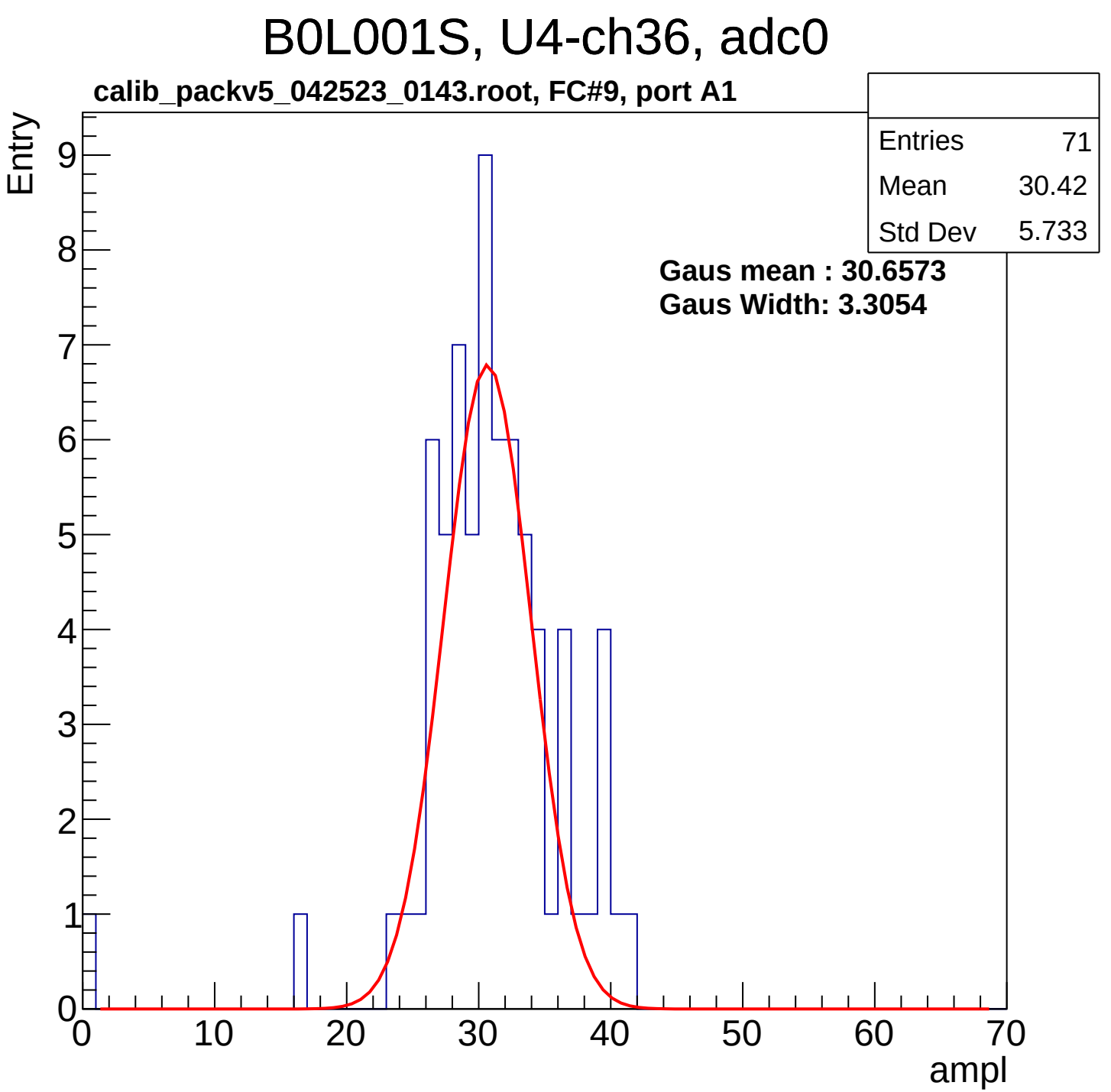
B0L001S, U4-ch36, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.42
Std Dev 5.733
Gaus mean : 30.6573
Gaus Width: 3.3054
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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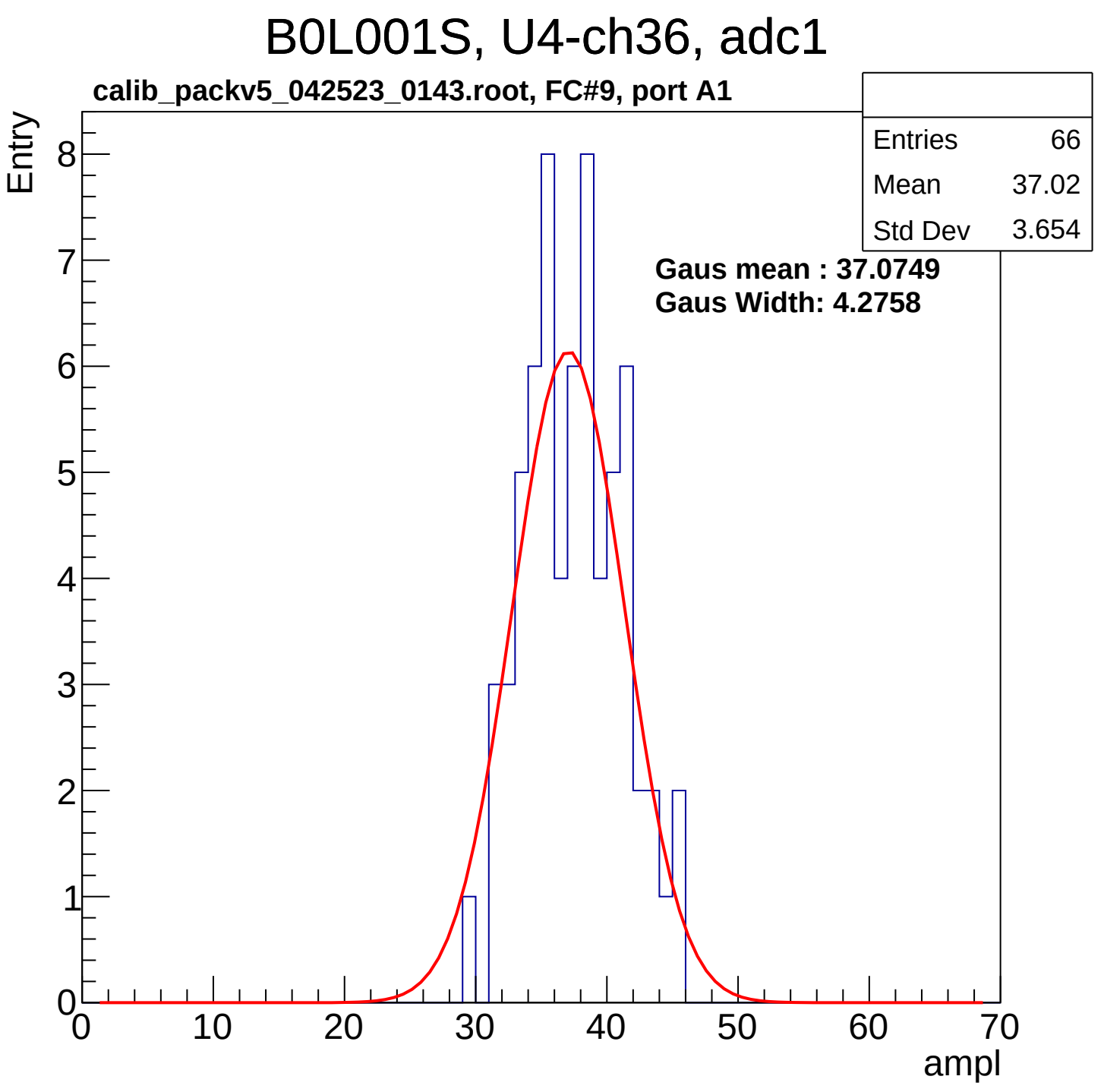
B0L001S, U4-ch36, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.02
Std Dev 3.654
Gaus mean : 37.0749
Gaus Width: 4.2758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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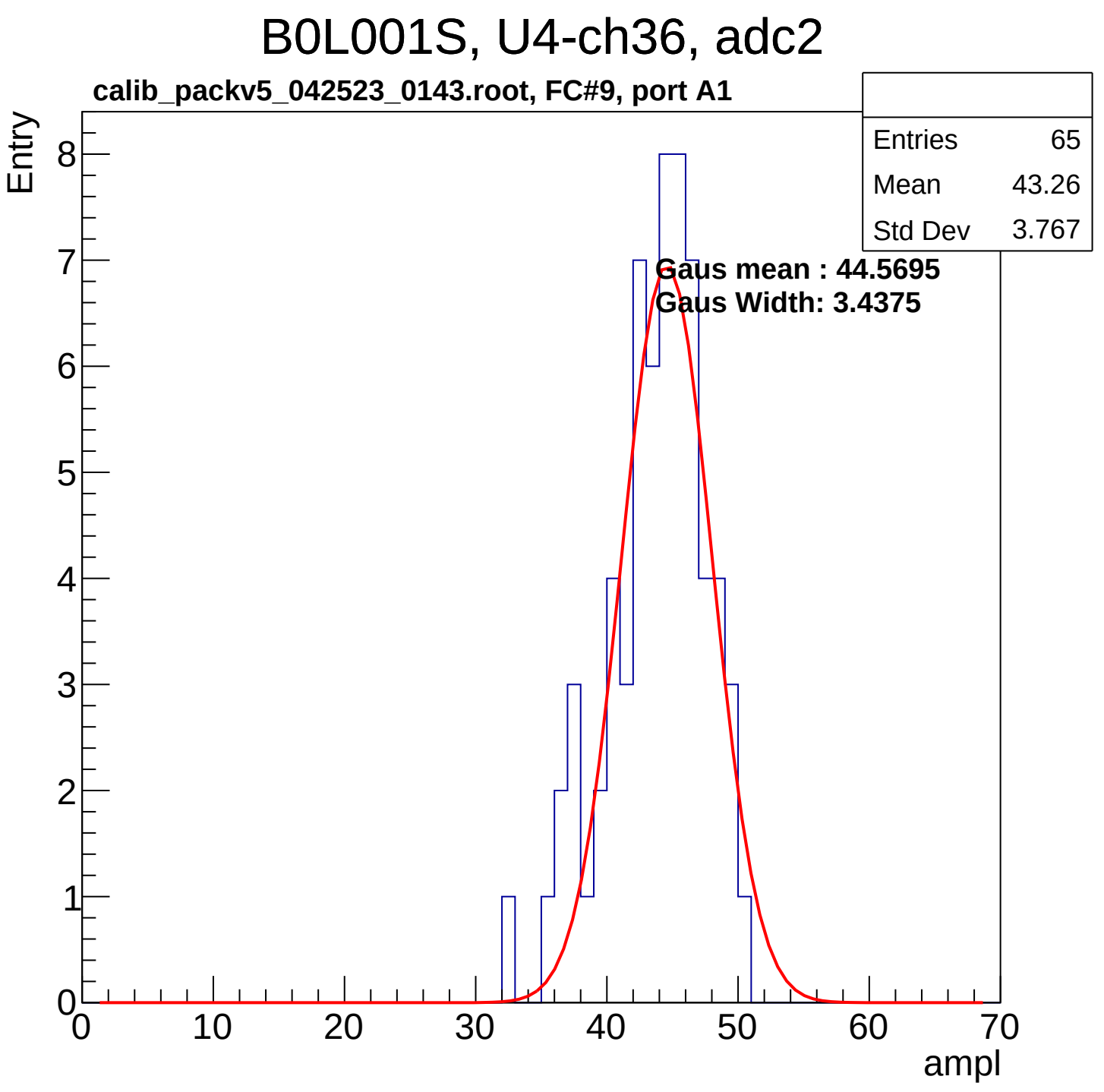
B0L001S, U4-ch36, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.26
Std Dev 3.767
Gaus mean : 44.5695
Gaus Width: 3.4375
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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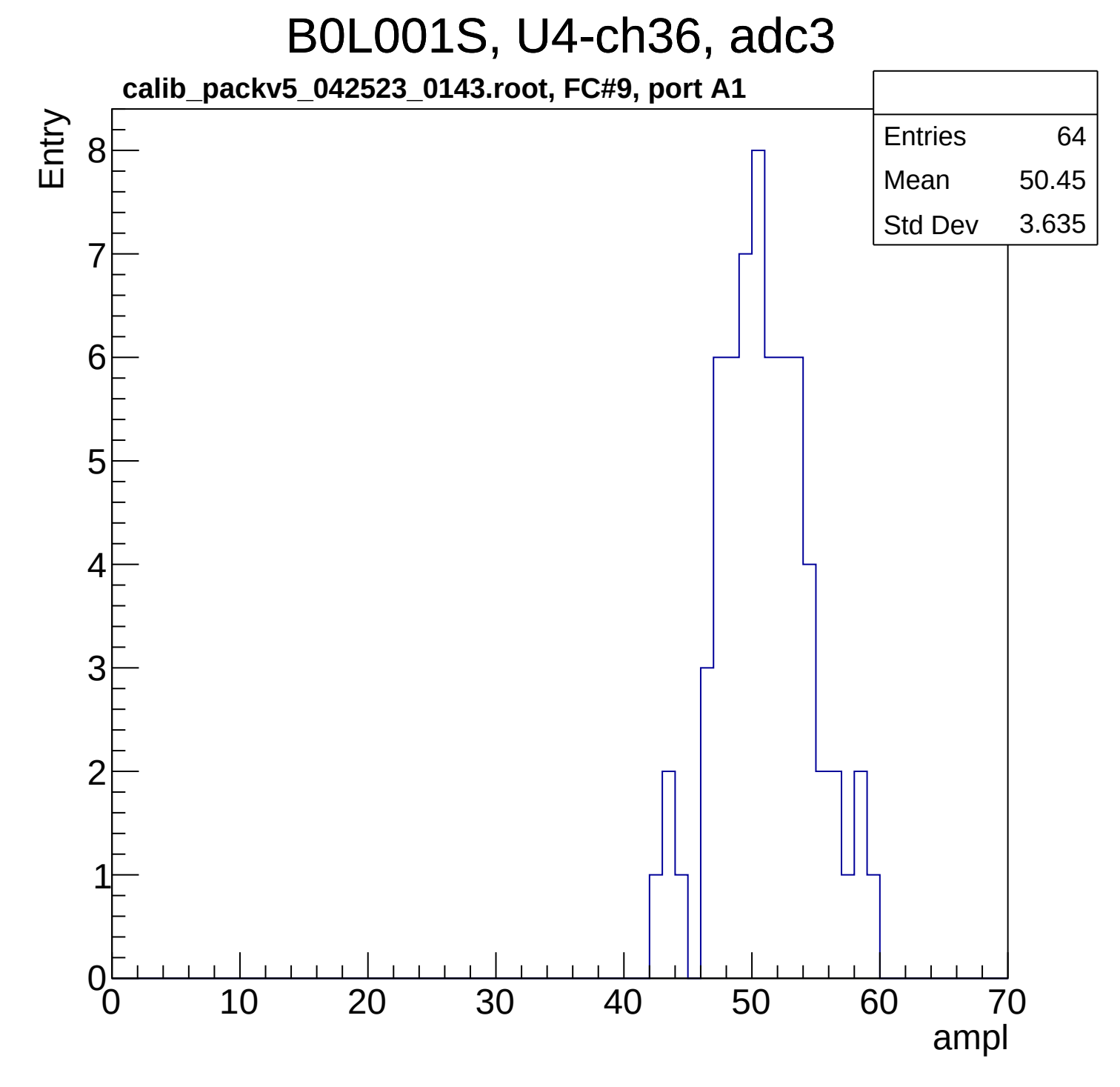
B0L001S, U4-ch36, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.45
Std Dev 3.635
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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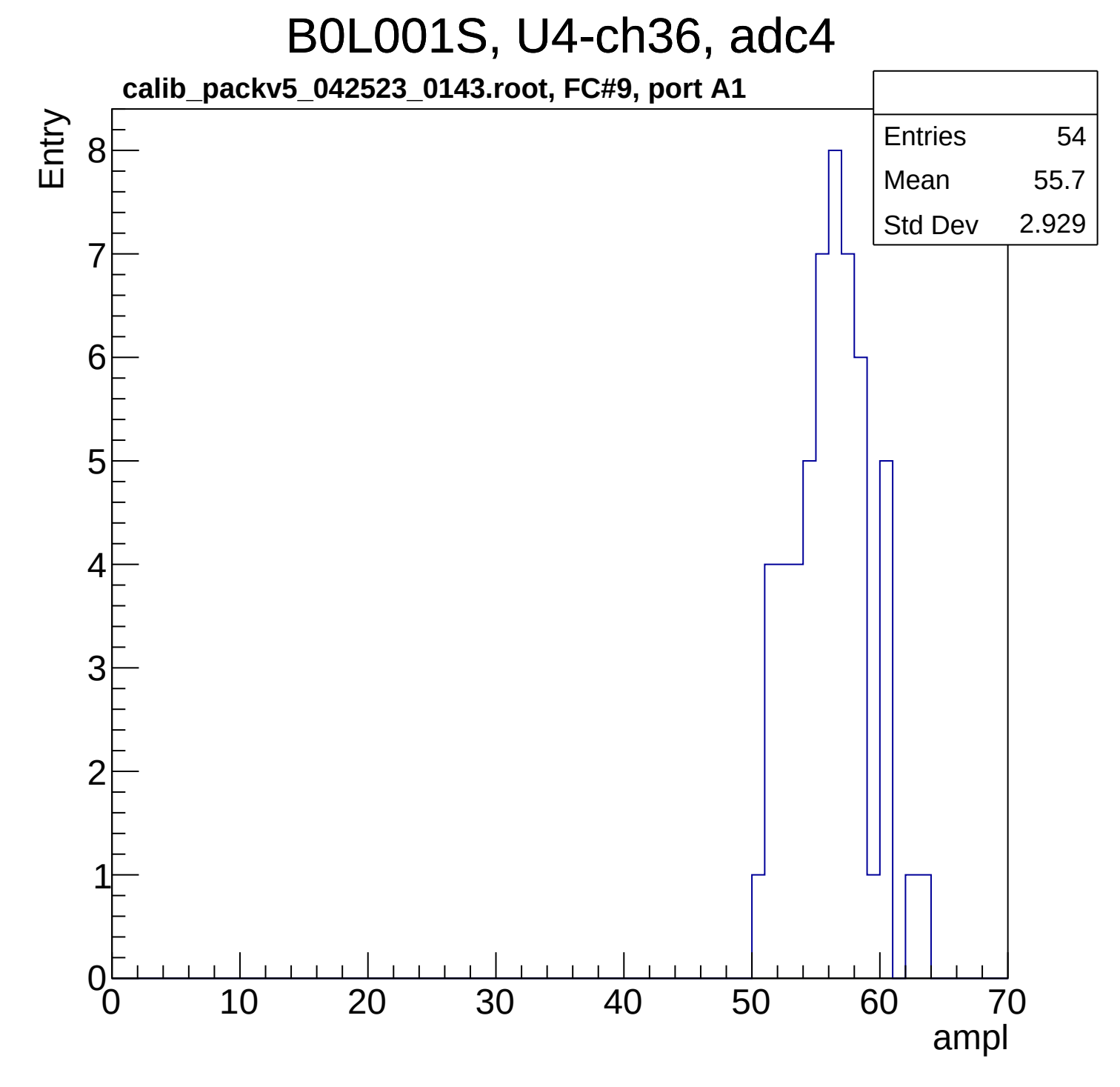
B0L001S, U4-ch36, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.7
Std Dev 2.929
Entry
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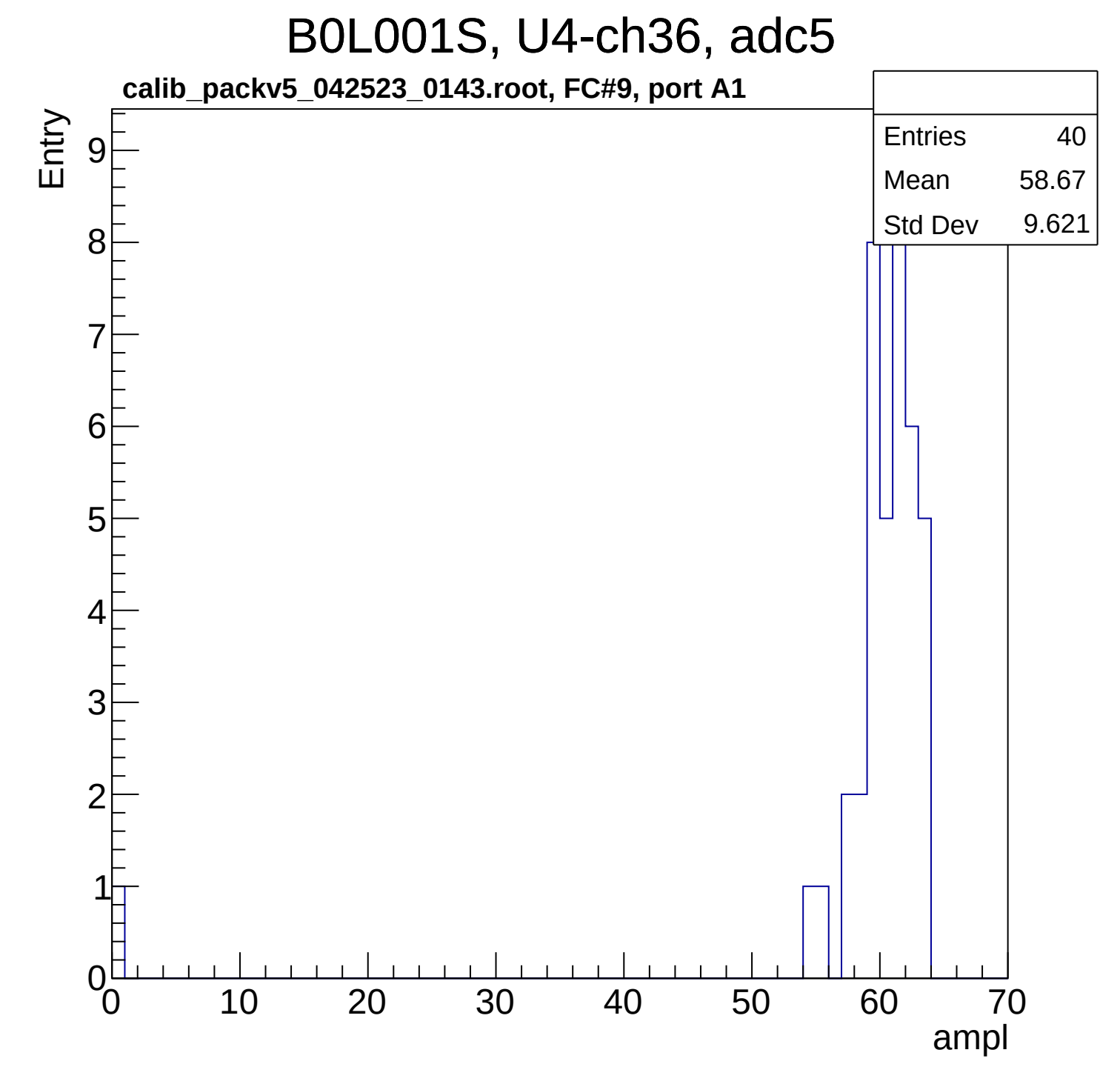
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch36, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.67
Std Dev 9.621
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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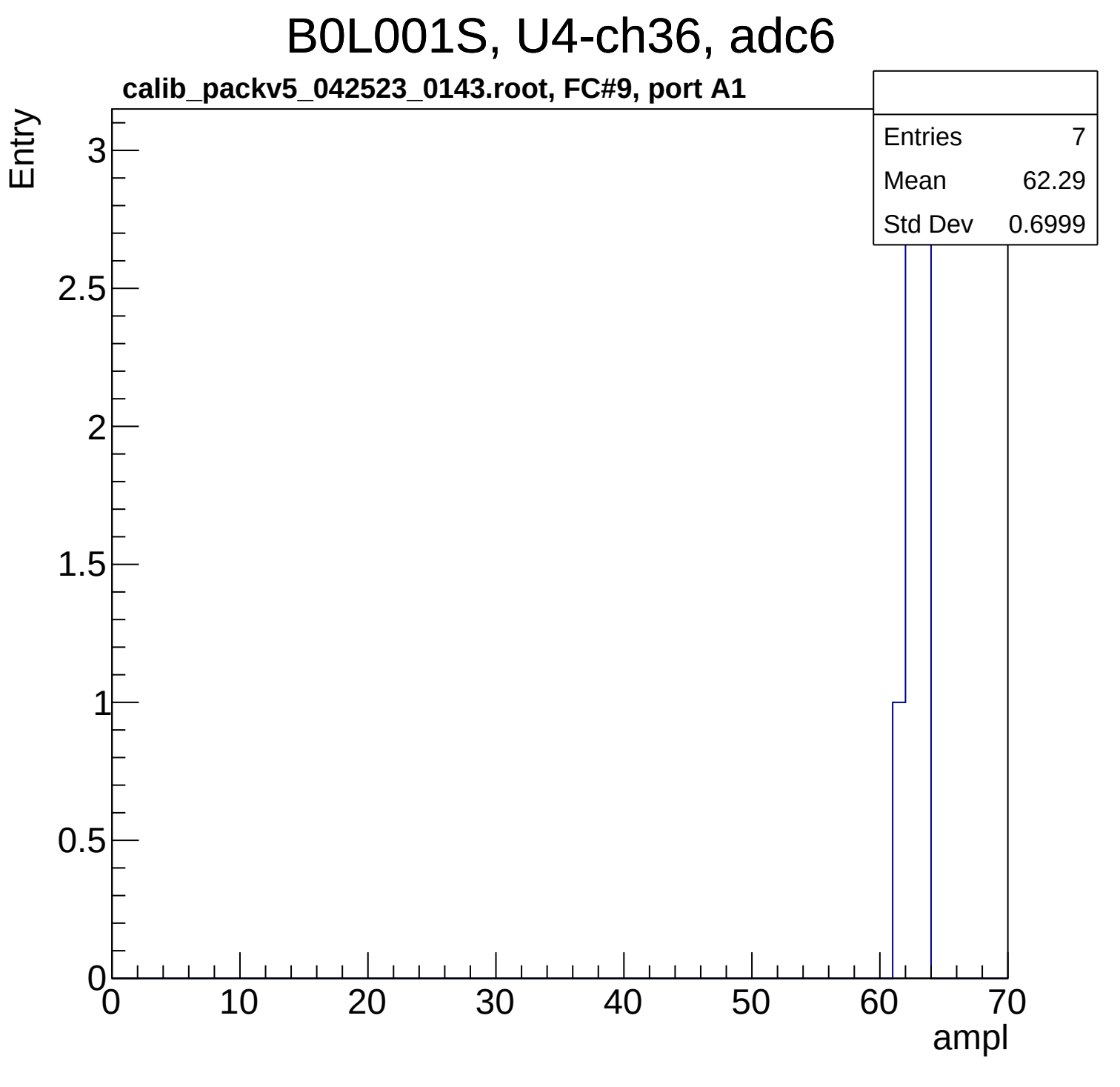
B0L001S, U4-ch36, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
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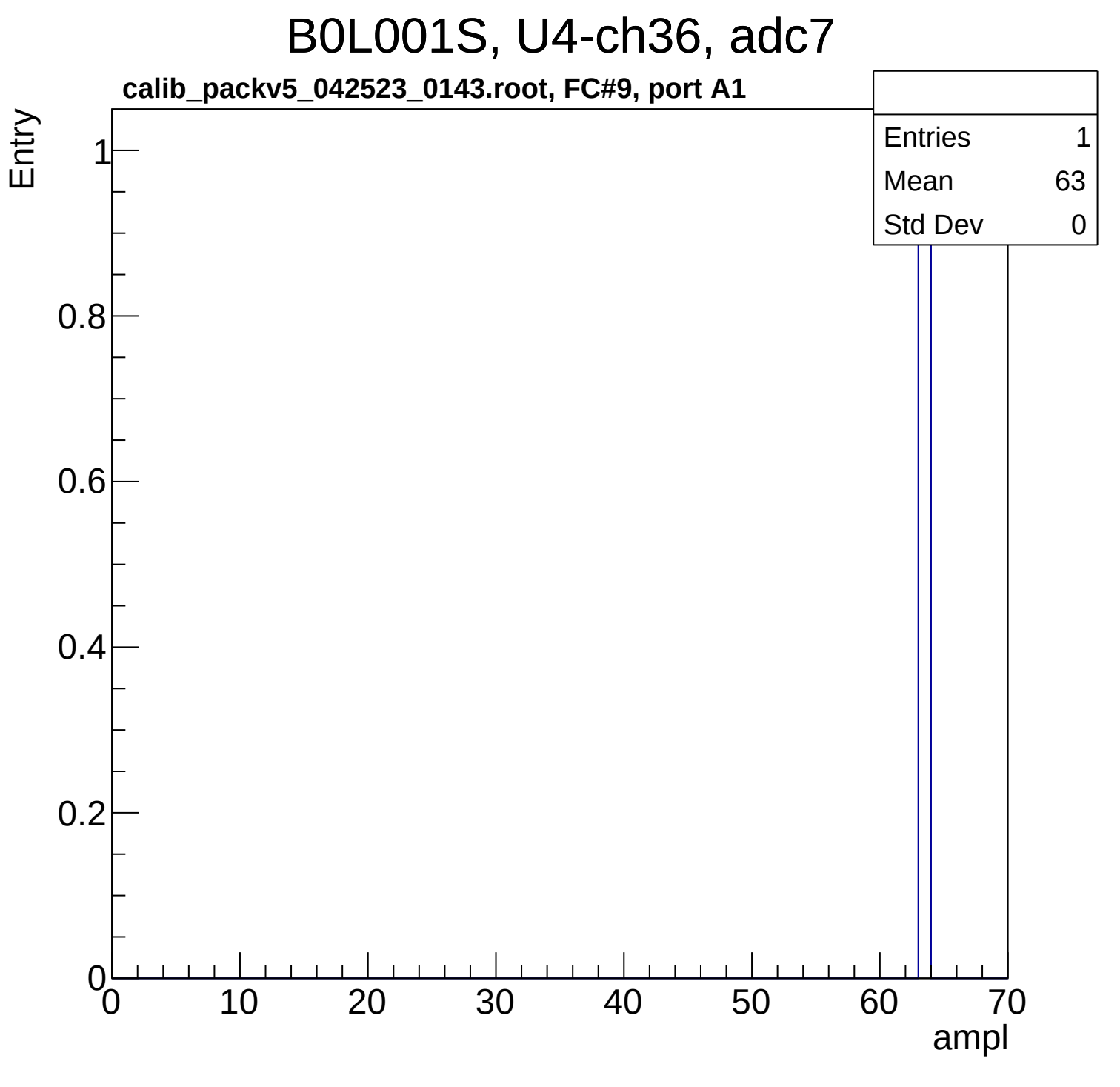
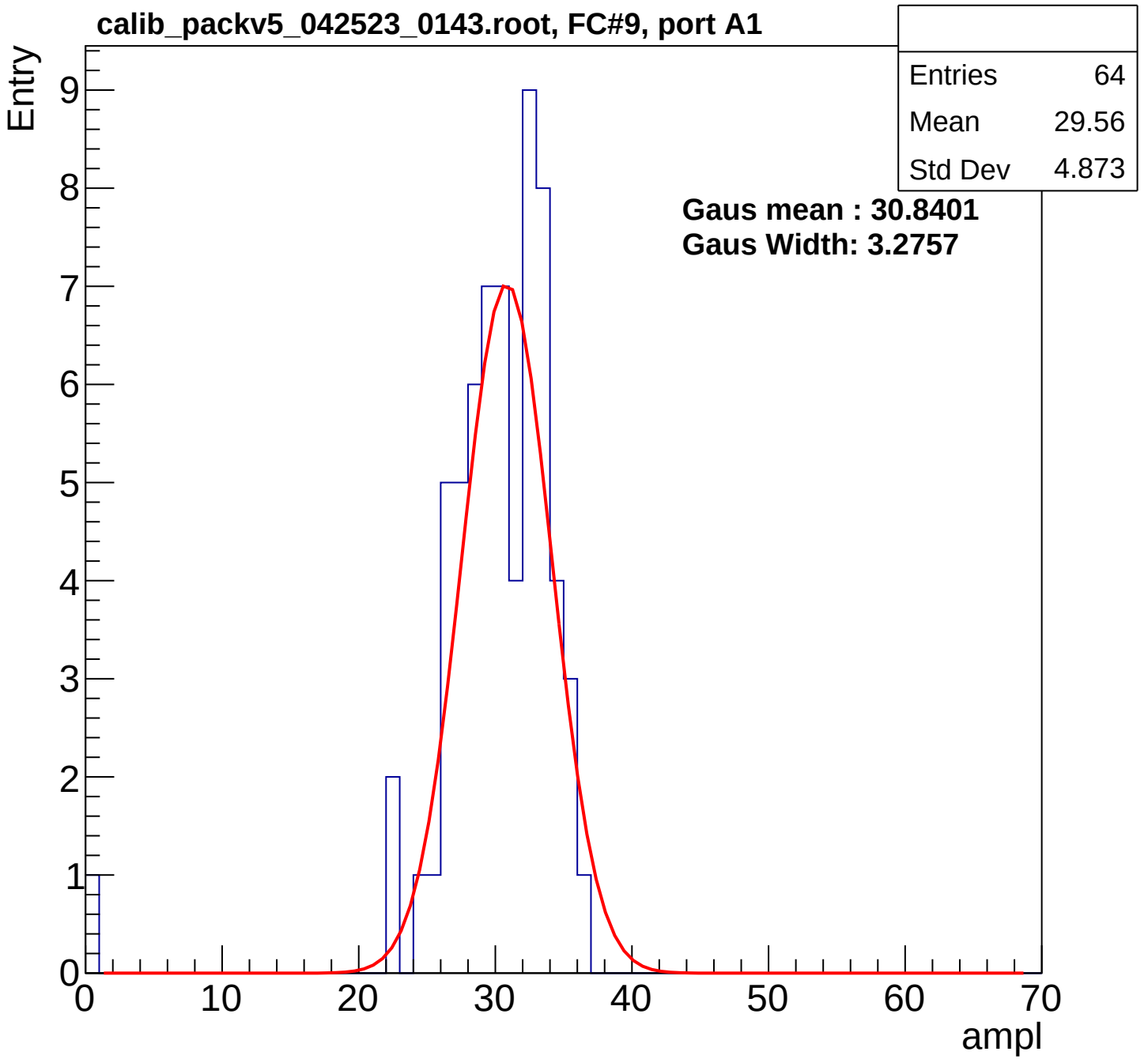
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U4-ch36, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch37, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 29.56
Std Dev 4.873
Gaus mean : 30.8401
Gaus Width: 3.2757
Entry
ampl
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2
3
4
5
6
7
8
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0
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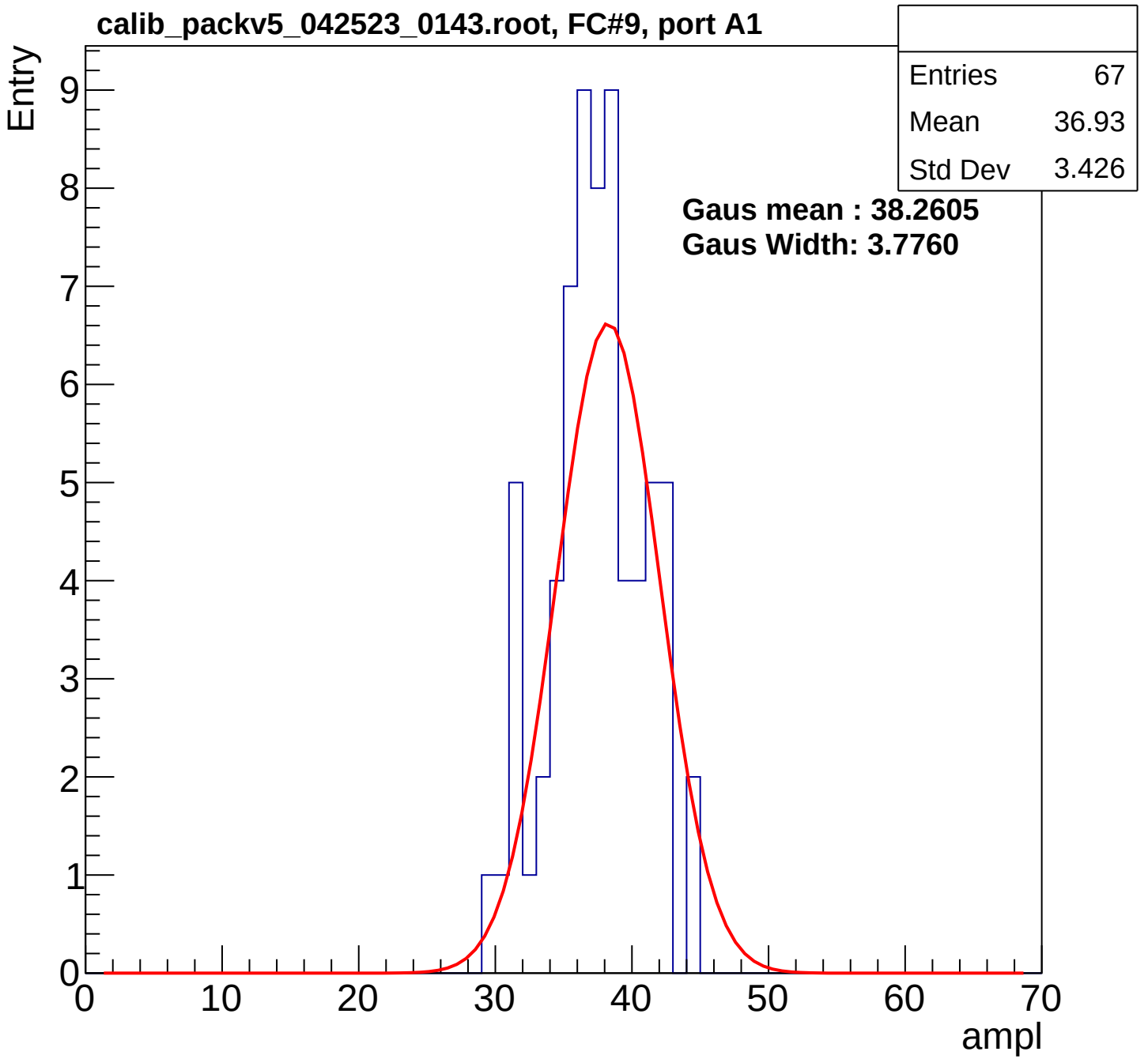
B0L001S, U4-ch37, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.93
Std Dev 3.426
Gaus mean : 38.2605
Gaus Width: 3.7760
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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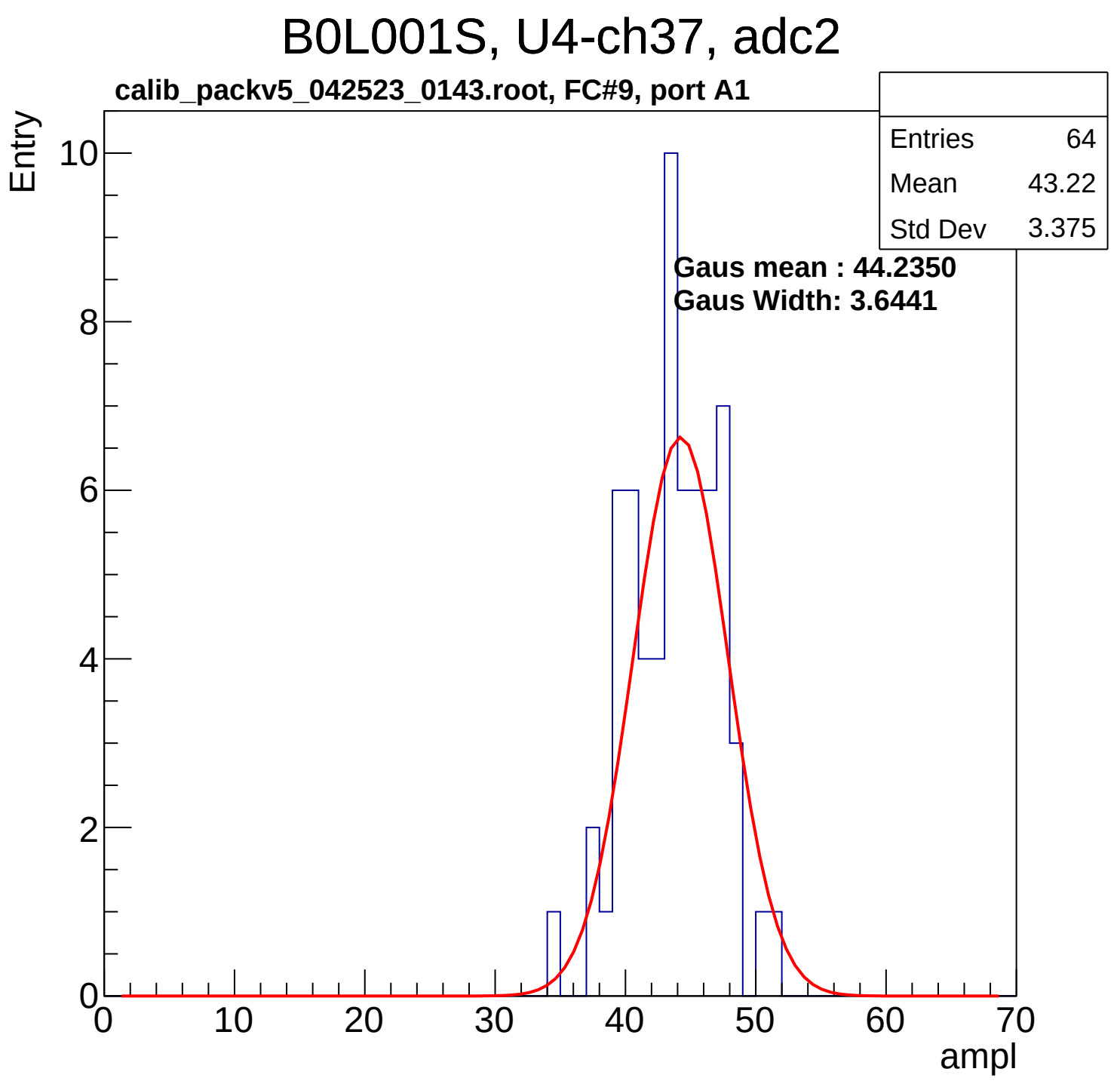
B0L001S, U4-ch37, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.22
Std Dev 3.375
Gaus mean : 44.2350
Gaus Width: 3.6441
Entry
ampl
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4
6
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10
0
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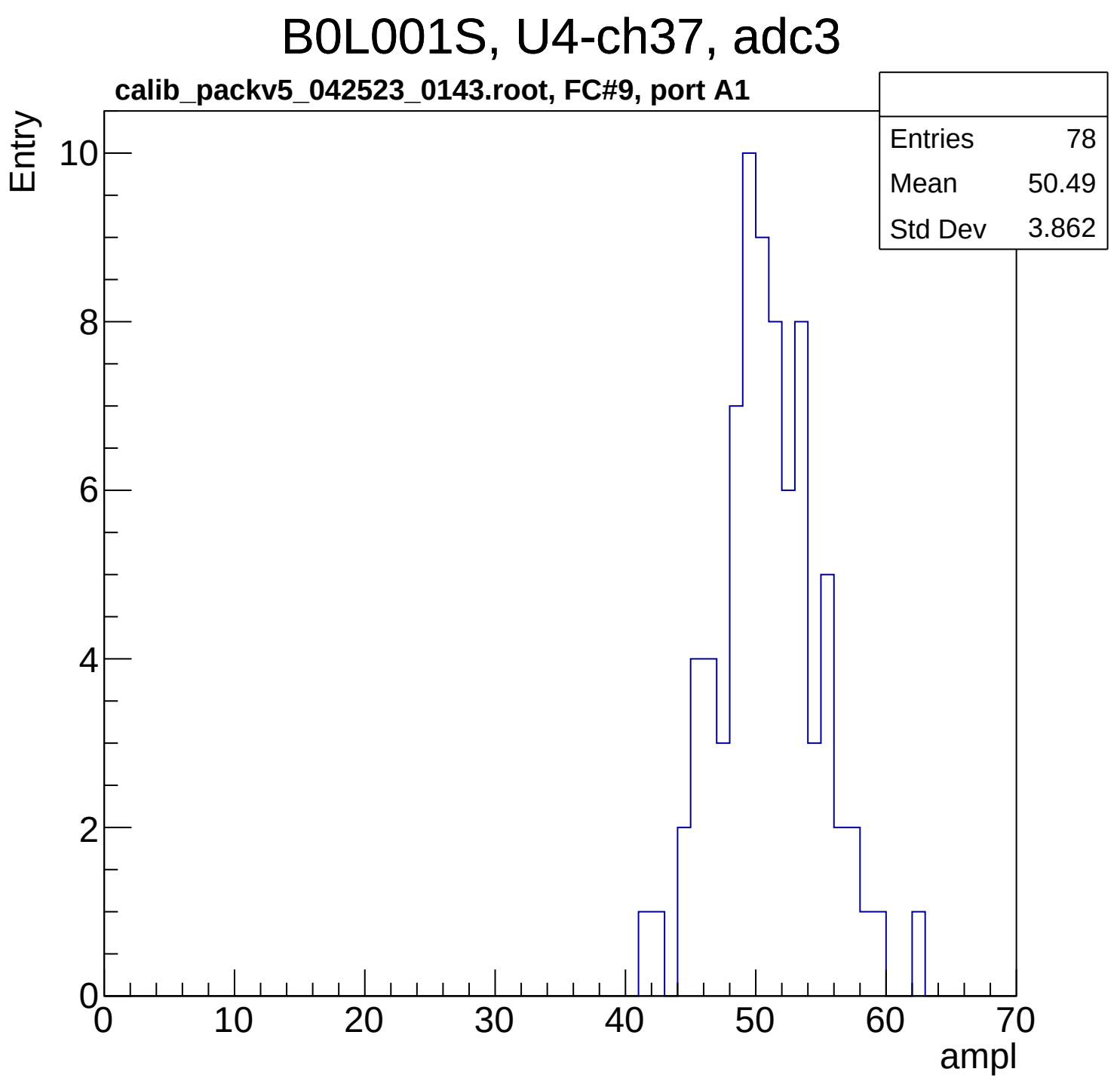
60
70


B0L001S, U4-ch37, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 50.49
Std Dev 3.862
Entry
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2
4
6
8
10
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10
20
30
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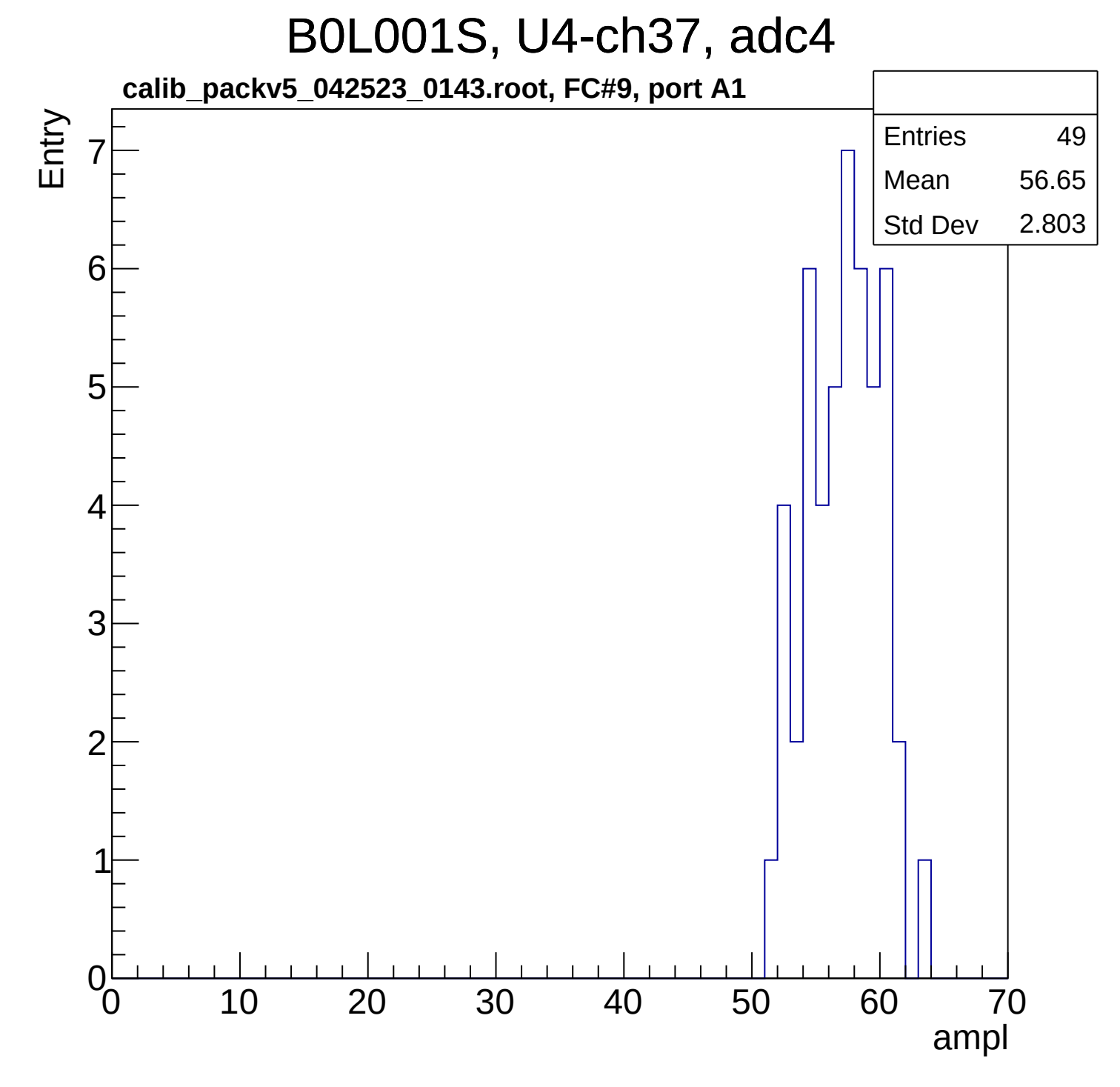
50
60
70
ampl


B0L001S, U4-ch37, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 56.65
Std Dev 2.803
Entry
ampl
0
1
2
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4
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6
7
0
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60
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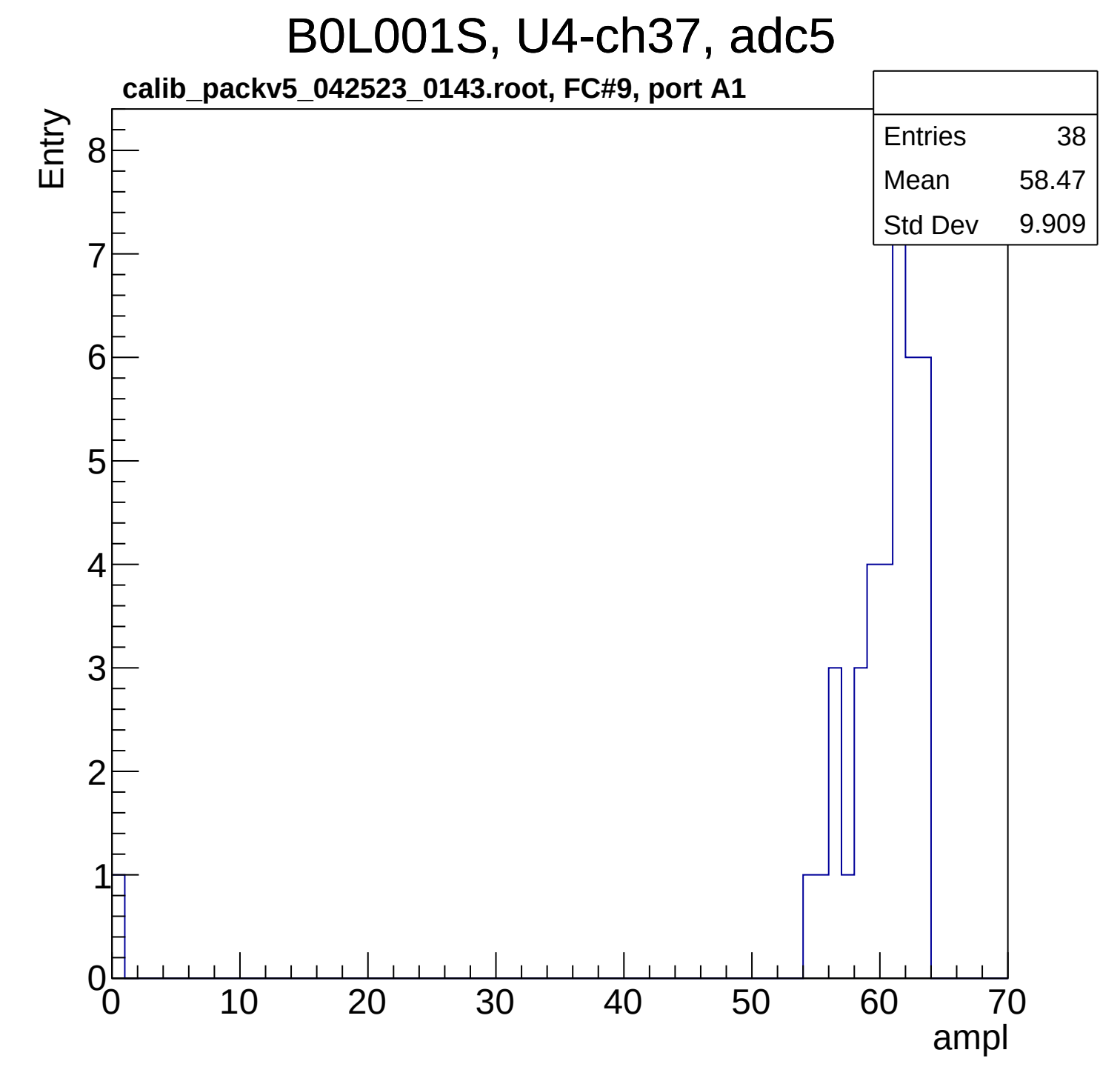
B0L001S, U4-ch37, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.47
Std Dev 9.909
Entry
ampl
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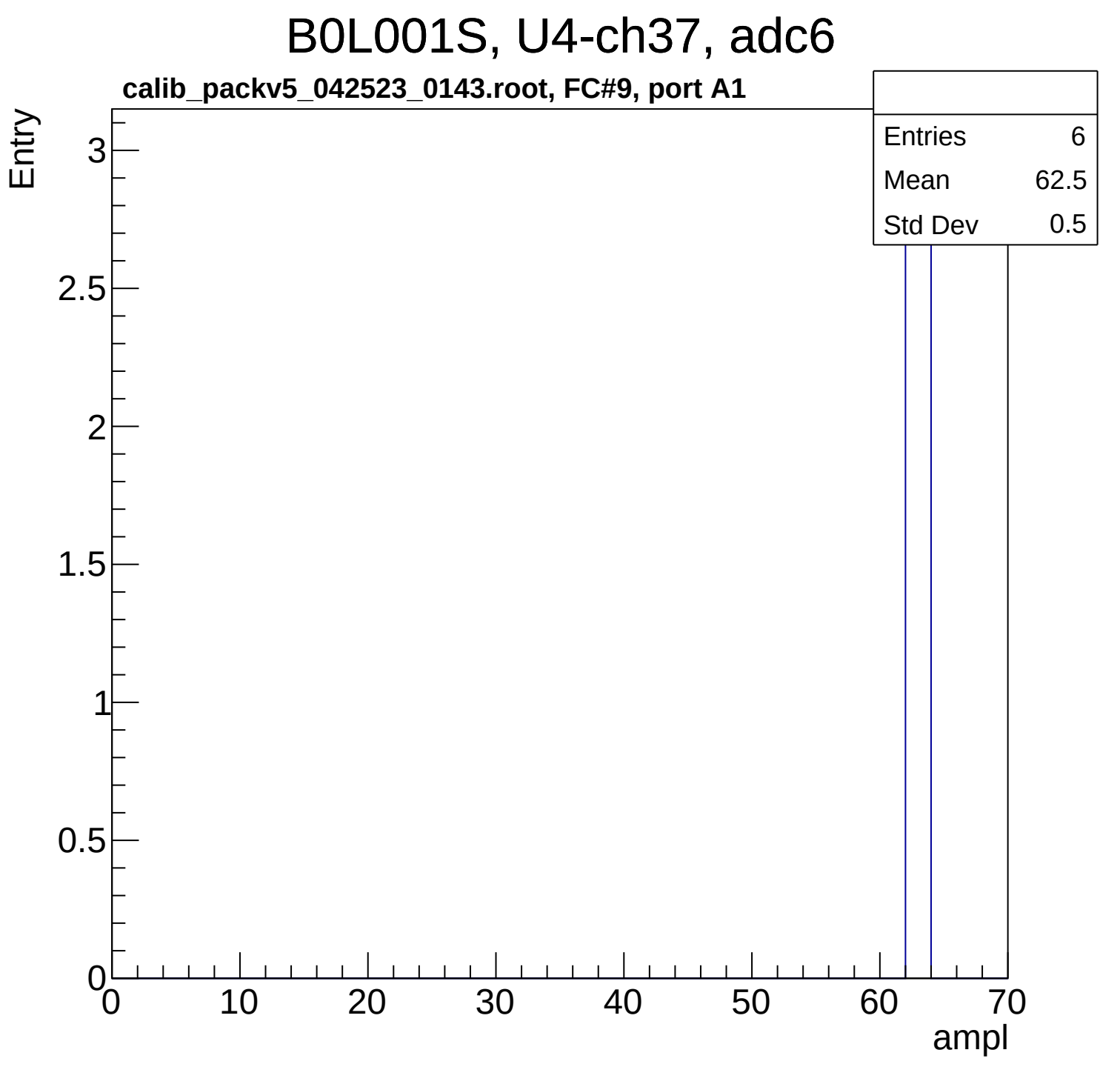
B0L001S, U4-ch37, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.5
Std Dev 0.5
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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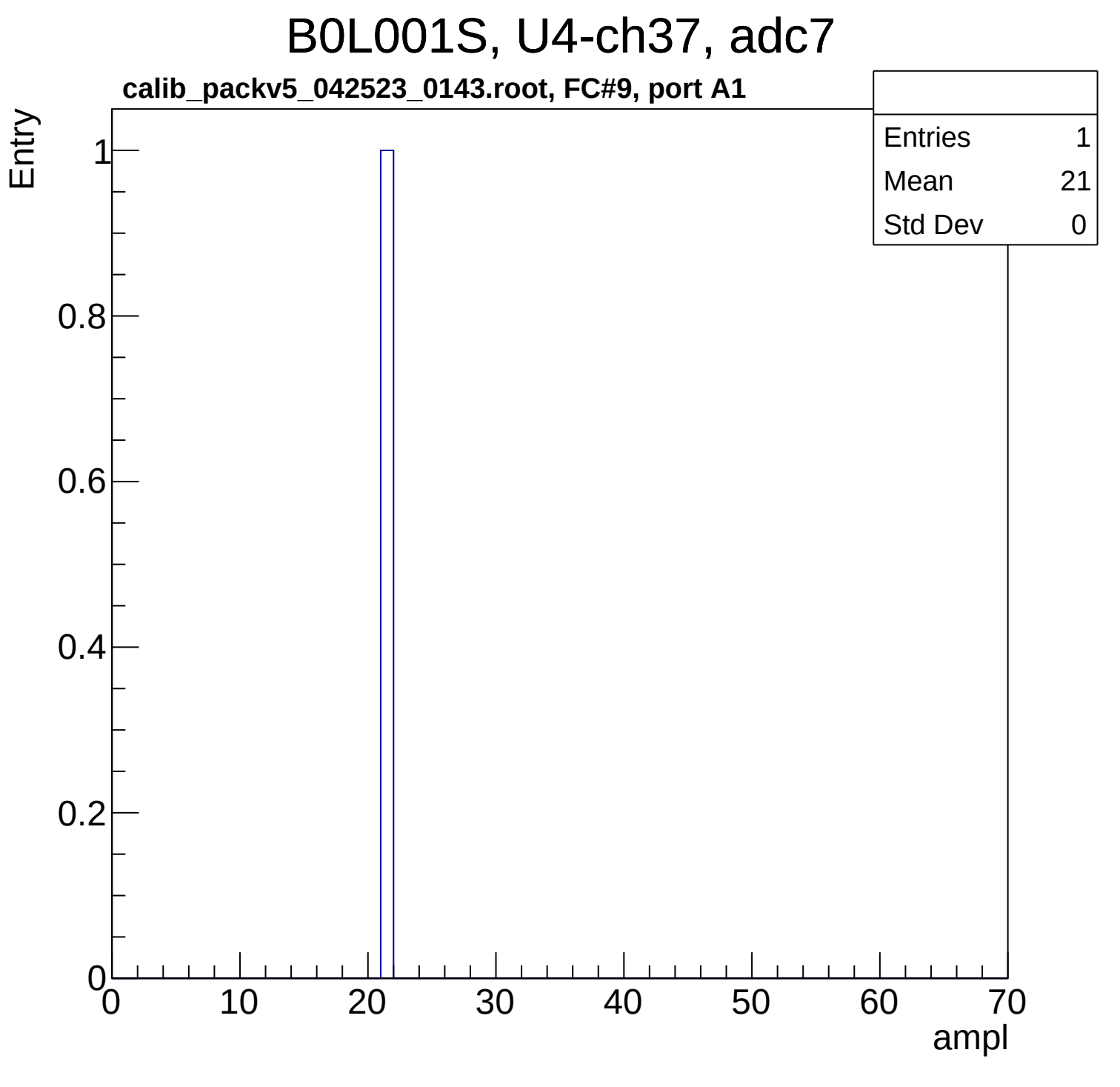
B0L001S, U4-ch37, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
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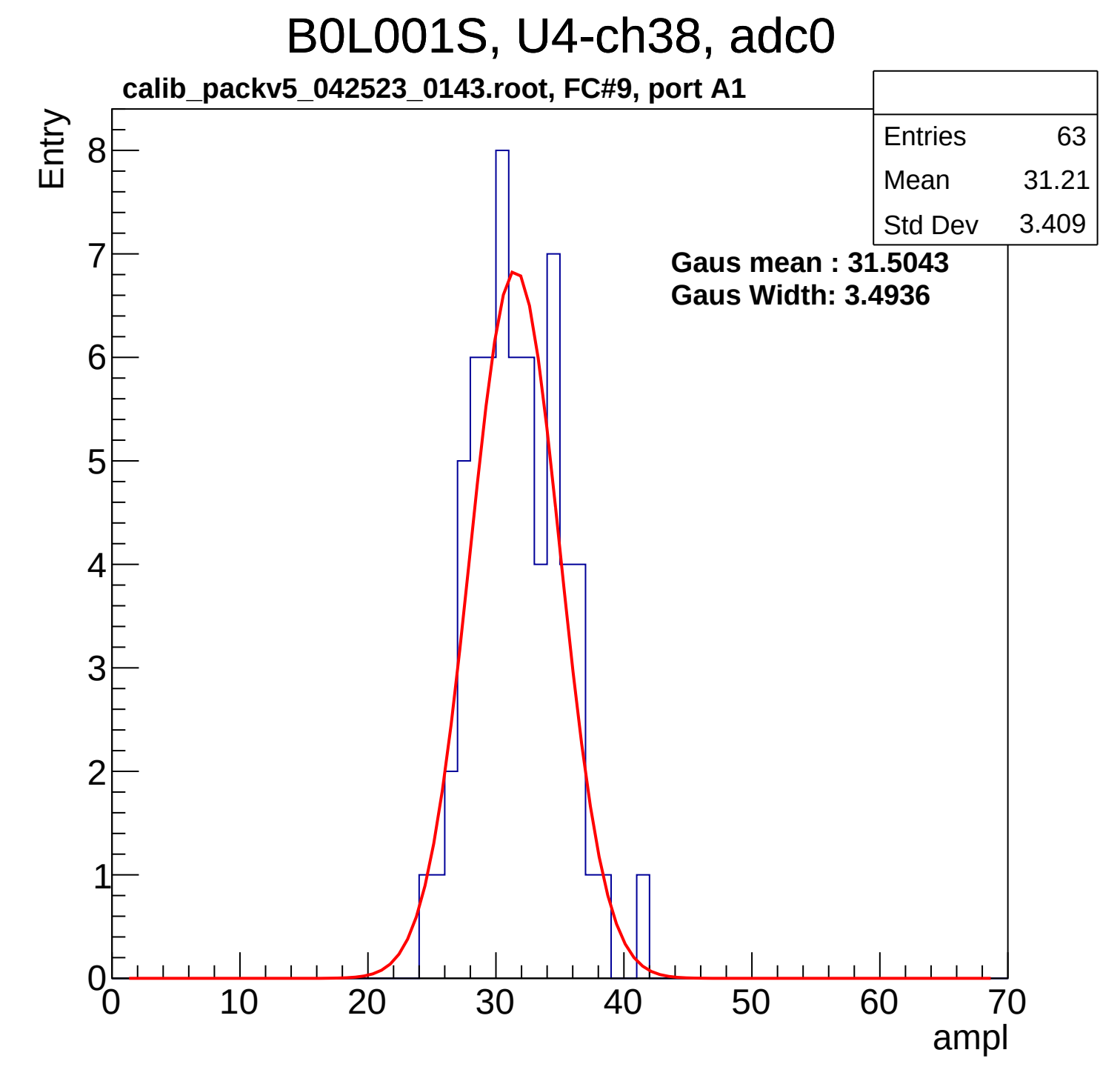
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch38, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.21
Std Dev 3.409
Gaus mean : 31.5043
Gaus Width: 3.4936
Entry
ampl
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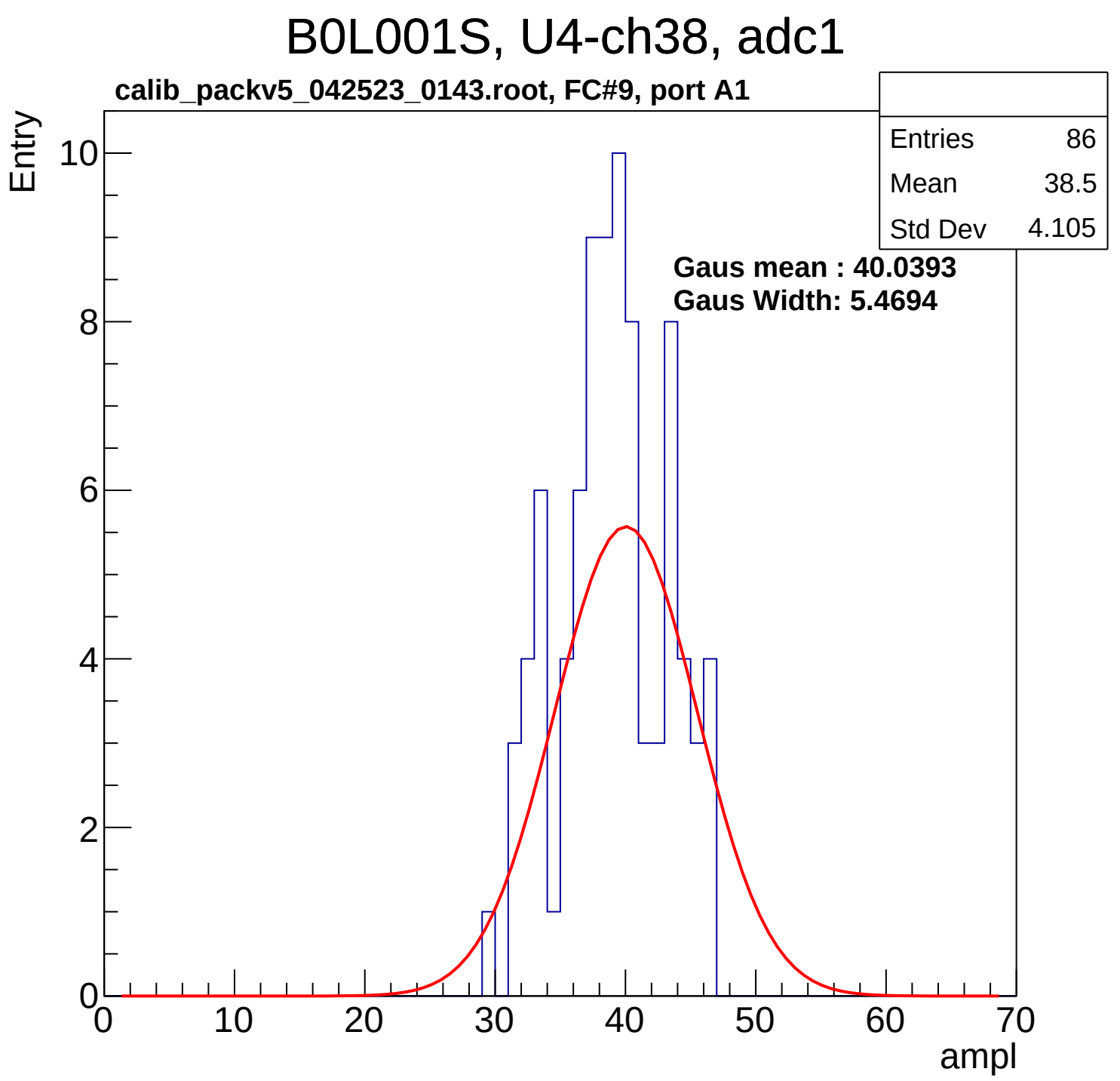
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch38, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 38.5
Std Dev 4.105
Gaus mean : 40.0393
Gaus Width: 5.4694
Entry
ampl
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6
8
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0
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20
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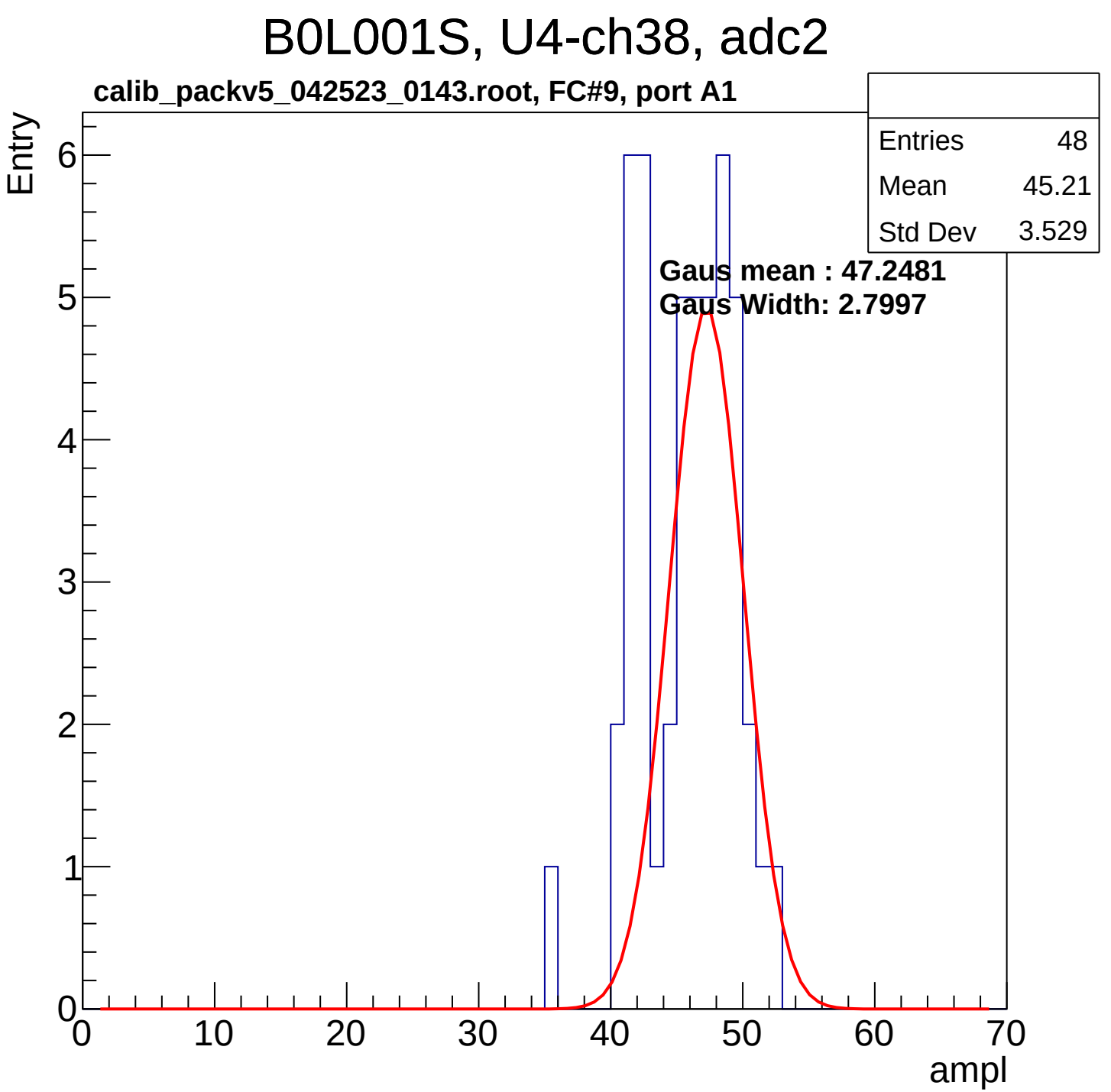
B0L001S, U4-ch38, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 45.21
Std Dev 3.529
Gaus mean : 47.2481
Gaus Width: 2.7997
Entry
ampl
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2
3
4
5
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0
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20
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60
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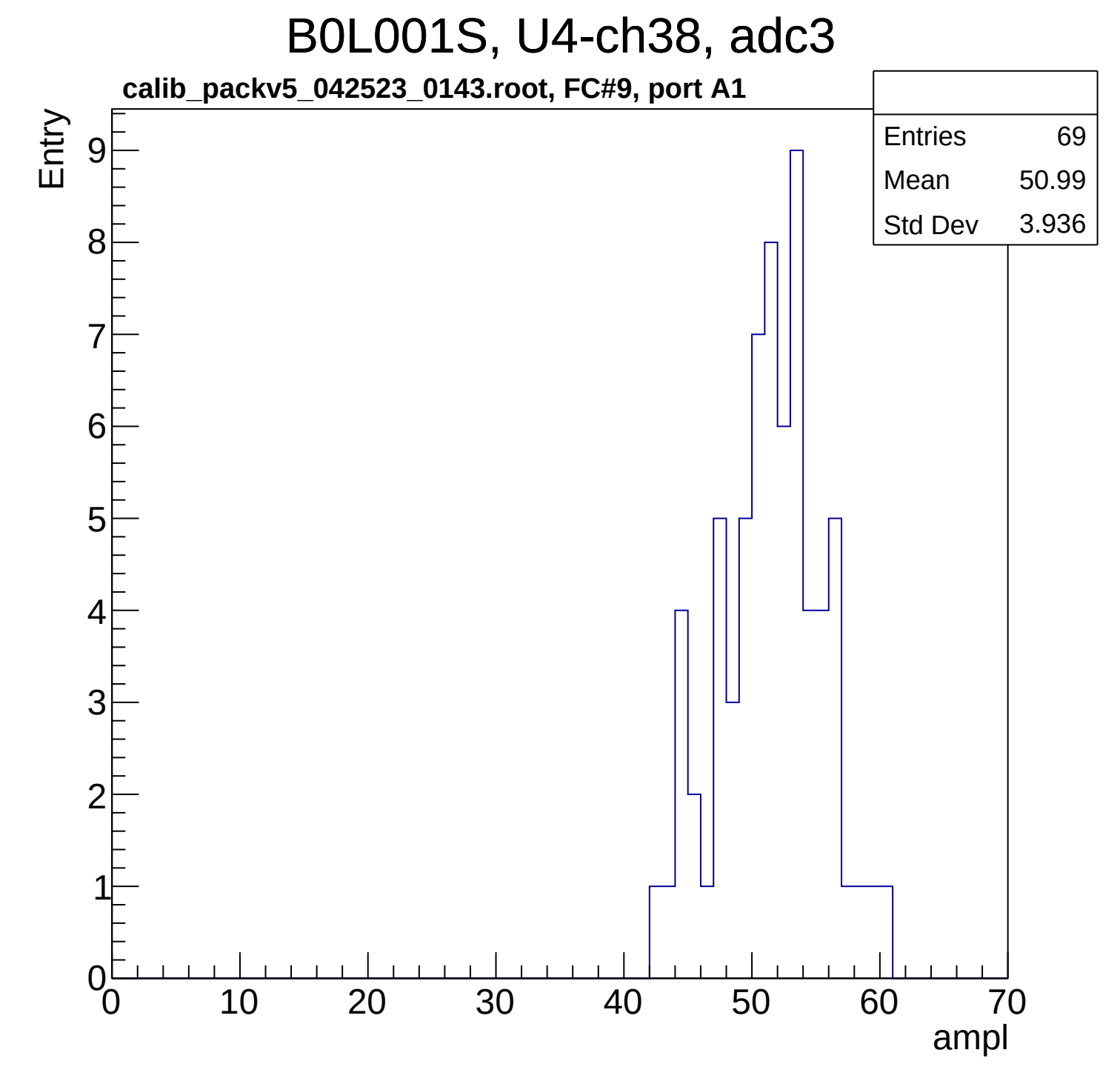
B0L001S, U4-ch38, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.99
Std Dev 3.936
Entry
ampl
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2
3
4
5
6
7
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0
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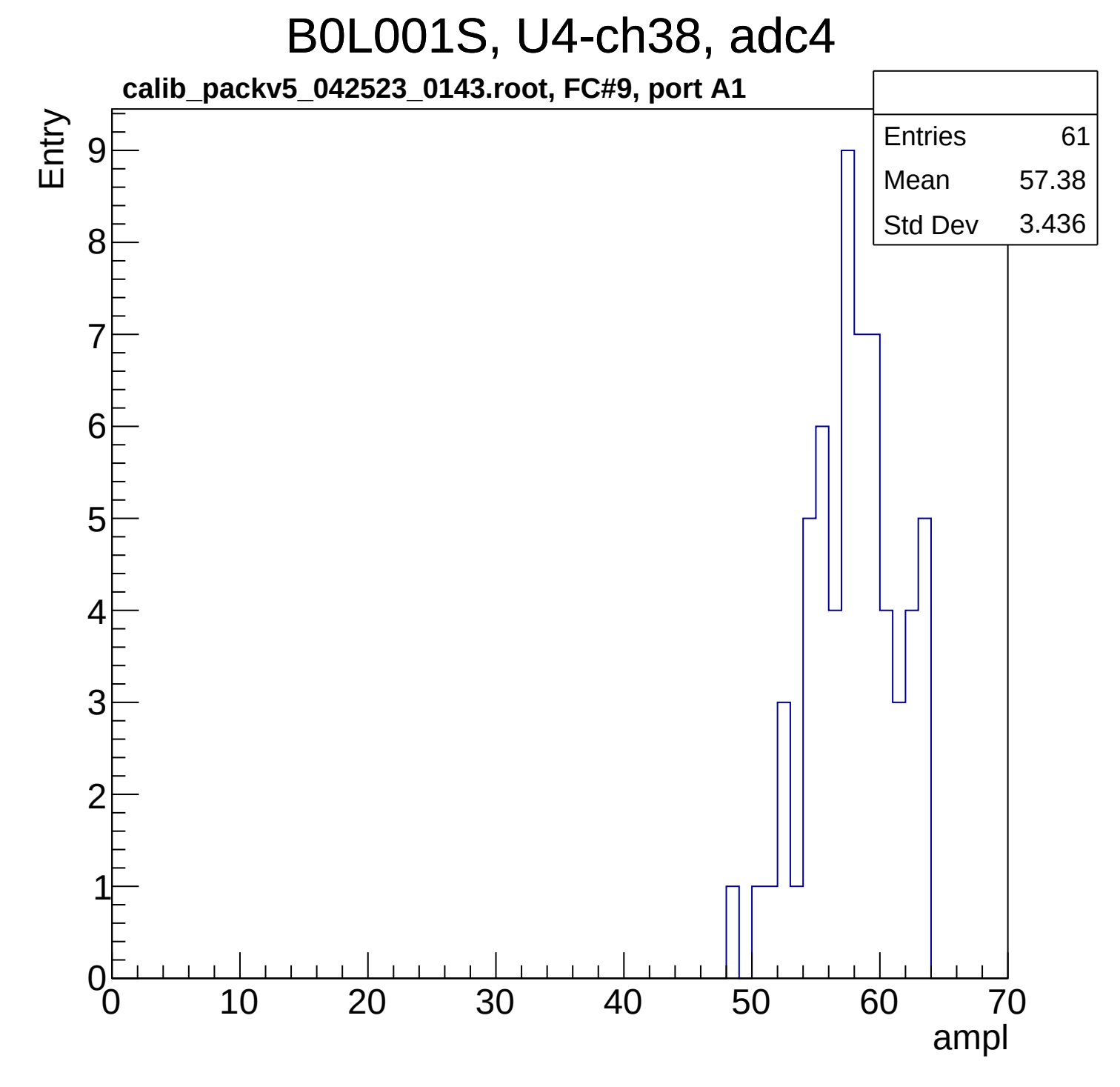
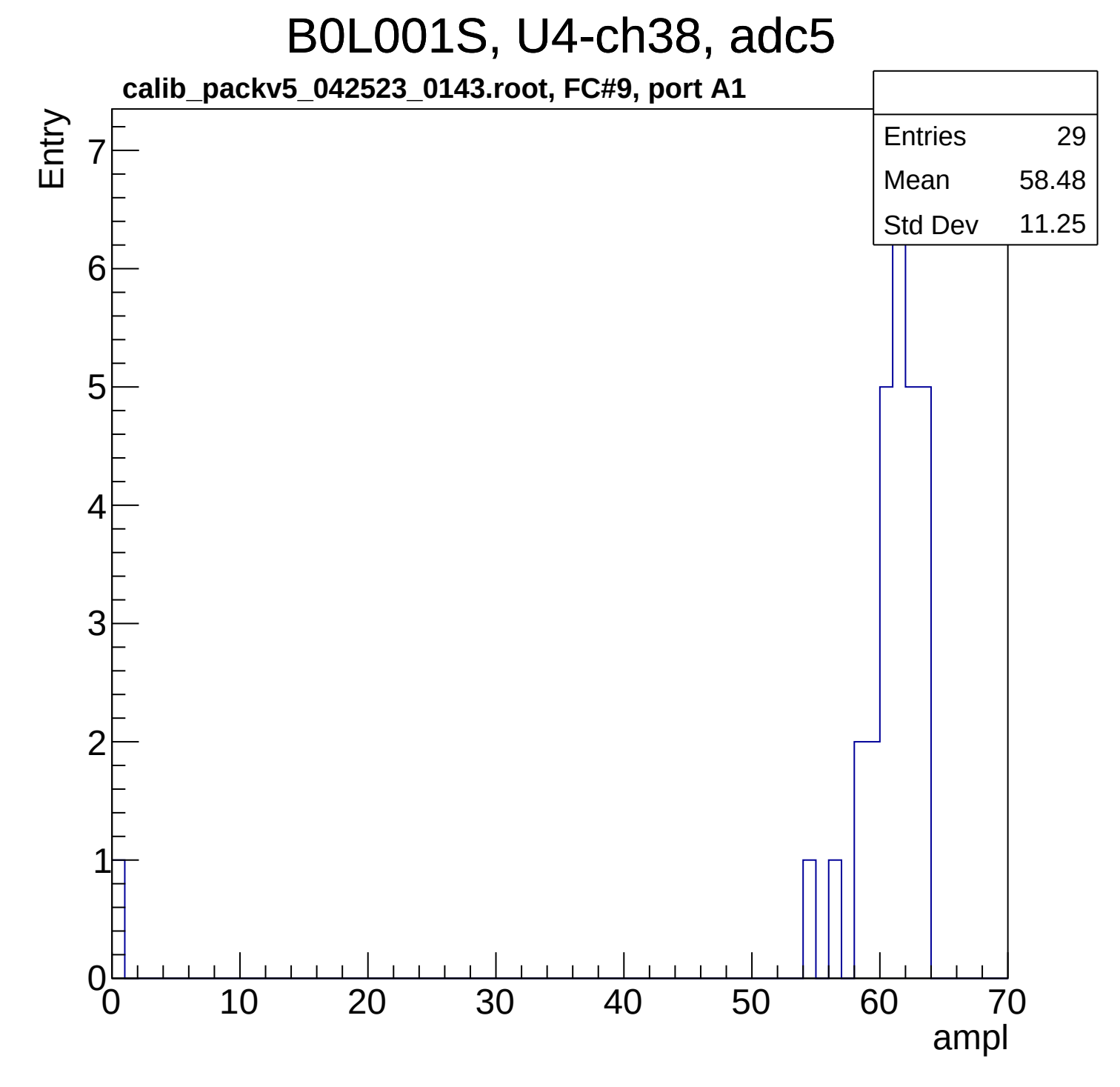
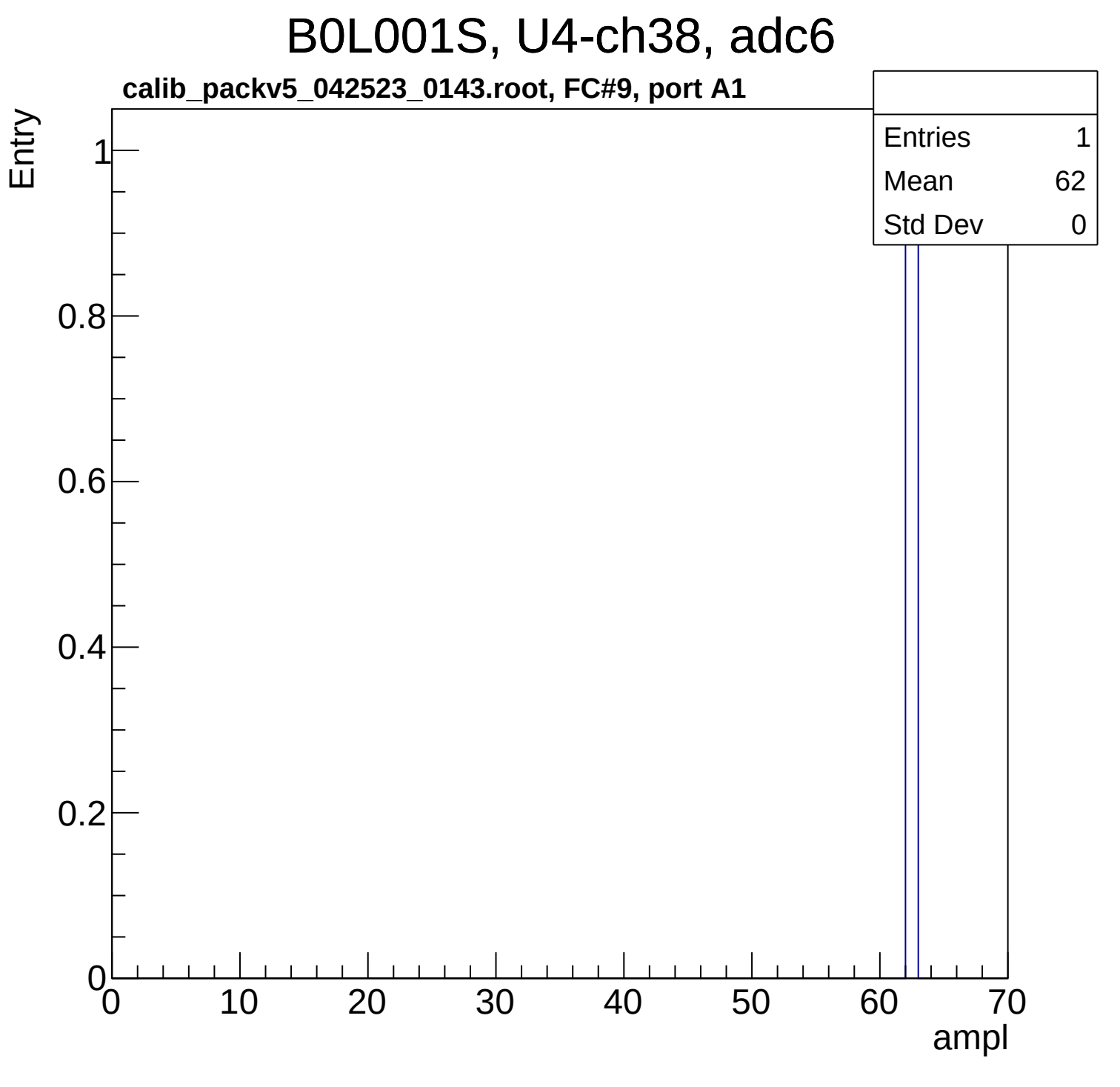
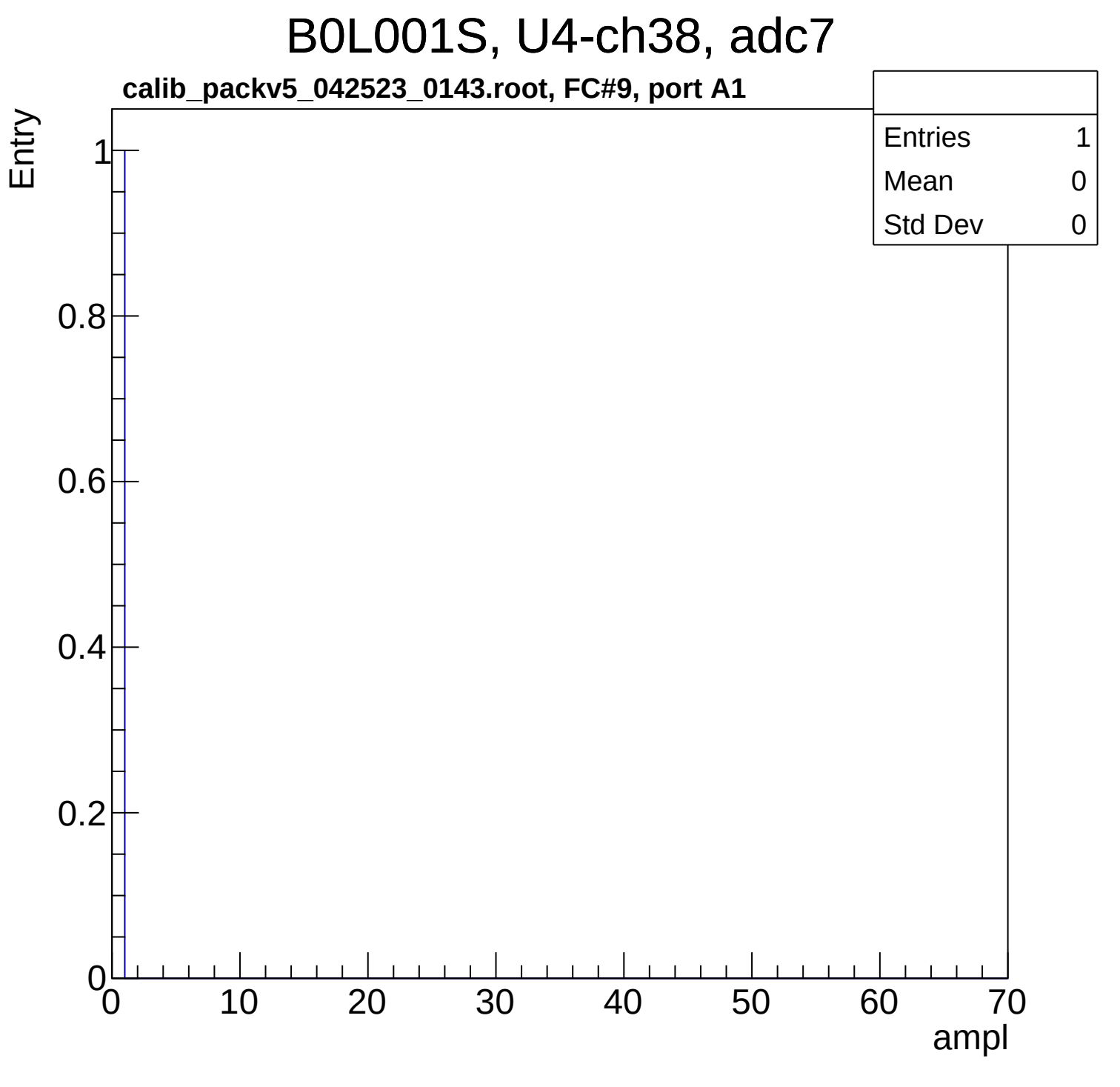
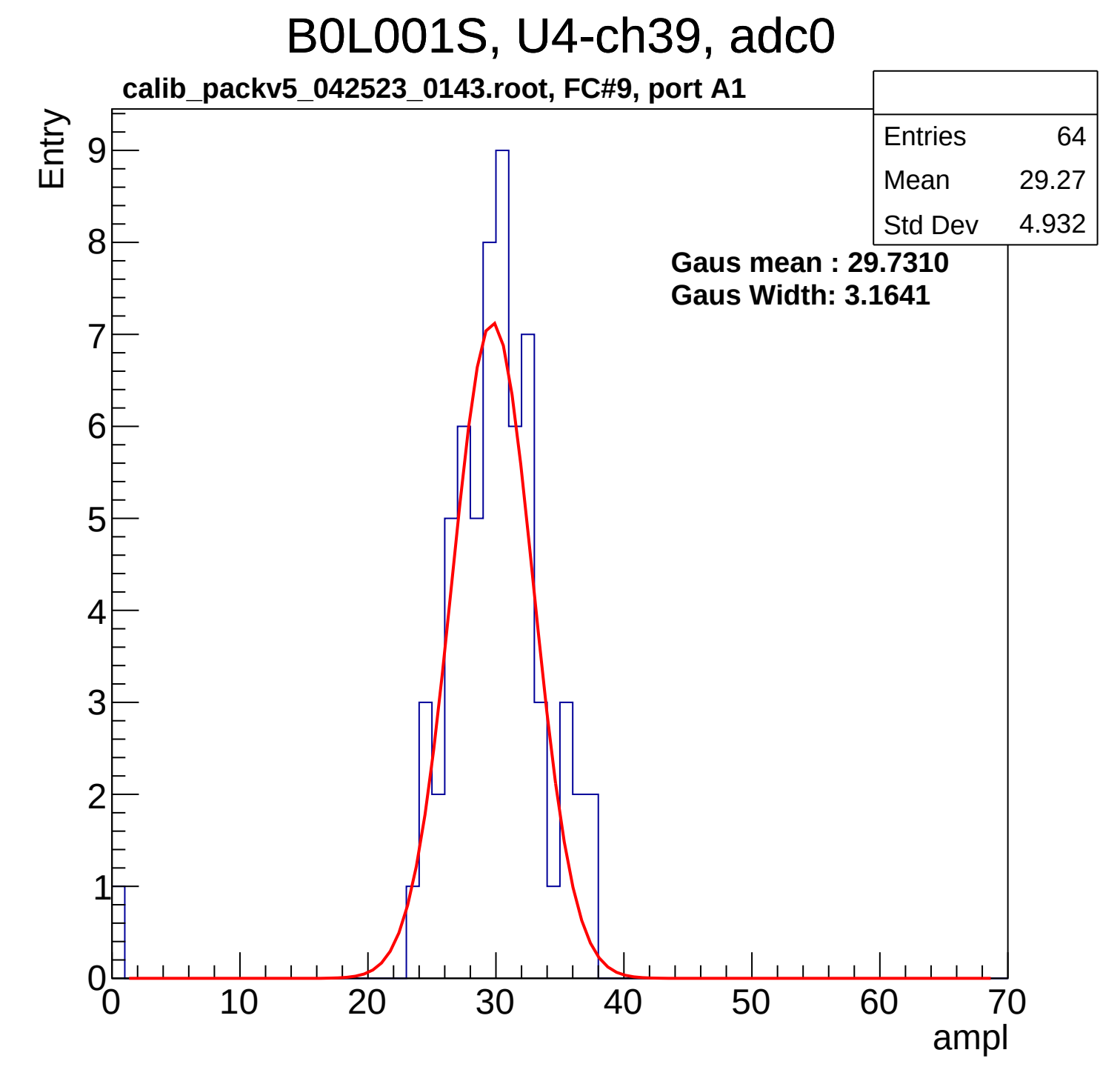

B0L001S, U4-ch38, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.38
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch38, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.48
Std Dev 11.25
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch38, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch38, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch39, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 29.27
Std Dev 4.932
Gaus mean : 29.7310
Gaus Width: 3.1641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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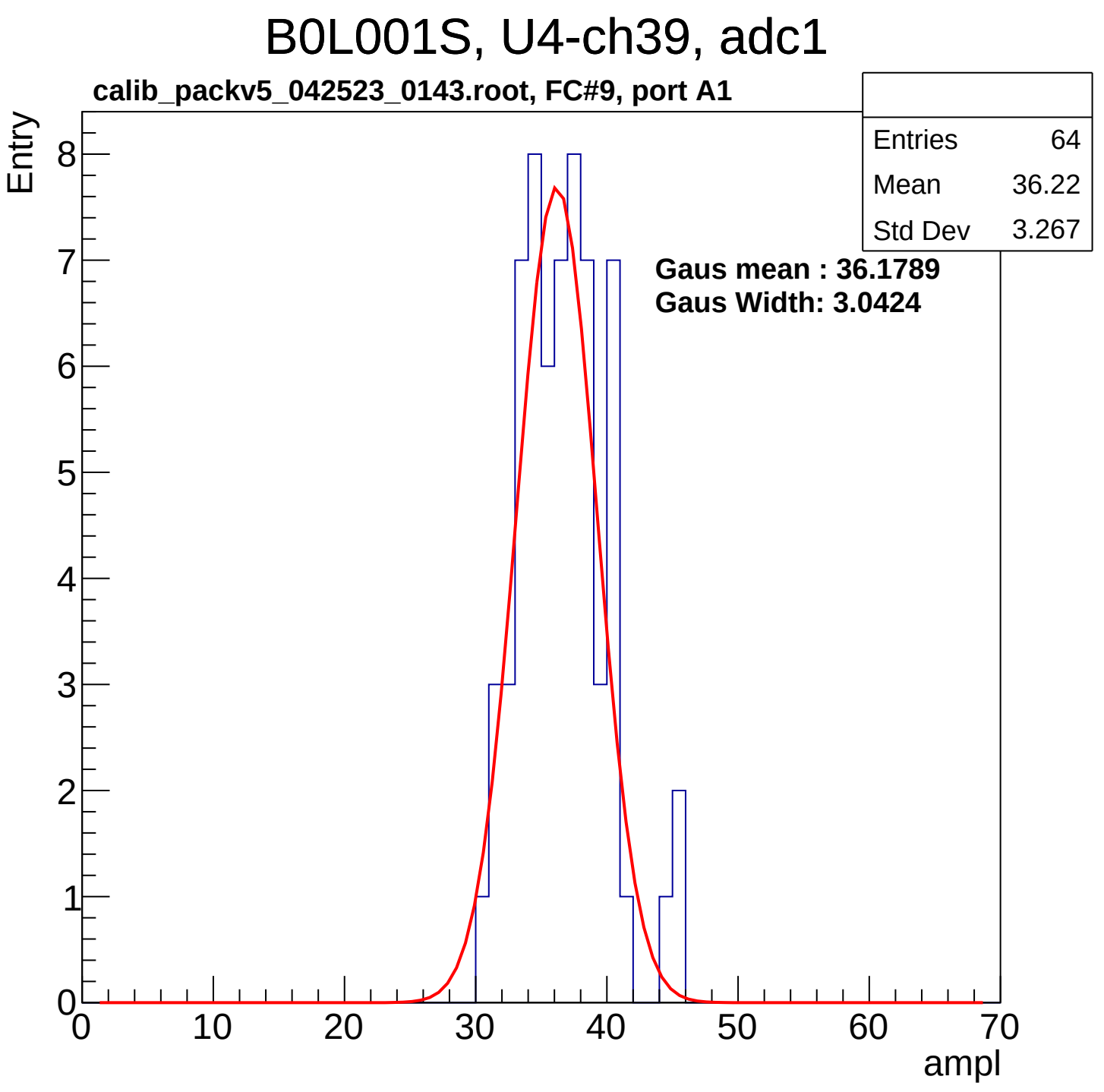
B0L001S, U4-ch39, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.22
Std Dev 3.267
Gaus mean : 36.1789
Gaus Width: 3.0424
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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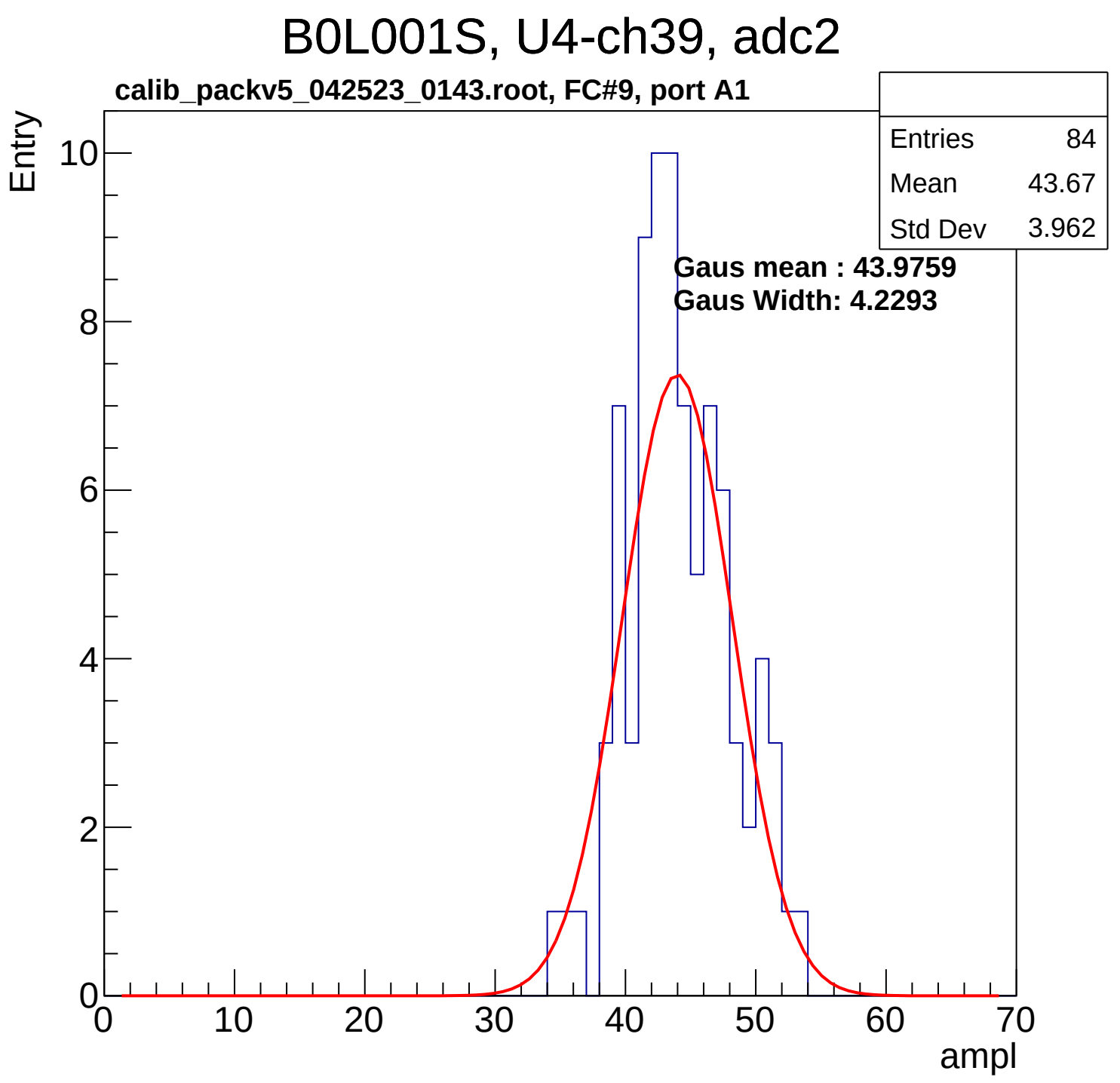
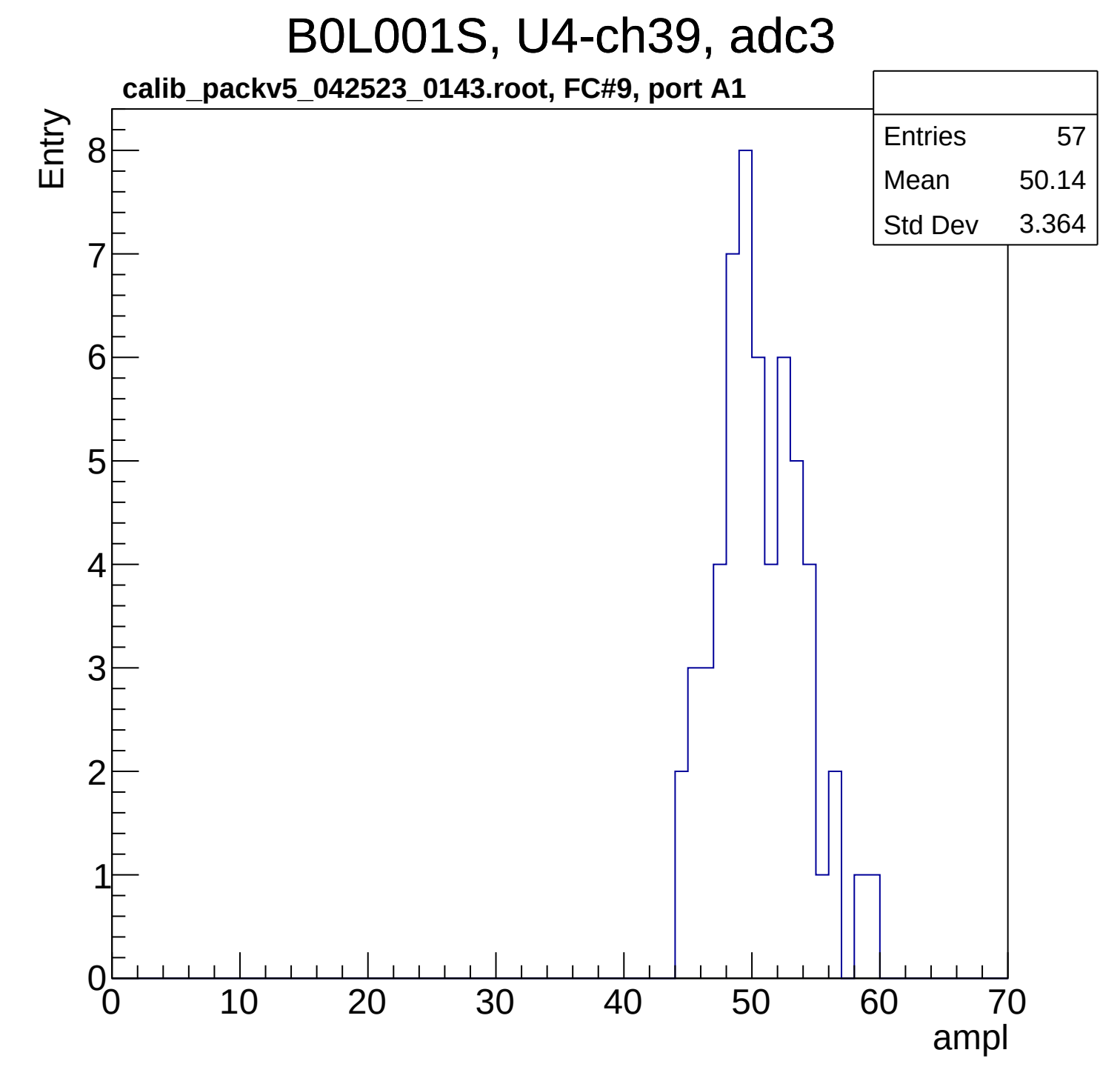
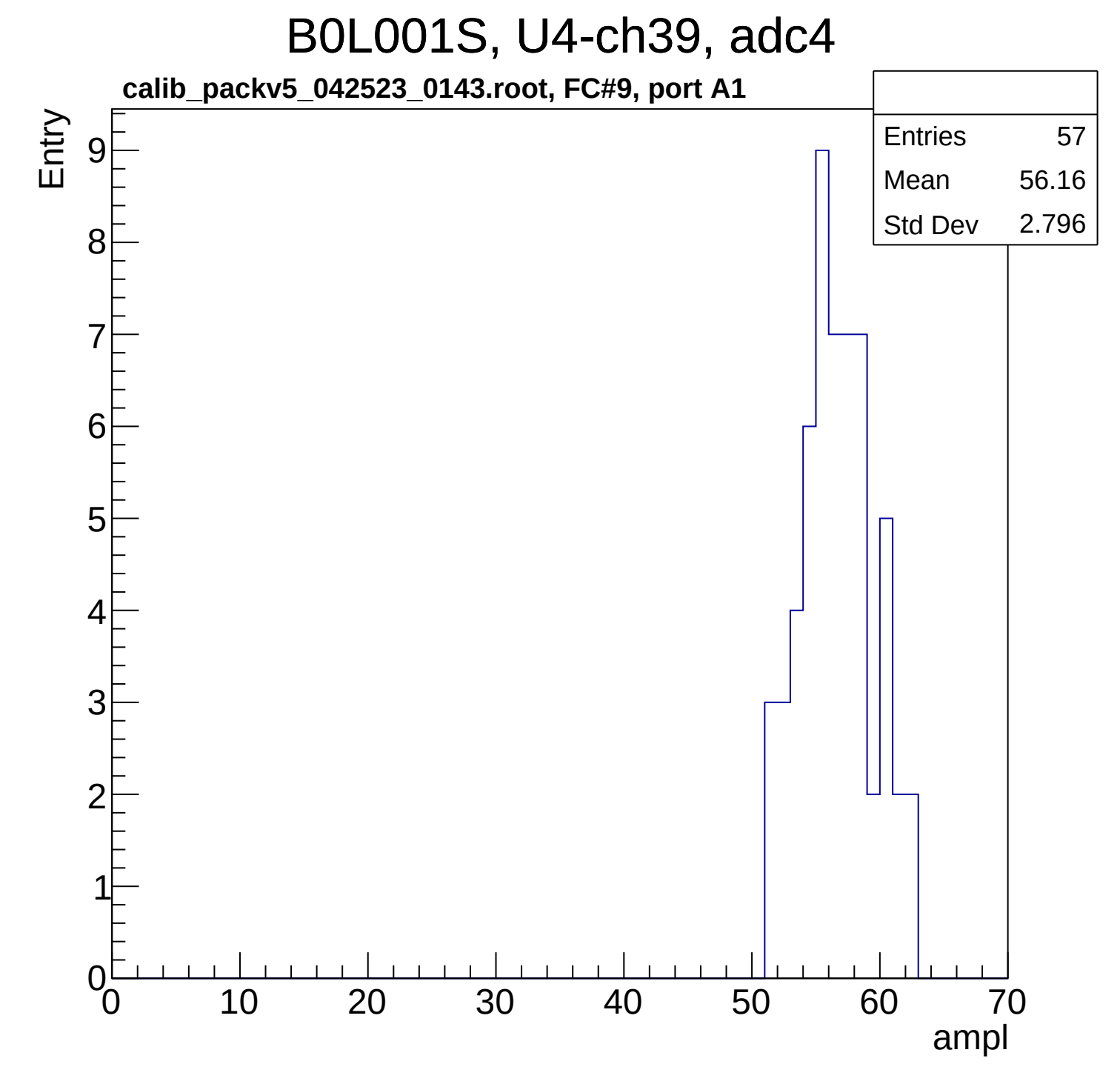
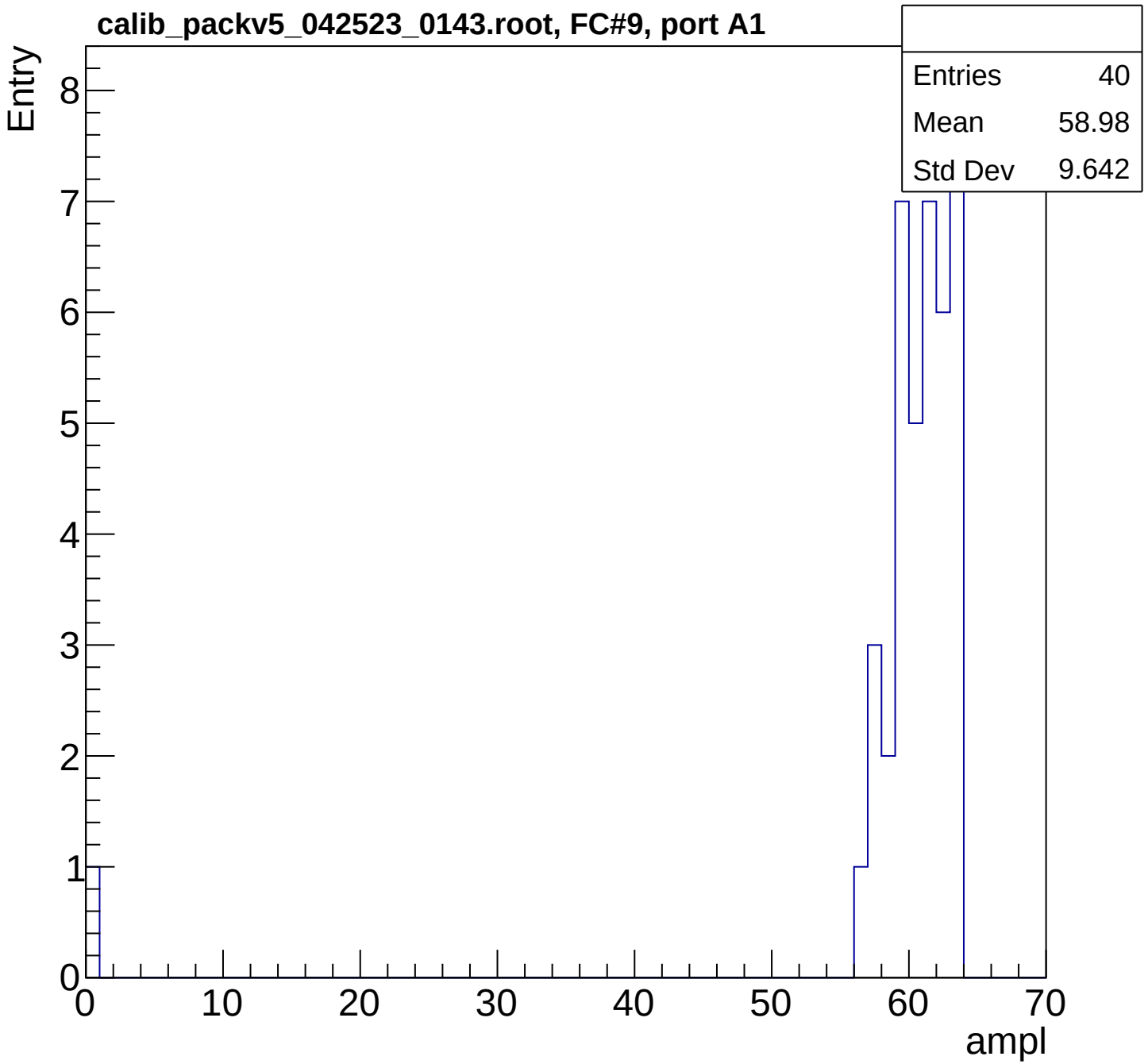
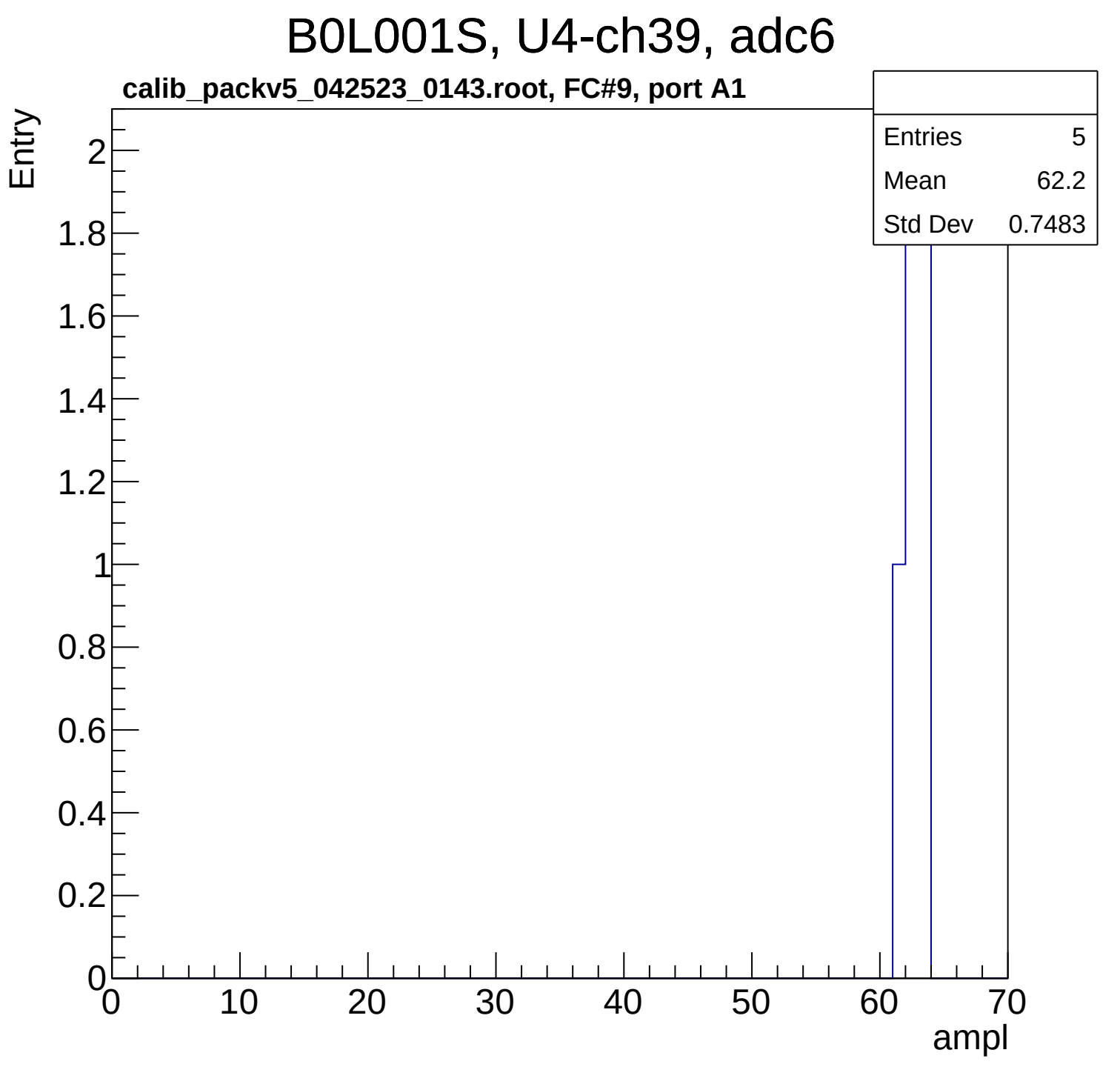
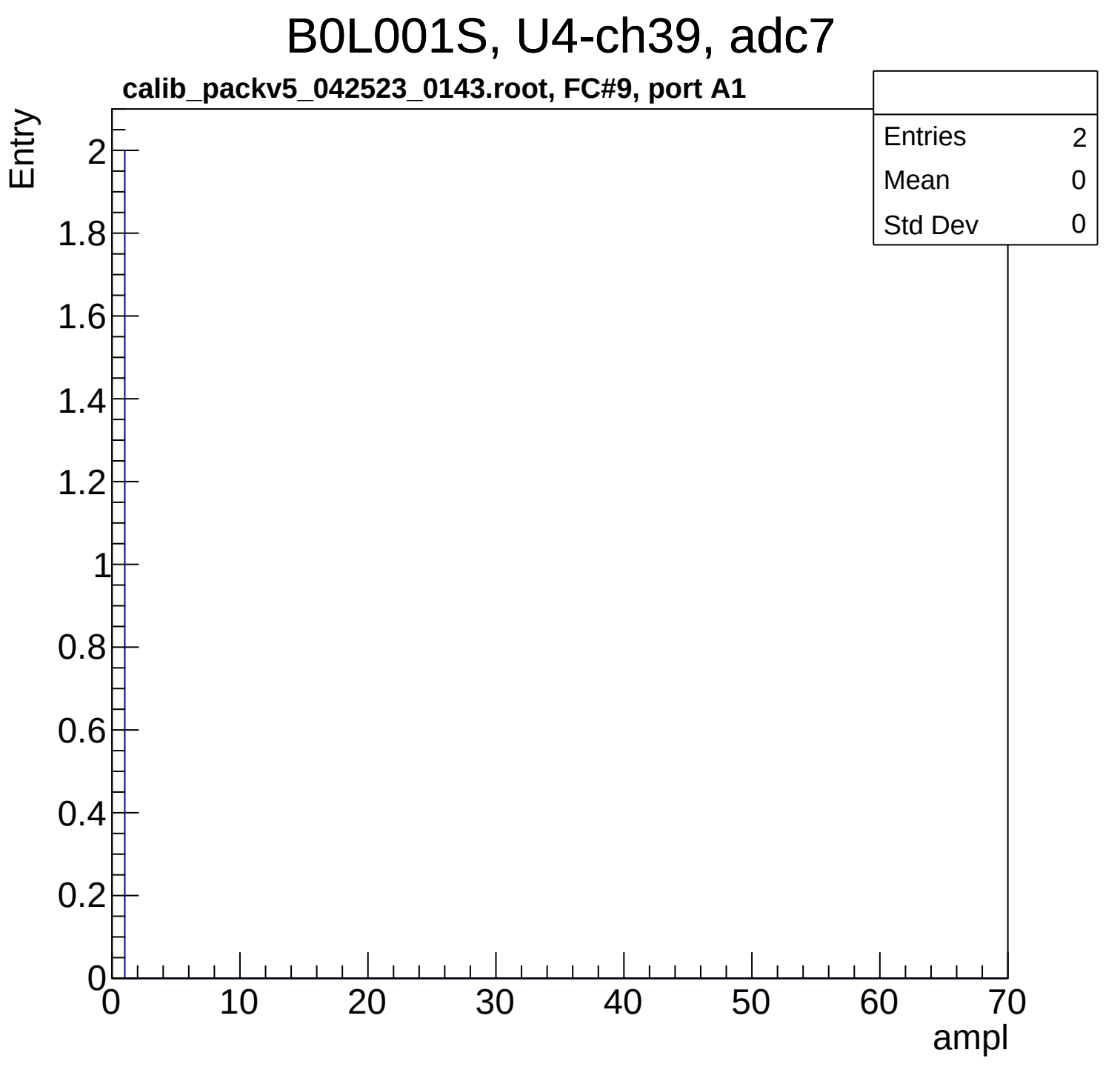

B0L001S, U4-ch39, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 43.67
Std Dev 3.962
Gaus mean : 43.9759
Gaus Width: 4.2293
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

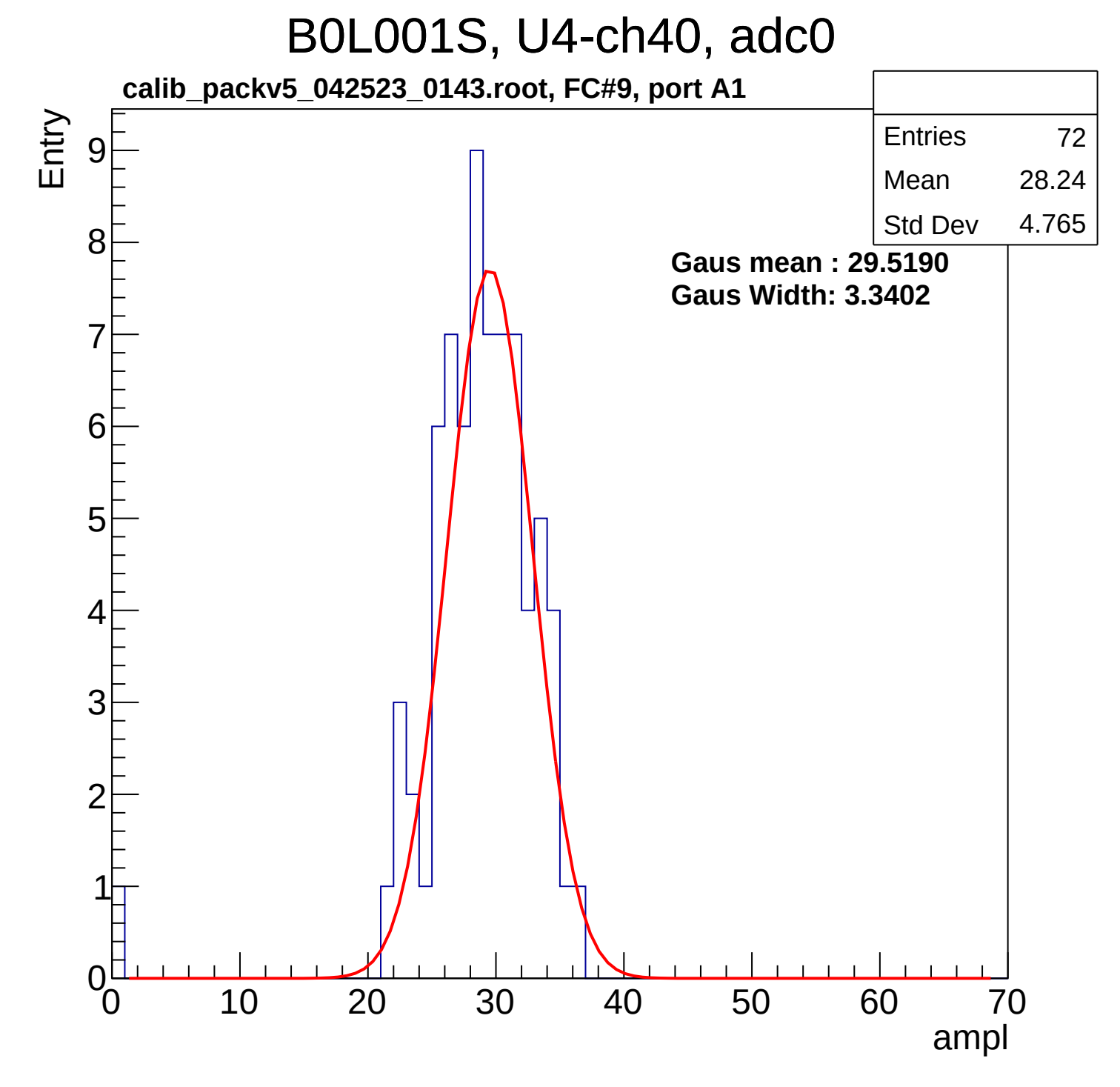

B0L001S, U4-ch39, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 50.14
Std Dev 3.364
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

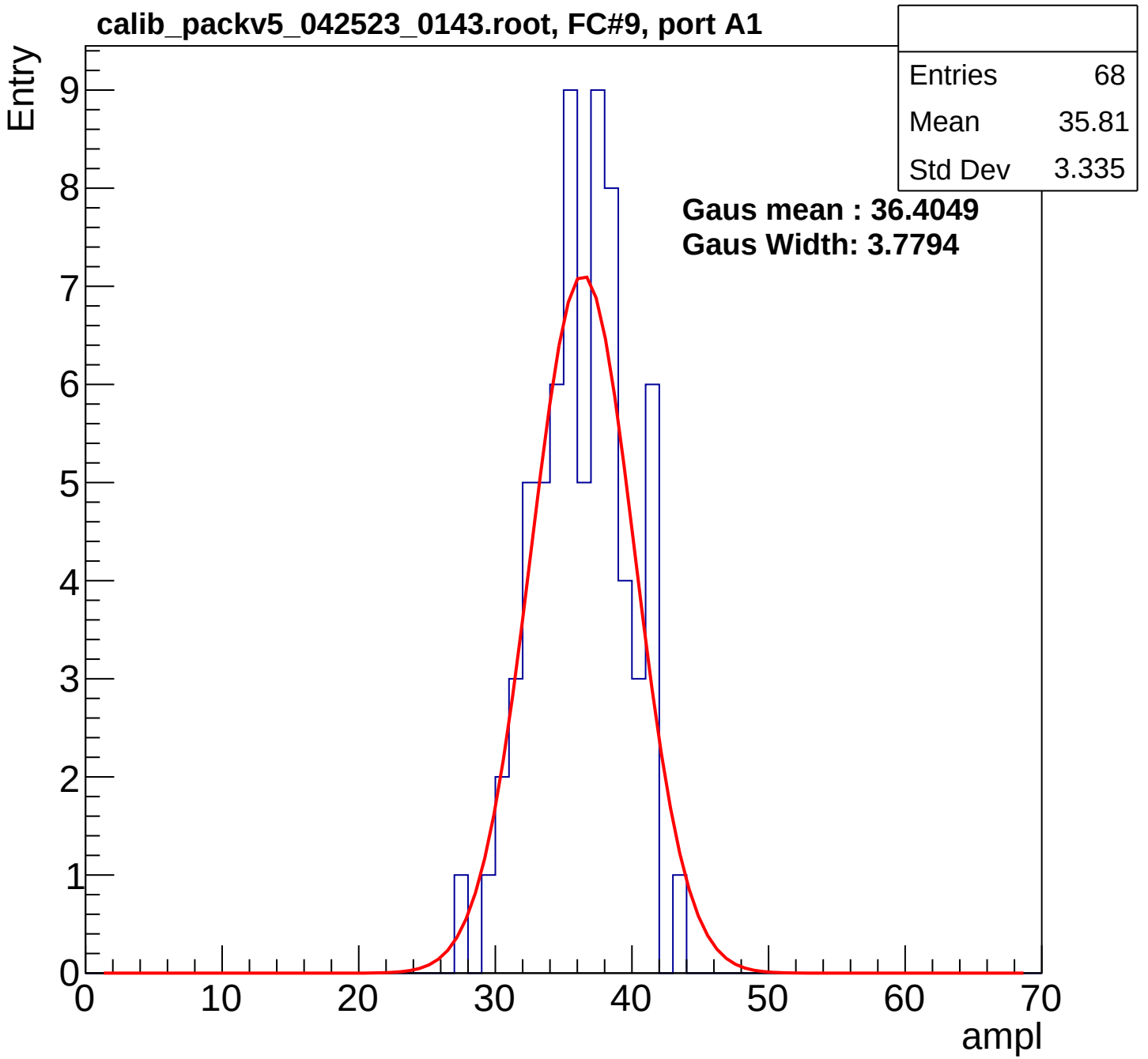

B0L001S, U4-ch39, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.16
Std Dev 2.796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

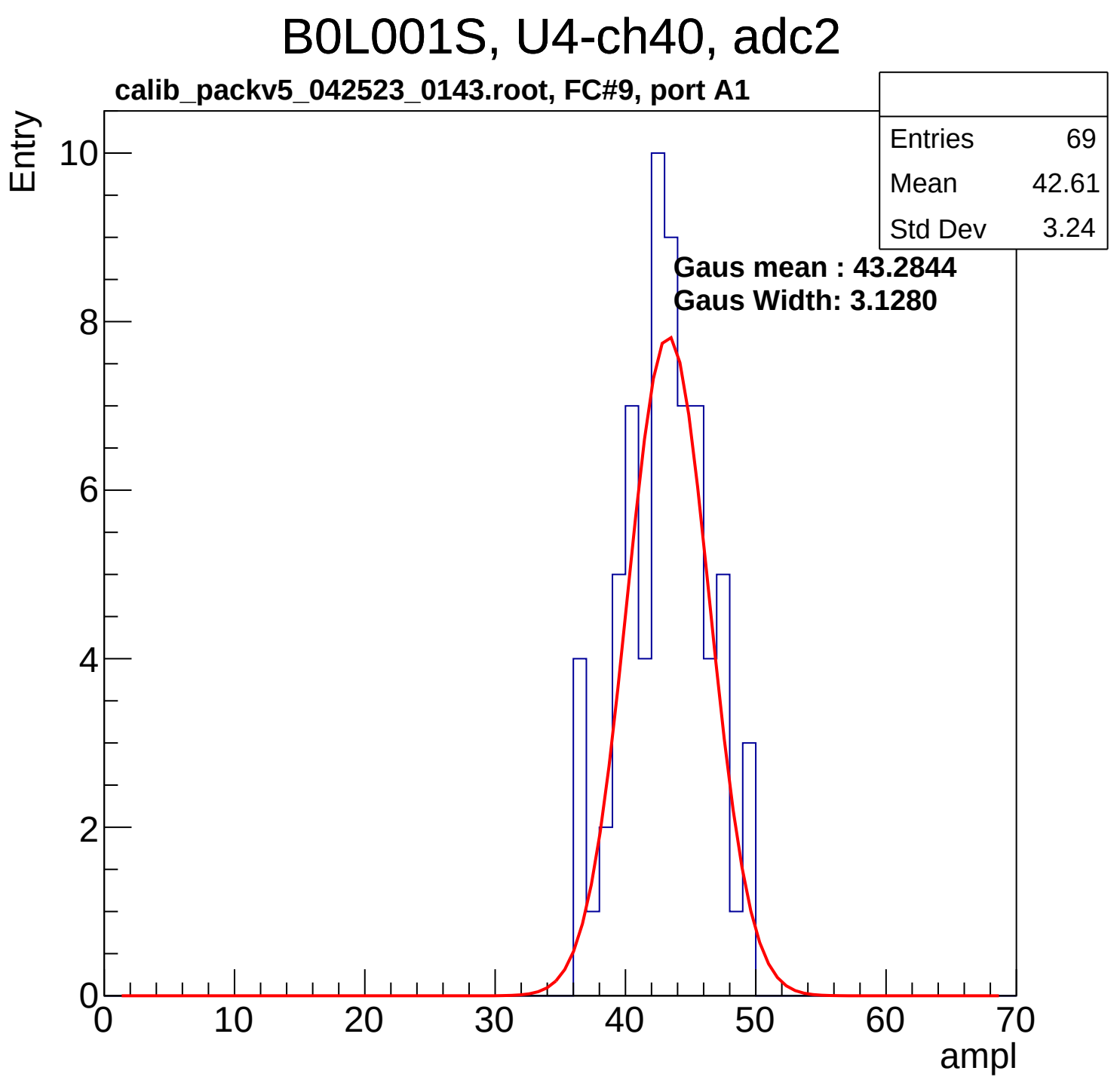

B0L001S, U4-ch39, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.98
Std Dev 9.642
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch39, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch39, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch40, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 28.24
Std Dev 4.765
Gaus mean : 29.5190
Gaus Width: 3.3402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch40, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 35.81
Std Dev 3.335
Gaus mean : 36.4049
Gaus Width: 3.7794
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch40, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 42.61
Std Dev 3.24
Gaus mean : 43.2844
Gaus Width: 3.1280
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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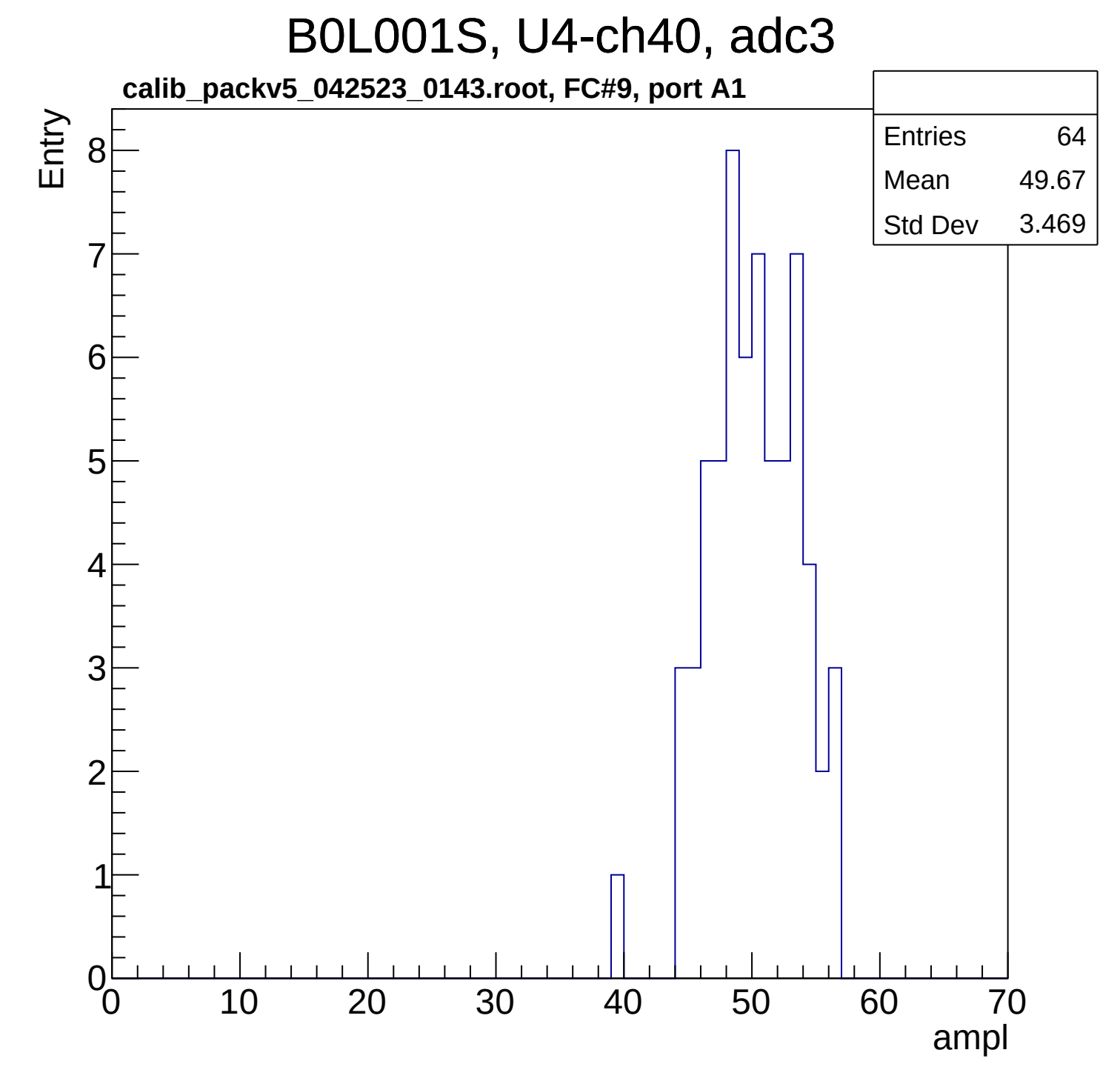
B0L001S, U4-ch40, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 49.67
Std Dev 3.469
Entry
ampl
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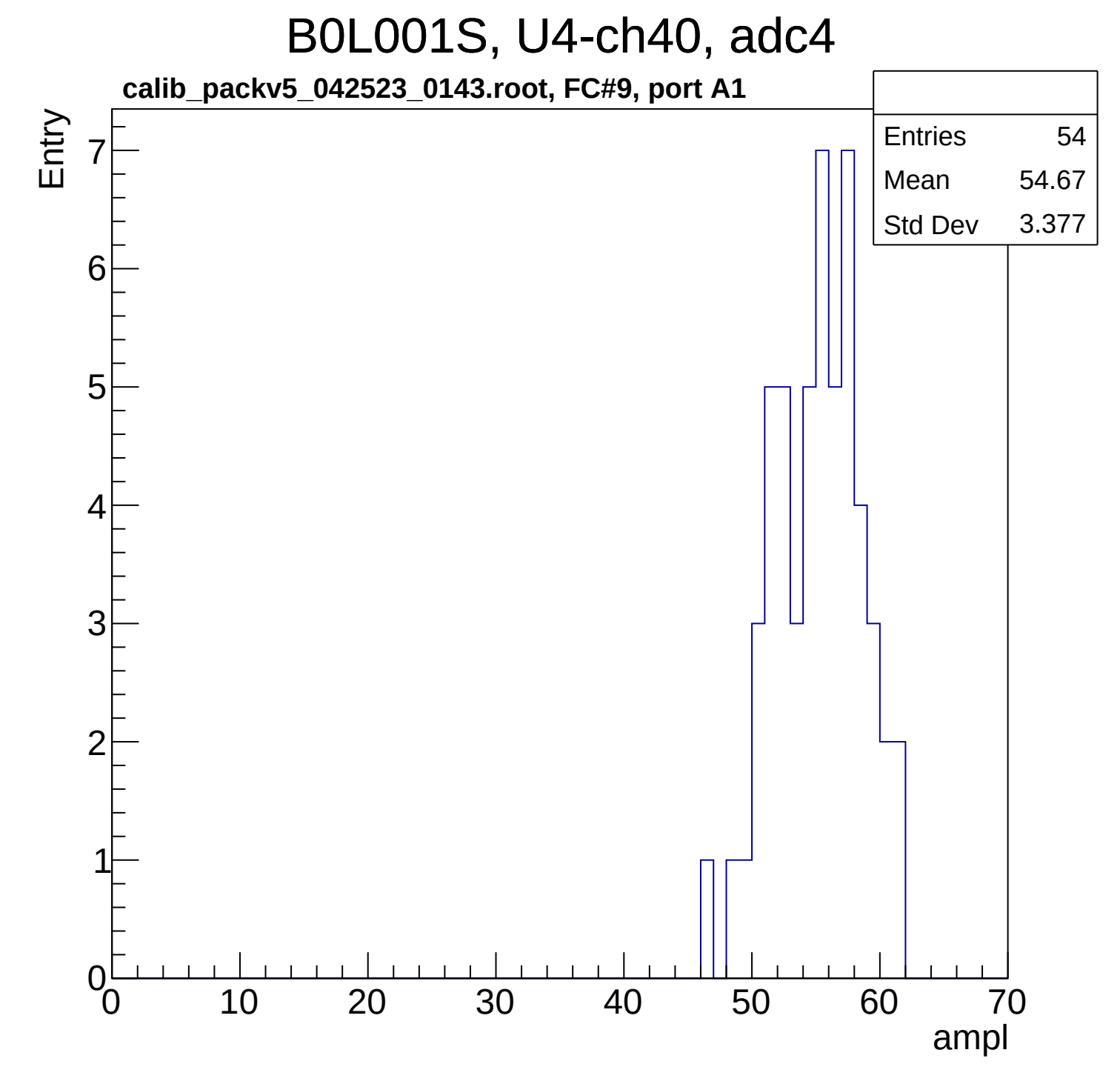
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch40, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 54.67
Std Dev 3.377
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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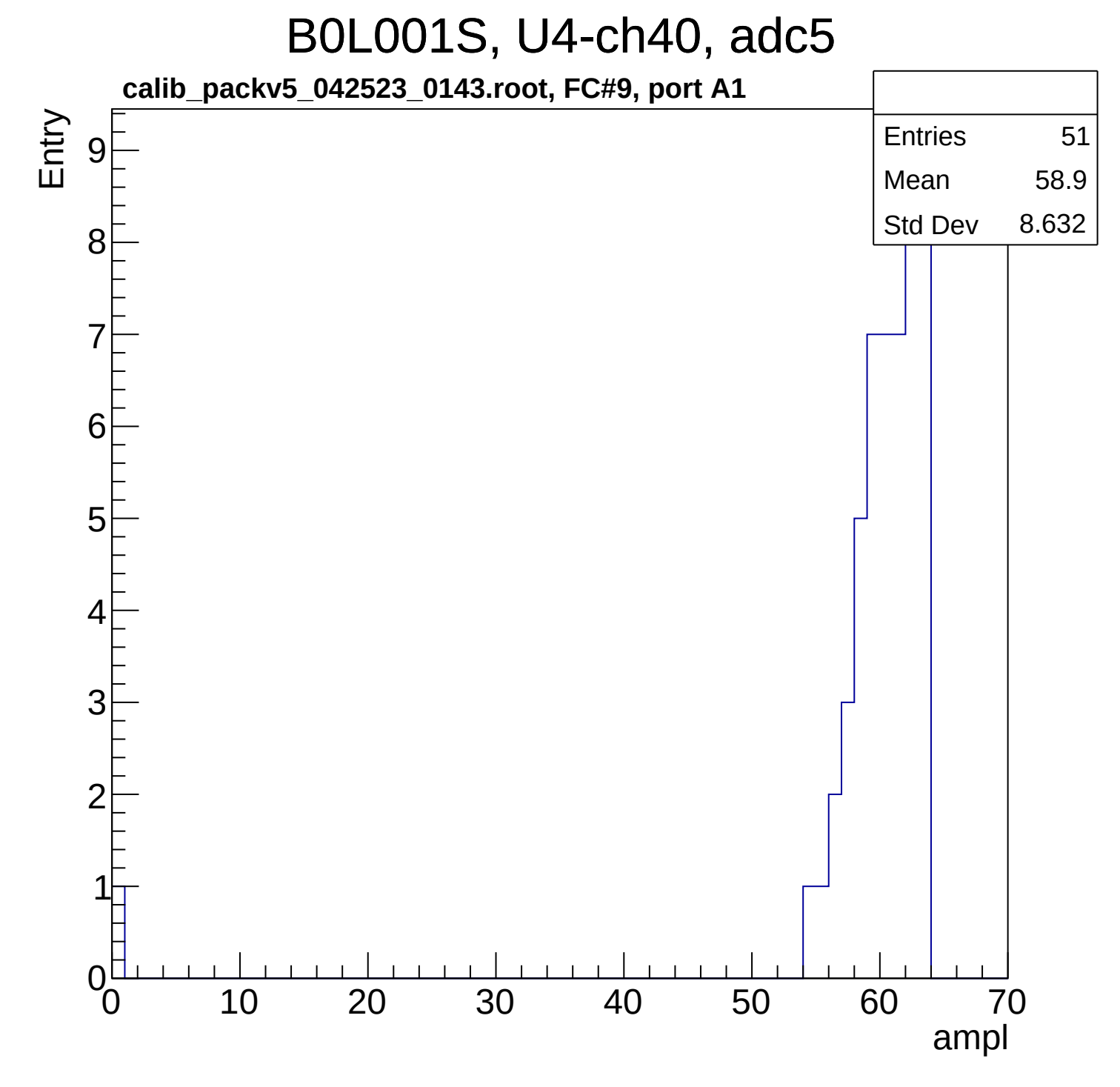
B0L001S, U4-ch40, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.9
Std Dev 8.632
Entry
ampl
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3
4
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6
7
8
9
0
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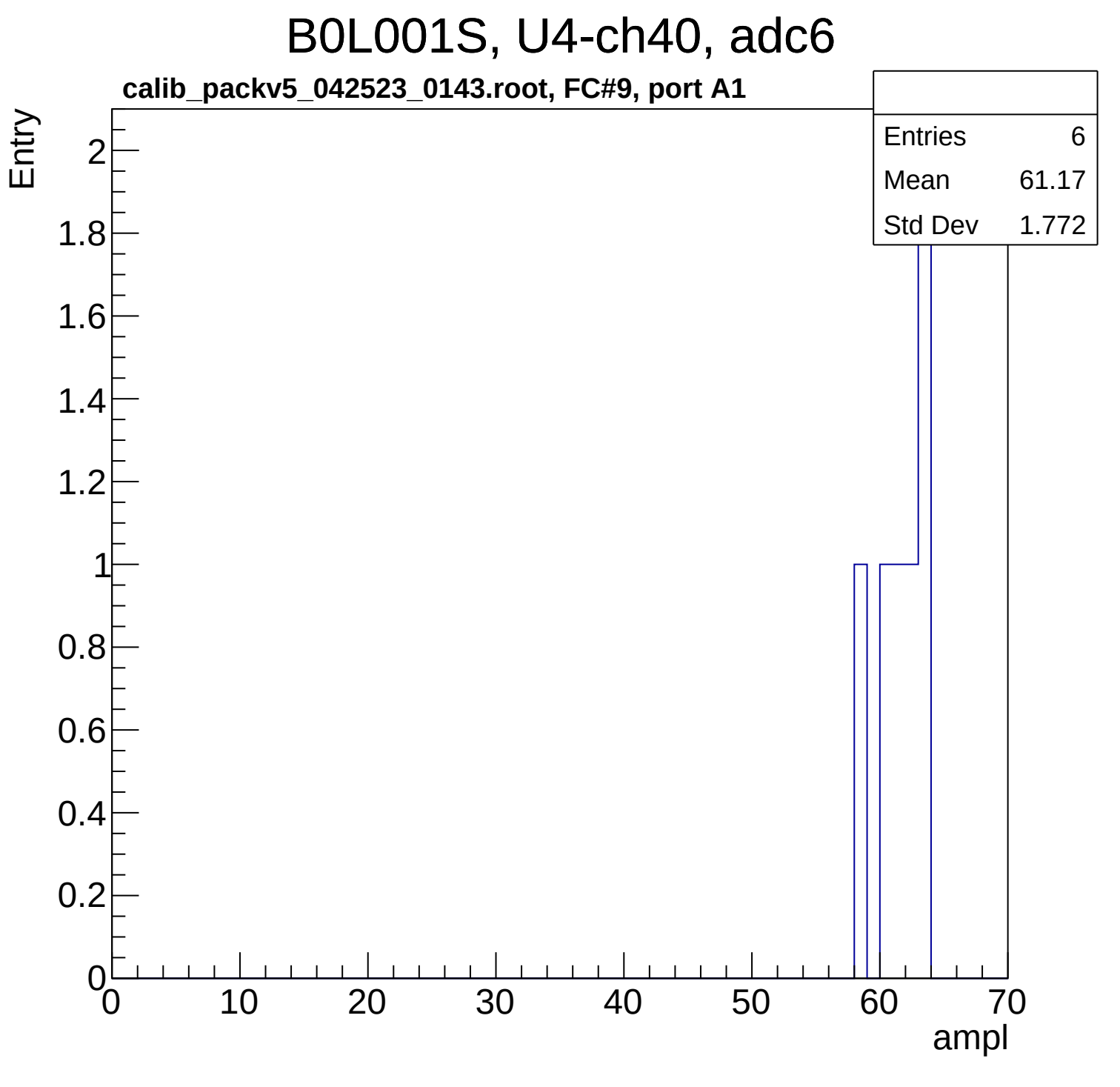
60
70


B0L001S, U4-ch40, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.17
Std Dev 1.772
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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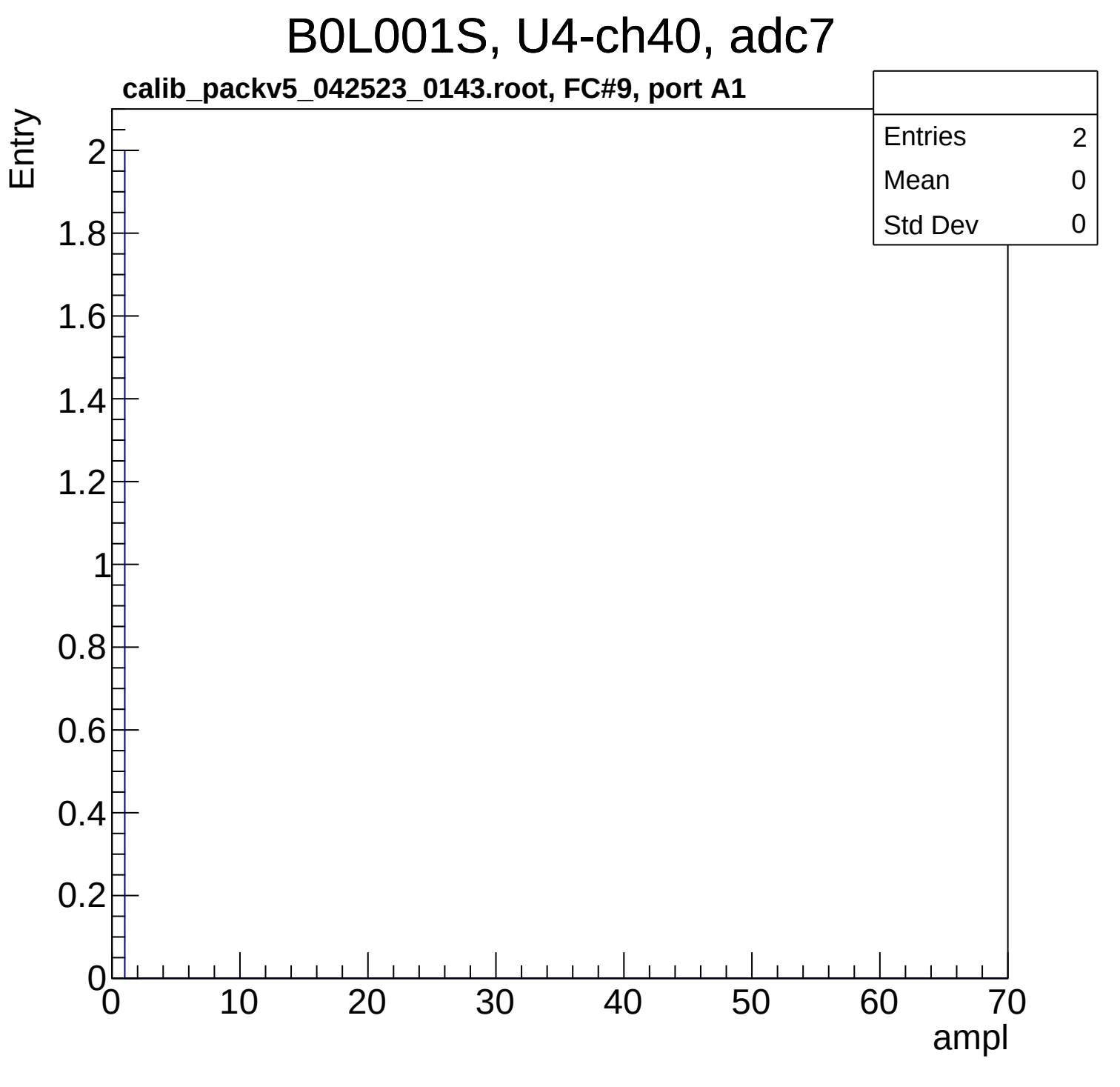
B0L001S, U4-ch40, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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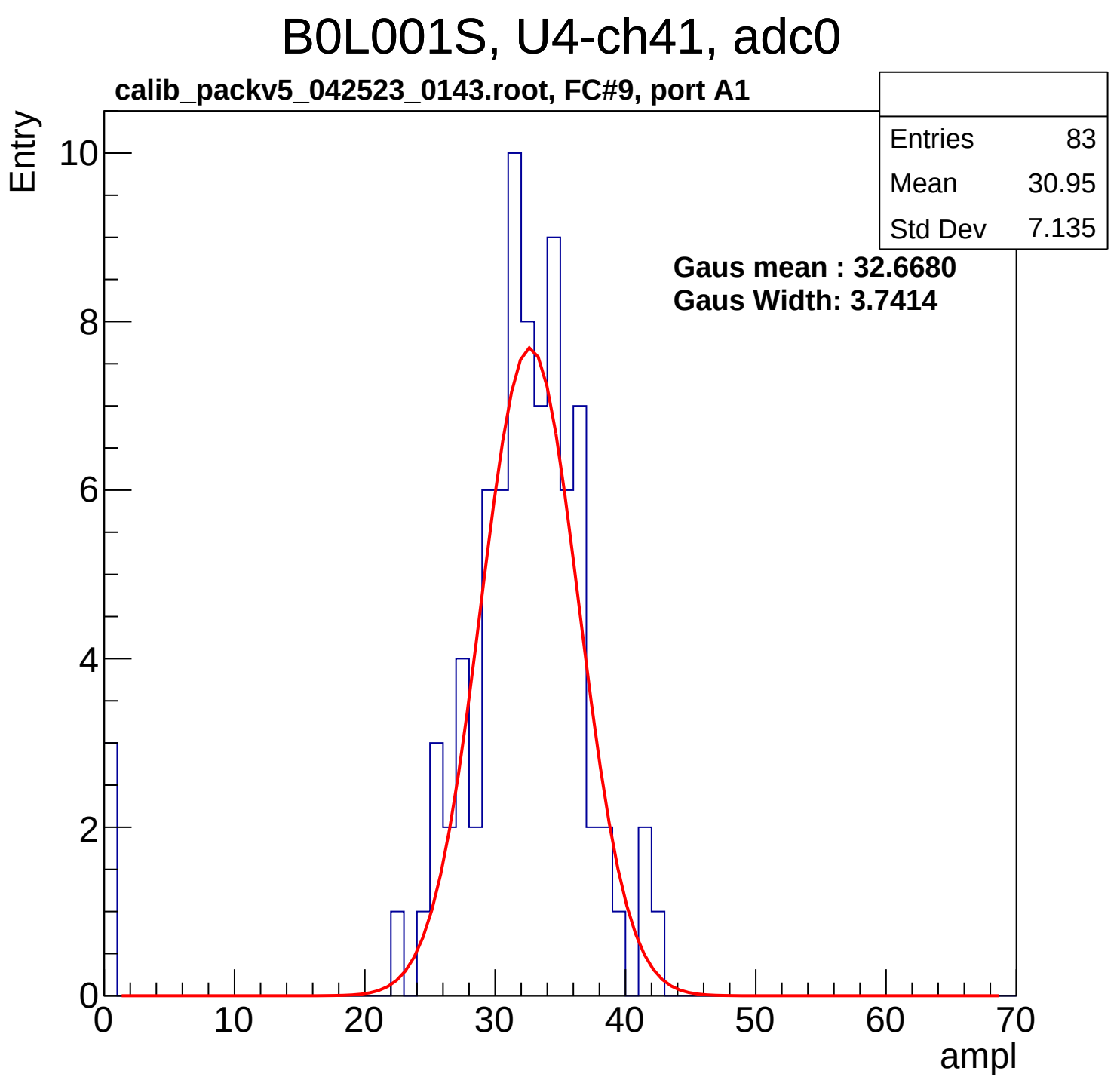
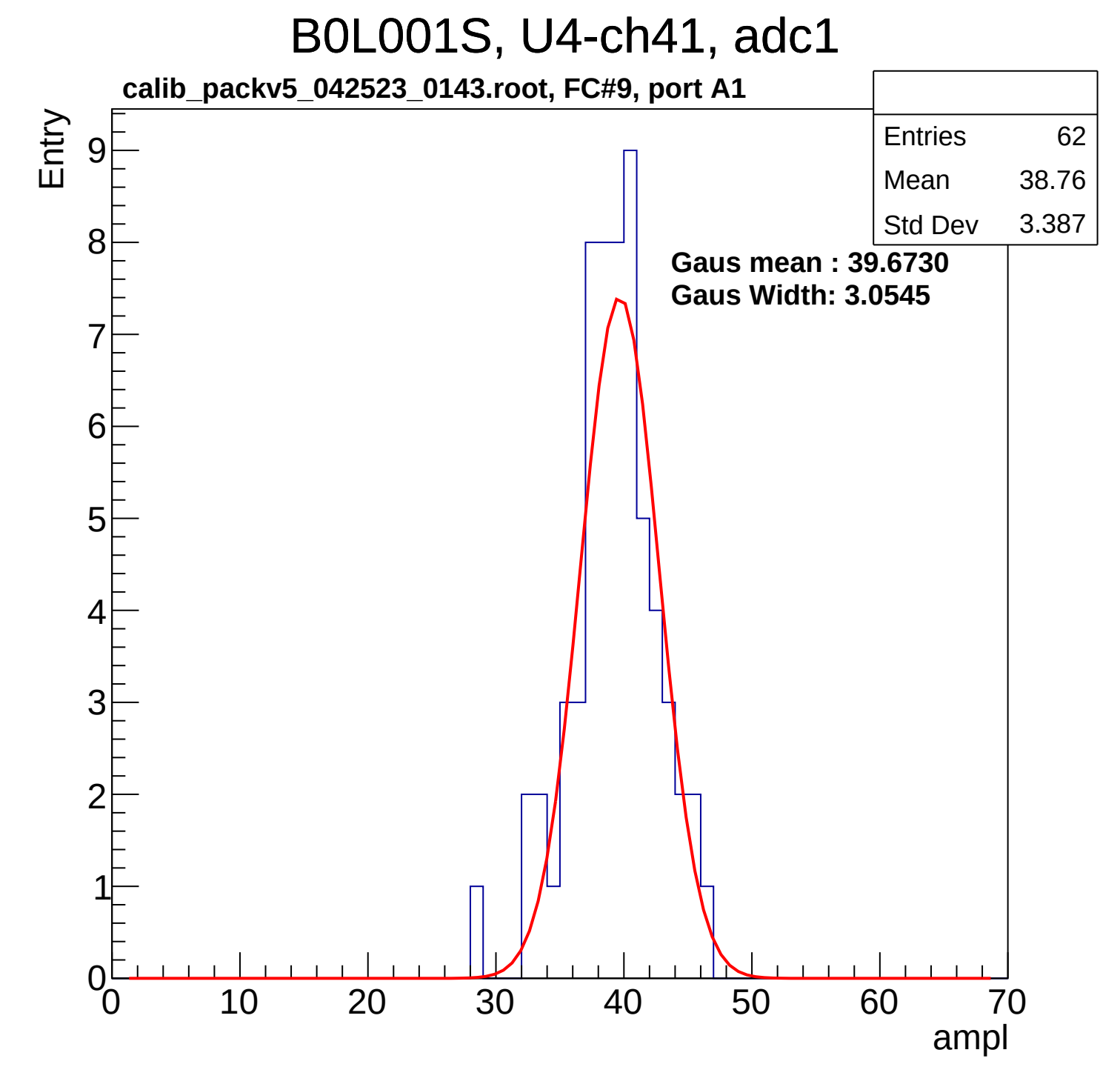
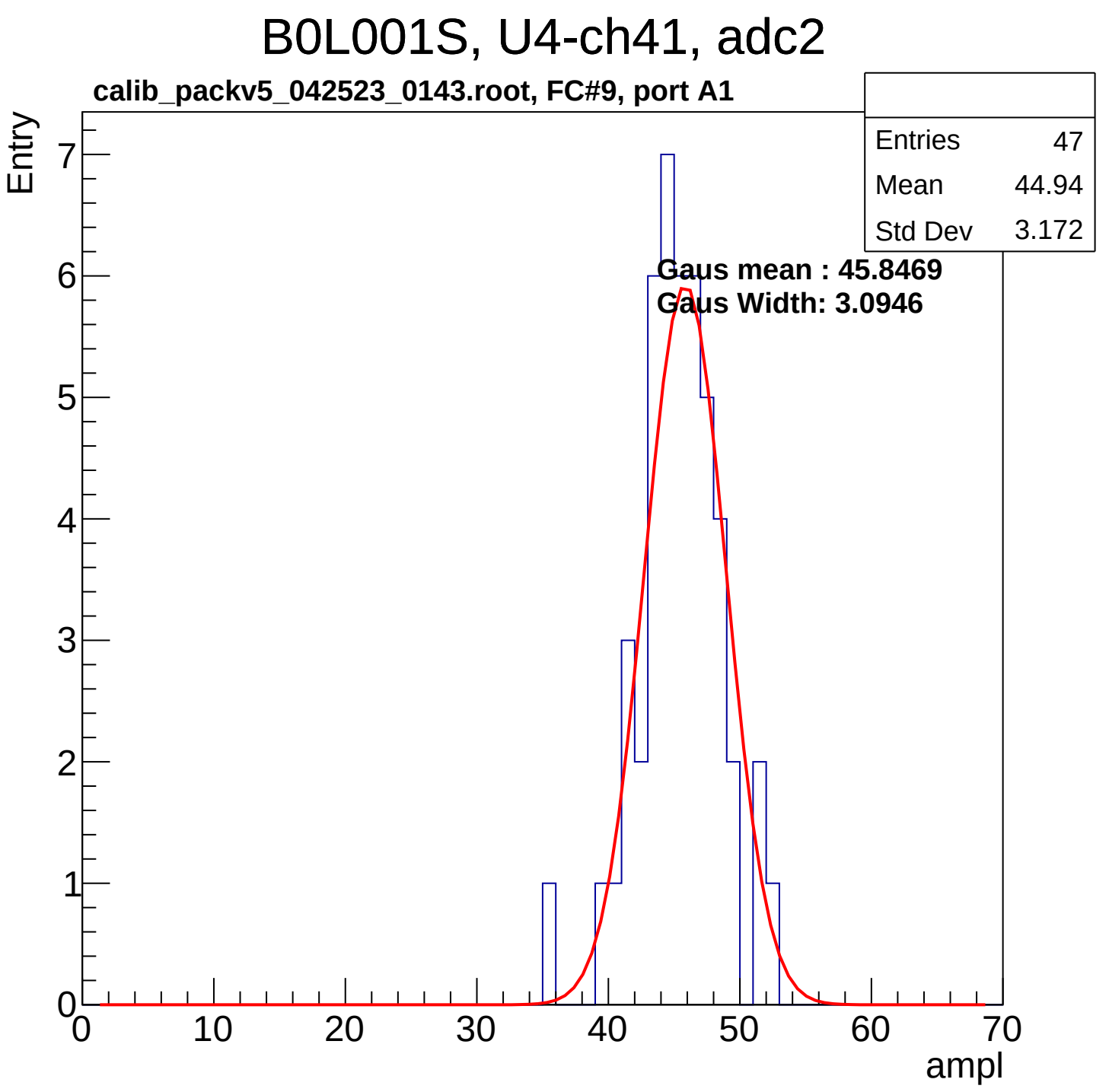
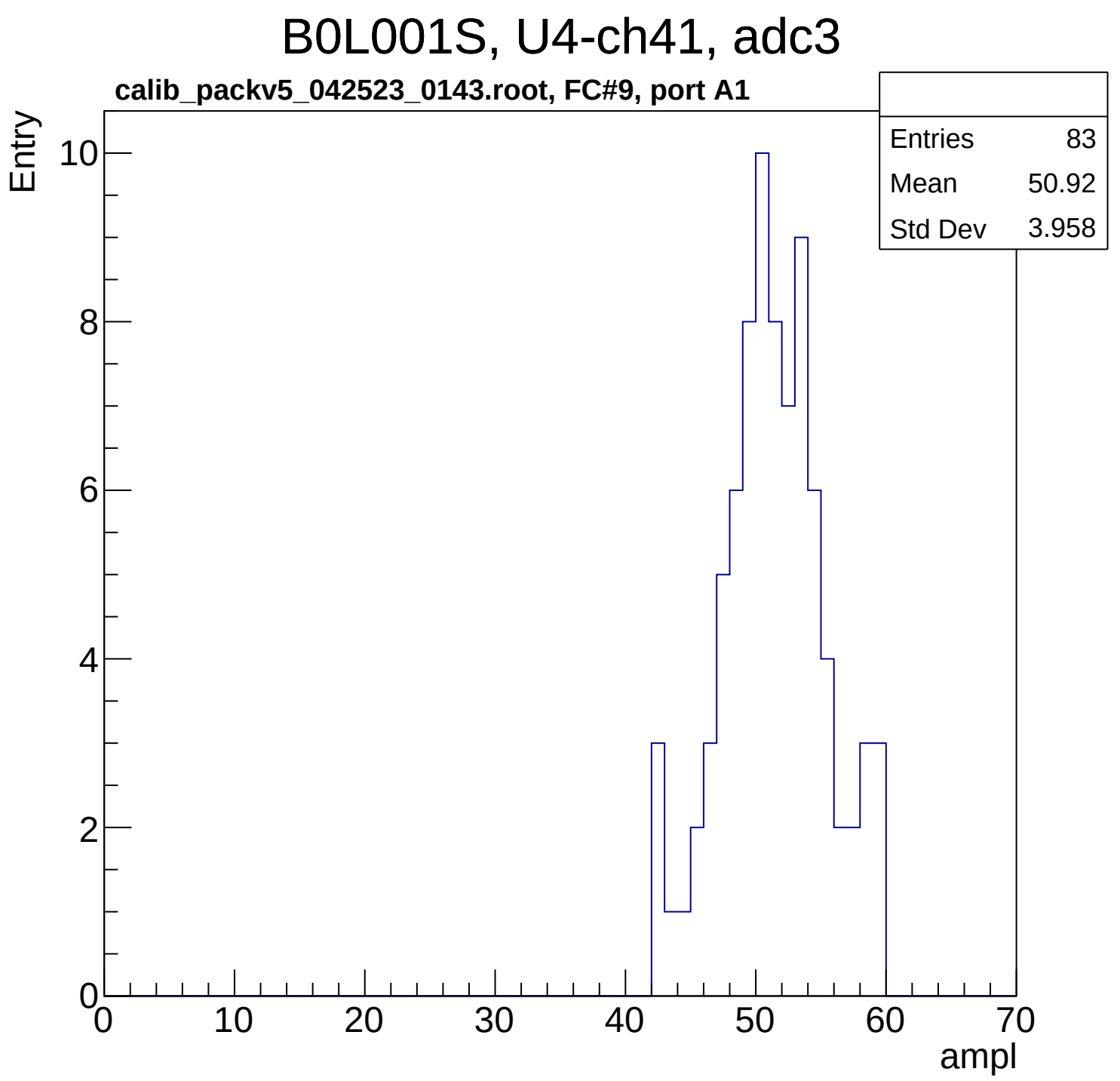

B0L001S, U4-ch41, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 30.95
Std Dev 7.135
Gaus mean : 32.6680
Gaus Width: 3.7414
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch41, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.76
Std Dev 3.387
Gaus mean : 39.6730
Gaus Width: 3.0545
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch41, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 44.94
Std Dev 3.172
Gaus mean : 45.8469
Gaus Width: 3.0946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch41, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 50.92
Std Dev 3.958
Entry
ampl
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2
4
6
8
10
0
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20
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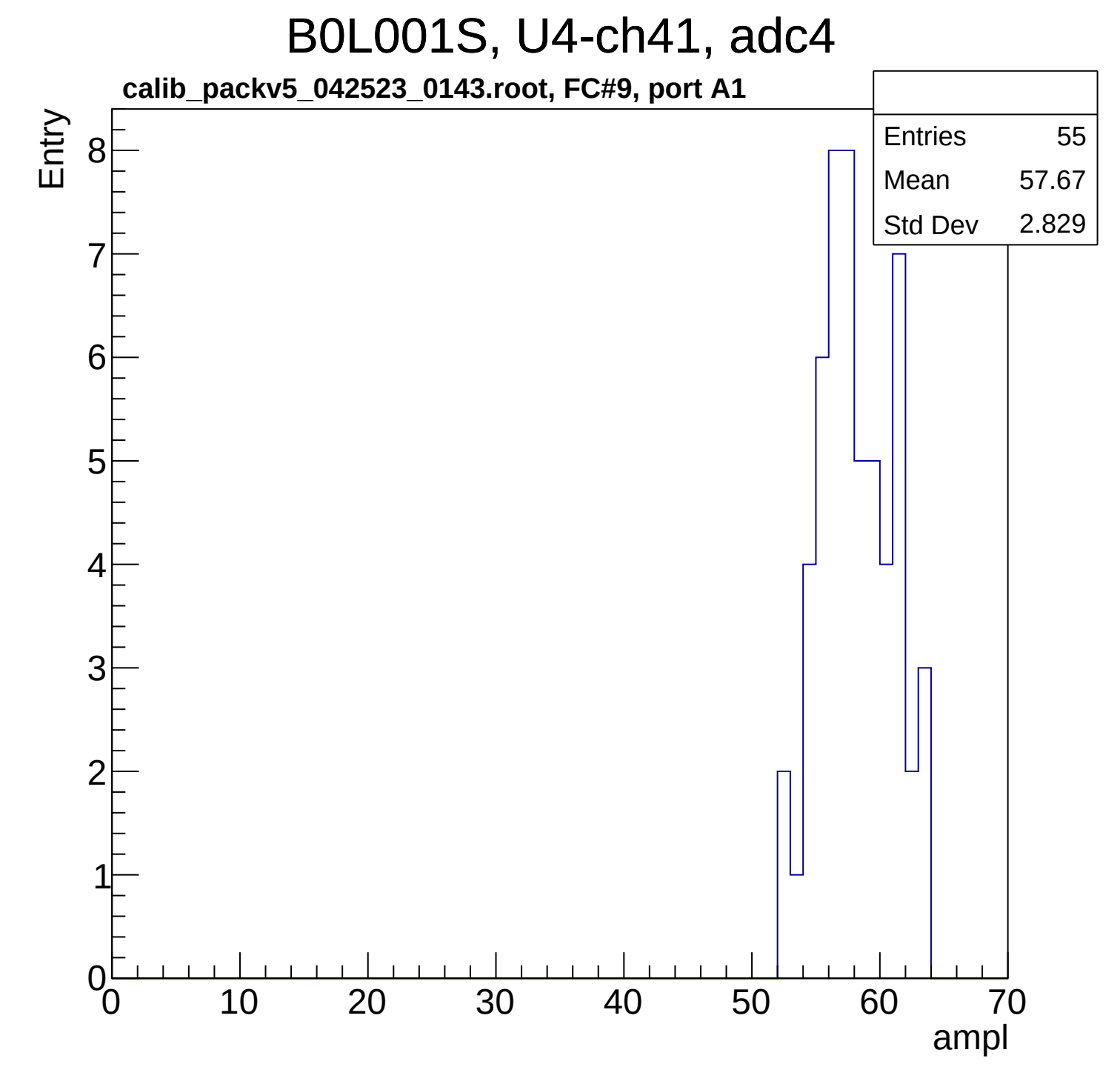
40
50
60
70


B0L001S, U4-ch41, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.67
Std Dev 2.829
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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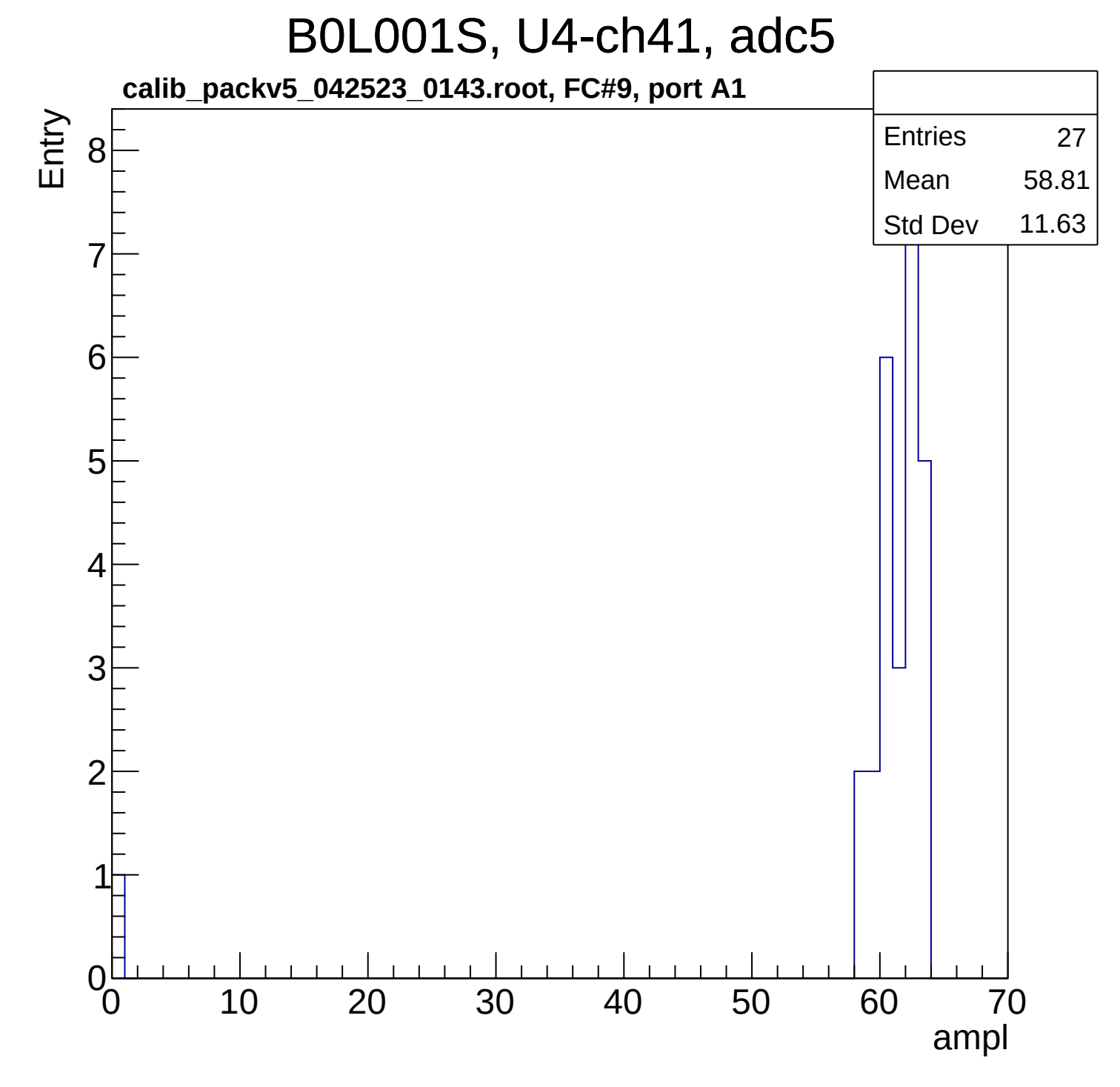
B0L001S, U4-ch41, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.81
Std Dev 11.63
Entry
ampl
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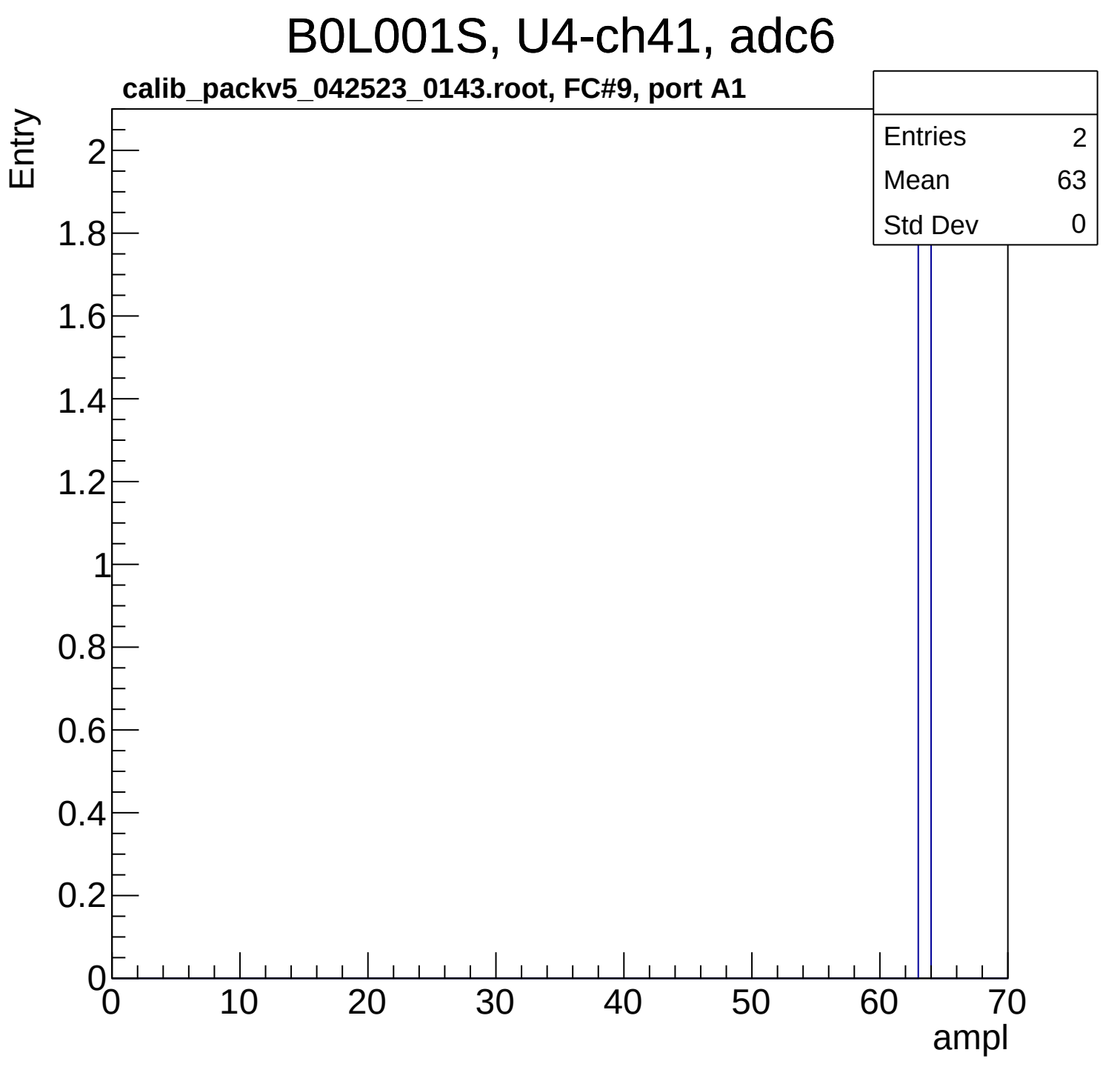
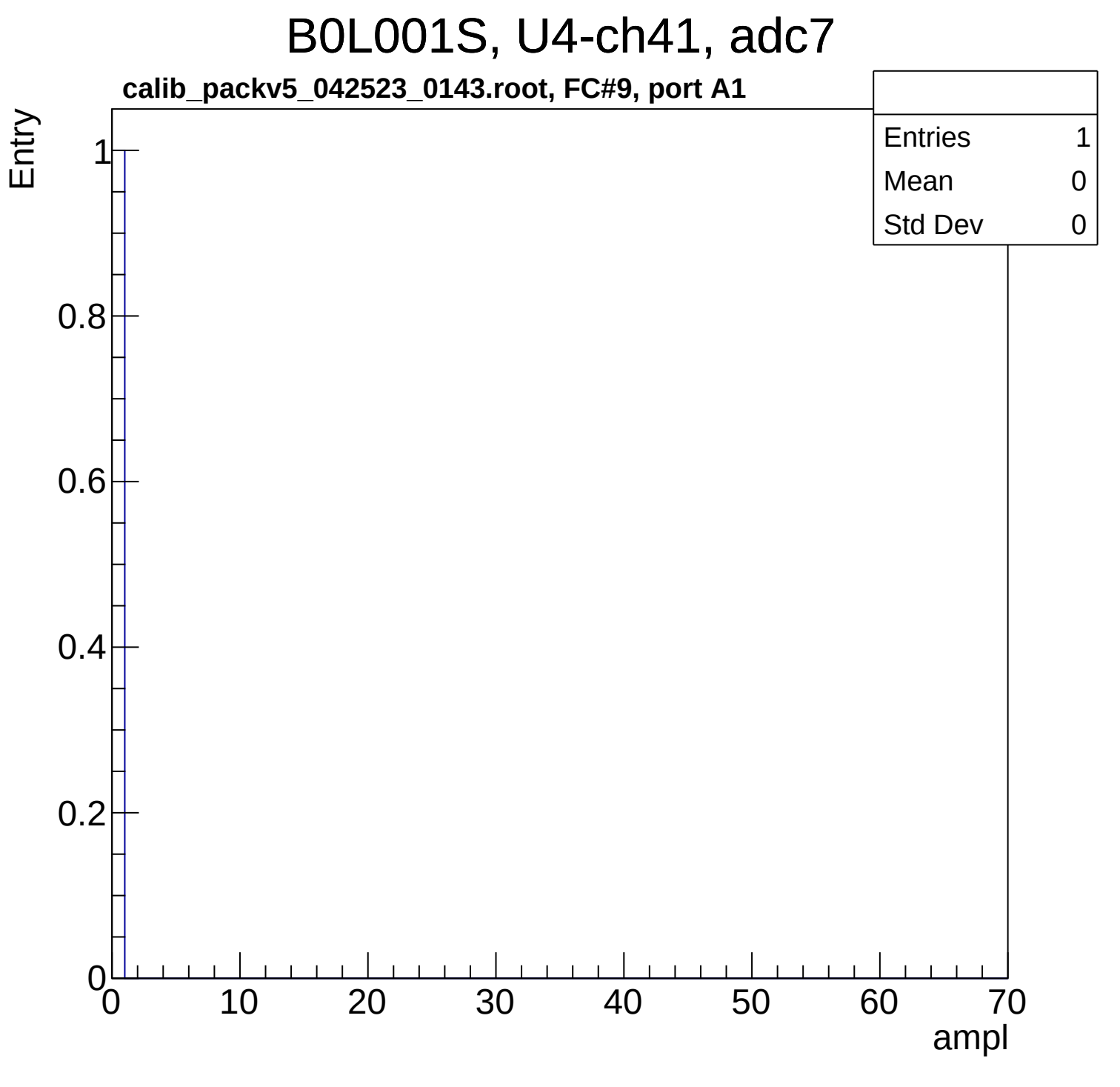
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch41, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch41, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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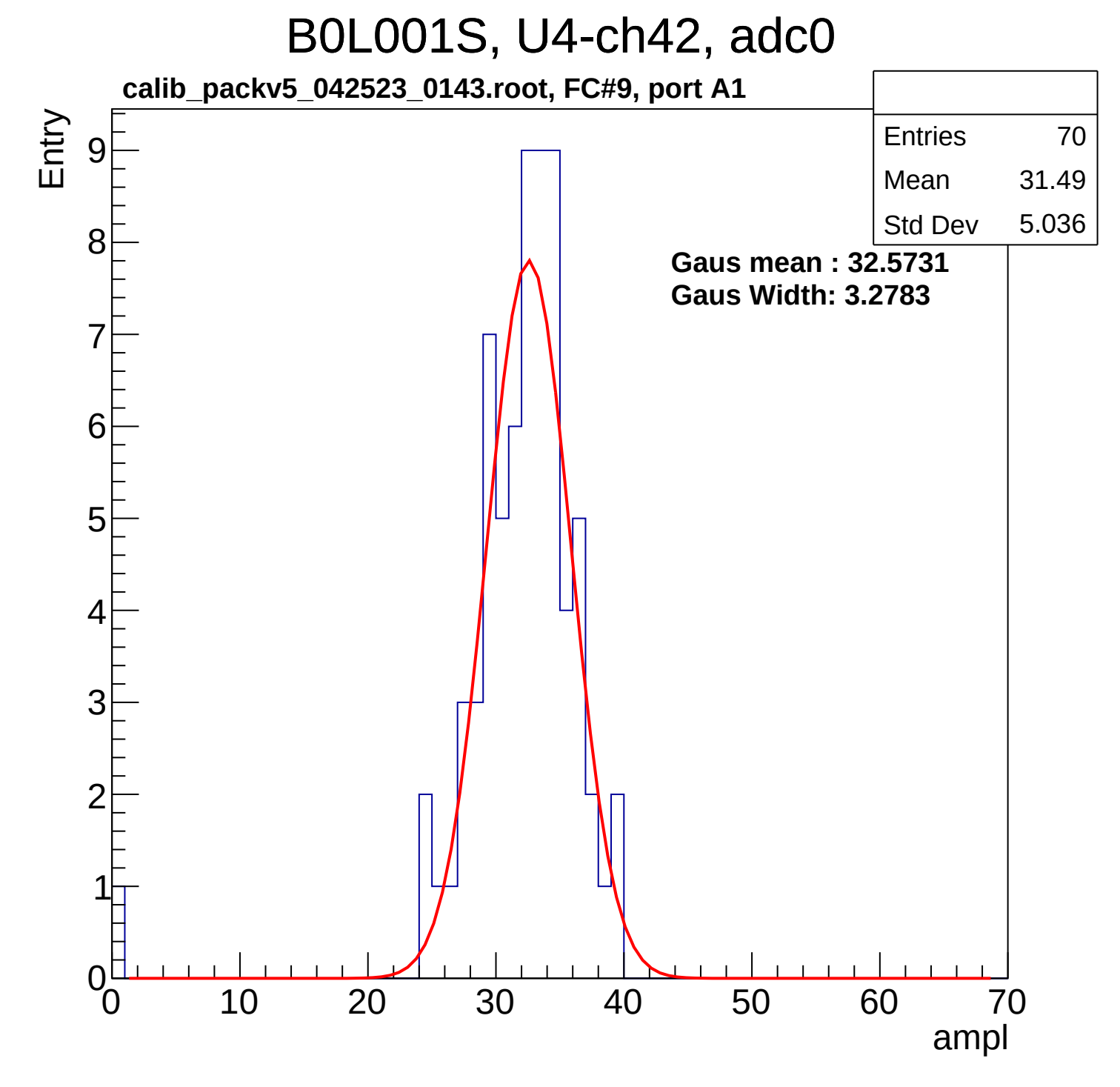
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch42, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.49
Std Dev 5.036
Gaus mean : 32.5731
Gaus Width: 3.2783
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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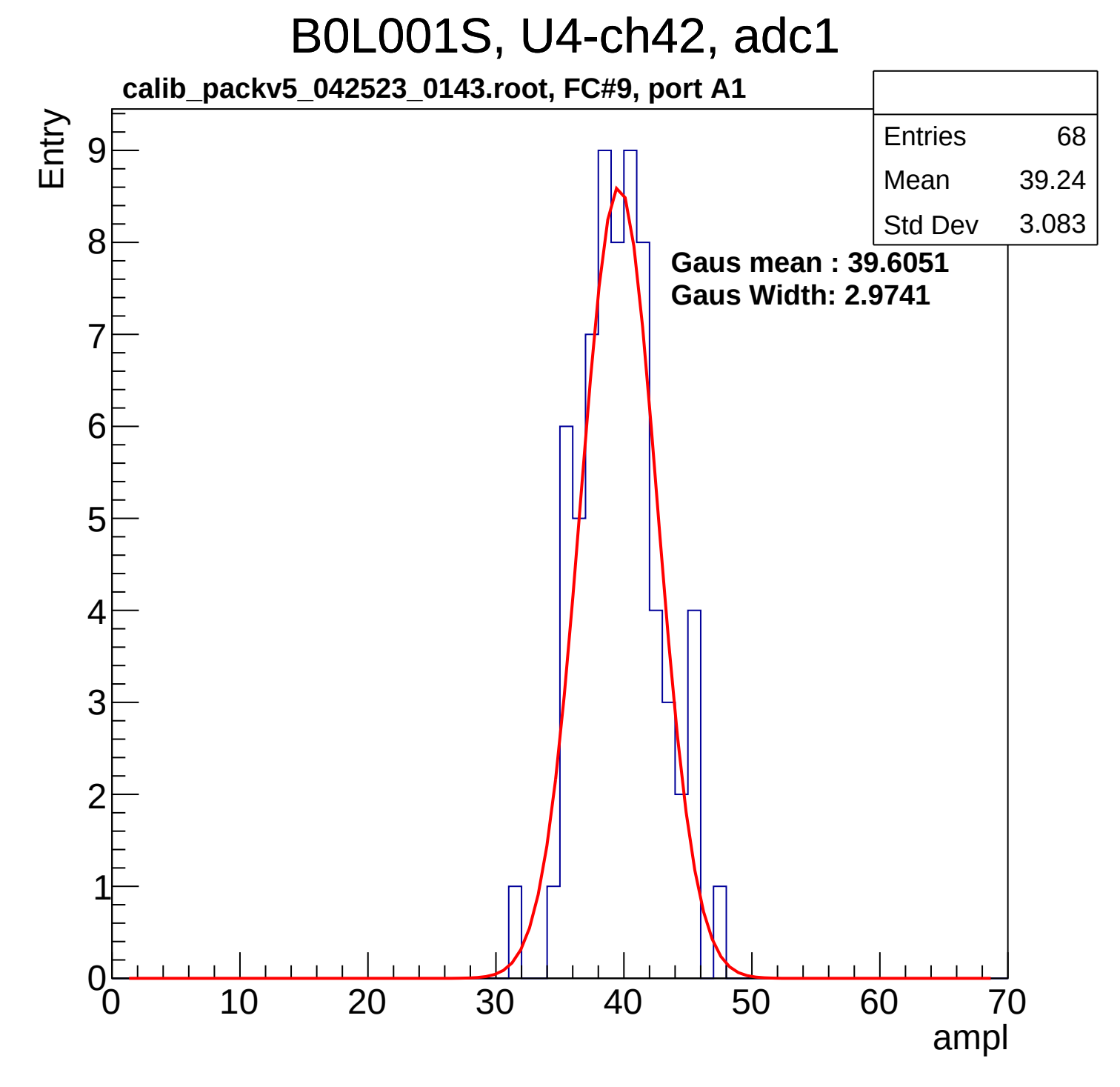
B0L001S, U4-ch42, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 39.24
Std Dev 3.083
Gaus mean : 39.6051
Gaus Width: 2.9741
Entry
ampl
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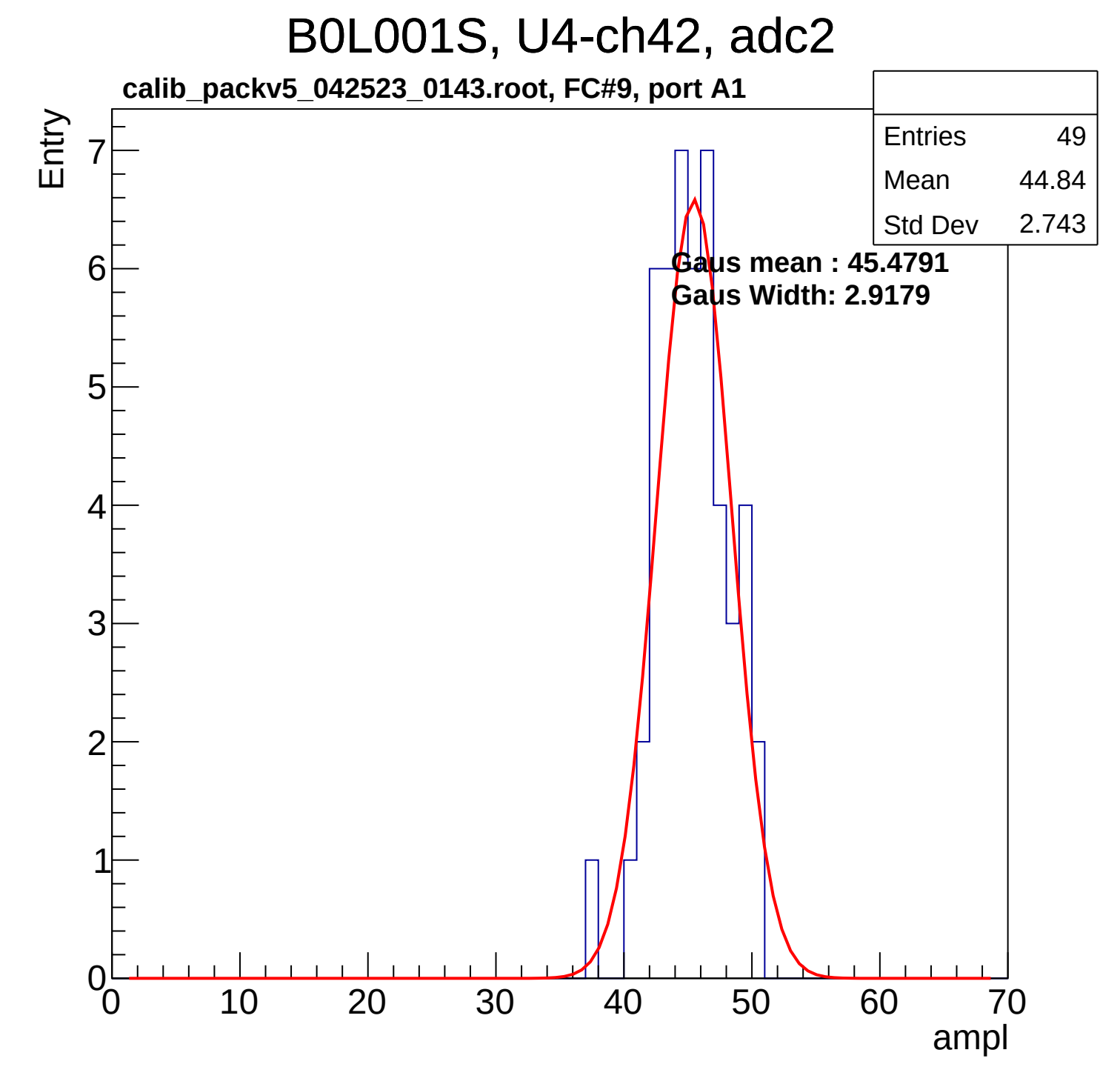
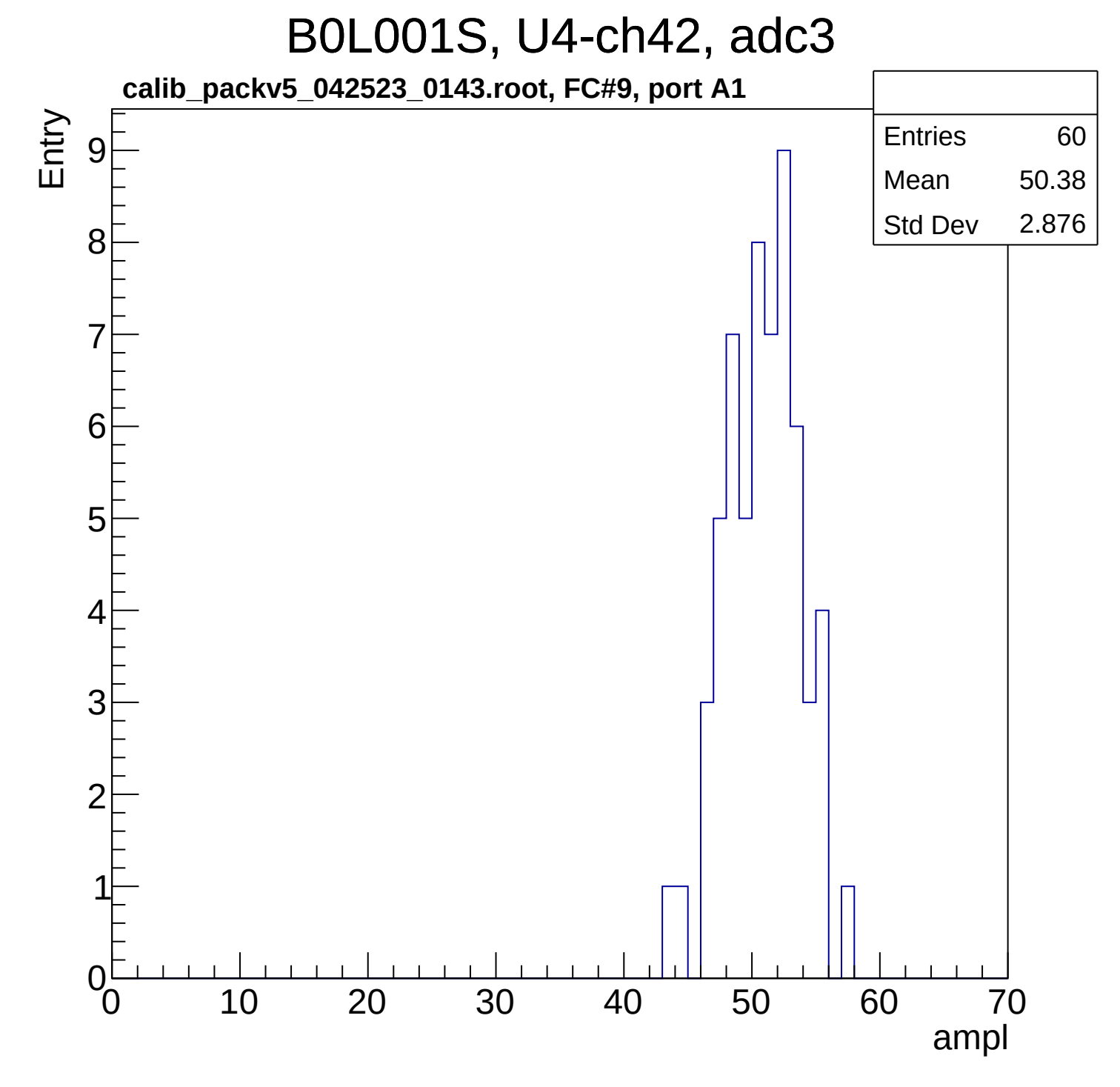
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch42, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 44.84
Std Dev 2.743
Gaus mean : 45.4791
Gaus Width: 2.9179
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch42, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.38
Std Dev 2.876
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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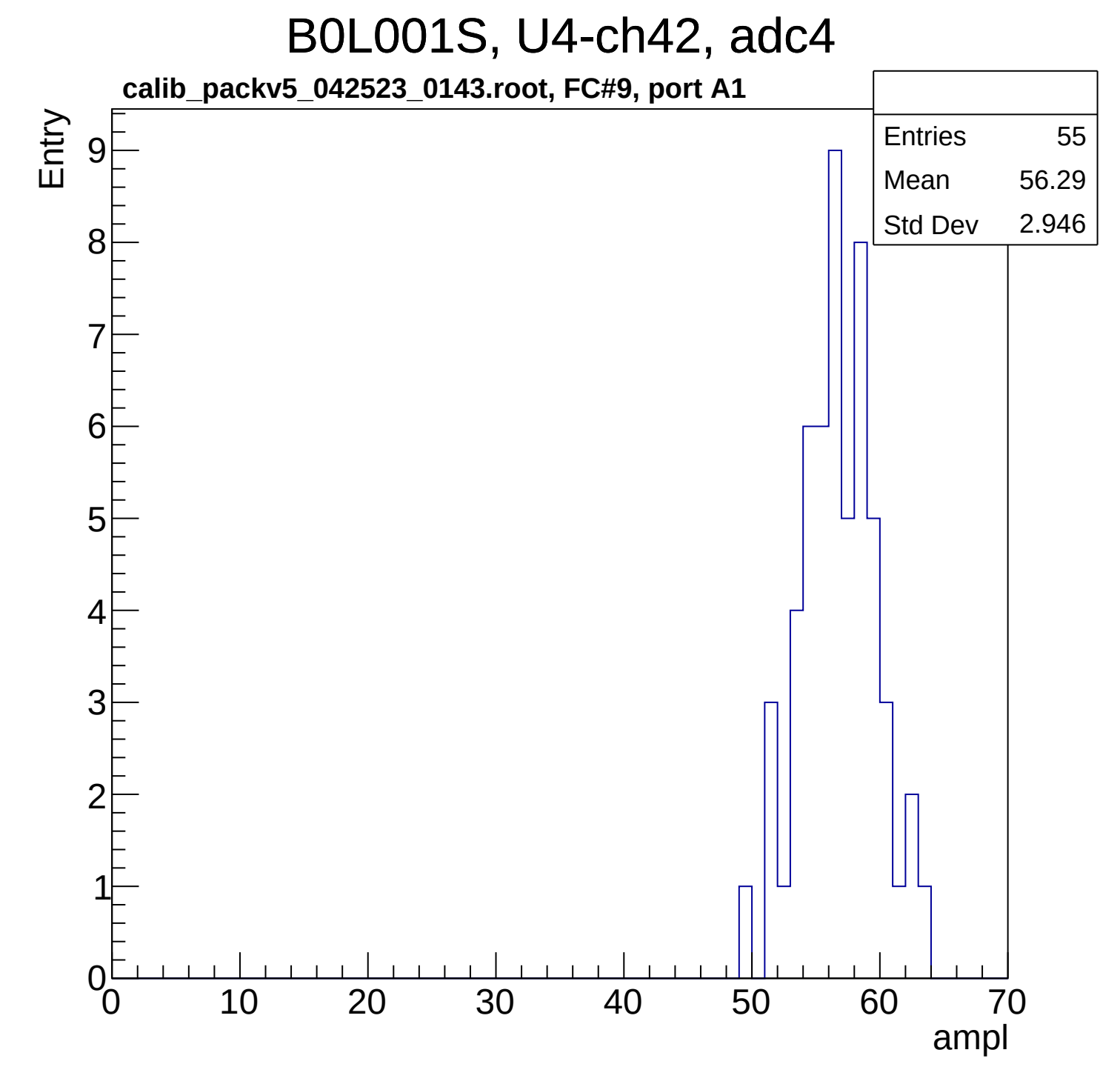
B0L001S, U4-ch42, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.29
Std Dev 2.946
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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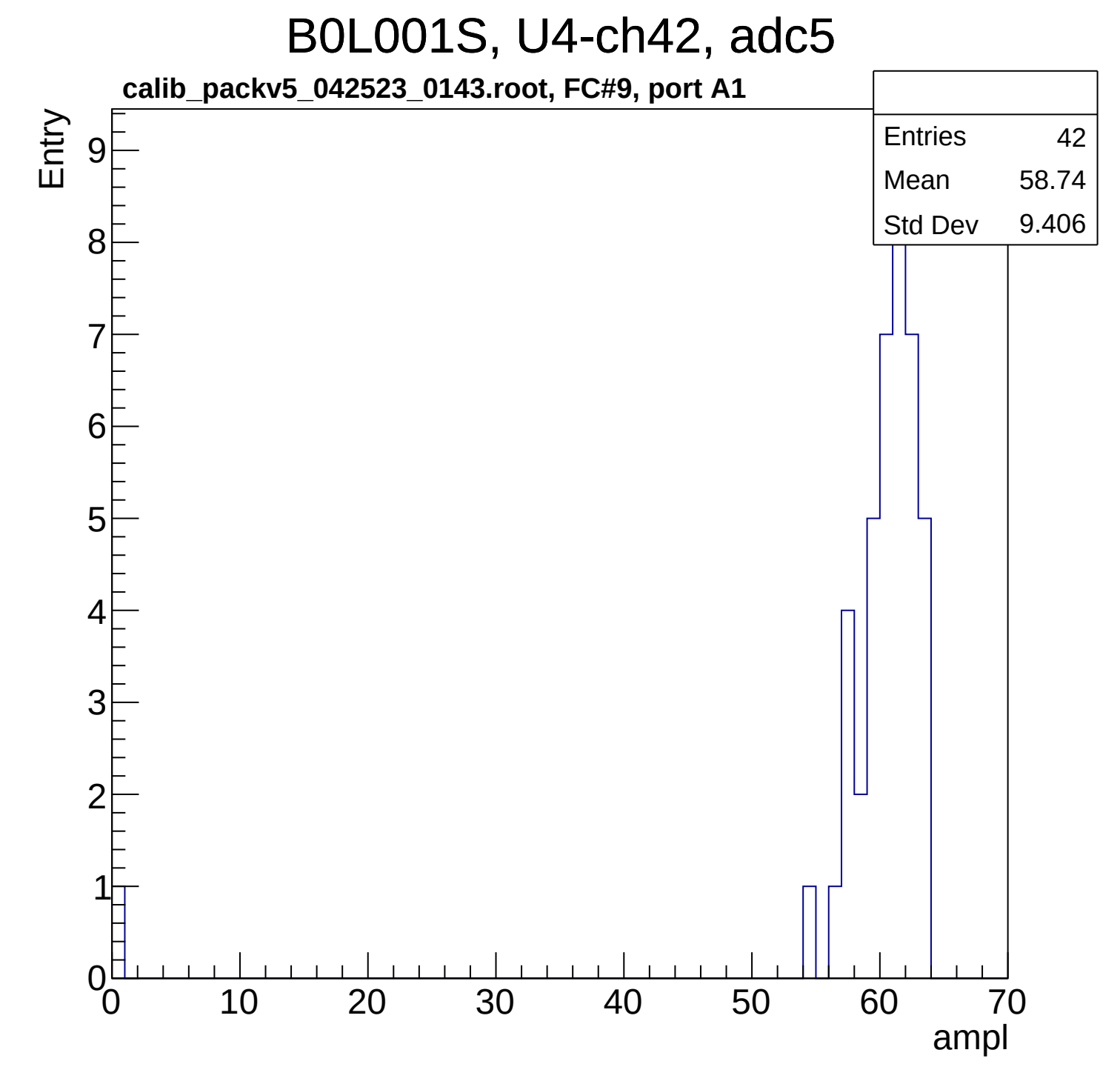
B0L001S, U4-ch42, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.74
Std Dev 9.406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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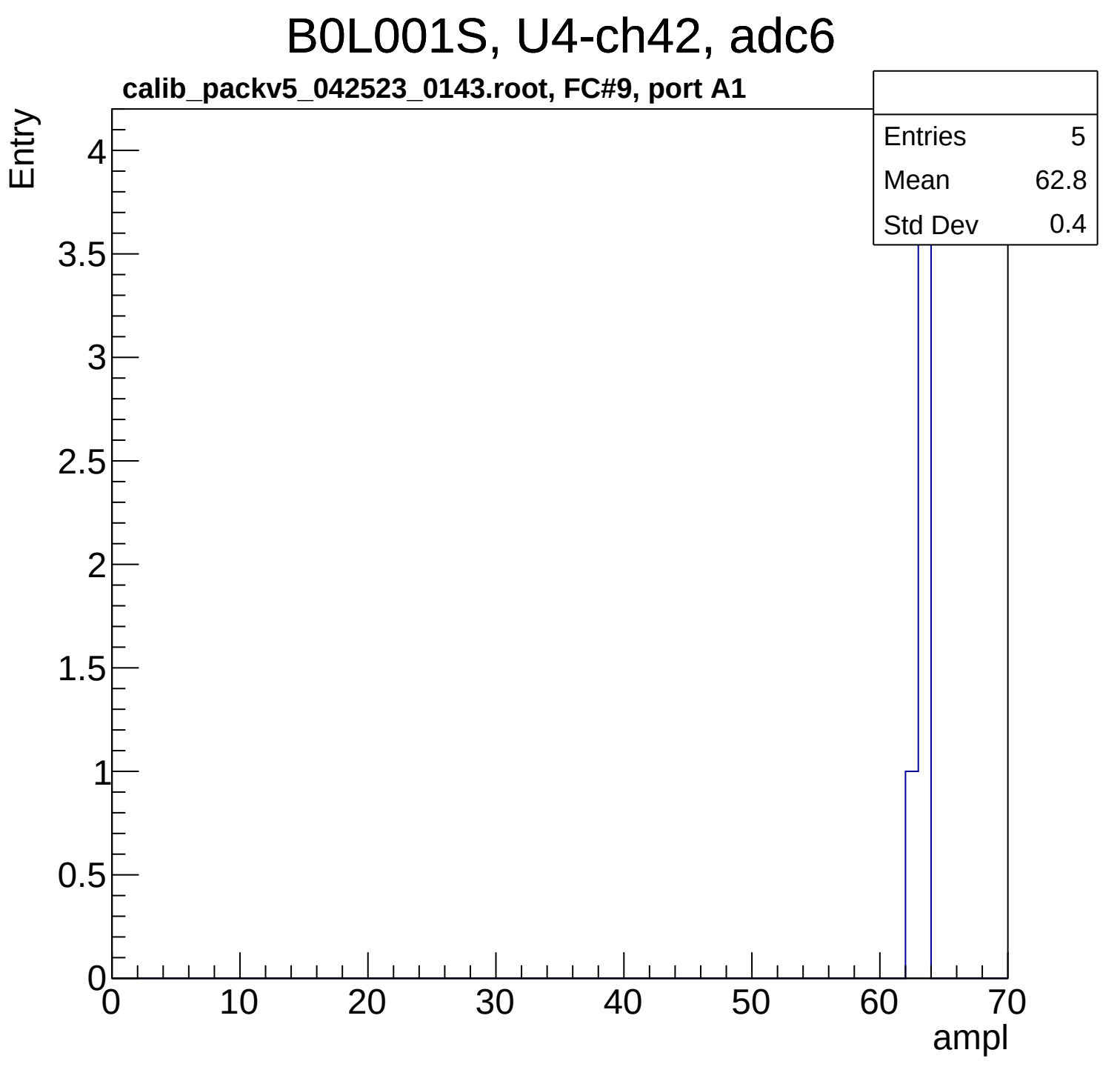
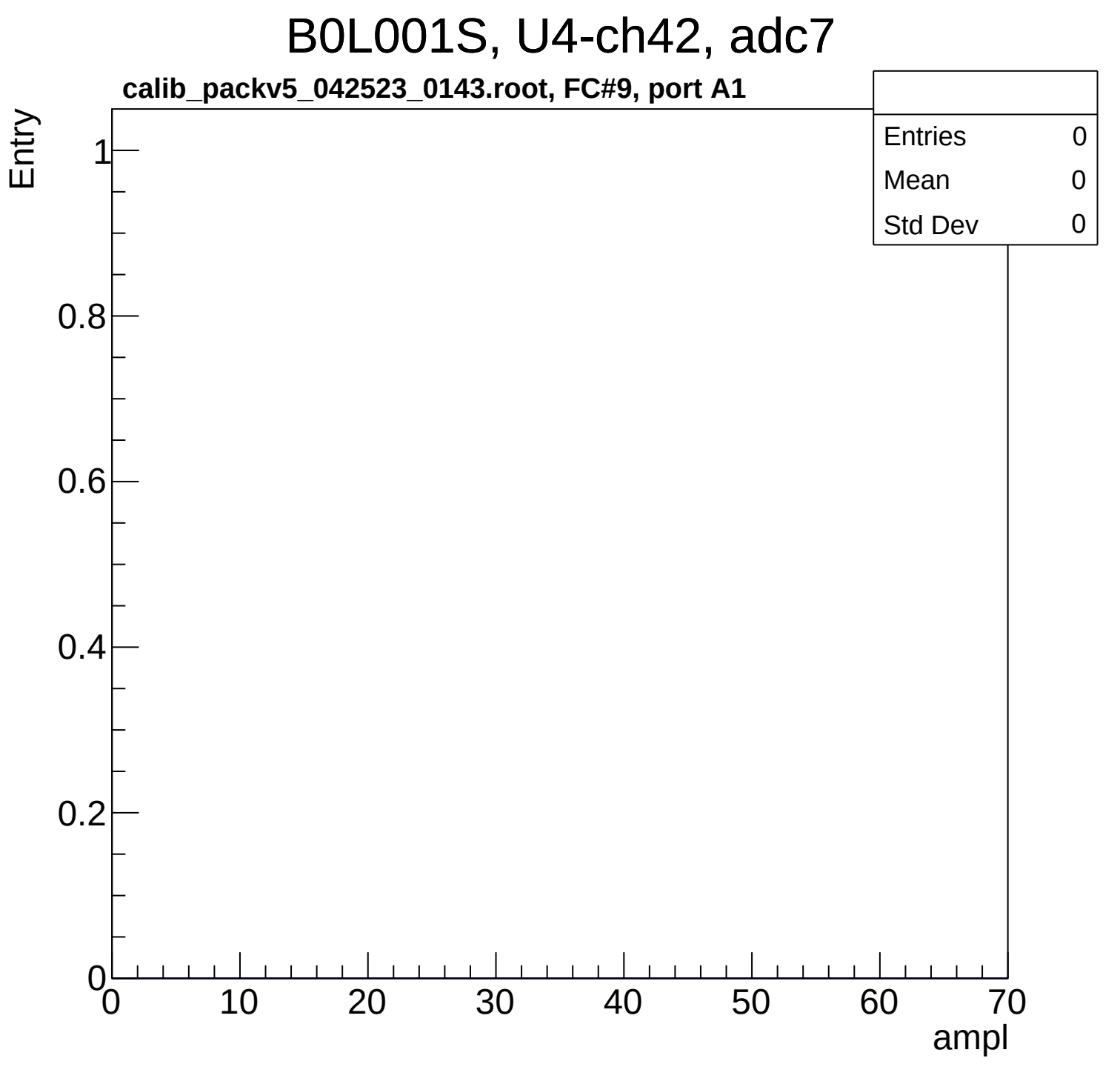
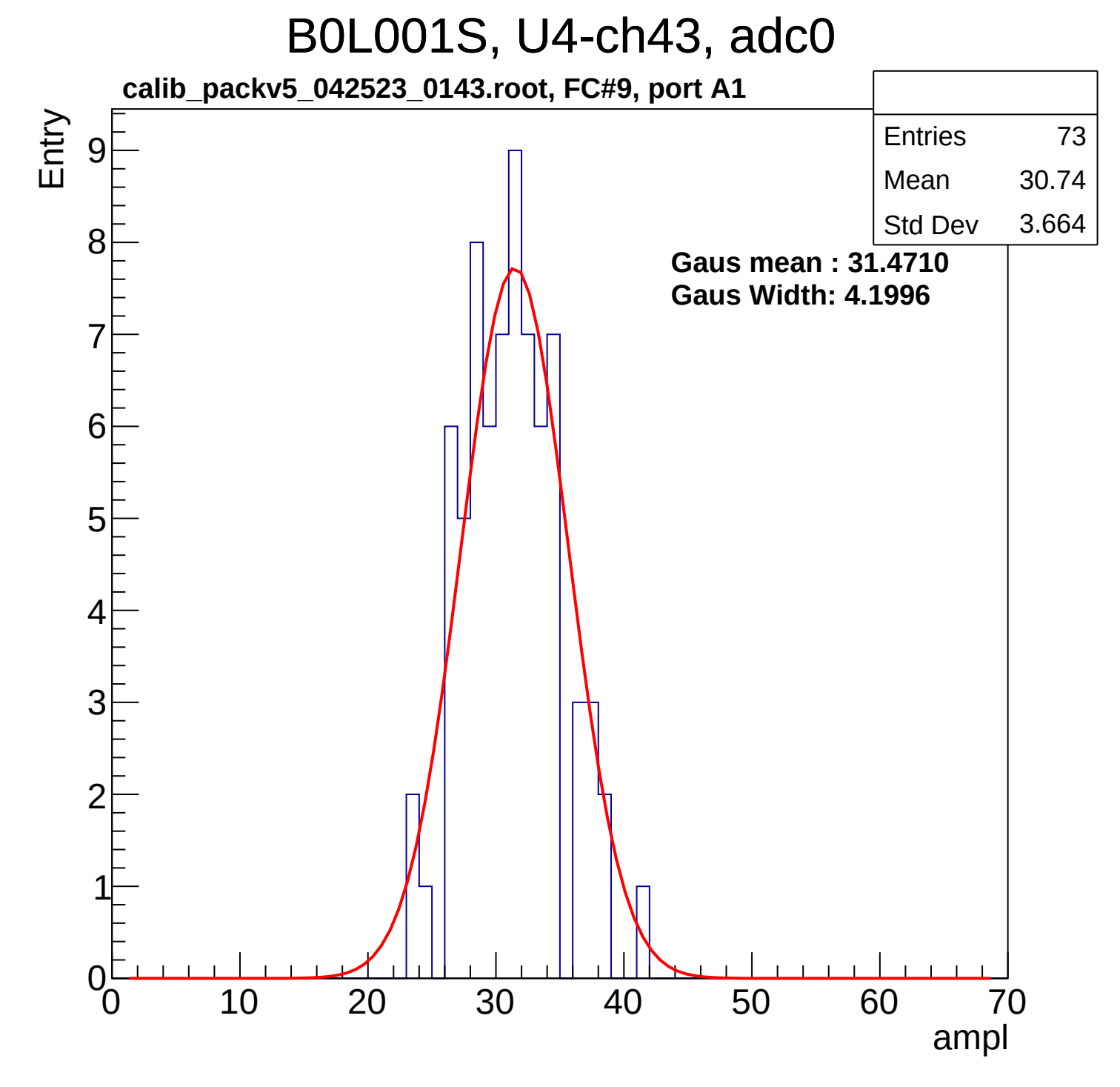
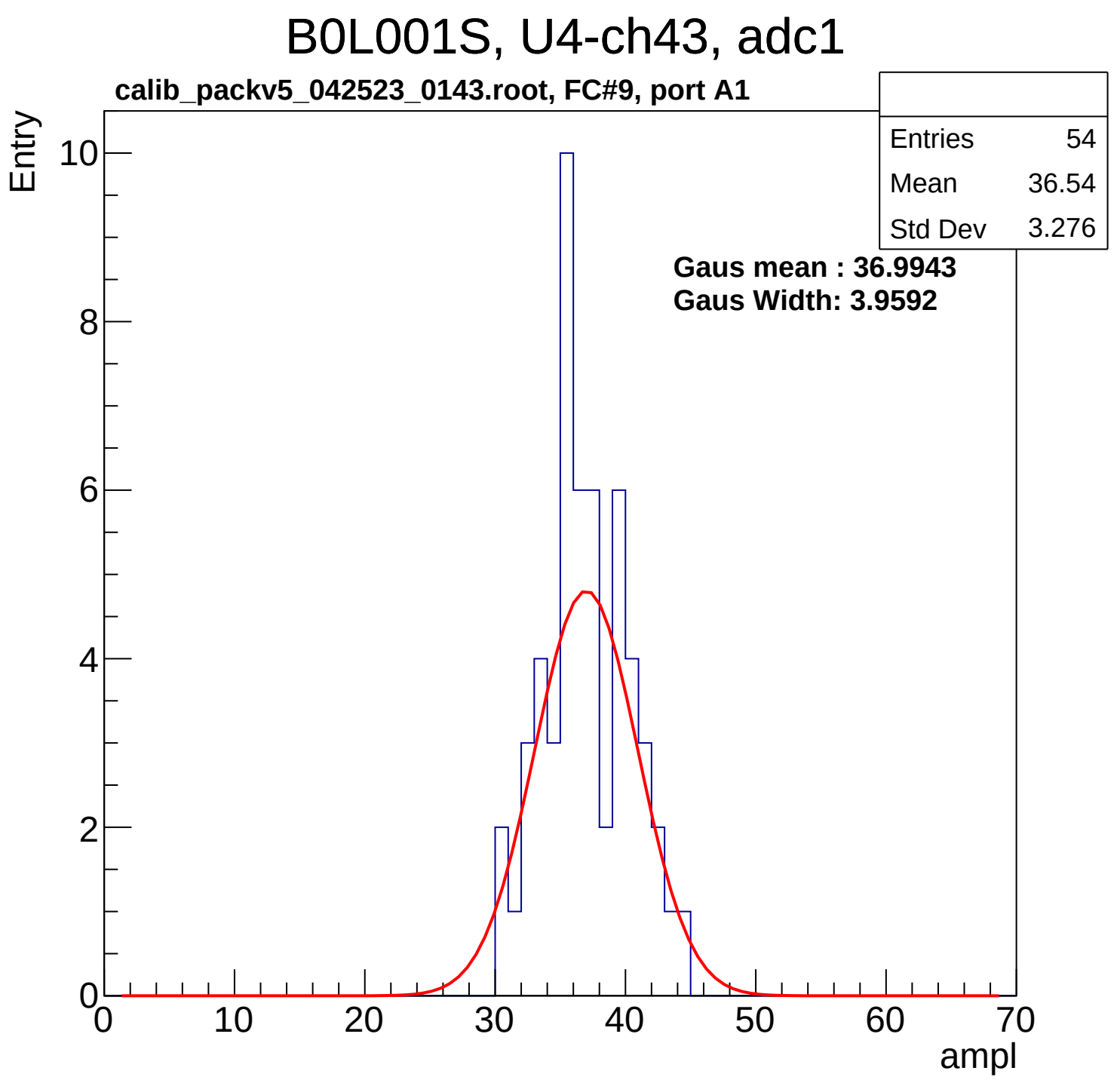
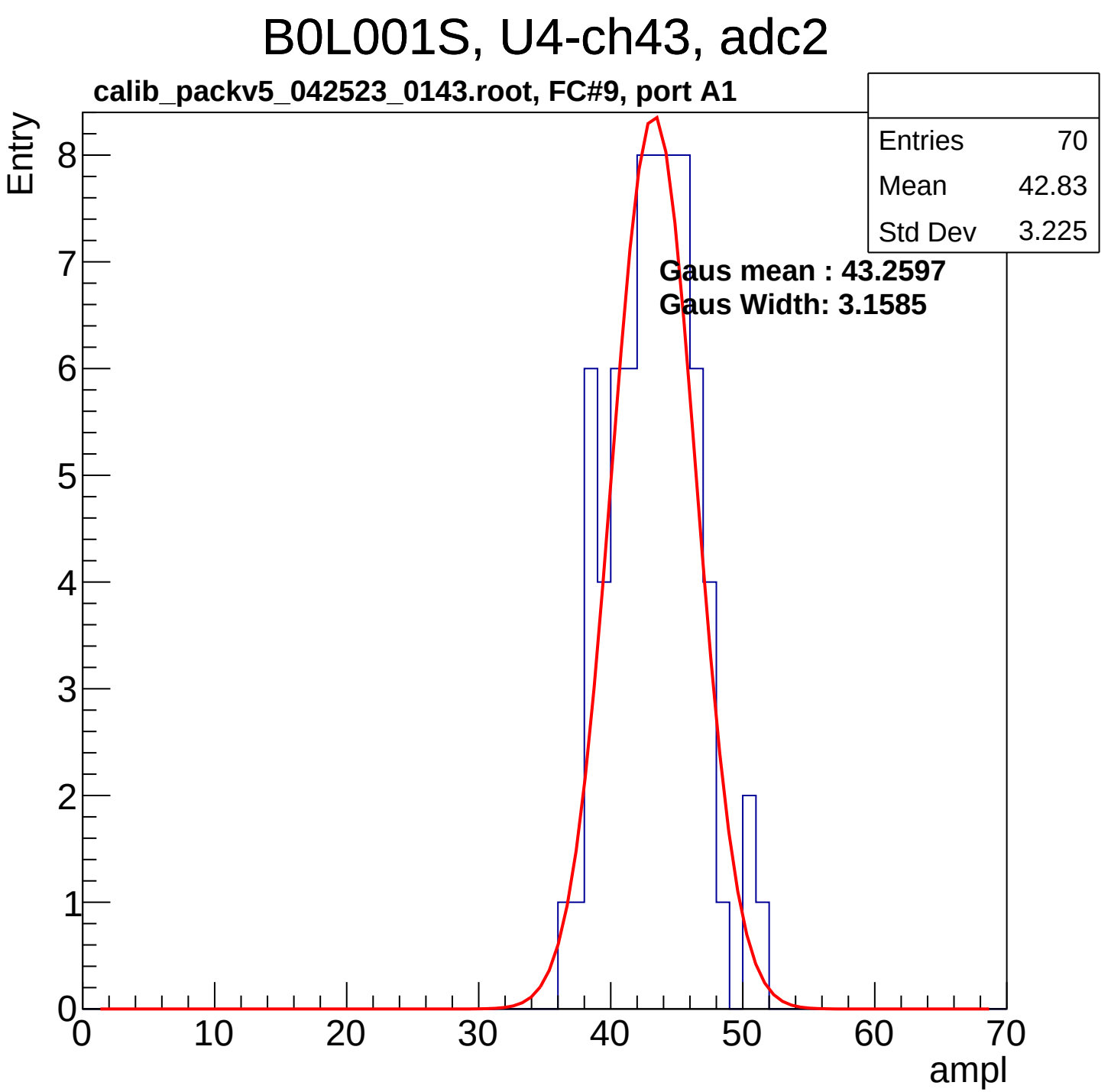
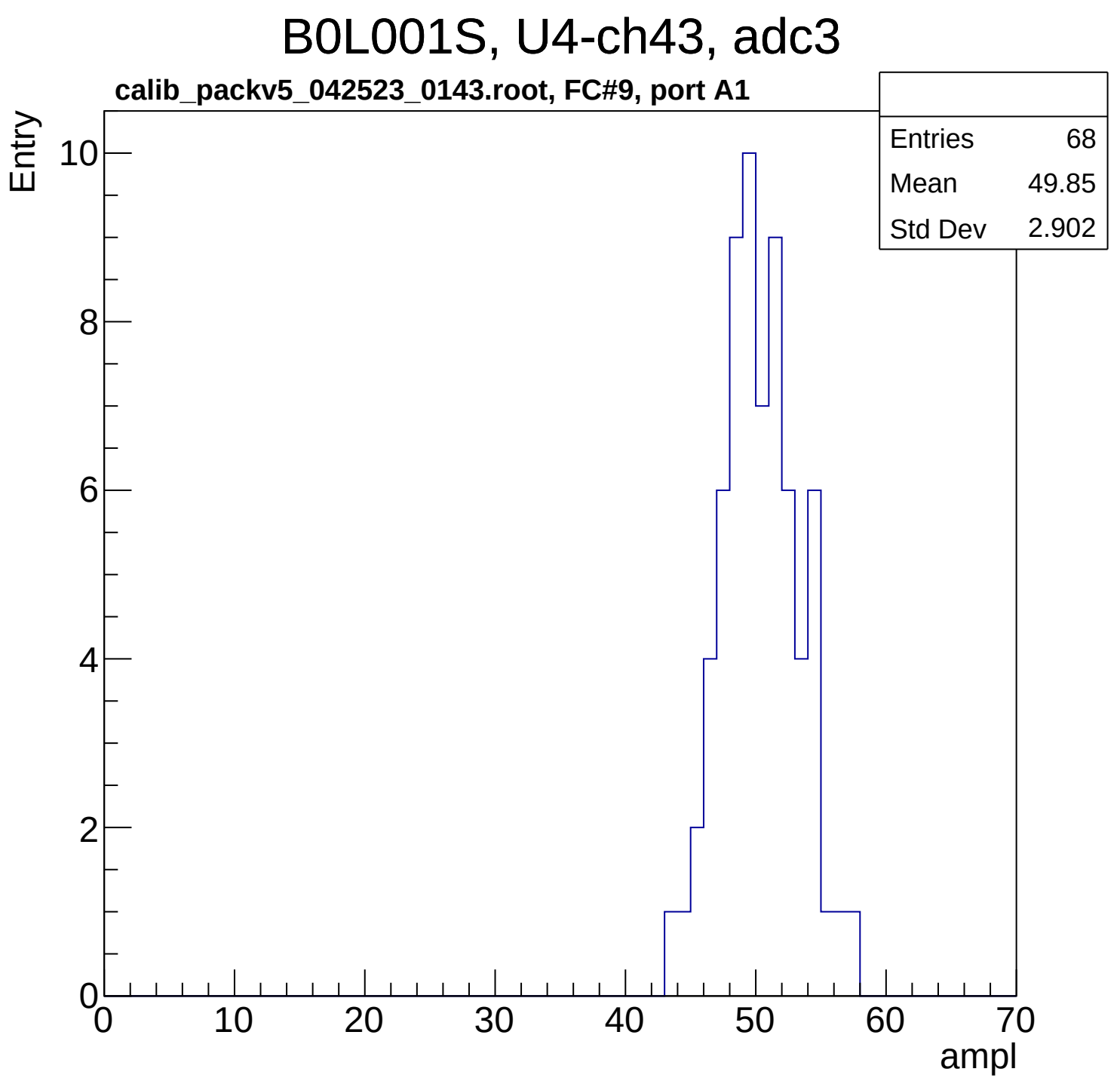

B0L001S, U4-ch42, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

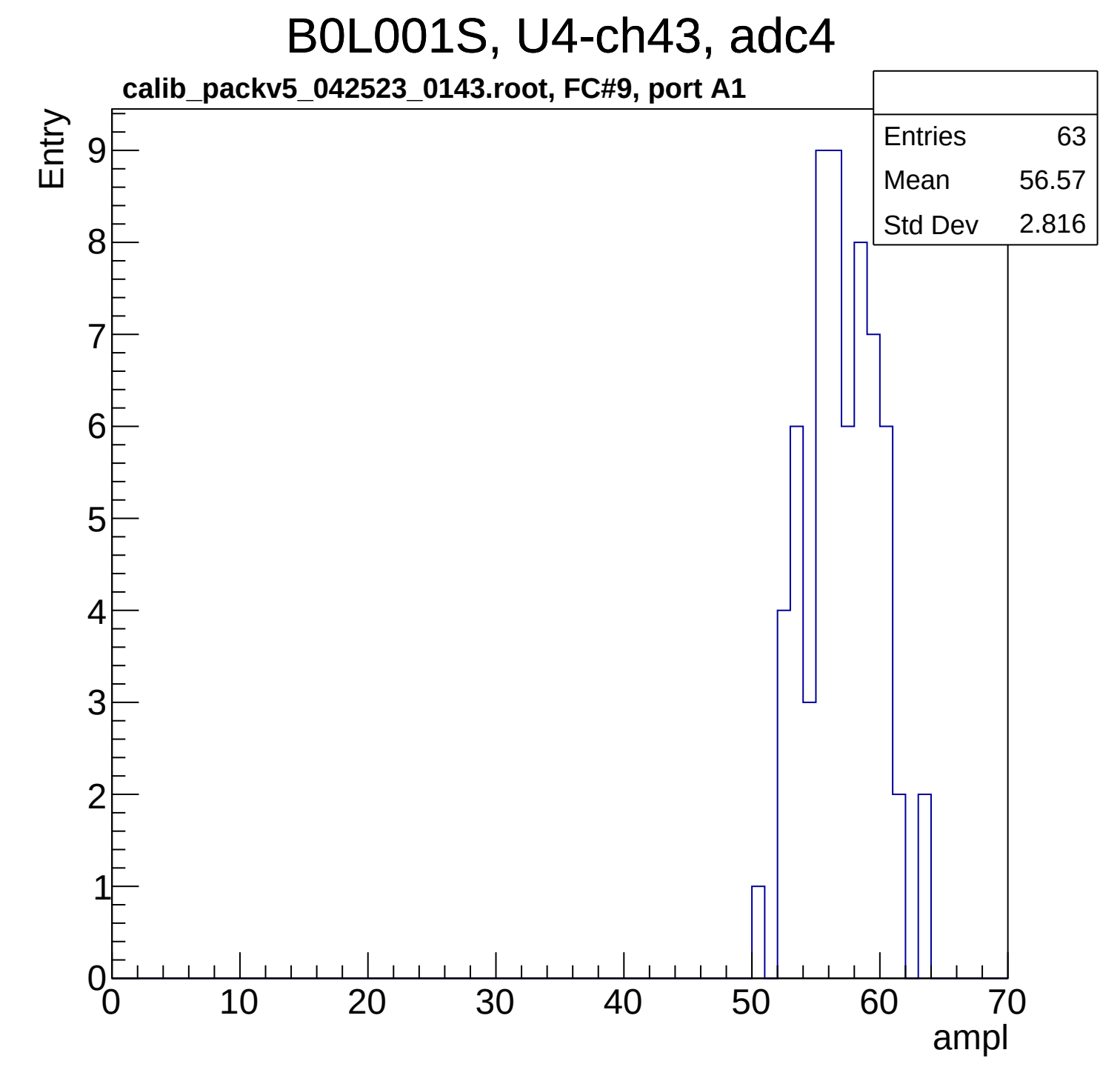

B0L001S, U4-ch42, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

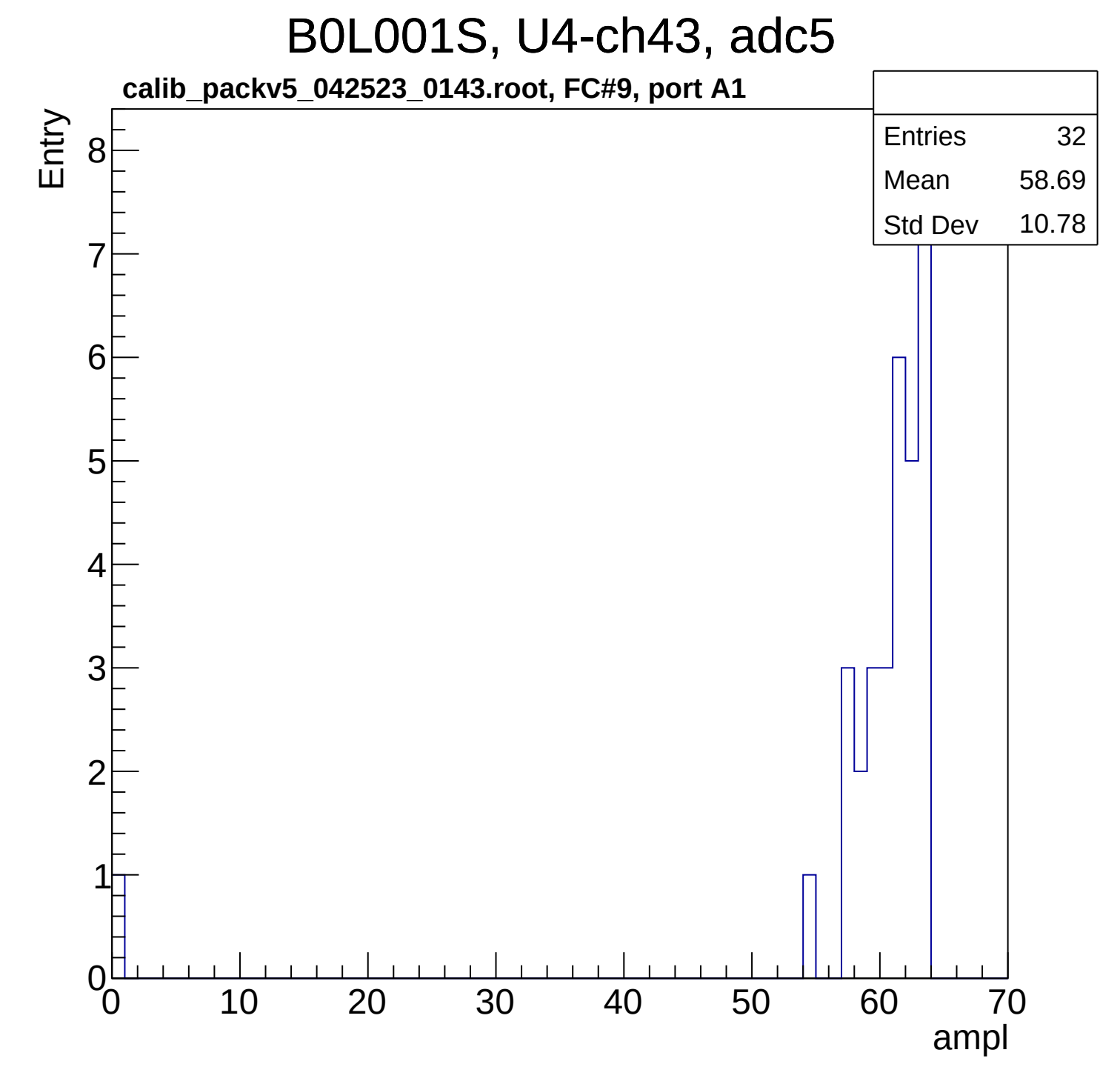

B0L001S, U4-ch43, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.74
Std Dev 3.664
Gaus mean : 31.4710
Gaus Width: 4.1996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

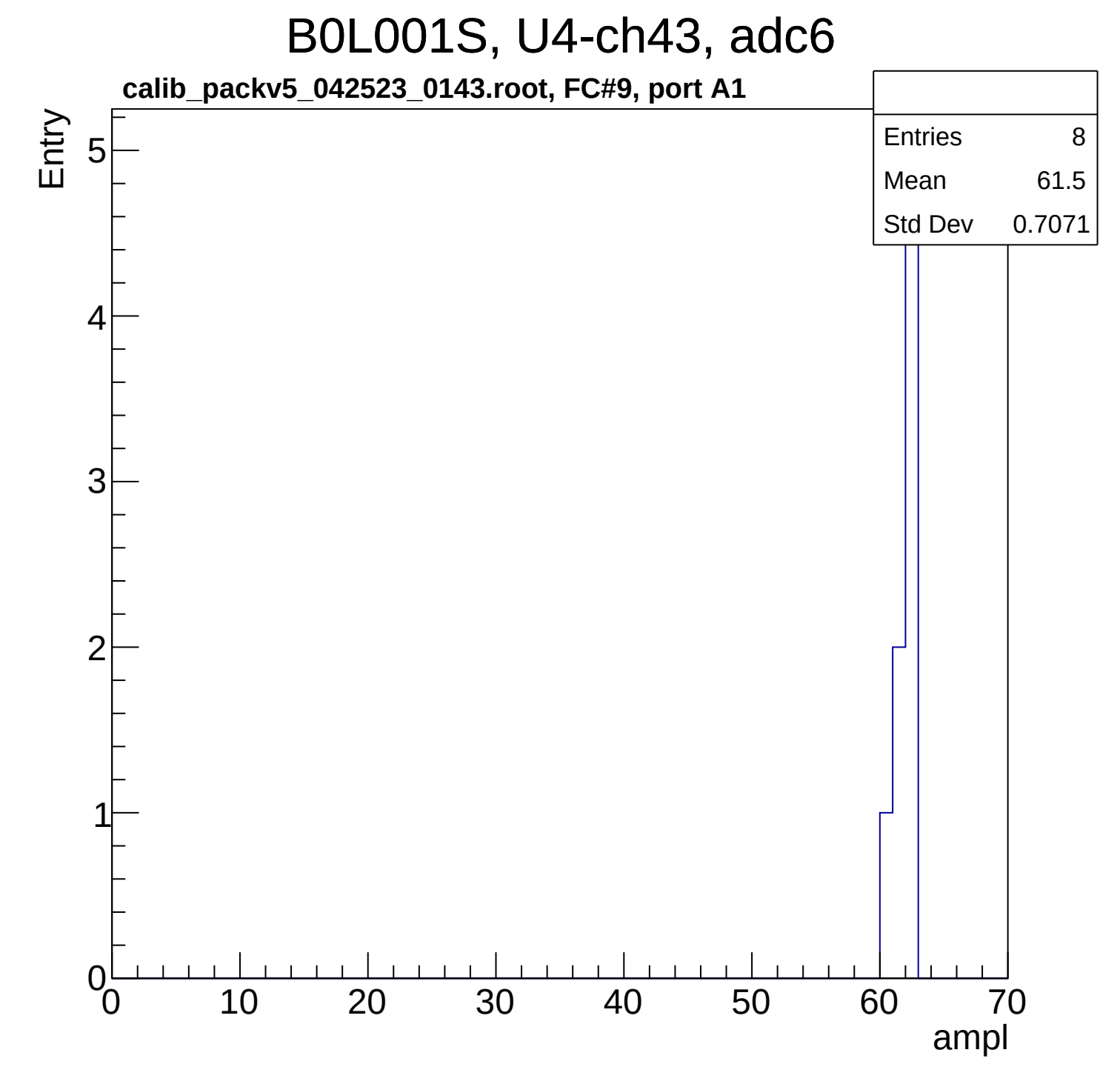

B0L001S, U4-ch43, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 36.54
Std Dev 3.276
Gaus mean : 36.9943
Gaus Width: 3.9592
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

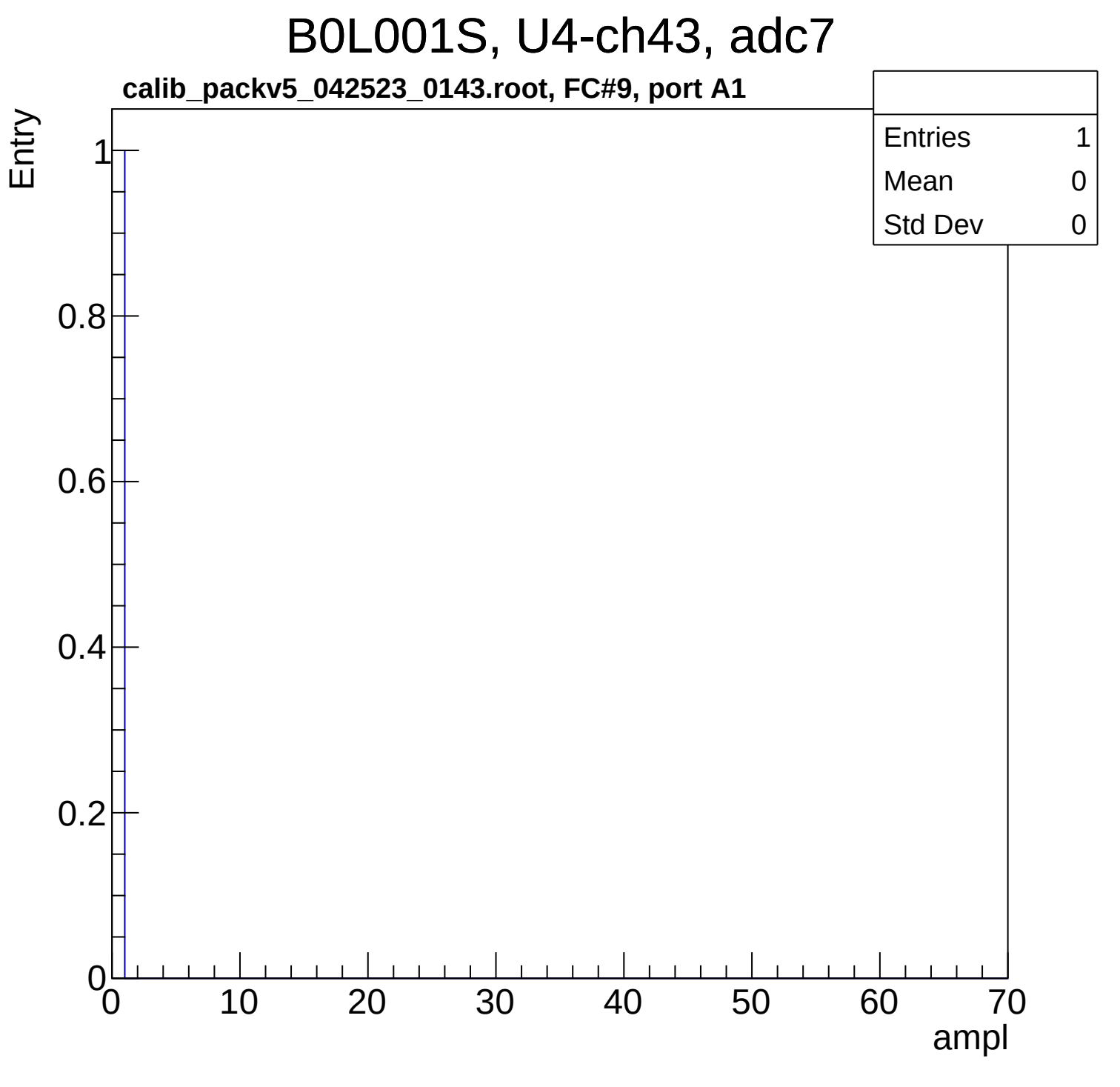

B0L001S, U4-ch43, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 42.83
Std Dev 3.225
Gaus mean : 43.2597
Gaus Width: 3.1585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

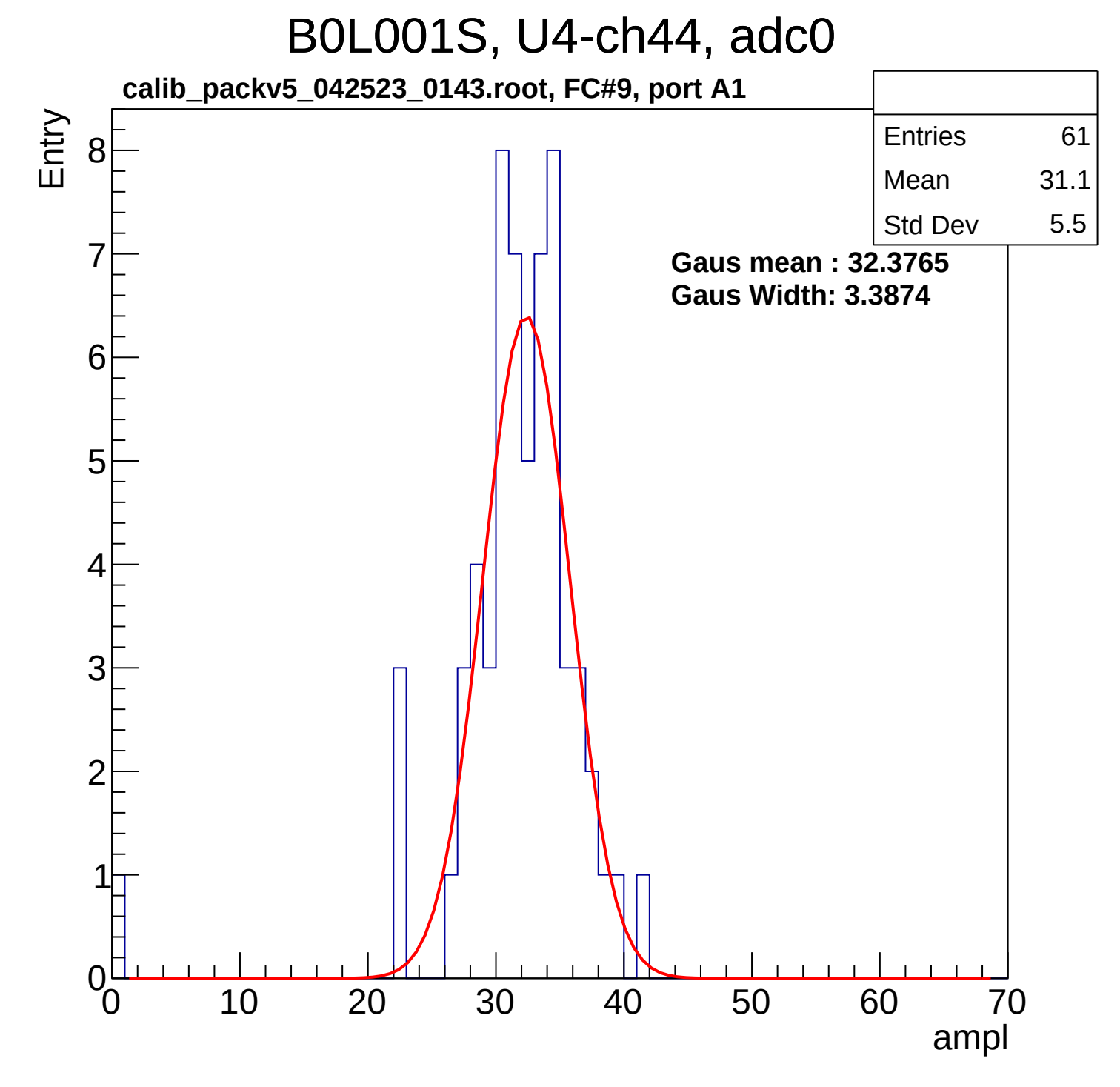

B0L001S, U4-ch43, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 49.85
Std Dev 2.902
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

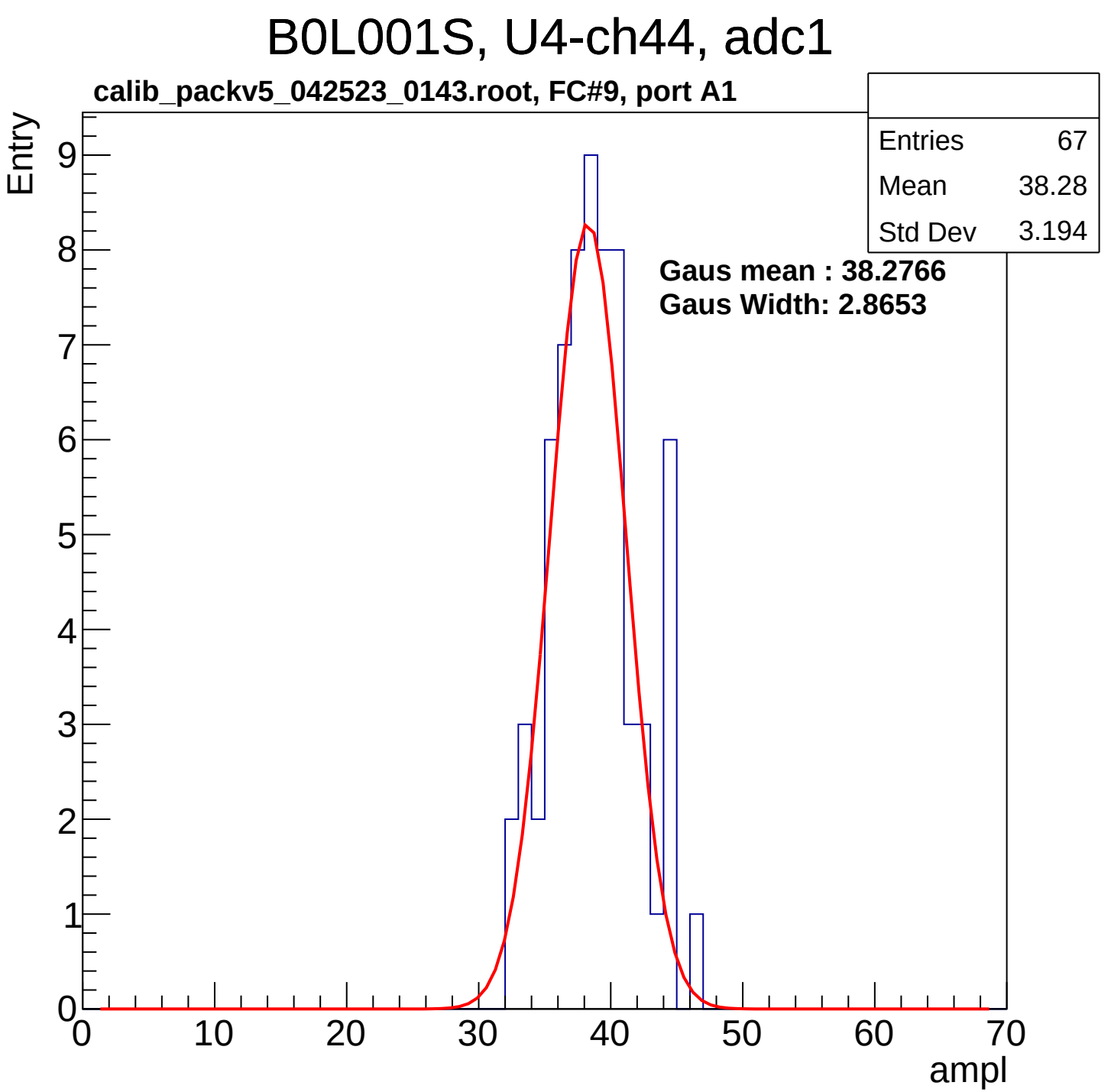

B0L001S, U4-ch43, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.57
Std Dev 2.816
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

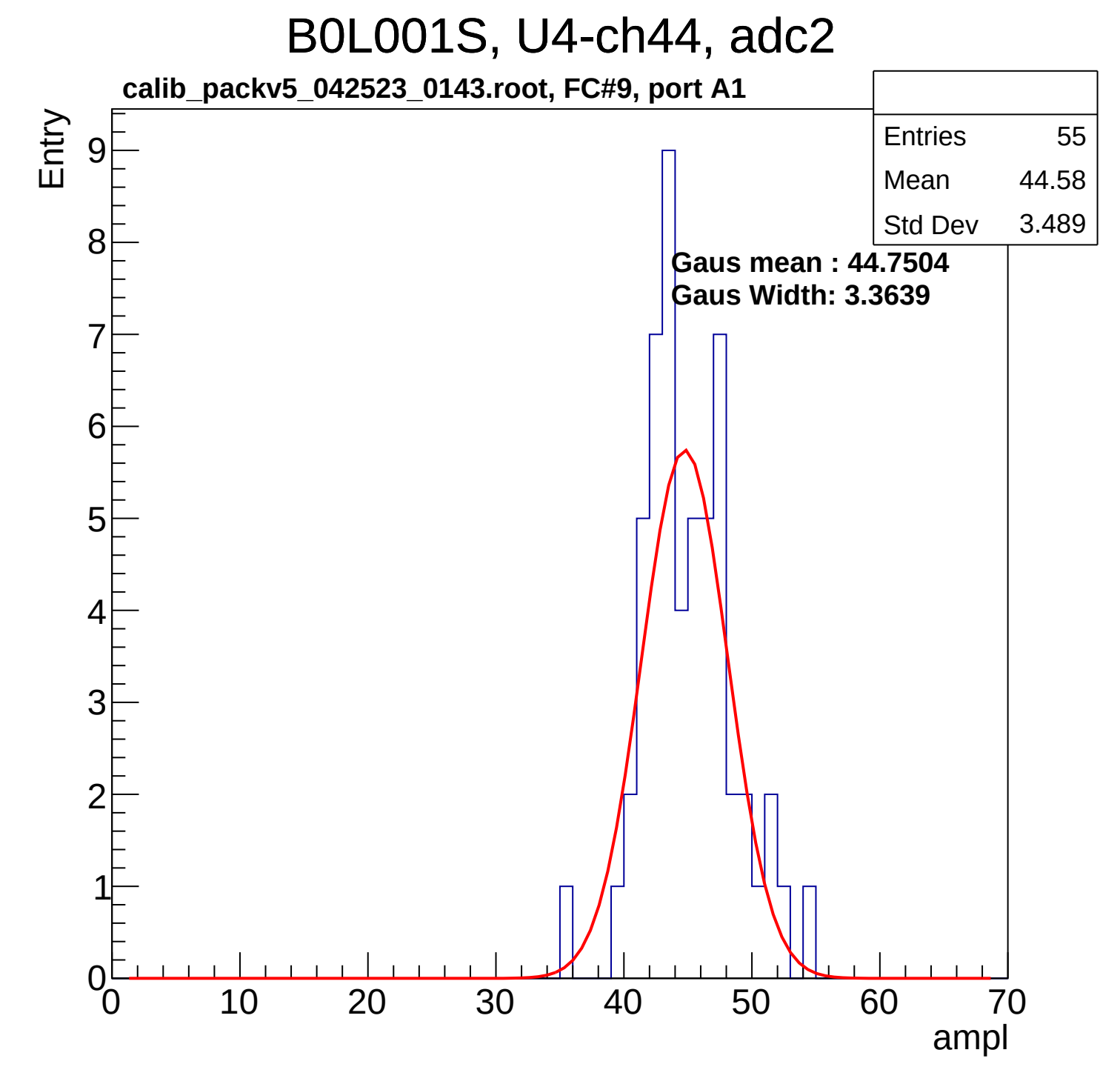

B0L001S, U4-ch43, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.69
Std Dev 10.78
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch43, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.5
Std Dev 0.7071
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch43, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch44, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.1
Std Dev 5.5
Gaus mean : 32.3765
Gaus Width: 3.3874
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch44, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.28
Std Dev 3.194
Gaus mean : 38.2766
Gaus Width: 2.8653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch44, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 44.58
Std Dev 3.489
Gaus mean : 44.7504
Gaus Width: 3.3639
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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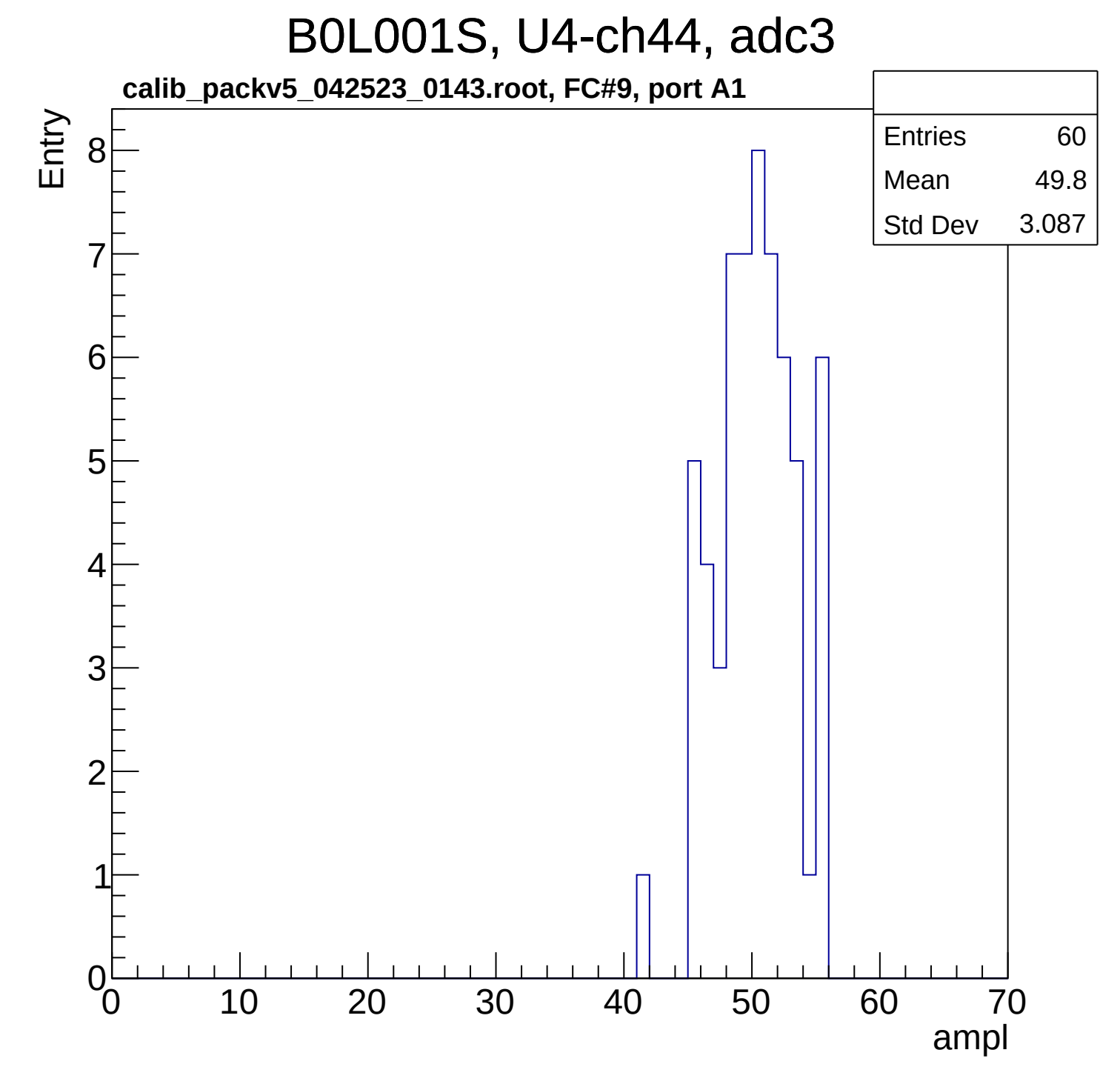
B0L001S, U4-ch44, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 49.8
Std Dev 3.087
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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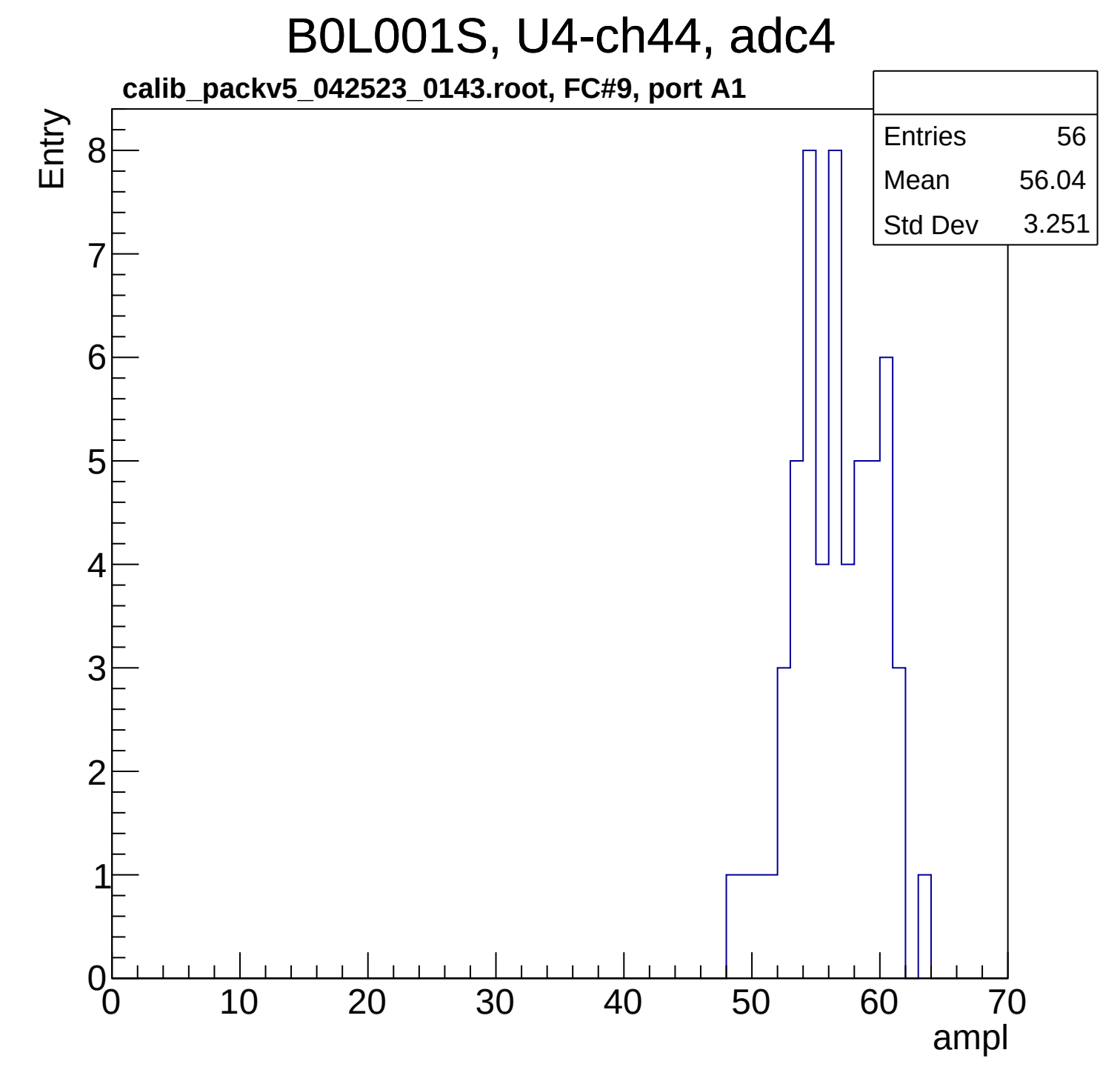
B0L001S, U4-ch44, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.04
Std Dev 3.251
Entry
ampl
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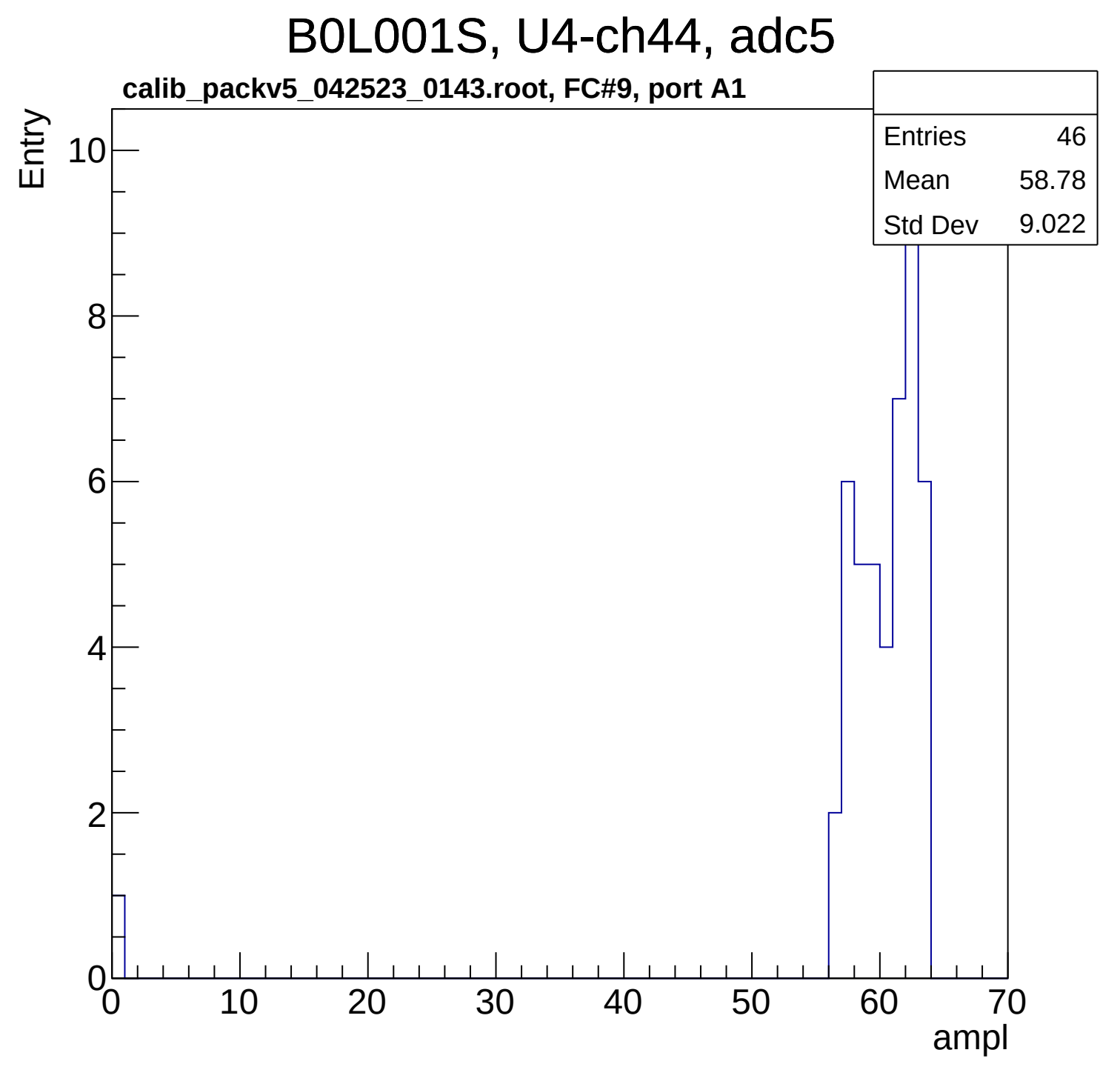
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch44, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.78
Std Dev 9.022
Entry
0
2
4
6
8
10
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10
20
30
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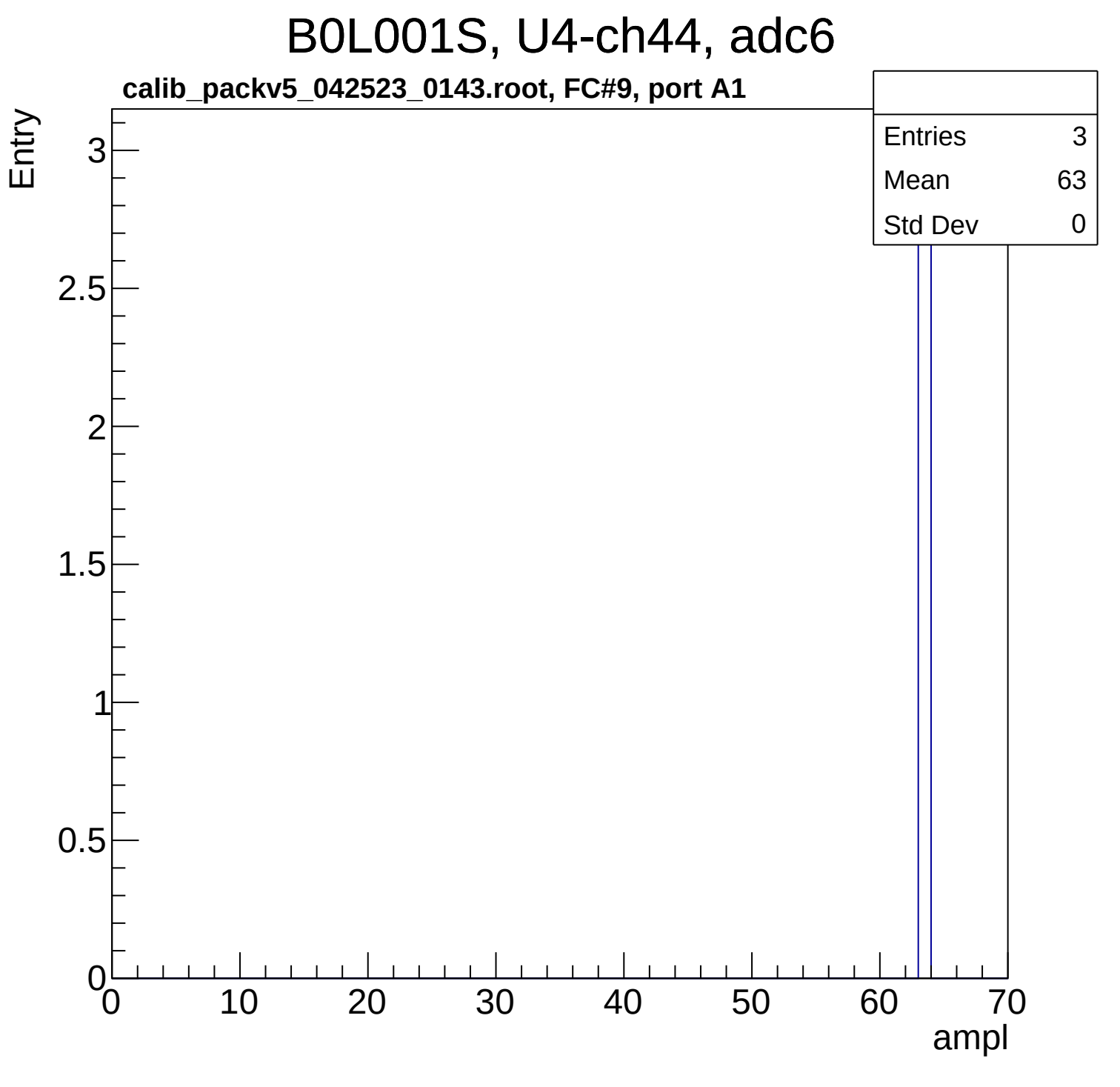
50
60
70
ampl


B0L001S, U4-ch44, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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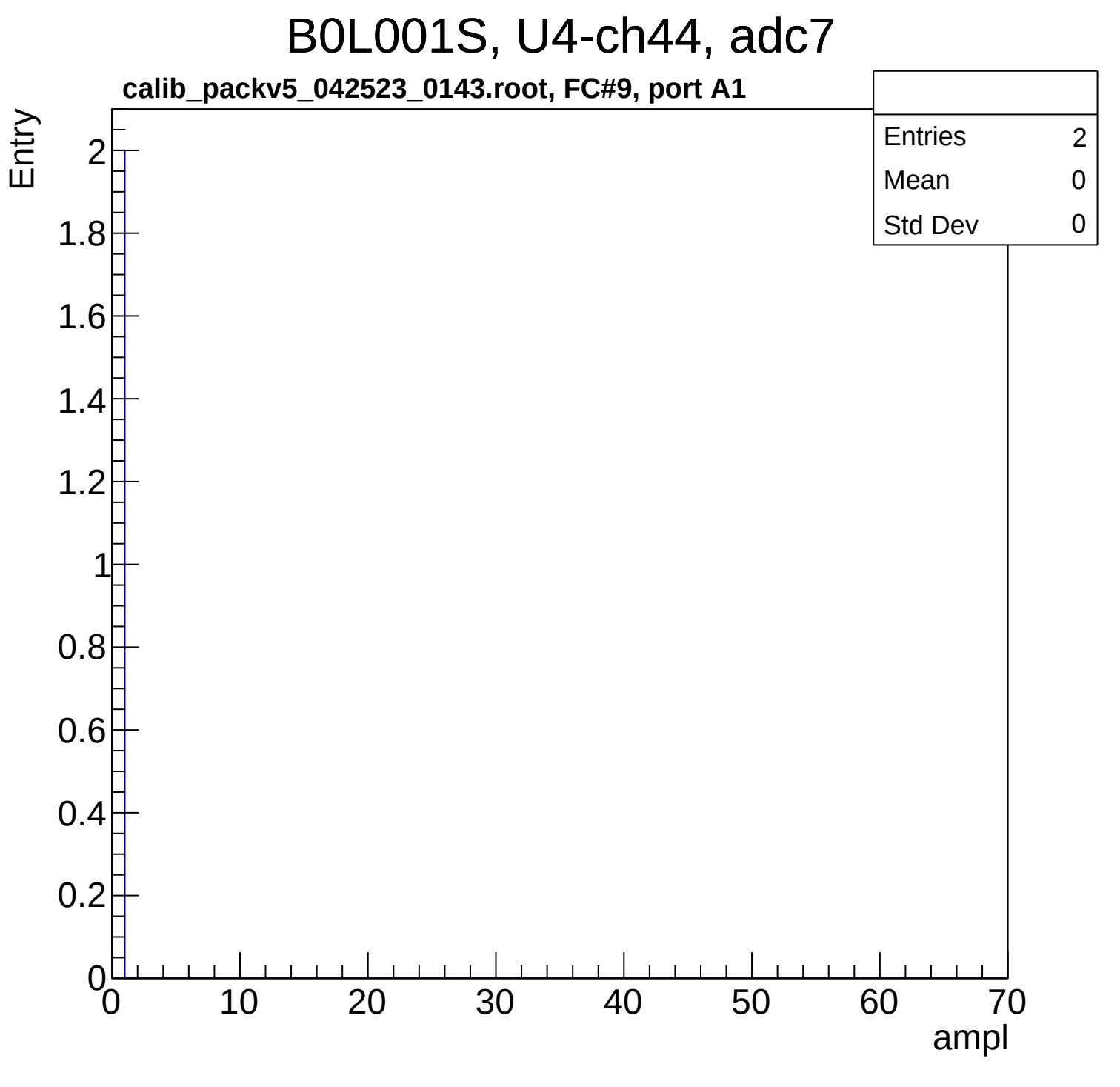
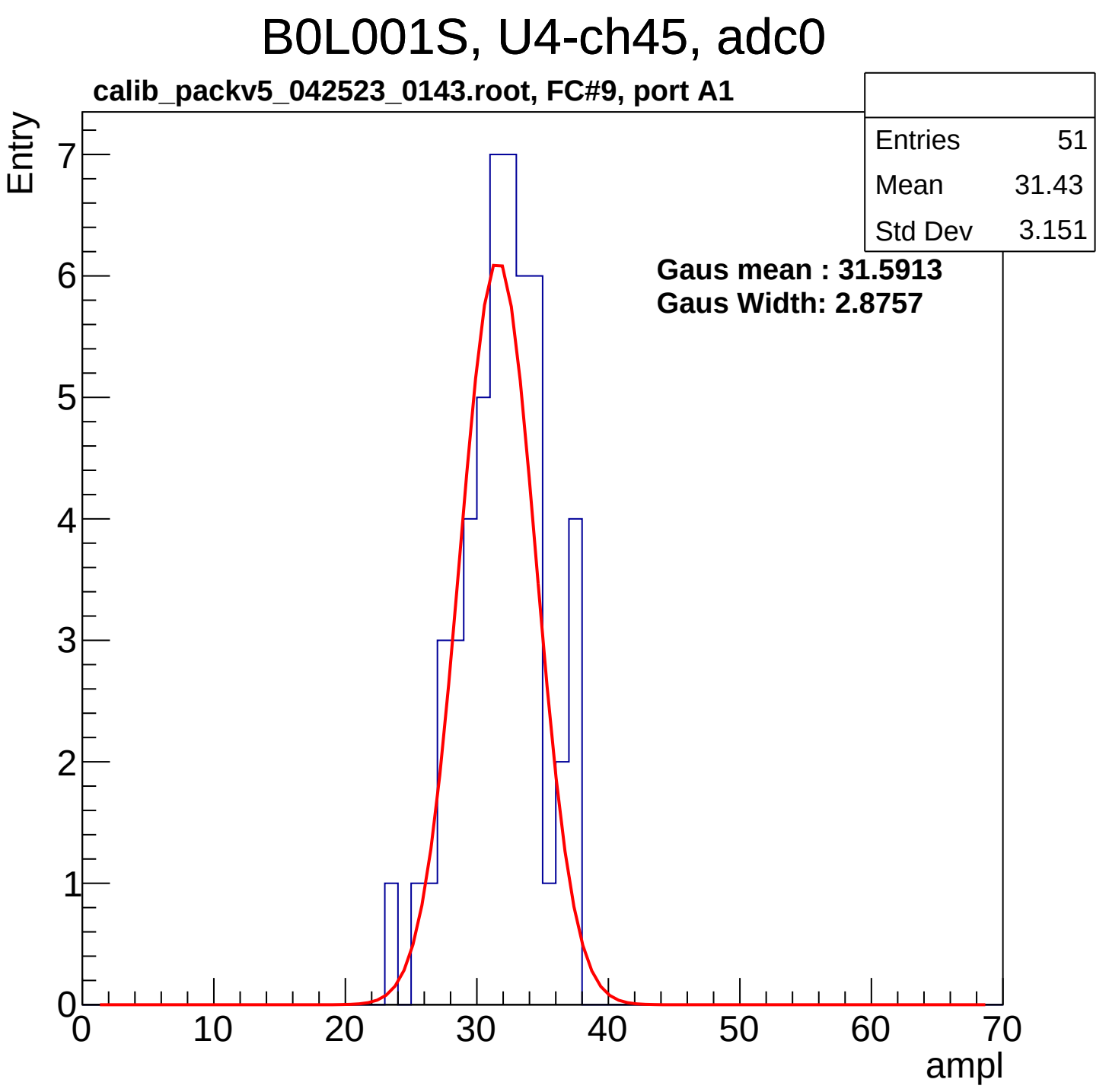
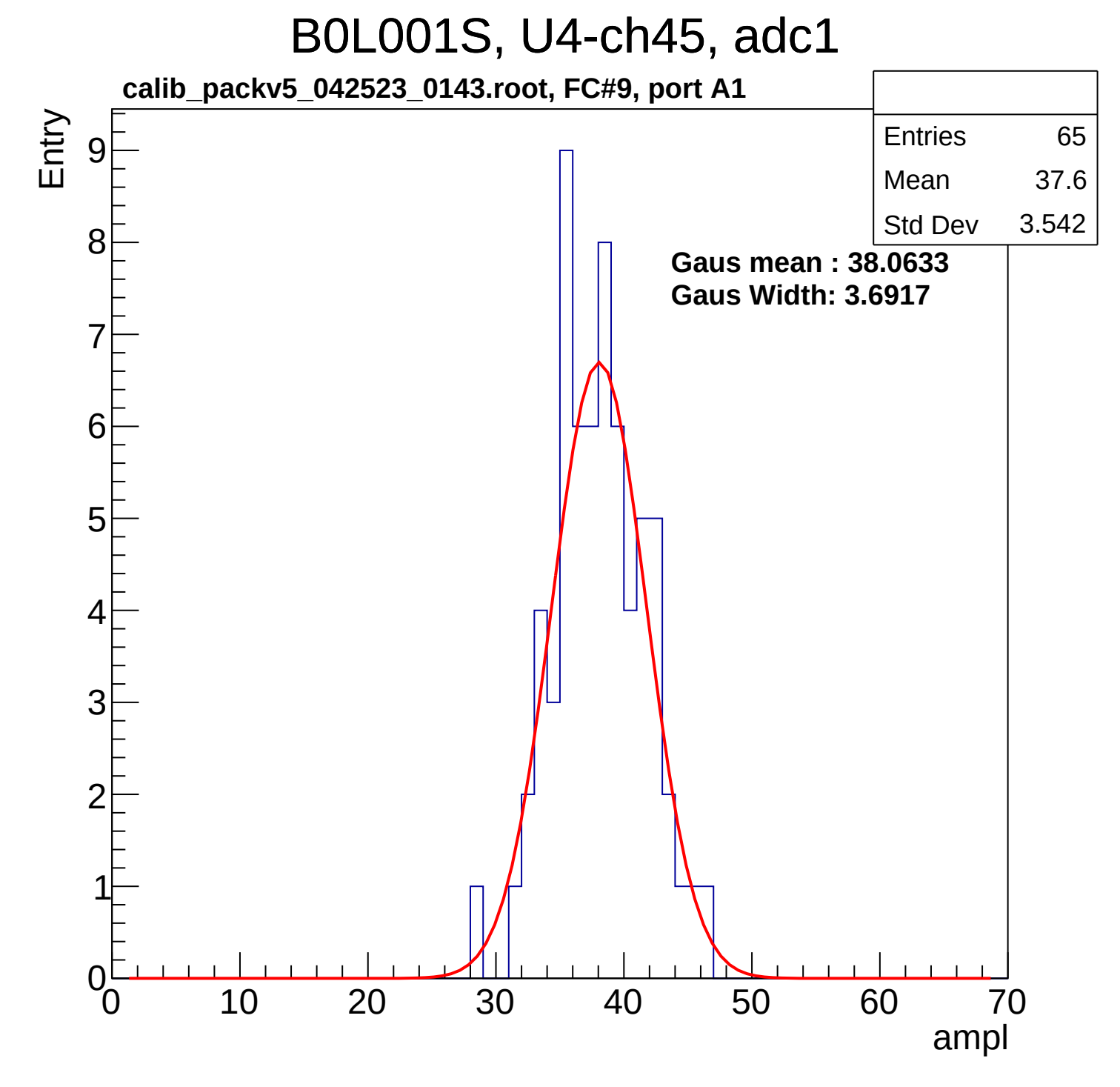
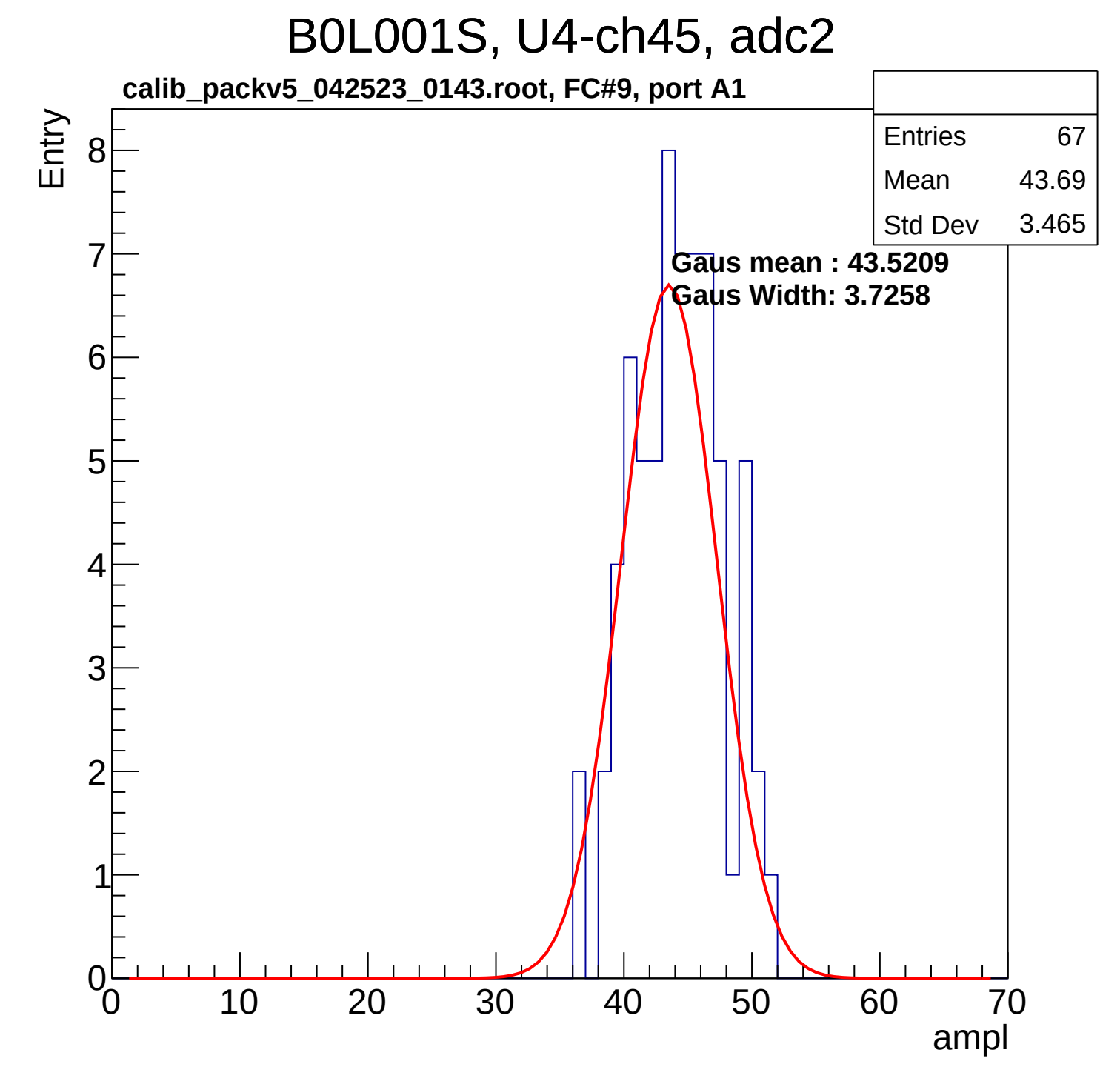
40
50
60
70


B0L001S, U4-ch44, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch45, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 31.43
Std Dev 3.151
Gaus mean : 31.5913
Gaus Width: 2.8757
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch45, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.6
Std Dev 3.542
Gaus mean : 38.0633
Gaus Width: 3.6917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch45, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.69
Std Dev 3.465
Gaus mean : 43.5209
Gaus Width: 3.7258
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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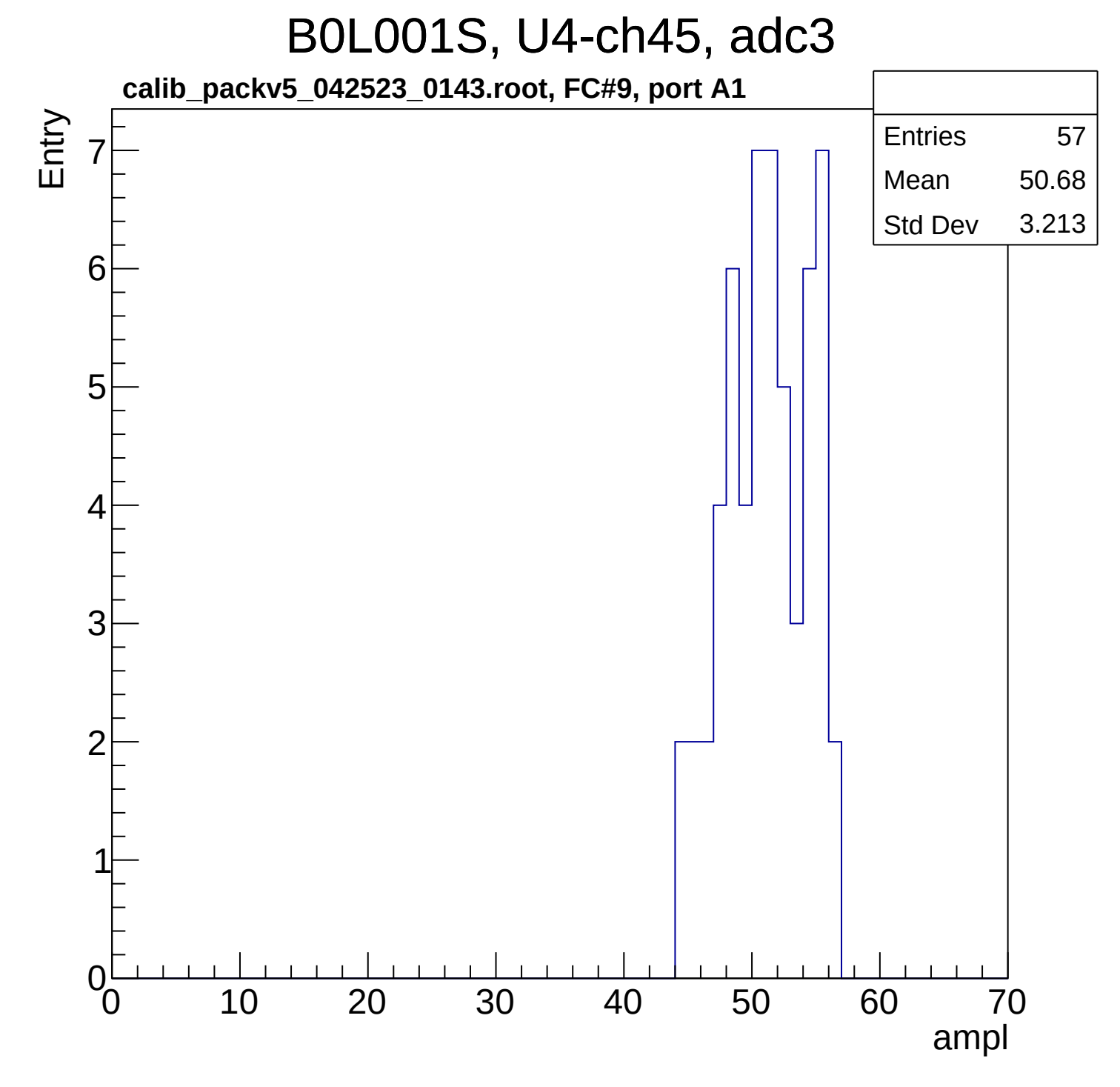
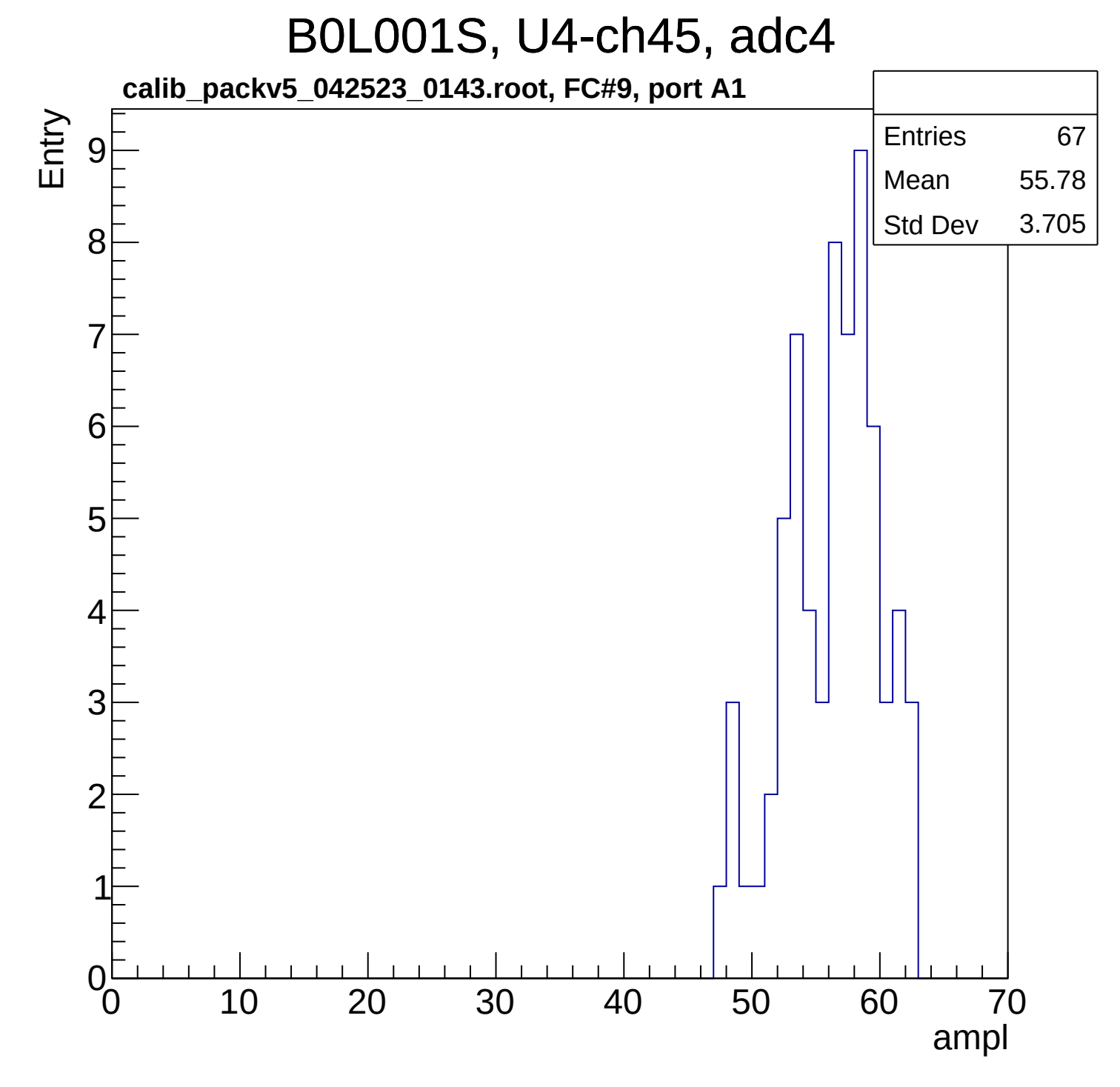
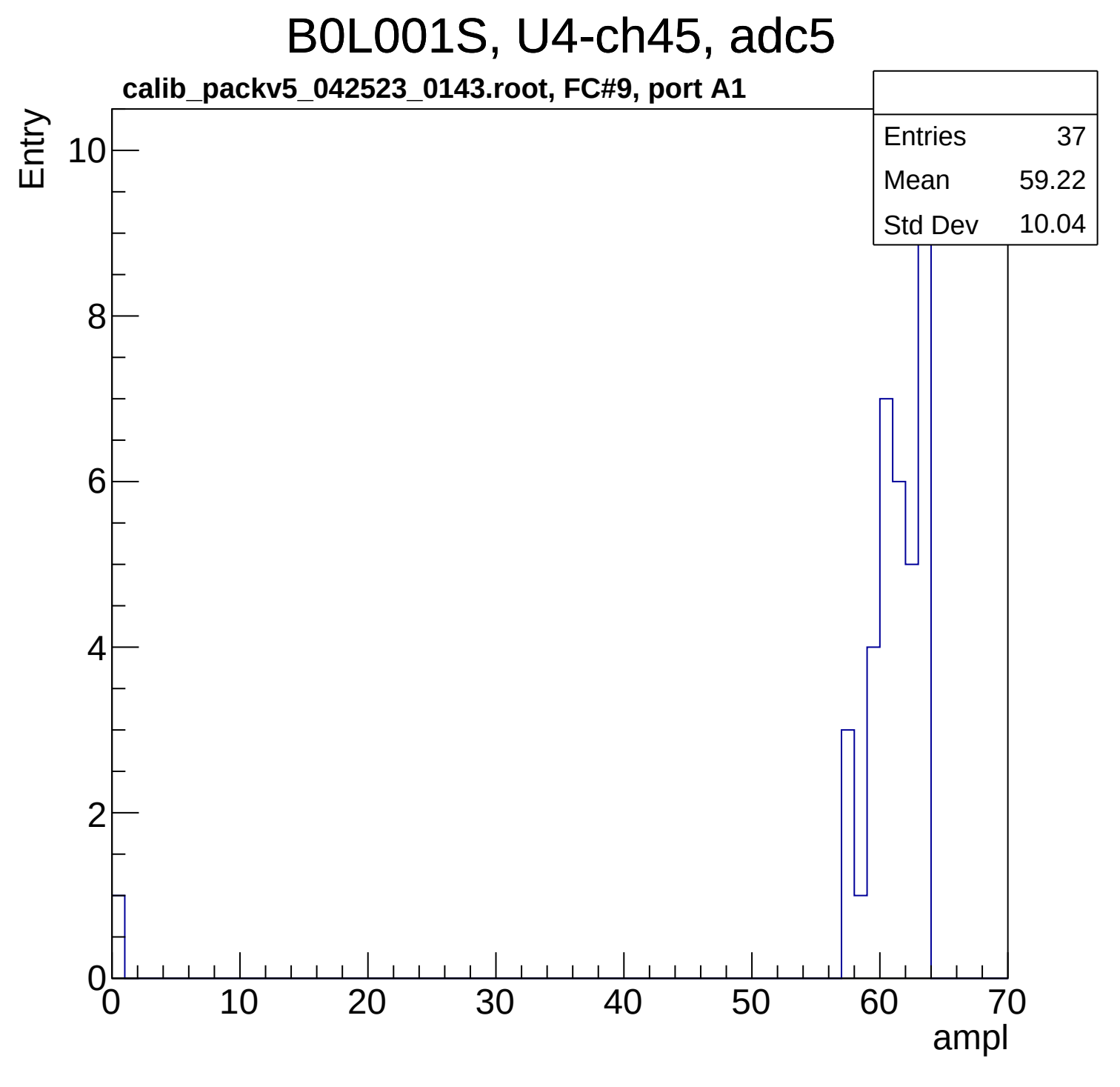
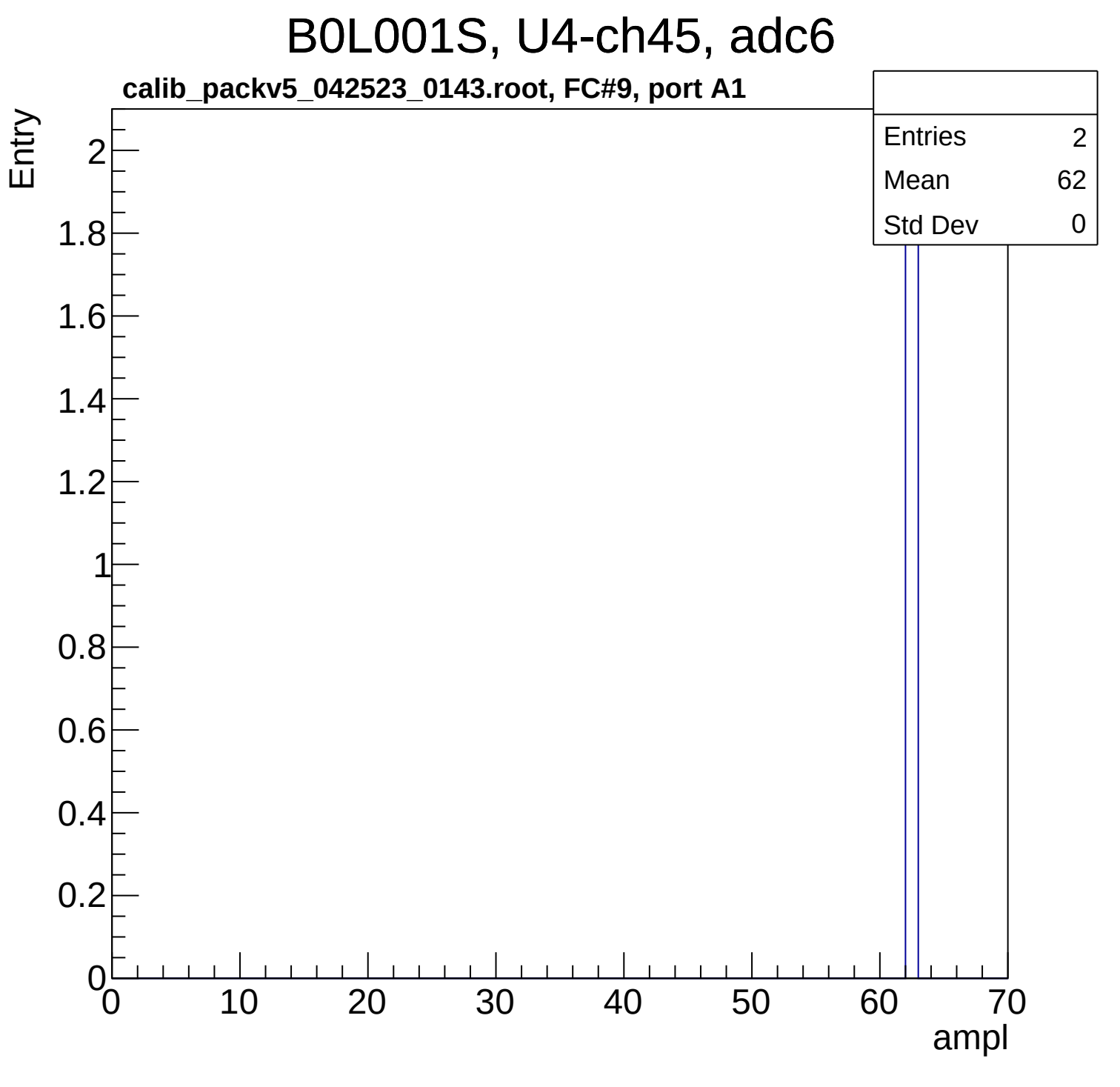

B0L001S, U4-ch45, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 50.68
Std Dev 3.213
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch45, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 55.78
Std Dev 3.705
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch45, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.22
Std Dev 10.04
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch45, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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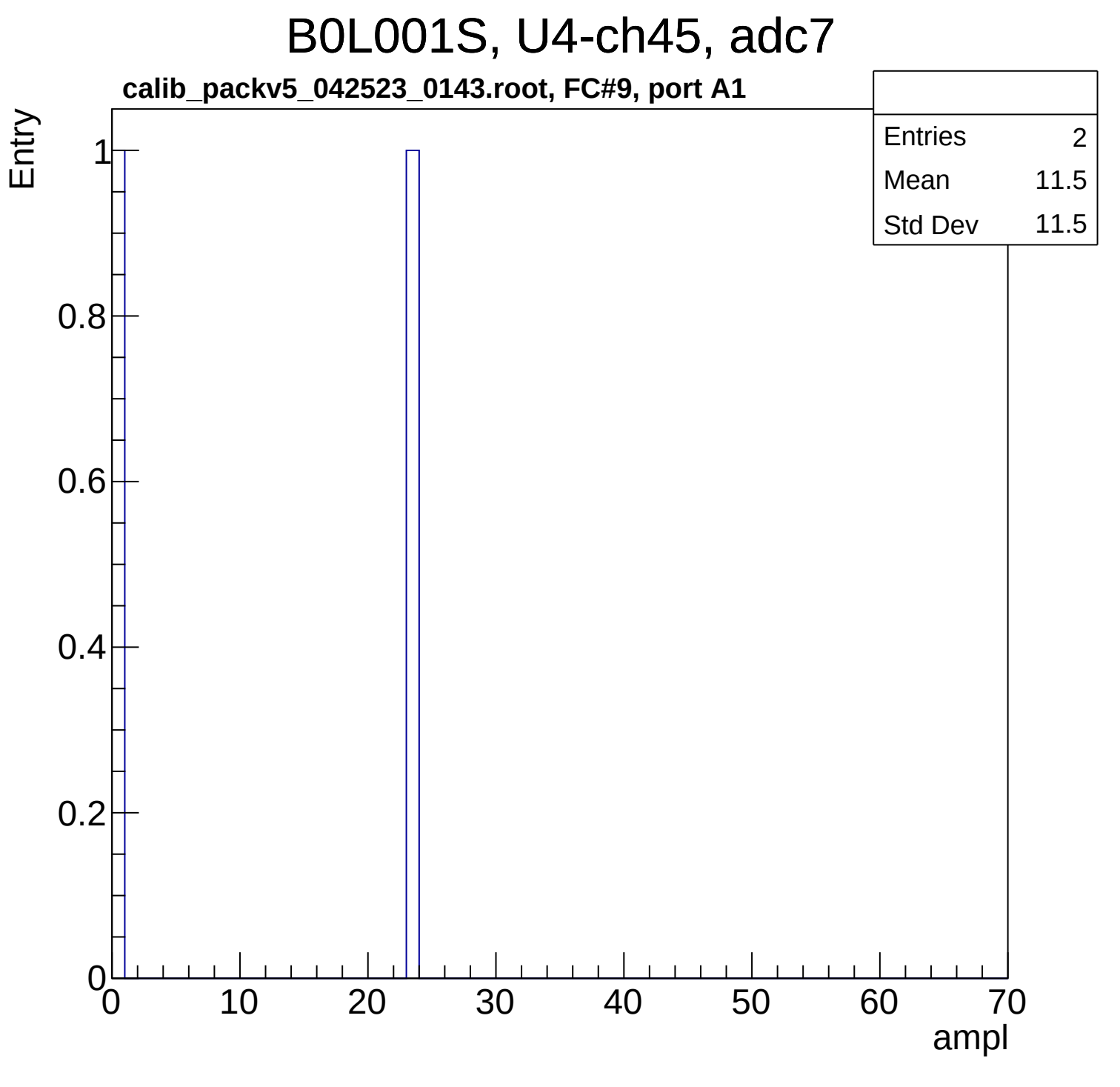
B0L001S, U4-ch45, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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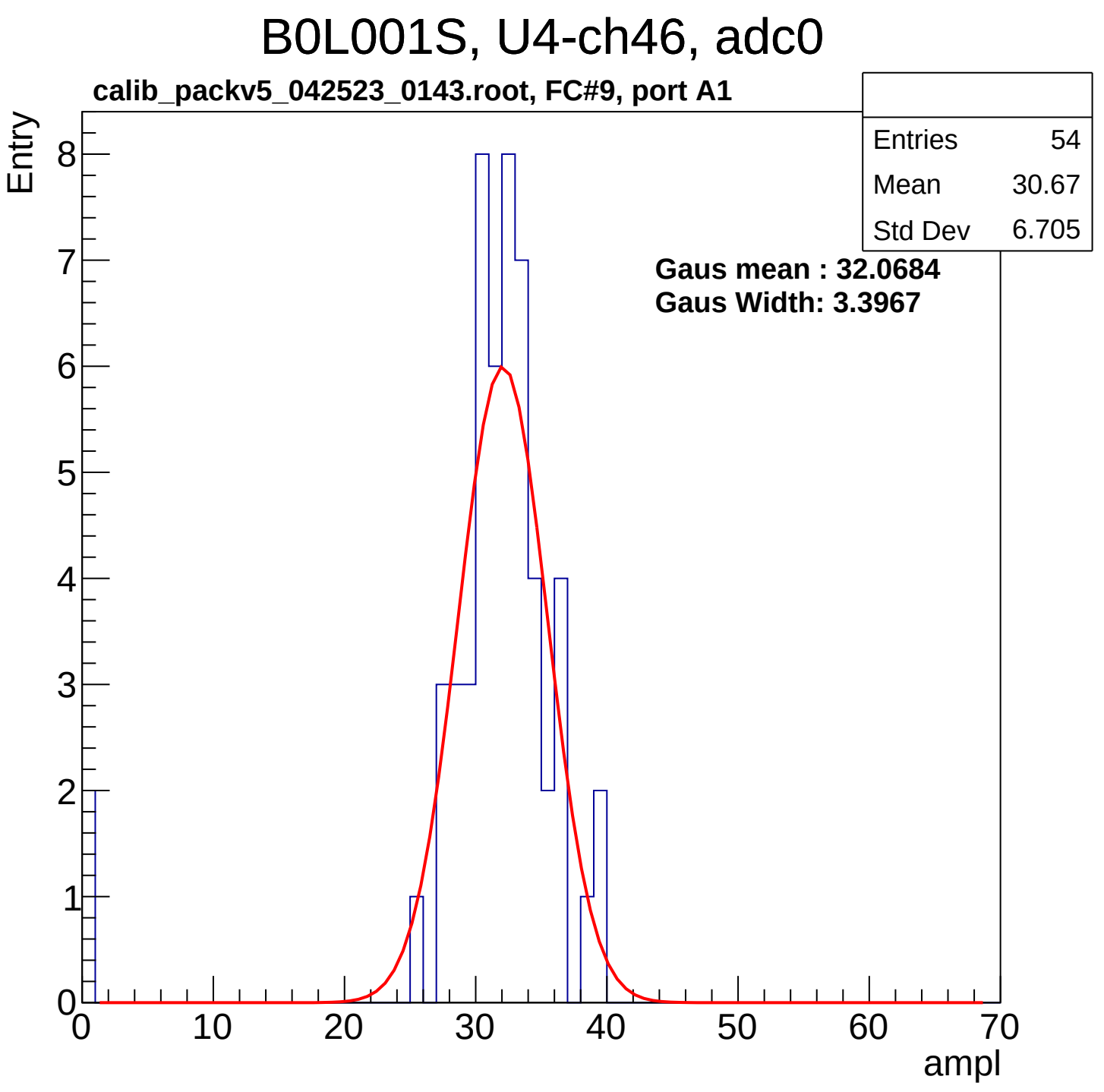
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch46, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 30.67
Std Dev 6.705
Gaus mean : 32.0684
Gaus Width: 3.3967
Entry
ampl
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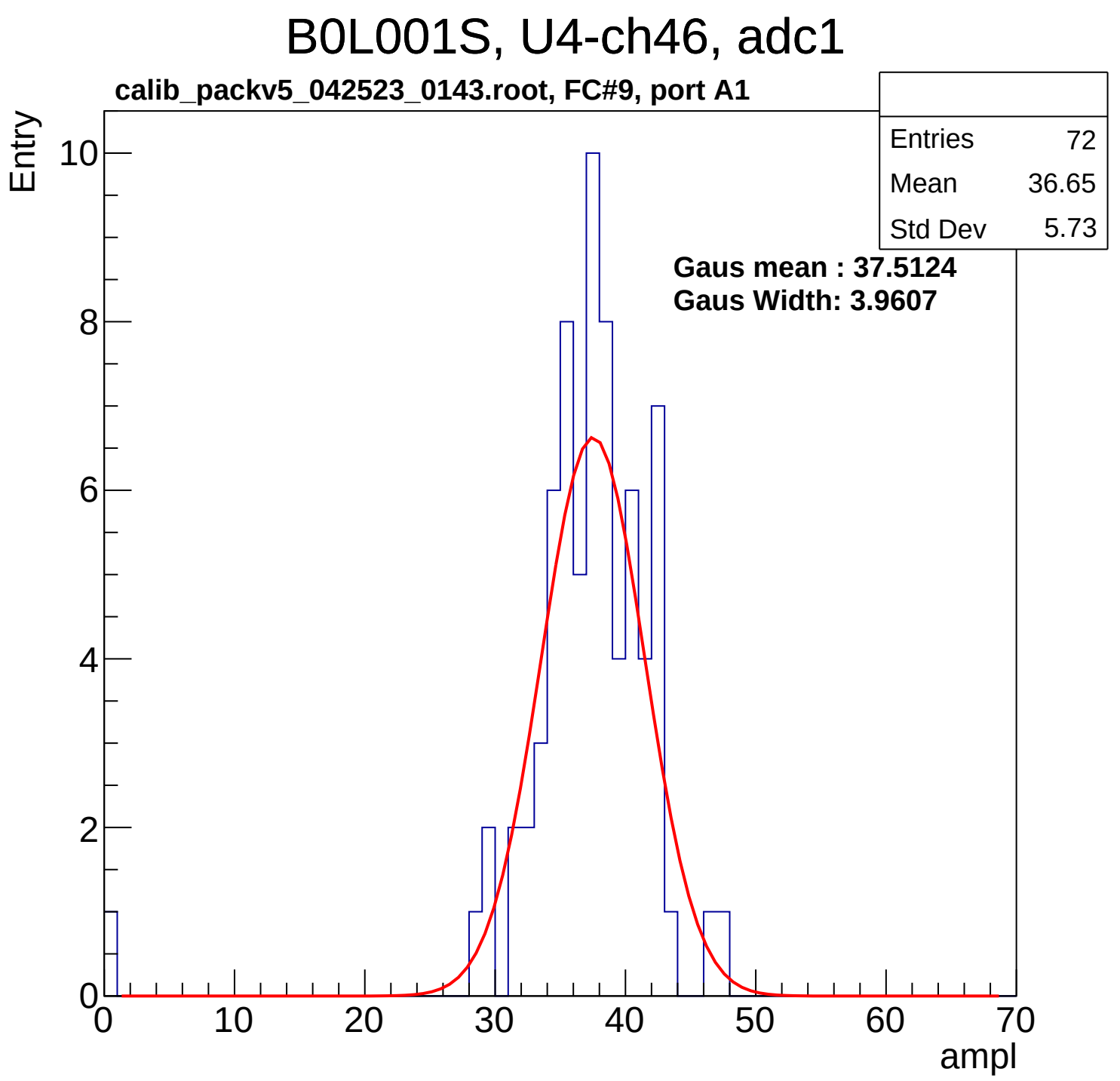
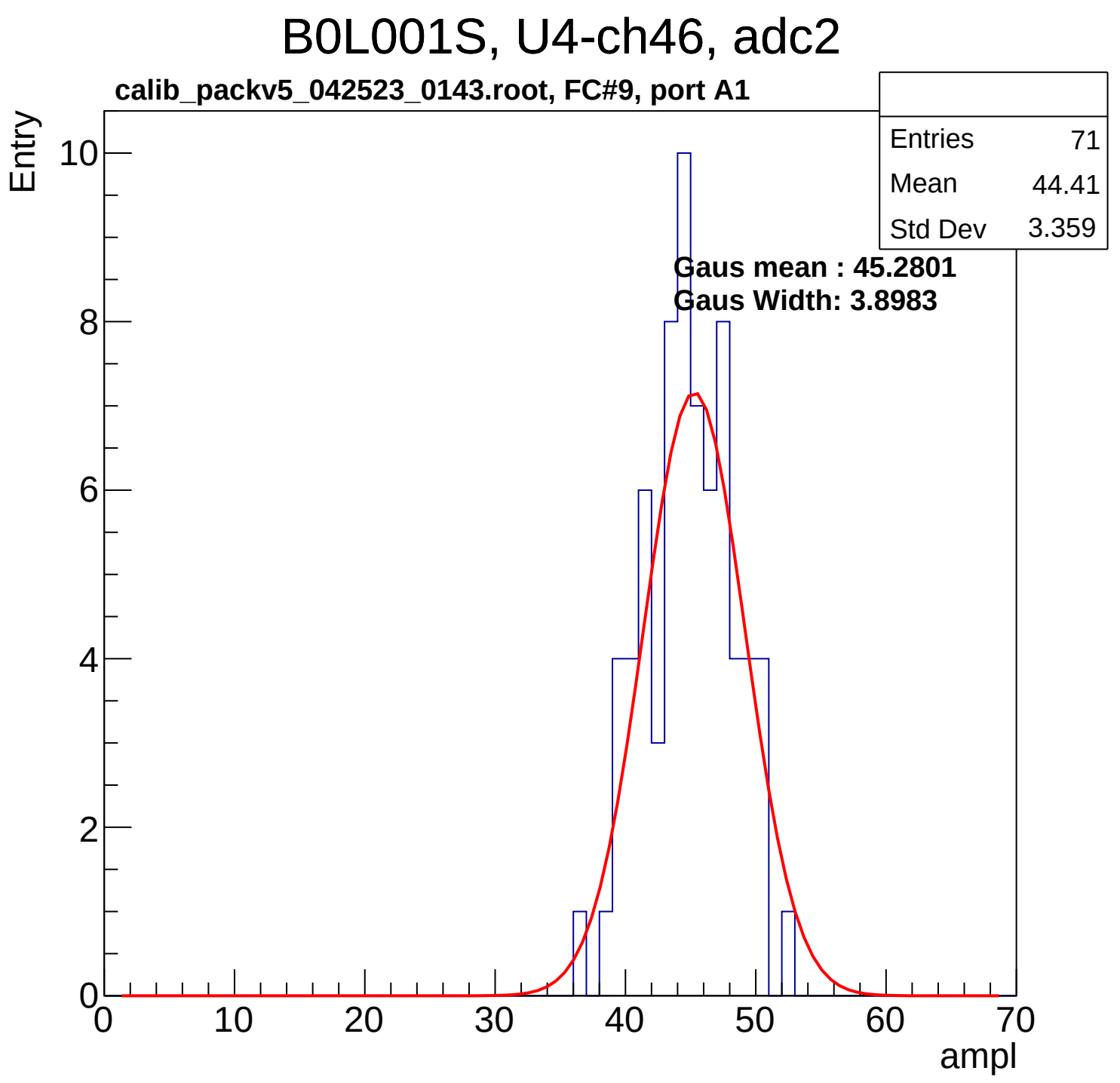
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch46, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 36.65
Std Dev 5.73
Gaus mean : 37.5124
Gaus Width: 3.9607
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch46, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.41
Std Dev 3.359
Gaus mean : 45.2801
Gaus Width: 3.8983
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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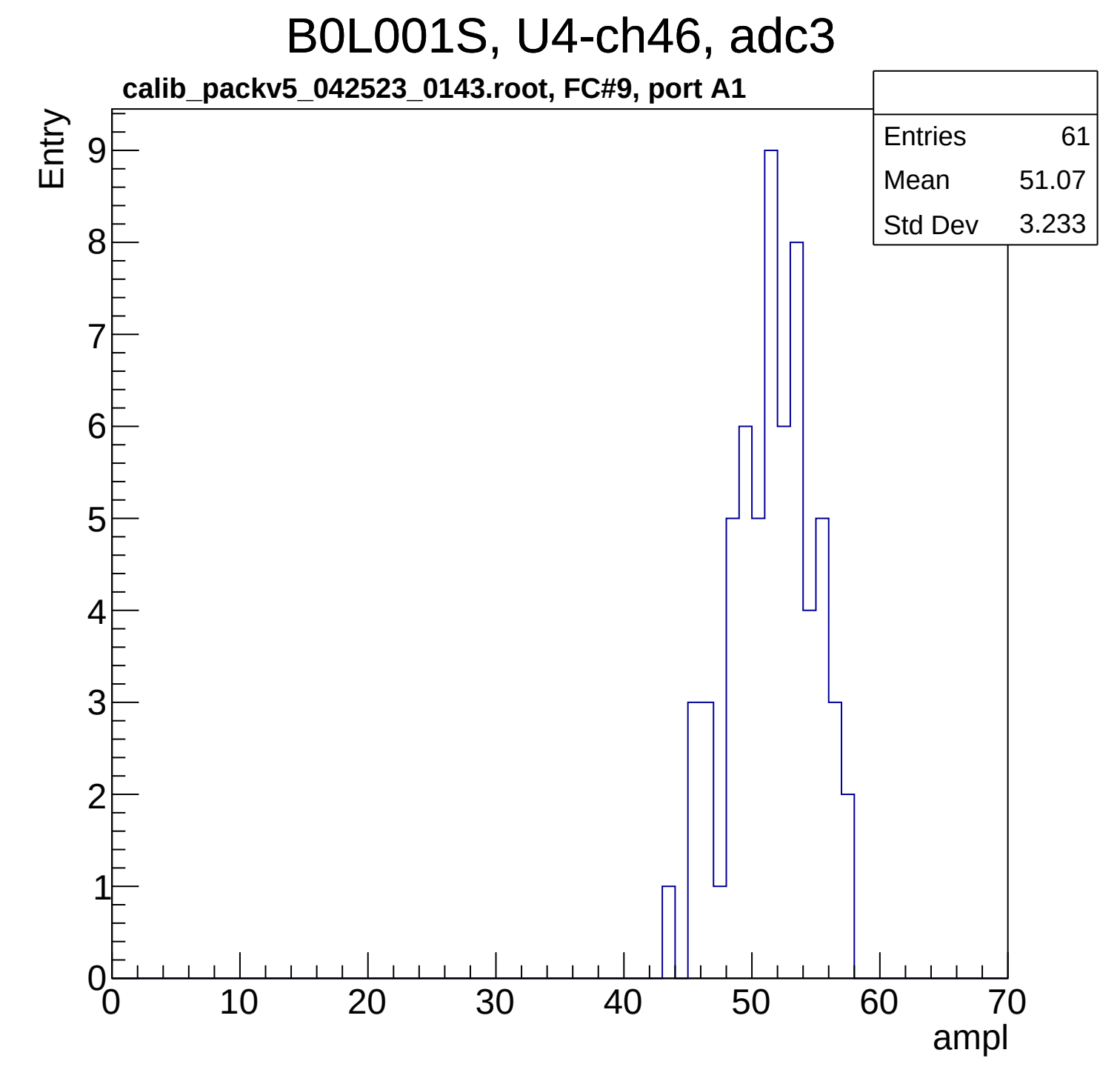
B0L001S, U4-ch46, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.07
Std Dev 3.233
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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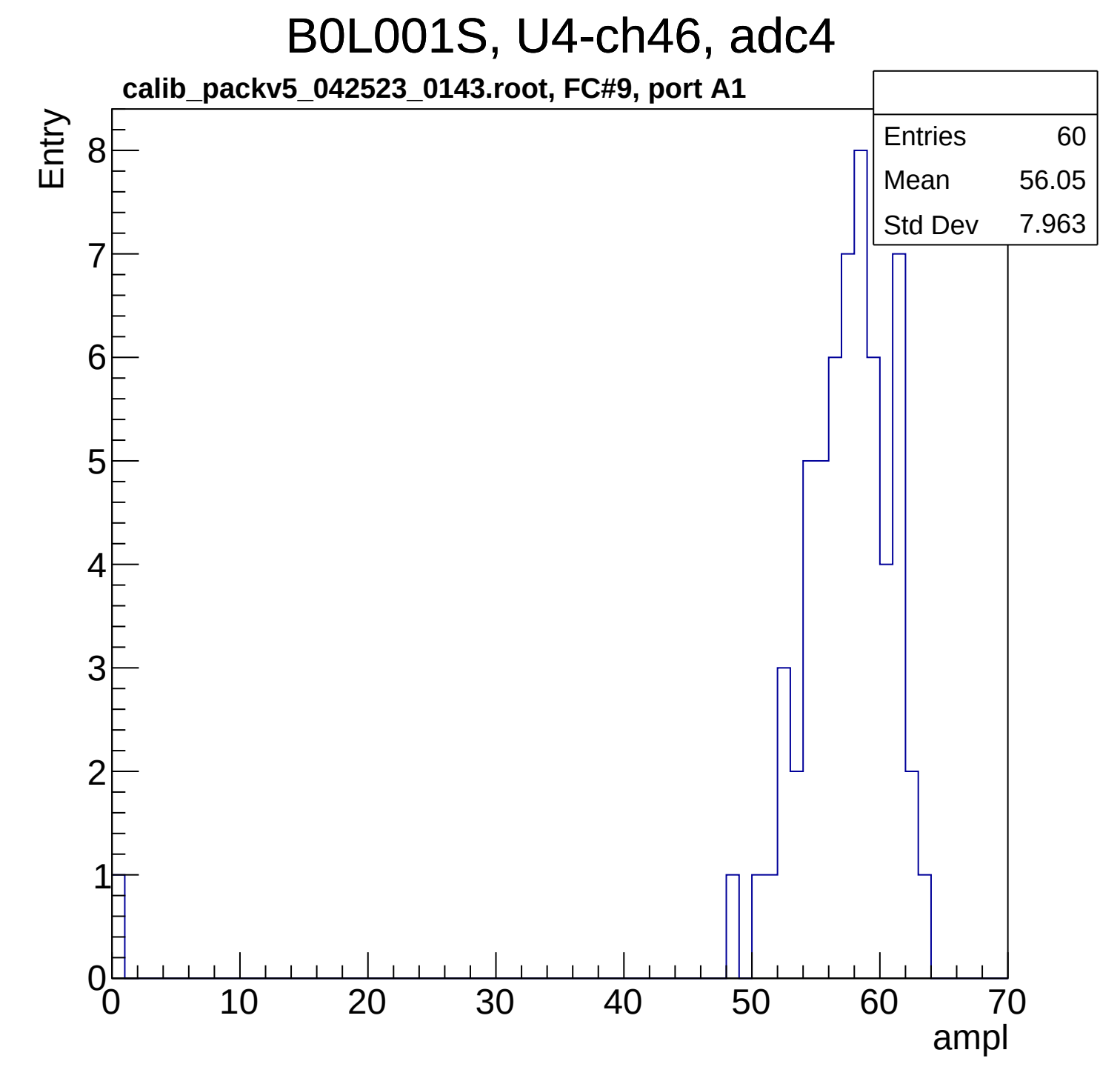
B0L001S, U4-ch46, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.05
Std Dev 7.963
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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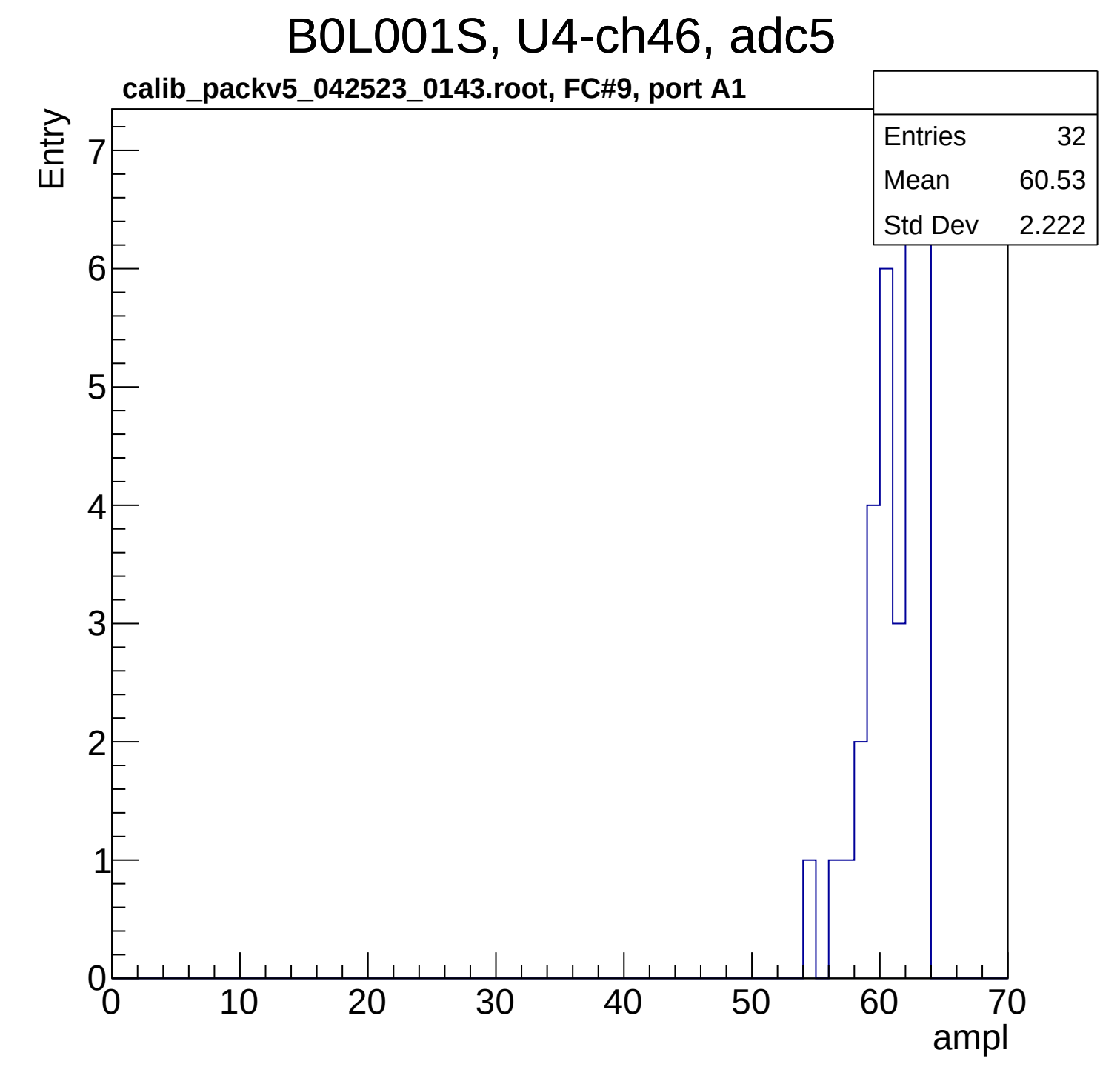
B0L001S, U4-ch46, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.53
Std Dev 2.222
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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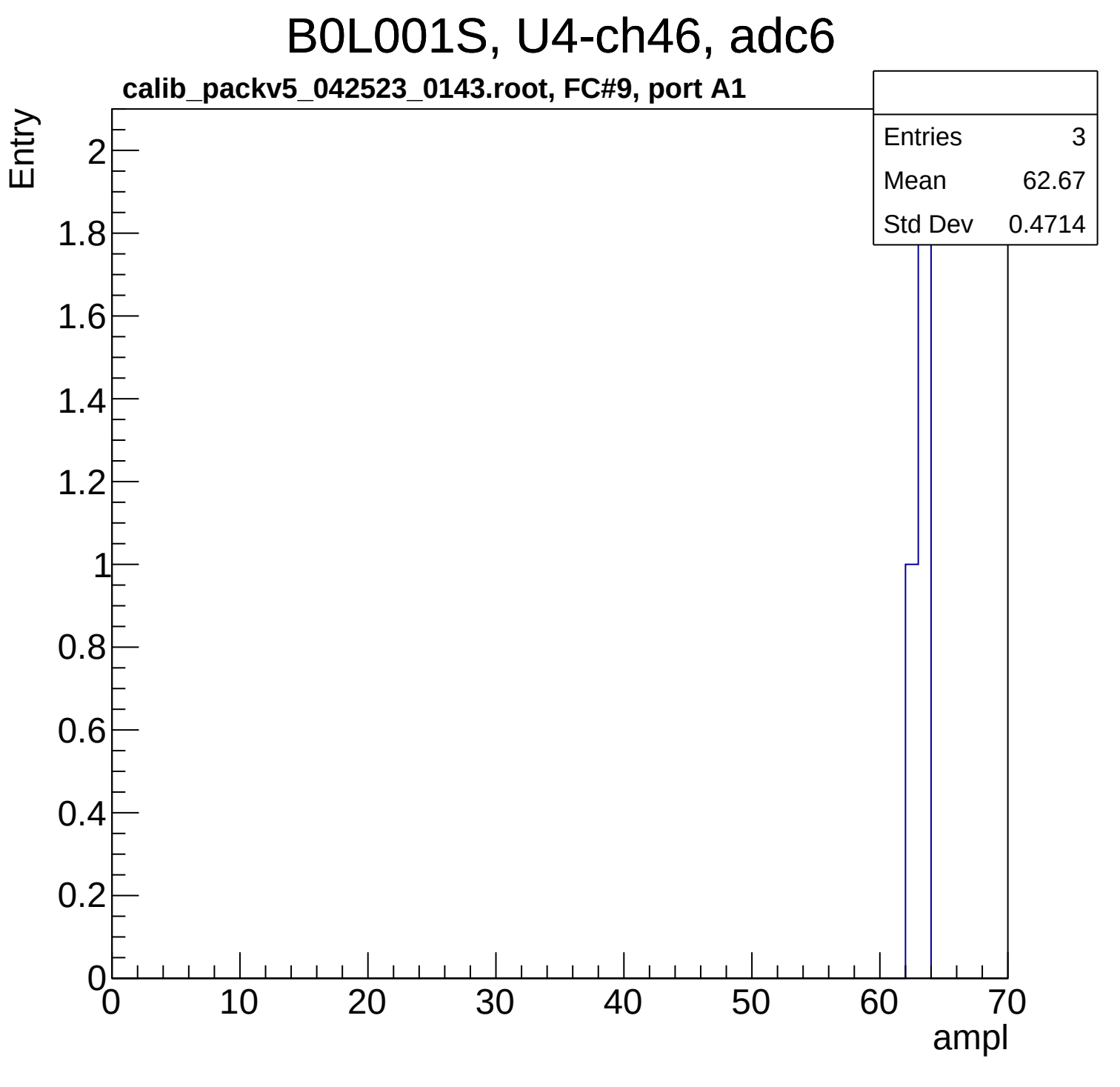
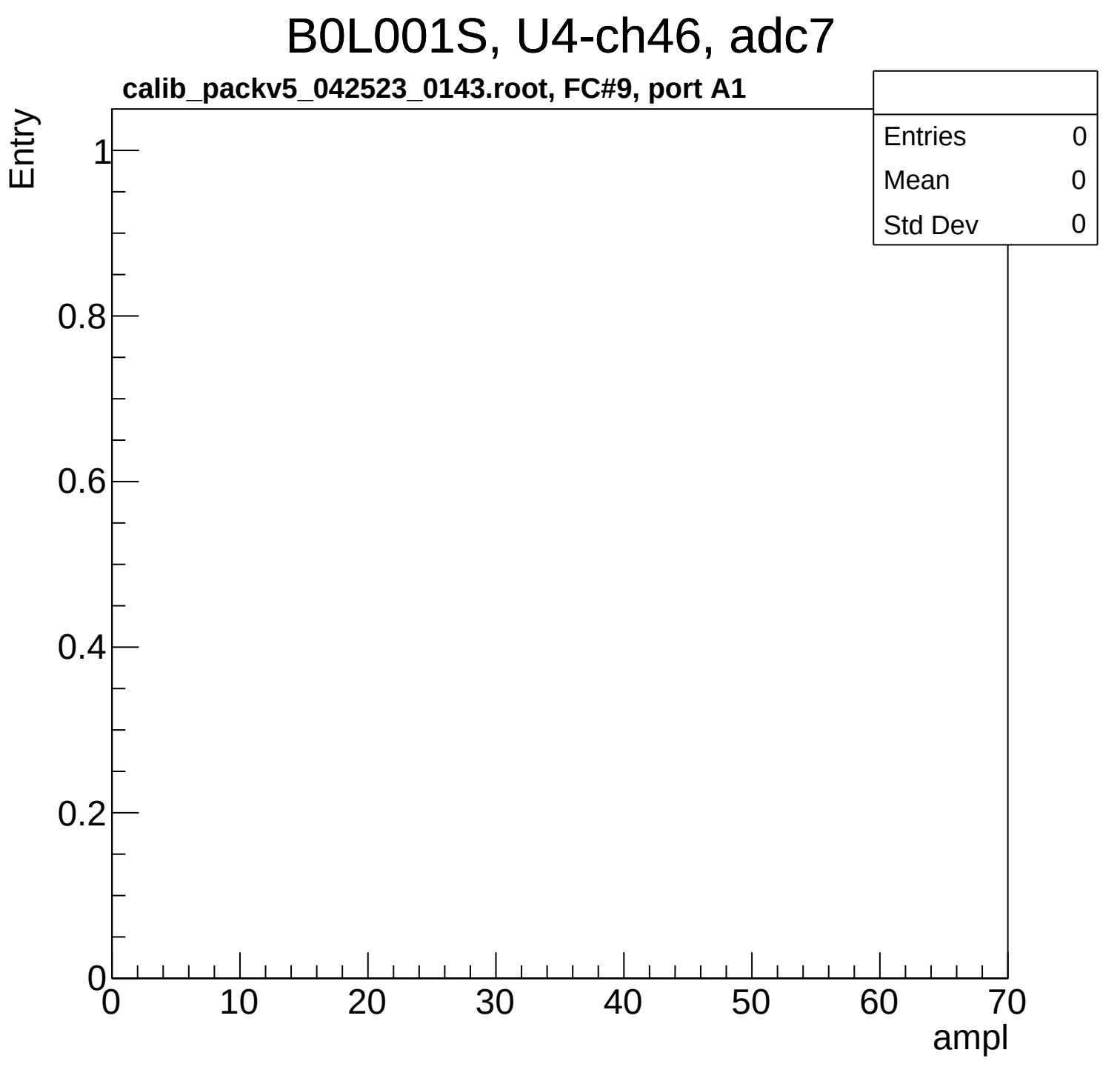
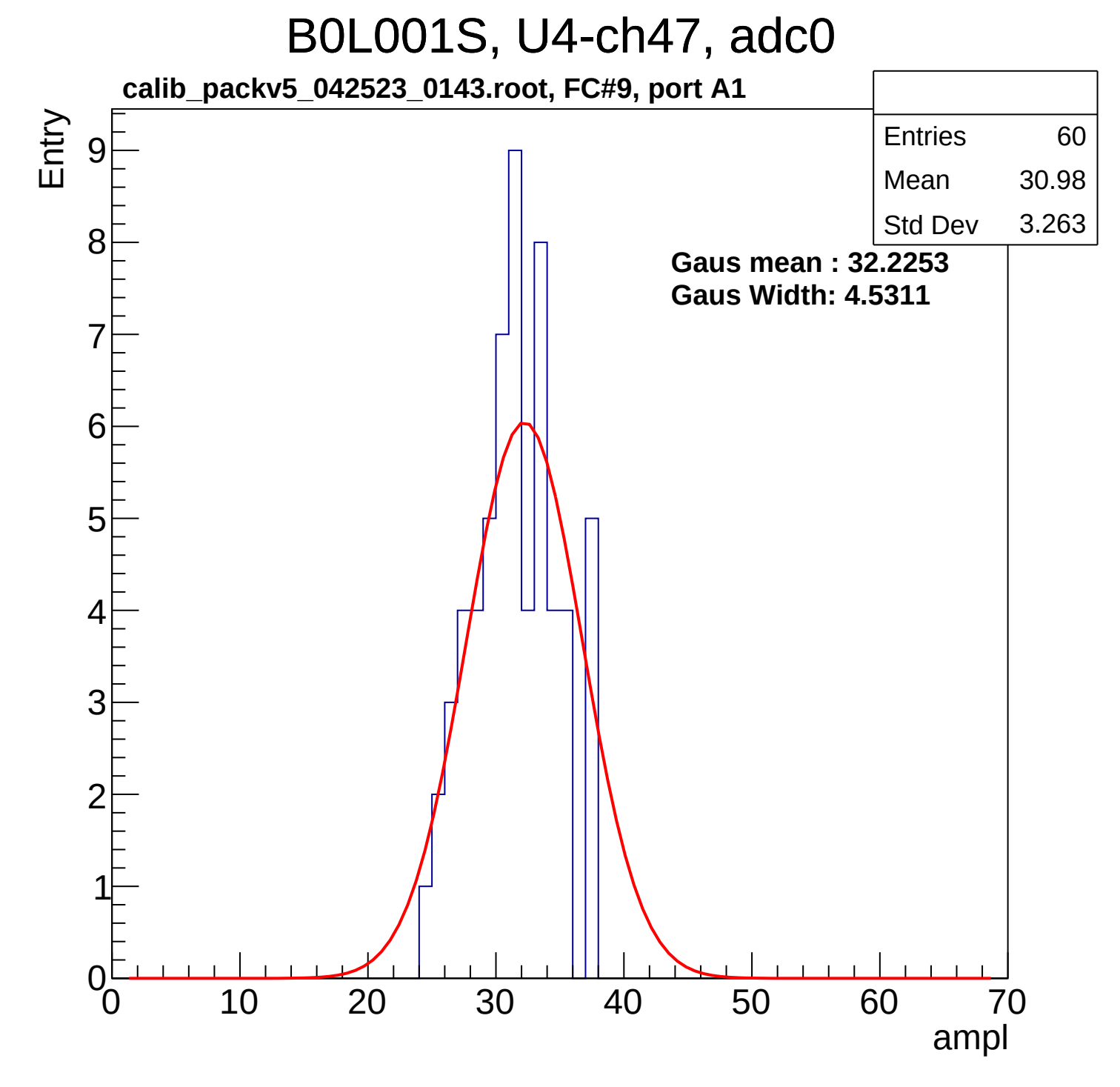

B0L001S, U4-ch46, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch46, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch47, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.98
Std Dev 3.263
Gaus mean : 32.2253
Gaus Width: 4.5311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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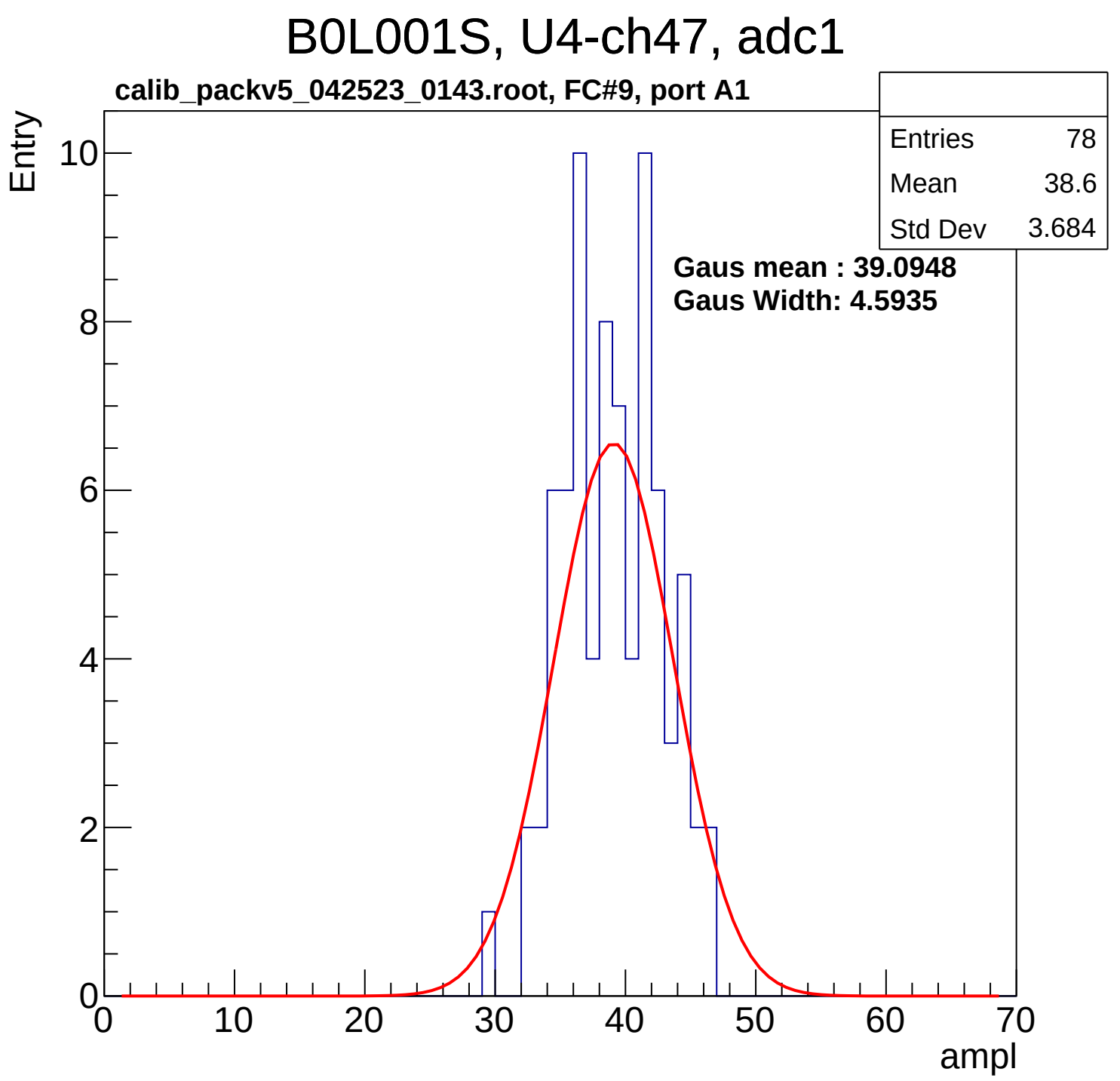
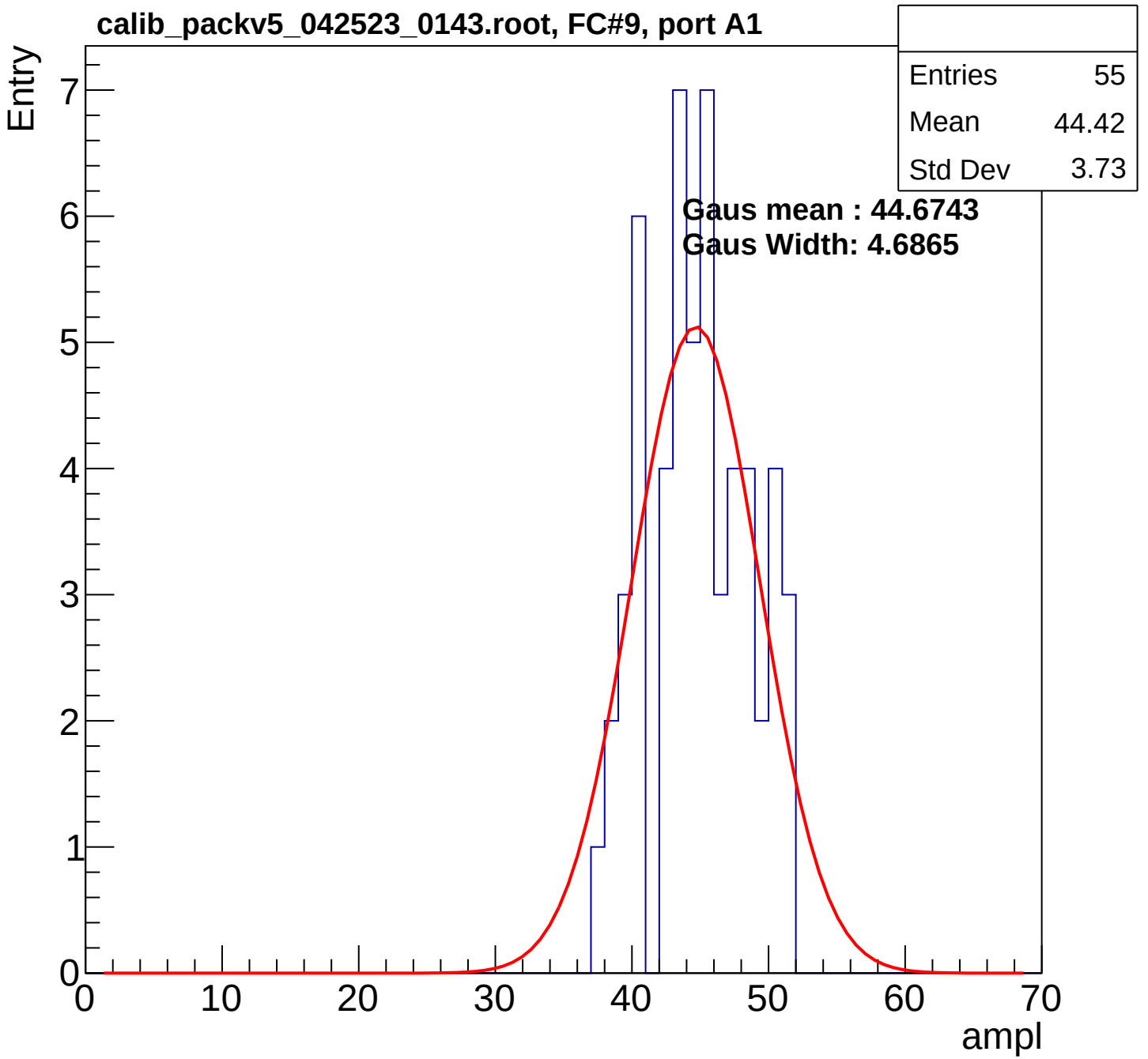
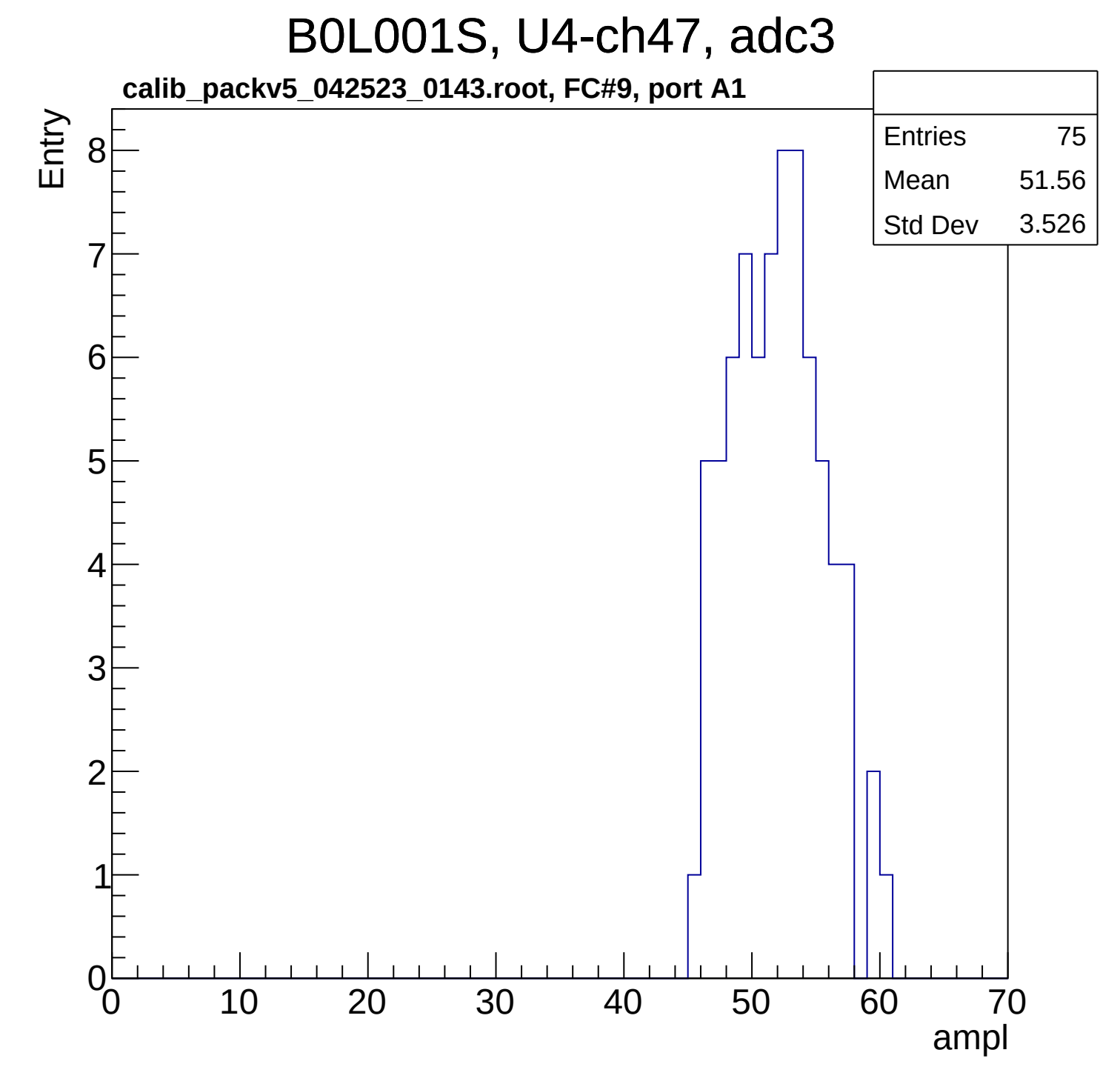

B0L001S, U4-ch47, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38.6
Std Dev 3.684
Gaus mean : 39.0948
Gaus Width: 4.5935
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch47, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 44.42
Std Dev 3.73
Gaus mean : 44.6743
Gaus Width: 4.6865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch47, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.56
Std Dev 3.526
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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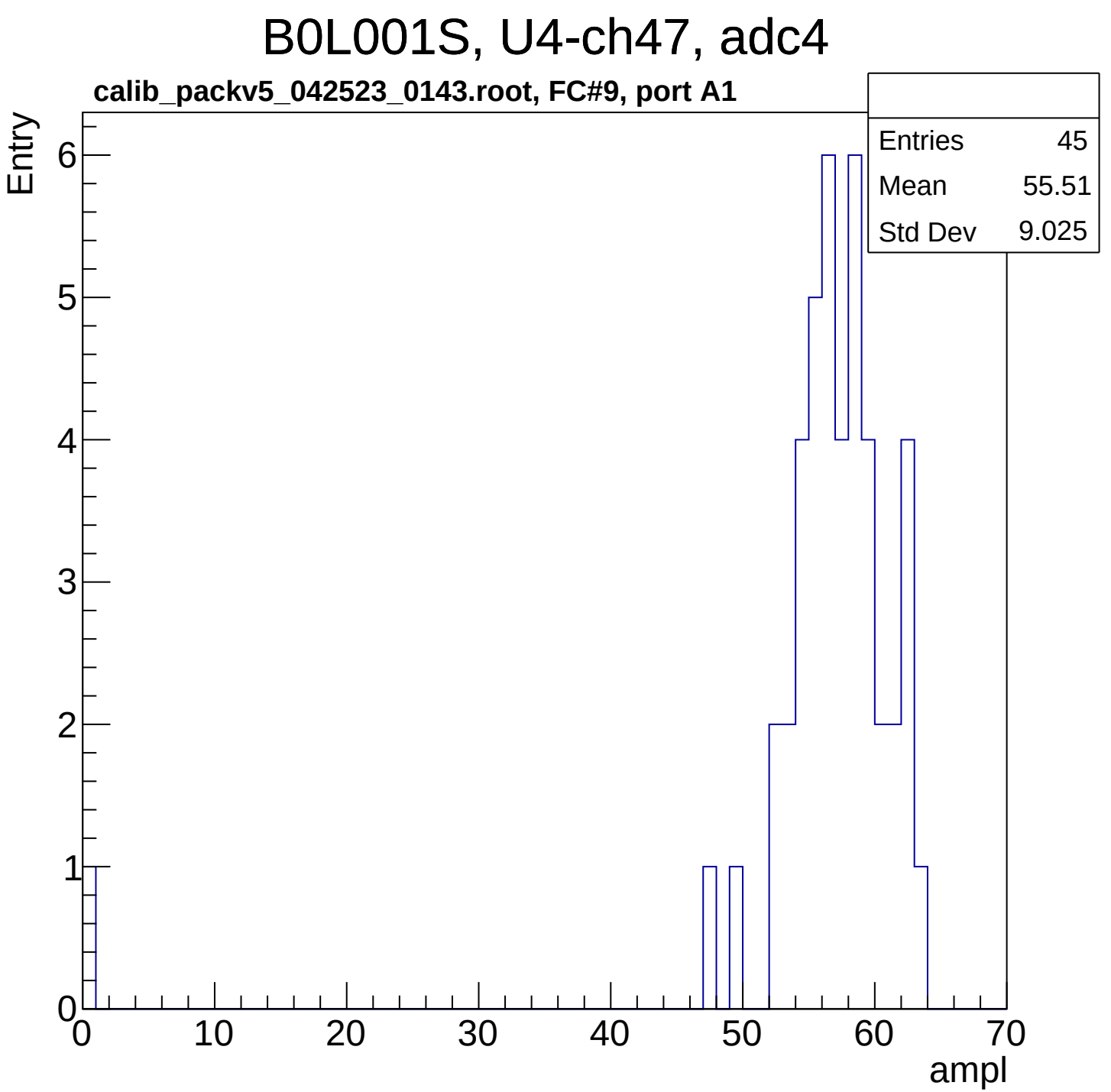
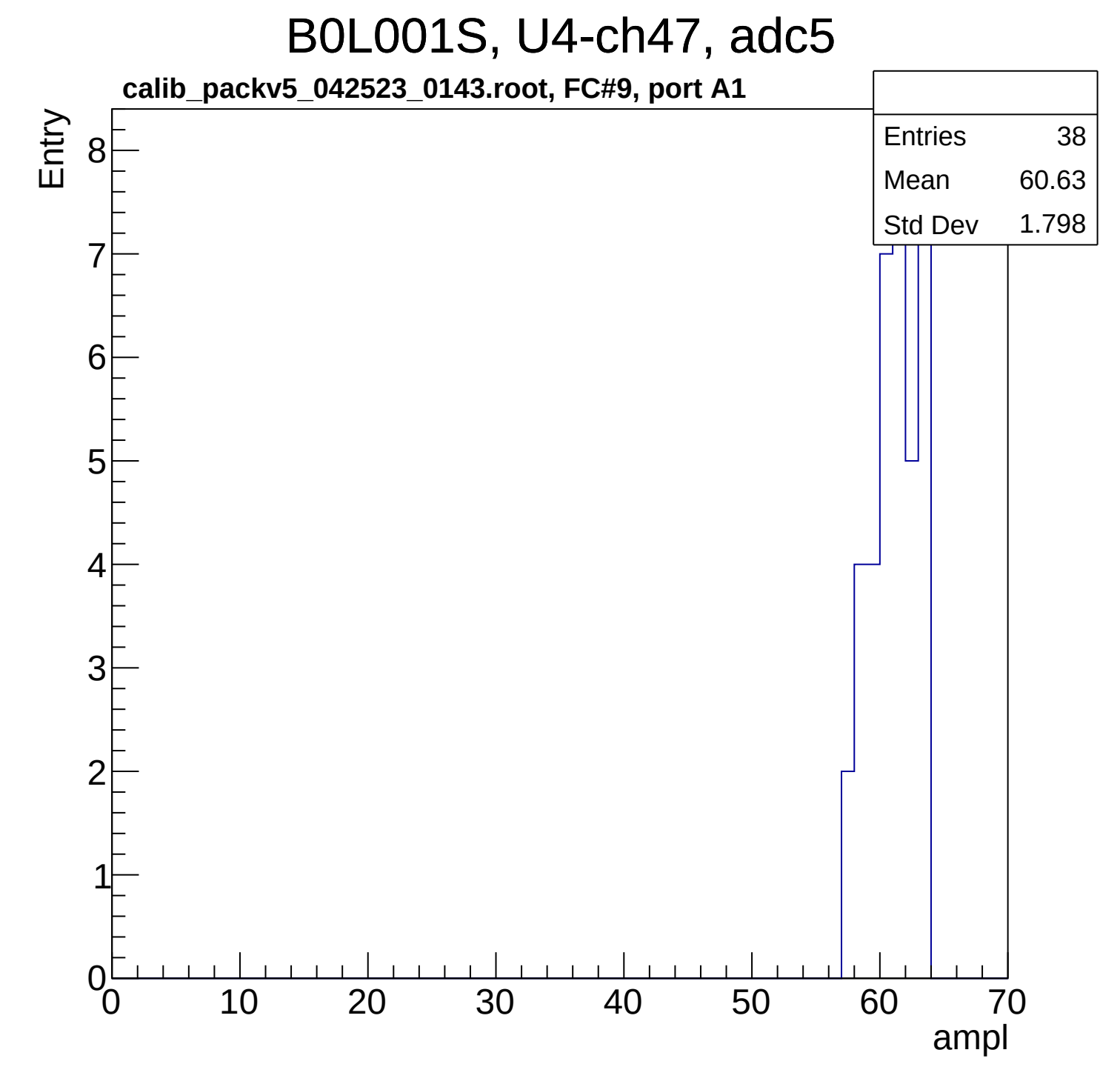

B0L001S, U4-ch47, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 55.51
Std Dev 9.025
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U4-ch47, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.63
Std Dev 1.798
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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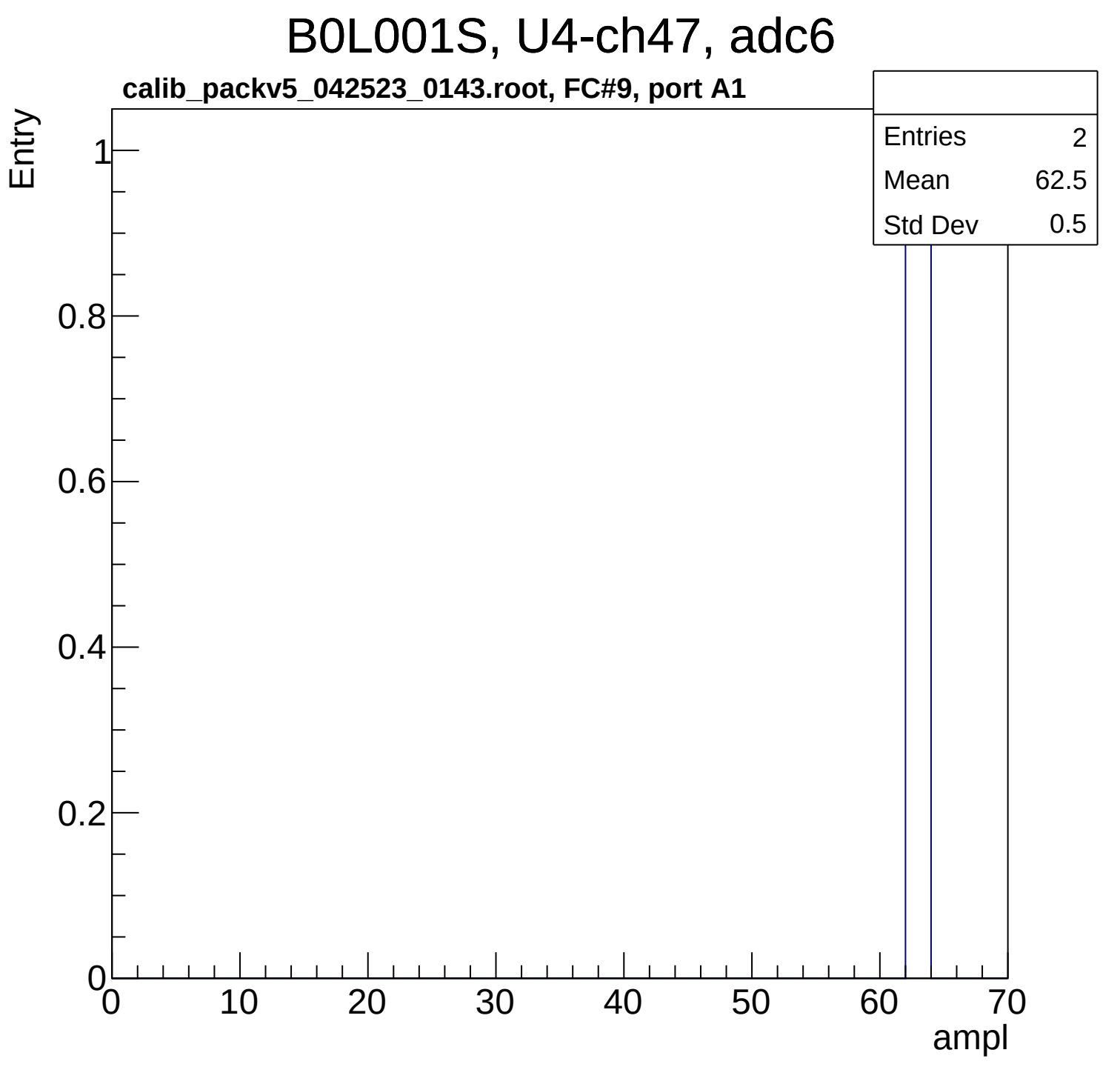
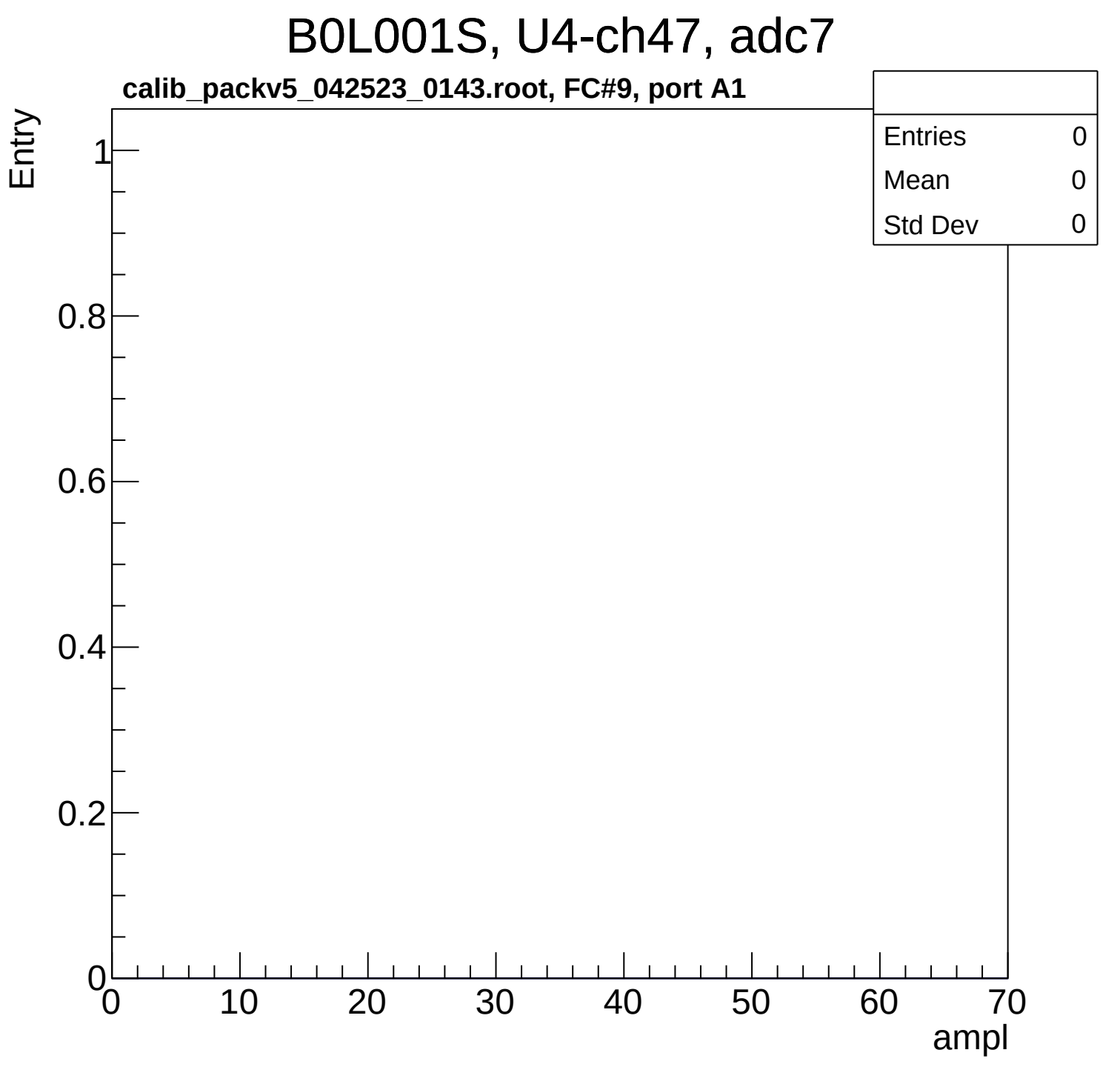
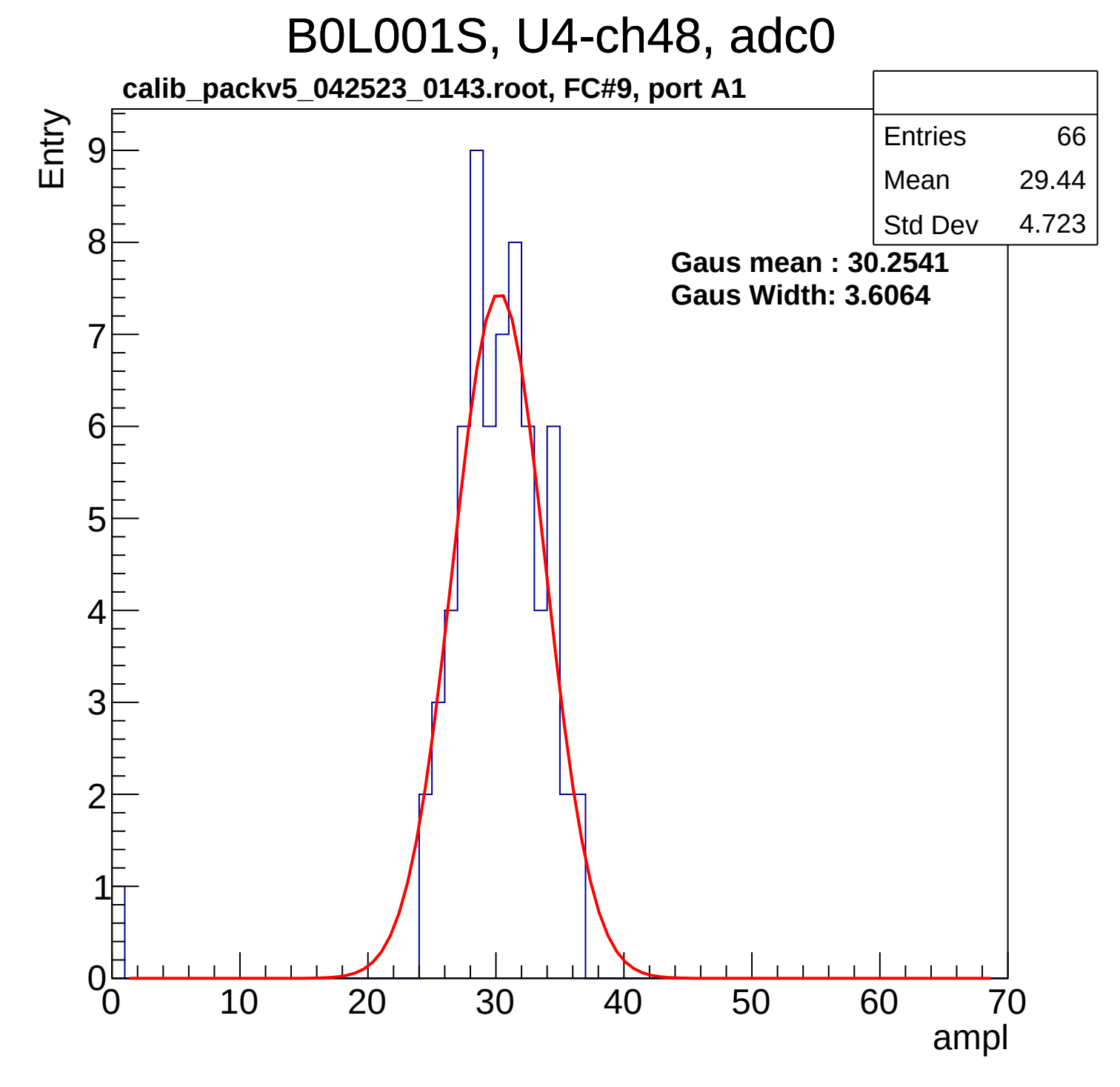
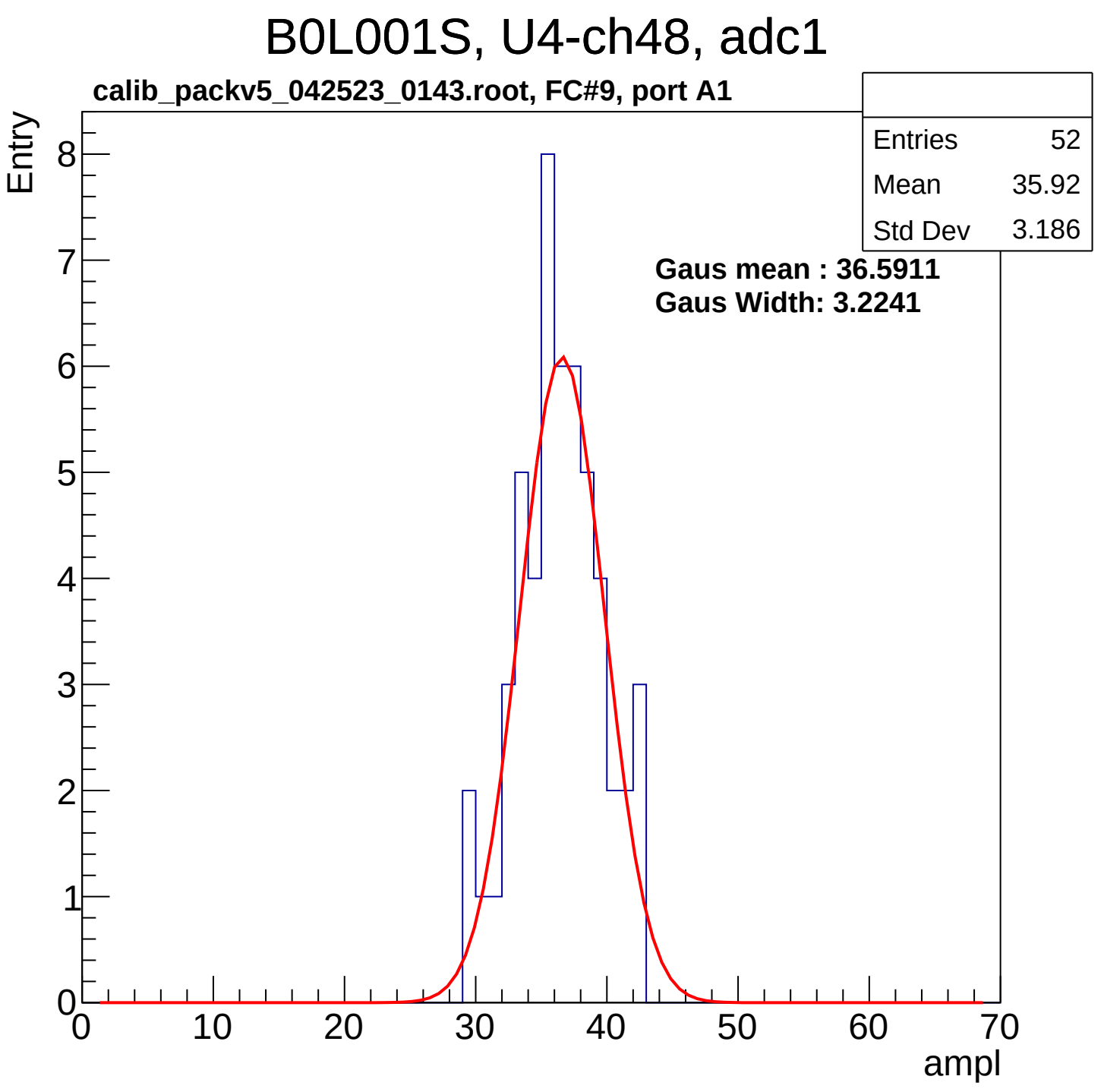
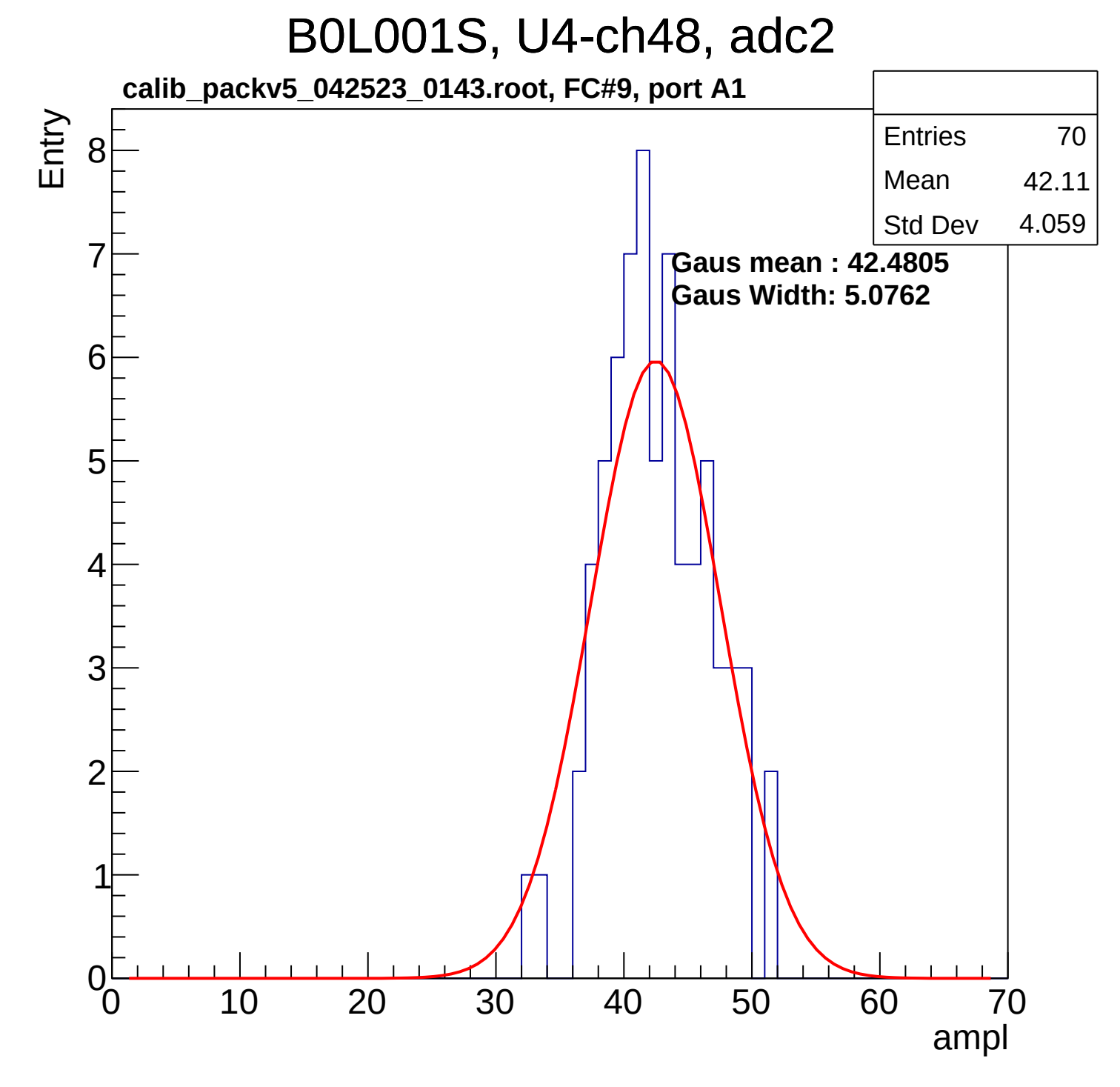
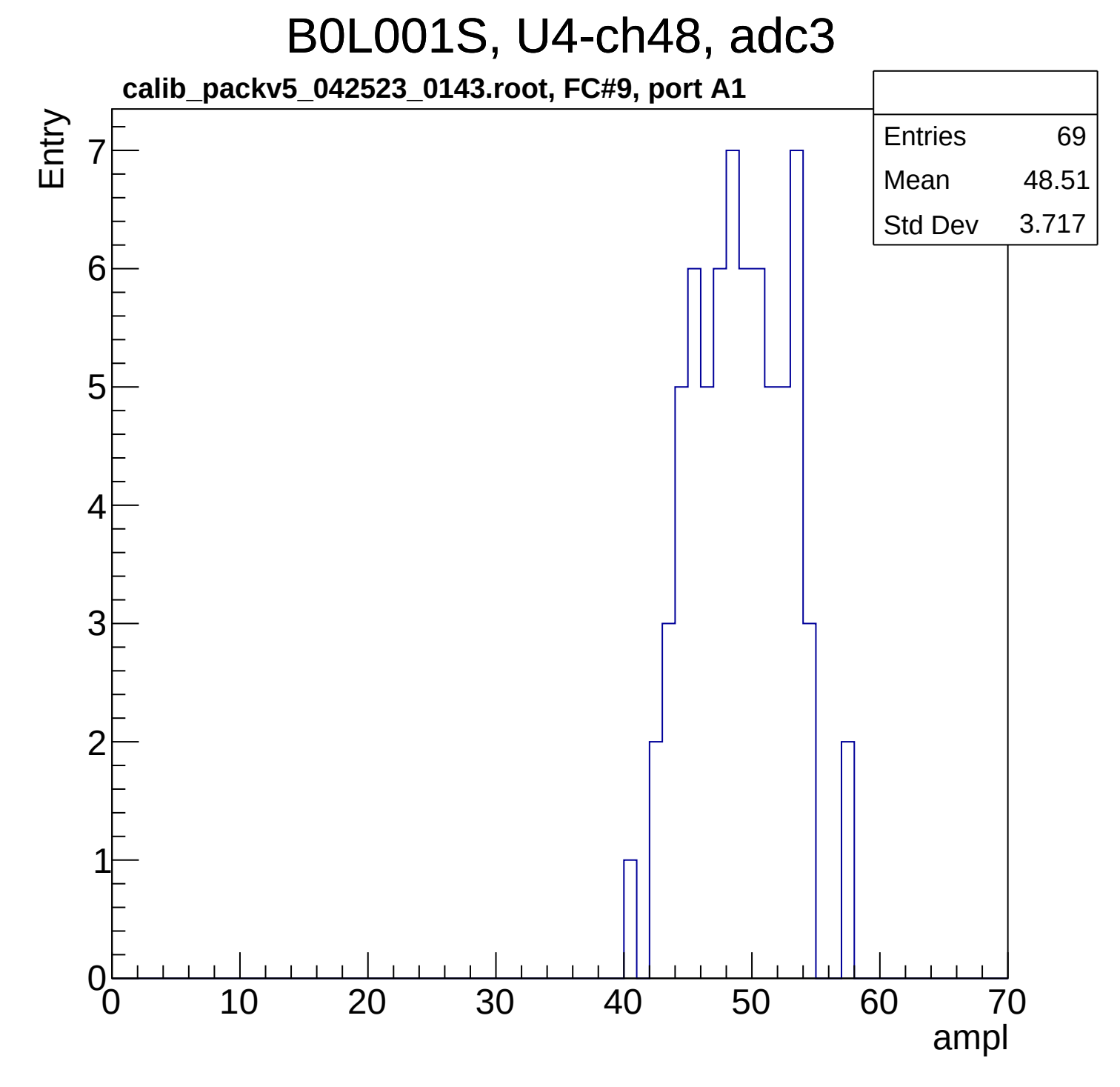

B0L001S, U4-ch47, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

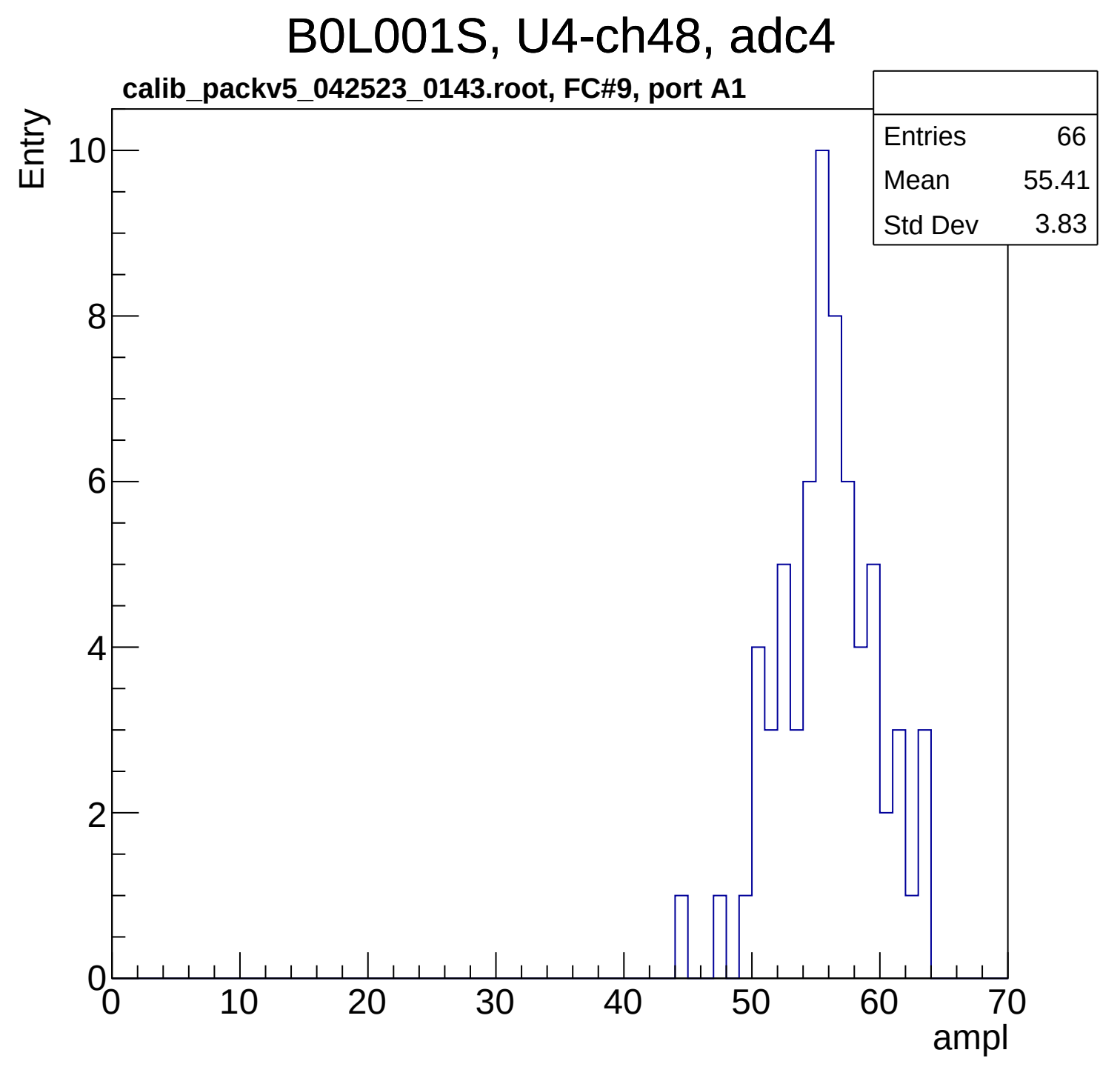

B0L001S, U4-ch47, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch48, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 29.44
Std Dev 4.723
Gaus mean : 30.2541
Gaus Width: 3.6064
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch48, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 35.92
Std Dev 3.186
Gaus mean : 36.5911
Gaus Width: 3.2241
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch48, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 42.11
Std Dev 4.059
Gaus mean : 42.4805
Gaus Width: 5.0762
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch48, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 48.51
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch48, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.41
Std Dev 3.83
Entry
0
2
4
6
8
10
0
10
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30
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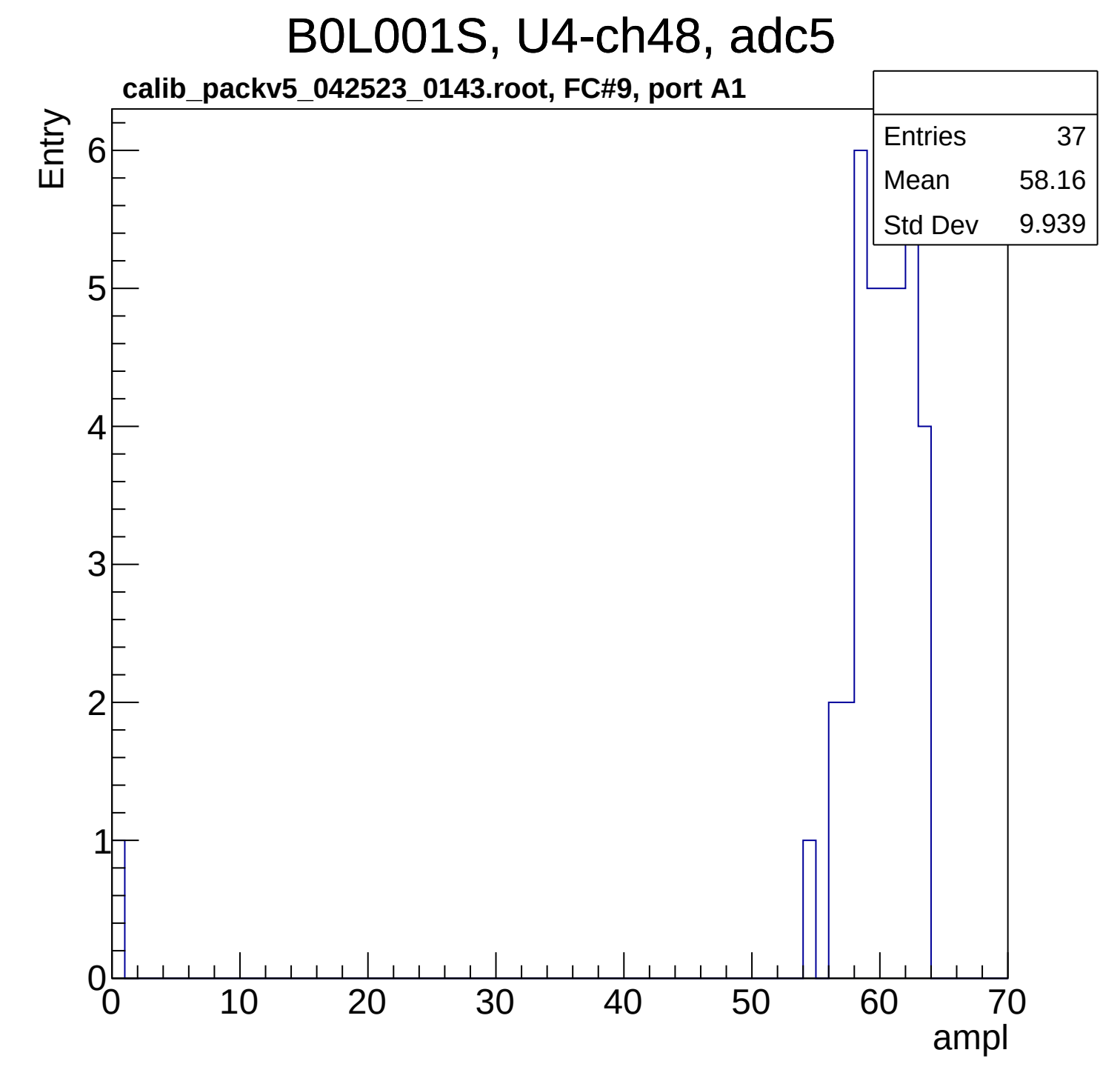
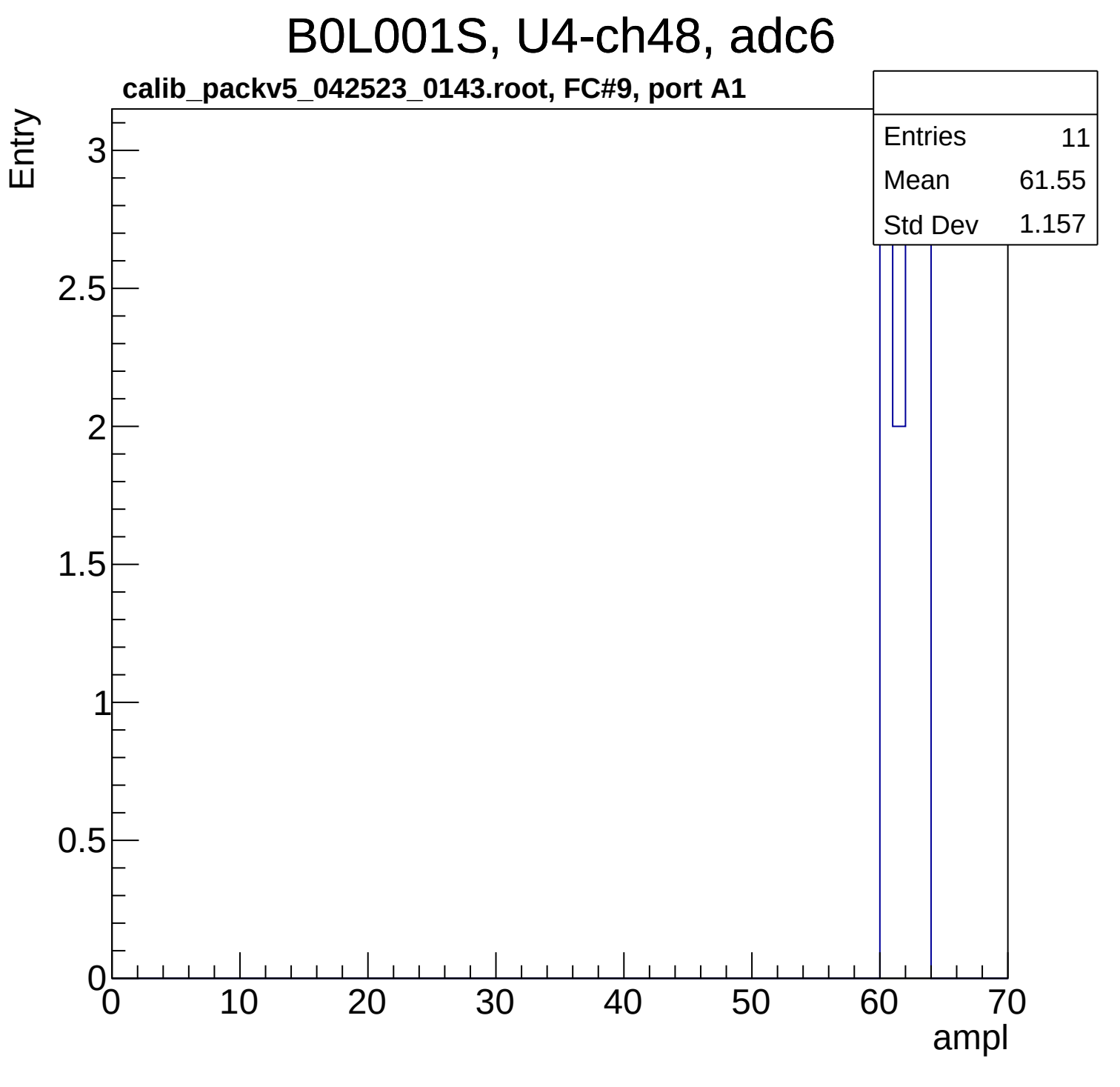
50
60
70
ampl


B0L001S, U4-ch48, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.16
Std Dev 9.939
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U4-ch48, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.55
Std Dev 1.157
Entry
ampl
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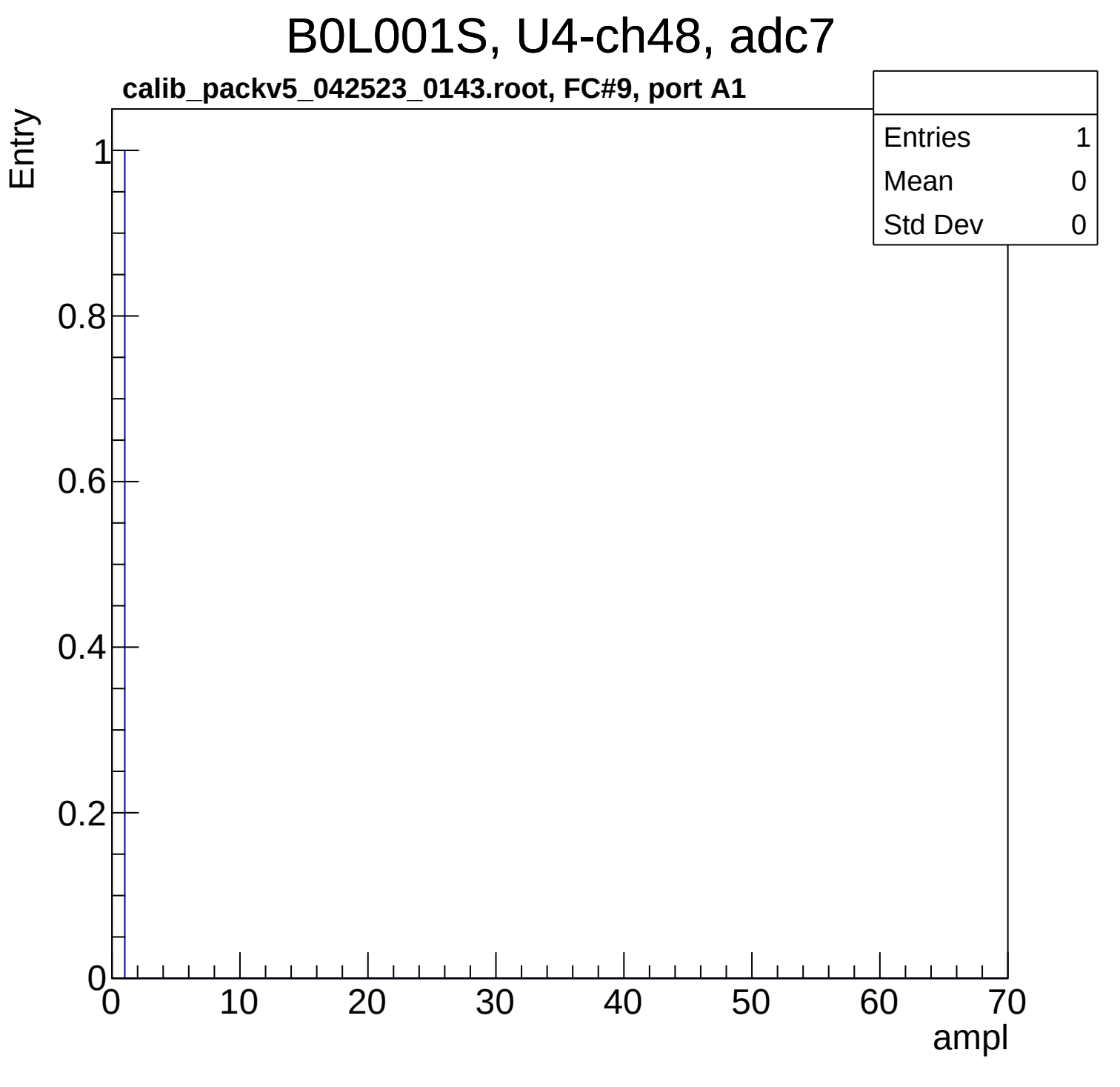
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U4-ch48, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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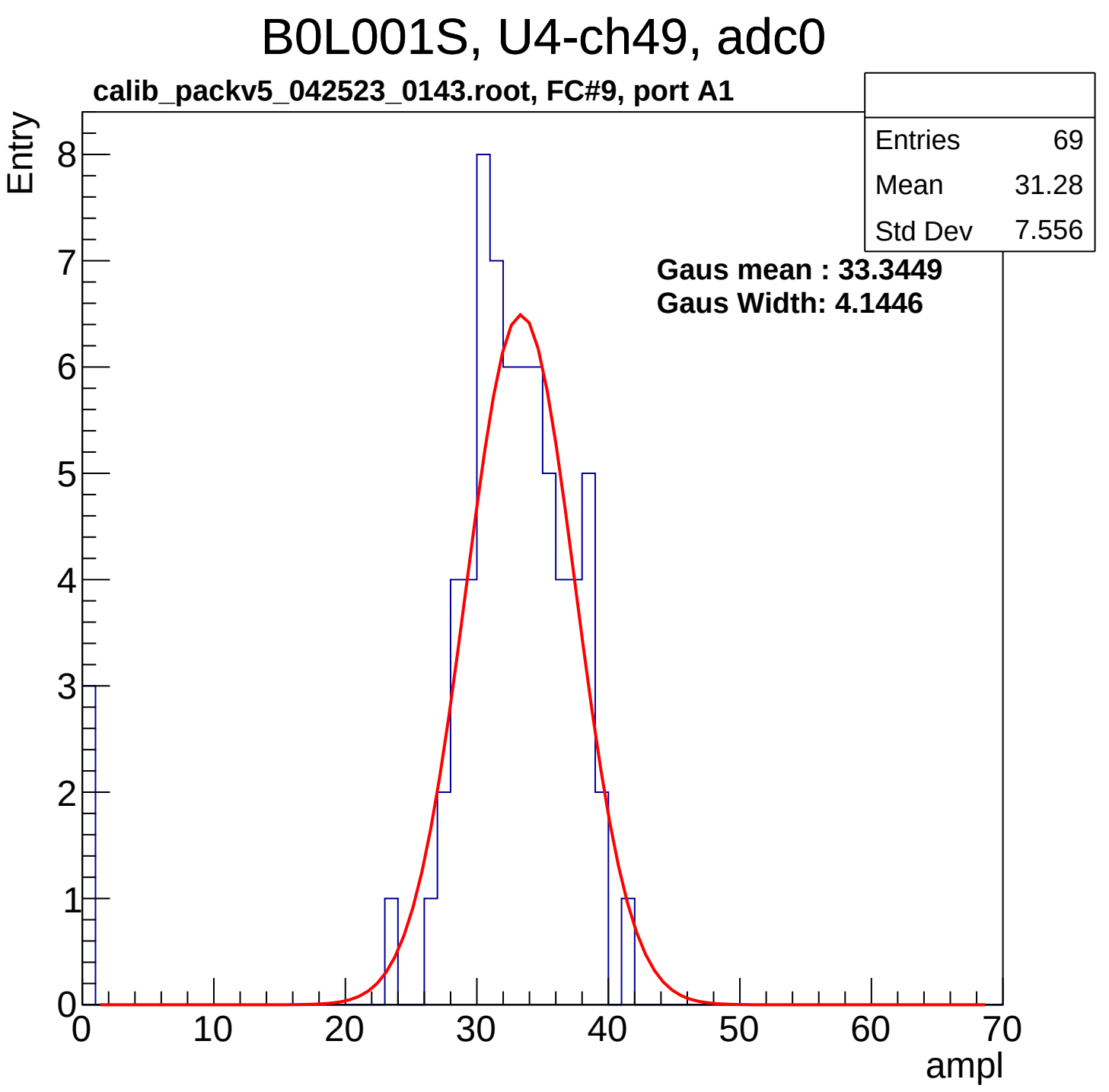
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch49, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 31.28
Std Dev 7.556
Gaus mean : 33.3449
Gaus Width: 4.1446
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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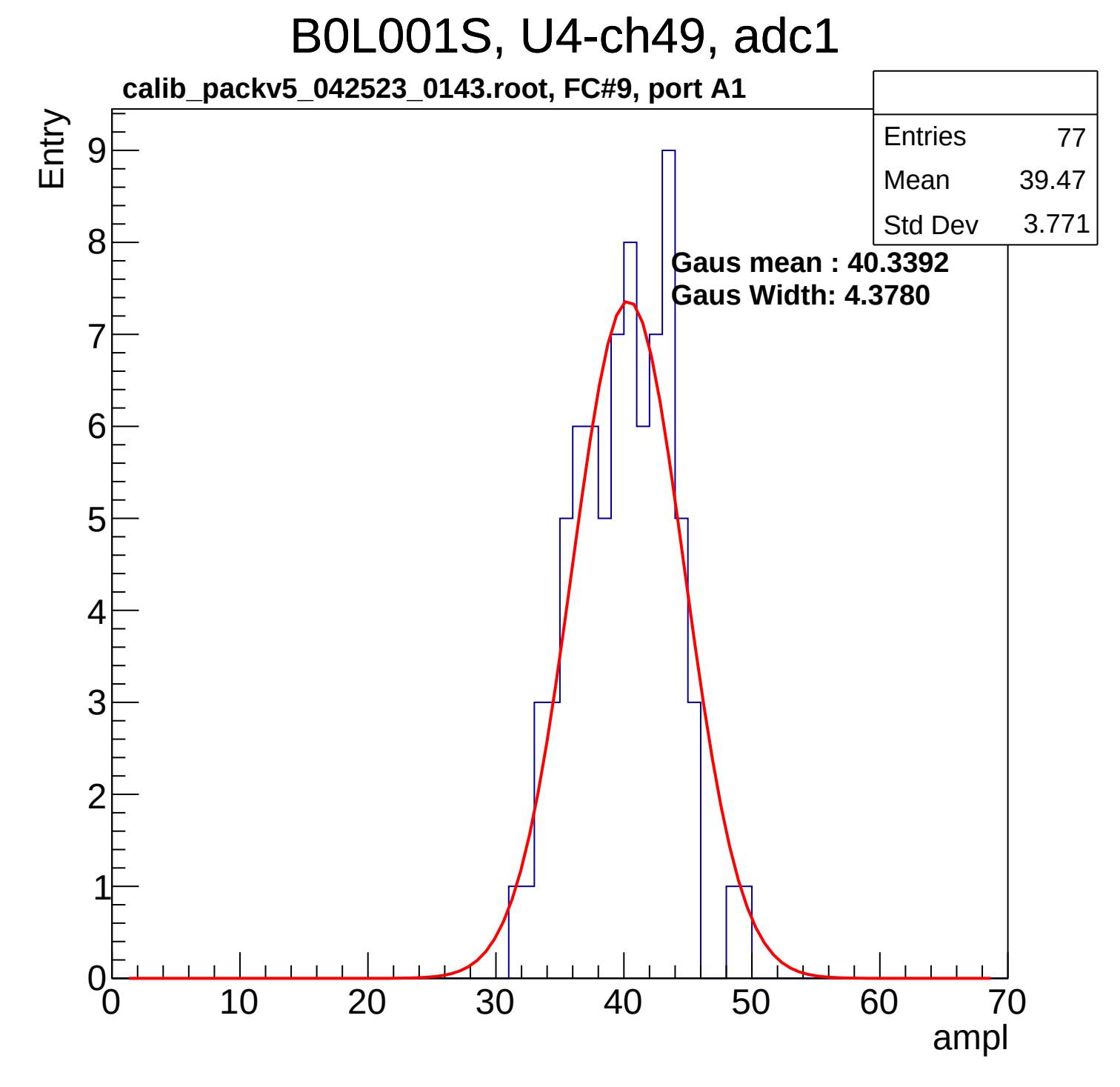
40
50
60
70


B0L001S, U4-ch49, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 39.47
Std Dev 3.771
Gaus mean : 40.3392
Gaus Width: 4.3780
Entry
ampl
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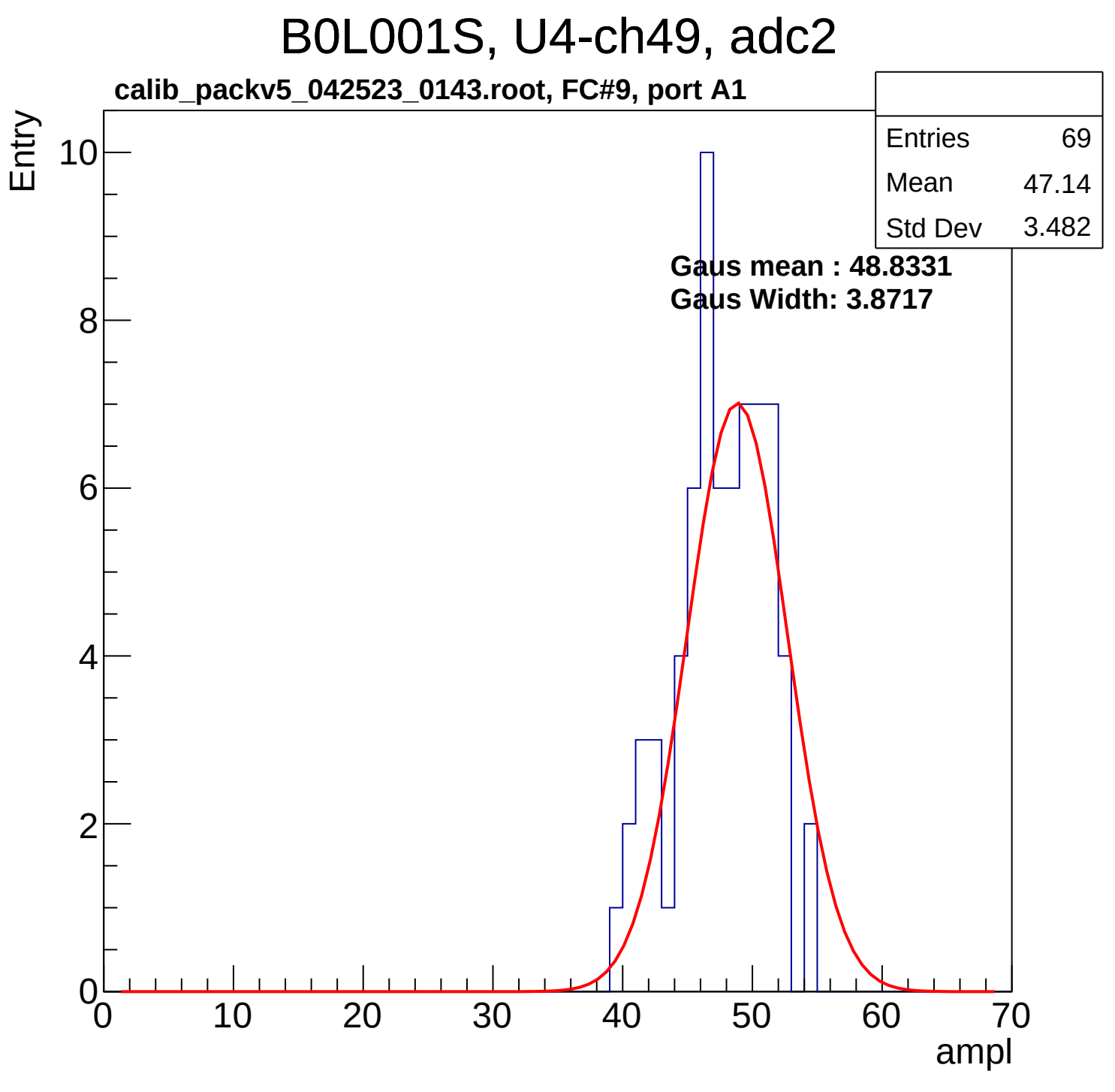
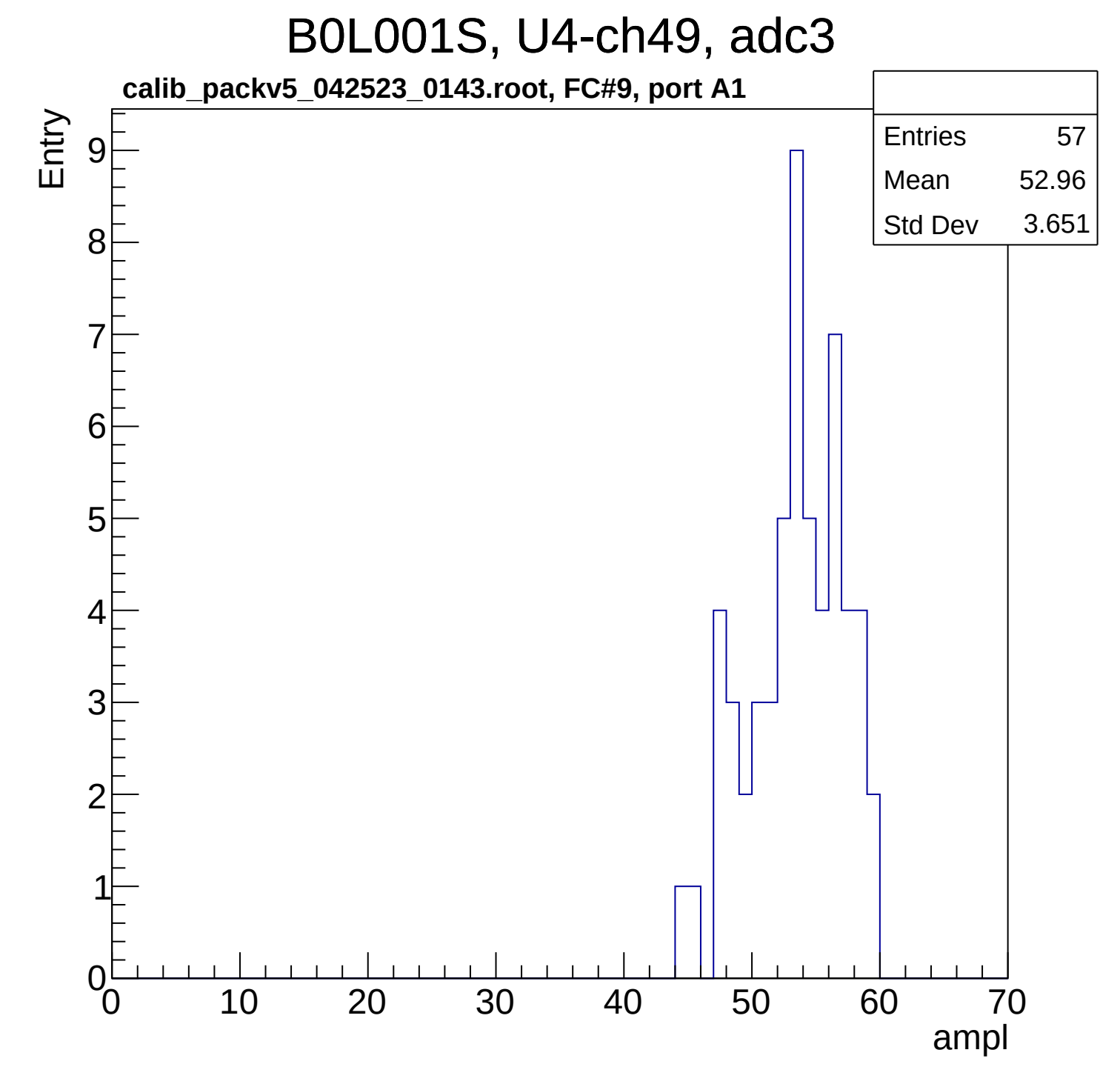
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch49, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 47.14
Std Dev 3.482
Gaus mean : 48.8331
Gaus Width: 3.8717
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch49, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 52.96
Std Dev 3.651
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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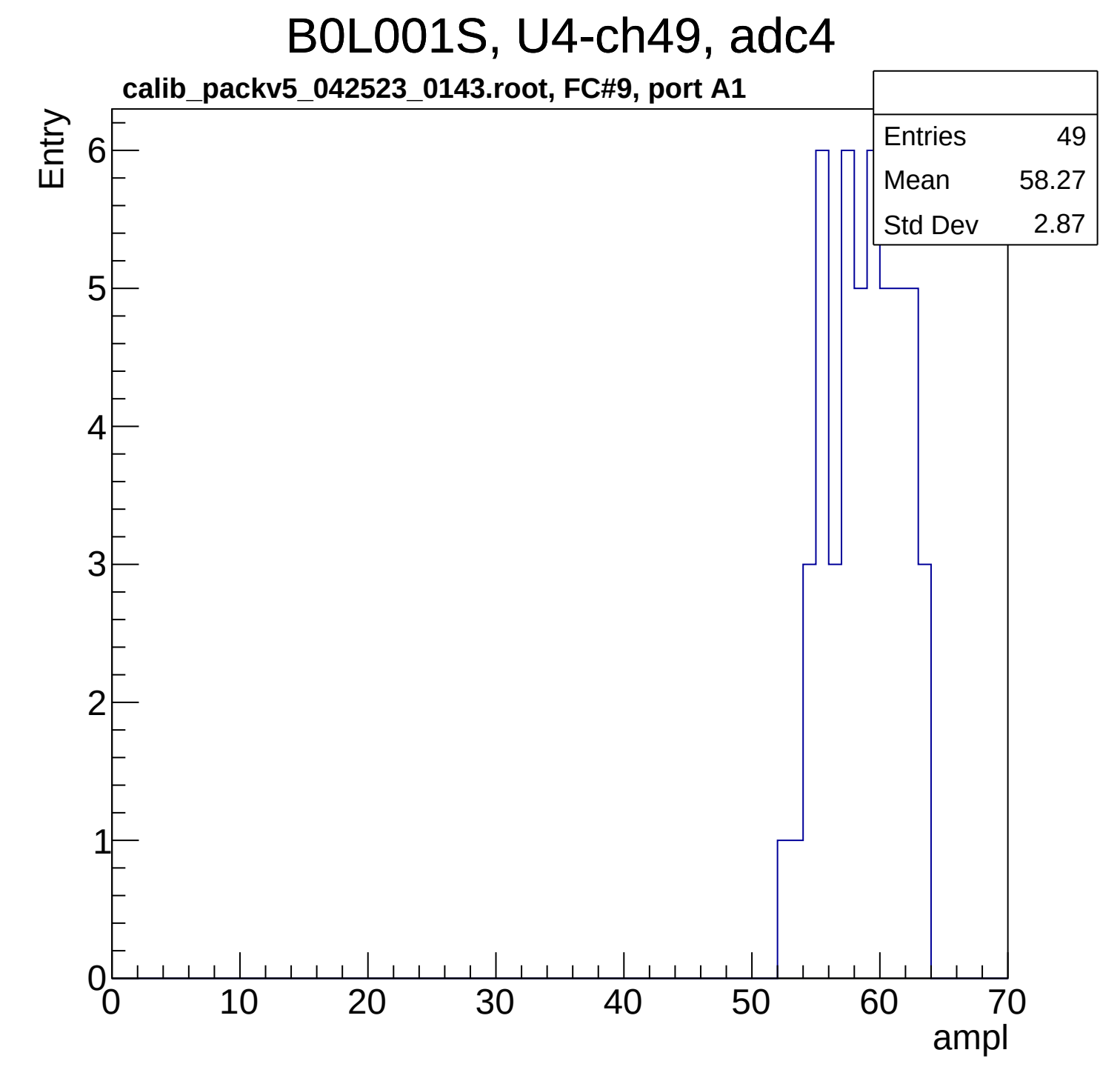
B0L001S, U4-ch49, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.27
Std Dev 2.87
Entry
0
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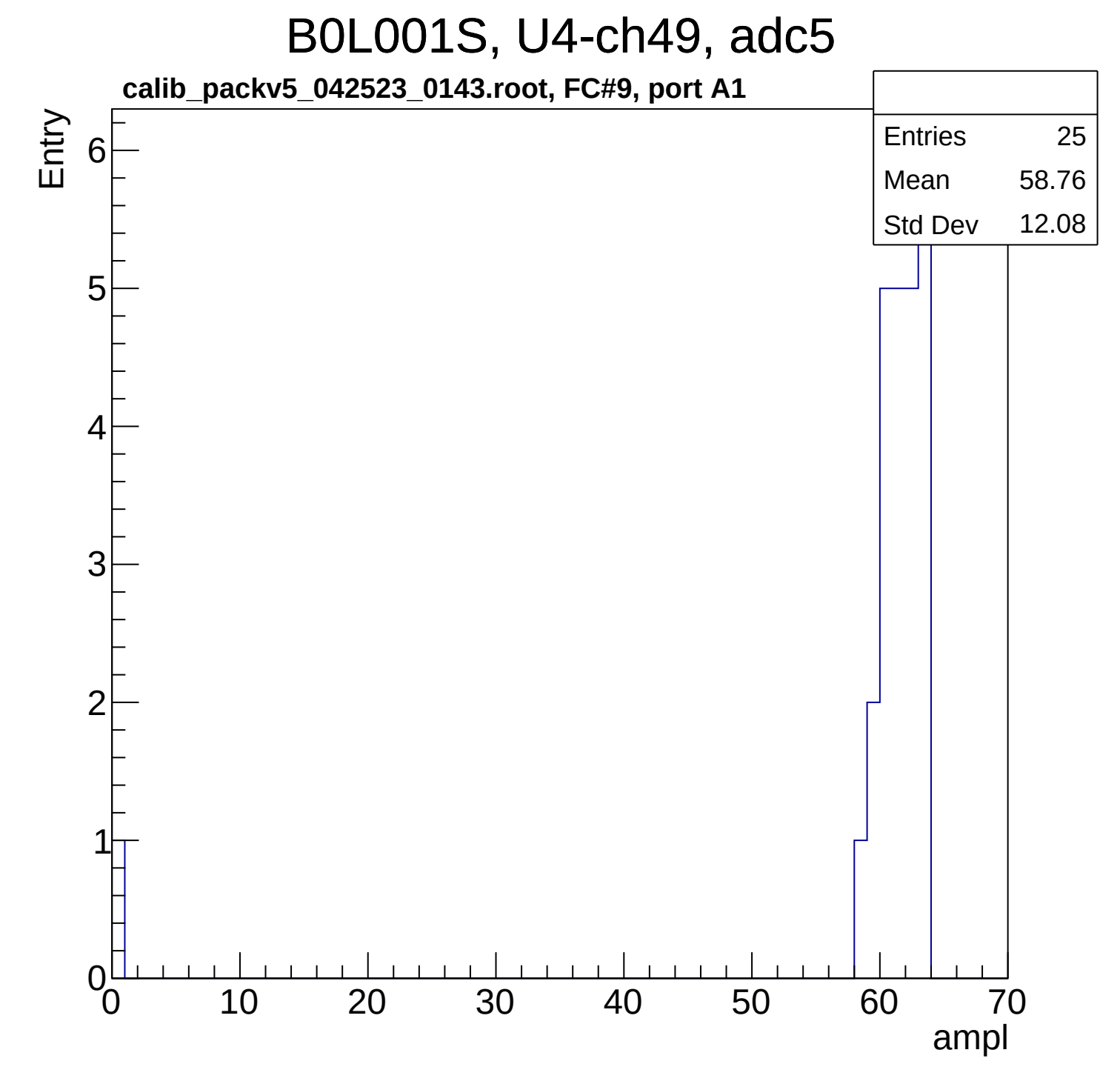
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch49, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.76
Std Dev 12.08
Entry
0
1
2
3
4
5
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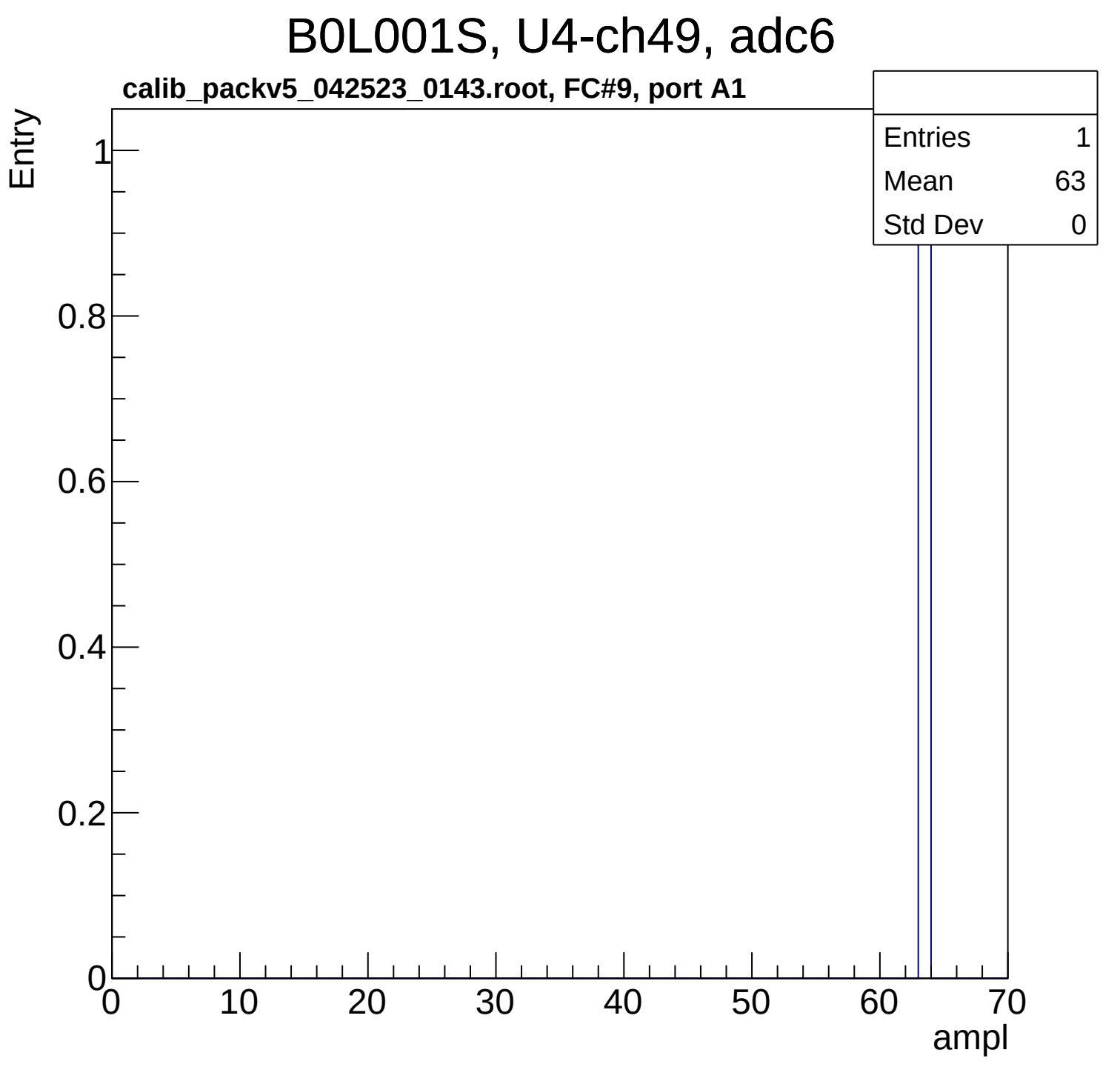
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch49, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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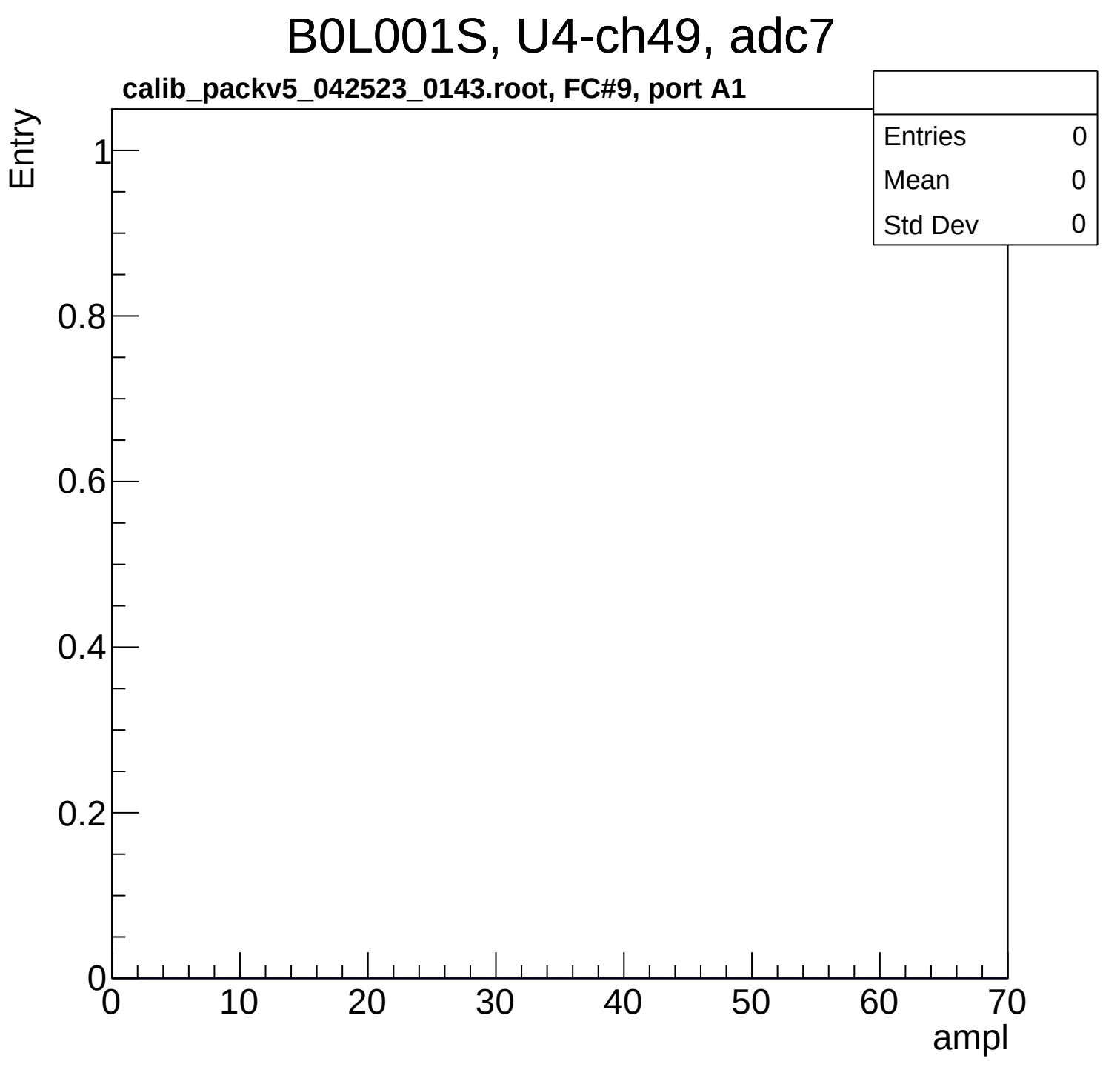
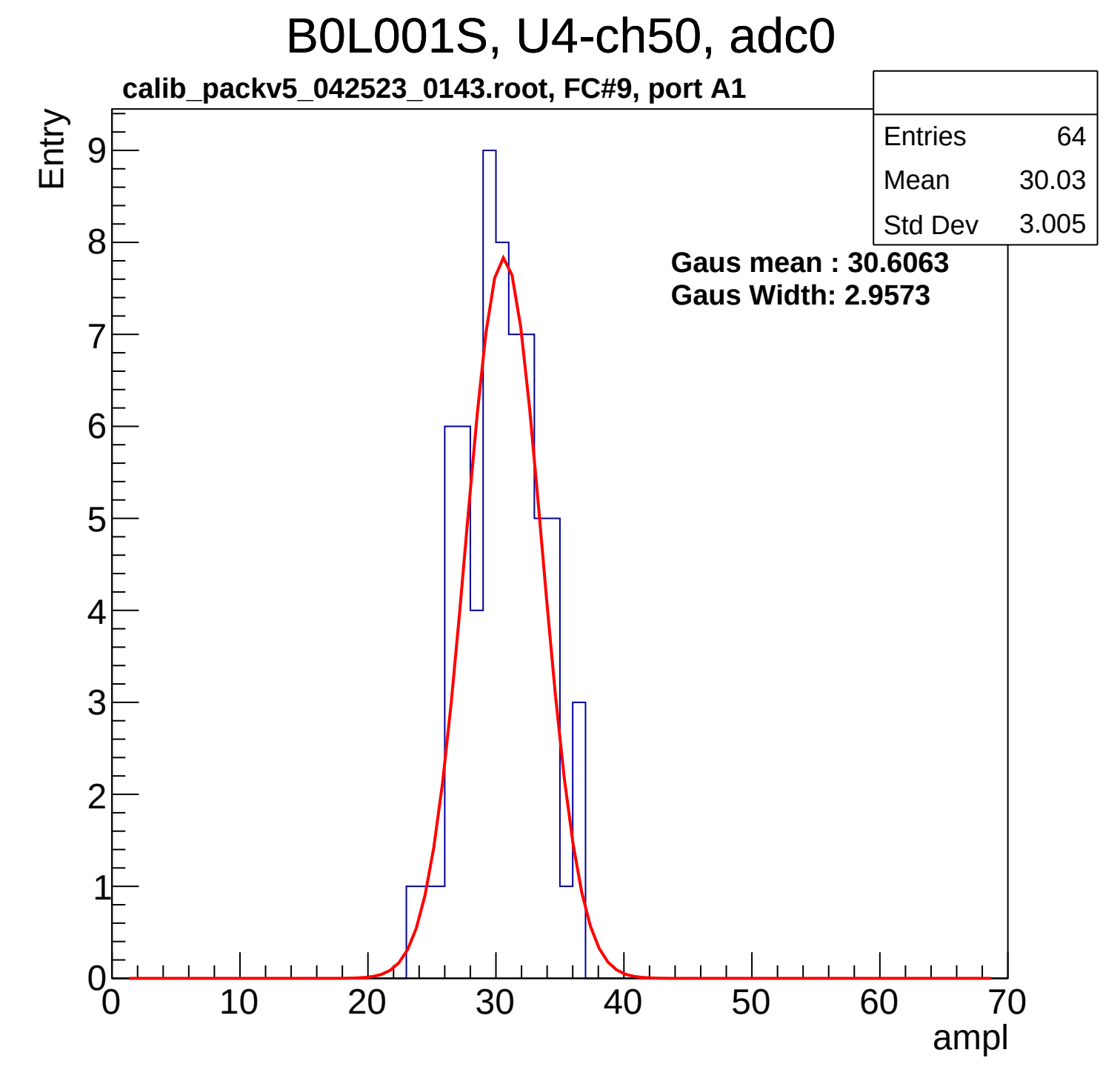
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch49, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch50, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.03
Std Dev 3.005
Gaus mean : 30.6063
Gaus Width: 2.9573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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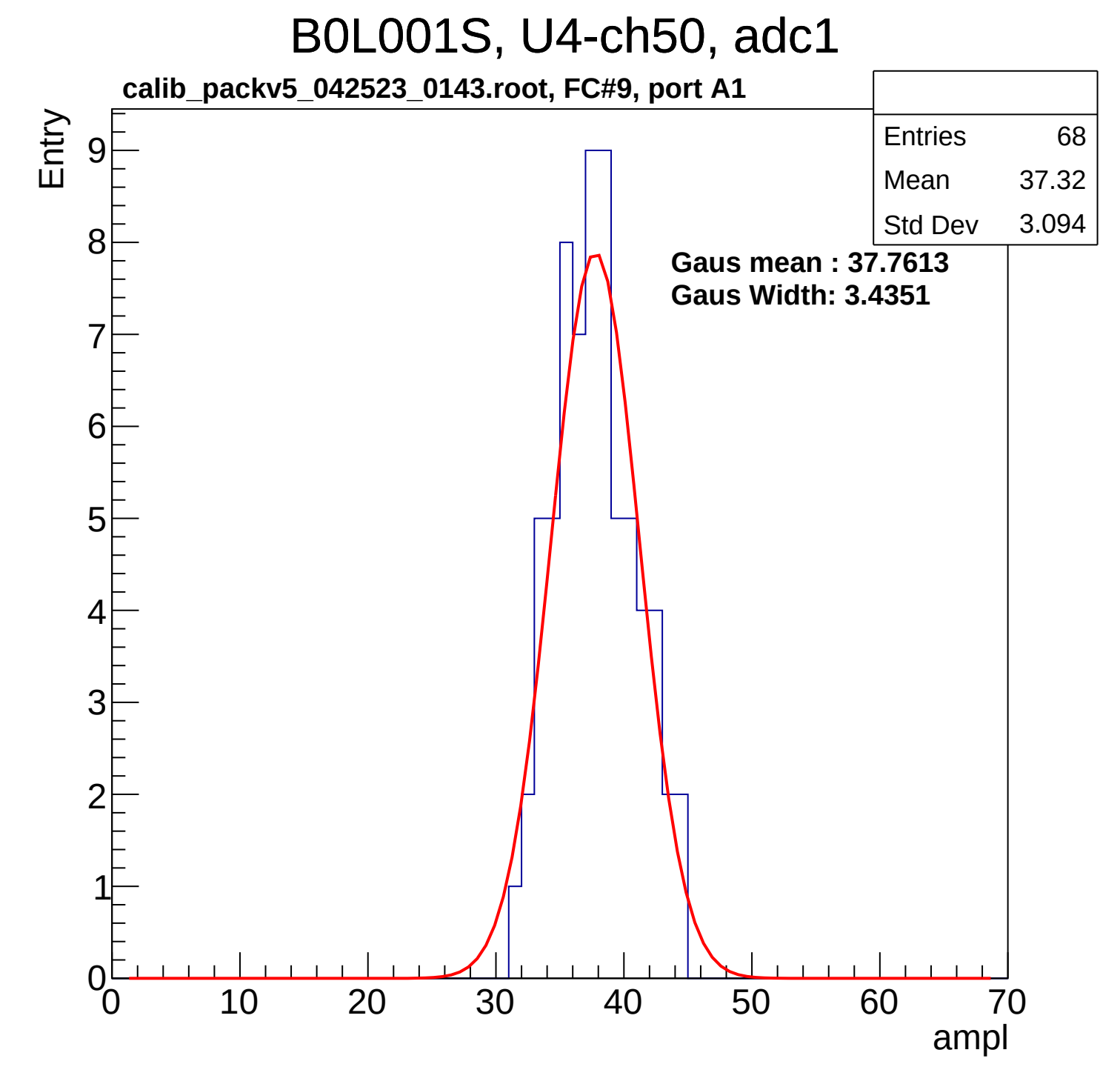
B0L001S, U4-ch50, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.32
Std Dev 3.094
Gaus mean : 37.7613
Gaus Width: 3.4351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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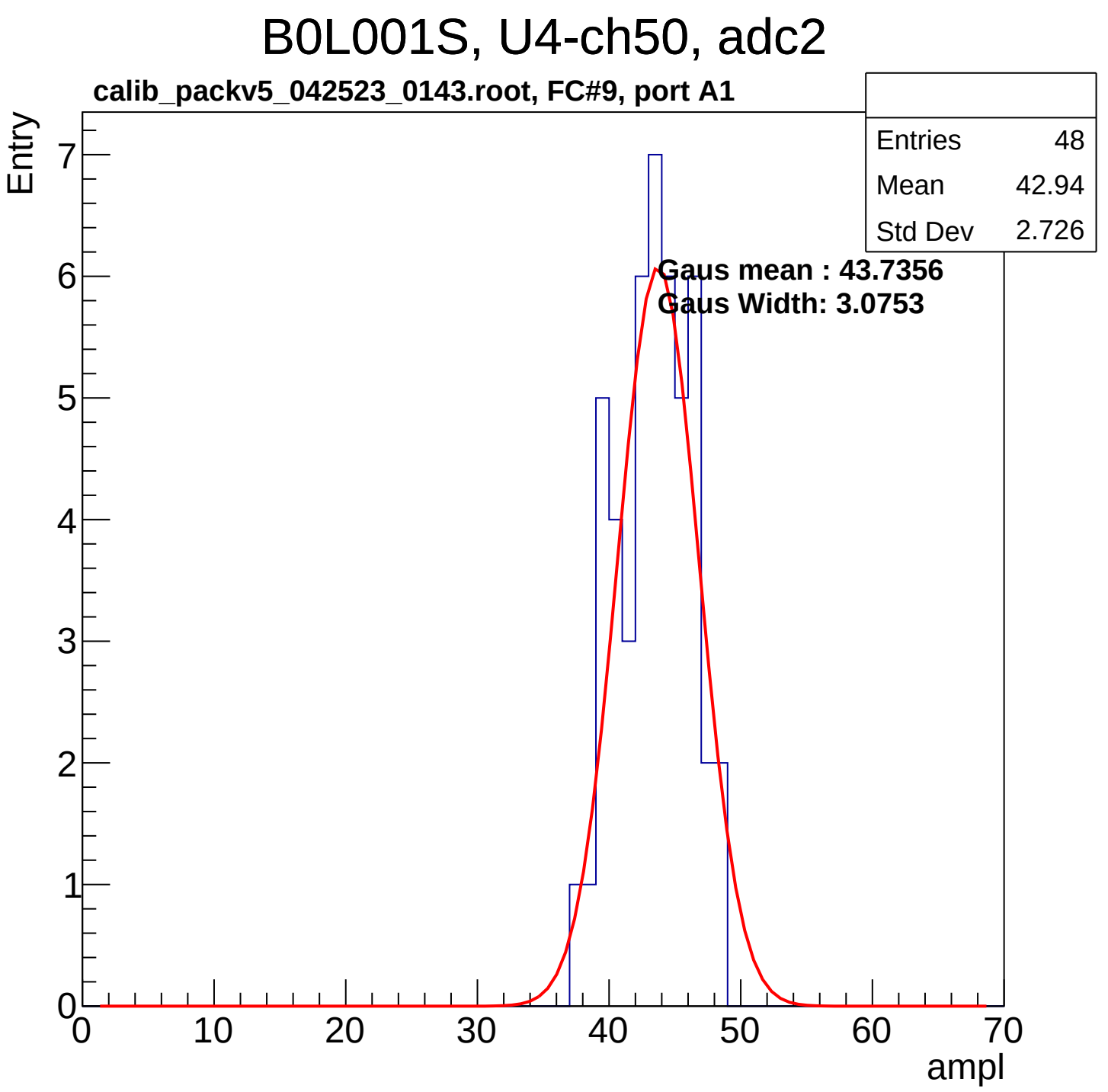
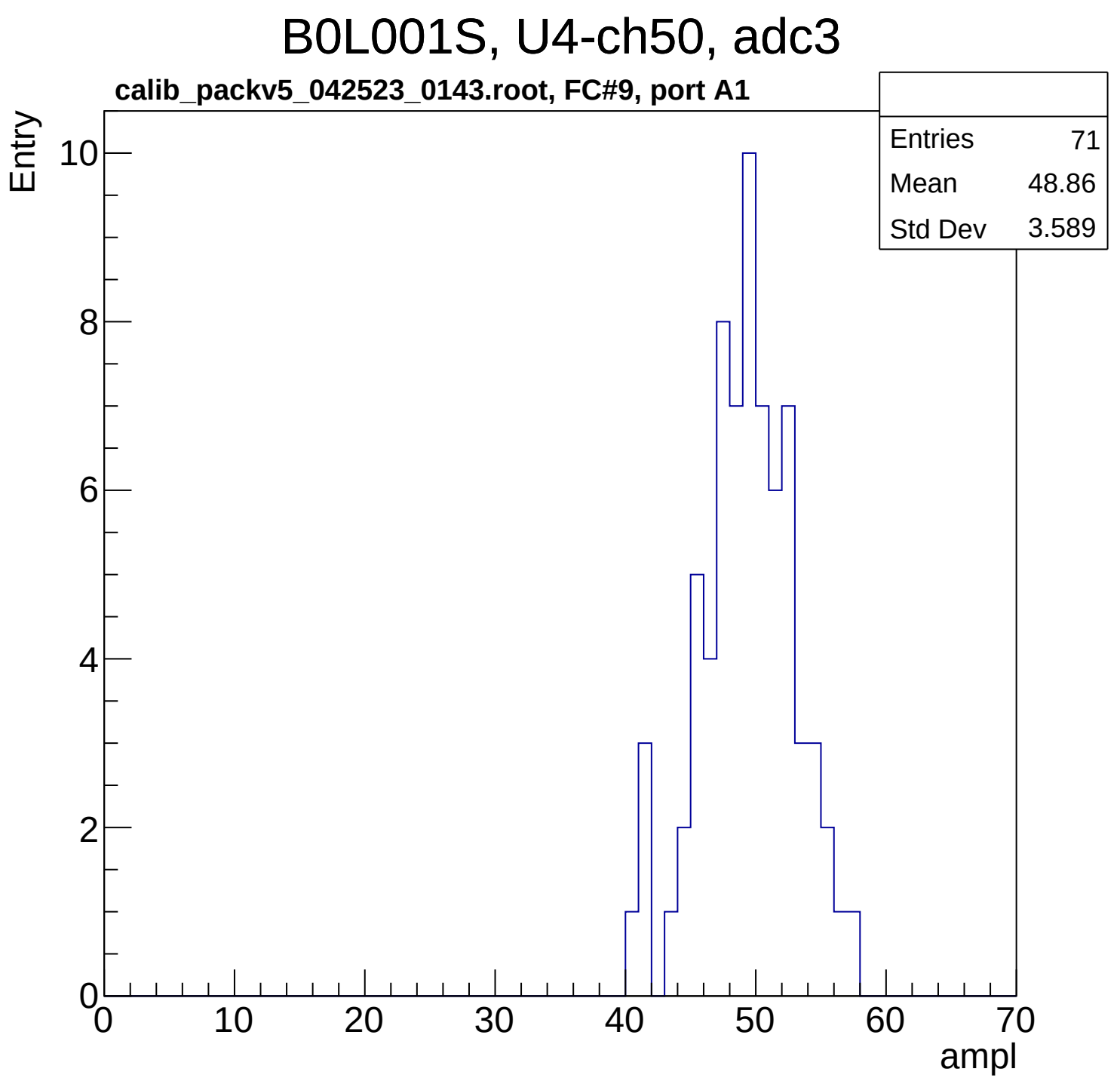
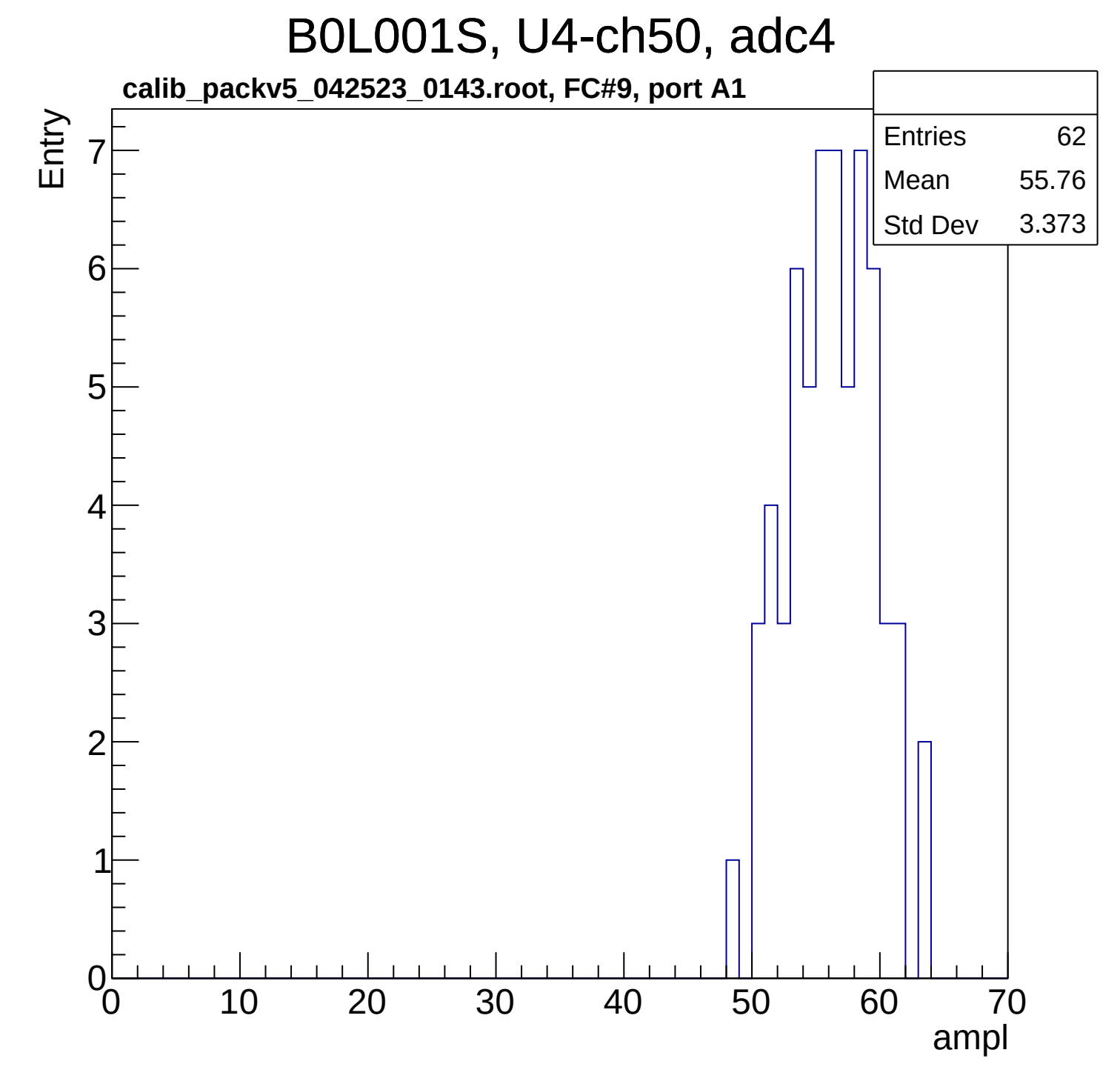
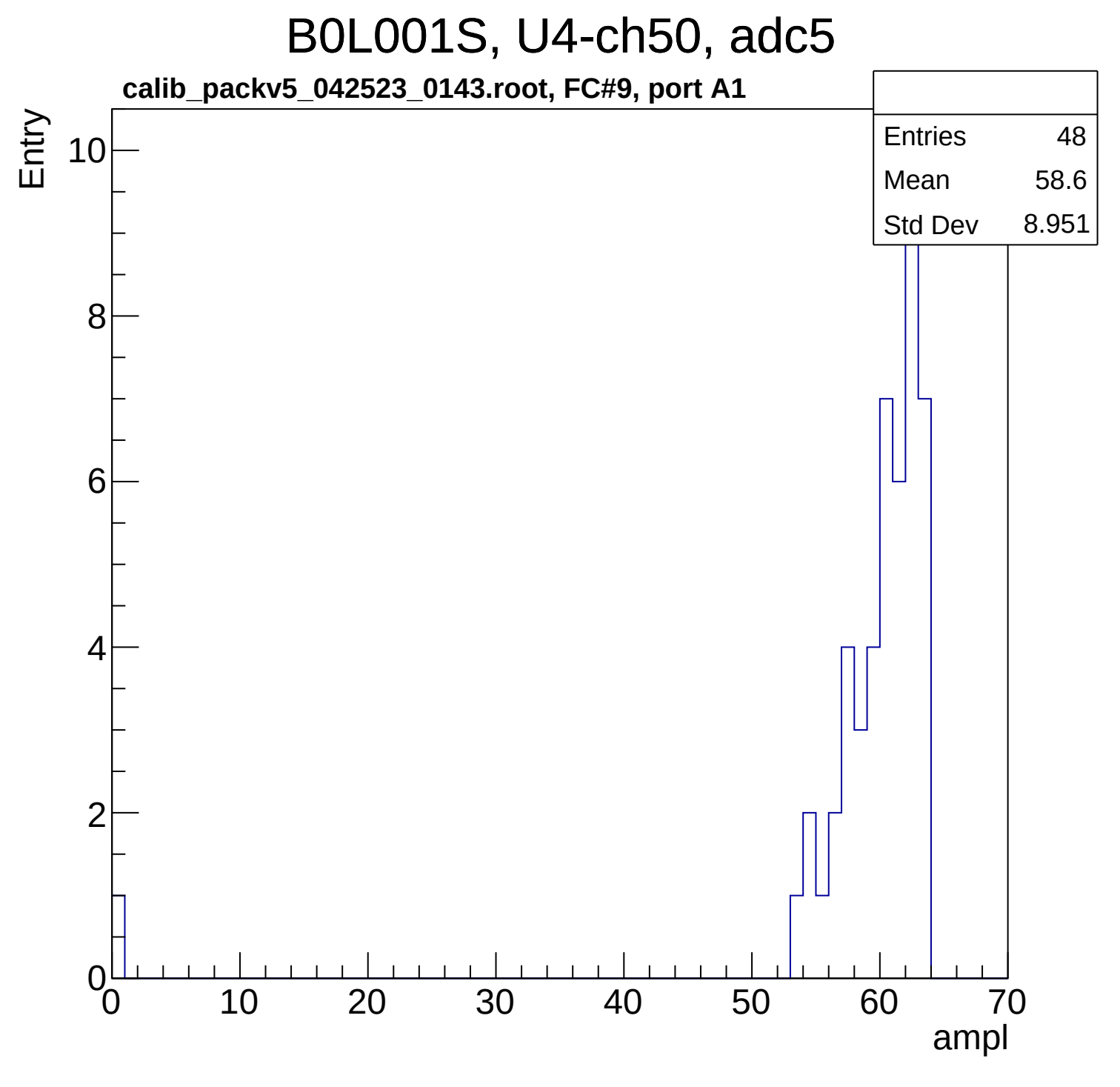
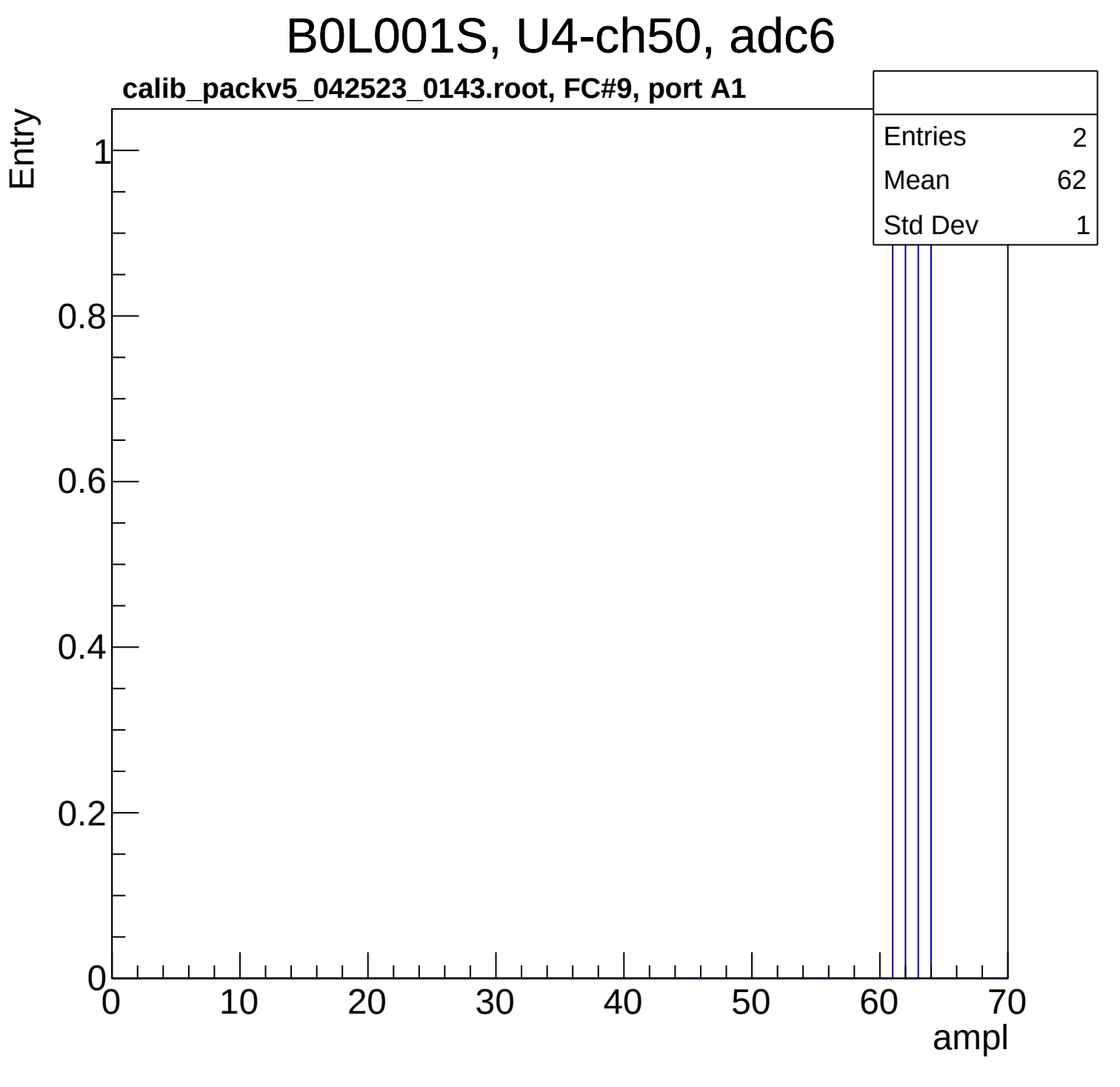
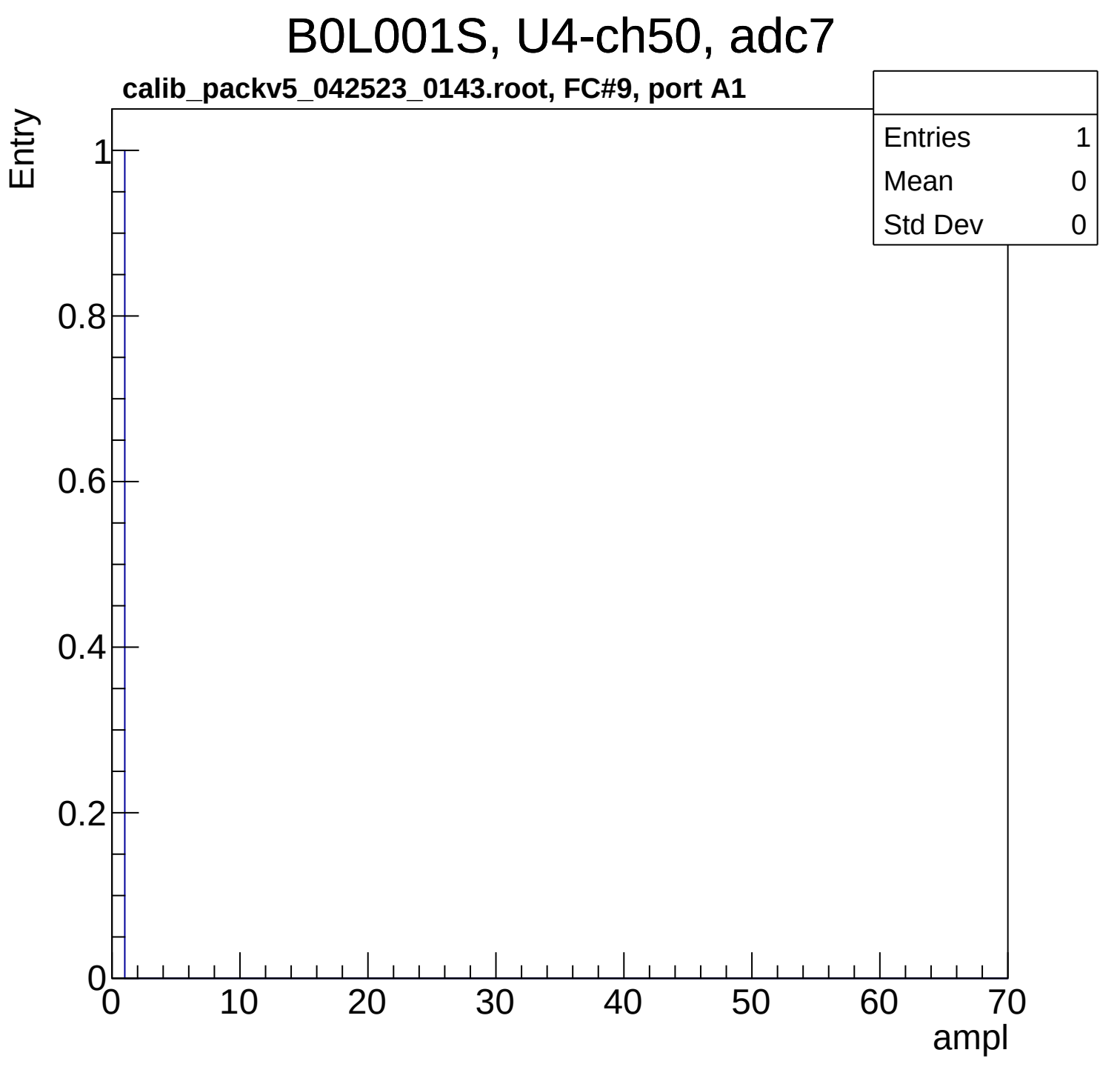

B0L001S, U4-ch50, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 42.94
Std Dev 2.726
Gaus mean : 43.7356
Gaus Width: 3.0753
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

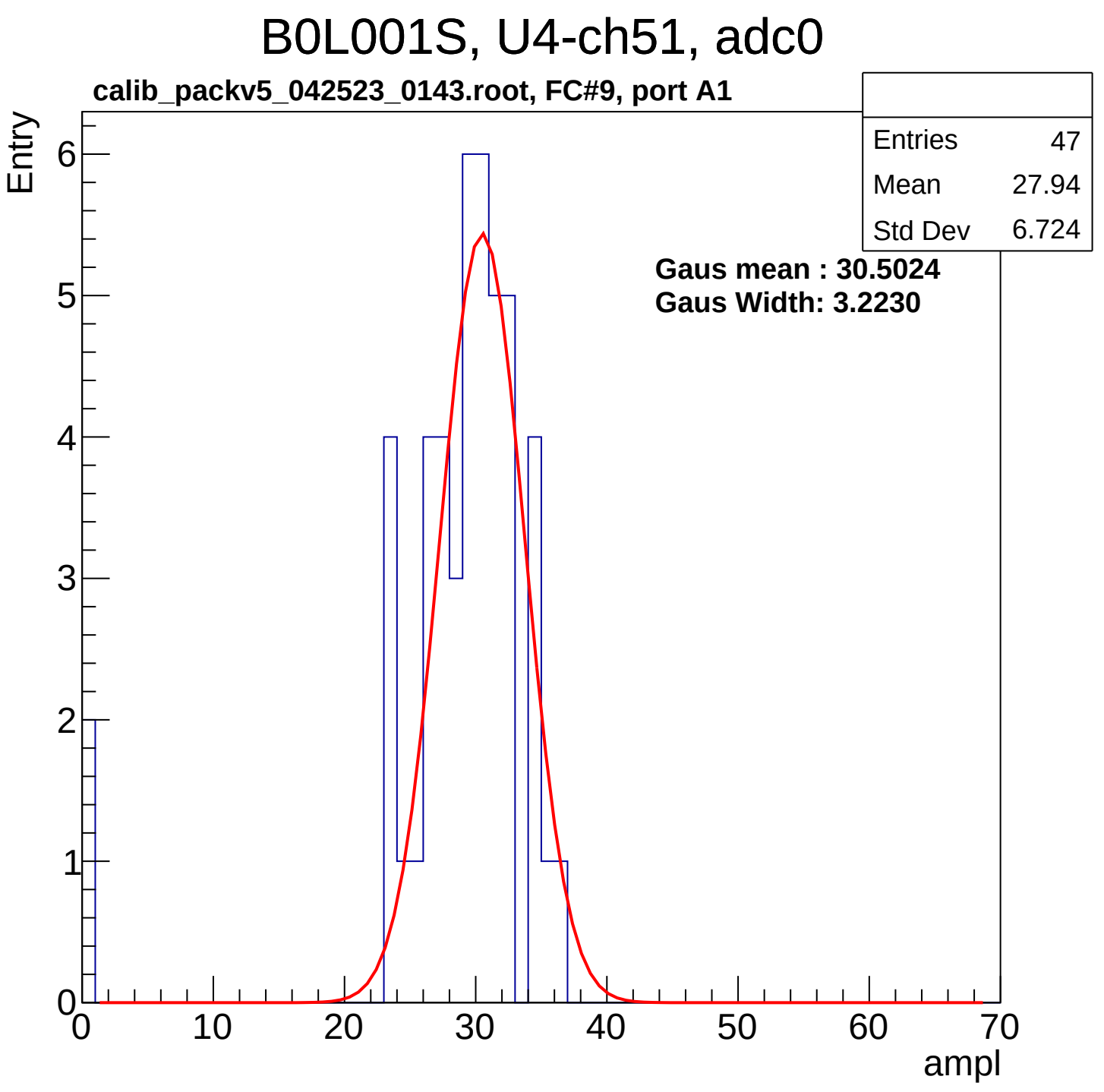

B0L001S, U4-ch50, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 48.86
Std Dev 3.589
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

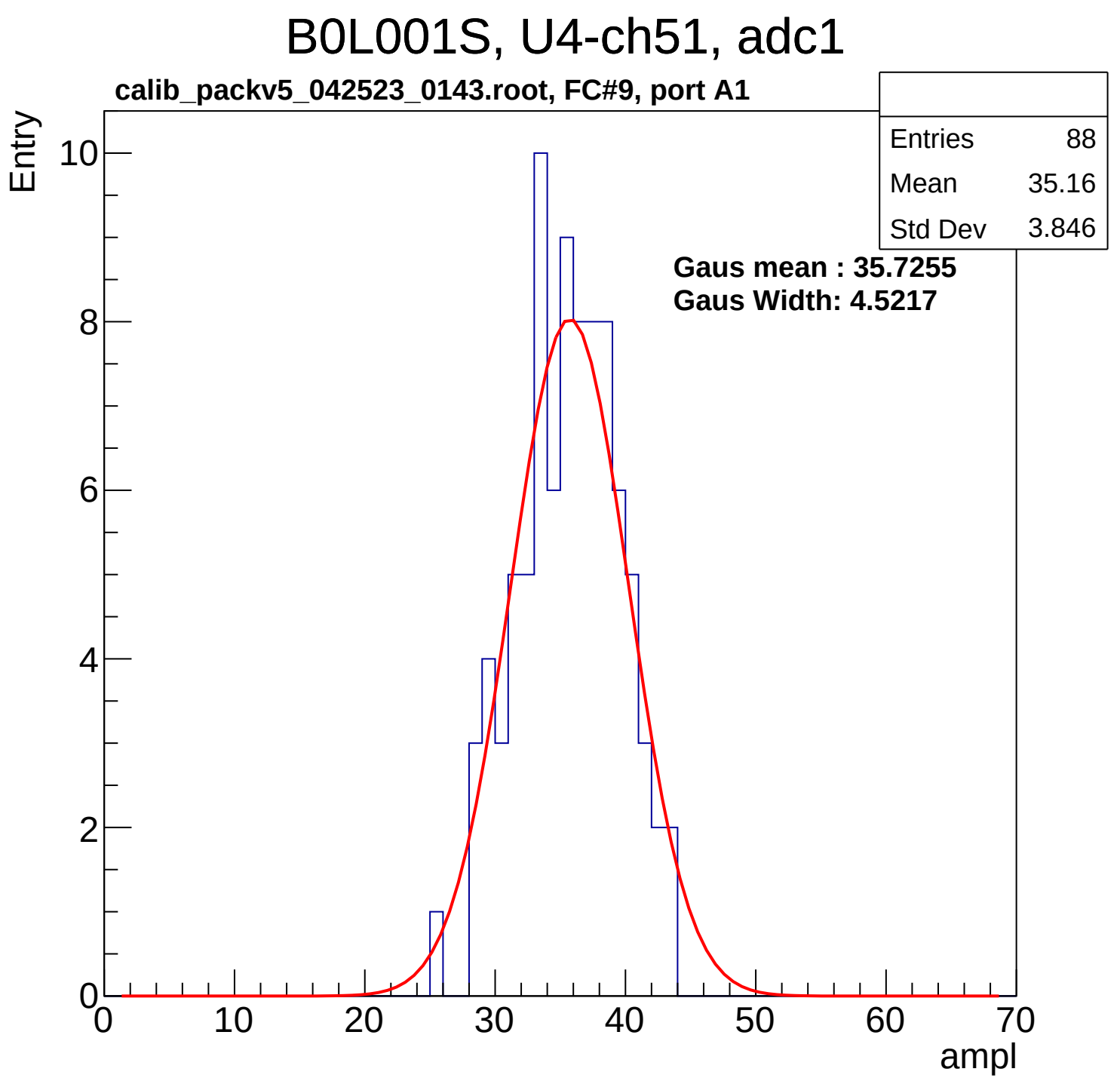

B0L001S, U4-ch50, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.76
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

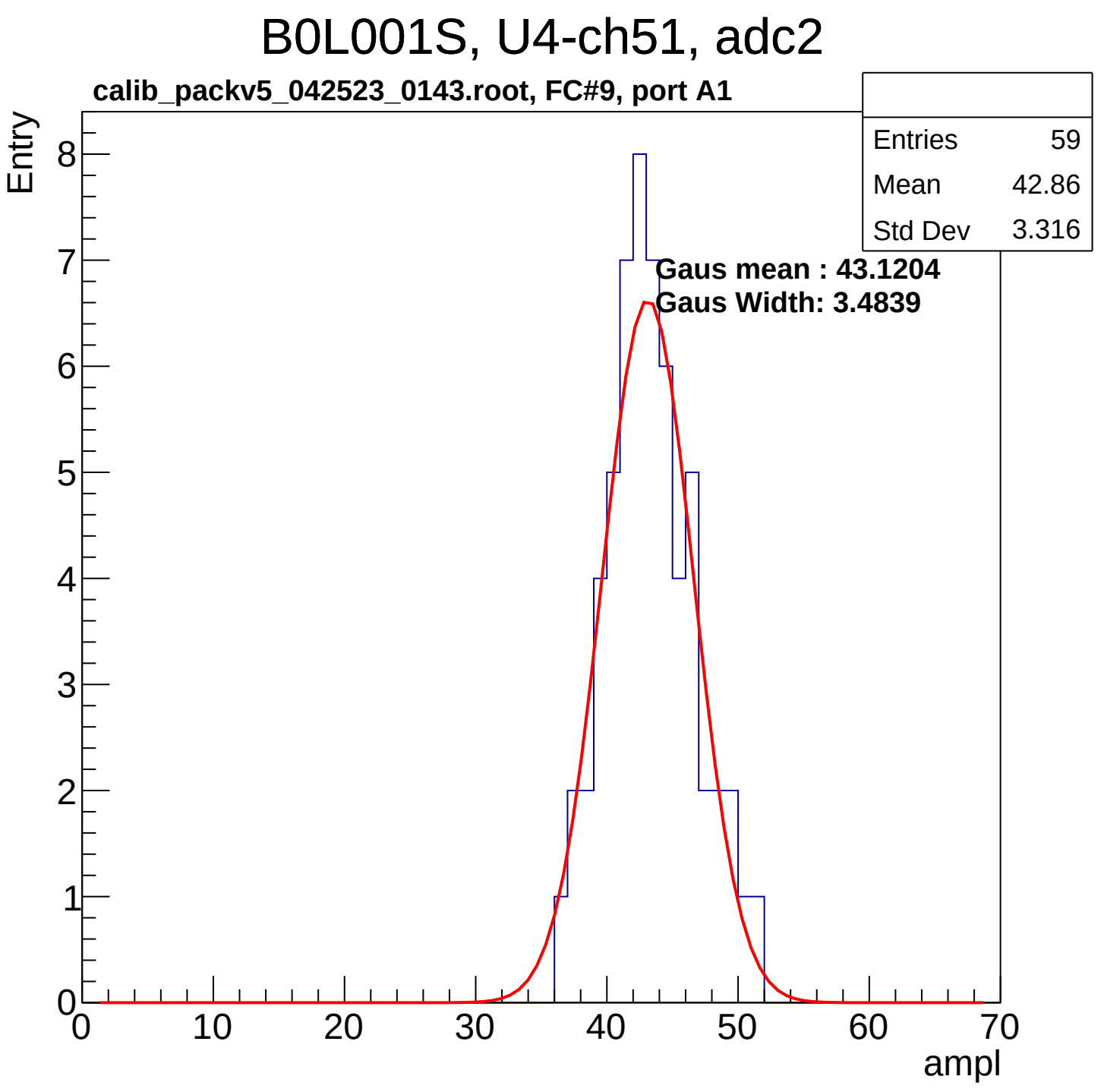

B0L001S, U4-ch50, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.6
Std Dev 8.951
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

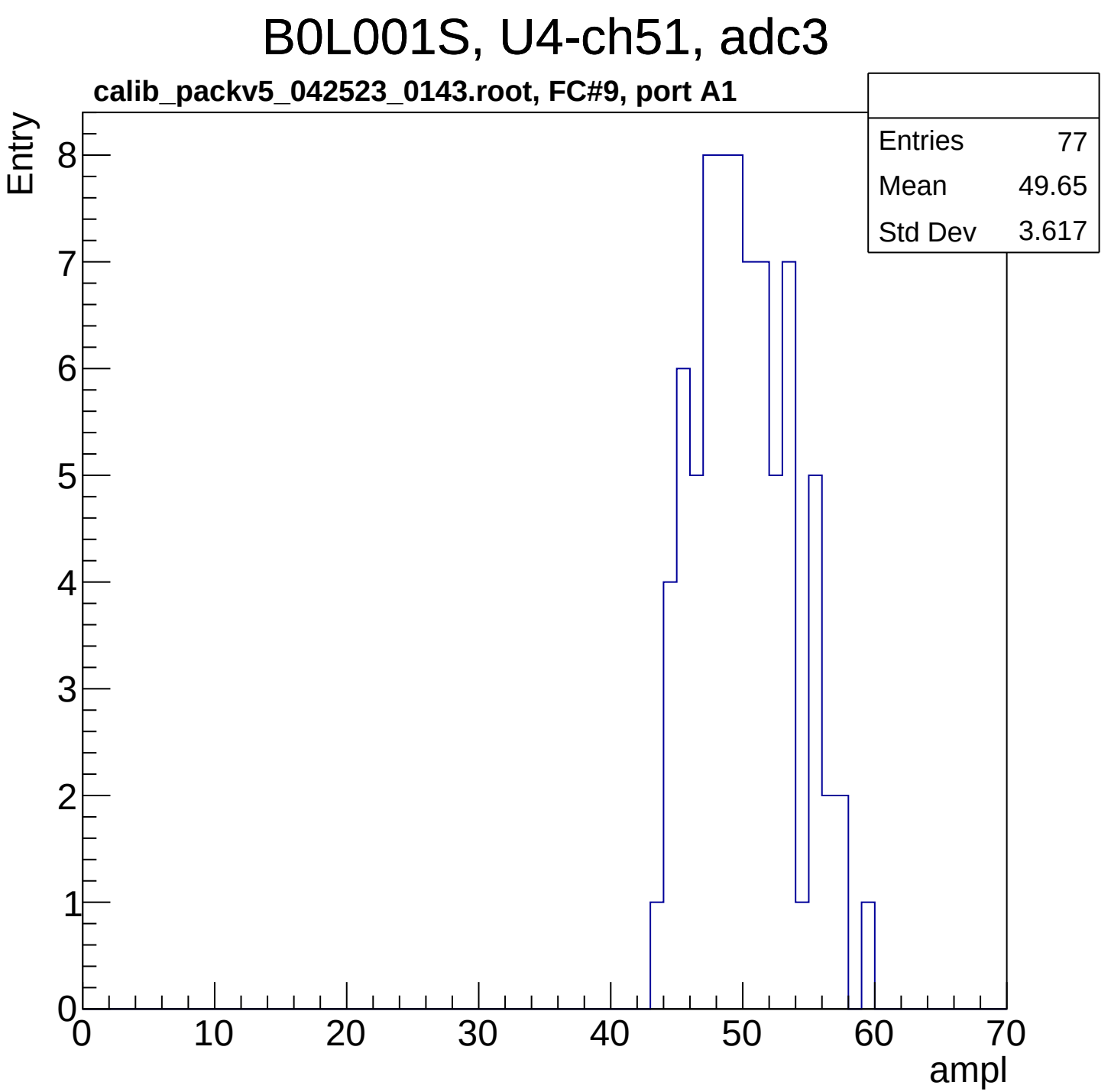

B0L001S, U4-ch50, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

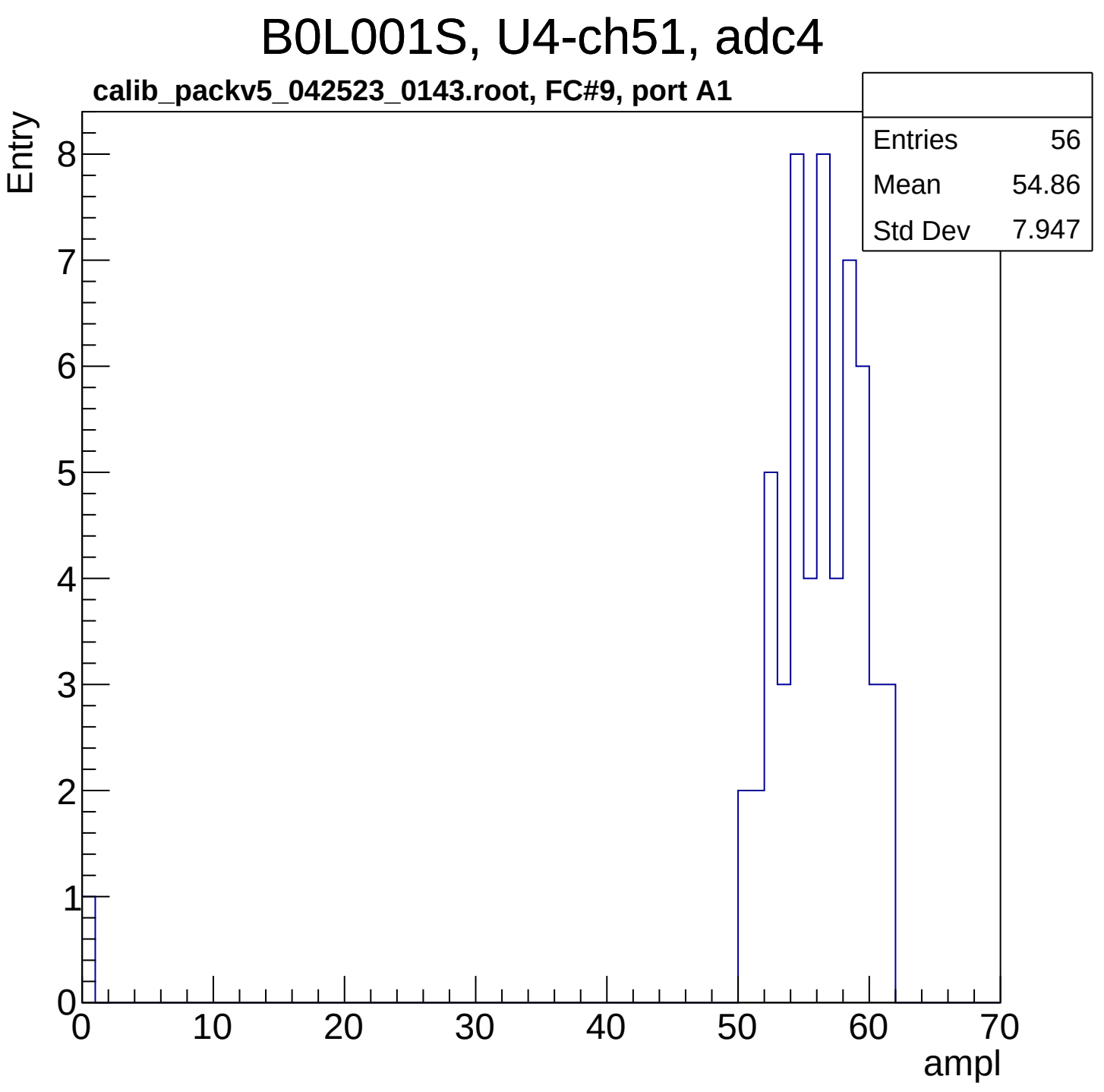

B0L001S, U4-ch50, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

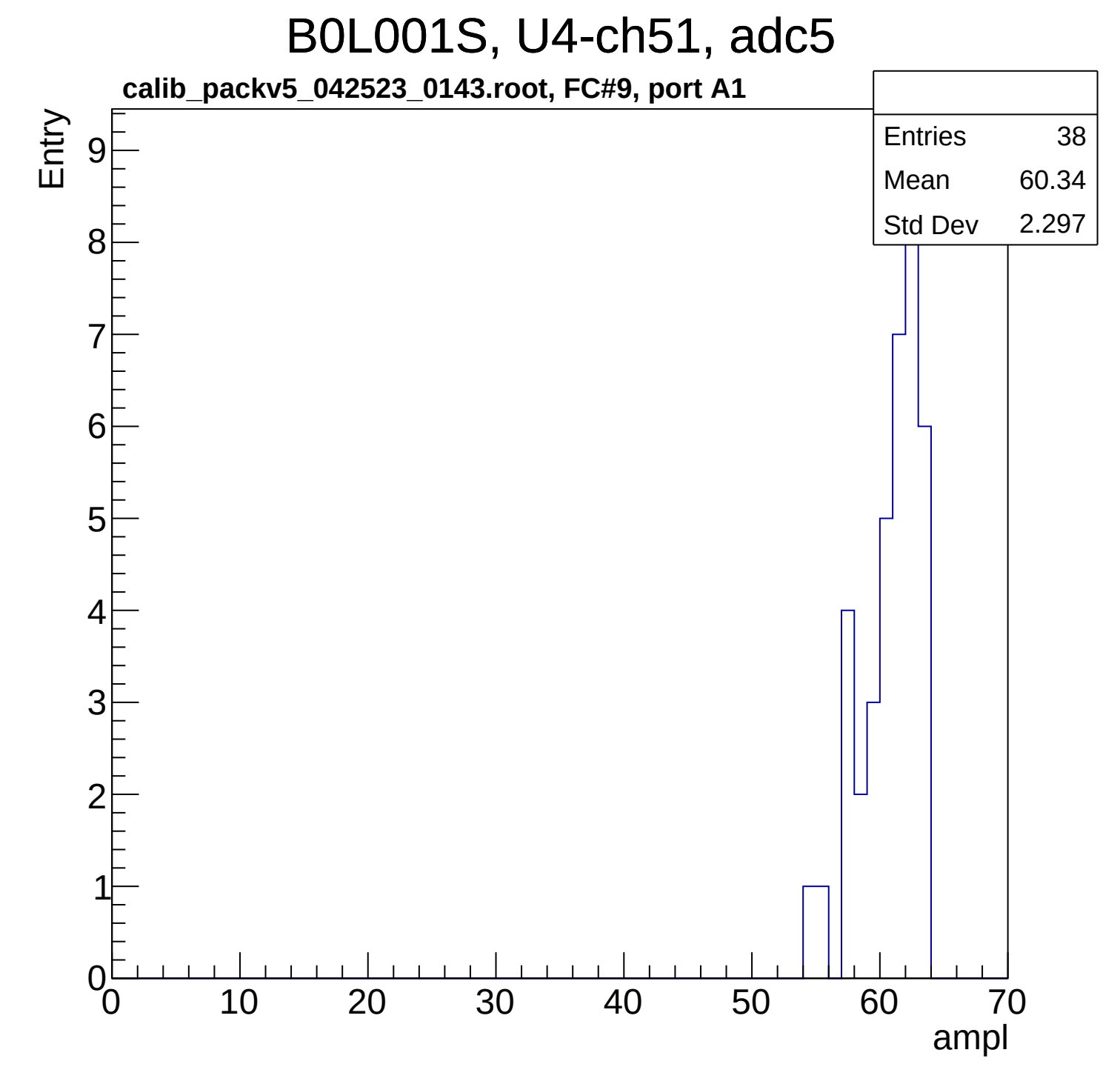

B0L001S, U4-ch51, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 27.94
Std Dev 6.724
Gaus mean : 30.5024
Gaus Width: 3.2230
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

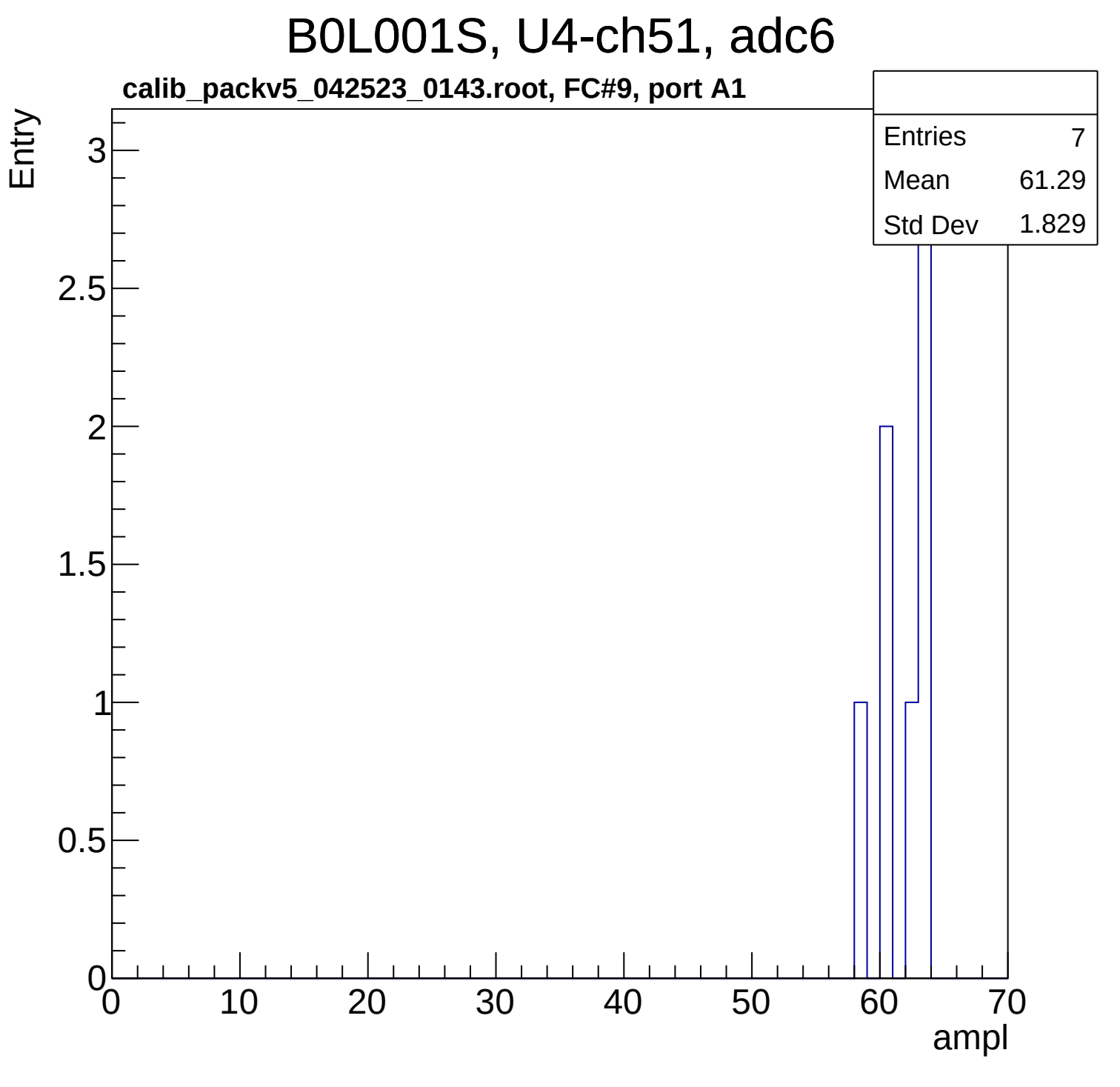

B0L001S, U4-ch51, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 35.16
Std Dev 3.846
Gaus mean : 35.7255
Gaus Width: 4.5217
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

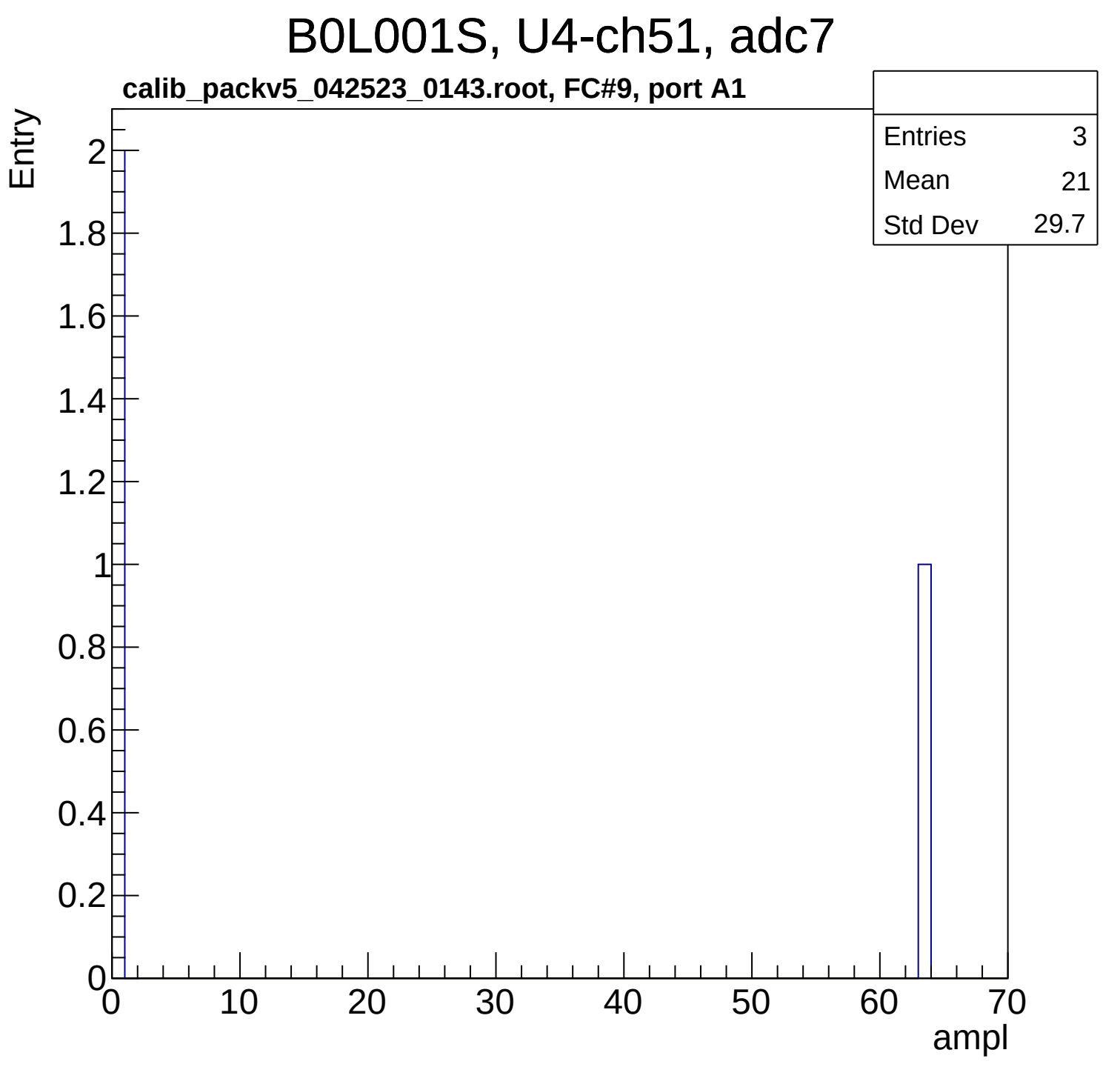

B0L001S, U4-ch51, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 42.86
Std Dev 3.316
Gaus mean : 43.1204
Gaus Width: 3.4839
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

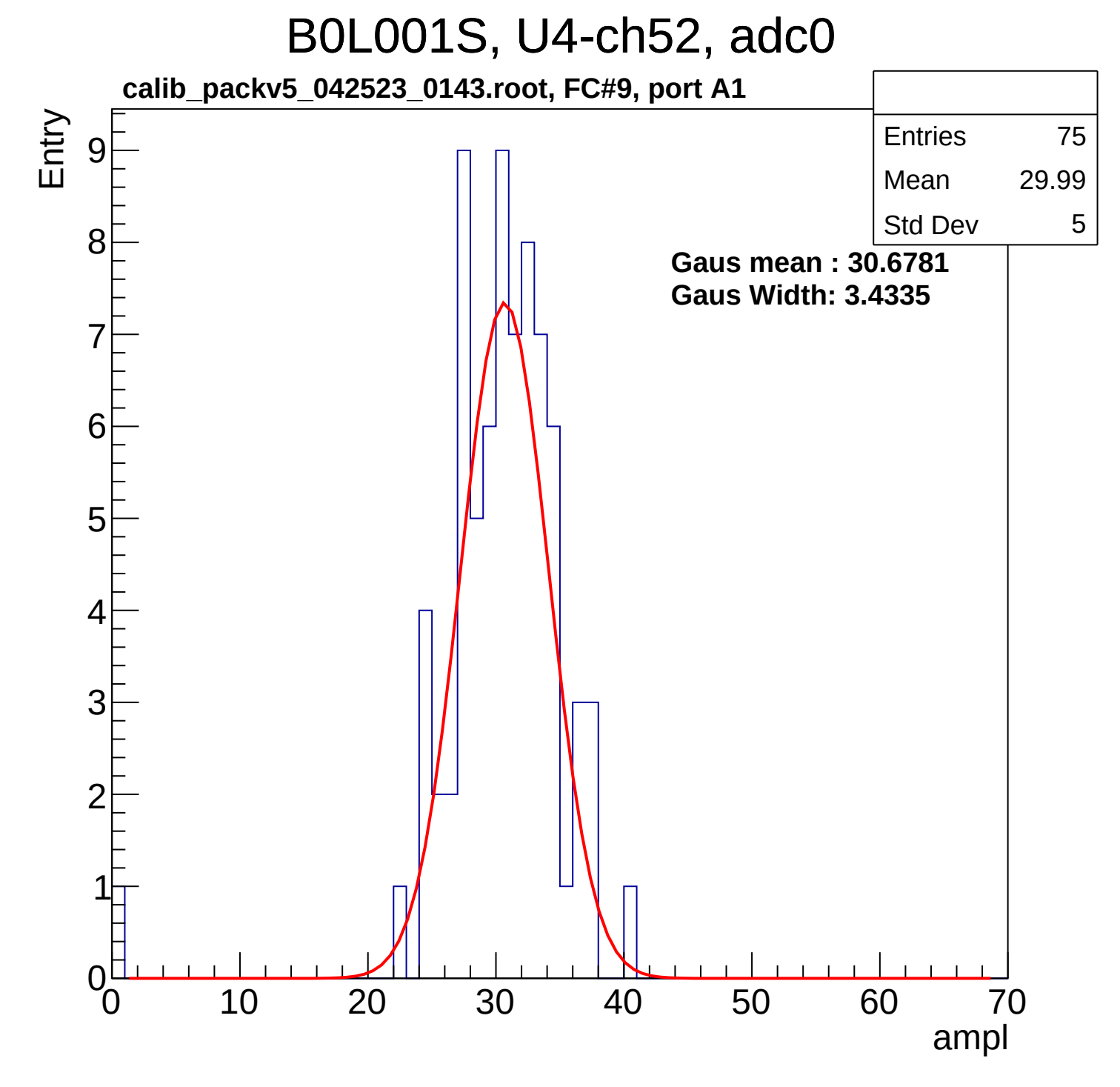

B0L001S, U4-ch51, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 49.65
Std Dev 3.617
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

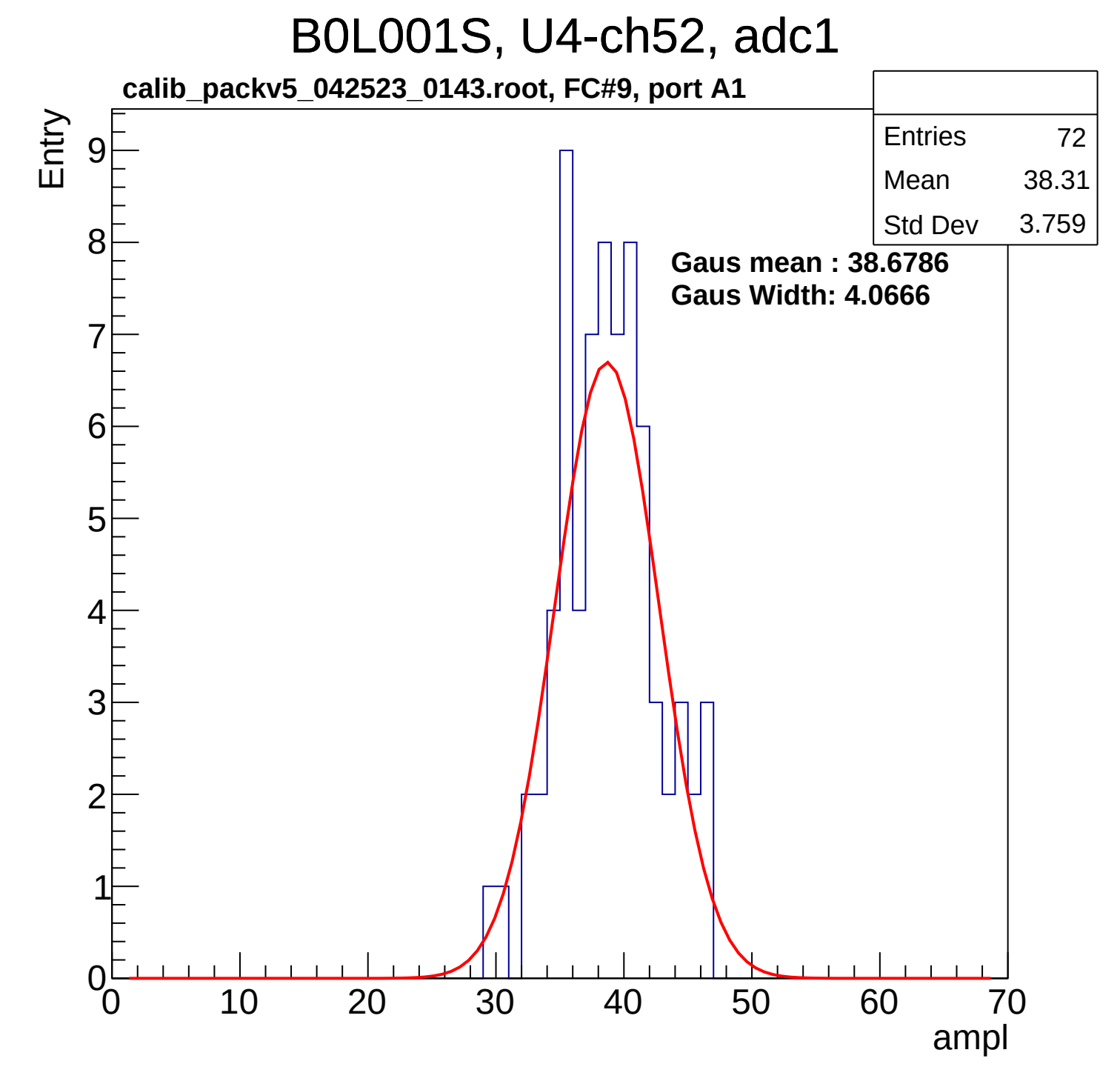

B0L001S, U4-ch51, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 54.86
Std Dev 7.947
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch51, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.34
Std Dev 2.297
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch51, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.29
Std Dev 1.829
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch51, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch52, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 29.99
Std Dev 5
Gaus mean : 30.6781
Gaus Width: 3.4335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch52, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.31
Std Dev 3.759
Gaus mean : 38.6786
Gaus Width: 4.0666
Entry
ampl
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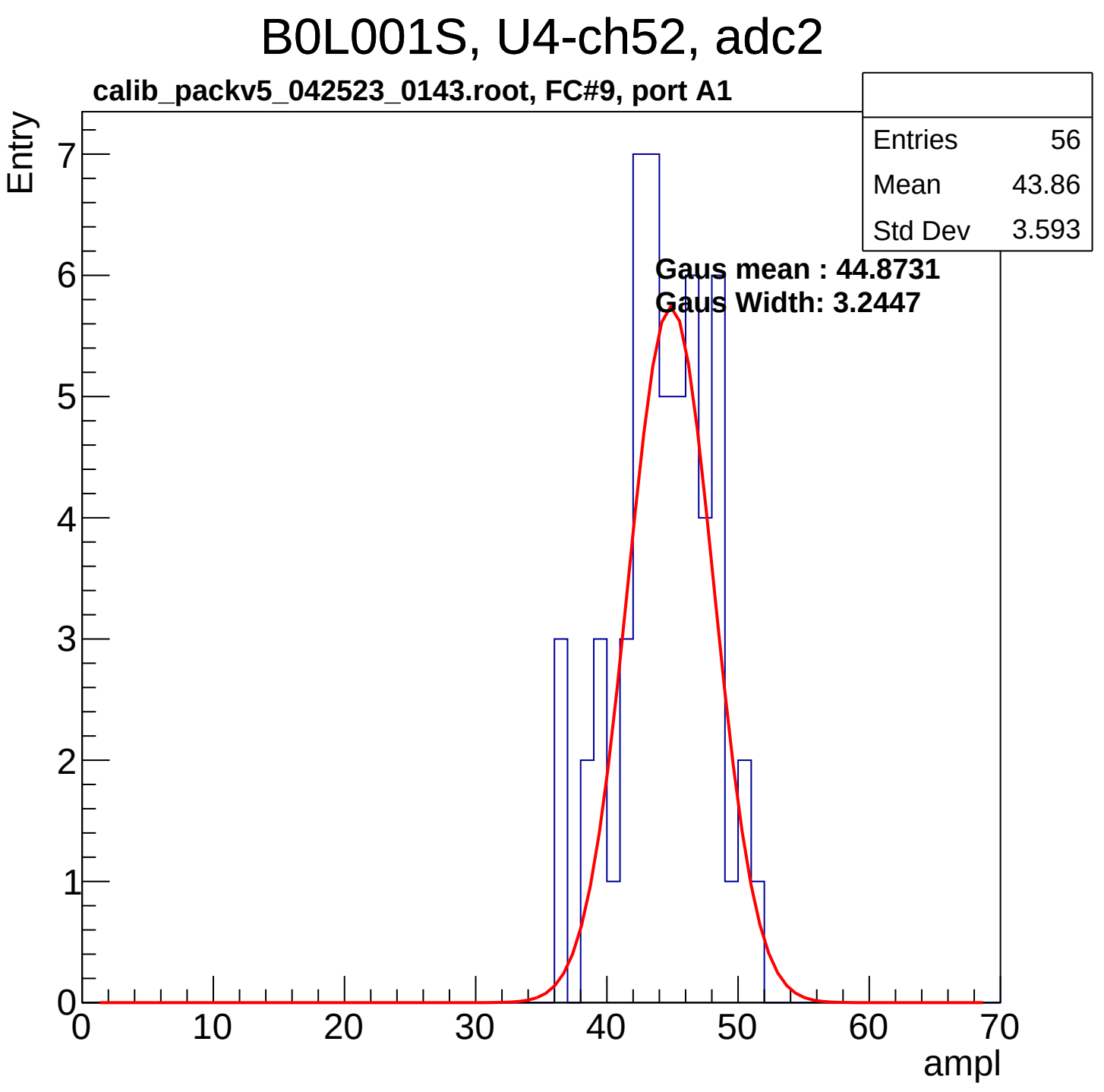
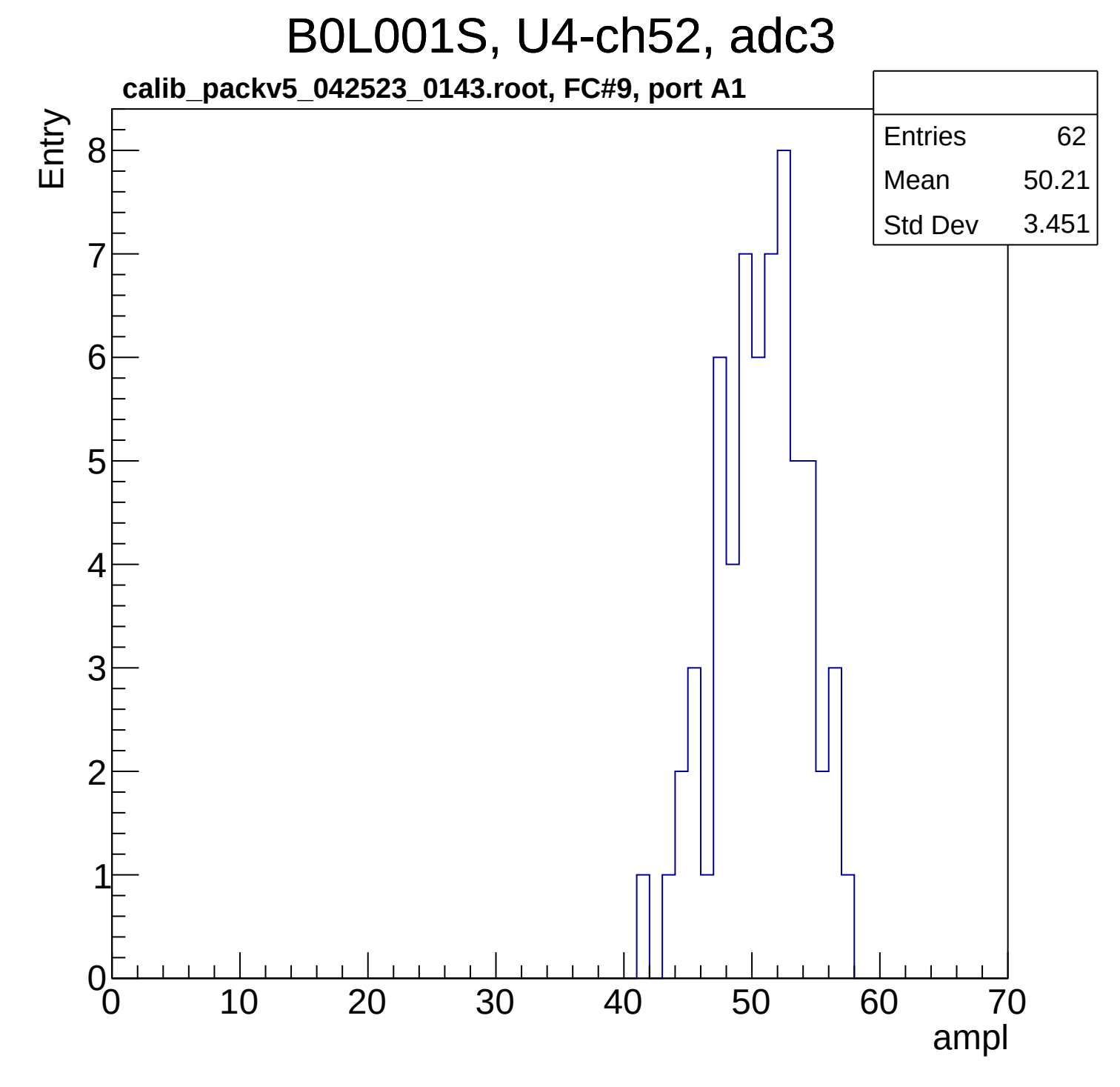
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch52, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 43.86
Std Dev 3.593
Gaus mean : 44.8731
Gaus Width: 3.2447
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch52, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.21
Std Dev 3.451
Entry
ampl
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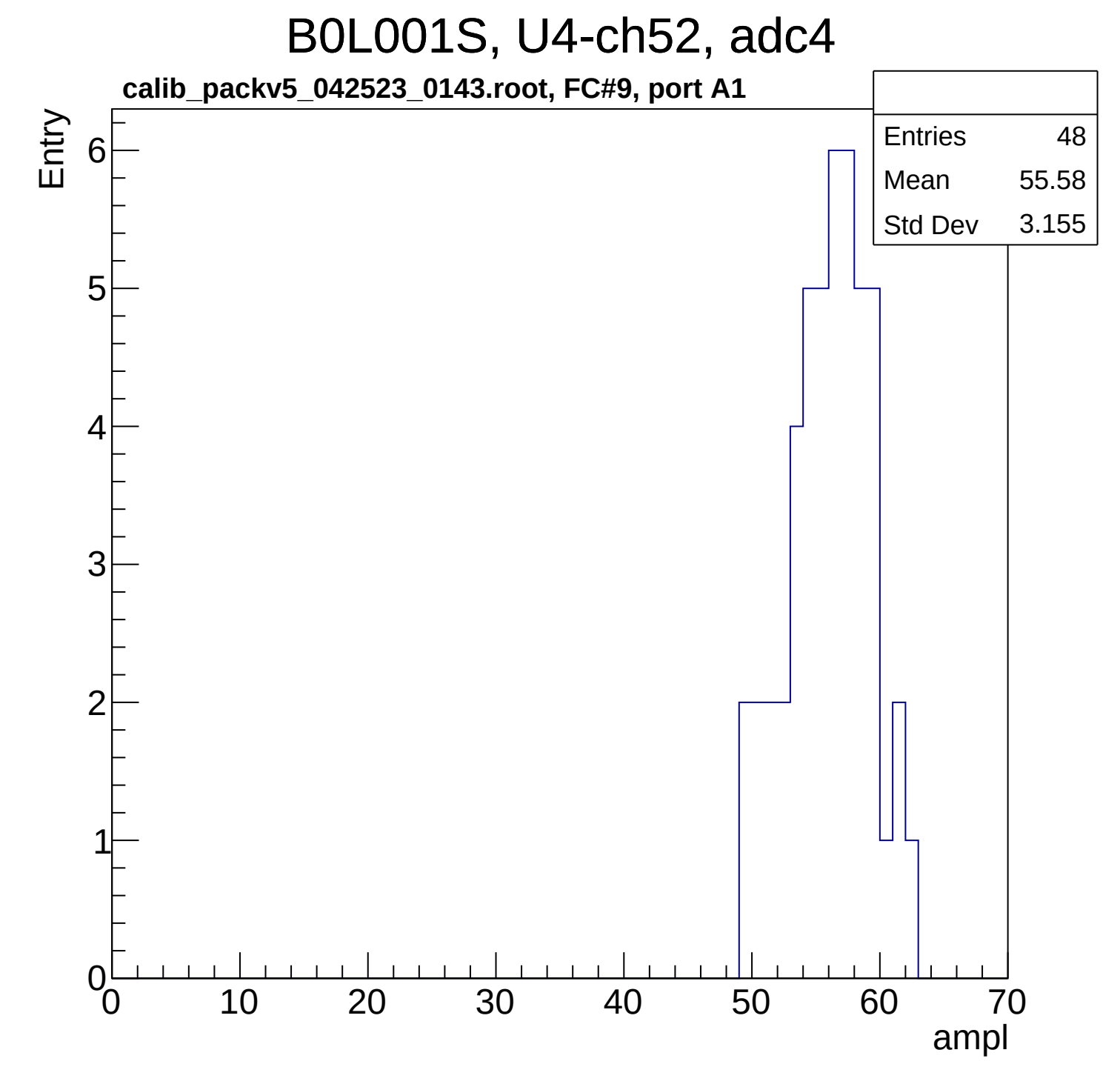
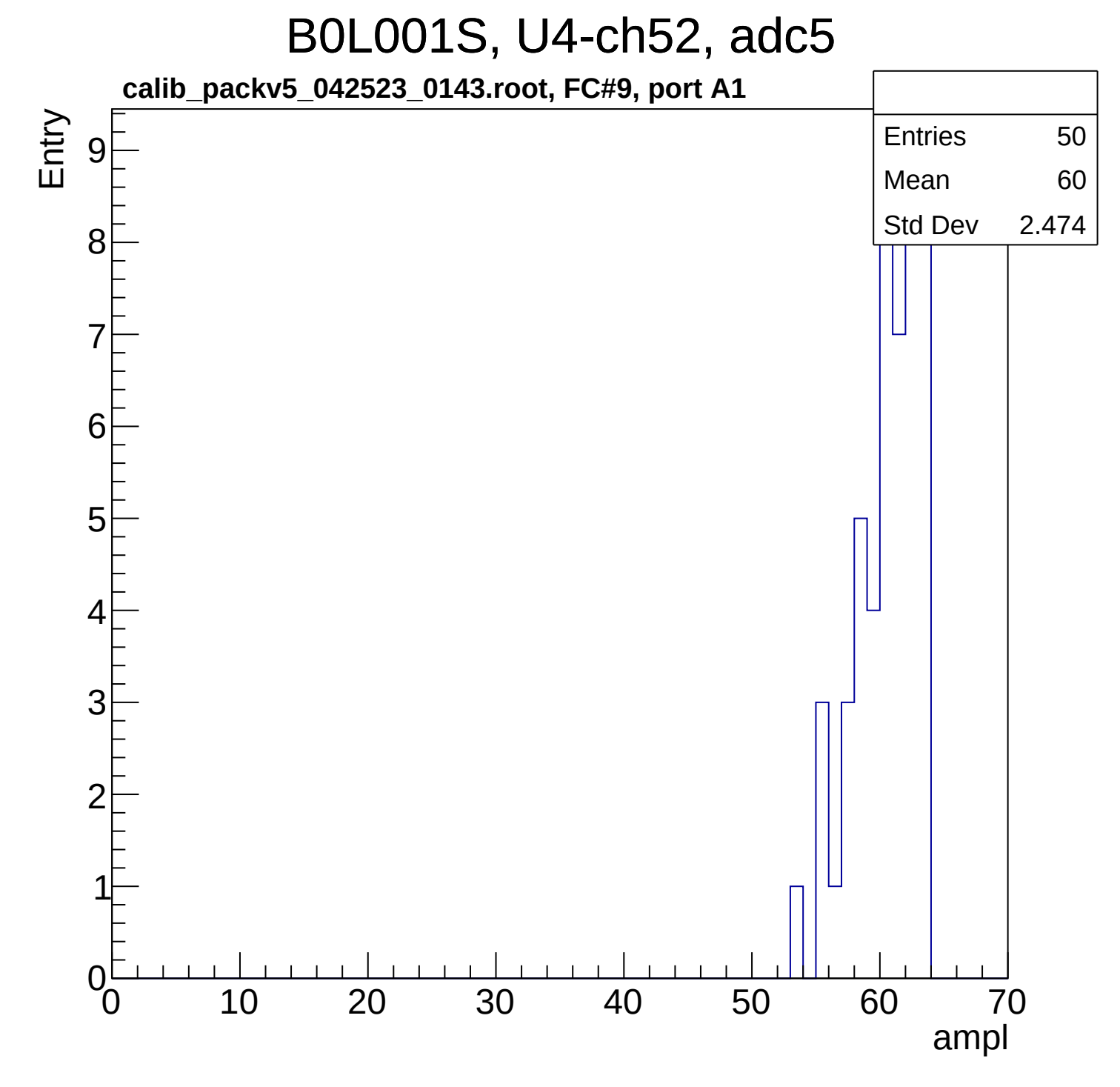
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch52, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.58
Std Dev 3.155
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch52, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 60
Std Dev 2.474
Entry
ampl
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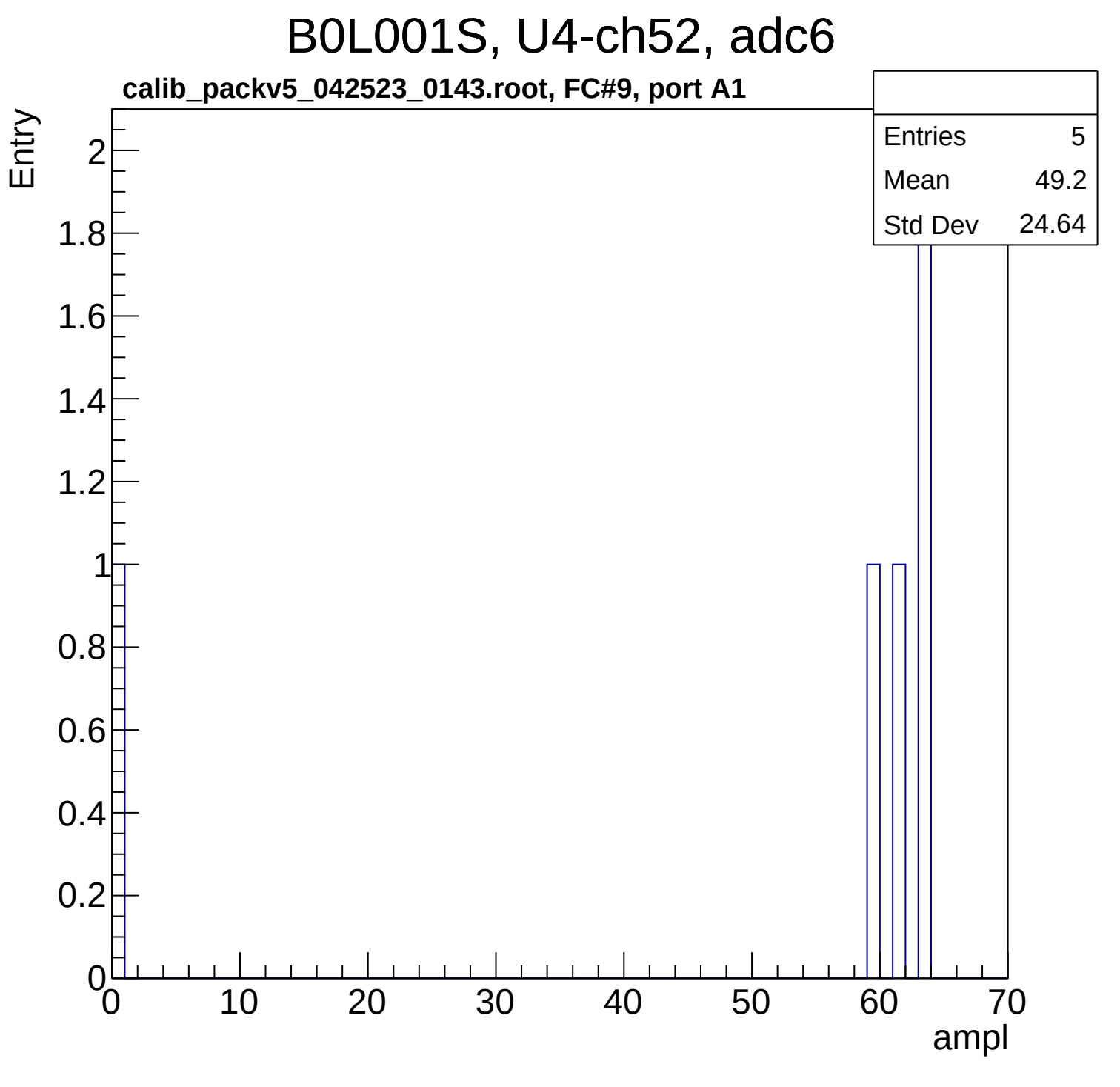
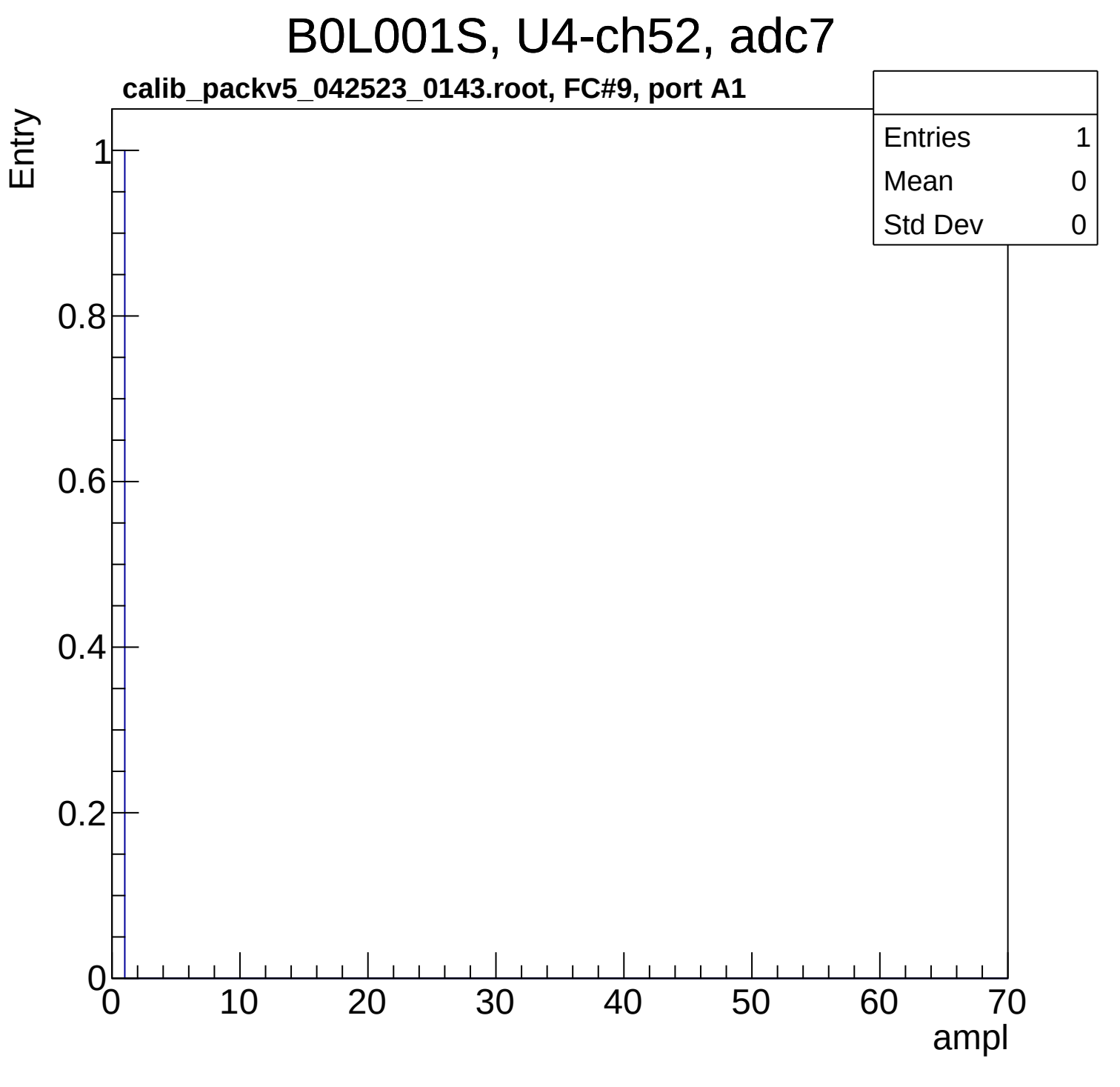
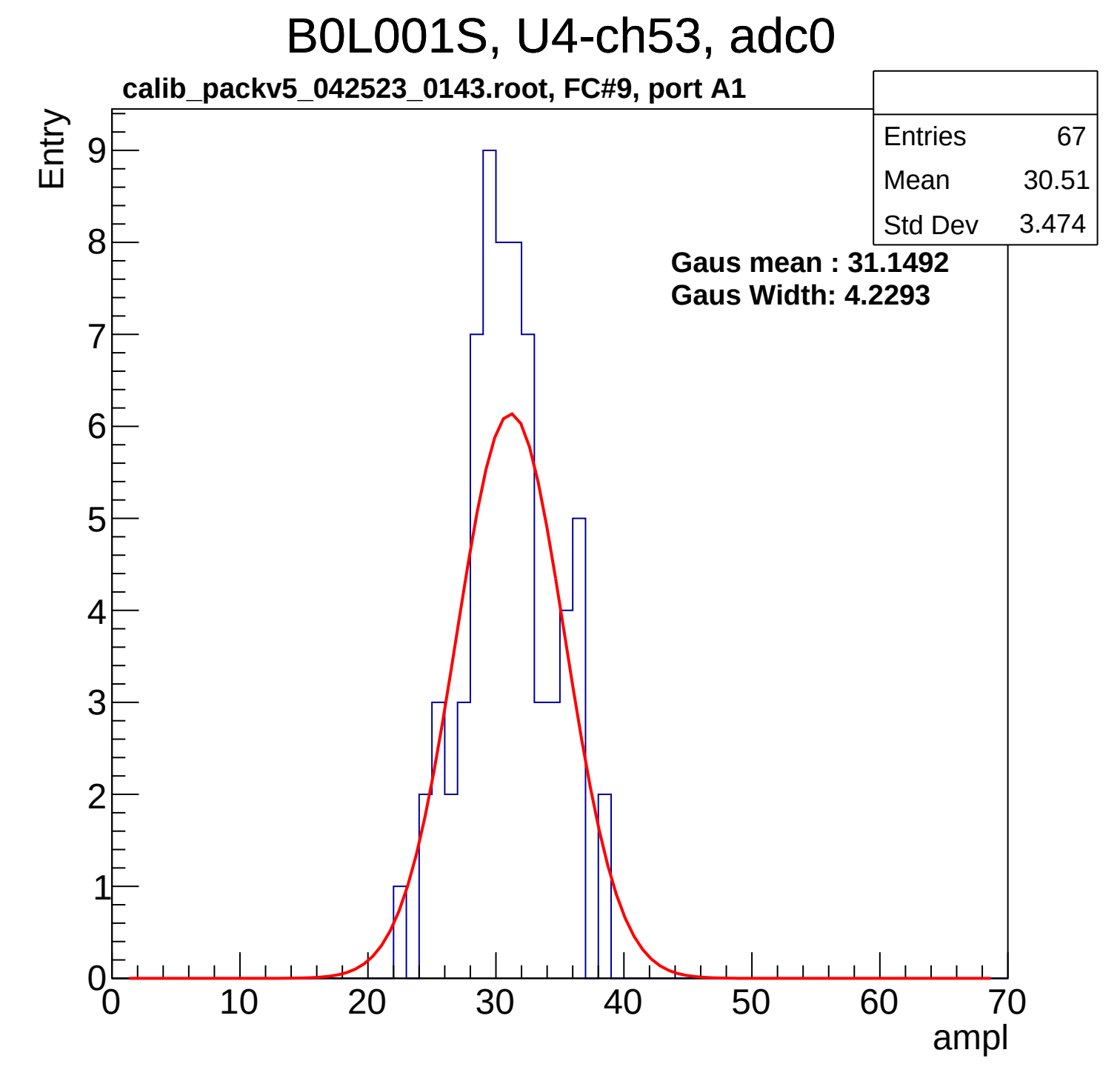
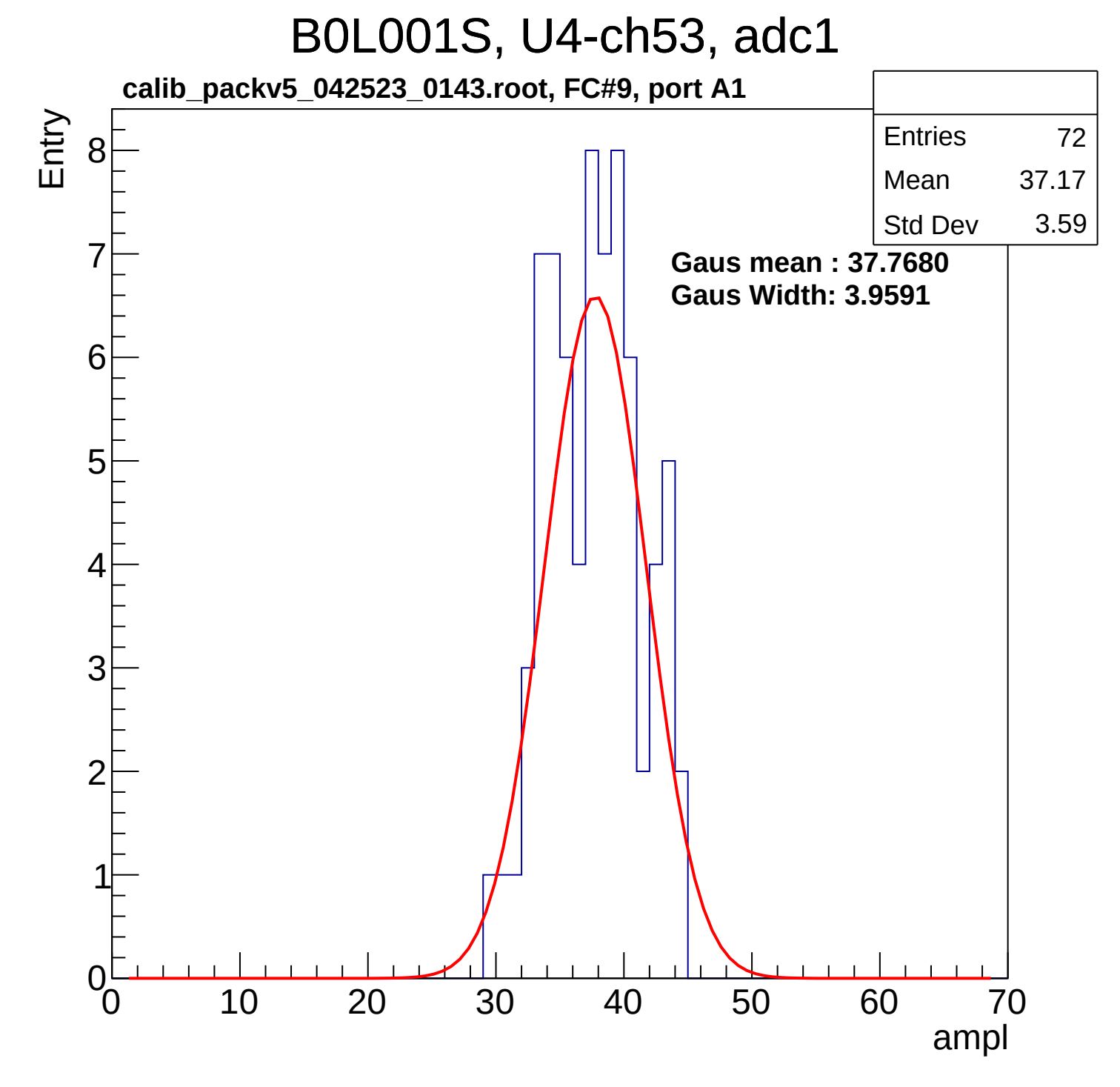
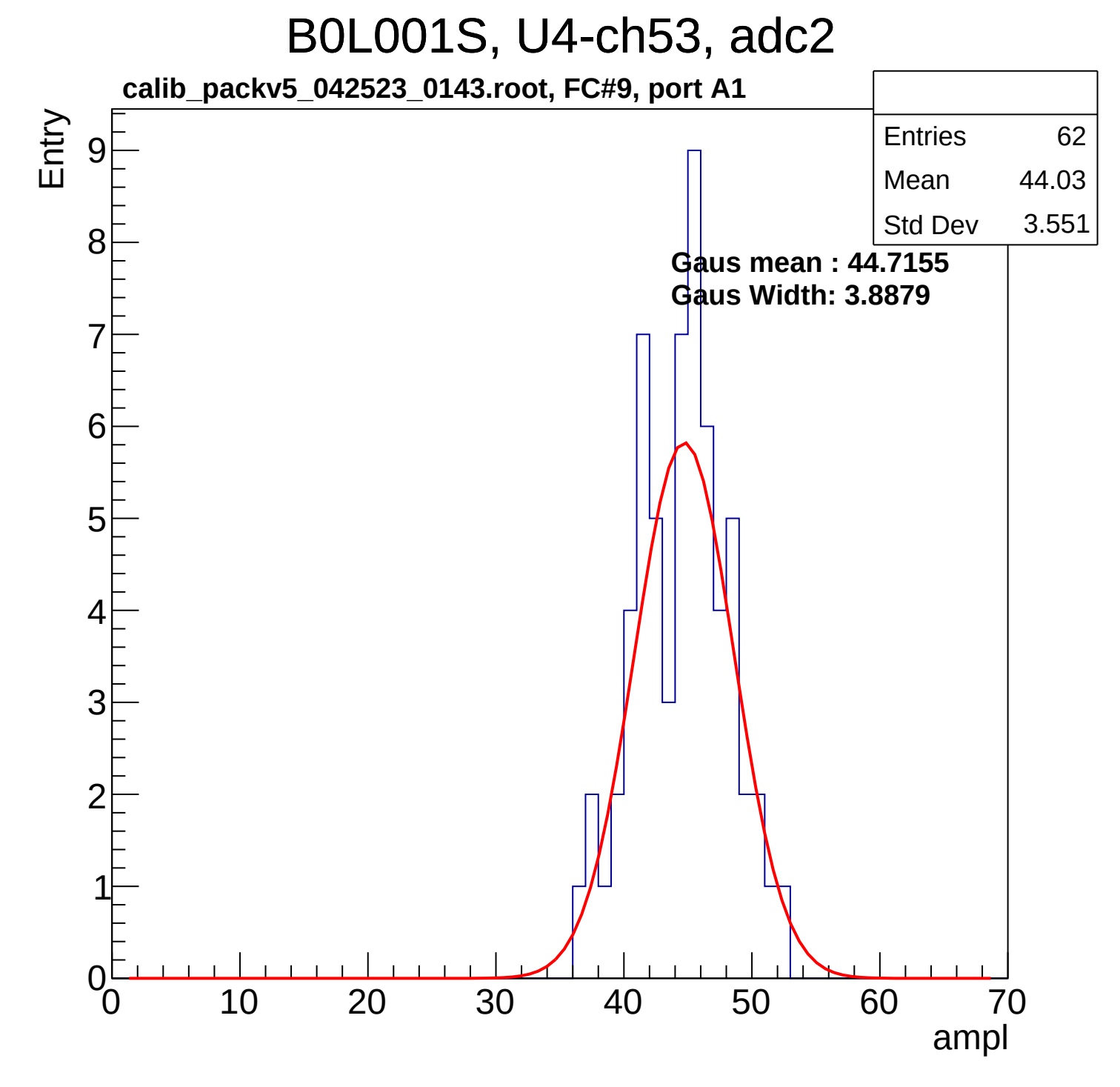
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

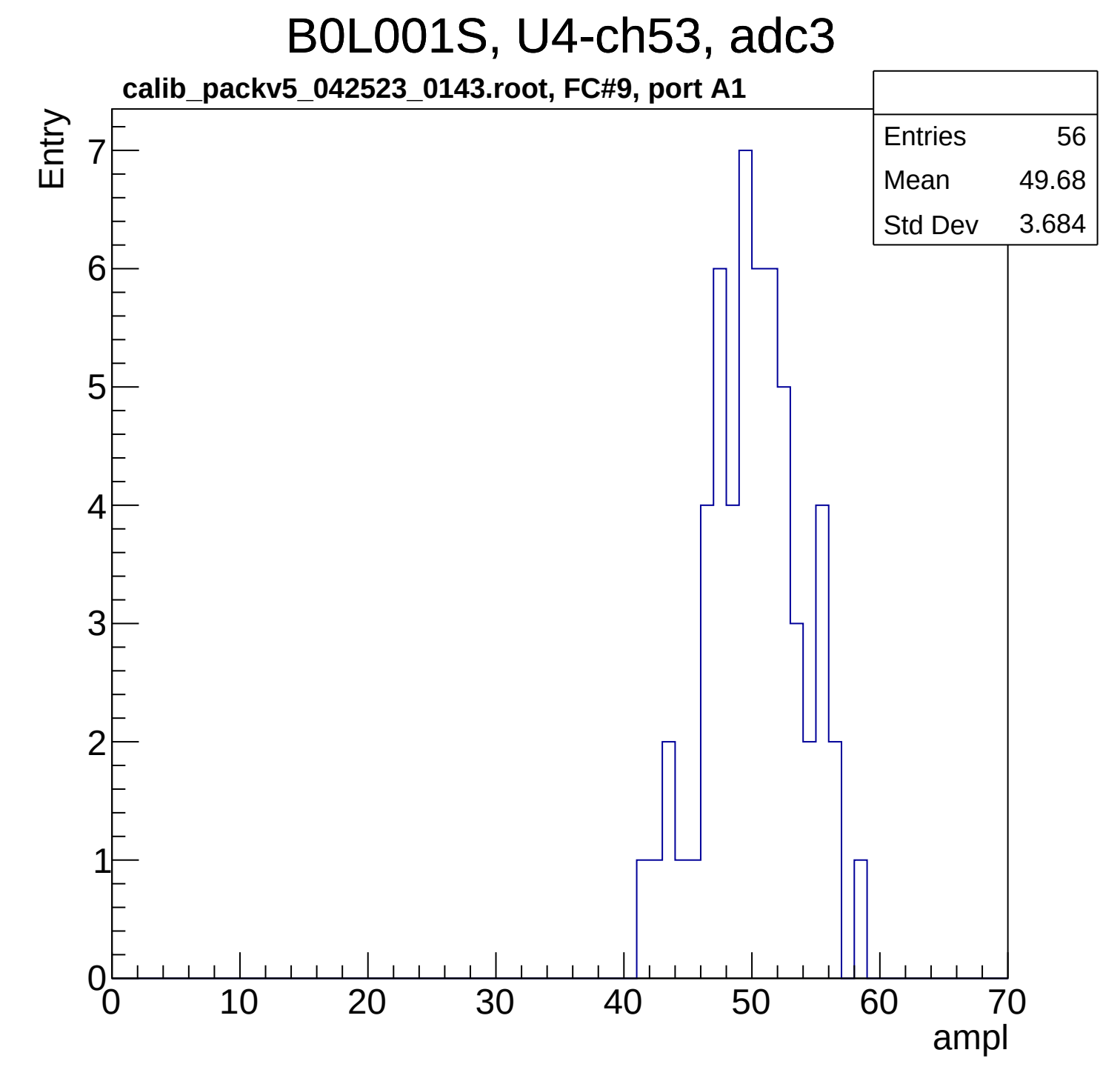

B0L001S, U4-ch52, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.2
Std Dev 24.64
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch52, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch53, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.51
Std Dev 3.474
Gaus mean : 31.1492
Gaus Width: 4.2293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch53, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.17
Std Dev 3.59
Gaus mean : 37.7680
Gaus Width: 3.9591
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch53, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.03
Std Dev 3.551
Gaus mean : 44.7155
Gaus Width: 3.8879
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch53, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.68
Std Dev 3.684
Entry
ampl
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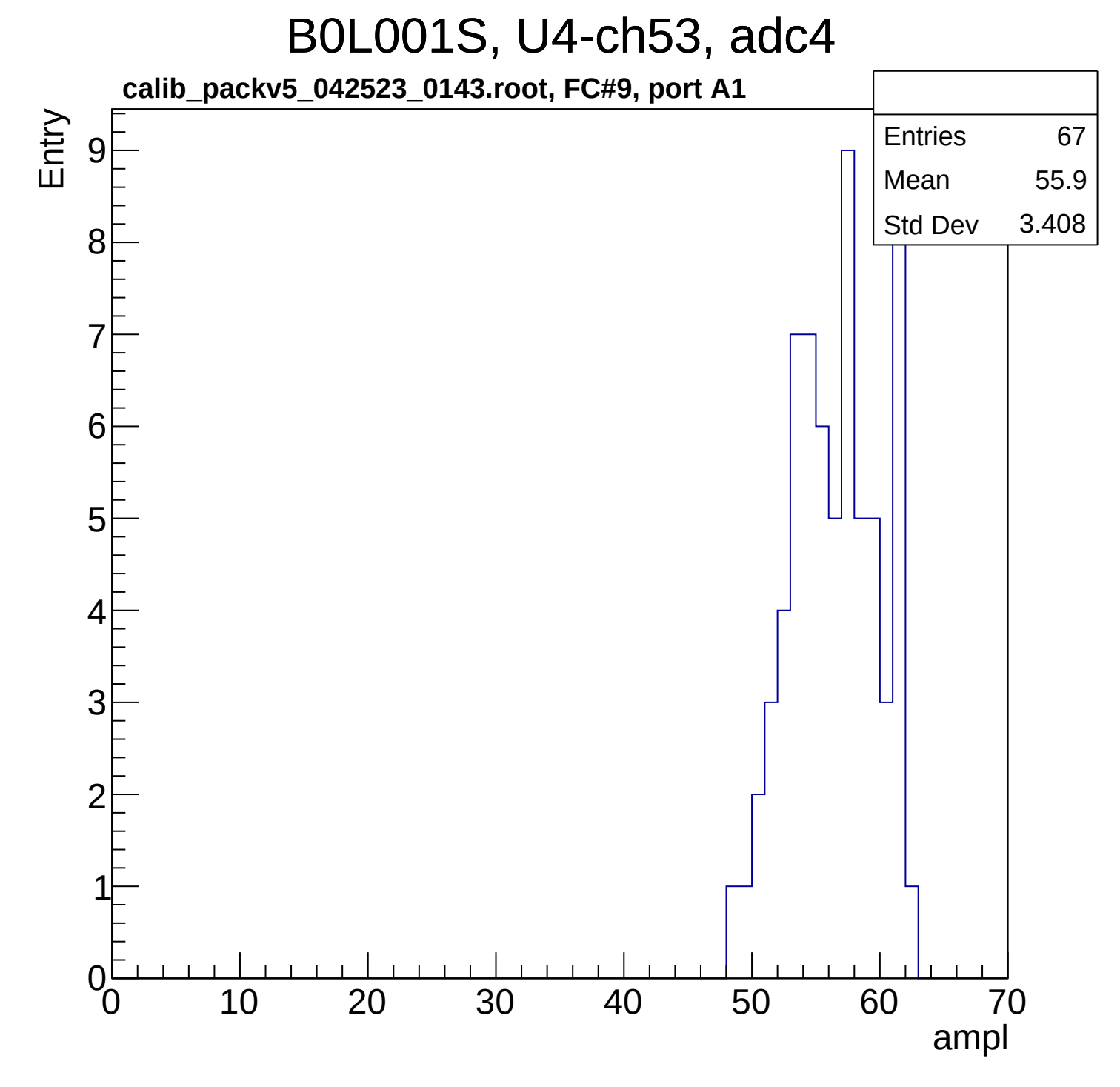
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch53, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 55.9
Std Dev 3.408
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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50
60
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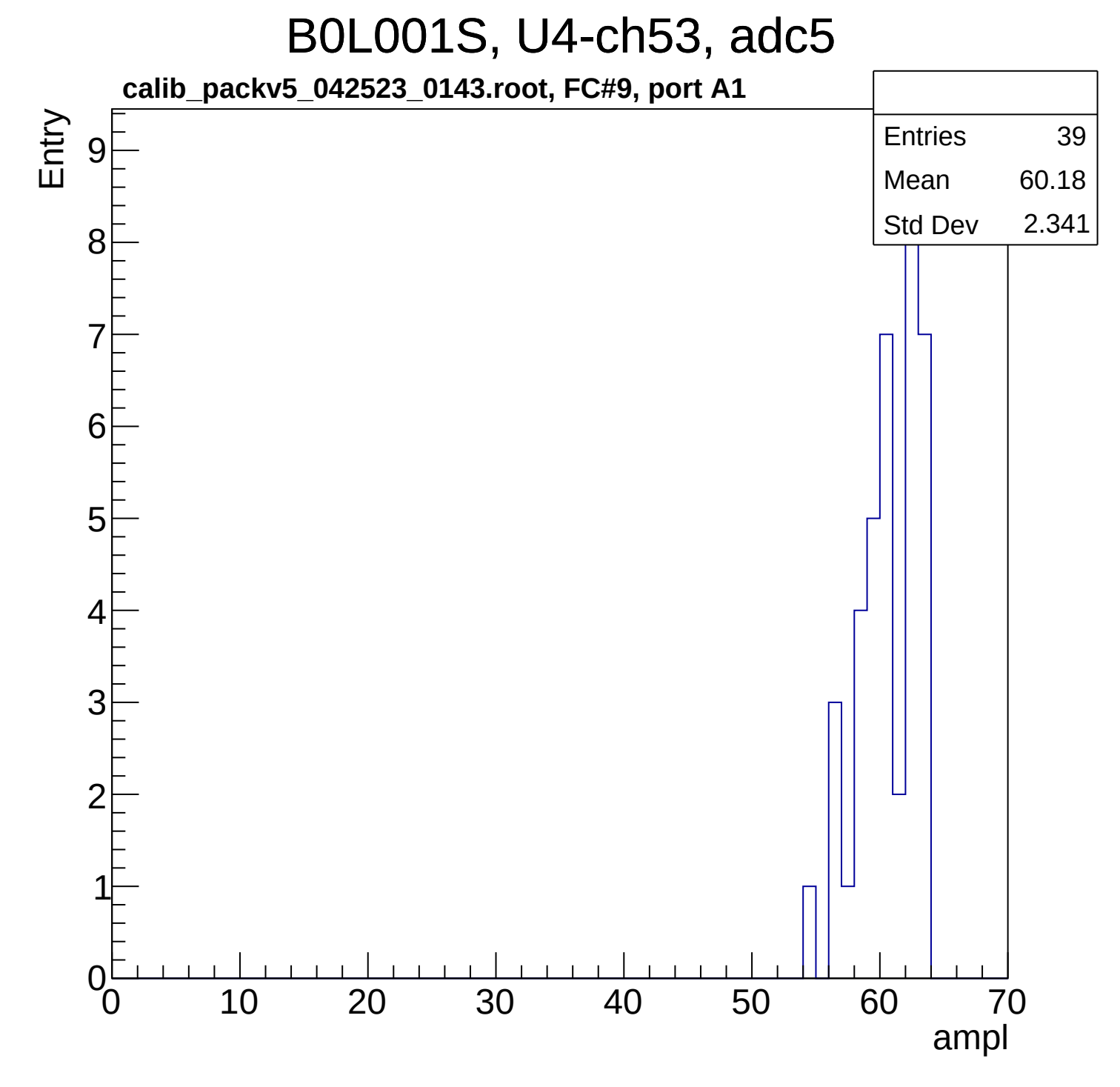
B0L001S, U4-ch53, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 60.18
Std Dev 2.341
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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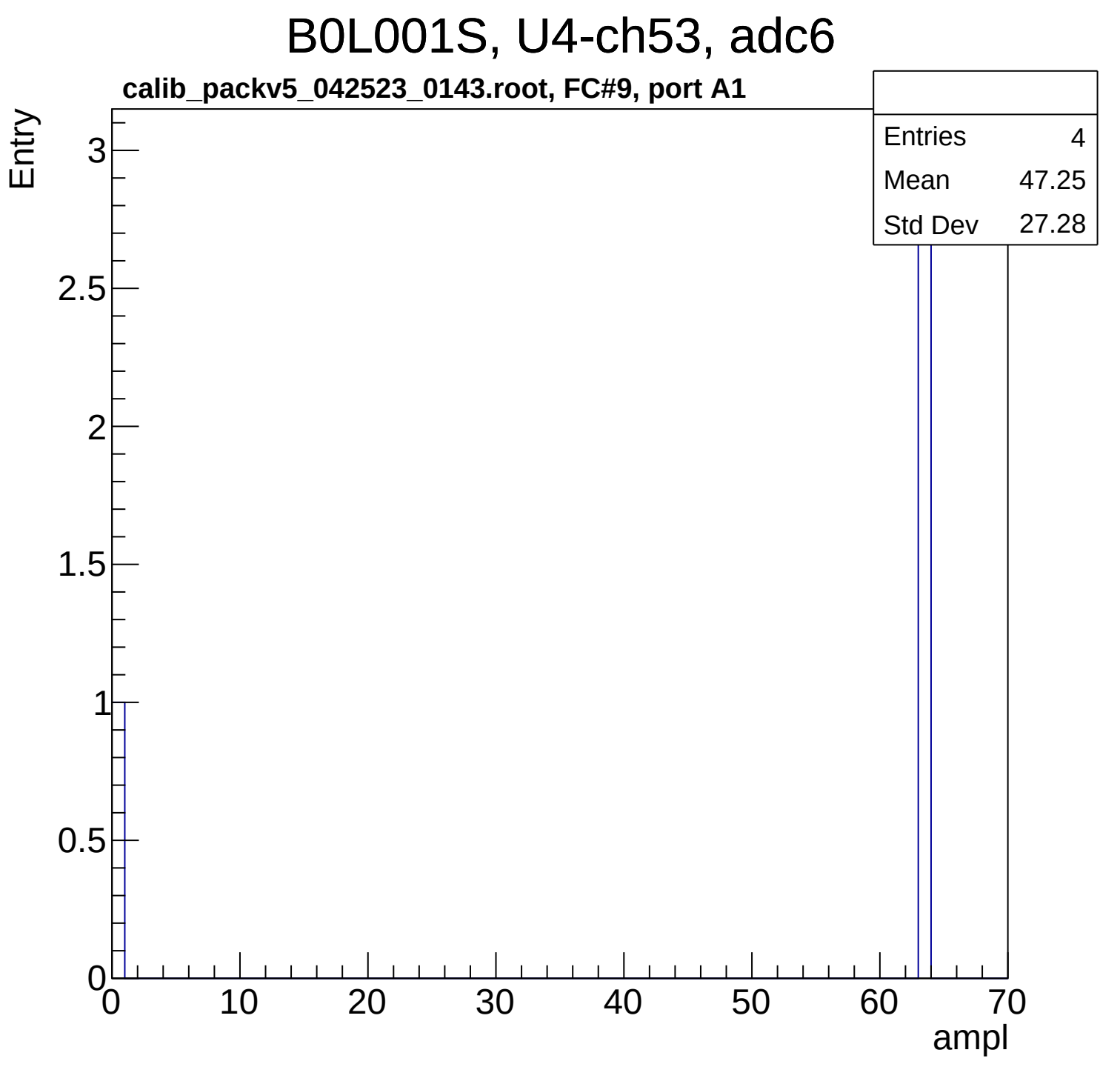
40
50
60
70


B0L001S, U4-ch53, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
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60
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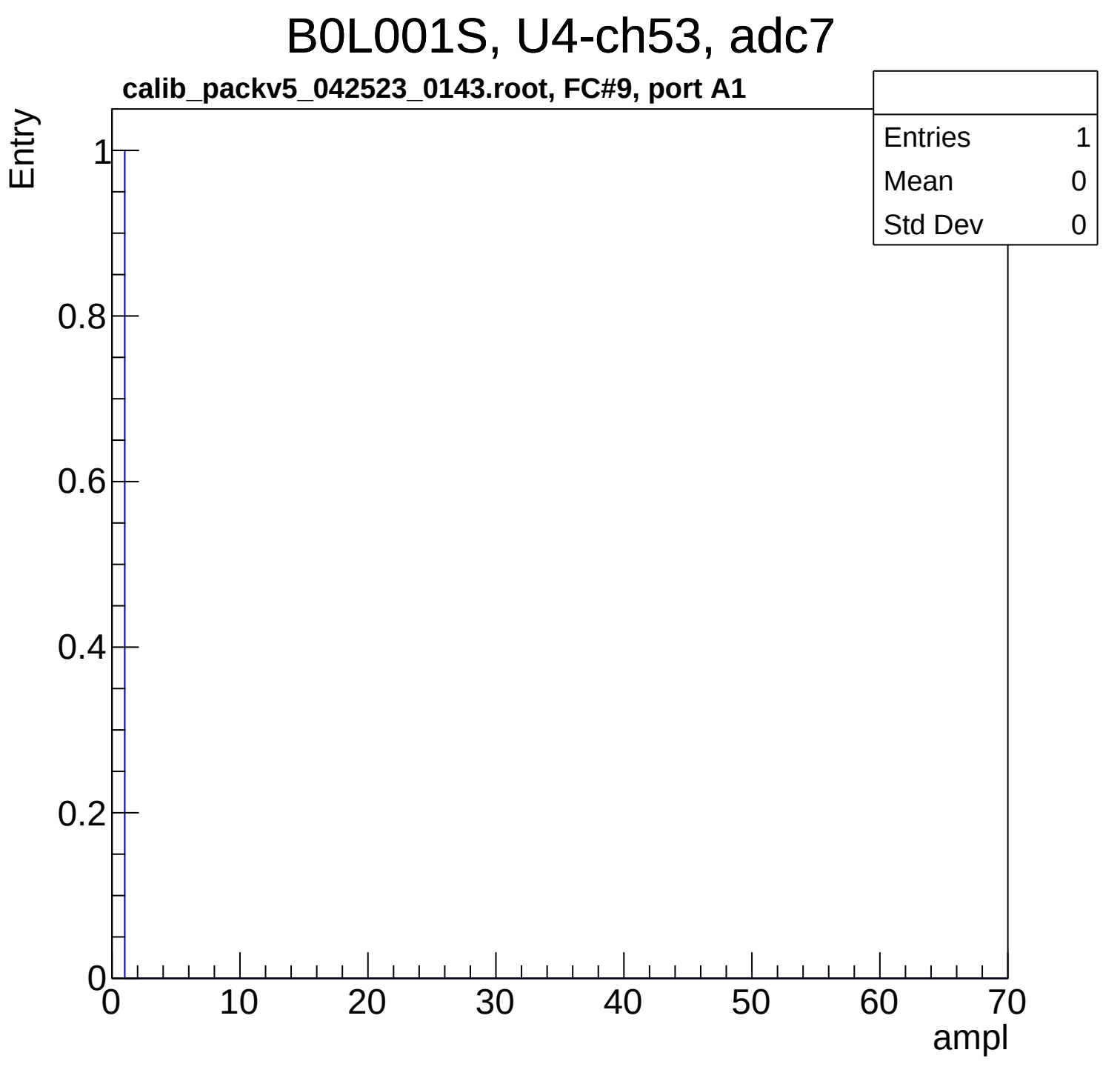
B0L001S, U4-ch53, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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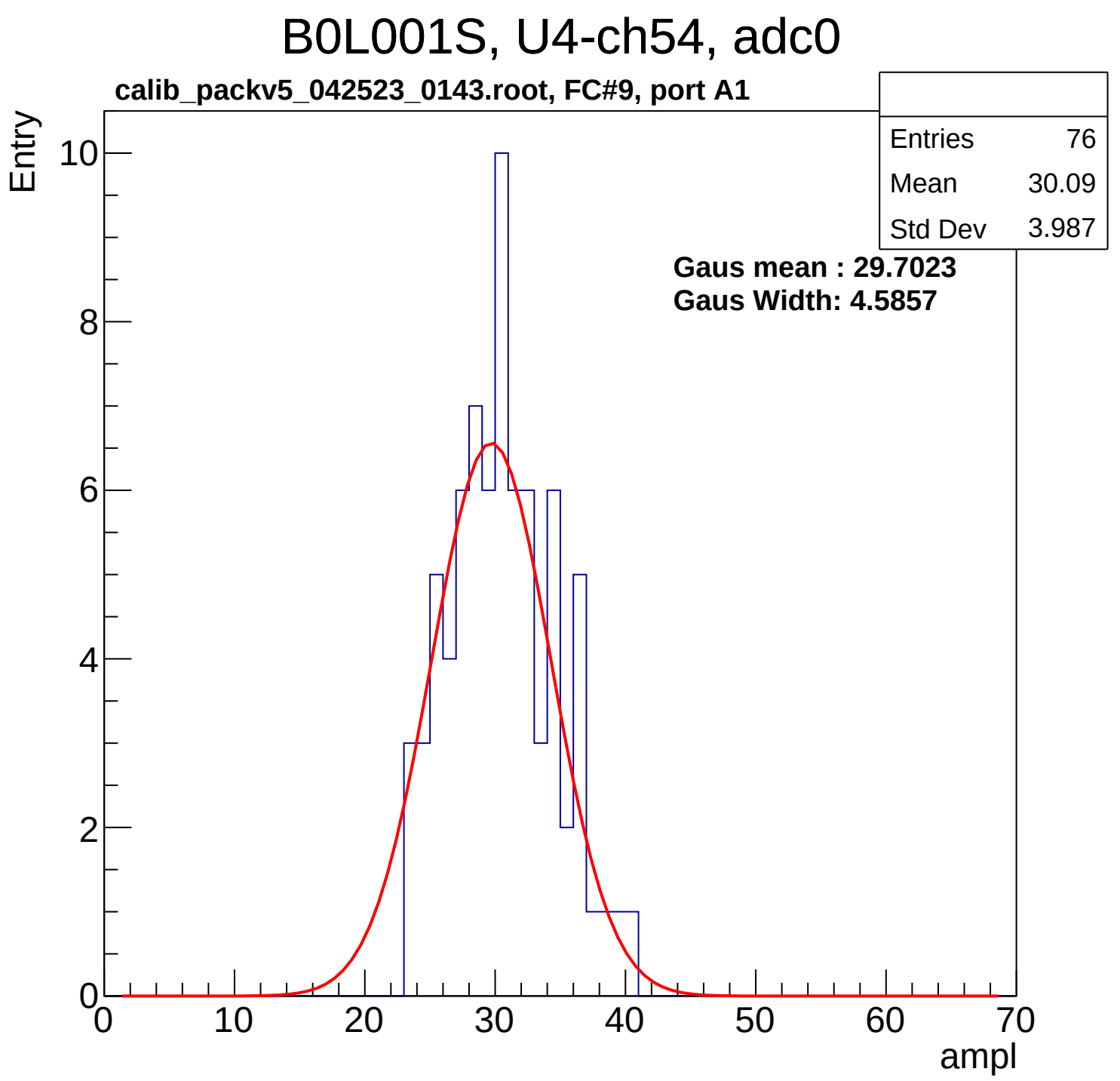
70
ampl


B0L001S, U4-ch54, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 30.09
Std Dev 3.987
Gaus mean : 29.7023
Gaus Width: 4.5857
Entry
ampl
0
2
4
6
8
10
0
10
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30
40
50
60
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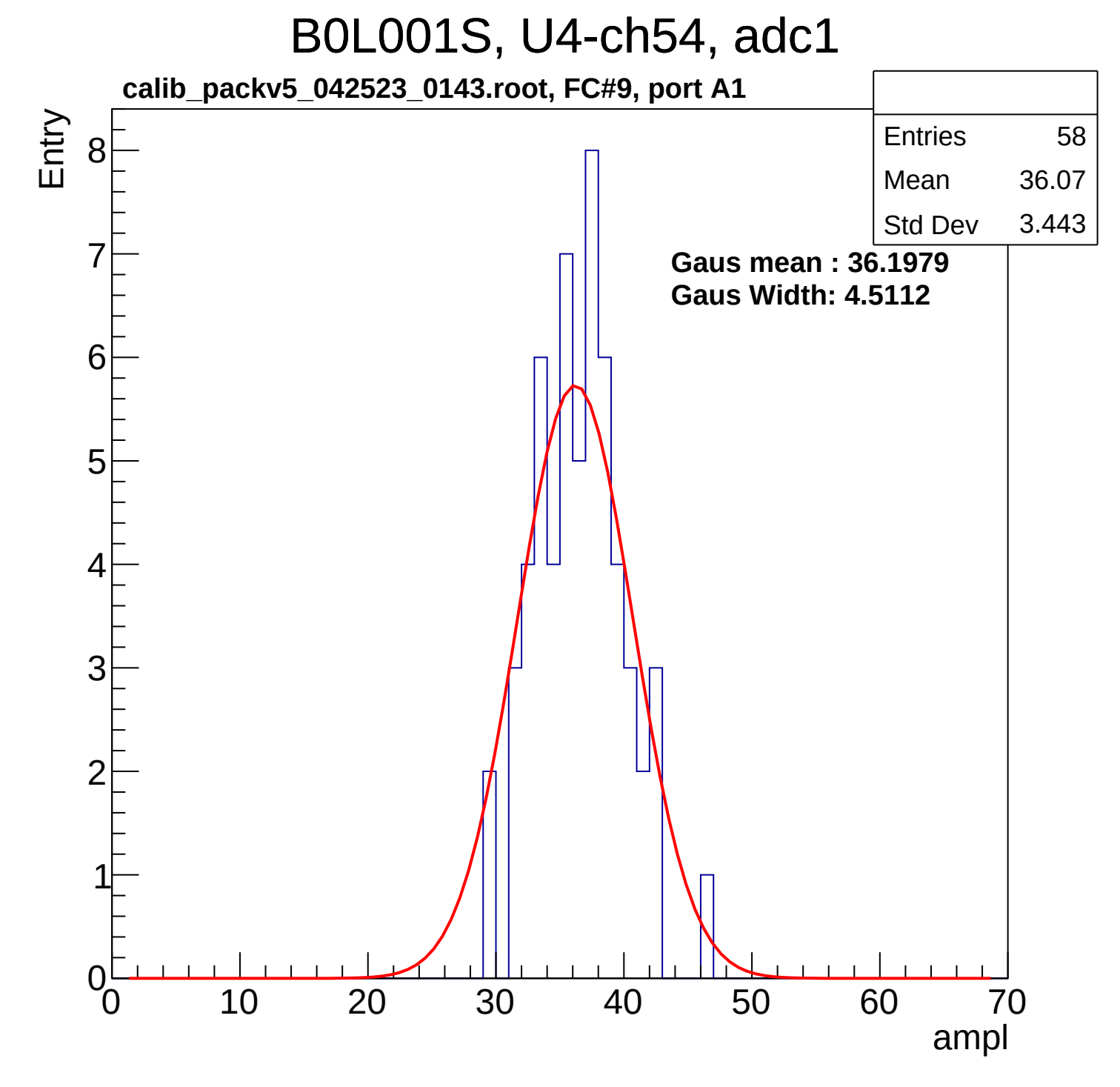
B0L001S, U4-ch54, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 36.07
Std Dev 3.443
Gaus mean : 36.1979
Gaus Width: 4.5112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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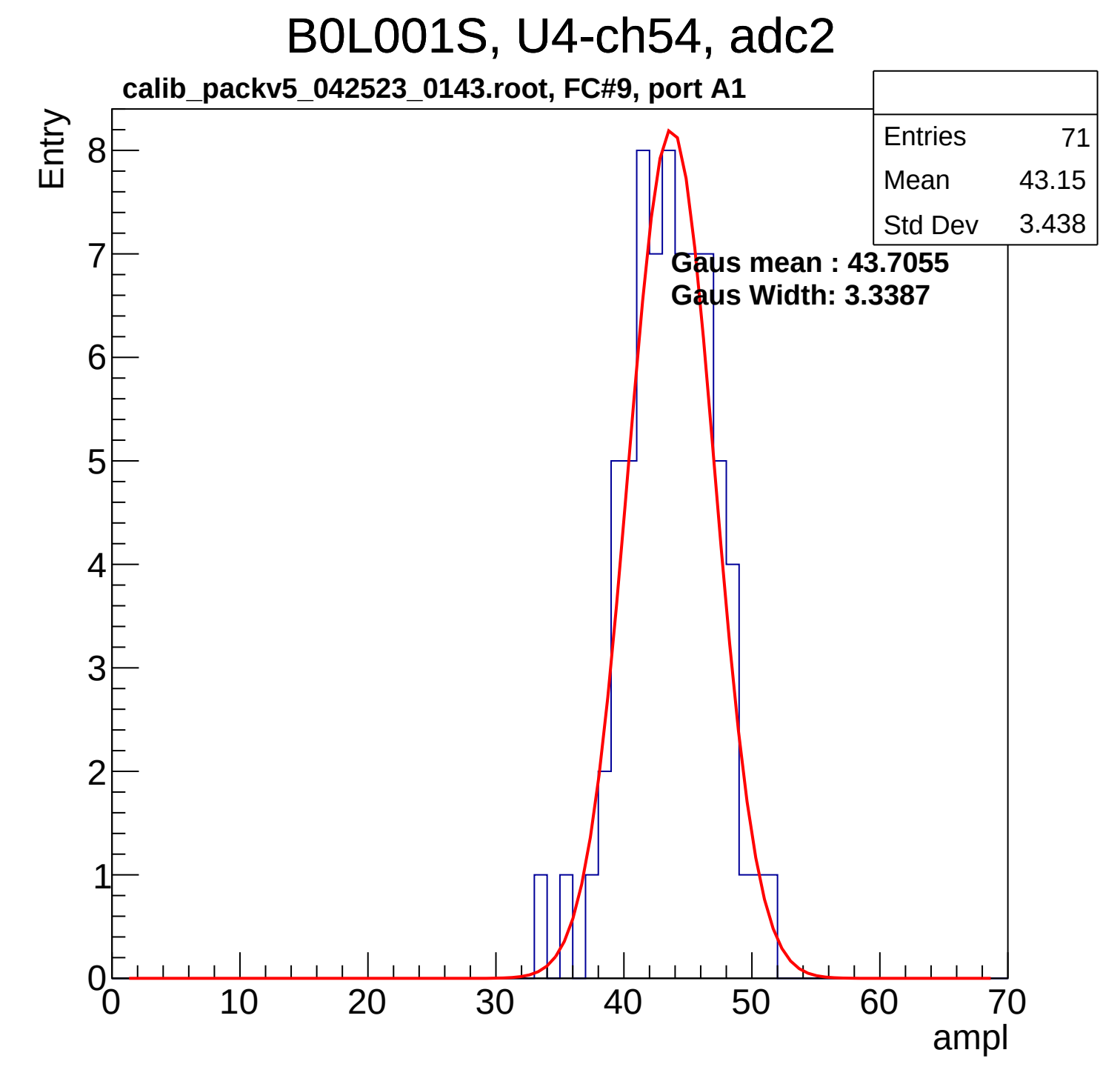
B0L001S, U4-ch54, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.15
Std Dev 3.438
Gaus mean : 43.7055
Gaus Width: 3.3387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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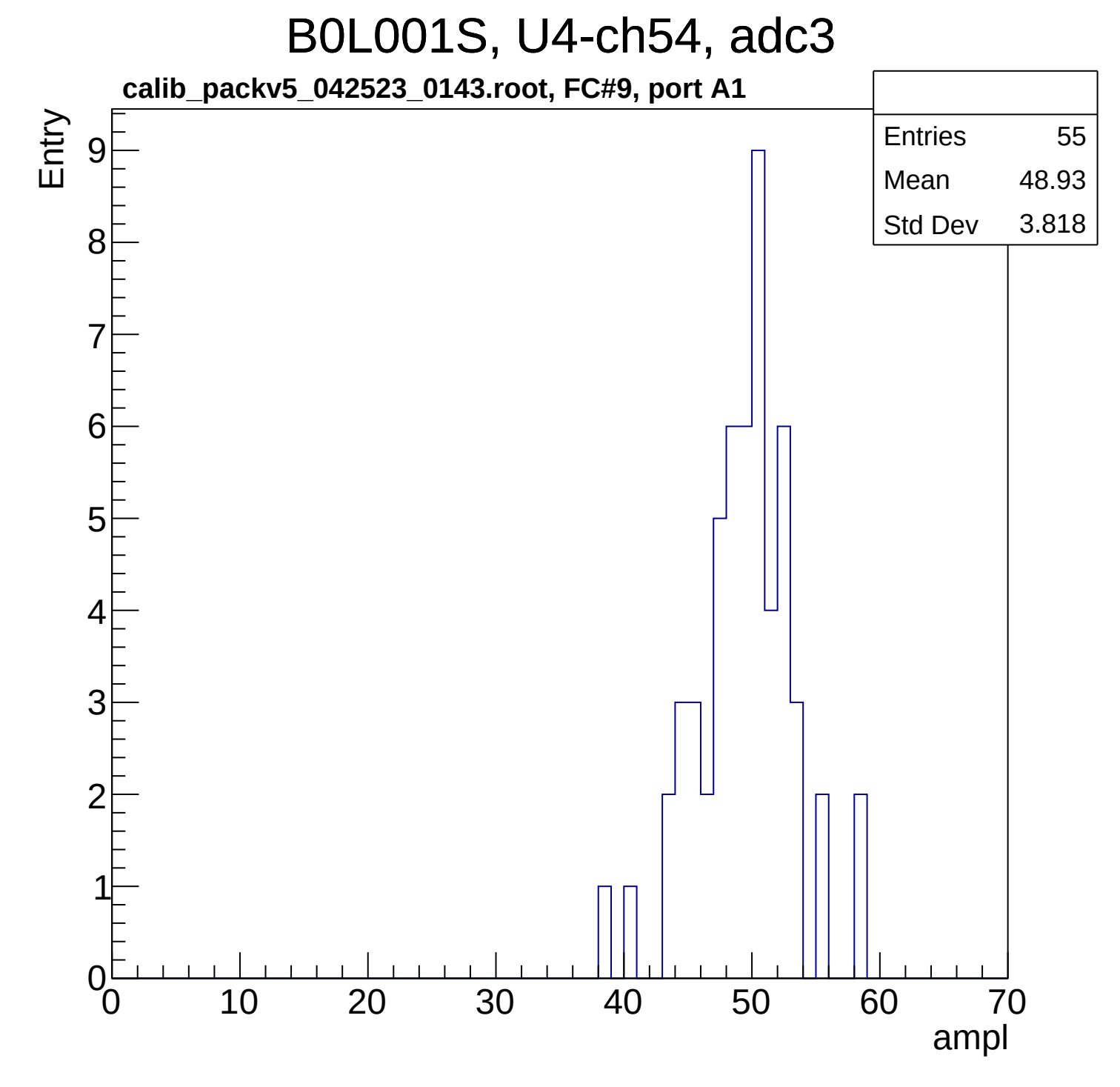
B0L001S, U4-ch54, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 48.93
Std Dev 3.818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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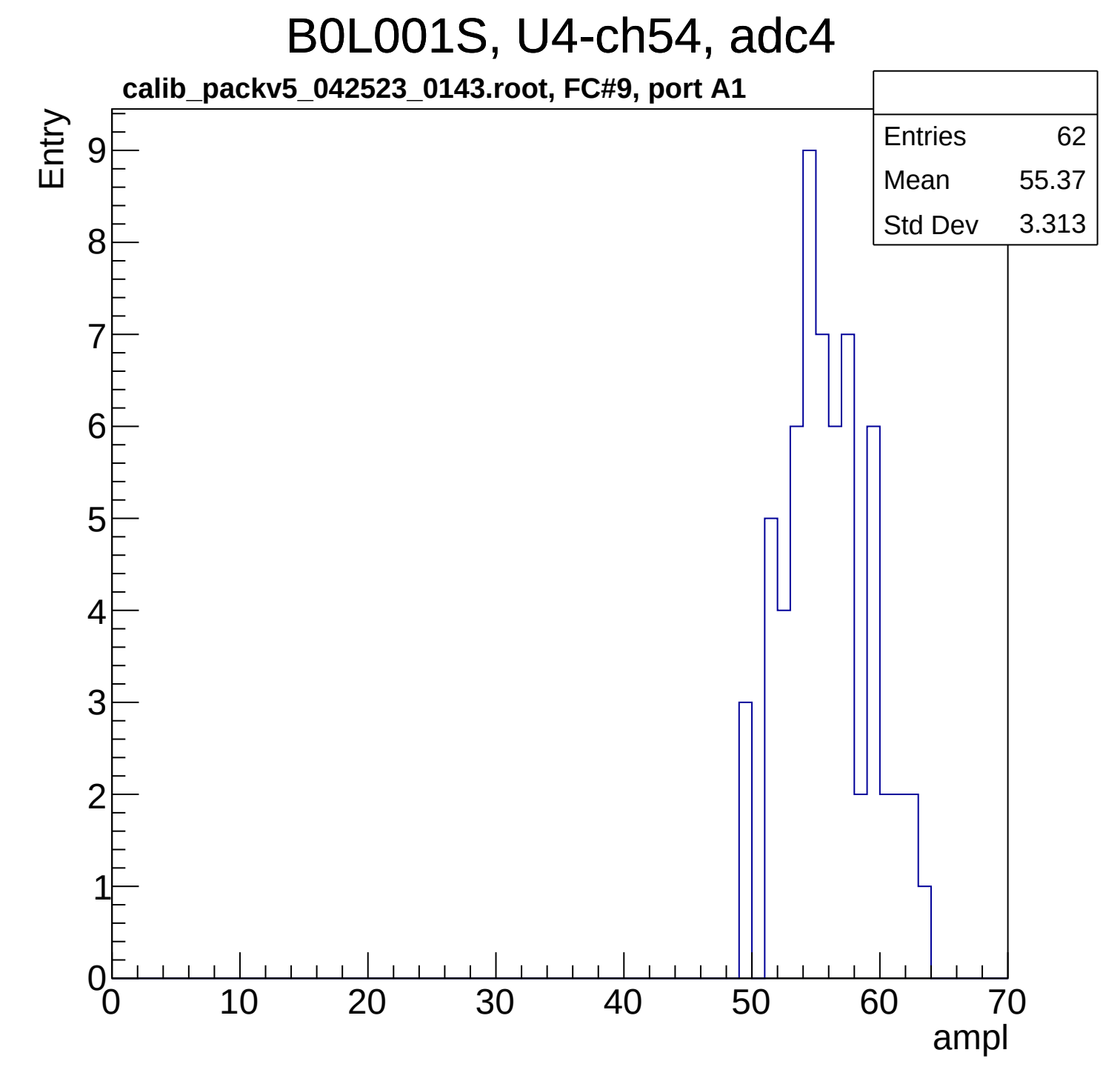
B0L001S, U4-ch54, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.37
Std Dev 3.313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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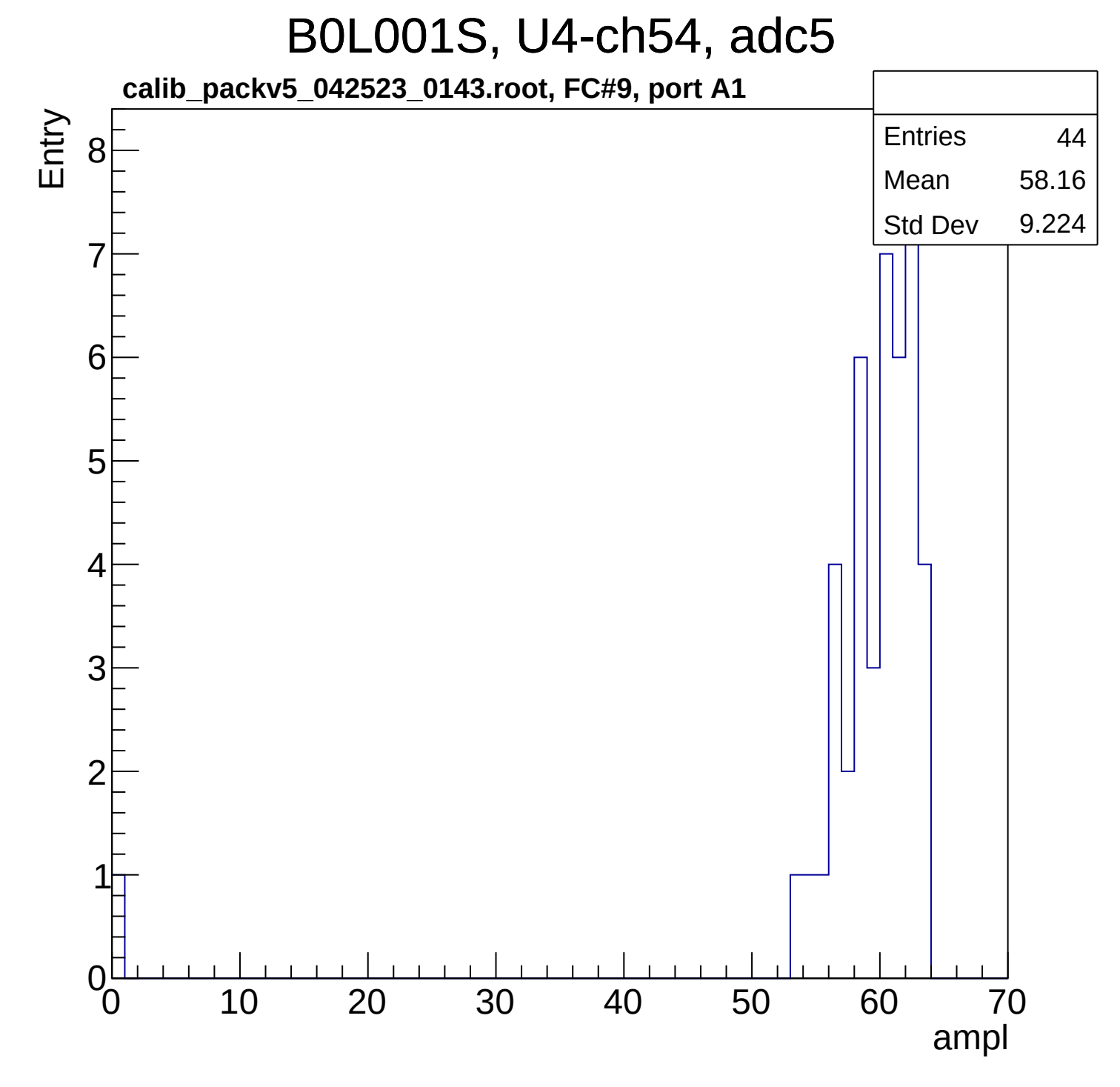
B0L001S, U4-ch54, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.16
Std Dev 9.224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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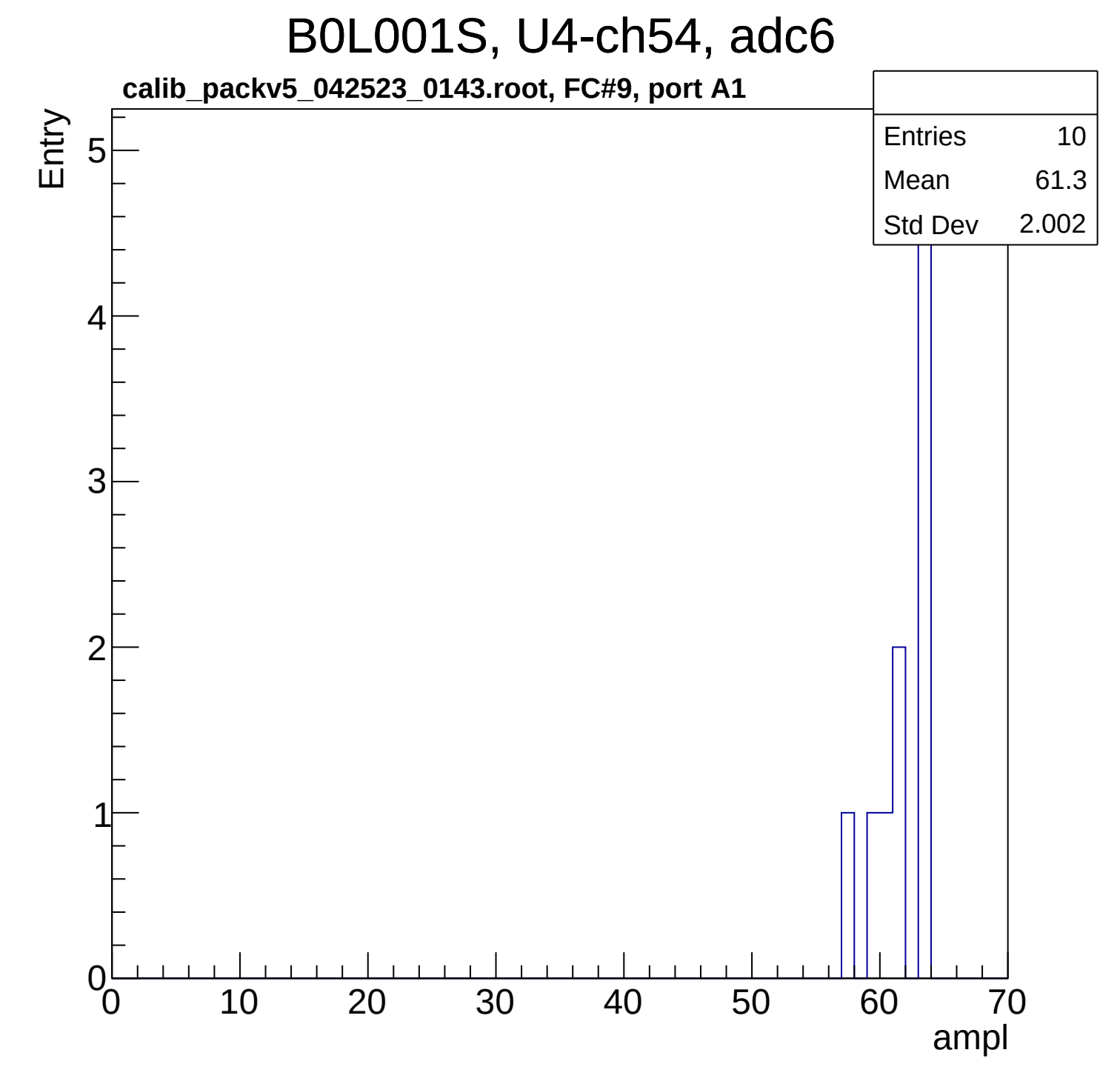
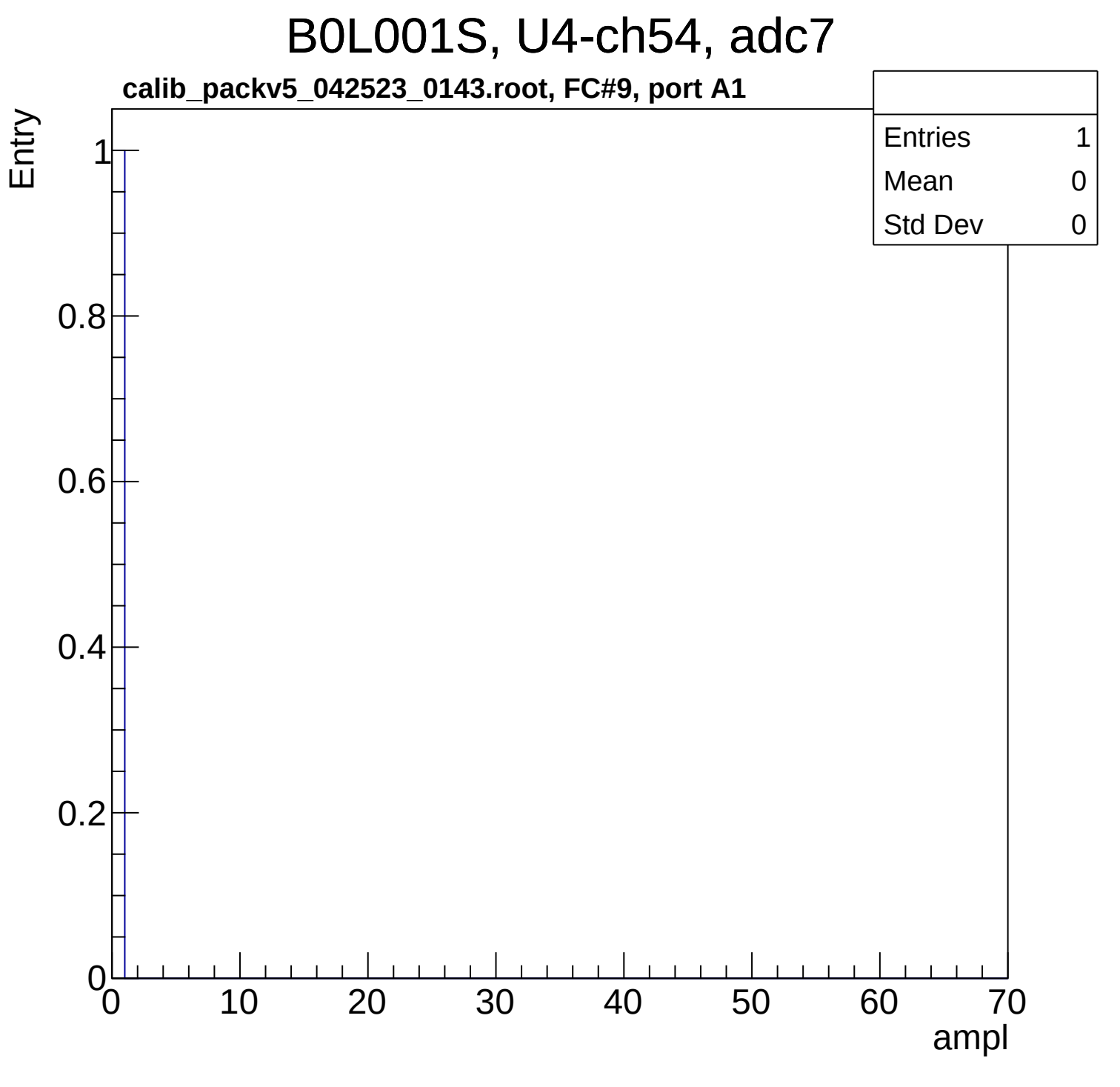
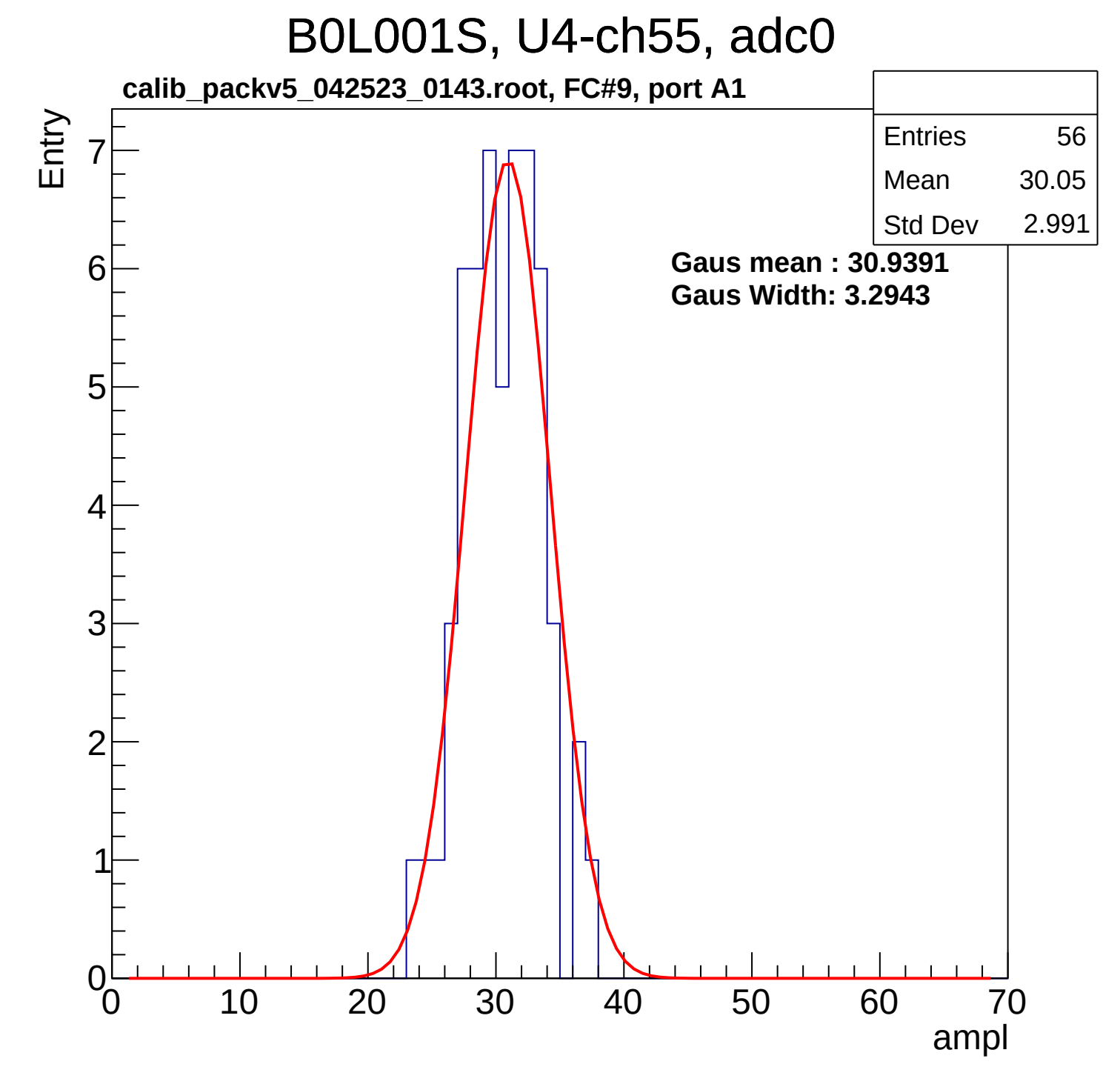
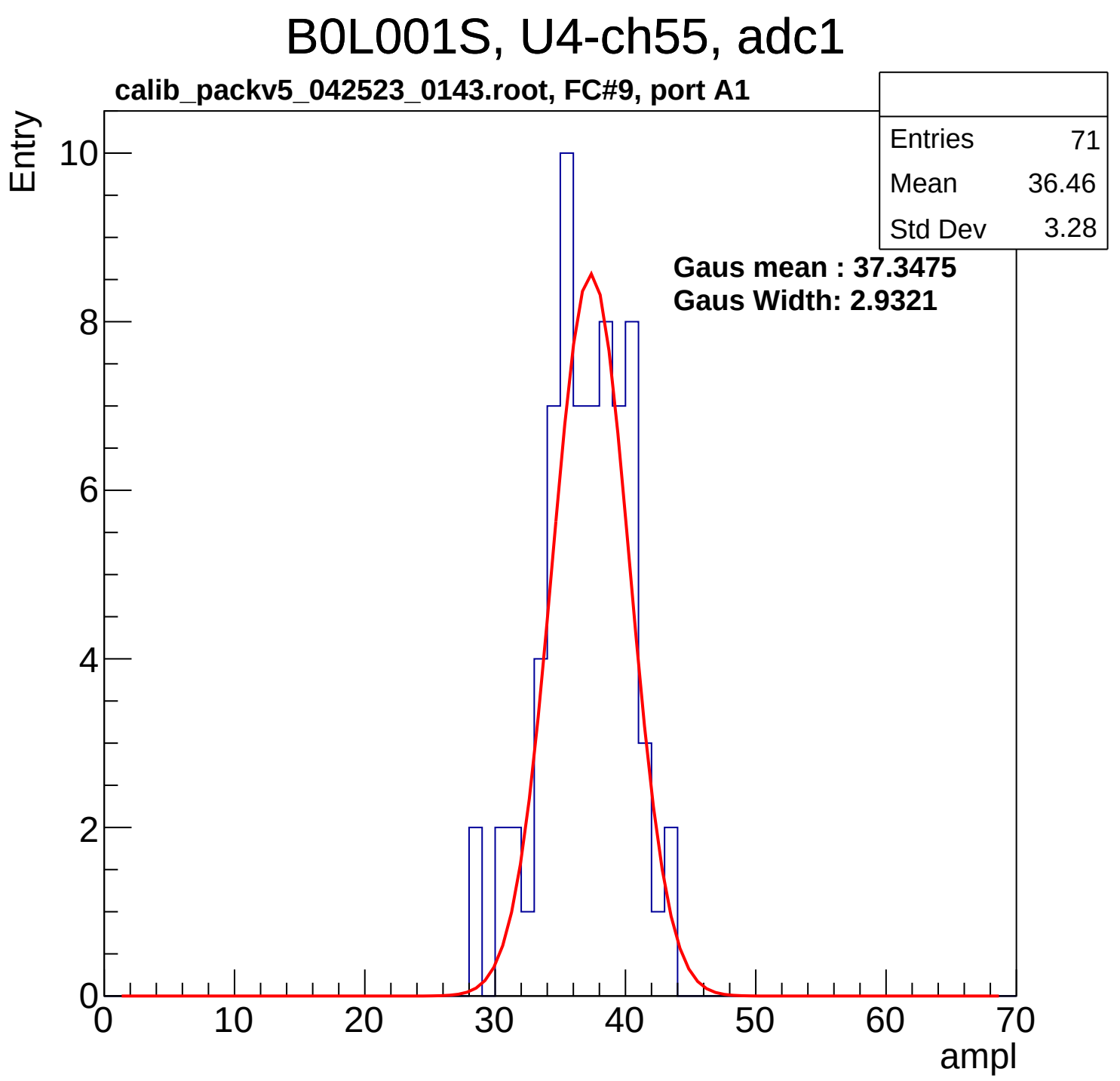
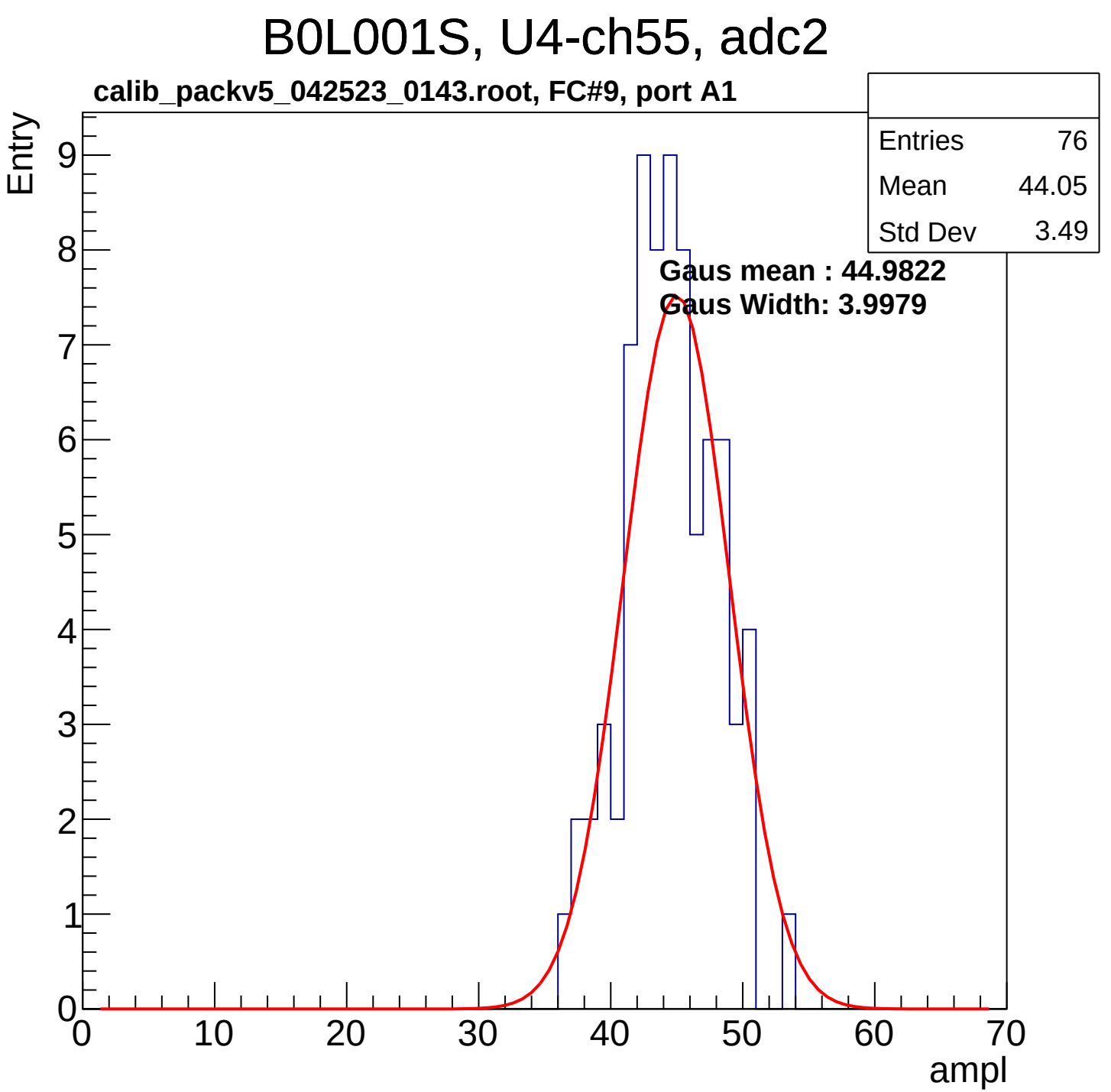
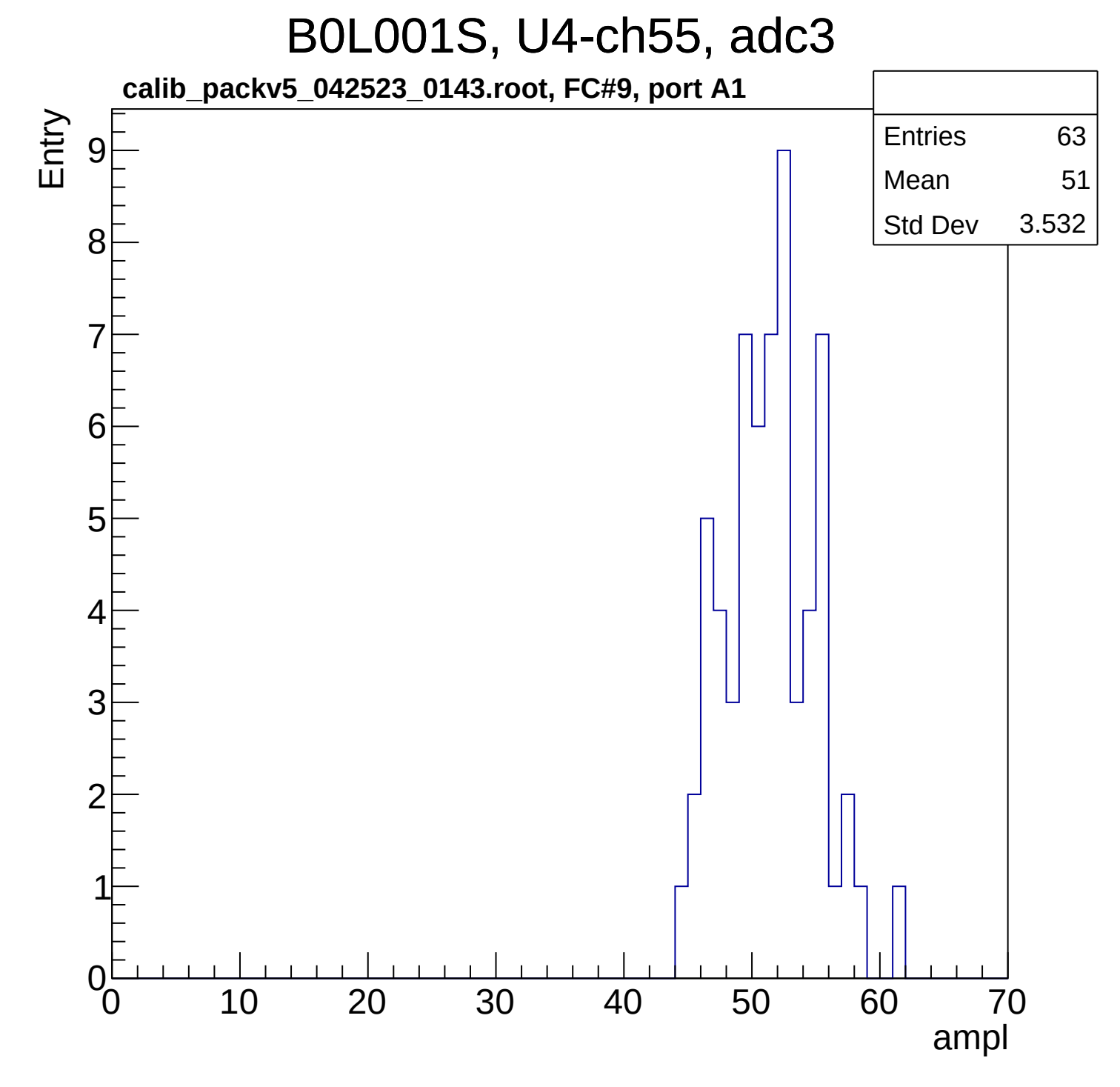

B0L001S, U4-ch54, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61.3
Std Dev 2.002
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

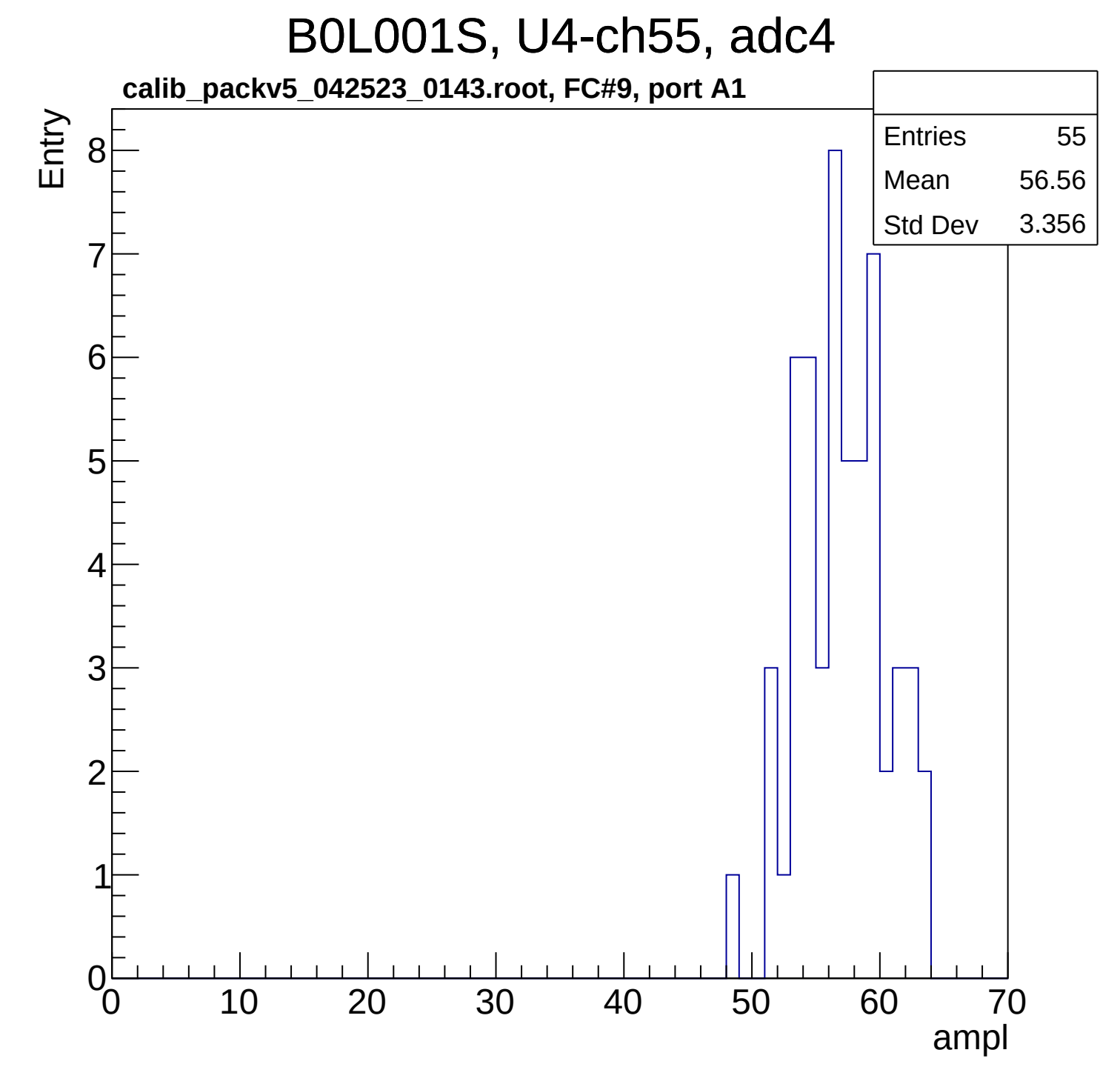

B0L001S, U4-ch54, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

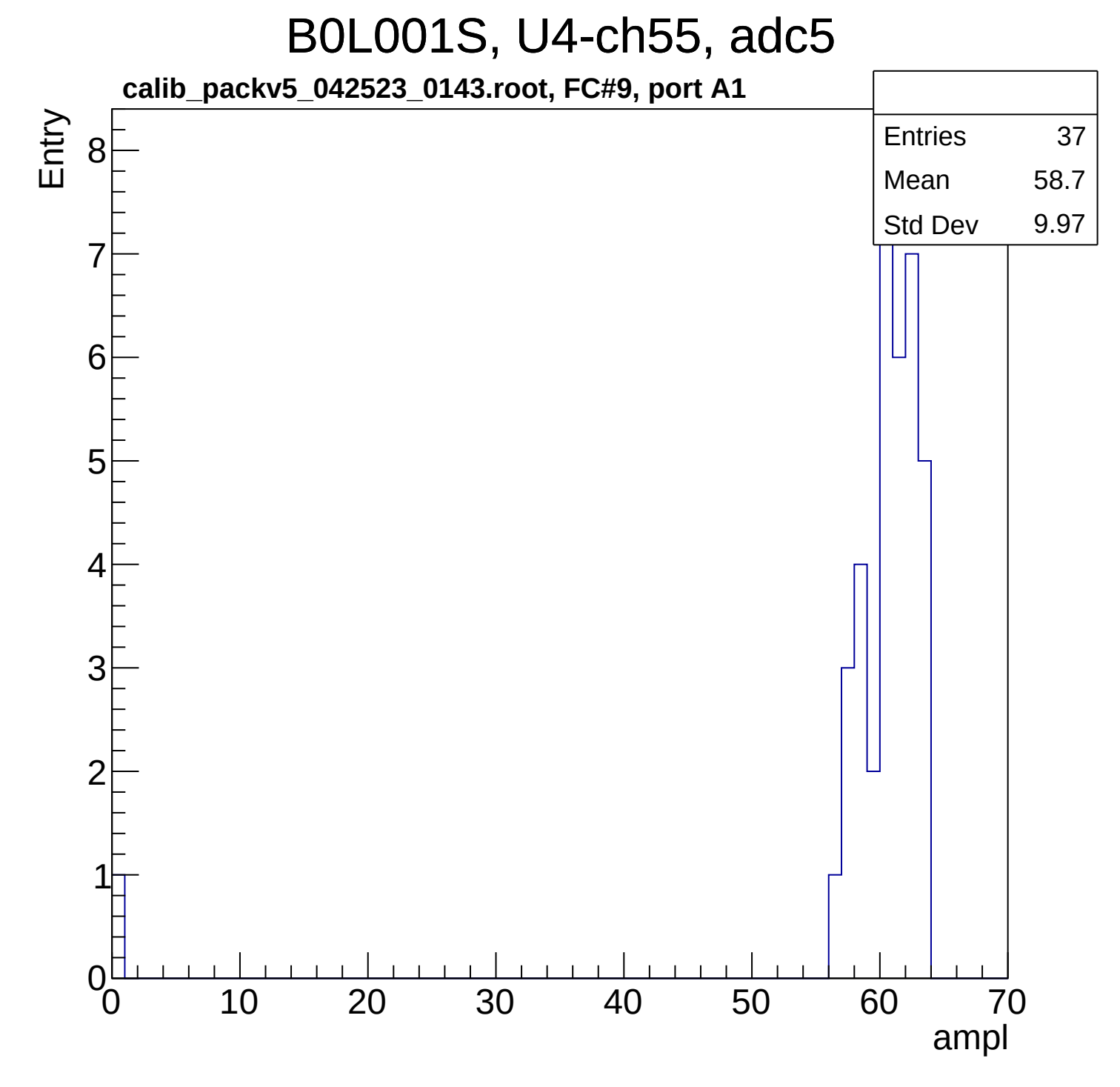

B0L001S, U4-ch55, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.05
Std Dev 2.991
Gaus mean : 30.9391
Gaus Width: 3.2943
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch55, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 36.46
Std Dev 3.28
Gaus mean : 37.3475
Gaus Width: 2.9321
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch55, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.05
Std Dev 3.49
Gaus mean : 44.9822
Gaus Width: 3.9979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch55, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51
Std Dev 3.532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch55, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.56
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch55, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.7
Std Dev 9.97
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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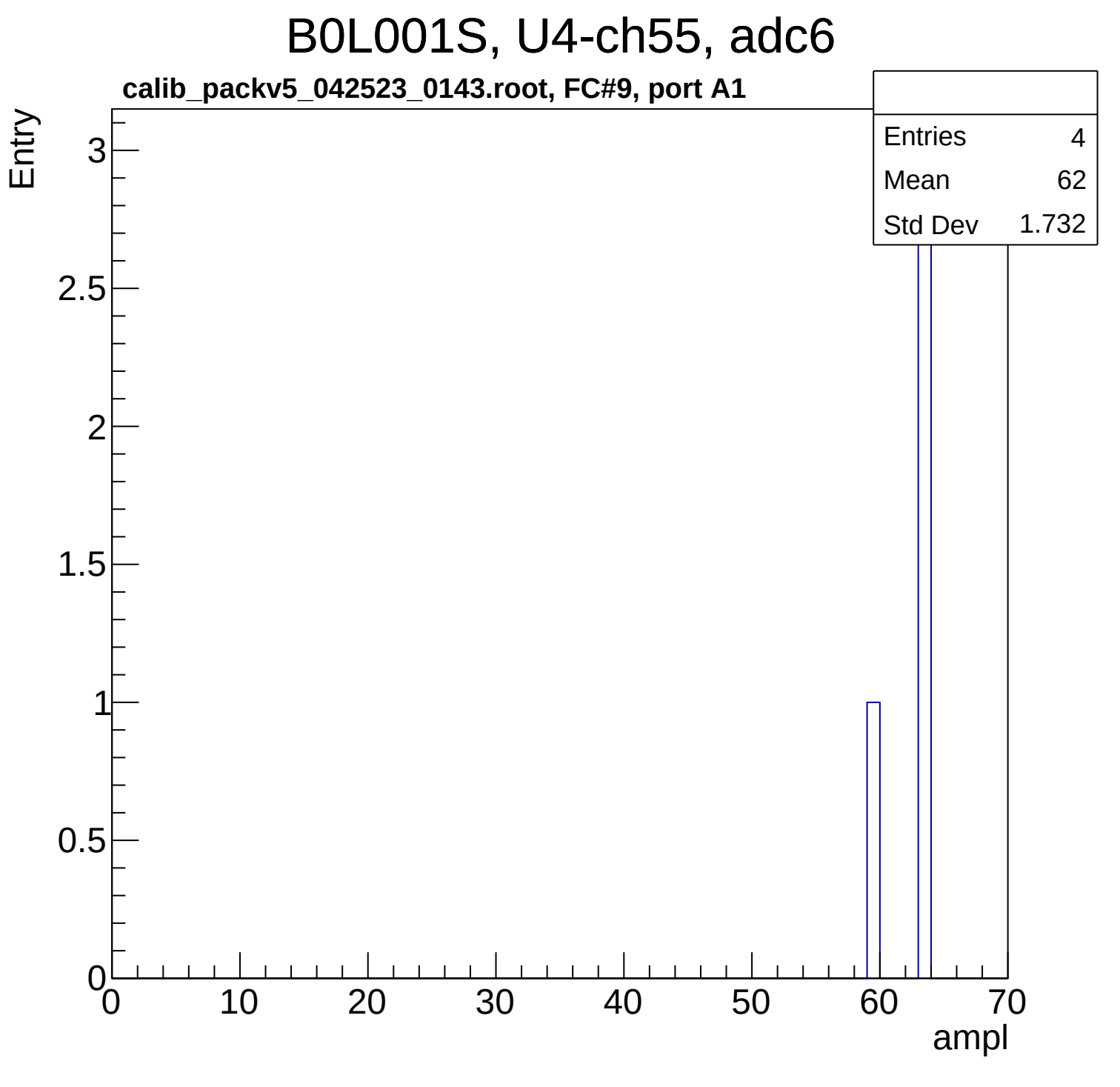
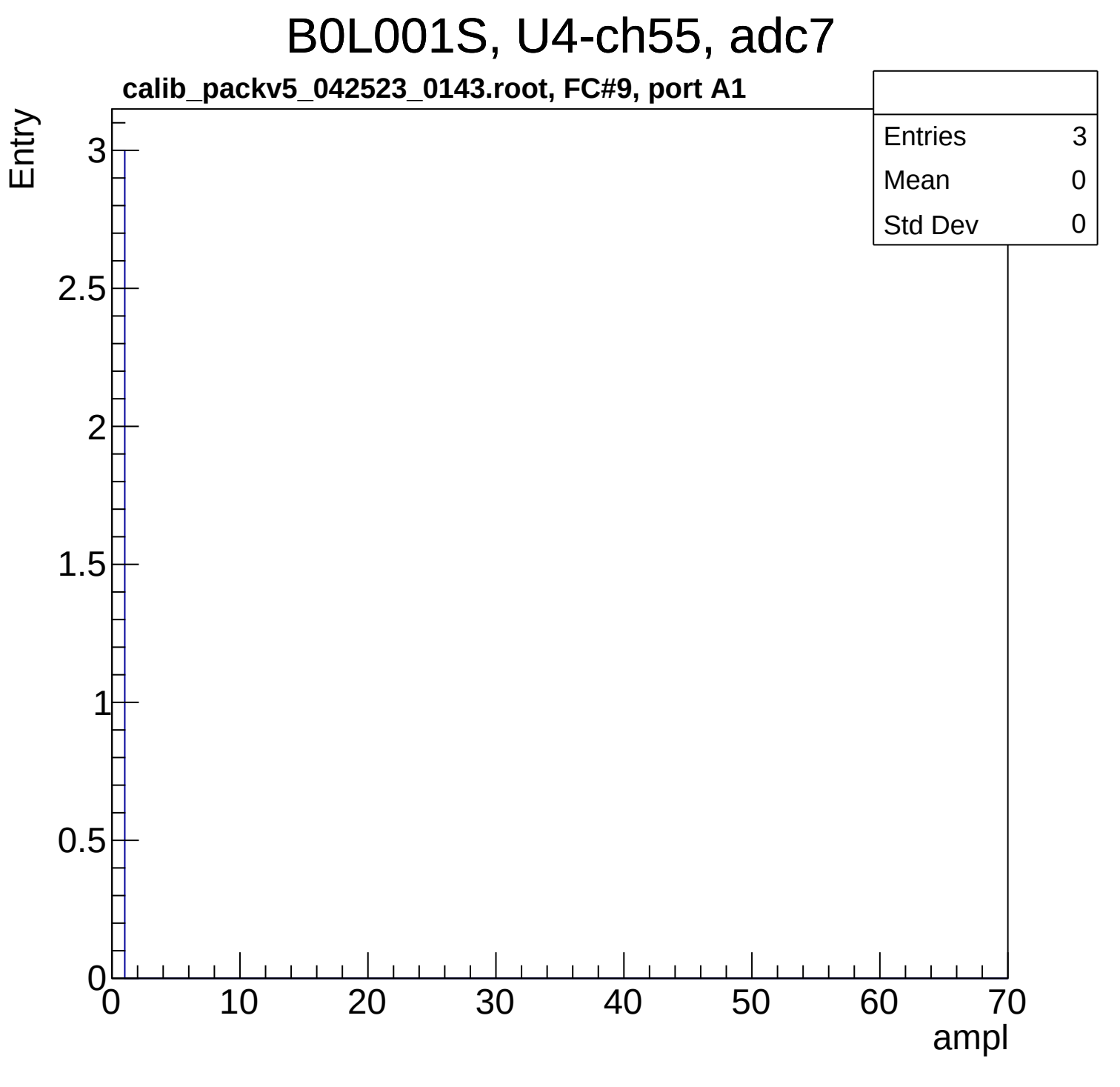
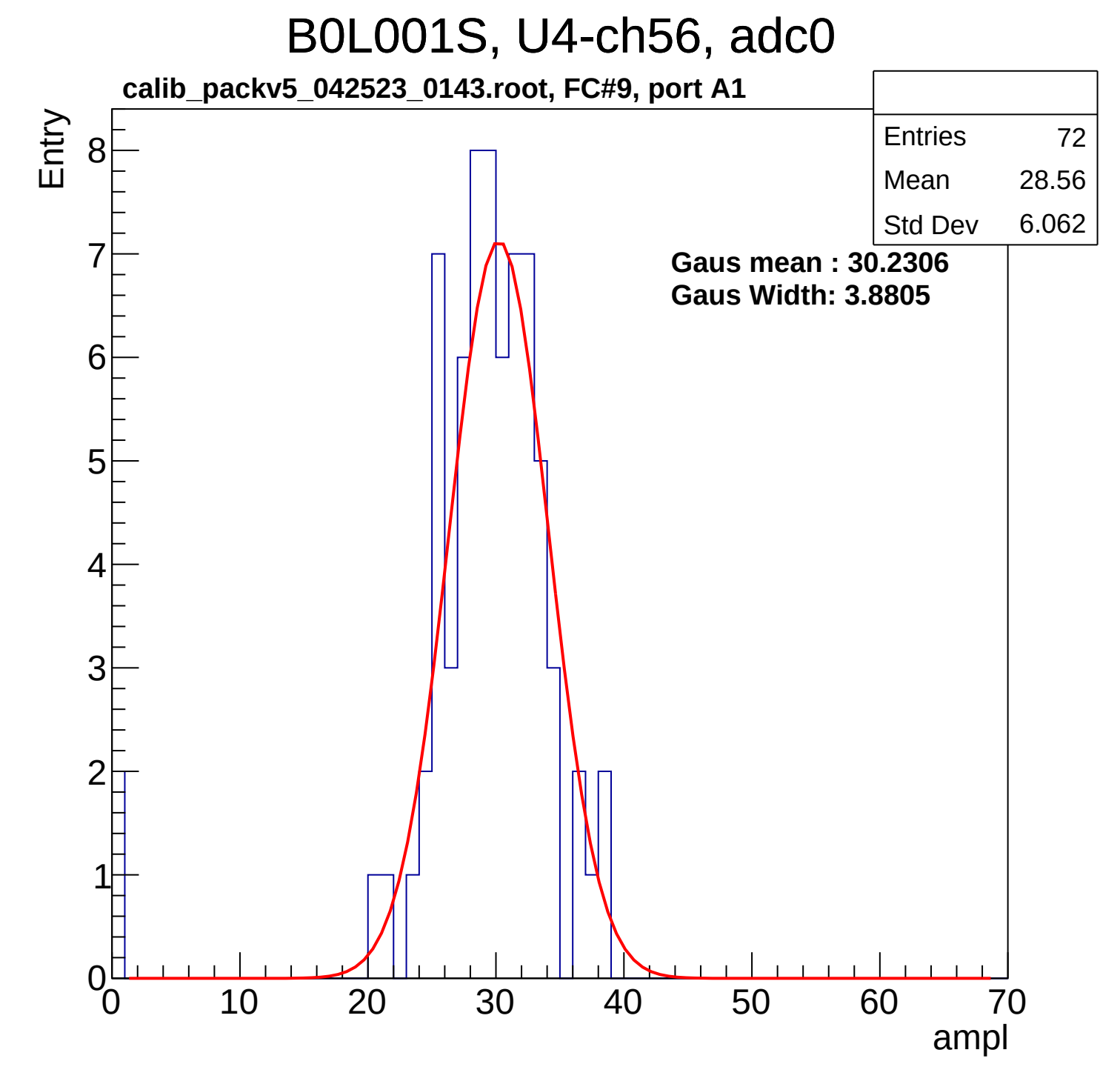
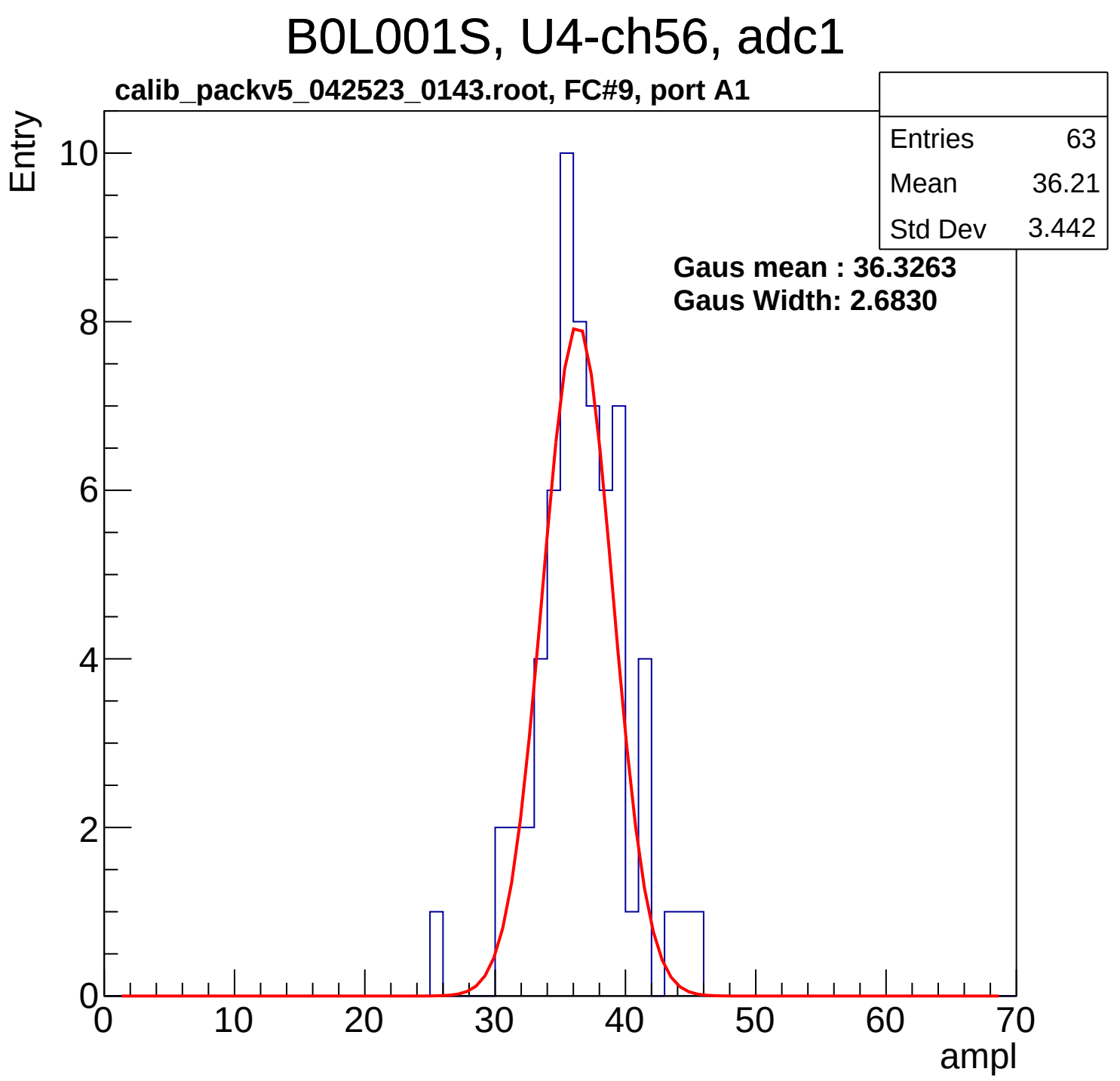
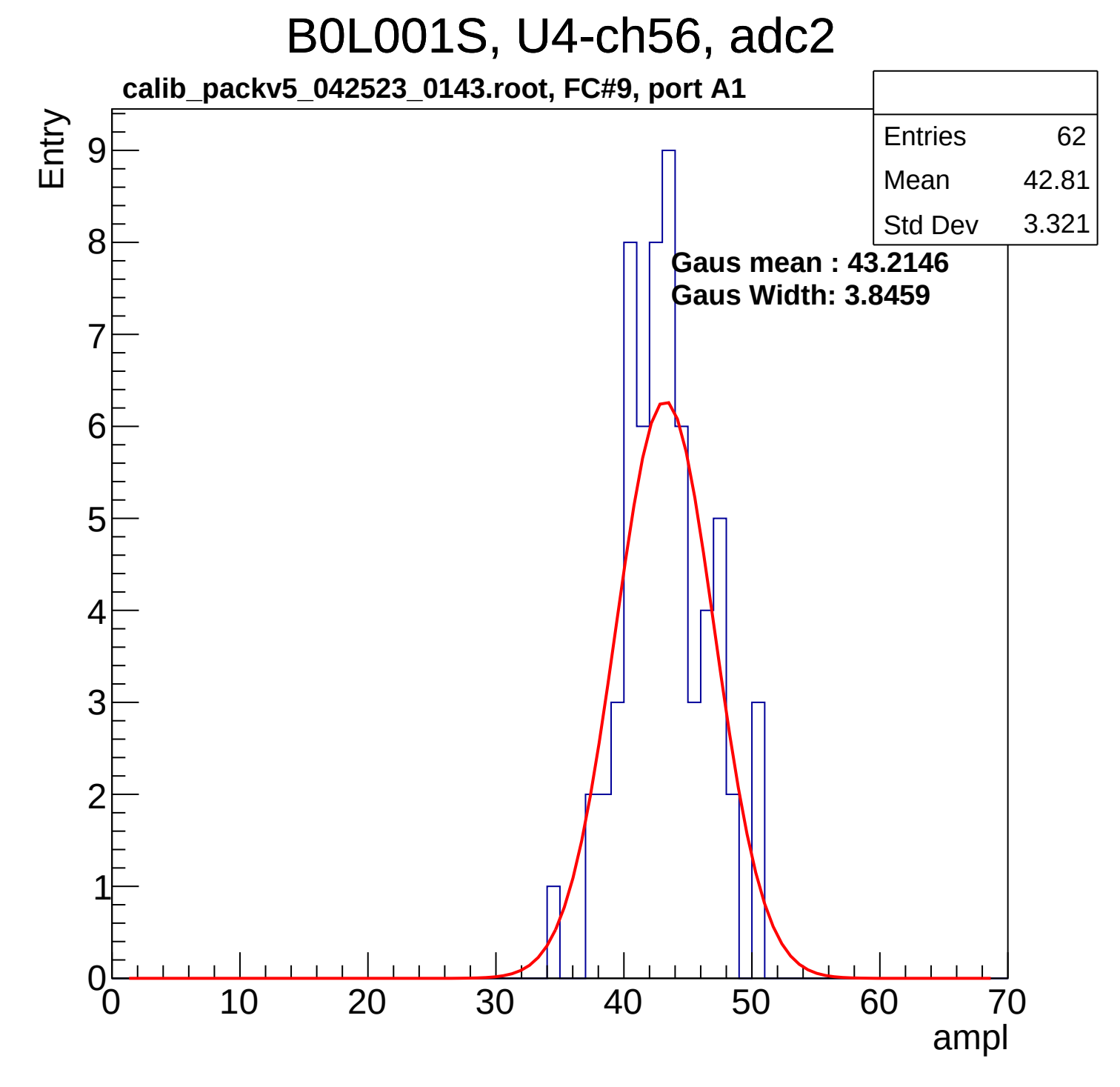

B0L001S, U4-ch55, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1.732
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U4-ch55, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch56, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 28.56
Std Dev 6.062
Gaus mean : 30.2306
Gaus Width: 3.8805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch56, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.21
Std Dev 3.442
Gaus mean : 36.3263
Gaus Width: 2.6830
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch56, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 42.81
Std Dev 3.321
Gaus mean : 43.2146
Gaus Width: 3.8459
Entry
ampl
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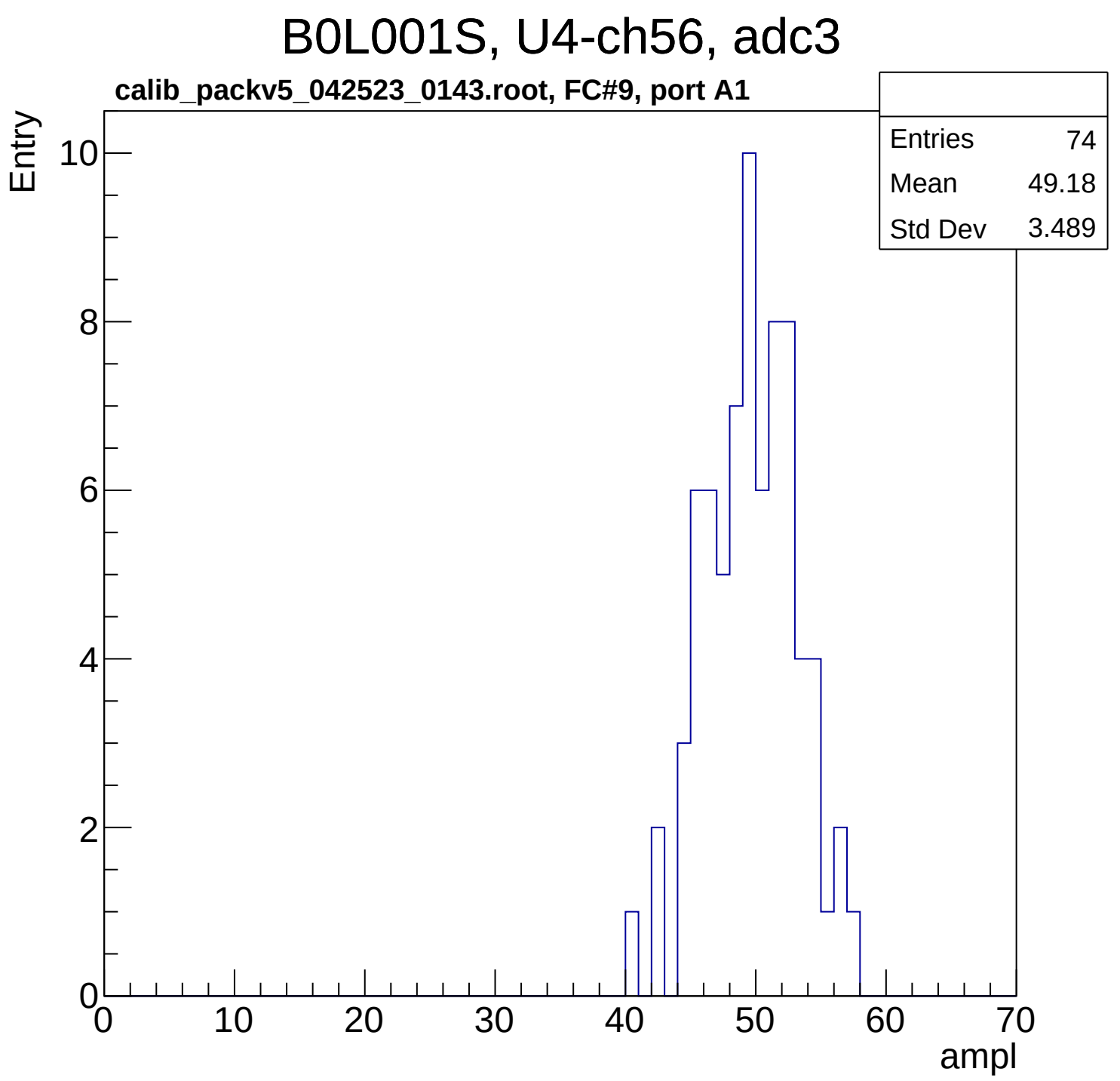
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch56, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 49.18
Std Dev 3.489
Entry
ampl
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2
4
6
8
10
0
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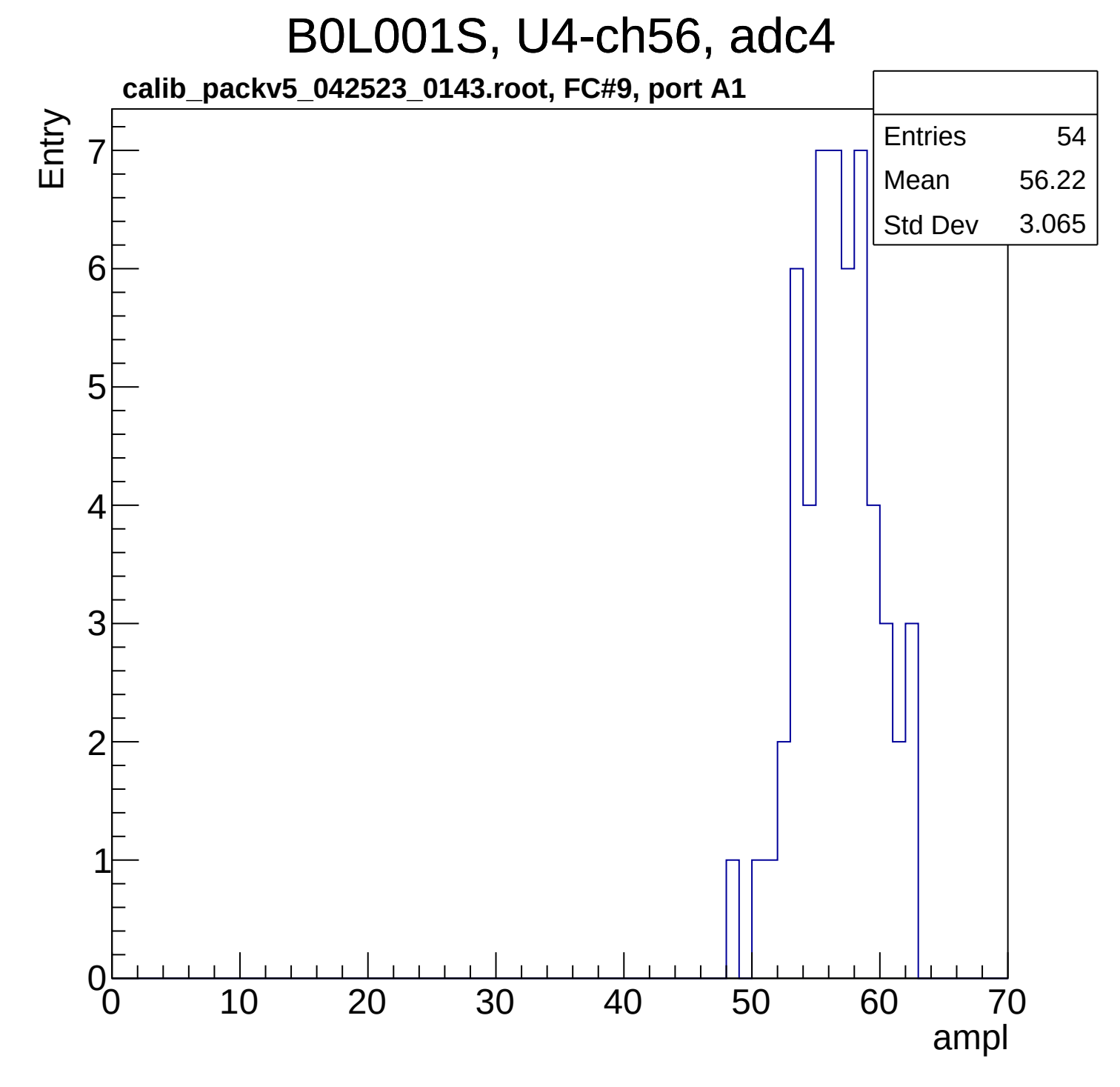
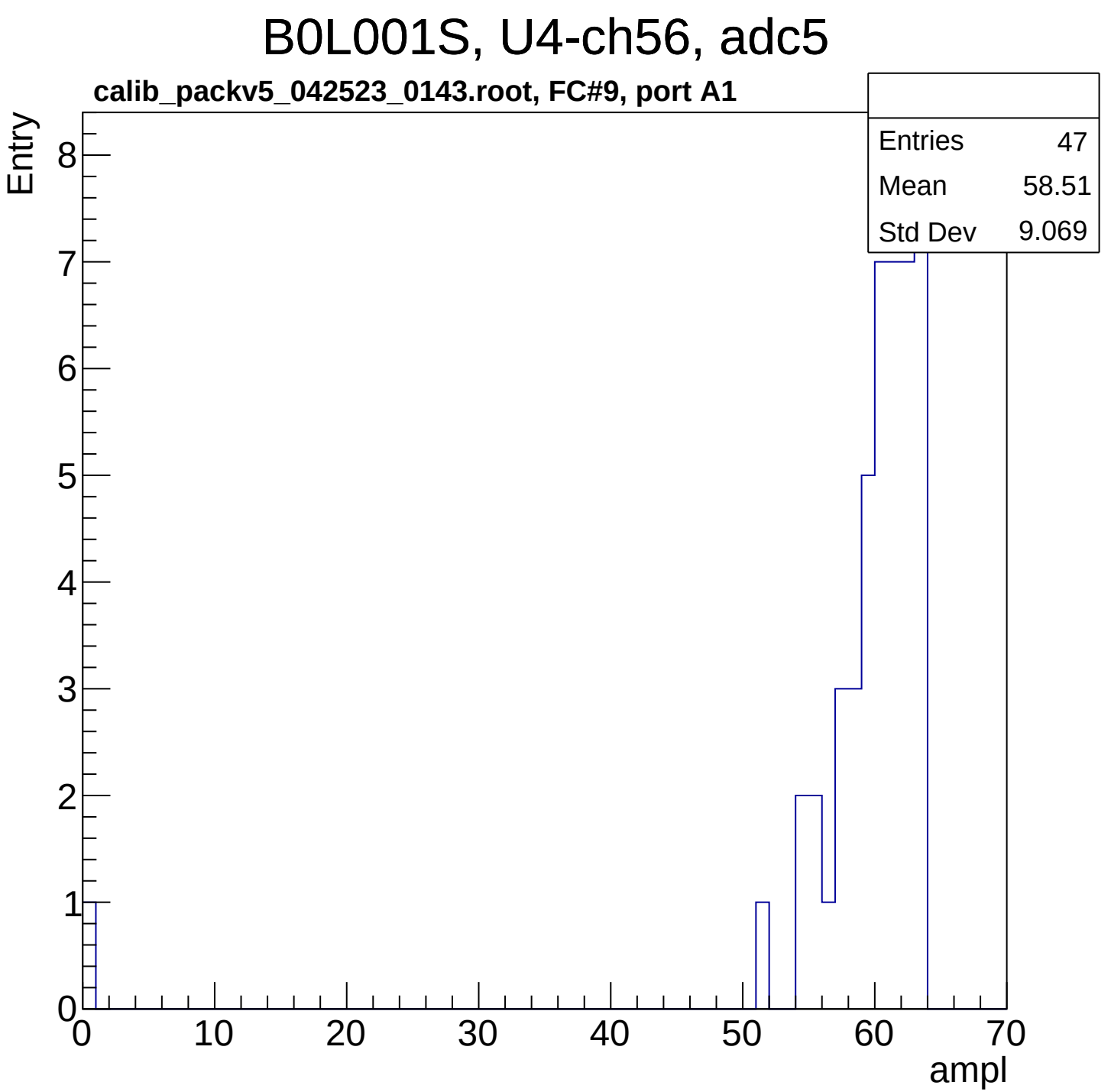
20
30
40
50
60
70


B0L001S, U4-ch56, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.22
Std Dev 3.065
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch56, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.51
Std Dev 9.069
Entry
ampl
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1
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3
4
5
6
7
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0
10
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40
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60
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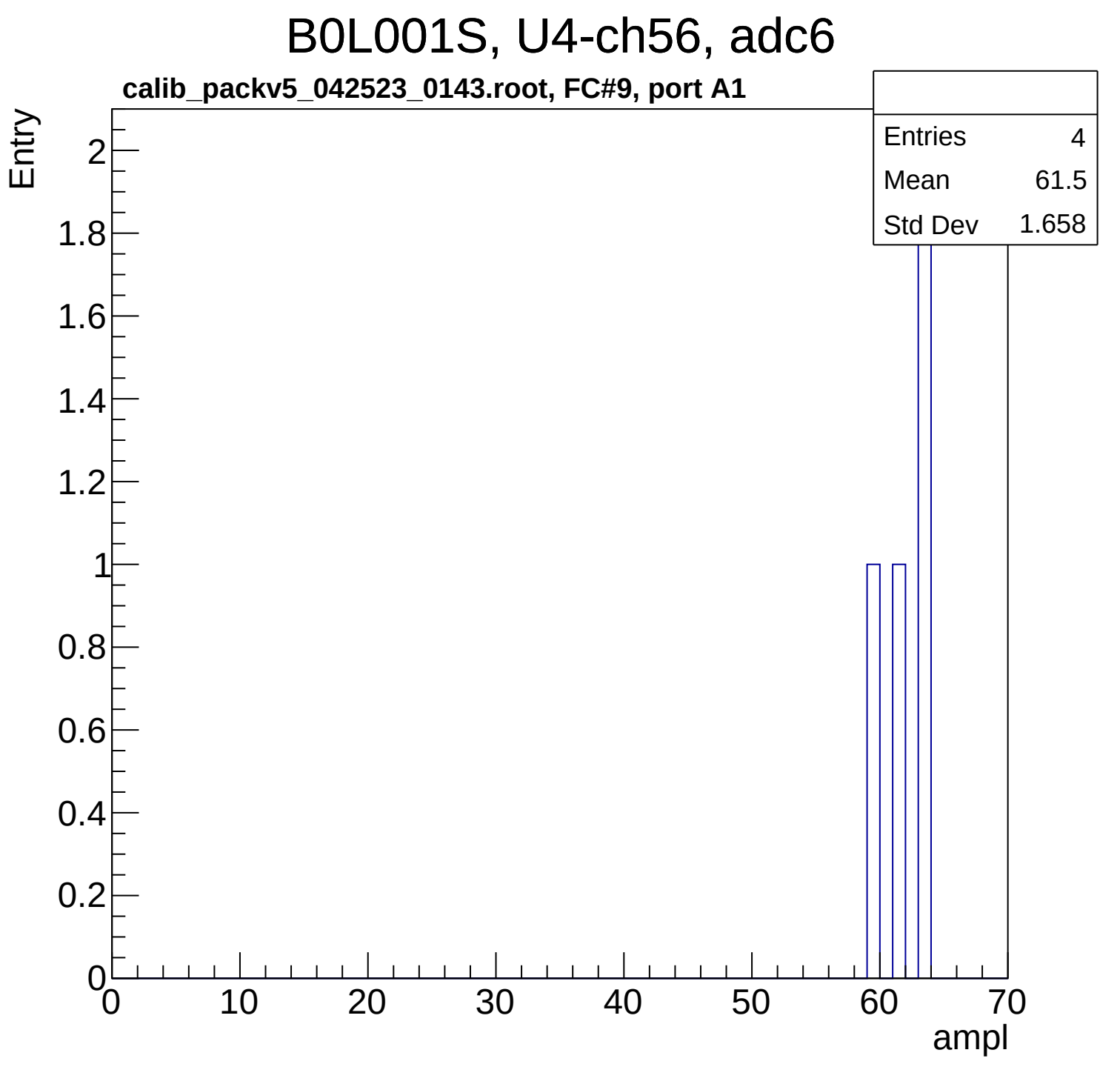
B0L001S, U4-ch56, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
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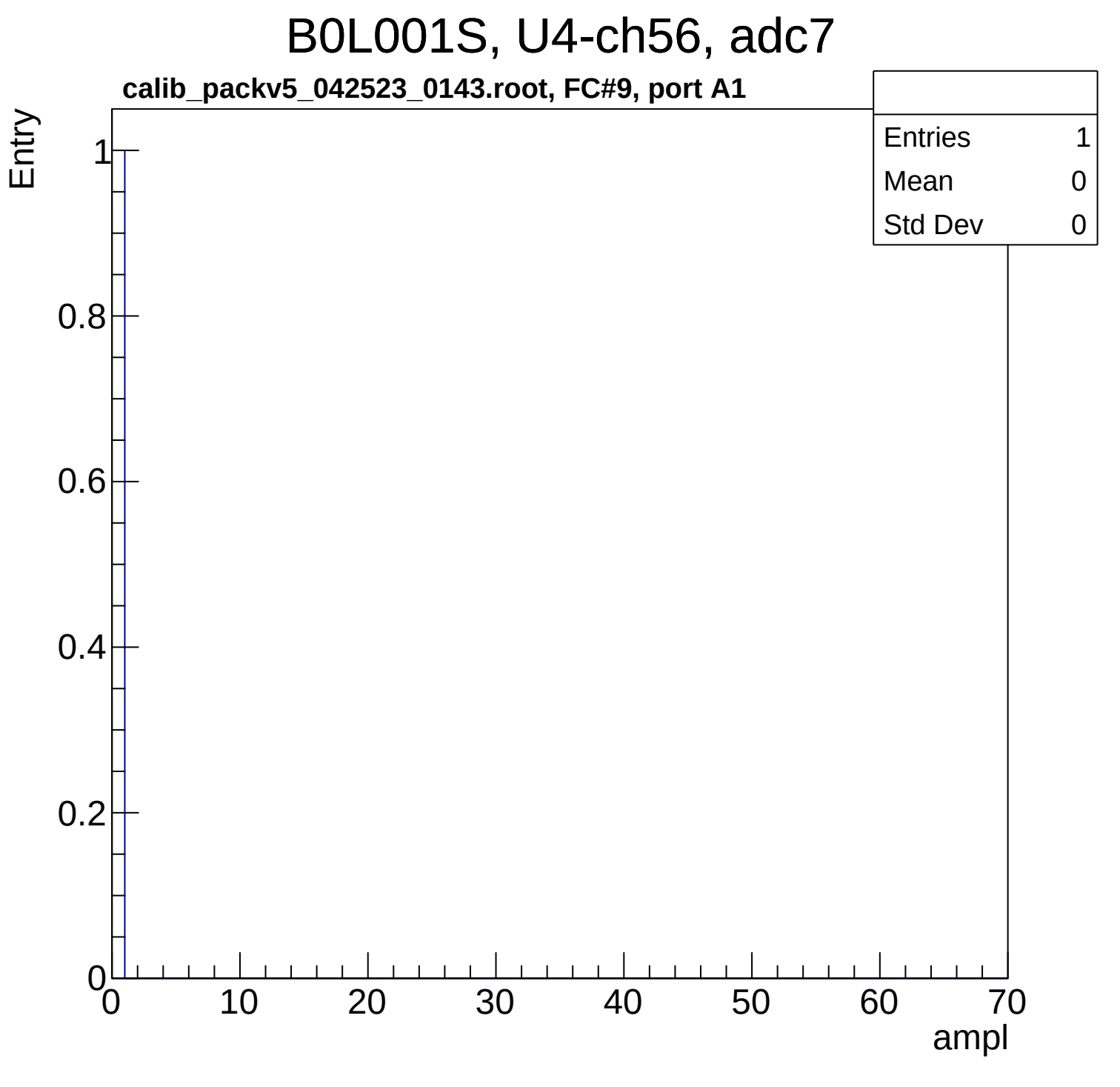
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch56, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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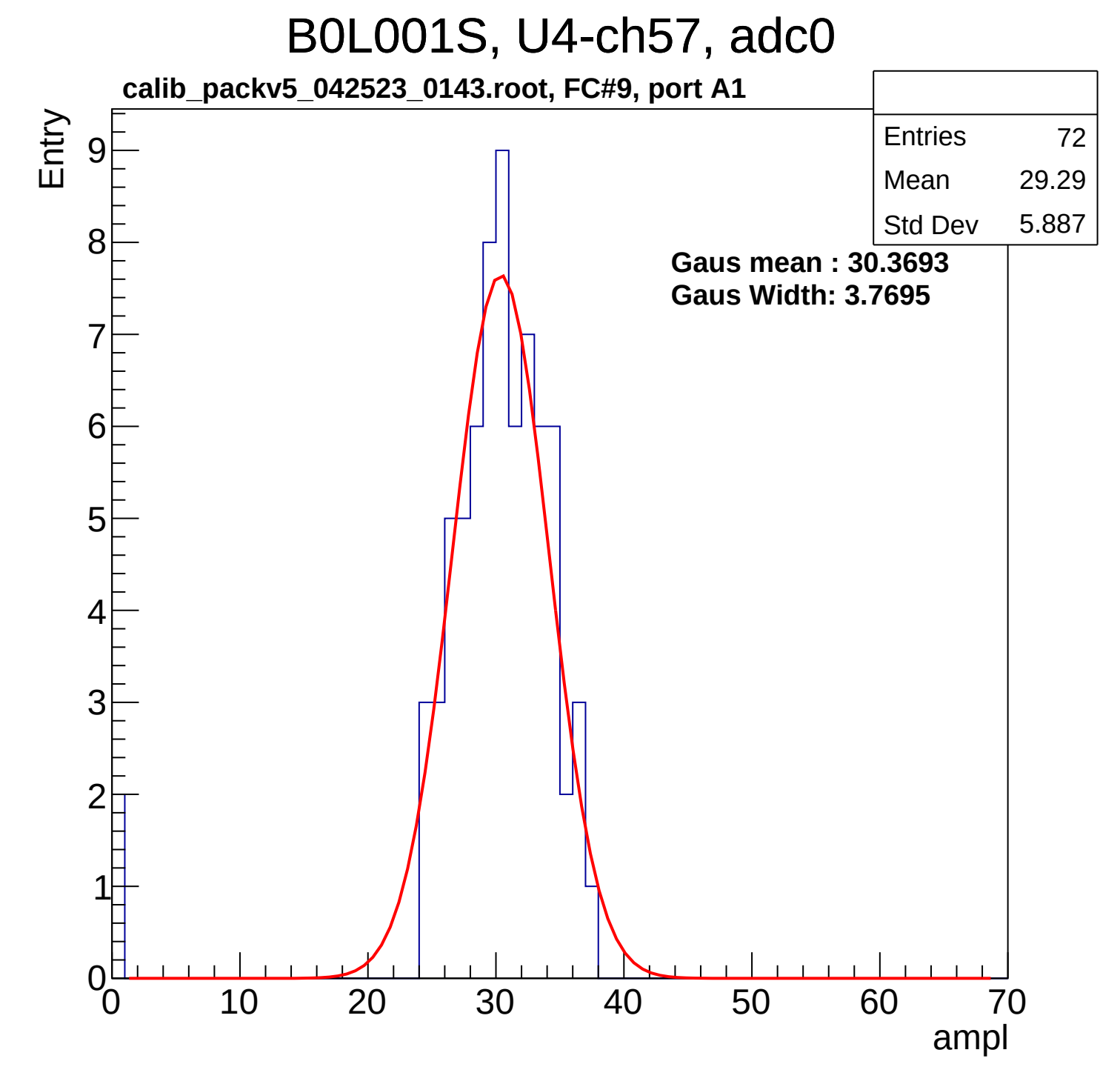
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch57, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.29
Std Dev 5.887
Gaus mean : 30.3693
Gaus Width: 3.7695
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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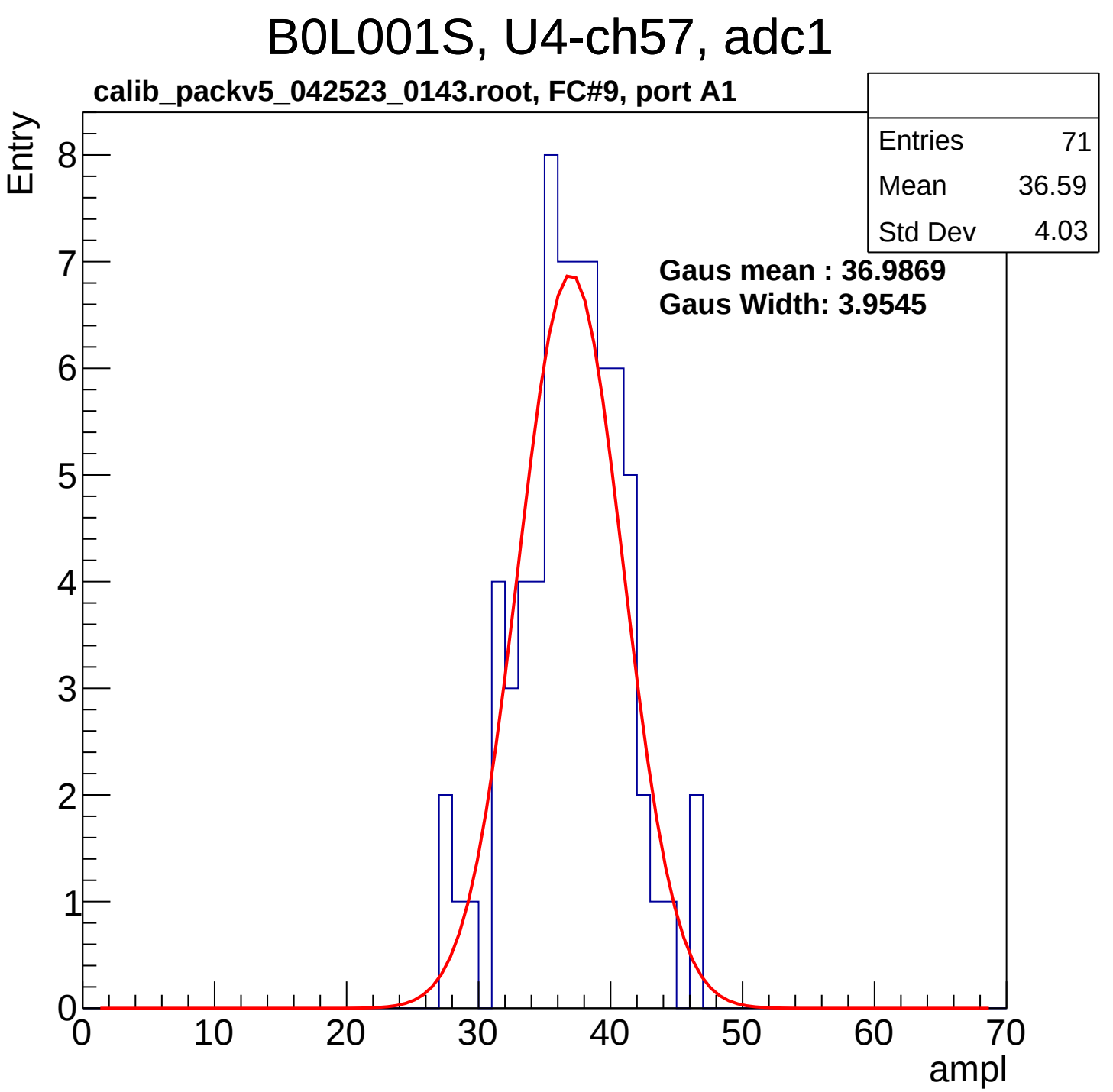
B0L001S, U4-ch57, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 36.59
Std Dev 4.03
Gaus mean : 36.9869
Gaus Width: 3.9545
Entry
ampl
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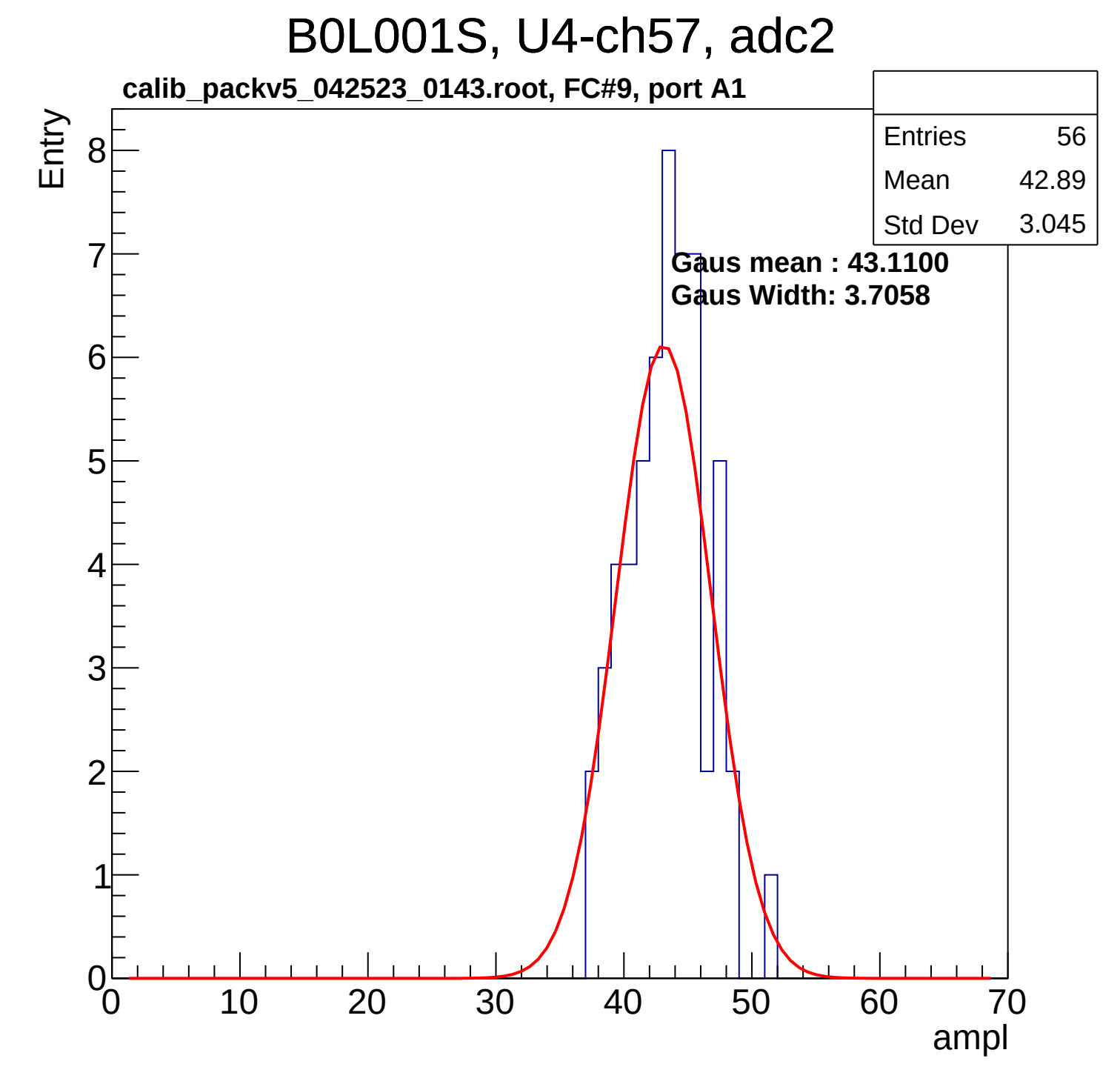
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch57, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.89
Std Dev 3.045
Gaus mean : 43.1100
Gaus Width: 3.7058
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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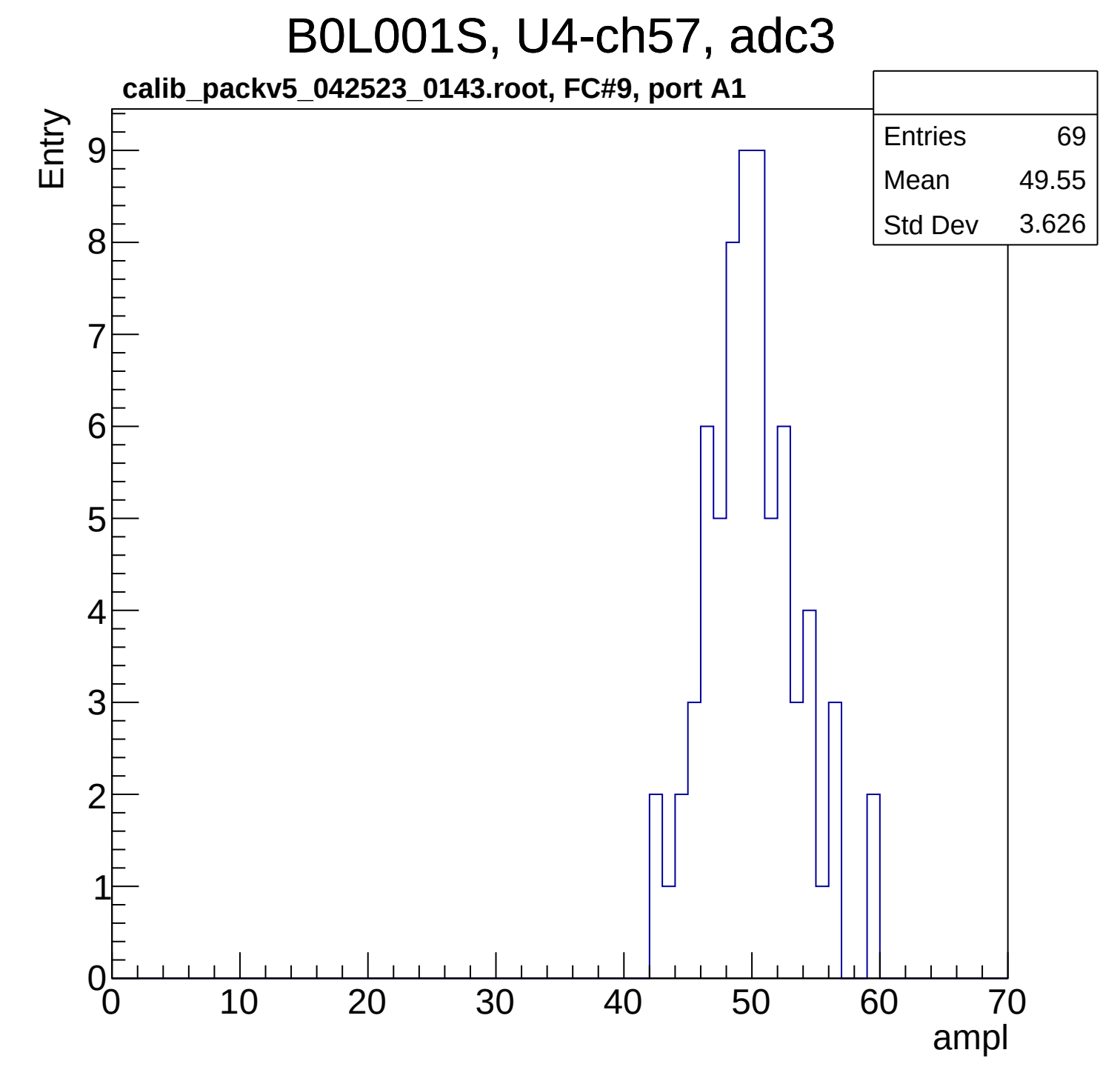
B0L001S, U4-ch57, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 49.55
Std Dev 3.626
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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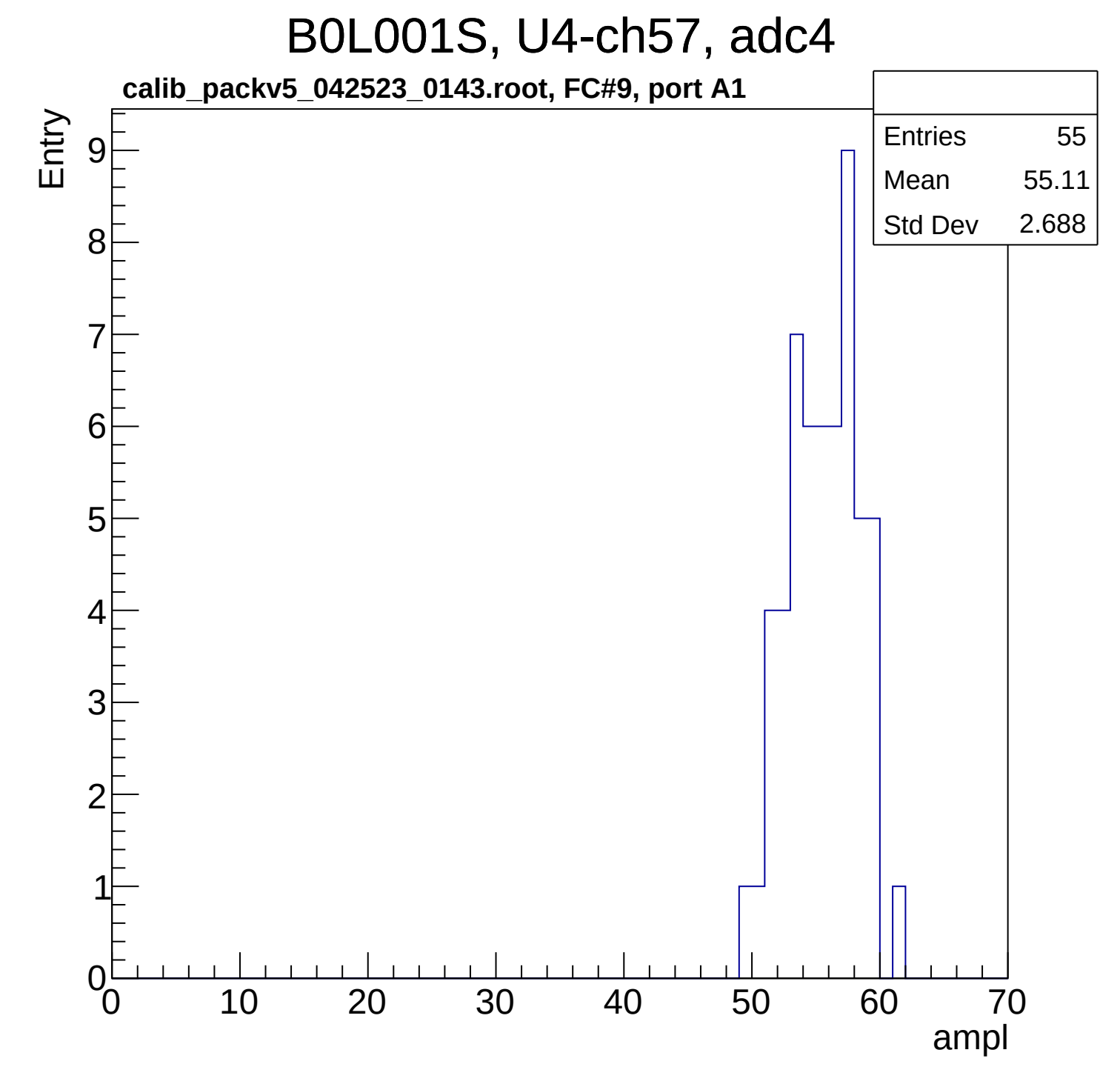
B0L001S, U4-ch57, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 55.11
Std Dev 2.688
Entry
ampl
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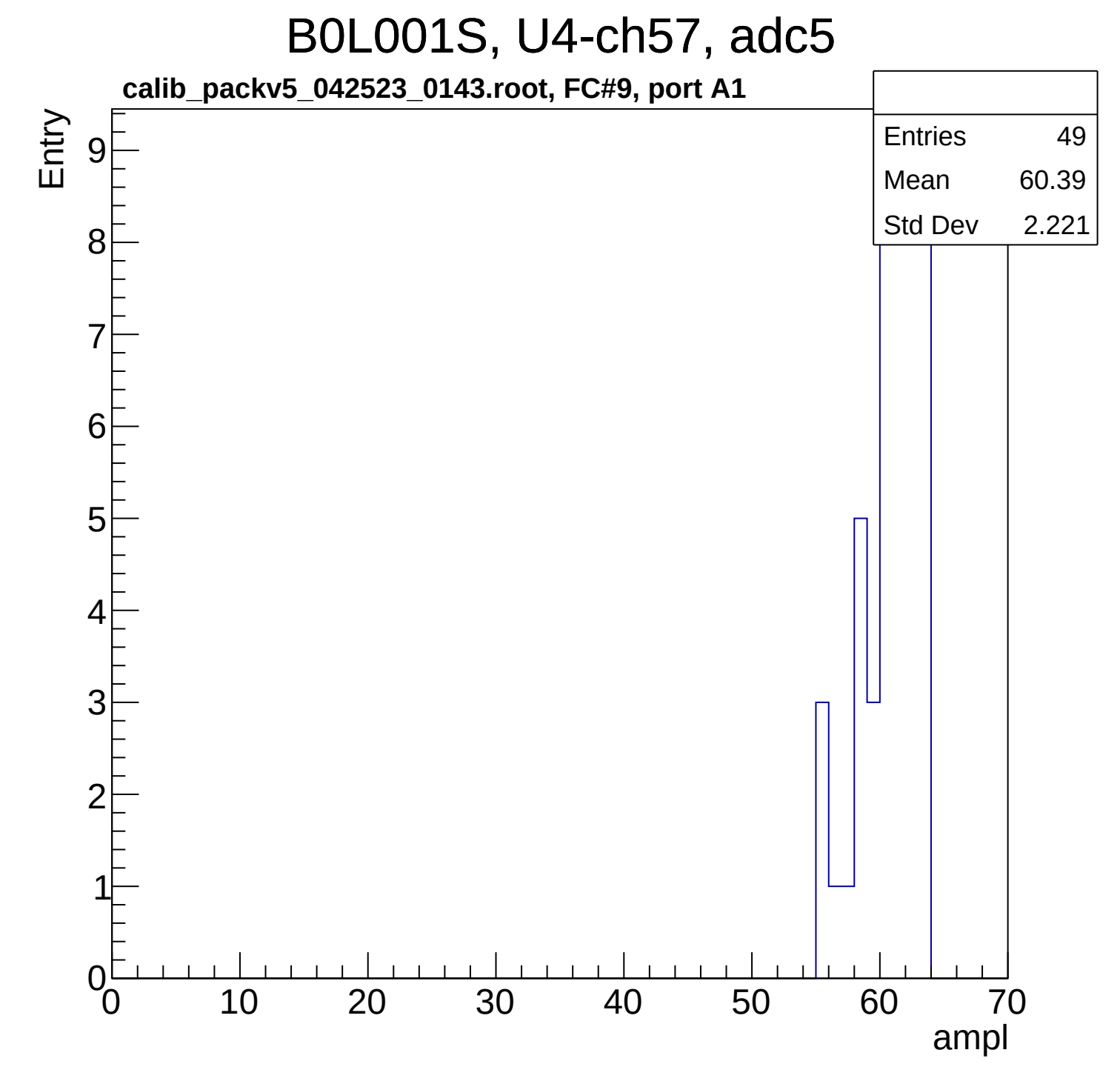
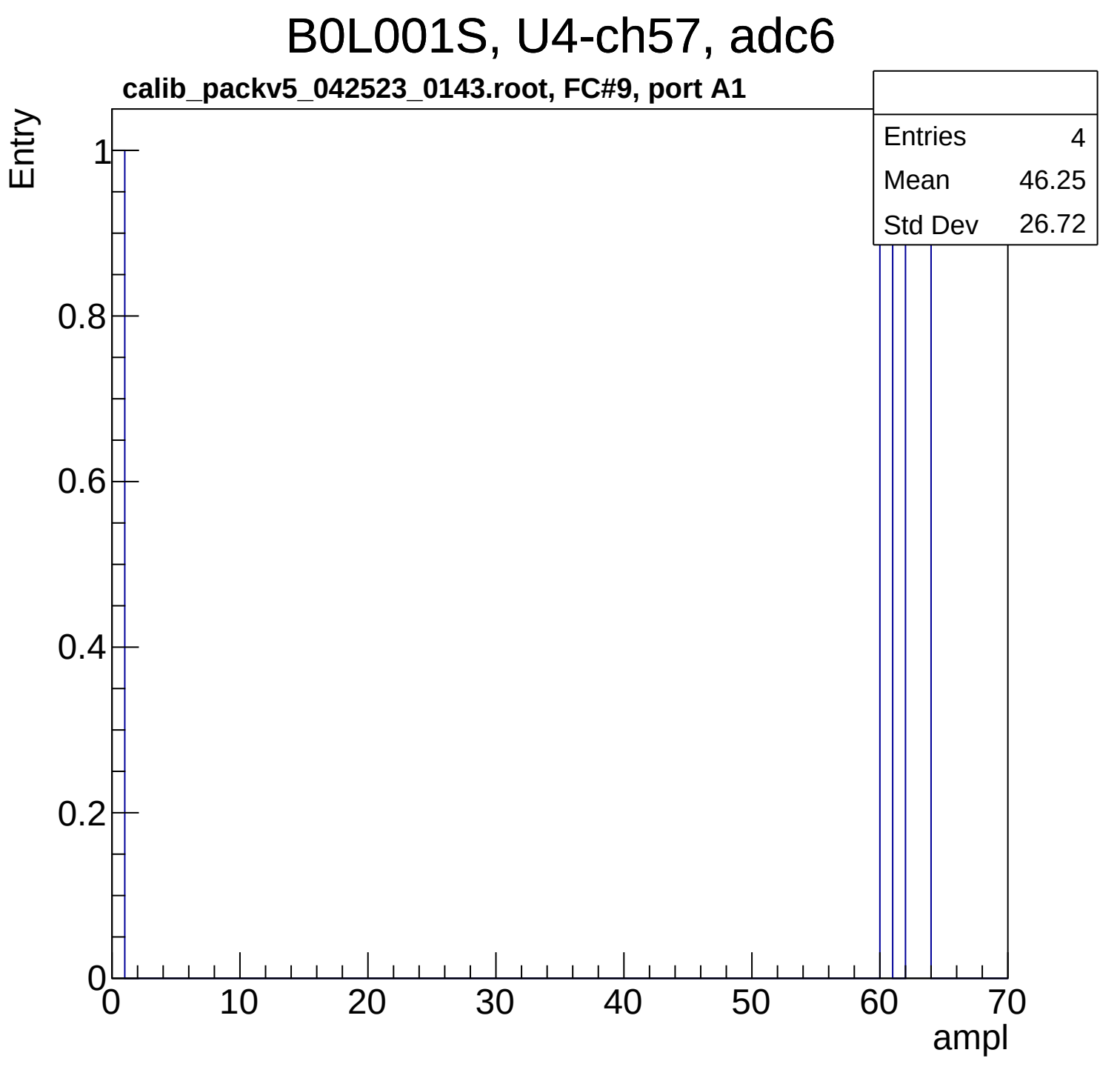
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch57, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 60.39
Std Dev 2.221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch57, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.25
Std Dev 26.72
Entry
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0.8
0.6
0.4
0.2
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10
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50
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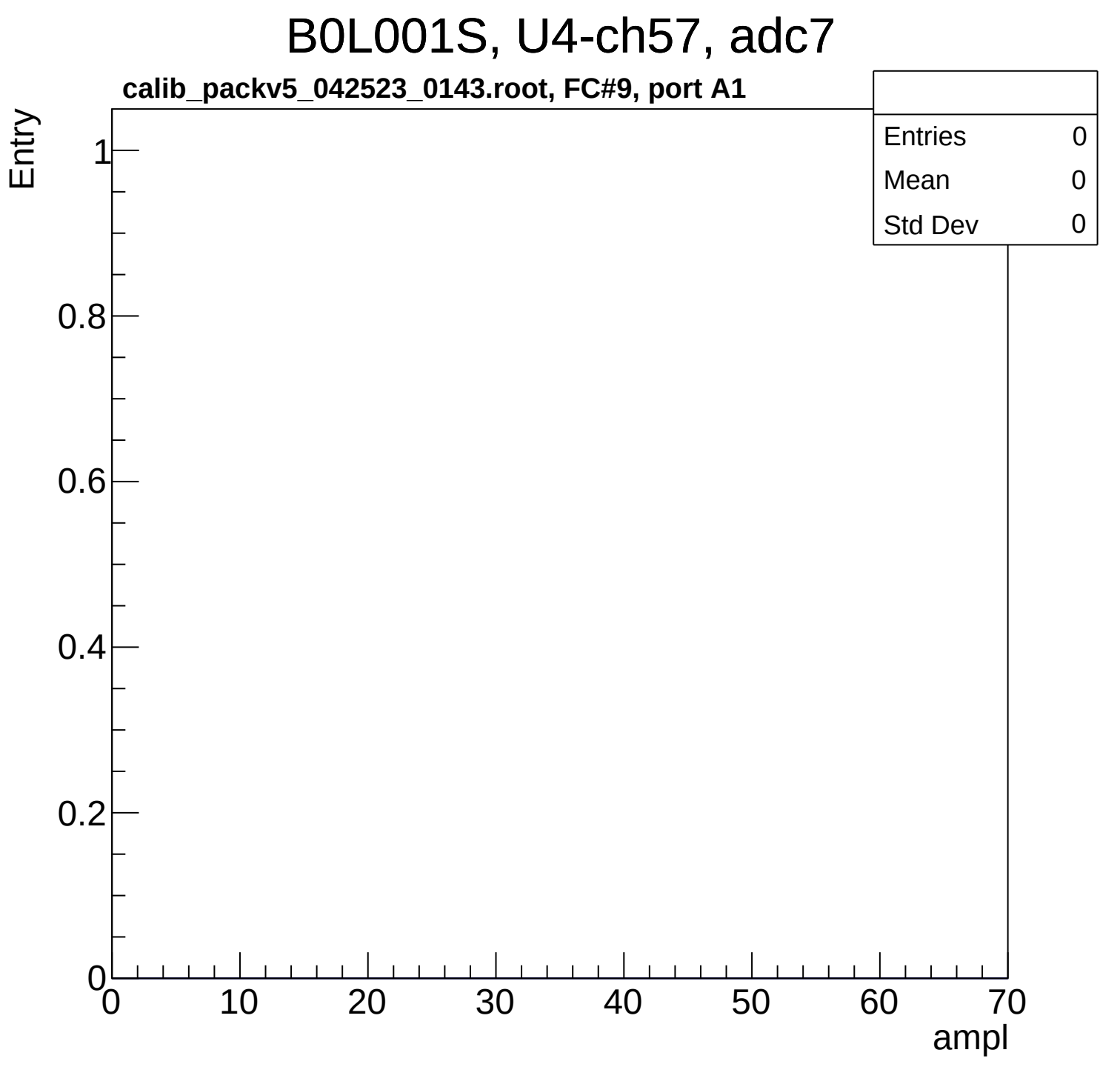
ampl


B0L001S, U4-ch57, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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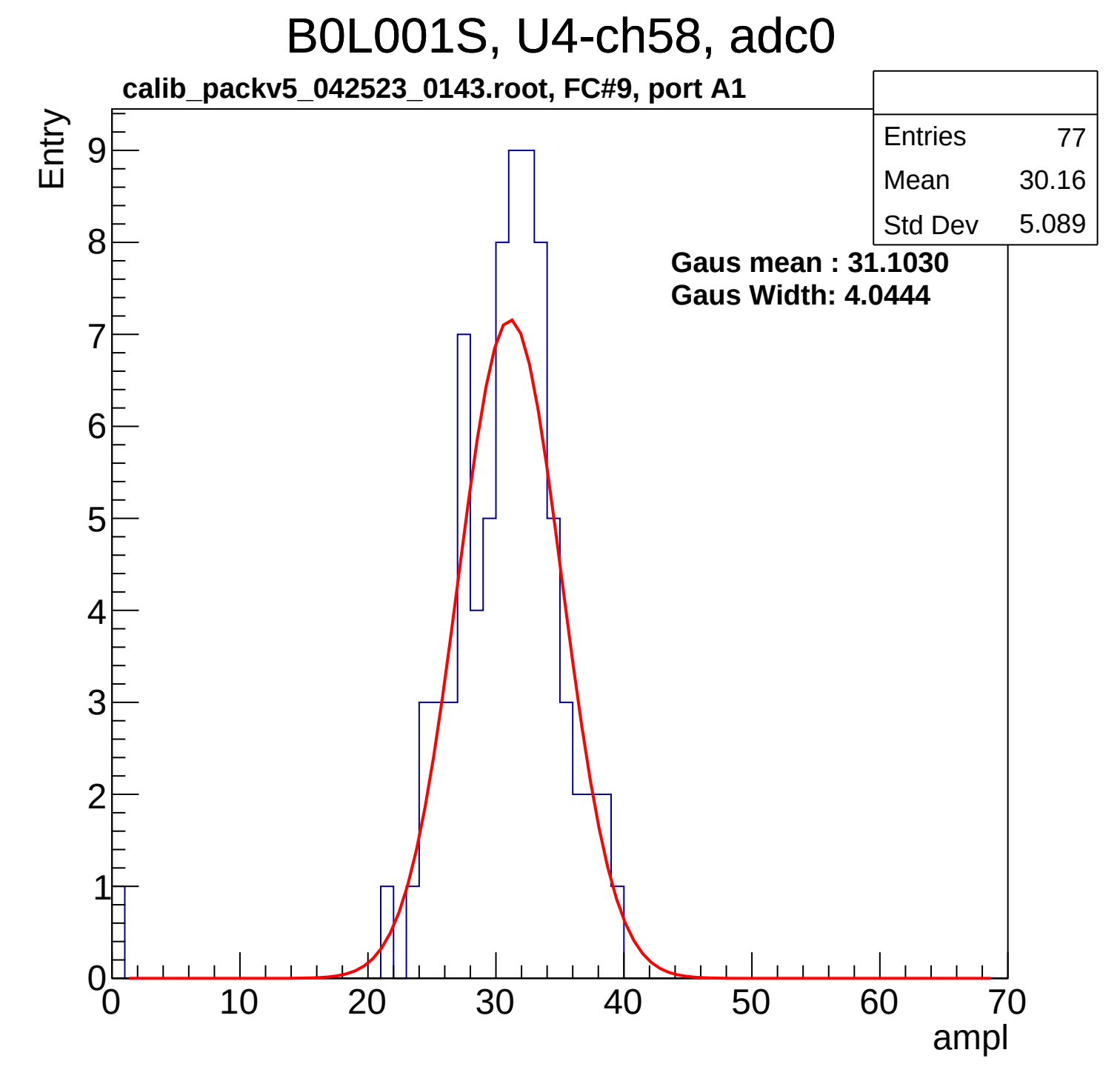
40
50
60
70
ampl


B0L001S, U4-ch58, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.16
Std Dev 5.089
Gaus mean : 31.1030
Gaus Width: 4.0444
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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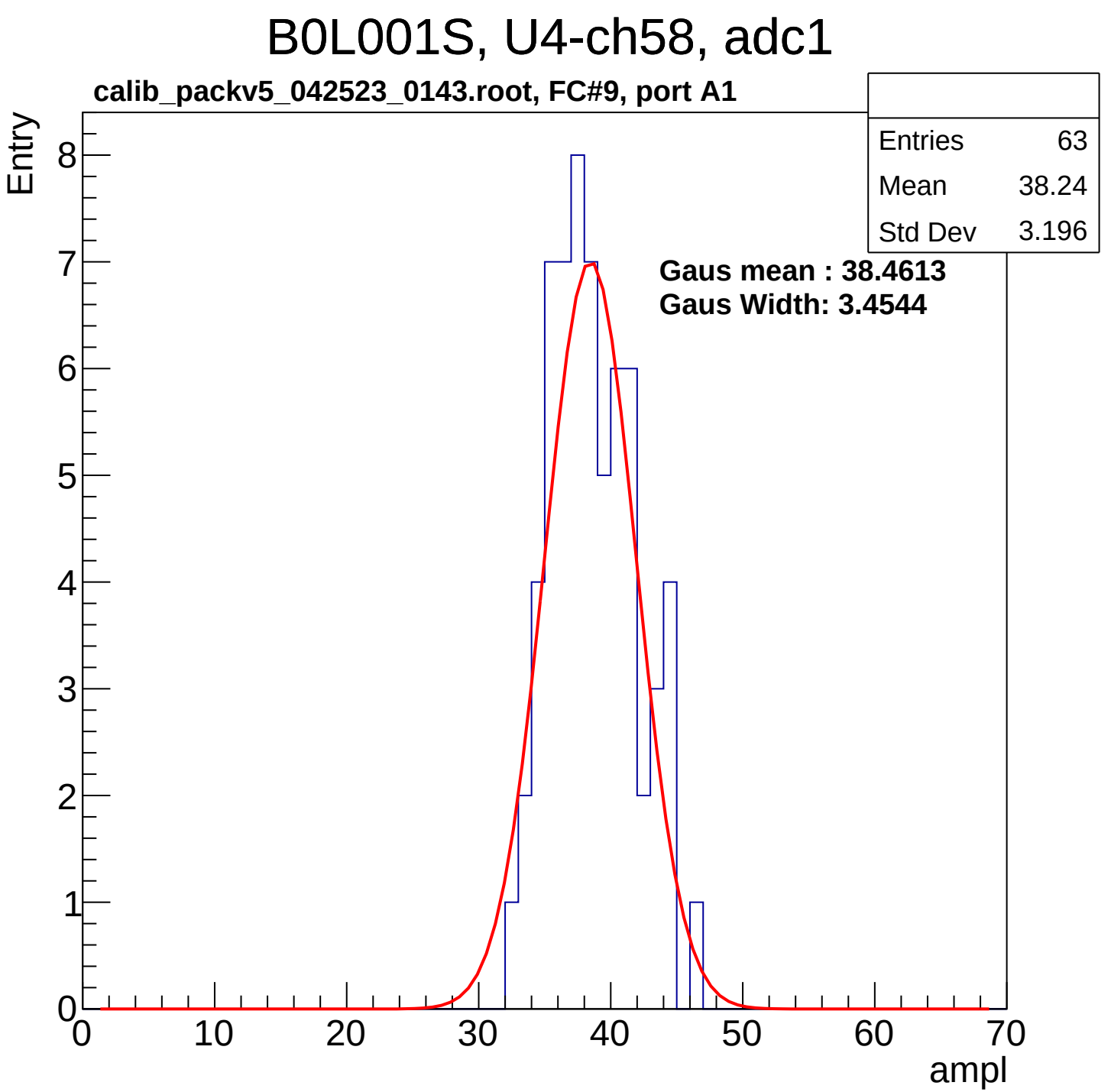
B0L001S, U4-ch58, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.24
Std Dev 3.196
Gaus mean : 38.4613
Gaus Width: 3.4544
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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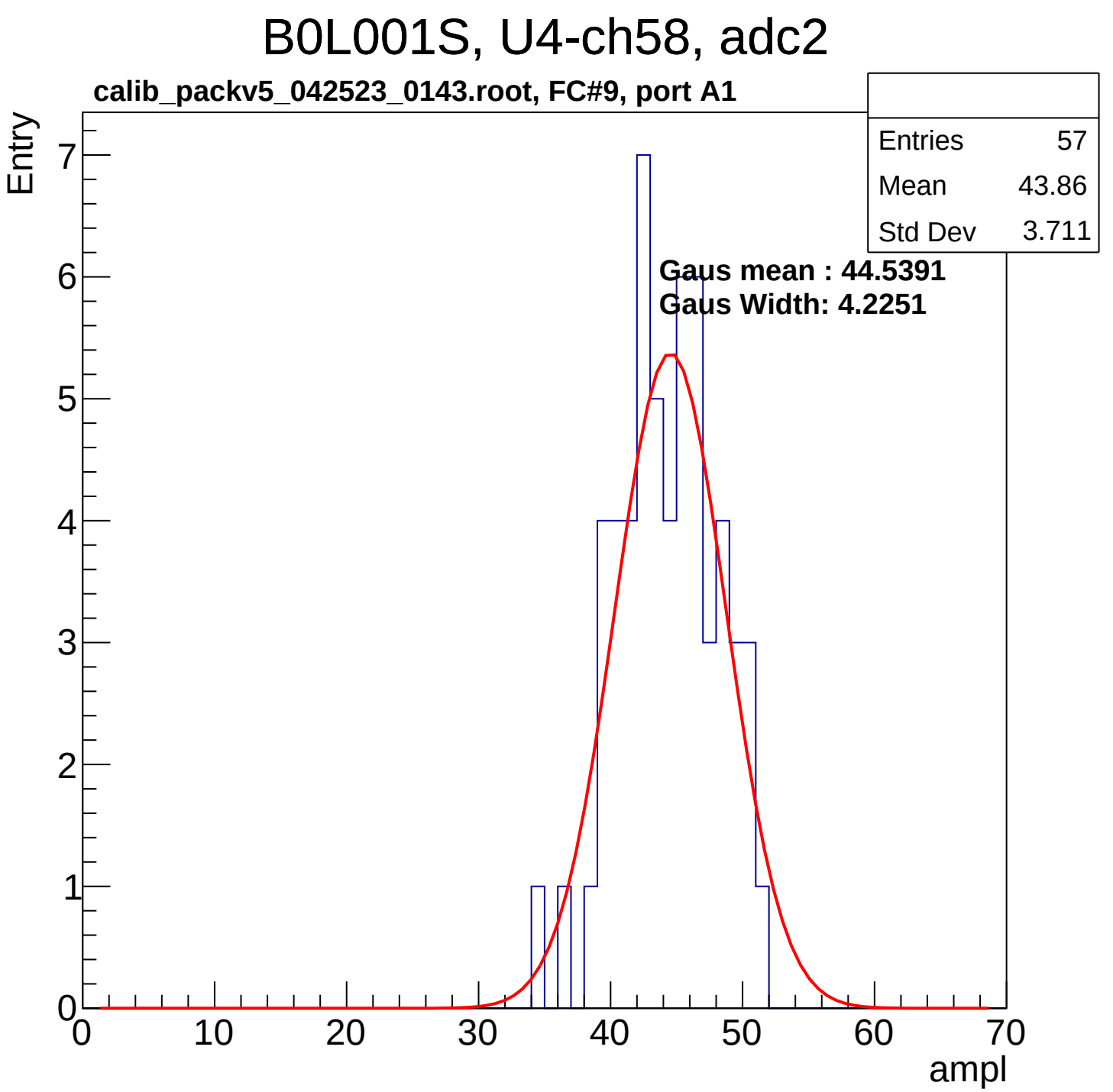
B0L001S, U4-ch58, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 43.86
Std Dev 3.711
Gaus mean : 44.5391
Gaus Width: 4.2251
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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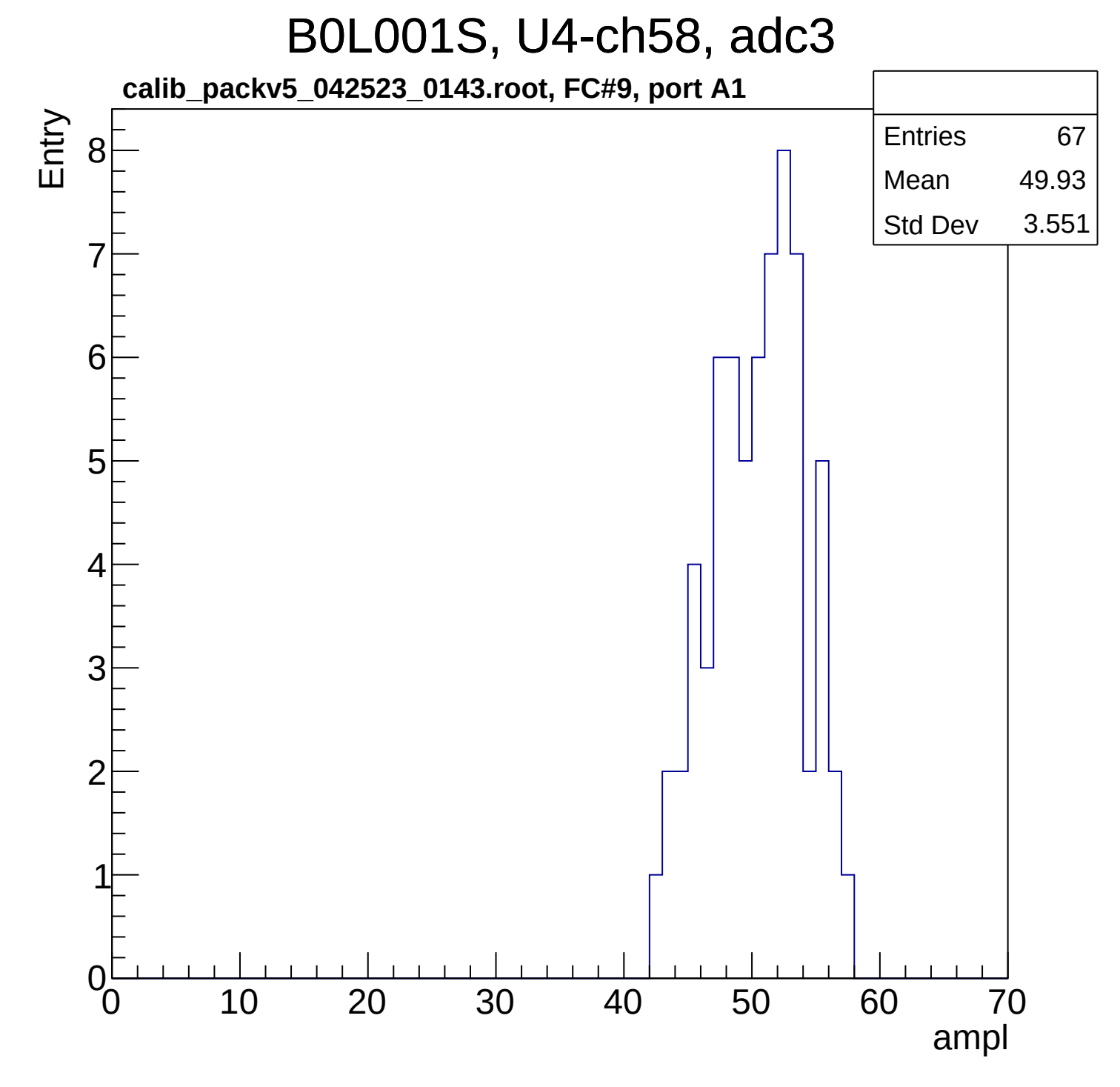
B0L001S, U4-ch58, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.93
Std Dev 3.551
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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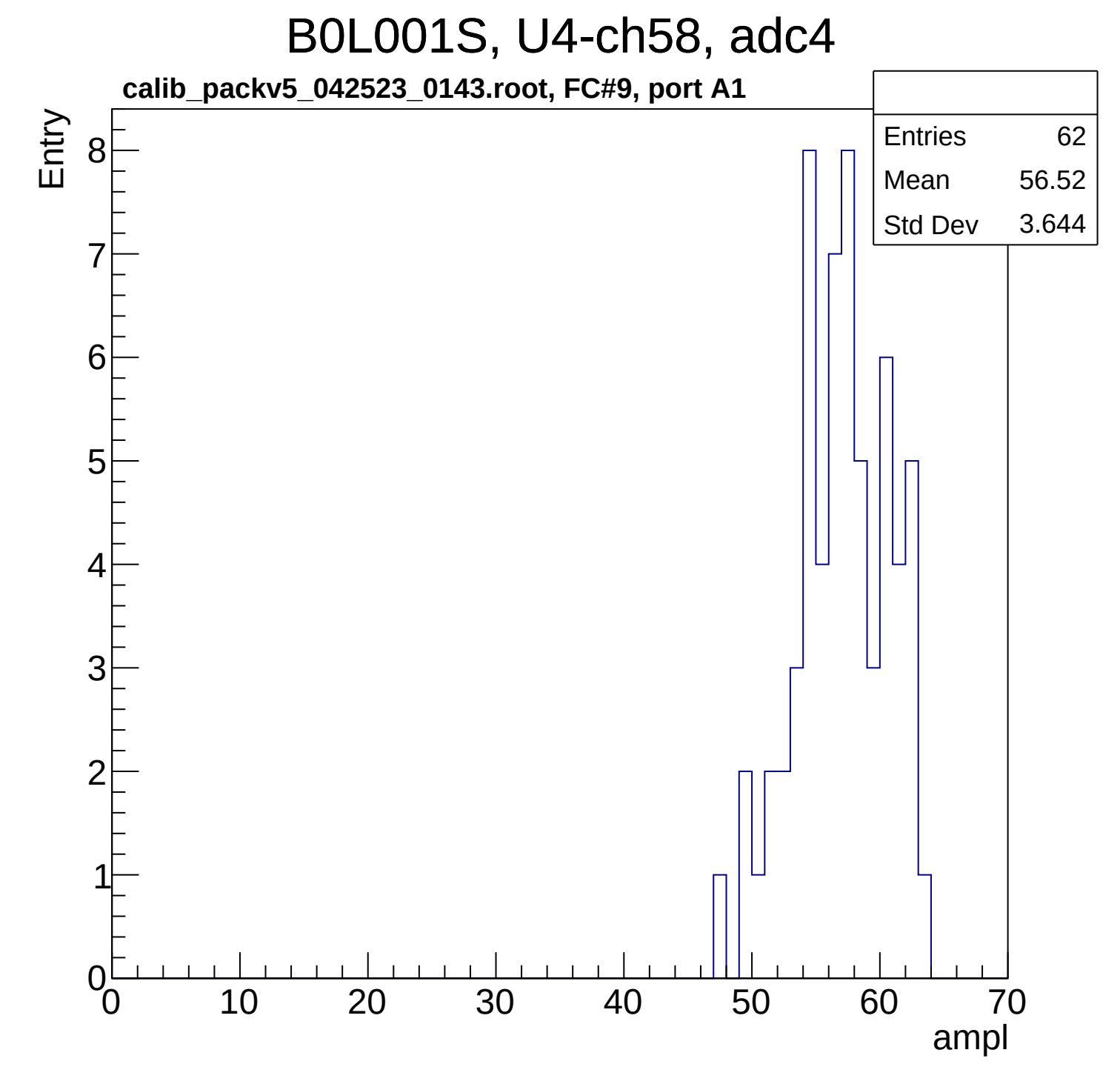
B0L001S, U4-ch58, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.52
Std Dev 3.644
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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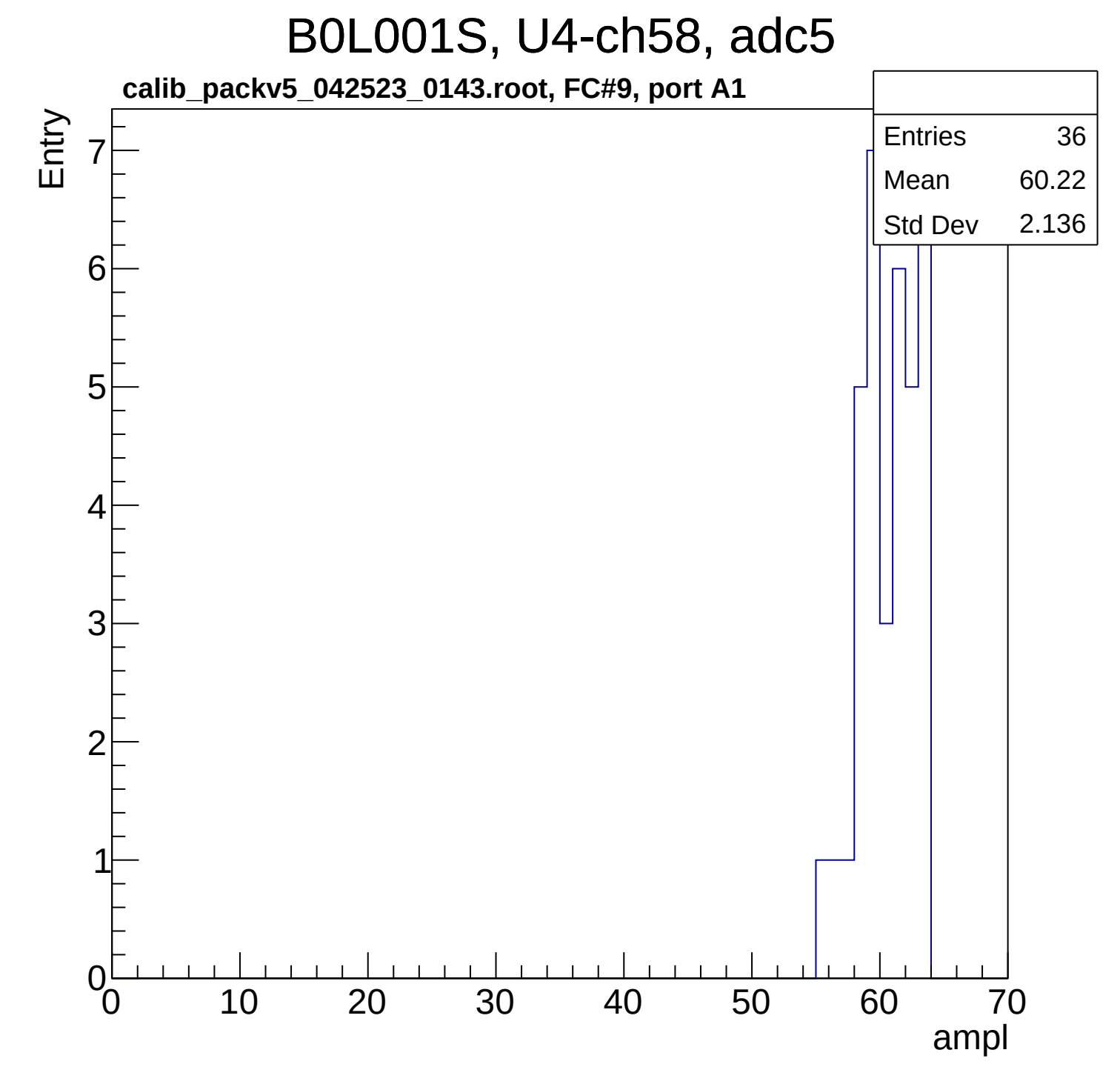
B0L001S, U4-ch58, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 60.22
Std Dev 2.136
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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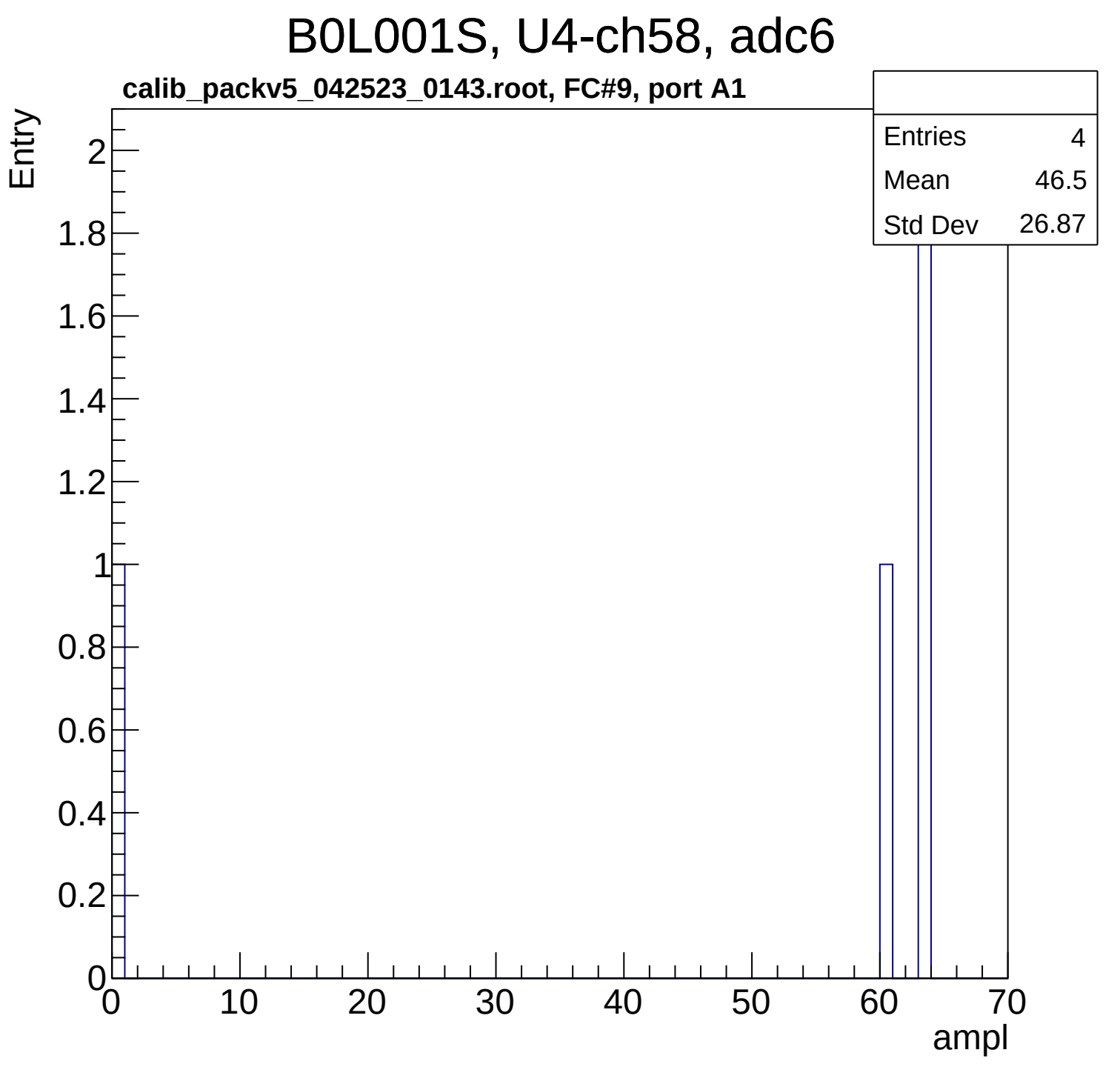
B0L001S, U4-ch58, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.5
Std Dev 26.87
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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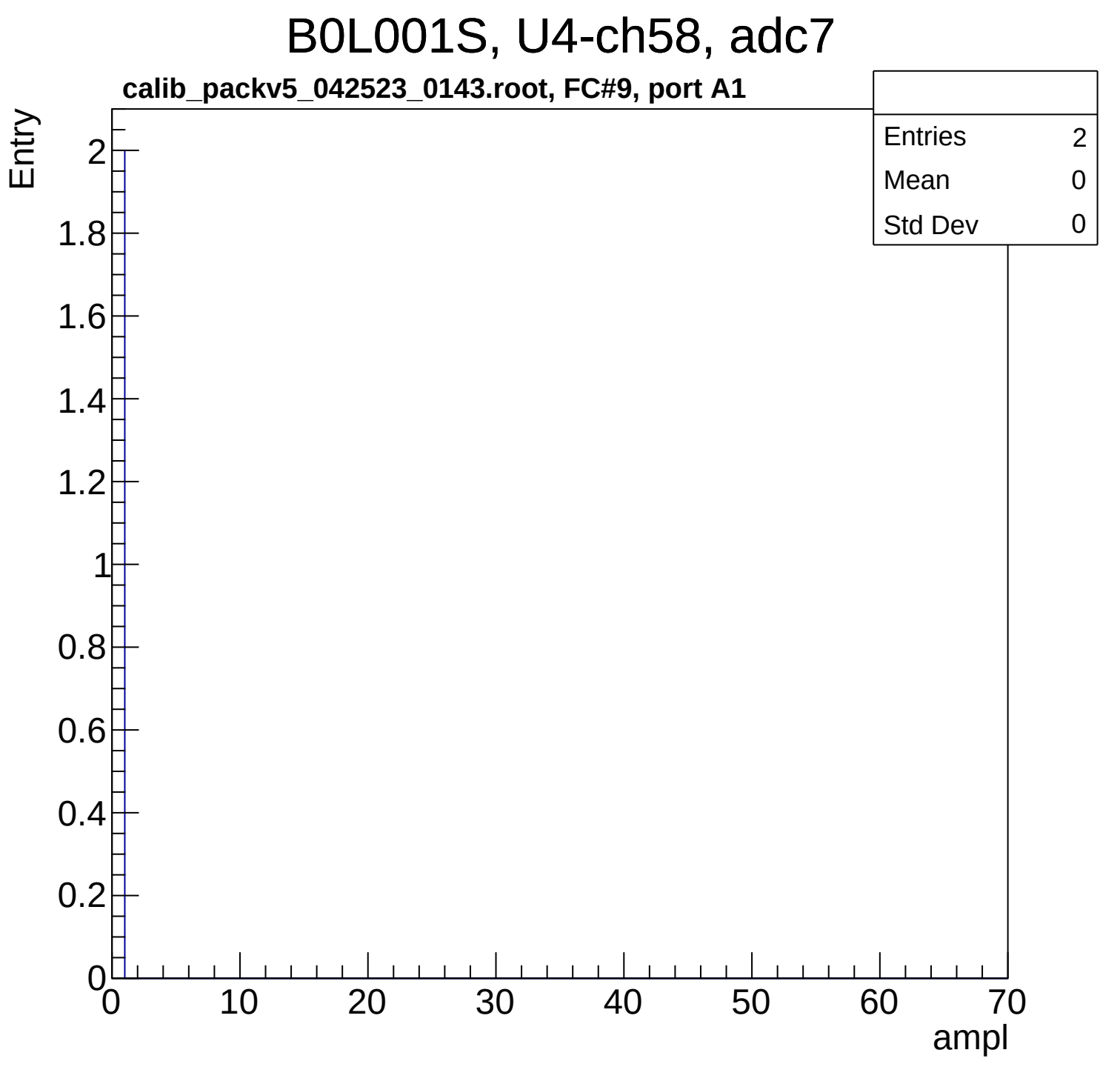
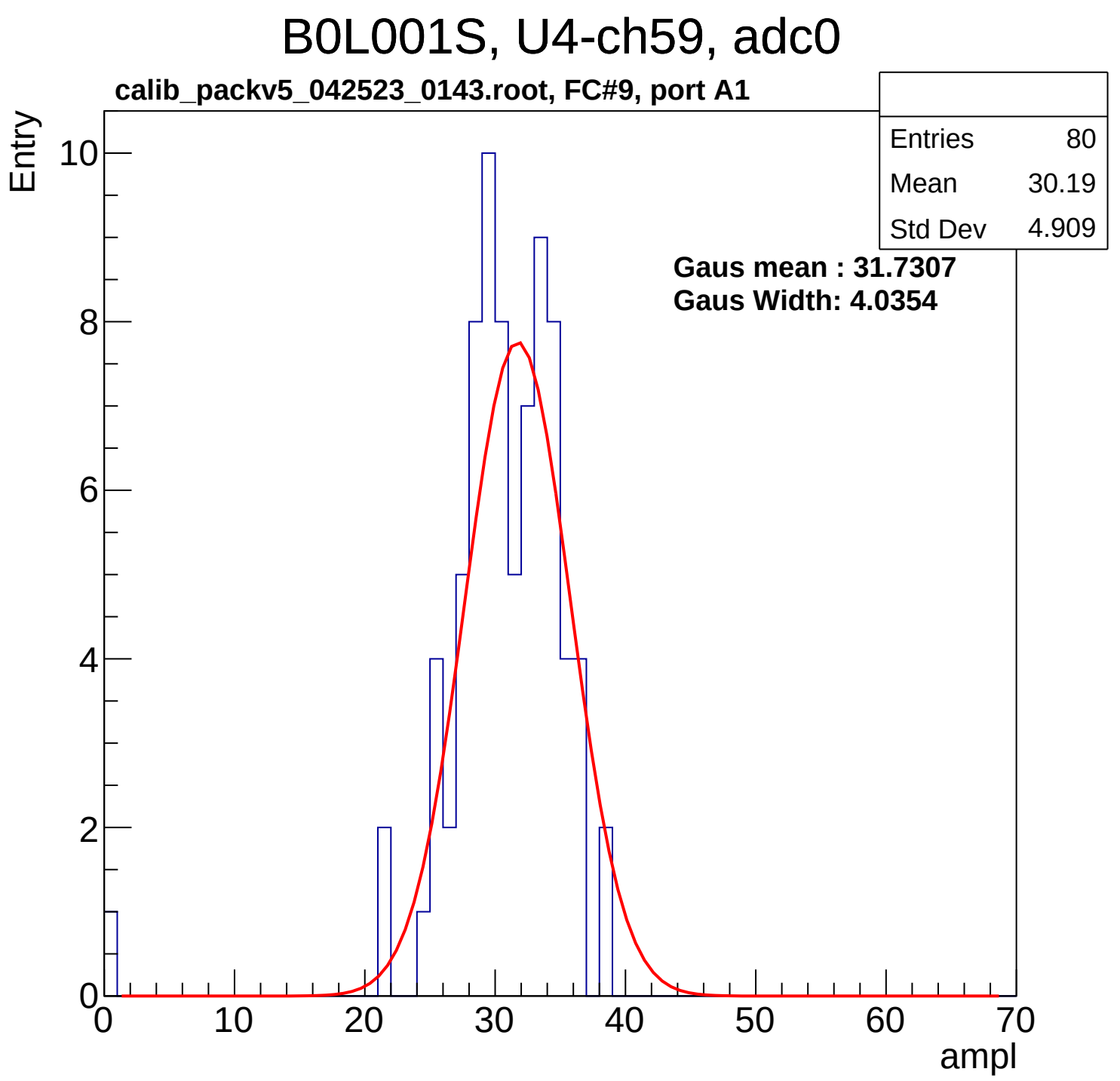
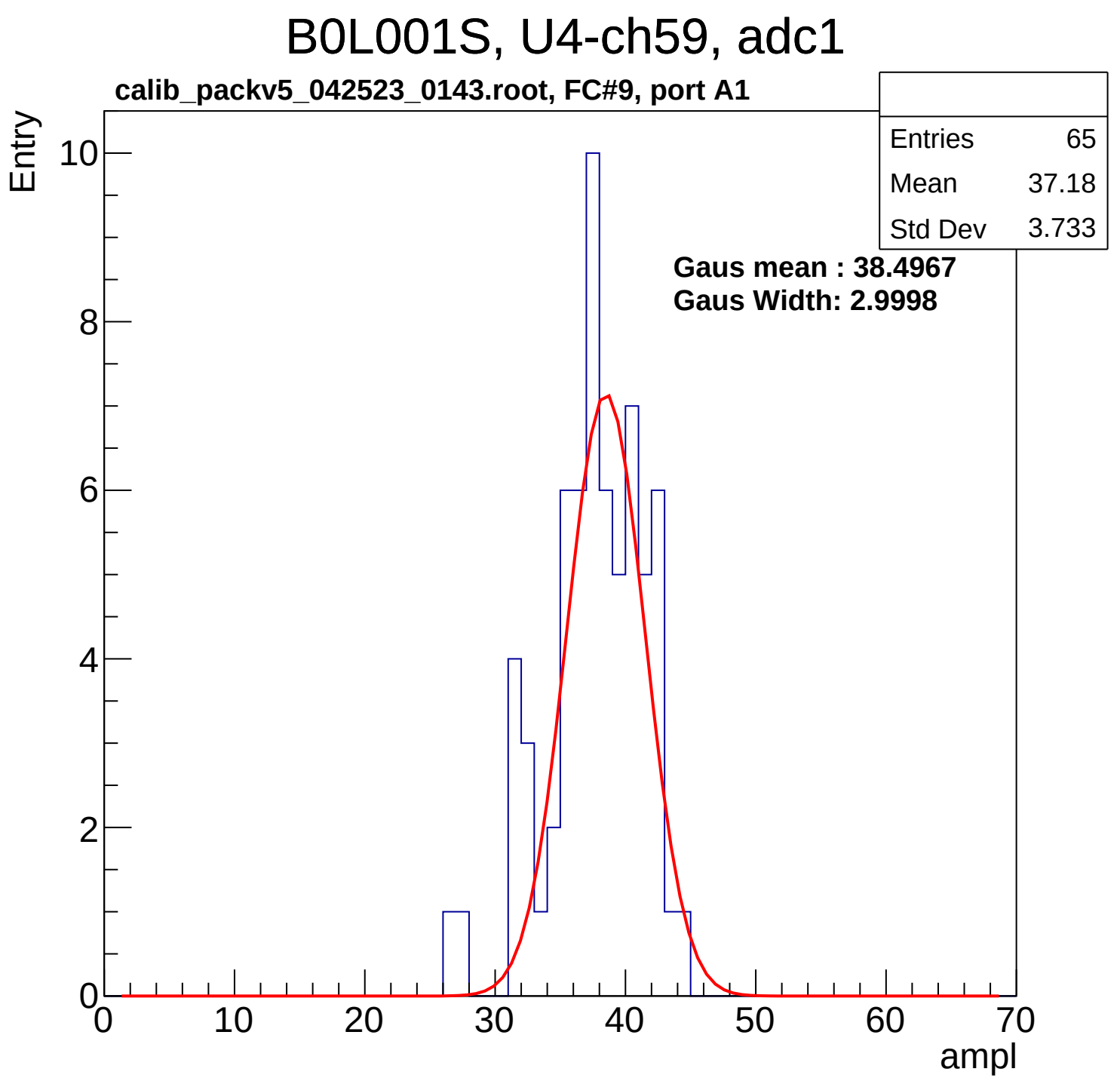
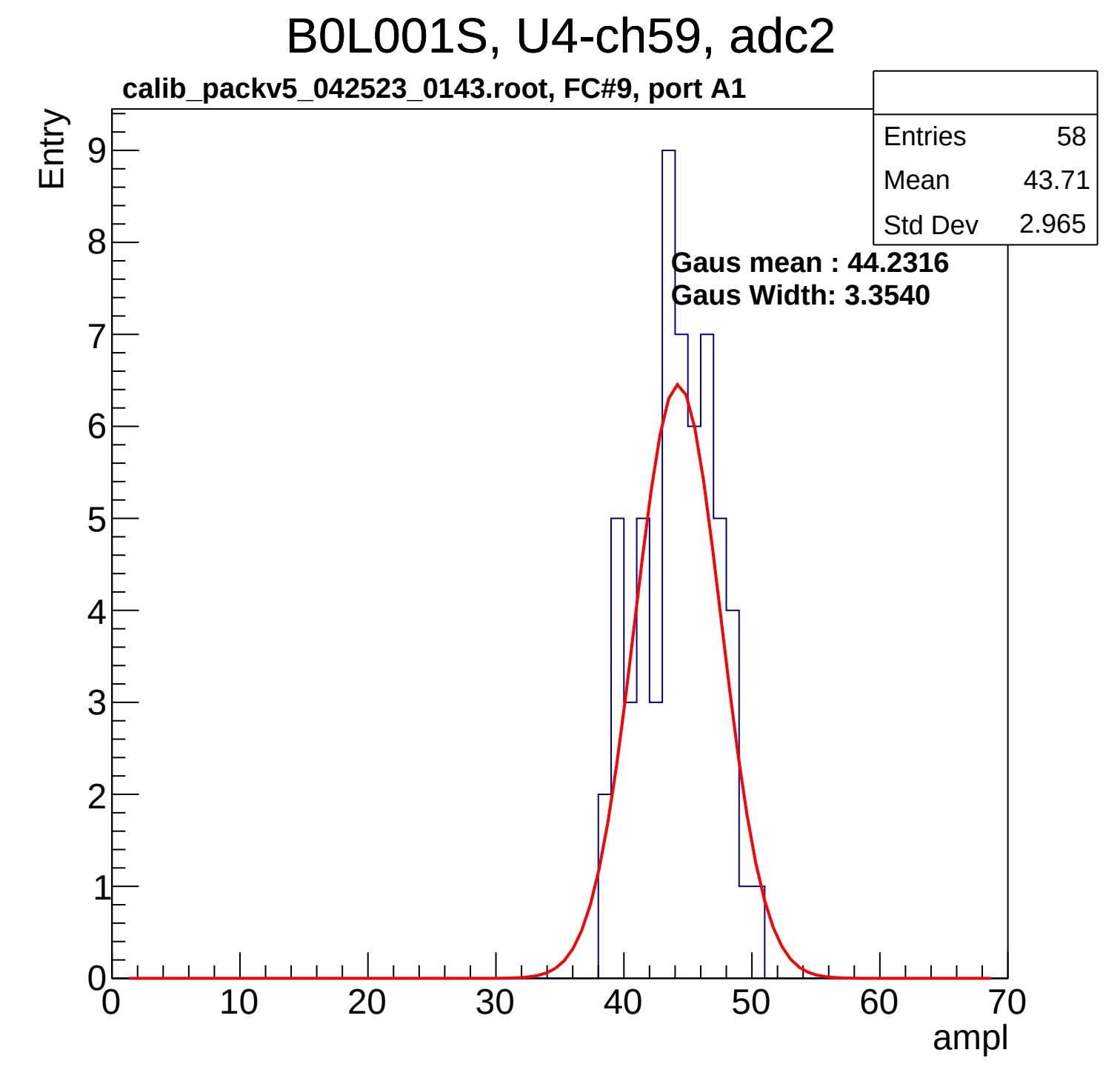

B0L001S, U4-ch58, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch59, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 30.19
Std Dev 4.909
Gaus mean : 31.7307
Gaus Width: 4.0354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch59, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.18
Std Dev 3.733
Gaus mean : 38.4967
Gaus Width: 2.9998
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch59, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.71
Std Dev 2.965
Gaus mean : 44.2316
Gaus Width: 3.3540
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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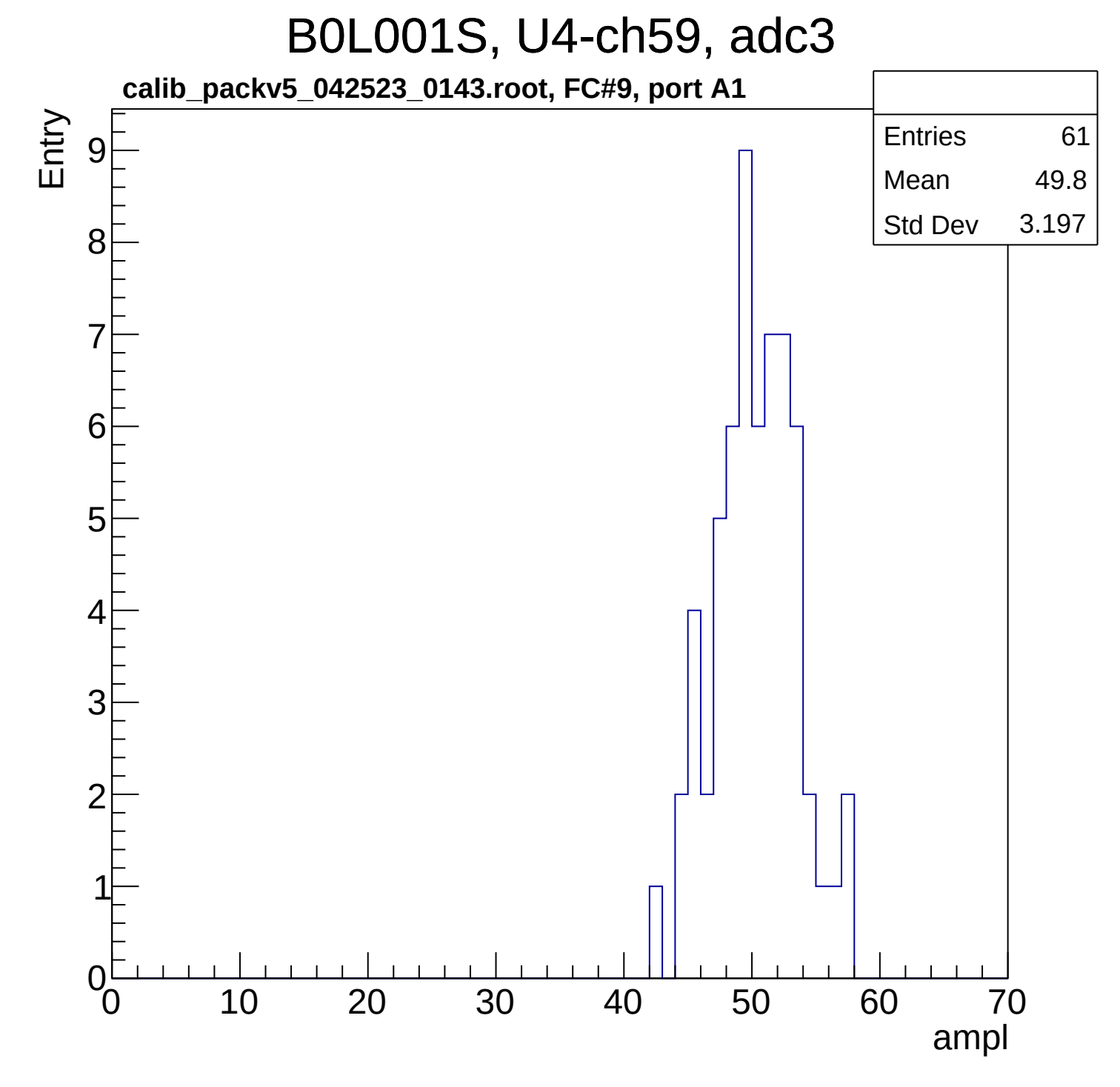
B0L001S, U4-ch59, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.8
Std Dev 3.197
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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40
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60
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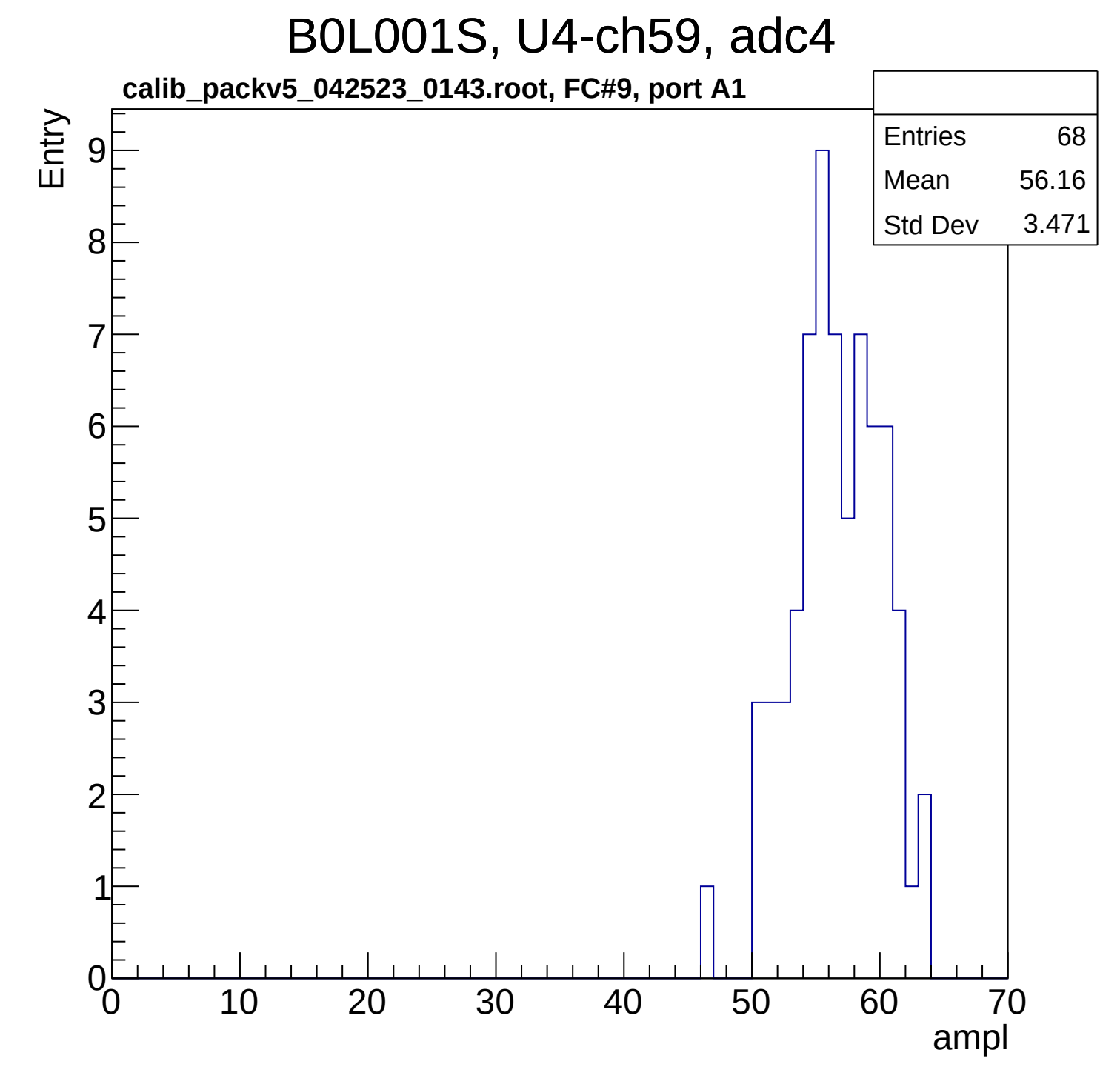
B0L001S, U4-ch59, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 56.16
Std Dev 3.471
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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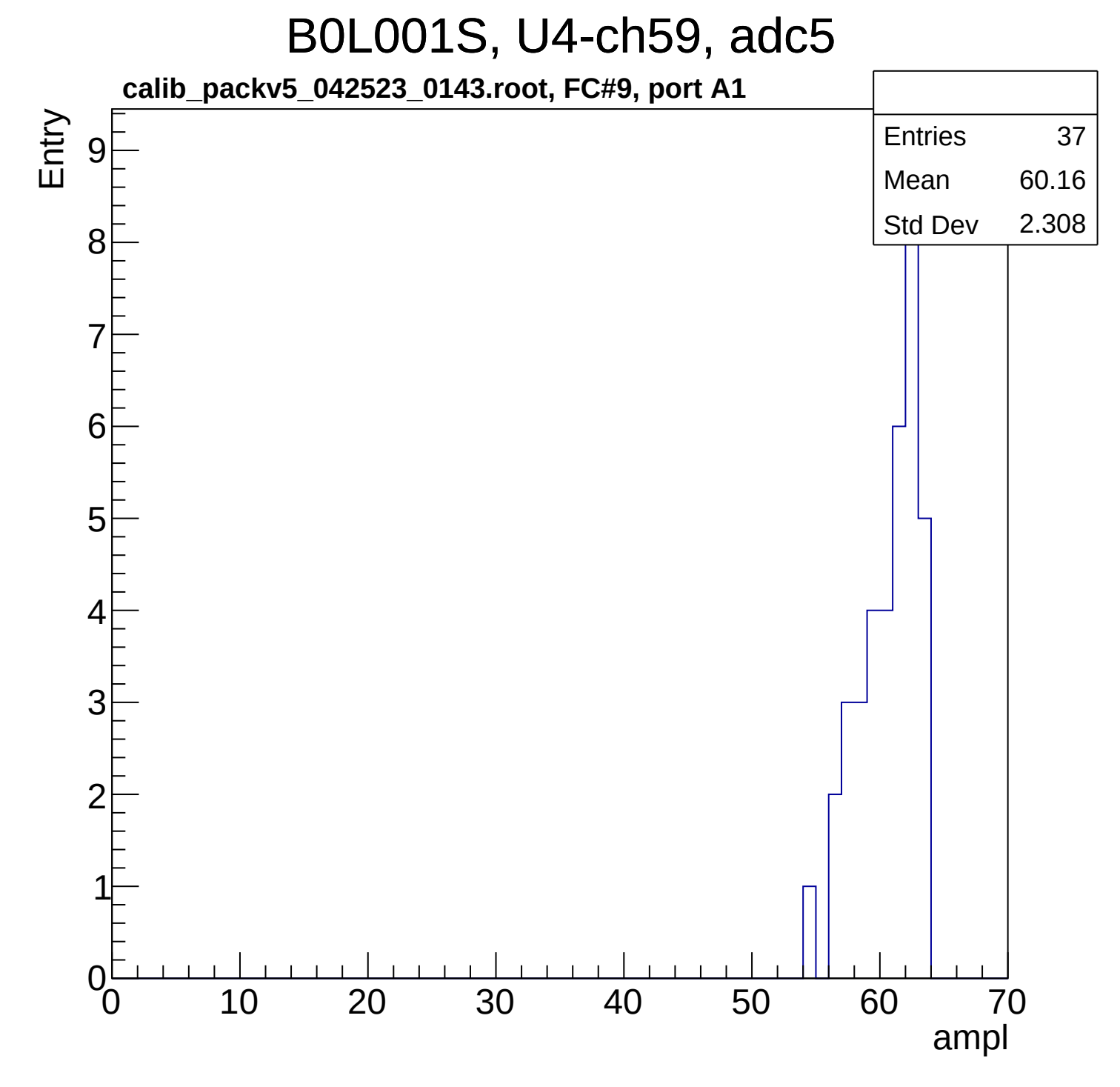
B0L001S, U4-ch59, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 60.16
Std Dev 2.308
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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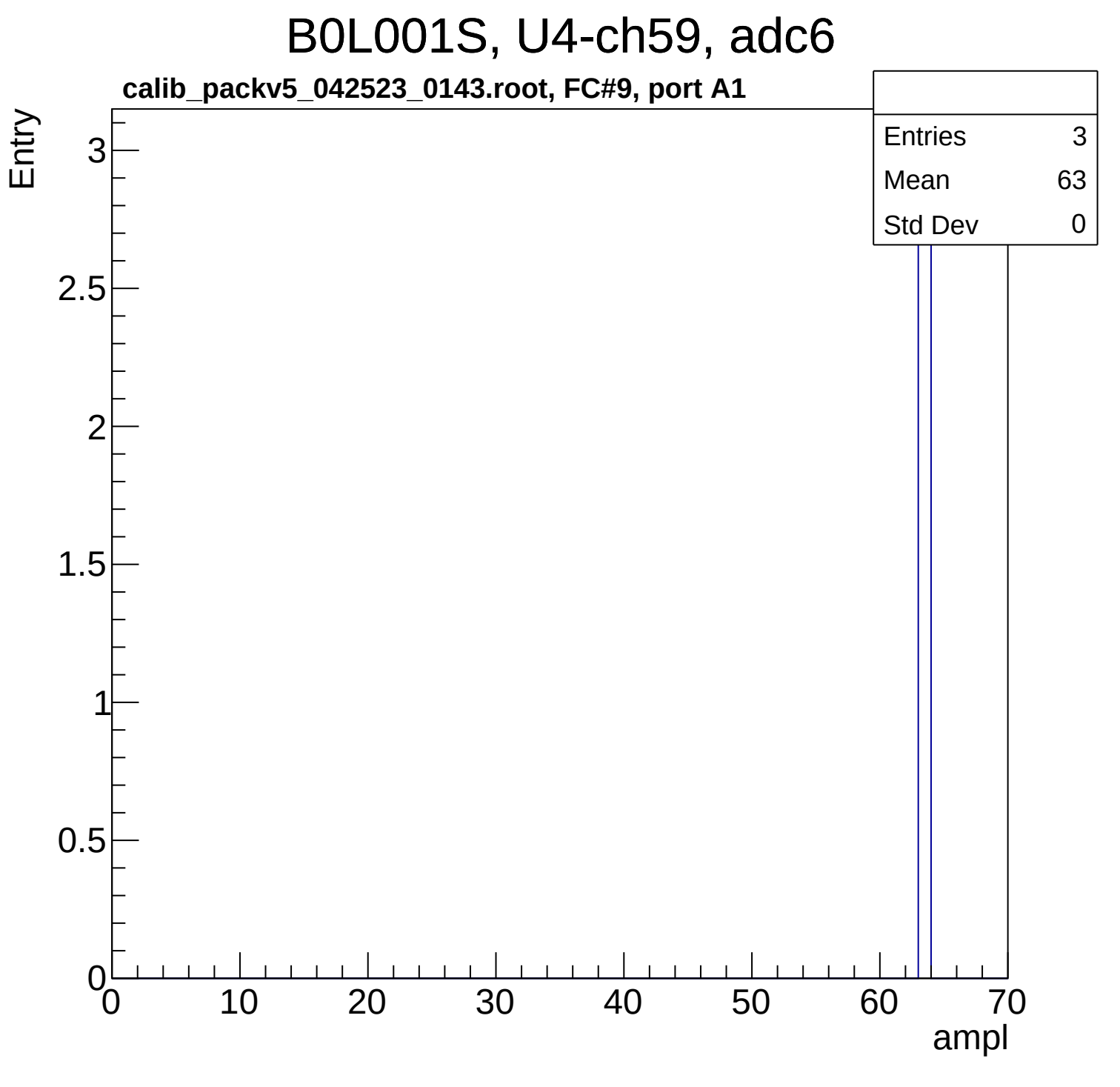
B0L001S, U4-ch59, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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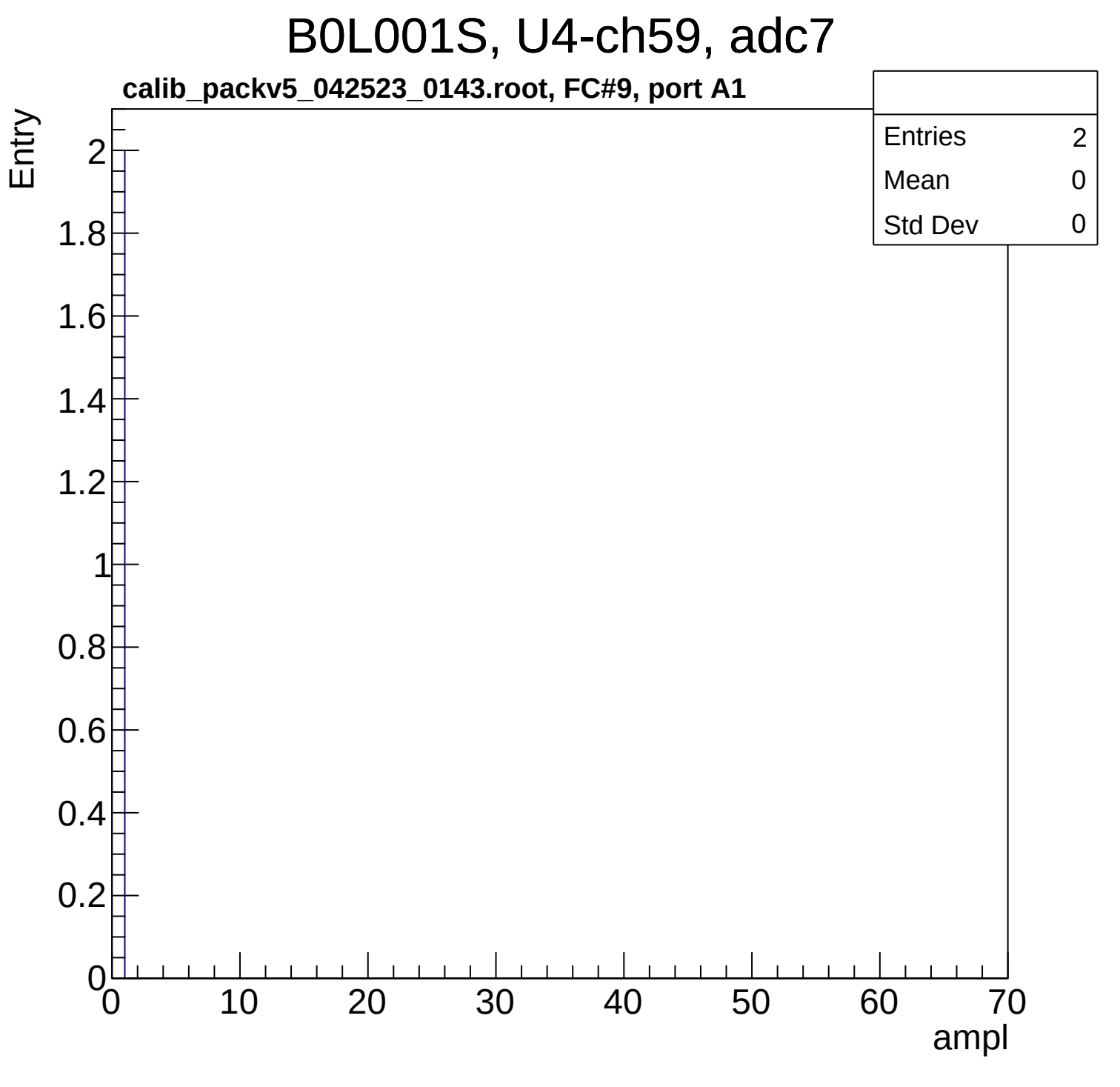
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U4-ch59, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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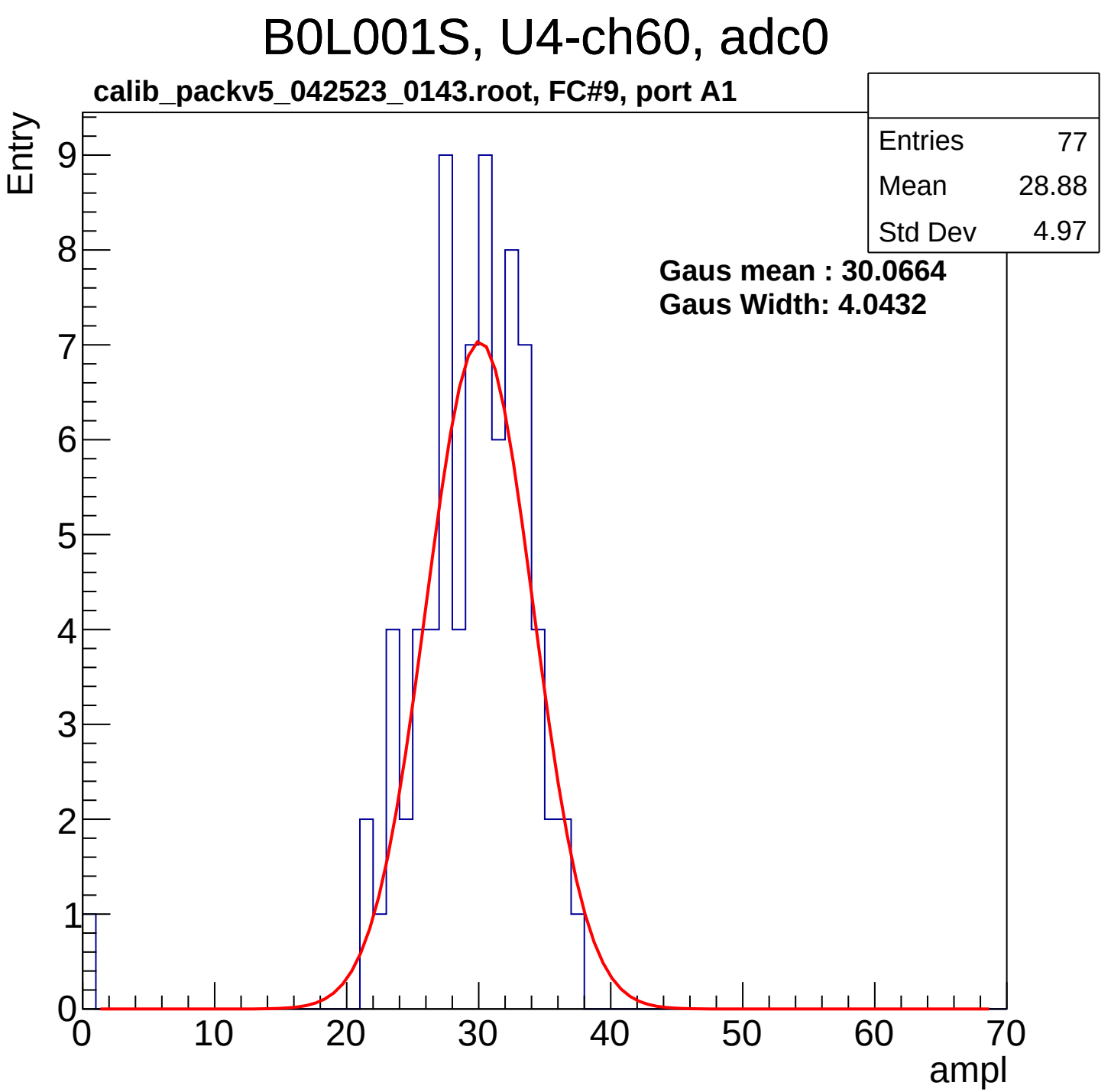
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch60, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 28.88
Std Dev 4.97
Gaus mean : 30.0664
Gaus Width: 4.0432
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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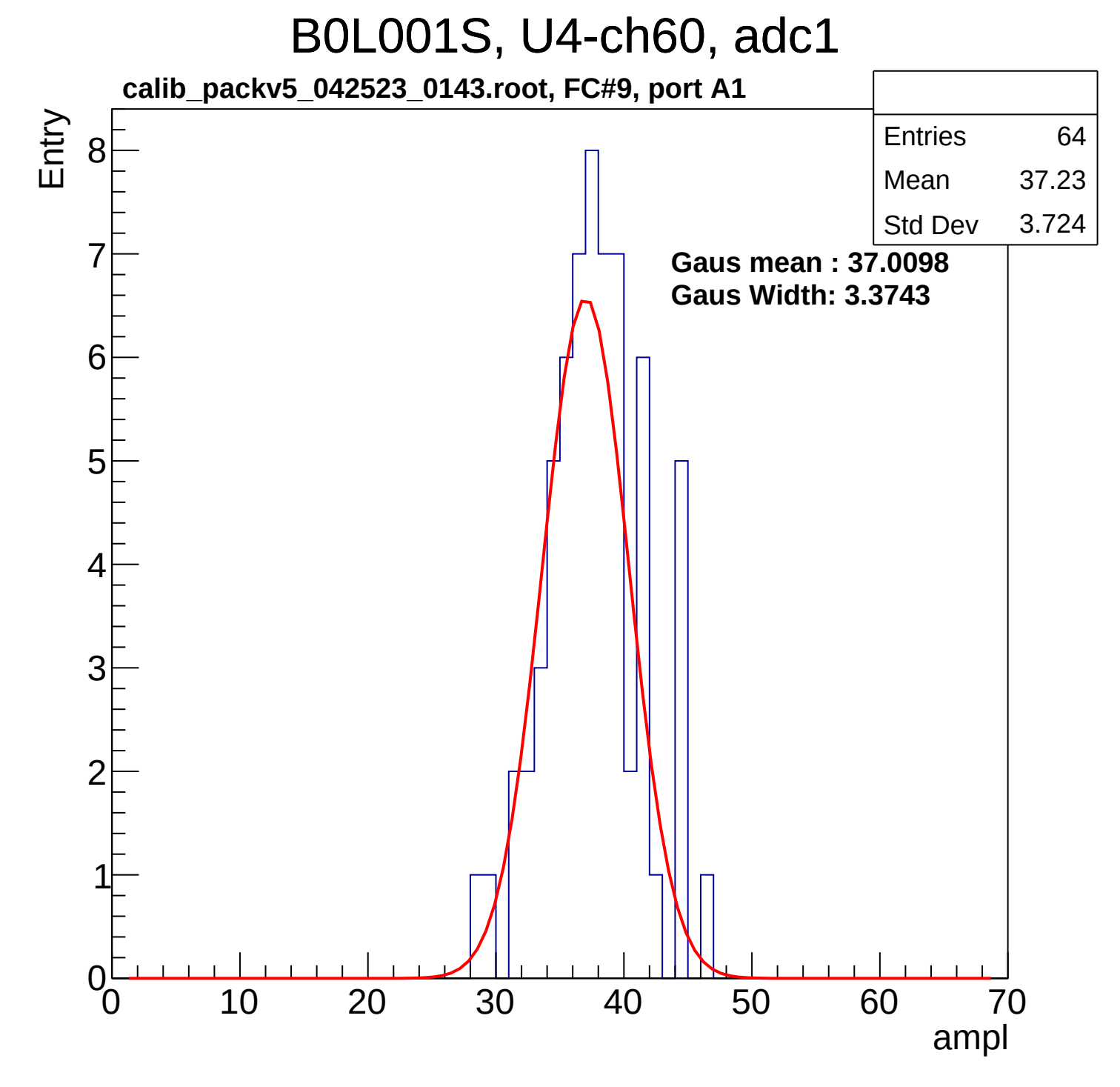
B0L001S, U4-ch60, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.23
Std Dev 3.724
Gaus mean : 37.0098
Gaus Width: 3.3743
Entry
ampl
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2
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5
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7
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0
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40
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60
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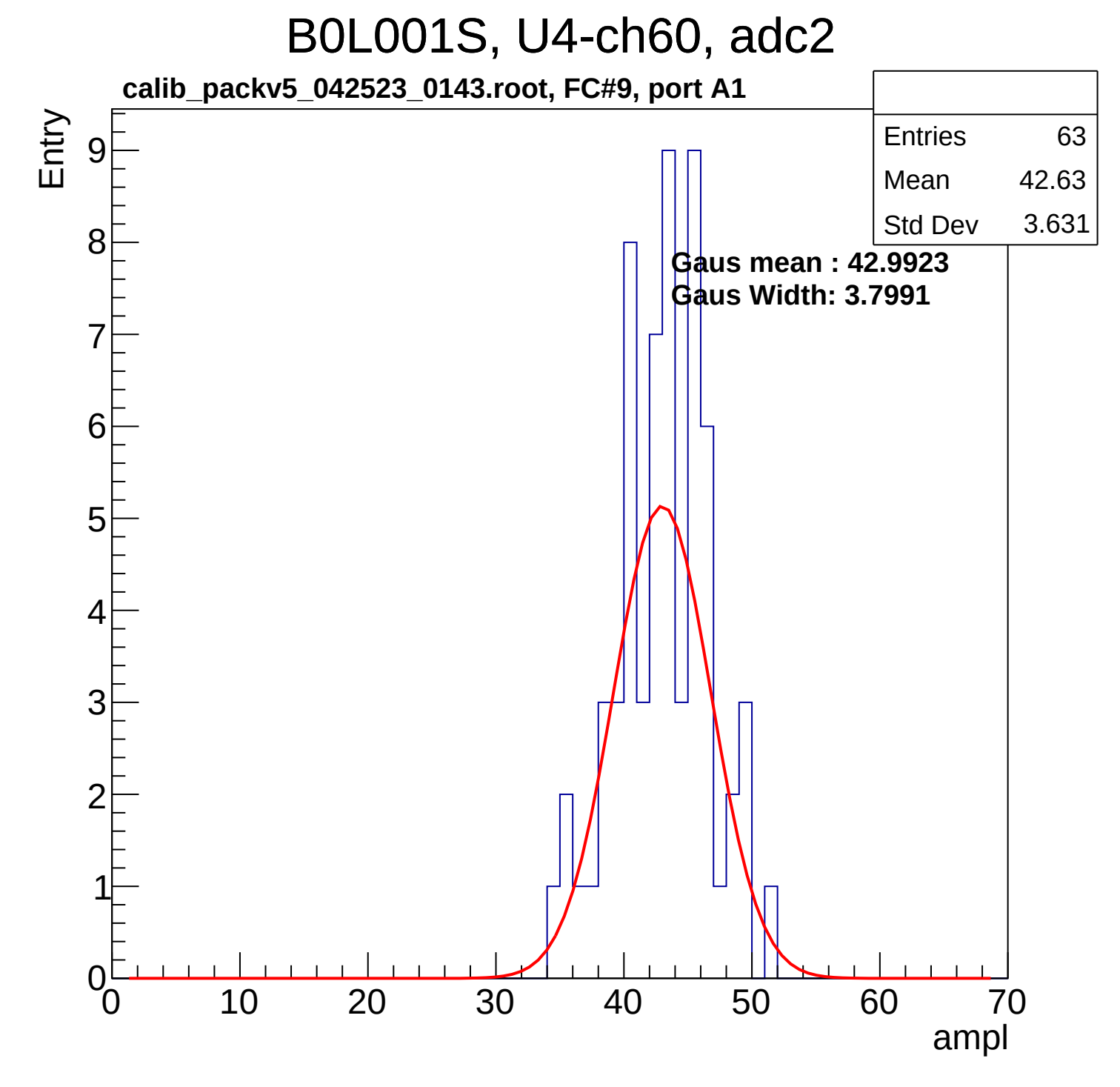
B0L001S, U4-ch60, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 42.63
Std Dev 3.631
Gaus mean : 42.9923
Gaus Width: 3.7991
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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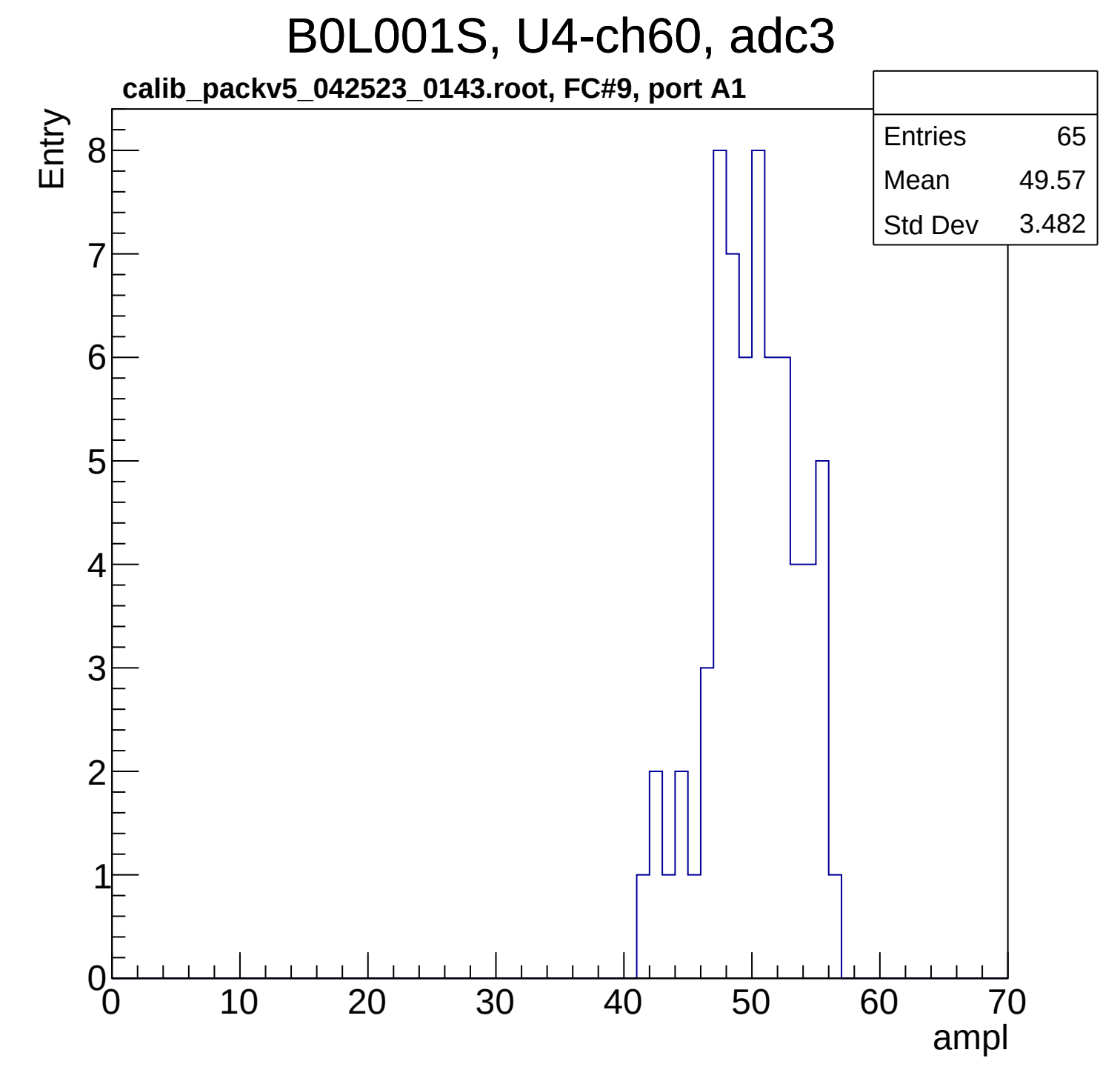
60
70


B0L001S, U4-ch60, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.57
Std Dev 3.482
Entry
ampl
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3
4
5
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7
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0
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60
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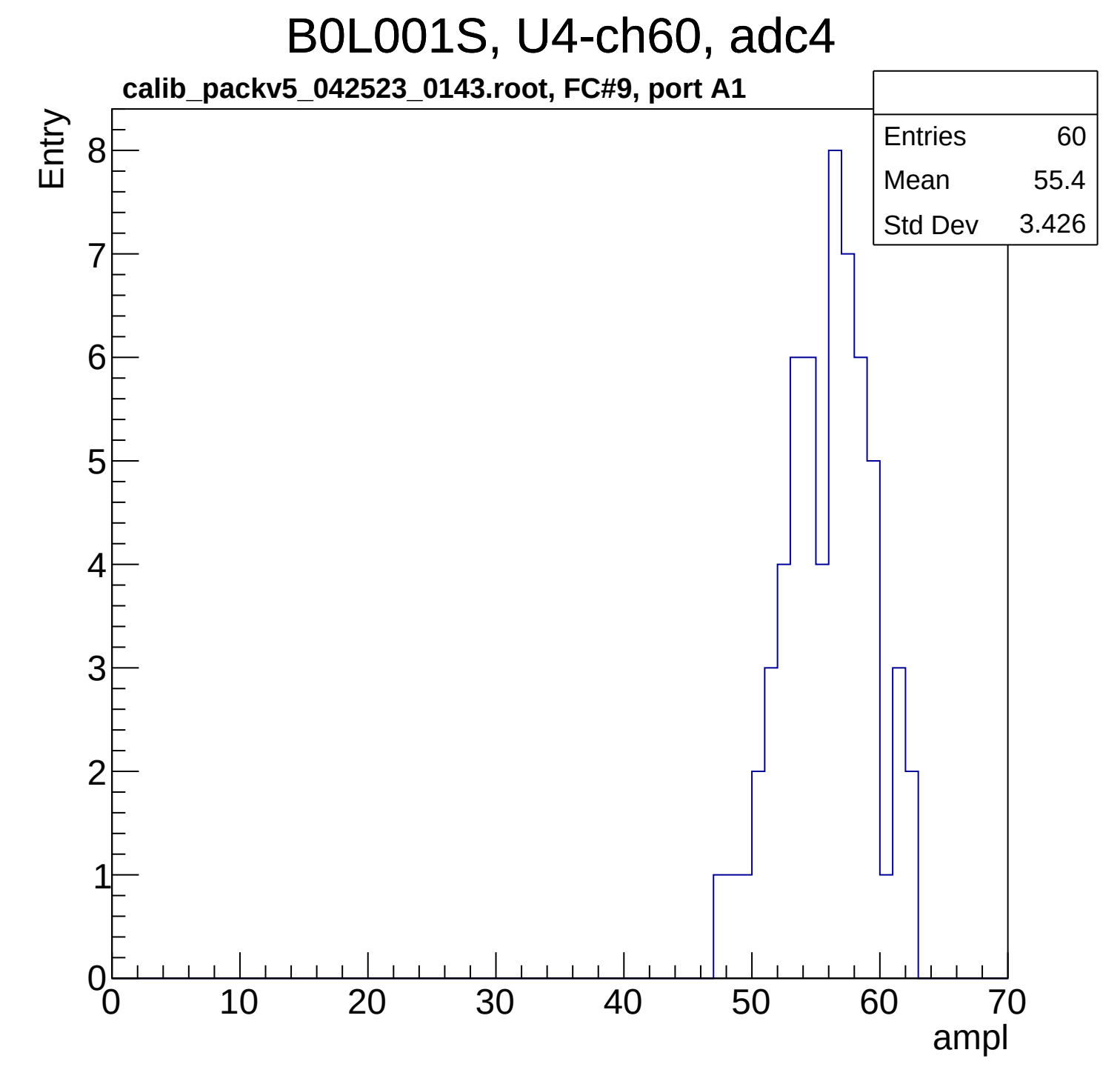
B0L001S, U4-ch60, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 55.4
Std Dev 3.426
Entry
ampl
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4
5
6
7
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0
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40
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60
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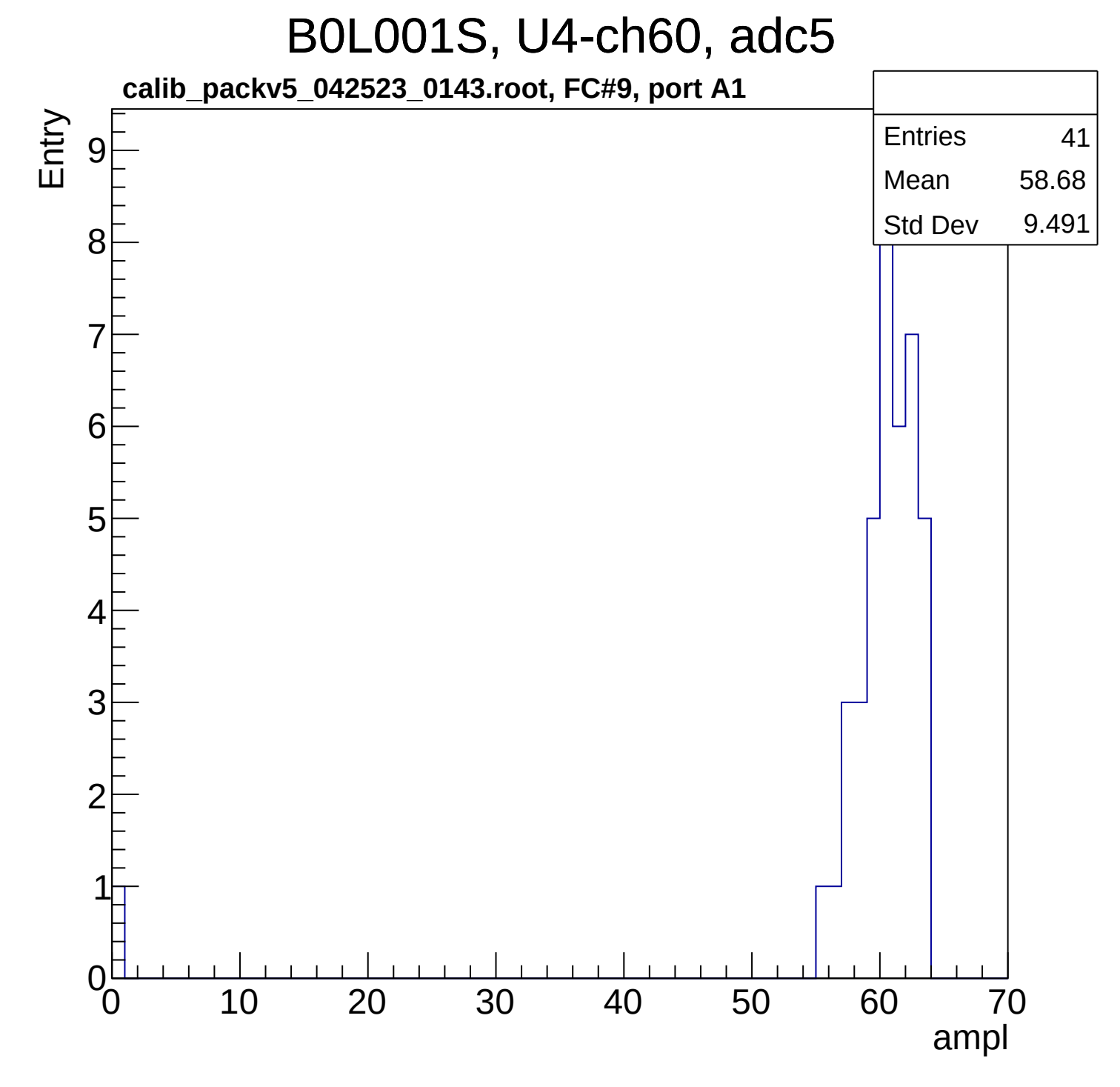
B0L001S, U4-ch60, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.68
Std Dev 9.491
Entry
ampl
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3
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5
6
7
8
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0
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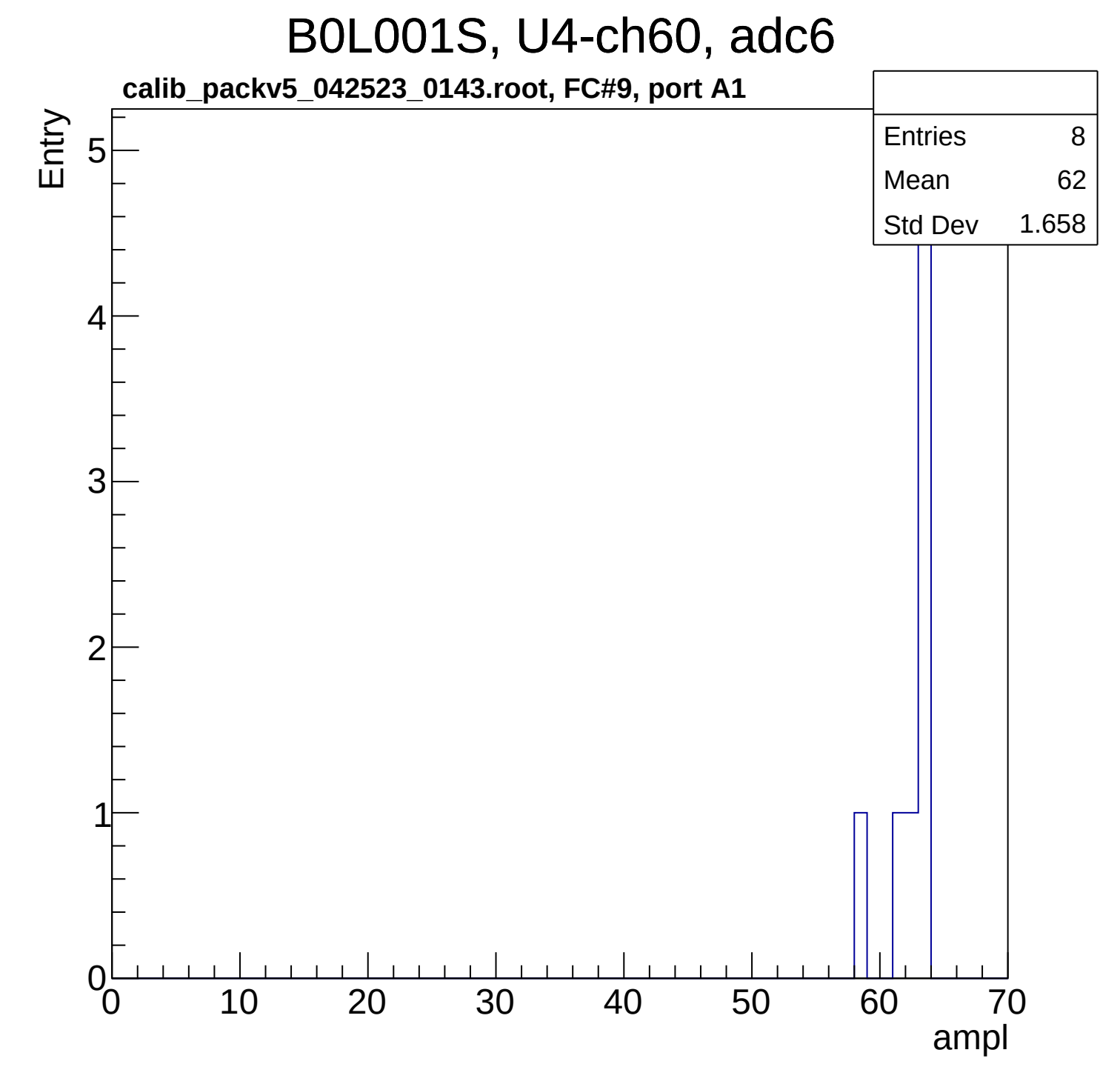
B0L001S, U4-ch60, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62
Std Dev 1.658
Entry
0
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2
3
4
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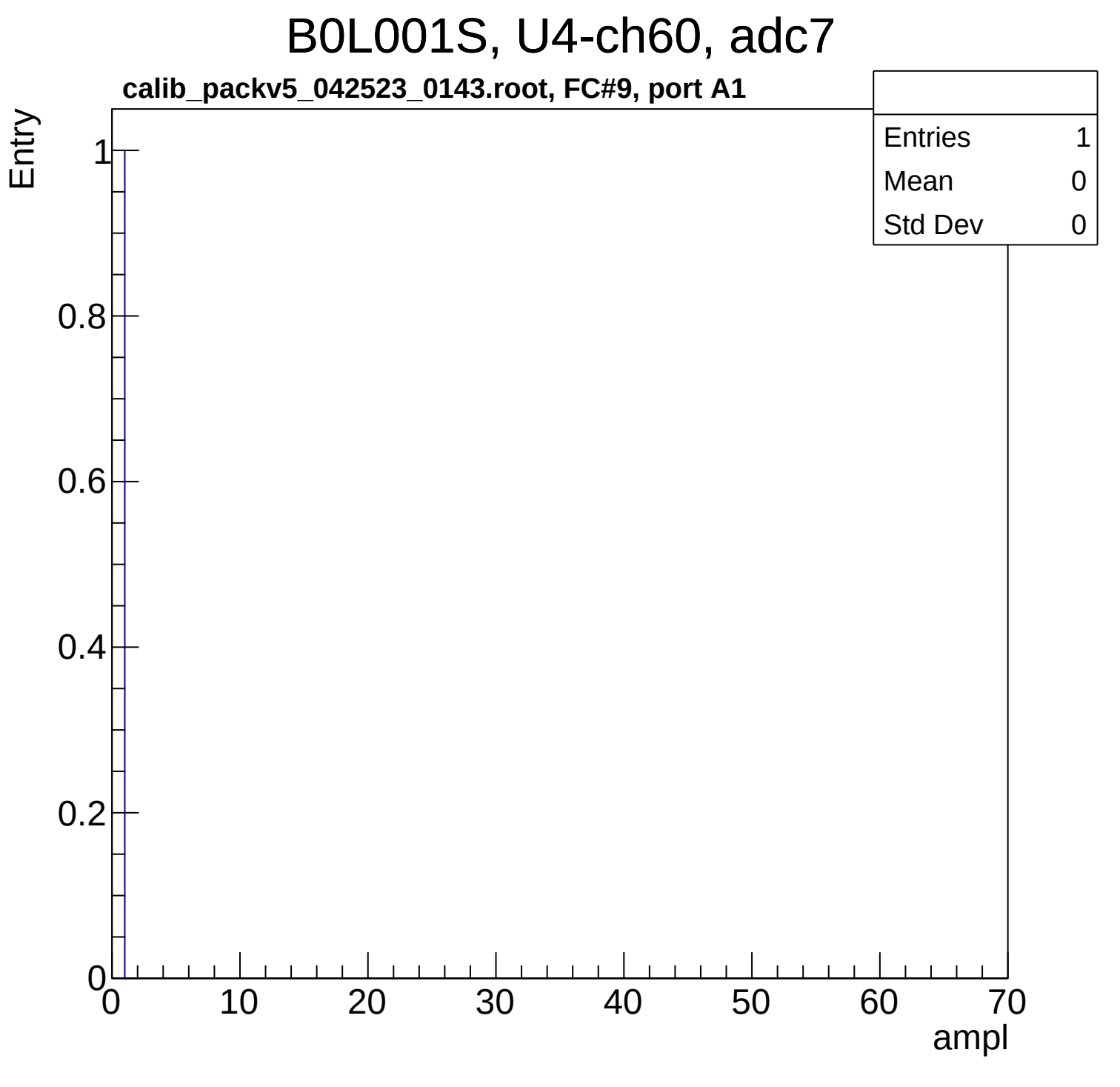
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch60, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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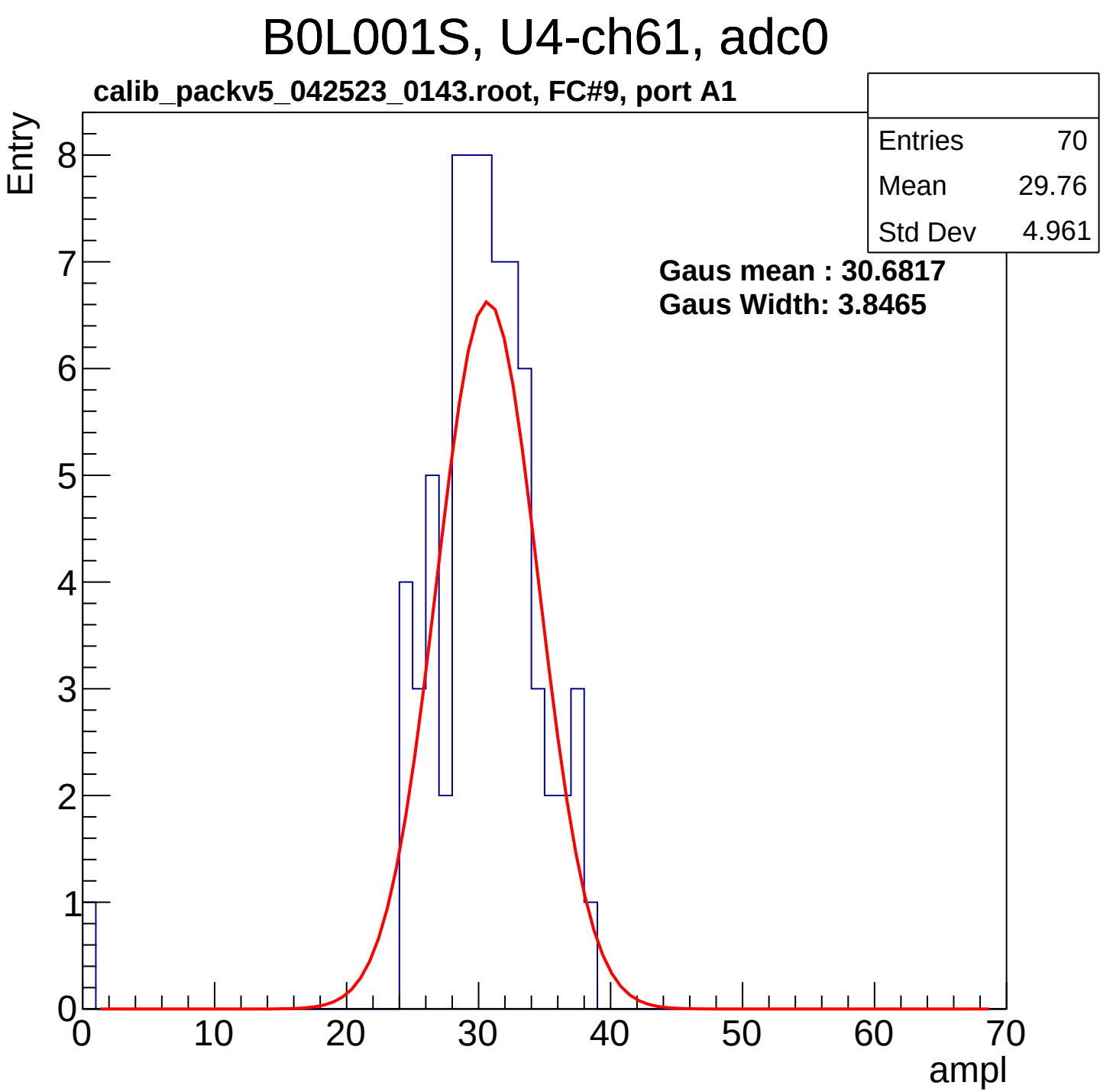
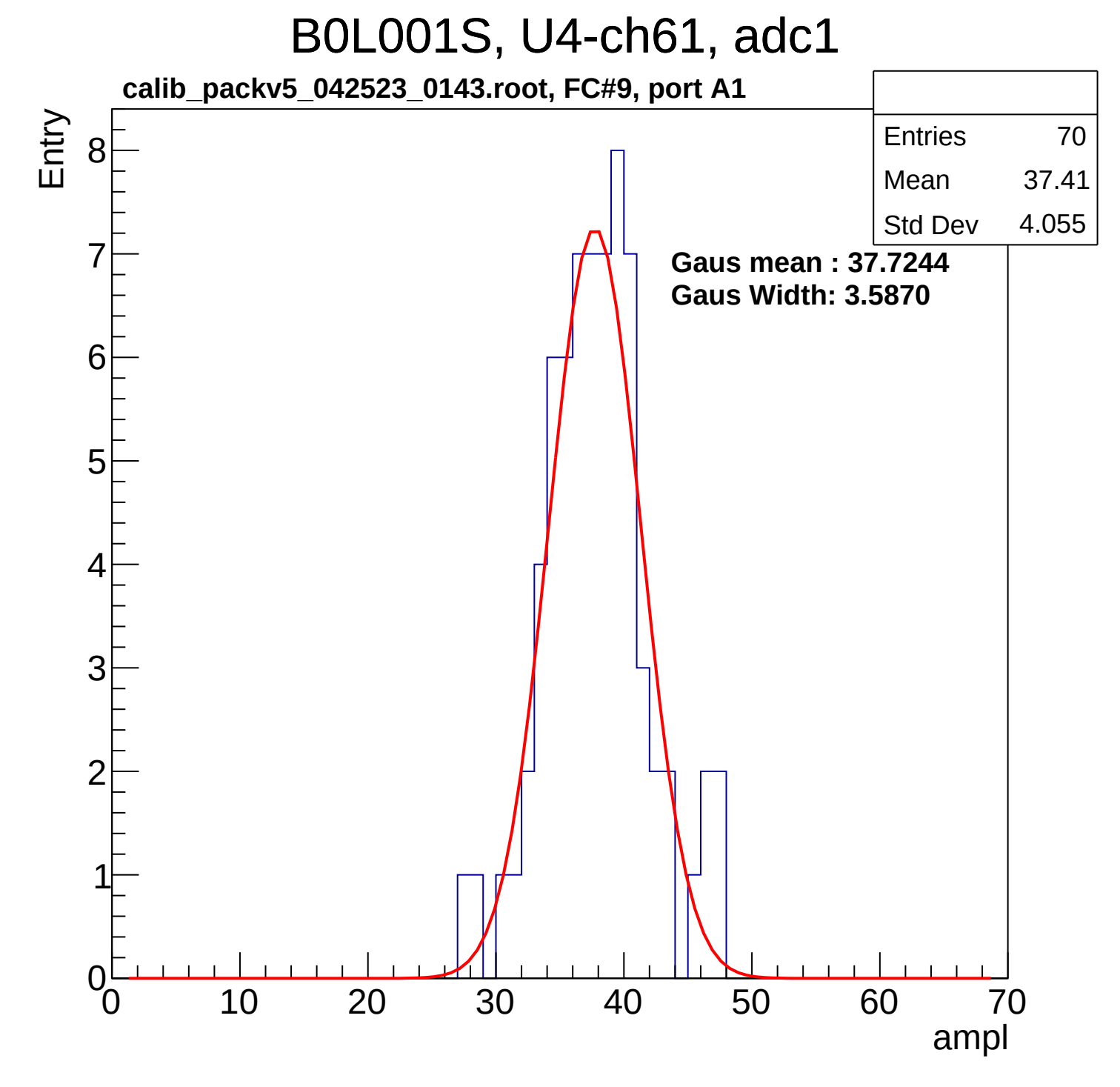
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch61, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.76
Std Dev 4.961
Gaus mean : 30.6817
Gaus Width: 3.8465
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch61, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.41
Std Dev 4.055
Gaus mean : 37.7244
Gaus Width: 3.5870
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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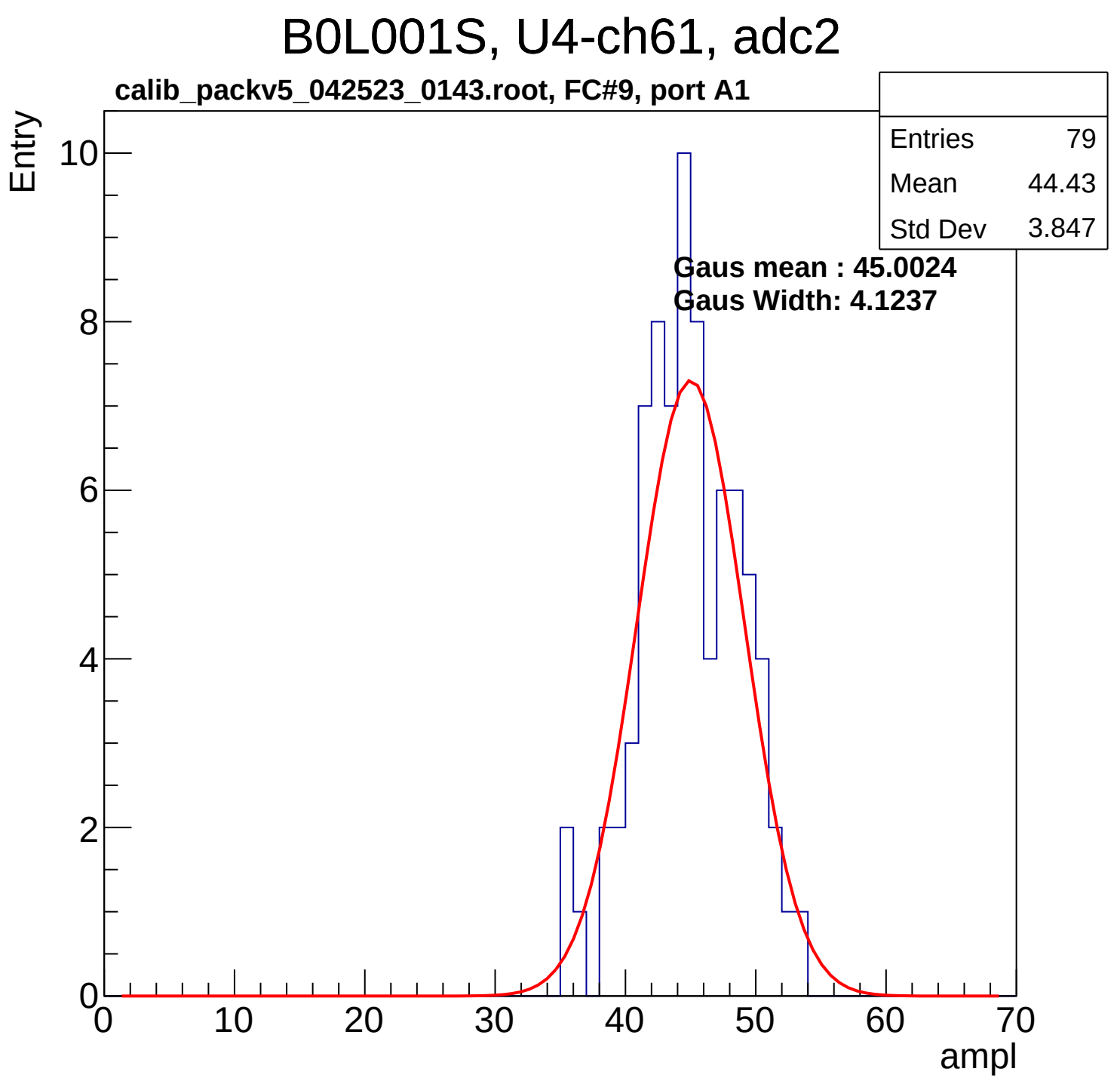
B0L001S, U4-ch61, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 44.43
Std Dev 3.847
Gaus mean : 45.0024
Gaus Width: 4.1237
Entry
ampl
0
2
4
6
8
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0
10
20
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50
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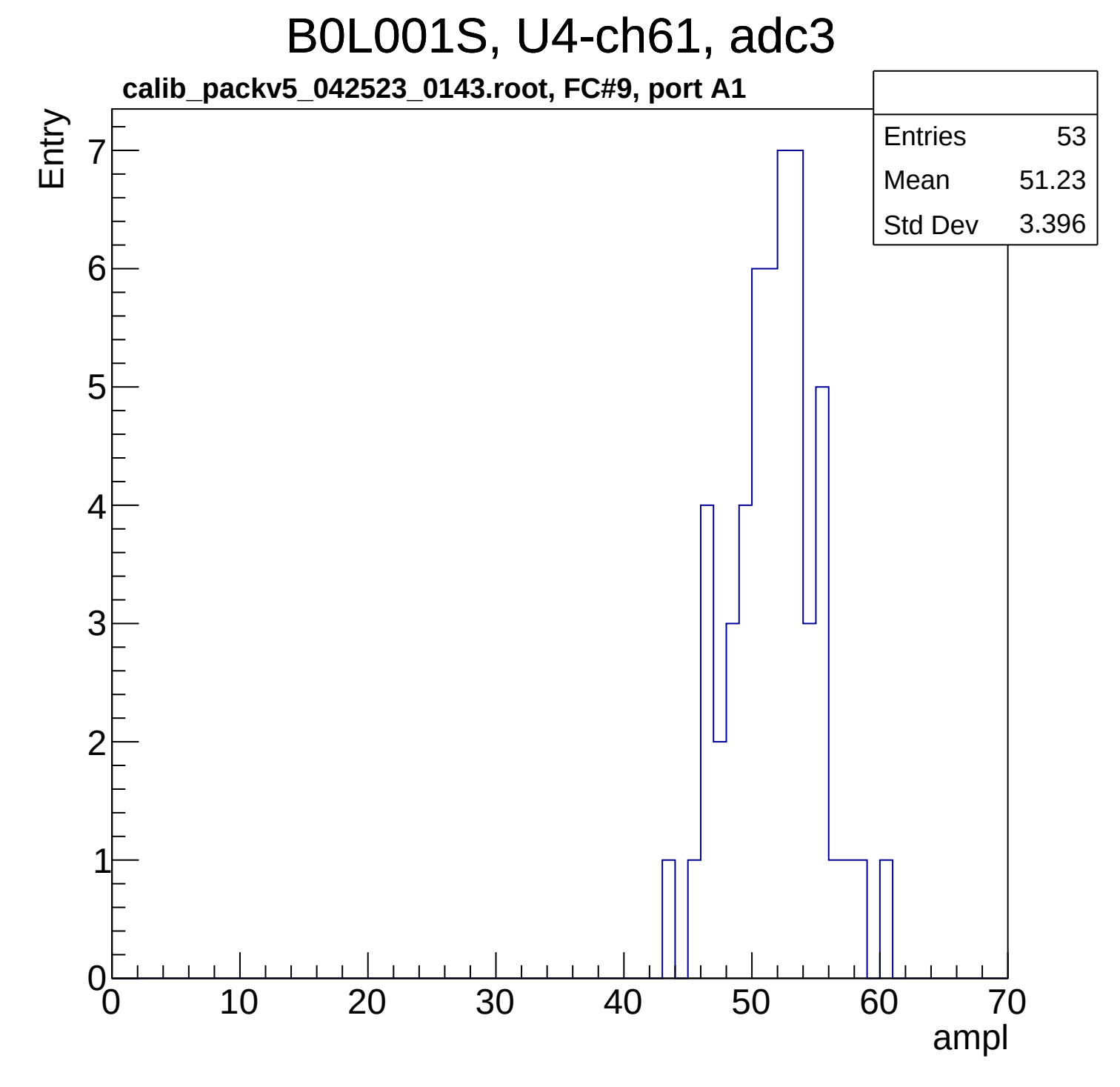
70


B0L001S, U4-ch61, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 51.23
Std Dev 3.396
Entry
ampl
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4
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7
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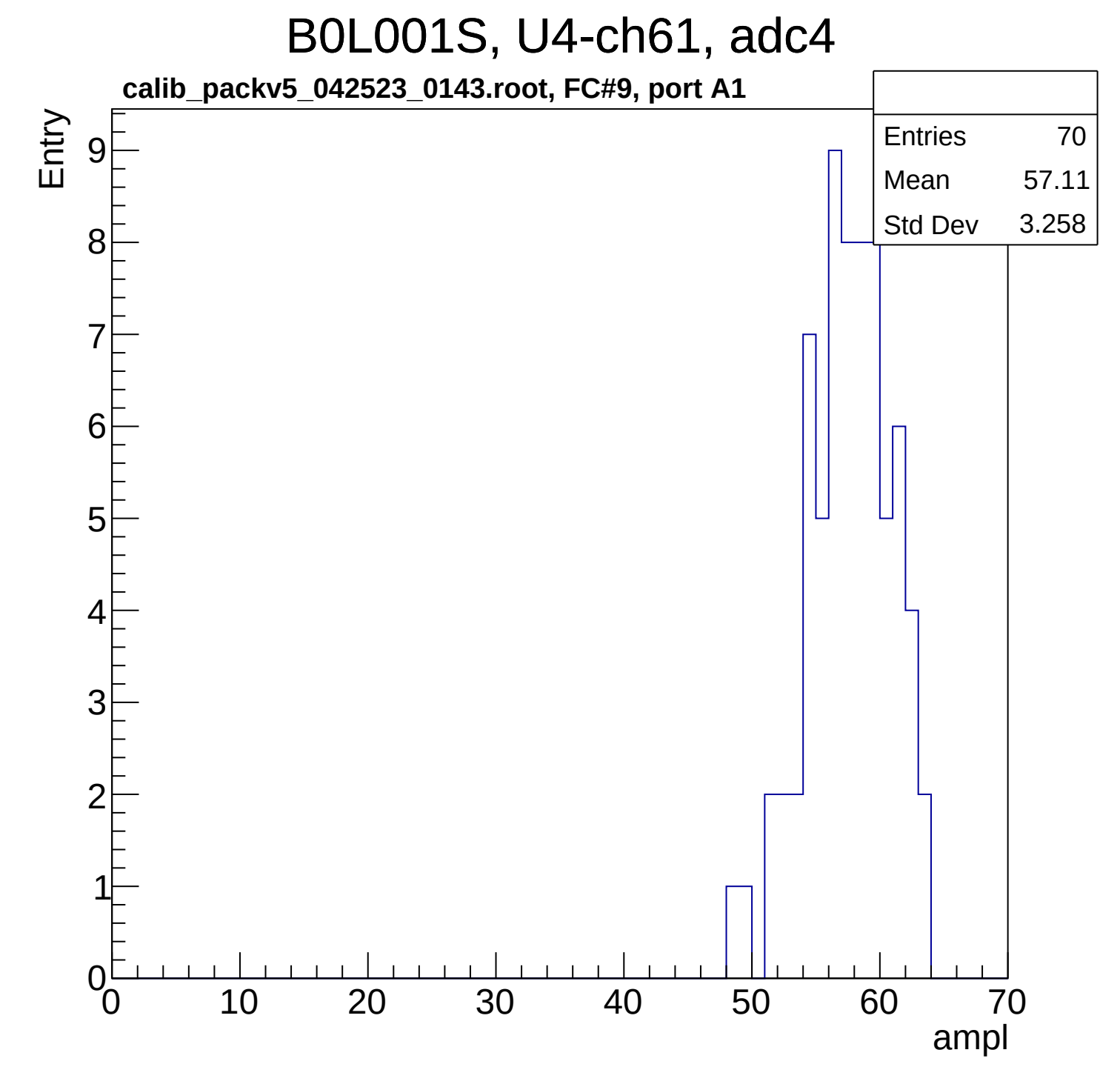
60
70


B0L001S, U4-ch61, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 57.11
Std Dev 3.258
Entry
ampl
0
1
2
3
4
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6
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8
9
0
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40
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60
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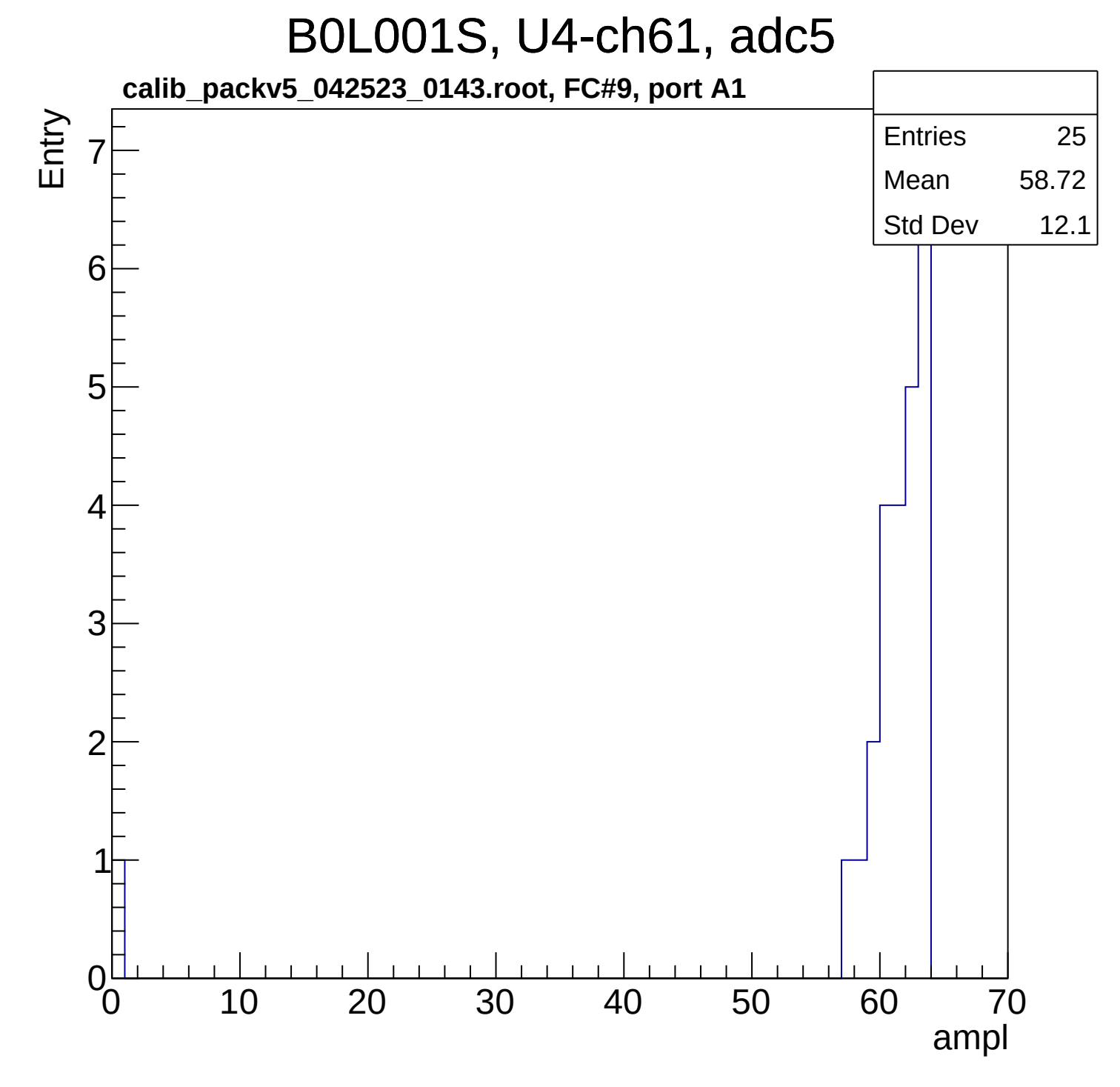
B0L001S, U4-ch61, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.72
Std Dev 12.1
Entry
ampl
0
1
2
3
4
5
6
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0
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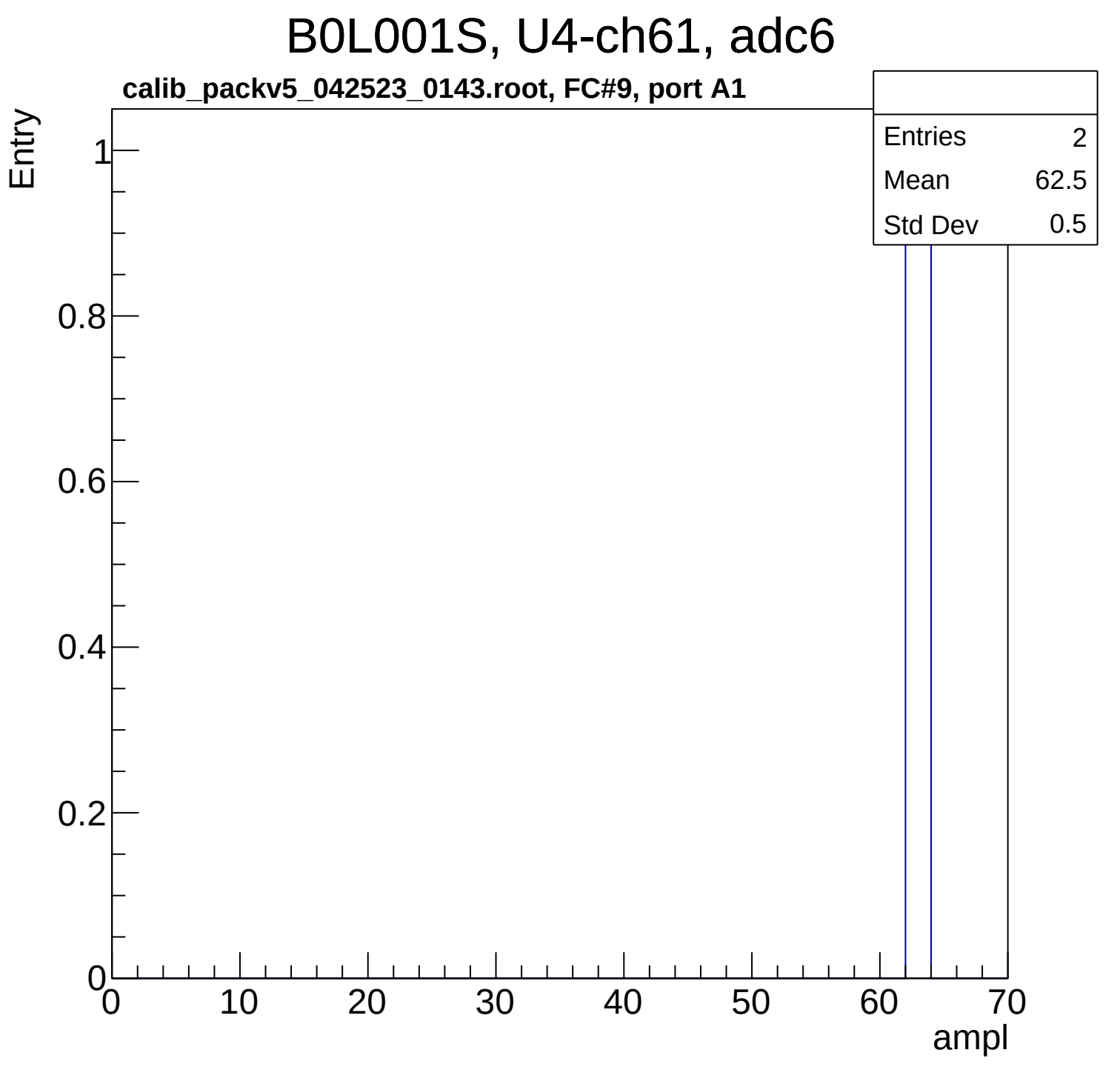
B0L001S, U4-ch61, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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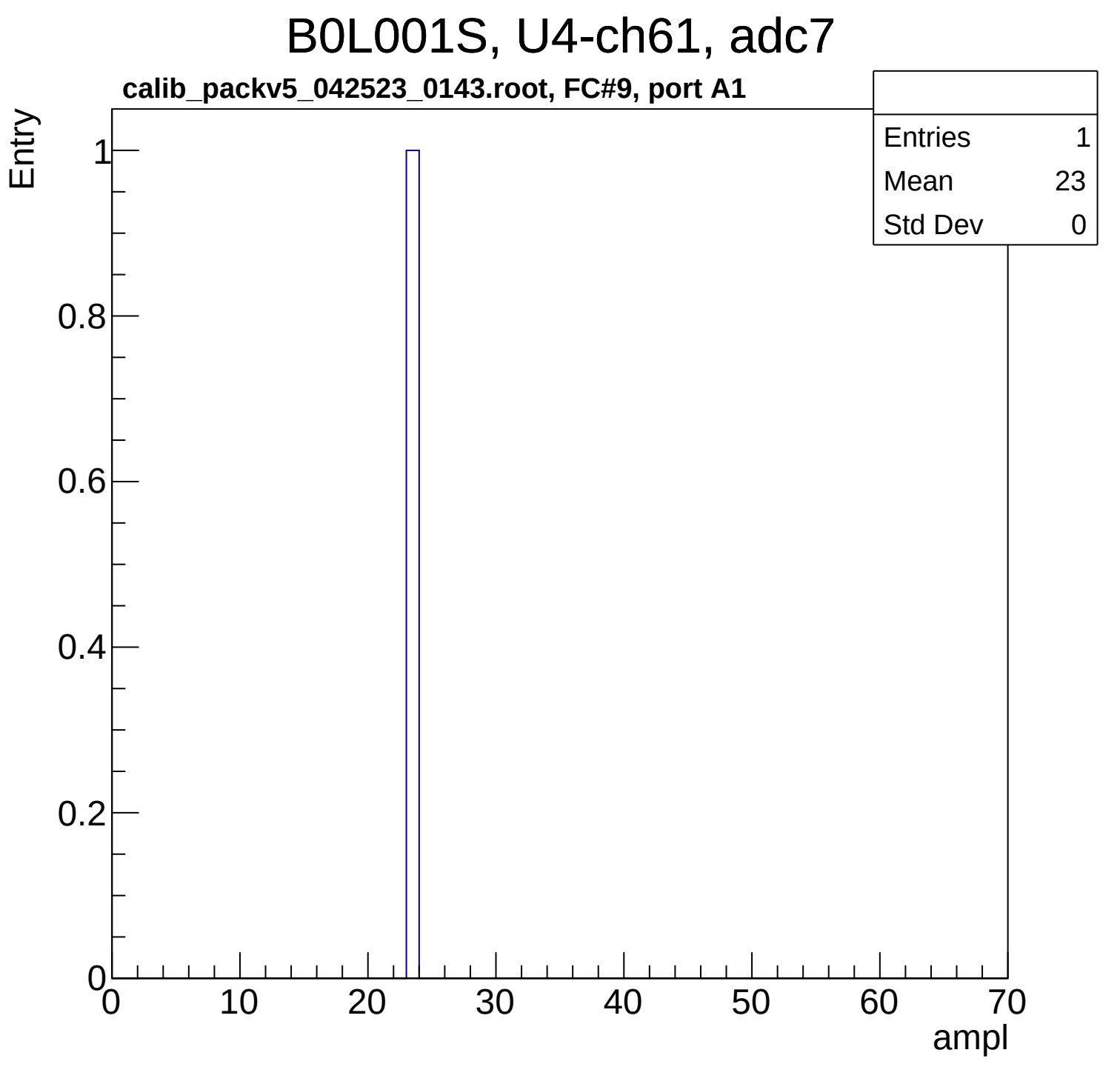
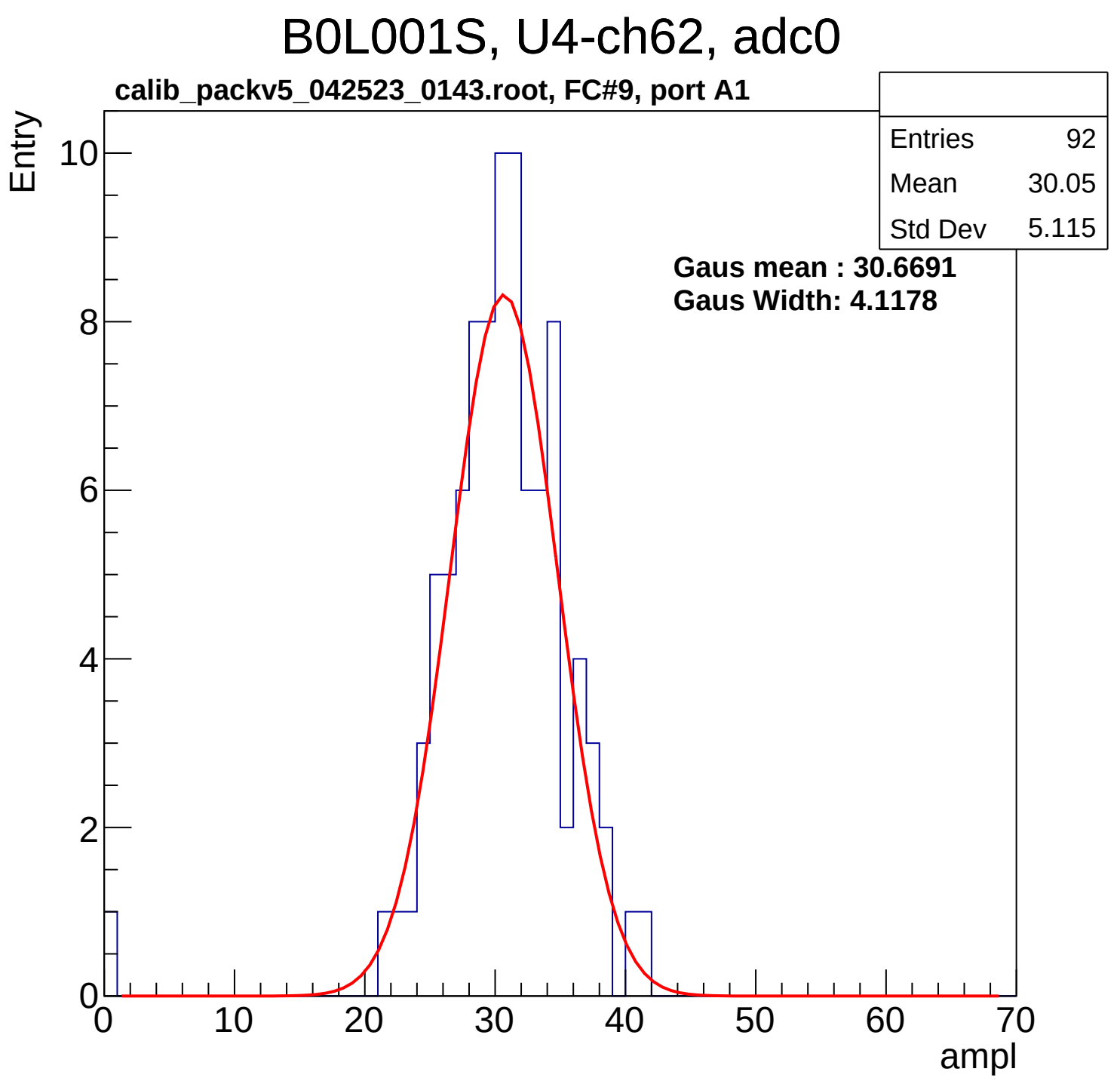
50
60
70
ampl


B0L001S, U4-ch61, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch62, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 92
Mean 30.05
Std Dev 5.115
Gaus mean : 30.6691
Gaus Width: 4.1178
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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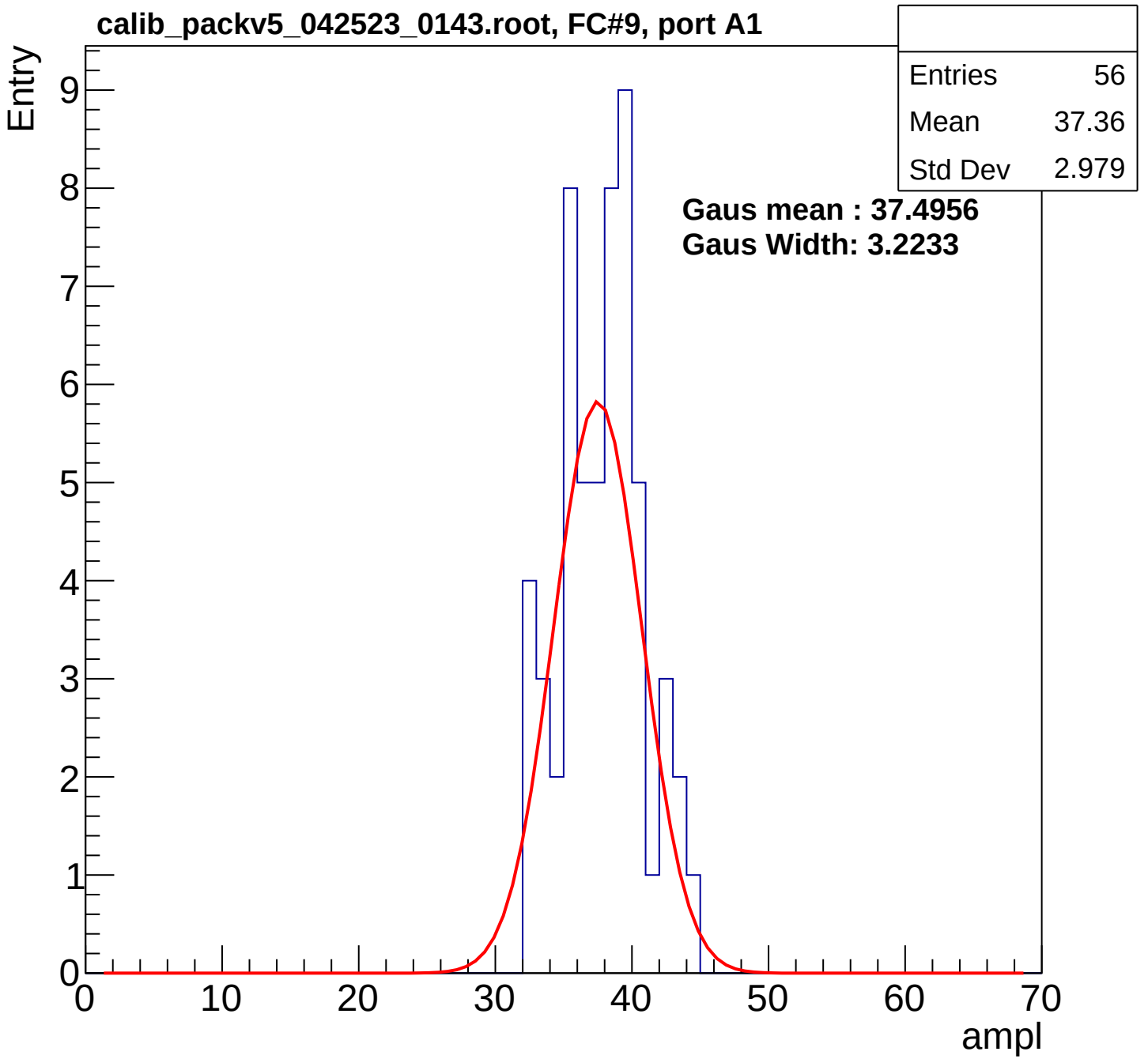
B0L001S, U4-ch62, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 37.36
Std Dev 2.979
Gaus mean : 37.4956
Gaus Width: 3.2233
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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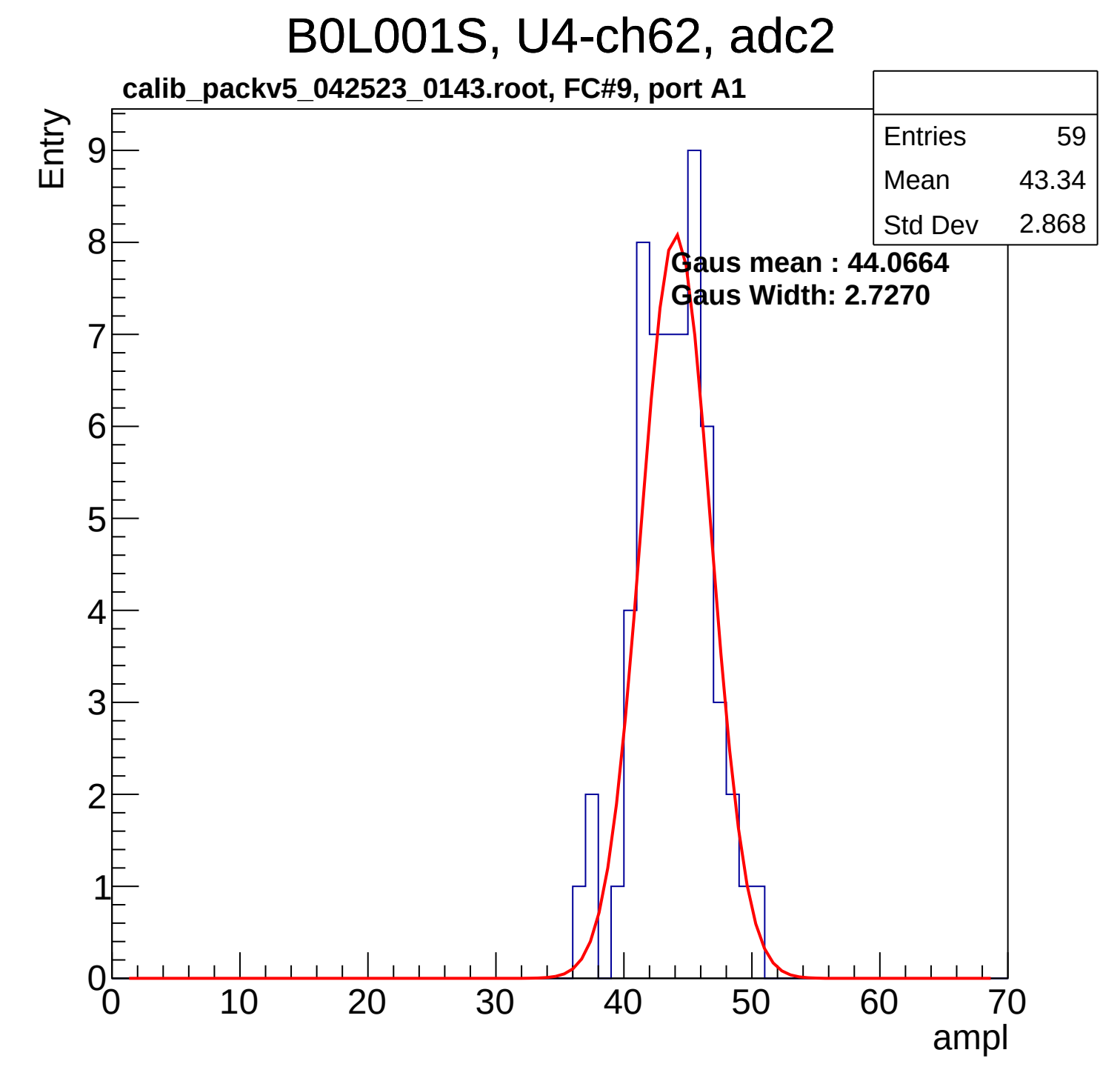
B0L001S, U4-ch62, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.34
Std Dev 2.868
Gaus mean : 44.0664
Gaus Width: 2.7270
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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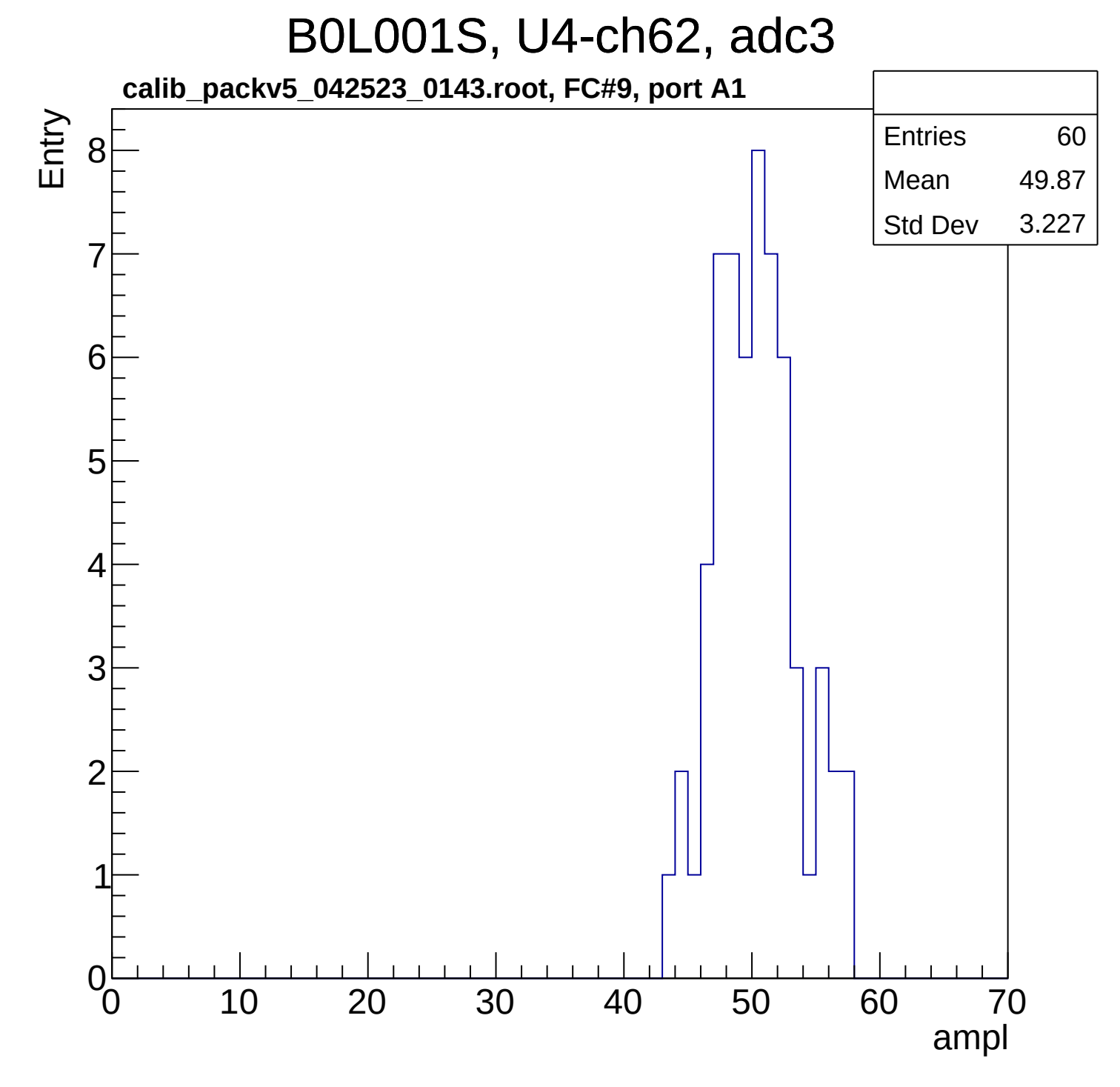
B0L001S, U4-ch62, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 49.87
Std Dev 3.227
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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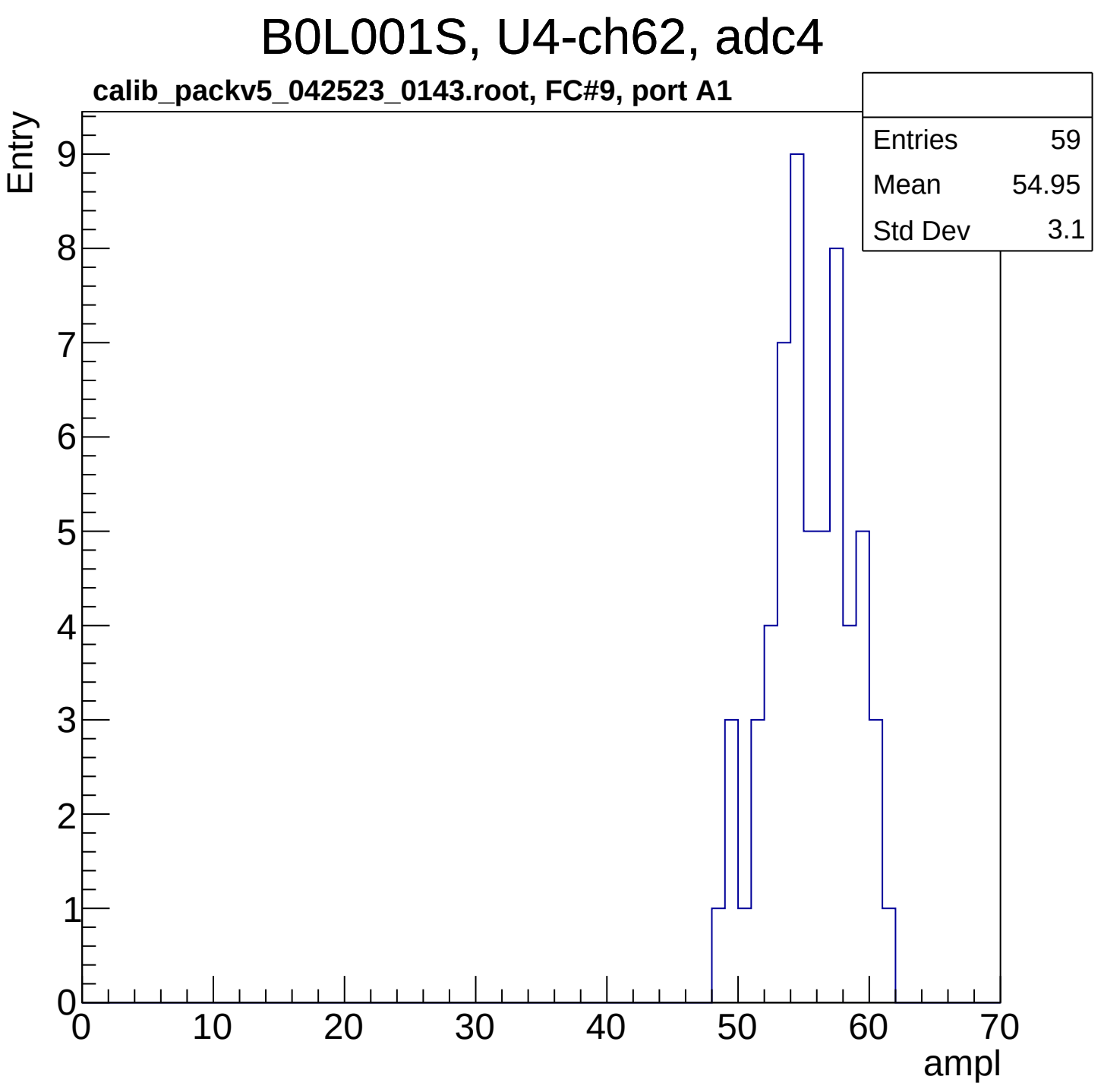
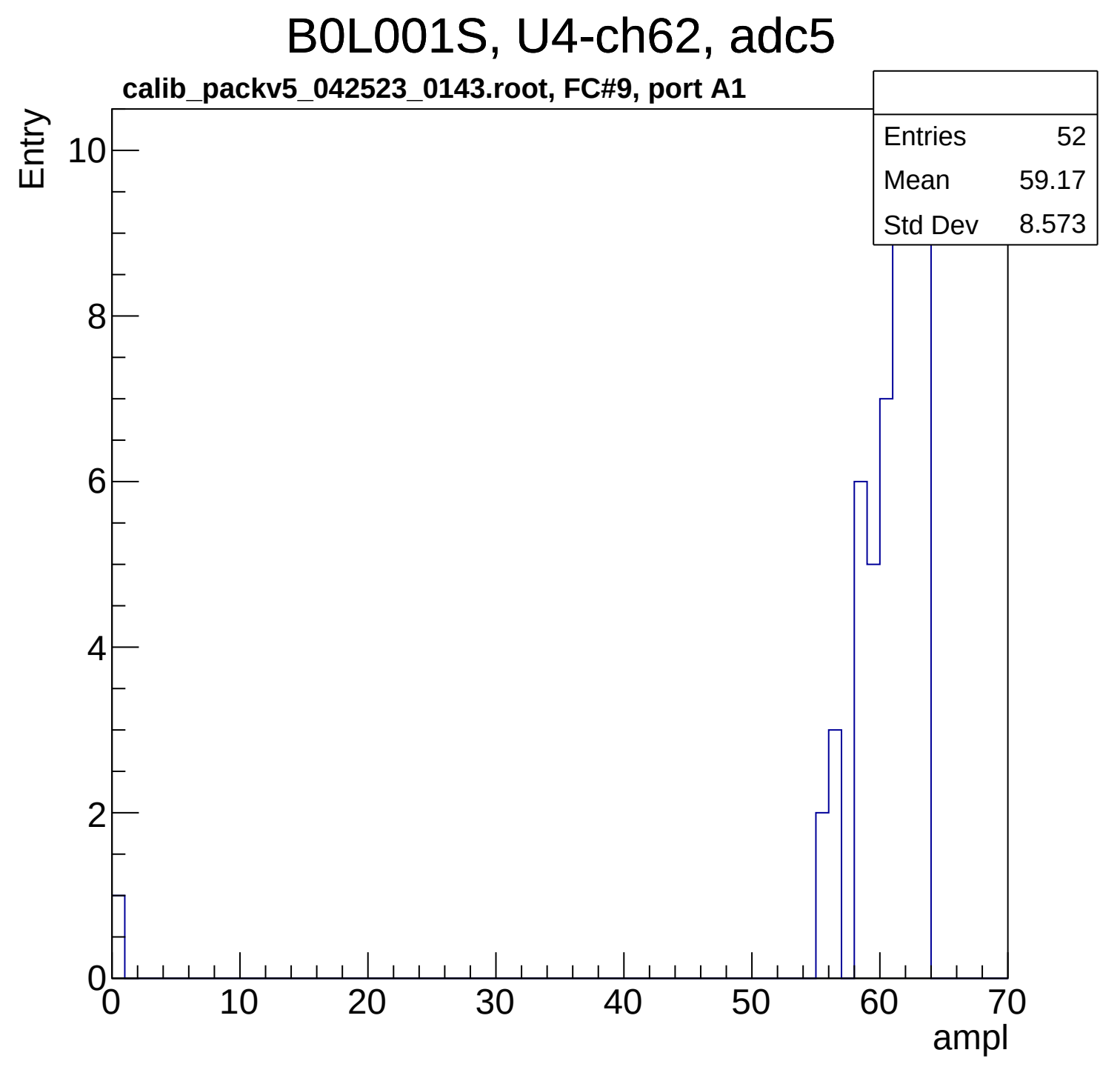
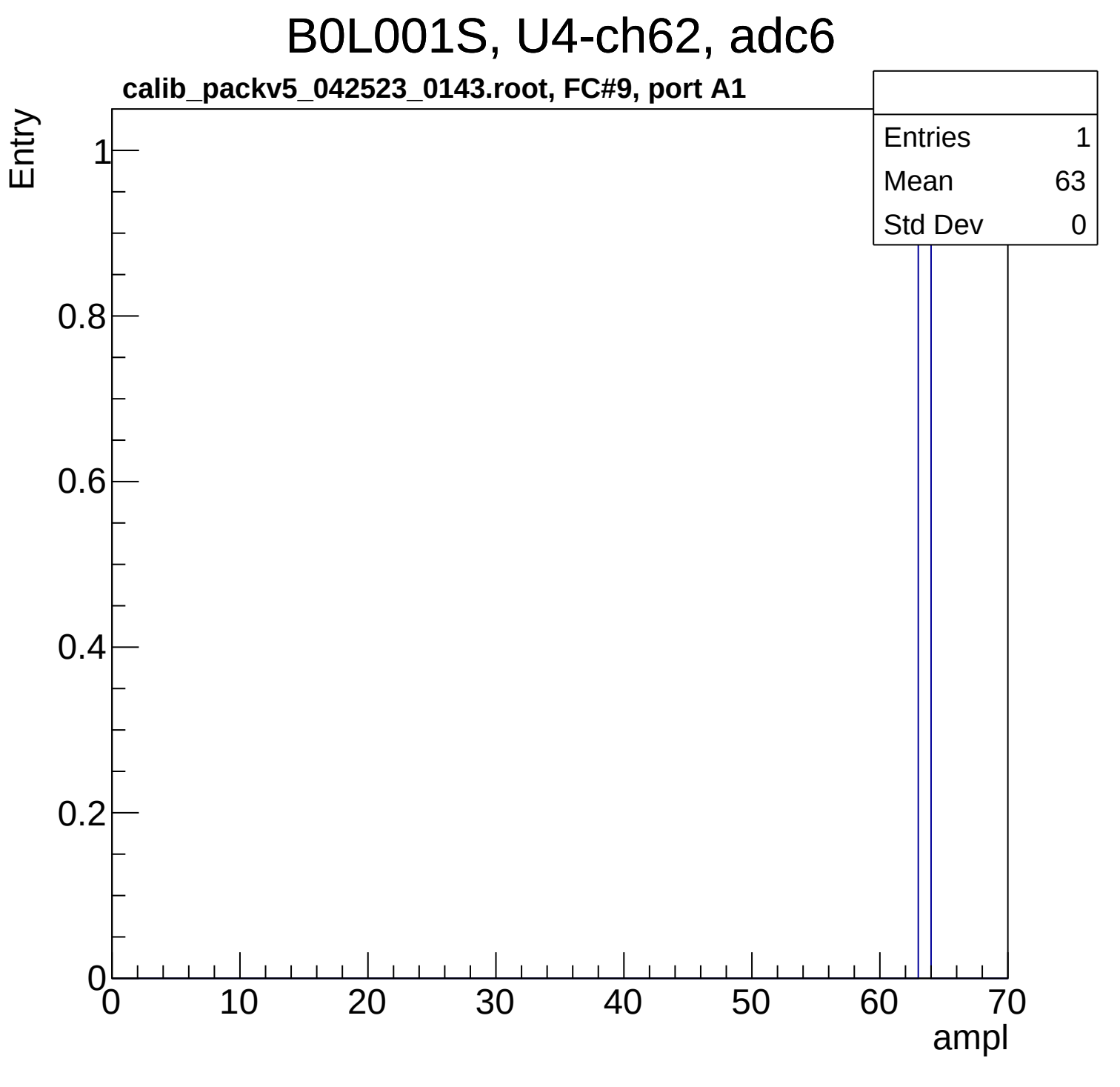
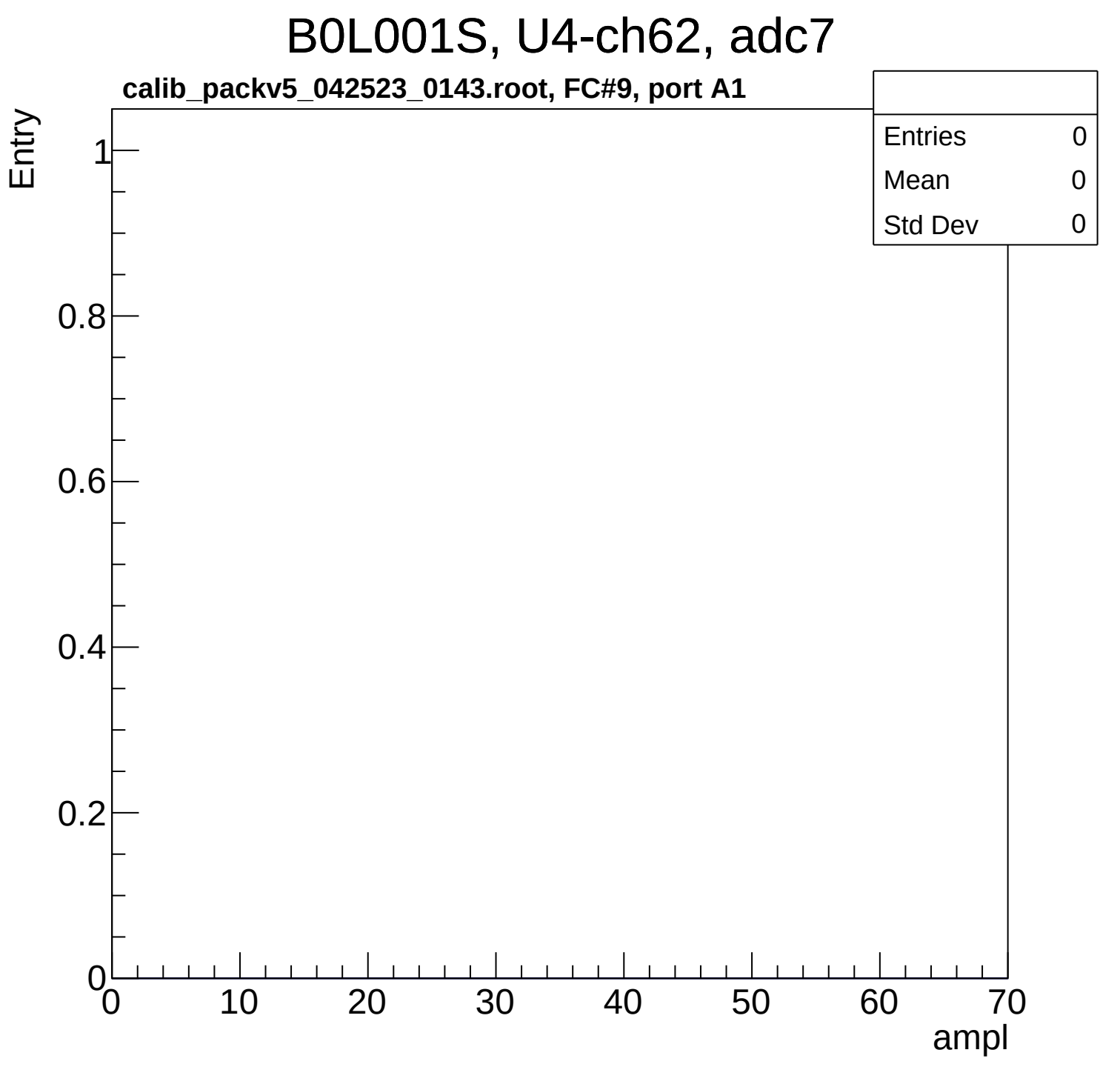
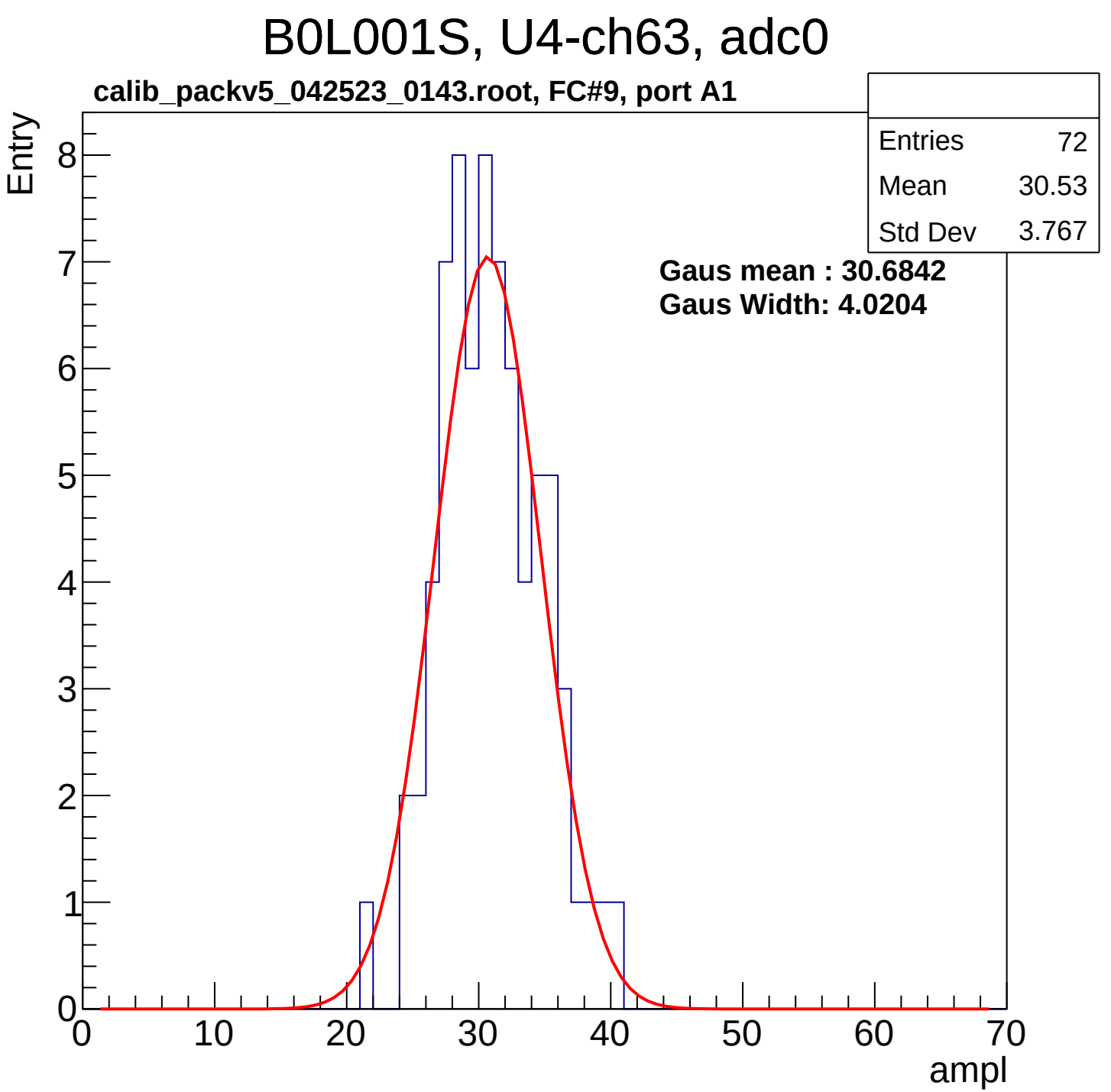
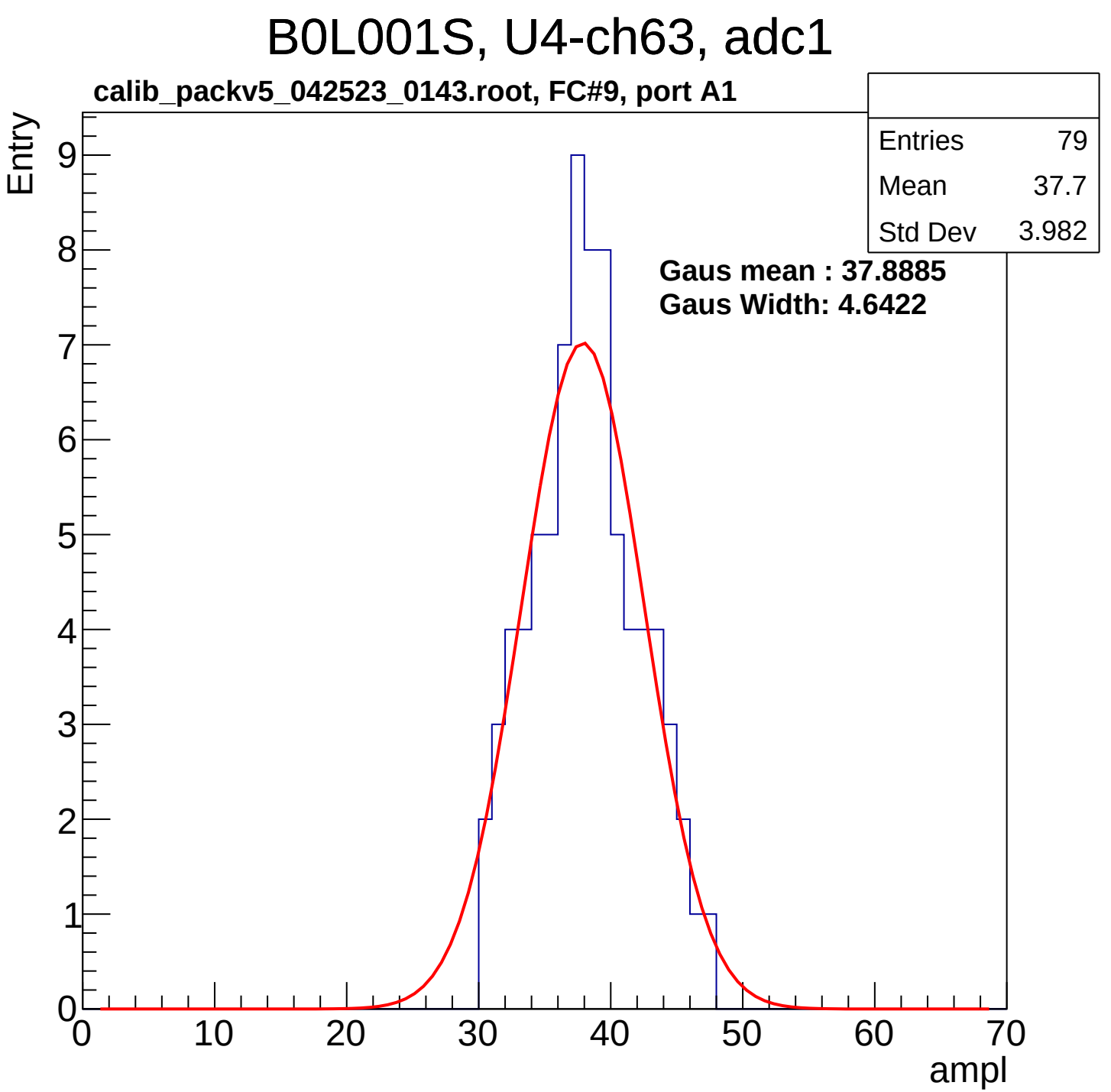

B0L001S, U4-ch62, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 54.95
Std Dev 3.1
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

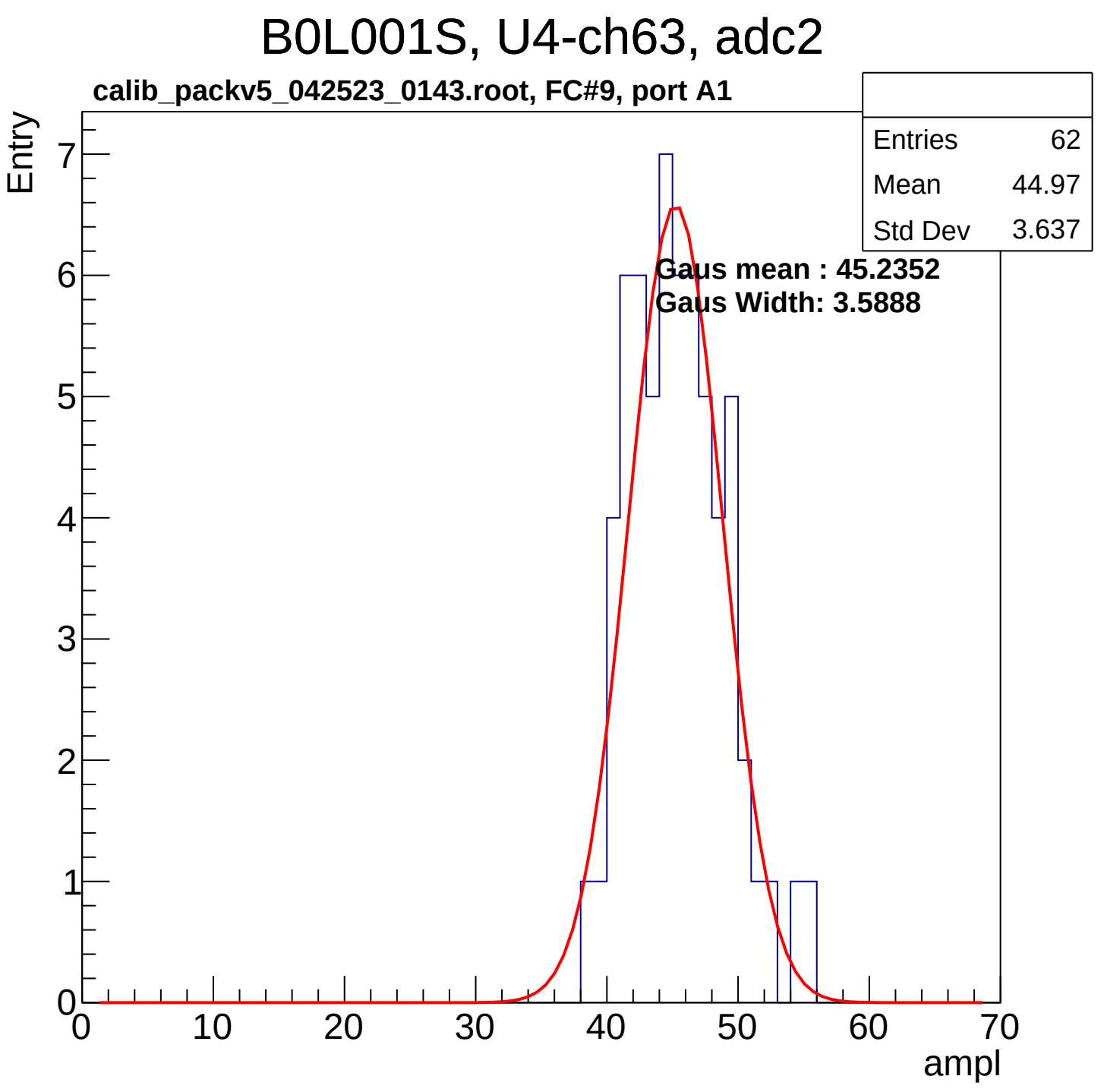

B0L001S, U4-ch62, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 59.17
Std Dev 8.573
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

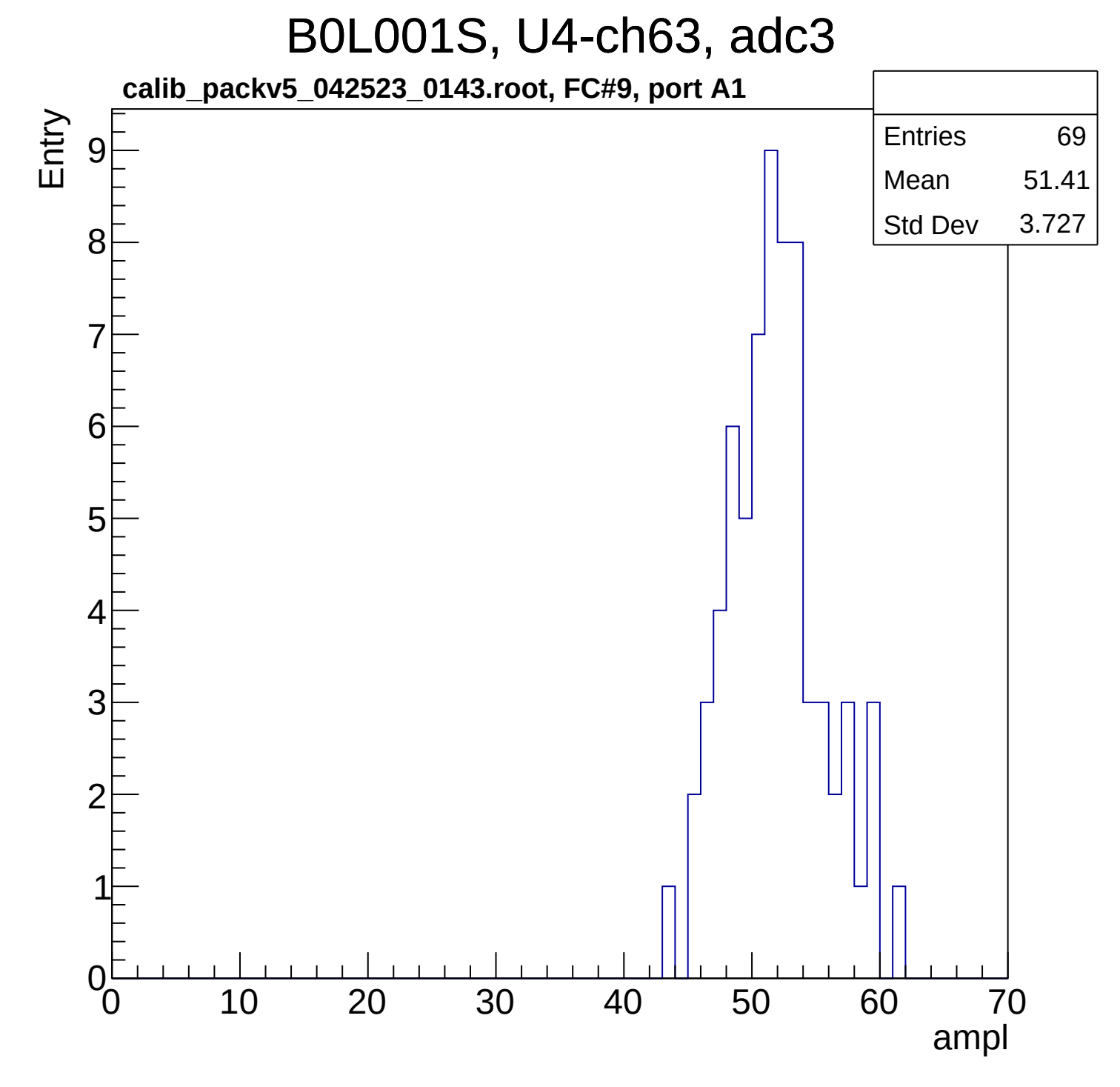

B0L001S, U4-ch62, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch62, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch63, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.53
Std Dev 3.767
Gaus mean : 30.6842
Gaus Width: 4.0204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch63, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.7
Std Dev 3.982
Gaus mean : 37.8885
Gaus Width: 4.6422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch63, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.97
Std Dev 3.637
Gaus mean : 45.2352
Gaus Width: 3.5888
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch63, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.41
Std Dev 3.727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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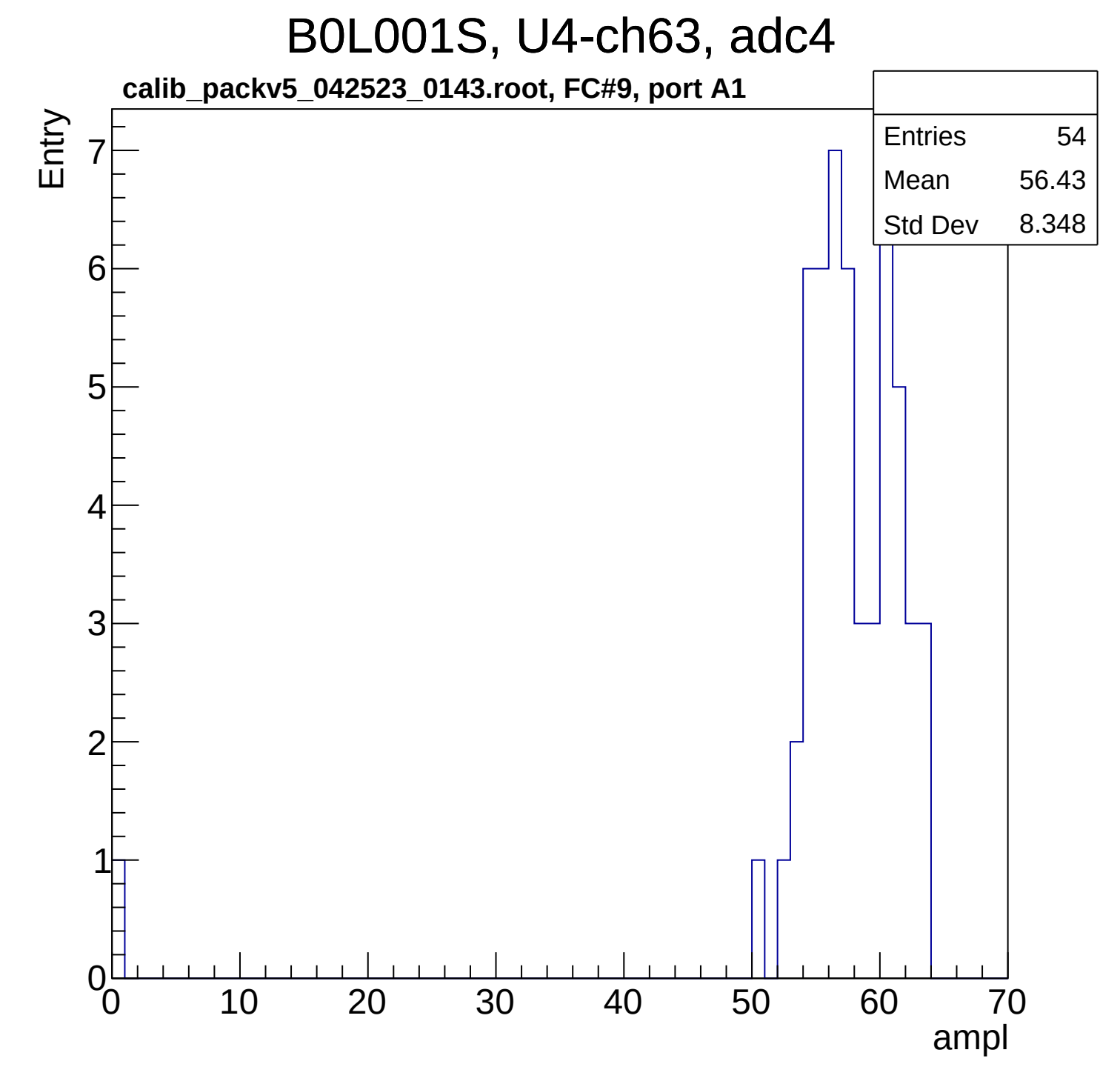
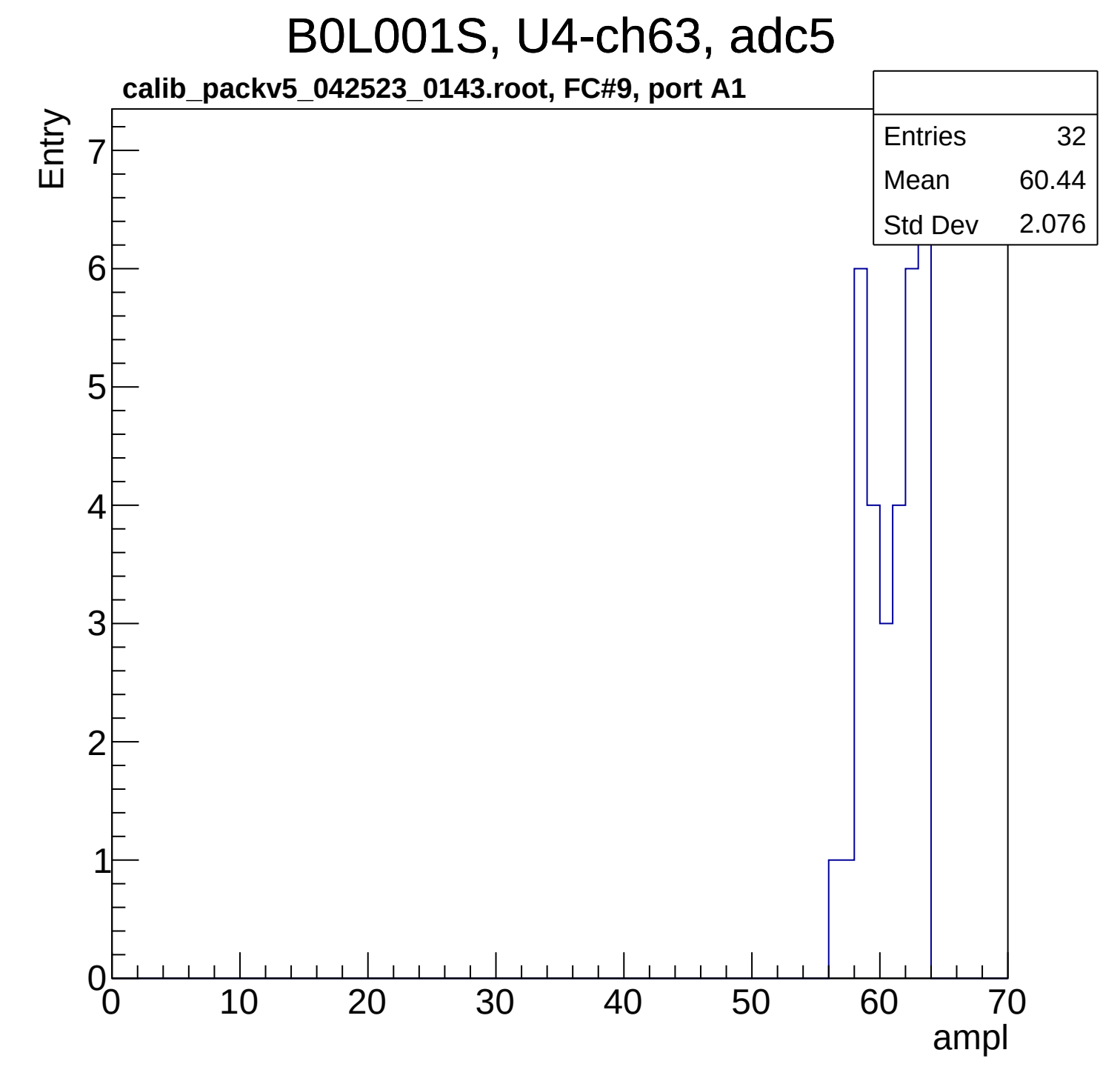
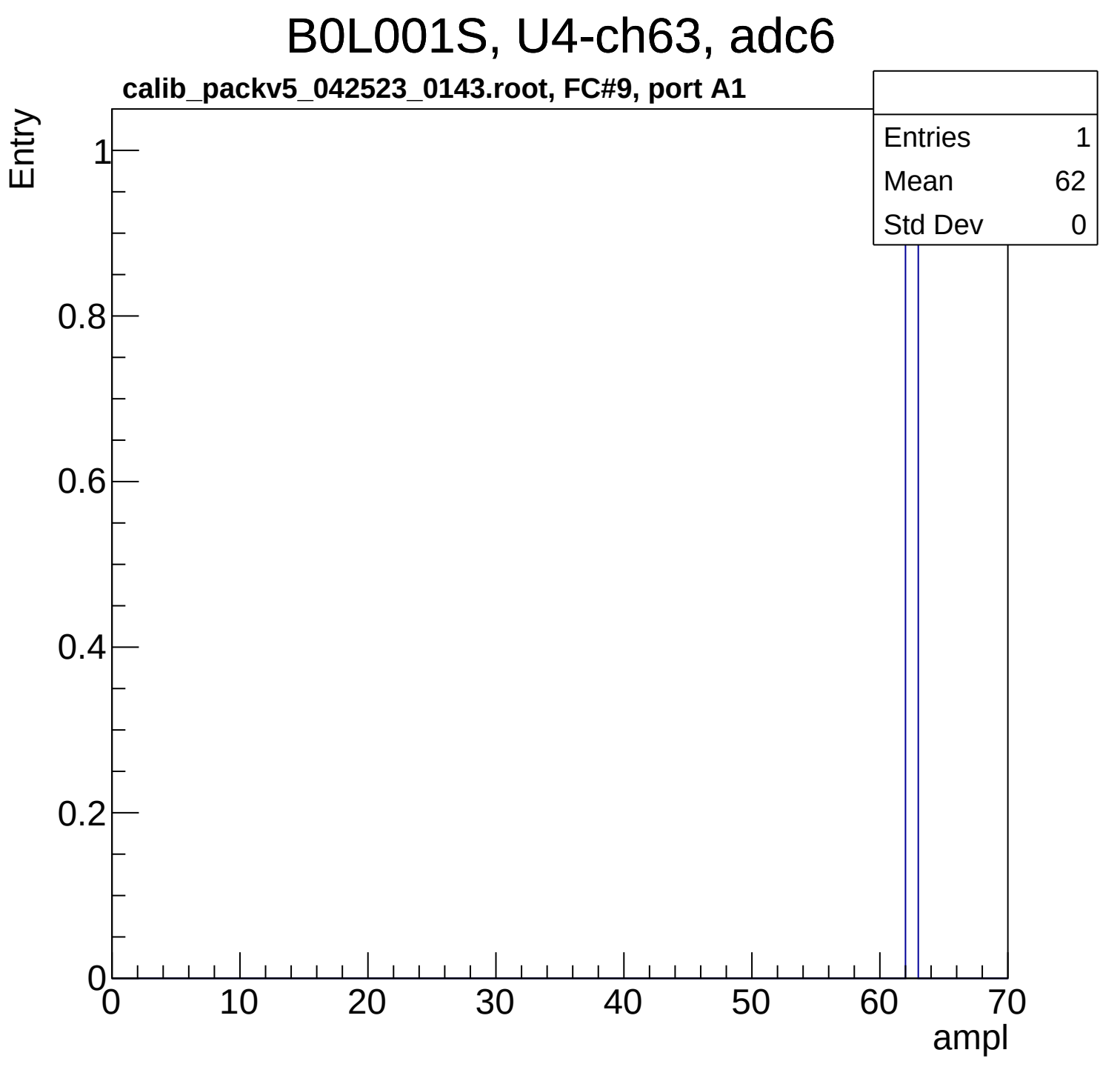
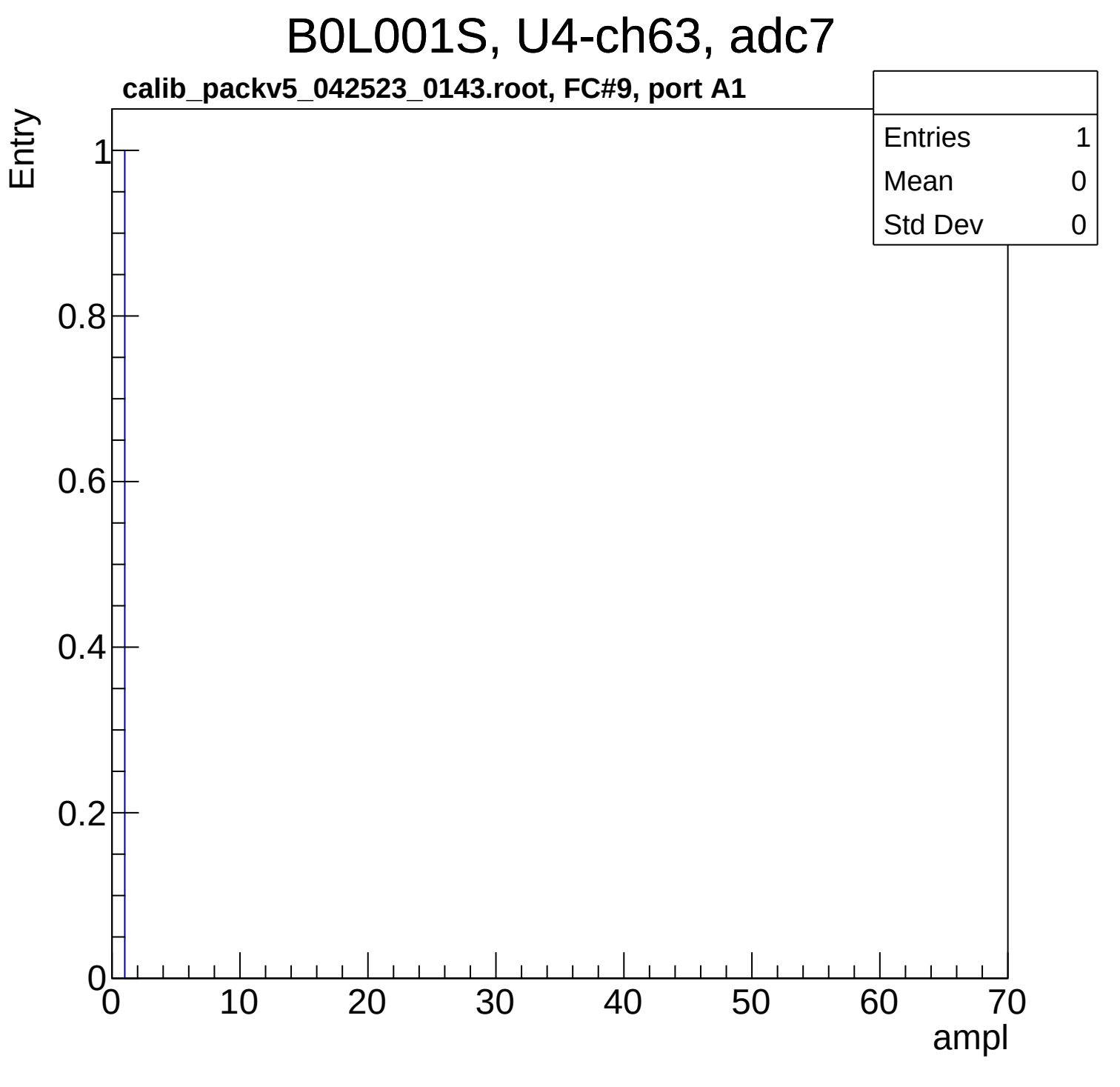
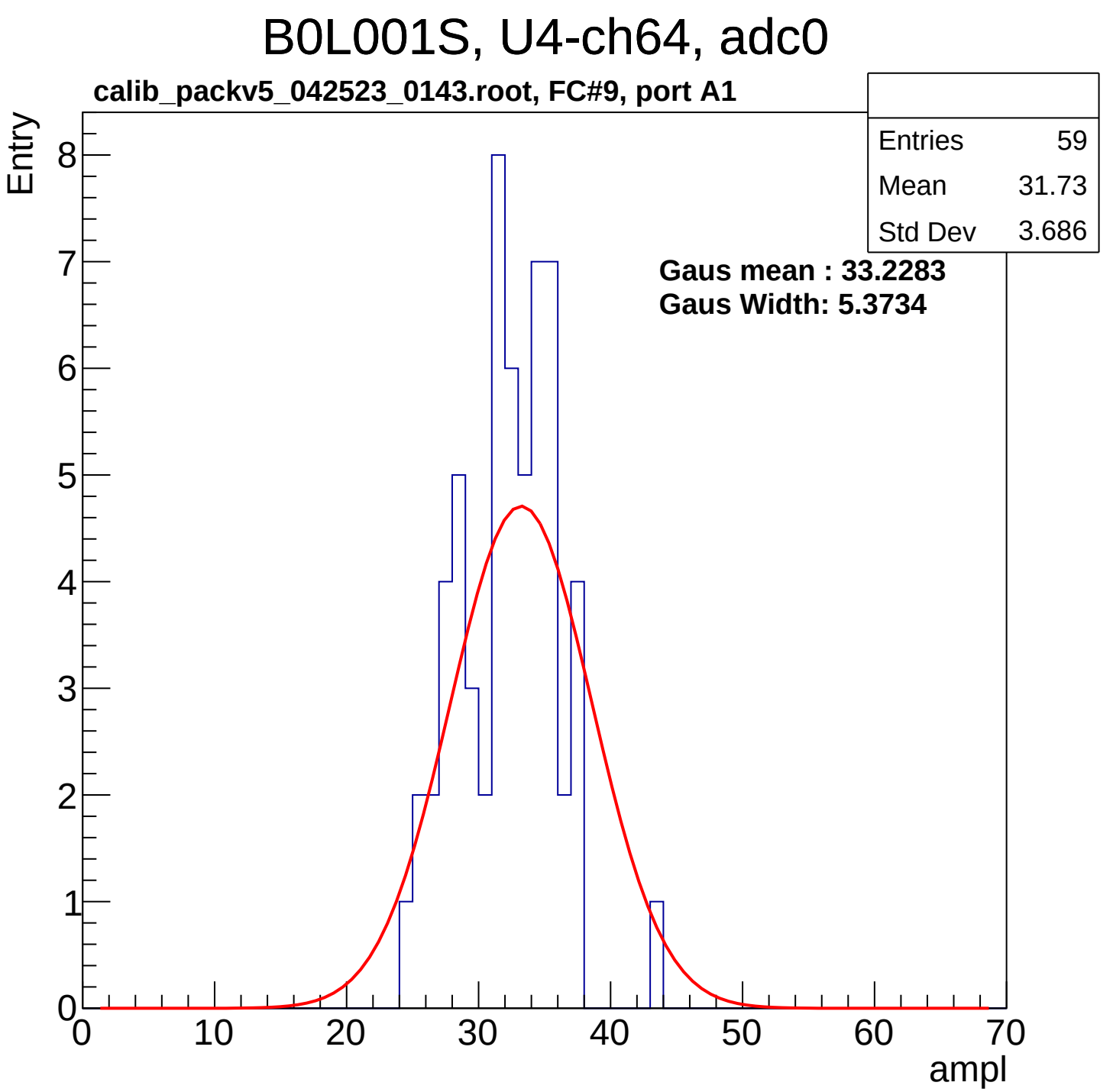
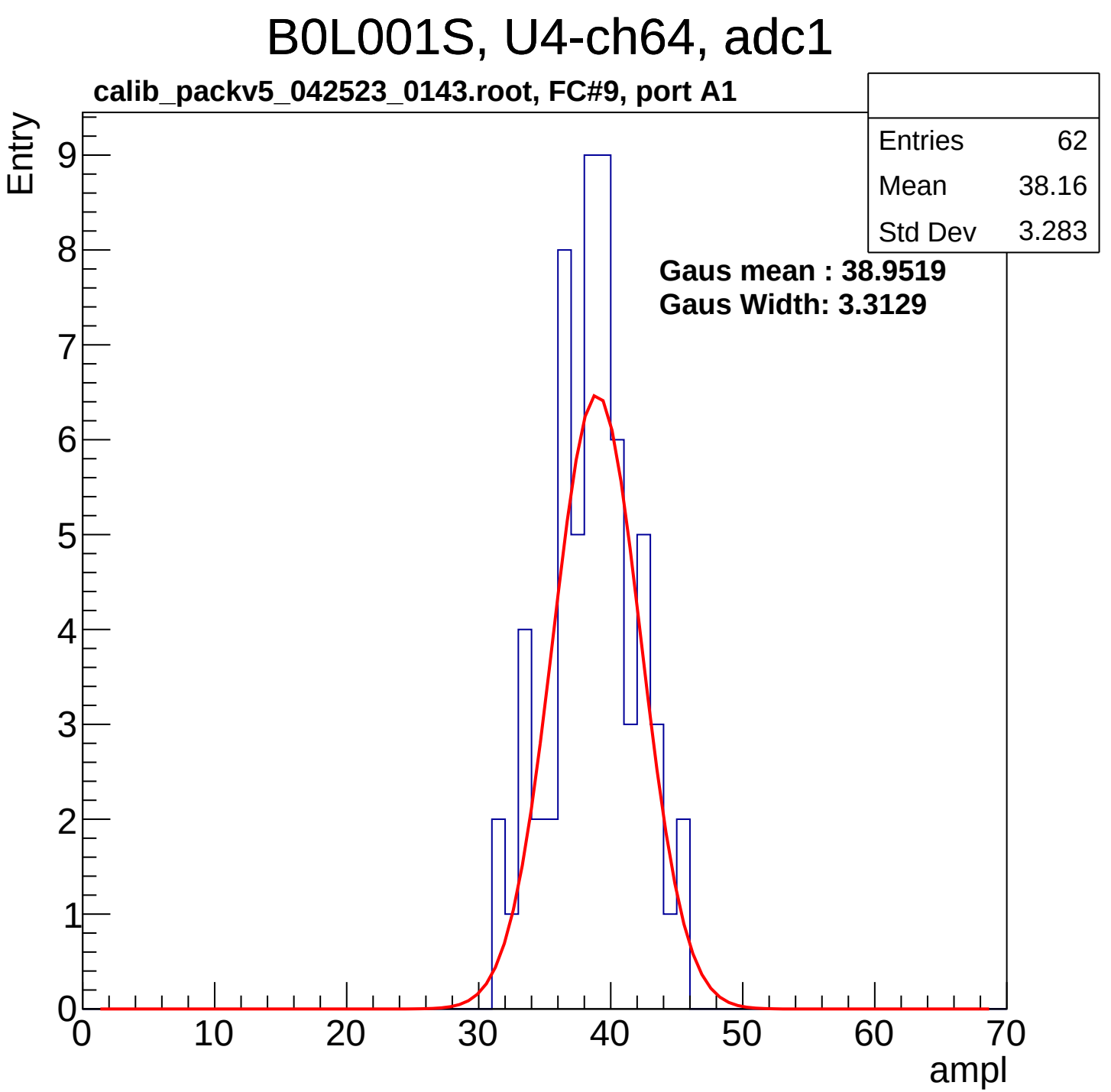

B0L001S, U4-ch63, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.43
Std Dev 8.348
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

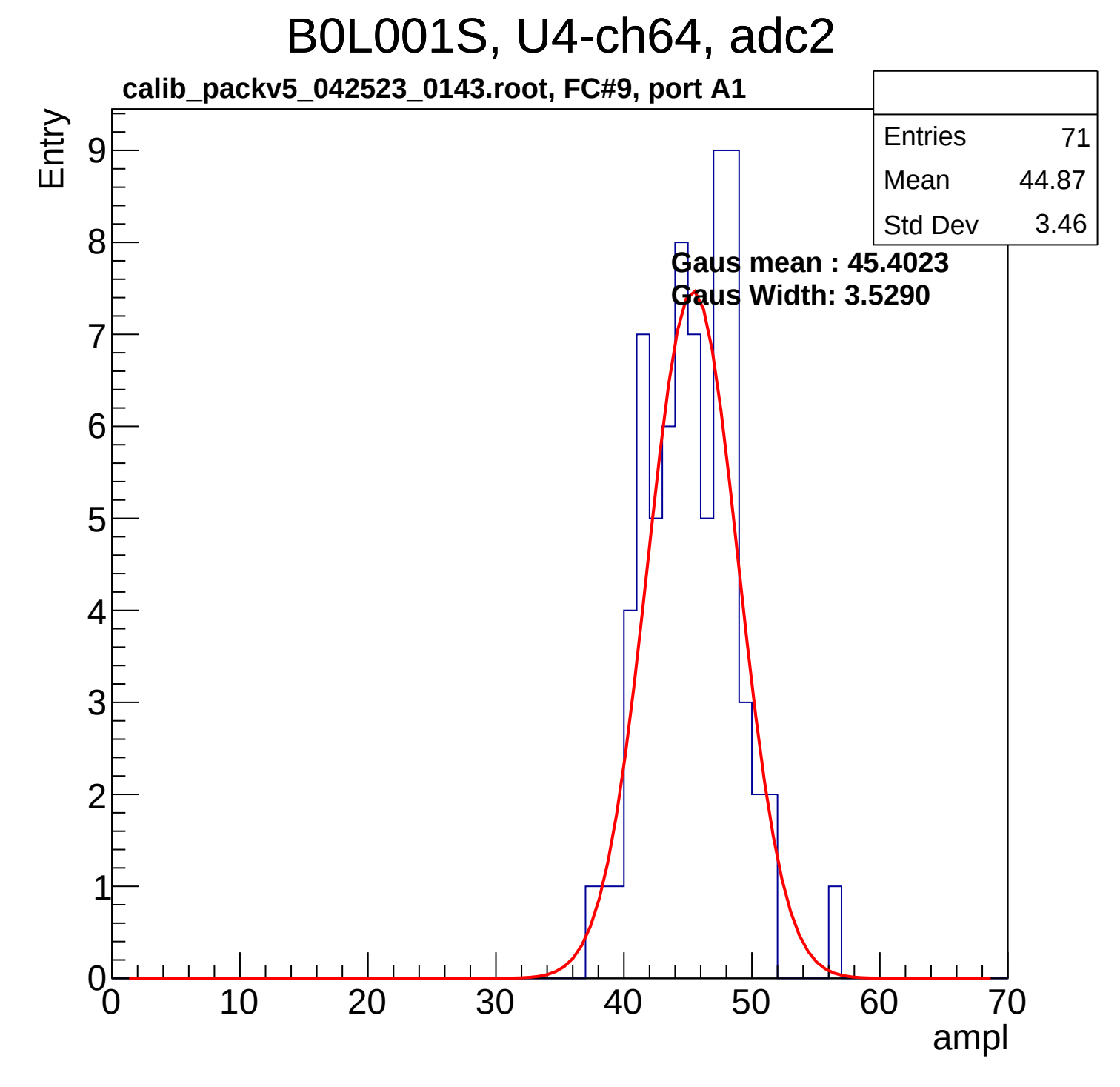

B0L001S, U4-ch63, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.44
Std Dev 2.076
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch63, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch63, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch64, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 31.73
Std Dev 3.686
Gaus mean : 33.2283
Gaus Width: 5.3734
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch64, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.16
Std Dev 3.283
Gaus mean : 38.9519
Gaus Width: 3.3129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch64, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.87
Std Dev 3.46
Gaus mean : 45.4023
Gaus Width: 3.5290
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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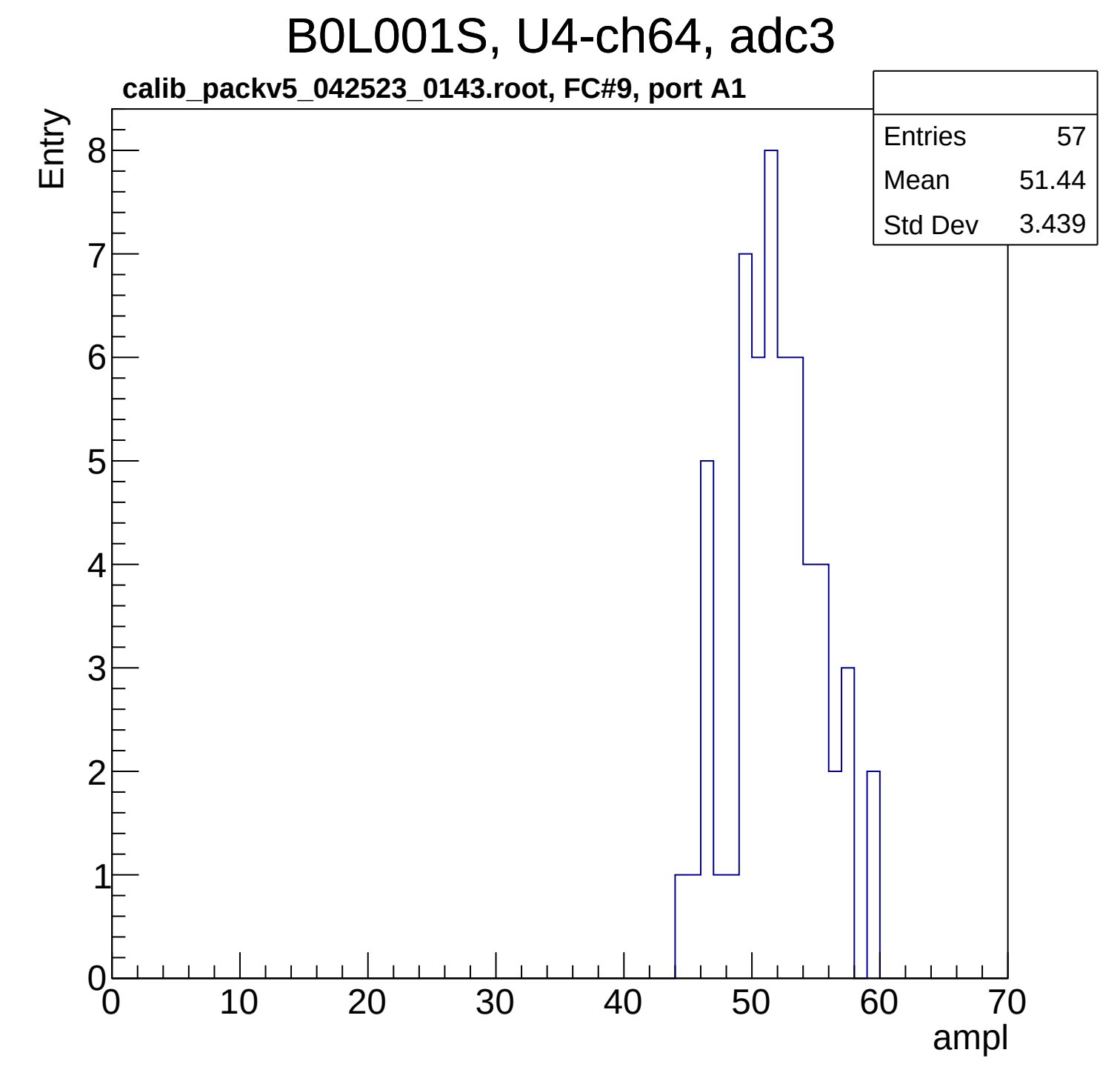
60
70


B0L001S, U4-ch64, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.44
Std Dev 3.439
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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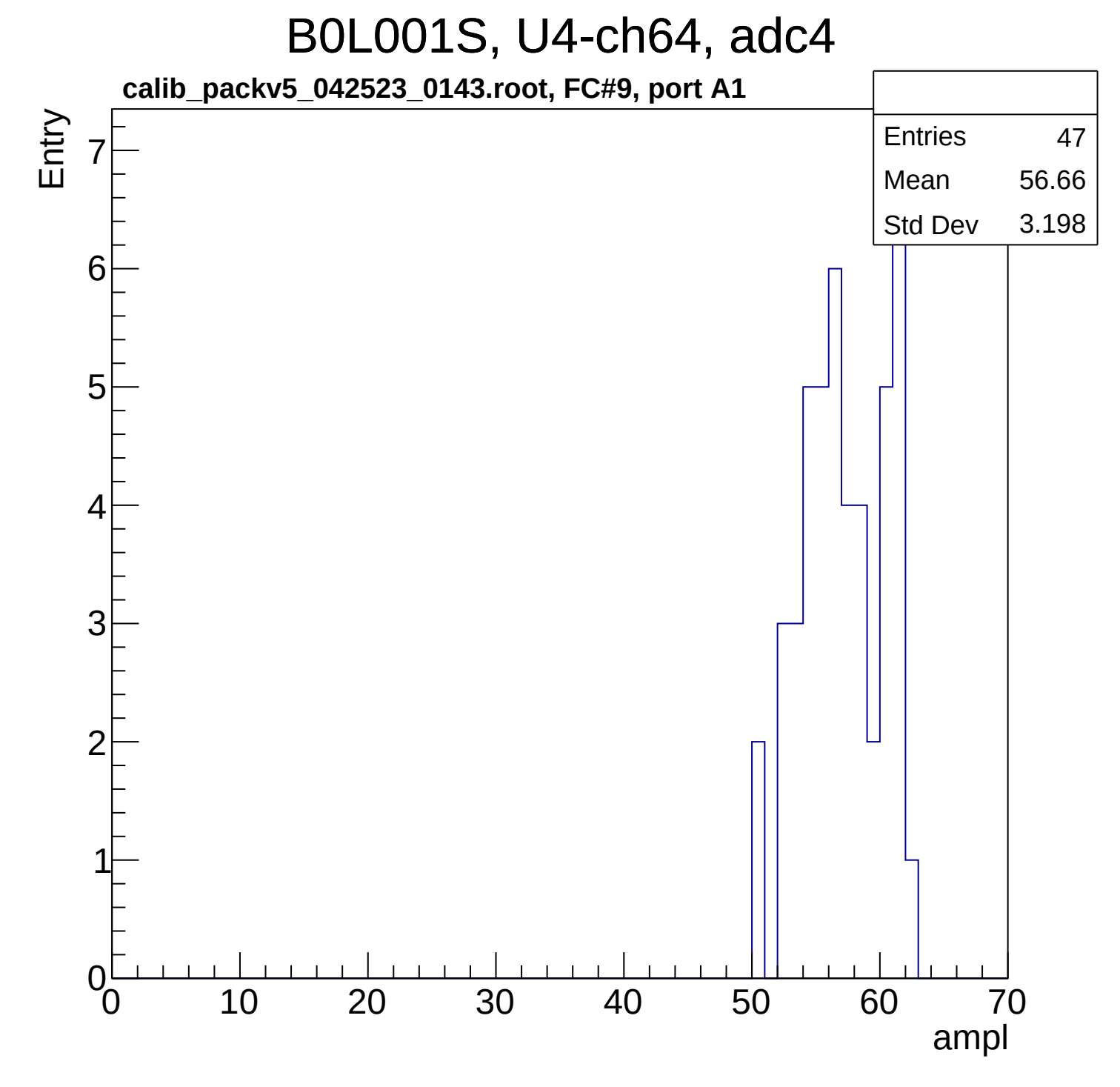
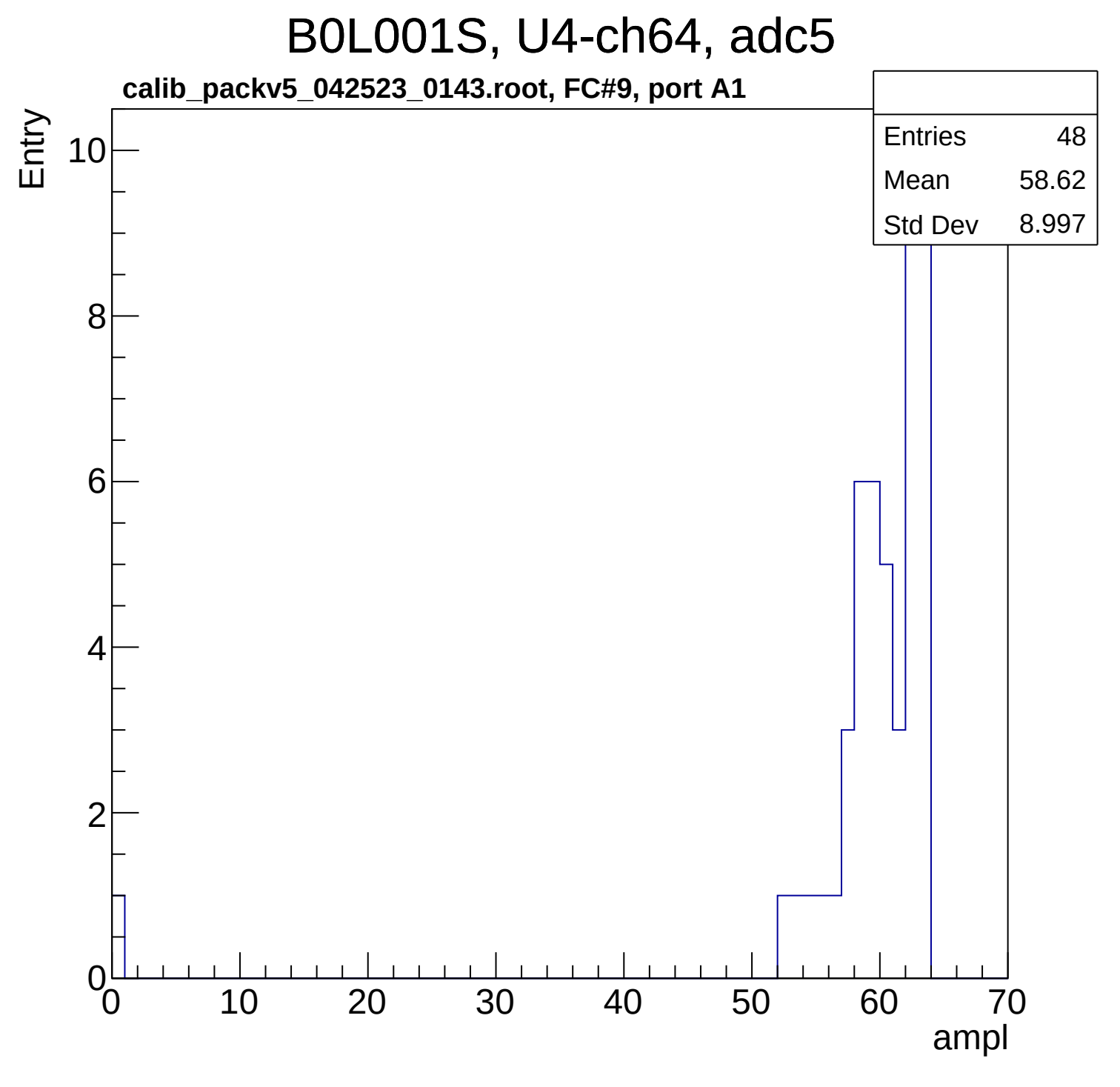
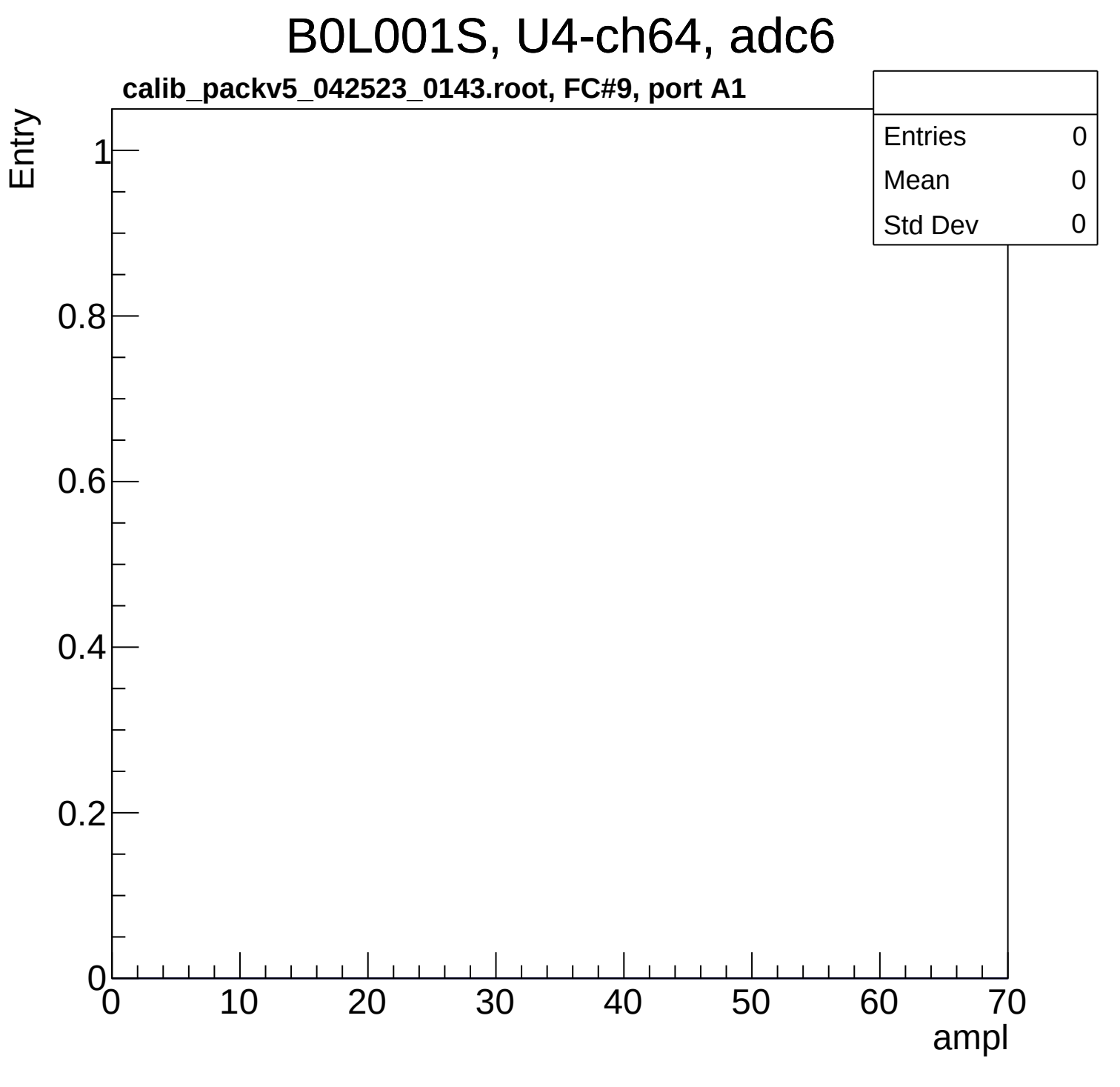

B0L001S, U4-ch64, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.66
Std Dev 3.198
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch64, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.62
Std Dev 8.997
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch64, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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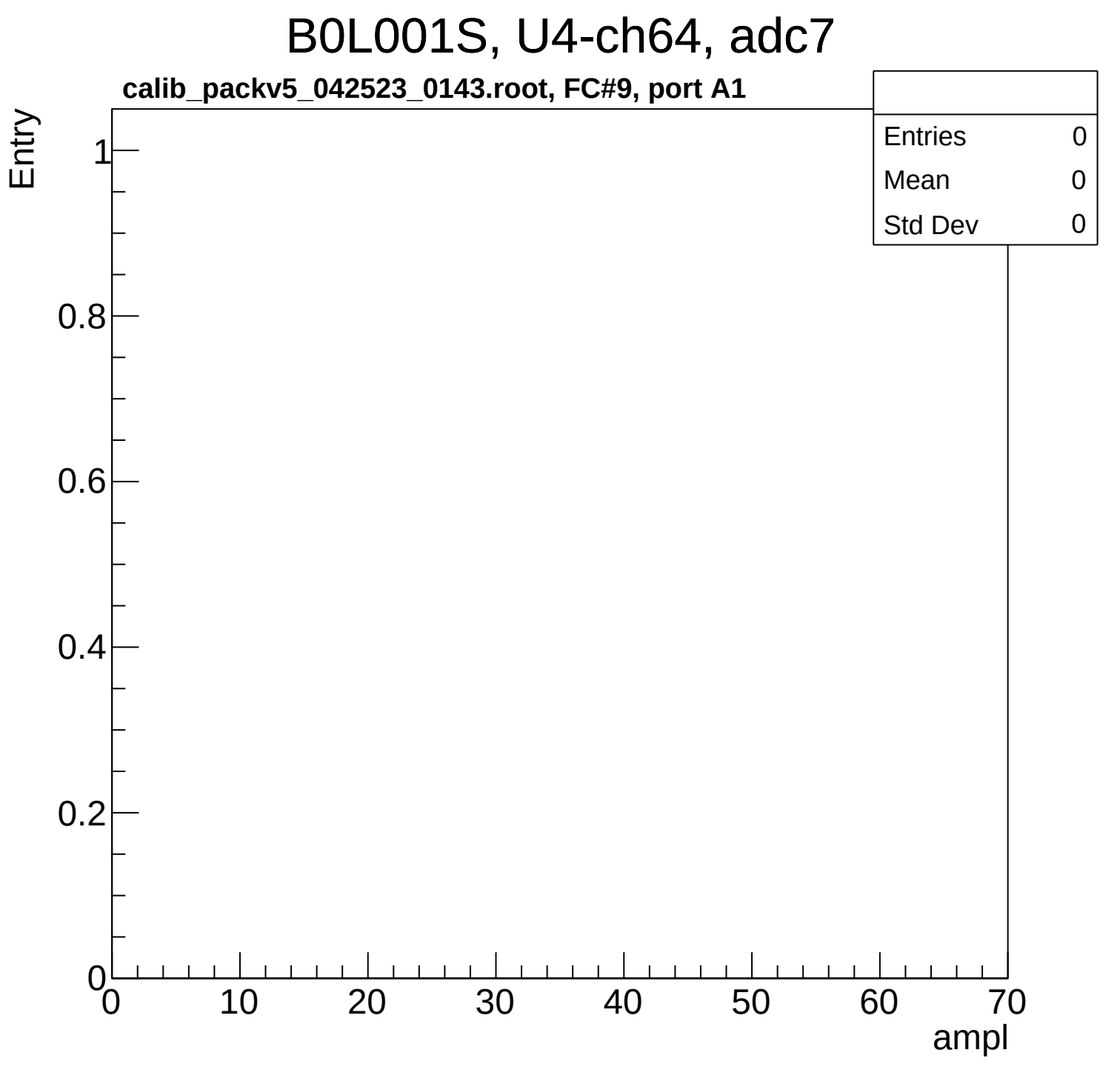
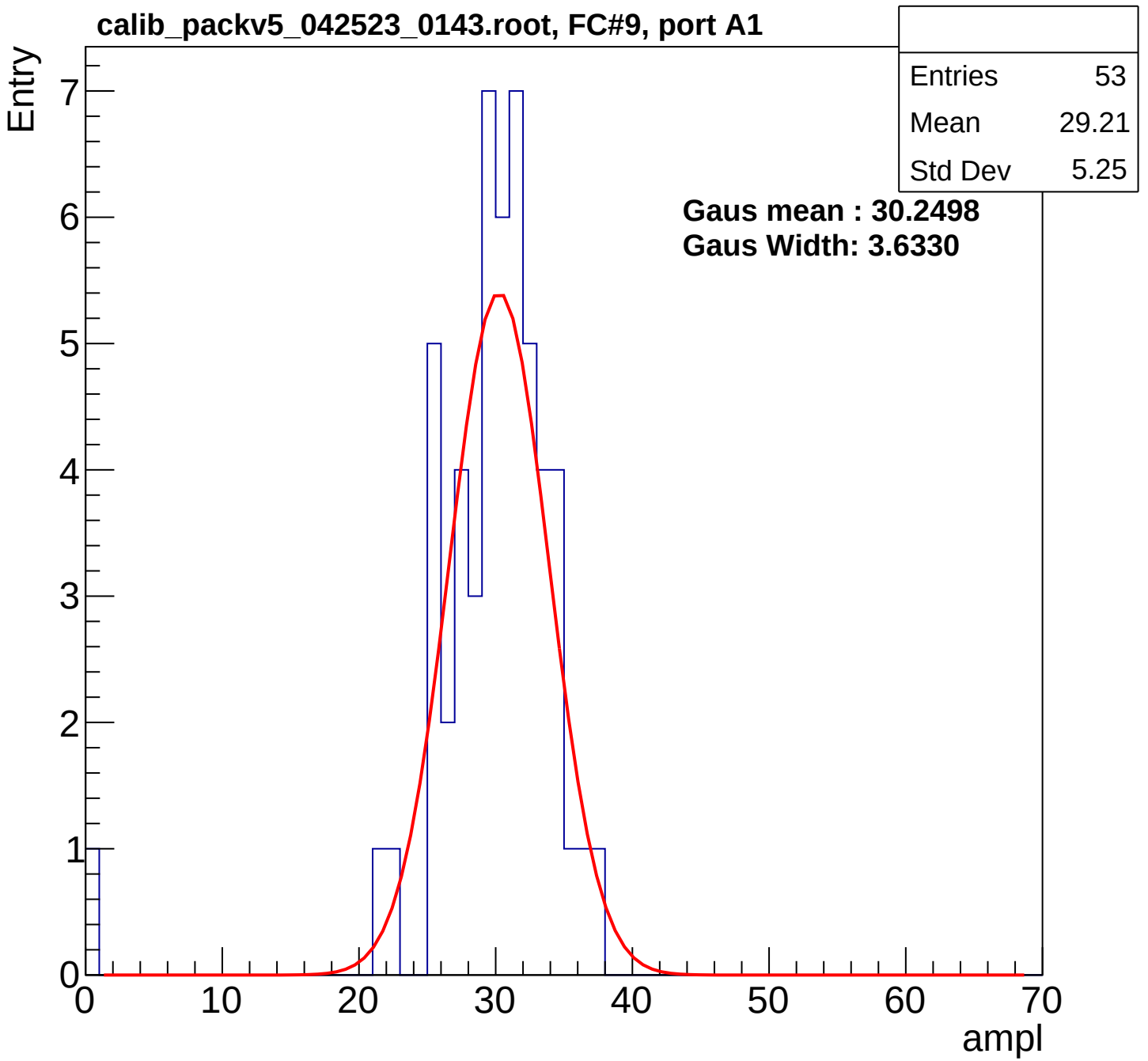
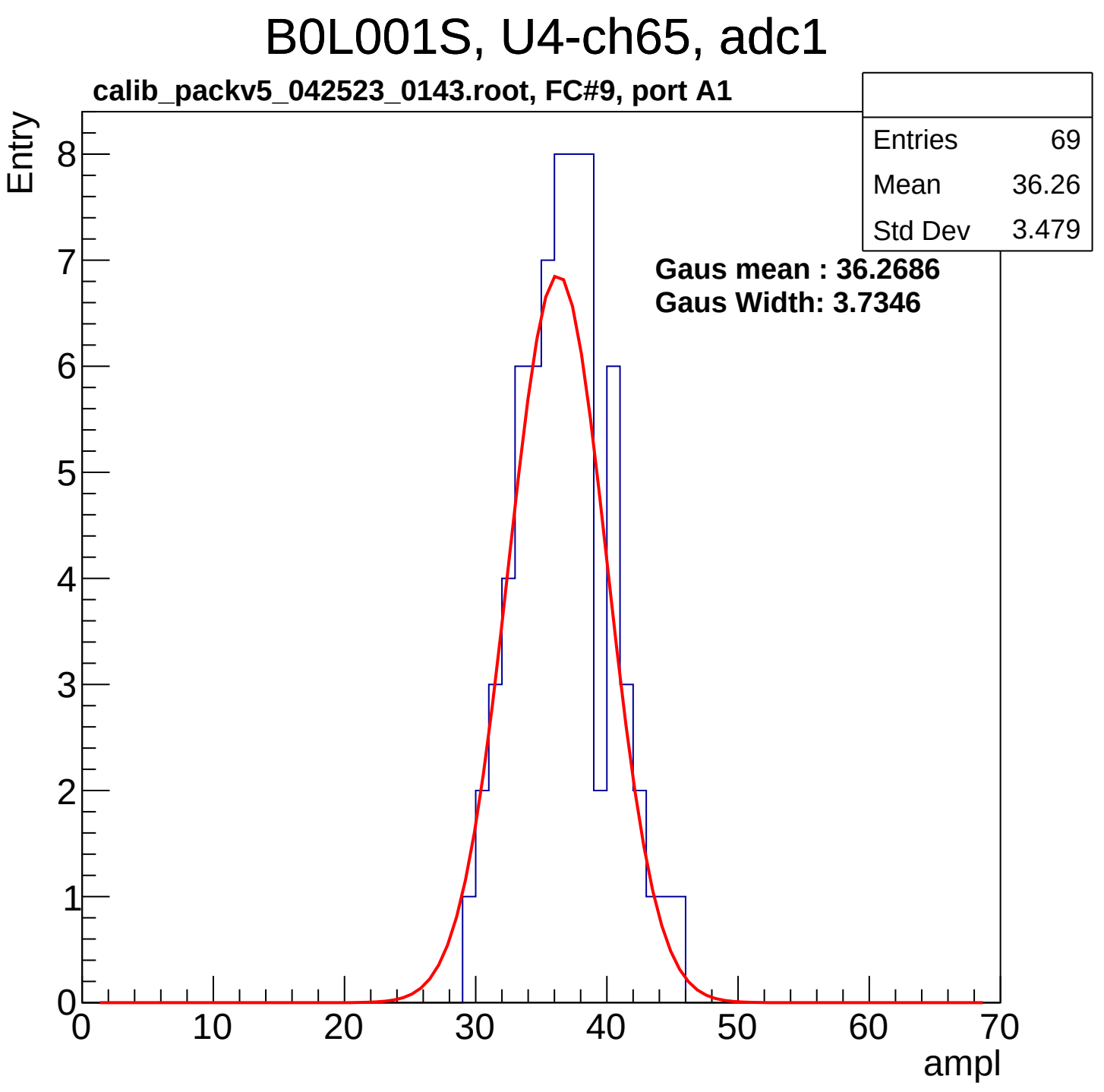
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch64, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch65, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 29.21
Std Dev 5.25
Gaus mean : 30.2498
Gaus Width: 3.6330
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch65, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 36.26
Std Dev 3.479
Gaus mean : 36.2686
Gaus Width: 3.7346
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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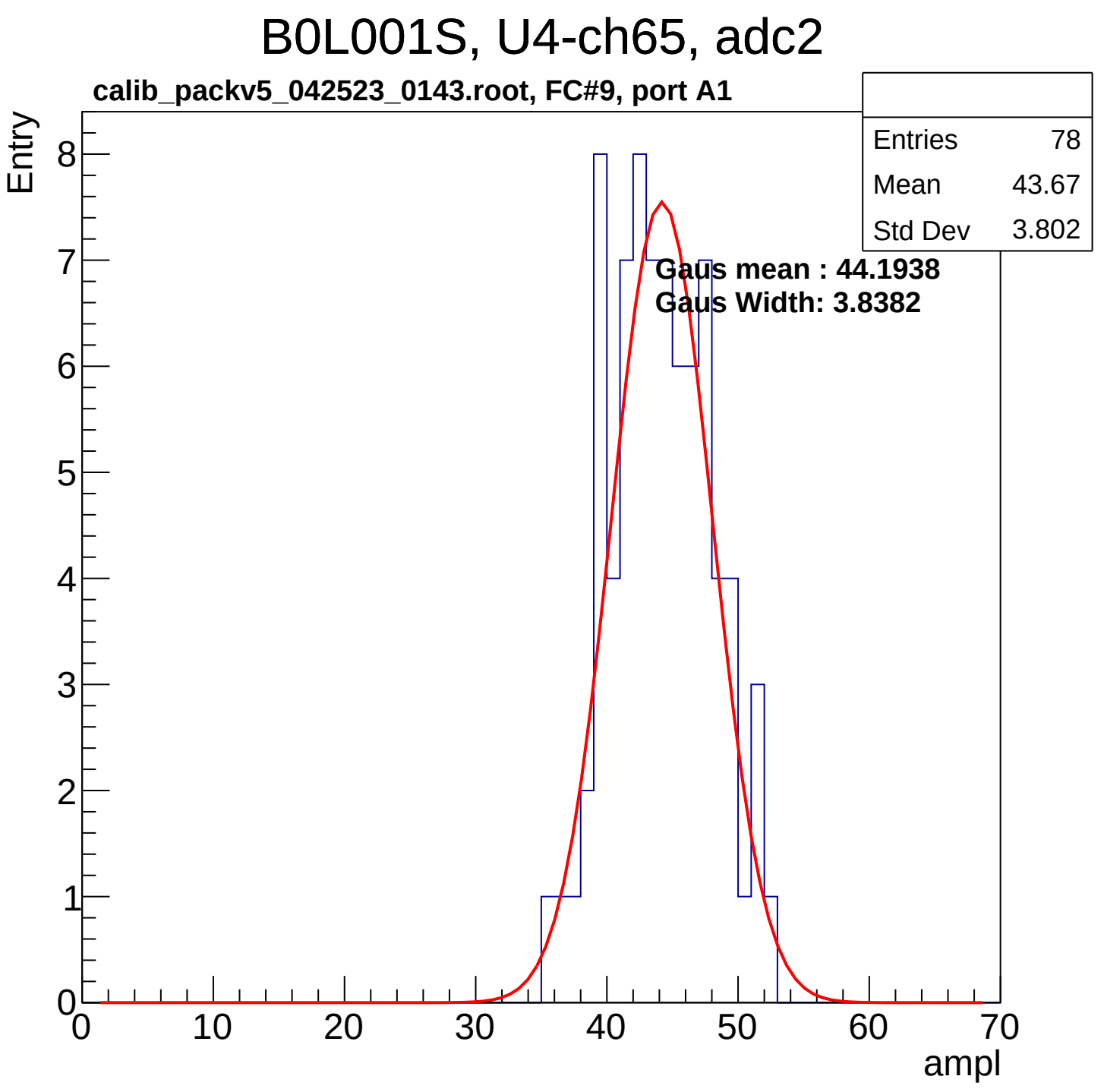
B0L001S, U4-ch65, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 43.67
Std Dev 3.802
Gaus mean : 44.1938
Gaus Width: 3.8382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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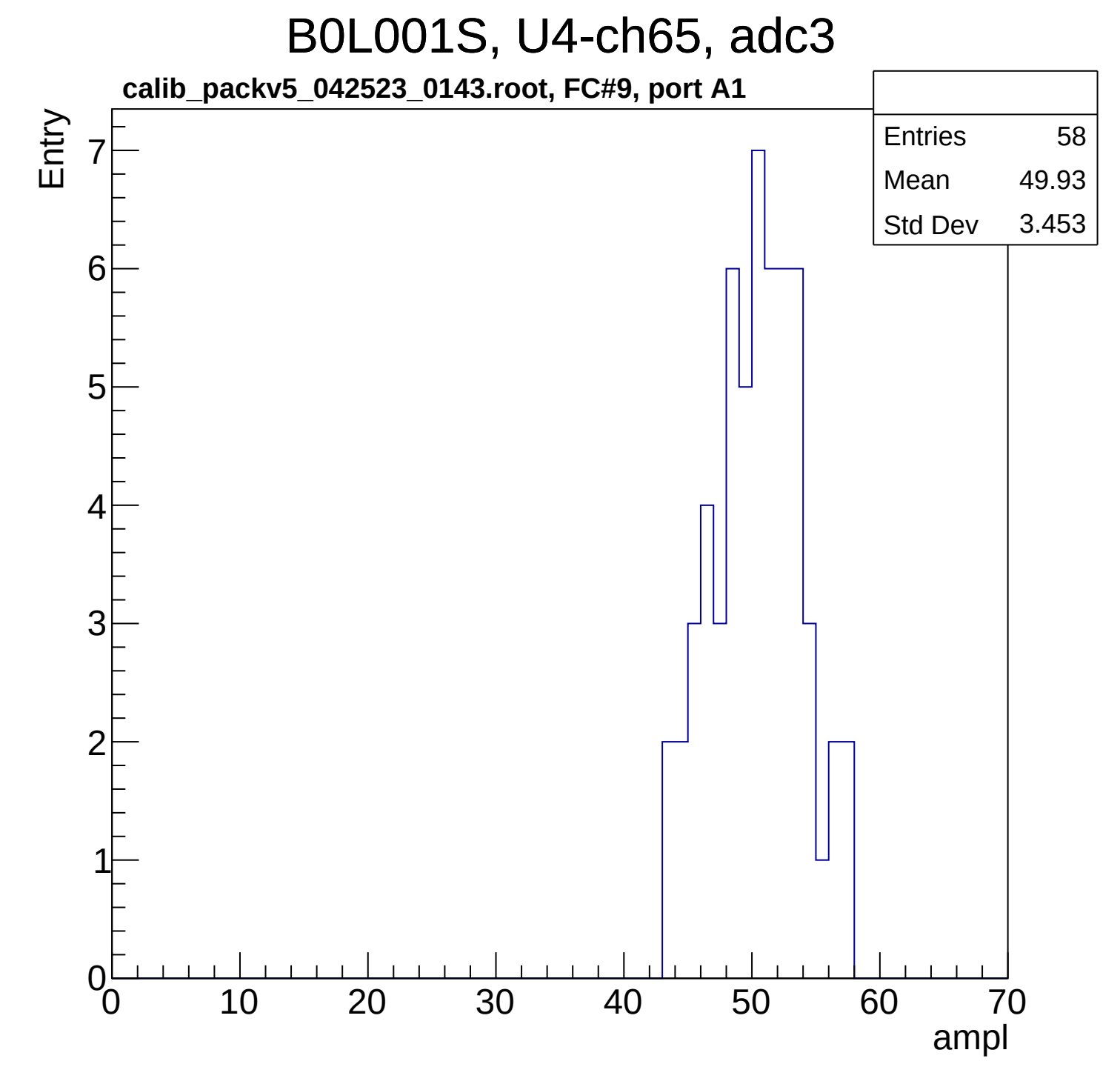
40
50
60
70


B0L001S, U4-ch65, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 49.93
Std Dev 3.453
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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60
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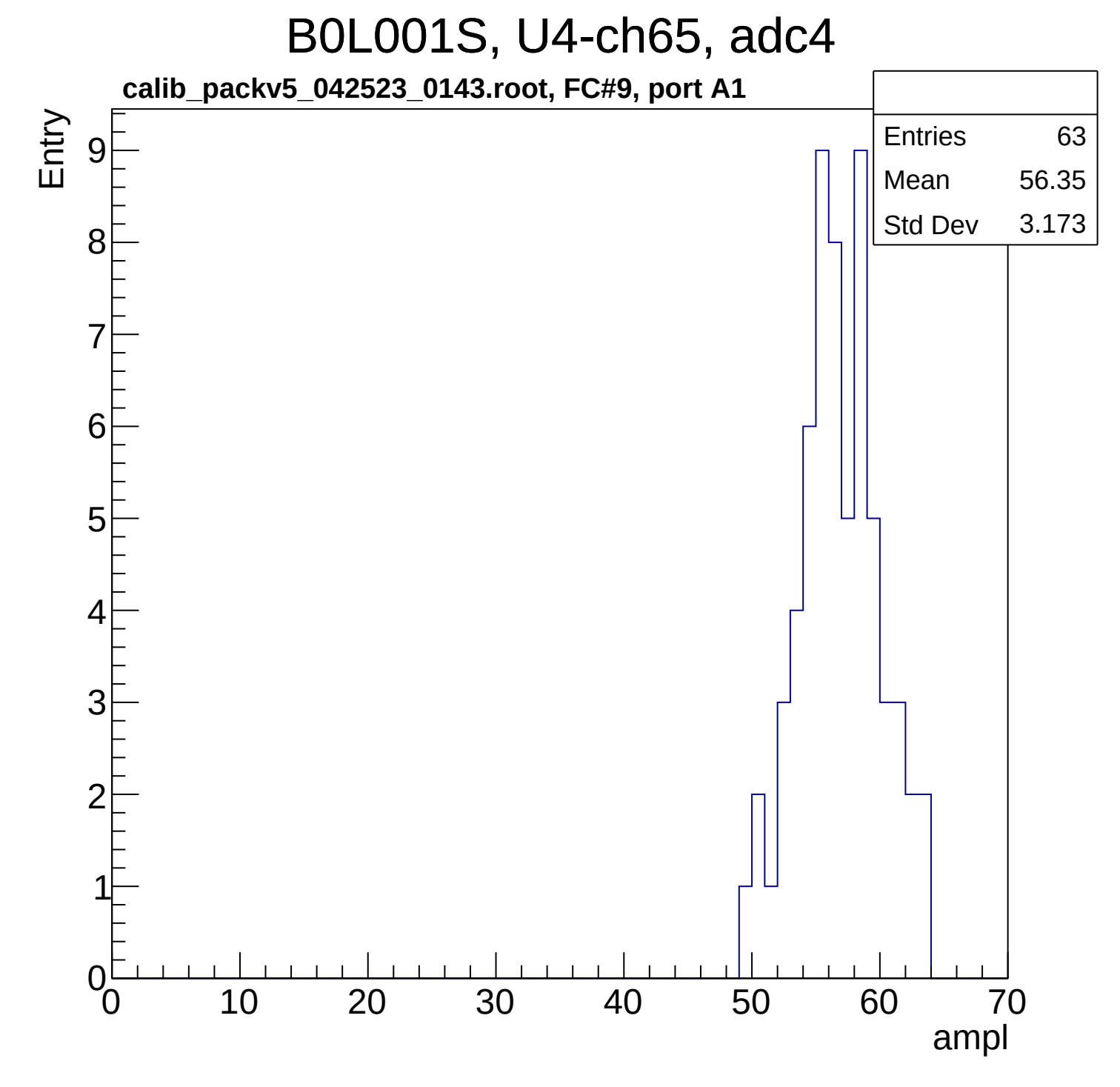
B0L001S, U4-ch65, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.35
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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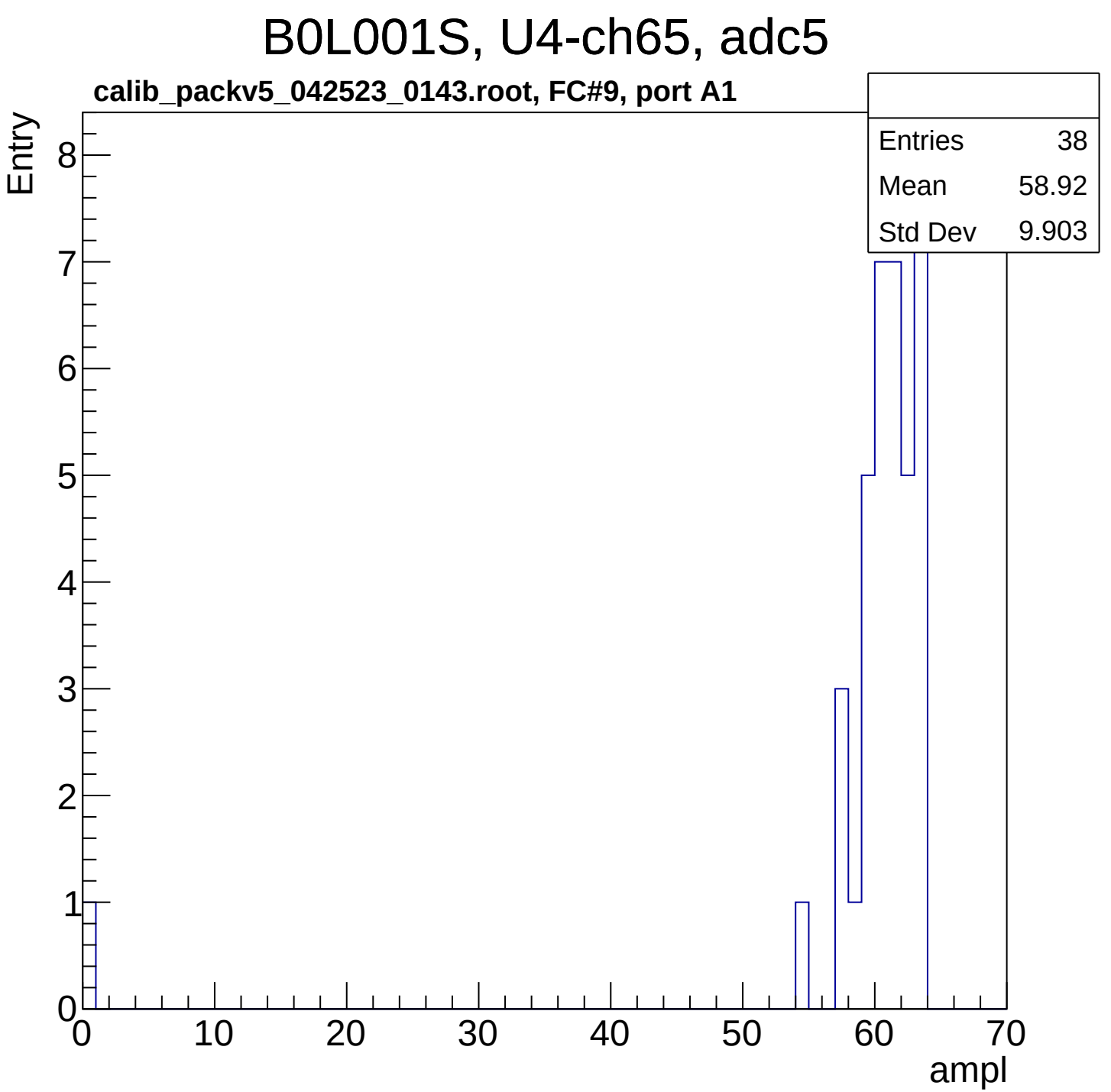
B0L001S, U4-ch65, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.92
Std Dev 9.903
Entry
ampl
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1
2
3
4
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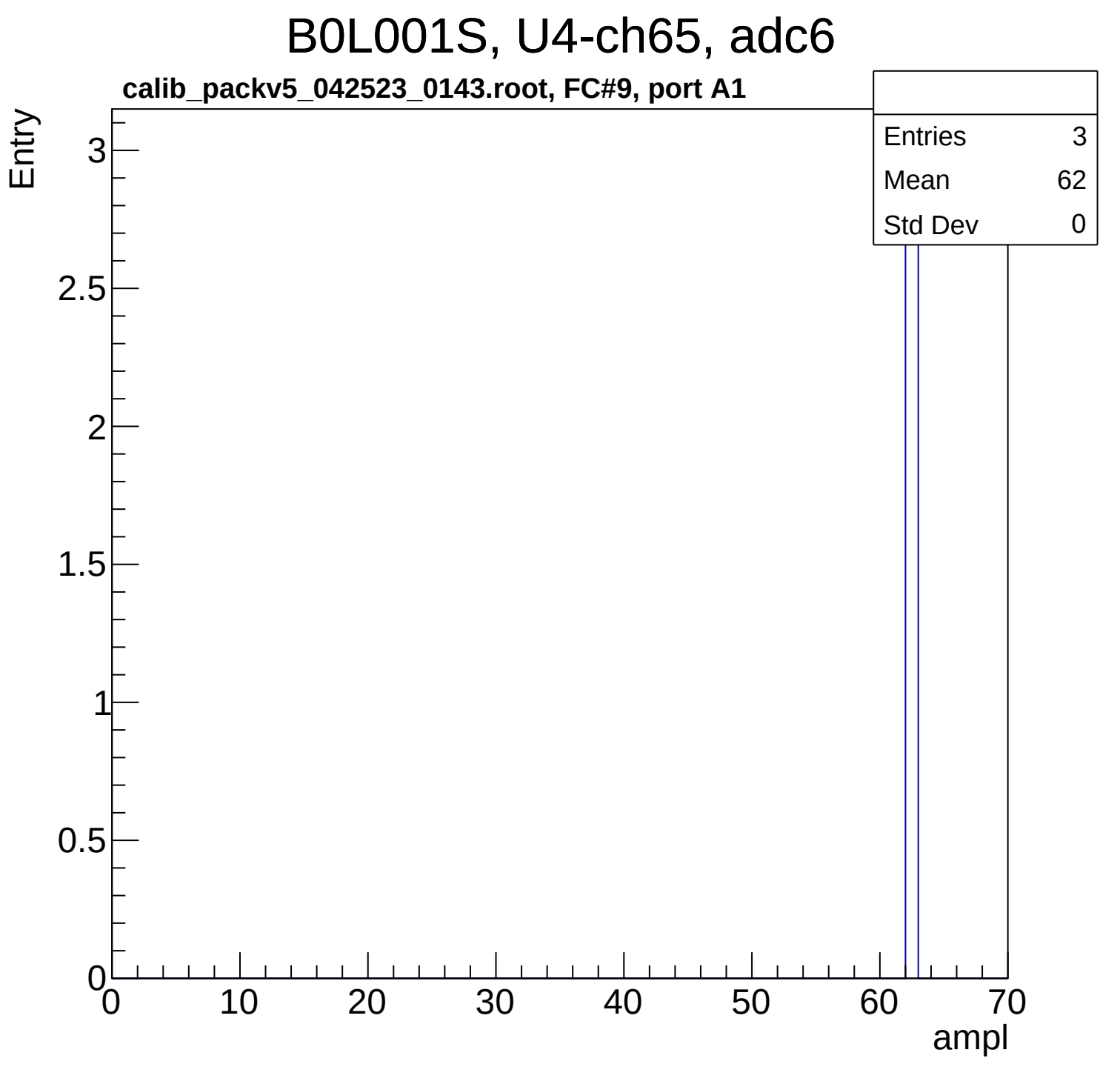
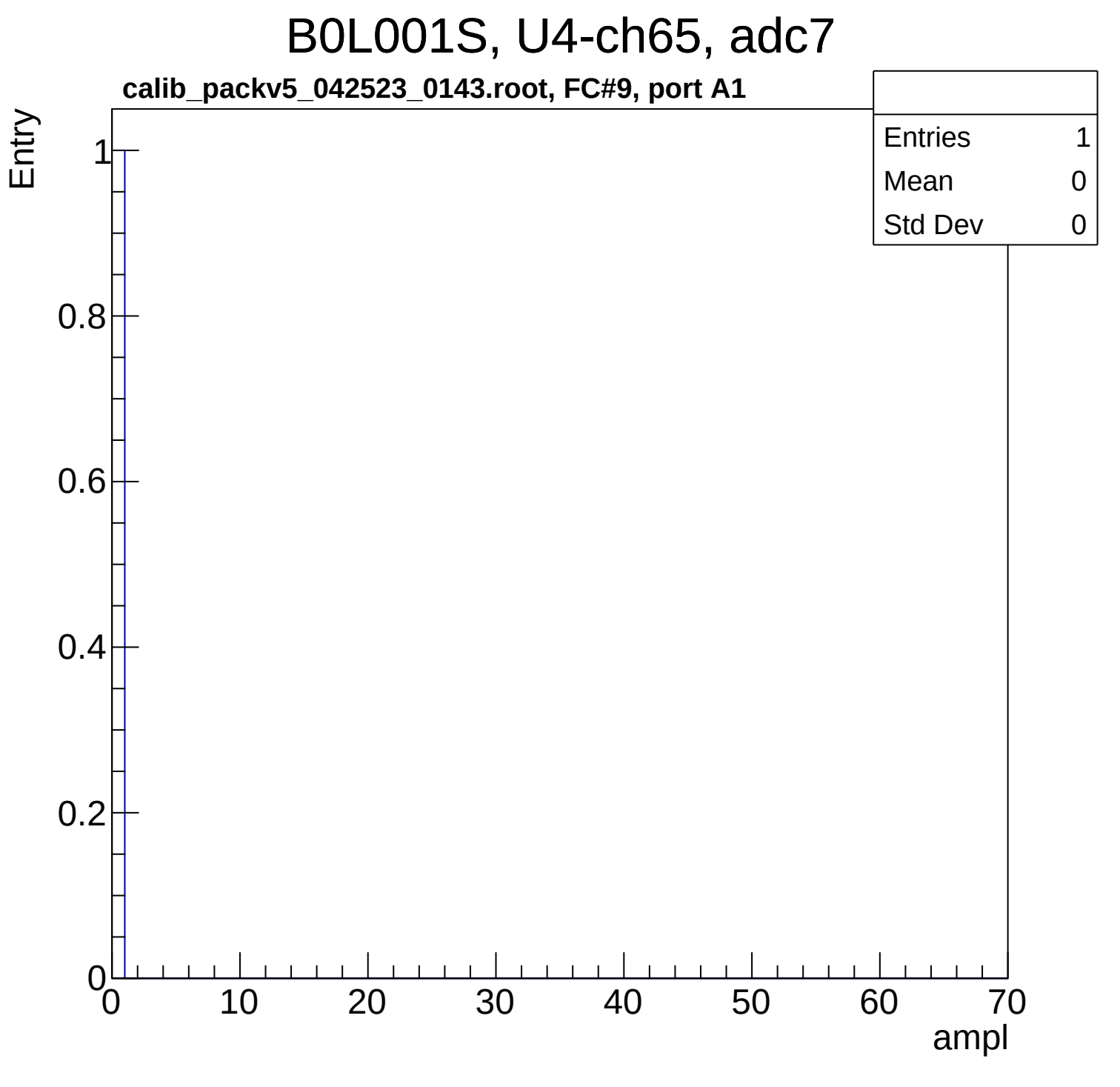
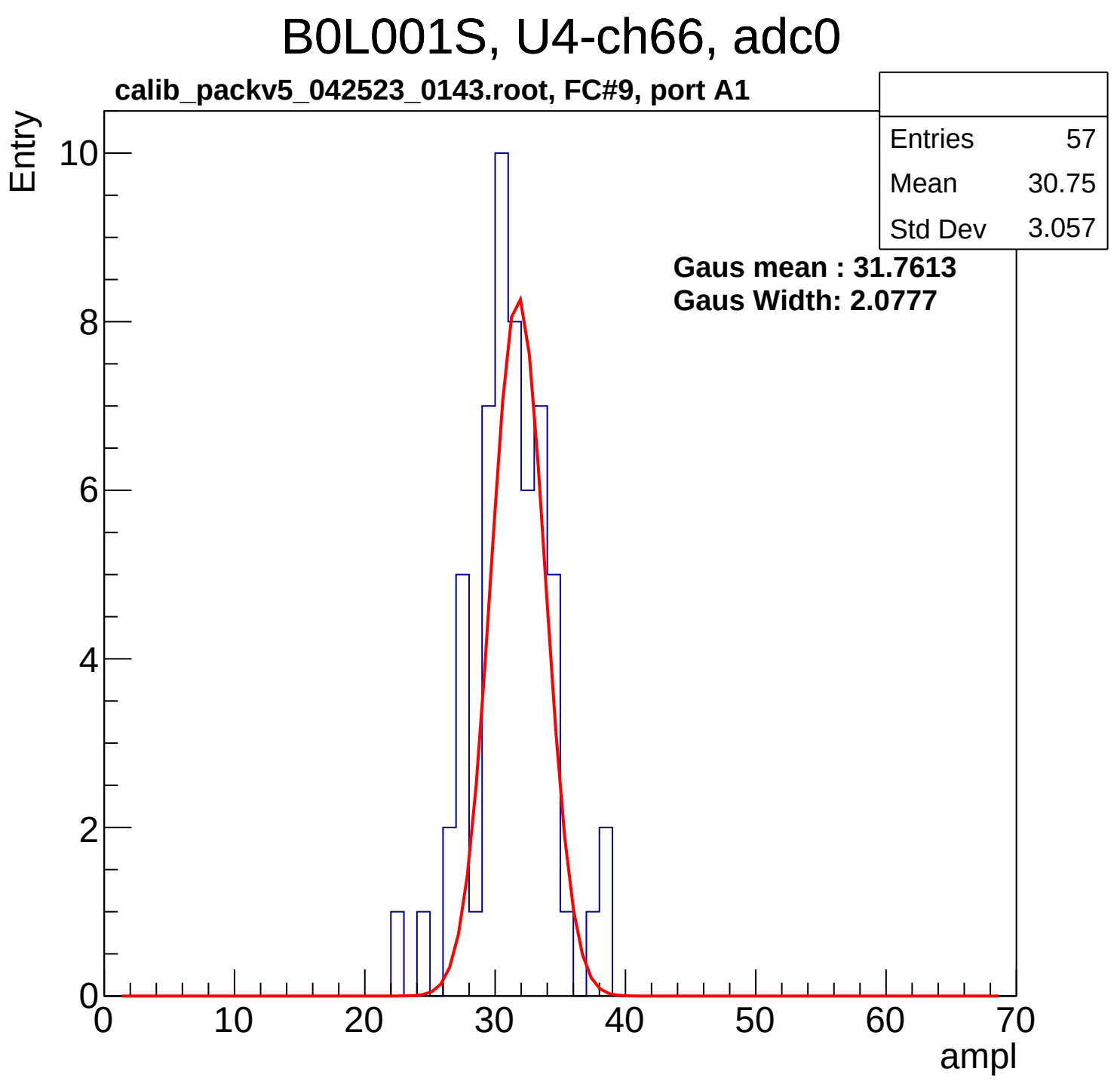

B0L001S, U4-ch65, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U4-ch65, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch66, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 30.75
Std Dev 3.057
Gaus mean : 31.7613
Gaus Width: 2.0777
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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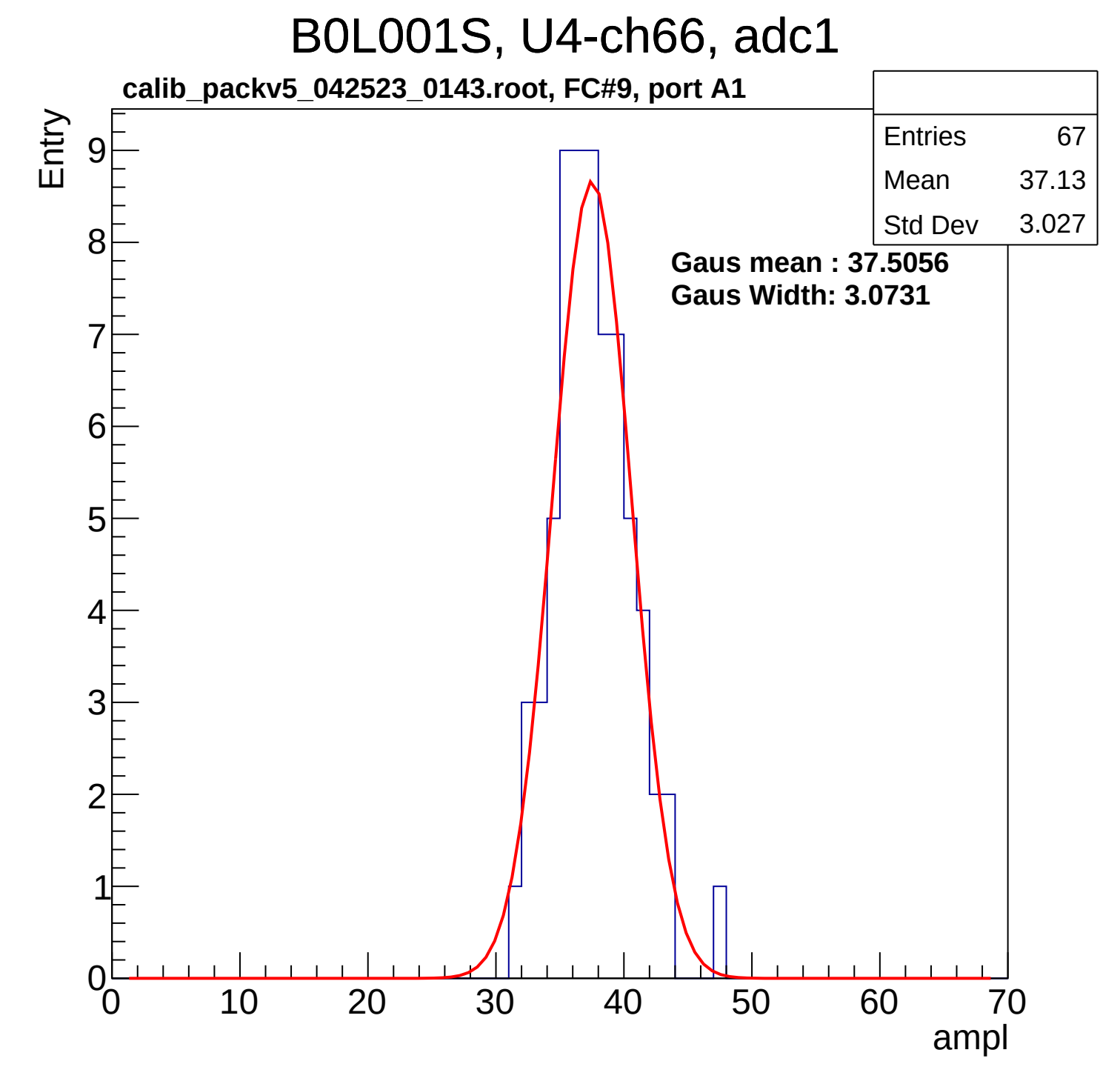
B0L001S, U4-ch66, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.13
Std Dev 3.027
Gaus mean : 37.5056
Gaus Width: 3.0731
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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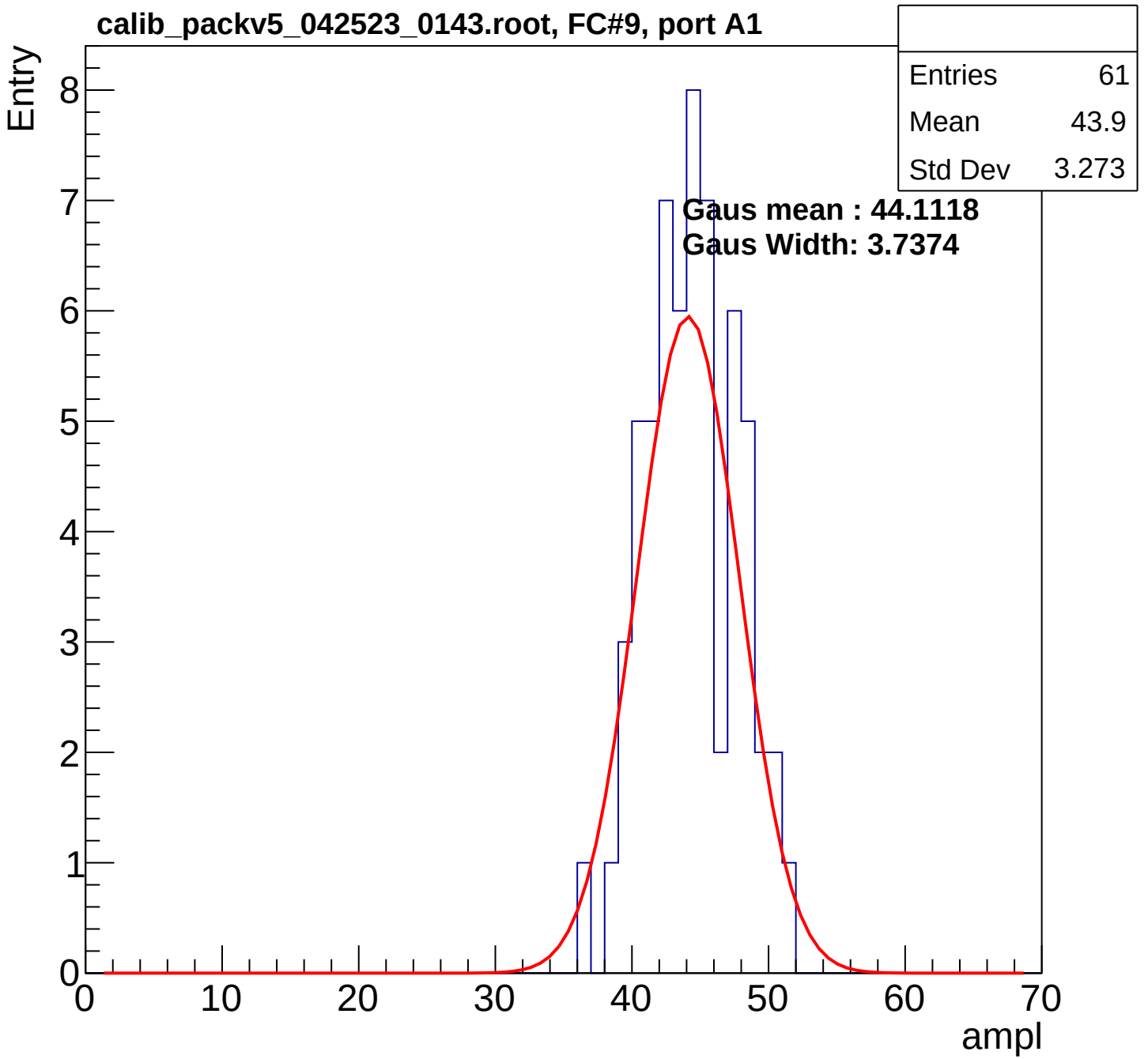
B0L001S, U4-ch66, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.9
Std Dev 3.273
Gaus mean : 44.1118
Gaus Width: 3.7374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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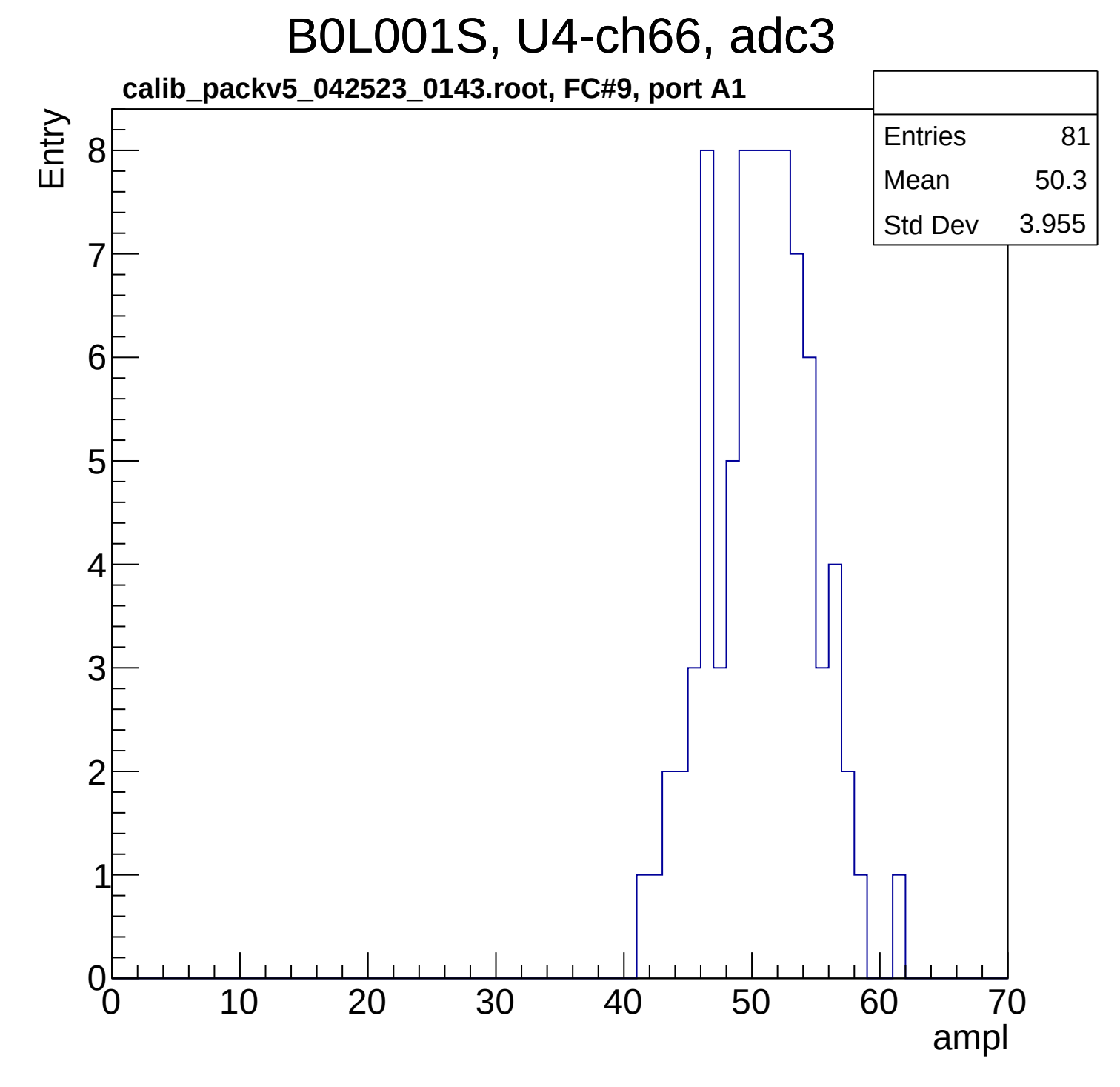
B0L001S, U4-ch66, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 50.3
Std Dev 3.955
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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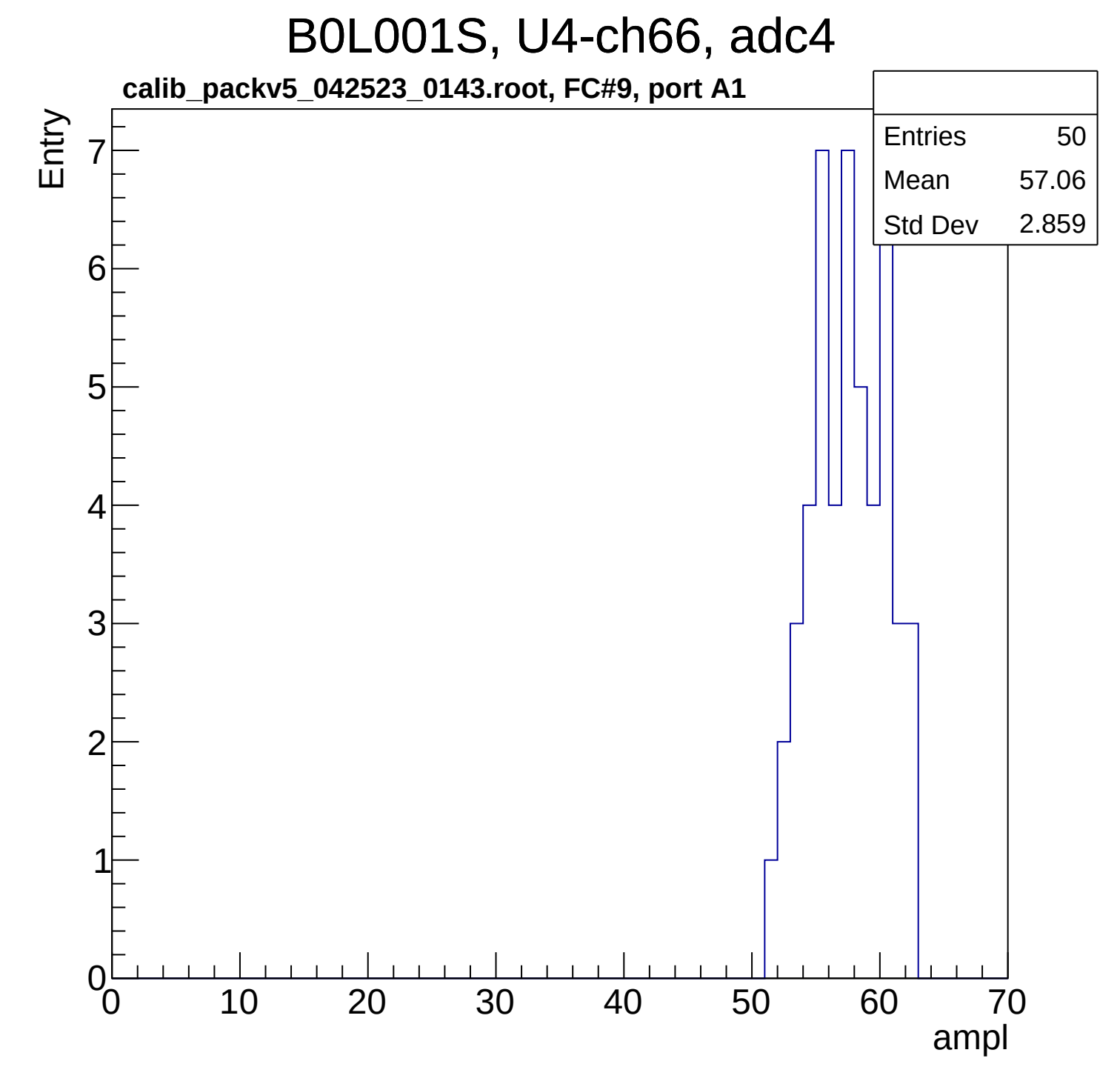
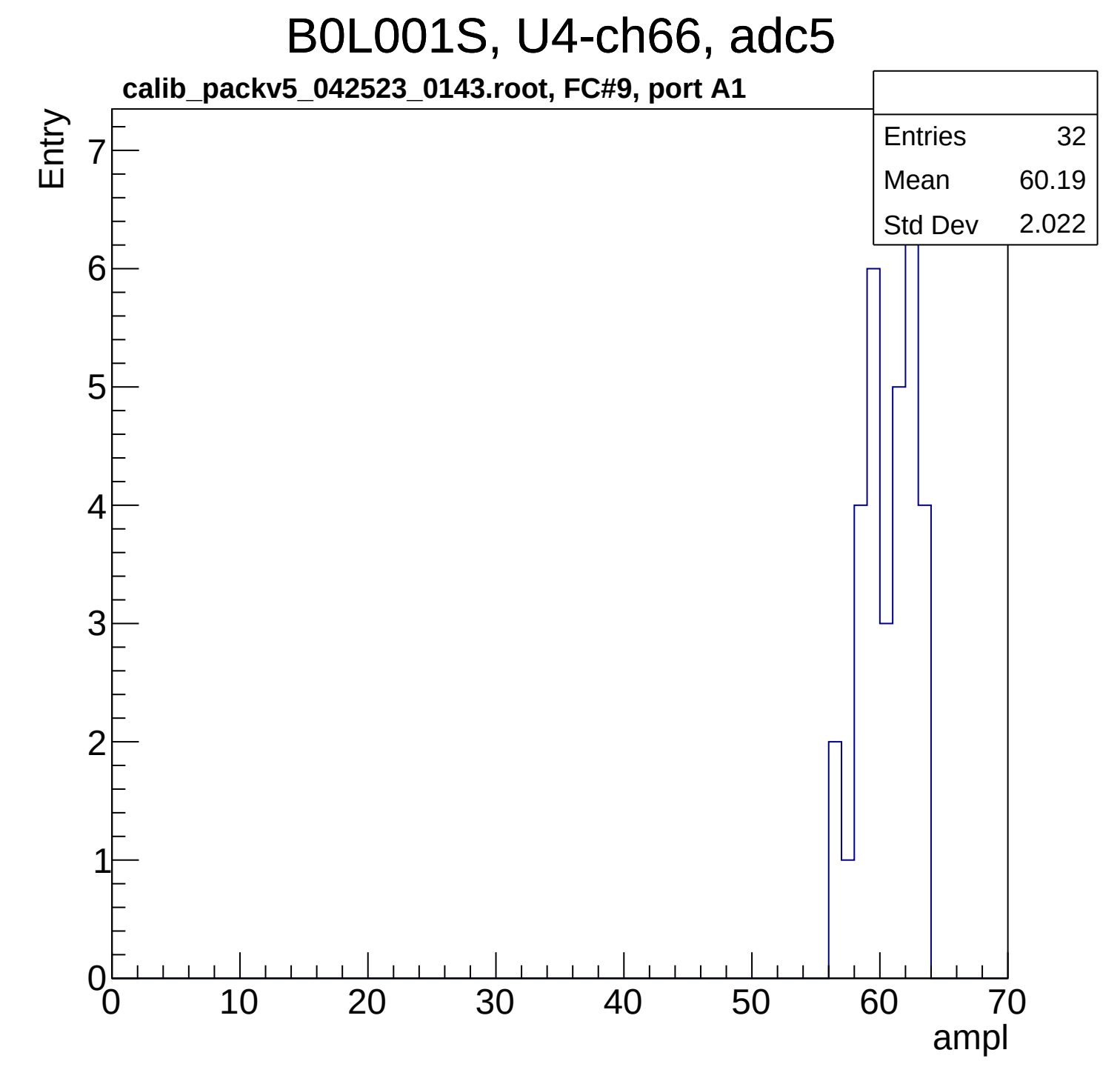
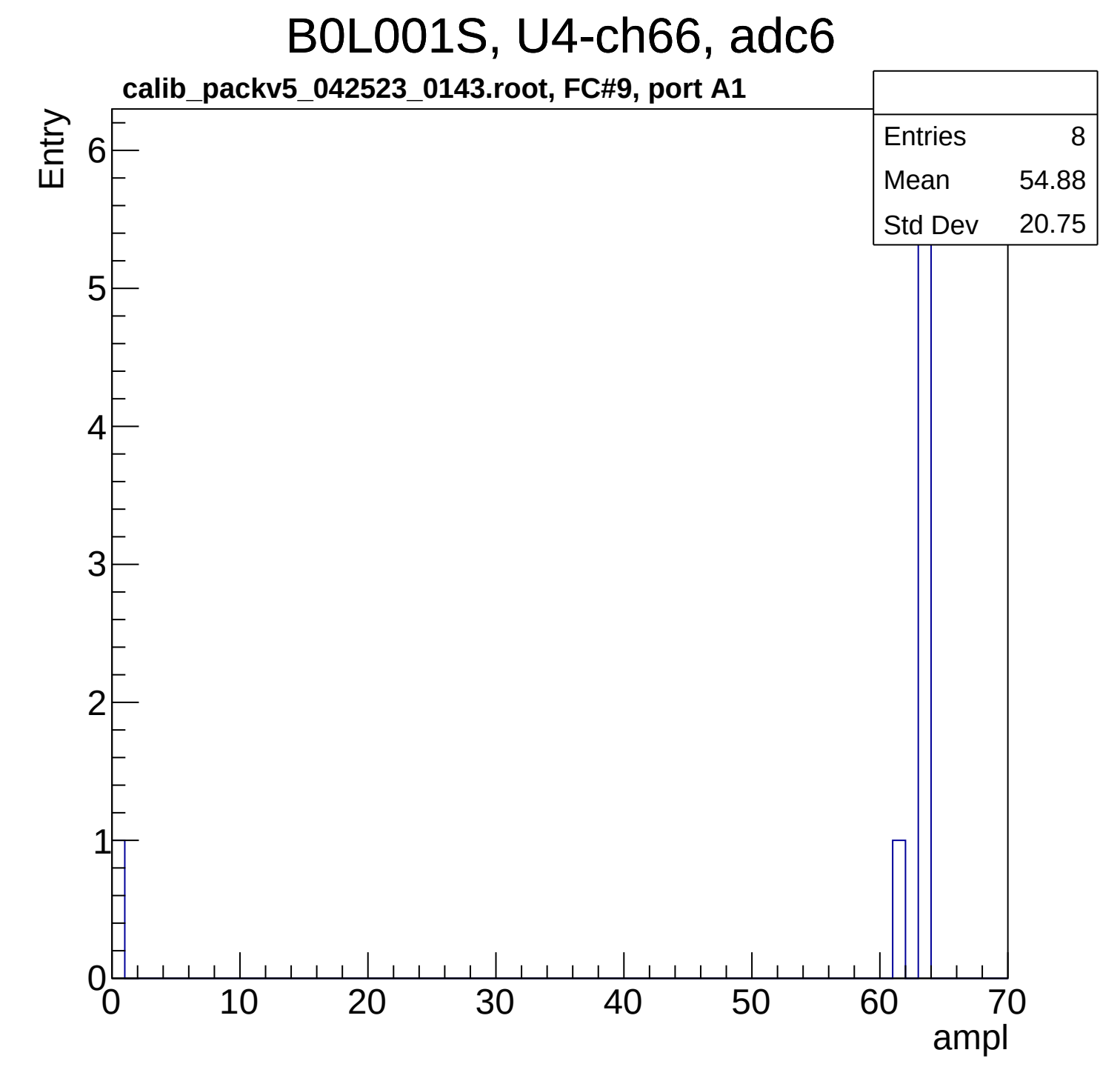
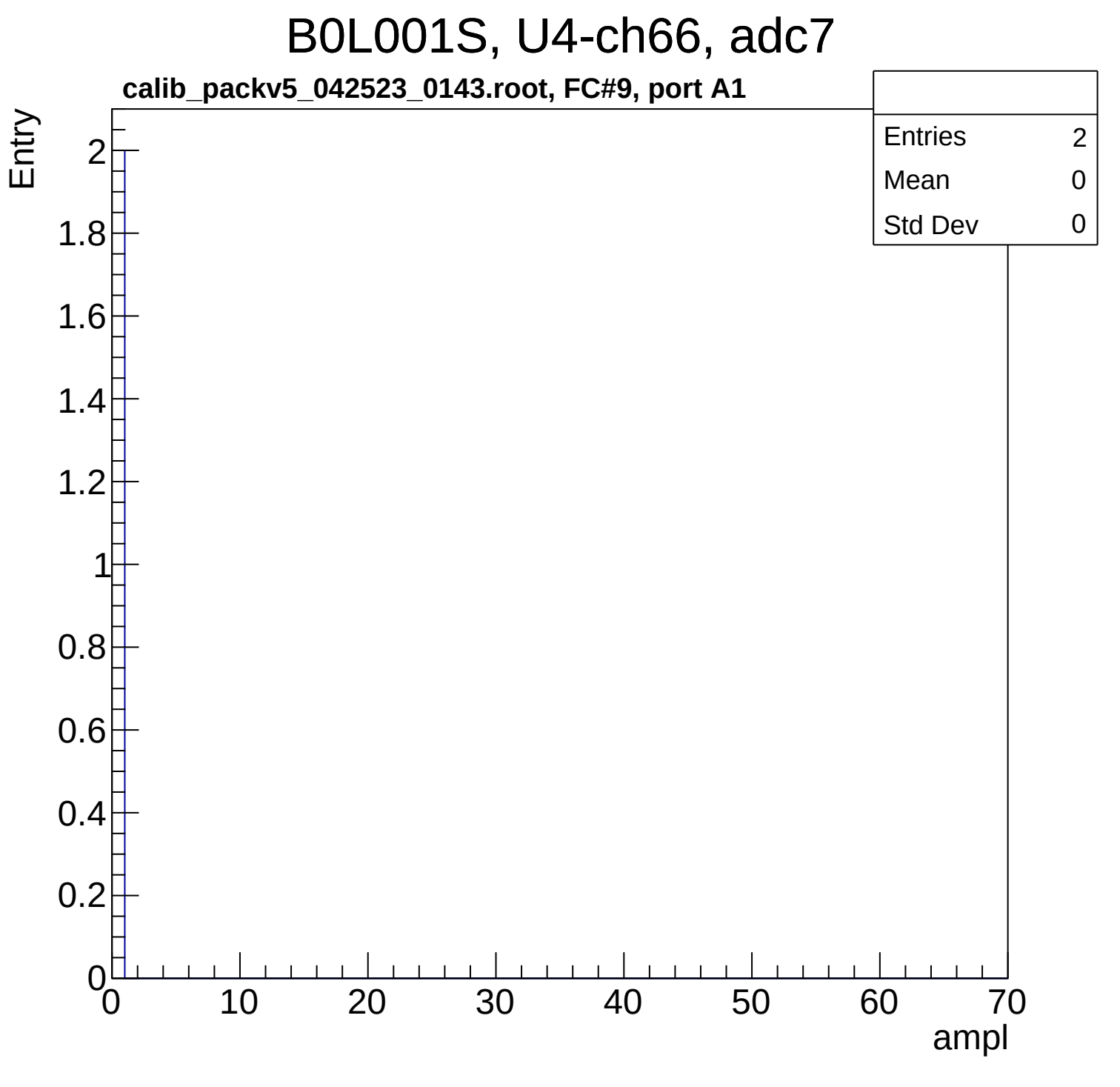
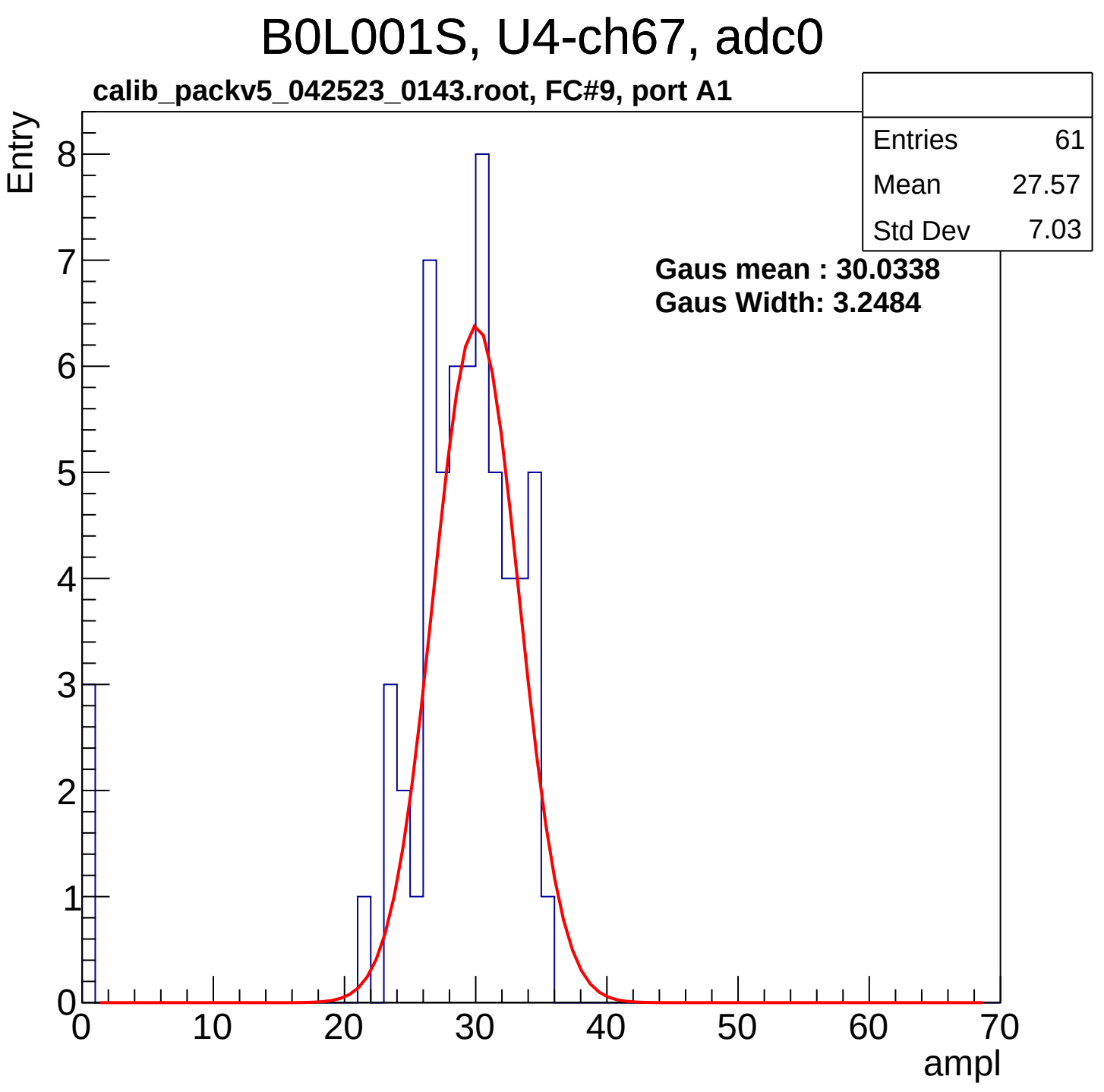
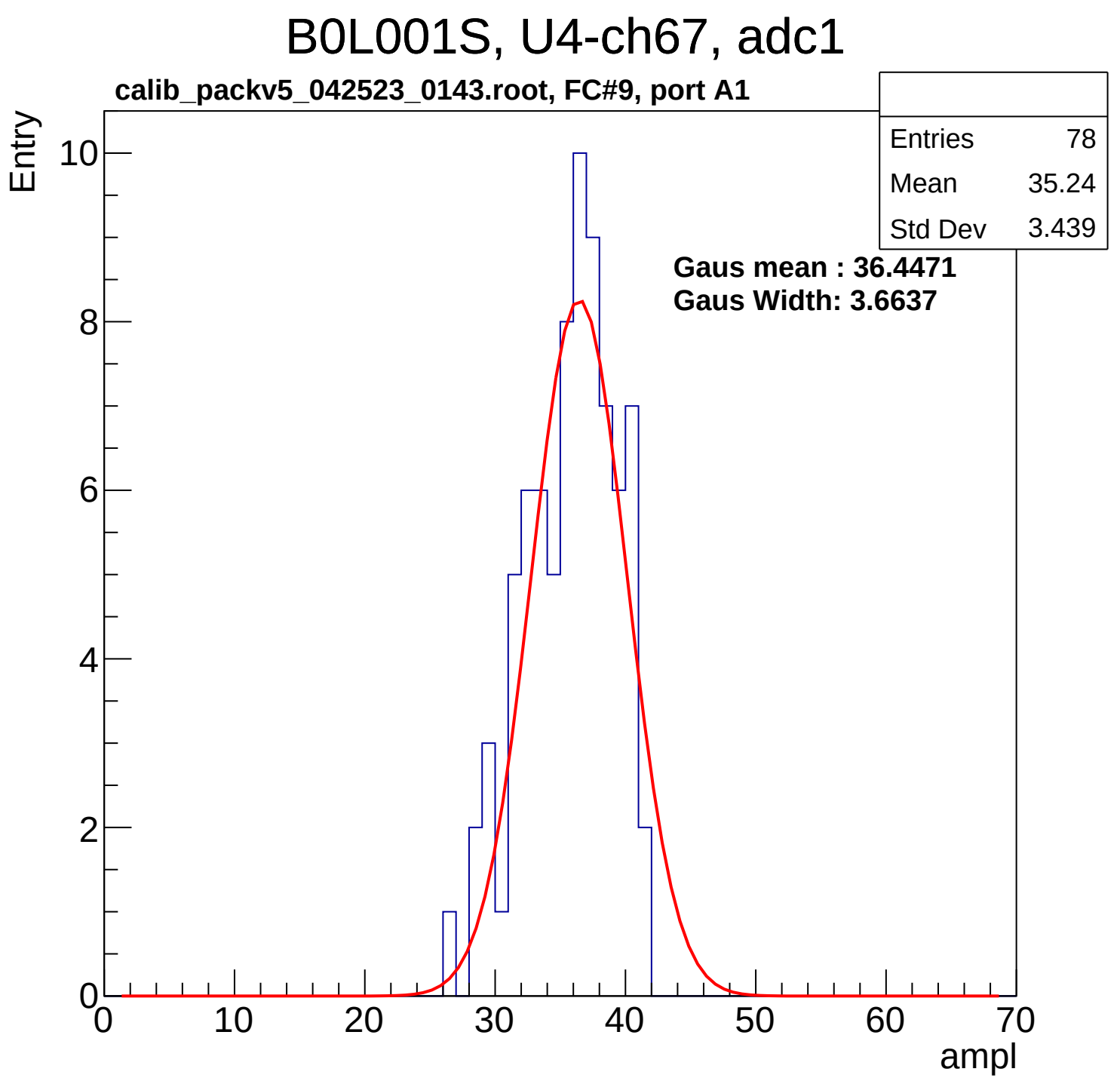

B0L001S, U4-ch66, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.06
Std Dev 2.859
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

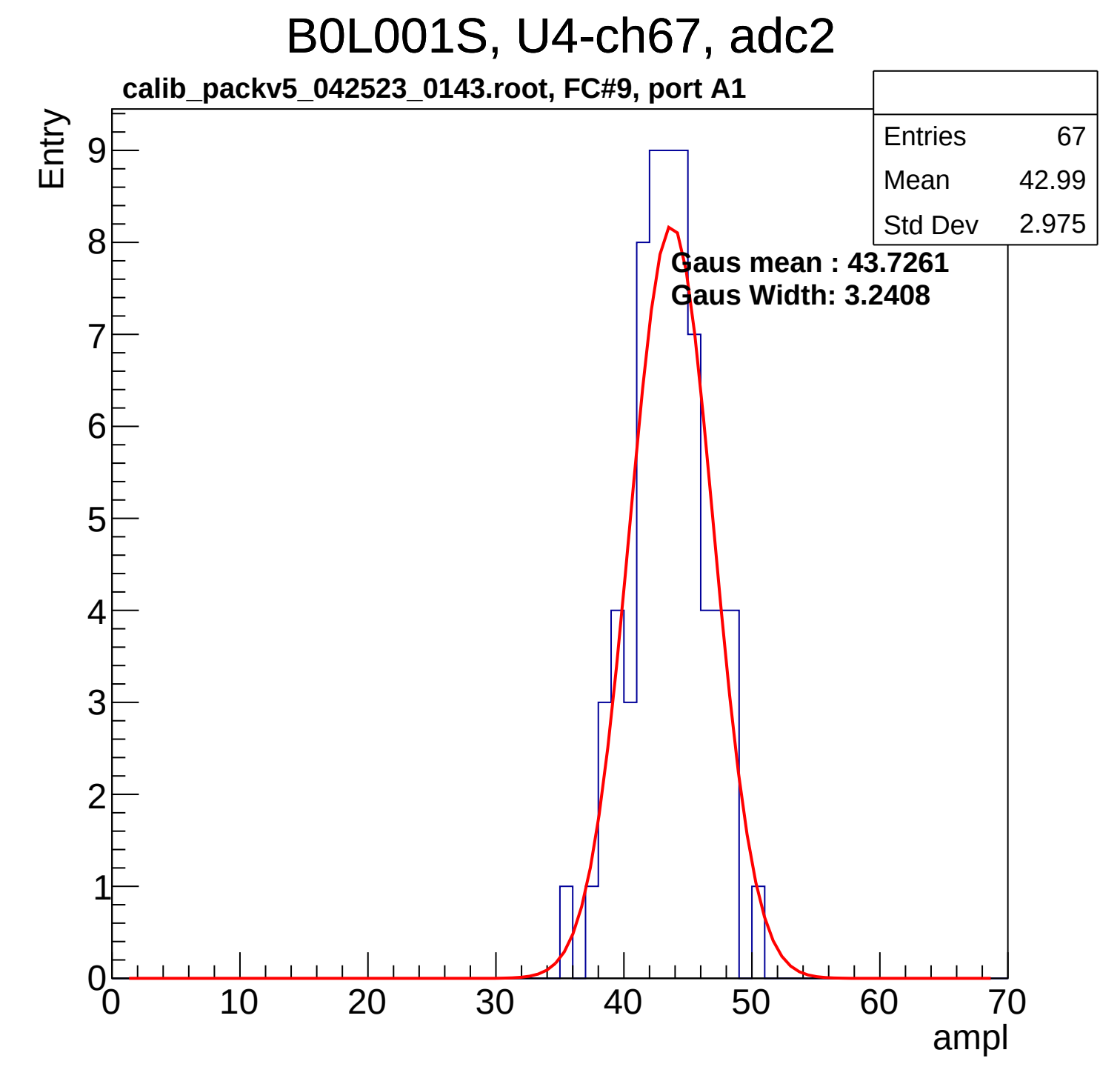

B0L001S, U4-ch66, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 60.19
Std Dev 2.022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

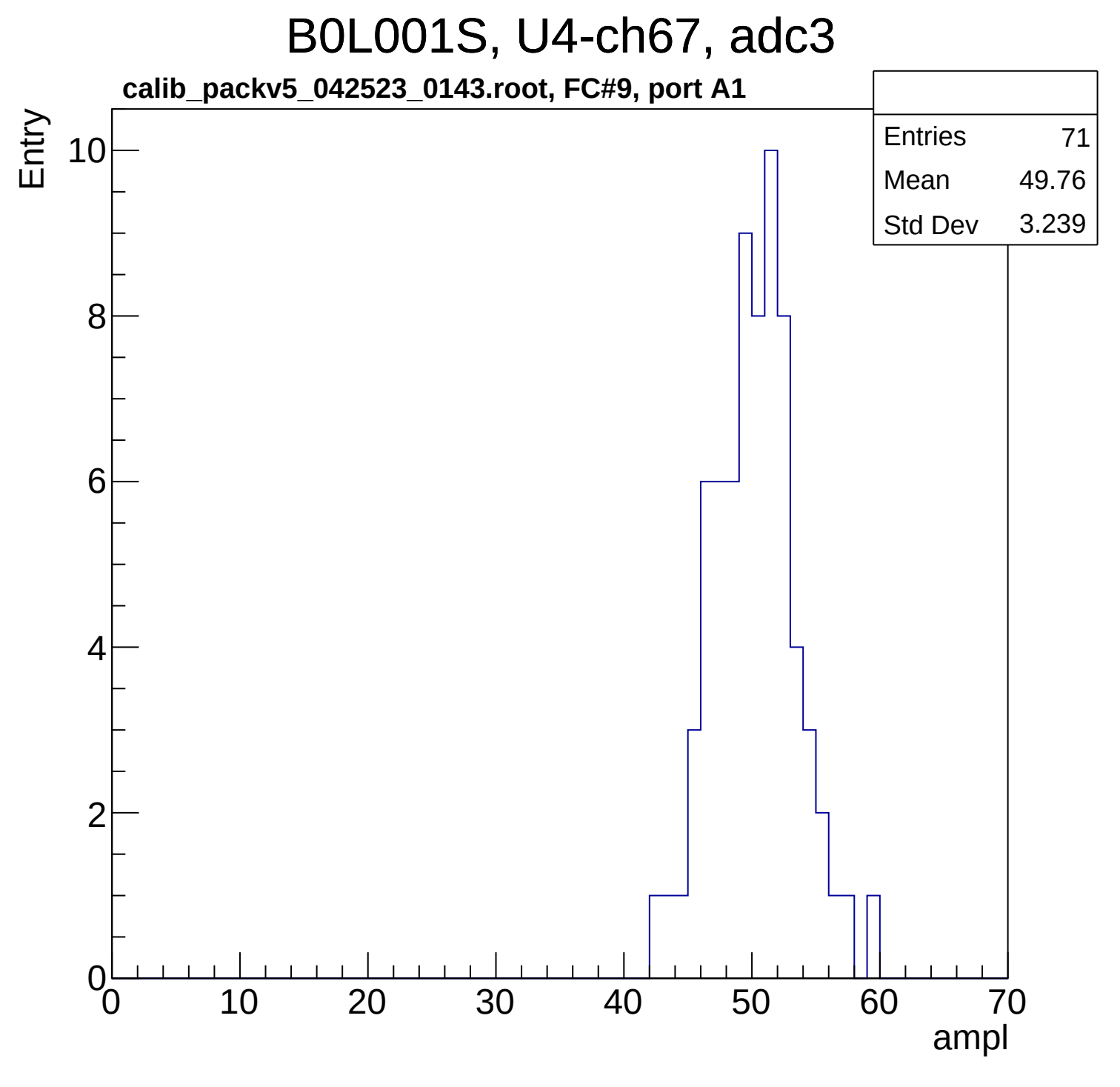

B0L001S, U4-ch66, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 54.88
Std Dev 20.75
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch66, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch67, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 27.57
Std Dev 7.03
Gaus mean : 30.0338
Gaus Width: 3.2484
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch67, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 35.24
Std Dev 3.439
Gaus mean : 36.4471
Gaus Width: 3.6637
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch67, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 42.99
Std Dev 2.975
Gaus mean : 43.7261
Gaus Width: 3.2408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch67, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 49.76
Std Dev 3.239
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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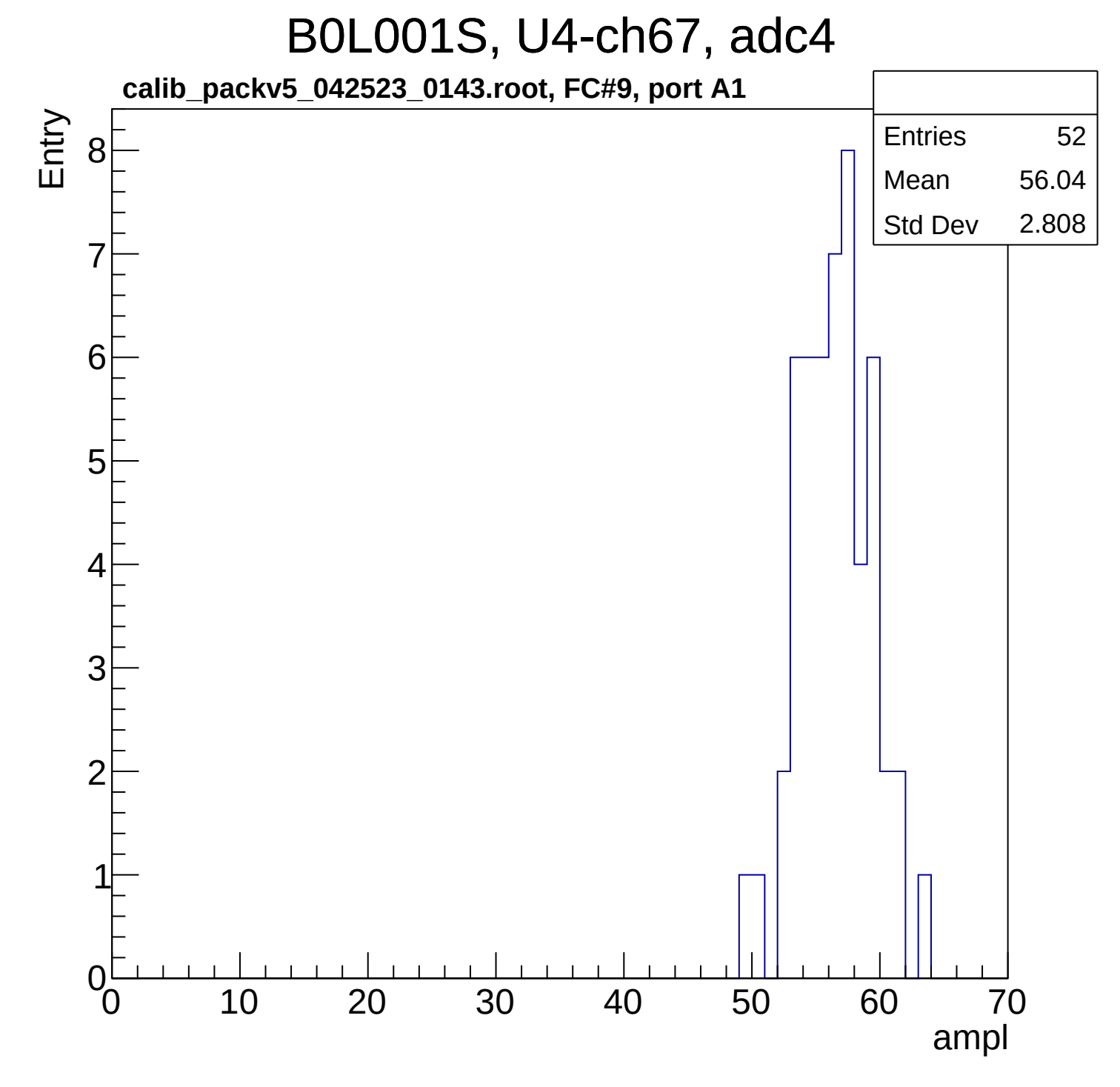
B0L001S, U4-ch67, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.04
Std Dev 2.808
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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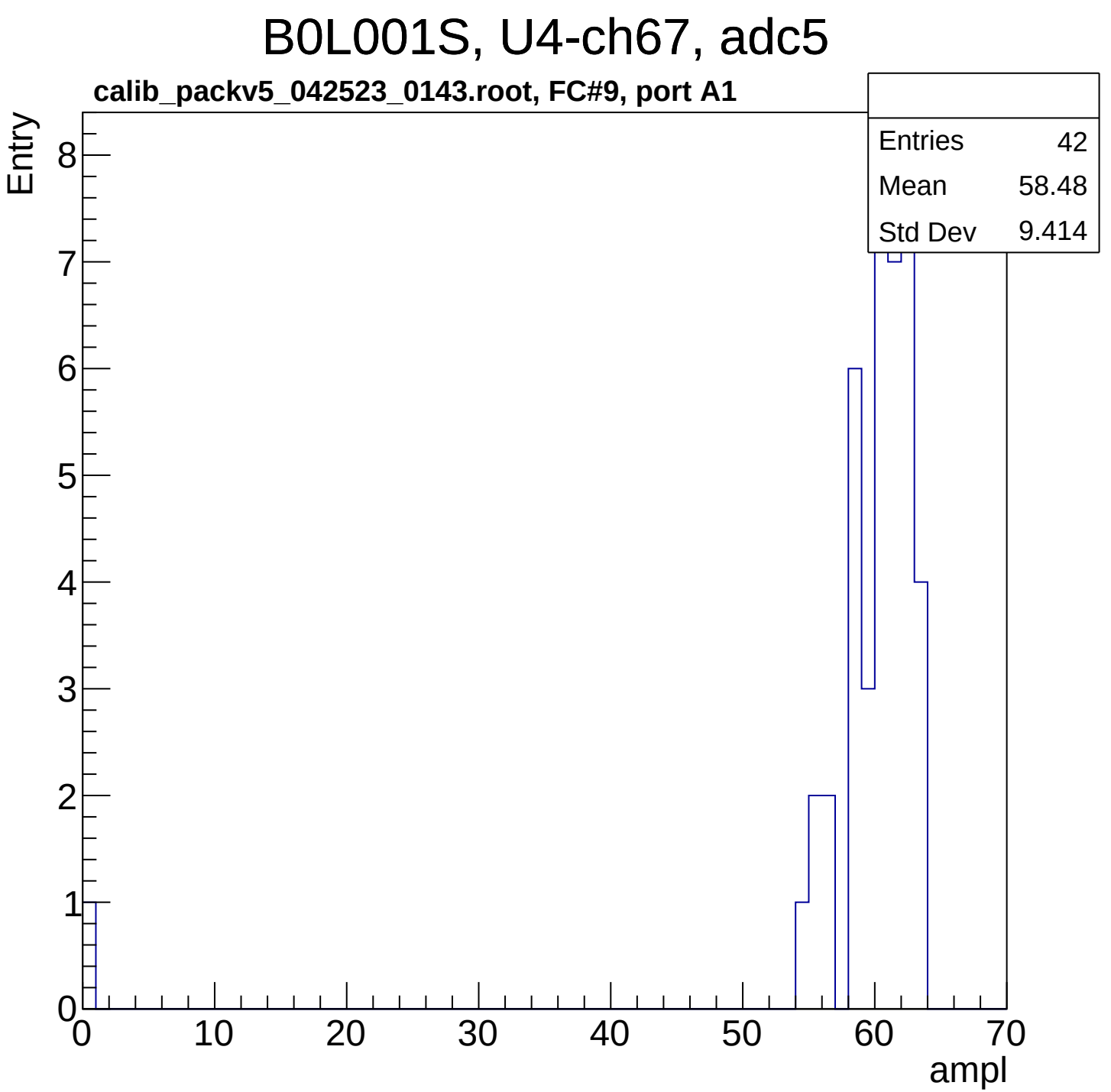
B0L001S, U4-ch67, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.48
Std Dev 9.414
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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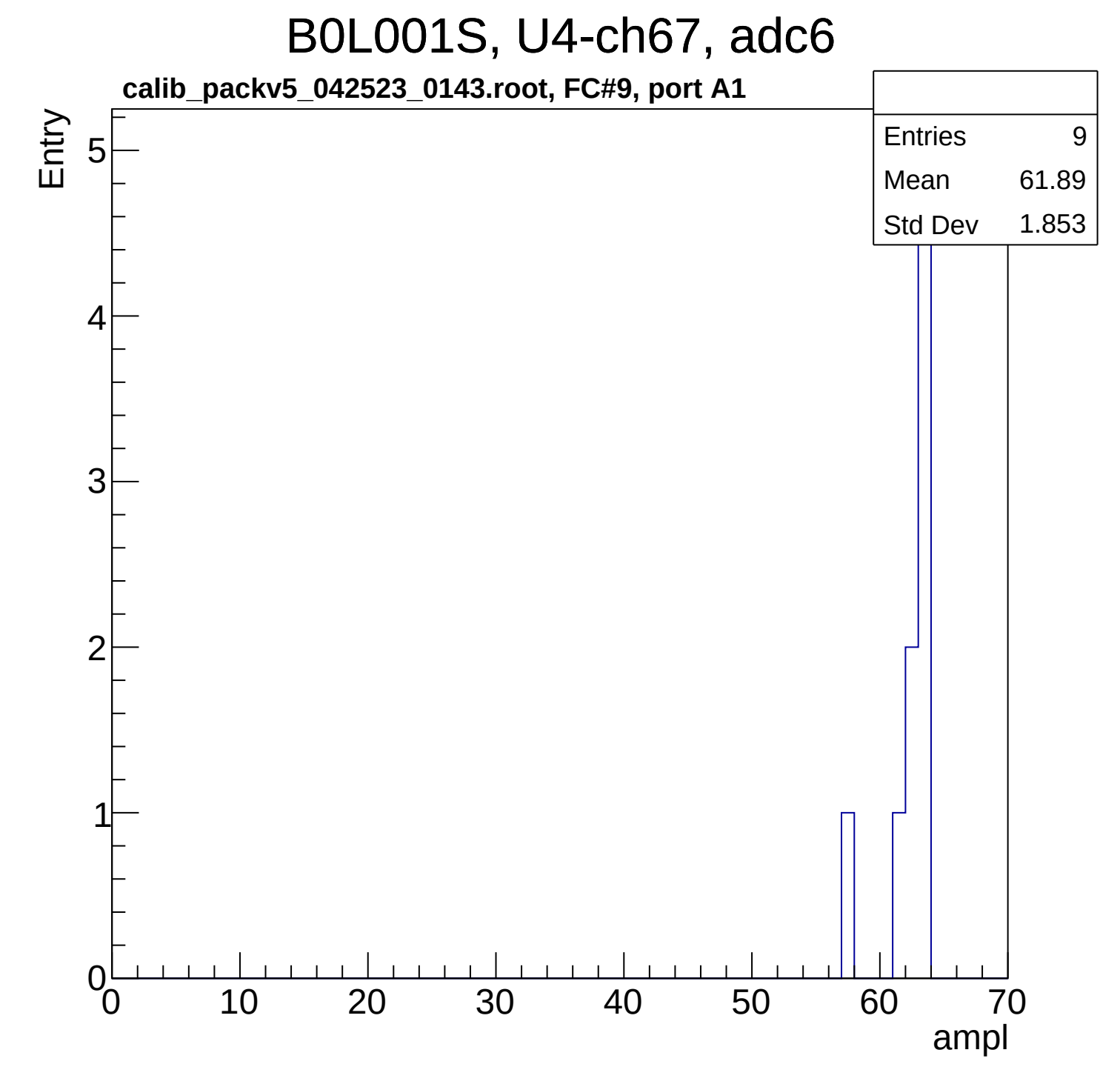
B0L001S, U4-ch67, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.89
Std Dev 1.853
Entry
ampl
0
1
2
3
4
5
0
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20
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40
50
60
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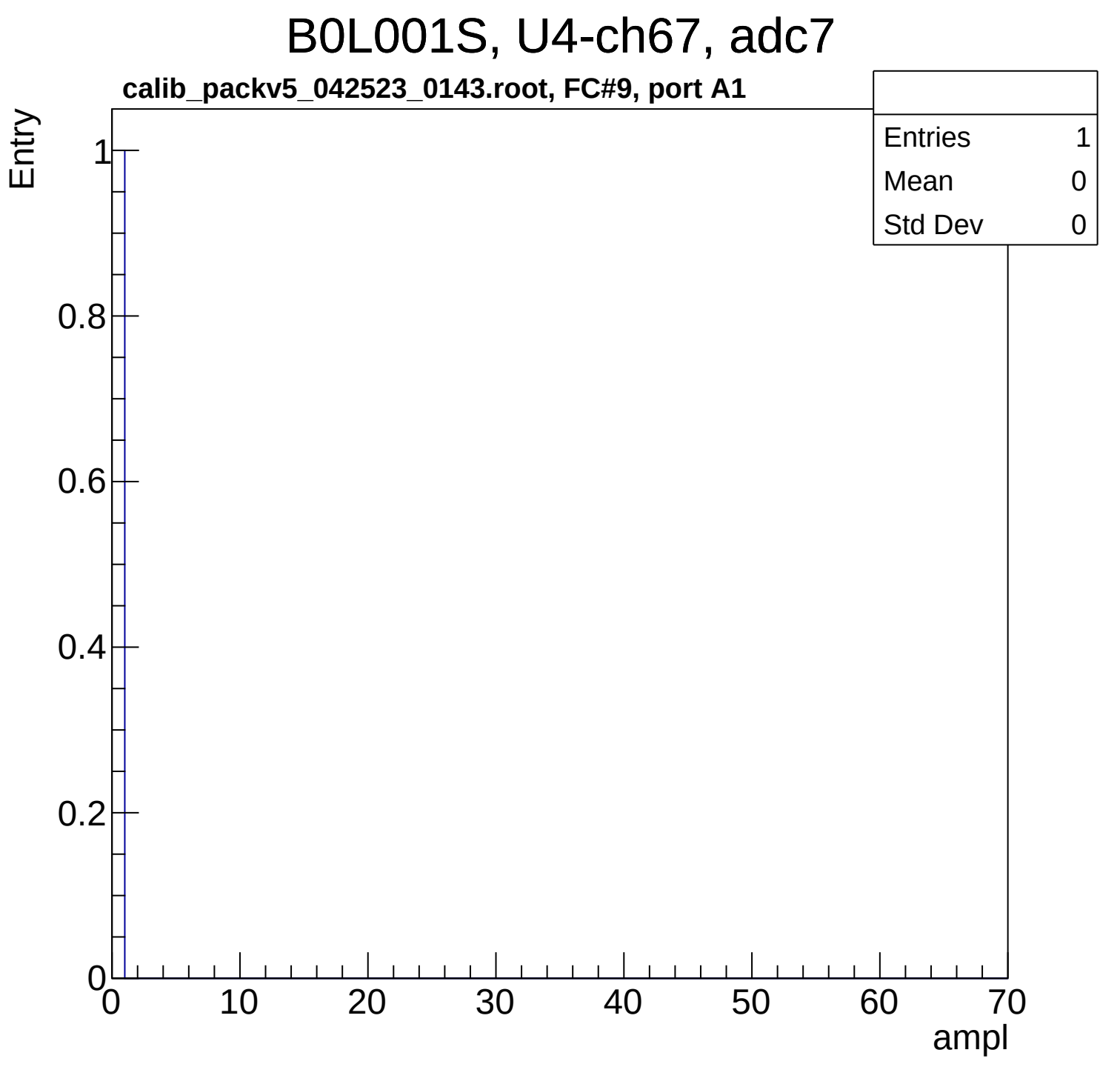
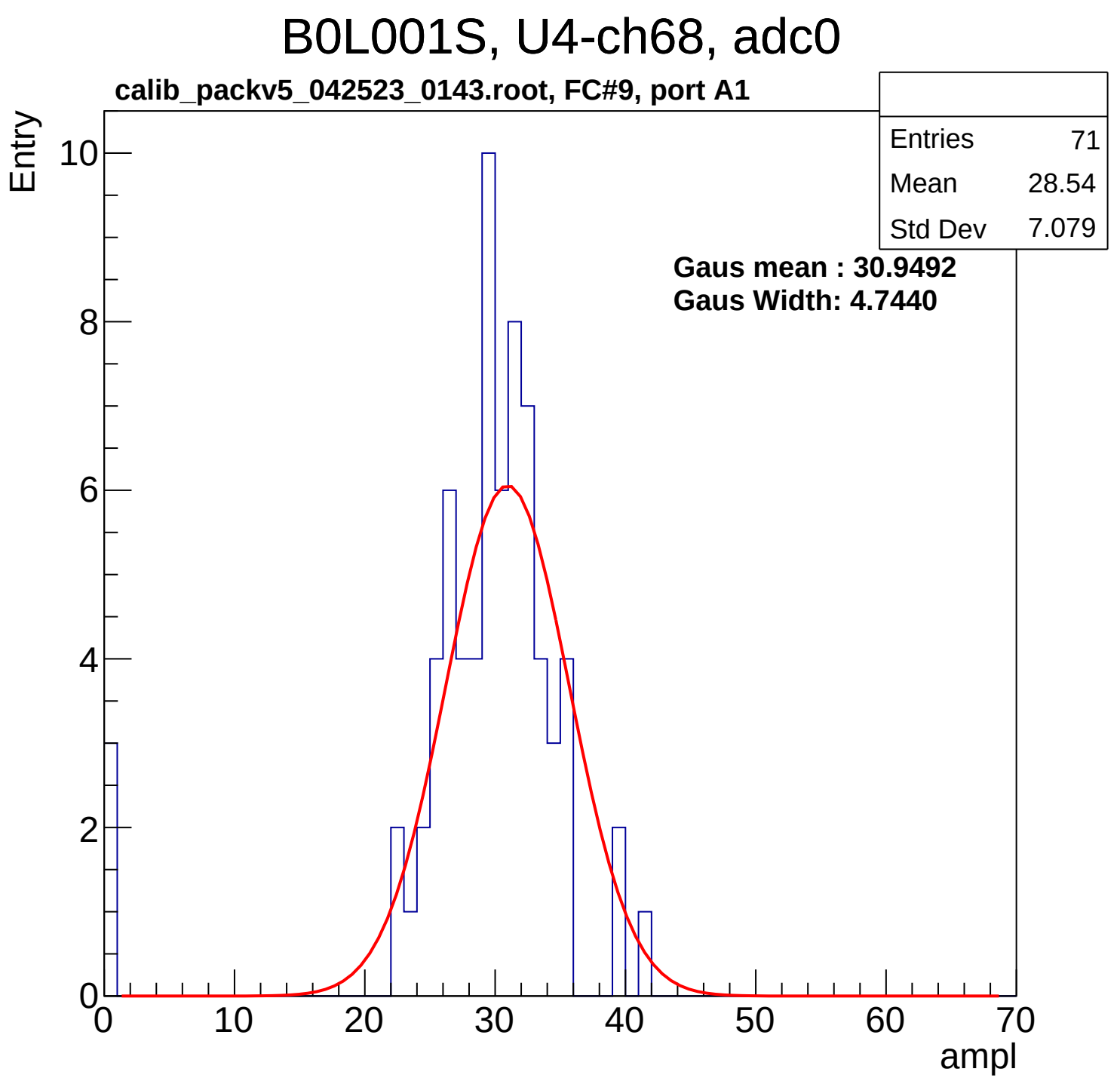
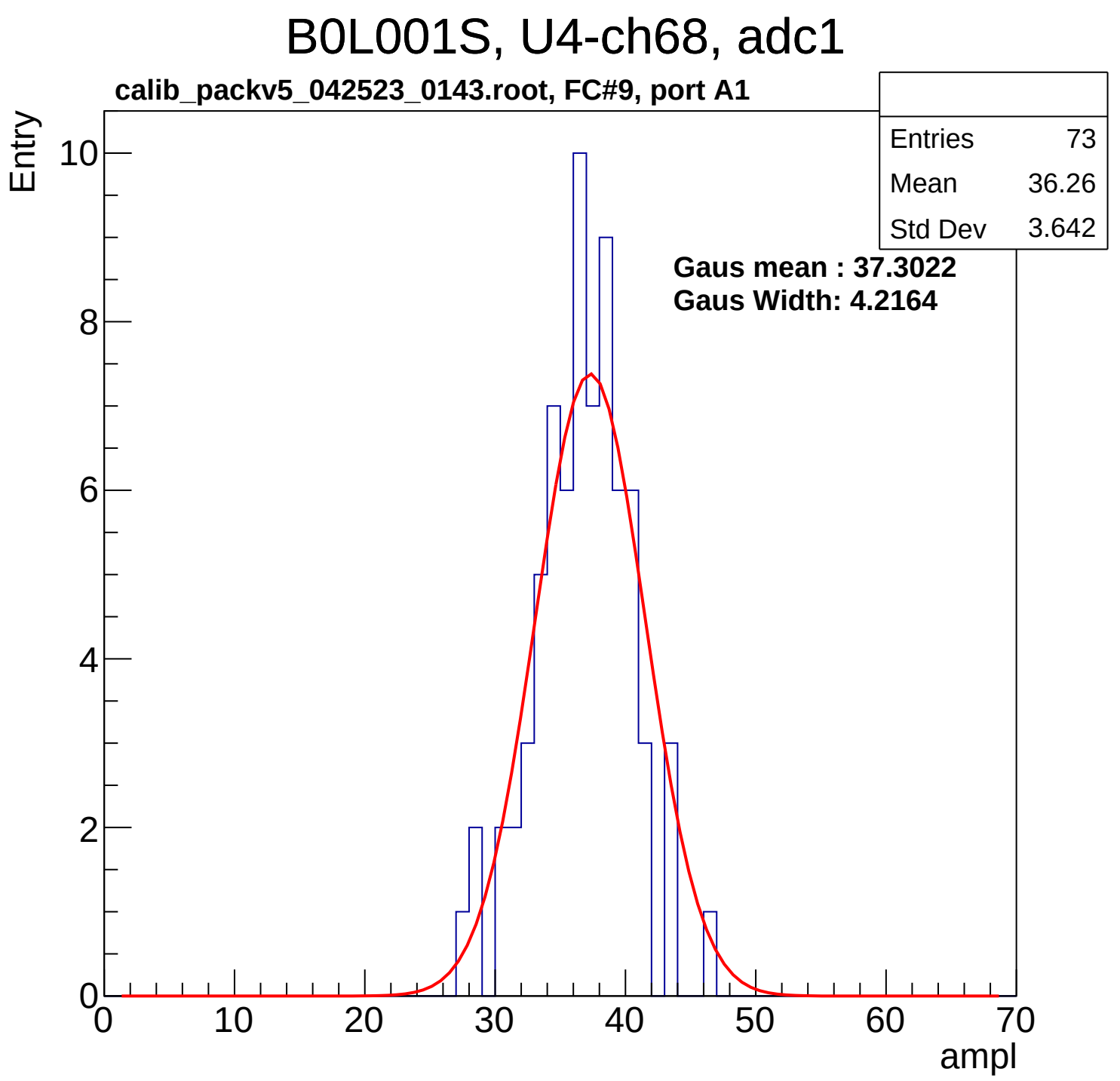
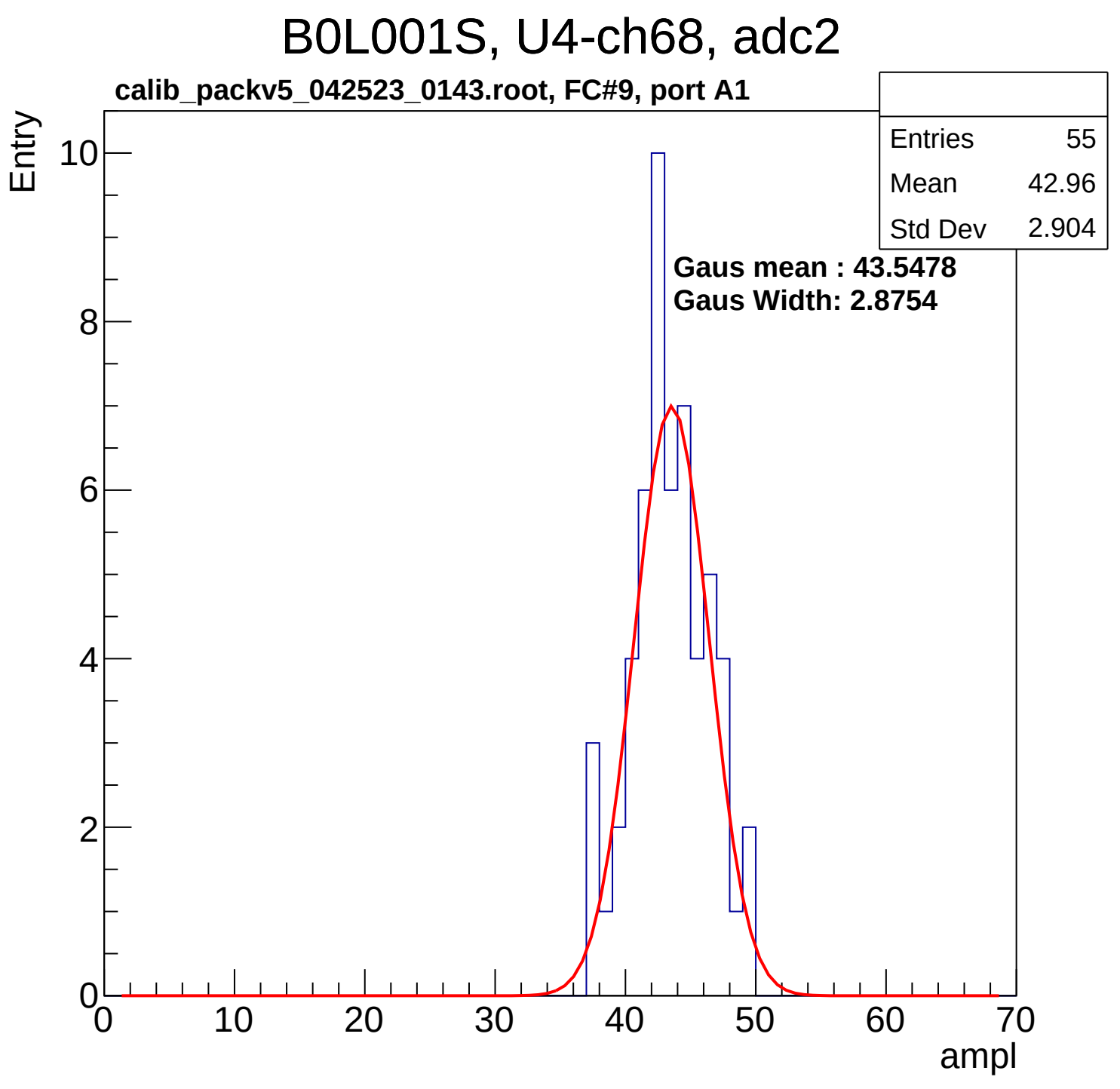
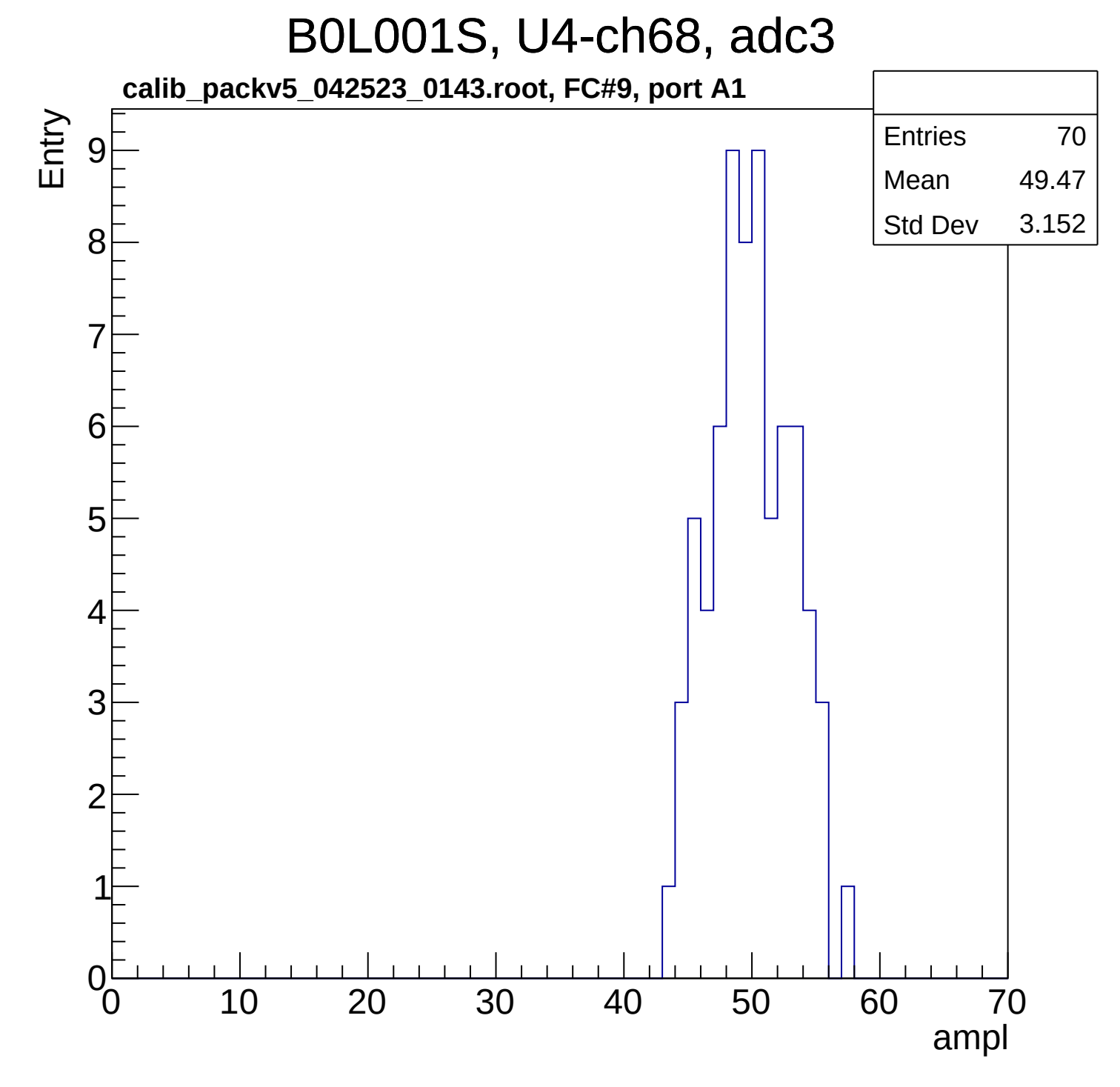

B0L001S, U4-ch67, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch68, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.54
Std Dev 7.079
Gaus mean : 30.9492
Gaus Width: 4.7440
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch68, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 36.26
Std Dev 3.642
Gaus mean : 37.3022
Gaus Width: 4.2164
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch68, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.96
Std Dev 2.904
Gaus mean : 43.5478
Gaus Width: 2.8754
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch68, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.47
Std Dev 3.152
Entry
ampl
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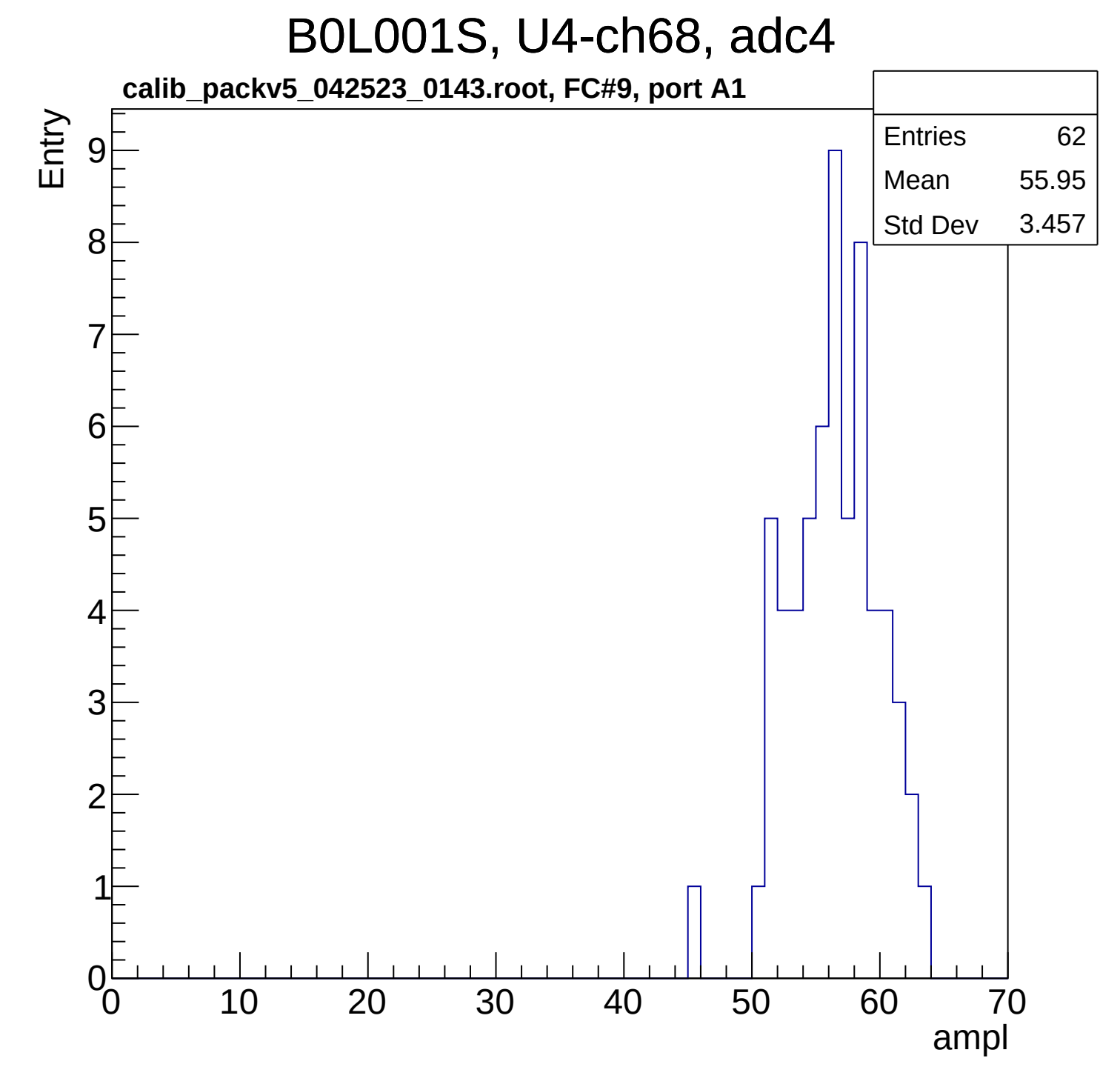
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch68, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.95
Std Dev 3.457
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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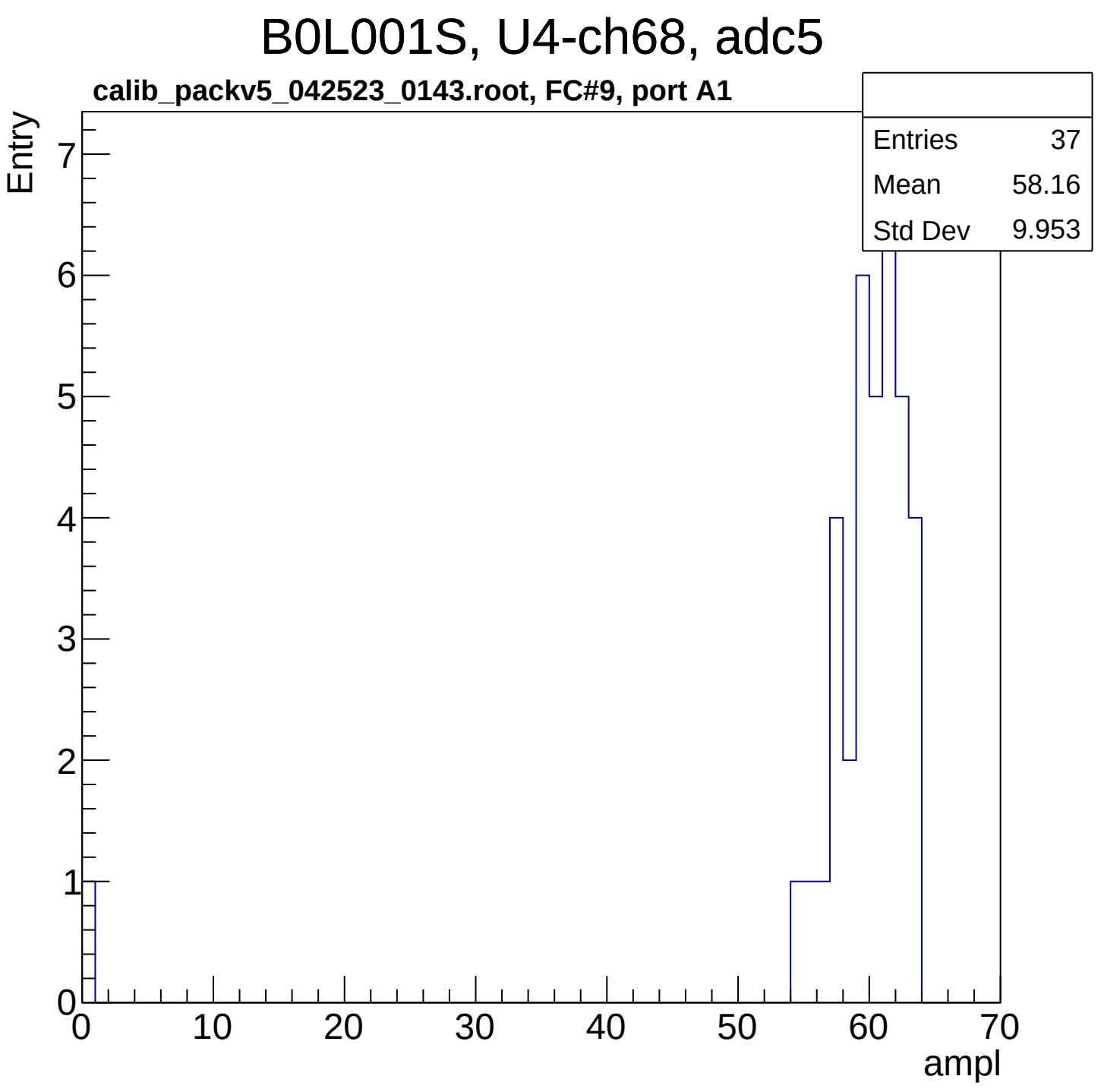
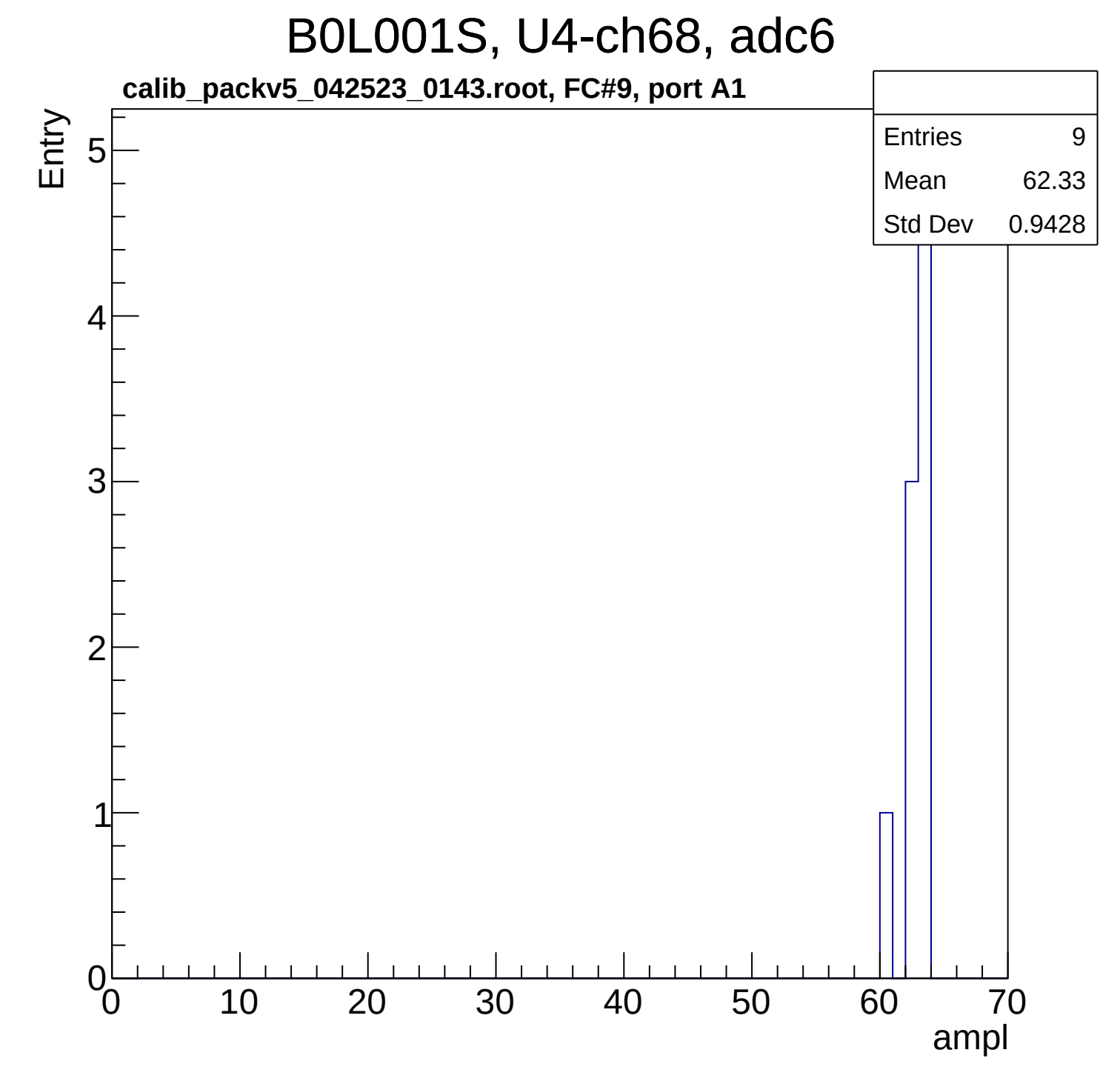

B0L001S, U4-ch68, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.16
Std Dev 9.953
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch68, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 62.33
Std Dev 0.9428
Entry
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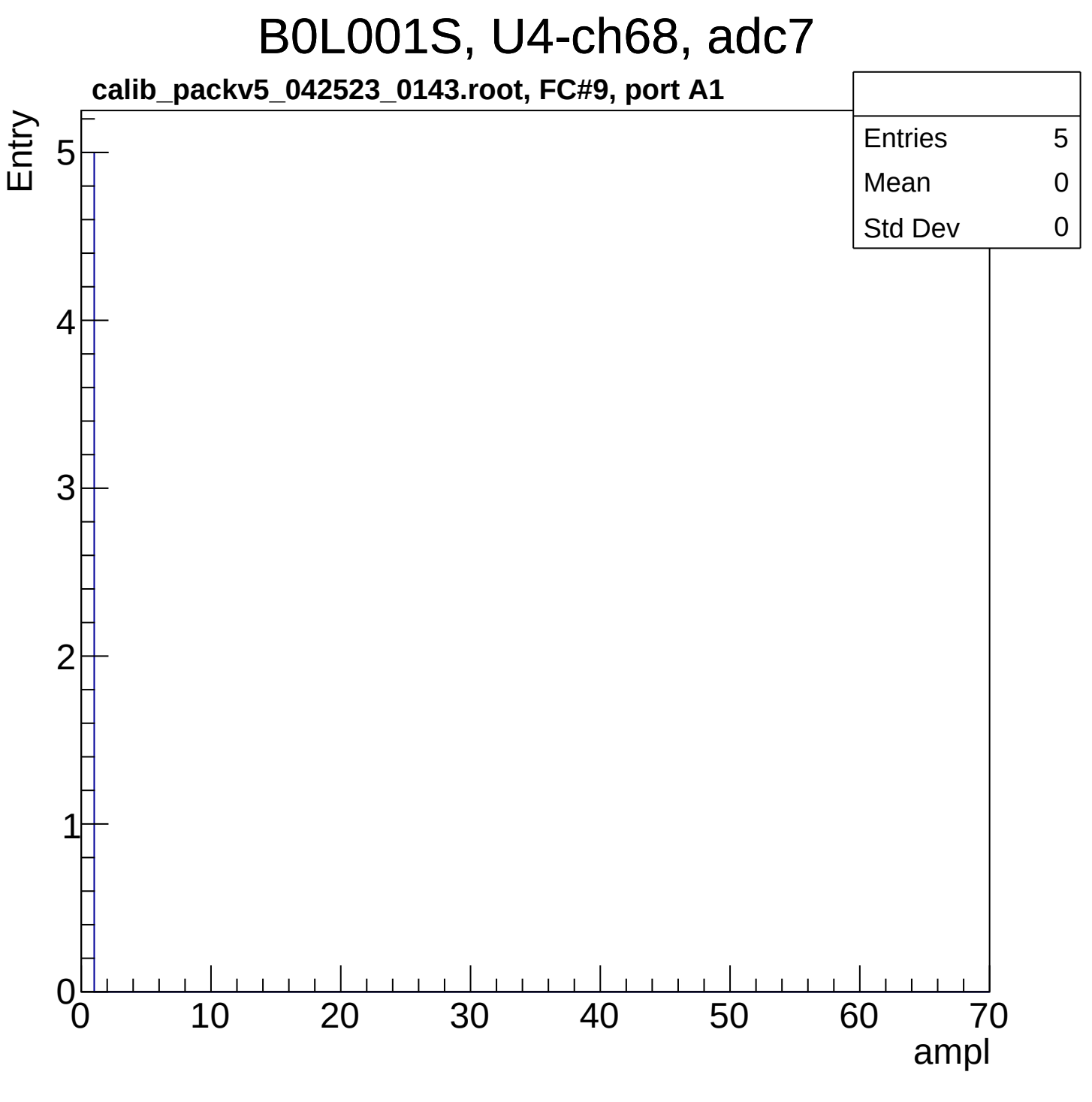
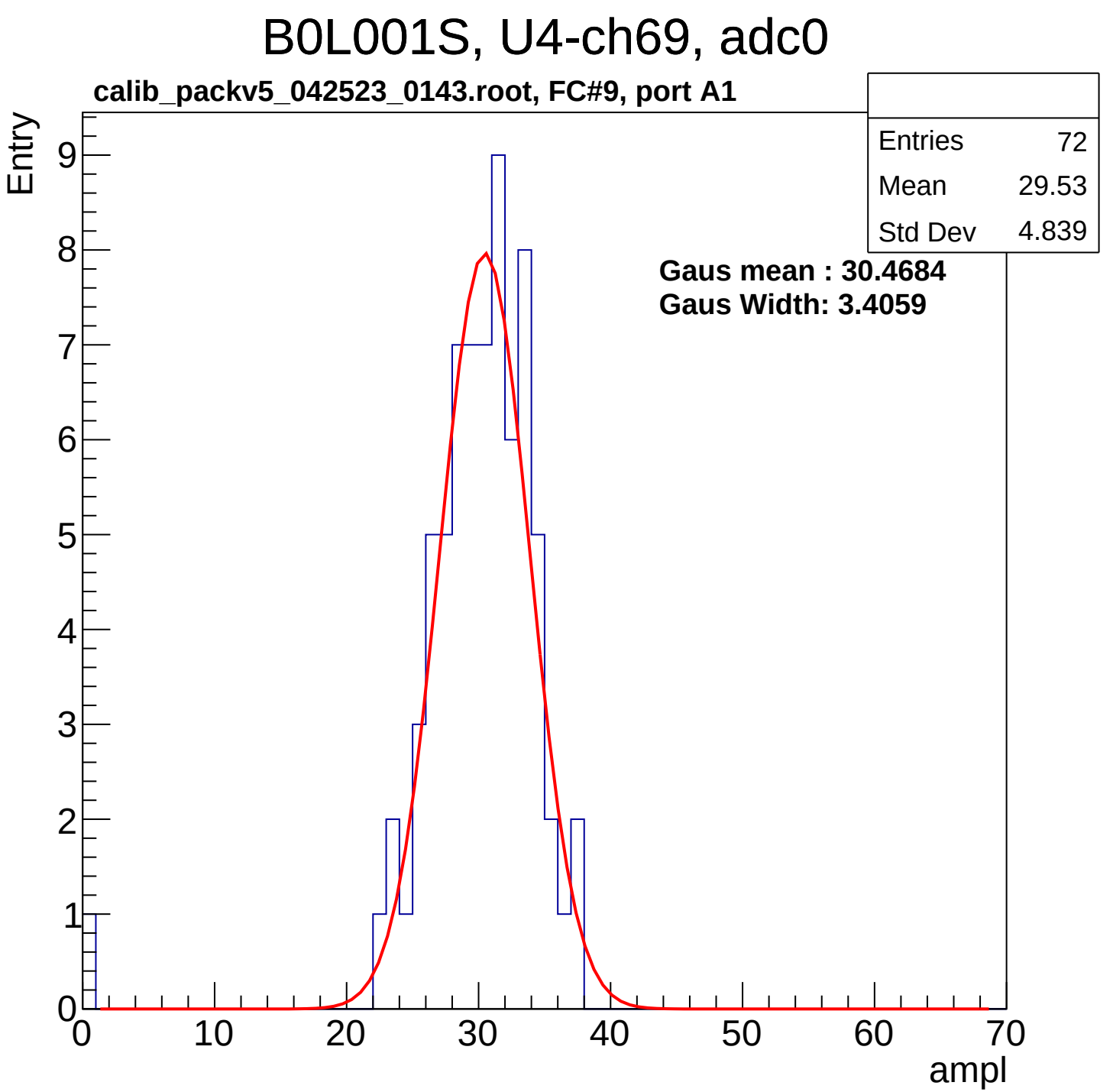
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch68, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 0
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch69, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.53
Std Dev 4.839
Gaus mean : 30.4684
Gaus Width: 3.4059
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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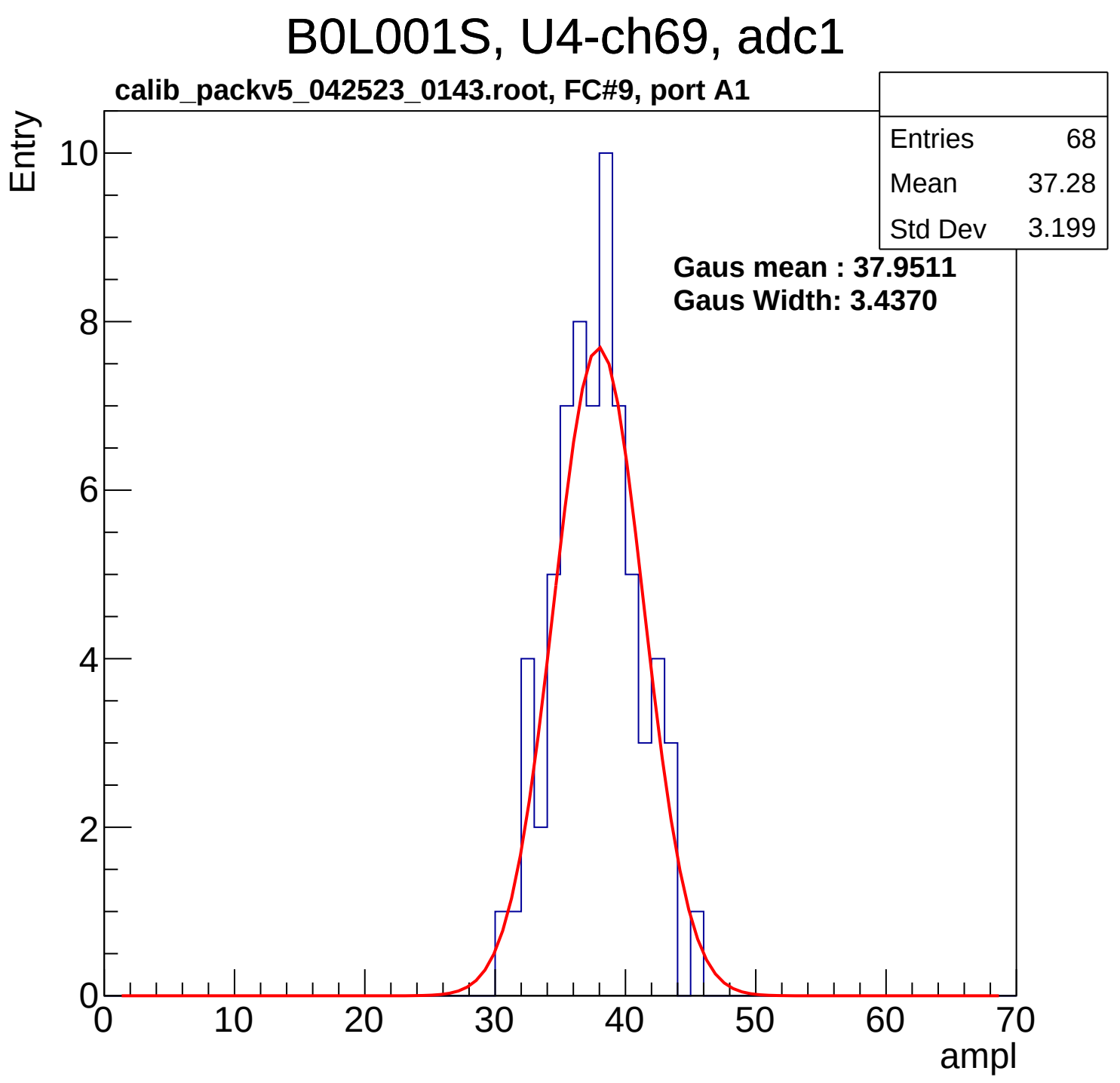
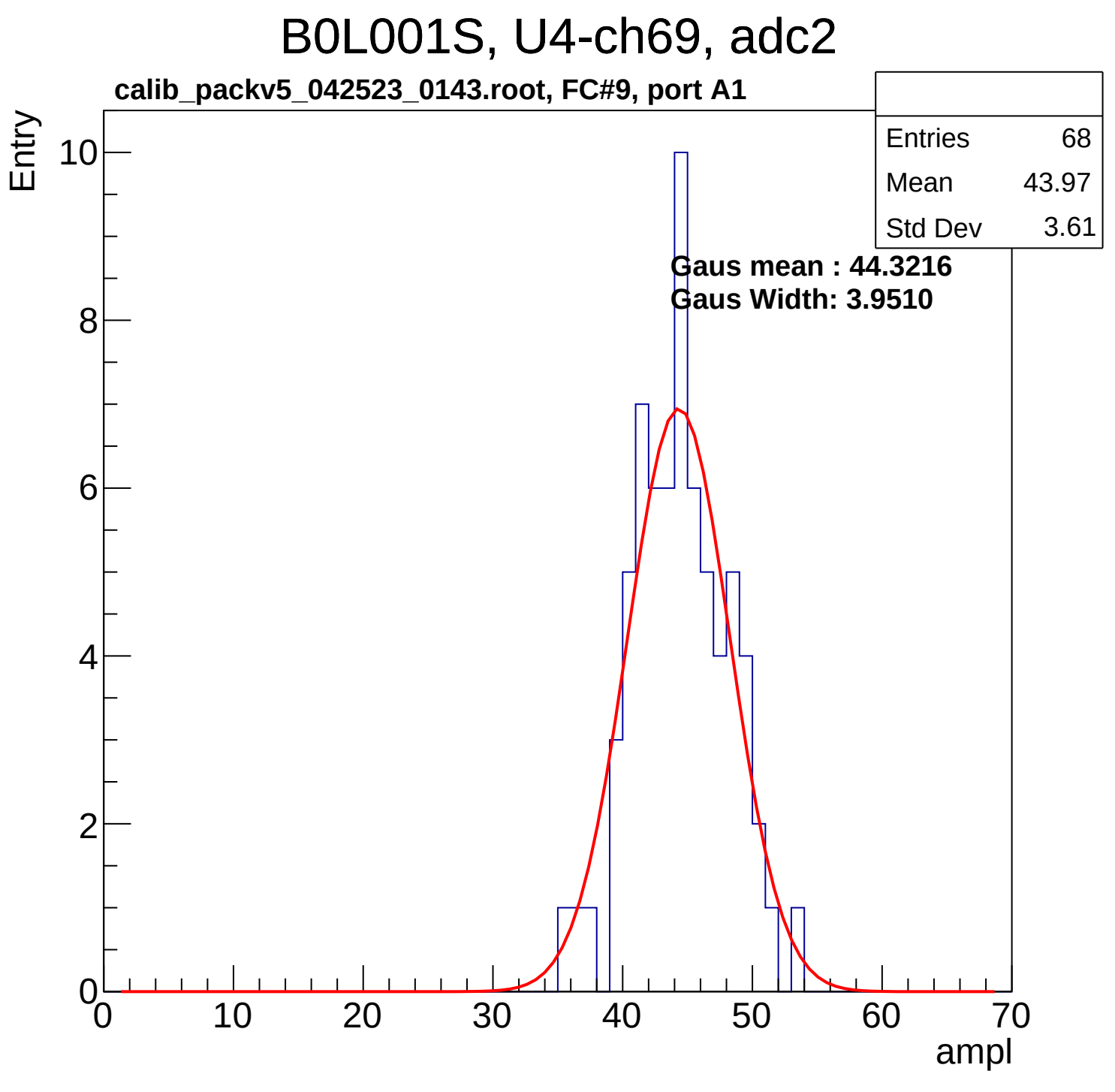
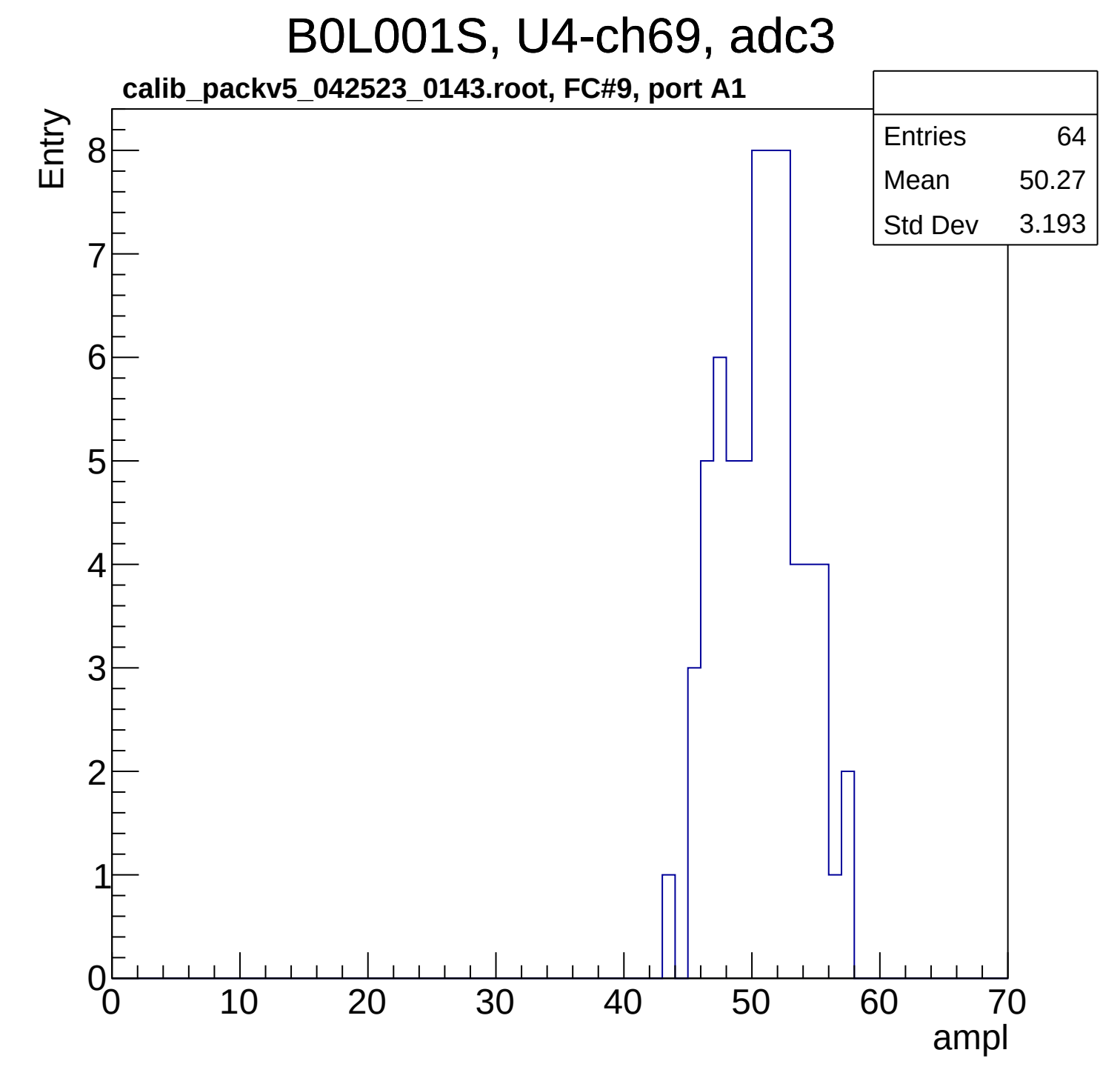

B0L001S, U4-ch69, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.28
Std Dev 3.199
Gaus mean : 37.9511
Gaus Width: 3.4370
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch69, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.97
Std Dev 3.61
Gaus mean : 44.3216
Gaus Width: 3.9510
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch69, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.27
Std Dev 3.193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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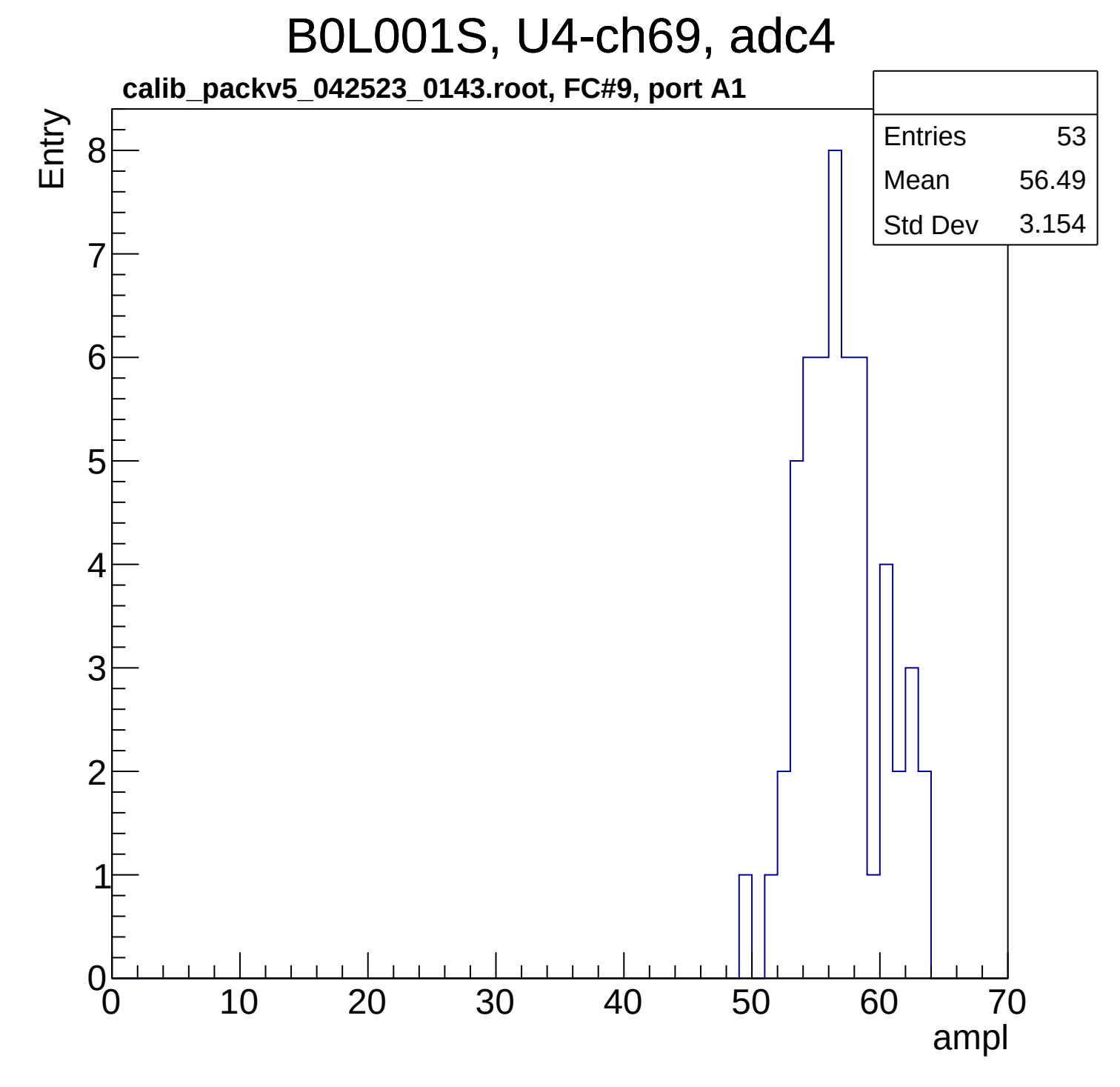
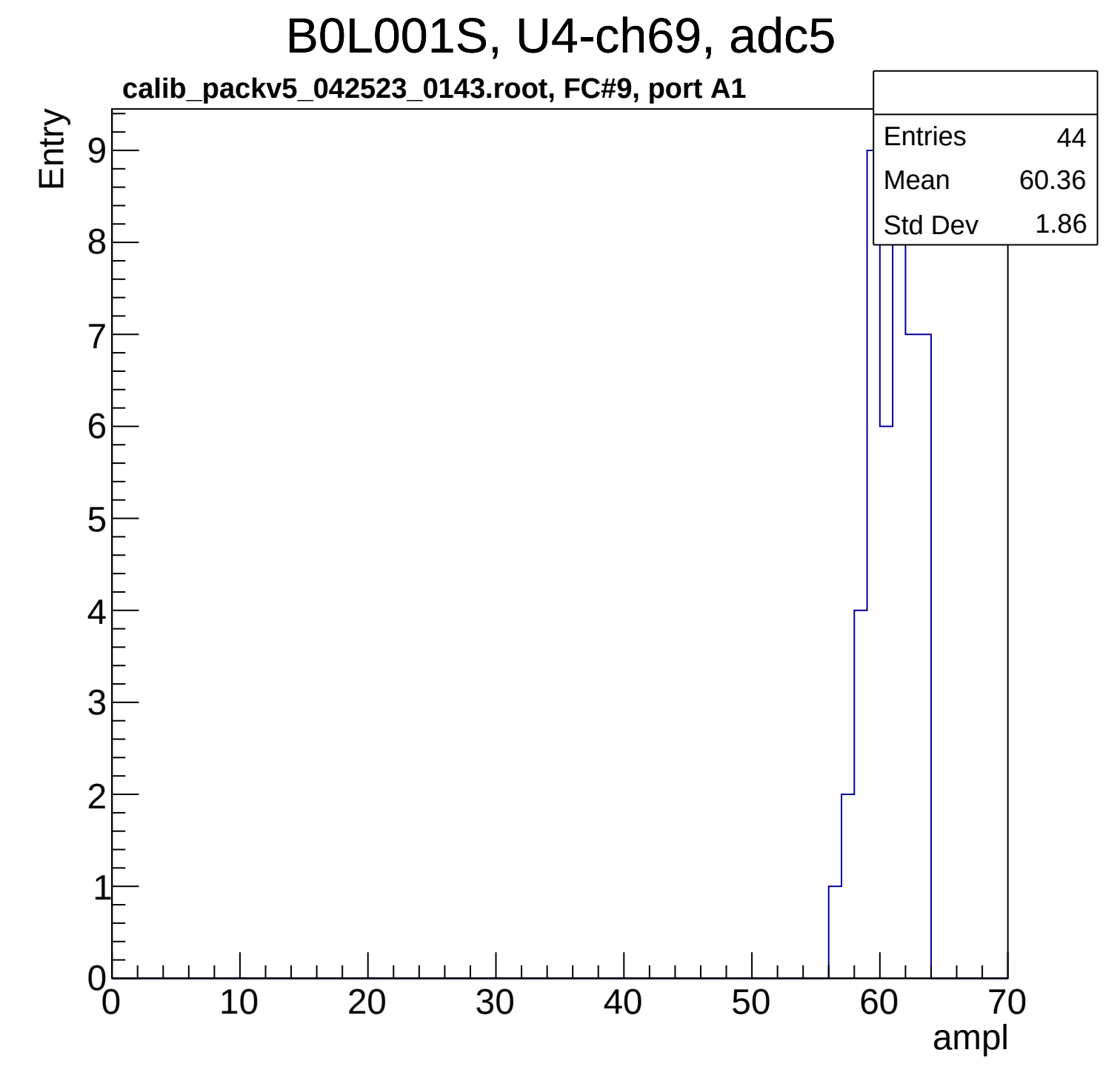
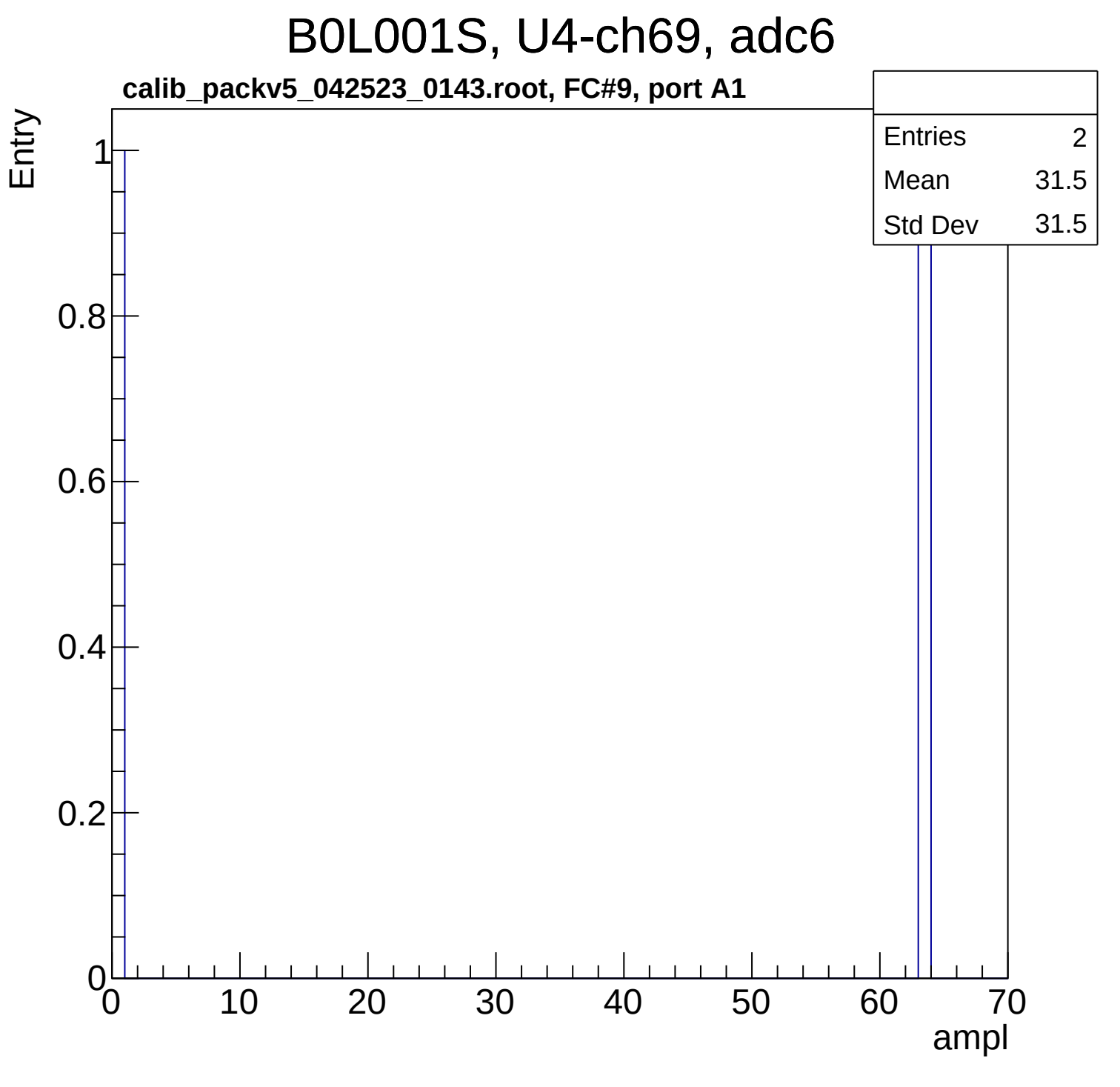
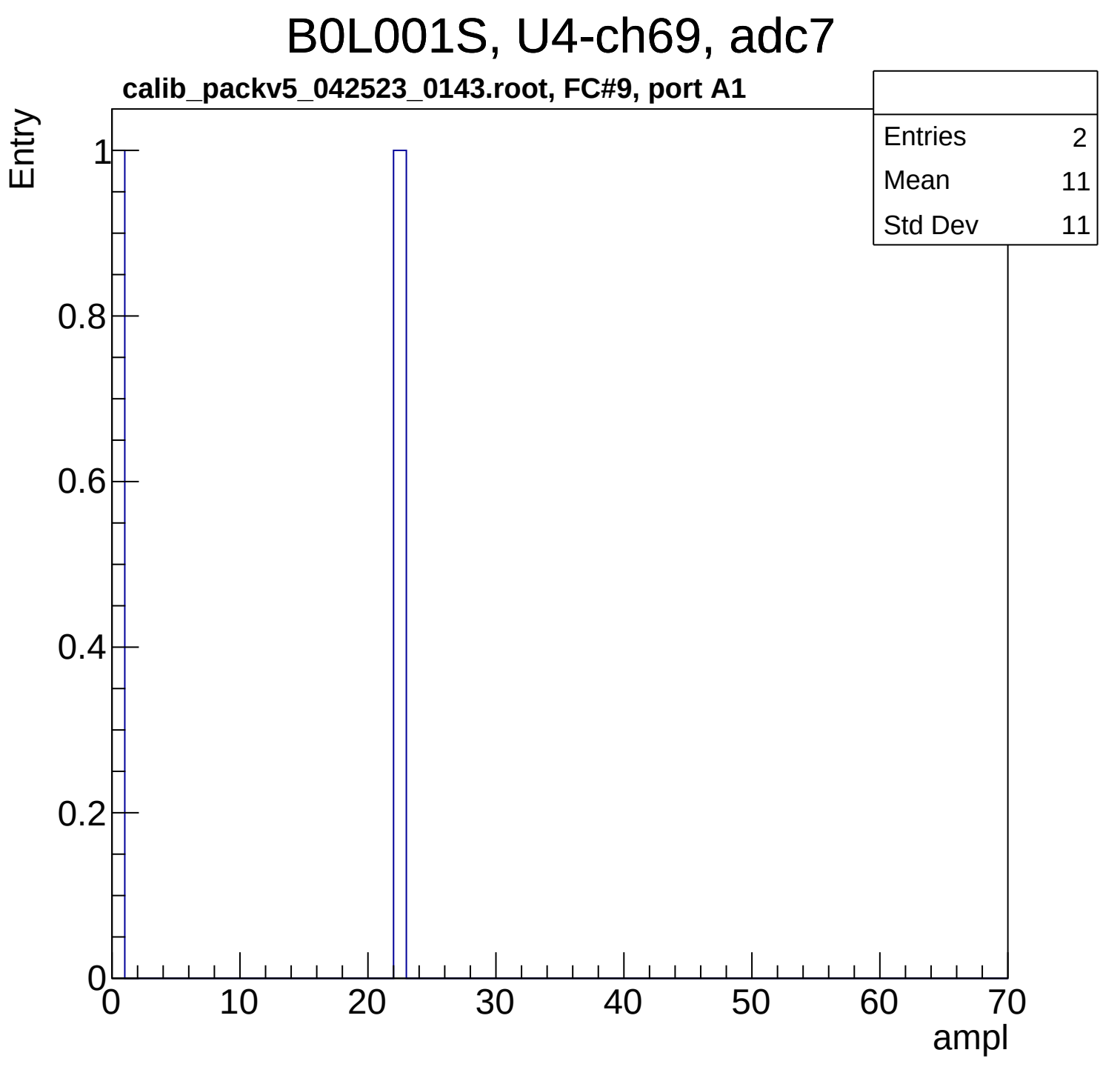
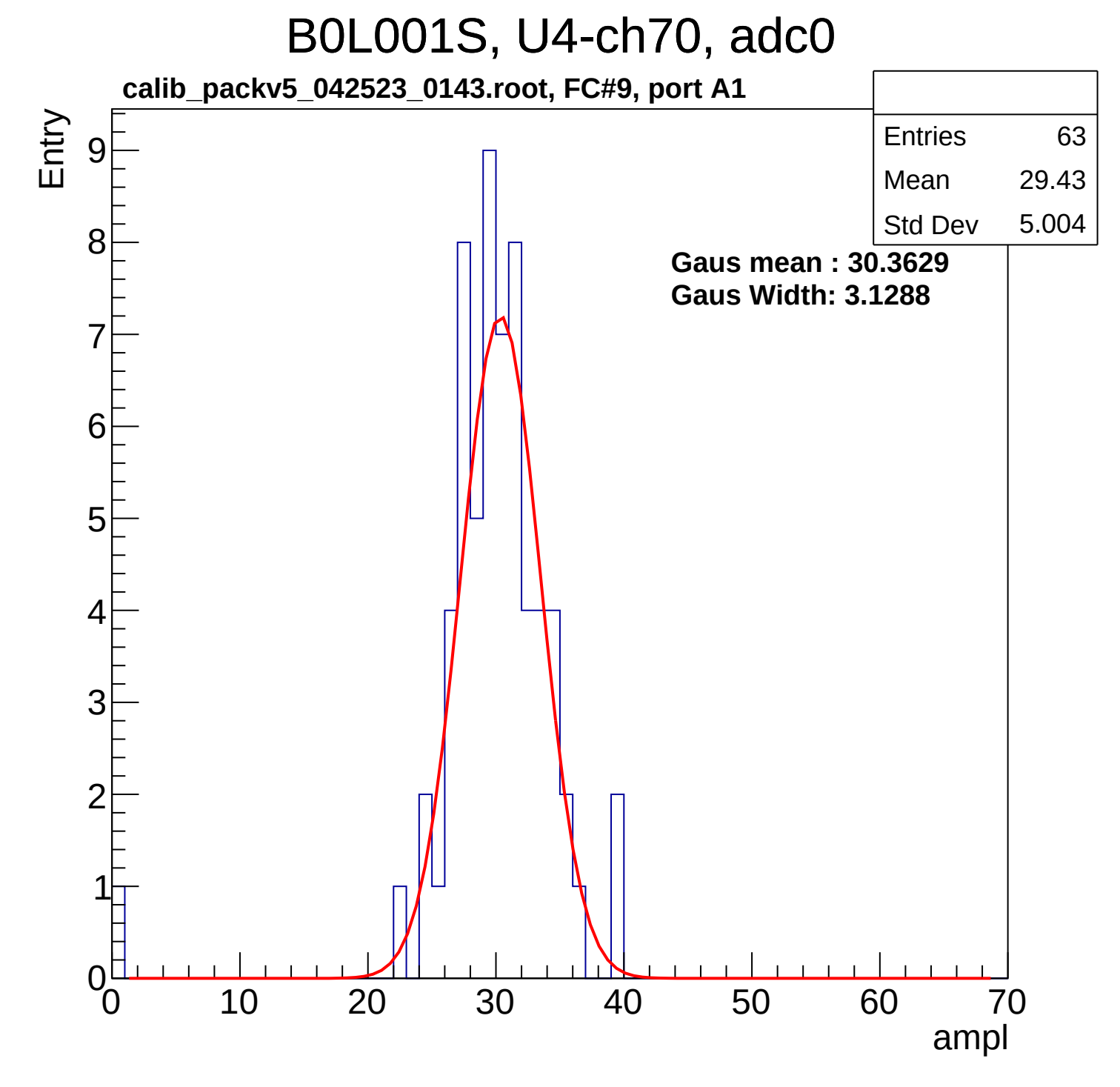
40
50
60
70

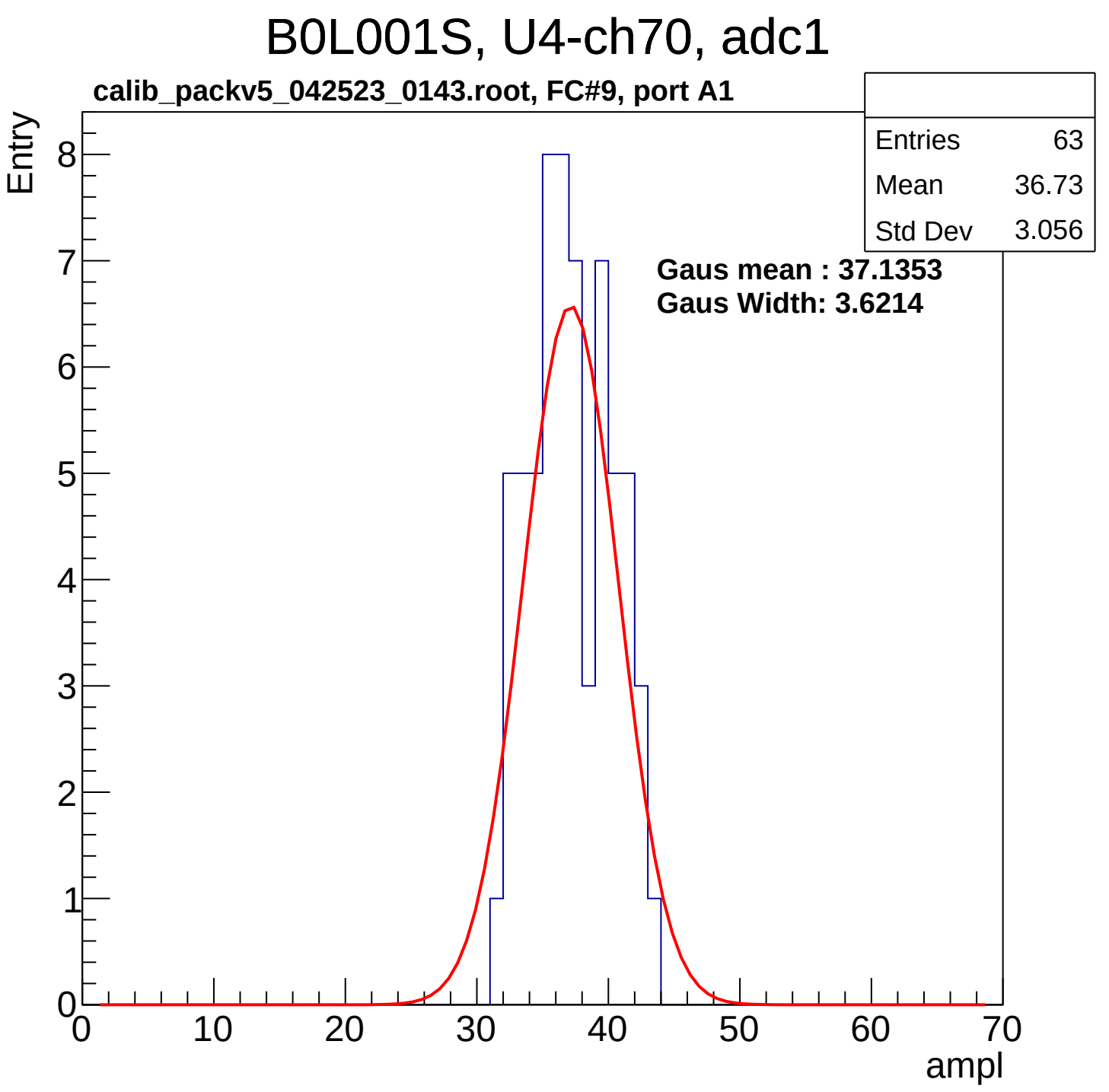

B0L001S, U4-ch69, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.49
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

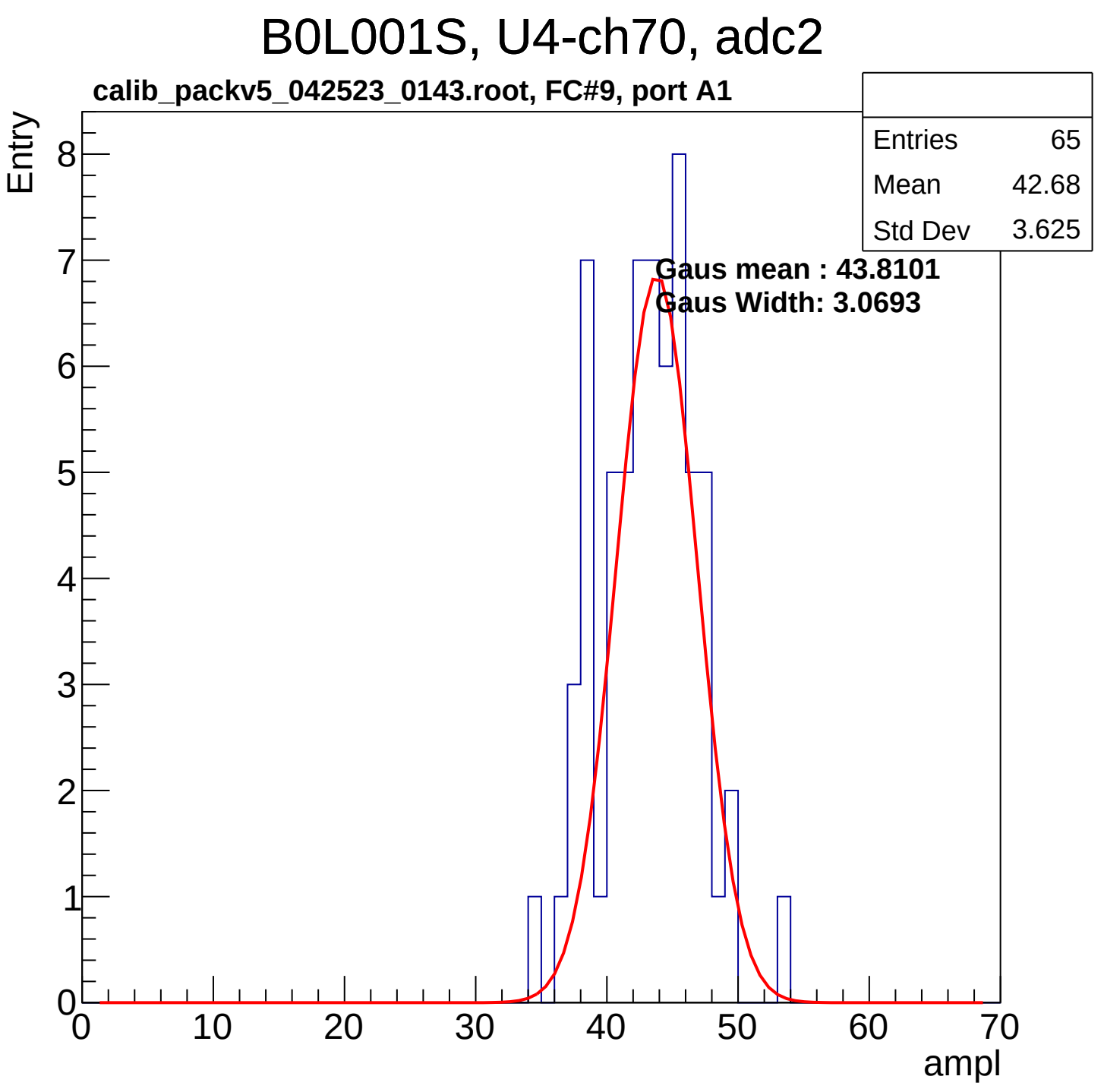

B0L001S, U4-ch69, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 60.36
Std Dev 1.86
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

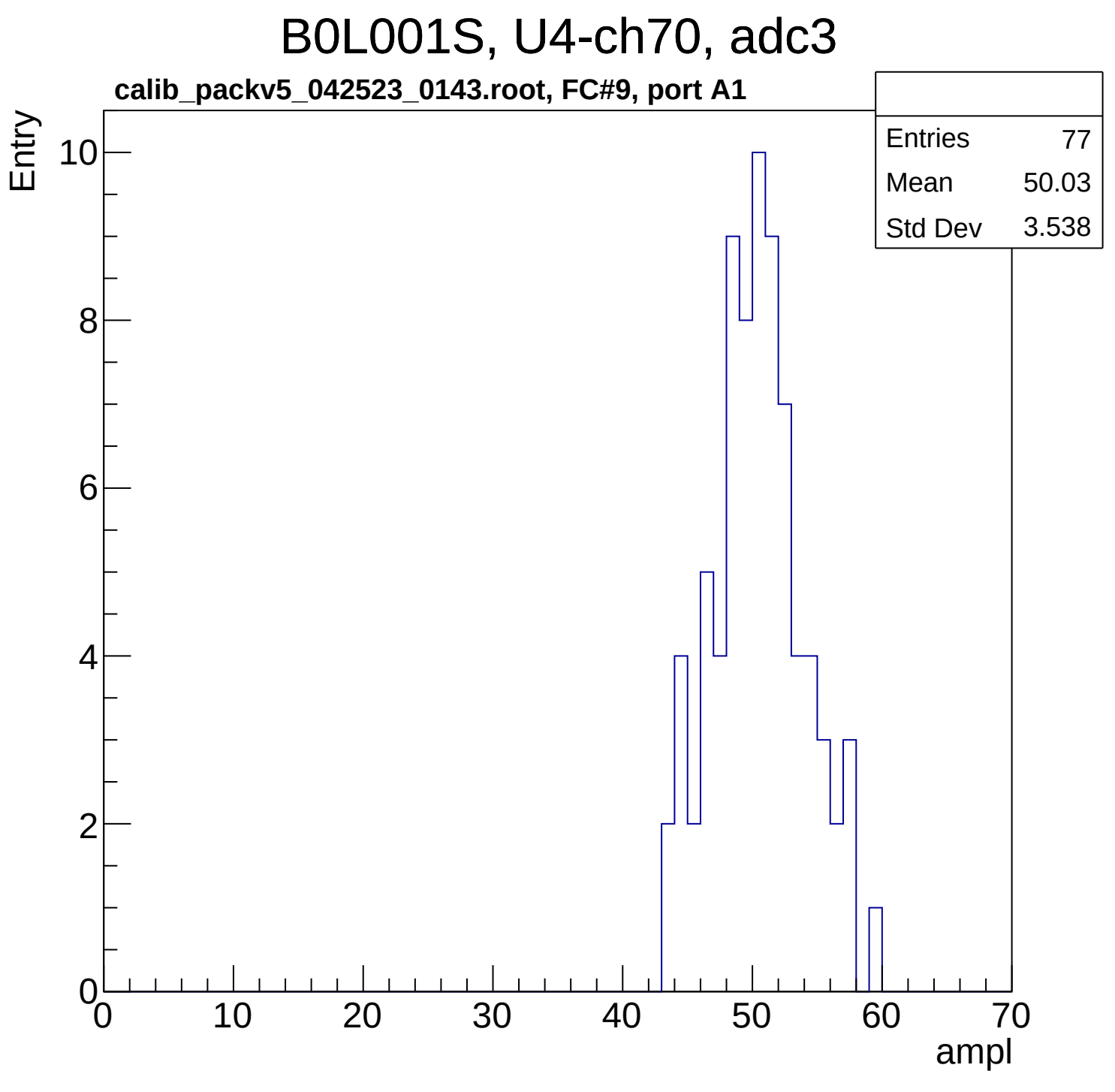

B0L001S, U4-ch69, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

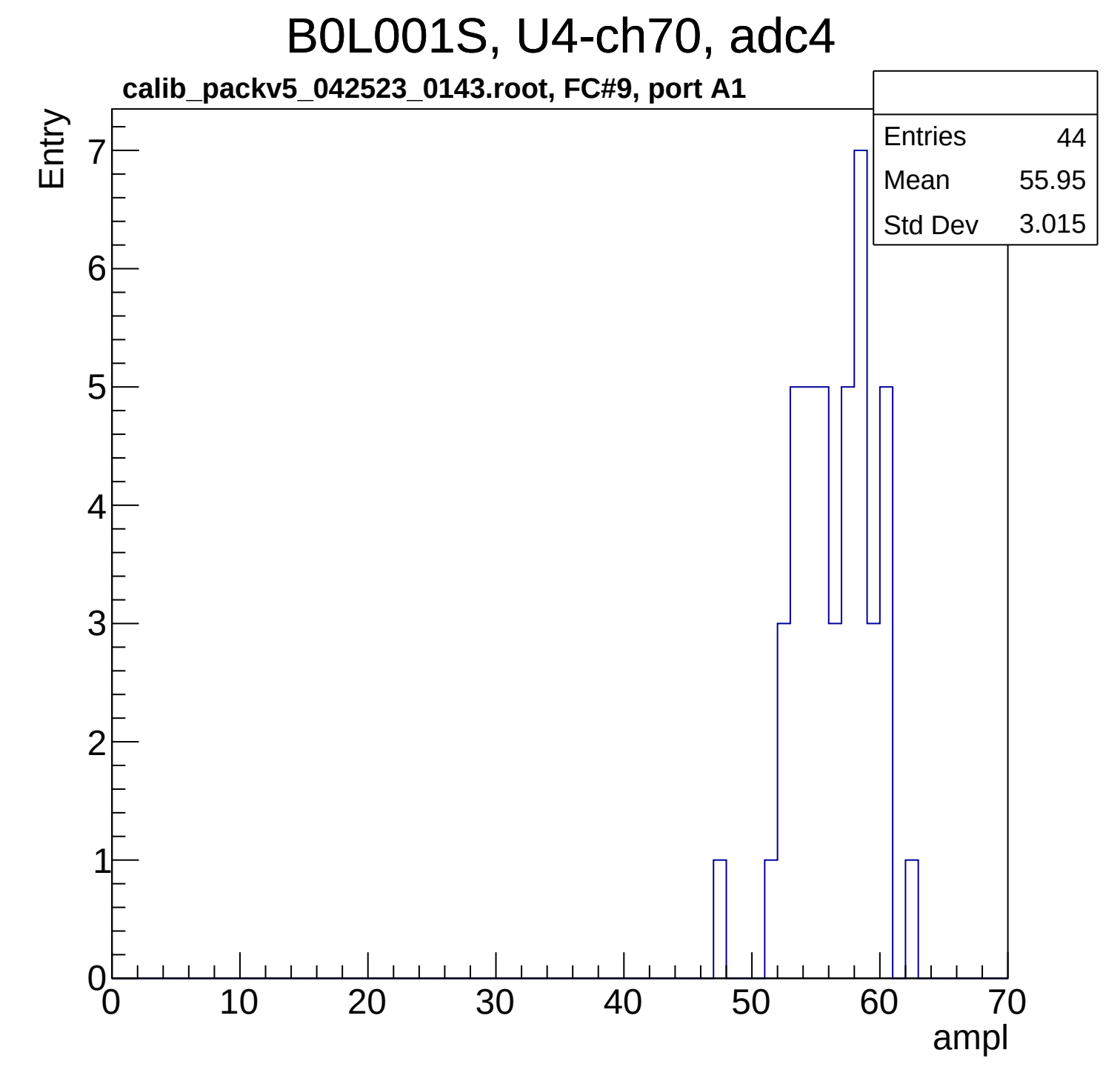

B0L001S, U4-ch69, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

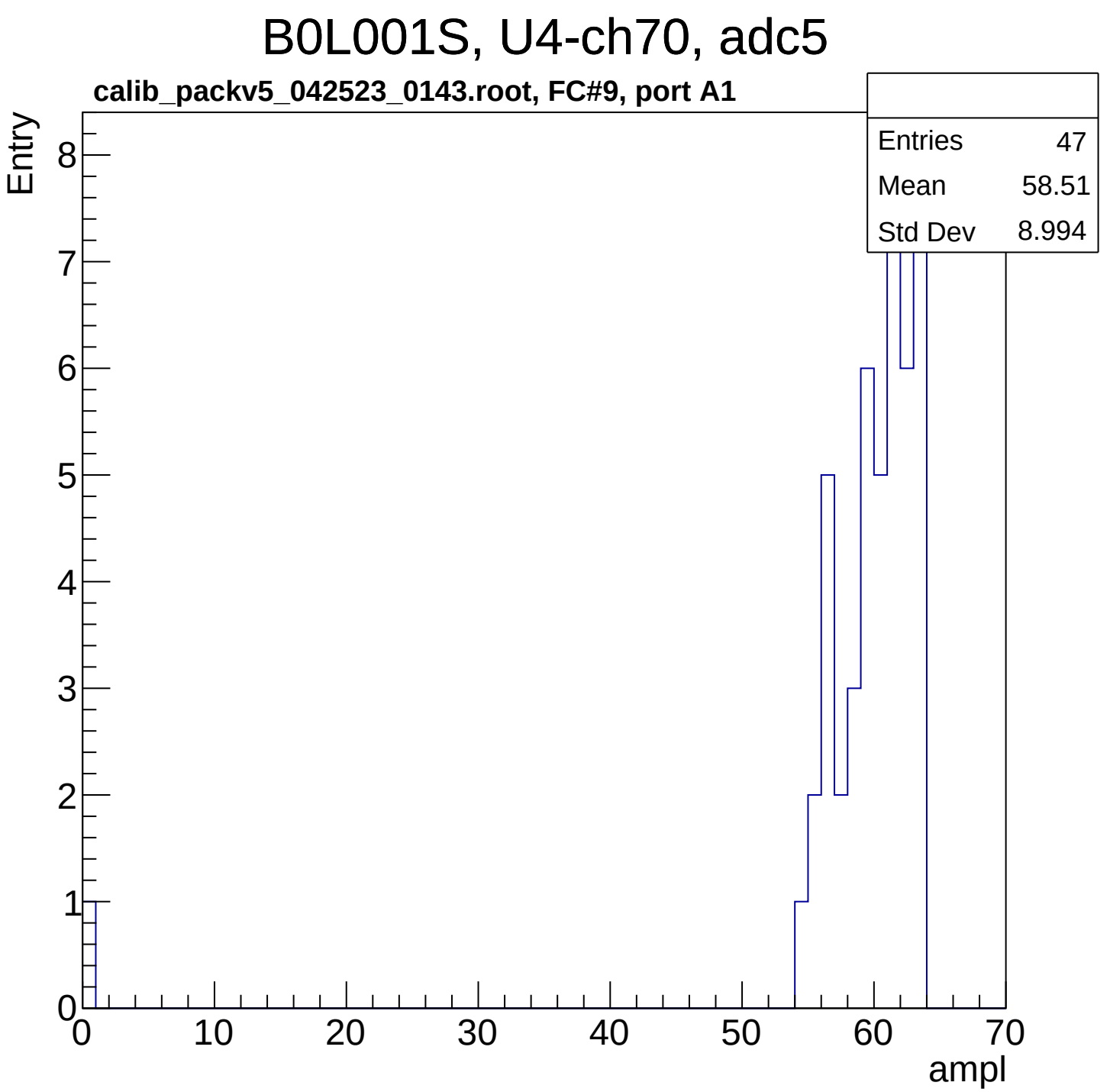

B0L001S, U4-ch70, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.43
Std Dev 5.004
Gaus mean : 30.3629
Gaus Width: 3.1288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

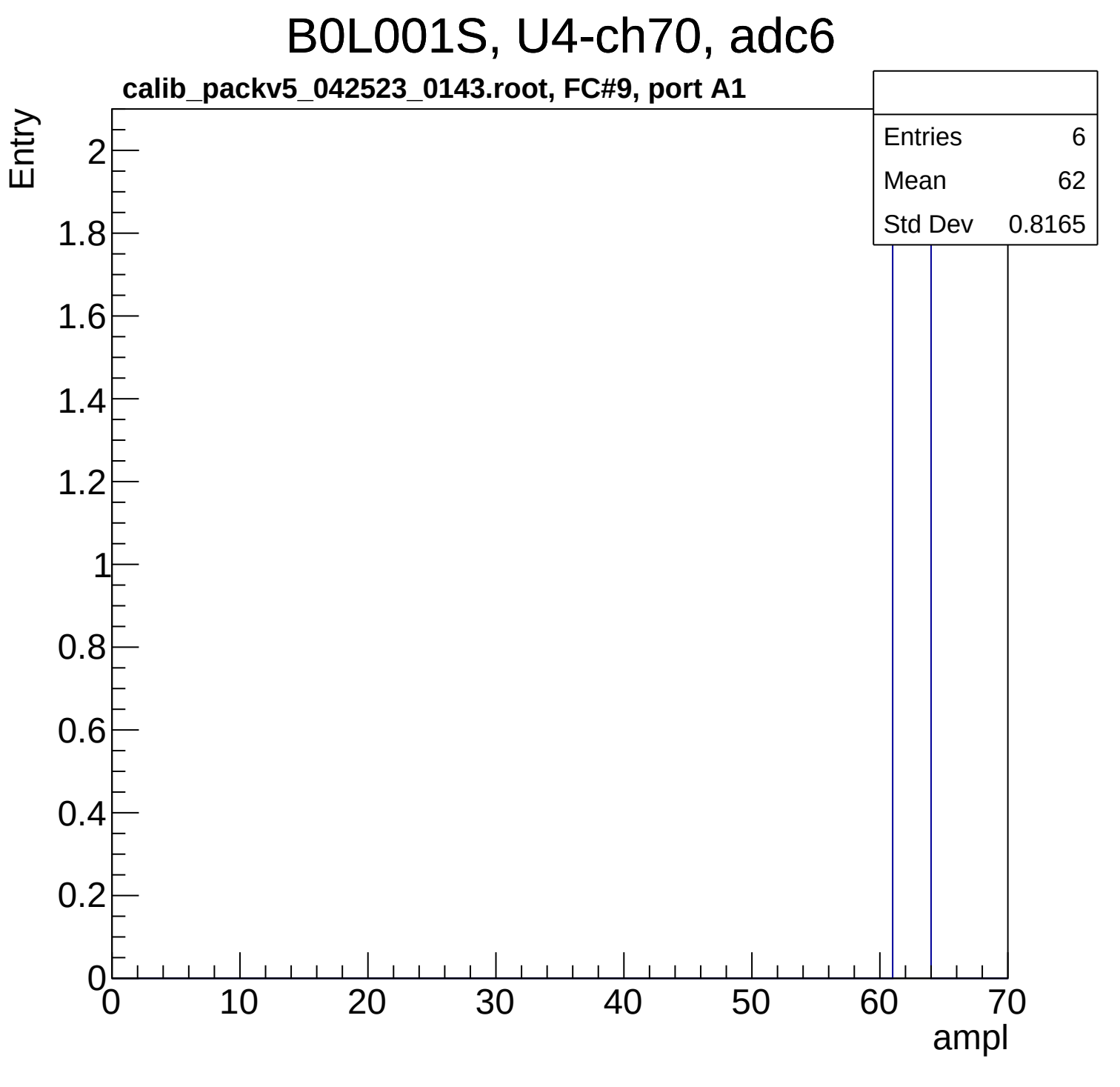

B0L001S, U4-ch70, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.73
Std Dev 3.056
Gaus mean : 37.1353
Gaus Width: 3.6214
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

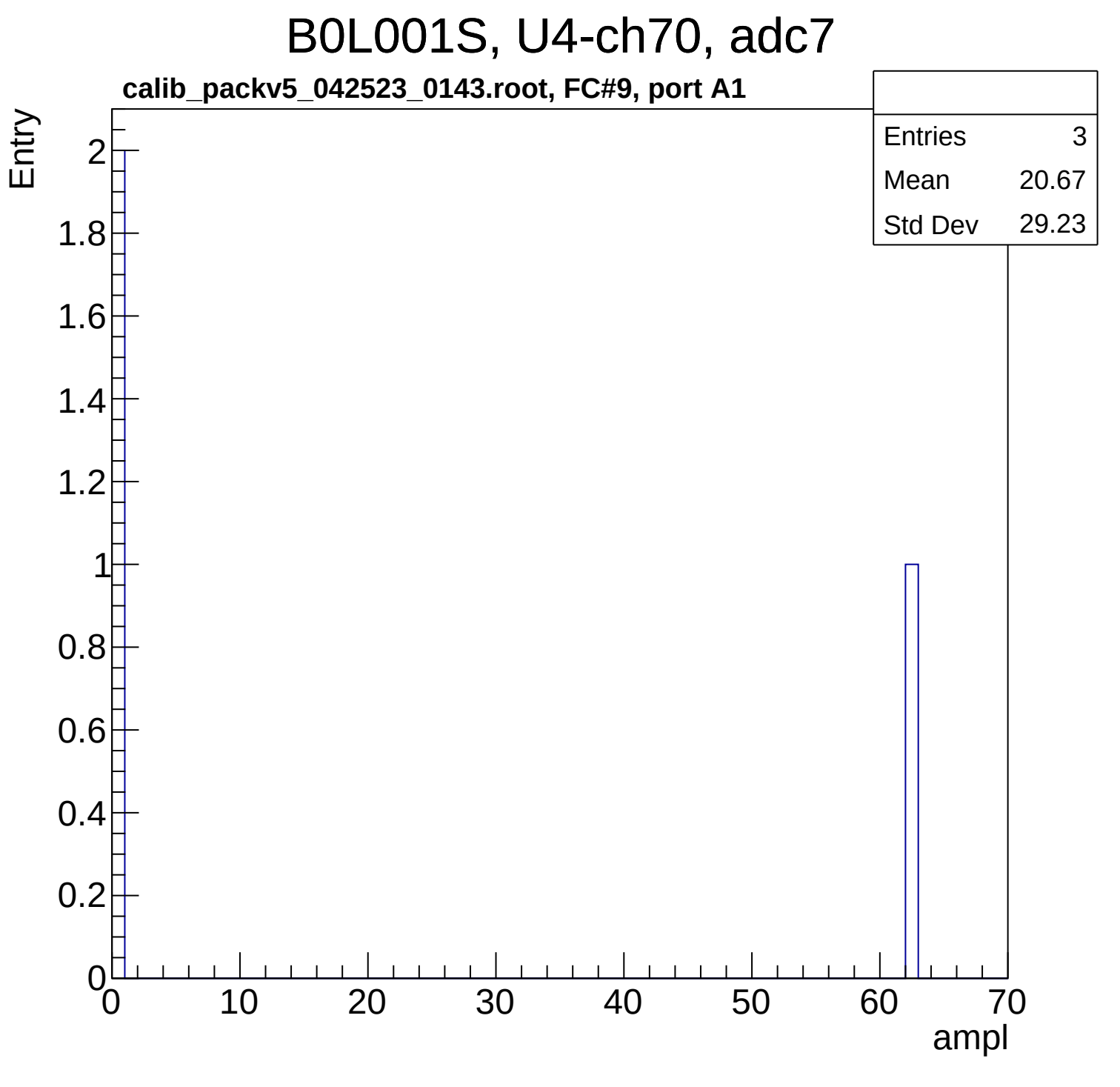

B0L001S, U4-ch70, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 42.68
Std Dev 3.625
Gaus mean : 43.8101
Gaus Width: 3.0693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

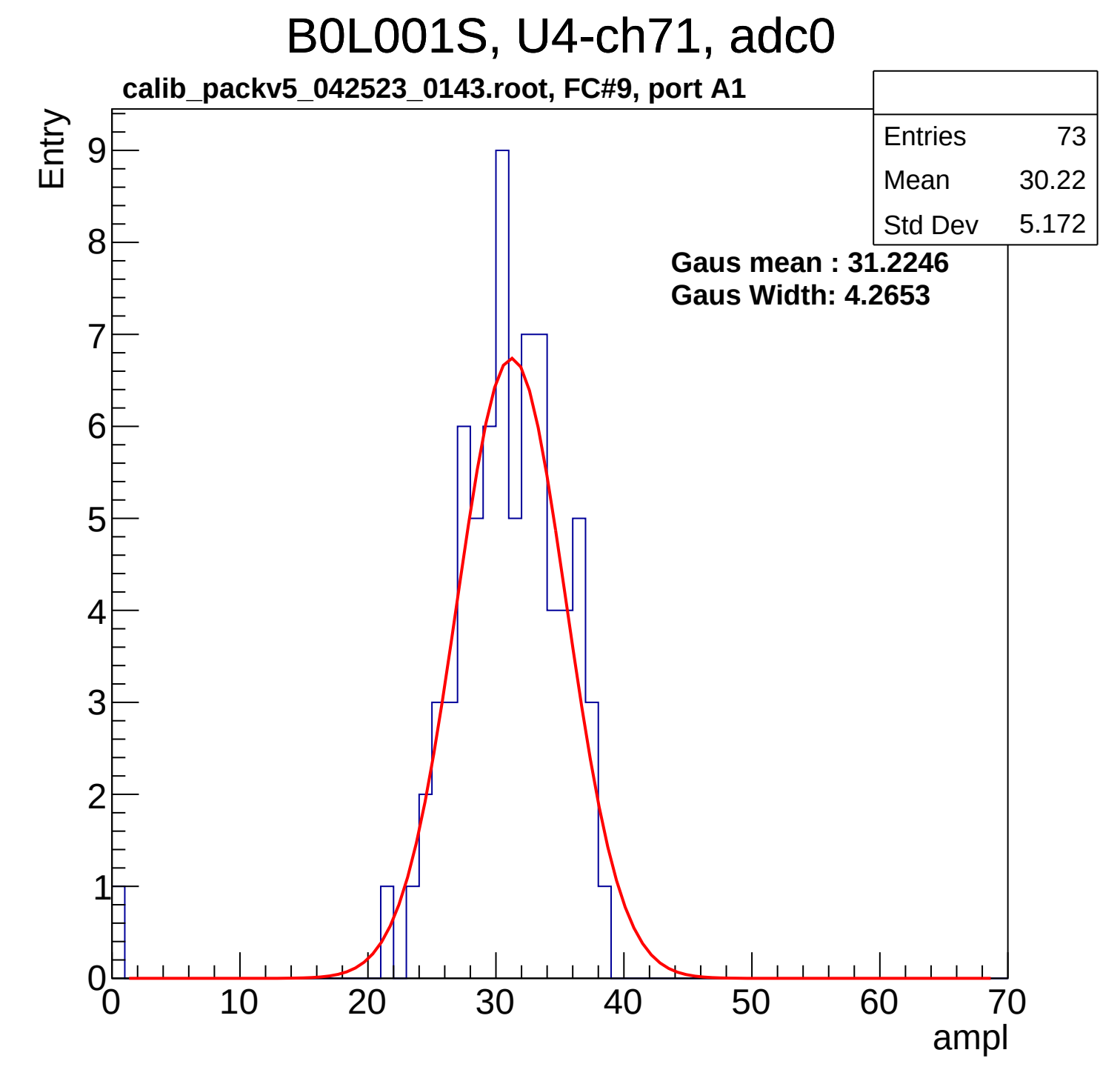

B0L001S, U4-ch70, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 50.03
Std Dev 3.538
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

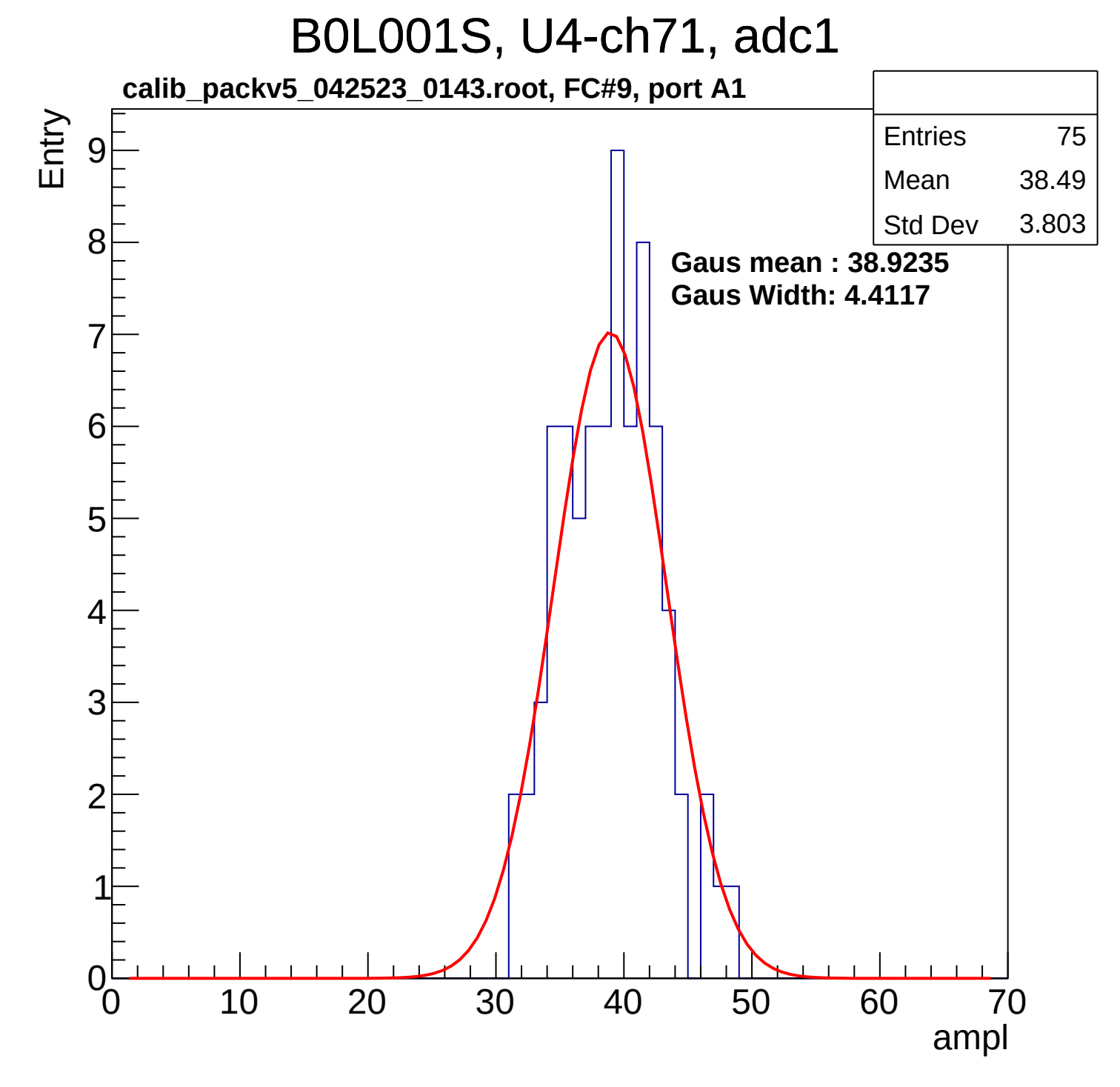

B0L001S, U4-ch70, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 55.95
Std Dev 3.015
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

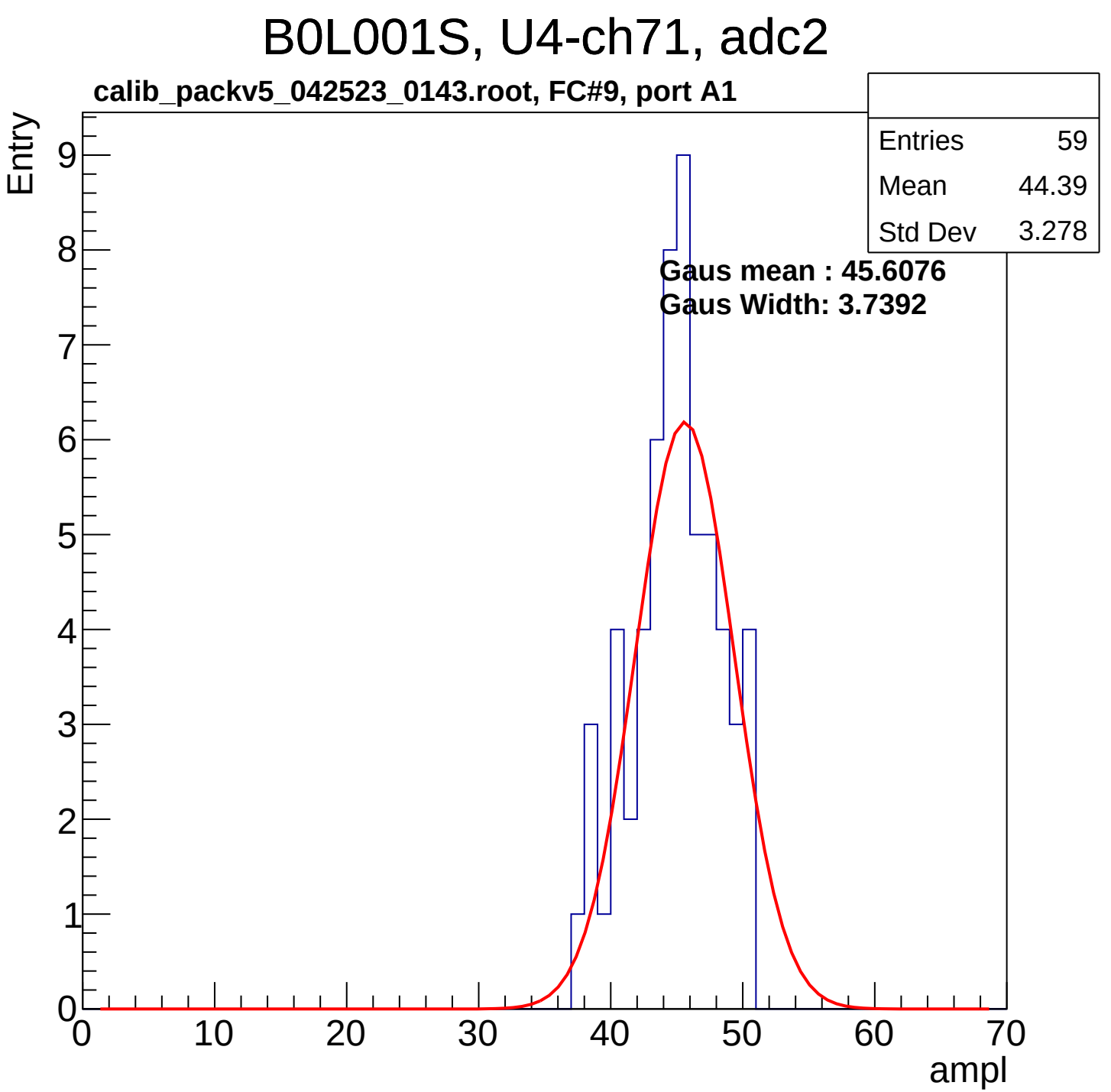

B0L001S, U4-ch70, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.51
Std Dev 8.994
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

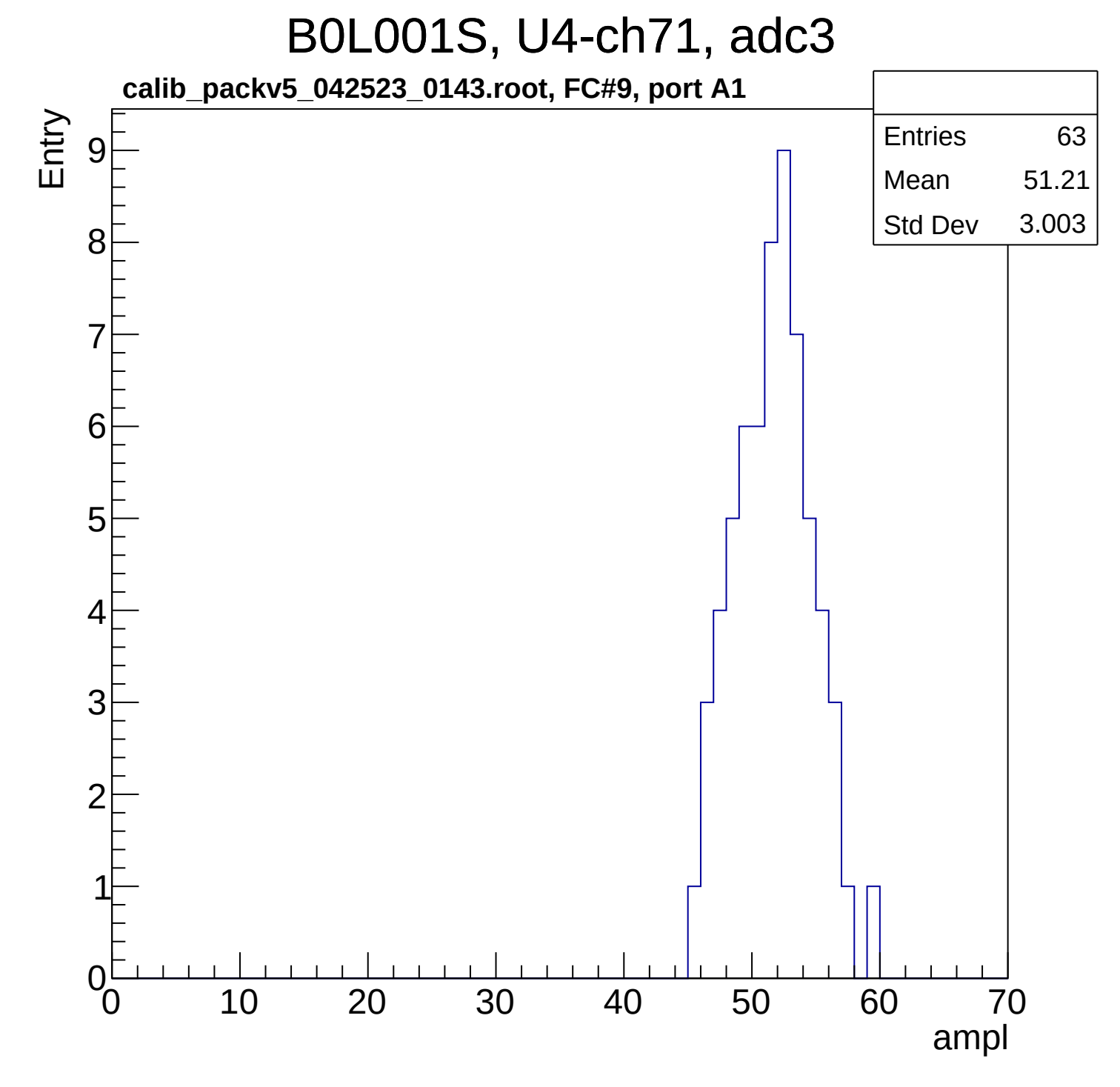

B0L001S, U4-ch70, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

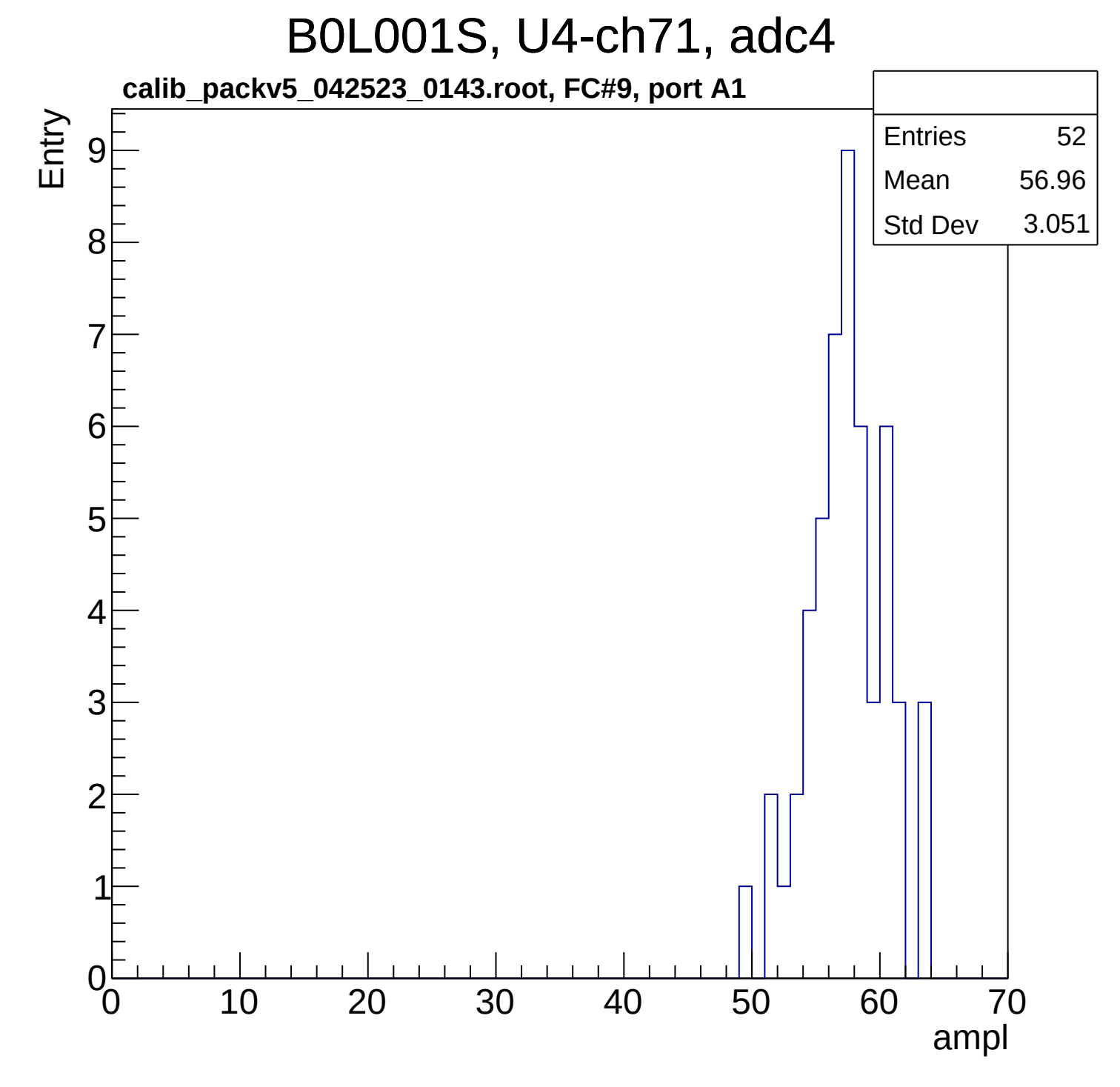

B0L001S, U4-ch70, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

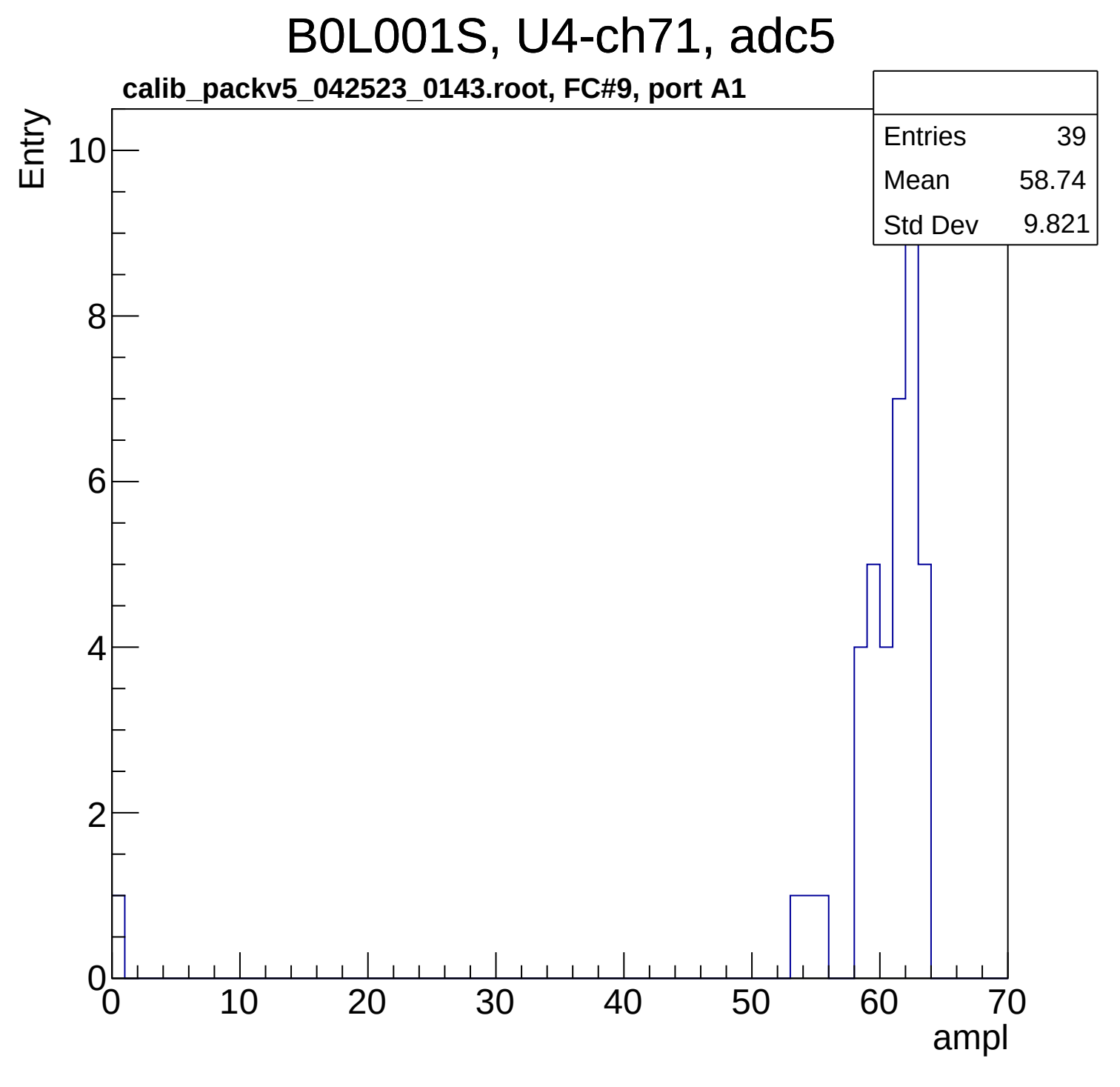

B0L001S, U4-ch71, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.22
Std Dev 5.172
Gaus mean : 31.2246
Gaus Width: 4.2653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

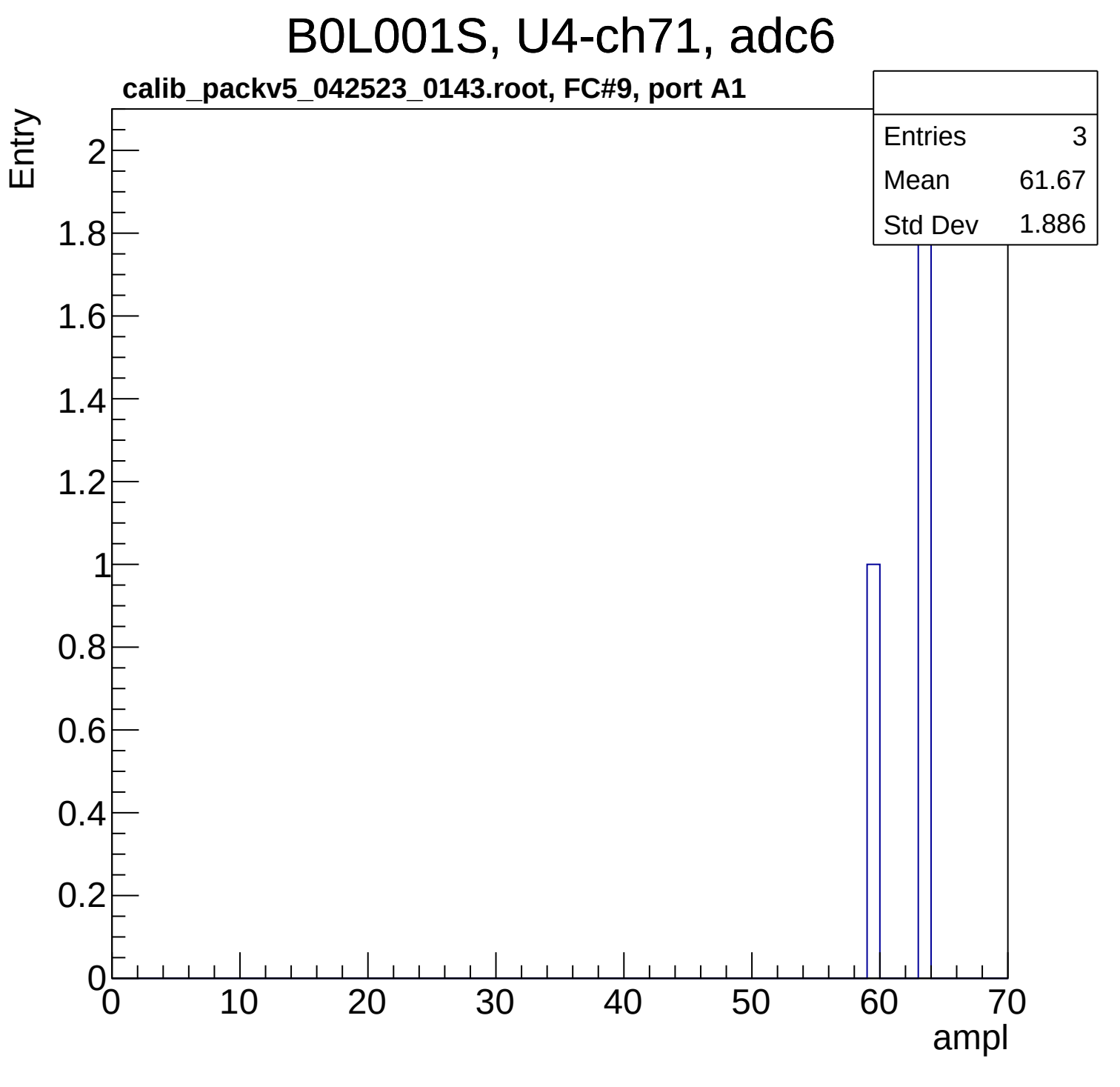

B0L001S, U4-ch71, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.49
Std Dev 3.803
Gaus mean : 38.9235
Gaus Width: 4.4117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

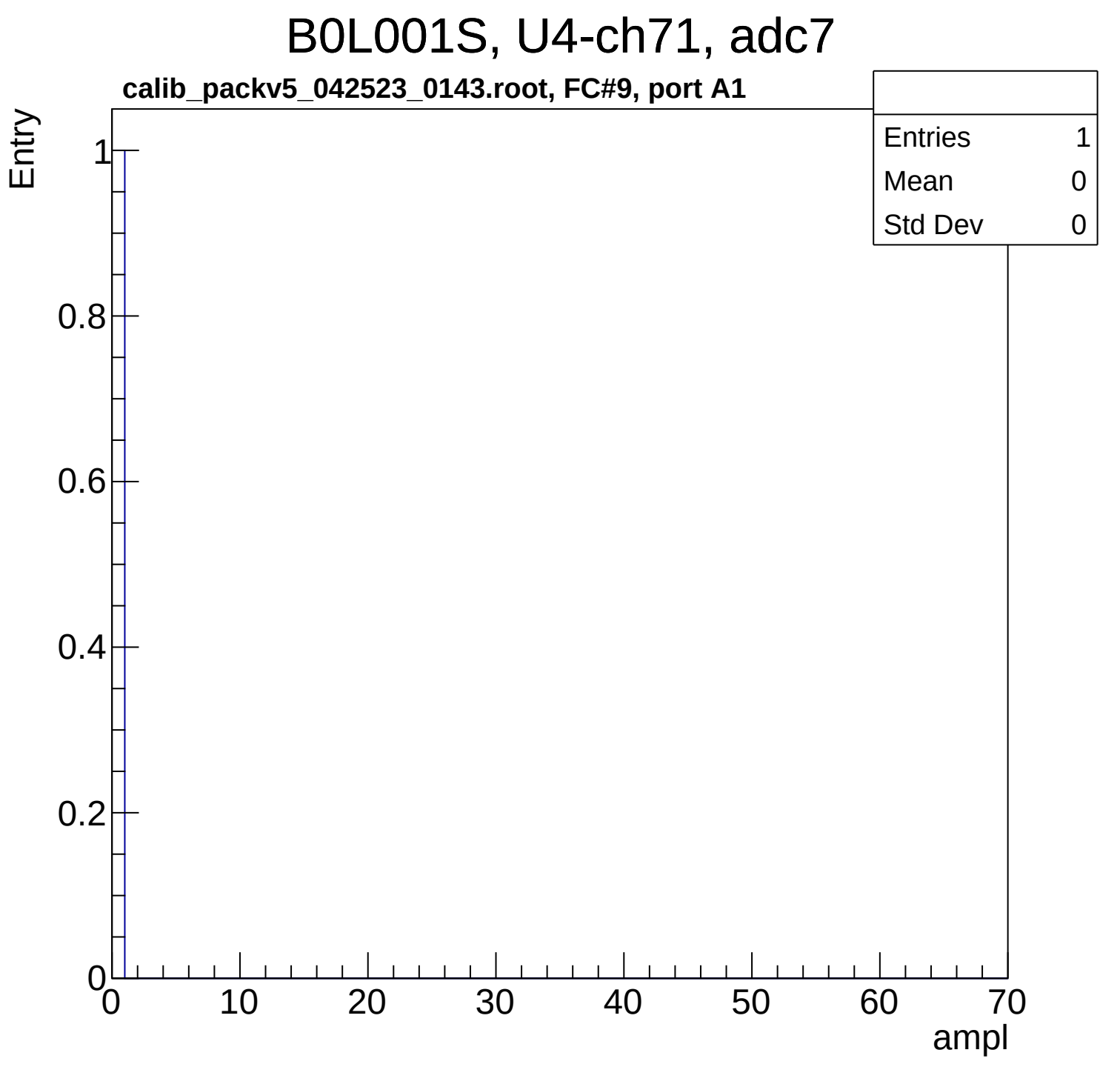

B0L001S, U4-ch71, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.39
Std Dev 3.278
Gaus mean : 45.6076
Gaus Width: 3.7392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

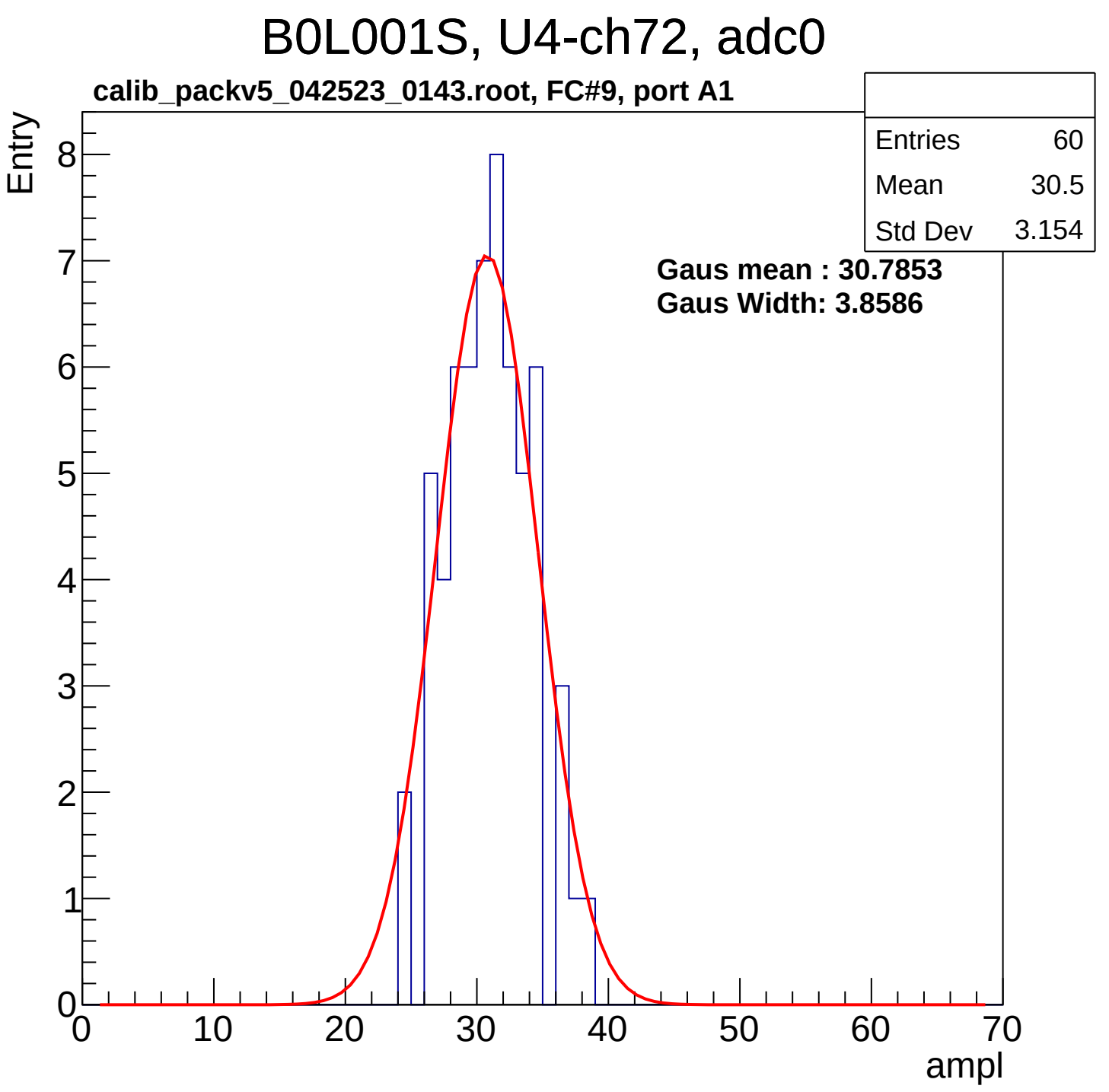

B0L001S, U4-ch71, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.21
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

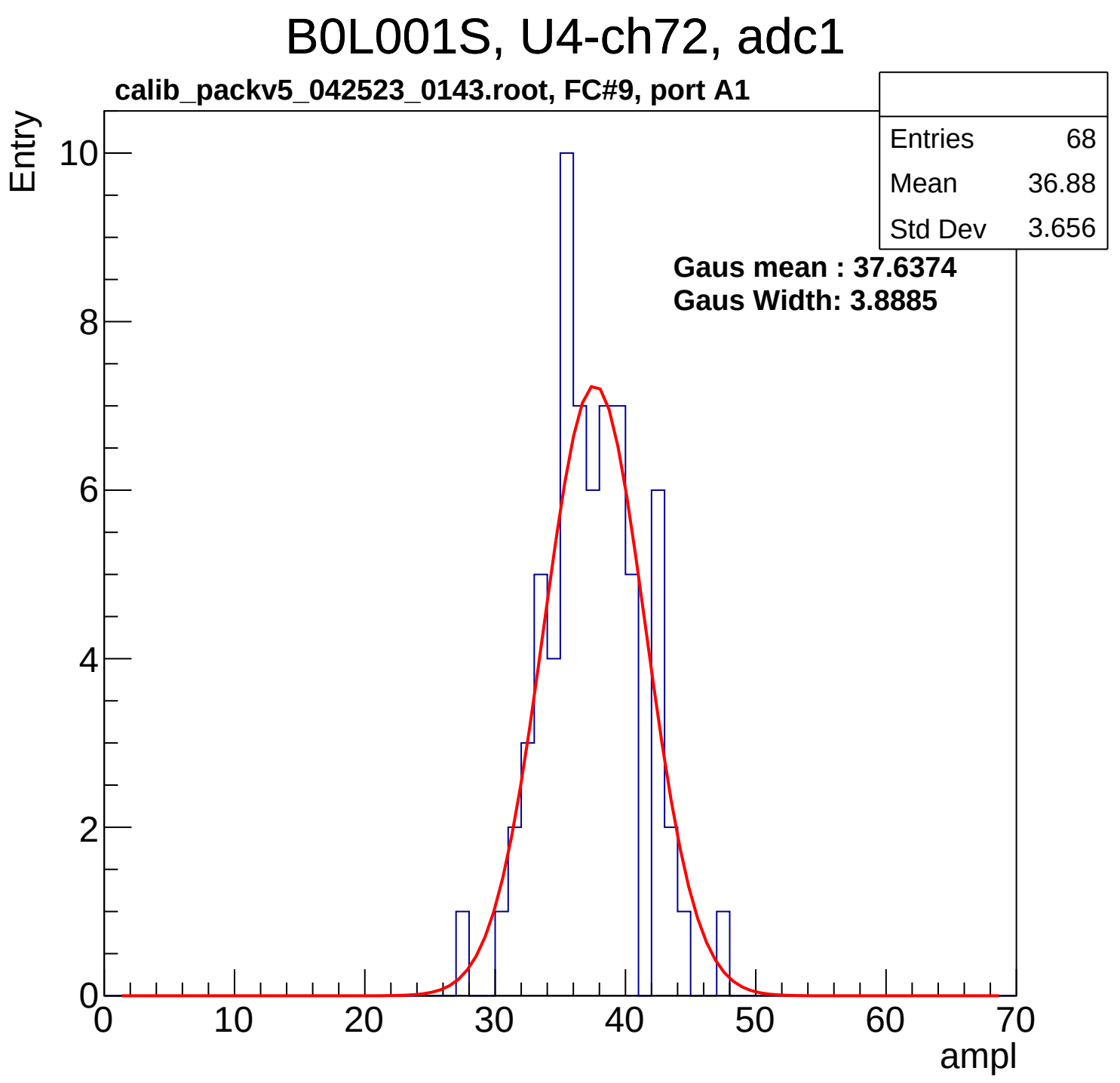

B0L001S, U4-ch71, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.96
Std Dev 3.051
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

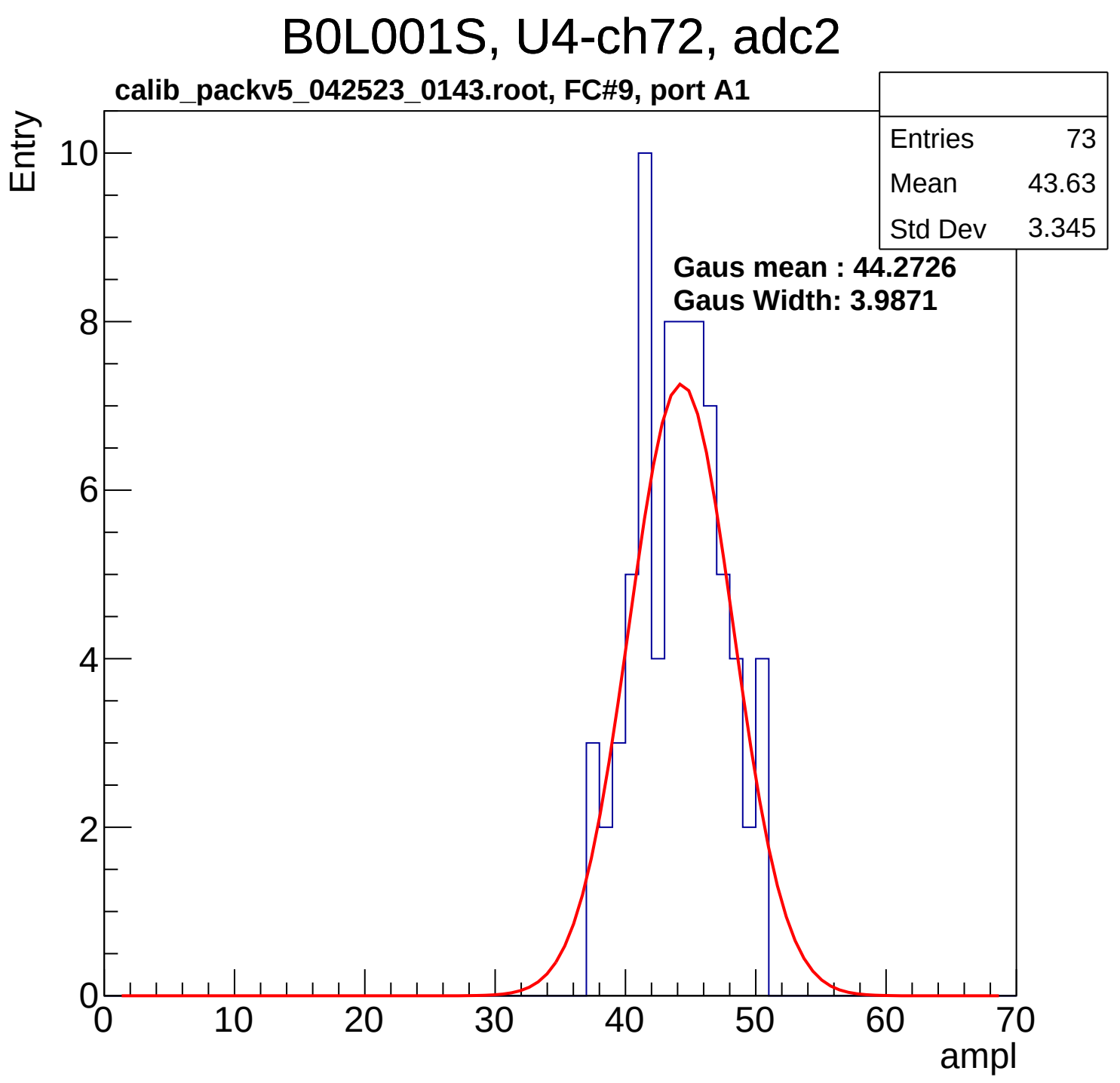

B0L001S, U4-ch71, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.74
Std Dev 9.821
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

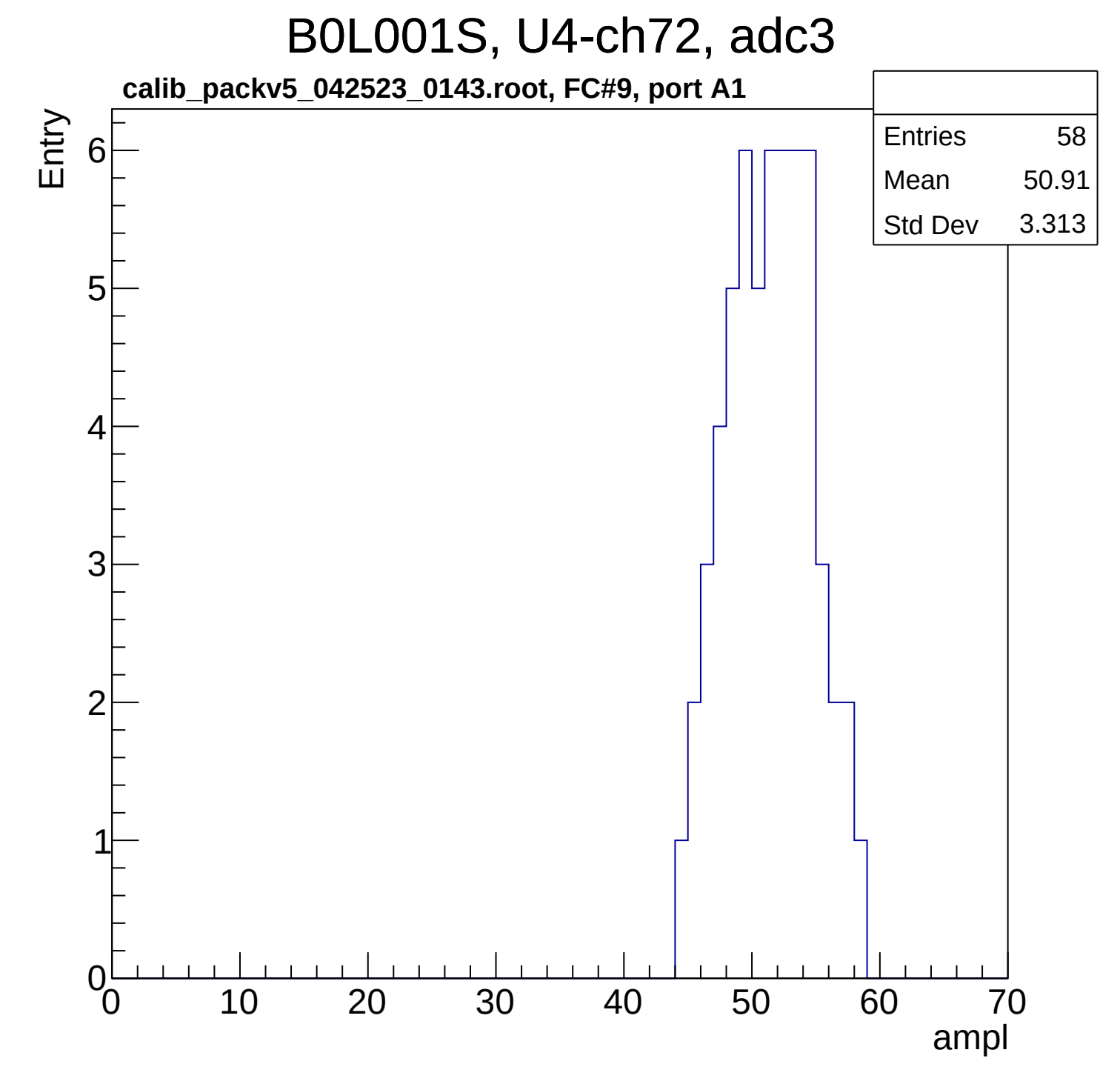

B0L001S, U4-ch71, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

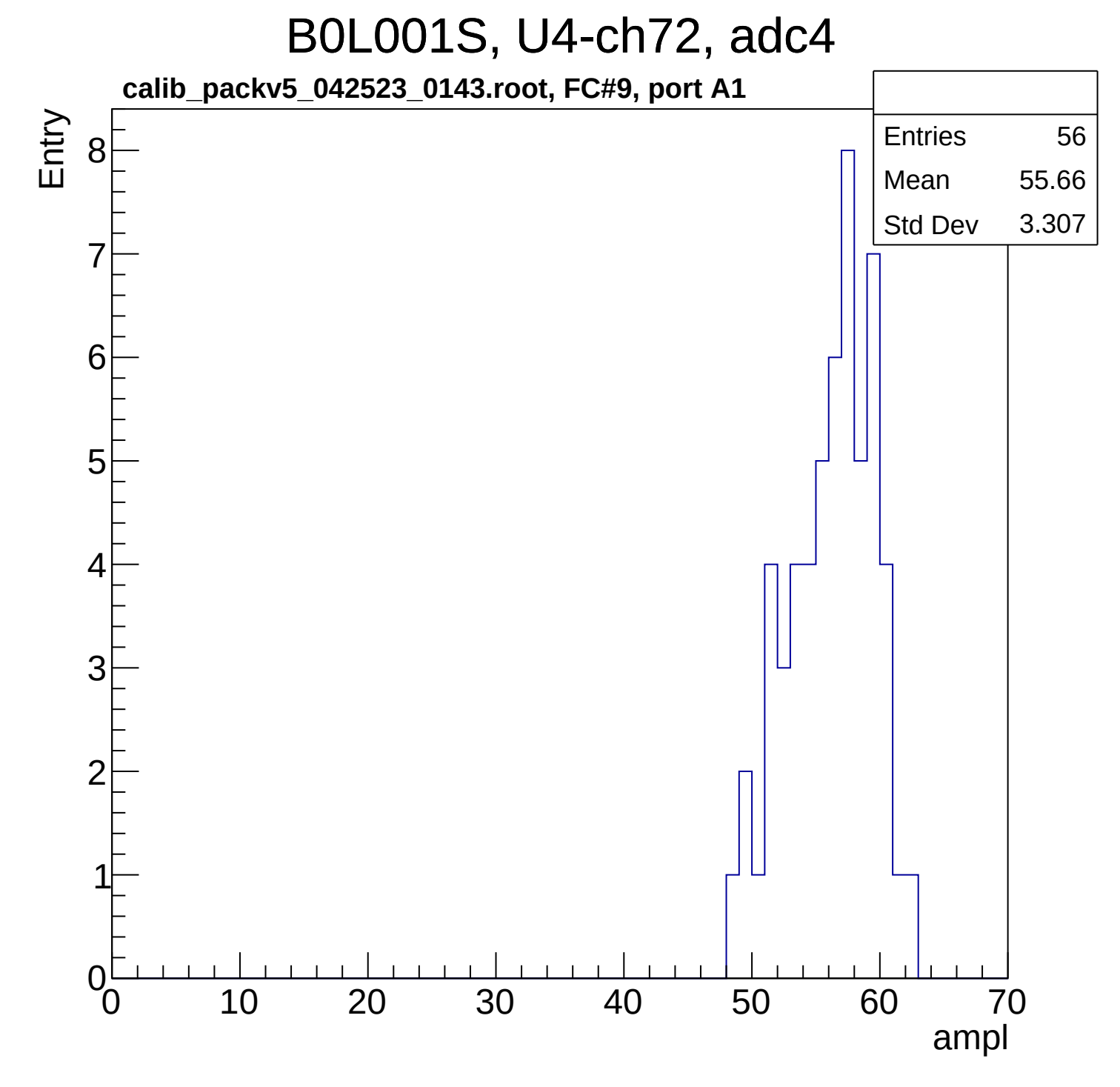

B0L001S, U4-ch71, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch72, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.5
Std Dev 3.154
Gaus mean : 30.7853
Gaus Width: 3.8586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch72, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.88
Std Dev 3.656
Gaus mean : 37.6374
Gaus Width: 3.8885
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch72, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.63
Std Dev 3.345
Gaus mean : 44.2726
Gaus Width: 3.9871
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch72, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 50.91
Std Dev 3.313
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch72, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.66
Std Dev 3.307
Entry
ampl
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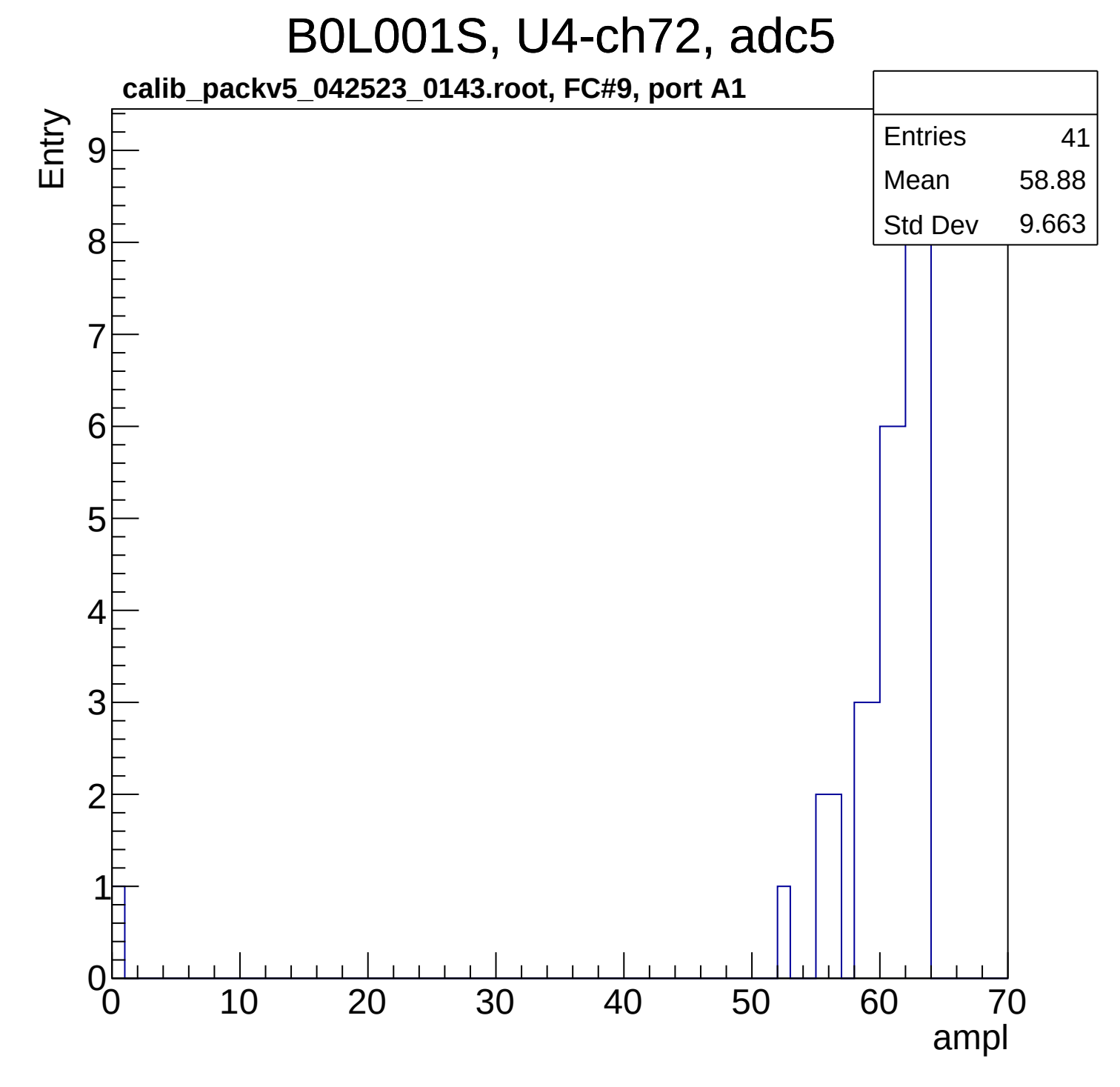
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch72, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.88
Std Dev 9.663
Entry
ampl
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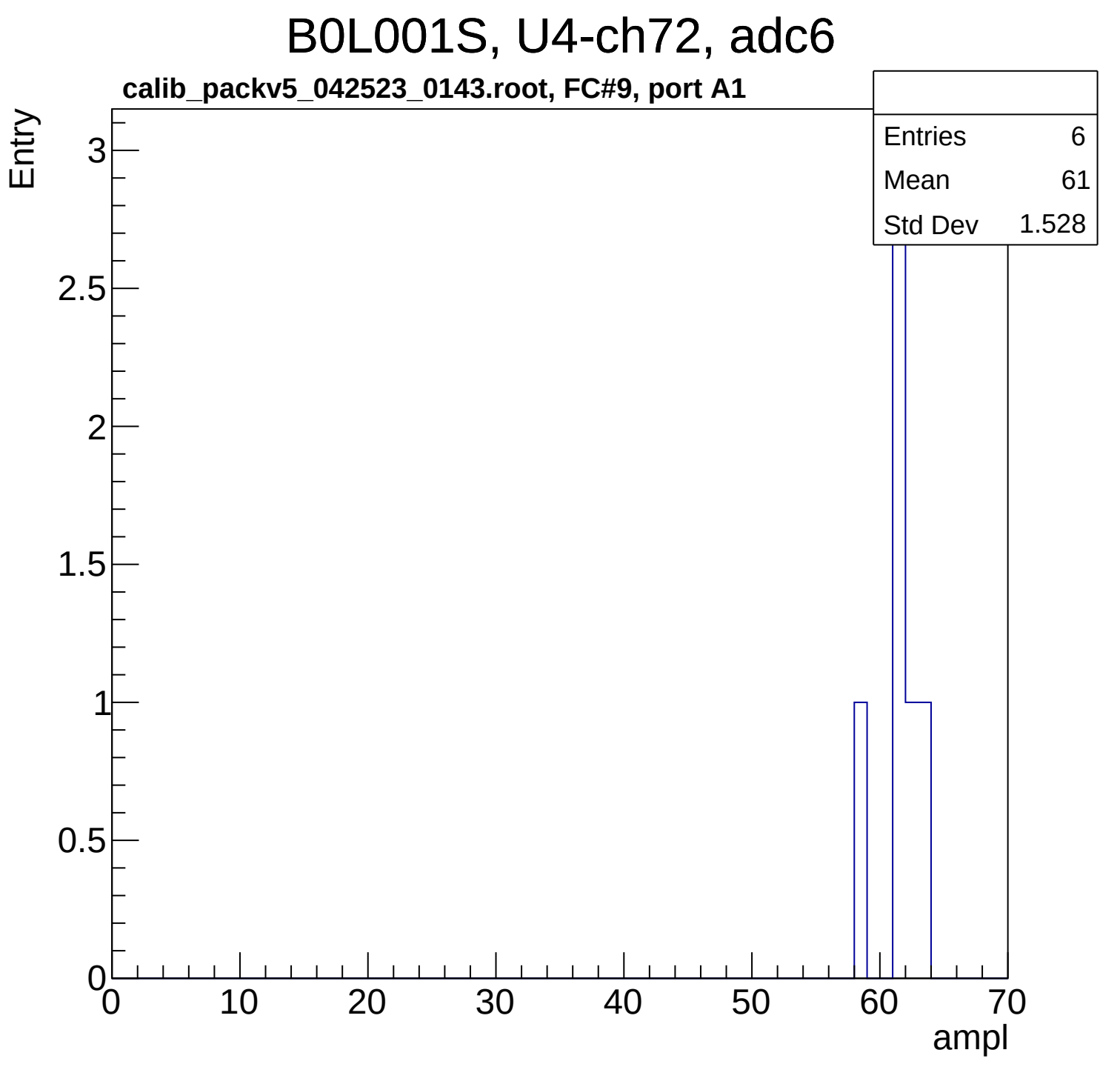
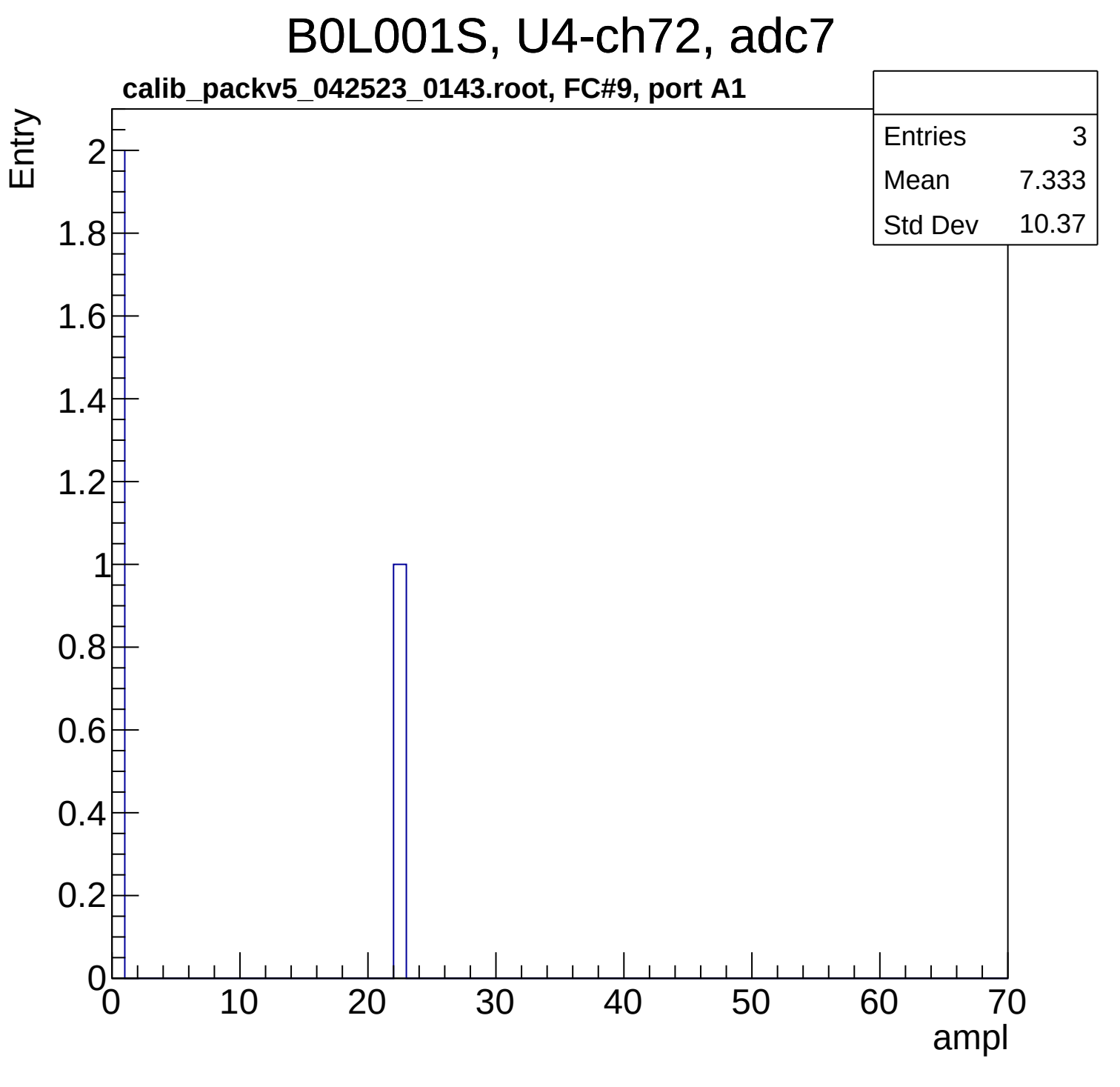
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch72, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61
Std Dev 1.528
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch72, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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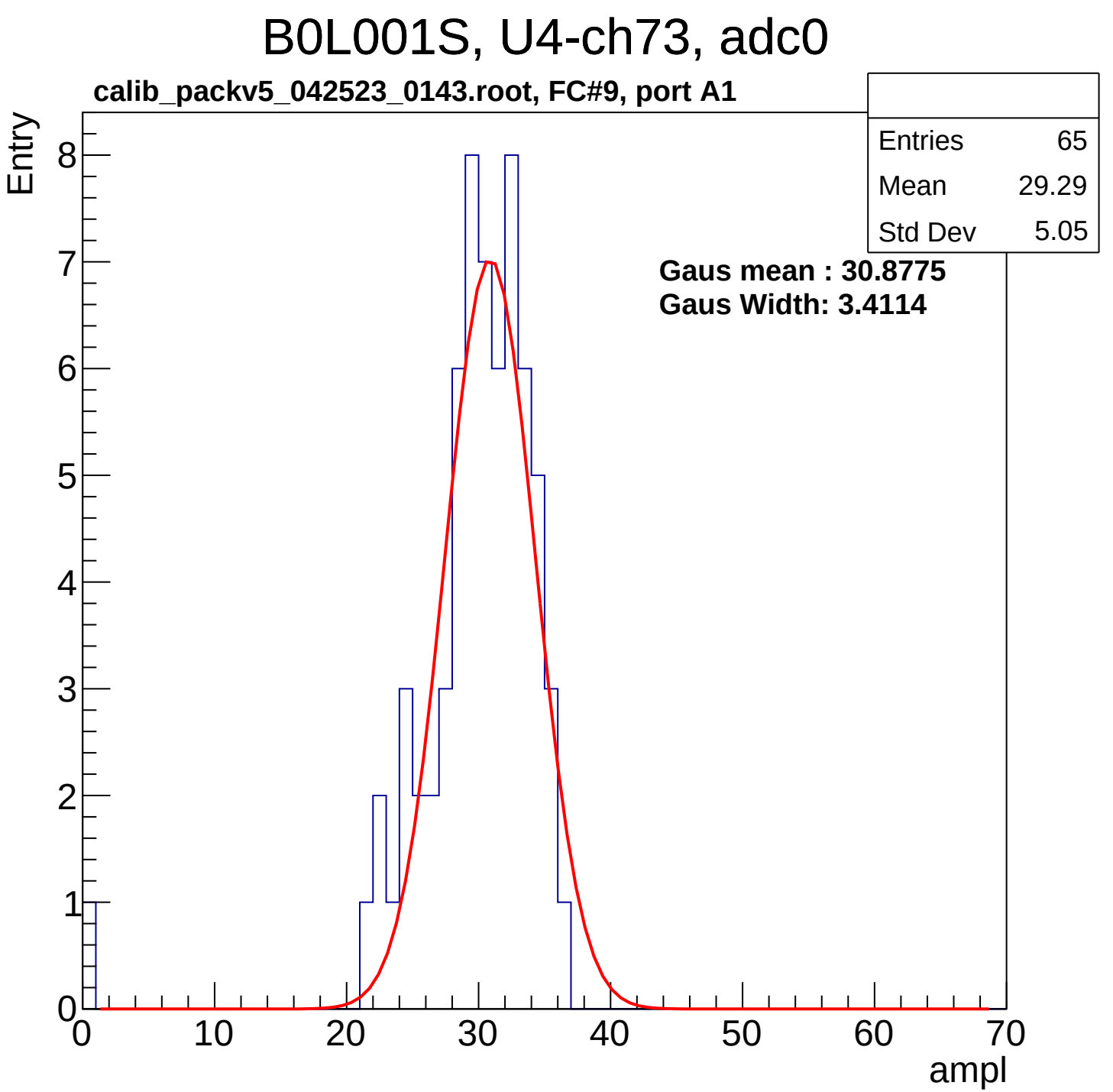
B0L001S, U4-ch73, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.29
Std Dev 5.05
Gaus mean : 30.8775
Gaus Width: 3.4114
Entry
ampl
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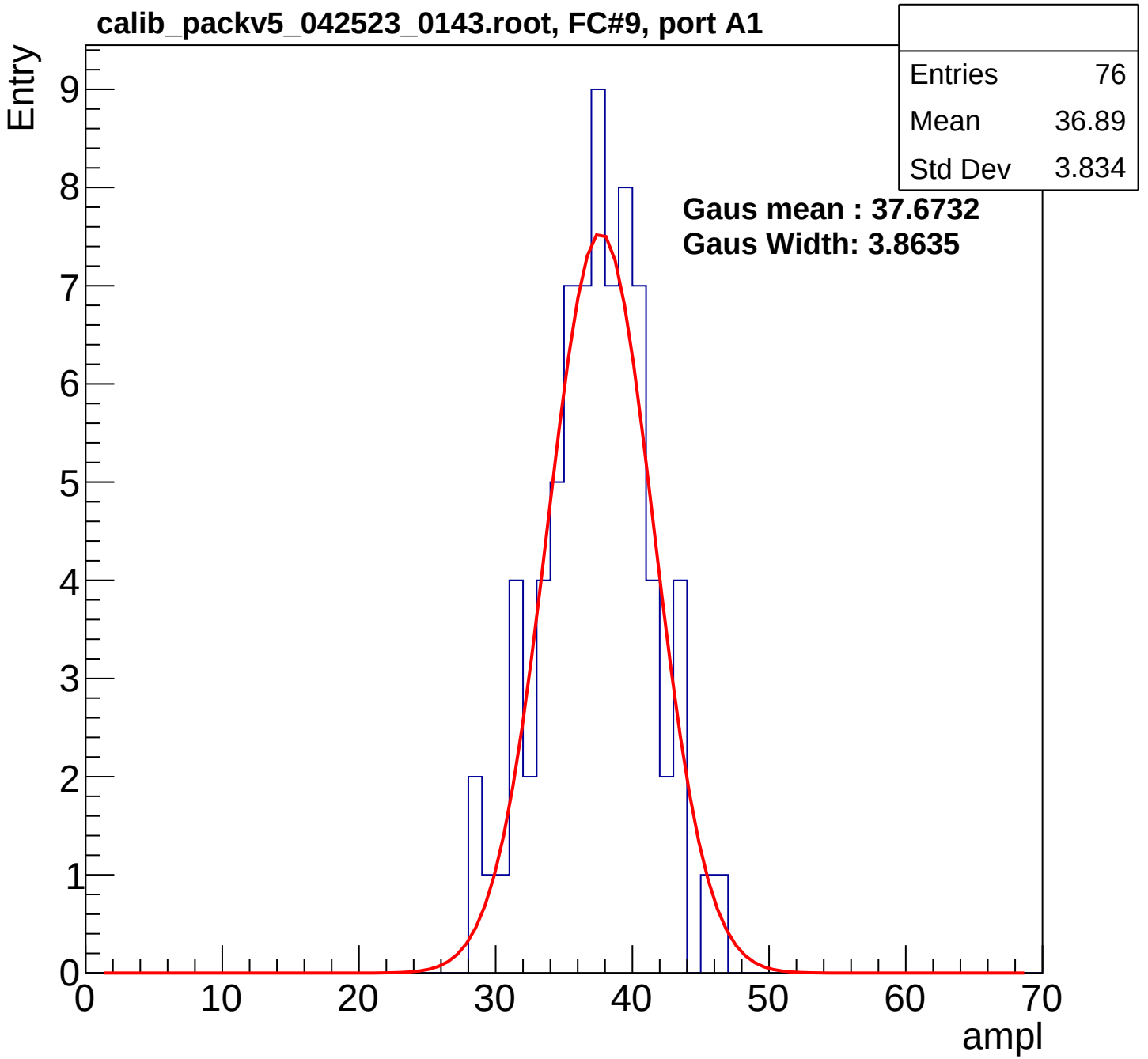
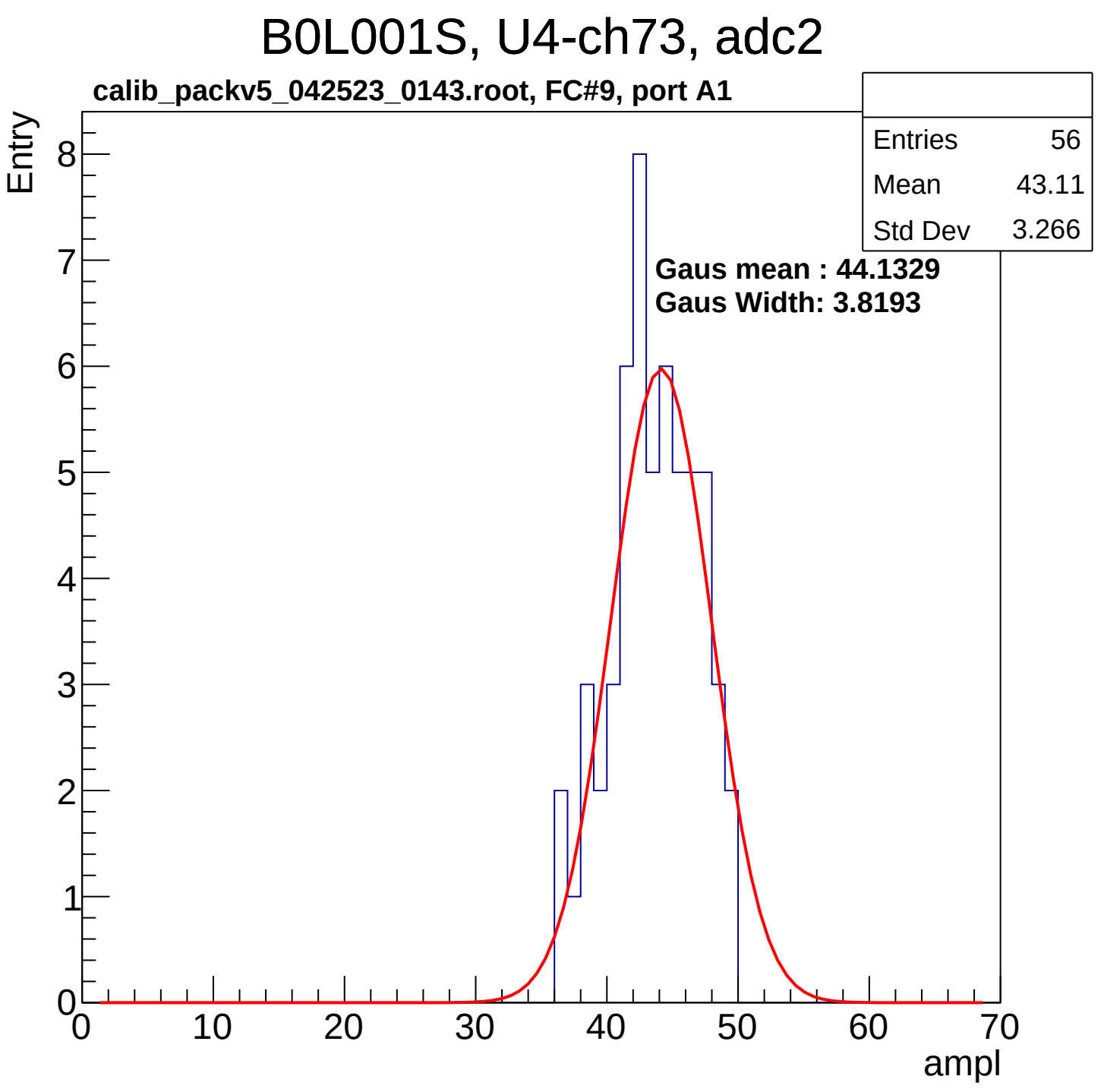
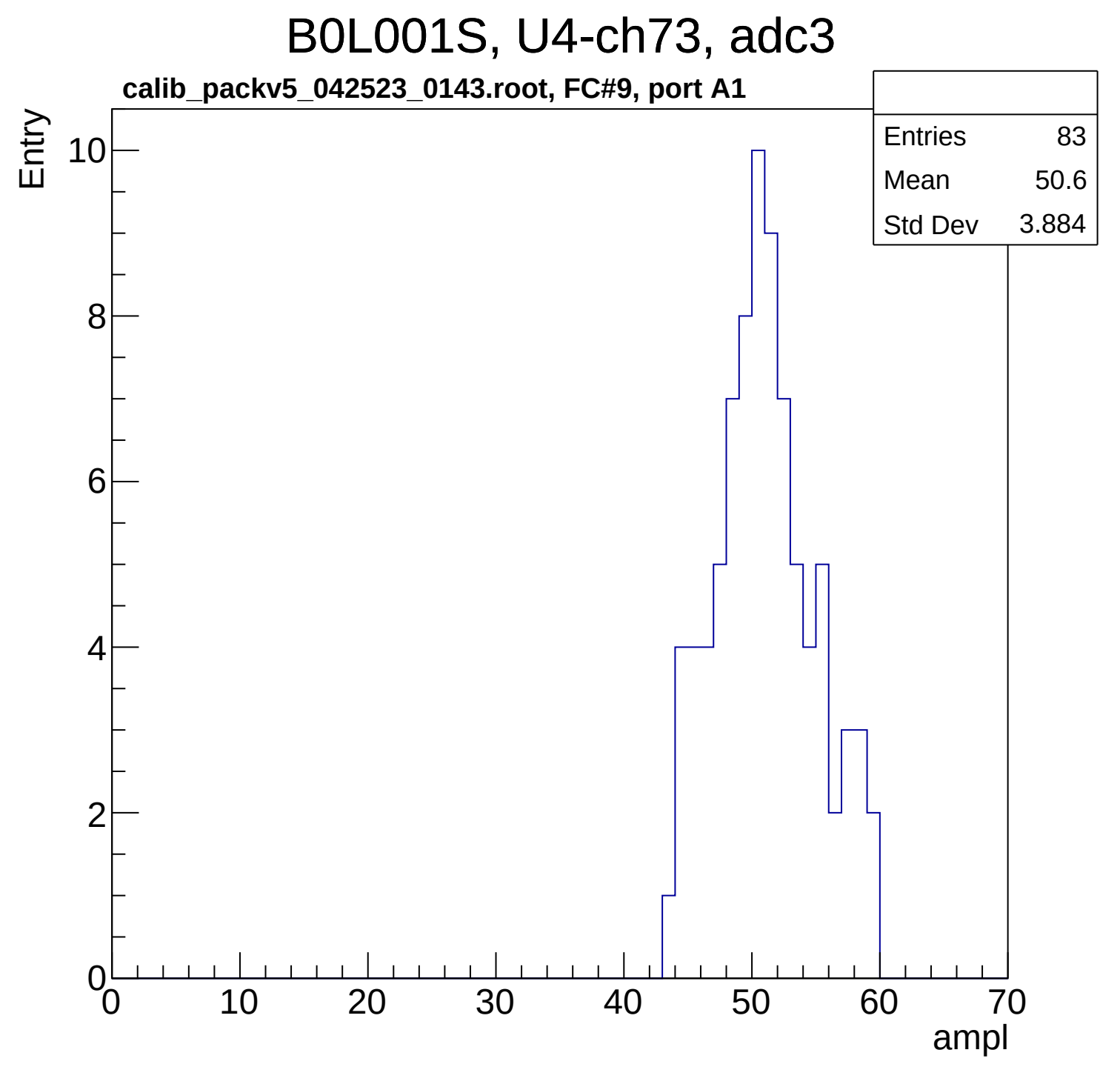
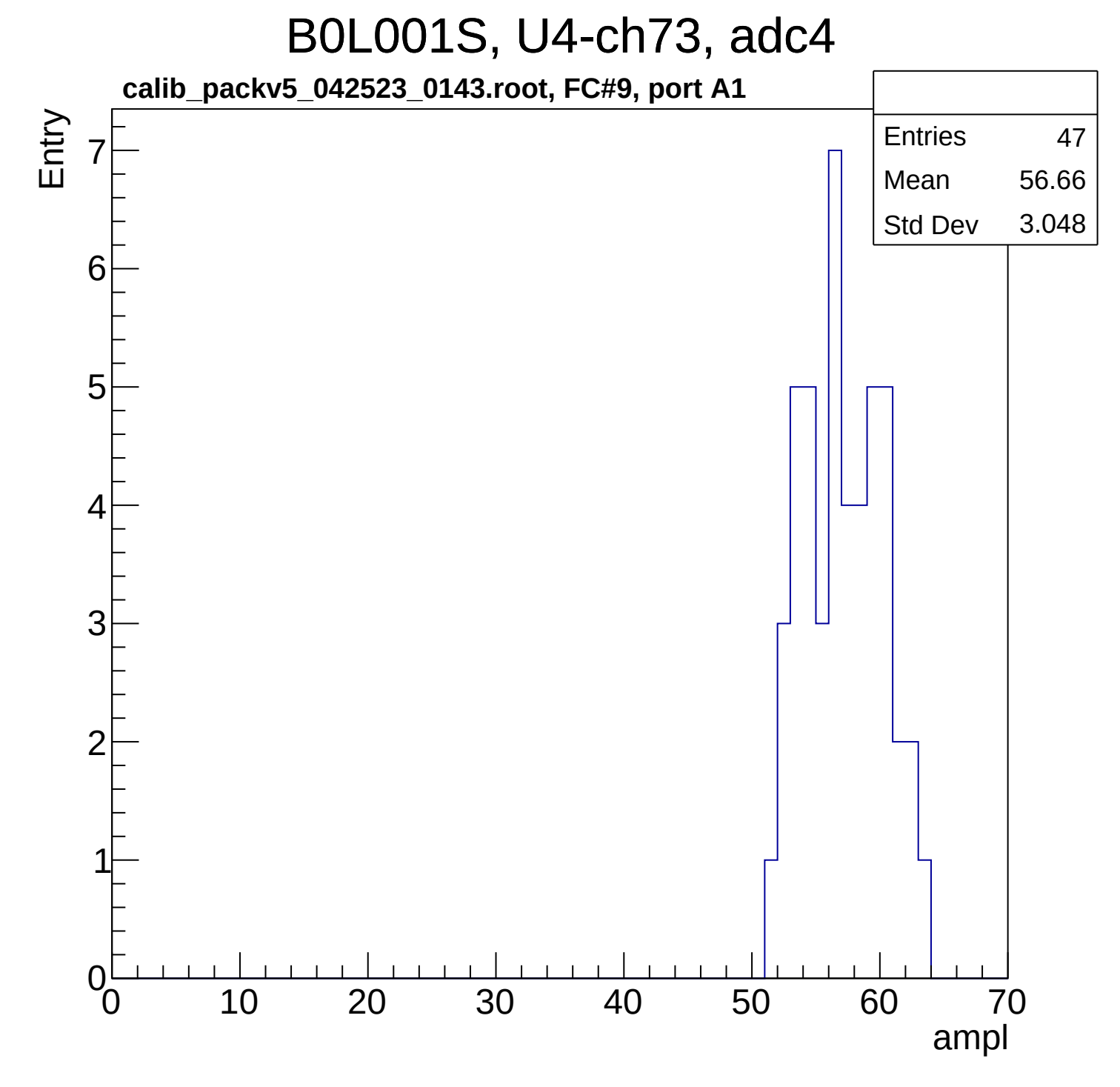
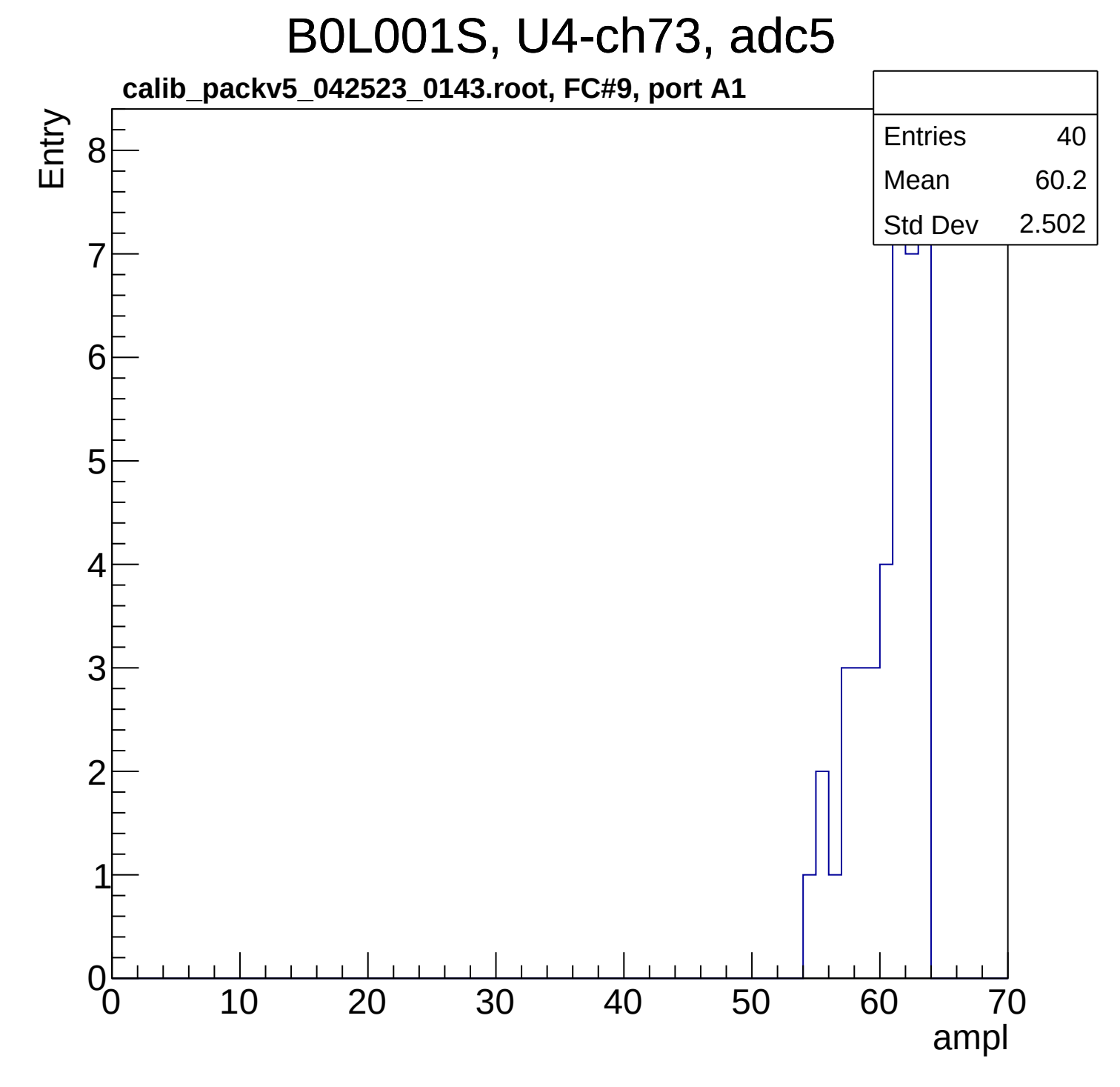
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

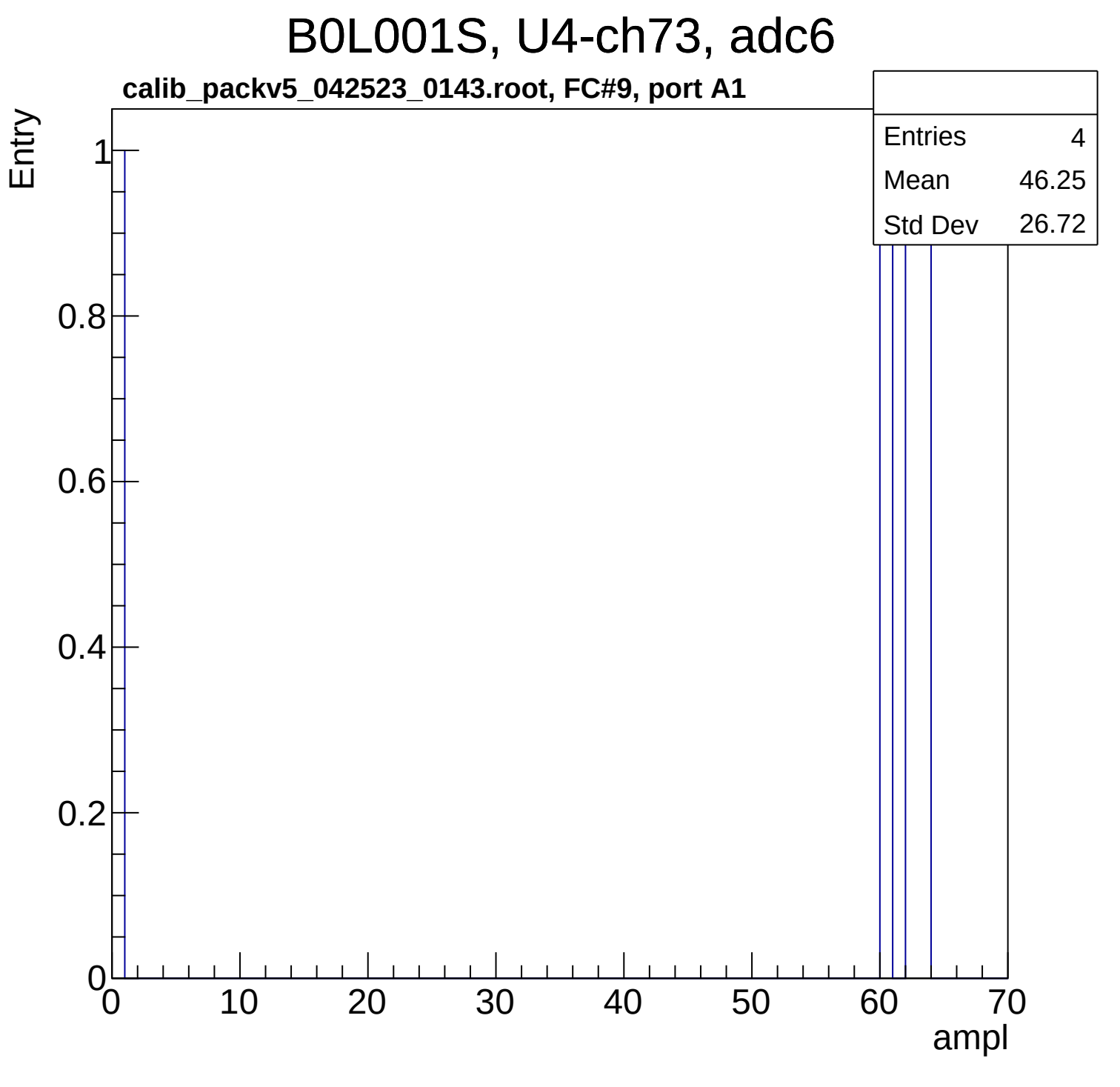

B0L001S, U4-ch73, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 36.89
Std Dev 3.834
Gaus mean : 37.6732
Gaus Width: 3.8635
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

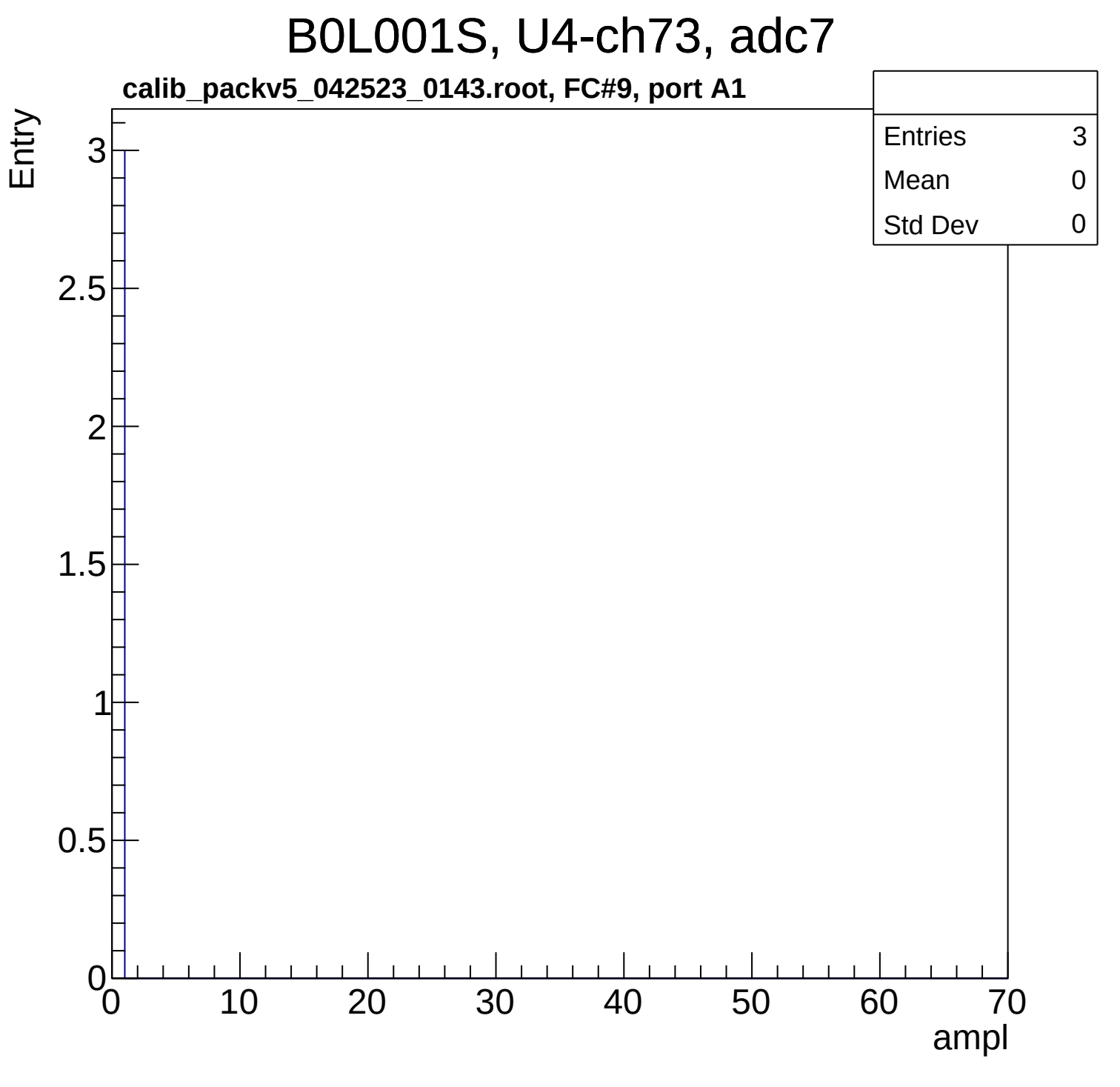

B0L001S, U4-ch73, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 43.11
Std Dev 3.266
Gaus mean : 44.1329
Gaus Width: 3.8193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

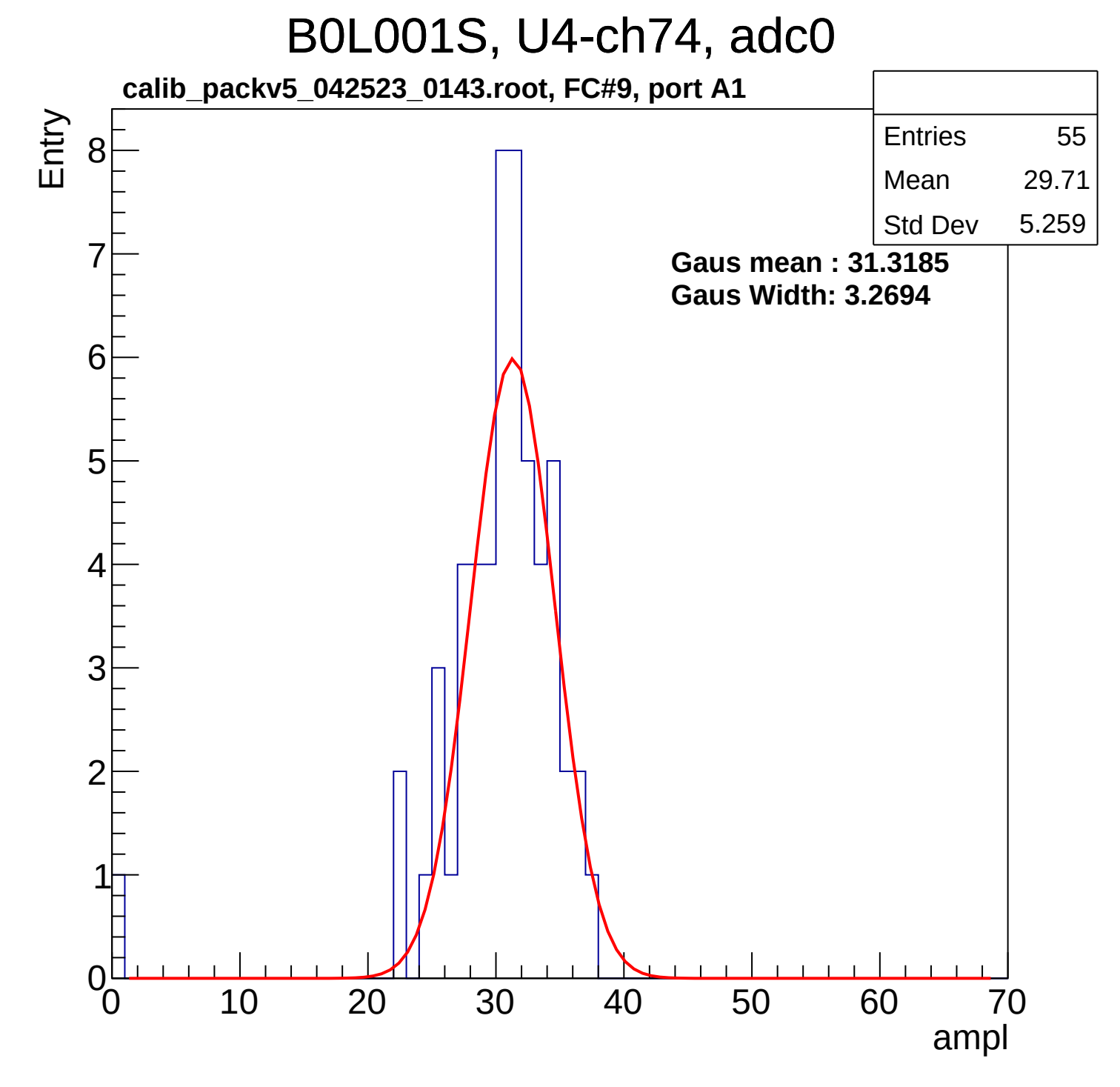

B0L001S, U4-ch73, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 50.6
Std Dev 3.884
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

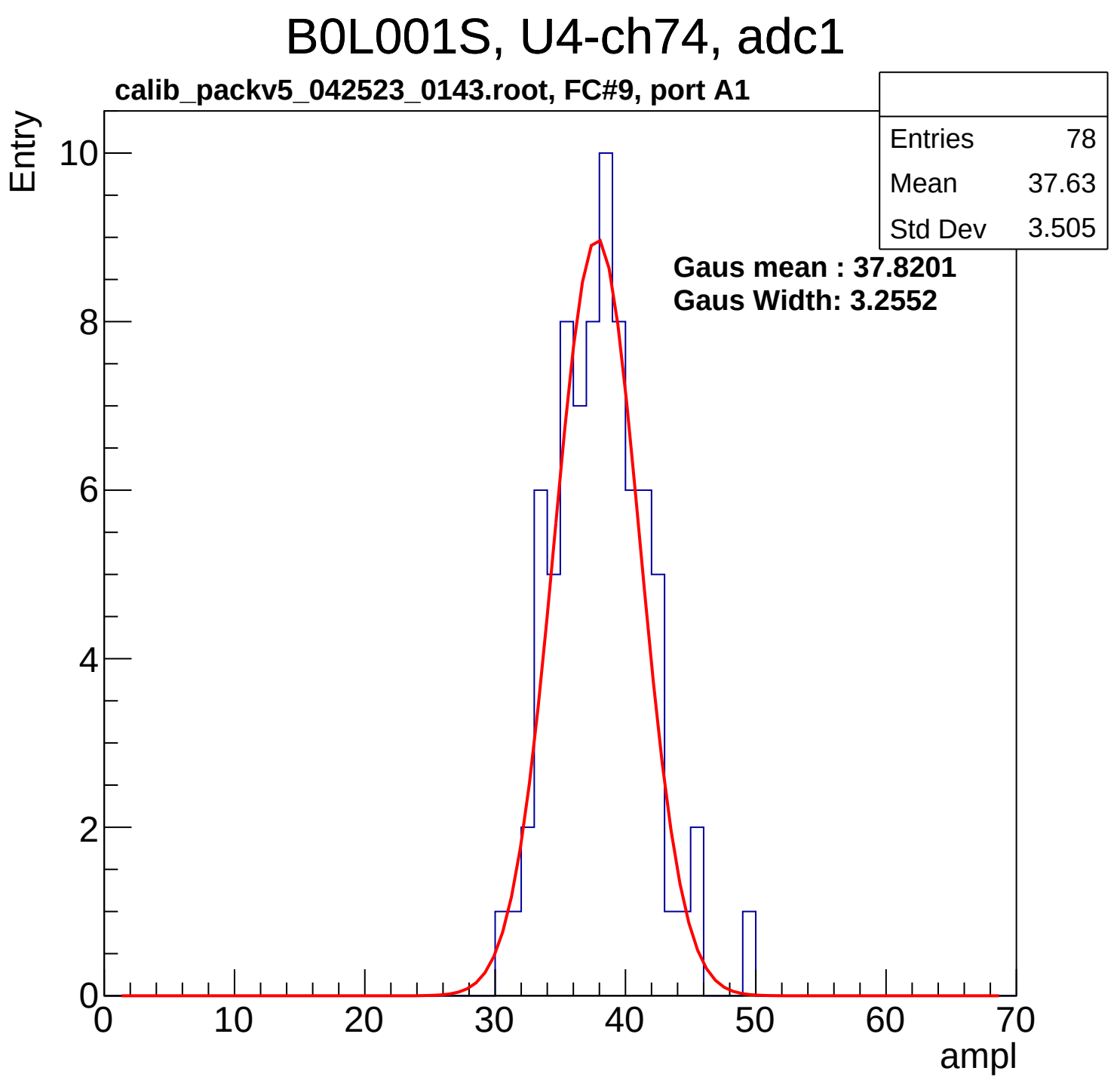

B0L001S, U4-ch73, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.66
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

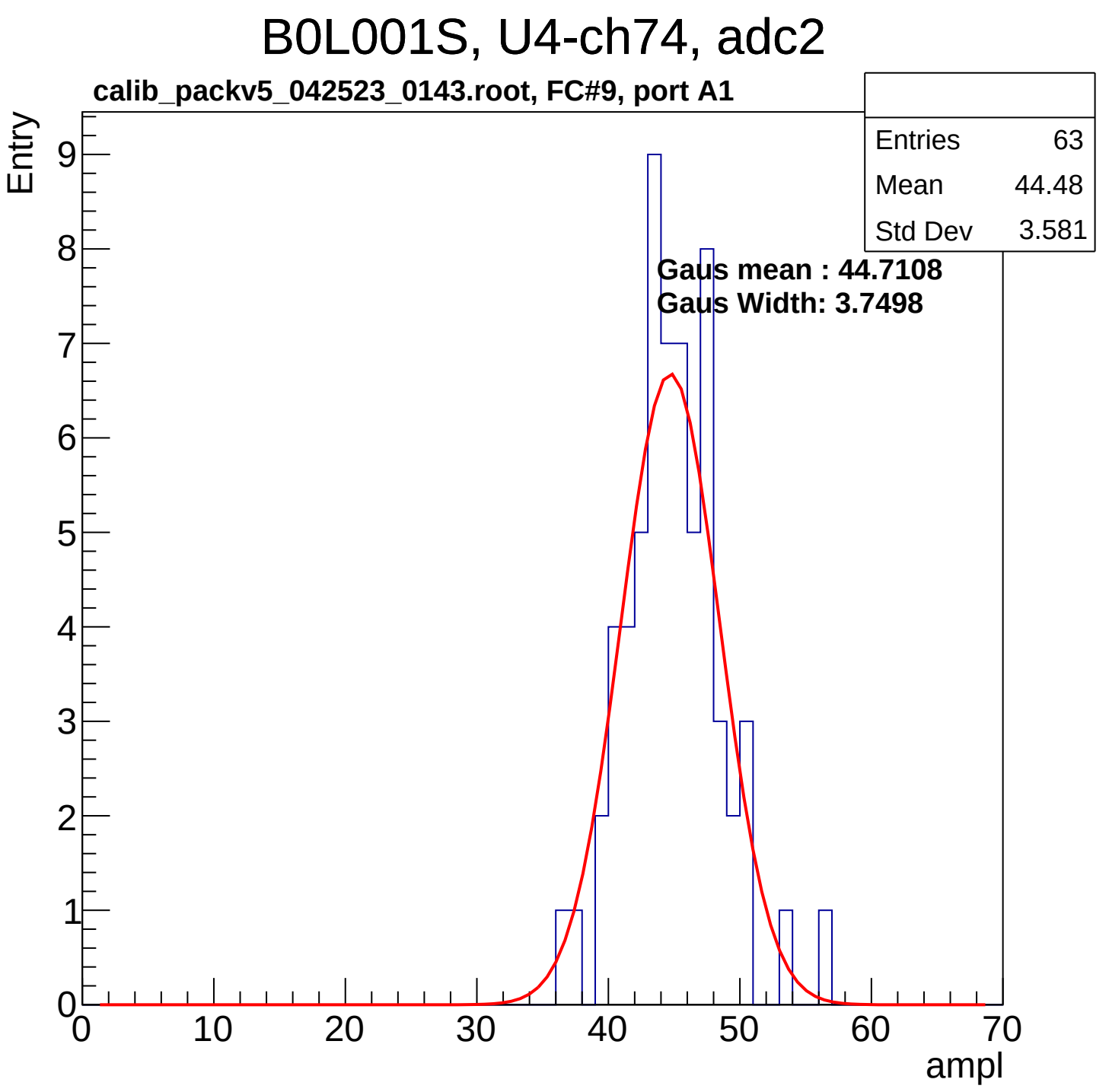

B0L001S, U4-ch73, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 60.2
Std Dev 2.502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

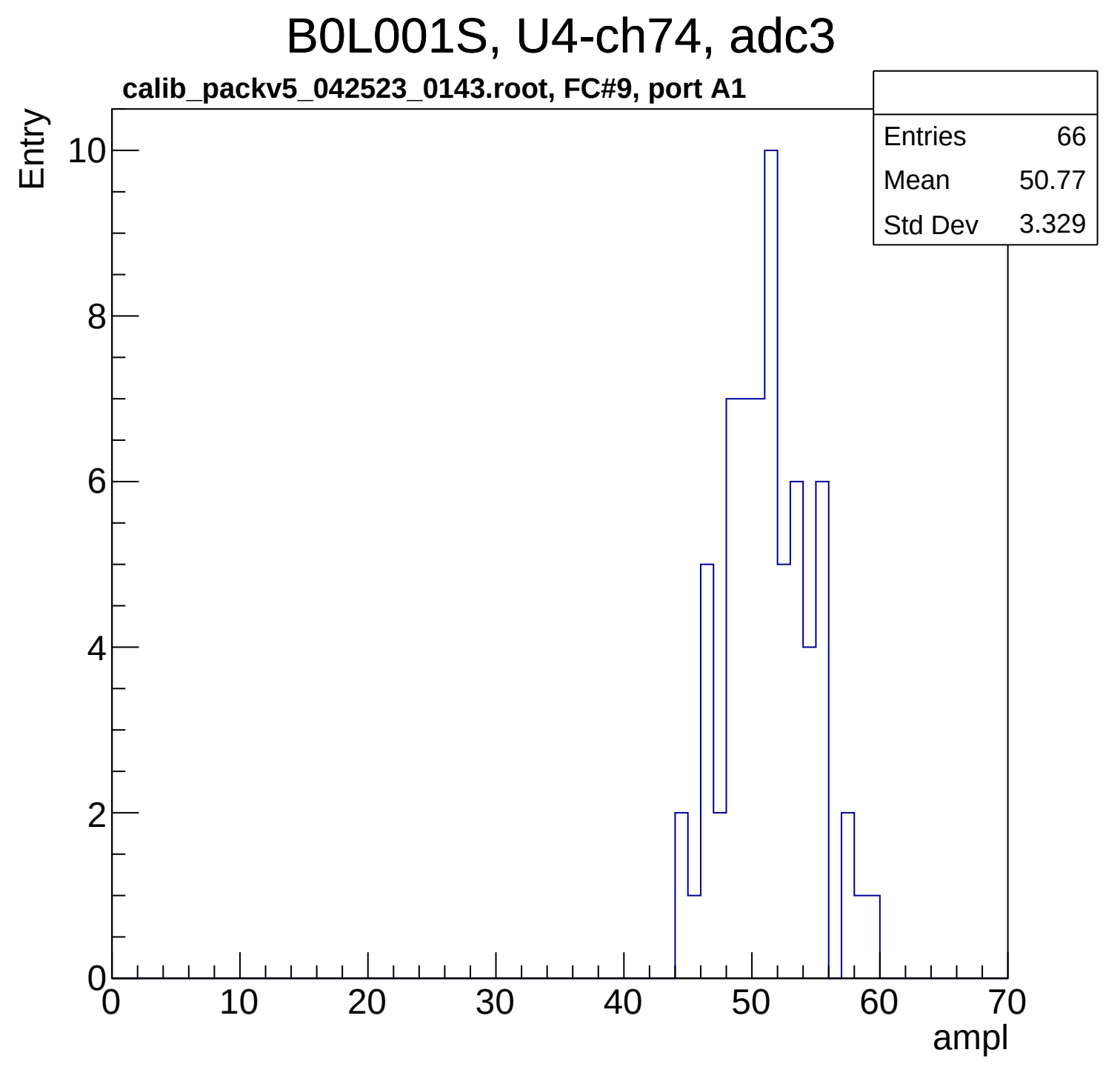

B0L001S, U4-ch73, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.25
Std Dev 26.72
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

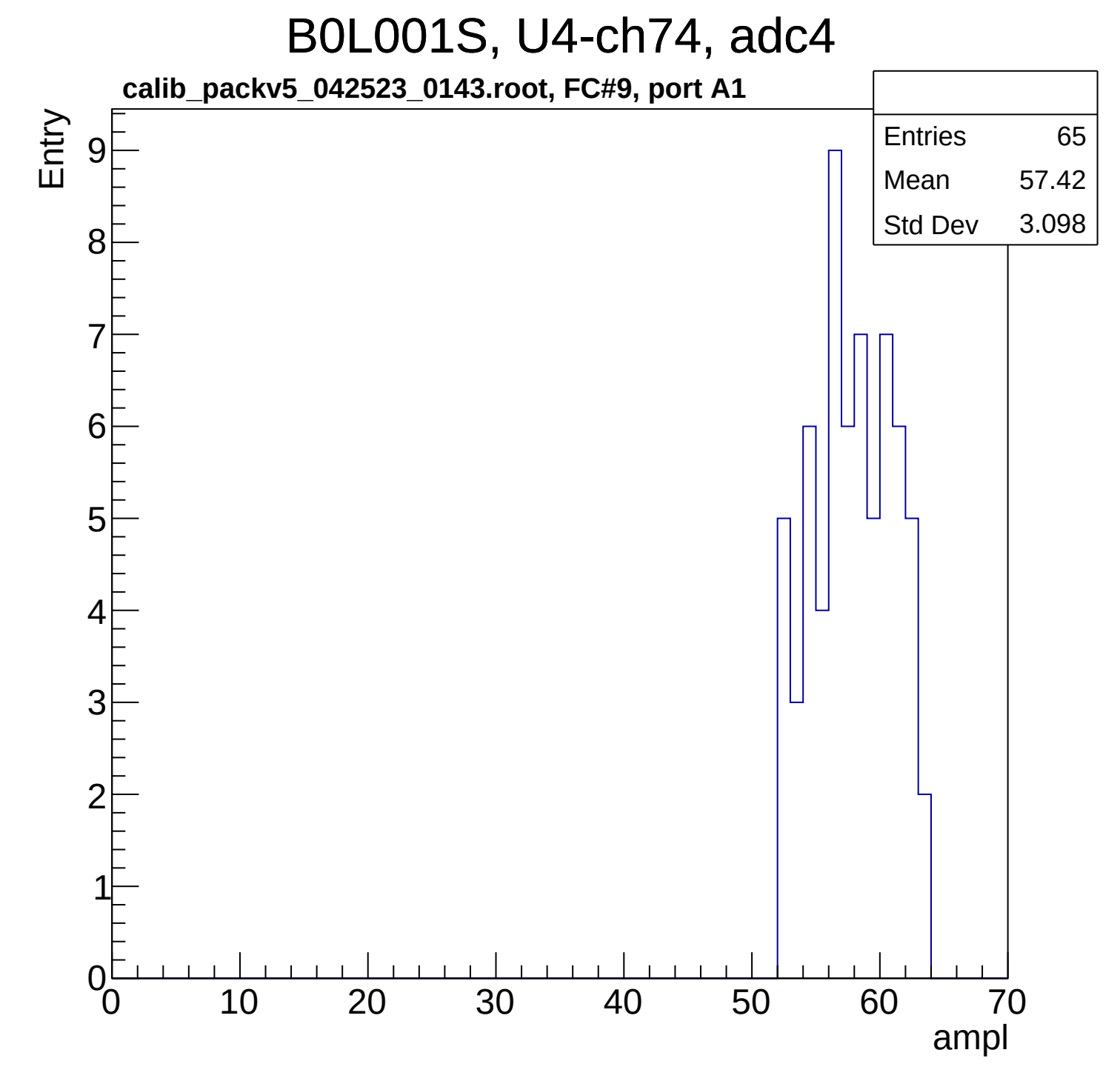

B0L001S, U4-ch73, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

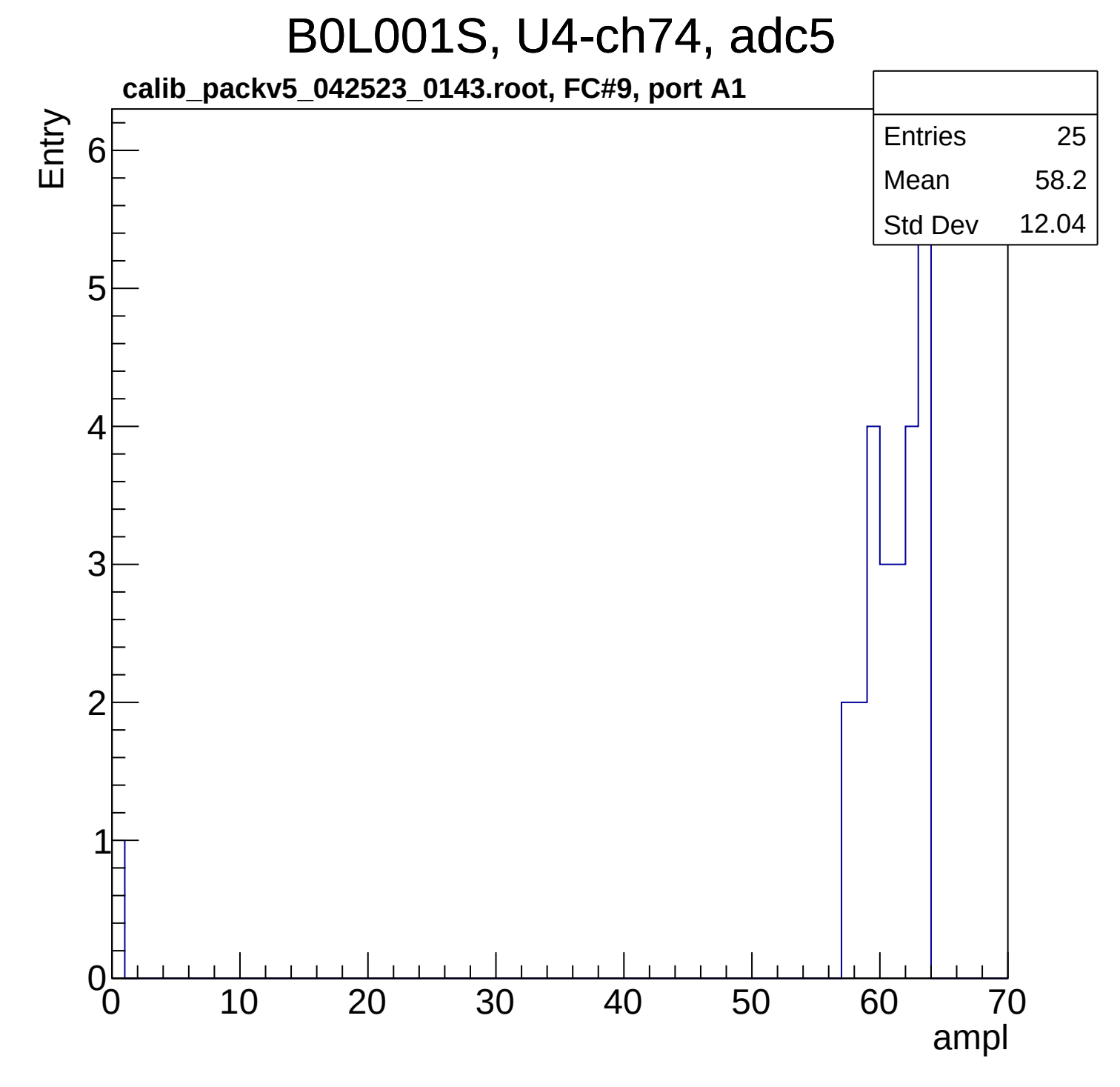

B0L001S, U4-ch74, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 29.71
Std Dev 5.259
Gaus mean : 31.3185
Gaus Width: 3.2694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

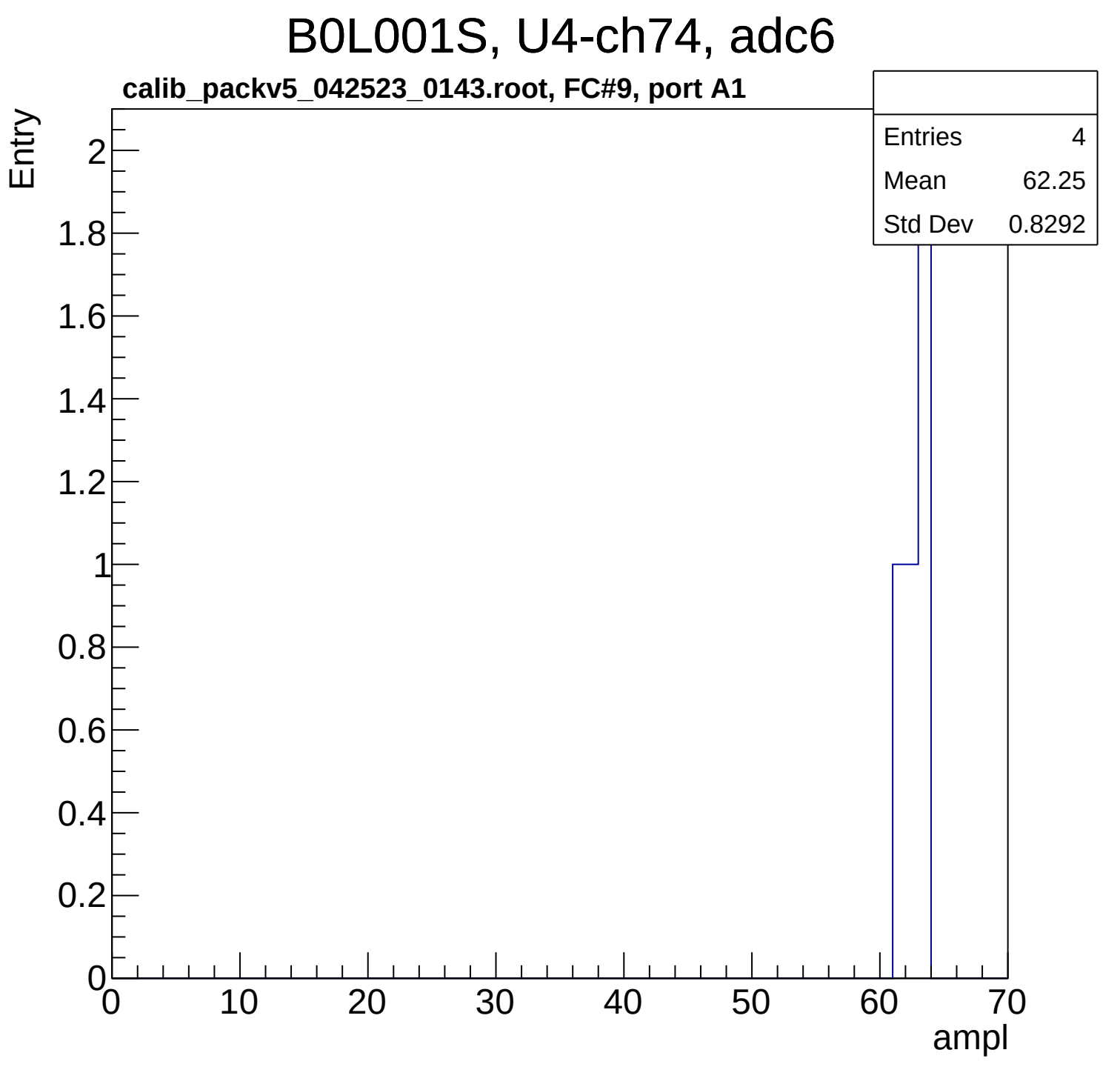

B0L001S, U4-ch74, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.63
Std Dev 3.505
Gaus mean : 37.8201
Gaus Width: 3.2552
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

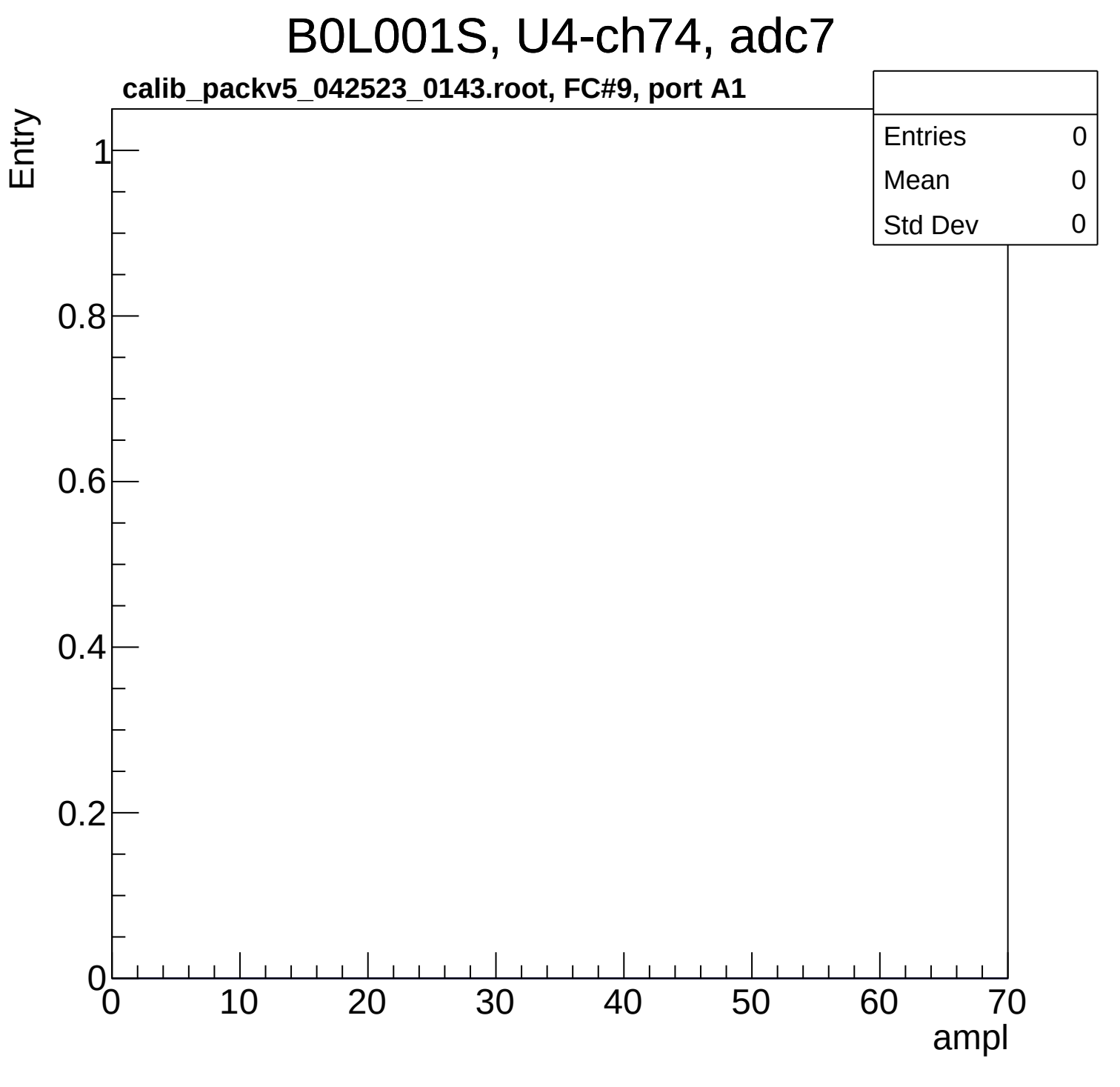

B0L001S, U4-ch74, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.48
Std Dev 3.581
Gaus mean : 44.7108
Gaus Width: 3.7498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

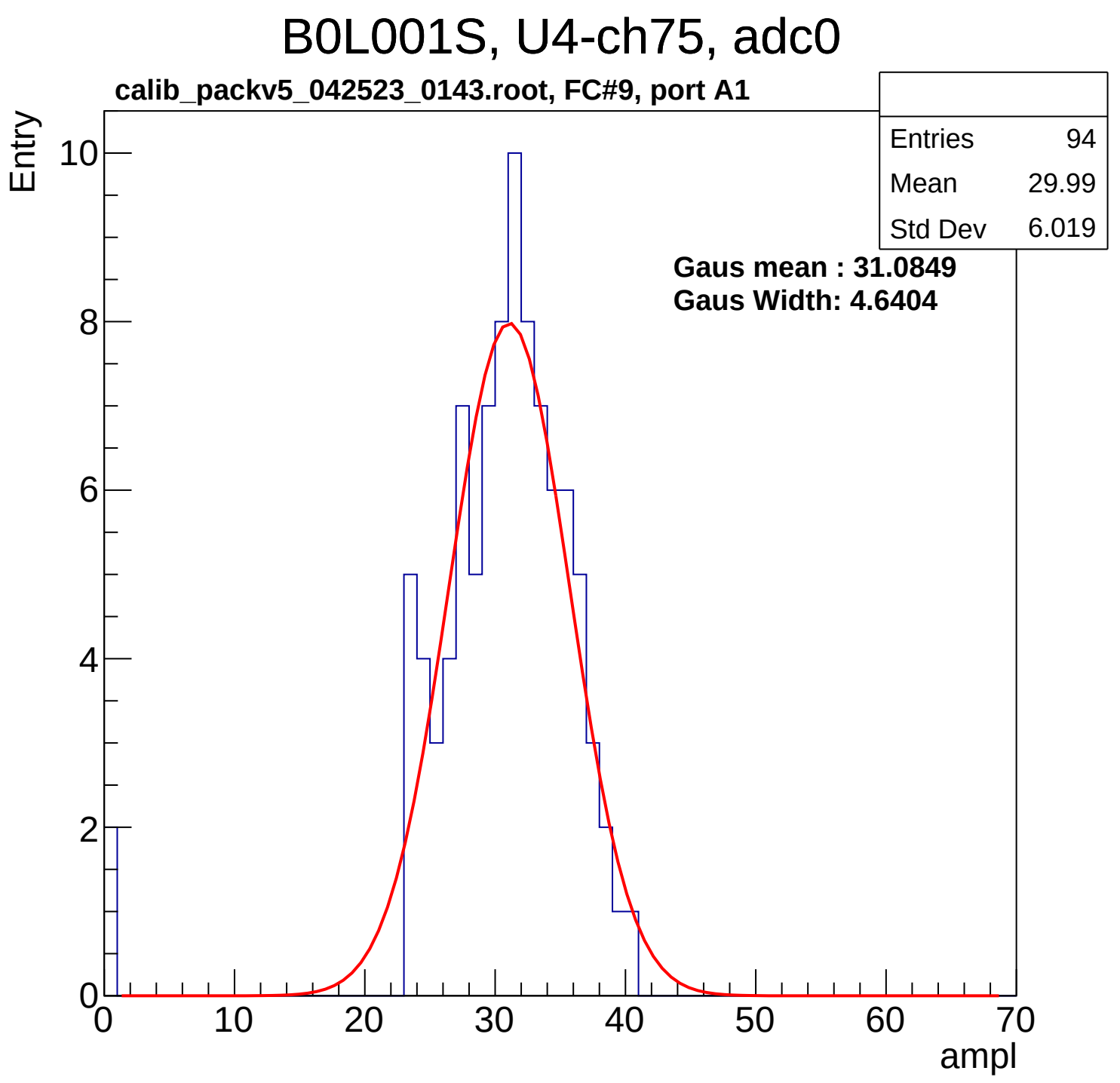

B0L001S, U4-ch74, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.77
Std Dev 3.329
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

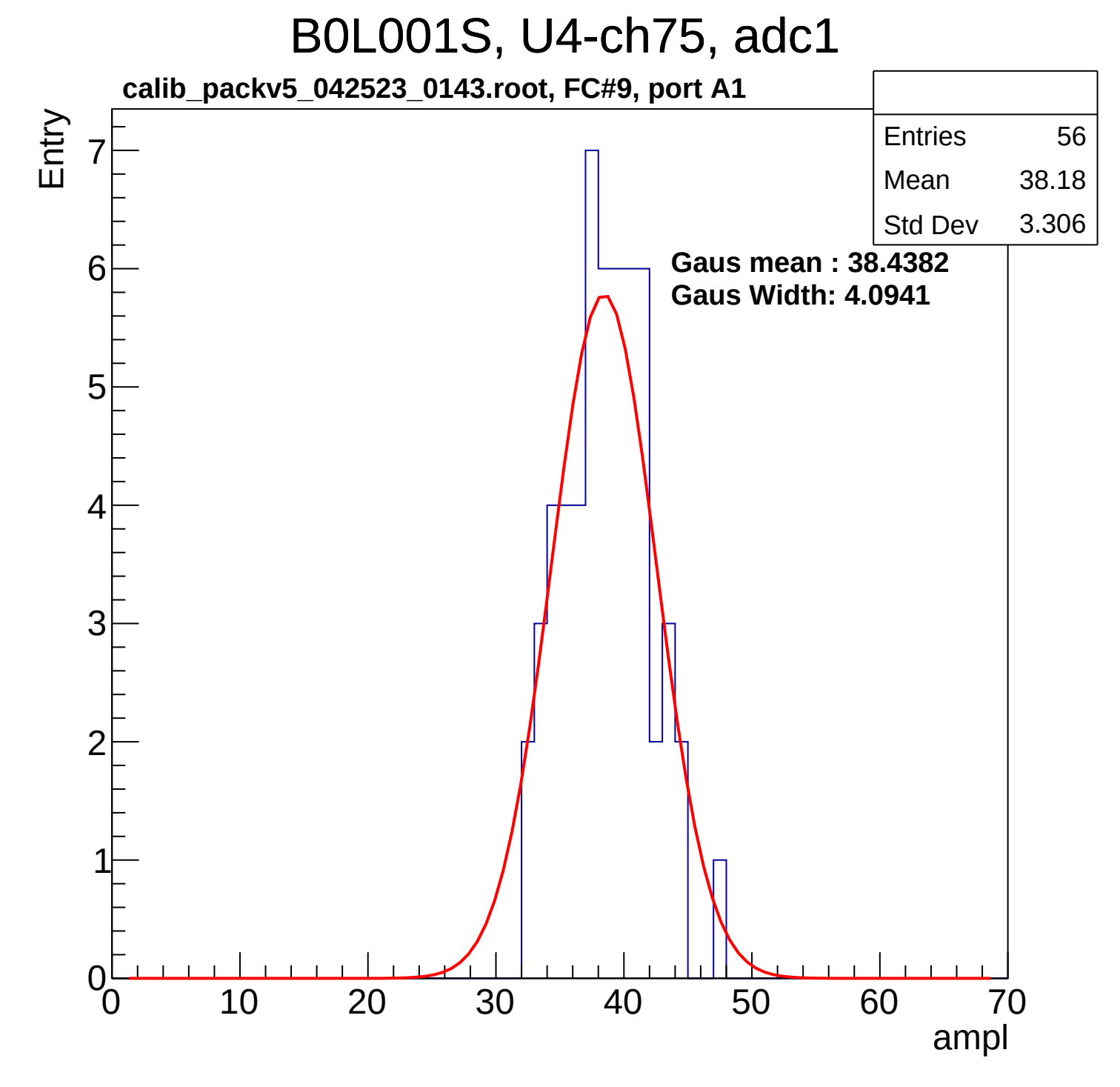

B0L001S, U4-ch74, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 57.42
Std Dev 3.098
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

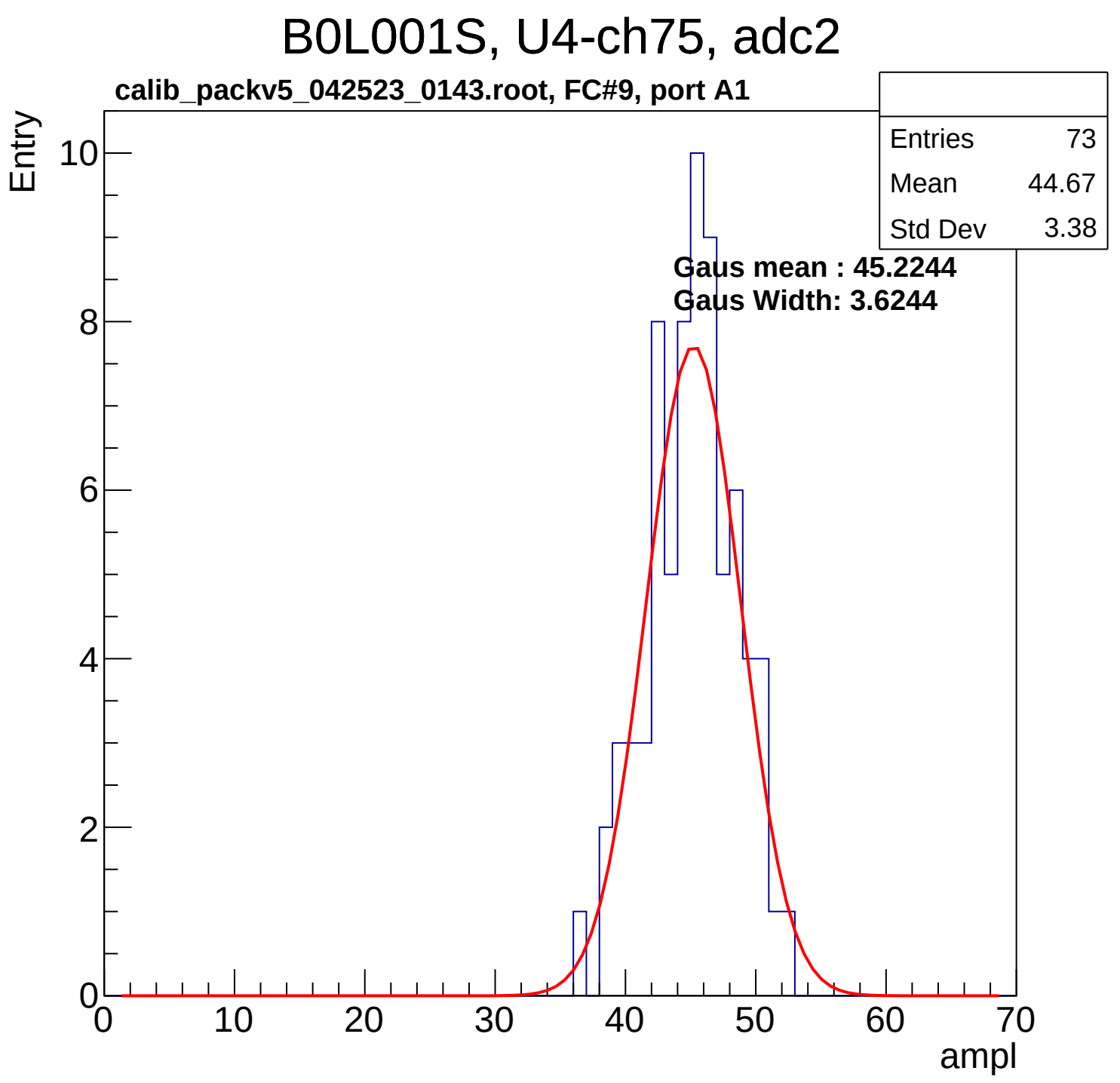

B0L001S, U4-ch74, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.2
Std Dev 12.04
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

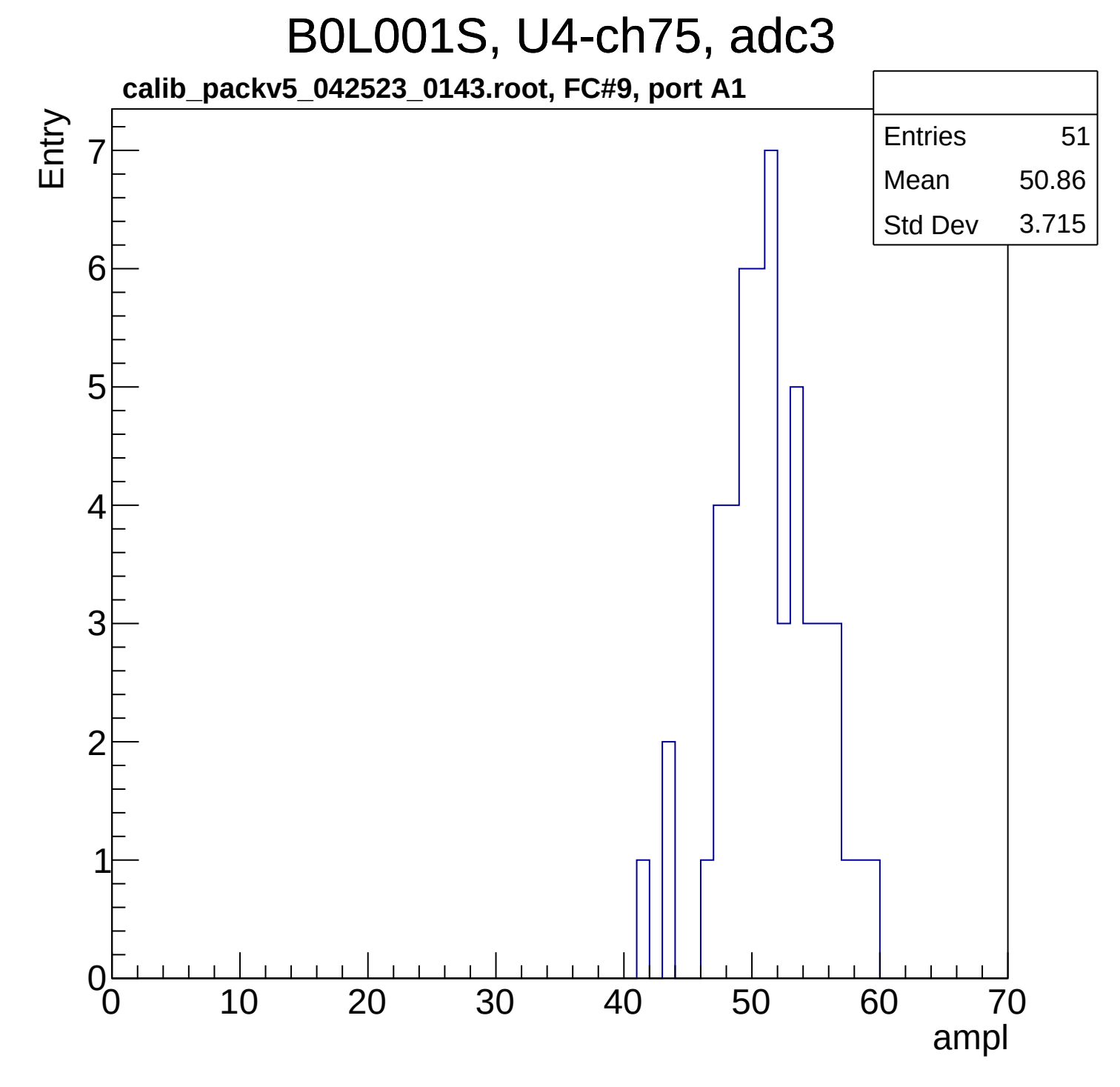

B0L001S, U4-ch74, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

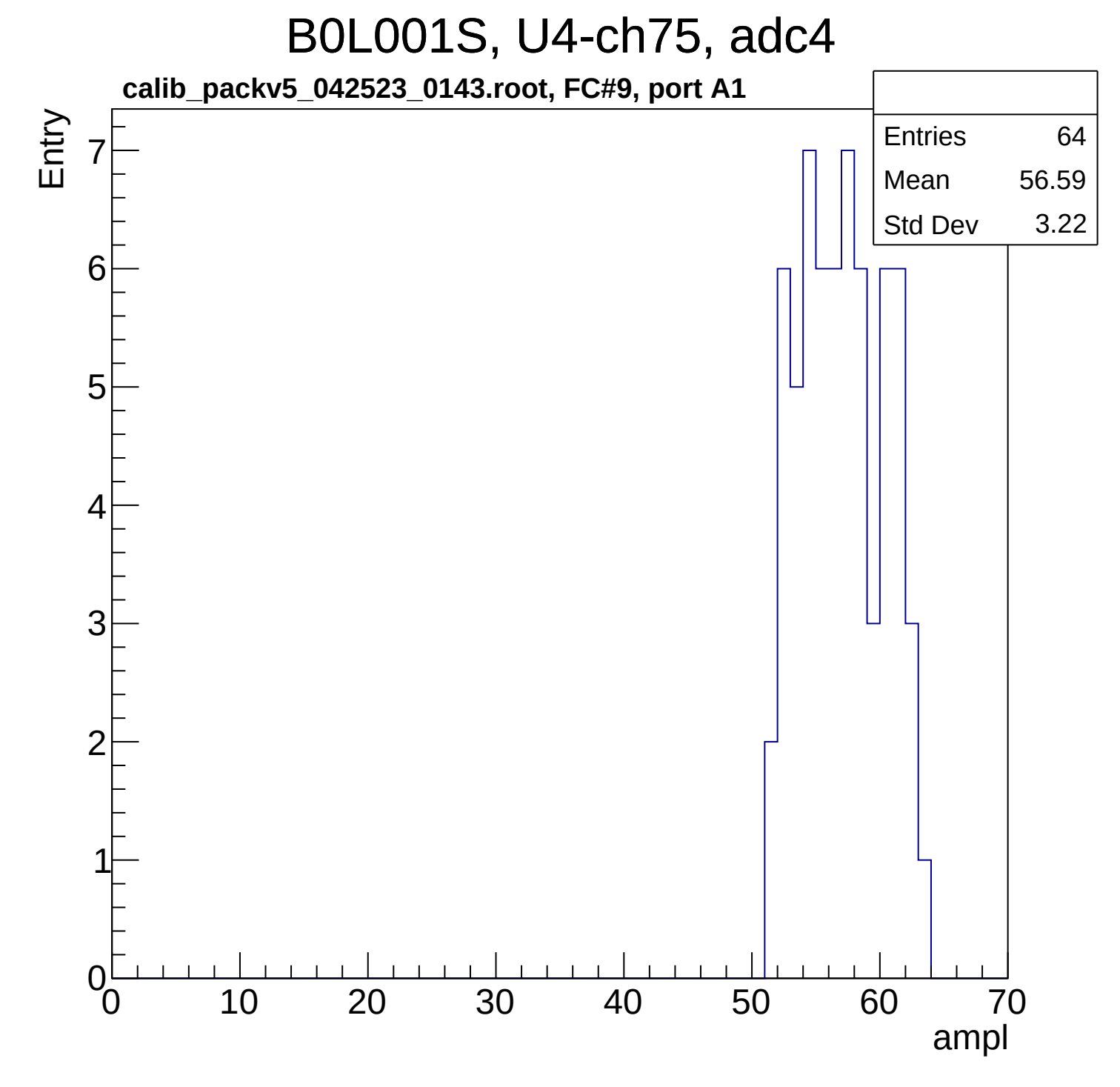

B0L001S, U4-ch74, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

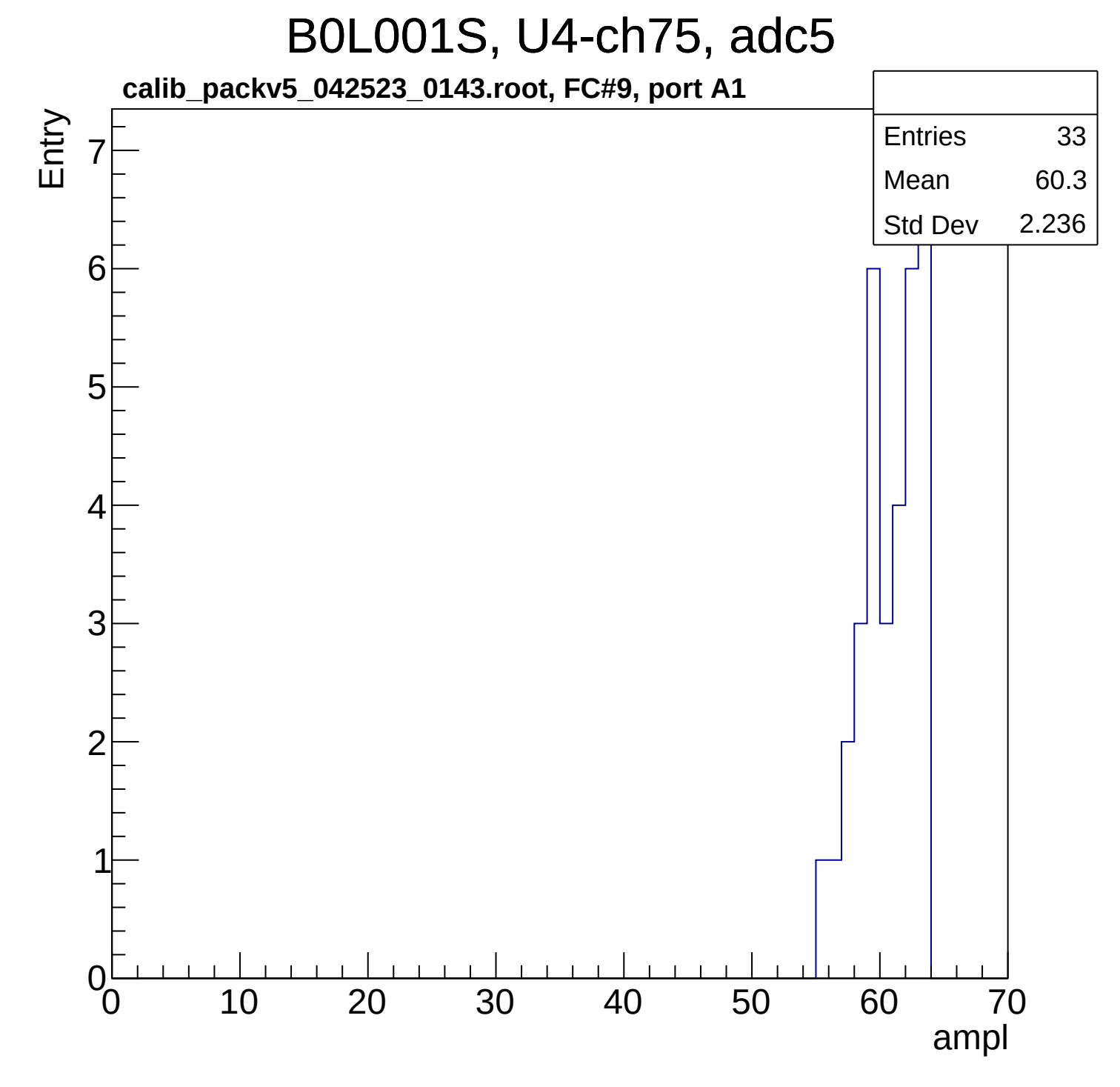

B0L001S, U4-ch75, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 94
Mean 29.99
Std Dev 6.019
Gaus mean : 31.0849
Gaus Width: 4.6404
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

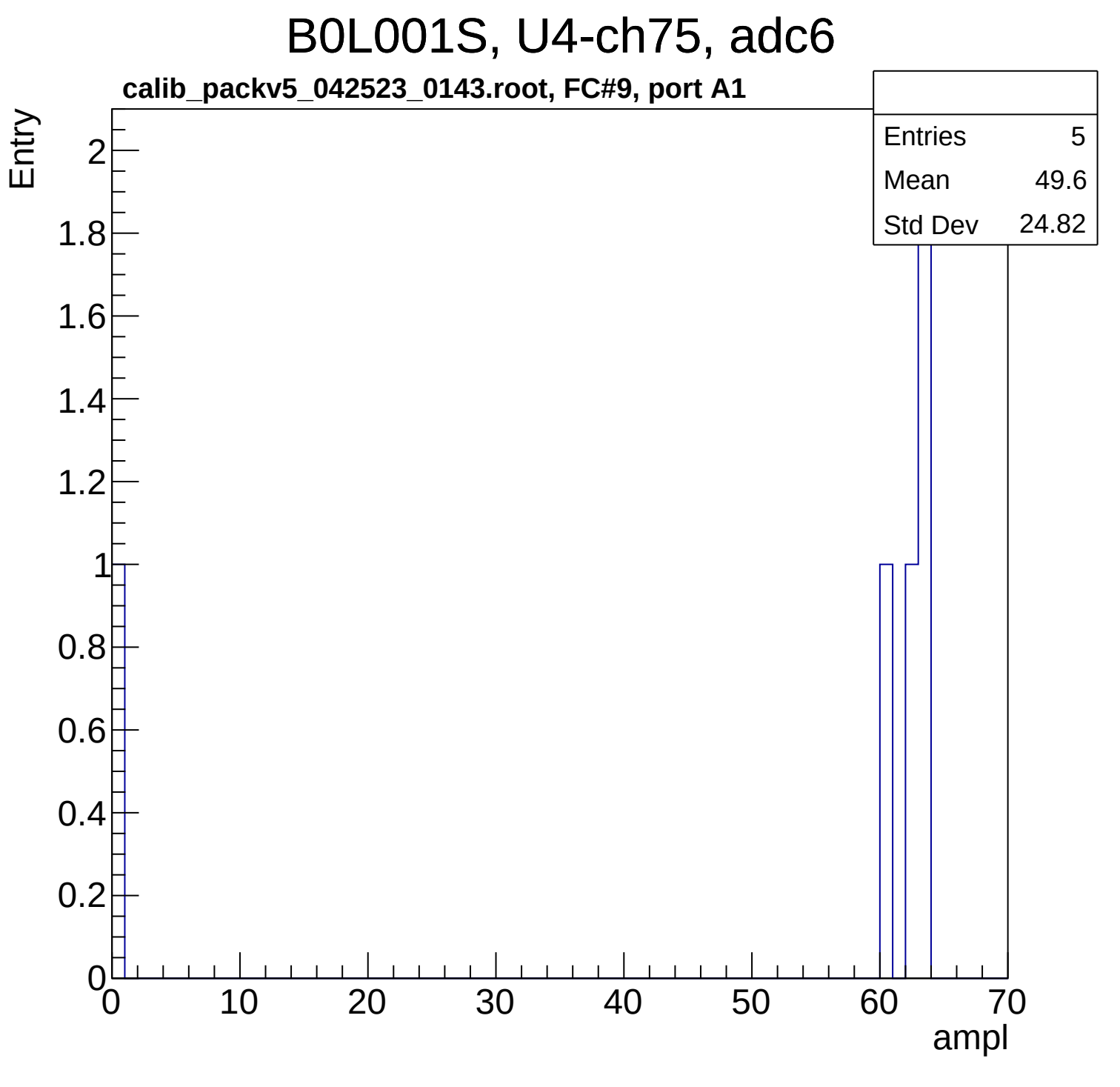

B0L001S, U4-ch75, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 38.18
Std Dev 3.306
Gaus mean : 38.4382
Gaus Width: 4.0941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

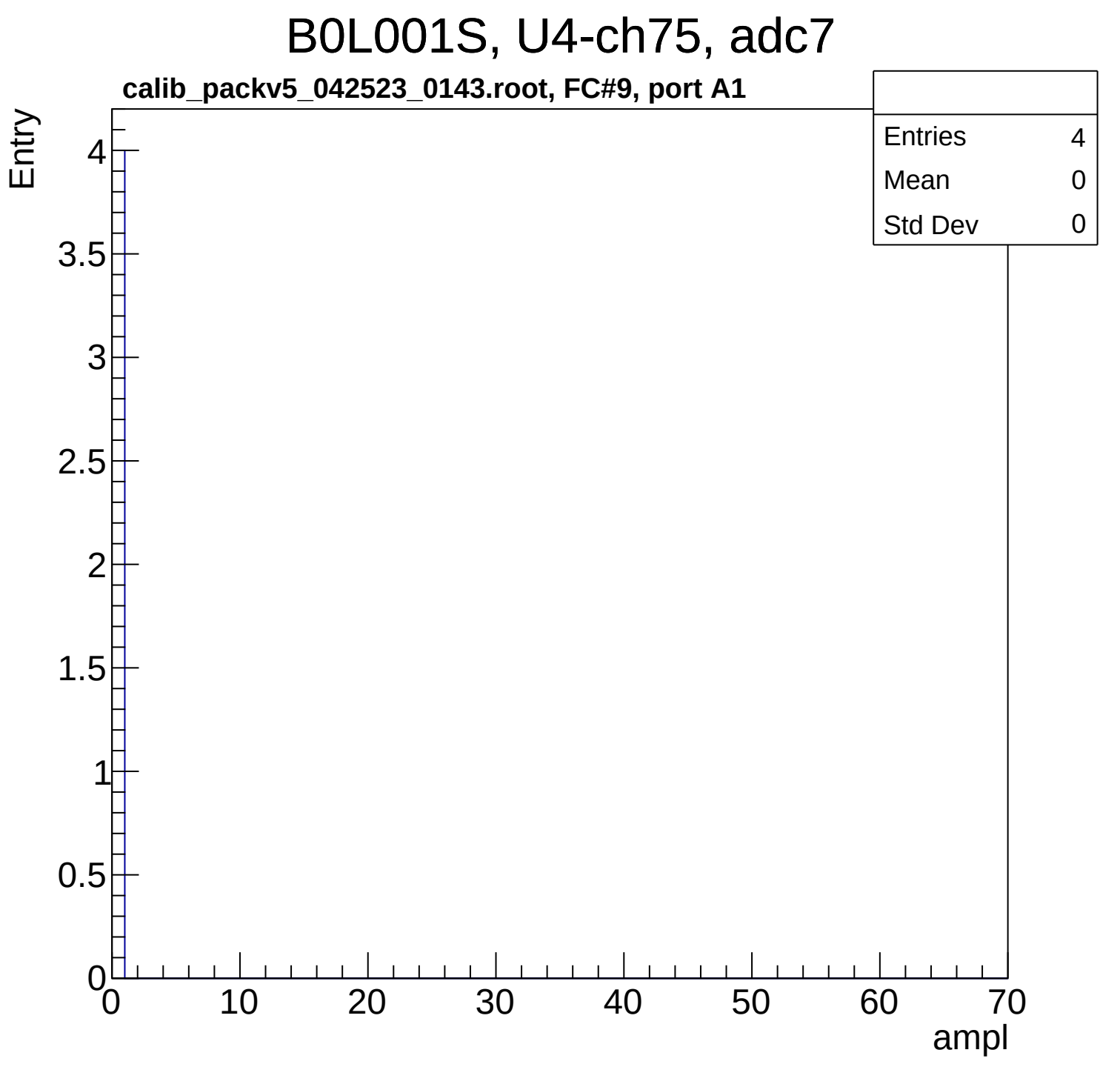

B0L001S, U4-ch75, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.67
Std Dev 3.38
Gaus mean : 45.2244
Gaus Width: 3.6244
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

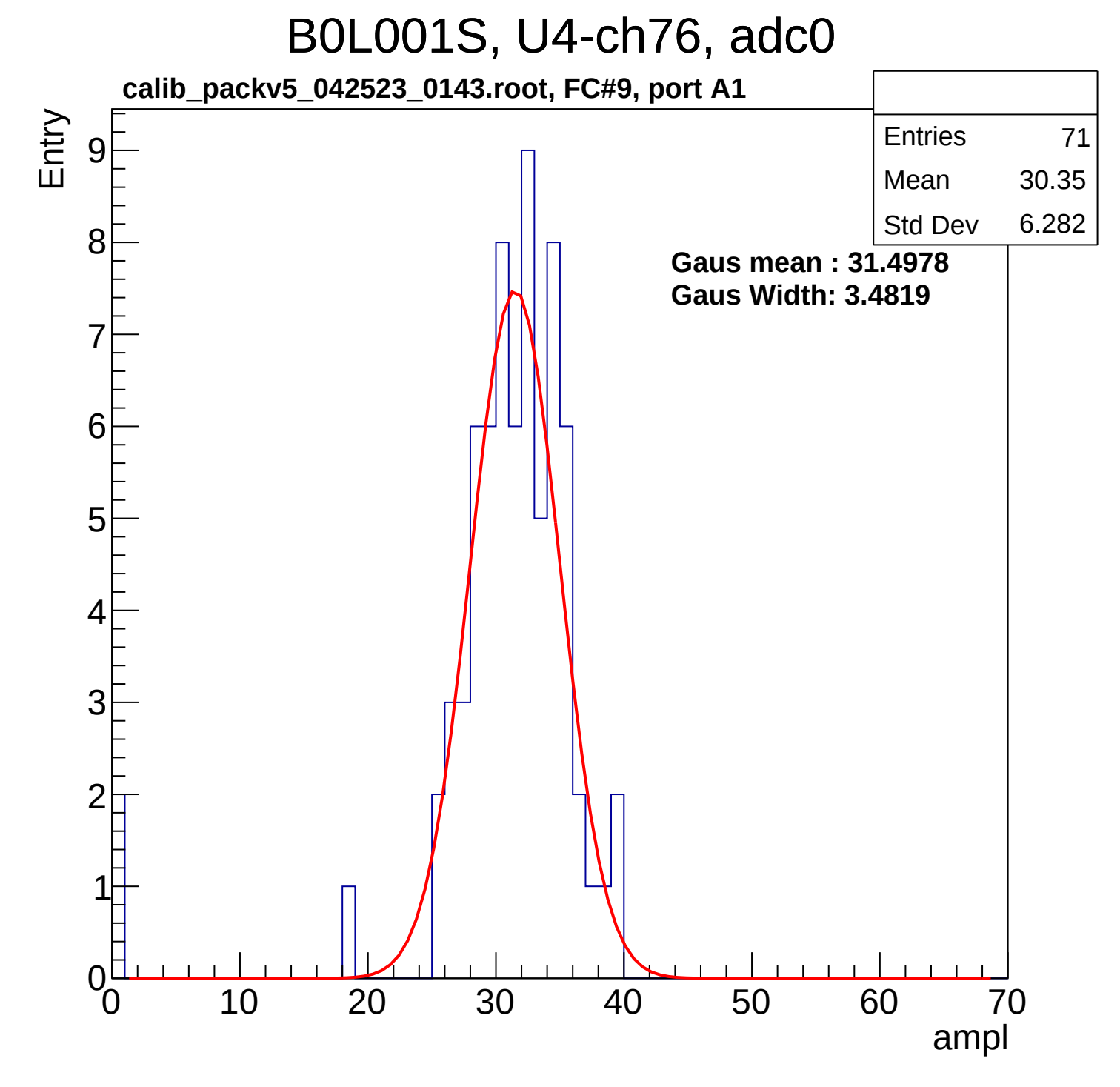

B0L001S, U4-ch75, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 50.86
Std Dev 3.715
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

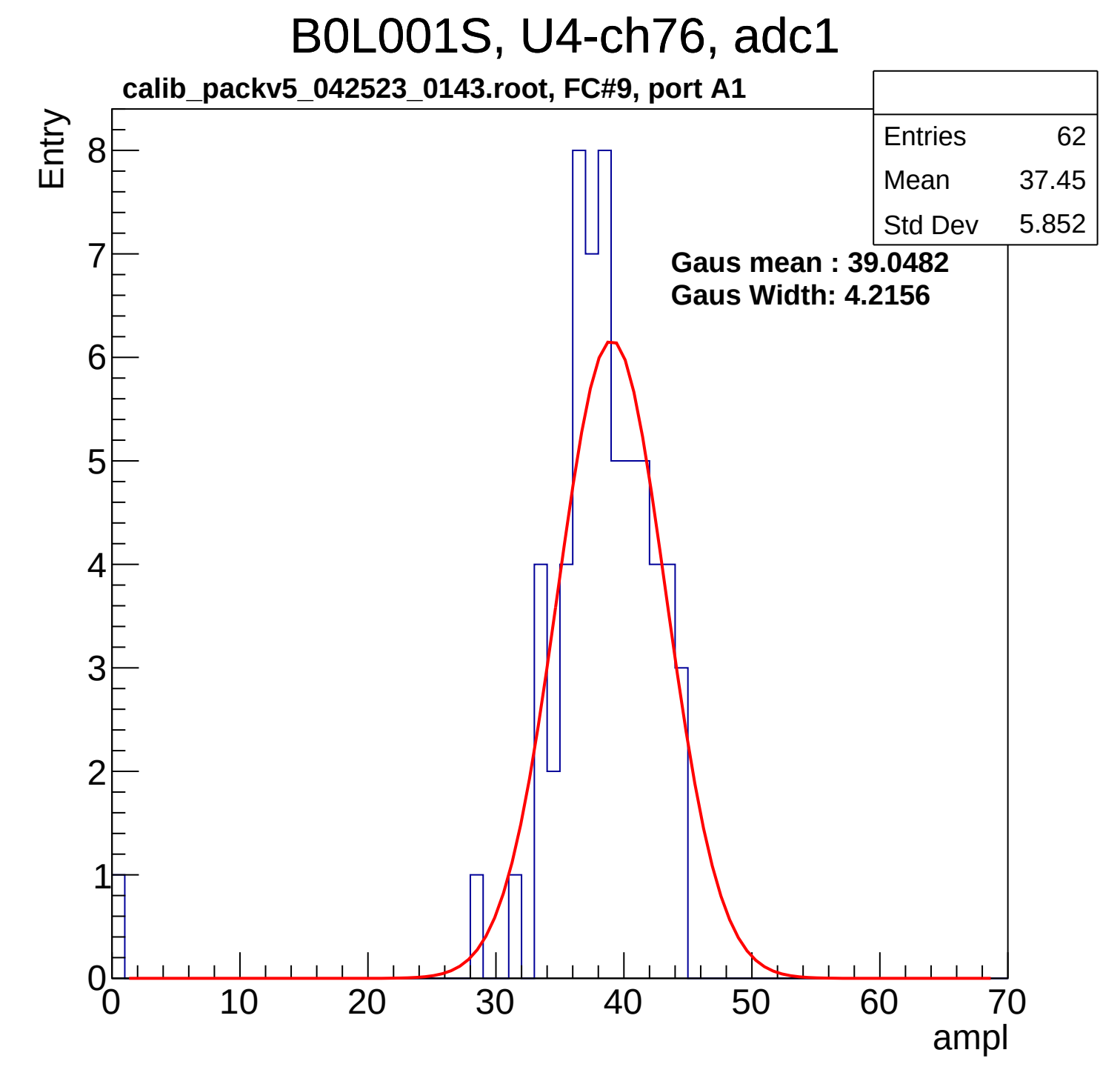

B0L001S, U4-ch75, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.59
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

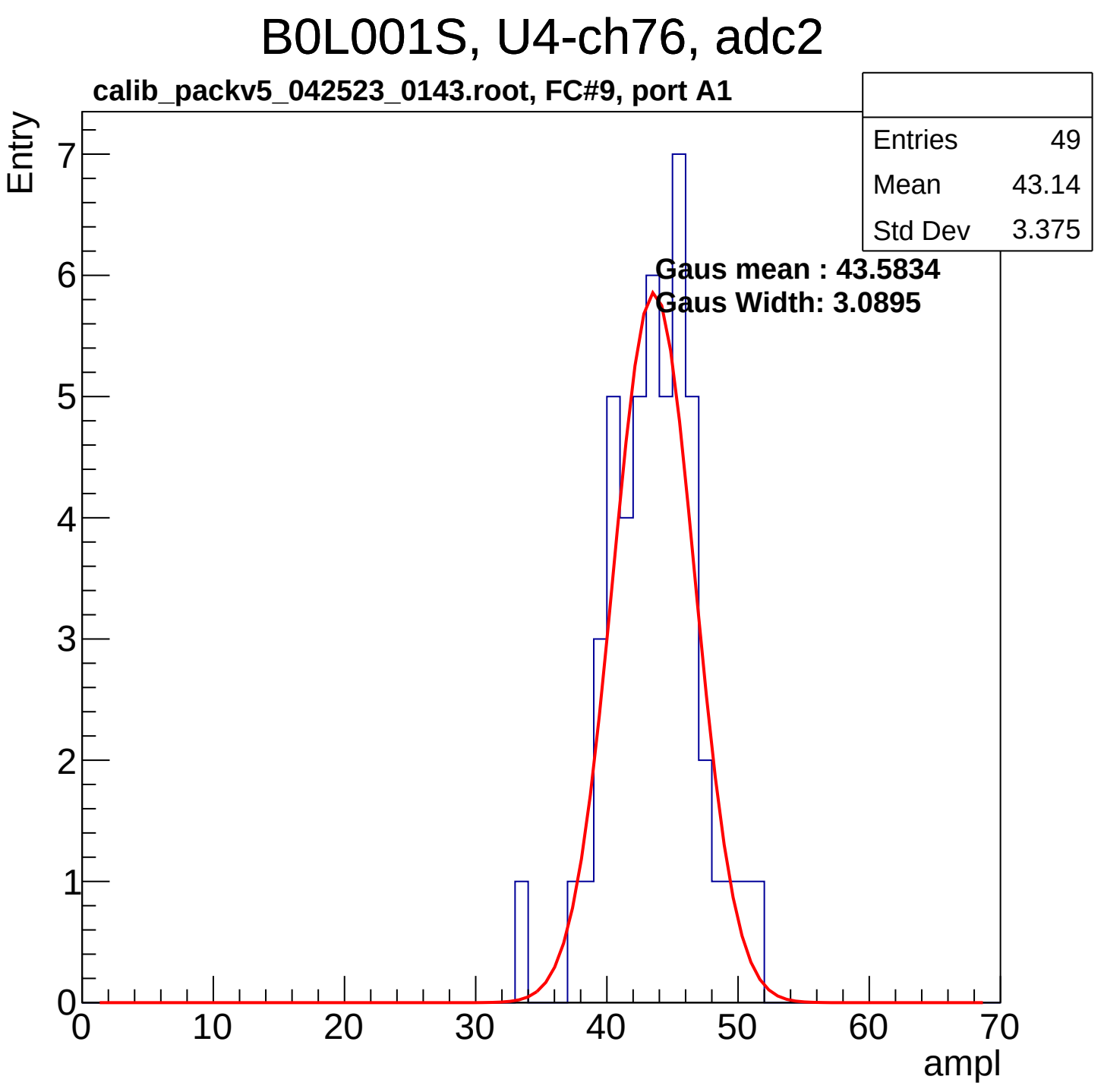

B0L001S, U4-ch75, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 60.3
Std Dev 2.236
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

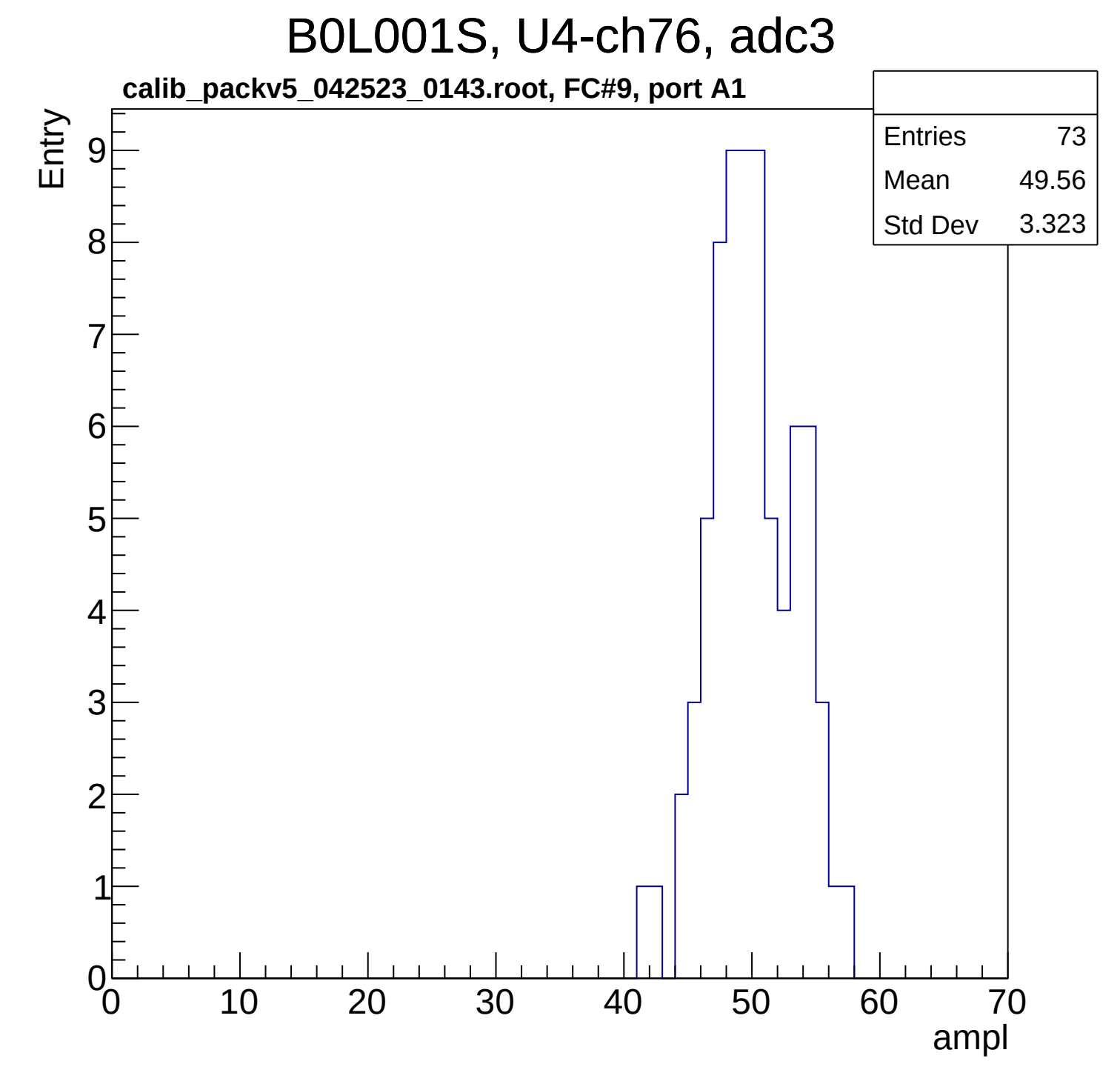

B0L001S, U4-ch75, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch75, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch76, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.35
Std Dev 6.282
Gaus mean : 31.4978
Gaus Width: 3.4819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch76, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.45
Std Dev 5.852
Gaus mean : 39.0482
Gaus Width: 4.2156
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch76, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 43.14
Std Dev 3.375
Gaus mean : 43.5834
Gaus Width: 3.0895
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch76, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 49.56
Std Dev 3.323
Entry
ampl
0
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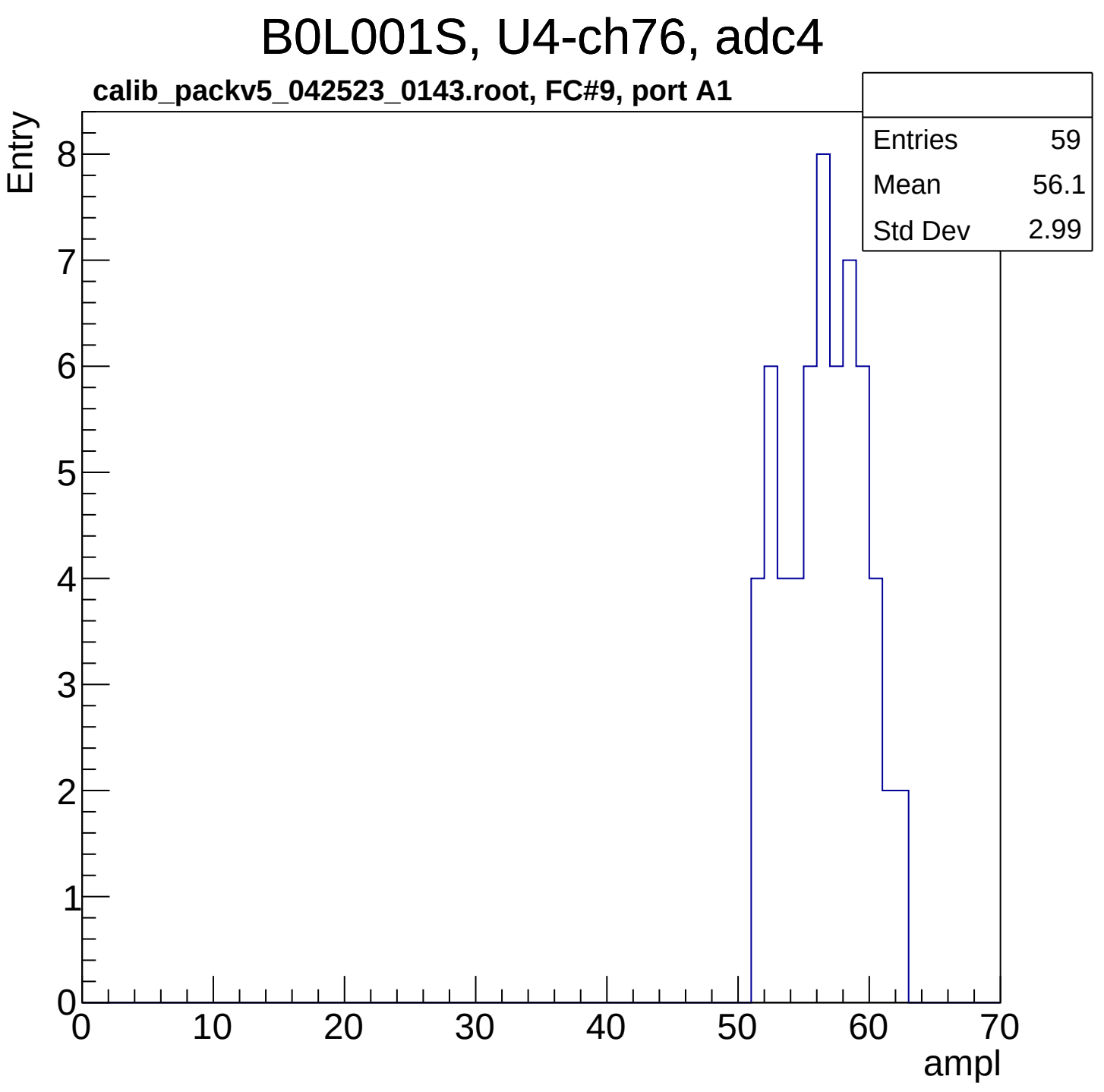
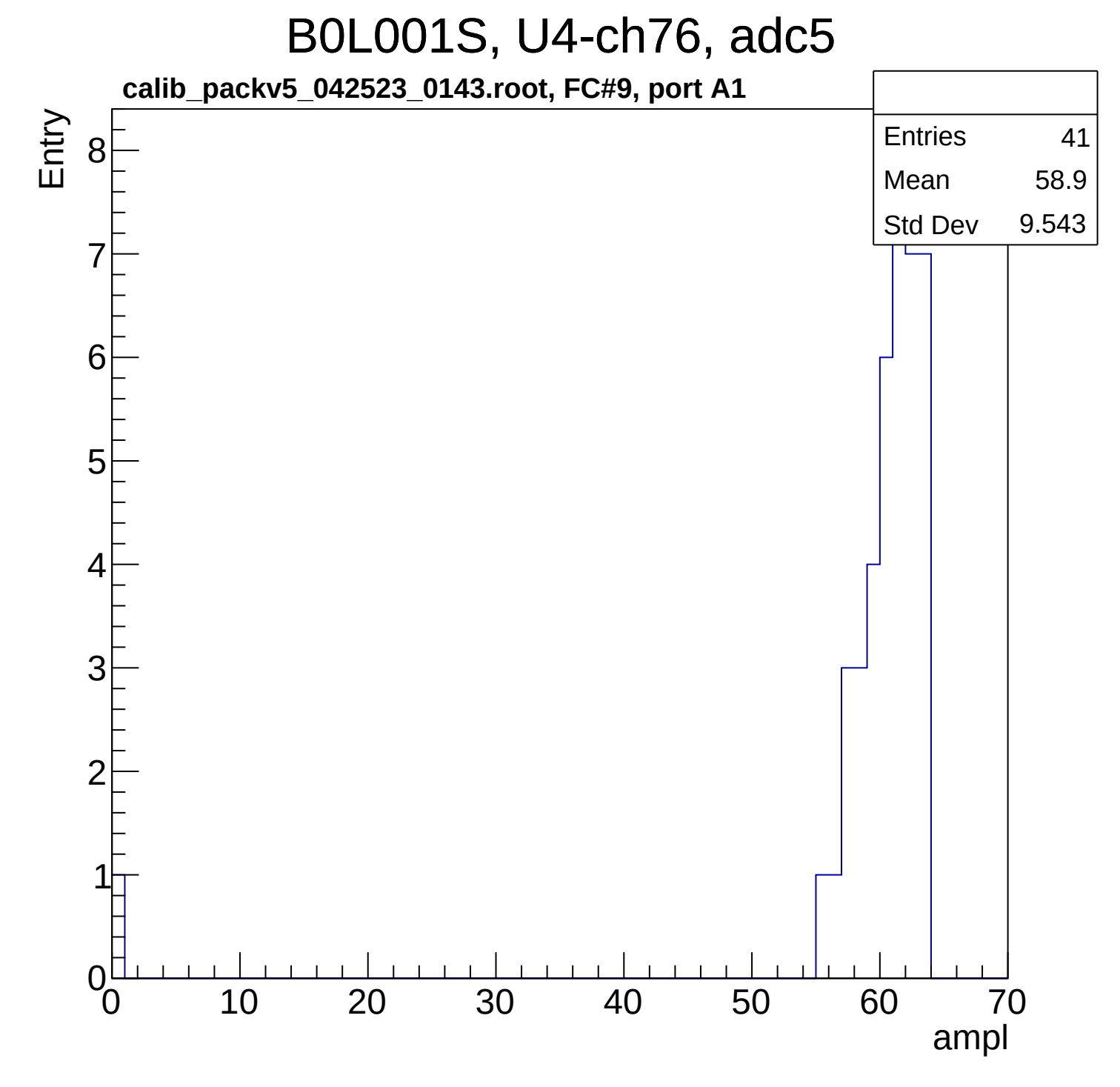
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch76, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.1
Std Dev 2.99
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch76, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.9
Std Dev 9.543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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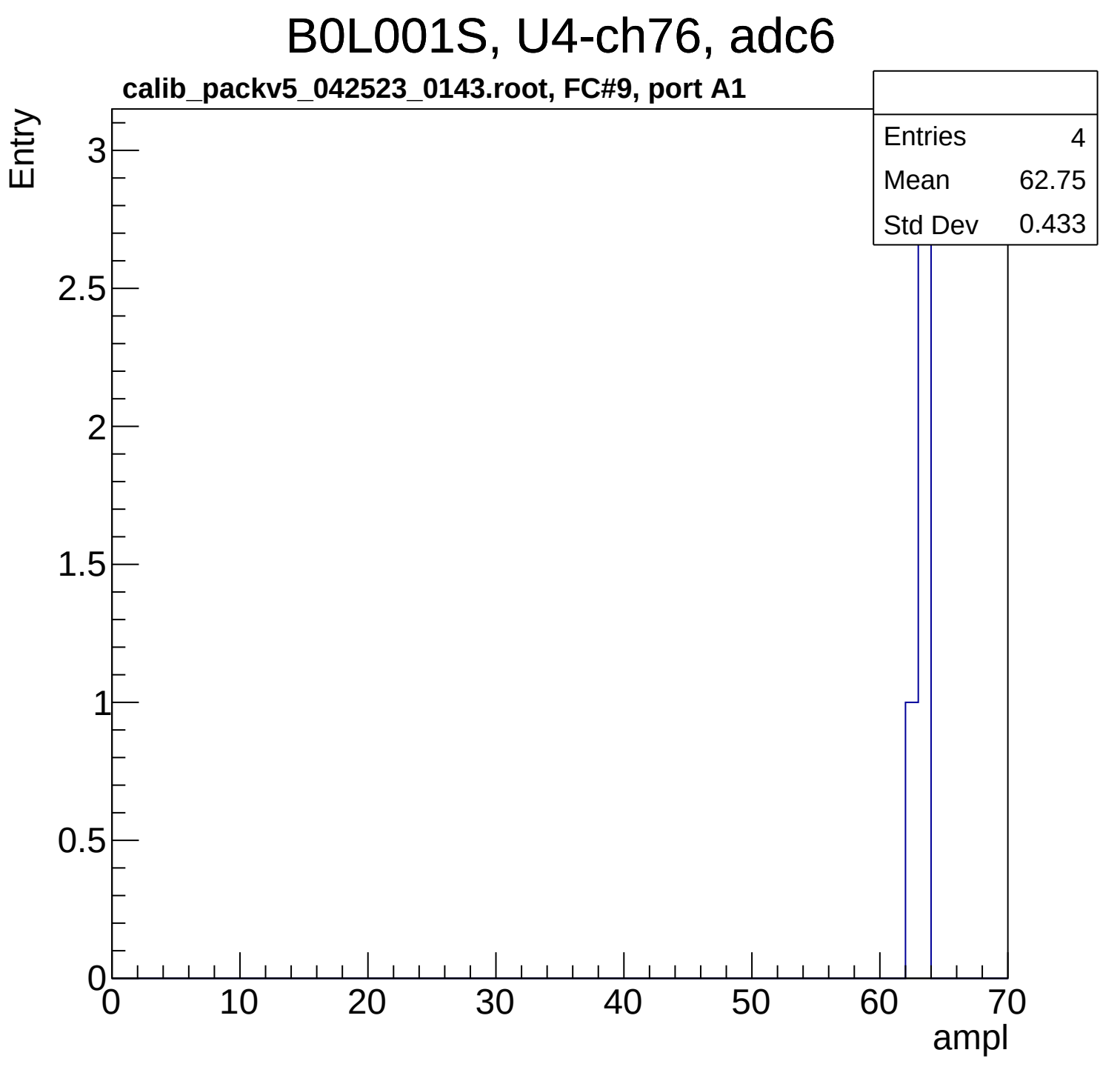
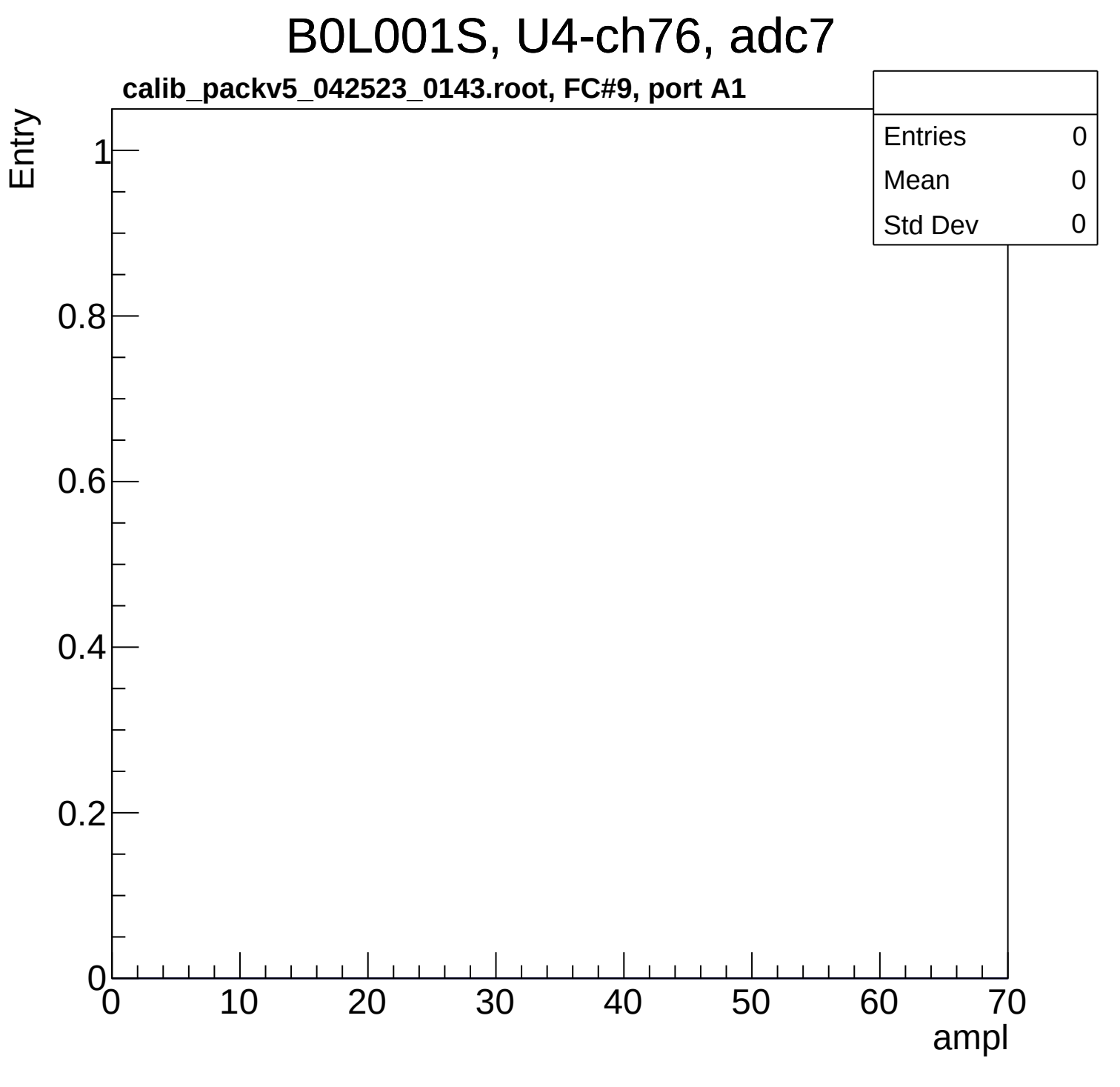
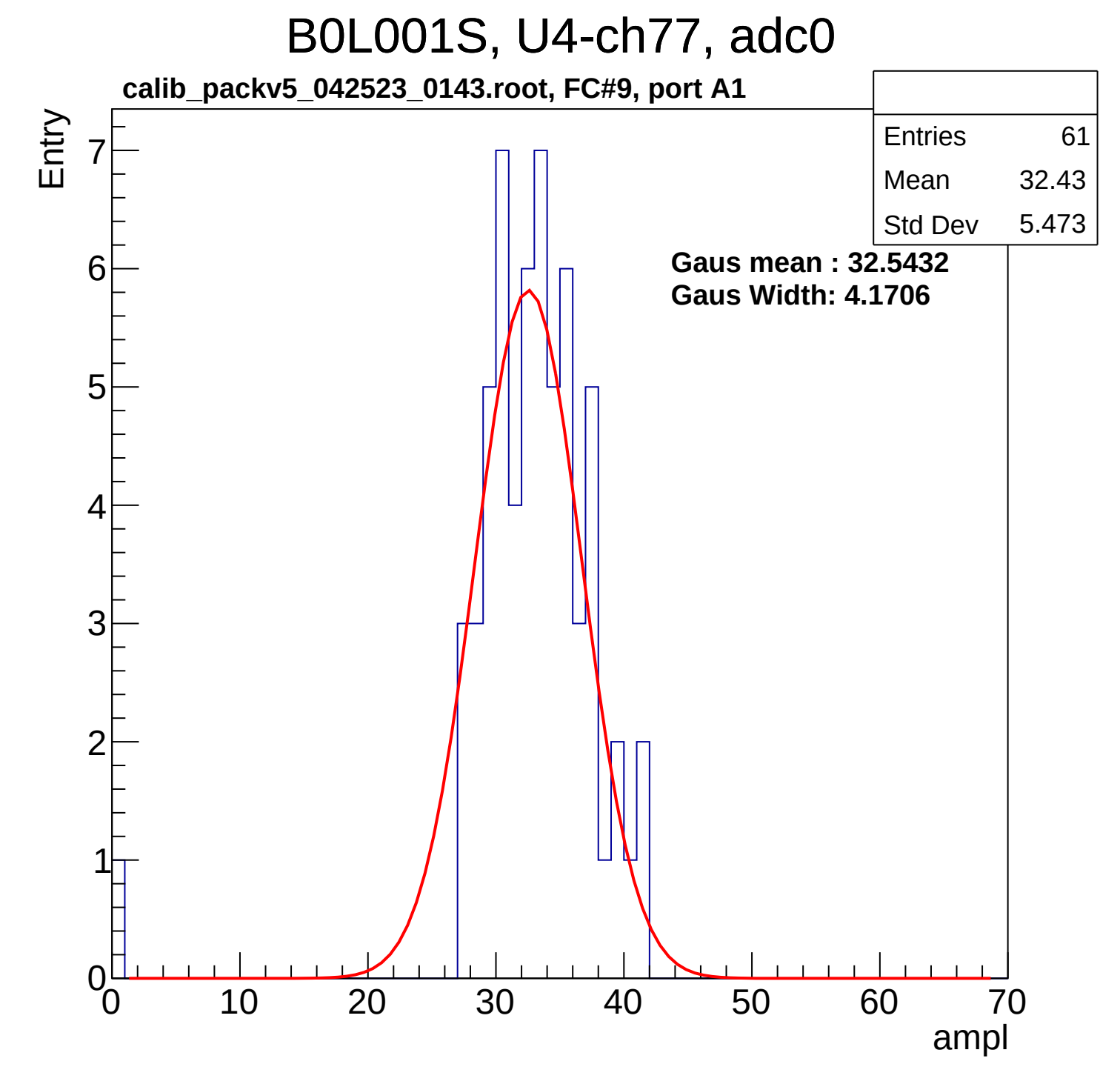
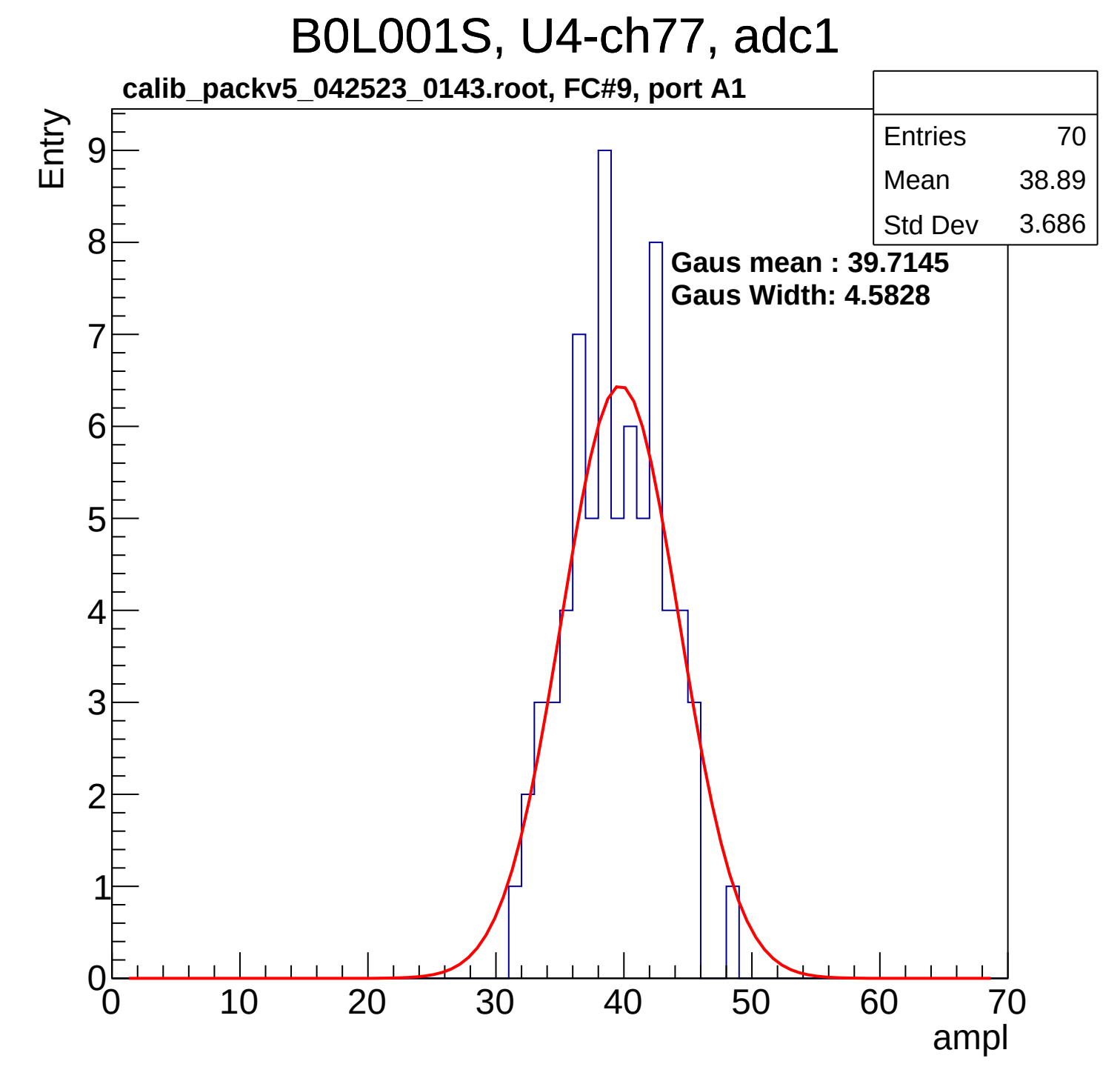
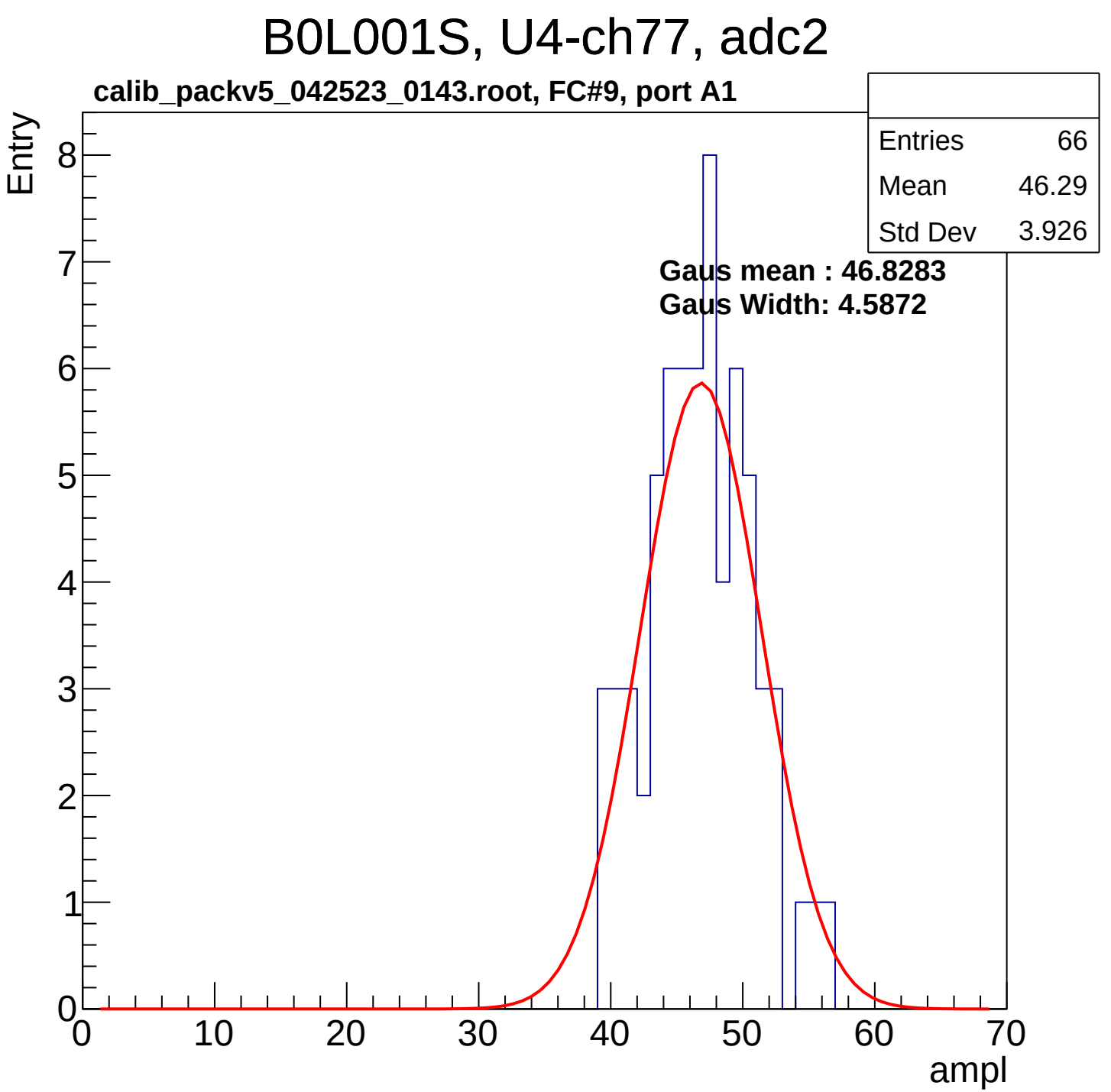
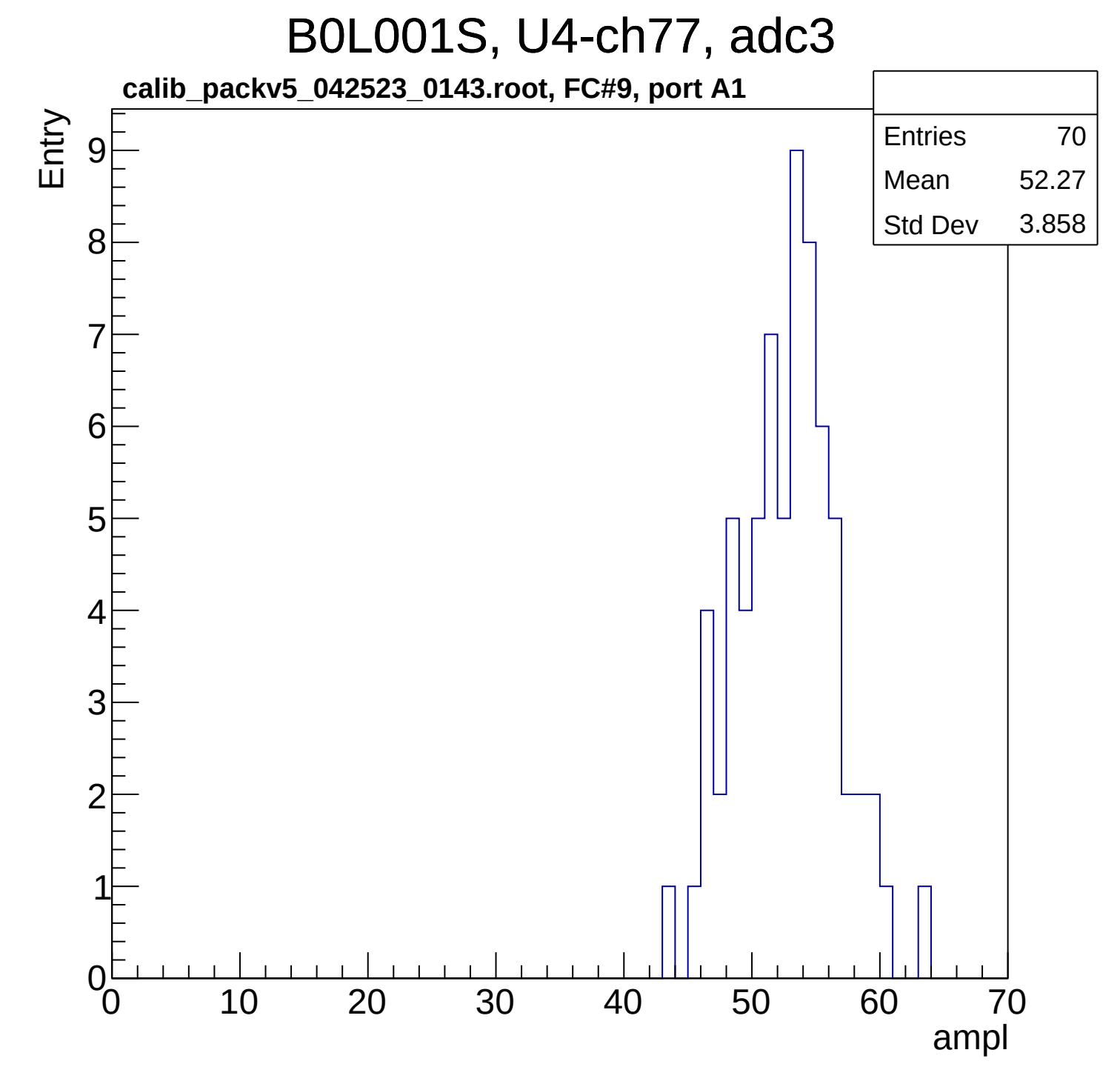

B0L001S, U4-ch76, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

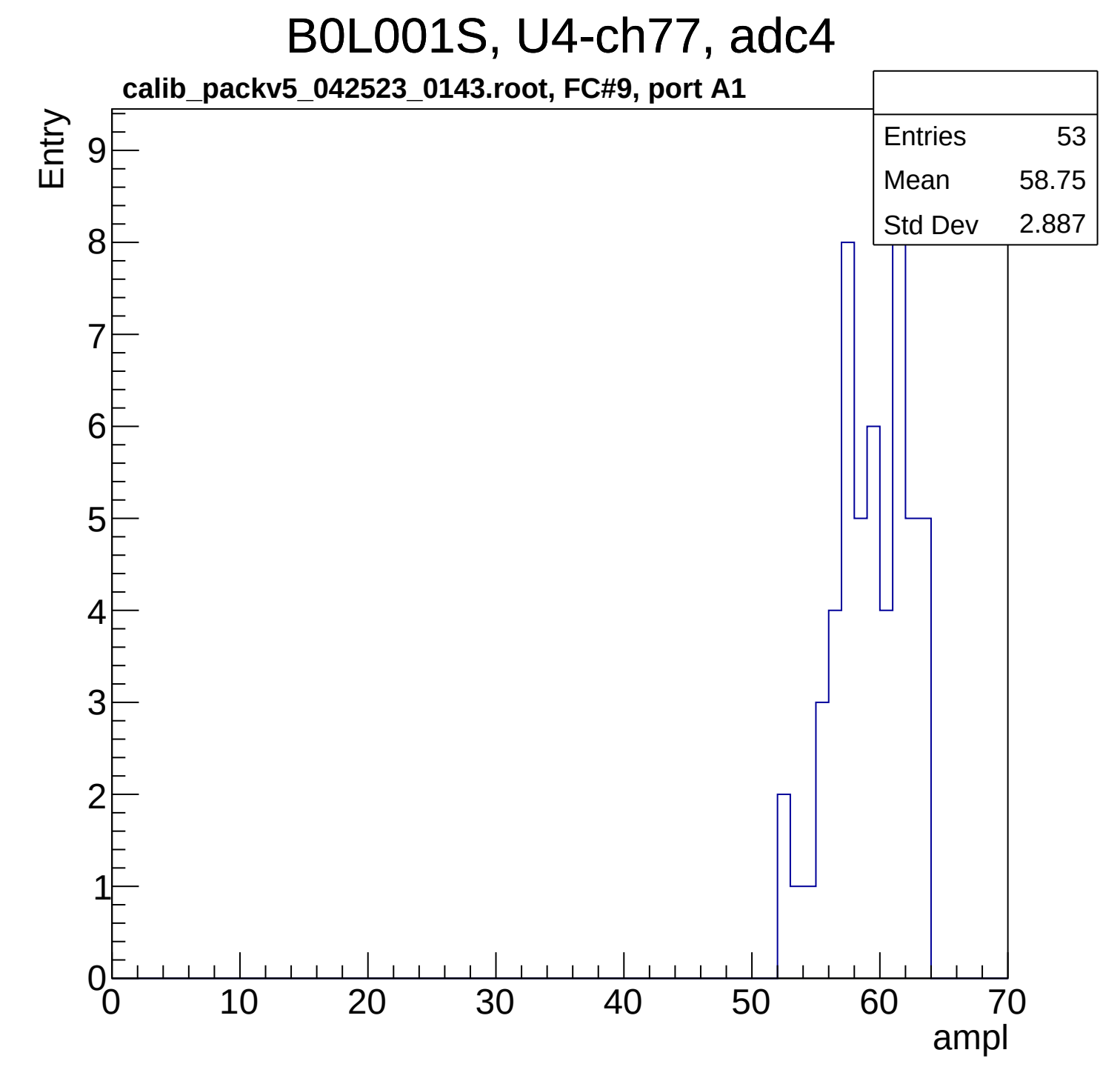

B0L001S, U4-ch76, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

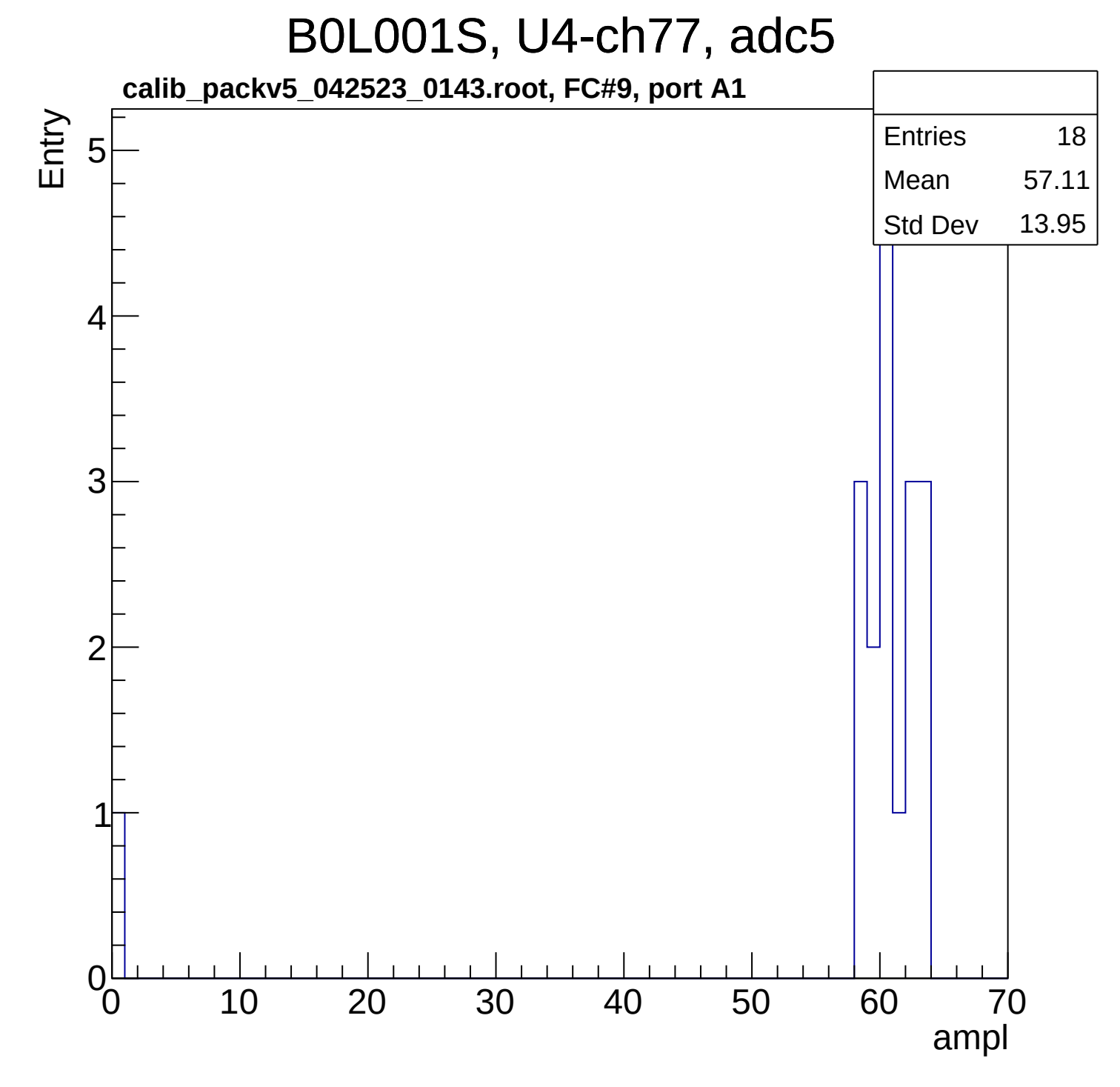

B0L001S, U4-ch77, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 32.43
Std Dev 5.473
Gaus mean : 32.5432
Gaus Width: 4.1706
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

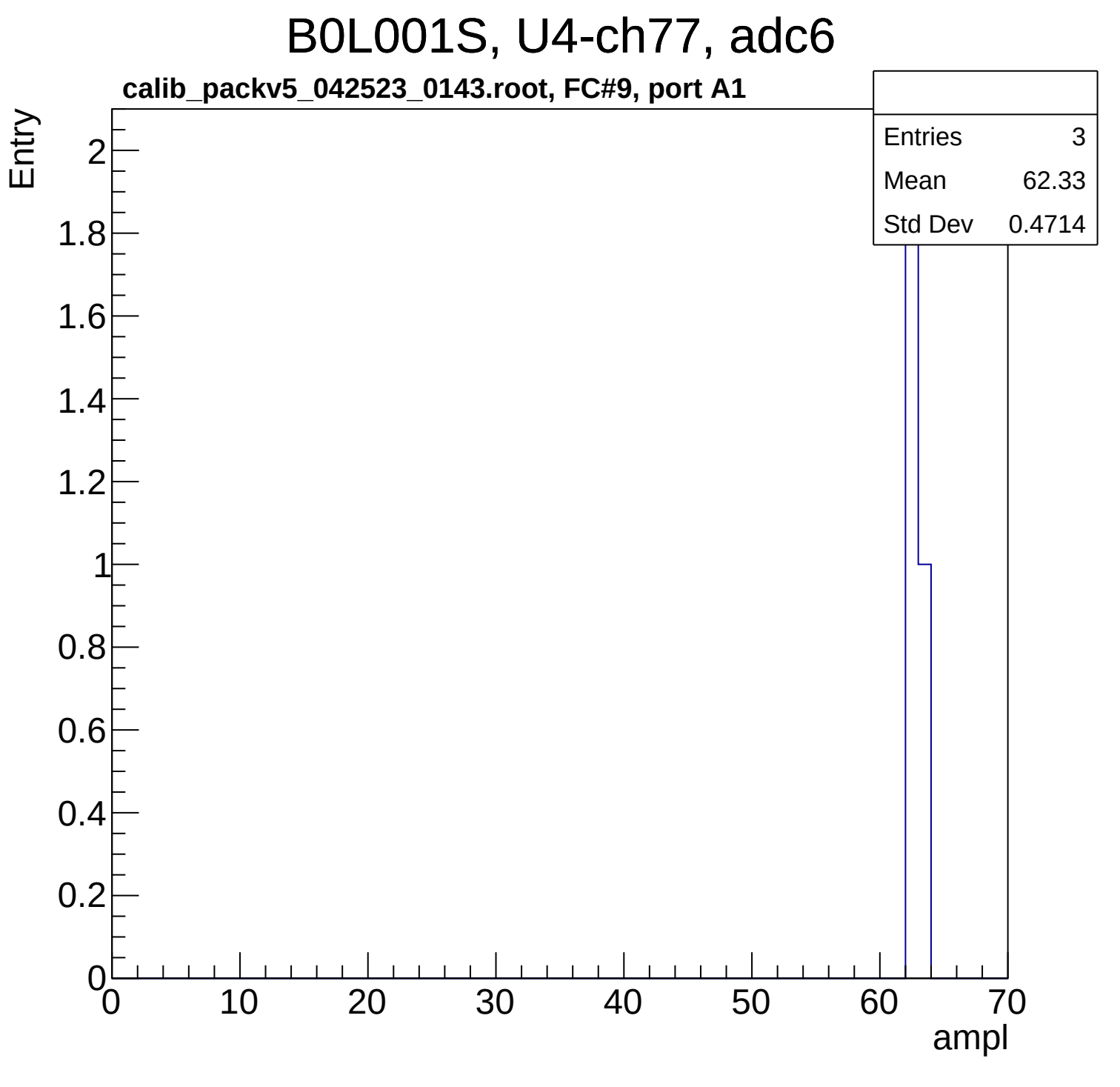

B0L001S, U4-ch77, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 38.89
Std Dev 3.686
Gaus mean : 39.7145
Gaus Width: 4.5828
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

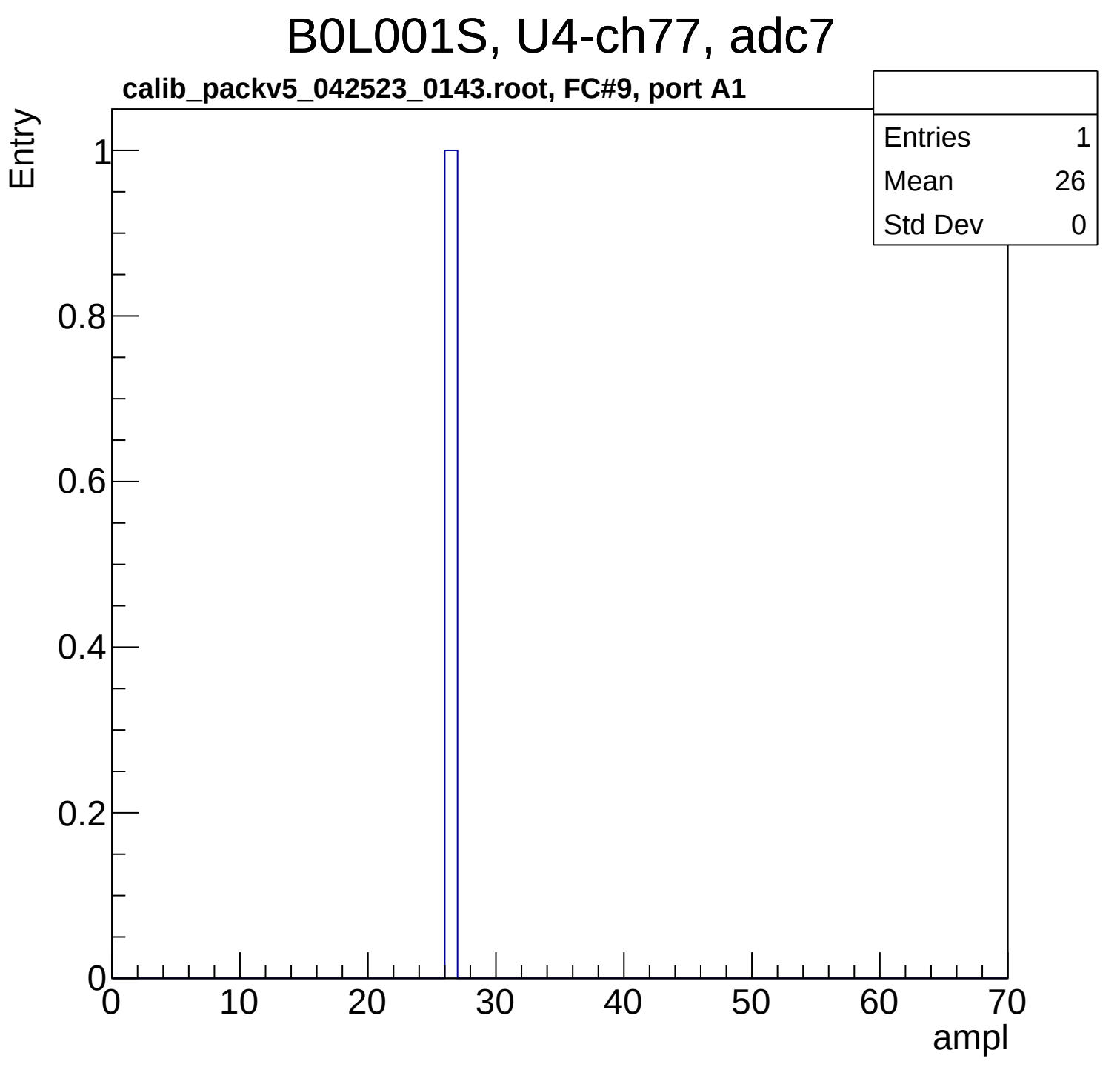

B0L001S, U4-ch77, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 46.29
Std Dev 3.926
Gaus mean : 46.8283
Gaus Width: 4.5872
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

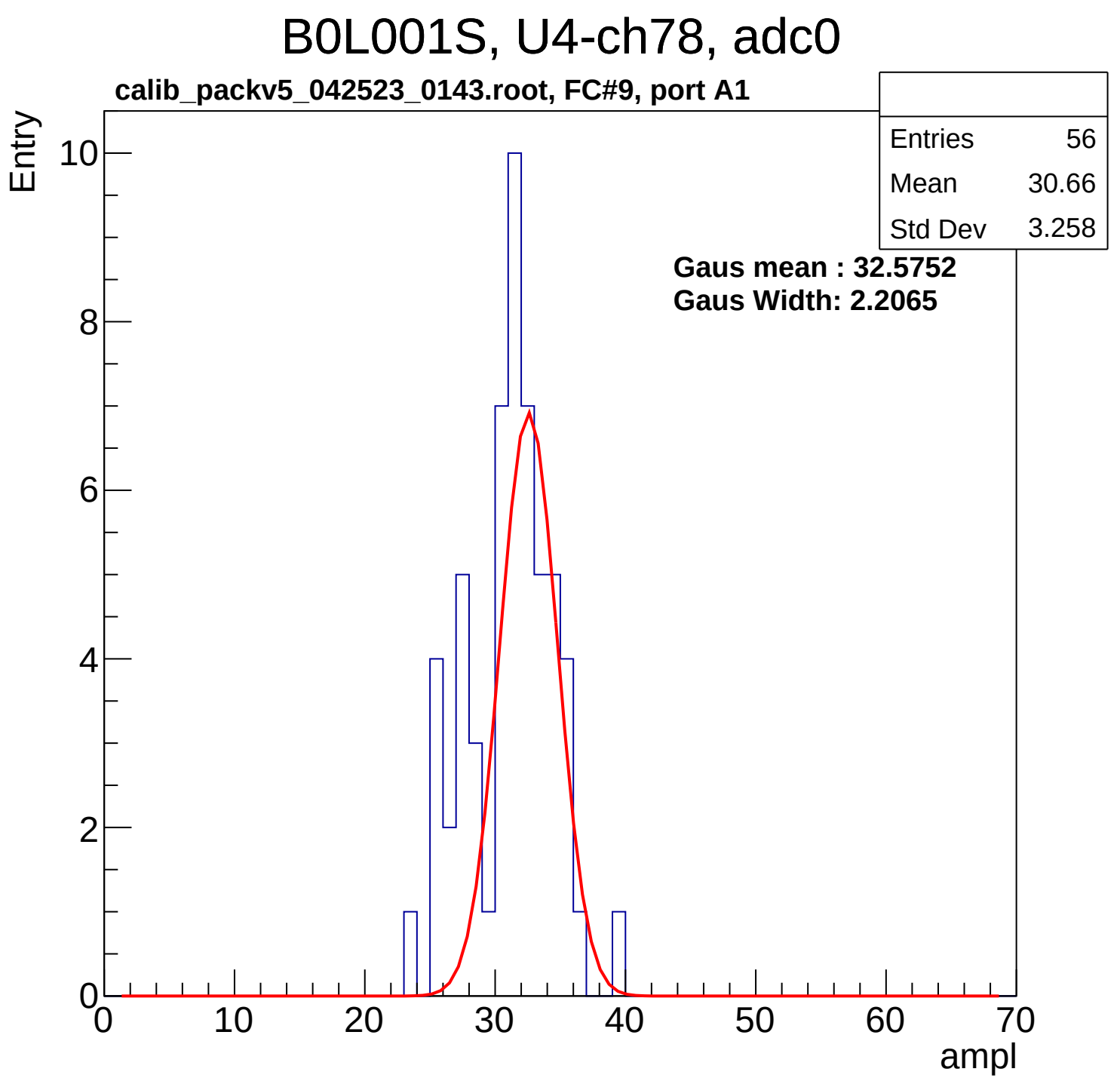

B0L001S, U4-ch77, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 52.27
Std Dev 3.858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

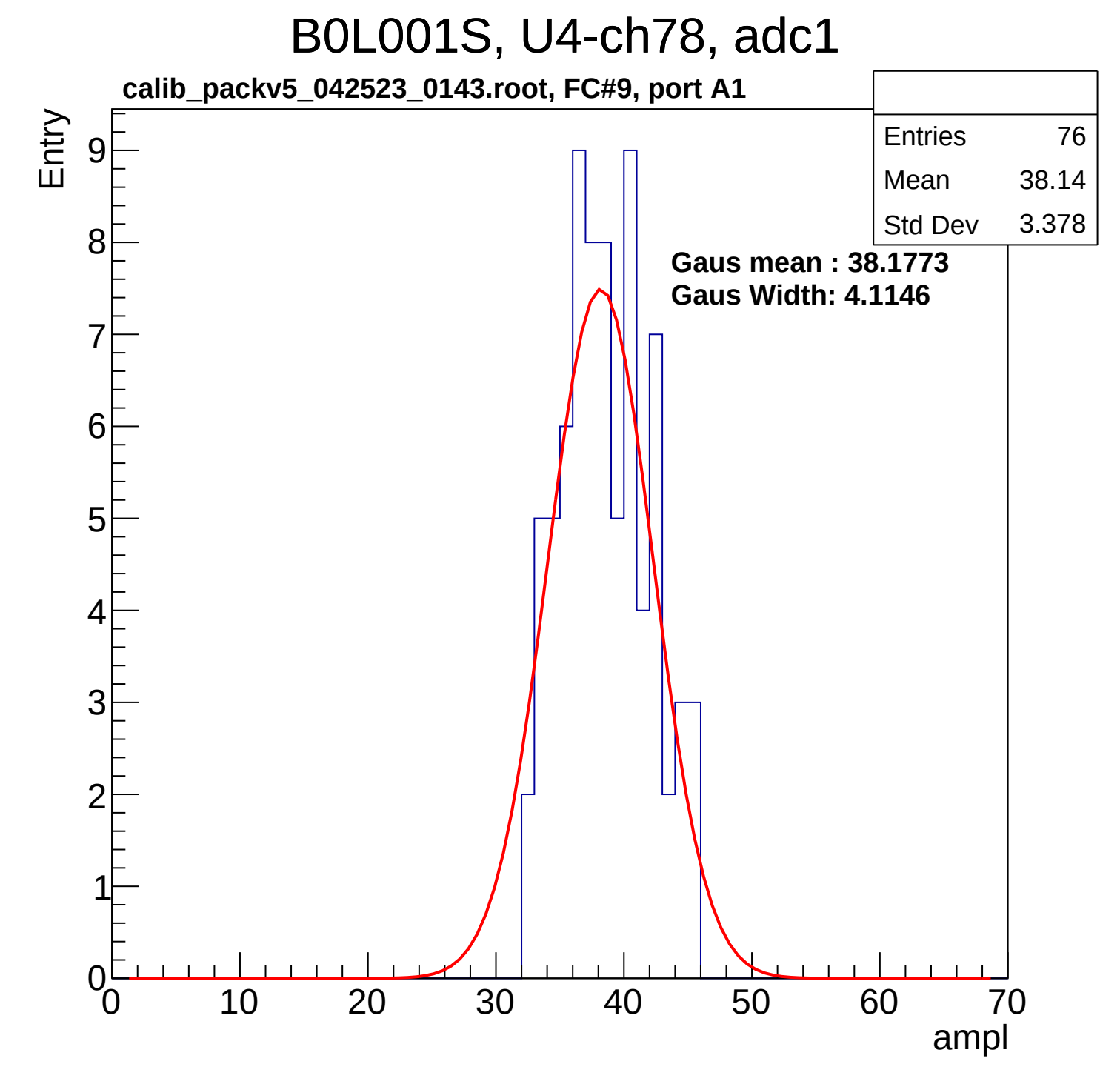

B0L001S, U4-ch77, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.75
Std Dev 2.887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

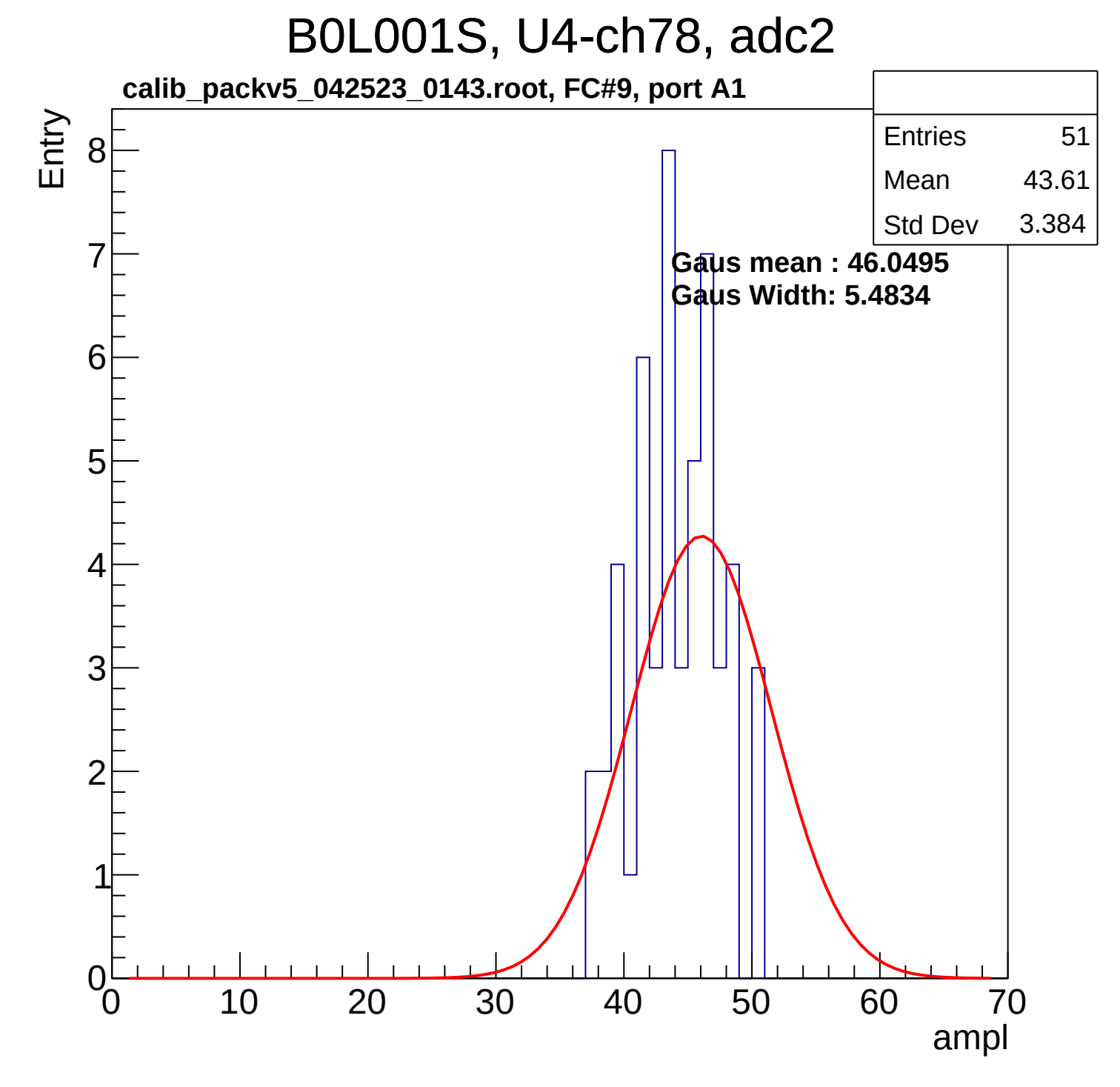

B0L001S, U4-ch77, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 57.11
Std Dev 13.95
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

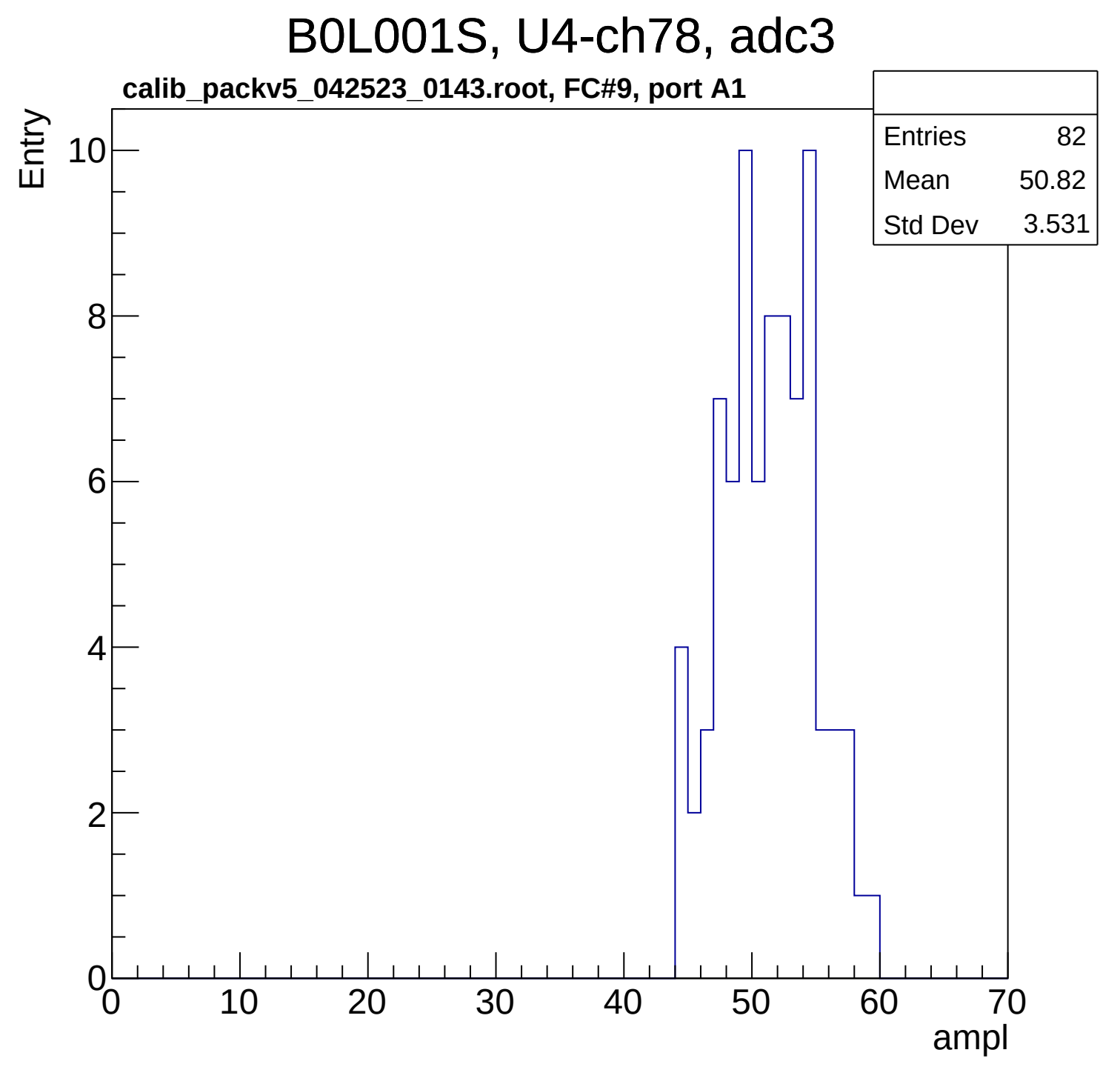

B0L001S, U4-ch77, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

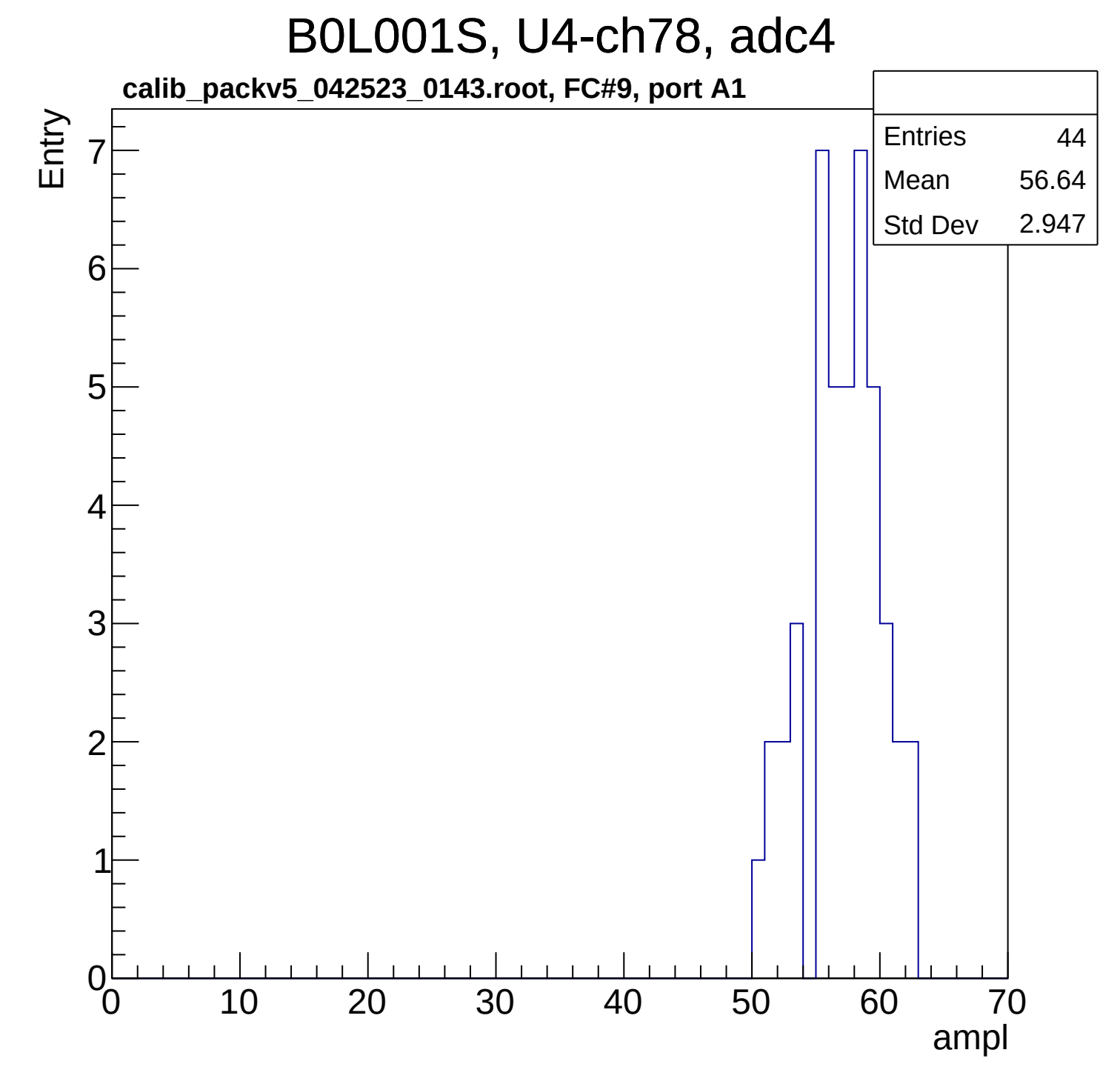

B0L001S, U4-ch77, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 26
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

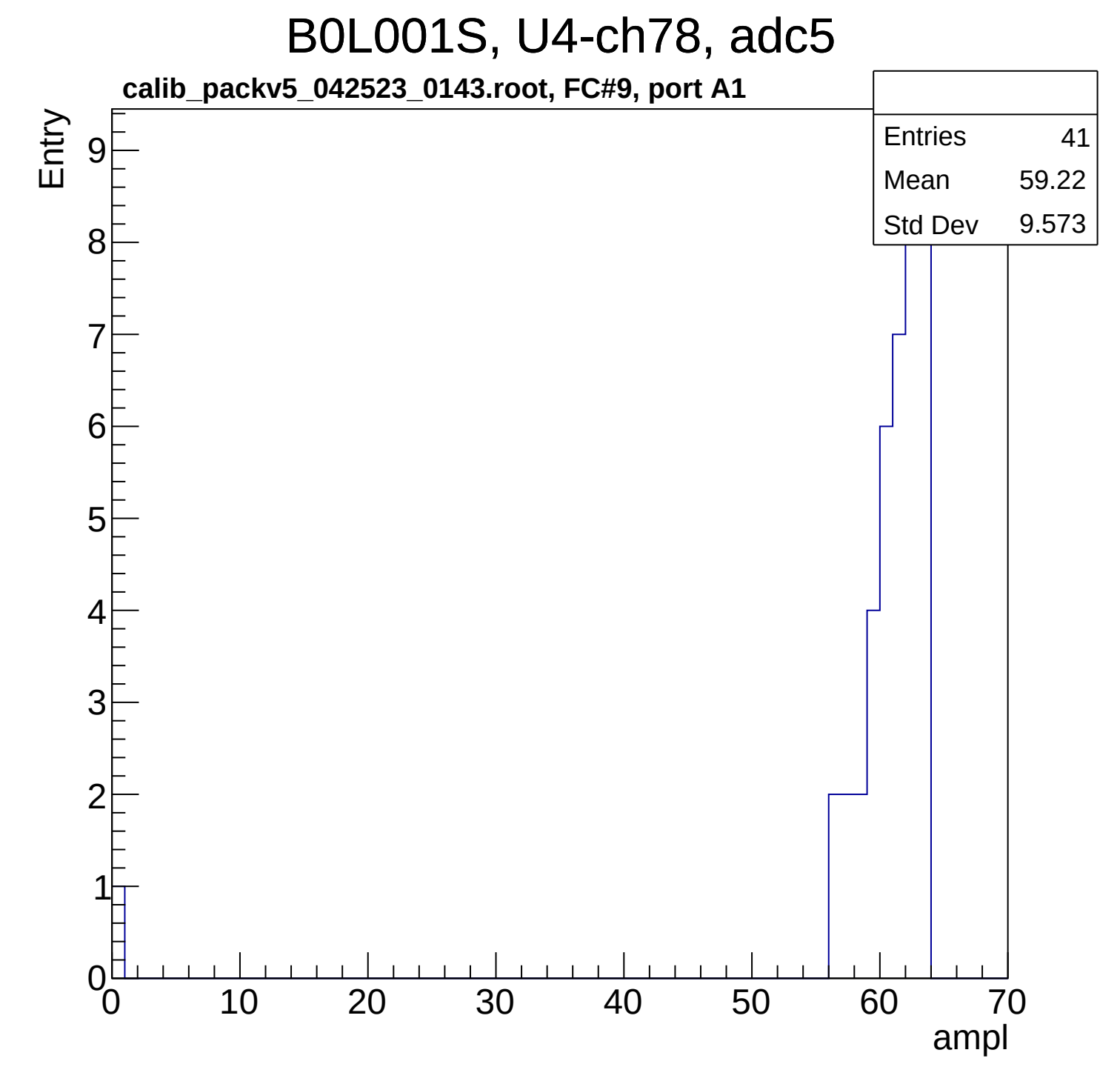

B0L001S, U4-ch78, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.66
Std Dev 3.258
Gaus mean : 32.5752
Gaus Width: 2.2065
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch78, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 38.14
Std Dev 3.378
Gaus mean : 38.1773
Gaus Width: 4.1146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch78, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 43.61
Std Dev 3.384
Gaus mean : 46.0495
Gaus Width: 5.4834
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch78, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 50.82
Std Dev 3.531
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch78, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 56.64
Std Dev 2.947
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch78, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59.22
Std Dev 9.573
Entry
ampl
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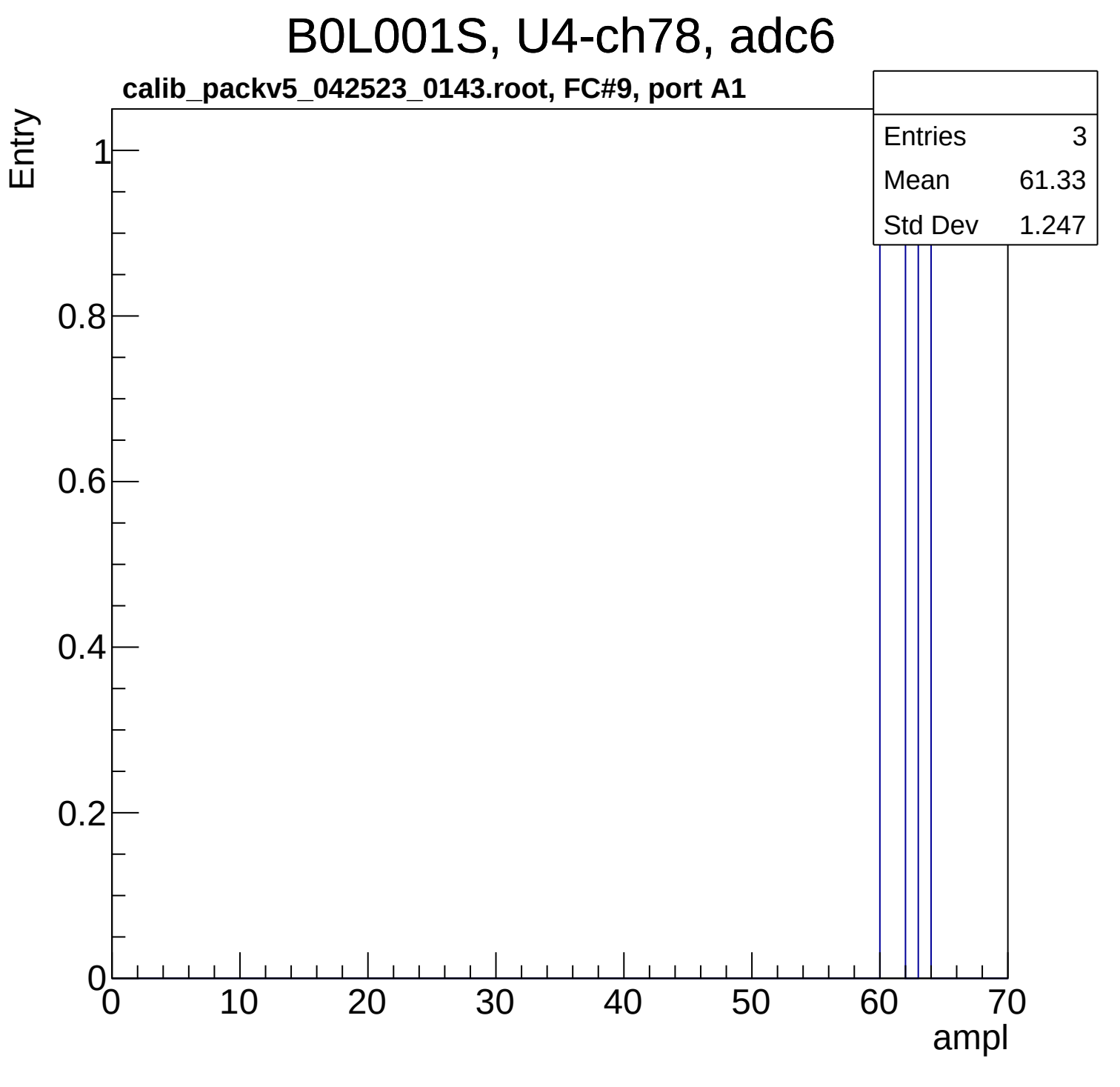
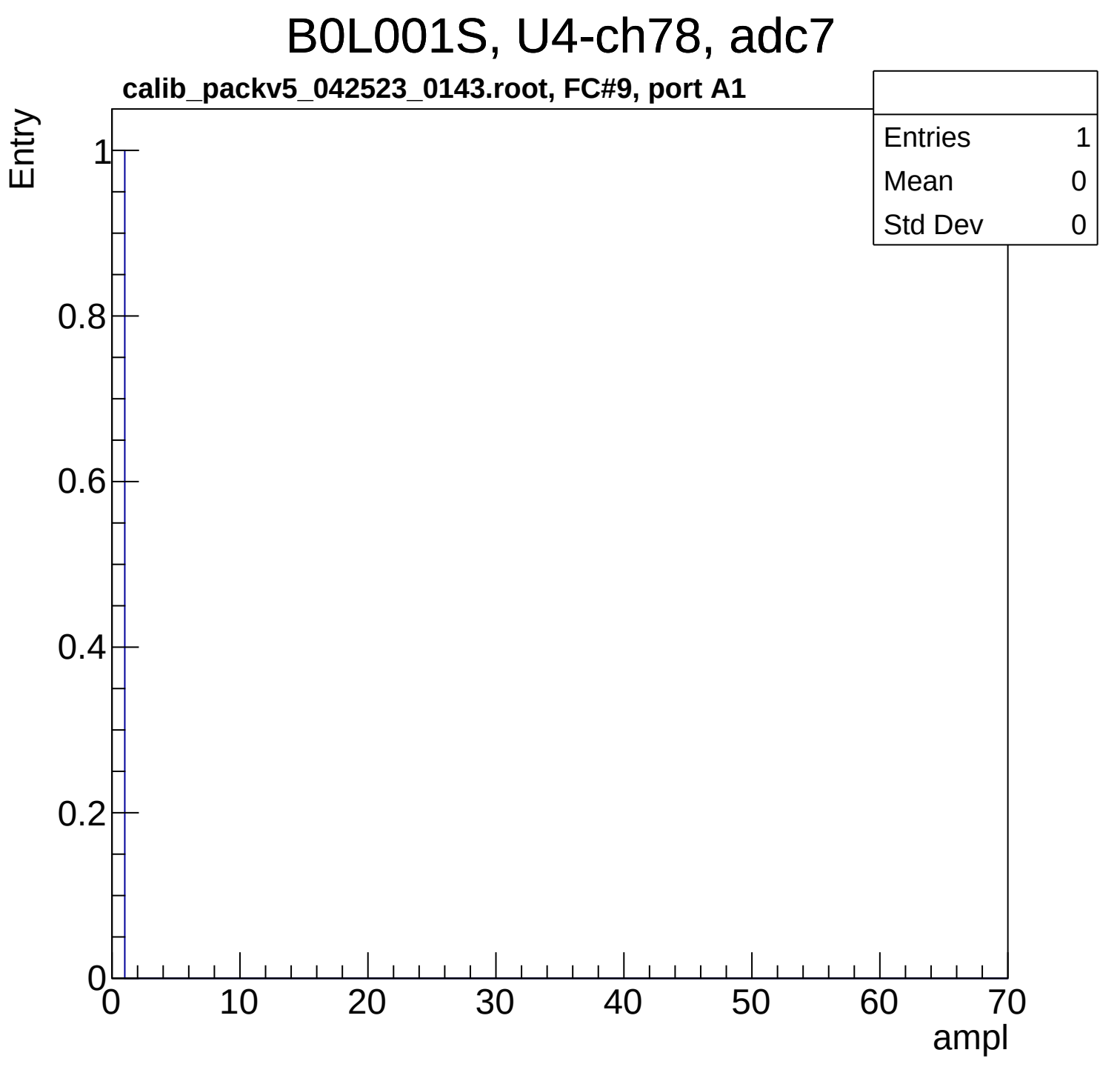
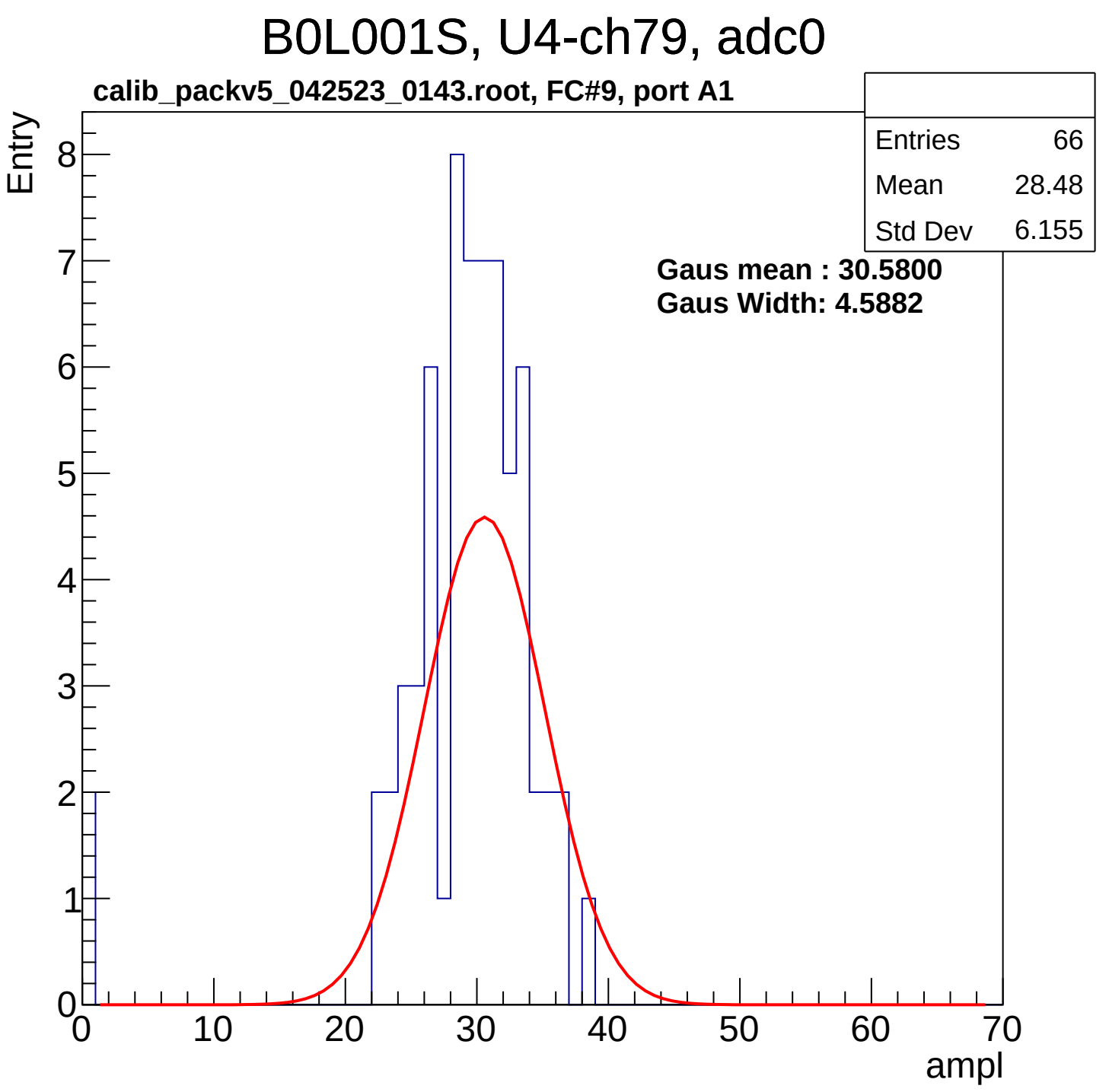
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch78, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 1.247
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch78, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch79, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.48
Std Dev 6.155
Gaus mean : 30.5800
Gaus Width: 4.5882
Entry
ampl
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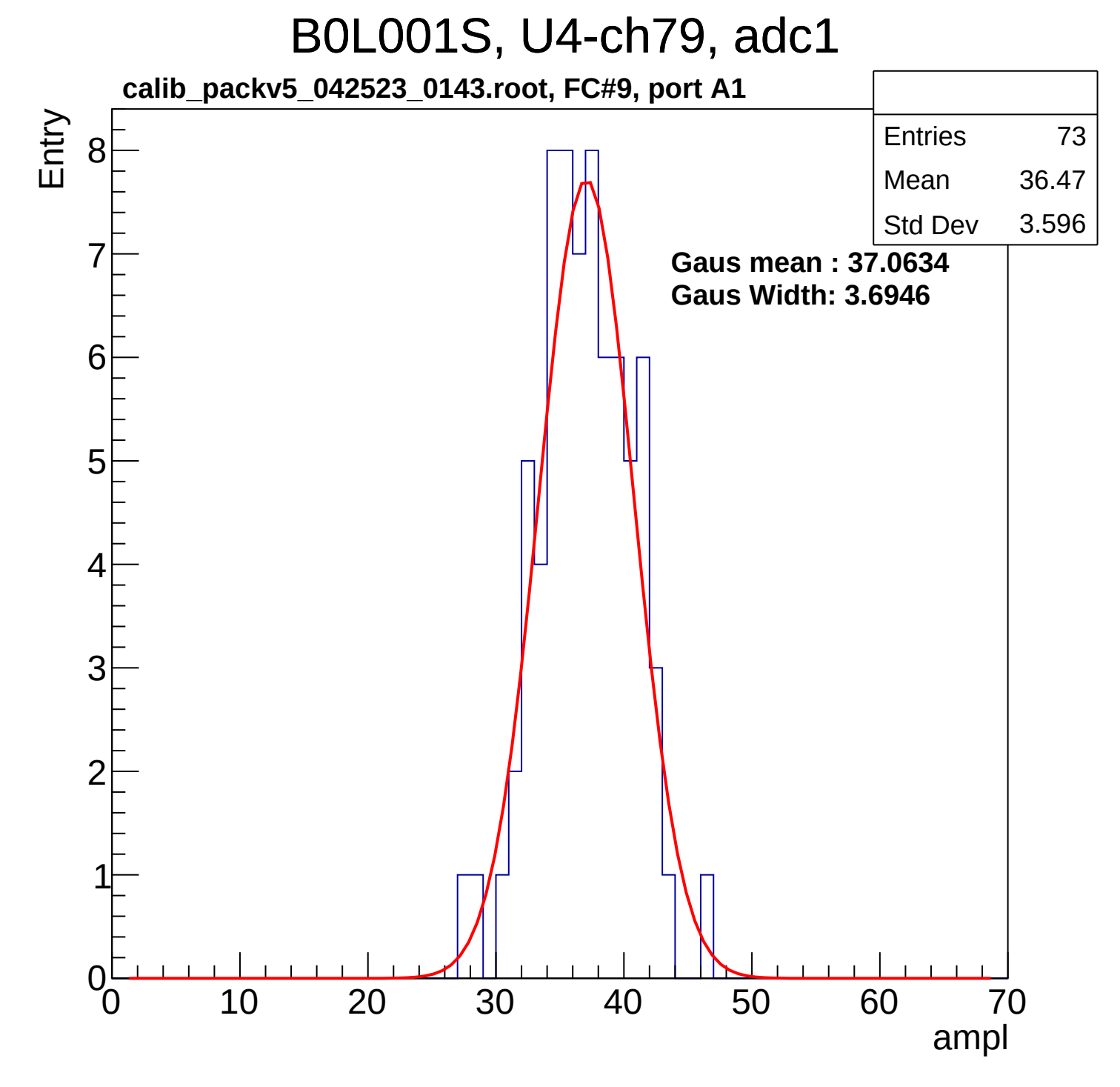
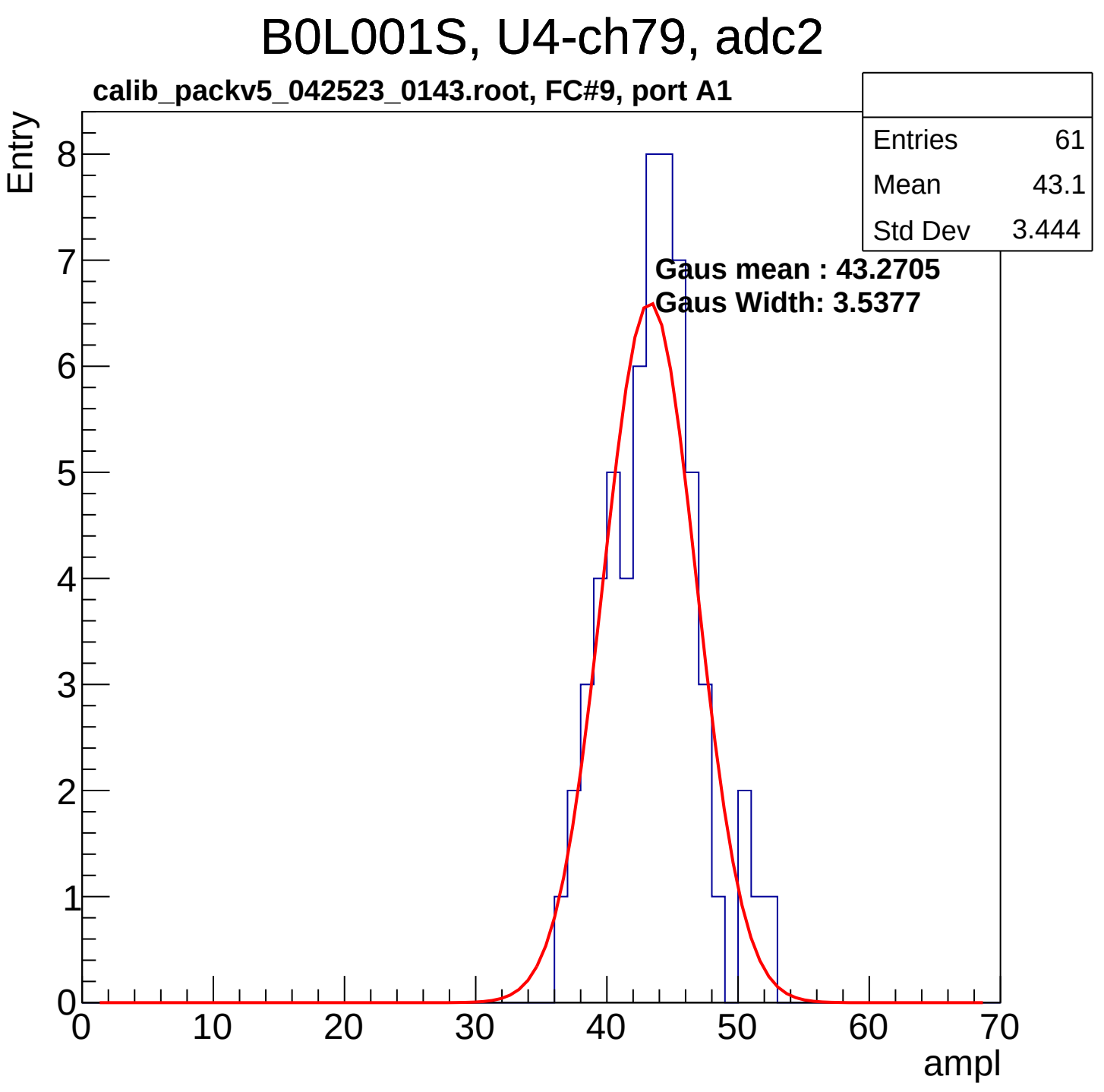
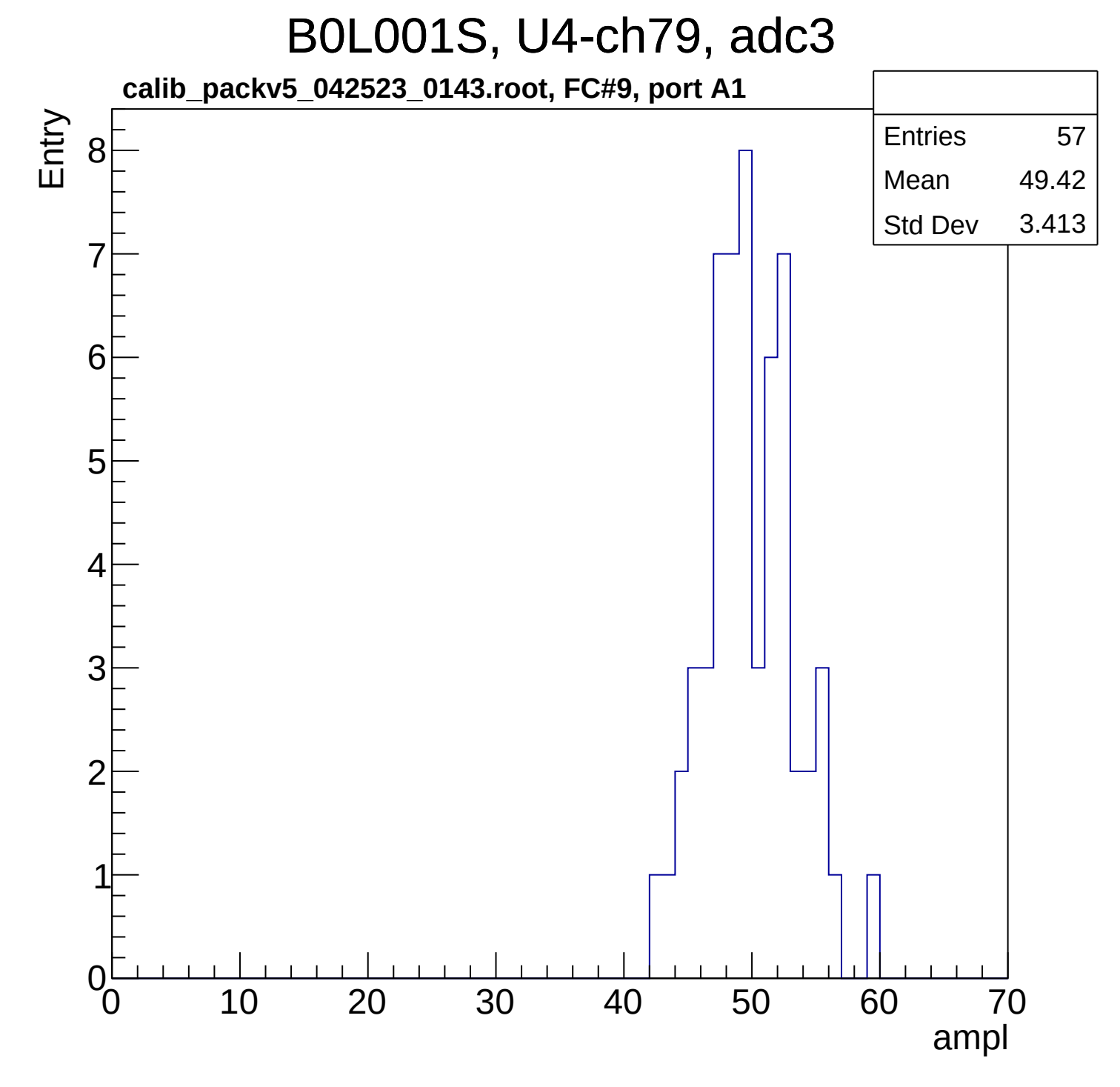
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch79, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 36.47
Std Dev 3.596
Gaus mean : 37.0634
Gaus Width: 3.6946
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch79, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.1
Std Dev 3.444
Gaus mean : 43.2705
Gaus Width: 3.5377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch79, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 49.42
Std Dev 3.413
Entry
ampl
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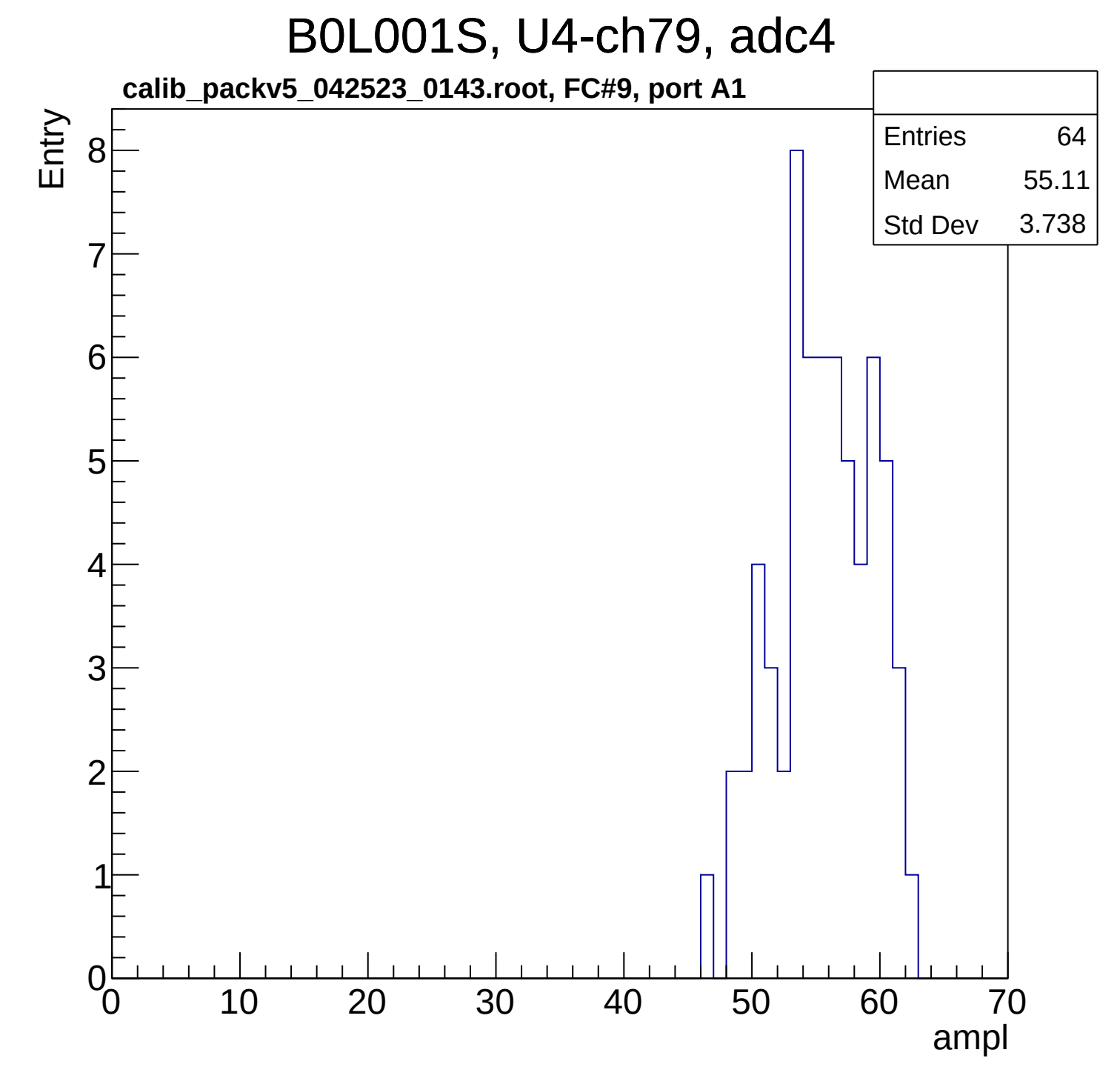
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch79, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55.11
Std Dev 3.738
Entry
ampl
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1
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3
4
5
6
7
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0
10
20
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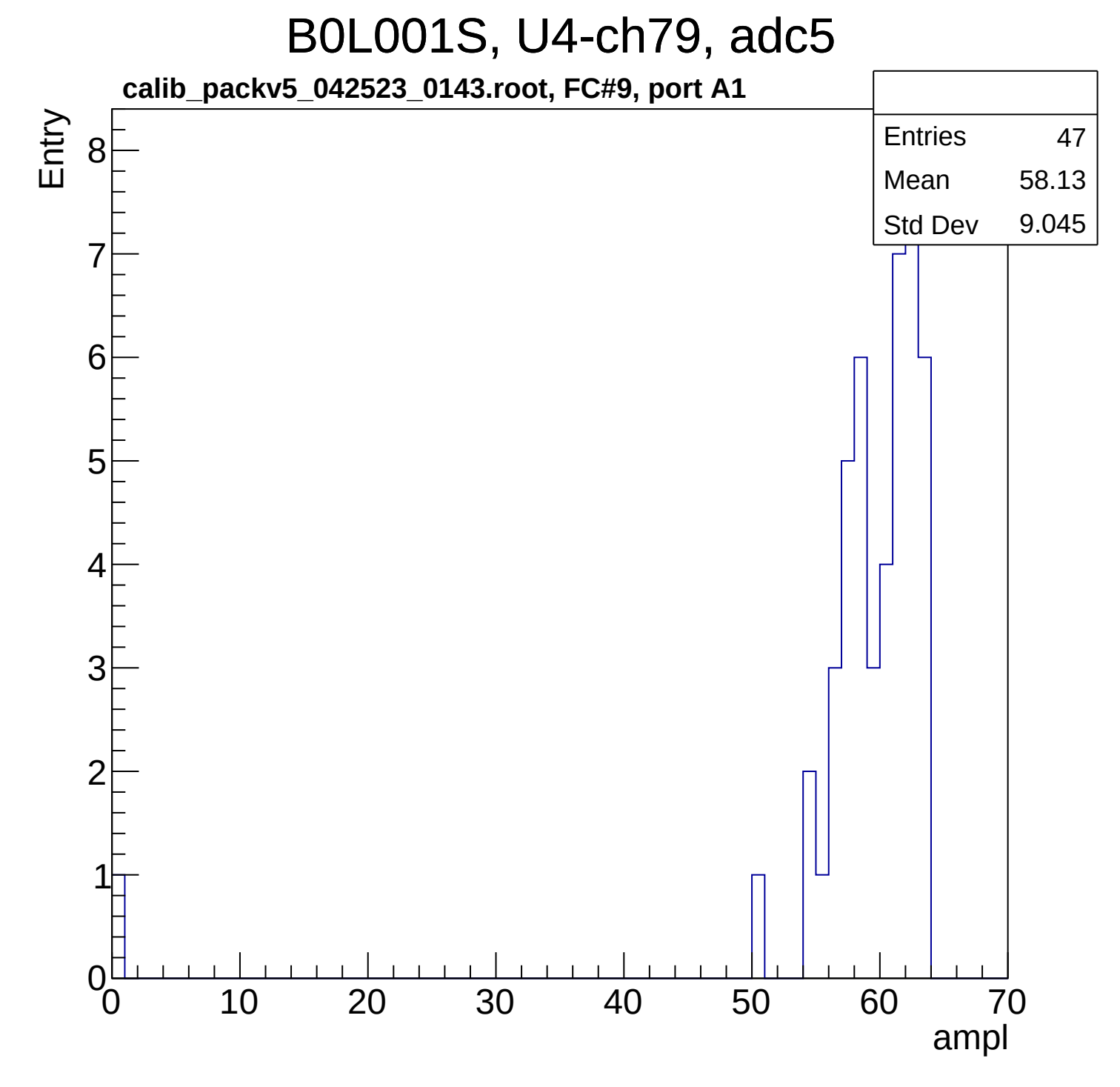
40
50
60
70


B0L001S, U4-ch79, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.13
Std Dev 9.045
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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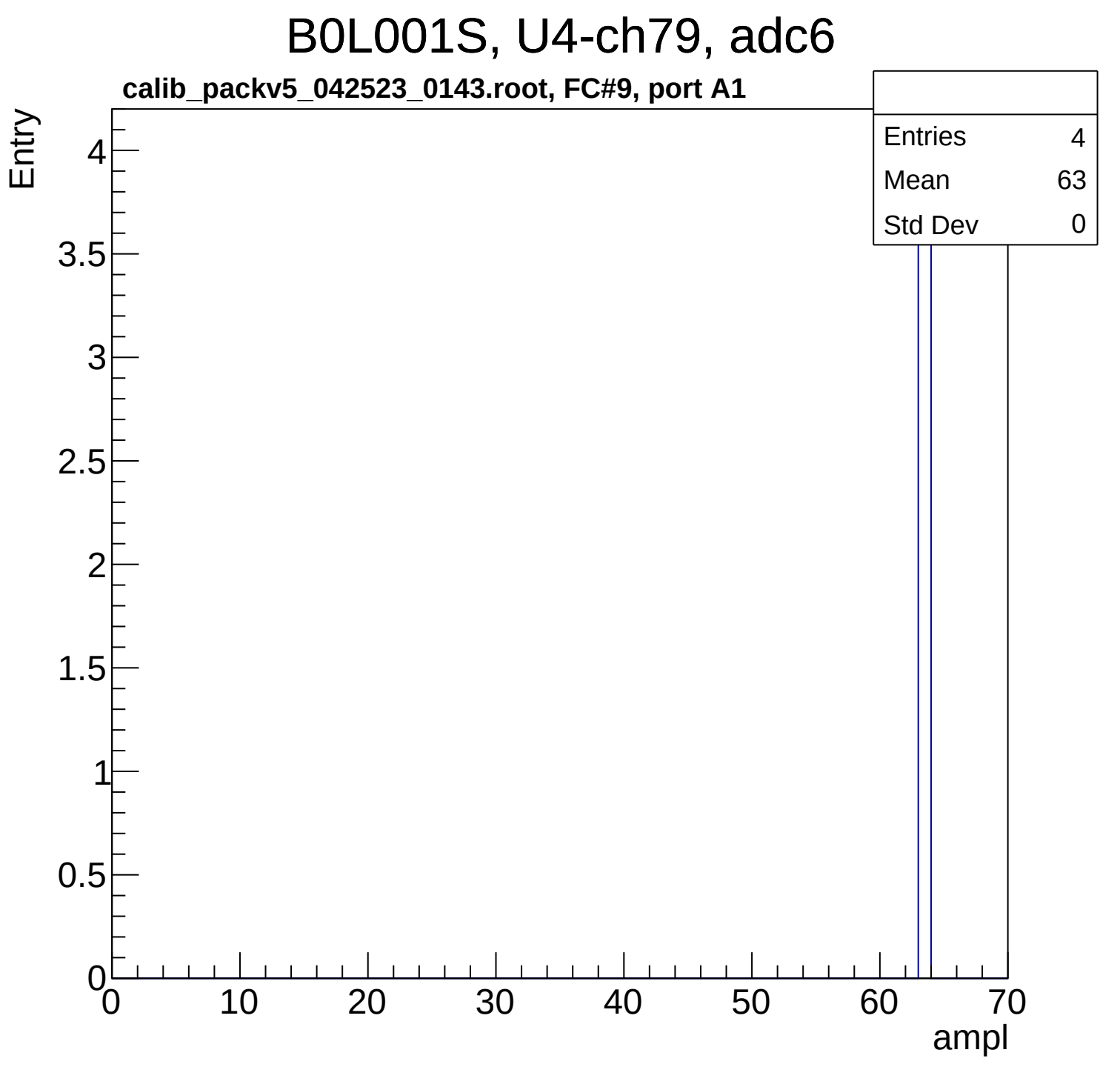
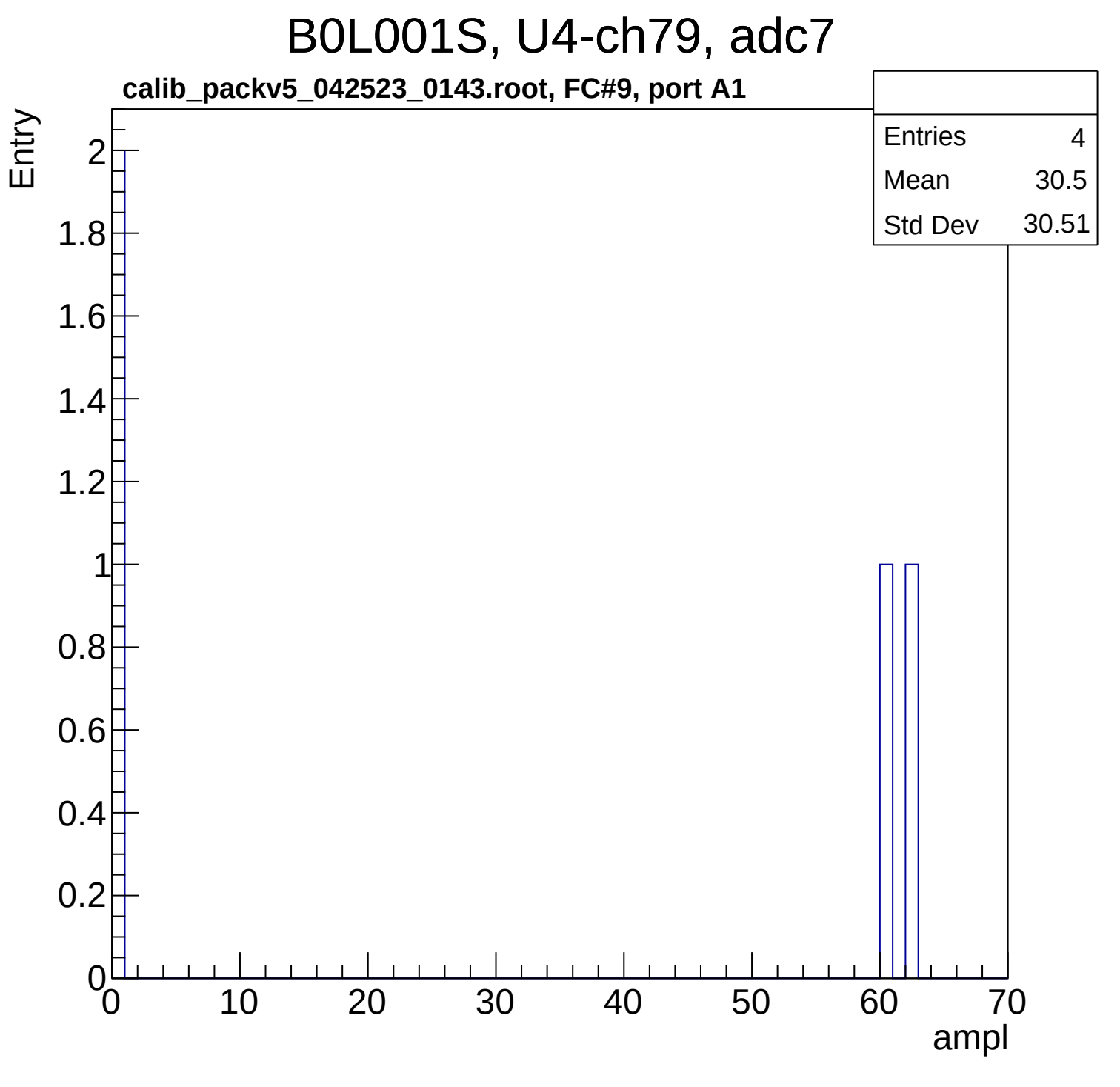
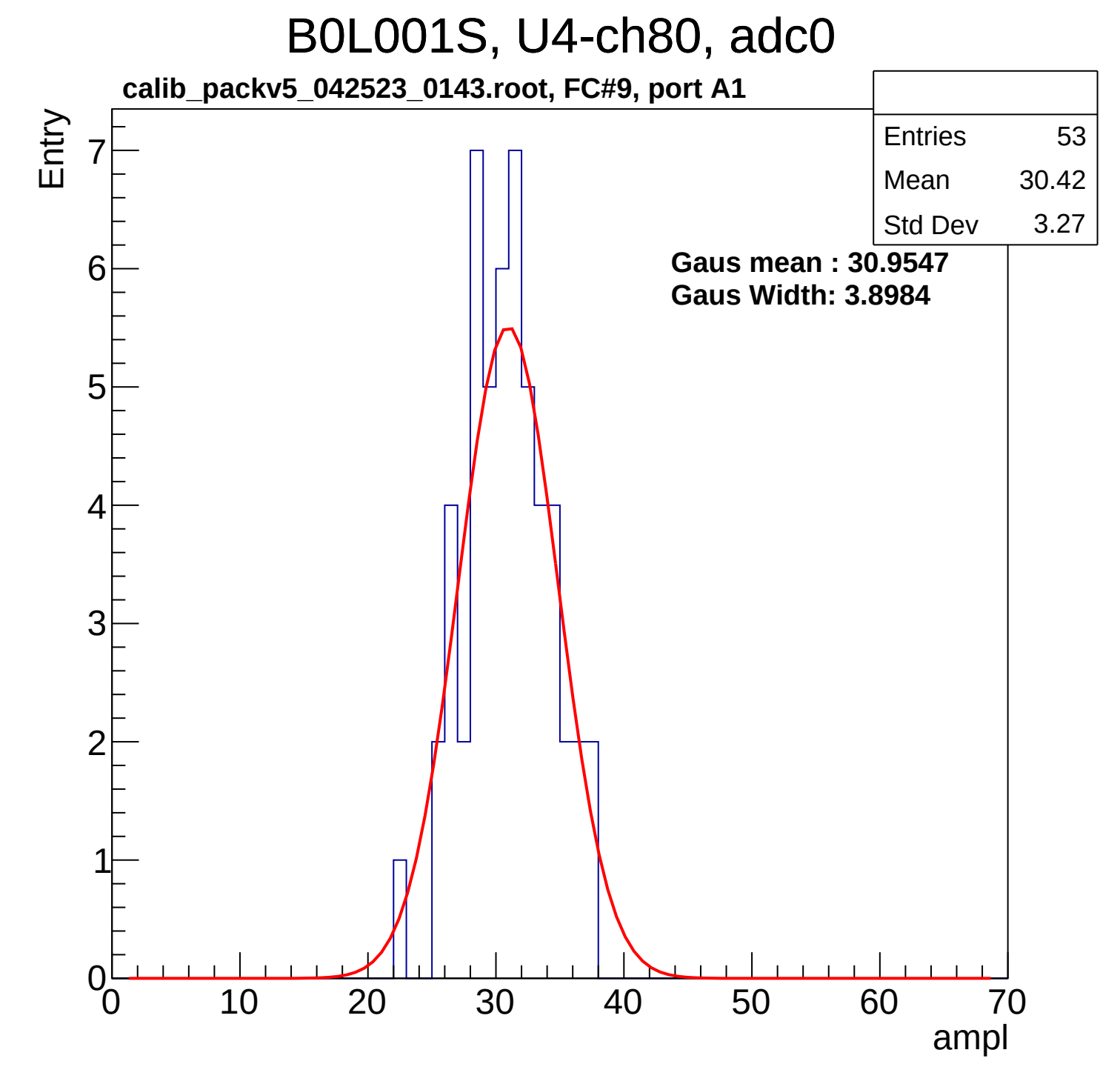
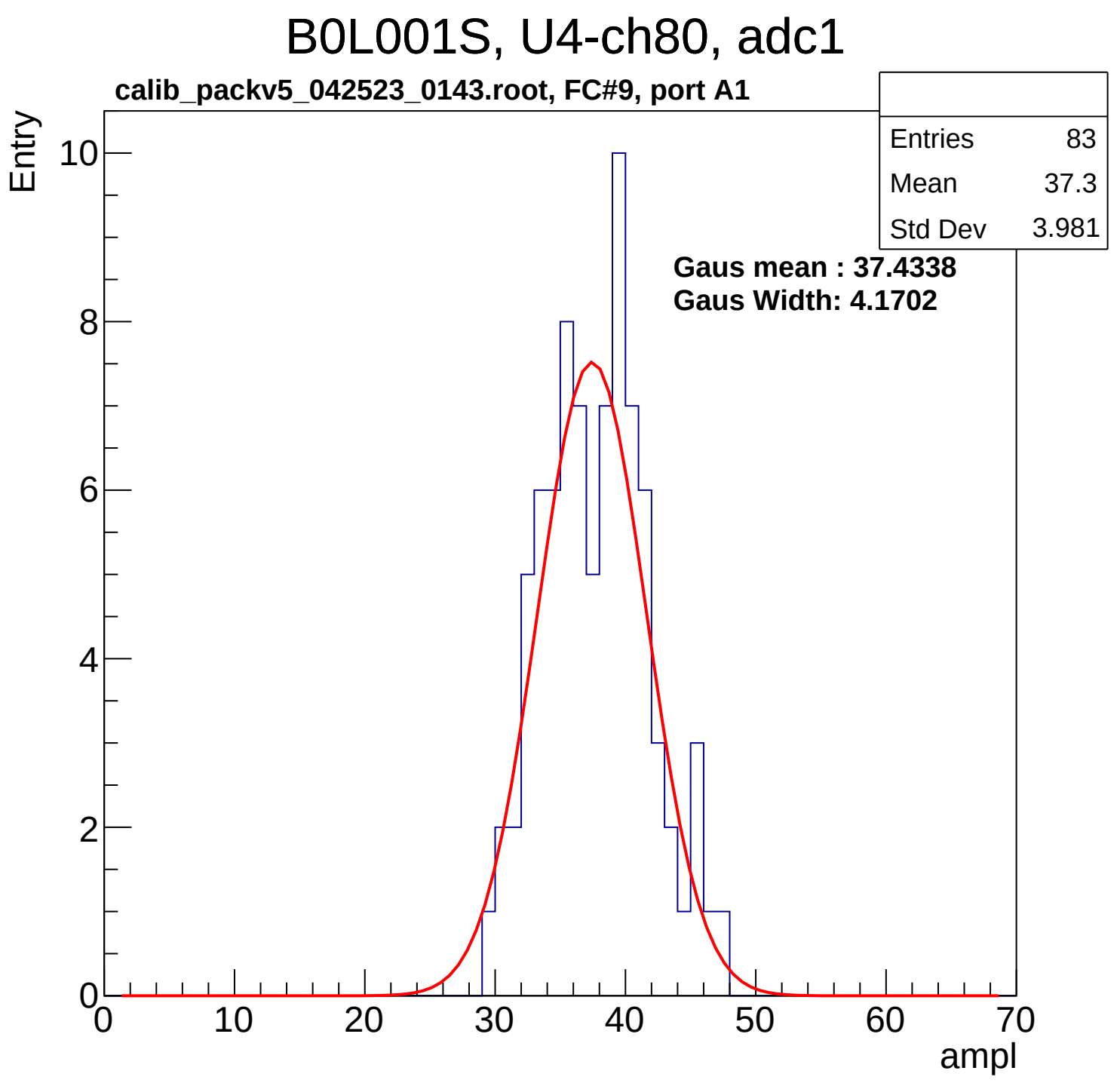
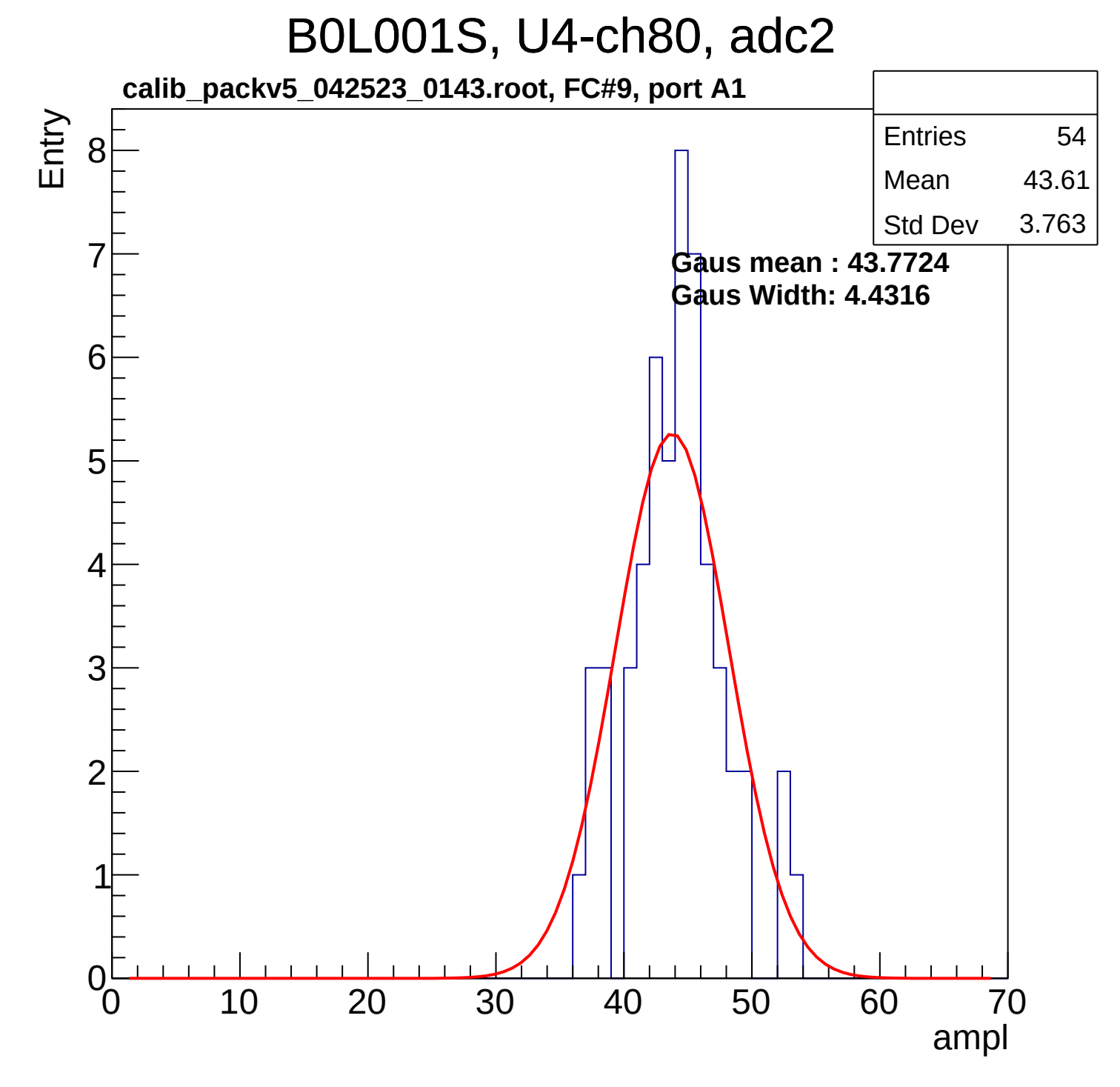

B0L001S, U4-ch79, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch79, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 30.5
Std Dev 30.51
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch80, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 30.42
Std Dev 3.27
Gaus mean : 30.9547
Gaus Width: 3.8984
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch80, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 37.3
Std Dev 3.981
Gaus mean : 37.4338
Gaus Width: 4.1702
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch80, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 43.61
Std Dev 3.763
Gaus mean : 43.7724
Gaus Width: 4.4316
Entry
ampl
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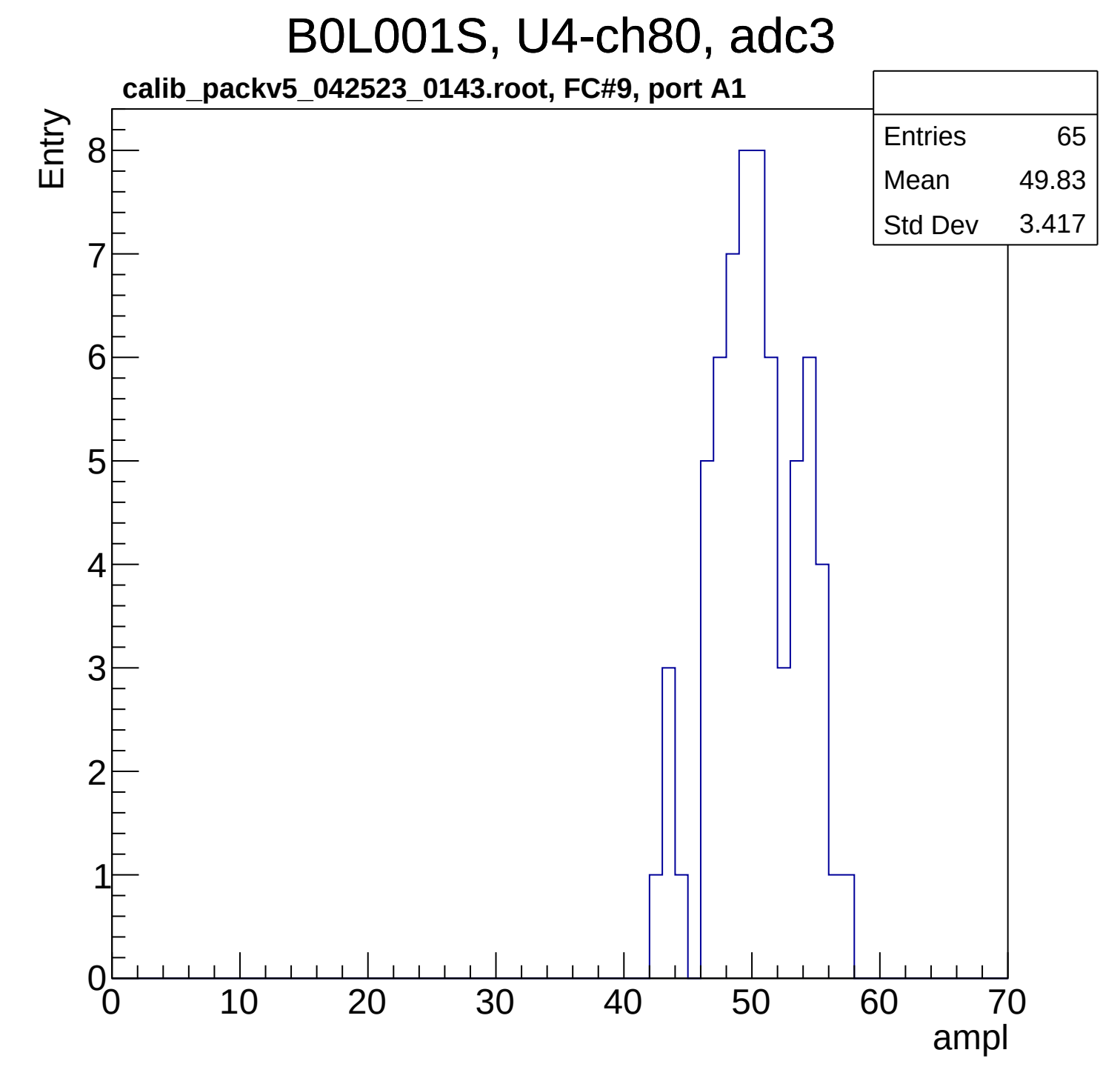
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch80, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.83
Std Dev 3.417
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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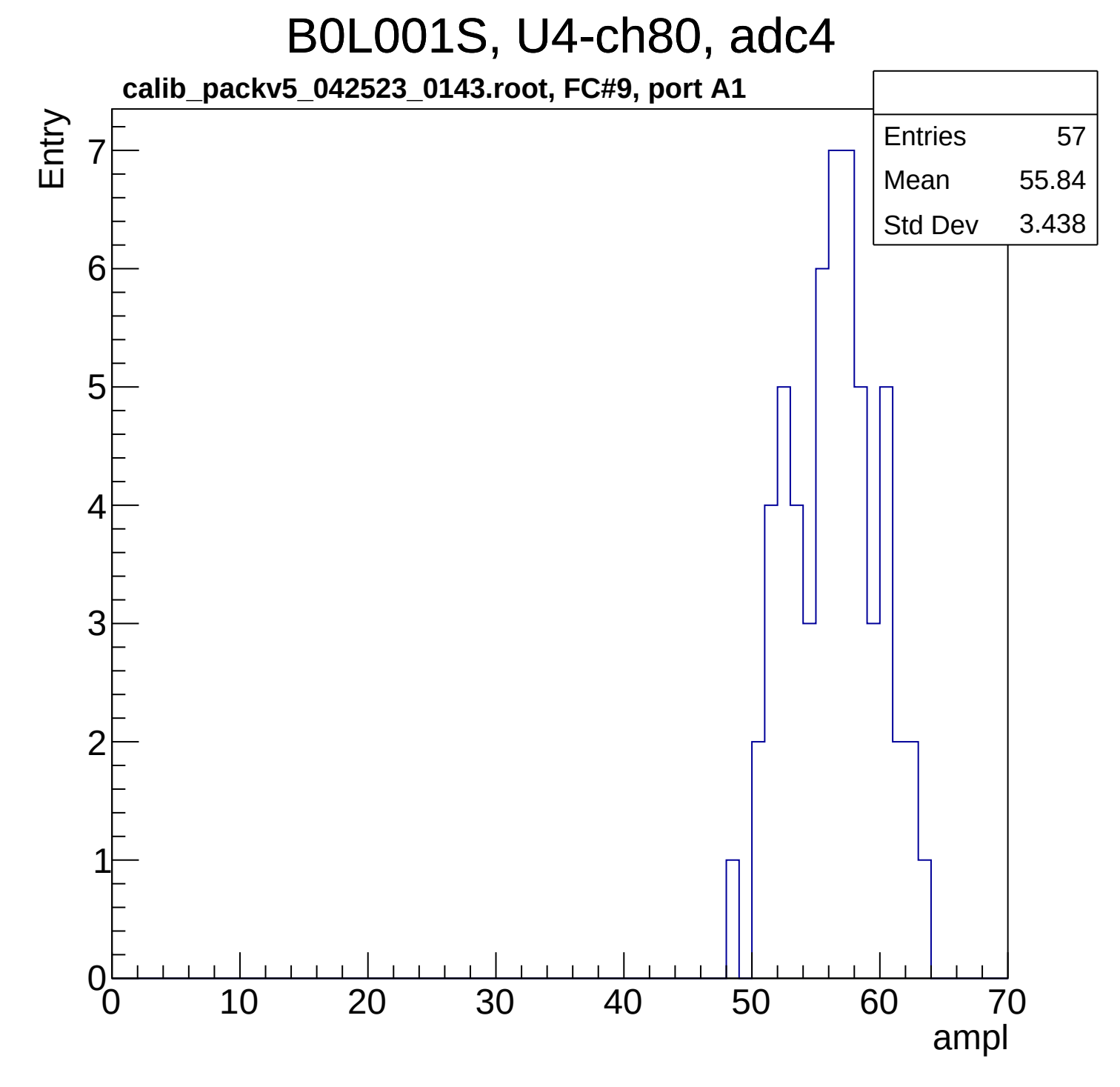
40
50
60
70


B0L001S, U4-ch80, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 55.84
Std Dev 3.438
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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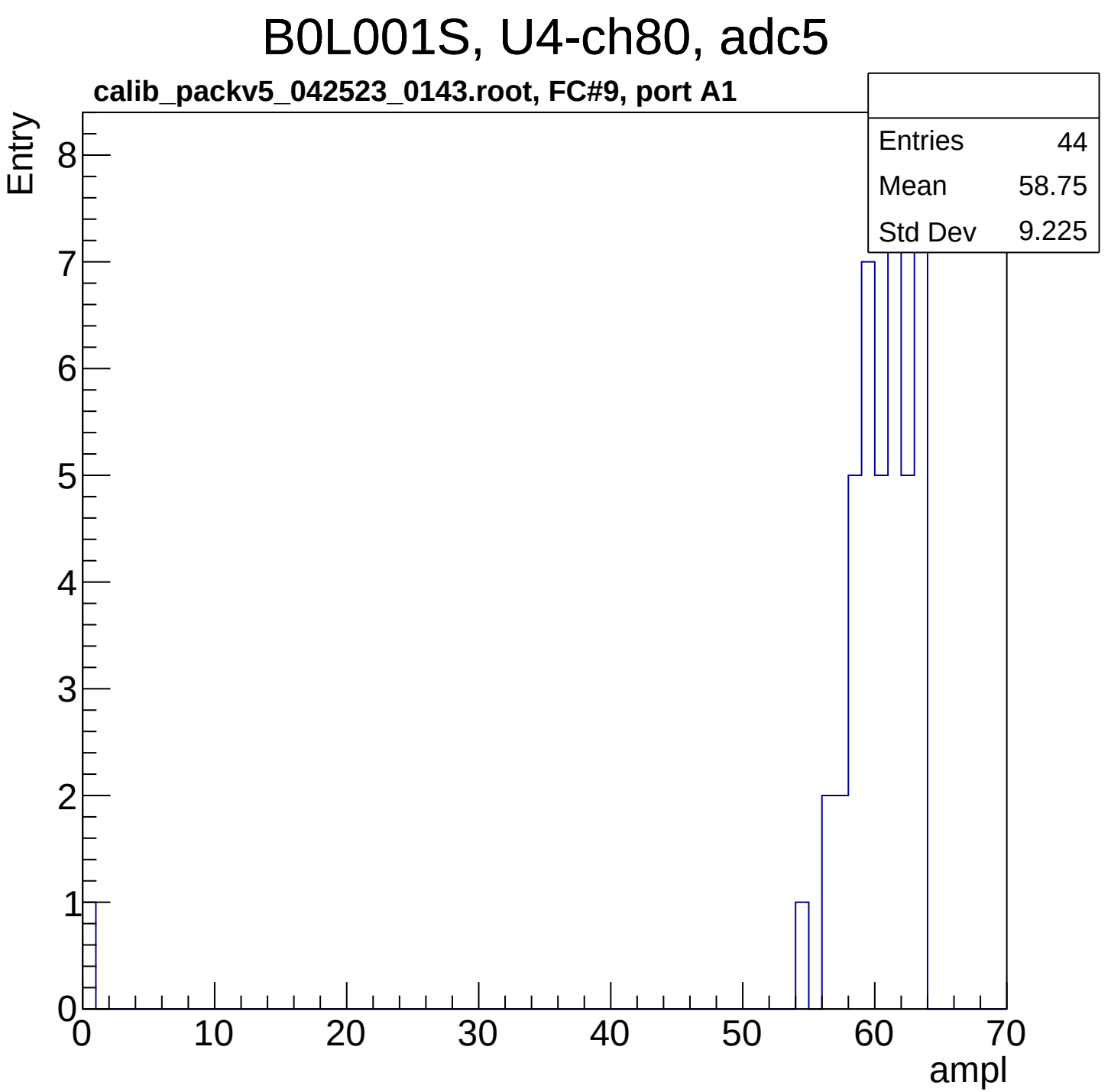
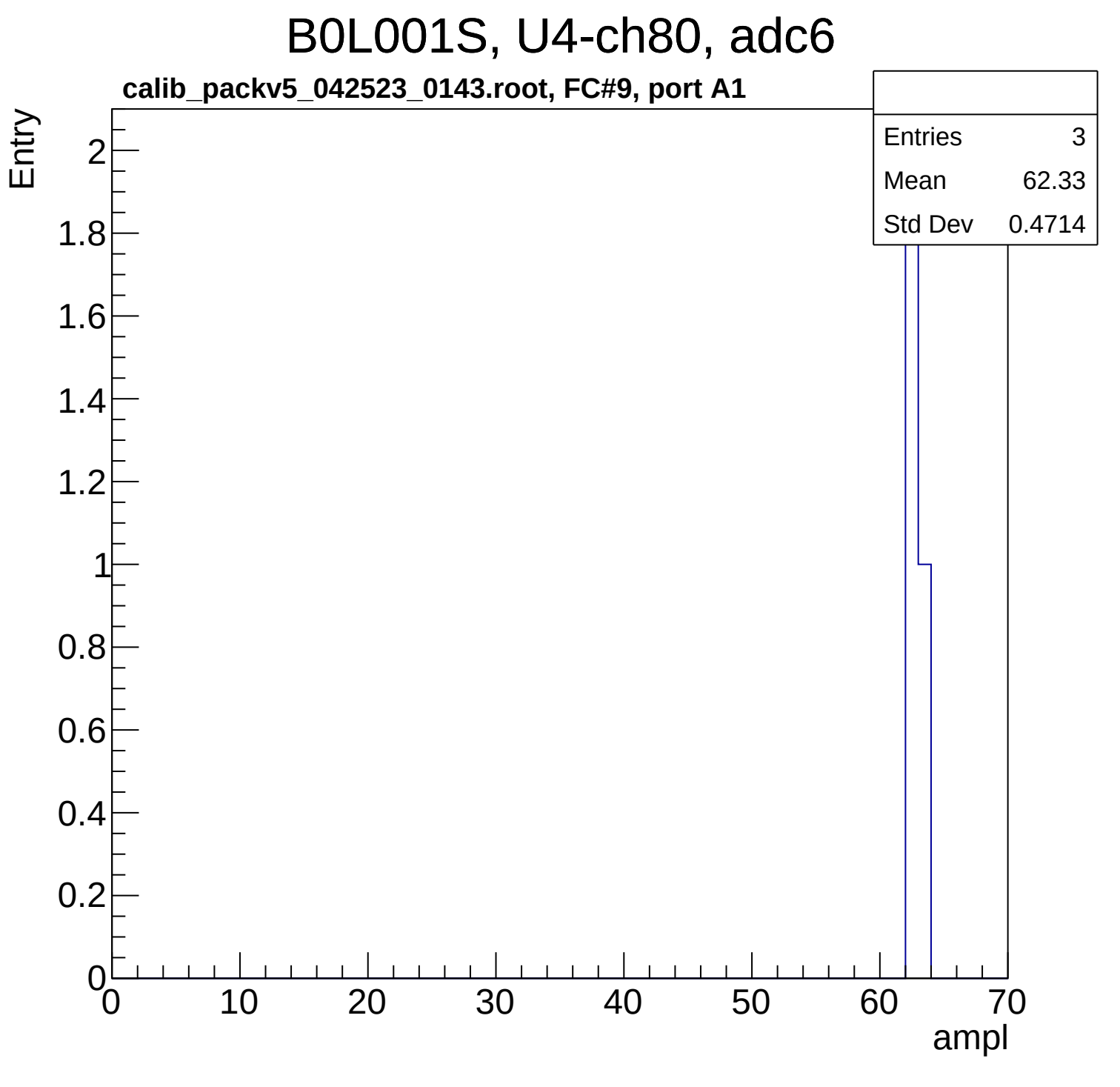
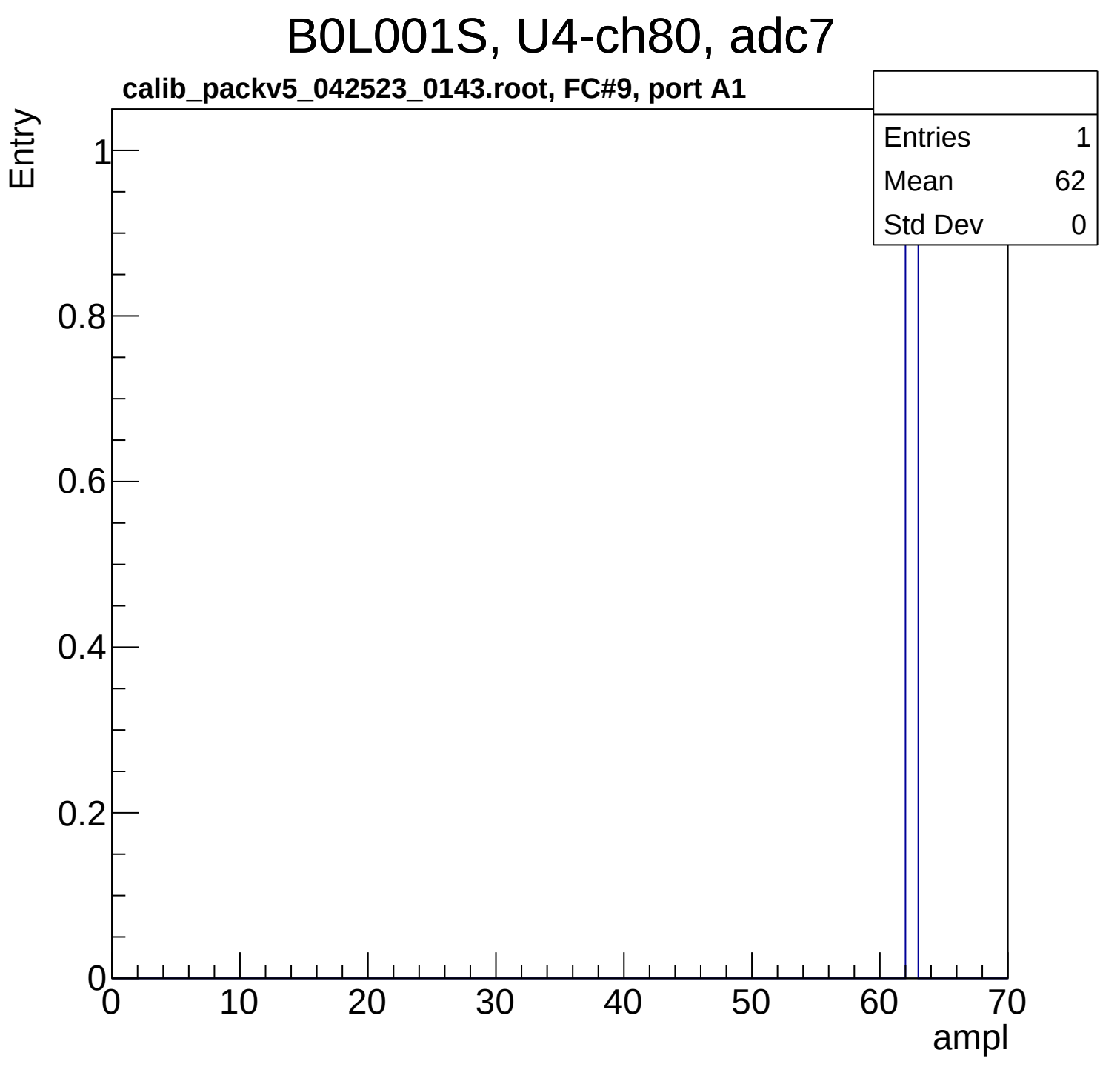

B0L001S, U4-ch80, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.75
Std Dev 9.225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch80, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch80, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
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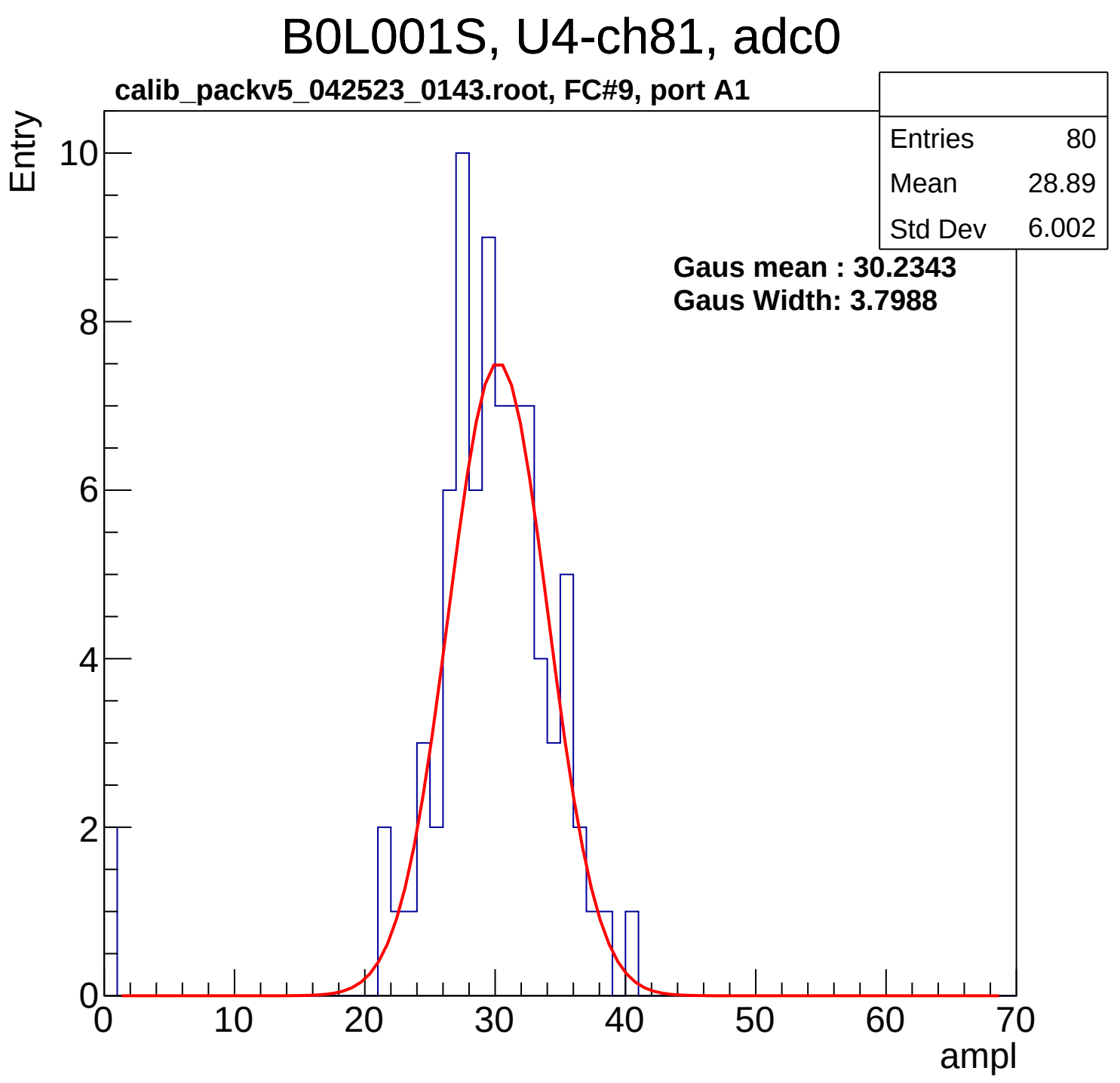
ampl


B0L001S, U4-ch81, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 28.89
Std Dev 6.002
Gaus mean : 30.2343
Gaus Width: 3.7988
Entry
ampl
0
2
4
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8
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0
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40
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60
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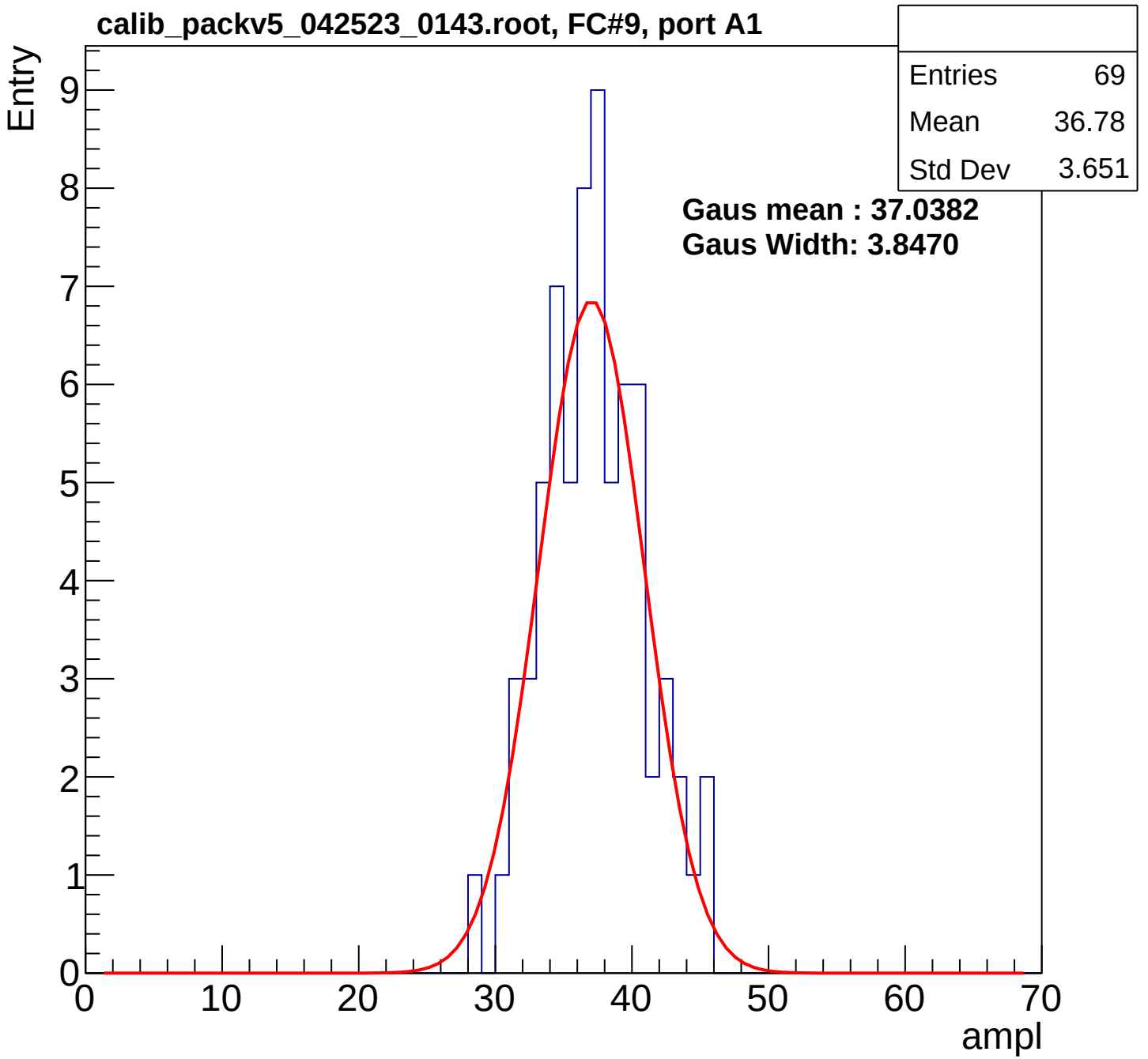
B0L001S, U4-ch81, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 36.78
Std Dev 3.651
Gaus mean : 37.0382
Gaus Width: 3.8470
Entry
ampl
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2
3
4
5
6
7
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0
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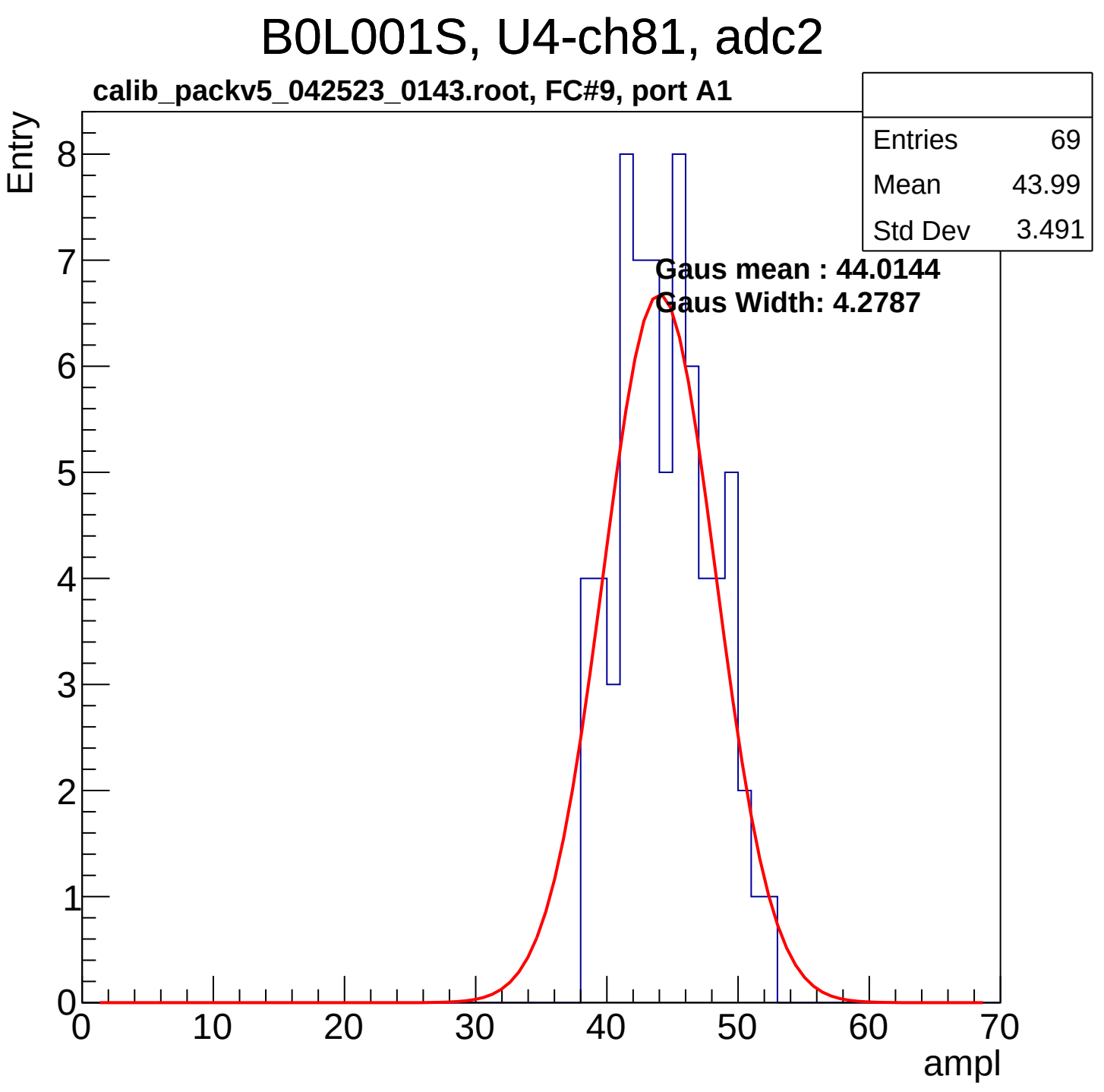
B0L001S, U4-ch81, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.99
Std Dev 3.491
Gaus mean : 44.0144
Gaus Width: 4.2787
Entry
ampl
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1
2
3
4
5
6
7
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0
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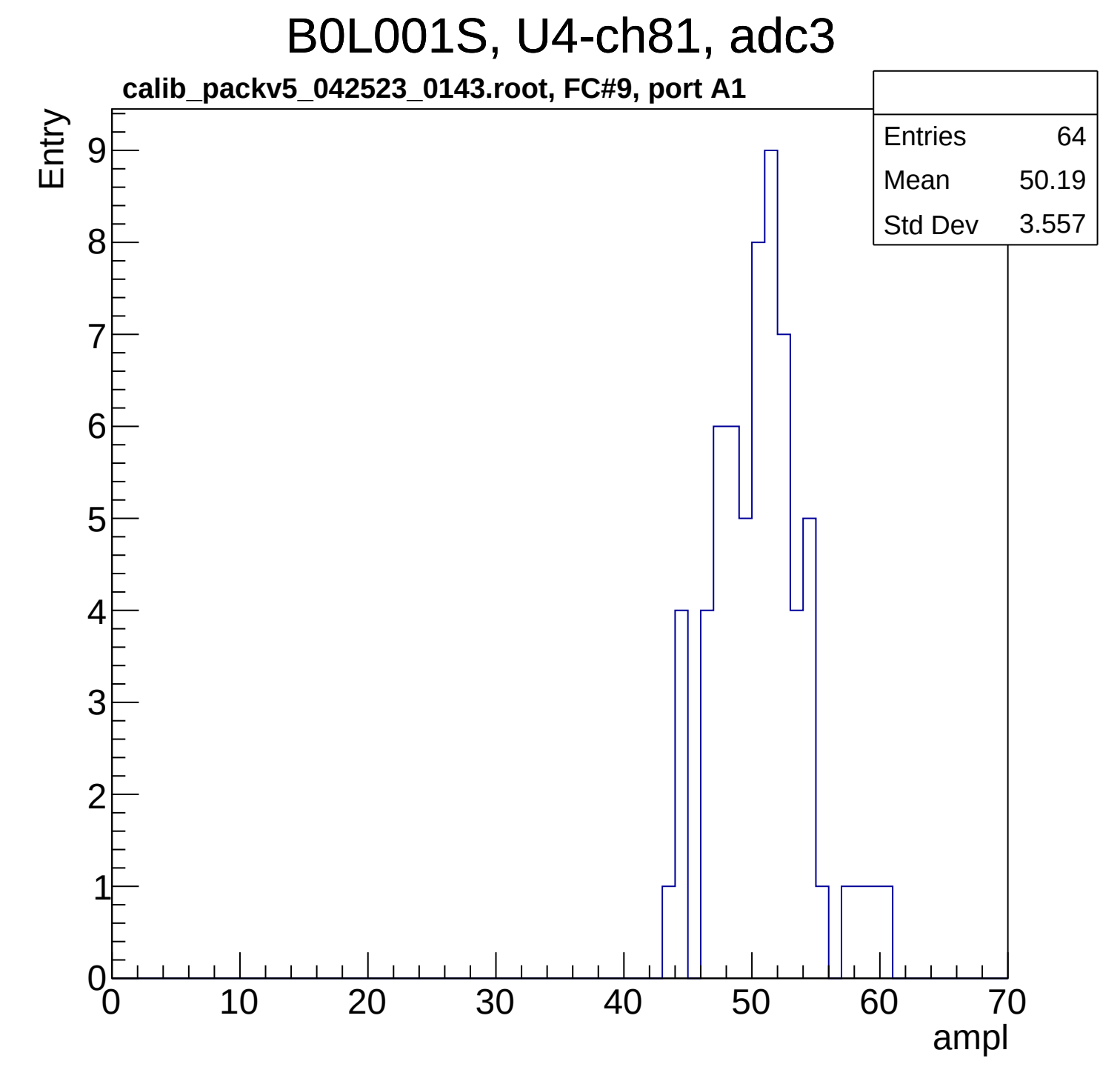
B0L001S, U4-ch81, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.19
Std Dev 3.557
Entry
ampl
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2
3
4
5
6
7
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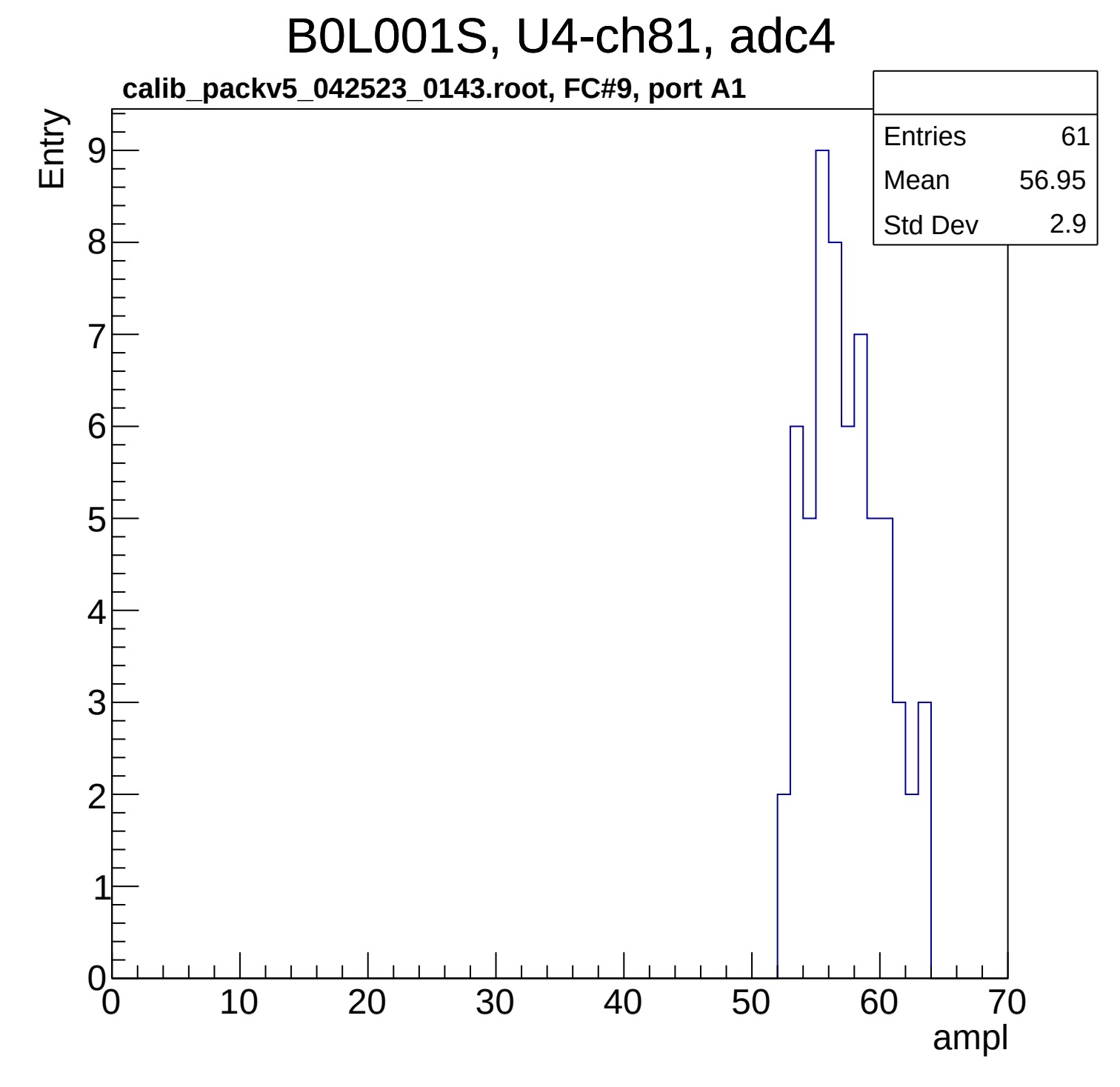
B0L001S, U4-ch81, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.95
Std Dev 2.9
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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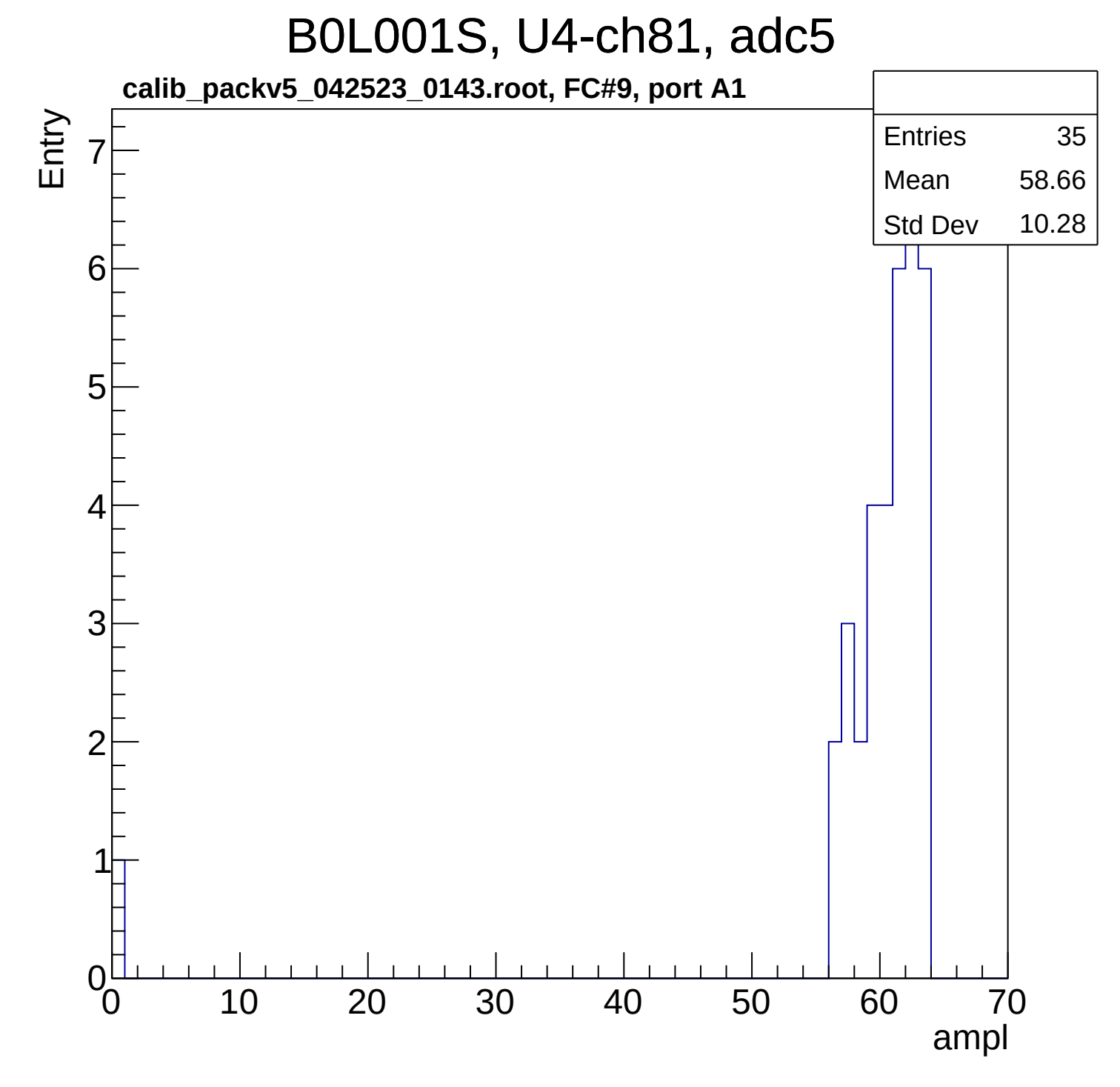
B0L001S, U4-ch81, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.66
Std Dev 10.28
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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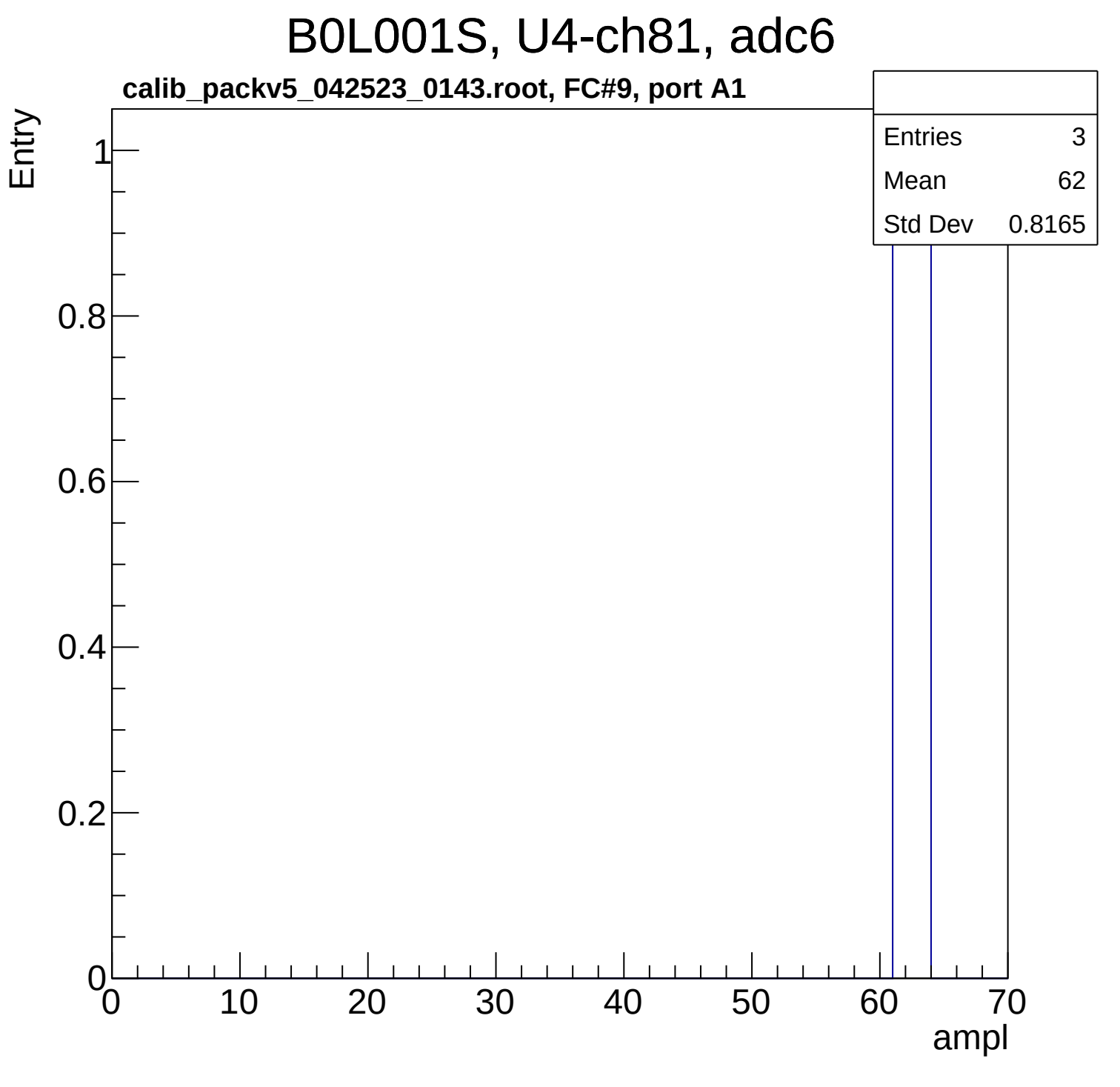
B0L001S, U4-ch81, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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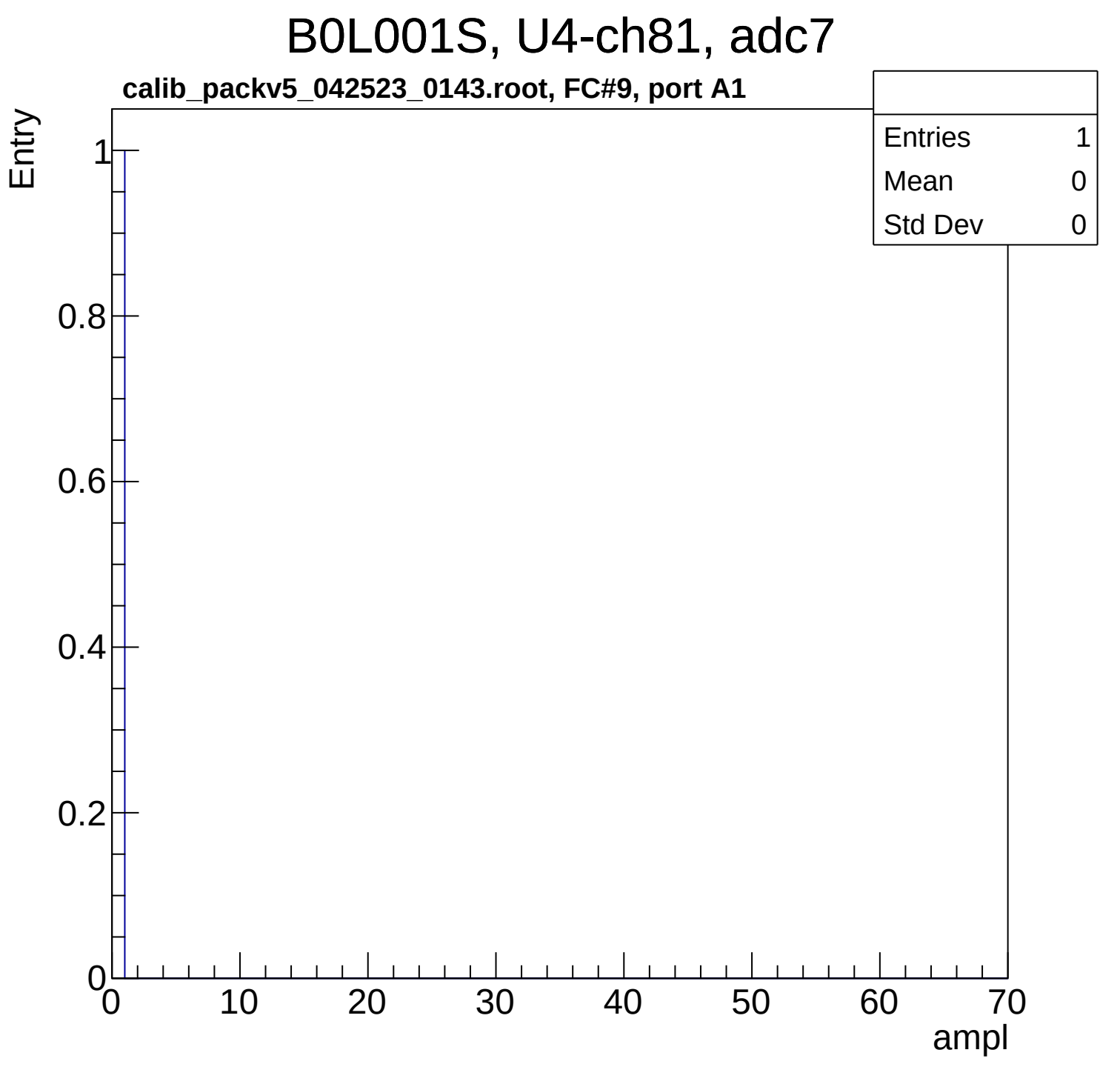
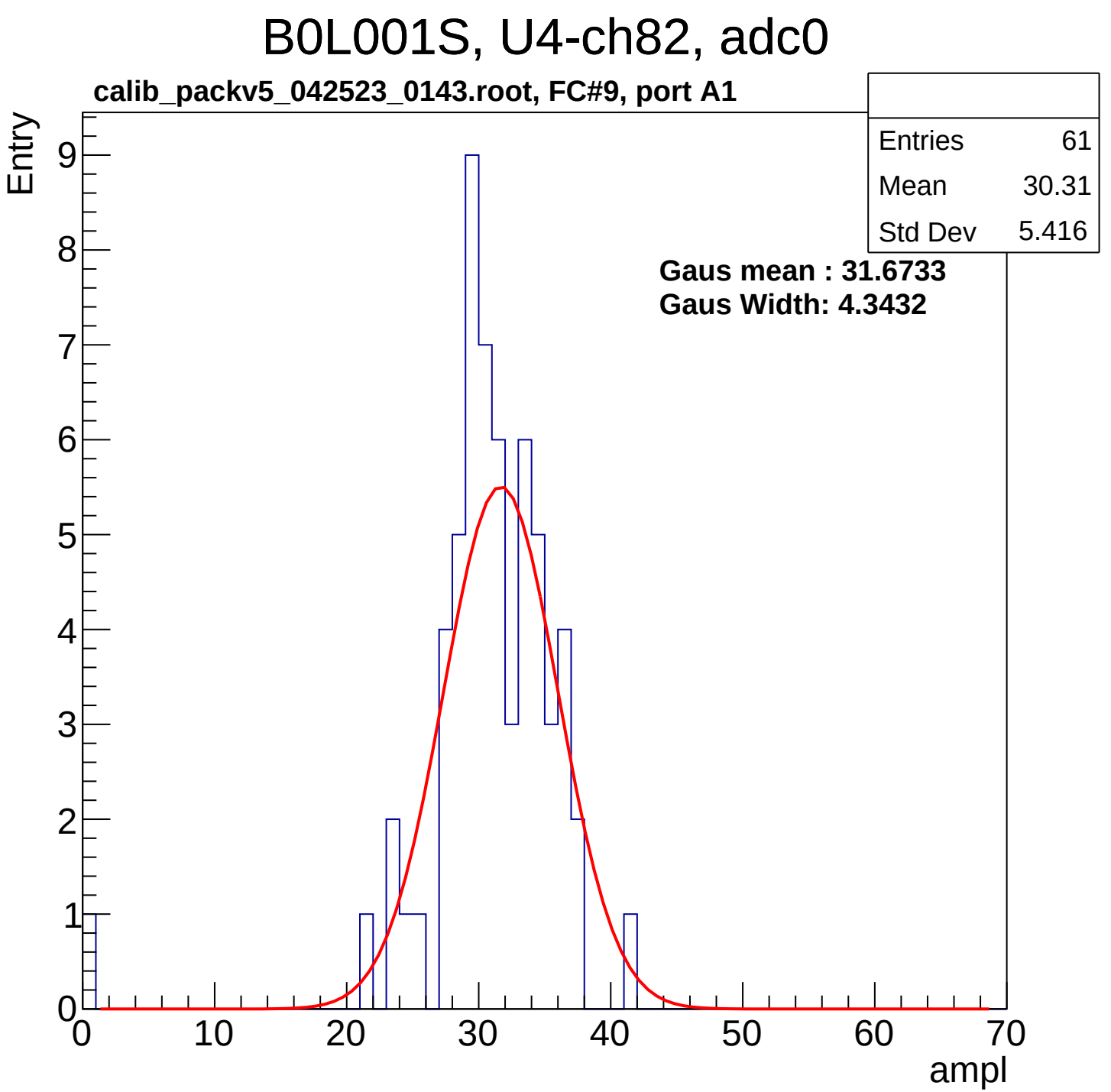
50
60
70
ampl


B0L001S, U4-ch81, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch82, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.31
Std Dev 5.416
Gaus mean : 31.6733
Gaus Width: 4.3432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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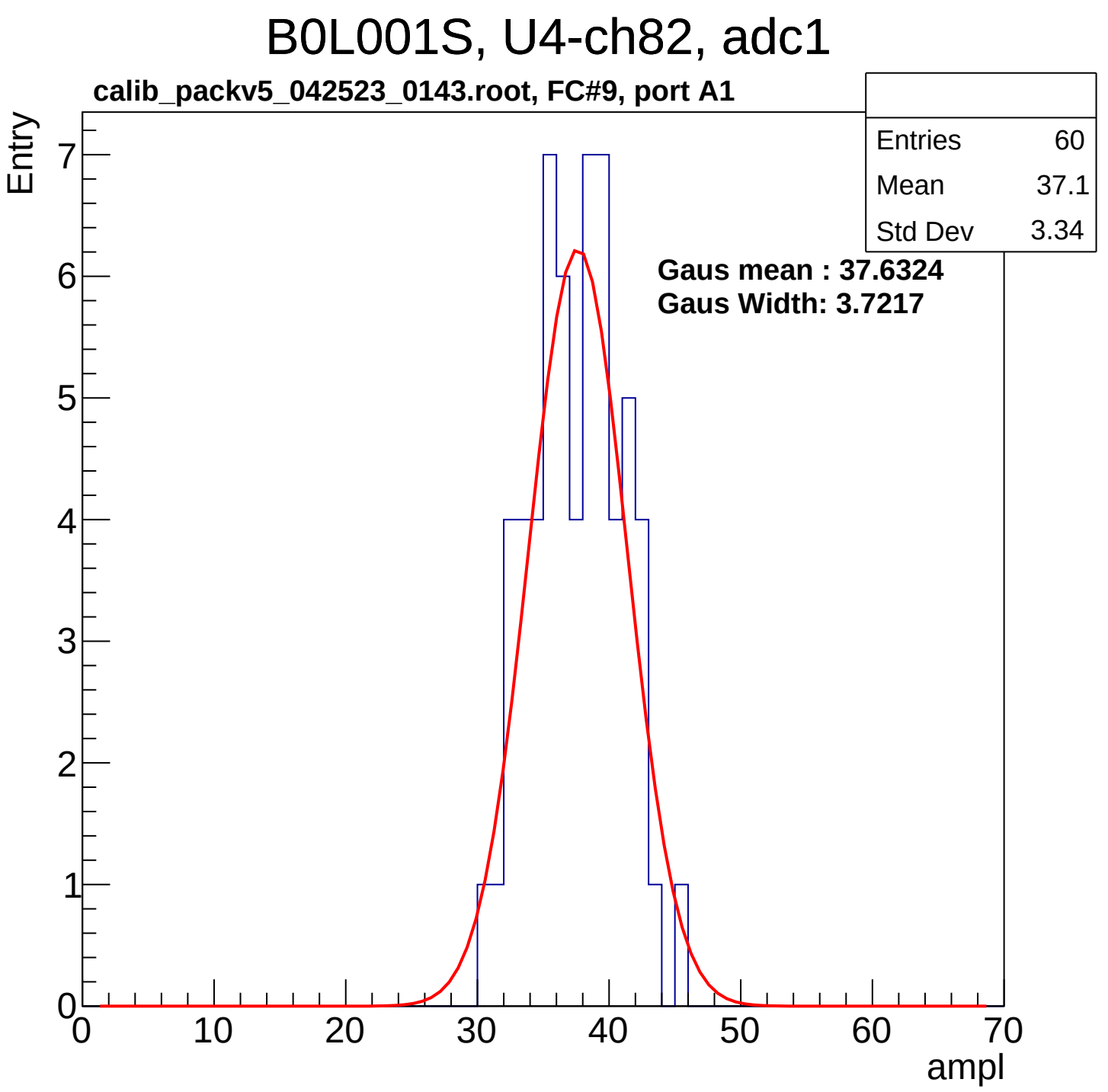
B0L001S, U4-ch82, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.1
Std Dev 3.34
Gaus mean : 37.6324
Gaus Width: 3.7217
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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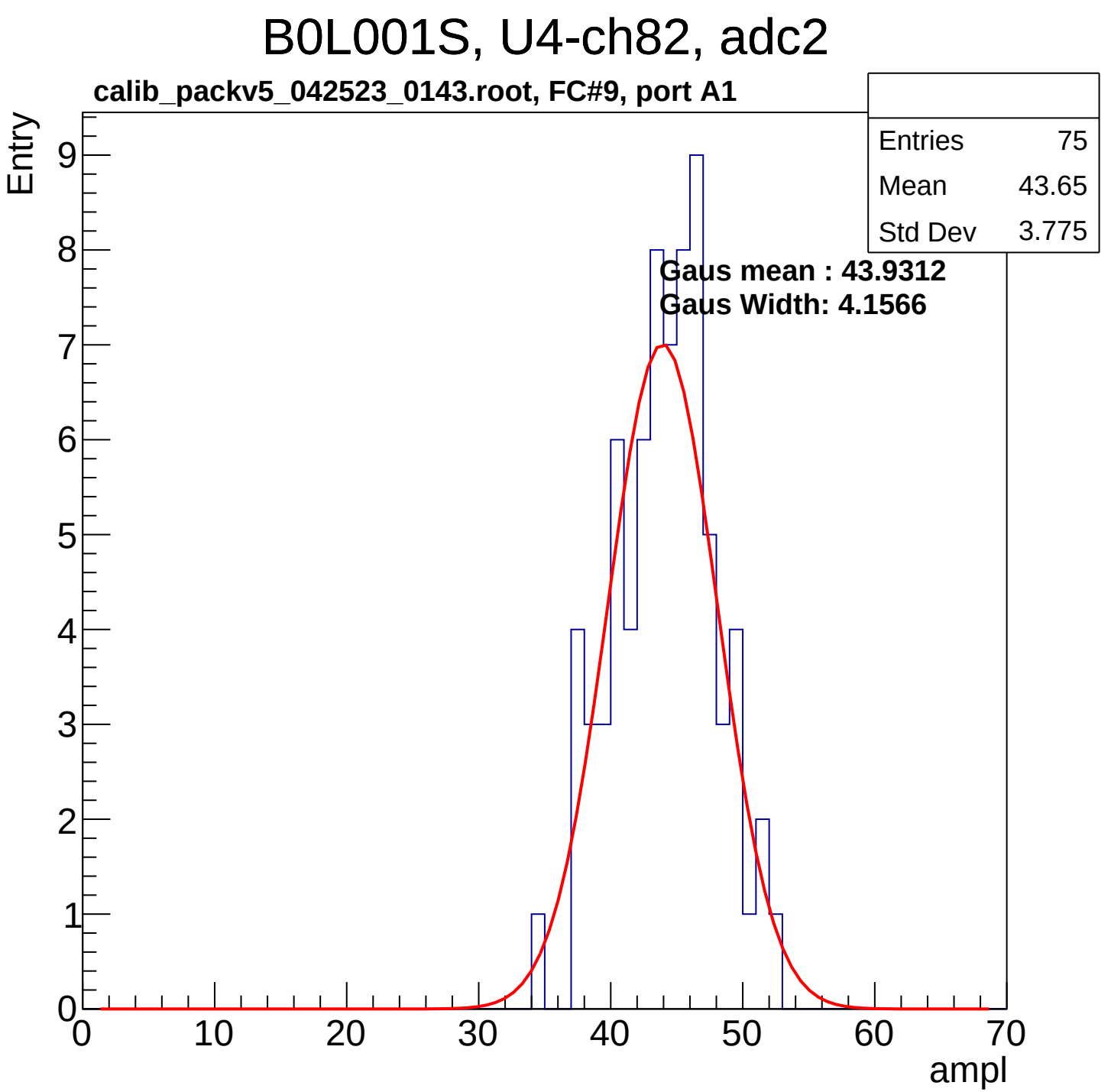
B0L001S, U4-ch82, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 43.65
Std Dev 3.775
Gaus mean : 43.9312
Gaus Width: 4.1566
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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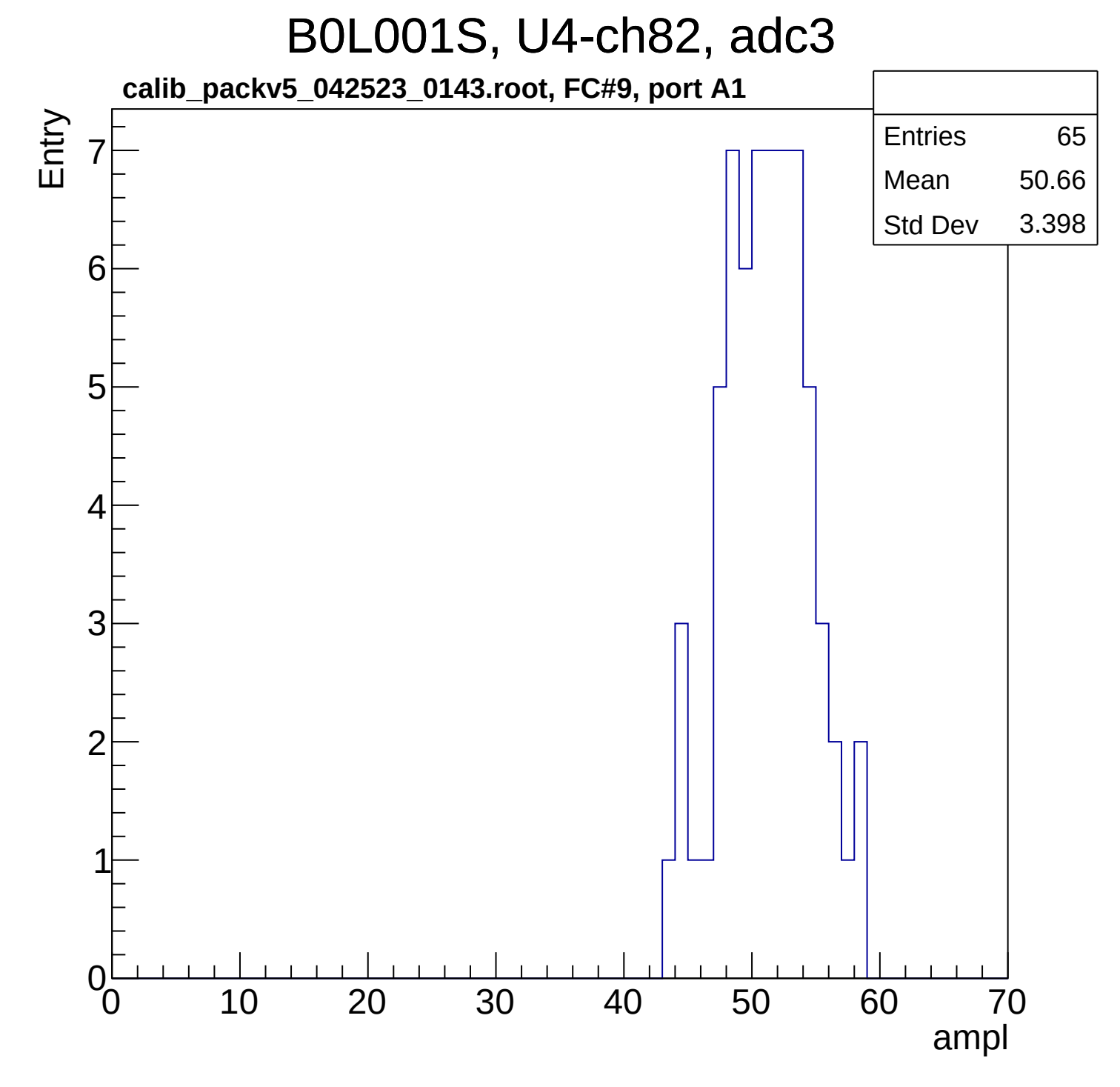
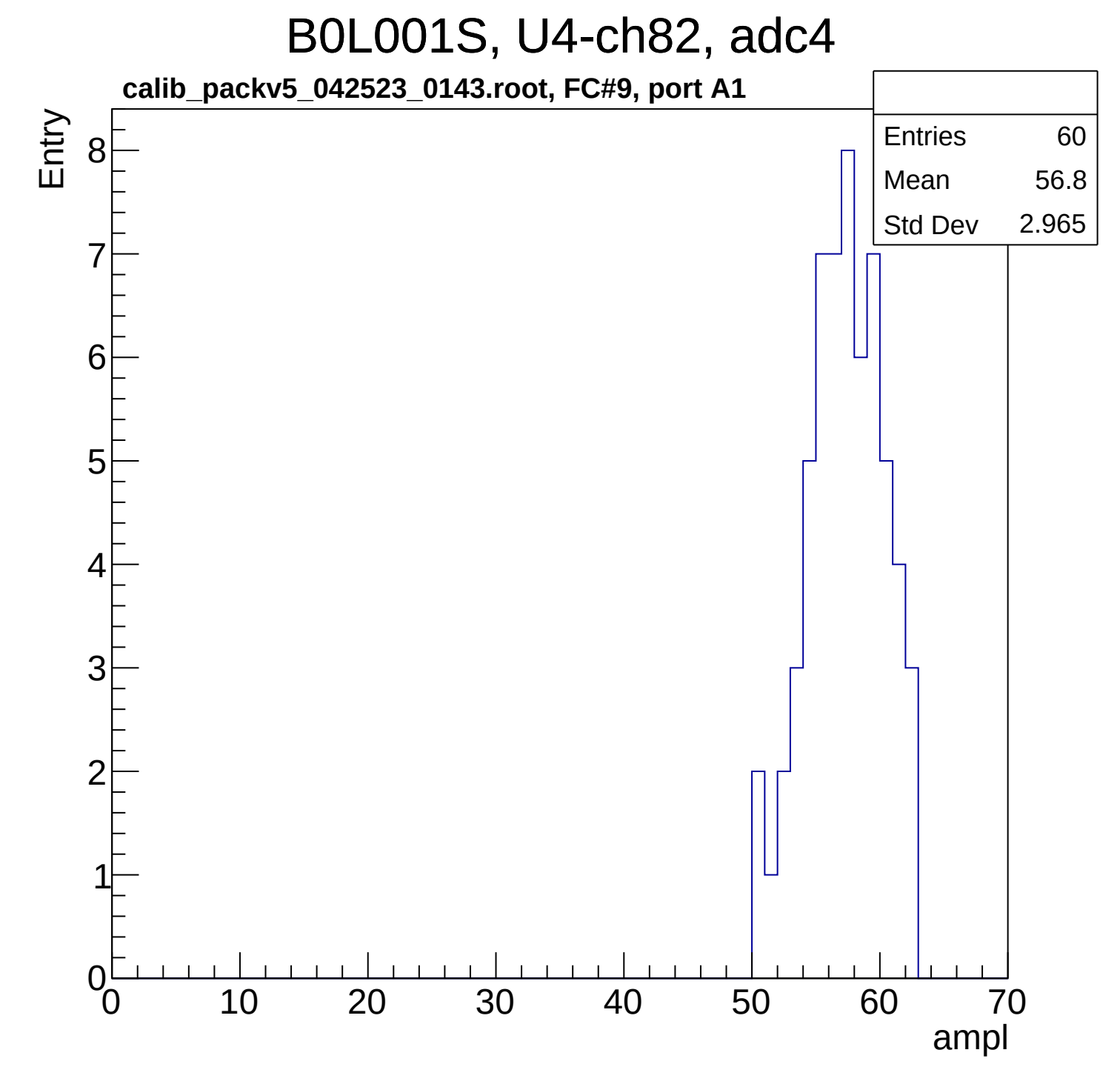

B0L001S, U4-ch82, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.66
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch82, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.8
Std Dev 2.965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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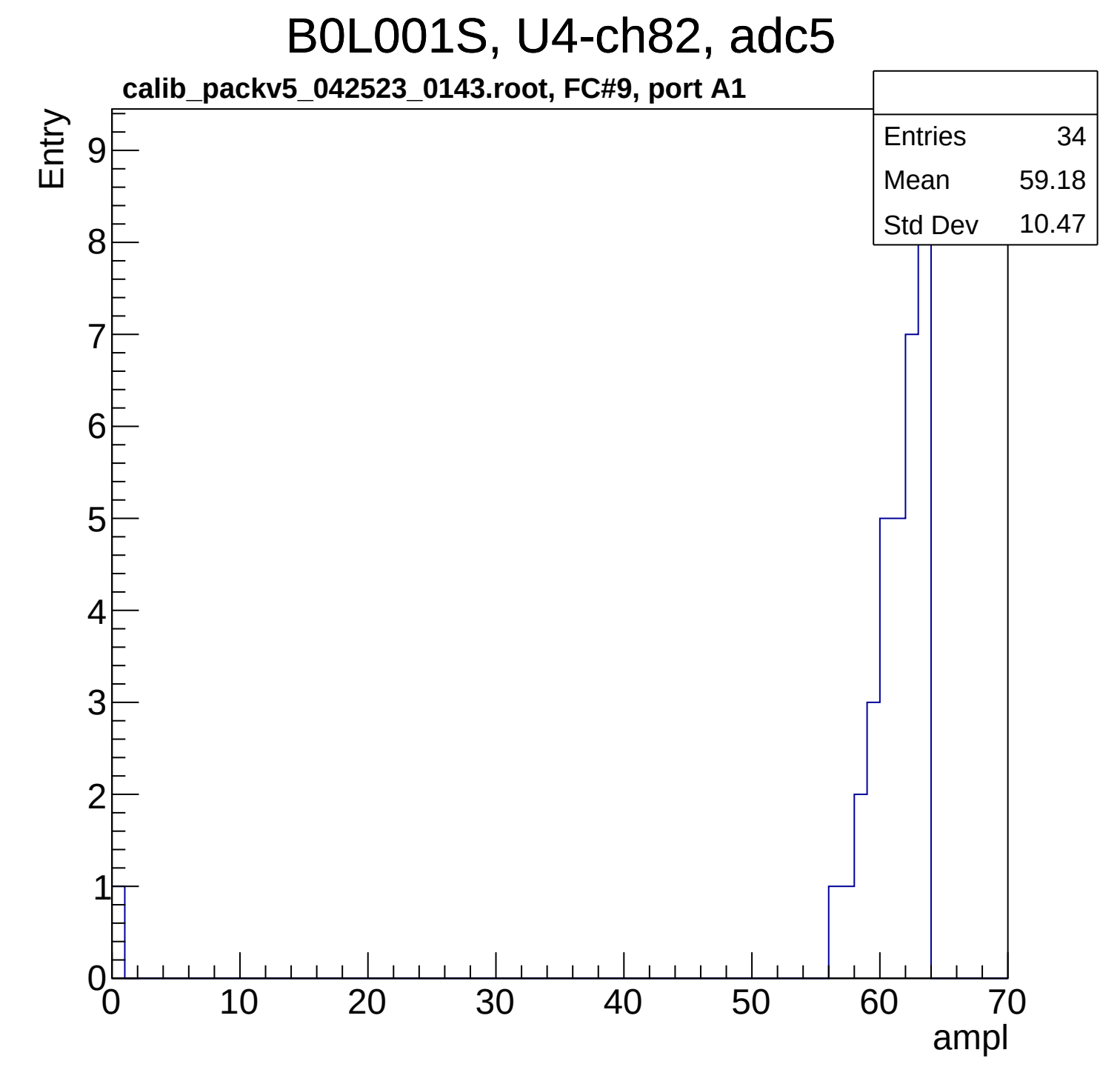
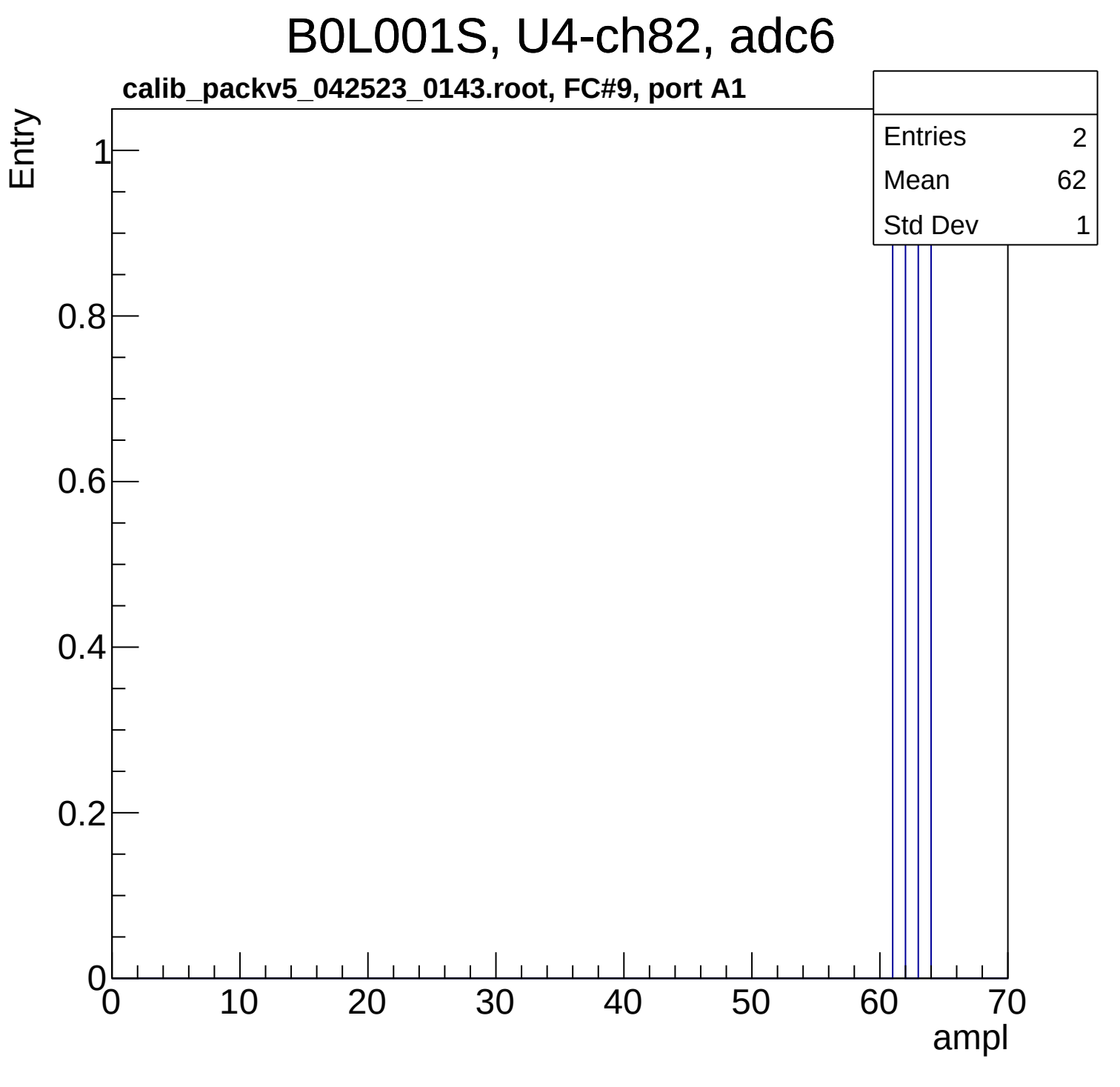
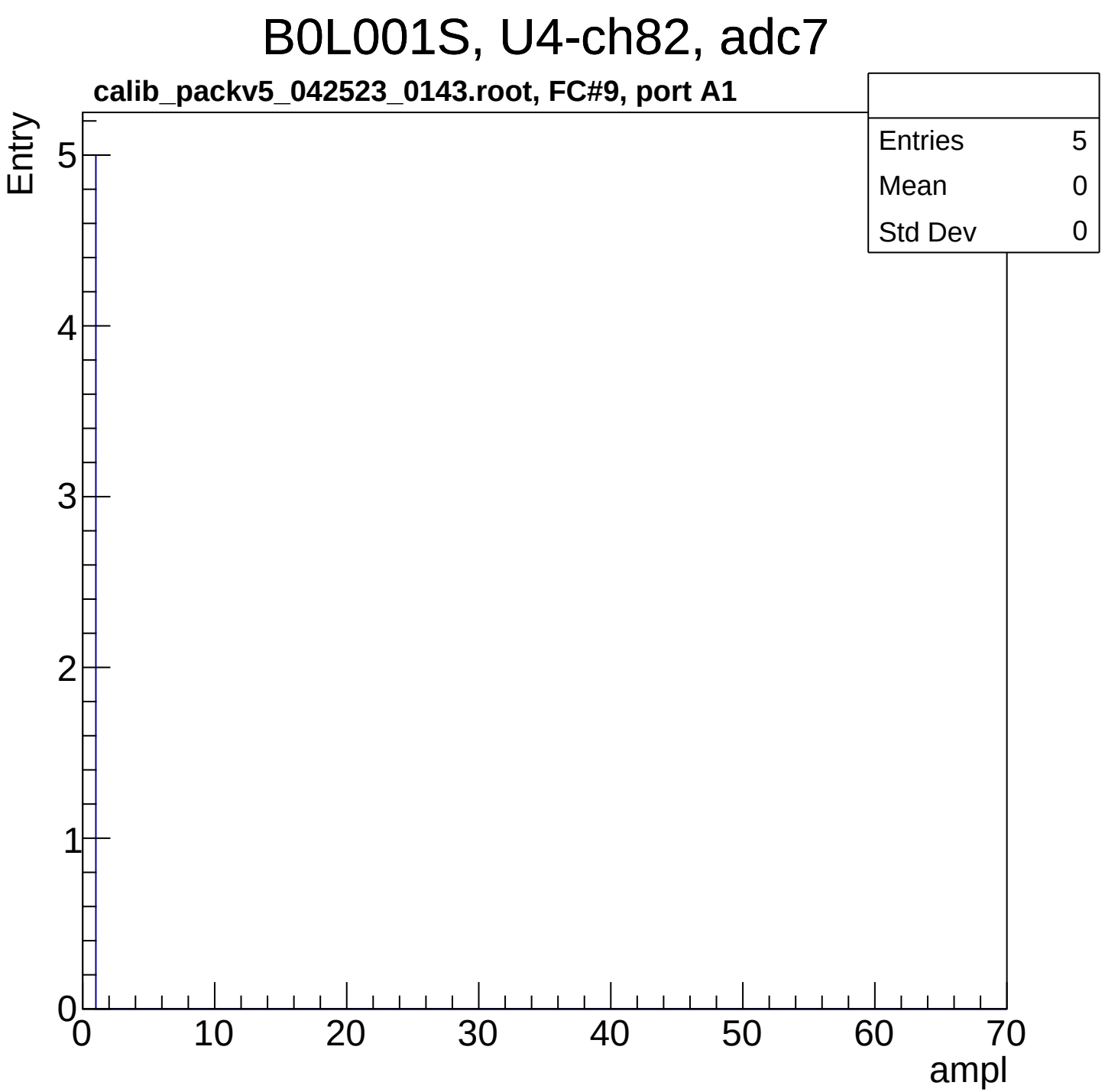
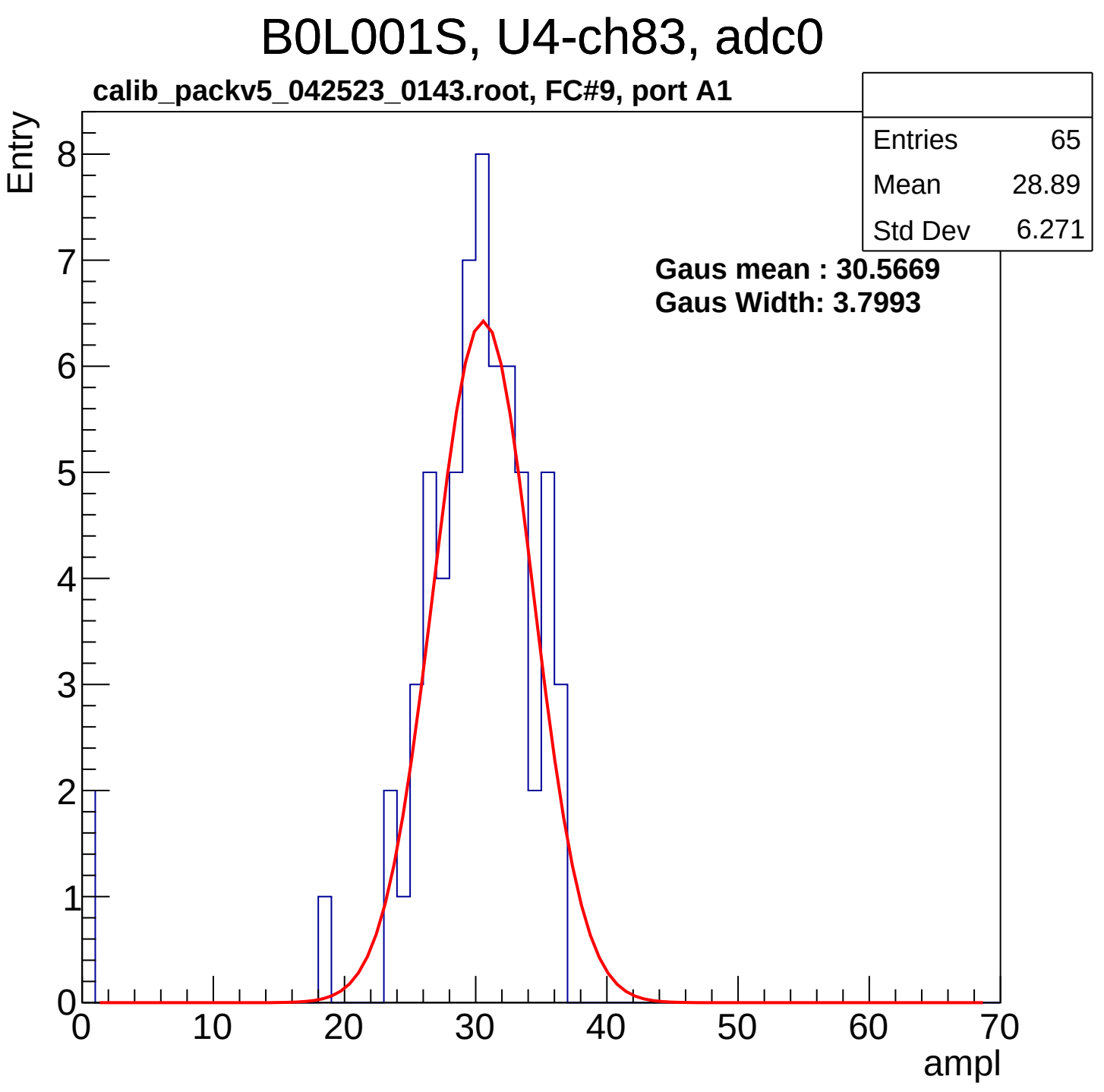

B0L001S, U4-ch82, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.18
Std Dev 10.47
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch82, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch82, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 0
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch83, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 28.89
Std Dev 6.271
Gaus mean : 30.5669
Gaus Width: 3.7993
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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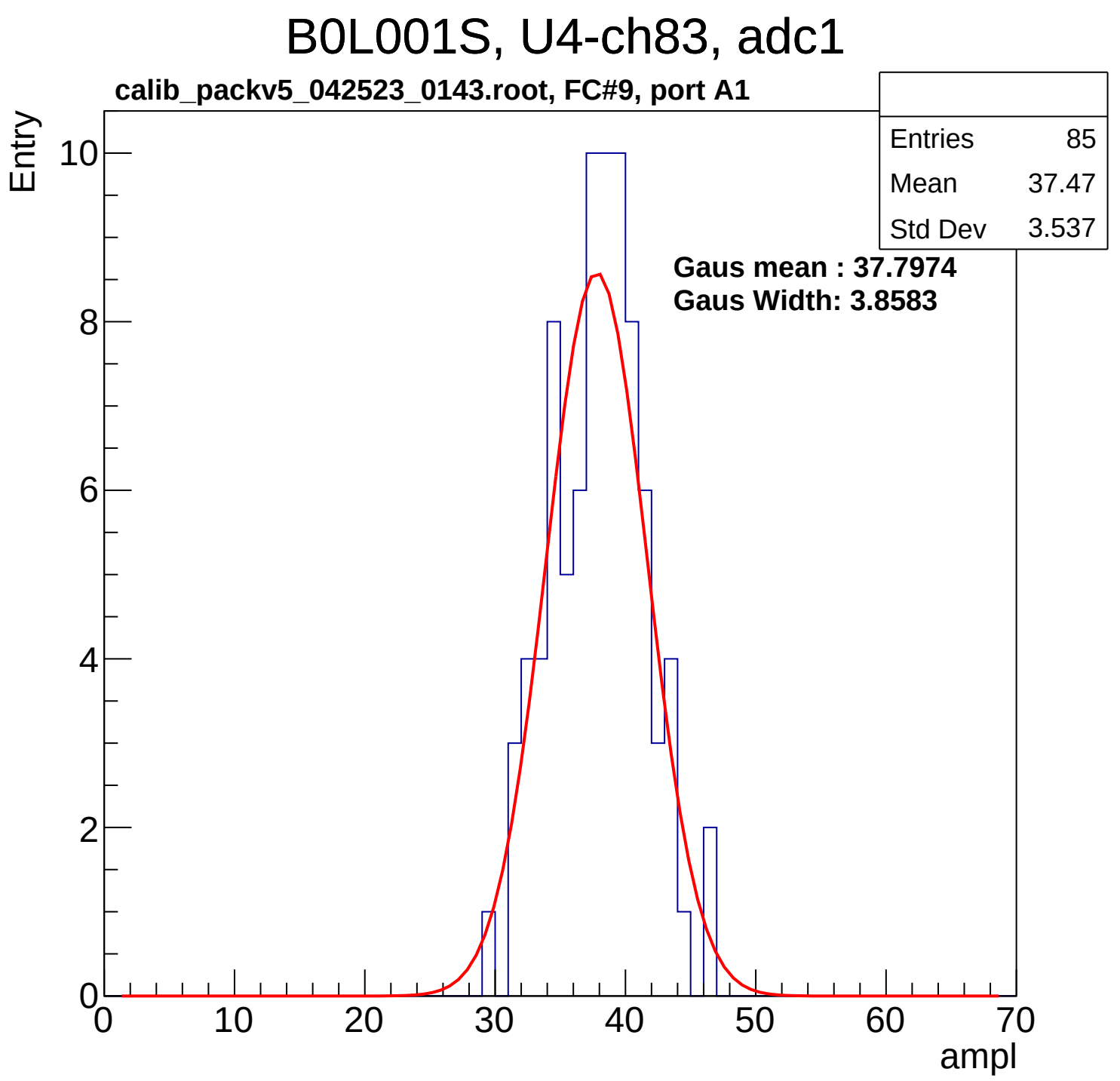
B0L001S, U4-ch83, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 37.47
Std Dev 3.537
Gaus mean : 37.7974
Gaus Width: 3.8583
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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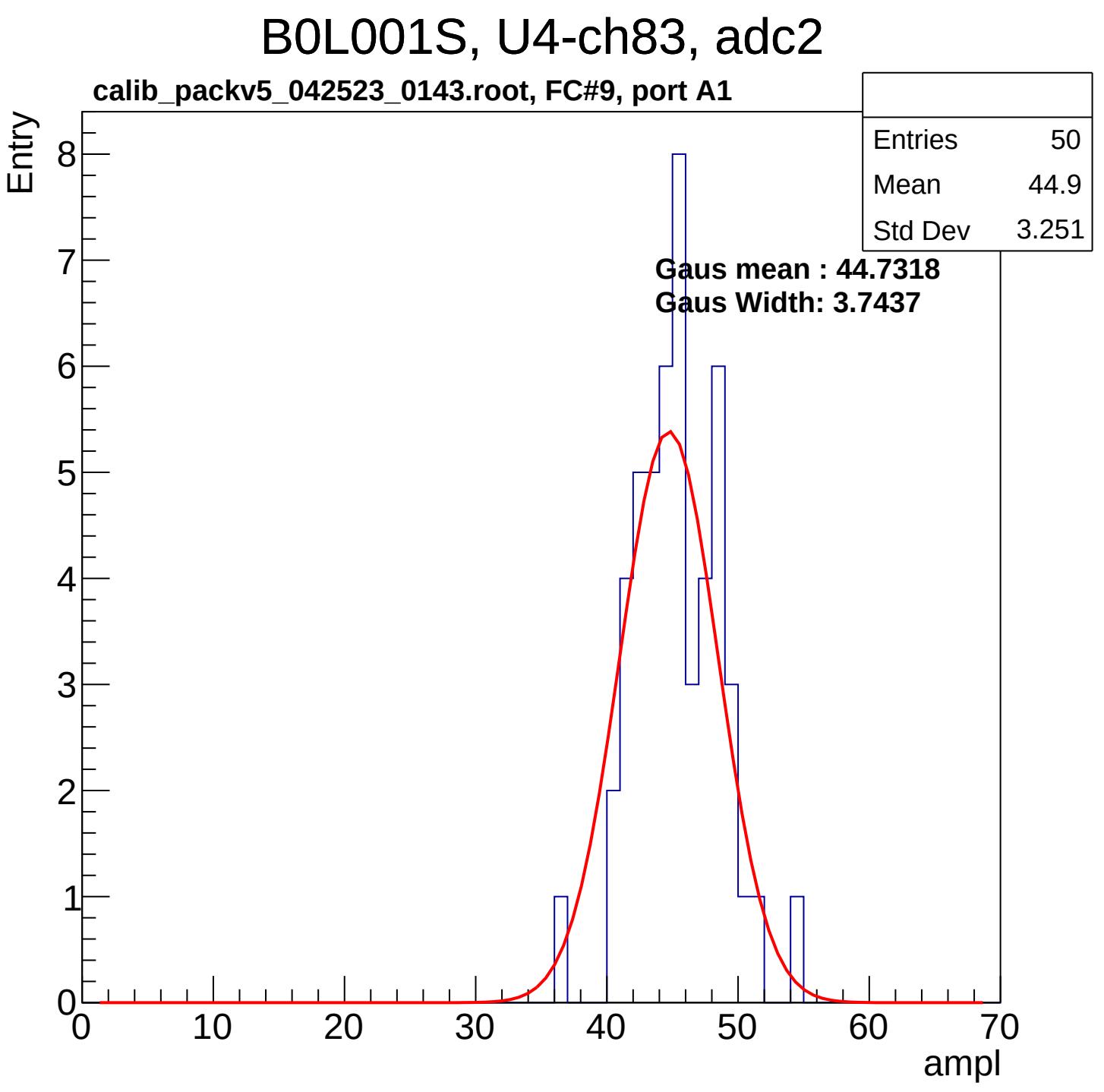
B0L001S, U4-ch83, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 44.9
Std Dev 3.251
Gaus mean : 44.7318
Gaus Width: 3.7437
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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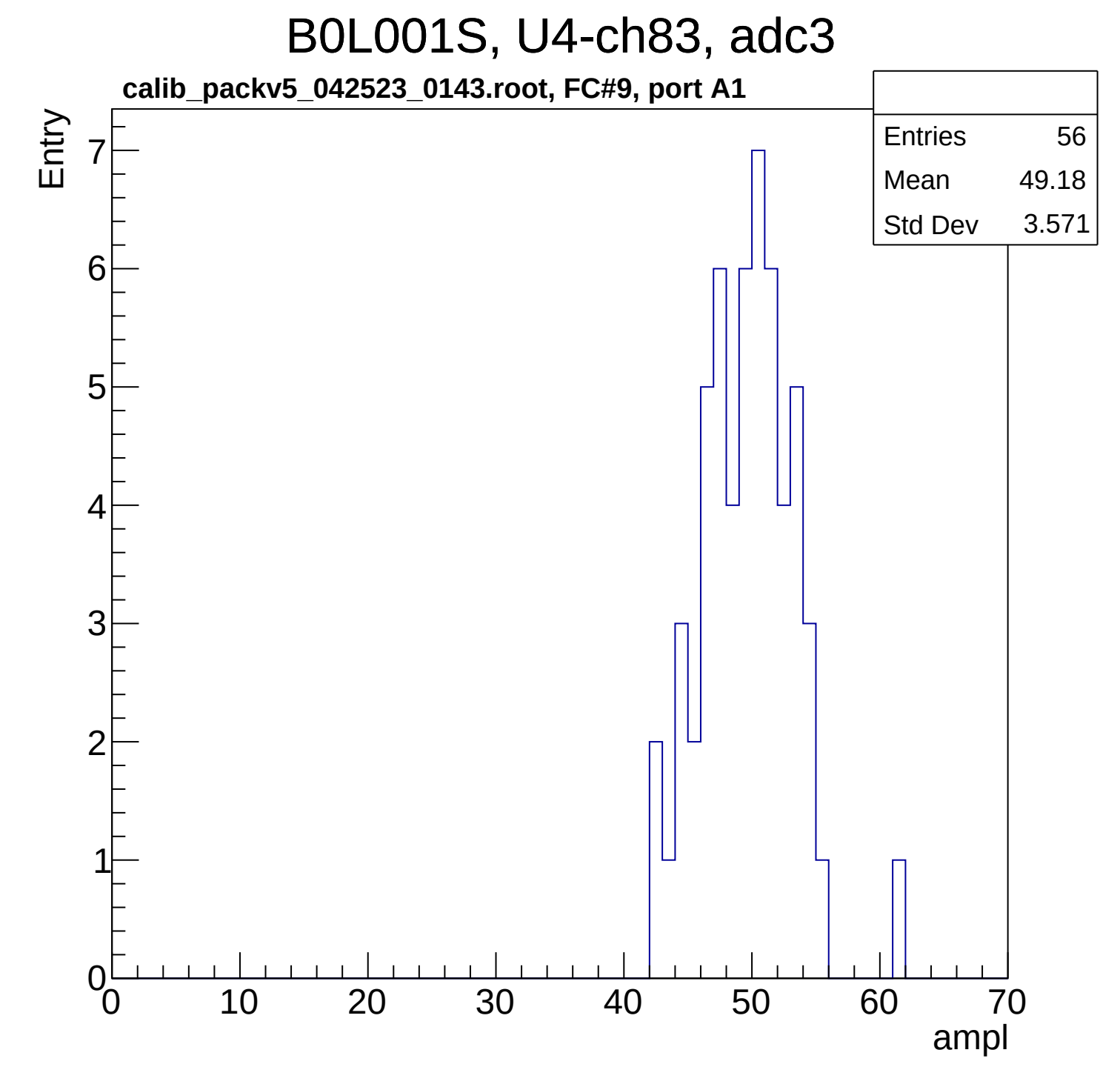
B0L001S, U4-ch83, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.18
Std Dev 3.571
Entry
ampl
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1
2
3
4
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0
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60
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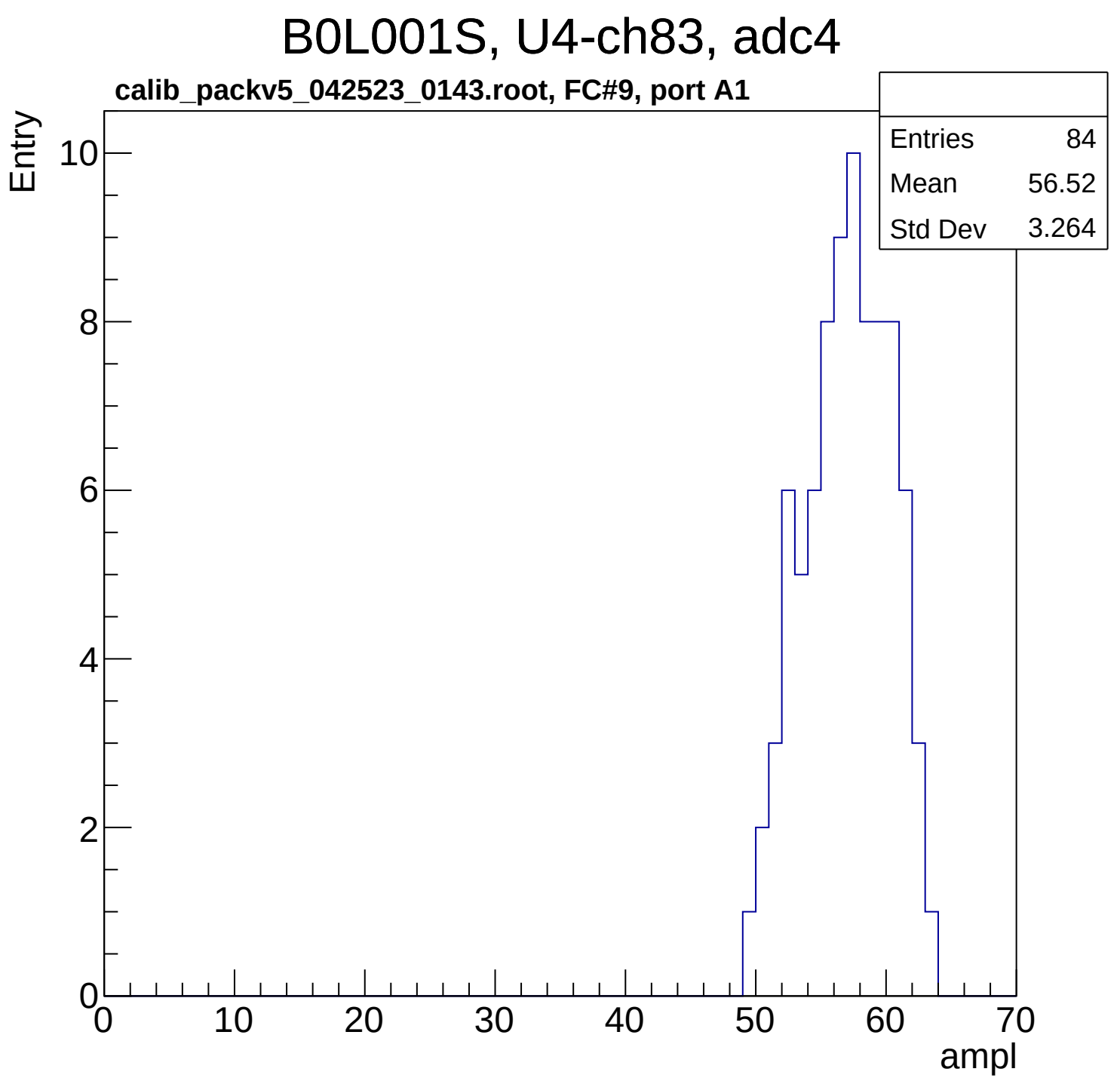
B0L001S, U4-ch83, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 56.52
Std Dev 3.264
Entry
ampl
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0
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40
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60
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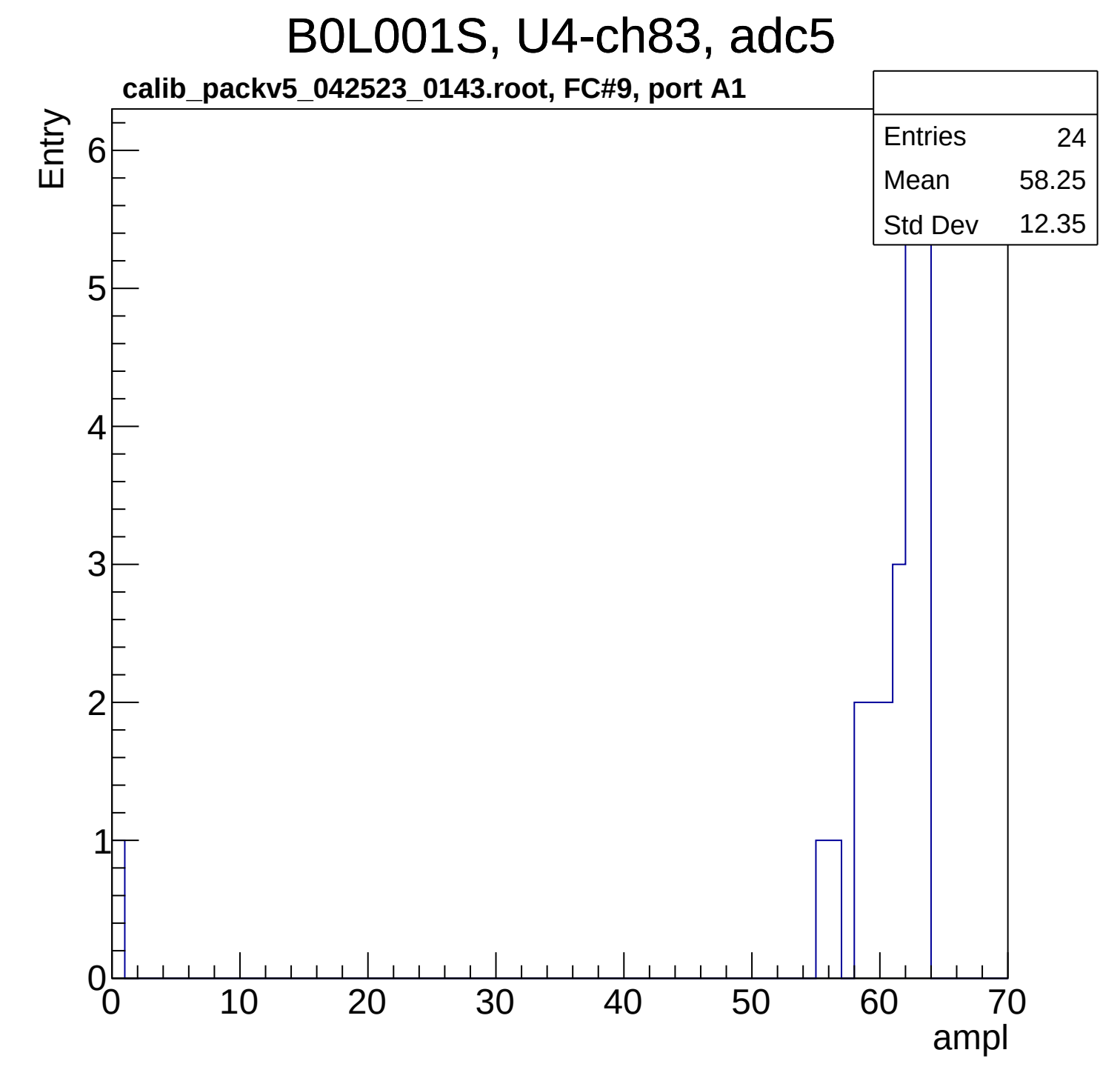
B0L001S, U4-ch83, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 58.25
Std Dev 12.35
Entry
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10
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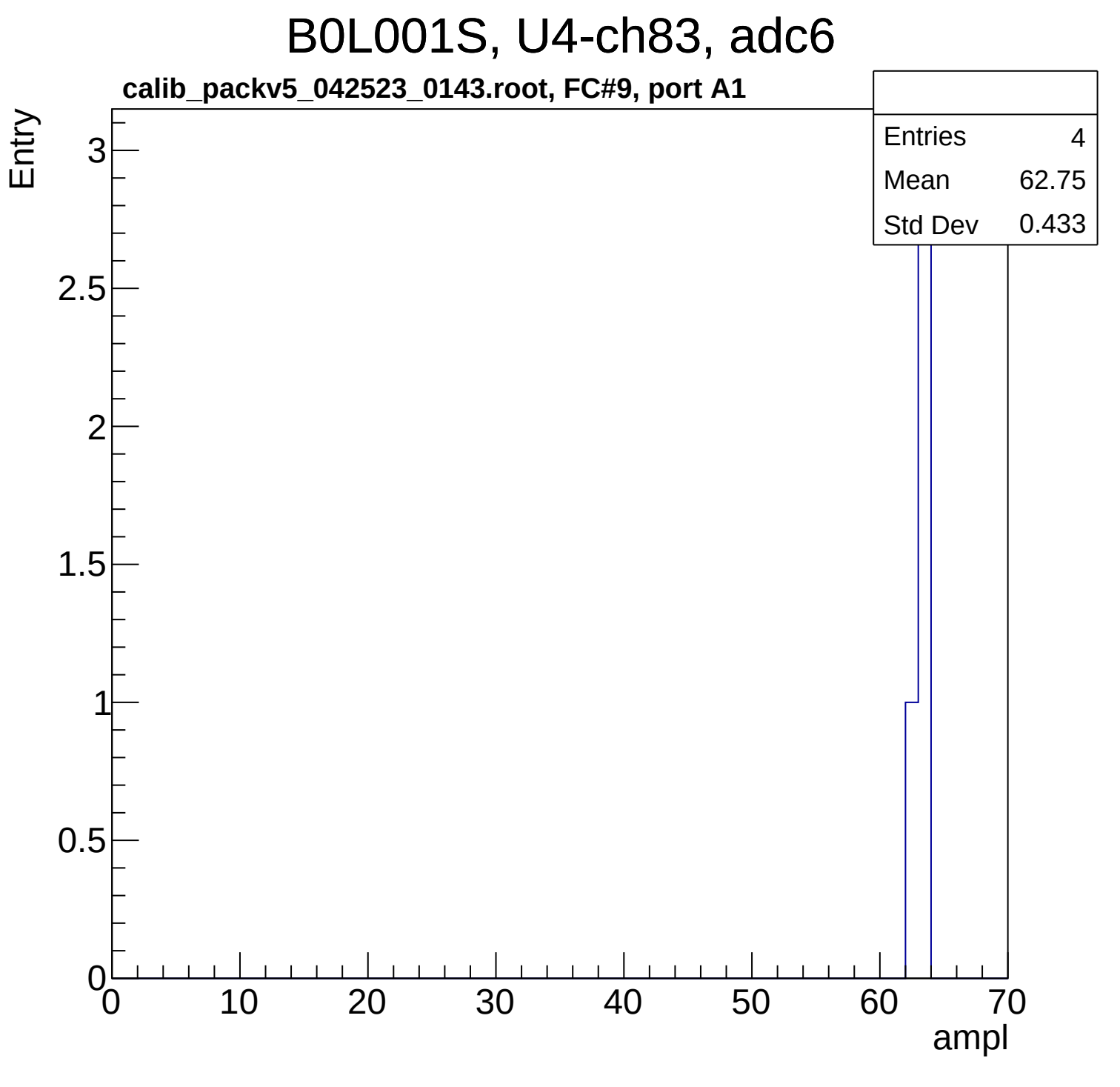
60
70
ampl


B0L001S, U4-ch83, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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1
1.5
2
2.5
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0
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20
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40
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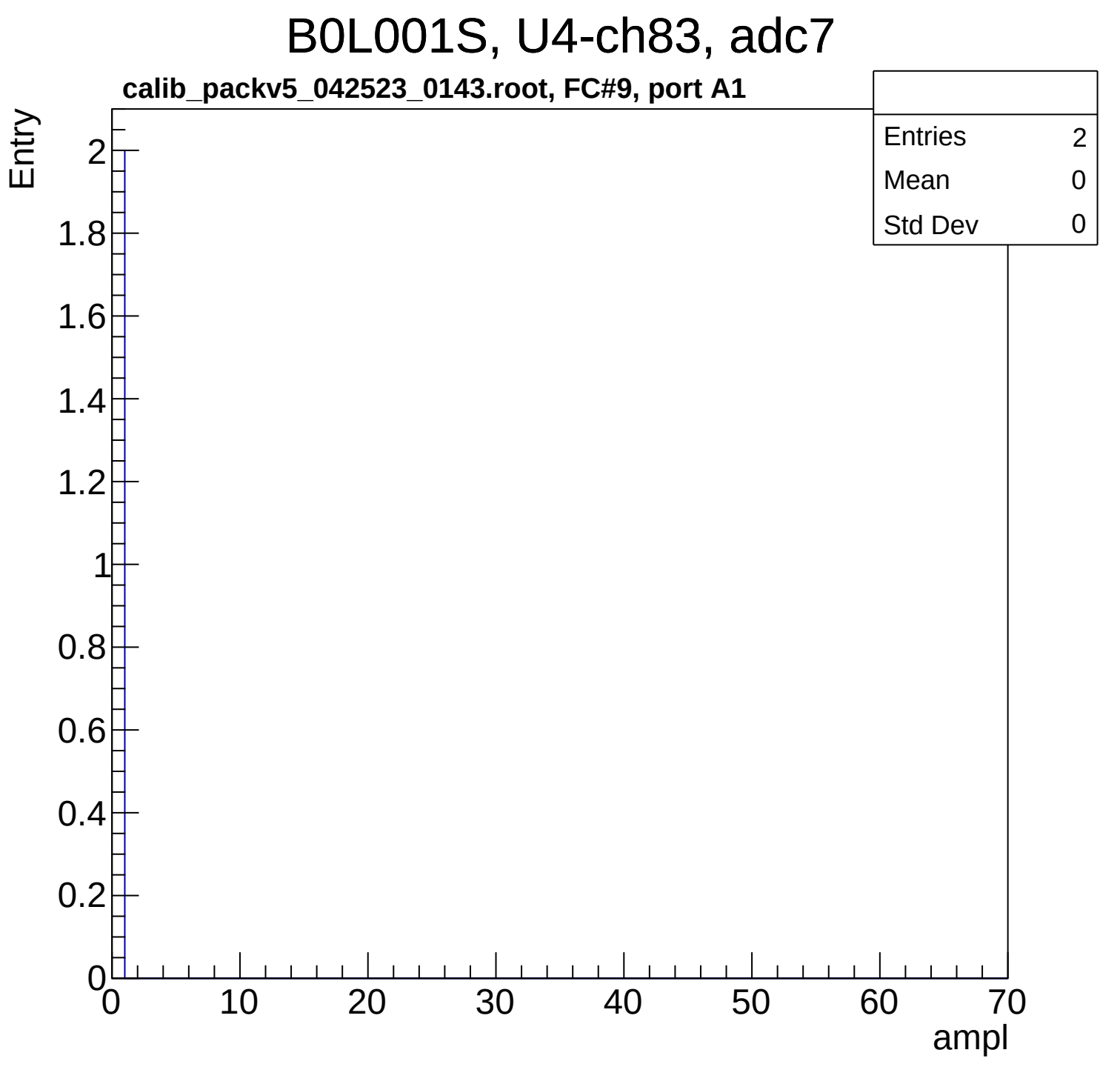
60
70


B0L001S, U4-ch83, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
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0.6
0.4
0.2
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0
10
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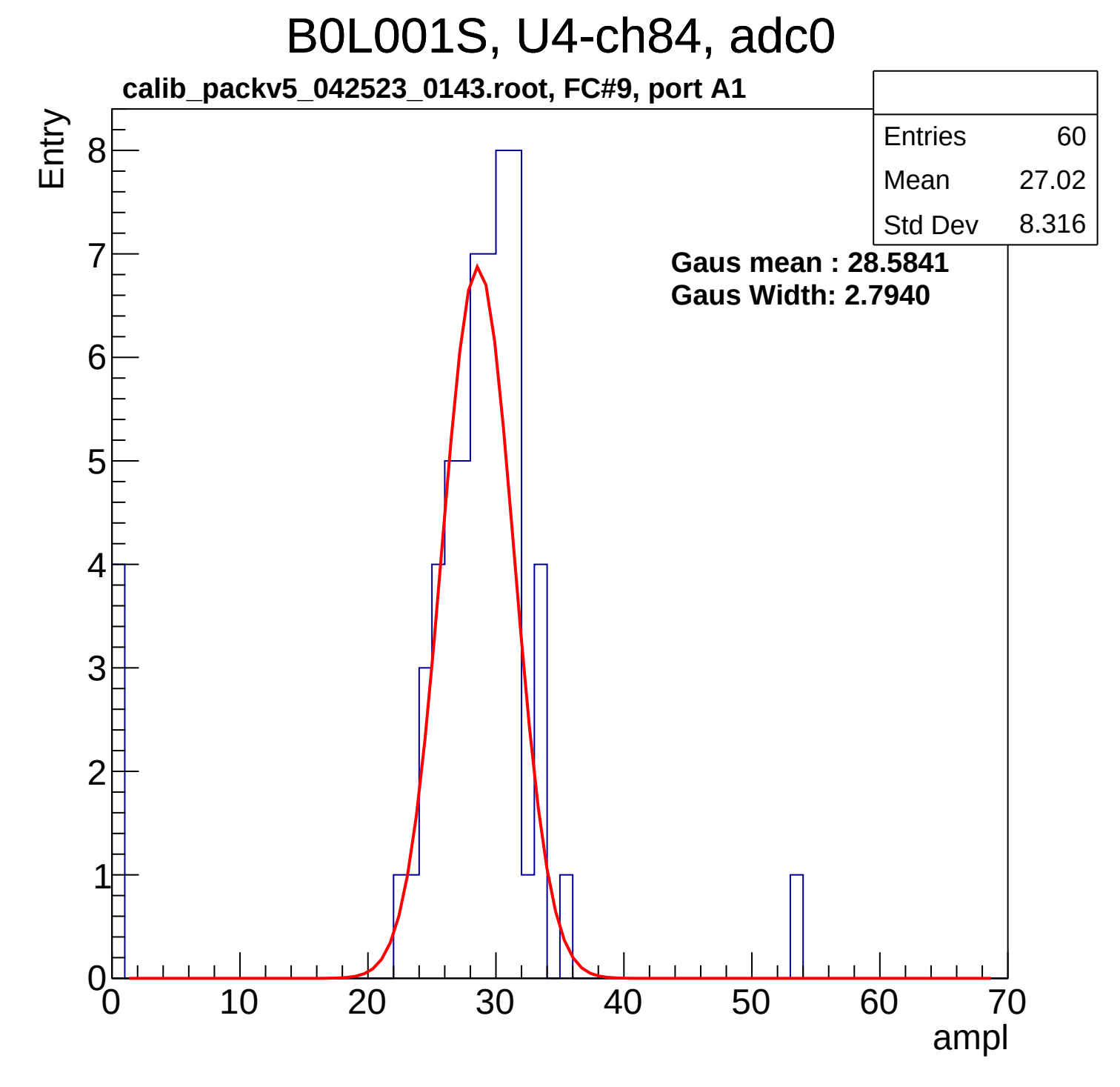
50
60
70
ampl


B0L001S, U4-ch84, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 27.02
Std Dev 8.316
Gaus mean : 28.5841
Gaus Width: 2.7940
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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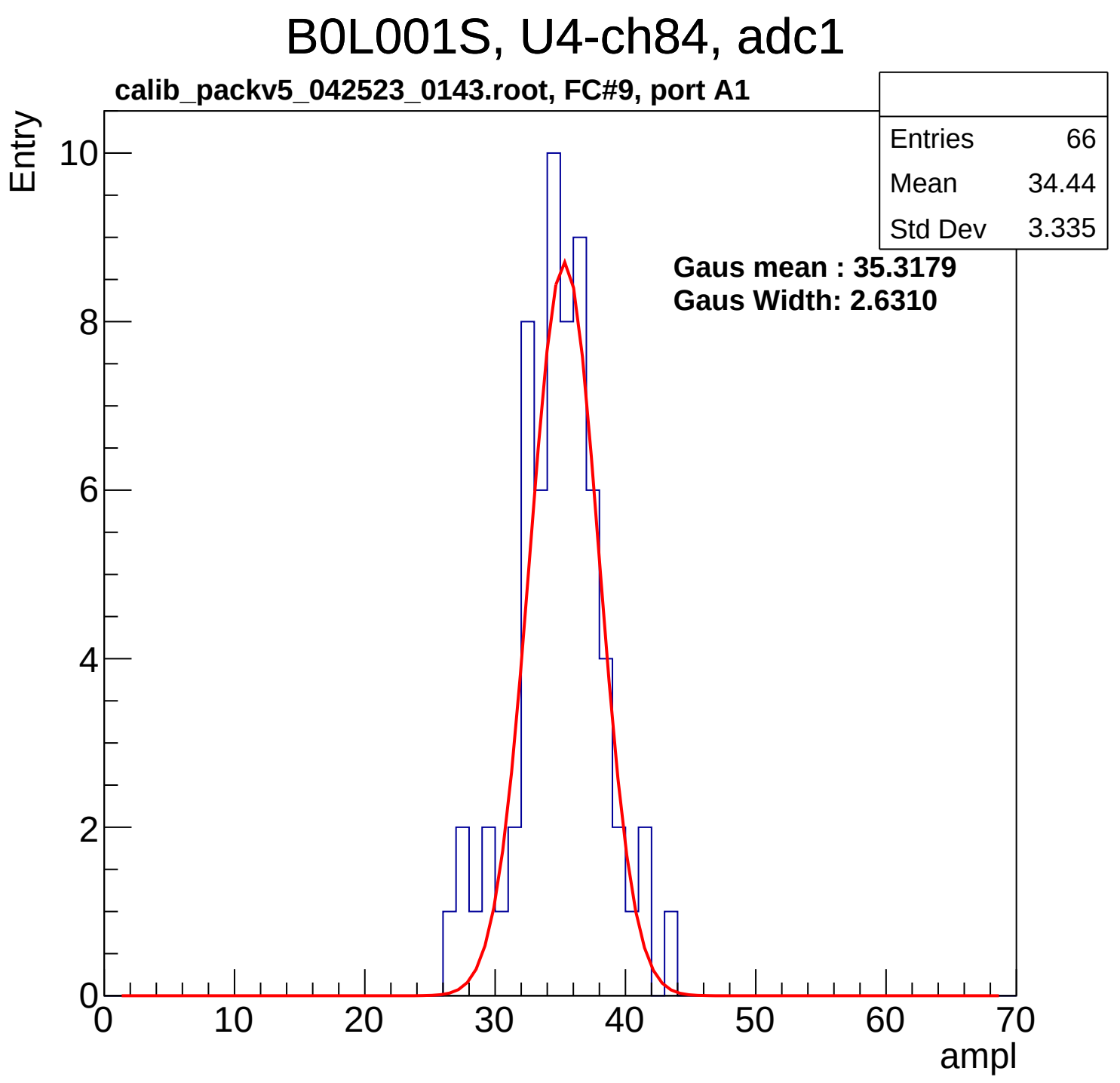
40
50
60
70


B0L001S, U4-ch84, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 34.44
Std Dev 3.335
Gaus mean : 35.3179
Gaus Width: 2.6310
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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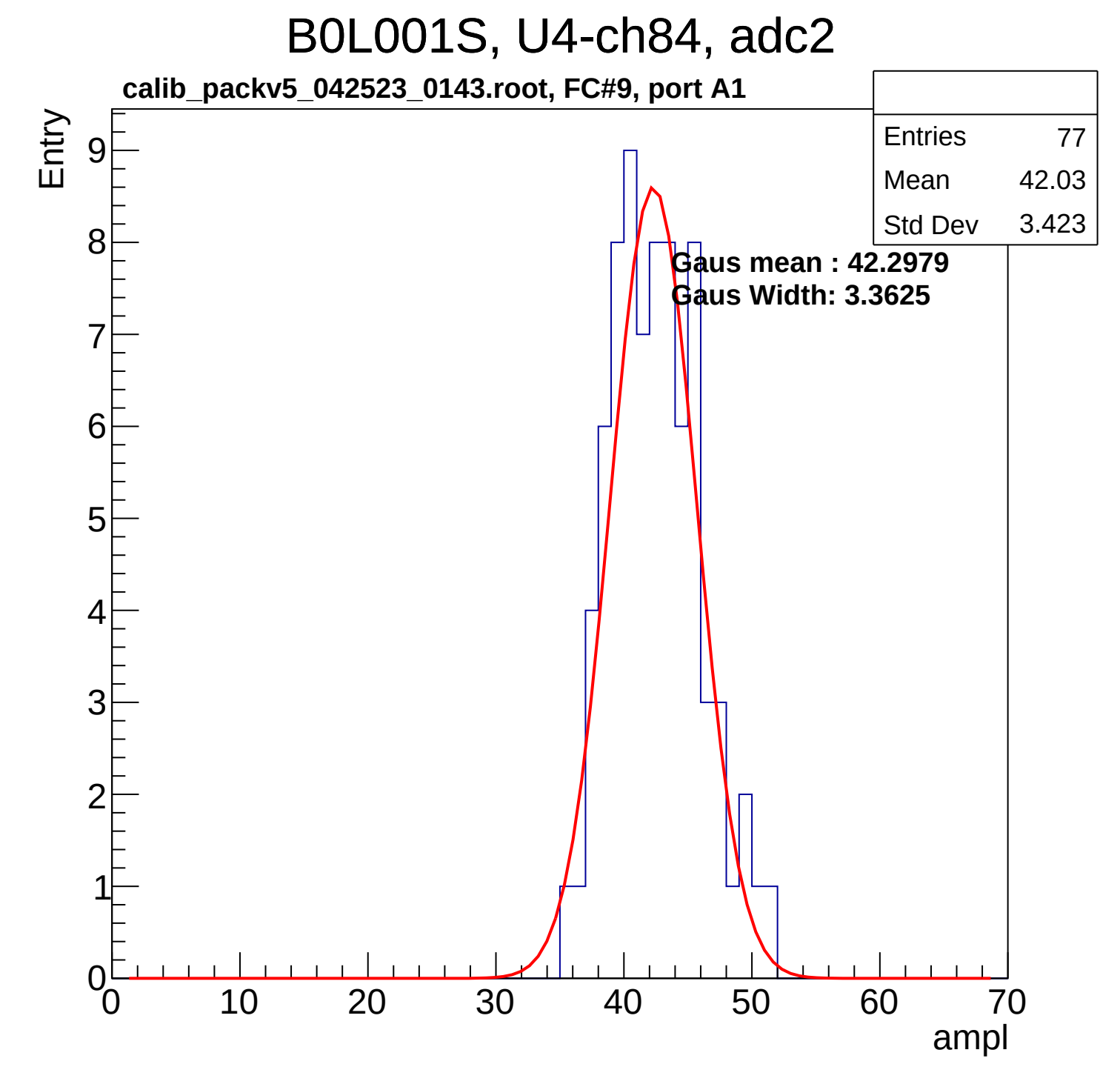
B0L001S, U4-ch84, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 42.03
Std Dev 3.423
Gaus mean : 42.2979
Gaus Width: 3.3625
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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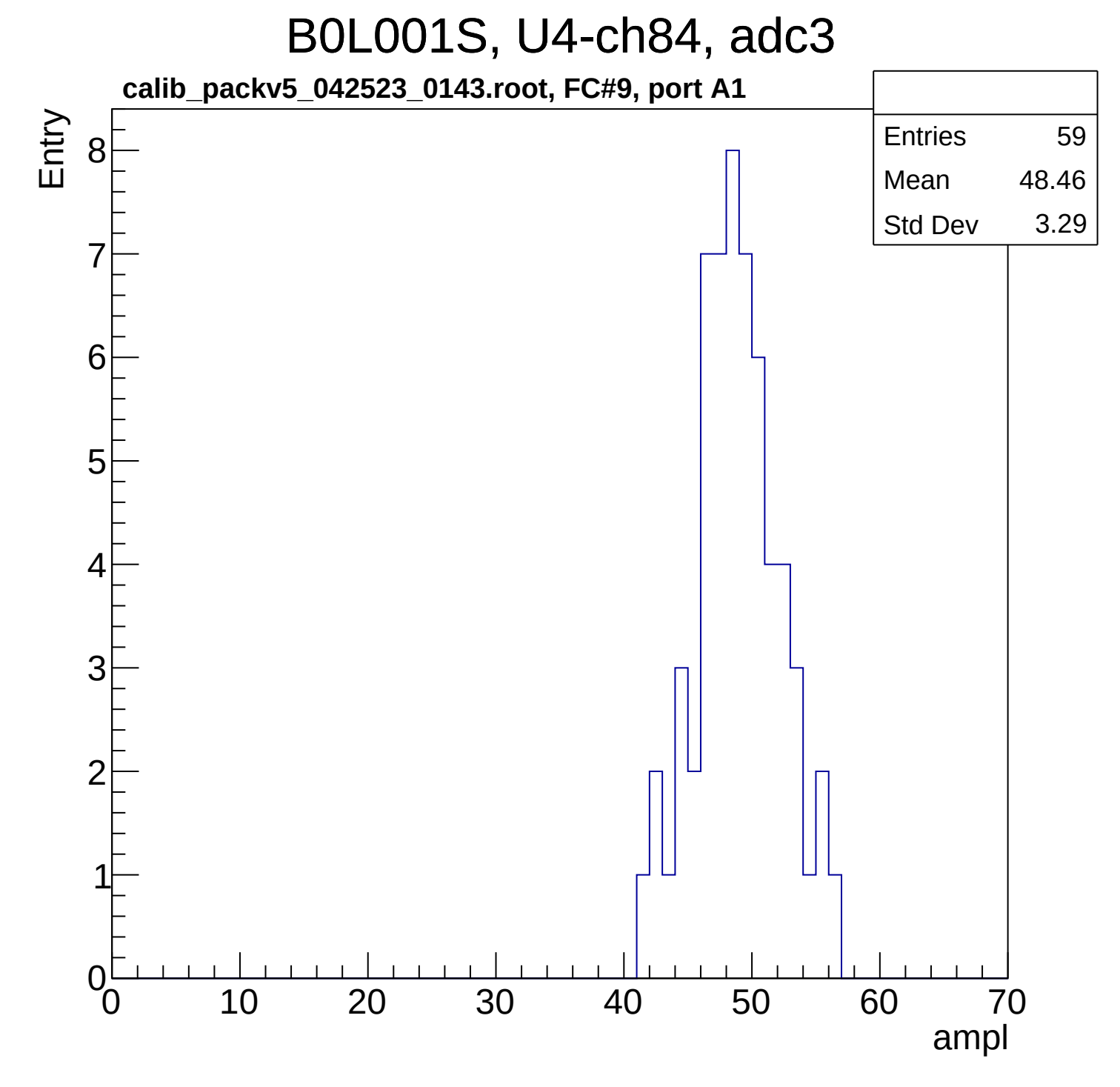
B0L001S, U4-ch84, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 48.46
Std Dev 3.29
Entry
ampl
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4
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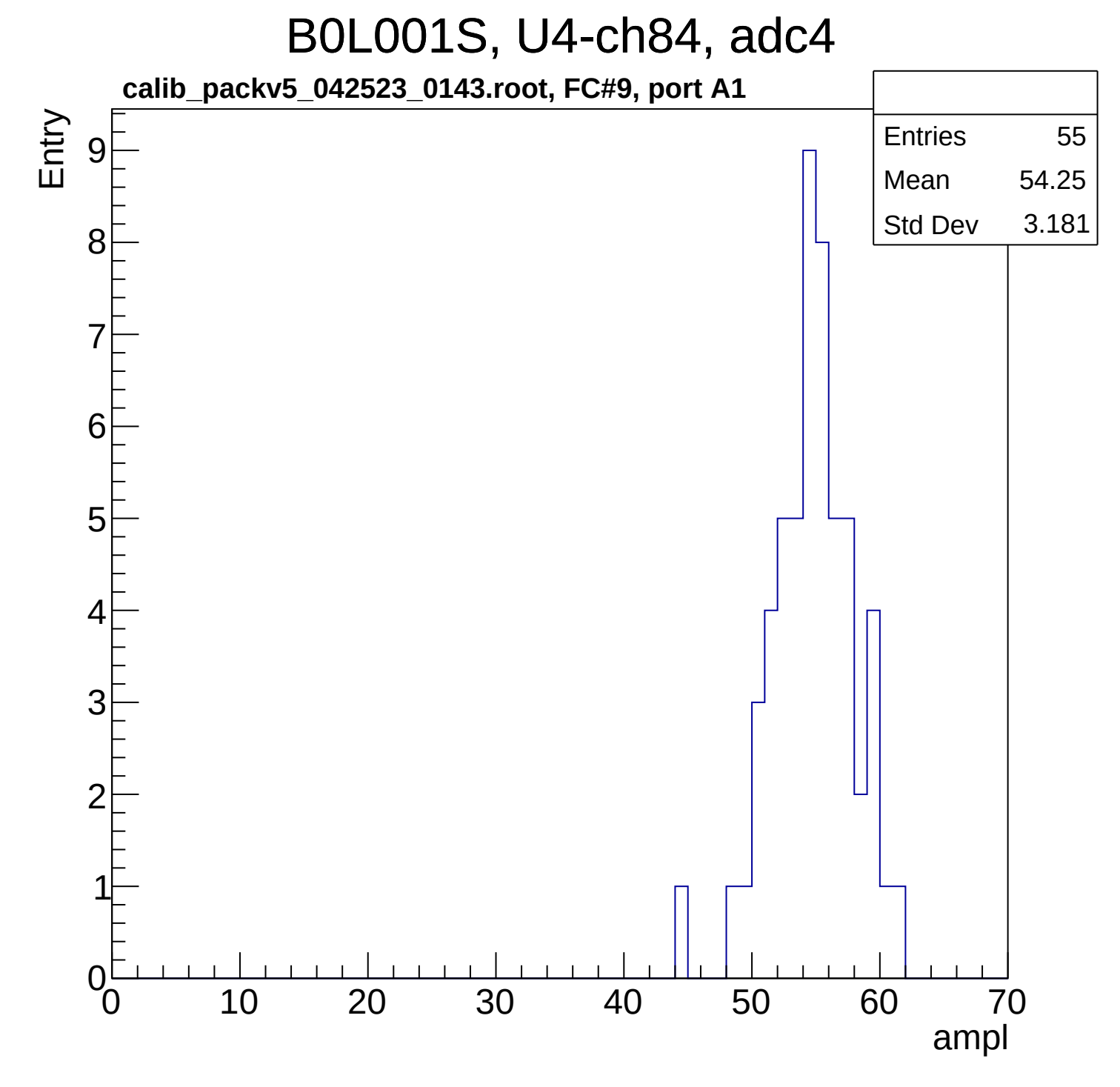
B0L001S, U4-ch84, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 54.25
Std Dev 3.181
Entry
ampl
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3
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0
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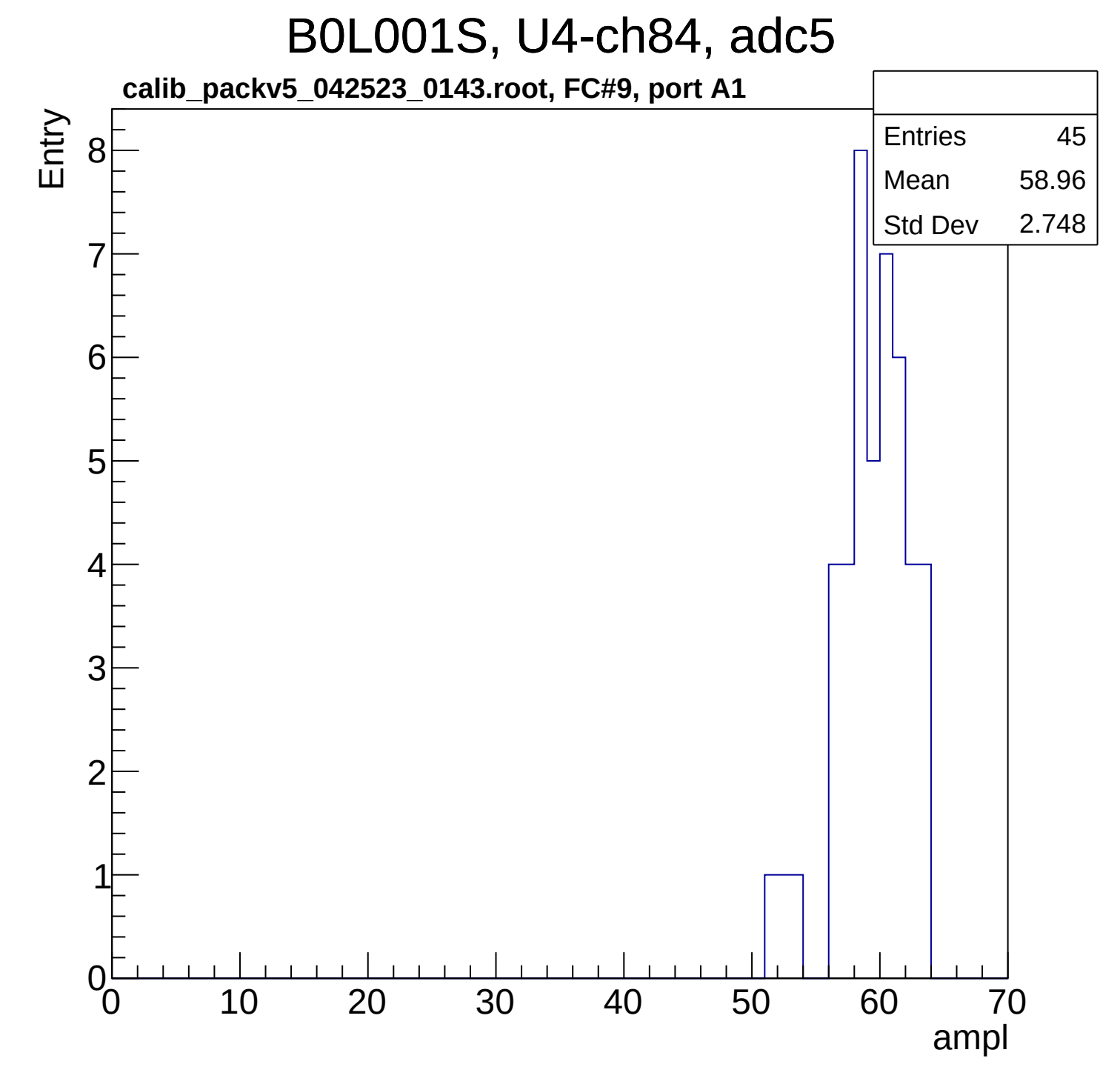
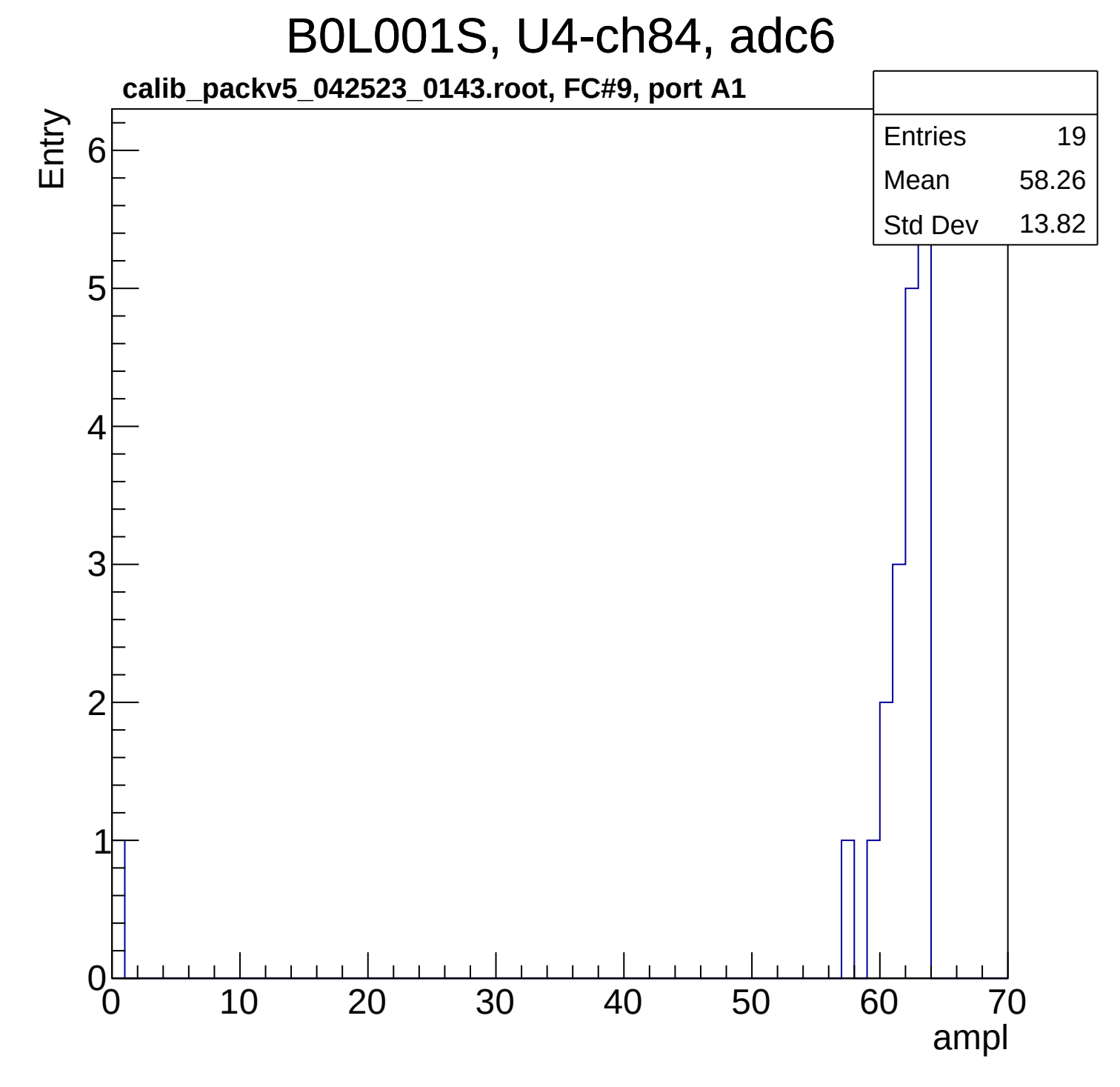

B0L001S, U4-ch84, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.96
Std Dev 2.748
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch84, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 58.26
Std Dev 13.82
Entry
0
1
2
3
4
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6
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10
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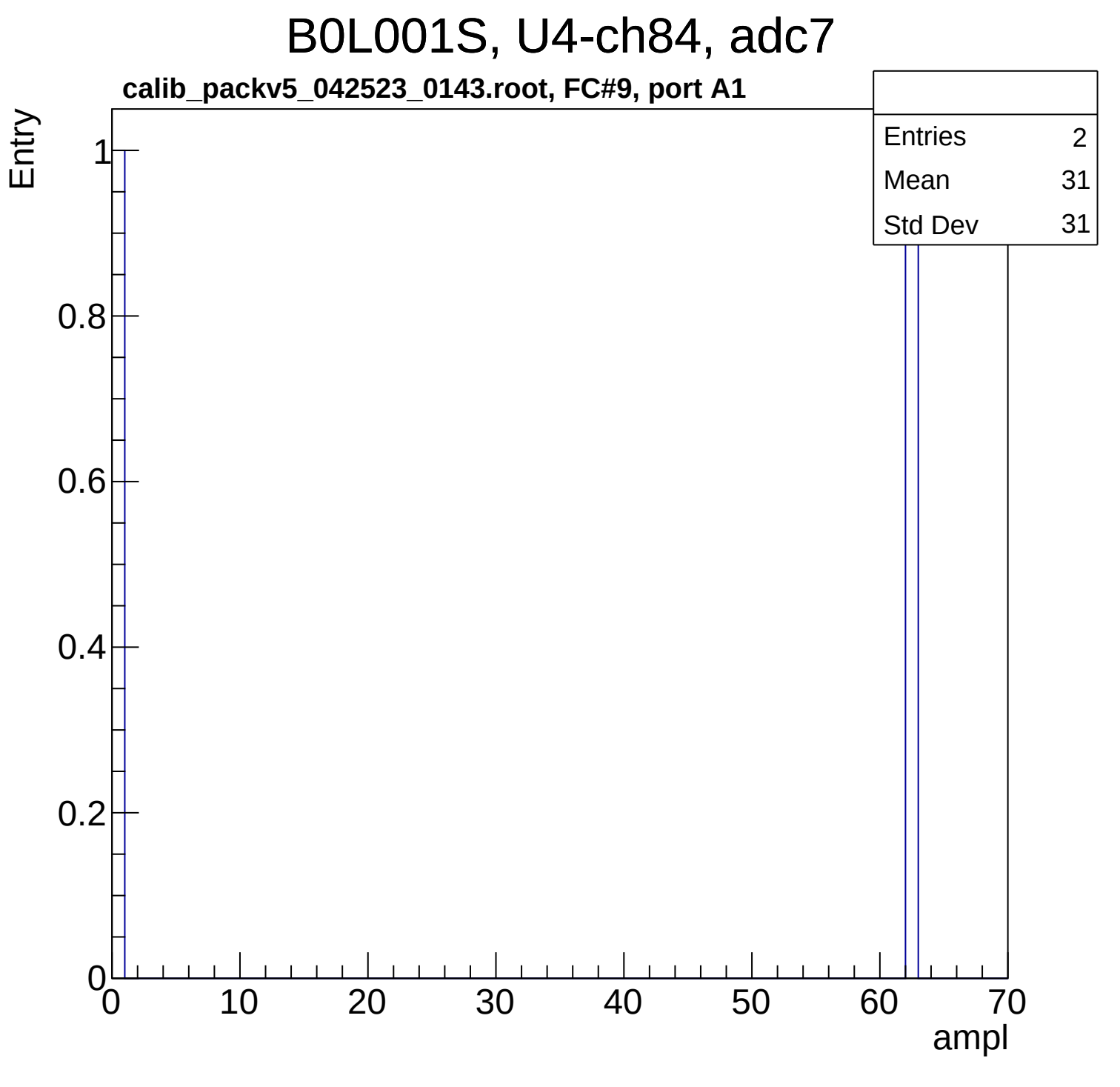
ampl


B0L001S, U4-ch84, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
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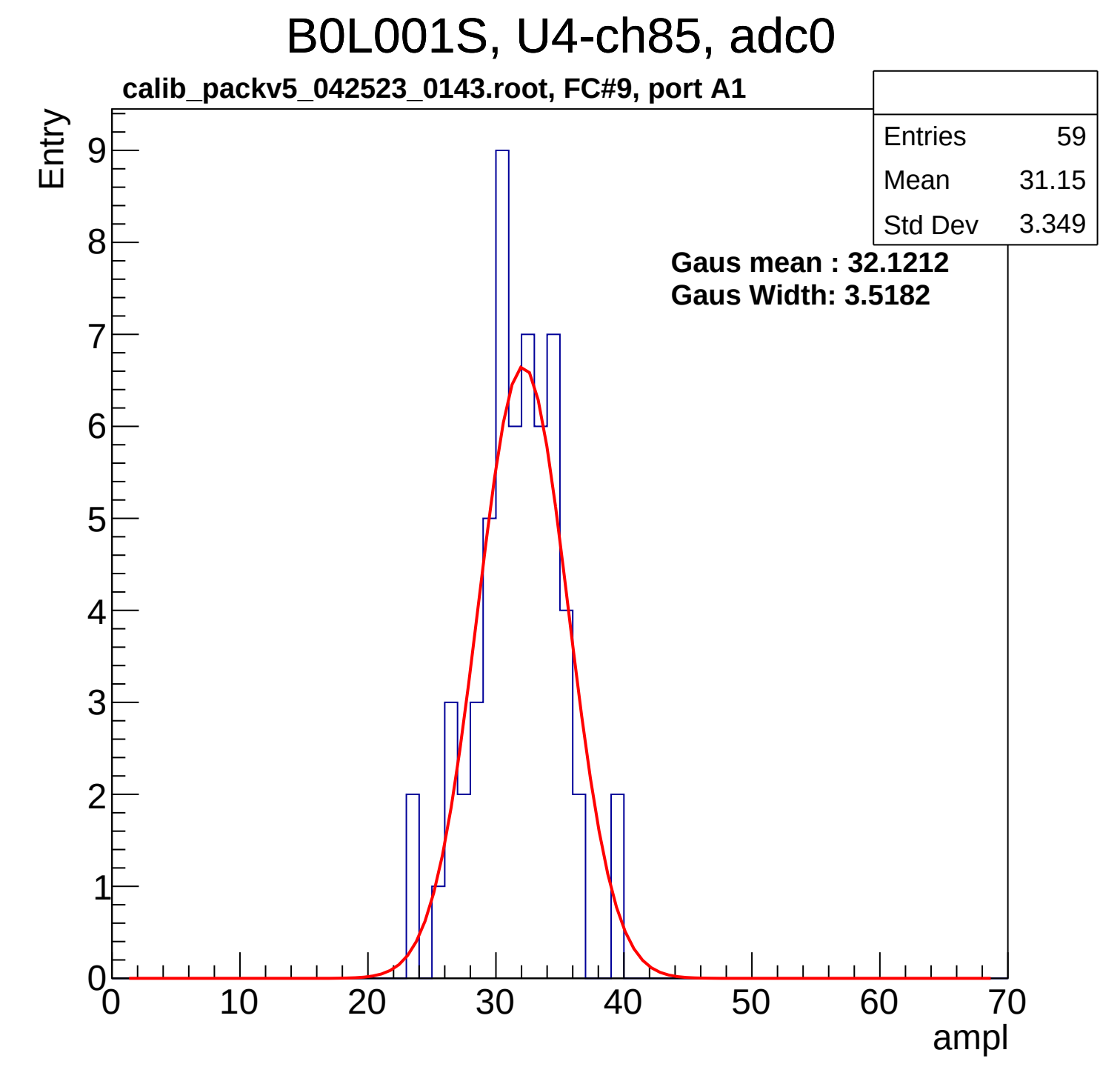
ampl


B0L001S, U4-ch85, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 31.15
Std Dev 3.349
Gaus mean : 32.1212
Gaus Width: 3.5182
Entry
ampl
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3
4
5
6
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0
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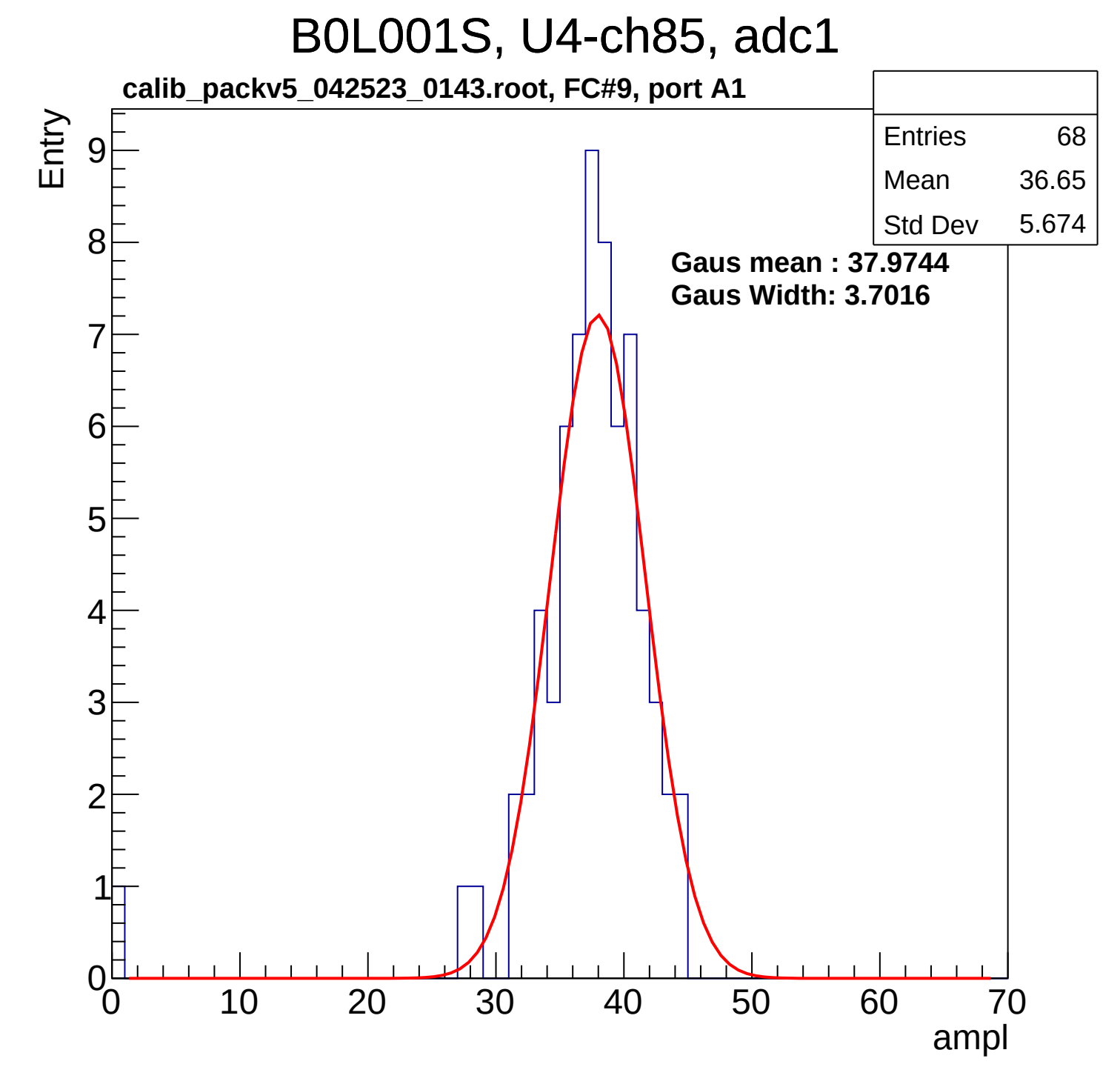
B0L001S, U4-ch85, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.65
Std Dev 5.674
Gaus mean : 37.9744
Gaus Width: 3.7016
Entry
ampl
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2
3
4
5
6
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0
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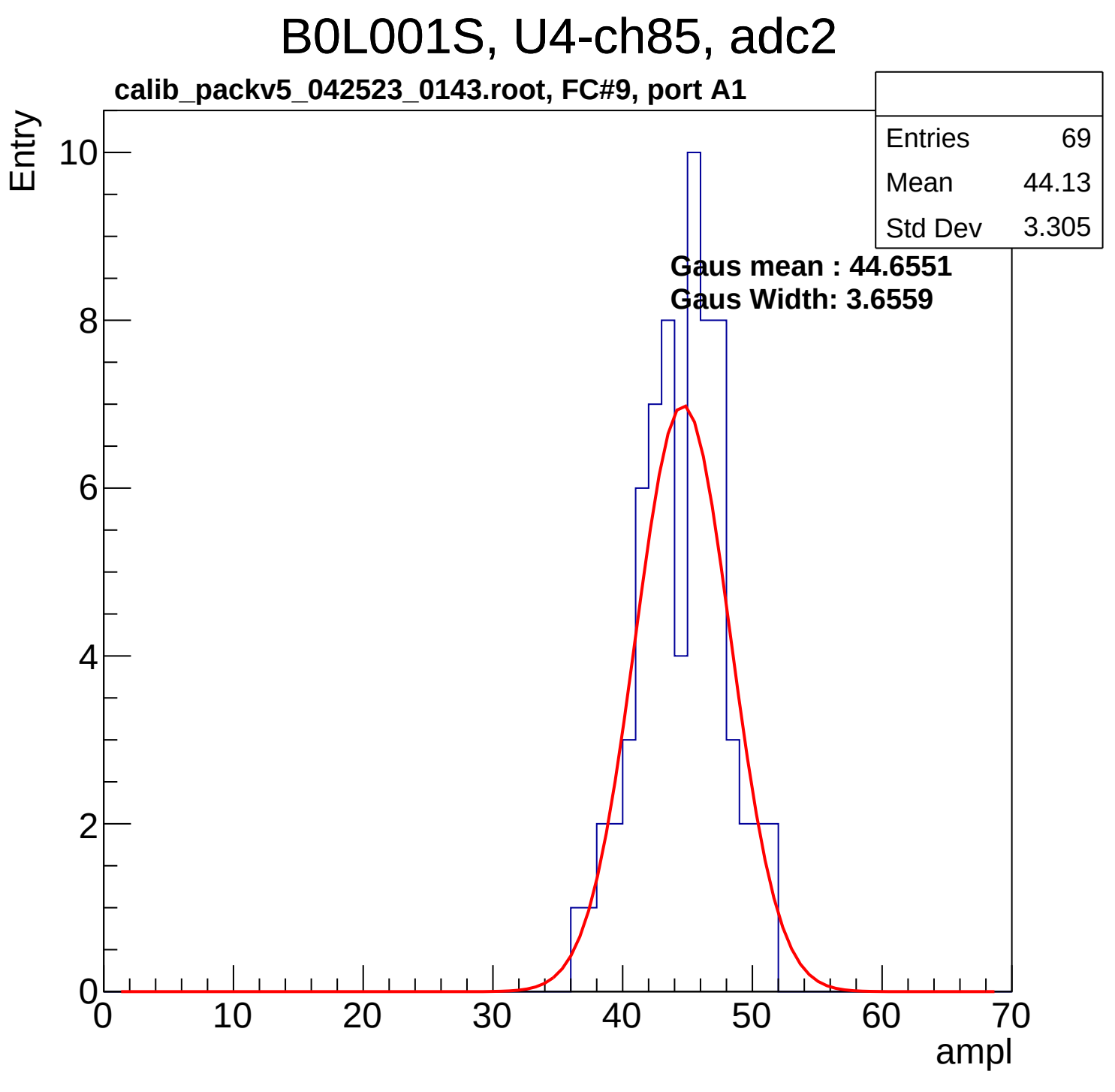
B0L001S, U4-ch85, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.13
Std Dev 3.305
Gaus mean : 44.6551
Gaus Width: 3.6559
Entry
ampl
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2
4
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0
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40
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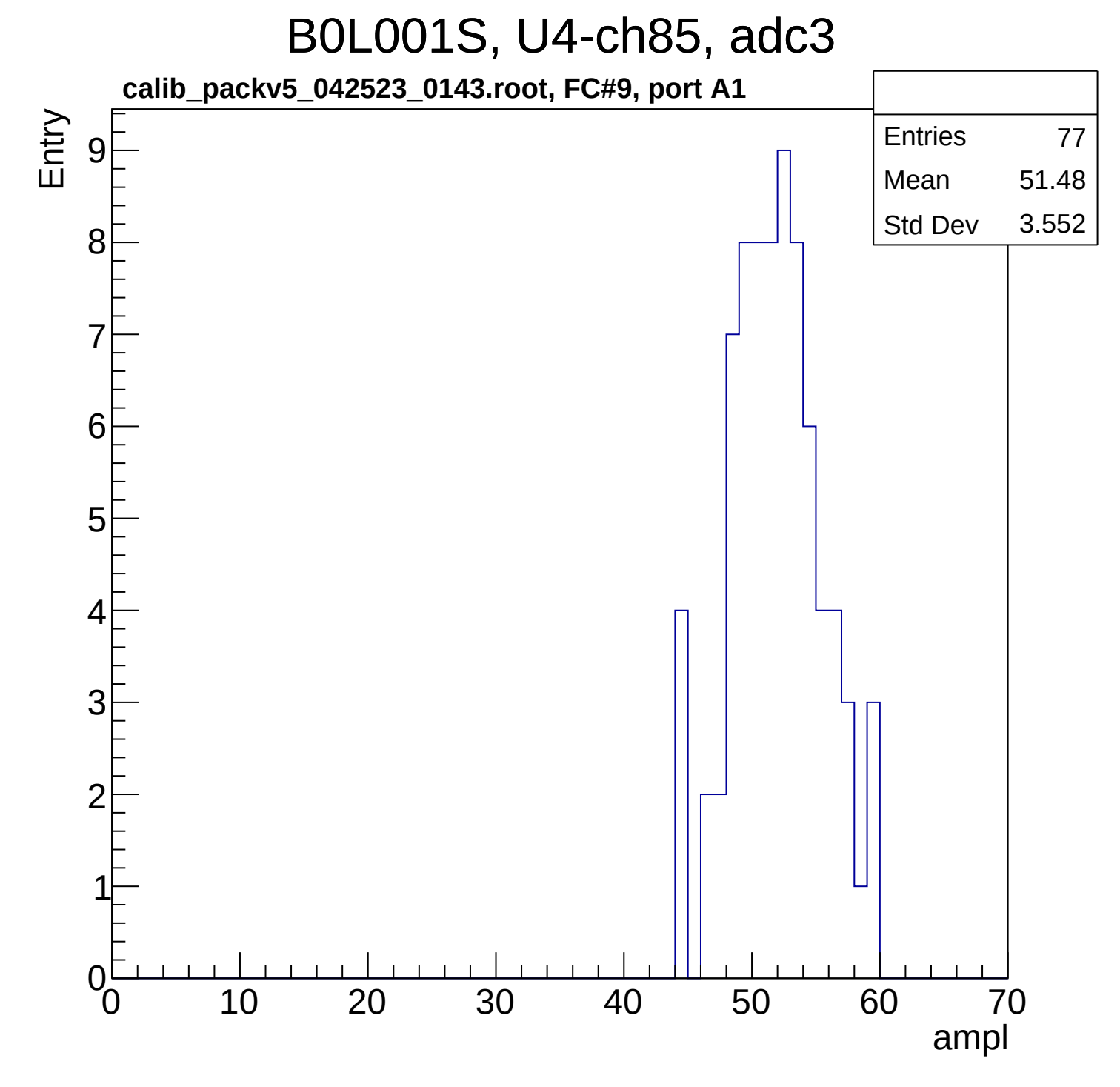
B0L001S, U4-ch85, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.48
Std Dev 3.552
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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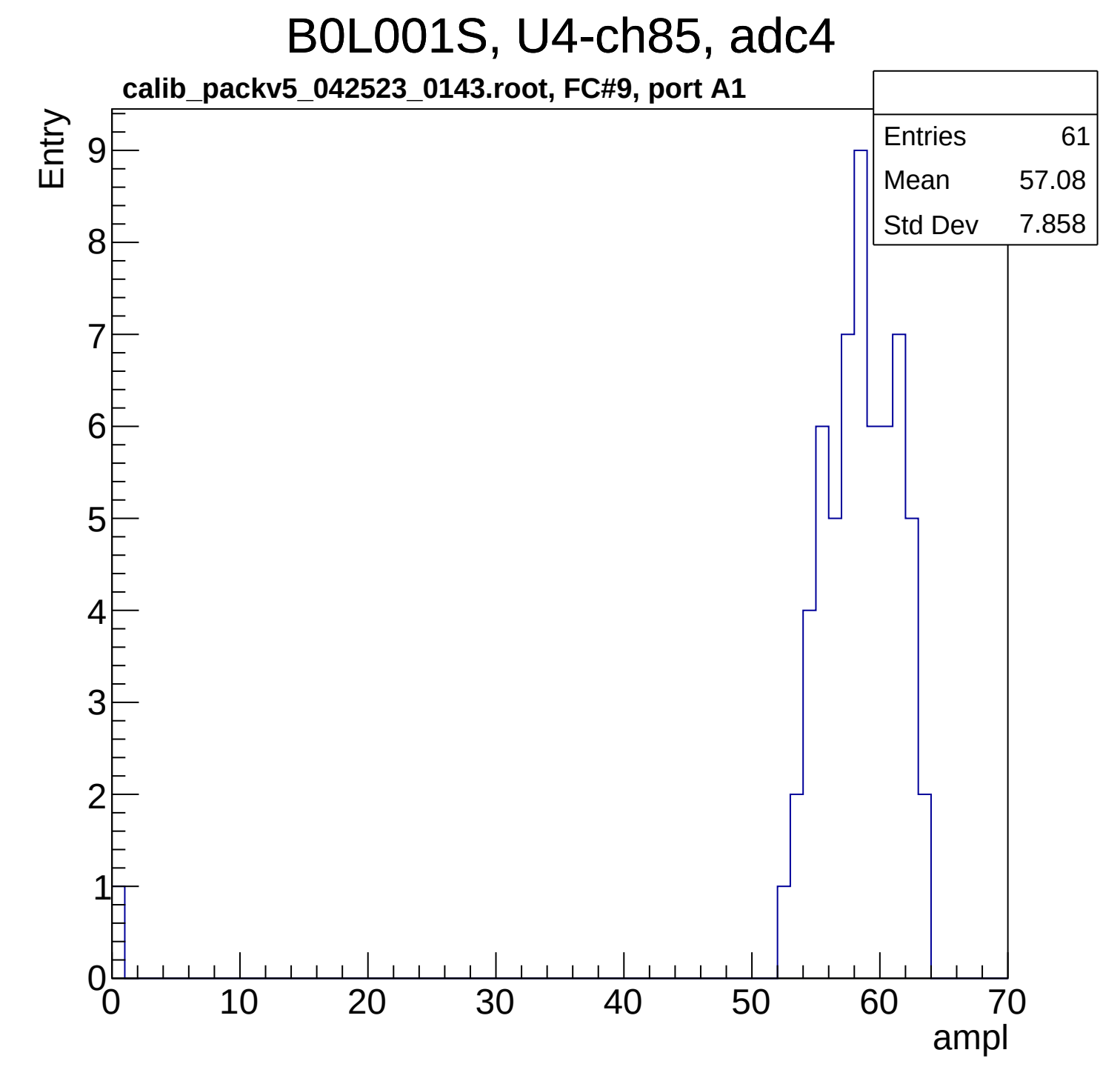
B0L001S, U4-ch85, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.08
Std Dev 7.858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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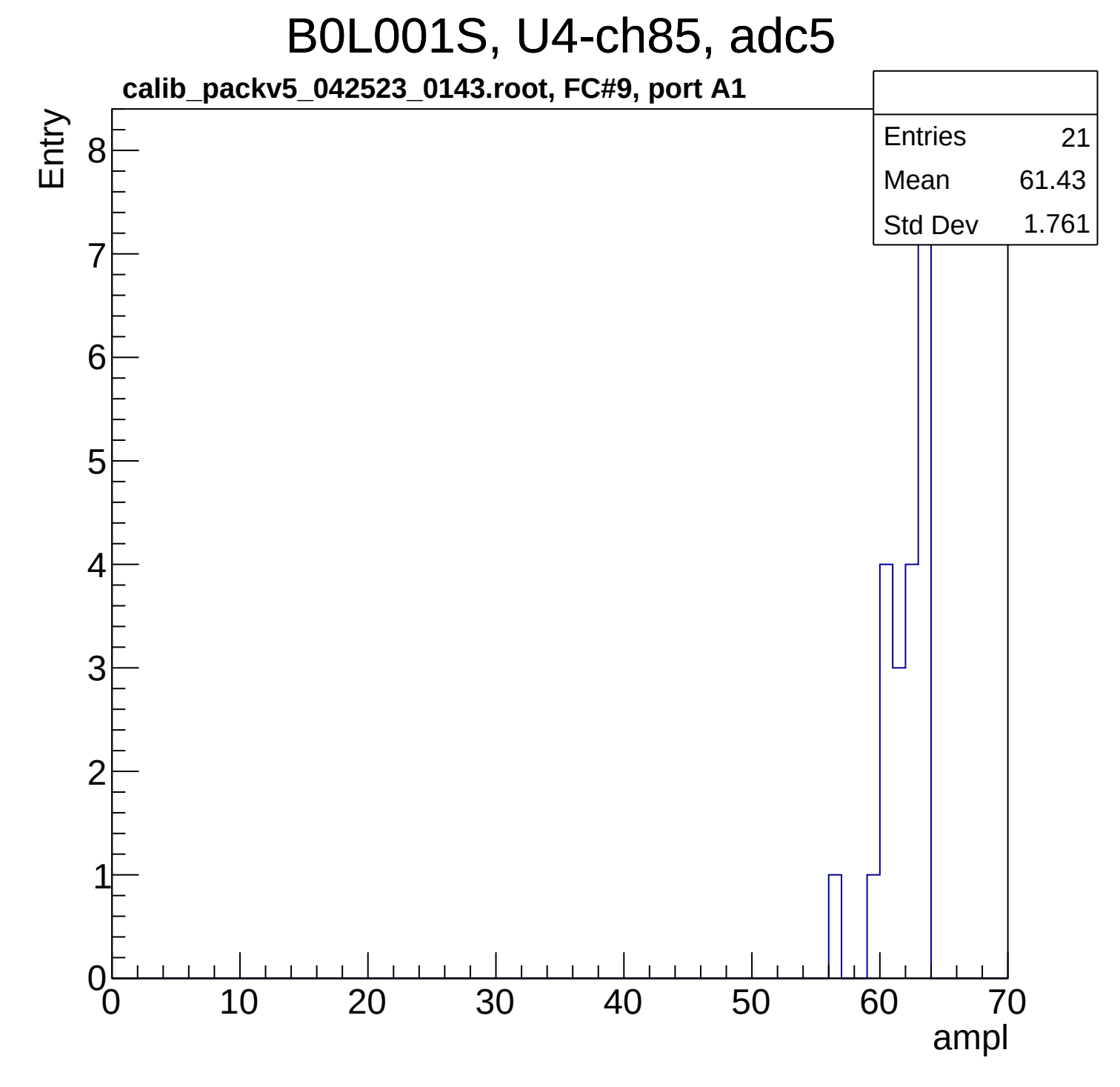
B0L001S, U4-ch85, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 61.43
Std Dev 1.761
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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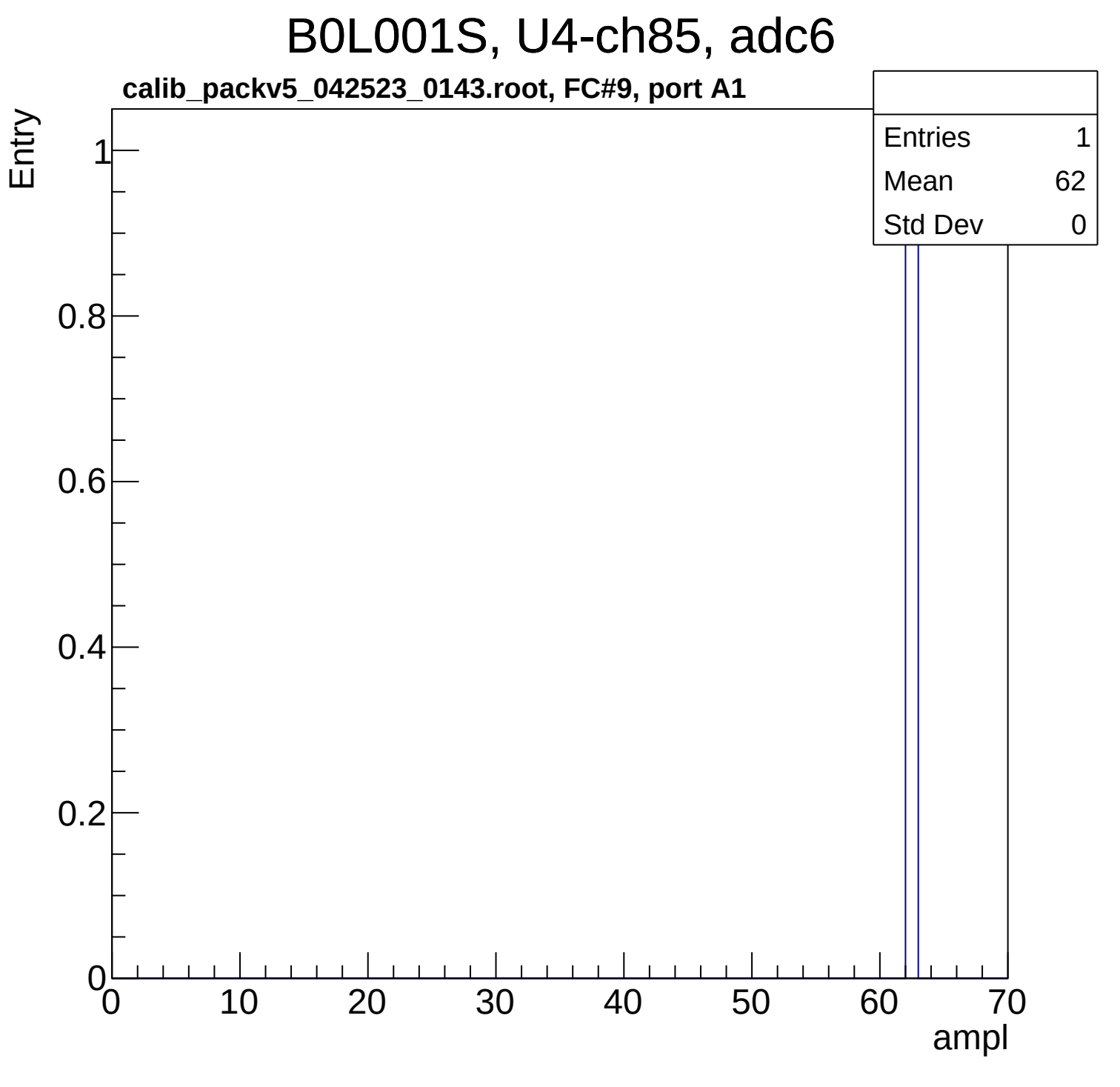
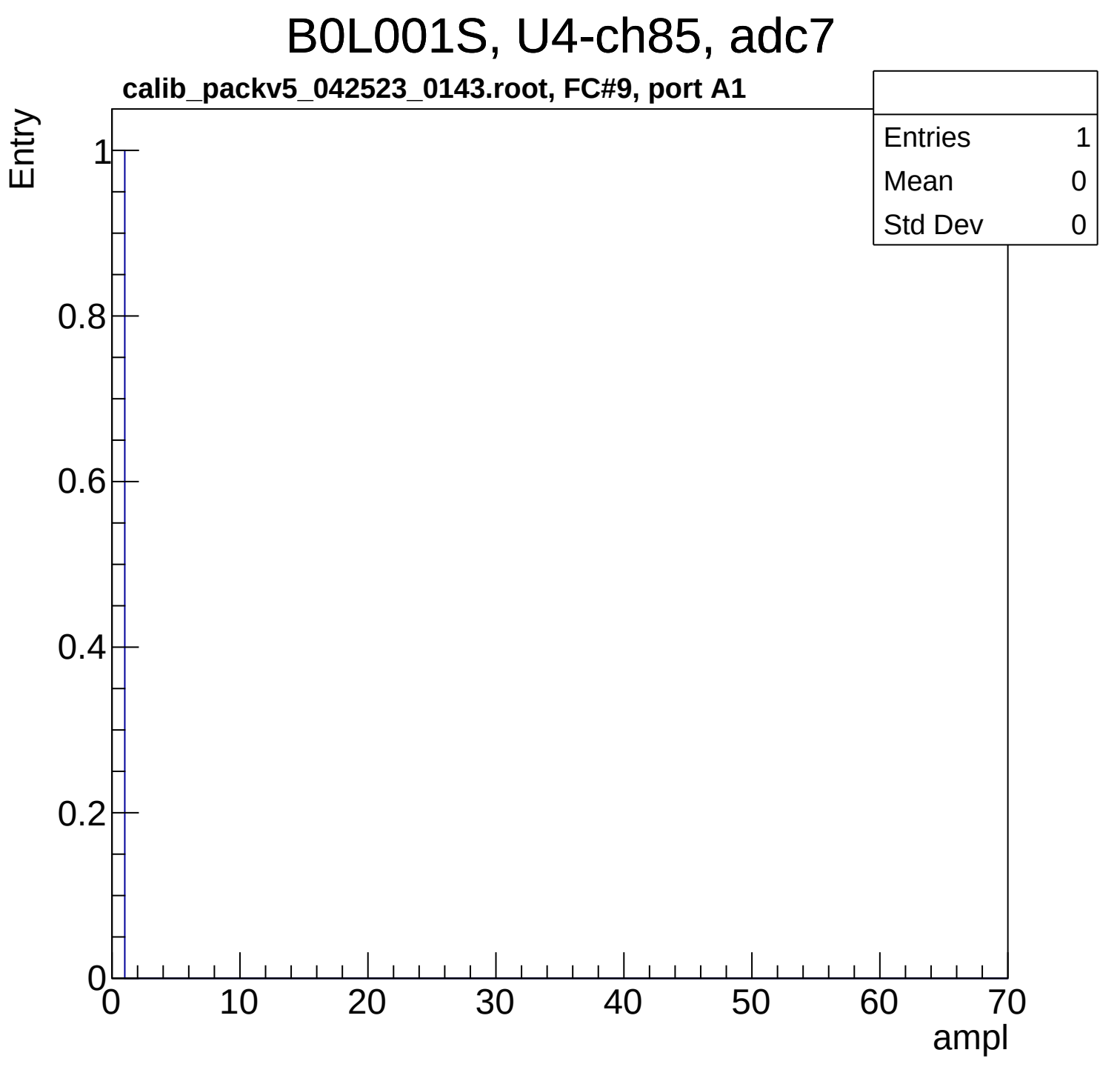
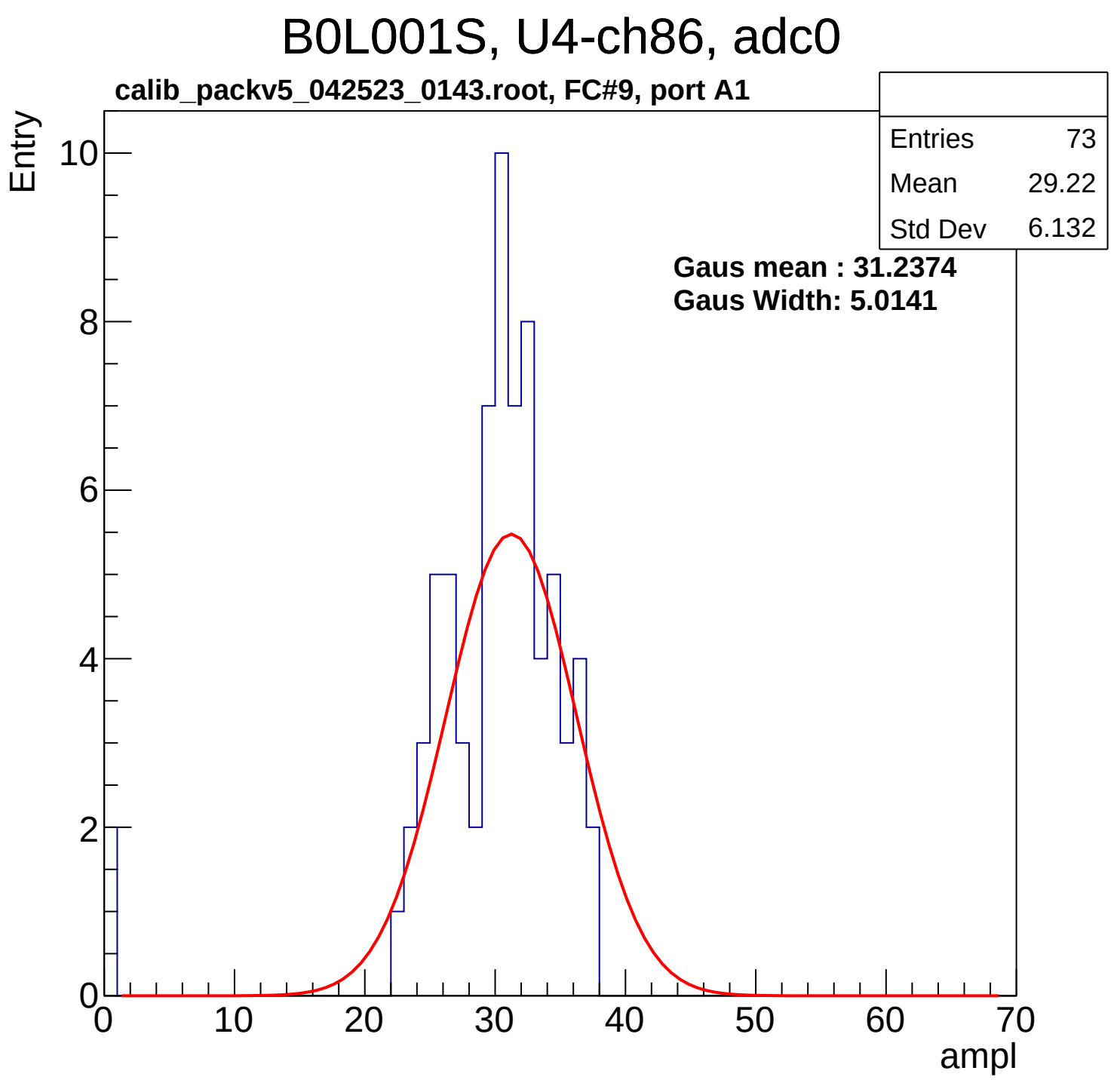
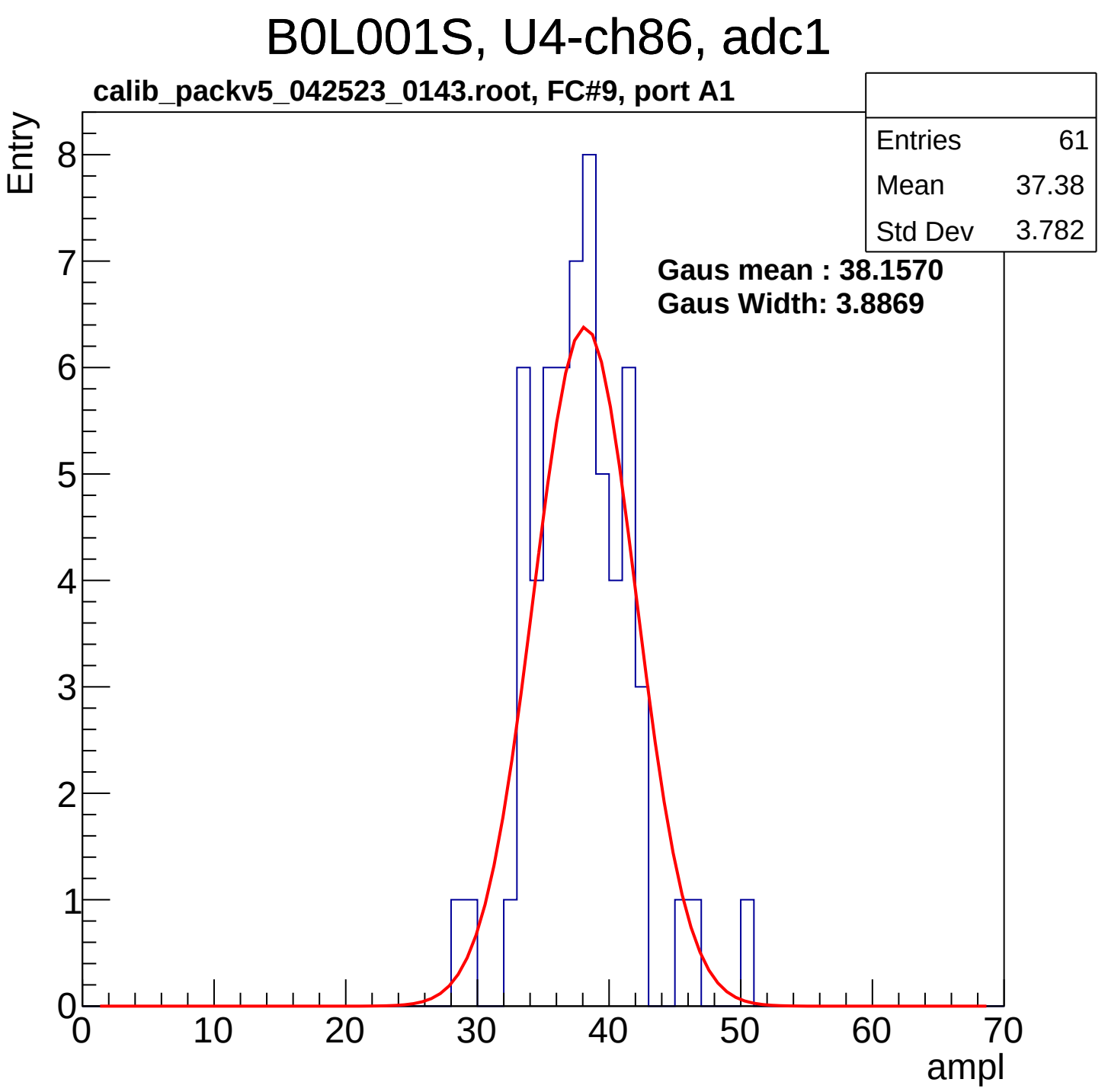

B0L001S, U4-ch85, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch85, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch86, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.22
Std Dev 6.132
Gaus mean : 31.2374
Gaus Width: 5.0141
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch86, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.38
Std Dev 3.782
Gaus mean : 38.1570
Gaus Width: 3.8869
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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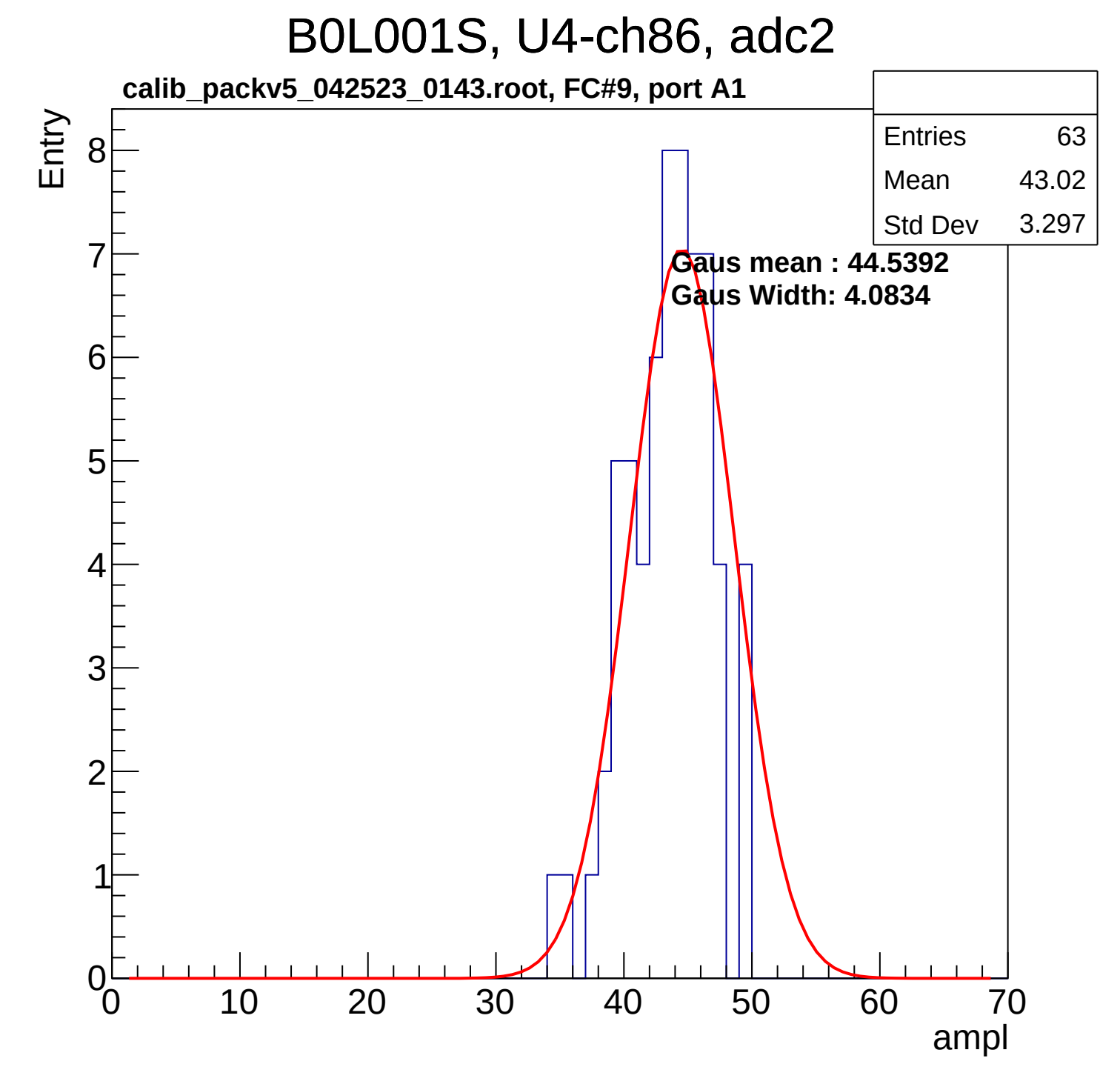
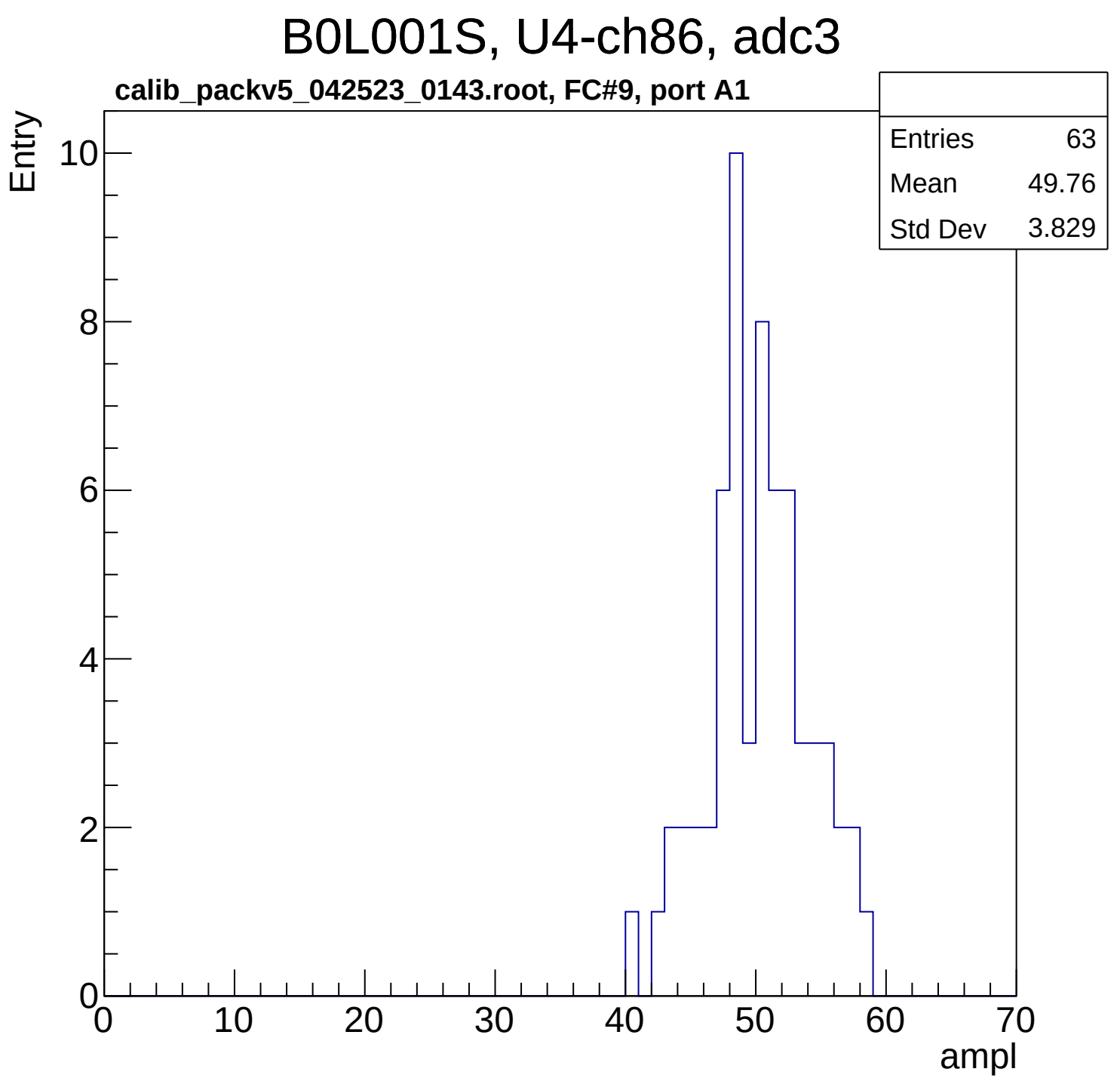
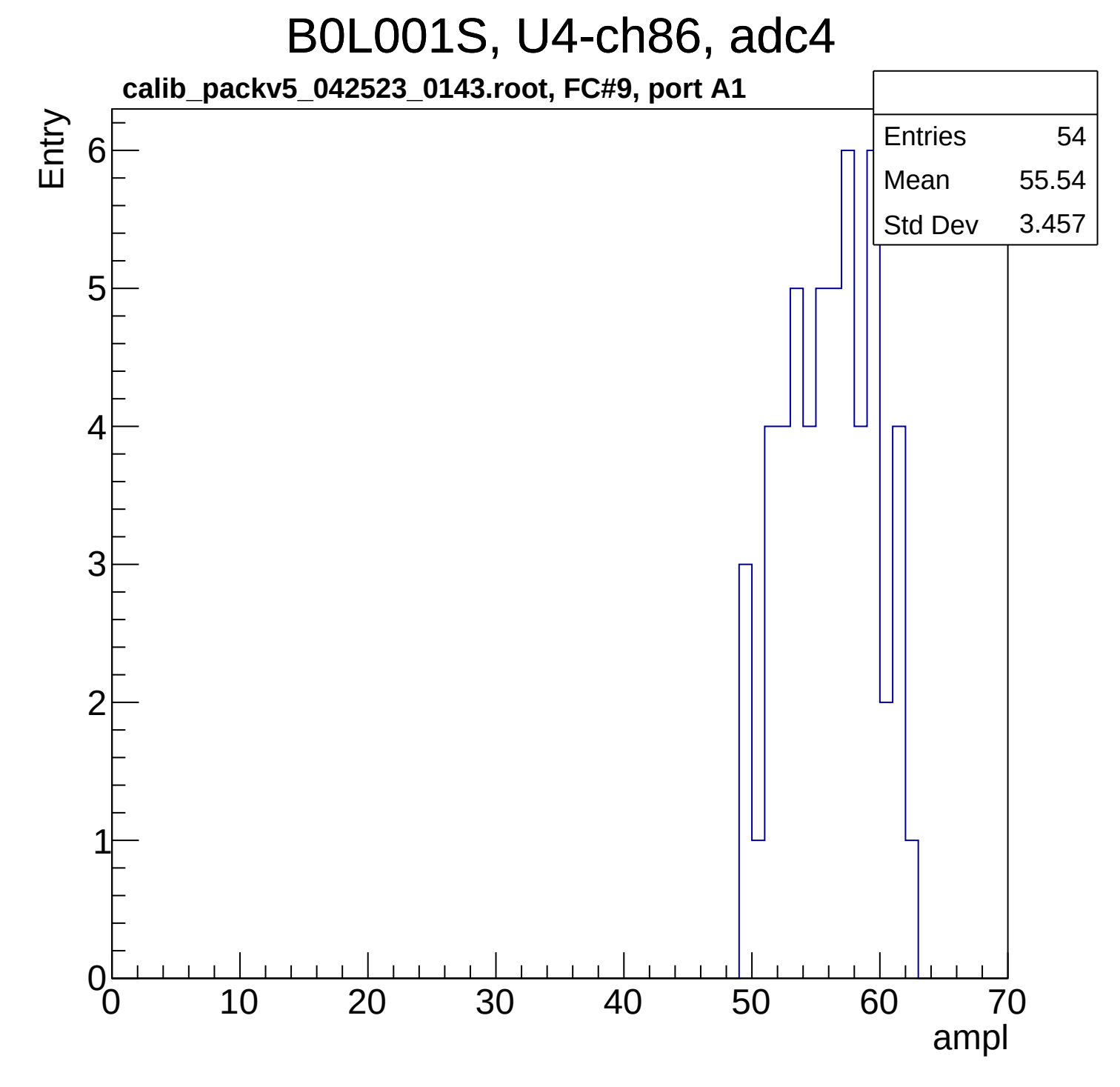
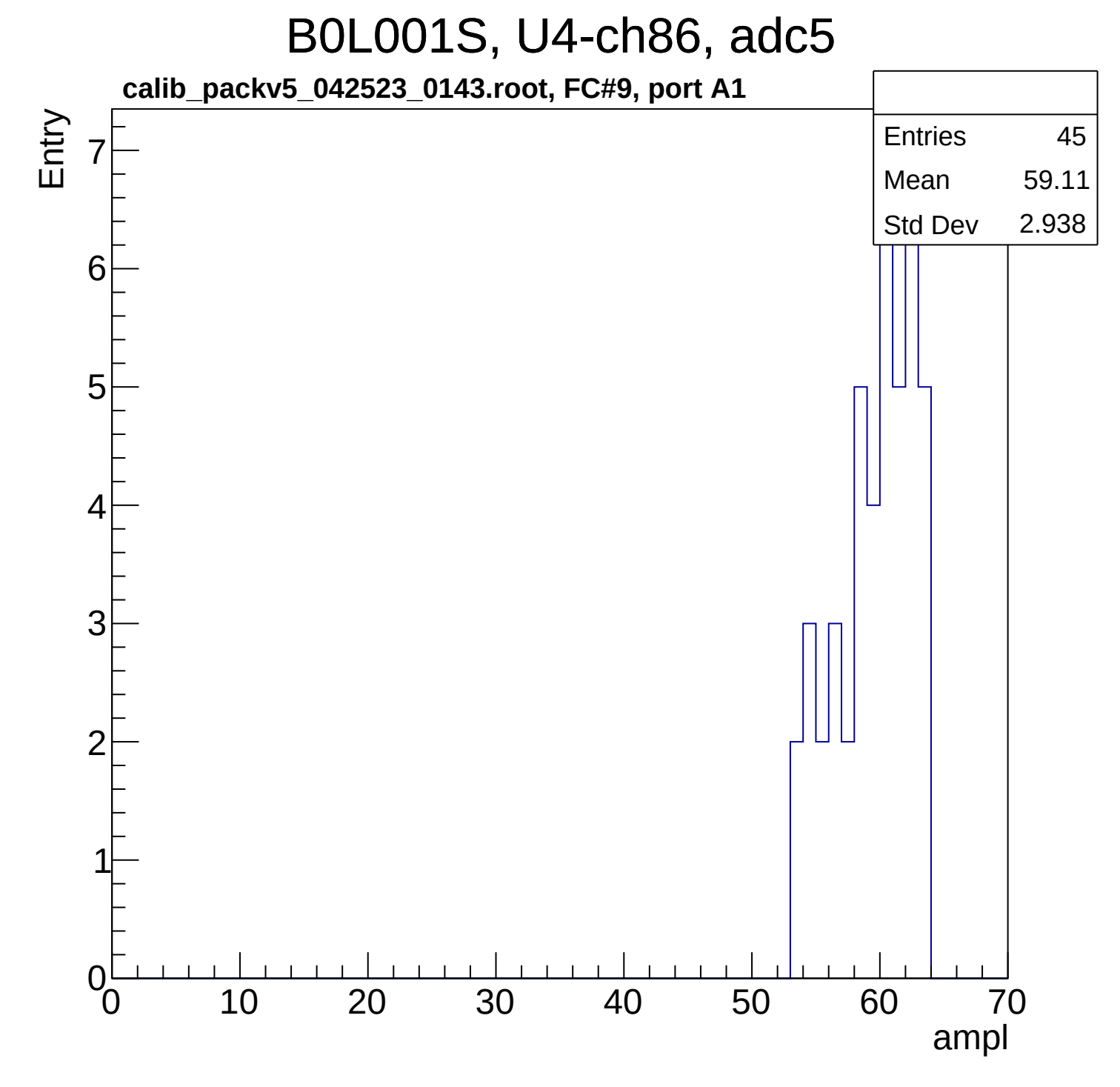
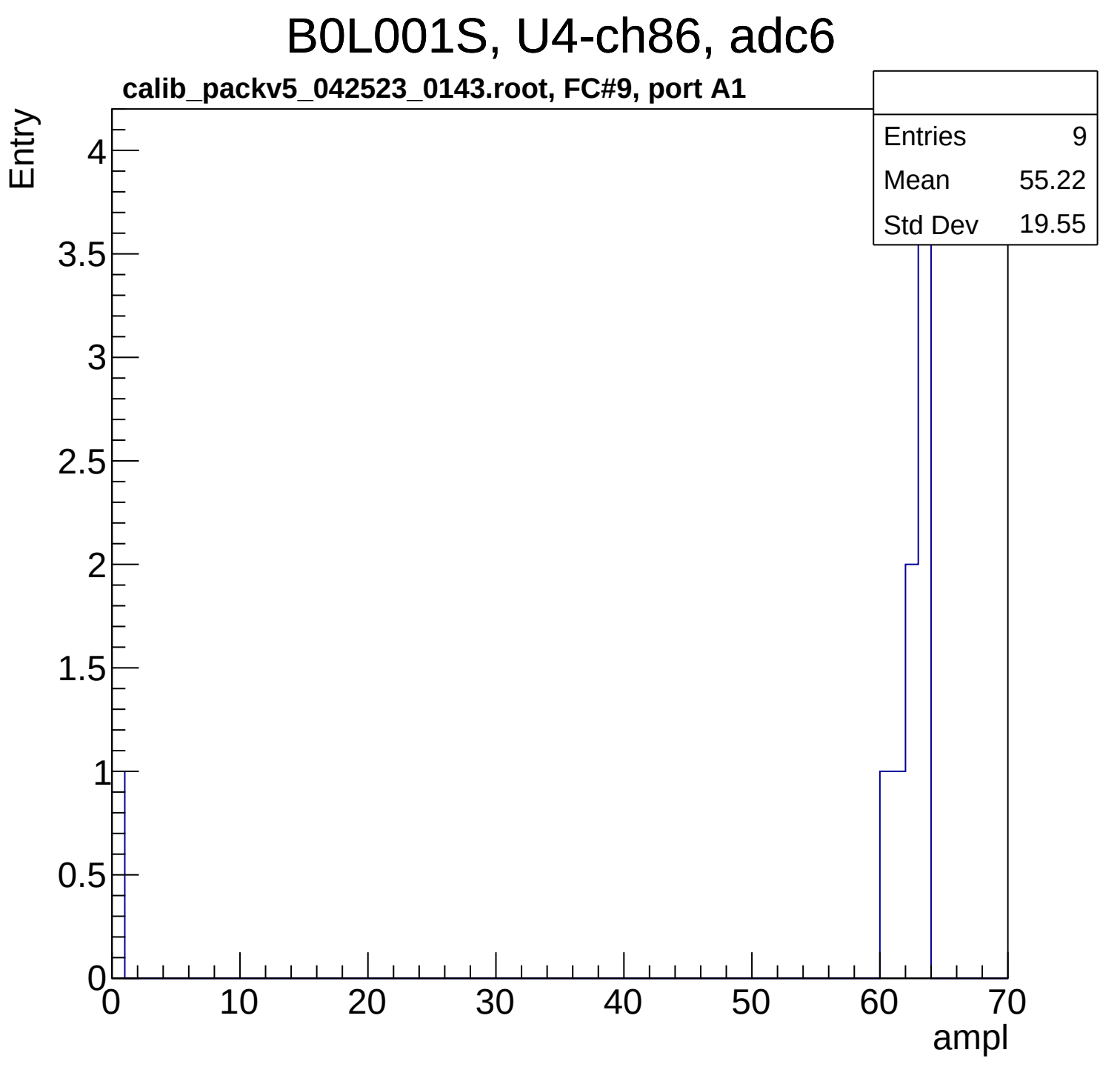

B0L001S, U4-ch86, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.02
Std Dev 3.297
Gaus mean : 44.5392
Gaus Width: 4.0834
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch86, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 49.76
Std Dev 3.829
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch86, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.54
Std Dev 3.457
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U4-ch86, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 59.11
Std Dev 2.938
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch86, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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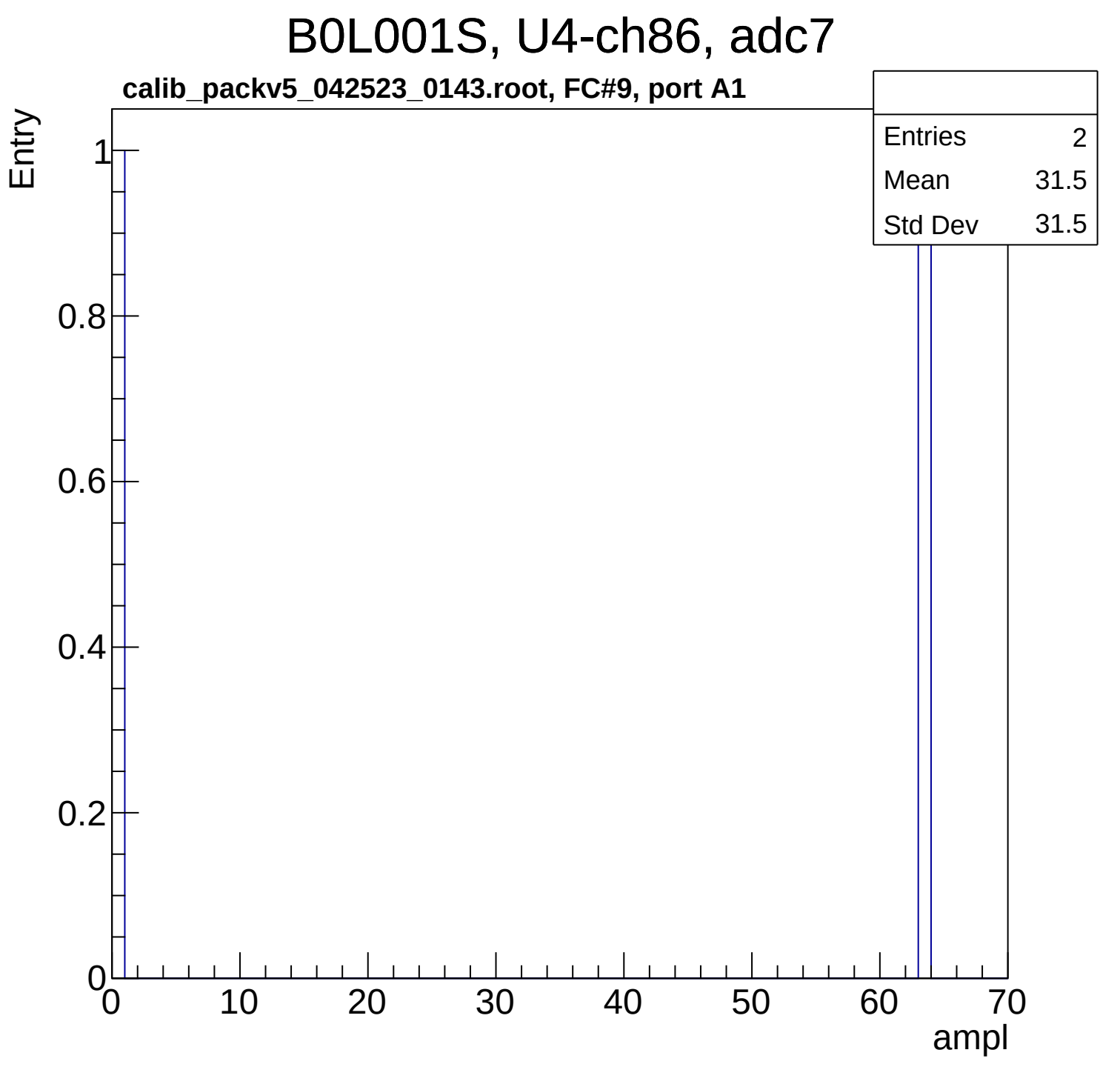
B0L001S, U4-ch86, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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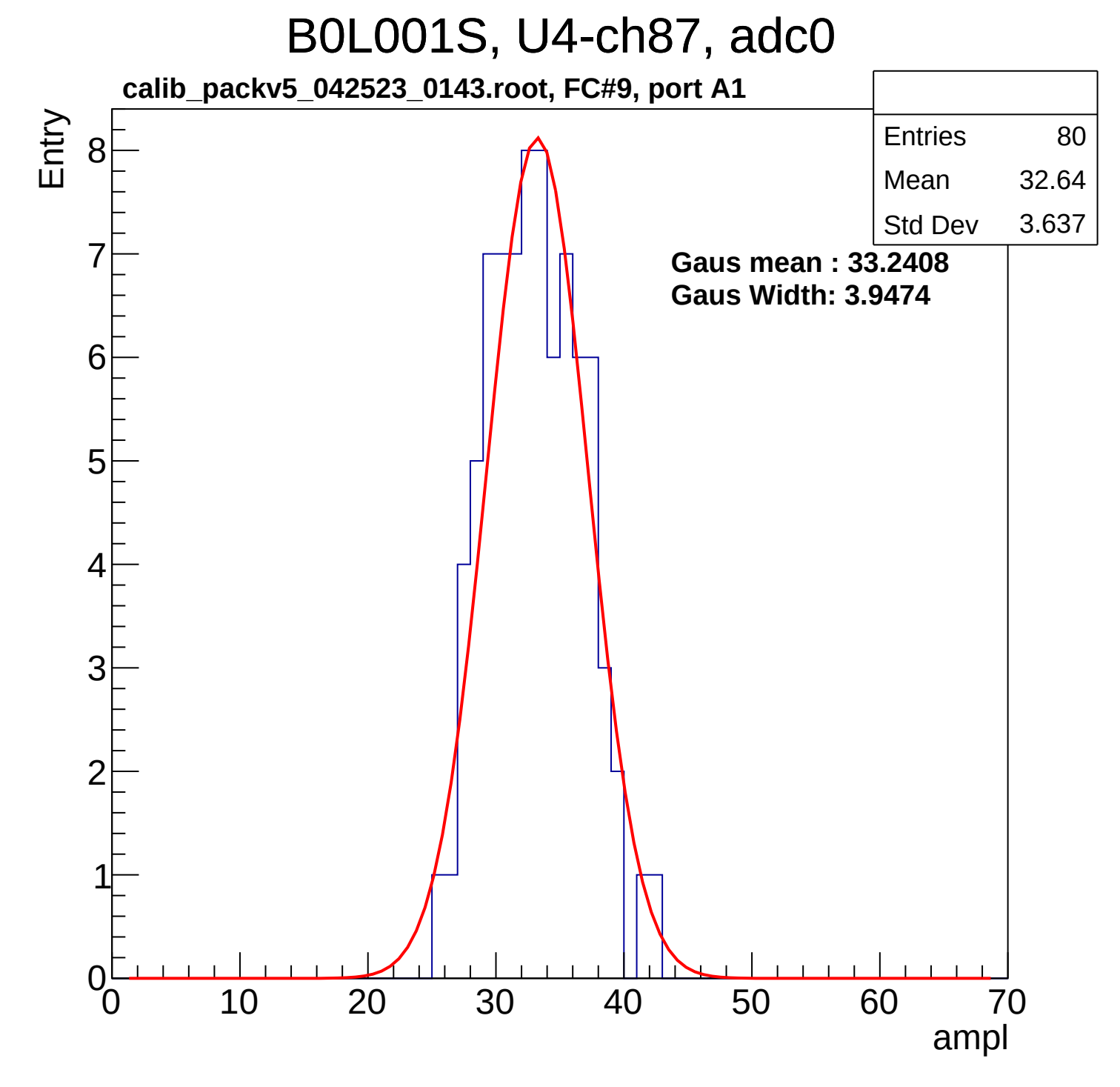
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch87, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 32.64
Std Dev 3.637
Gaus mean : 33.2408
Gaus Width: 3.9474
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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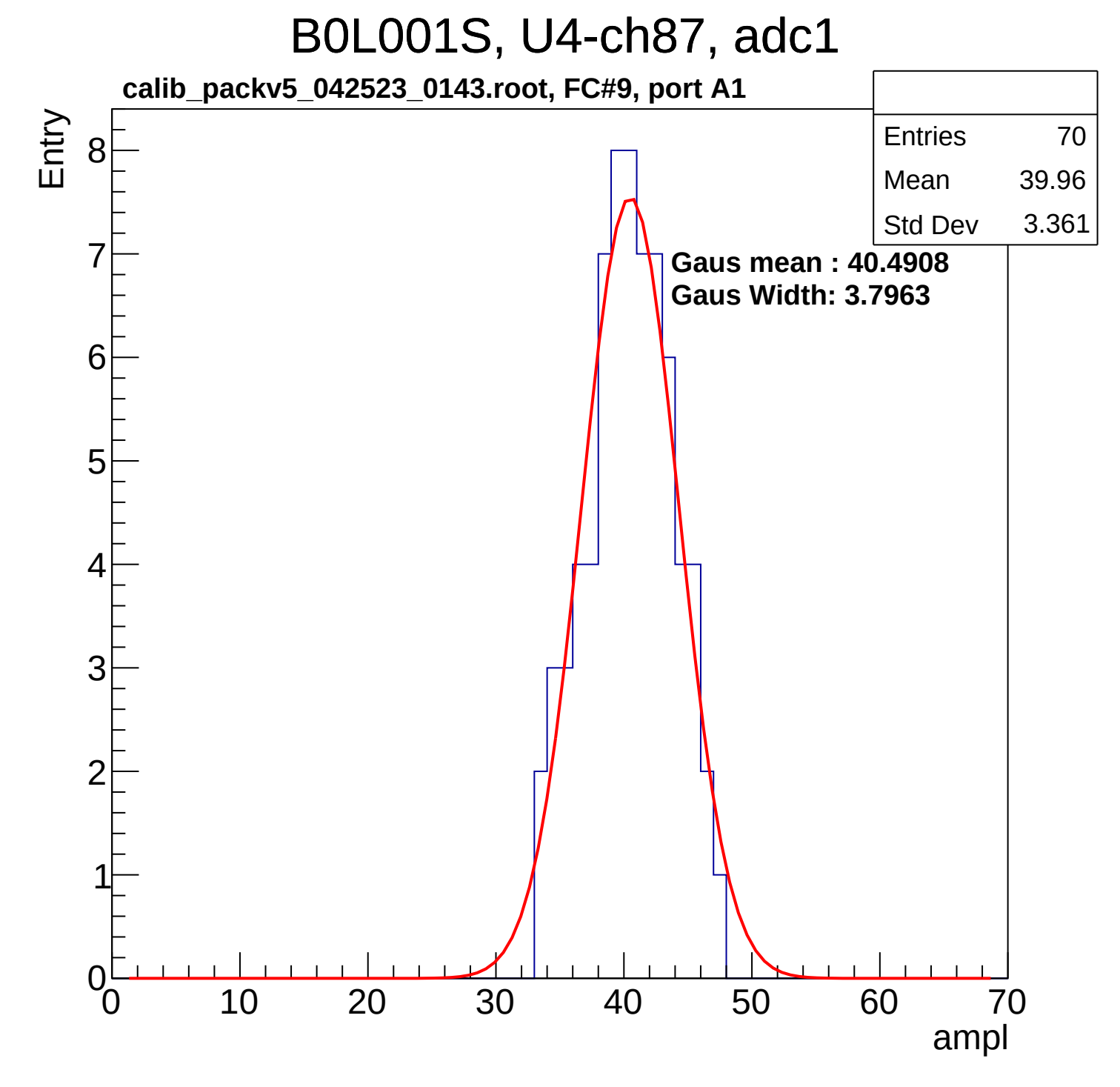
40
50
60
70


B0L001S, U4-ch87, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 39.96
Std Dev 3.361
Gaus mean : 40.4908
Gaus Width: 3.7963
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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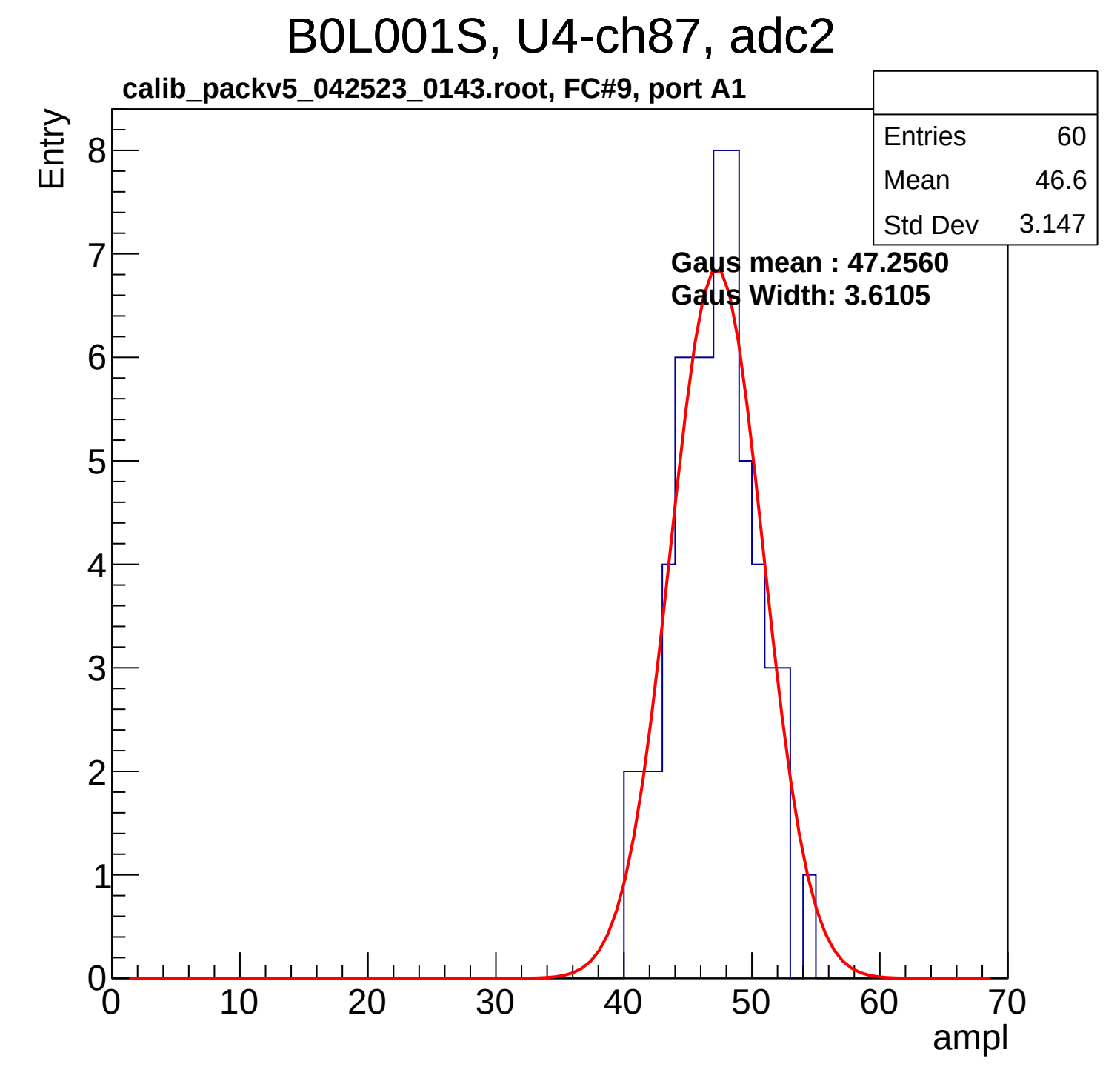
B0L001S, U4-ch87, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 46.6
Std Dev 3.147
Gaus mean : 47.2560
Gaus Width: 3.6105
Entry
ampl
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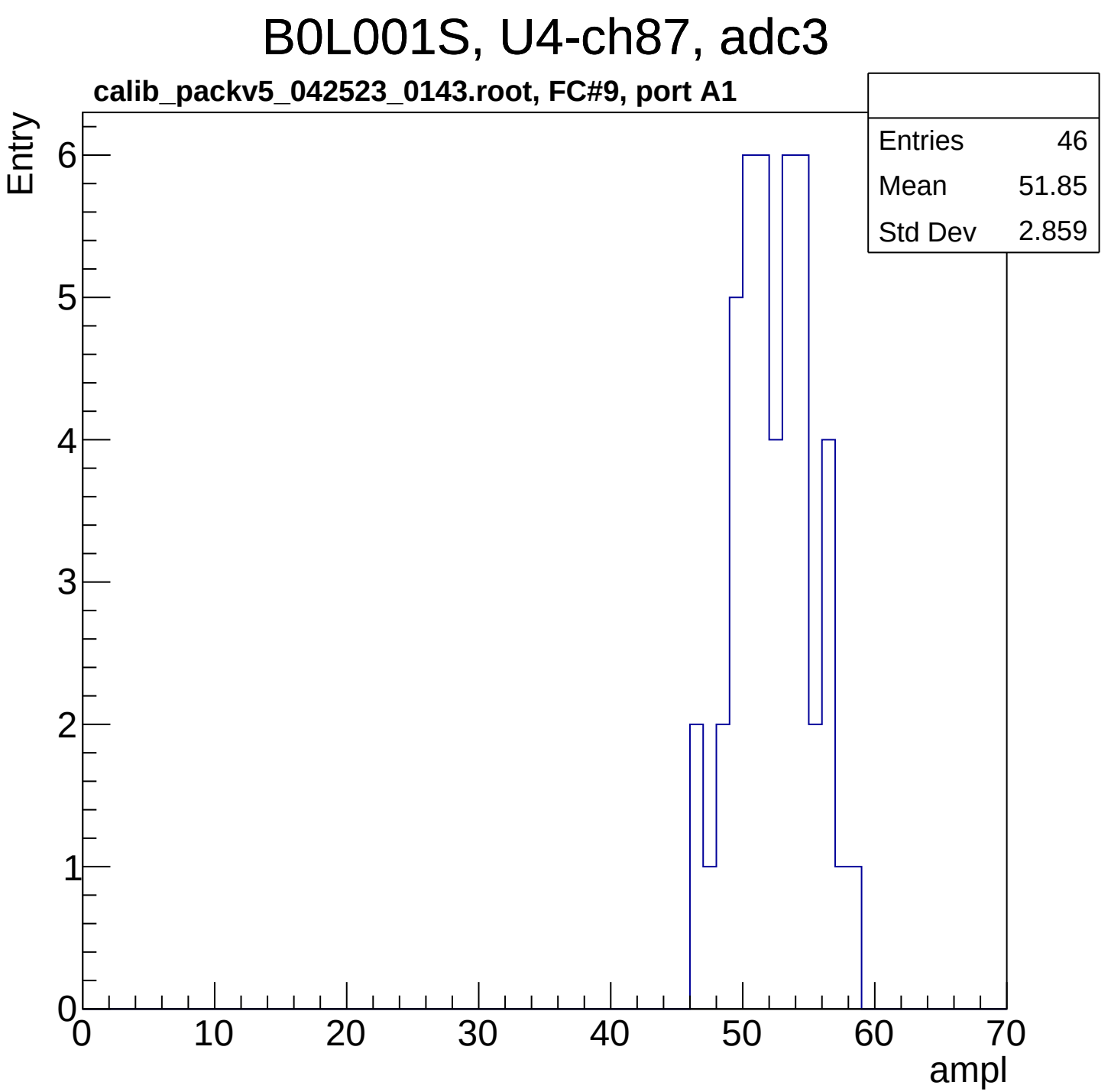
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch87, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 51.85
Std Dev 2.859
Entry
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6
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10
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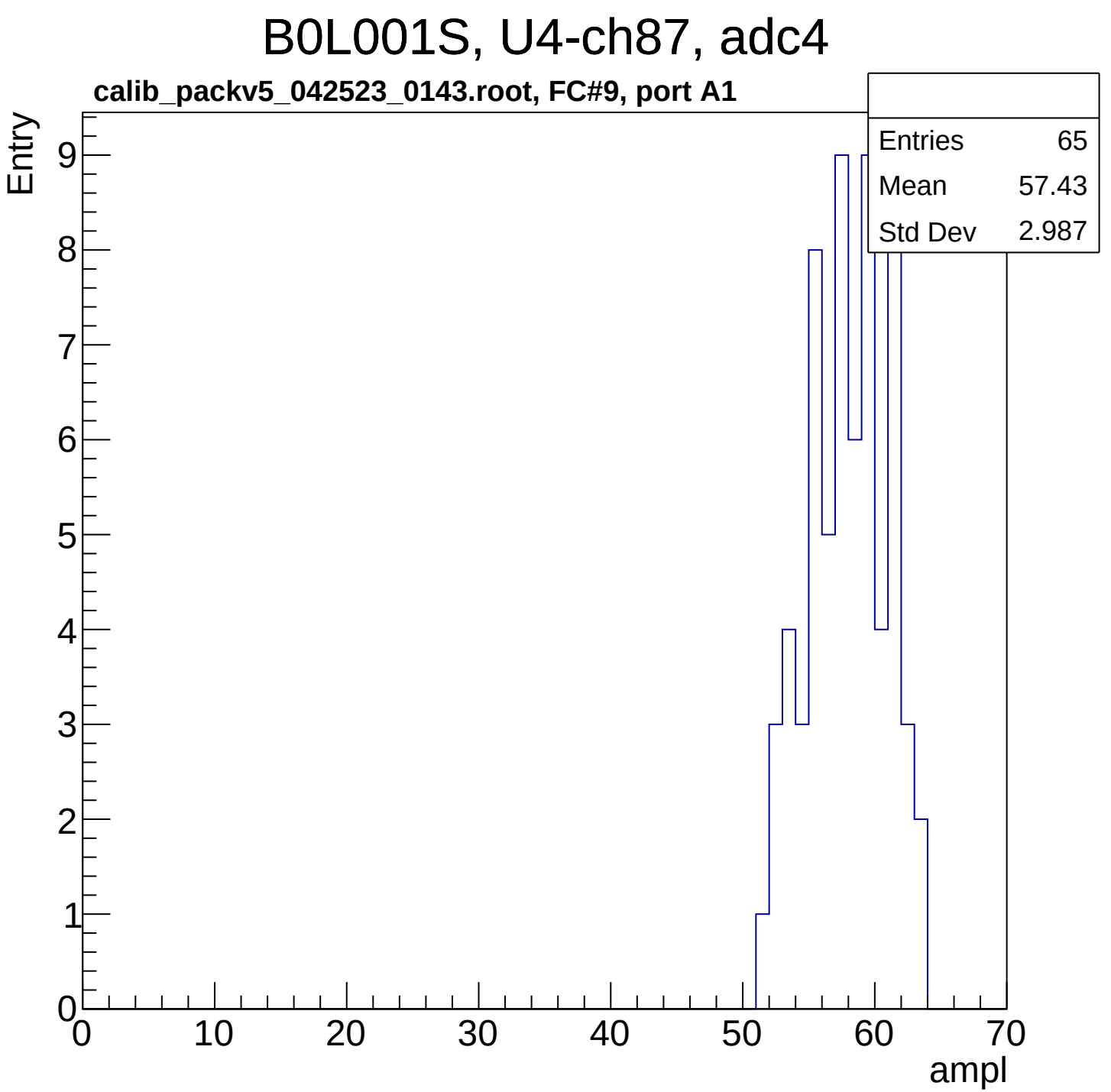
50
60
70
ampl


B0L001S, U4-ch87, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 57.43
Std Dev 2.987
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
50
60
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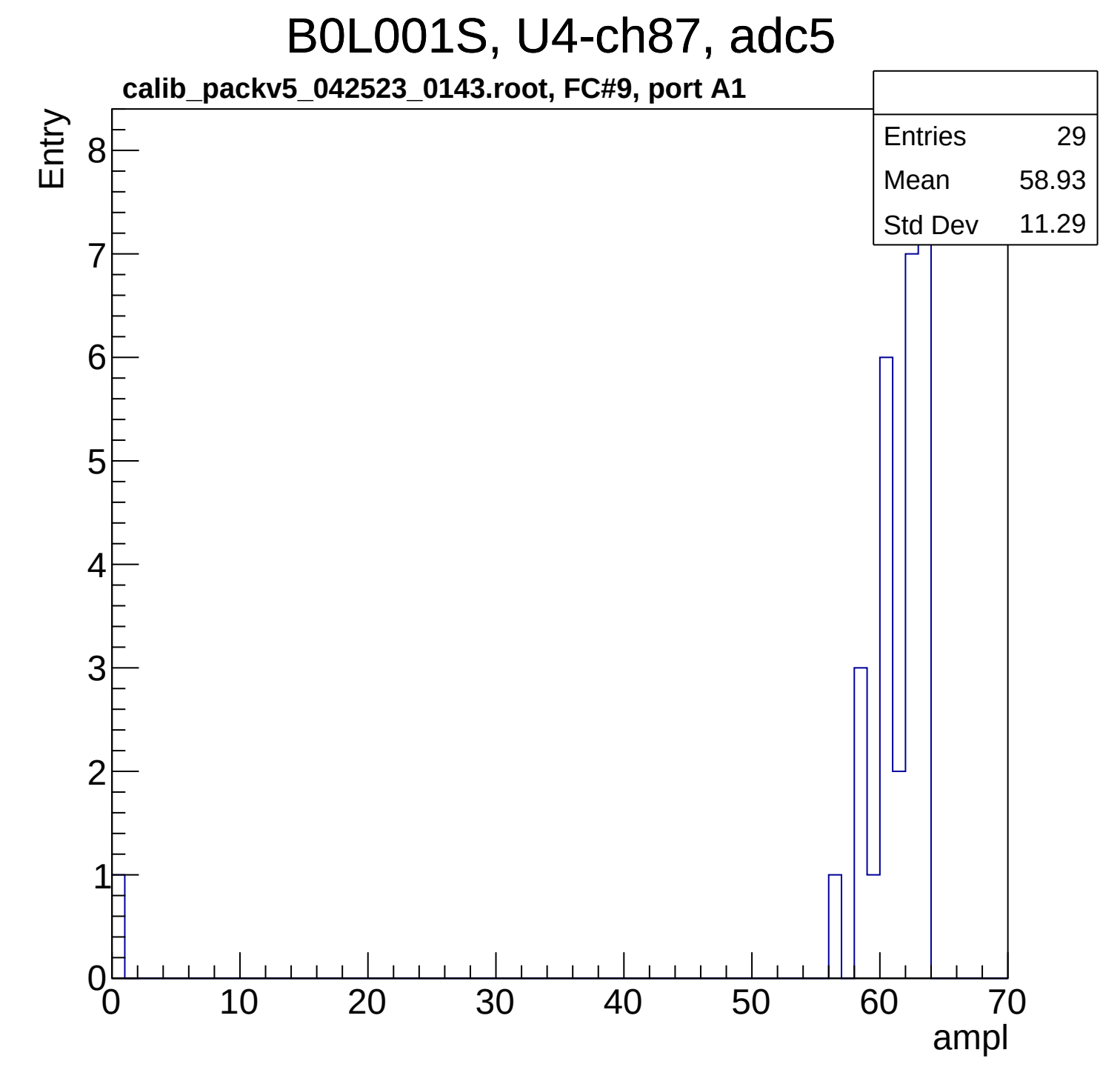
B0L001S, U4-ch87, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.93
Std Dev 11.29
Entry
ampl
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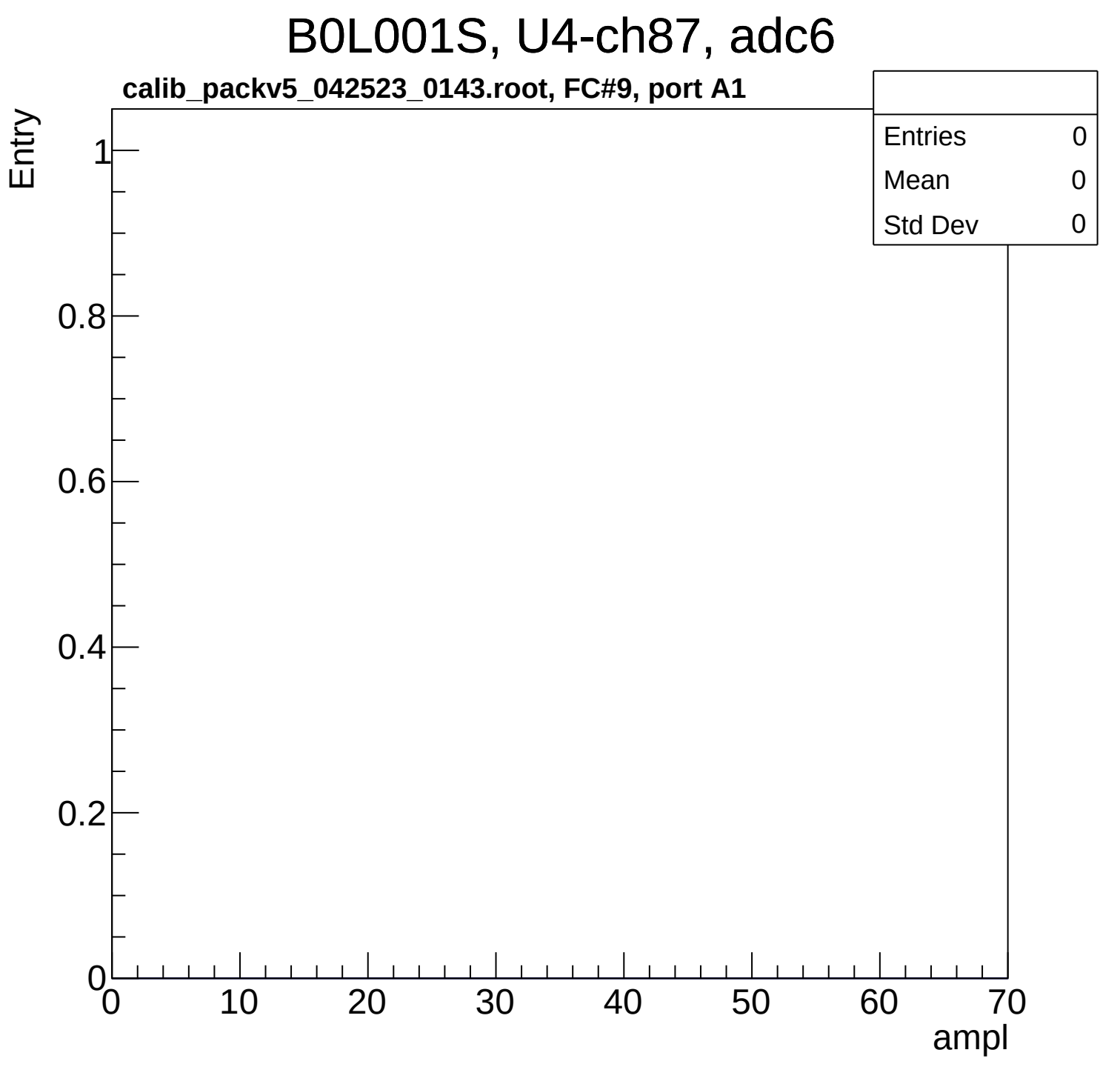
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch87, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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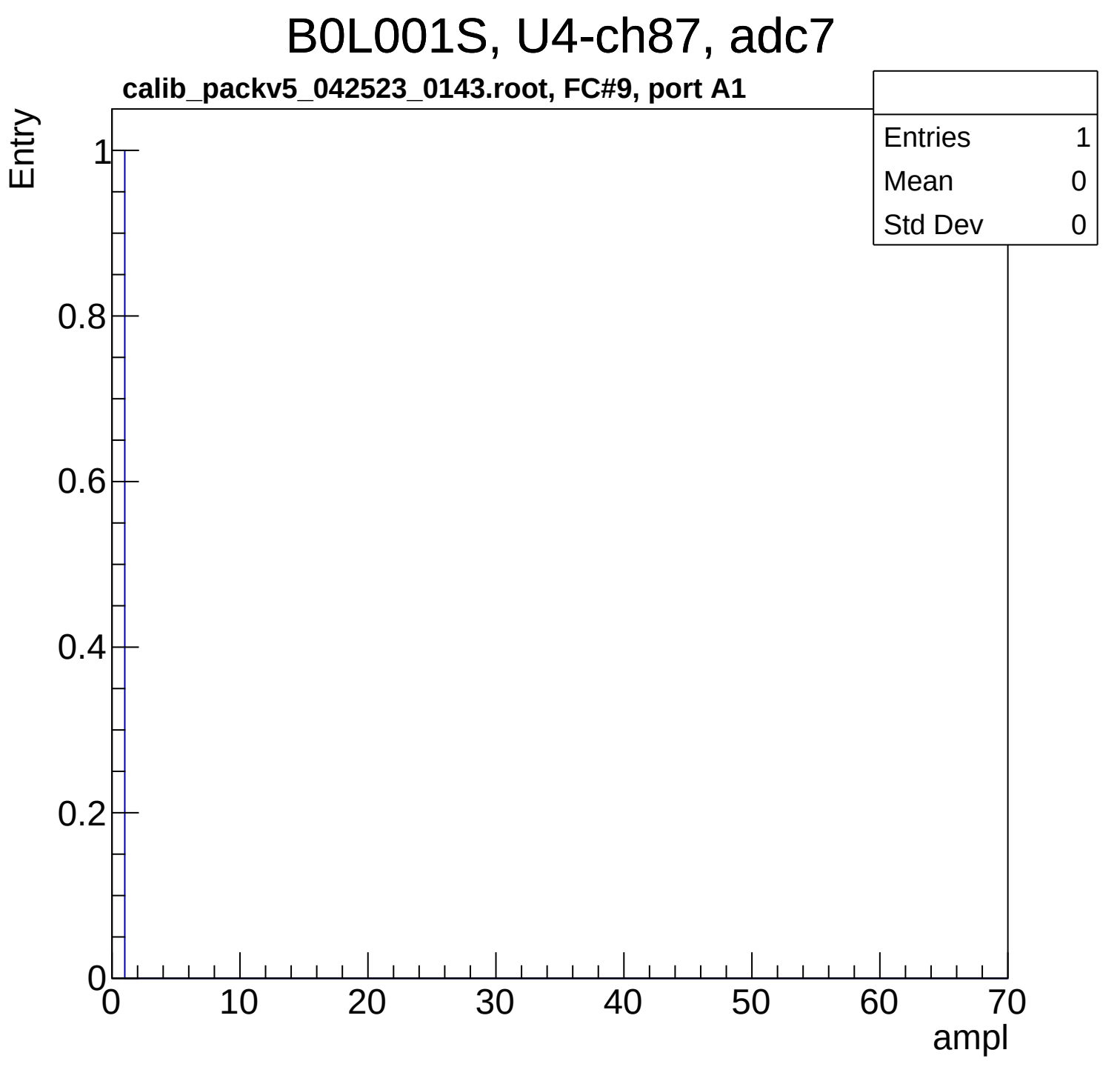
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch87, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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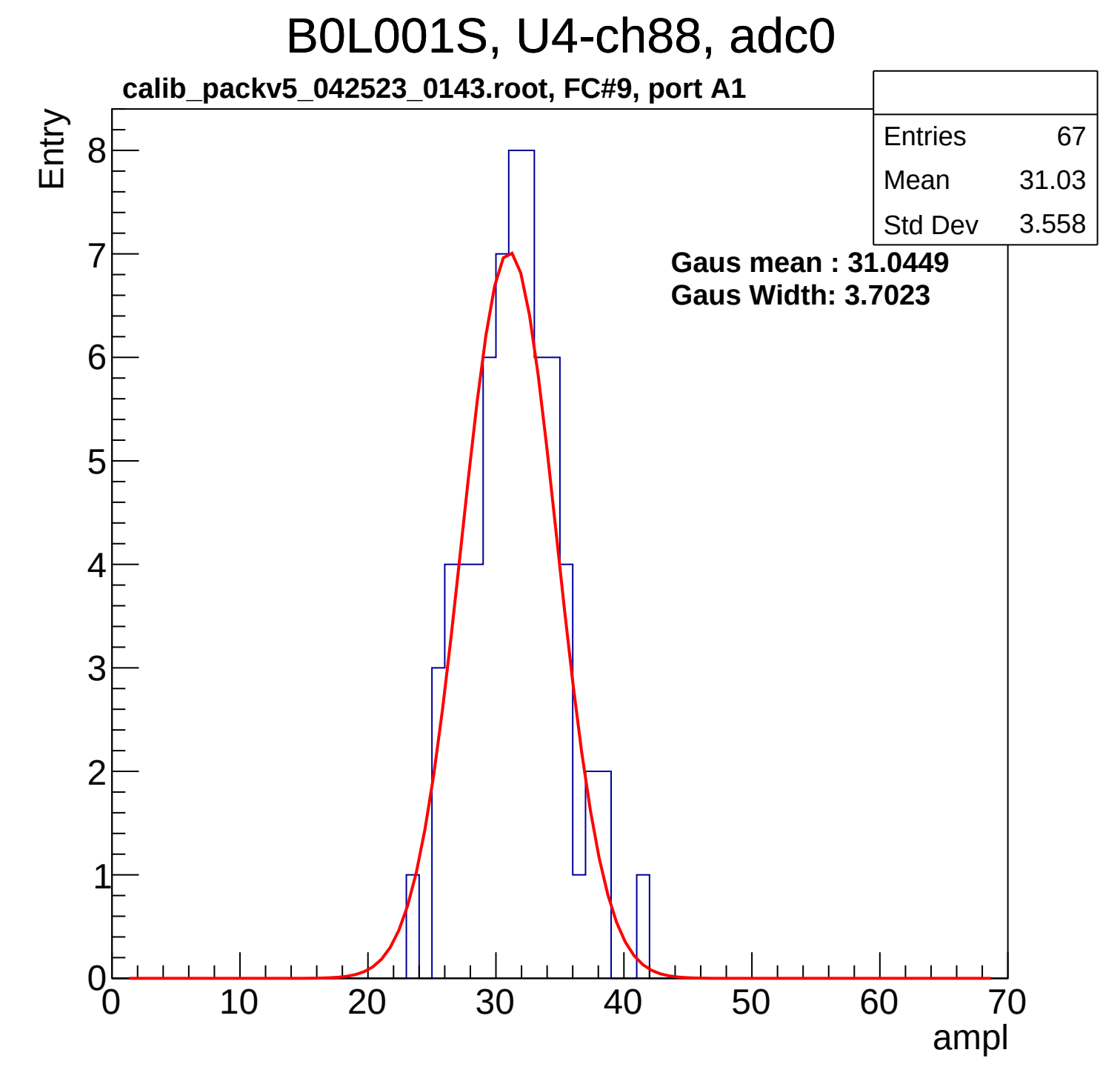
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch88, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.03
Std Dev 3.558
Gaus mean : 31.0449
Gaus Width: 3.7023
Entry
ampl
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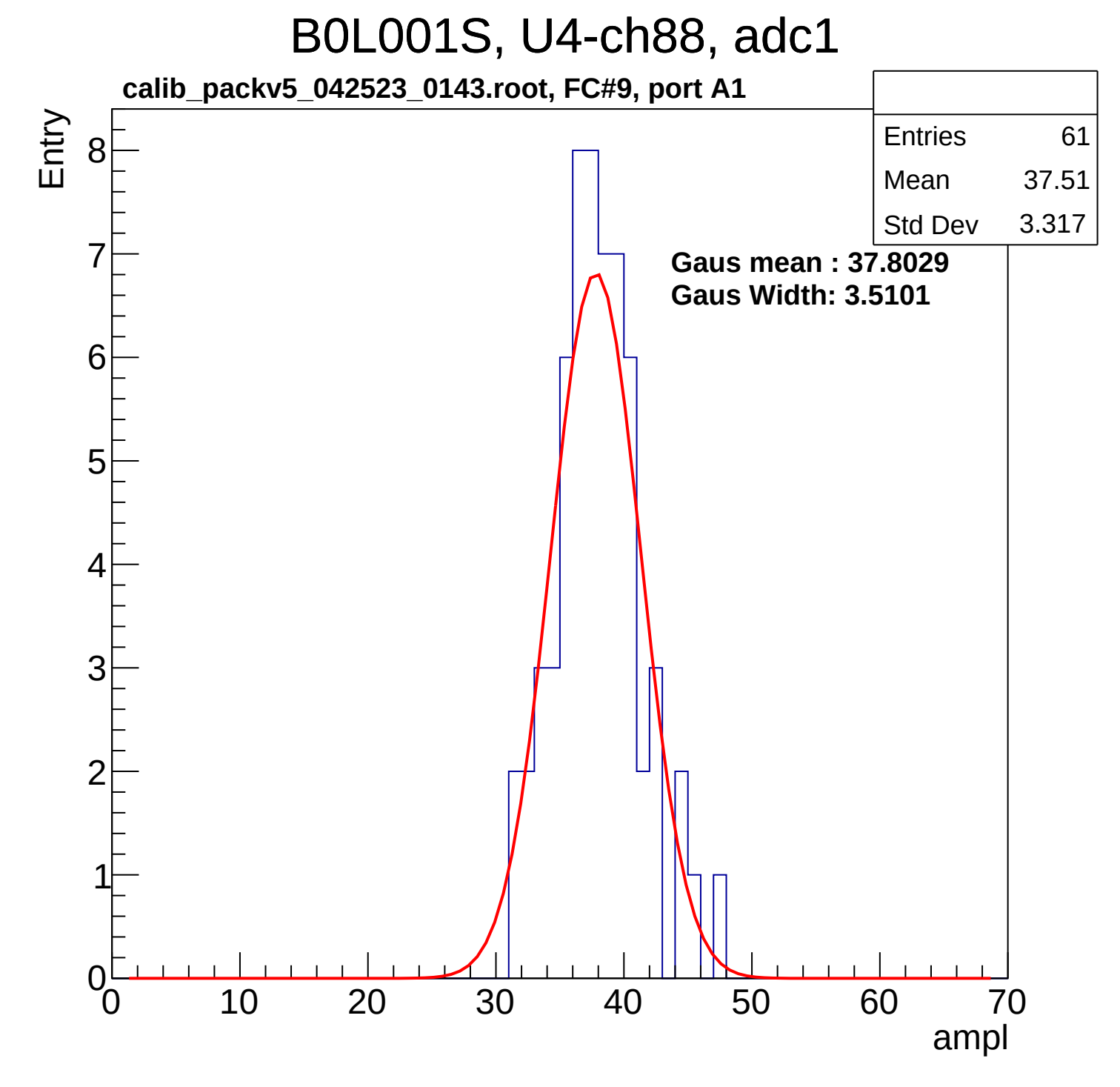
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch88, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.51
Std Dev 3.317
Gaus mean : 37.8029
Gaus Width: 3.5101
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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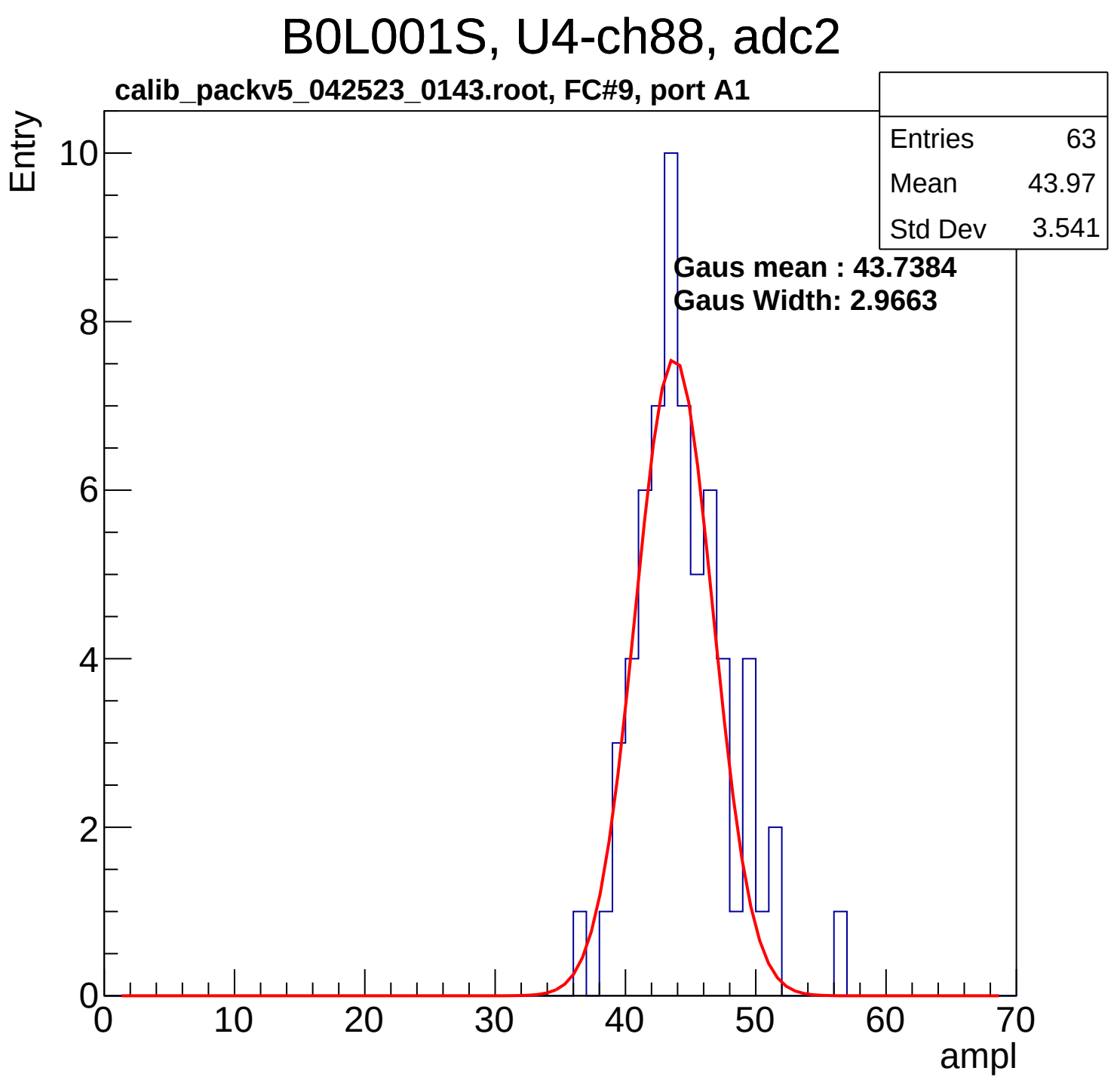
B0L001S, U4-ch88, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.97
Std Dev 3.541
Gaus mean : 43.7384
Gaus Width: 2.9663
Entry
ampl
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4
6
8
10
0
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20
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40
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60
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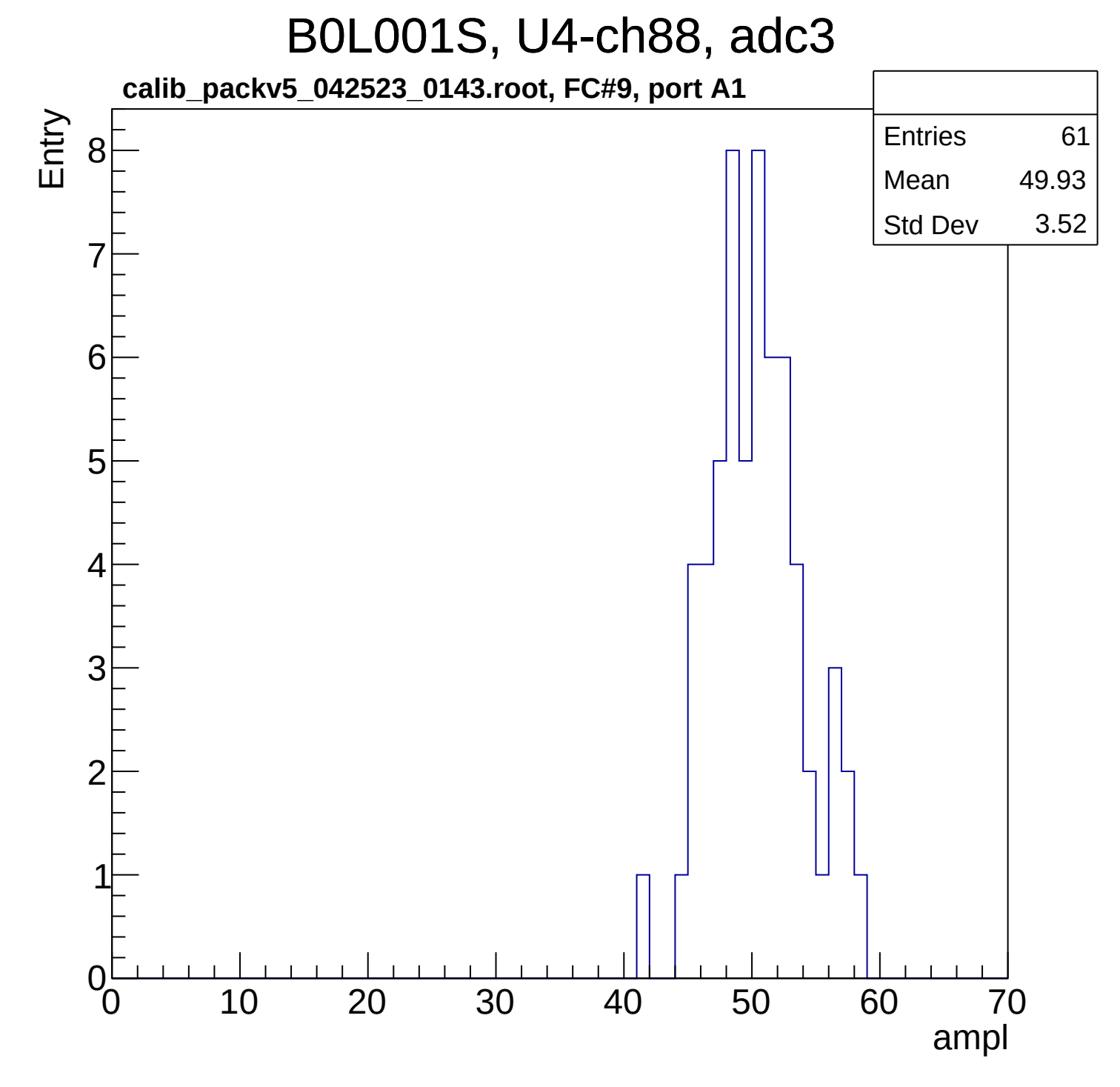
B0L001S, U4-ch88, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.93
Std Dev 3.52
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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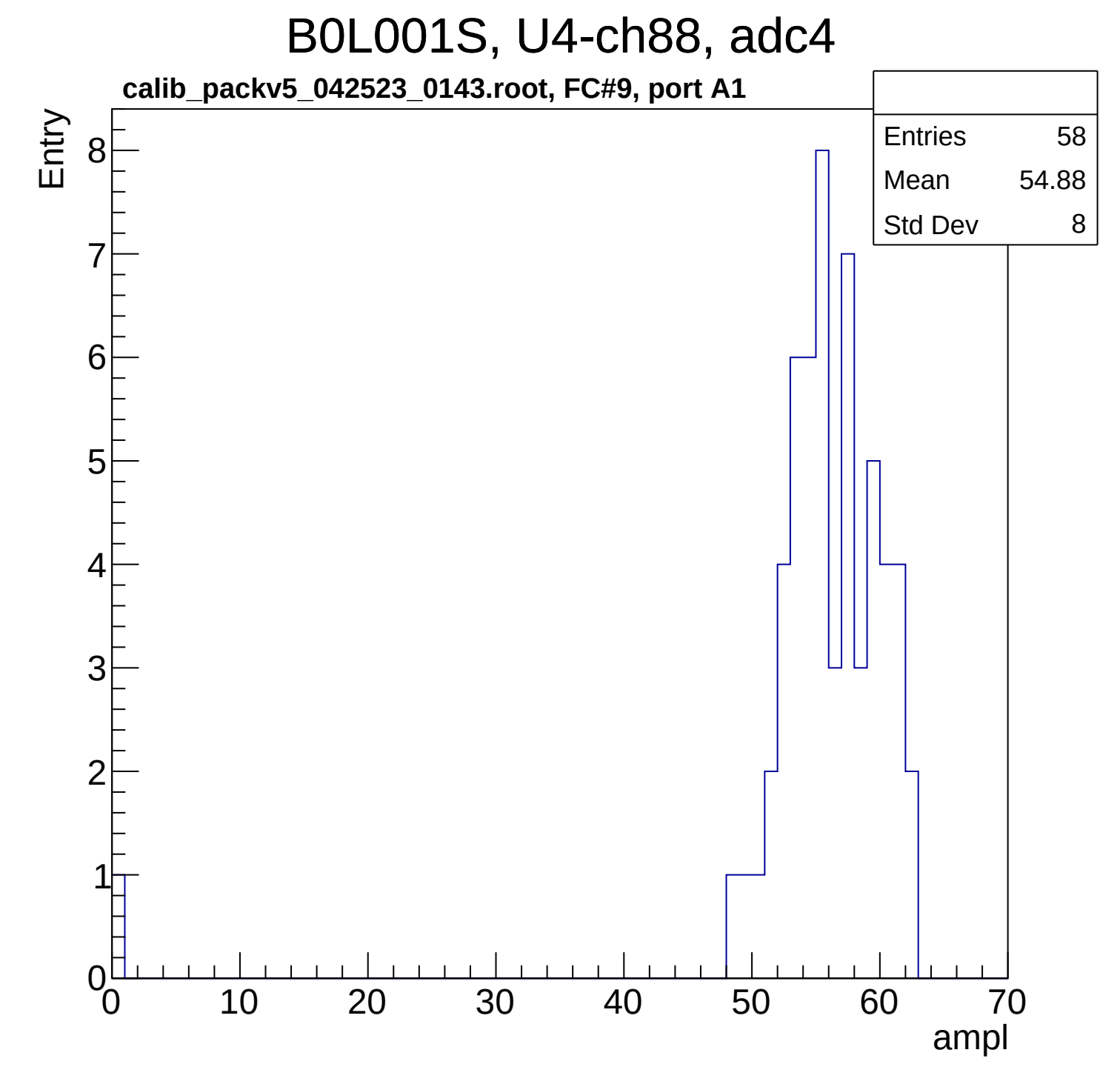
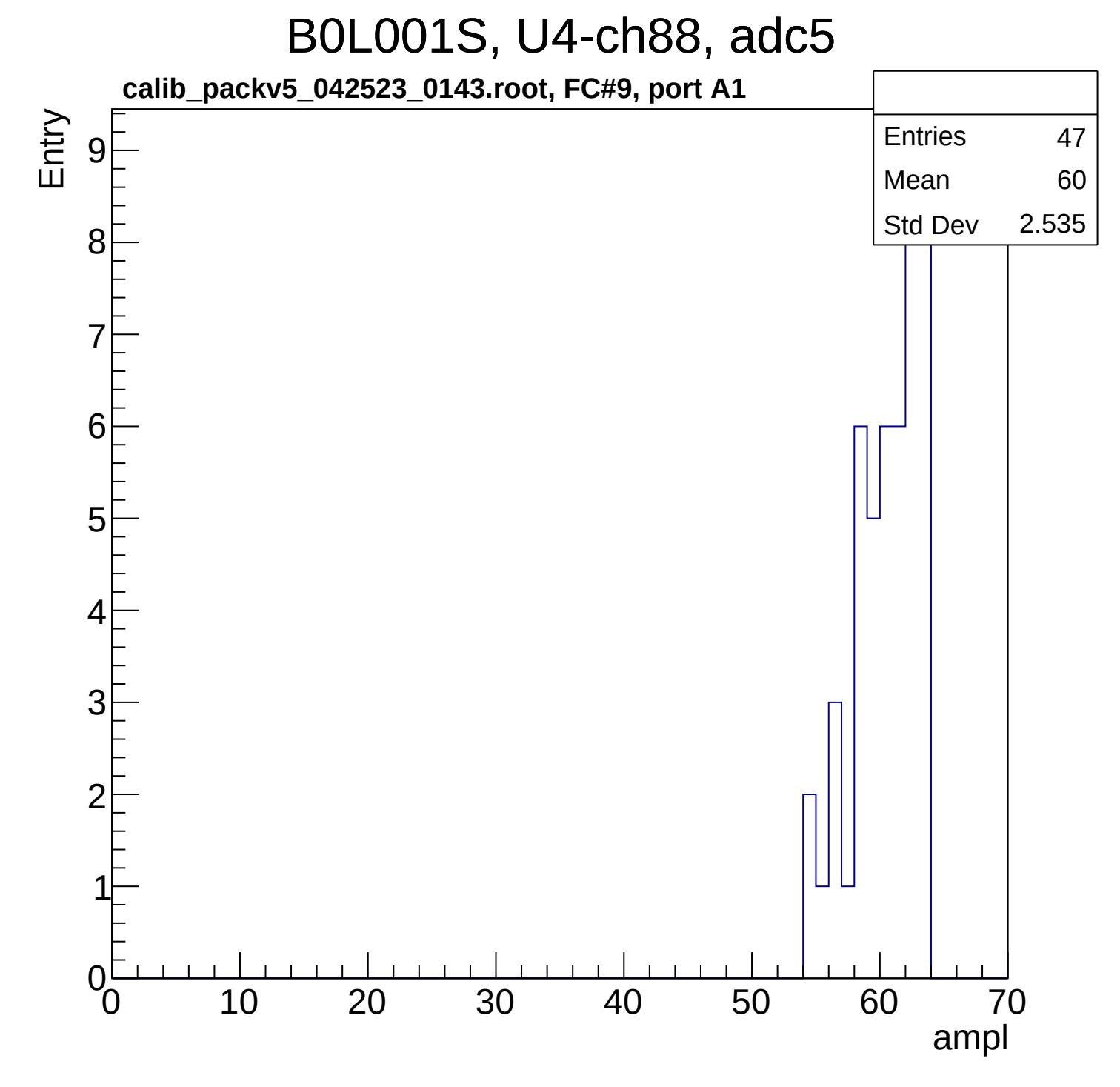

B0L001S, U4-ch88, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 54.88
Std Dev 8
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch88, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 60
Std Dev 2.535
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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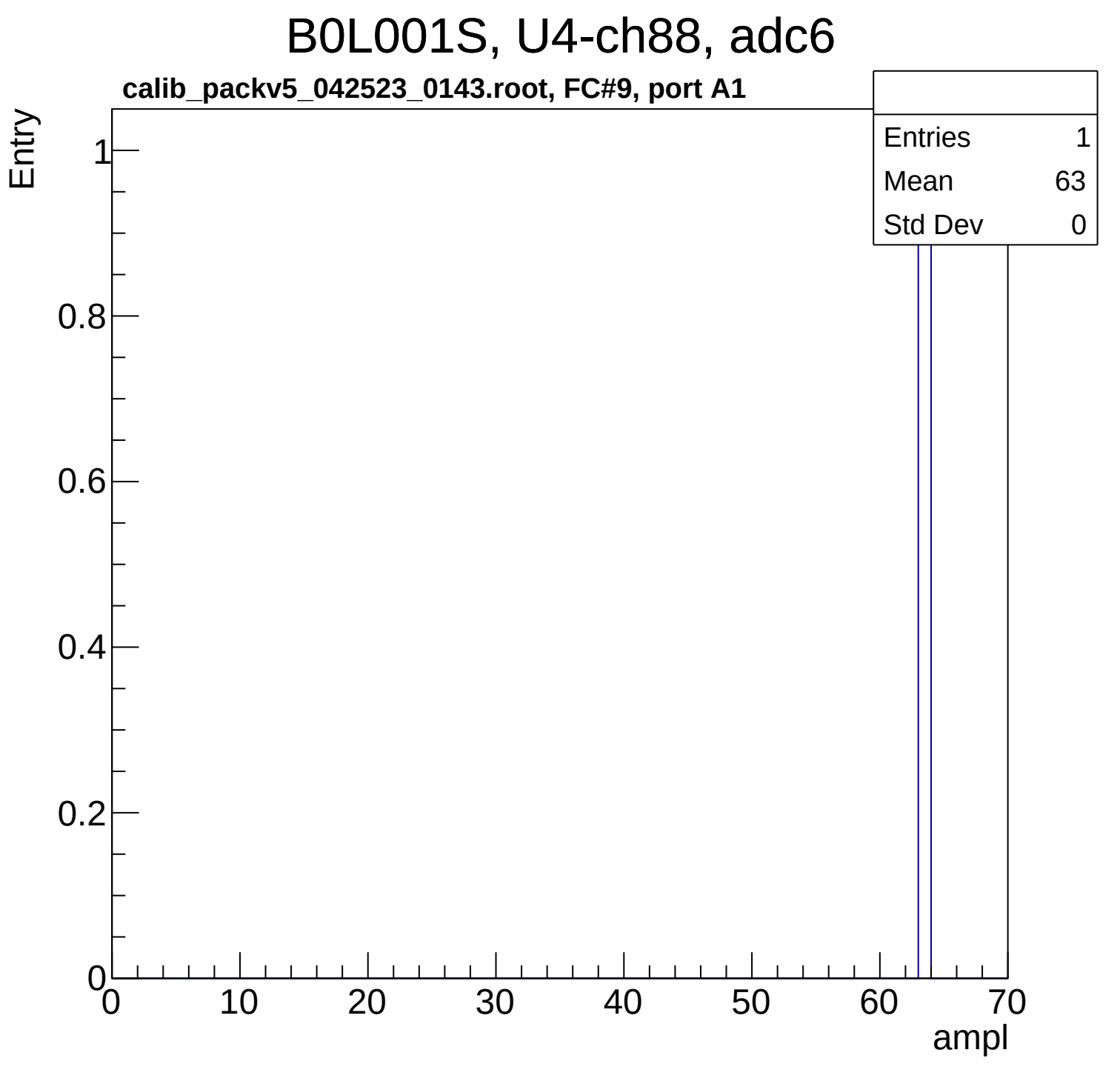
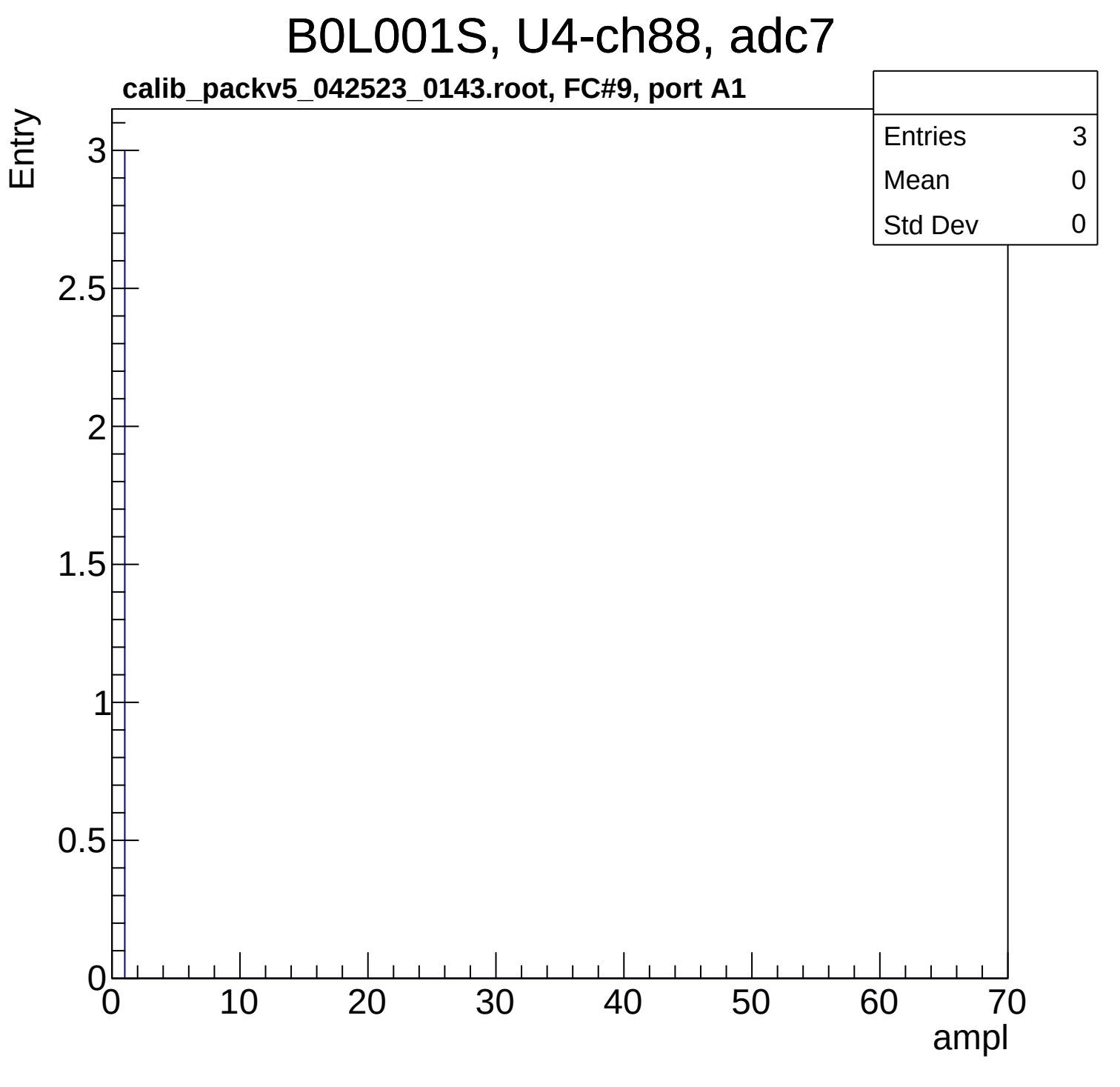
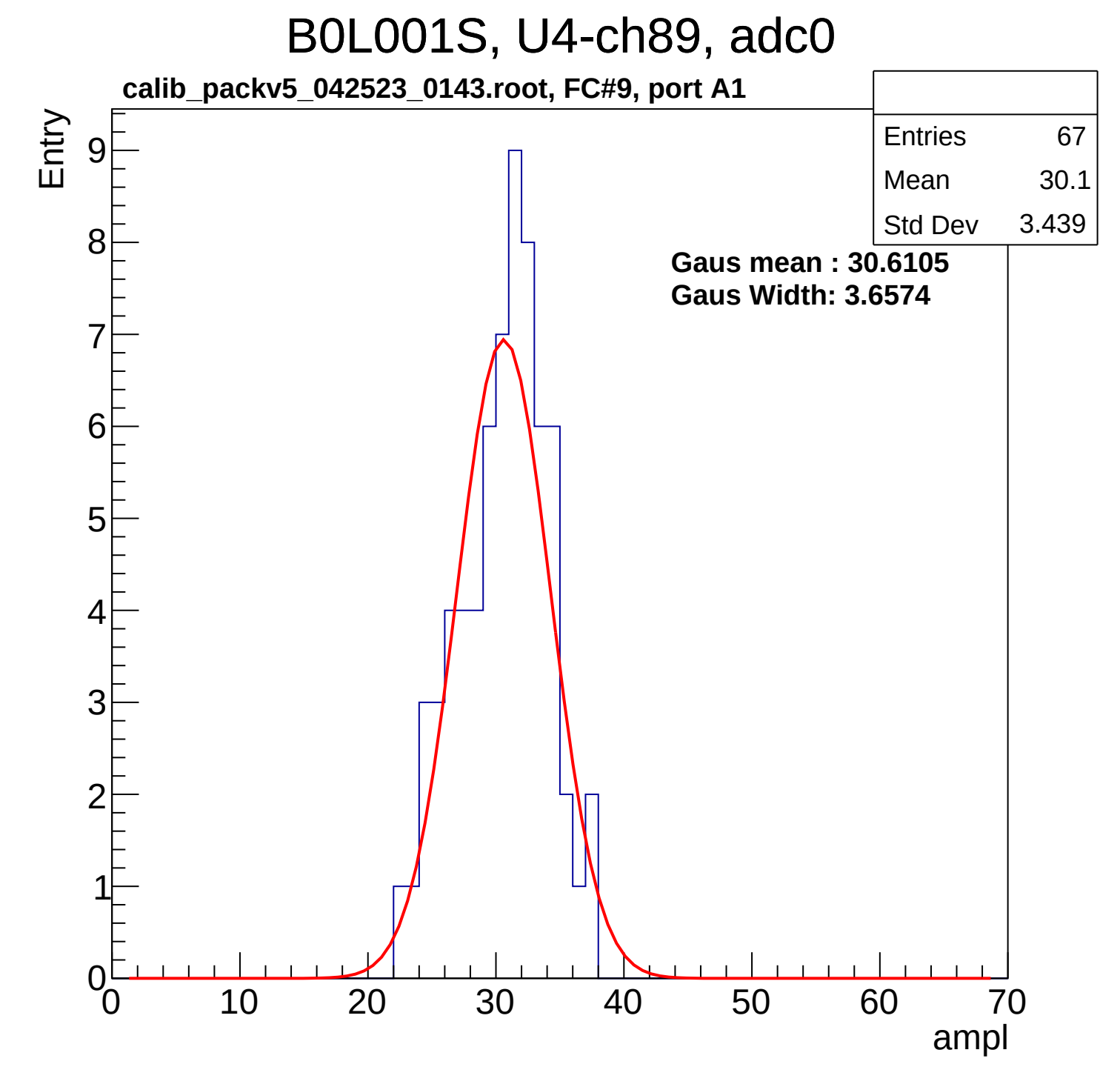

B0L001S, U4-ch88, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch88, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch89, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.1
Std Dev 3.439
Gaus mean : 30.6105
Gaus Width: 3.6574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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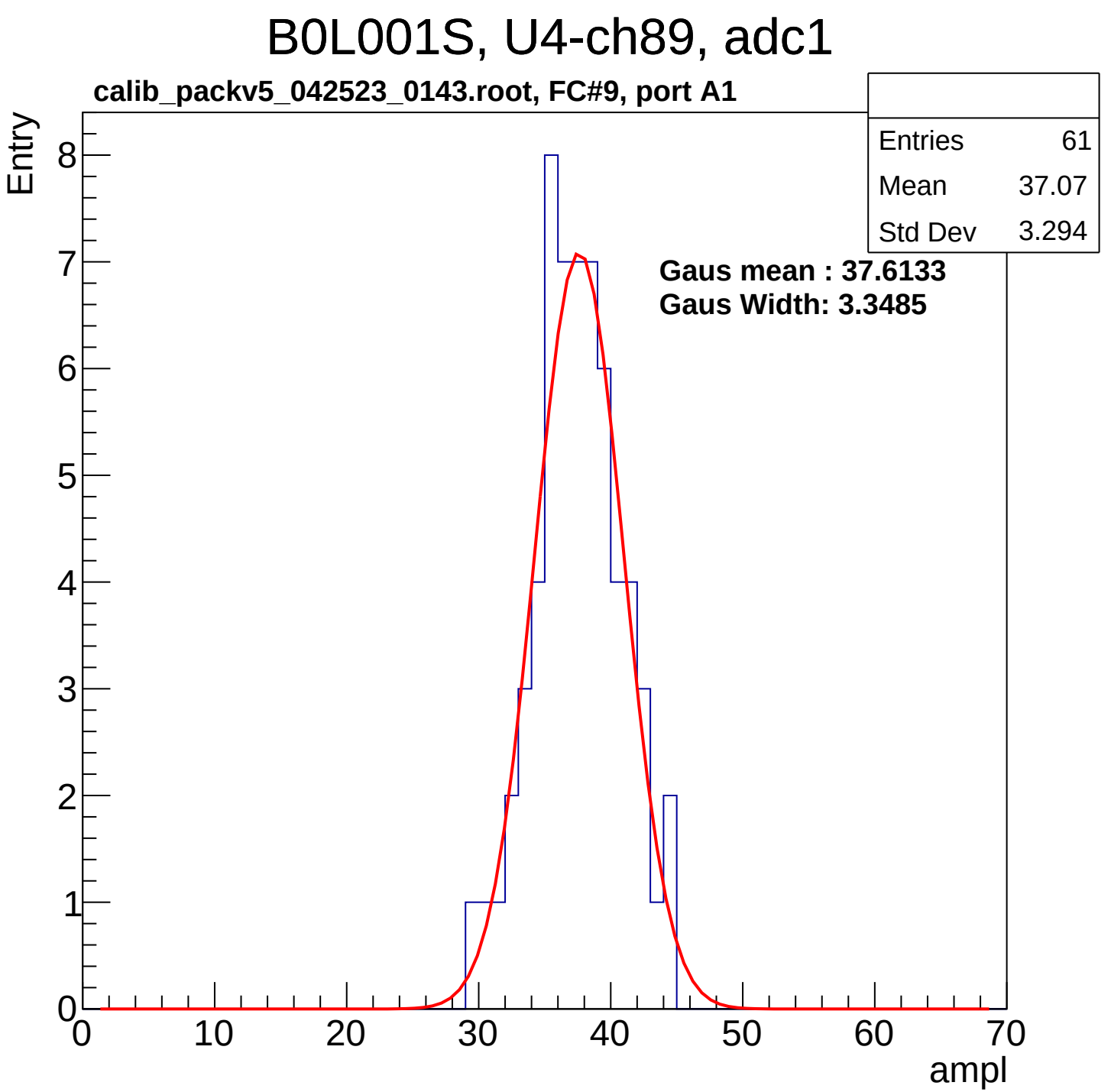
B0L001S, U4-ch89, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.07
Std Dev 3.294
Gaus mean : 37.6133
Gaus Width: 3.3485
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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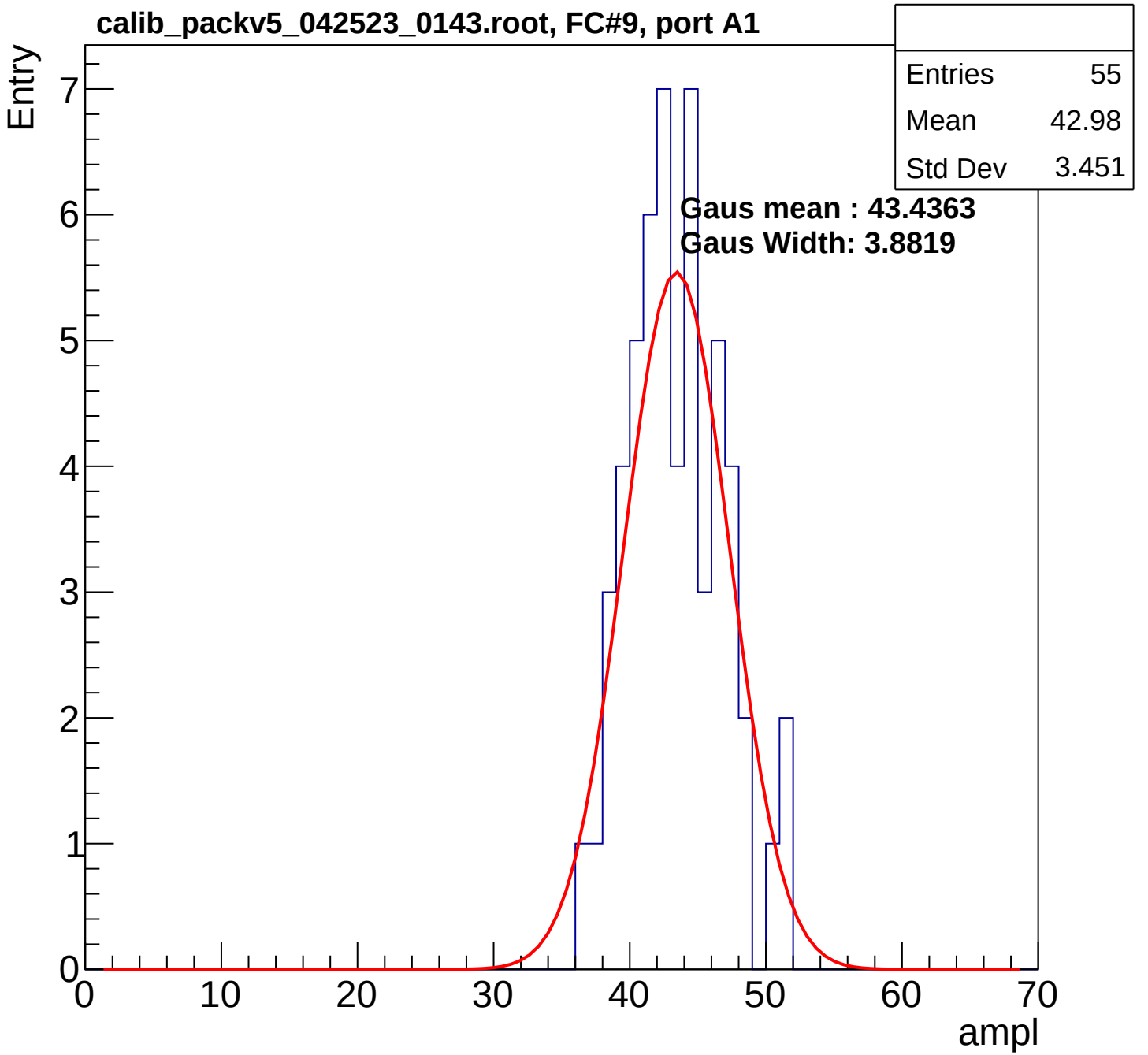
B0L001S, U4-ch89, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.98
Std Dev 3.451
Gaus mean : 43.4363
Gaus Width: 3.8819
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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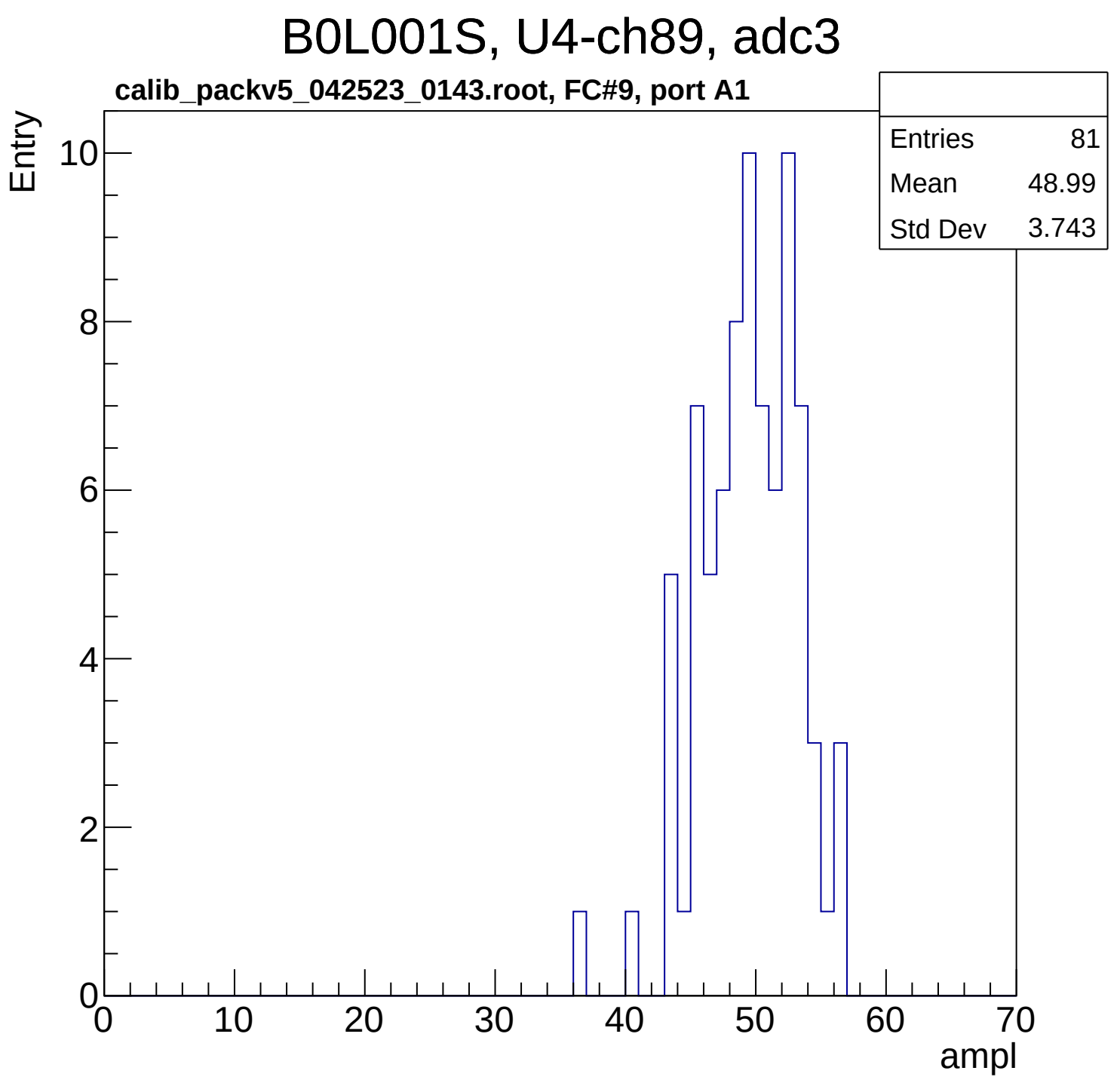
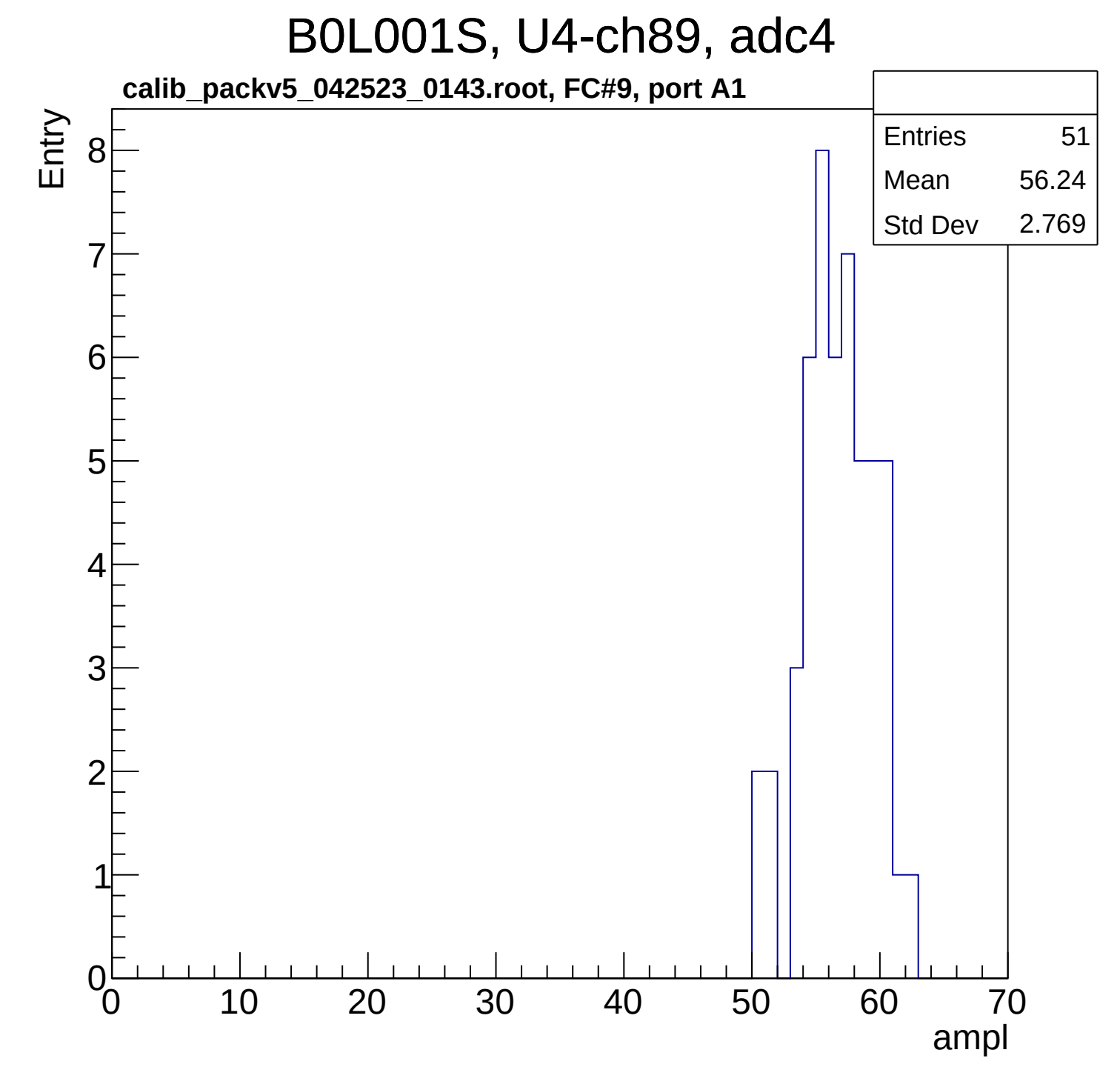

B0L001S, U4-ch89, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 48.99
Std Dev 3.743
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch89, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.24
Std Dev 2.769
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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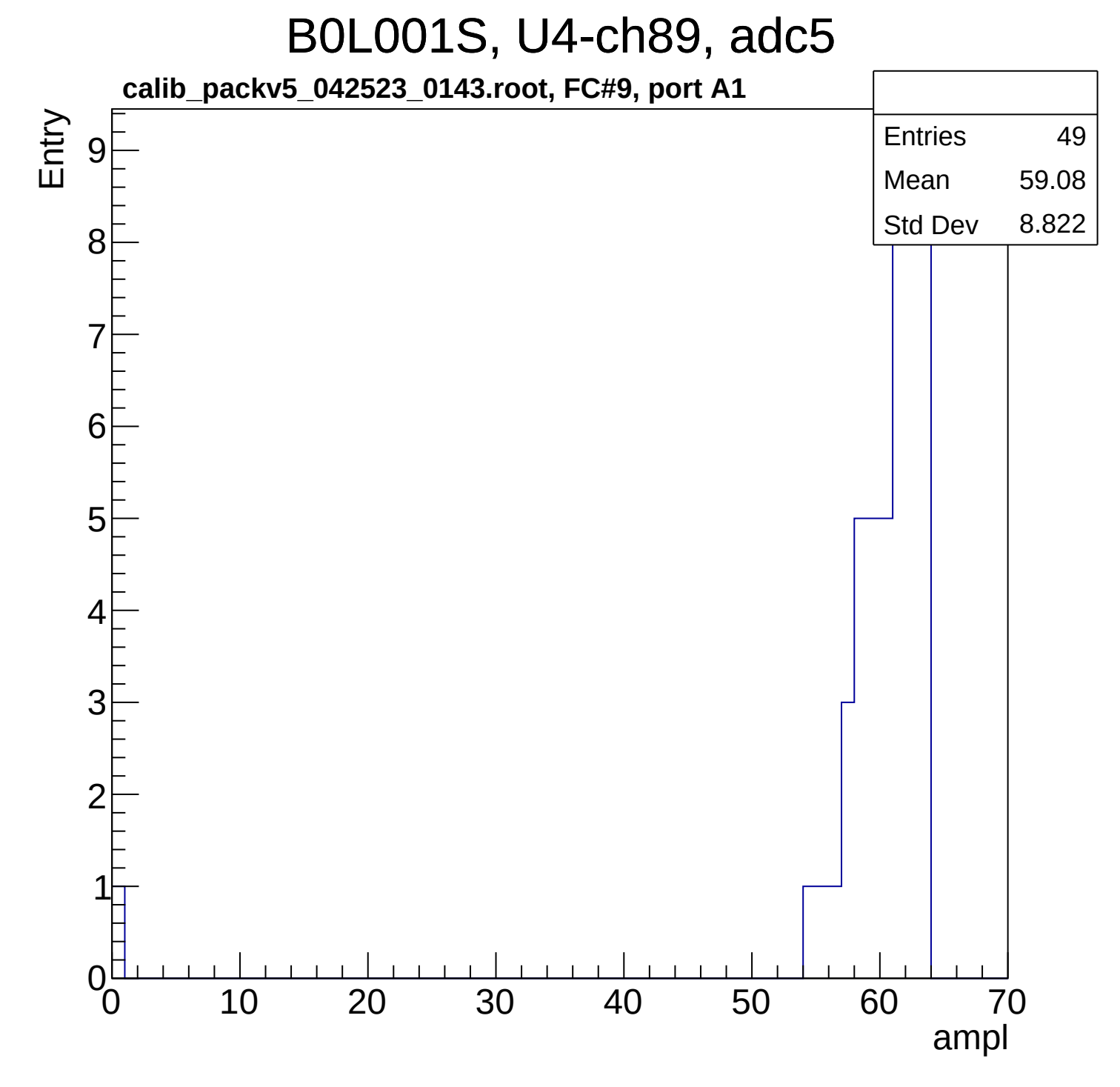
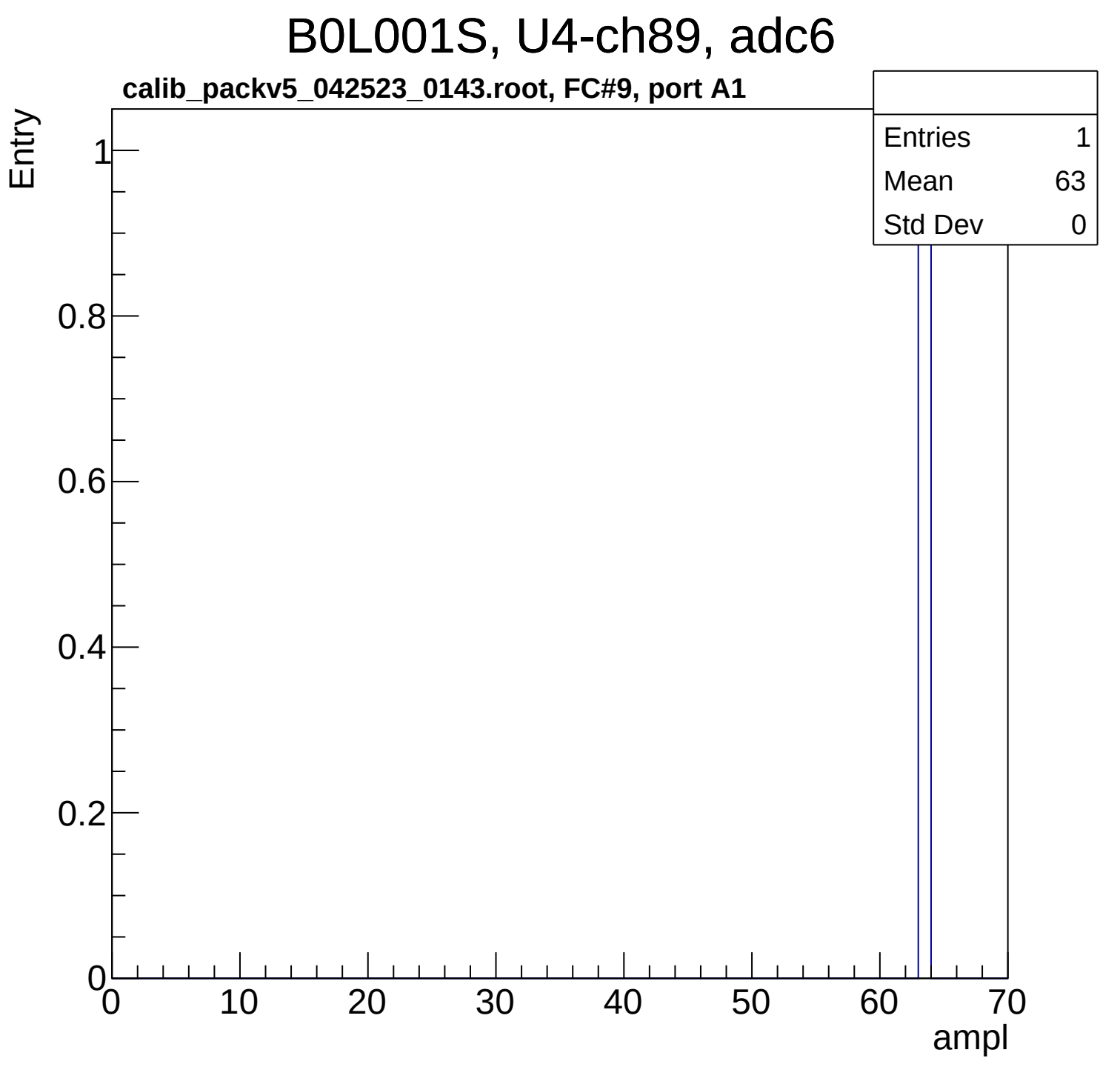
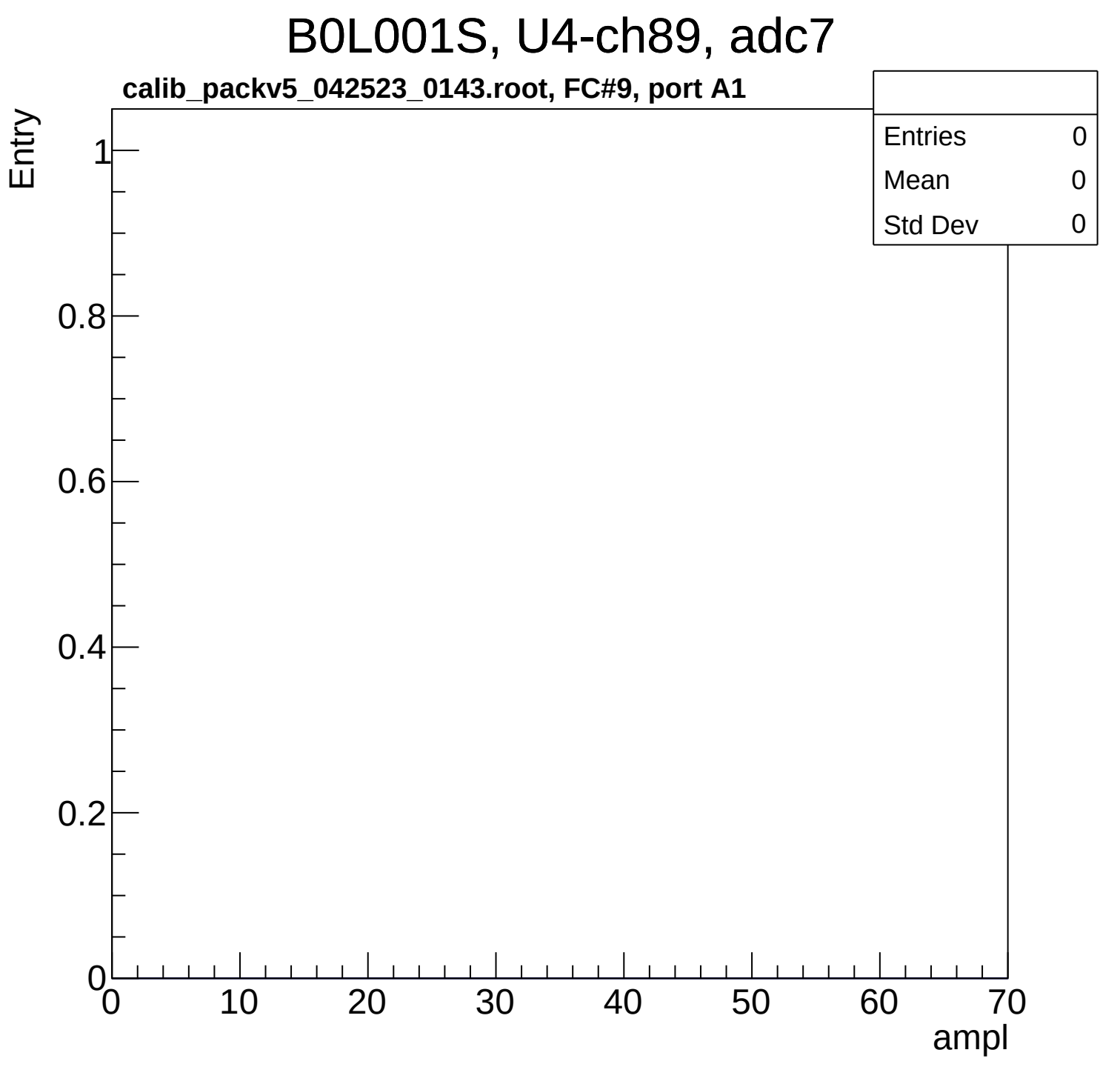
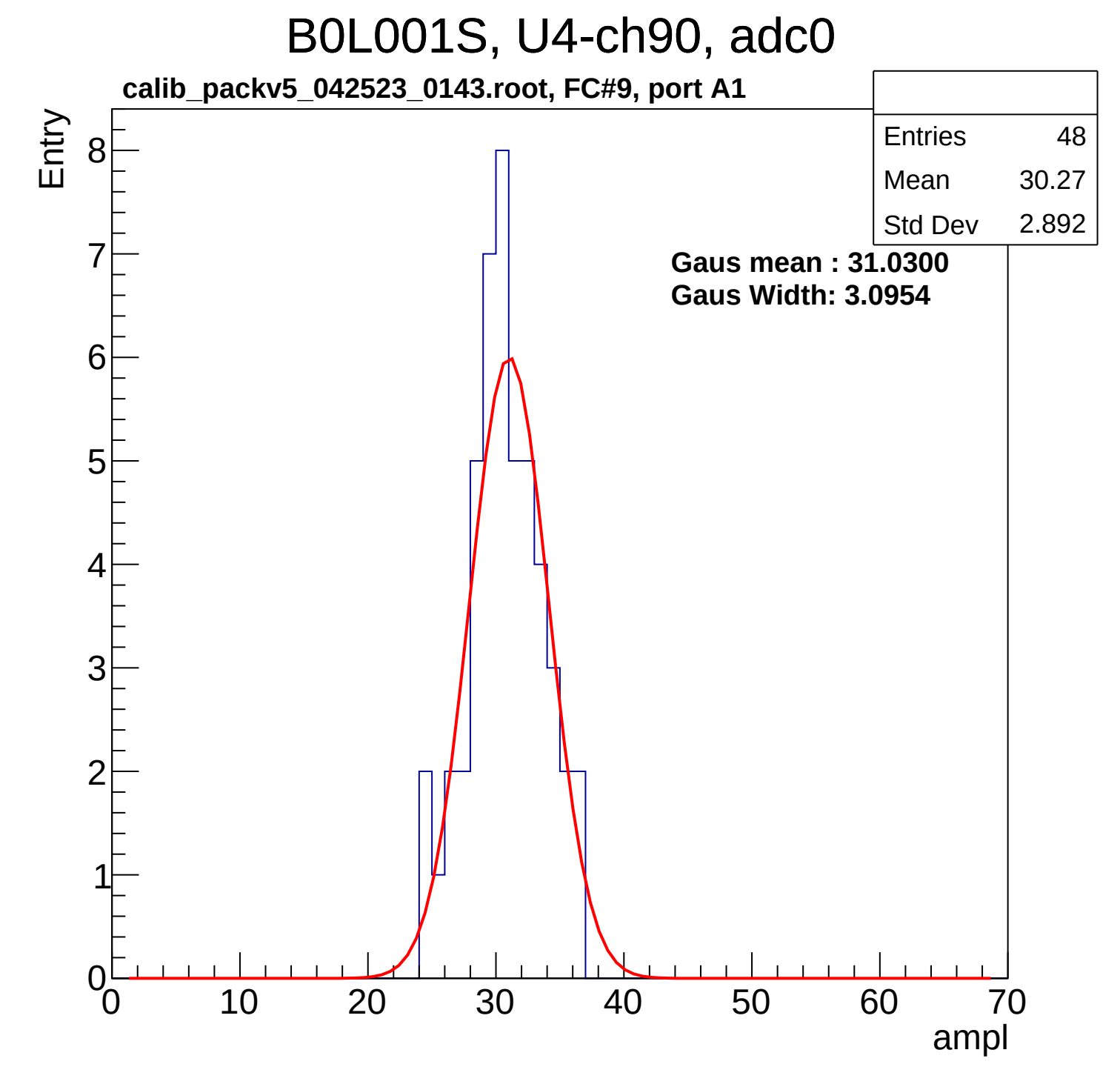
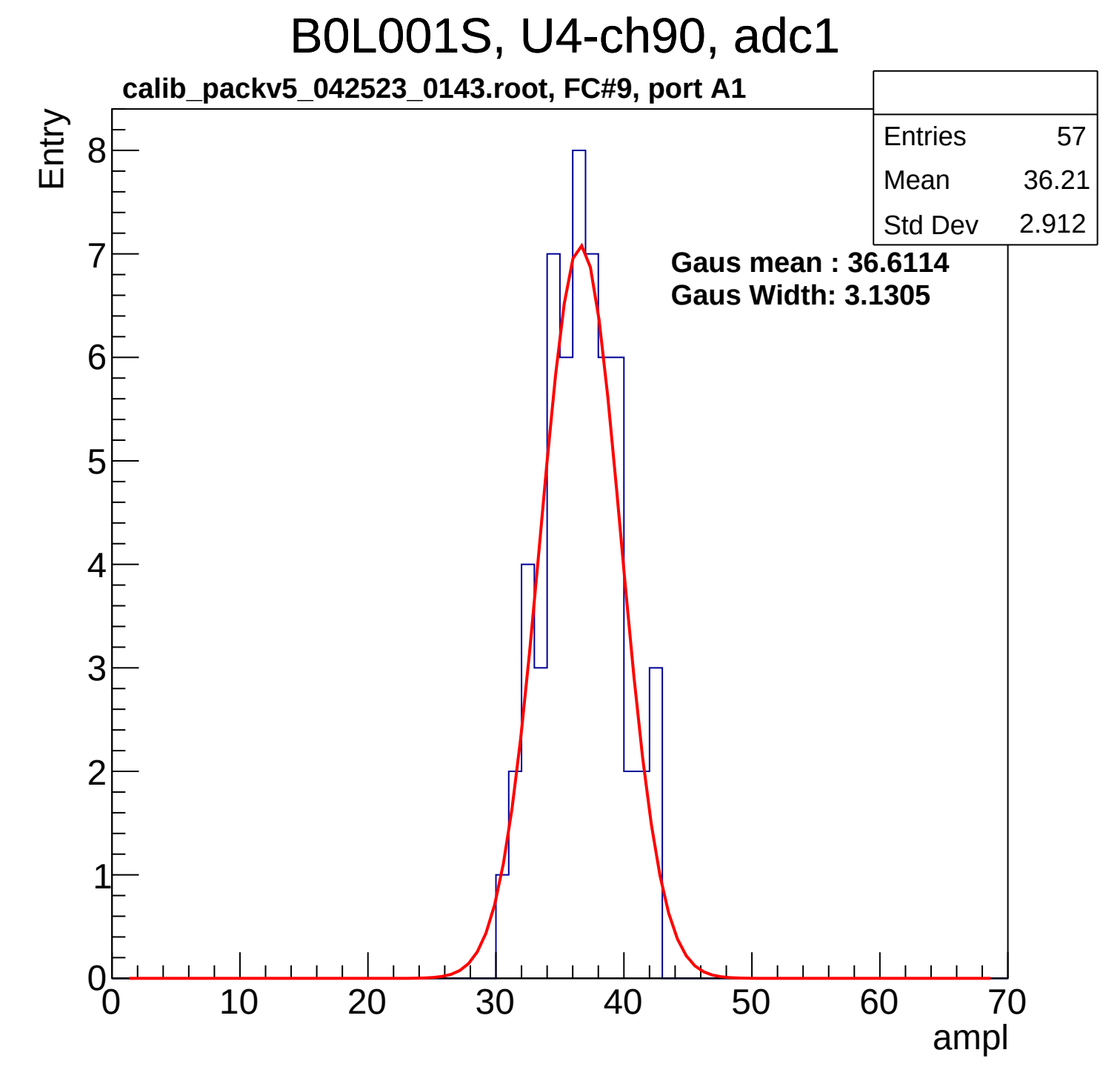
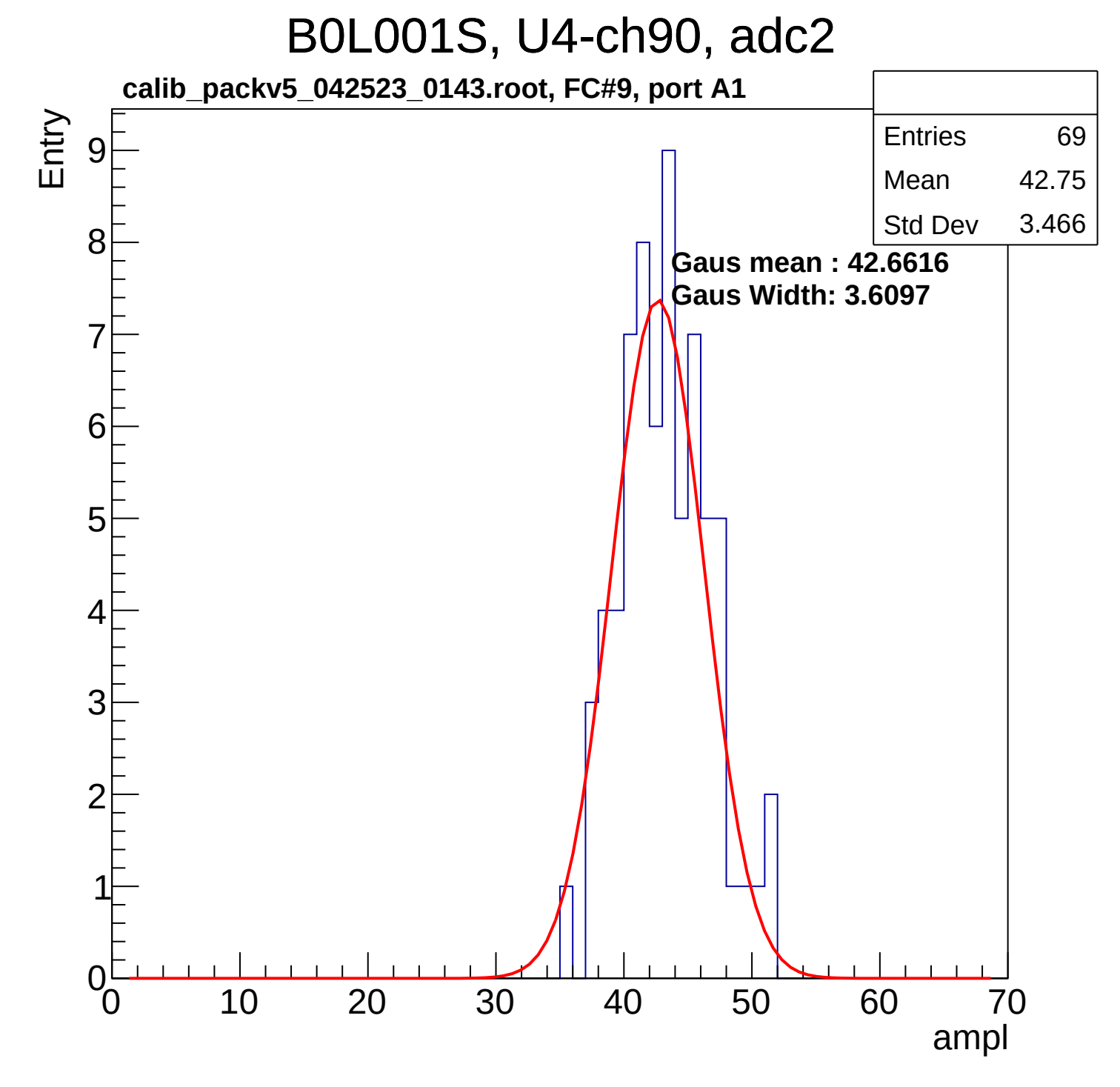

B0L001S, U4-ch89, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 59.08
Std Dev 8.822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch89, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch89, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch90, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 30.27
Std Dev 2.892
Gaus mean : 31.0300
Gaus Width: 3.0954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch90, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 36.21
Std Dev 2.912
Gaus mean : 36.6114
Gaus Width: 3.1305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch90, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 42.75
Std Dev 3.466
Gaus mean : 42.6616
Gaus Width: 3.6097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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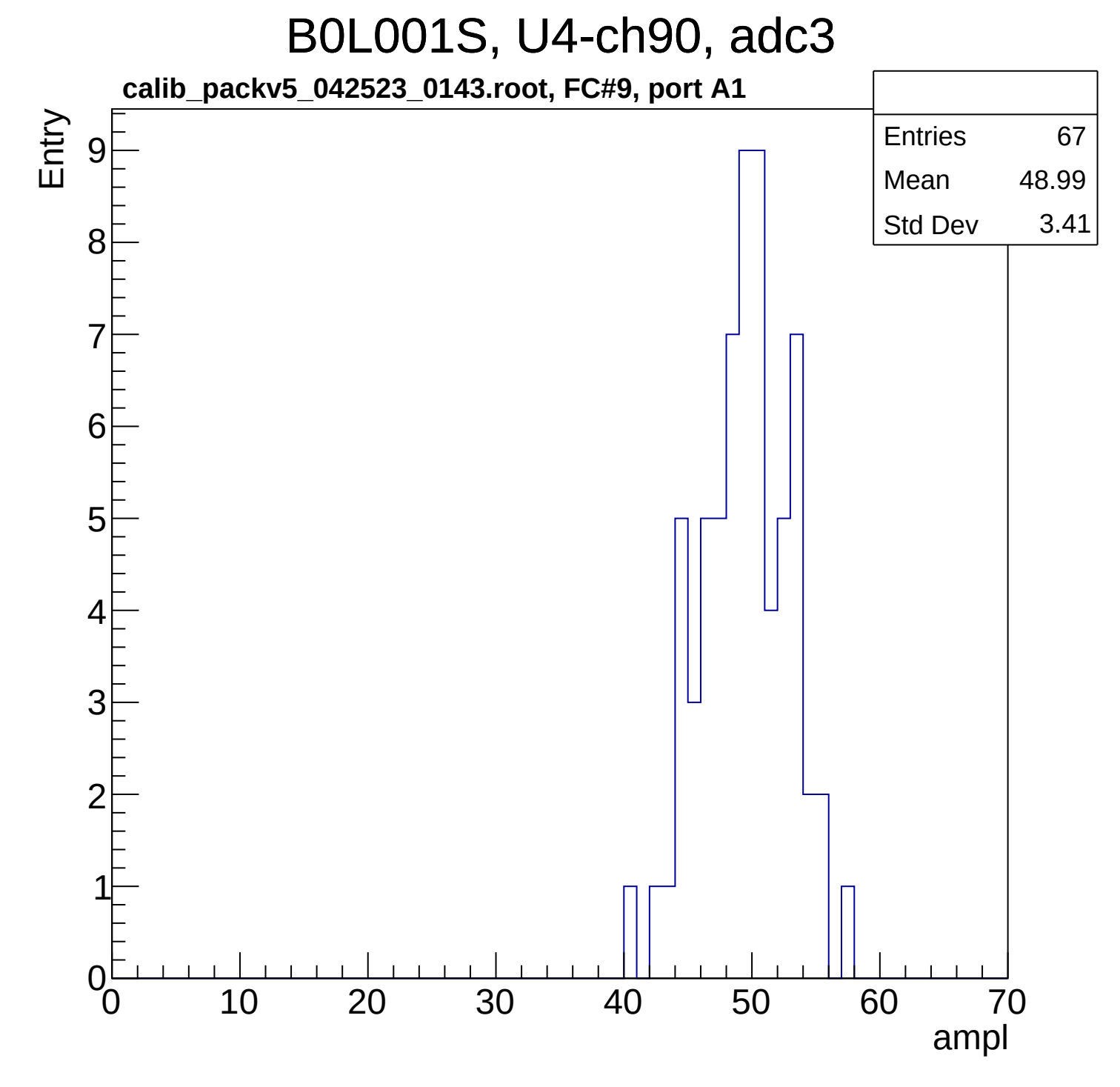
B0L001S, U4-ch90, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 48.99
Std Dev 3.41
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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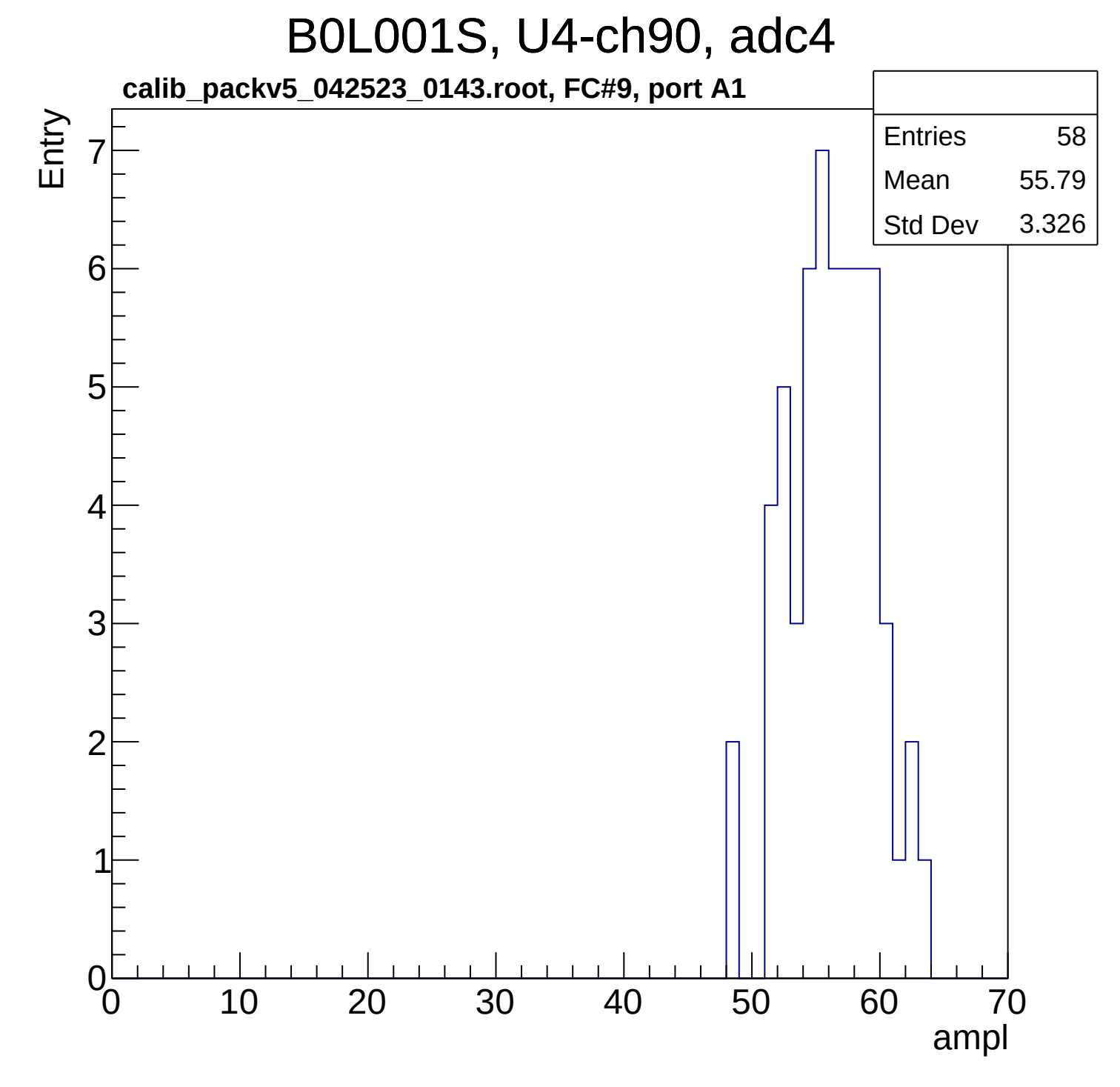
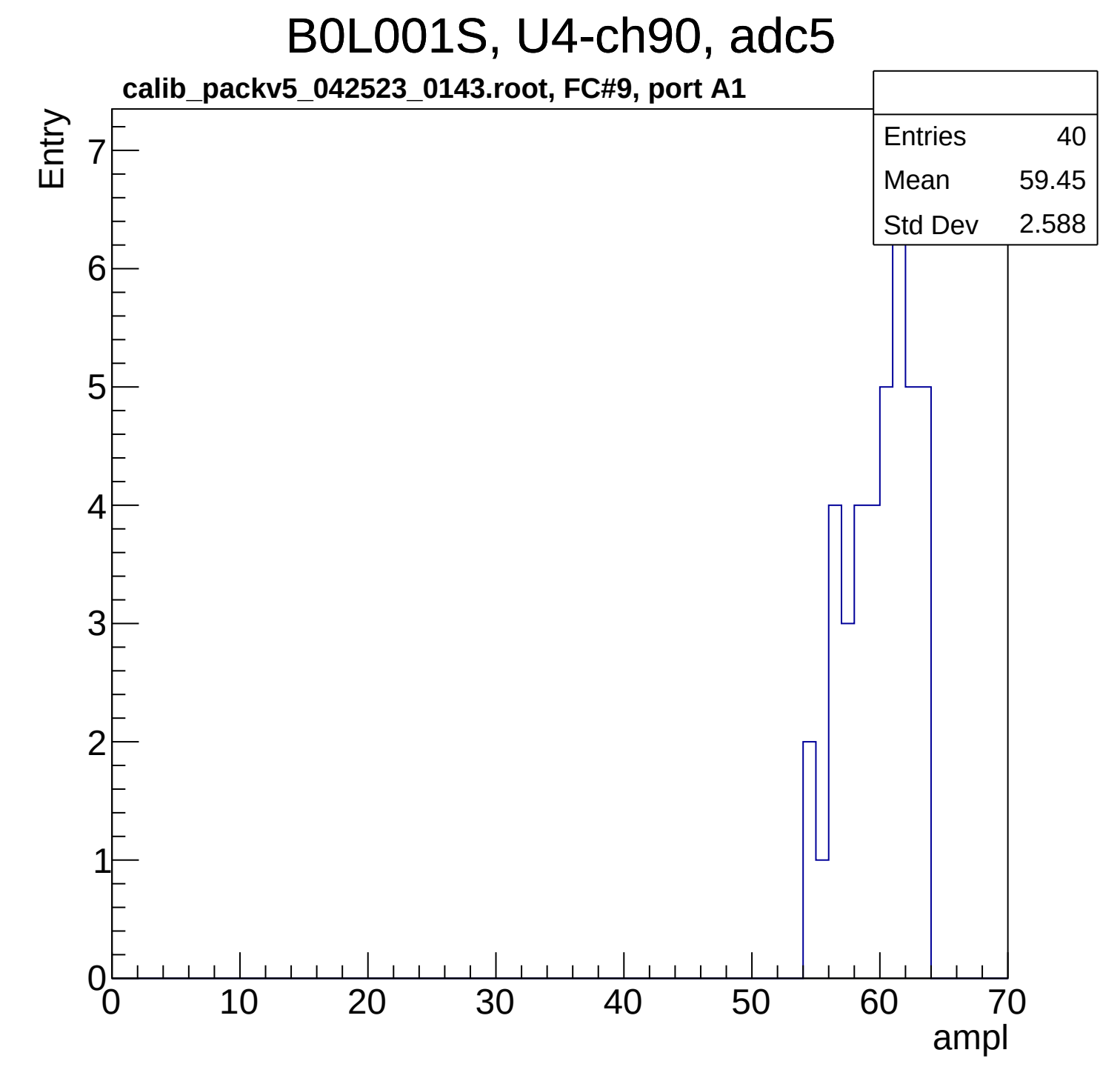
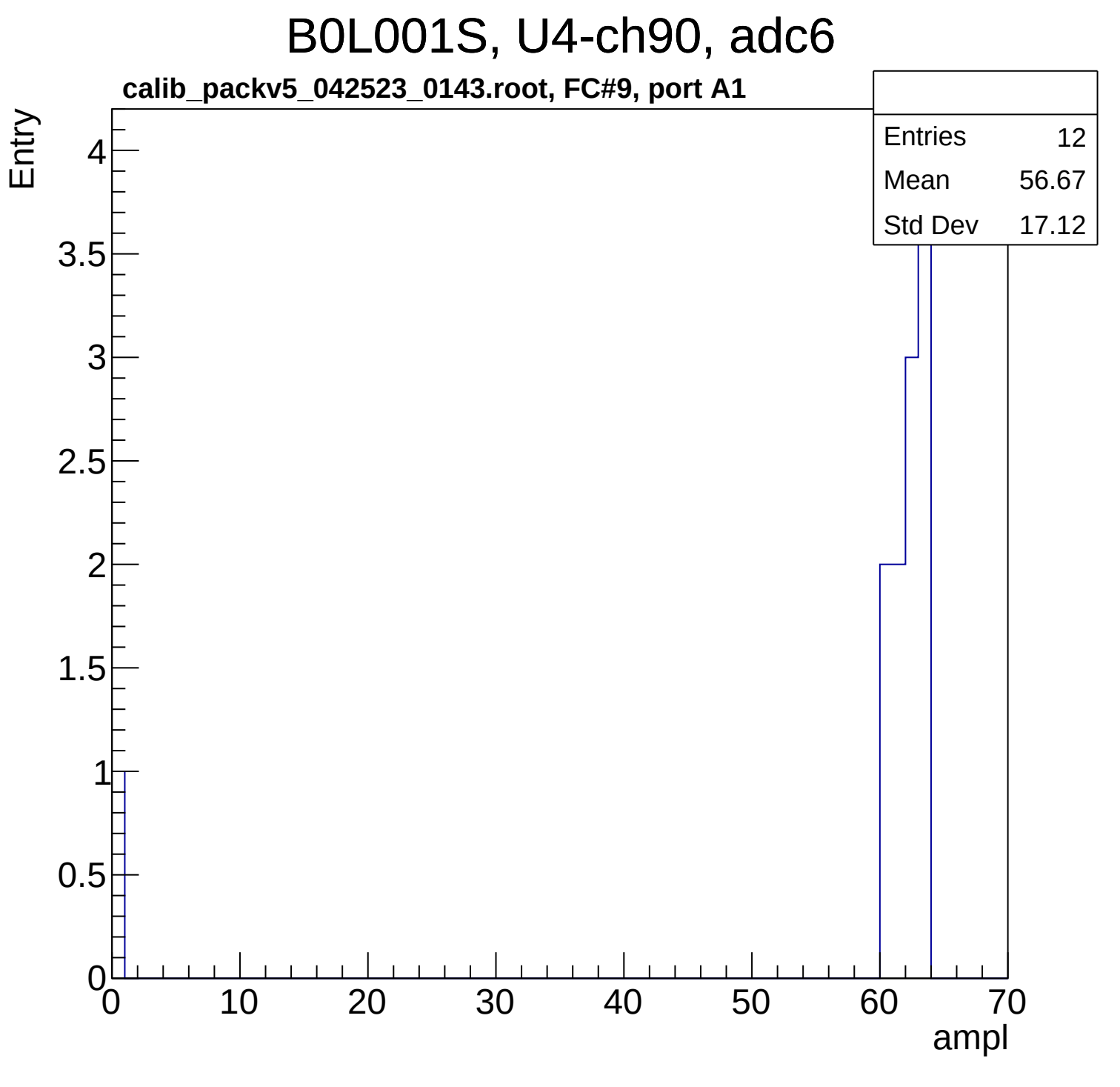

B0L001S, U4-ch90, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.79
Std Dev 3.326
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch90, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.45
Std Dev 2.588
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch90, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.67
Std Dev 17.12
Entry
ampl
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0.5
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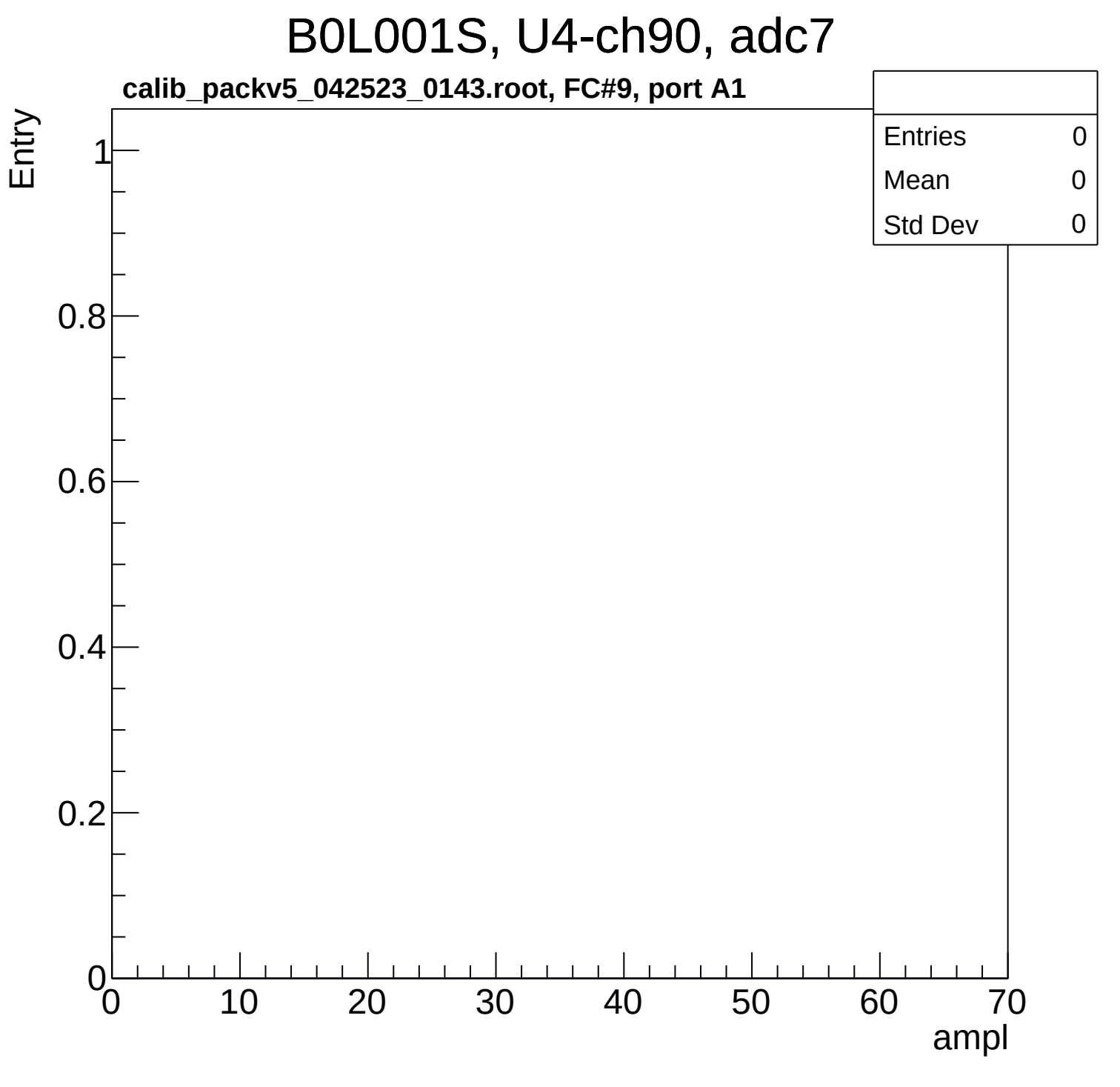
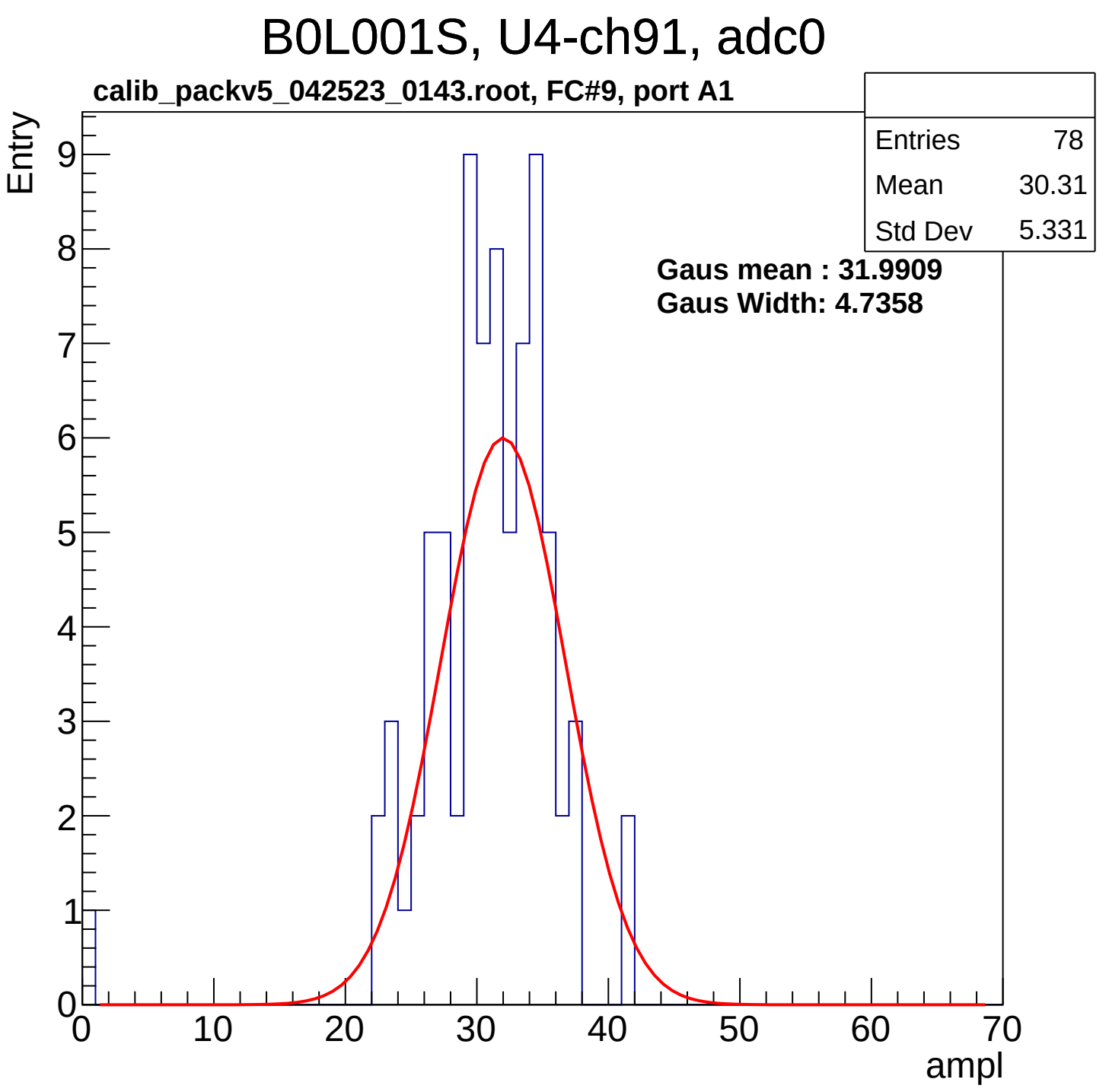
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch90, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch91, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.31
Std Dev 5.331
Gaus mean : 31.9909
Gaus Width: 4.7358
Entry
ampl
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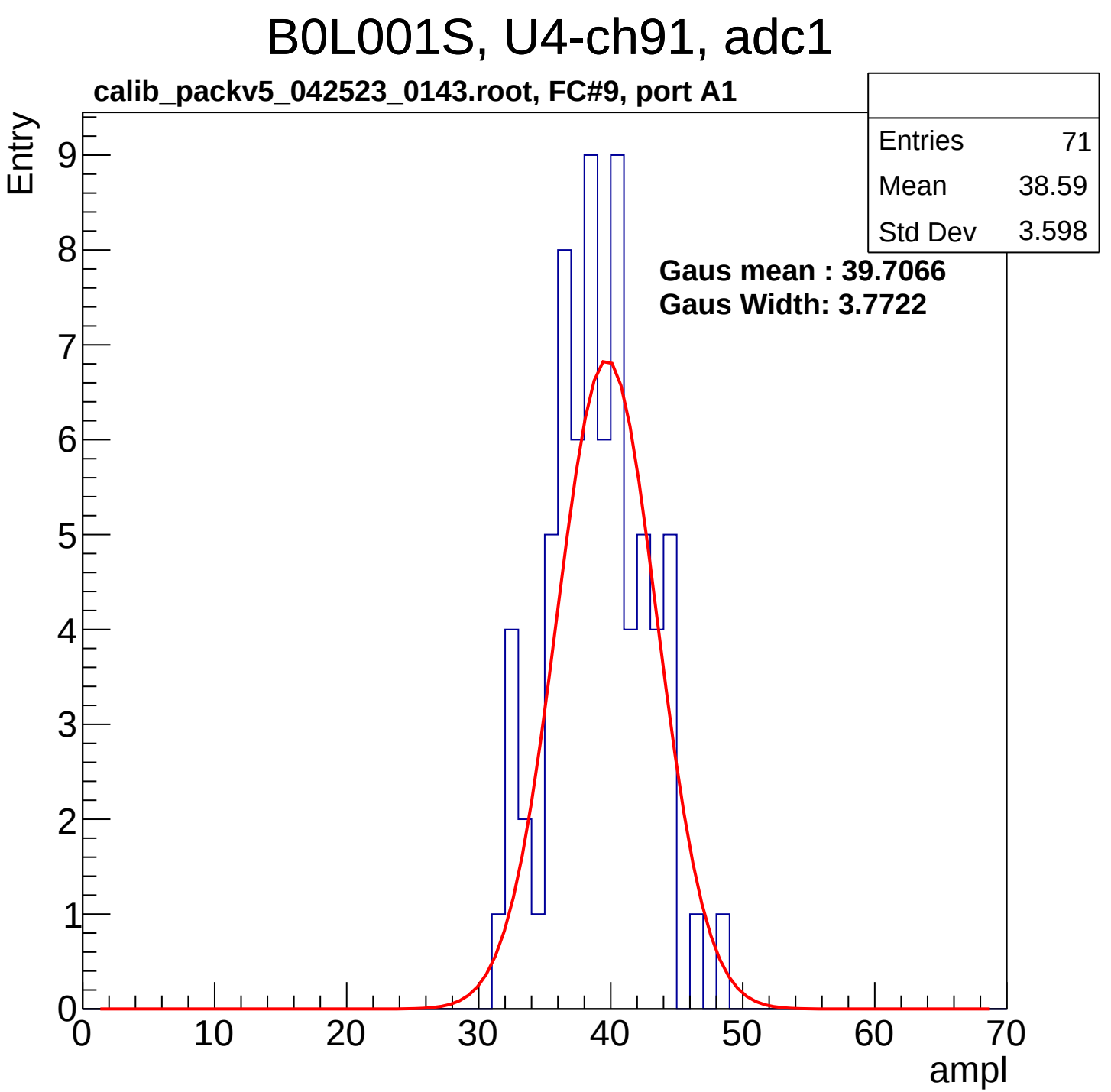
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch91, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 38.59
Std Dev 3.598
Gaus mean : 39.7066
Gaus Width: 3.7722
Entry
ampl
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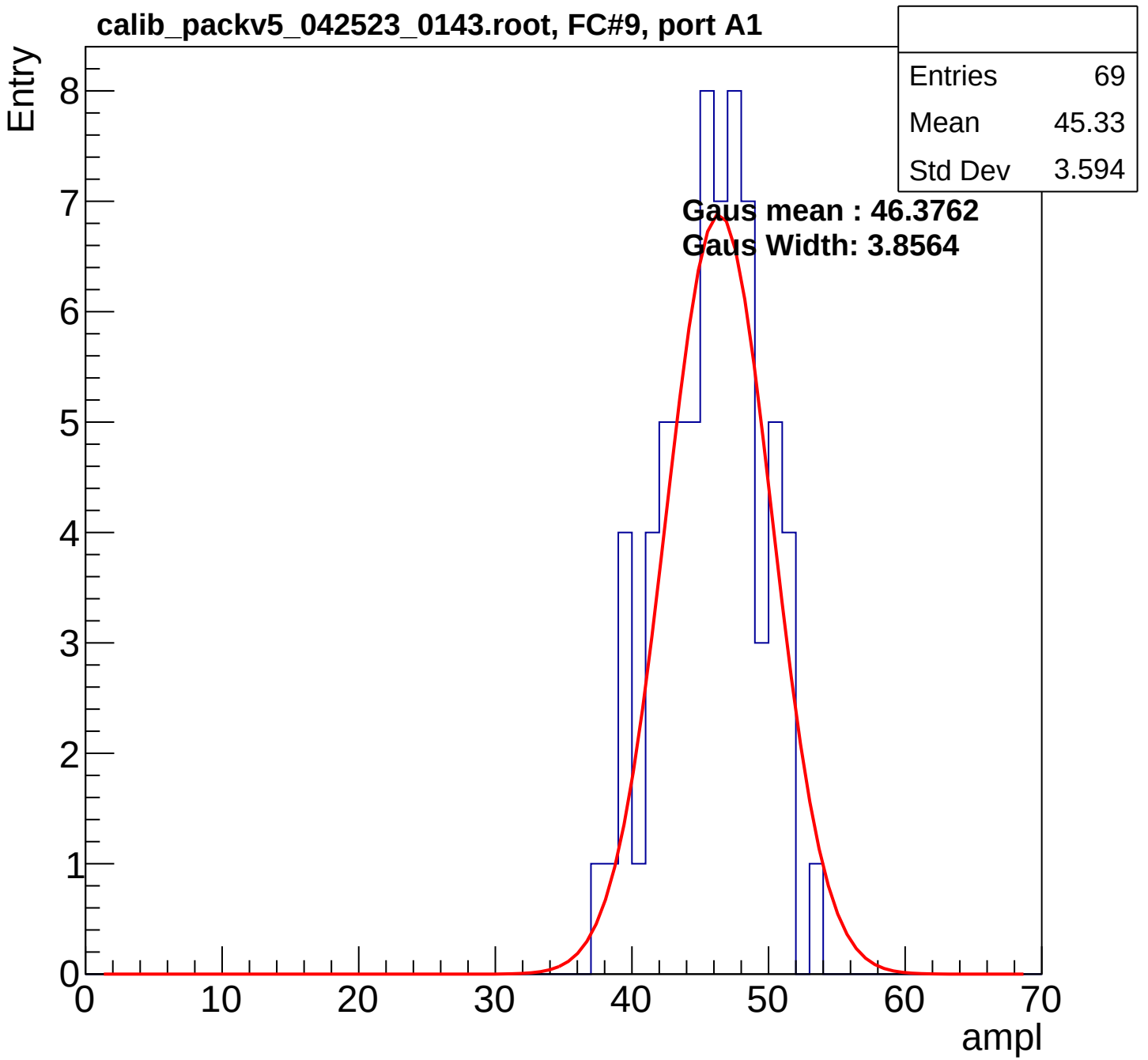
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch91, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 45.33
Std Dev 3.594
Gaus mean : 46.3762
Gaus Width: 3.8564
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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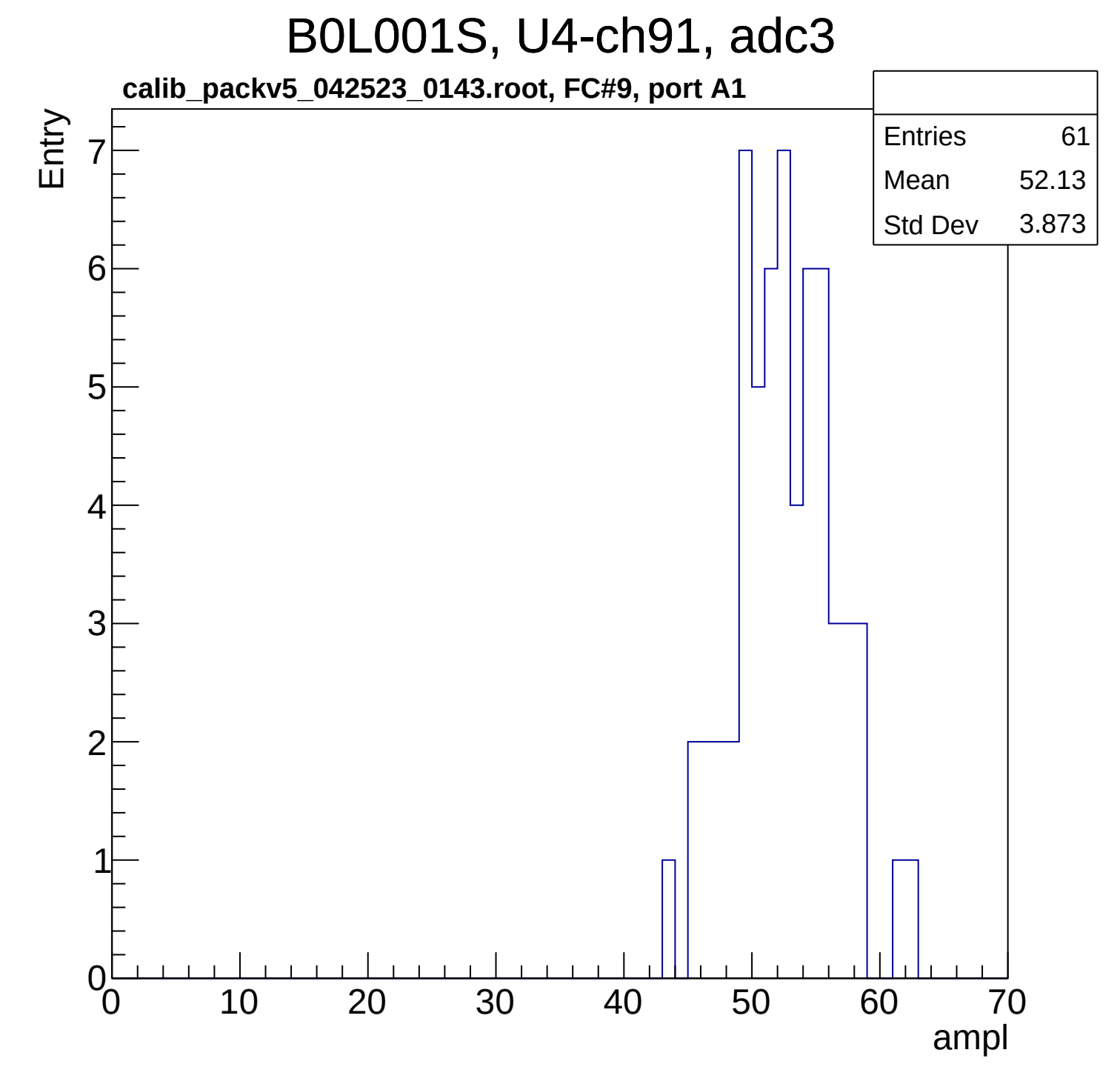
B0L001S, U4-ch91, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 52.13
Std Dev 3.873
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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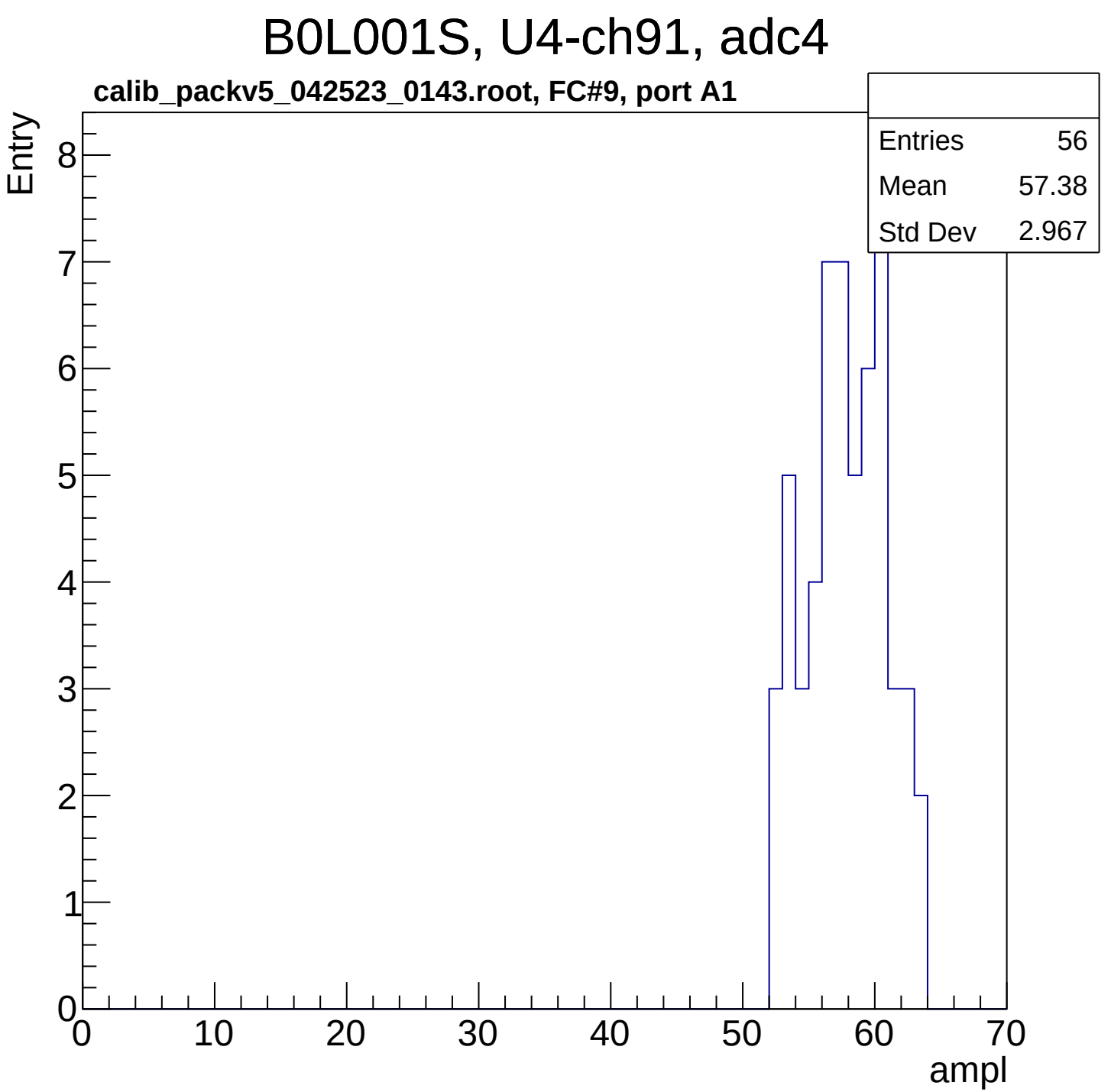
B0L001S, U4-ch91, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.38
Std Dev 2.967
Entry
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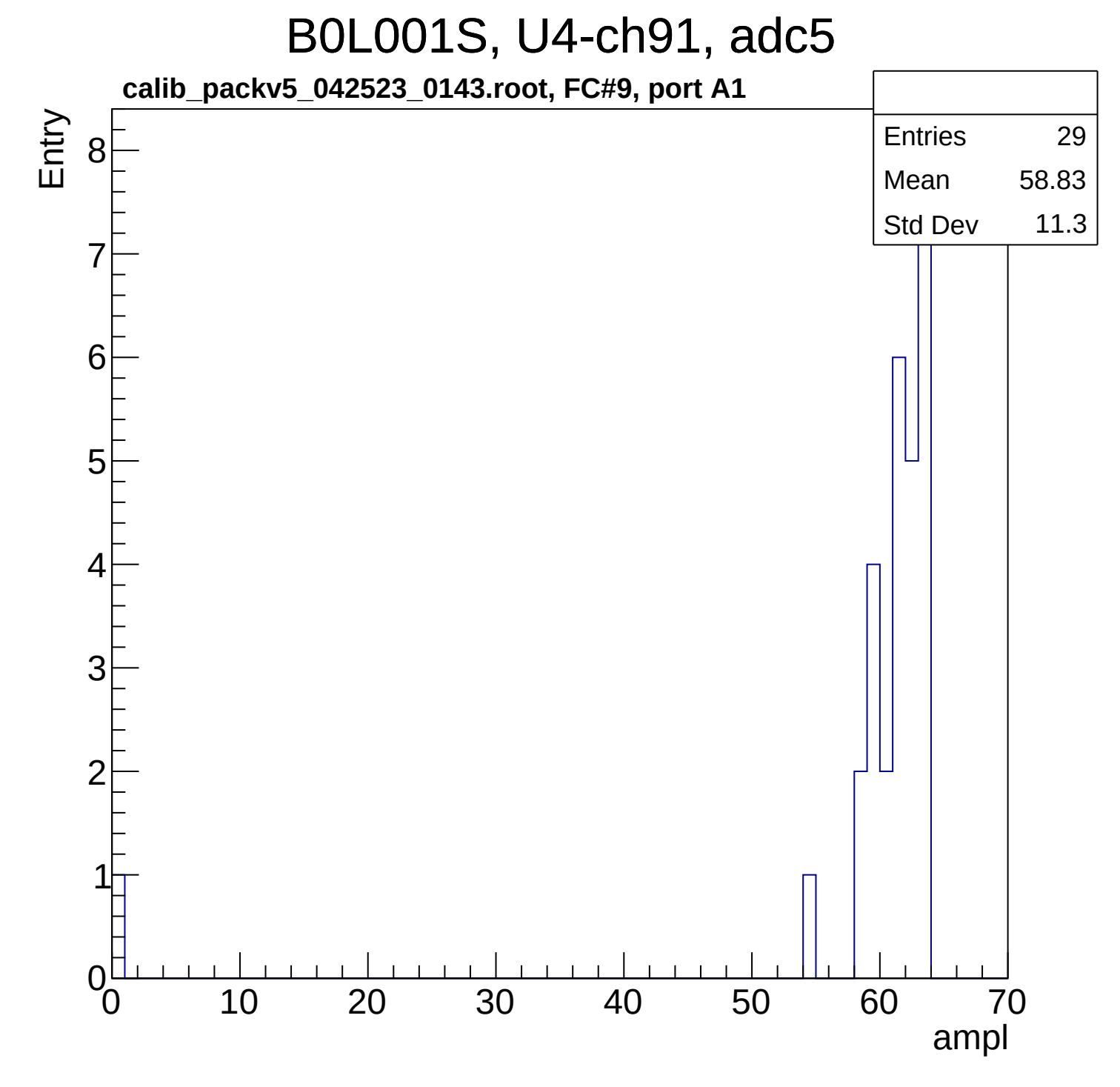
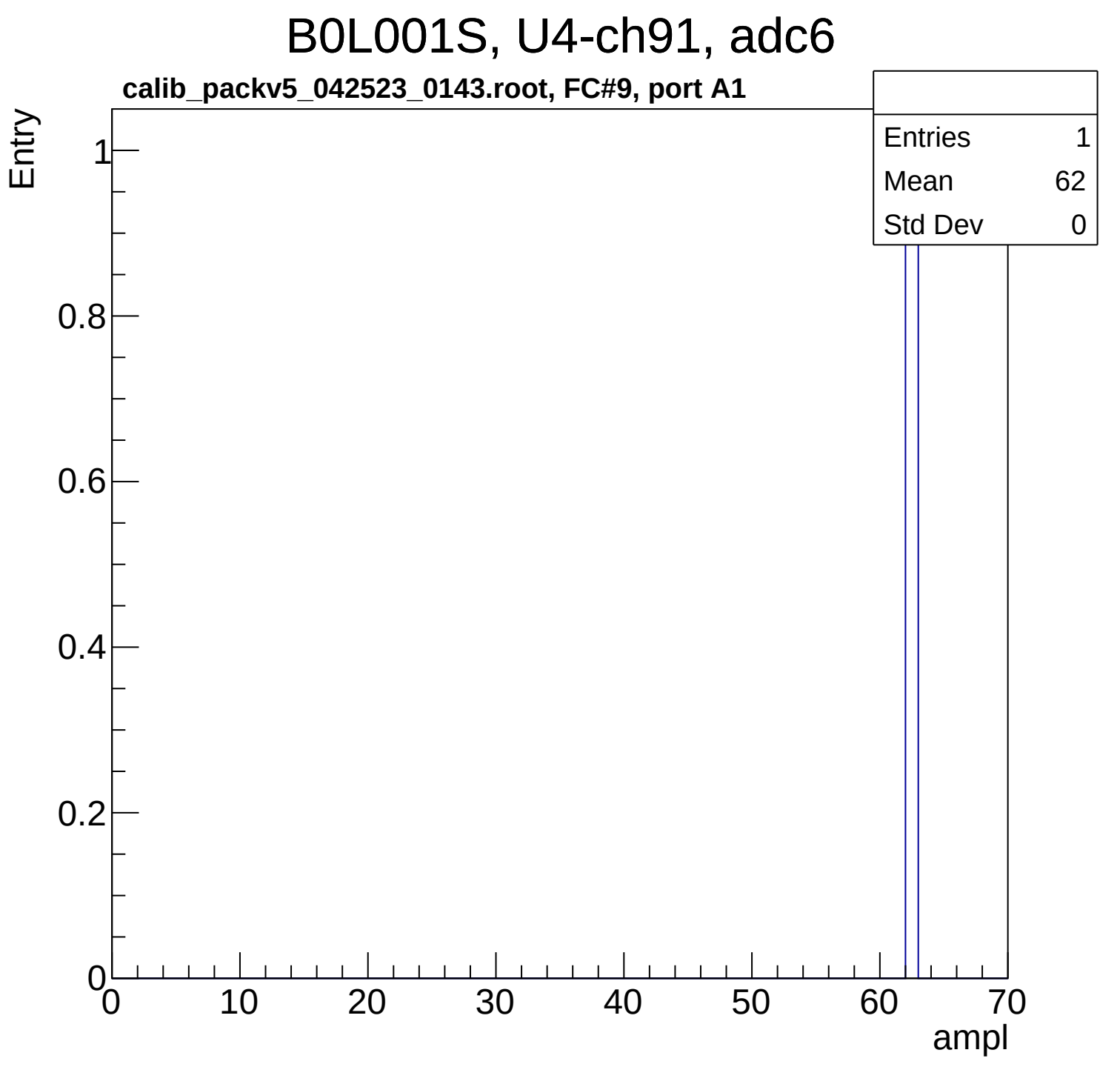
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch91, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.83
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch91, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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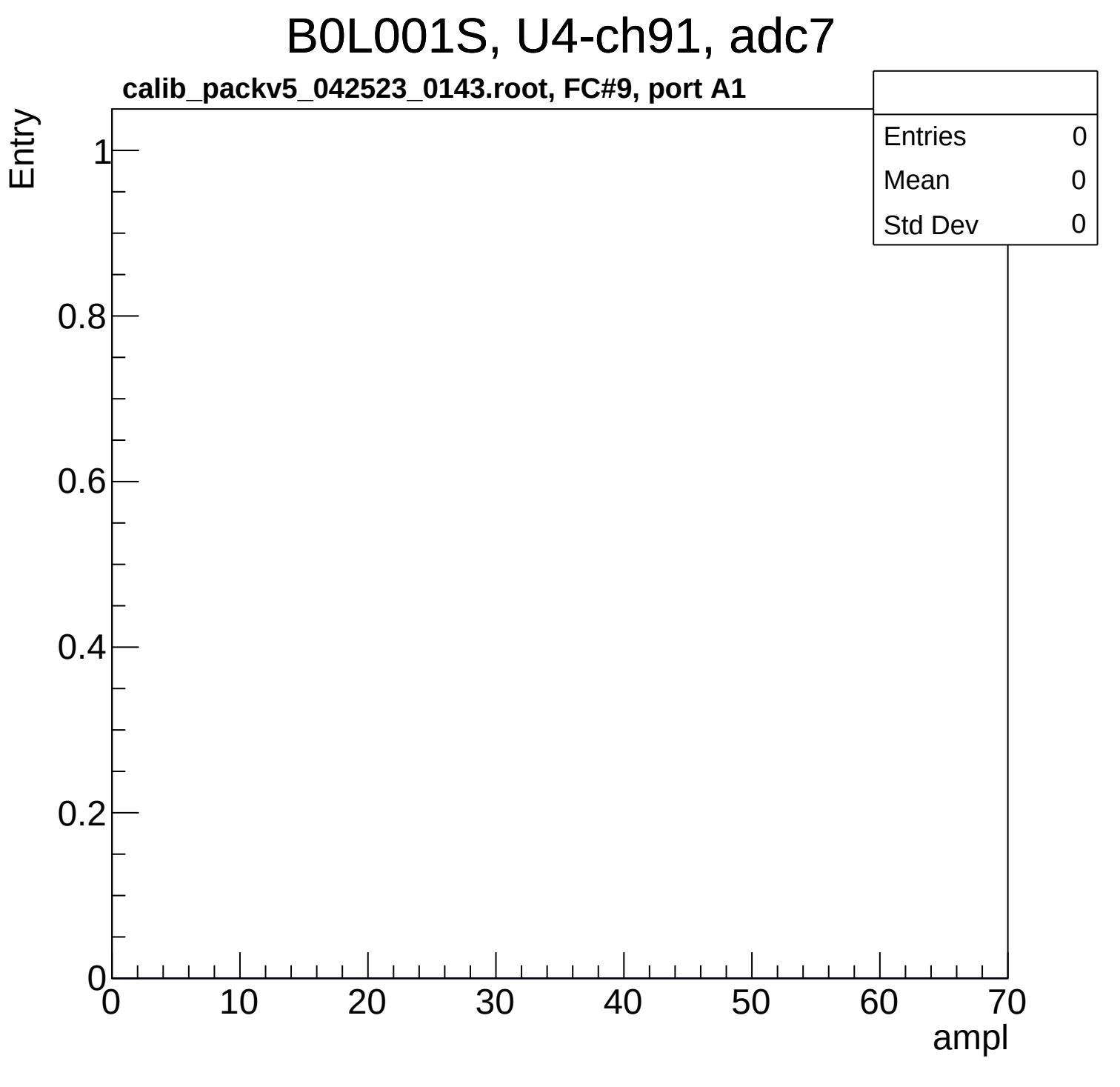
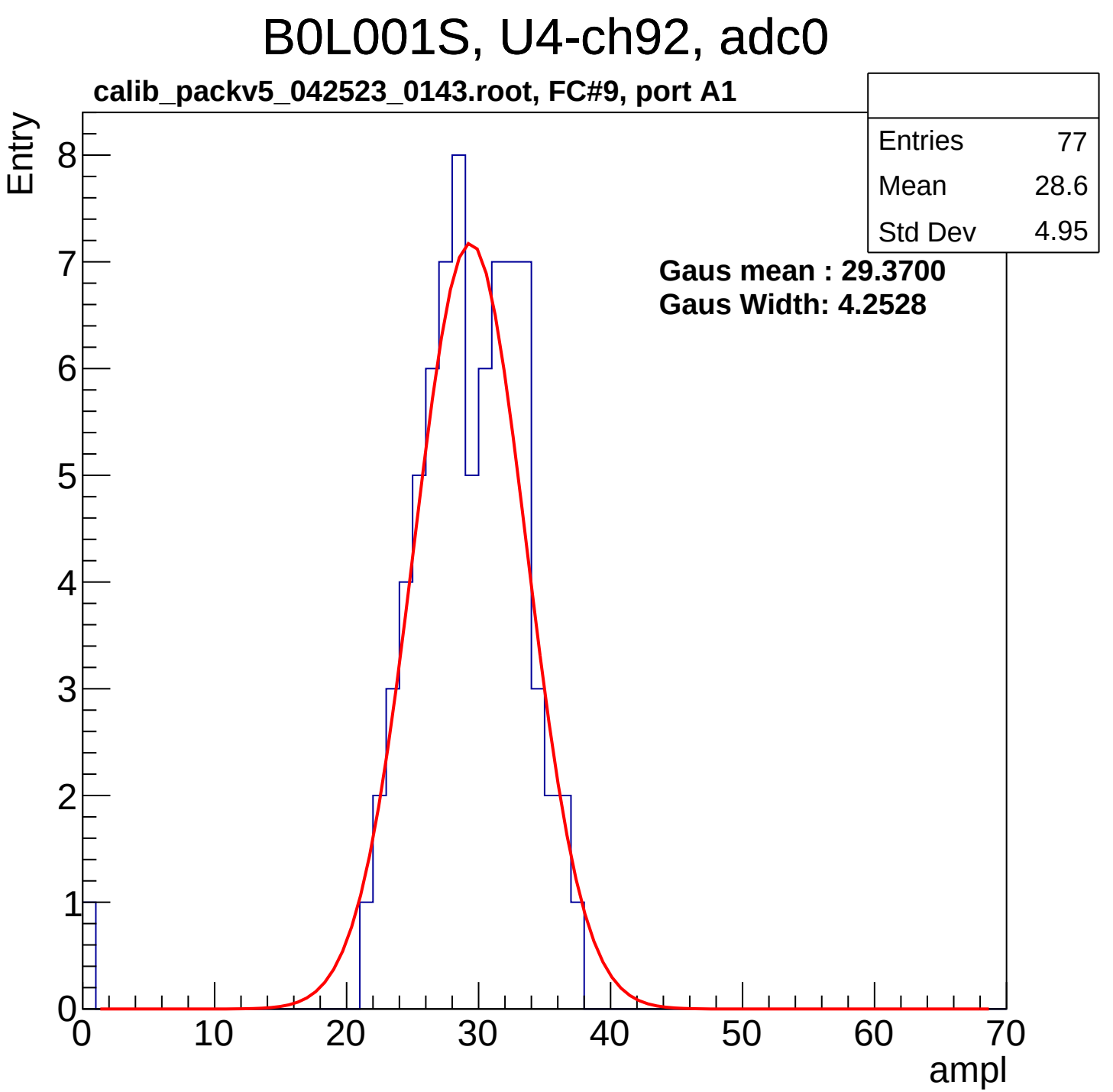
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch91, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch92, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 28.6
Std Dev 4.95
Gaus mean : 29.3700
Gaus Width: 4.2528
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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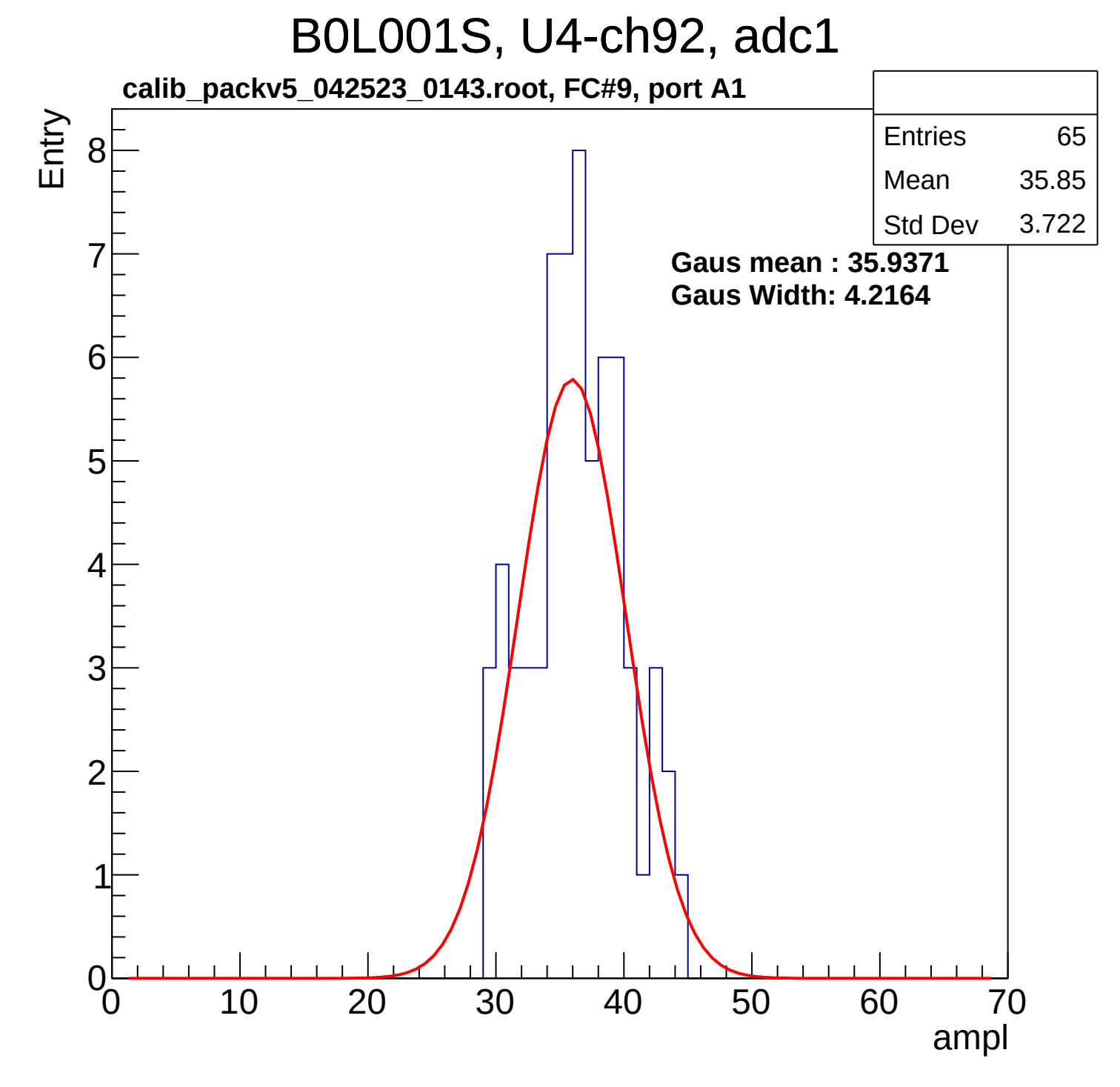
B0L001S, U4-ch92, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 35.85
Std Dev 3.722
Gaus mean : 35.9371
Gaus Width: 4.2164
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
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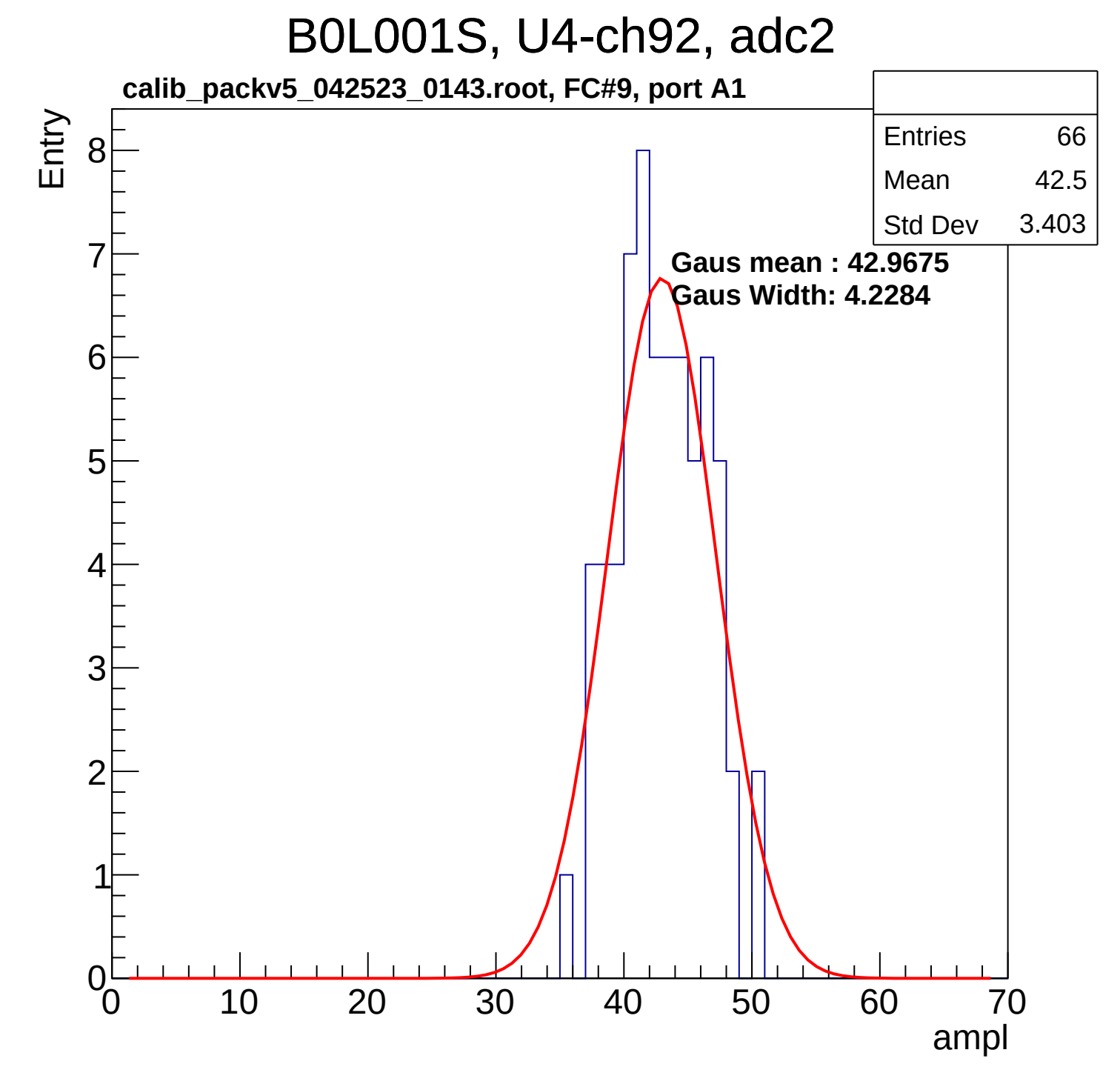
B0L001S, U4-ch92, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.5
Std Dev 3.403
Gaus mean : 42.9675
Gaus Width: 4.2284
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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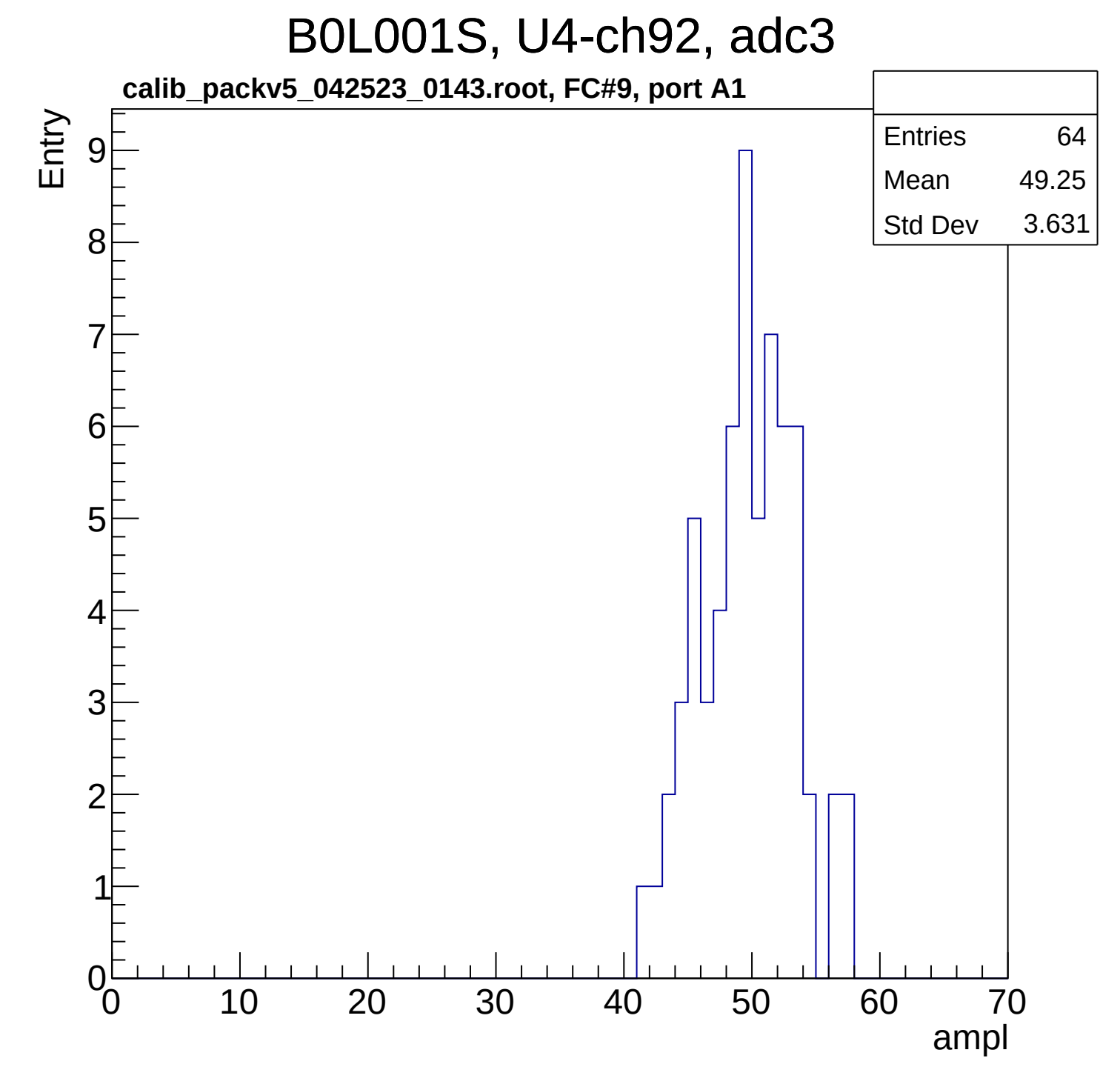
B0L001S, U4-ch92, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 49.25
Std Dev 3.631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
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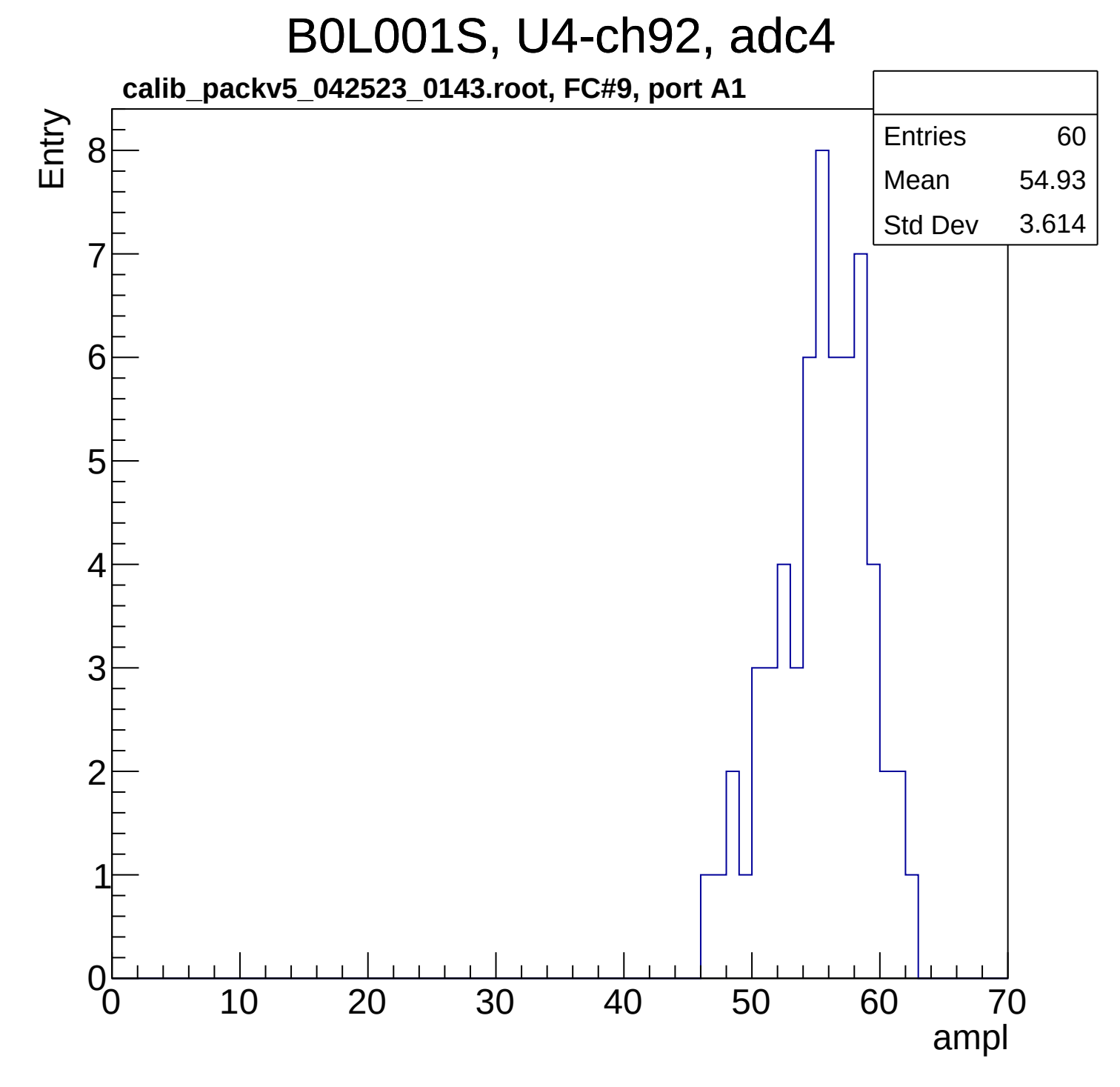
B0L001S, U4-ch92, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 54.93
Std Dev 3.614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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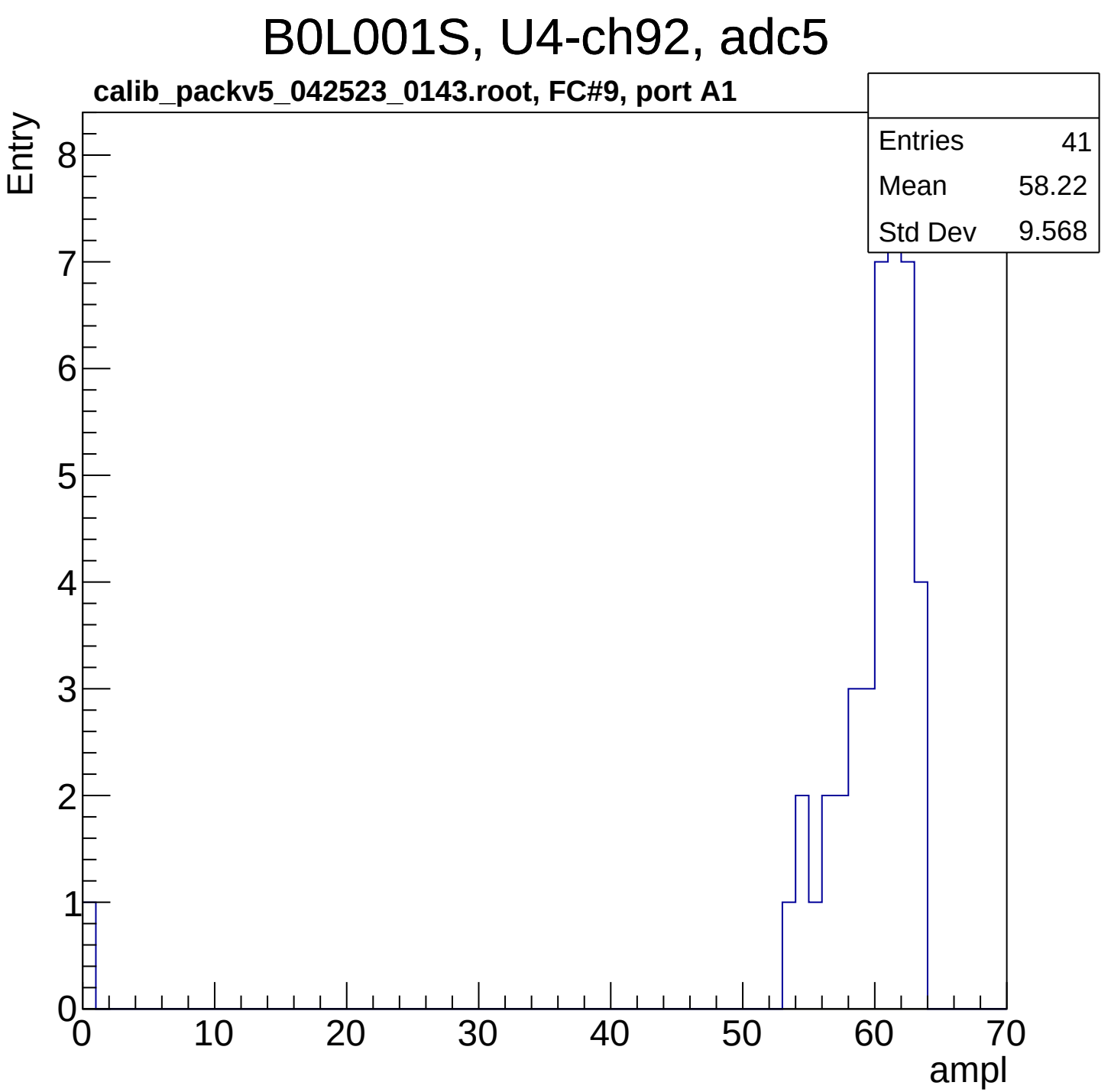
B0L001S, U4-ch92, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.22
Std Dev 9.568
Entry
ampl
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1
2
3
4
5
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7
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0
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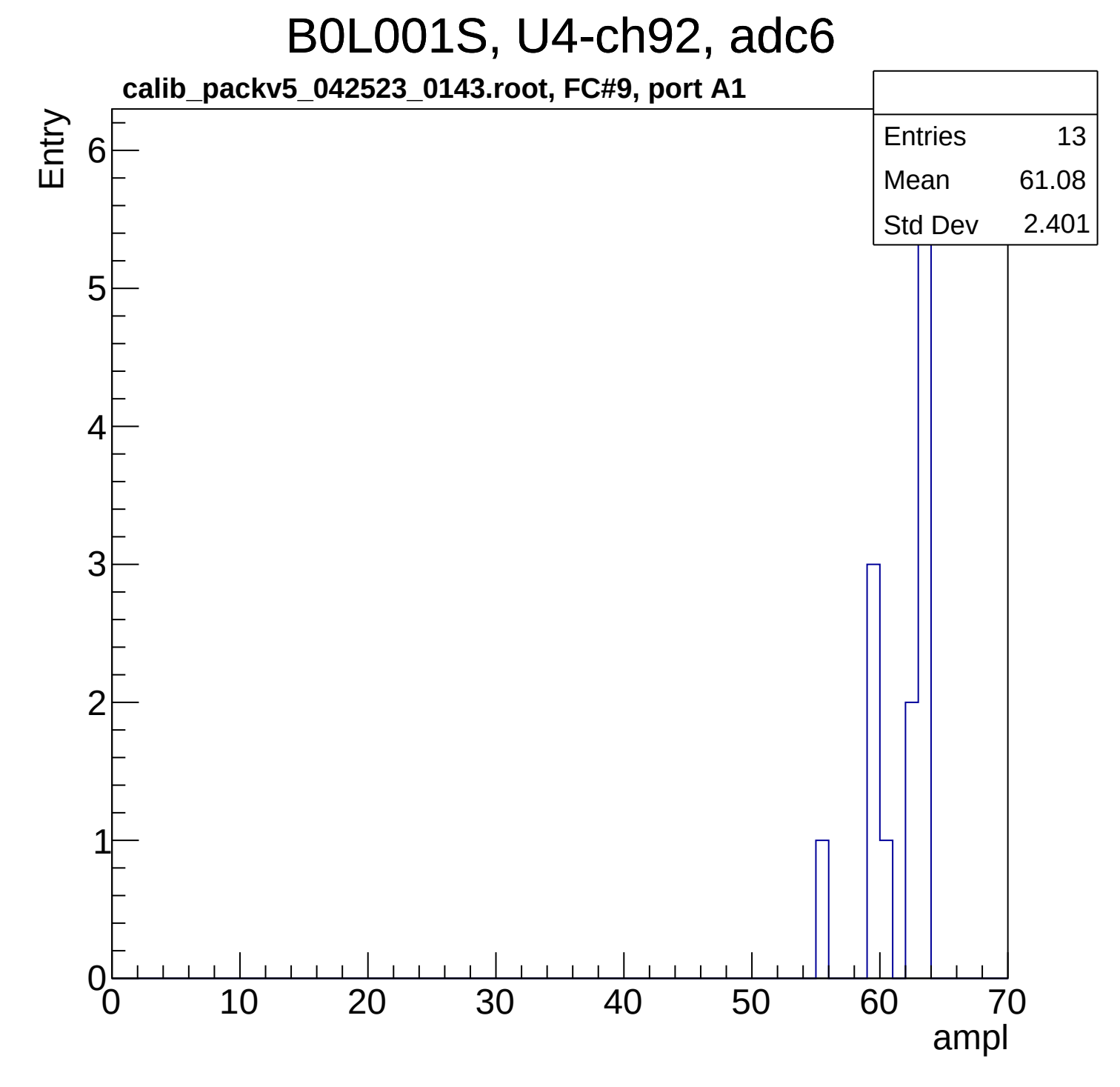
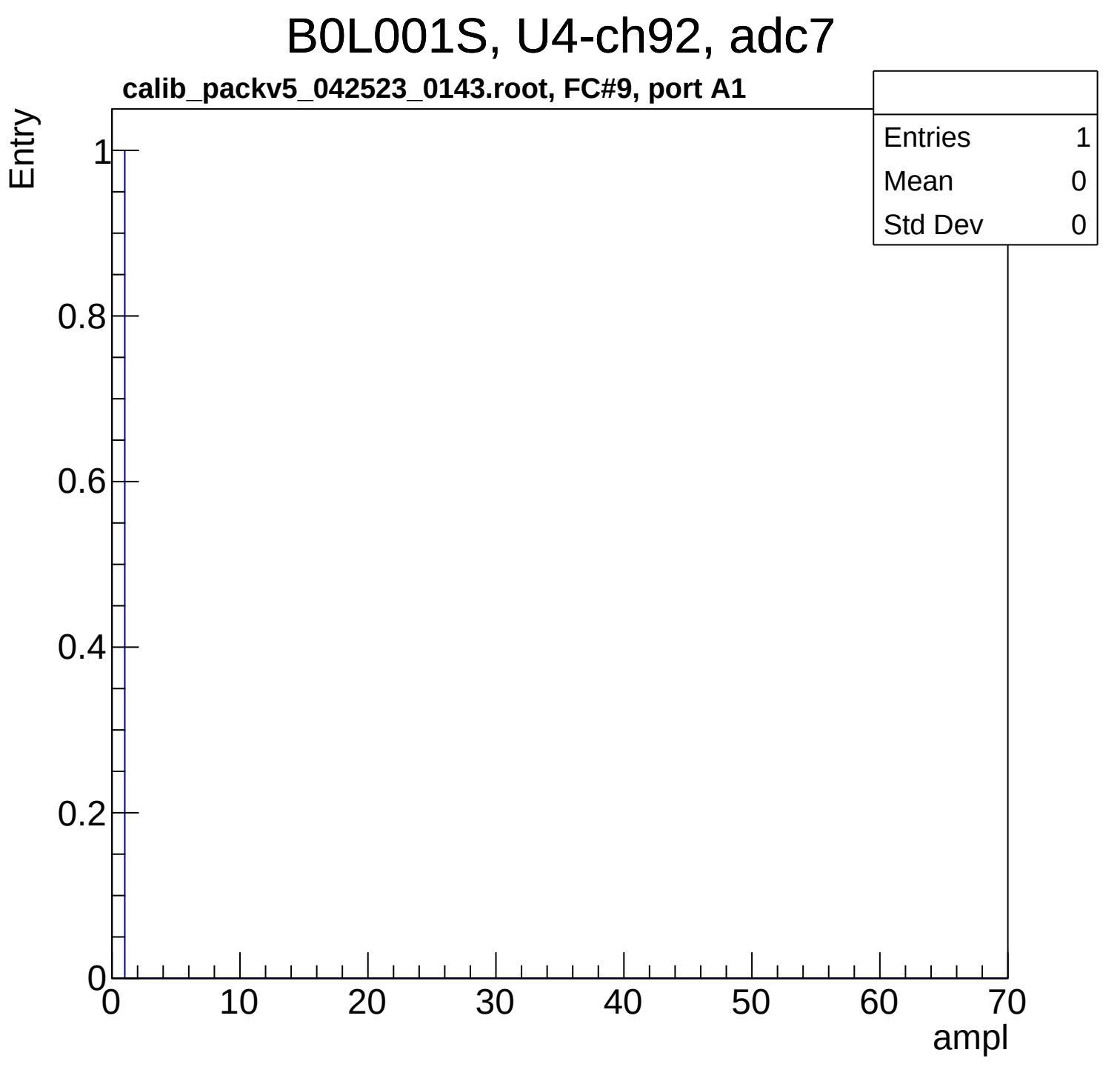
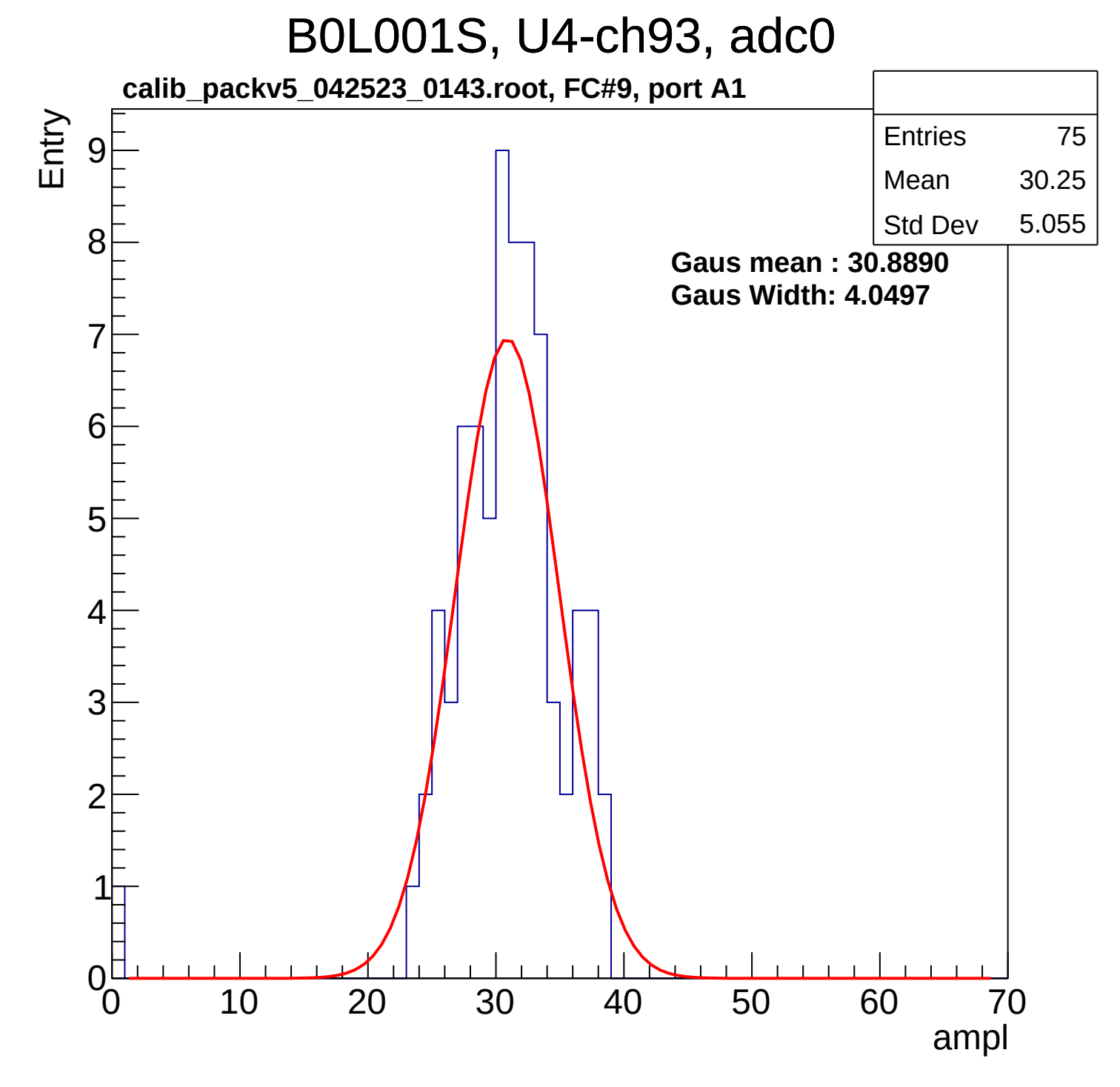

B0L001S, U4-ch92, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 61.08
Std Dev 2.401
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch92, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch93, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.25
Std Dev 5.055
Gaus mean : 30.8890
Gaus Width: 4.0497
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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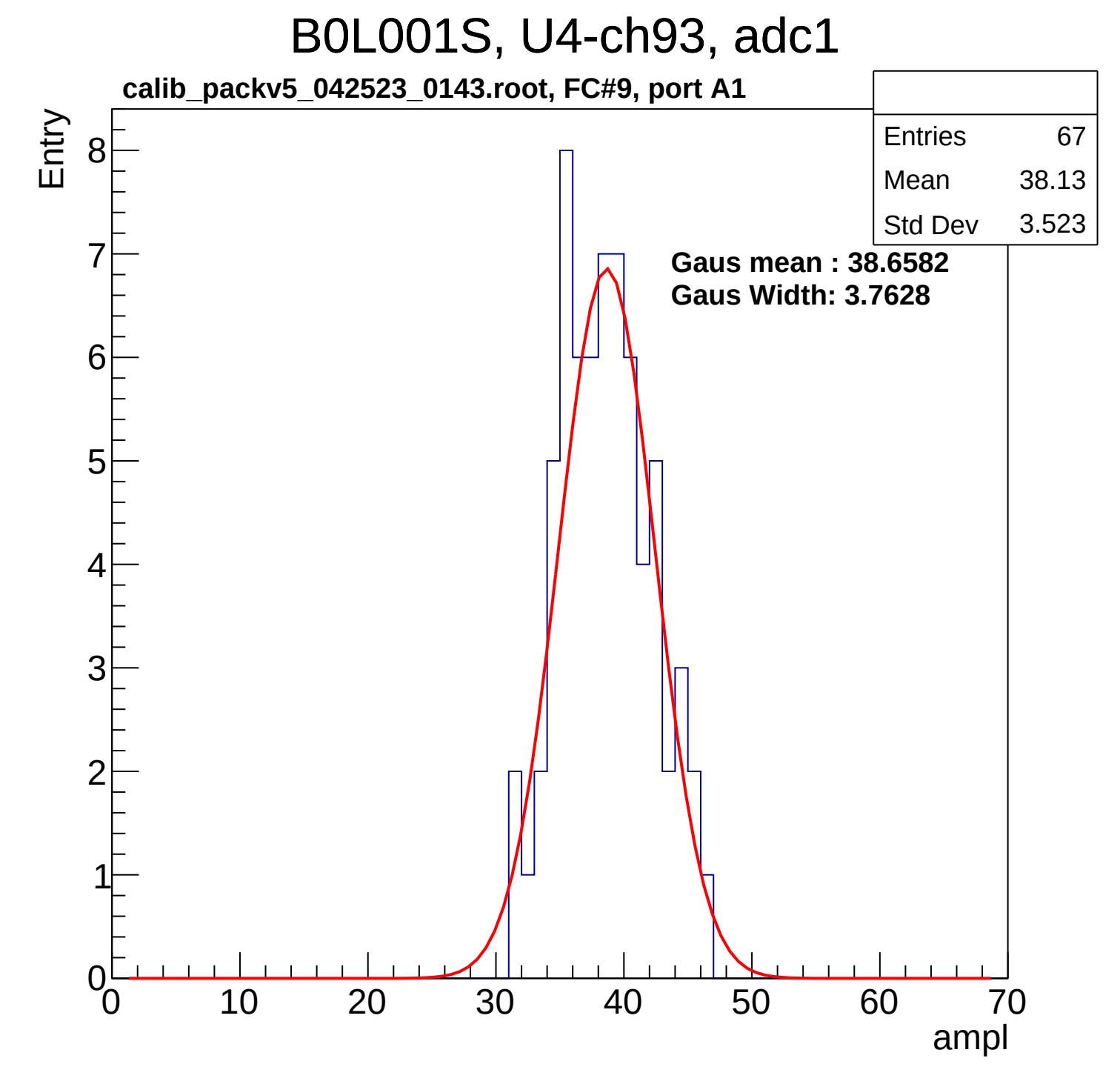
B0L001S, U4-ch93, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.13
Std Dev 3.523
Gaus mean : 38.6582
Gaus Width: 3.7628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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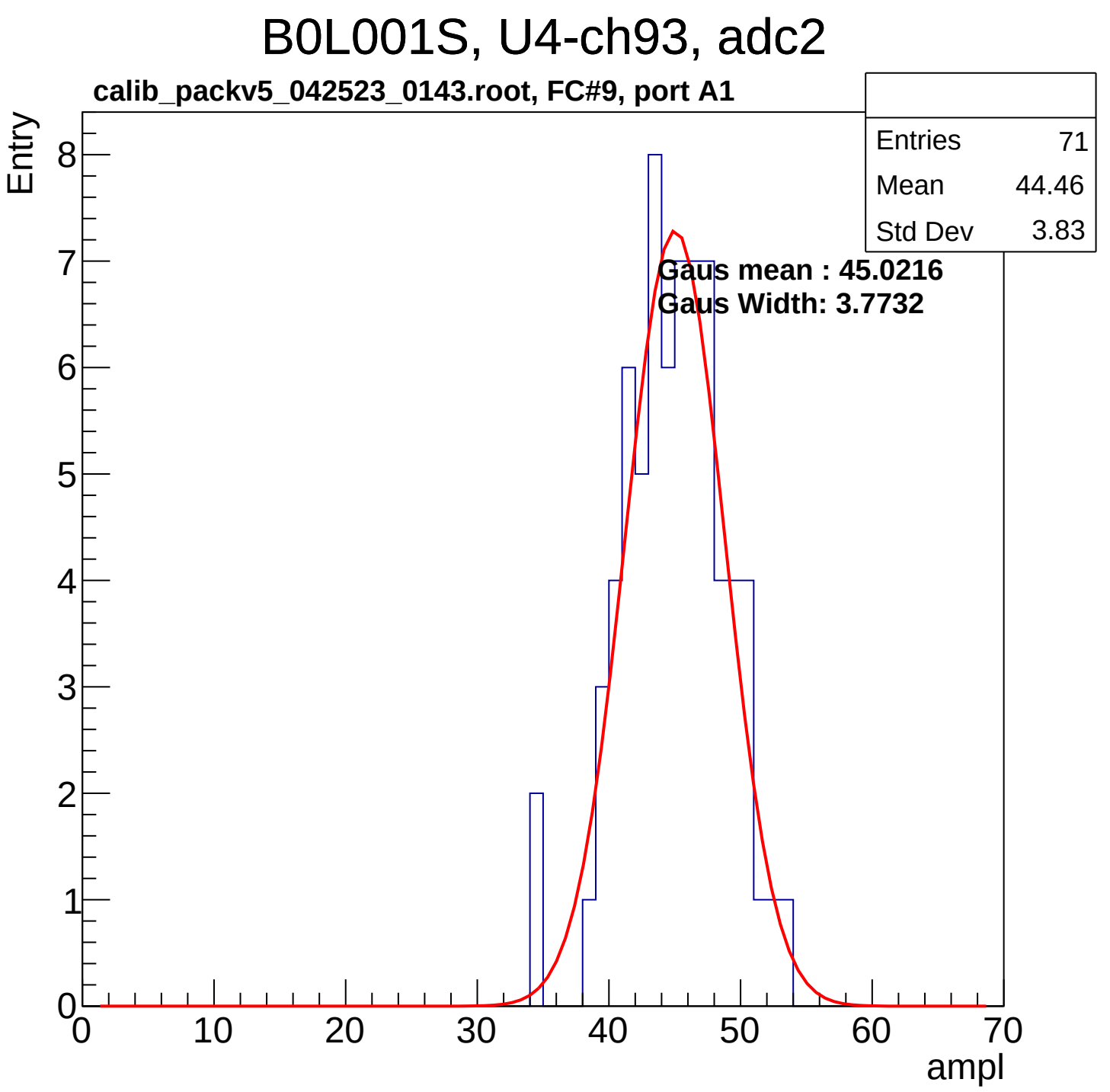
B0L001S, U4-ch93, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.46
Std Dev 3.83
Gaus mean : 45.0216
Gaus Width: 3.7732
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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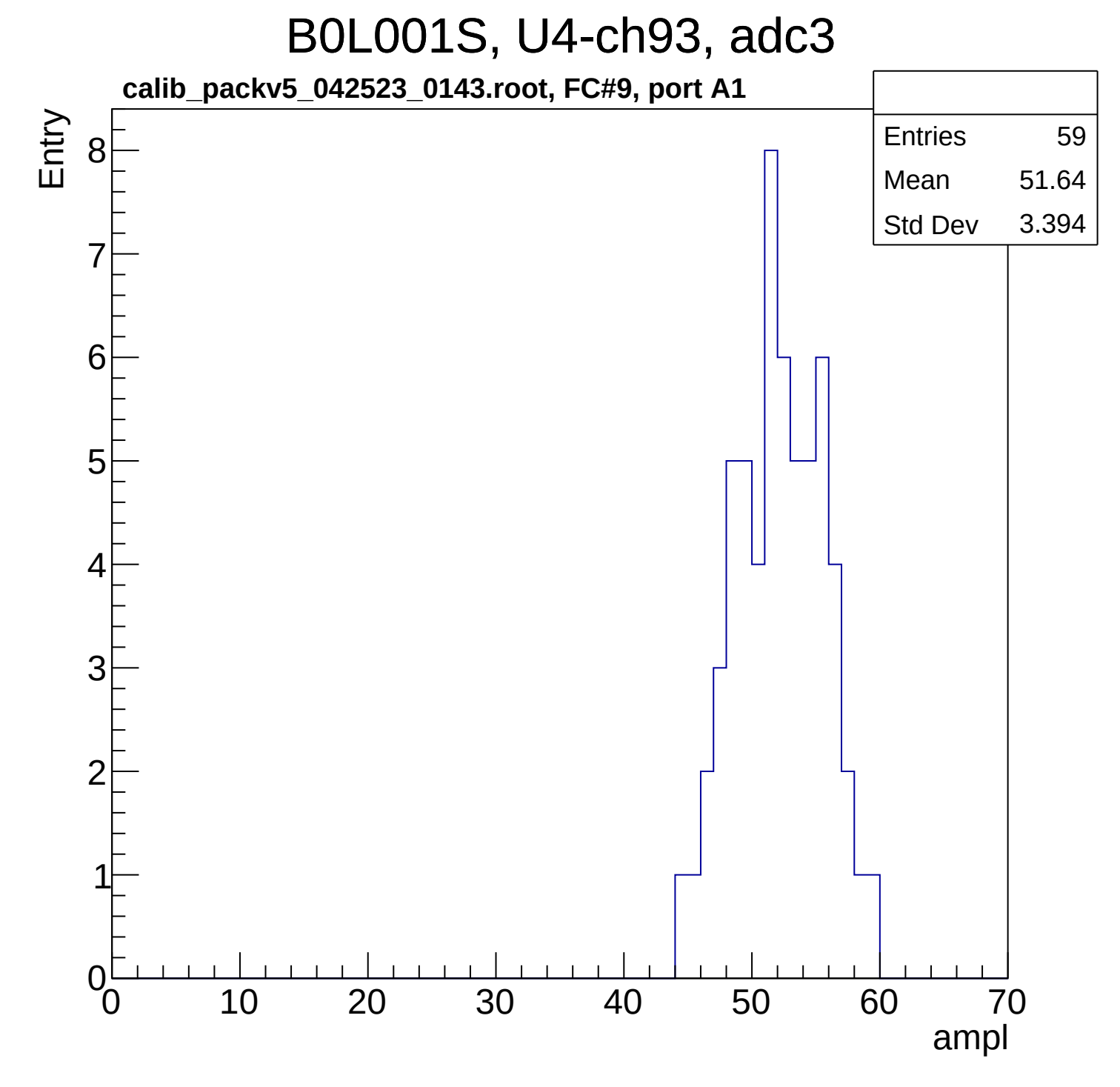
B0L001S, U4-ch93, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.64
Std Dev 3.394
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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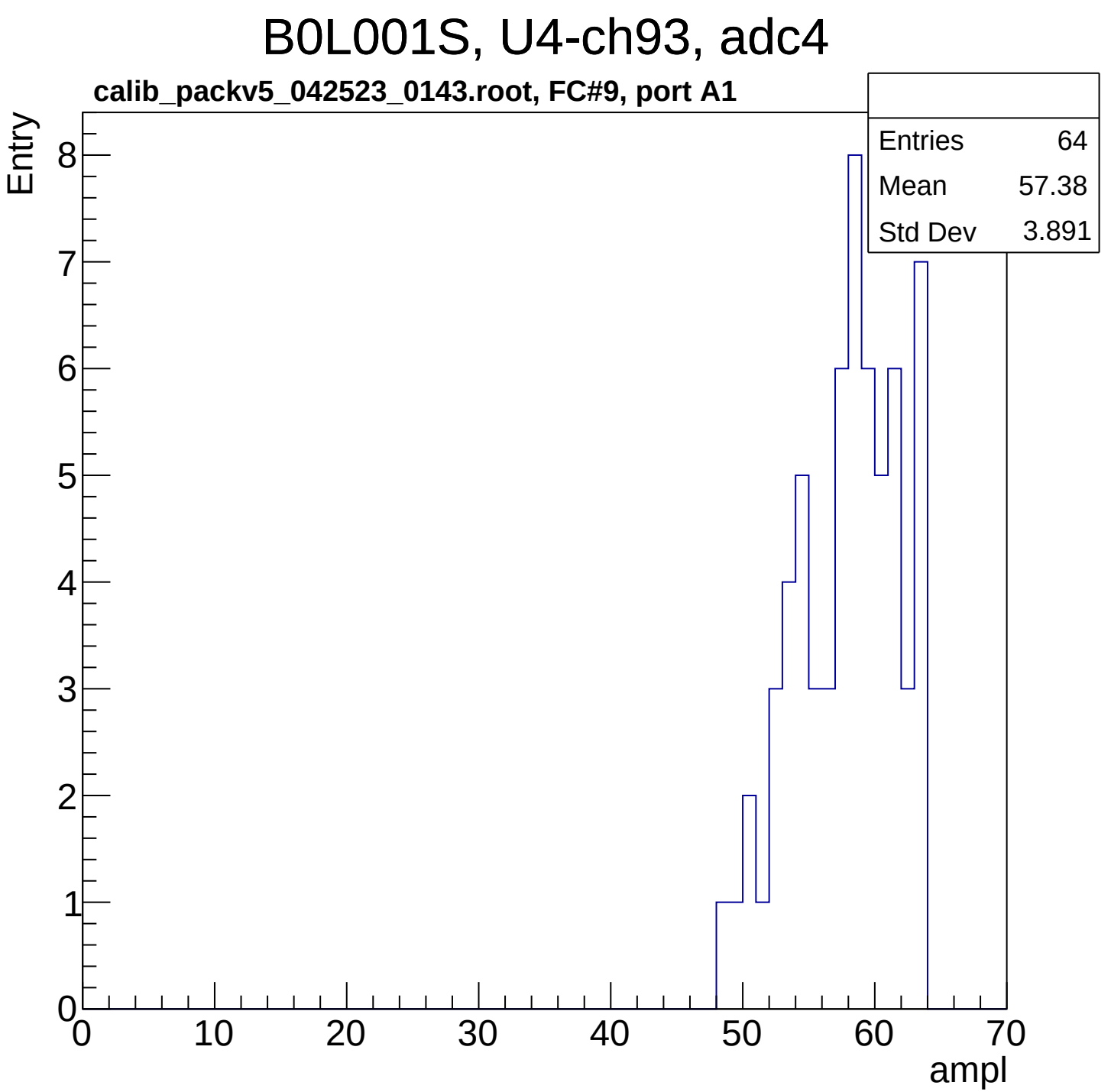
B0L001S, U4-ch93, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.38
Std Dev 3.891
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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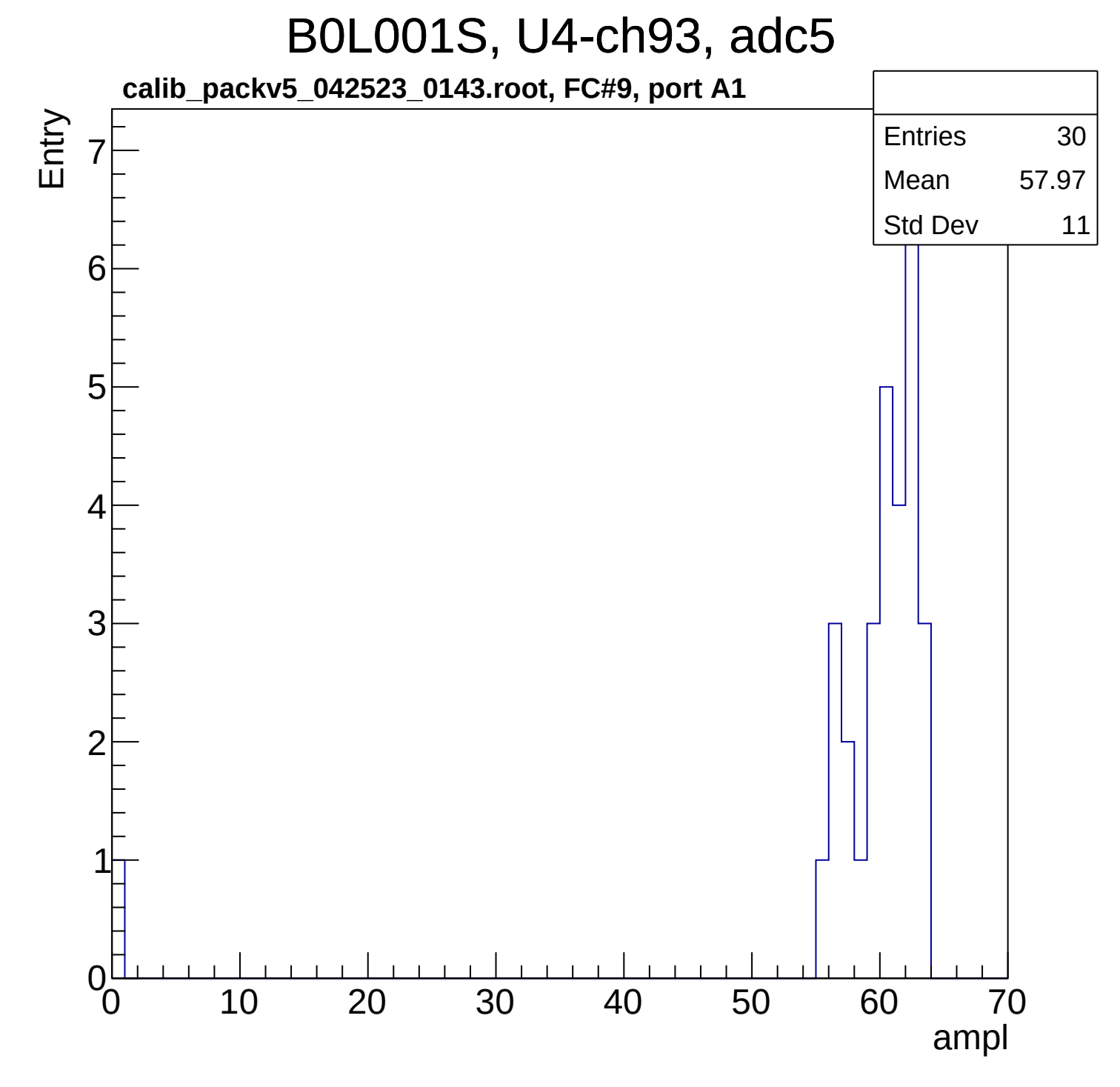
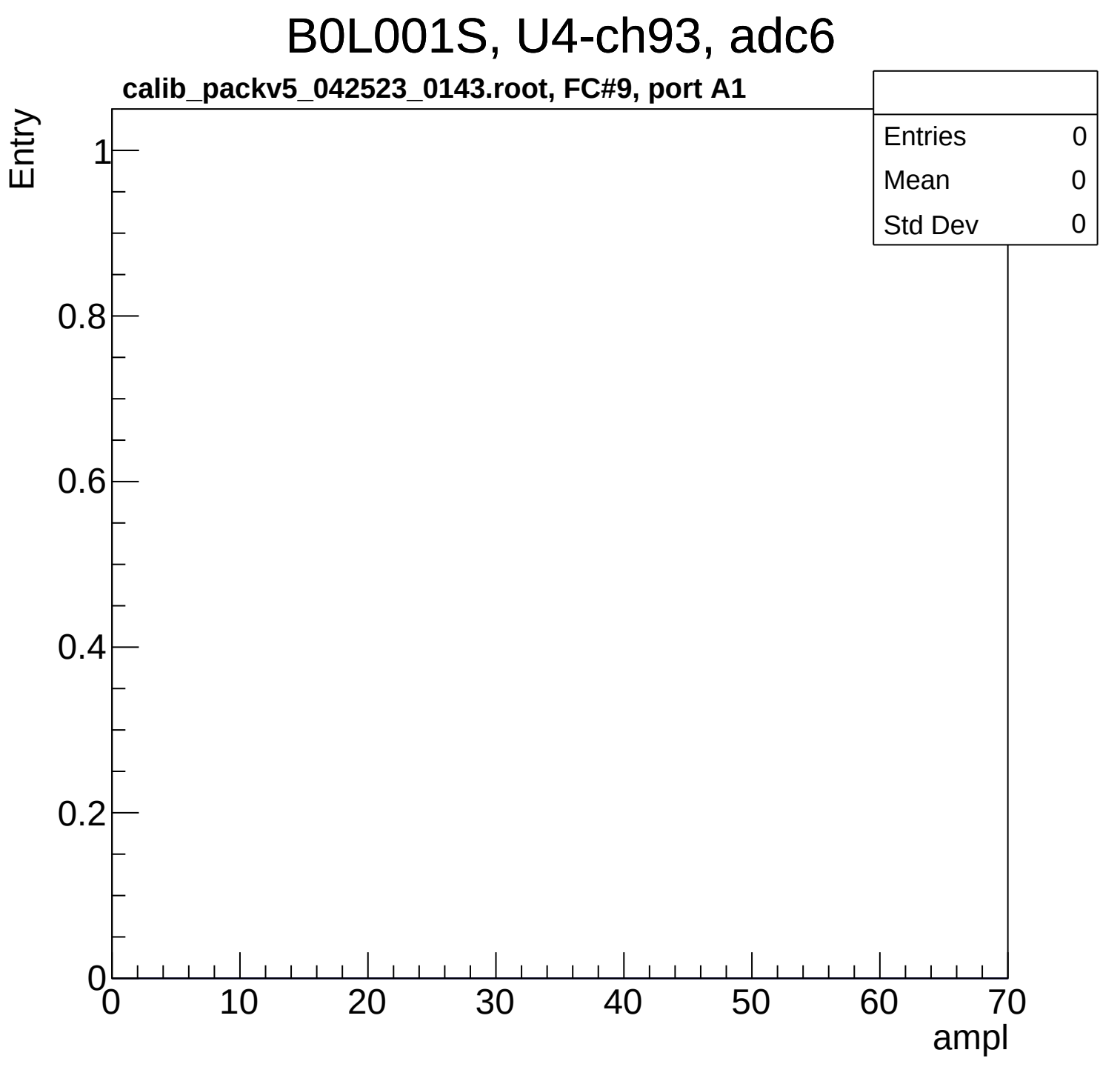
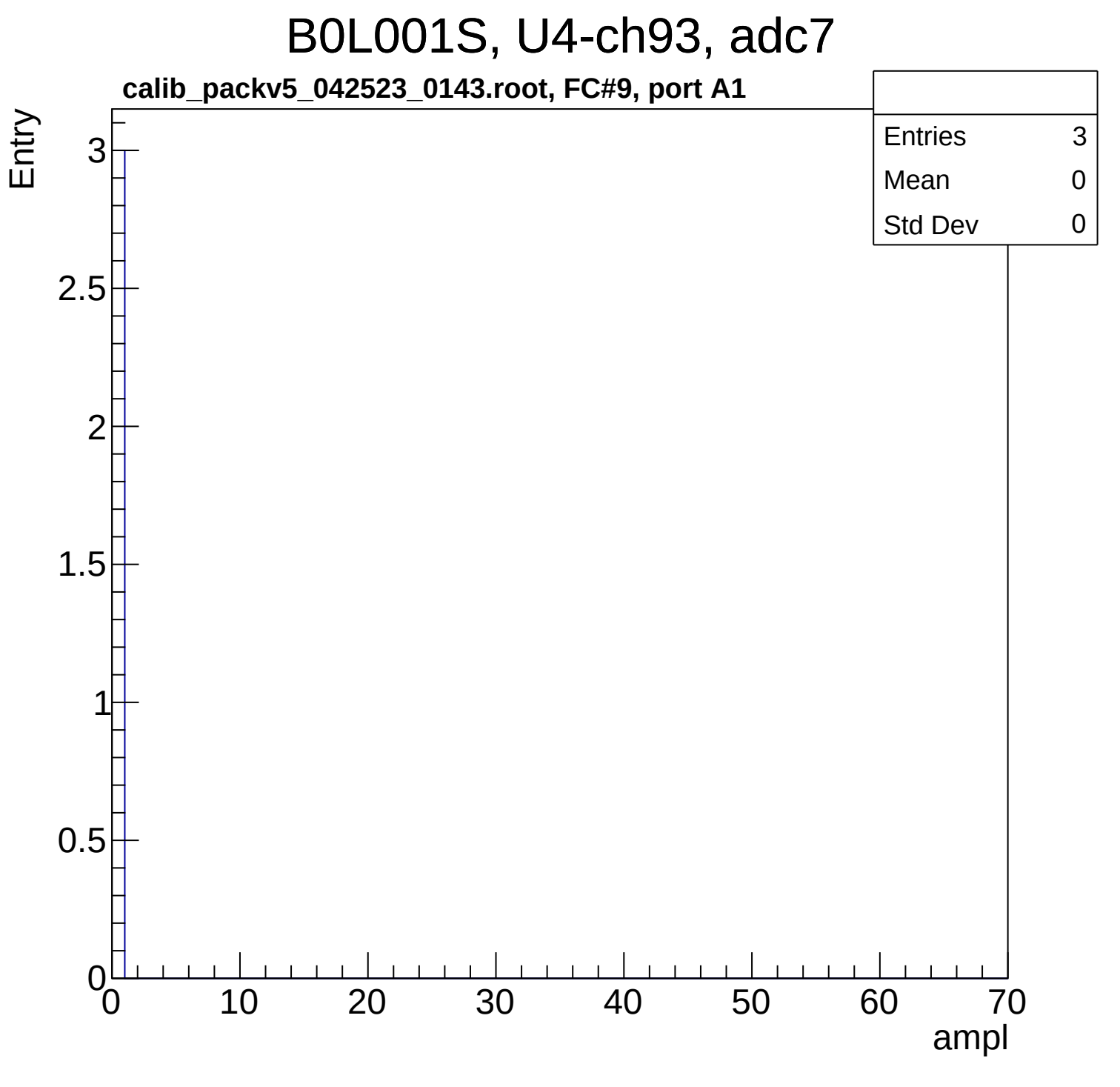

B0L001S, U4-ch93, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 57.97
Std Dev 11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch93, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch93, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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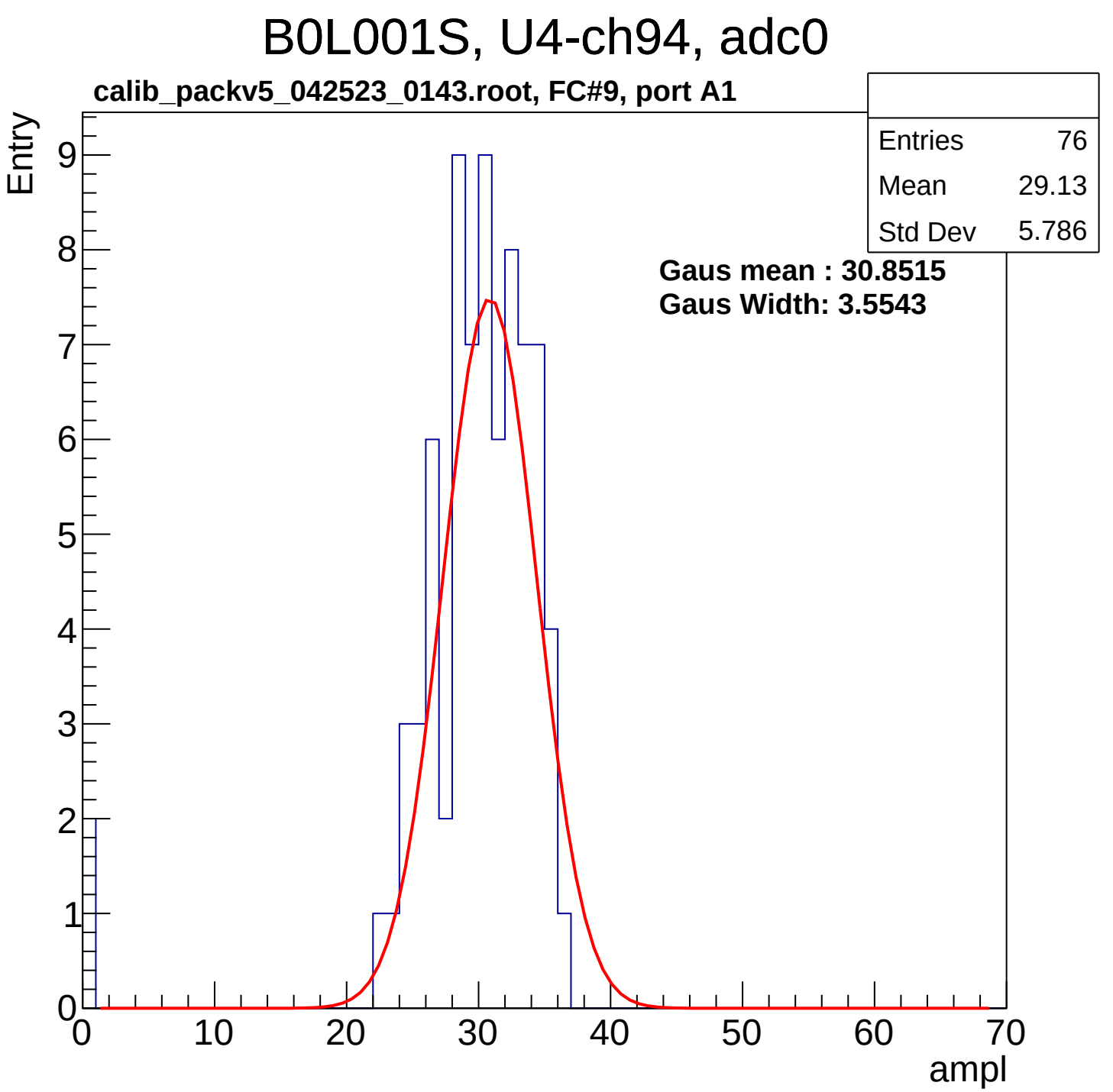
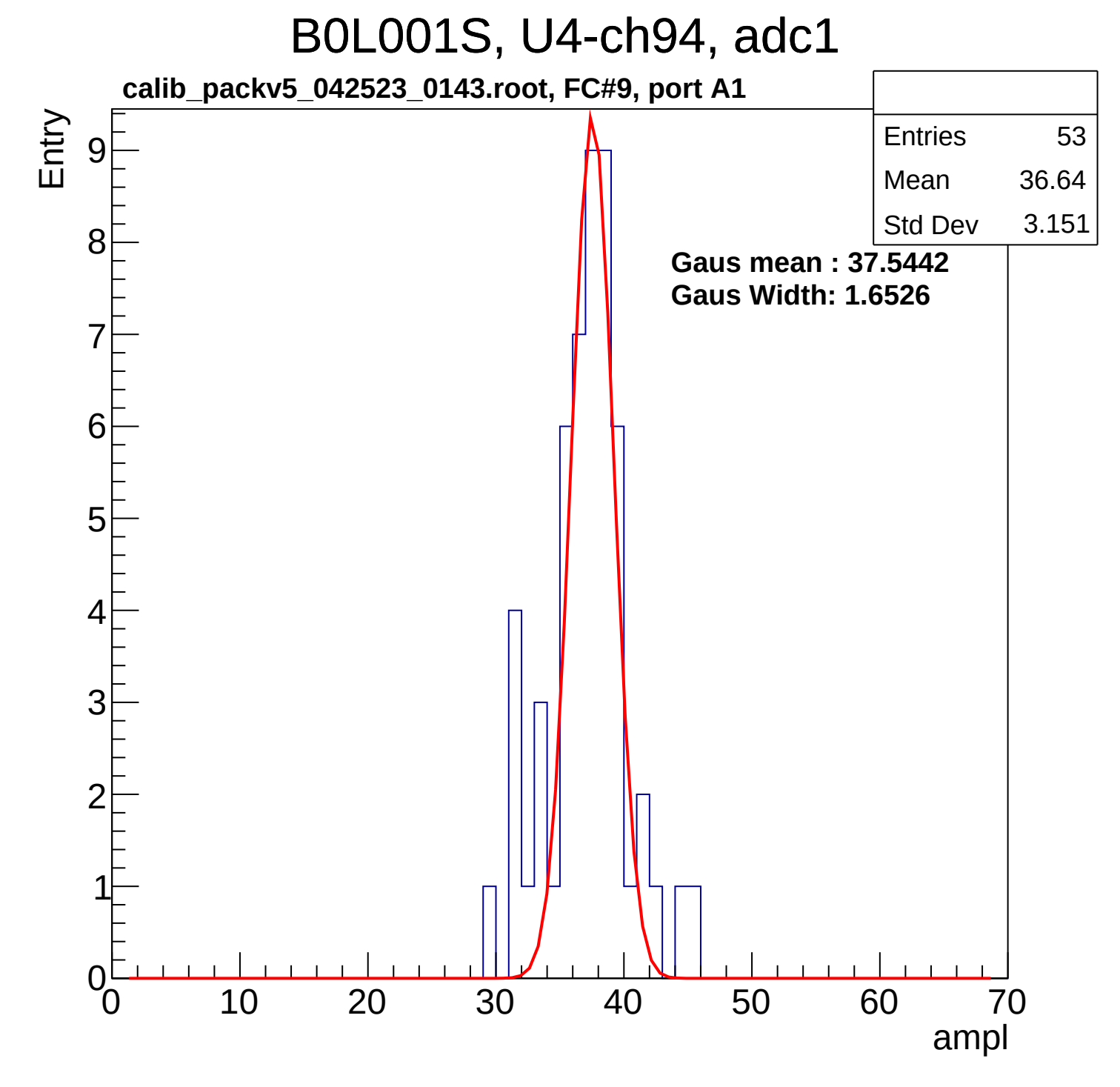
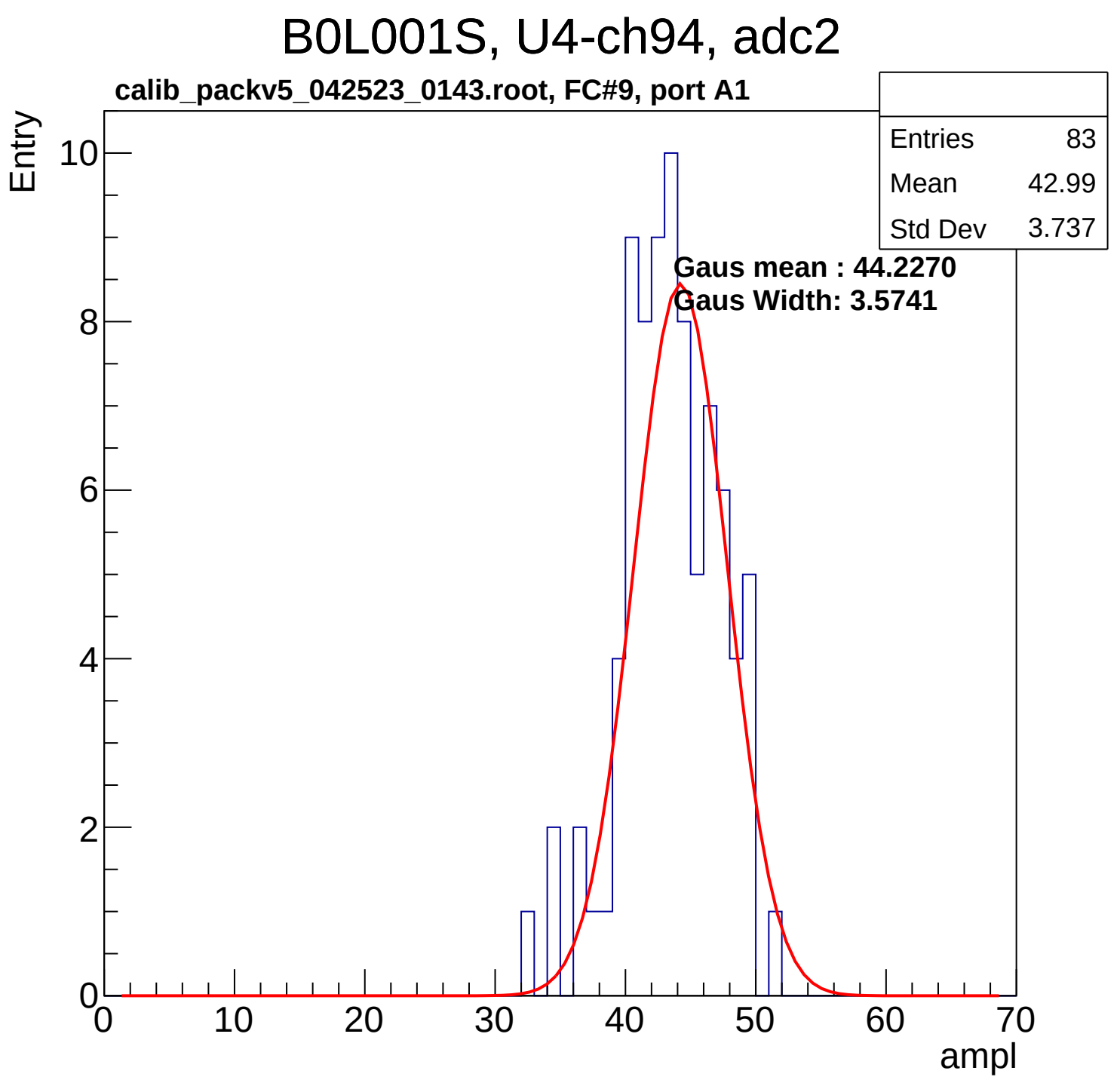
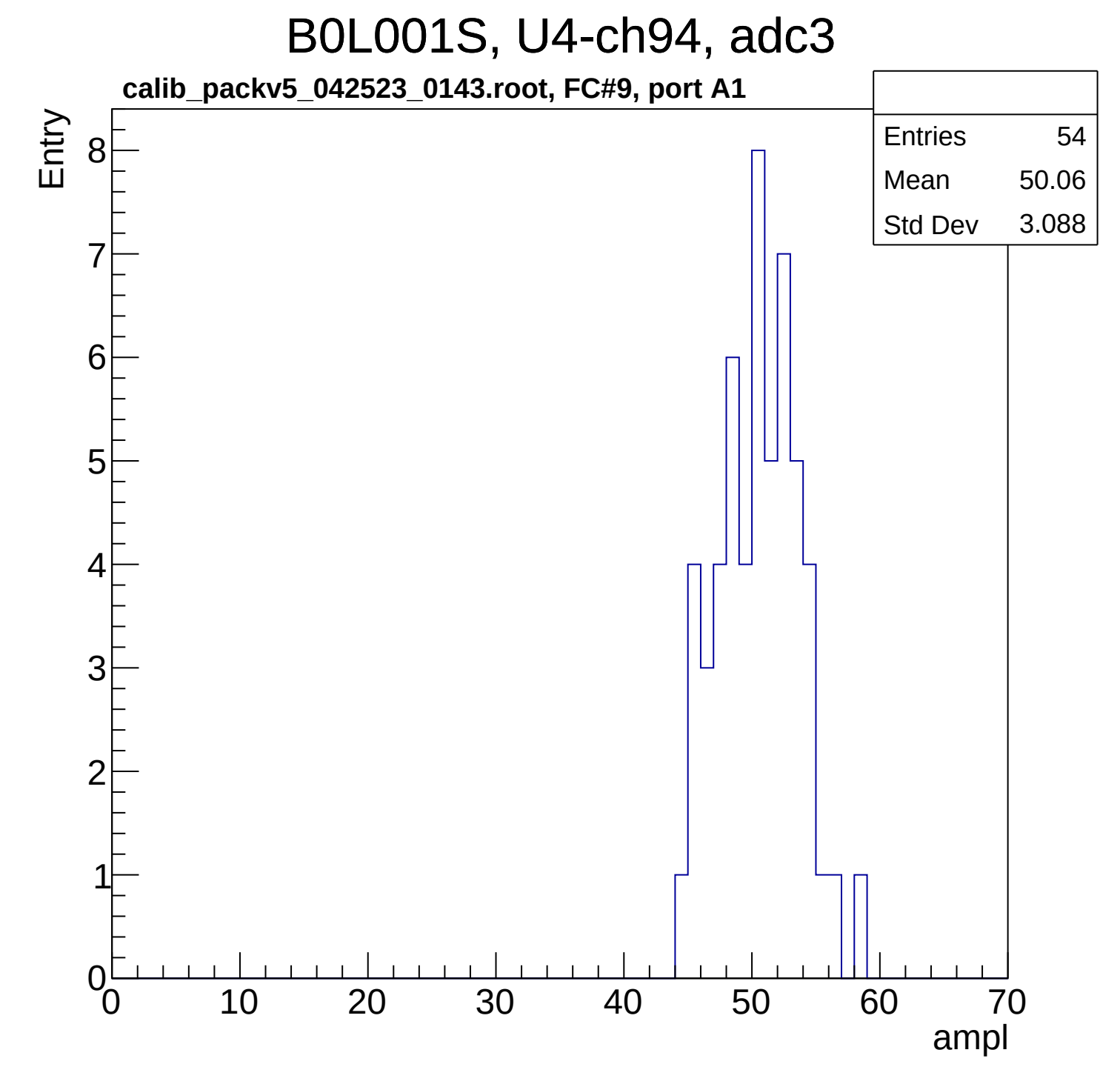
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch94, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 29.13
Std Dev 5.786
Gaus mean : 30.8515
Gaus Width: 3.5543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch94, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 36.64
Std Dev 3.151
Gaus mean : 37.5442
Gaus Width: 1.6526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch94, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 42.99
Std Dev 3.737
Gaus mean : 44.2270
Gaus Width: 3.5741
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch94, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 50.06
Std Dev 3.088
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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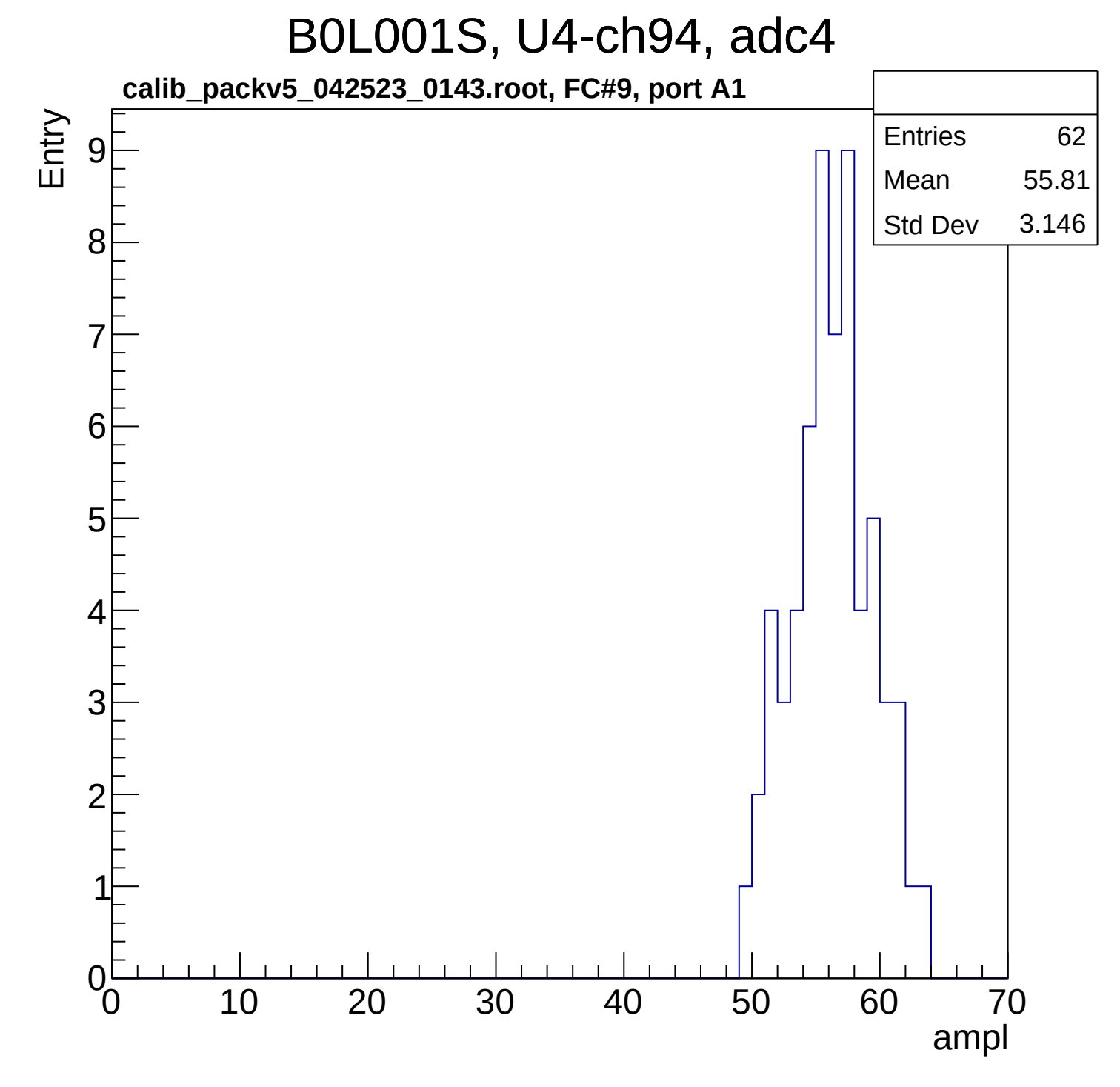
40
50
60
70


B0L001S, U4-ch94, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.81
Std Dev 3.146
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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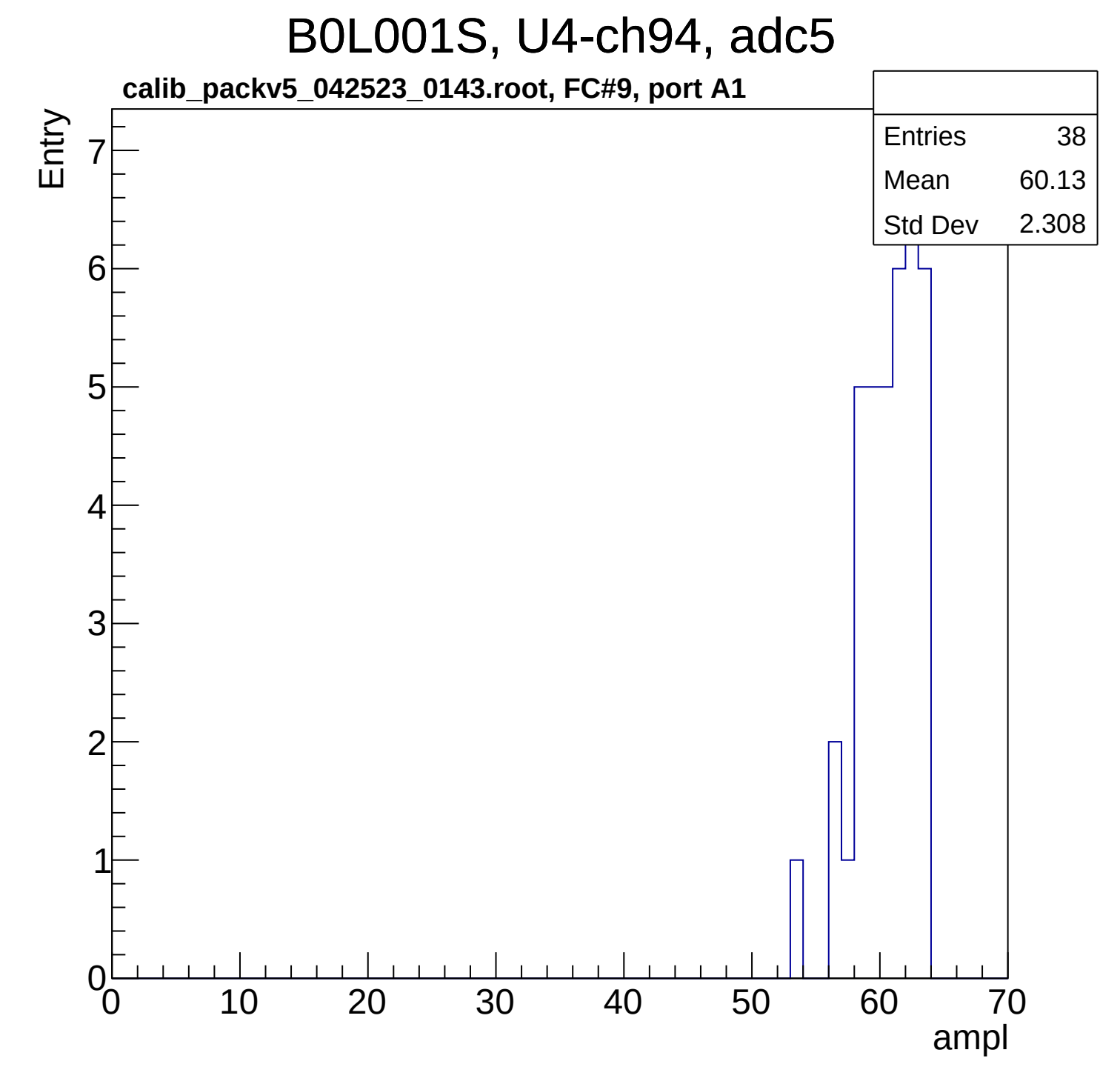
B0L001S, U4-ch94, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.13
Std Dev 2.308
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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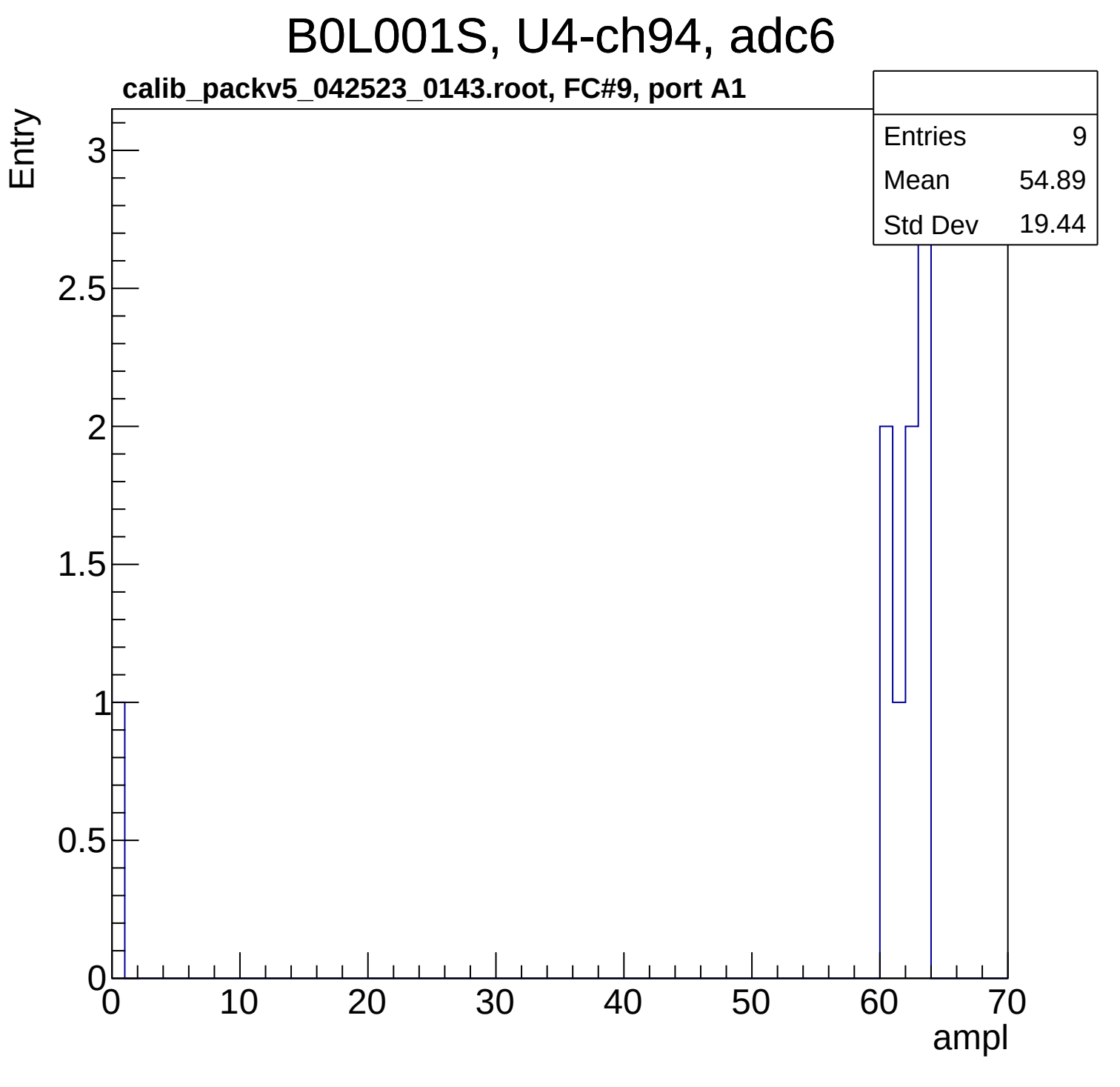
B0L001S, U4-ch94, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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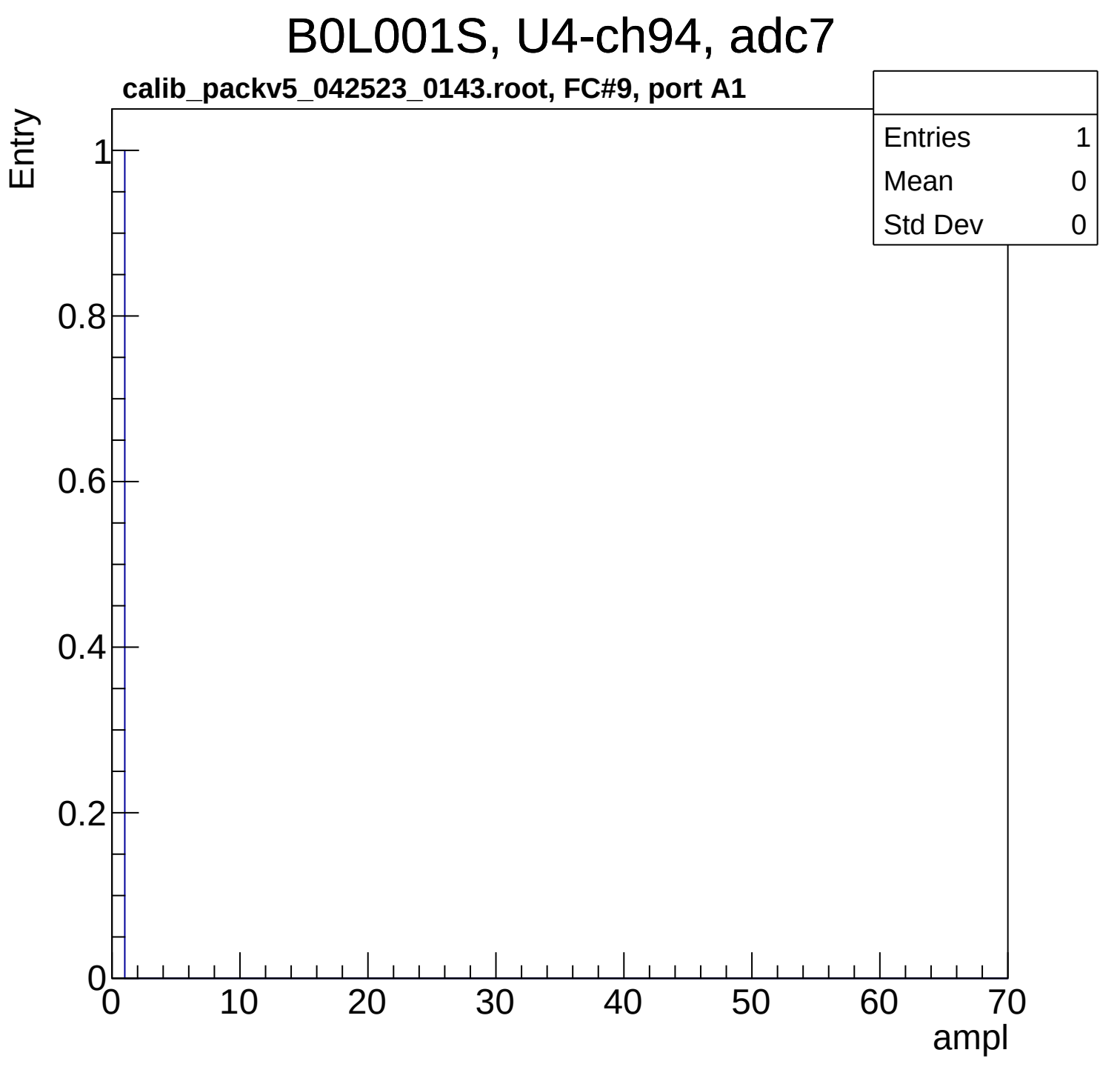
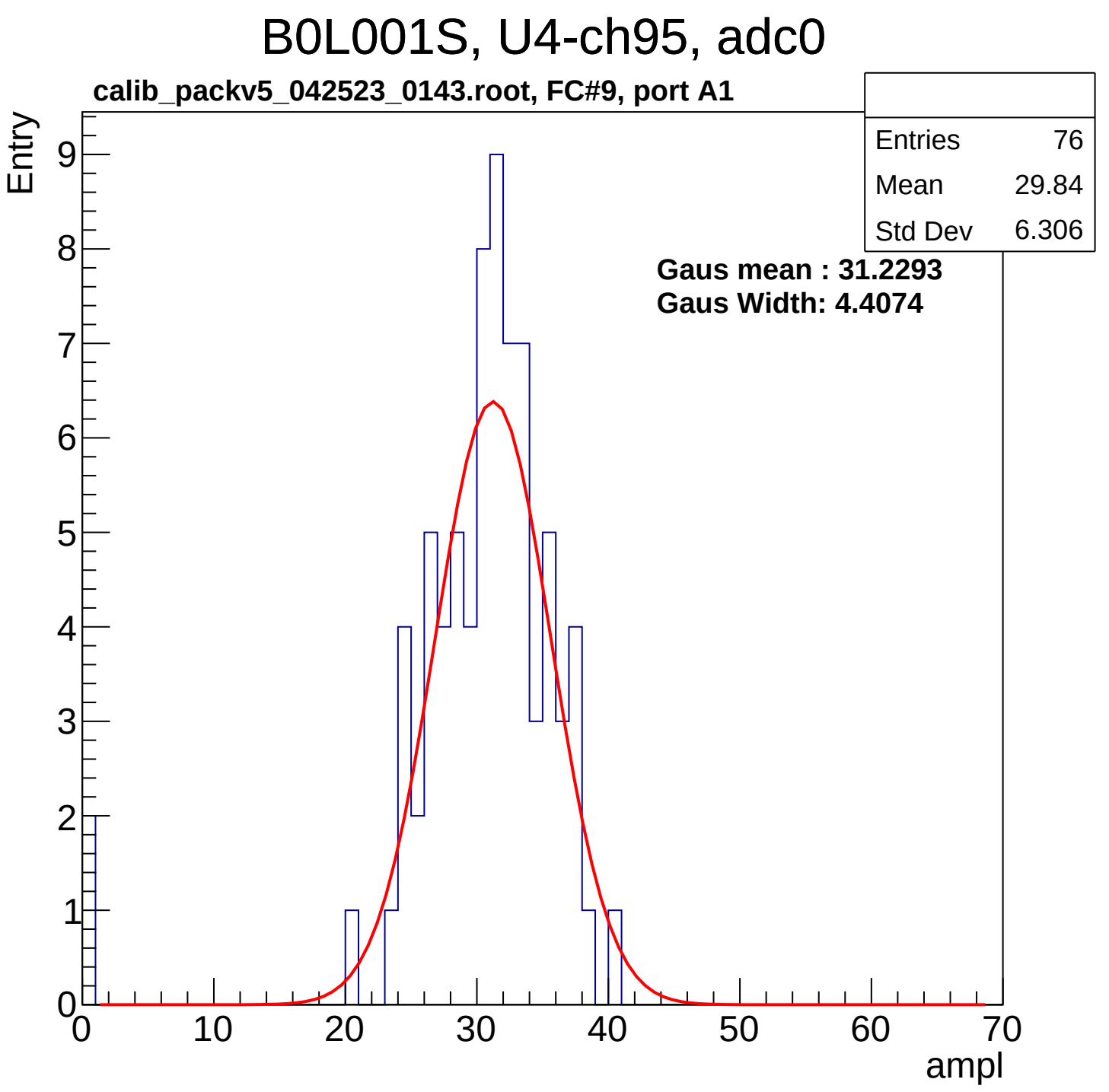
40
50
60
70


B0L001S, U4-ch94, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch95, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 29.84
Std Dev 6.306
Gaus mean : 31.2293
Gaus Width: 4.4074
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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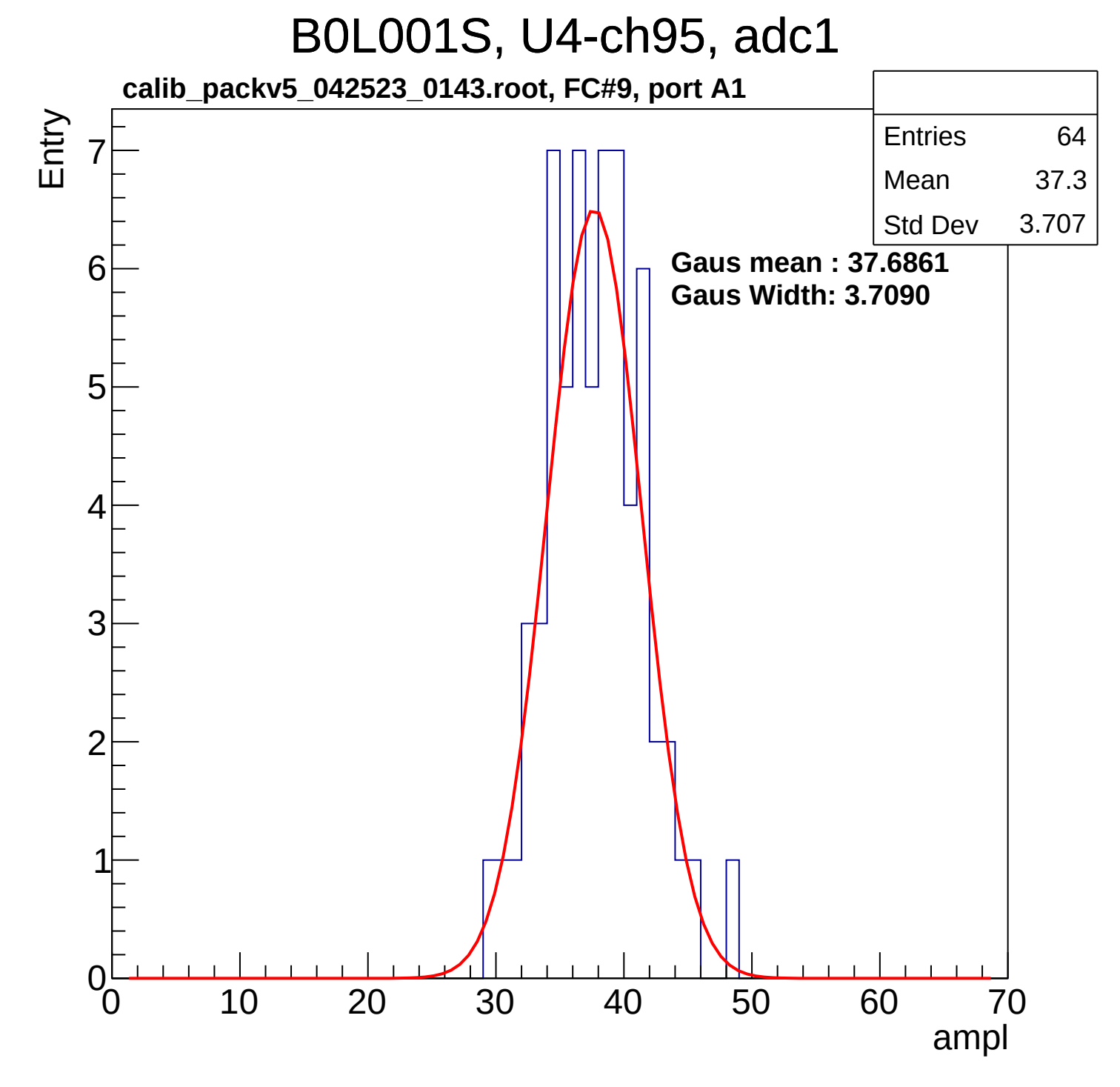
B0L001S, U4-ch95, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.3
Std Dev 3.707
Gaus mean : 37.6861
Gaus Width: 3.7090
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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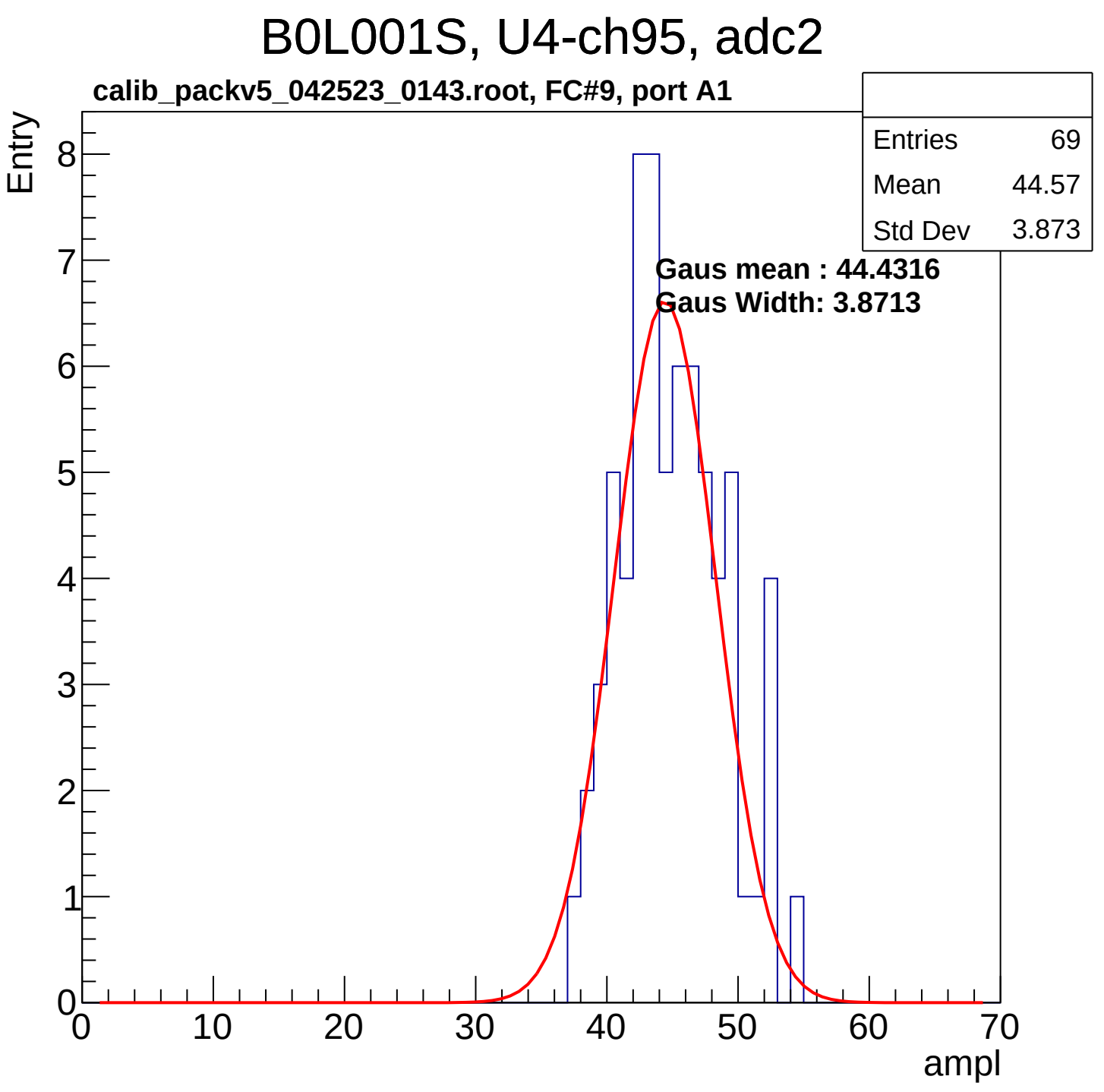
40
50
60
70


B0L001S, U4-ch95, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.57
Std Dev 3.873
Gaus mean : 44.4316
Gaus Width: 3.8713
Entry
ampl
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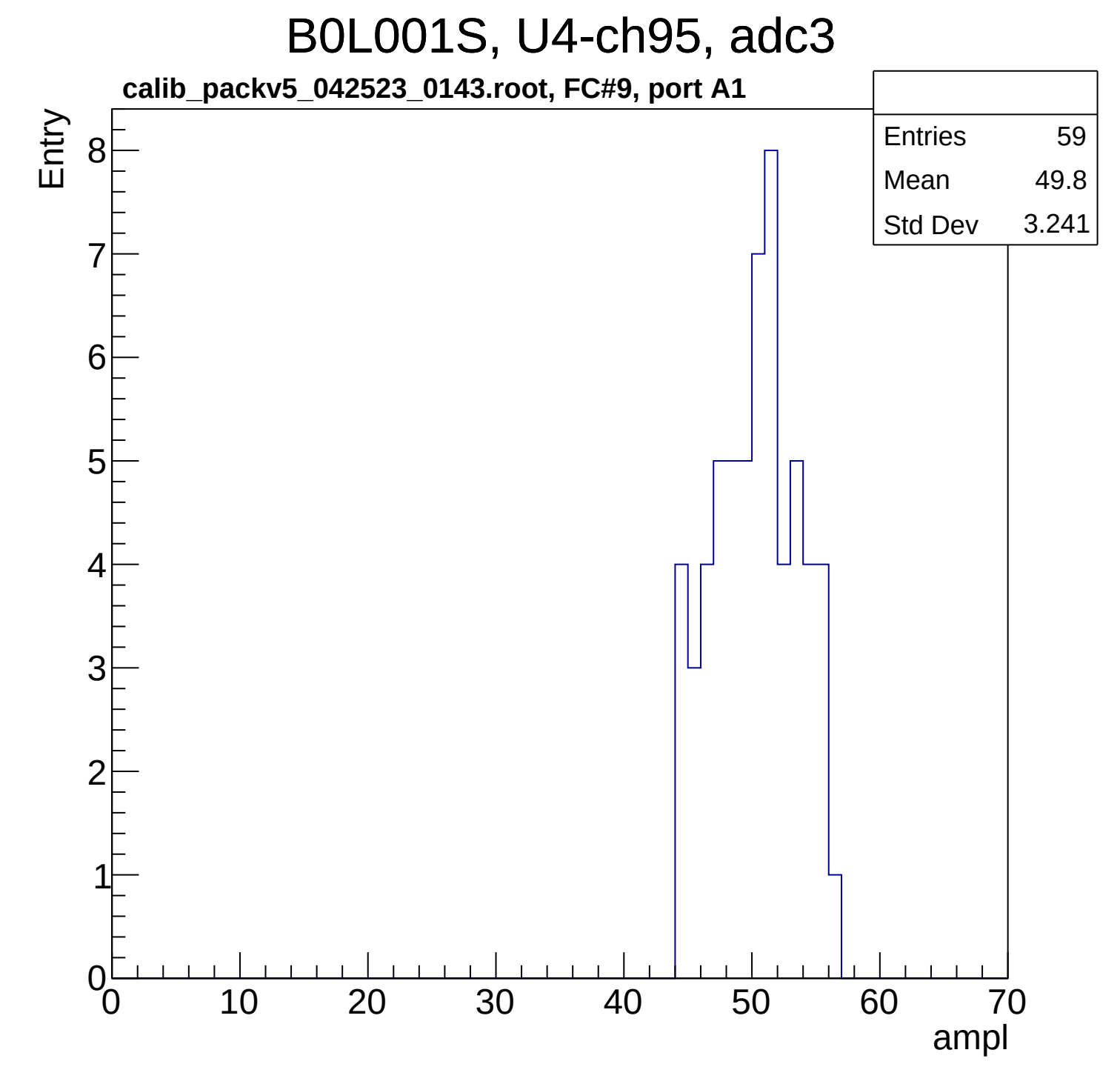
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch95, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 49.8
Std Dev 3.241
Entry
ampl
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3
4
5
6
7
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0
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60
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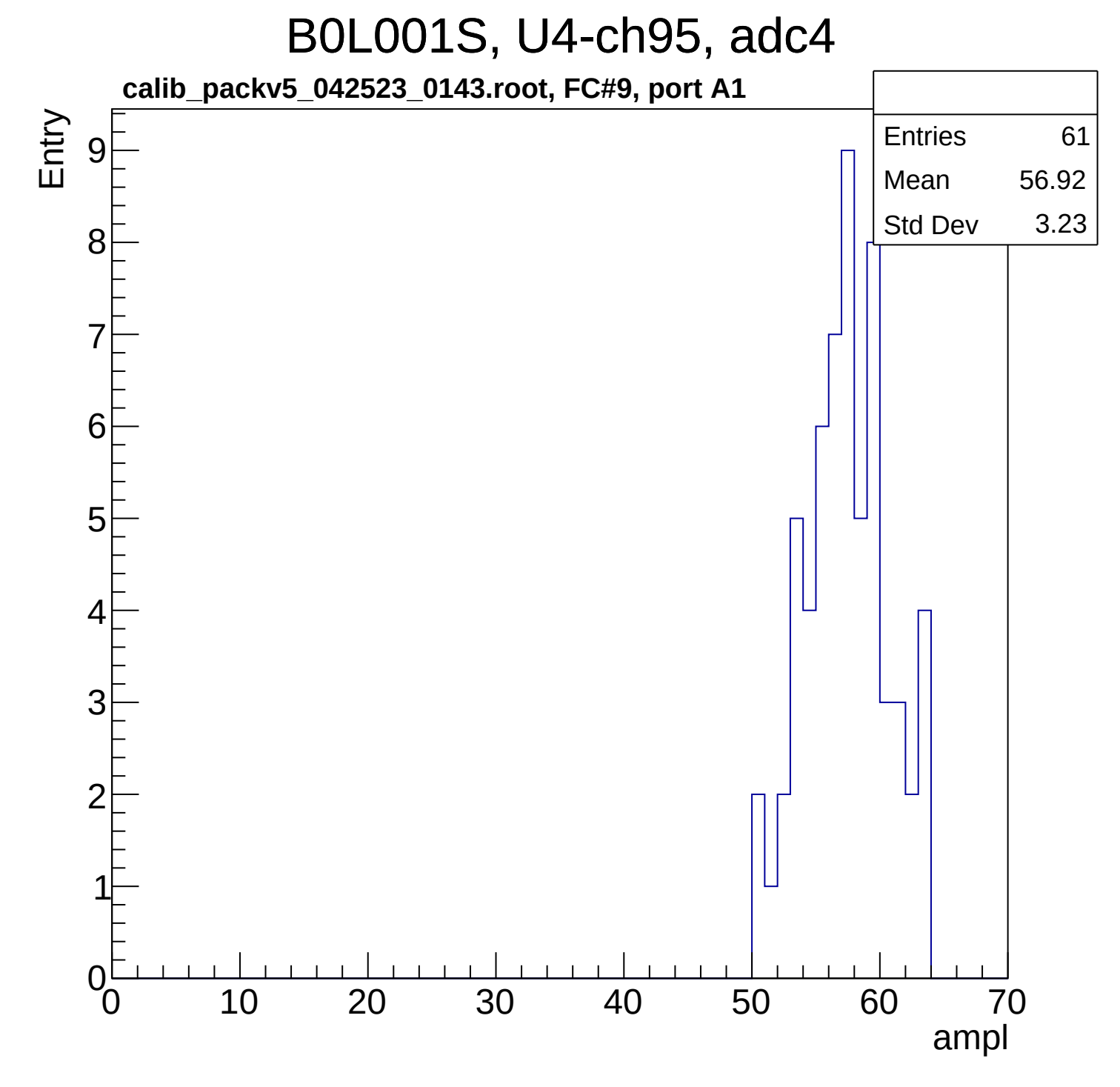
B0L001S, U4-ch95, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.92
Std Dev 3.23
Entry
ampl
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3
4
5
6
7
8
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0
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60
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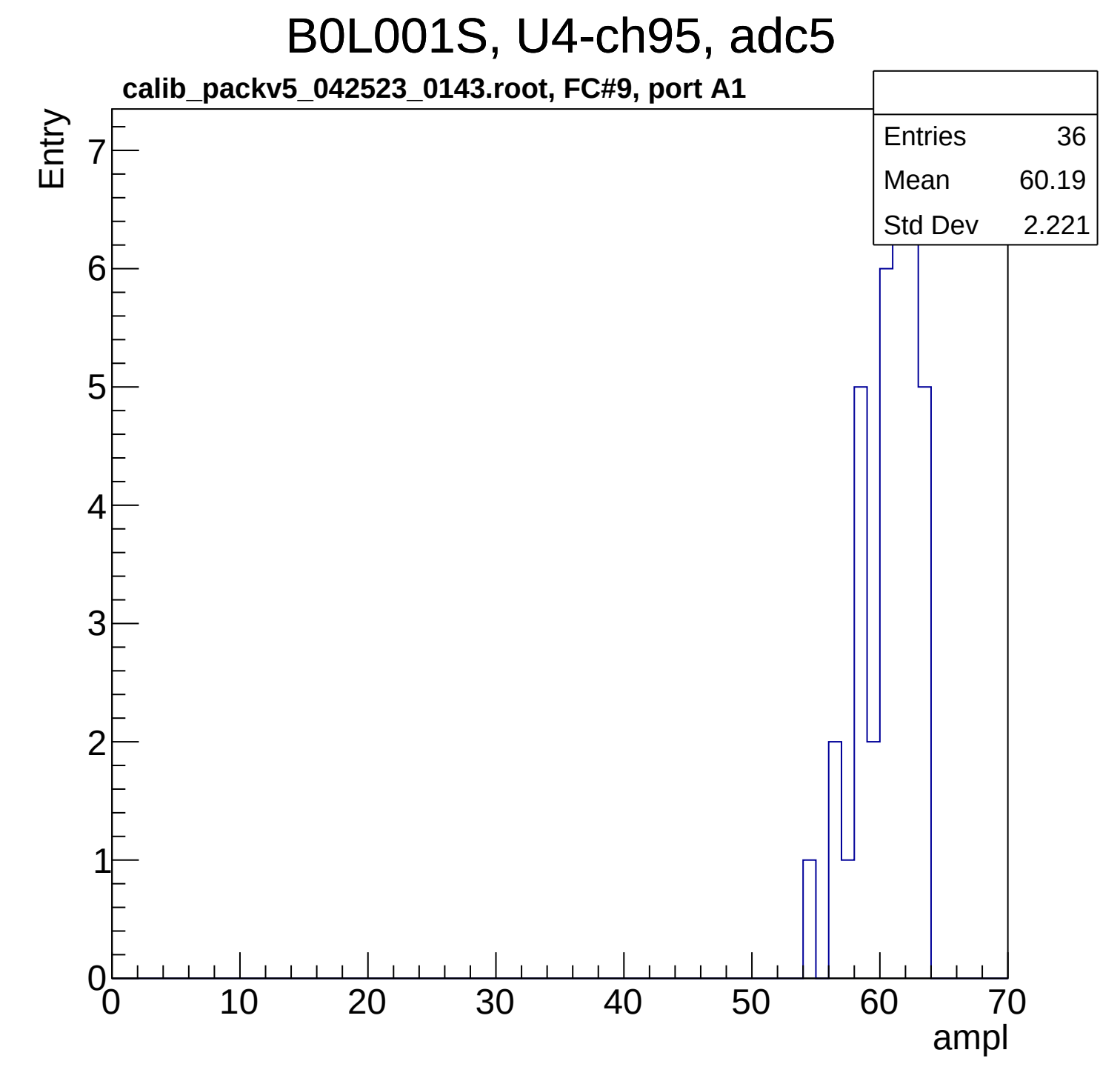
B0L001S, U4-ch95, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 60.19
Std Dev 2.221
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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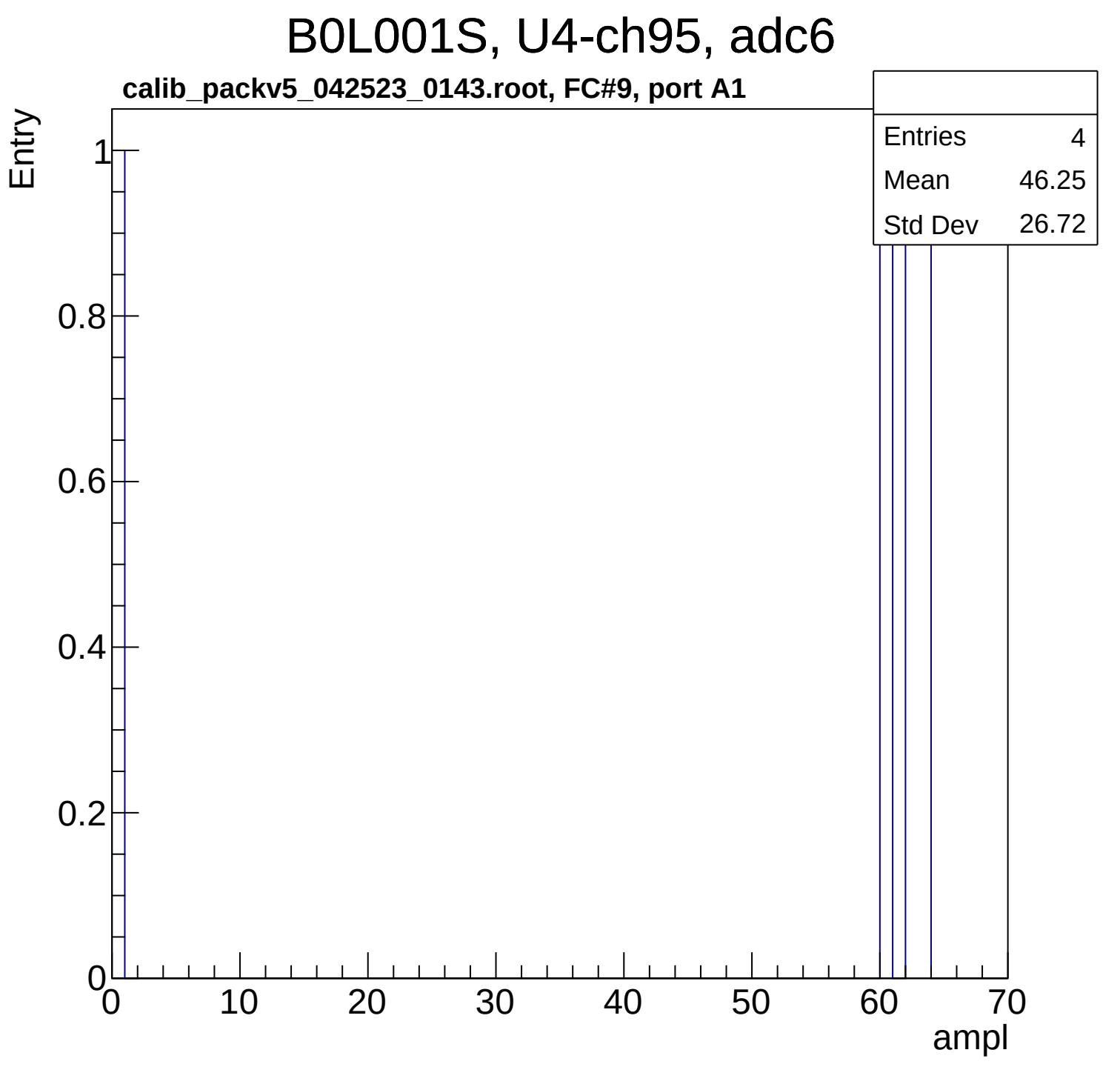
B0L001S, U4-ch95, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.25
Std Dev 26.72
Entry
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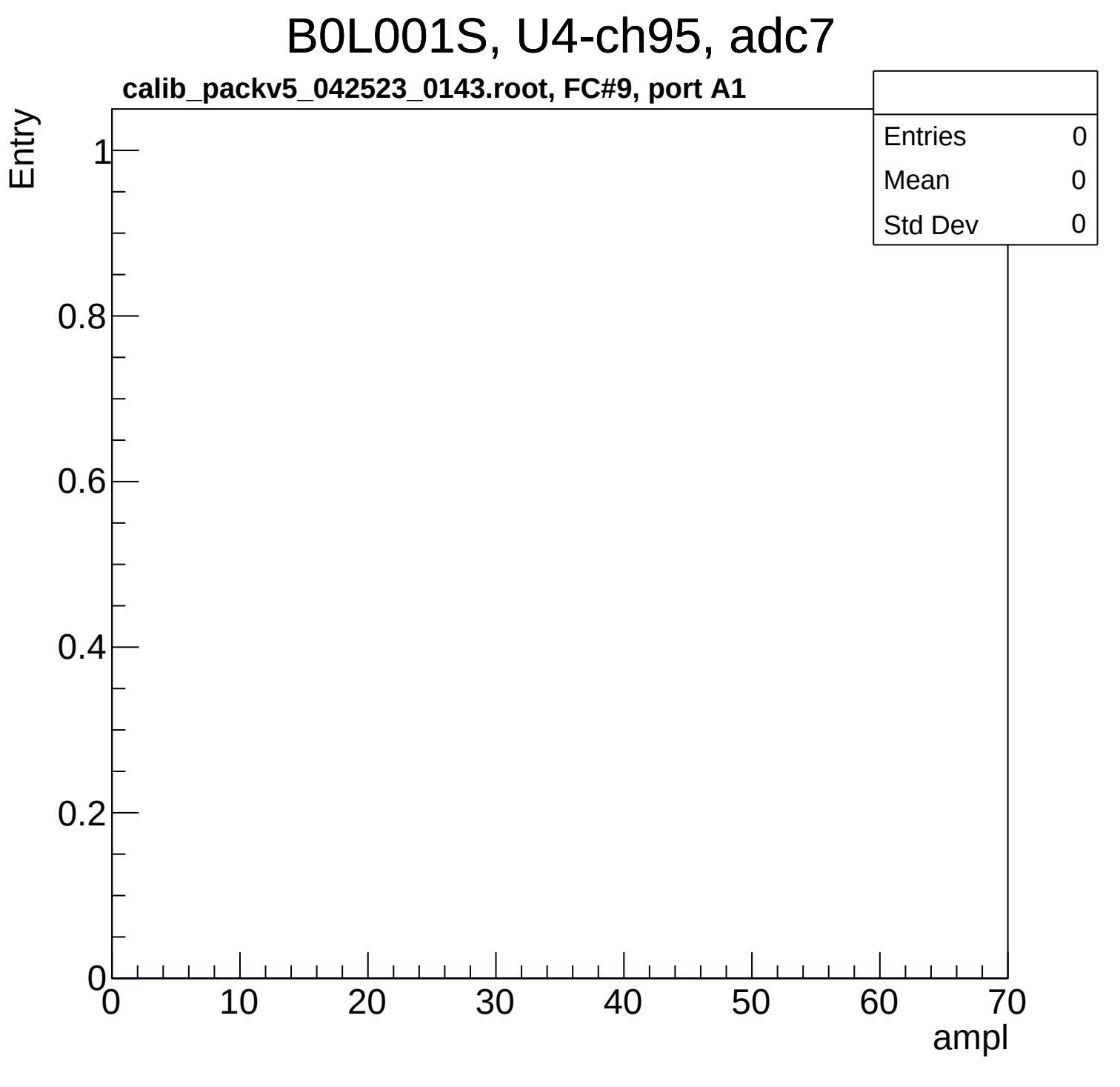
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch95, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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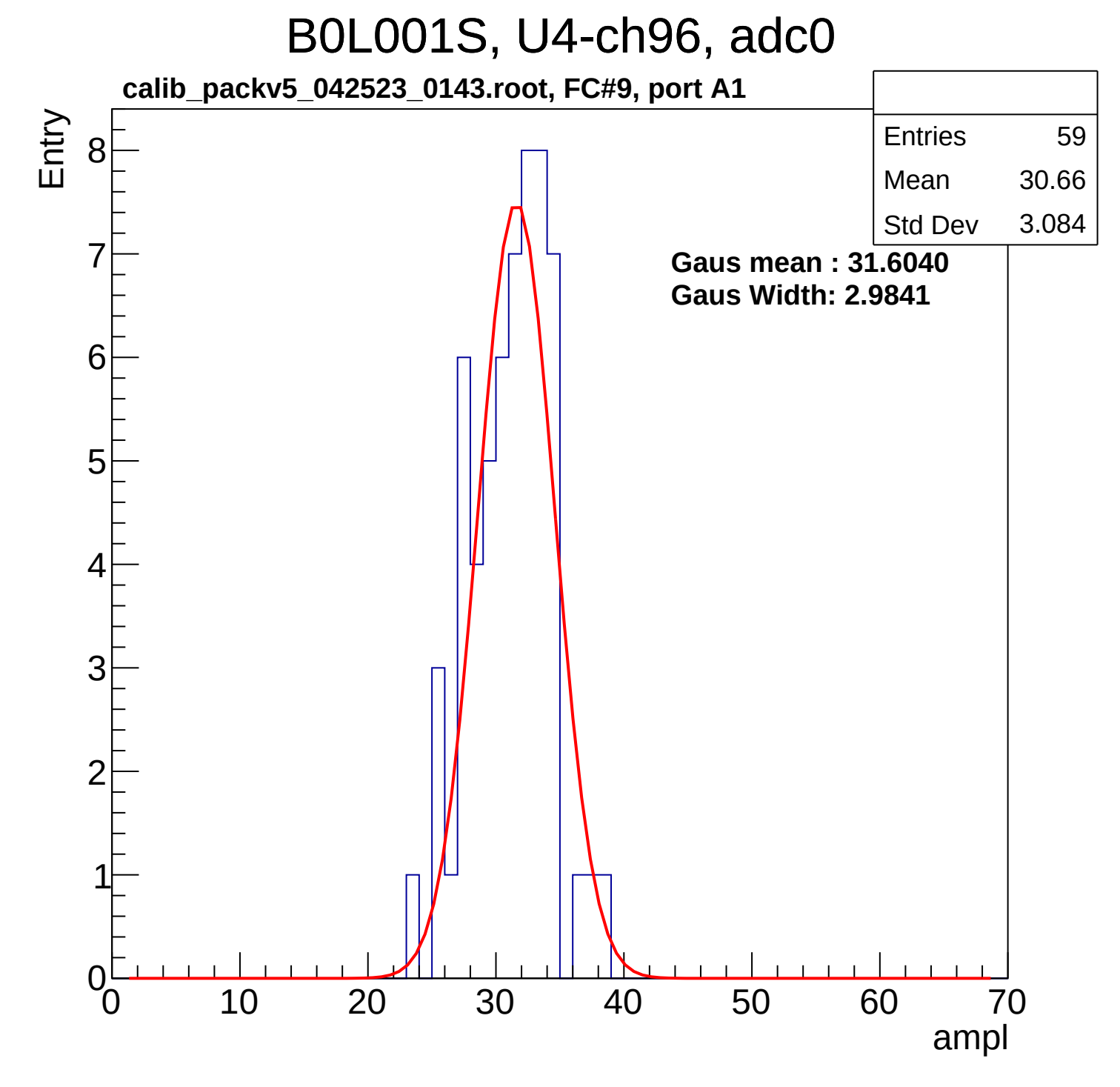
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch96, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.66
Std Dev 3.084
Gaus mean : 31.6040
Gaus Width: 2.9841
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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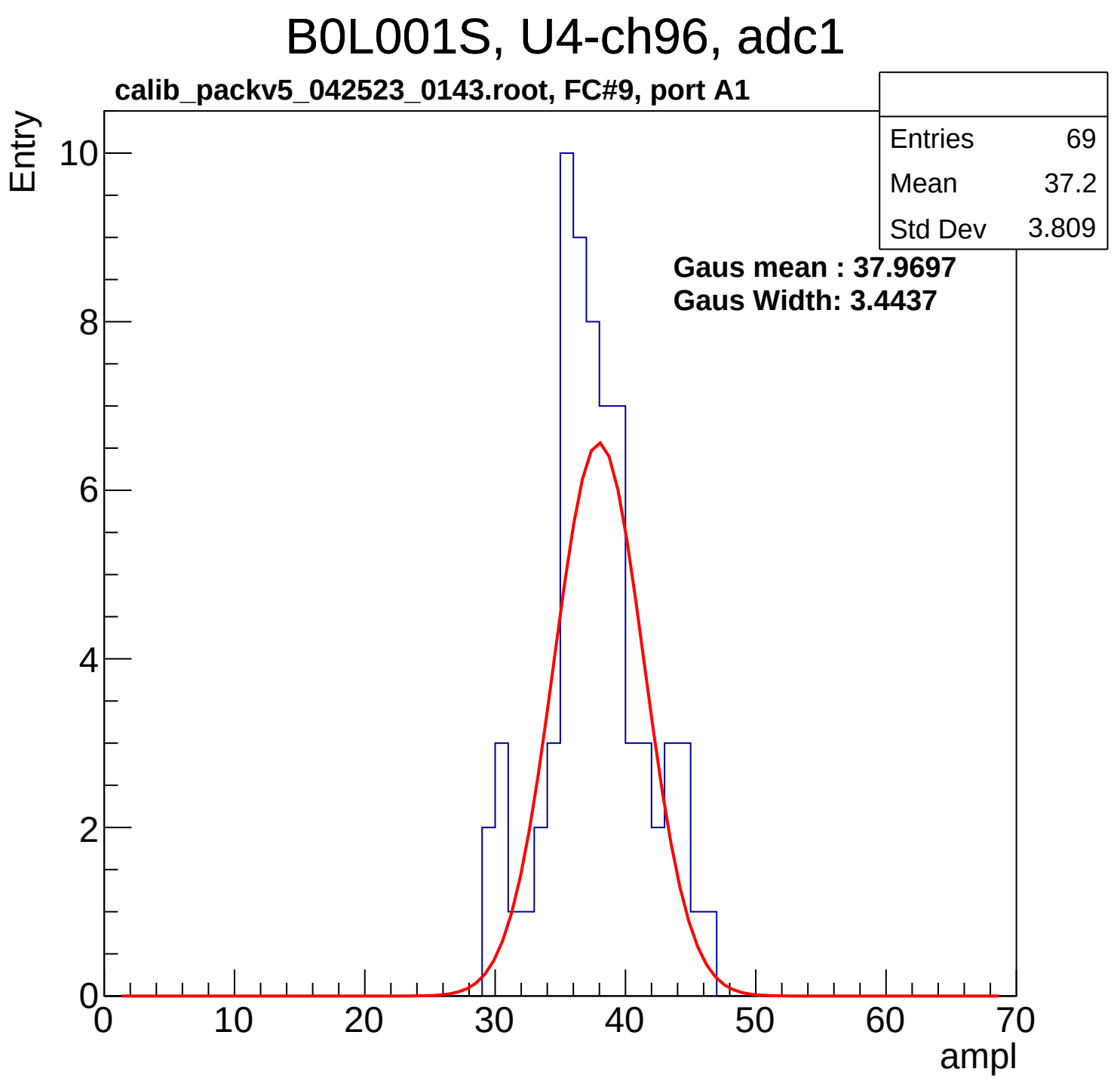
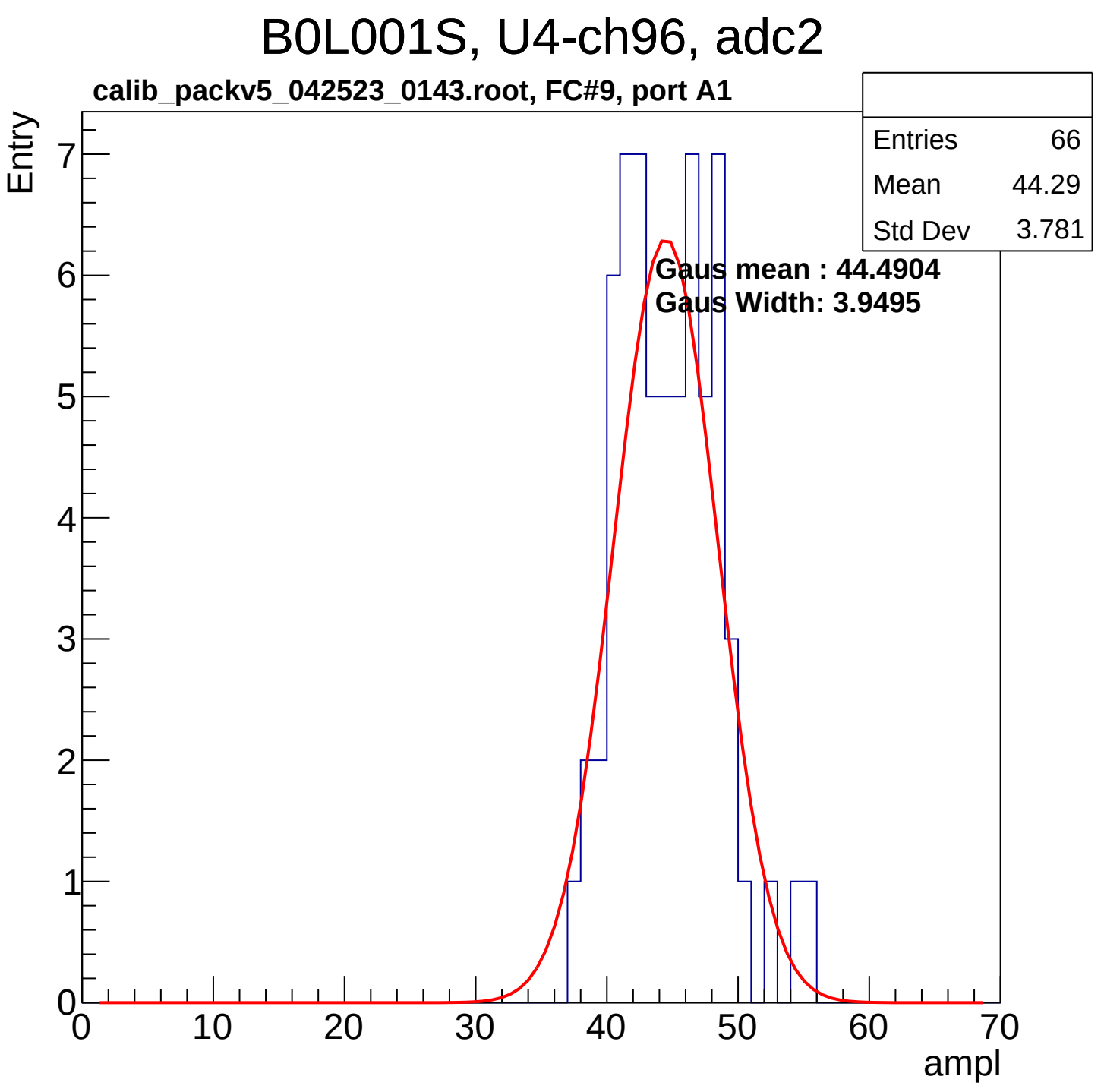
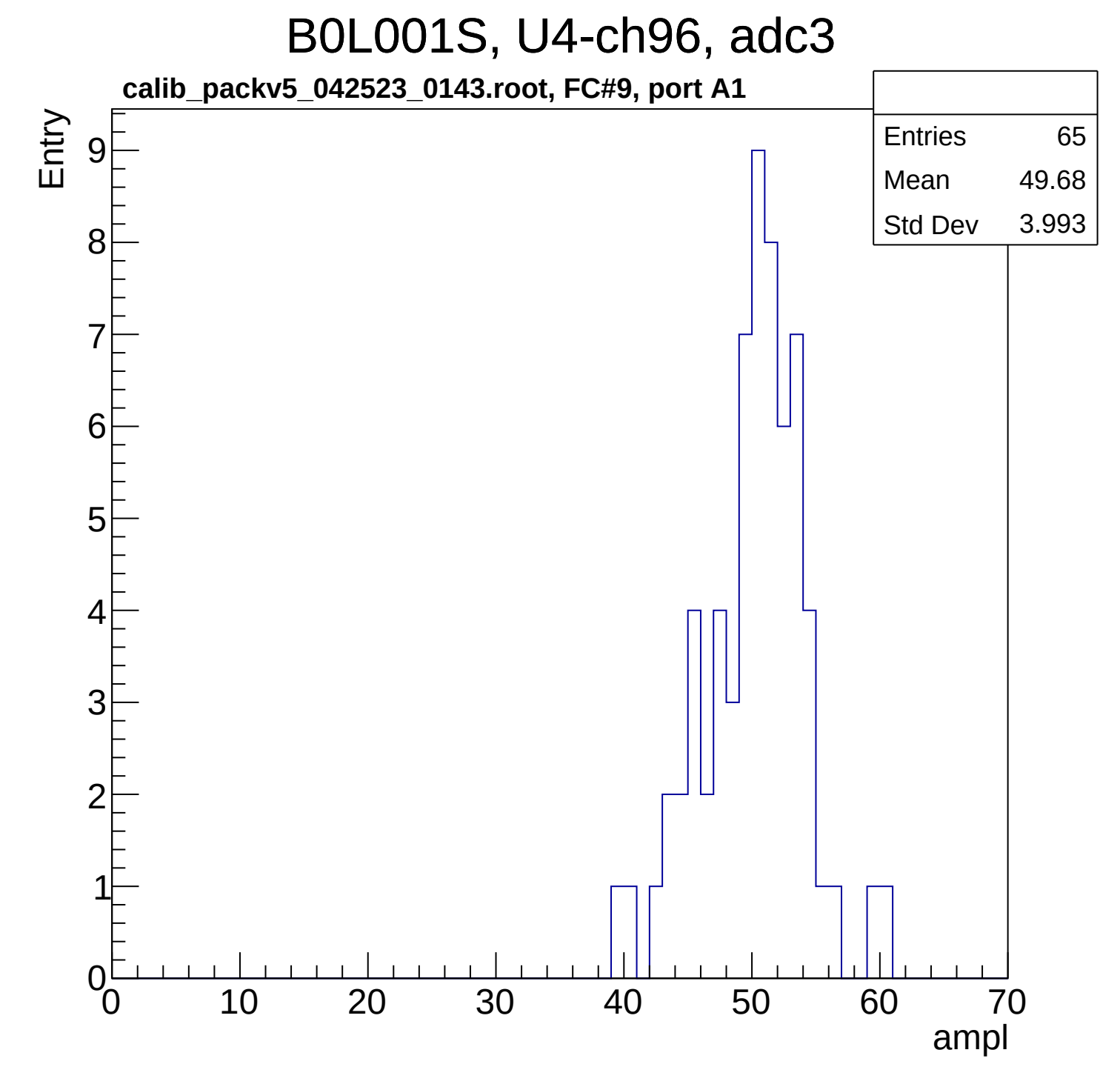

B0L001S, U4-ch96, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.2
Std Dev 3.809
Gaus mean : 37.9697
Gaus Width: 3.4437
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch96, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.29
Std Dev 3.781
Gaus mean : 44.4904
Gaus Width: 3.9495
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch96, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.68
Std Dev 3.993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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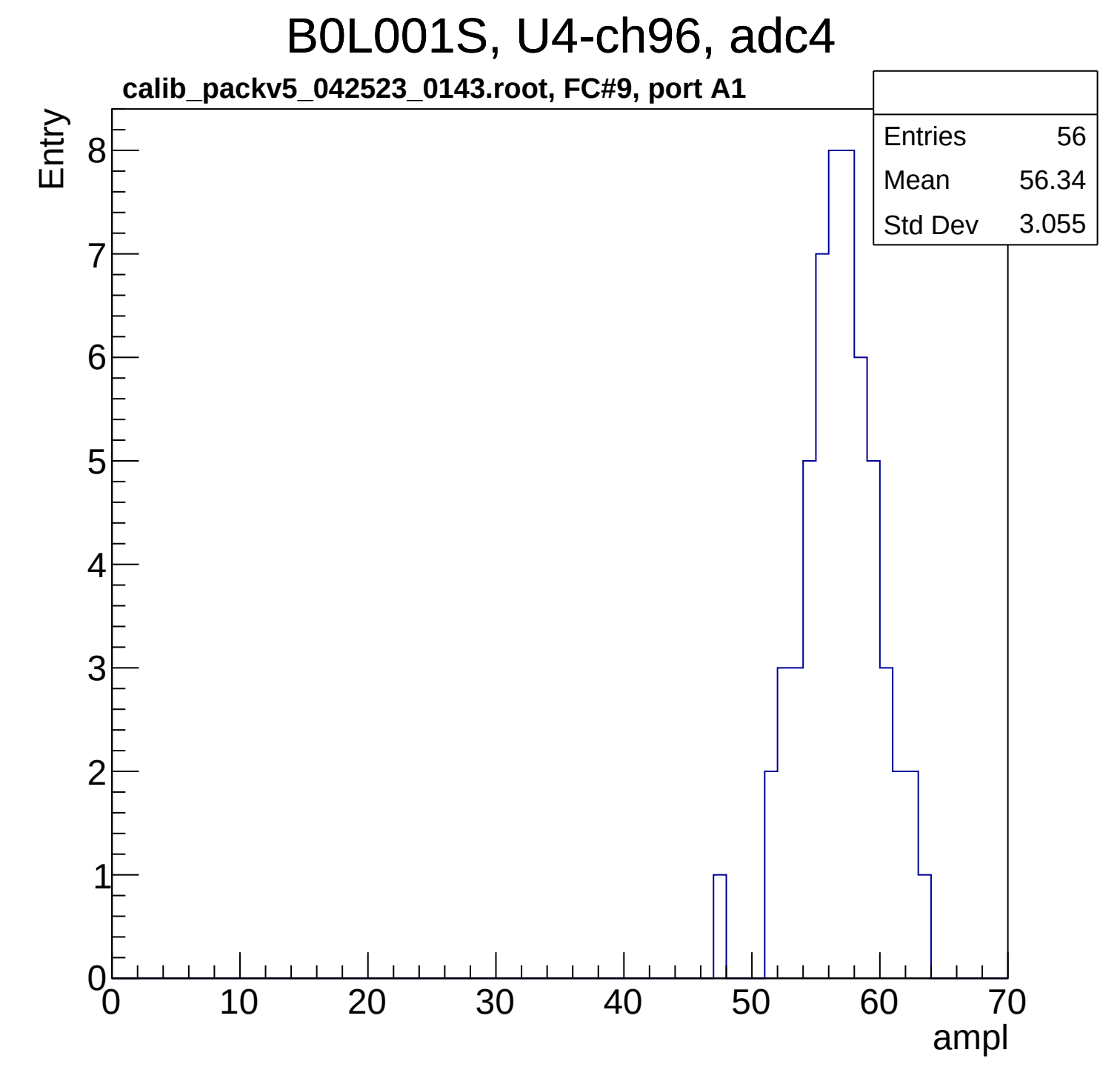
B0L001S, U4-ch96, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.34
Std Dev 3.055
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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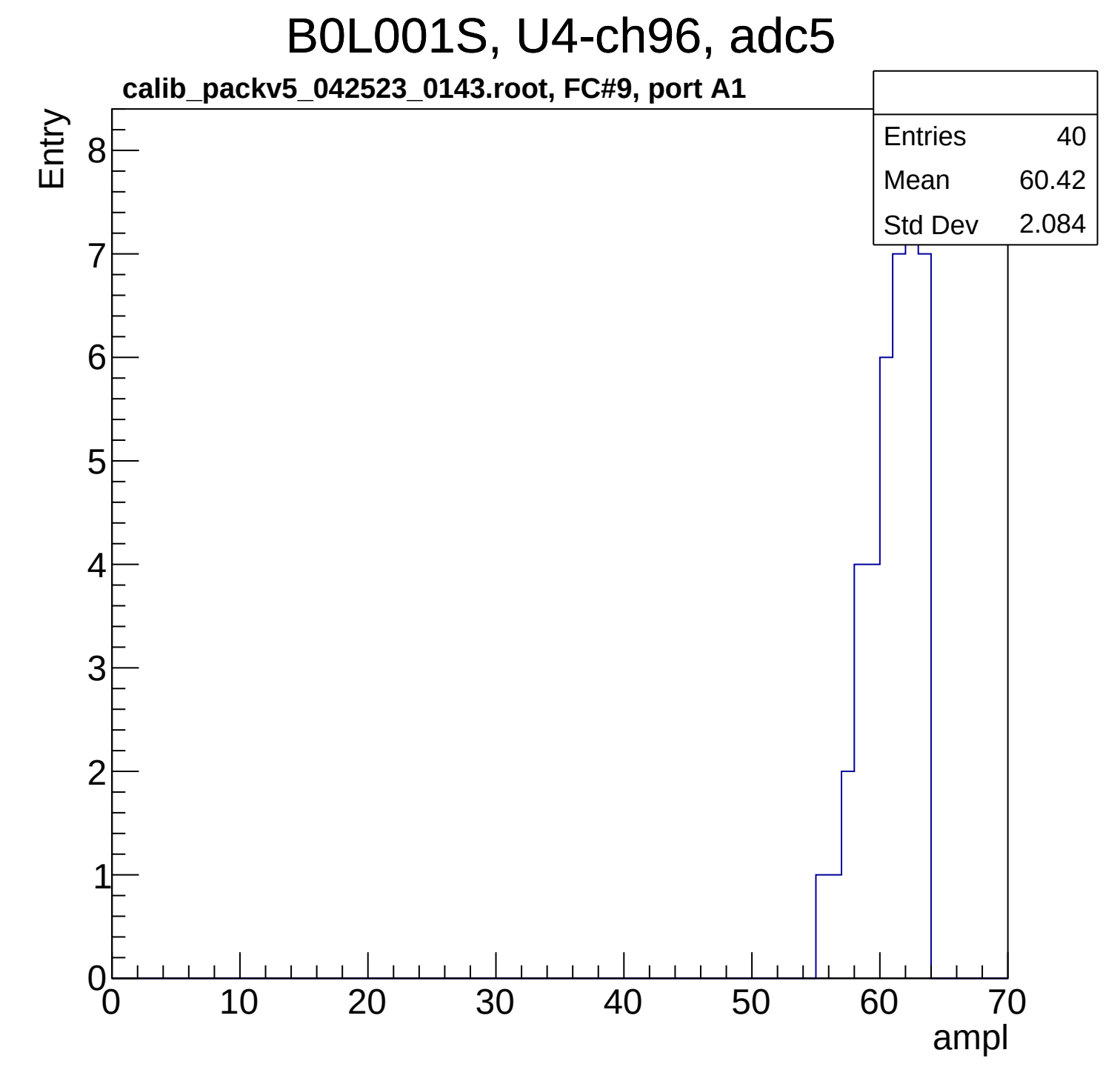
B0L001S, U4-ch96, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 60.42
Std Dev 2.084
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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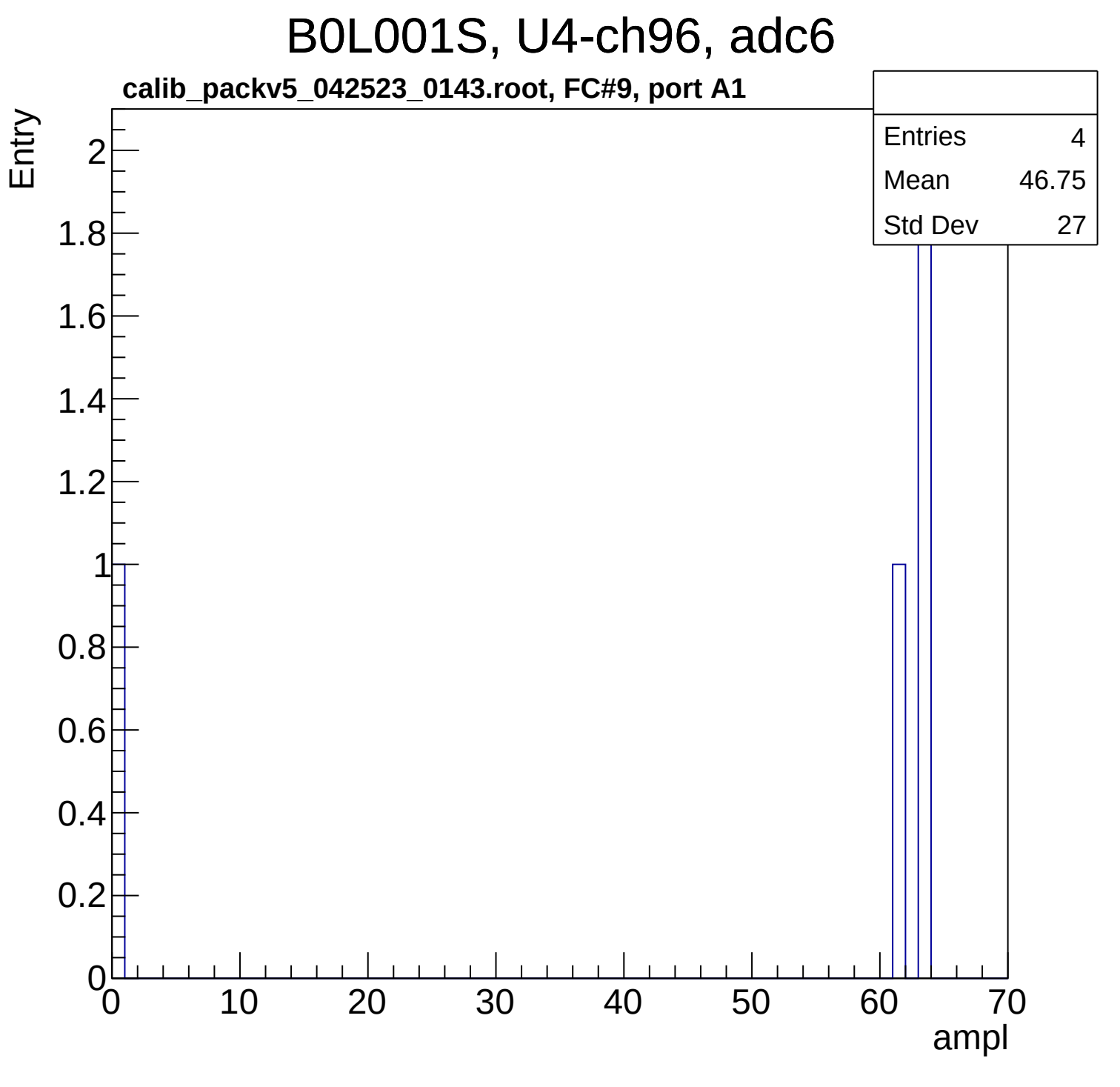
B0L001S, U4-ch96, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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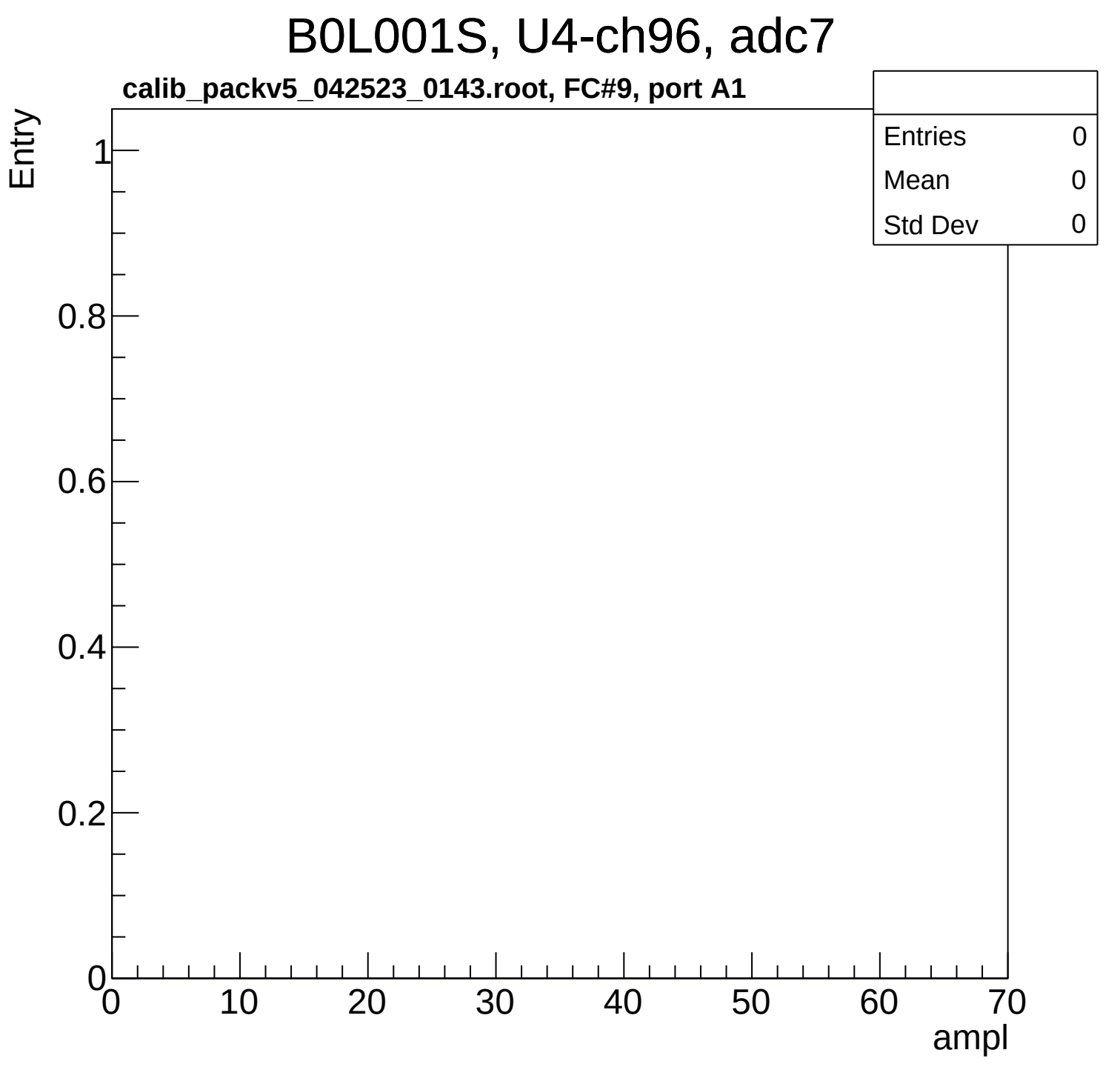
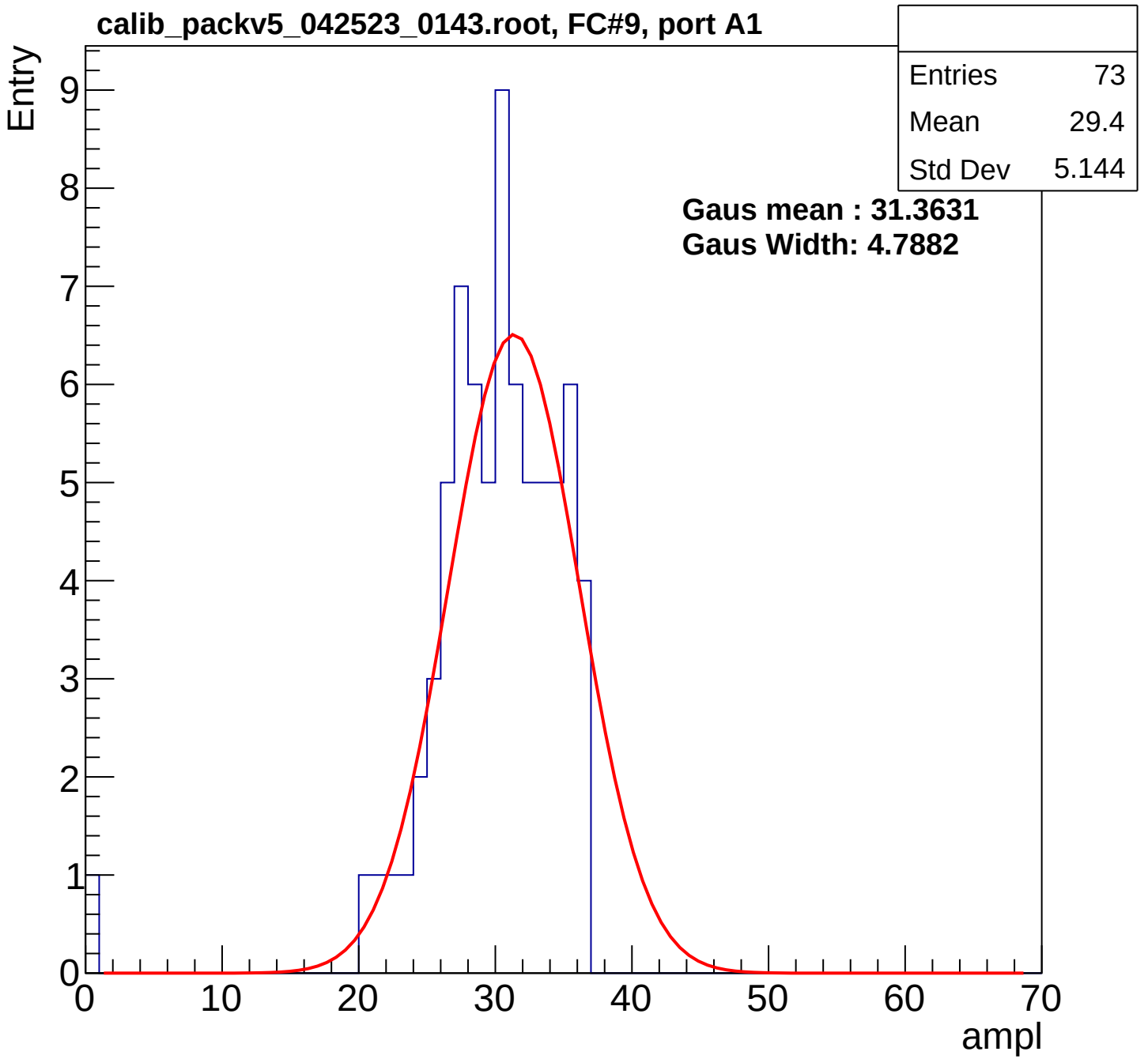

B0L001S, U4-ch96, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch97, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.4
Std Dev 5.144
Gaus mean : 31.3631
Gaus Width: 4.7882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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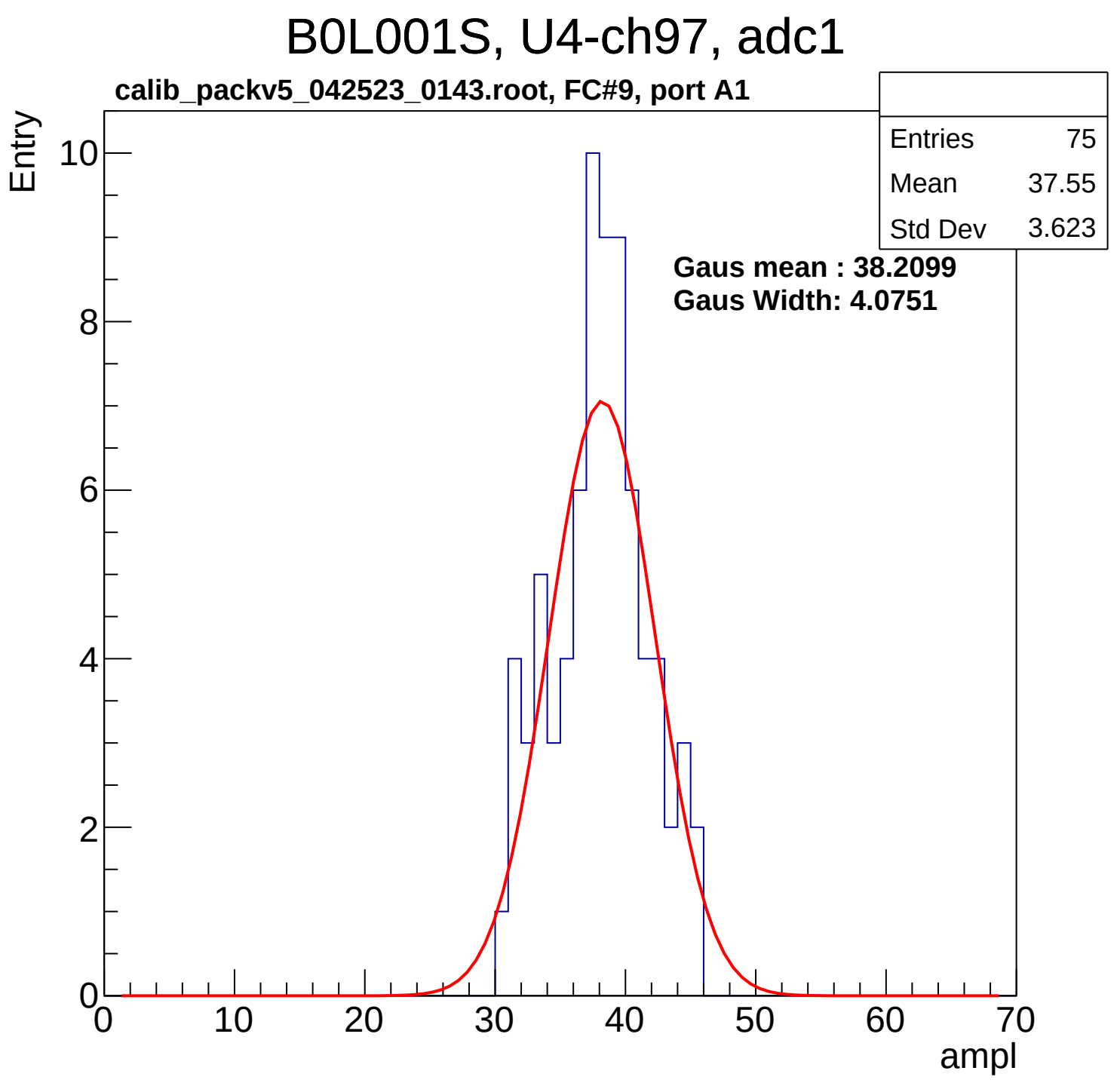
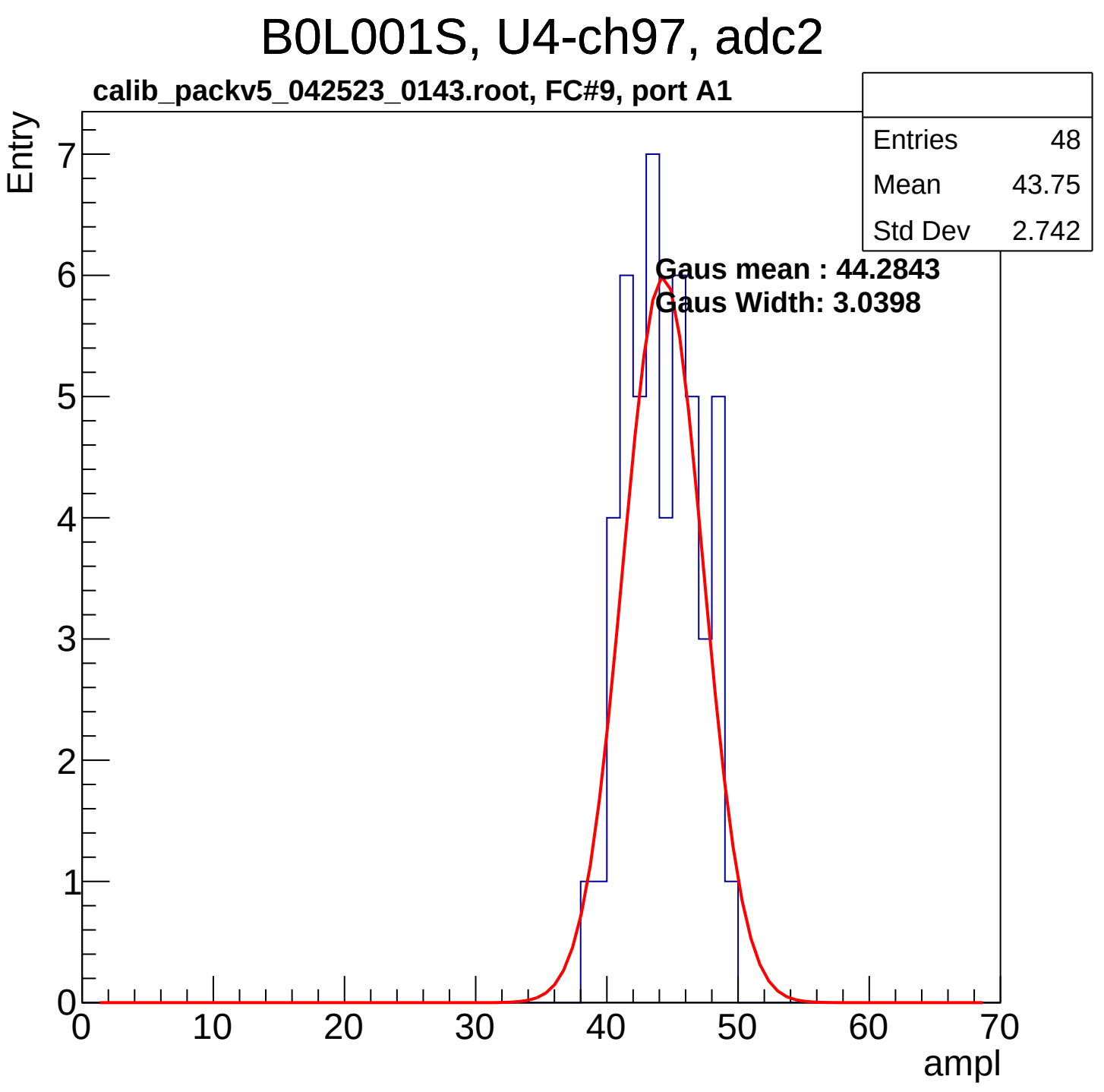
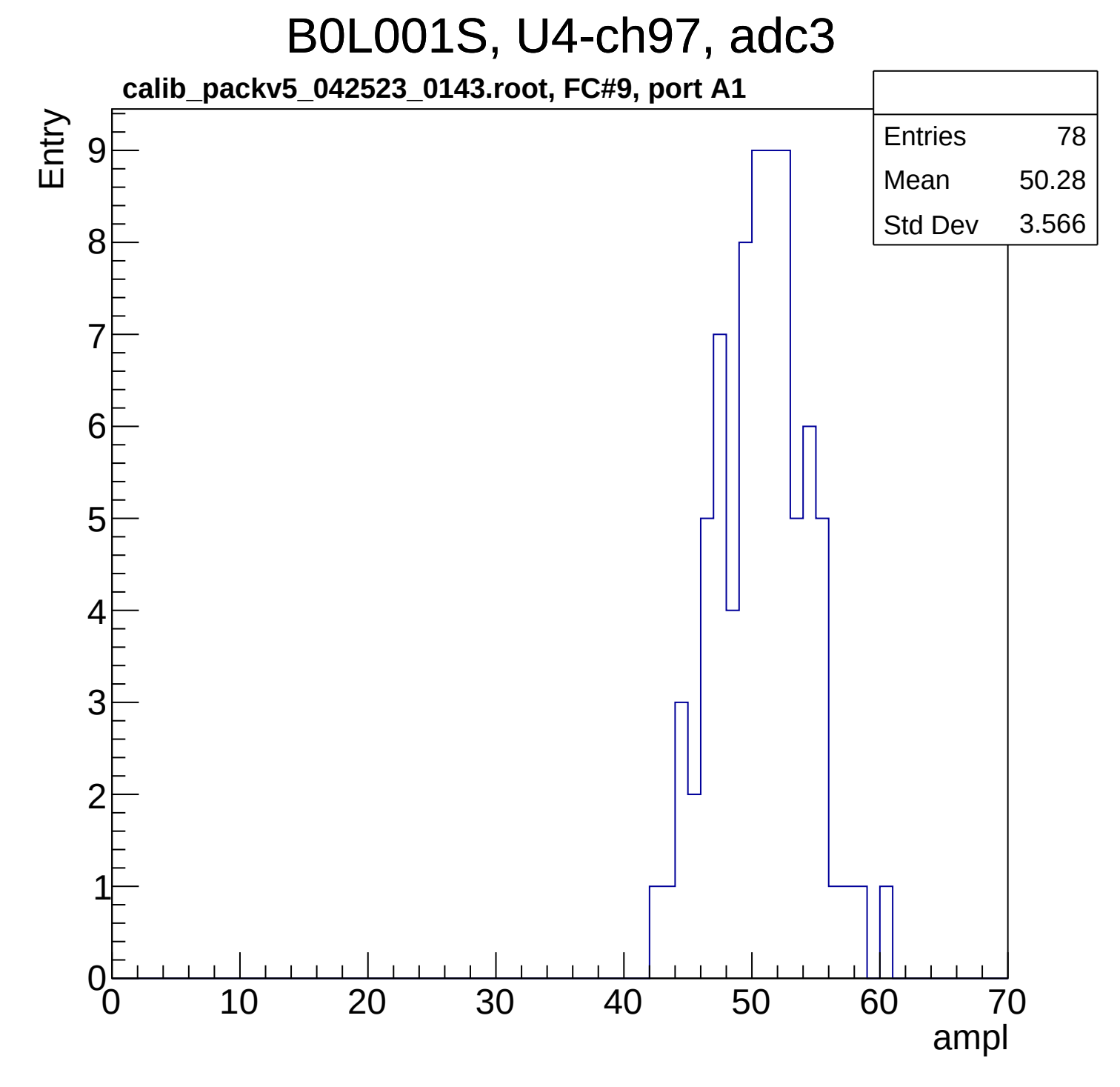
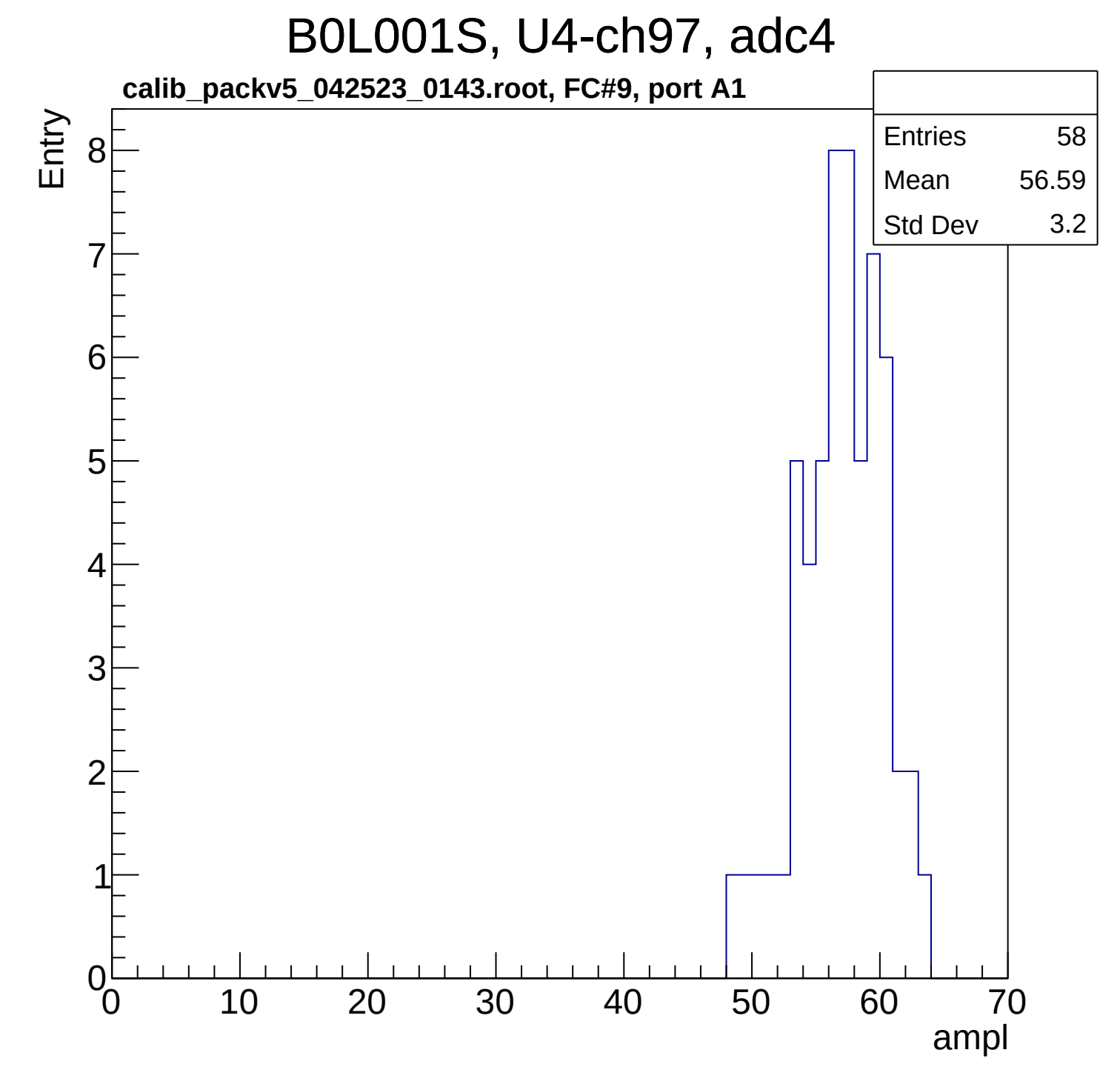
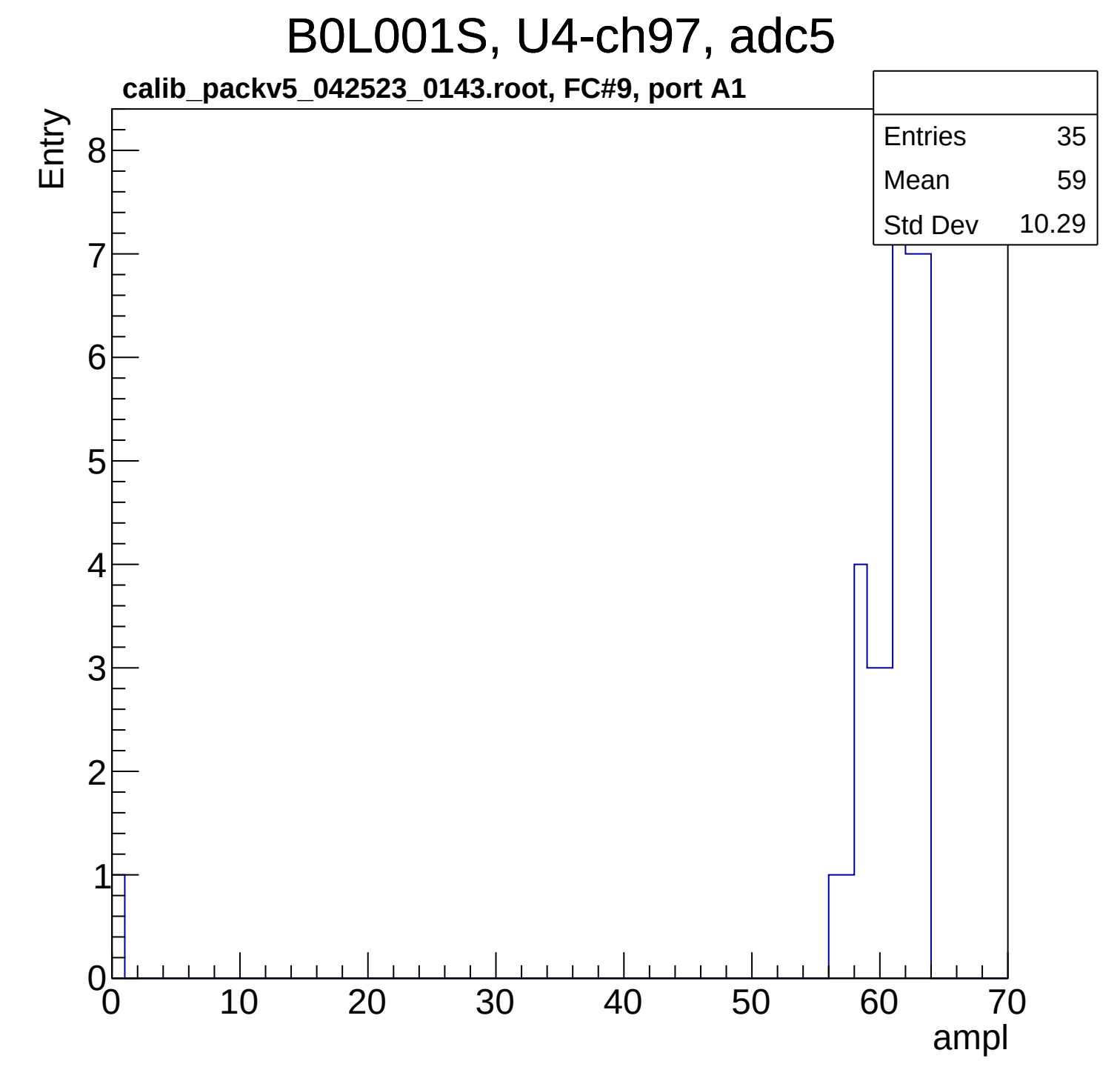
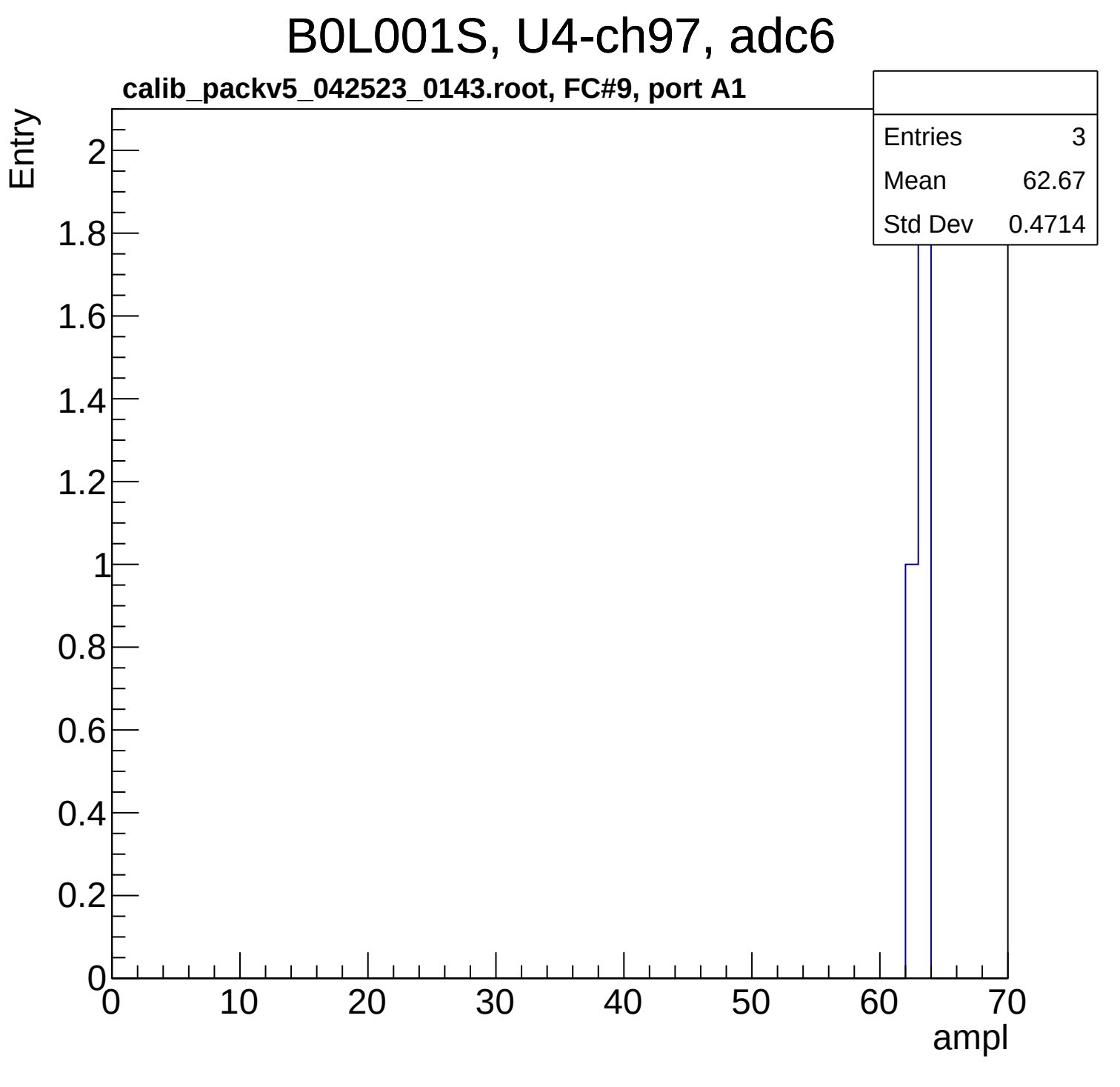

B0L001S, U4-ch97, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.55
Std Dev 3.623
Gaus mean : 38.2099
Gaus Width: 4.0751
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch97, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 43.75
Std Dev 2.742
Gaus mean : 44.2843
Gaus Width: 3.0398
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch97, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 50.28
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch97, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.59
Std Dev 3.2
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch97, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59
Std Dev 10.29
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch97, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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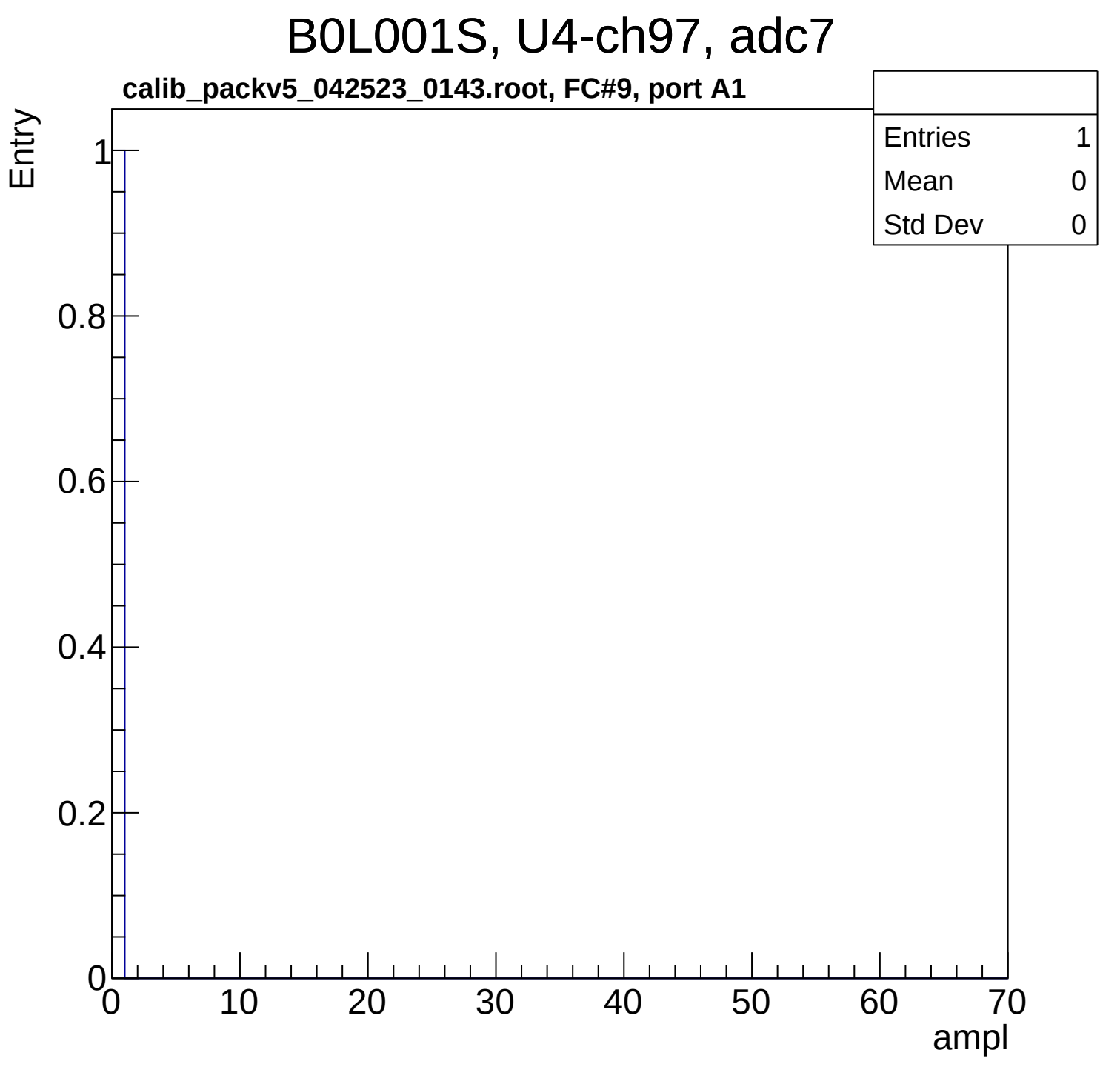
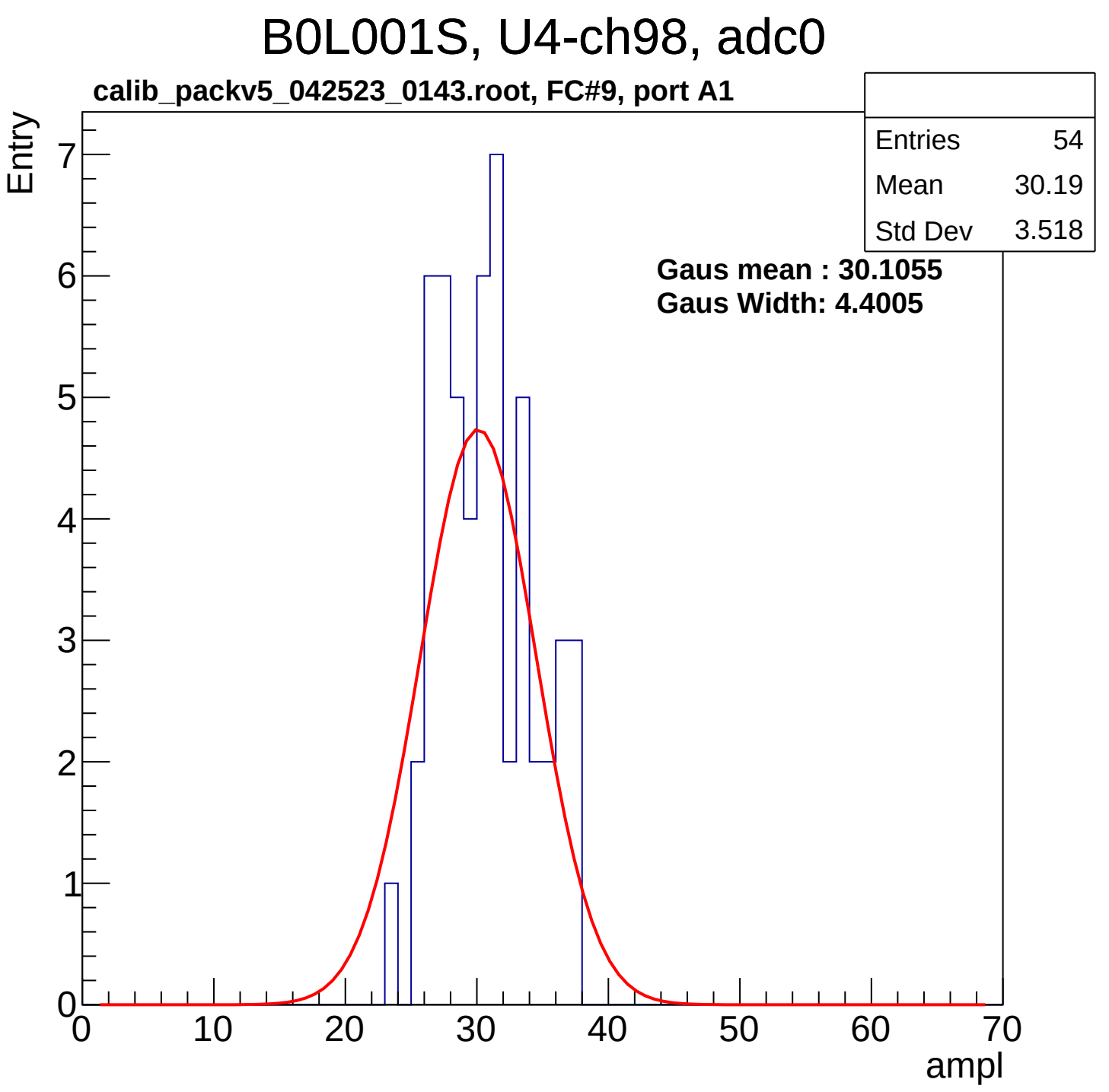
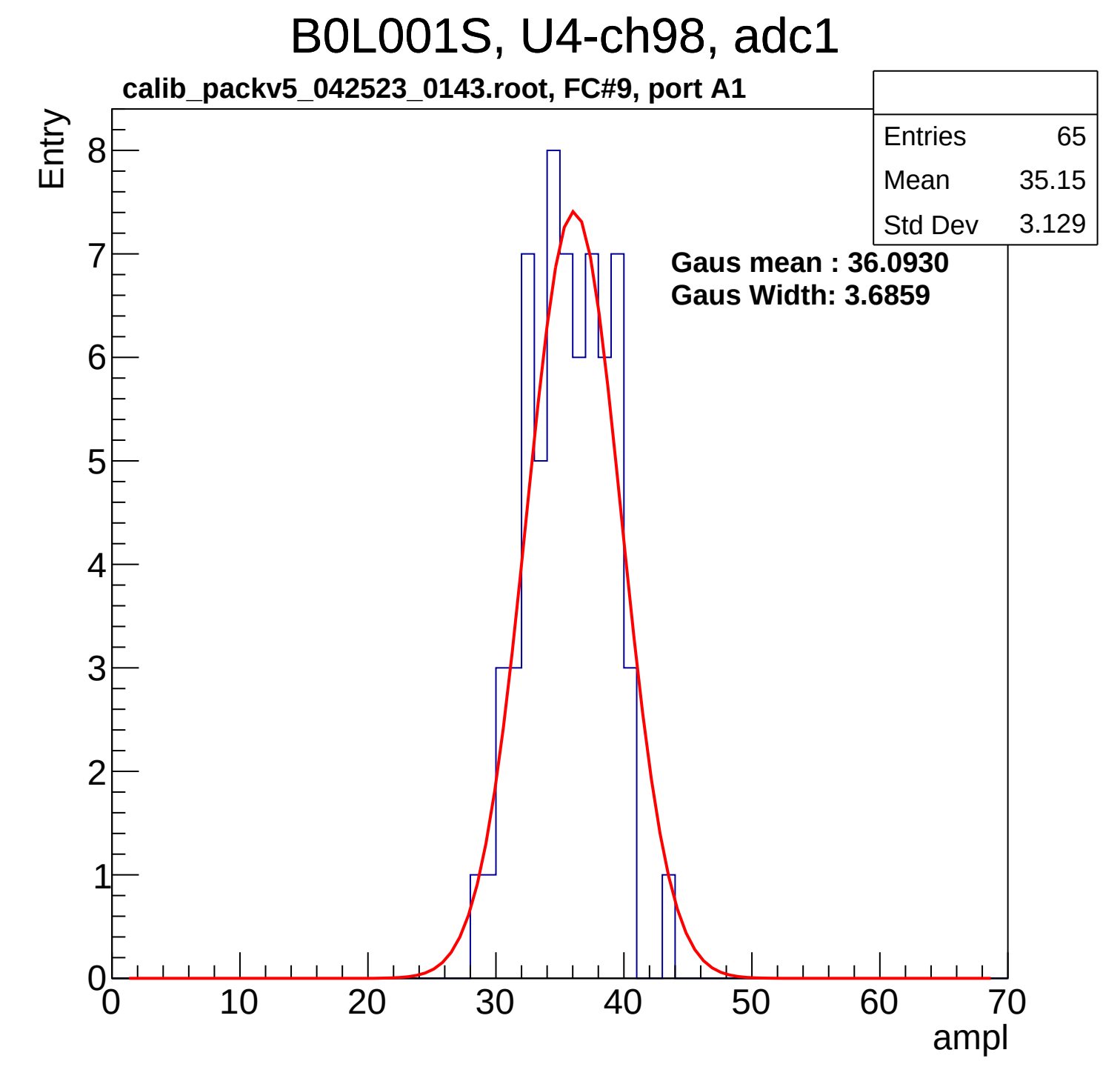
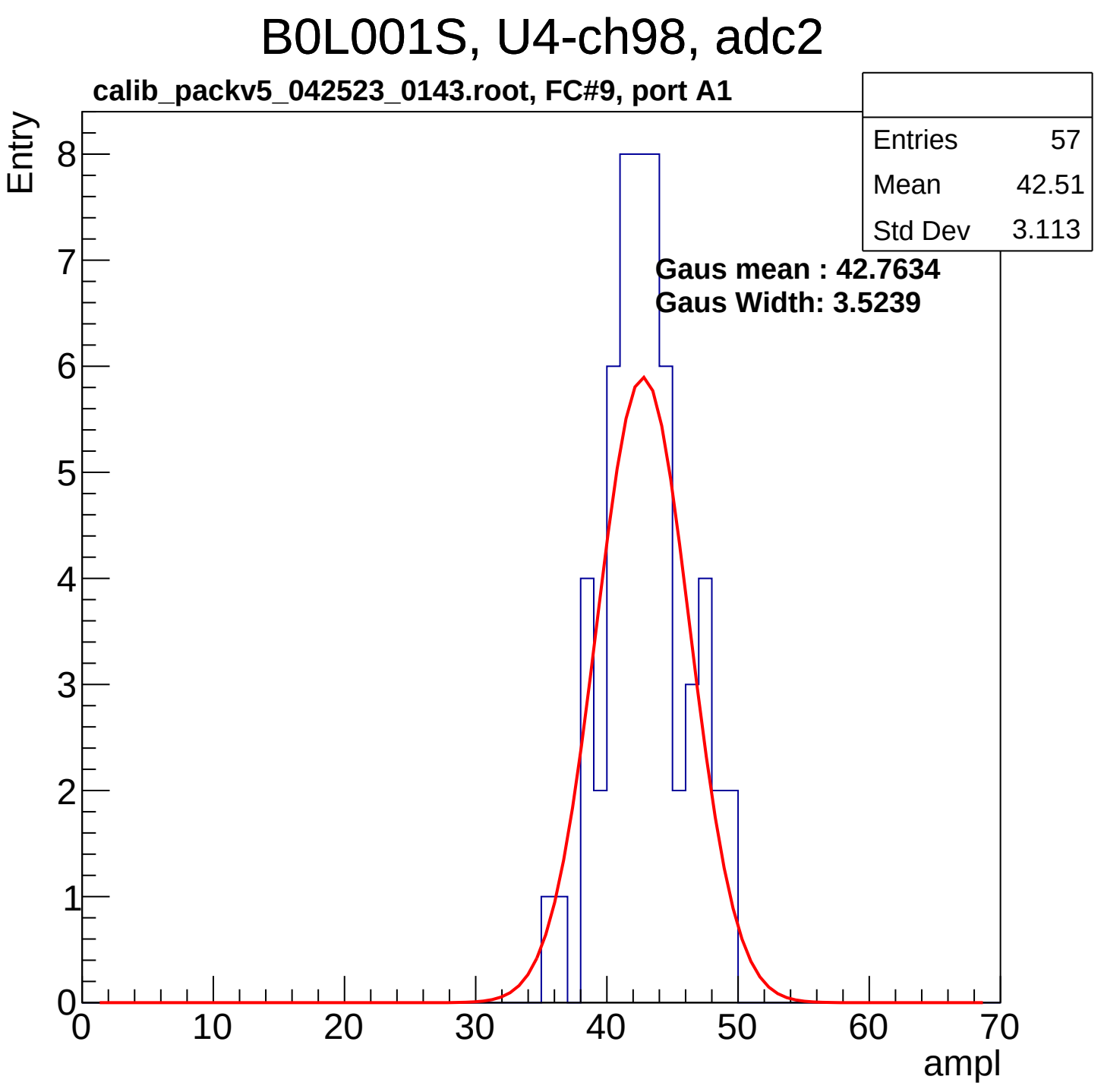
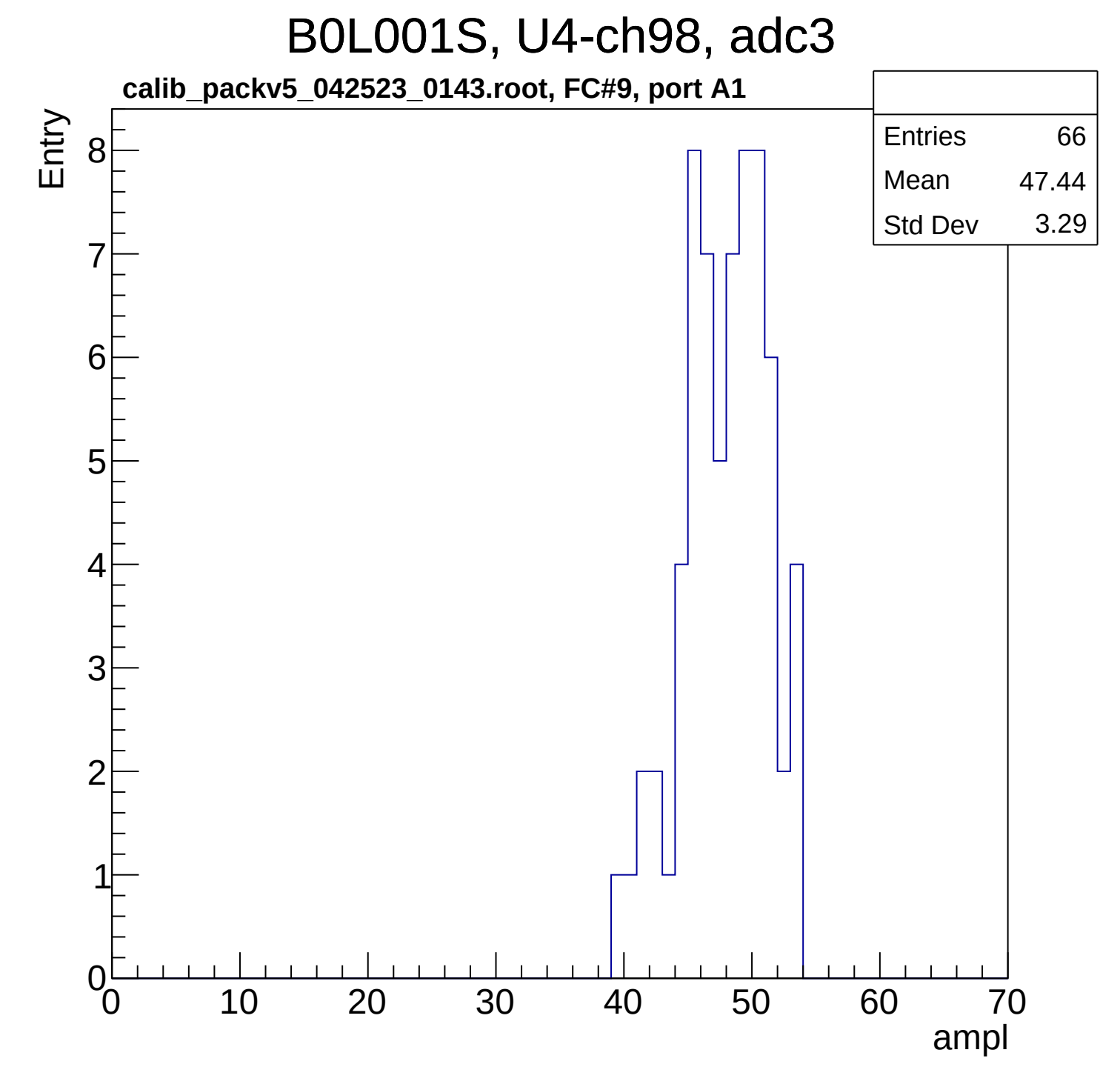

B0L001S, U4-ch97, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch98, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 30.19
Std Dev 3.518
Gaus mean : 30.1055
Gaus Width: 4.4005
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch98, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 35.15
Std Dev 3.129
Gaus mean : 36.0930
Gaus Width: 3.6859
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch98, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 42.51
Std Dev 3.113
Gaus mean : 42.7634
Gaus Width: 3.5239
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch98, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 47.44
Std Dev 3.29
Entry
ampl
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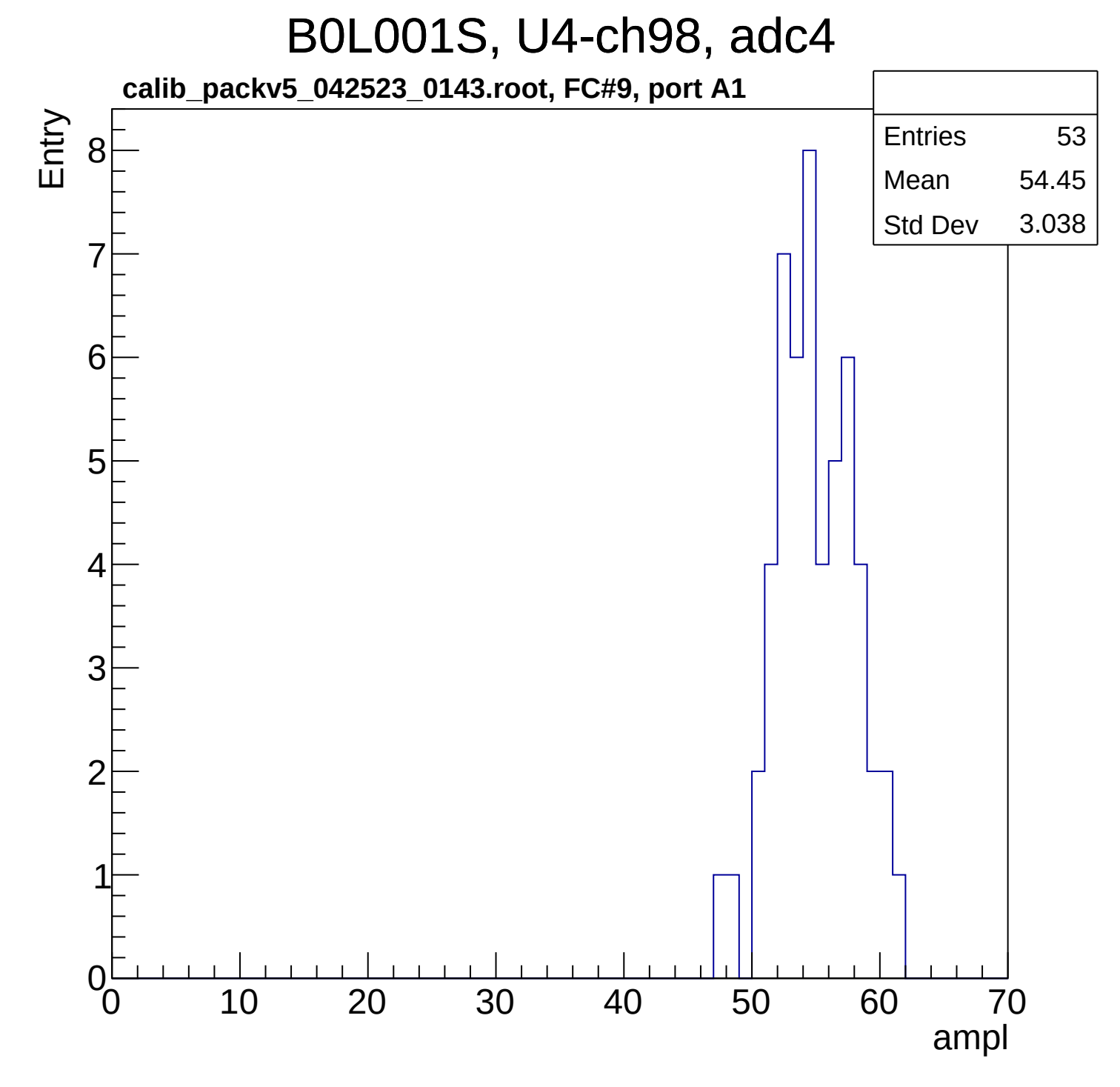
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch98, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 54.45
Std Dev 3.038
Entry
ampl
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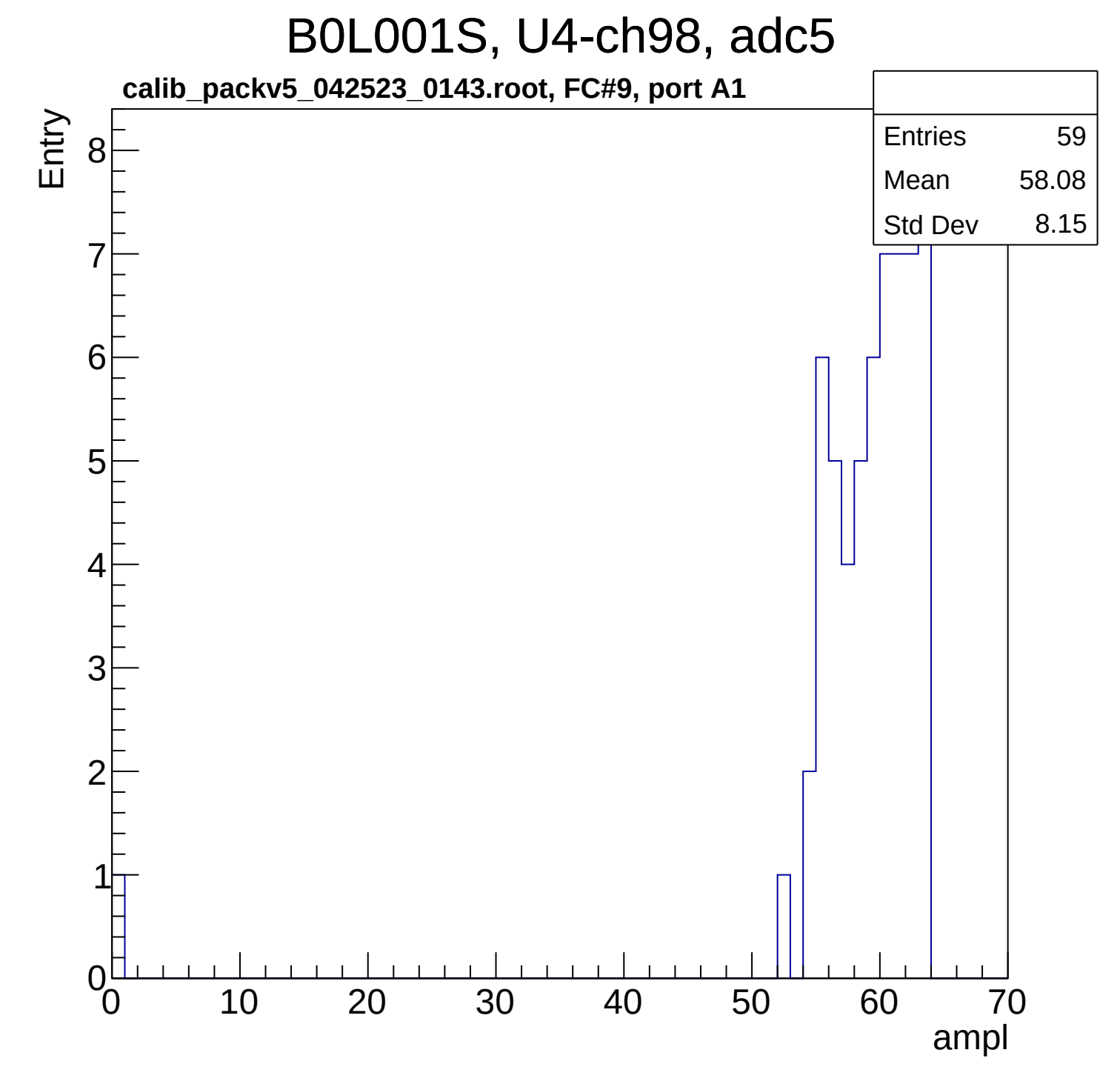
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch98, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 58.08
Std Dev 8.15
Entry
ampl
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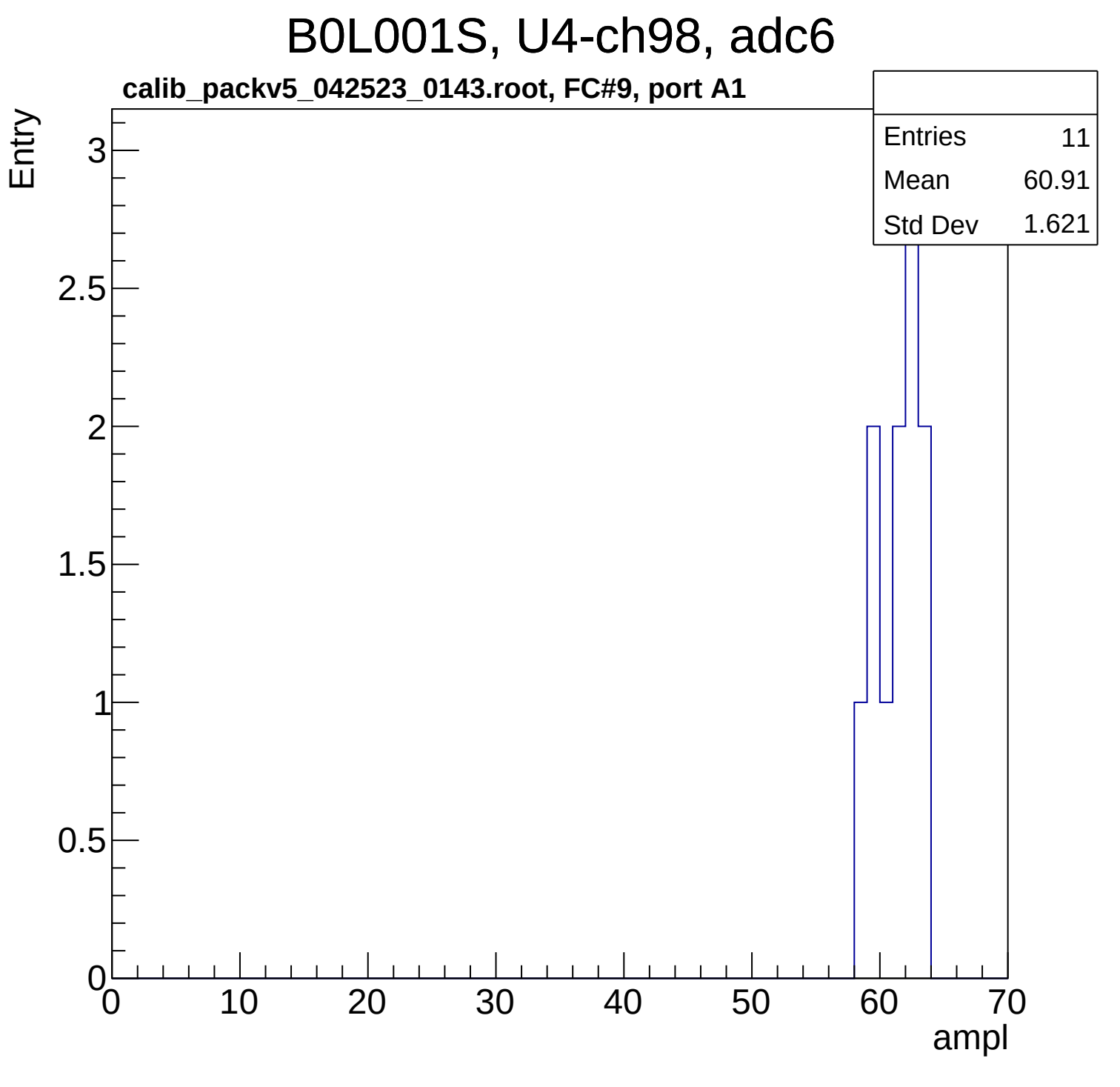
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch98, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 60.91
Std Dev 1.621
Entry
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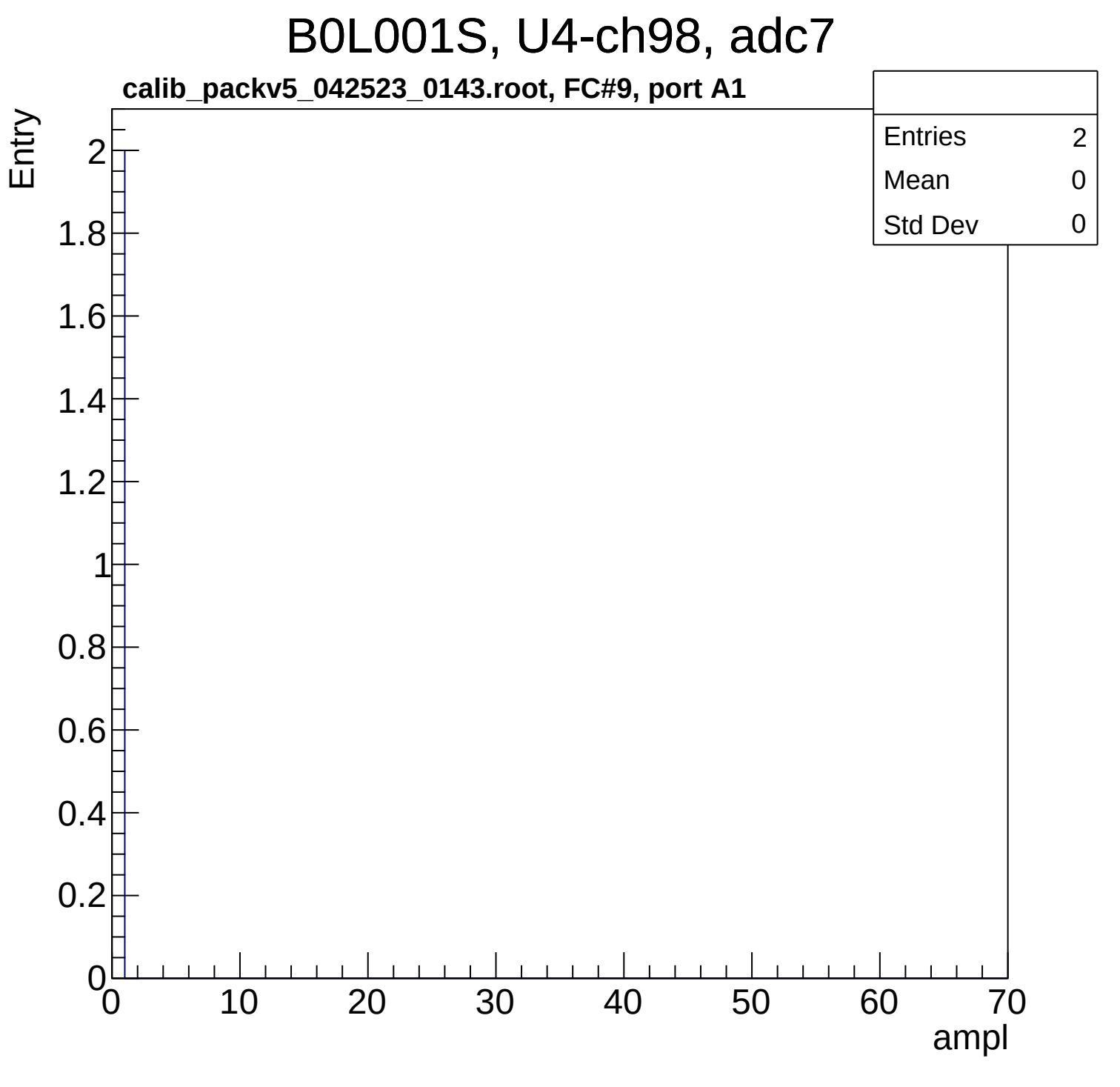
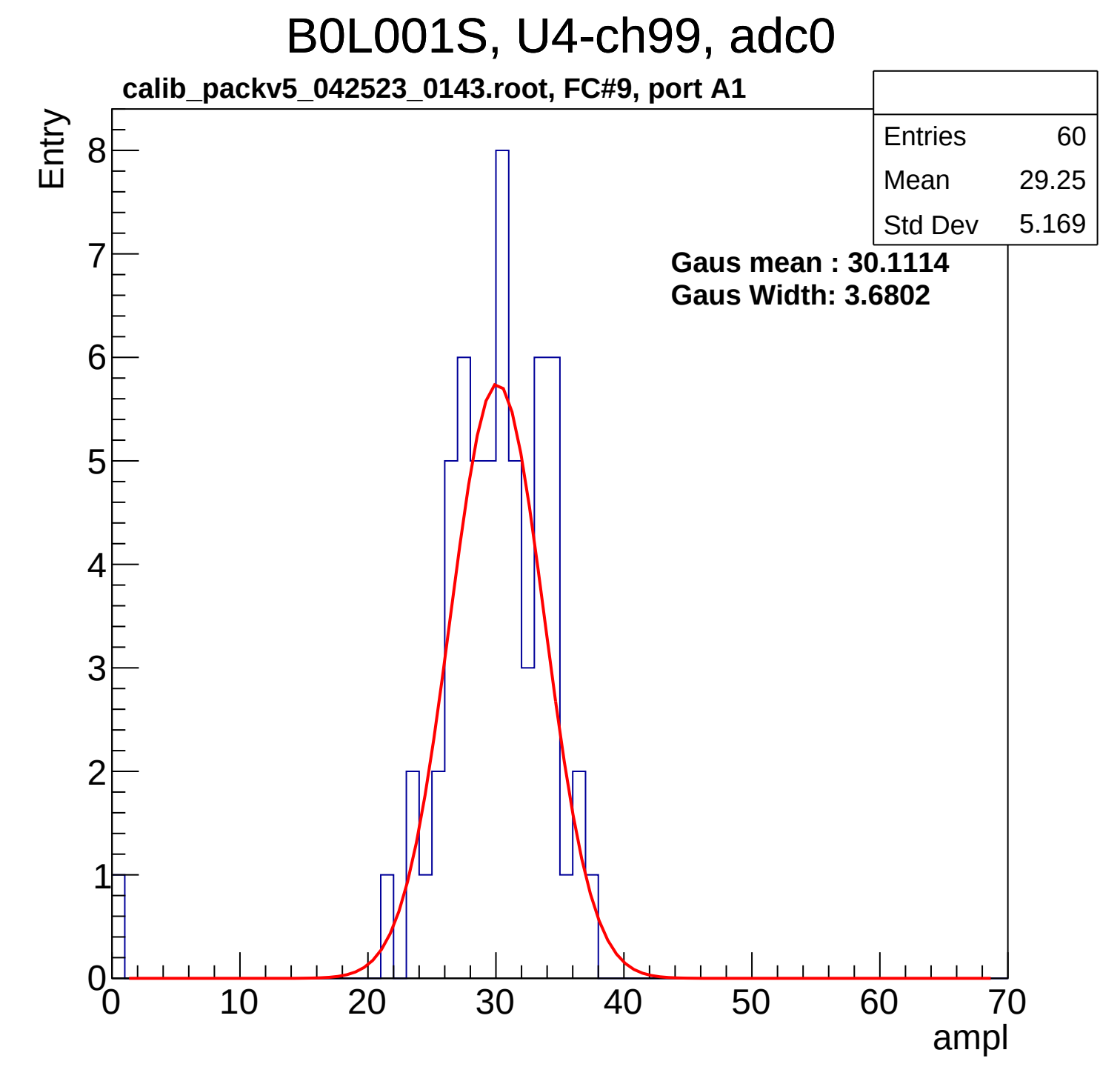
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch98, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch99, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.25
Std Dev 5.169
Gaus mean : 30.1114
Gaus Width: 3.6802
Entry
ampl
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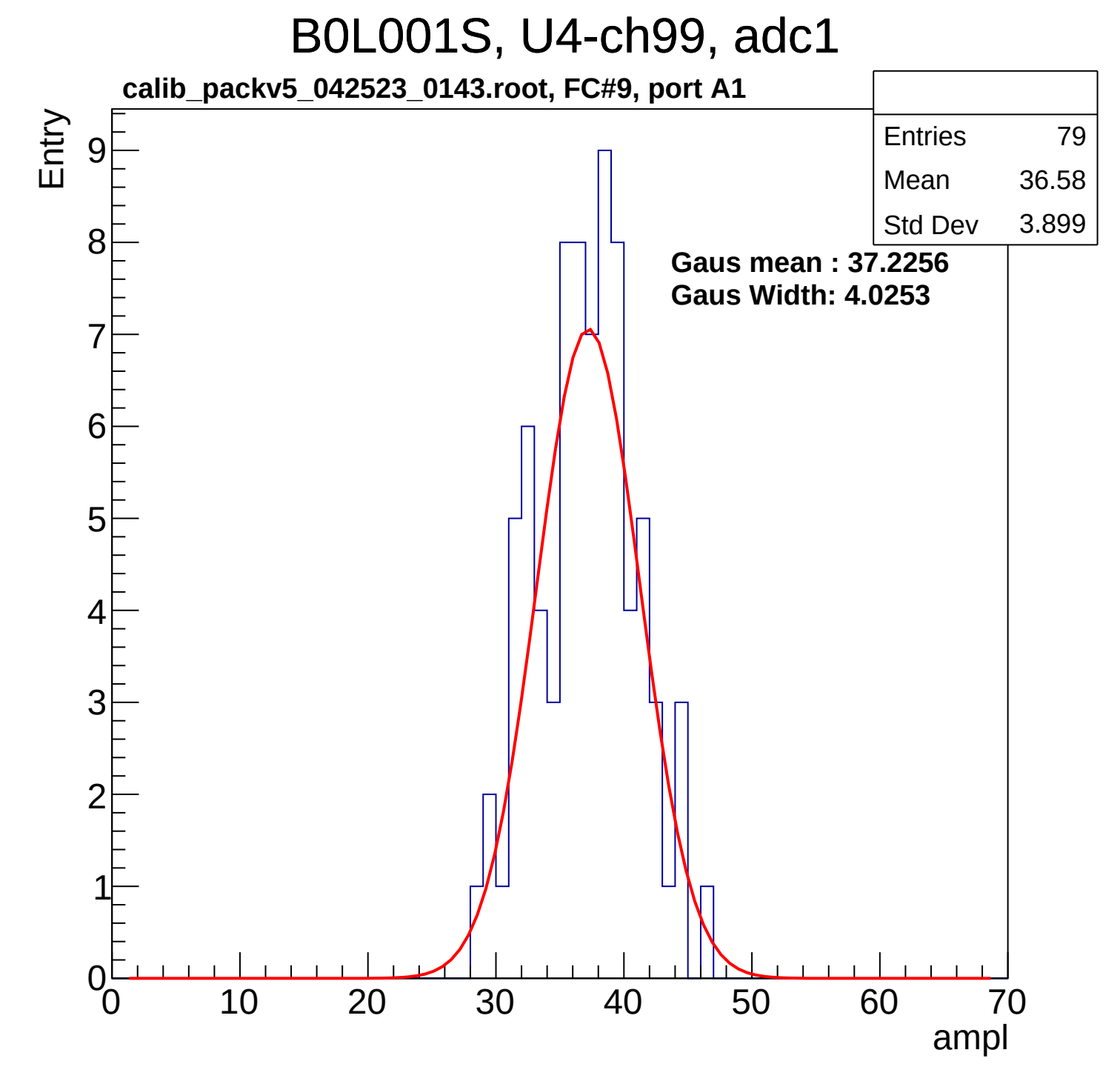
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch99, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 36.58
Std Dev 3.899
Gaus mean : 37.2256
Gaus Width: 4.0253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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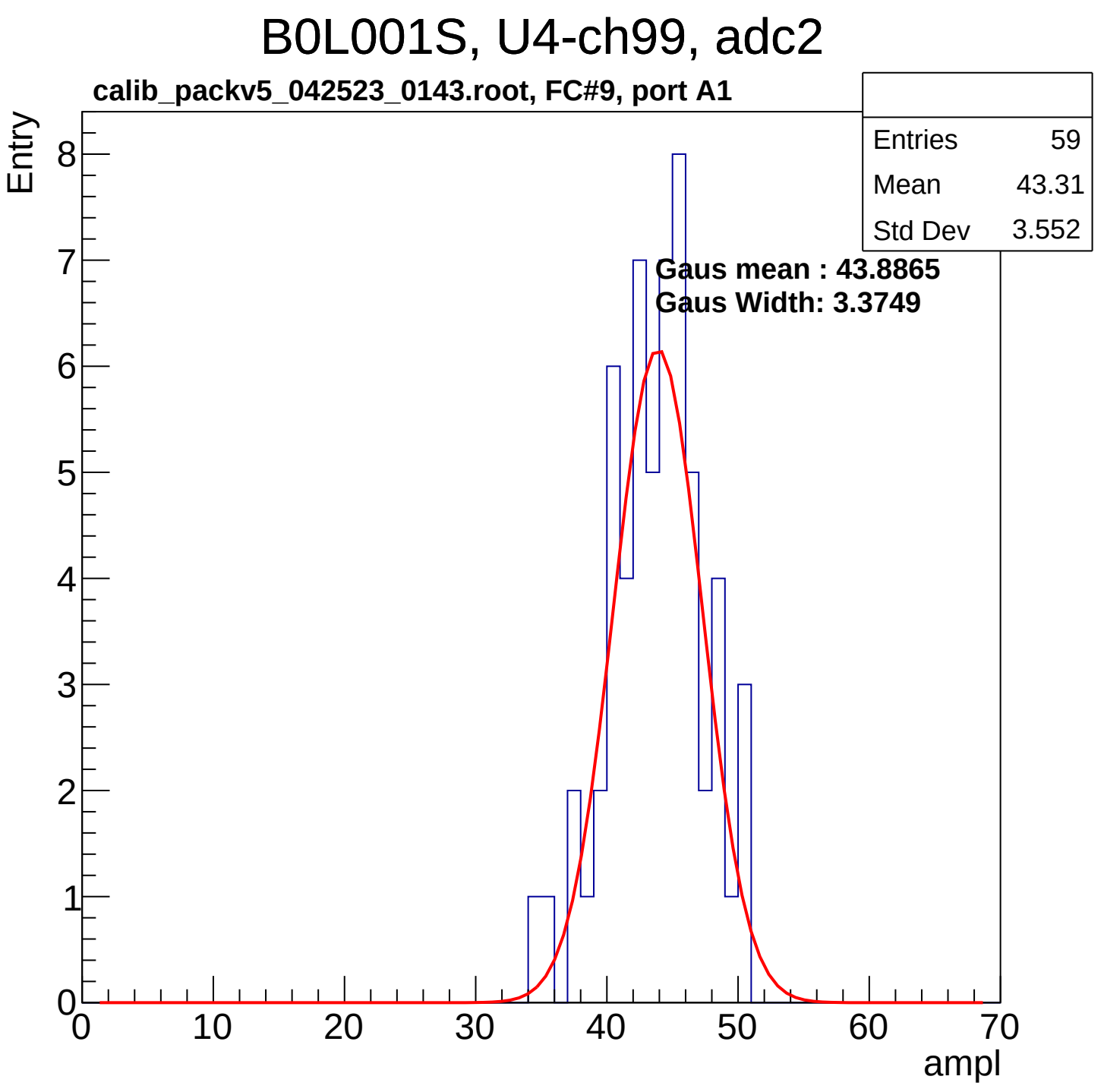
B0L001S, U4-ch99, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.31
Std Dev 3.552
Gaus mean : 43.8865
Gaus Width: 3.3749
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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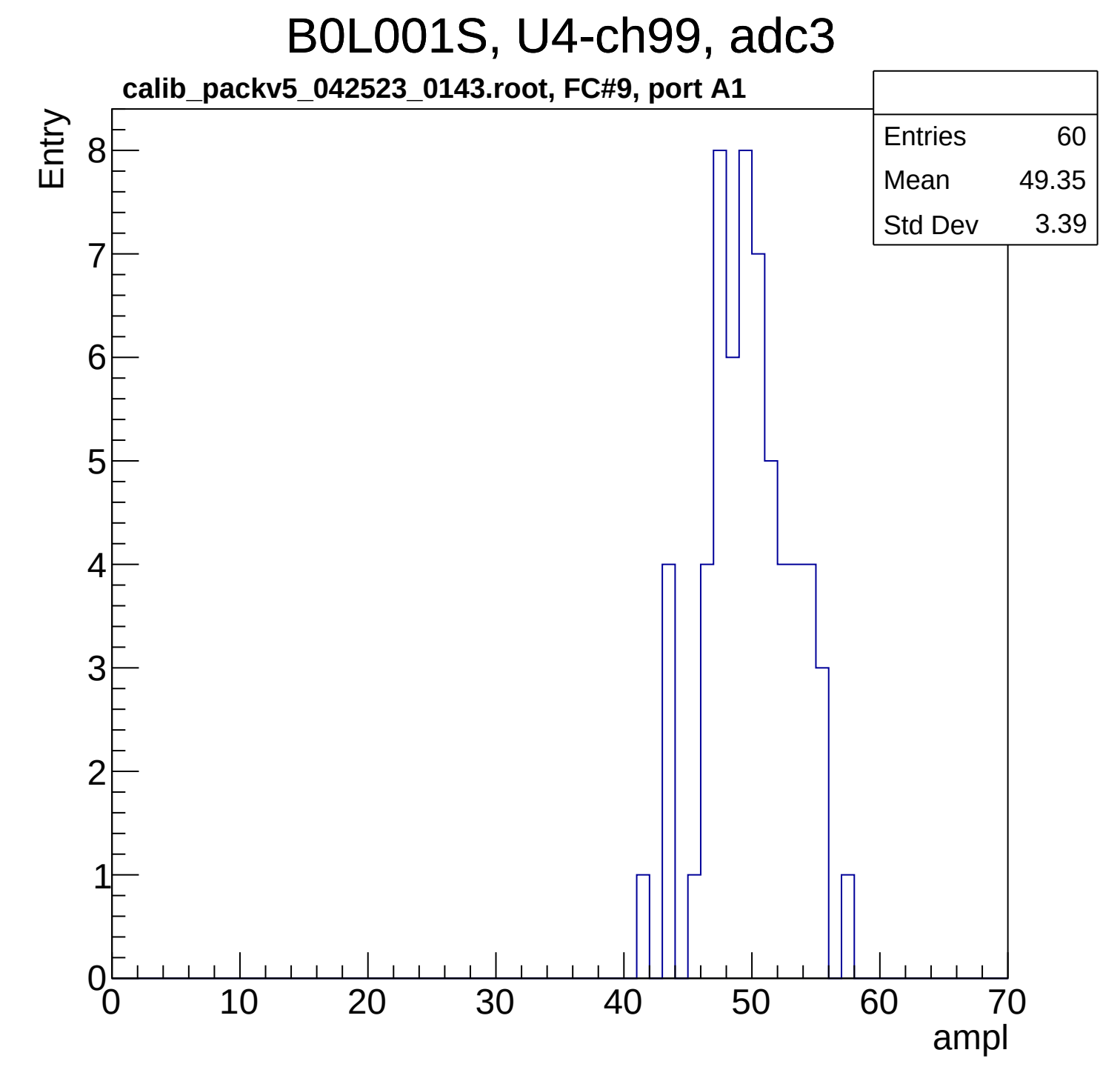
B0L001S, U4-ch99, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 49.35
Std Dev 3.39
Entry
ampl
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3
4
5
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7
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0
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60
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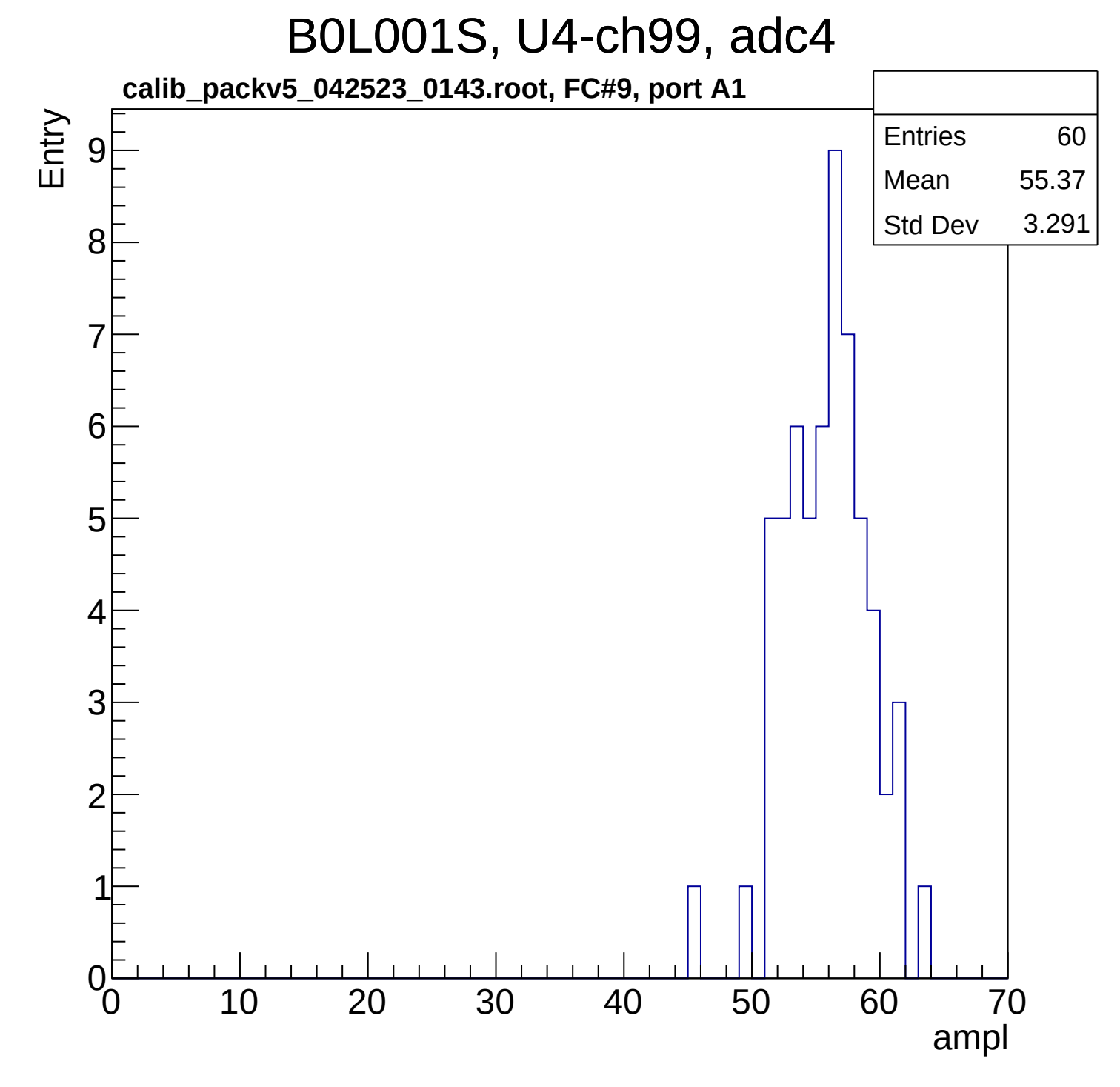
B0L001S, U4-ch99, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 55.37
Std Dev 3.291
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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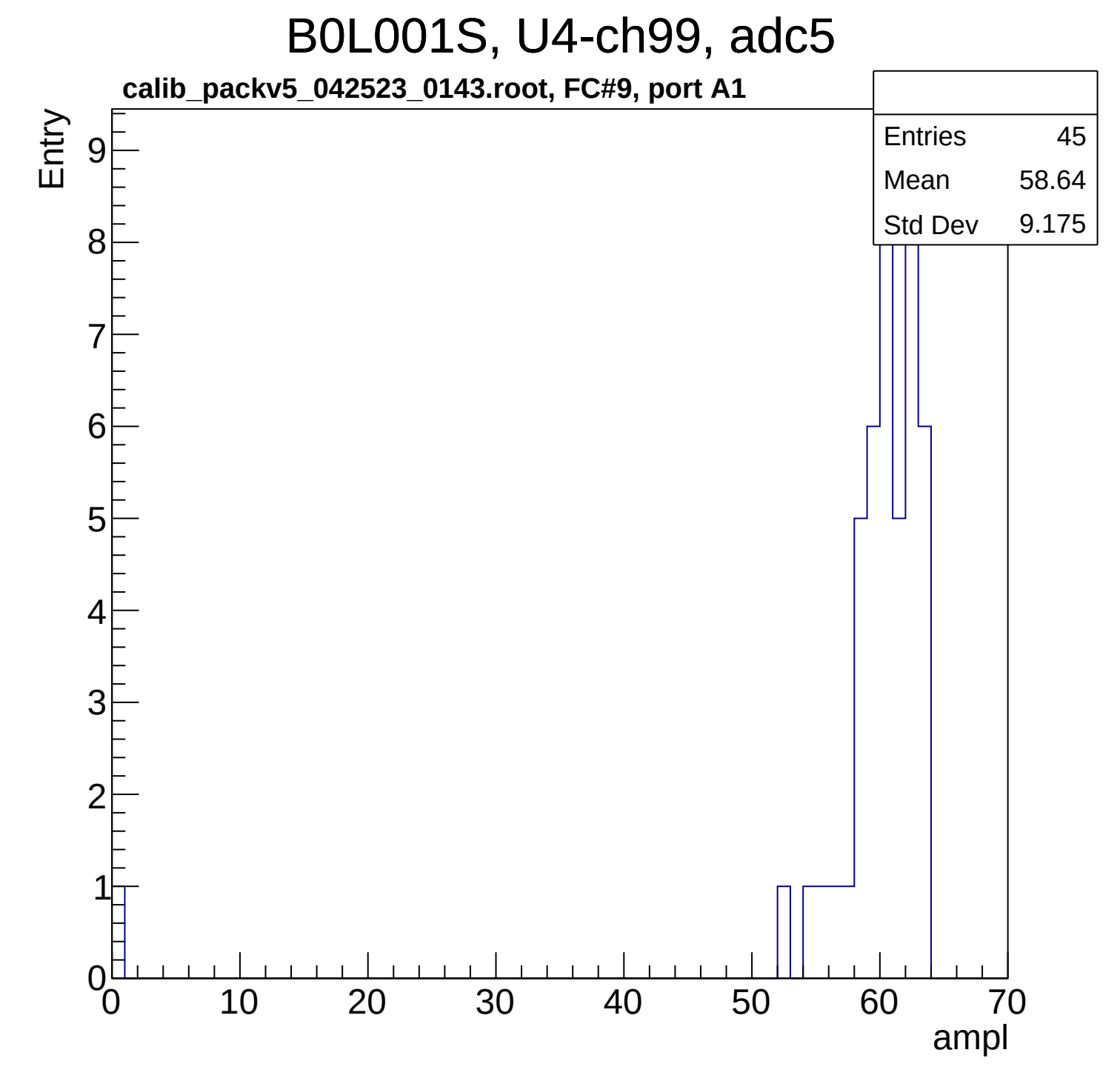
B0L001S, U4-ch99, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.64
Std Dev 9.175
Entry
ampl
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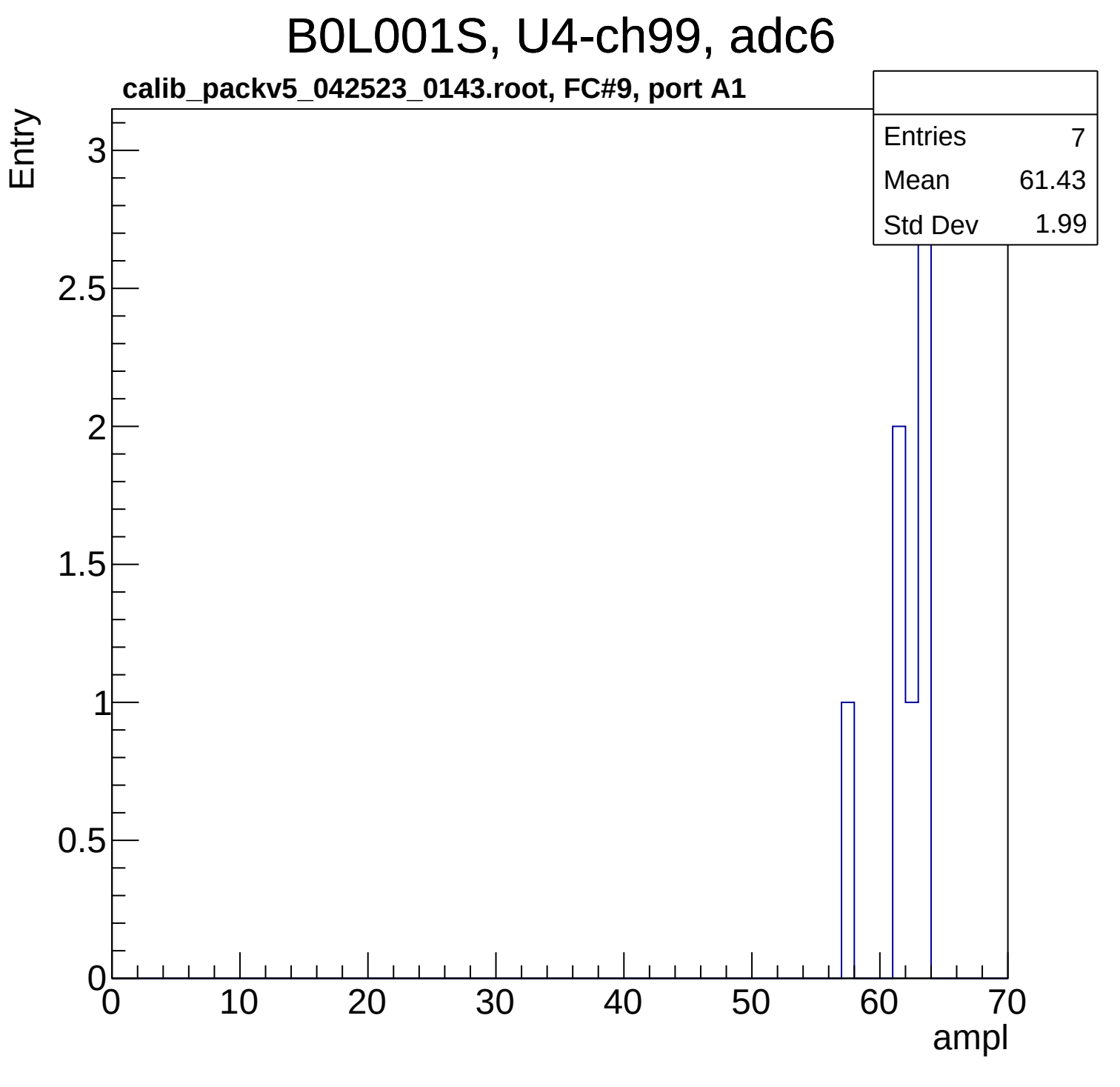
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch99, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.43
Std Dev 1.99
Entry
ampl
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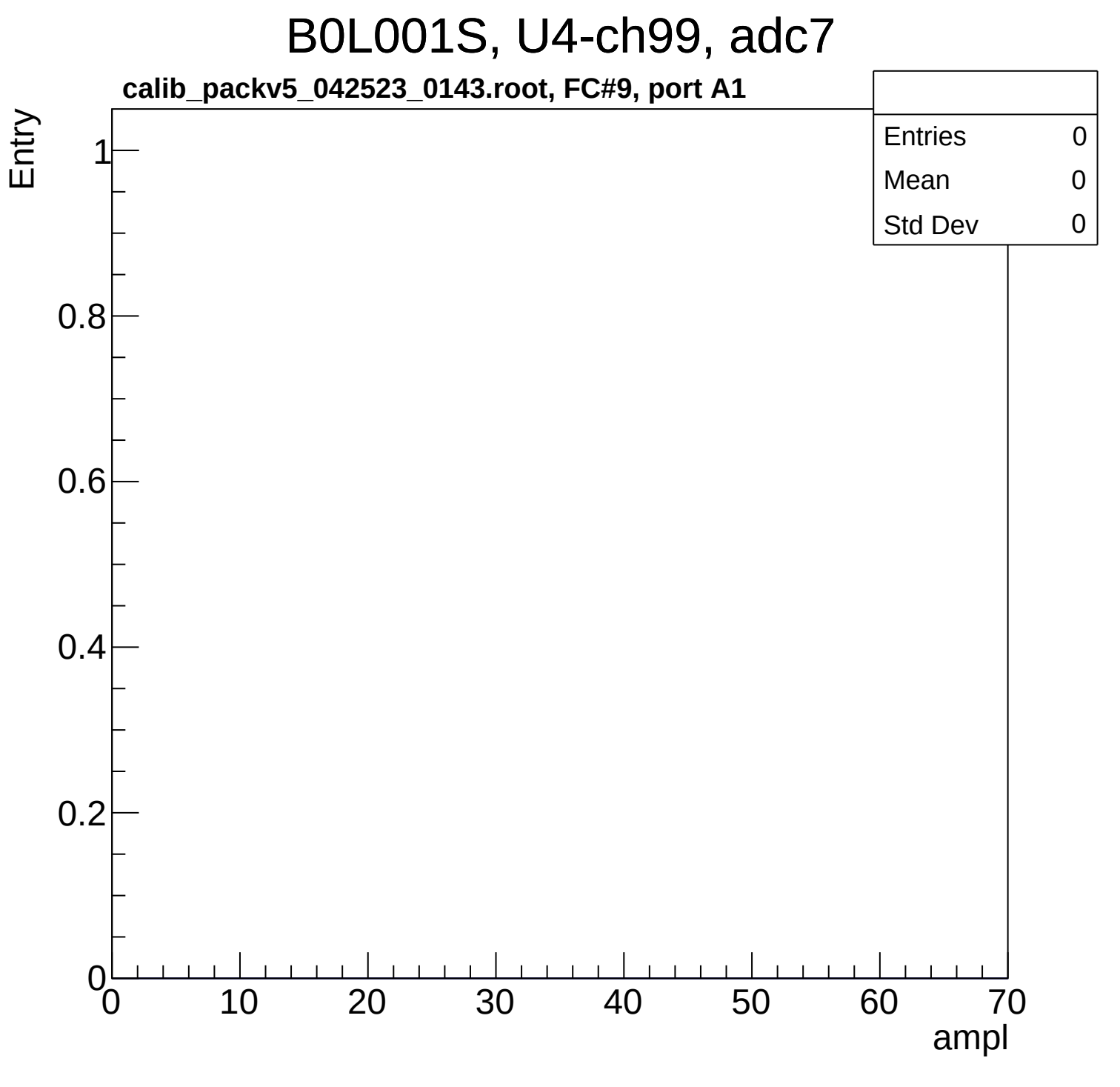
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U4-ch99, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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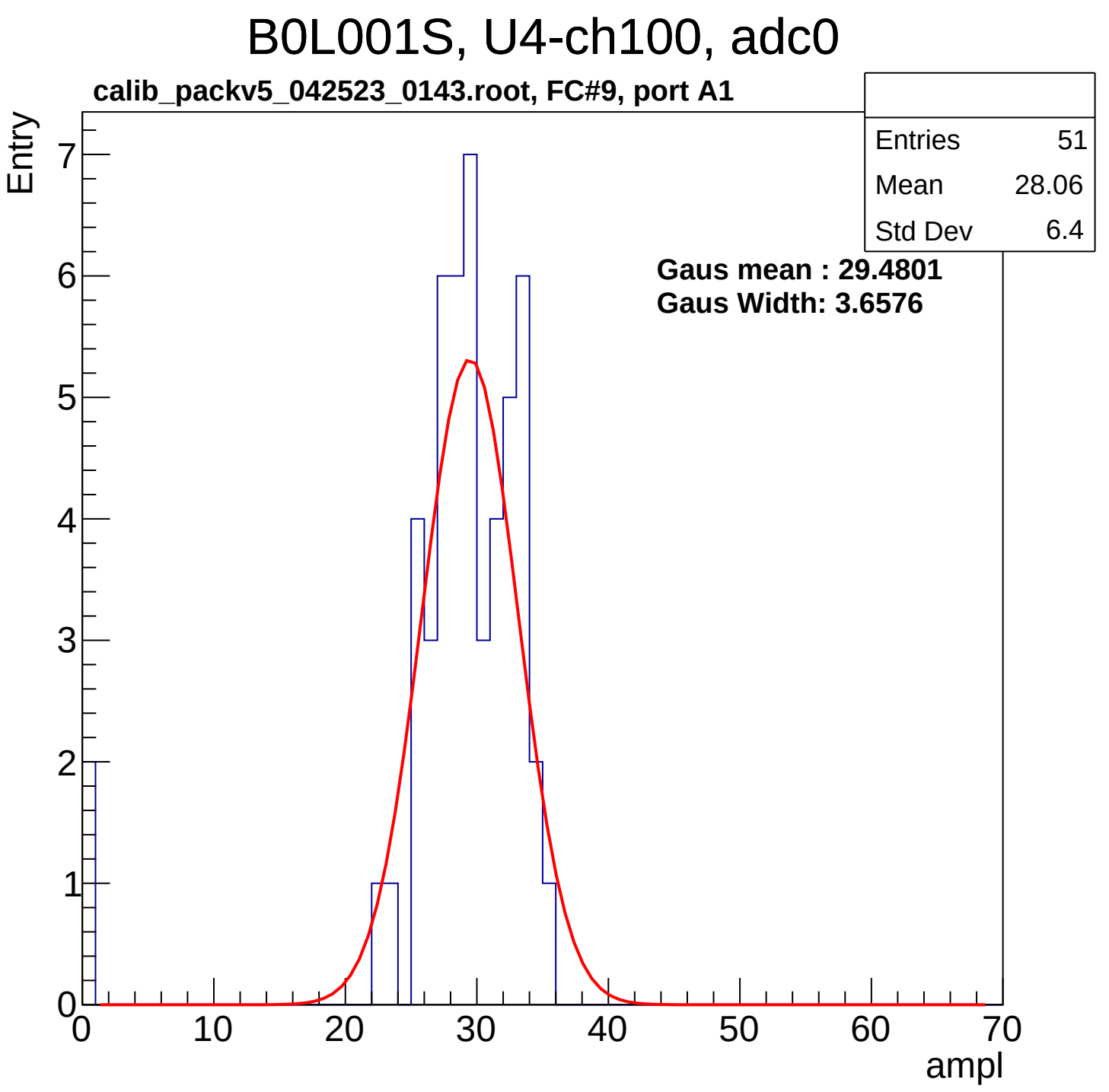
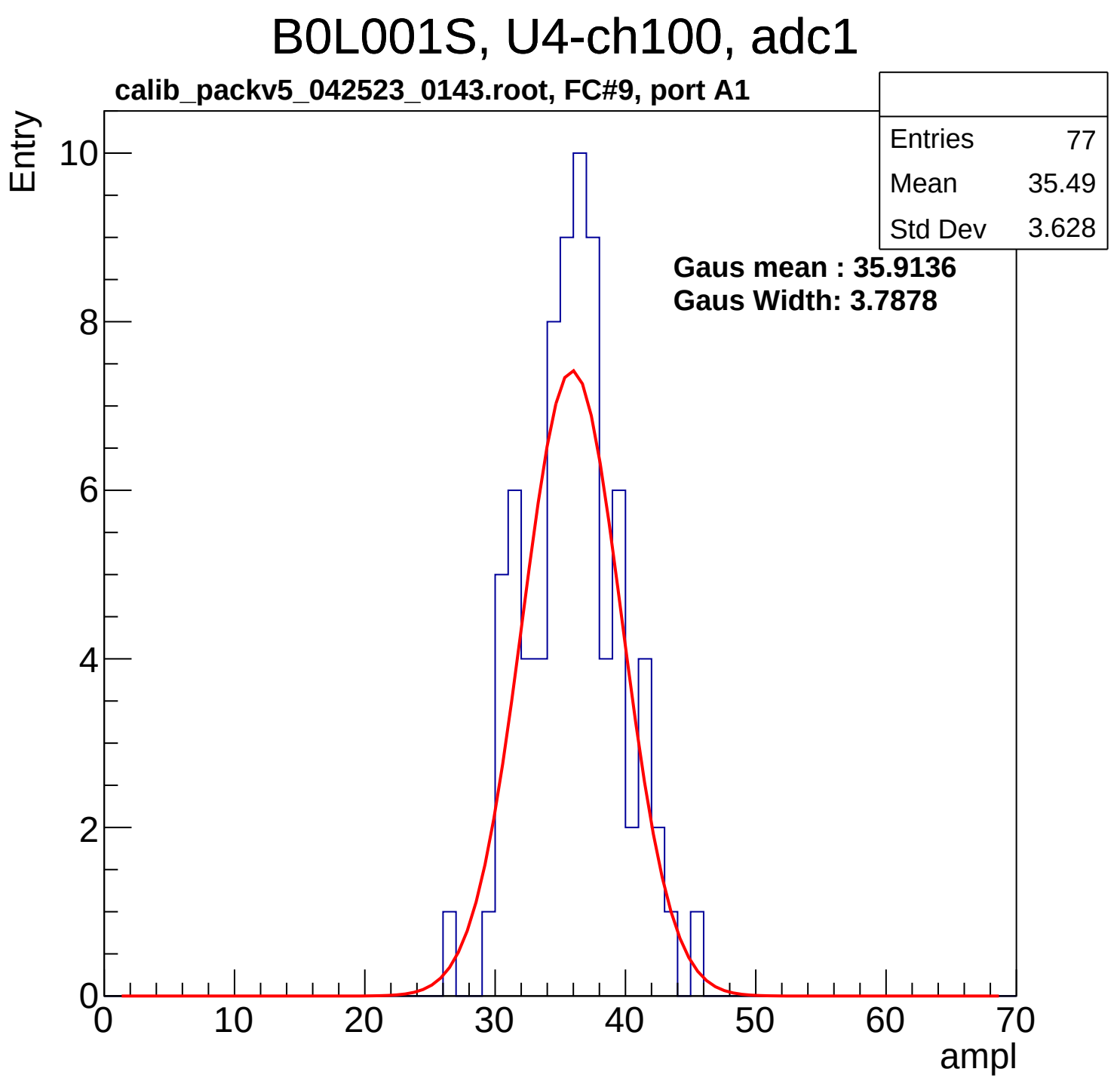
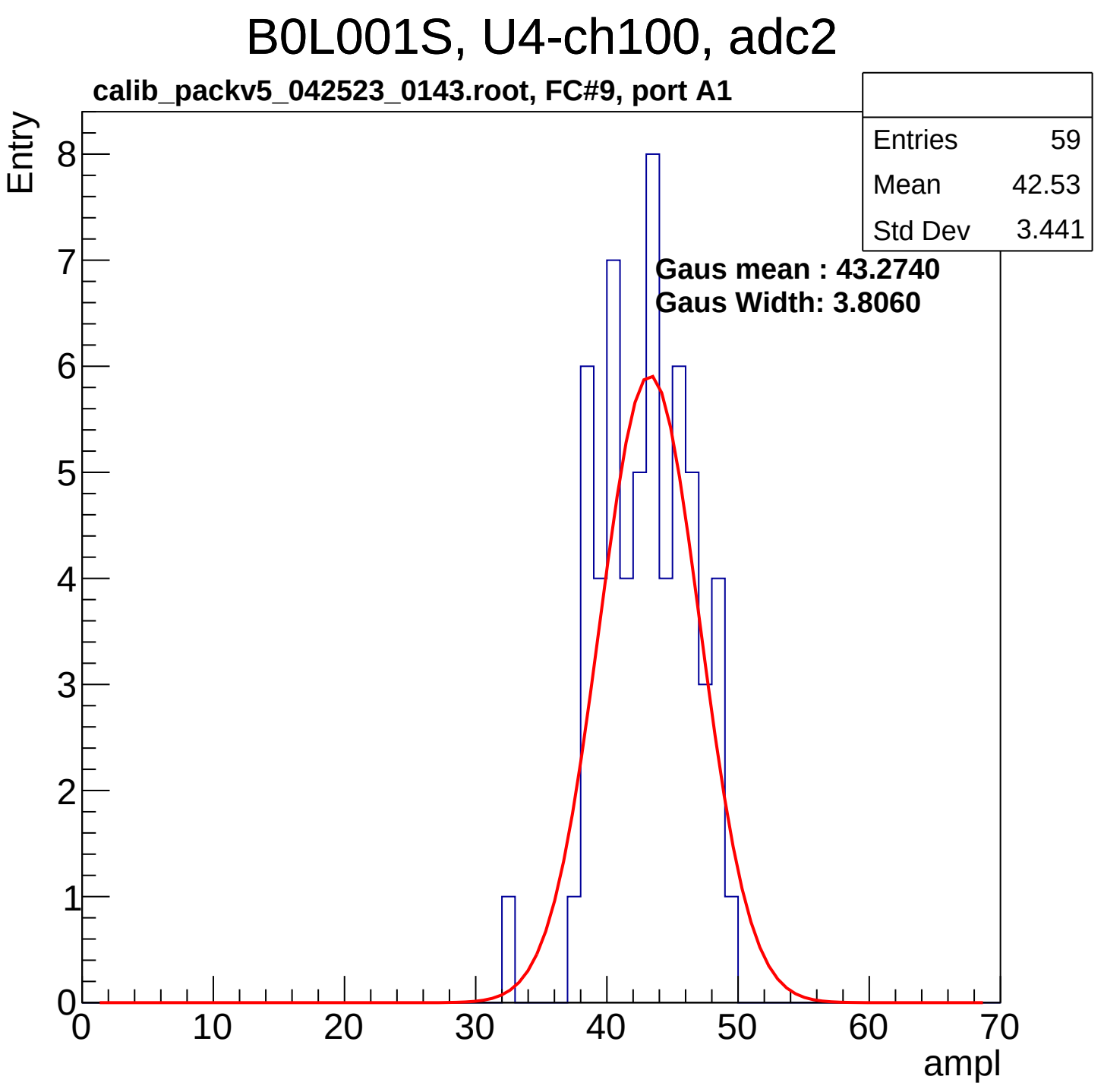
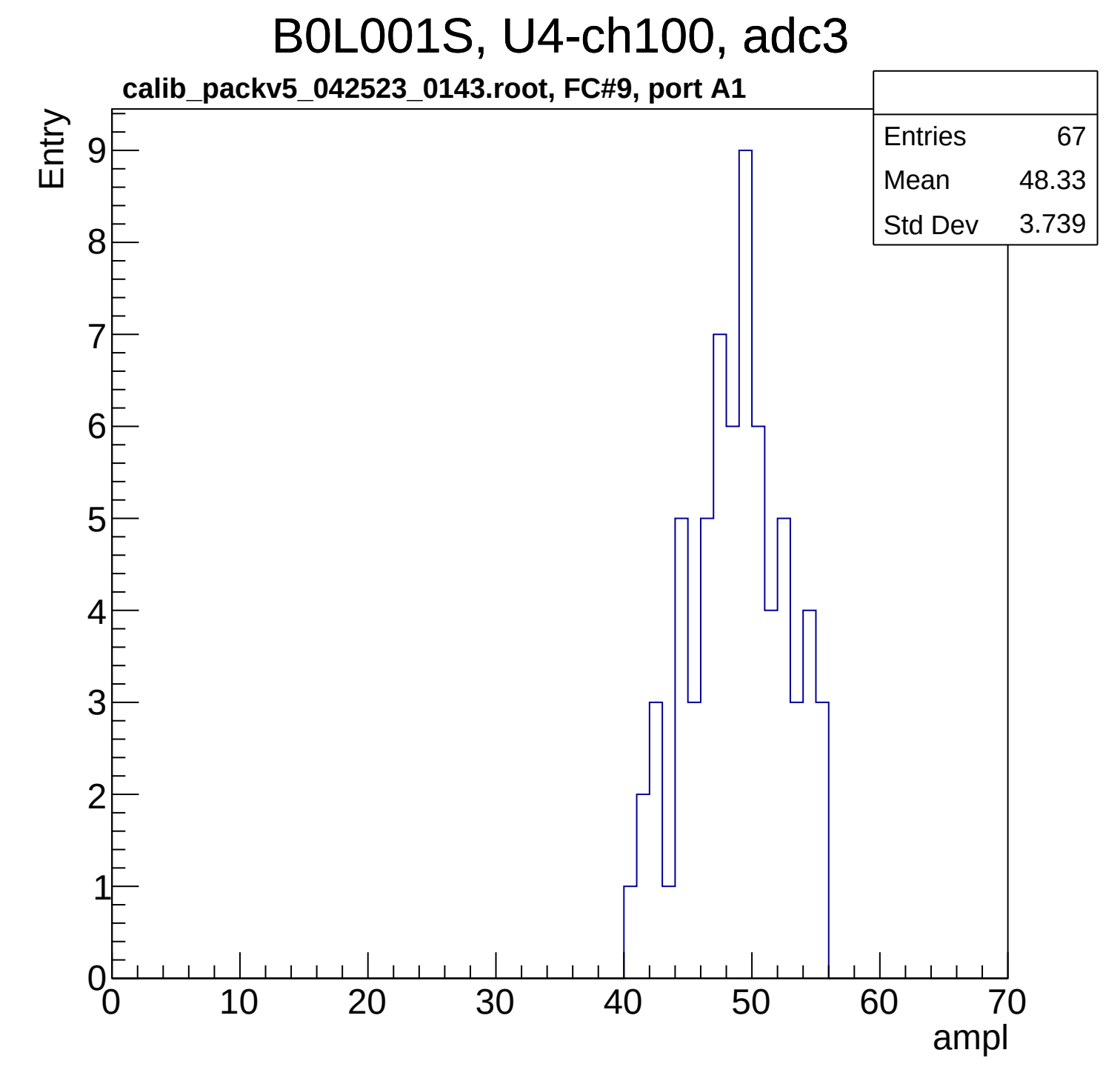
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch100, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 28.06
Std Dev 6.4
Gaus mean : 29.4801
Gaus Width: 3.6576
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch100, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 35.49
Std Dev 3.628
Gaus mean : 35.9136
Gaus Width: 3.7878
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch100, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 42.53
Std Dev 3.441
Gaus mean : 43.2740
Gaus Width: 3.8060
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch100, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 48.33
Std Dev 3.739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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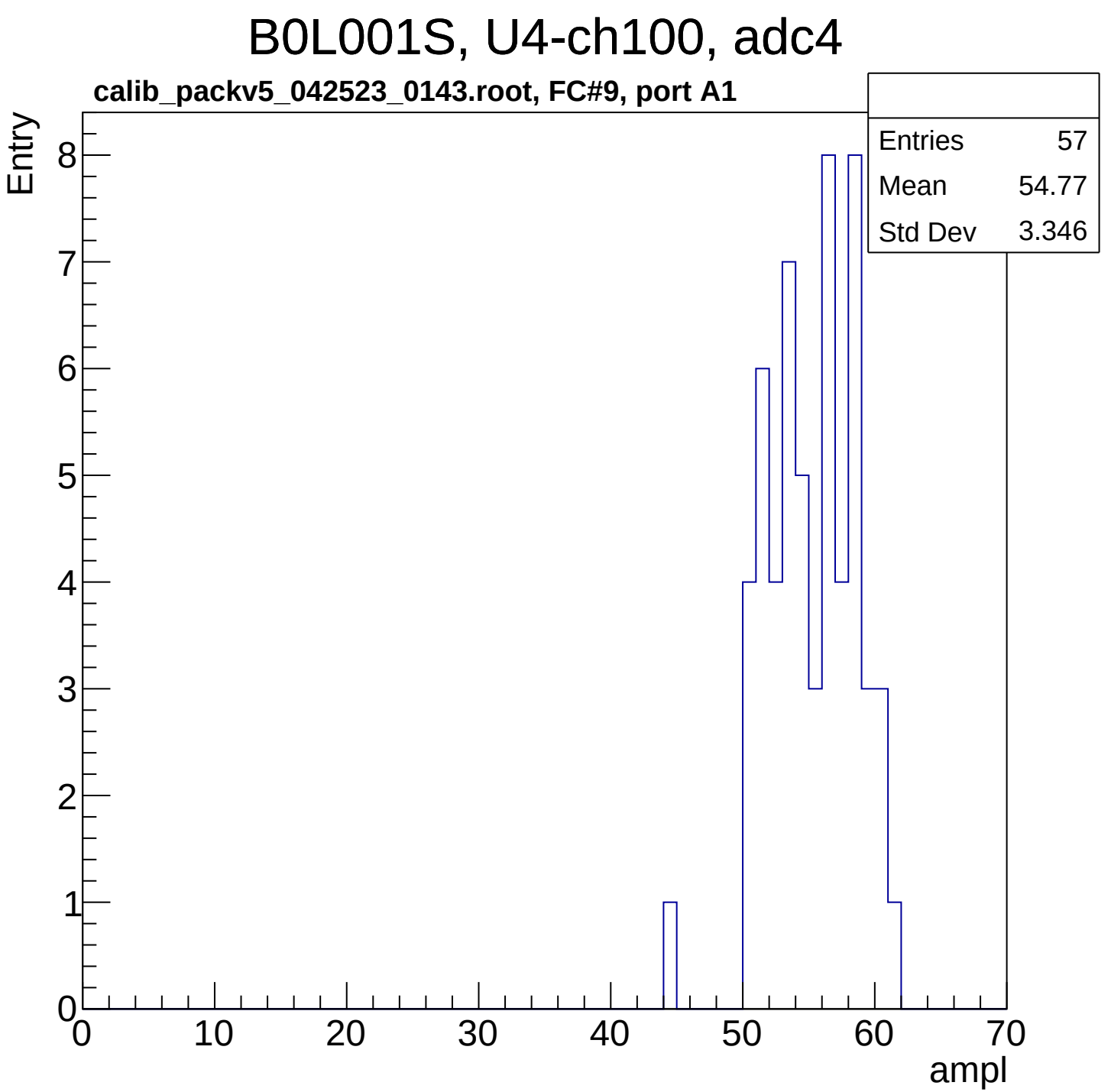
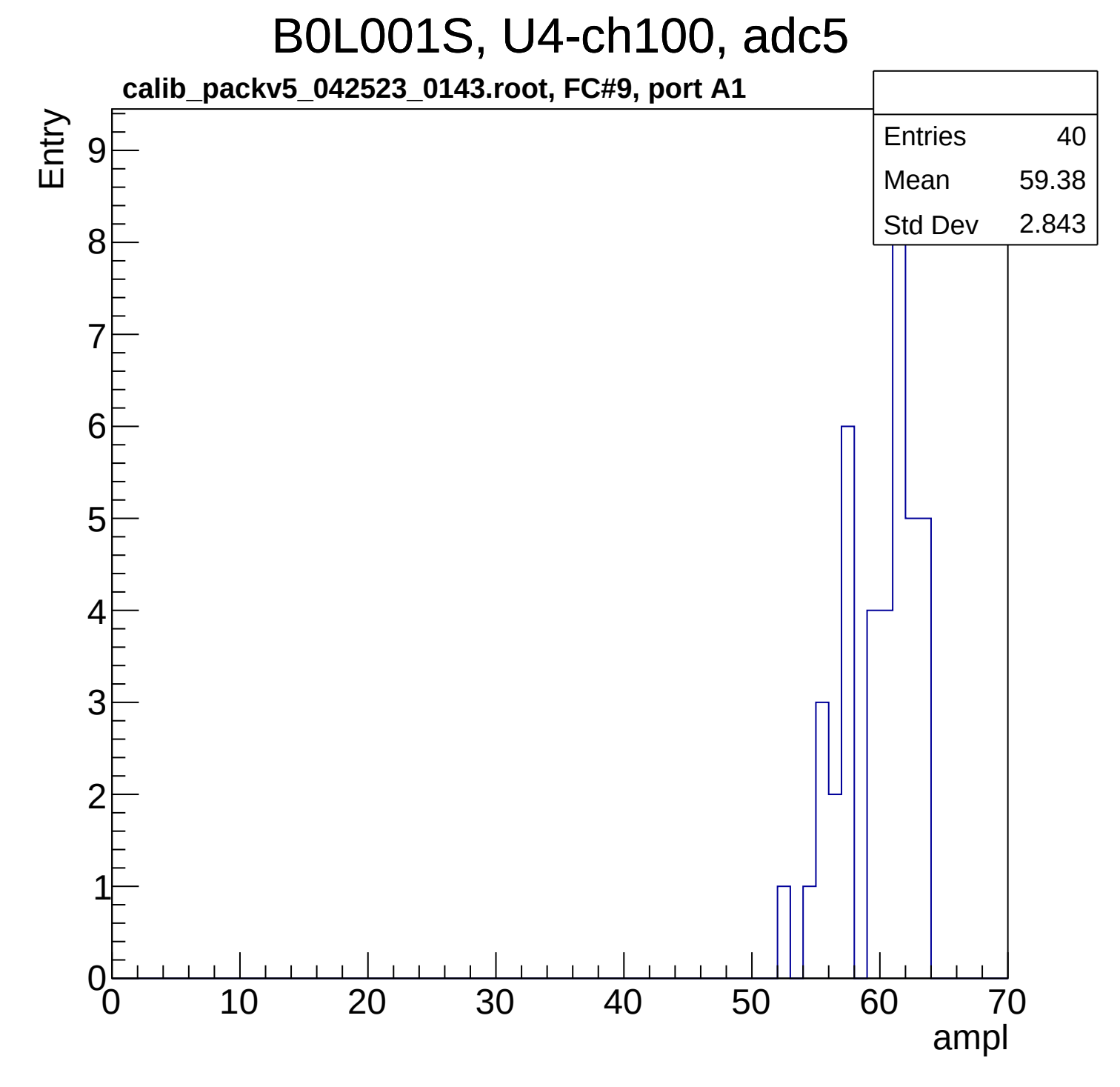
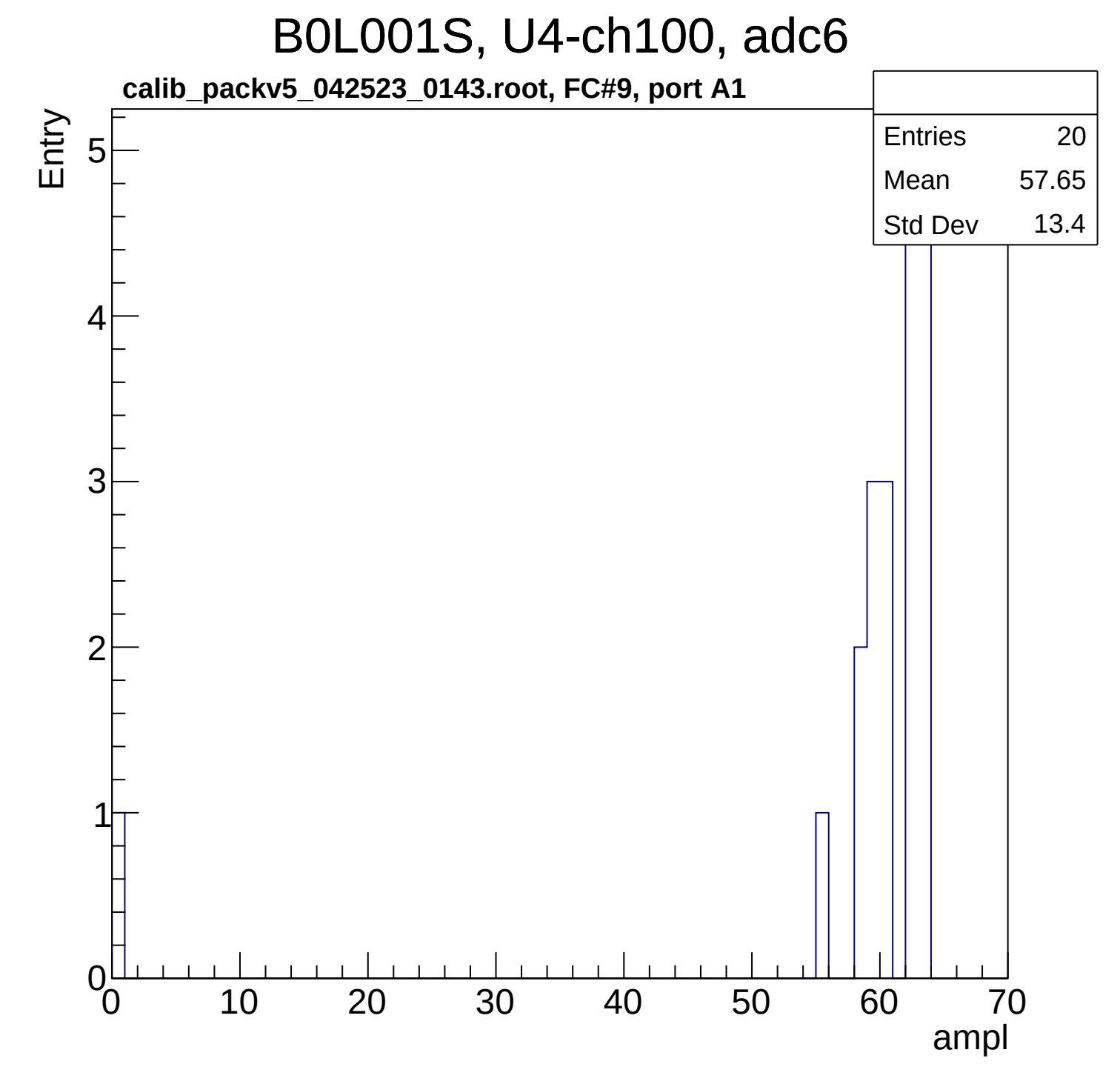
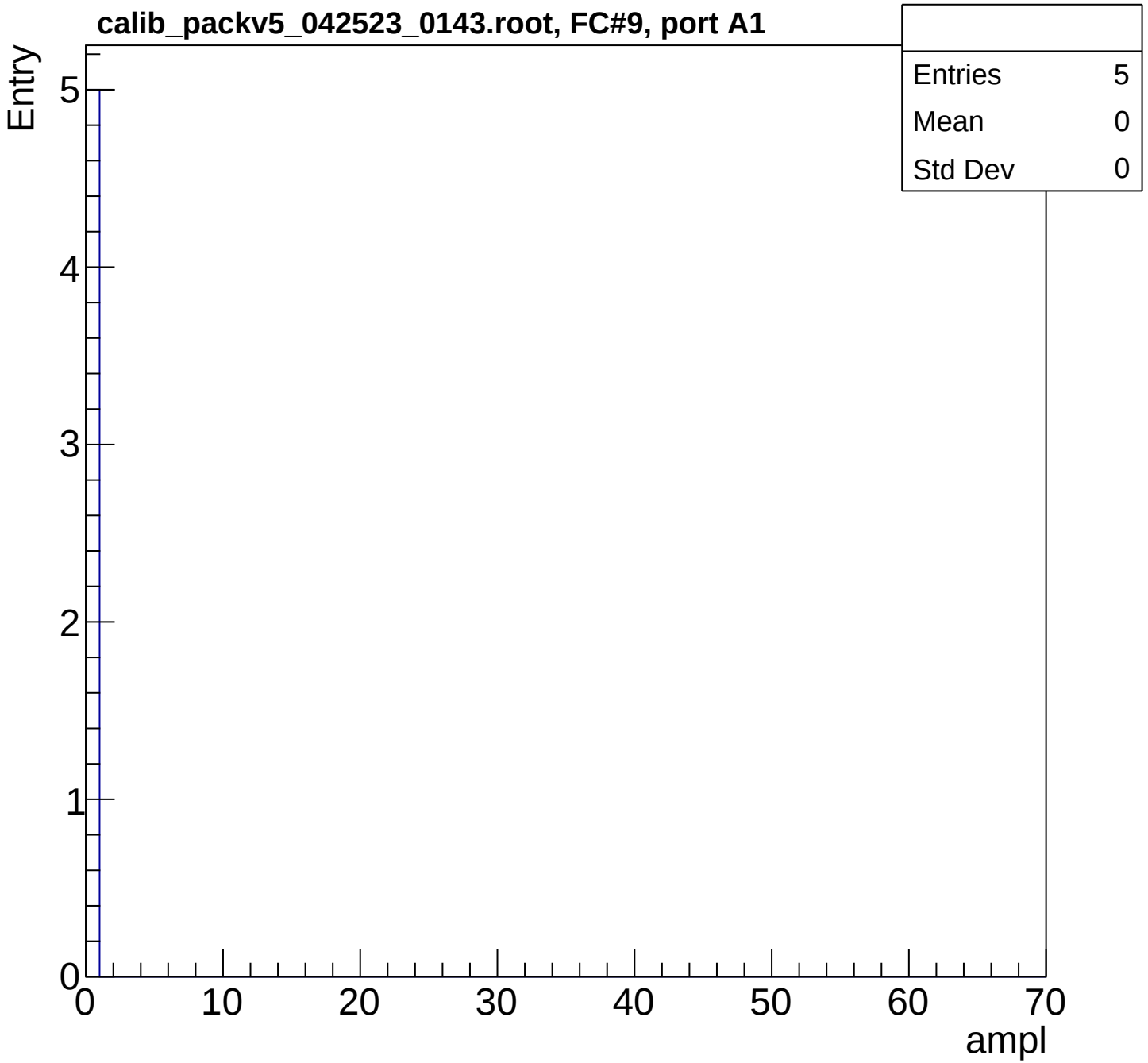
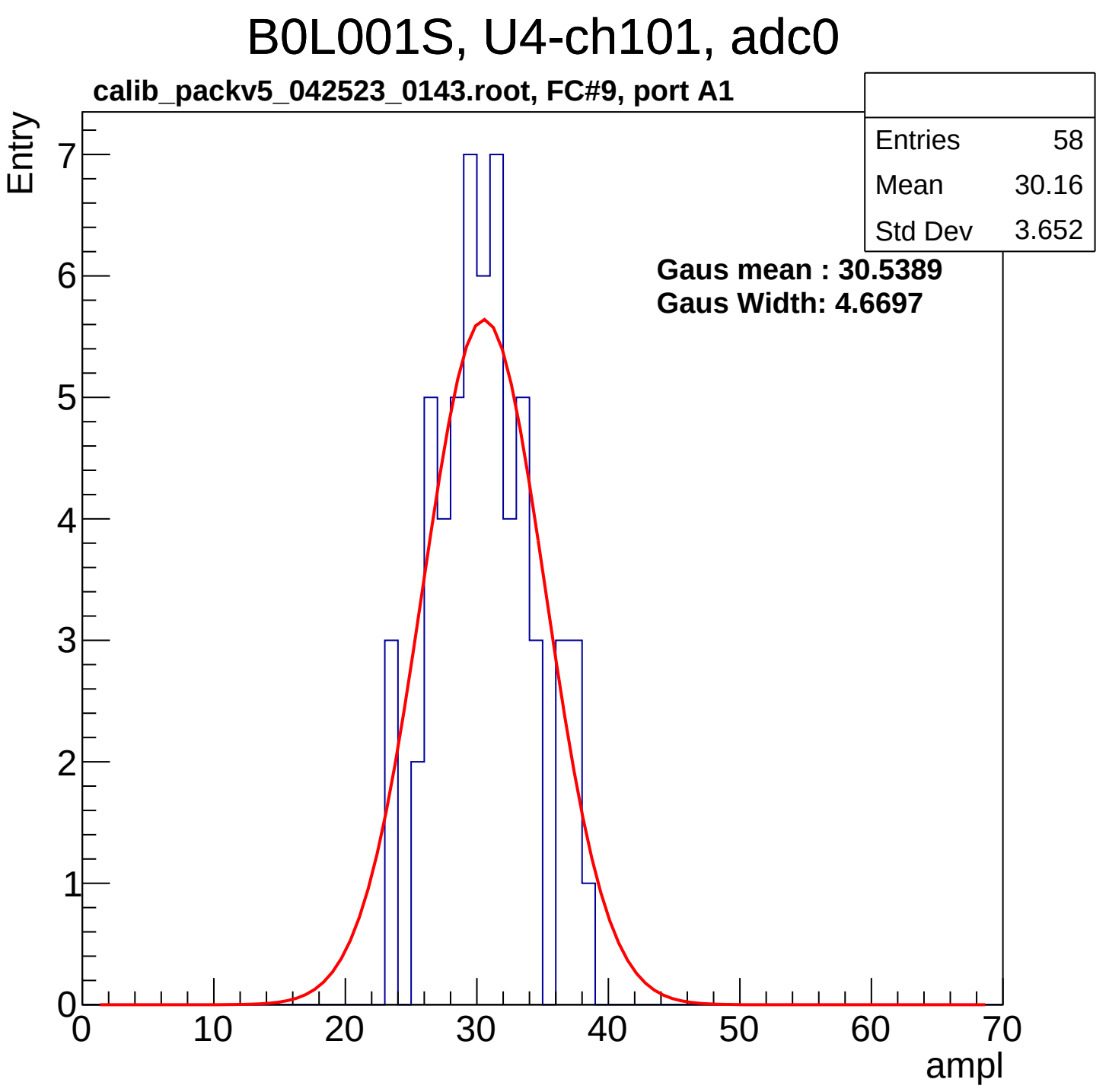
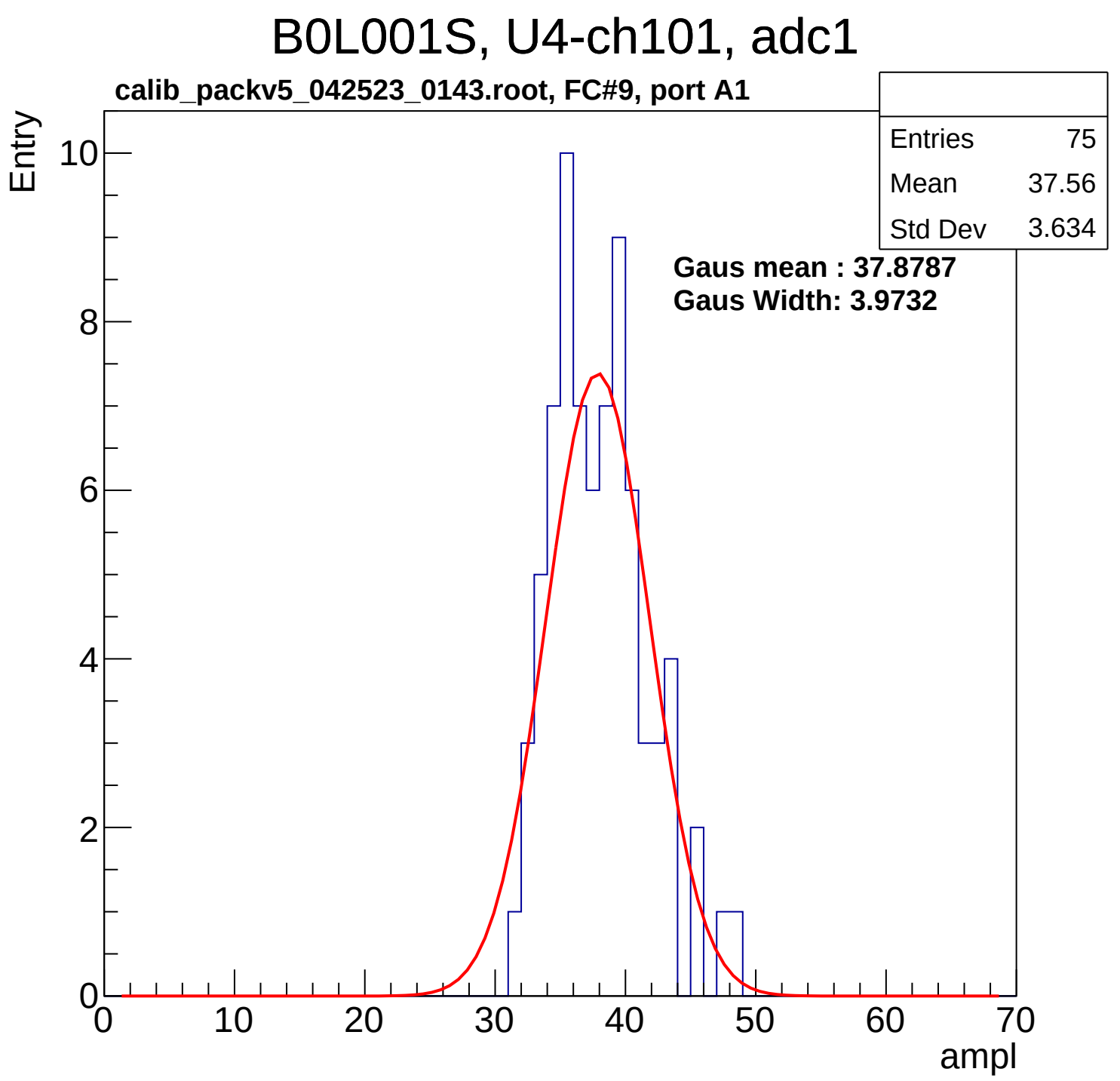

B0L001S, U4-ch100, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 54.77
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

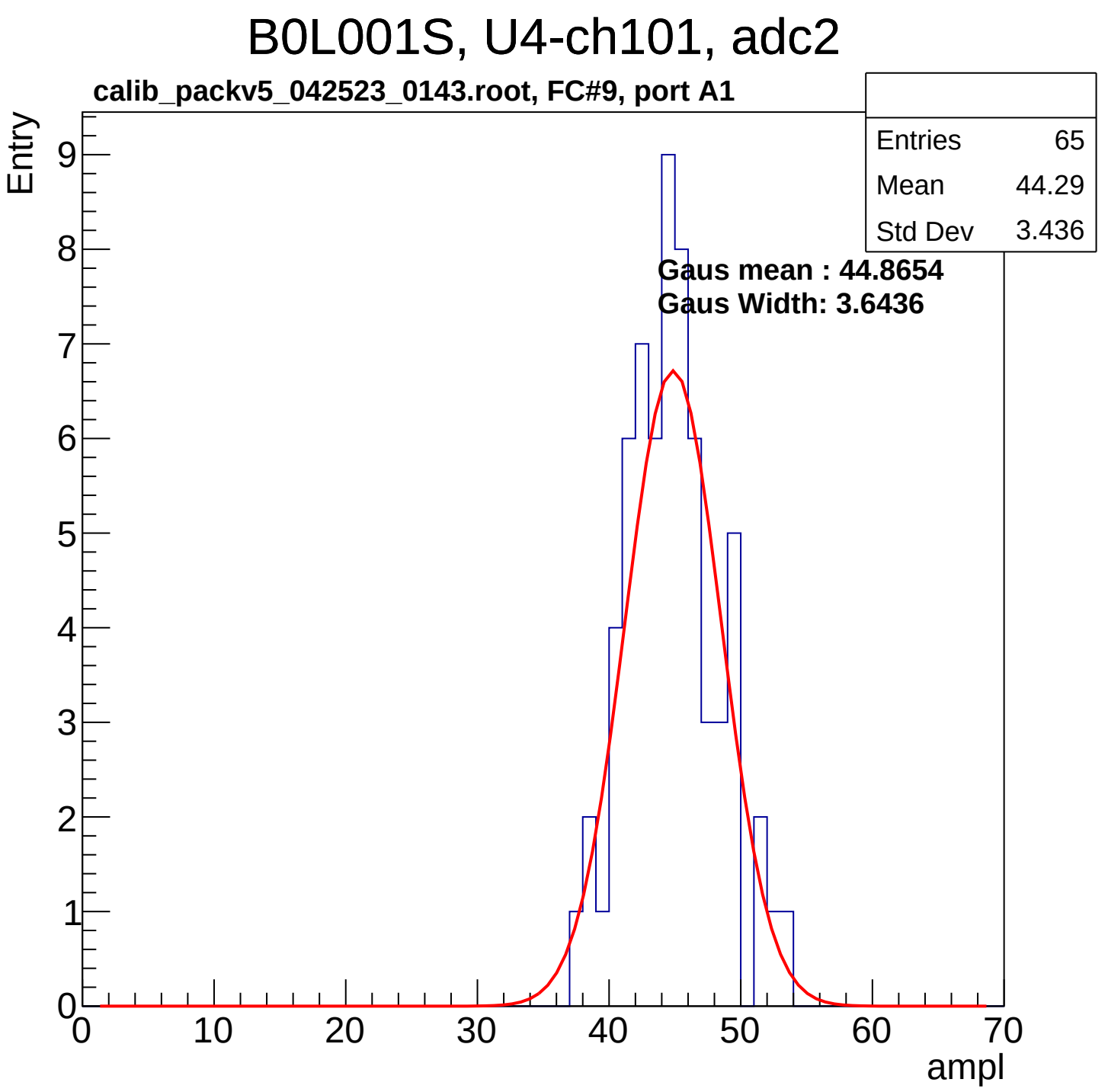

B0L001S, U4-ch100, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.38
Std Dev 2.843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

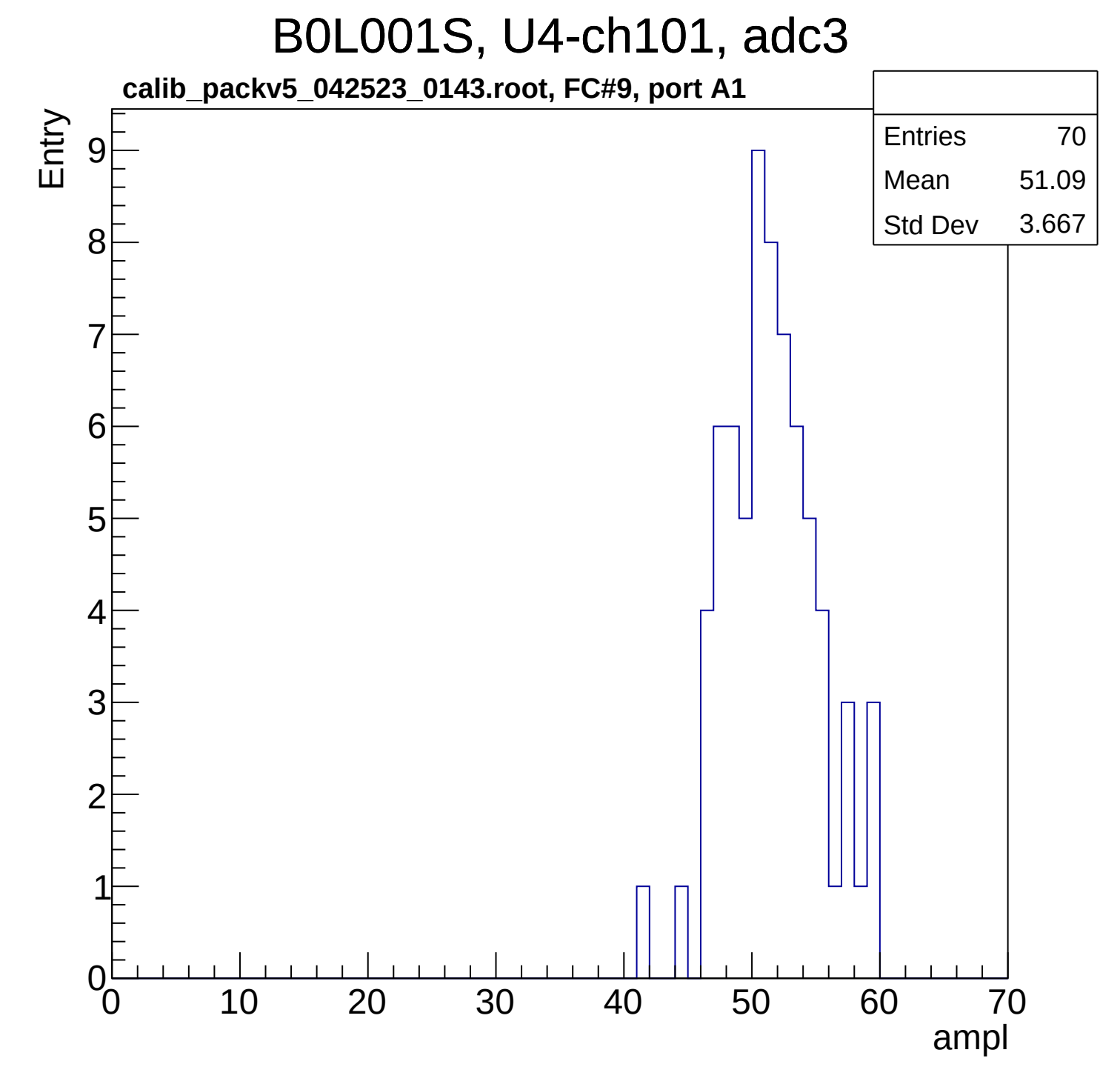

B0L001S, U4-ch100, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 57.65
Std Dev 13.4
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

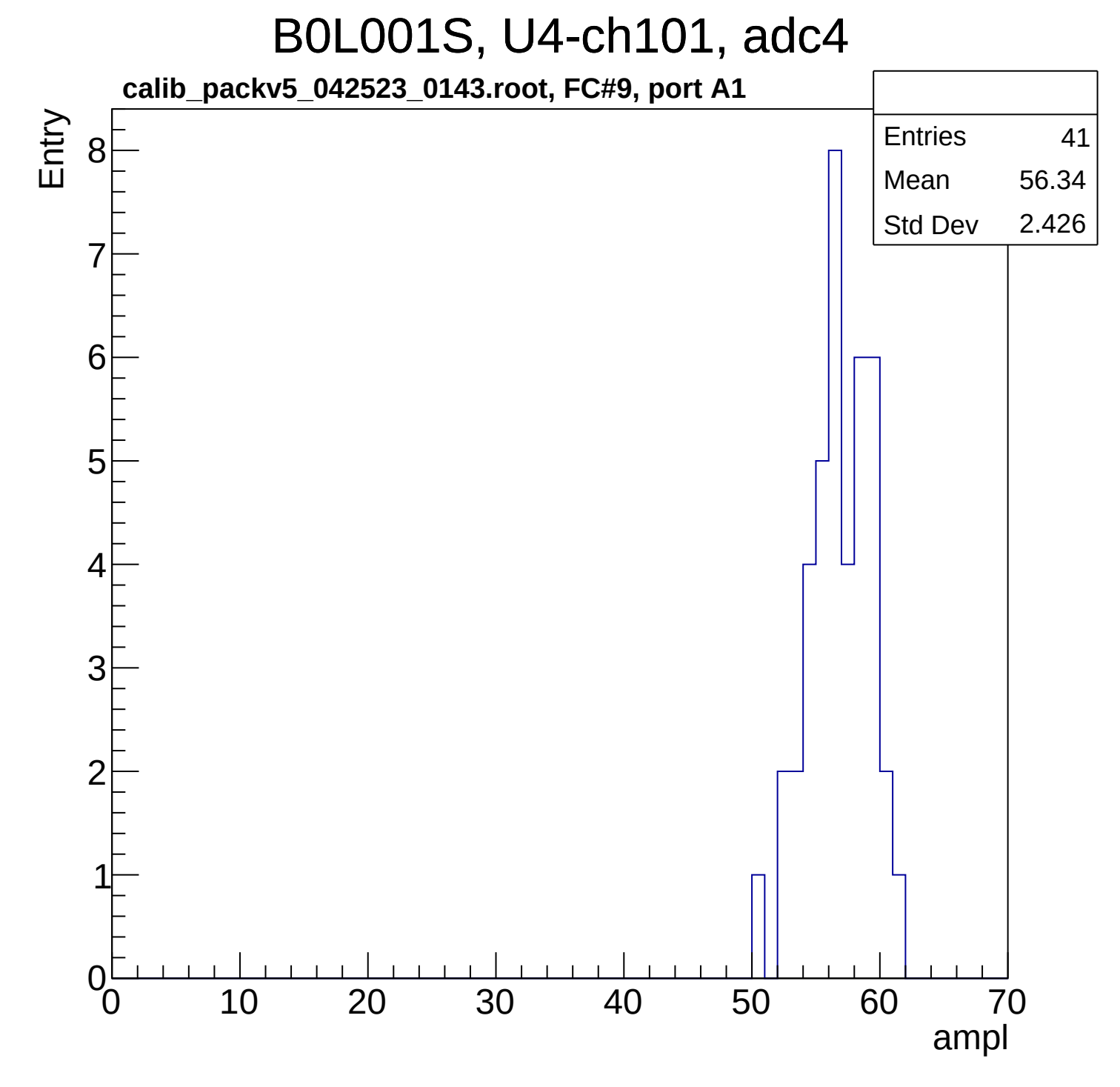

B0L001S, U4-ch100, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 0
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

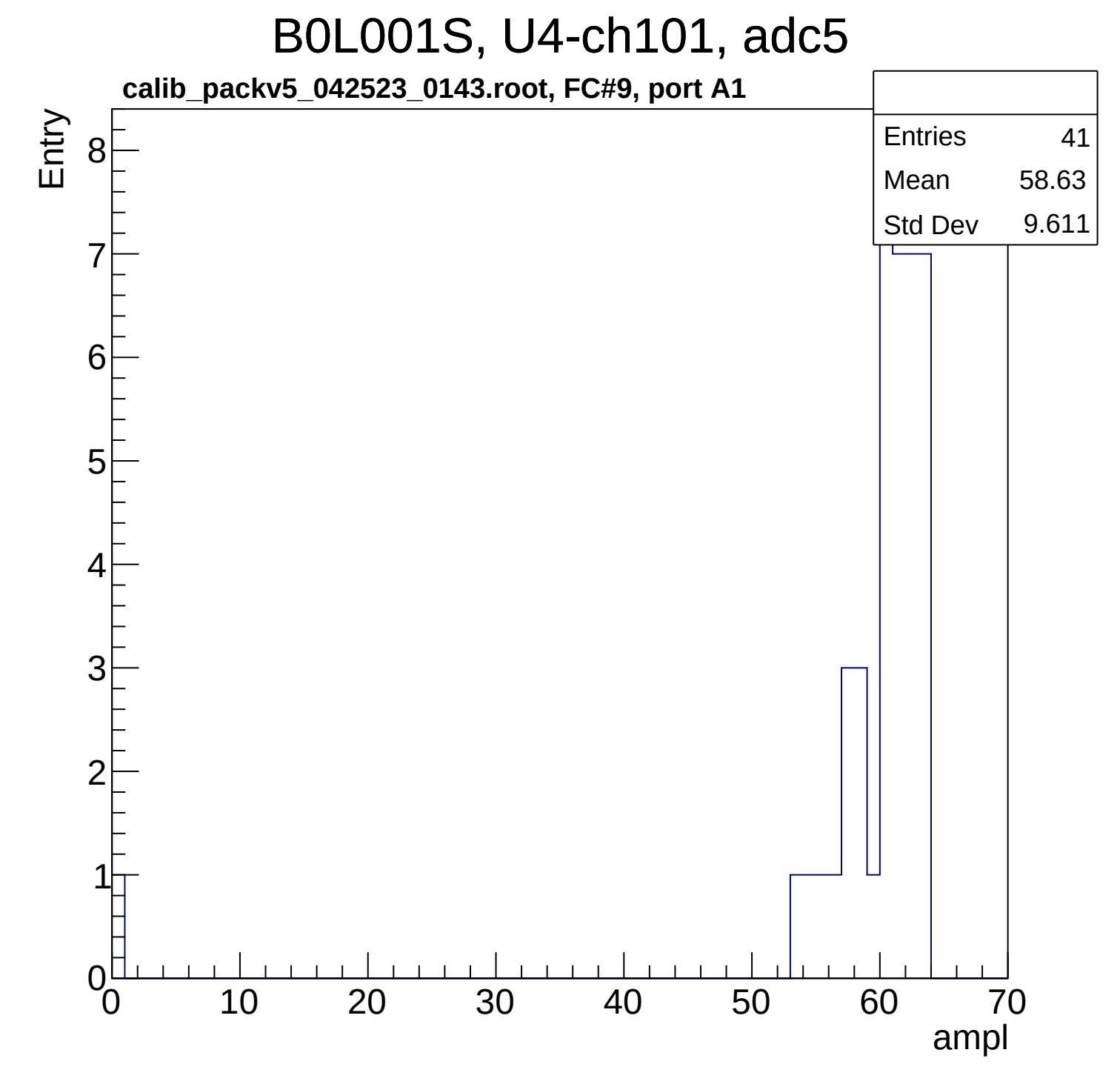

B0L001S, U4-ch101, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 30.16
Std Dev 3.652
Gaus mean : 30.5389
Gaus Width: 4.6697
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch101, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.56
Std Dev 3.634
Gaus mean : 37.8787
Gaus Width: 3.9732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch101, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.29
Std Dev 3.436
Gaus mean : 44.8654
Gaus Width: 3.6436
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch101, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 51.09
Std Dev 3.667
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch101, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 56.34
Std Dev 2.426
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch101, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.63
Std Dev 9.611
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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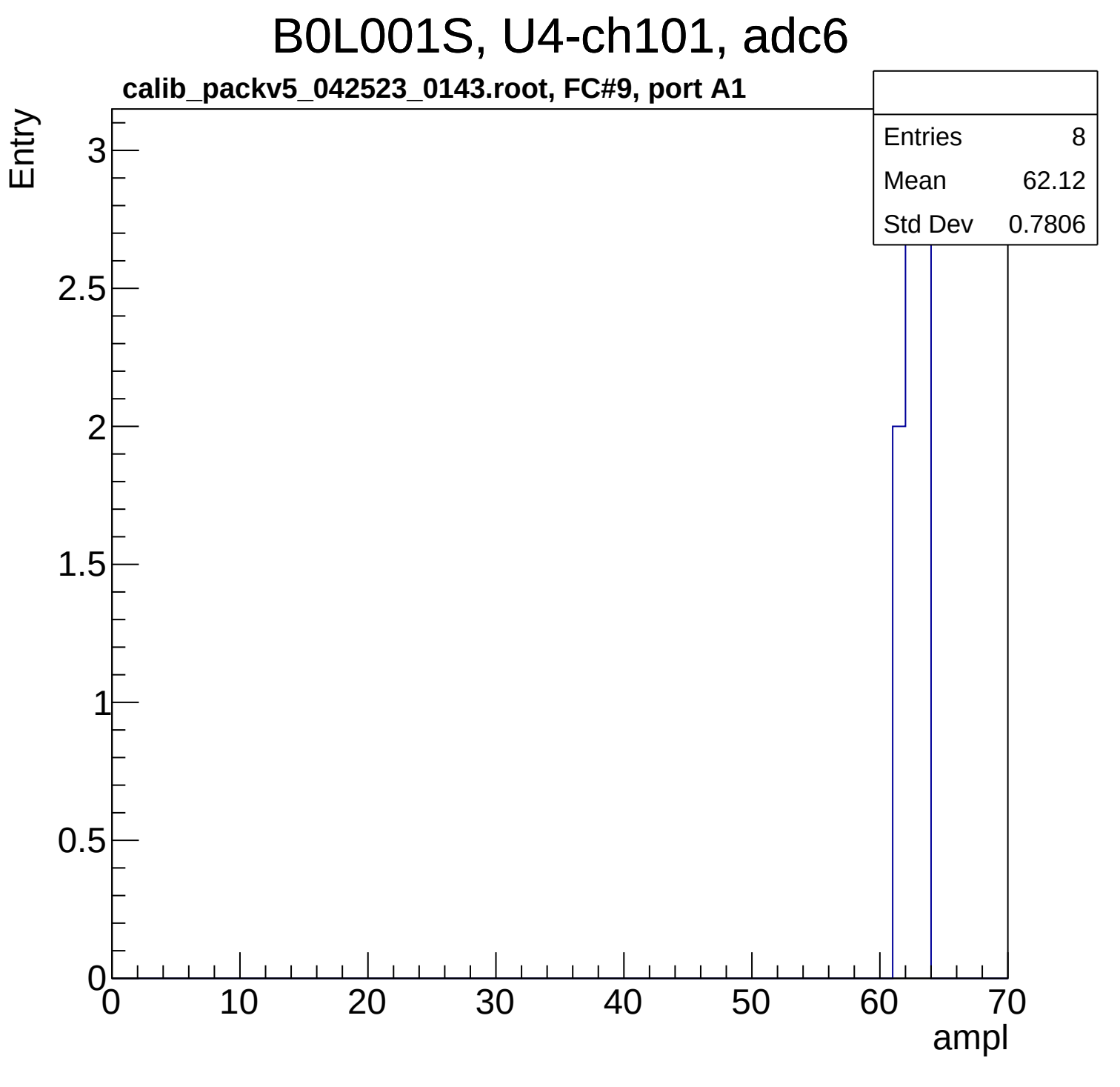
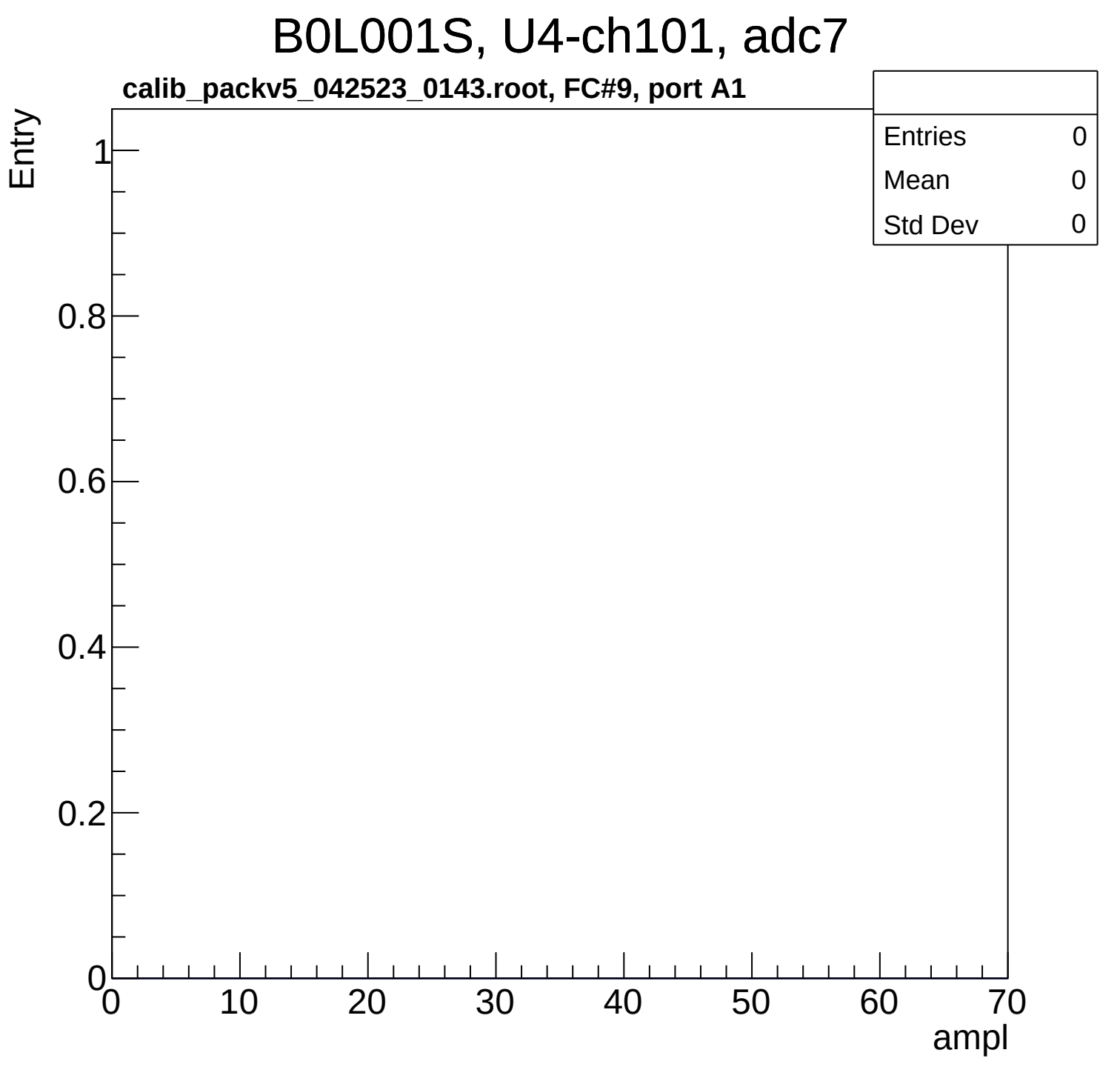
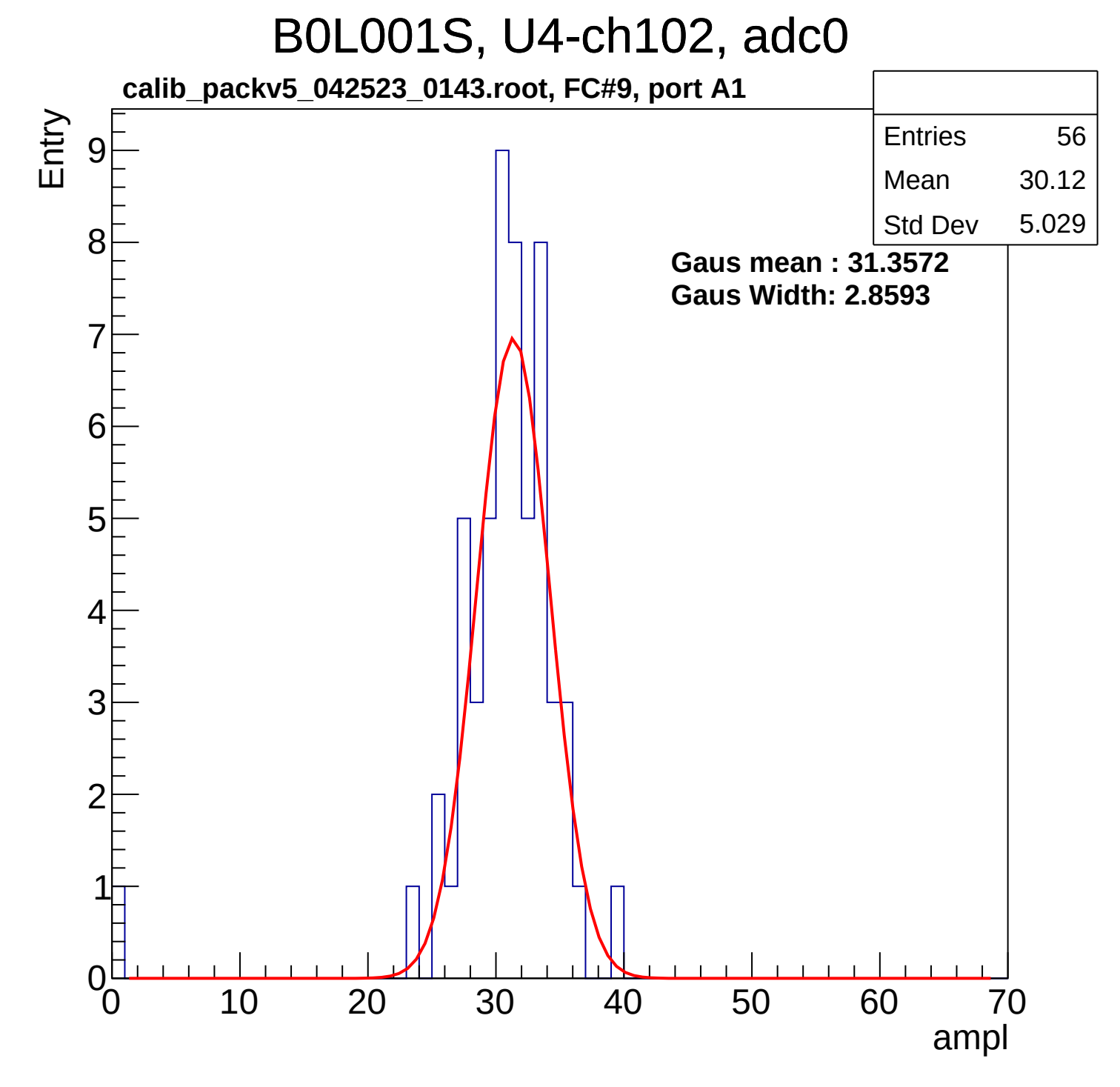
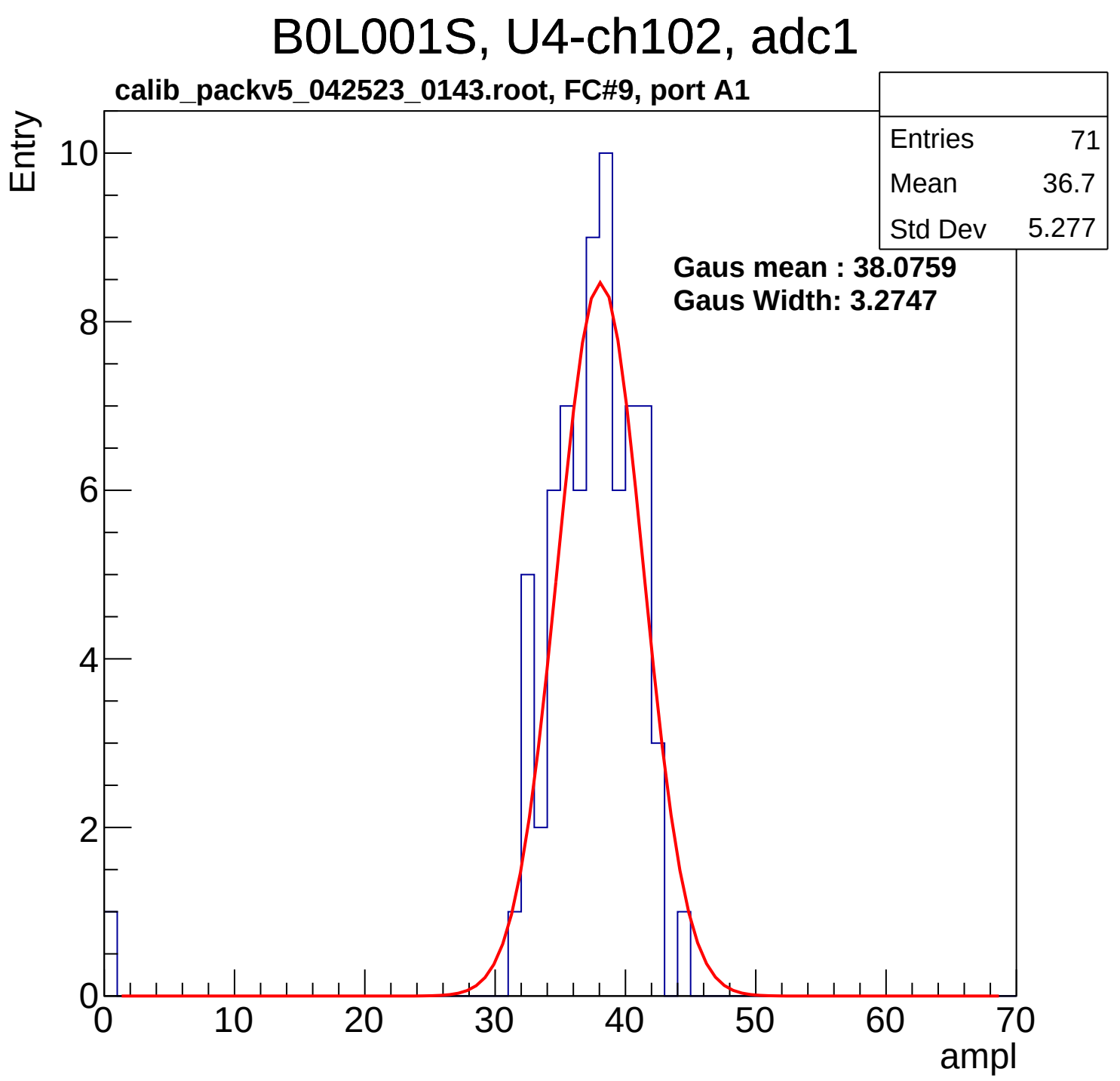
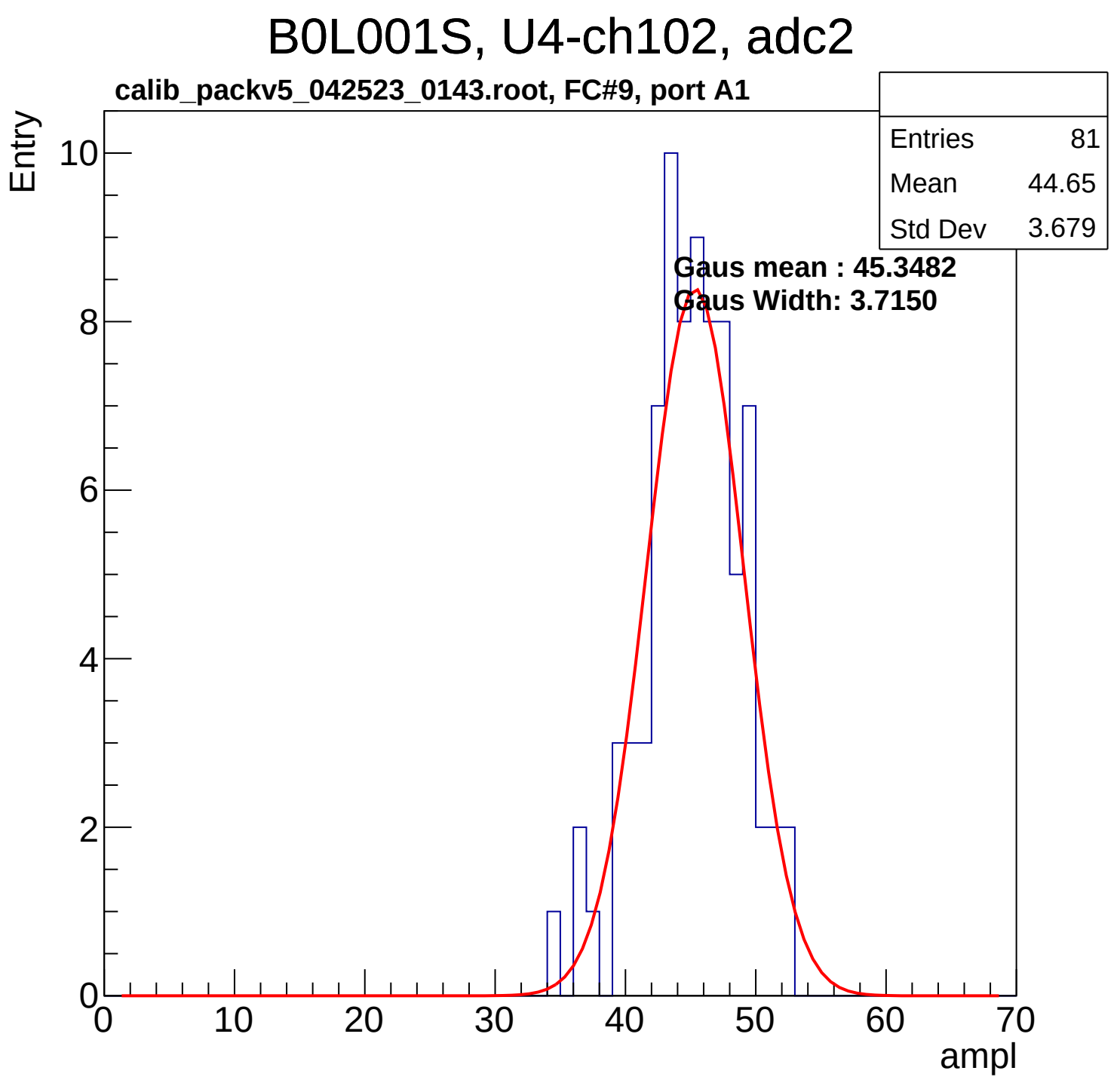
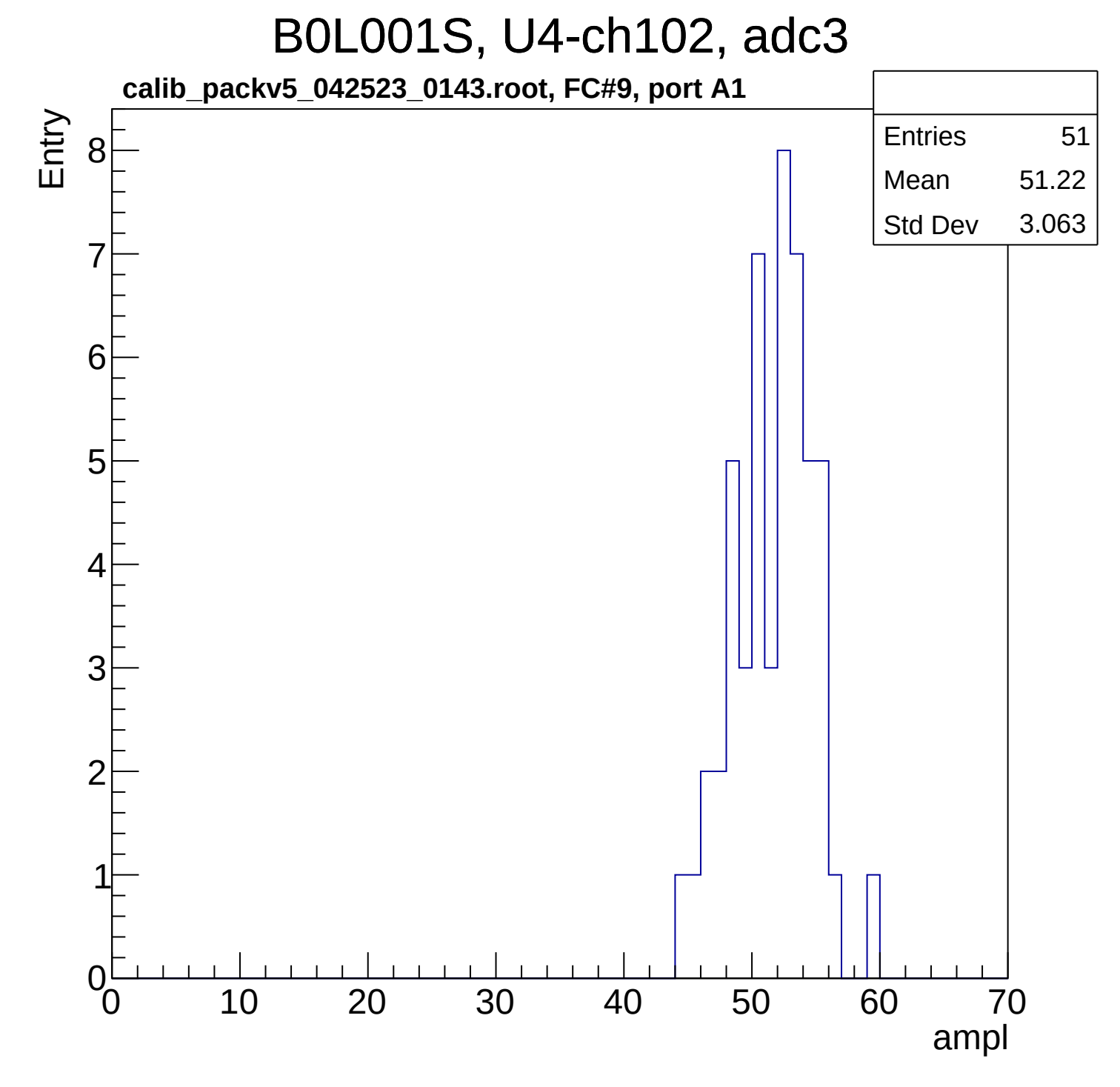

B0L001S, U4-ch101, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62.12
Std Dev 0.7806
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

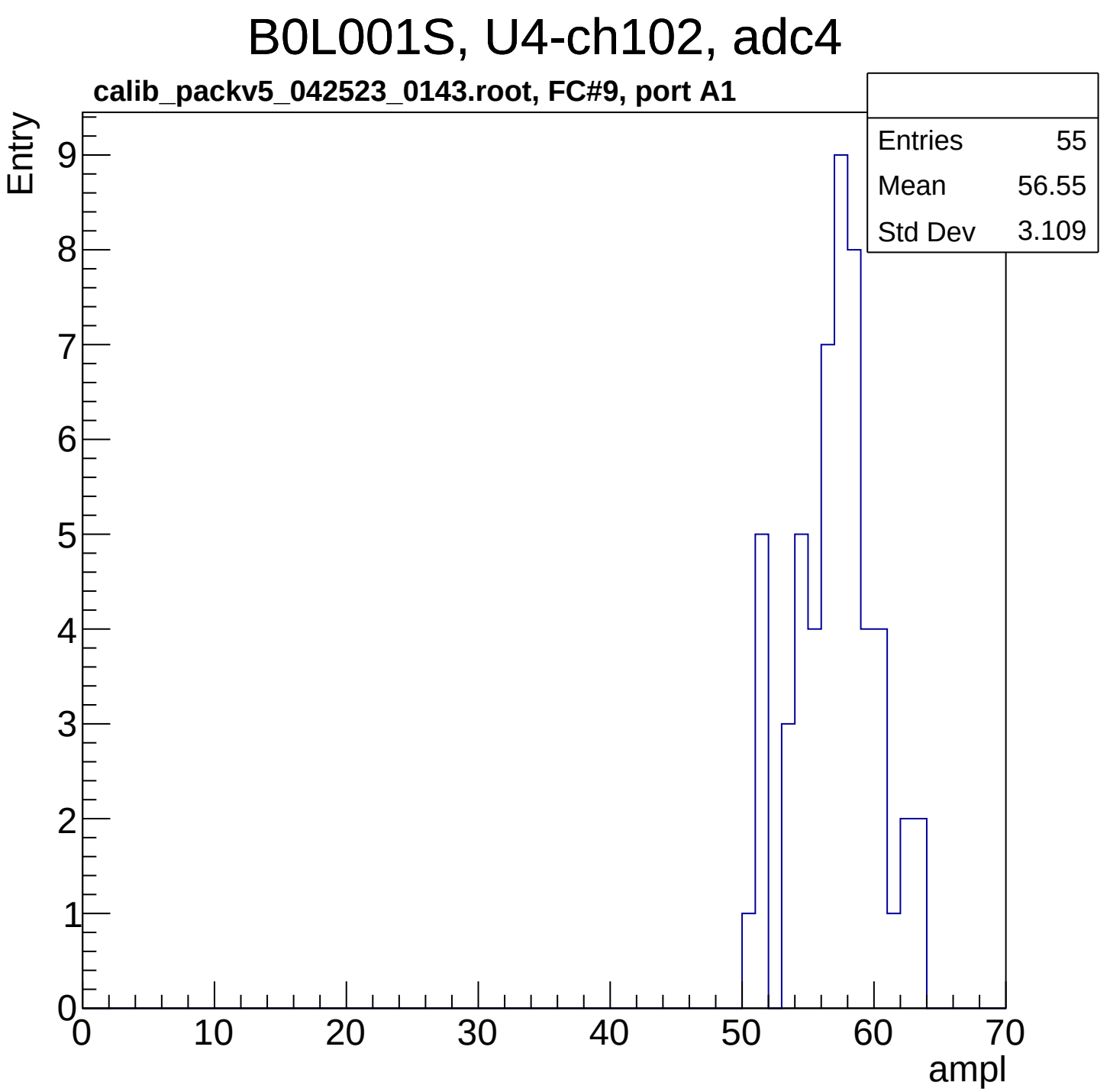

B0L001S, U4-ch101, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch102, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.12
Std Dev 5.029
Gaus mean : 31.3572
Gaus Width: 2.8593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch102, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 36.7
Std Dev 5.277
Gaus mean : 38.0759
Gaus Width: 3.2747
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch102, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 44.65
Std Dev 3.679
Gaus mean : 45.3482
Gaus Width: 3.7150
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch102, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 51.22
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch102, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.55
Std Dev 3.109
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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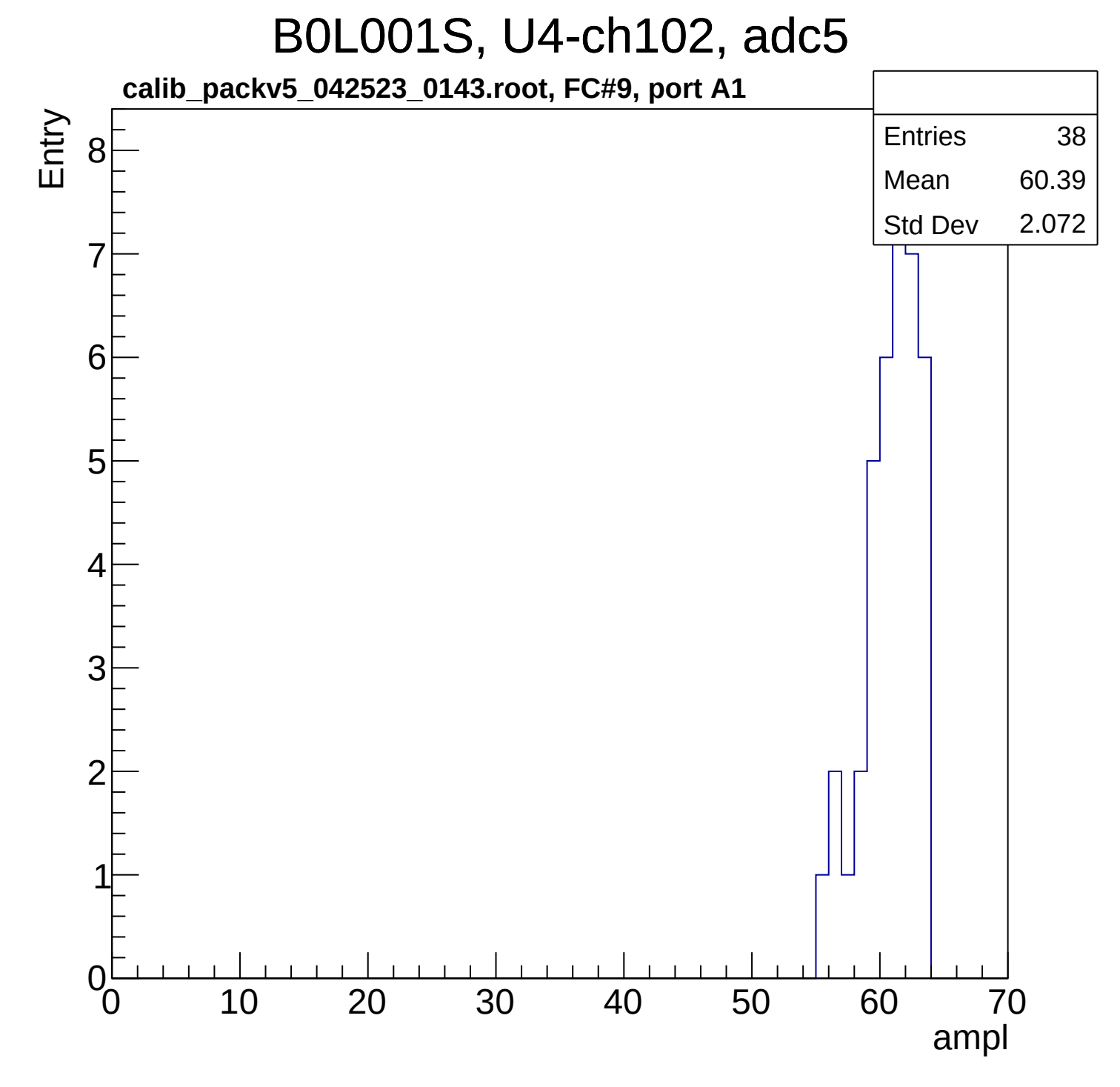
B0L001S, U4-ch102, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.39
Std Dev 2.072
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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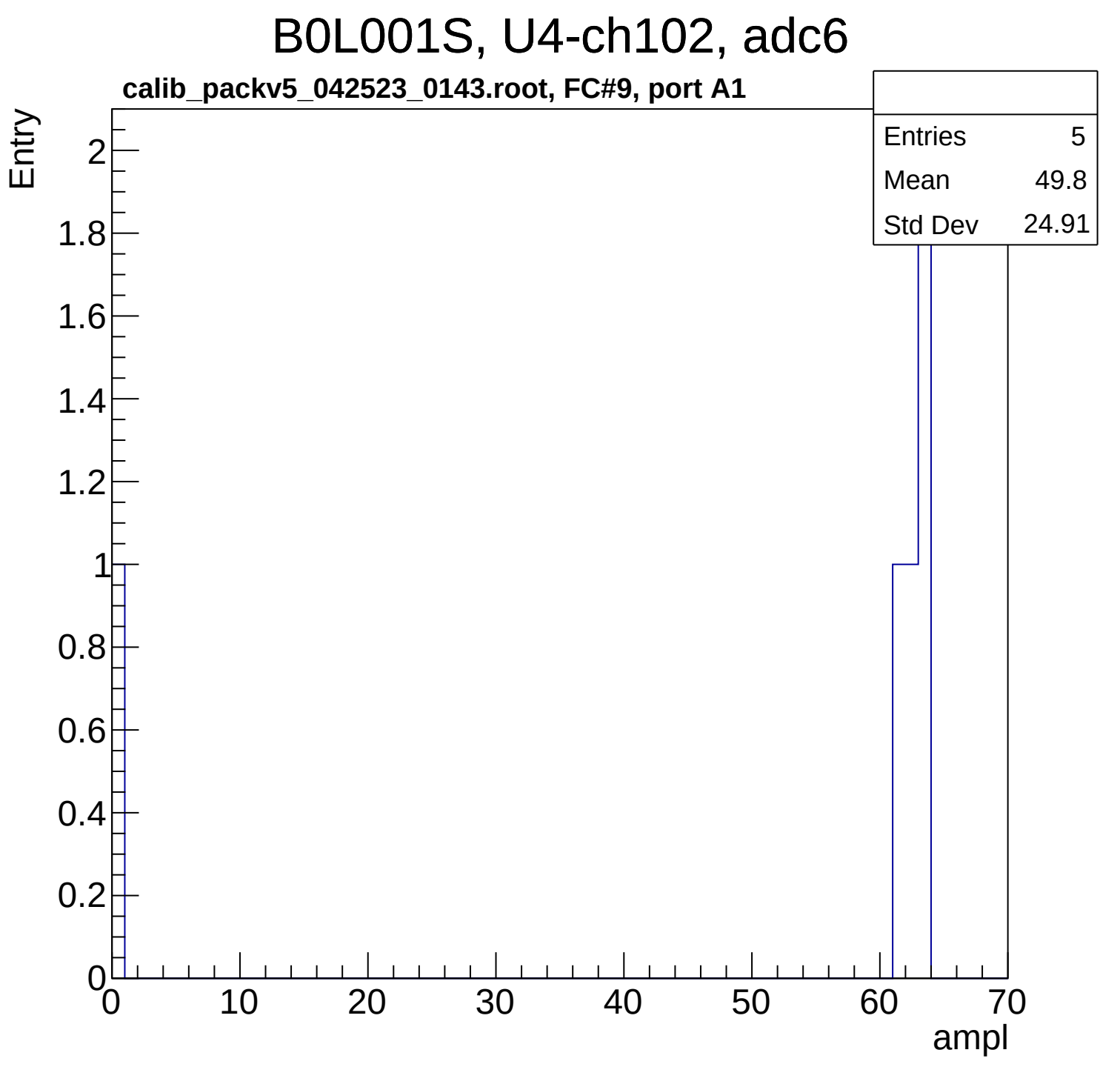
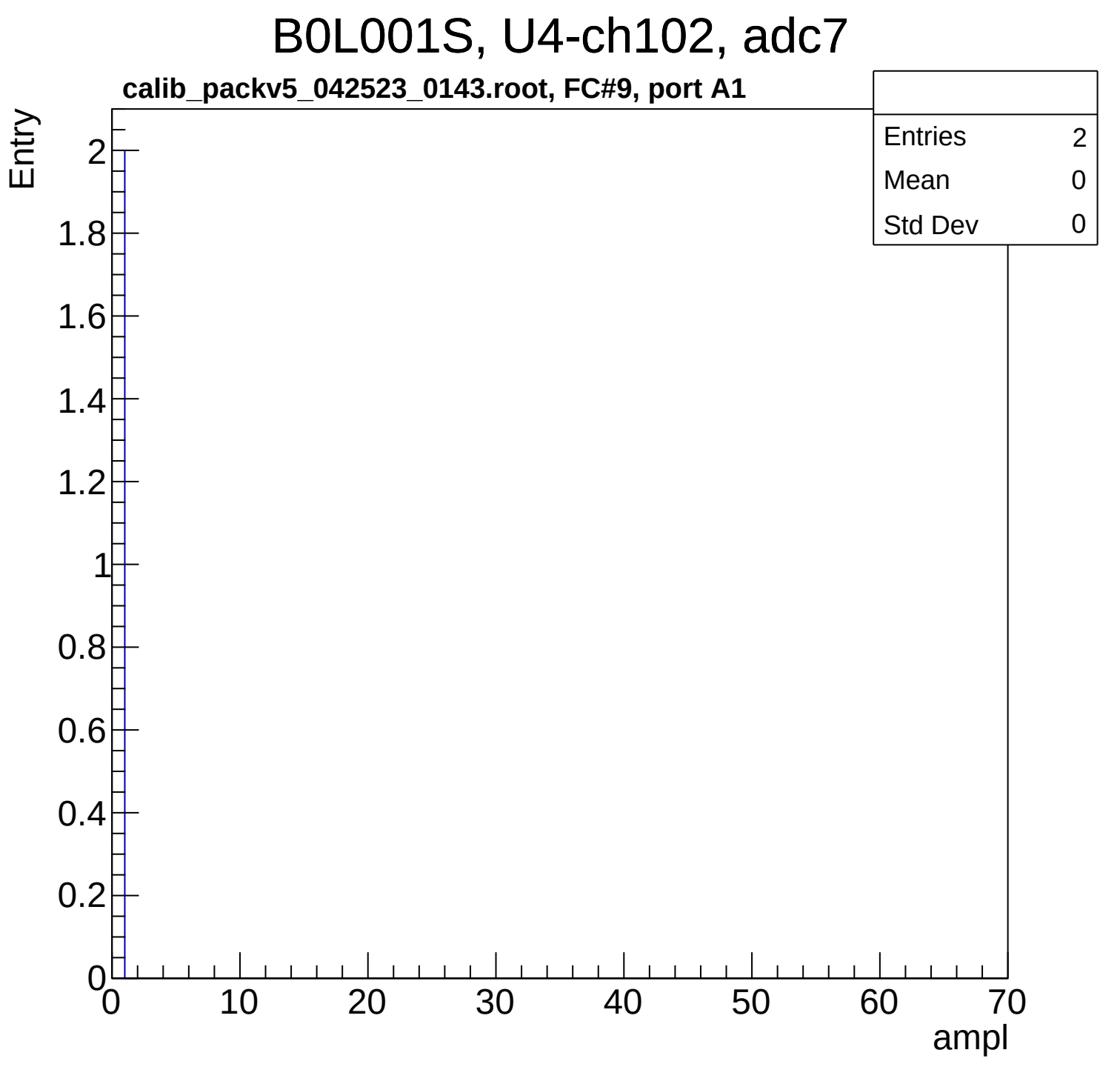
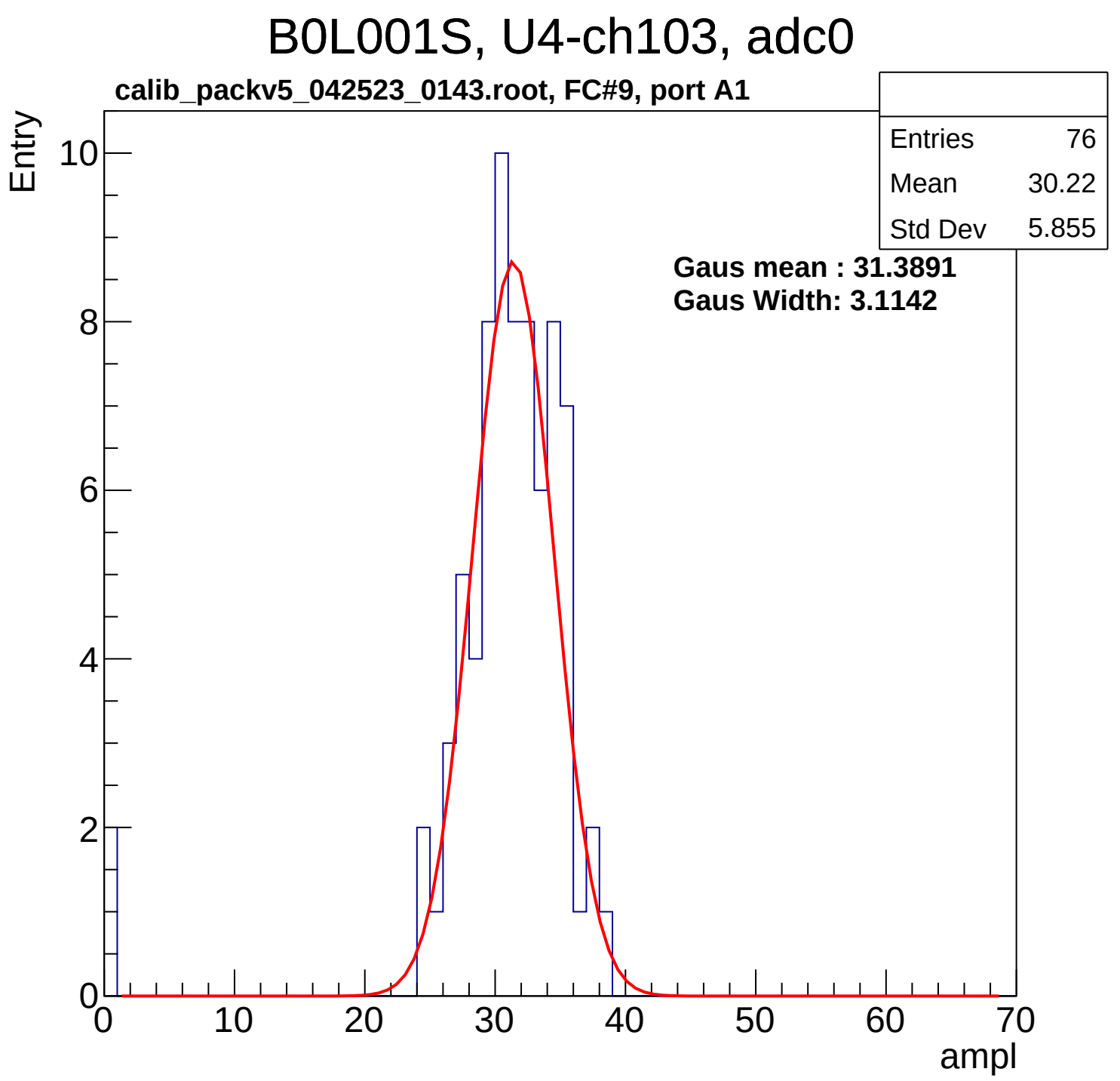
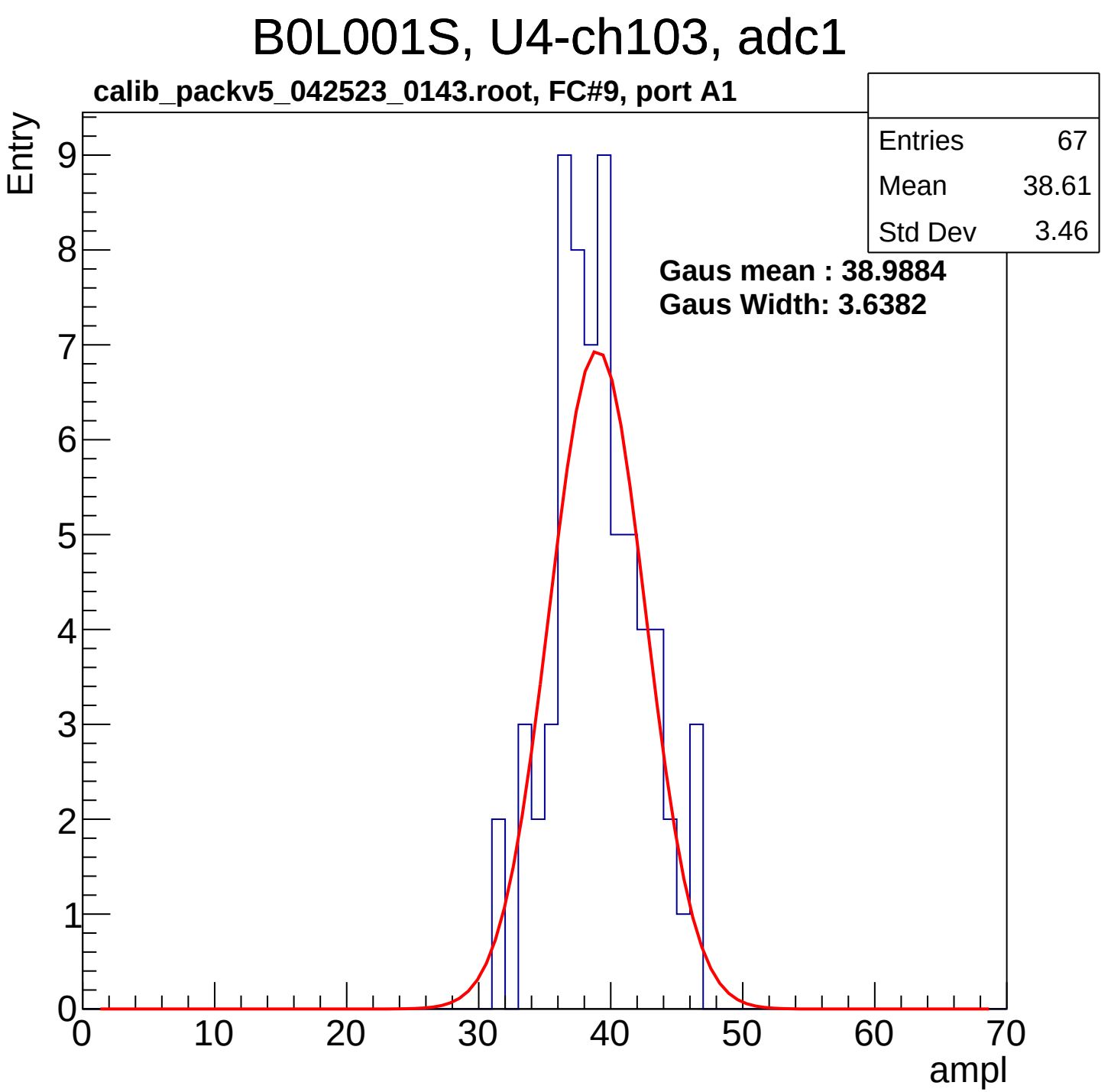
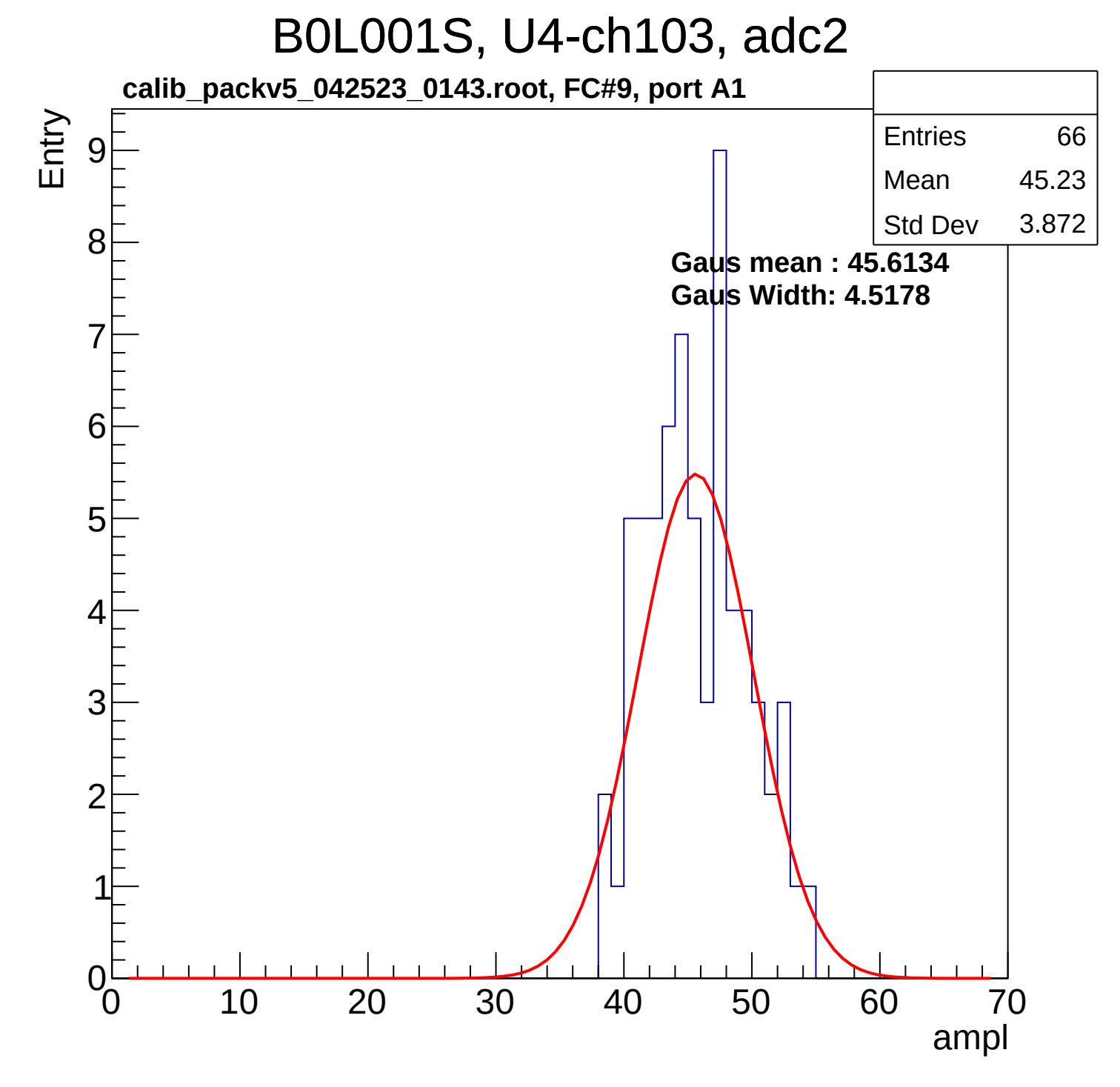

B0L001S, U4-ch102, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch102, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch103, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 30.22
Std Dev 5.855
Gaus mean : 31.3891
Gaus Width: 3.1142
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch103, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.61
Std Dev 3.46
Gaus mean : 38.9884
Gaus Width: 3.6382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch103, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 45.23
Std Dev 3.872
Gaus mean : 45.6134
Gaus Width: 4.5178
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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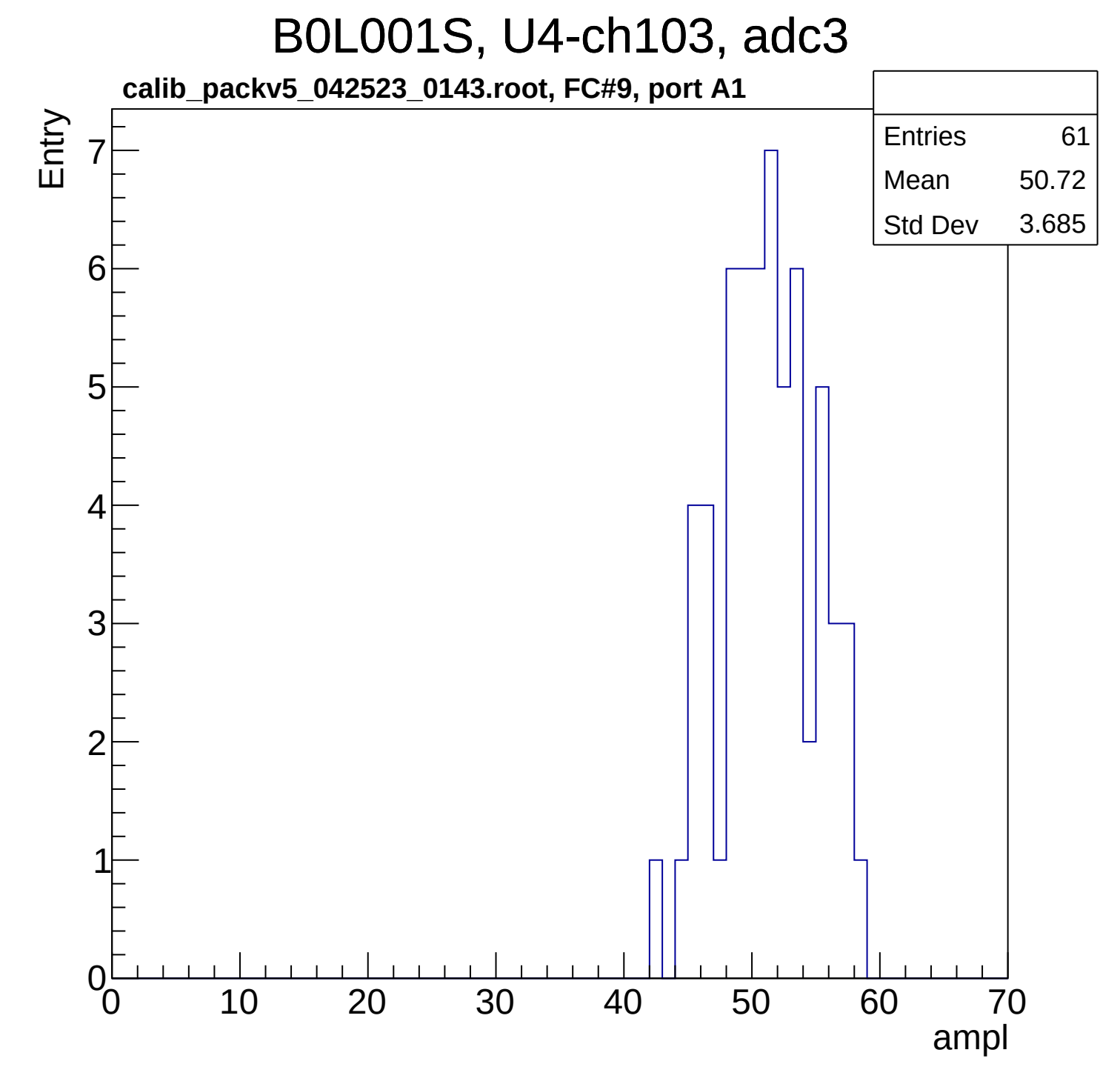
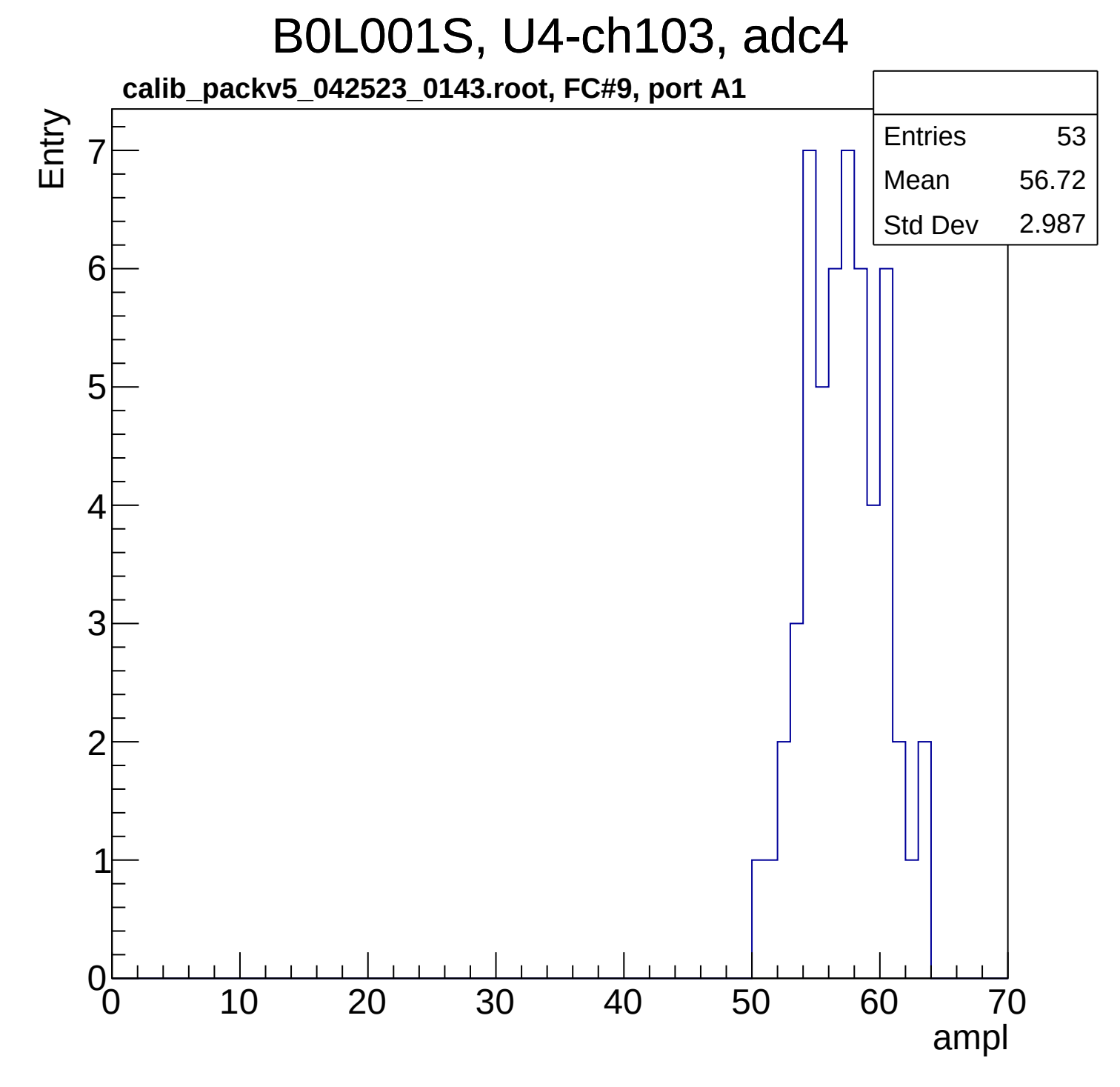
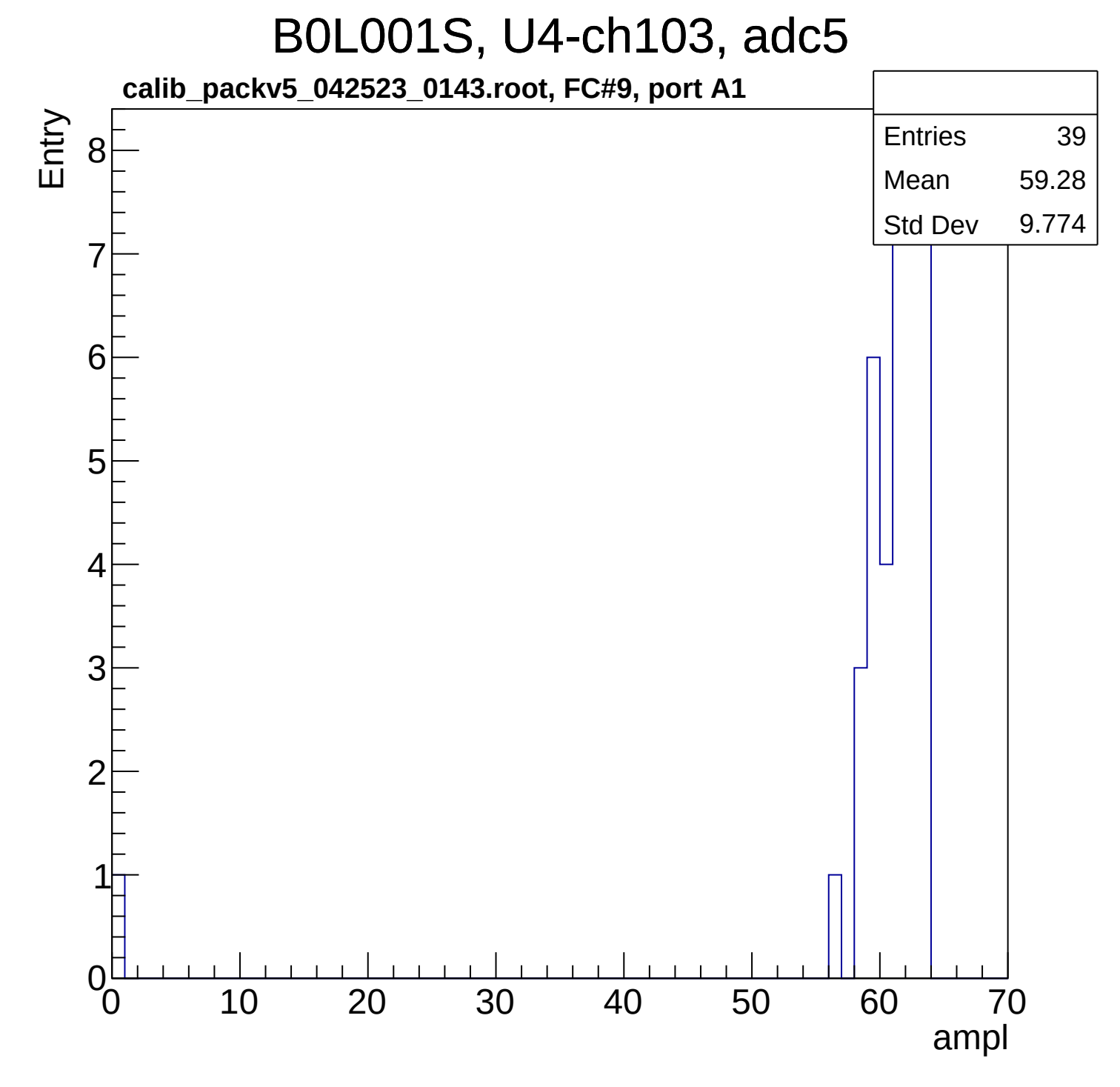

B0L001S, U4-ch103, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 50.72
Std Dev 3.685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch103, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.72
Std Dev 2.987
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch103, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59.28
Std Dev 9.774
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
20
30
40
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60
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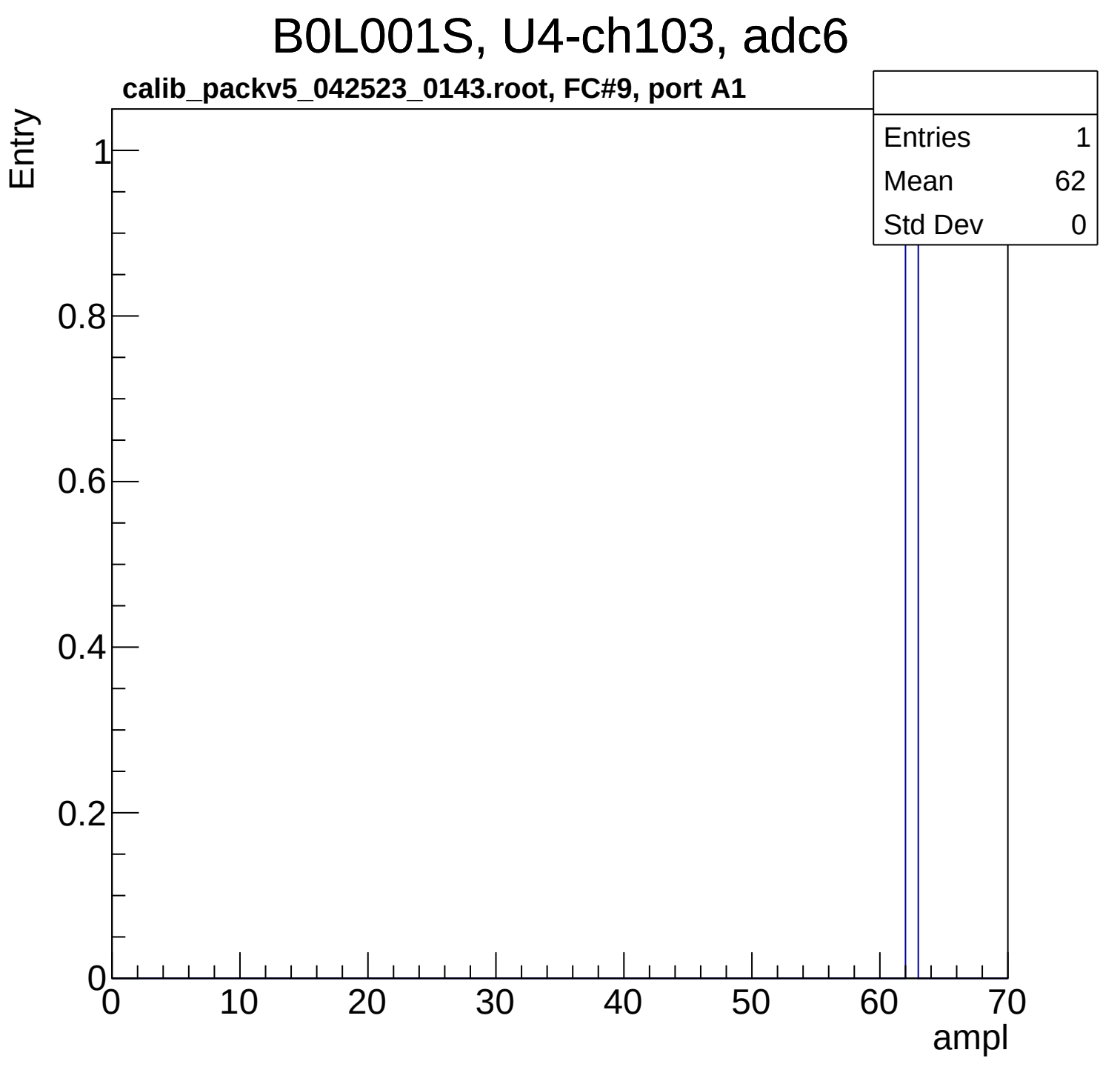
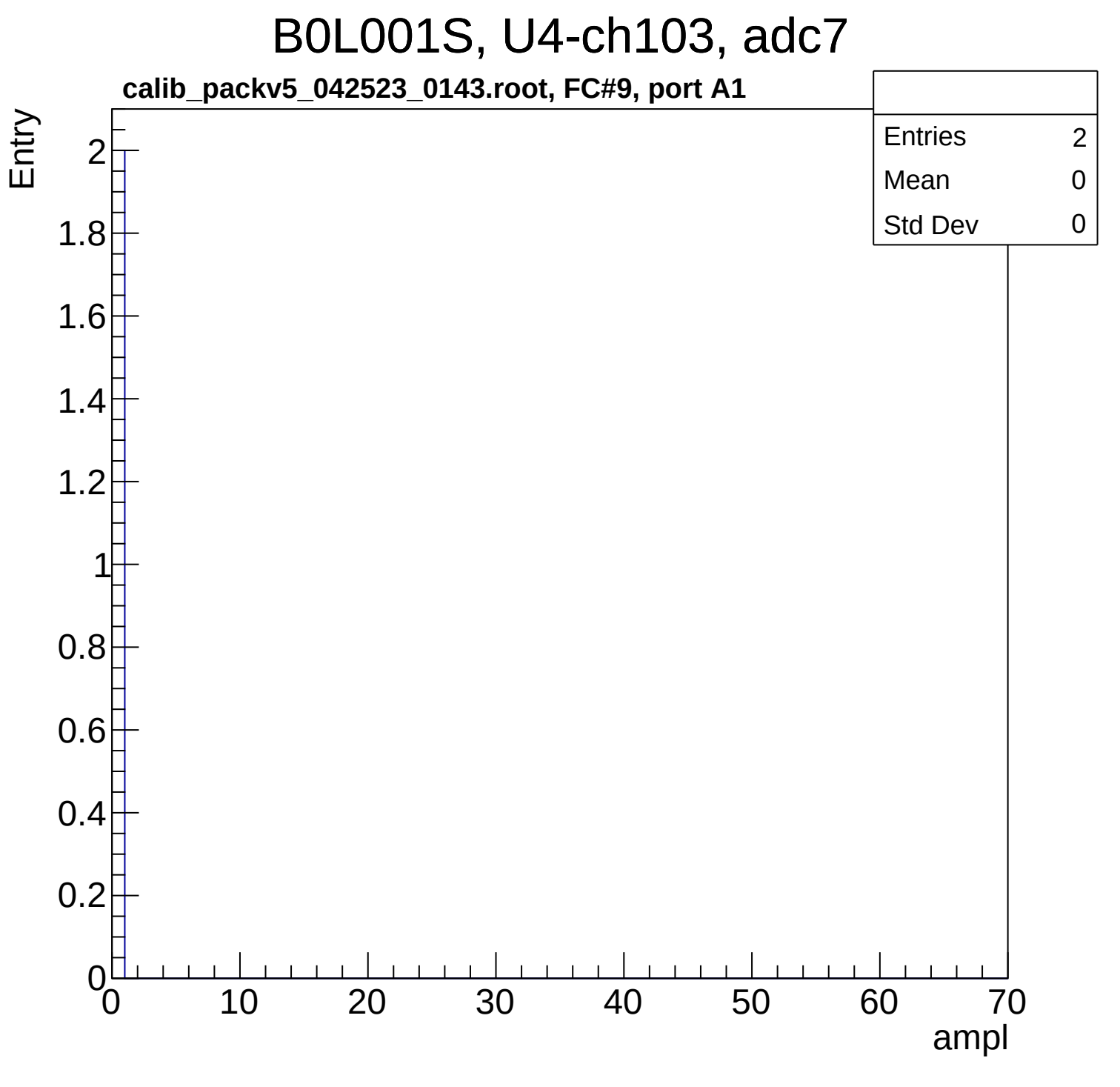
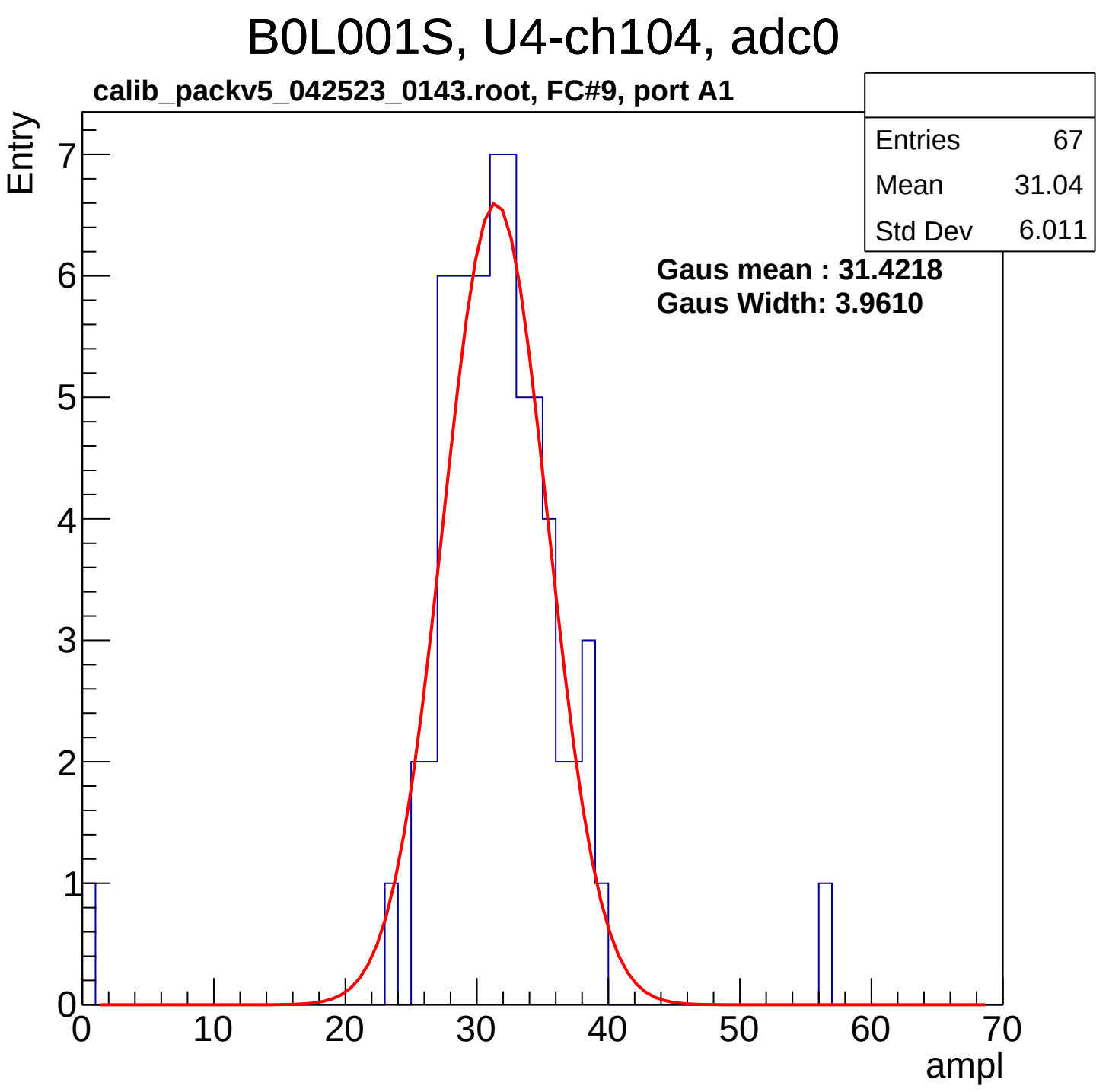
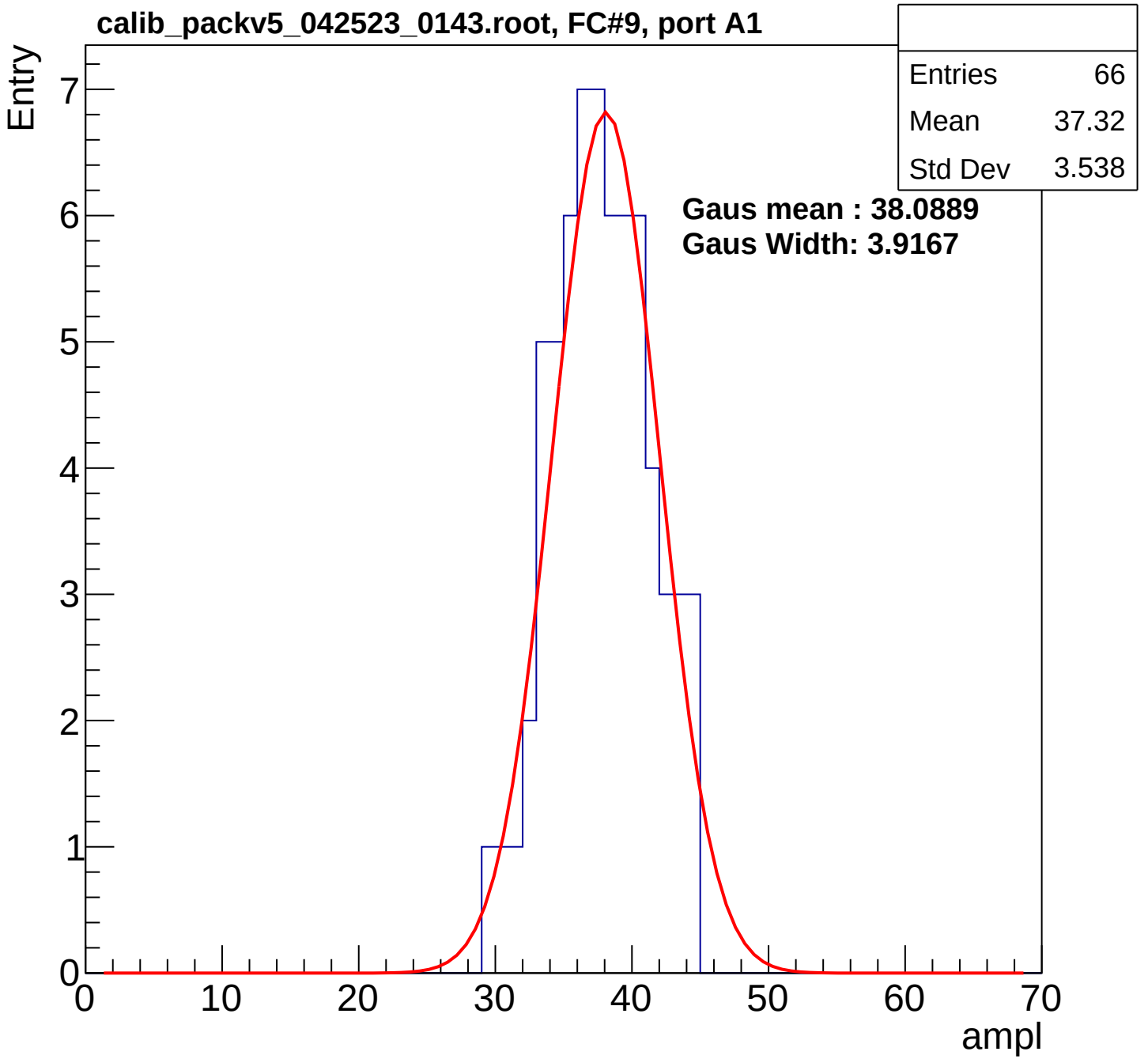

B0L001S, U4-ch103, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch103, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch104, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.04
Std Dev 6.011
Gaus mean : 31.4218
Gaus Width: 3.9610
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch104, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.32
Std Dev 3.538
Gaus mean : 38.0889
Gaus Width: 3.9167
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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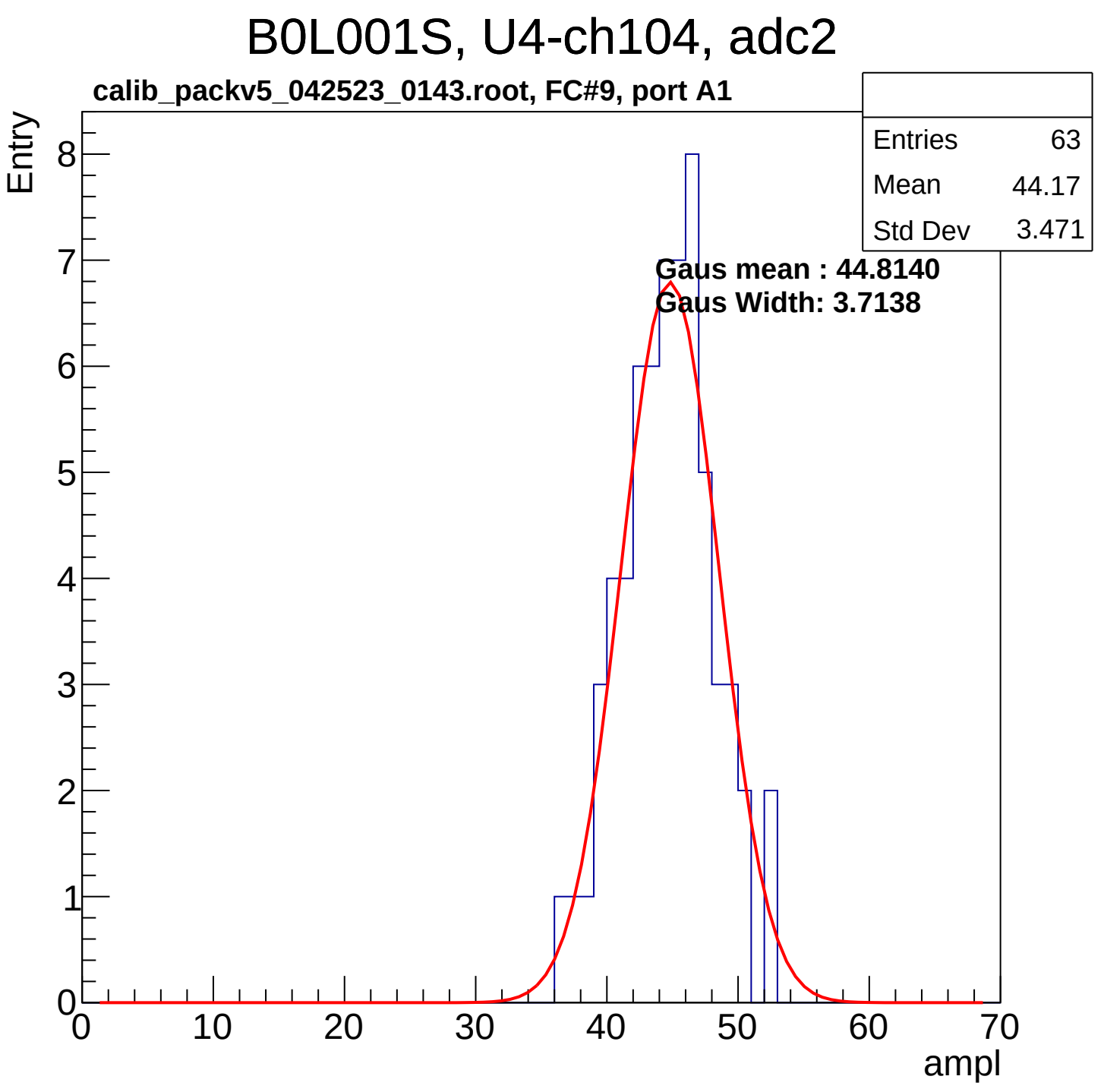
B0L001S, U4-ch104, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.17
Std Dev 3.471
Gaus mean : 44.8140
Gaus Width: 3.7138
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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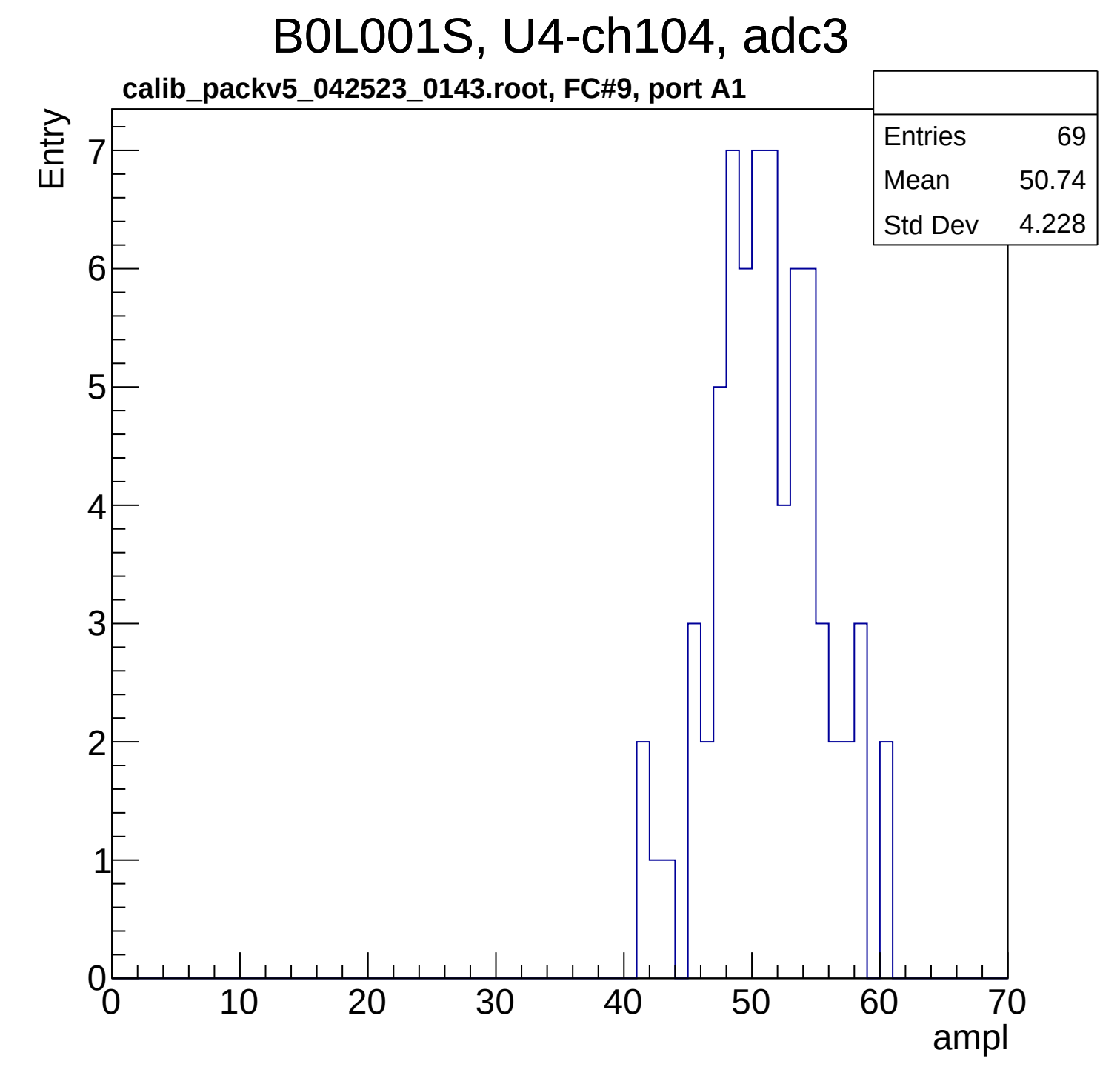
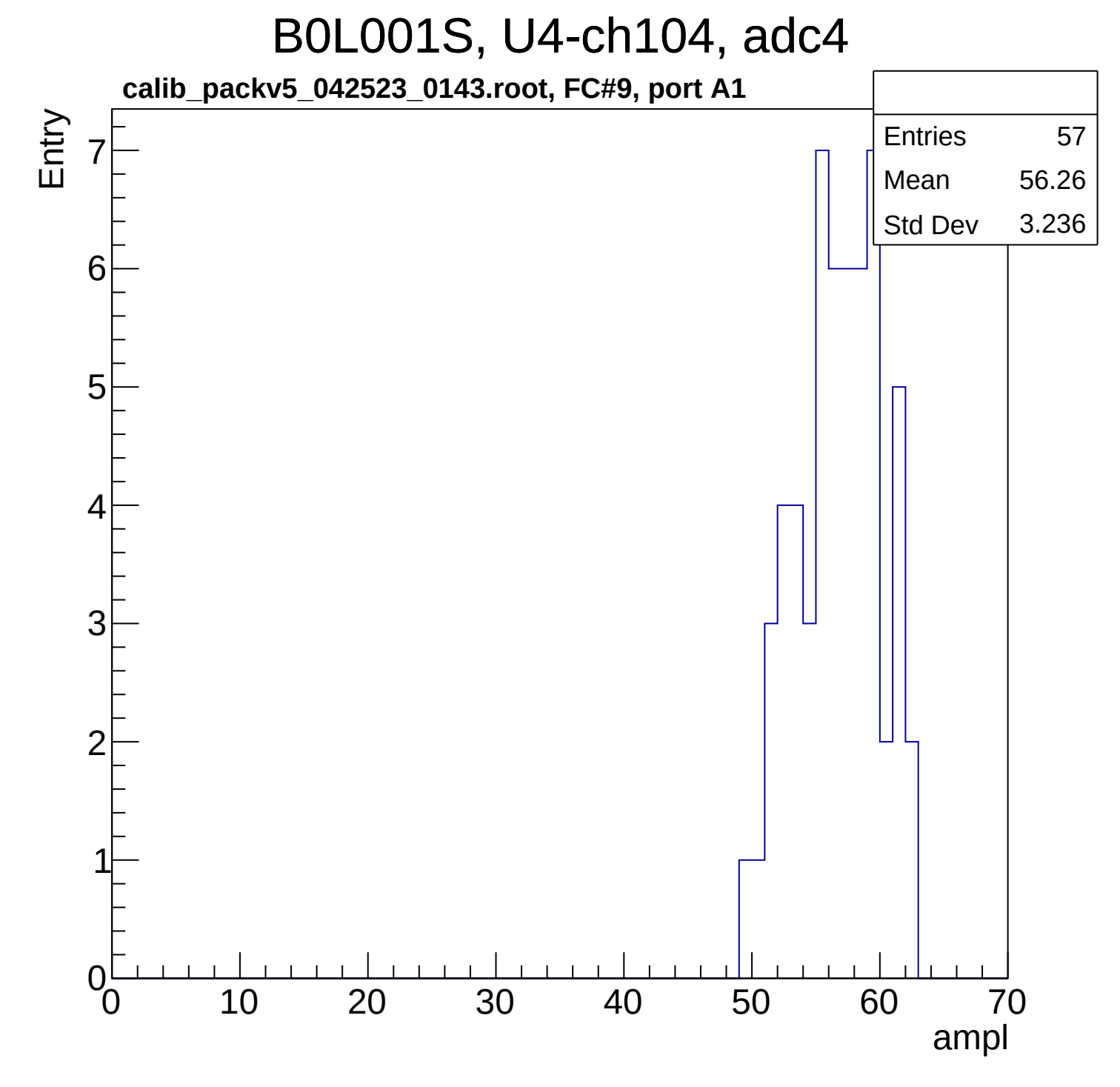
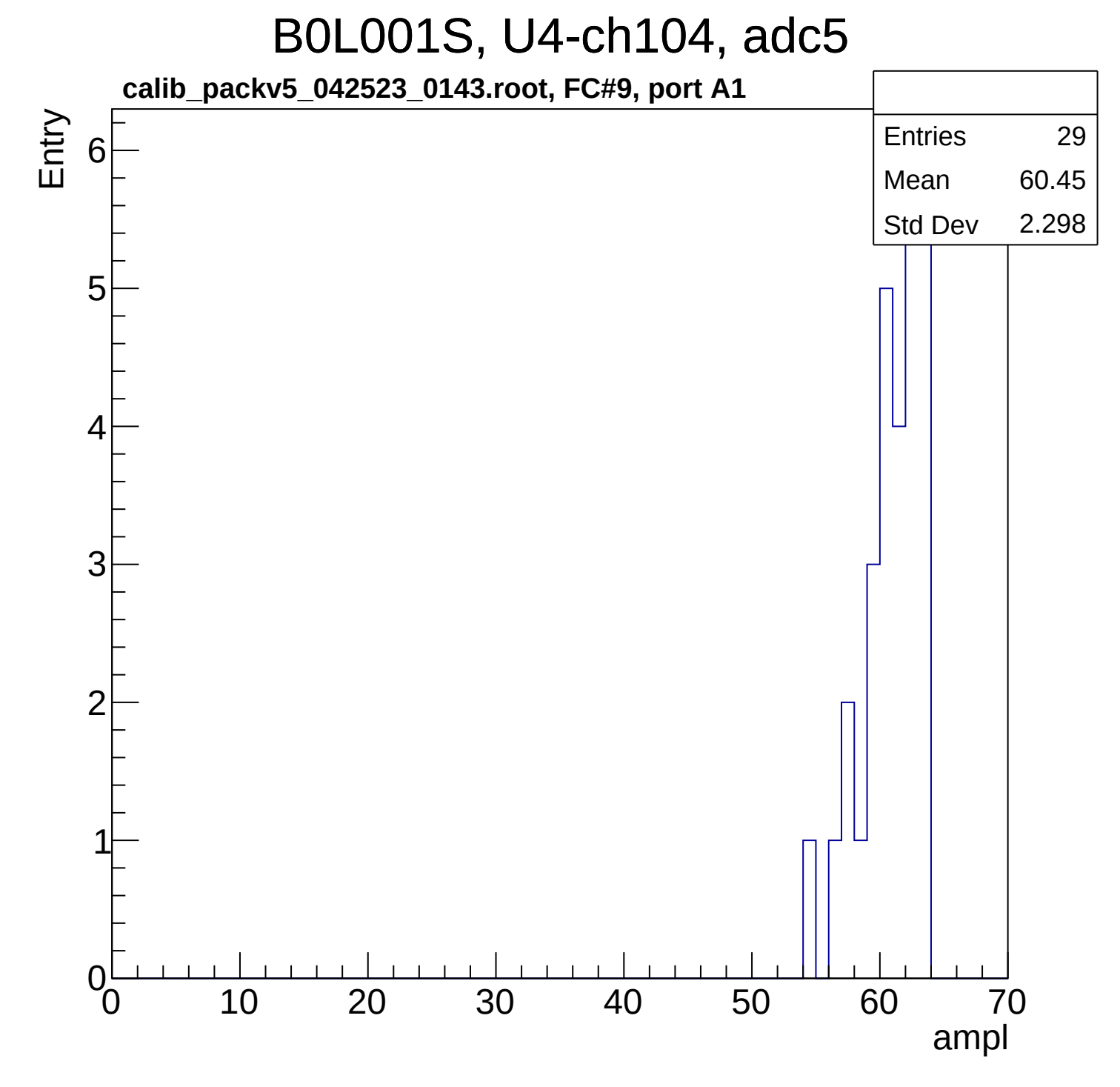
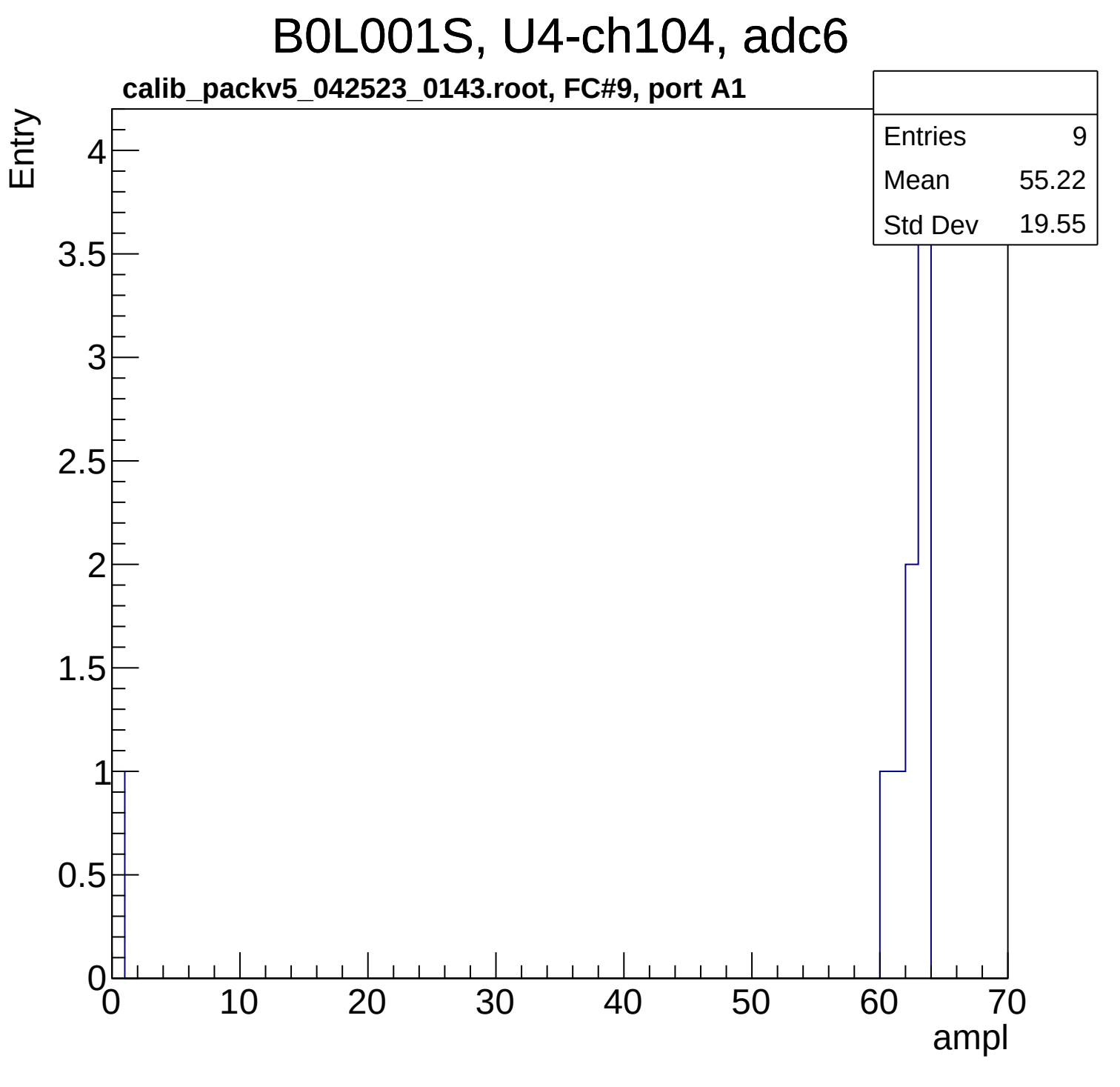

B0L001S, U4-ch104, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.74
Std Dev 4.228
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch104, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.26
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch104, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 60.45
Std Dev 2.298
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch104, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
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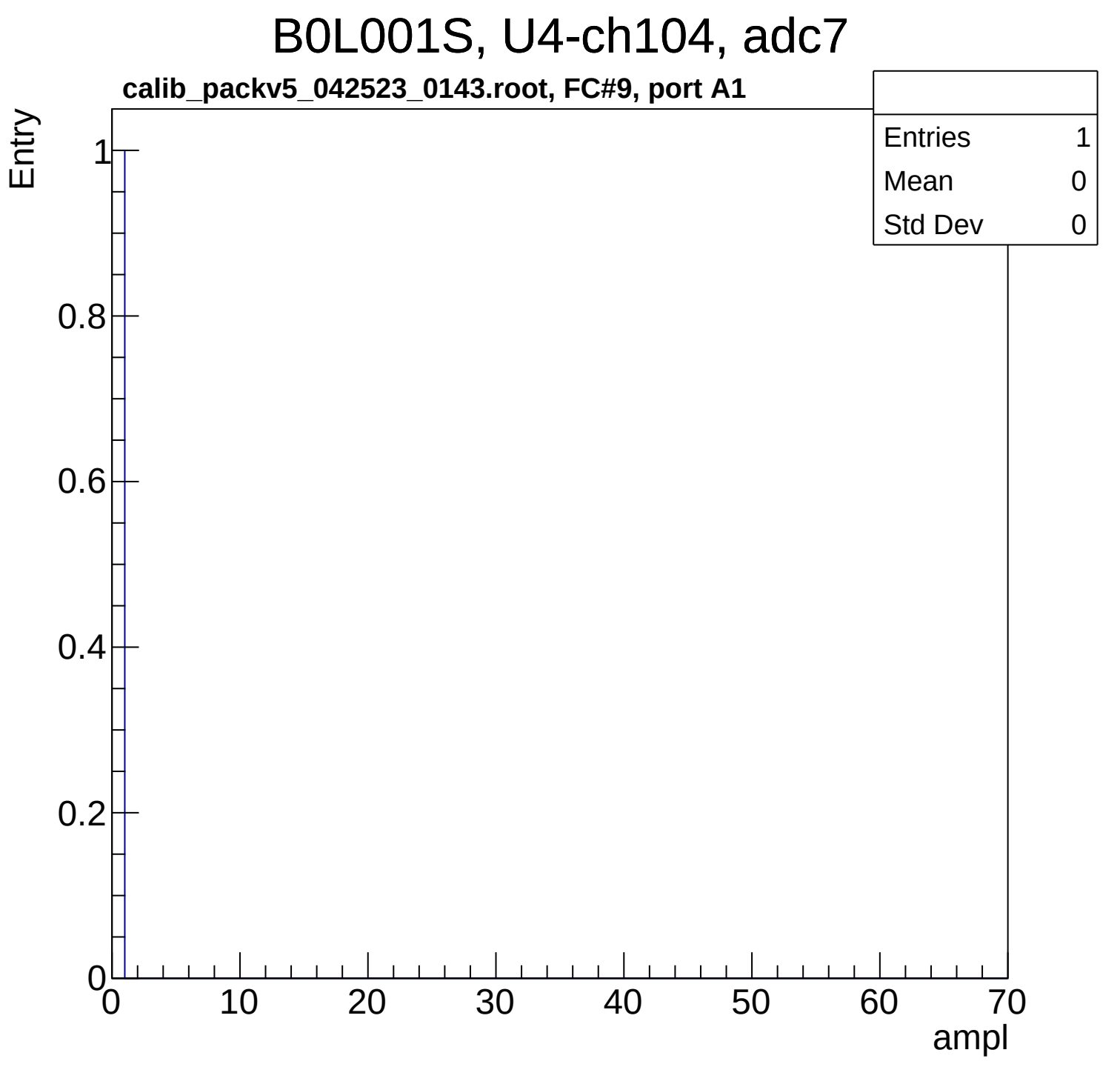
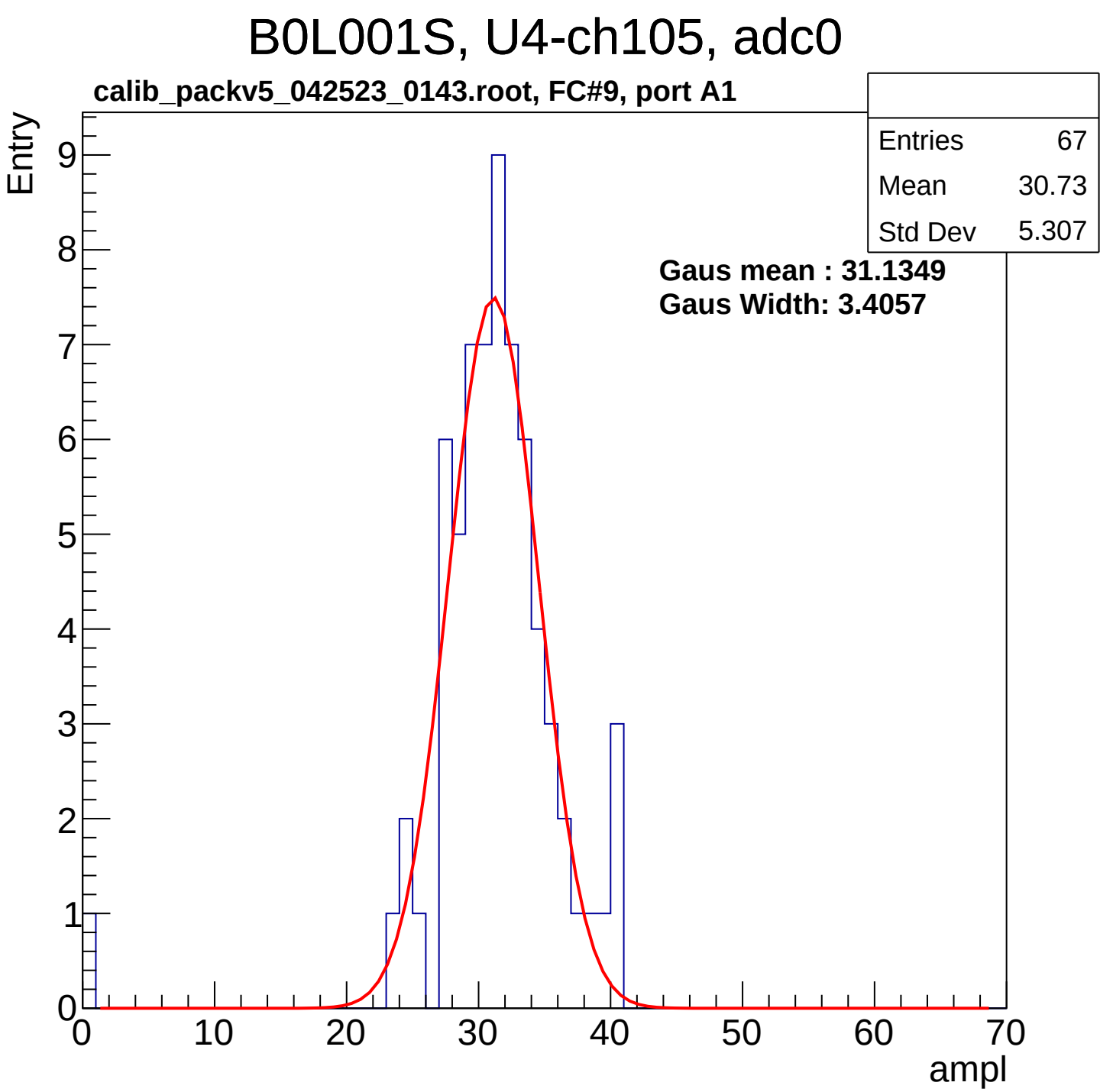
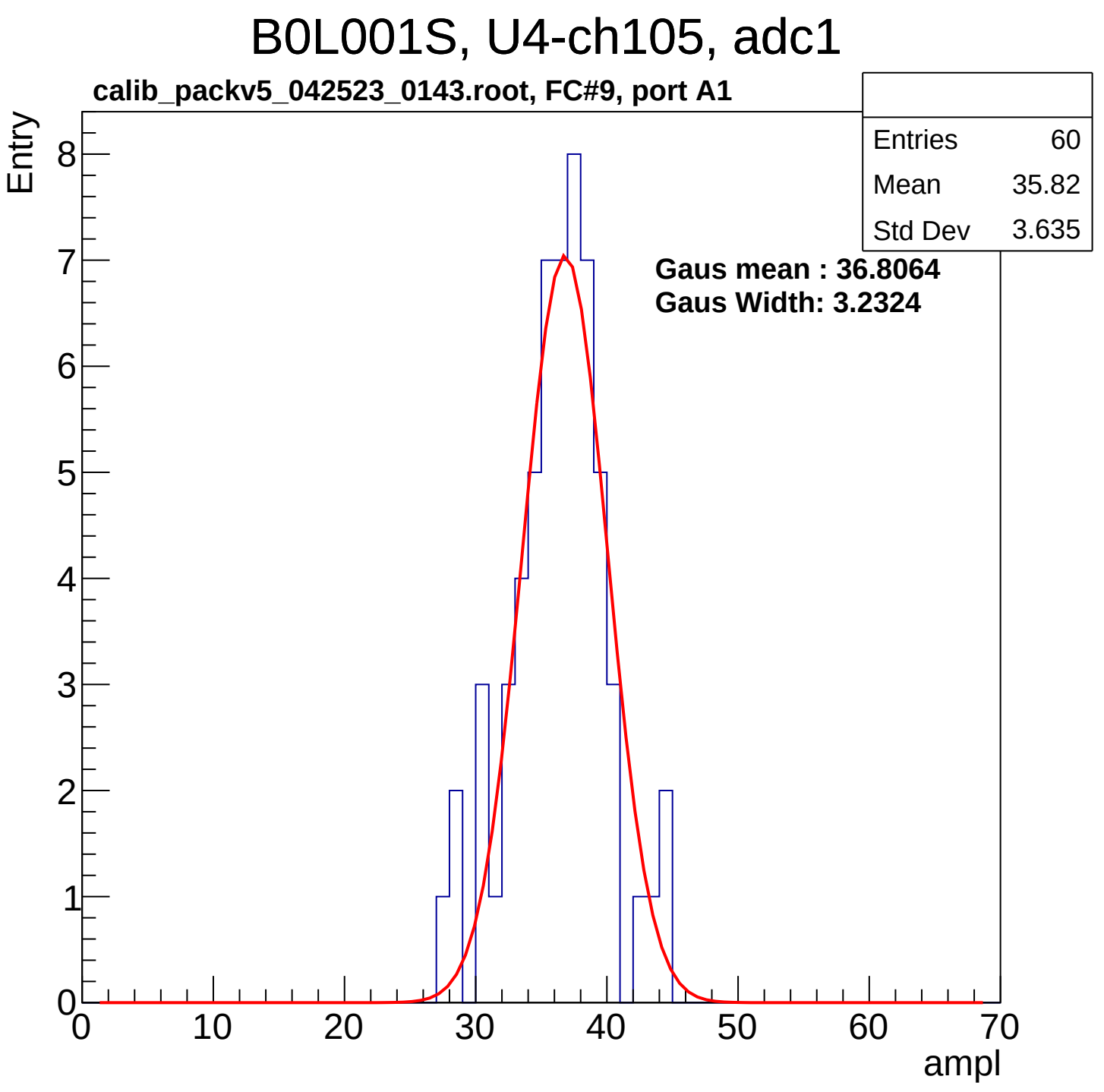
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch104, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch105, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.73
Std Dev 5.307
Gaus mean : 31.1349
Gaus Width: 3.4057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch105, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 35.82
Std Dev 3.635
Gaus mean : 36.8064
Gaus Width: 3.2324
Entry
ampl
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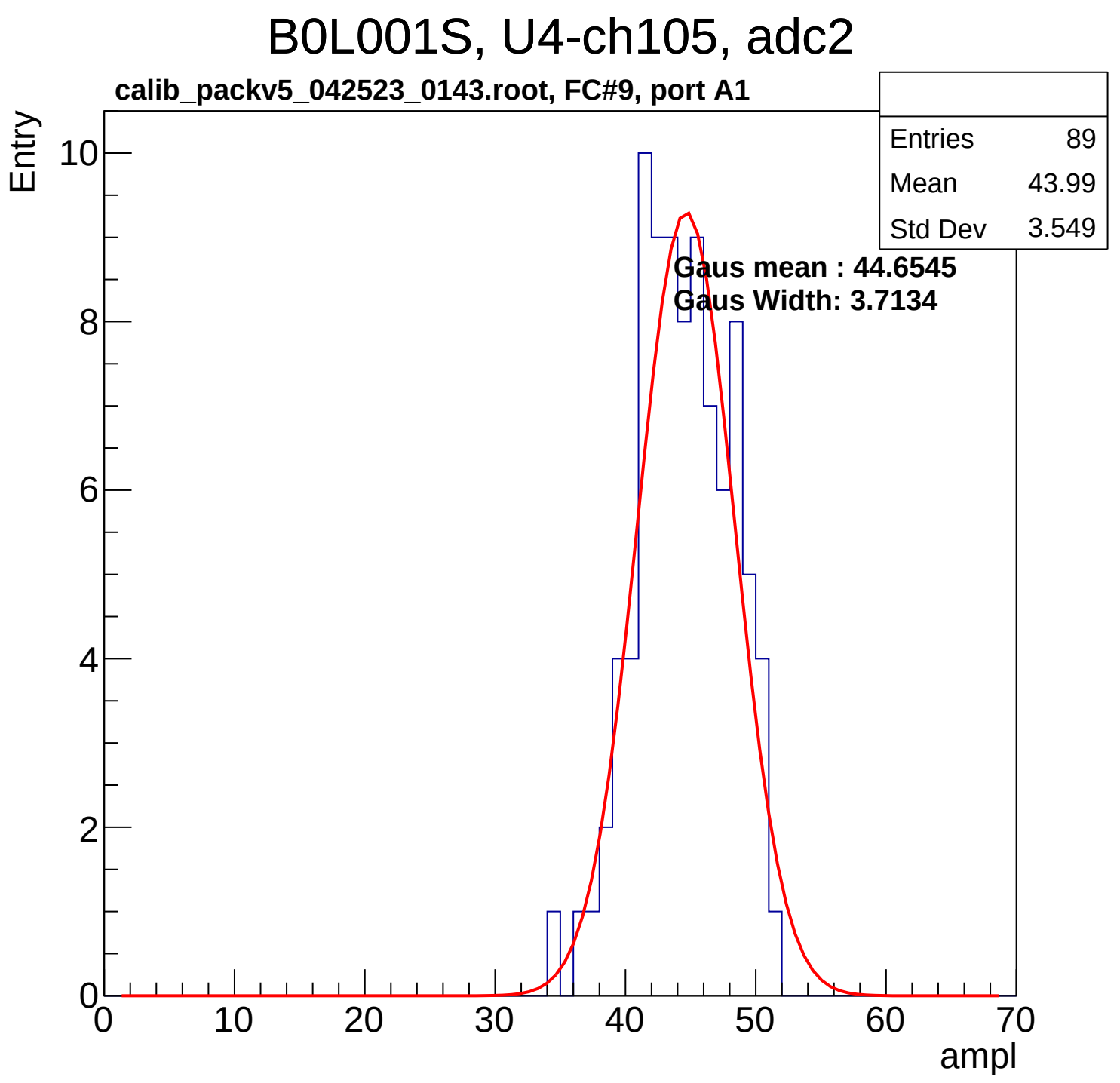
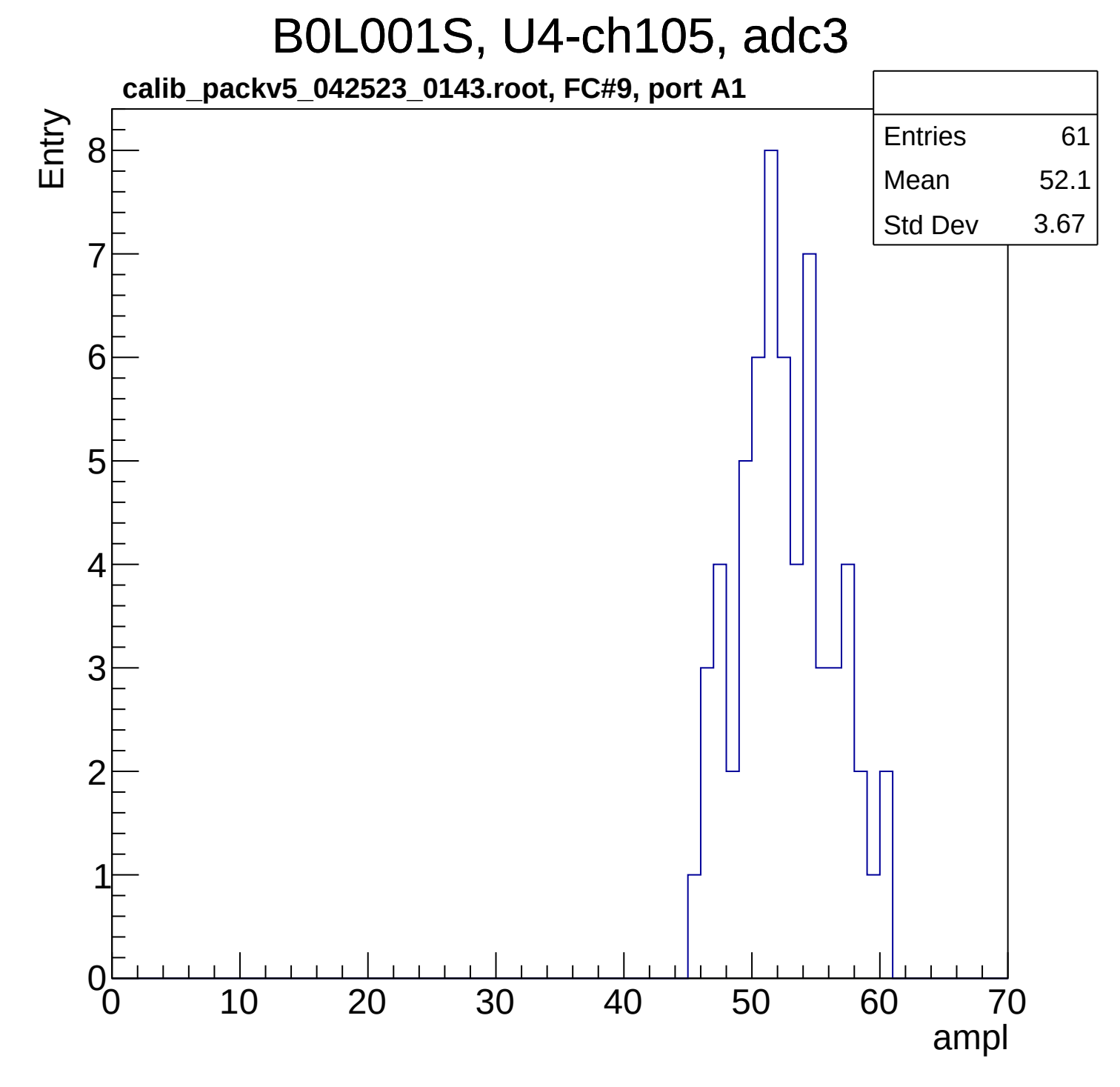
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch105, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 43.99
Std Dev 3.549
Gaus mean : 44.6545
Gaus Width: 3.7134
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch105, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 52.1
Std Dev 3.67
Entry
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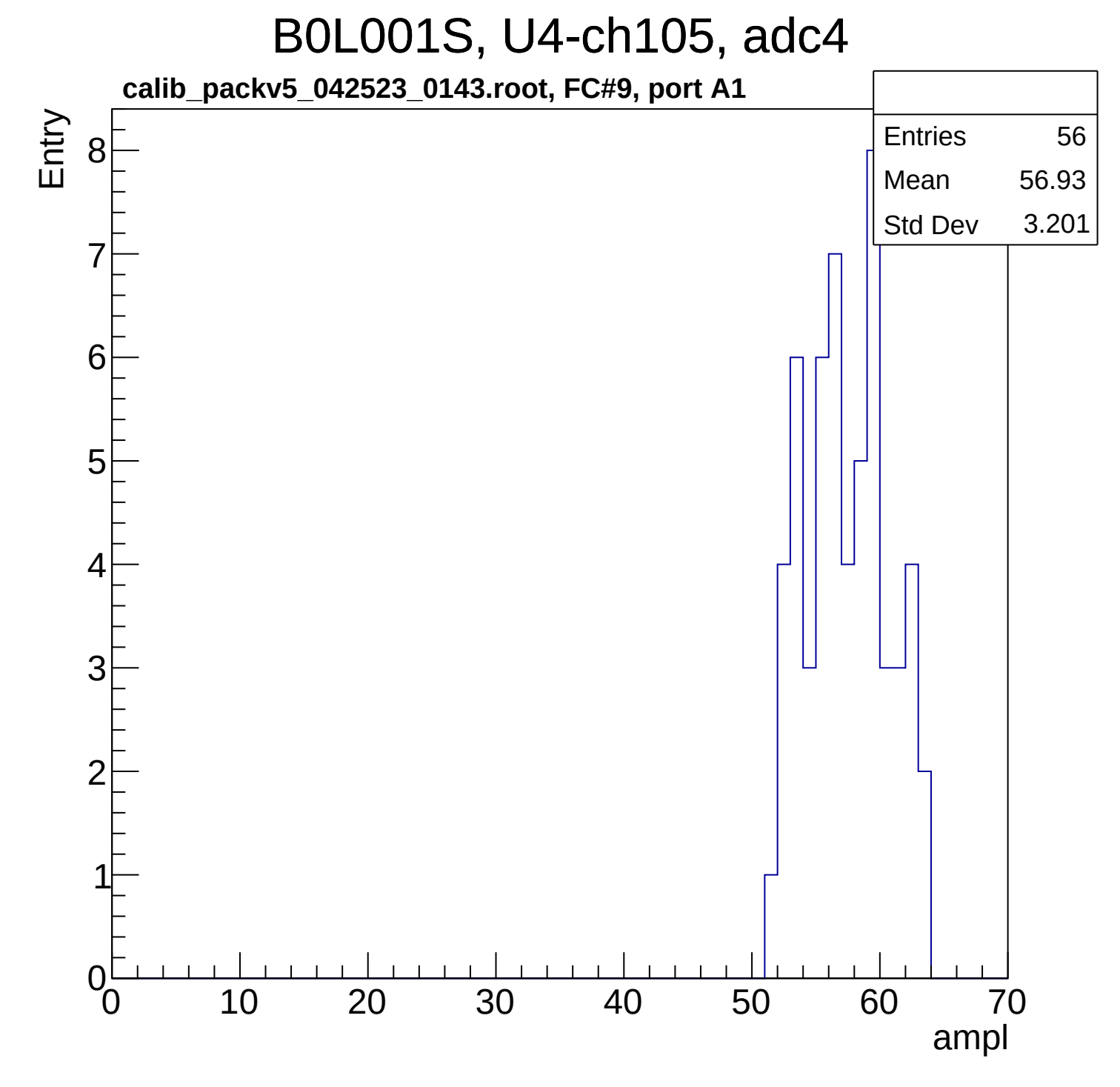
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch105, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.93
Std Dev 3.201
Entry
ampl
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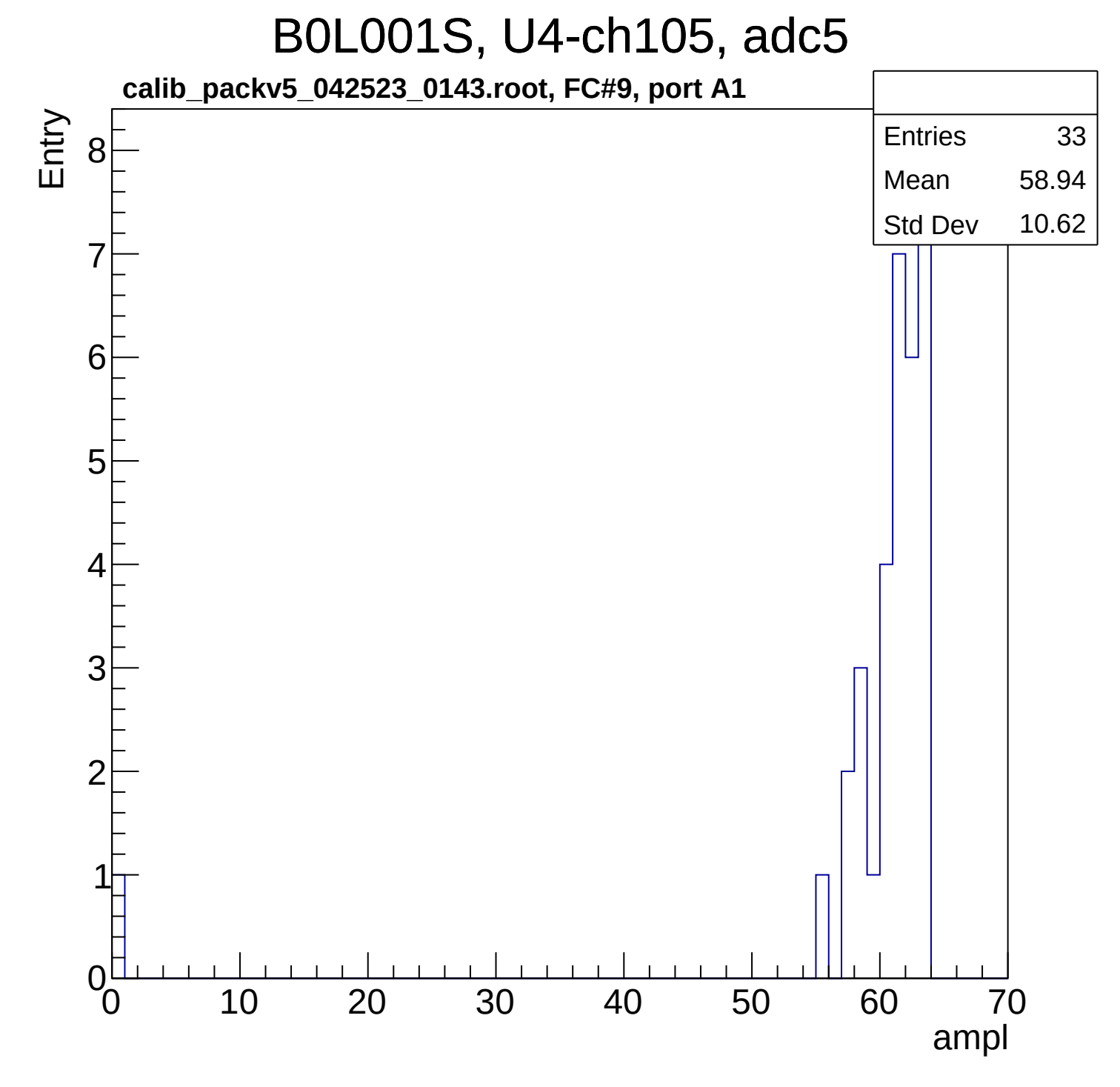
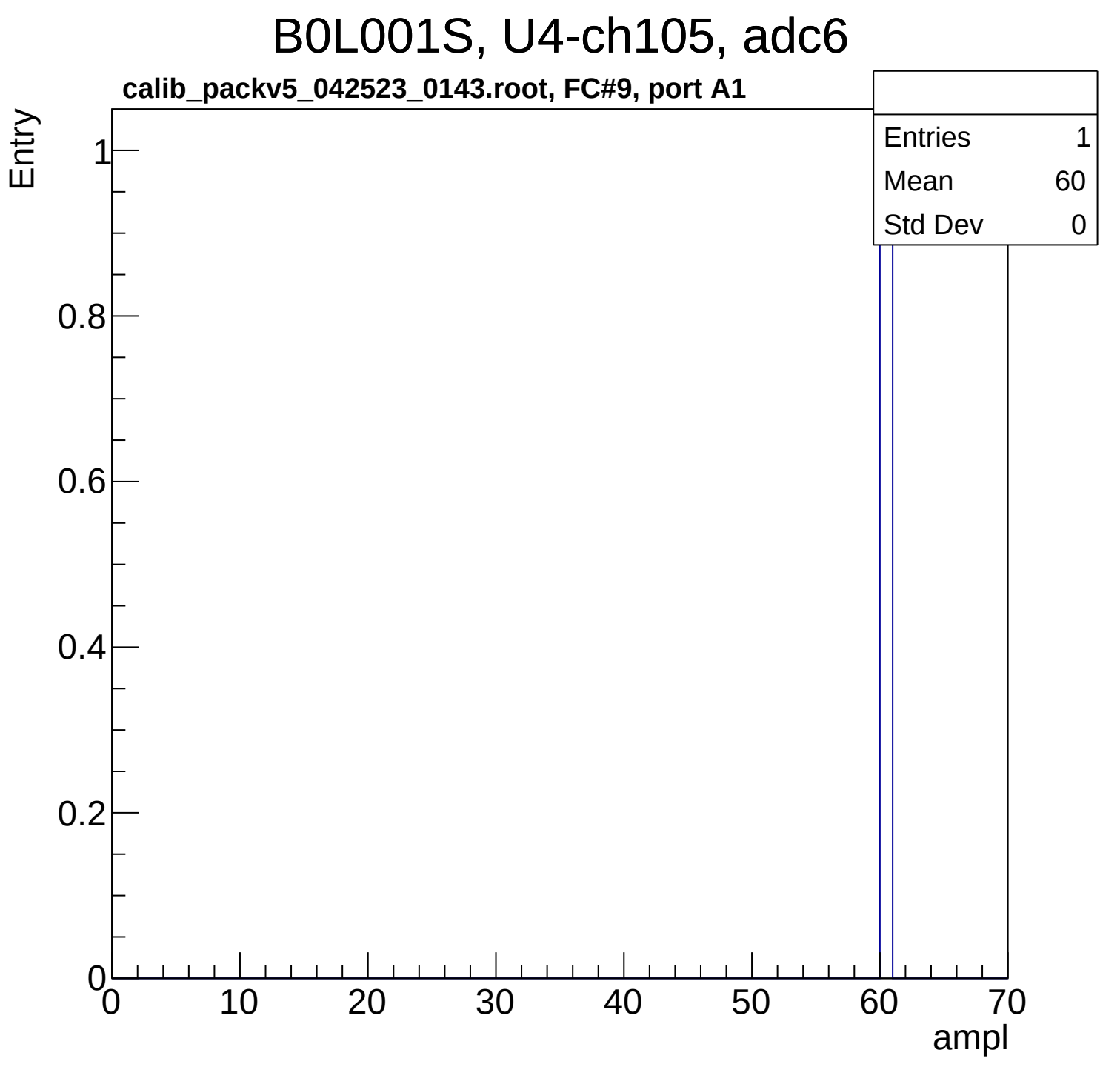
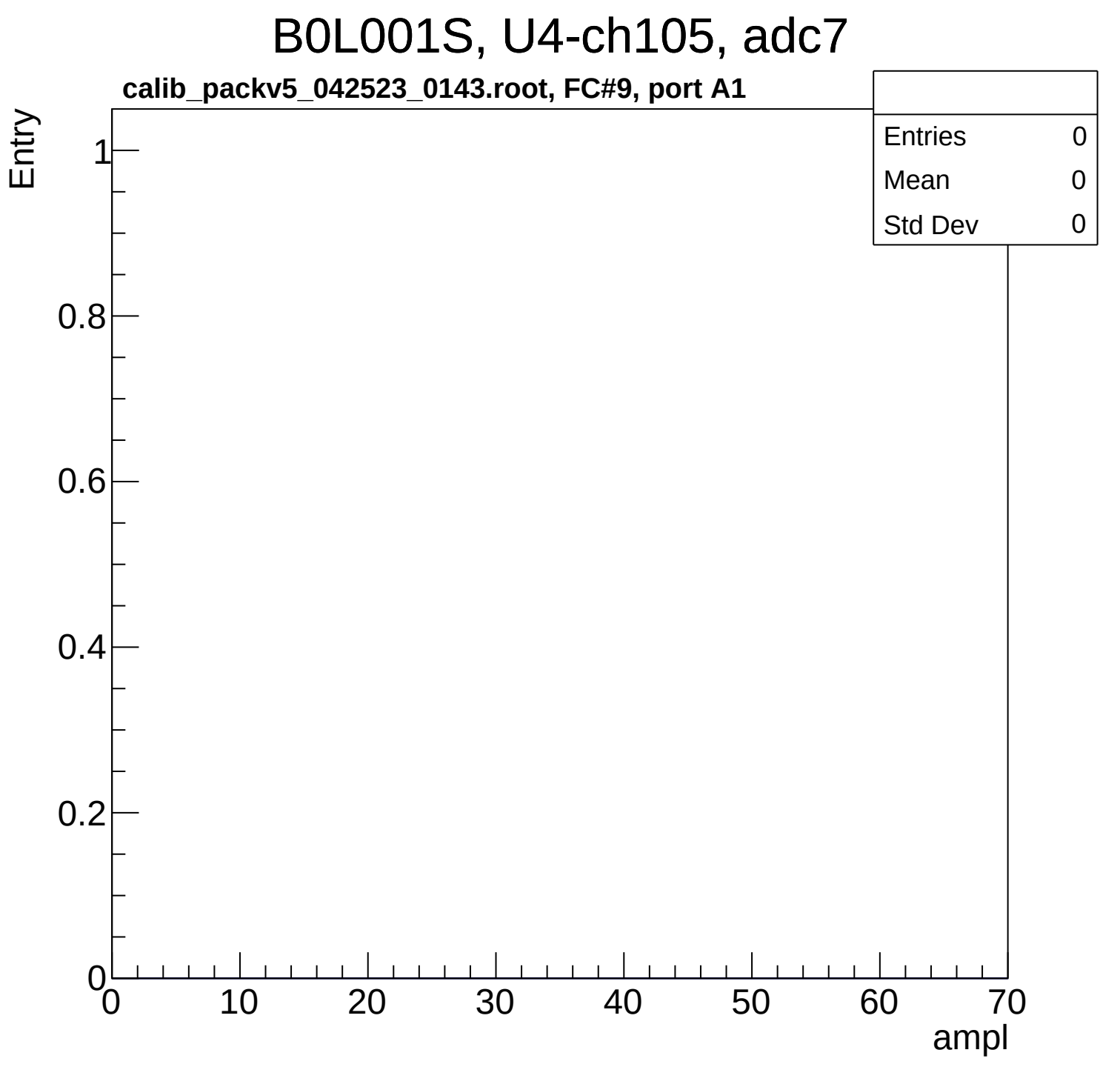
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch105, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.94
Std Dev 10.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch105, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch105, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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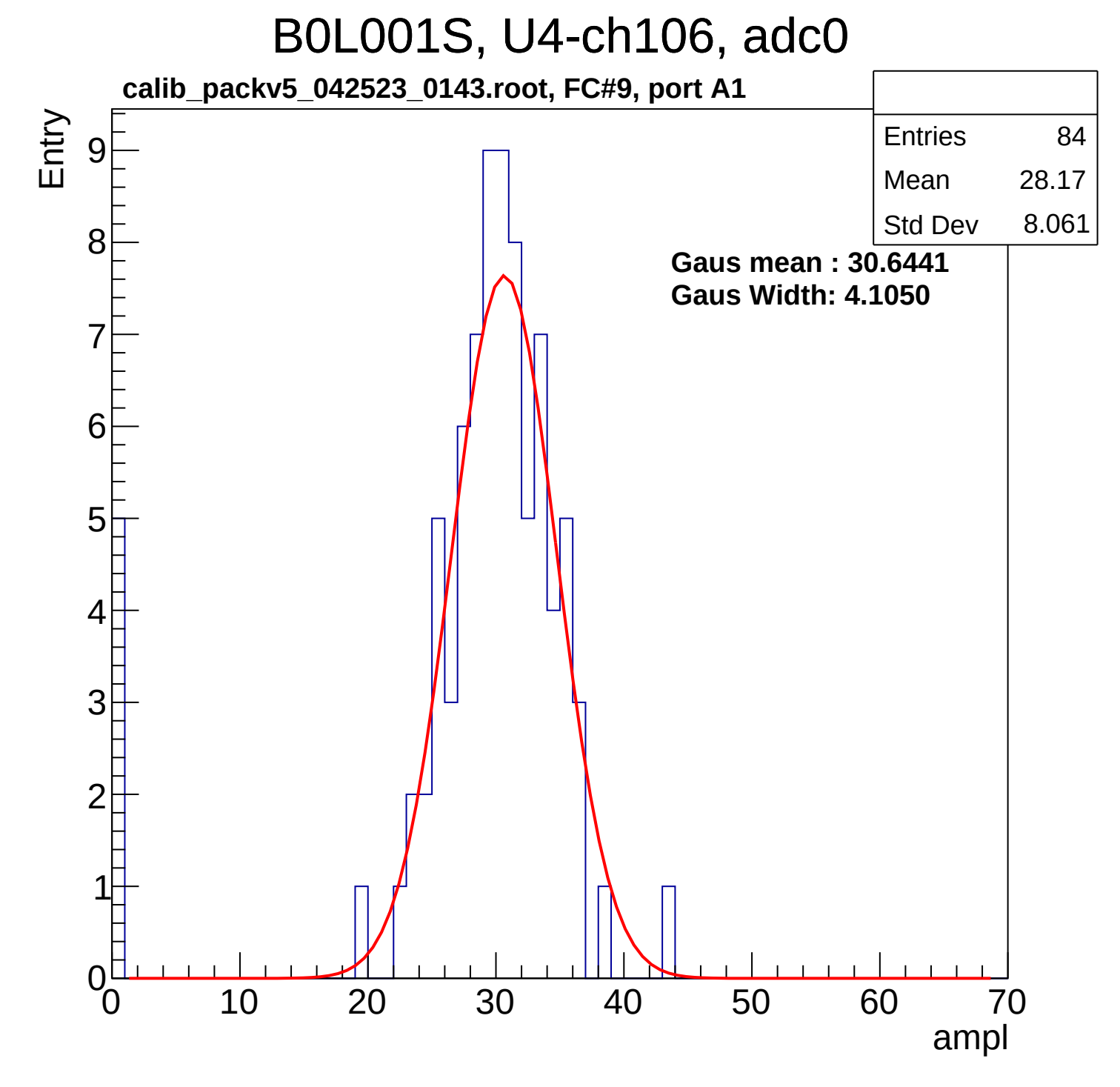
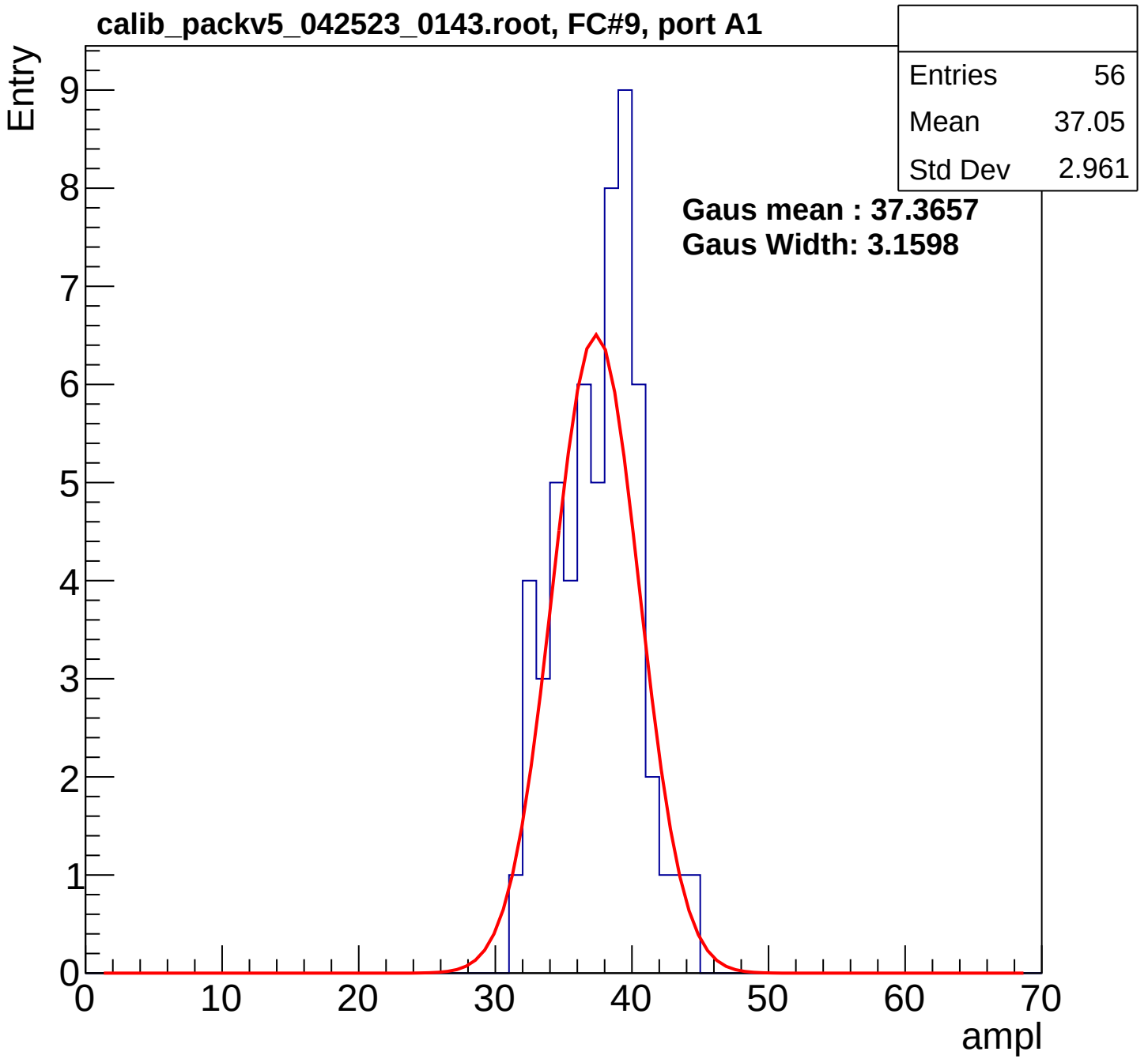
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch106, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 28.17
Std Dev 8.061
Gaus mean : 30.6441
Gaus Width: 4.1050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch106, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 37.05
Std Dev 2.961
Gaus mean : 37.3657
Gaus Width: 3.1598
Entry
ampl
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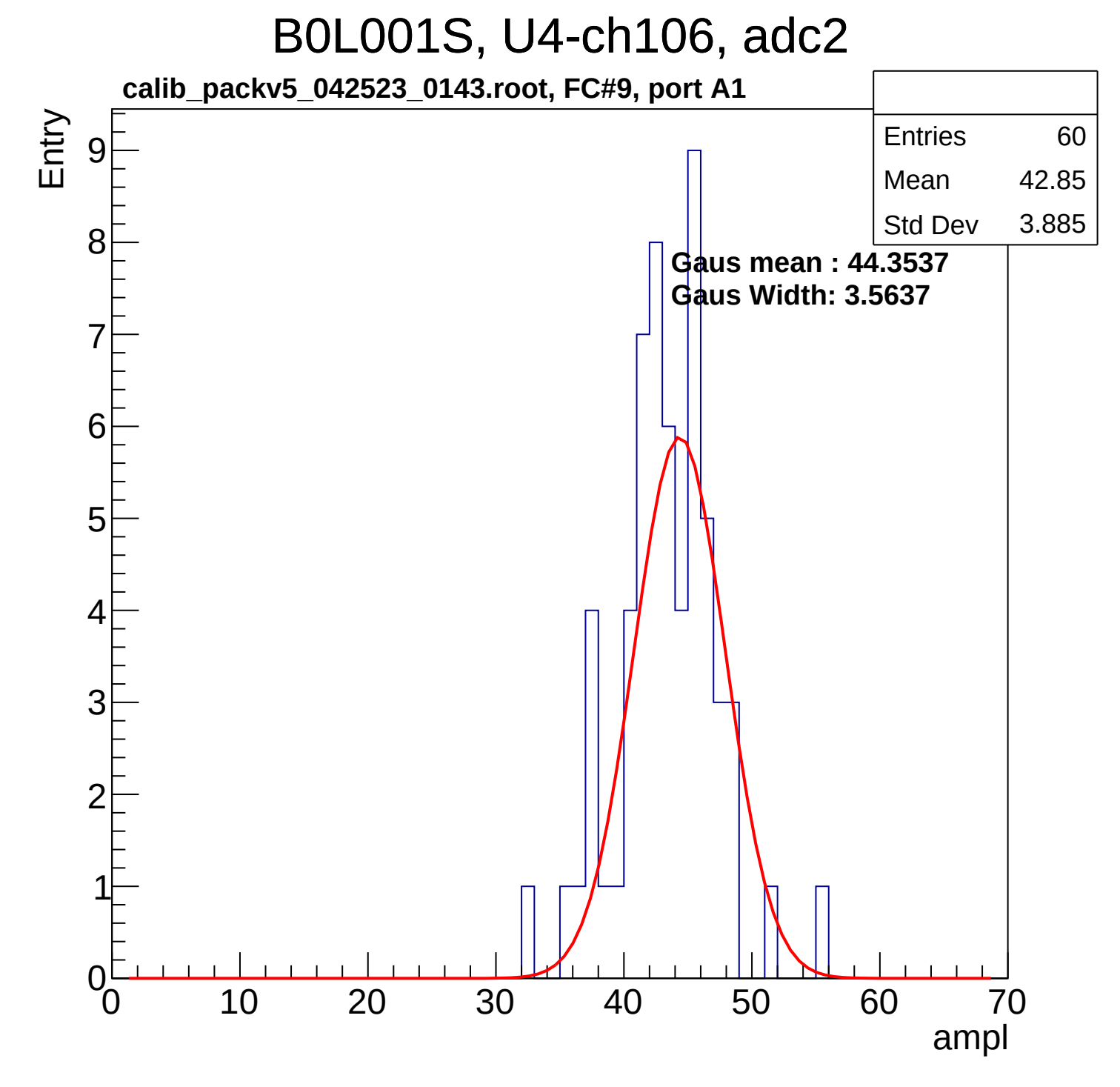
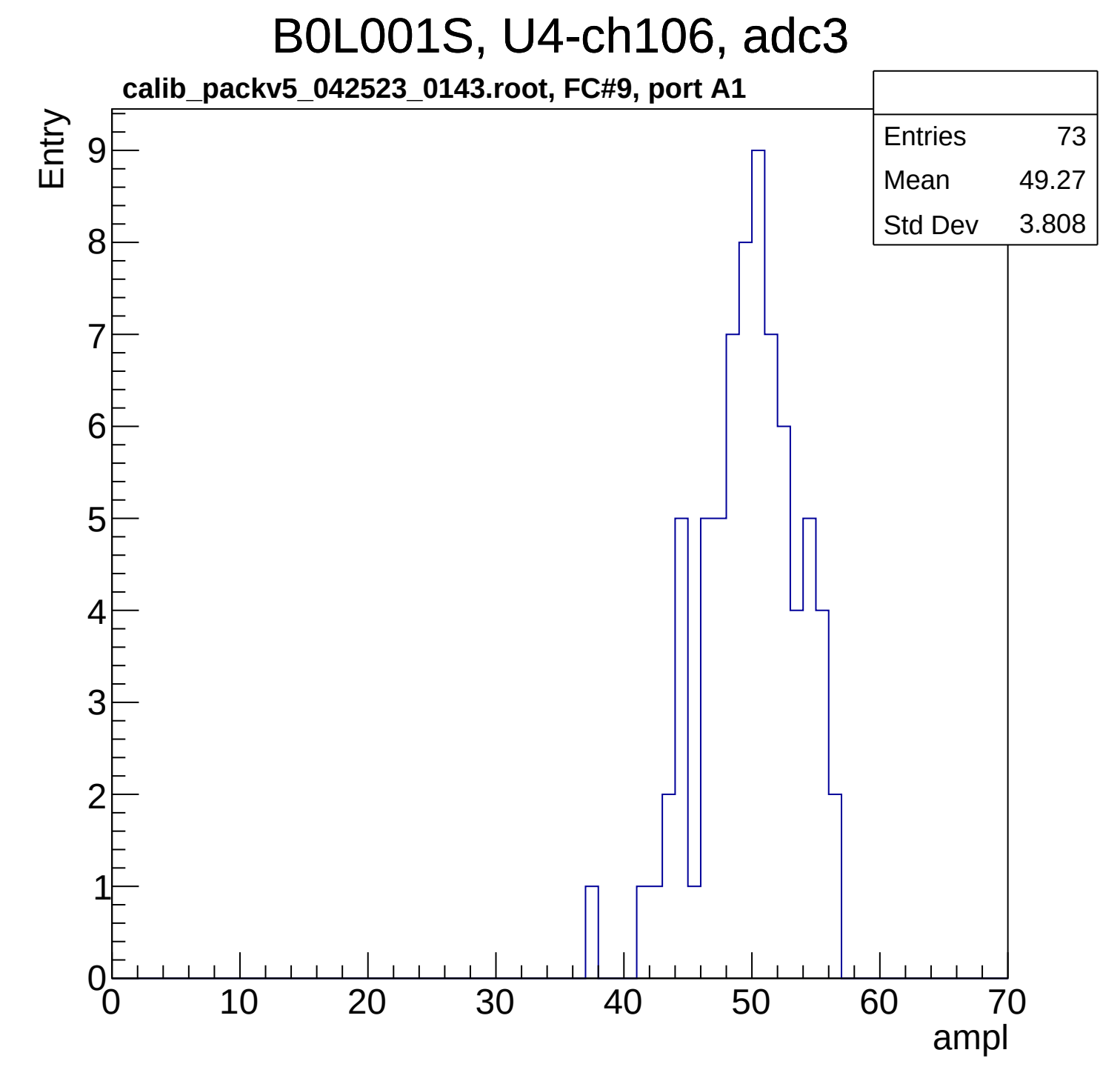
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch106, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 42.85
Std Dev 3.885
Gaus mean : 44.3537
Gaus Width: 3.5637
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch106, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 49.27
Std Dev 3.808
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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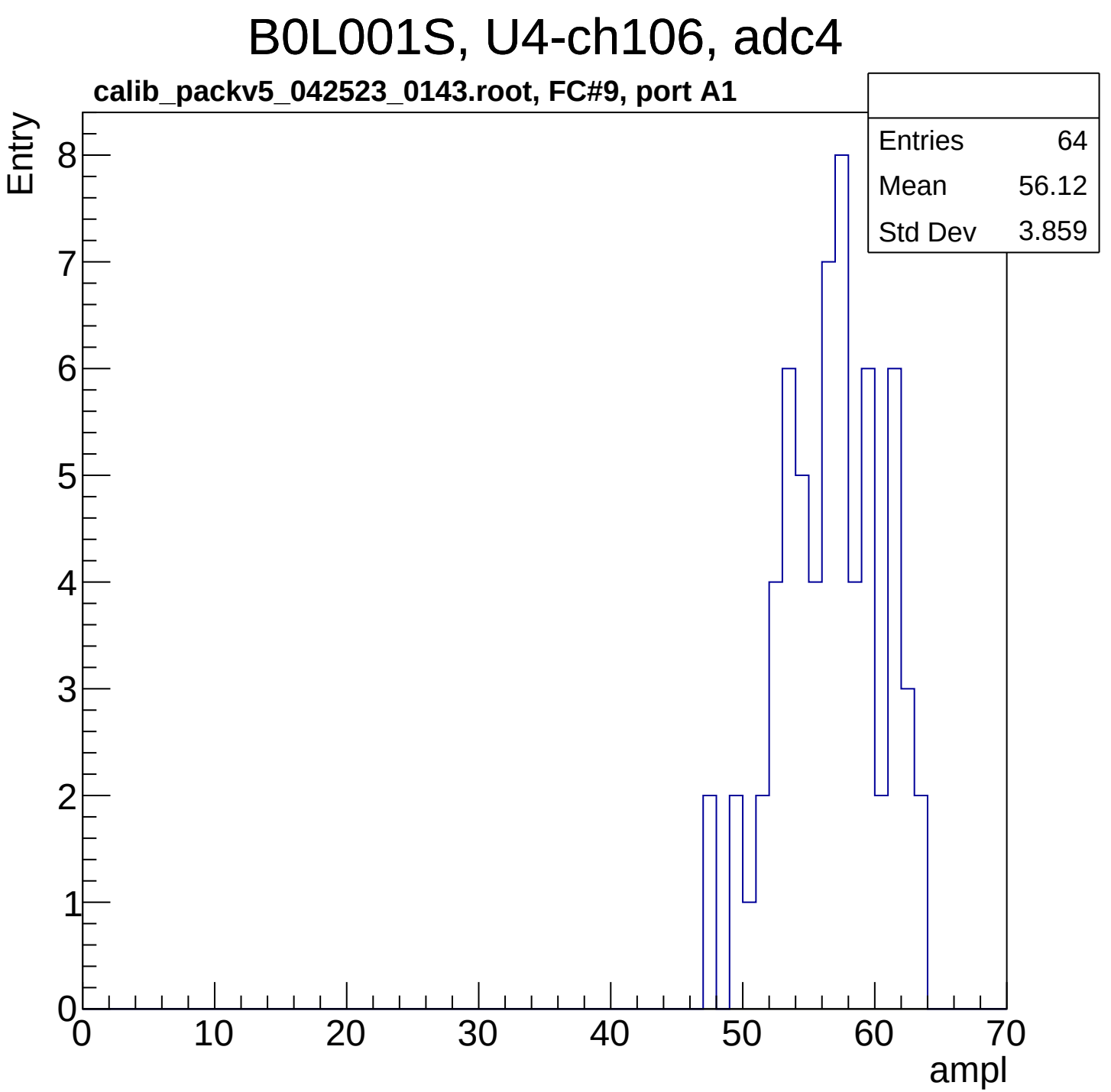
B0L001S, U4-ch106, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.12
Std Dev 3.859
Entry
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2
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4
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6
7
8
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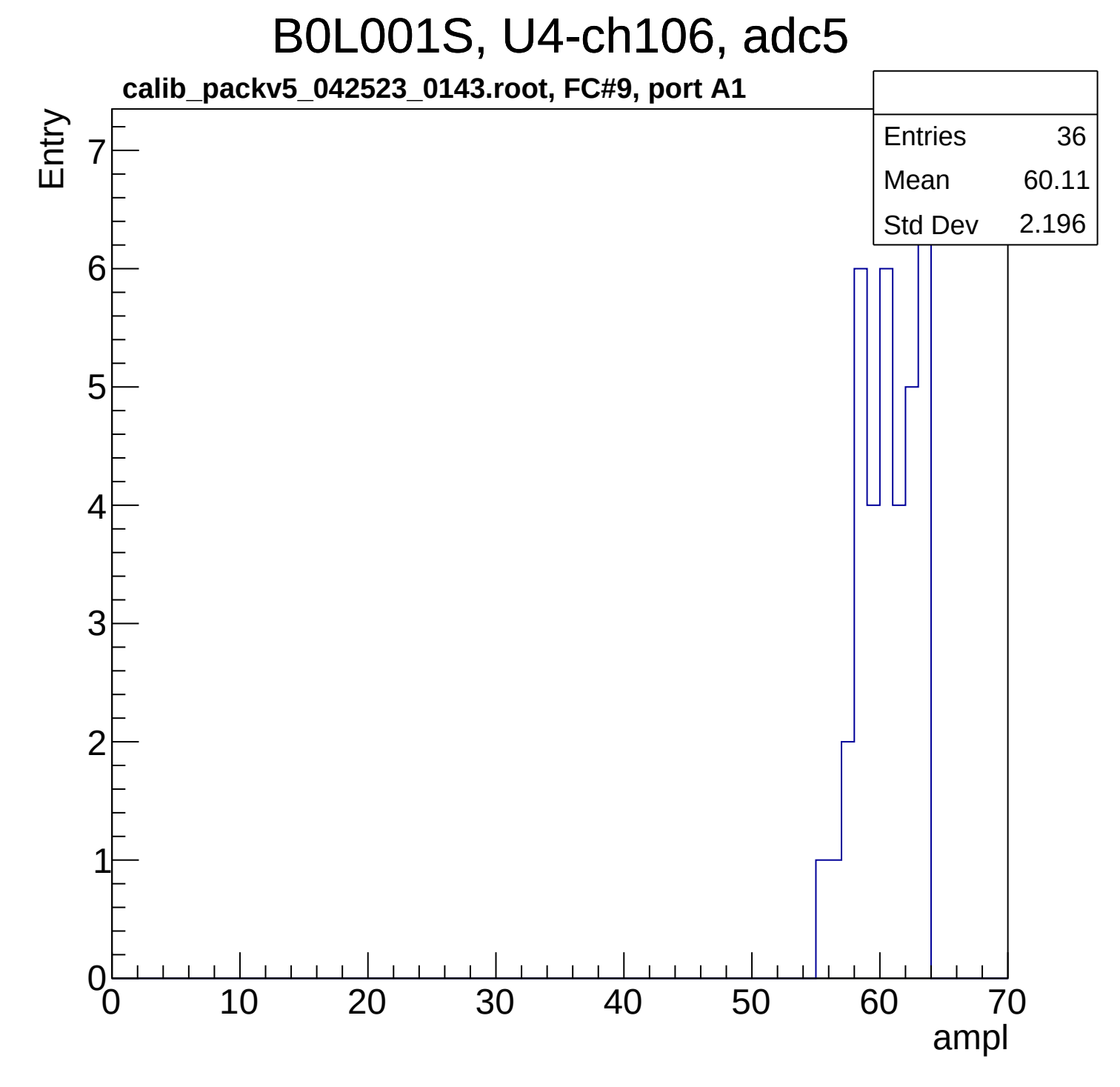
50
60
70
ampl


B0L001S, U4-ch106, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 60.11
Std Dev 2.196
Entry
ampl
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3
4
5
6
7
0
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40
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60
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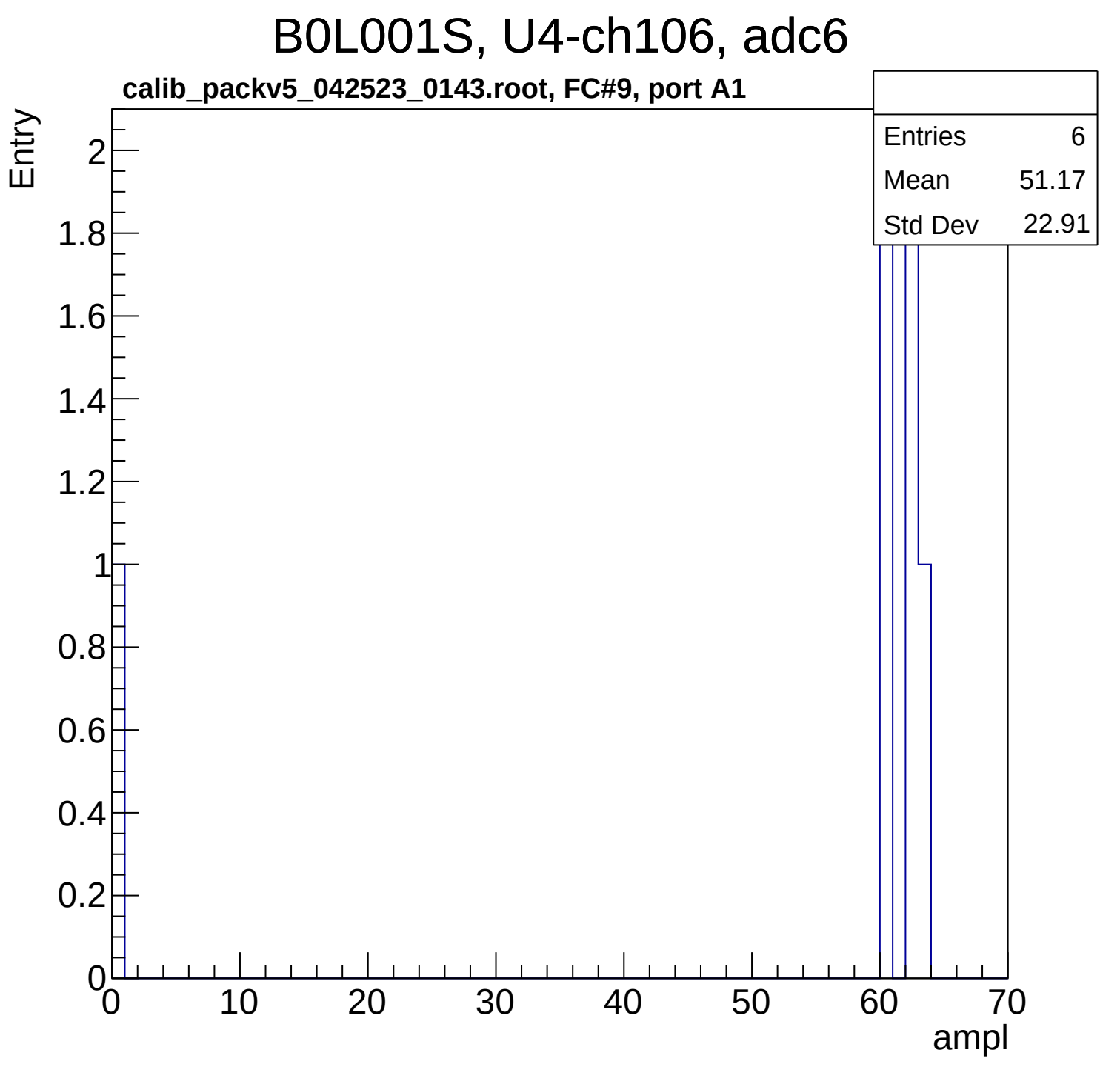
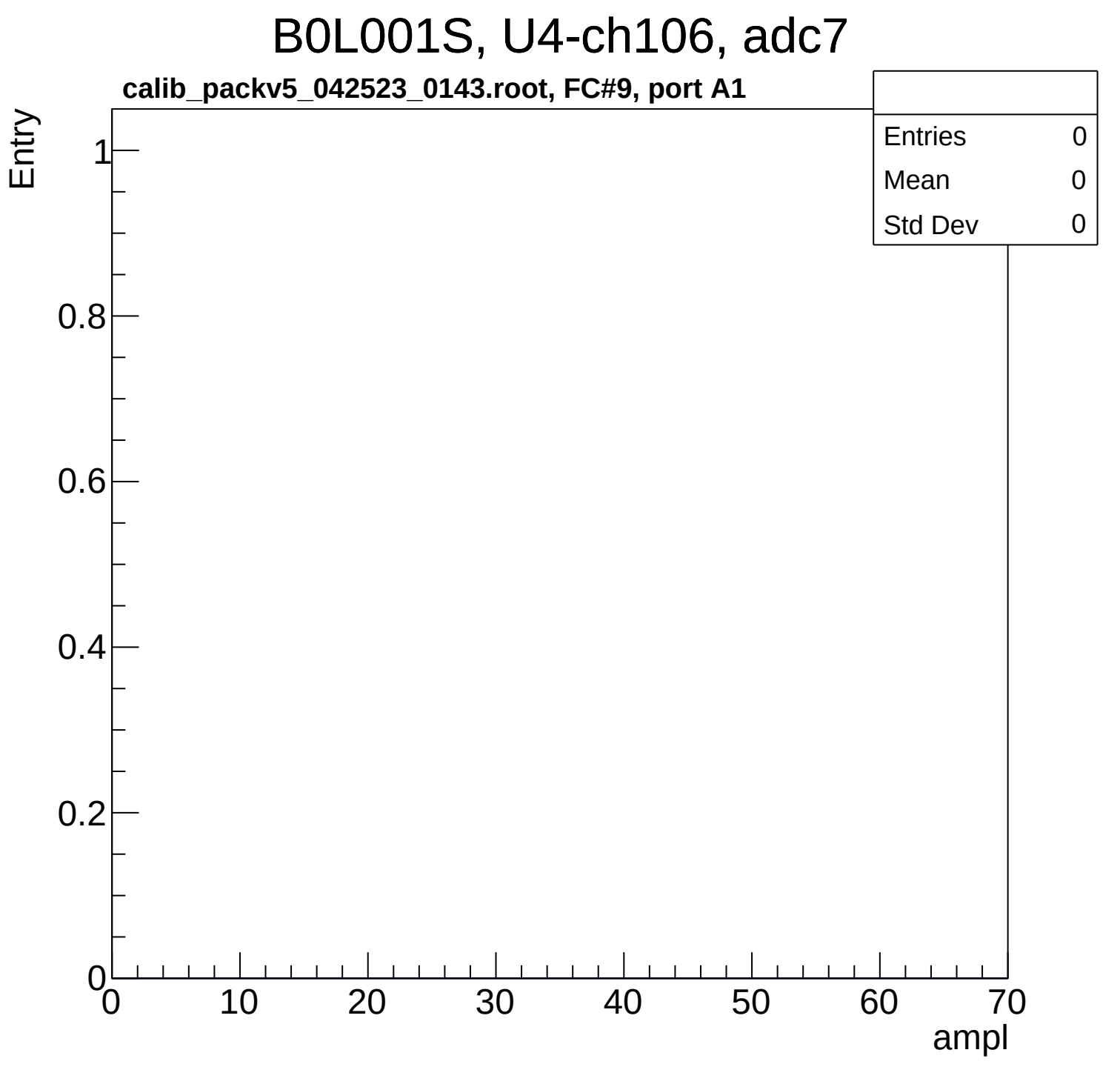

B0L001S, U4-ch106, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.17
Std Dev 22.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch106, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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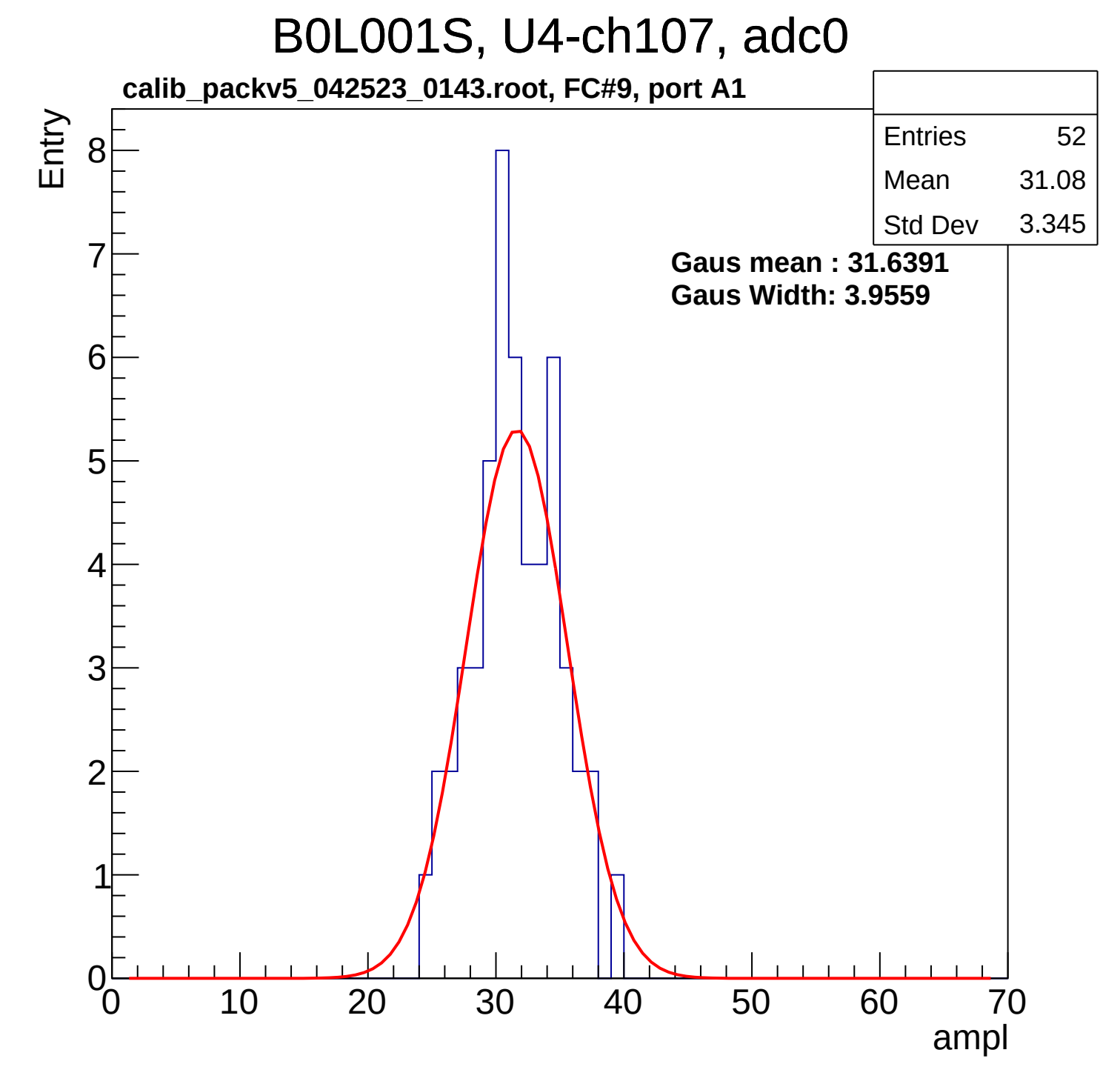
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch107, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 31.08
Std Dev 3.345
Gaus mean : 31.6391
Gaus Width: 3.9559
Entry
ampl
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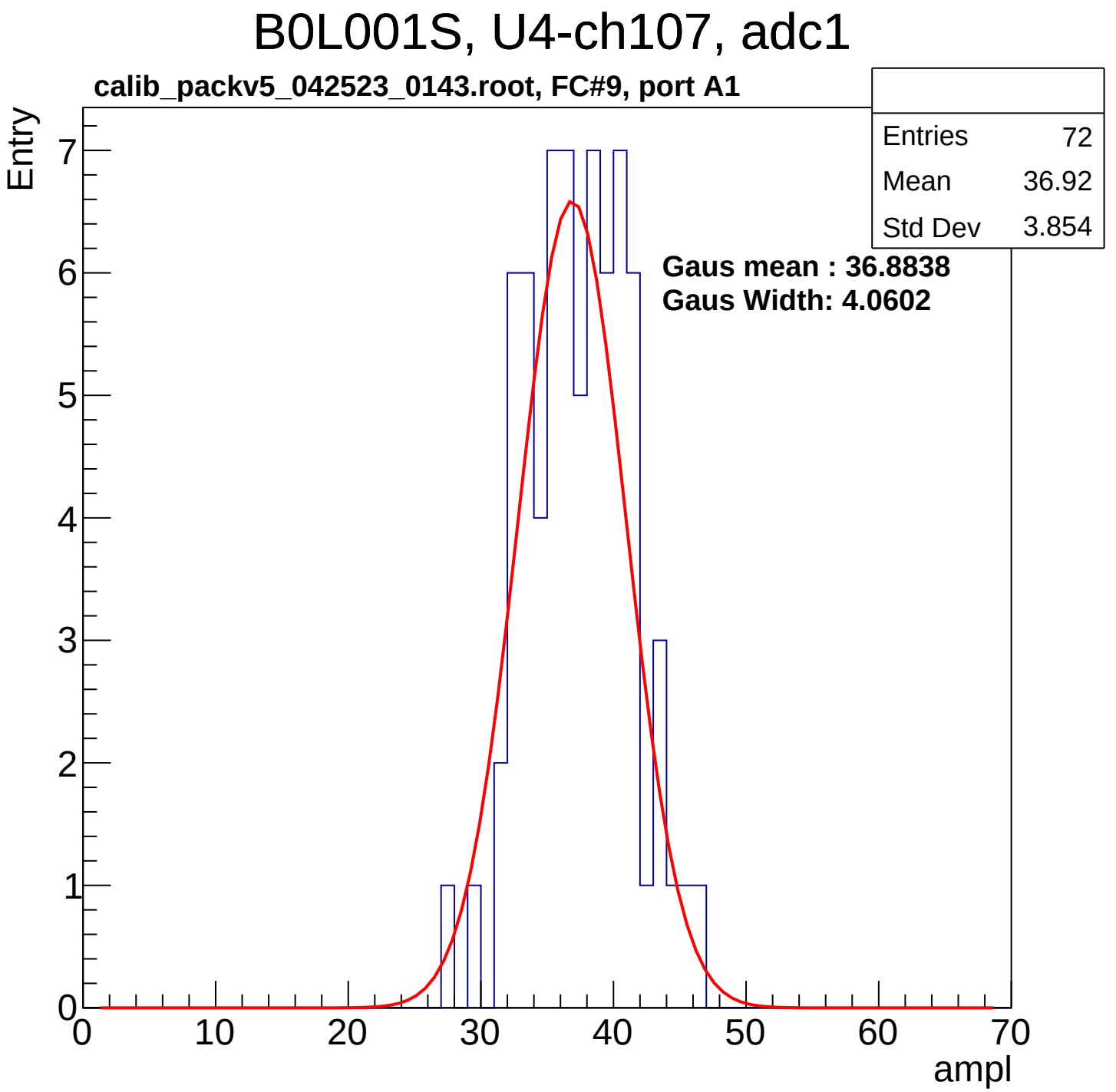
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch107, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 36.92
Std Dev 3.854
Gaus mean : 36.8838
Gaus Width: 4.0602
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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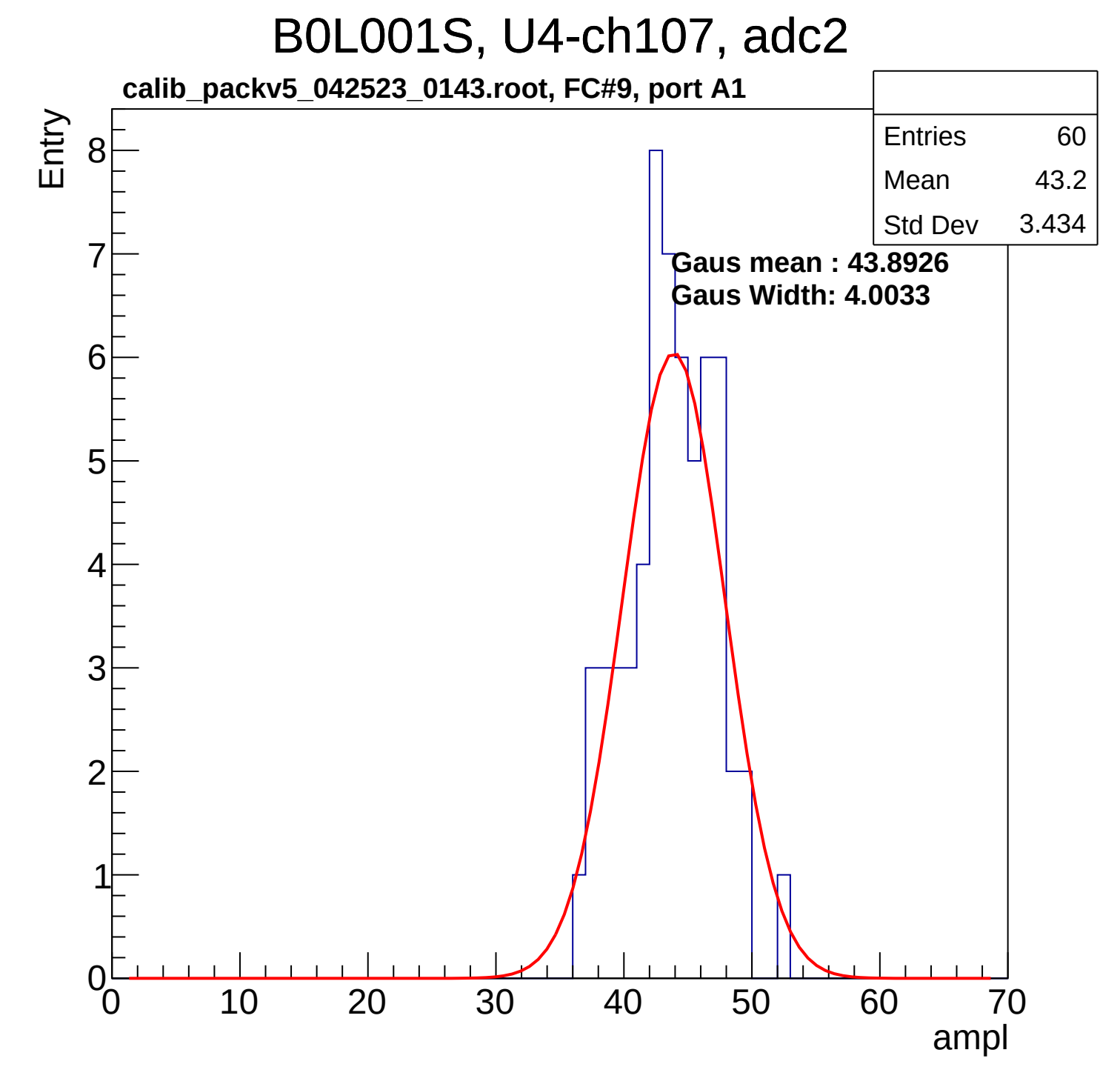
B0L001S, U4-ch107, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 43.2
Std Dev 3.434
Gaus mean : 43.8926
Gaus Width: 4.0033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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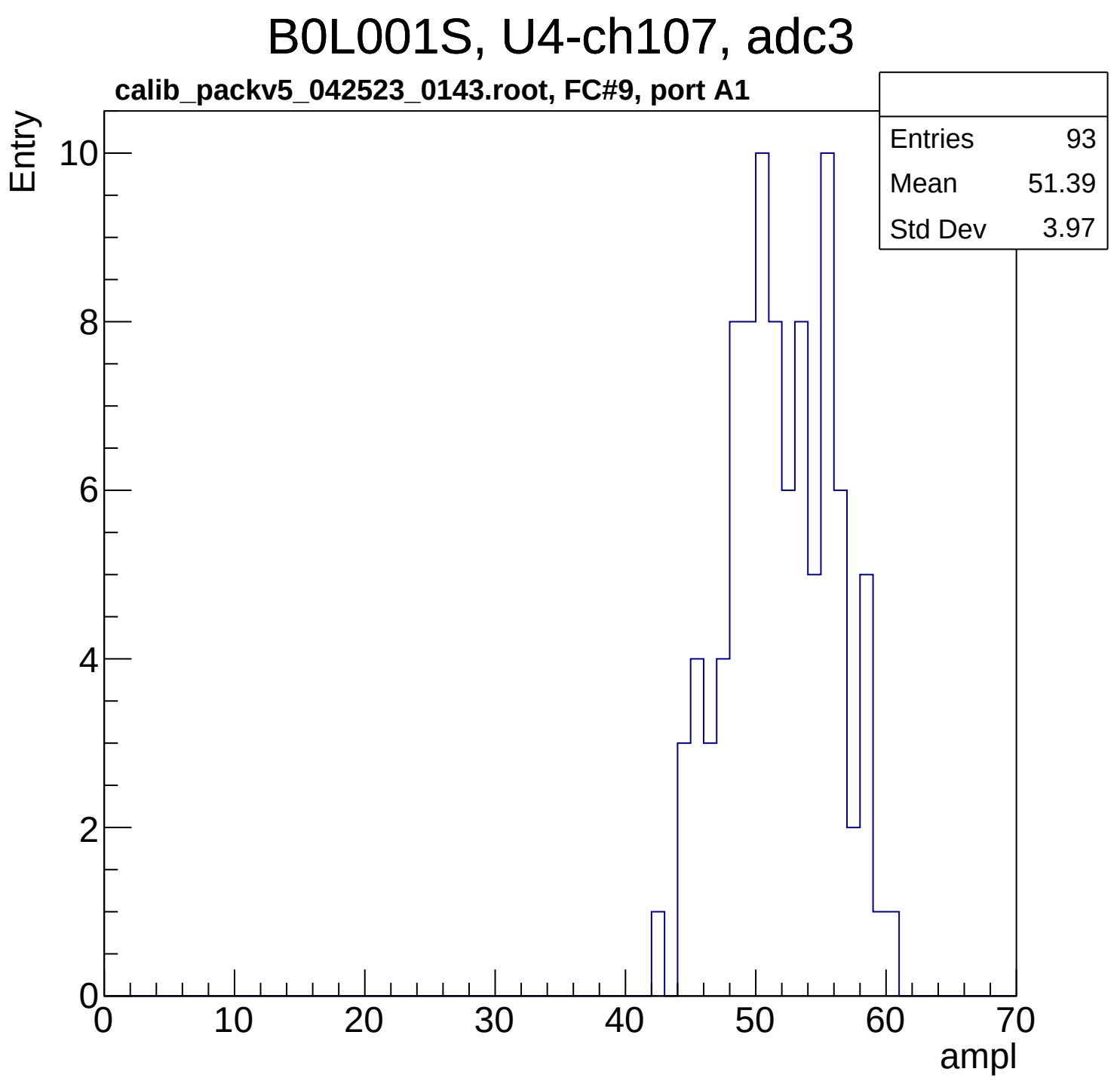
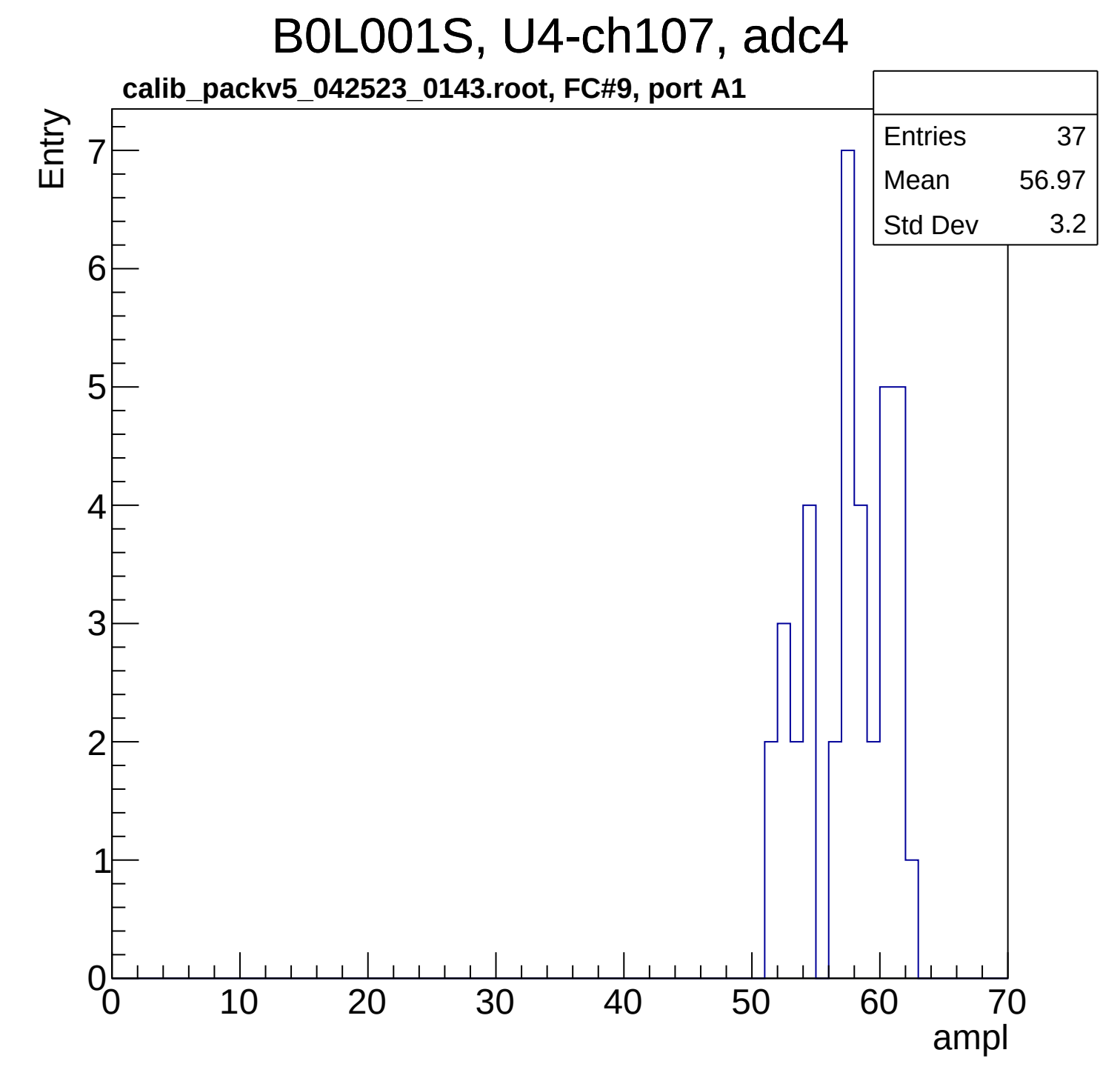

B0L001S, U4-ch107, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 93
Mean 51.39
Std Dev 3.97
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch107, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 56.97
Std Dev 3.2
Entry
ampl
0
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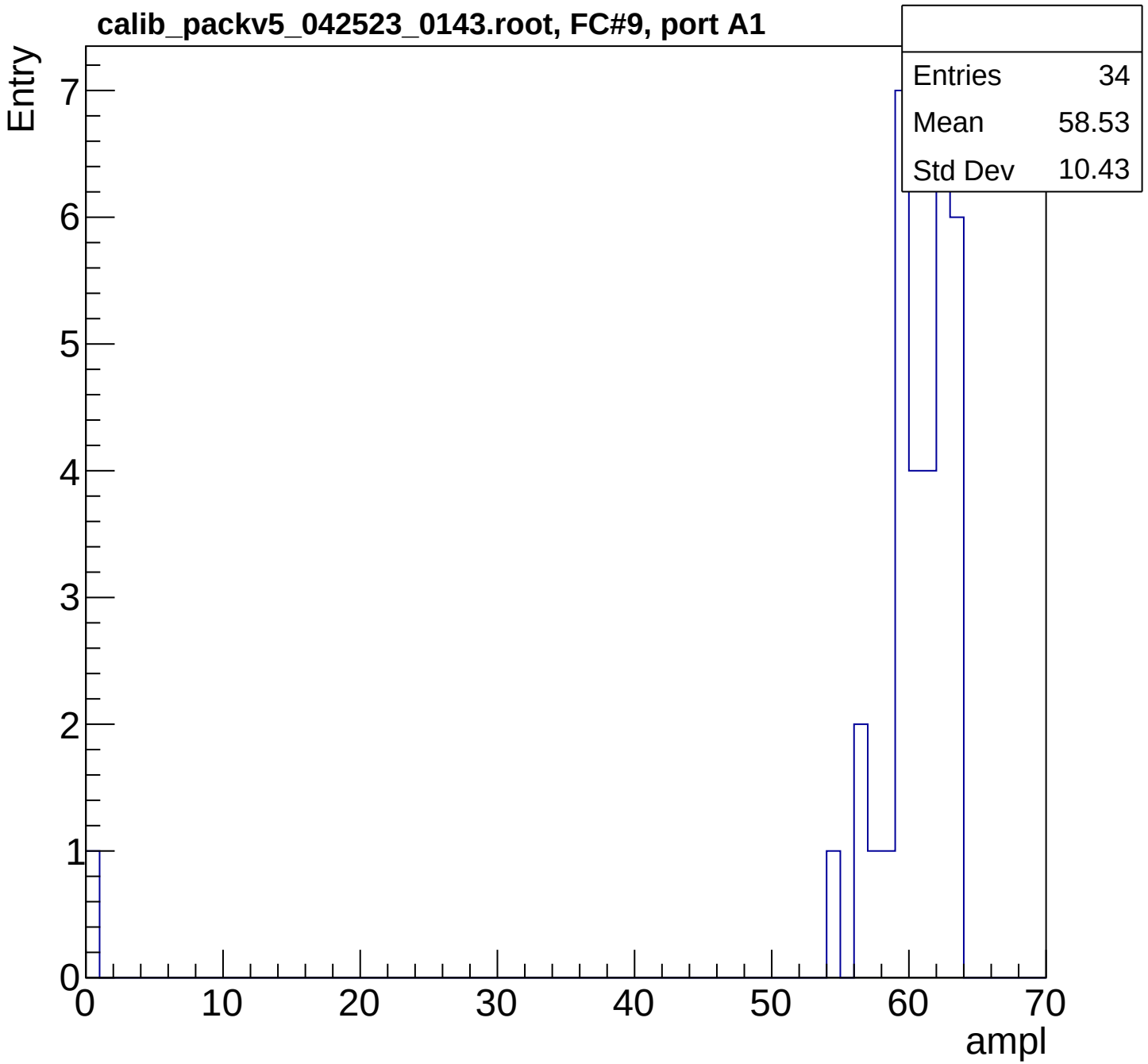
B0L001S, U4-ch107, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.53
Std Dev 10.43
Entry
ampl
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4
5
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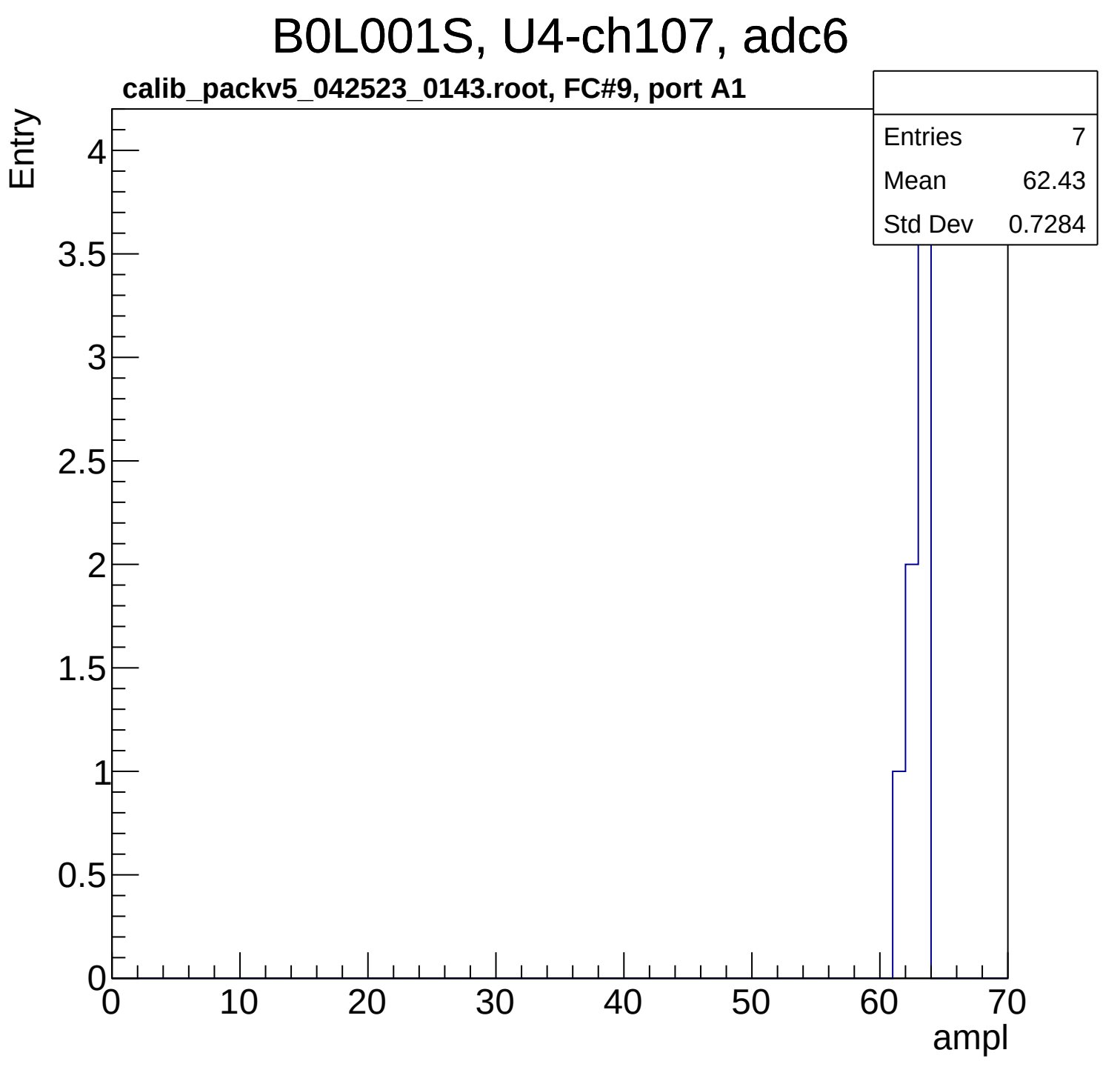
B0L001S, U4-ch107, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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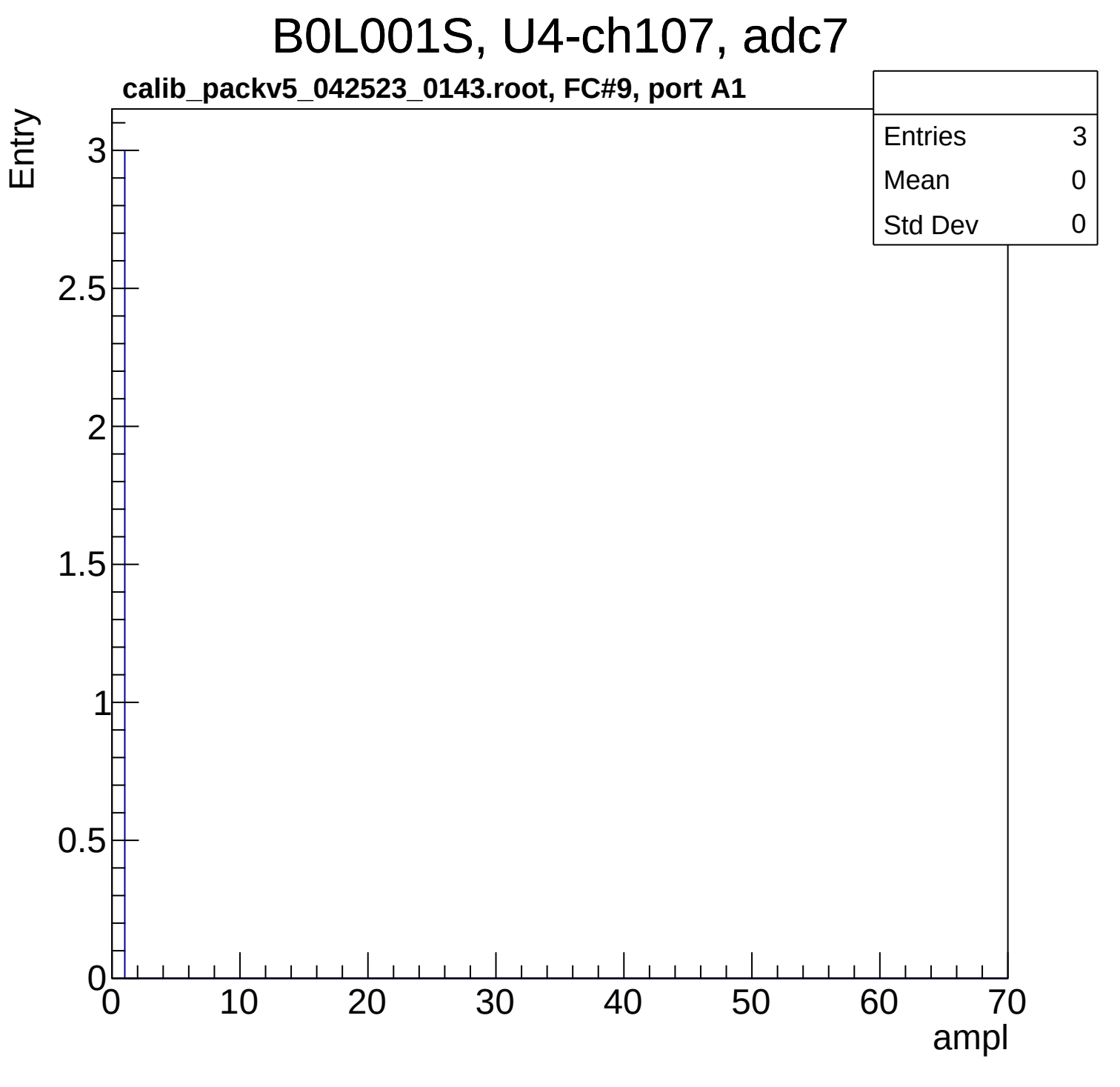
B0L001S, U4-ch107, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
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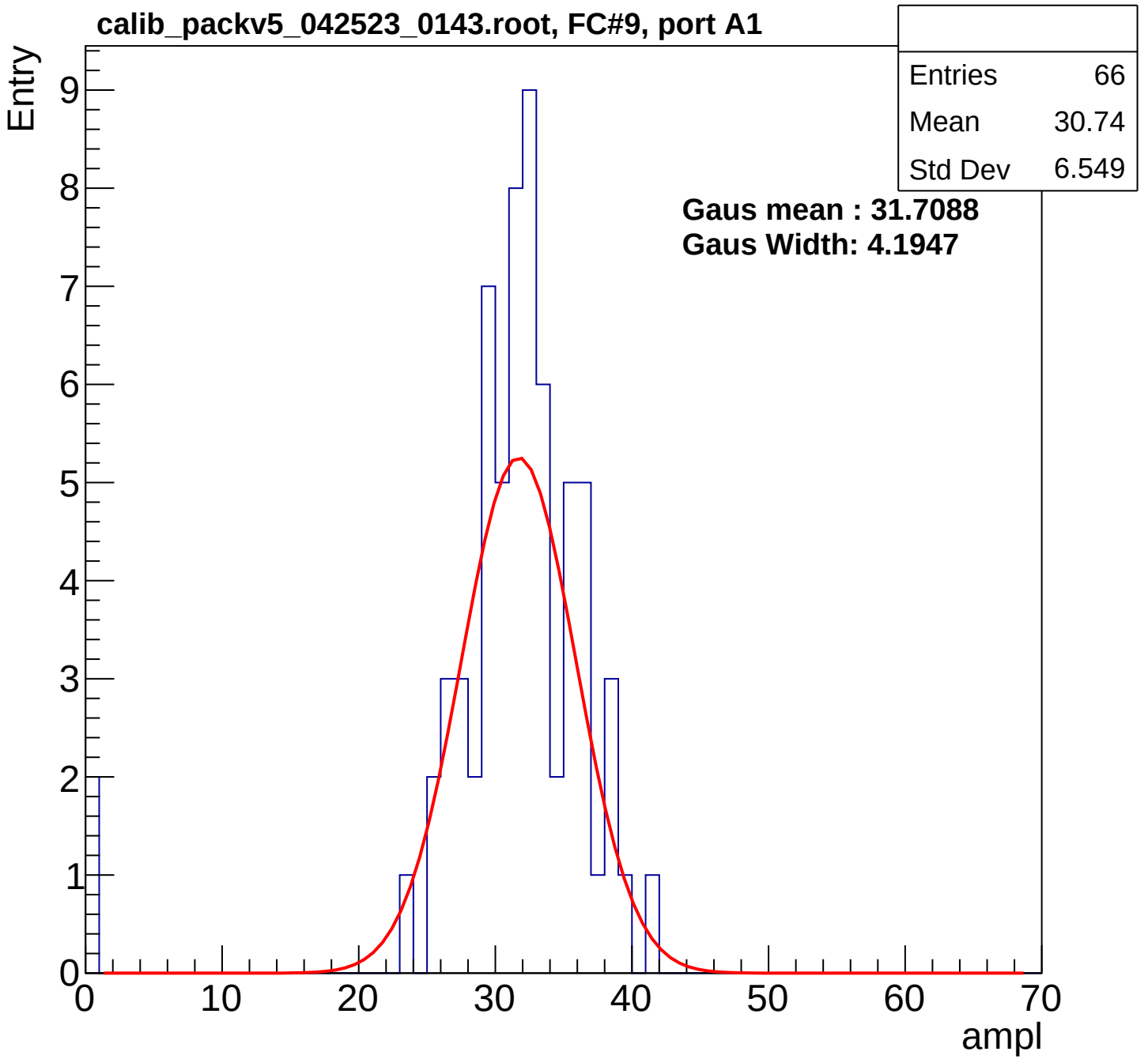
30
40
50
60
70
ampl


B0L001S, U4-ch108, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.74
Std Dev 6.549
Gaus mean : 31.7088
Gaus Width: 4.1947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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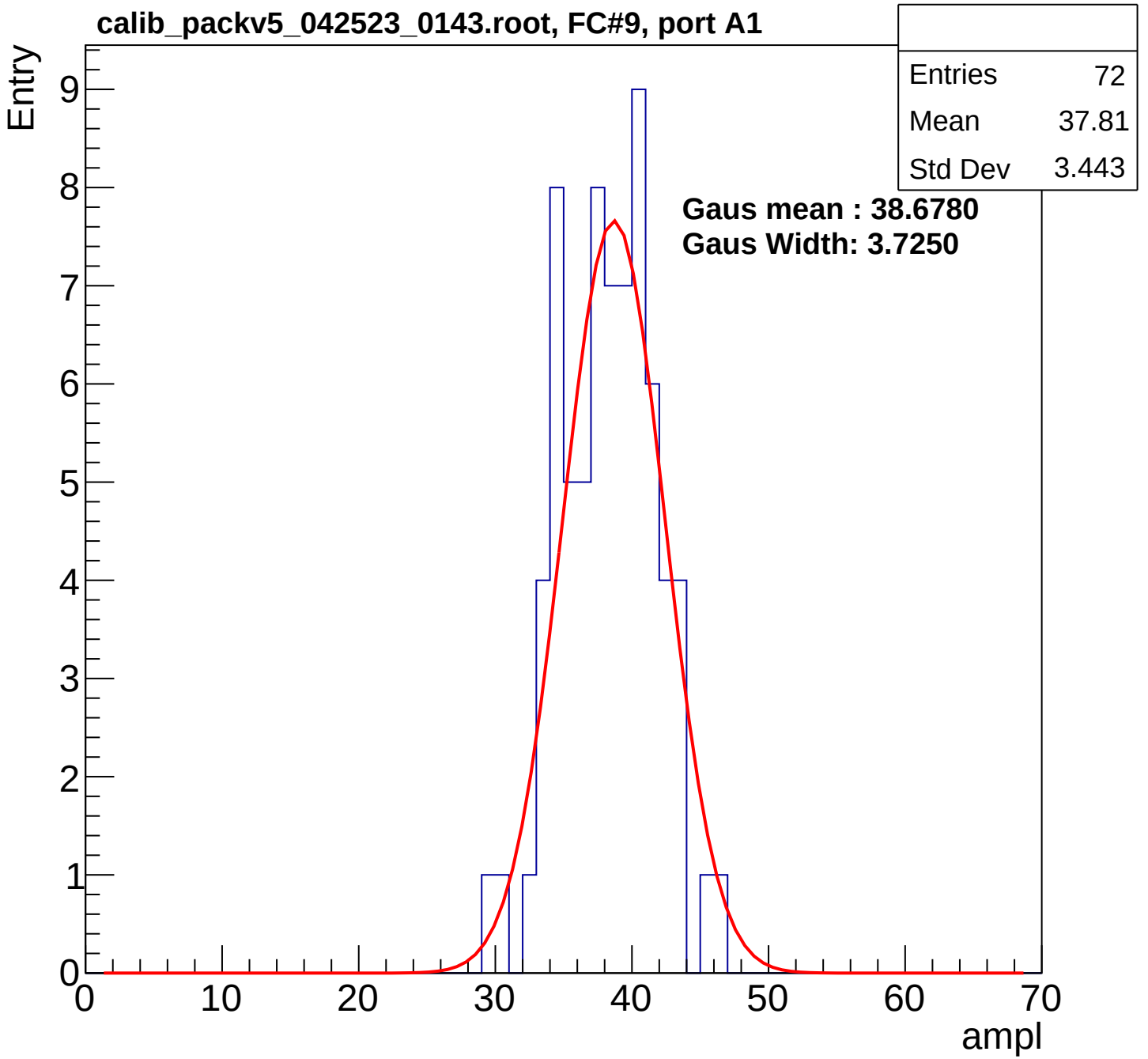
B0L001S, U4-ch108, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.81
Std Dev 3.443
Gaus mean : 38.6780
Gaus Width: 3.7250
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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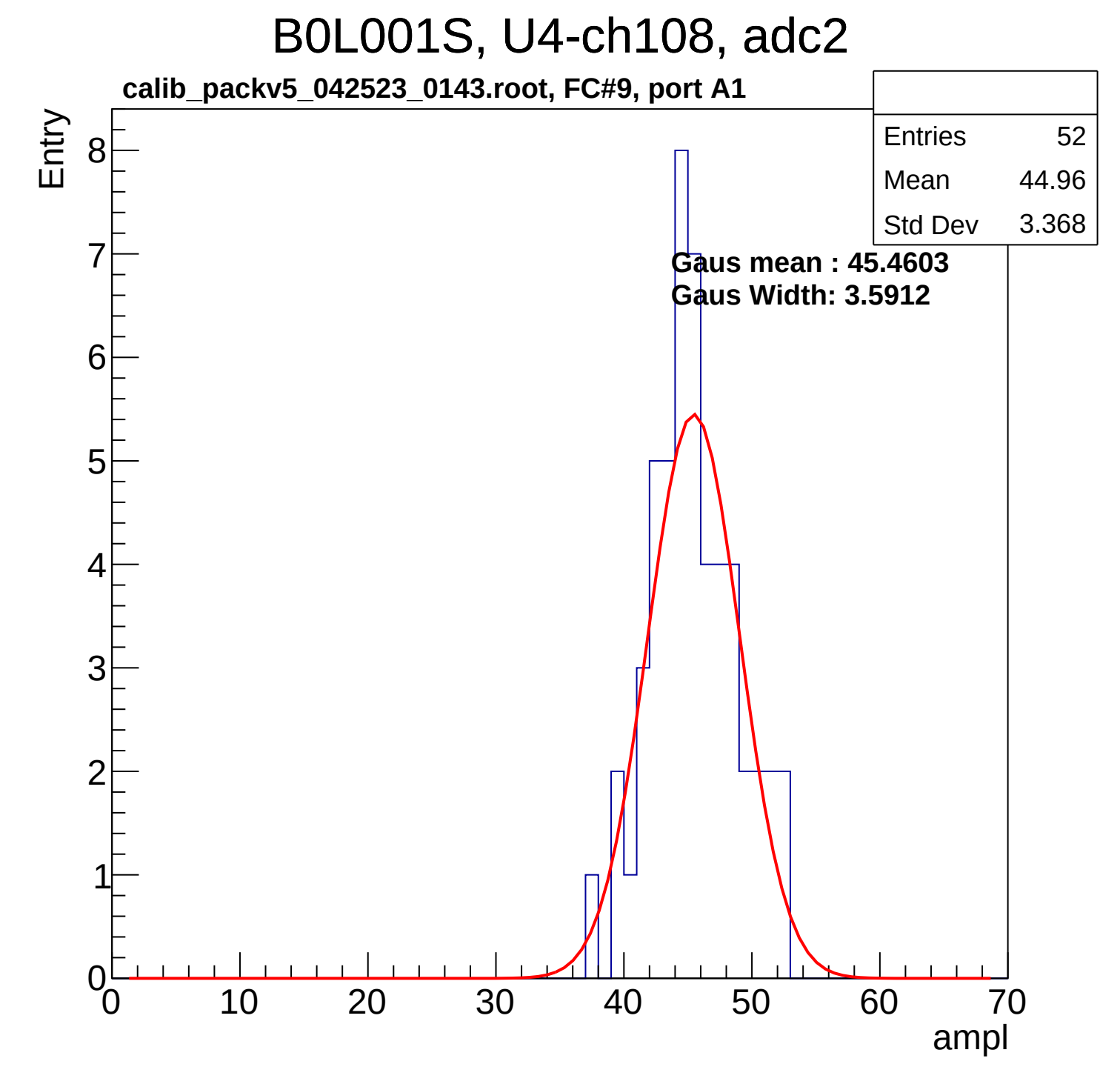
B0L001S, U4-ch108, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 44.96
Std Dev 3.368
Gaus mean : 45.4603
Gaus Width: 3.5912
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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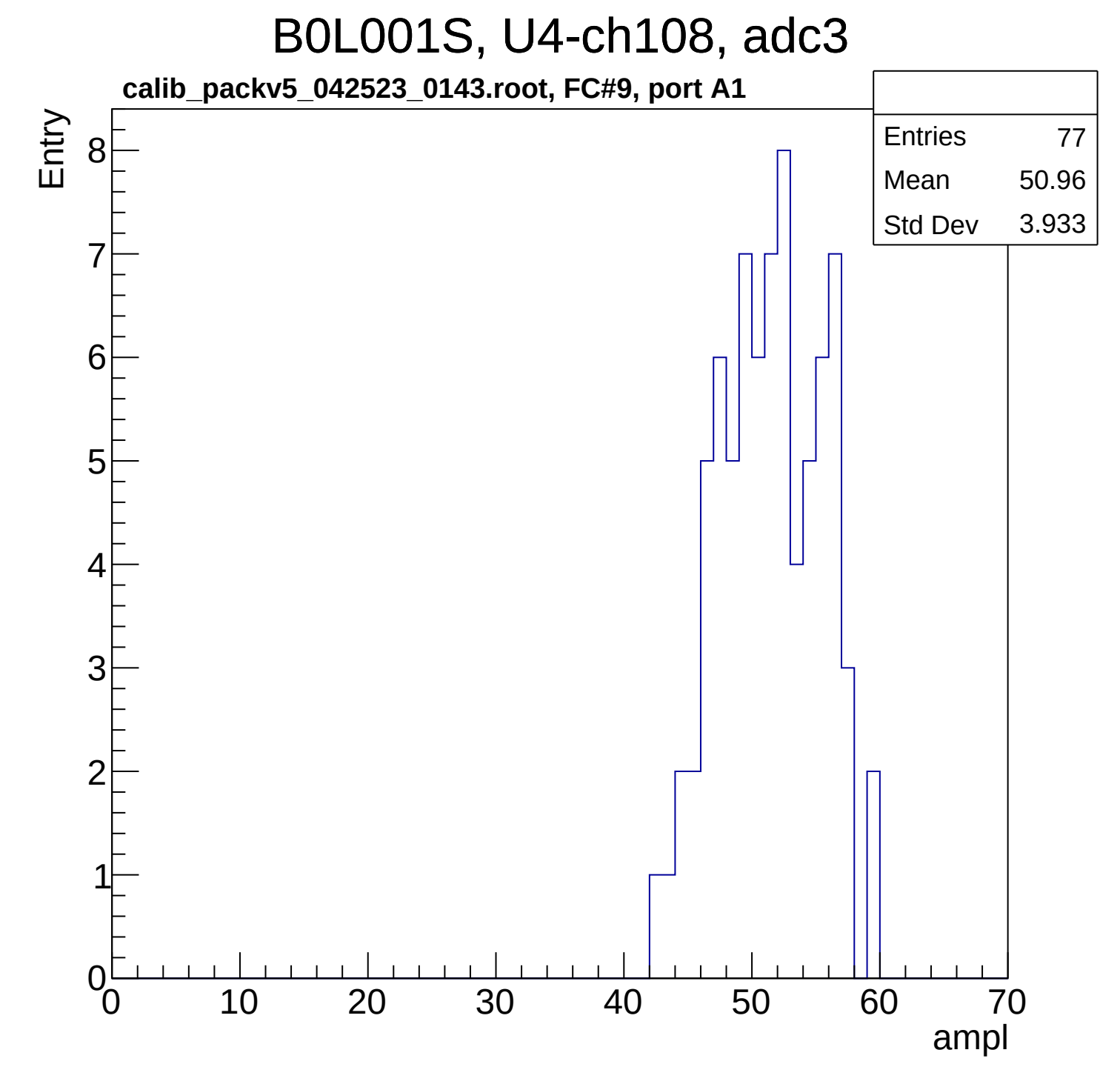
B0L001S, U4-ch108, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 50.96
Std Dev 3.933
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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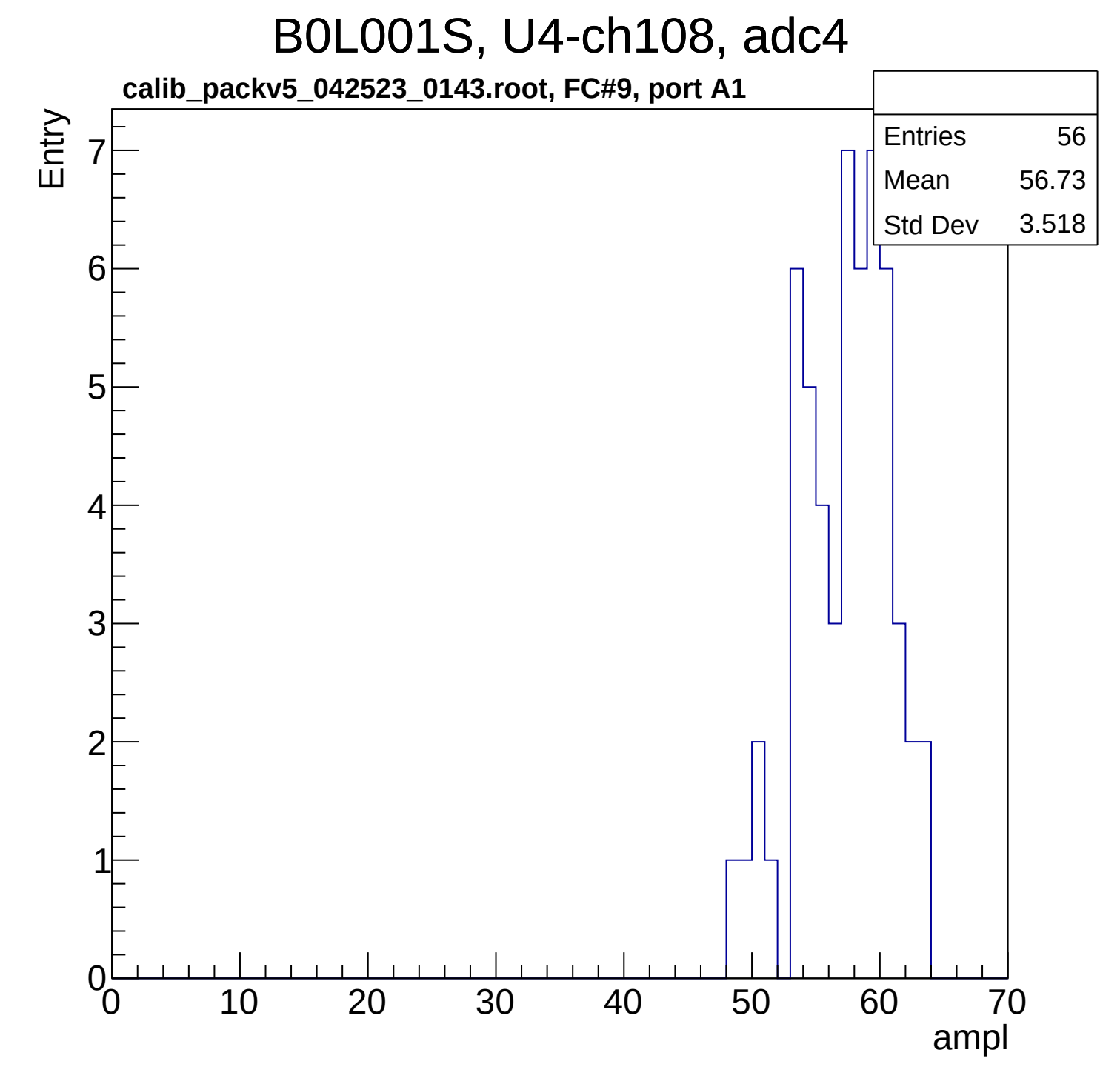
B0L001S, U4-ch108, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.73
Std Dev 3.518
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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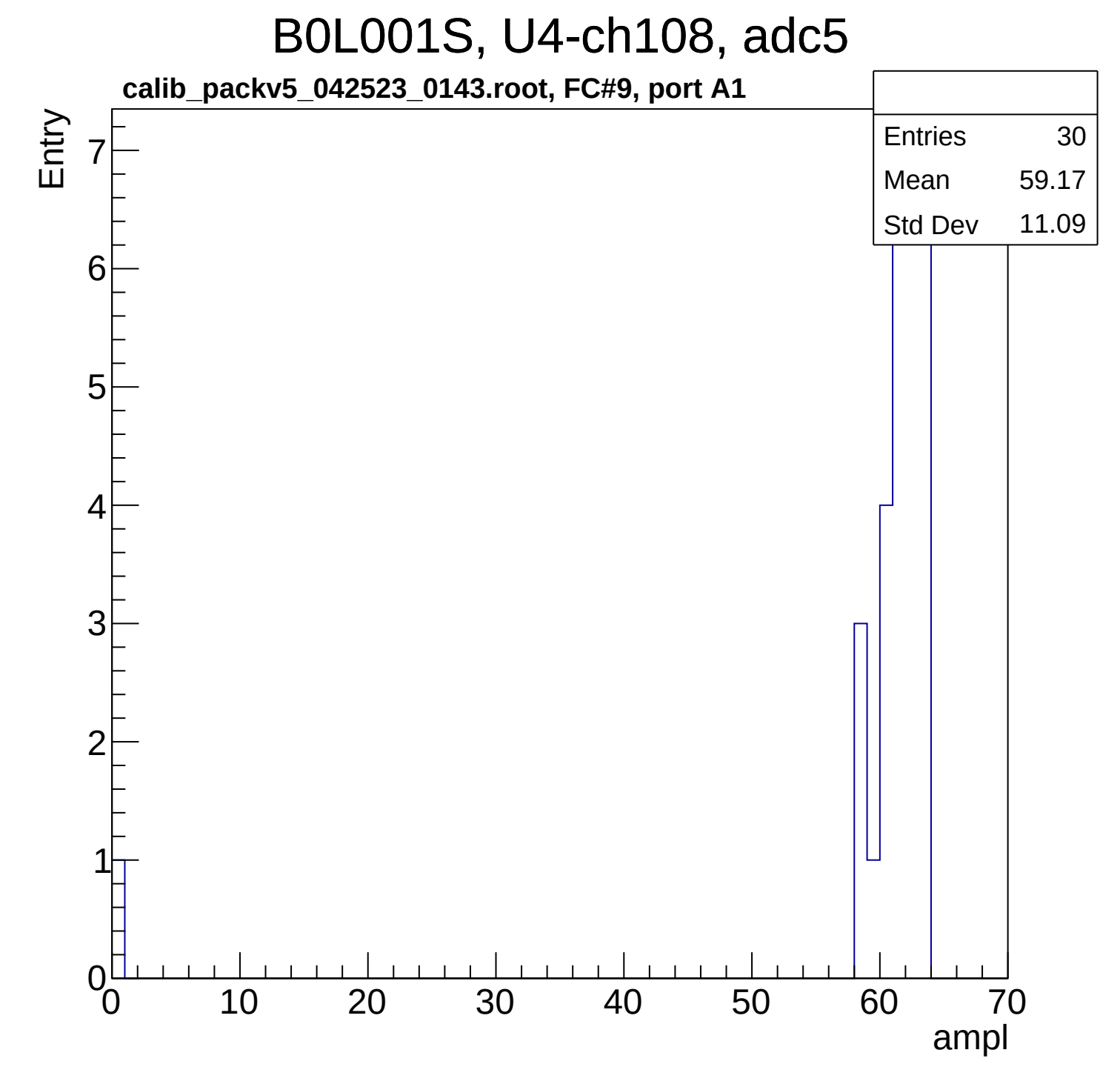
B0L001S, U4-ch108, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 59.17
Std Dev 11.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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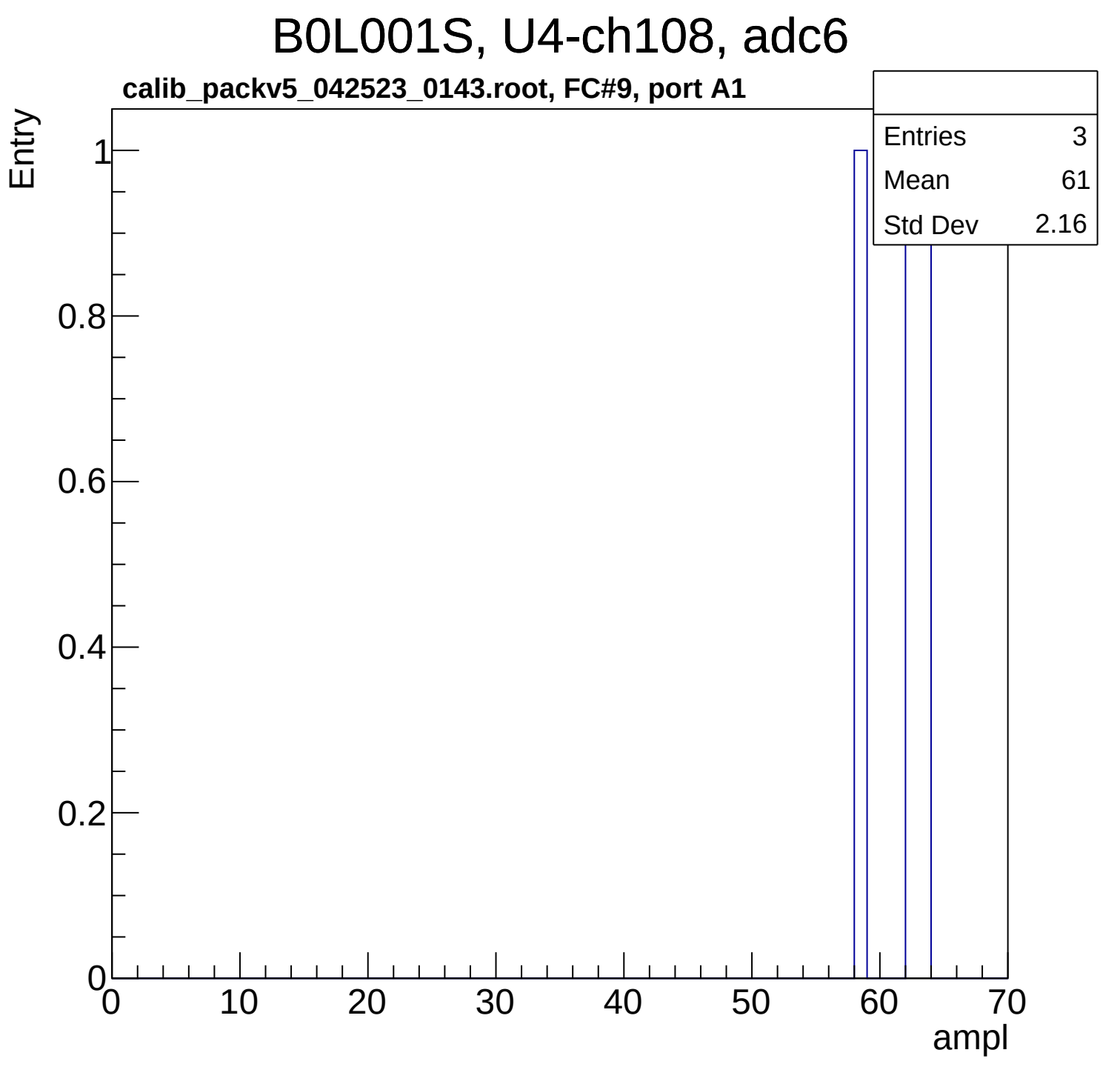
B0L001S, U4-ch108, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 2.16
Entry
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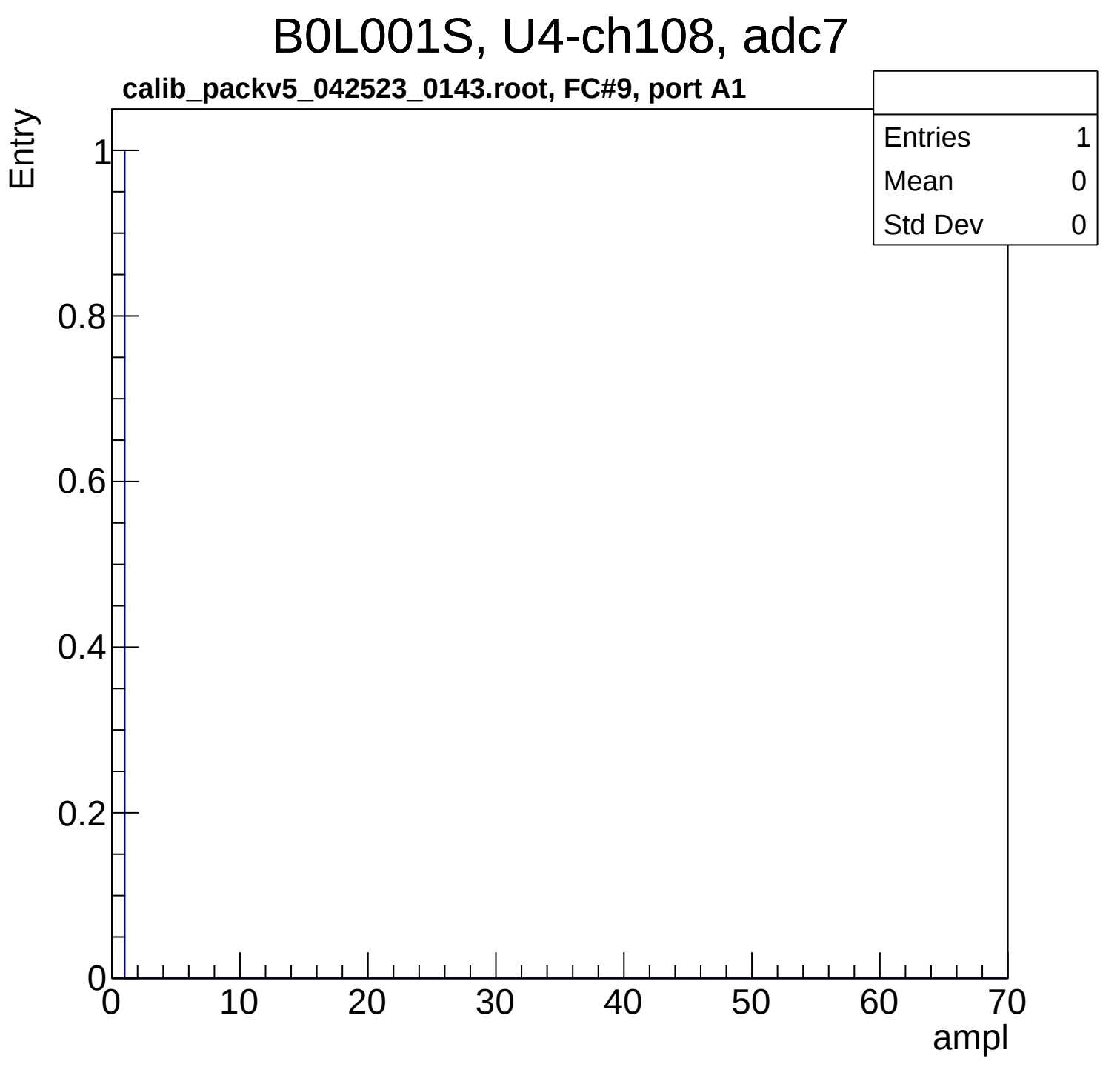
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch108, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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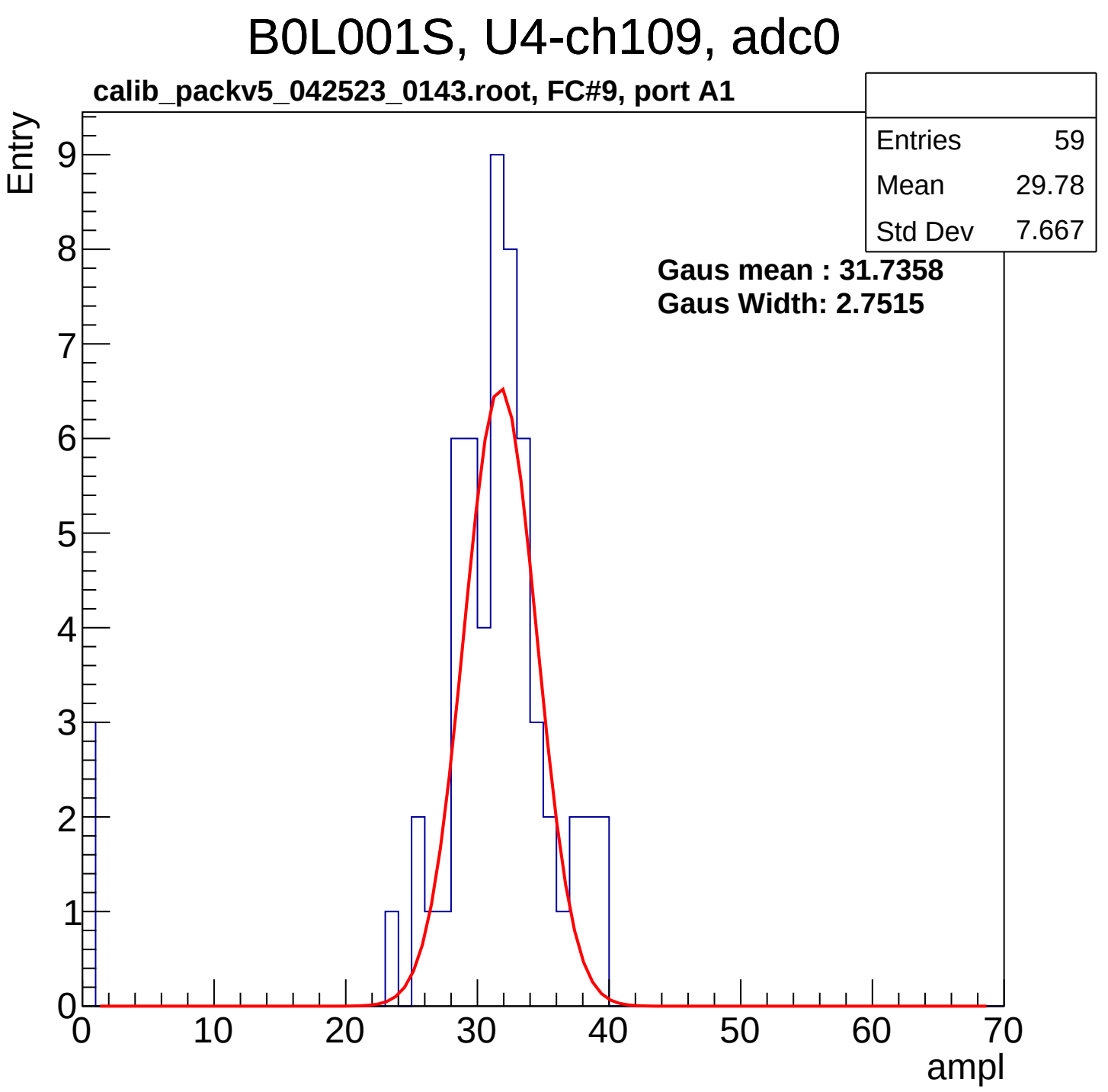
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch109, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.78
Std Dev 7.667
Gaus mean : 31.7358
Gaus Width: 2.7515
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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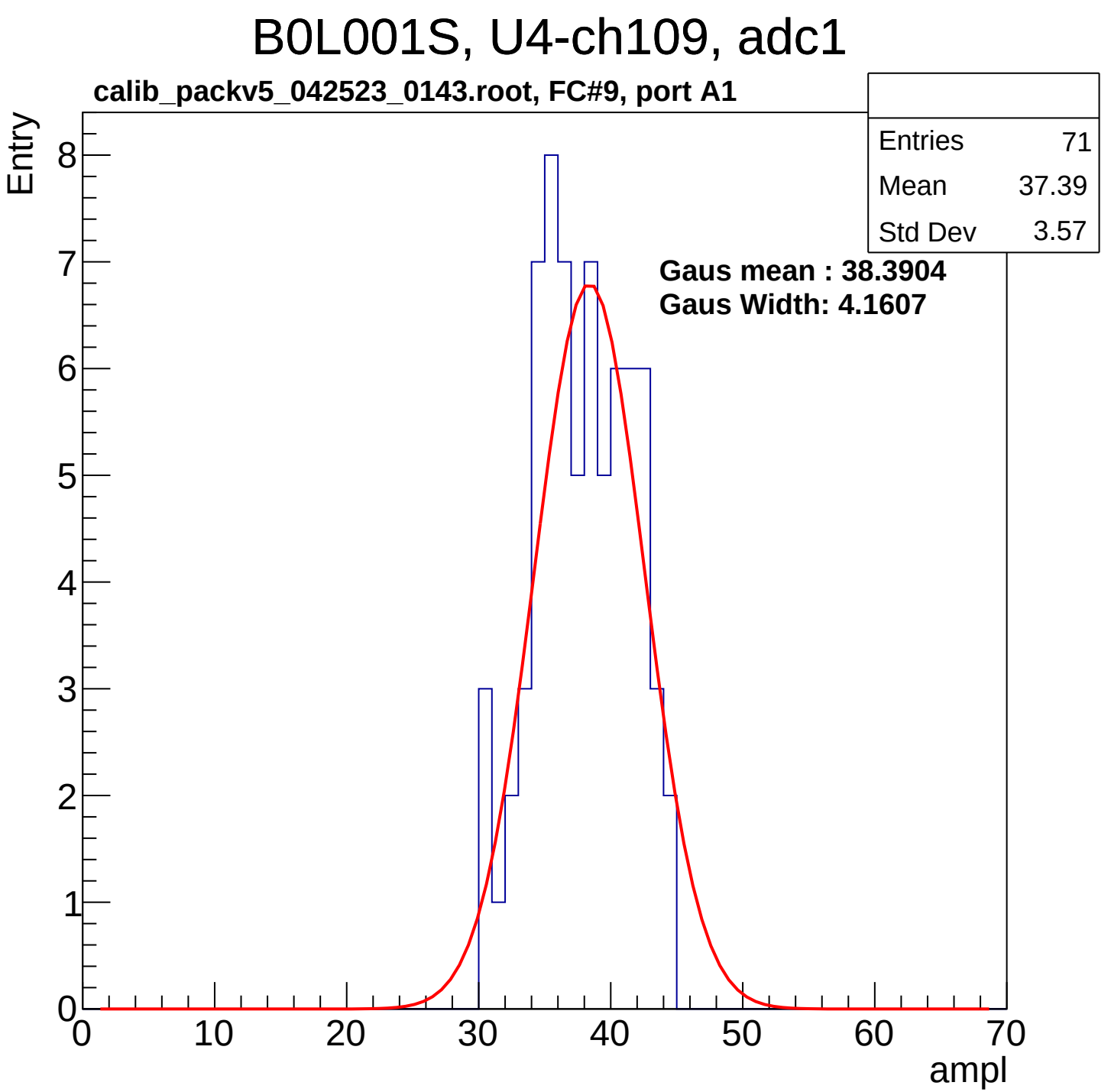
B0L001S, U4-ch109, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.39
Std Dev 3.57
Gaus mean : 38.3904
Gaus Width: 4.1607
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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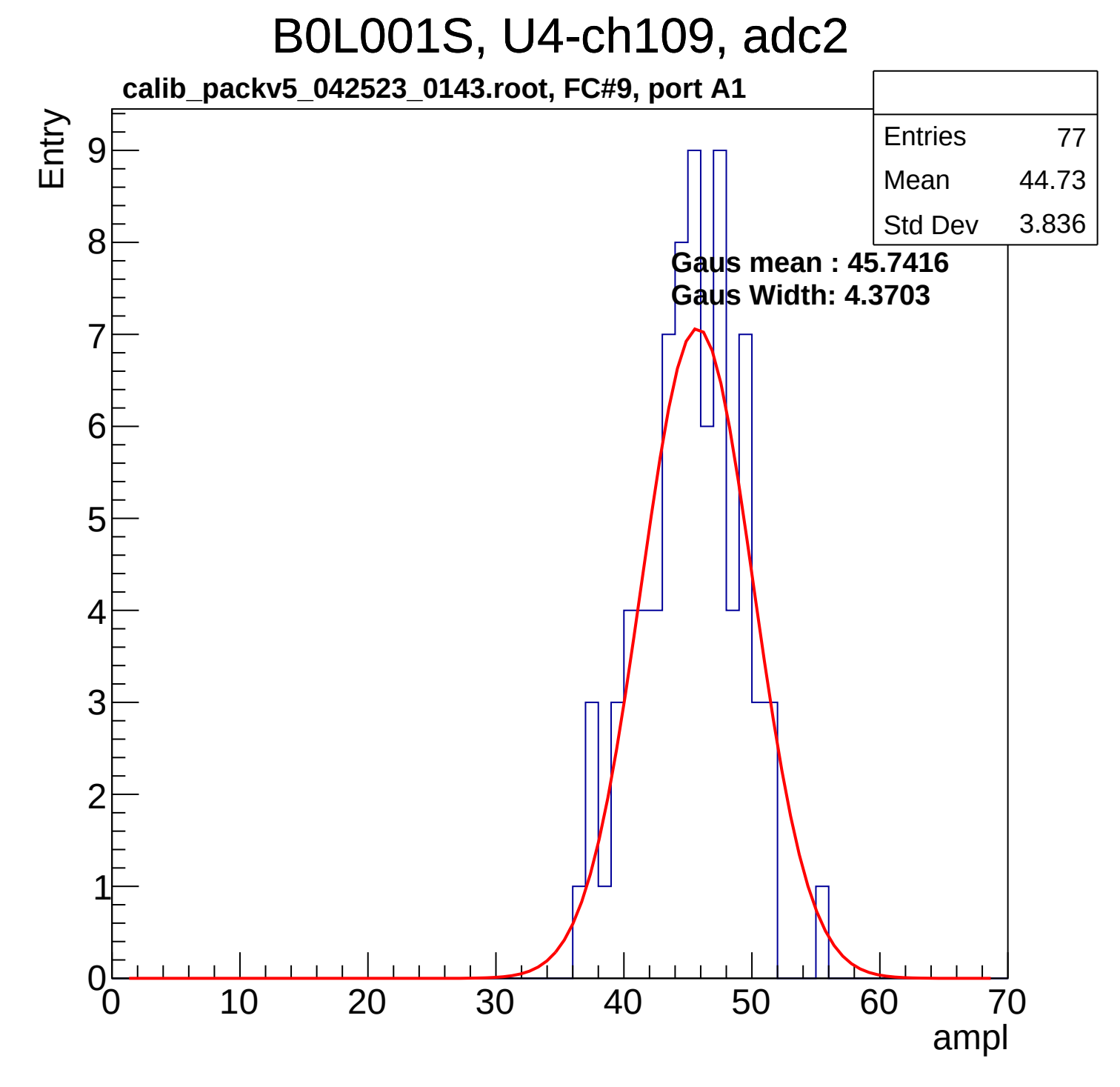
B0L001S, U4-ch109, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 44.73
Std Dev 3.836
Gaus mean : 45.7416
Gaus Width: 4.3703
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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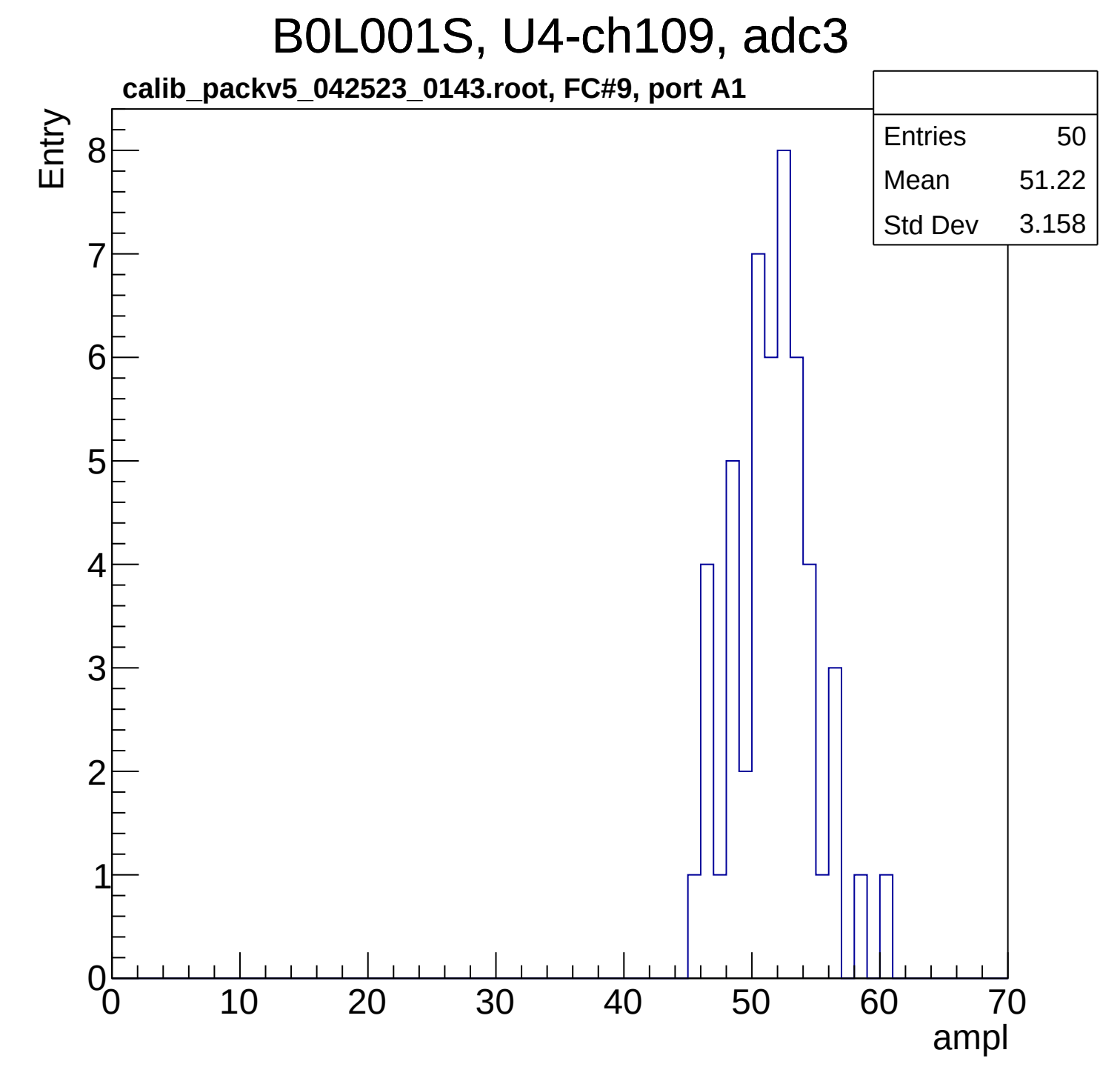
B0L001S, U4-ch109, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 51.22
Std Dev 3.158
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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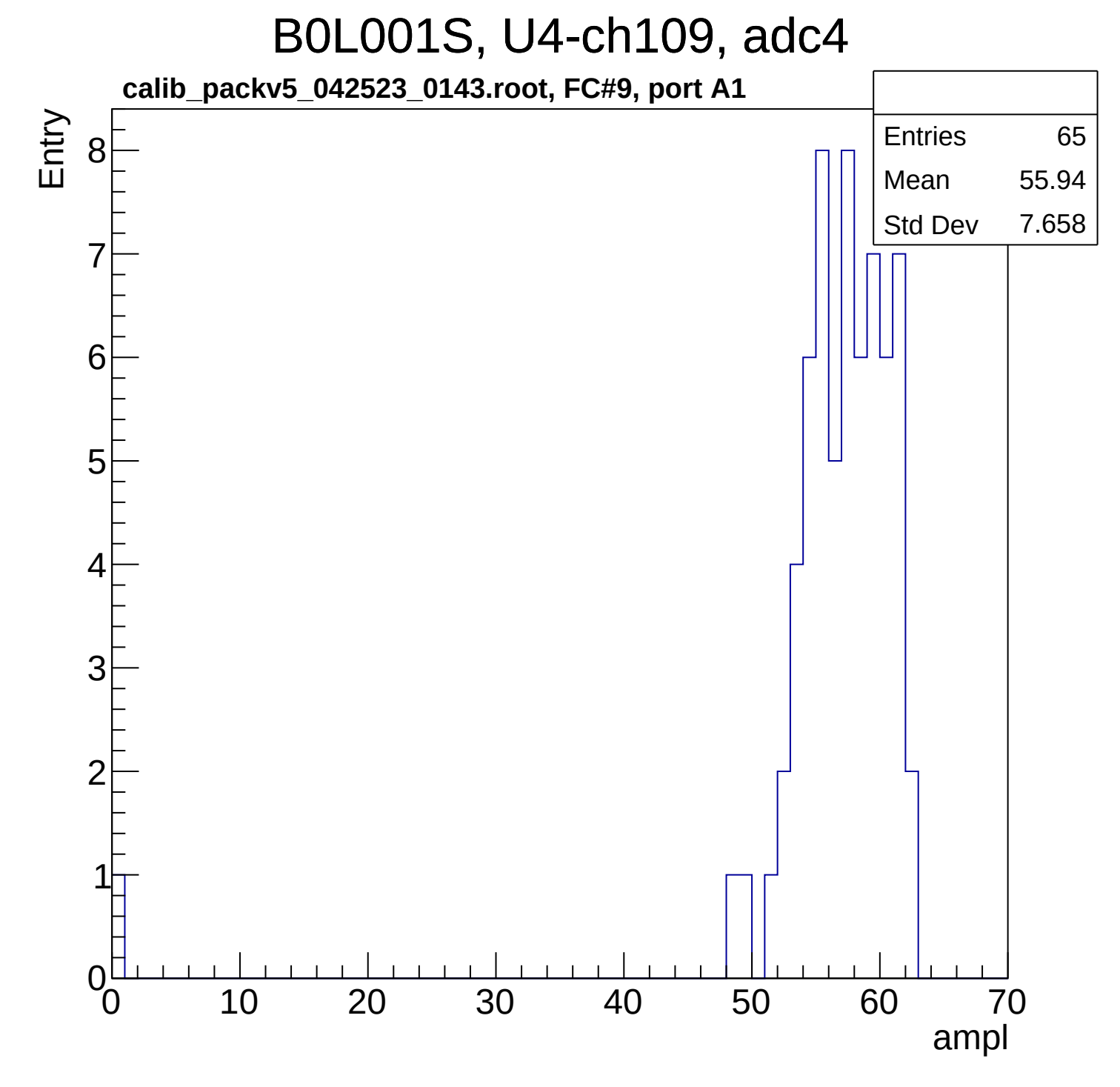
B0L001S, U4-ch109, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.94
Std Dev 7.658
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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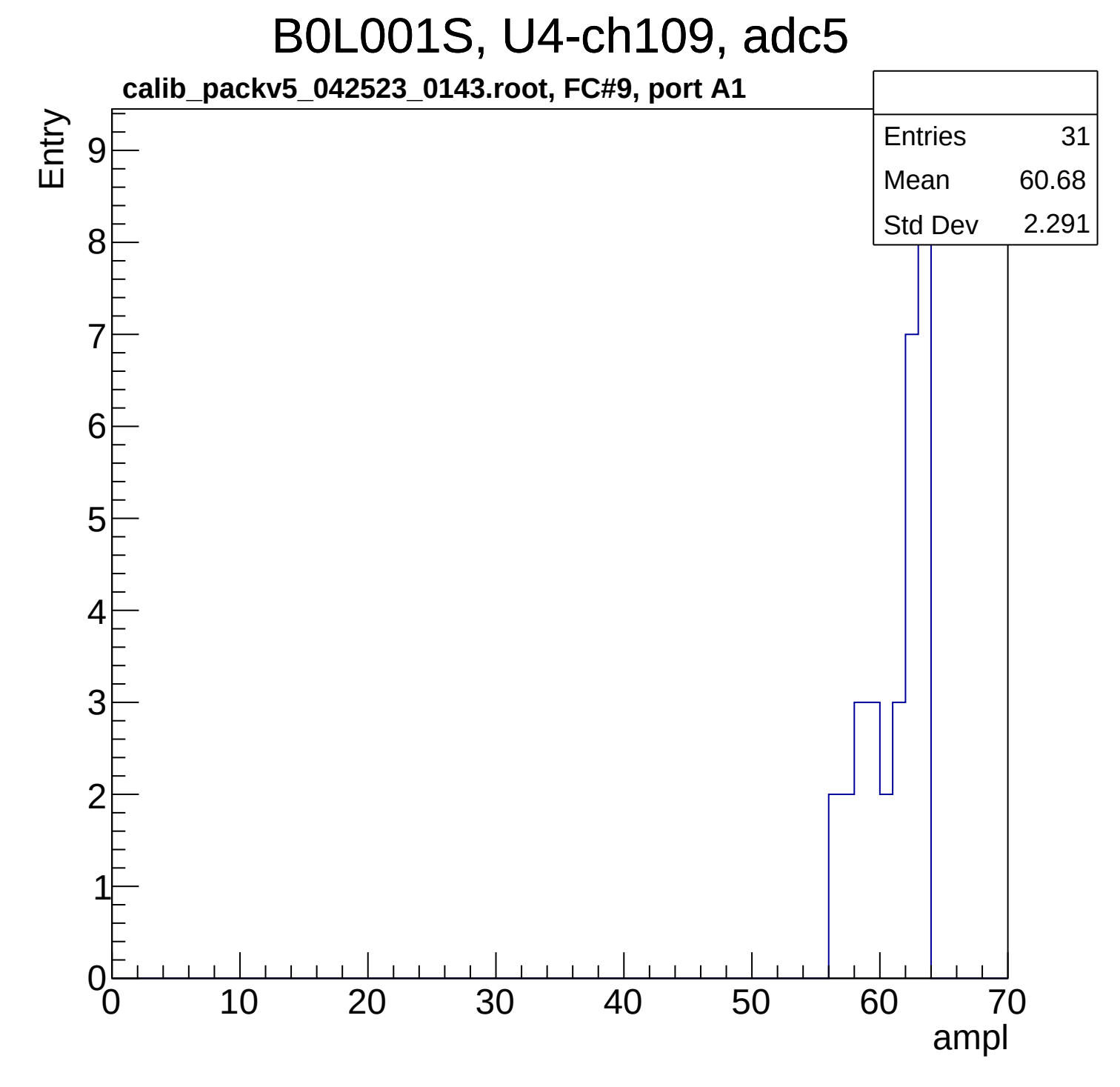
B0L001S, U4-ch109, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 60.68
Std Dev 2.291
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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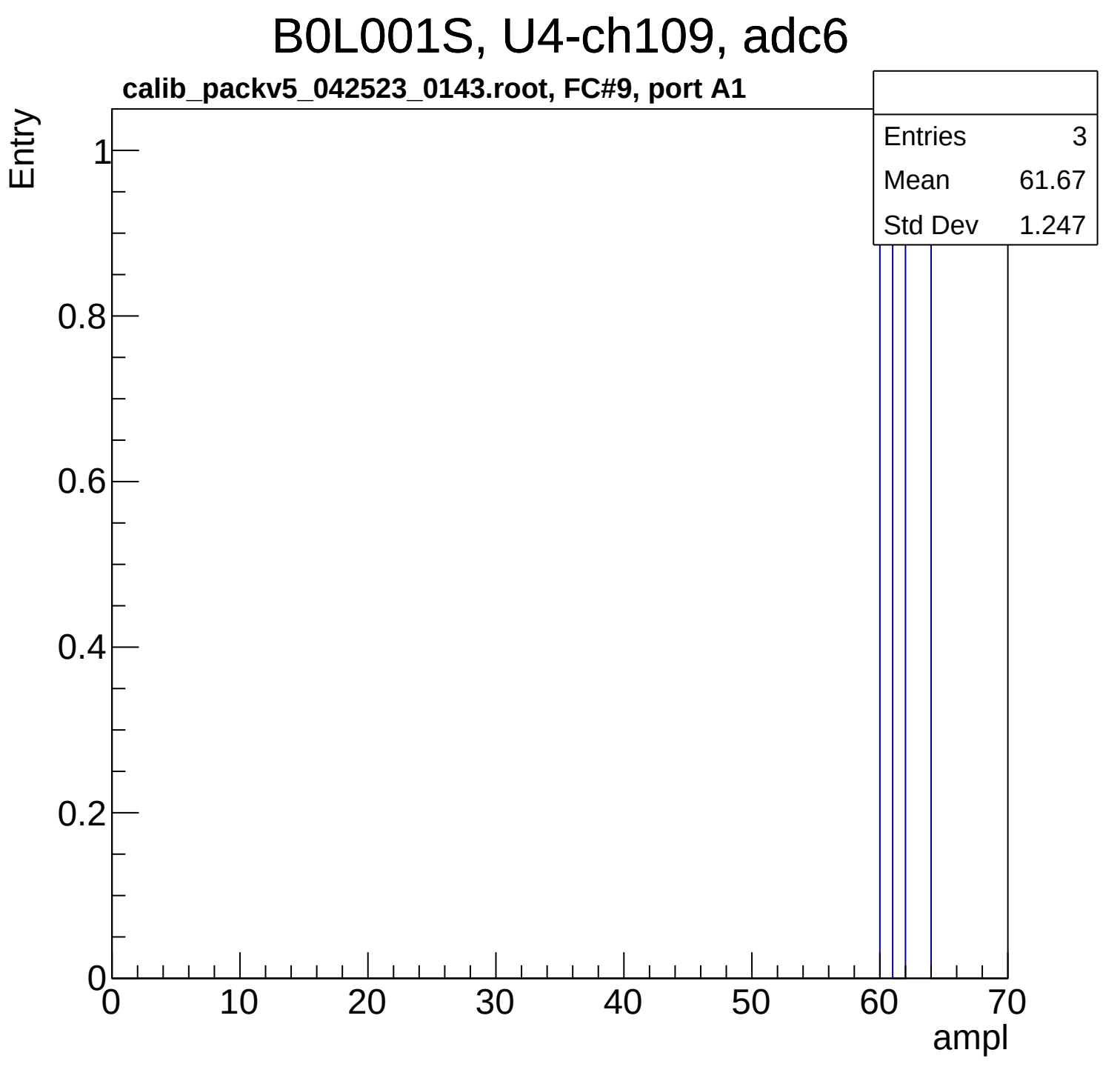
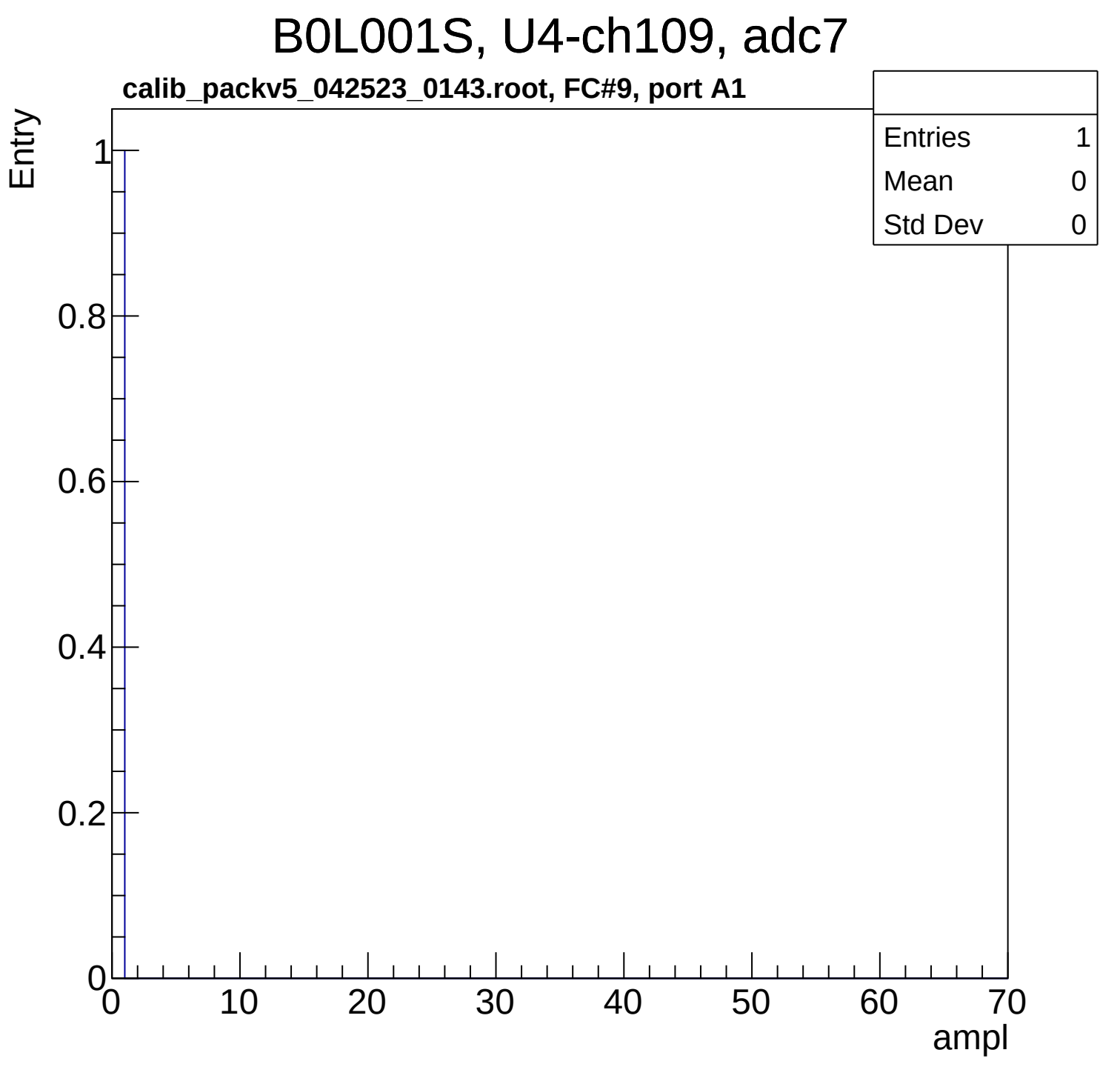
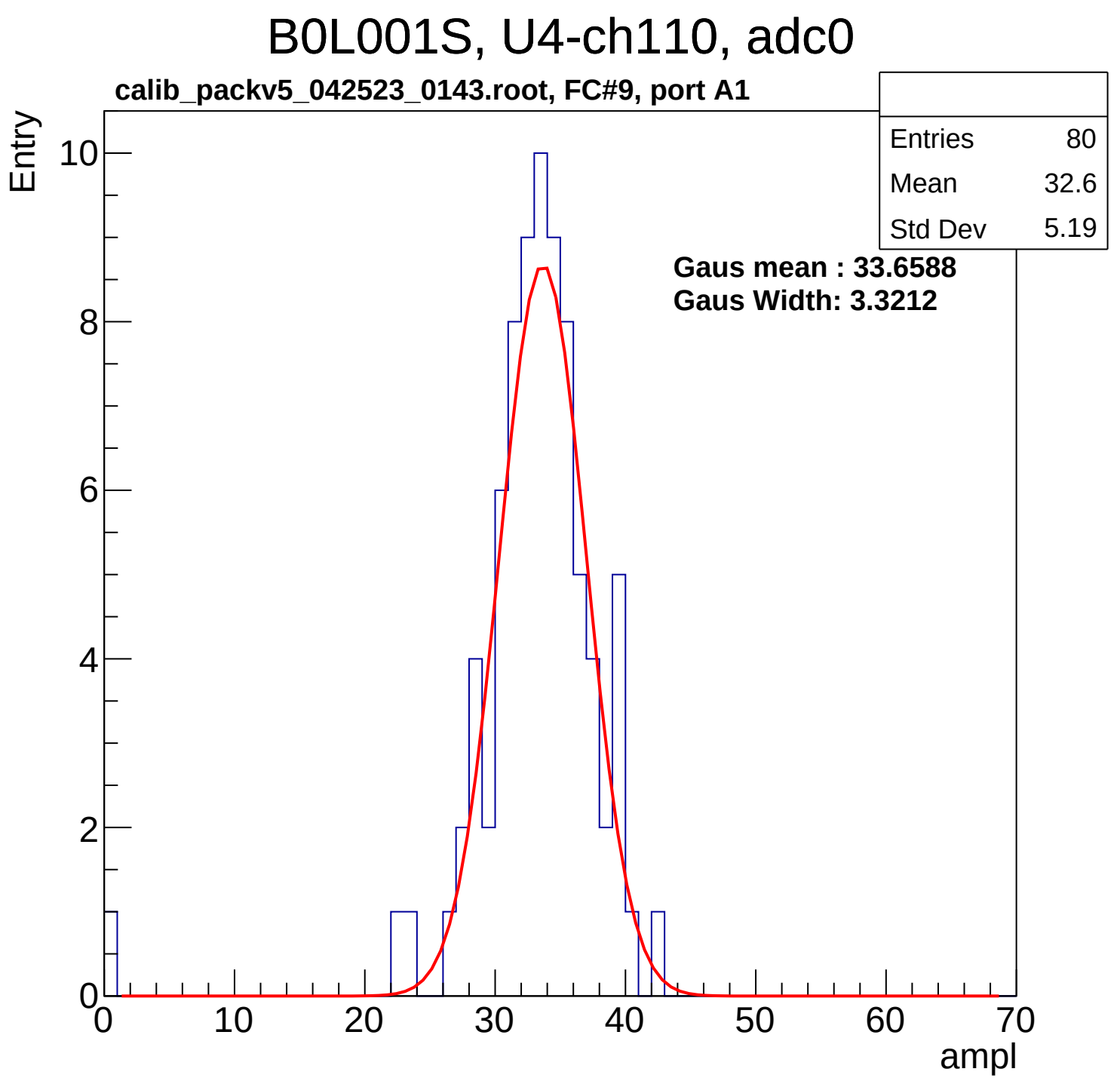

B0L001S, U4-ch109, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L001S, U4-ch109, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch110, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 32.6
Std Dev 5.19
Gaus mean : 33.6588
Gaus Width: 3.3212
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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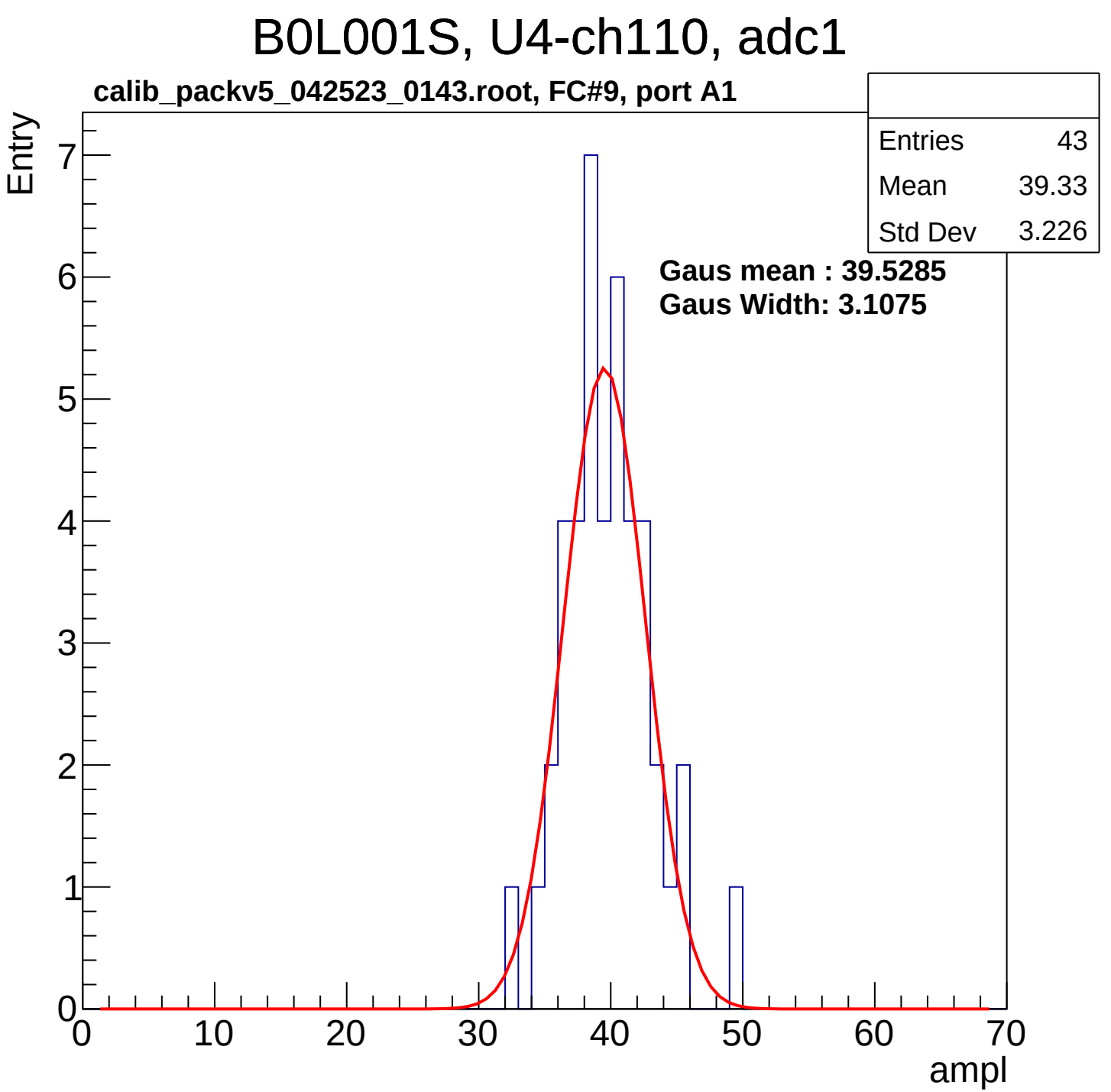
B0L001S, U4-ch110, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 39.33
Std Dev 3.226
Gaus mean : 39.5285
Gaus Width: 3.1075
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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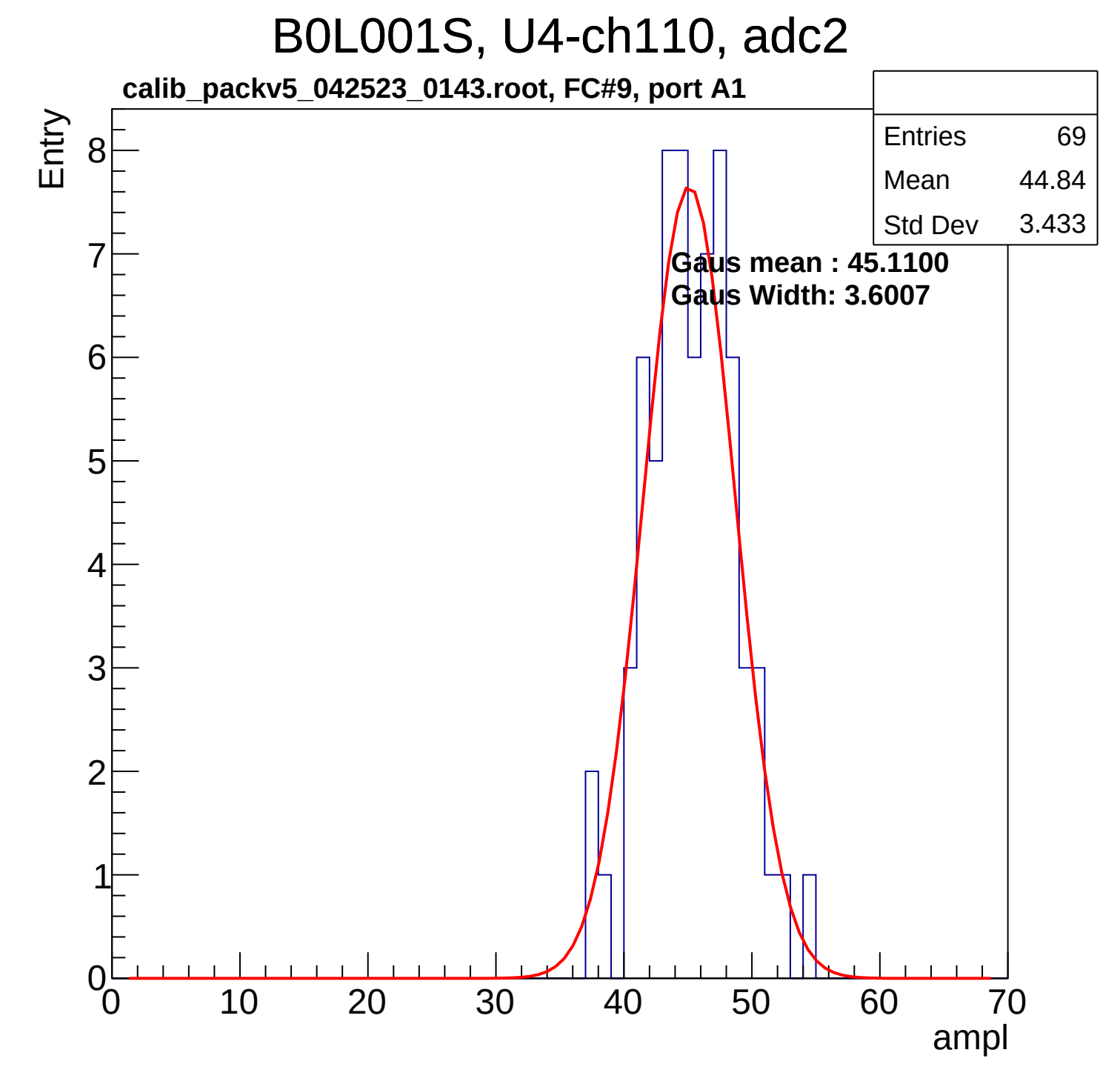
B0L001S, U4-ch110, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.84
Std Dev 3.433
Gaus mean : 45.1100
Gaus Width: 3.6007
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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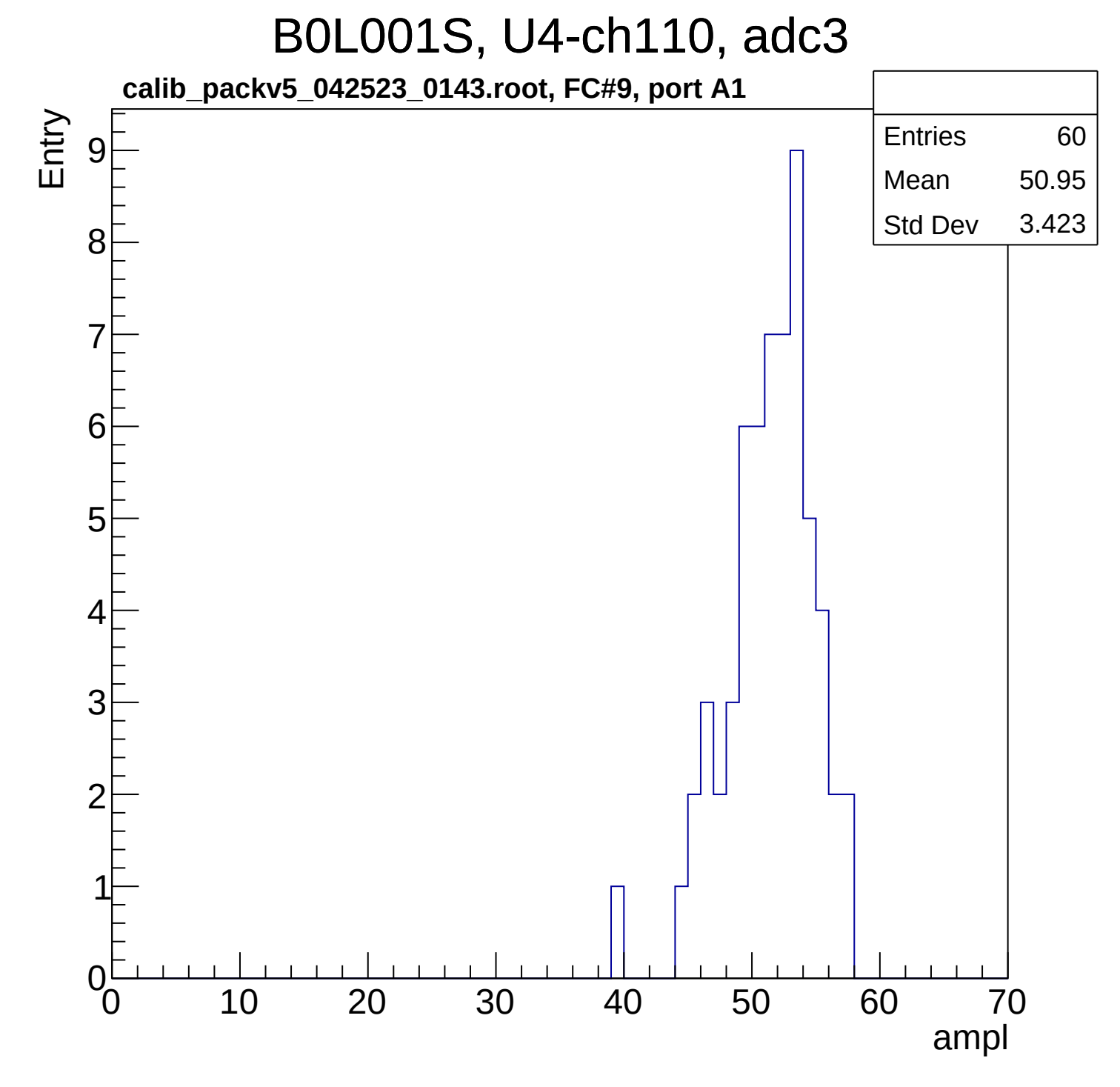
B0L001S, U4-ch110, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.95
Std Dev 3.423
Entry
ampl
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0
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60
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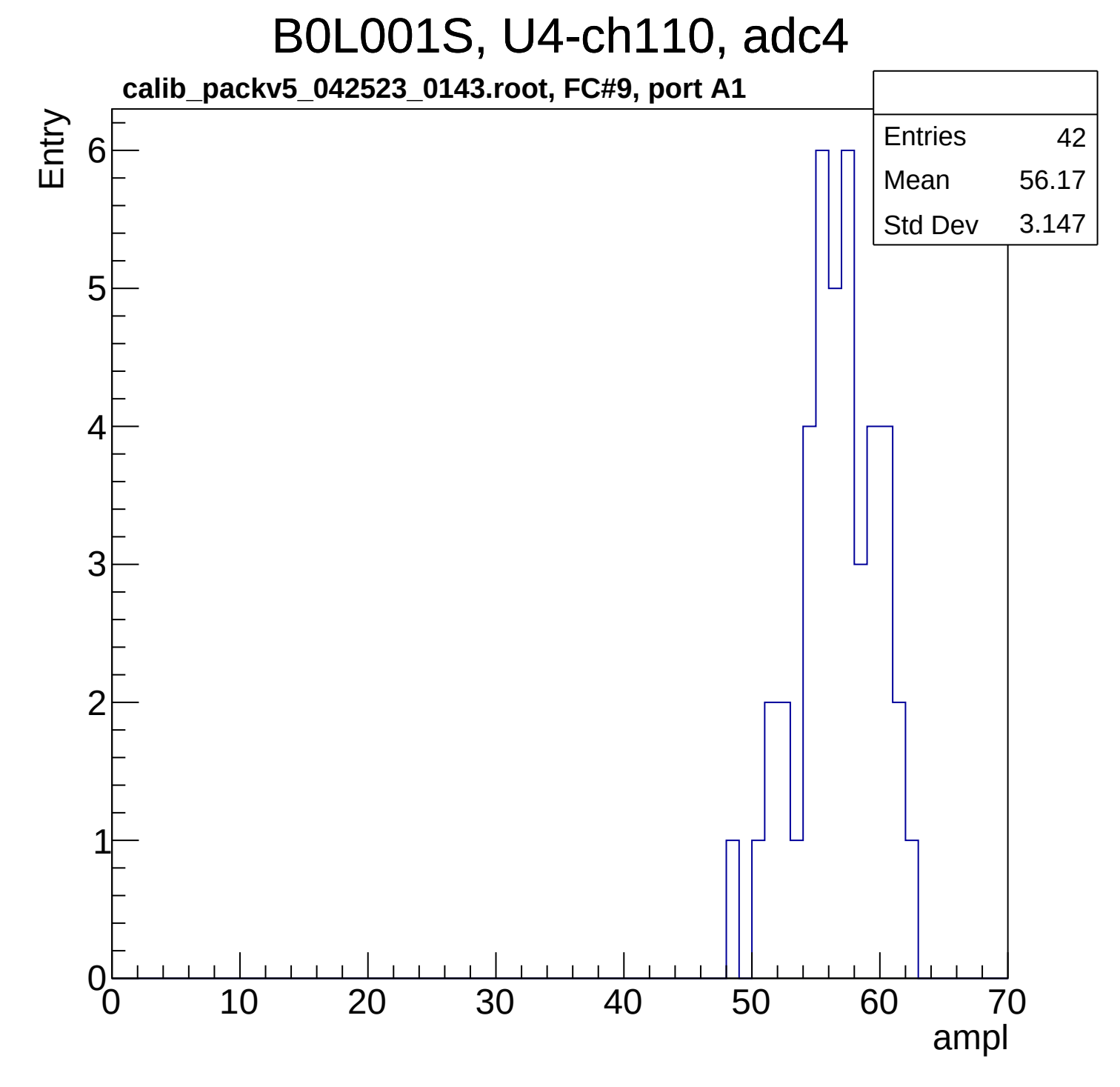
B0L001S, U4-ch110, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 56.17
Std Dev 3.147
Entry
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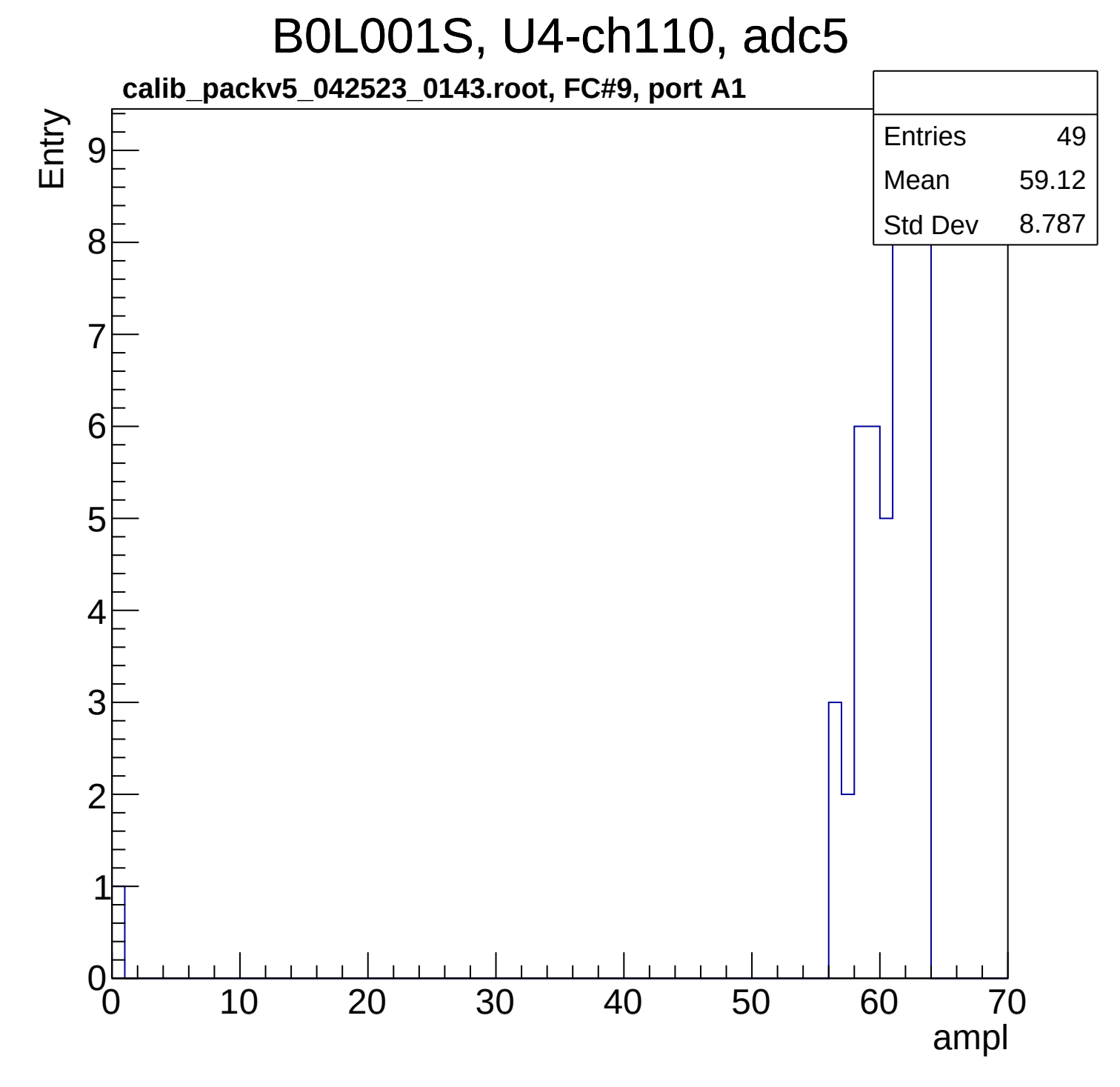
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch110, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 59.12
Std Dev 8.787
Entry
ampl
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4
5
6
7
8
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0
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40
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60
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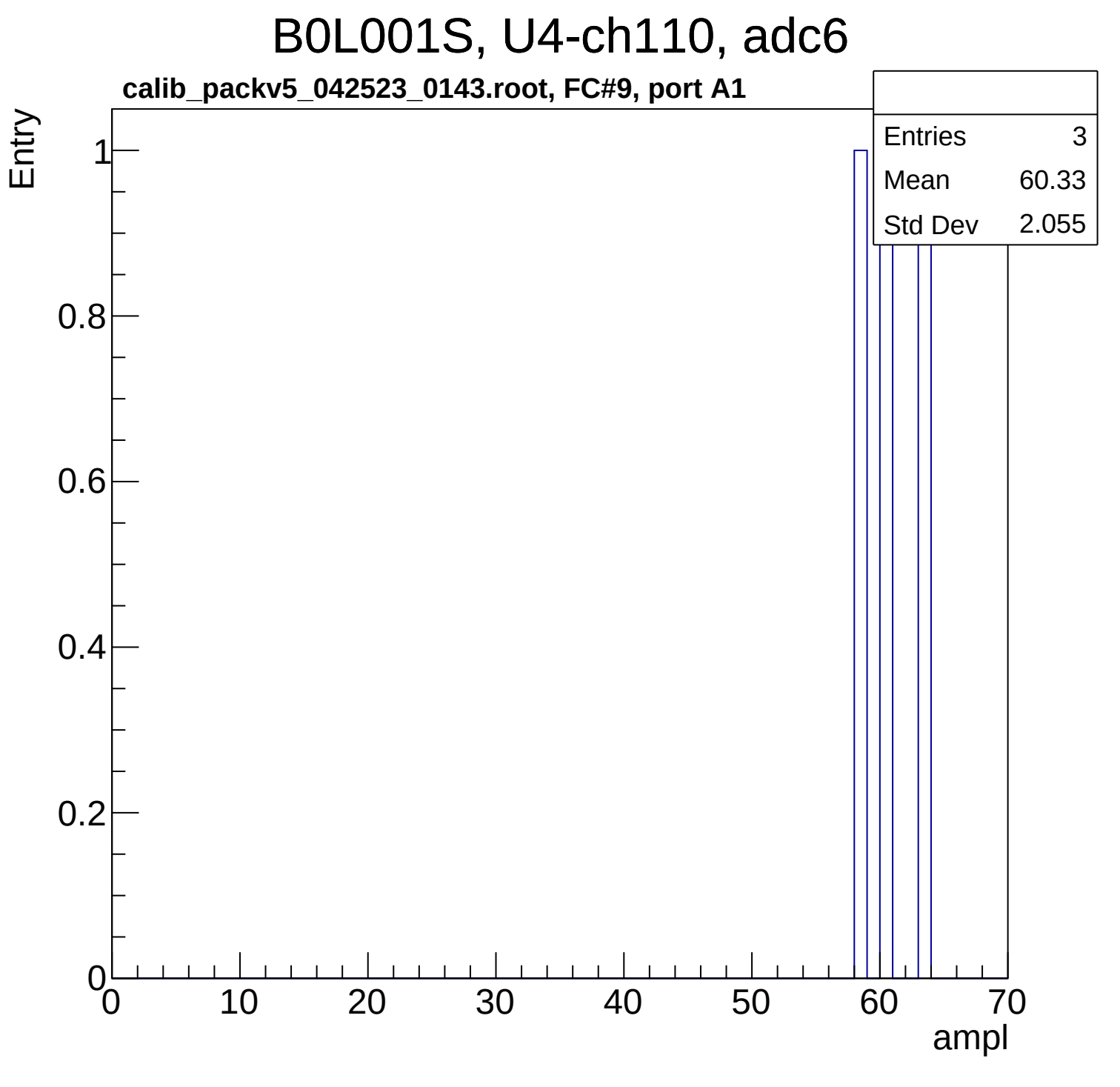
B0L001S, U4-ch110, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 60.33
Std Dev 2.055
Entry
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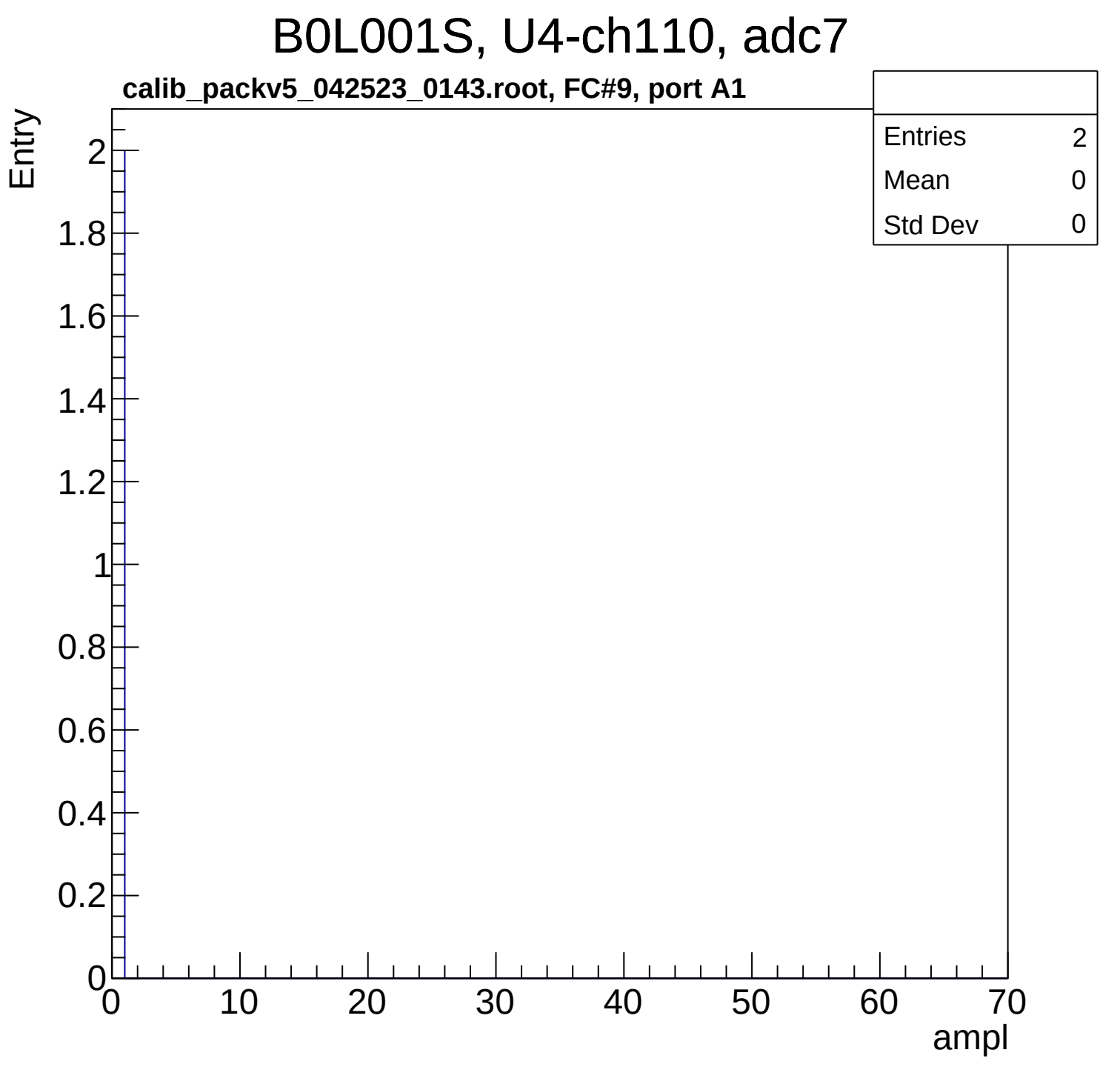
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch110, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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10
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50
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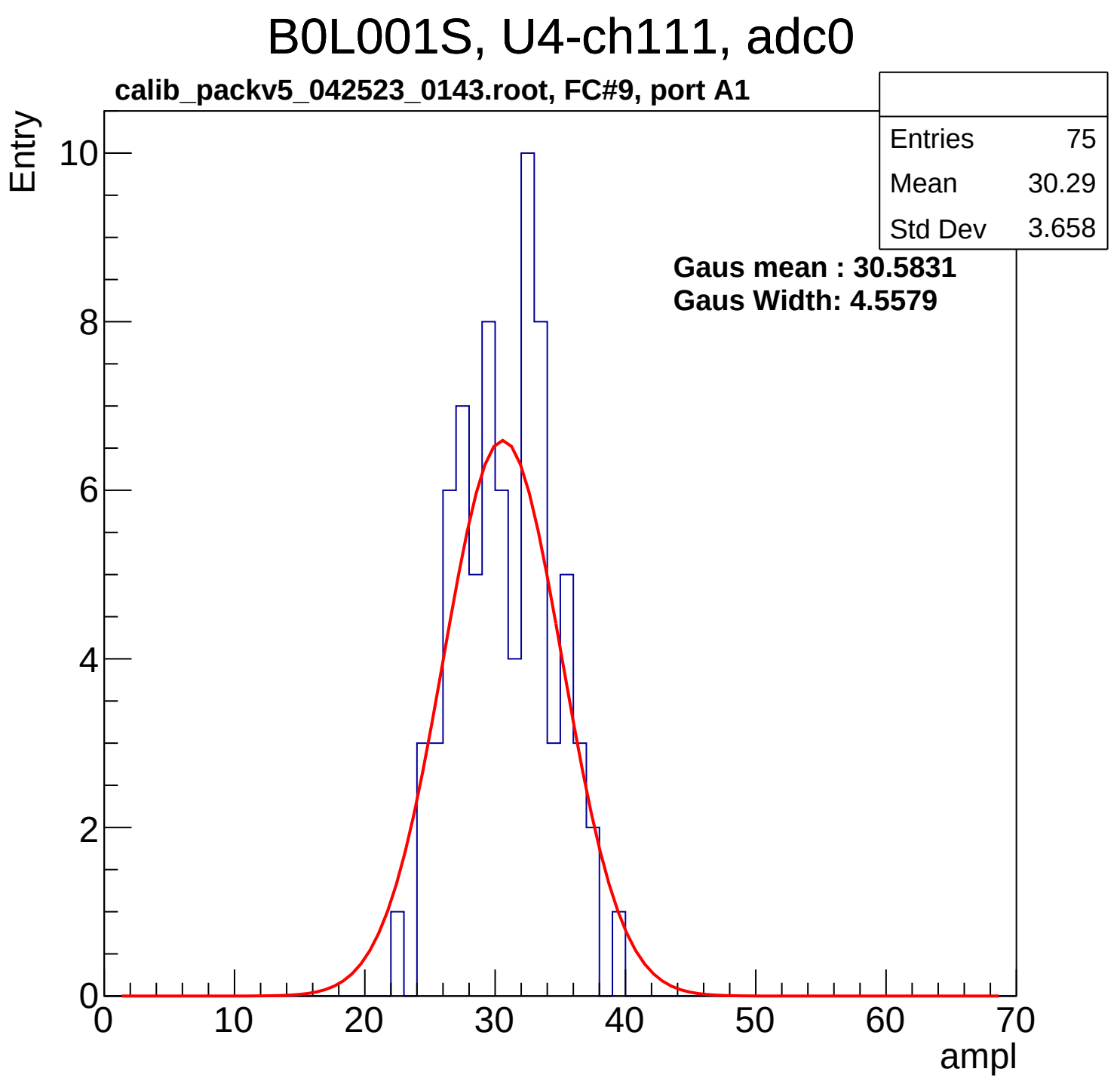
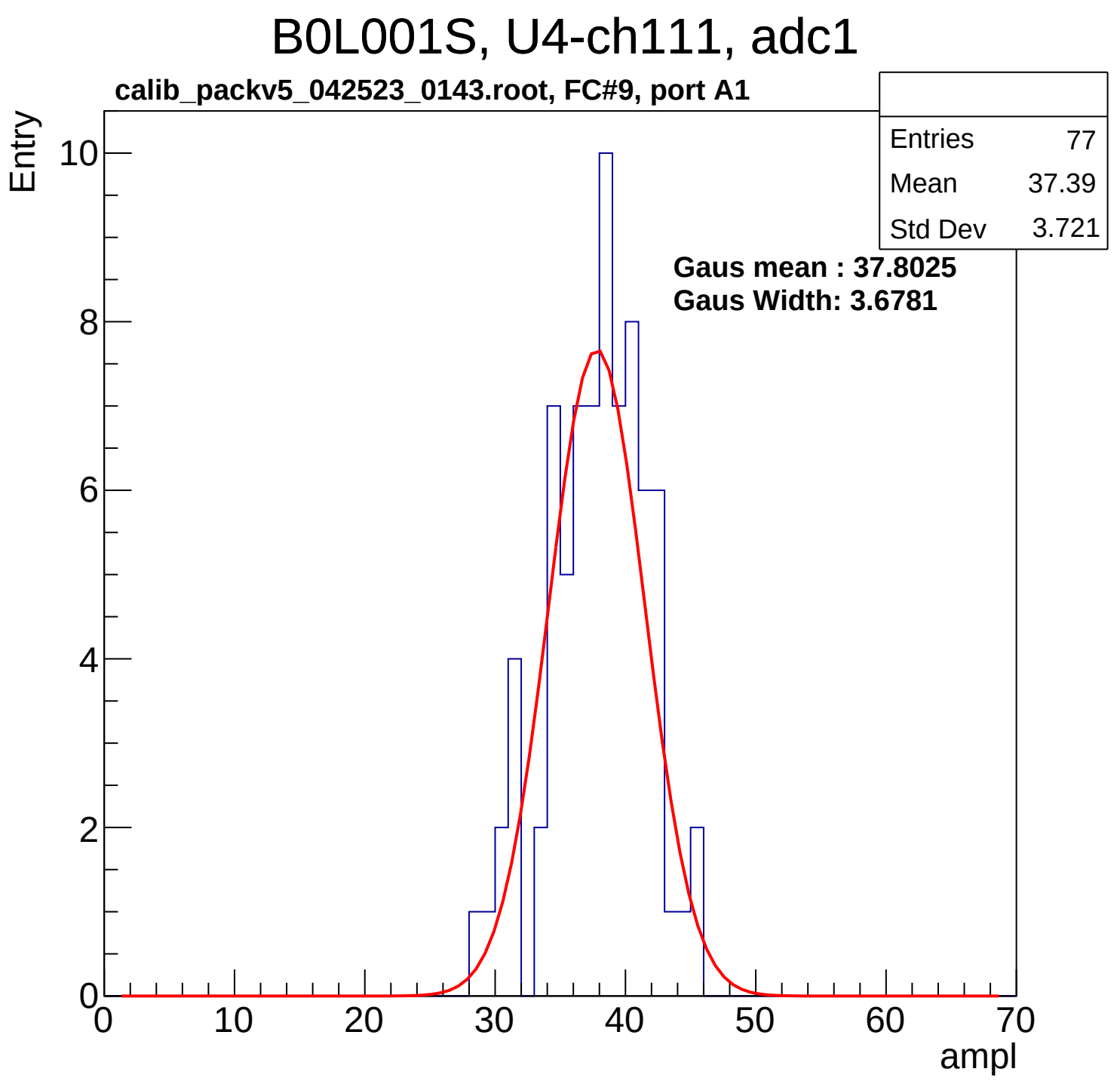
70
ampl


B0L001S, U4-ch111, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.29
Std Dev 3.658
Gaus mean : 30.5831
Gaus Width: 4.5579
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch111, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.39
Std Dev 3.721
Gaus mean : 37.8025
Gaus Width: 3.6781
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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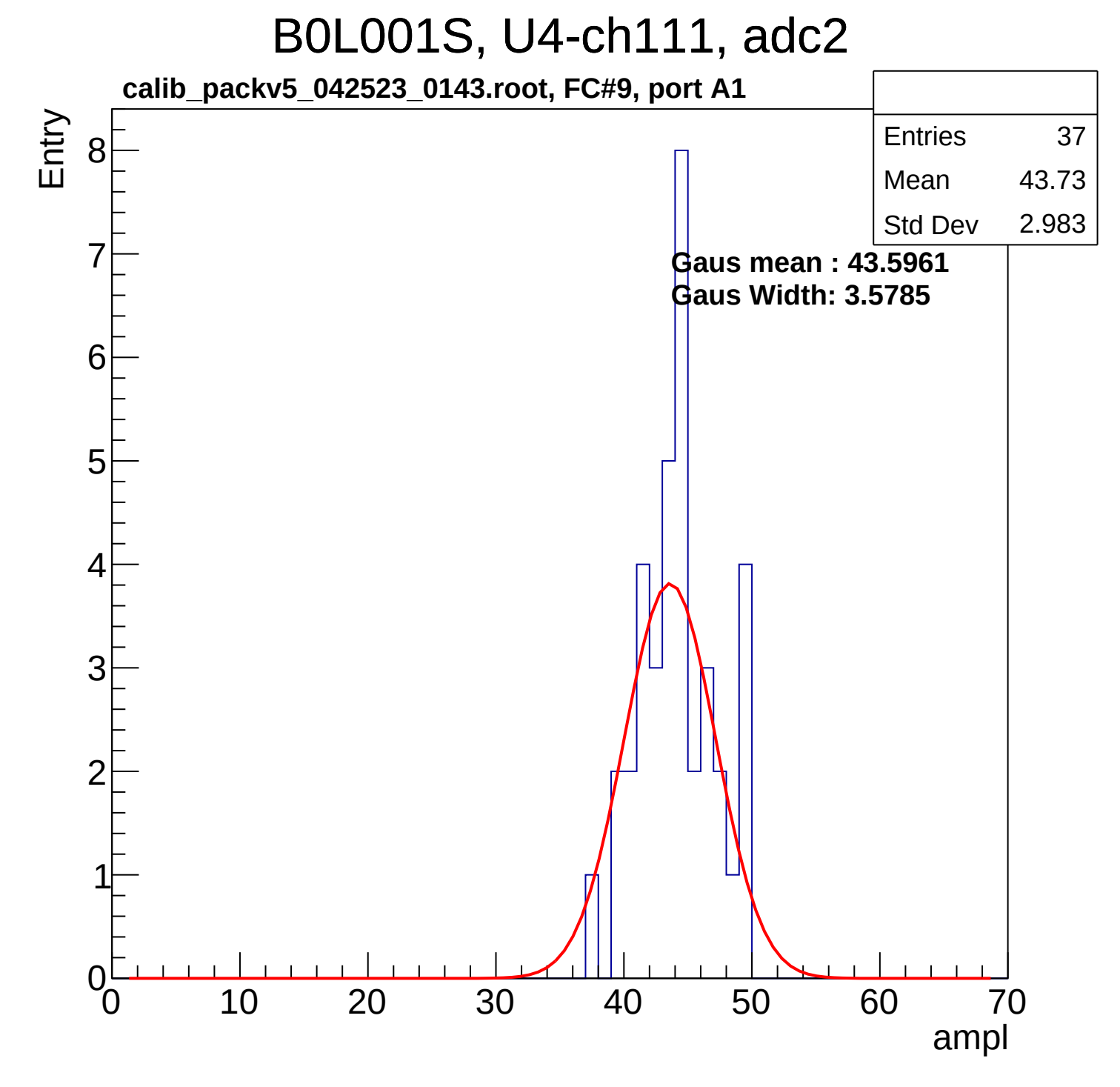
B0L001S, U4-ch111, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 43.73
Std Dev 2.983
Gaus mean : 43.5961
Gaus Width: 3.5785
Entry
ampl
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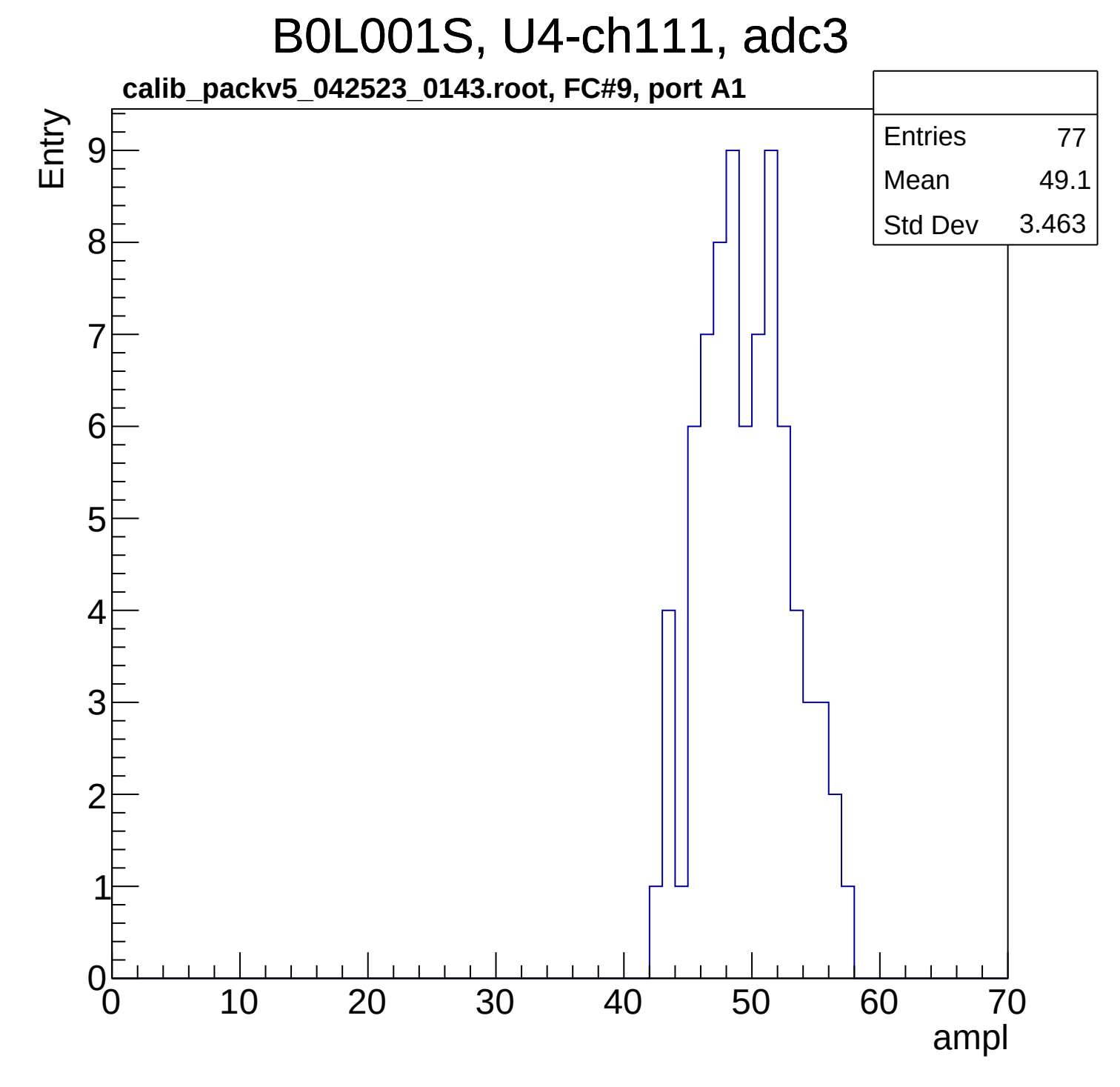
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch111, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 49.1
Std Dev 3.463
Entry
ampl
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3
4
5
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7
8
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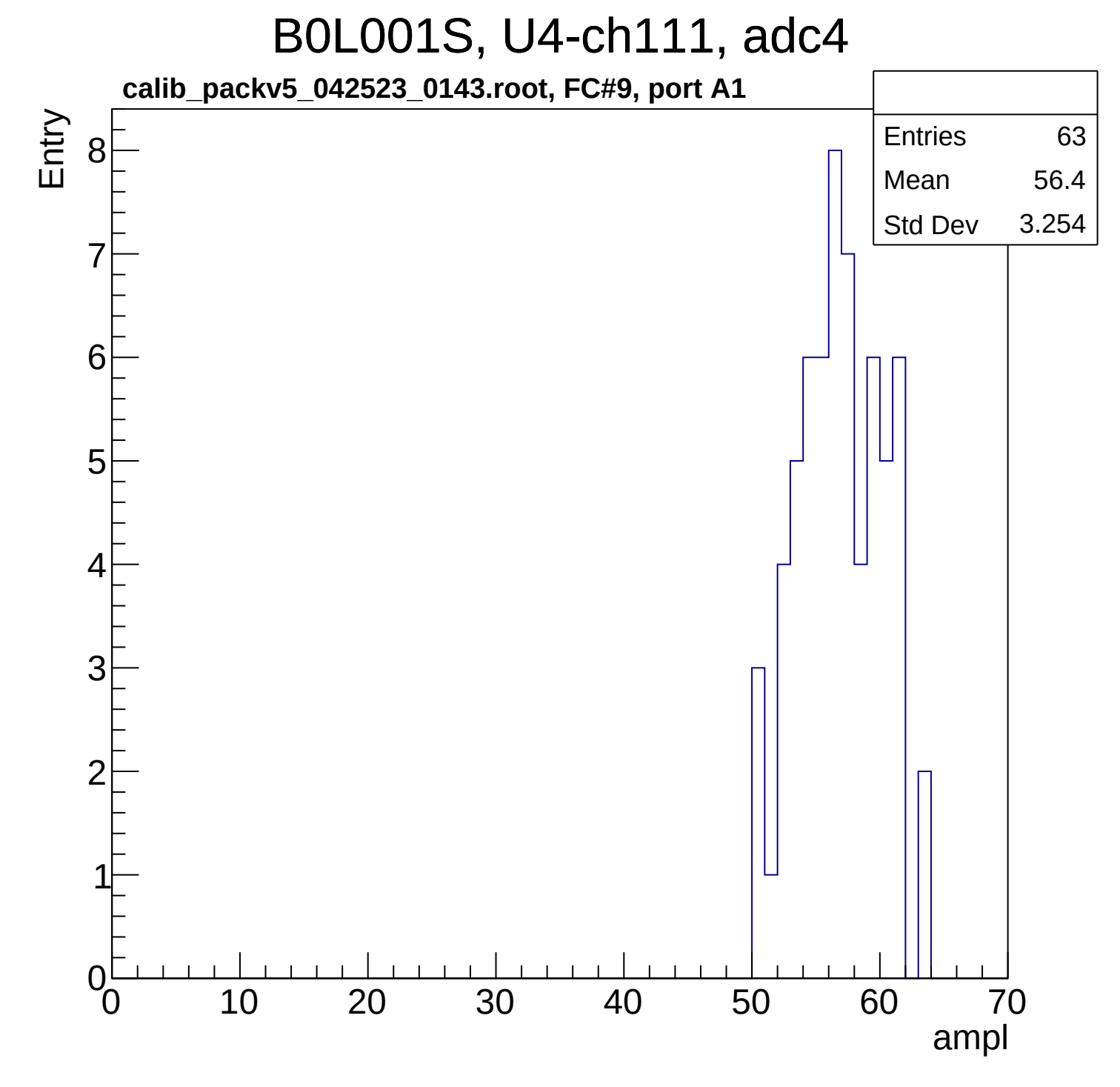
B0L001S, U4-ch111, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.4
Std Dev 3.254
Entry
ampl
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4
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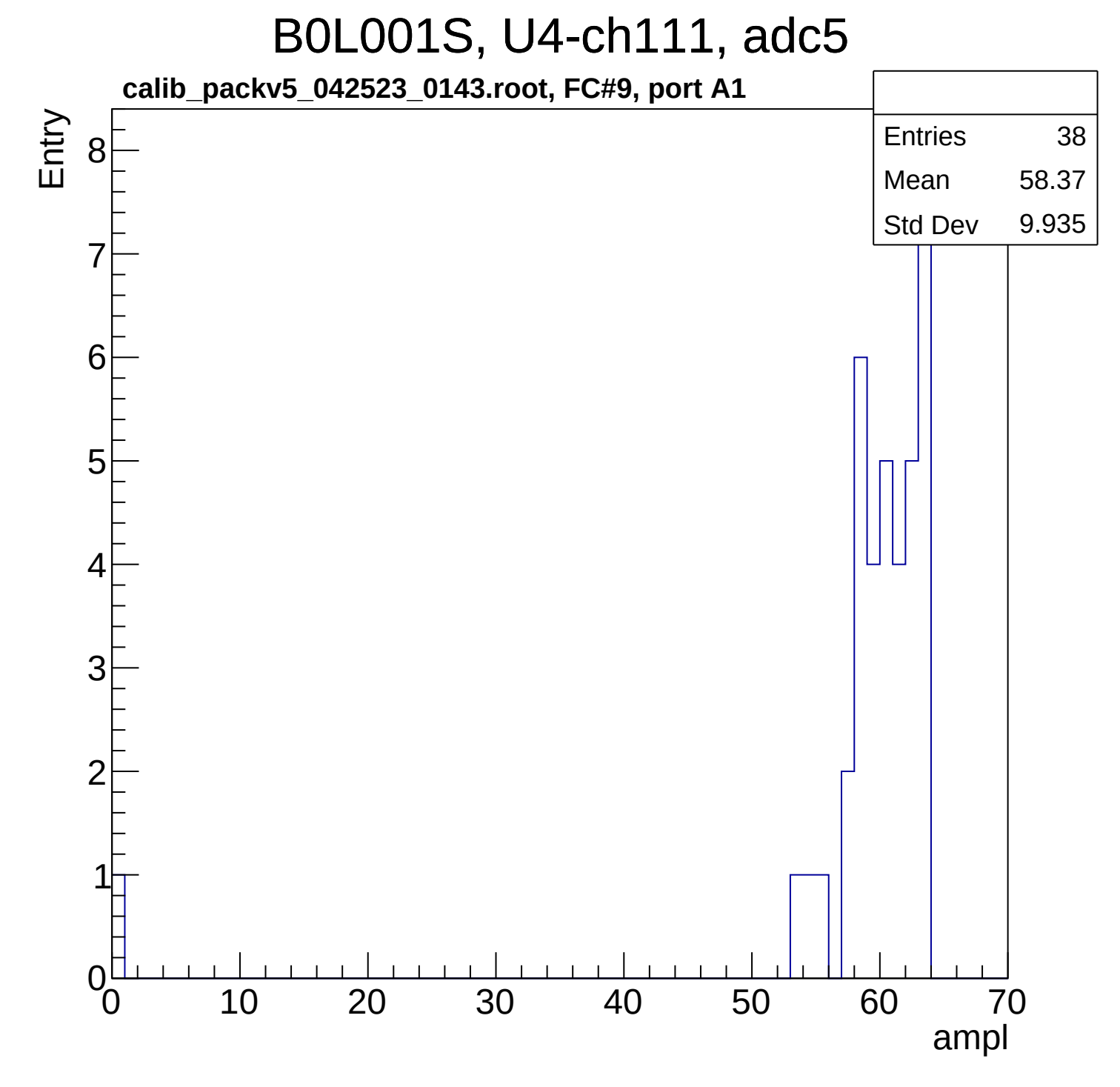
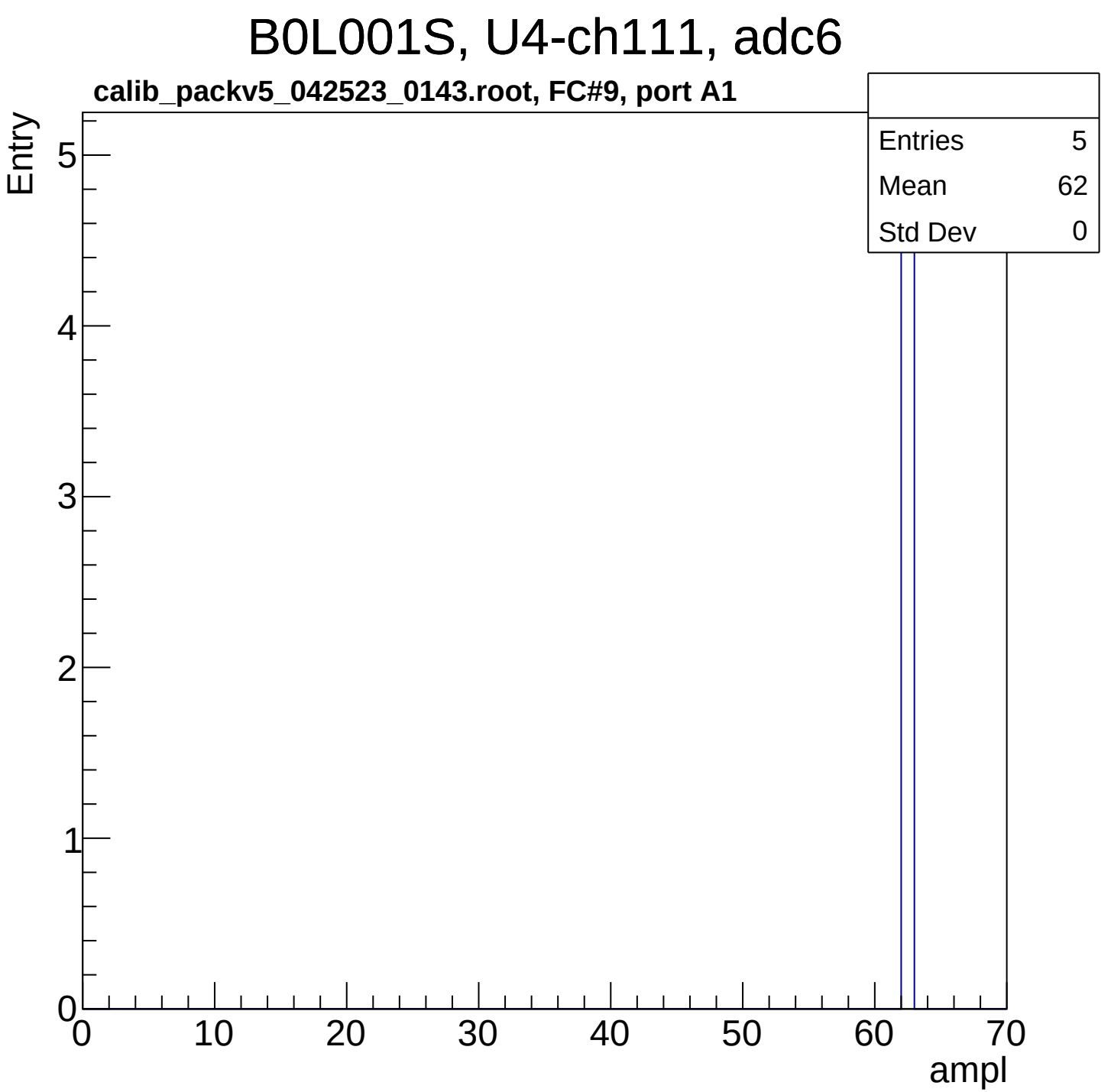

B0L001S, U4-ch111, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.37
Std Dev 9.935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch111, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
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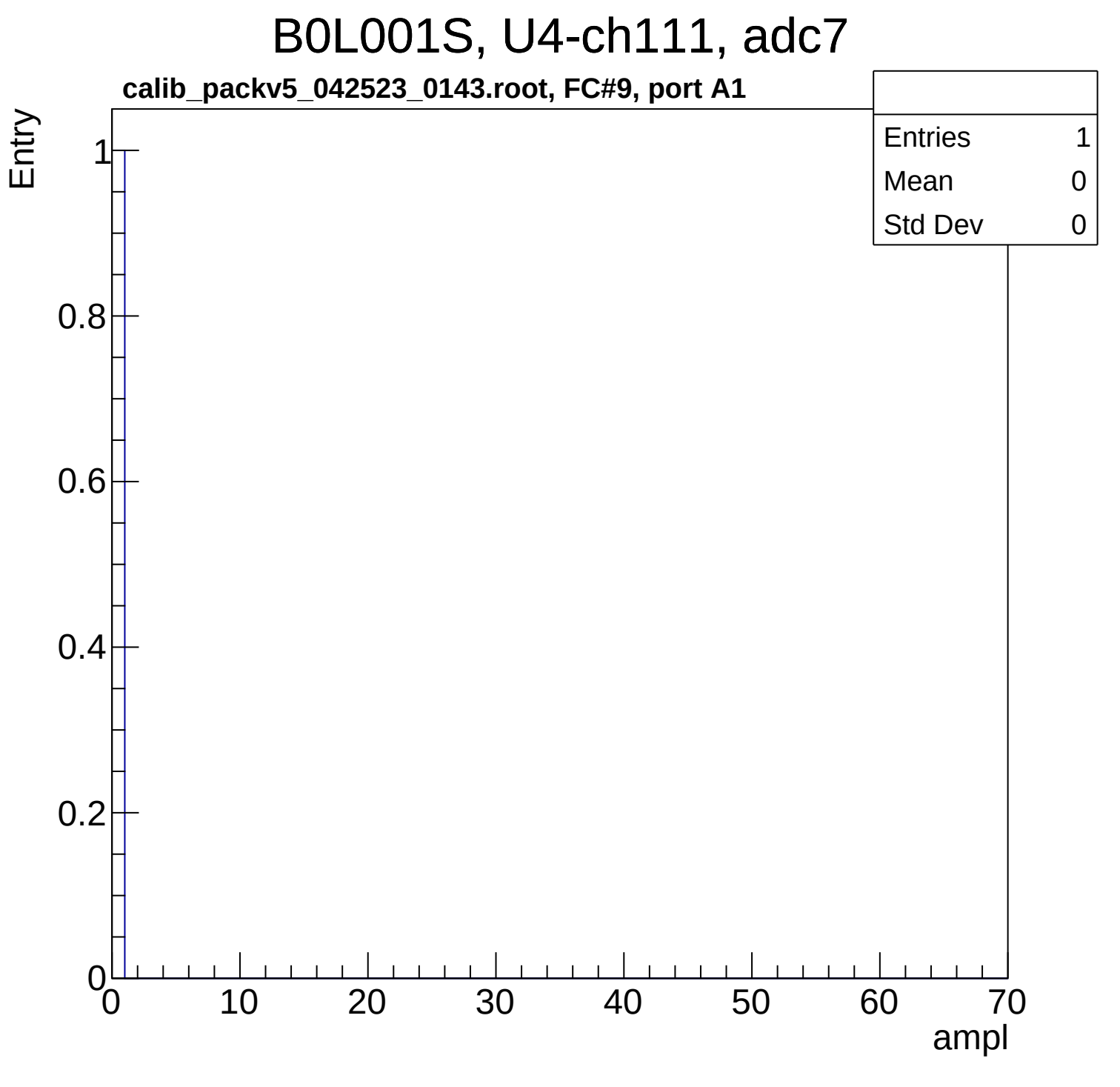
50
60
70
ampl


B0L001S, U4-ch111, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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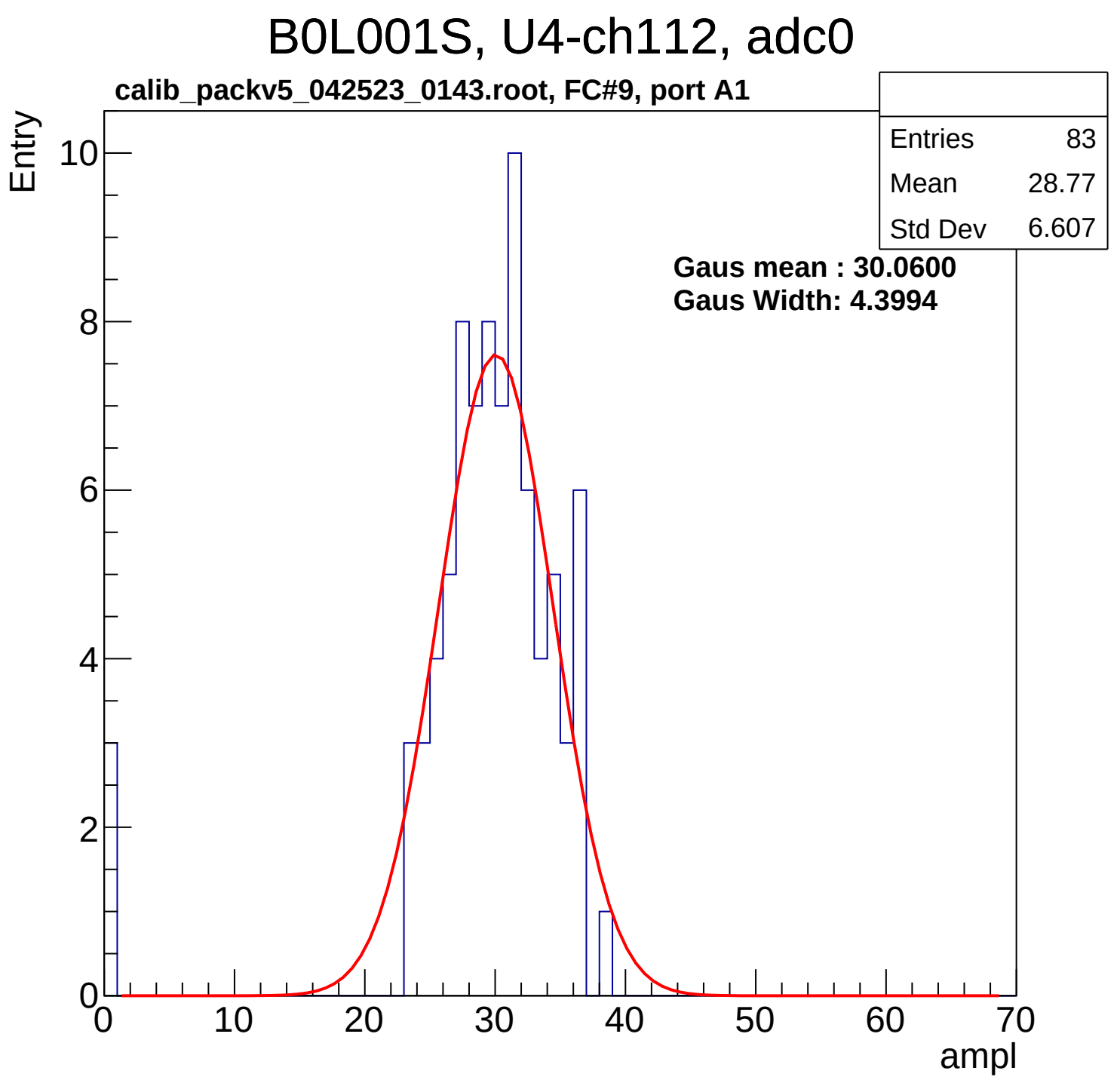
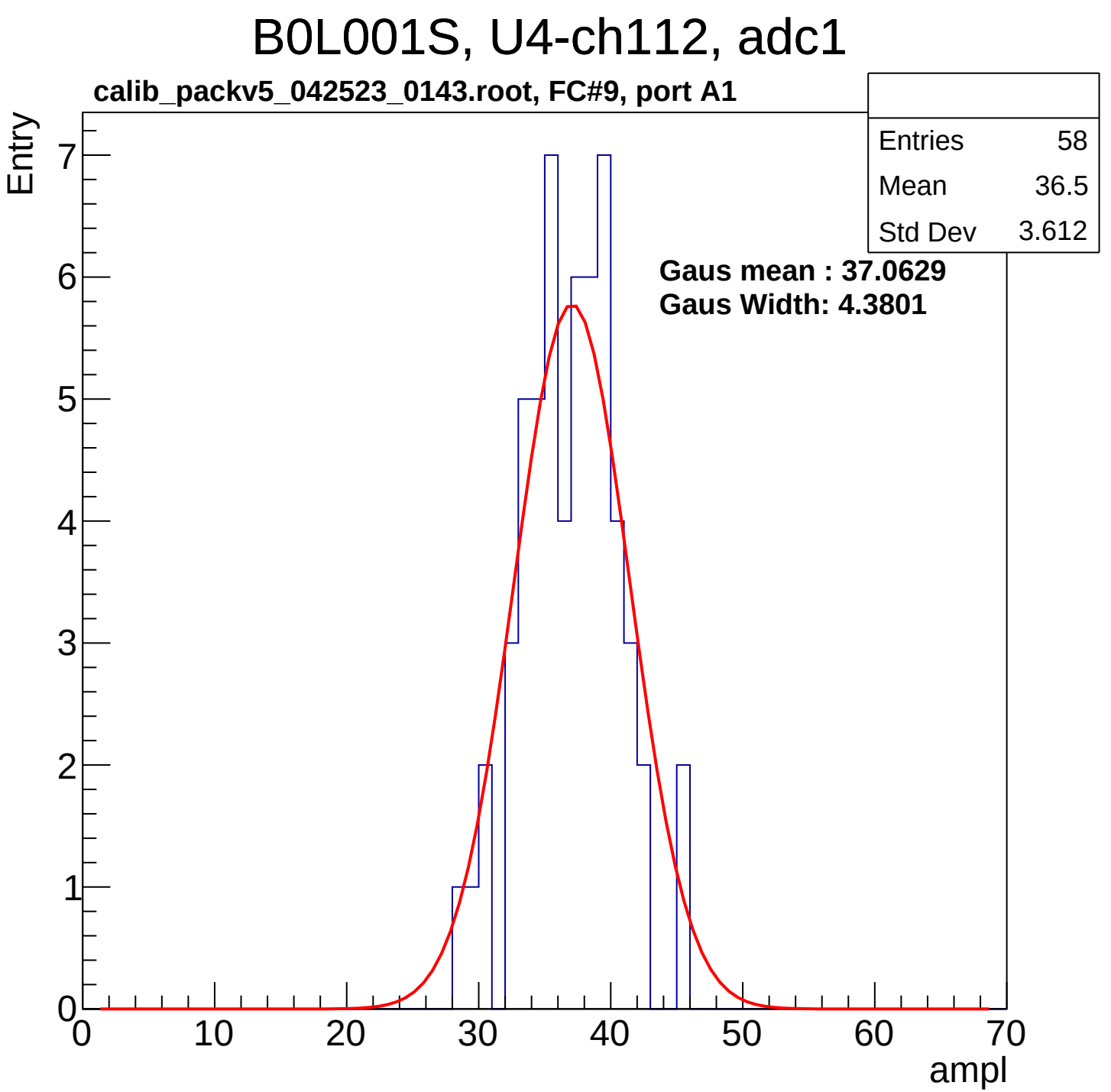
50
60
70
ampl


B0L001S, U4-ch112, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 28.77
Std Dev 6.607
Gaus mean : 30.0600
Gaus Width: 4.3994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch112, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 36.5
Std Dev 3.612
Gaus mean : 37.0629
Gaus Width: 4.3801
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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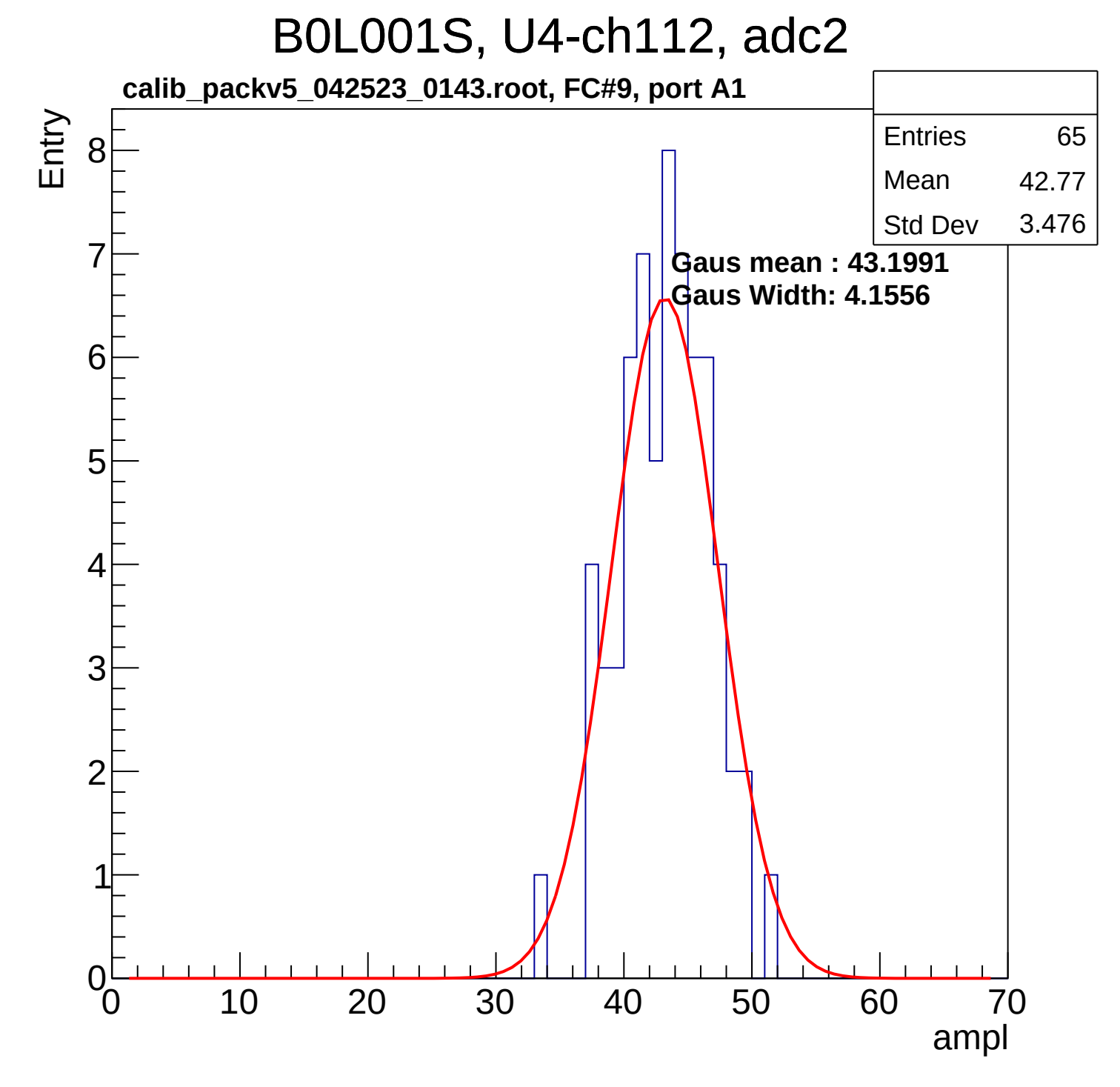
B0L001S, U4-ch112, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 42.77
Std Dev 3.476
Gaus mean : 43.1991
Gaus Width: 4.1556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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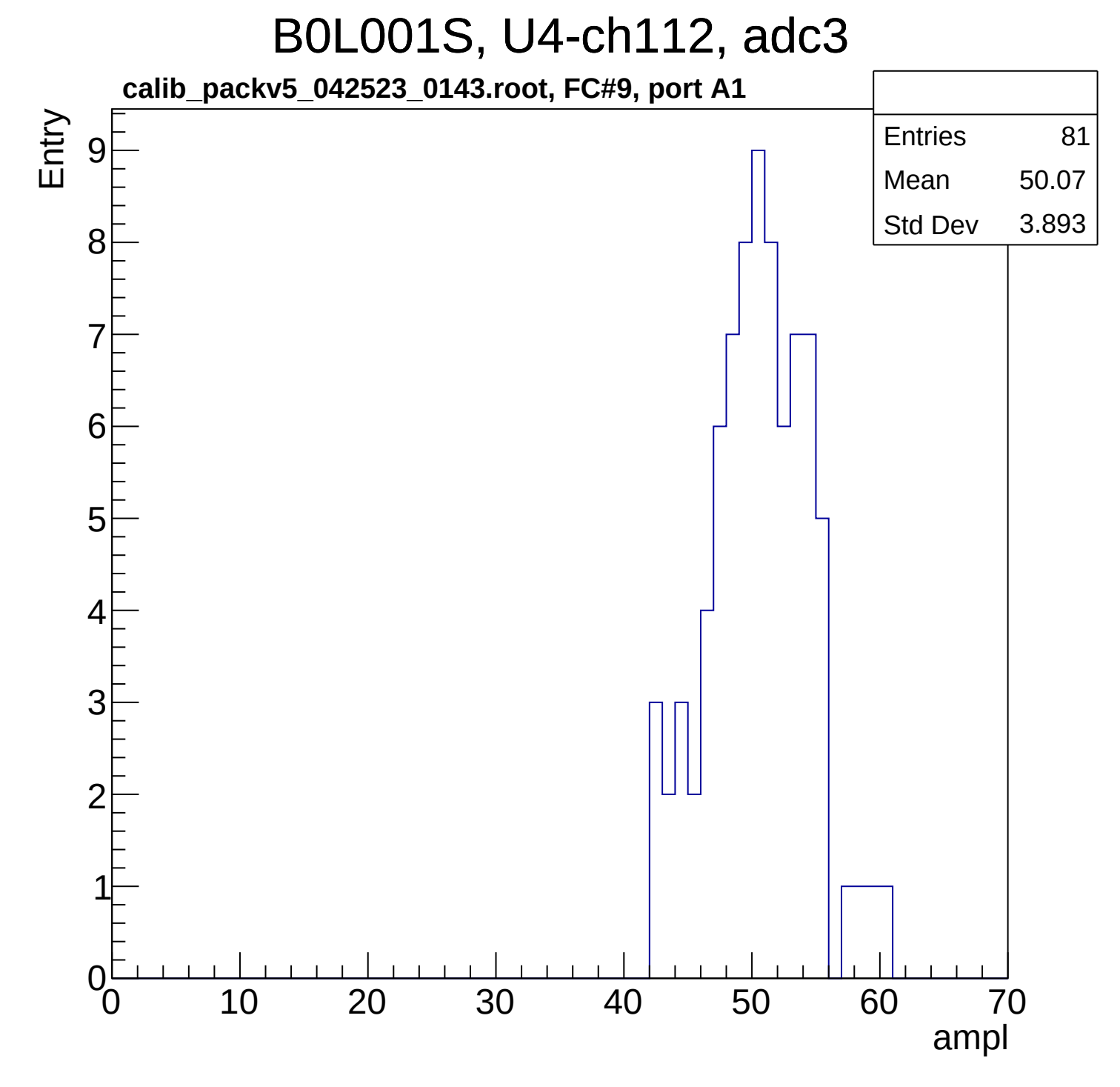
B0L001S, U4-ch112, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 50.07
Std Dev 3.893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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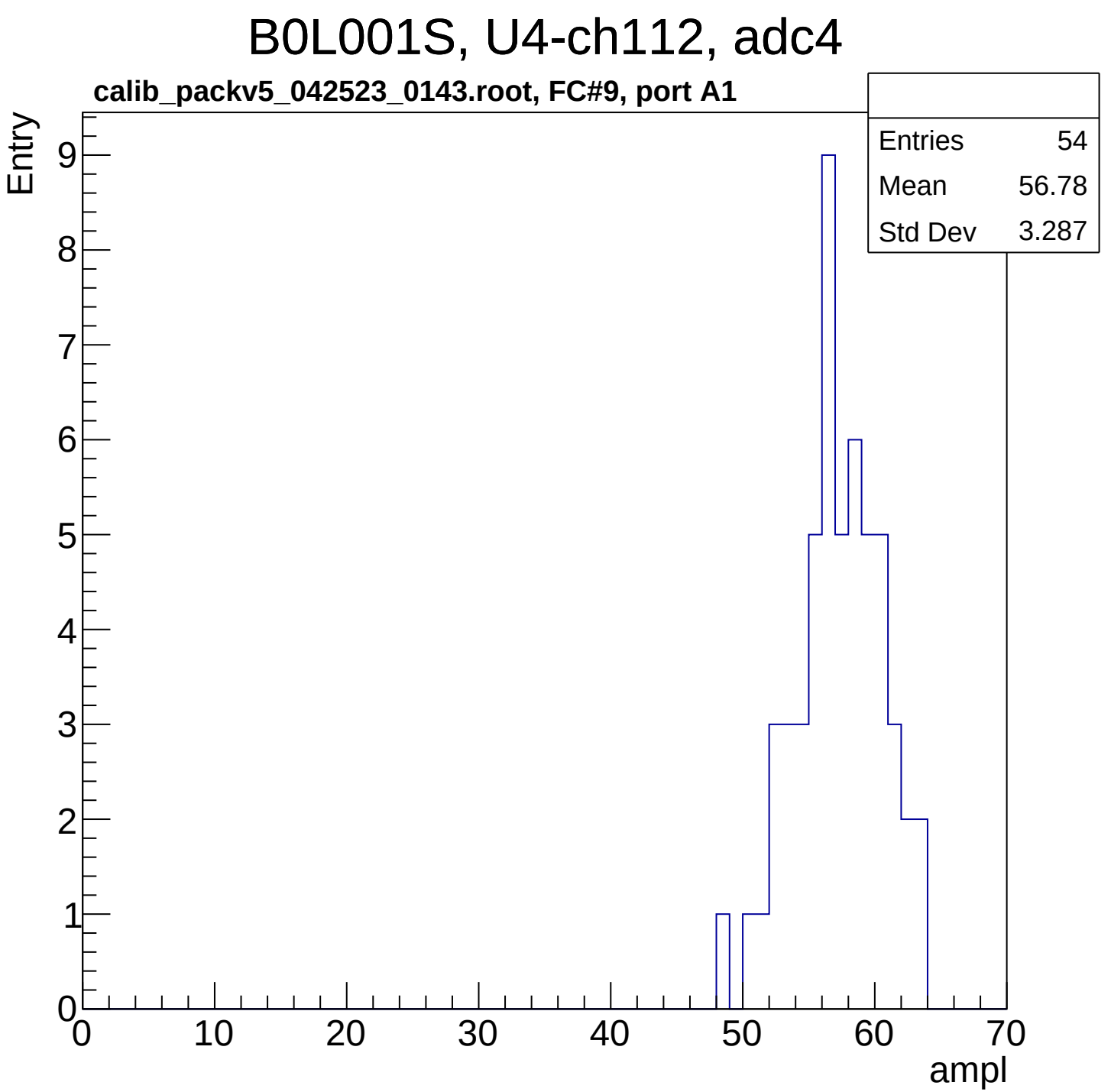
B0L001S, U4-ch112, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.78
Std Dev 3.287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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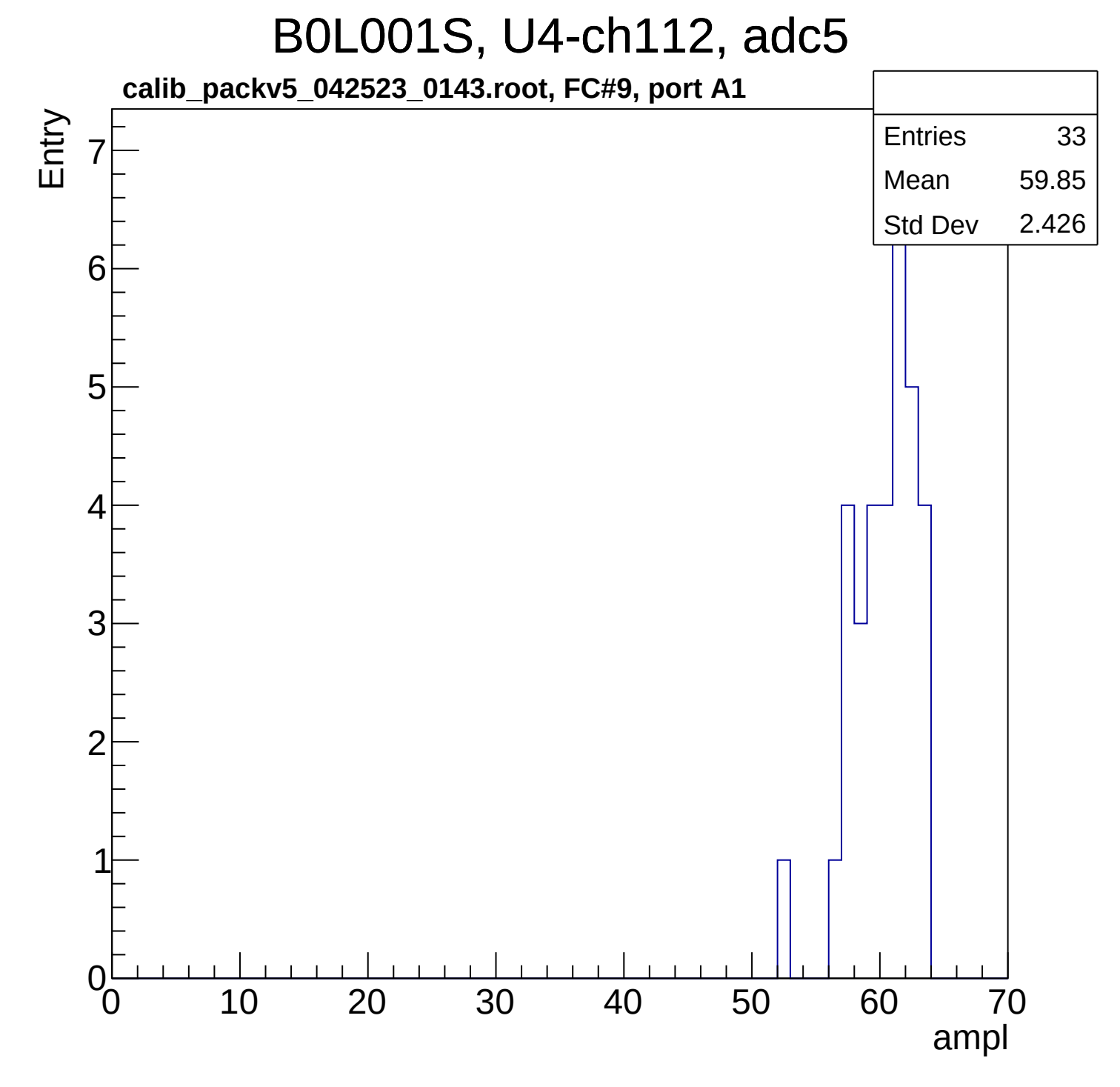
B0L001S, U4-ch112, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 59.85
Std Dev 2.426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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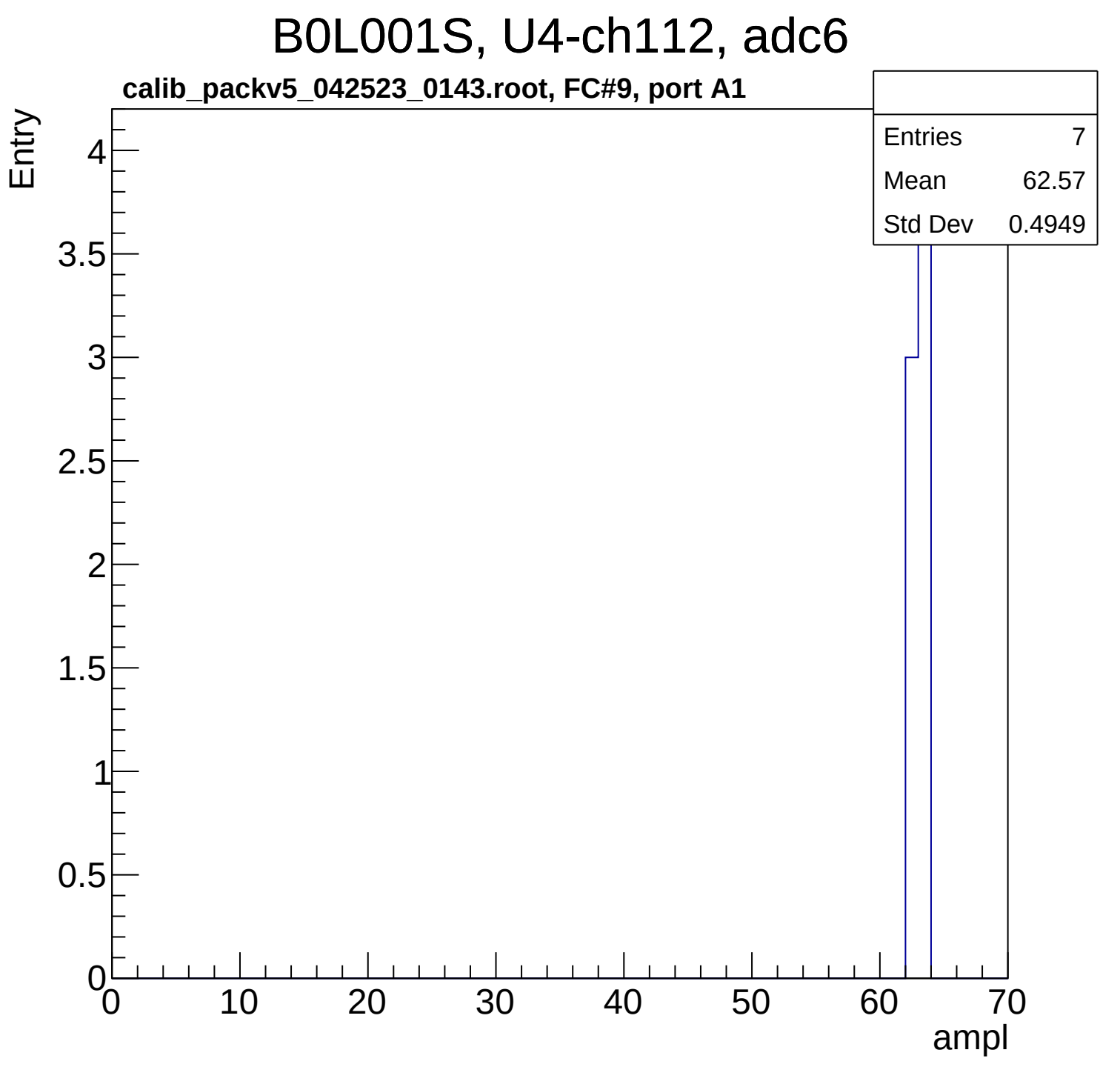
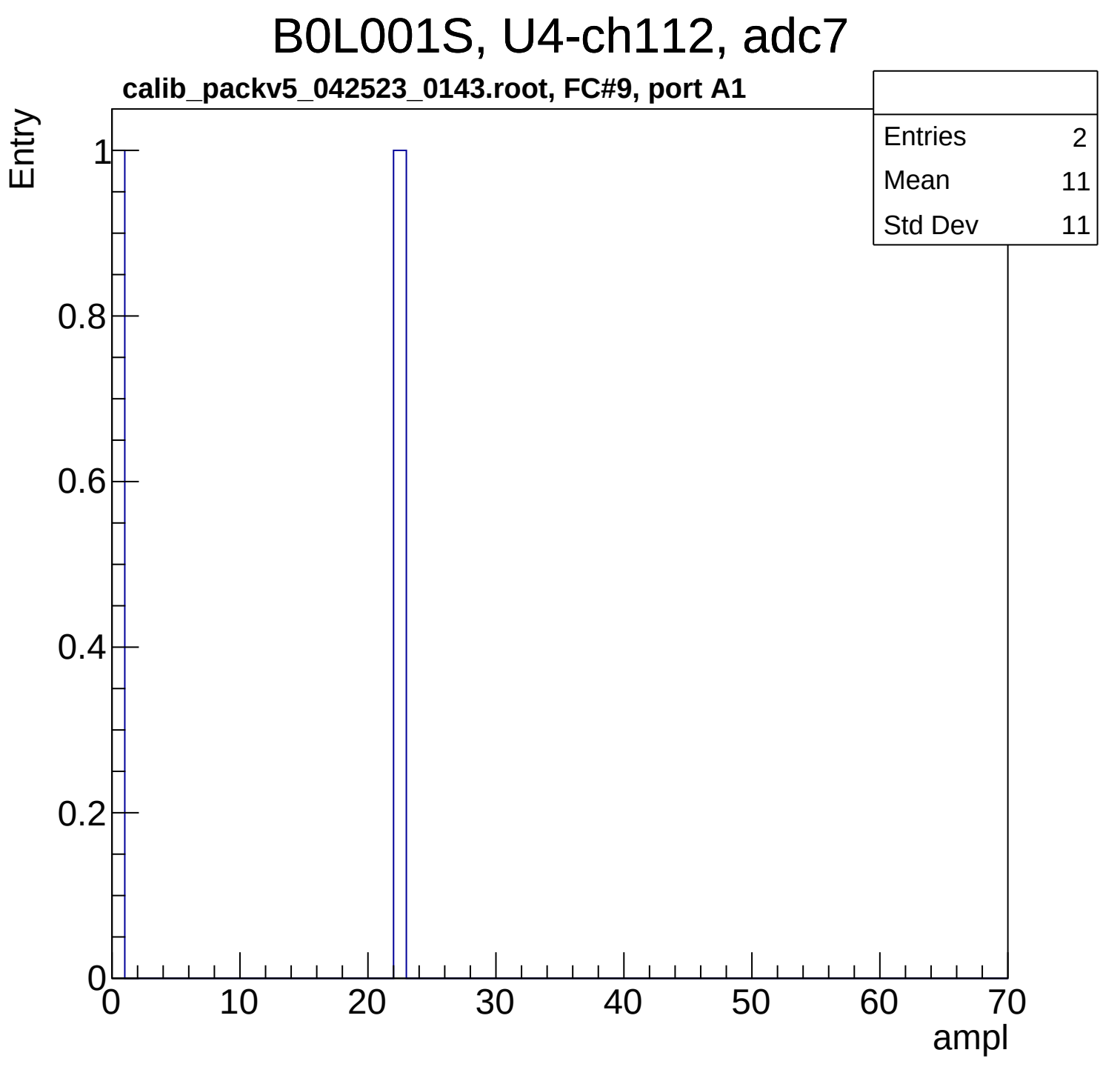

B0L001S, U4-ch112, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch112, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
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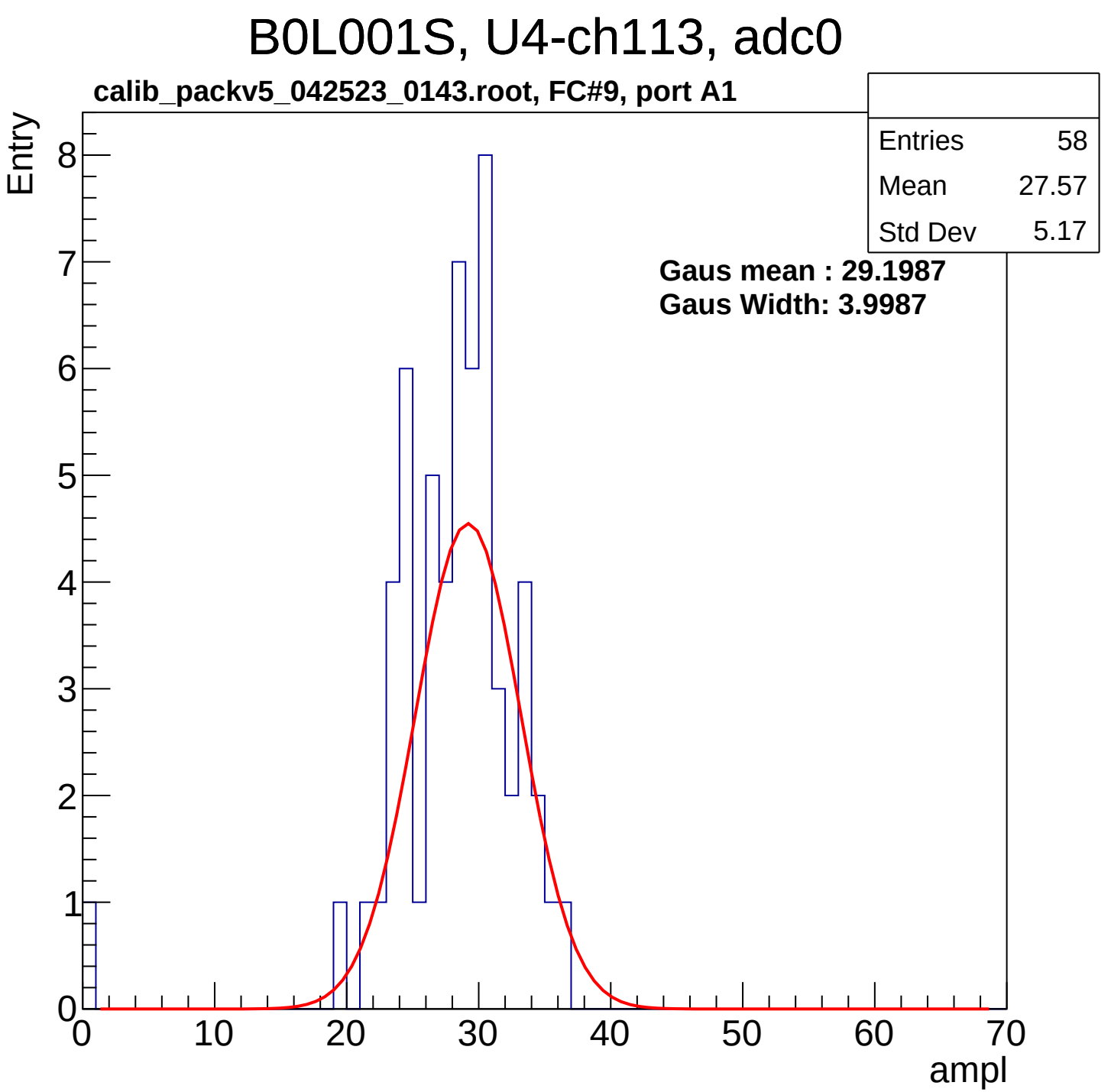
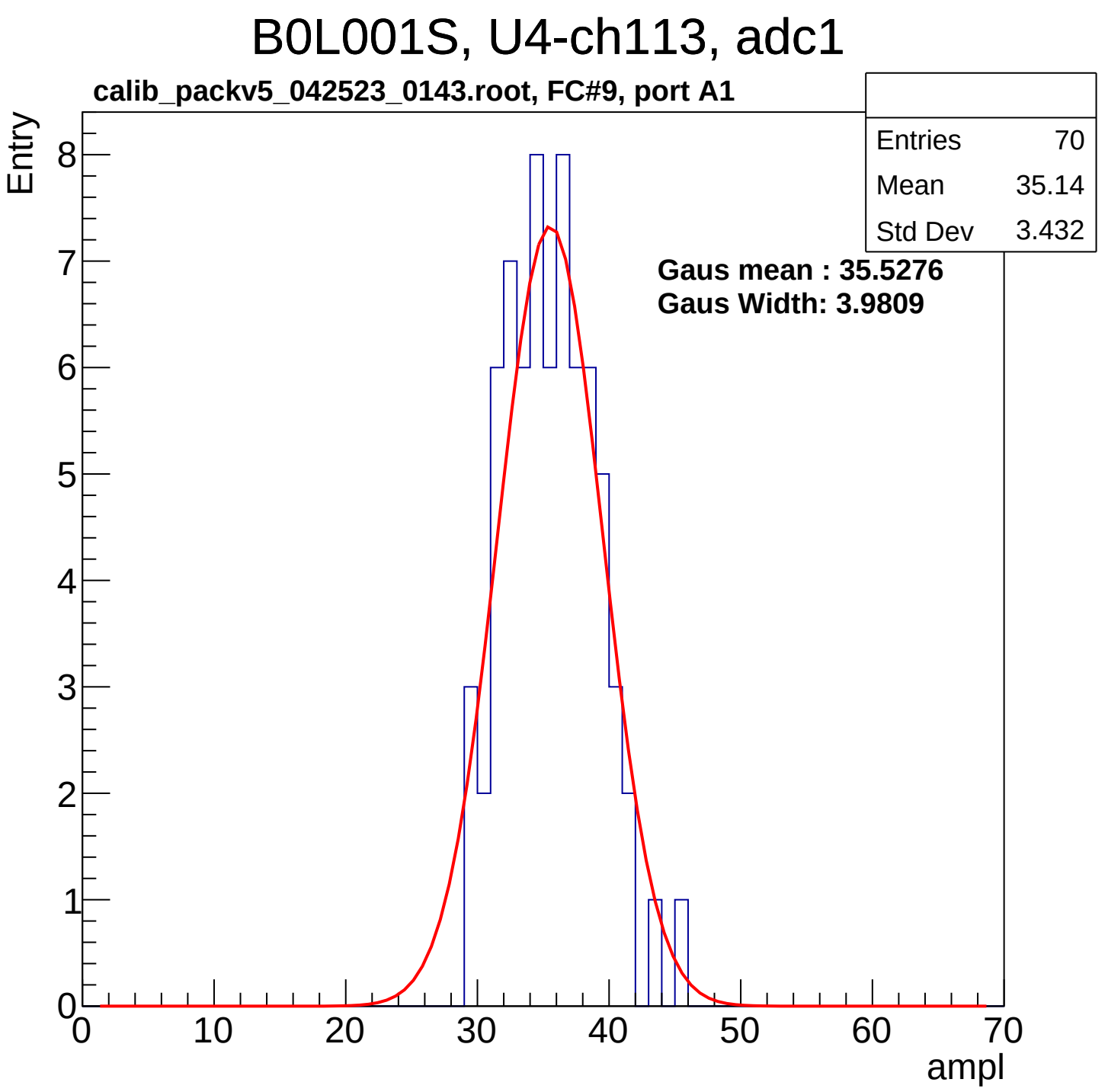
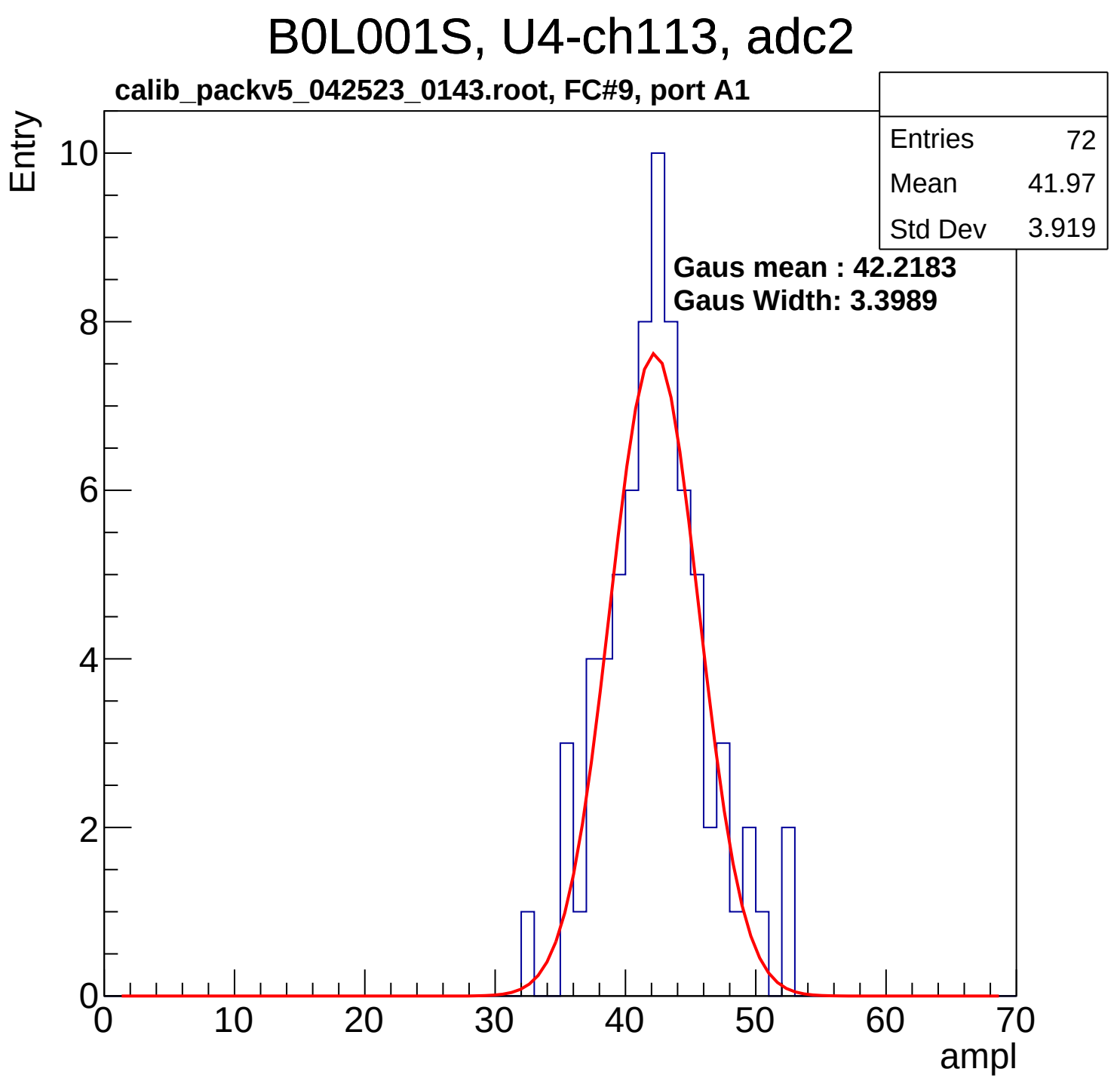
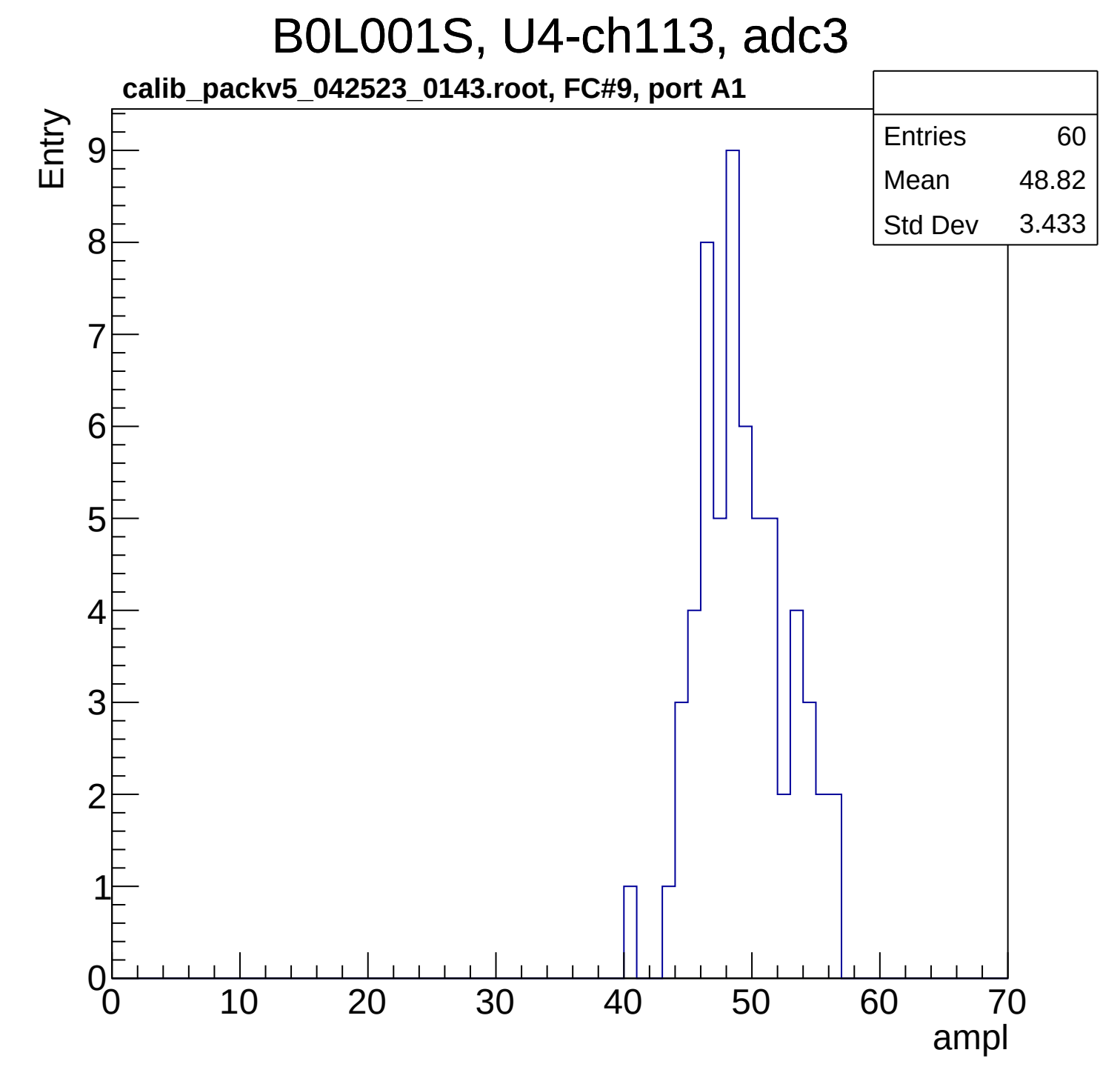
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch113, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 27.57
Std Dev 5.17
Gaus mean : 29.1987
Gaus Width: 3.9987
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch113, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 35.14
Std Dev 3.432
Gaus mean : 35.5276
Gaus Width: 3.9809
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch113, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 41.97
Std Dev 3.919
Gaus mean : 42.2183
Gaus Width: 3.3989
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch113, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 48.82
Std Dev 3.433
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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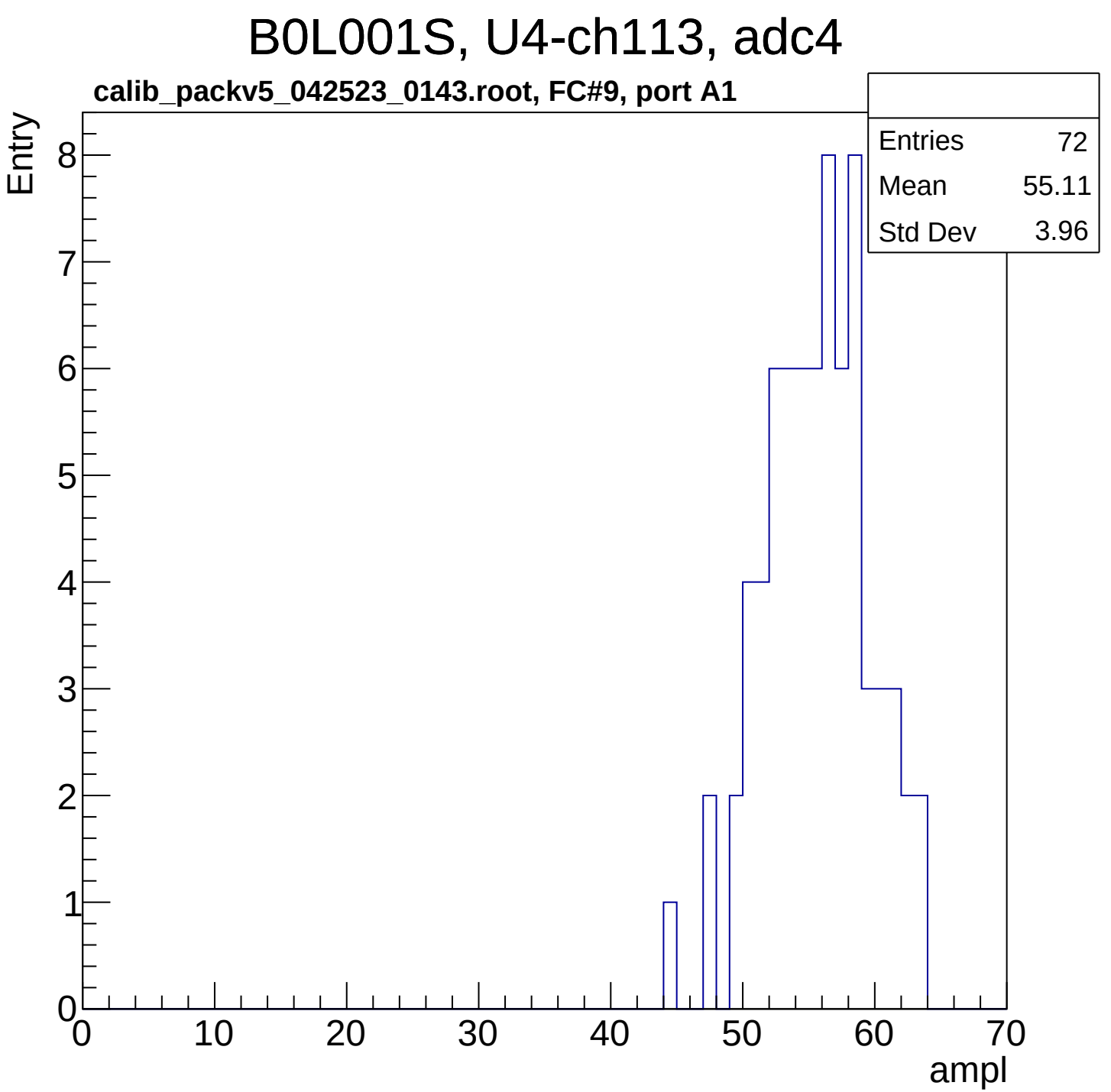
B0L001S, U4-ch113, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 55.11
Std Dev 3.96
Entry
ampl
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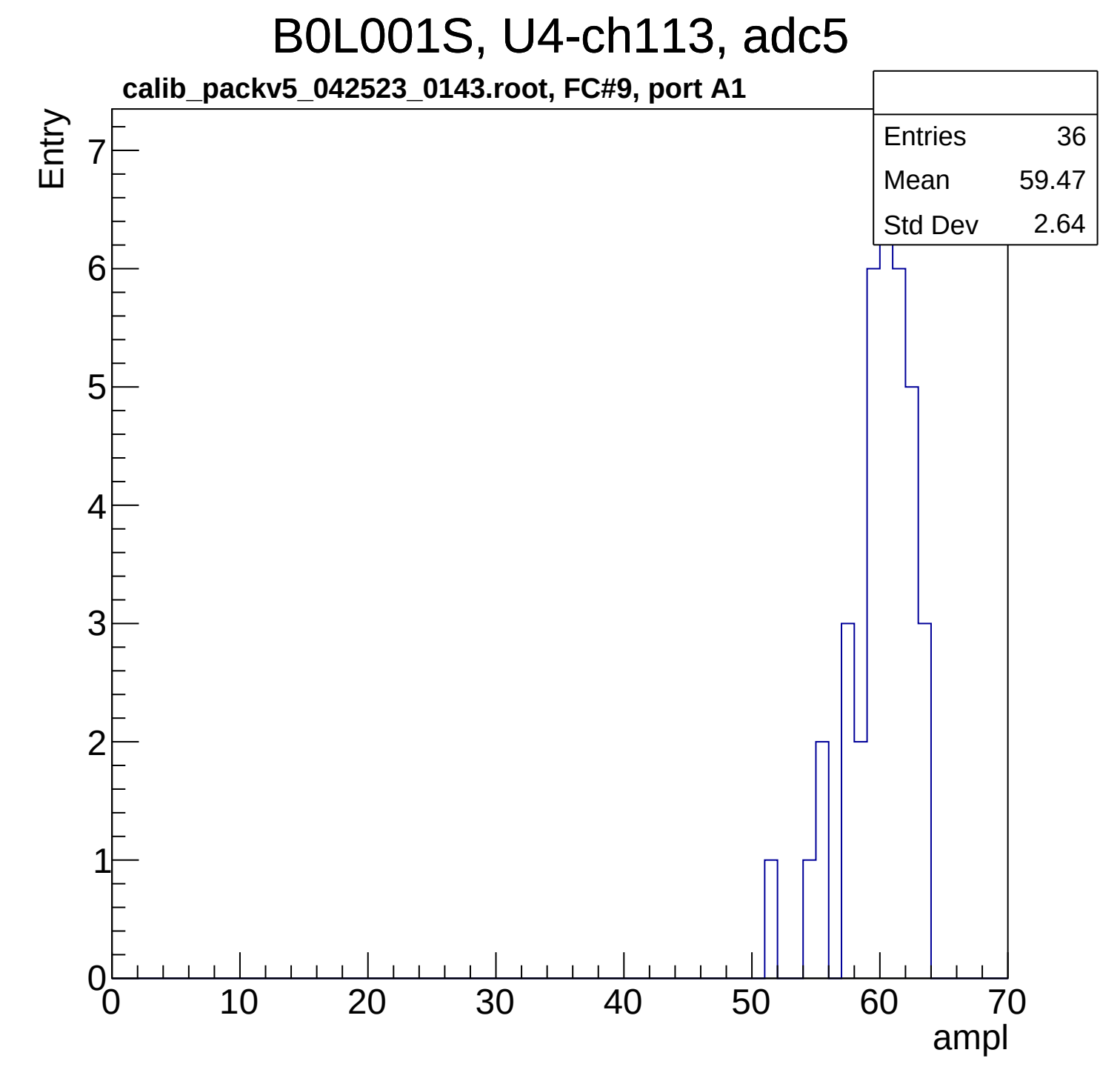
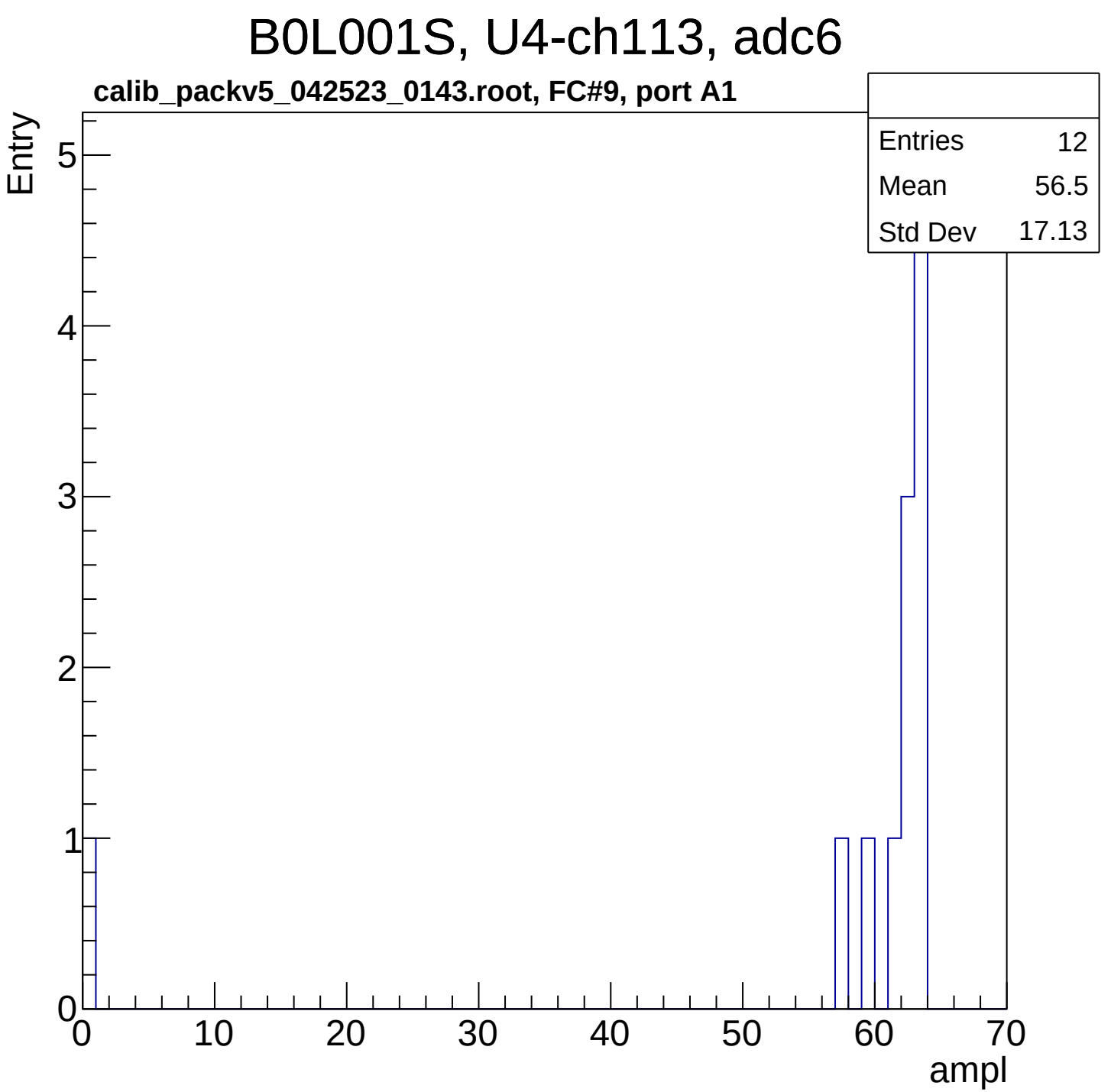
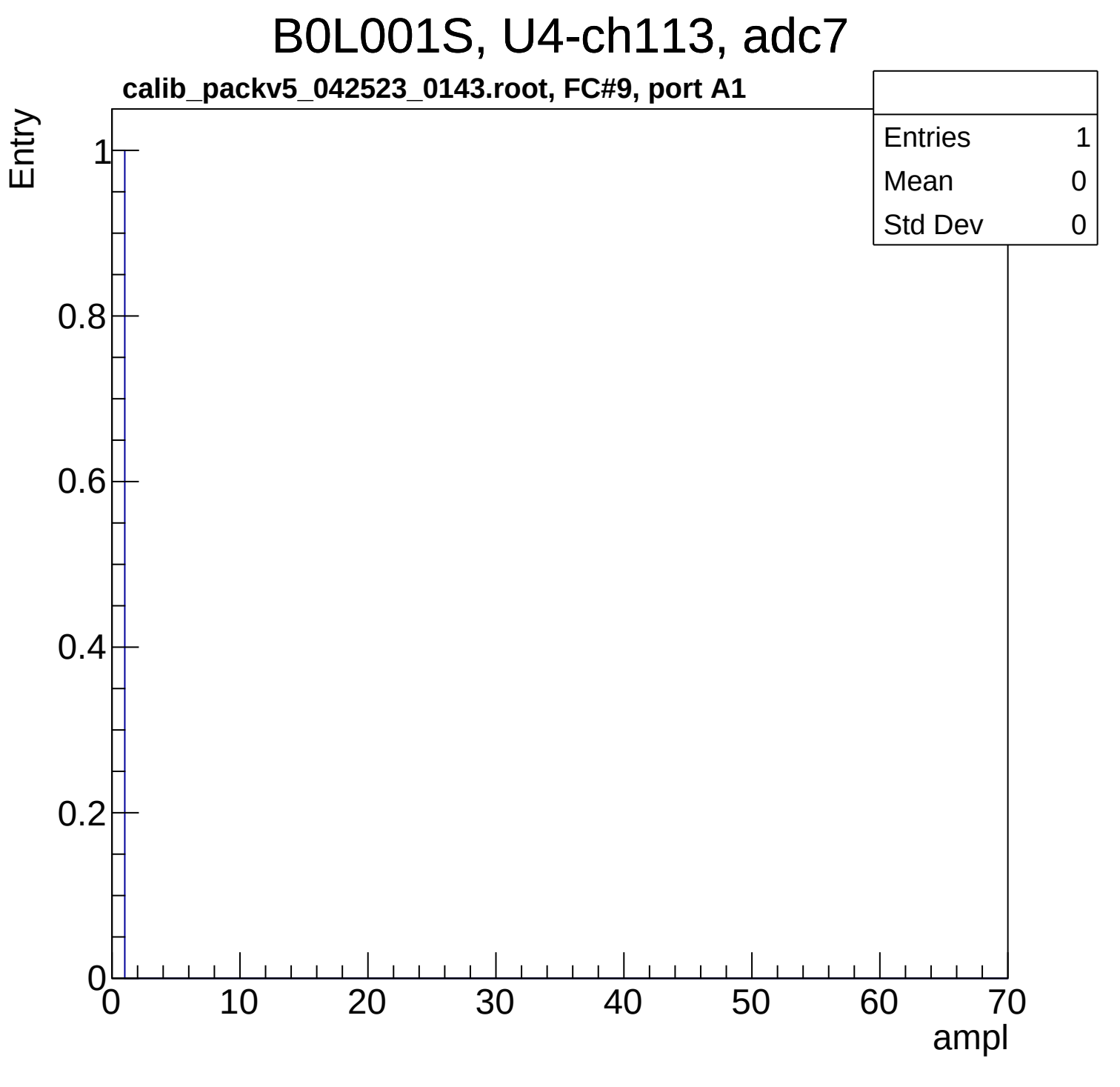
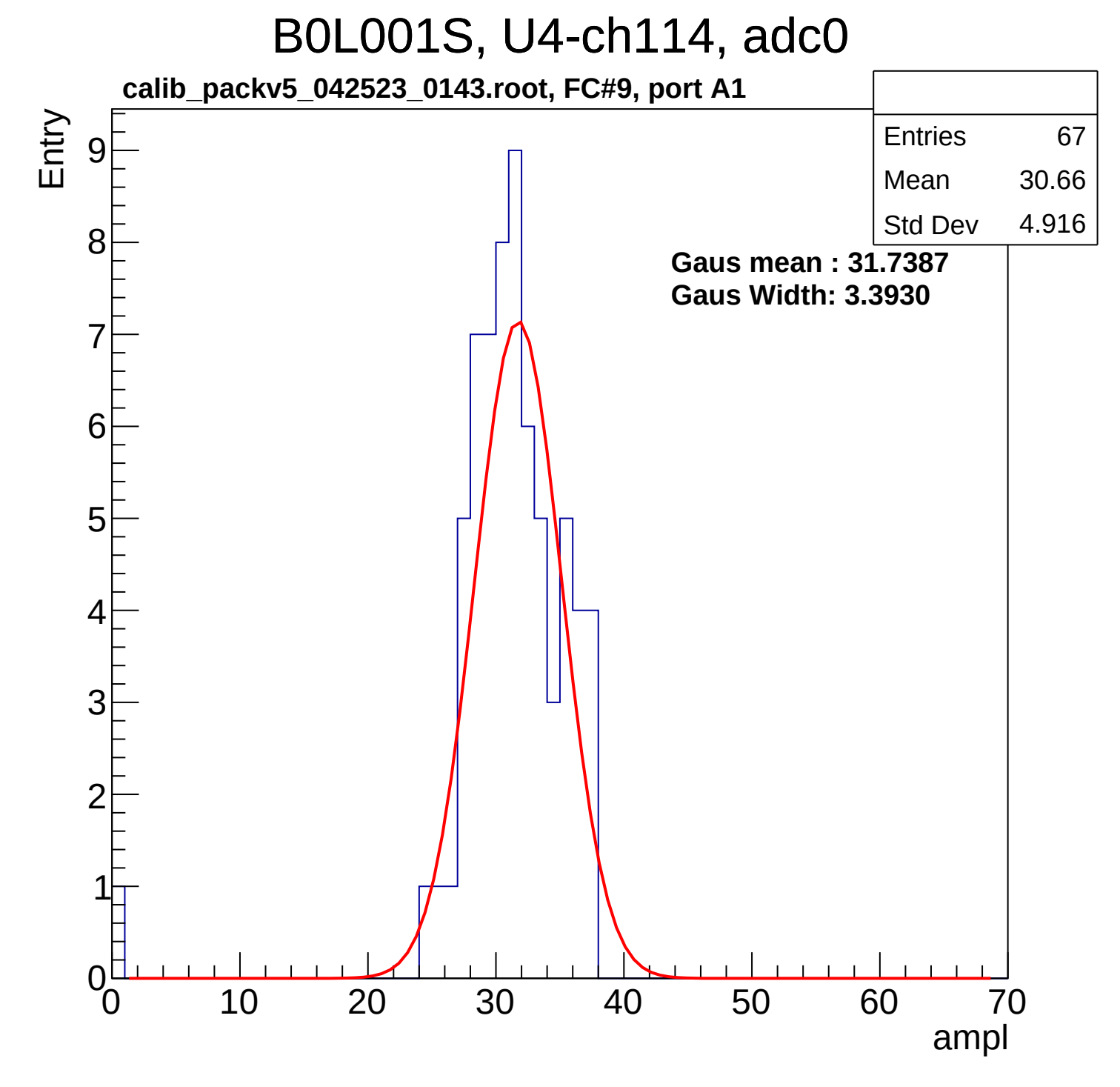
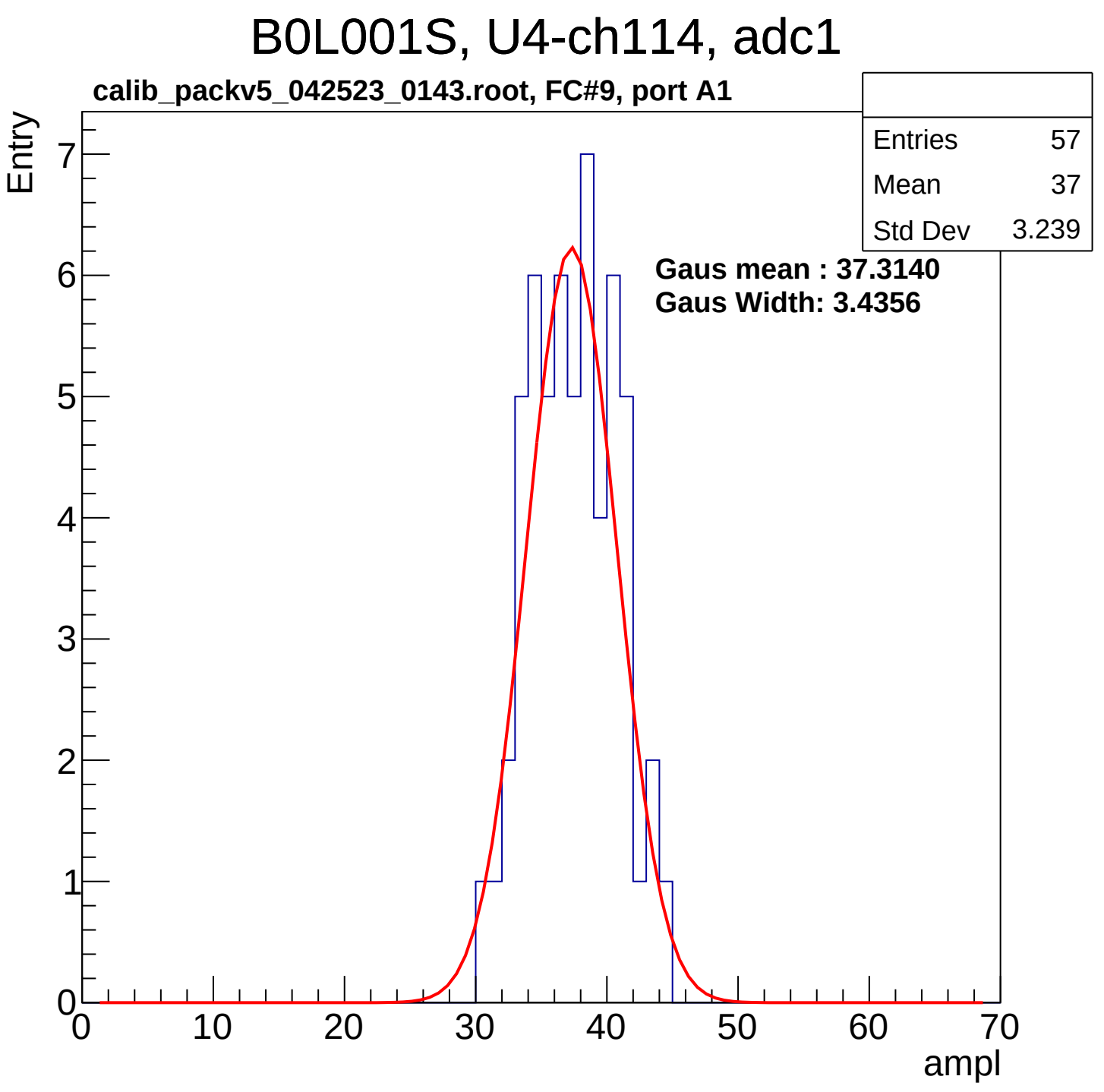
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

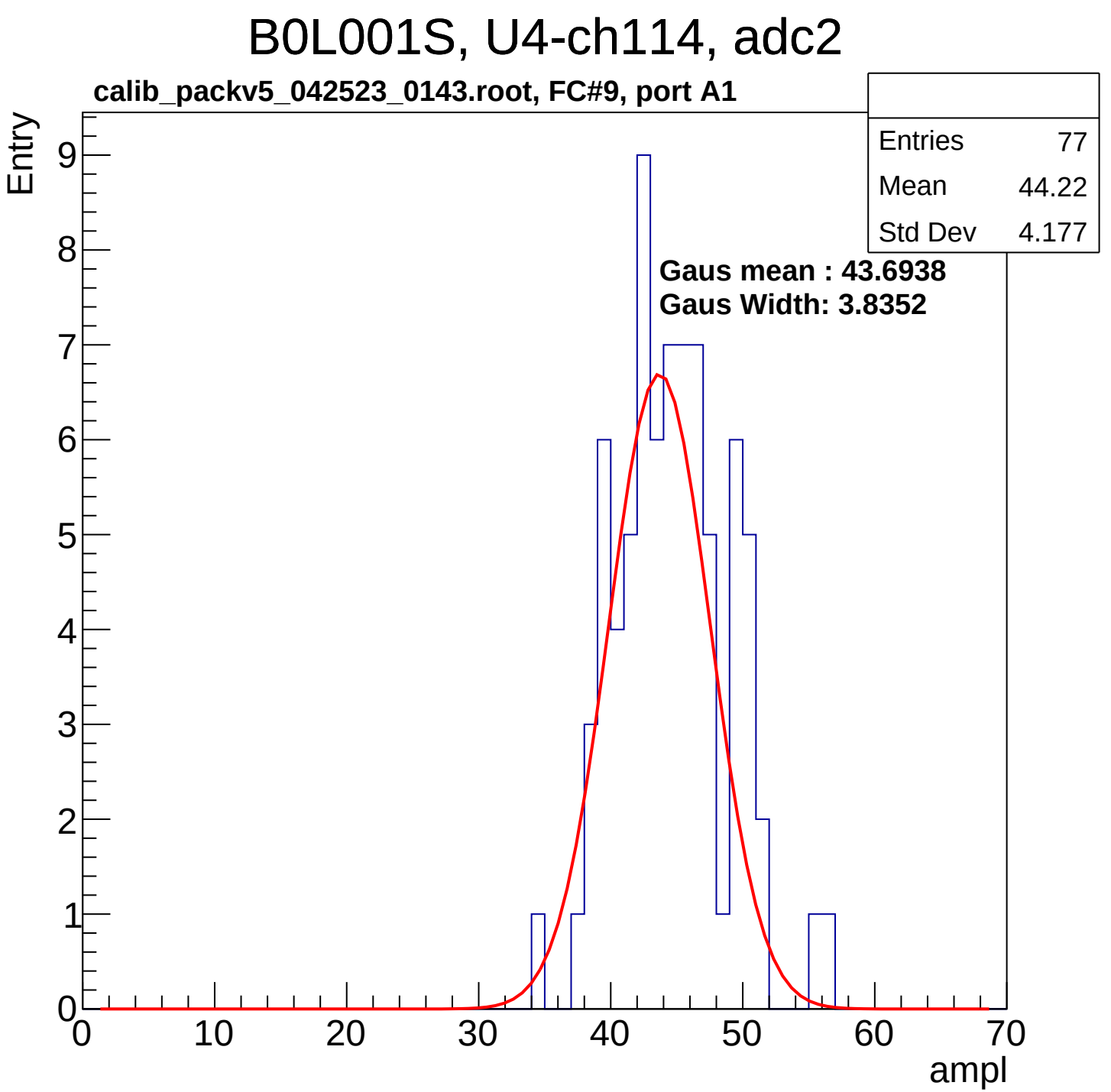

B0L001S, U4-ch113, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.47
Std Dev 2.64
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

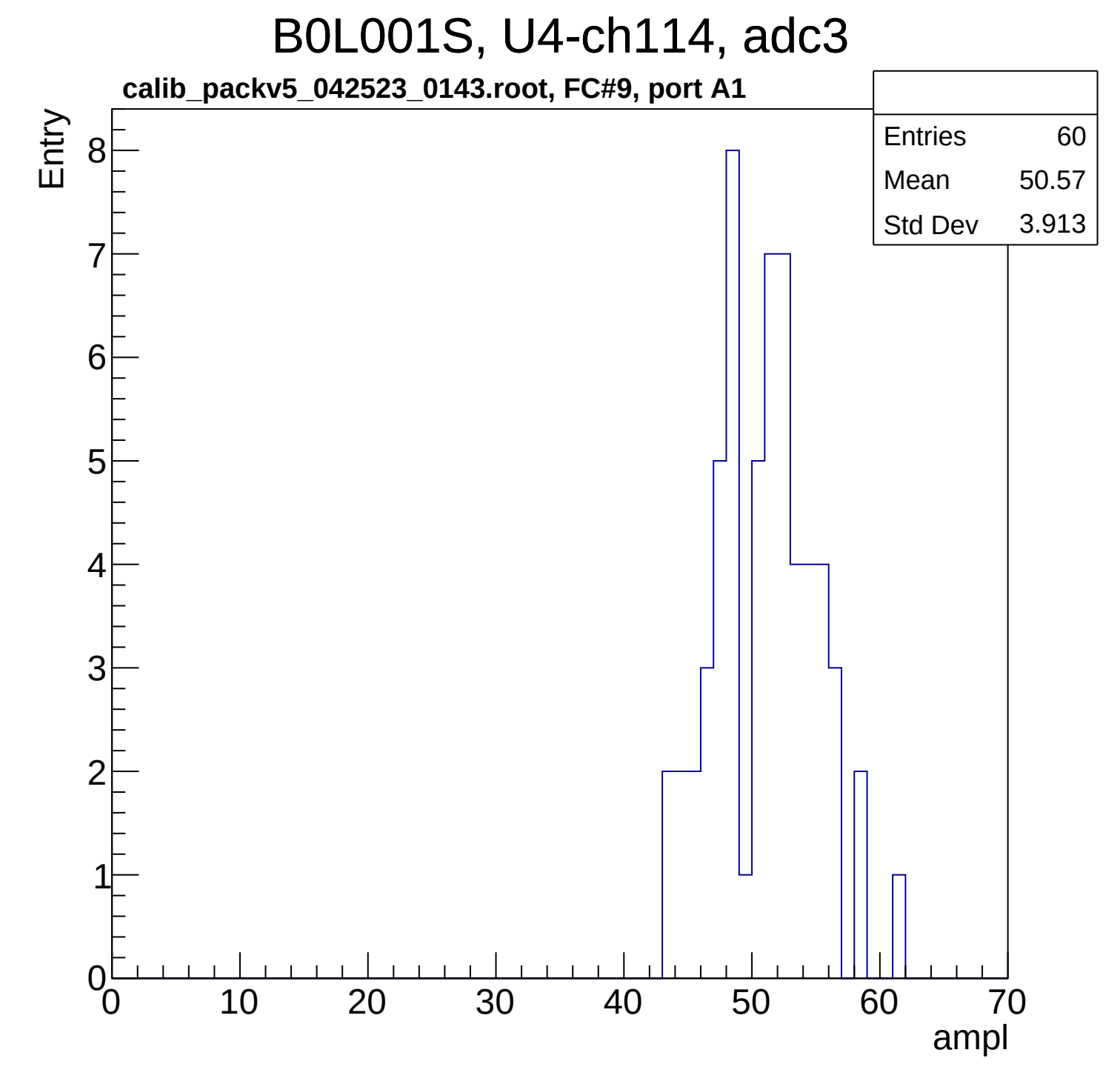

B0L001S, U4-ch113, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.5
Std Dev 17.13
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

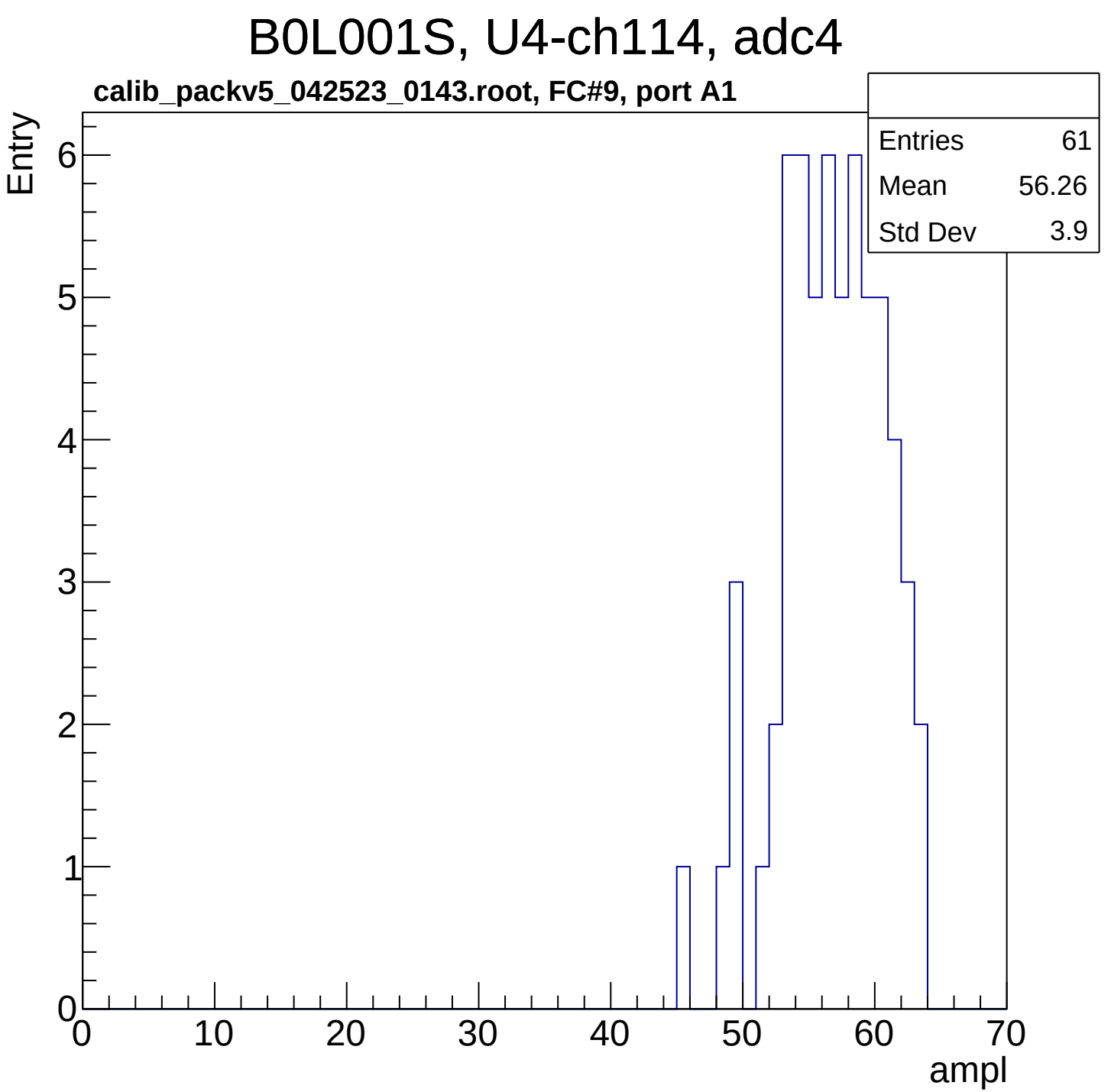

B0L001S, U4-ch113, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch114, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.66
Std Dev 4.916
Gaus mean : 31.7387
Gaus Width: 3.3930
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch114, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37
Std Dev 3.239
Gaus mean : 37.3140
Gaus Width: 3.4356
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch114, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 44.22
Std Dev 4.177
Gaus mean : 43.6938
Gaus Width: 3.8352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch114, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.57
Std Dev 3.913
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch114, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.26
Std Dev 3.9
Entry
0
1
2
3
4
5
6
0
10
20
30
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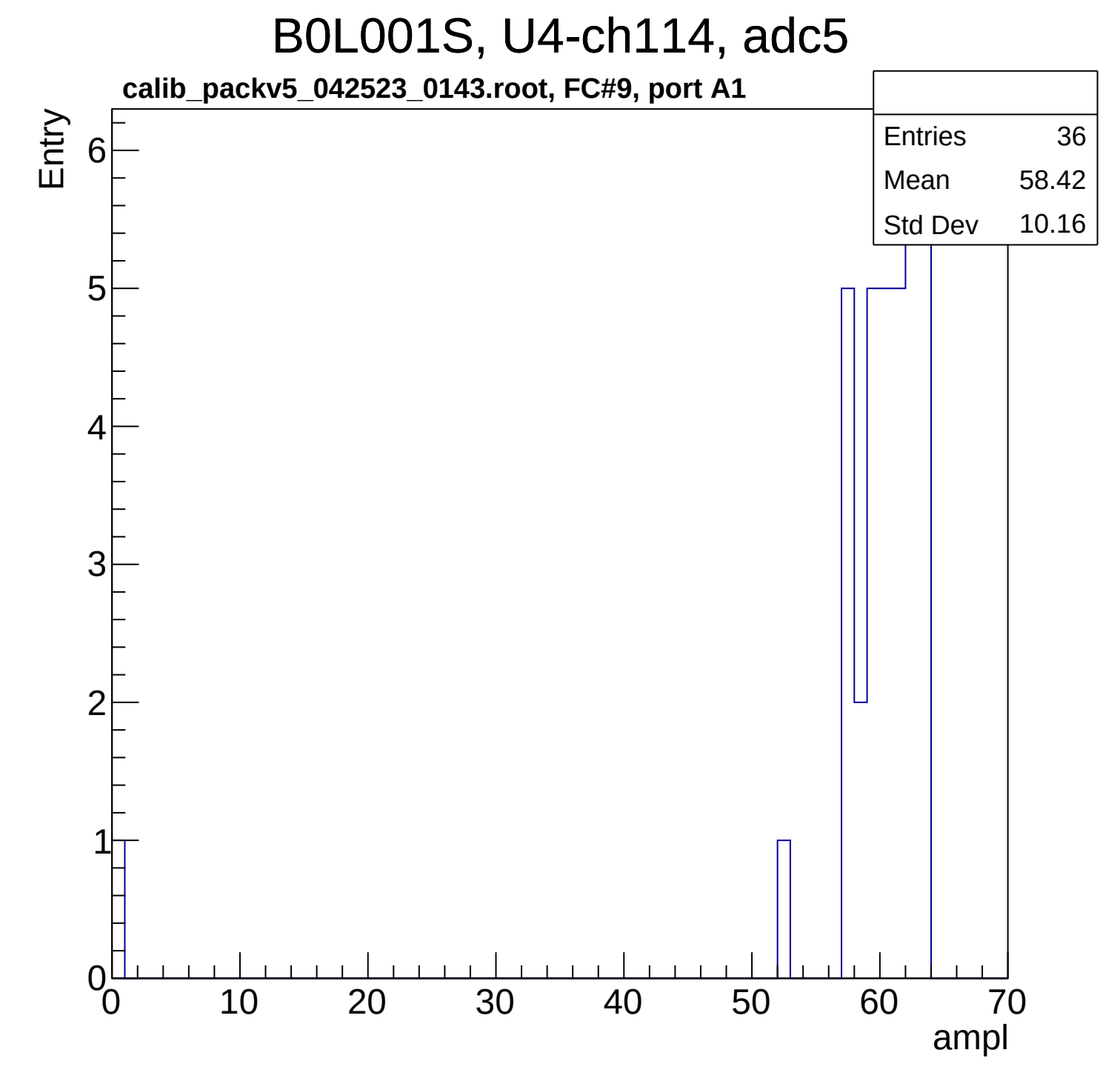
50
60
70
ampl


B0L001S, U4-ch114, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.42
Std Dev 10.16
Entry
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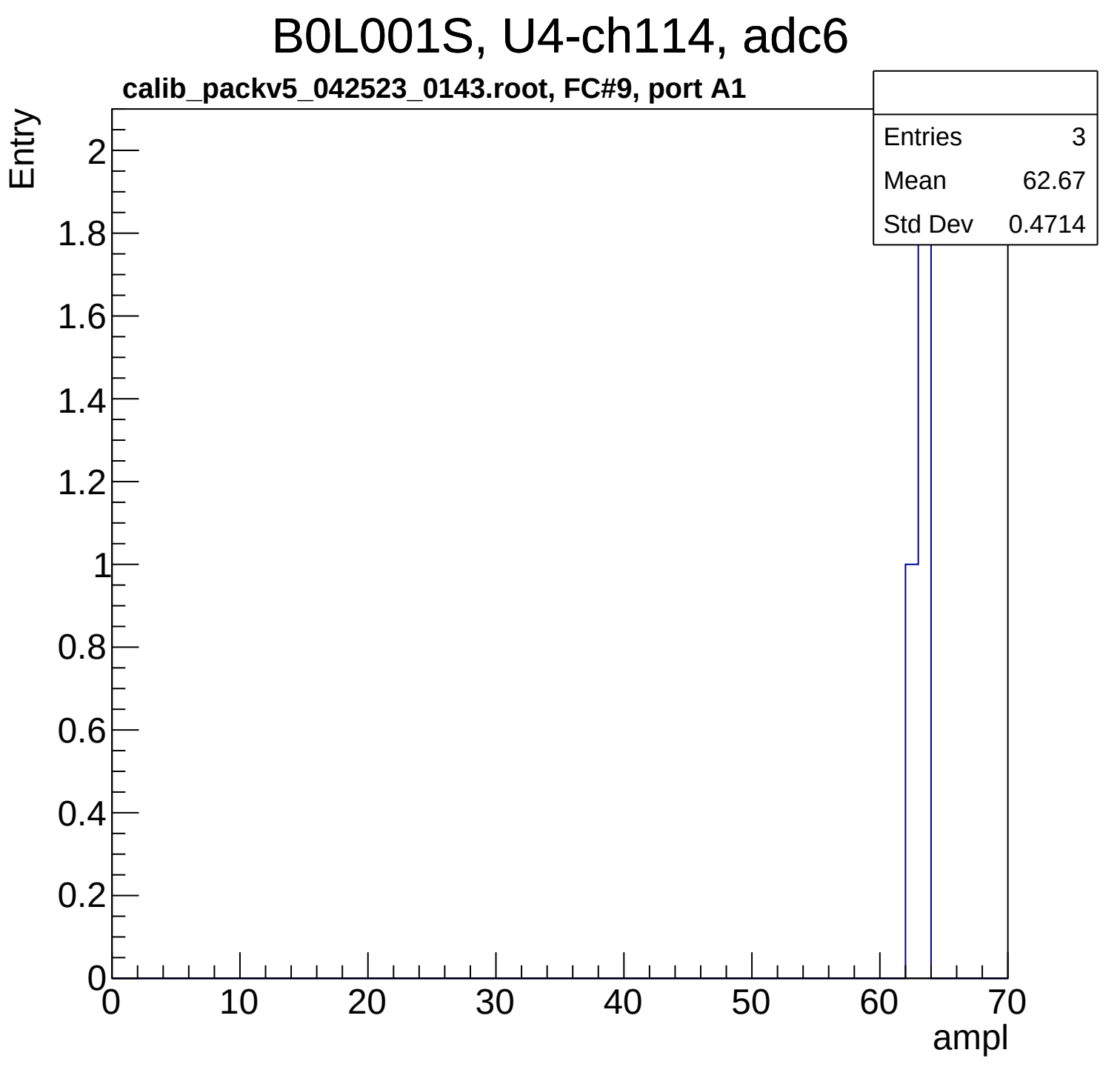
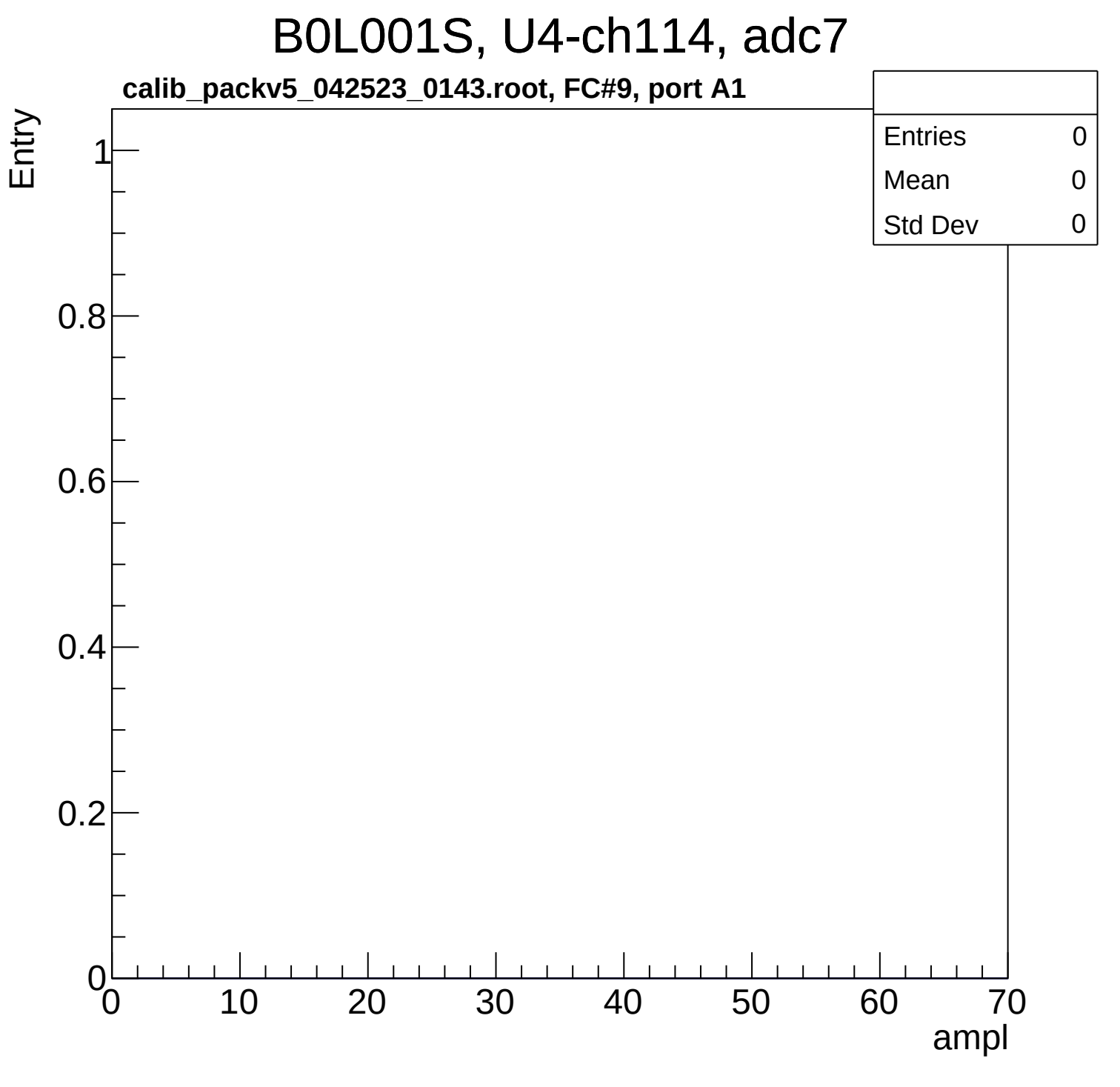
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch114, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch114, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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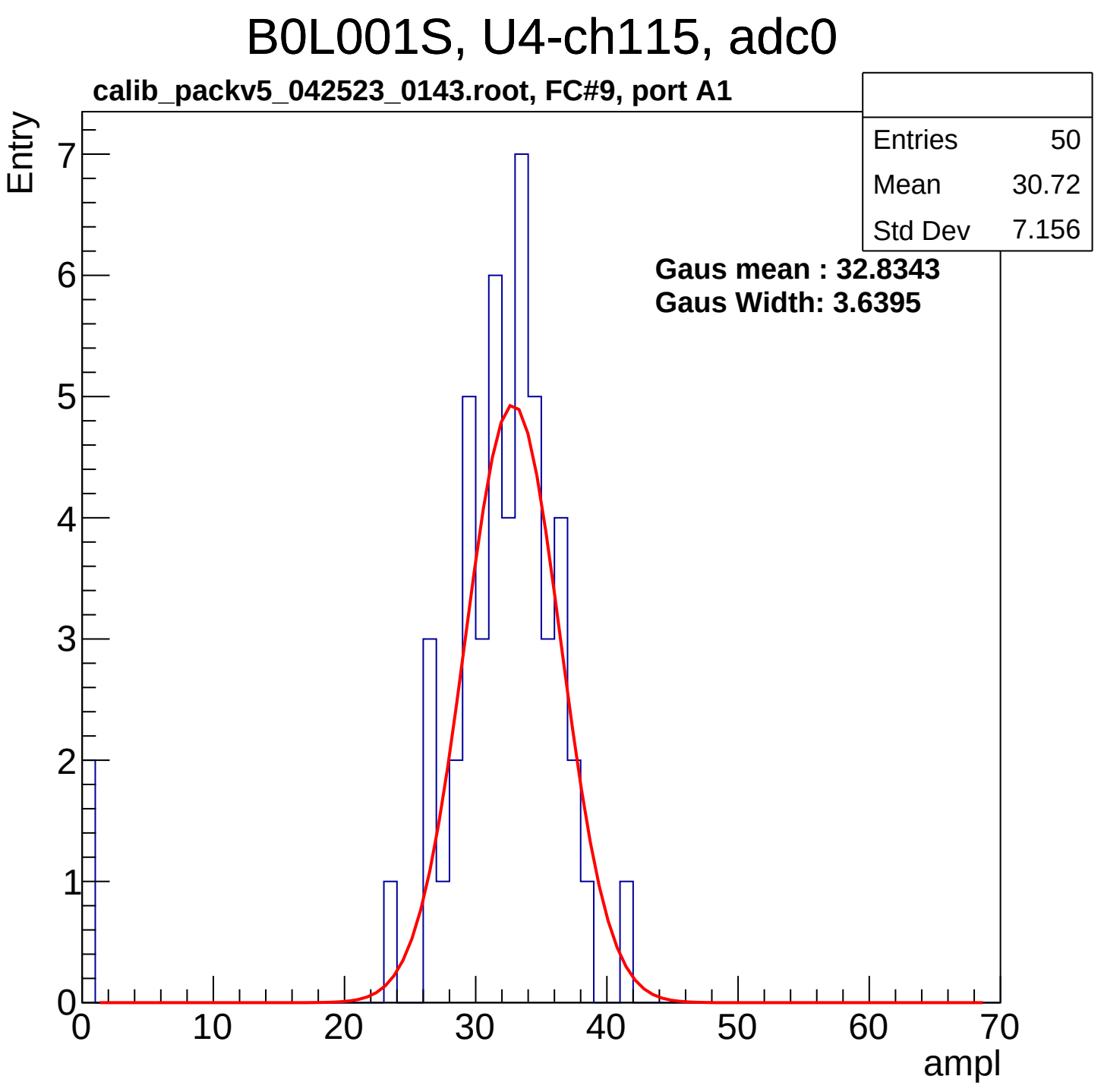
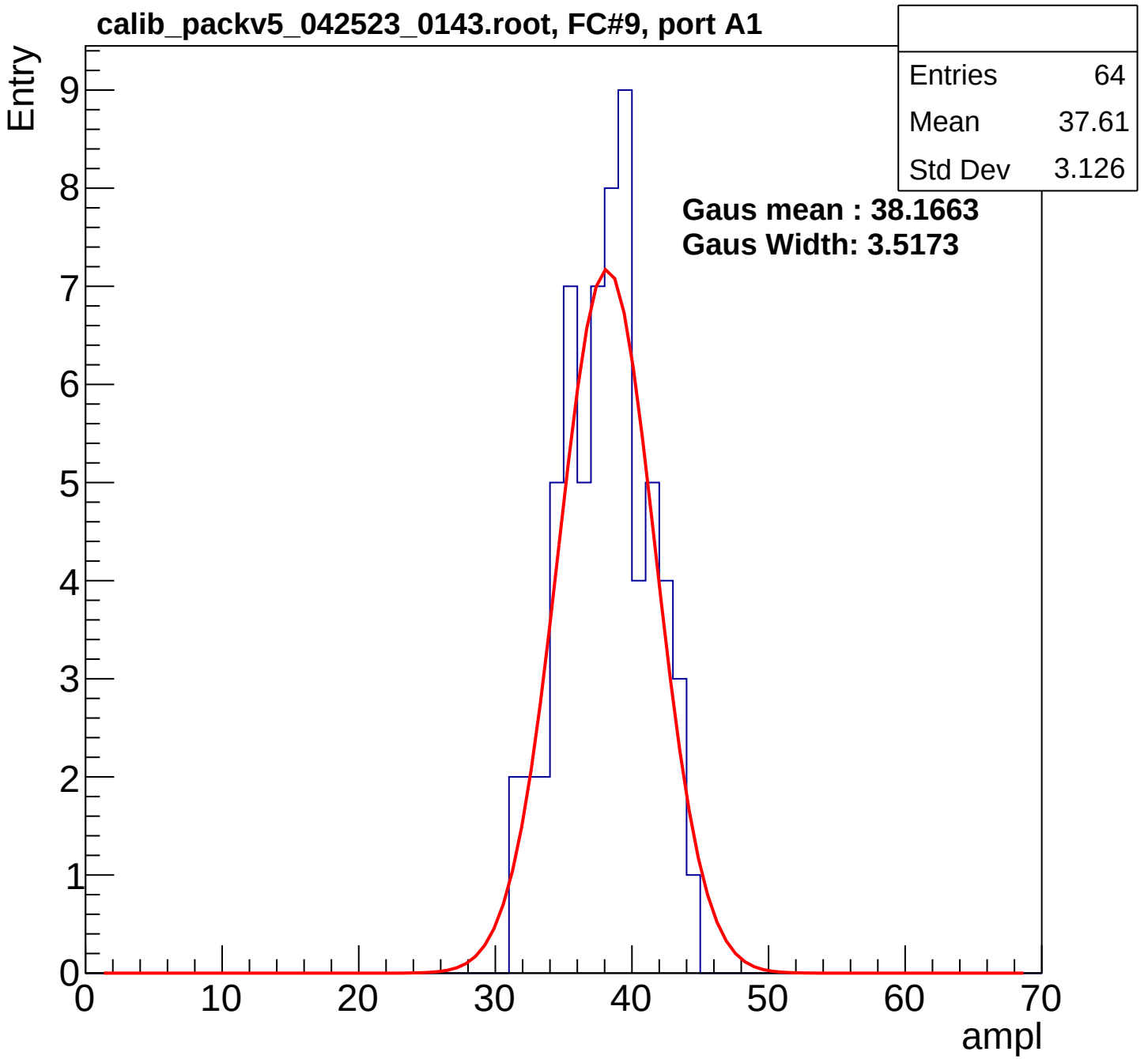
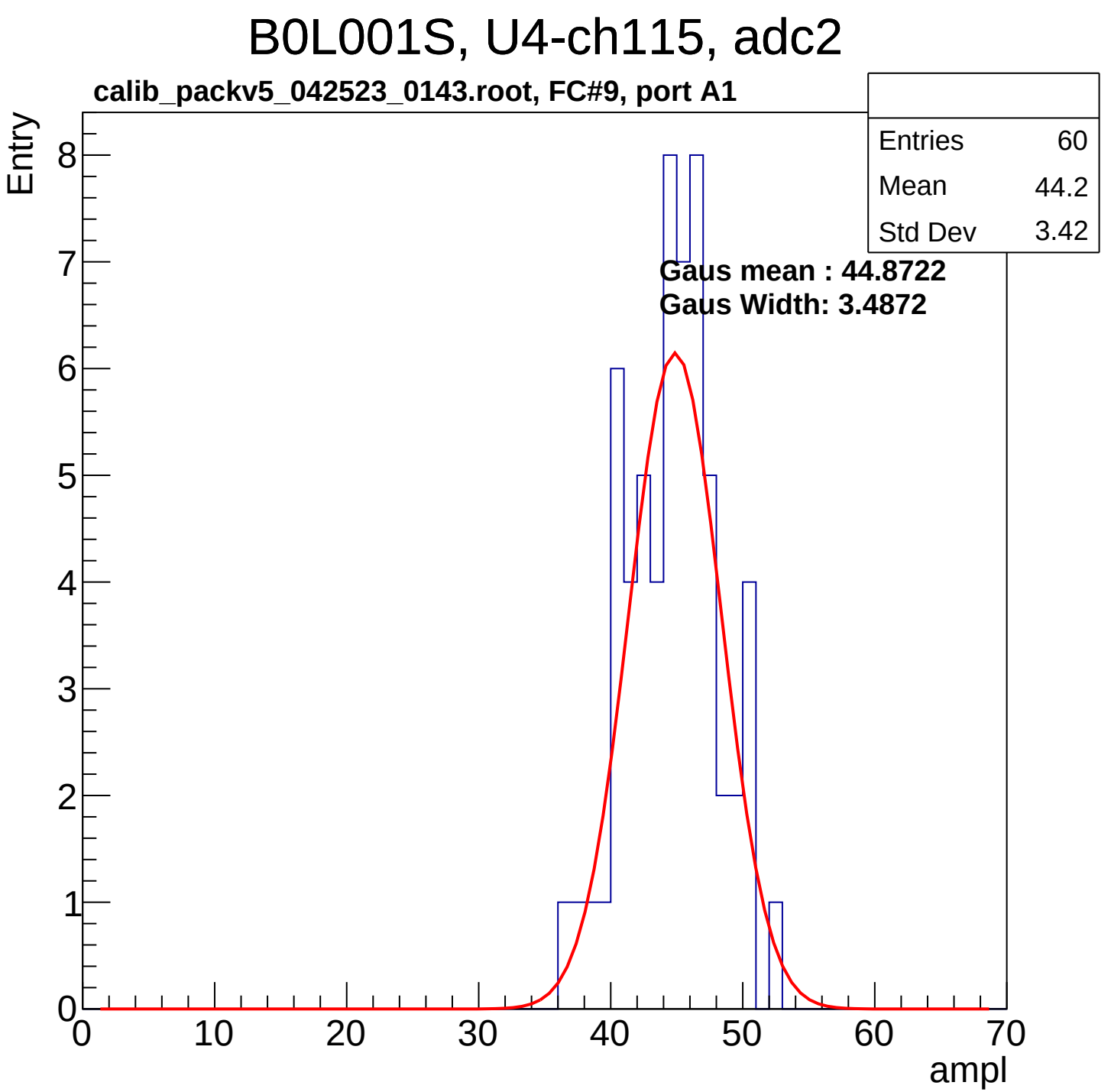
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch115, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 30.72
Std Dev 7.156
Gaus mean : 32.8343
Gaus Width: 3.6395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch115, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.61
Std Dev 3.126
Gaus mean : 38.1663
Gaus Width: 3.5173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch115, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.2
Std Dev 3.42
Gaus mean : 44.8722
Gaus Width: 3.4872
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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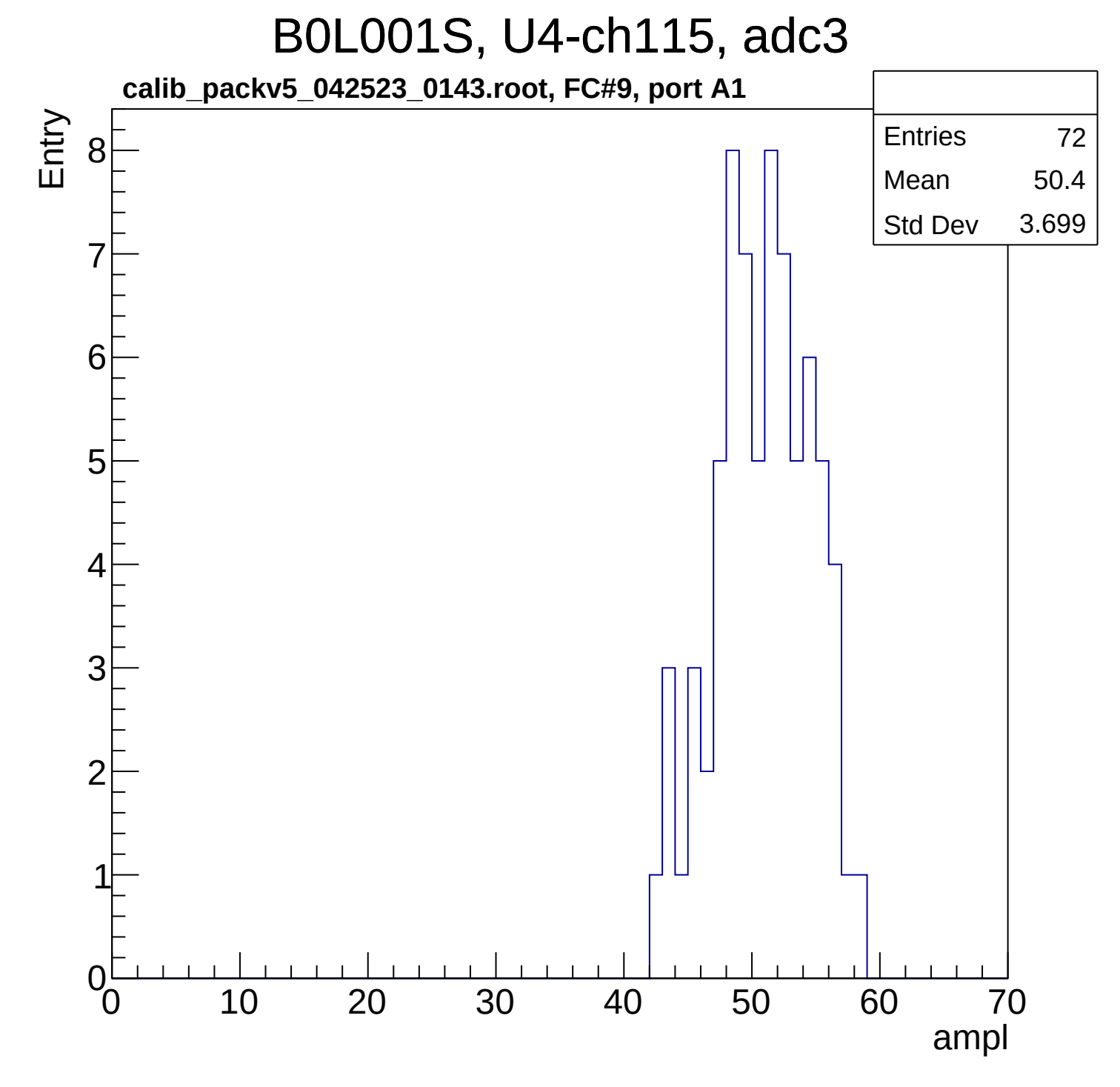
B0L001S, U4-ch115, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 50.4
Std Dev 3.699
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
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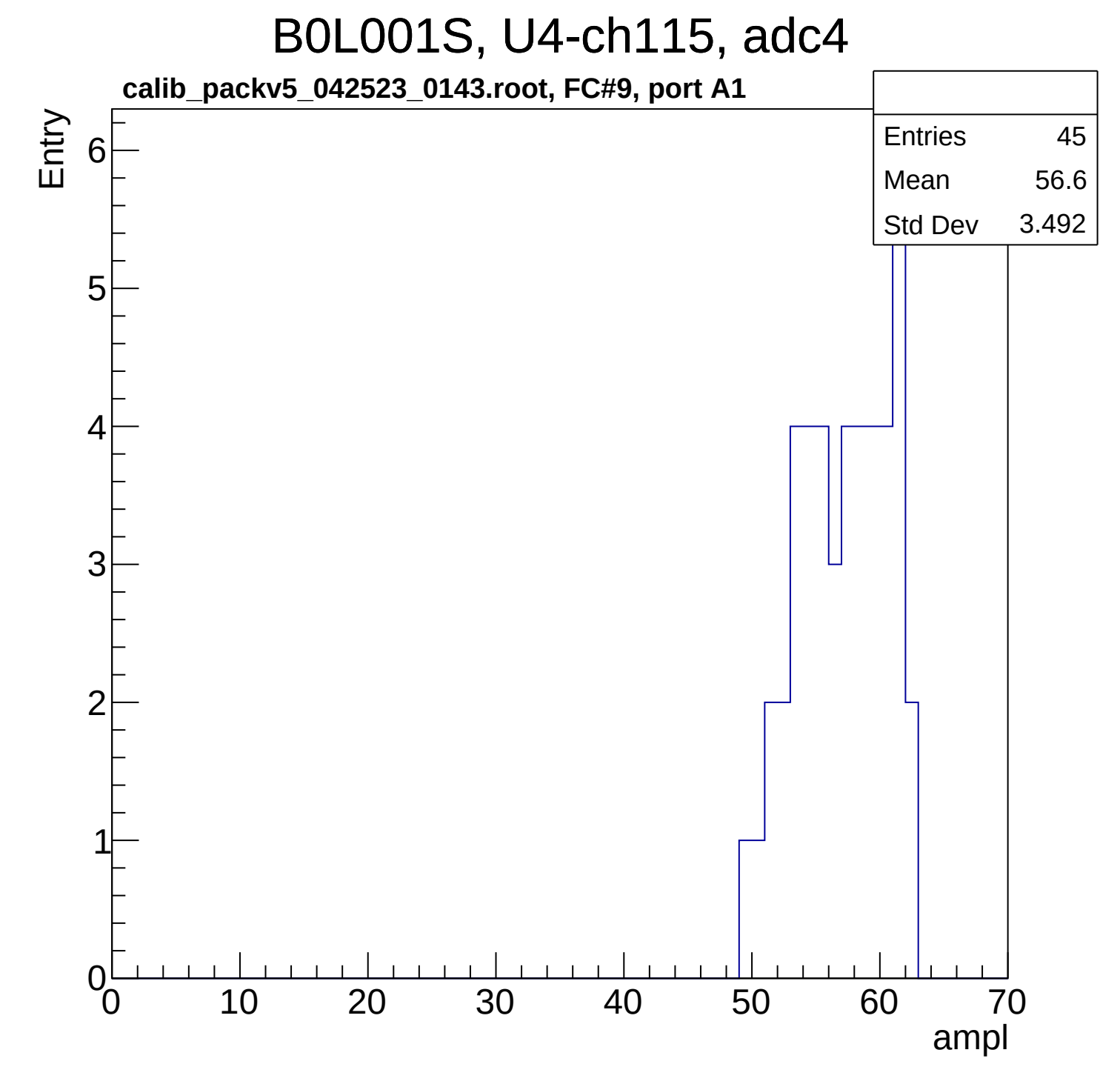
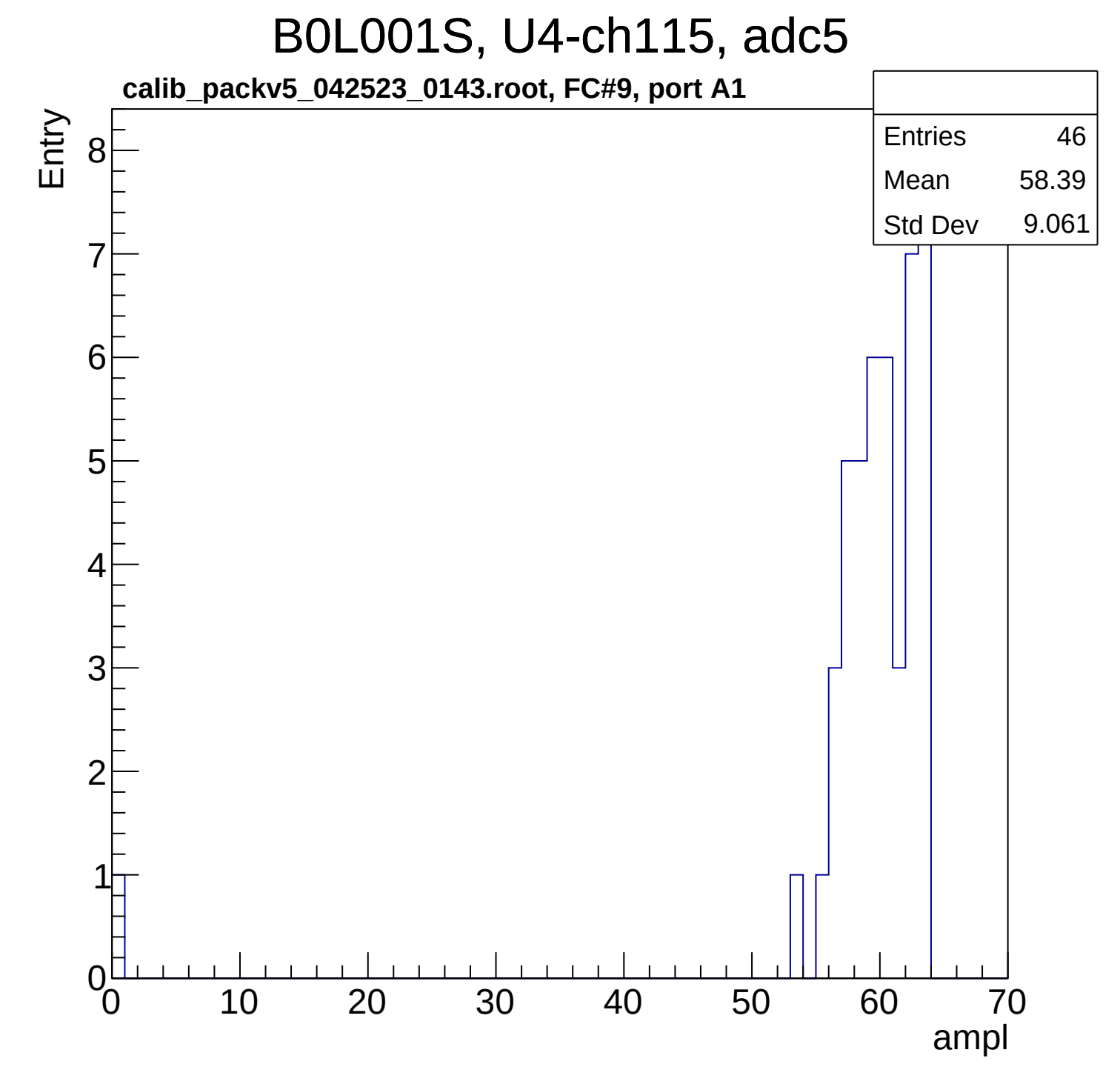

B0L001S, U4-ch115, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 56.6
Std Dev 3.492
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch115, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.39
Std Dev 9.061
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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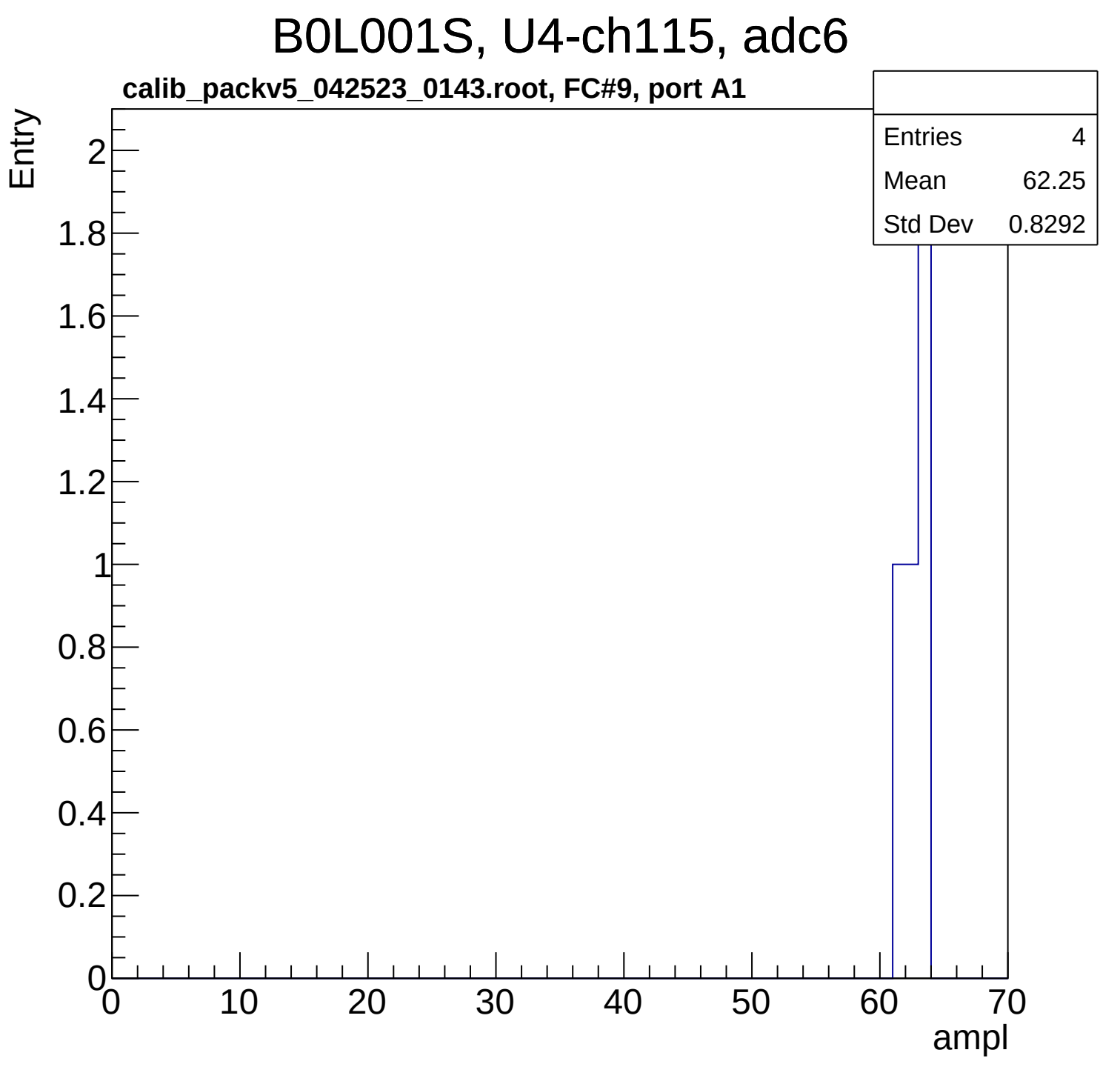
B0L001S, U4-ch115, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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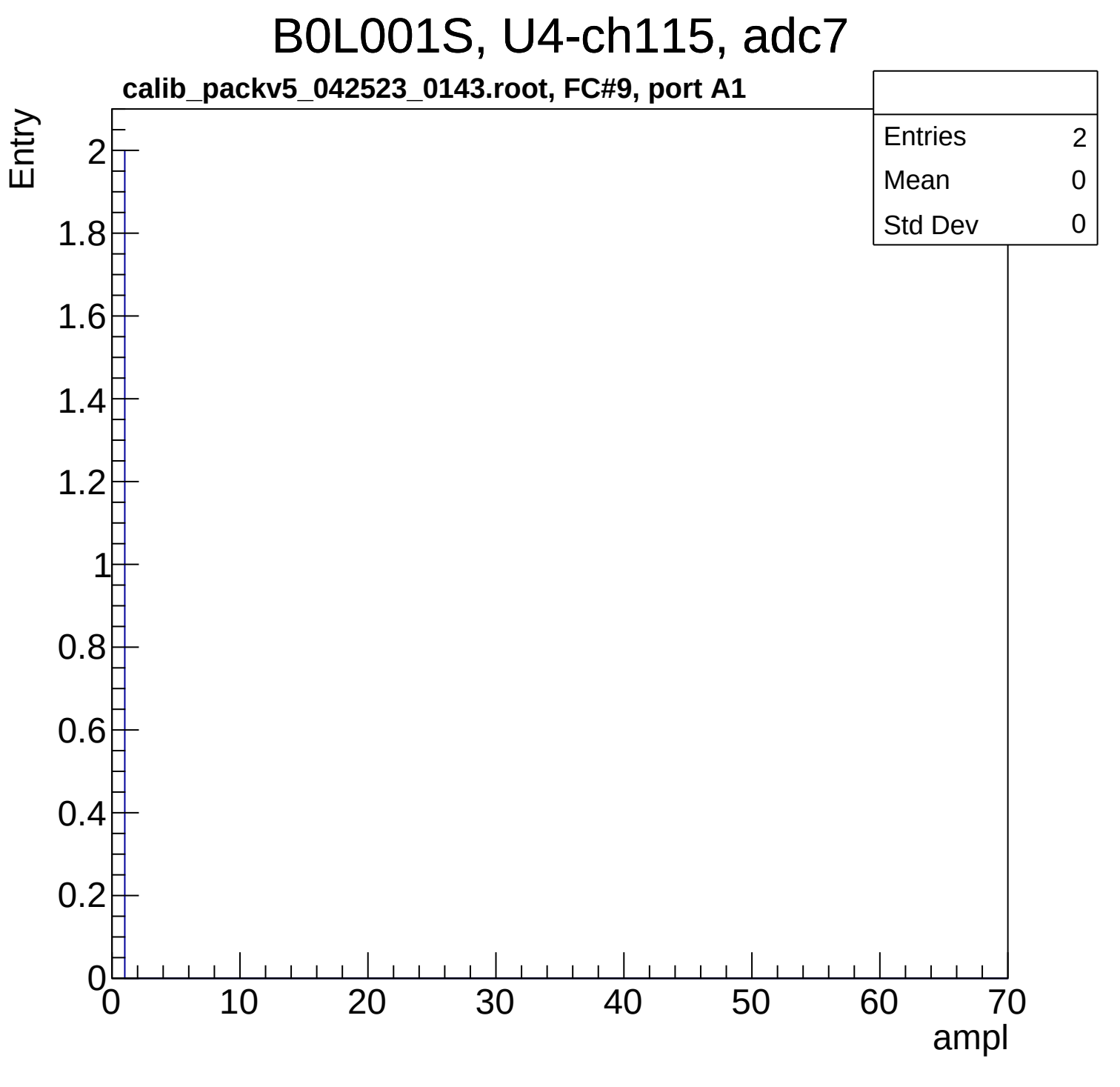
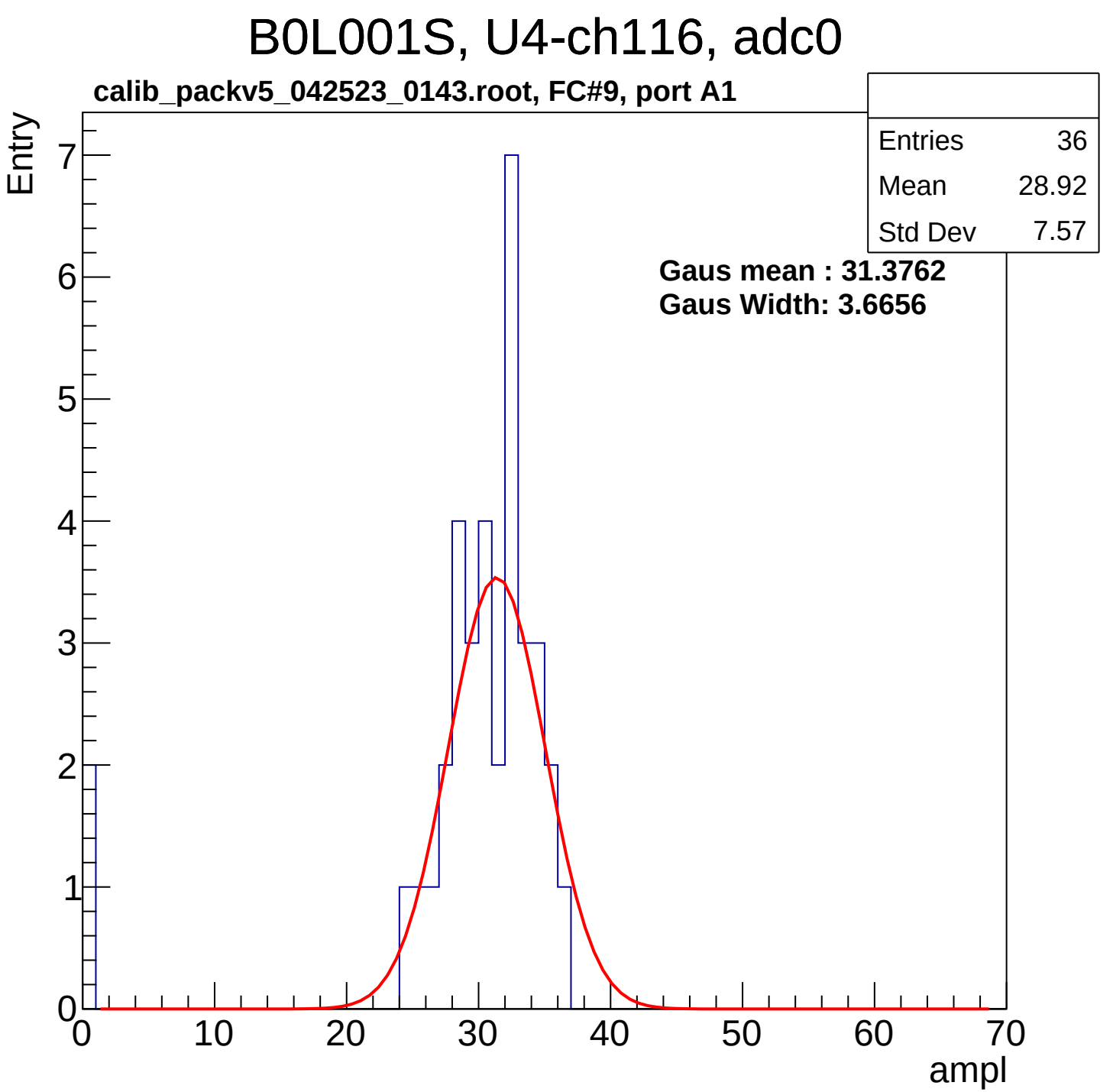
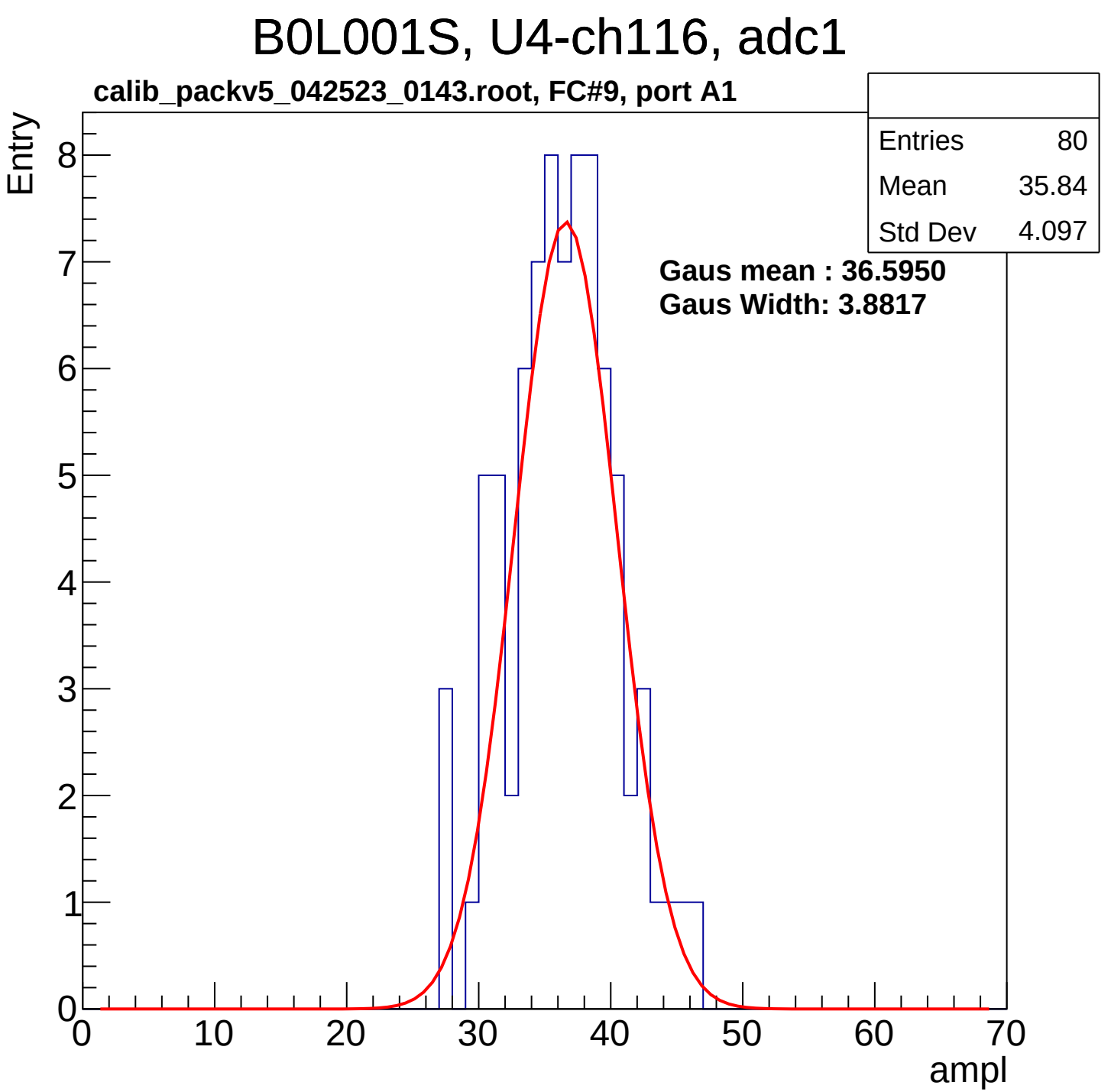

B0L001S, U4-ch115, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch116, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 28.92
Std Dev 7.57
Gaus mean : 31.3762
Gaus Width: 3.6656
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch116, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 35.84
Std Dev 4.097
Gaus mean : 36.5950
Gaus Width: 3.8817
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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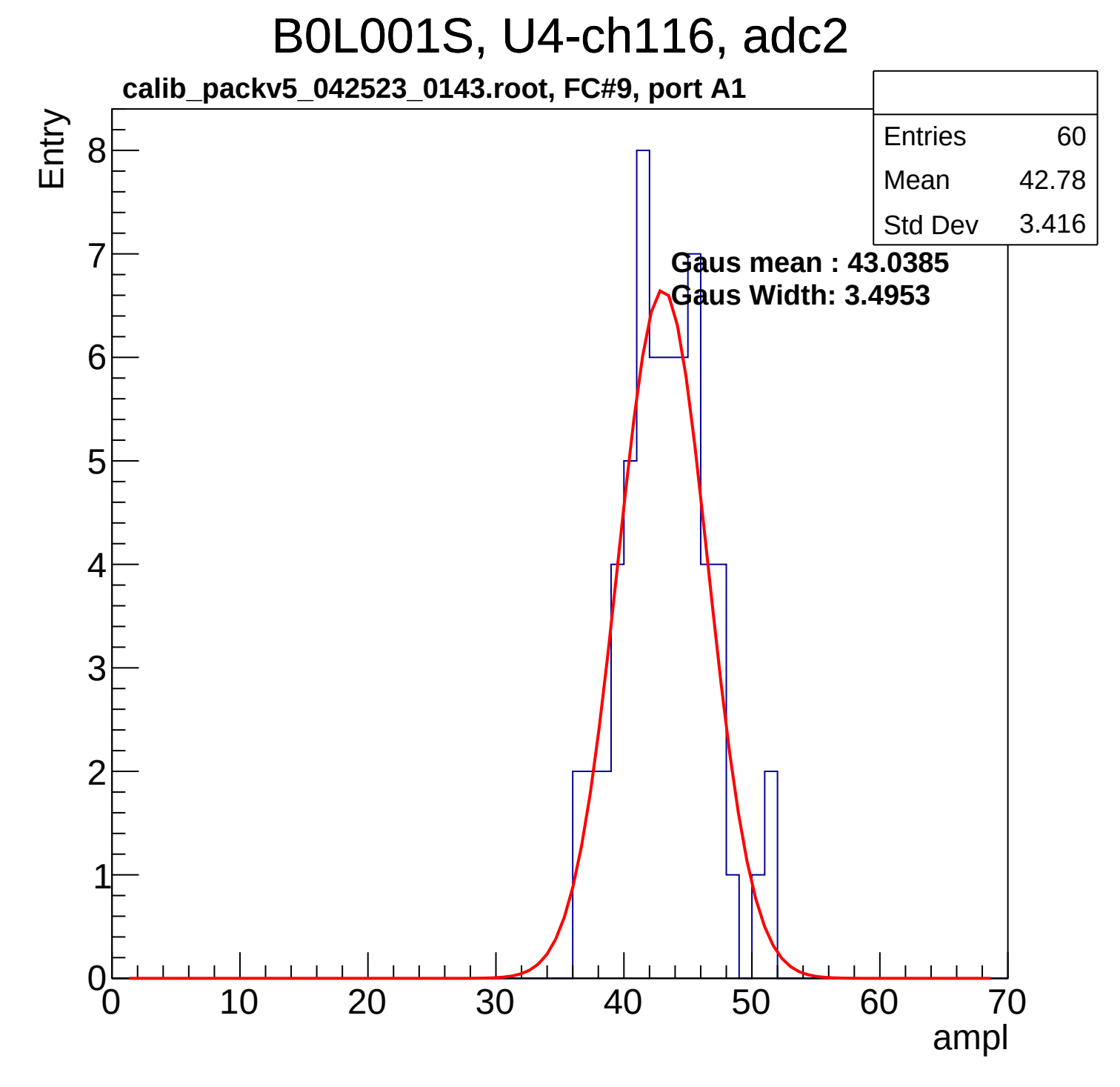
B0L001S, U4-ch116, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 42.78
Std Dev 3.416
Gaus mean : 43.0385
Gaus Width: 3.4953
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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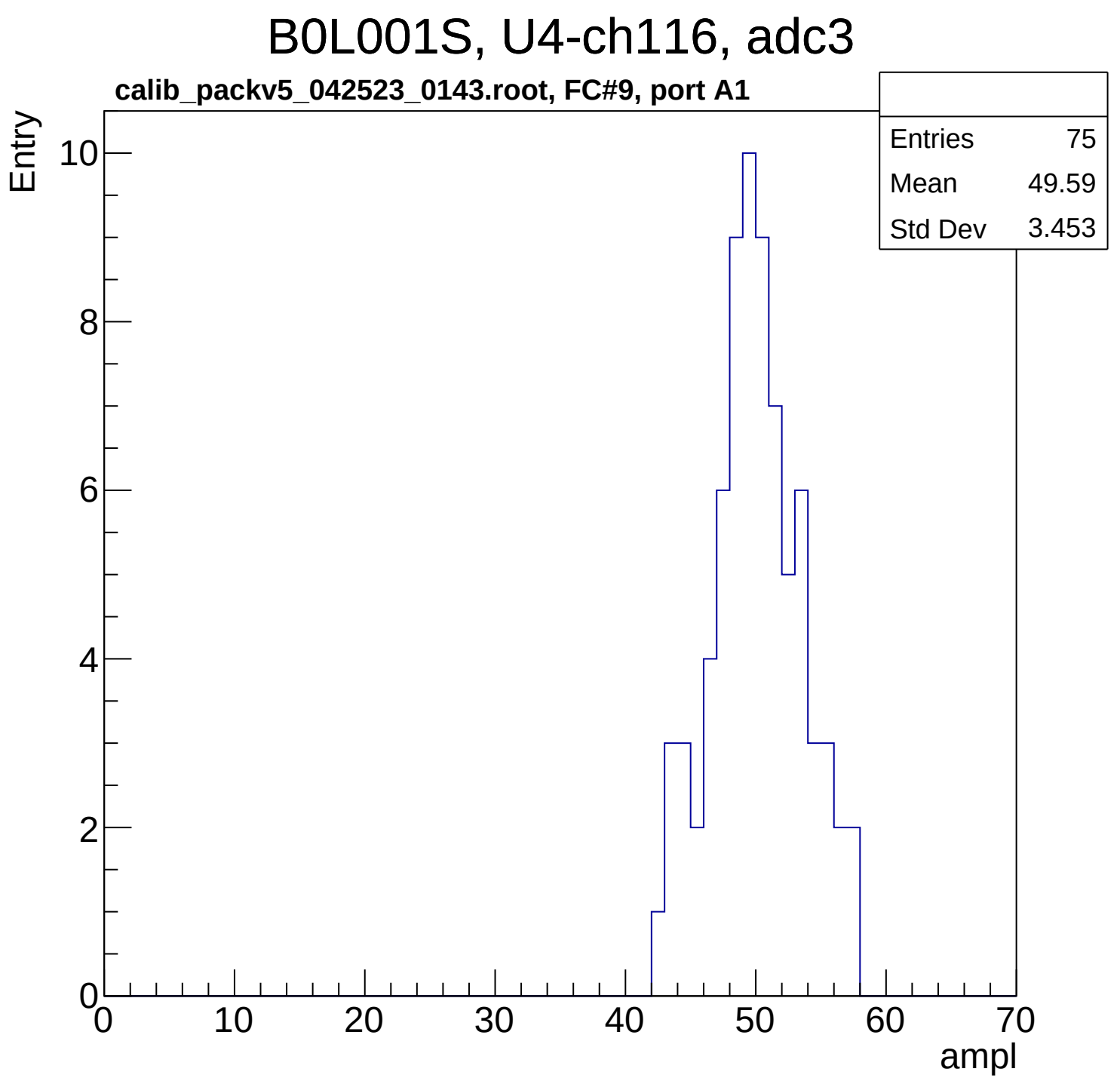
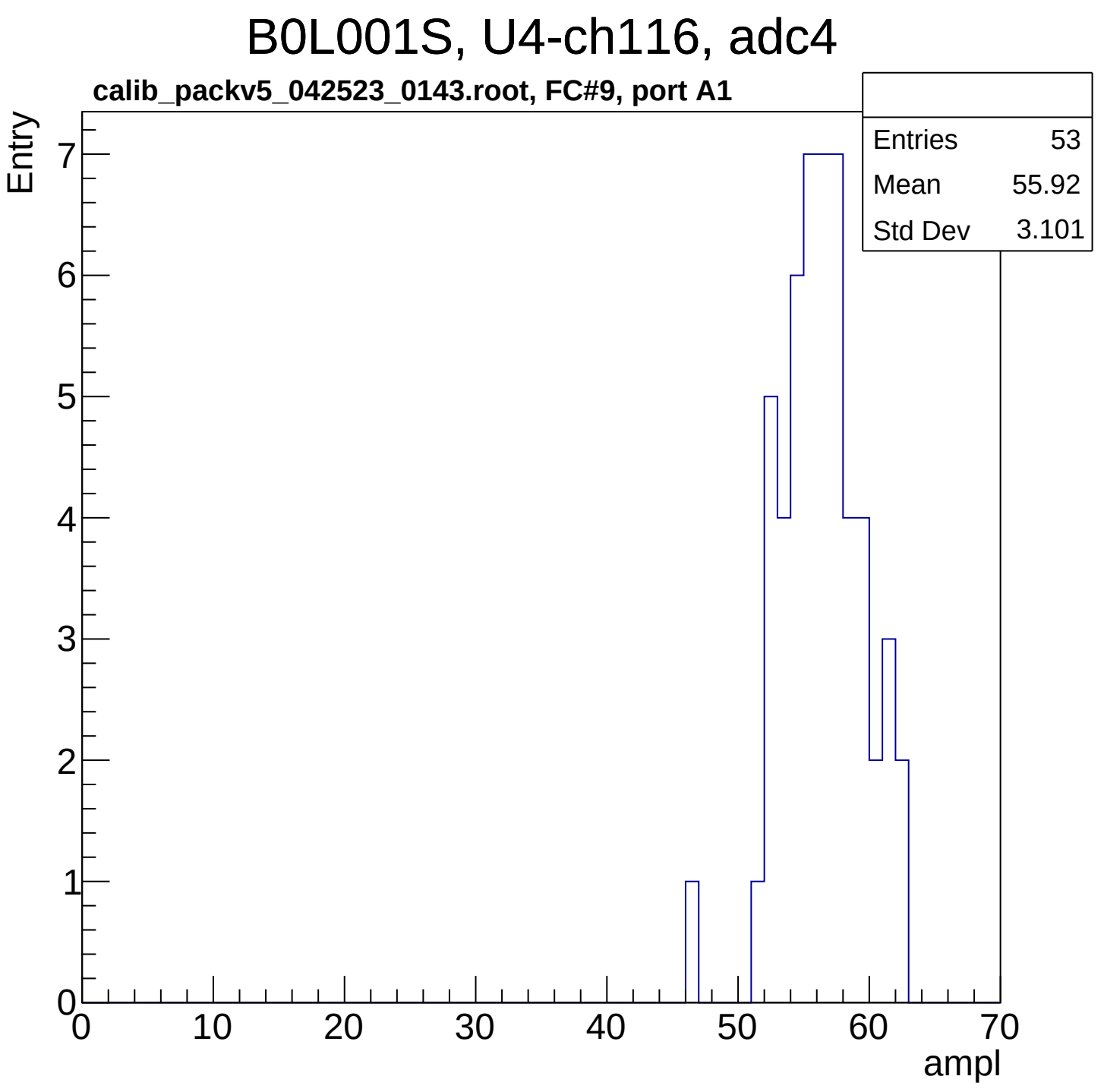
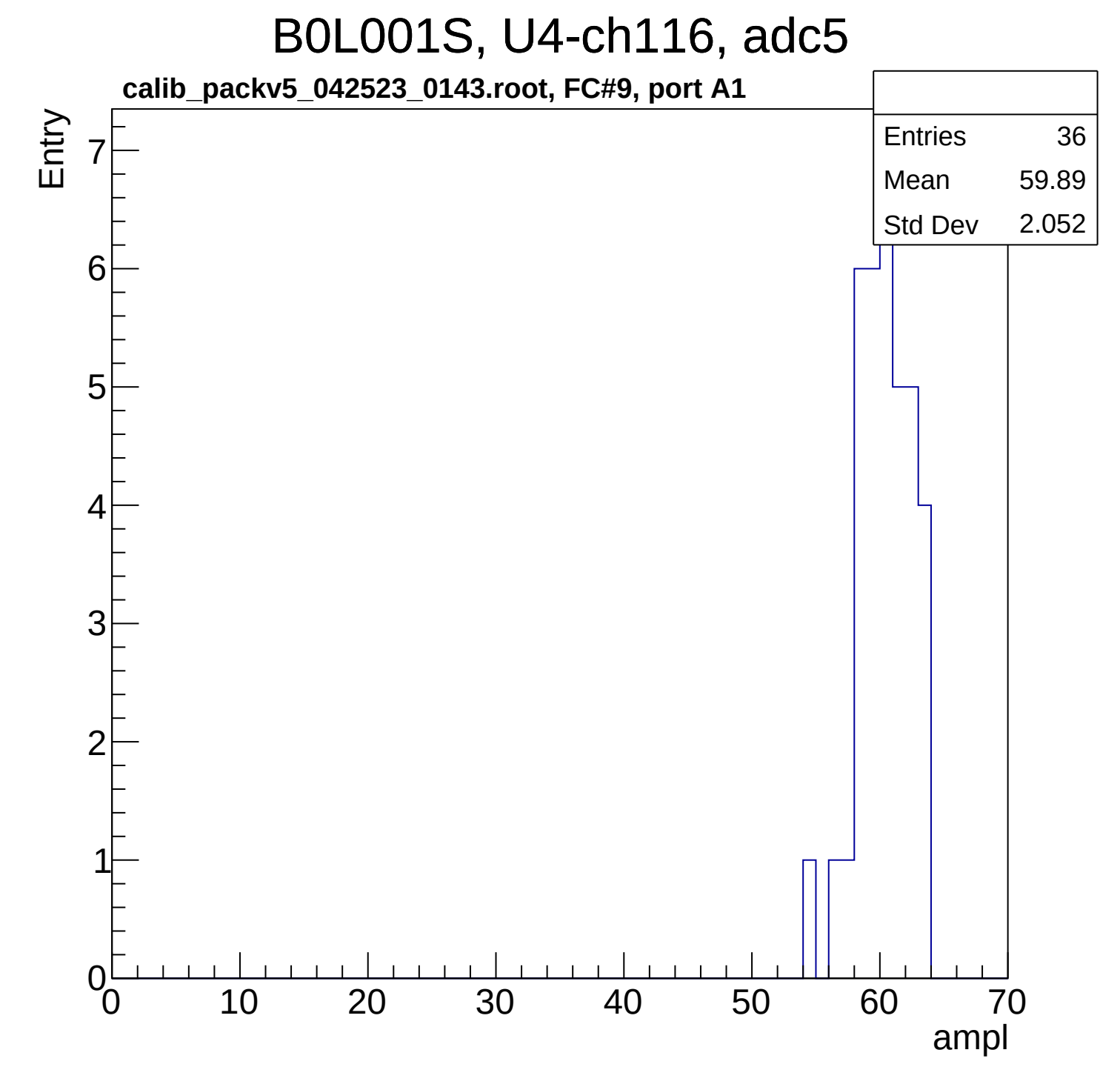
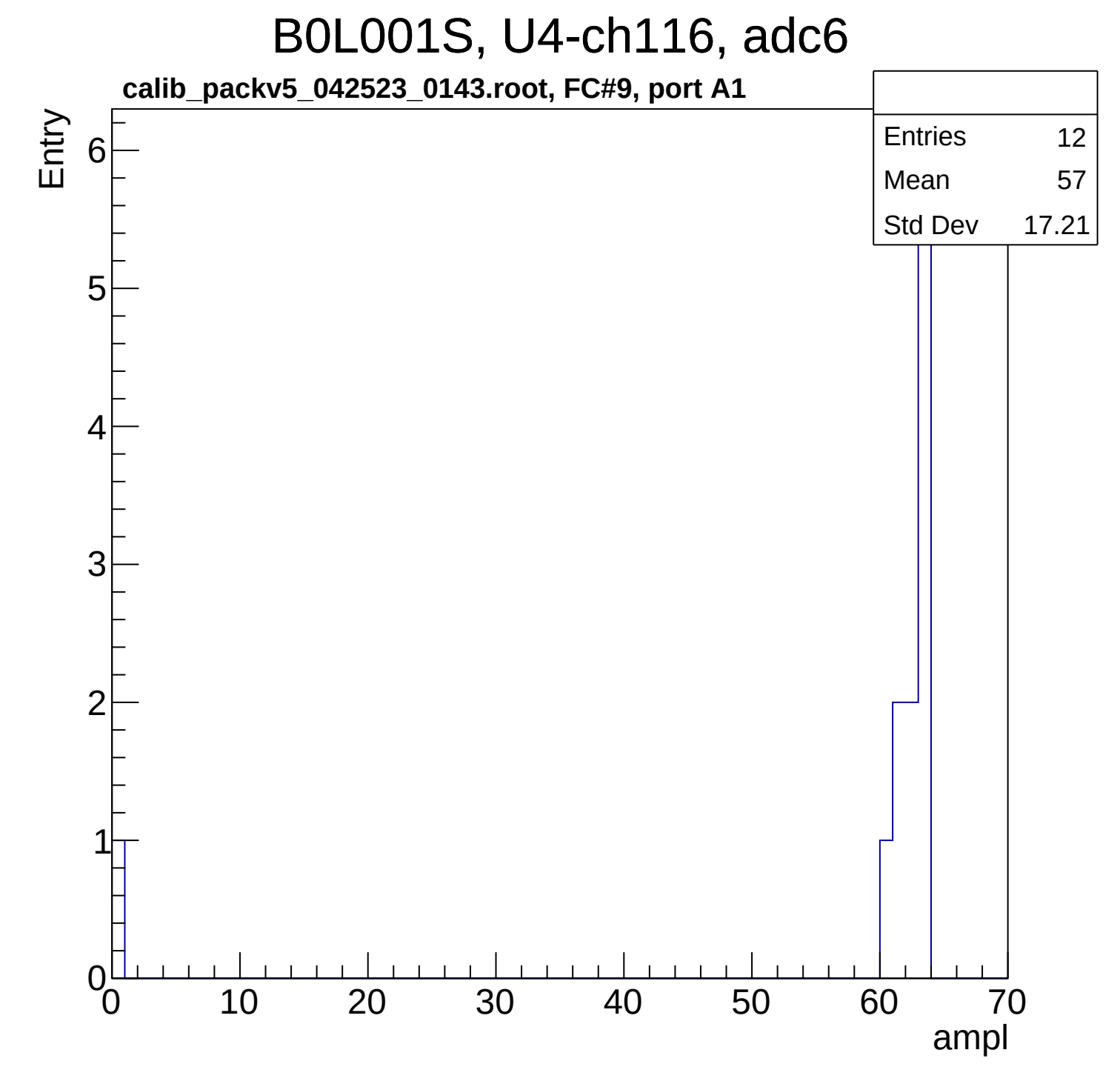
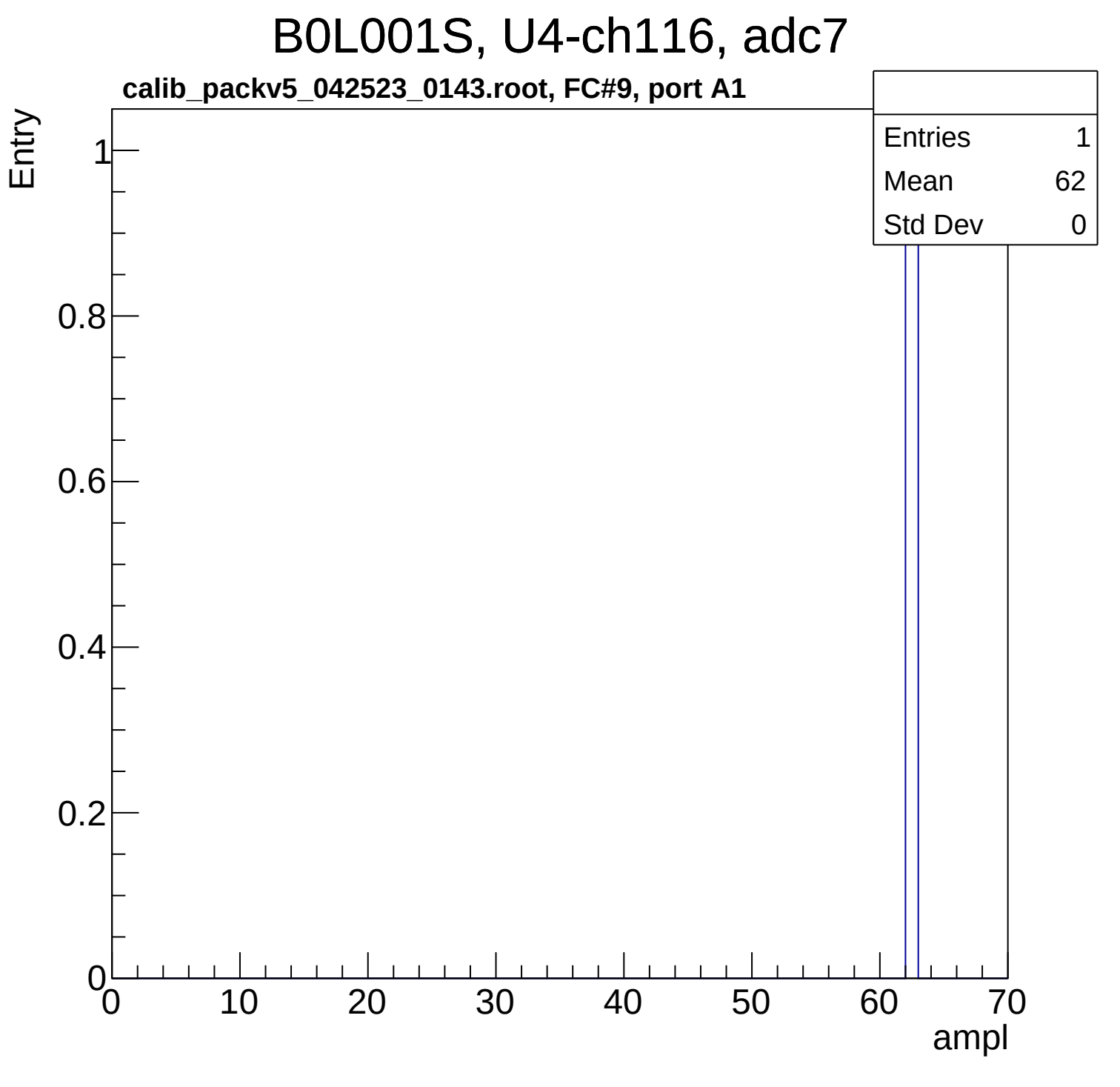

B0L001S, U4-ch116, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 49.59
Std Dev 3.453
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch116, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.92
Std Dev 3.101
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch116, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.89
Std Dev 2.052
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch116, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 57
Std Dev 17.21
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch116, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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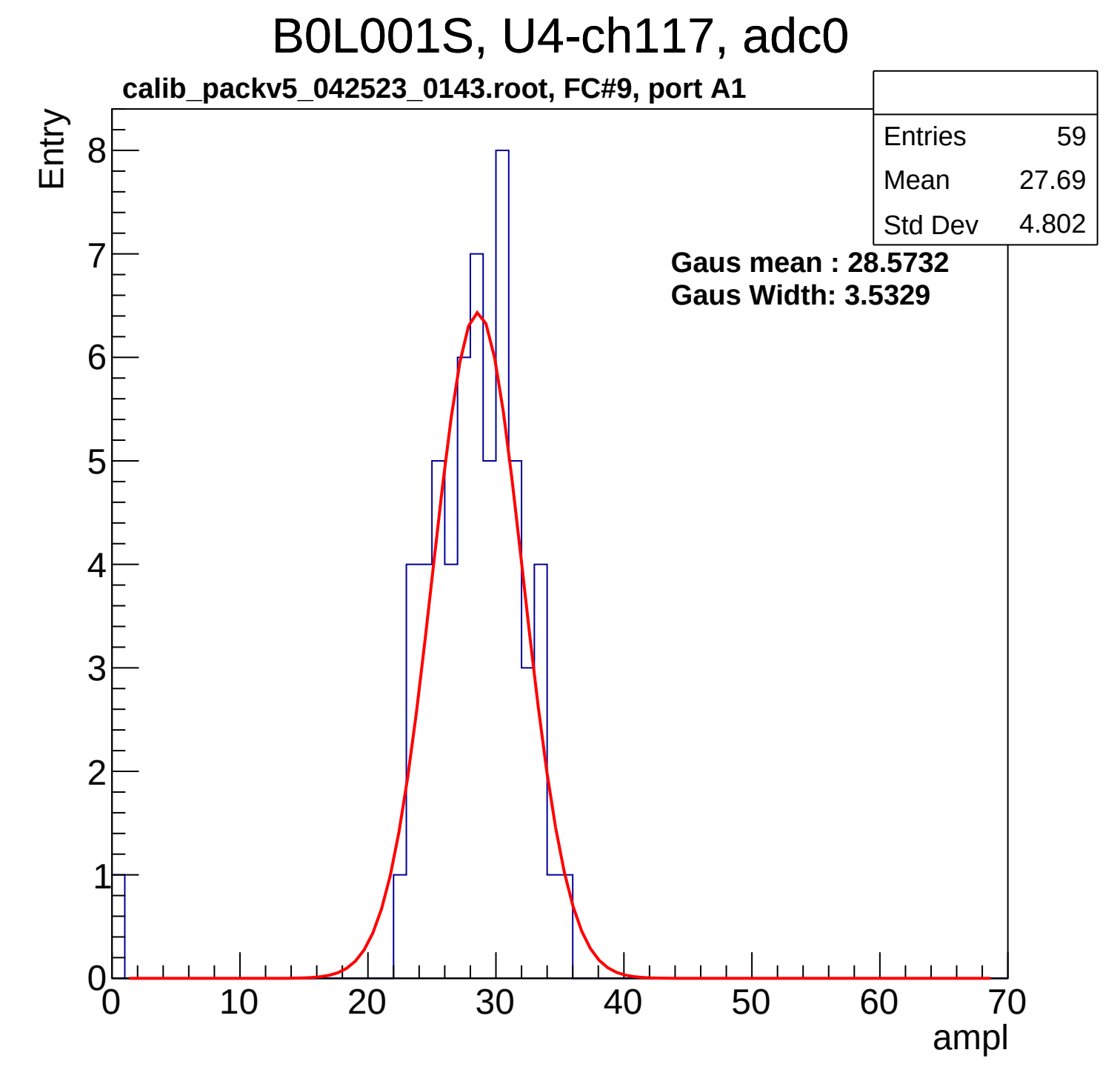
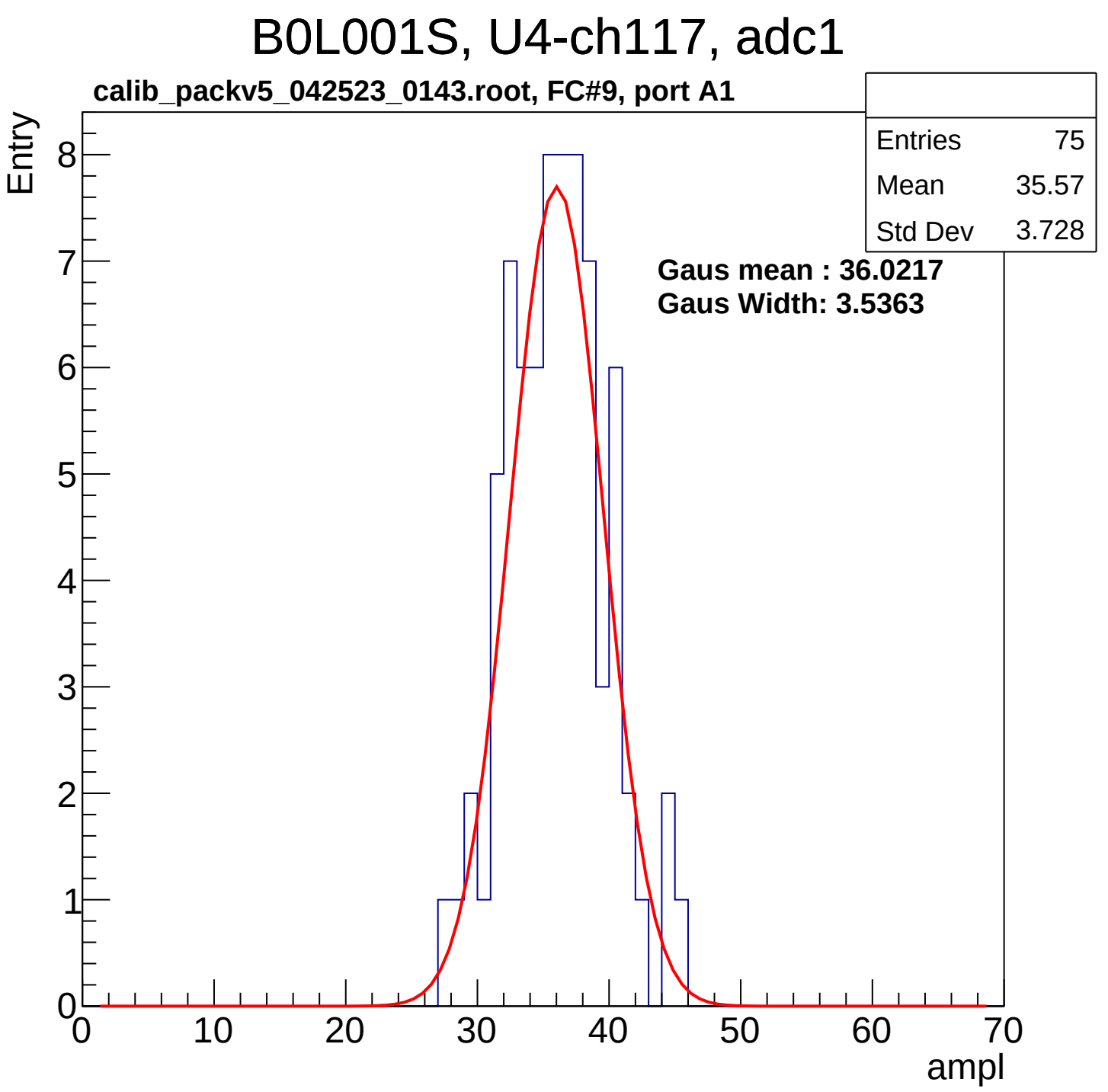
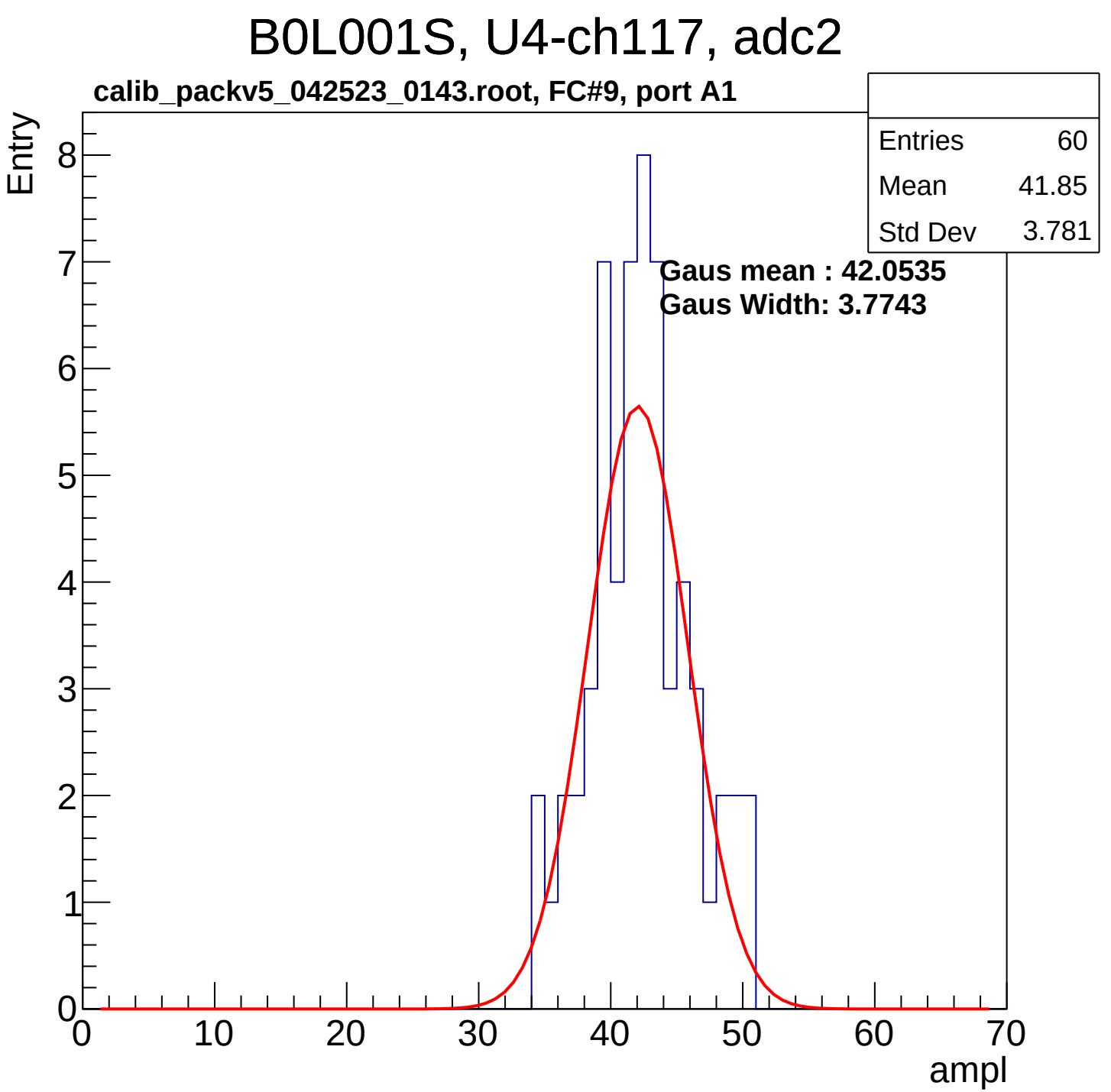
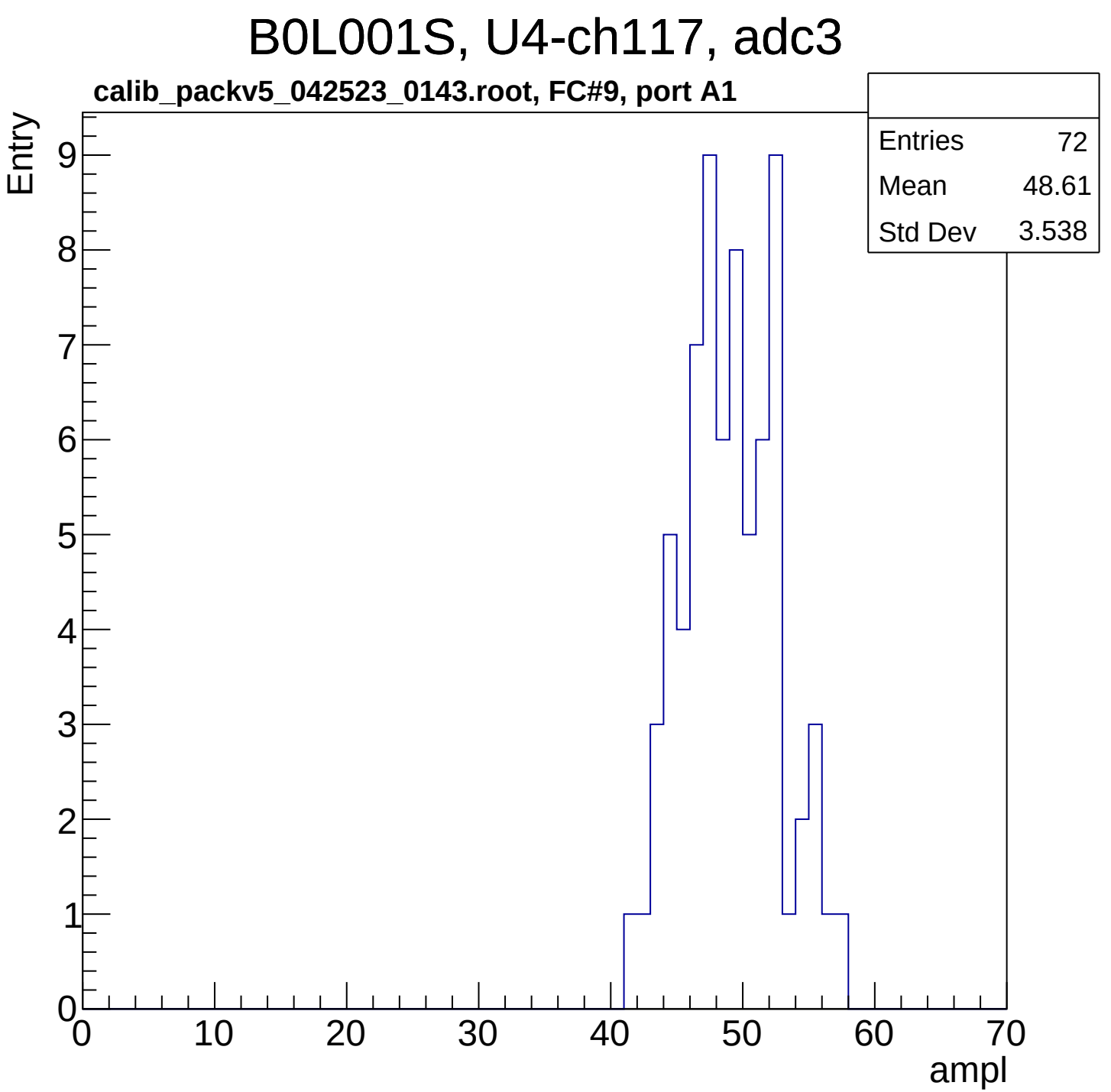
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch117, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 27.69
Std Dev 4.802
Gaus mean : 28.5732
Gaus Width: 3.5329
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch117, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 35.57
Std Dev 3.728
Gaus mean : 36.0217
Gaus Width: 3.5363
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch117, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 41.85
Std Dev 3.781
Gaus mean : 42.0535
Gaus Width: 3.7743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch117, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 48.61
Std Dev 3.538
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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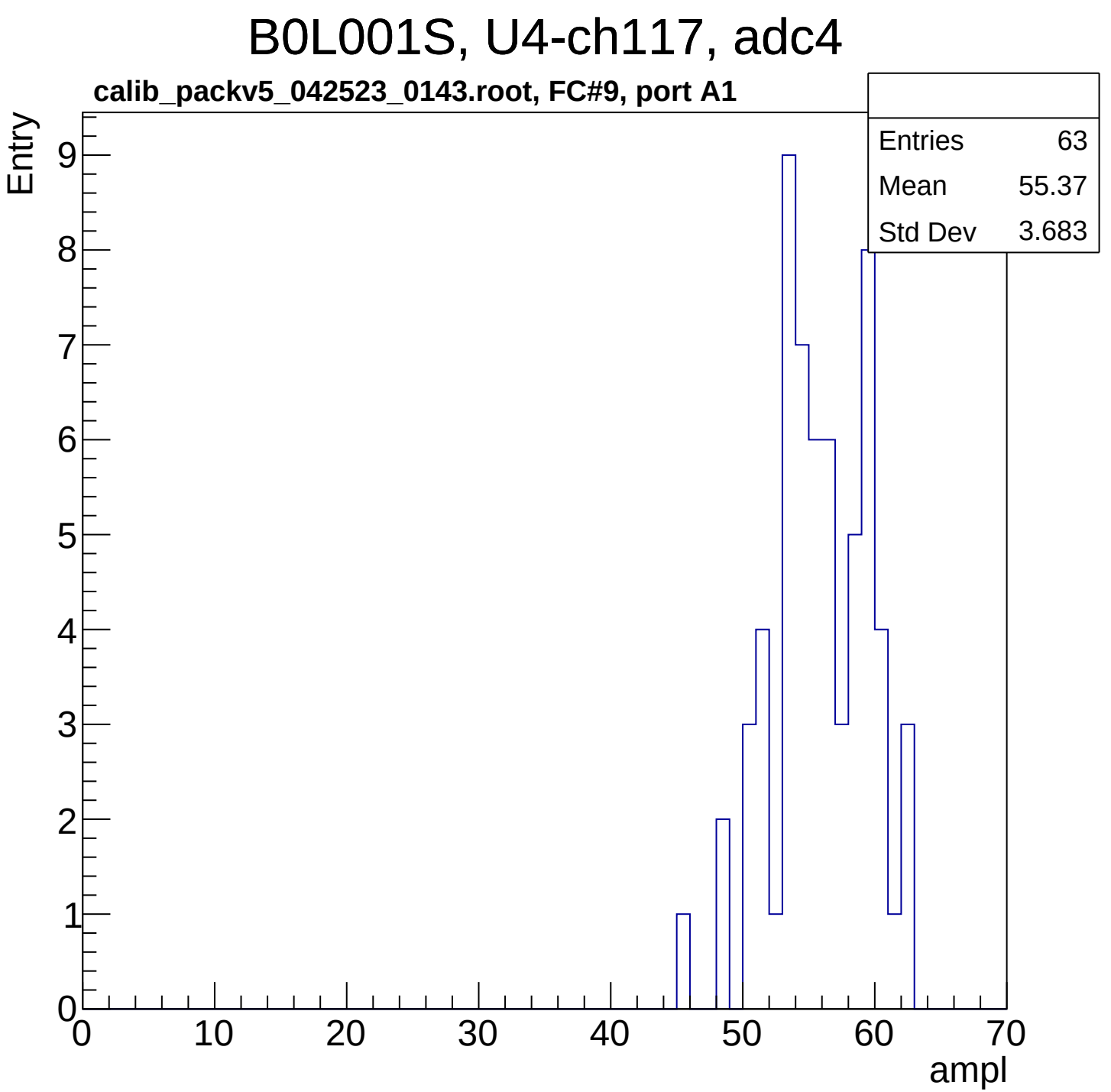
B0L001S, U4-ch117, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 55.37
Std Dev 3.683
Entry
ampl
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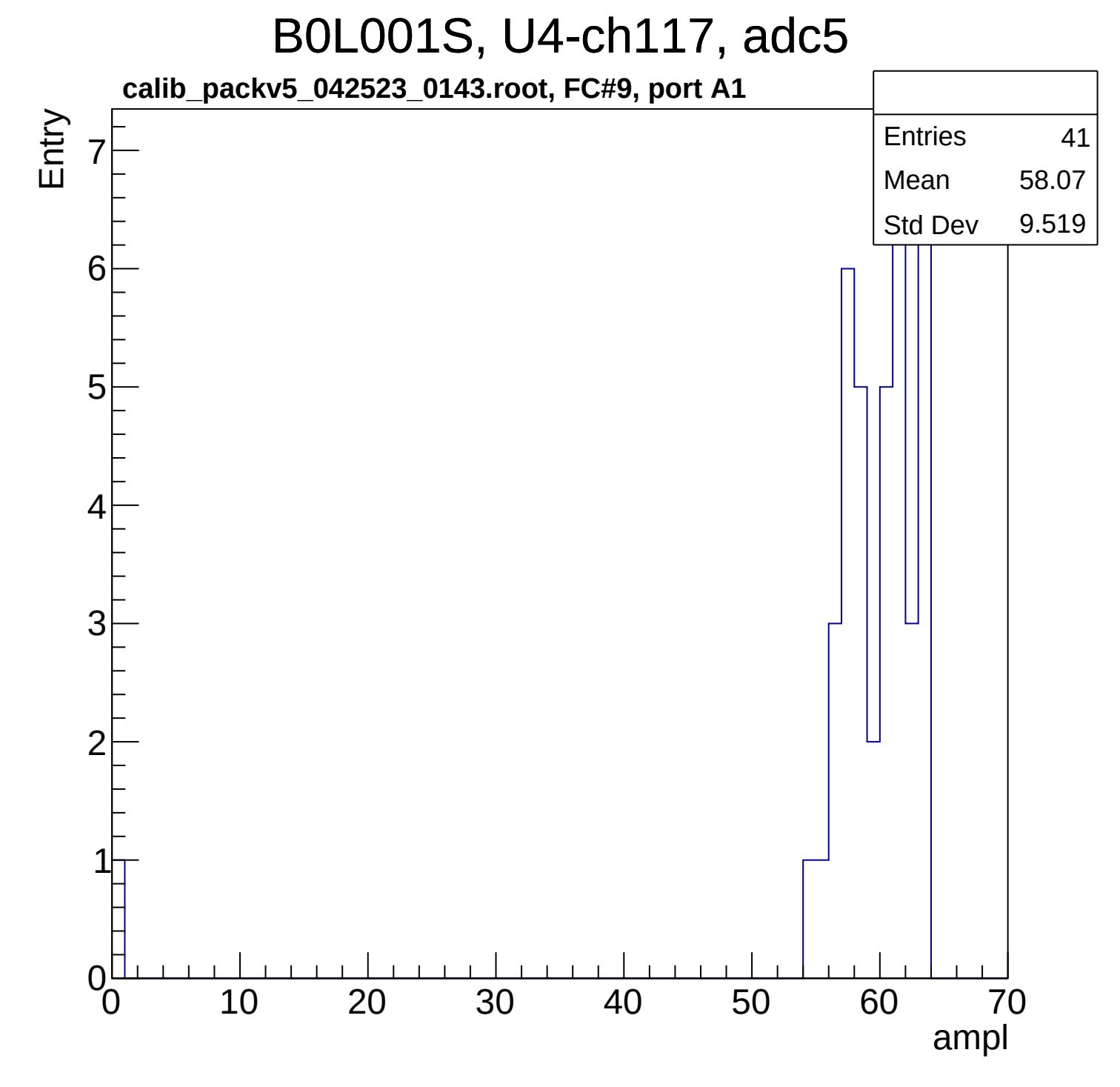
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch117, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.07
Std Dev 9.519
Entry
ampl
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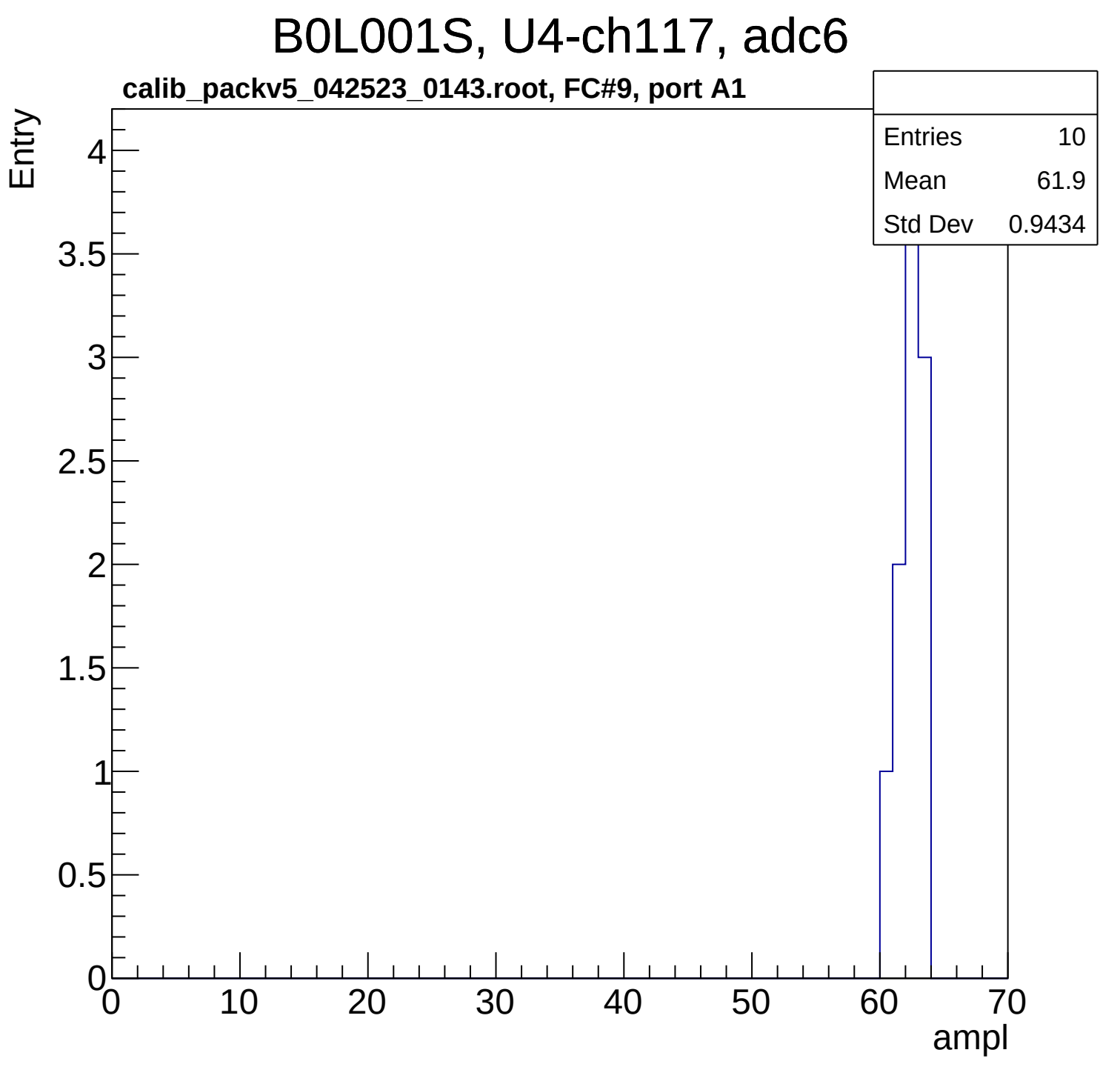
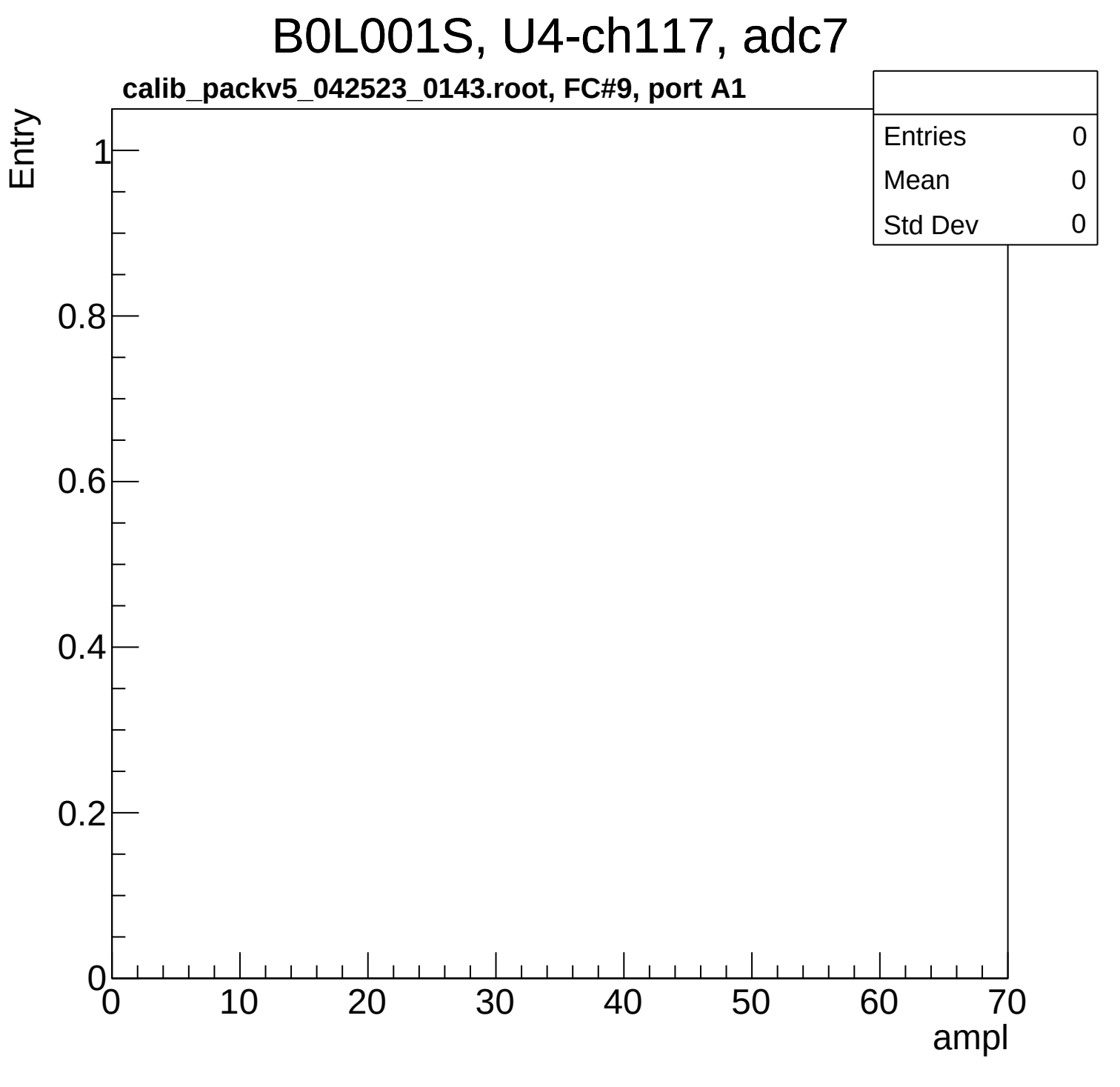
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U4-ch117, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61.9
Std Dev 0.9434
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch117, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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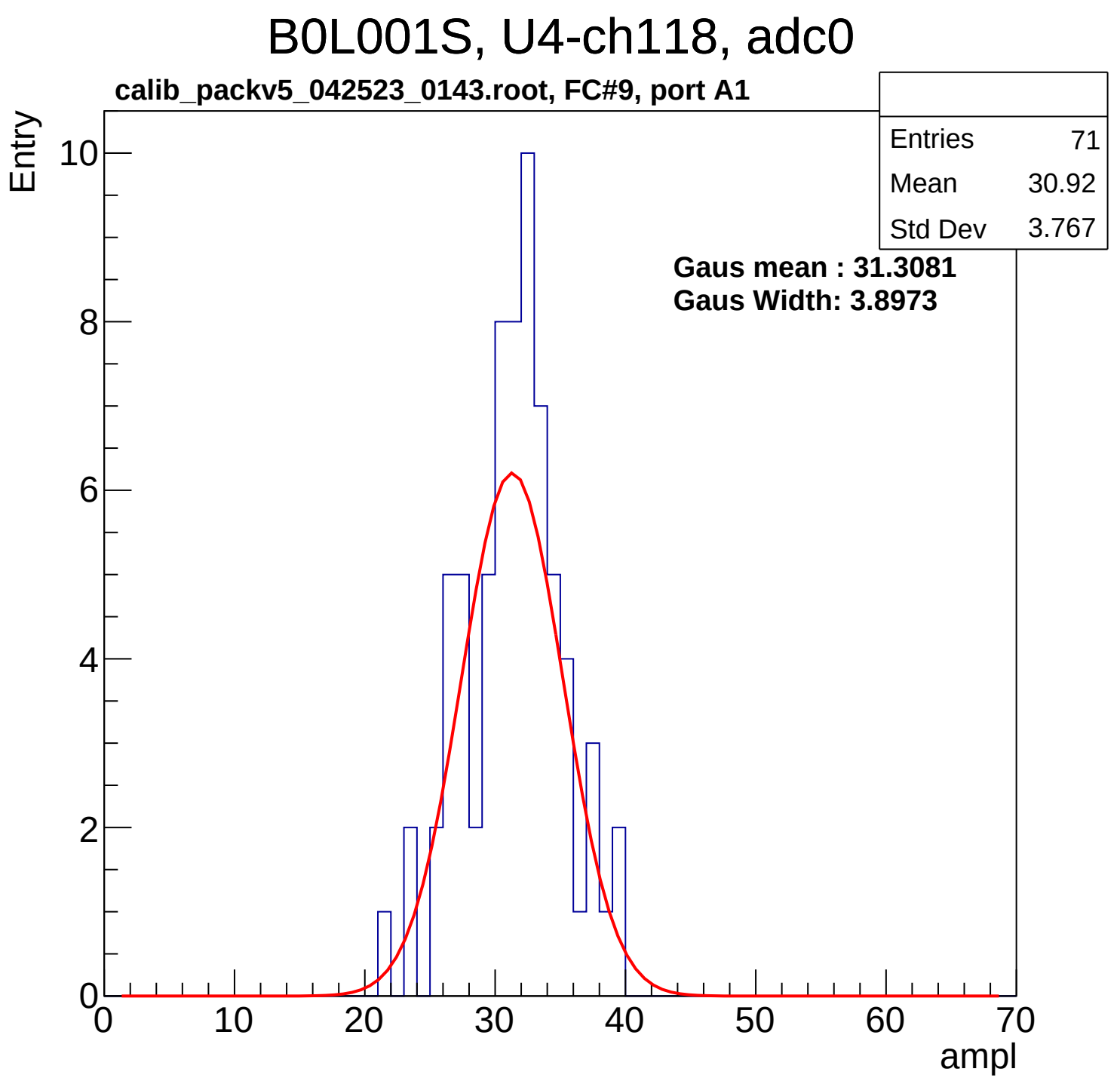
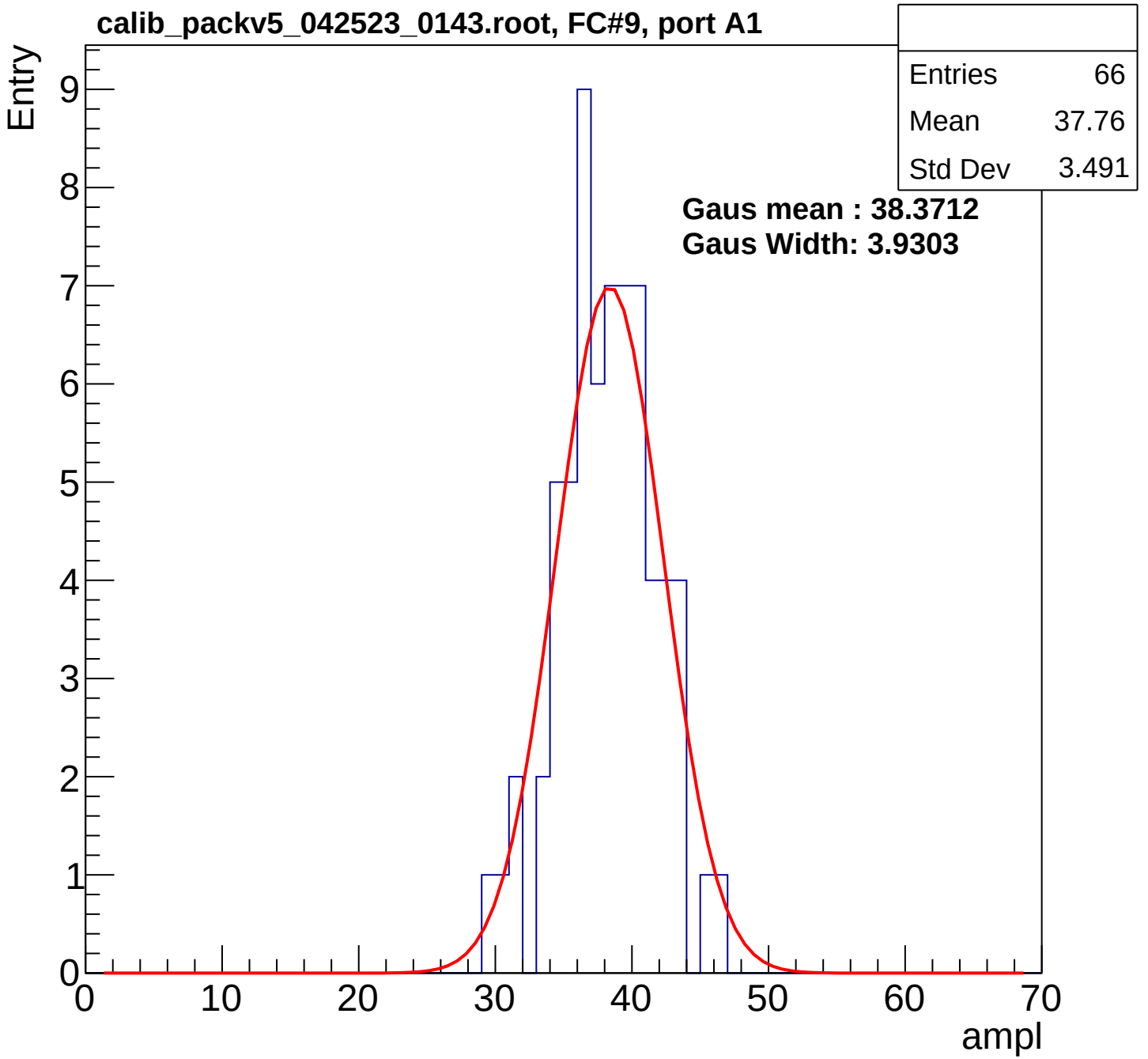
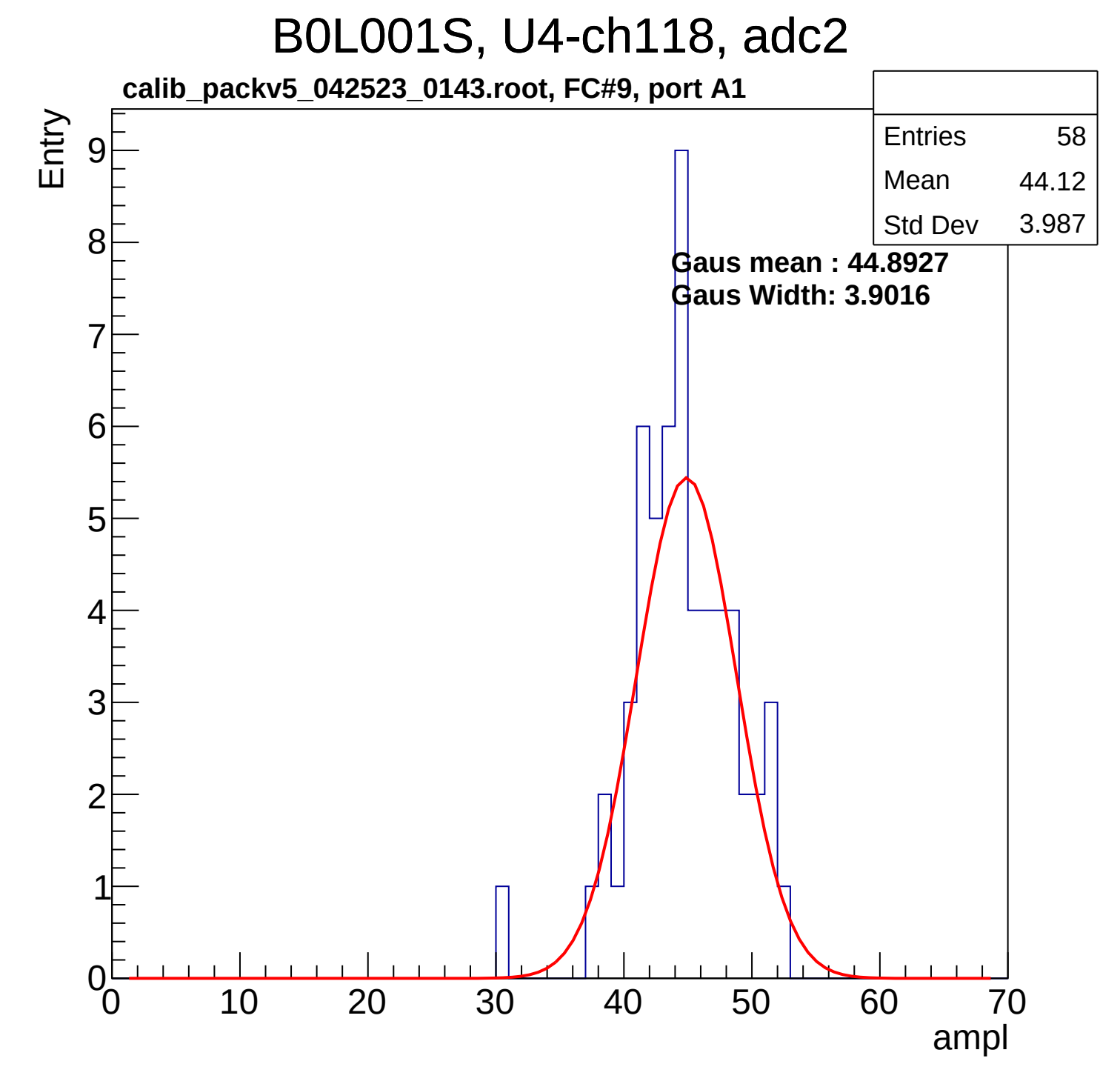
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch118, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.92
Std Dev 3.767
Gaus mean : 31.3081
Gaus Width: 3.8973
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch118, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.76
Std Dev 3.491
Gaus mean : 38.3712
Gaus Width: 3.9303
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch118, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.12
Std Dev 3.987
Gaus mean : 44.8927
Gaus Width: 3.9016
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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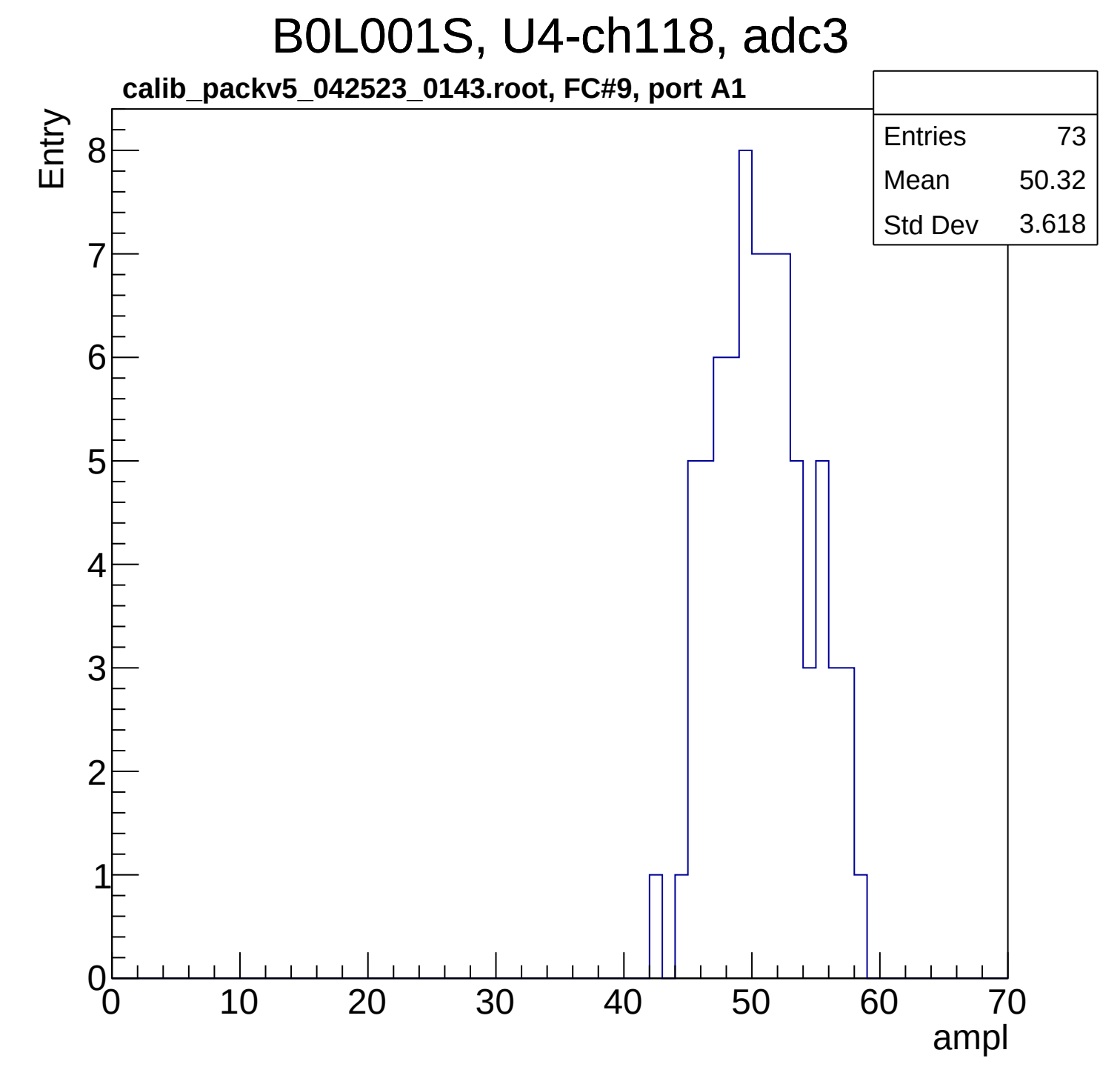
B0L001S, U4-ch118, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 50.32
Std Dev 3.618
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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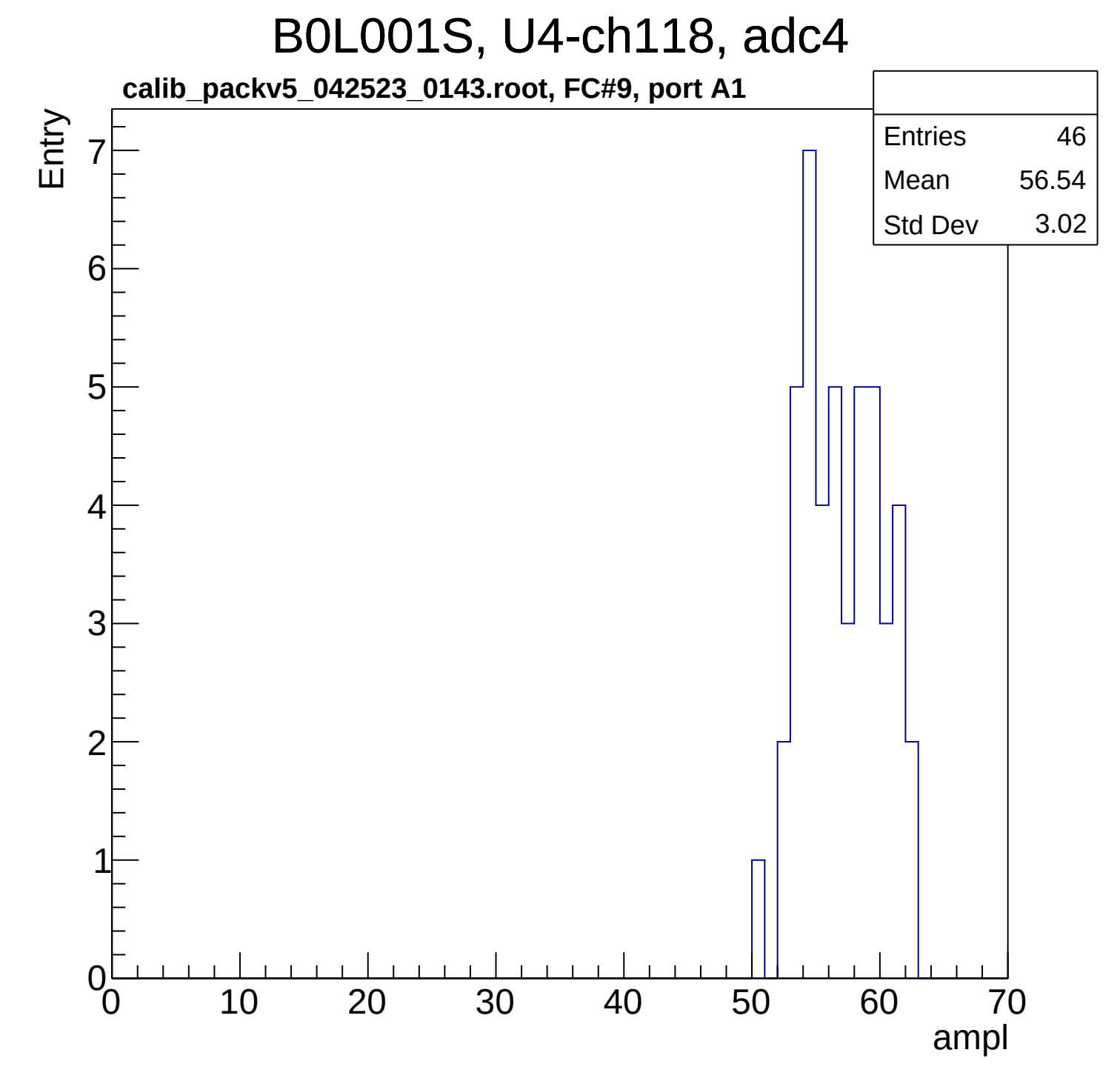
40
50
60
70


B0L001S, U4-ch118, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 56.54
Std Dev 3.02
Entry
ampl
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1
2
3
4
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0
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40
50
60
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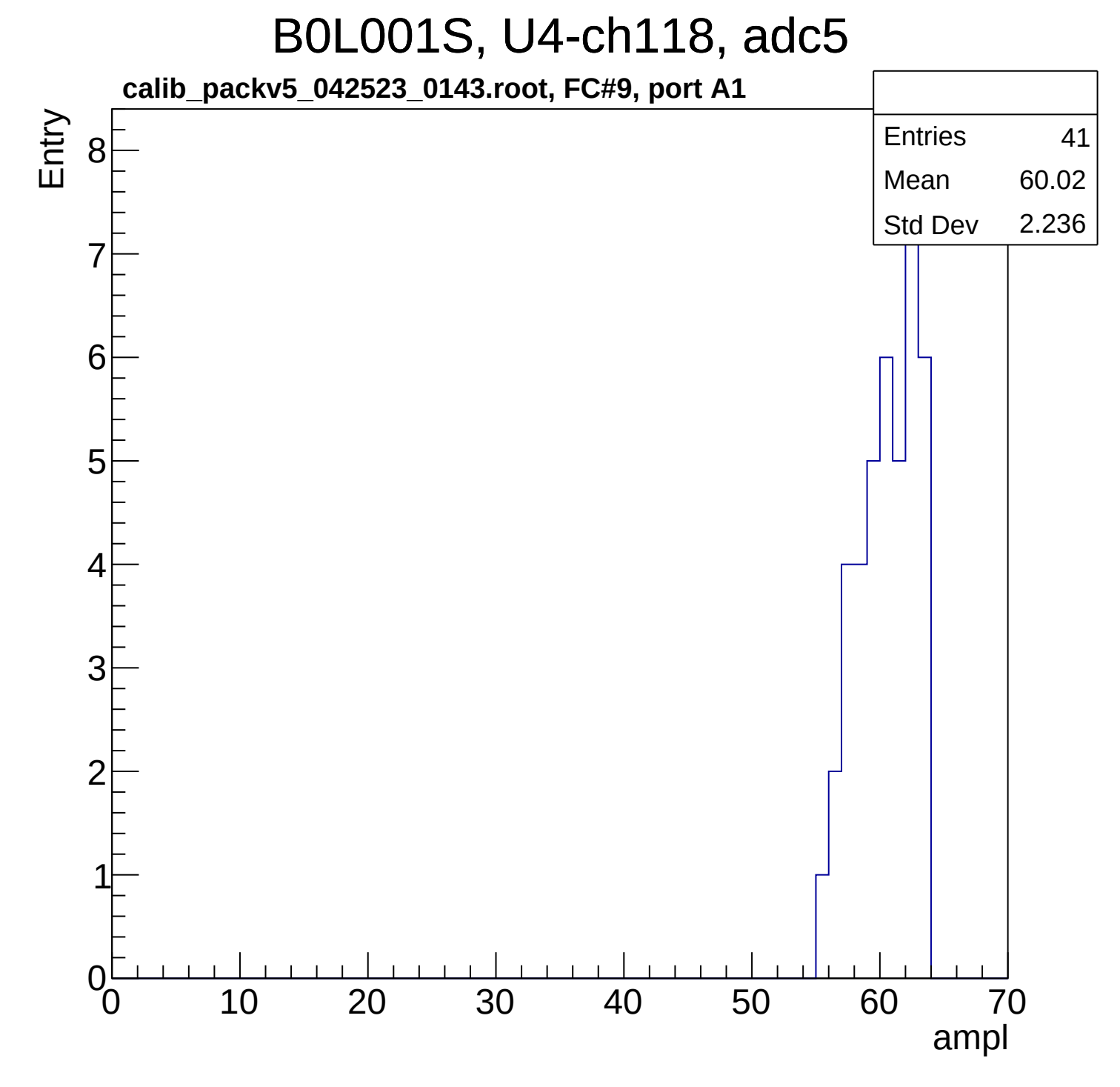
B0L001S, U4-ch118, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 60.02
Std Dev 2.236
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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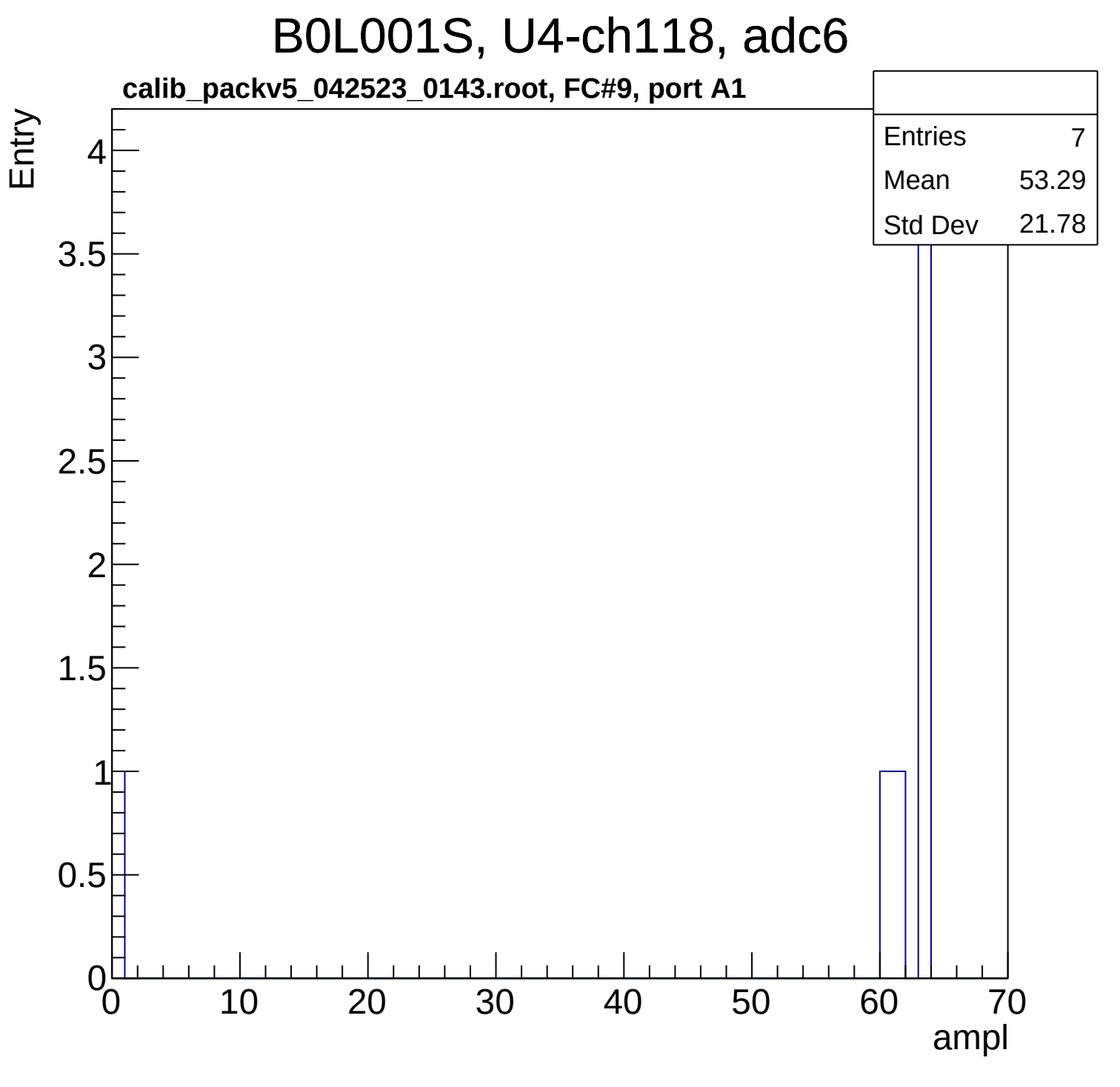
B0L001S, U4-ch118, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 53.29
Std Dev 21.78
Entry
ampl
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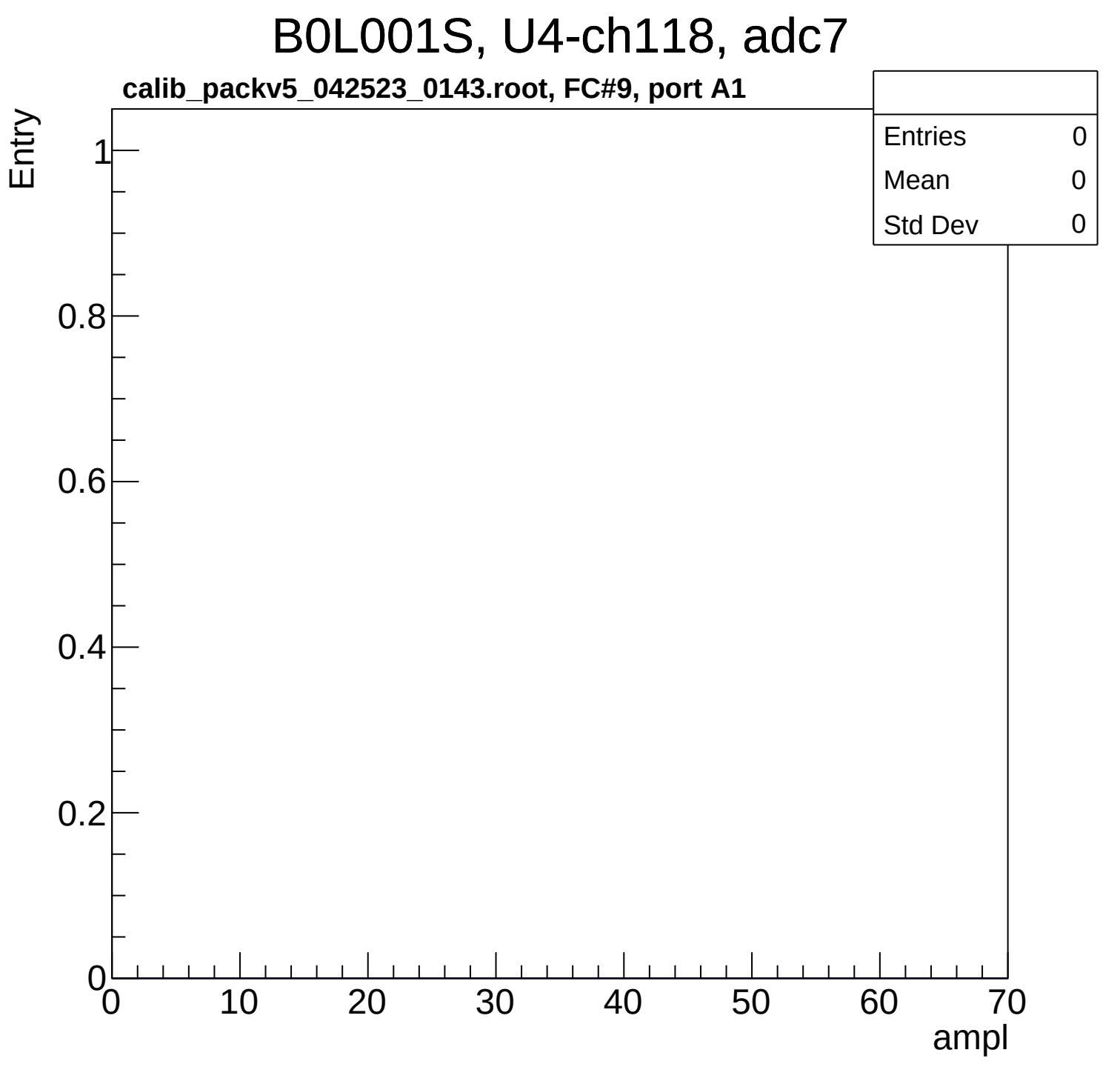
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U4-ch118, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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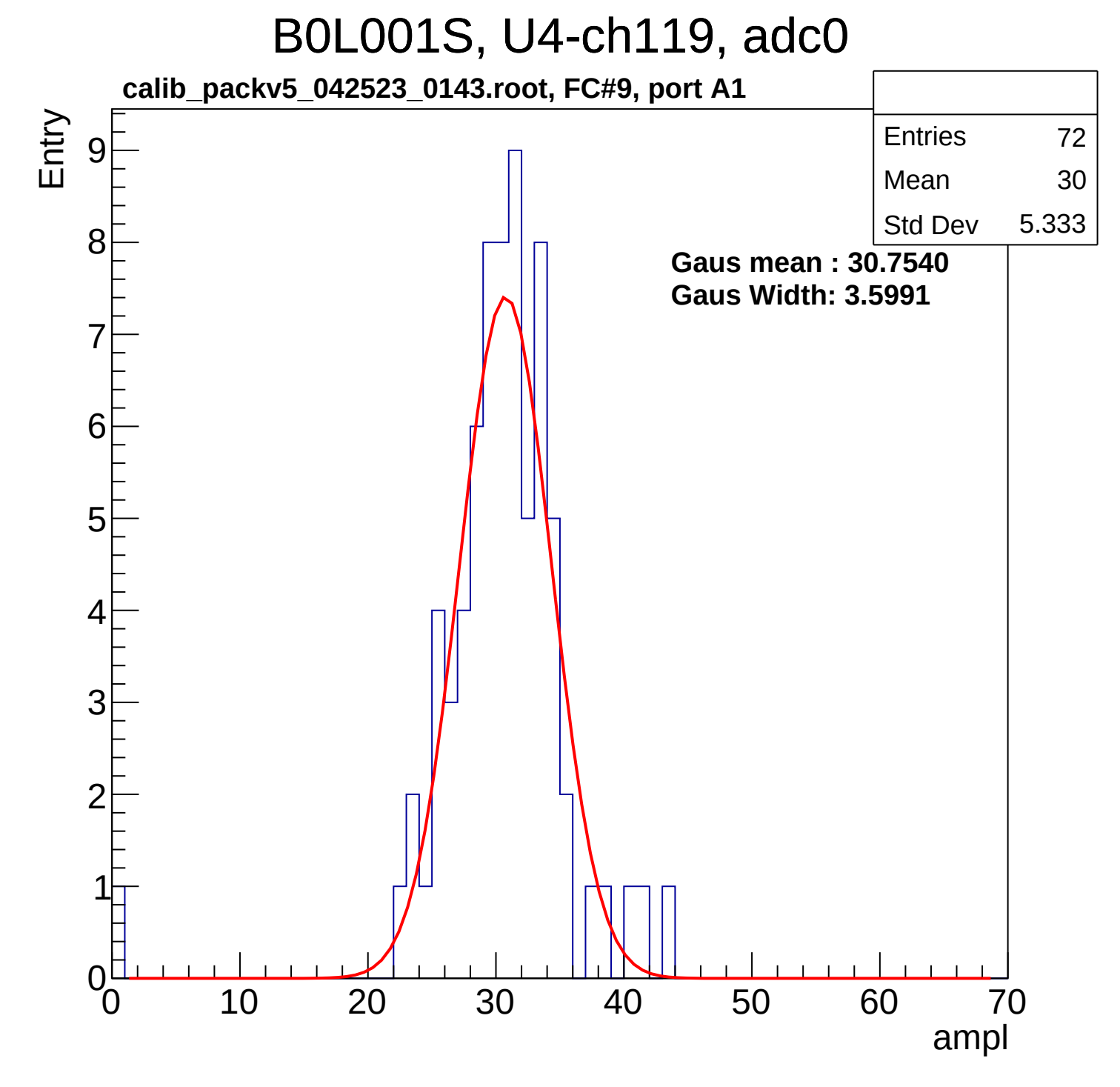
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch119, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30
Std Dev 5.333
Gaus mean : 30.7540
Gaus Width: 3.5991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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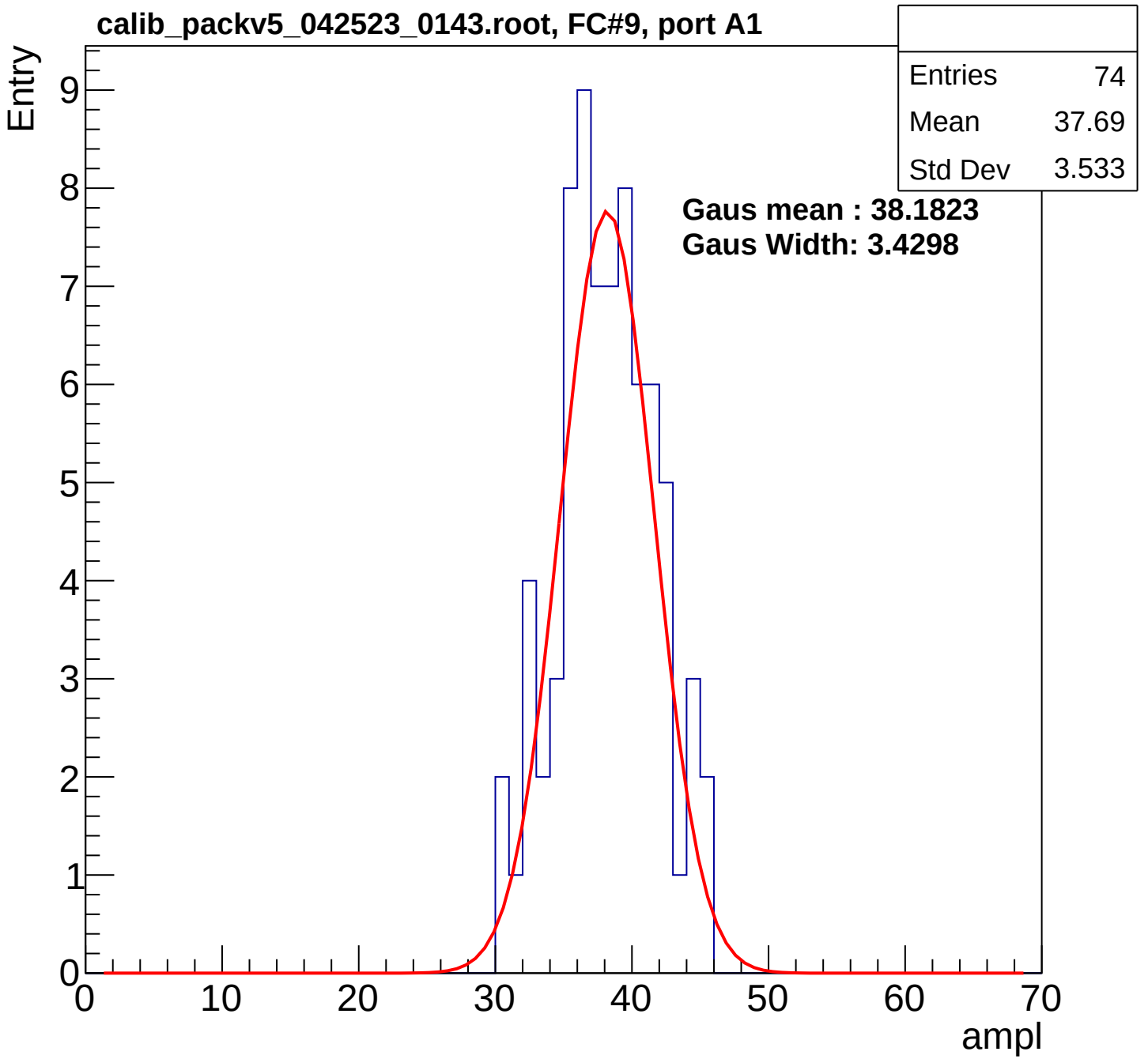
B0L001S, U4-ch119, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.69
Std Dev 3.533
Gaus mean : 38.1823
Gaus Width: 3.4298
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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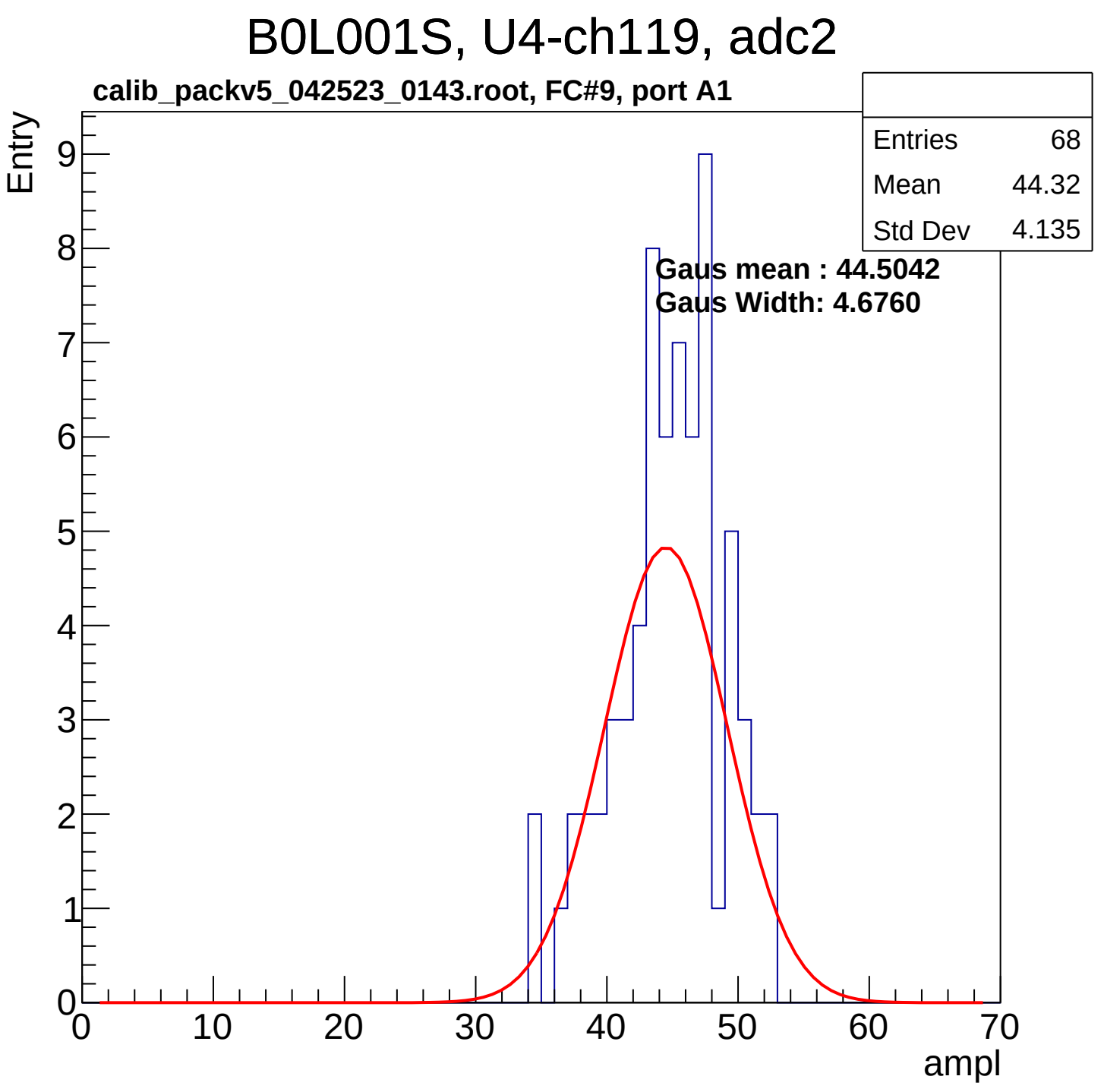
B0L001S, U4-ch119, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.32
Std Dev 4.135
Gaus mean : 44.5042
Gaus Width: 4.6760
Entry
ampl
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2
3
4
5
6
7
8
9
0
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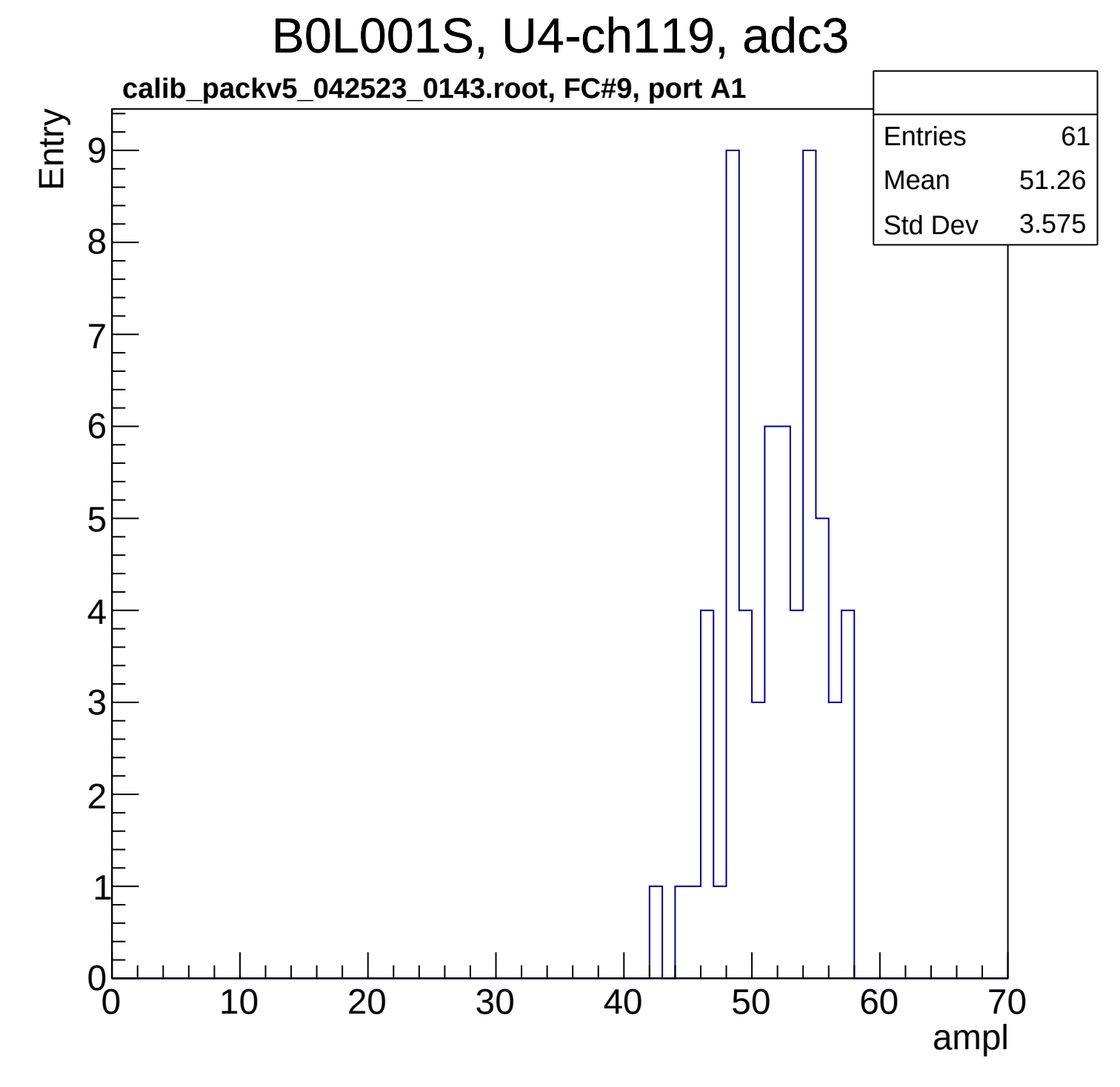
B0L001S, U4-ch119, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.26
Std Dev 3.575
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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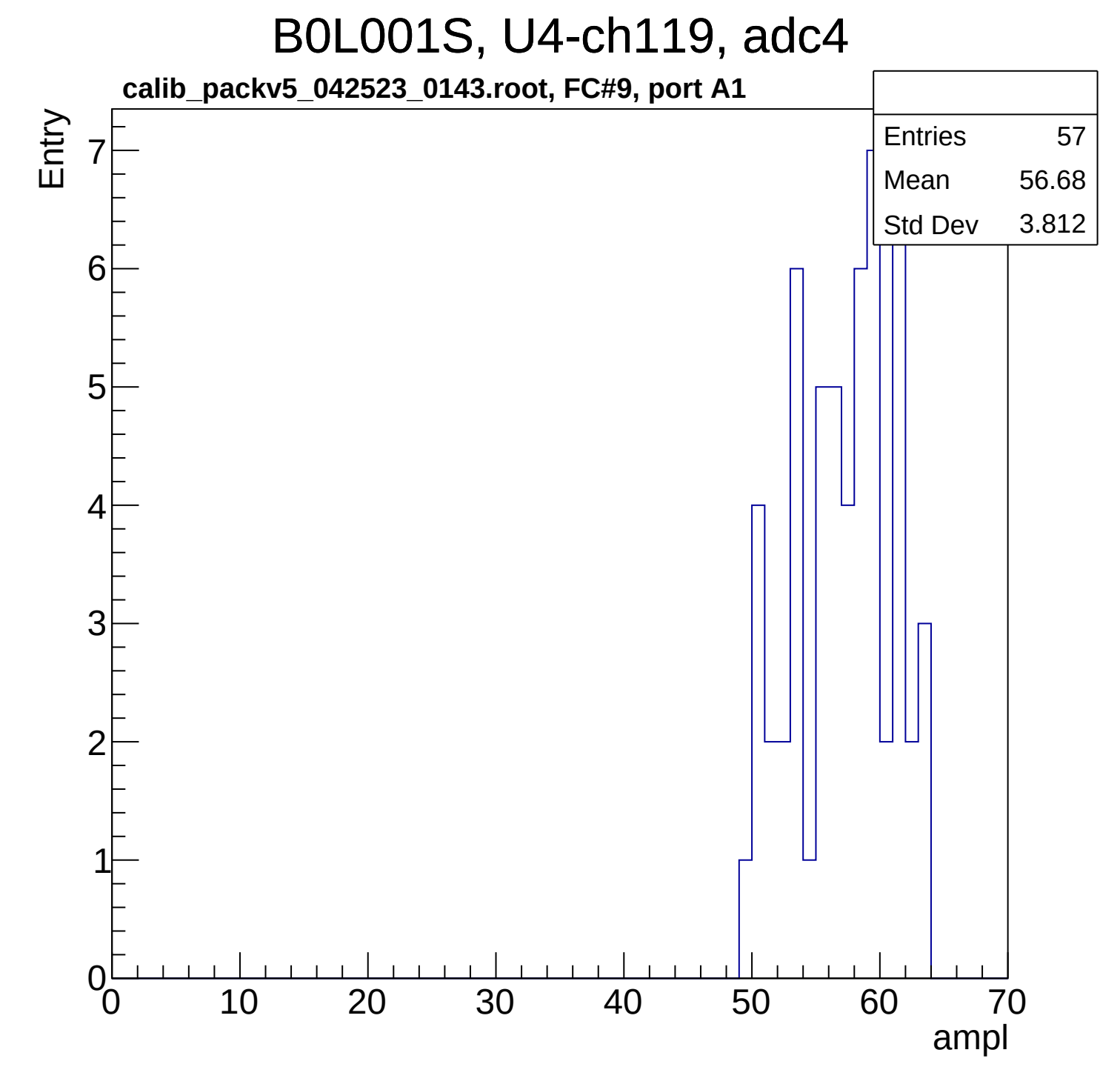
B0L001S, U4-ch119, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.68
Std Dev 3.812
Entry
ampl
0
1
2
3
4
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6
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0
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40
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60
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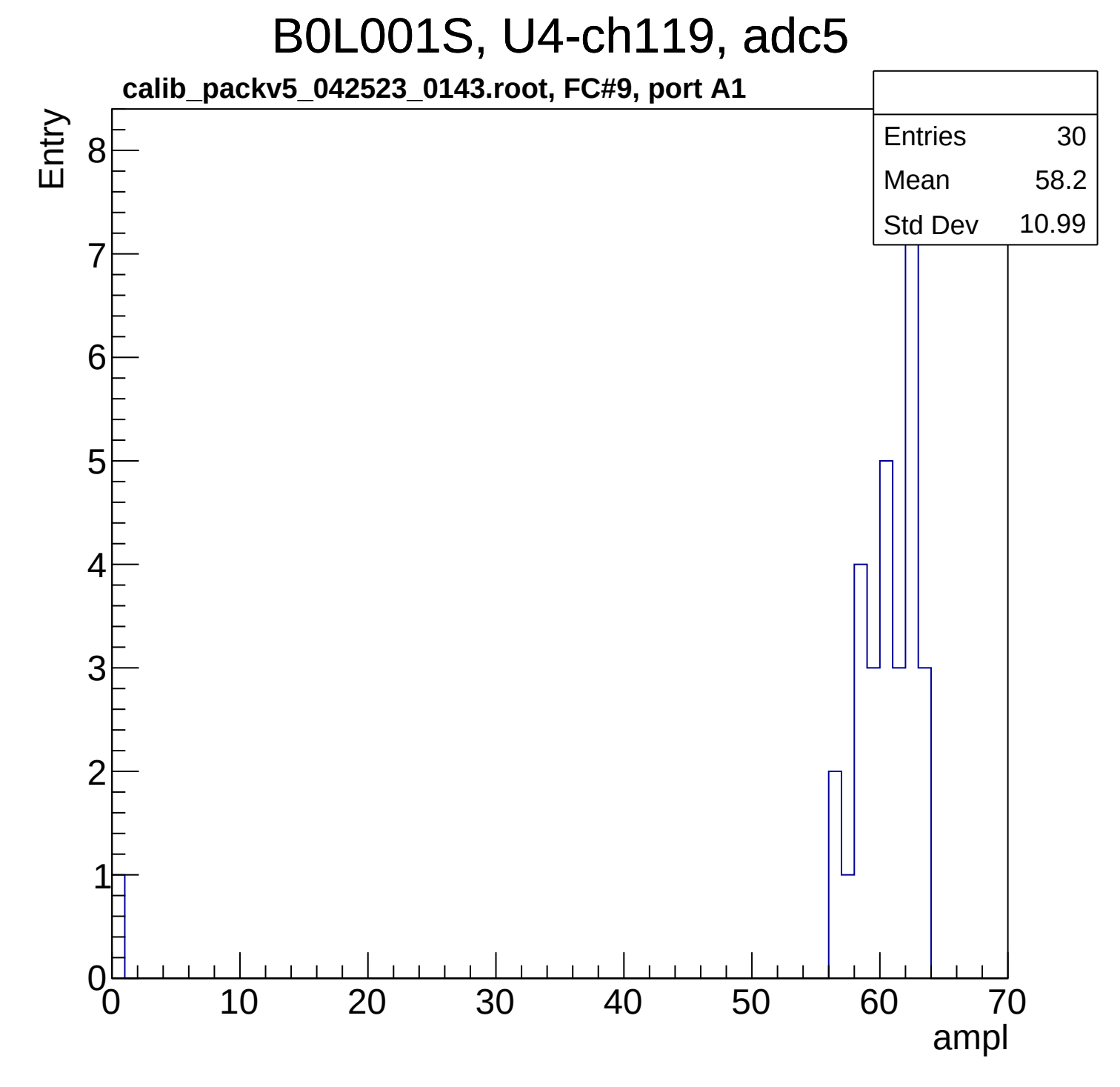
B0L001S, U4-ch119, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.2
Std Dev 10.99
Entry
ampl
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1
2
3
4
5
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7
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0
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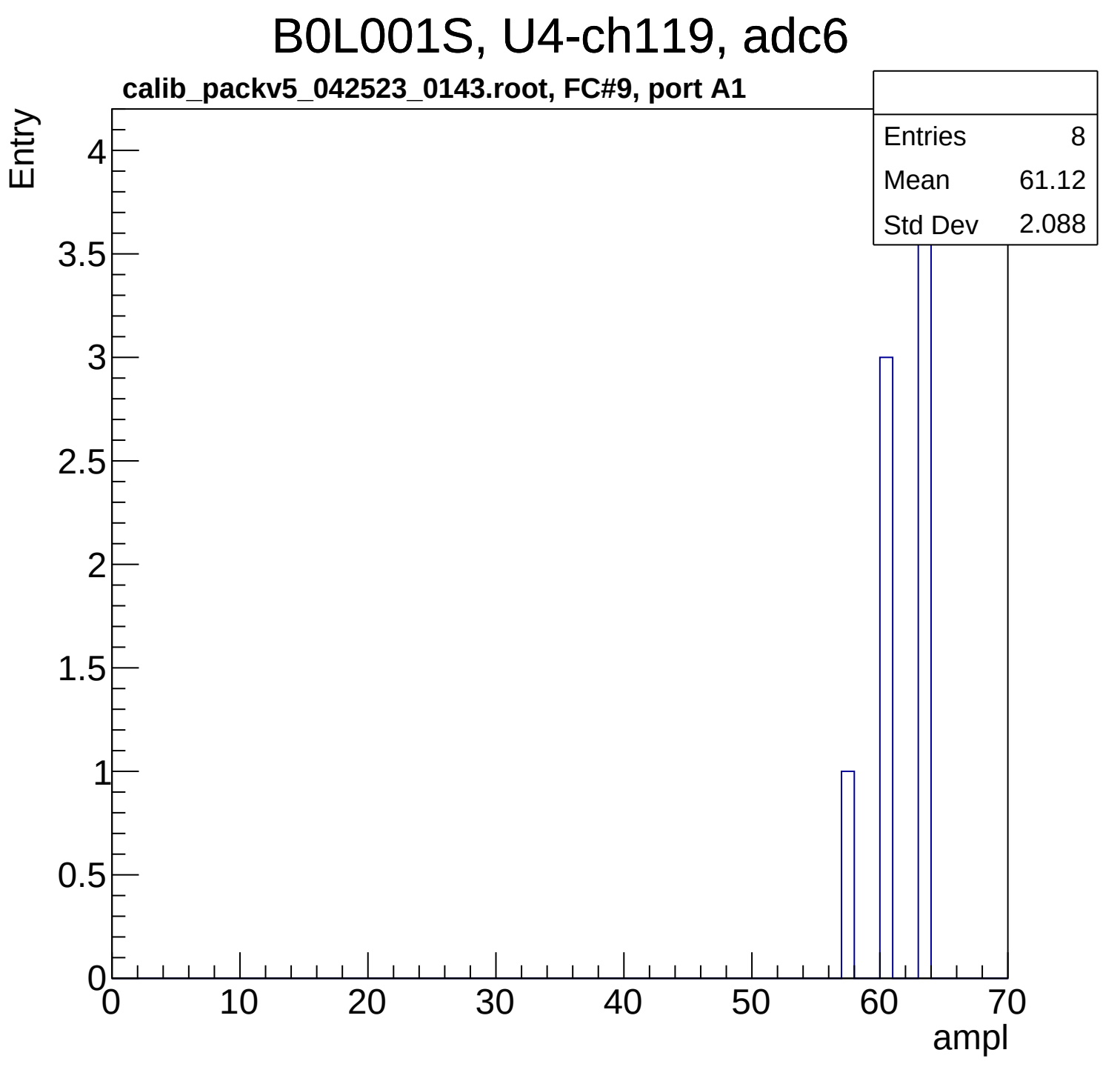
B0L001S, U4-ch119, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.12
Std Dev 2.088
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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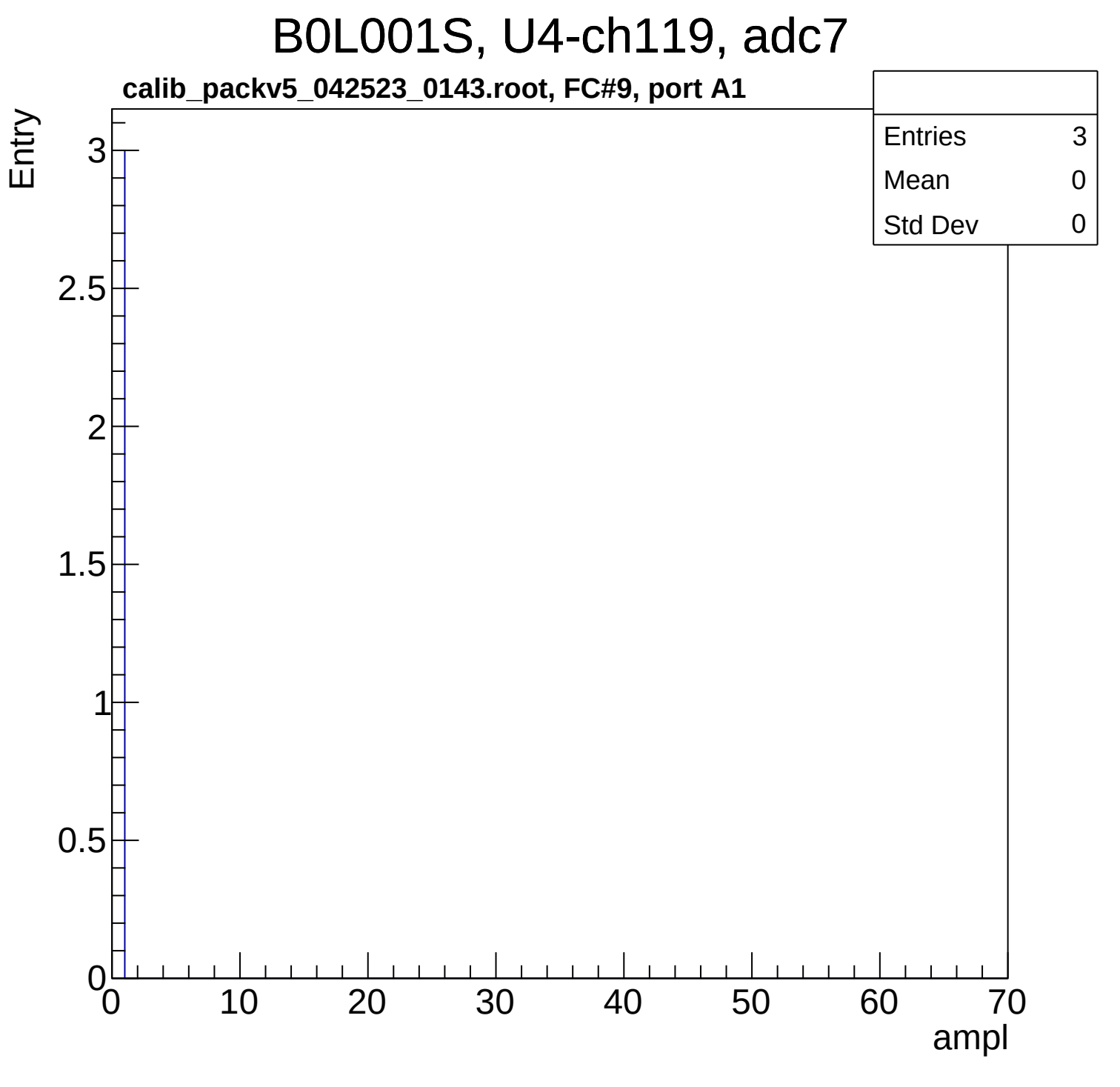
B0L001S, U4-ch119, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
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30
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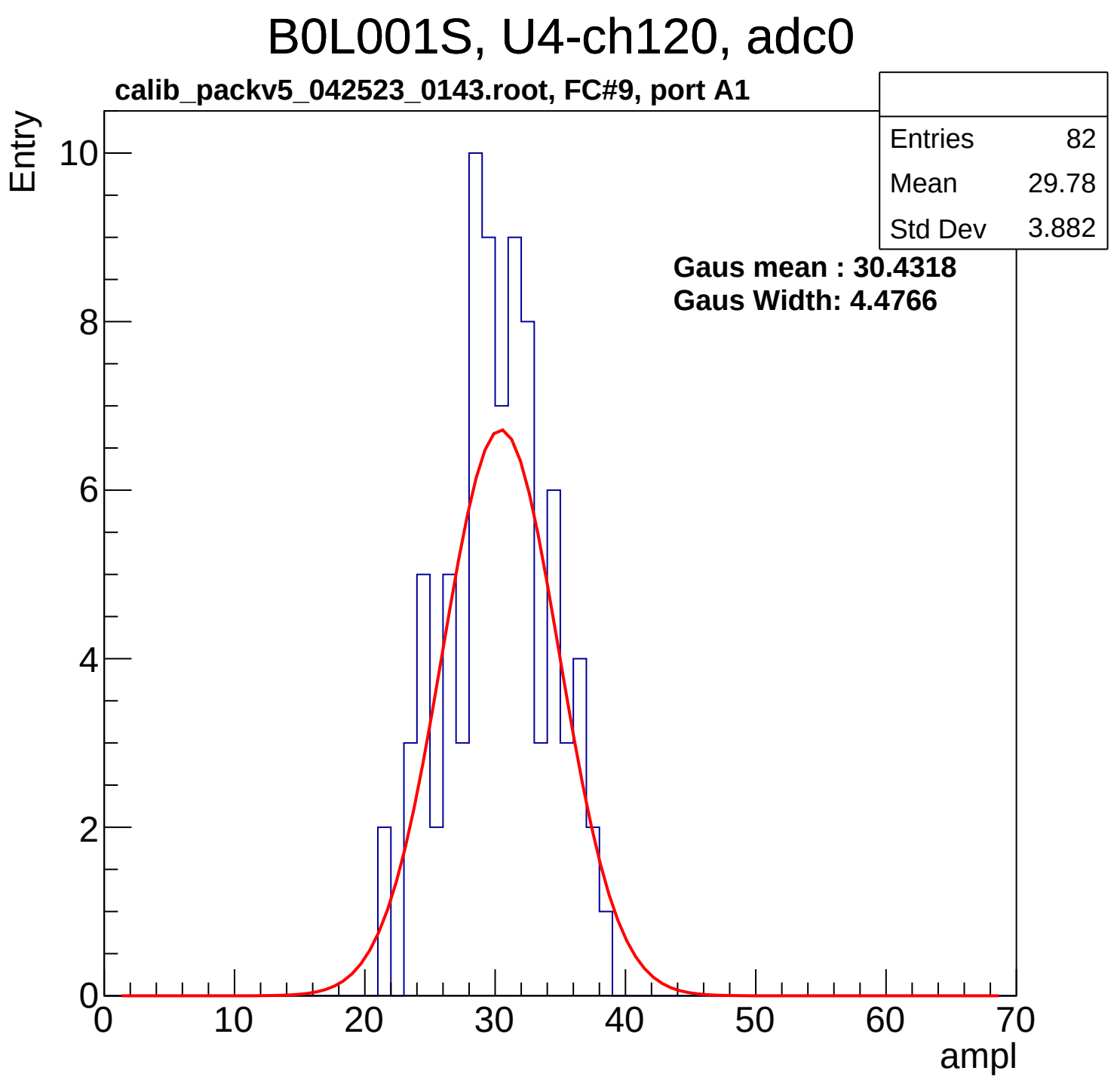
50
60
70
ampl


B0L001S, U4-ch120, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 29.78
Std Dev 3.882
Gaus mean : 30.4318
Gaus Width: 4.4766
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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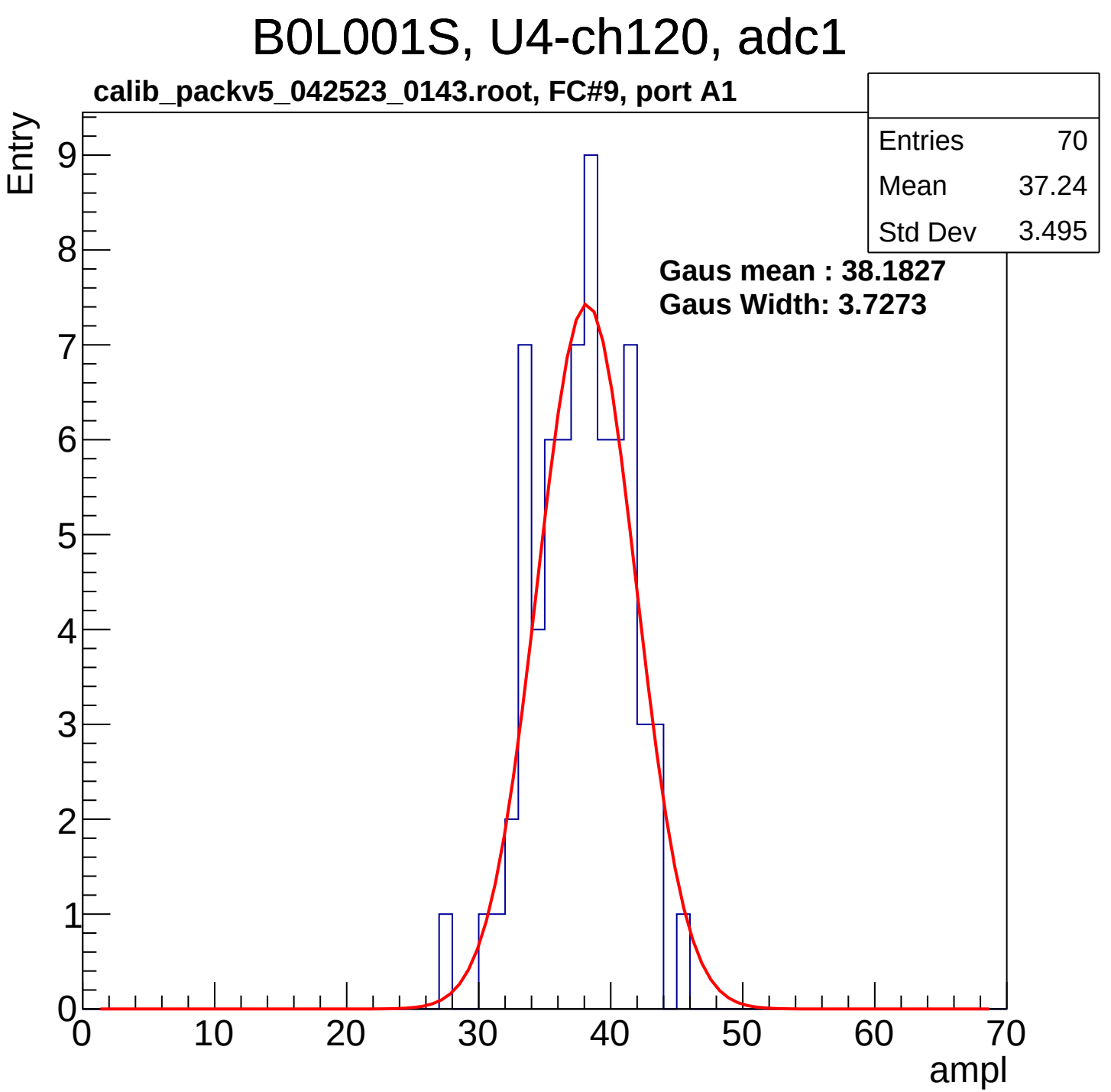
B0L001S, U4-ch120, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.24
Std Dev 3.495
Gaus mean : 38.1827
Gaus Width: 3.7273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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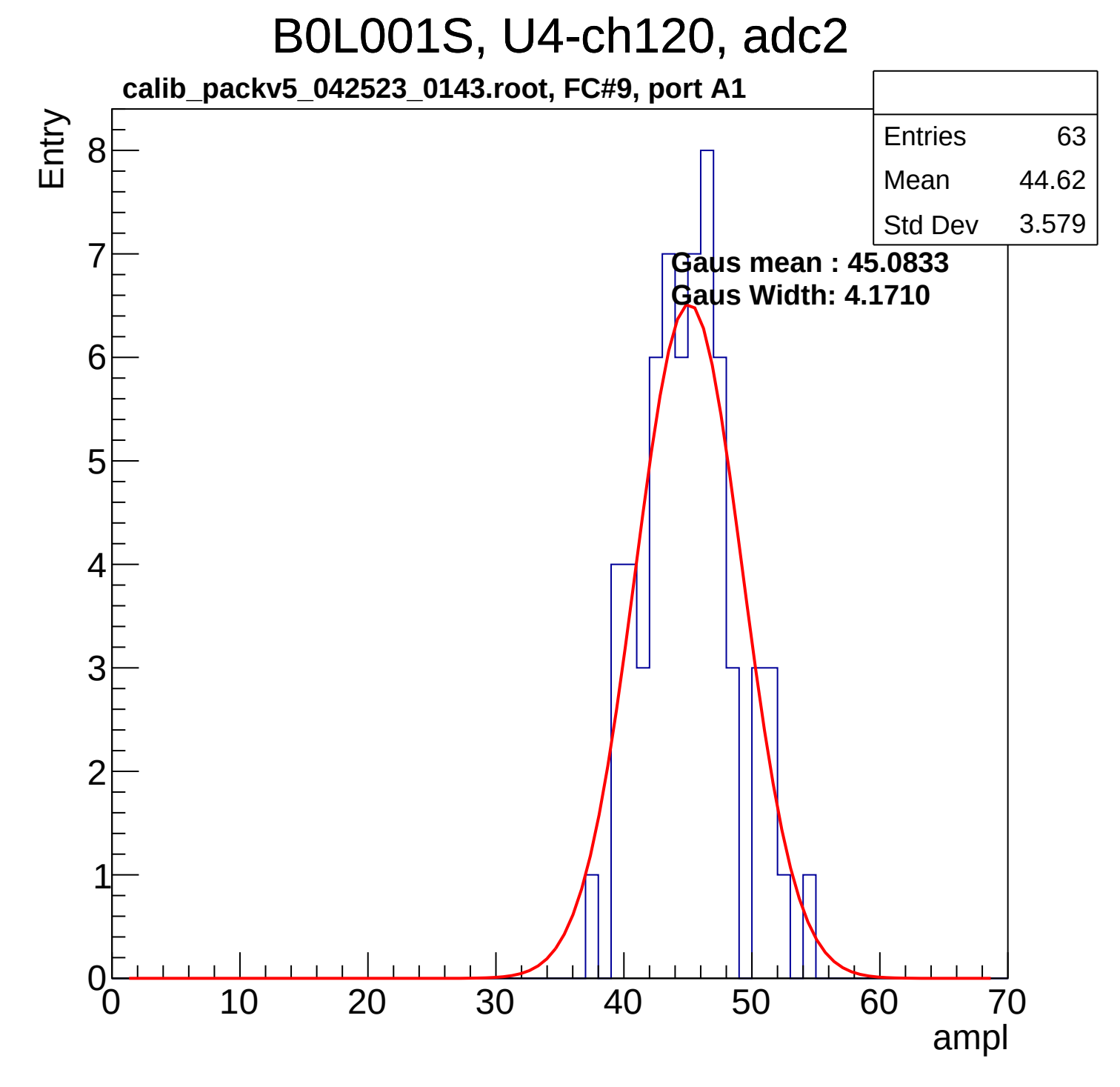
B0L001S, U4-ch120, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.62
Std Dev 3.579
Gaus mean : 45.0833
Gaus Width: 4.1710
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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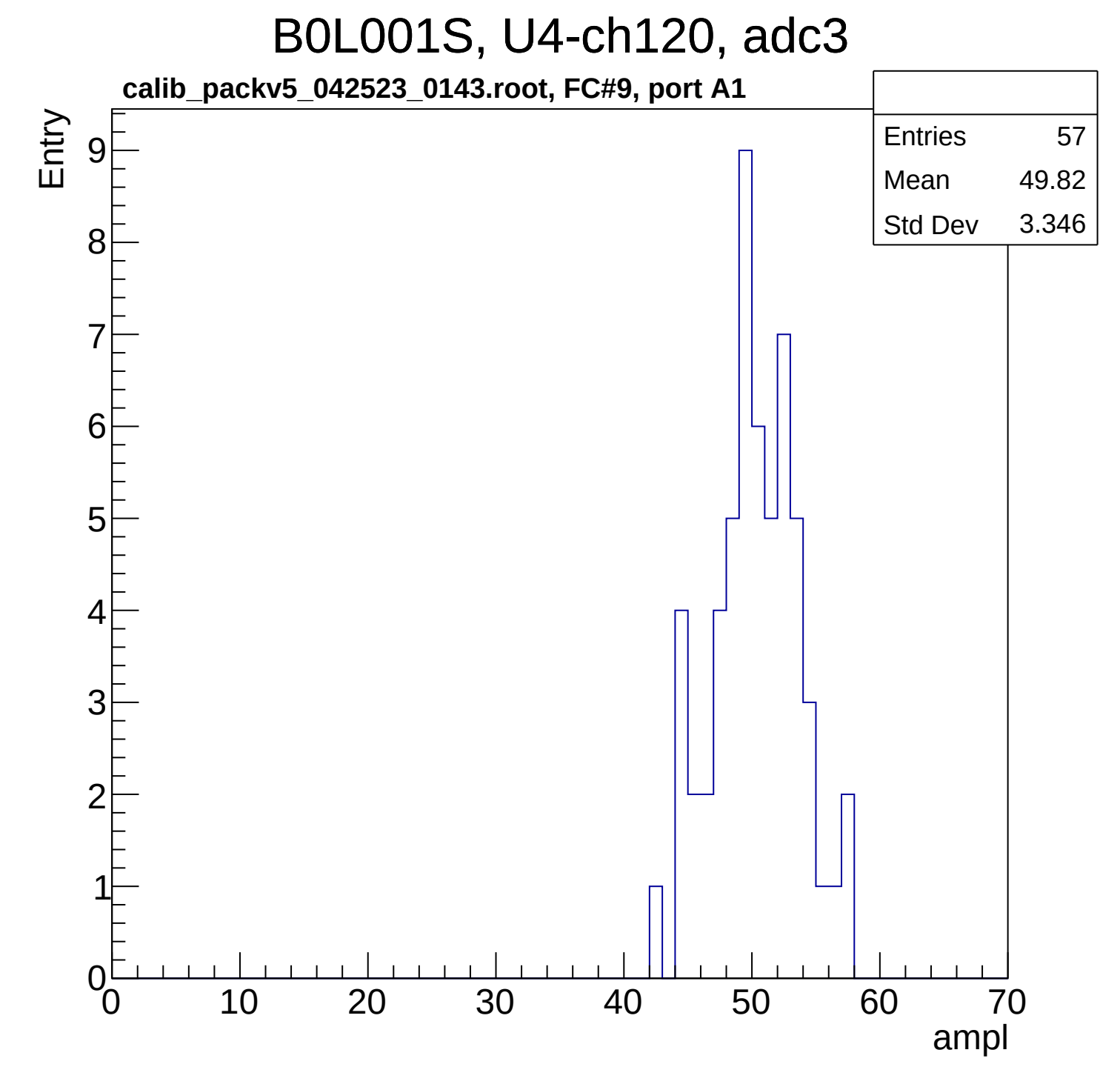
B0L001S, U4-ch120, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 49.82
Std Dev 3.346
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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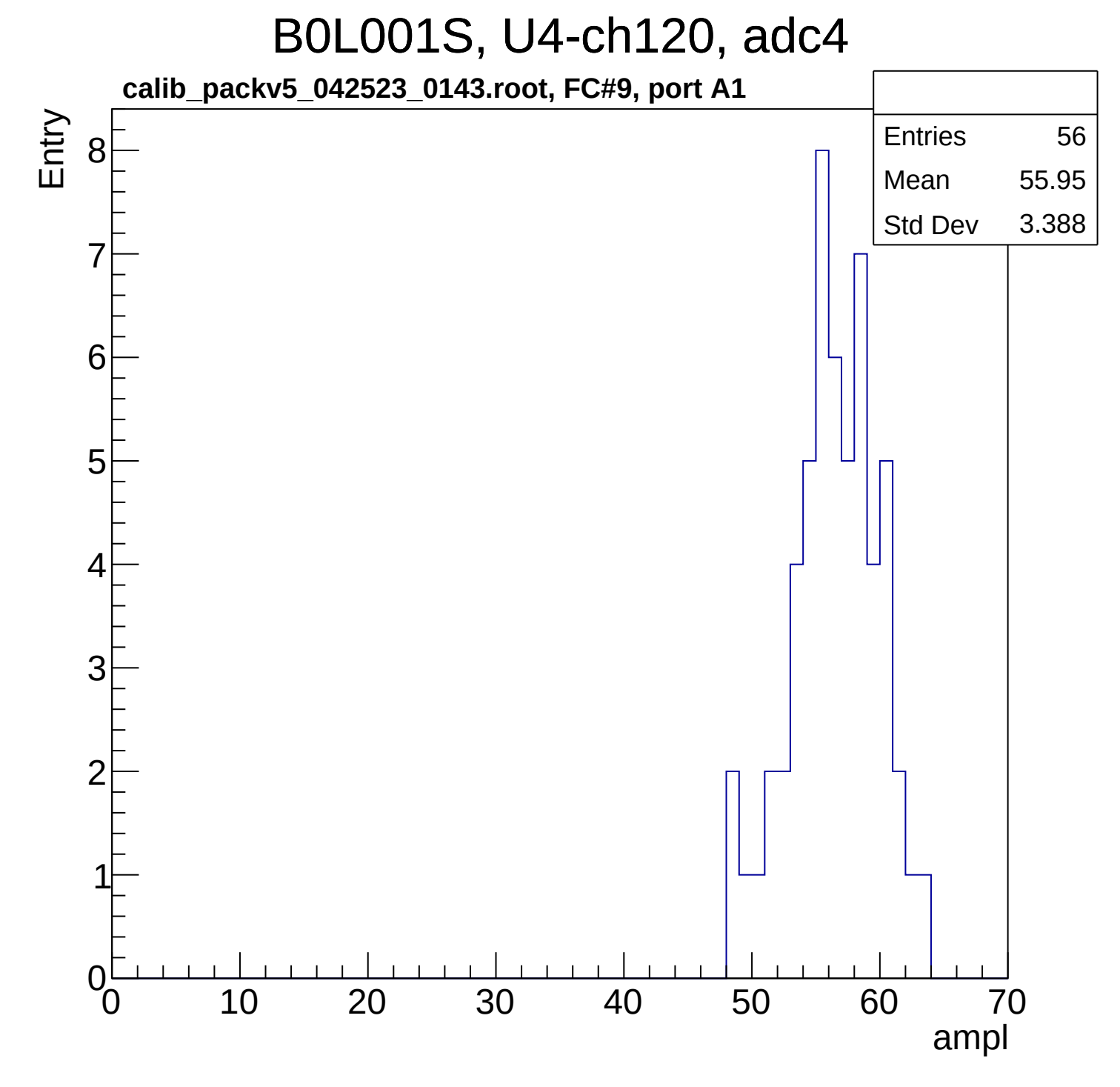
B0L001S, U4-ch120, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.95
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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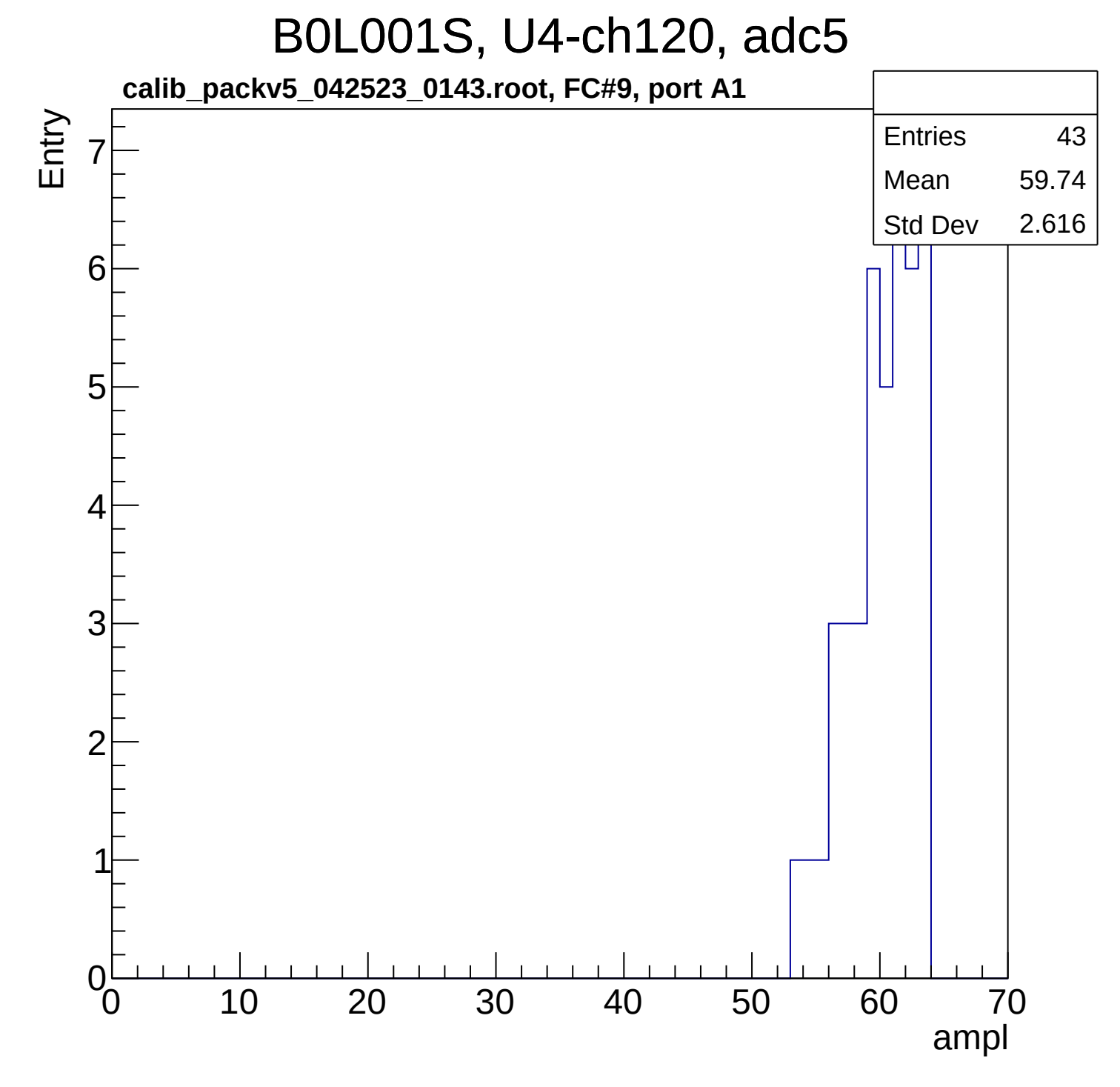
40
50
60
70


B0L001S, U4-ch120, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.74
Std Dev 2.616
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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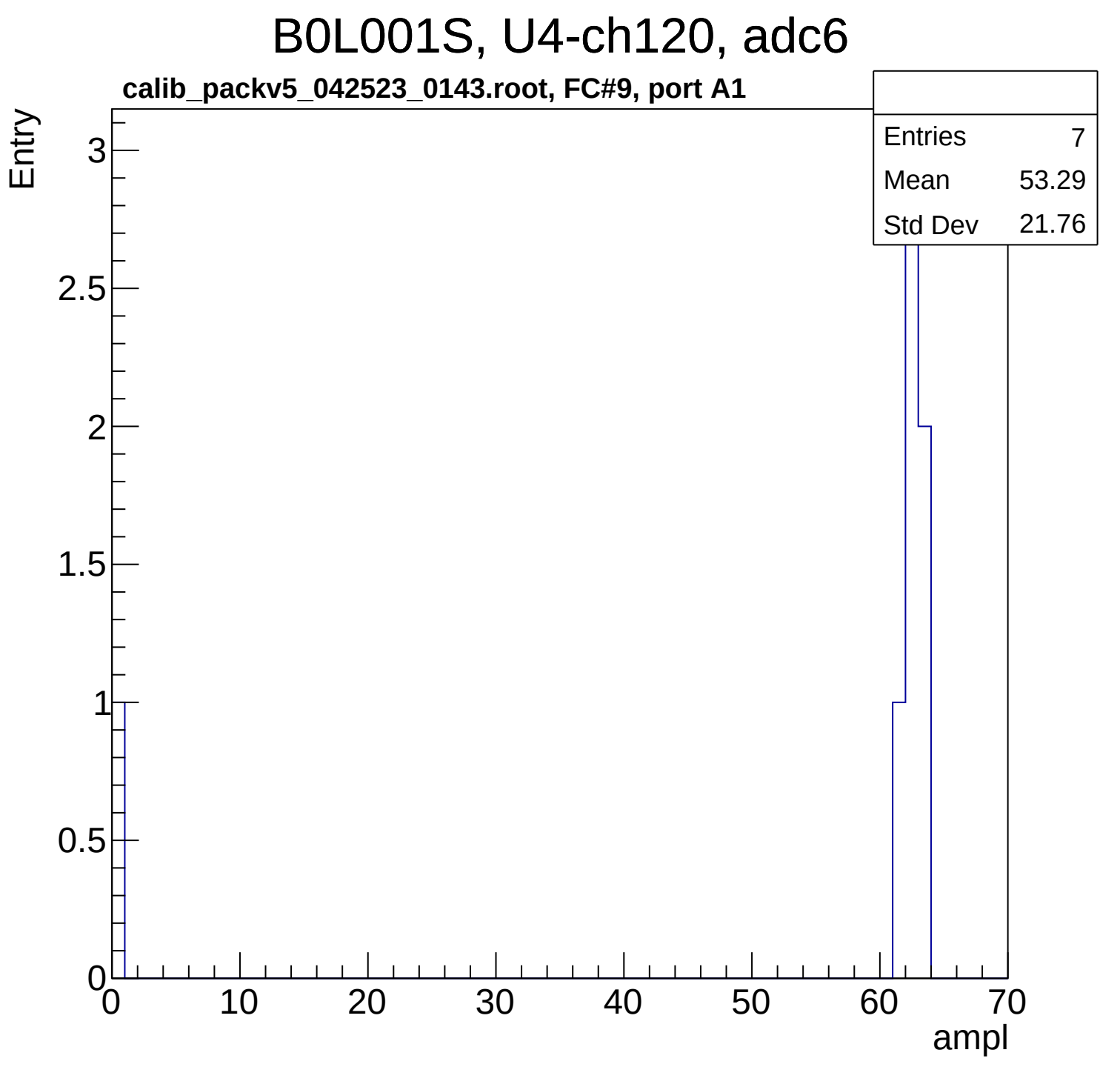
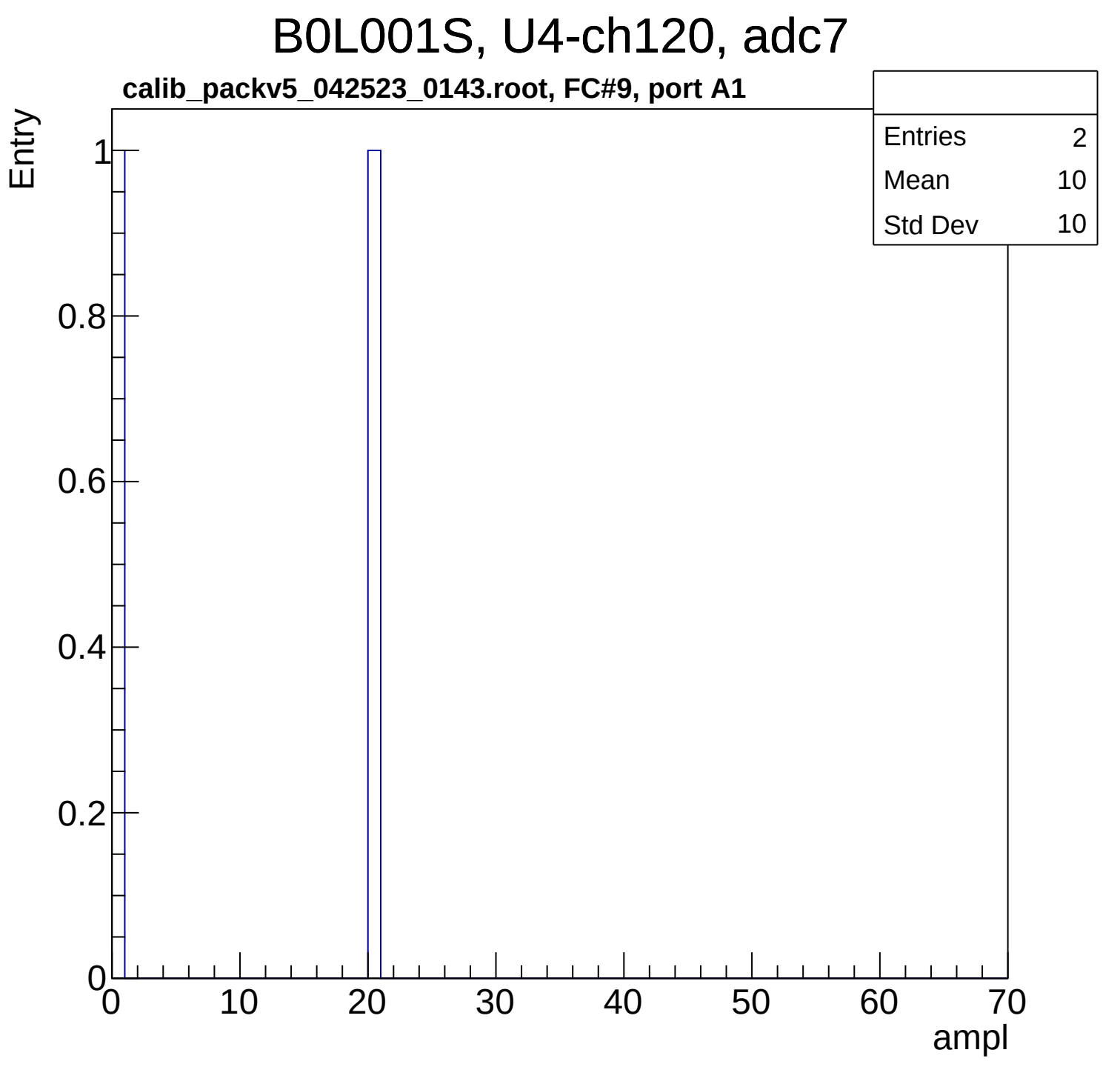

B0L001S, U4-ch120, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 53.29
Std Dev 21.76
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch120, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10
Std Dev 10
Entry
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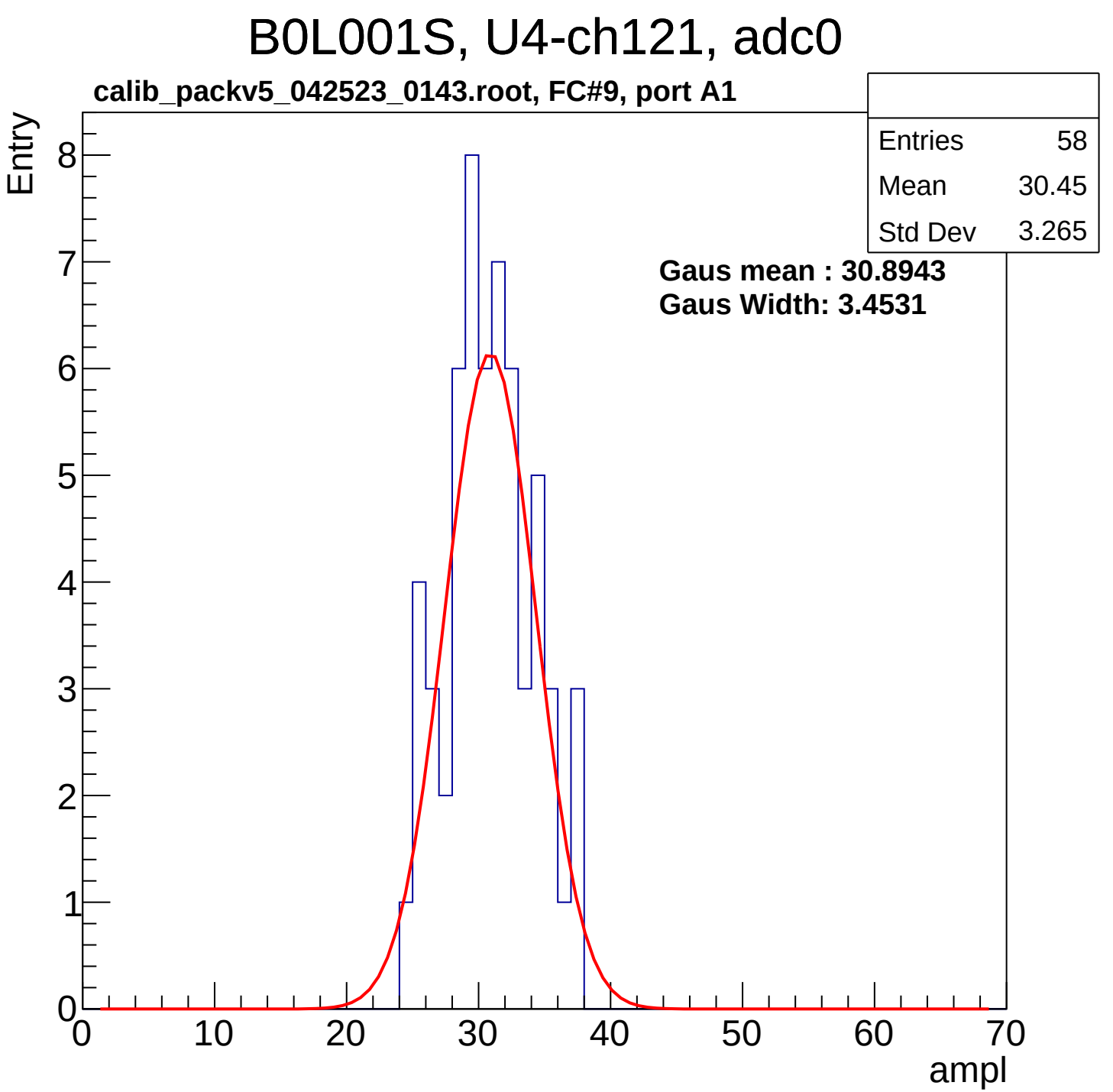
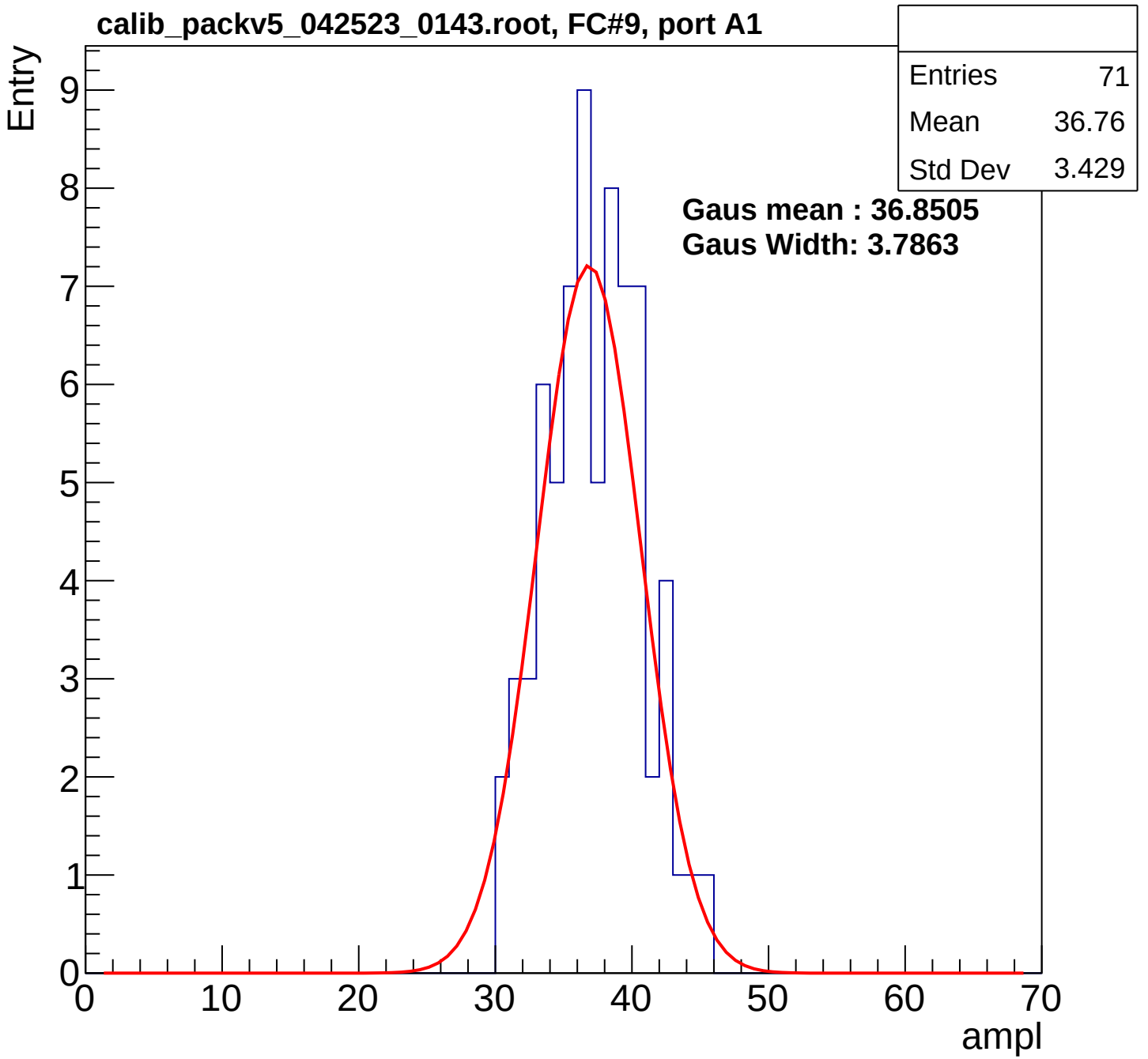
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch121, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 30.45
Std Dev 3.265
Gaus mean : 30.8943
Gaus Width: 3.4531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch121, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 36.76
Std Dev 3.429
Gaus mean : 36.8505
Gaus Width: 3.7863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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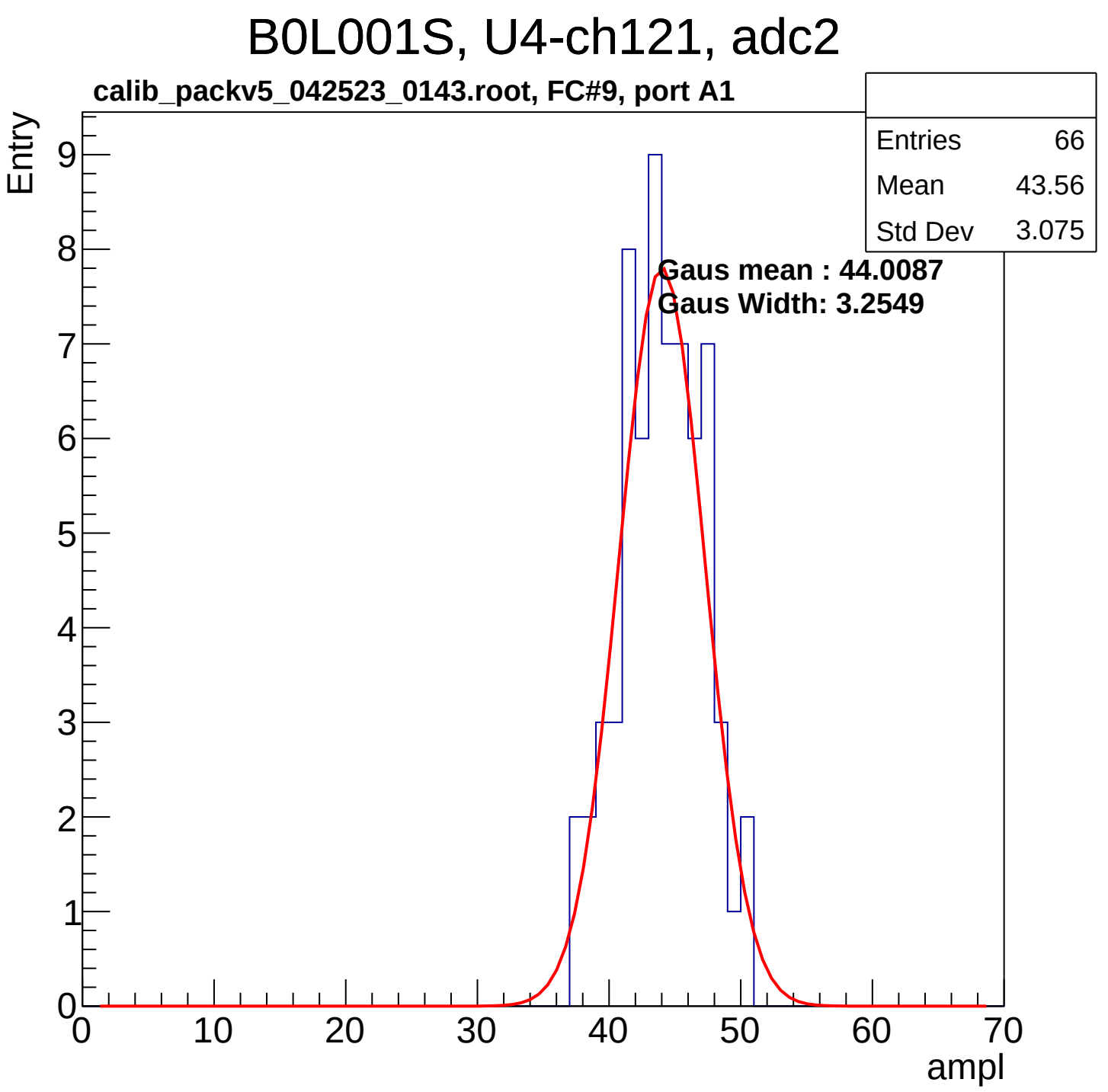
B0L001S, U4-ch121, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.56
Std Dev 3.075
Gaus mean : 44.0087
Gaus Width: 3.2549
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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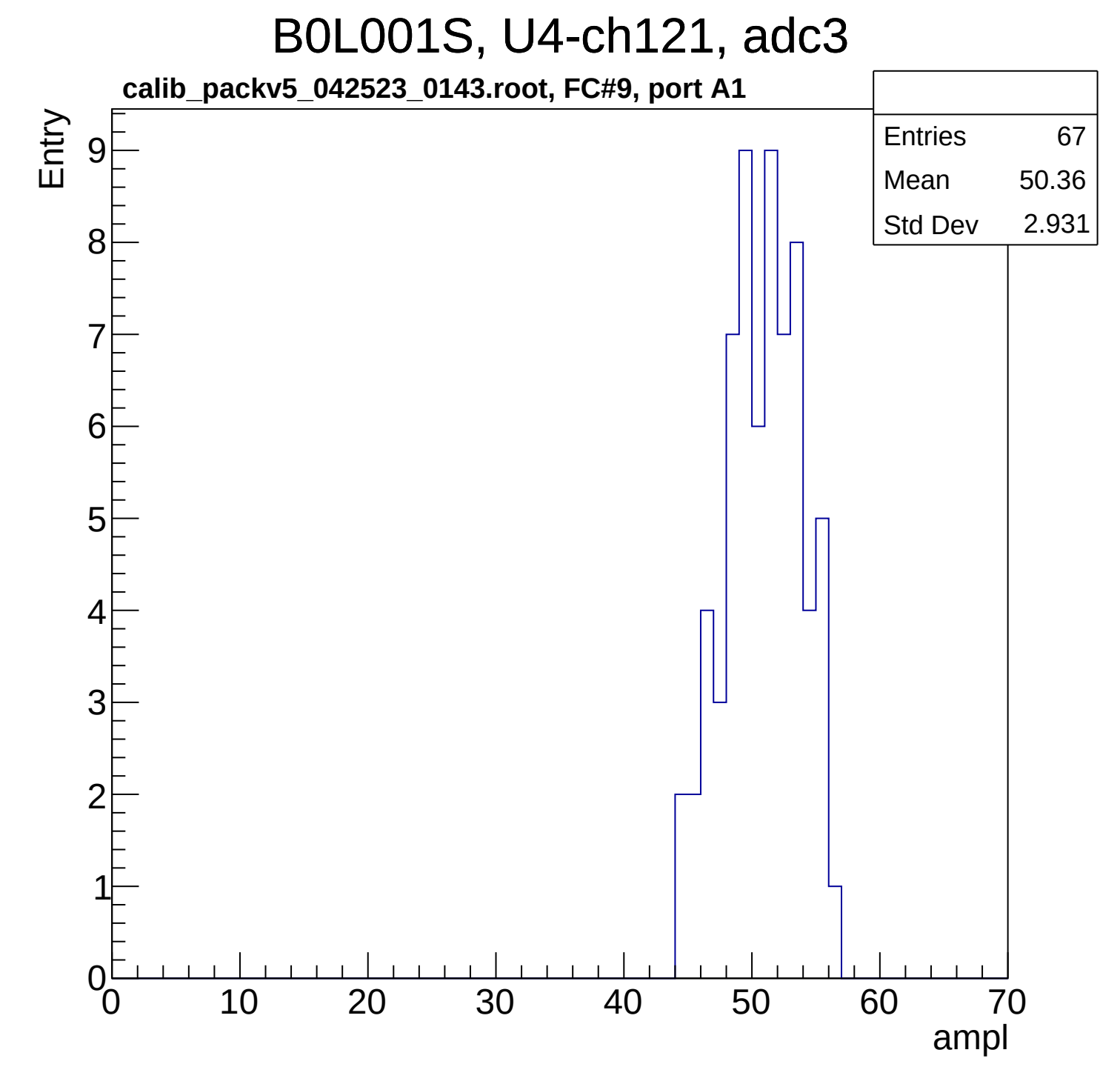
B0L001S, U4-ch121, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.36
Std Dev 2.931
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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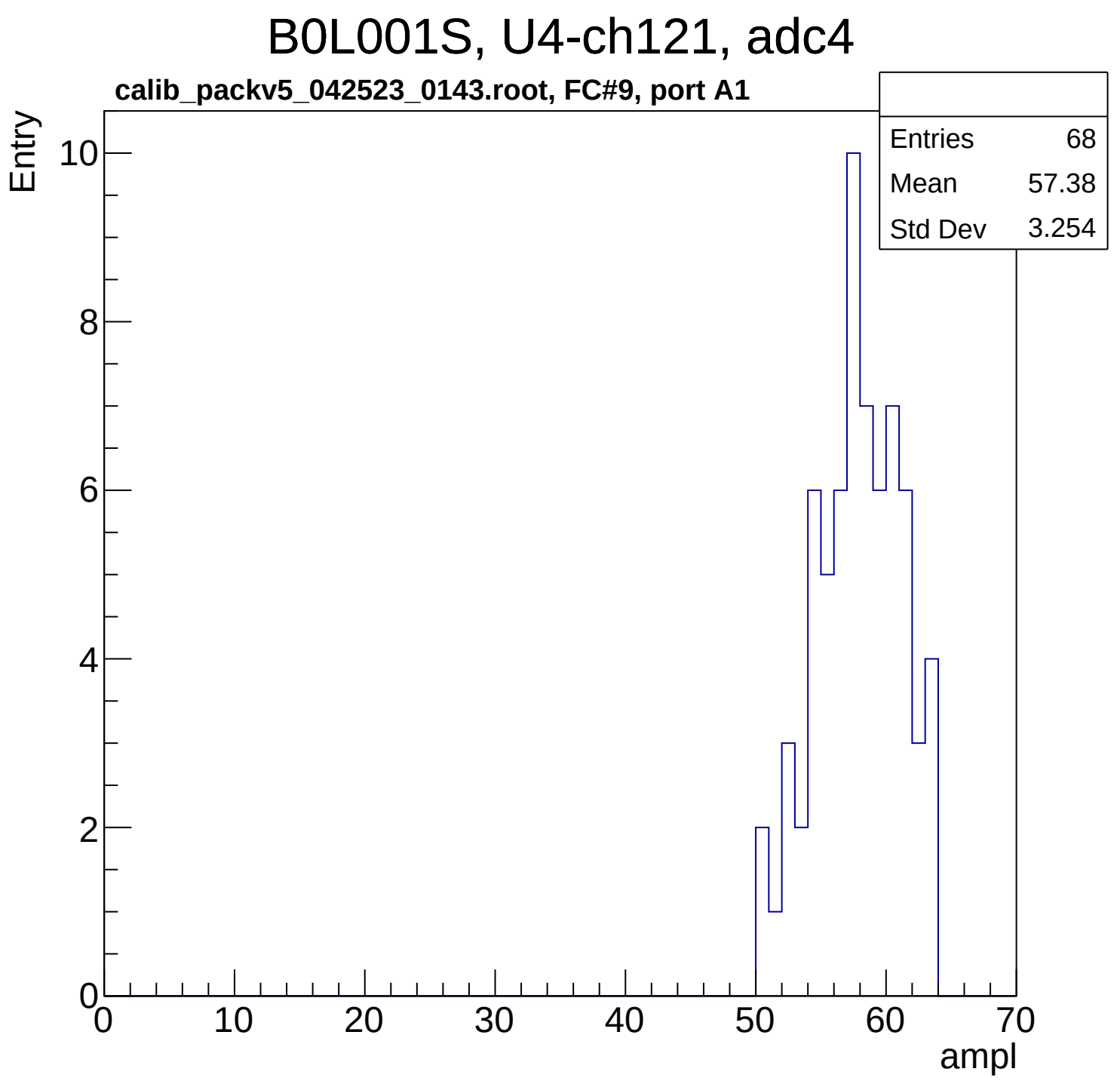
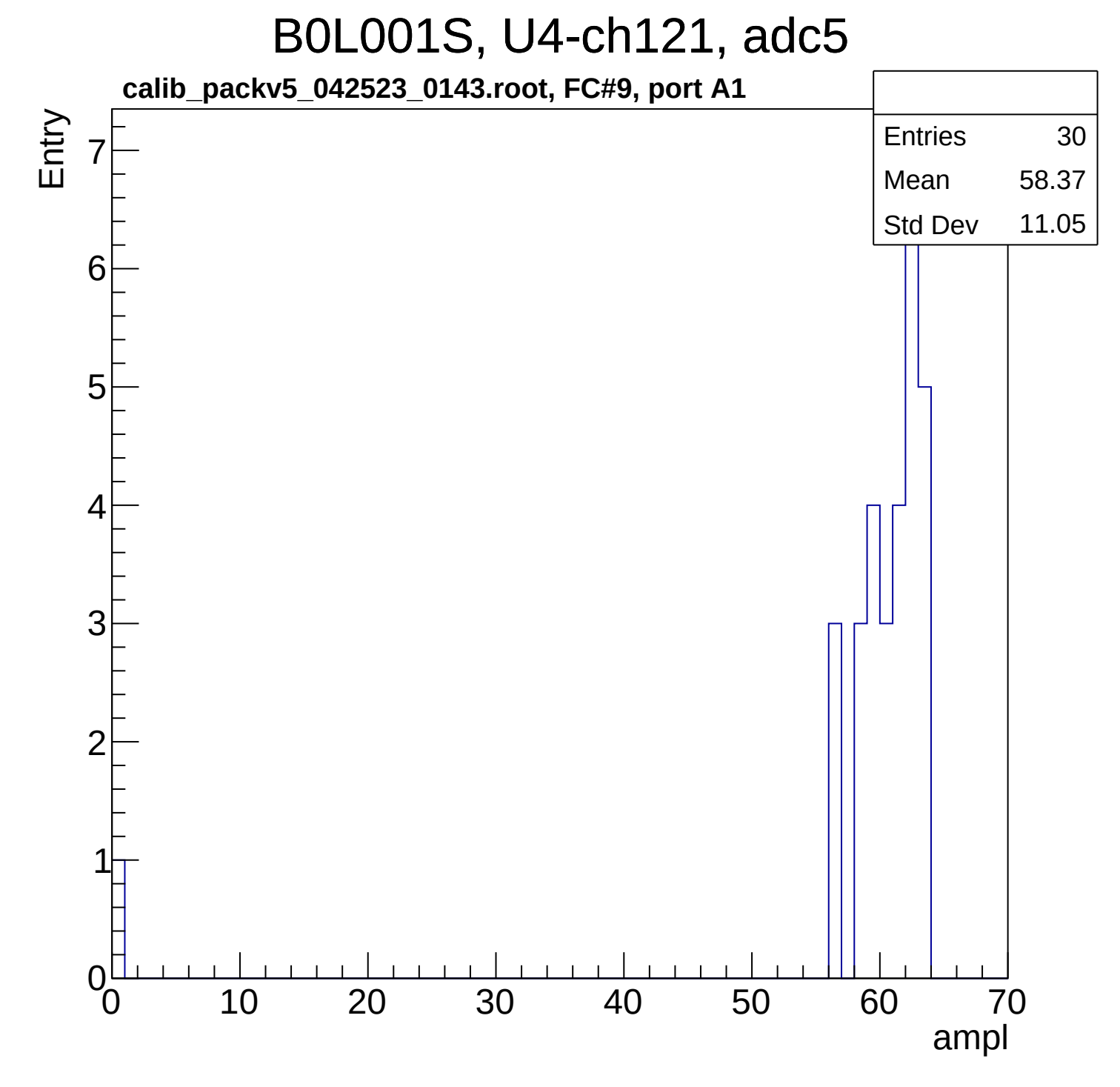

B0L001S, U4-ch121, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 57.38
Std Dev 3.254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch121, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.37
Std Dev 11.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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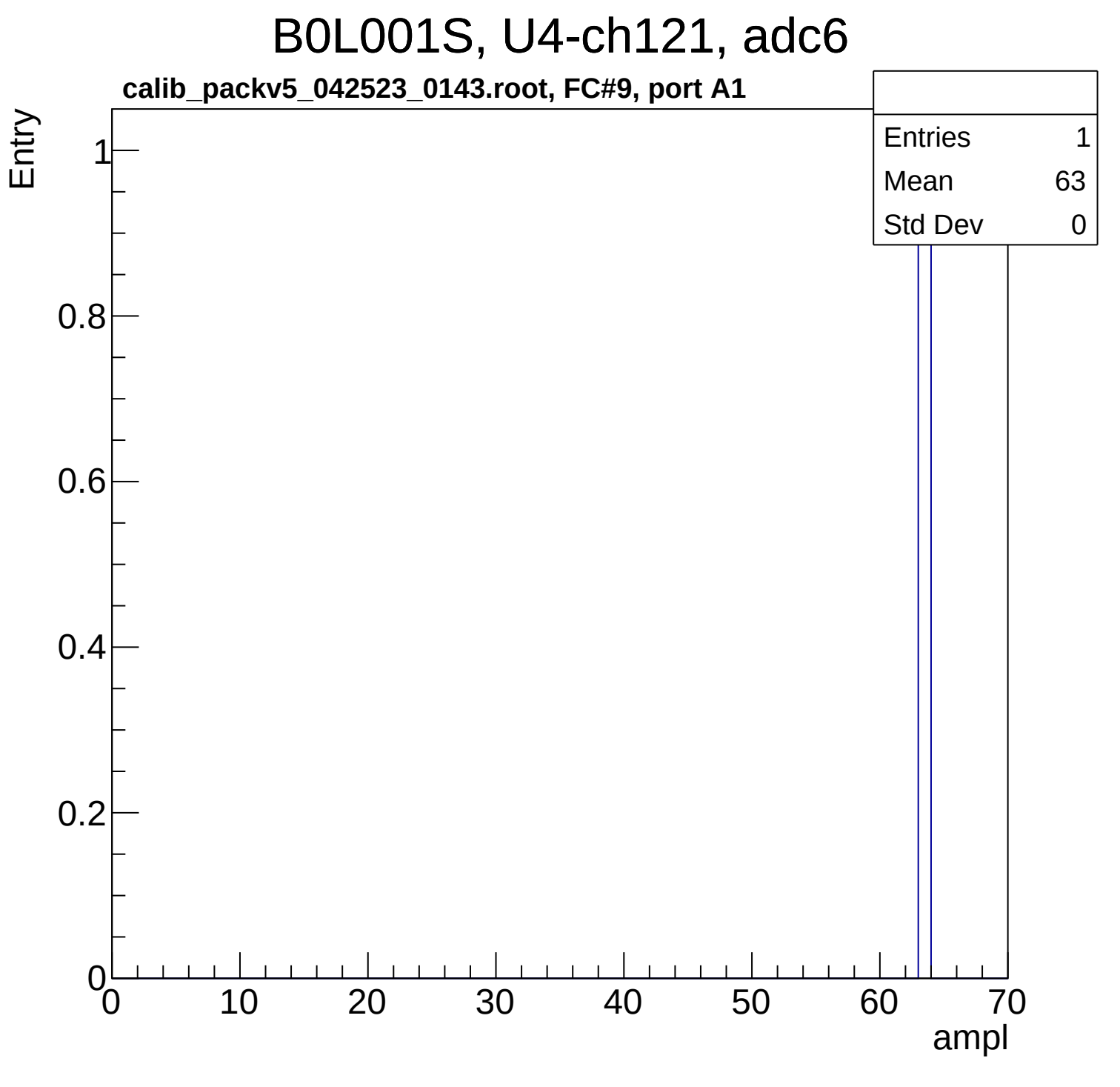
B0L001S, U4-ch121, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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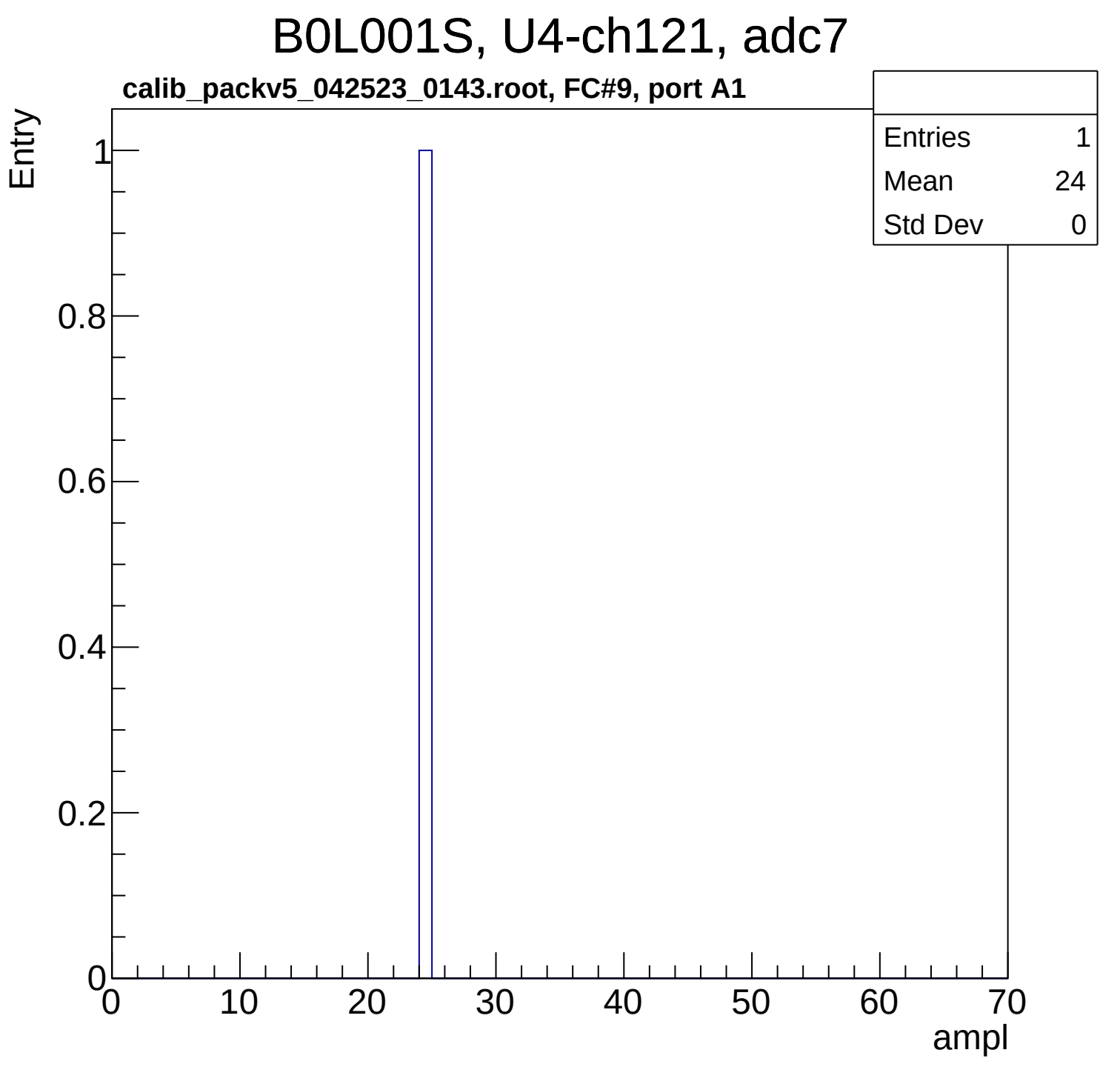
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch121, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
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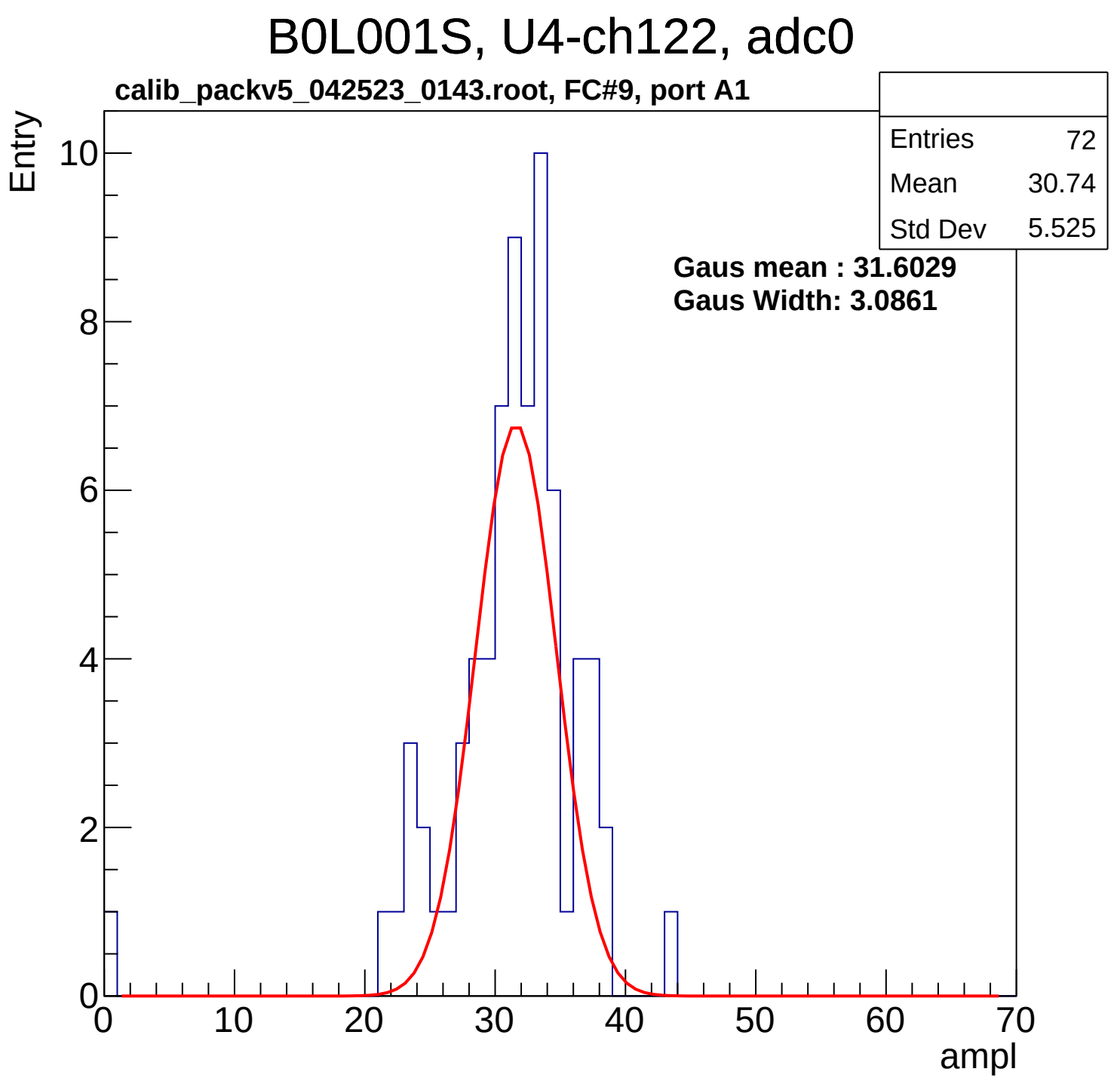
60
70
ampl


B0L001S, U4-ch122, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.74
Std Dev 5.525
Gaus mean : 31.6029
Gaus Width: 3.0861
Entry
ampl
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2
4
6
8
10
0
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20
30
40
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60
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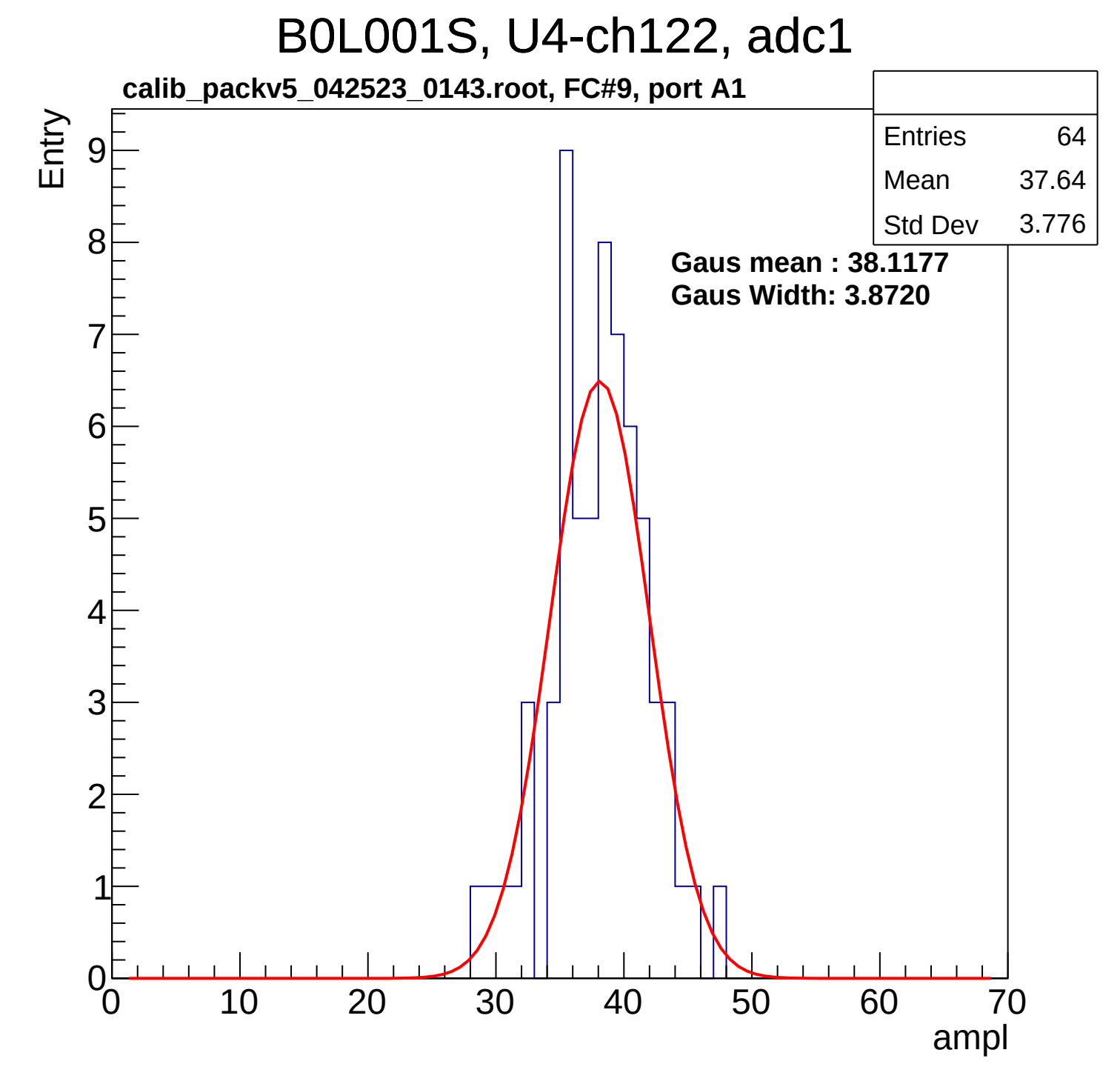
B0L001S, U4-ch122, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.64
Std Dev 3.776
Gaus mean : 38.1177
Gaus Width: 3.8720
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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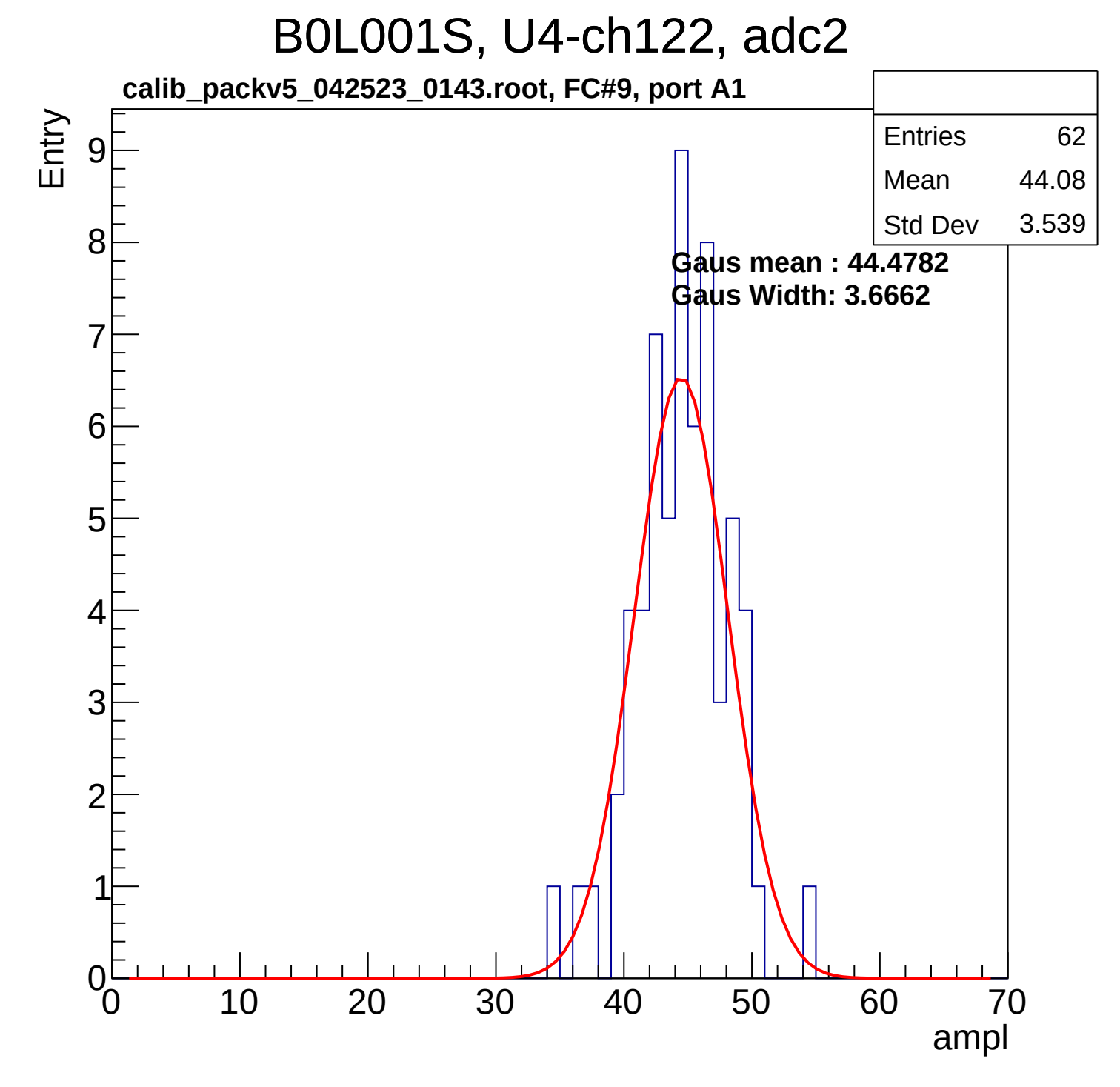
B0L001S, U4-ch122, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.08
Std Dev 3.539
Gaus mean : 44.4782
Gaus Width: 3.6662
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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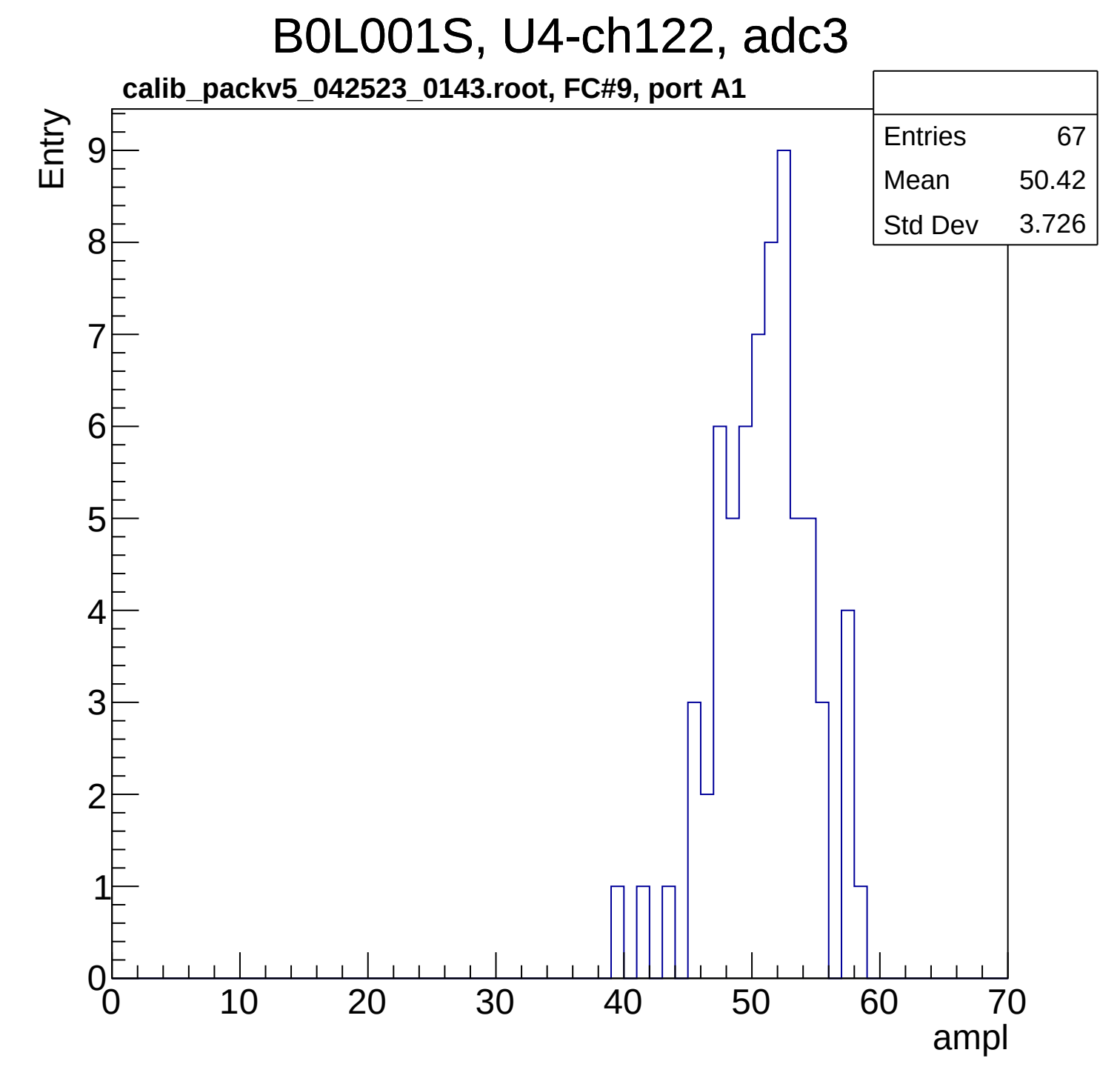
B0L001S, U4-ch122, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.42
Std Dev 3.726
Entry
ampl
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3
4
5
6
7
8
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0
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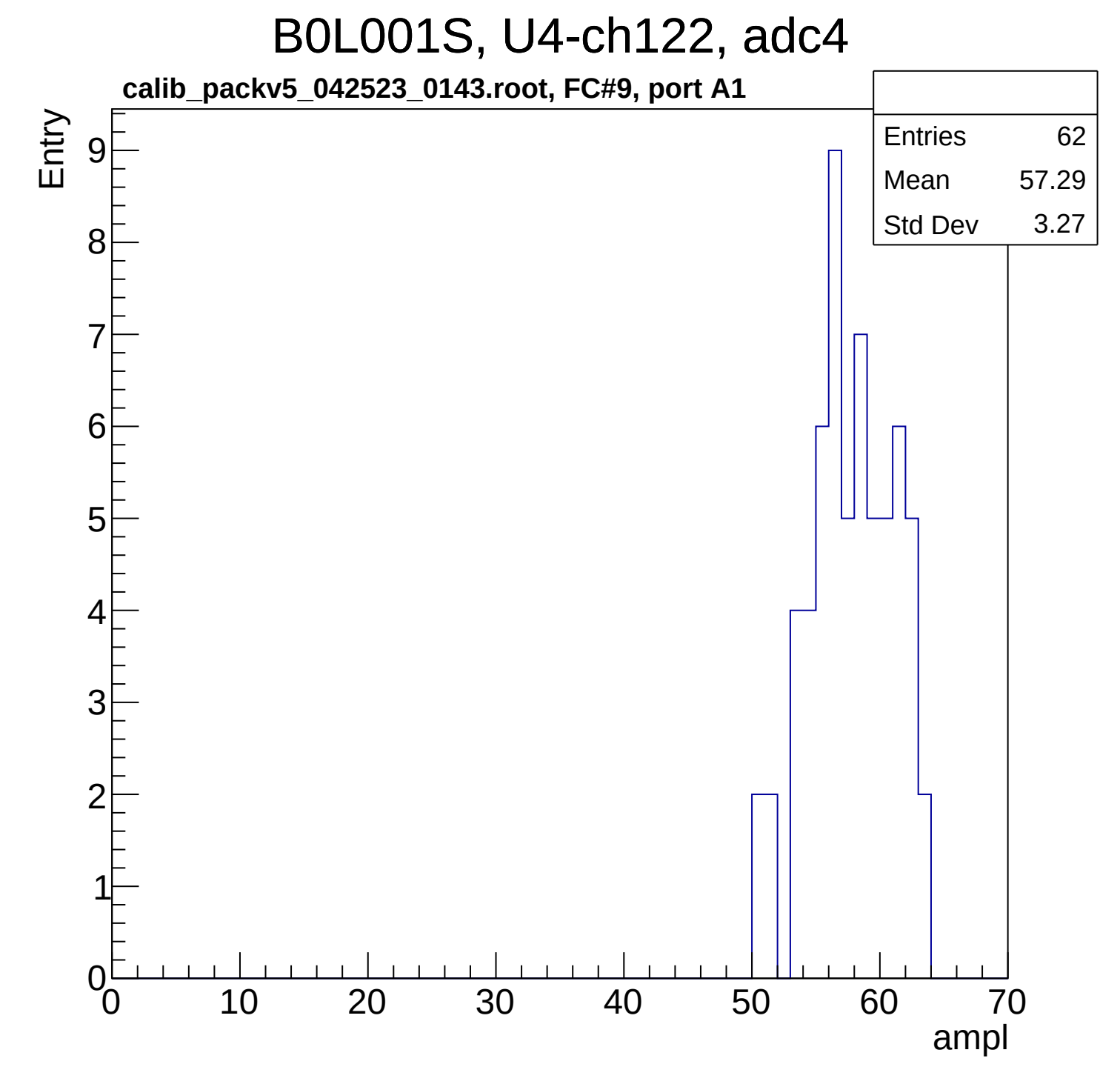
B0L001S, U4-ch122, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 57.29
Std Dev 3.27
Entry
ampl
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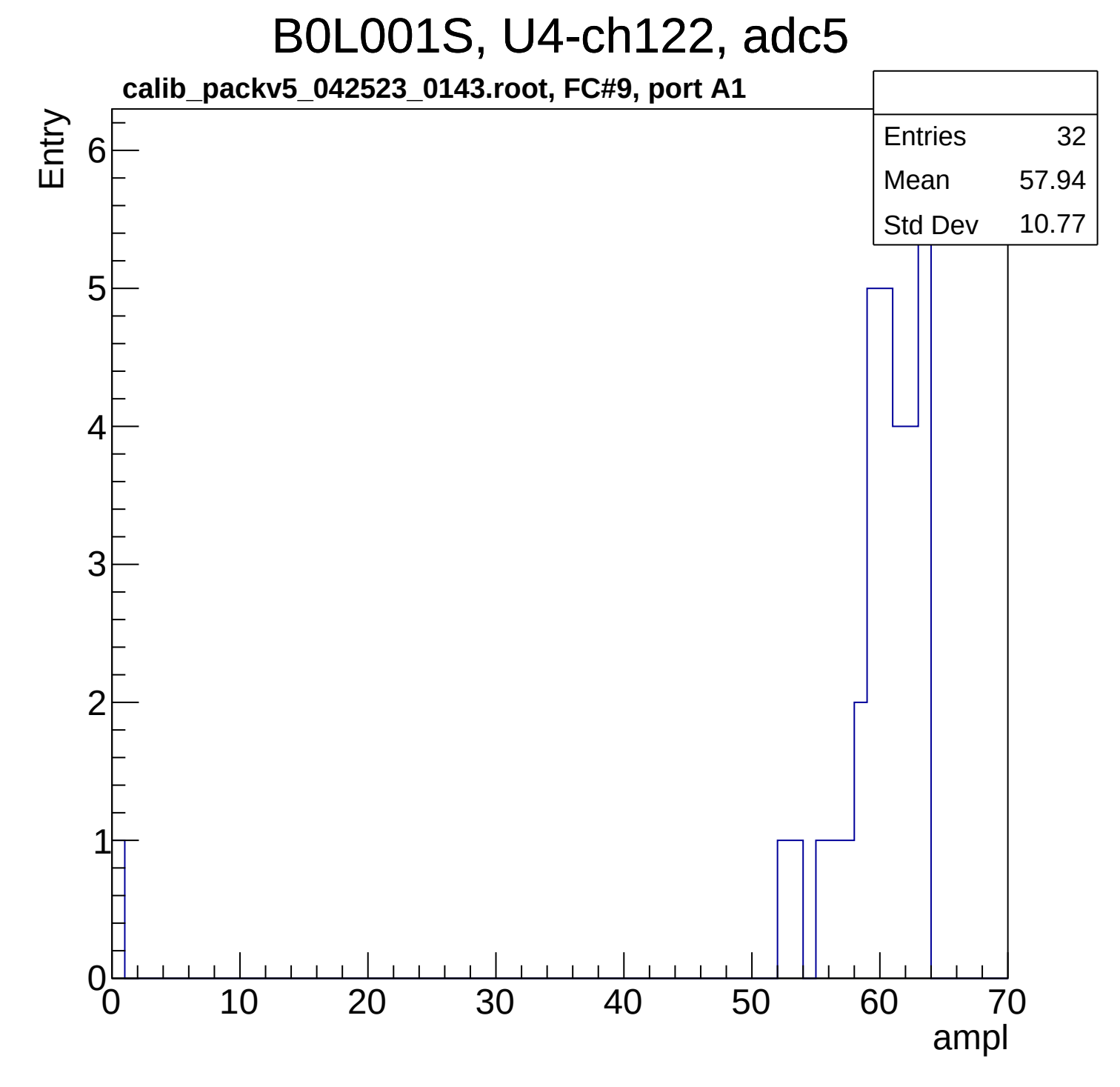
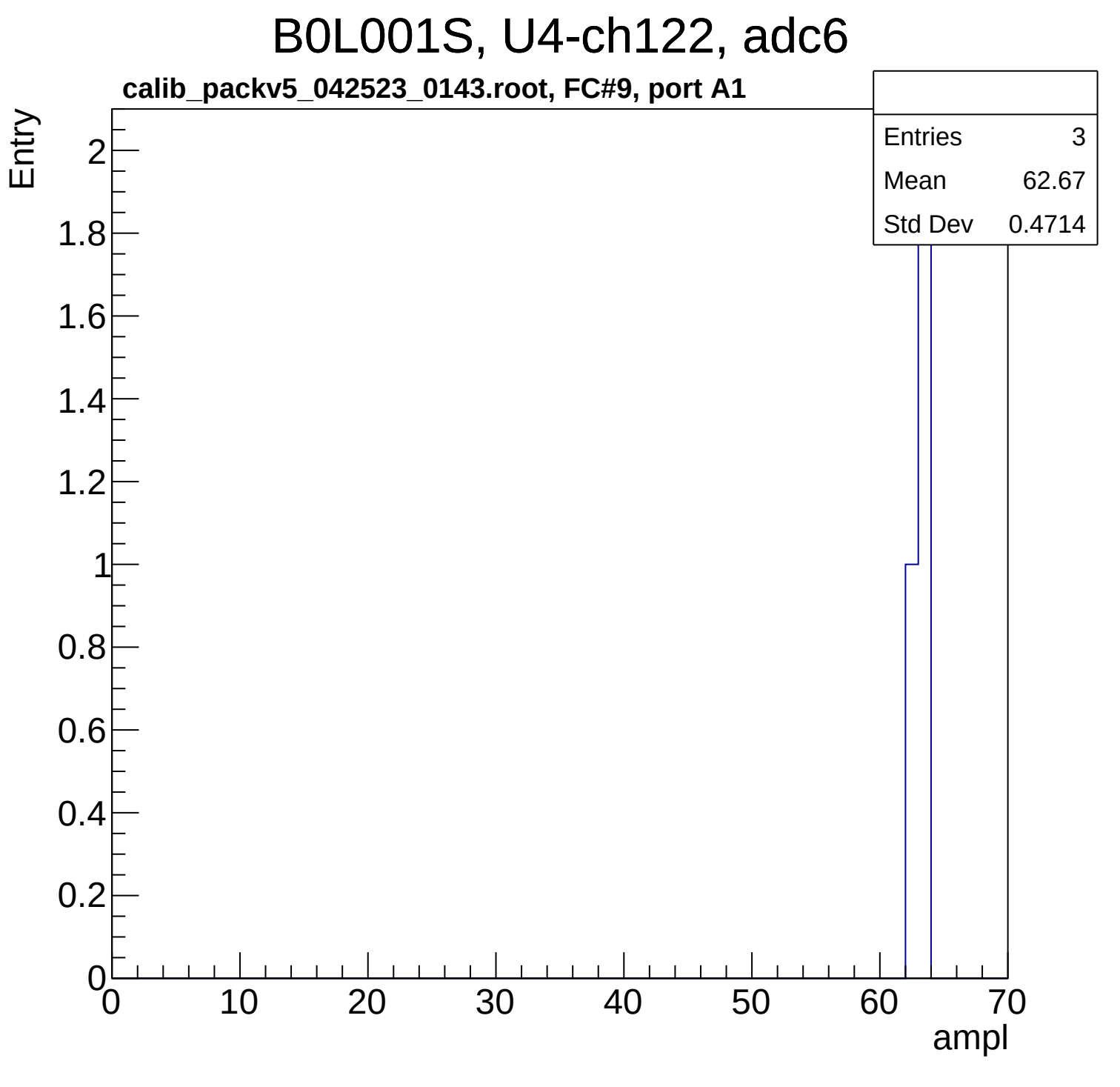
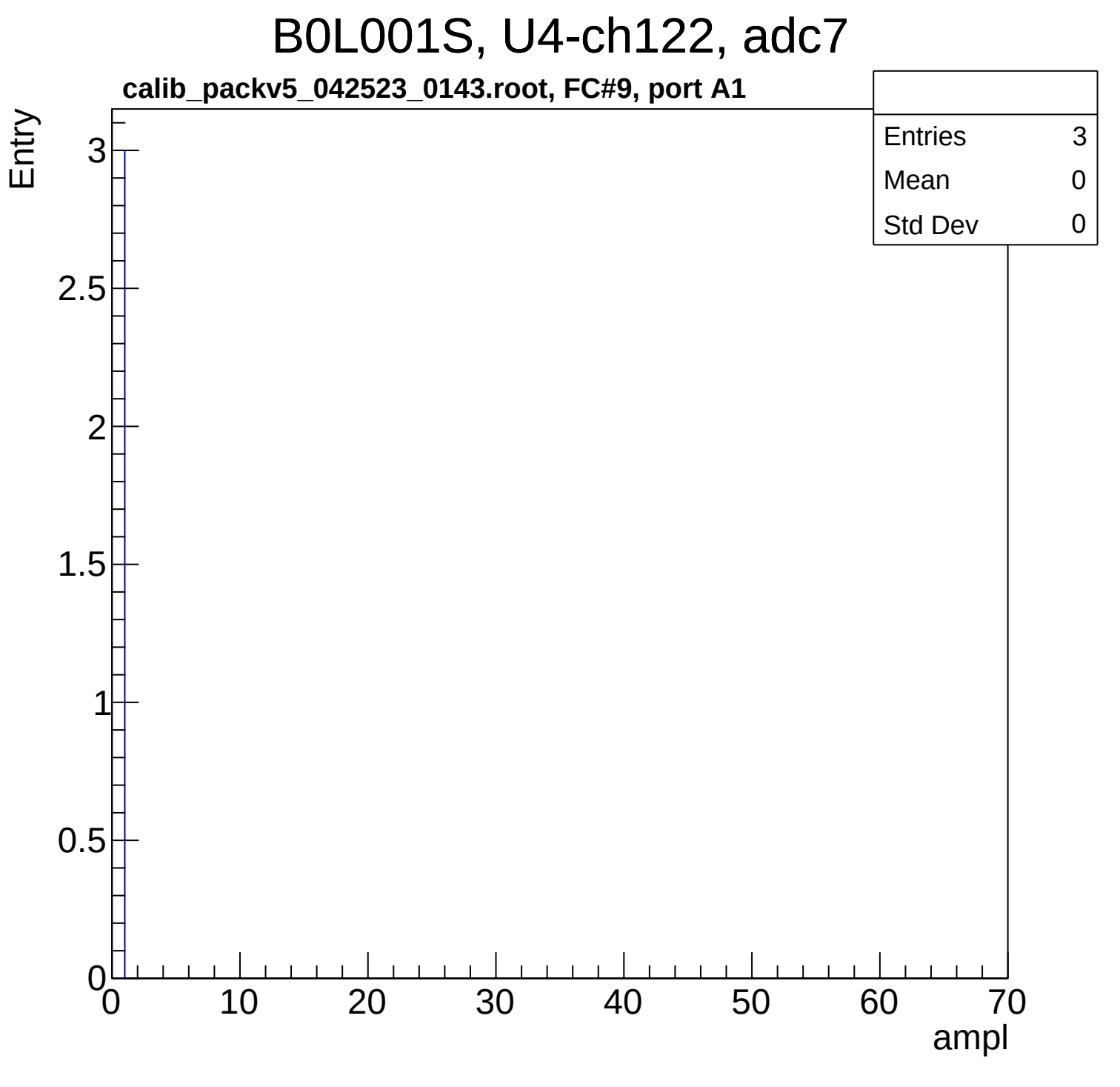
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch122, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 57.94
Std Dev 10.77
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch122, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch122, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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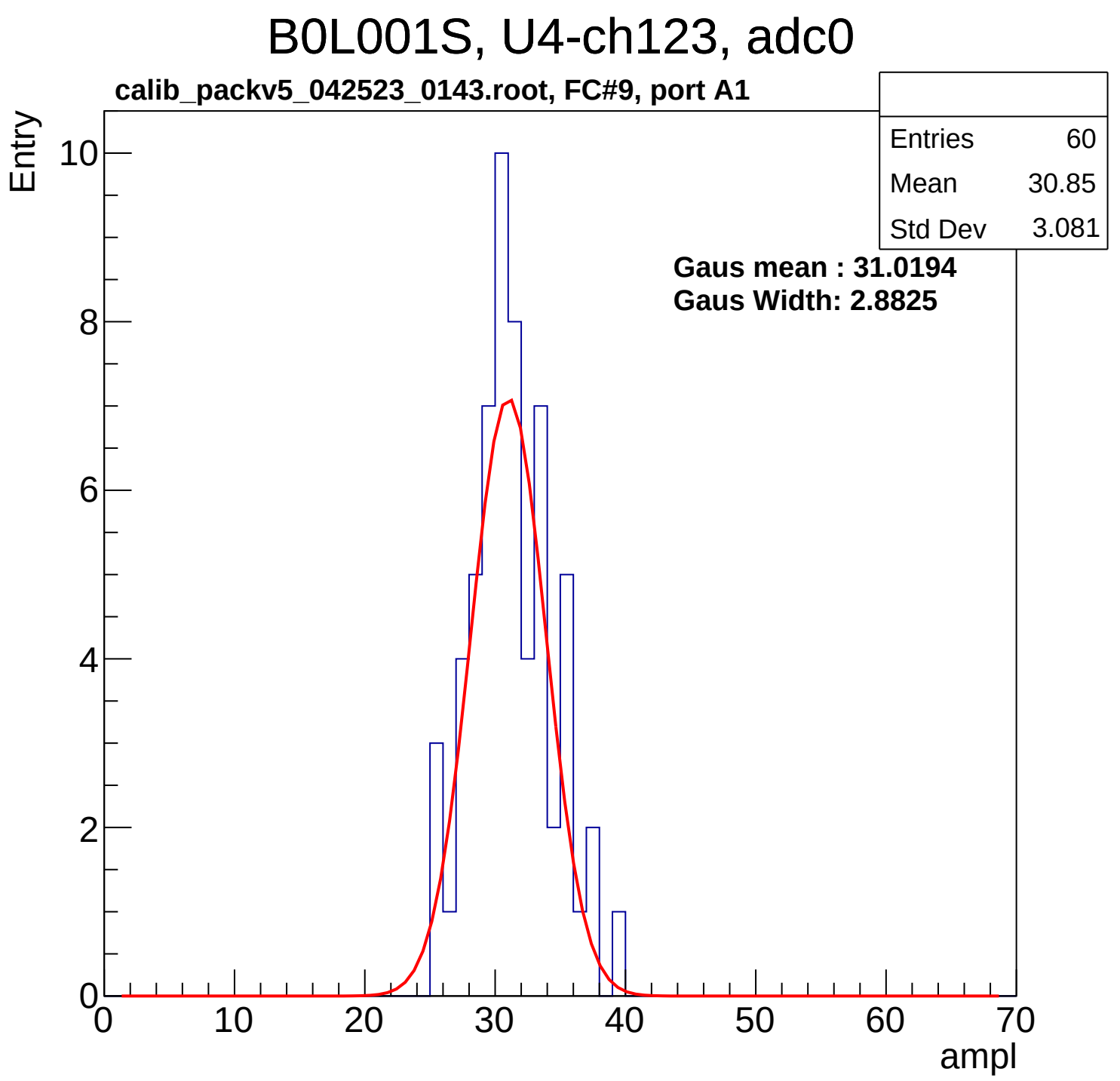
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch123, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.85
Std Dev 3.081
Gaus mean : 31.0194
Gaus Width: 2.8825
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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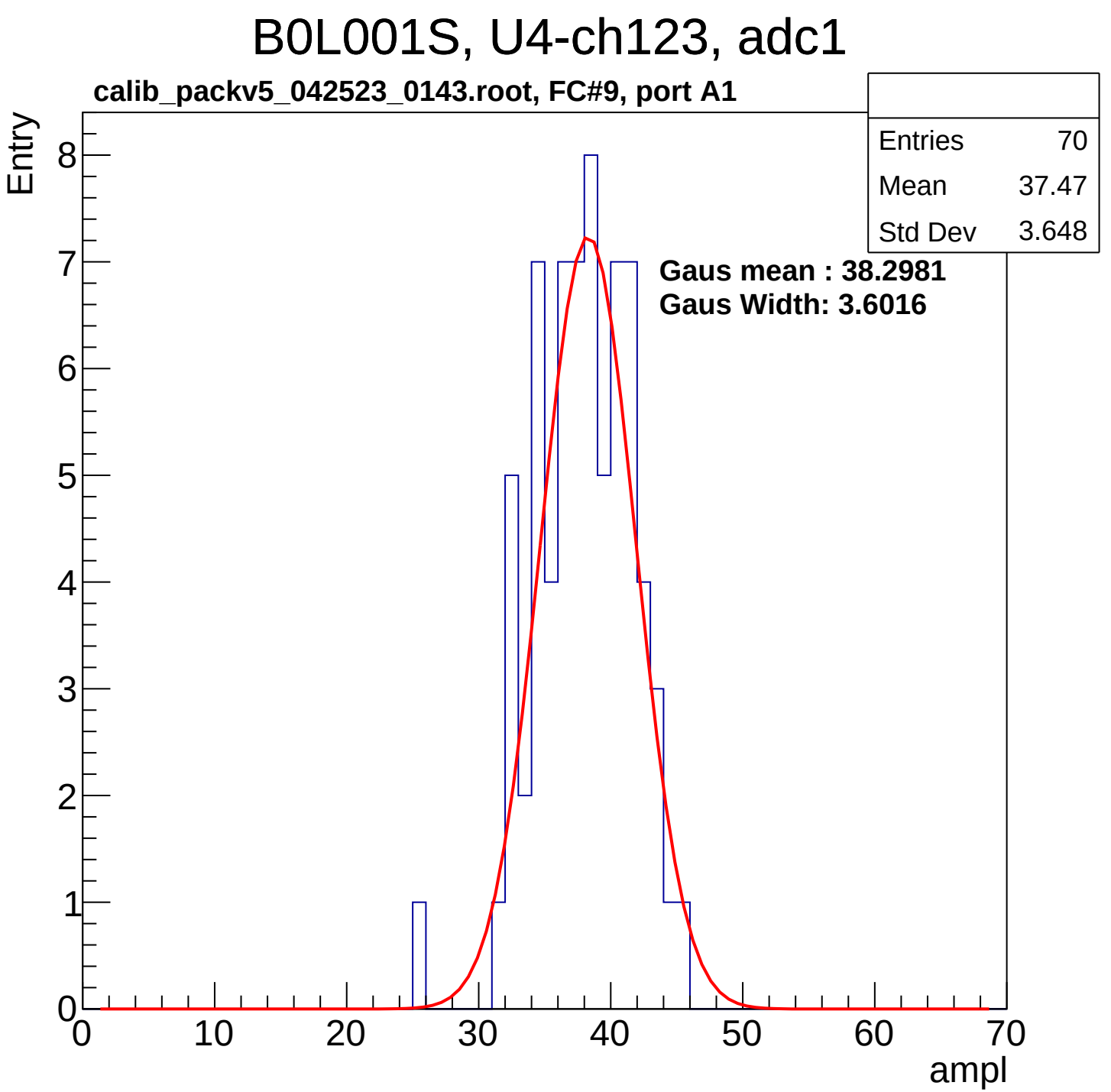
B0L001S, U4-ch123, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.47
Std Dev 3.648
Gaus mean : 38.2981
Gaus Width: 3.6016
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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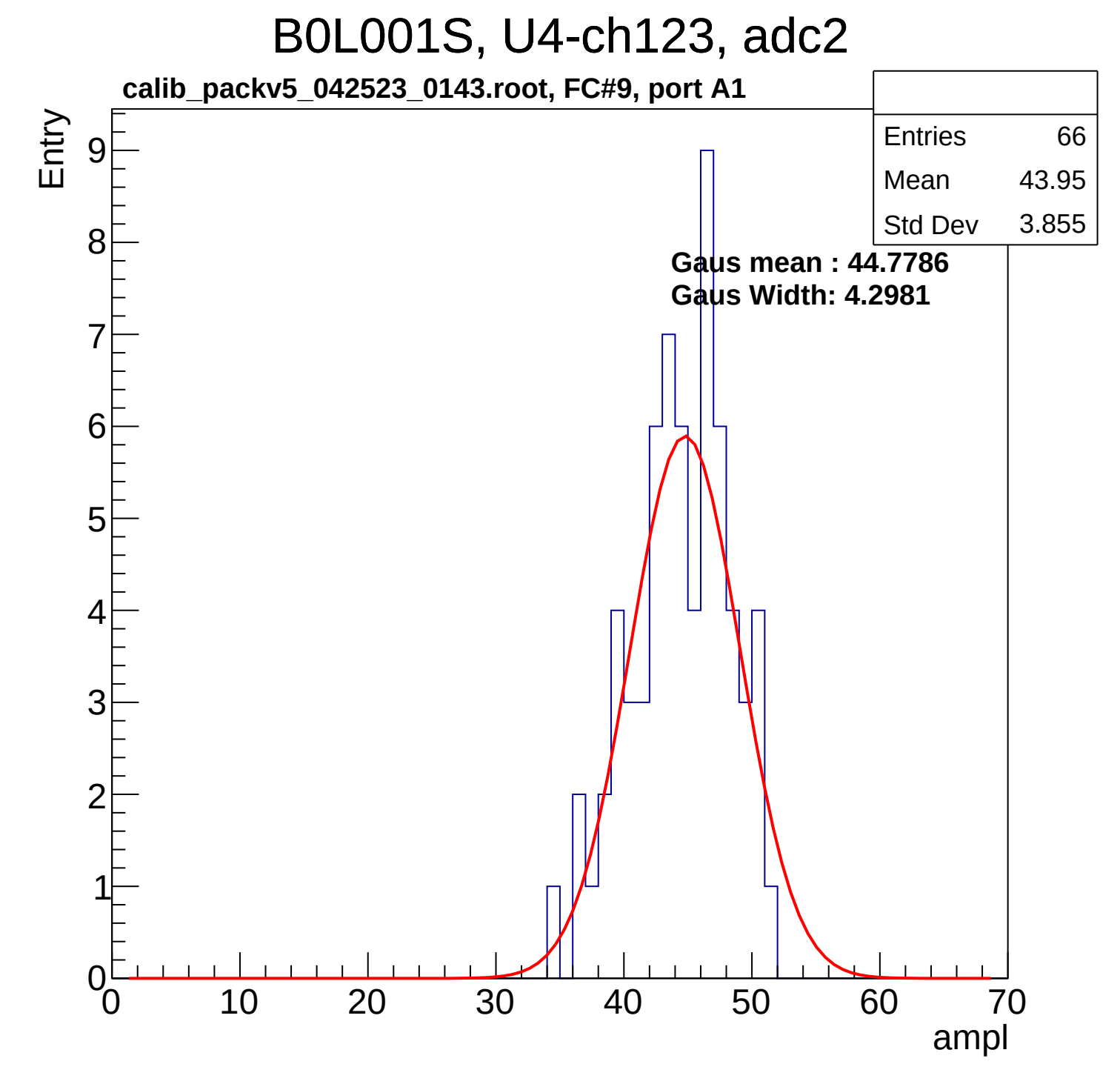
B0L001S, U4-ch123, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.95
Std Dev 3.855
Gaus mean : 44.7786
Gaus Width: 4.2981
Entry
ampl
0
1
2
3
4
5
6
7
8
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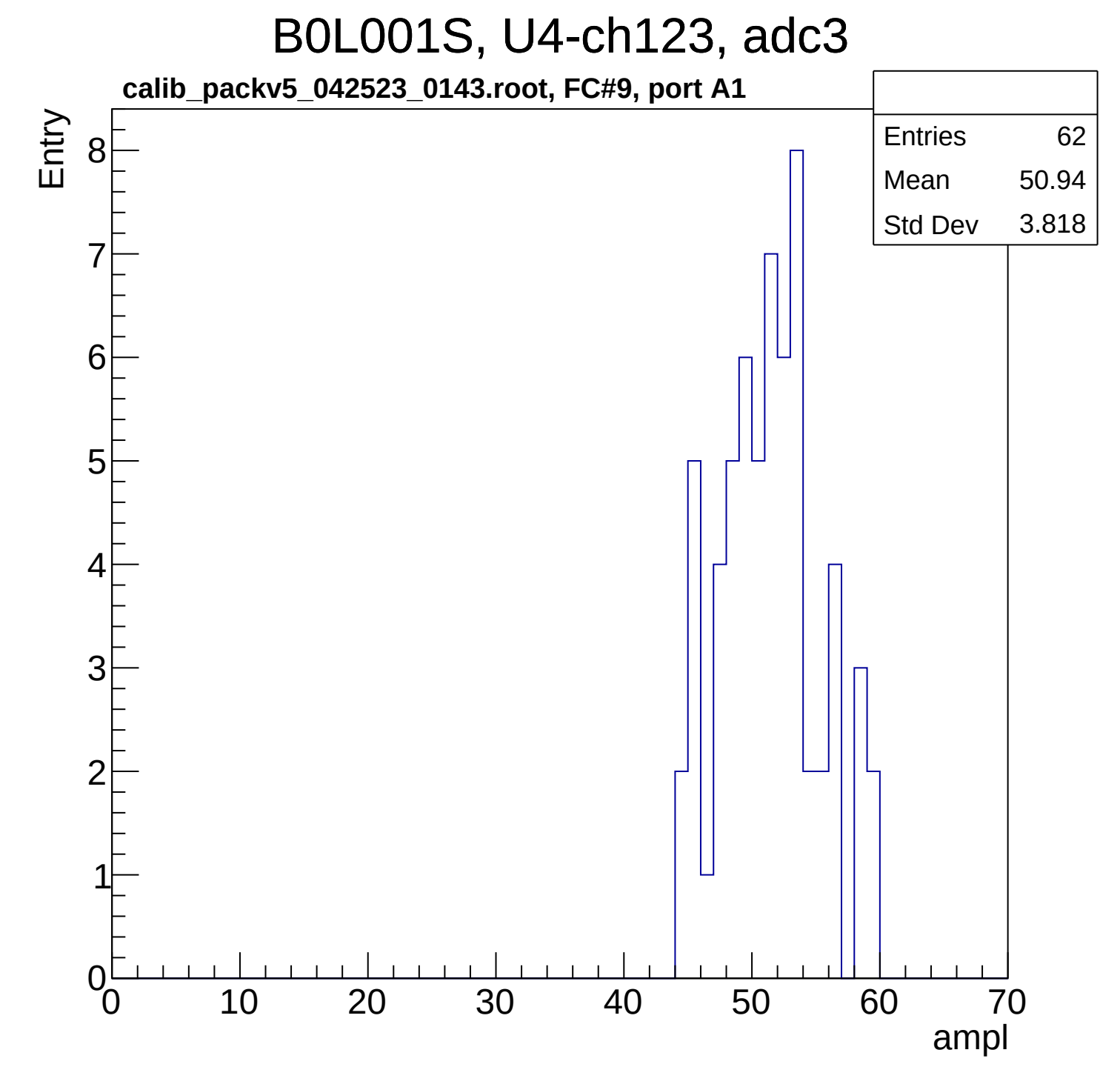
B0L001S, U4-ch123, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.94
Std Dev 3.818
Entry
ampl
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1
2
3
4
5
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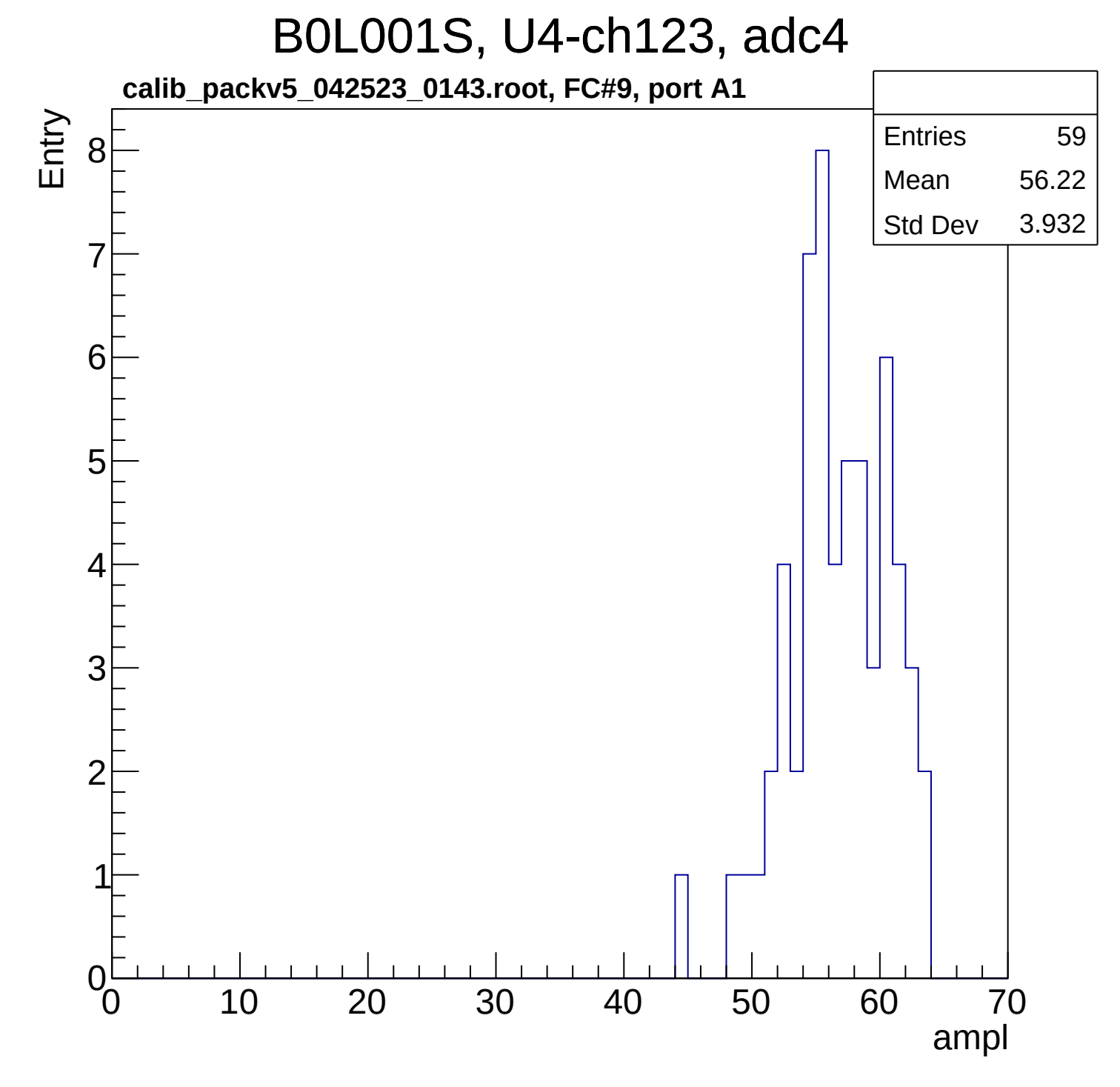
B0L001S, U4-ch123, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.22
Std Dev 3.932
Entry
ampl
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1
2
3
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5
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7
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0
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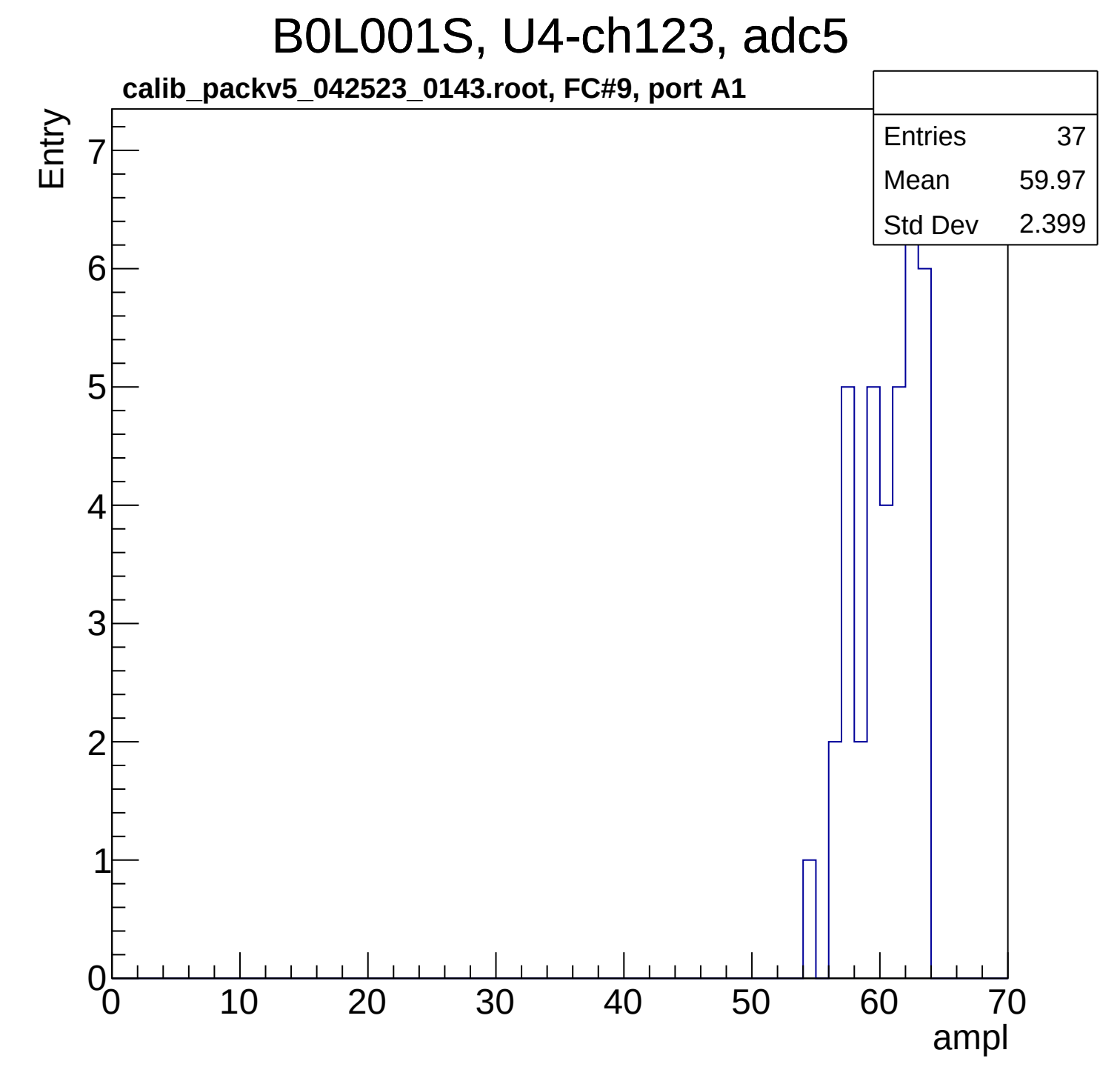
B0L001S, U4-ch123, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.97
Std Dev 2.399
Entry
ampl
0
1
2
3
4
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6
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0
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20
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40
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60
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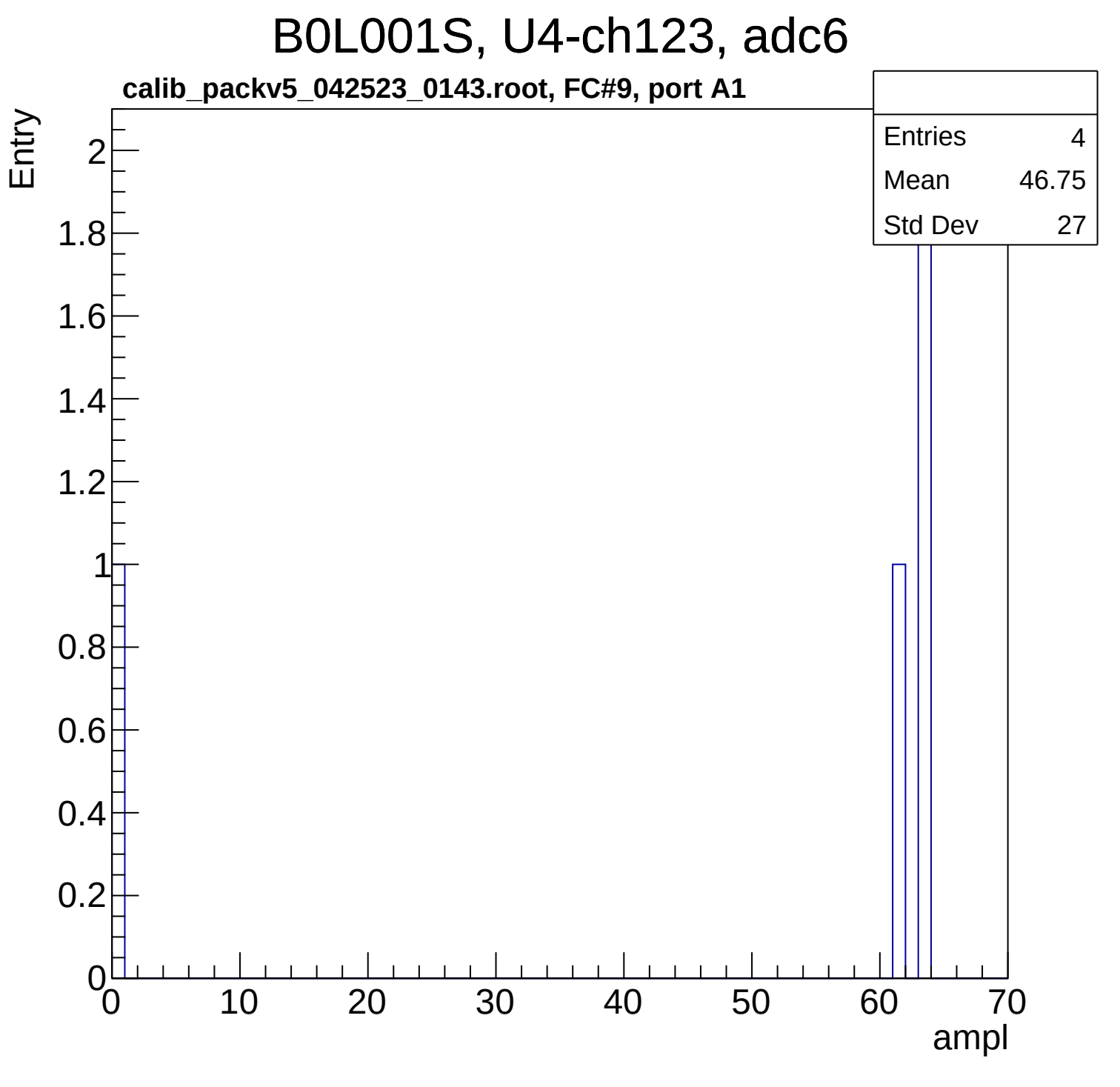
B0L001S, U4-ch123, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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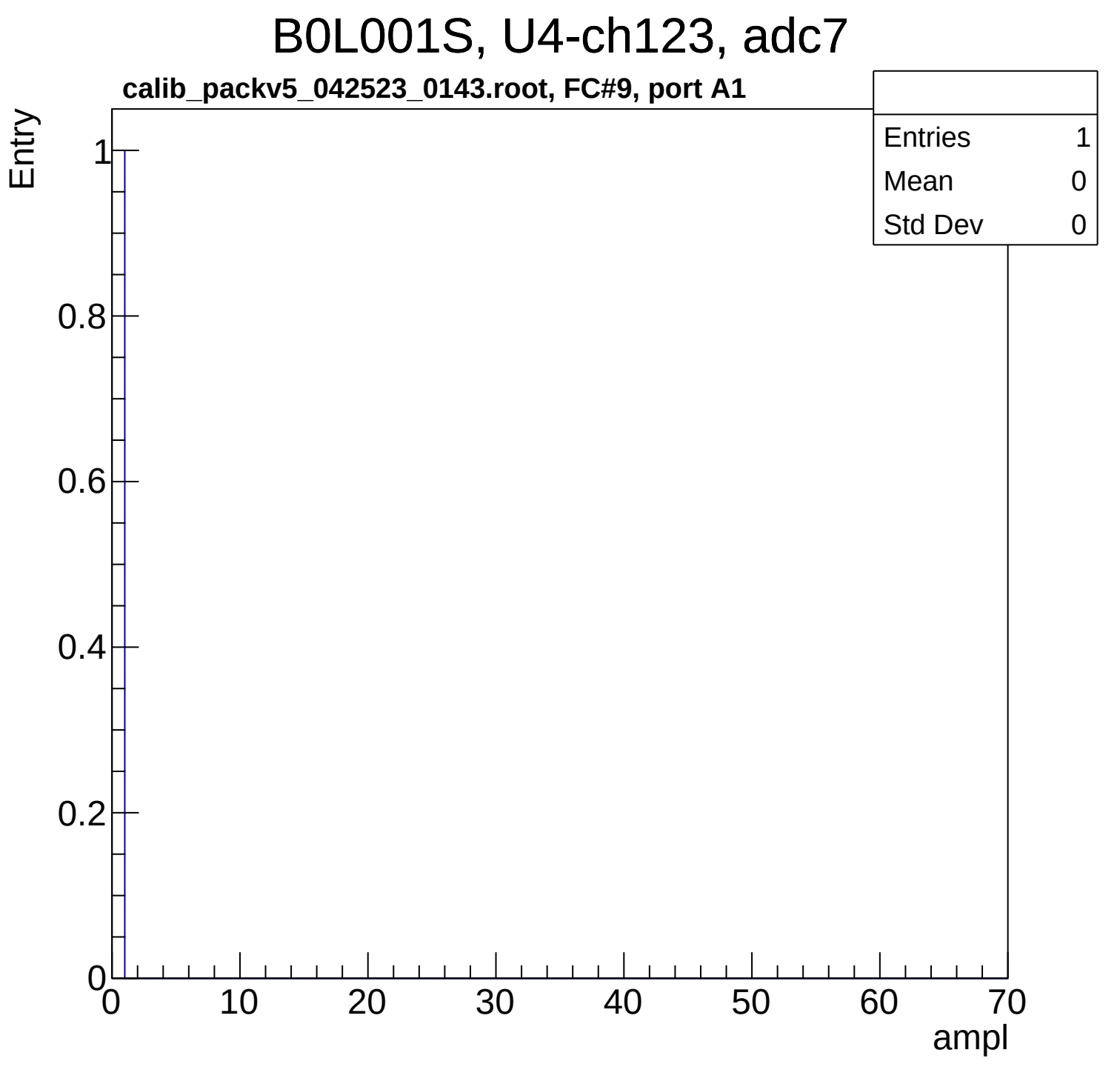
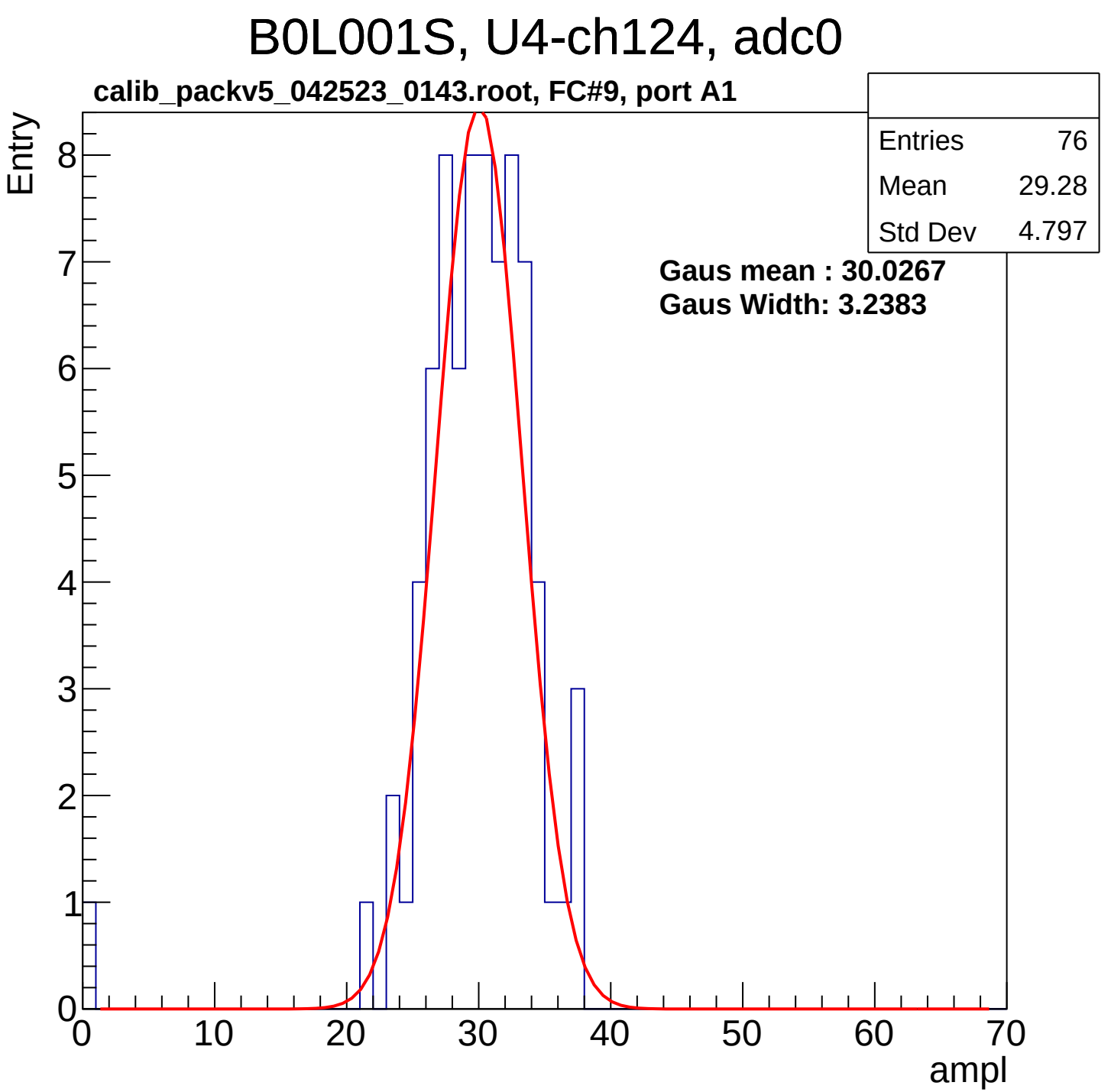

B0L001S, U4-ch123, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch124, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 29.28
Std Dev 4.797
Gaus mean : 30.0267
Gaus Width: 3.2383
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
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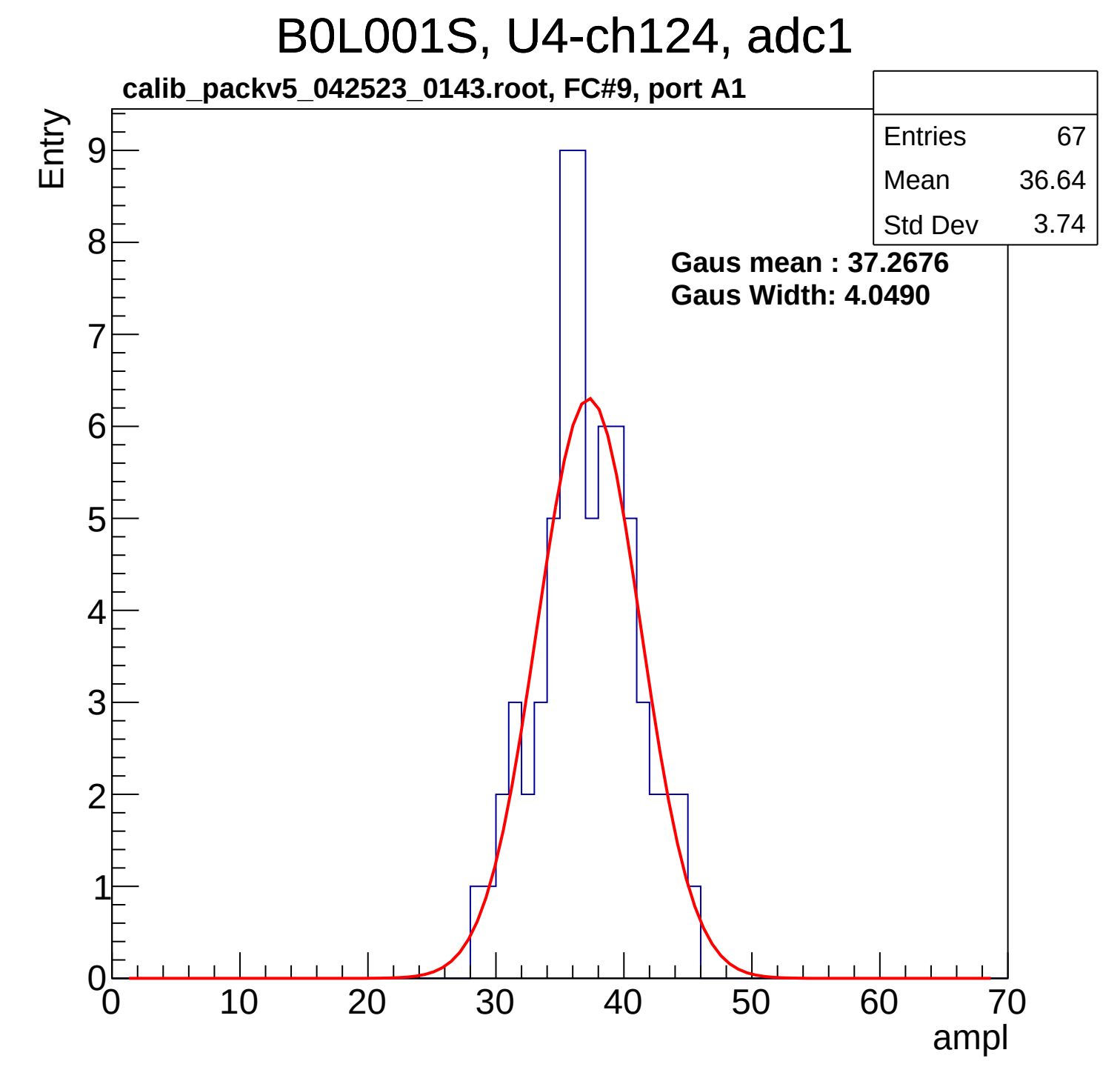
B0L001S, U4-ch124, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.64
Std Dev 3.74
Gaus mean : 37.2676
Gaus Width: 4.0490
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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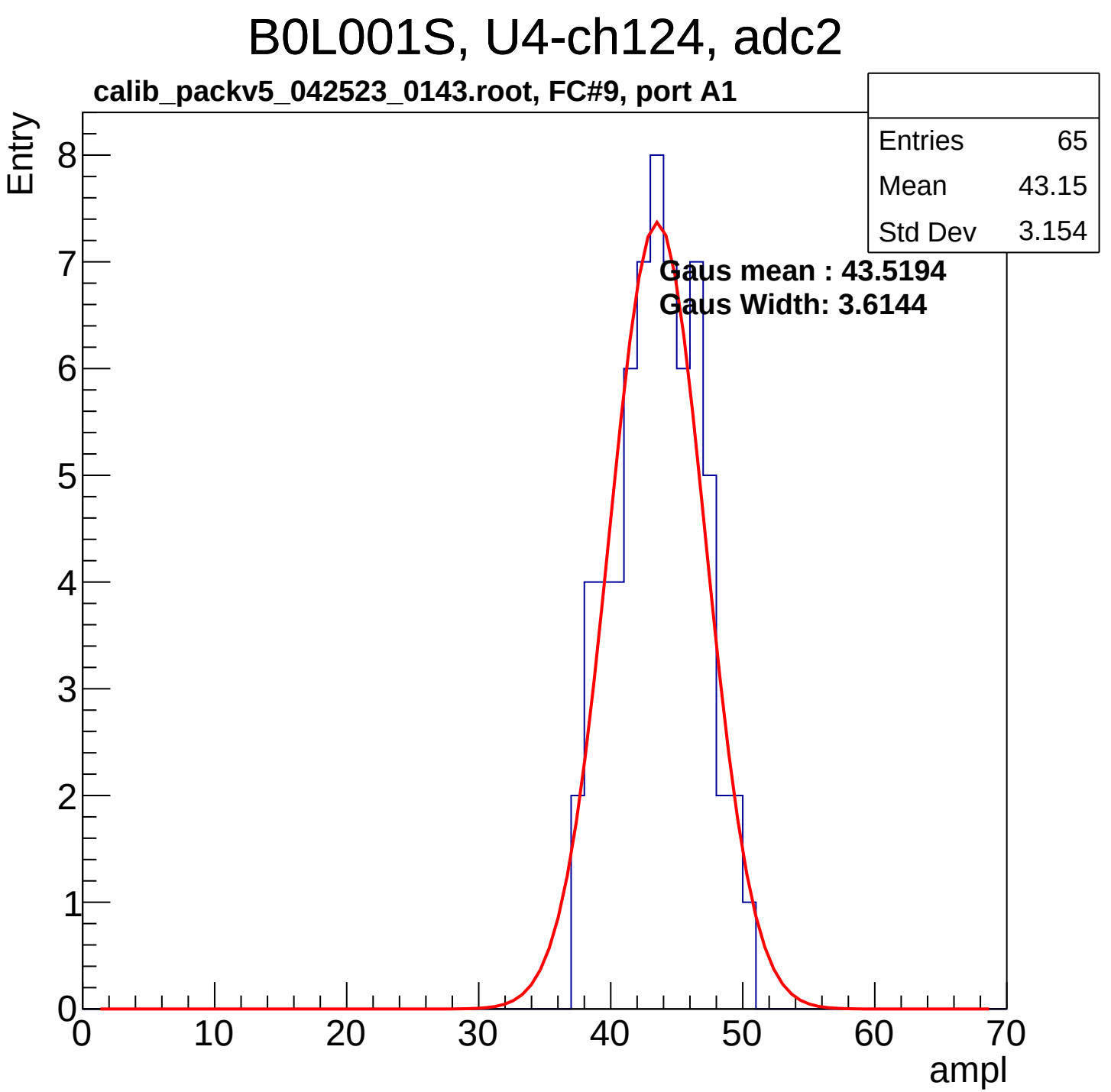
B0L001S, U4-ch124, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.15
Std Dev 3.154
Gaus mean : 43.5194
Gaus Width: 3.6144
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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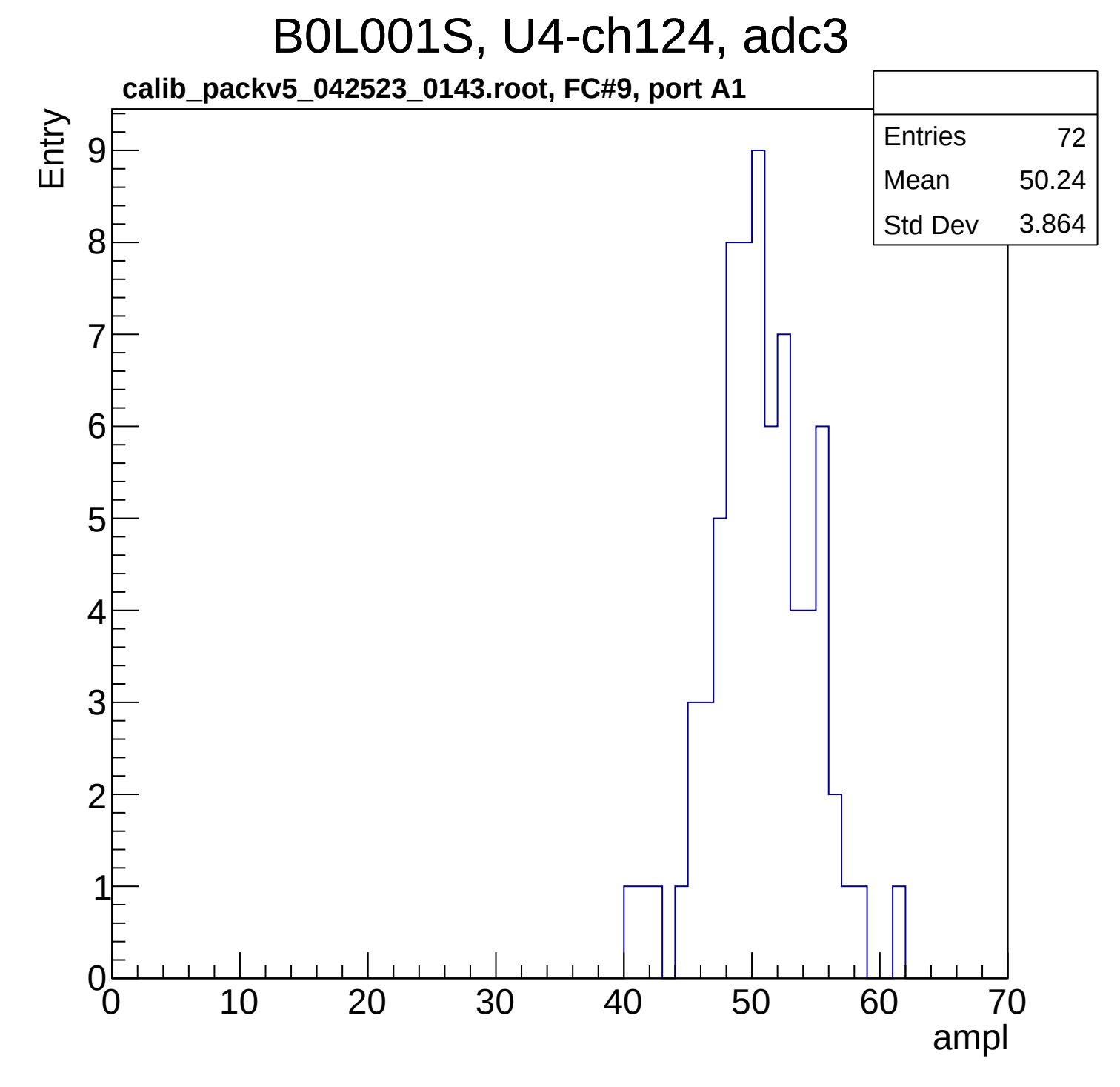
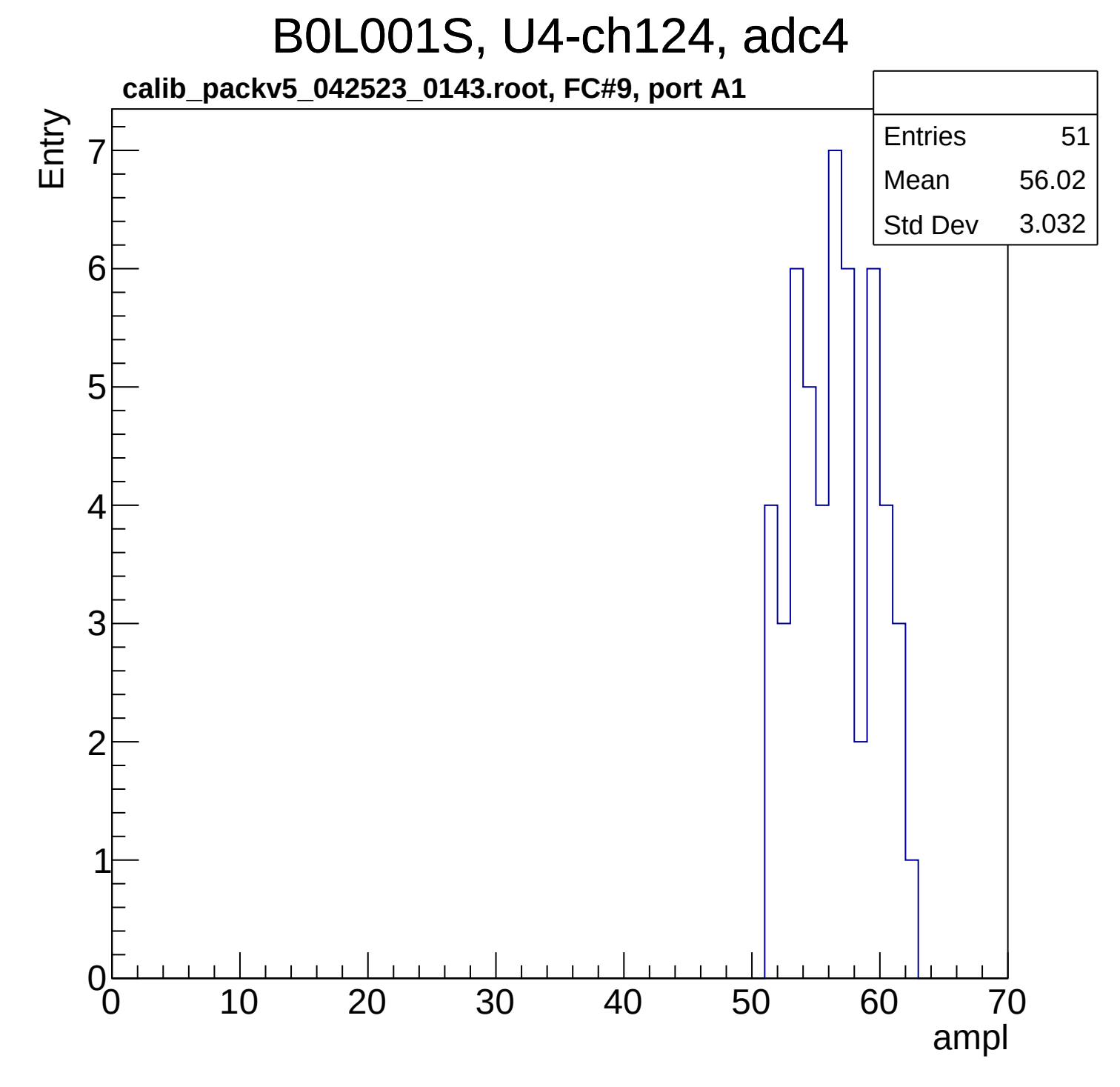

B0L001S, U4-ch124, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 50.24
Std Dev 3.864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch124, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.02
Std Dev 3.032
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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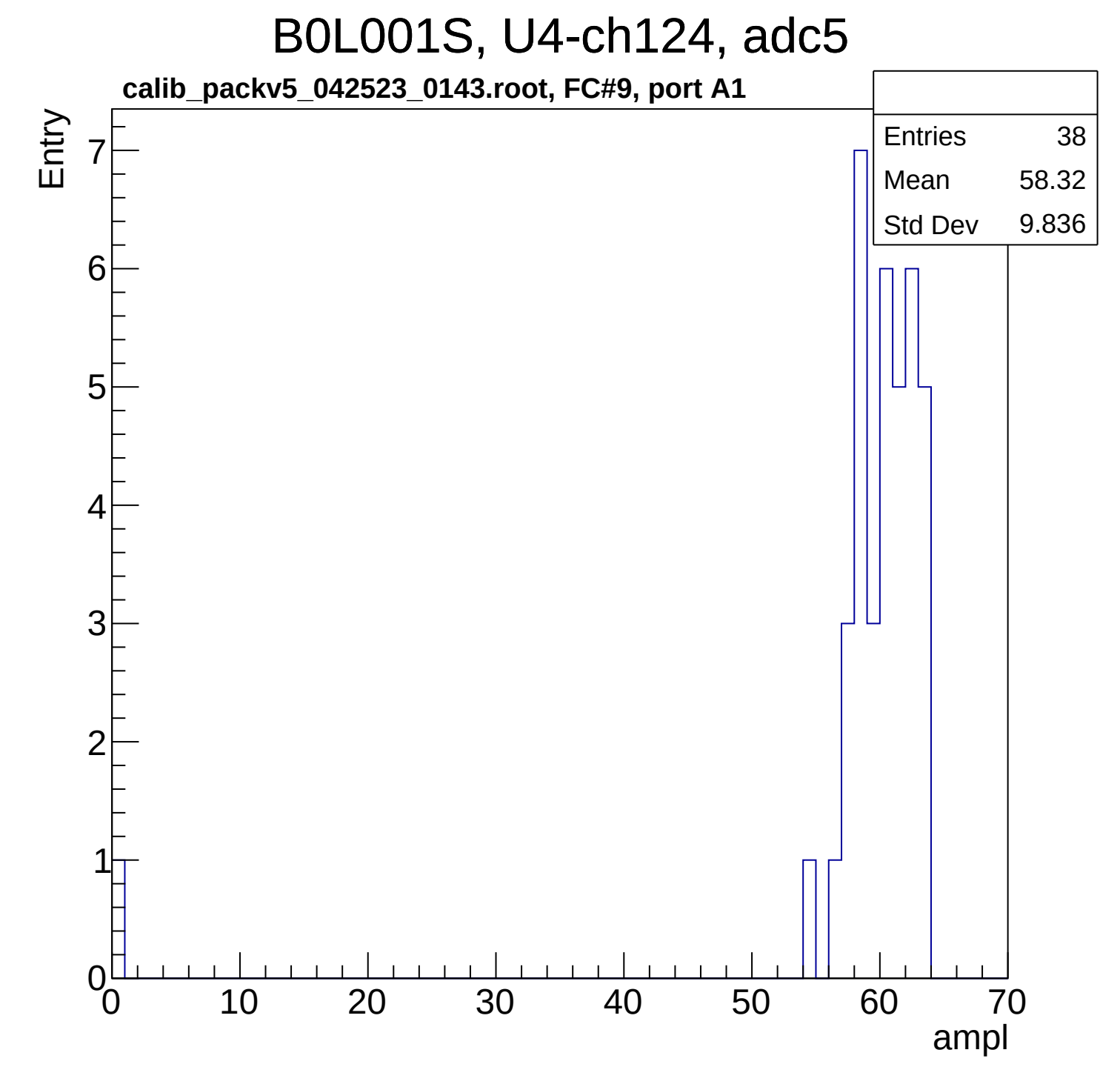
B0L001S, U4-ch124, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.32
Std Dev 9.836
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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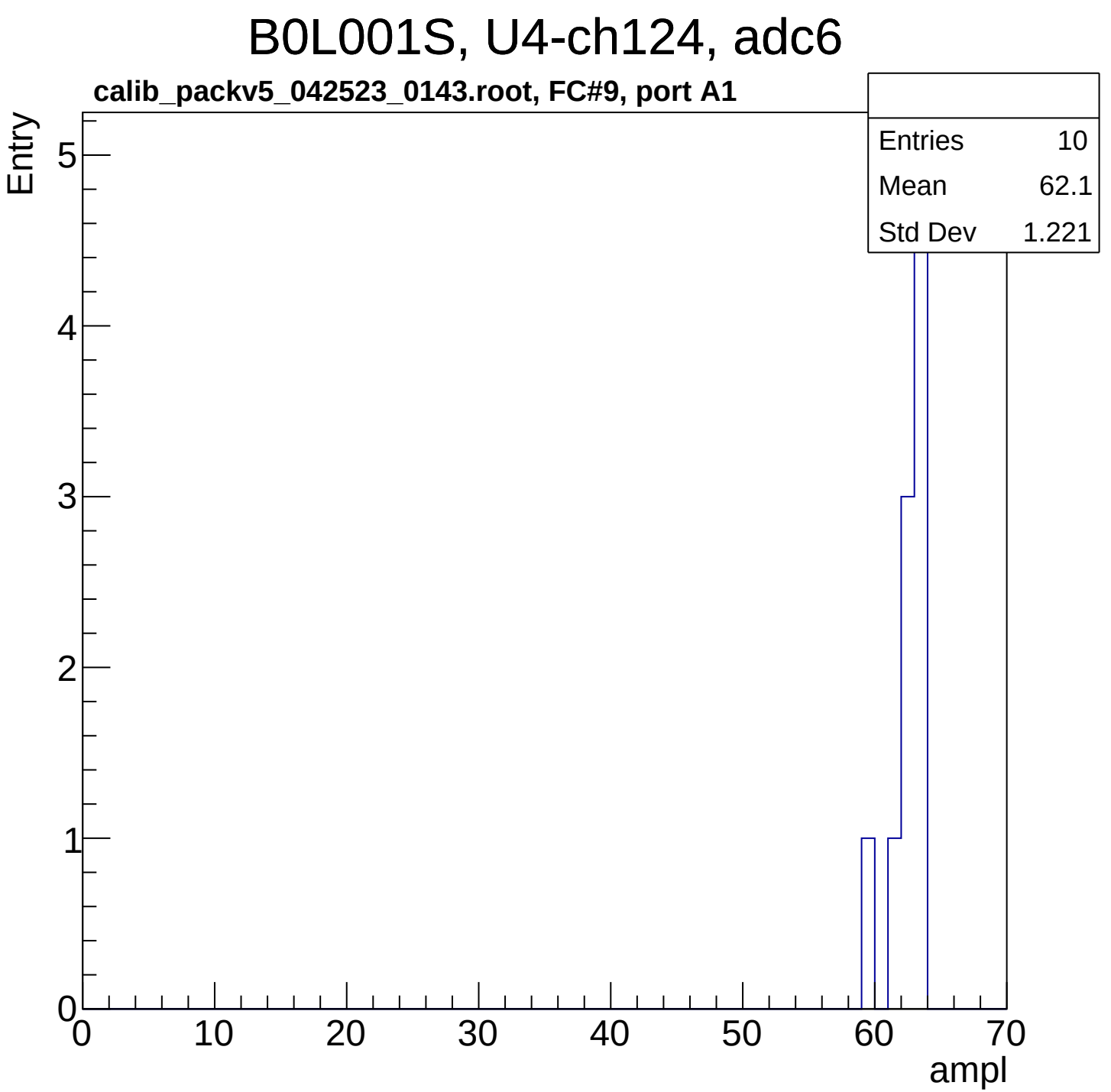
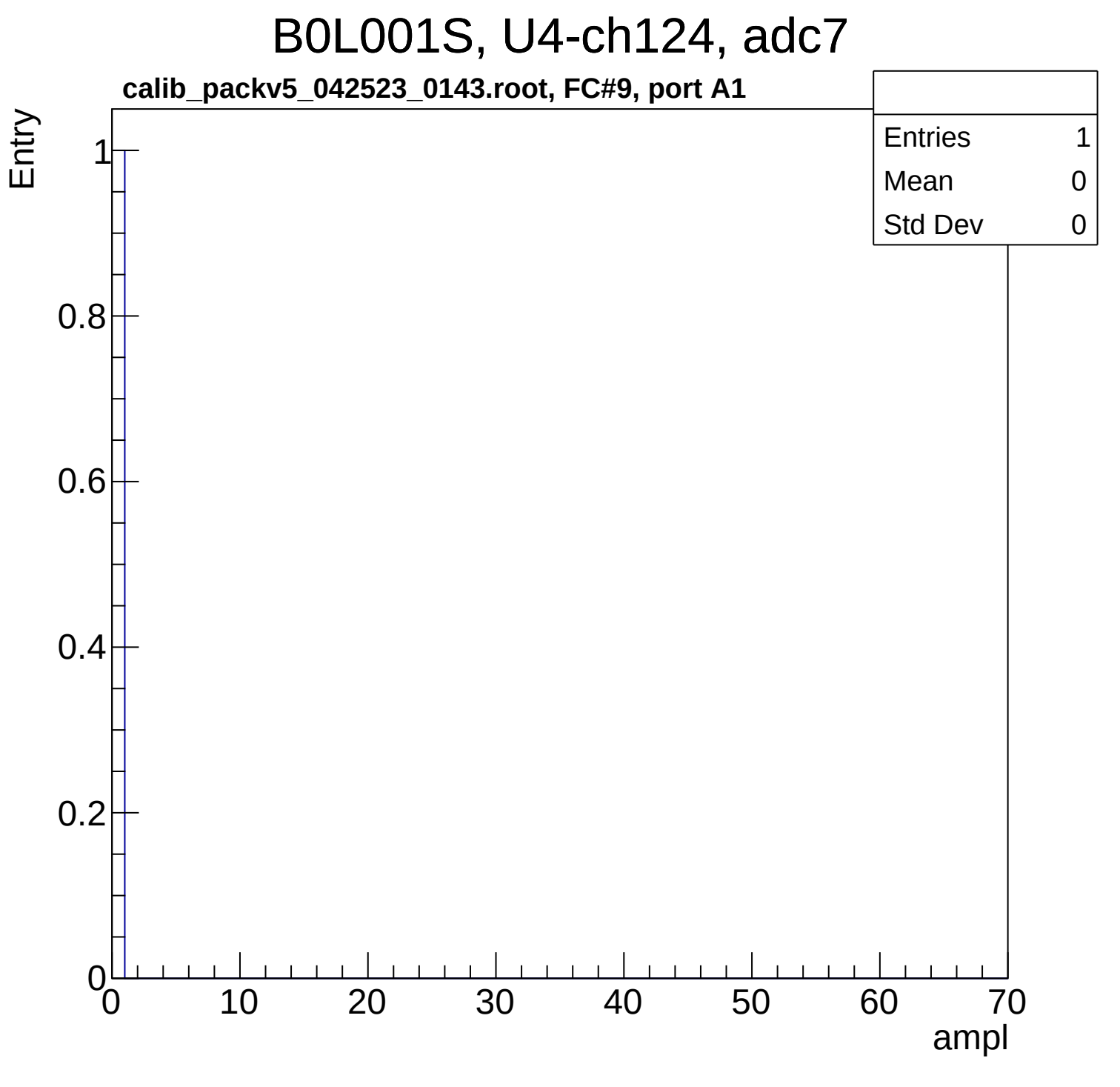
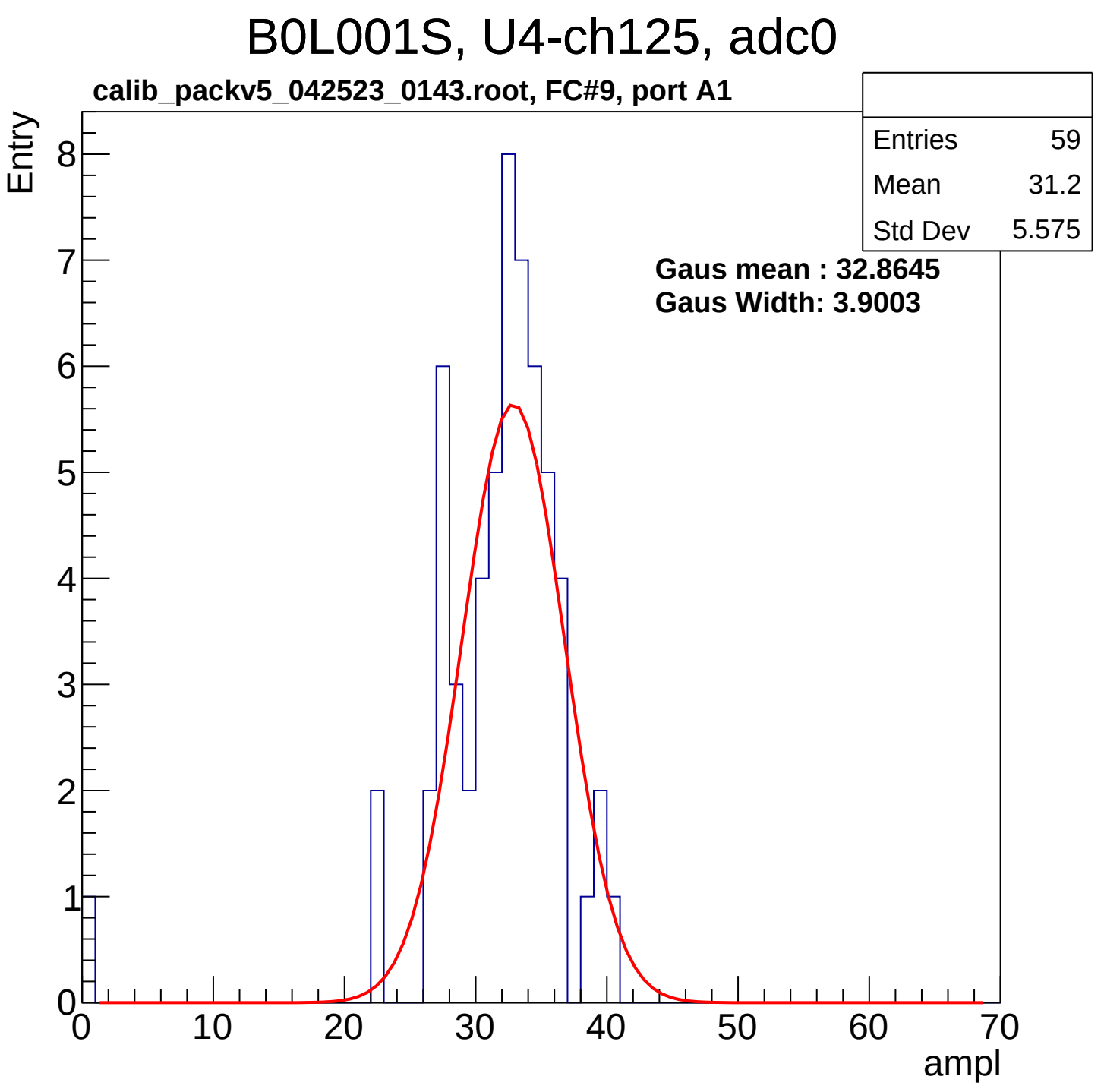
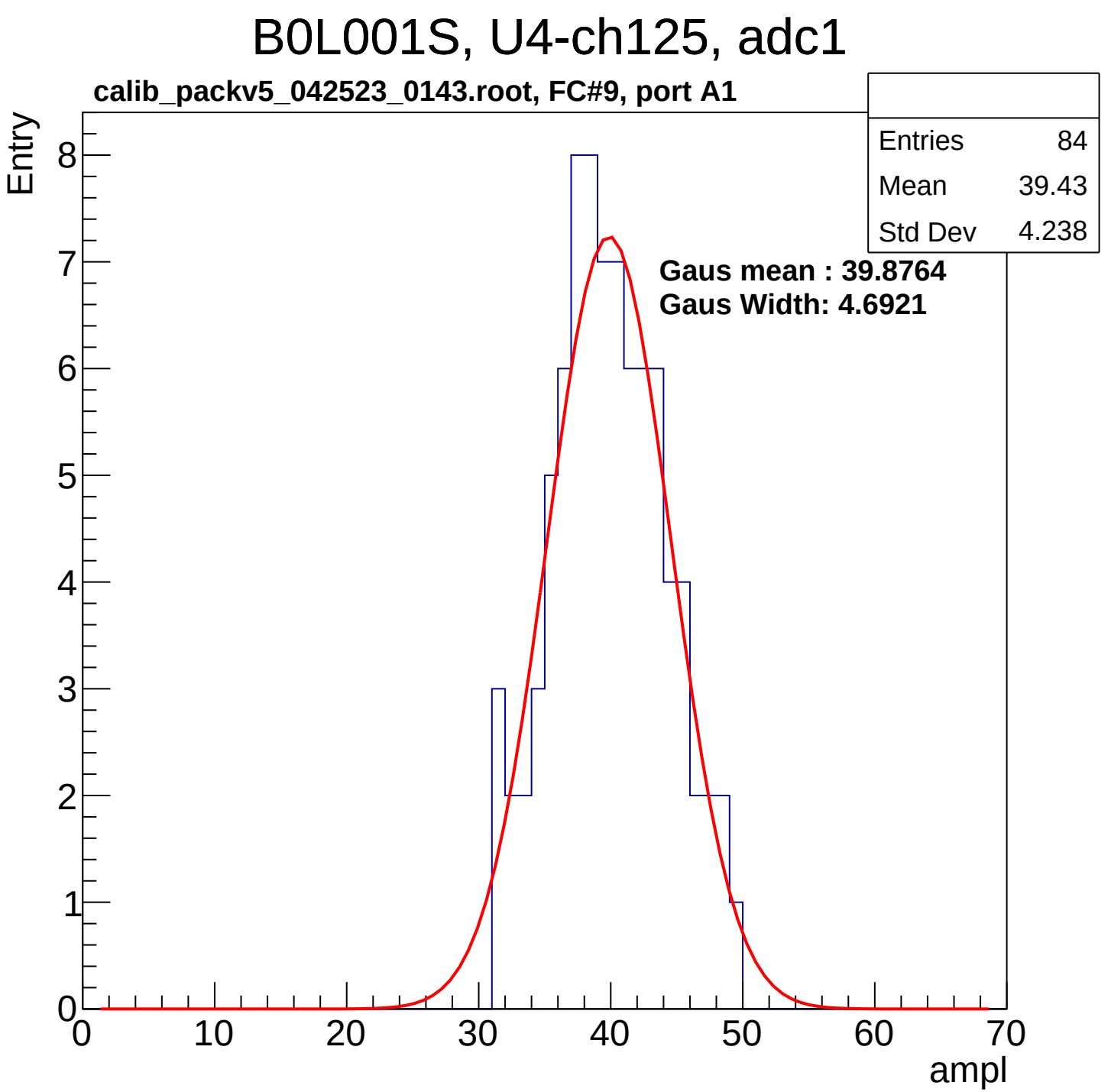
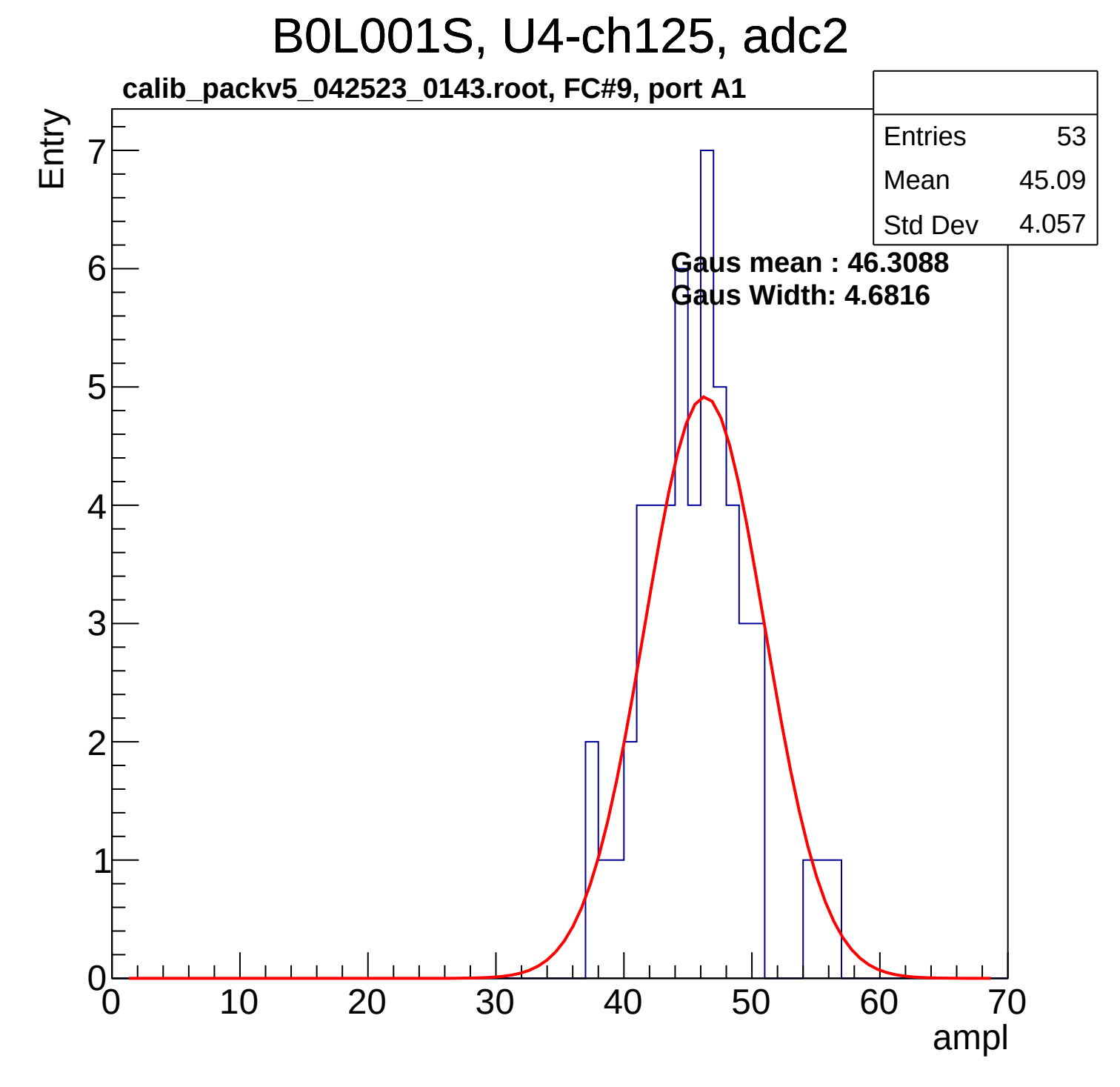

B0L001S, U4-ch124, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 62.1
Std Dev 1.221
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch124, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch125, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 31.2
Std Dev 5.575
Gaus mean : 32.8645
Gaus Width: 3.9003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch125, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 39.43
Std Dev 4.238
Gaus mean : 39.8764
Gaus Width: 4.6921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch125, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 45.09
Std Dev 4.057
Gaus mean : 46.3088
Gaus Width: 4.6816
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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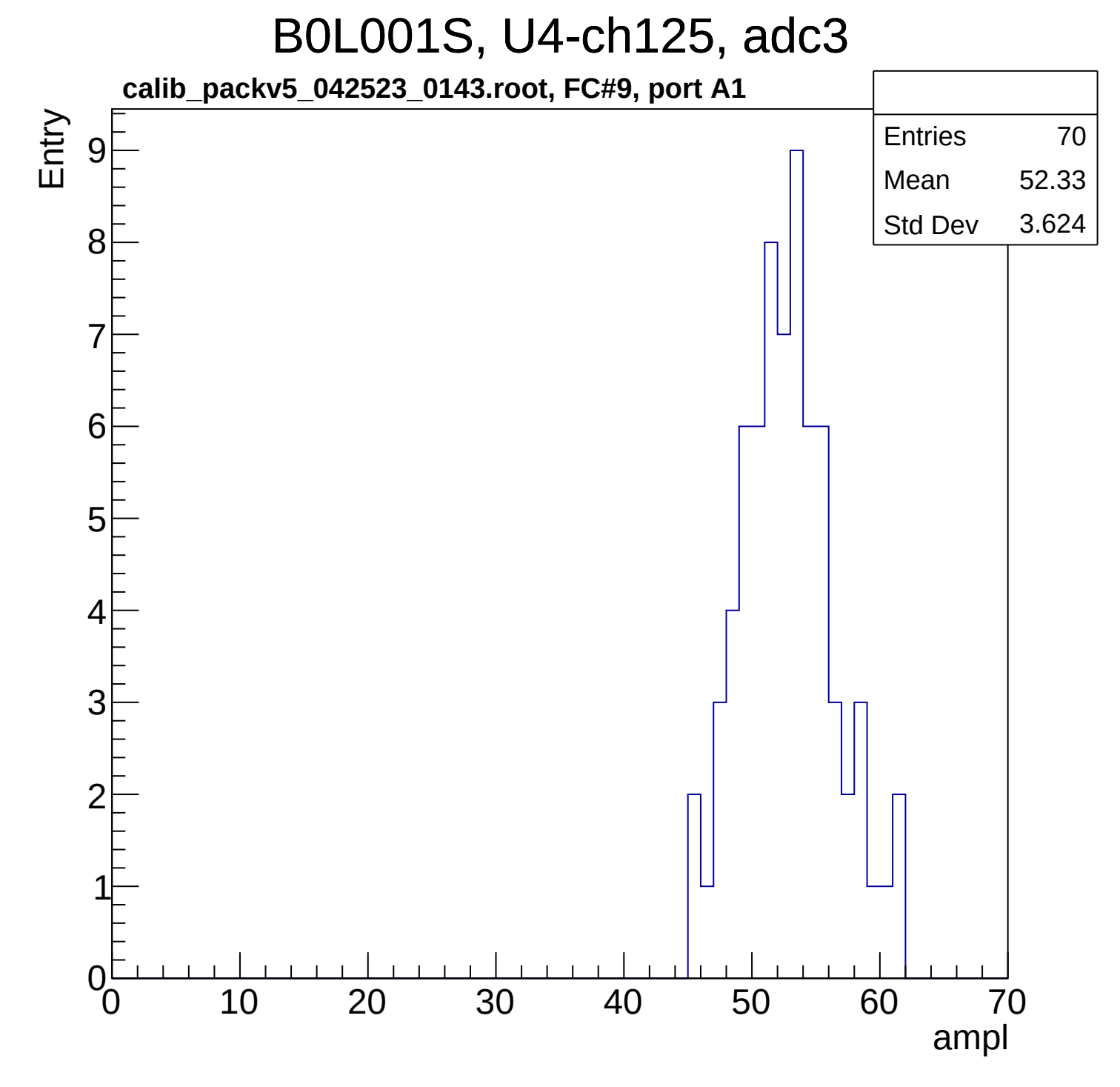
B0L001S, U4-ch125, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 52.33
Std Dev 3.624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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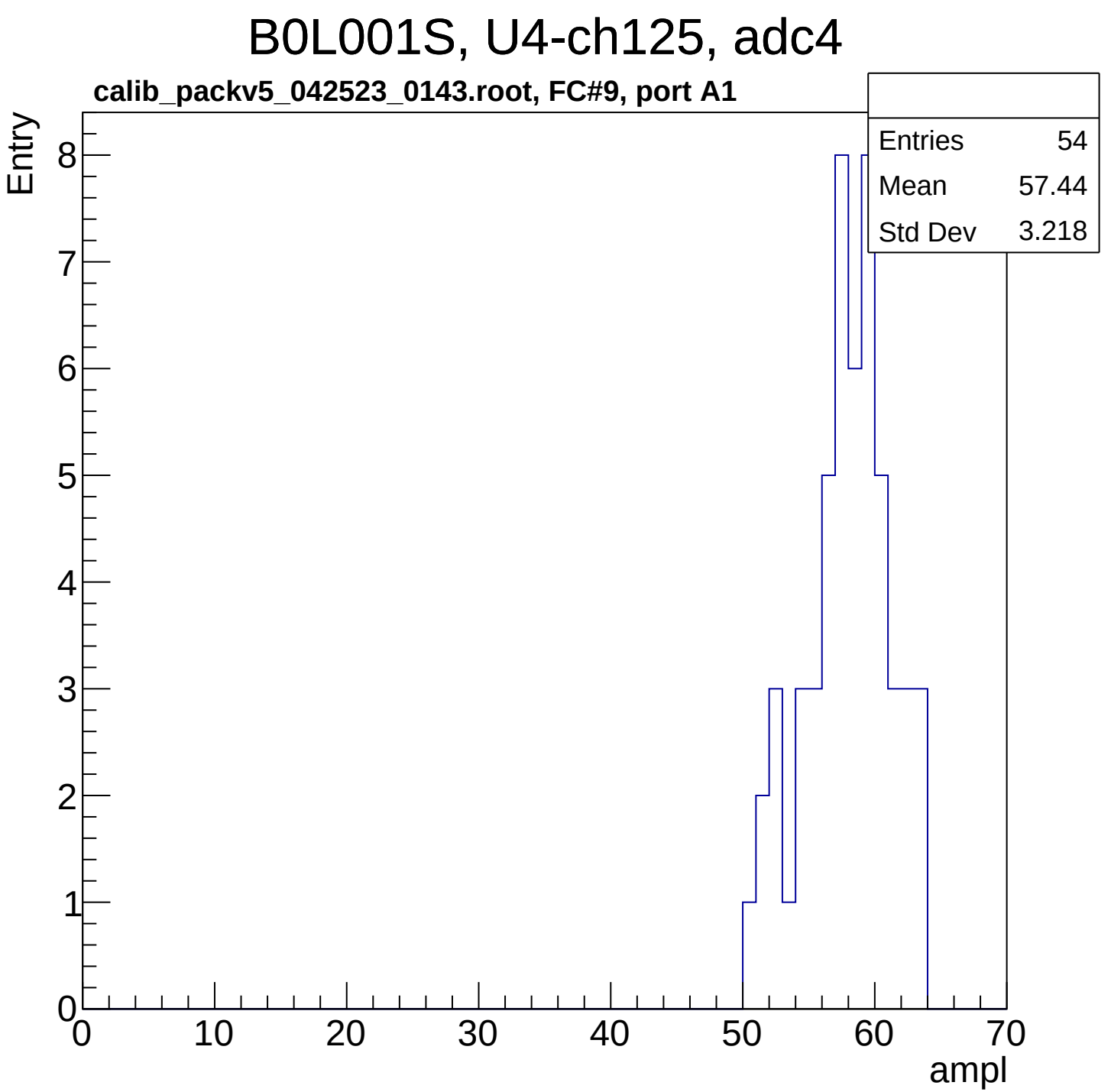
B0L001S, U4-ch125, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.44
Std Dev 3.218
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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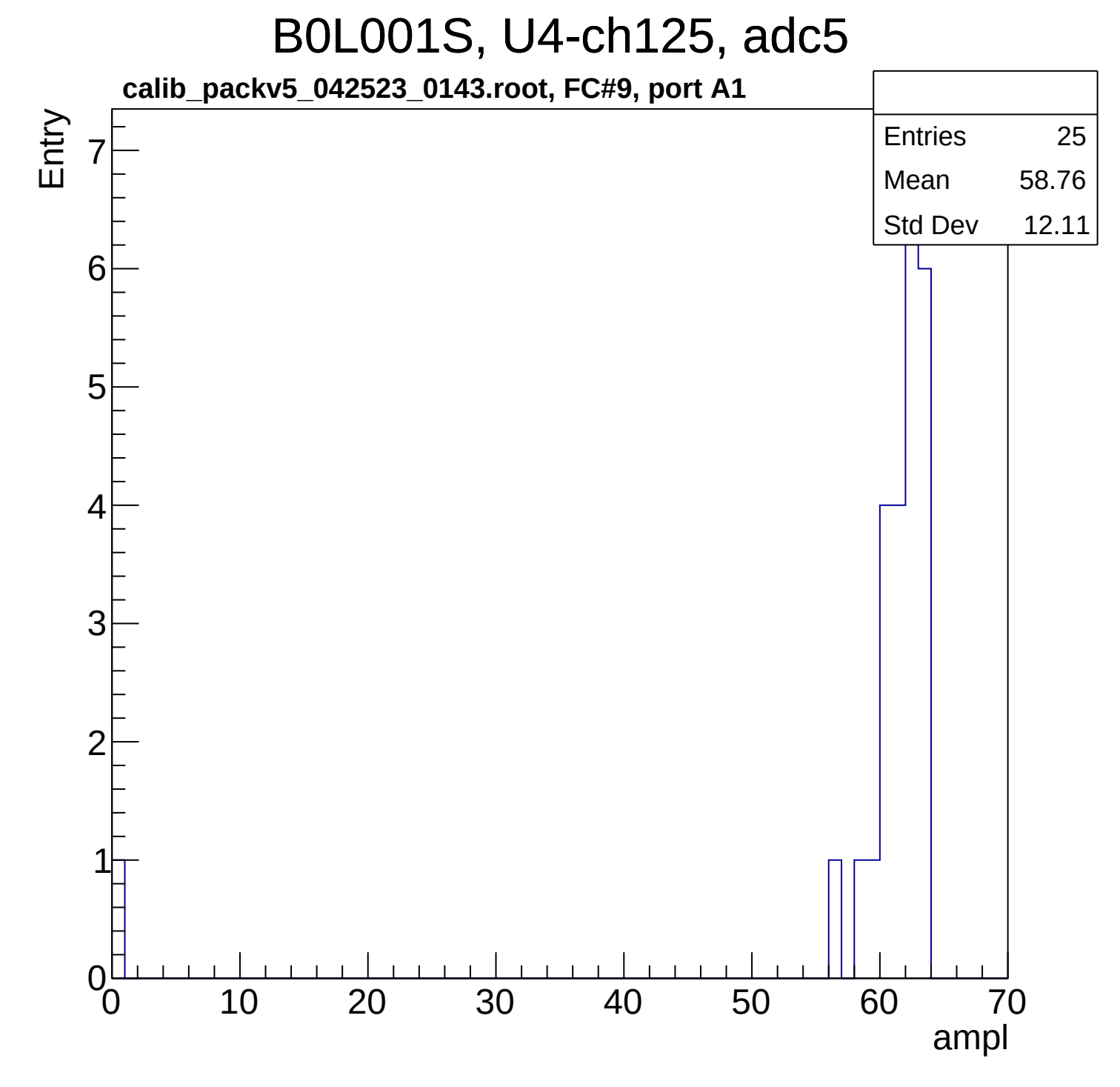
B0L001S, U4-ch125, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.76
Std Dev 12.11
Entry
ampl
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2
3
4
5
6
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0
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40
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60
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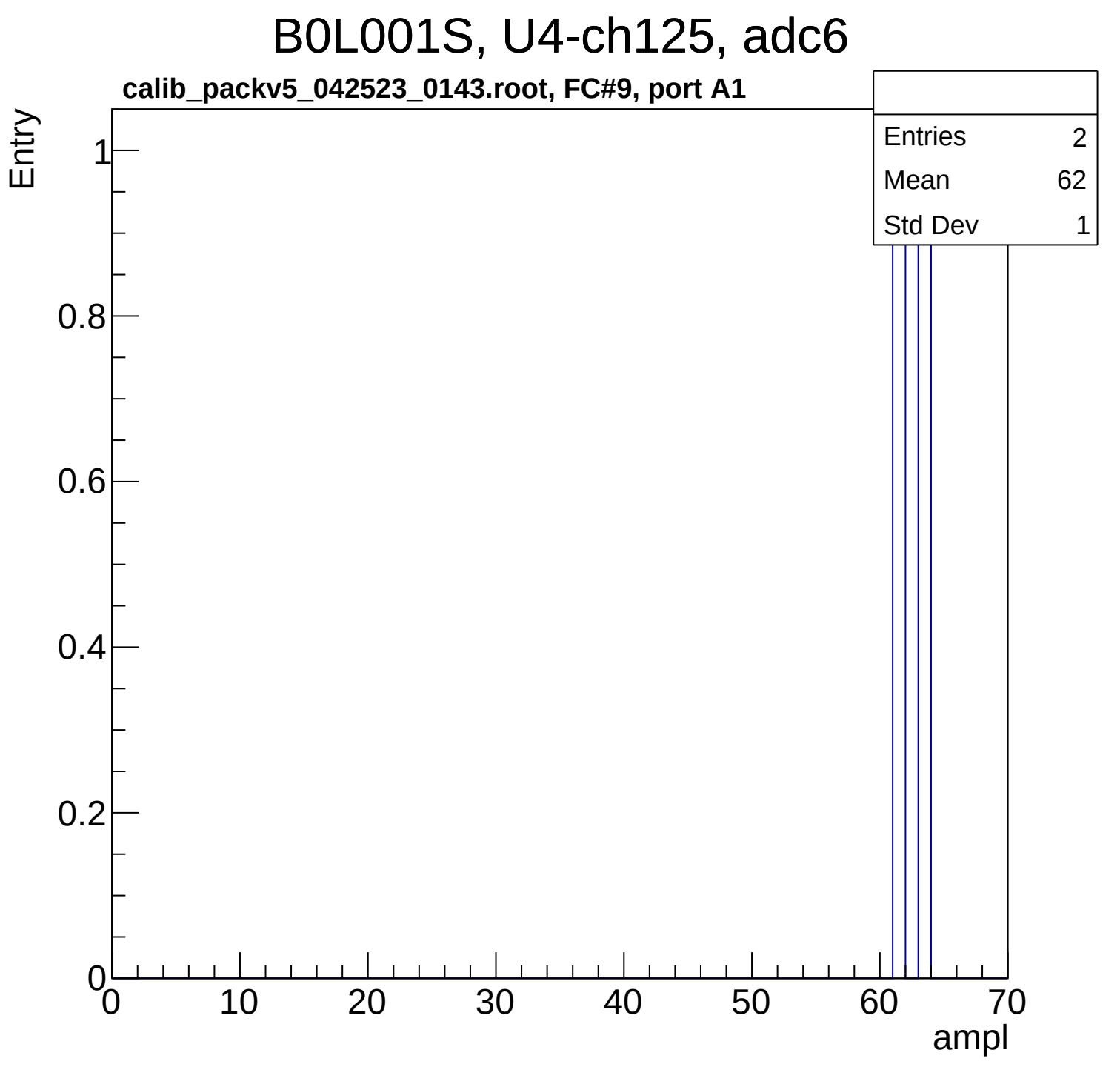
B0L001S, U4-ch125, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
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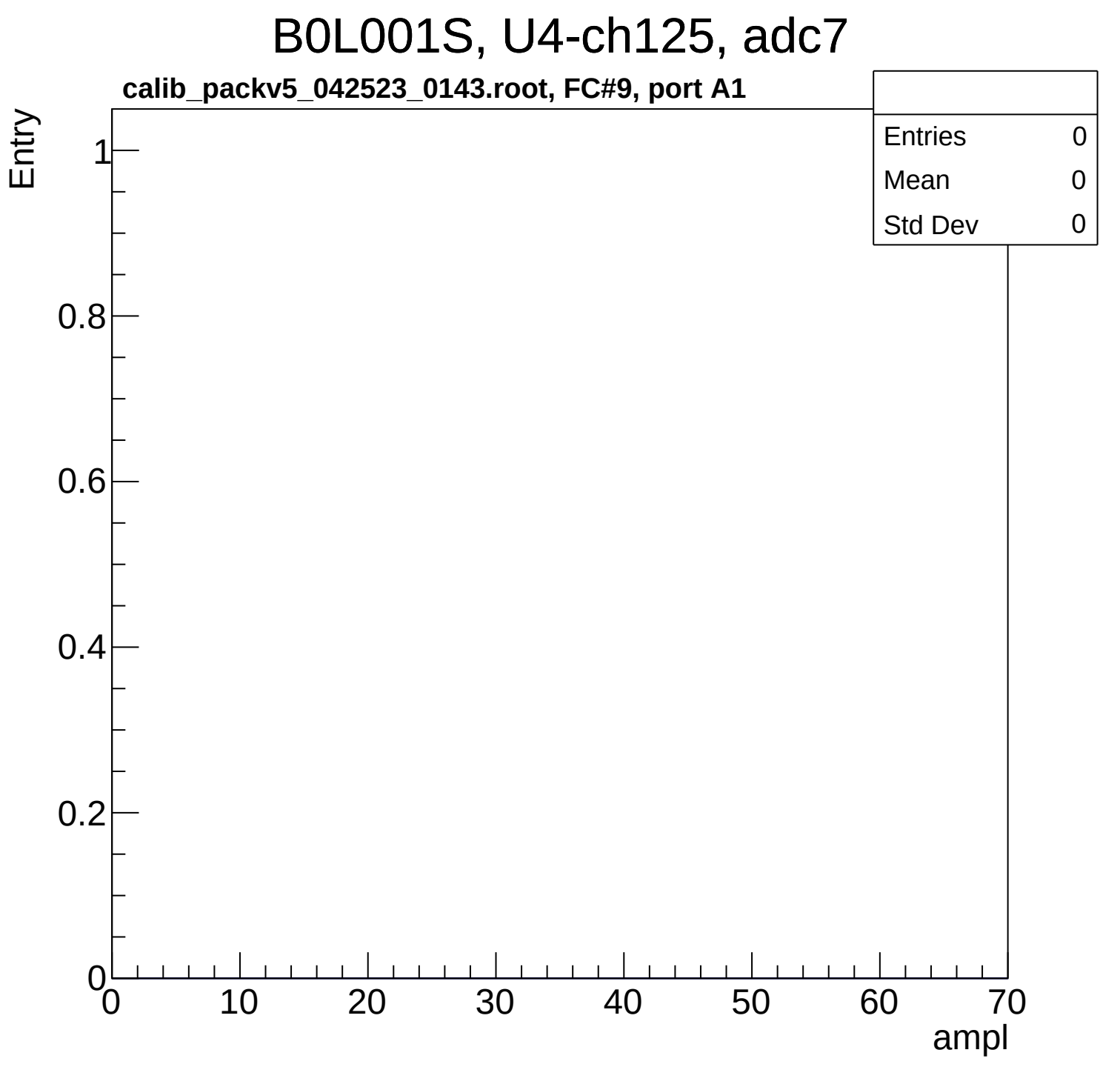
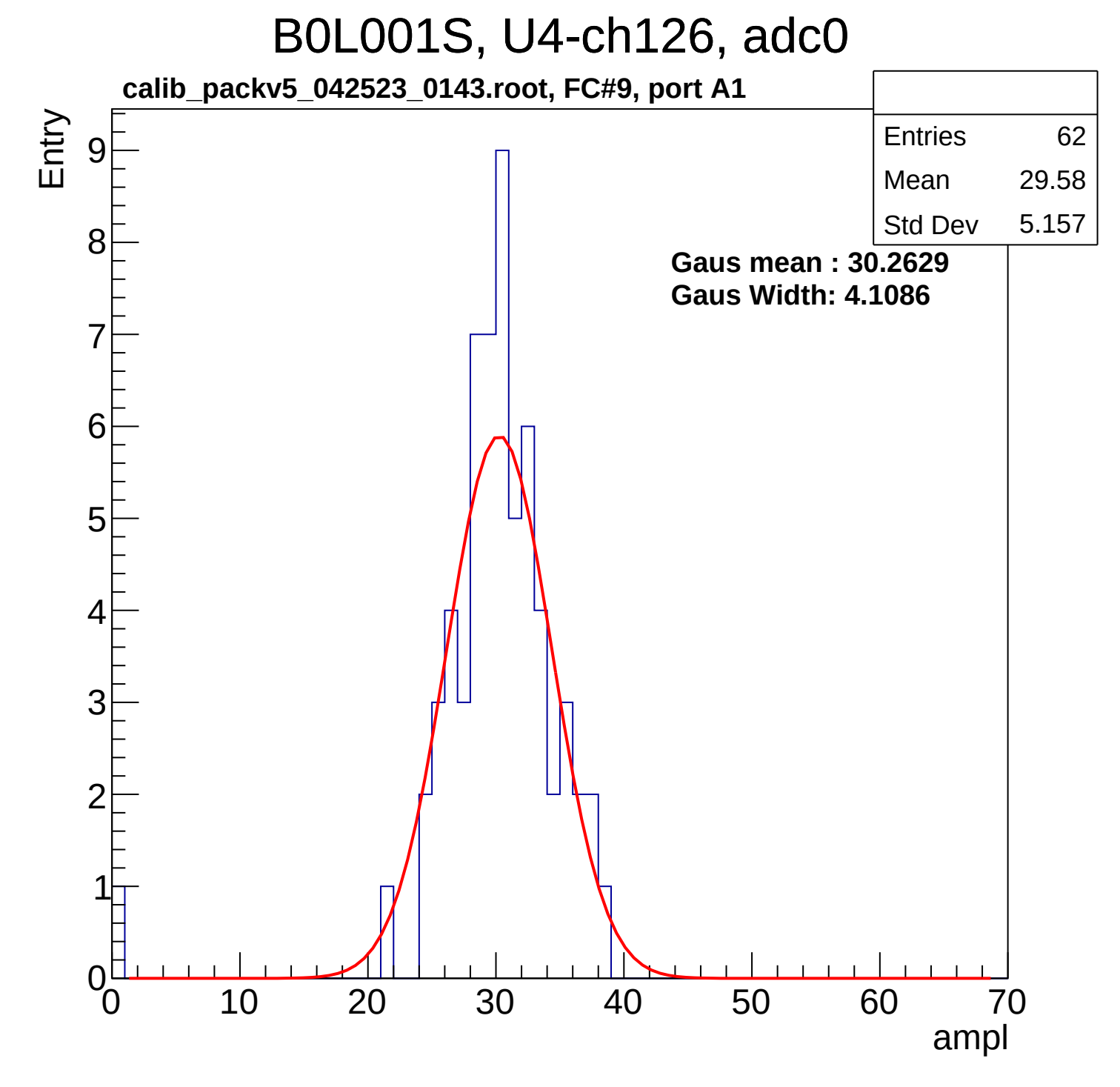
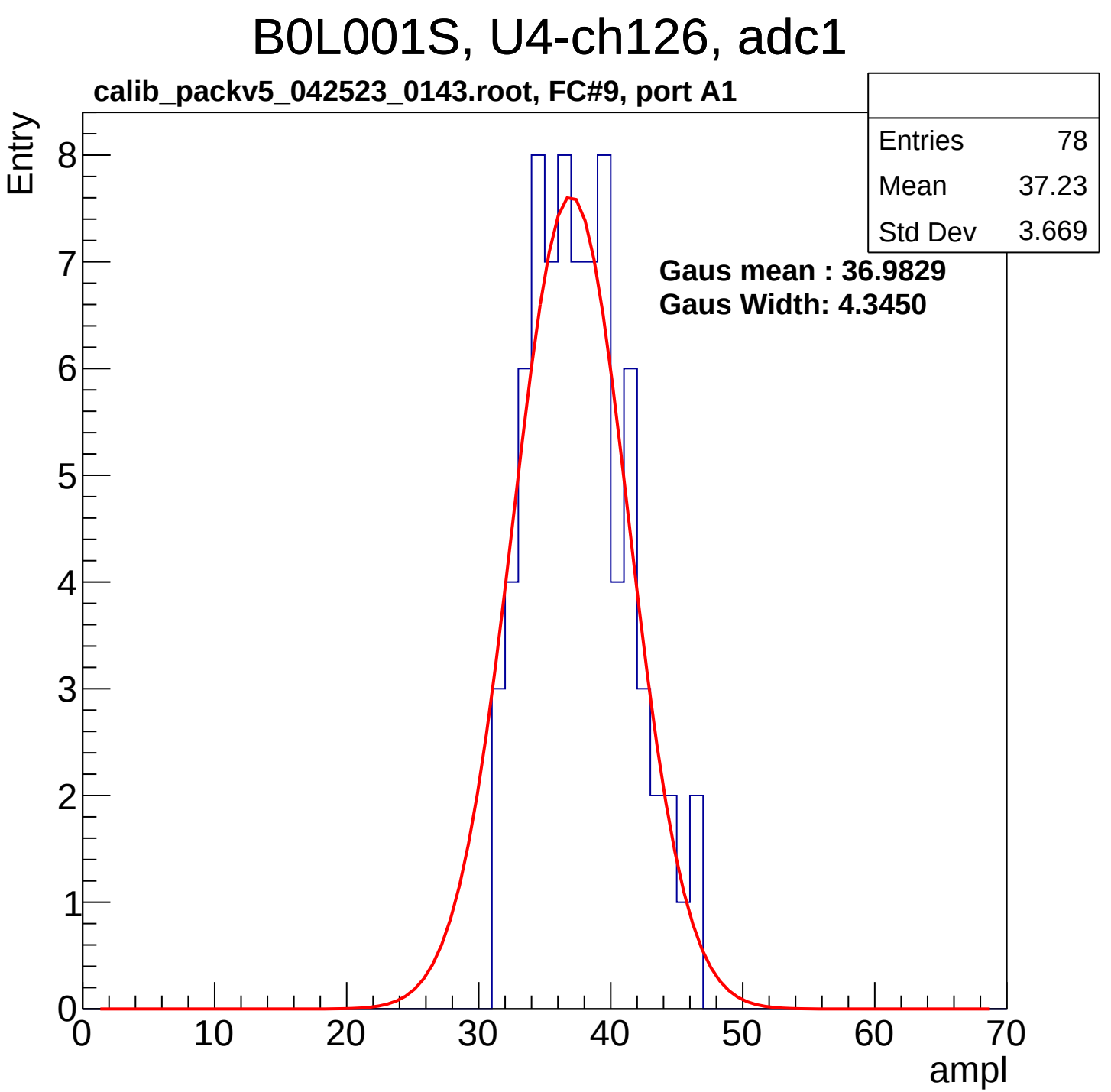
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch125, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch126, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 29.58
Std Dev 5.157
Gaus mean : 30.2629
Gaus Width: 4.1086
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch126, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.23
Std Dev 3.669
Gaus mean : 36.9829
Gaus Width: 4.3450
Entry
ampl
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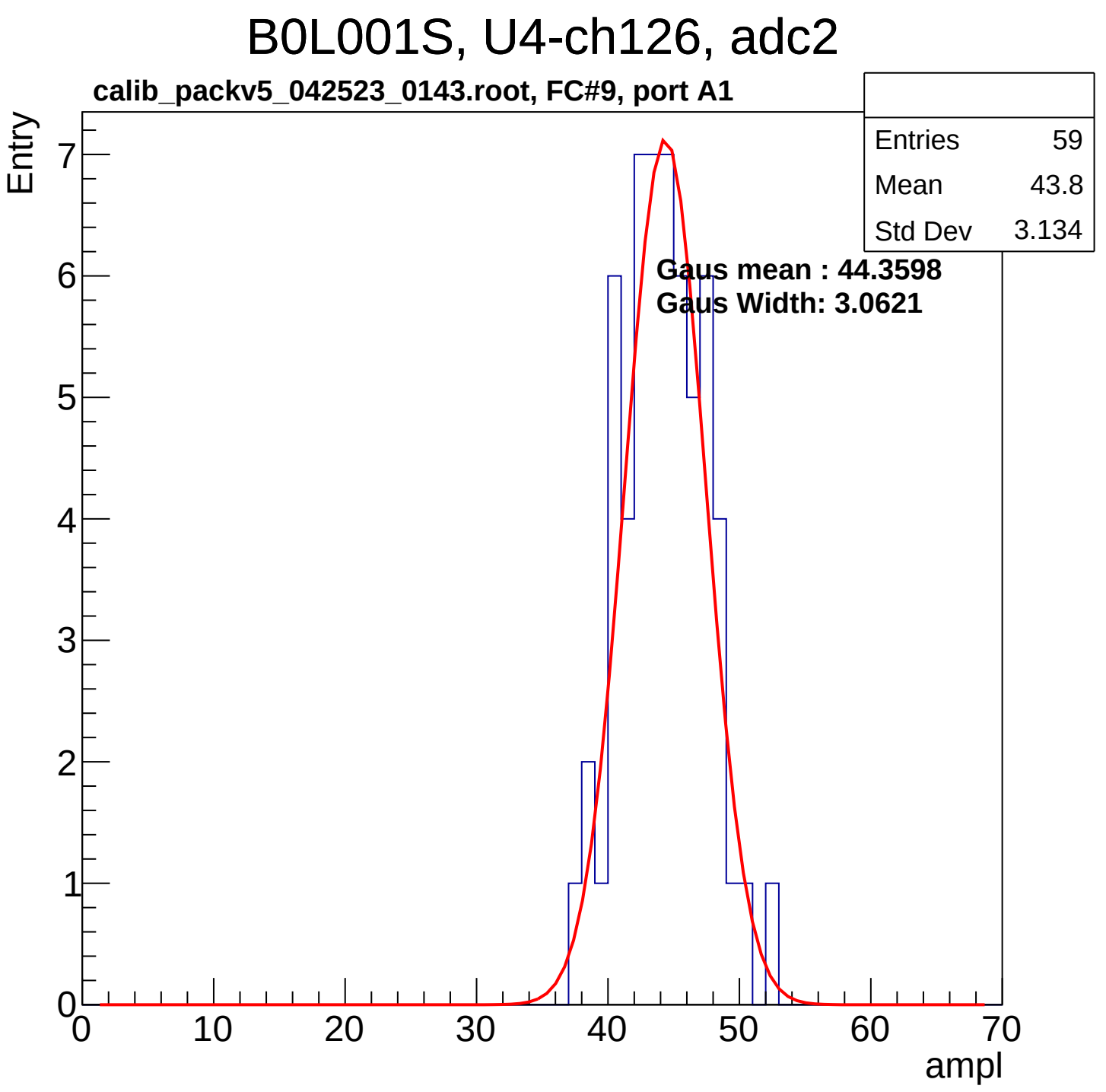
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch126, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.8
Std Dev 3.134
Gaus mean : 44.3598
Gaus Width: 3.0621
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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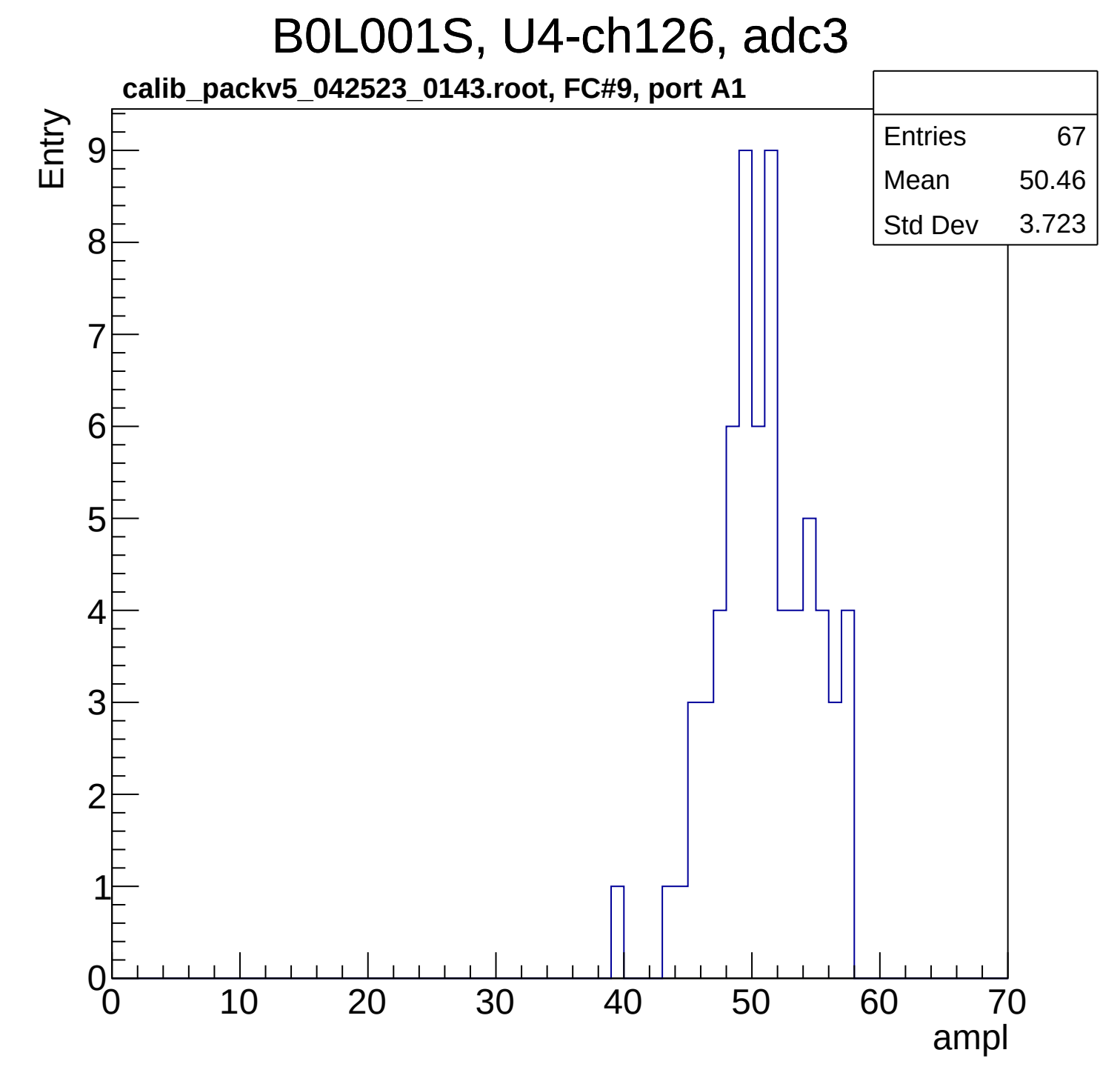
B0L001S, U4-ch126, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.46
Std Dev 3.723
Entry
ampl
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3
4
5
6
7
8
9
0
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20
30
40
50
60
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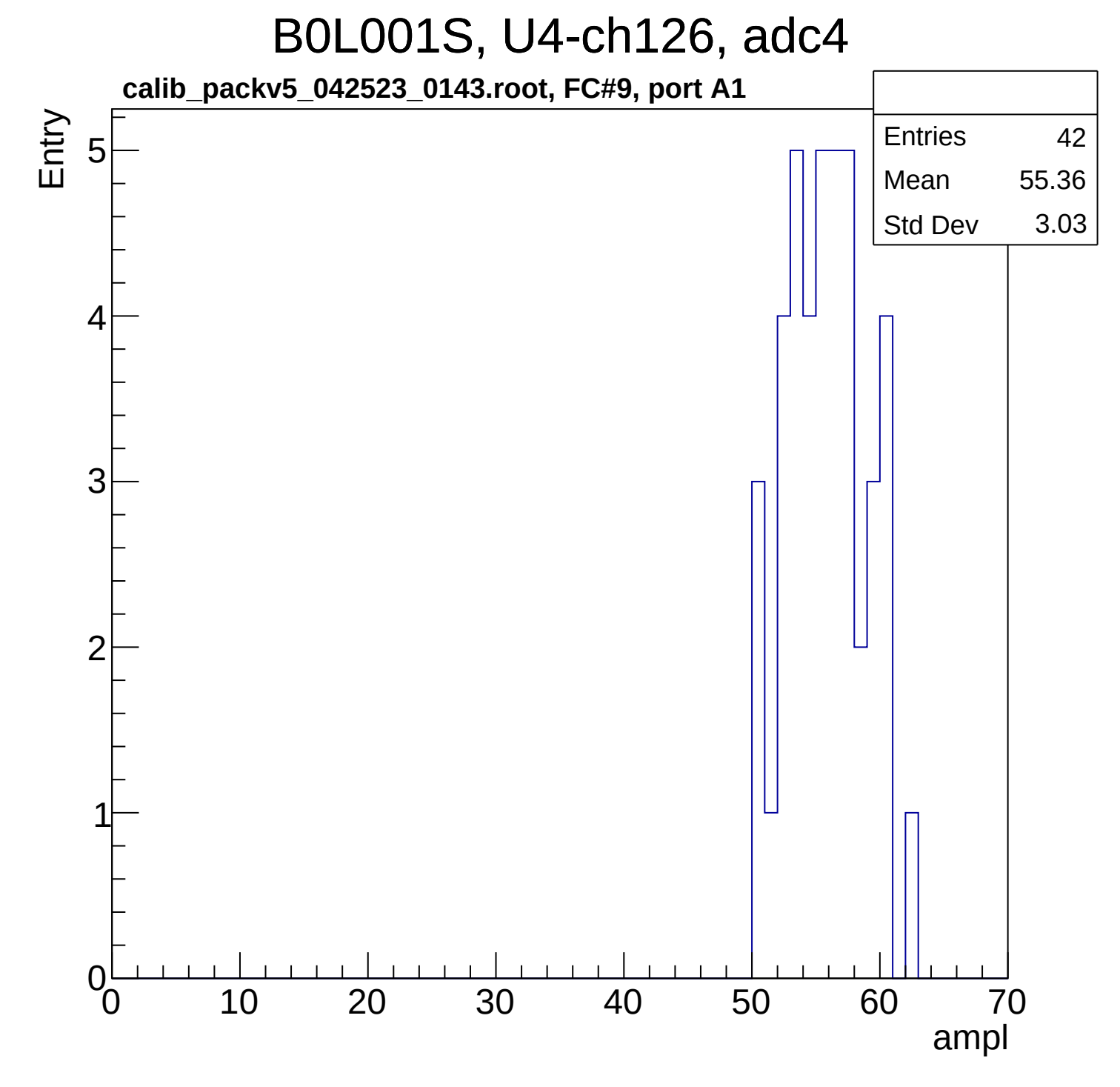
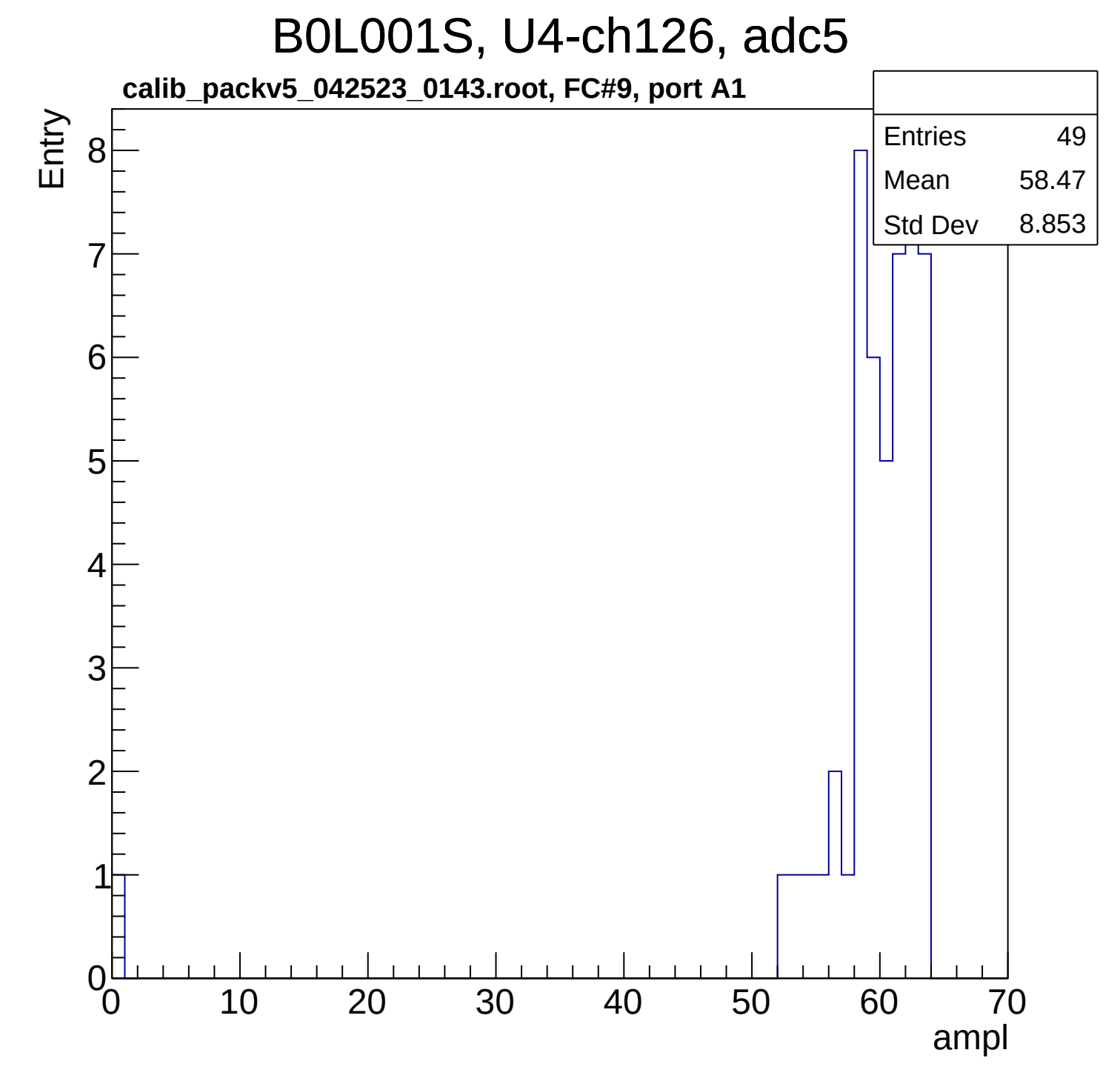

B0L001S, U4-ch126, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 55.36
Std Dev 3.03
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch126, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.47
Std Dev 8.853
Entry
ampl
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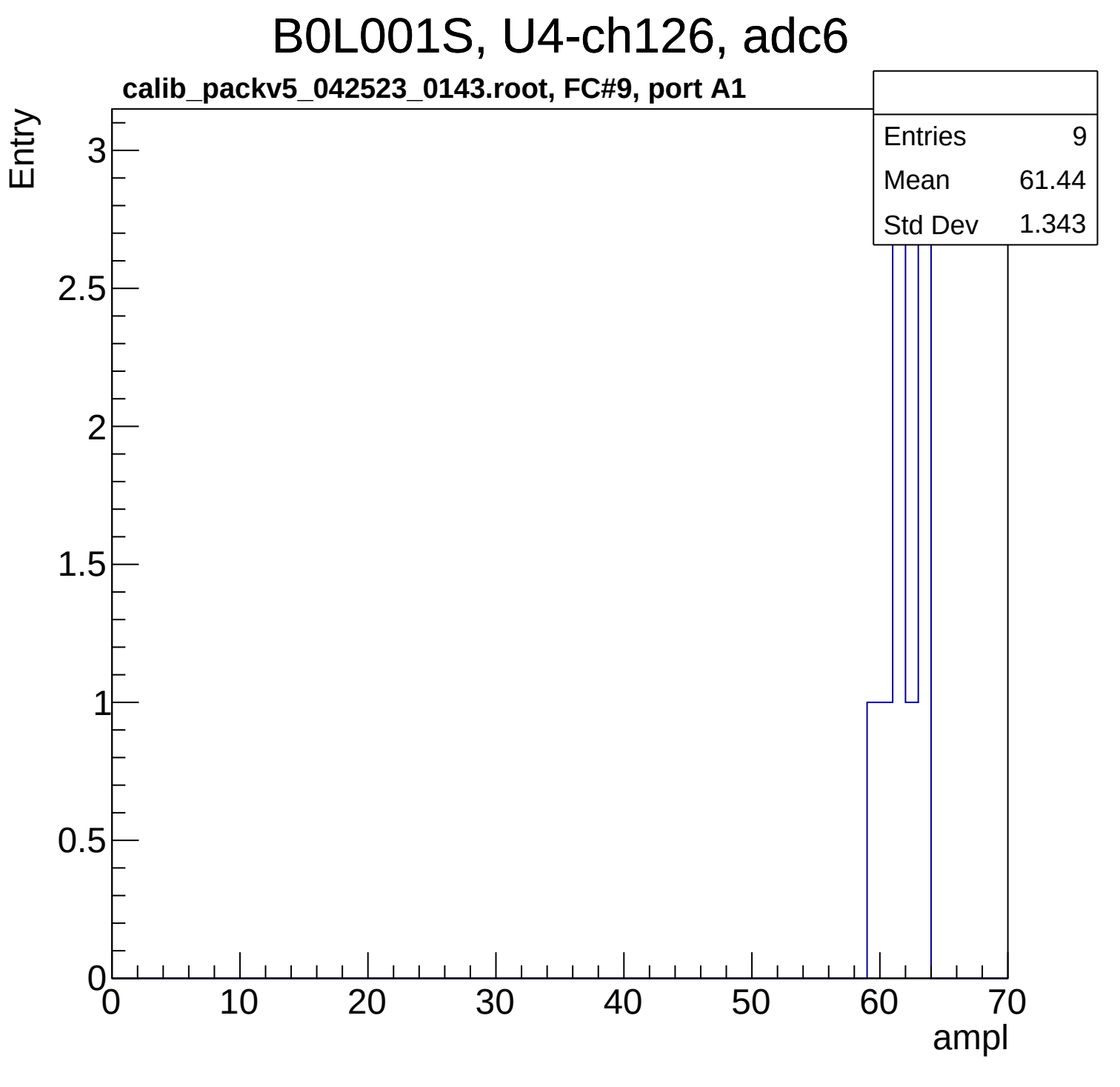
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U4-ch126, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.44
Std Dev 1.343
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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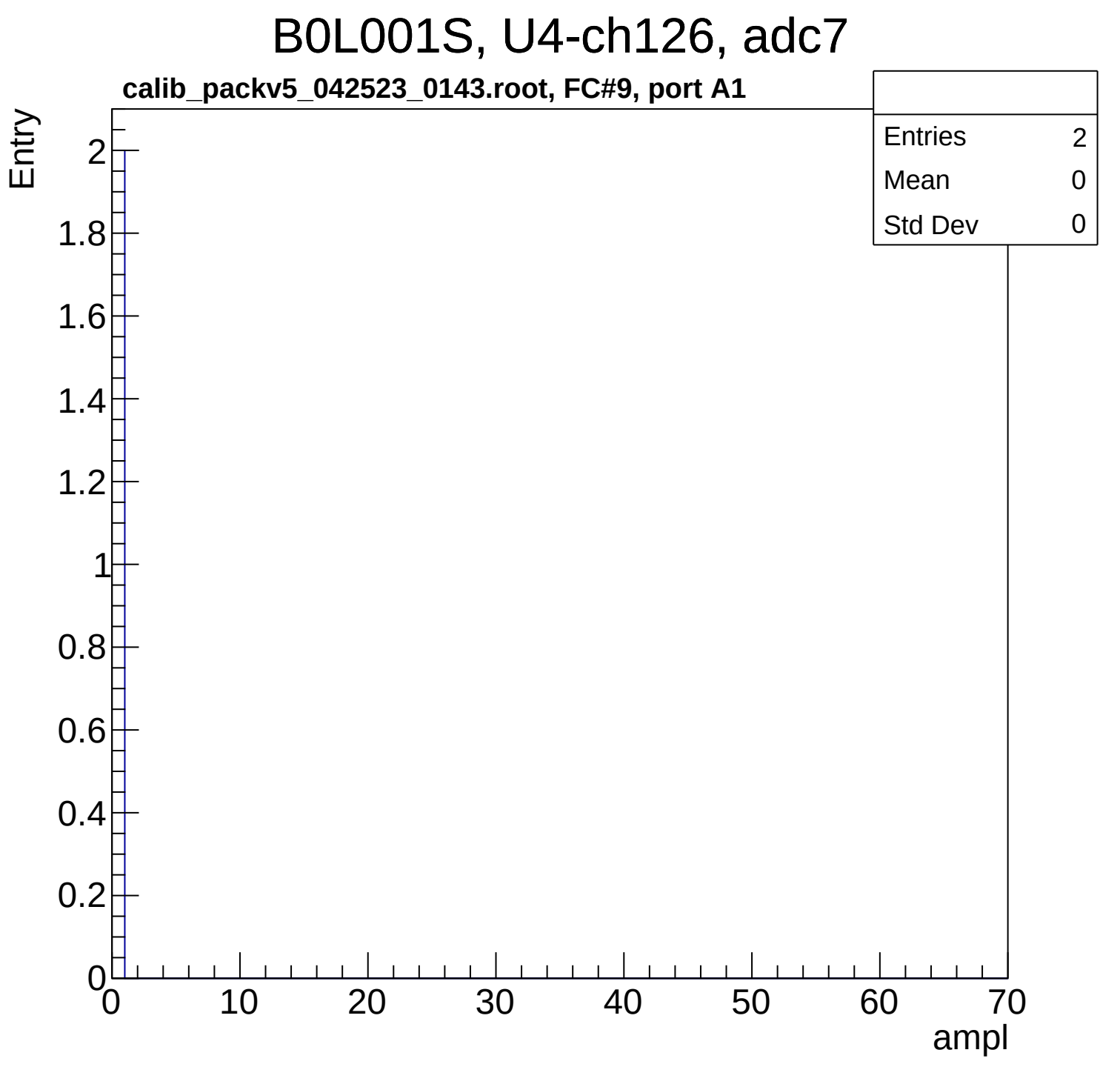
40
50
60
70


B0L001S, U4-ch126, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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60
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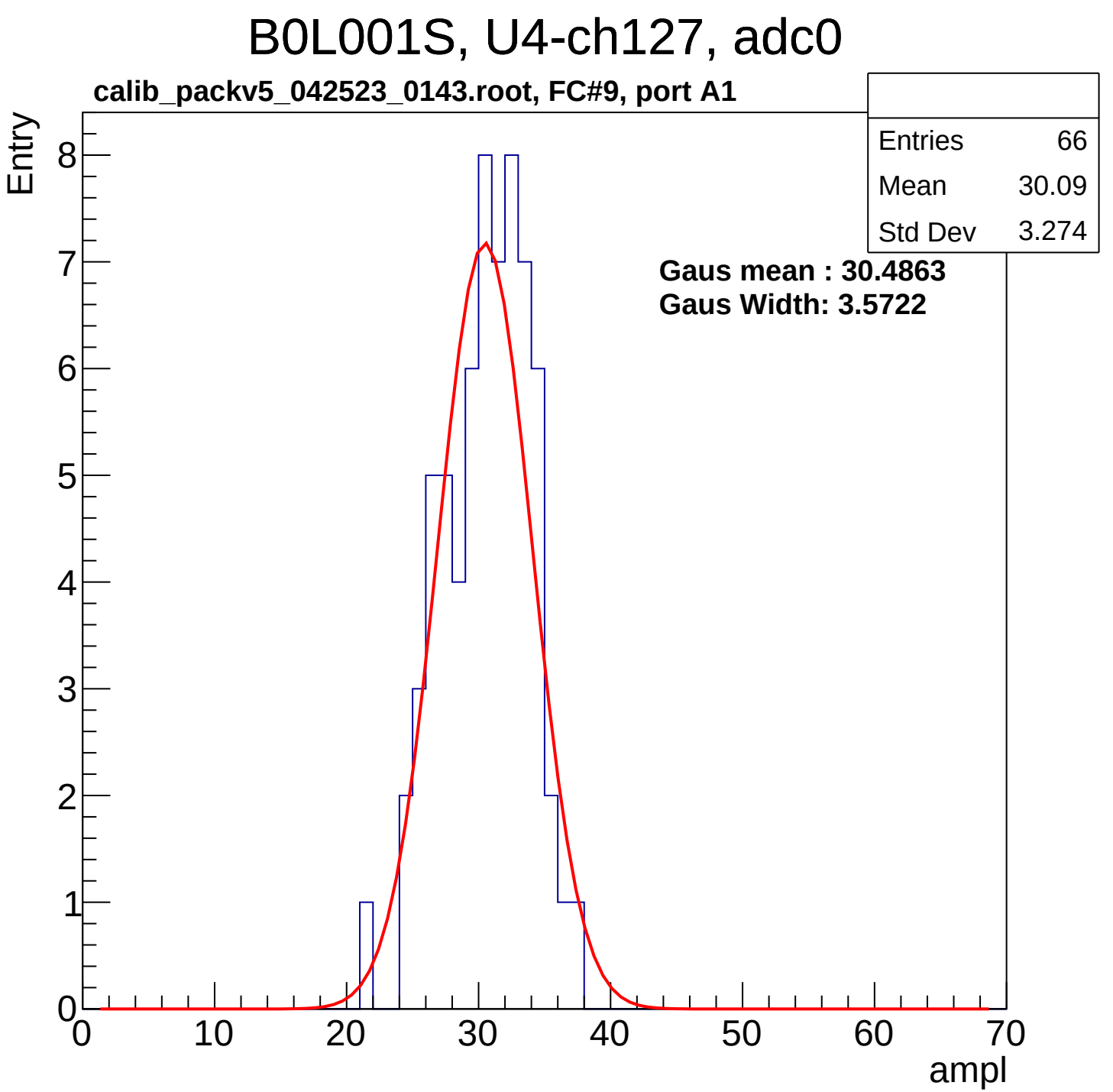
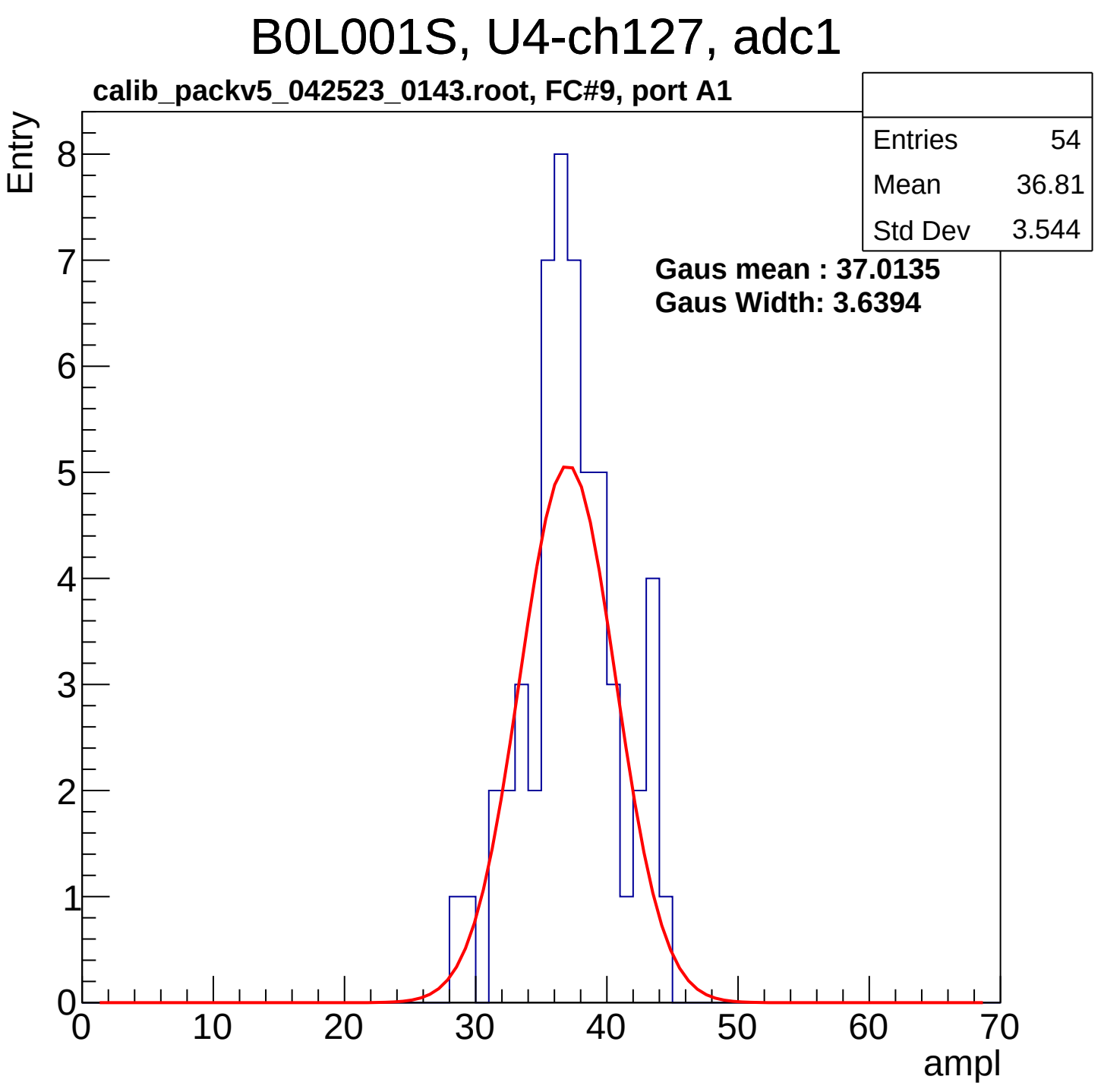
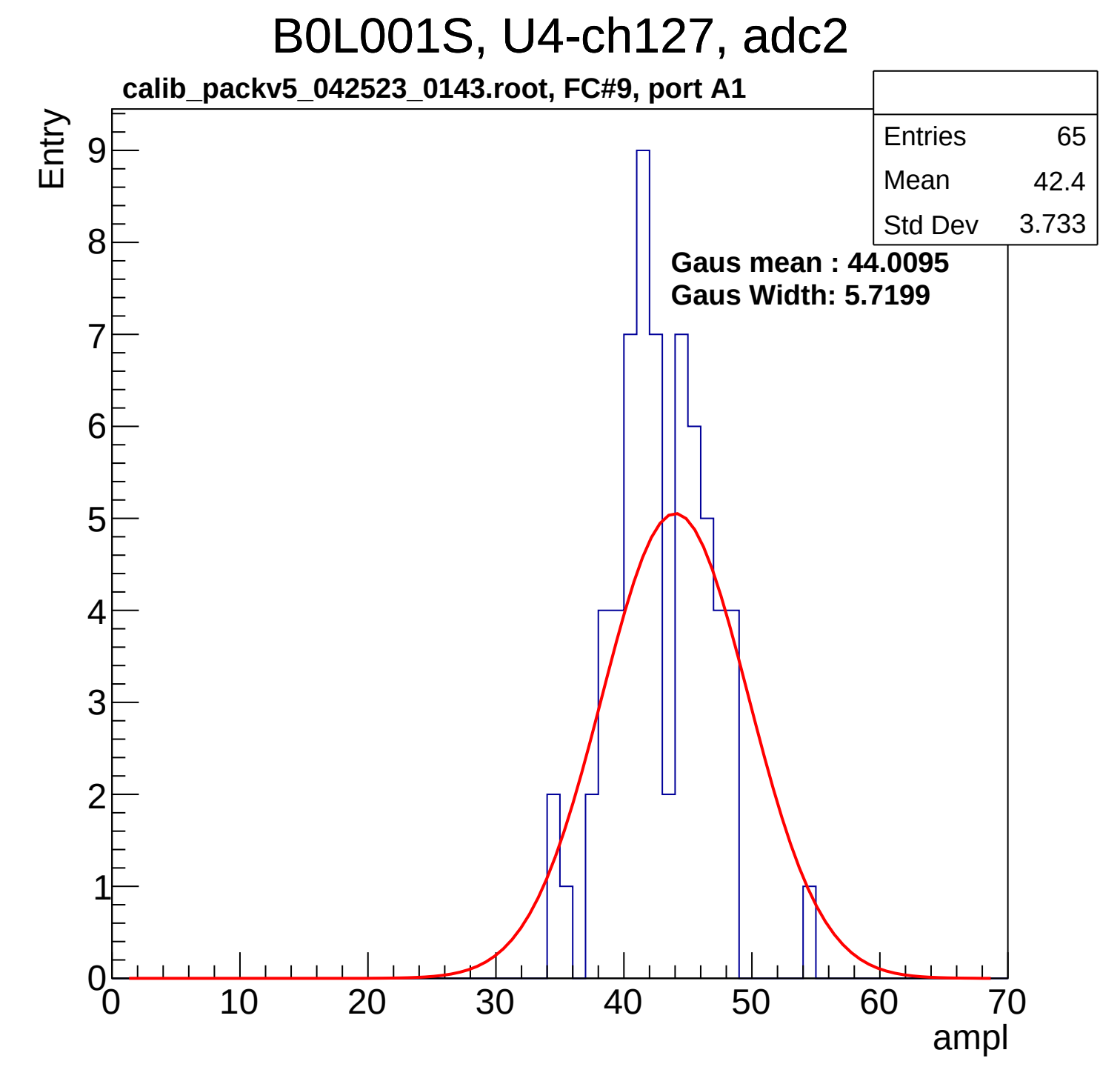
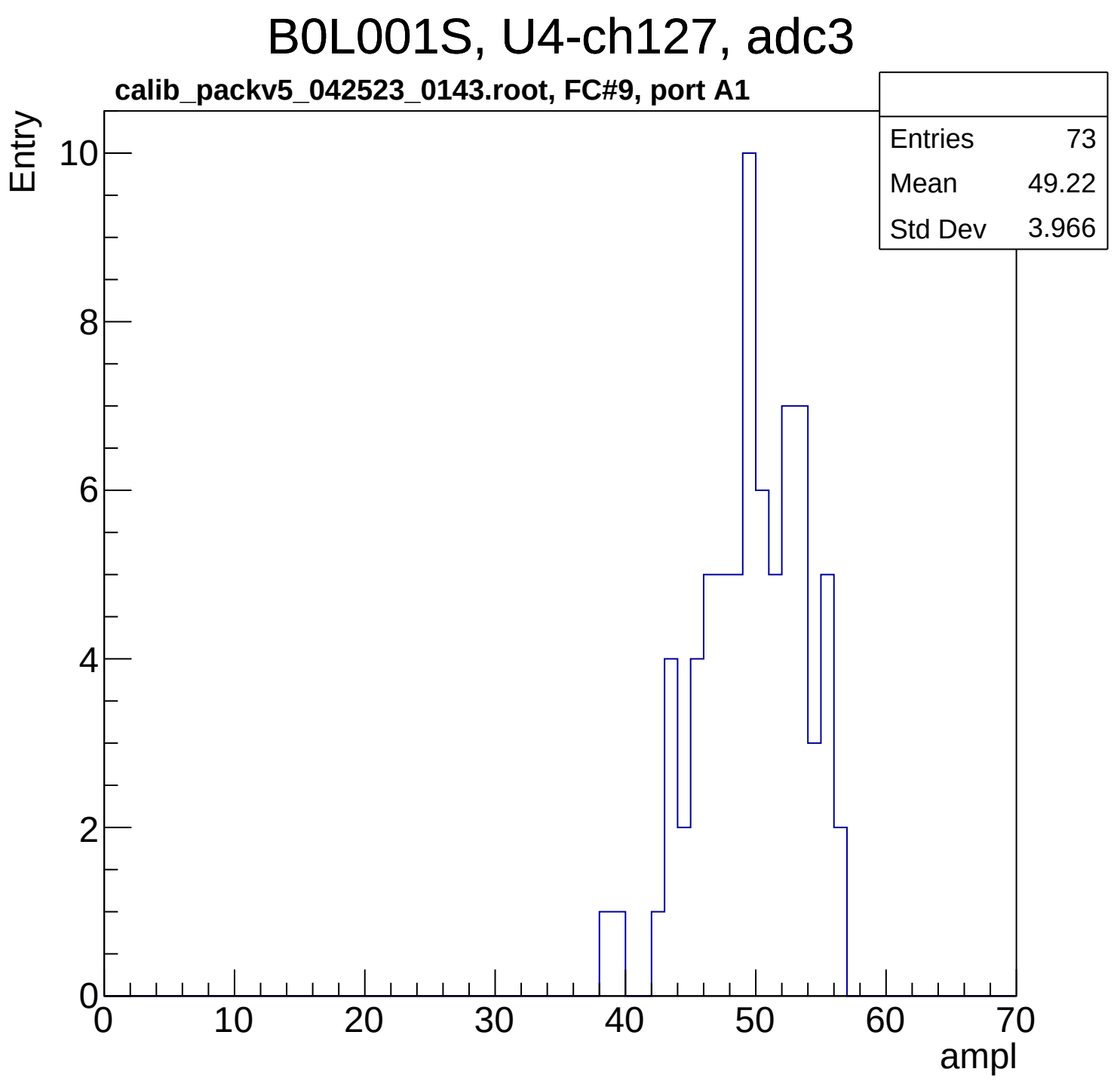
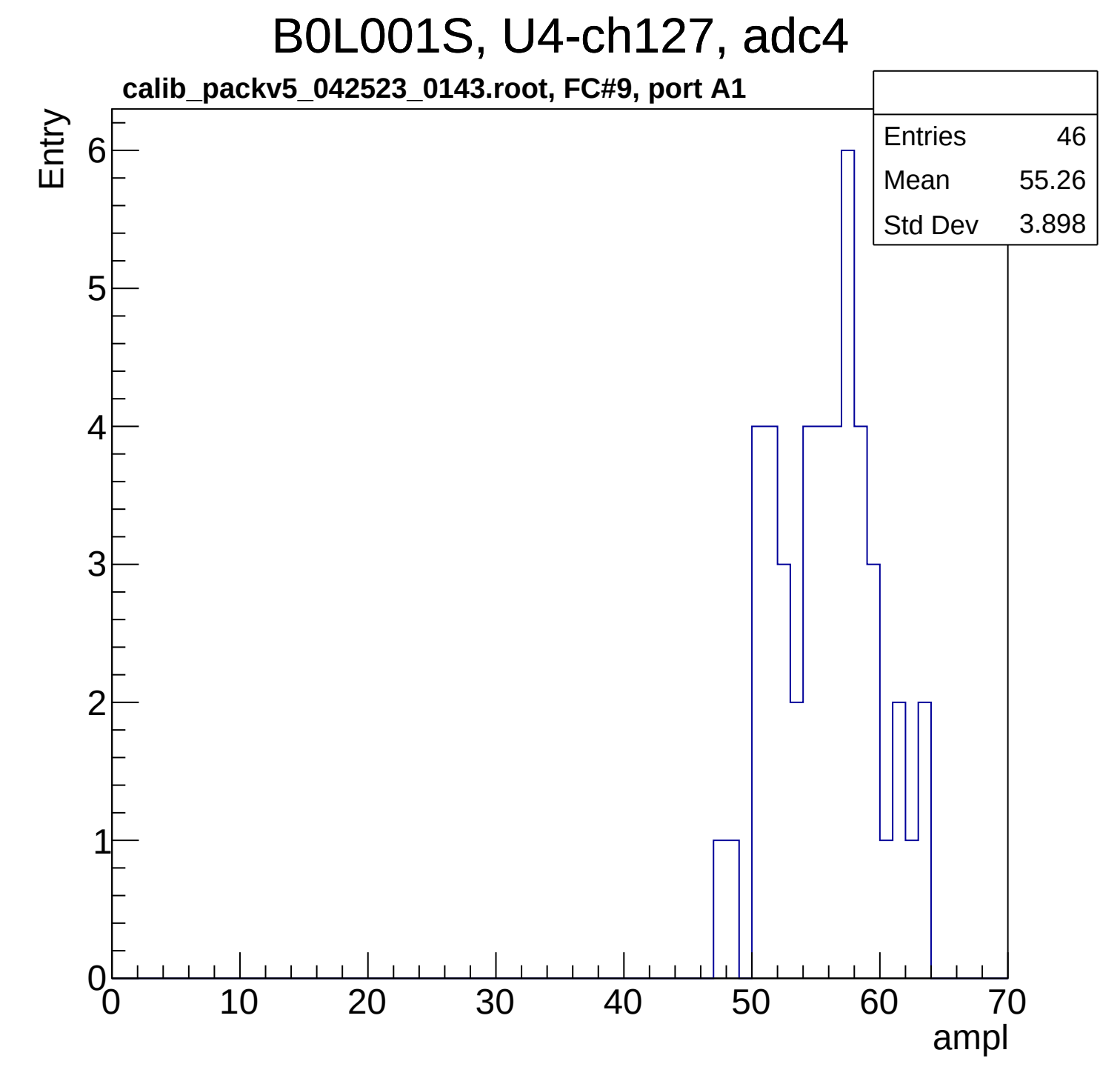
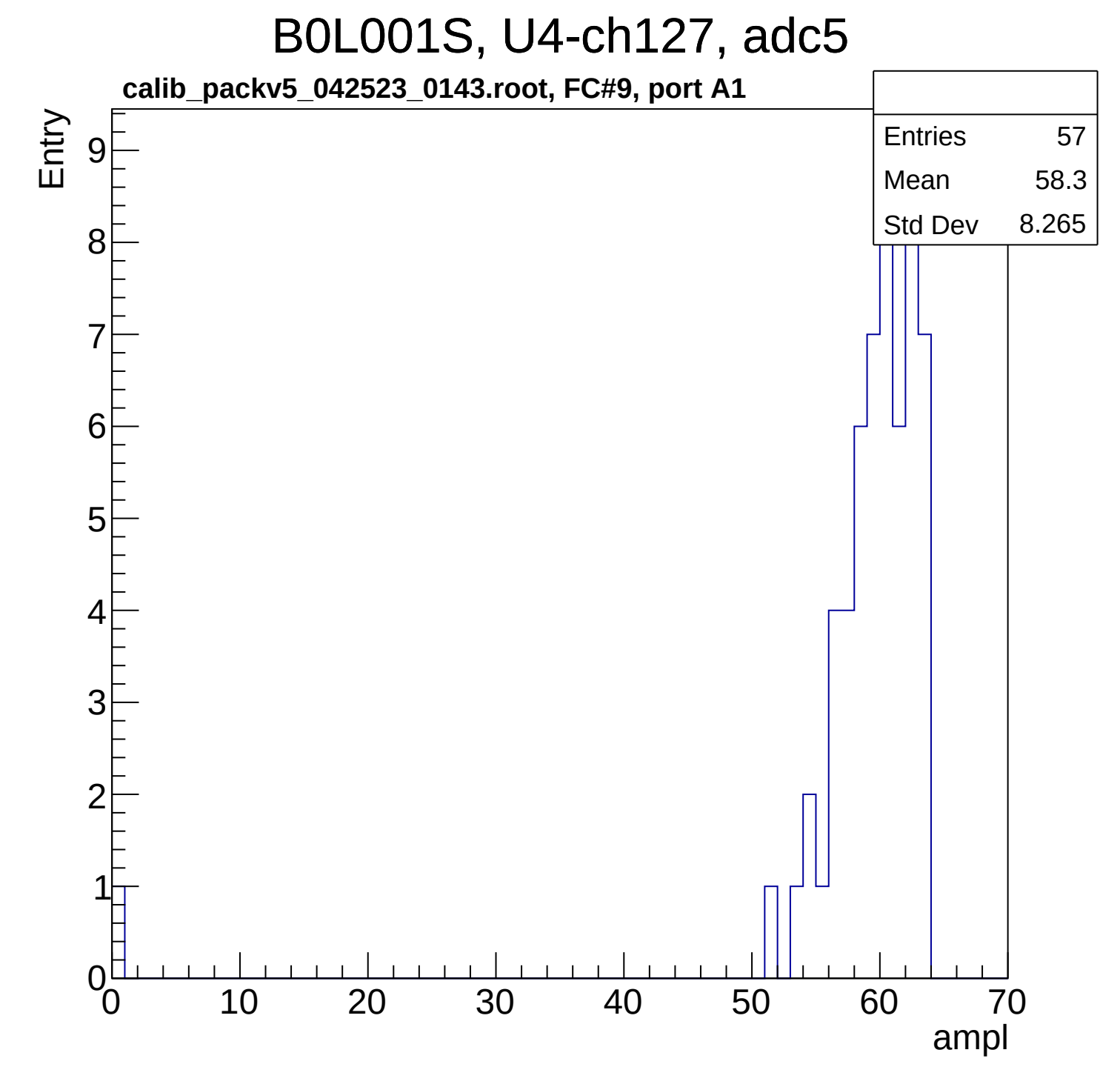

B0L001S, U4-ch127, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.09
Std Dev 3.274
Gaus mean : 30.4863
Gaus Width: 3.5722
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

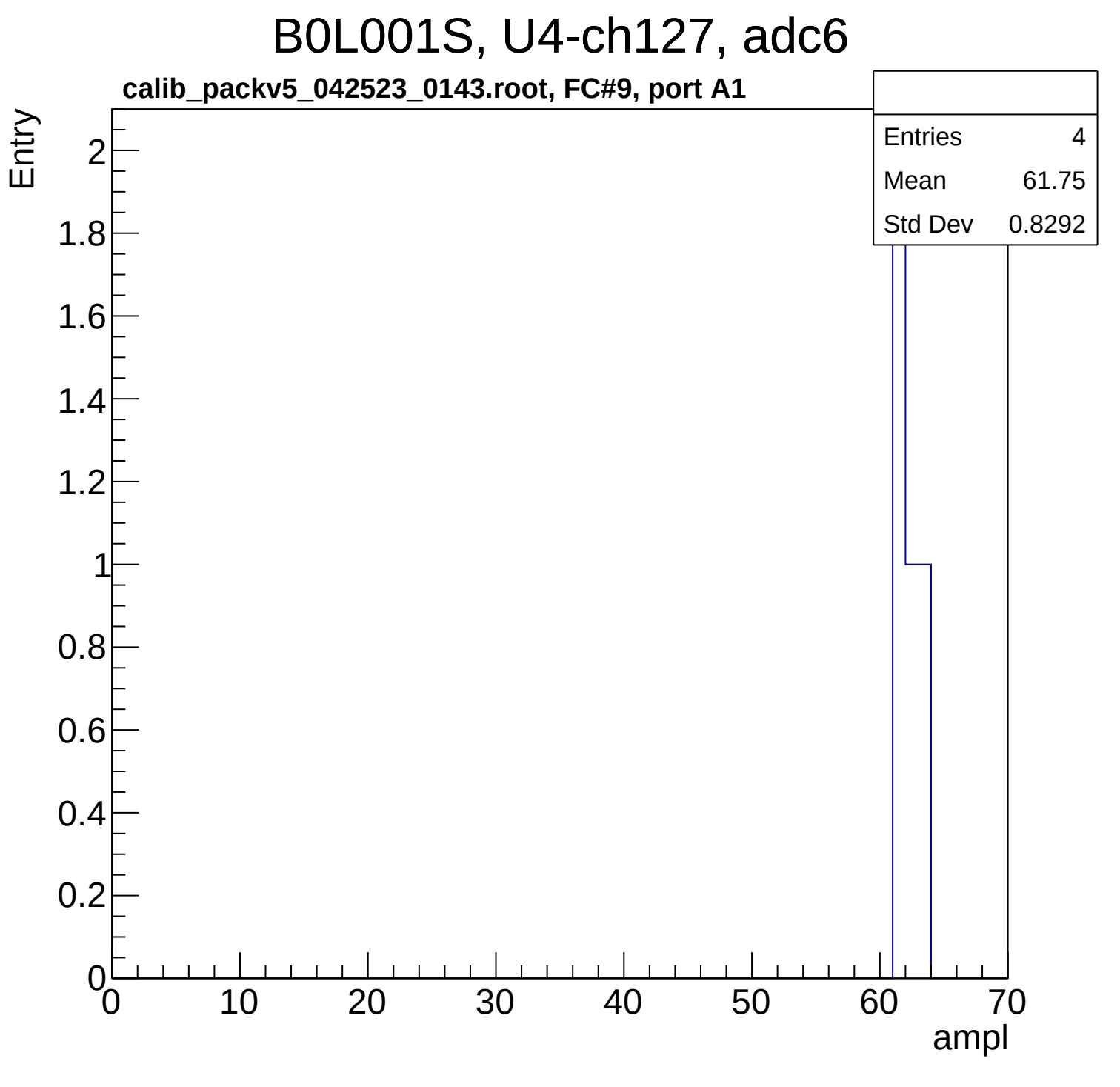

B0L001S, U4-ch127, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 36.81
Std Dev 3.544
Gaus mean : 37.0135
Gaus Width: 3.6394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

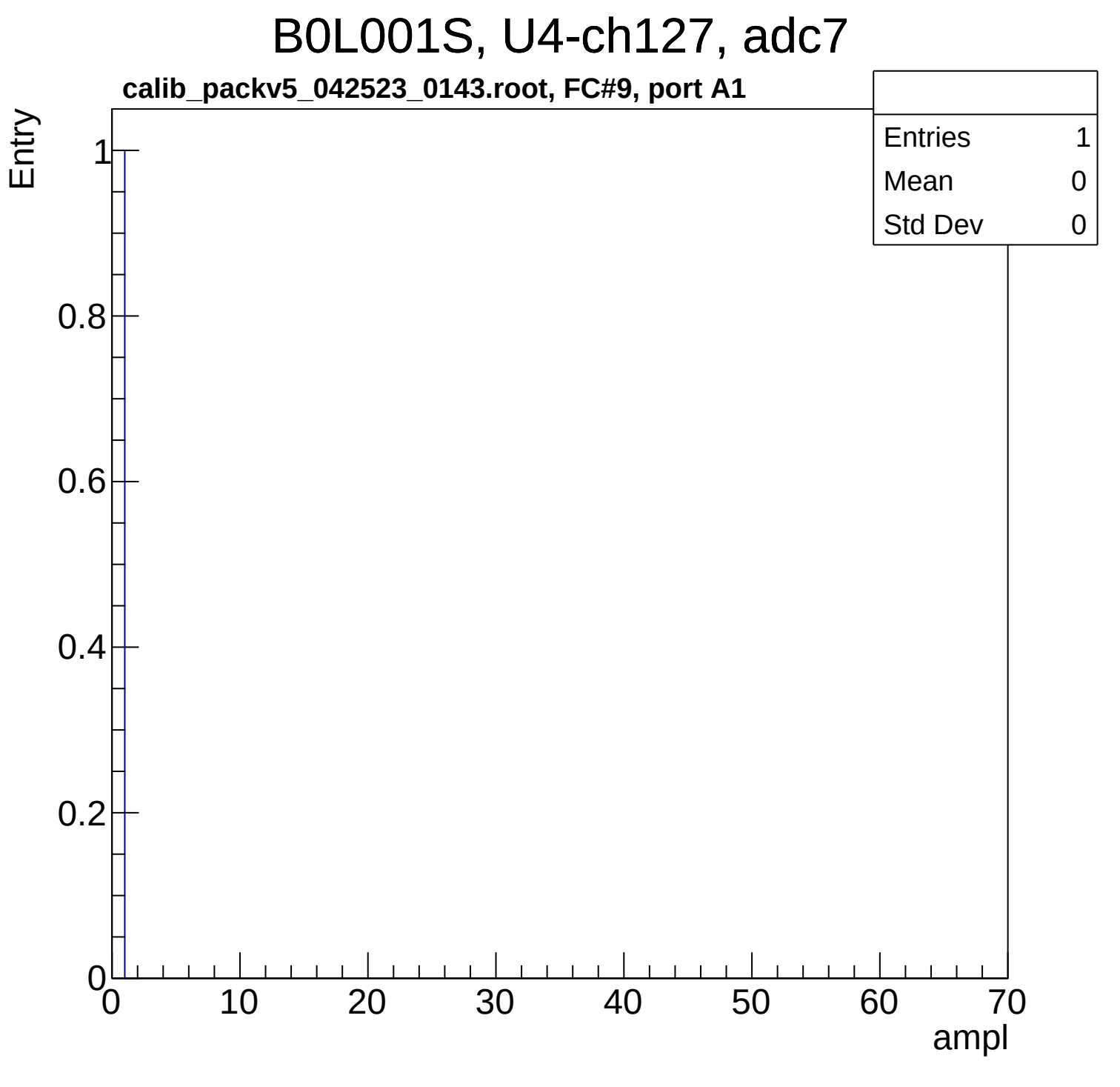

B0L001S, U4-ch127, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 42.4
Std Dev 3.733
Gaus mean : 44.0095
Gaus Width: 5.7199
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

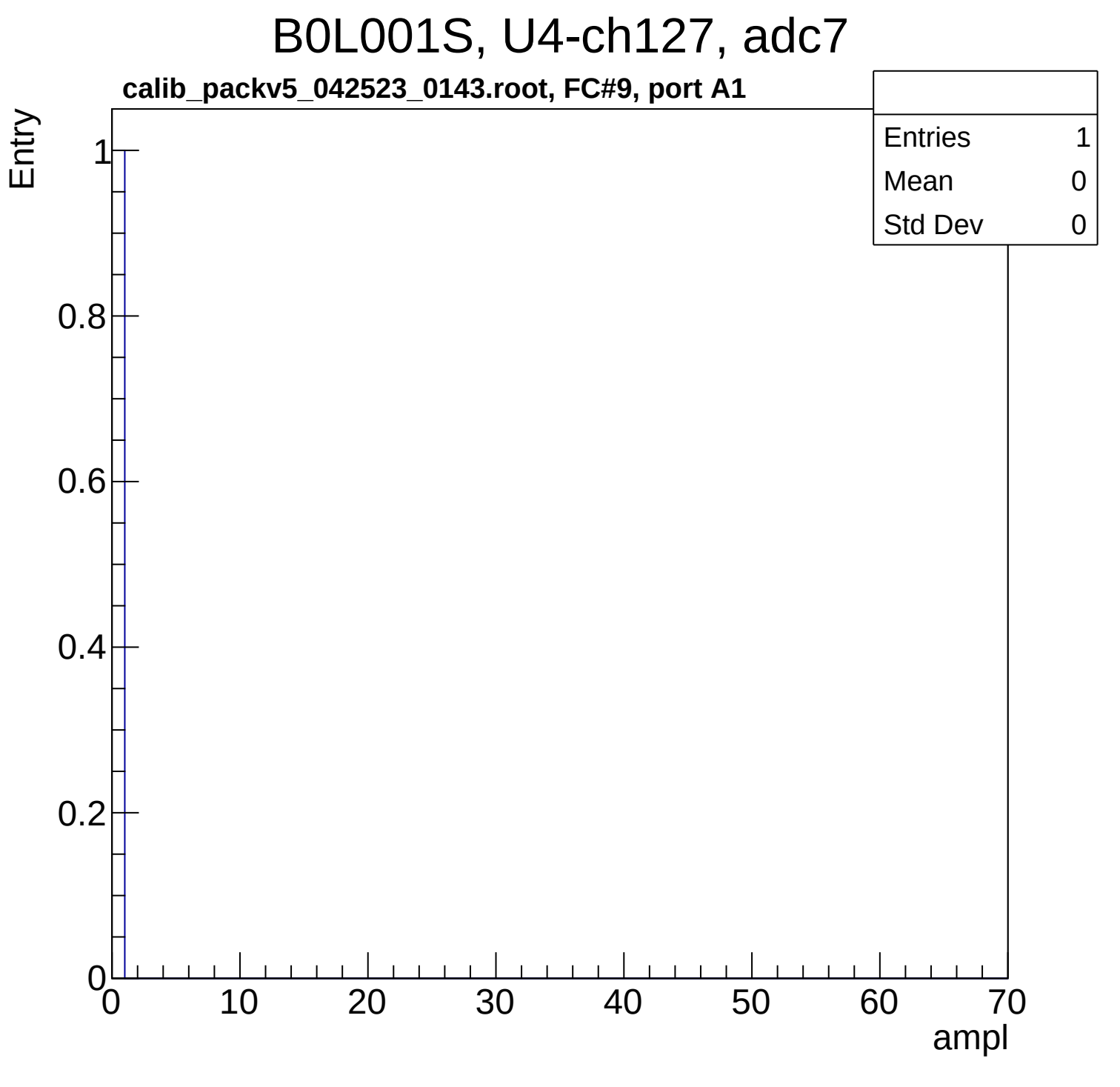

B0L001S, U4-ch127, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 49.22
Std Dev 3.966
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U4-ch127, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 55.26
Std Dev 3.898
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch127, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 58.3
Std Dev 8.265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U4-ch127, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U4-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U4-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl