

B1L101S, U23-ch0

calib_packv5_042523_0143.root, FC#0, port D2

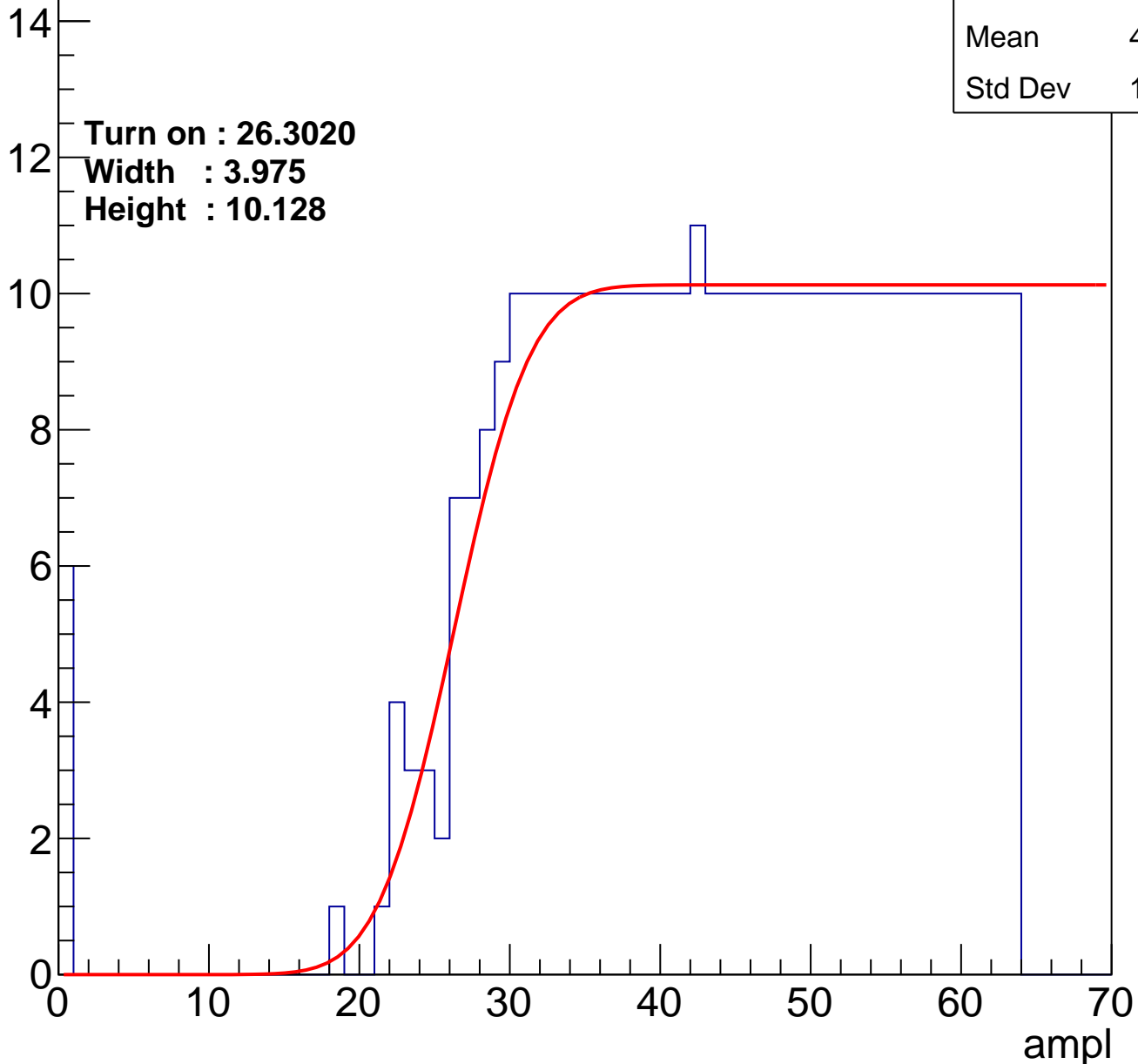
Entries	392
Mean	43.43
Std Dev	12.48

Turn on : 26.3020

Width : 3.975

Height : 10.128

Entry



B1L101S, U23-ch1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 26.7499

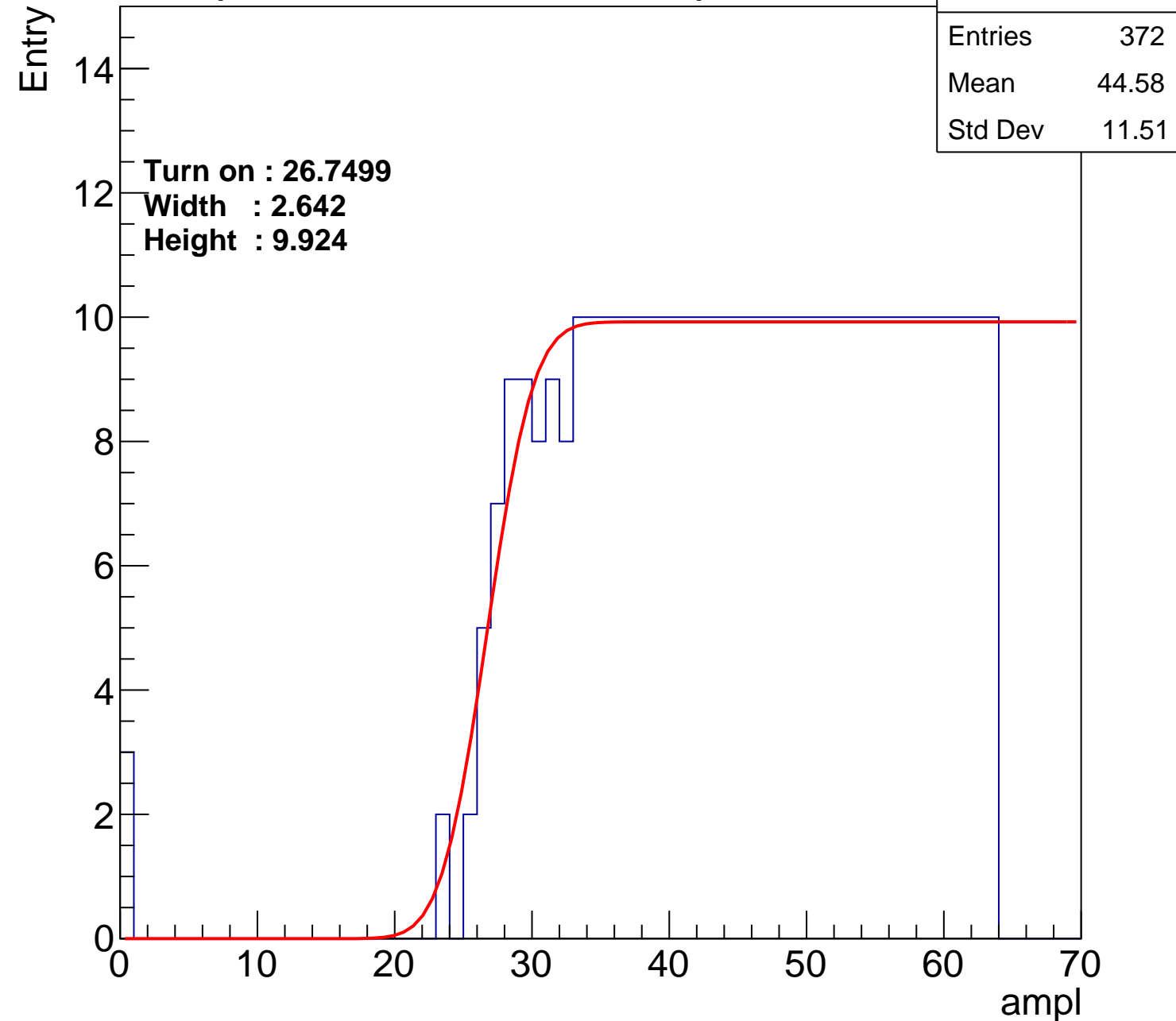
Width : 2.642

Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	384
Mean	44.11
Std Dev	11.48

Turn on : 26.4688

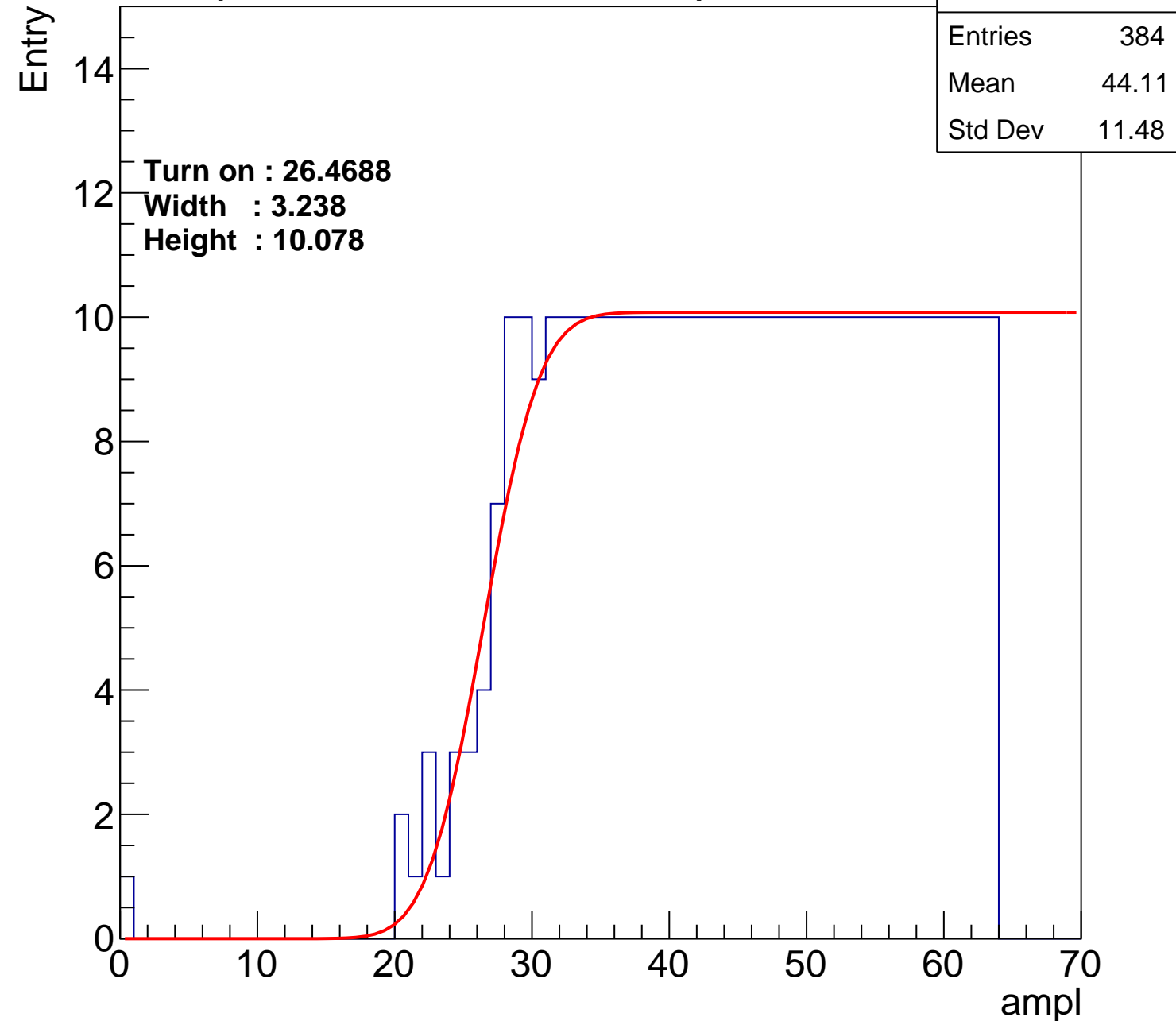
Width : 3.238

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.94
Std Dev	11.18

Turn on : 27.8380

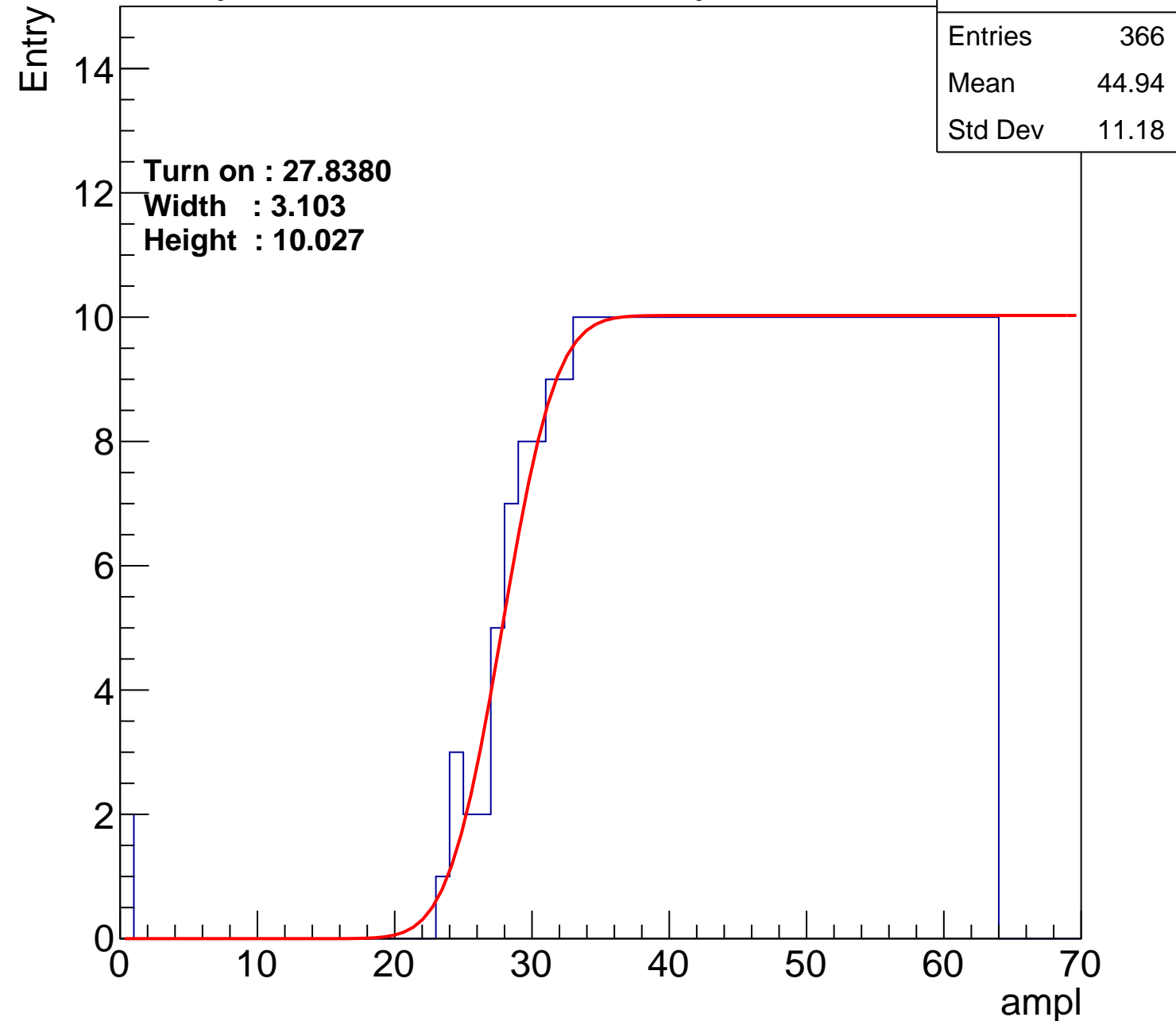
Width : 3.103

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch4

calib_packv5_042523_0143.root, FC#0, port D2

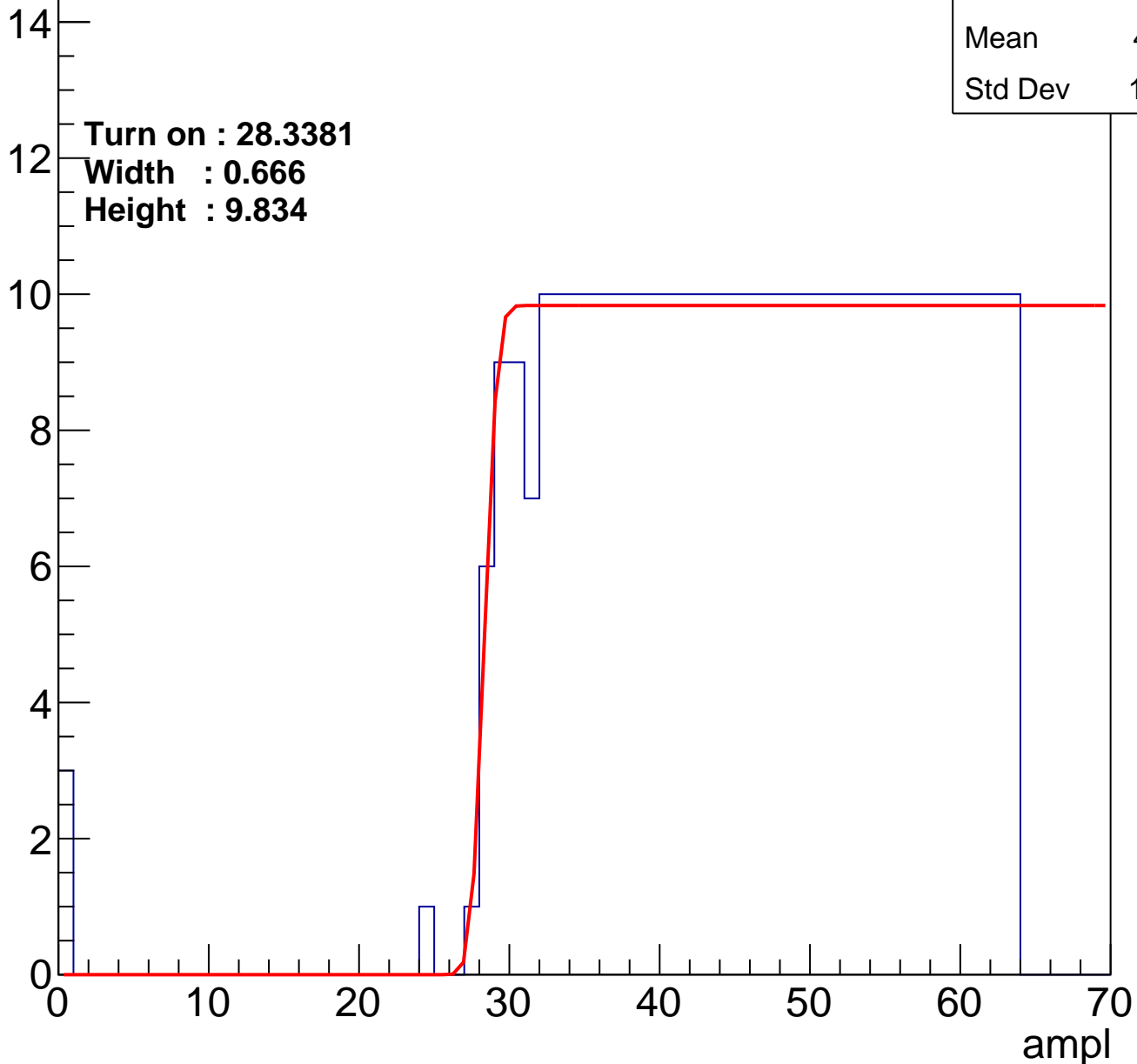
Entries	356
Mean	45.41
Std Dev	11.05

Turn on : 28.3381

Width : 0.666

Height : 9.834

Entry



B1L101S, U23-ch5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	357
Mean	45.21
Std Dev	11.4

Turn on : 28.4248

Width : 2.656

Height : 9.893

Entry

14

12

10

8

6

4

2

0

0

10

20

30

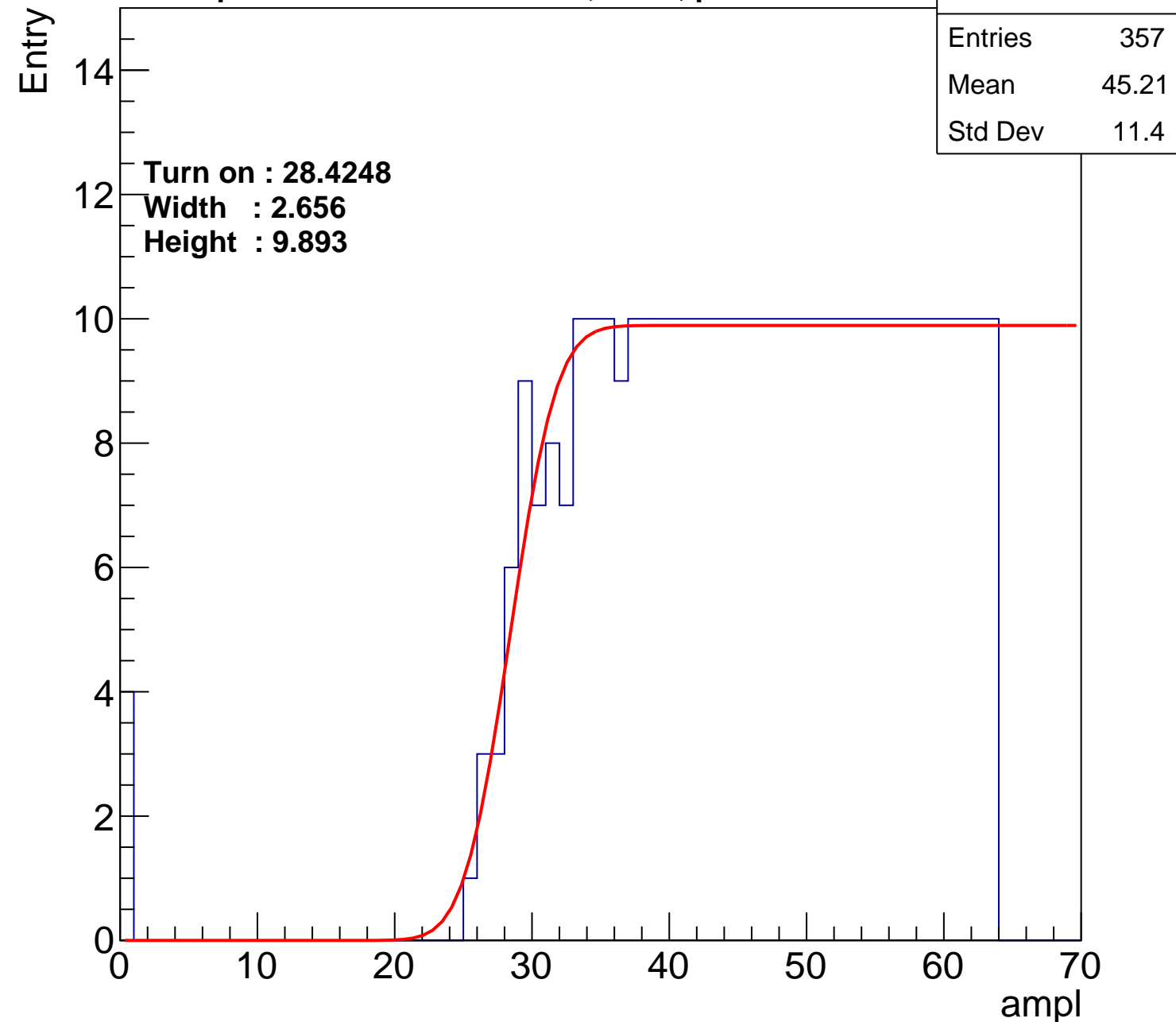
40

50

60

70

ampl



B1L101S, U23-ch6

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.66
Std Dev	11.49

Turn on : 27.8711

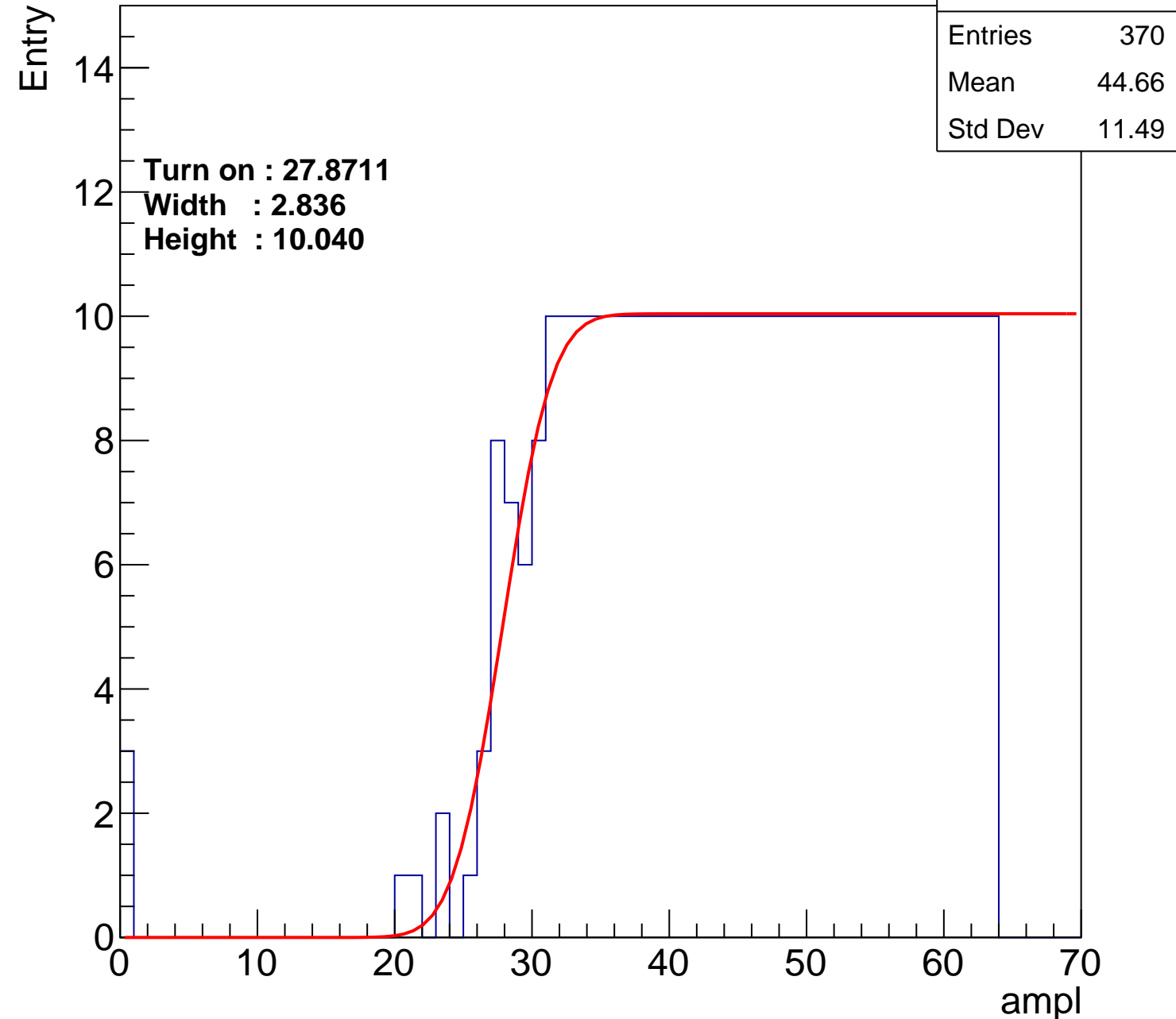
Width : 2.836

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch7

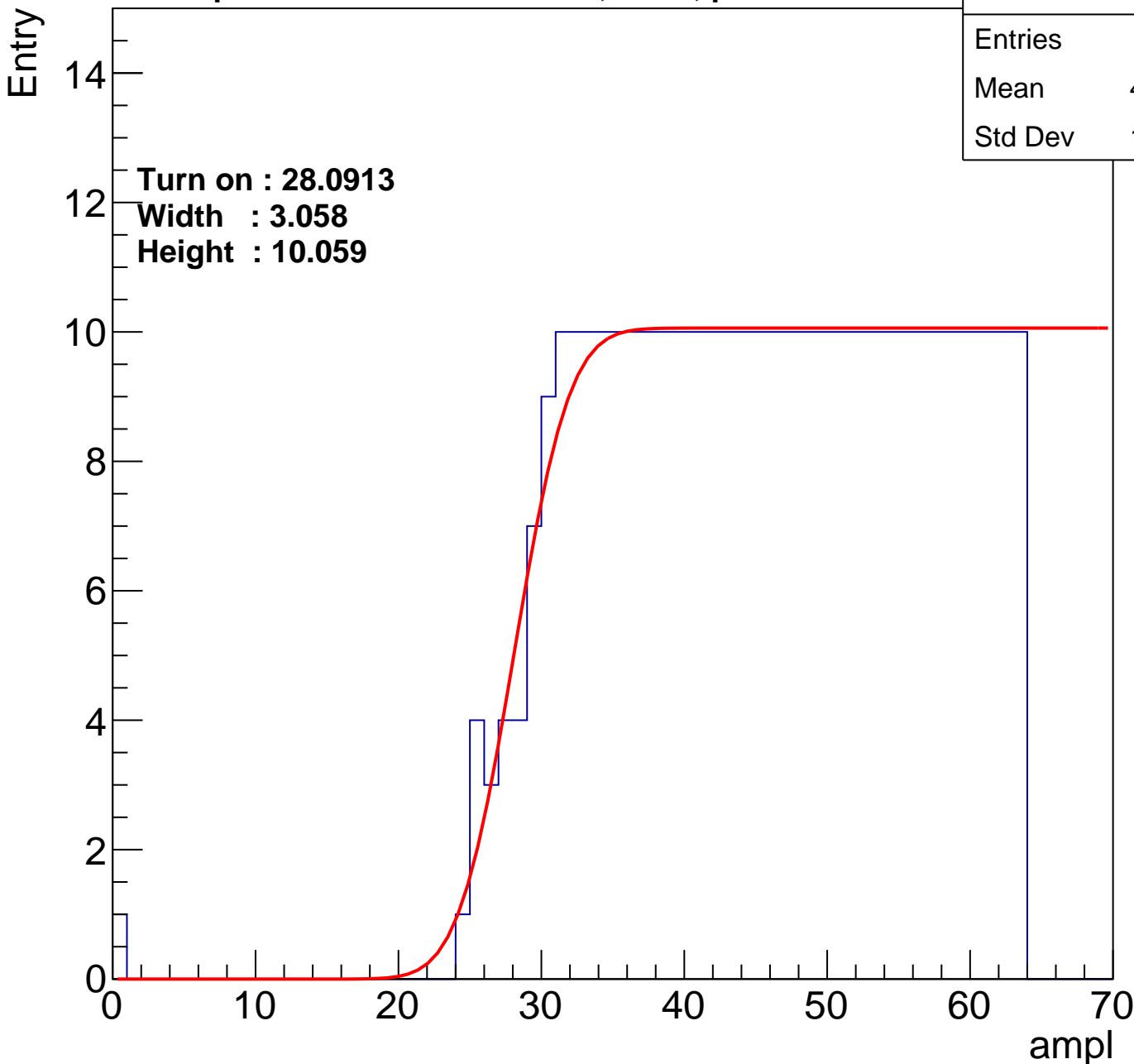
calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.19
Std Dev	10.84

Turn on : 28.0913

Width : 3.058

Height : 10.059



B1L101S, U23-ch8

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.67
Std Dev	11.09

Turn on : 26.7160

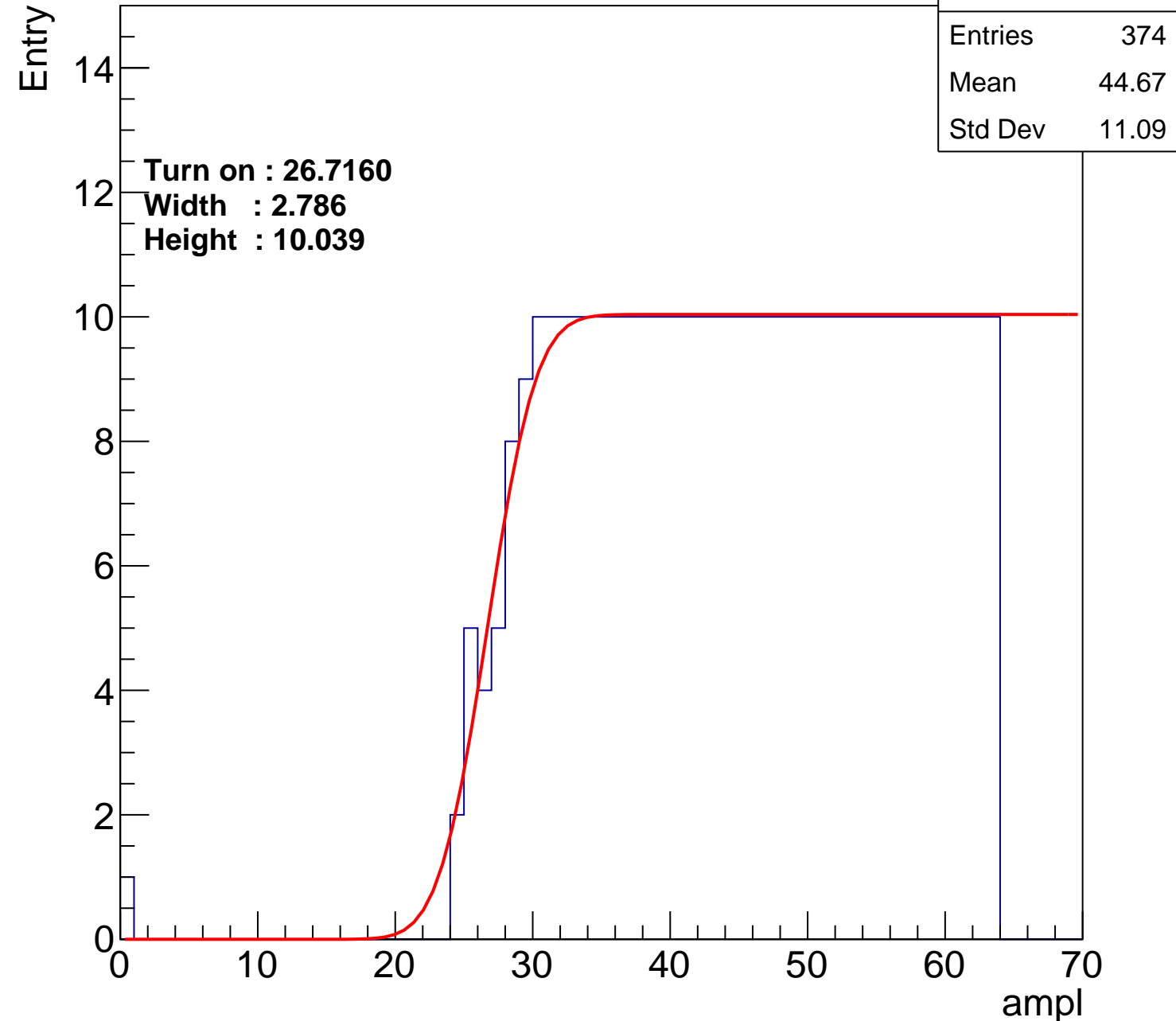
Width : 2.786

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch9

calib_packv5_042523_0143.root, FC#0, port D2

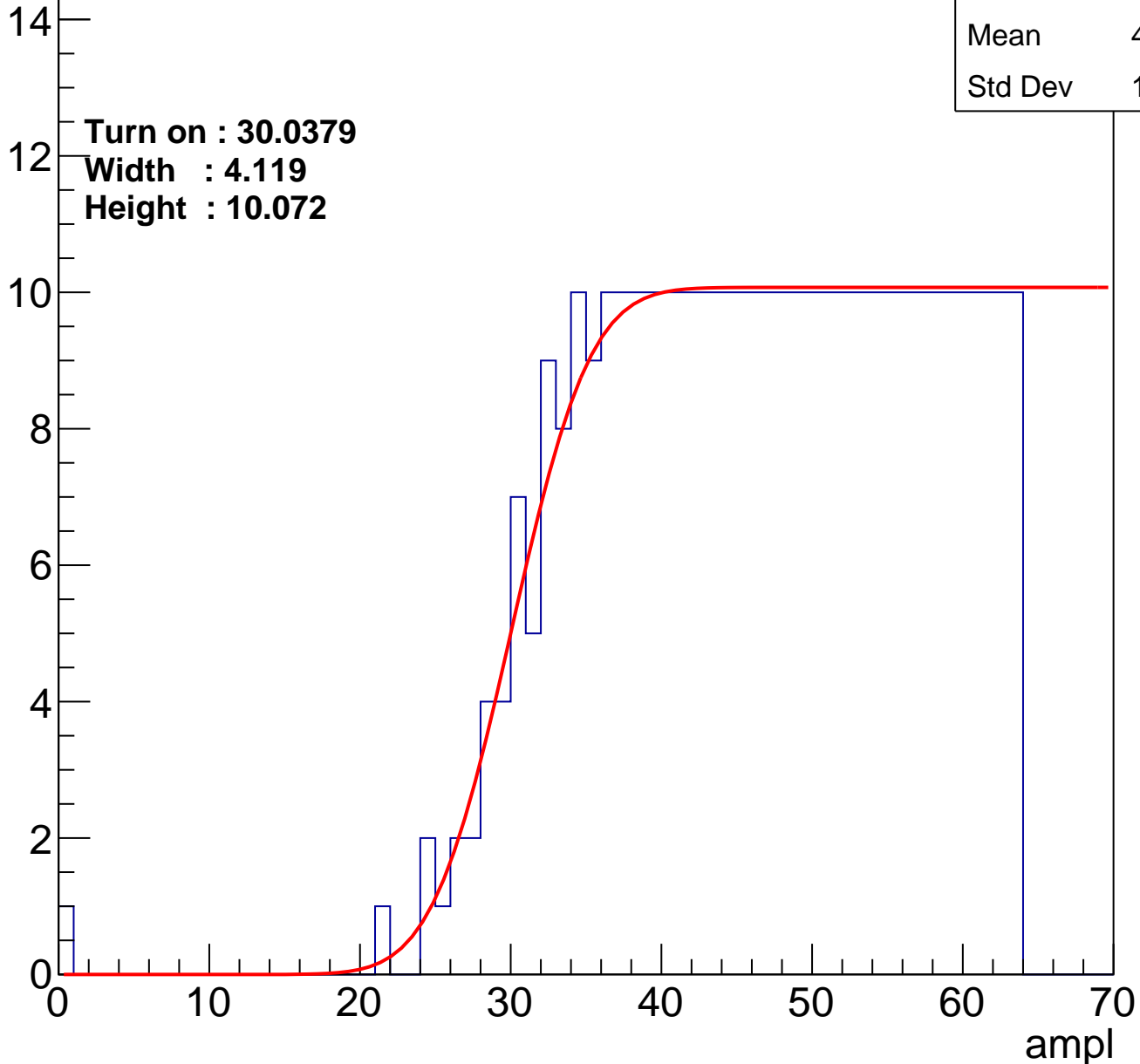
Entries	345
Mean	45.97
Std Dev	10.55

Turn on : 30.0379

Width : 4.119

Height : 10.072

Entry



B1L101S, U23-ch10

calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	45.09
Std Dev	11.3

Turn on : 28.2310

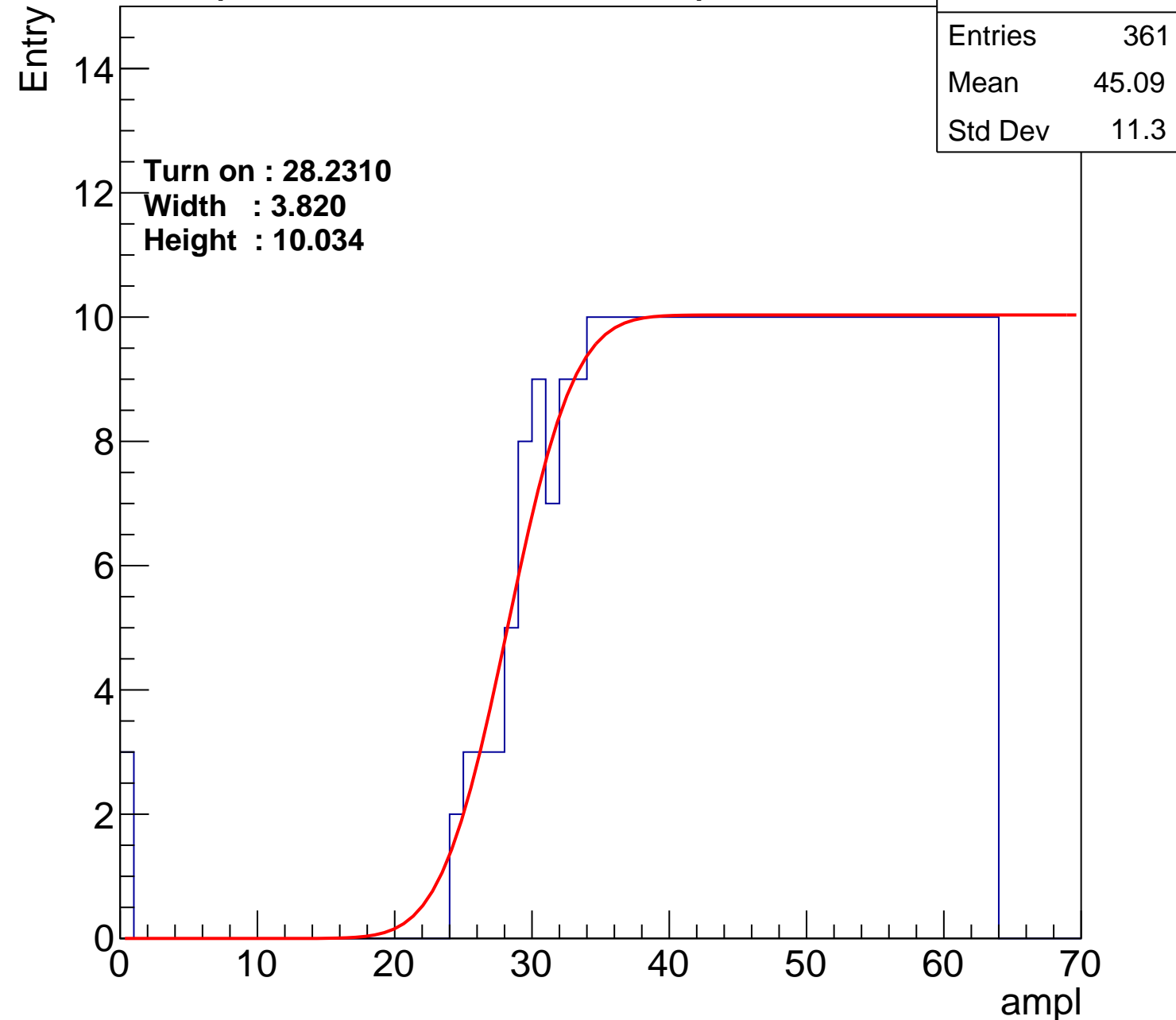
Width : 3.820

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch11

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.01
Std Dev	11.32

Turn on : 28.5276

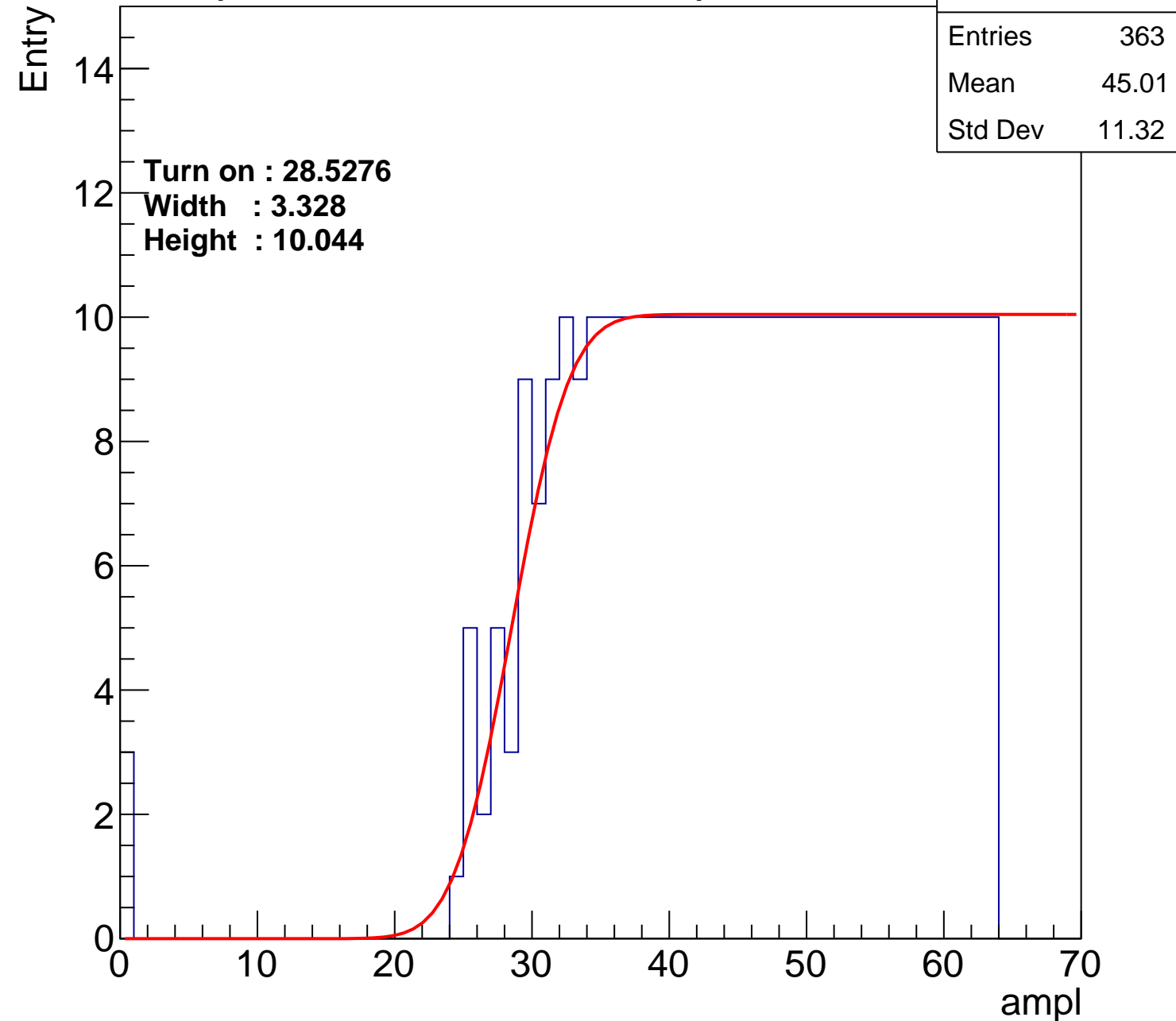
Width : 3.328

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch12

calib_packv5_042523_0143.root, FC#0, port D2

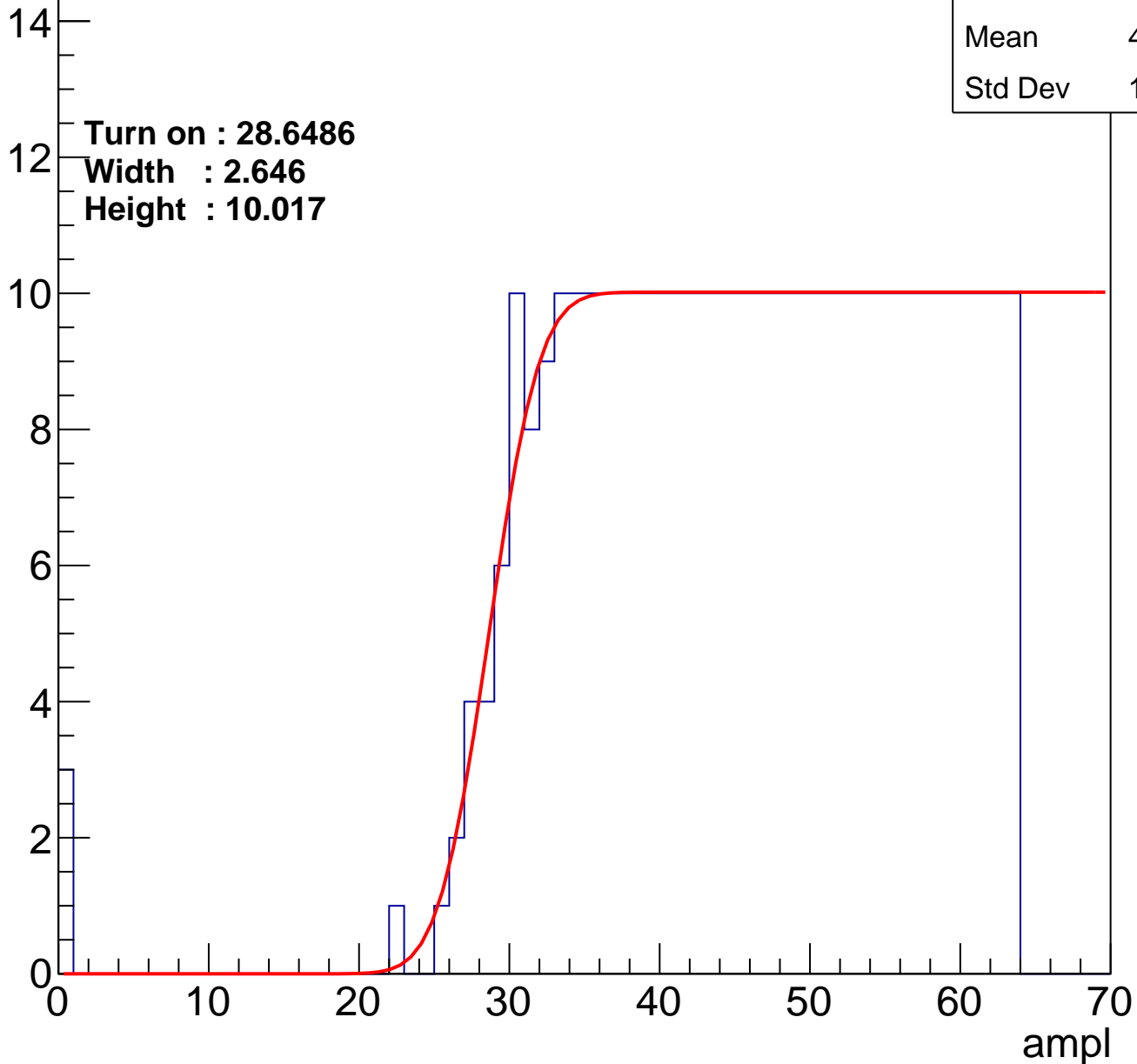
Entries	358
Mean	45.28
Std Dev	11.16

Turn on : 28.6486

Width : 2.646

Height : 10.017

Entry



B1L101S, U23-ch13

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.02
Std Dev	11.2

Turn on : 27.7302

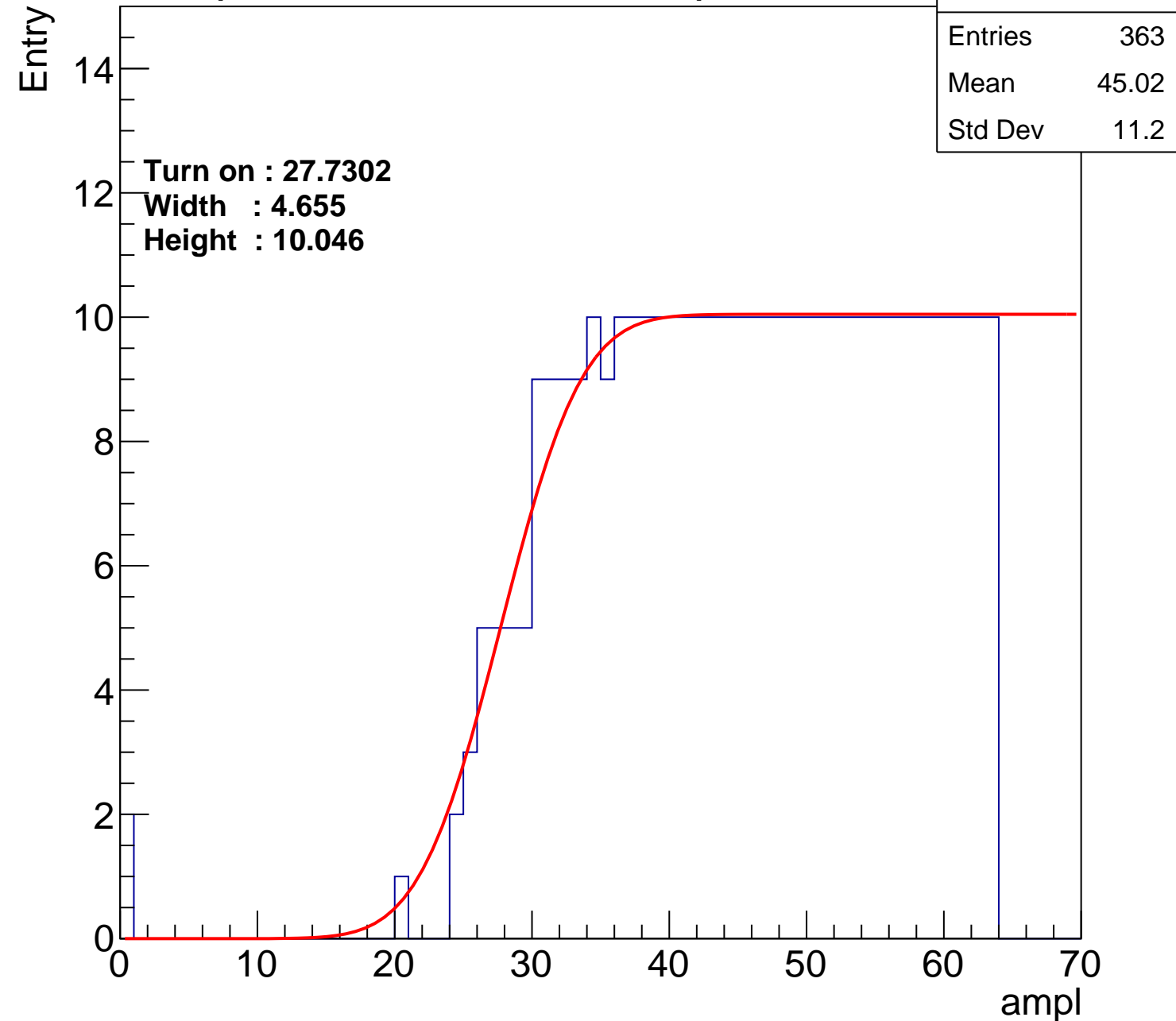
Width : 4.655

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch14

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.39
Std Dev	11.48

Turn on : 26.8079

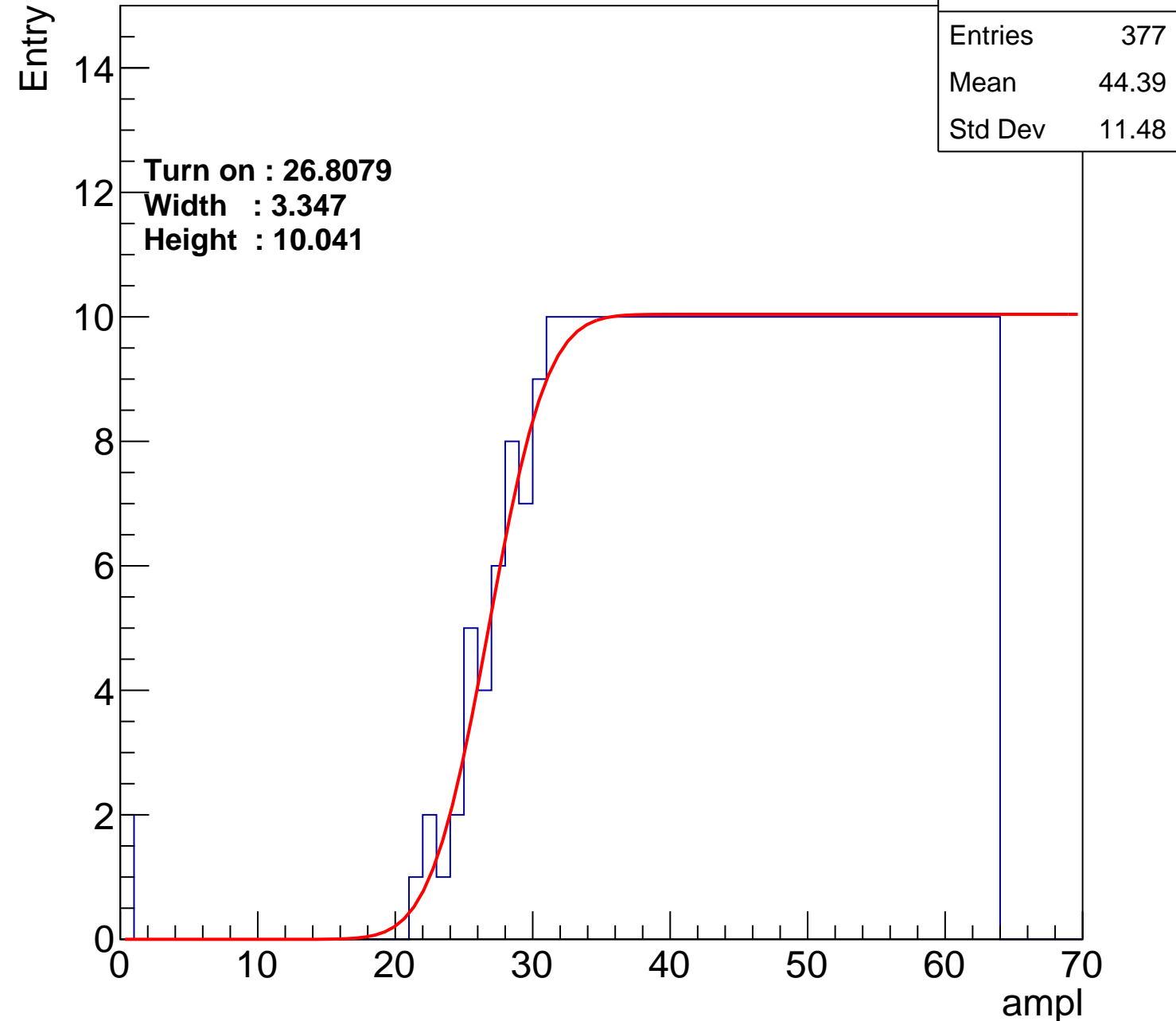
Width : 3.347

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch15

calib_packv5_042523_0143.root, FC#0, port D2

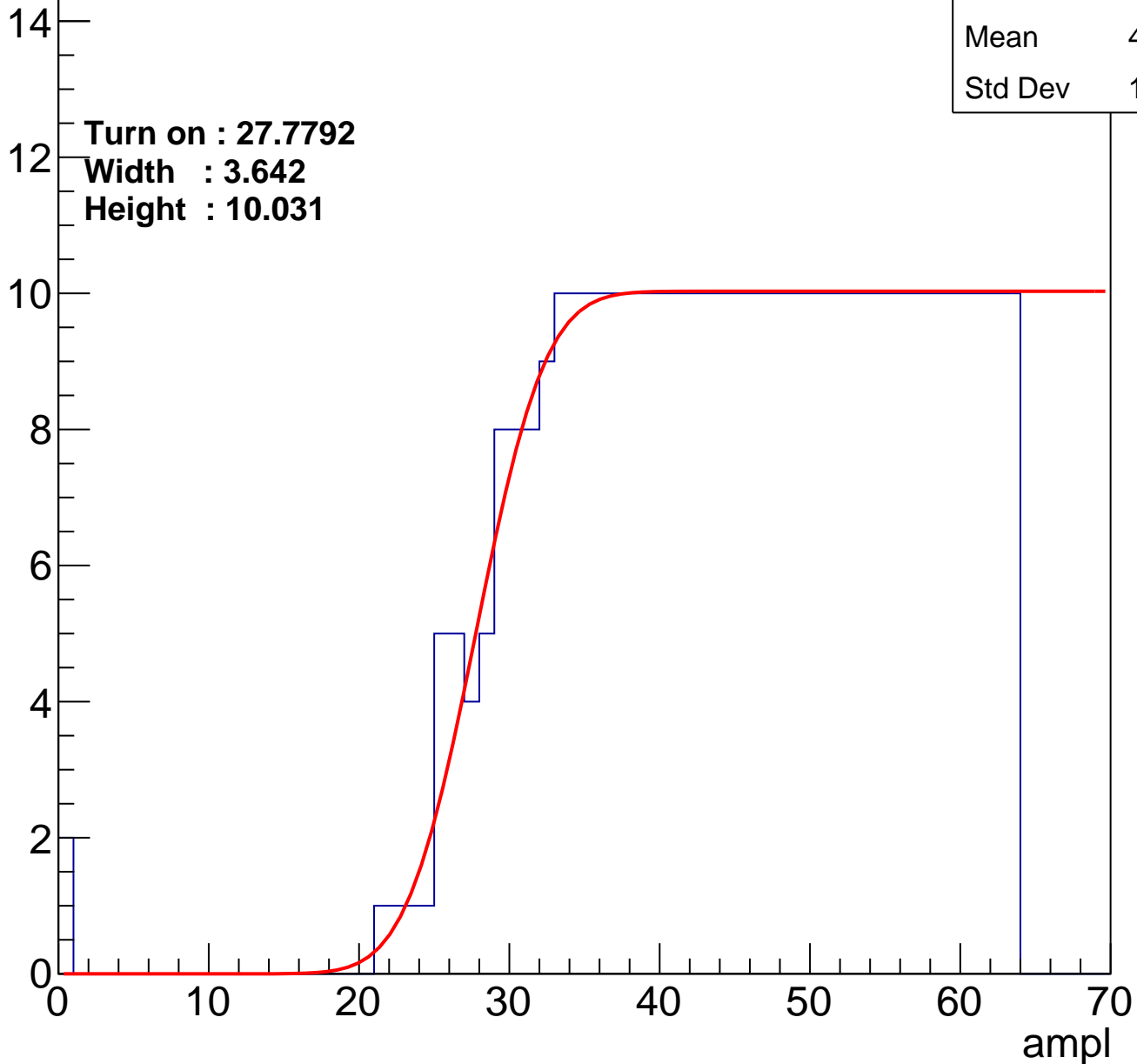
Entries	368
Mean	44.79
Std Dev	11.32

Turn on : 27.7792

Width : 3.642

Height : 10.031

Entry



B1L101S, U23-ch16

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.56
Std Dev	11.37

Turn on : 27.0978

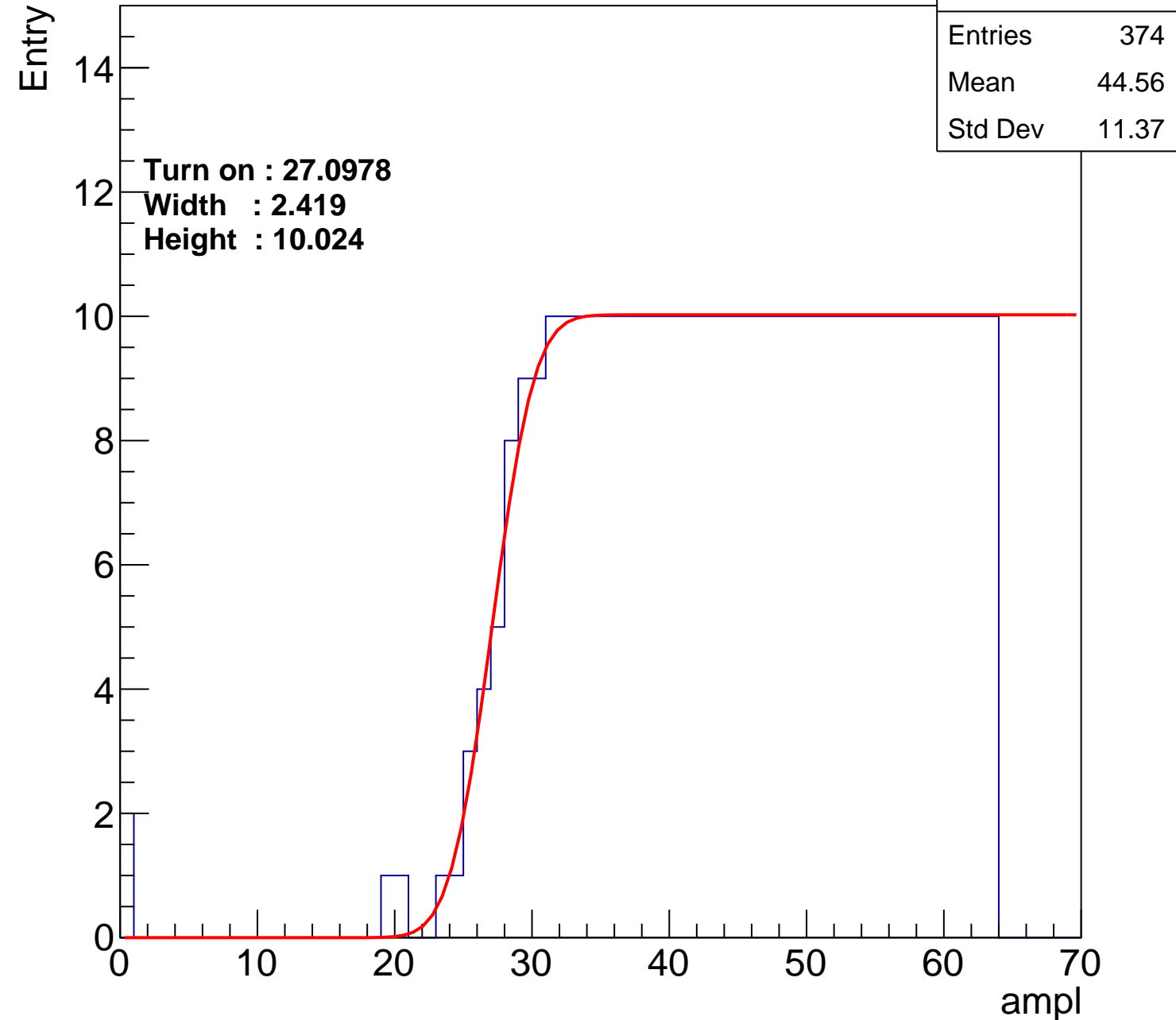
Width : 2.419

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch17

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.03
Std Dev	11.18

Turn on : 27.8878

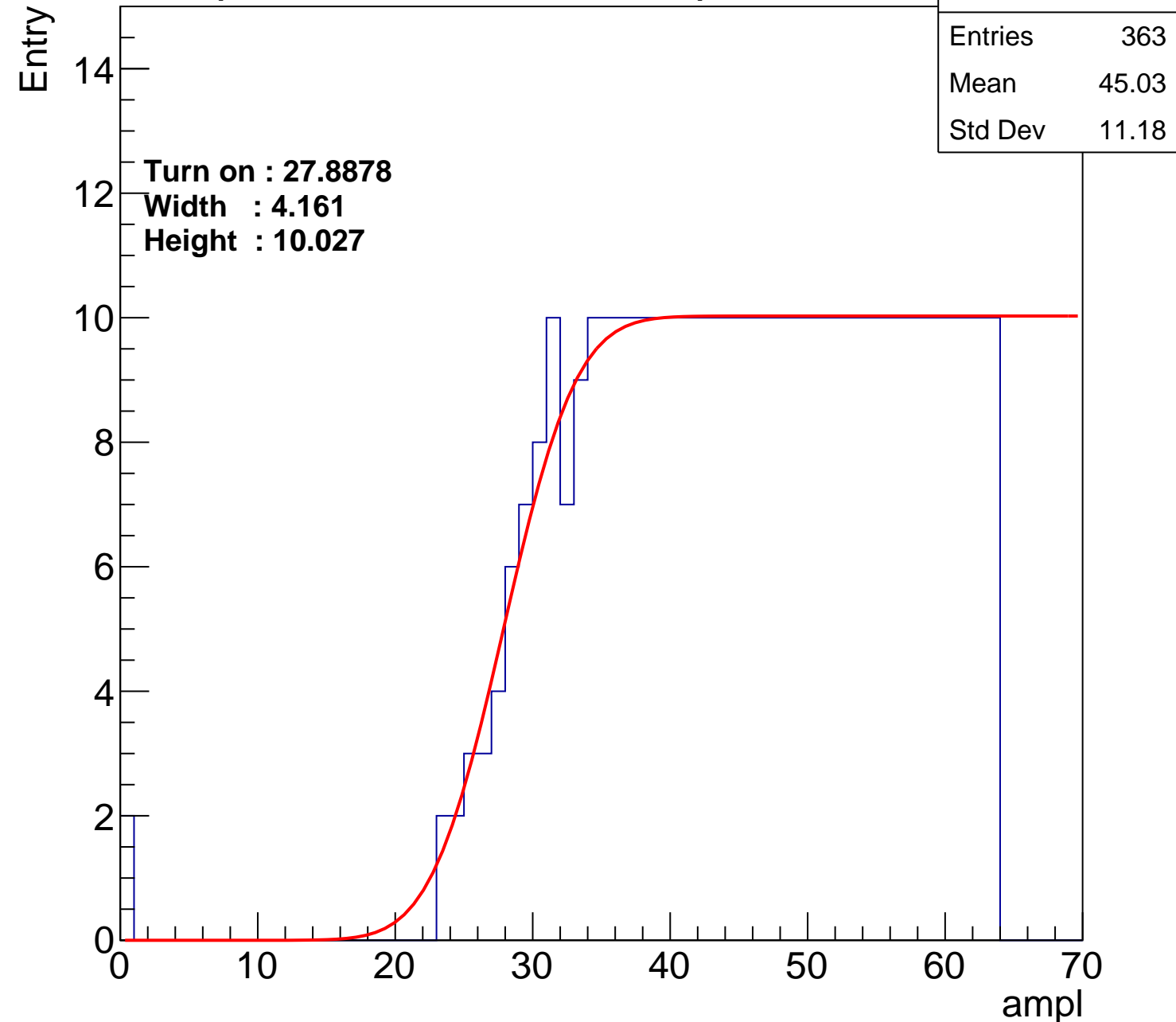
Width : 4.161

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch18

calib_packv5_042523_0143.root, FC#0, port D2

Entries	380
Mean	44.23
Std Dev	11.56

Turn on : 26.2708

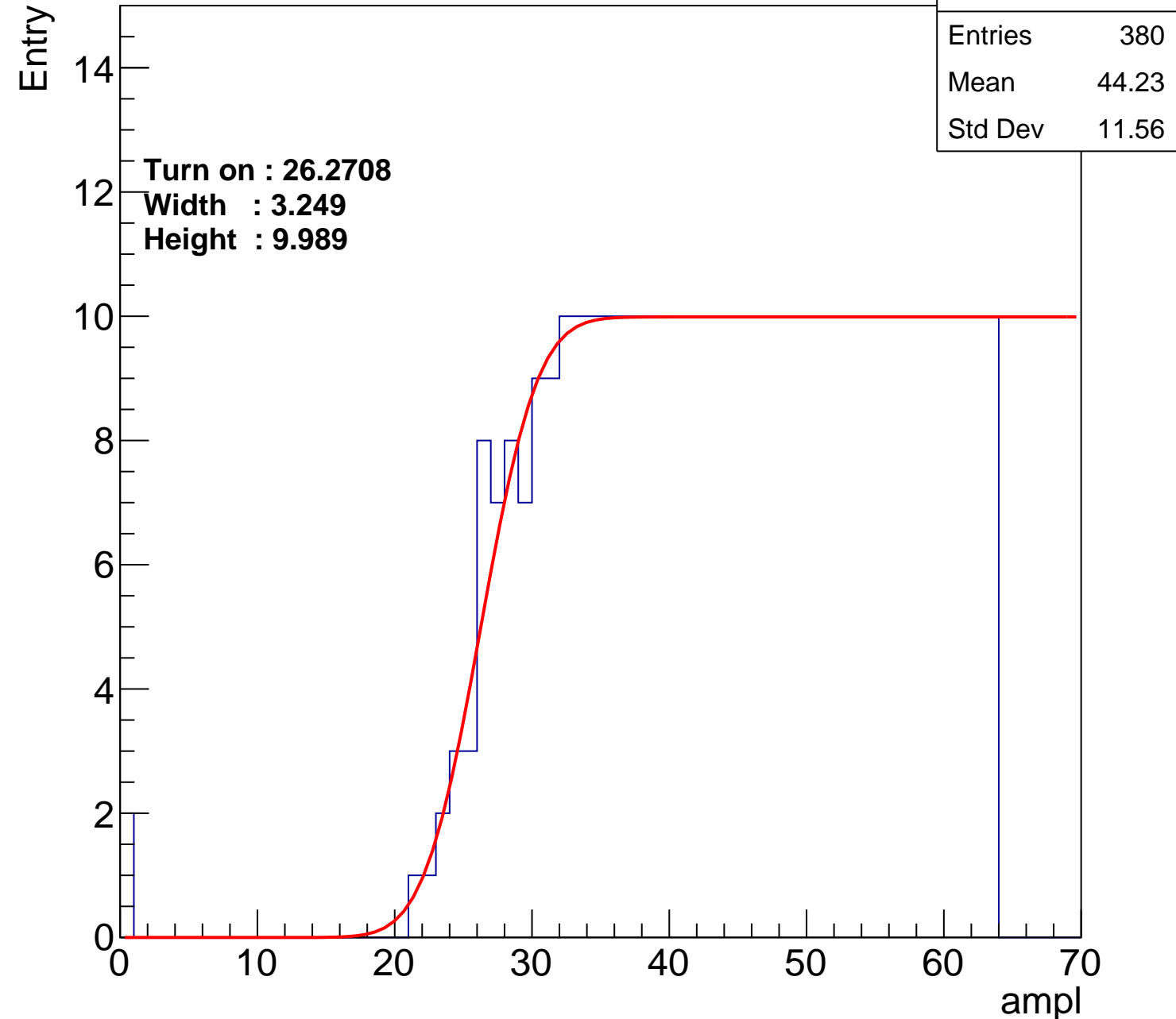
Width : 3.249

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch19

calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	45.27
Std Dev	10.83

Turn on : 28.3030

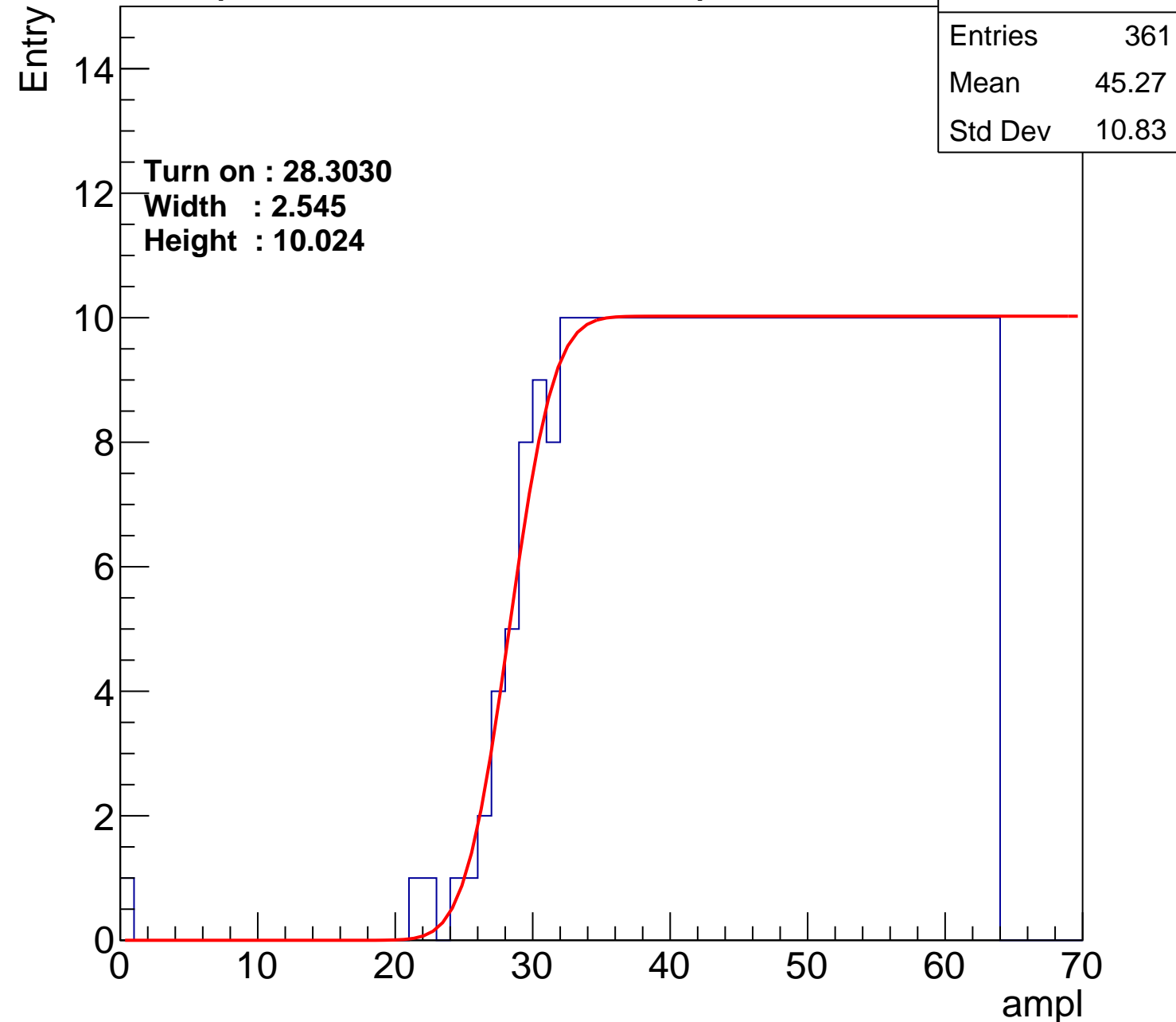
Width : 2.545

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch20

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.55
Std Dev	11.83

Turn on : 27.7634

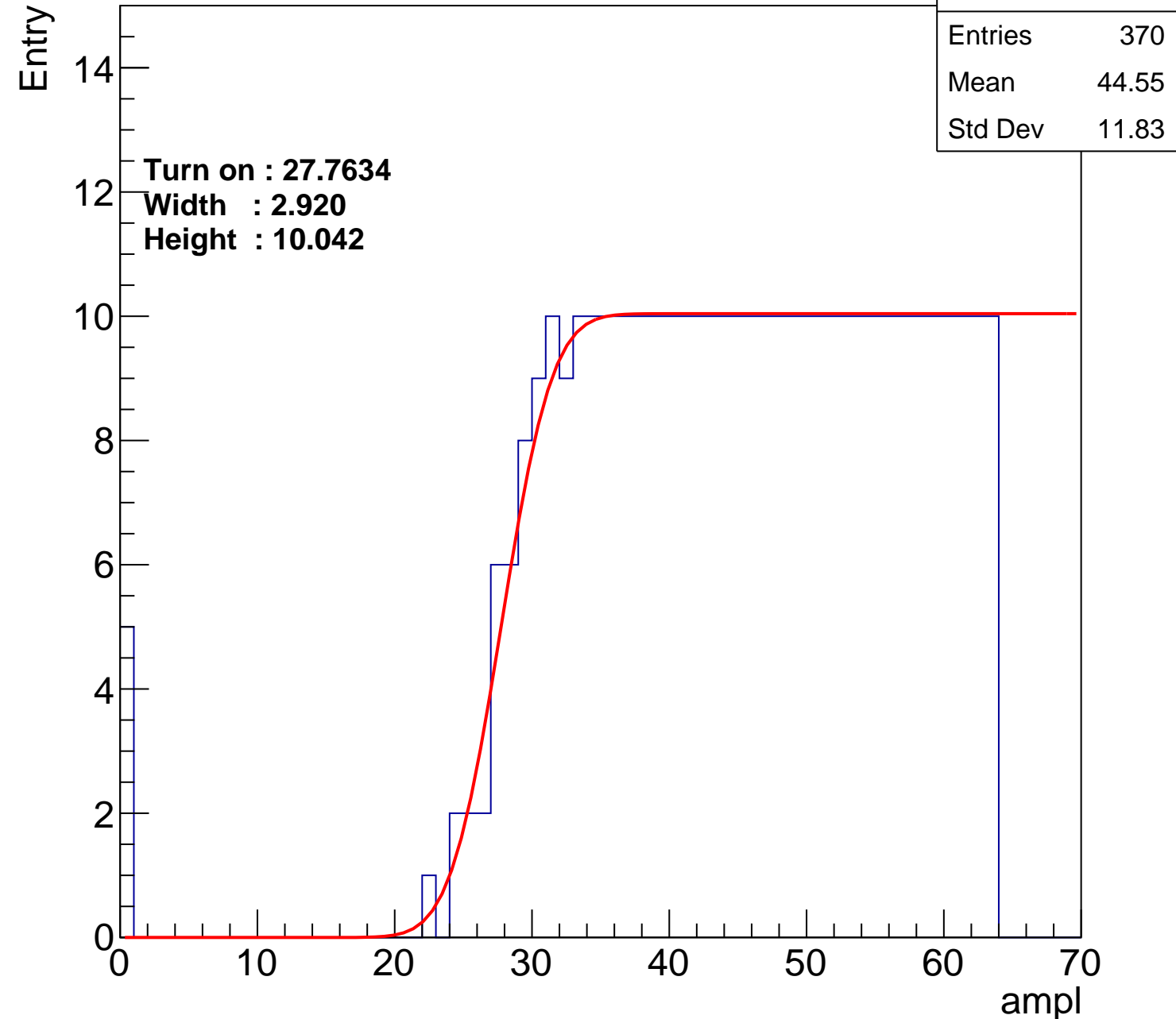
Width : 2.920

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch21

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.86
Std Dev	11.38

Turn on : 27.4827

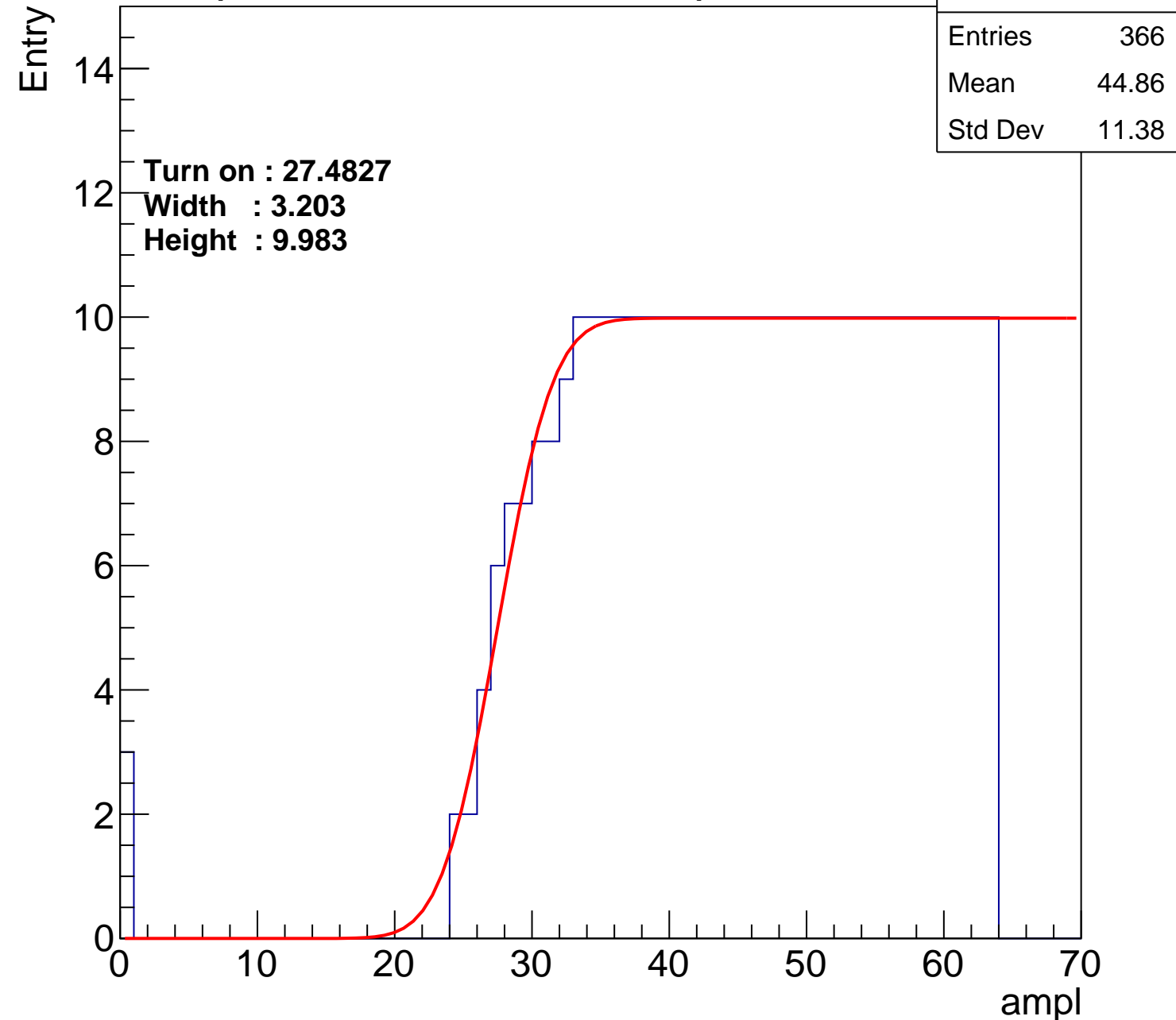
Width : 3.203

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch22

calib_packv5_042523_0143.root, FC#0, port D2

Entries	357
Mean	45.45
Std Dev	10.74

Turn on : 28.7565

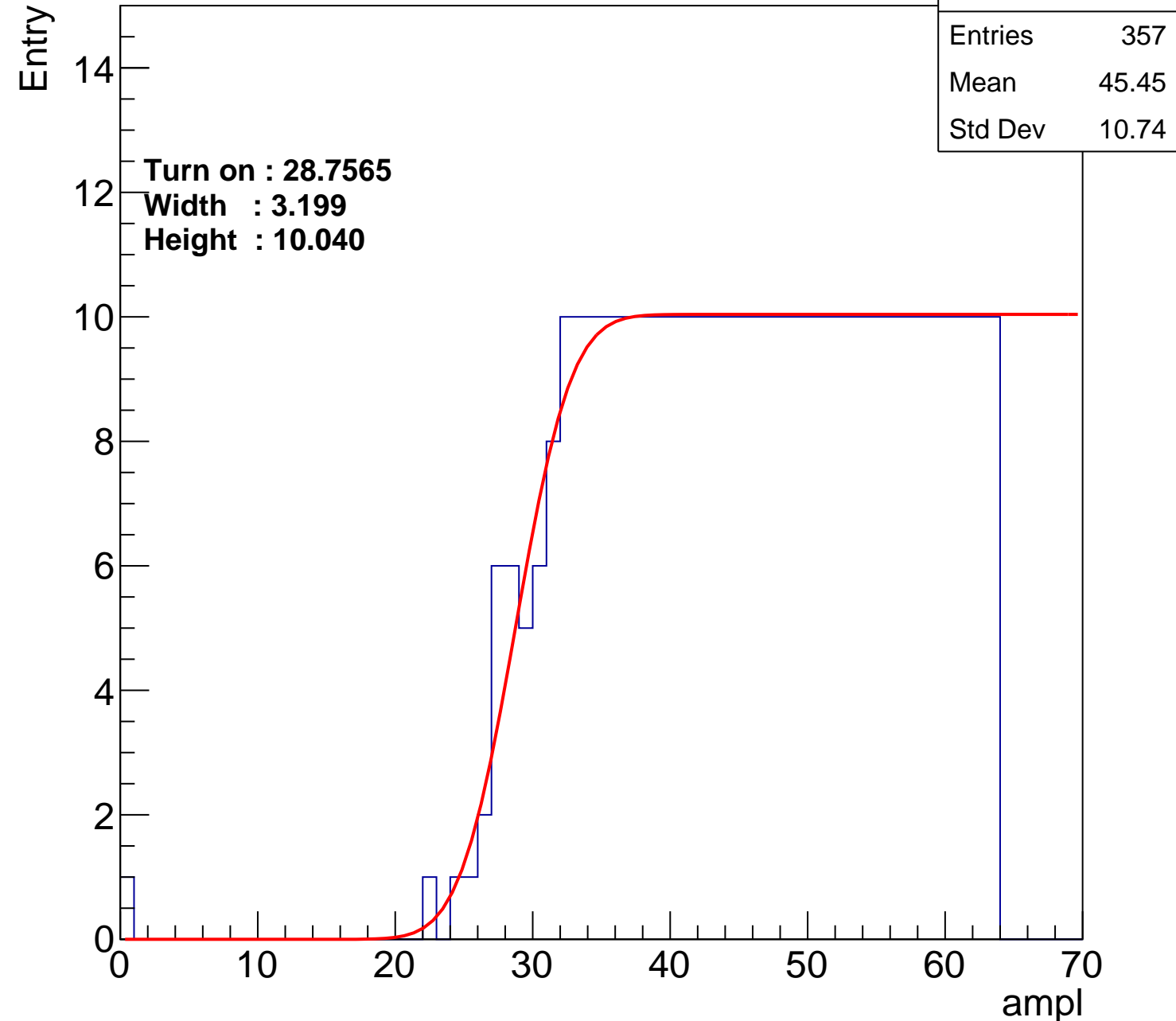
Width : 3.199

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch23

calib_packv5_042523_0143.root, FC#0, port D2

Entries	379
Mean	44.34
Std Dev	11.38

Turn on : 26.3294

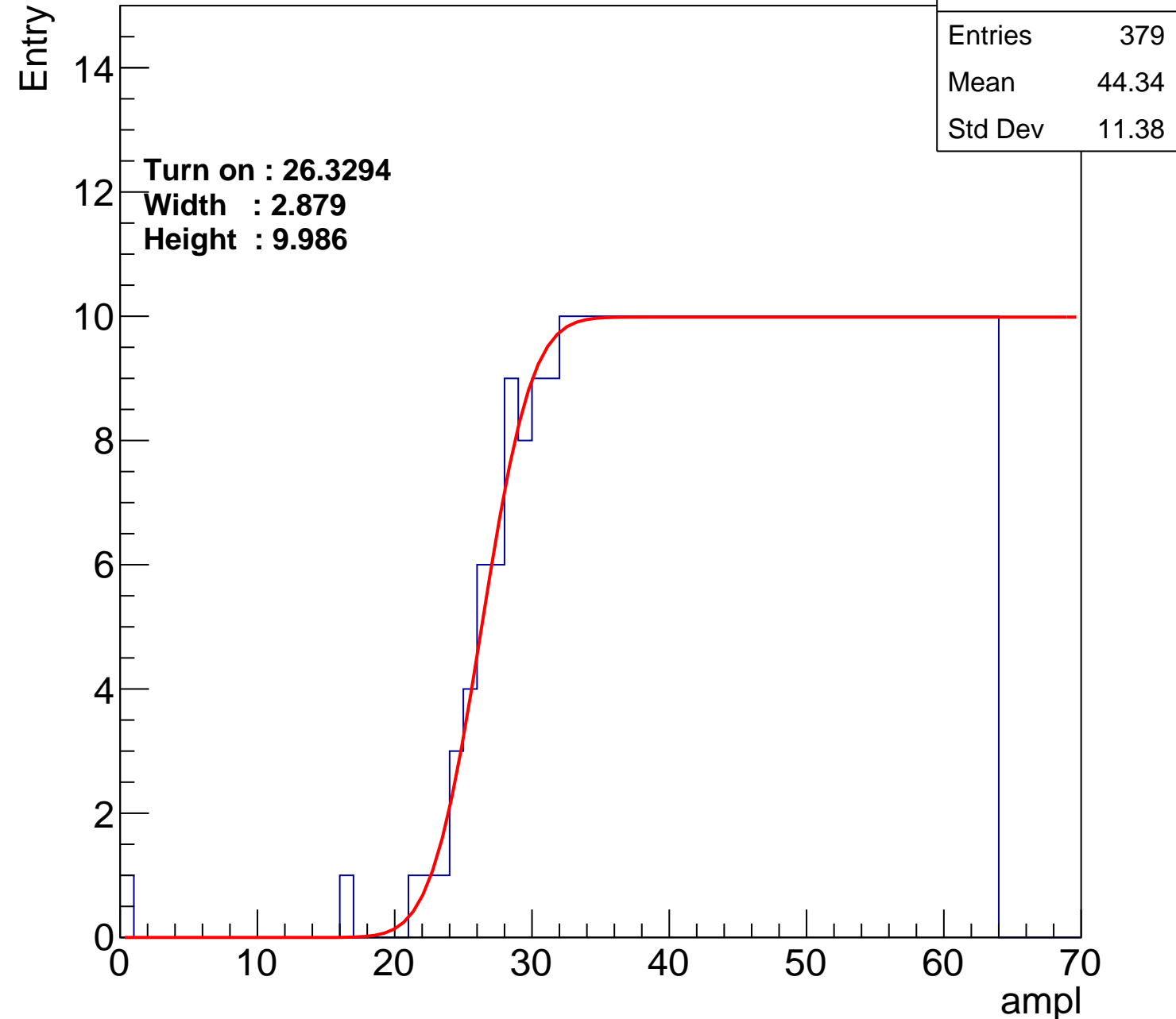
Width : 2.879

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch24

calib_packv5_042523_0143.root, FC#0, port D2

Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 26.3194

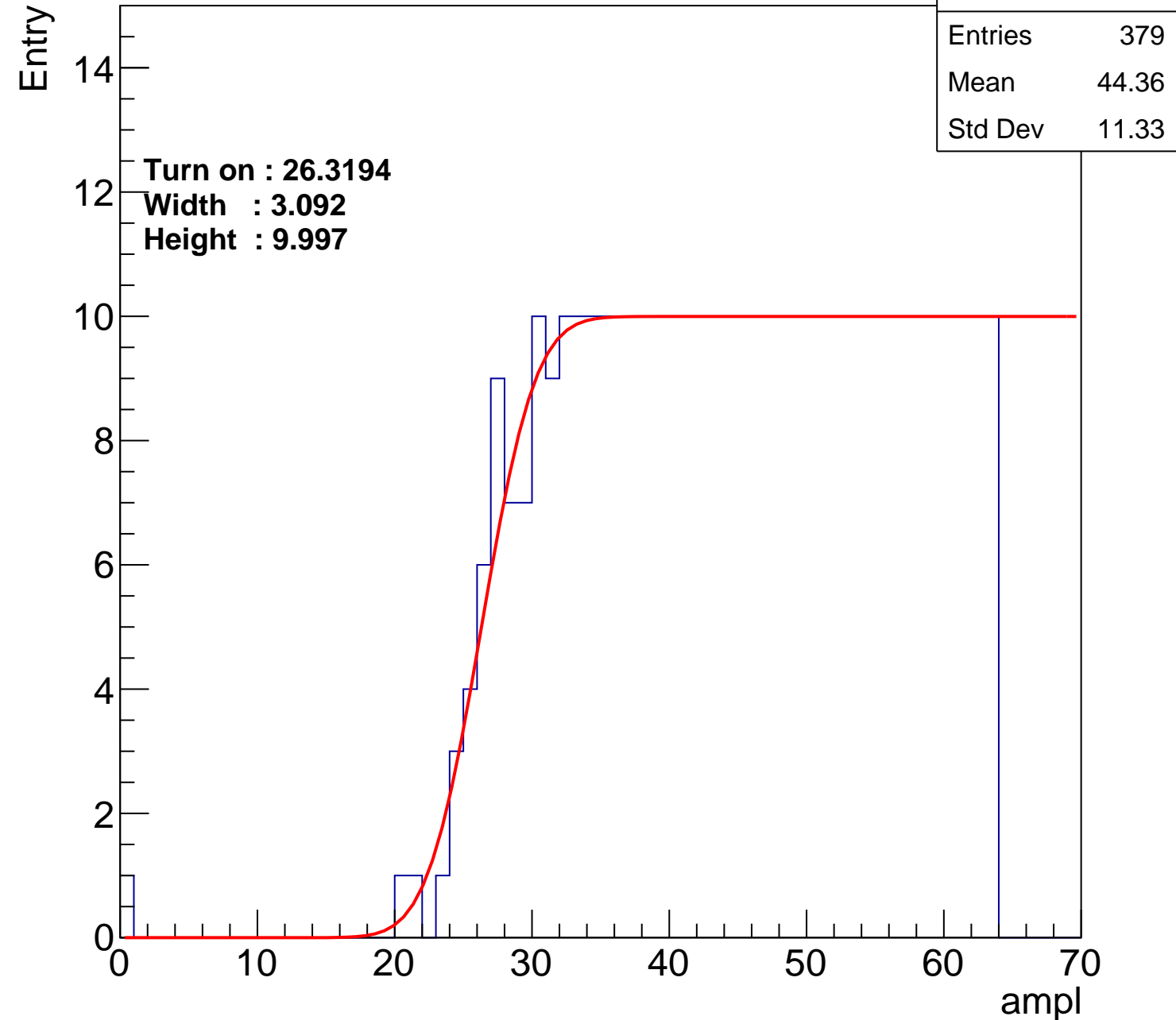
Width : 3.092

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch25

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.9
Std Dev	11.19

Turn on : 27.5167

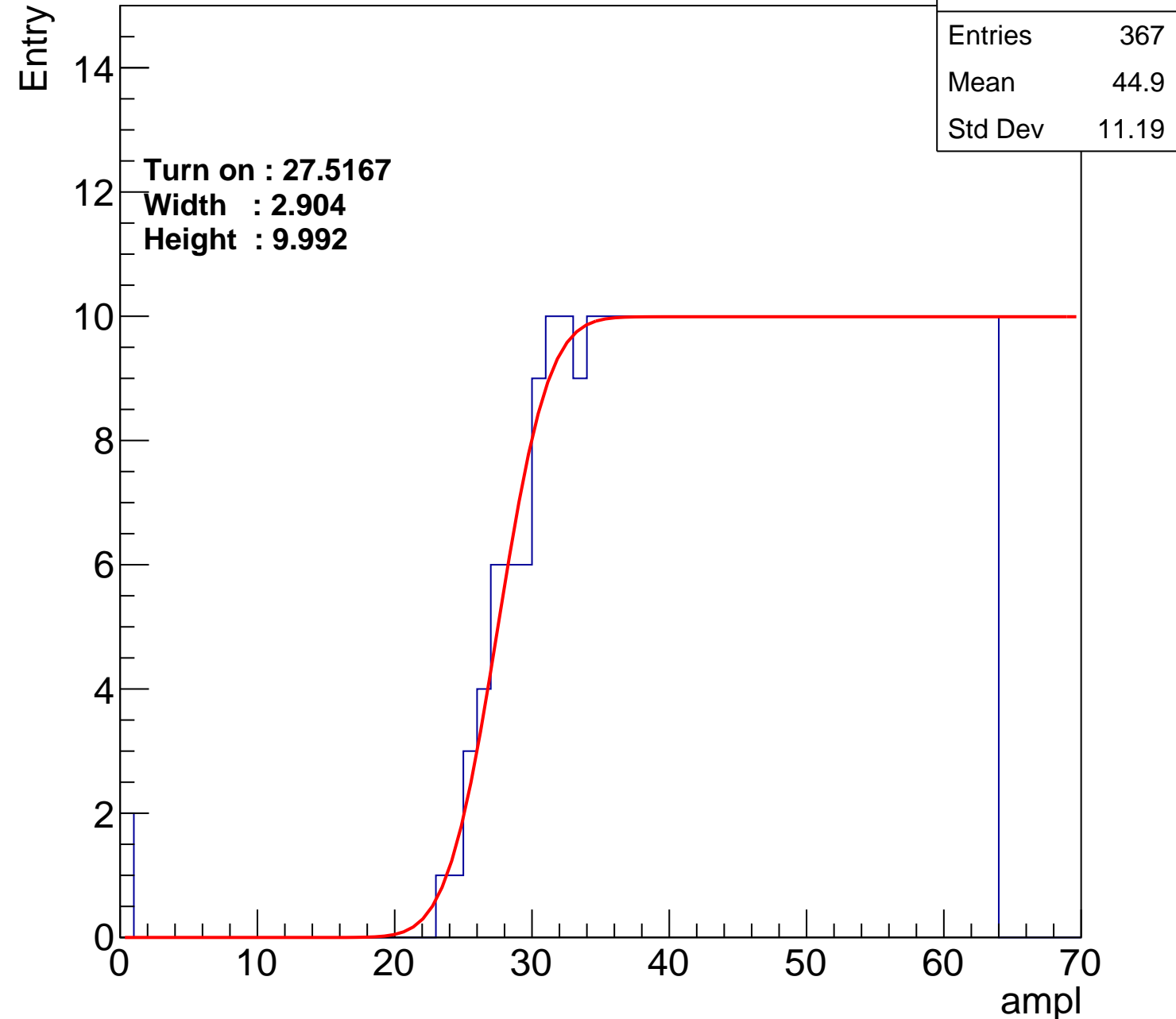
Width : 2.904

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch26

calib_packv5_042523_0143.root, FC#0, port D2

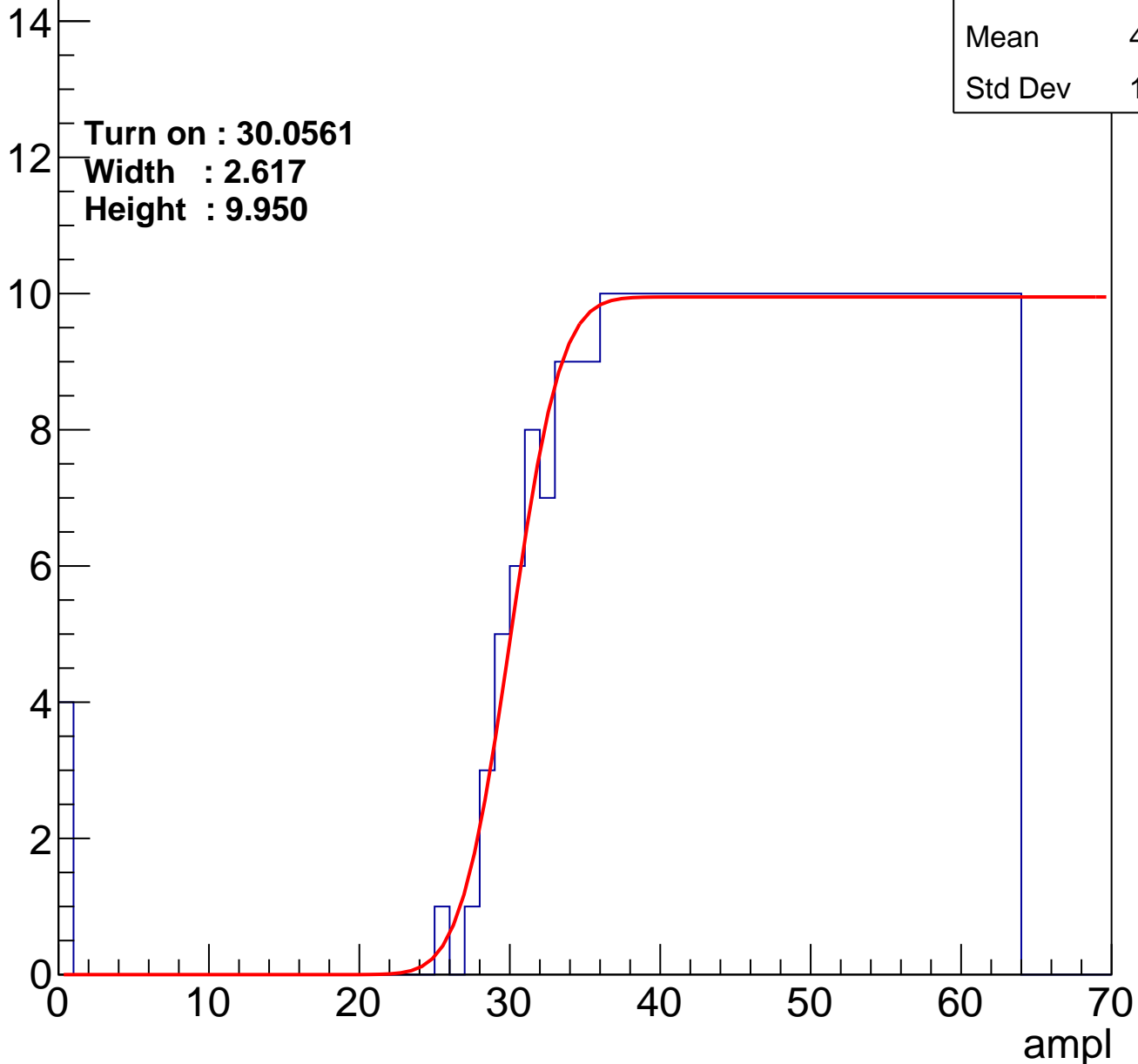
Entries	342
Mean	45.94
Std Dev	11.08

Turn on : 30.0561

Width : 2.617

Height : 9.950

Entry



B1L101S, U23-ch27

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.55
Std Dev	11.5

Turn on : 27.6260

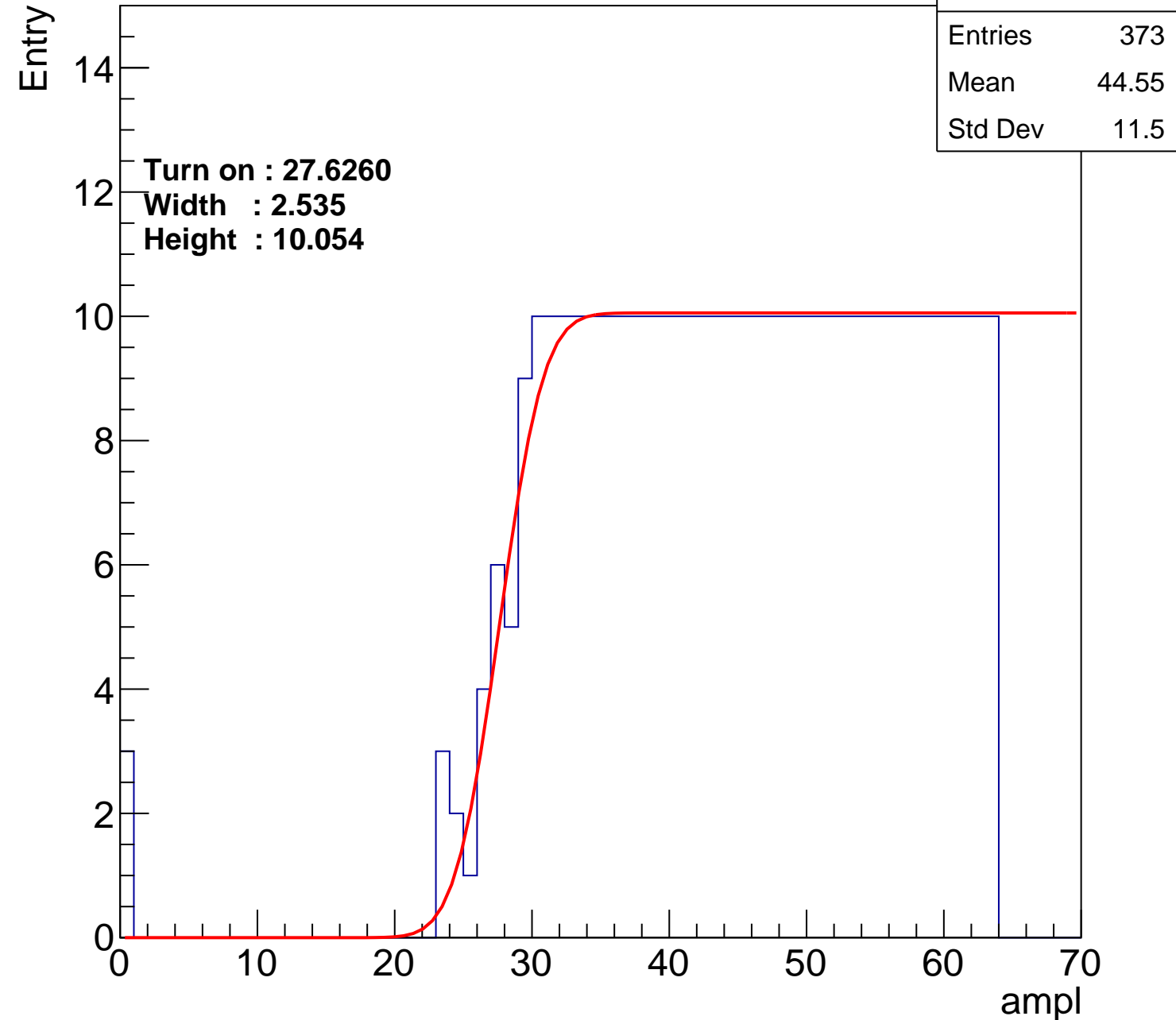
Width : 2.535

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch28

calib_packv5_042523_0143.root, FC#0, port D2

Entries	383
Mean	44
Std Dev	11.85

Turn on : 26.5808

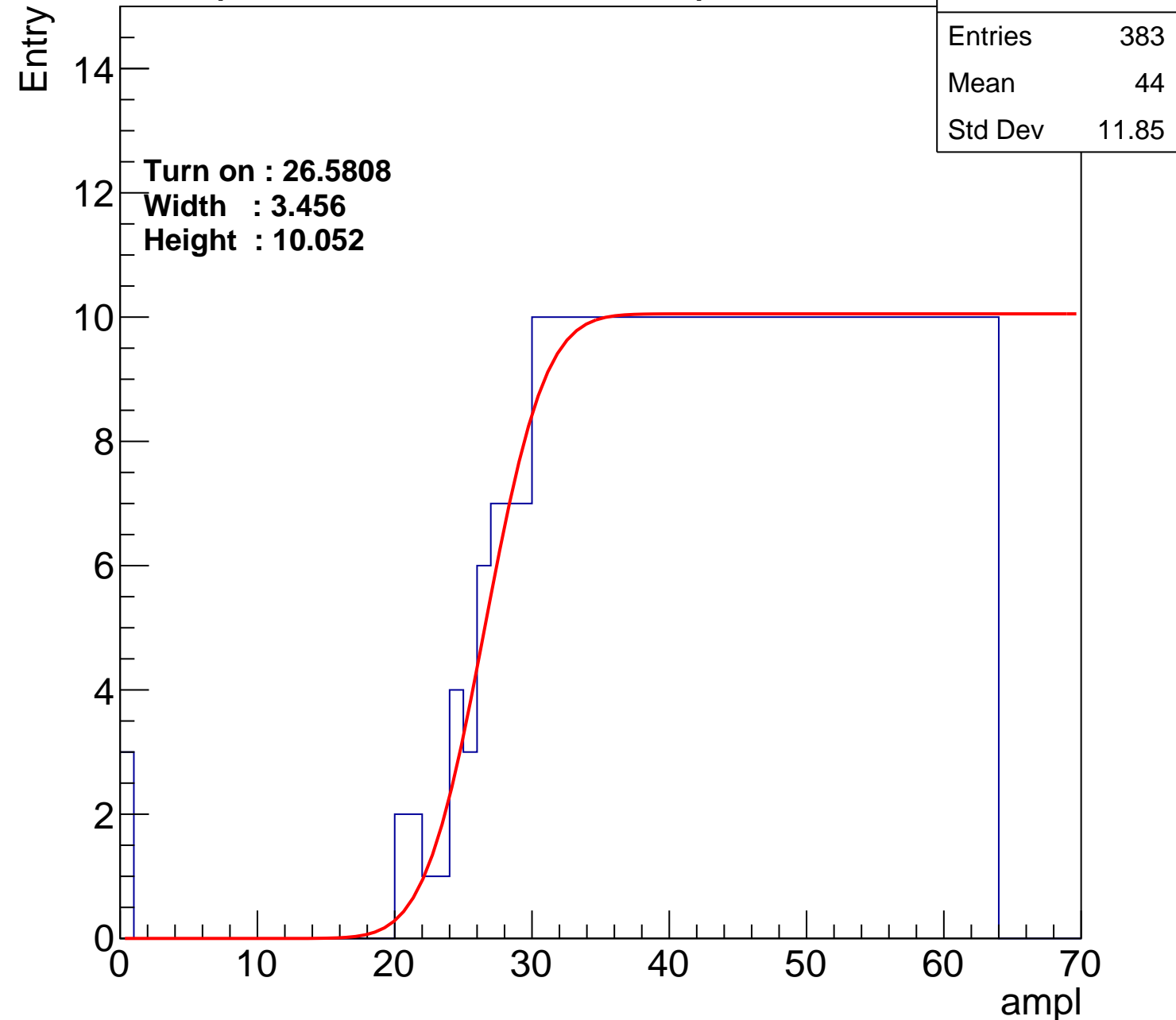
Width : 3.456

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch29

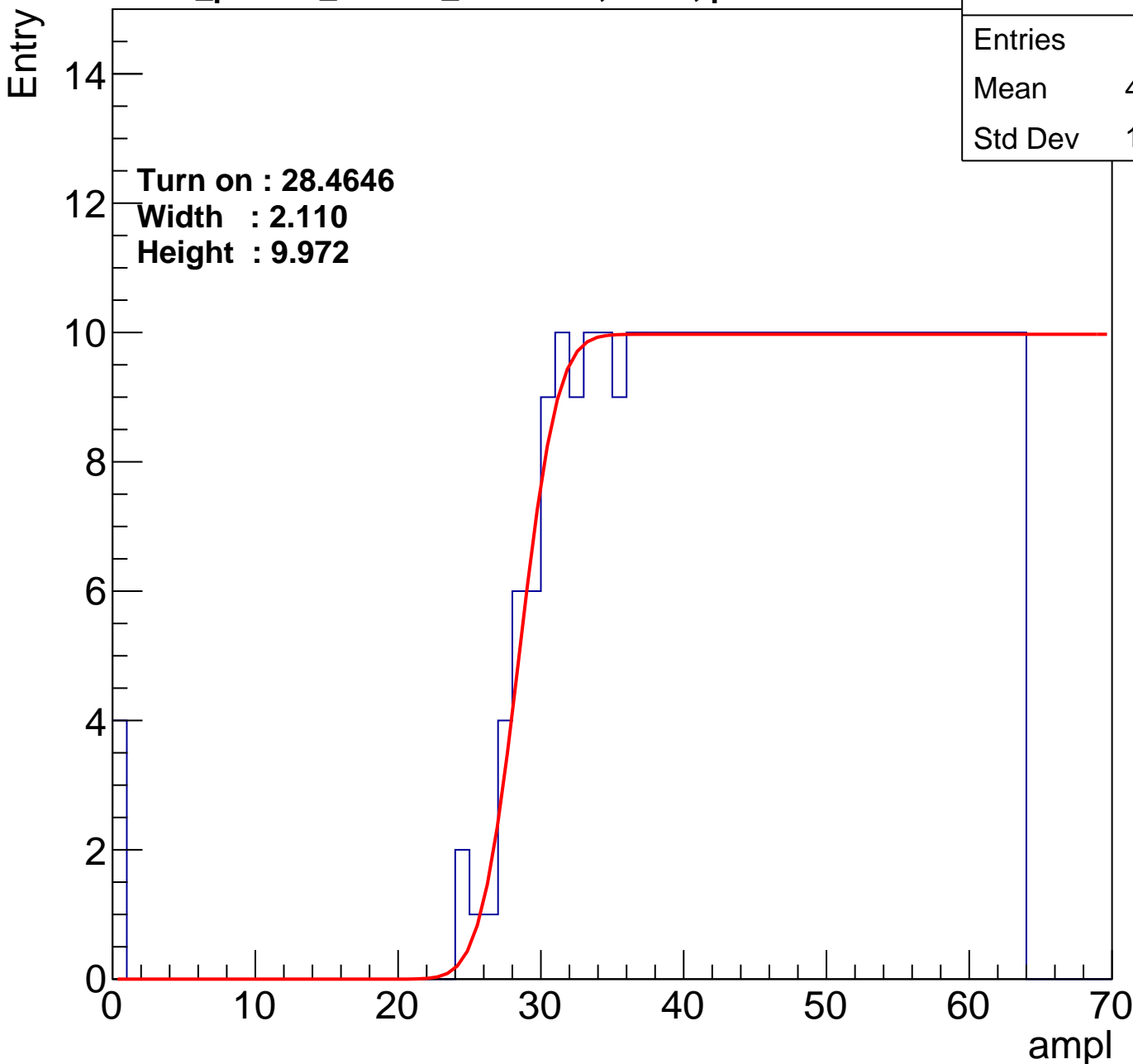
calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	45.05
Std Dev	11.45

Turn on : 28.4646

Width : 2.110

Height : 9.972



B1L101S, U23-ch30

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.87
Std Dev	11.24

Turn on : 27.4057

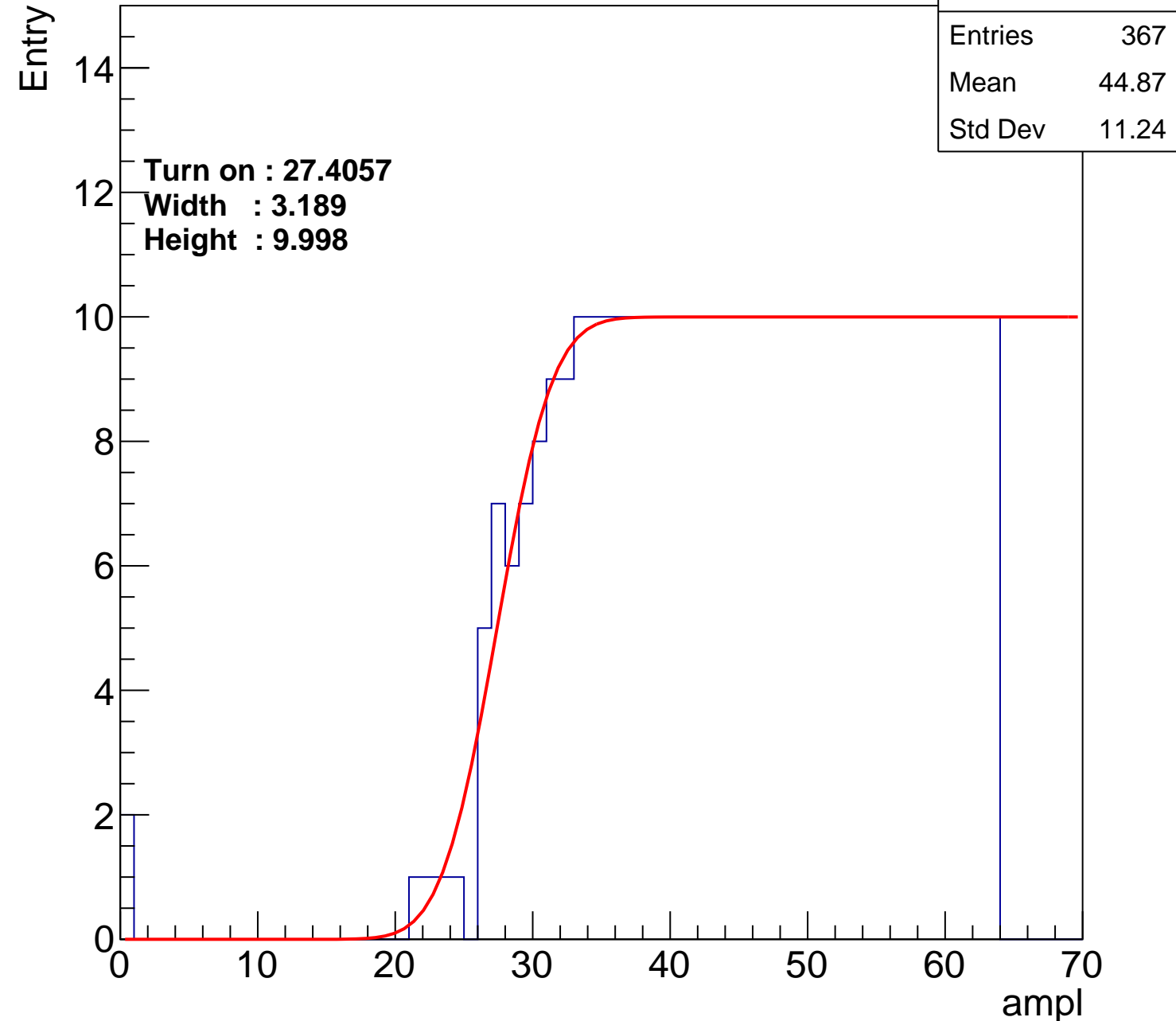
Width : 3.189

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch31

calib_packv5_042523_0143.root, FC#0, port D2

Entries	376
Mean	44.24
Std Dev	11.99

Turn on : 27.2073

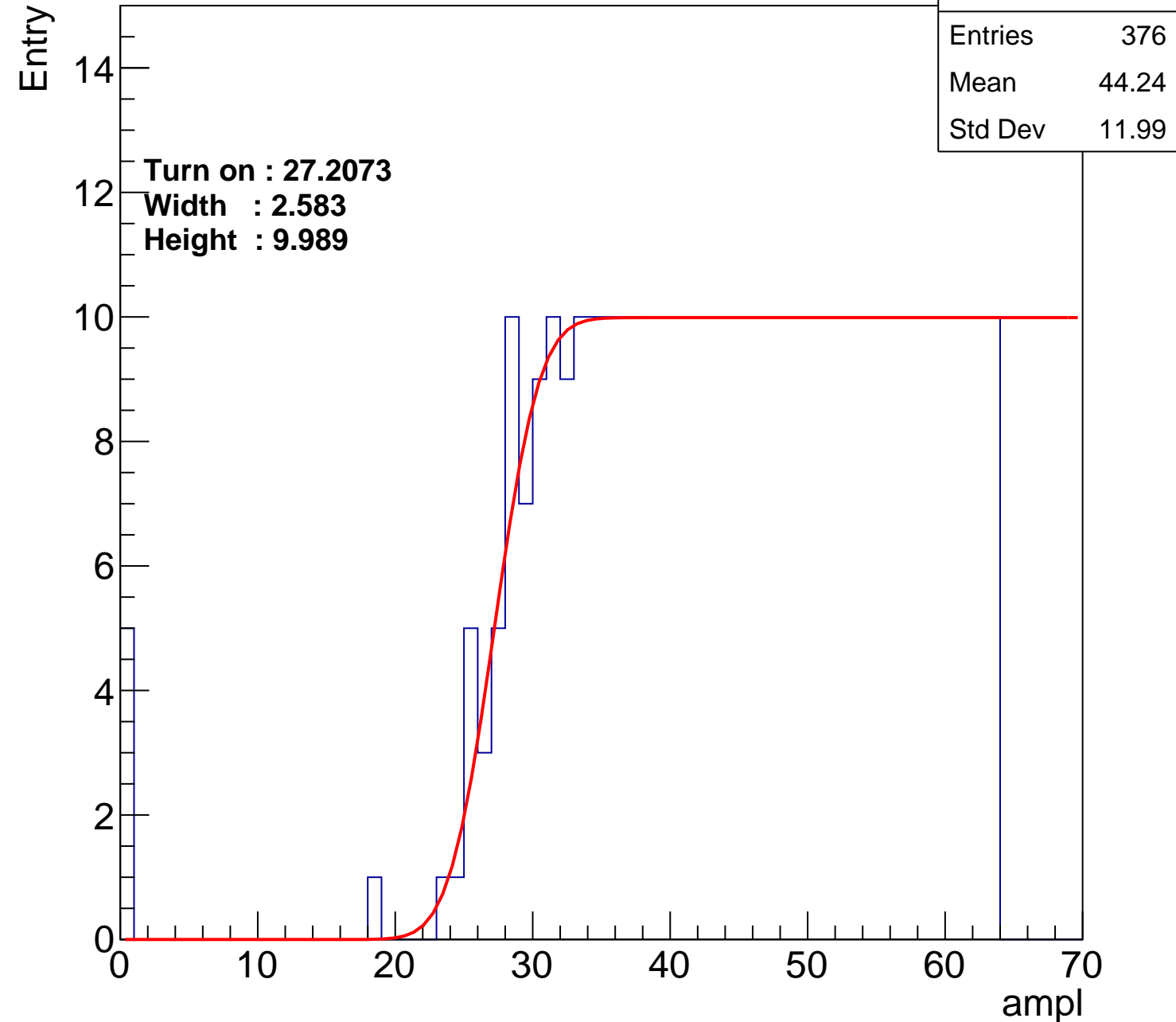
Width : 2.583

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch32

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.02
Std Dev	11.3

Turn on : 28.3042

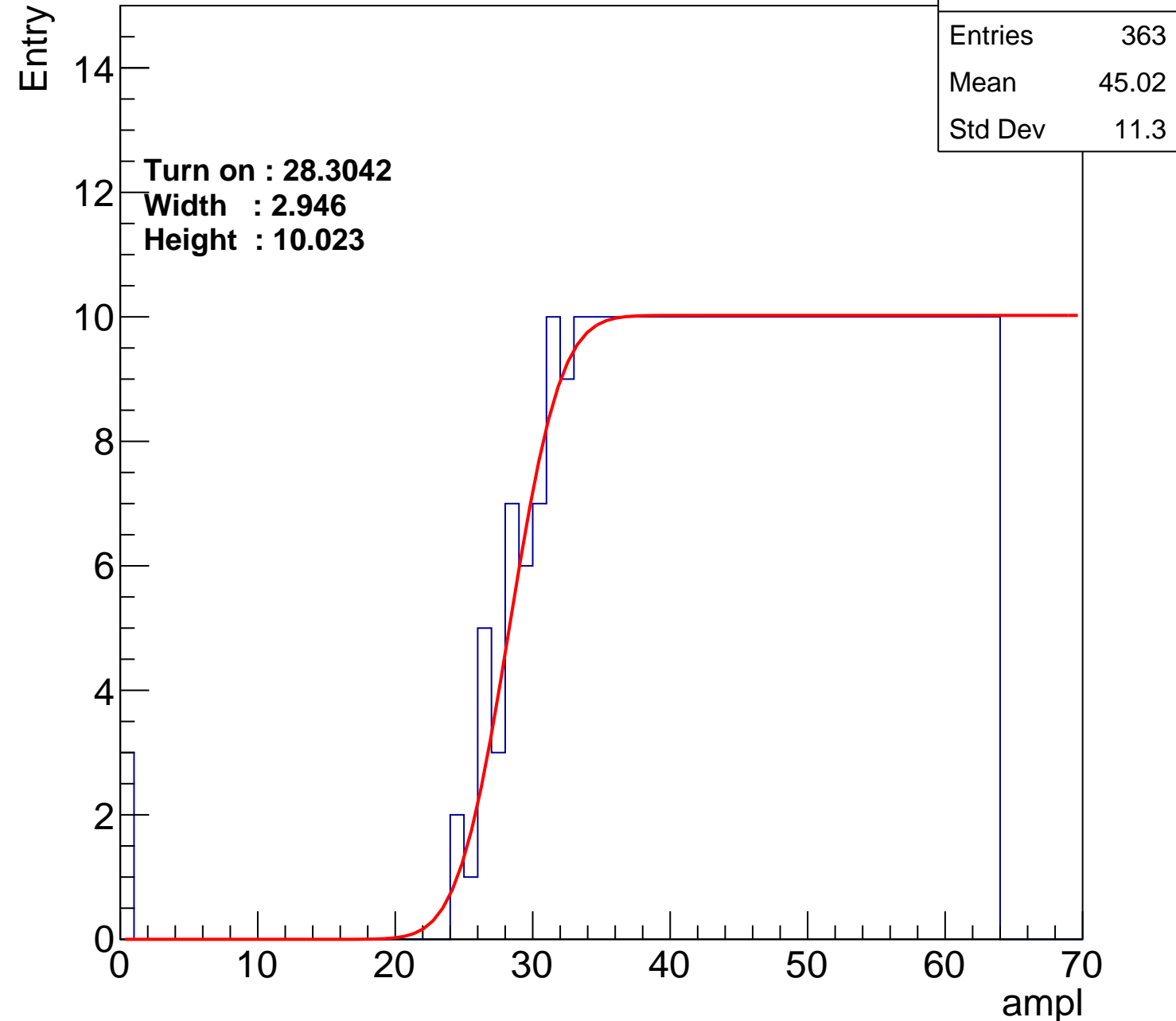
Width : 2.946

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch33

calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	44.95
Std Dev	11.69

Turn on : 29.2198

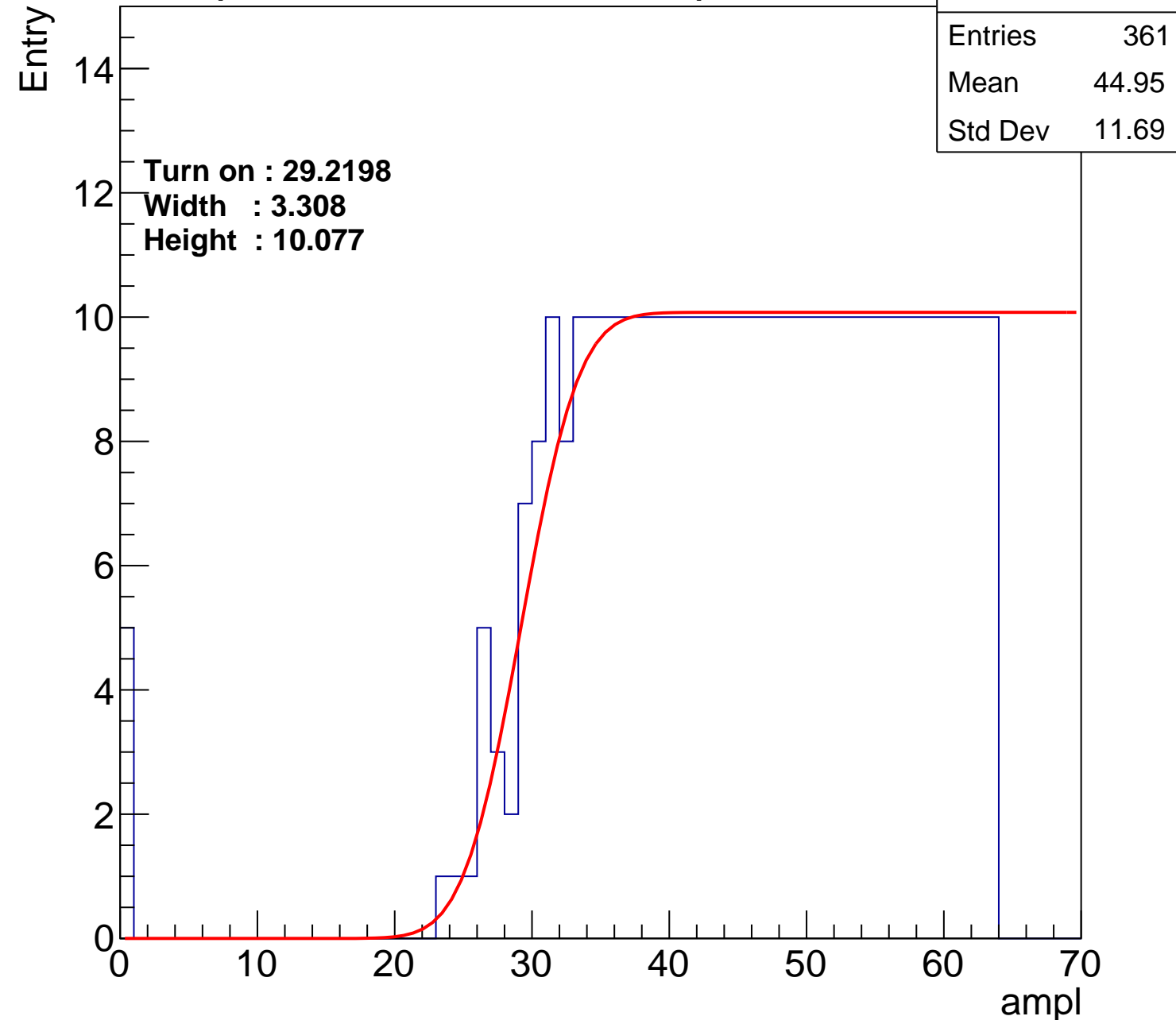
Width : 3.308

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch34

calib_packv5_042523_0143.root, FC#0, port D2

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.4643

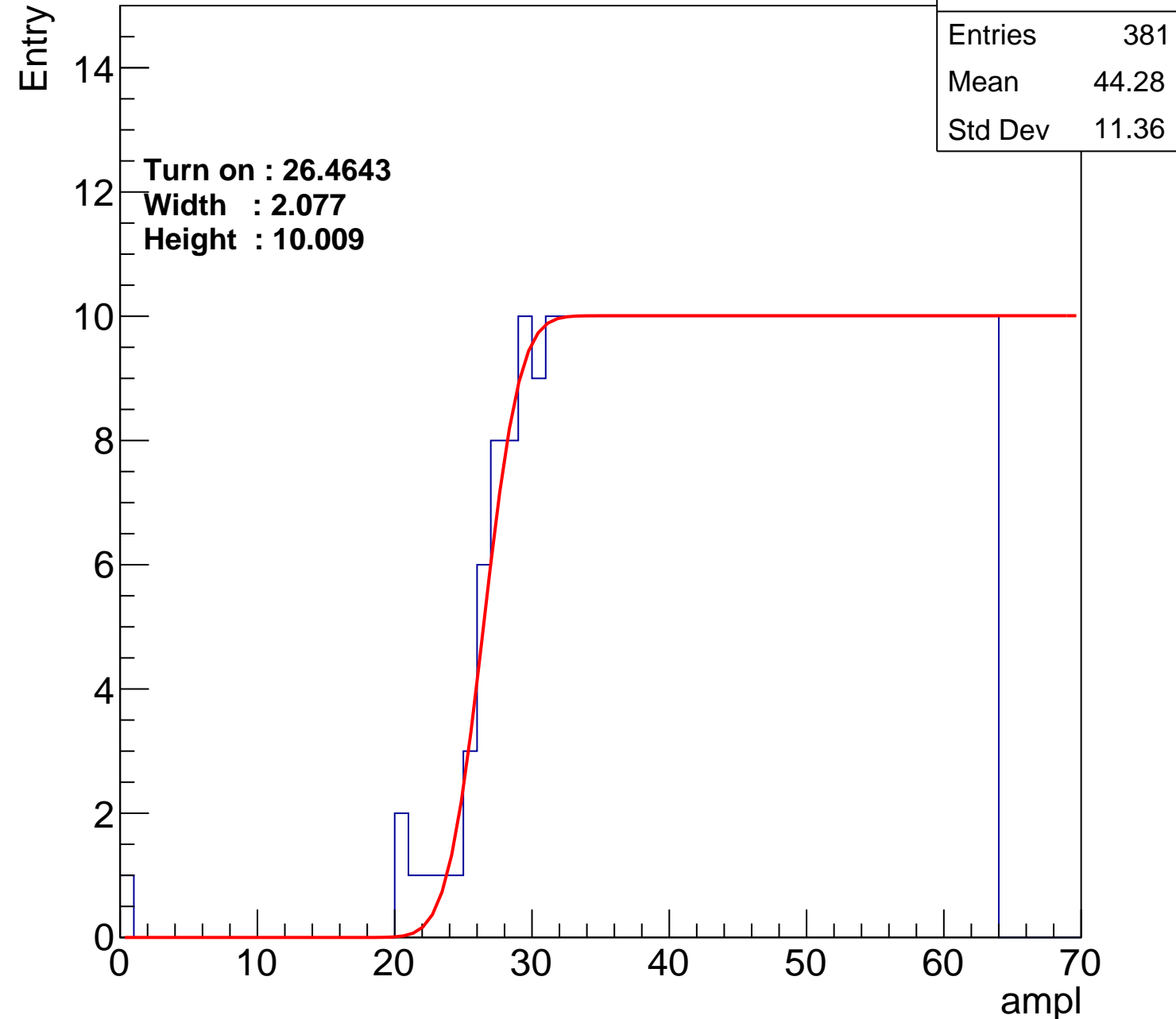
Width : 2.077

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch35

calib_packv5_042523_0143.root, FC#0, port D2

Entries	376
Mean	44.56
Std Dev	11.16

Turn on : 26.2575

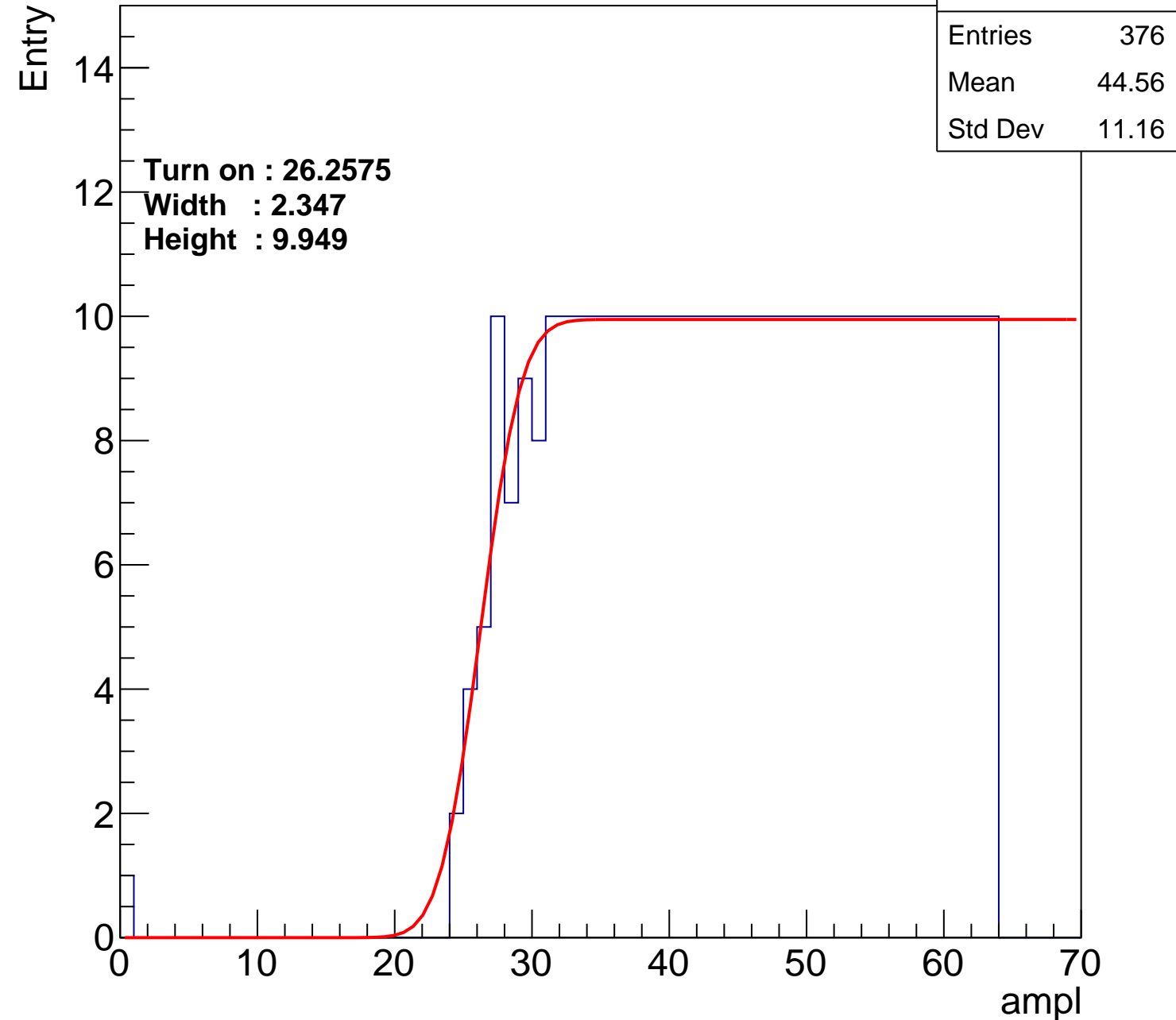
Width : 2.347

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch36

calib_packv5_042523_0143.root, FC#0, port D2

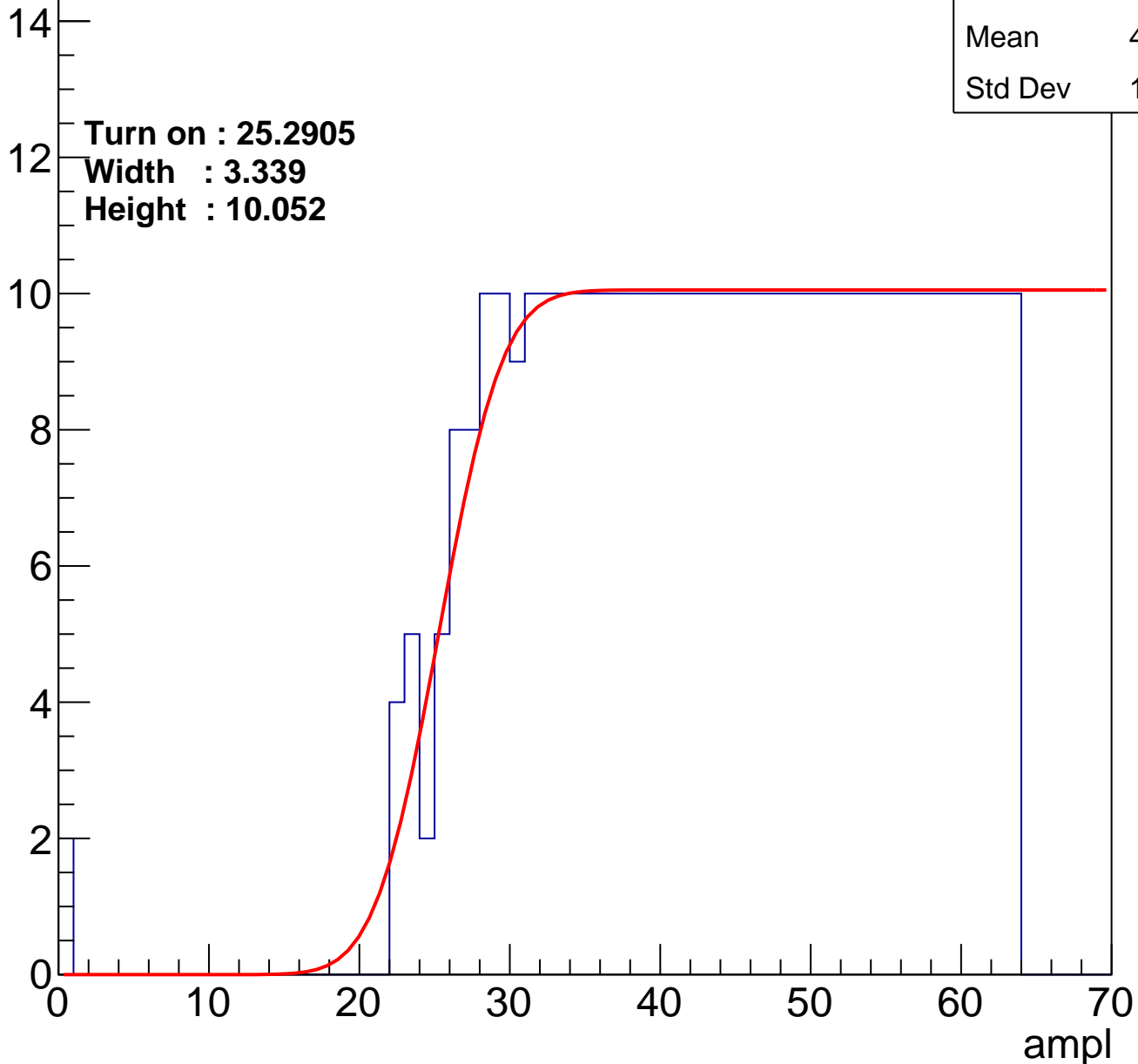
Entries	393
Mean	43.64
Std Dev	11.82

Turn on : 25.2905

Width : 3.339

Height : 10.052

Entry



B1L101S, U23-ch37

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 27.6144

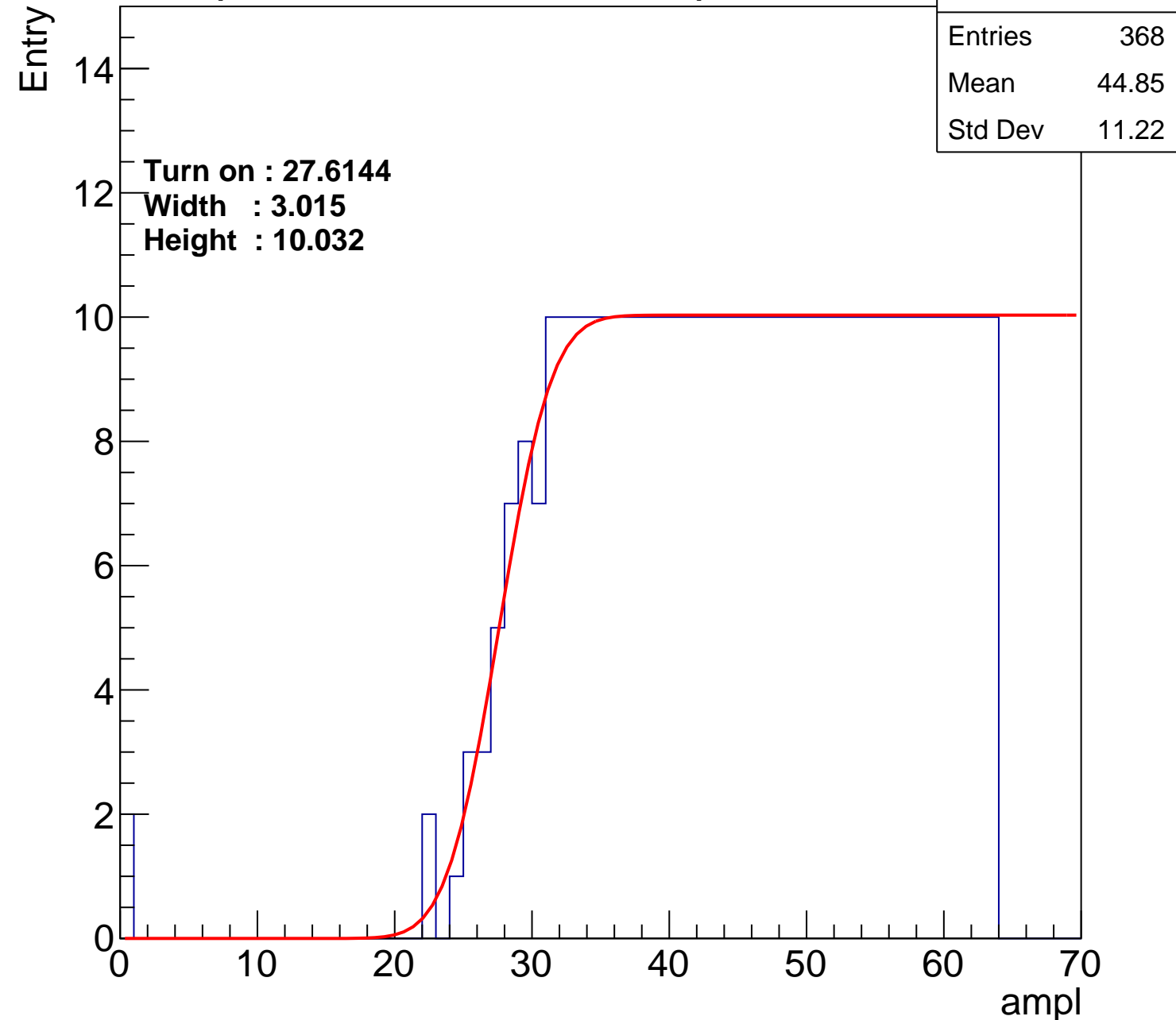
Width : 3.015

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch38

calib_packv5_042523_0143.root, FC#0, port D2

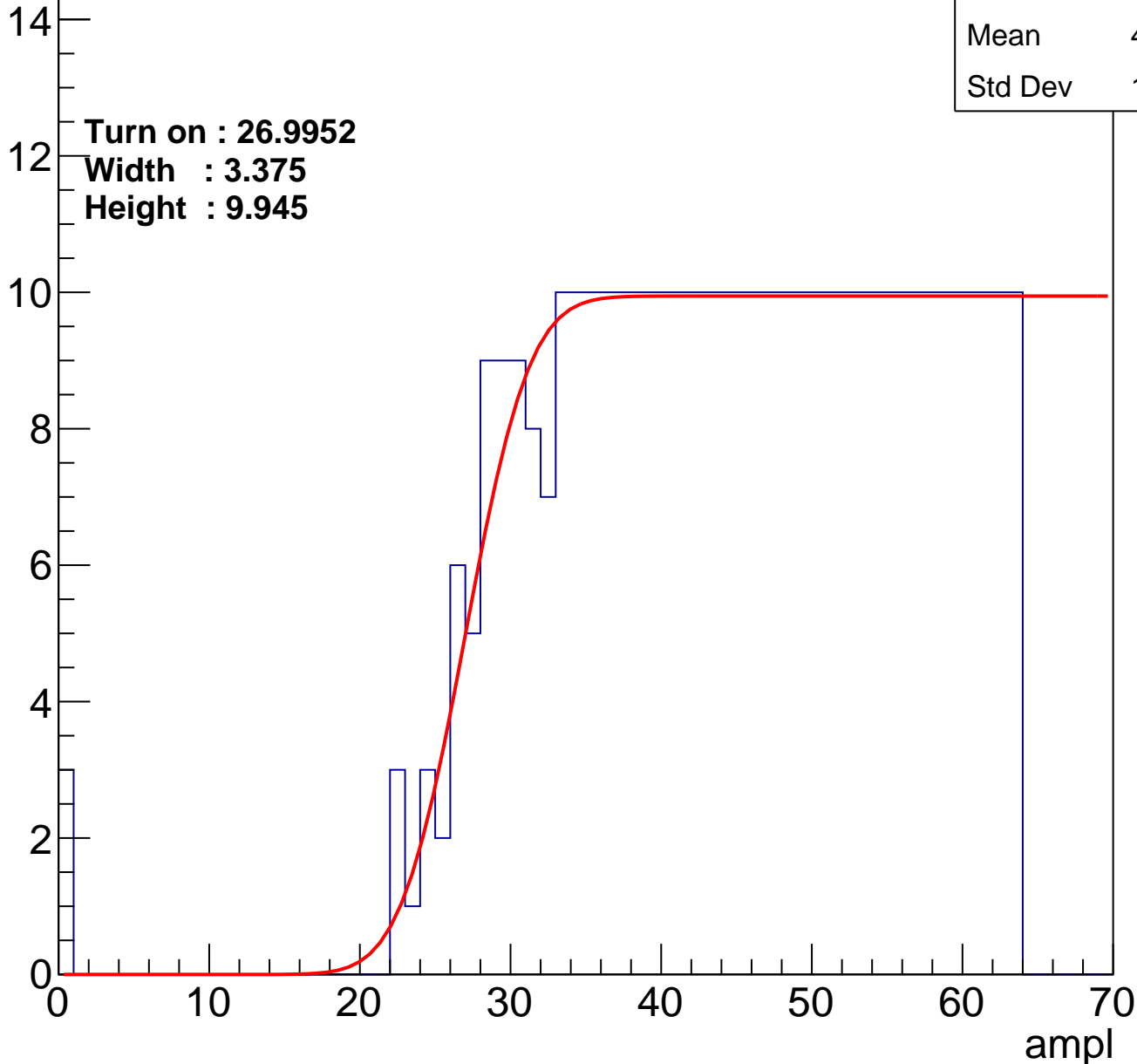
Entries	375
Mean	44.37
Std Dev	11.68

Turn on : 26.9952

Width : 3.375

Height : 9.945

Entry



B1L101S, U23-ch39

calib_packv5_042523_0143.root, FC#0, port D2

Entries	365
Mean	45.05
Std Dev	10.97

Turn on : 27.6832

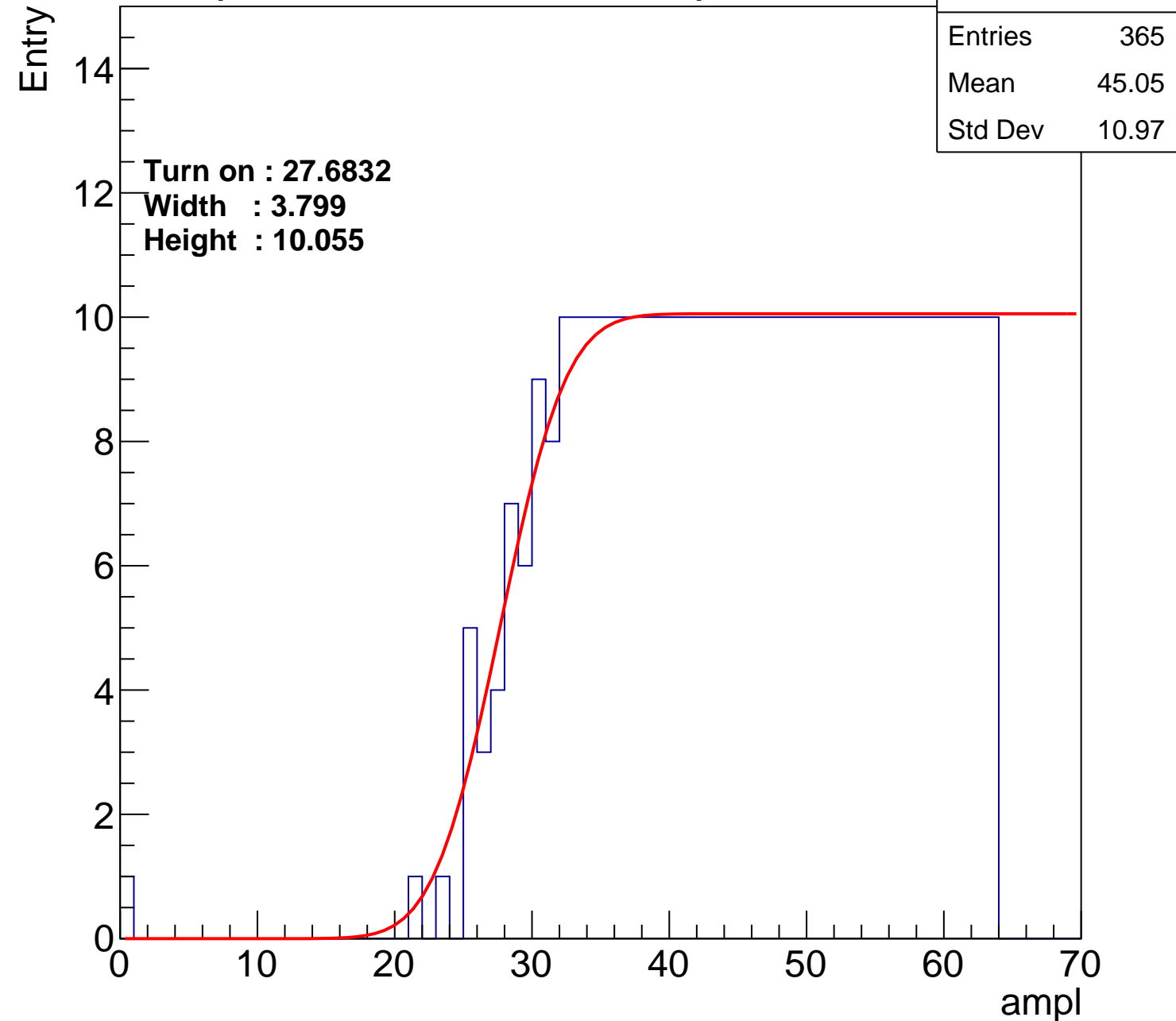
Width : 3.799

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch40

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.41
Std Dev	11.74

Turn on : 27.4367

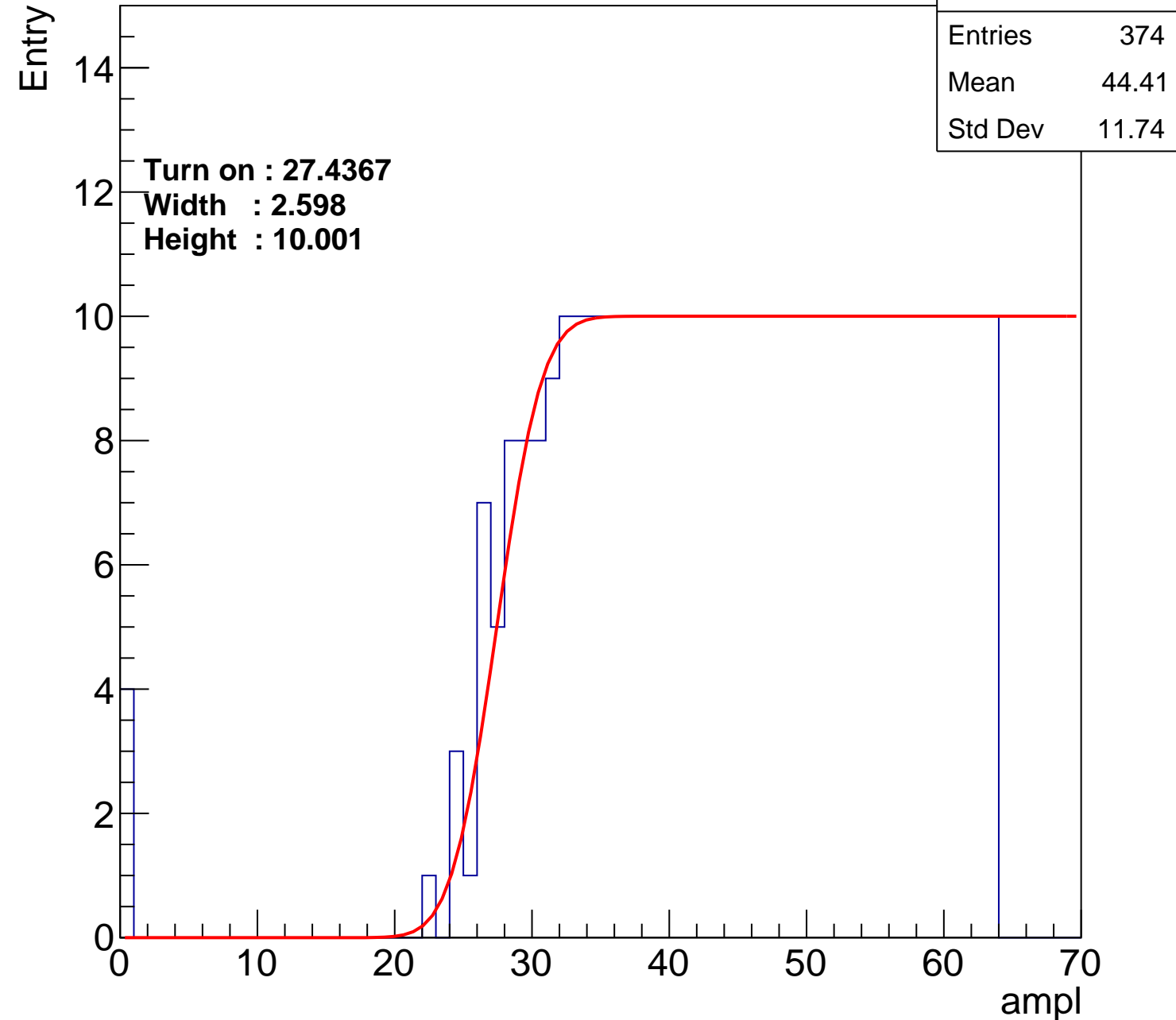
Width : 2.598

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch41

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.62
Std Dev	11.5

Turn on : 27.4056

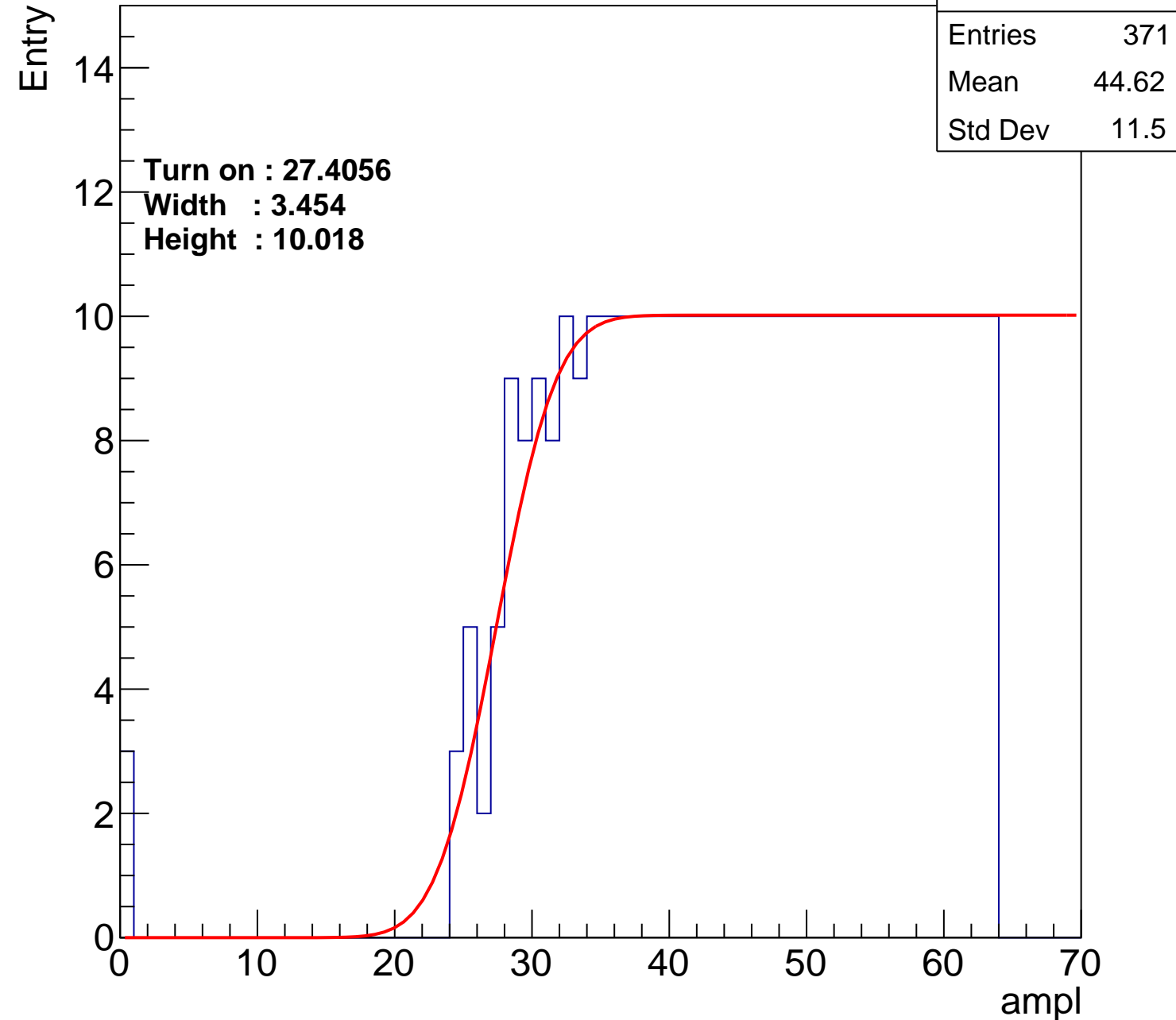
Width : 3.454

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch42

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.5700

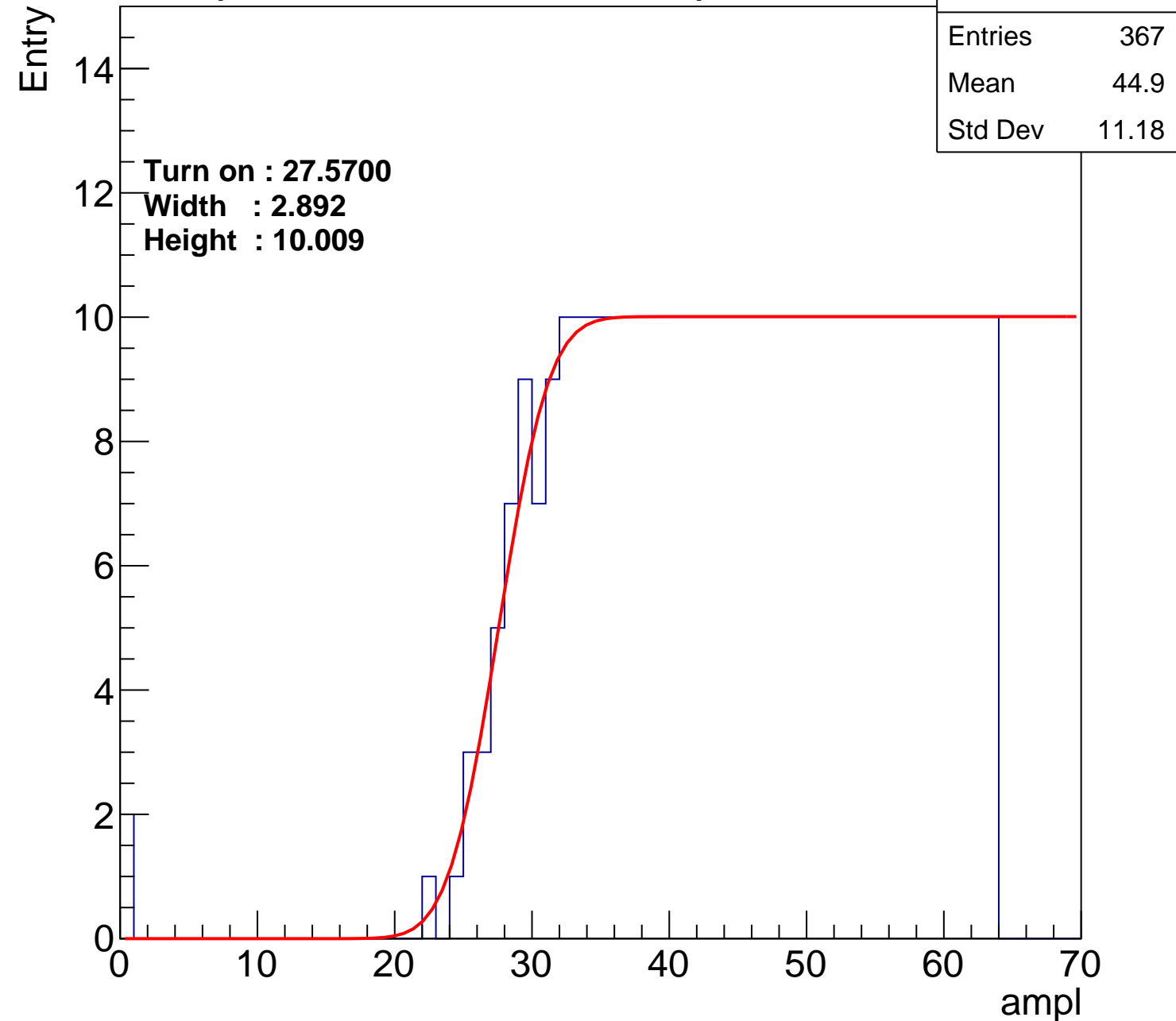
Width : 2.892

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch43

calib_packv5_042523_0143.root, FC#0, port D2

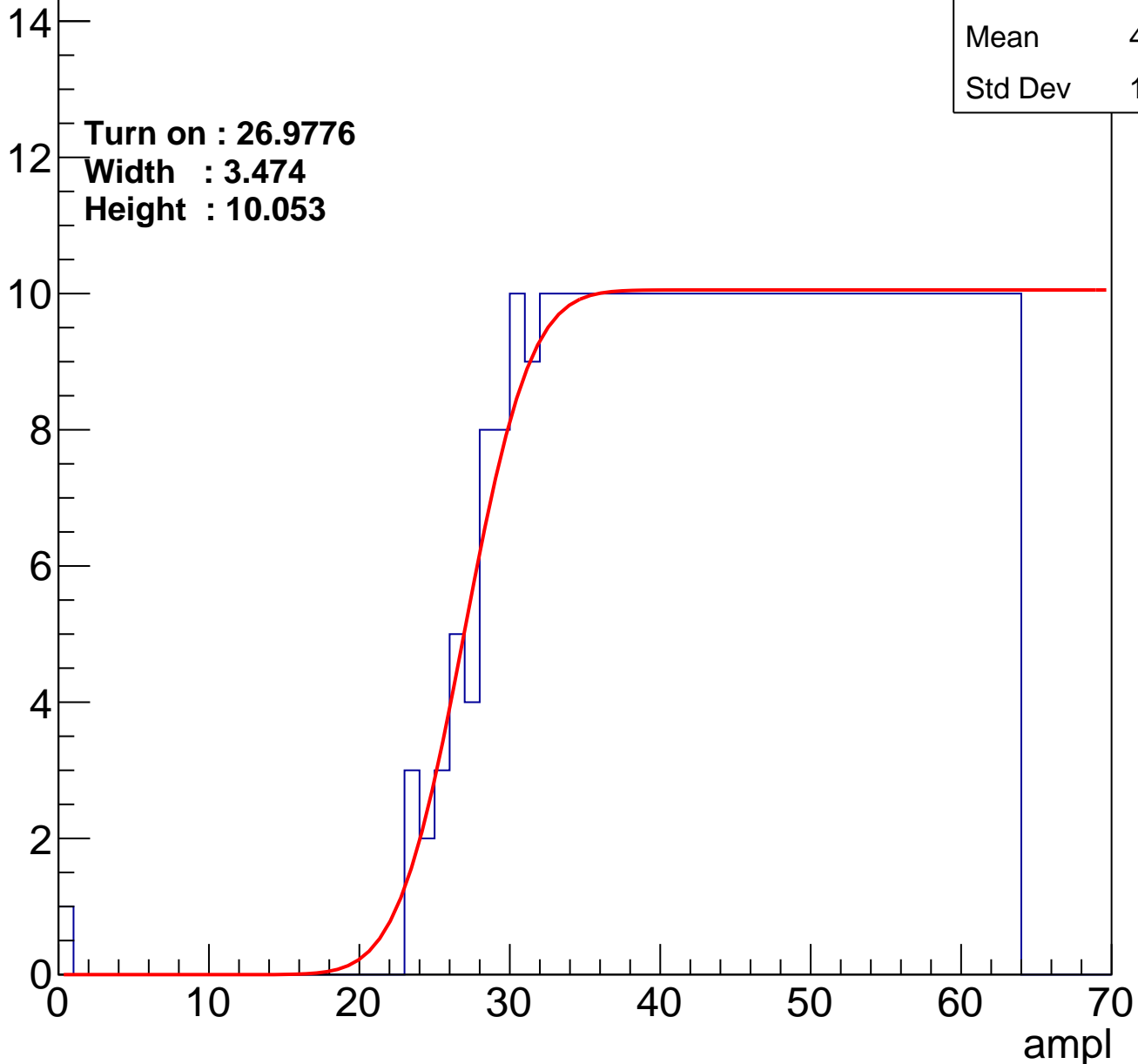
Entries	373
Mean	44.68
Std Dev	11.14

Turn on : 26.9776

Width : 3.474

Height : 10.053

Entry



B1L101S, U23-ch44

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.79
Std Dev	11.26

Turn on : 27.4611

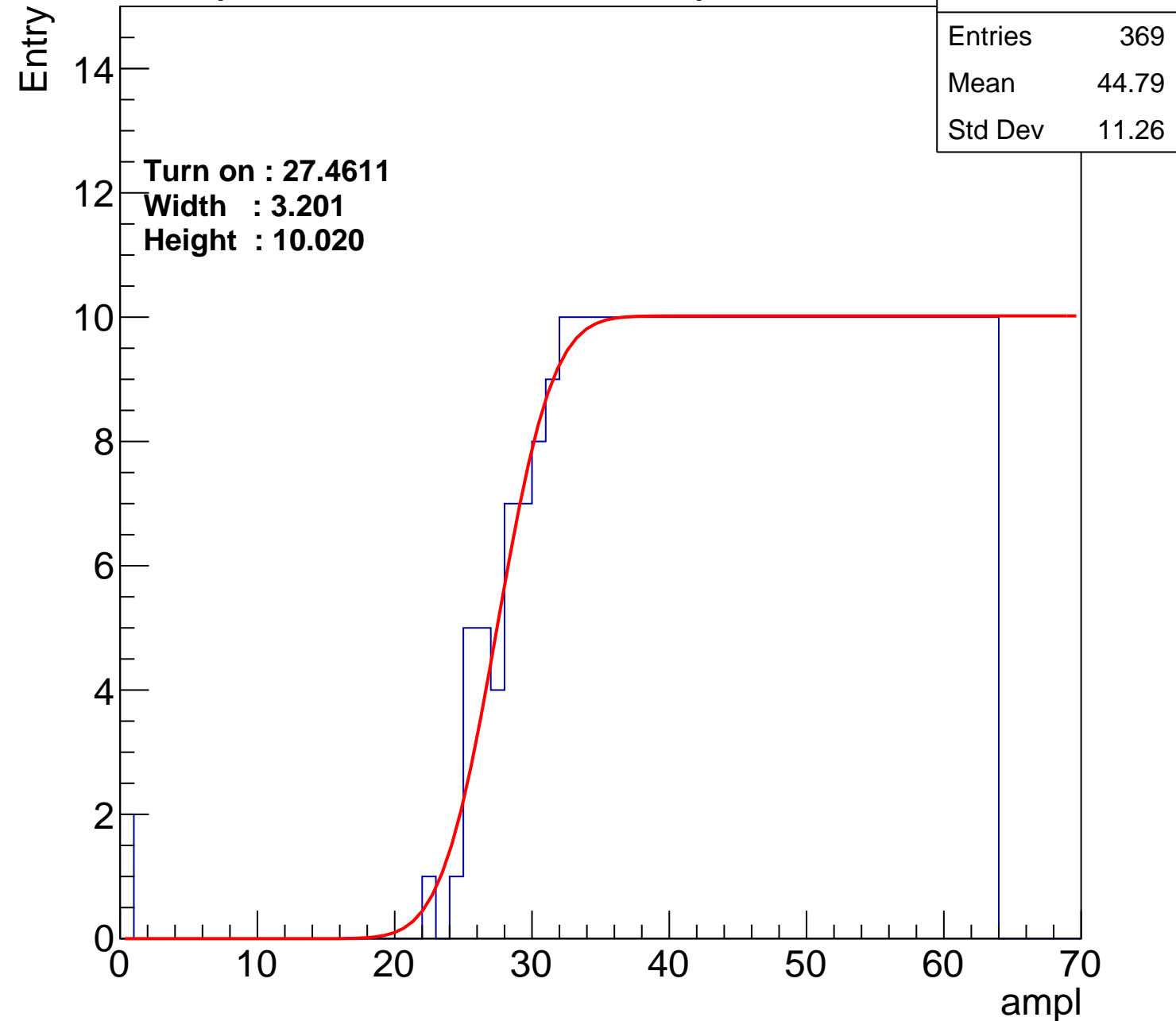
Width : 3.201

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch45

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.73
Std Dev	11.24

Turn on : 27.3946

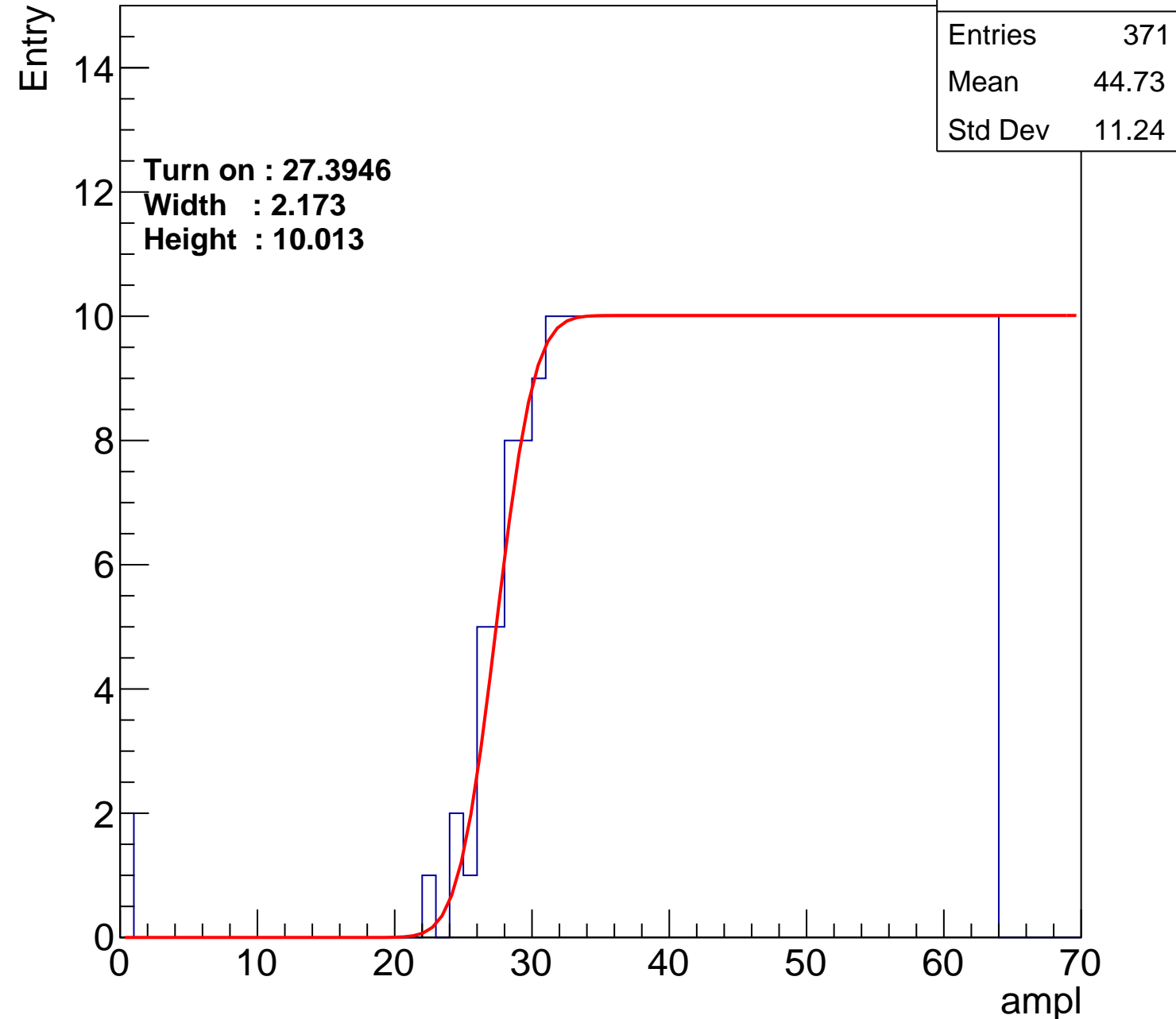
Width : 2.173

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch46

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.5
Std Dev	11.54

Turn on : 26.9804

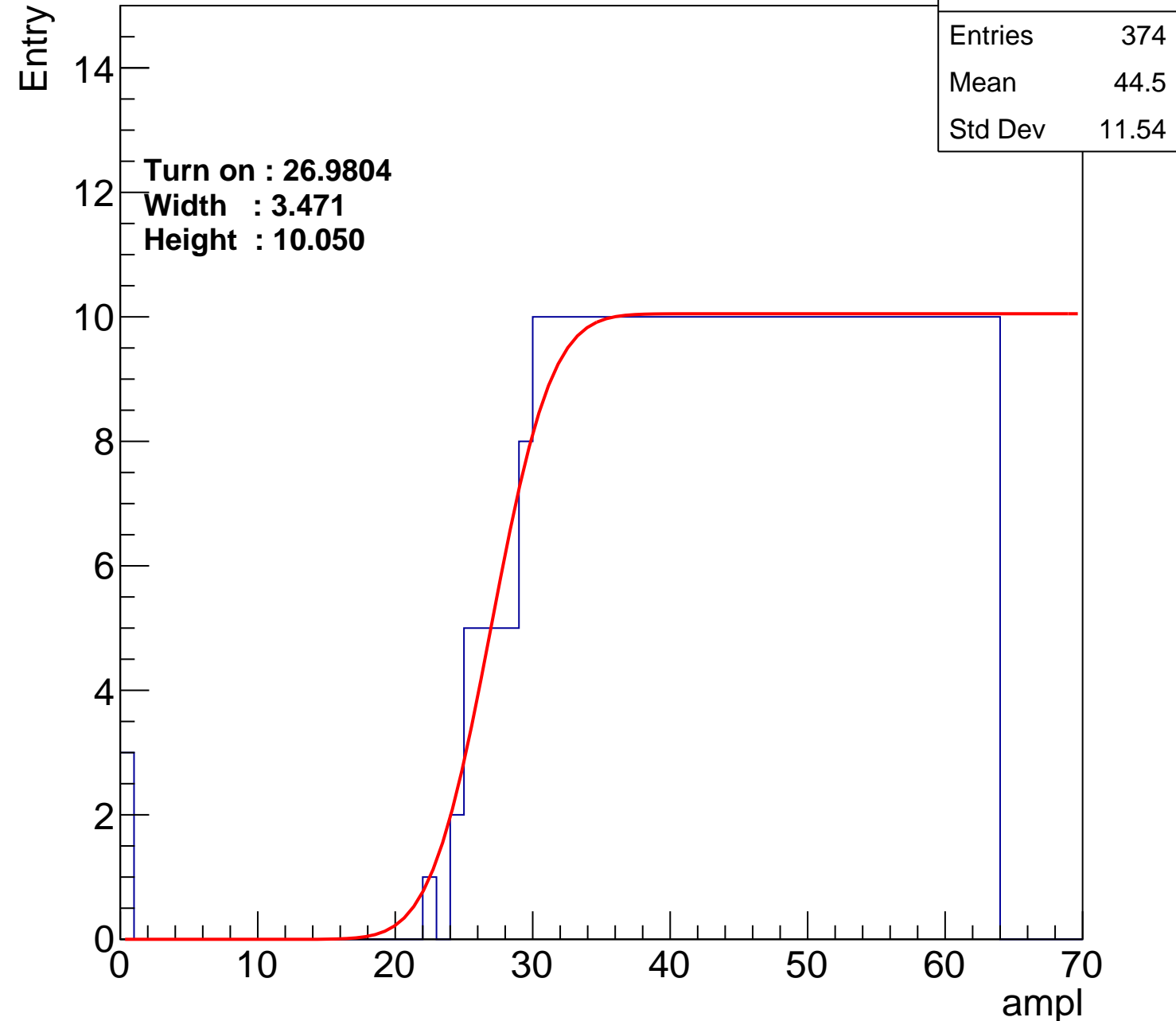
Width : 3.471

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch47

calib_packv5_042523_0143.root, FC#0, port D2

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 26.6388

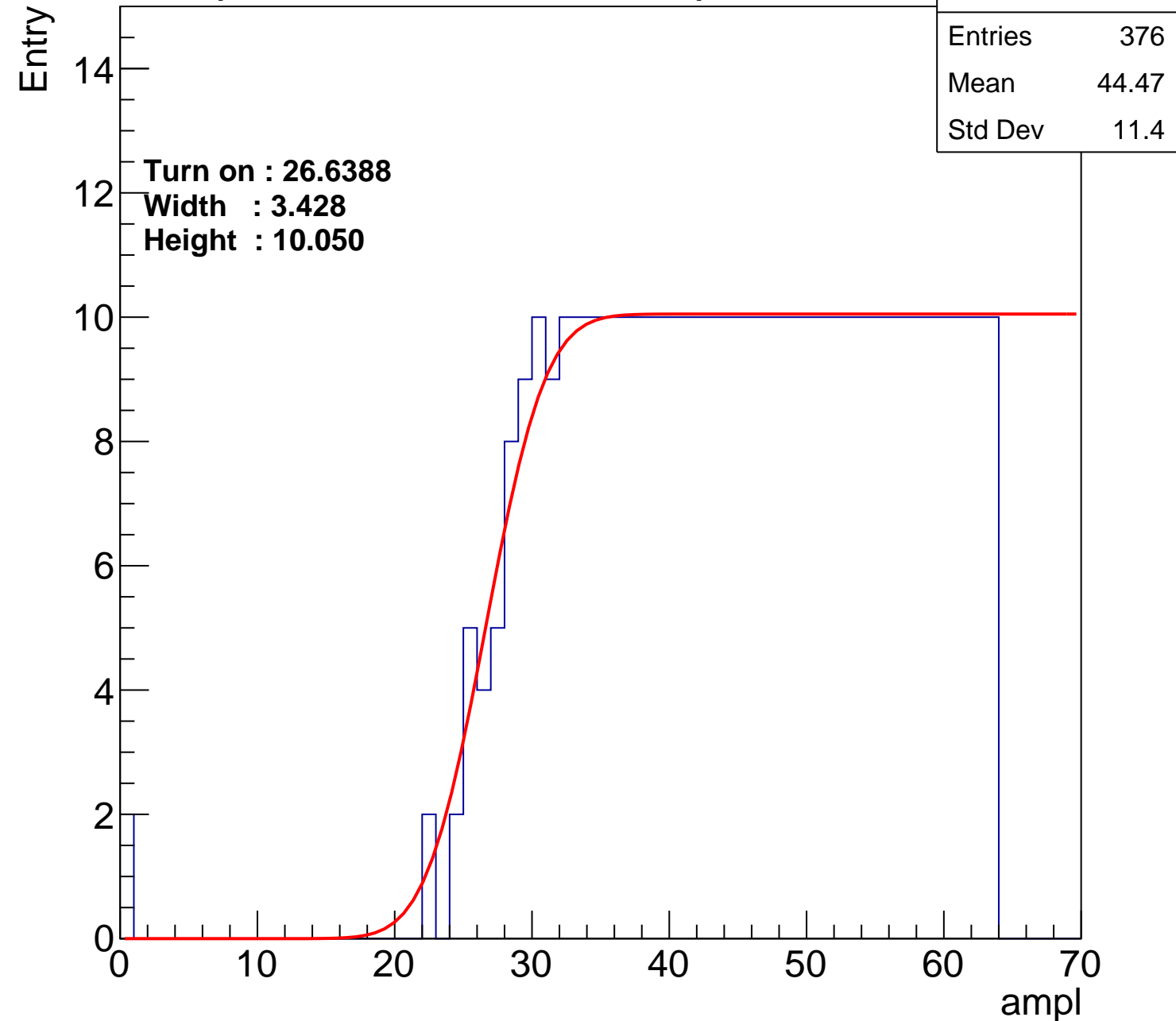
Width : 3.428

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch48

calib_packv5_042523_0143.root, FC#0, port D2

Entries	362
Mean	44.98
Std Dev	11.5

Turn on : 28.7745

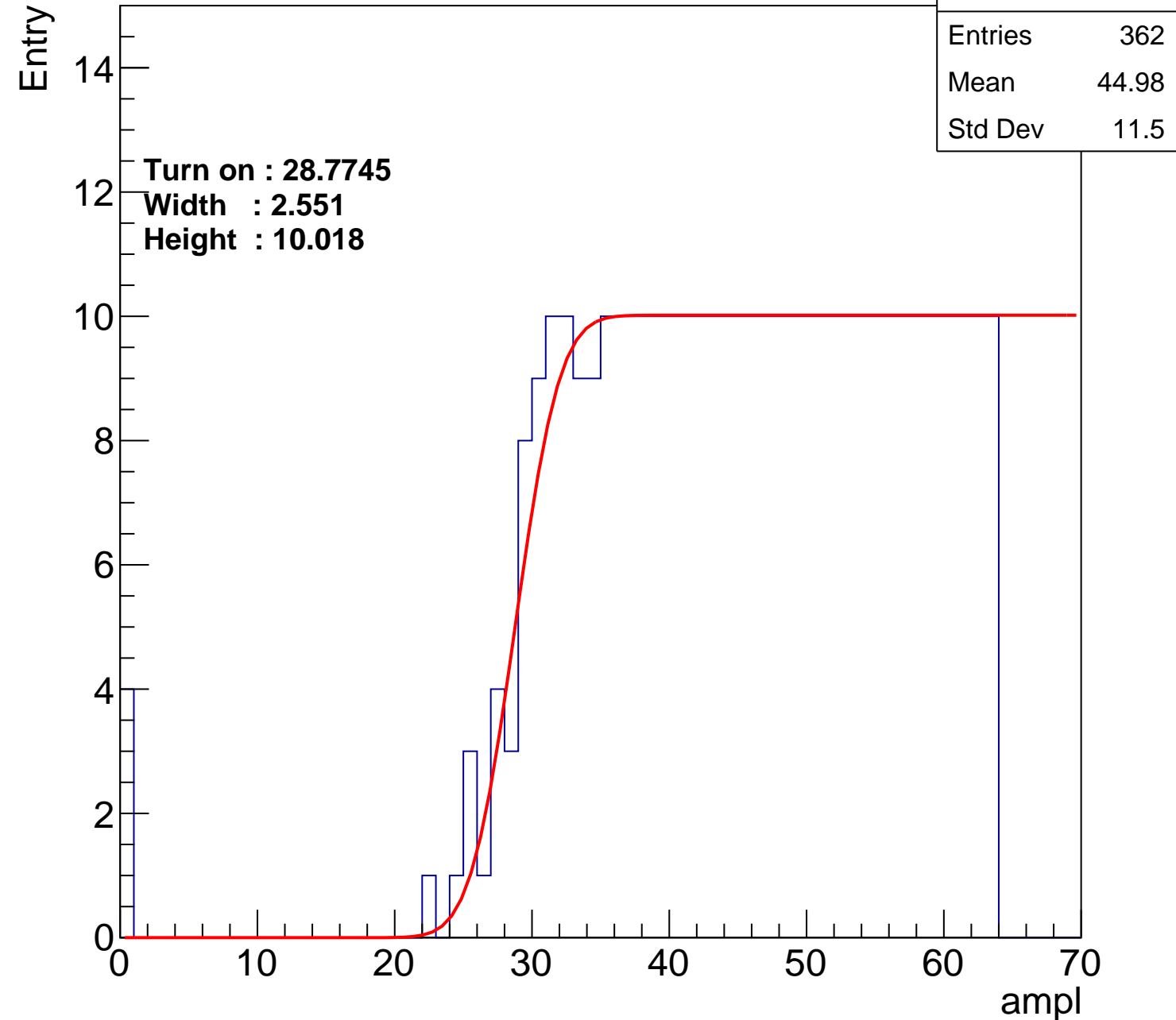
Width : 2.551

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch49

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.77
Std Dev	11.05

Turn on : 27.1501

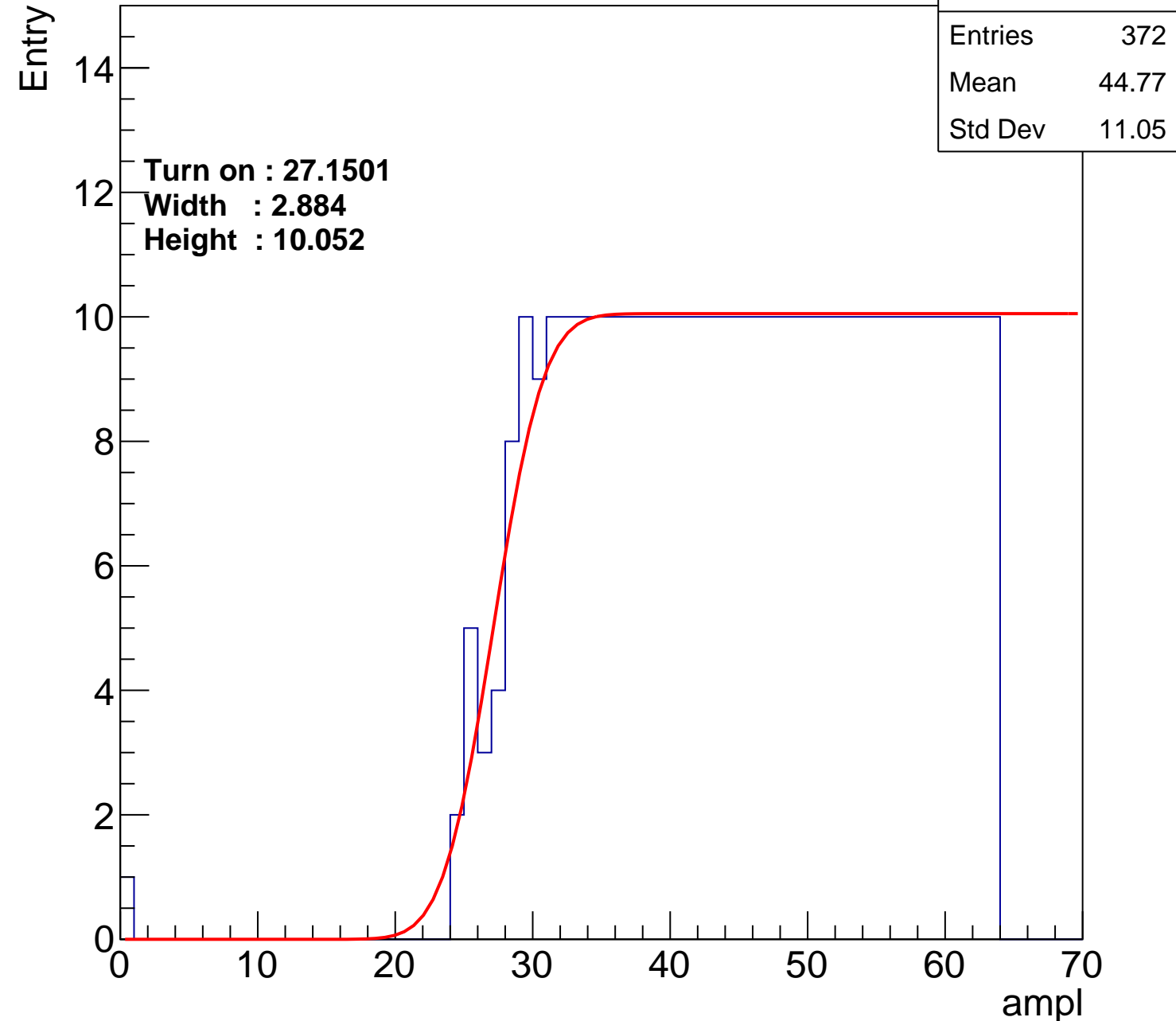
Width : 2.884

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch50

calib_packv5_042523_0143.root, FC#0, port D2

Entries	364
Mean	45.03
Std Dev	11.14

Turn on : 27.8983

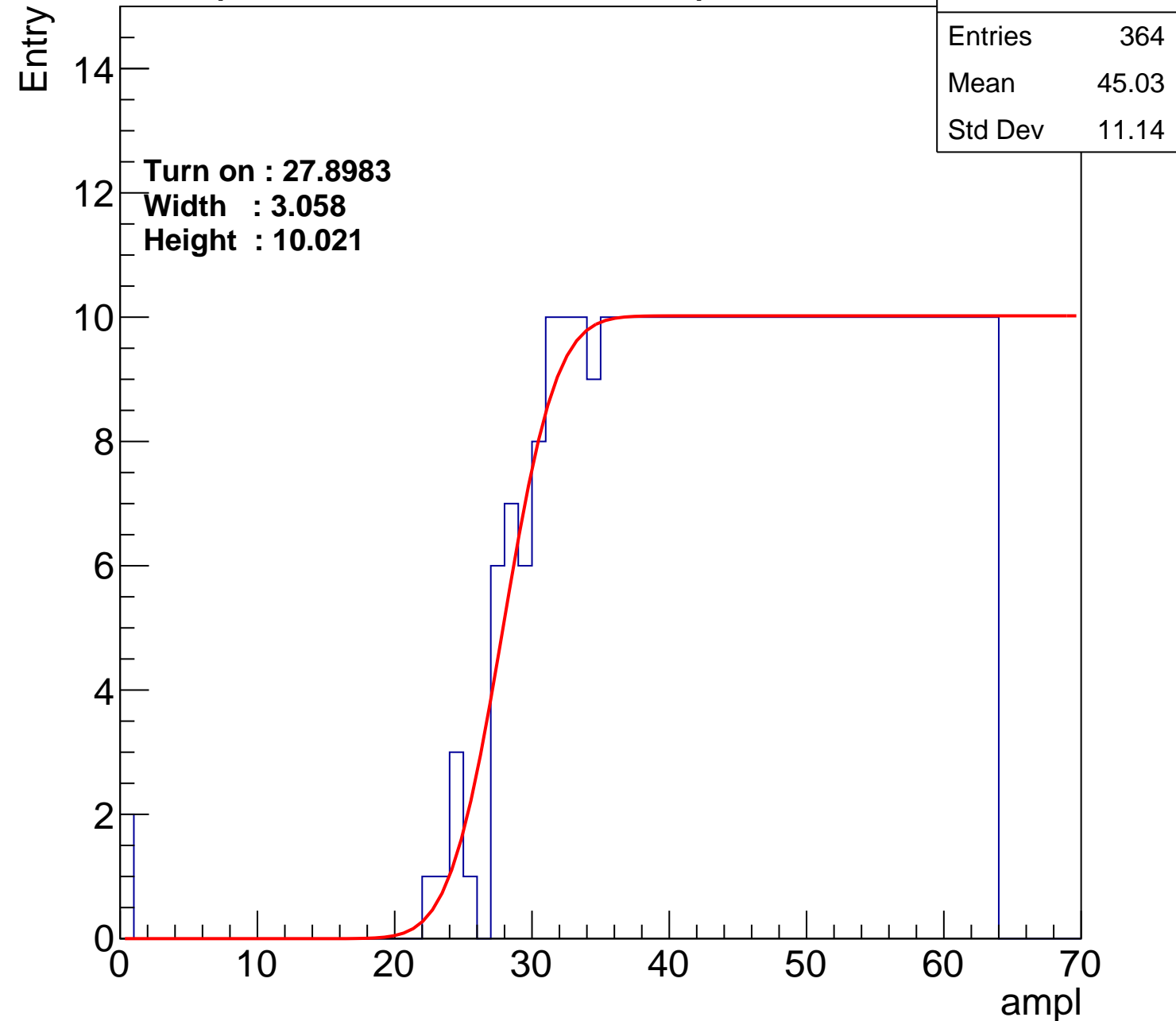
Width : 3.058

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch51

calib_packv5_042523_0143.root, FC#0, port D2

Entries	345
Mean	45.71
Std Dev	11.27

Turn on : 30.1148

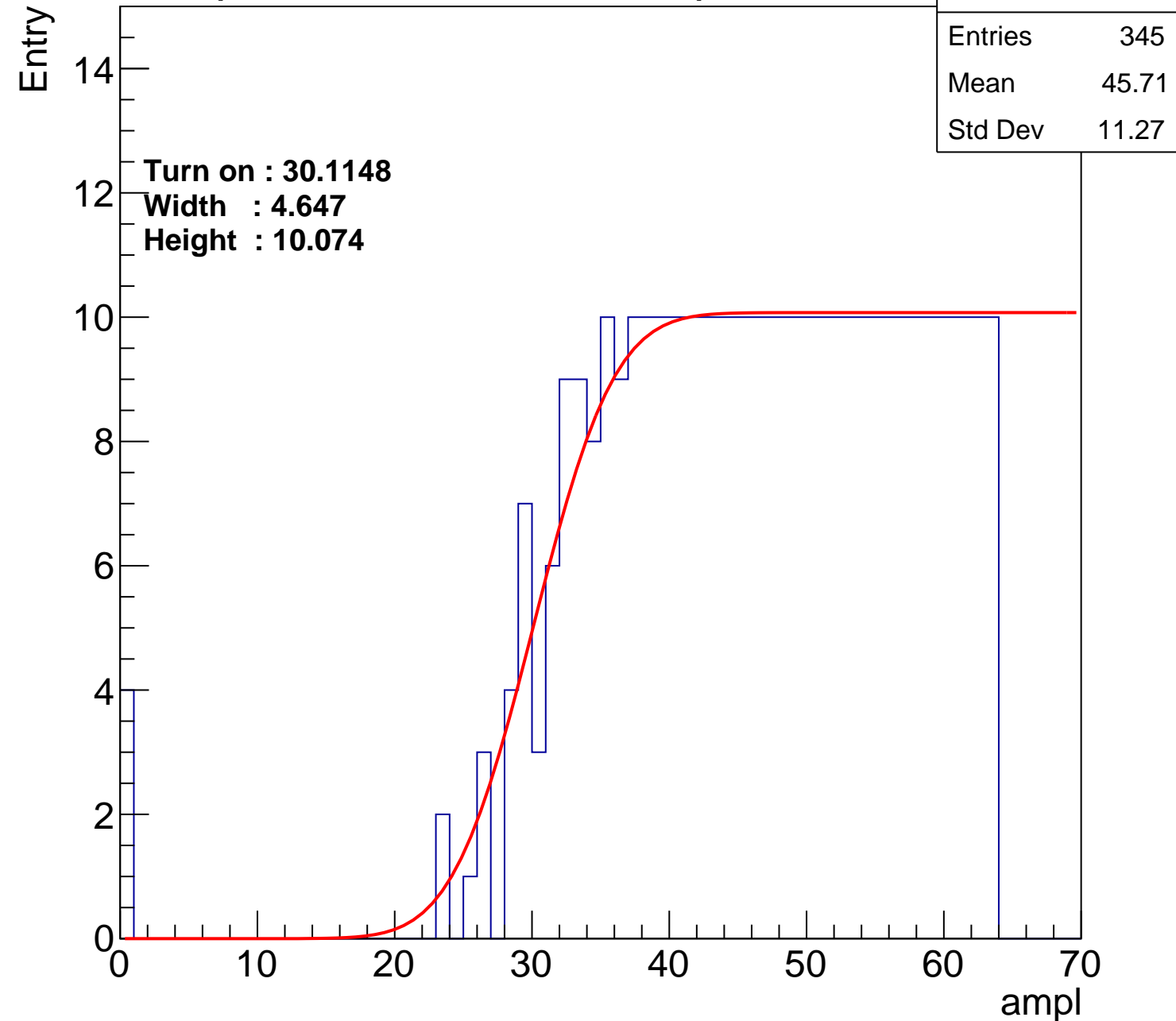
Width : 4.647

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch52

calib_packv5_042523_0143.root, FC#0, port D2

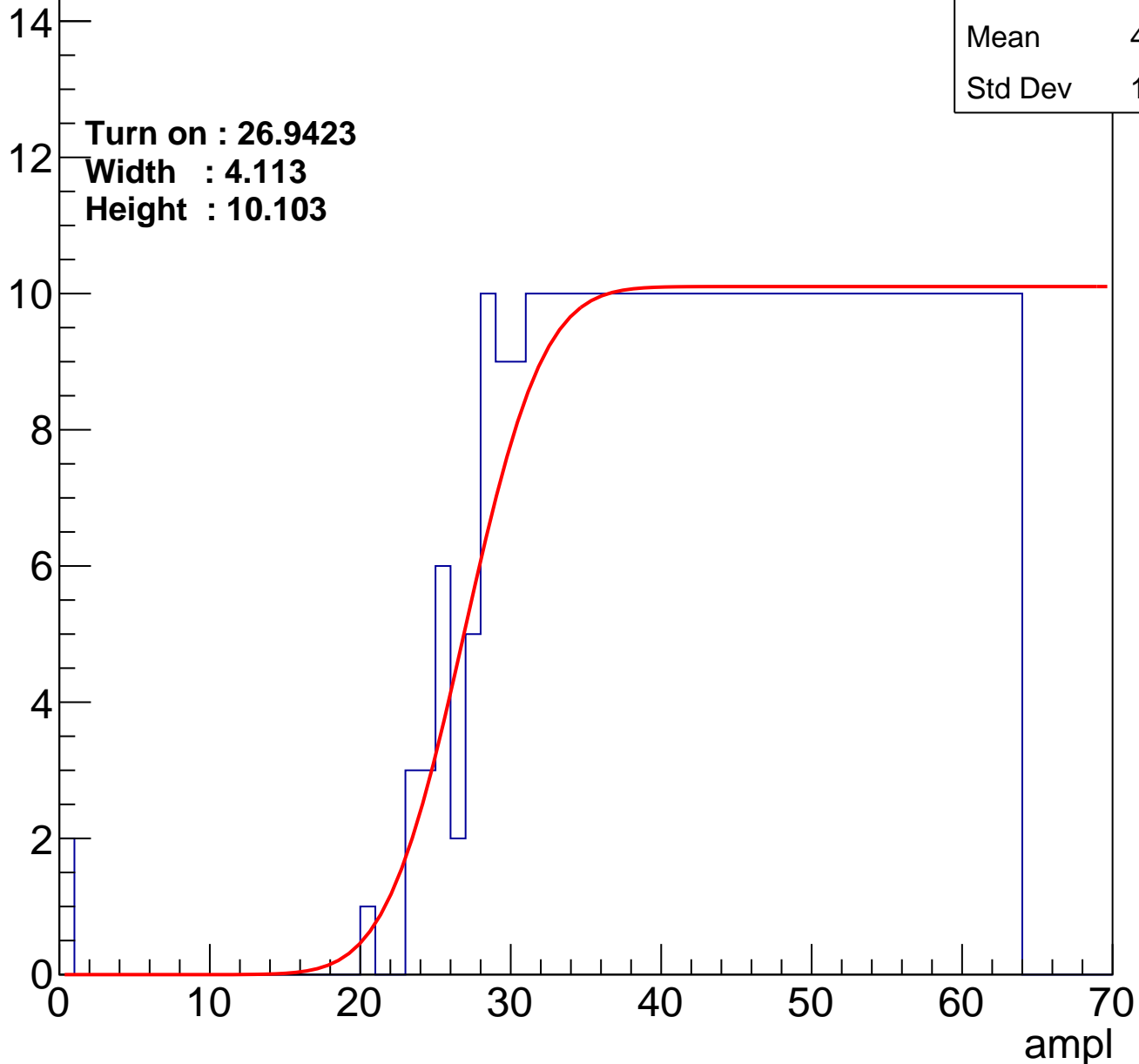
Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.9423

Width : 4.113

Height : 10.103

Entry



B1L101S, U23-ch53

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.9
Std Dev	11.33

Turn on : 27.9829

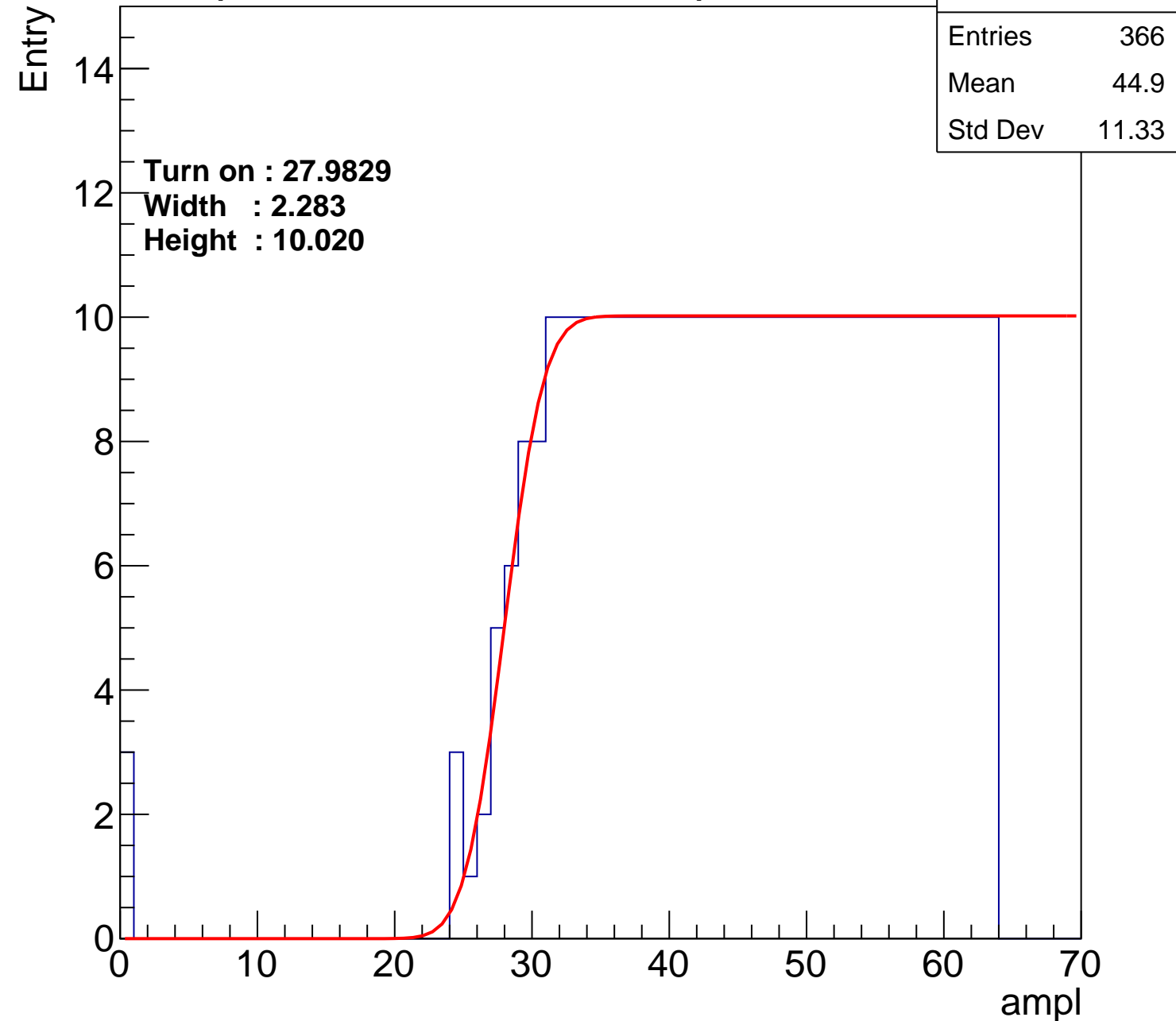
Width : 2.283

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch54

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.76
Std Dev	11.16

Turn on : 27.4799

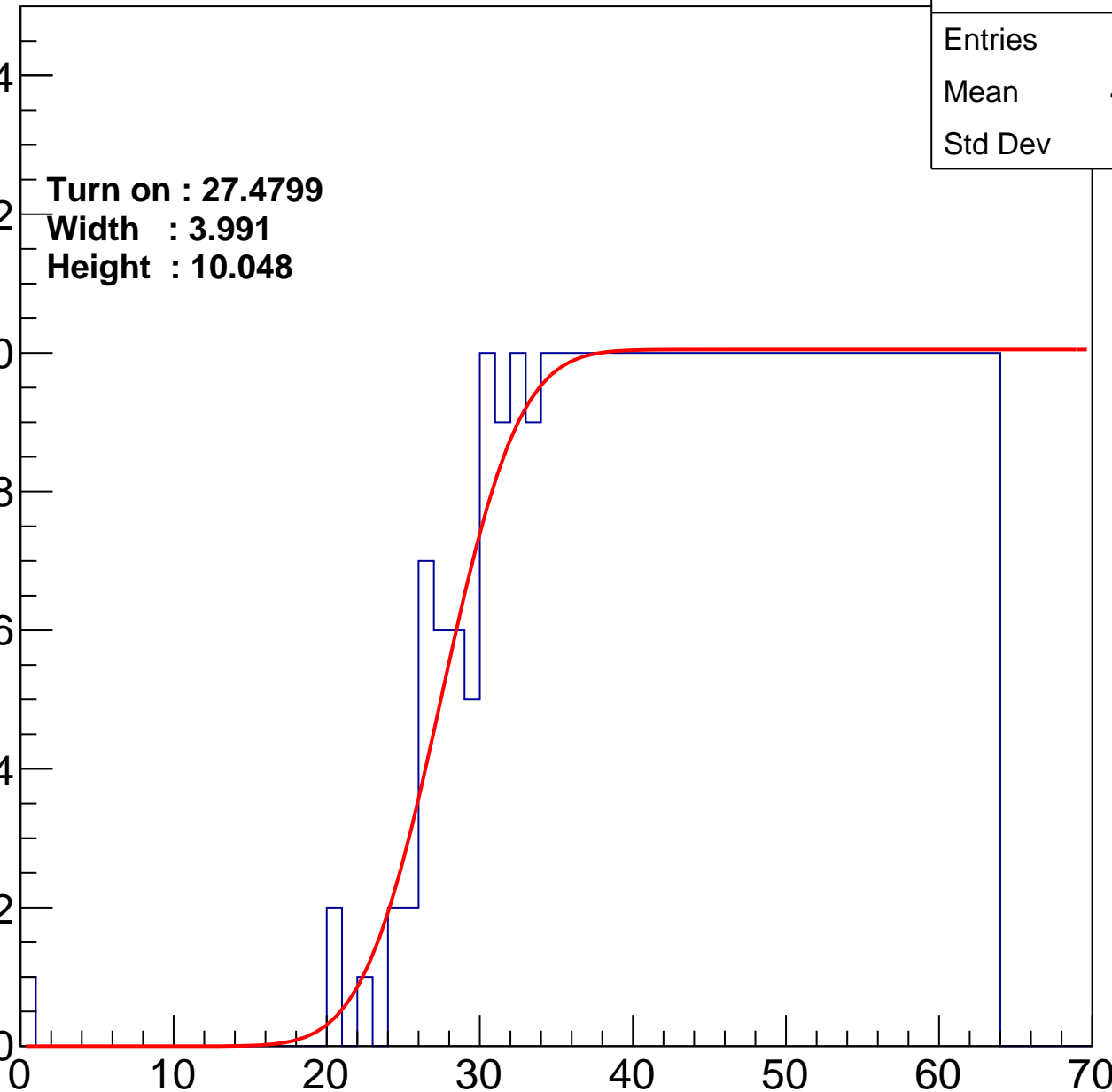
Width : 3.991

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch55

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.54
Std Dev	11.45

Turn on : 27.2109

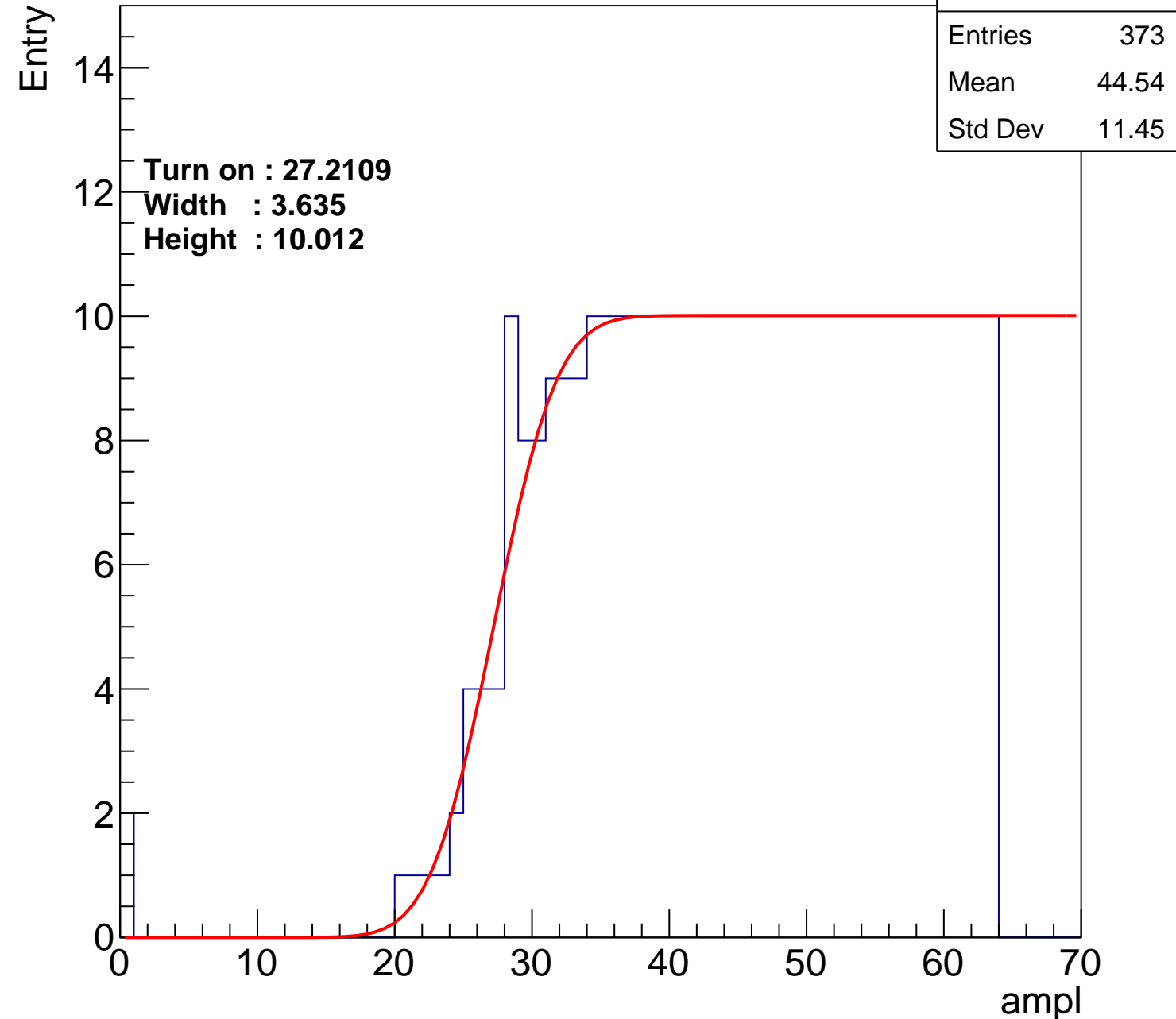
Width : 3.635

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch56

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.46
Std Dev	11.29

Turn on : 26.7158

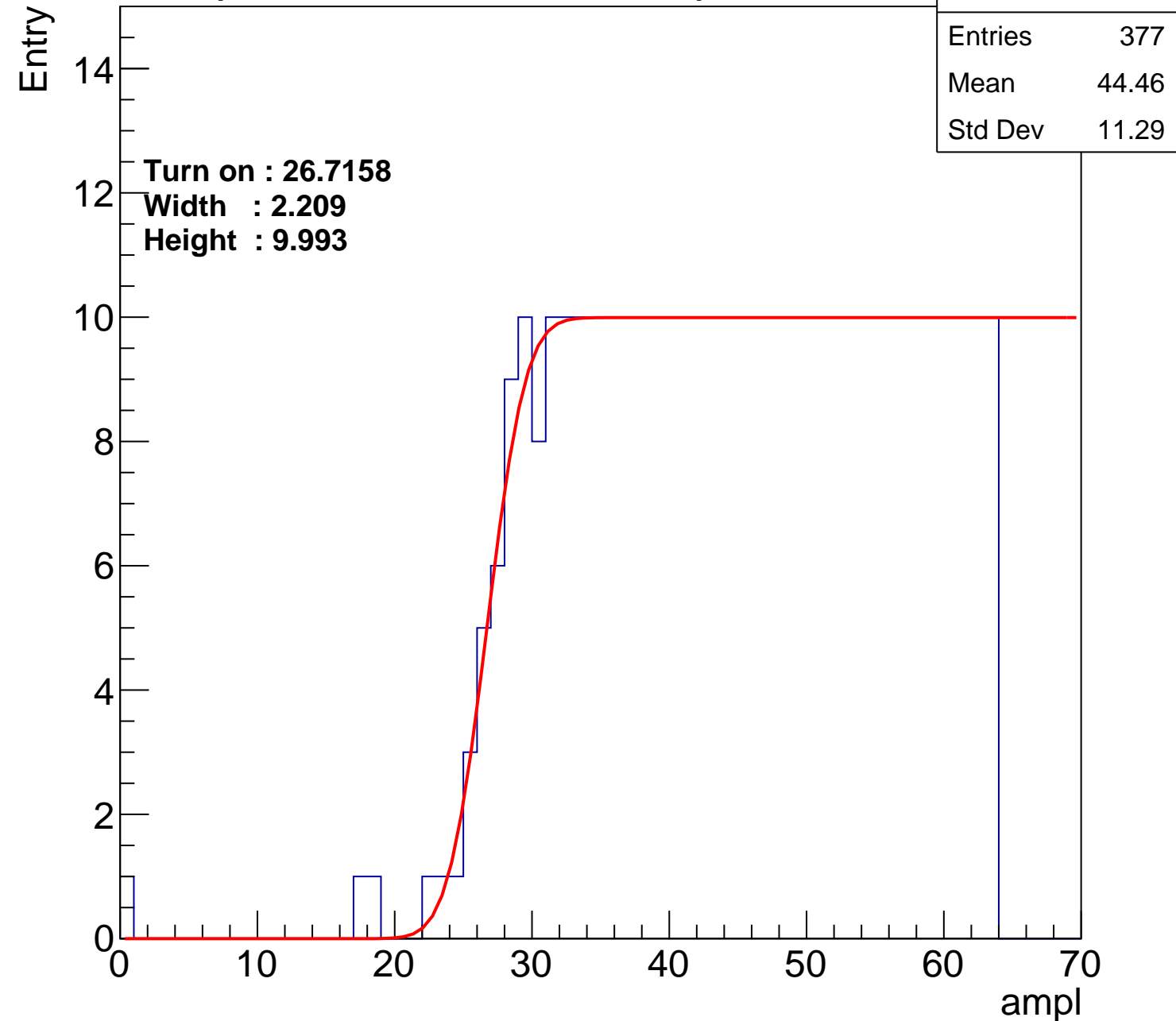
Width : 2.209

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch57

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 27.5615

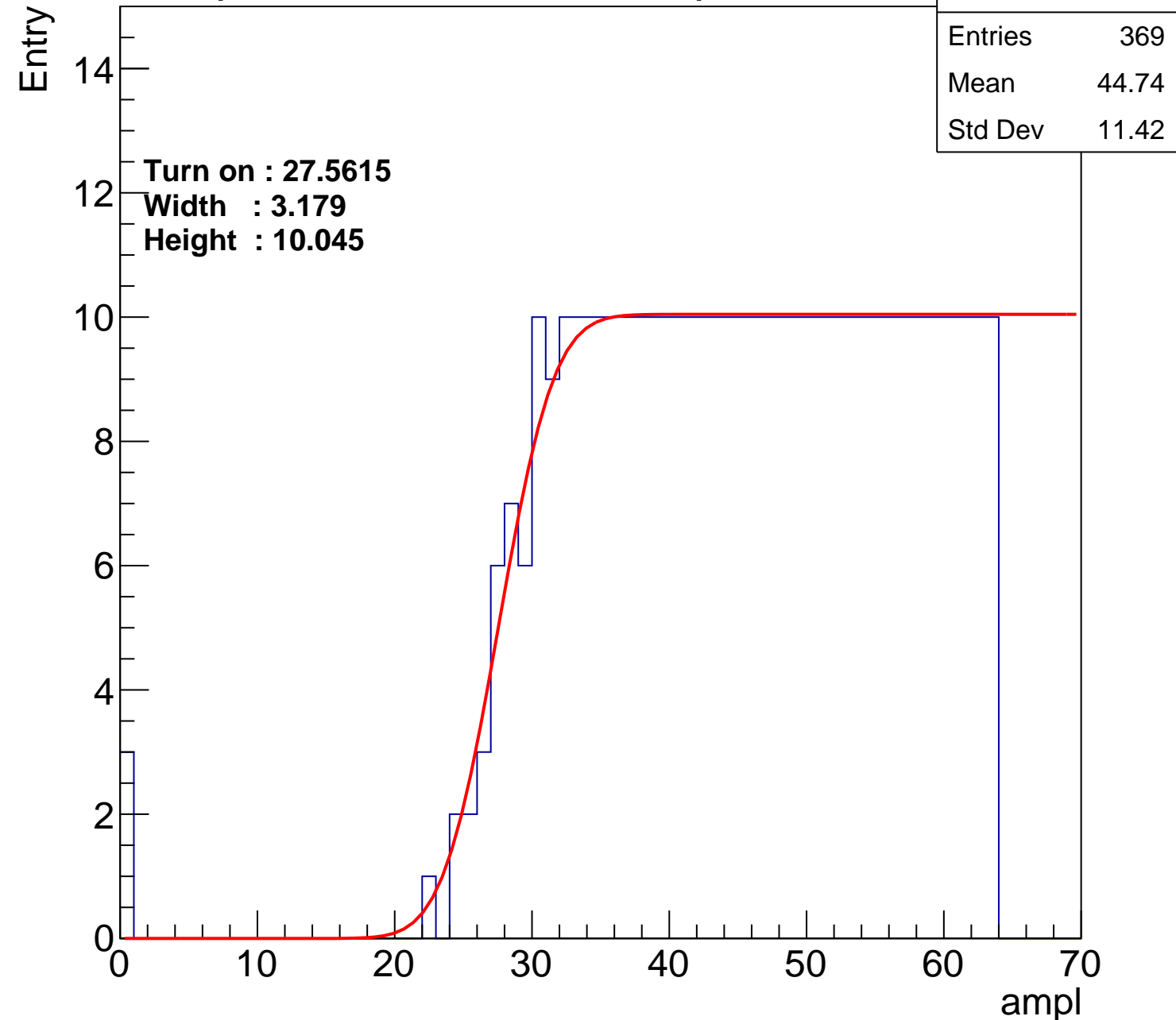
Width : 3.179

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch58

calib_packv5_042523_0143.root, FC#0, port D2

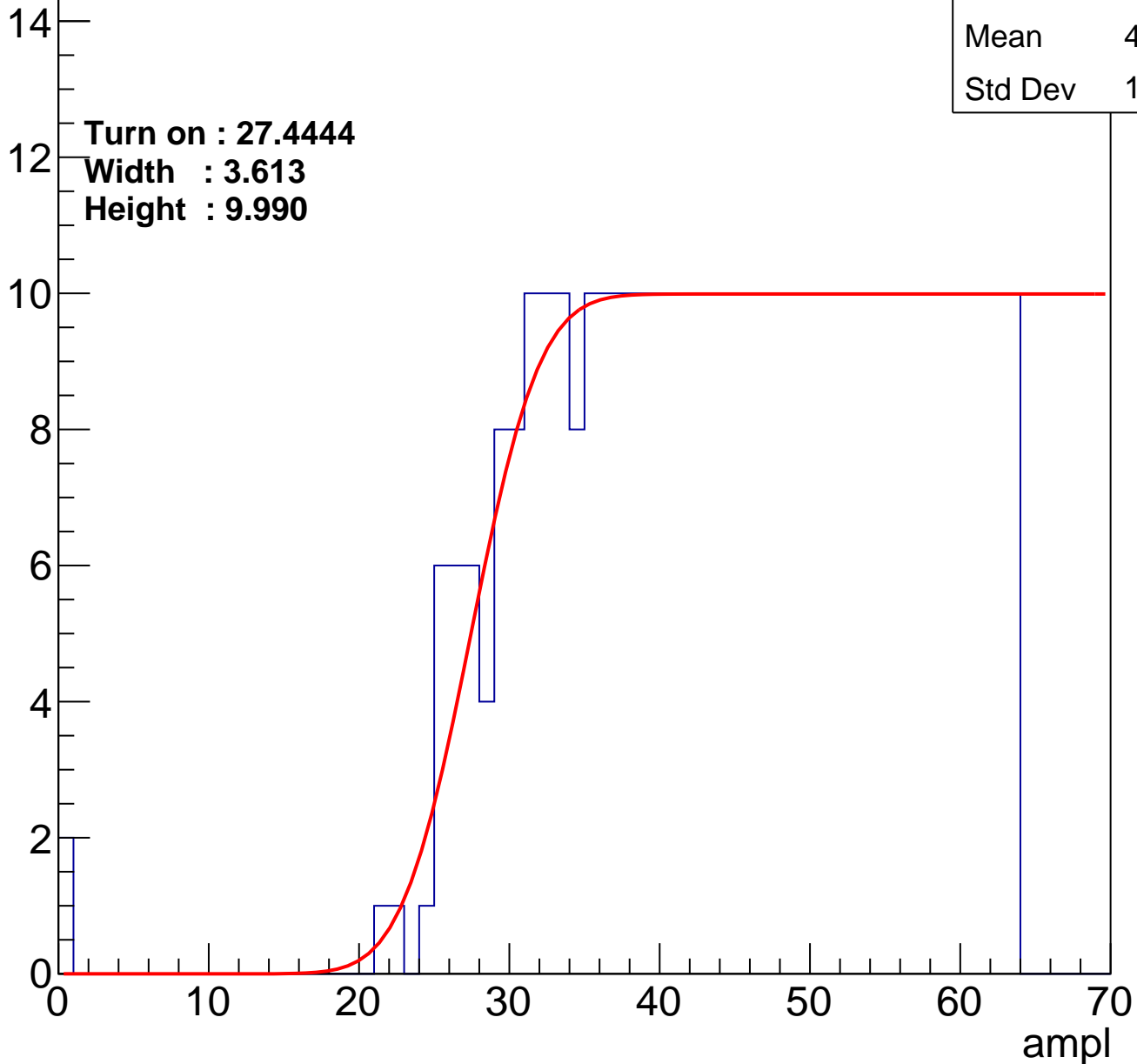
Entries	371
Mean	44.64
Std Dev	11.38

Turn on : 27.4444

Width : 3.613

Height : 9.990

Entry



B1L101S, U23-ch59

calib_packv5_042523_0143.root, FC#0, port D2

Entries	379
Mean	44.23
Std Dev	11.7

Turn on : 26.7861

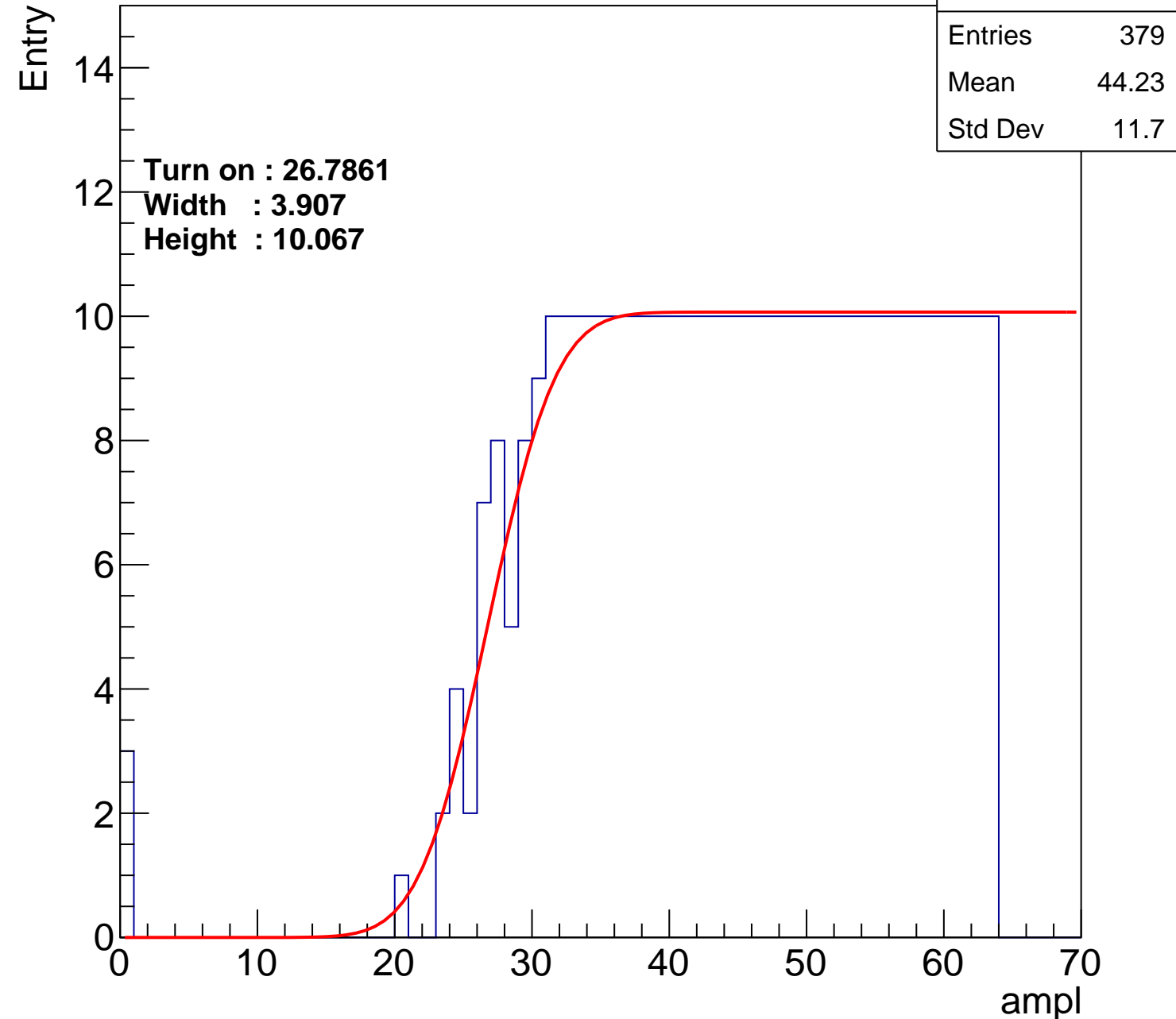
Width : 3.907

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch60

calib_packv5_042523_0143.root, FC#0, port D2

Entries	378
Mean	44.31
Std Dev	11.63

Turn on : 26.8277

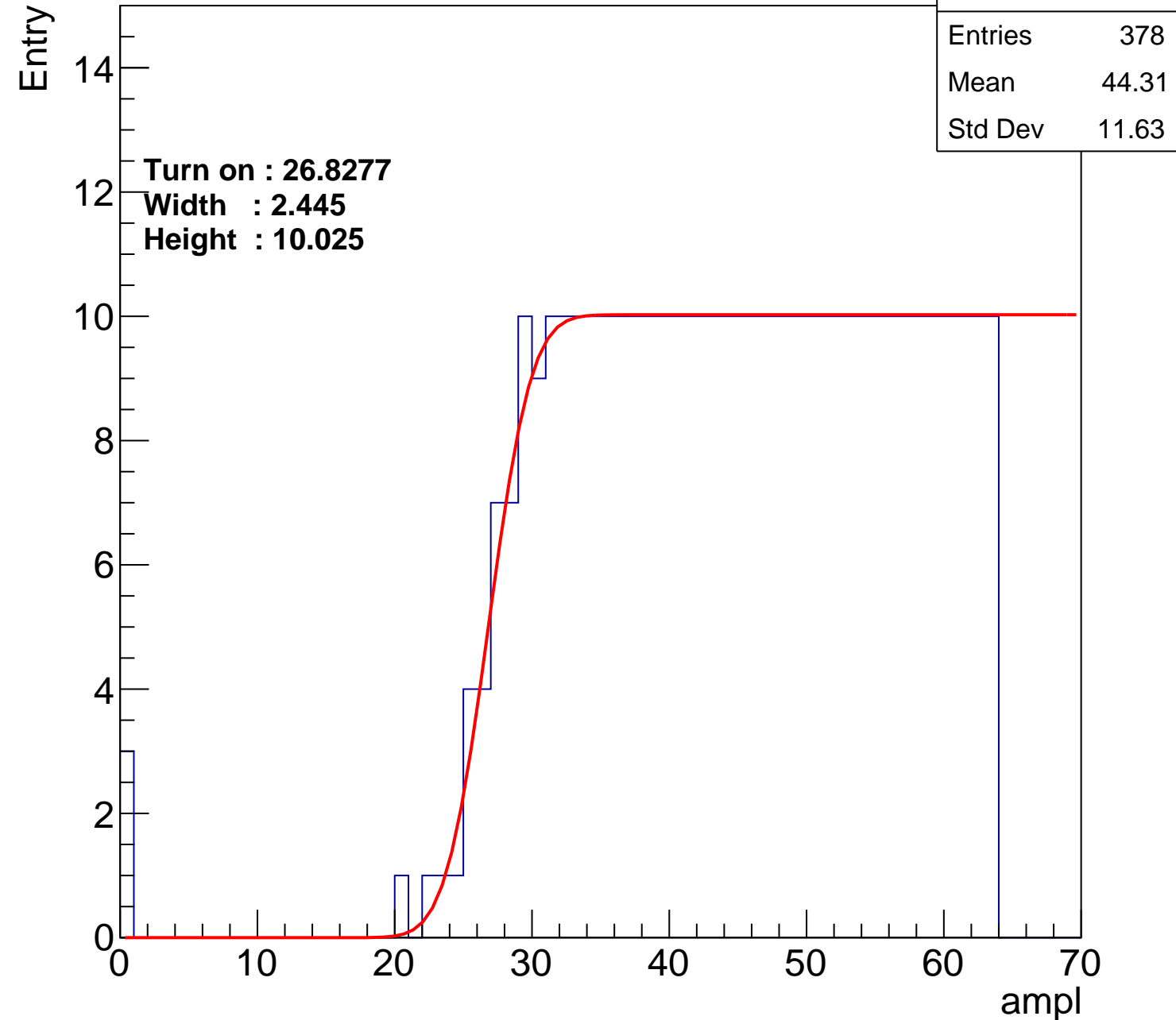
Width : 2.445

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch61

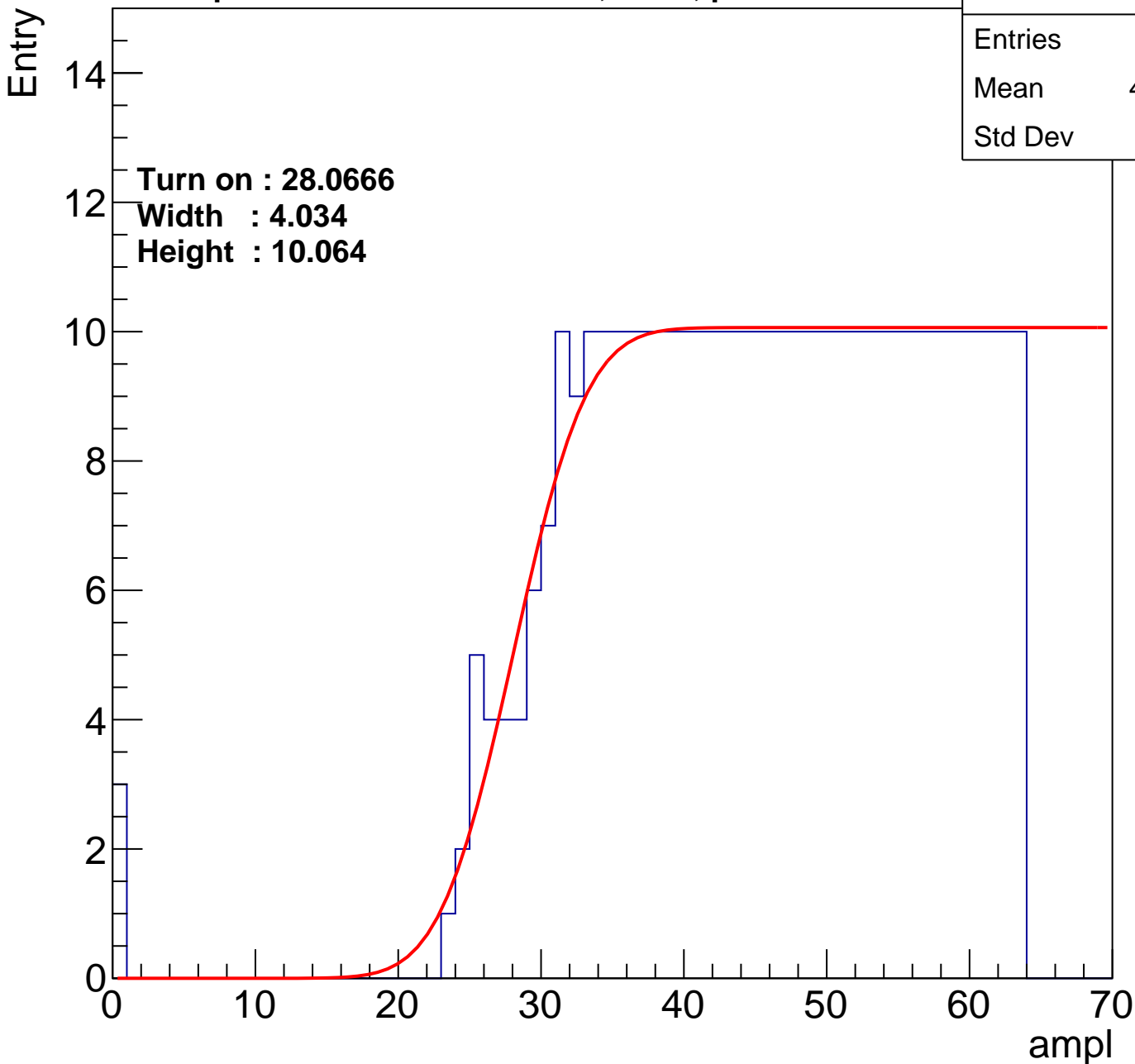
calib_packv5_042523_0143.root, FC#0, port D2

Entries	365
Mean	44.88
Std Dev	11.41

Turn on : 28.0666

Width : 4.034

Height : 10.064



B1L101S, U23-ch62

calib_packv5_042523_0143.root, FC#0, port D2

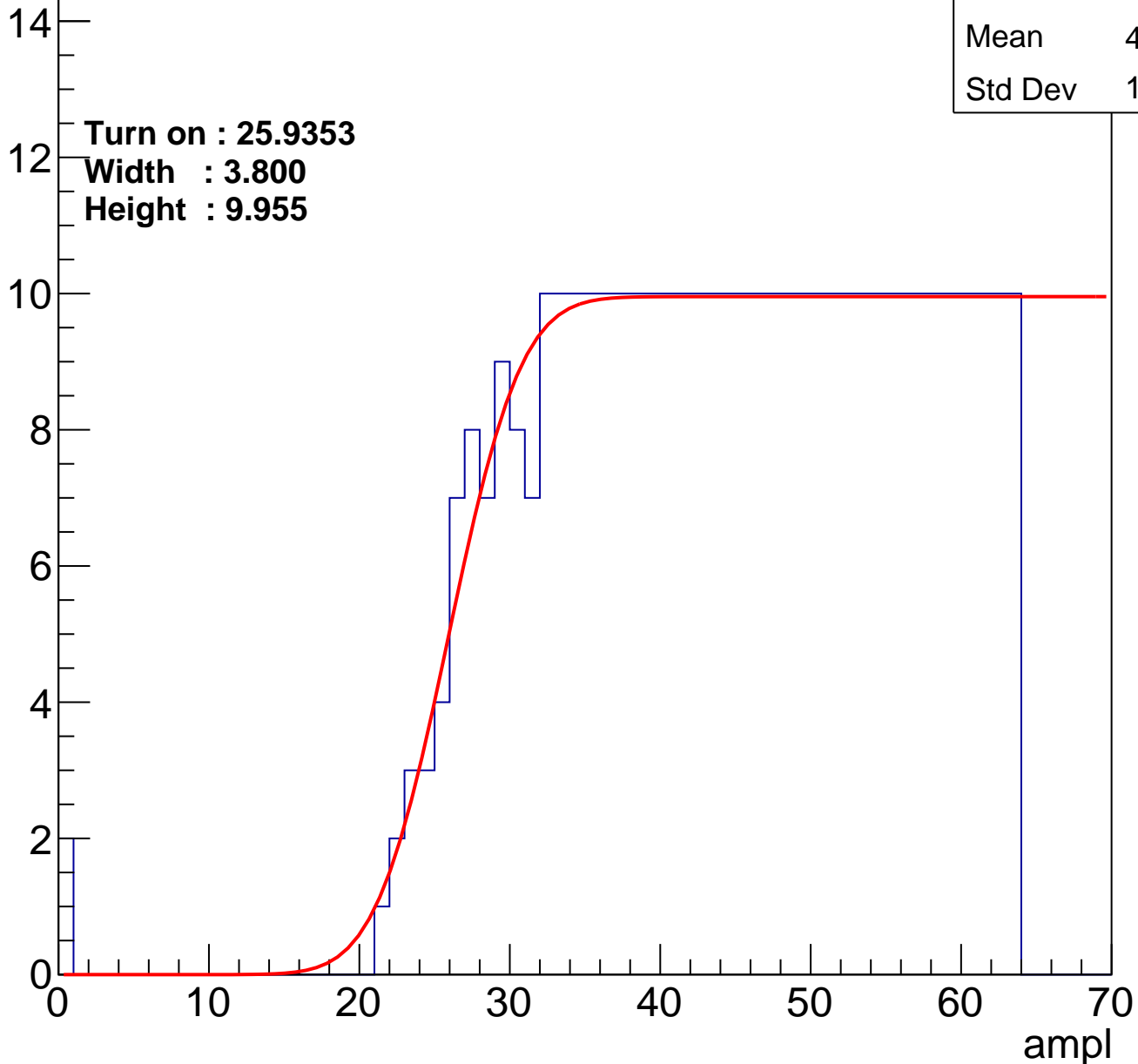
Entries	381
Mean	44.14
Std Dev	11.65

Turn on : 25.9353

Width : 3.800

Height : 9.955

Entry



B1L101S, U23-ch63

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.76
Std Dev	11.44

Turn on : 27.2315

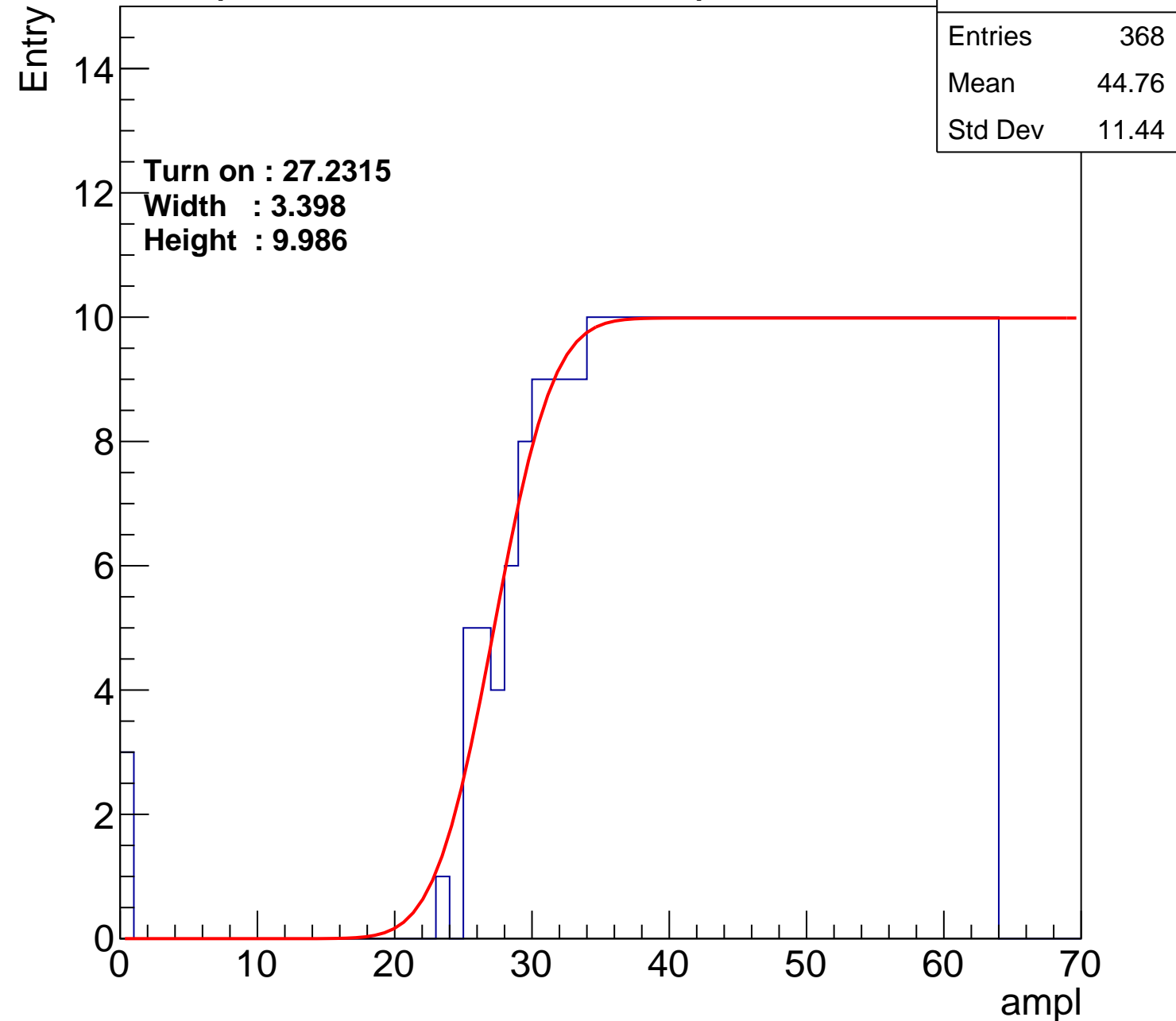
Width : 3.398

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch64

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.0770

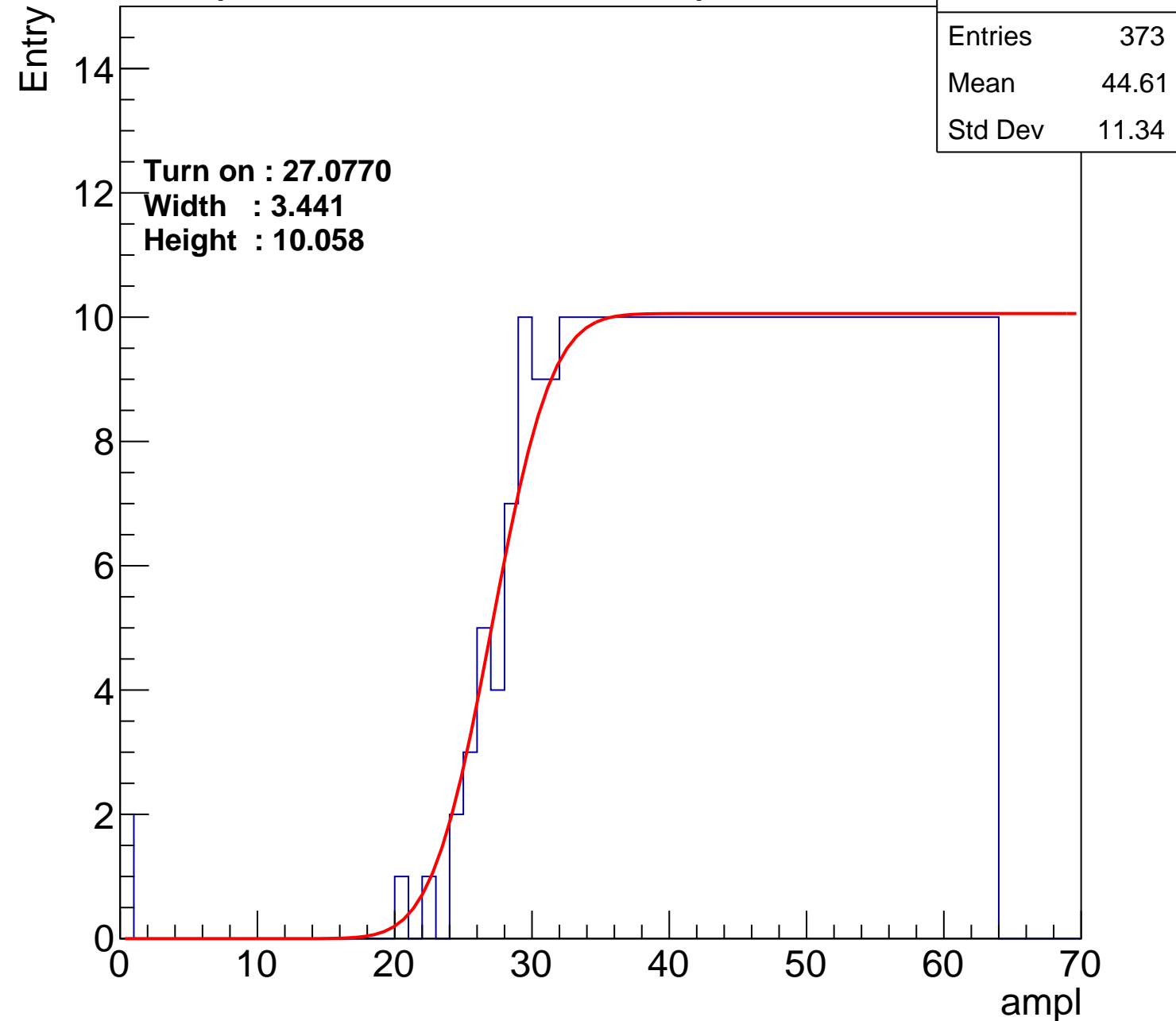
Width : 3.441

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch65

calib_packv5_042523_0143.root, FC#0, port D2

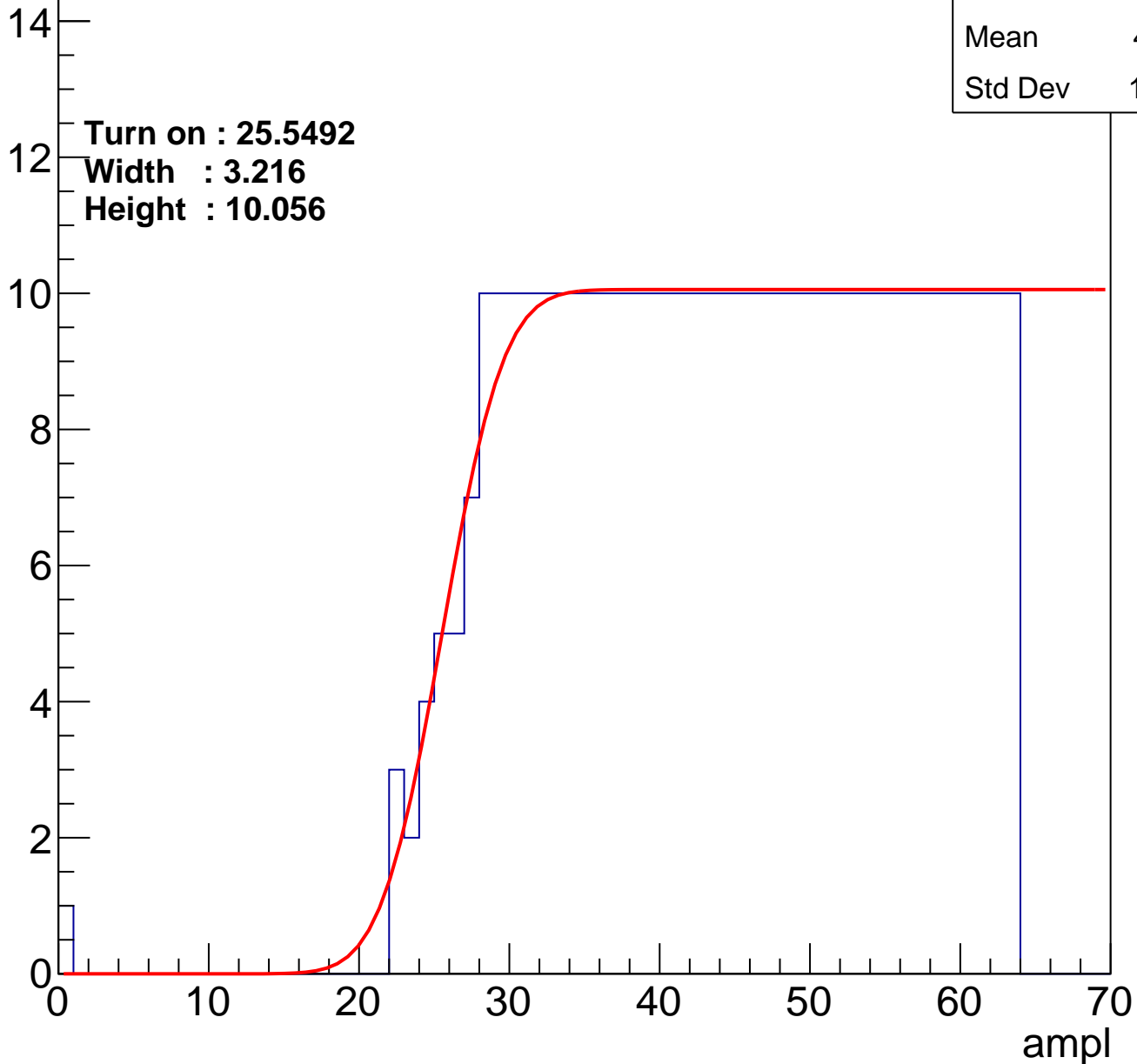
Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 25.5492

Width : 3.216

Height : 10.056

Entry



B1L101S, U23-ch66

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.67
Std Dev	11.79

Turn on : 27.0920

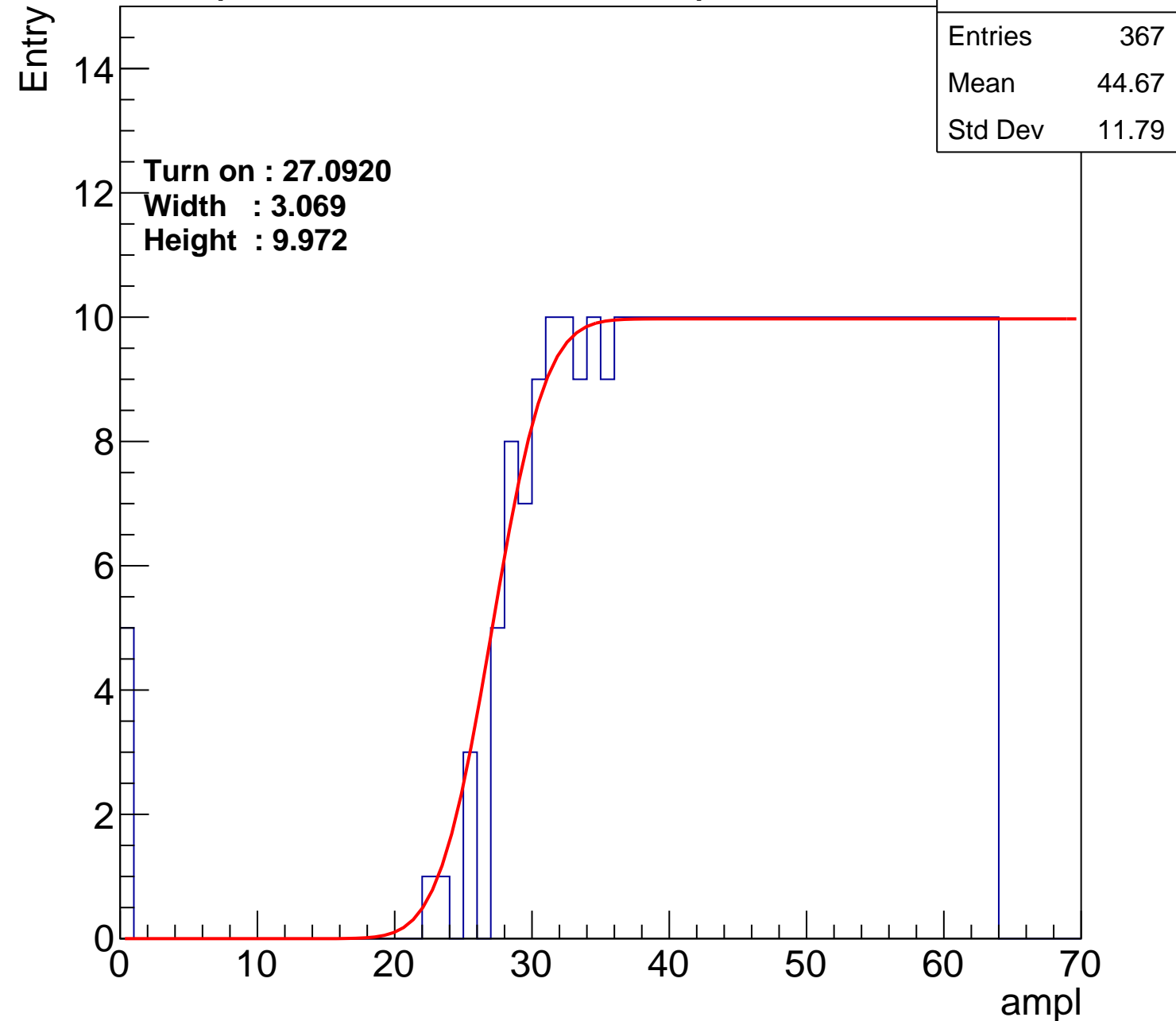
Width : 3.069

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch67

calib_packv5_042523_0143.root, FC#0, port D2

Entries	359
Mean	45.24
Std Dev	11.17

Turn on : 28.5947

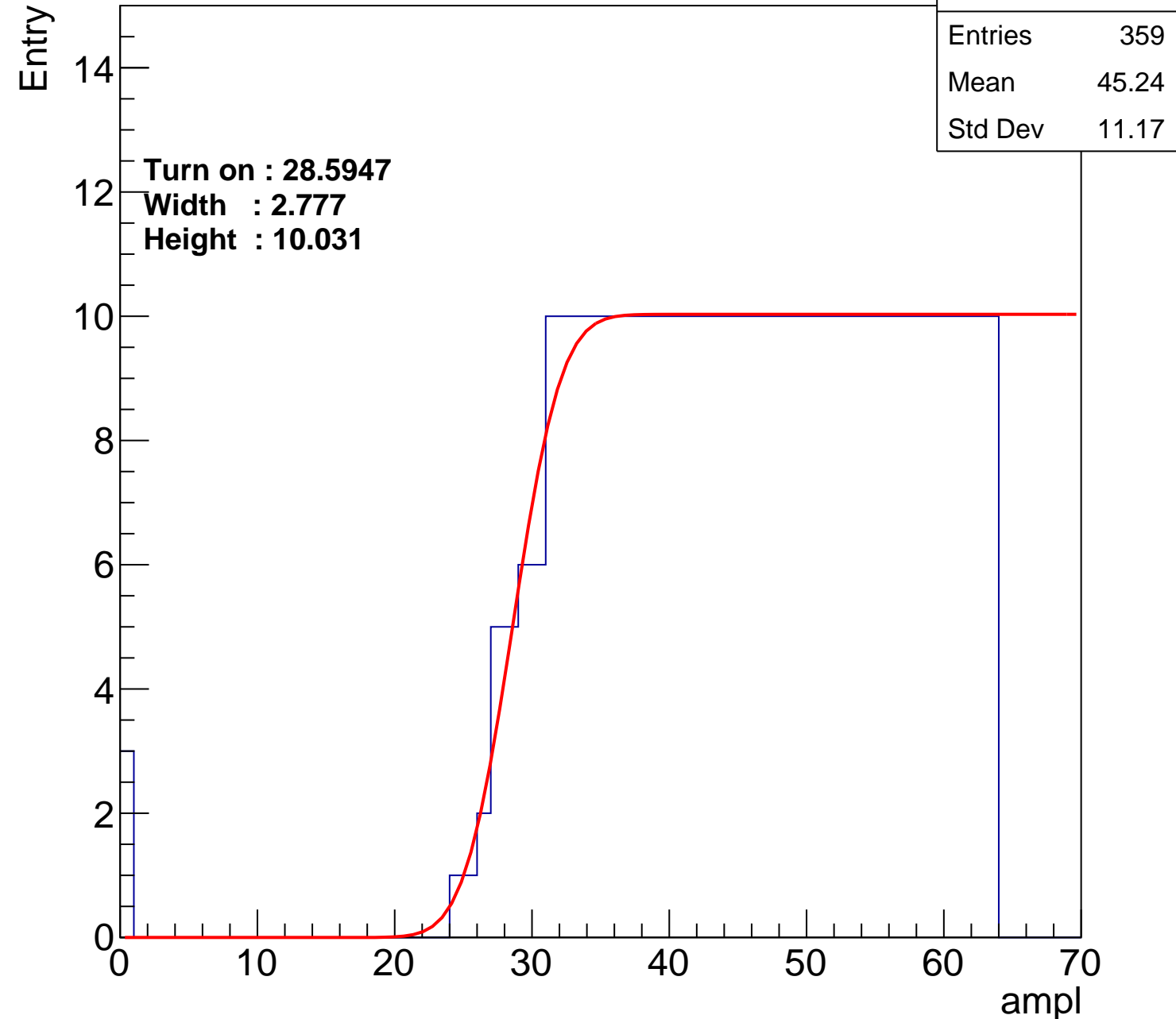
Width : 2.777

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch68

calib_packv5_042523_0143.root, FC#0, port D2

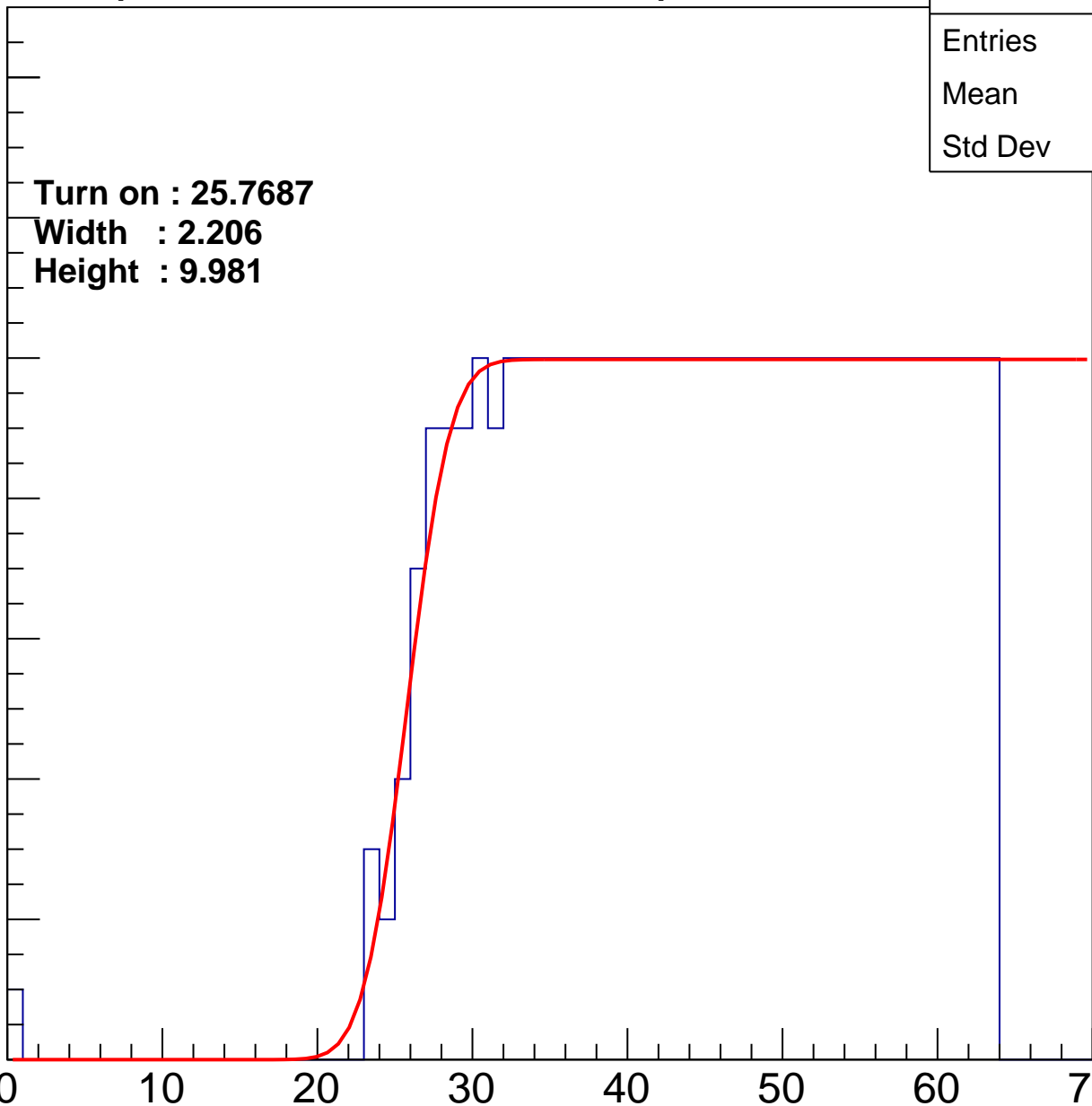
Entry

14
12
10
8
6
4
2
0

Turn on : 25.7687
Width : 2.206
Height : 9.981

Entries	383
Mean	44.21
Std Dev	11.35

ampl



B1L101S, U23-ch69

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.78
Std Dev	11.47

Turn on : 27.8901

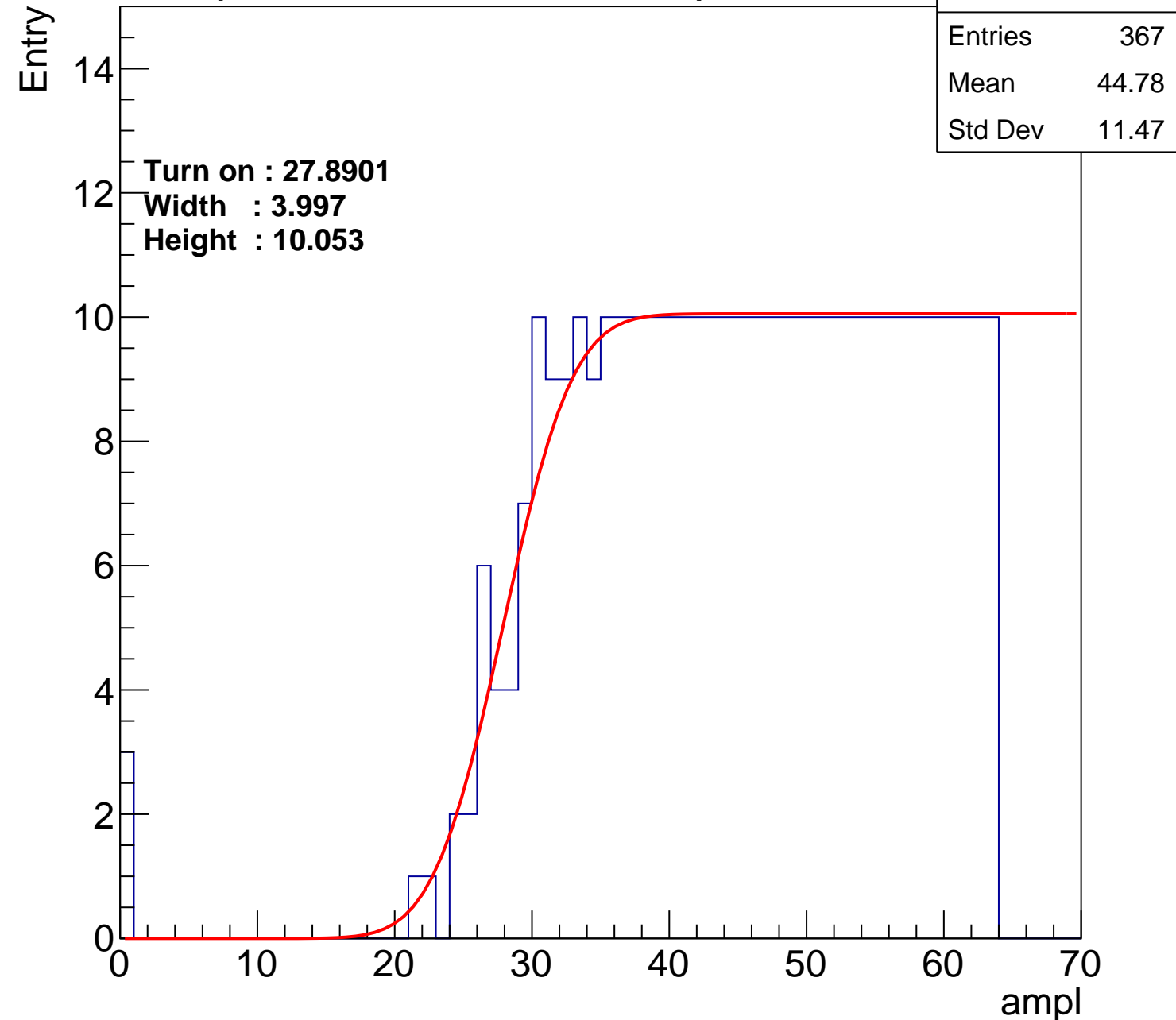
Width : 3.997

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch70

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.62
Std Dev	11.36

Turn on : 27.1742

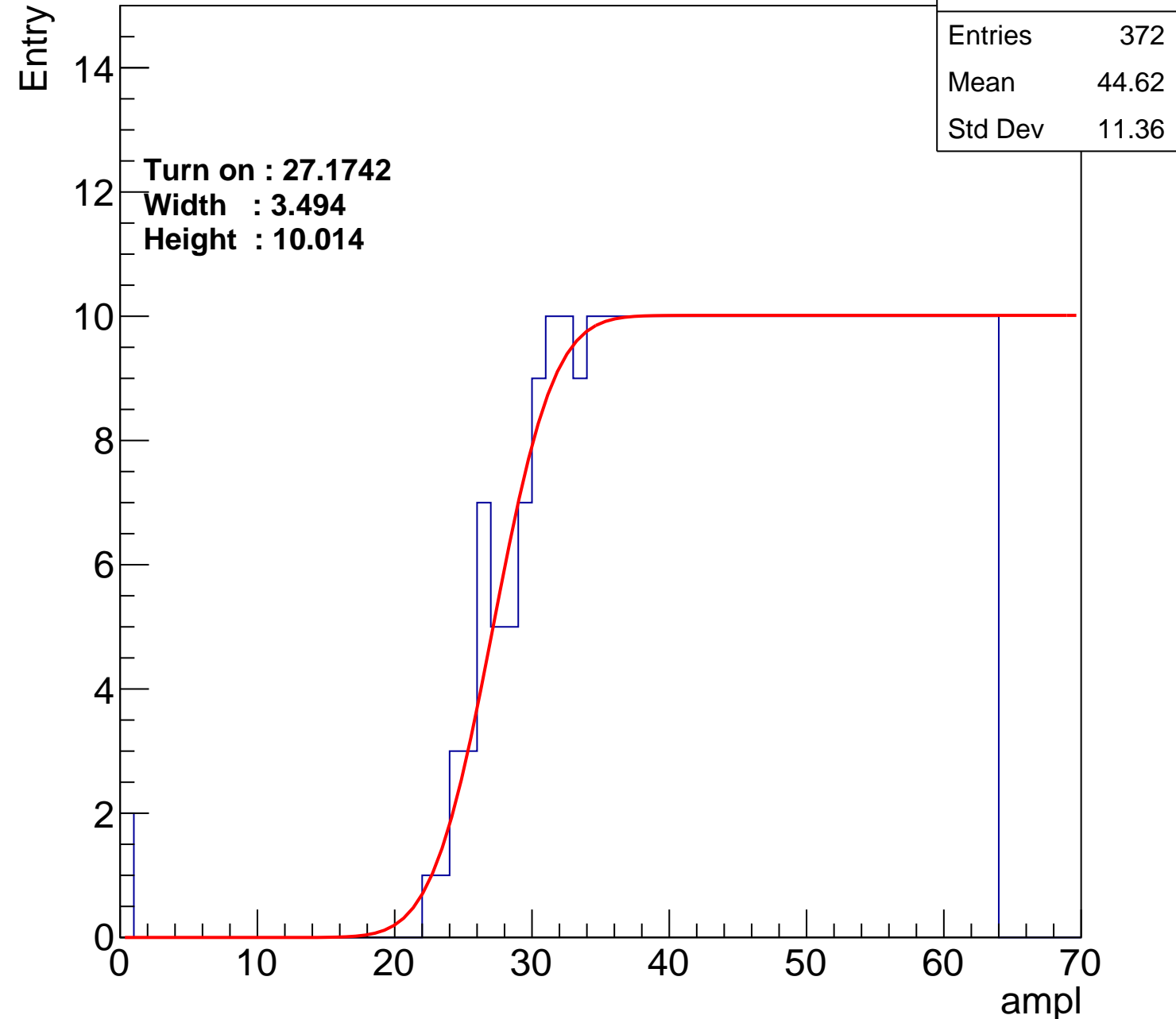
Width : 3.494

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch71

calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	45.04
Std Dev	11.48

Turn on : 29.2610

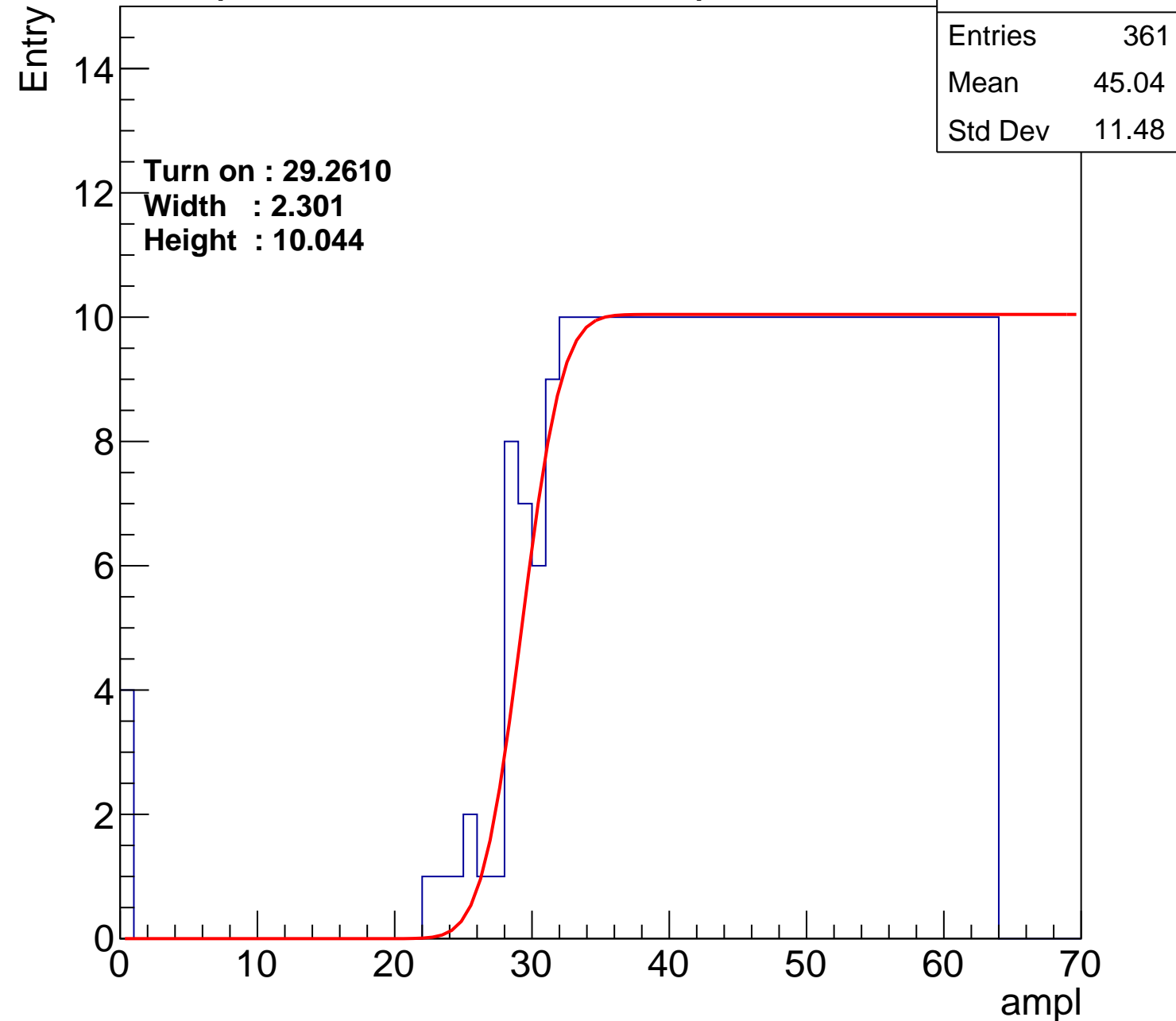
Width : 2.301

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch72

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.59
Std Dev	11.5

Turn on : 27.5657

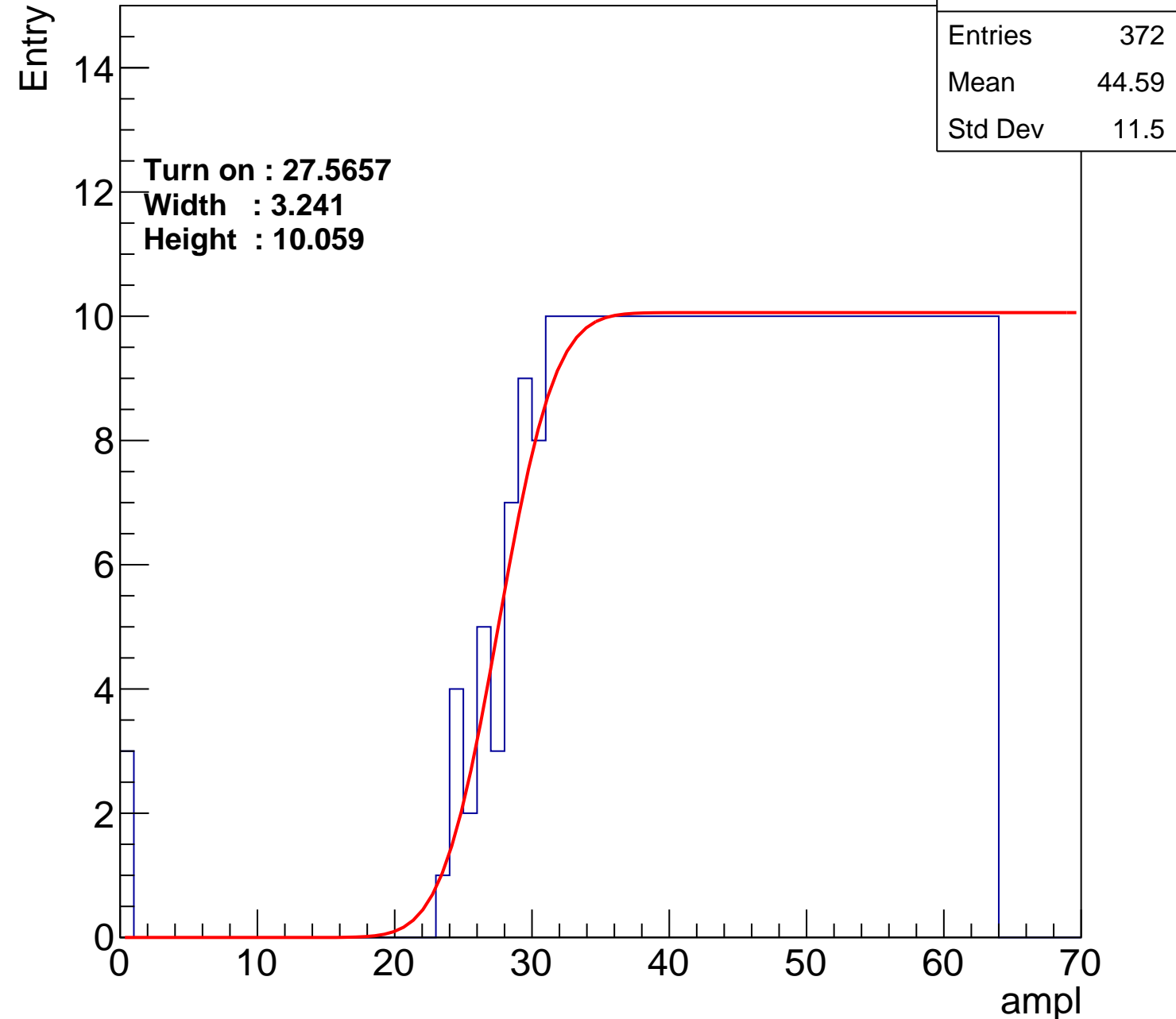
Width : 3.241

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch73

calib_packv5_042523_0143.root, FC#0, port D2

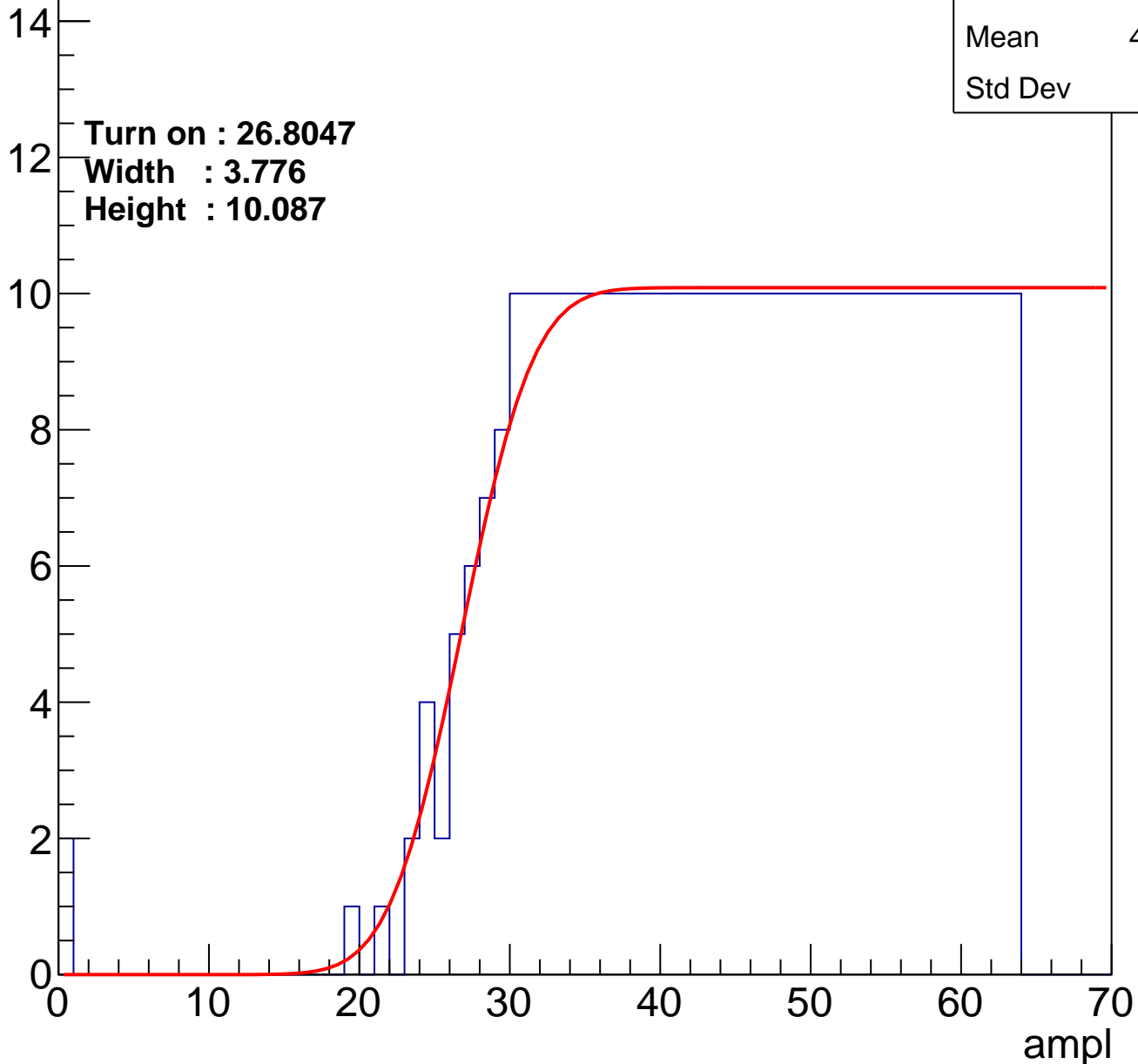
Entries	378
Mean	44.34
Std Dev	11.5

Turn on : 26.8047

Width : 3.776

Height : 10.087

Entry



B1L101S, U23-ch74

calib_packv5_042523_0143.root, FC#0, port D2

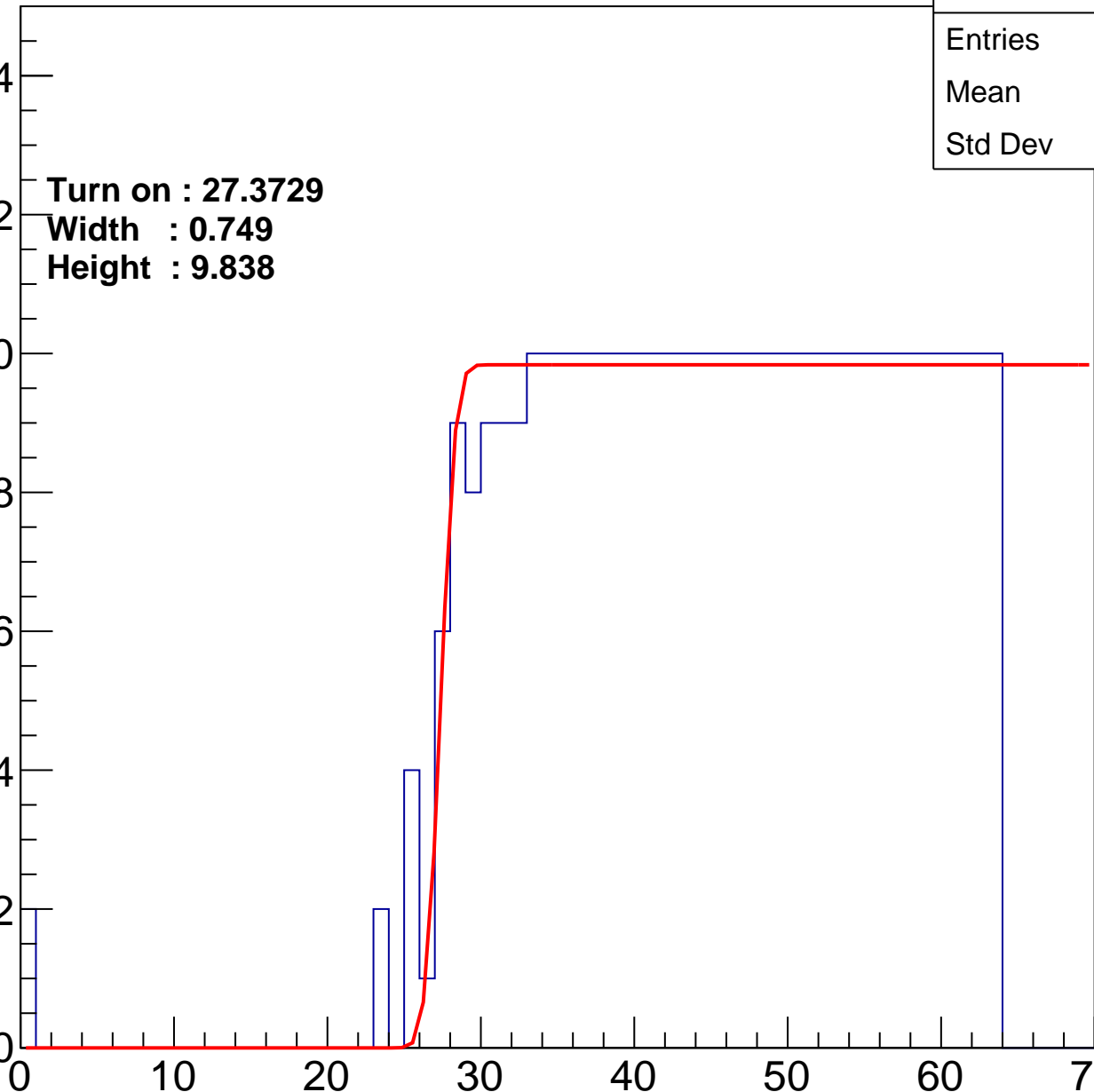
Entry

14
12
10
8
6
4
2
0

Turn on : 27.3729
Width : 0.749
Height : 9.838

Entries	369
Mean	44.81
Std Dev	11.22

ampl



B1L101S, U23-ch75

calib_packv5_042523_0143.root, FC#0, port D2

Entries	353
Mean	45.59
Std Dev	10.83

Turn on : 29.3299

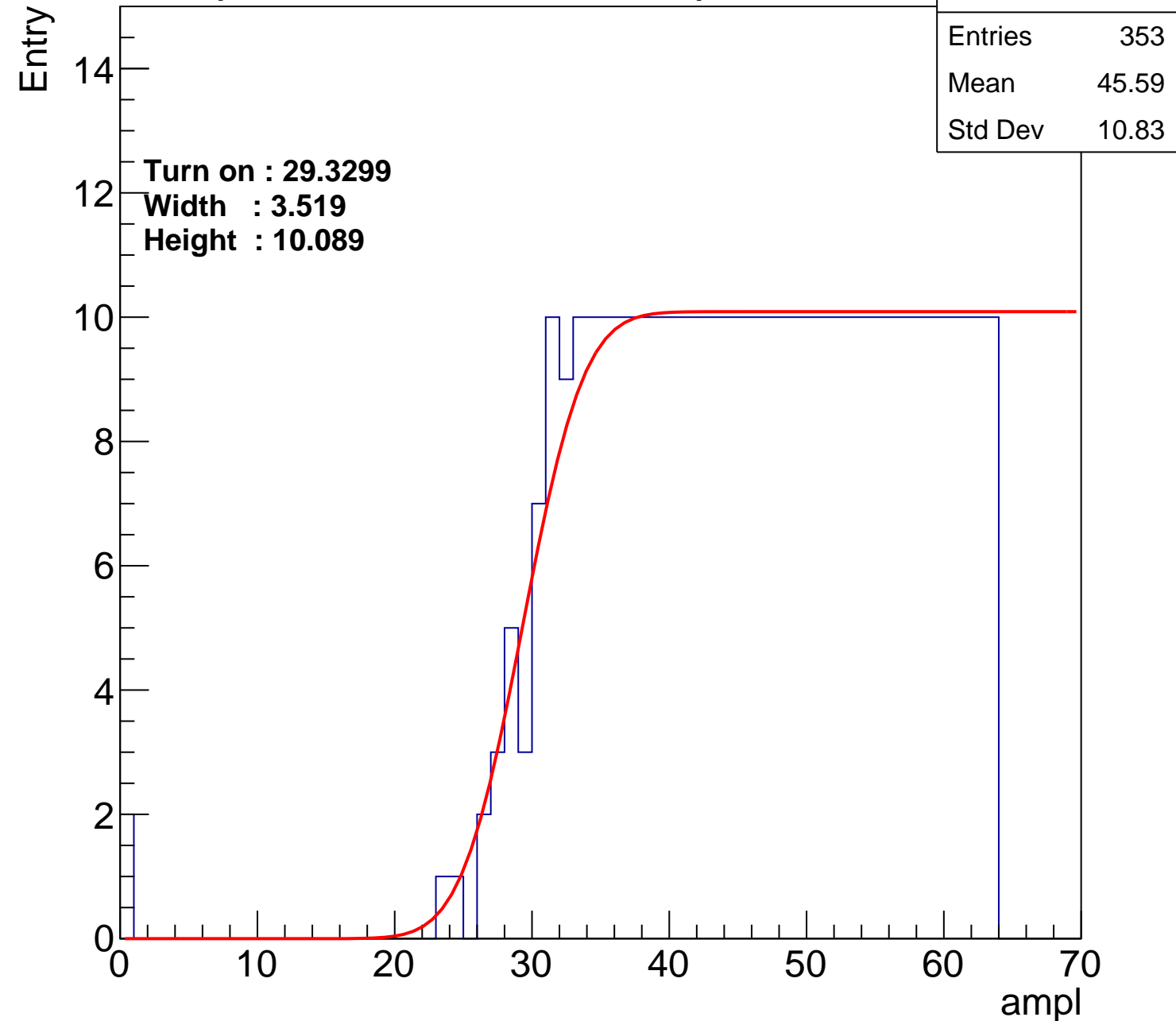
Width : 3.519

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch76

calib_packv5_042523_0143.root, FC#0, port D2

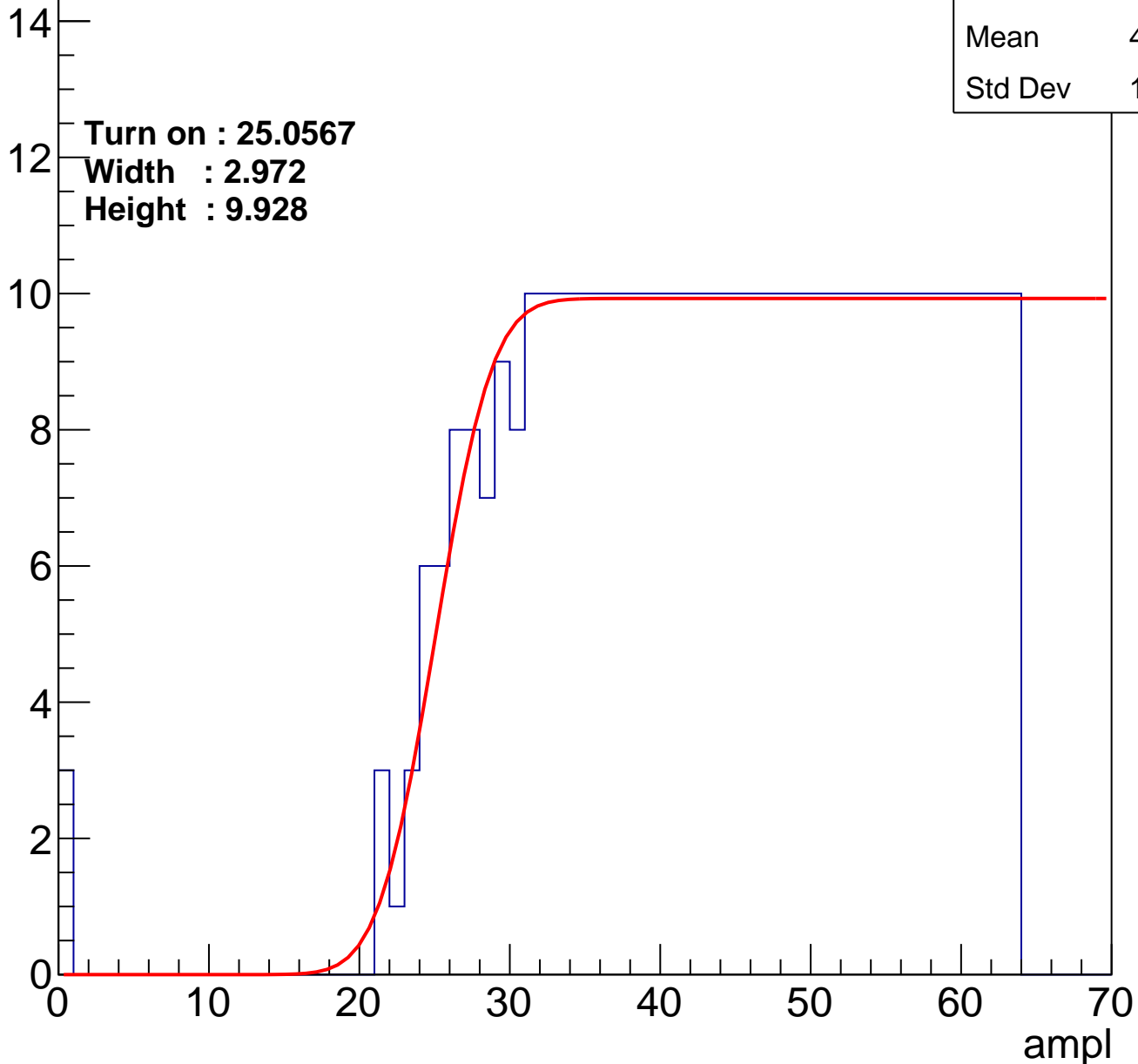
Entries	392
Mean	43.57
Std Dev	12.05

Turn on : 25.0567

Width : 2.972

Height : 9.928

Entry



B1L101S, U23-ch77

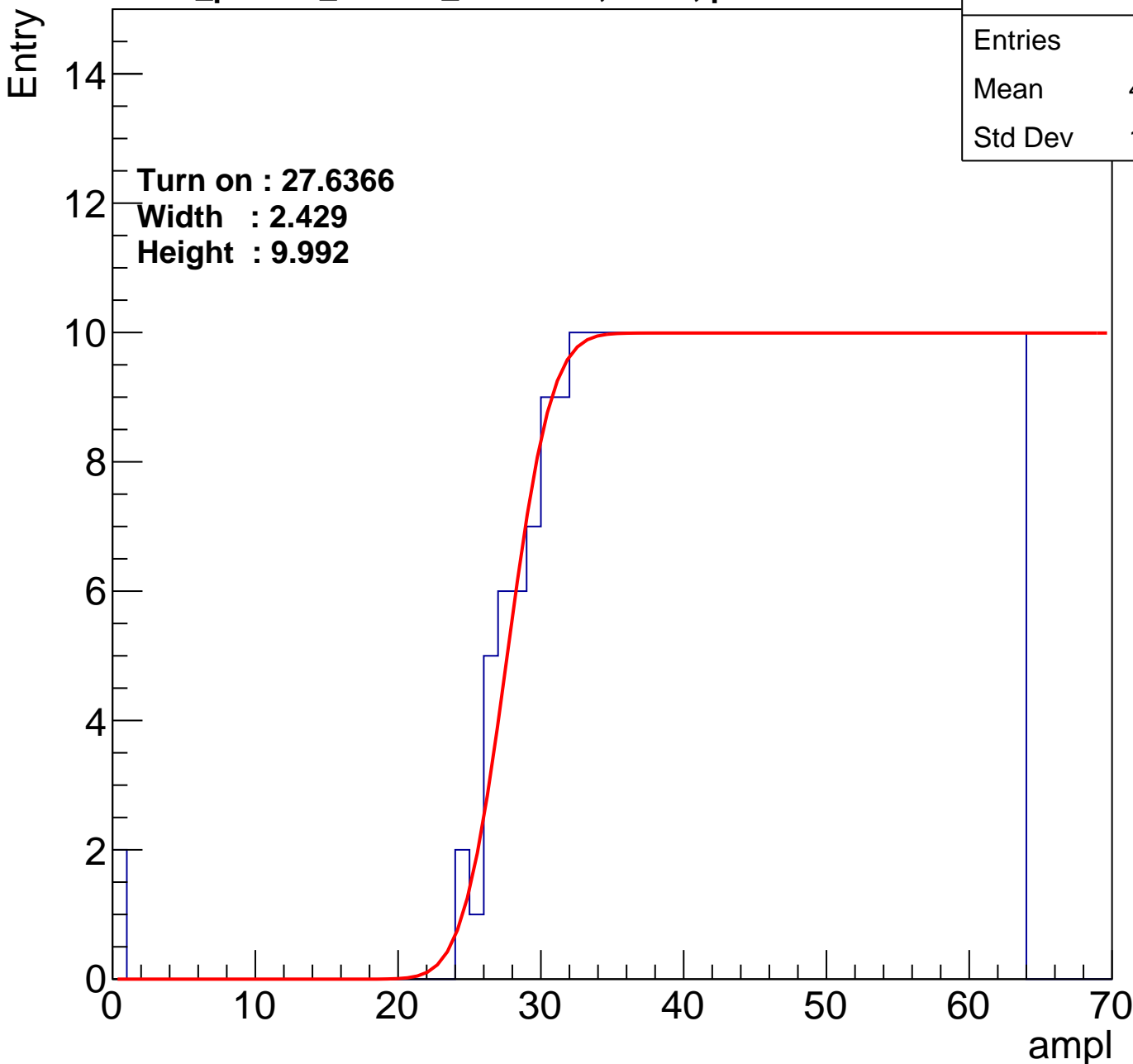
calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.6366

Width : 2.429

Height : 9.992



B1L101S, U23-ch78

calib_packv5_042523_0143.root, FC#0, port D2

Entries	393
Mean	43.63
Std Dev	11.85

Turn on : 24.8464

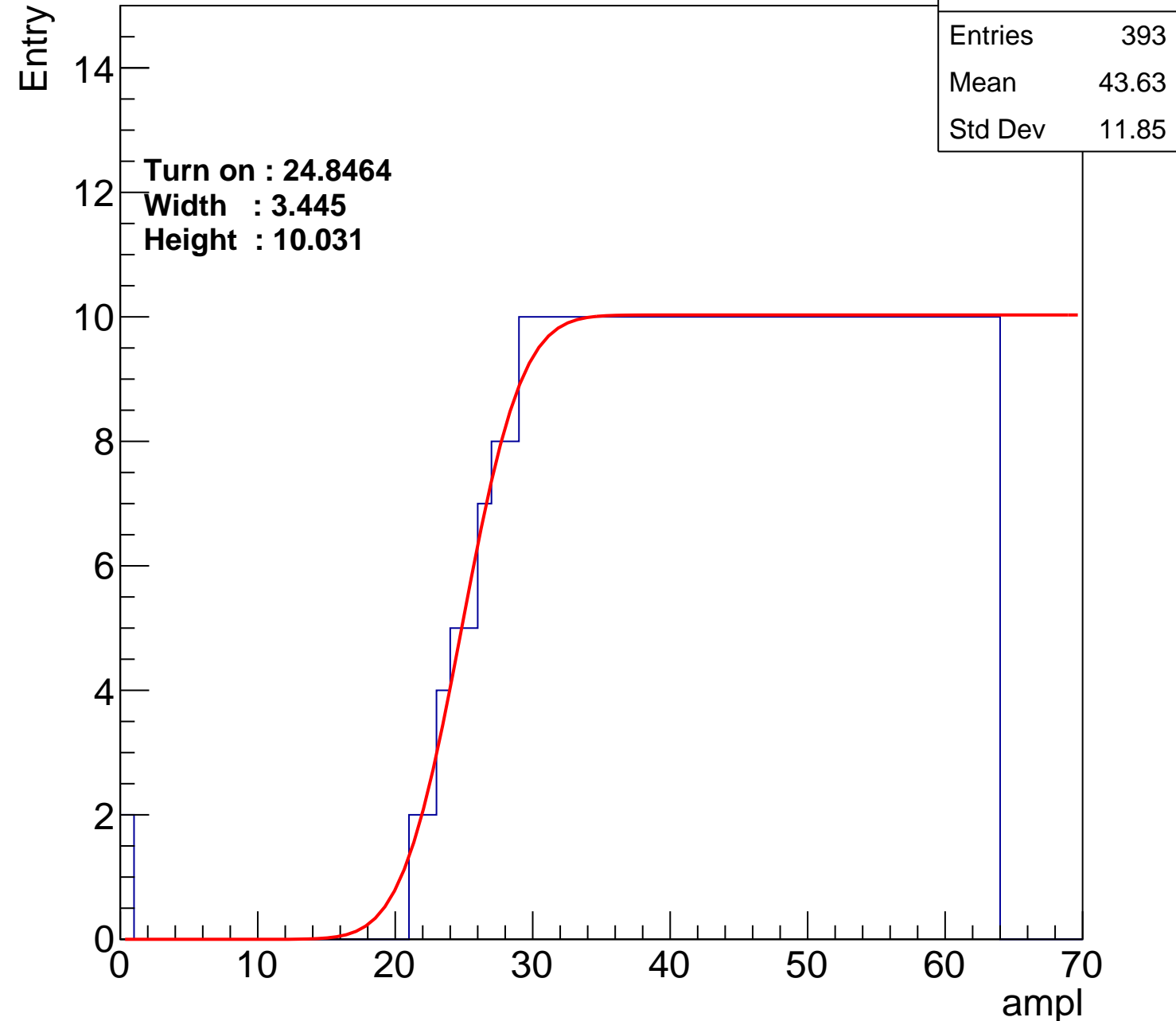
Width : 3.445

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch79

calib_packv5_042523_0143.root, FC#0, port D2

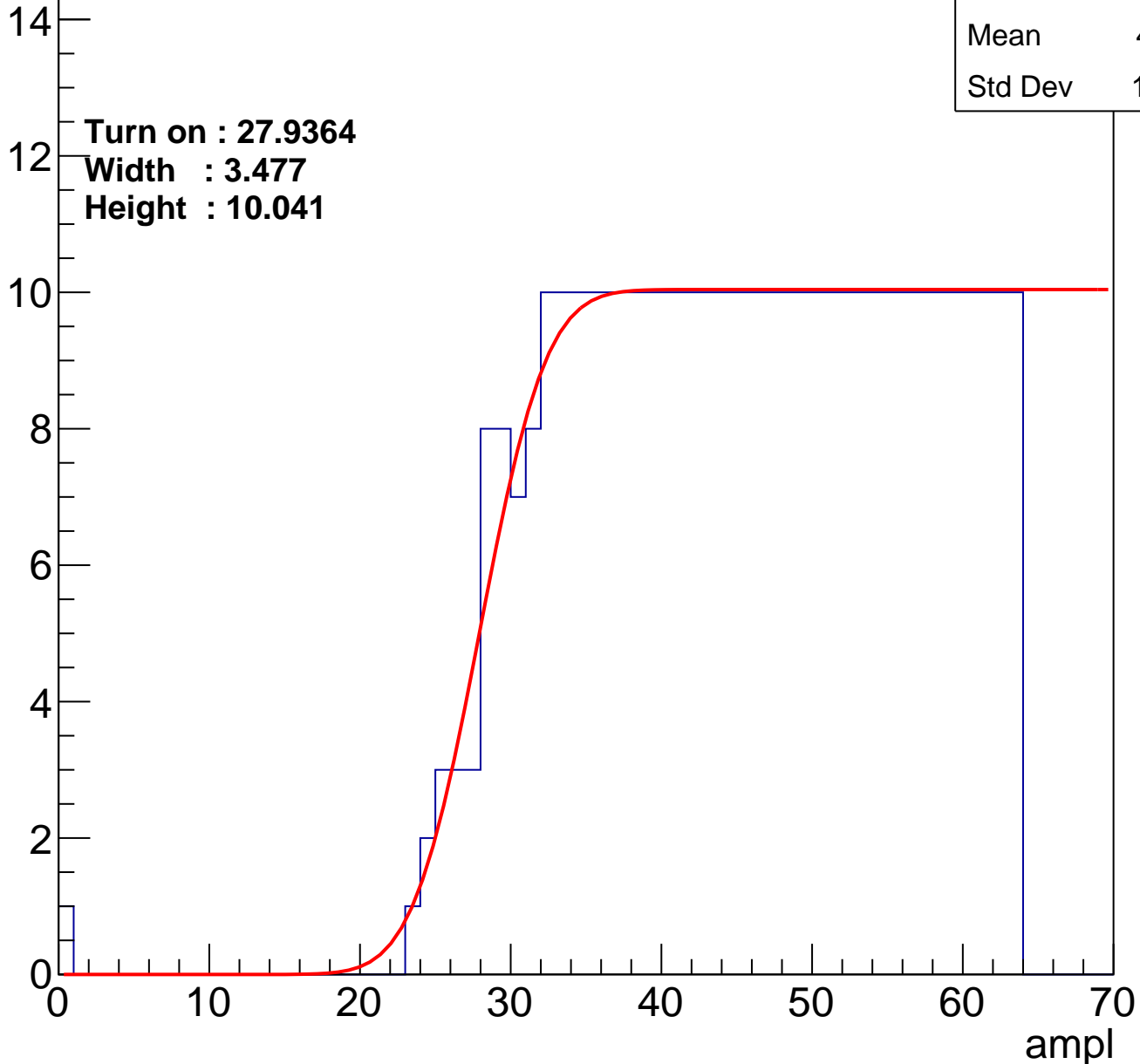
Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 27.9364

Width : 3.477

Height : 10.041

Entry



B1L101S, U23-ch80

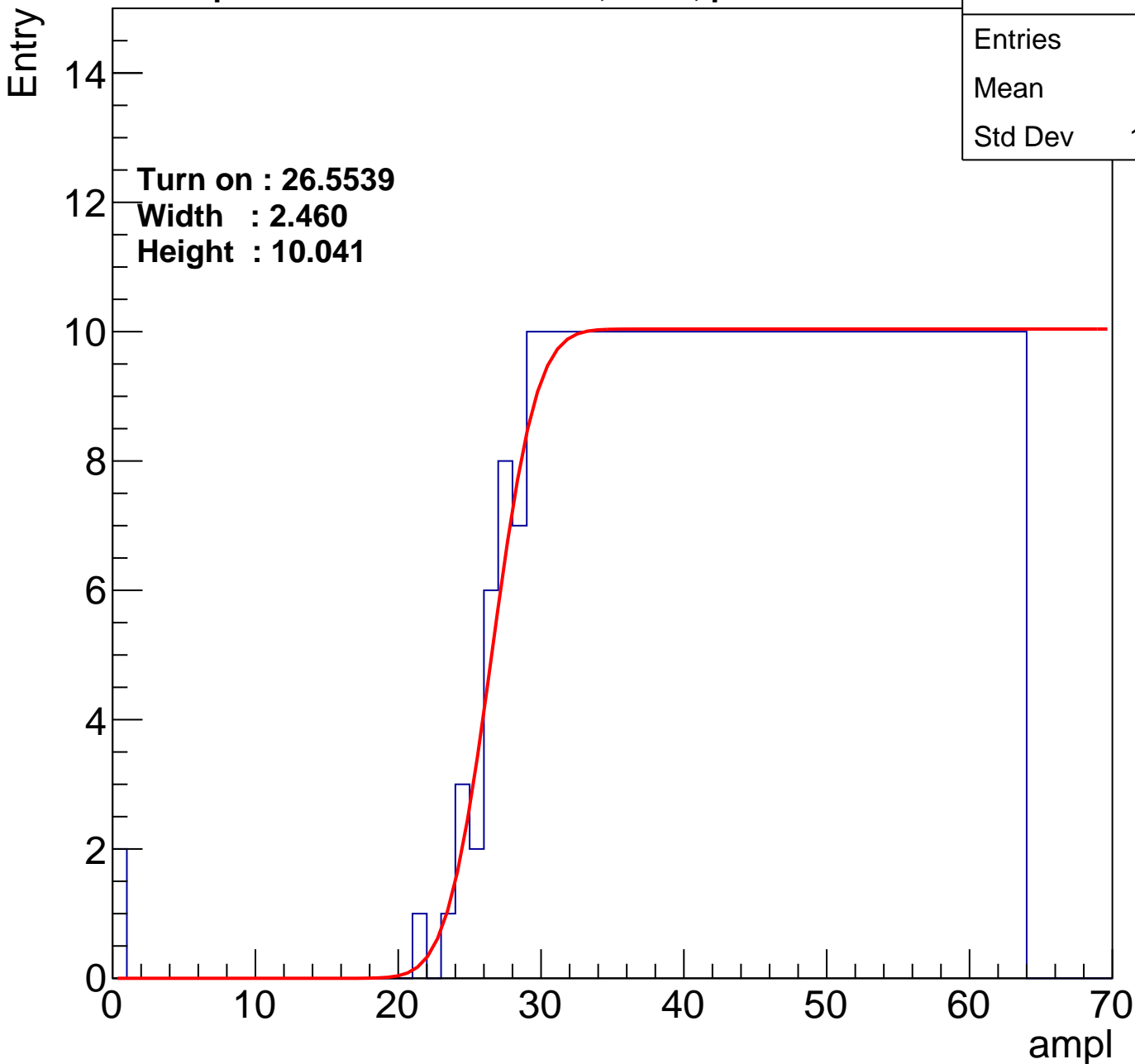
calib_packv5_042523_0143.root, FC#0, port D2

Entries	380
Mean	44.3
Std Dev	11.46

Turn on : 26.5539

Width : 2.460

Height : 10.041



B1L101S, U23-ch81

calib_packv5_042523_0143.root, FC#0, port D2

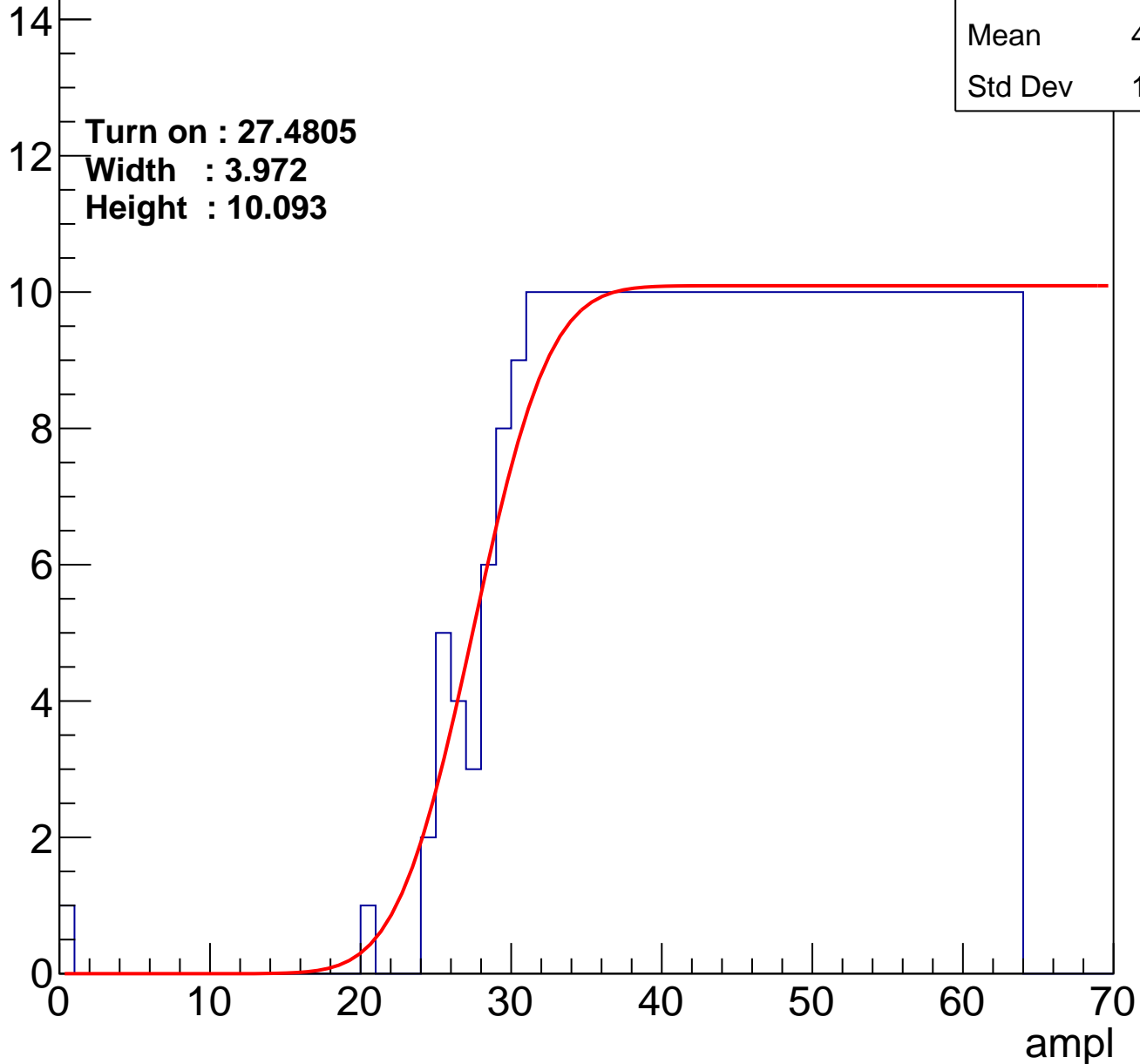
Entries	369
Mean	44.87
Std Dev	11.04

Turn on : 27.4805

Width : 3.972

Height : 10.093

Entry



B1L101S, U23-ch82

calib_packv5_042523_0143.root, FC#0, port D2

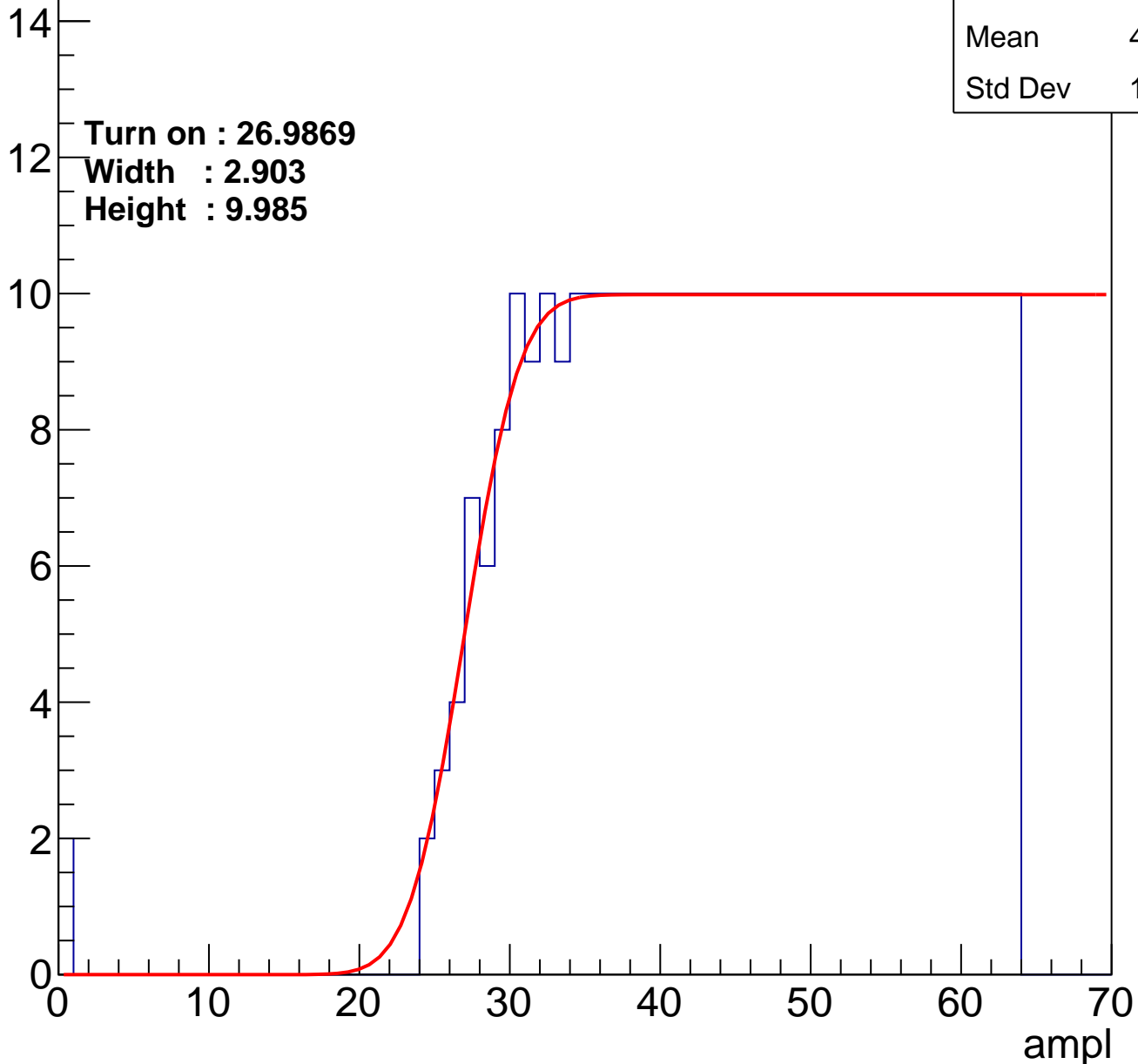
Entries	370
Mean	44.76
Std Dev	11.24

Turn on : 26.9869

Width : 2.903

Height : 9.985

Entry



B1L101S, U23-ch83

calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	45.16
Std Dev	11.1

Turn on : 28.3019

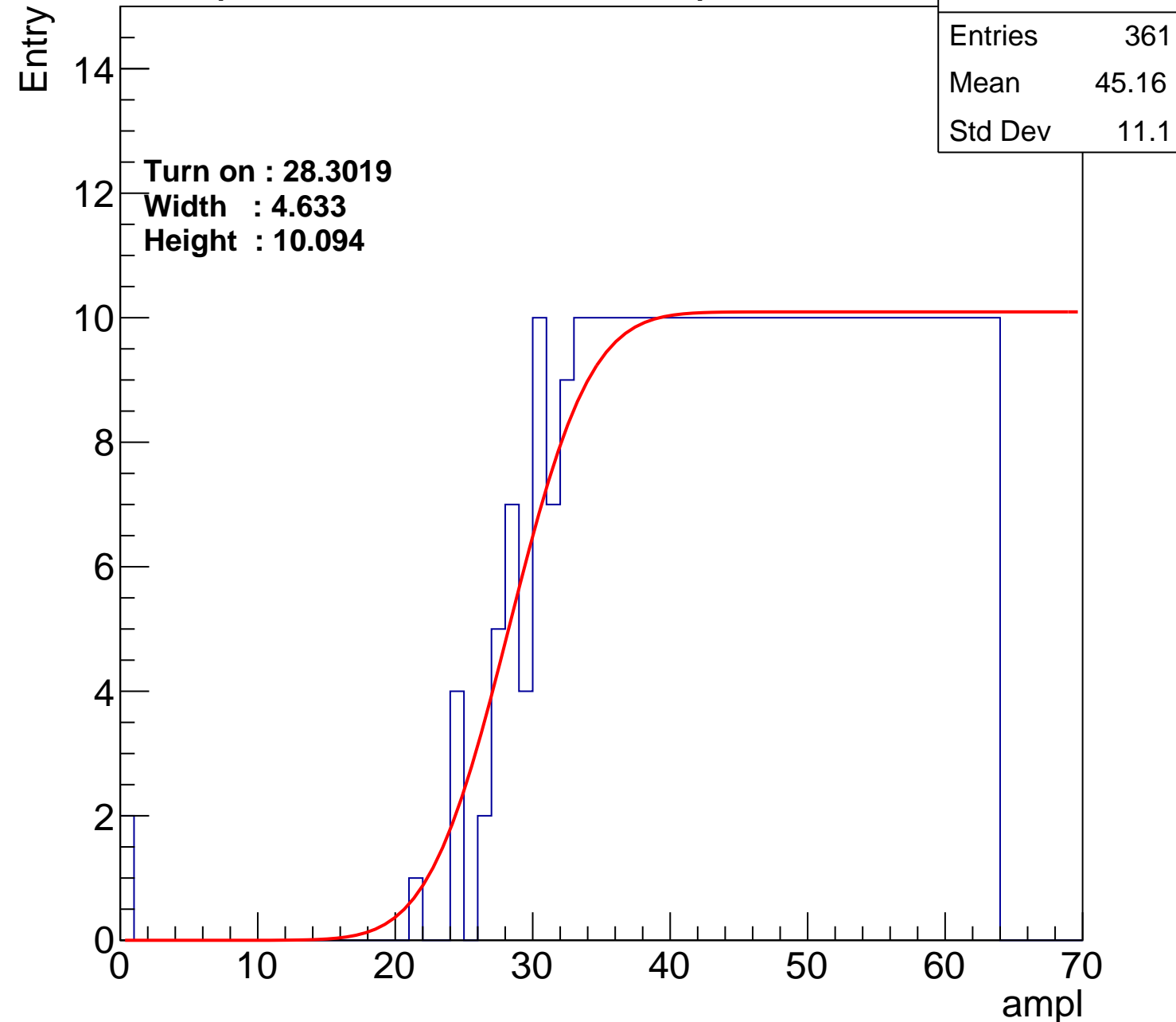
Width : 4.633

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch84

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.14
Std Dev	10.91

Turn on : 27.8407

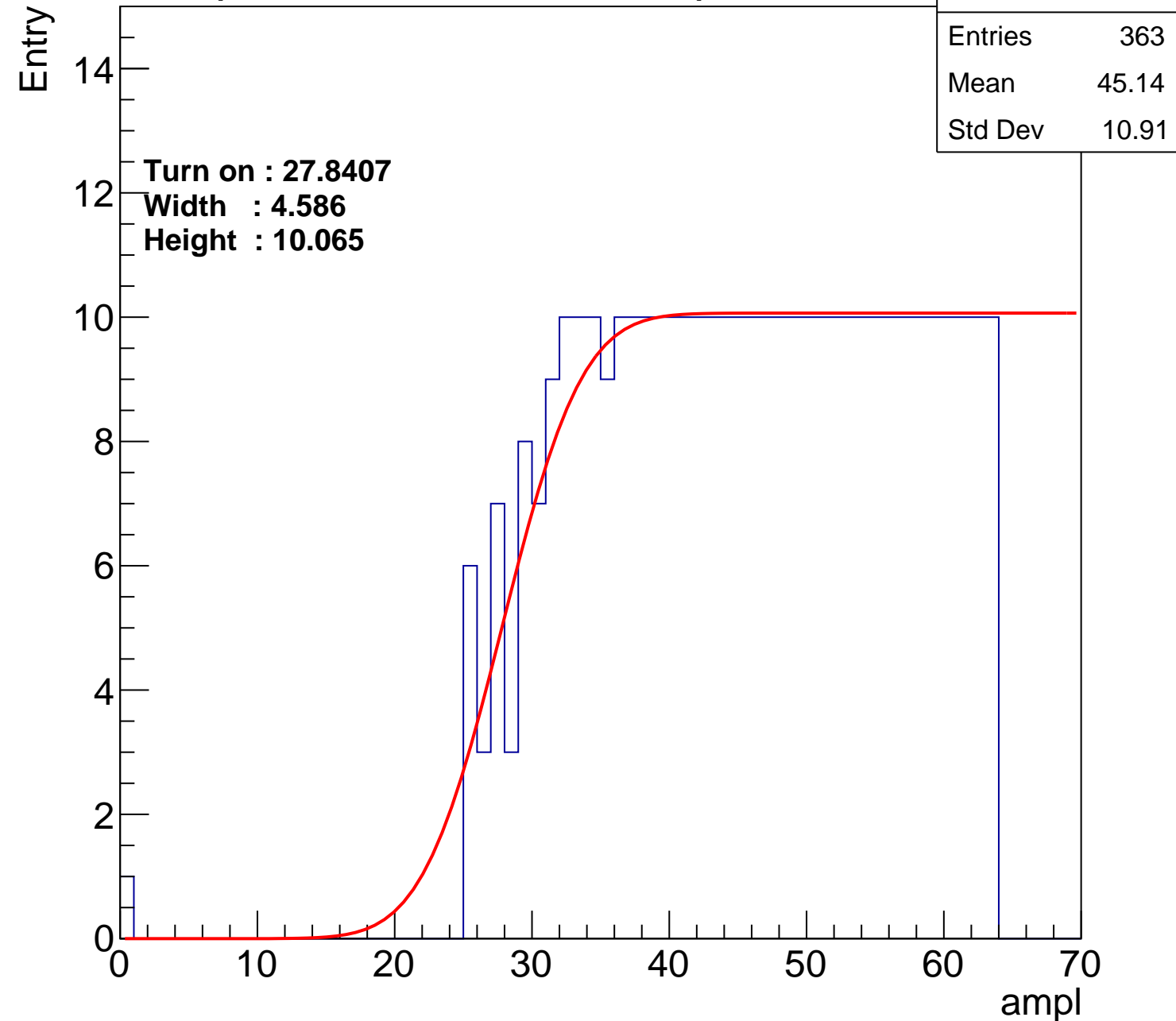
Width : 4.586

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch85

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.85
Std Dev	11.01

Turn on : 27.2912

Width : 2.985

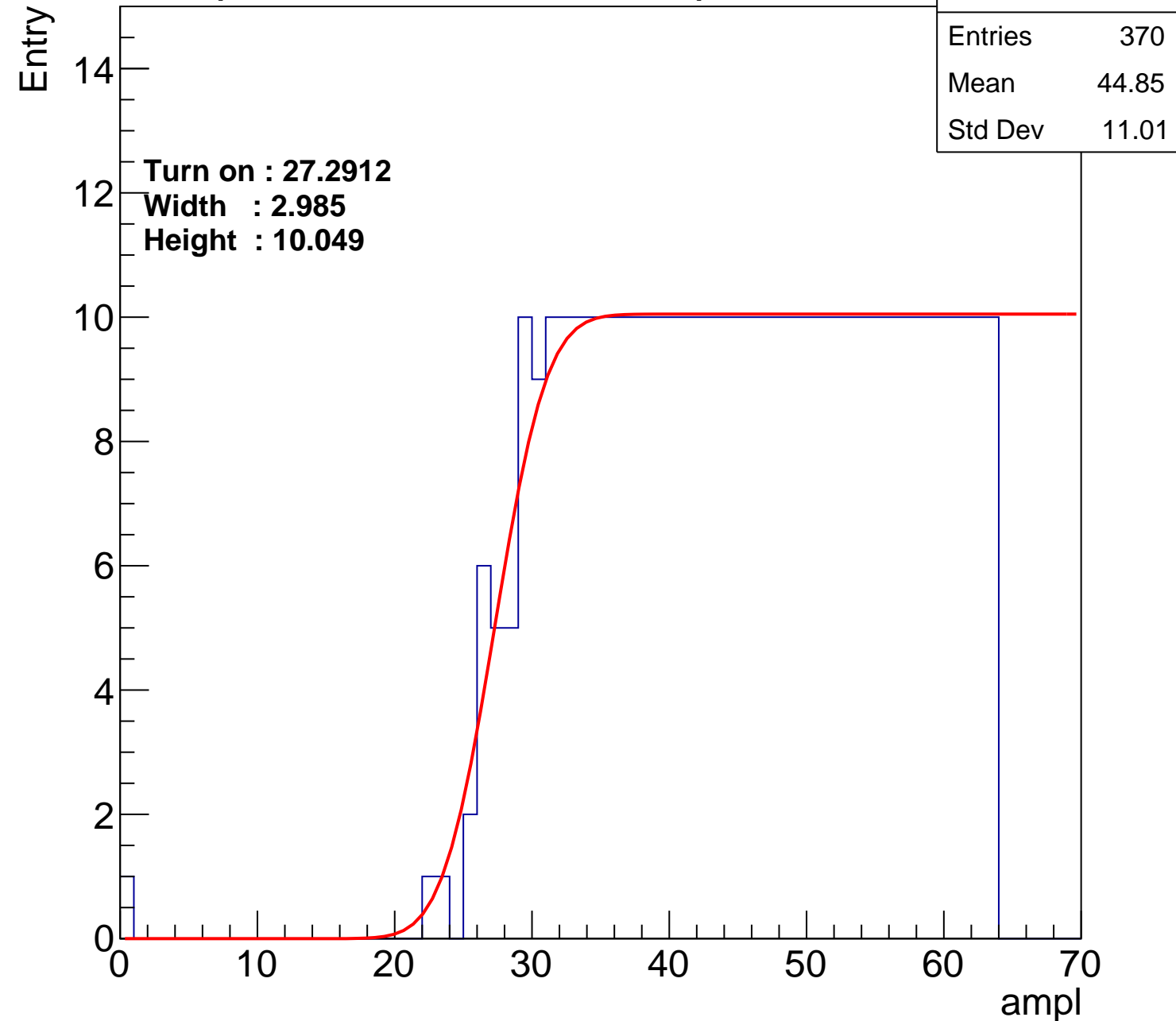
Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch86

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.38
Std Dev	11.49

Turn on : 26.5917

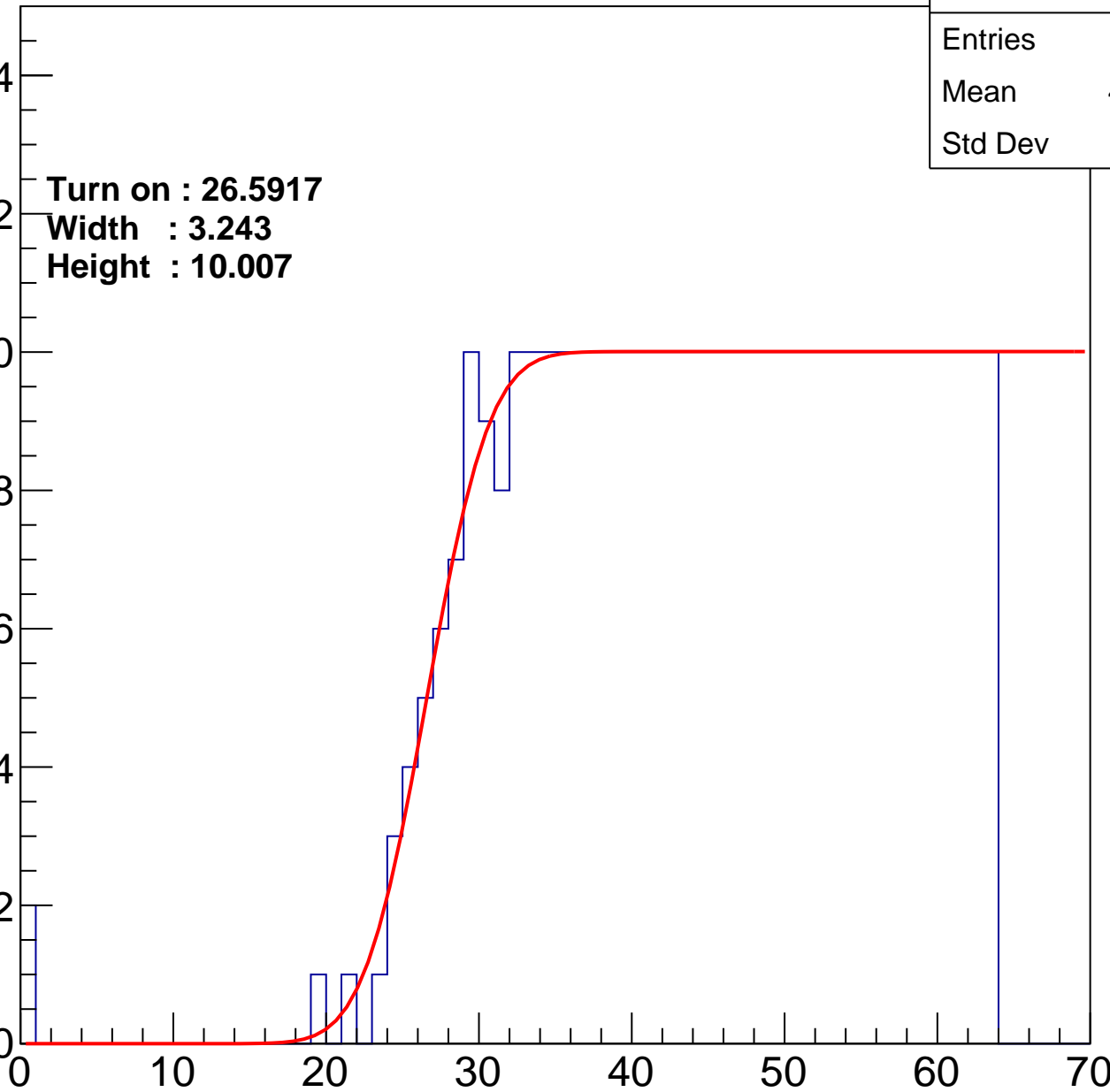
Width : 3.243

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch87

calib_packv5_042523_0143.root, FC#0, port D2

Entries	352
Mean	45.58
Std Dev	11.01

Turn on : 29.0903

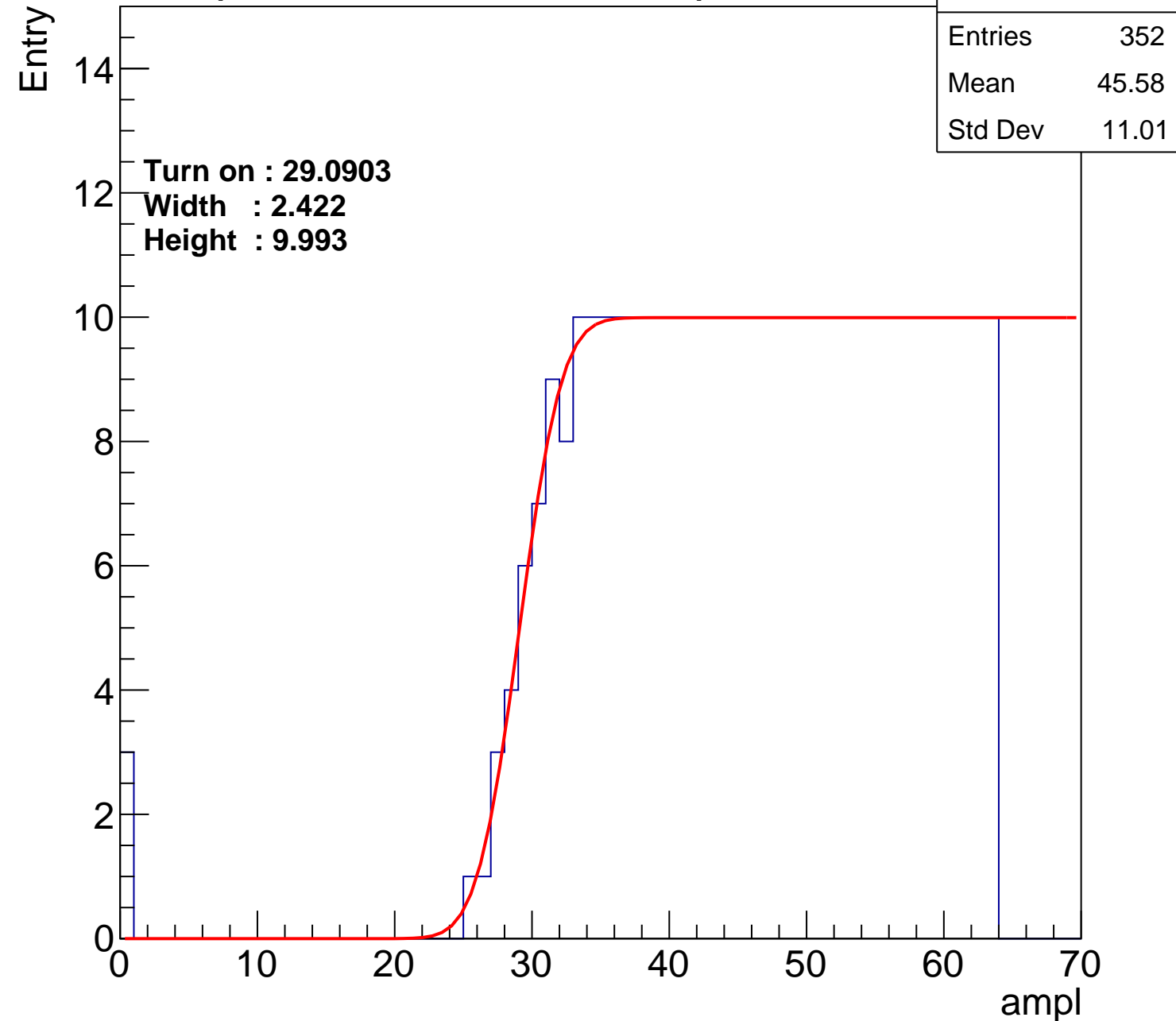
Width : 2.422

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch88

calib_packv5_042523_0143.root, FC#0, port D2

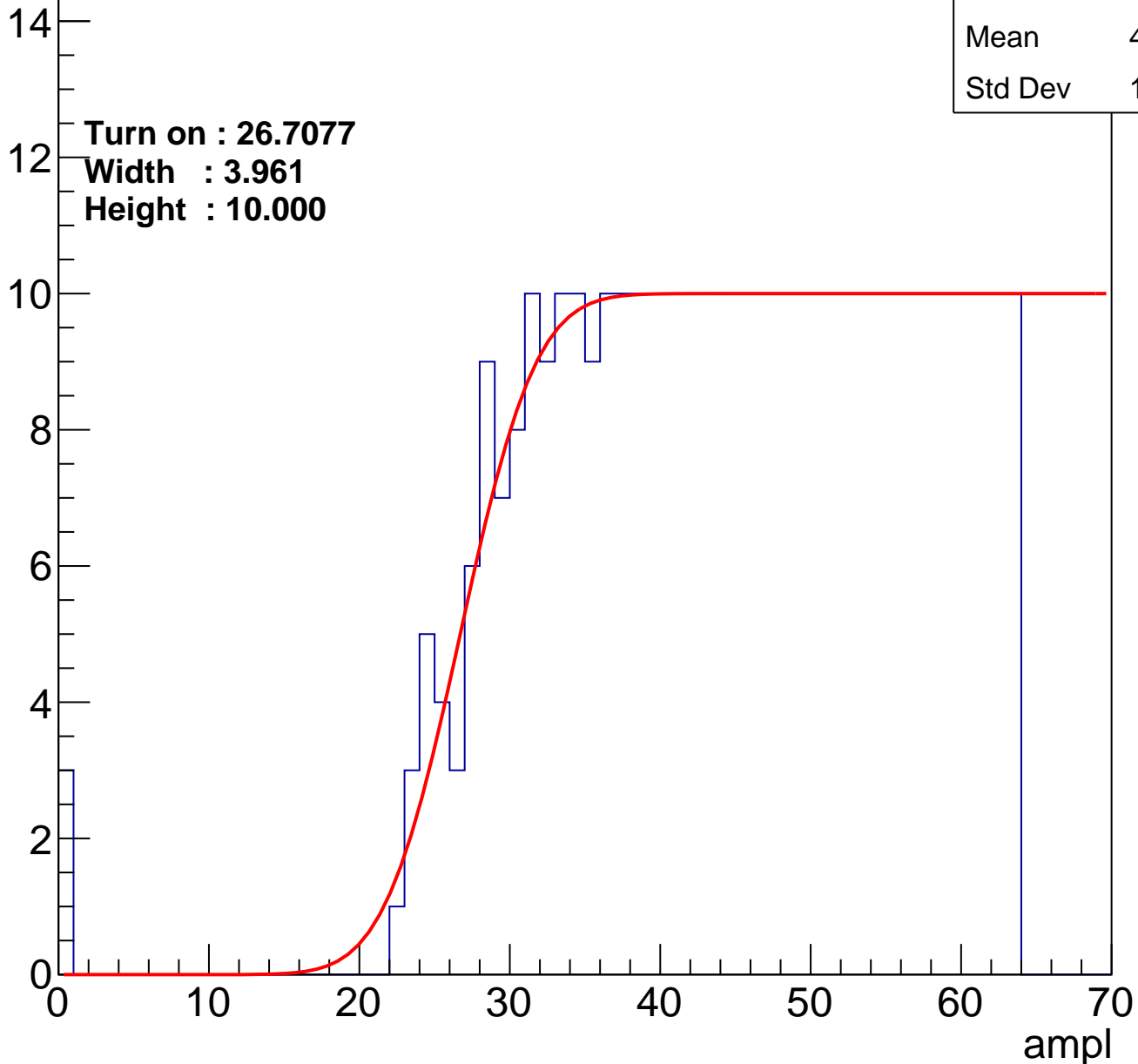
Entries	377
Mean	44.27
Std Dev	11.73

Turn on : 26.7077

Width : 3.961

Height : 10.000

Entry



B1L101S, U23-ch89

calib_packv5_042523_0143.root, FC#0, port D2

Entries	379
Mean	44.38
Std Dev	11.3

Turn on : 26.3152

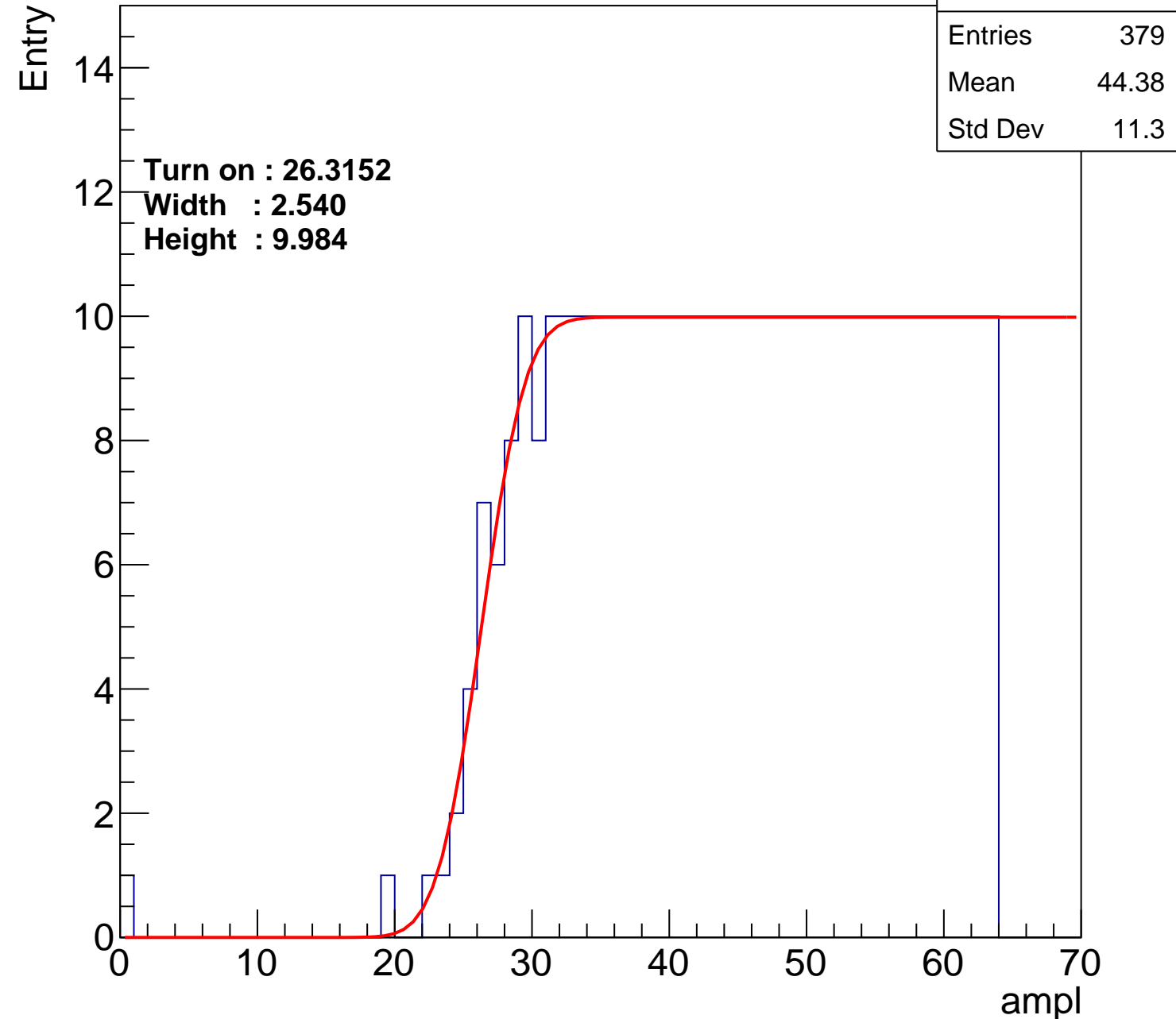
Width : 2.540

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch90

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.88
Std Dev	11.28

Turn on : 28.3957

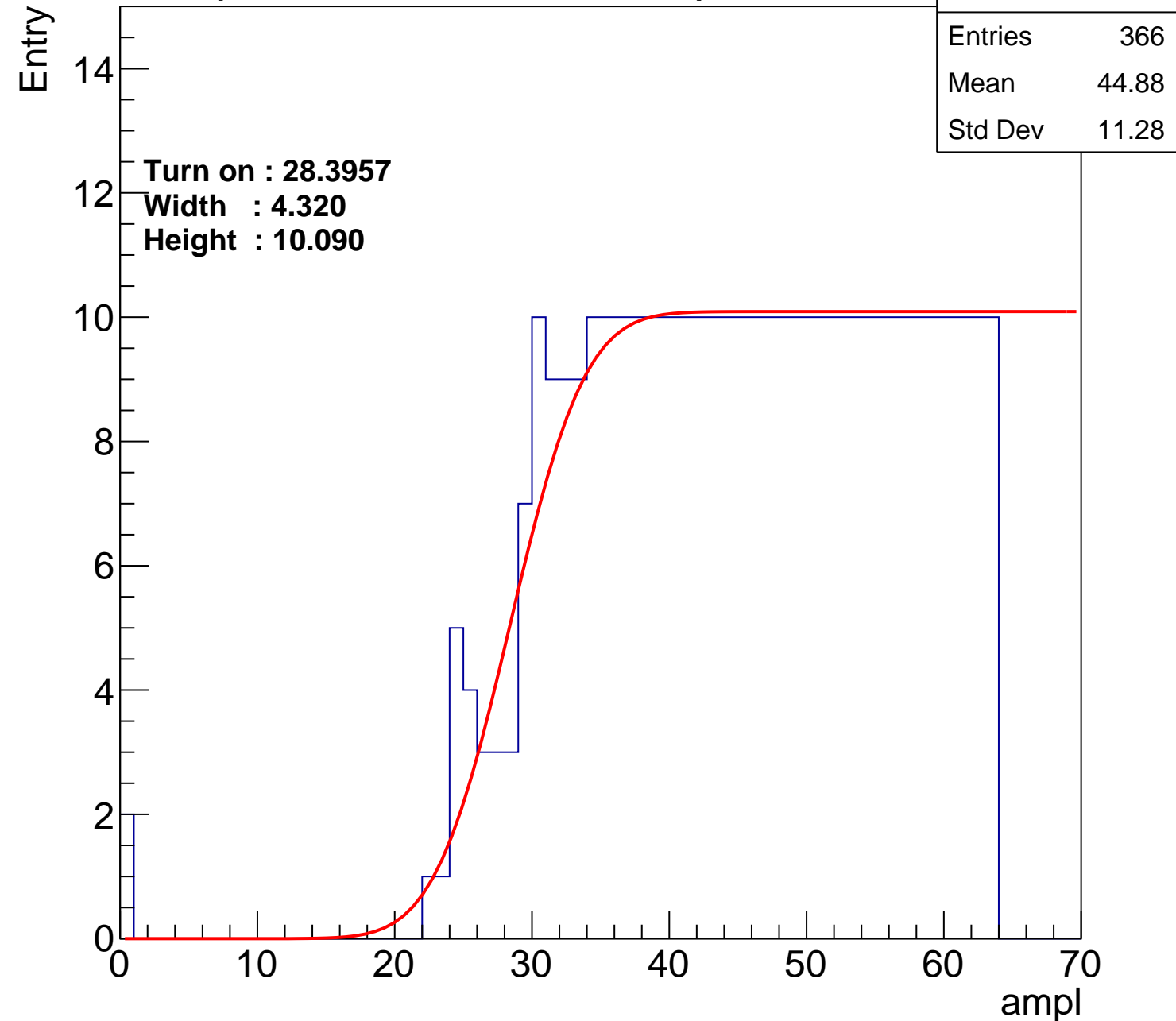
Width : 4.320

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch91

calib_packv5_042523_0143.root, FC#0, port D2

Entries	376
Mean	44.44
Std Dev	11.45

Turn on : 26.9746

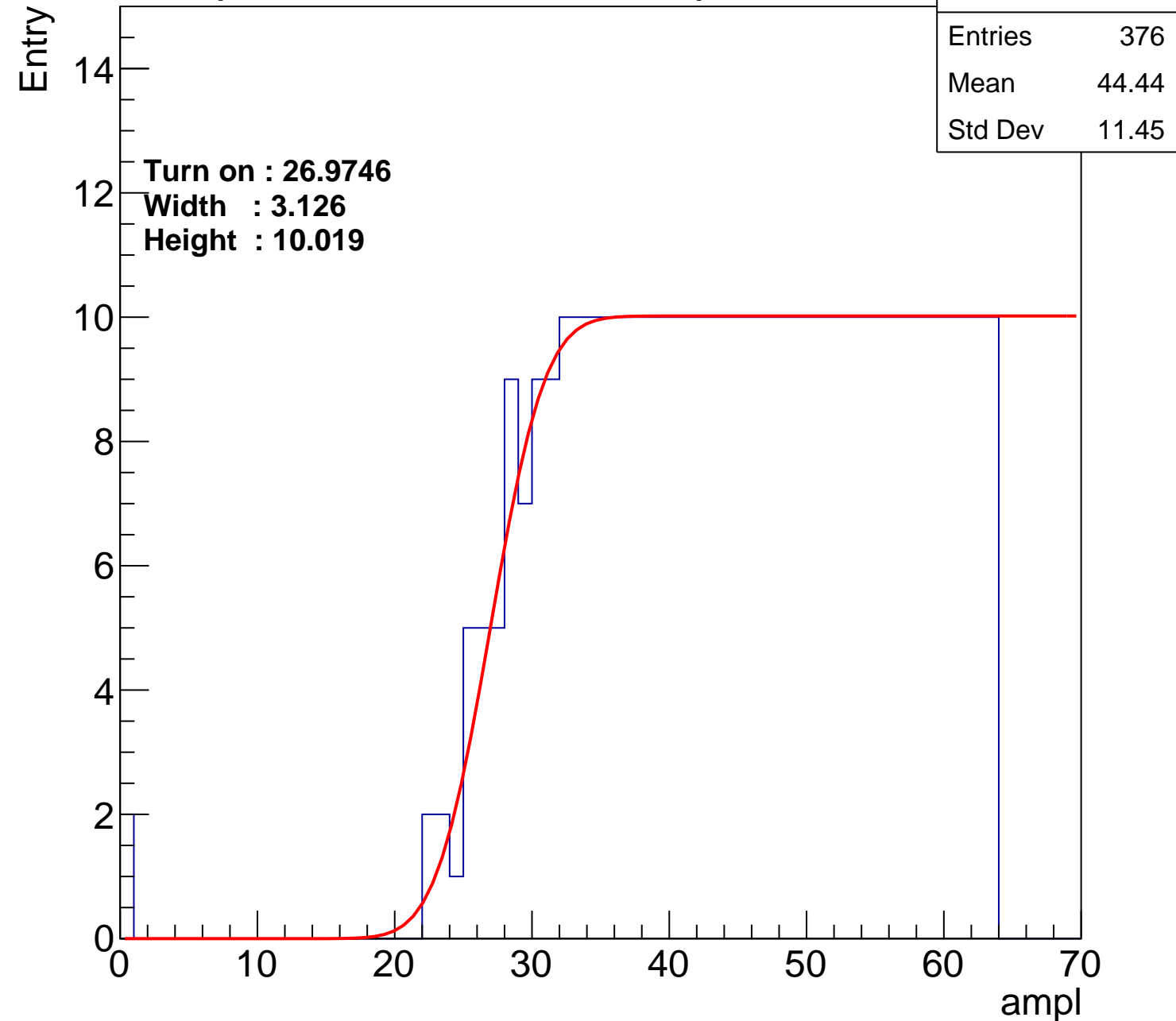
Width : 3.126

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch92

calib_packv5_042523_0143.root, FC#0, port D2

Entries	359
Mean	45.25
Std Dev	11.05

Turn on : 28.4546

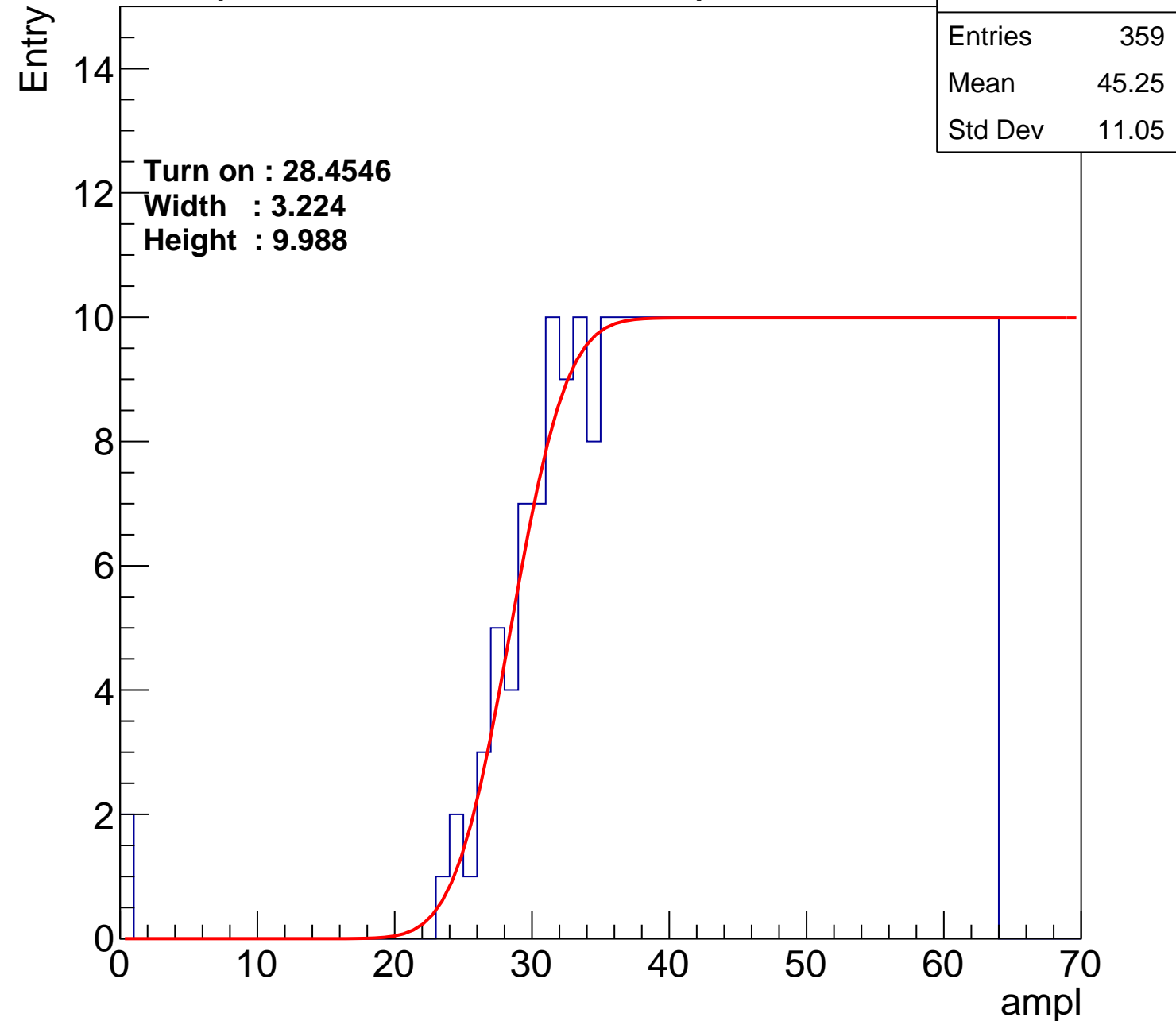
Width : 3.224

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch93

calib_packv5_042523_0143.root, FC#0, port D2

Entries	356
Mean	45.39
Std Dev	11.09

Turn on : 28.4896

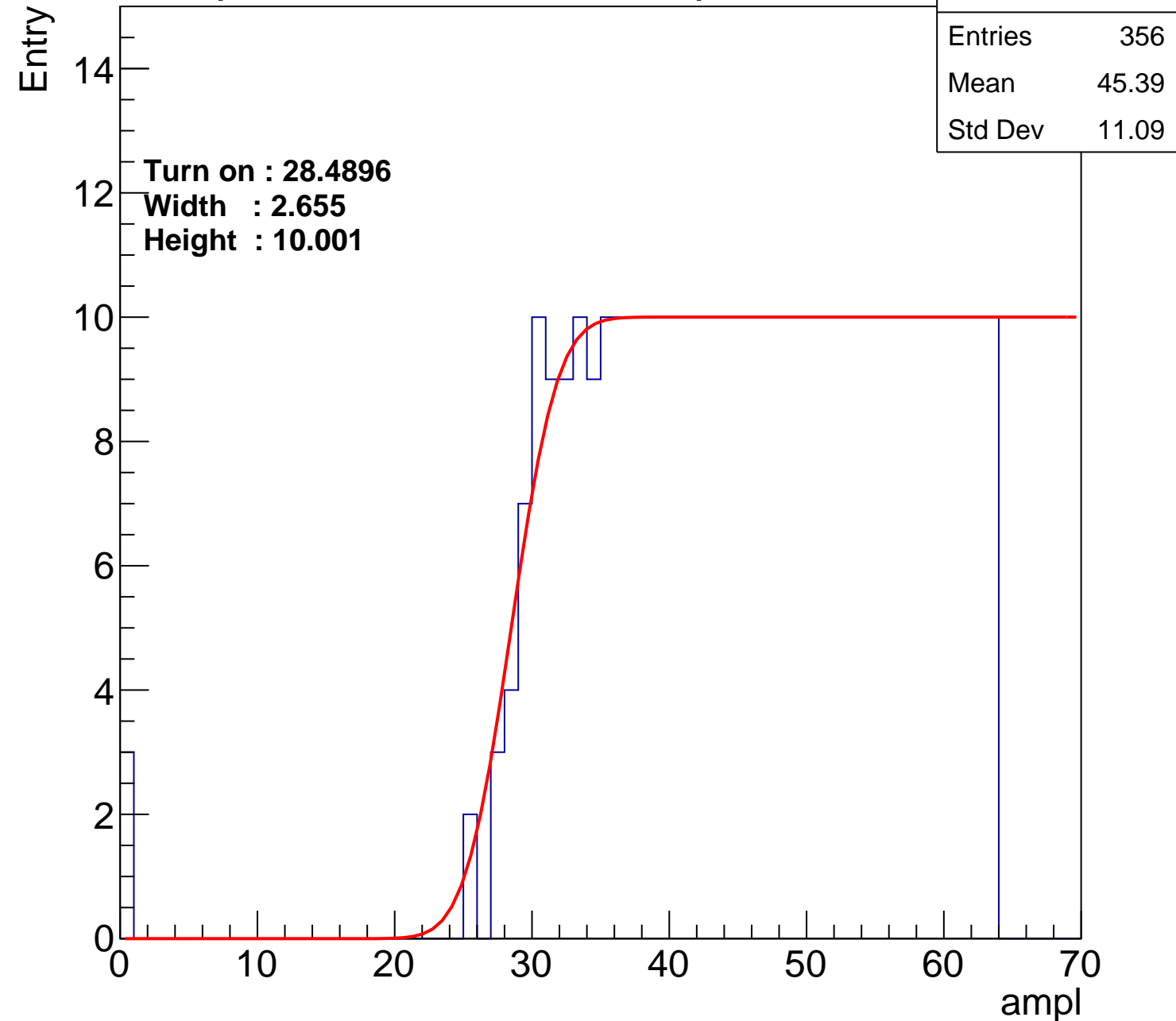
Width : 2.655

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch94

calib_packv5_042523_0143.root, FC#0, port D2

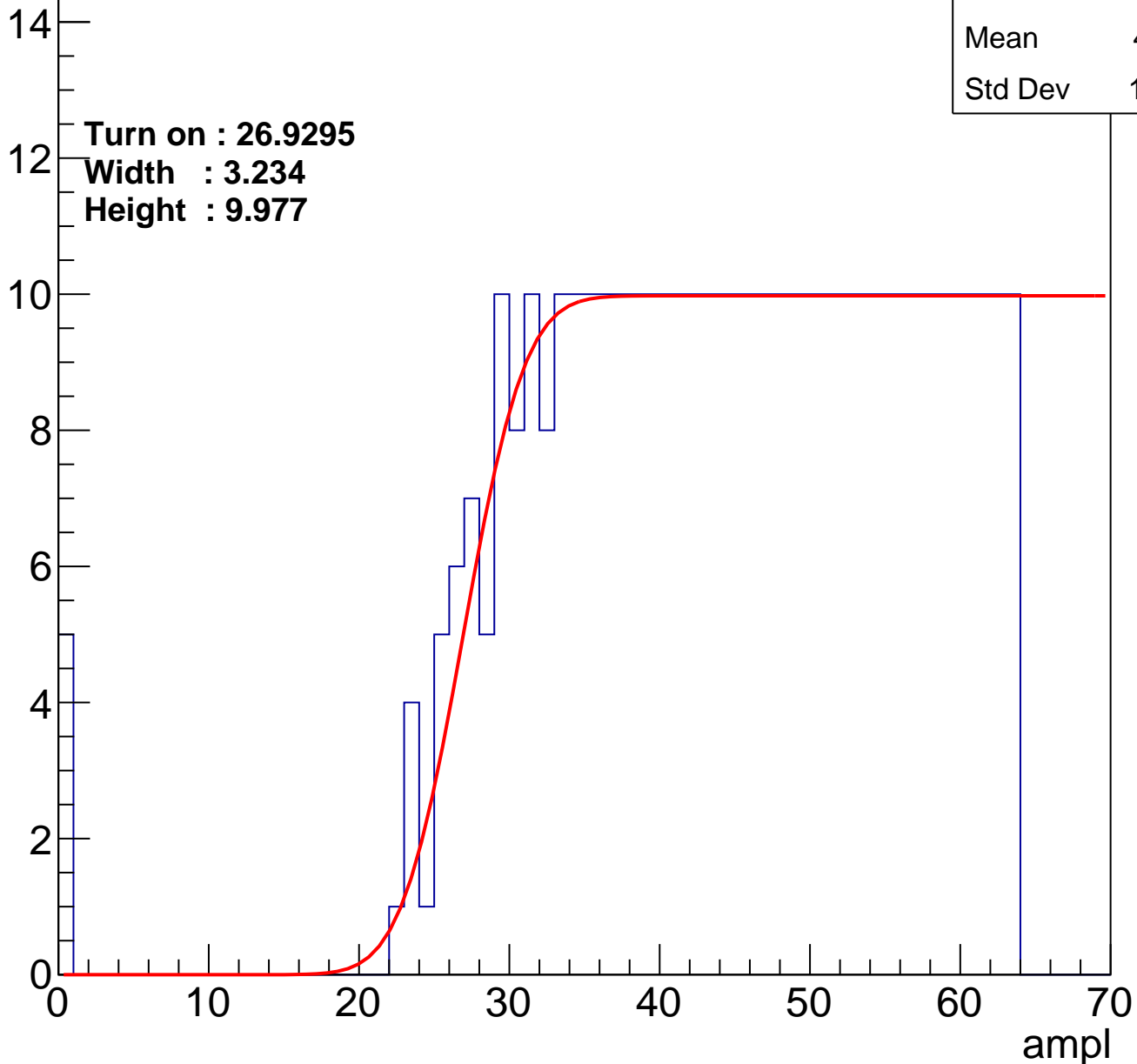
Entry

Entries	380
Mean	44.01
Std Dev	12.12

Turn on : 26.9295

Width : 3.234

Height : 9.977



B1L101S, U23-ch95

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.74
Std Dev	11.62

Turn on : 27.9400

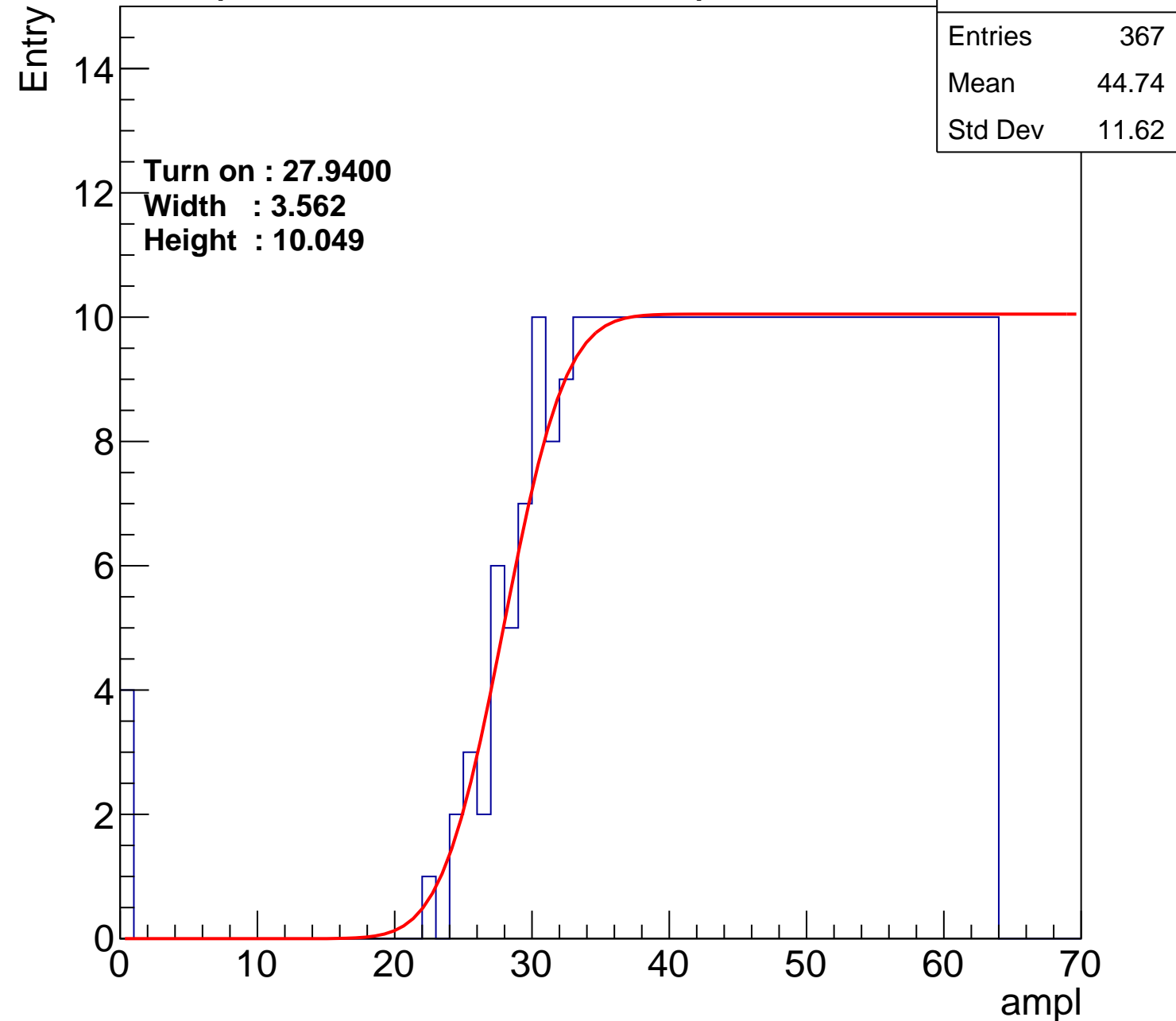
Width : 3.562

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch96

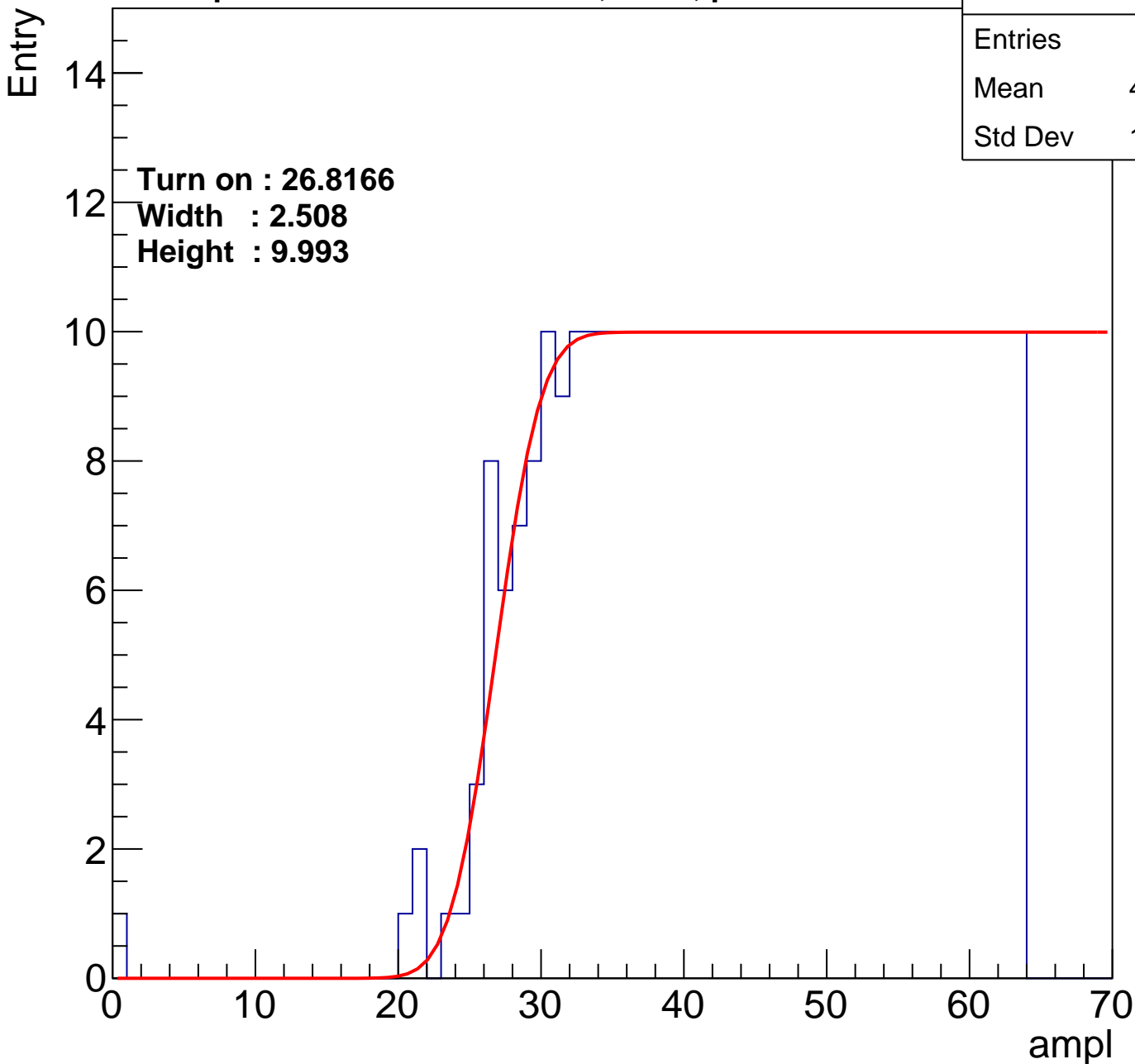
calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.46
Std Dev	11.29

Turn on : 26.8166

Width : 2.508

Height : 9.993



B1L101S, U23-ch97

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	44.95
Std Dev	11.5

Turn on : 28.4515

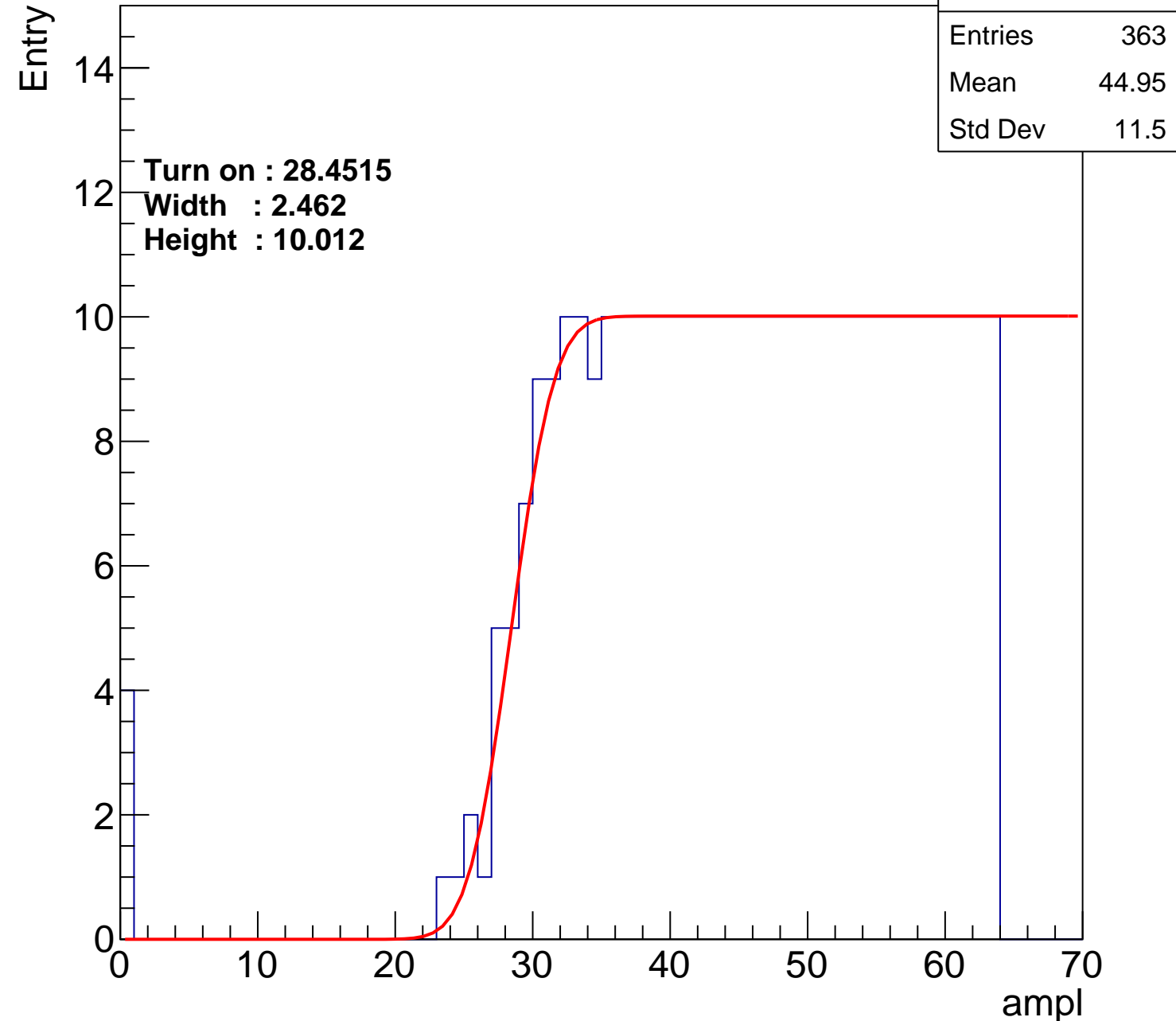
Width : 2.462

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch98

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.39
Std Dev	11.47

Turn on : 26.6865

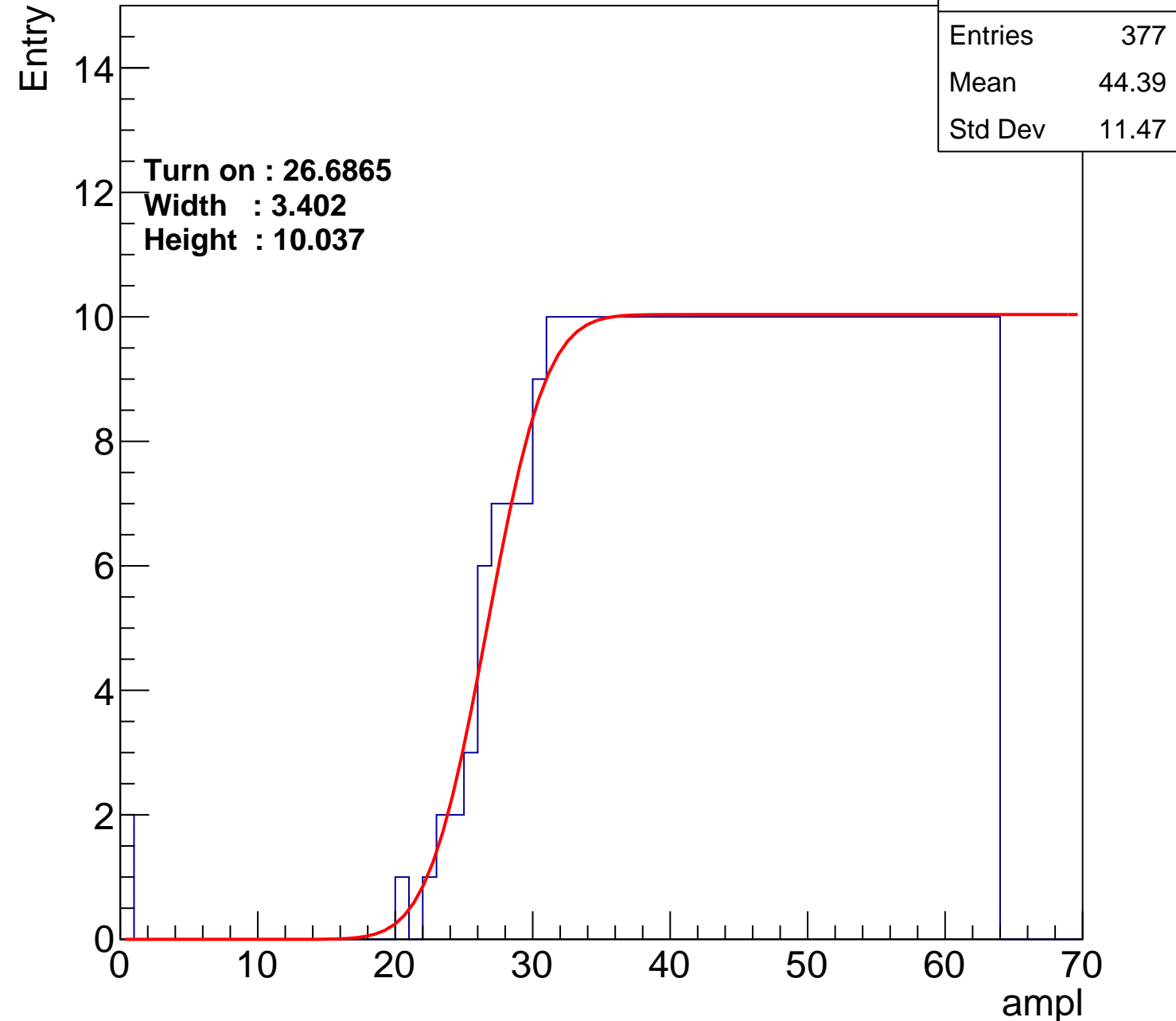
Width : 3.402

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch99

calib_packv5_042523_0143.root, FC#0, port D2

Entries	335
Mean	46.52
Std Dev	10.19

Turn on : 31.2313

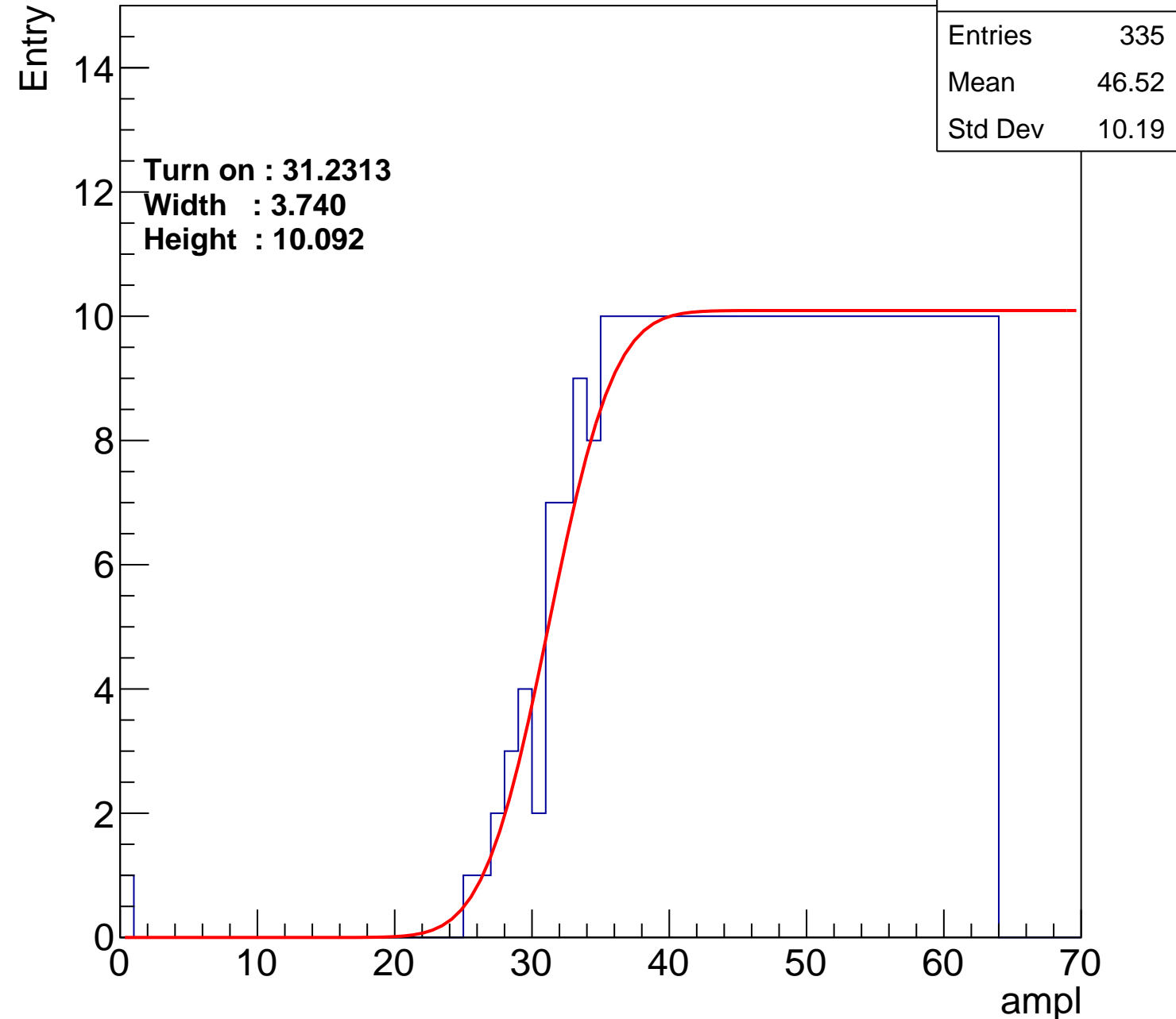
Width : 3.740

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch100

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.9568

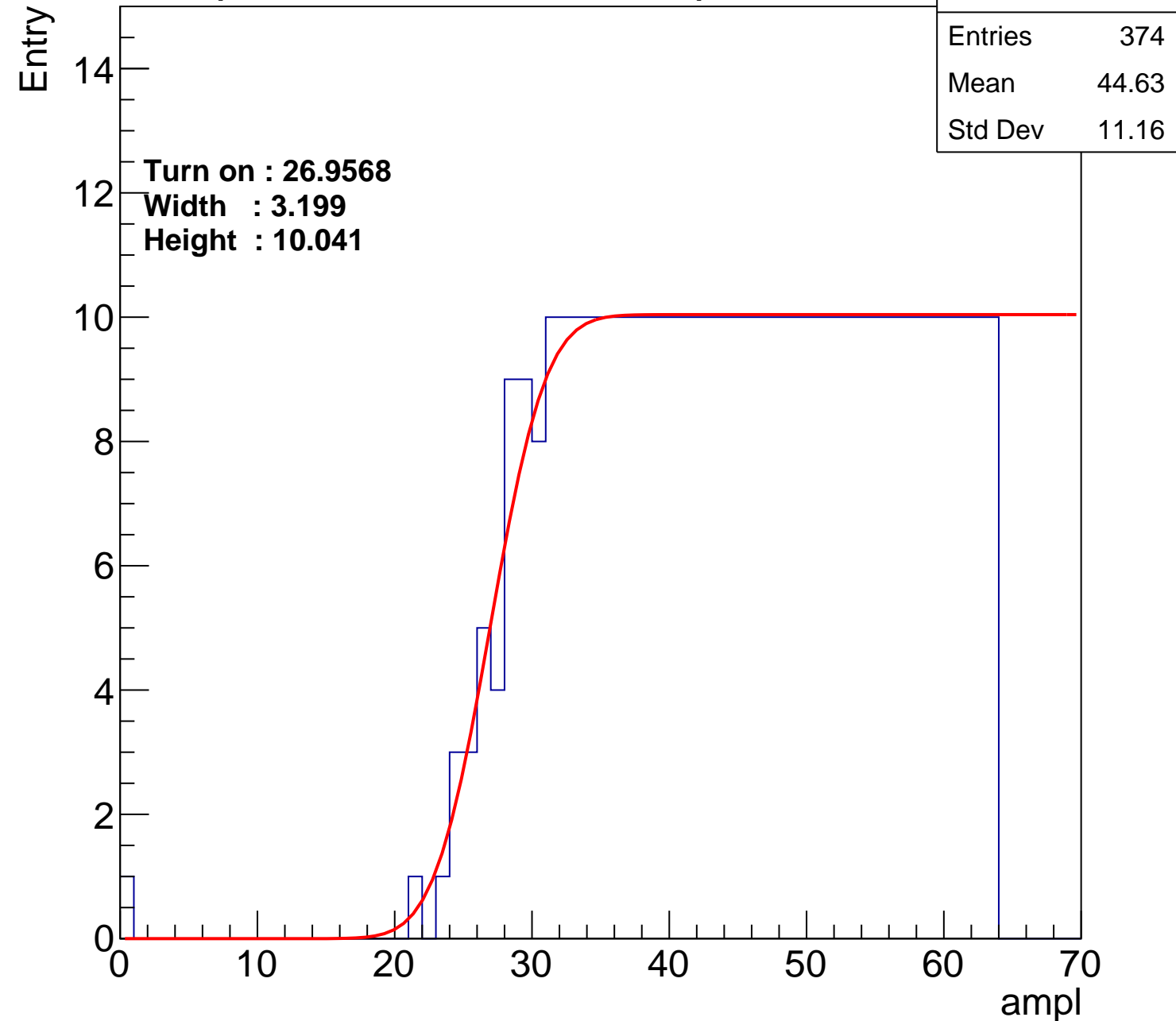
Width : 3.199

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch101

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.39
Std Dev	11.79

Turn on : 27.2714

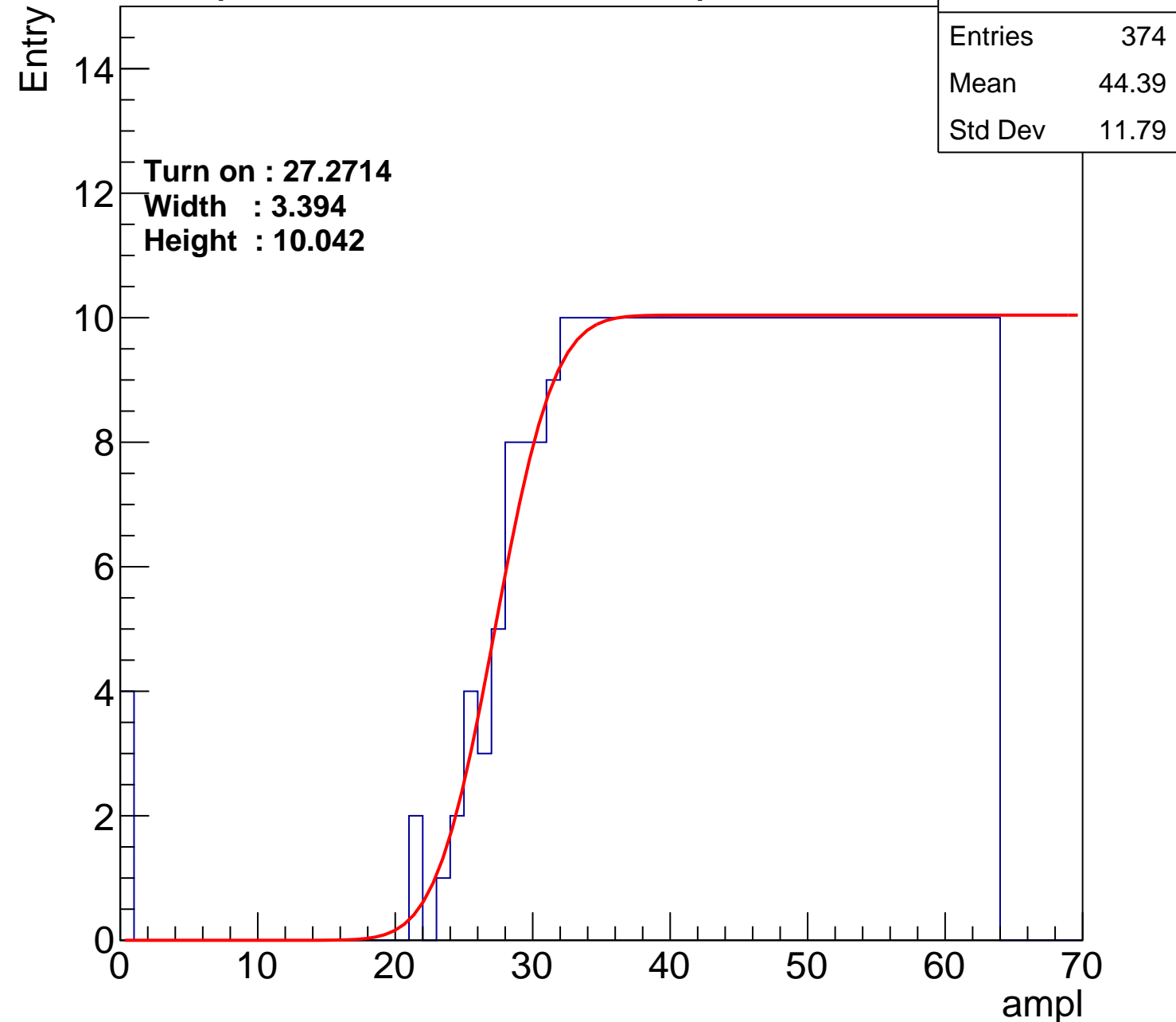
Width : 3.394

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch102

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.82
Std Dev	11.26

Turn on : 27.3015

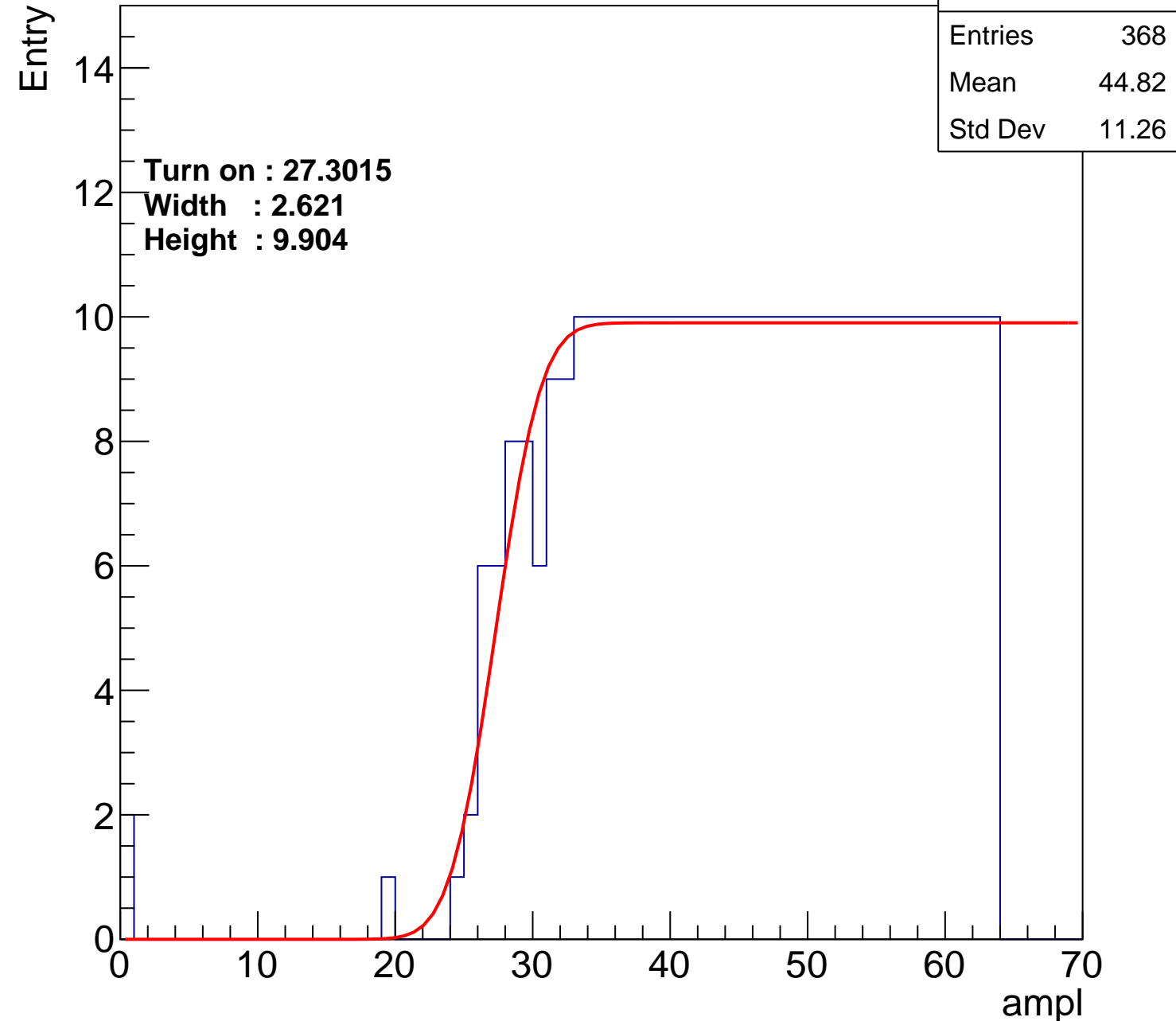
Width : 2.621

Height : 9.904

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch103

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.9
Std Dev	11.24

Turn on : 27.5164

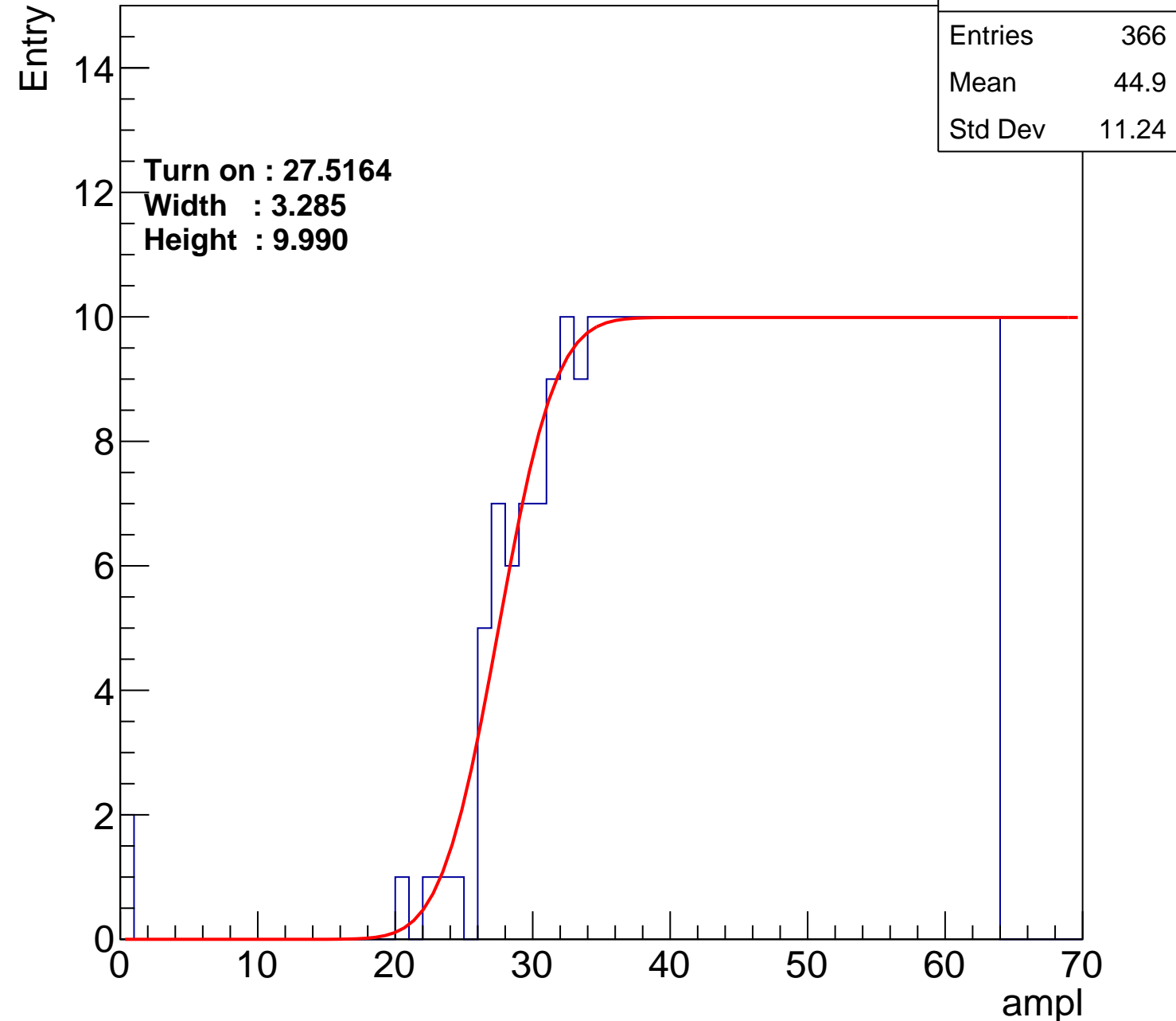
Width : 3.285

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch104

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.72
Std Dev	11.32

Turn on : 27.6053

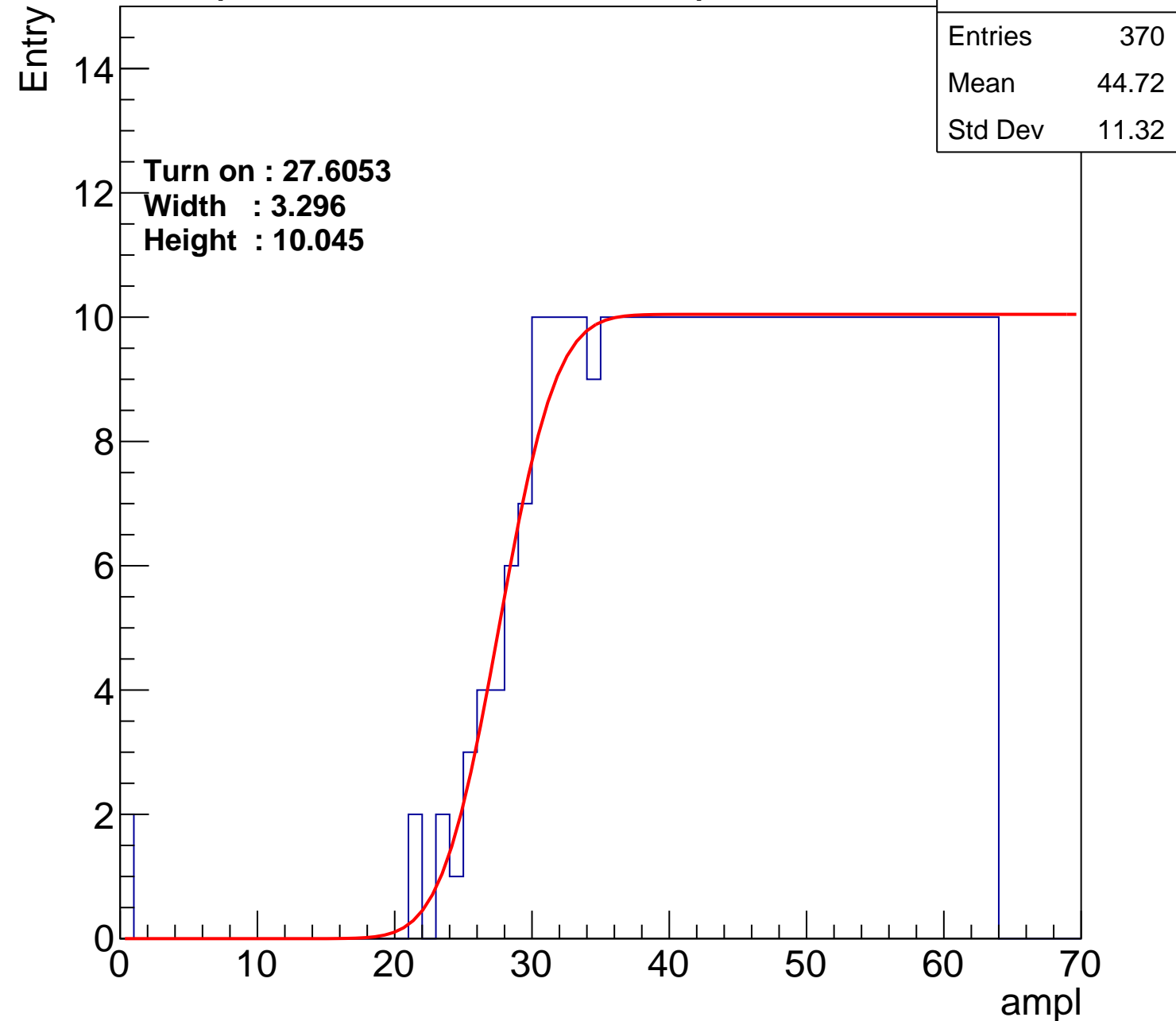
Width : 3.296

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch105

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.26
Std Dev	11.83

Turn on : 26.8970

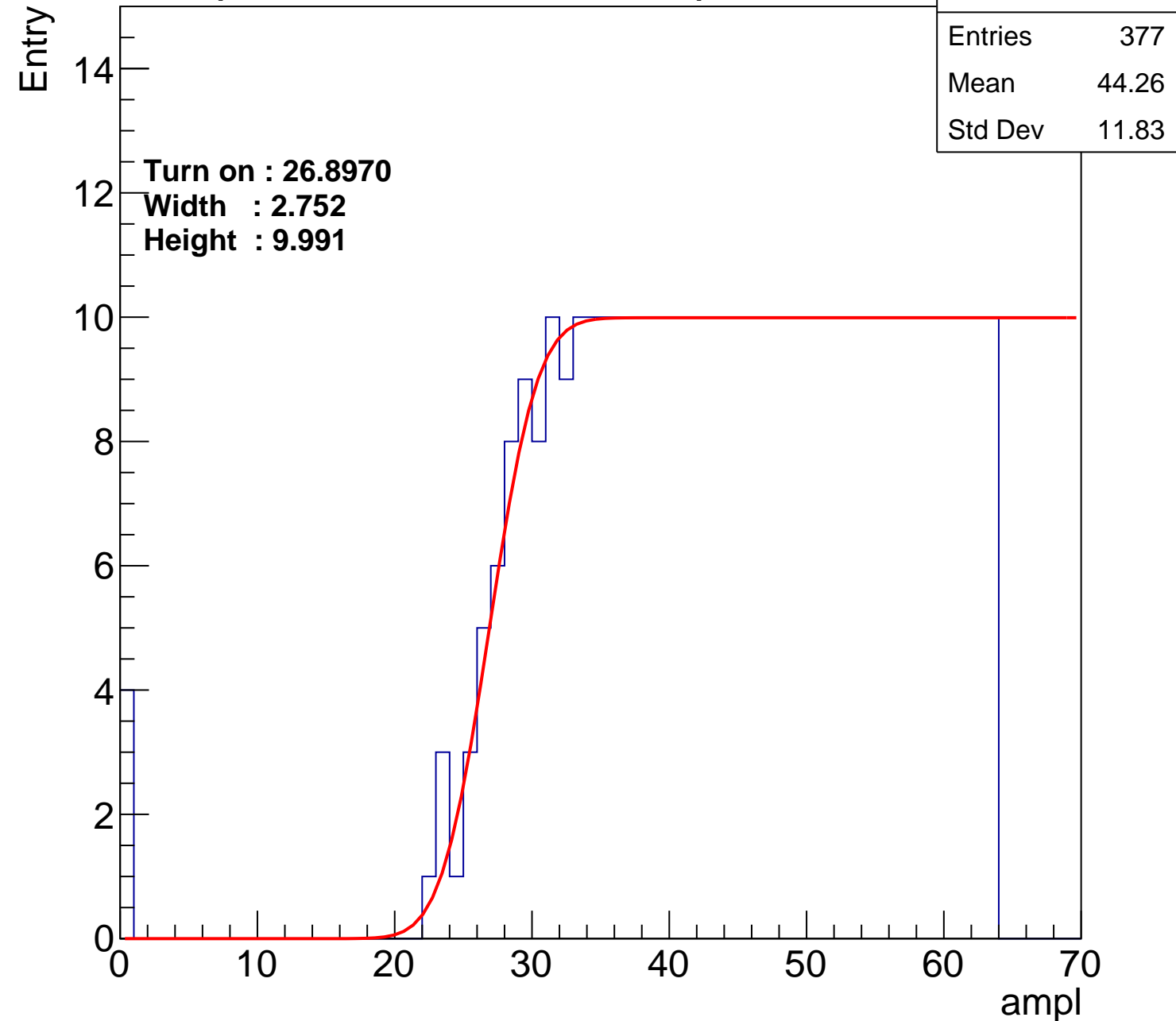
Width : 2.752

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch106

calib_packv5_042523_0143.root, FC#0, port D2

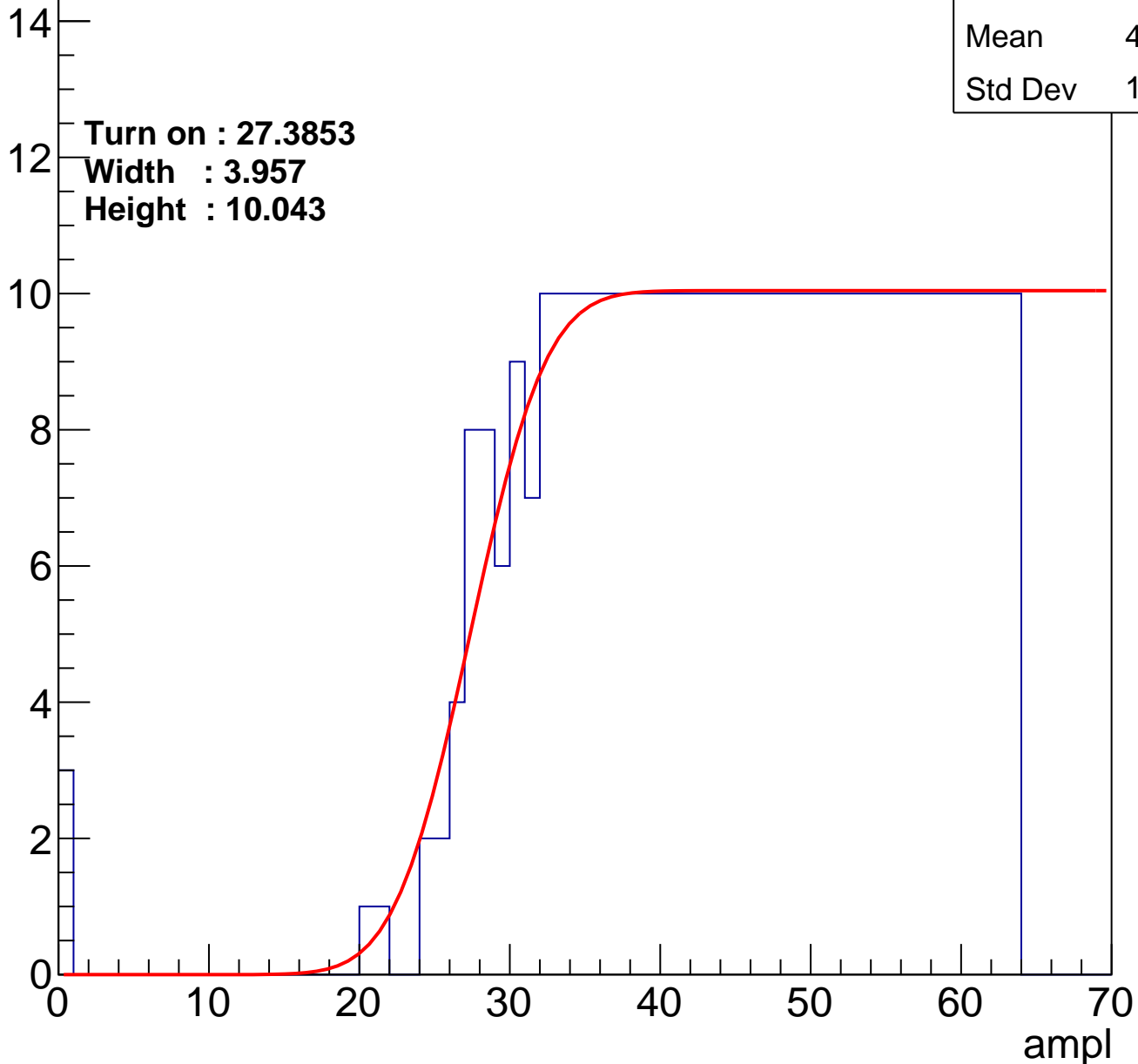
Entries	371
Mean	44.59
Std Dev	11.55

Turn on : 27.3853

Width : 3.957

Height : 10.043

Entry



B1L101S, U23-ch107

calib_packv5_042523_0143.root, FC#0, port D2

Entries	349
Mean	45.7
Std Dev	10.86

Turn on : 29.2296

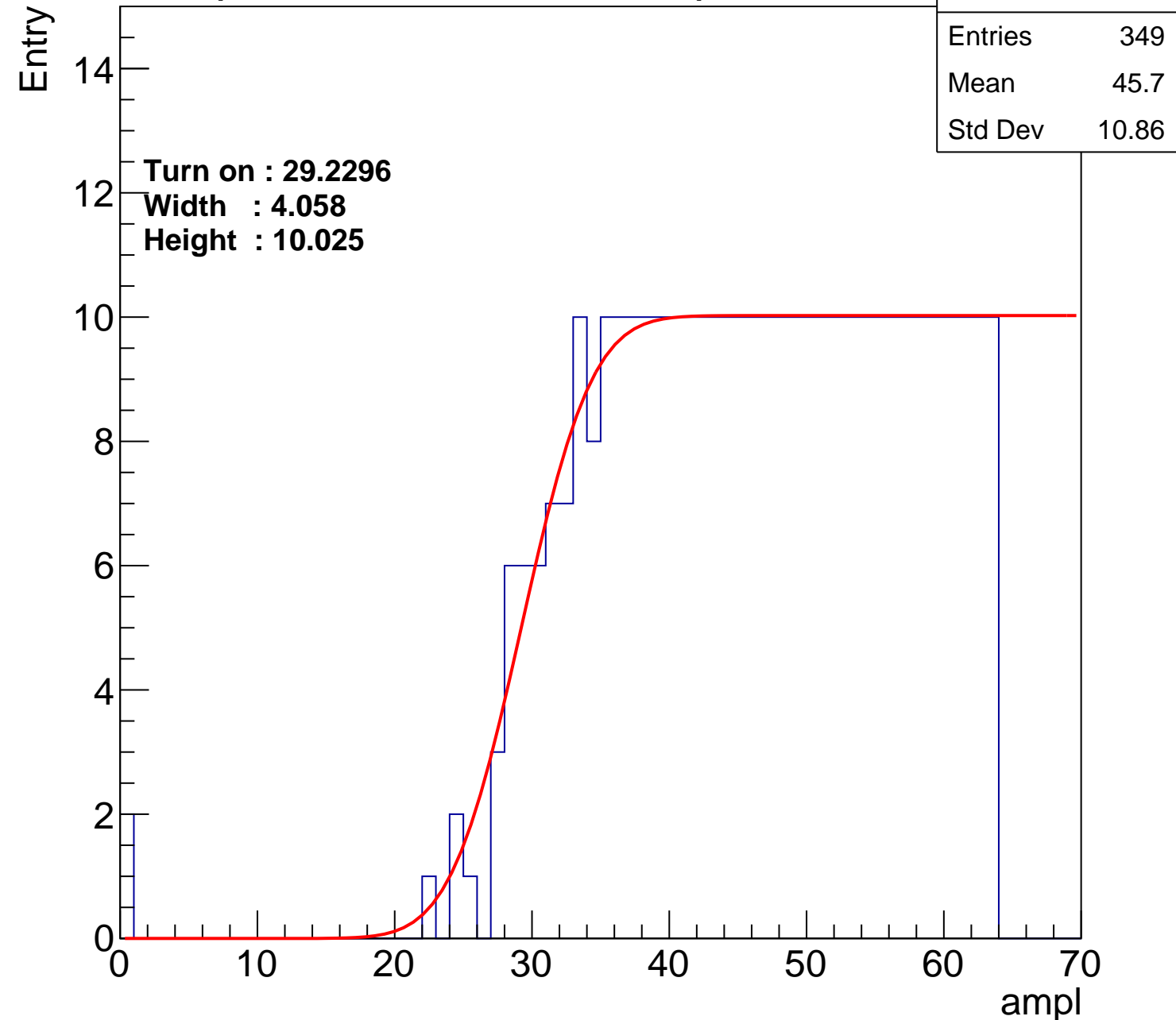
Width : 4.058

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch108

calib_packv5_042523_0143.root, FC#0, port D2

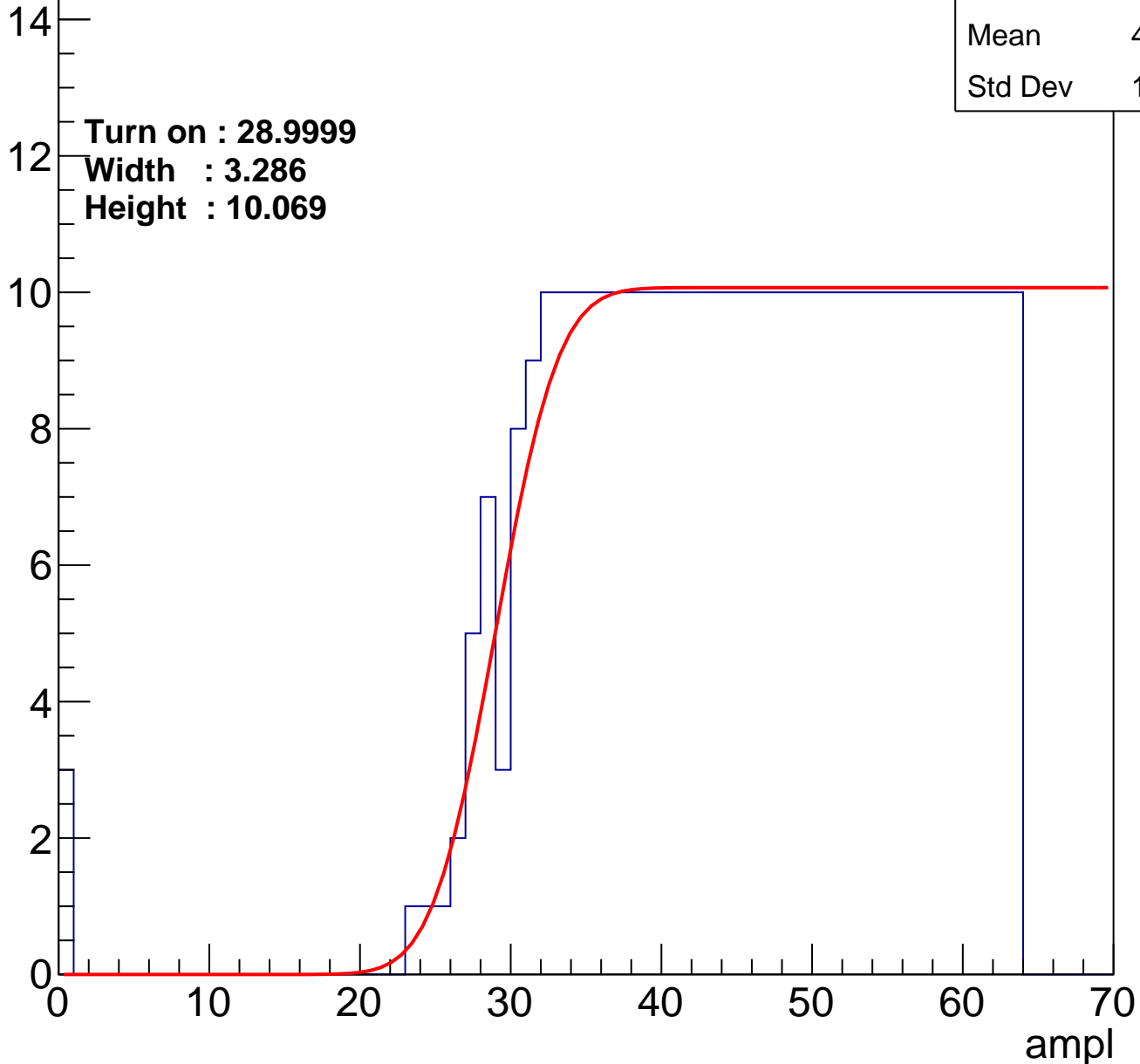
Entries	360
Mean	45.17
Std Dev	11.23

Turn on : 28.9999

Width : 3.286

Height : 10.069

Entry



B1L101S, U23-ch109

calib_packv5_042523_0143.root, FC#0, port D2

Entries	348
Mean	45.67
Std Dev	11.08

Turn on : 30.1777

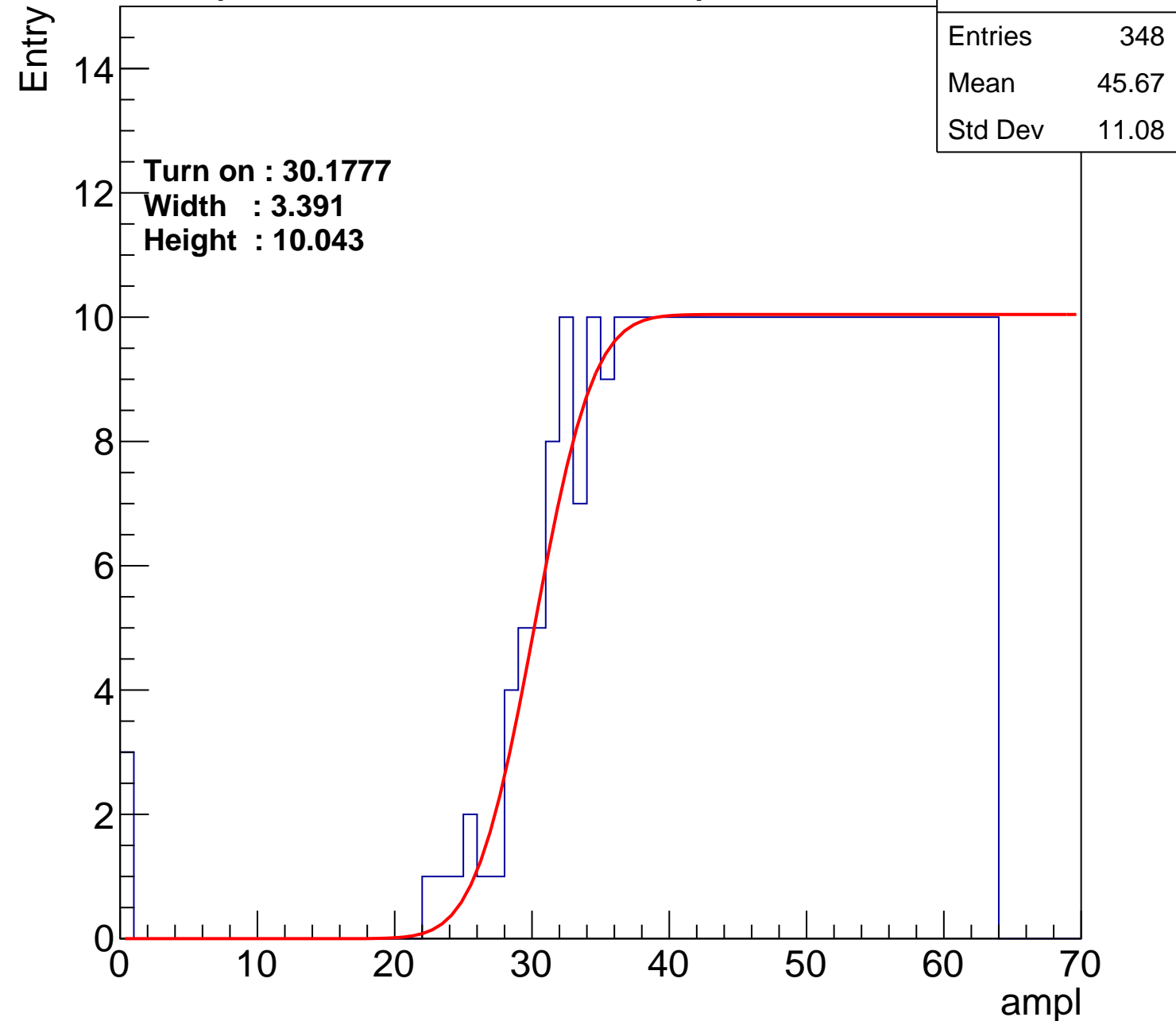
Width : 3.391

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch110

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.25
Std Dev	11.85

Turn on : 27.0481

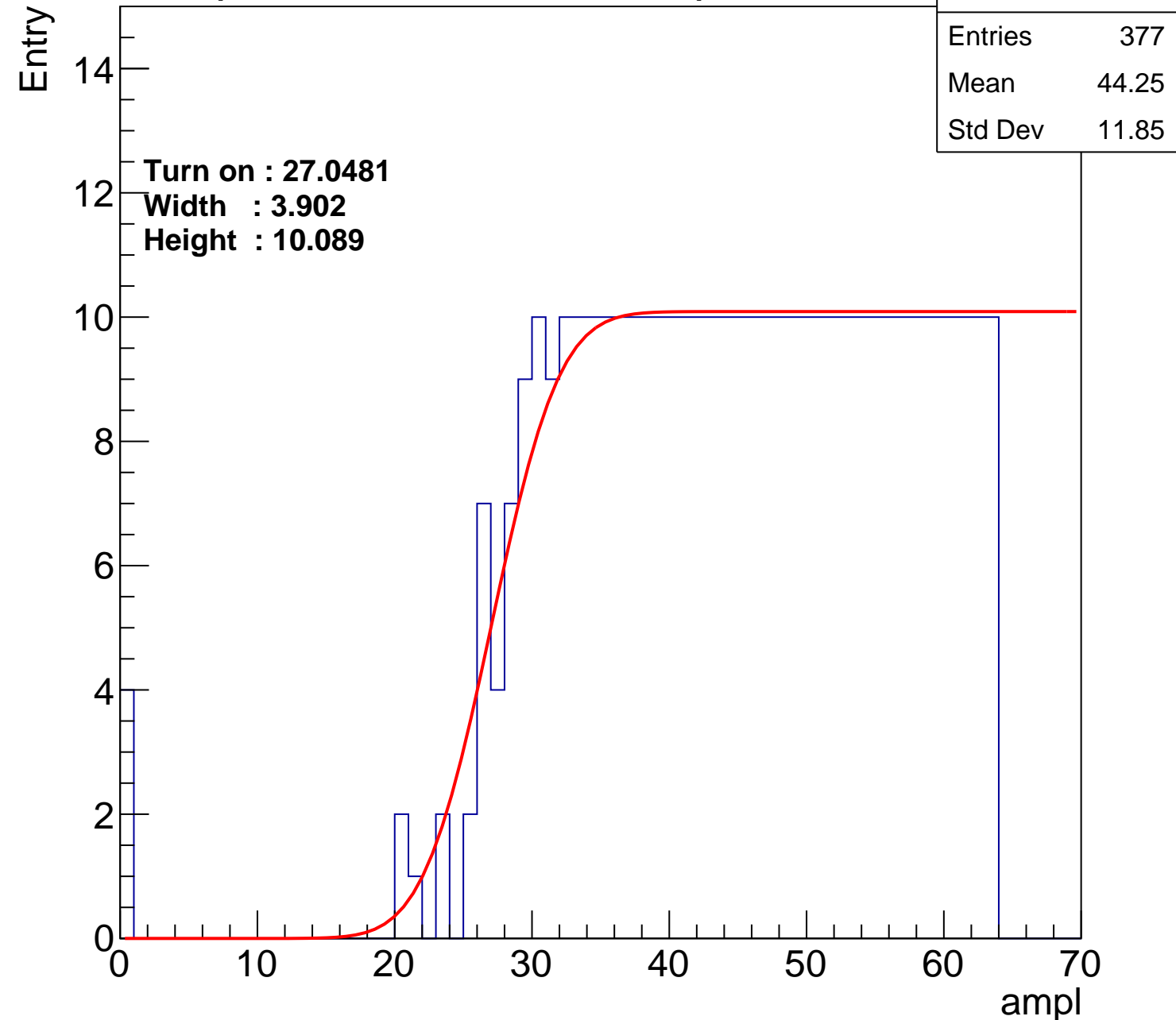
Width : 3.902

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch111

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.2259

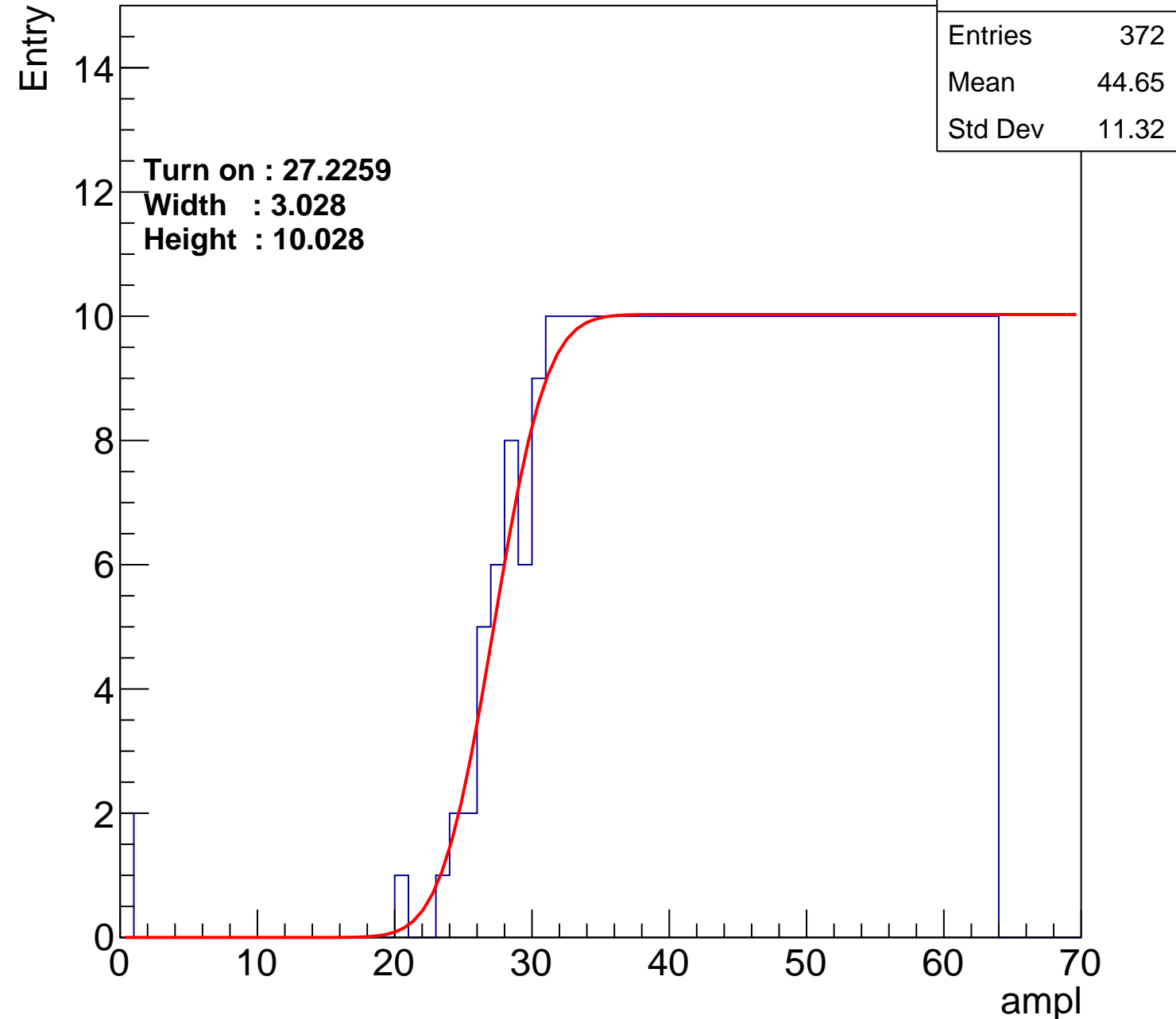
Width : 3.028

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch112

calib_packv5_042523_0143.root, FC#0, port D2

Entries	365
Mean	44.92
Std Dev	11.34

Turn on : 28.1637

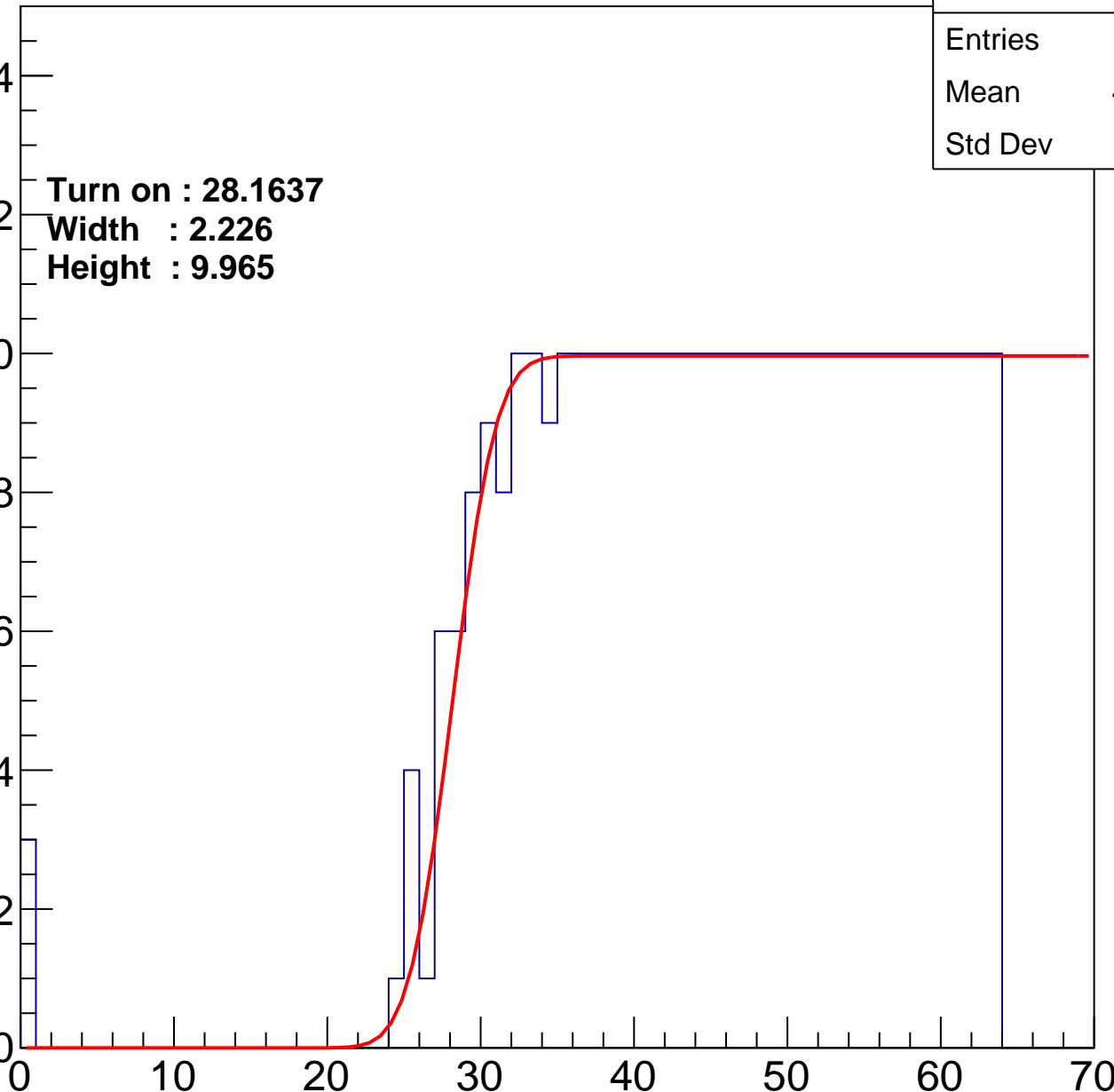
Width : 2.226

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch113

calib_packv5_042523_0143.root, FC#0, port D2

Entries	390
Mean	43.65
Std Dev	12.09

Turn on : 26.0047

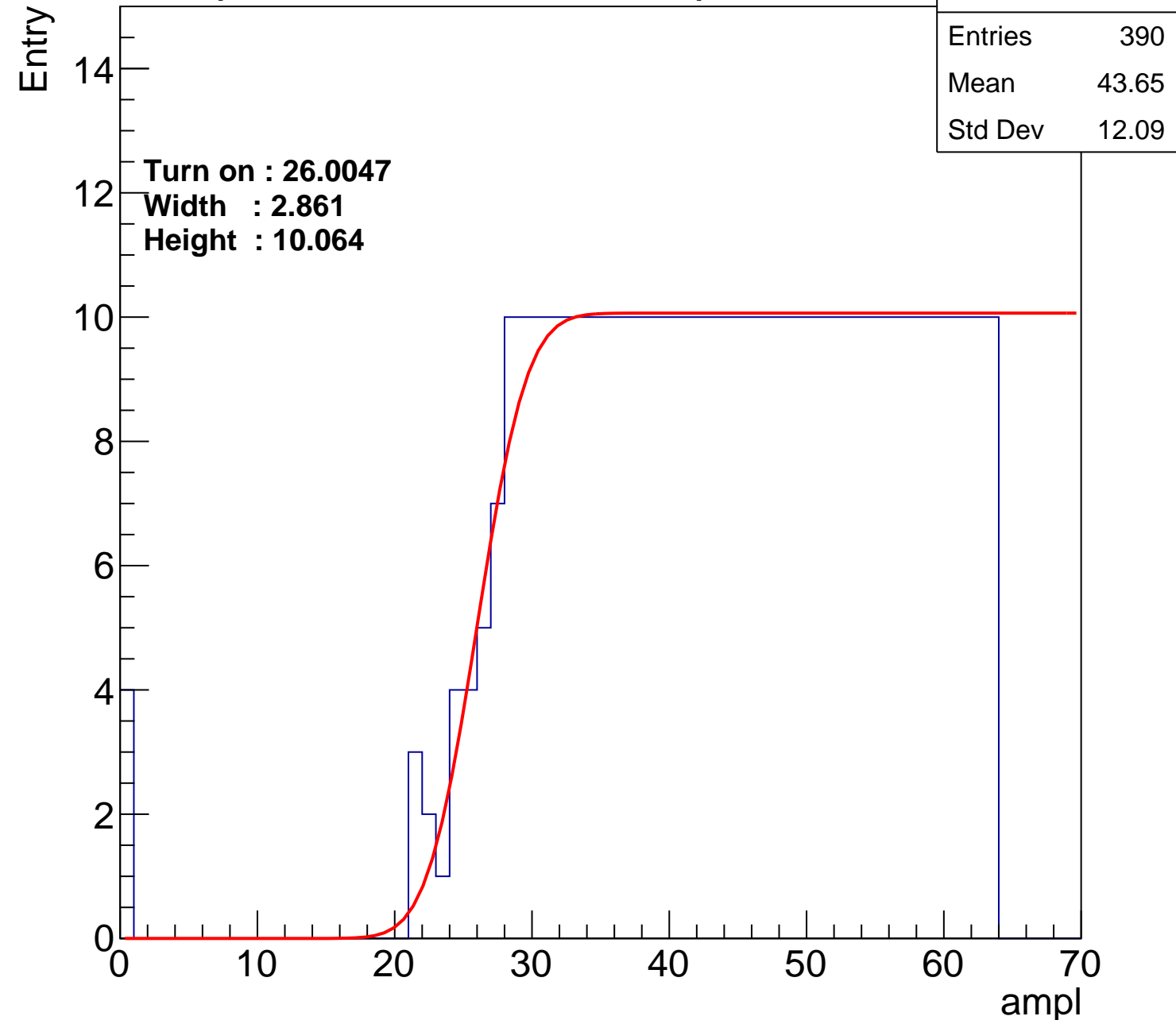
Width : 2.861

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch114

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.72
Std Dev	11.14

Turn on : 27.6596

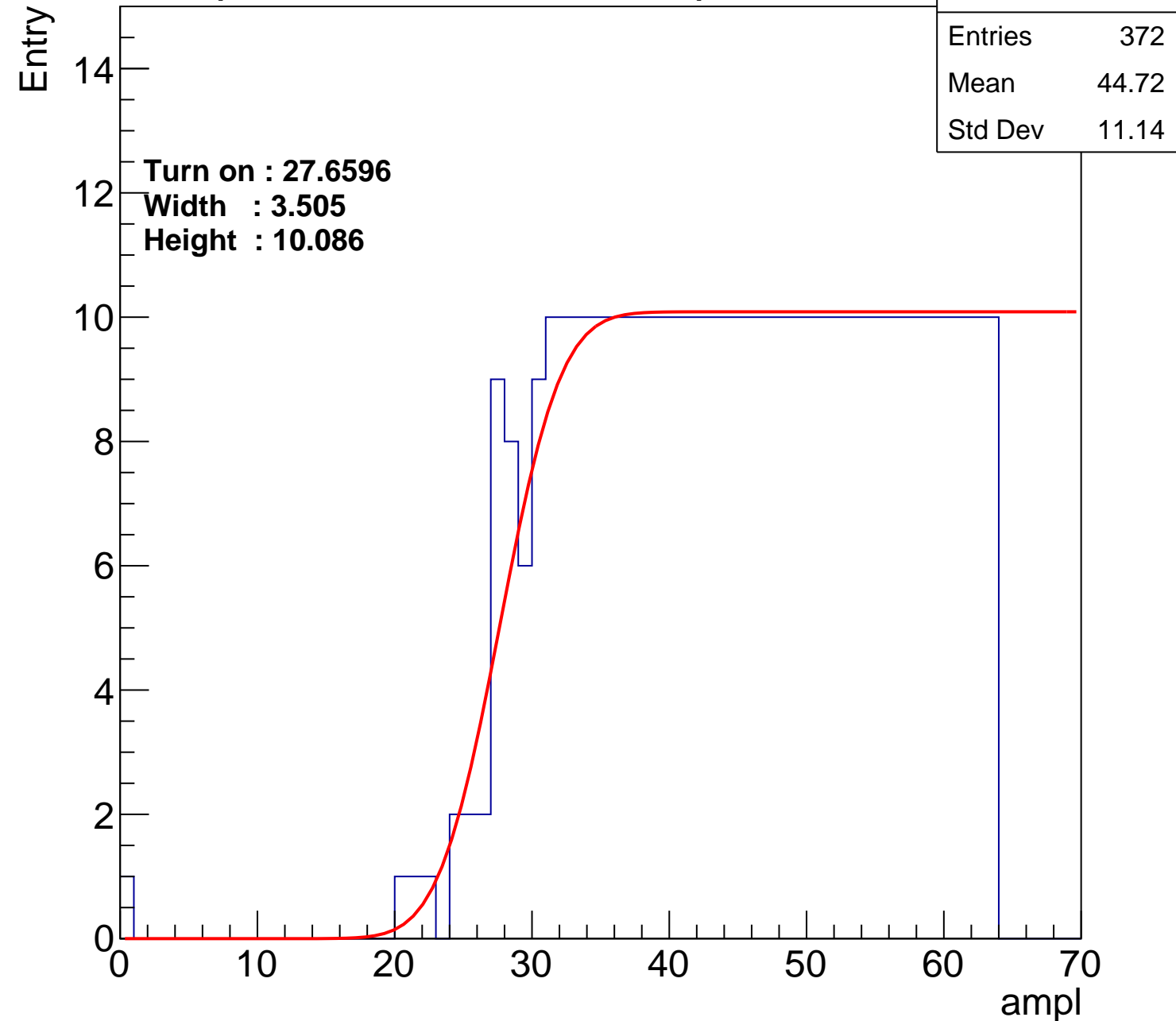
Width : 3.505

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch115

calib_packv5_042523_0143.root, FC#0, port D2

Entries	376
Mean	44.32
Std Dev	11.7

Turn on : 27.1881

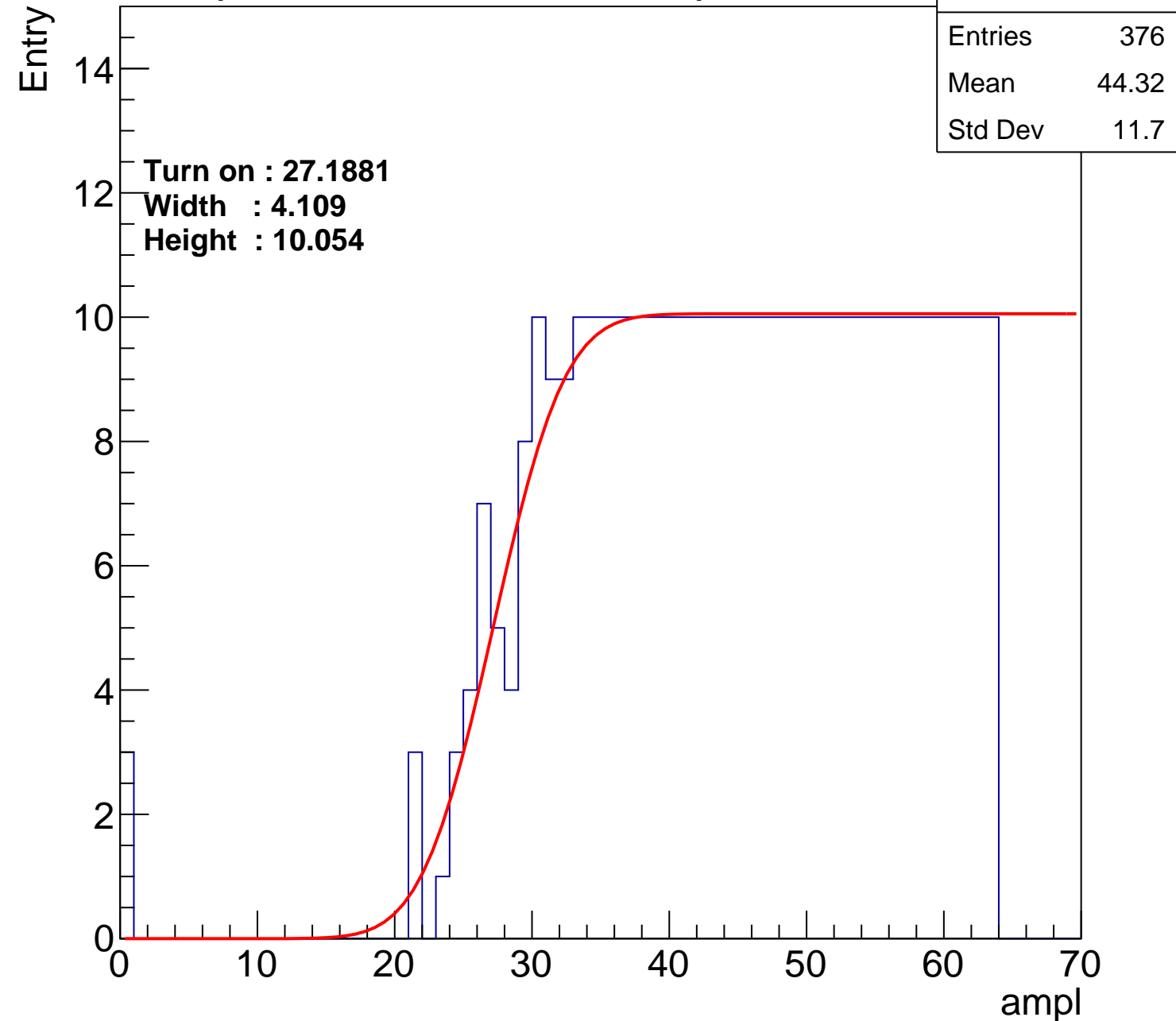
Width : 4.109

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch116

calib_packv5_042523_0143.root, FC#0, port D2

Entries	356
Mean	45.53
Std Dev	10.67

Turn on : 28.3855

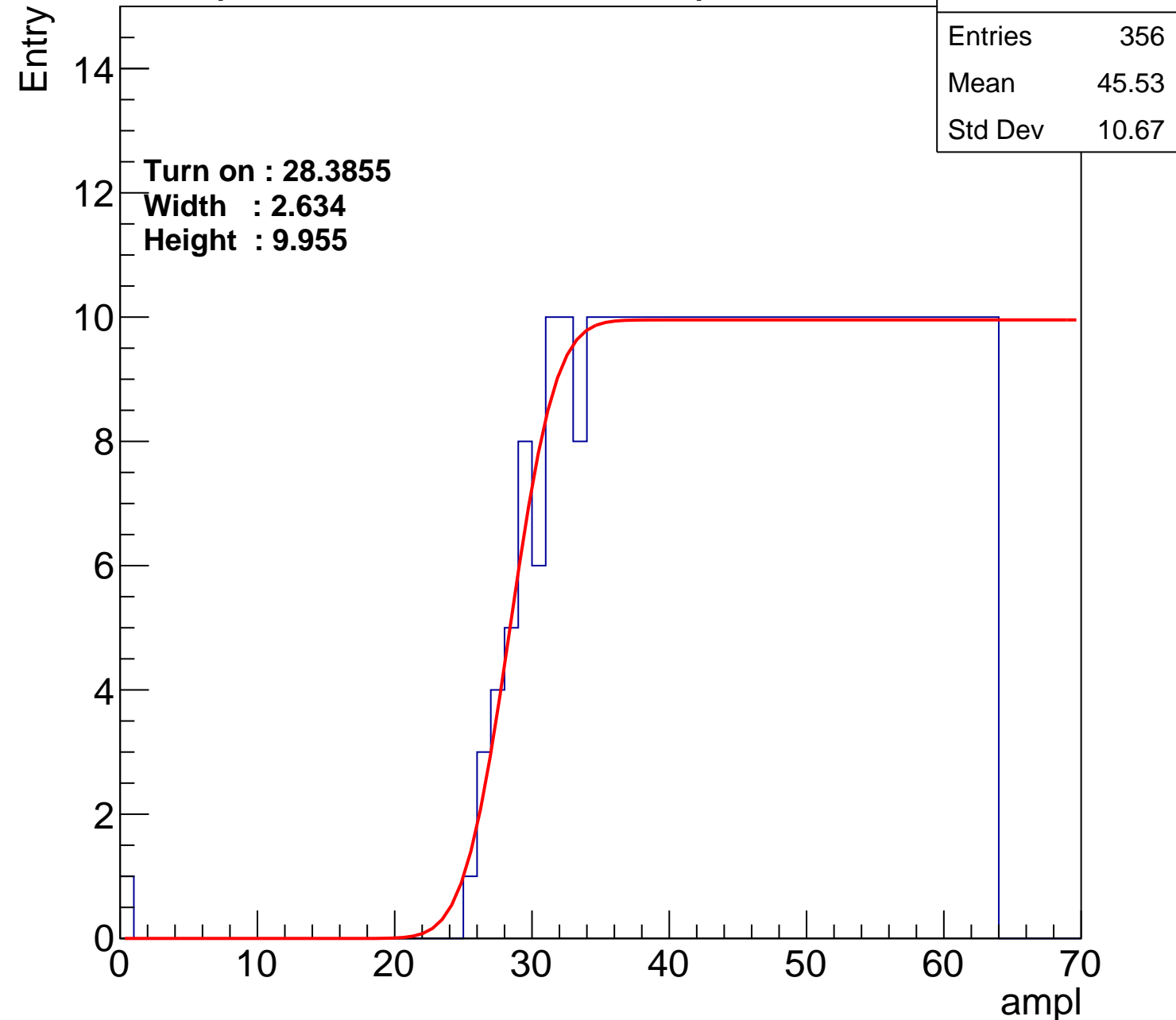
Width : 2.634

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch117

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.45
Std Dev	11.76

Turn on : 27.6370

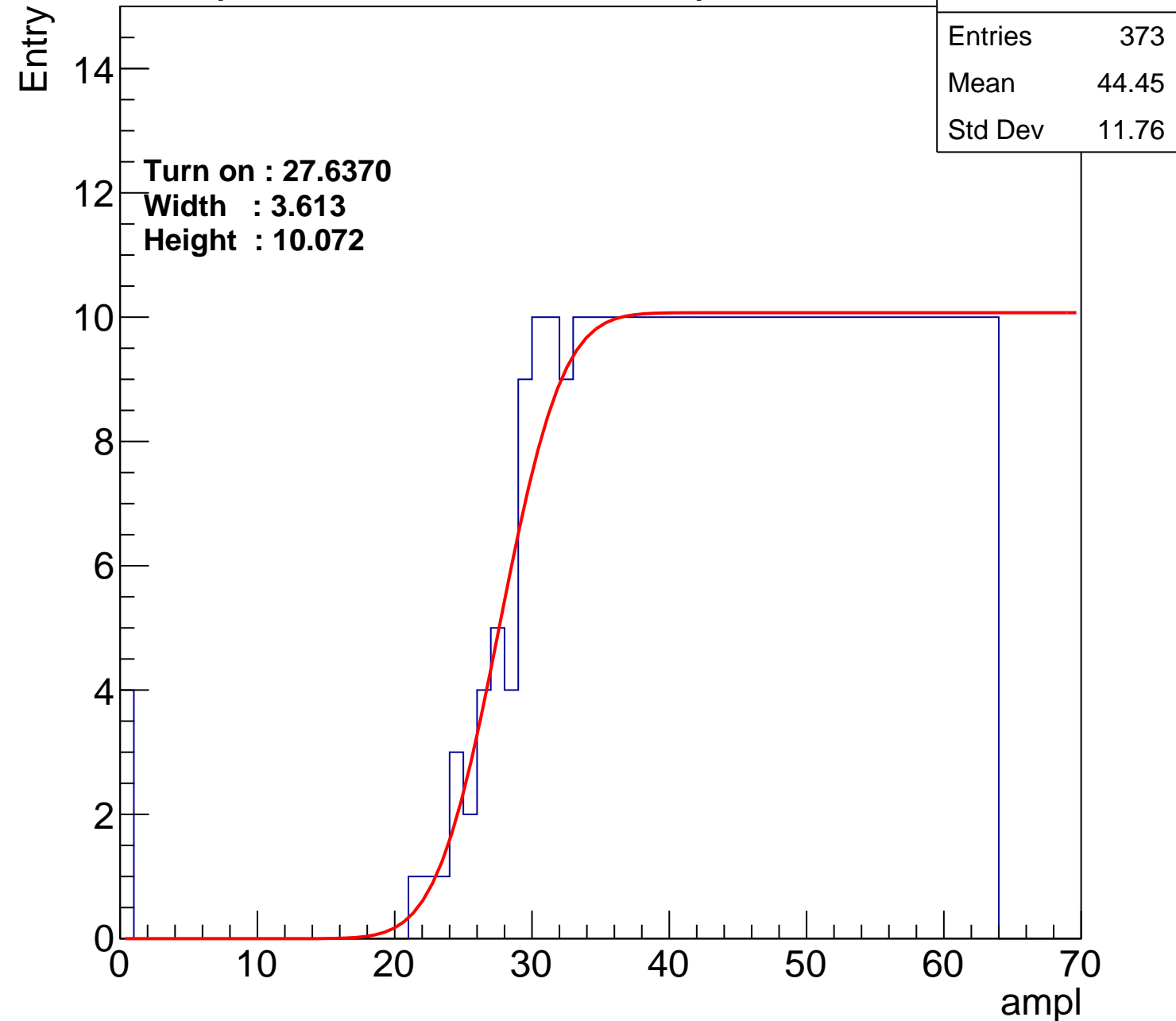
Width : 3.613

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch118

calib_packv5_042523_0143.root, FC#0, port D2

Entries	364
Mean	44.95
Std Dev	11.27

Turn on : 28.1156

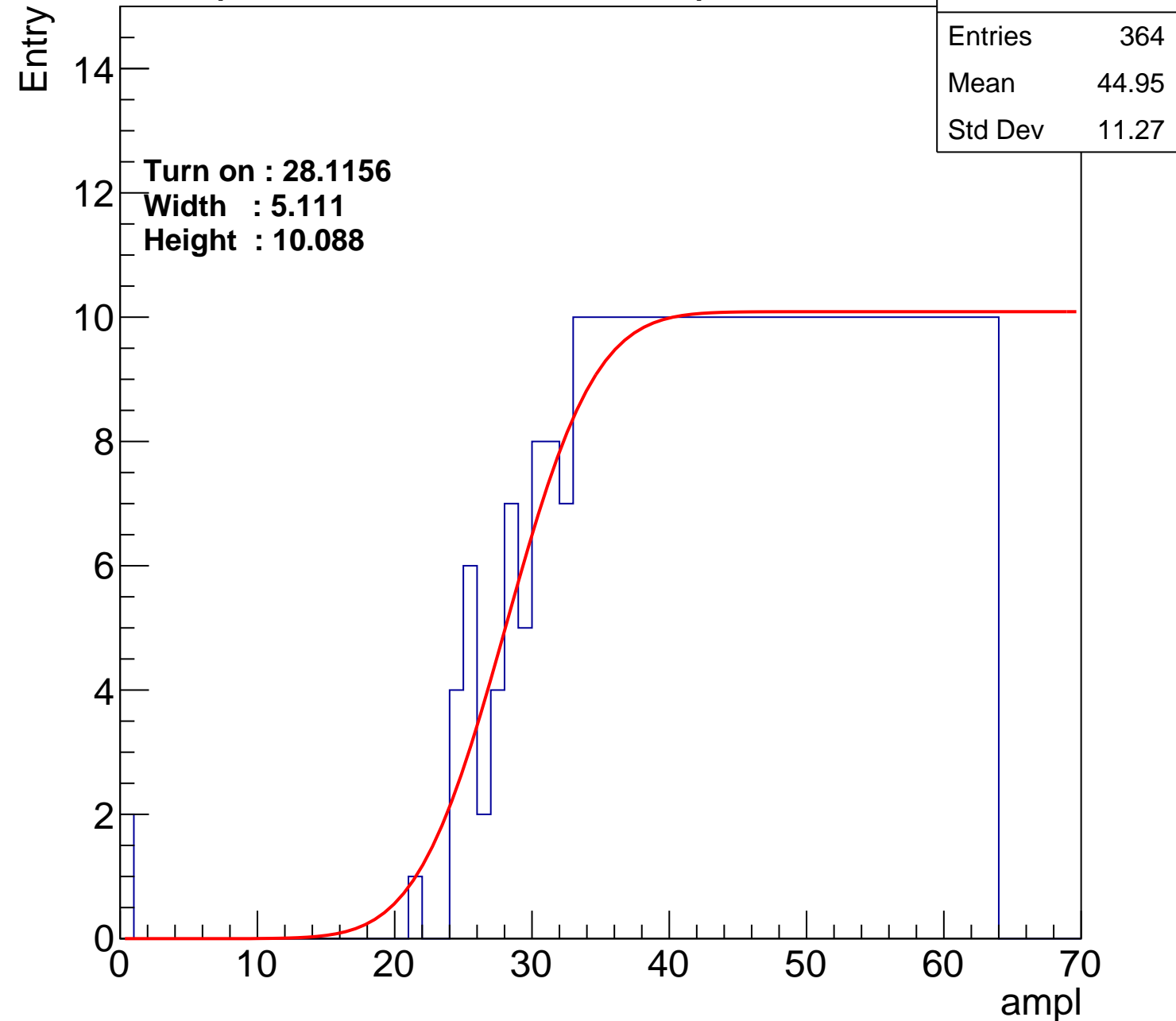
Width : 5.111

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch119

calib_packv5_042523_0143.root, FC#0, port D2

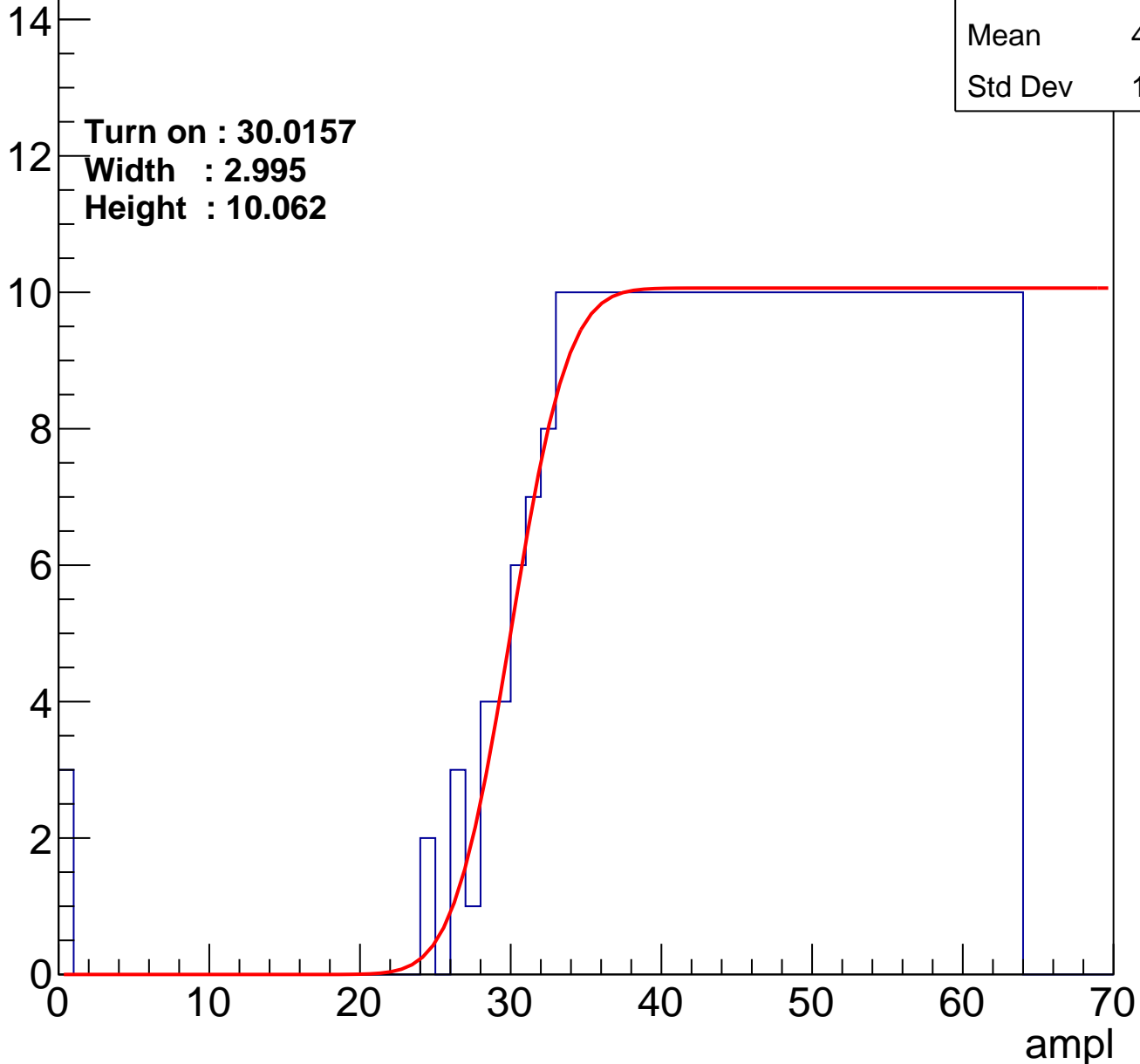
Entries	348
Mean	45.73
Std Dev	10.99

Turn on : 30.0157

Width : 2.995

Height : 10.062

Entry



B1L101S, U23-ch120

calib_packv5_042523_0143.root, FC#0, port D2

Entries	391
Mean	43.46
Std Dev	12.47

Turn on : 25.6641

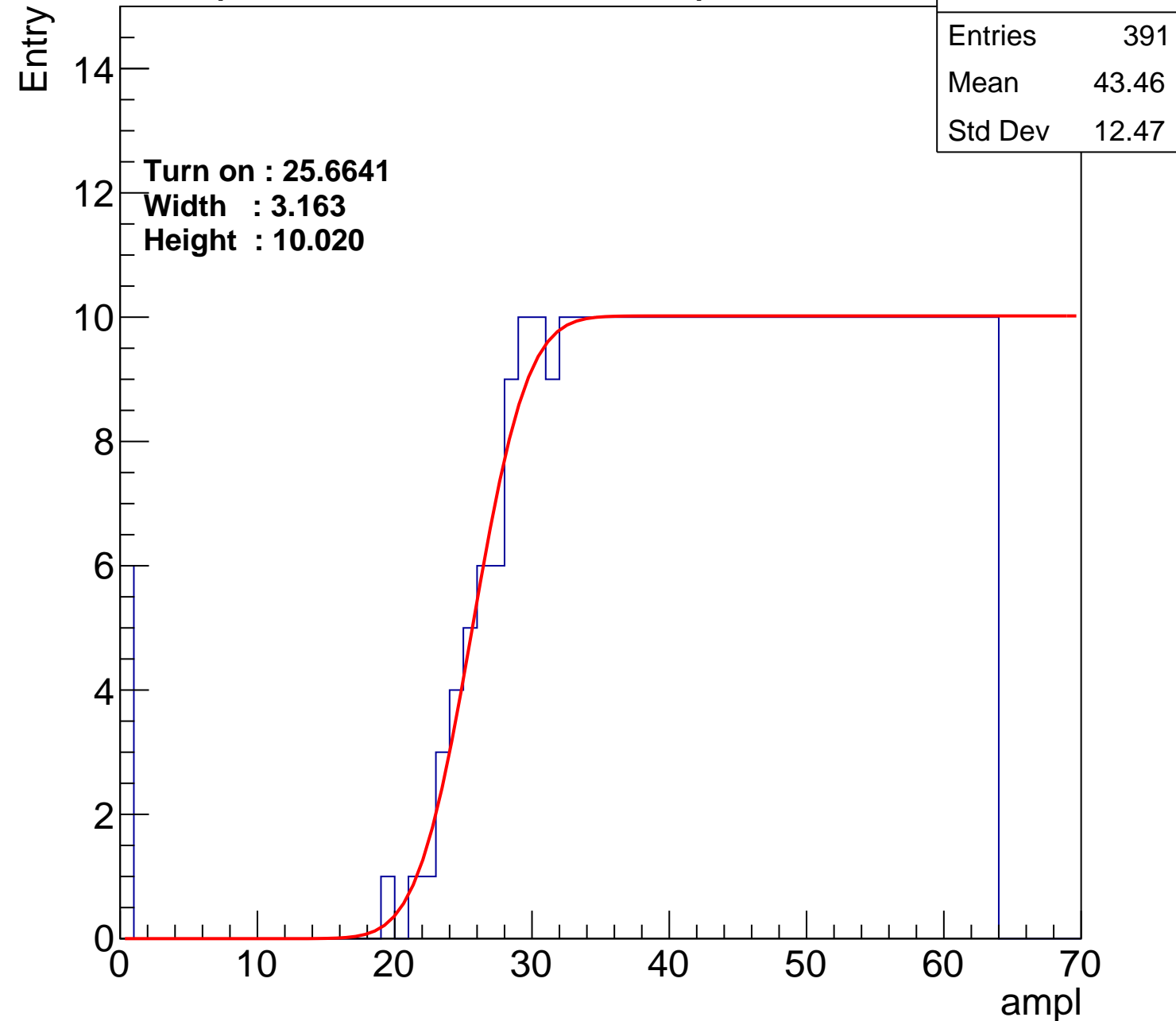
Width : 3.163

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch121

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.96
Std Dev	10.98

Turn on : 27.1135

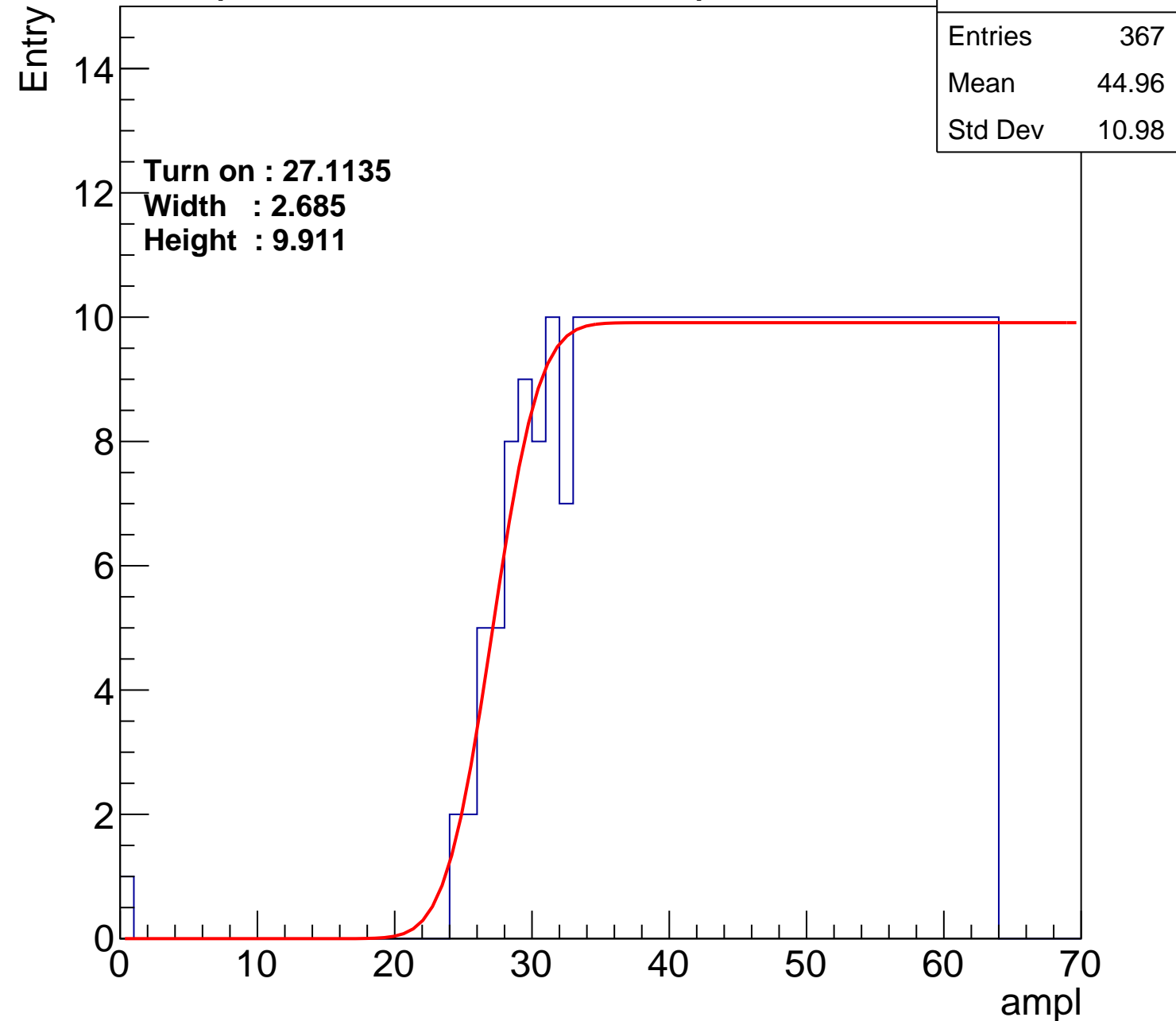
Width : 2.685

Height : 9.911

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch122

calib_packv5_042523_0143.root, FC#0, port D2

Entries	379
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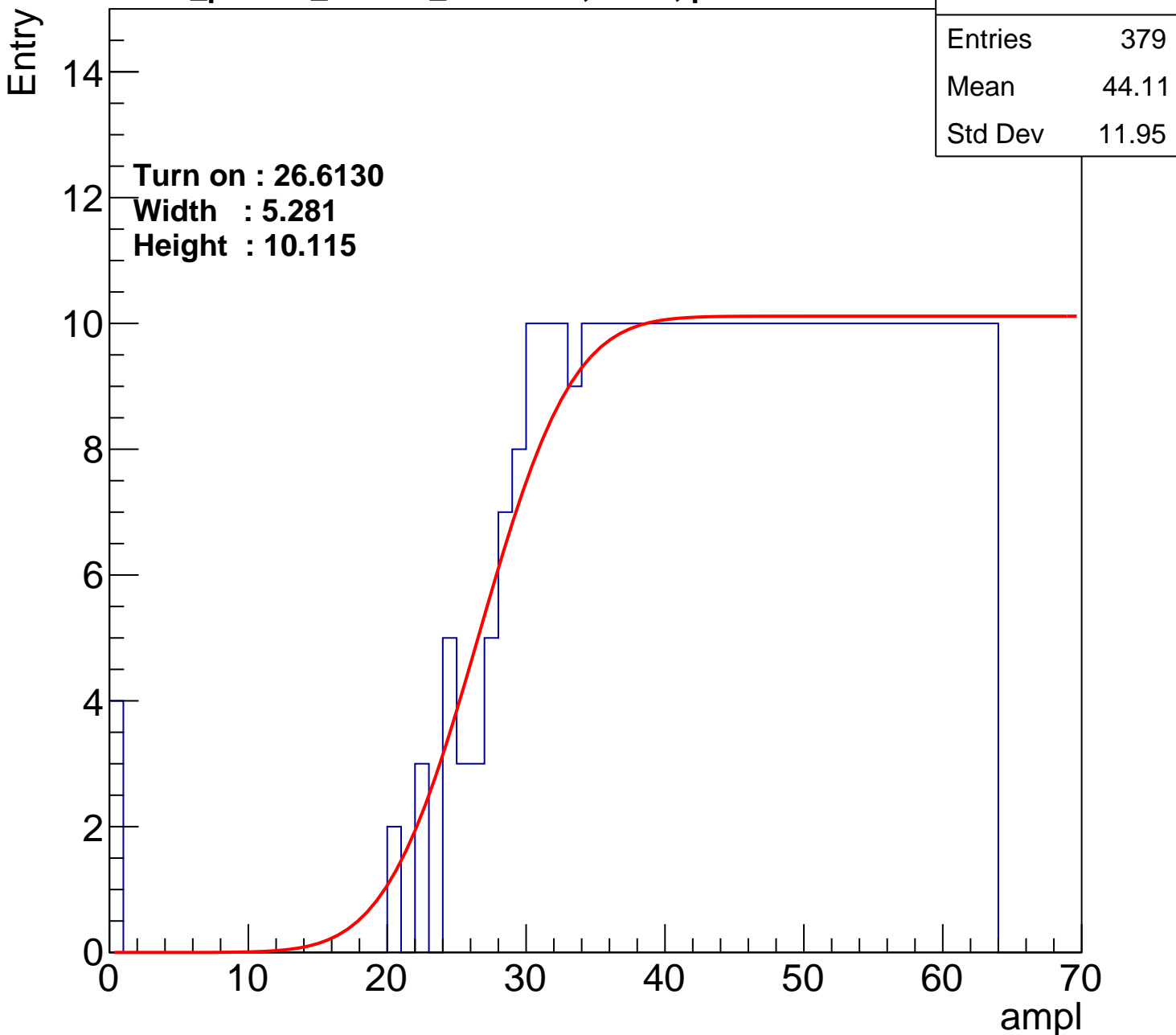
Mean	44.11
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Std Dev	11.95
---------	-------

Turn on : 26.6130

Width : 5.281

Height : 10.115



B1L101S, U23-ch123

calib_packv5_042523_0143.root, FC#0, port D2

Entries	362
Mean	45.07
Std Dev	11.29

Turn on : 28.5306

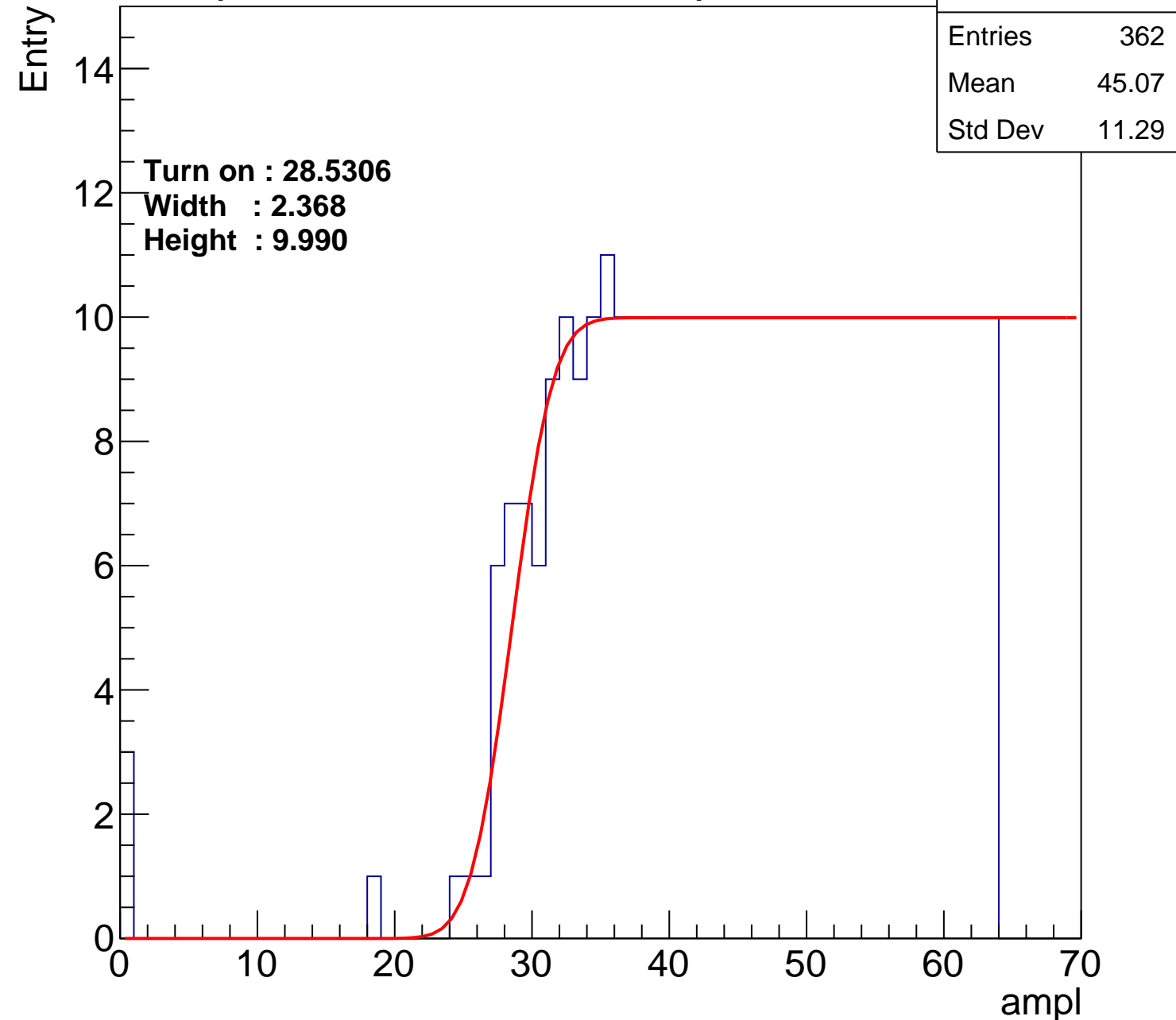
Width : 2.368

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch124

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.64
Std Dev	11.54

Turn on : 27.4696

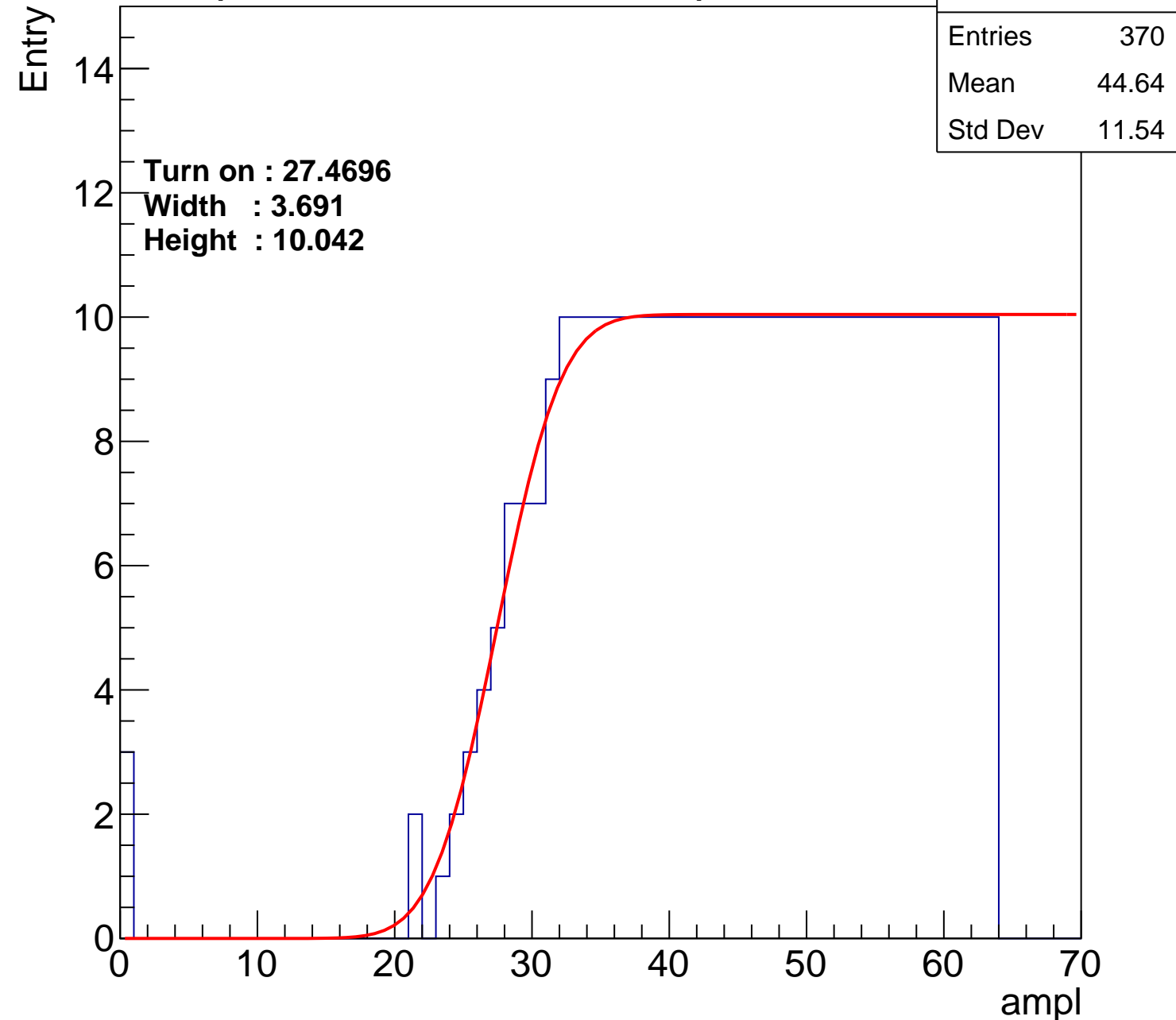
Width : 3.691

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch125

calib_packv5_042523_0143.root, FC#0, port D2

Entries	354
Mean	45.4
Std Dev	11.29

Turn on : 29.6932

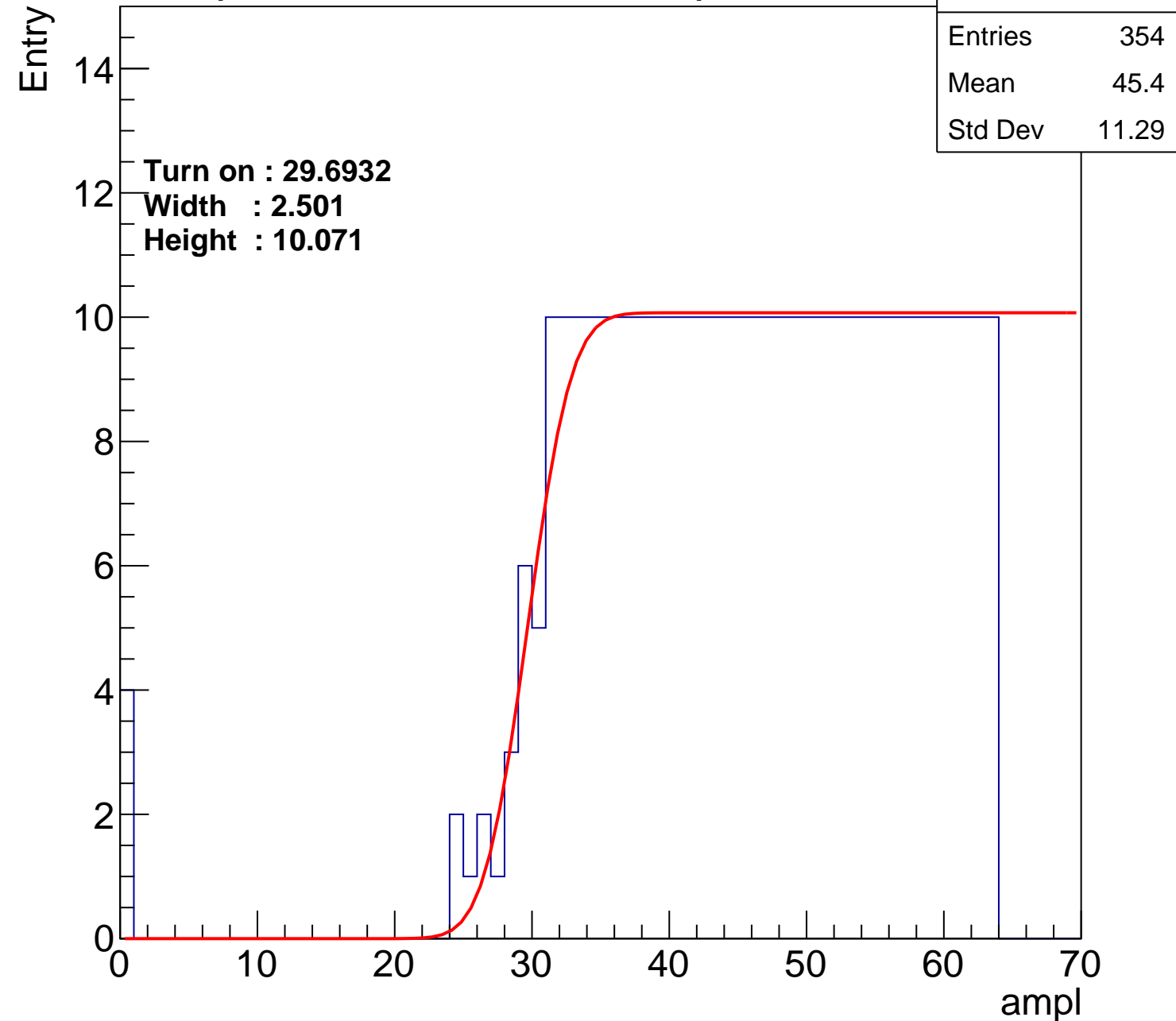
Width : 2.501

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch126

calib_packv5_042523_0143.root, FC#0, port D2

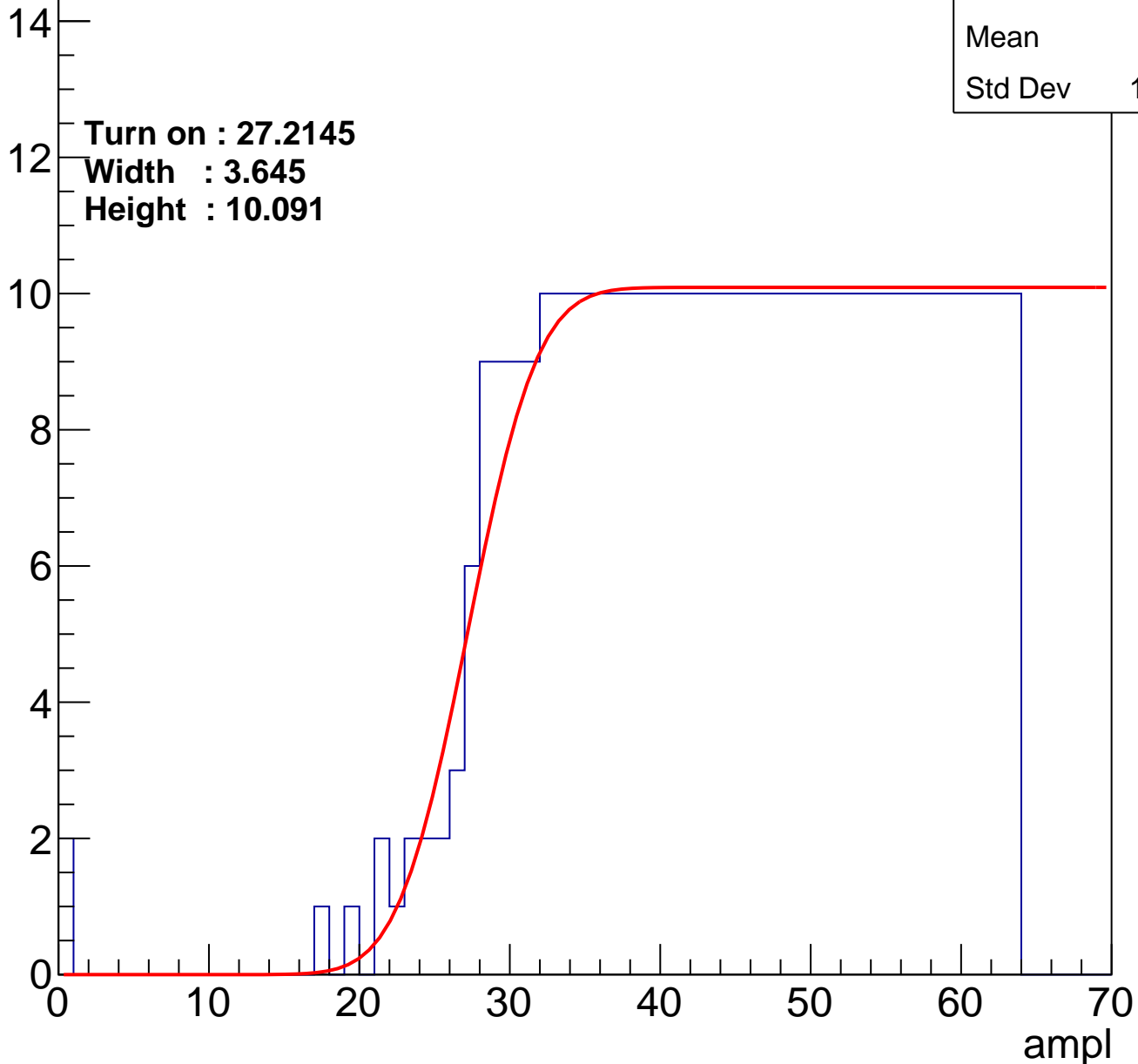
Entries	378
Mean	44.3
Std Dev	11.58

Turn on : 27.2145

Width : 3.645

Height : 10.091

Entry



B1L101S, U23-ch127

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.76
Std Dev	11.24

Turn on : 27.3432

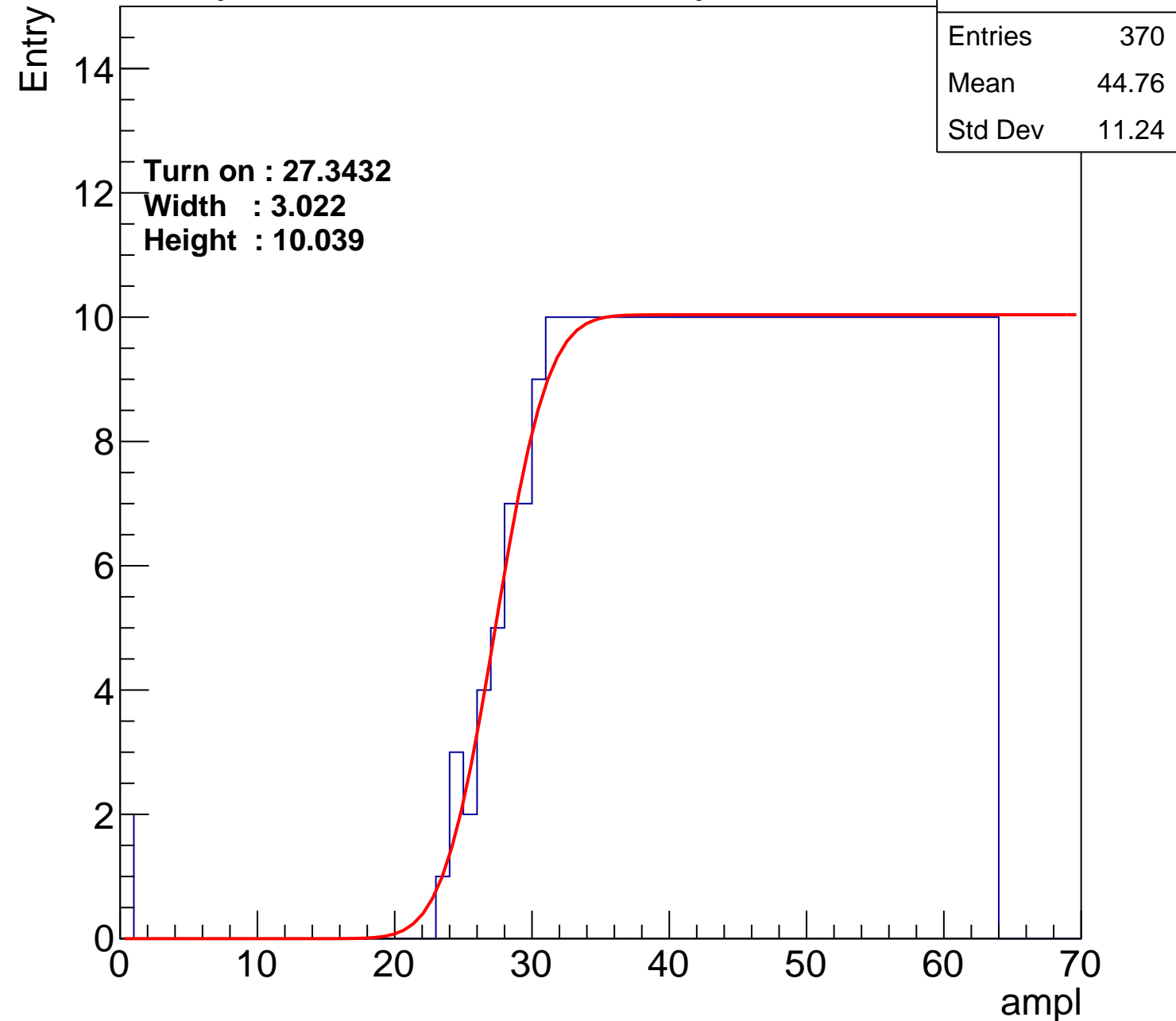
Width : 3.022

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U23-ch127

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.76
Std Dev	11.24

Turn on : 27.3432

Width : 3.022

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl

