

B0L103S, U14-ch0

calib_packv5_040323_1717.root, FC#2, port C3

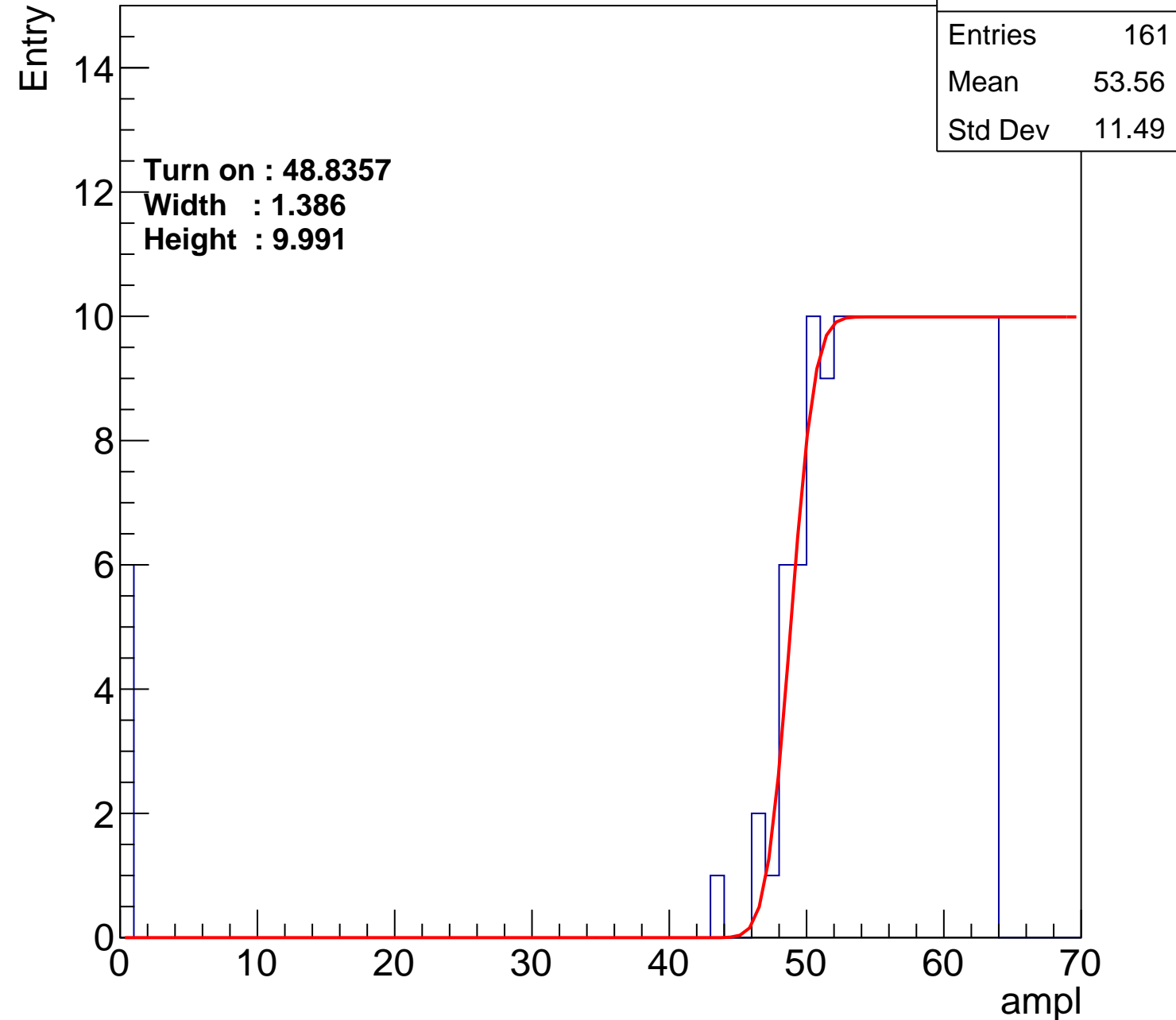
Entry

14
12
10
8
6
4
2
0

Turn on : 48.8357
Width : 1.386
Height : 9.991

Entries	161
Mean	53.56
Std Dev	11.49

ampl



B0L103S, U14-ch1

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	54.3
Std Dev	12.36

Turn on : 51.5382

Width : 2.722

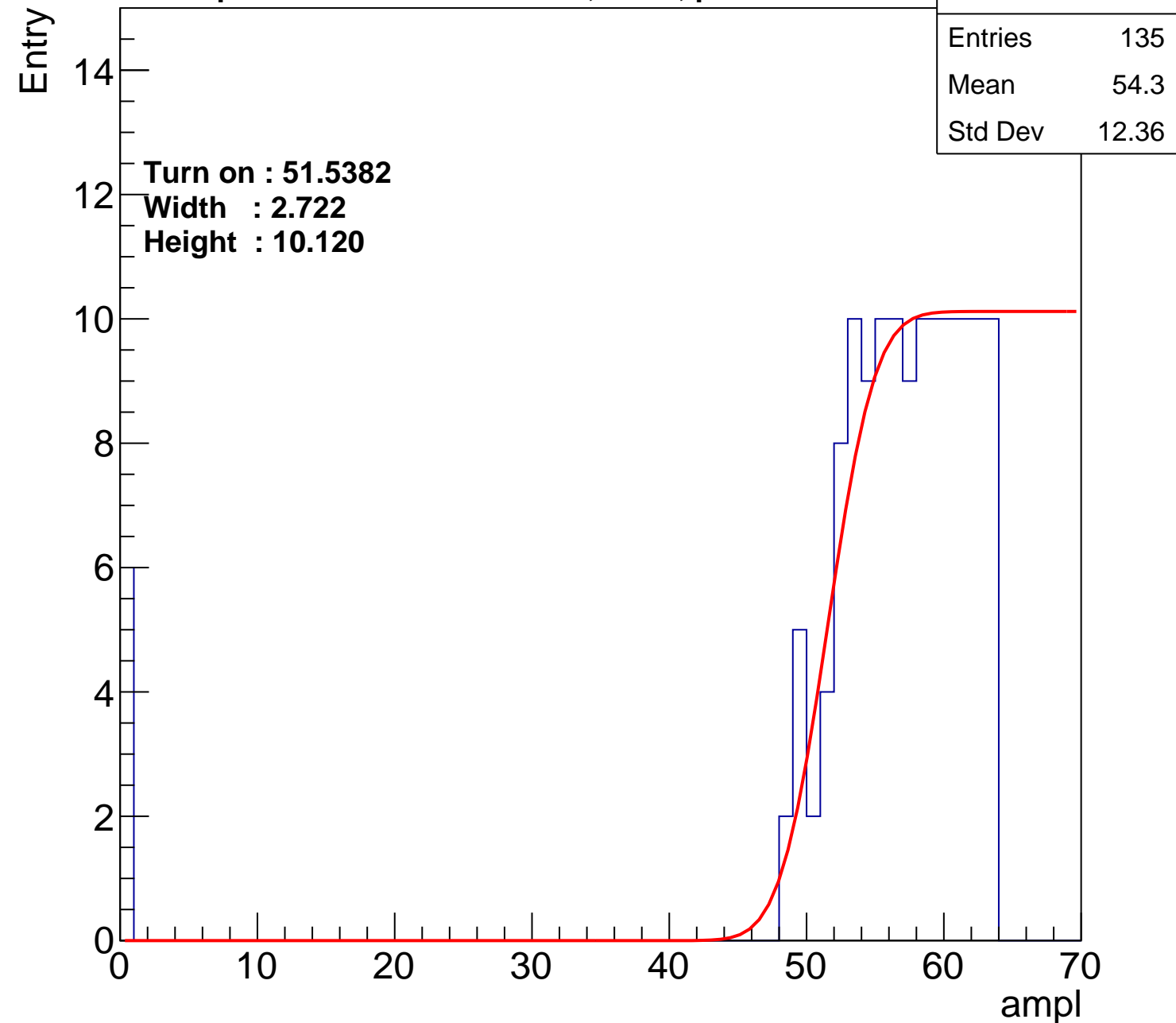
Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U14-ch2

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	55.54
Std Dev	8.084

Turn on : 50.4705

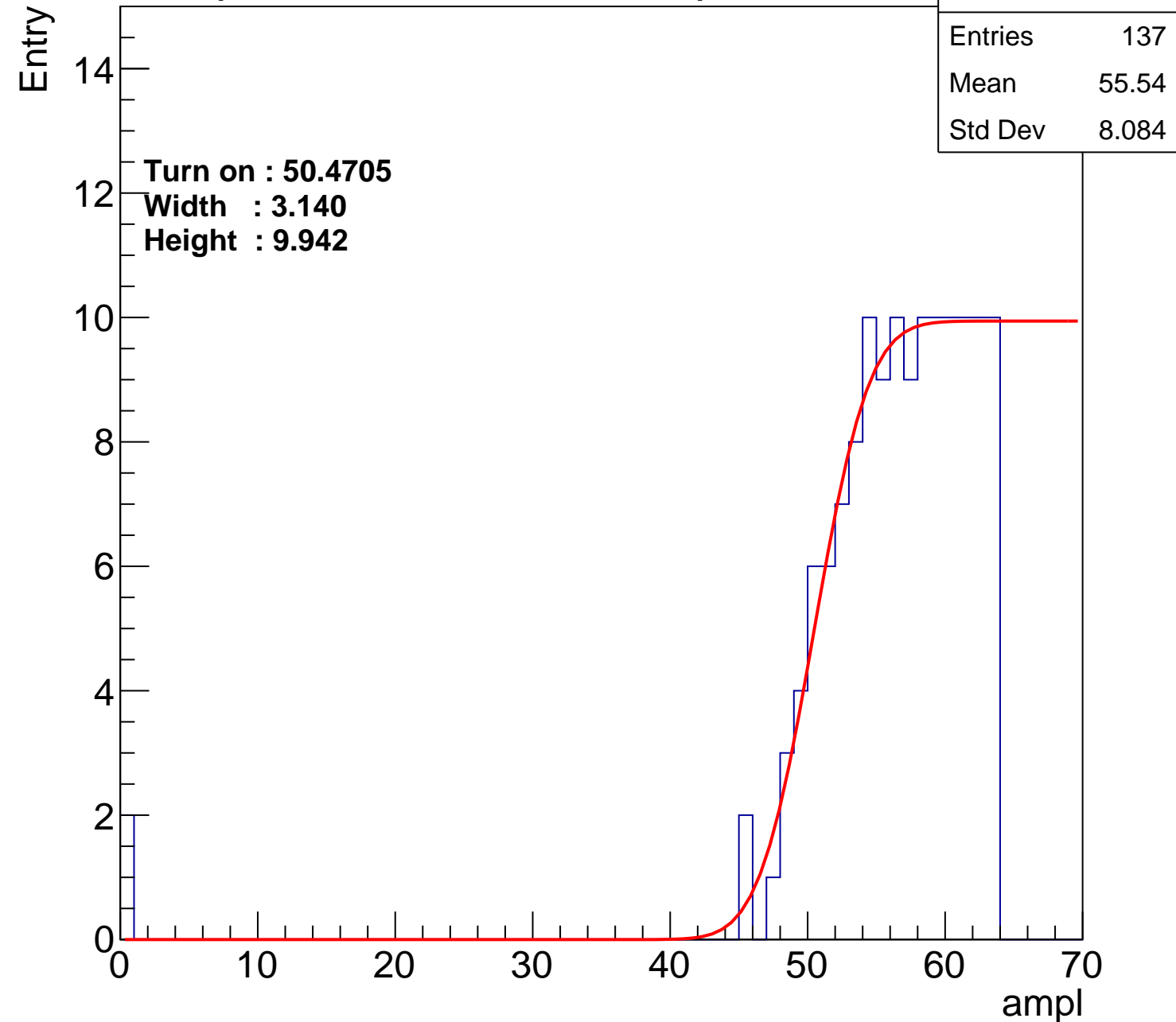
Width : 3.140

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch3

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	54.99
Std Dev	10.43

Turn on : 50.9863

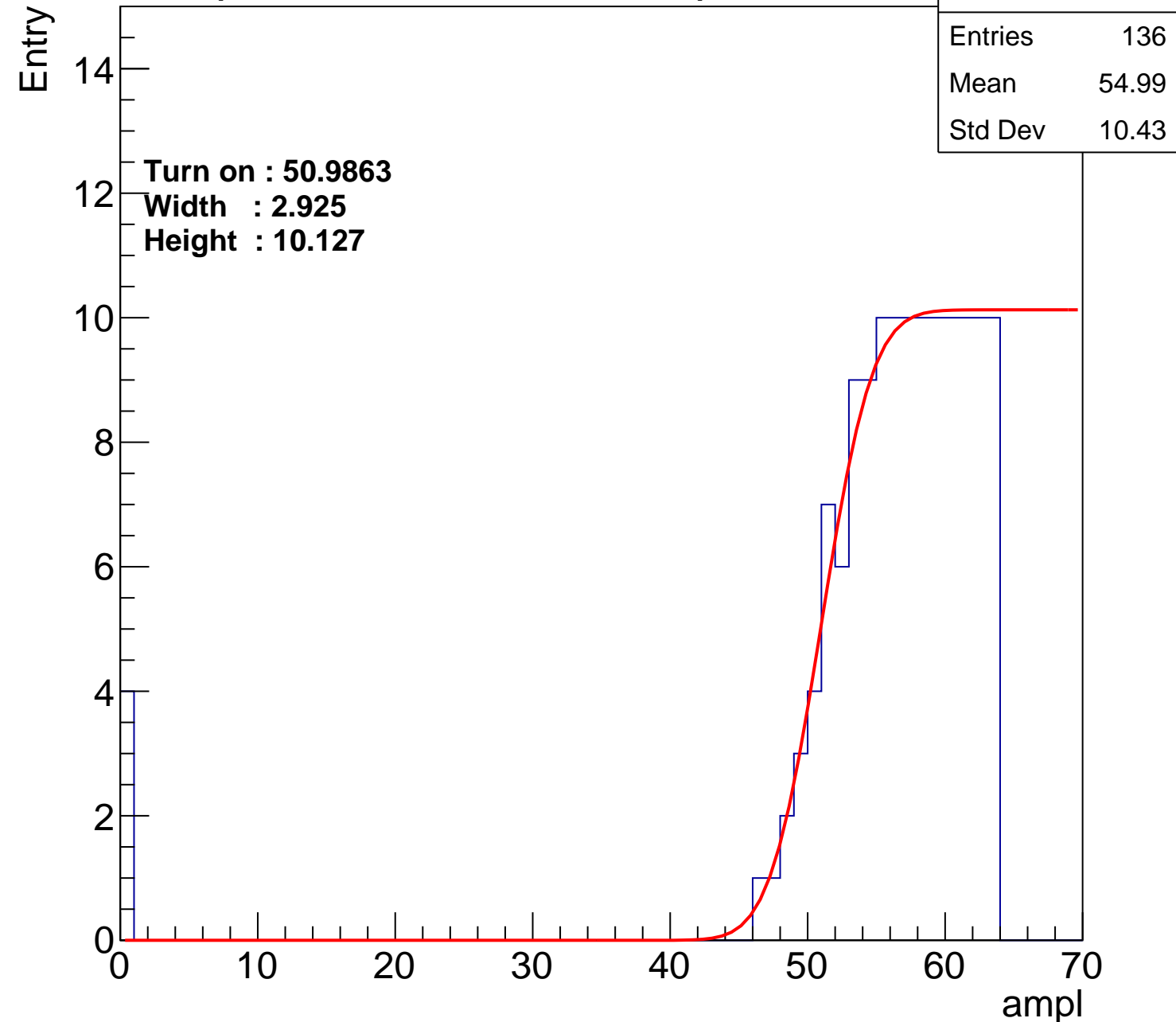
Width : 2.925

Height : 10.127

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch4

calib_packv5_040323_1717.root, FC#2, port C3

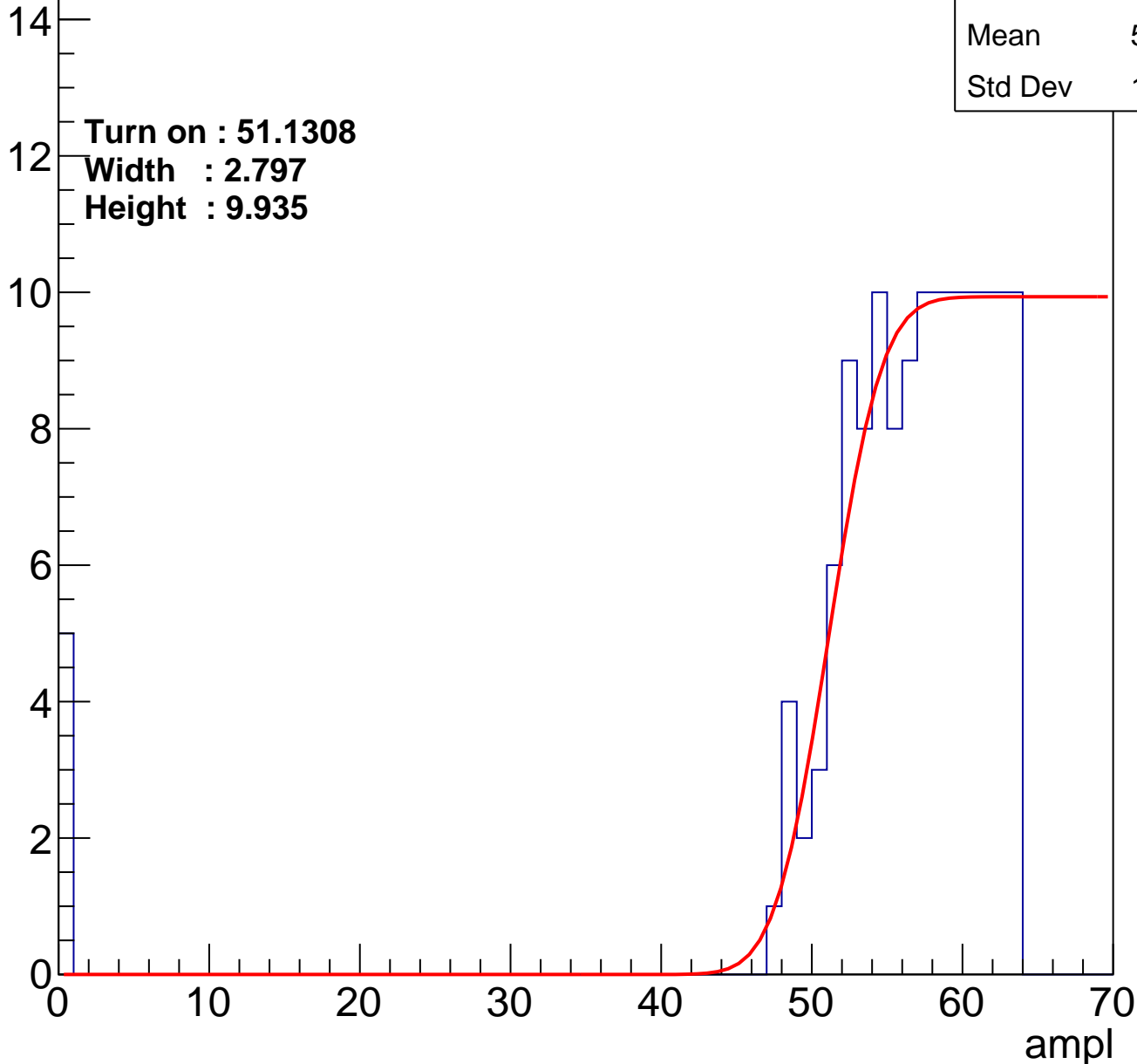
Entry

Entries	135
Mean	54.59
Std Dev	11.47

Turn on : 51.1308

Width : 2.797

Height : 9.935



B0L103S, U14-ch5

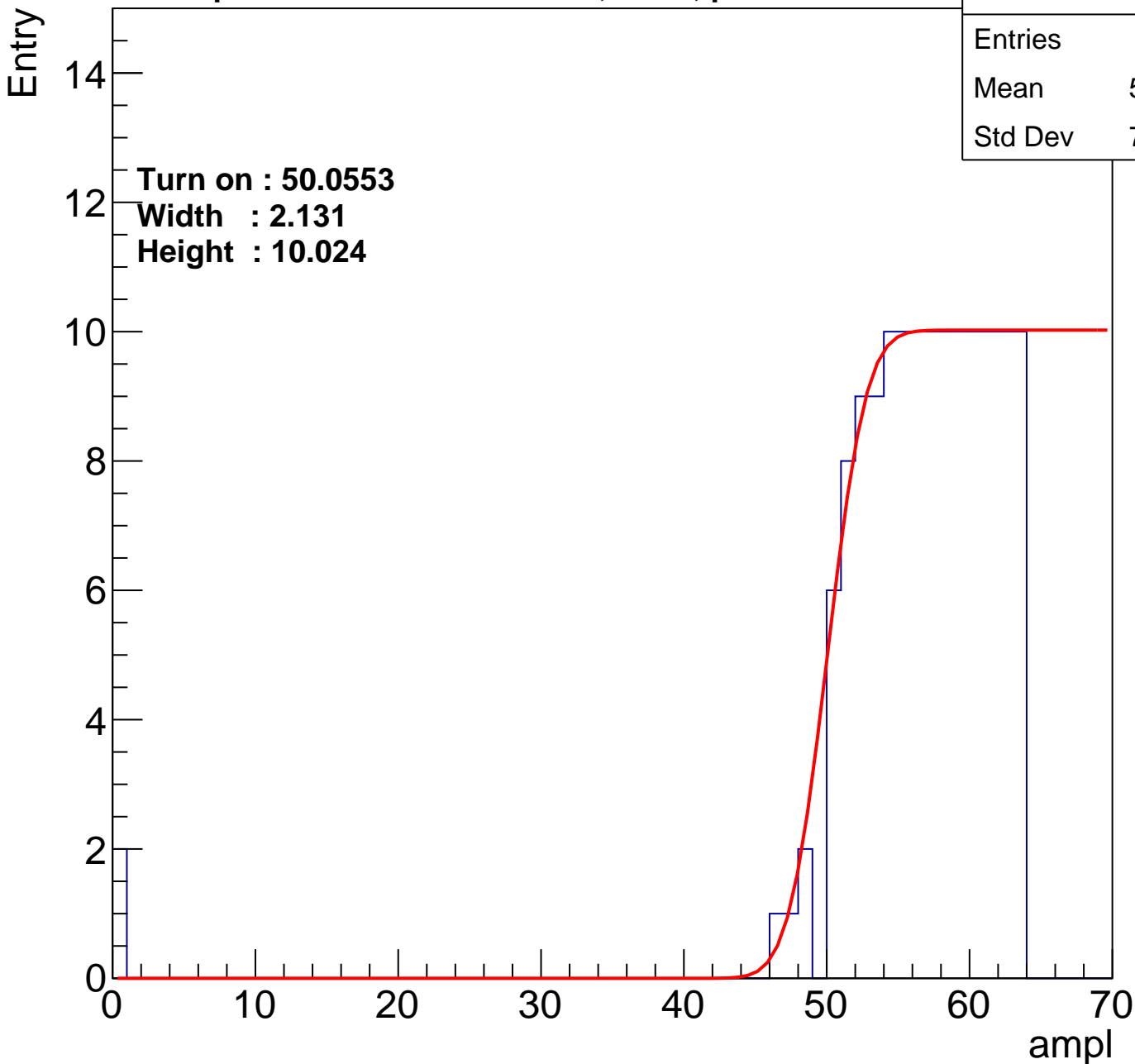
calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.74
Std Dev	7.923

Turn on : 50.0553

Width : 2.131

Height : 10.024



B0L103S, U14-ch6

calib_packv5_040323_1717.root, FC#2, port C3

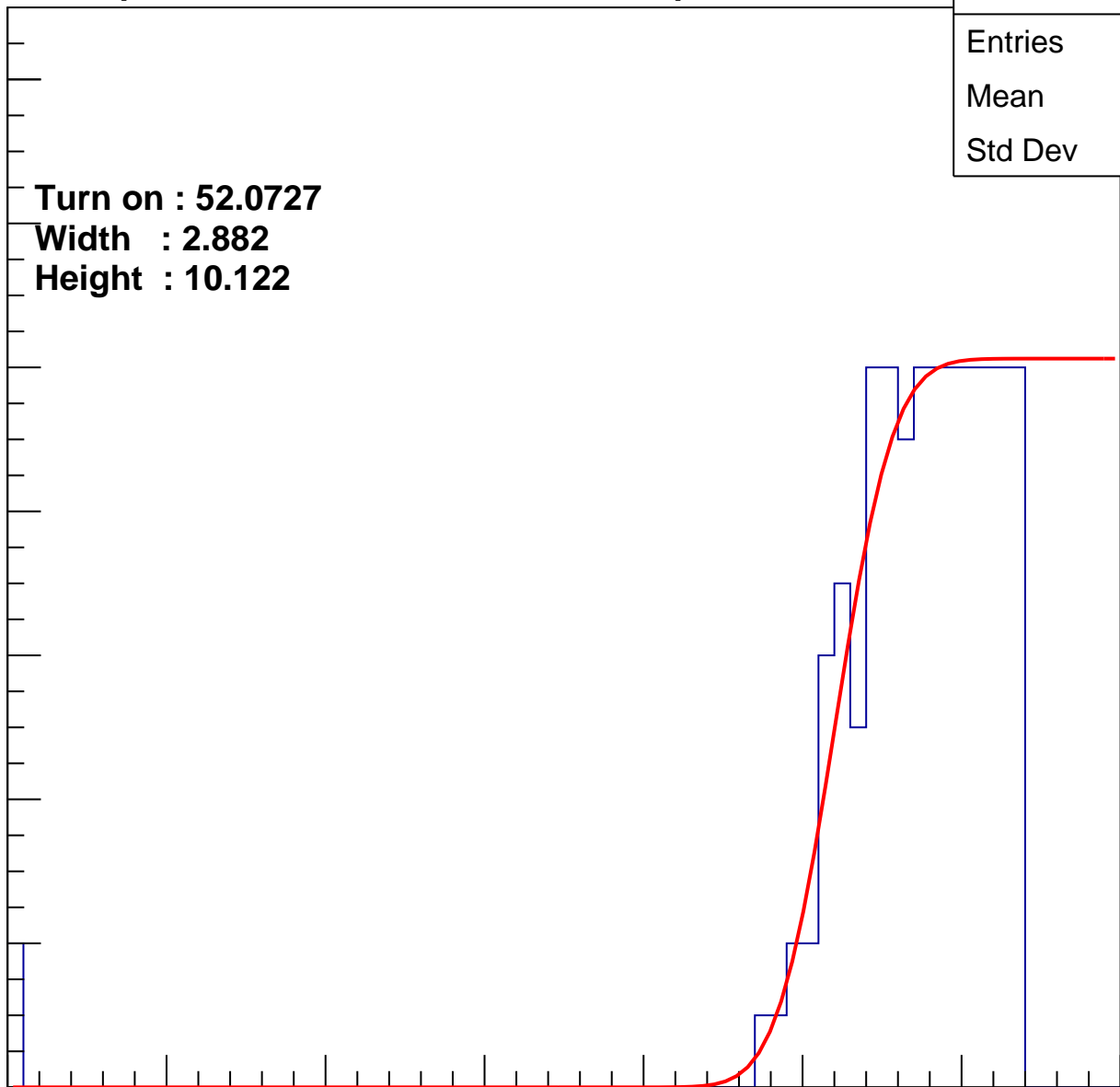
Entry

14
12
10
8
6
4
2
0

Turn on : 52.0727
Width : 2.882
Height : 10.122

Entries	125
Mean	56.18
Std Dev	8.168

ampl



B0L103S, U14-ch7

calib_packv5_040323_1717.root, FC#2, port C3

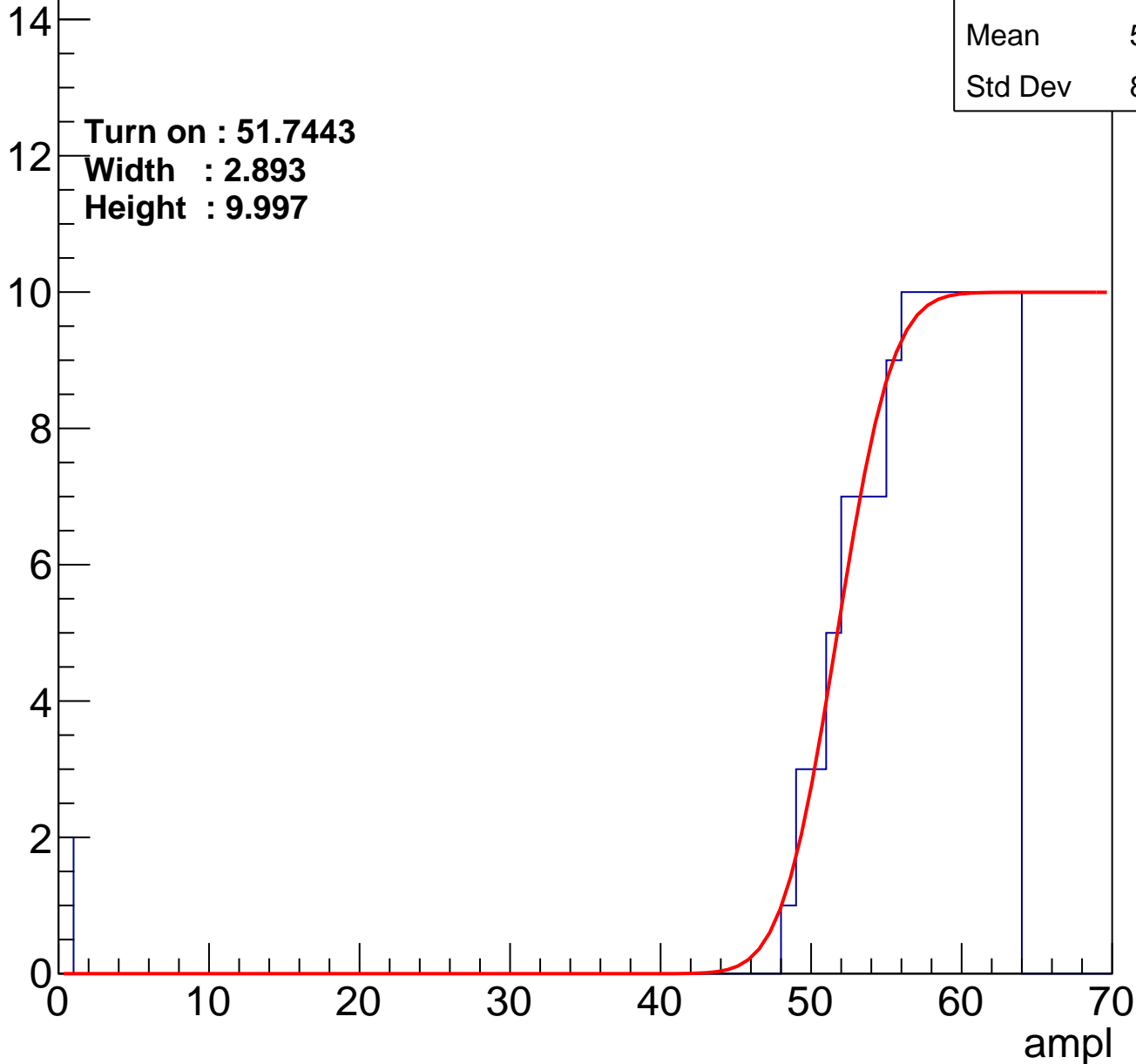
Entry

Entries	124
Mean	56.19
Std Dev	8.193

Turn on : 51.7443

Width : 2.893

Height : 9.997



B0L103S, U14-ch8

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	54.06
Std Dev	13.48

Turn on : 52.2007

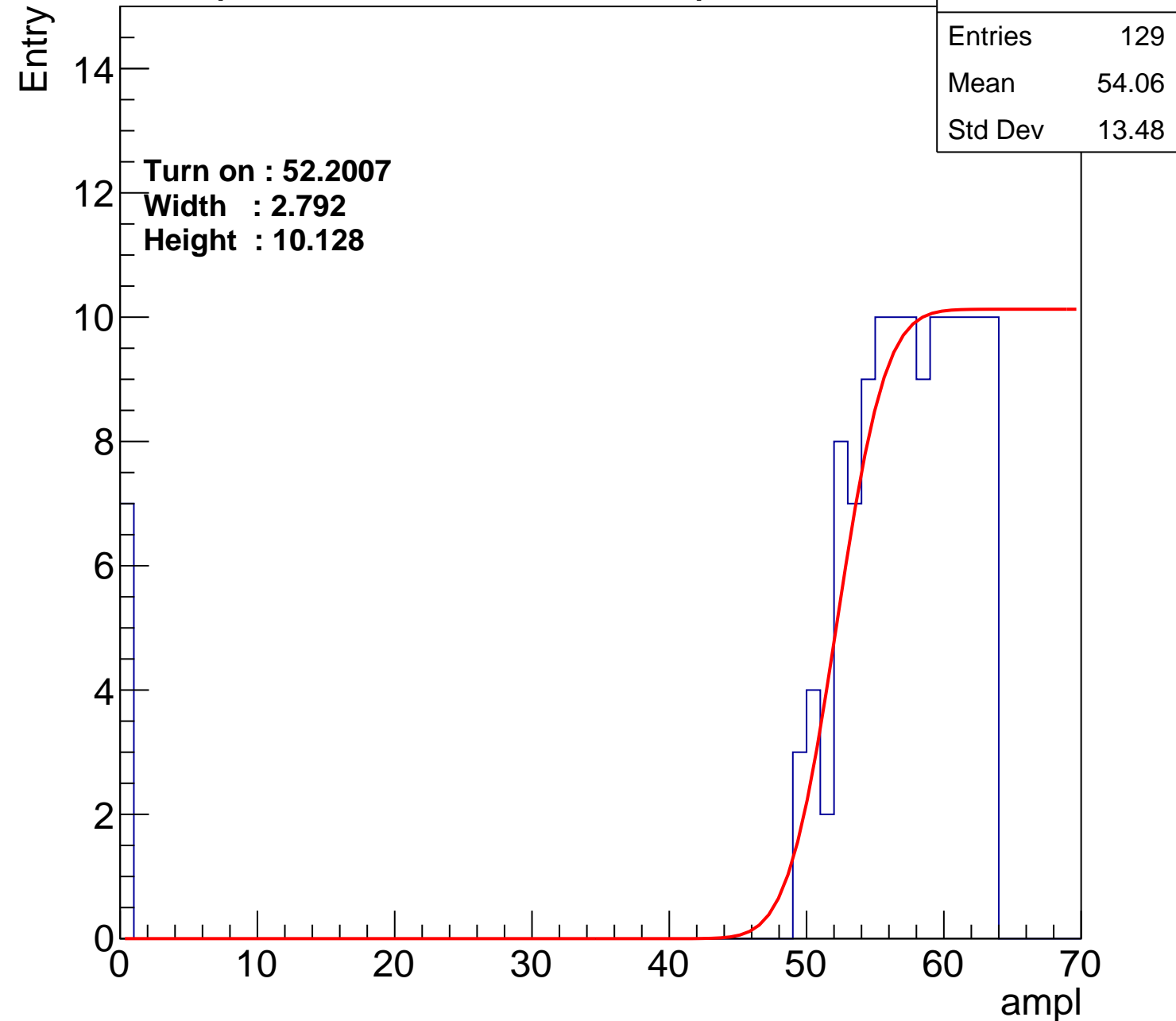
Width : 2.792

Height : 10.128

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch9

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.83
Std Dev	8.061

Turn on : 51.1133

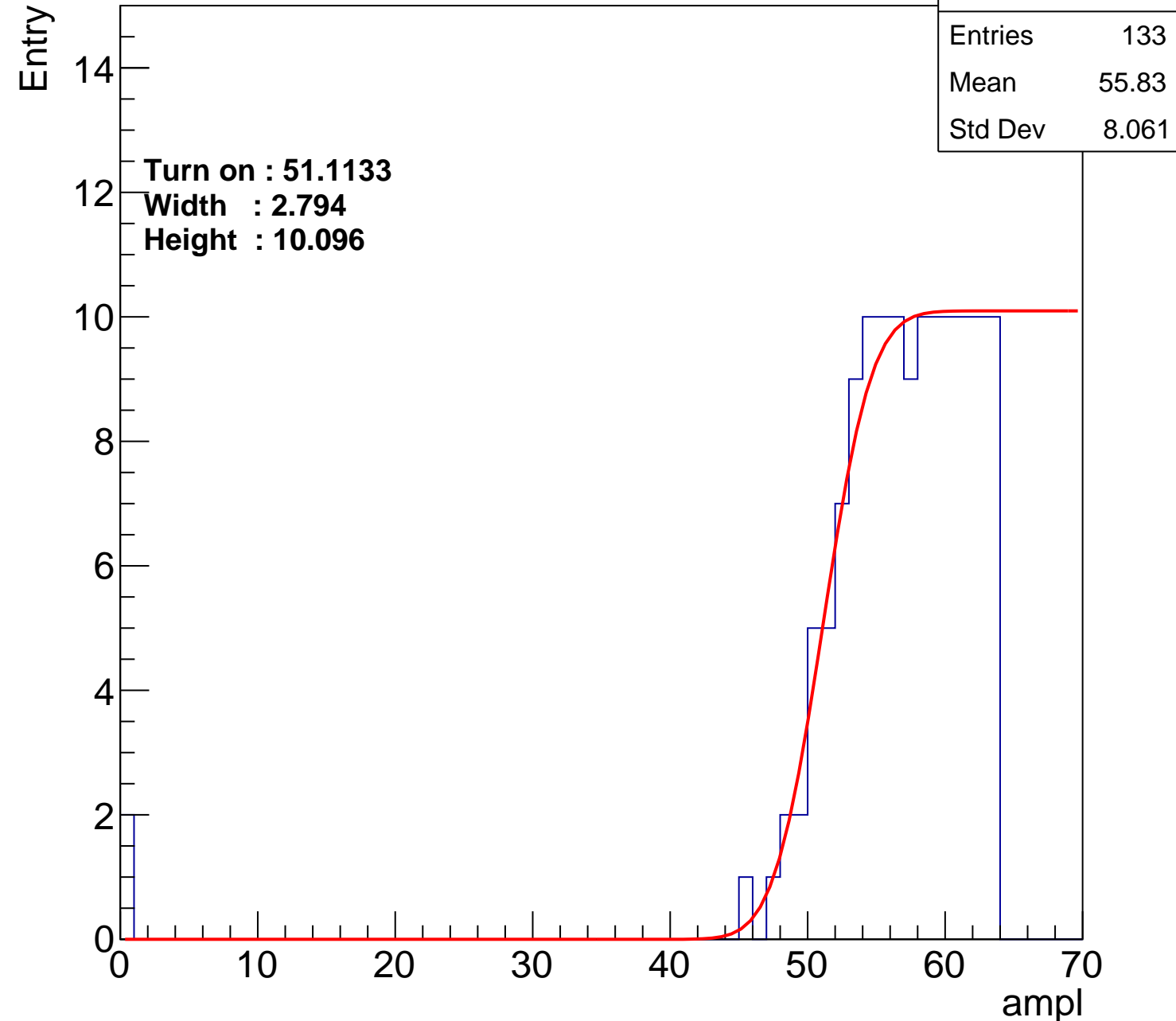
Width : 2.794

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch10

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	56.13
Std Dev	6.505

Turn on : 51.1183

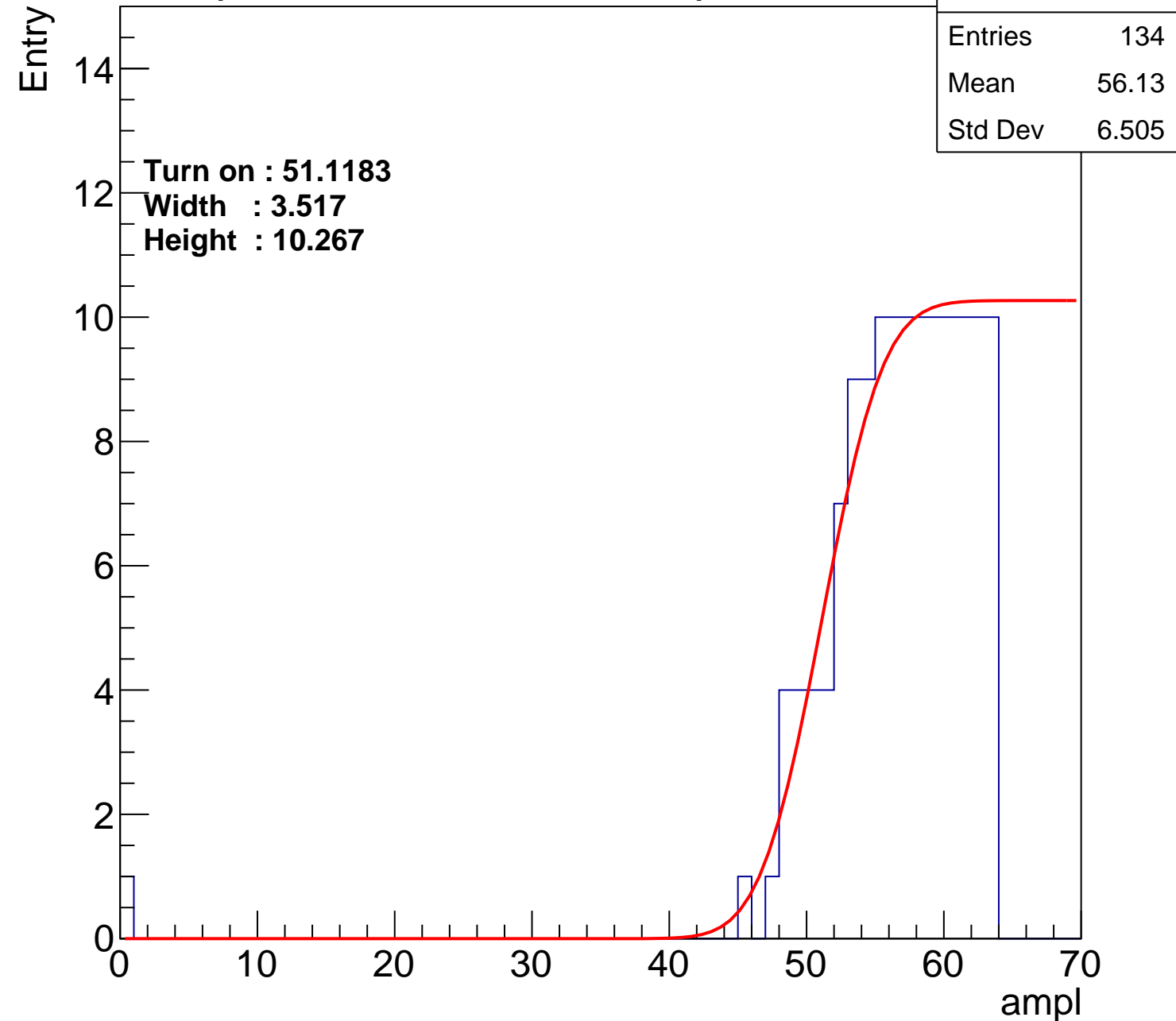
Width : 3.517

Height : 10.267

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch11

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	53.13
Std Dev	13.32

Turn on : 49.6269

Width : 3.759

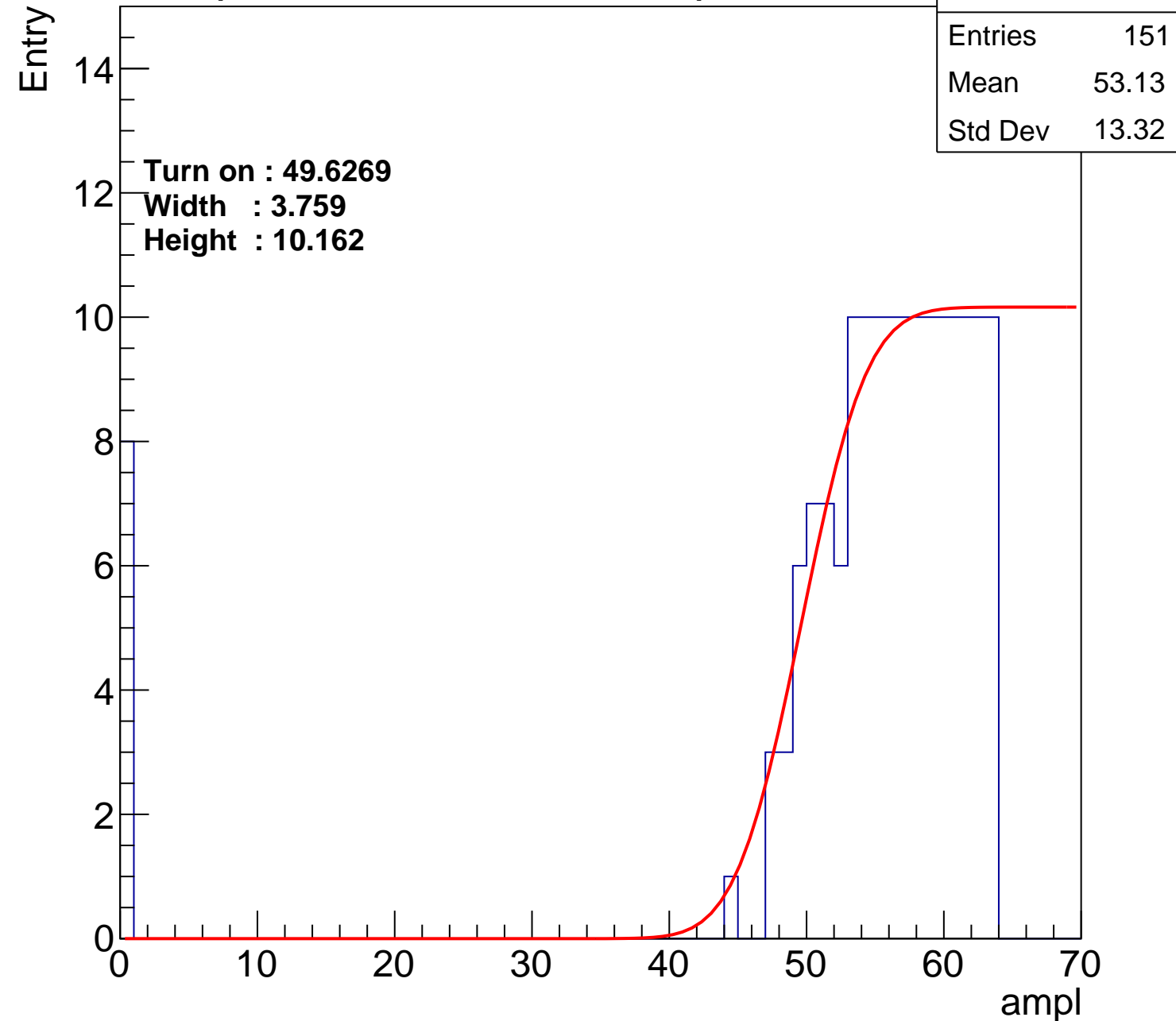
Height : 10.162

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U14-ch12

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.43
Std Dev	9.305

Turn on : 50.3239

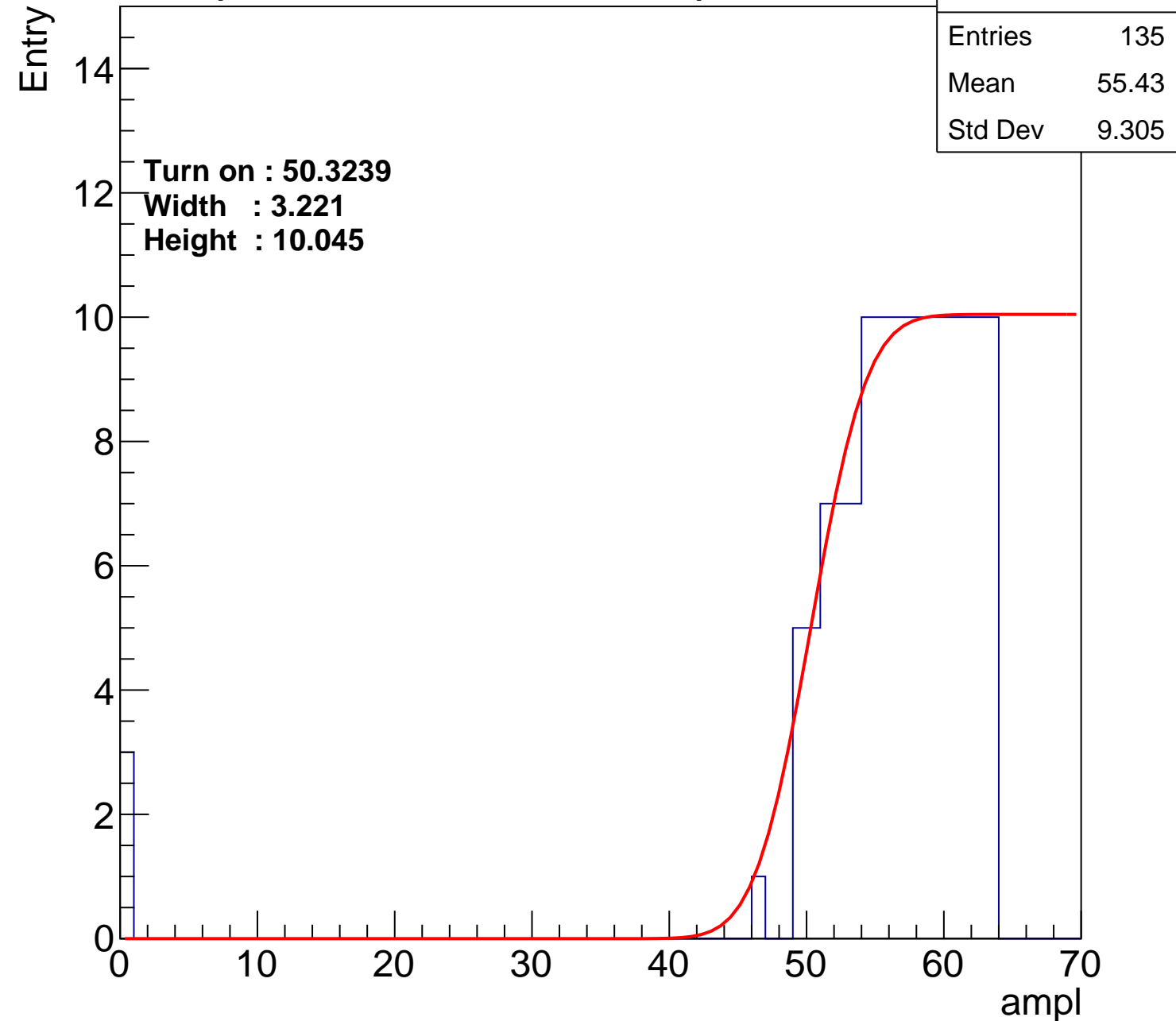
Width : 3.221

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch13

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.04
Std Dev	9.164

Turn on : 50.3698

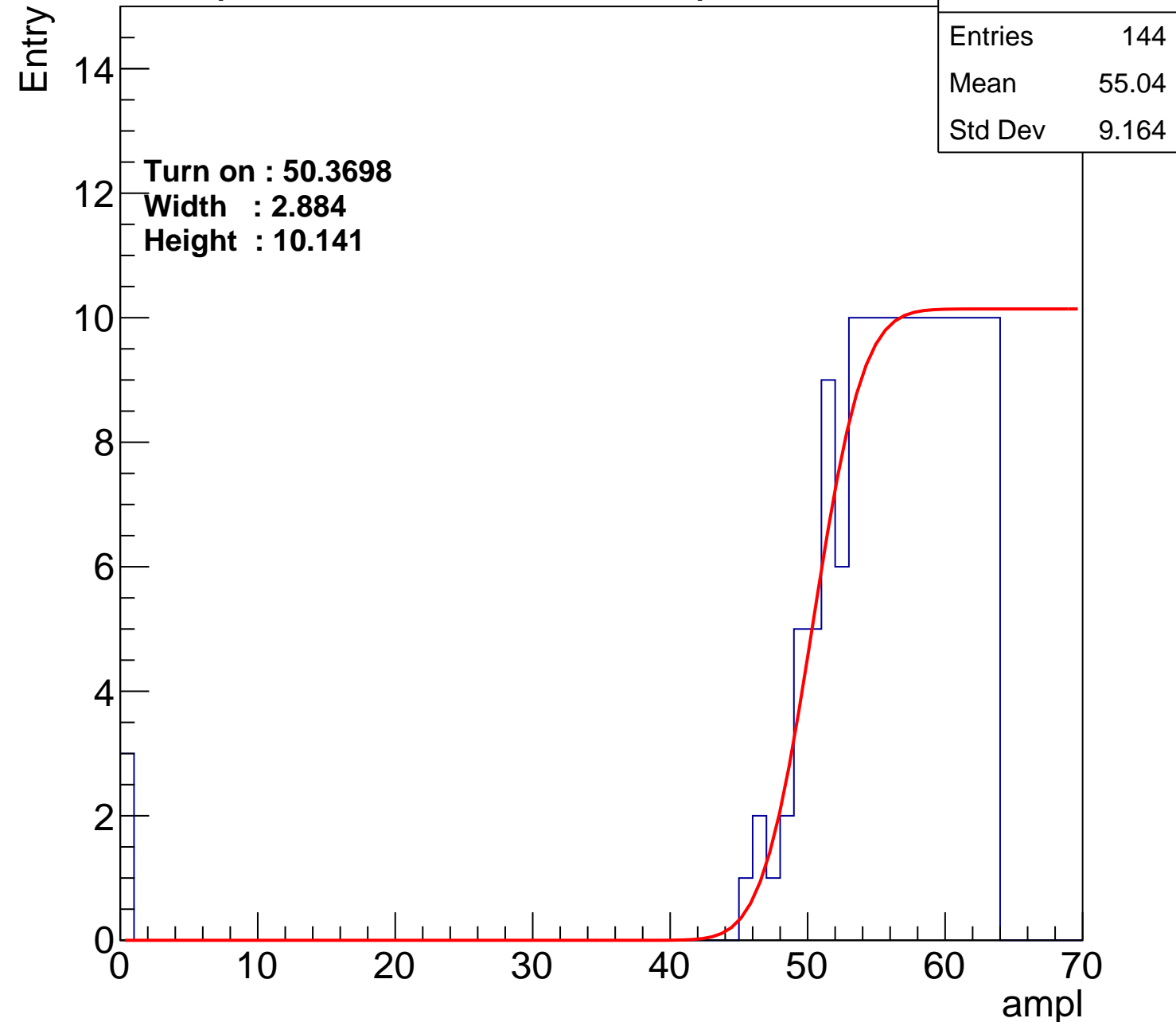
Width : 2.884

Height : 10.141

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch14

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	55.27
Std Dev	7.851

Turn on : 49.3843

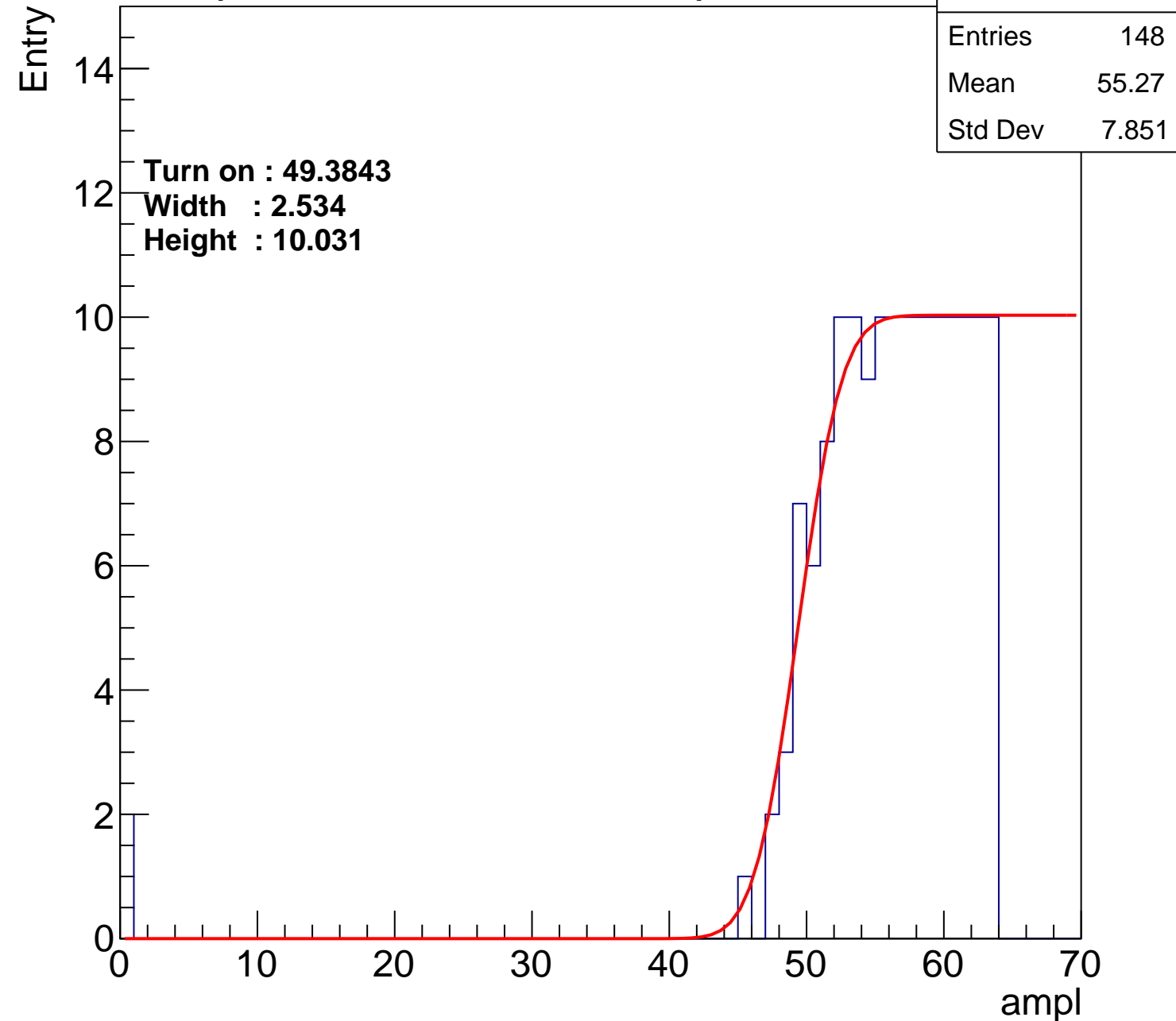
Width : 2.534

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch15

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	54.57
Std Dev	10.19

Turn on : 49.9078

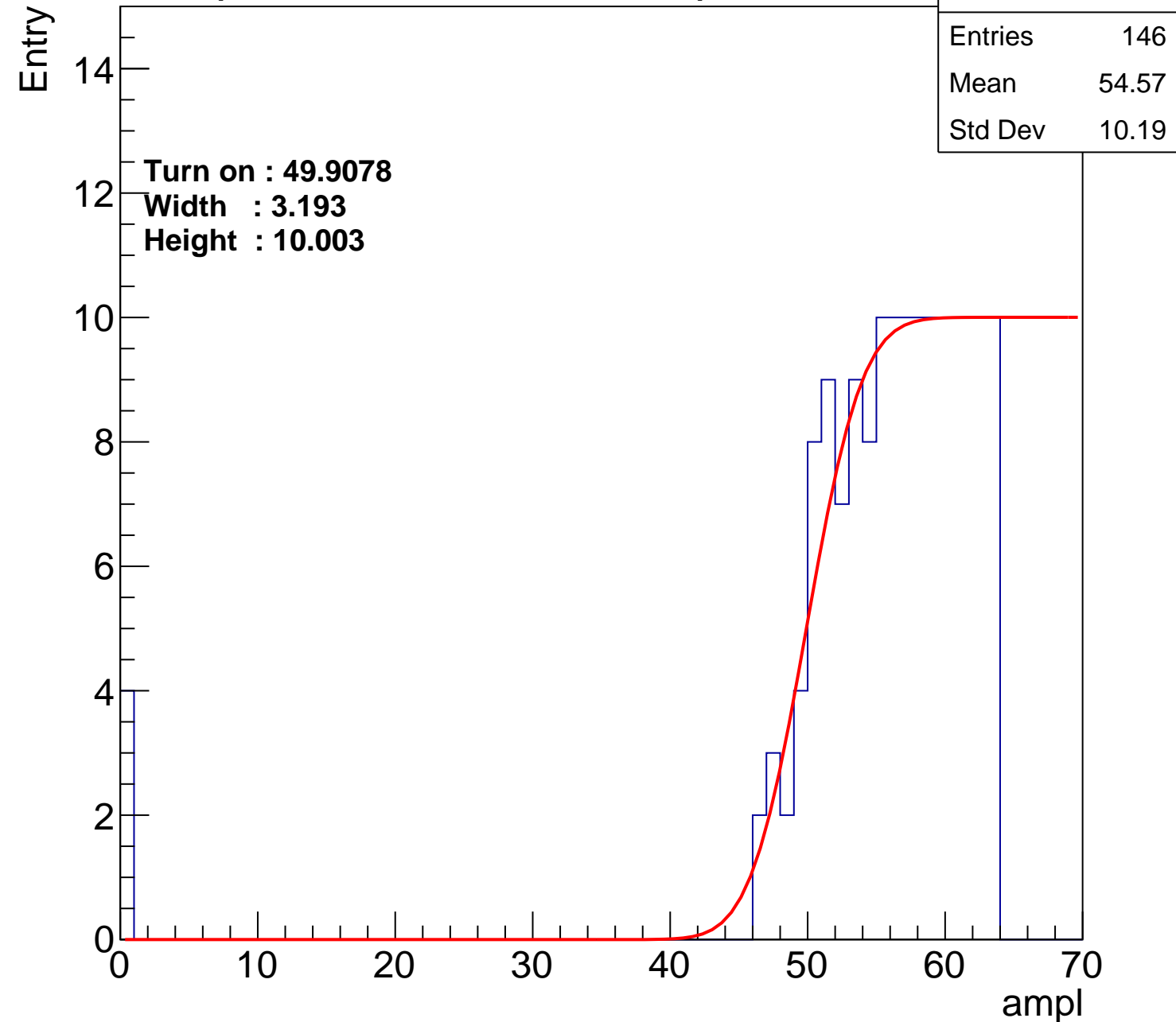
Width : 3.193

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch16

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	54.95
Std Dev	9.135

Turn on : 50.0857

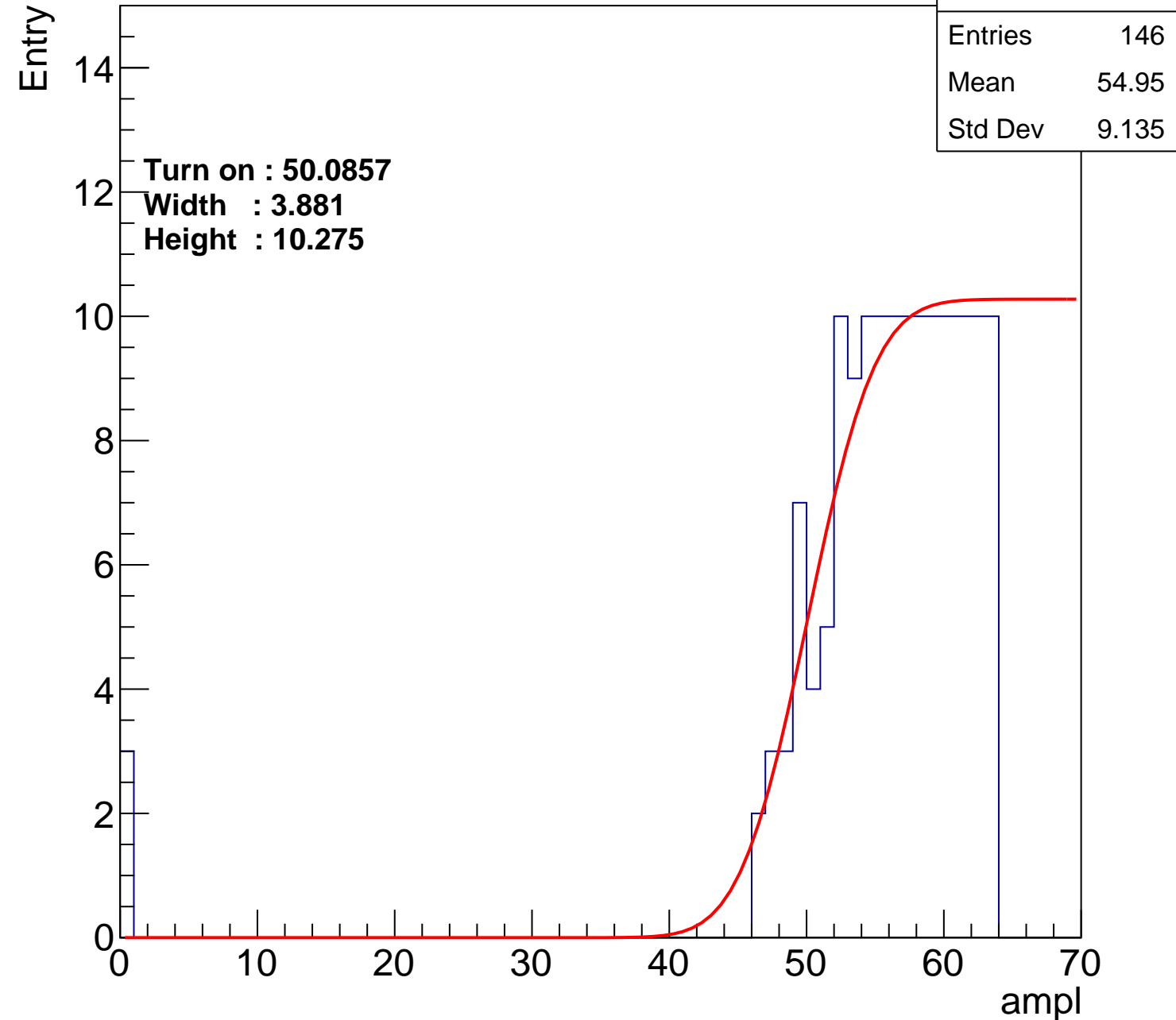
Width : 3.881

Height : 10.275

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch17

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	54.43
Std Dev	10.17

Turn on : 50.0057

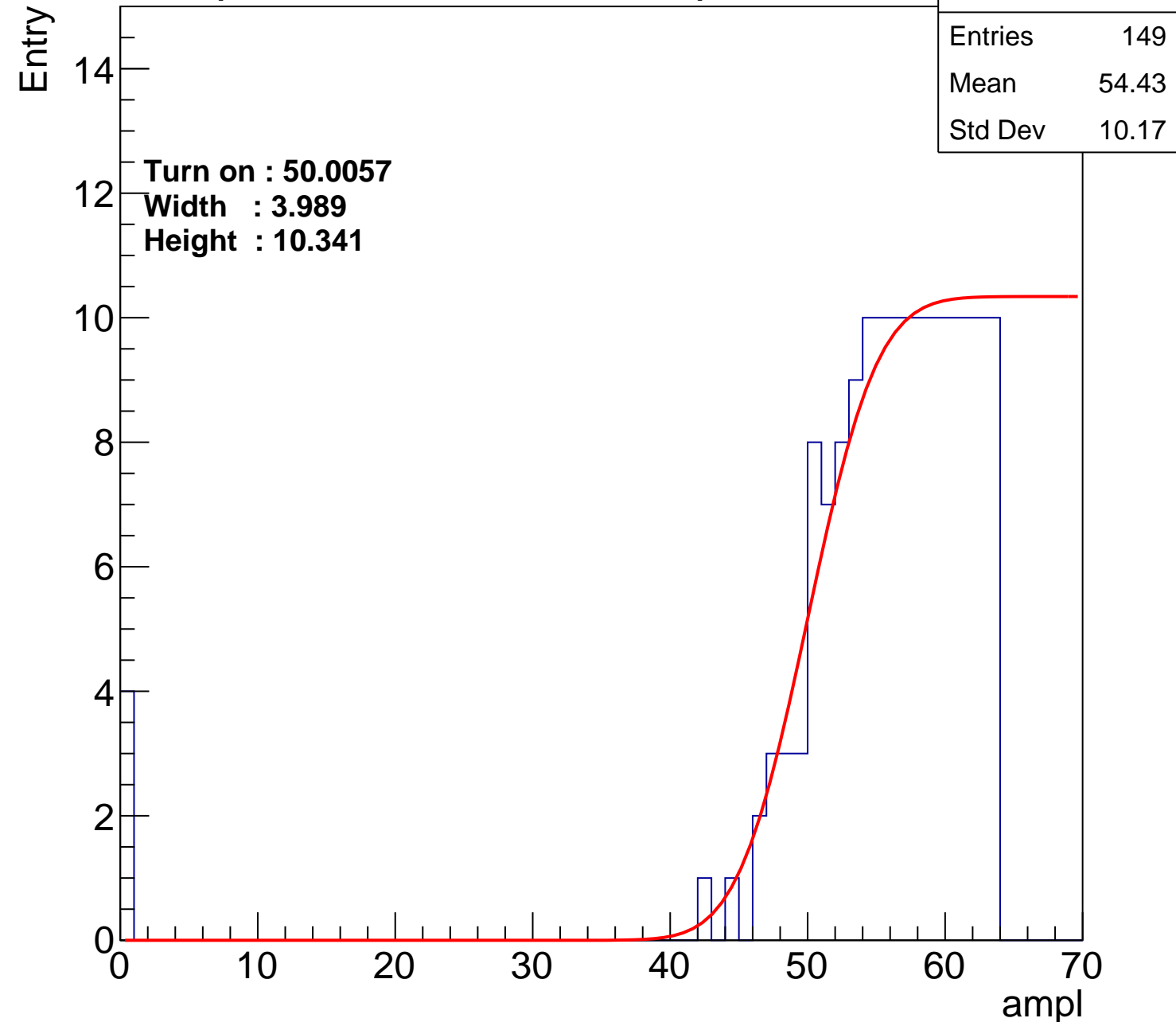
Width : 3.989

Height : 10.341

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch18

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	54.46
Std Dev	10.16

Turn on : 50.3805

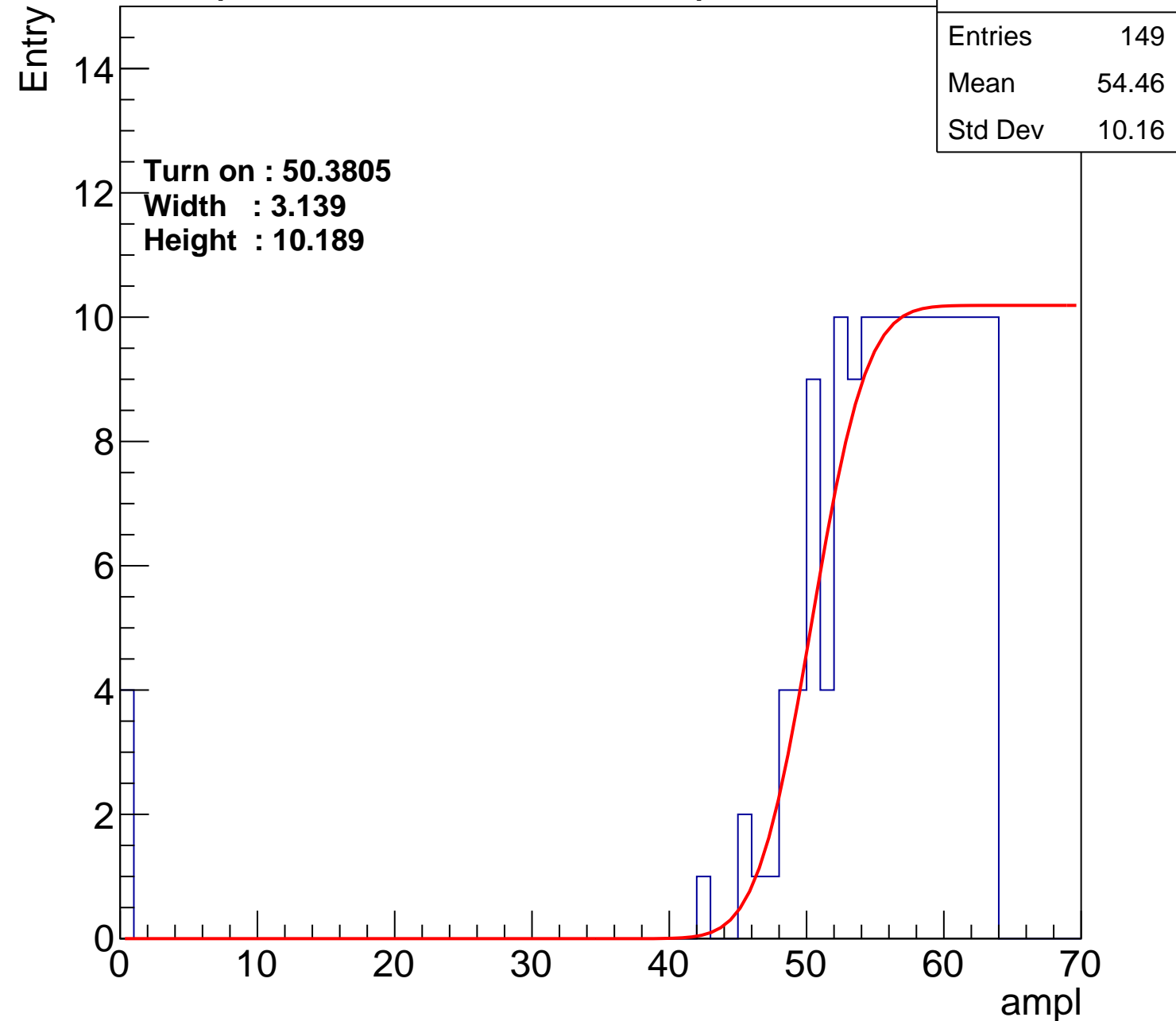
Width : 3.139

Height : 10.189

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch19

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	54.88
Std Dev	10.38

Turn on : 50.4724

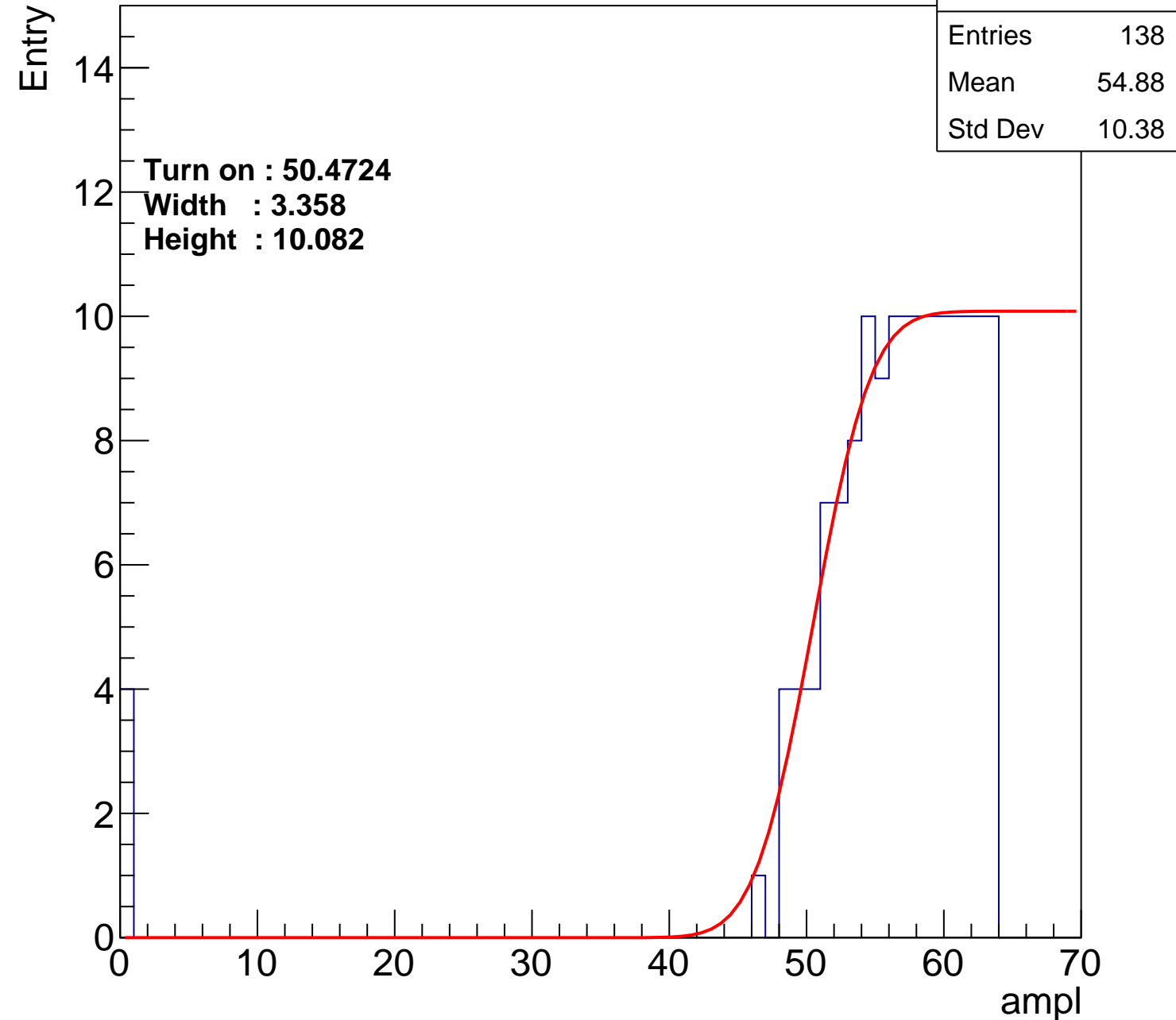
Width : 3.358

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch20

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.02
Std Dev	11.2

Turn on : 50.5357

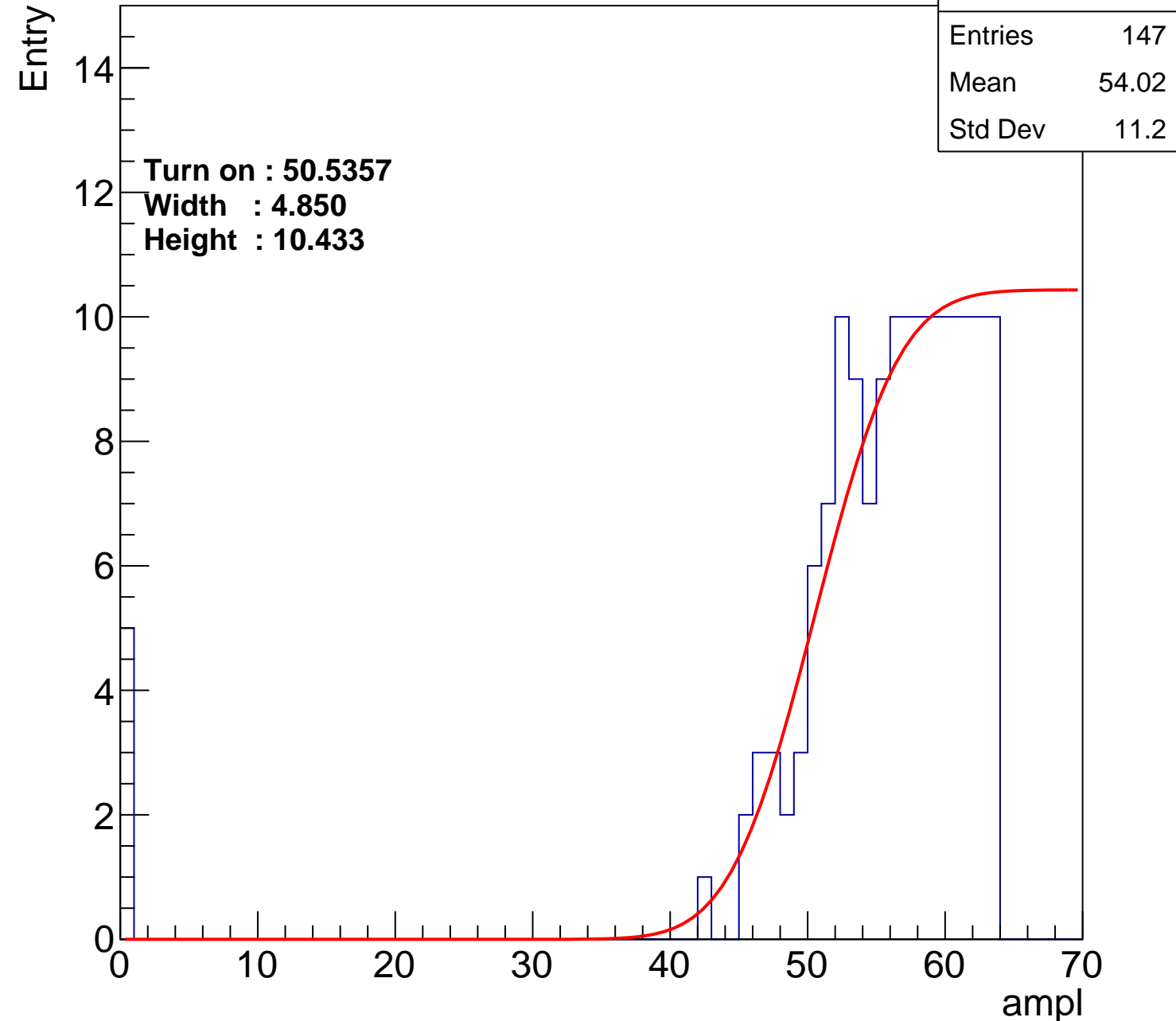
Width : 4.850

Height : 10.433

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch21

calib_packv5_040323_1717.root, FC#2, port C3

Entries	156
Mean	54.78
Std Dev	7.906

Turn on : 48.1894

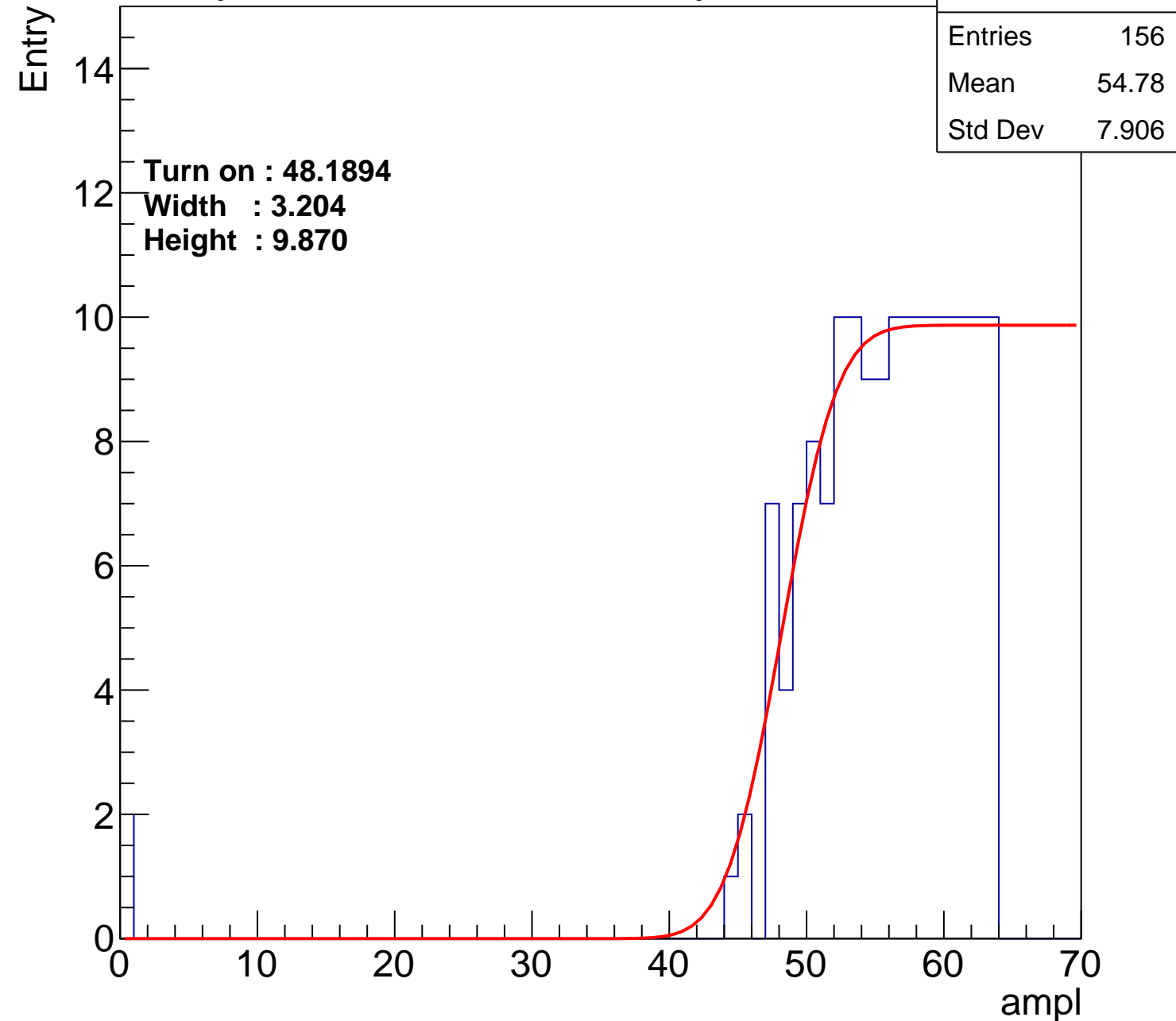
Width : 3.204

Height : 9.870

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch22

calib_packv5_040323_1717.root, FC#2, port C3

Entries	164
Mean	54.18
Std Dev	8.946

Turn on : 48.1226

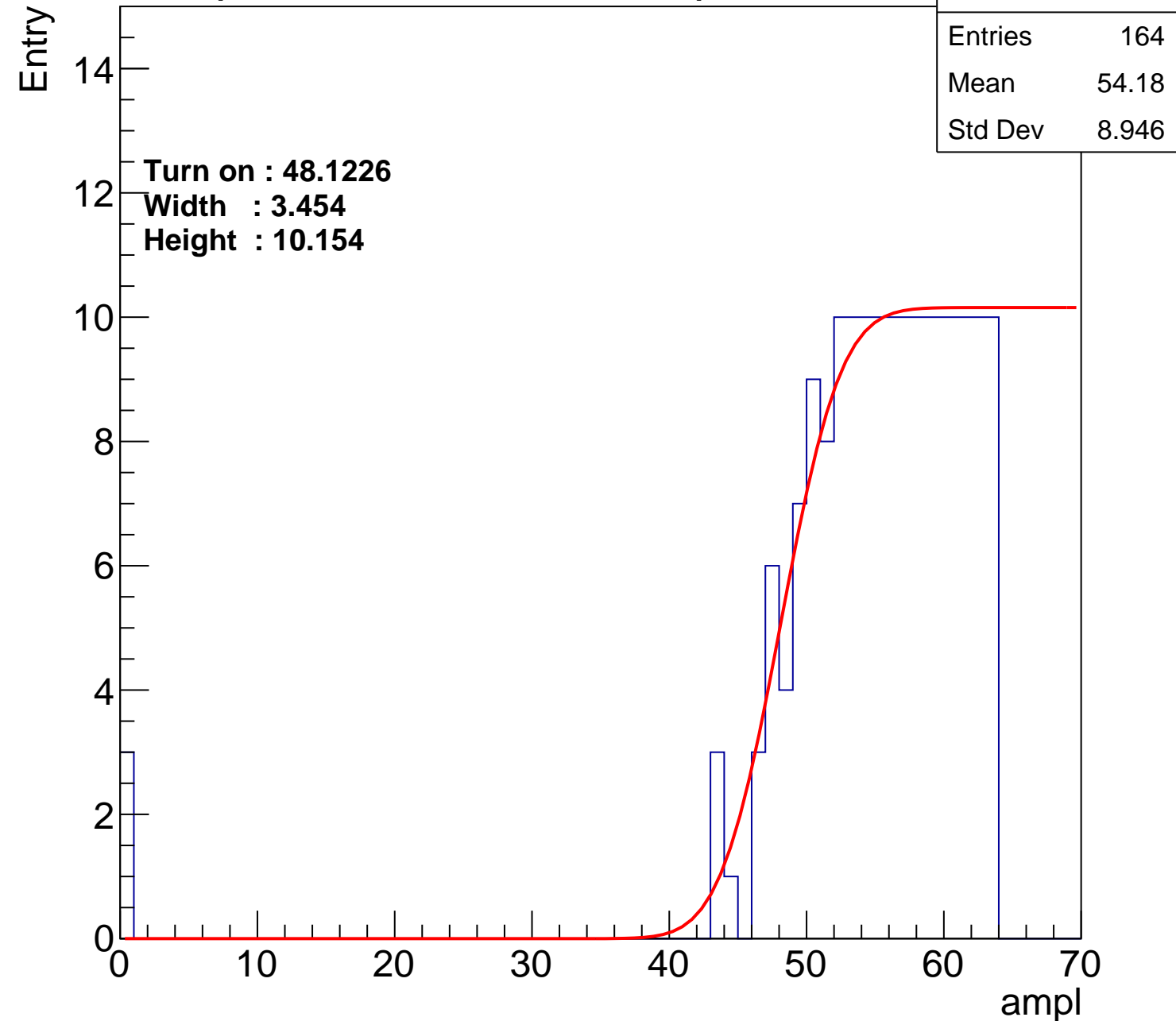
Width : 3.454

Height : 10.154

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch23

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	54.45
Std Dev	11.34

Turn on : 50.9132

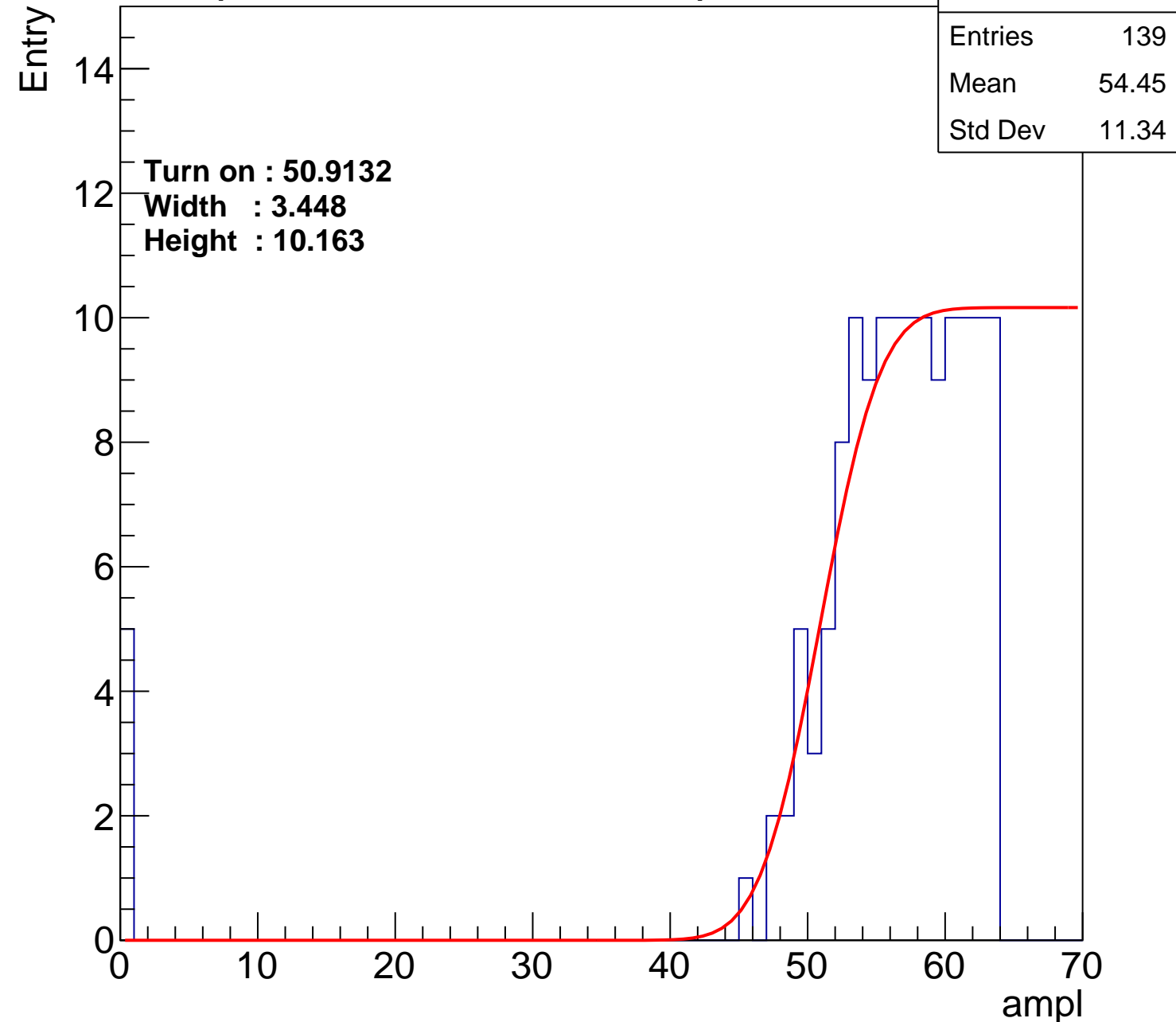
Width : 3.448

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch24

calib_packv5_040323_1717.root, FC#2, port C3

Entries	150
Mean	54.55
Std Dev	10.05

Turn on : 49.7432

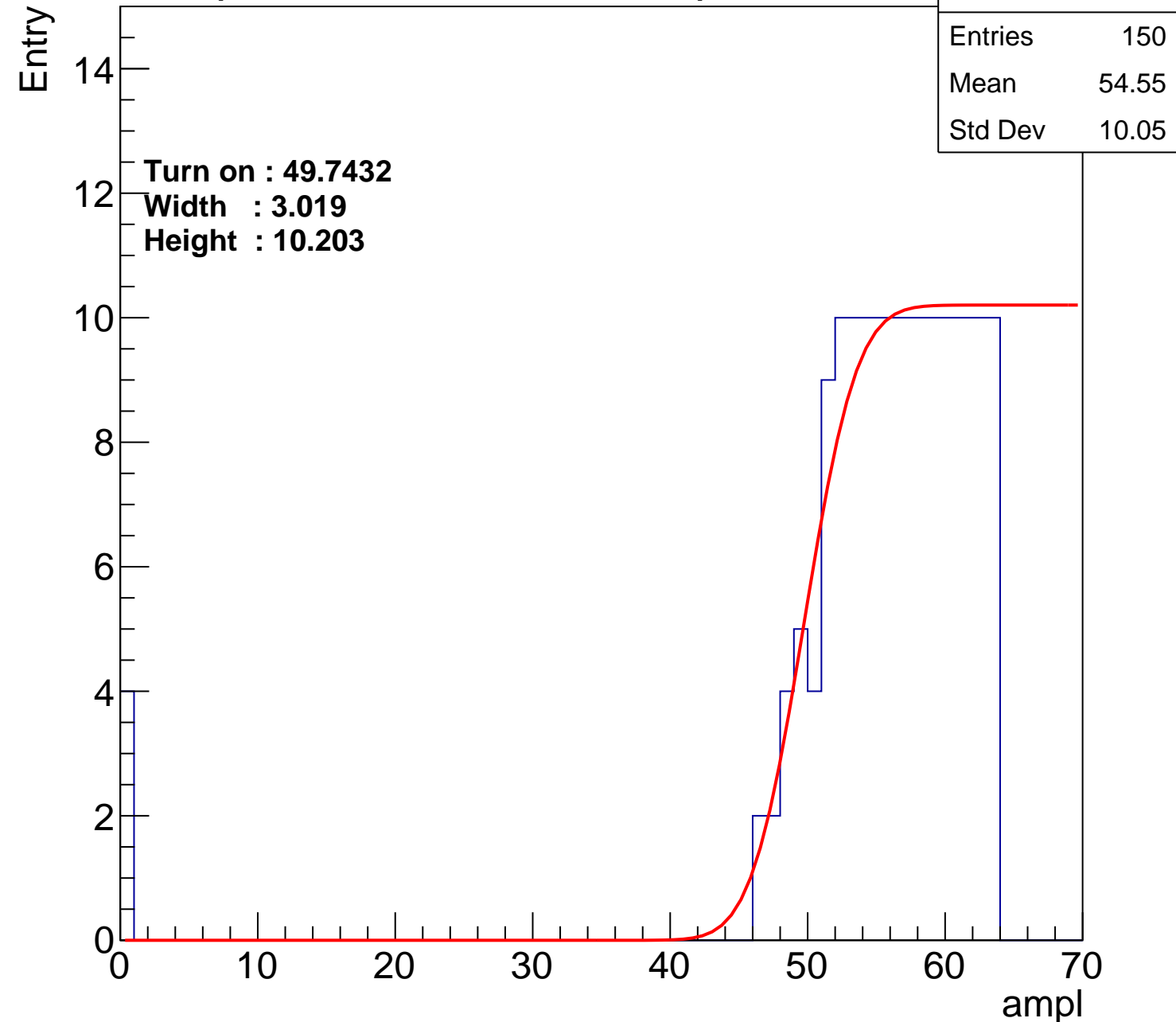
Width : 3.019

Height : 10.203

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch25

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.98
Std Dev	6.476

Turn on : 50.7807

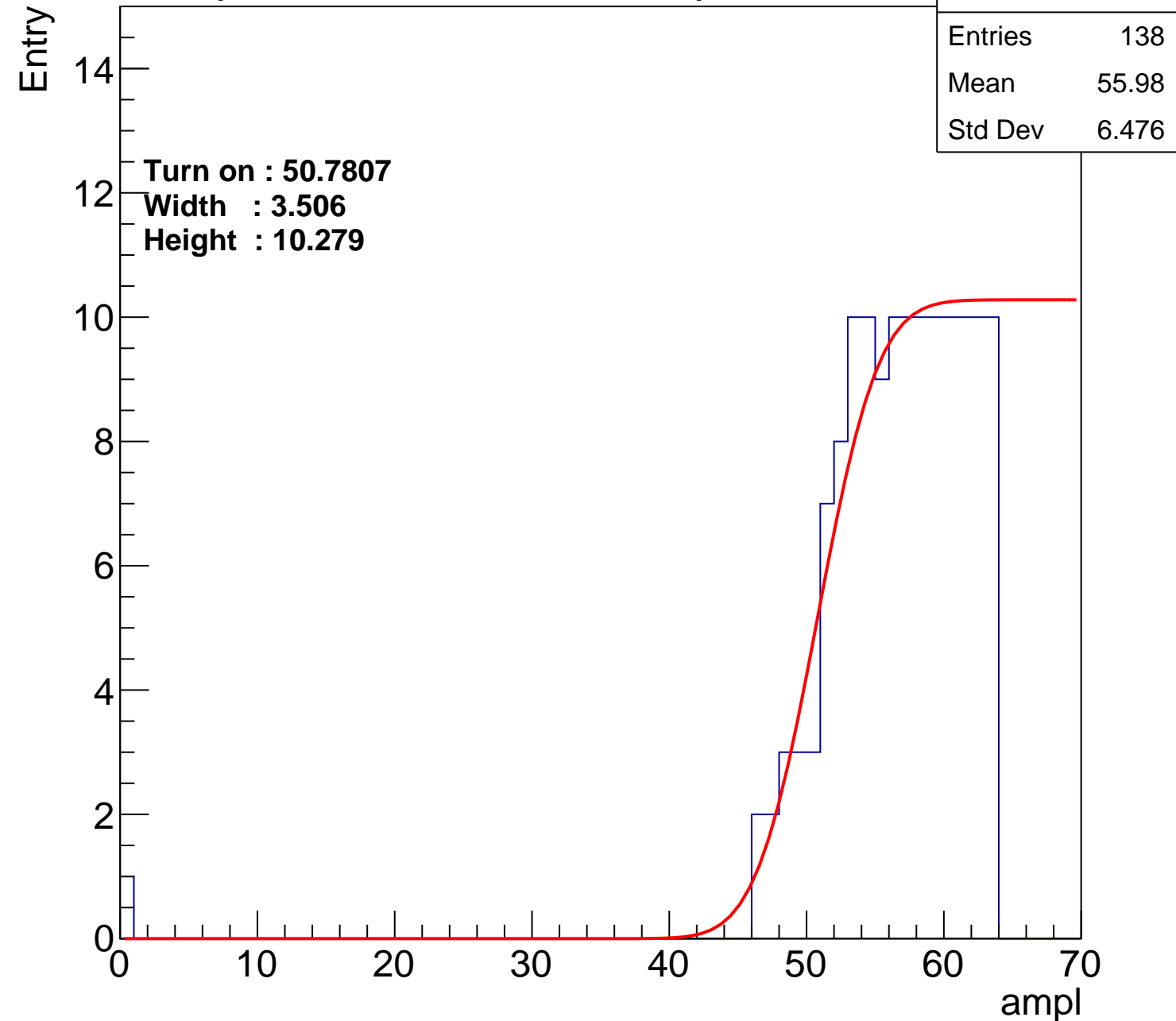
Width : 3.506

Height : 10.279

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch26

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.65
Std Dev	11.59

Turn on : 51.1201

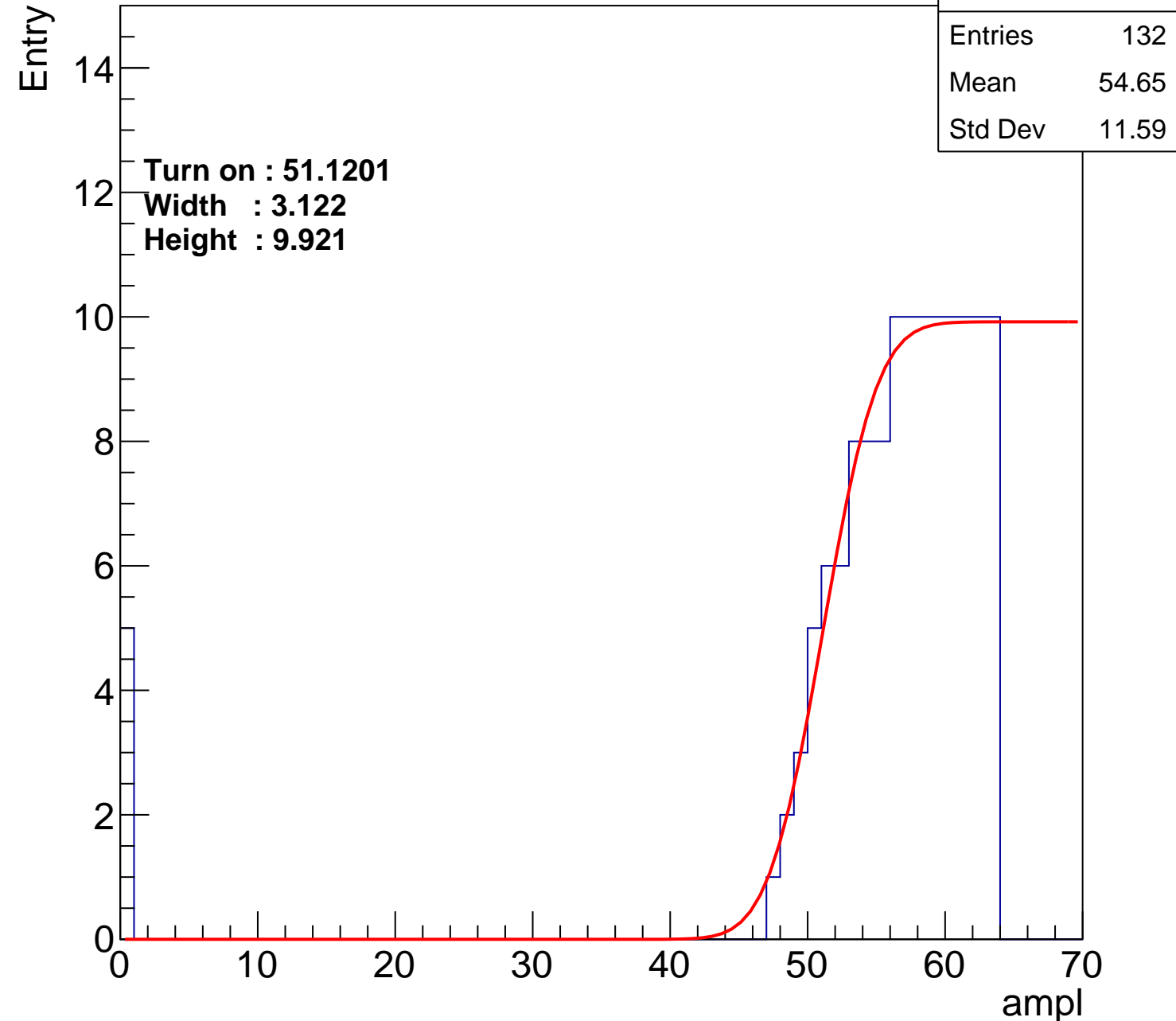
Width : 3.122

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch27

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.27
Std Dev	9.387

Turn on : 51.1246

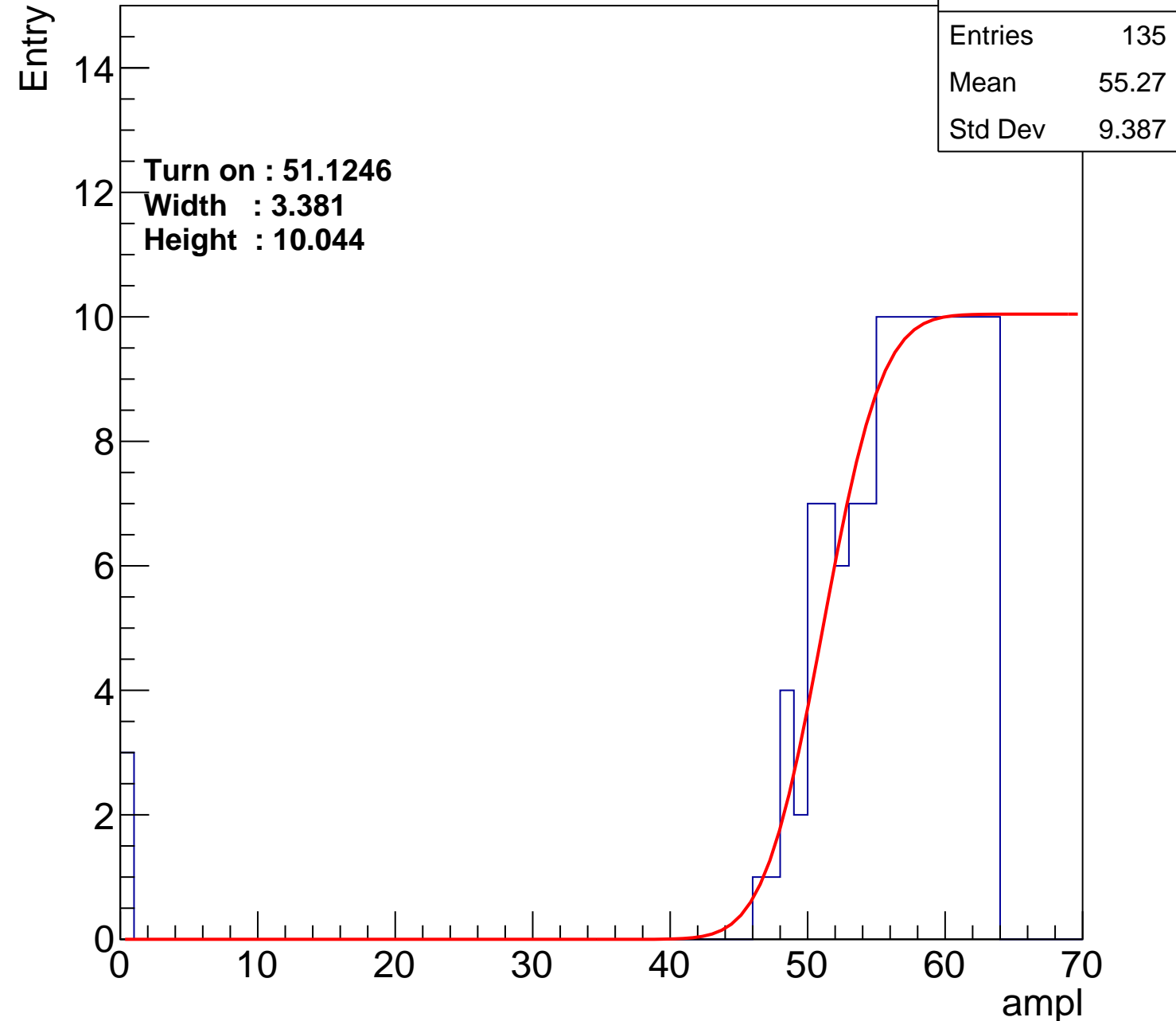
Width : 3.381

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch28

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.5
Std Dev	8.059

Turn on : 50.9442

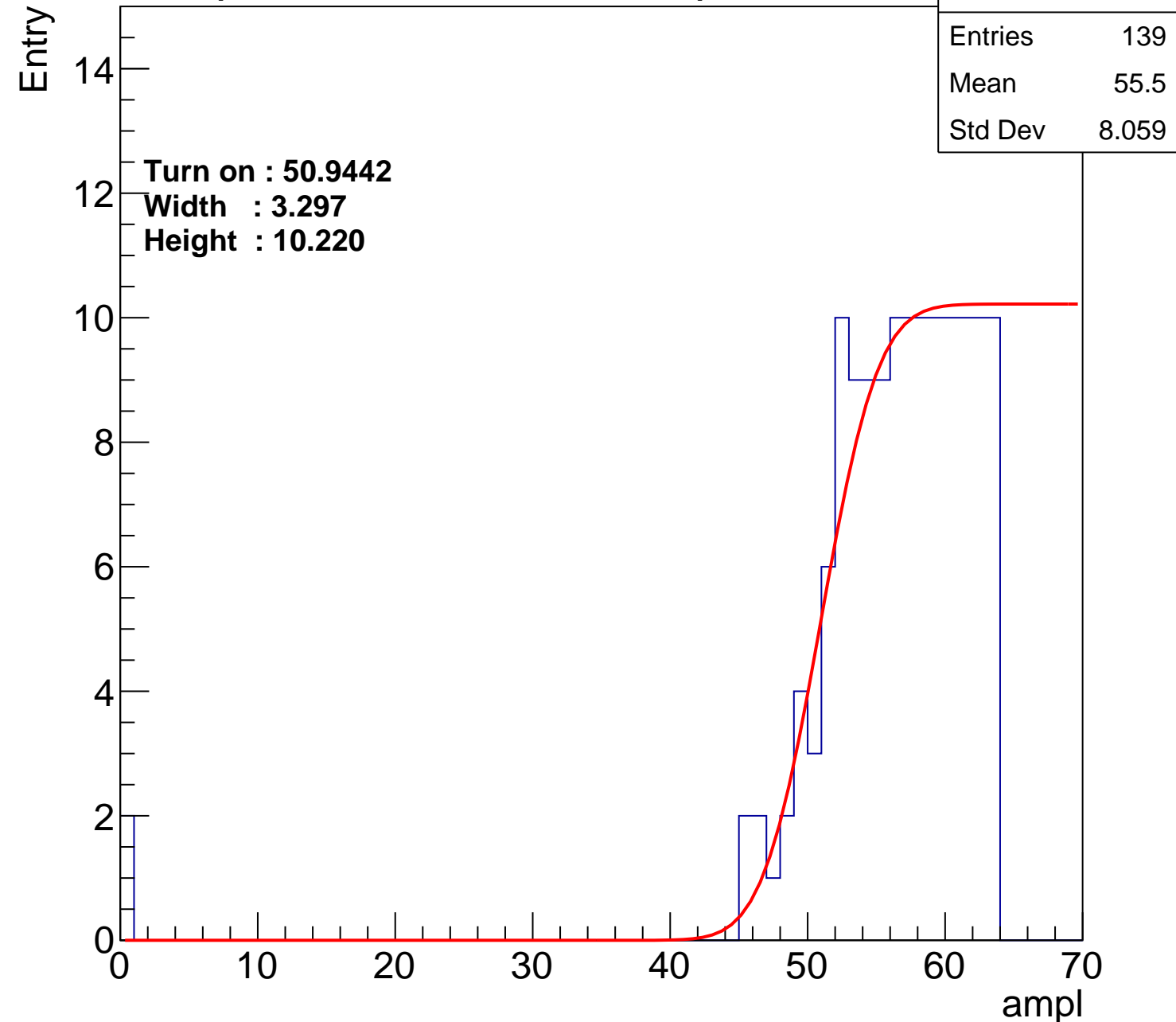
Width : 3.297

Height : 10.220

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch29

calib_packv5_040323_1717.root, FC#2, port C3

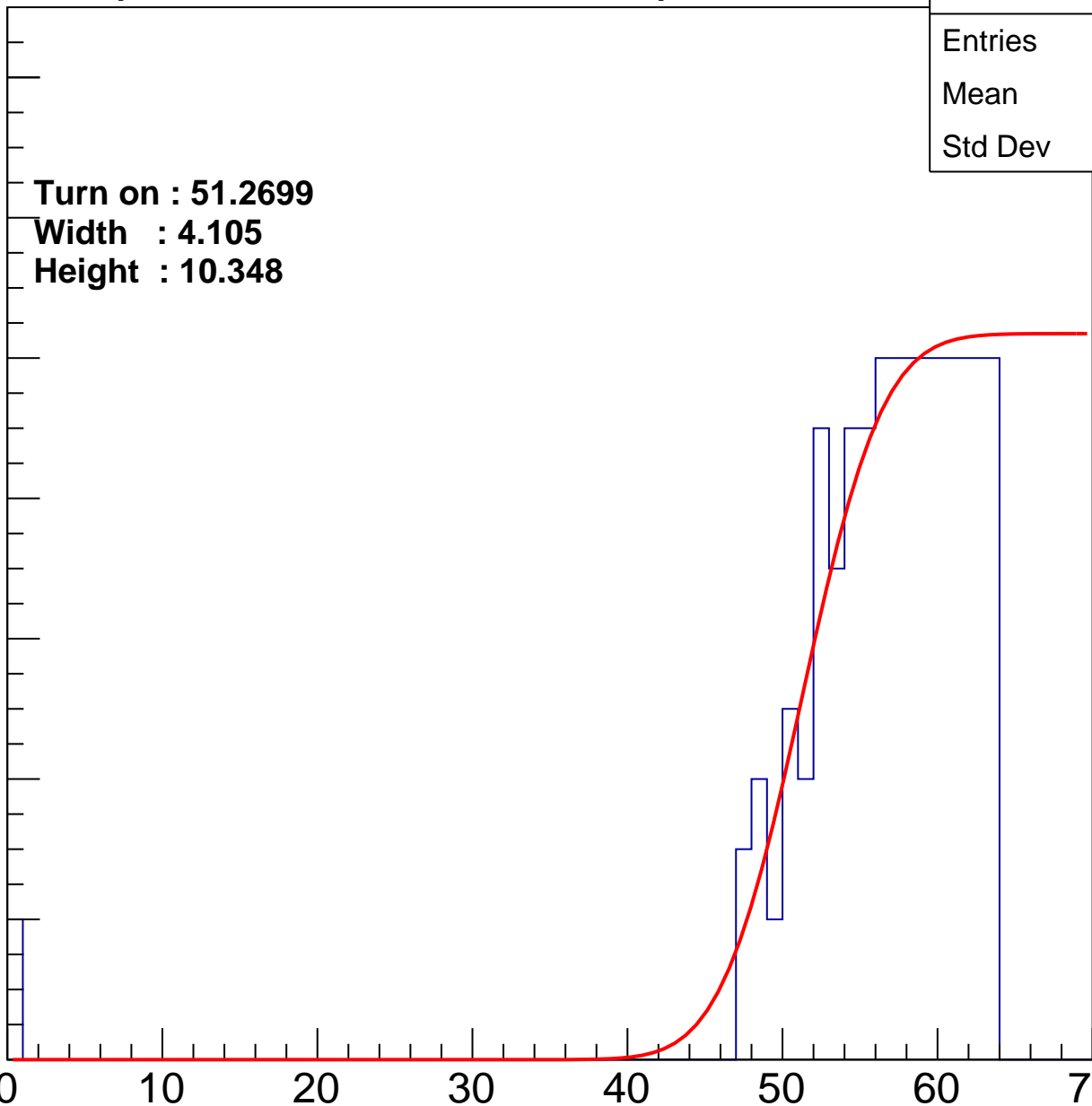
Entry

14
12
10
8
6
4
2
0

Turn on : 51.2699
Width : 4.105
Height : 10.348

Entries	134
Mean	55.71
Std Dev	8.099

ampl



B0L103S, U14-ch30

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.98
Std Dev	9.027

Turn on : 49.5864

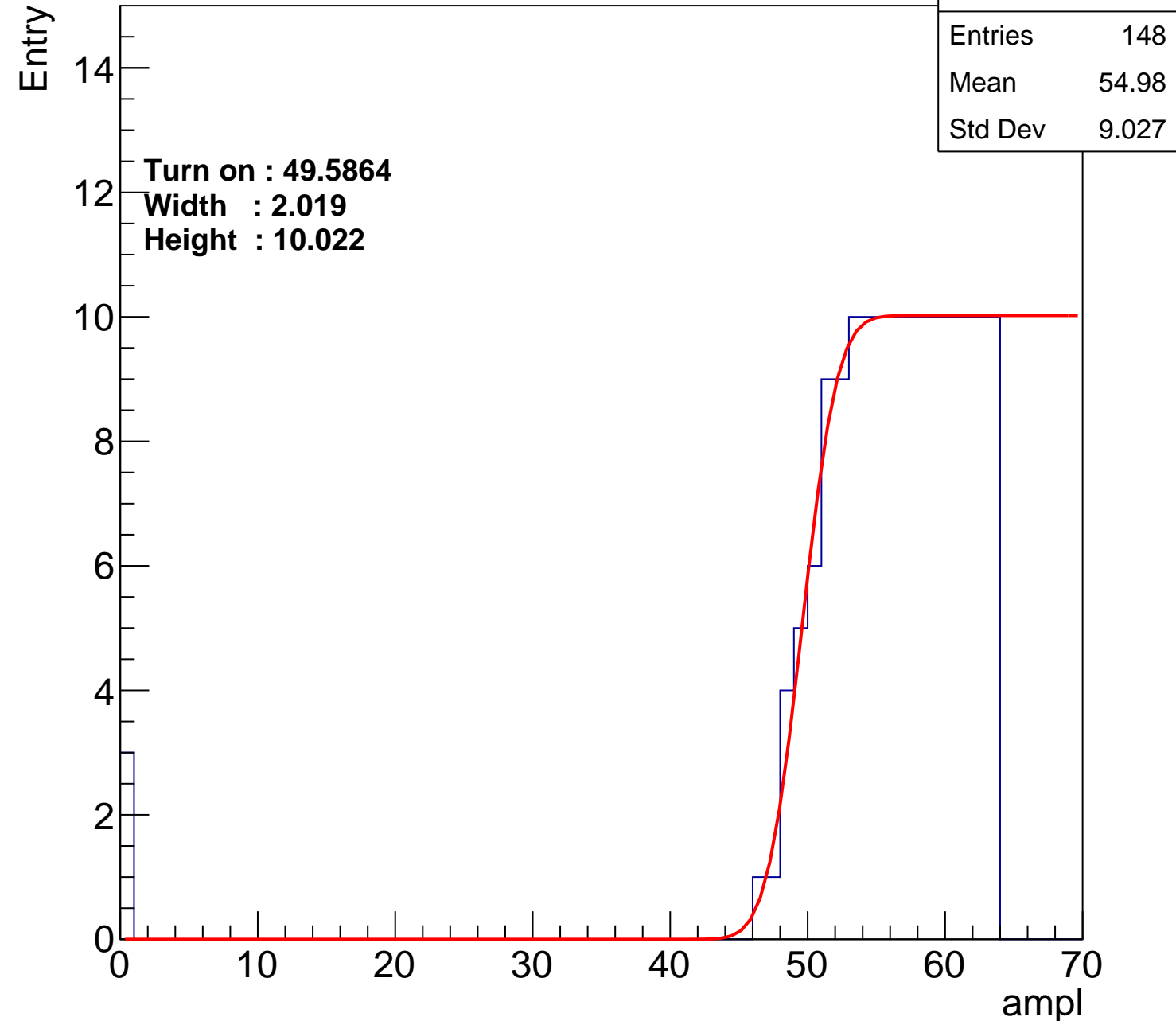
Width : 2.019

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch31

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.43
Std Dev	8

Turn on : 50.2103

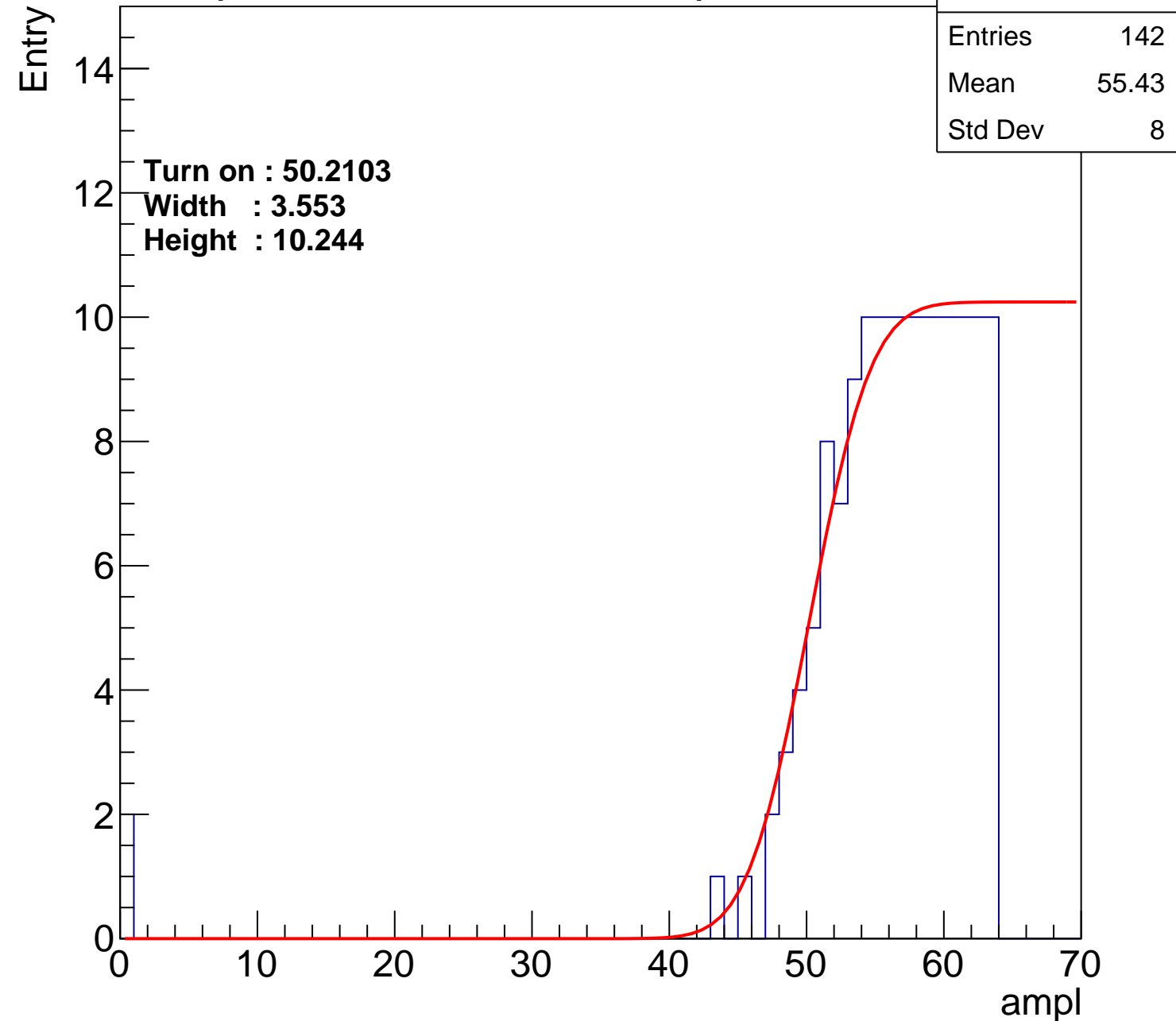
Width : 3.553

Height : 10.244

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch32

calib_packv5_040323_1717.root, FC#2, port C3

Entries	152
Mean	54.94
Std Dev	7.924

Turn on : 48.3107

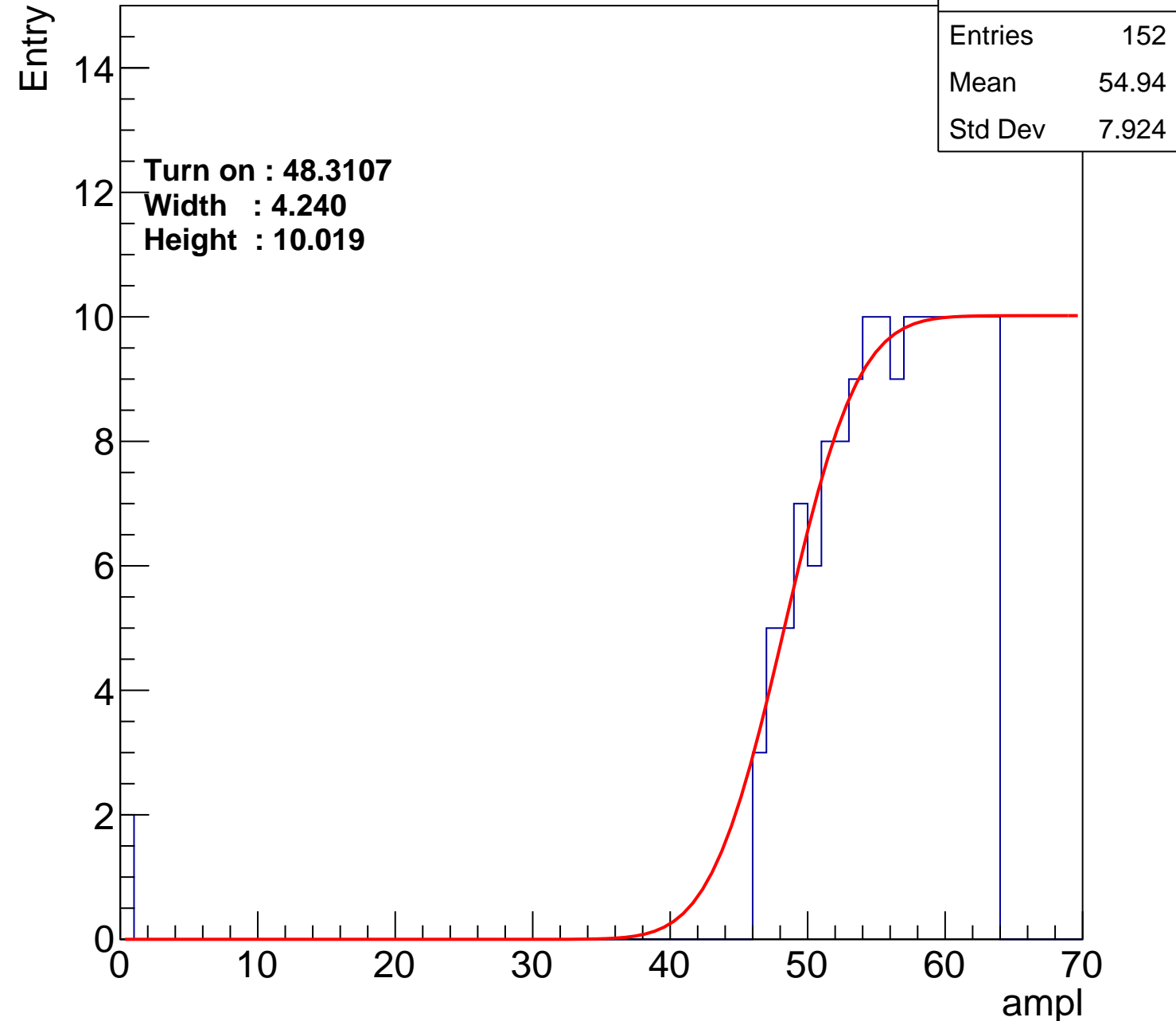
Width : 4.240

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch33

calib_packv5_040323_1717.root, FC#2, port C3

Entries	158
Mean	54.43
Std Dev	8.973

Turn on : 48.3895

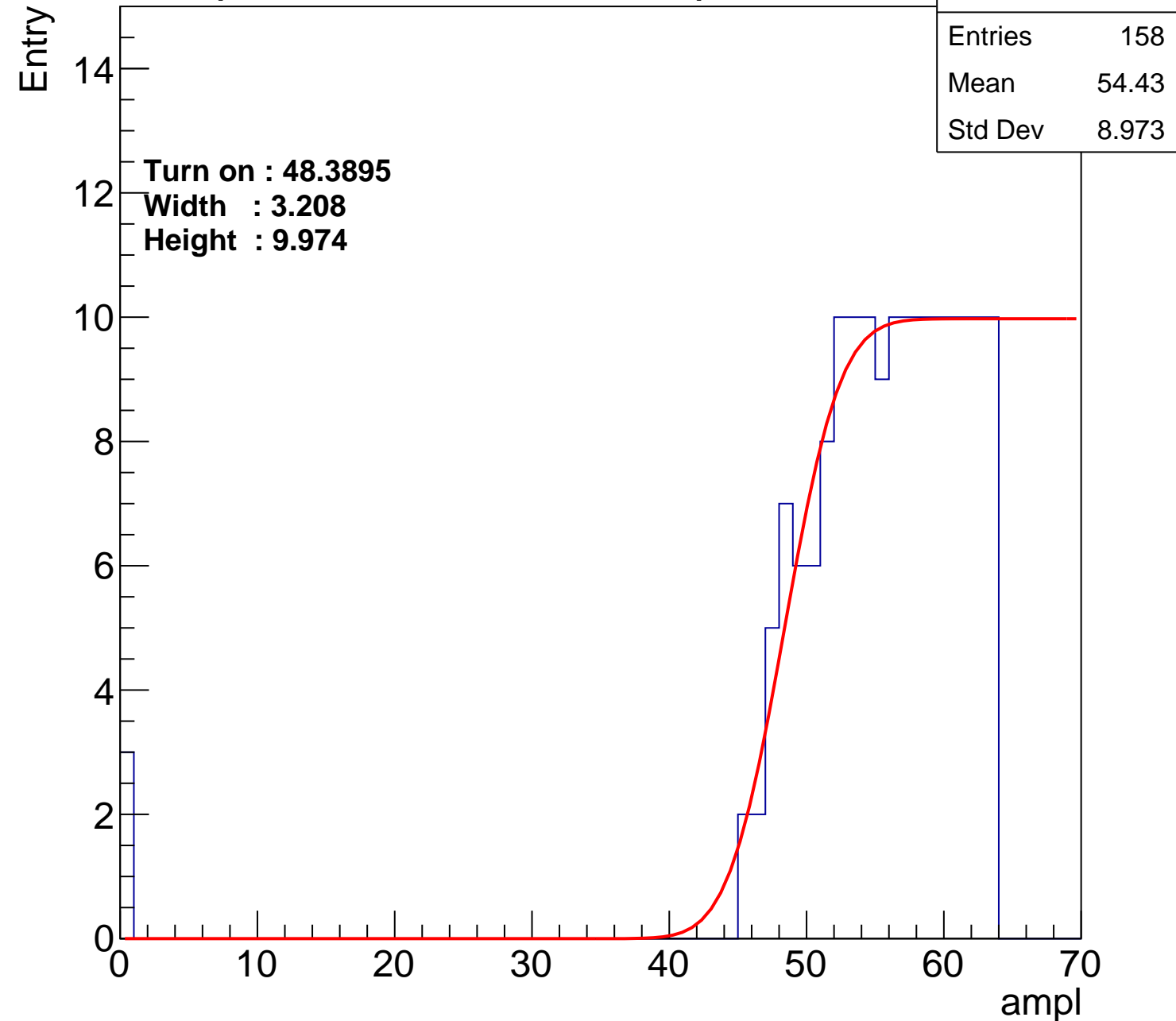
Width : 3.208

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch34

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.63
Std Dev	7.963

Turn on : 50.6038

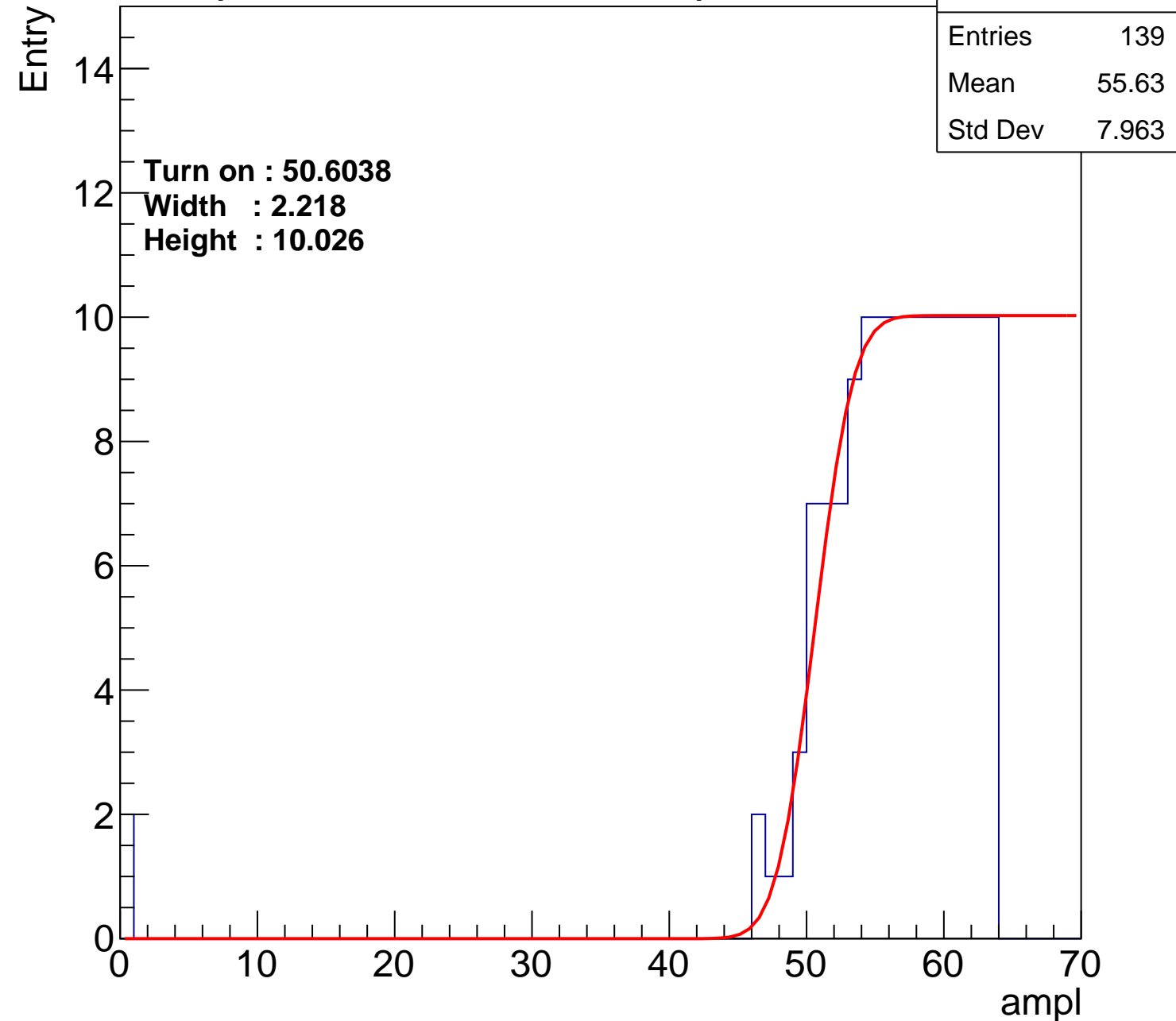
Width : 2.218

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch35

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	54.89
Std Dev	9.169

Turn on : 50.0600

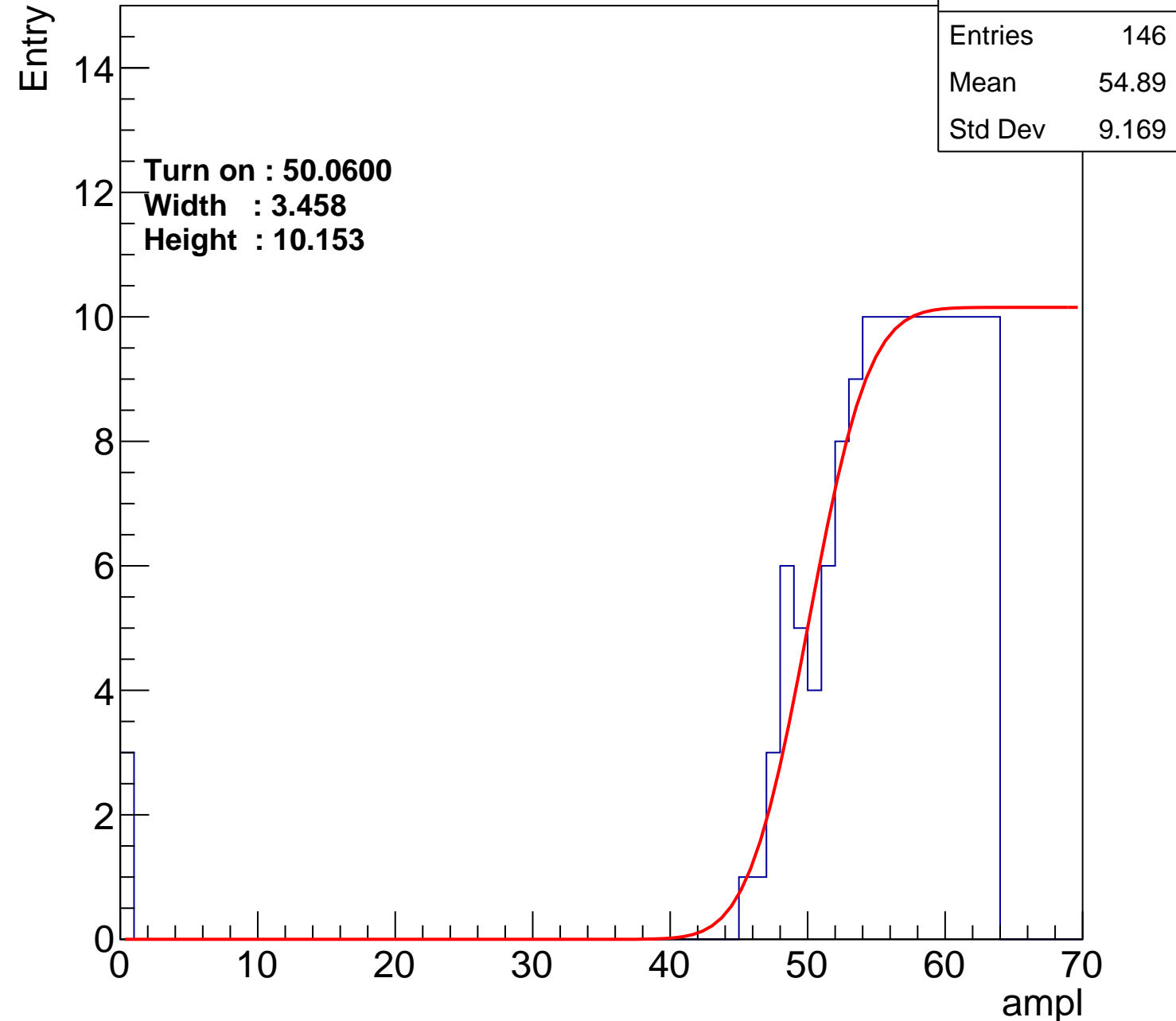
Width : 3.458

Height : 10.153

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch36

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	56.25
Std Dev	6.421

Turn on : 50.2062

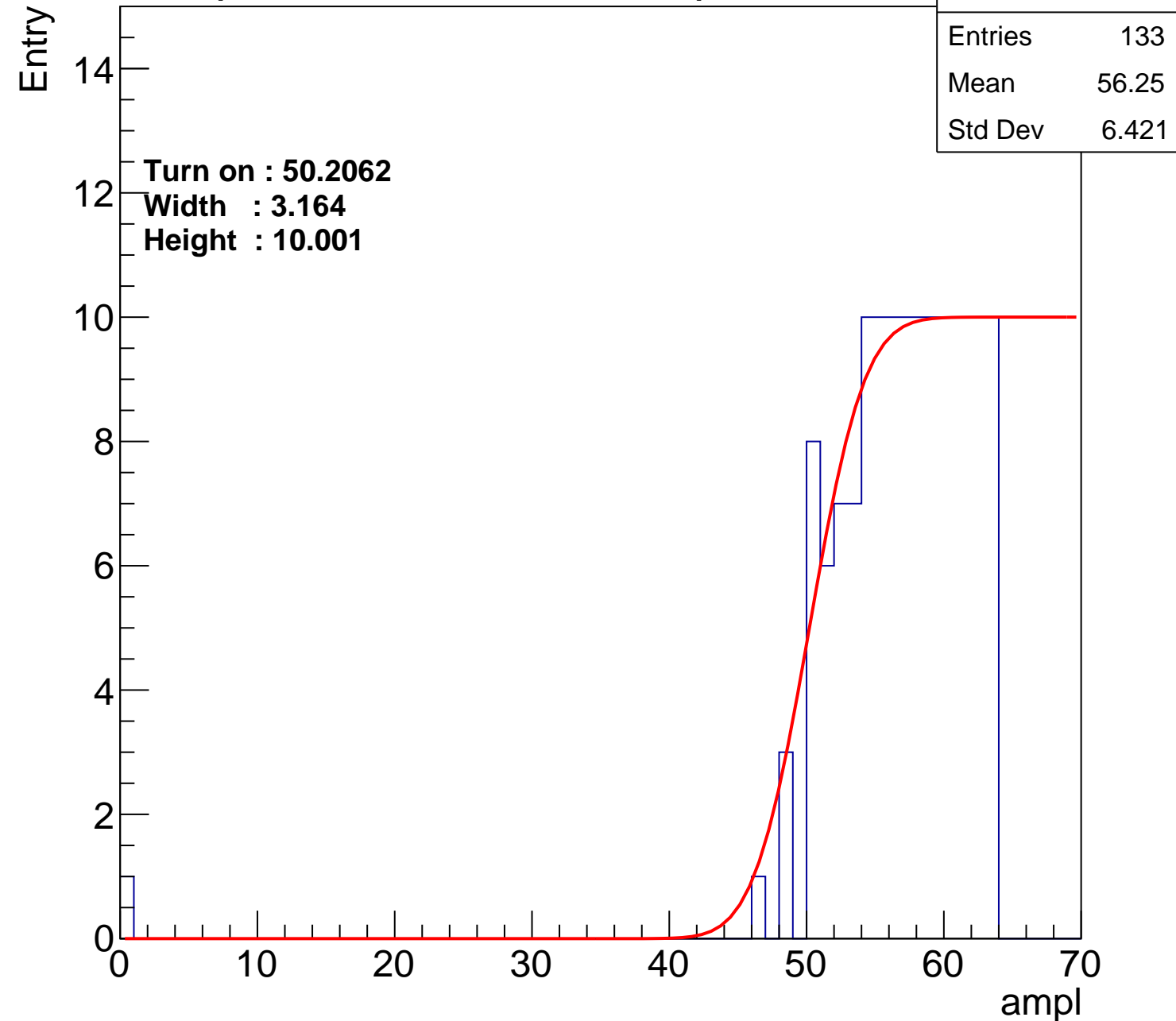
Width : 3.164

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch37

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.82
Std Dev	8.053

Turn on : 51.3544

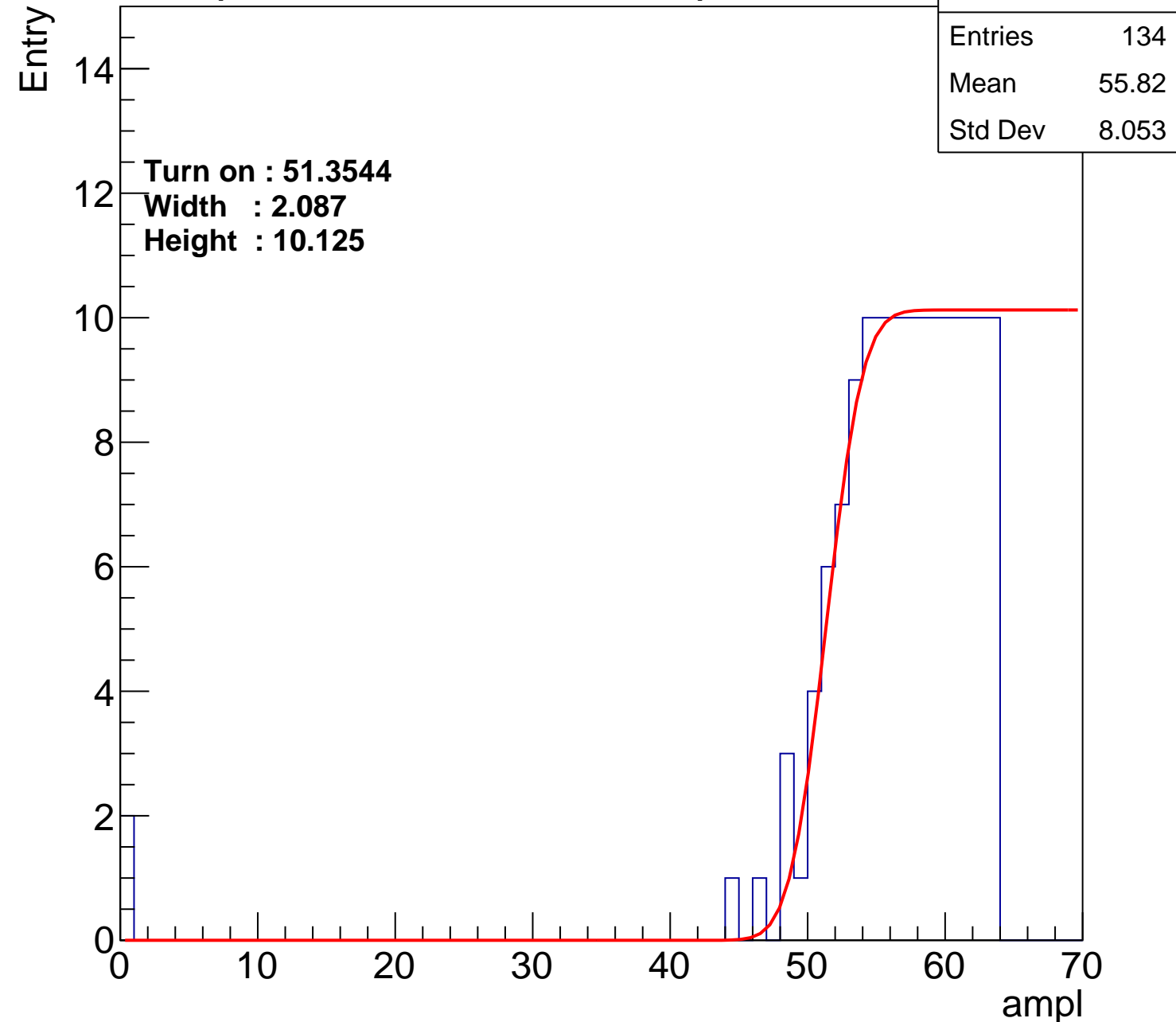
Width : 2.087

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch38

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.84
Std Dev	9.135

Turn on : 51.4142

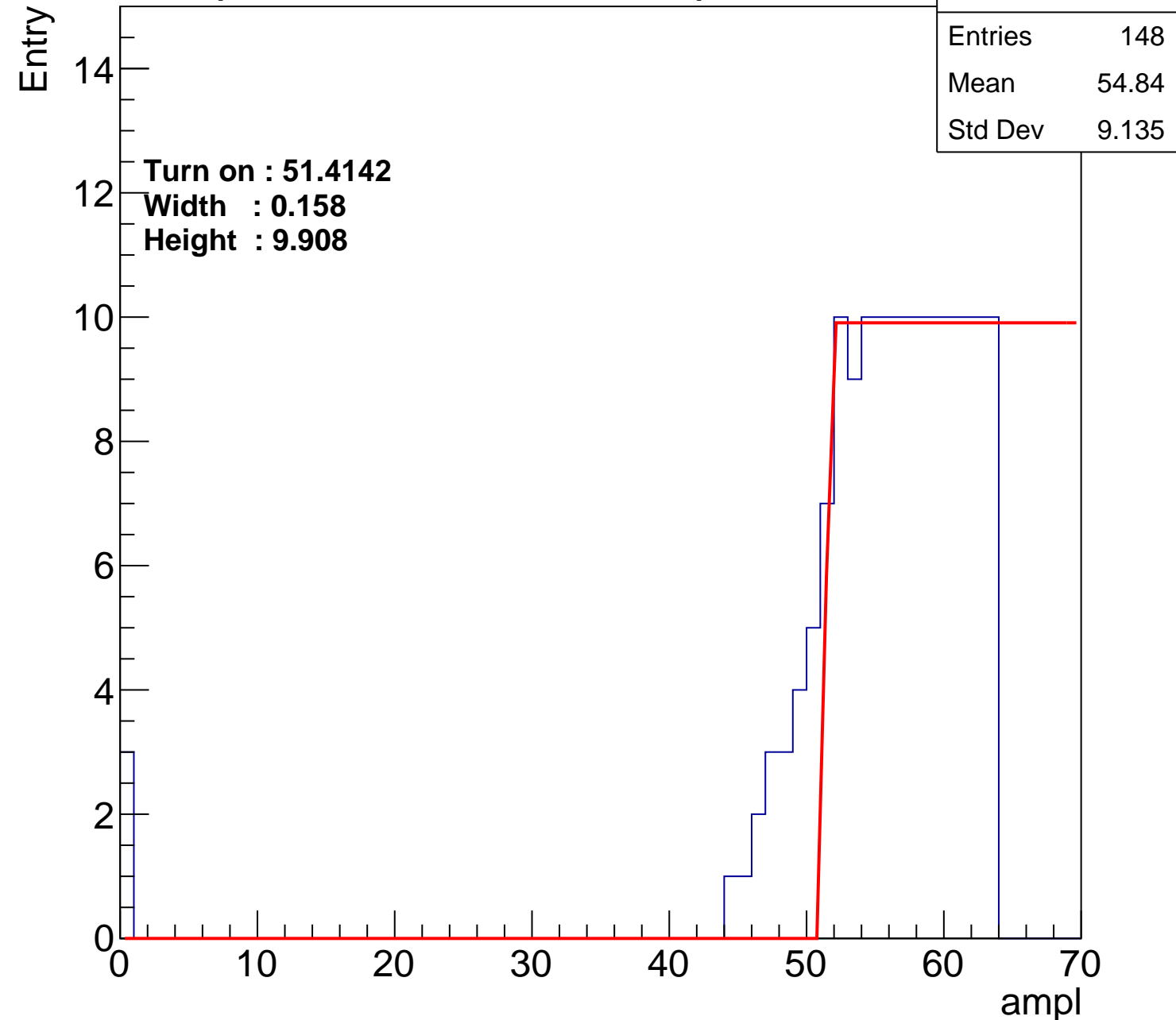
Width : 0.158

Height : 9.908

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch39

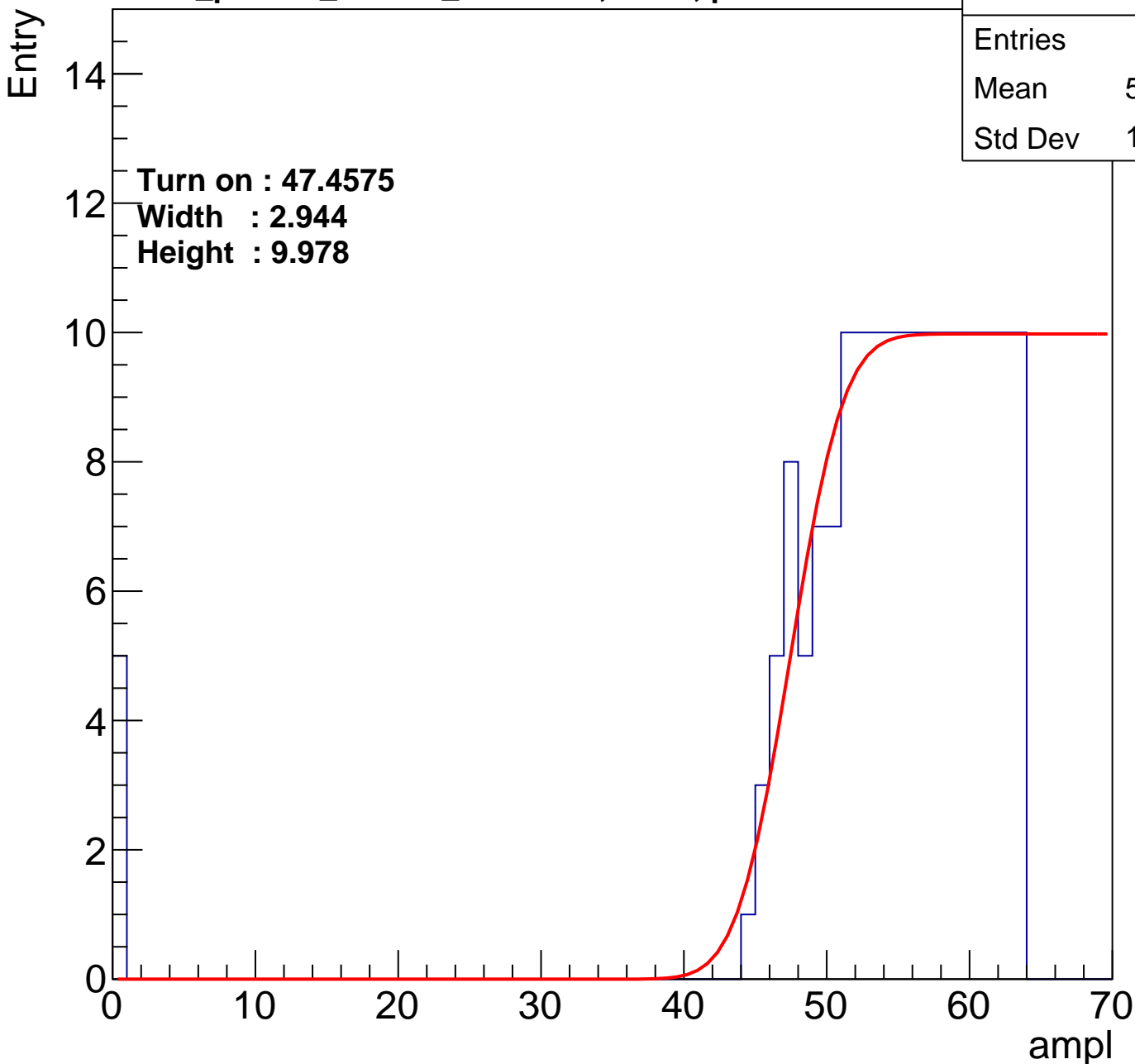
calib_packv5_040323_1717.root, FC#2, port C3

Entries	171
Mean	53.38
Std Dev	10.55

Turn on : 47.4575

Width : 2.944

Height : 9.978



B0L103S, U14-ch40

calib_packv5_040323_1717.root, FC#2, port C3

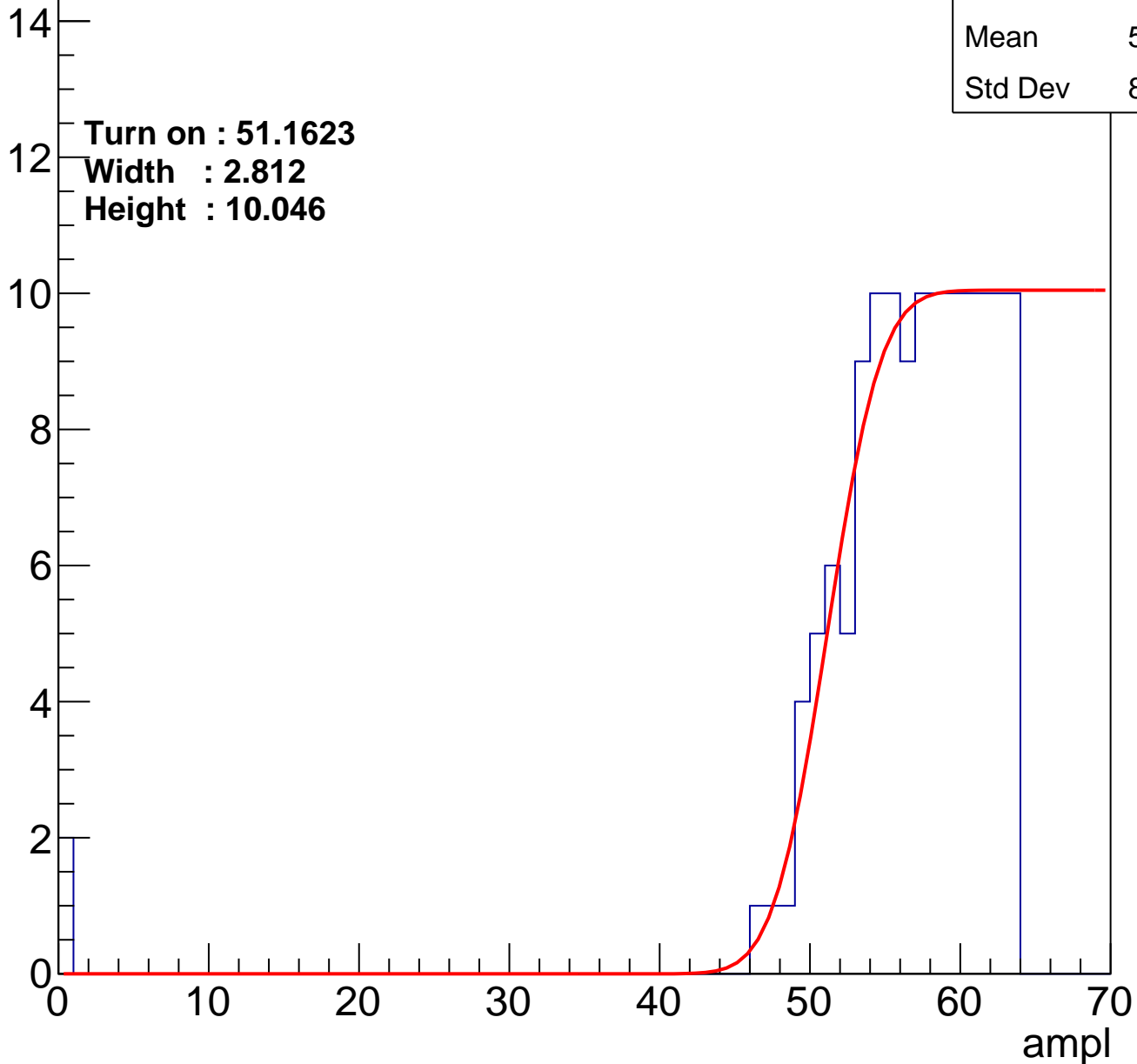
Entries	133
Mean	55.82
Std Dev	8.064

Turn on : 51.1623

Width : 2.812

Height : 10.046

Entry



B0L103S, U14-ch41

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.39
Std Dev	9.375

Turn on : 51.2766

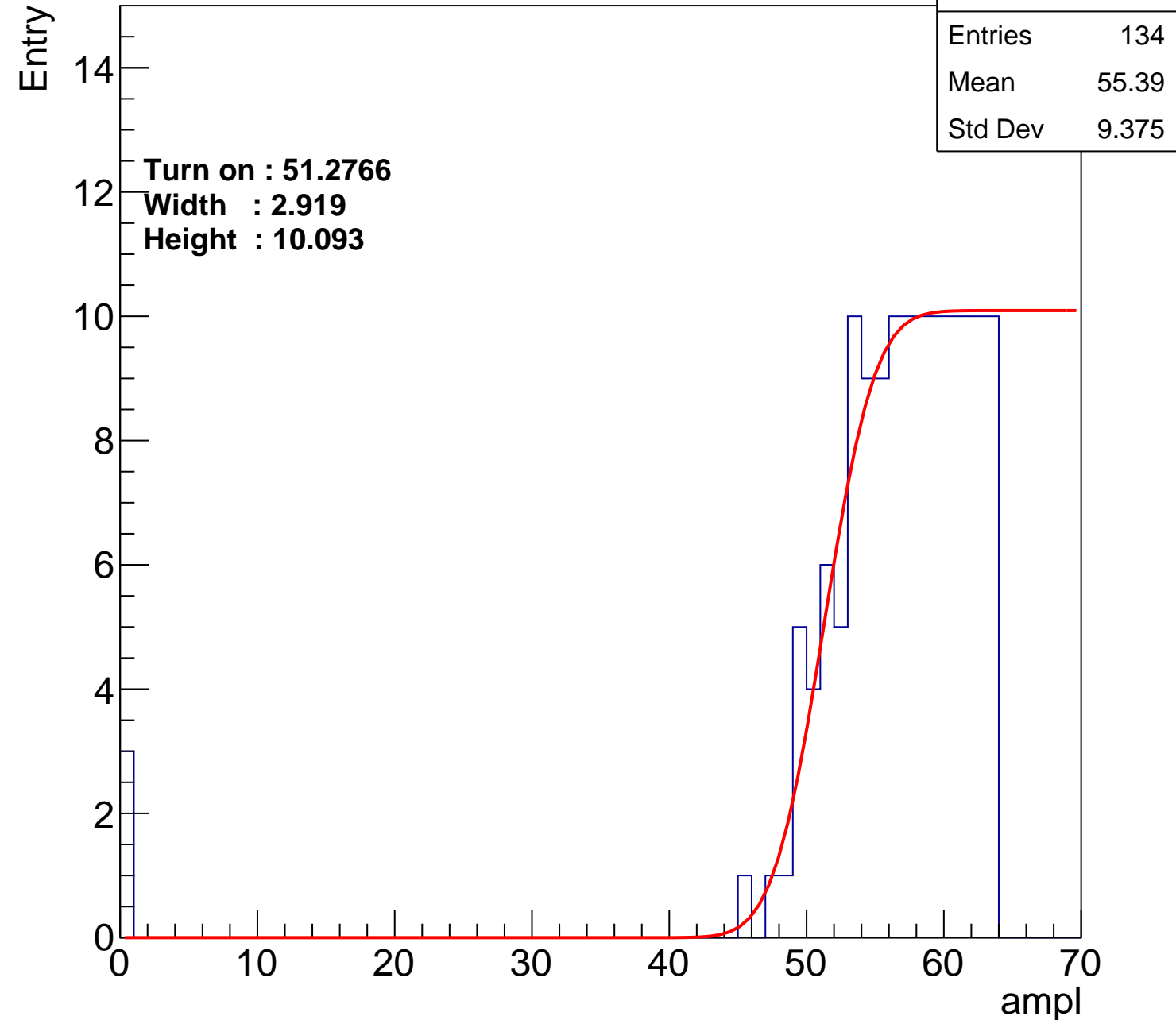
Width : 2.919

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch42

calib_packv5_040323_1717.root, FC#2, port C3

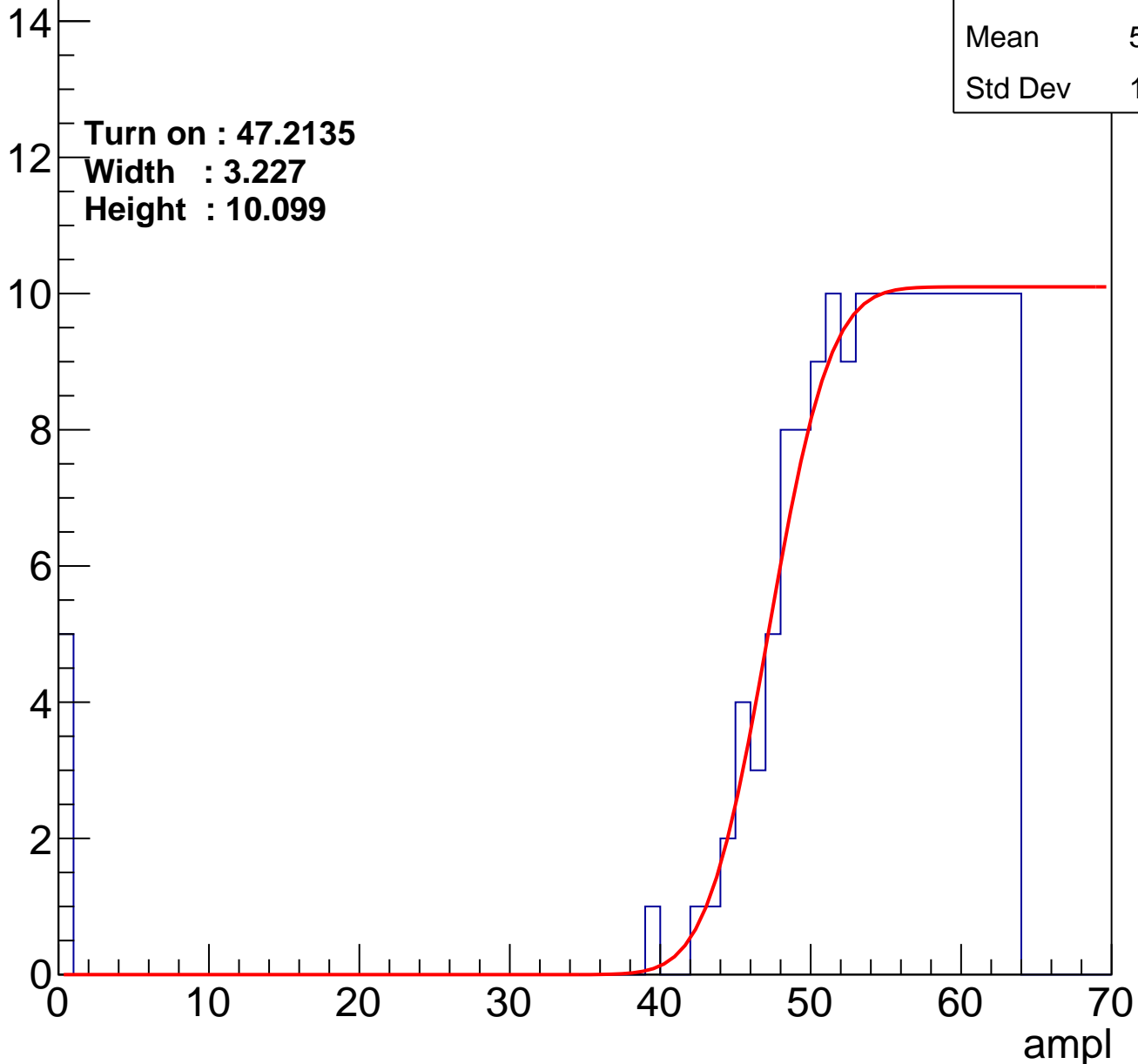
Entries	176
Mean	53.12
Std Dev	10.53

Turn on : 47.2135

Width : 3.227

Height : 10.099

Entry



B0L103S, U14-ch43

calib_packv5_040323_1717.root, FC#2, port C3

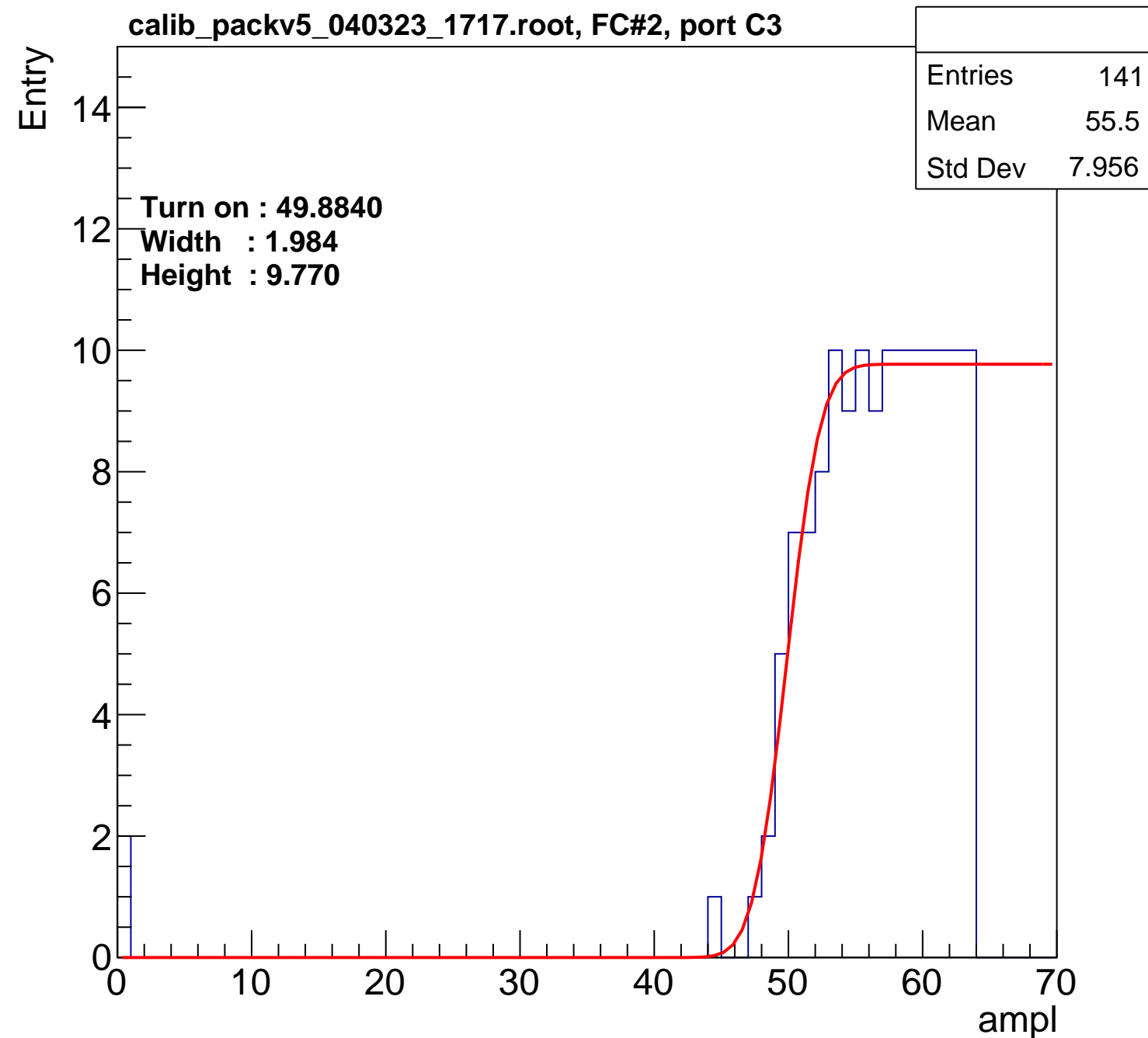
Entry

14
12
10
8
6
4
2
0

Turn on : 49.8840
Width : 1.984
Height : 9.770

Entries	141
Mean	55.5
Std Dev	7.956

ampl



B0L103S, U14-ch44

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.47
Std Dev	8.091

Turn on : 50.9810

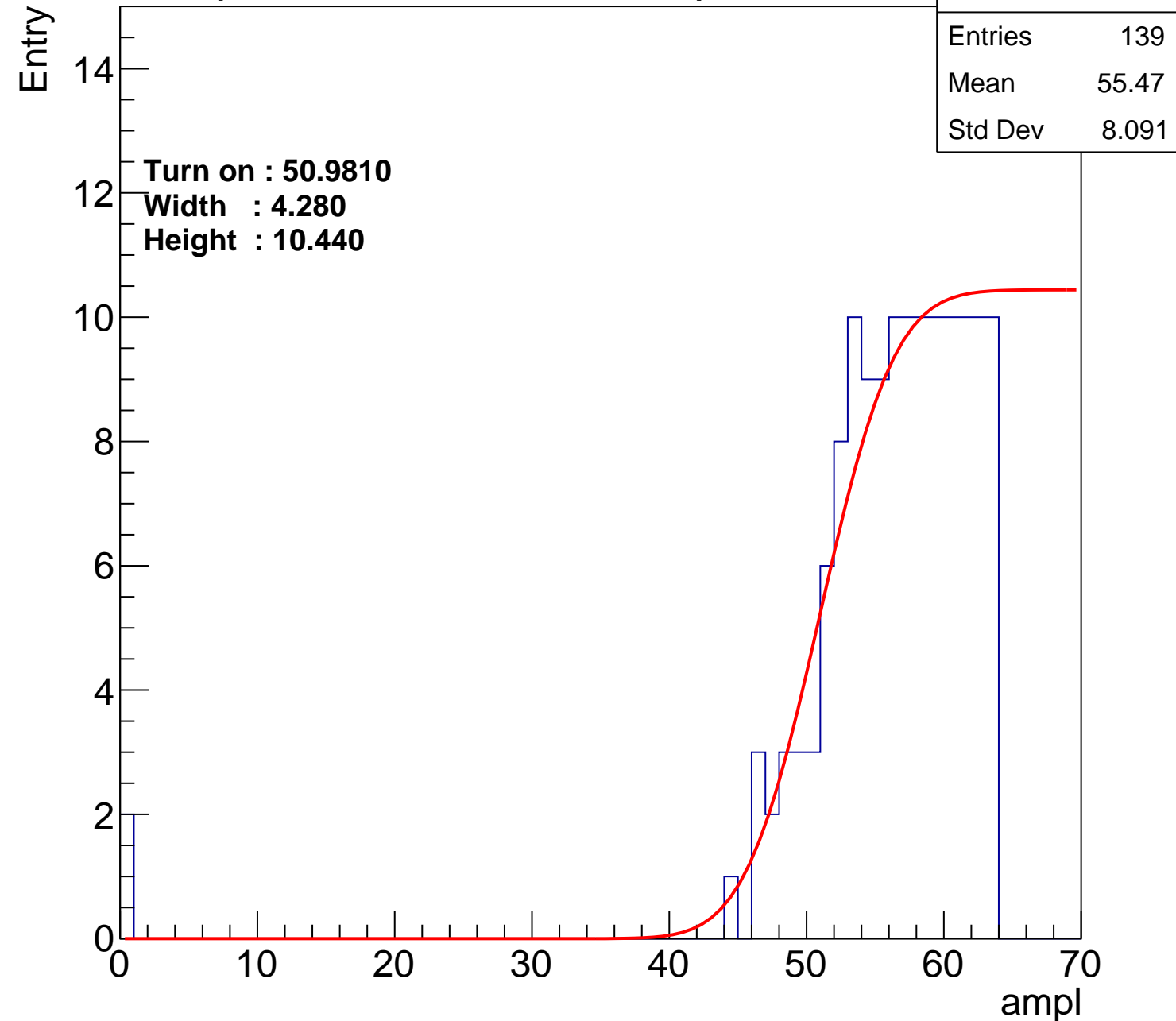
Width : 4.280

Height : 10.440

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch45

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	54.9
Std Dev	9.301

Turn on : 51.6438

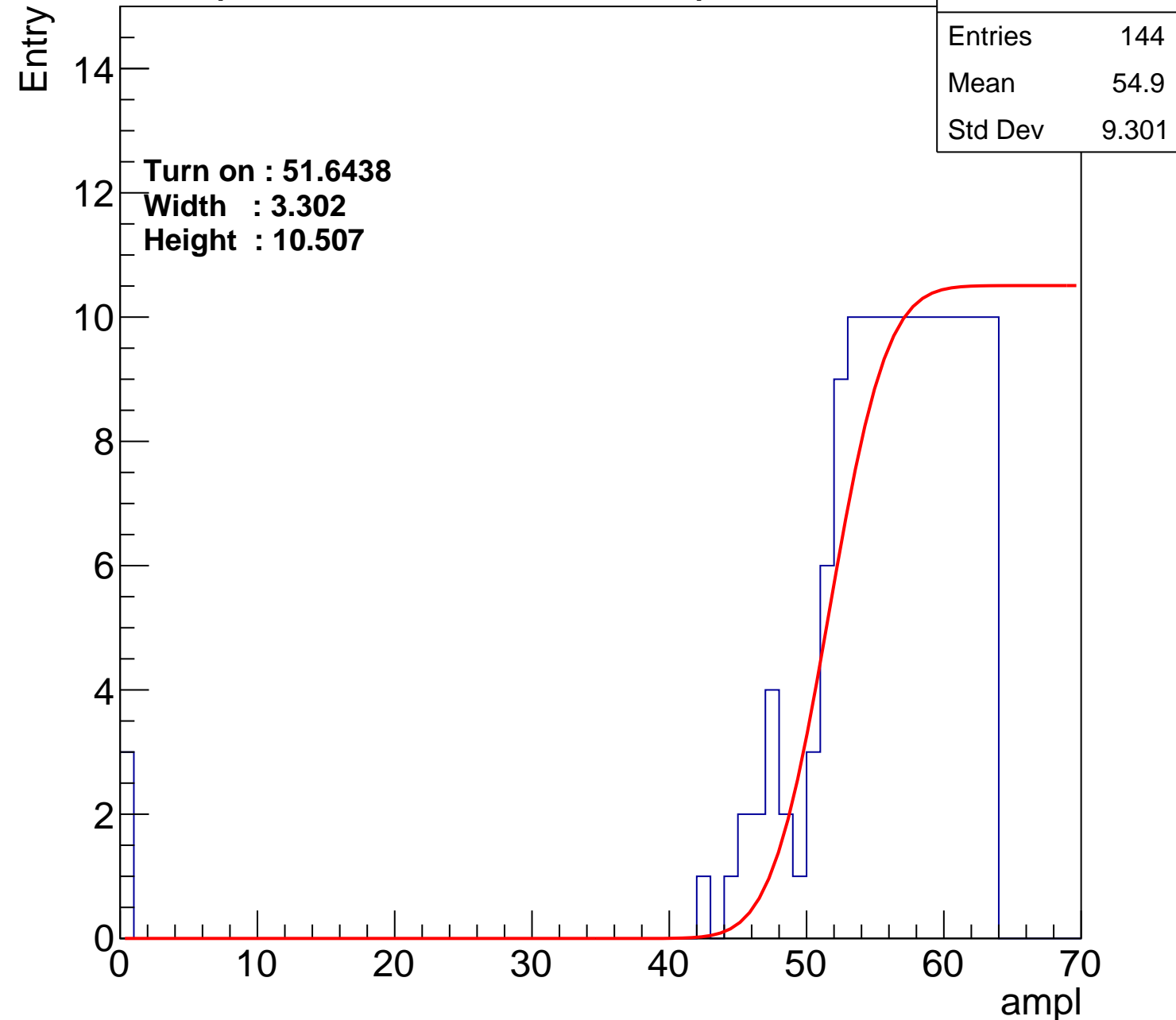
Width : 3.302

Height : 10.507

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch46

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	55.04
Std Dev	9.194

Turn on : 50.1103

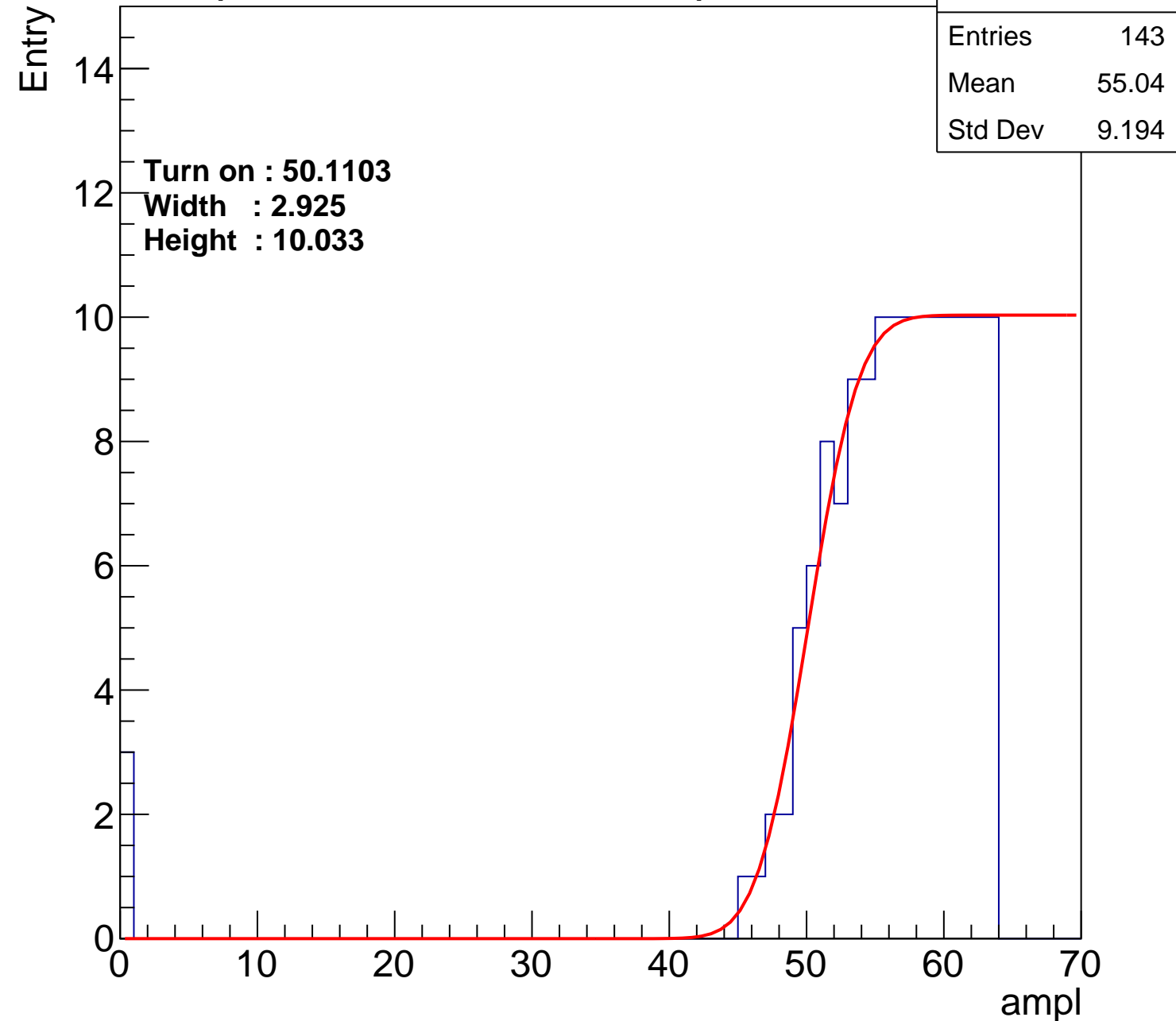
Width : 2.925

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch47

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	55.1
Std Dev	9.157

Turn on : 49.5007

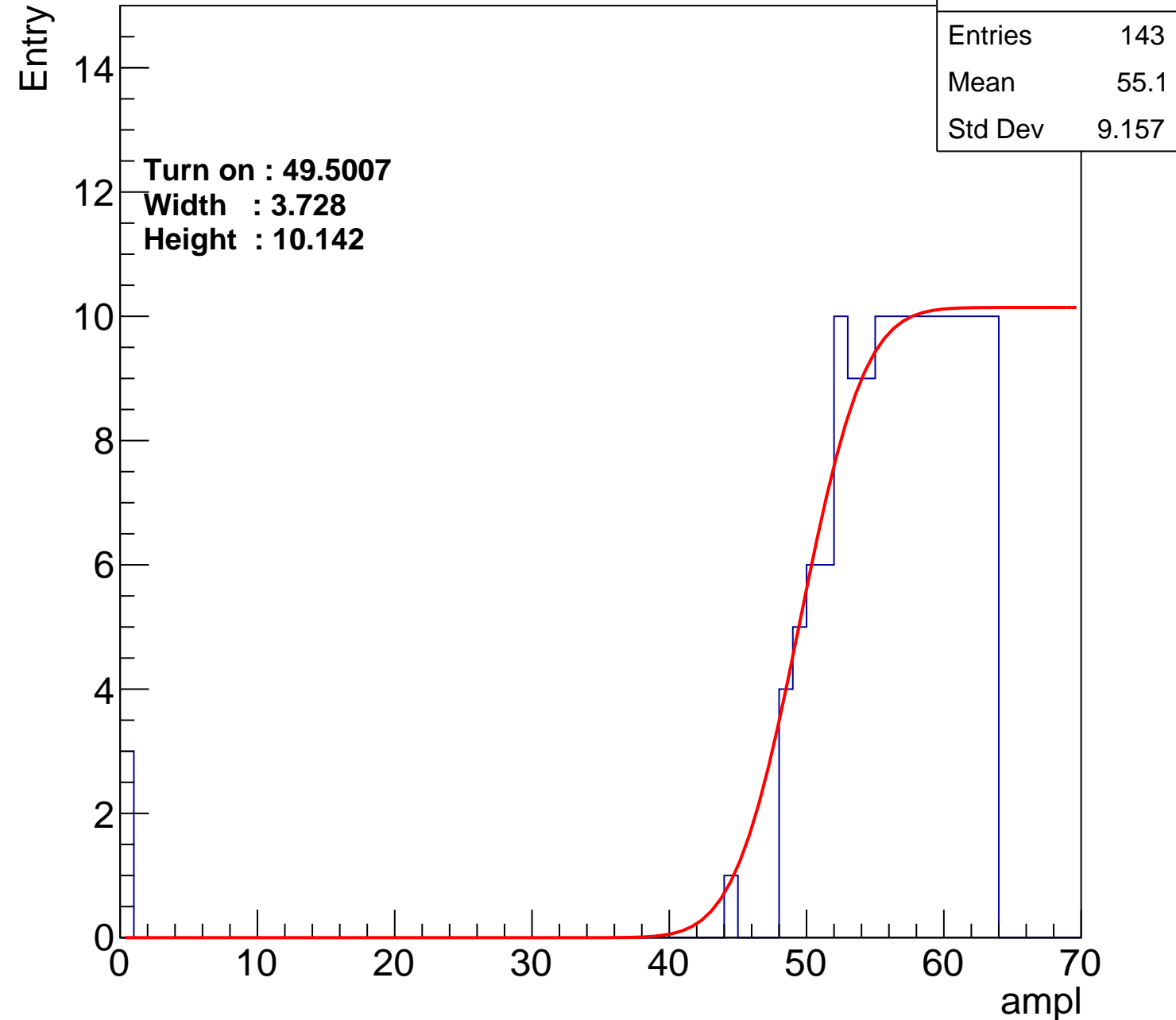
Width : 3.728

Height : 10.142

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch48

calib_packv5_040323_1717.root, FC#2, port C3

Entries	150
Mean	54.8
Std Dev	9.077

Turn on : 49.6752

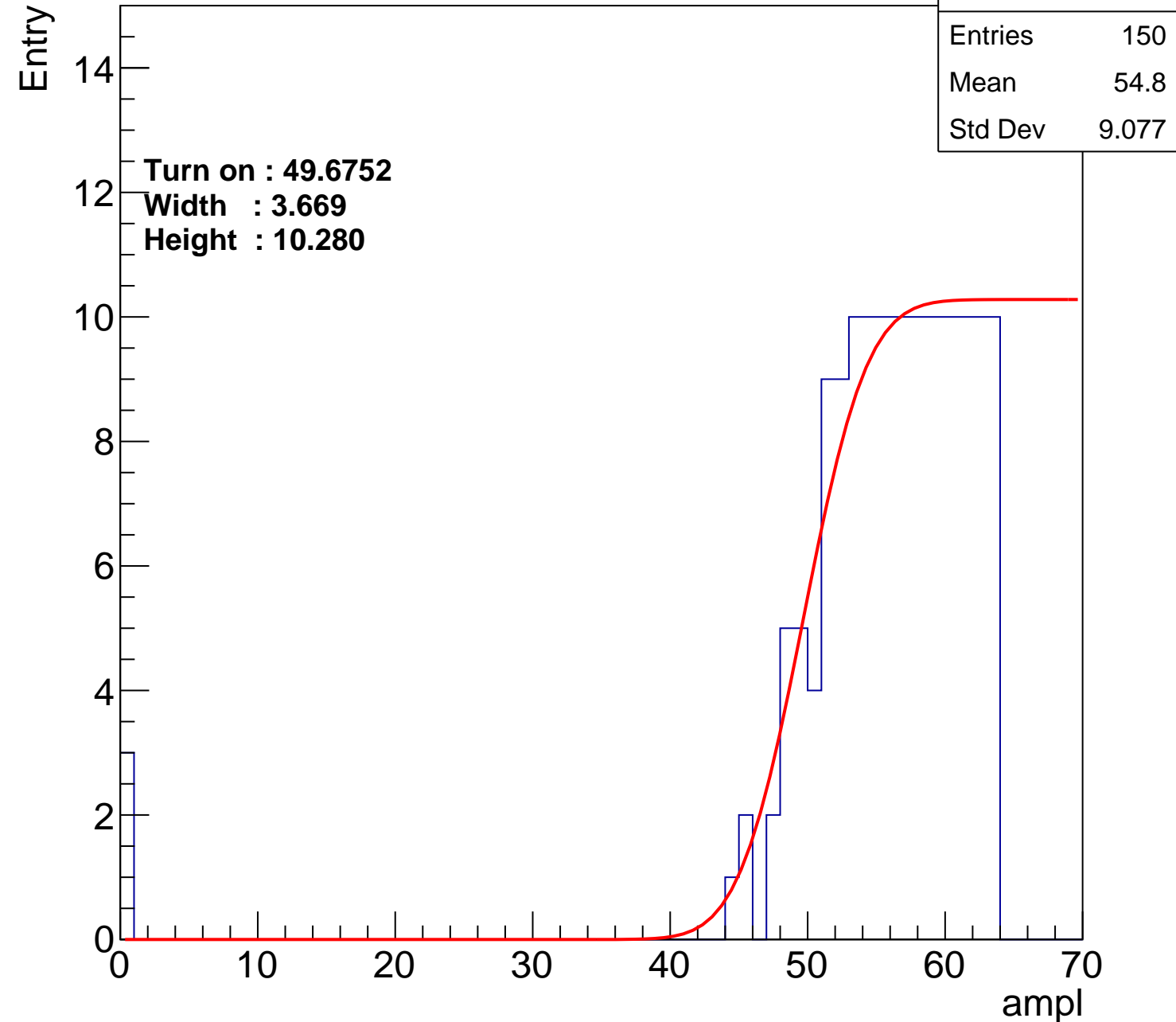
Width : 3.669

Height : 10.280

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch49

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	54.27
Std Dev	12.39

Turn on : 51.5432

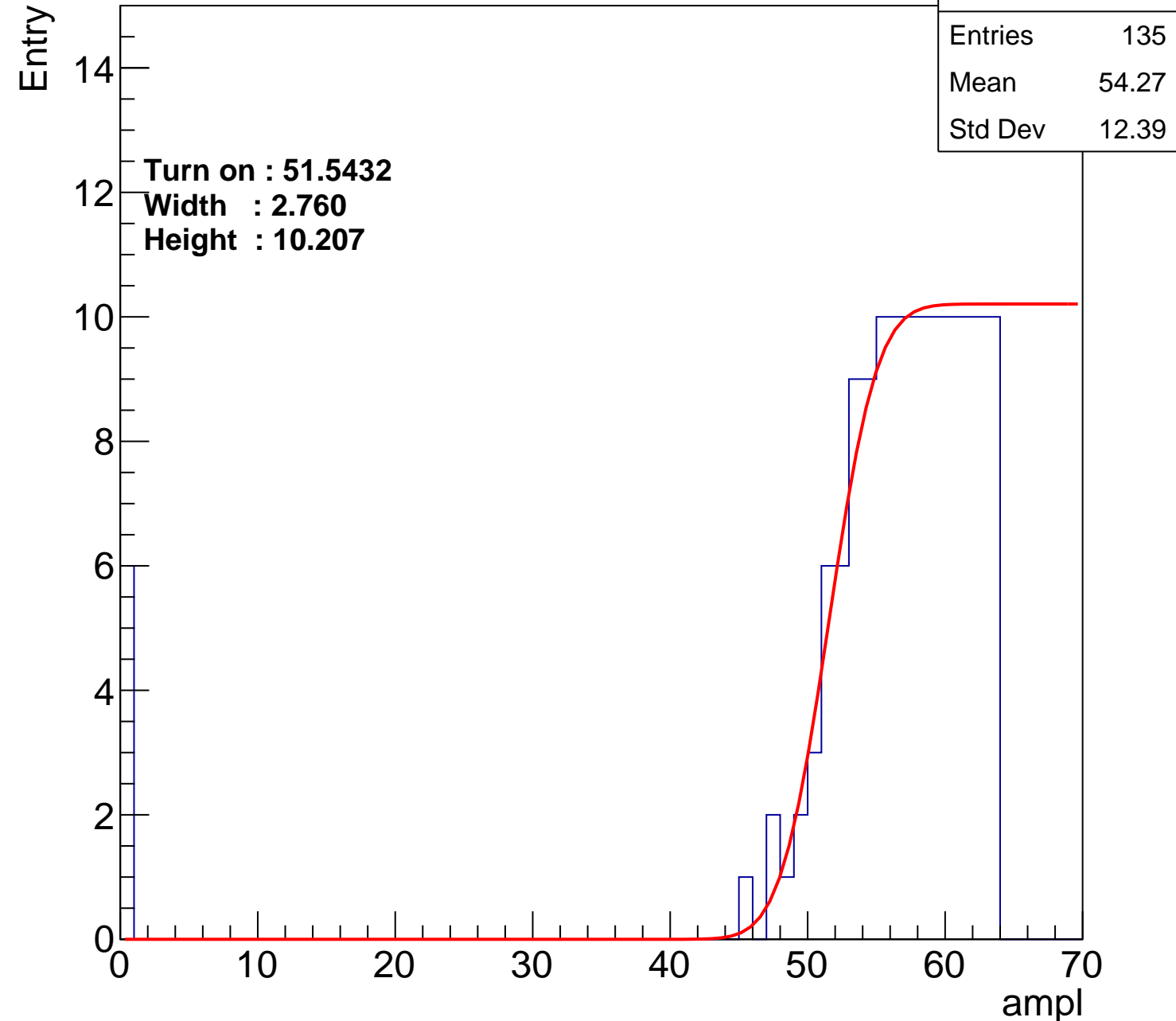
Width : 2.760

Height : 10.207

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch50

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.32
Std Dev	10.7

Turn on : 52.0742

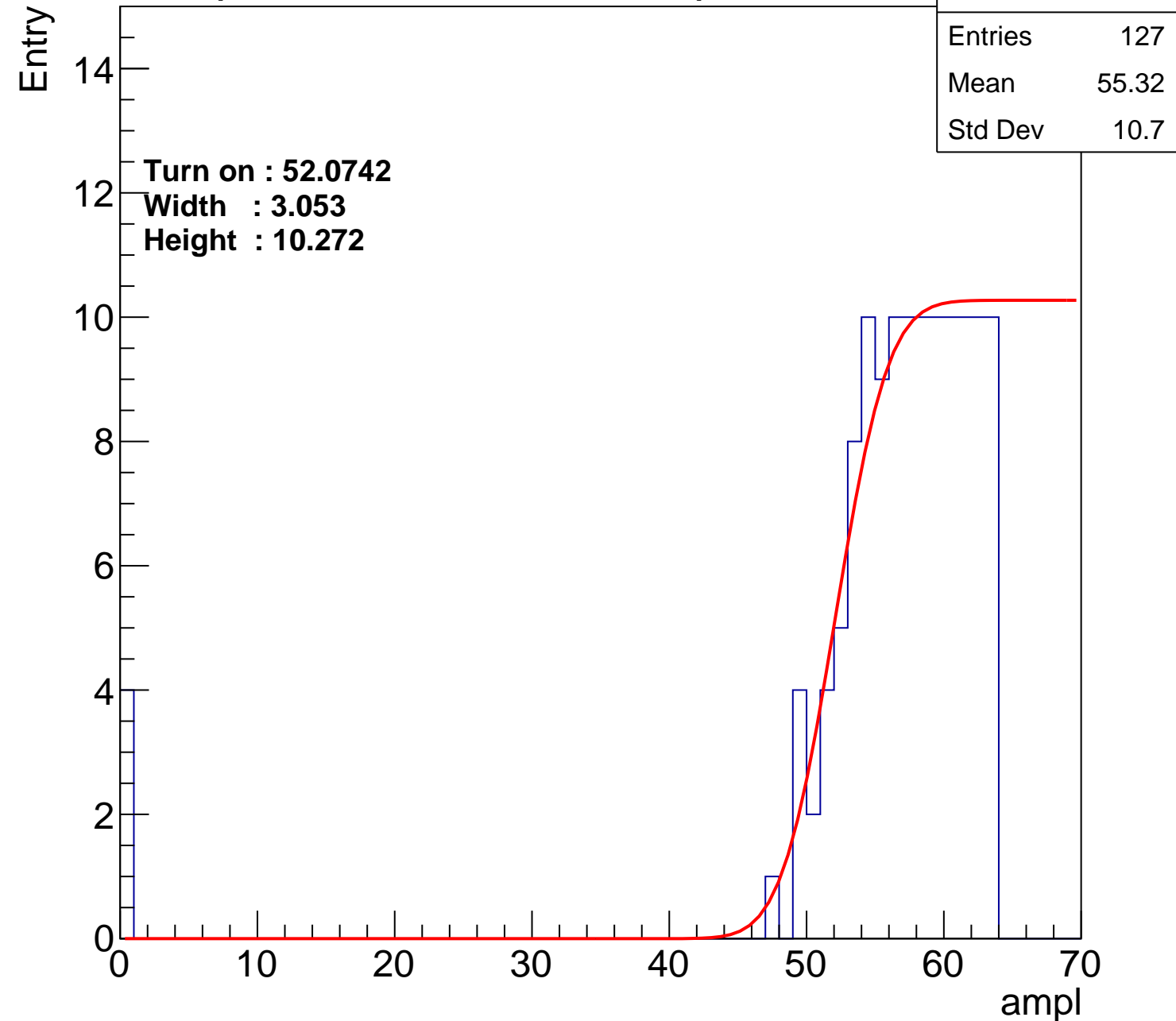
Width : 3.053

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch51

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.62
Std Dev	6.567

Turn on : 49.8253

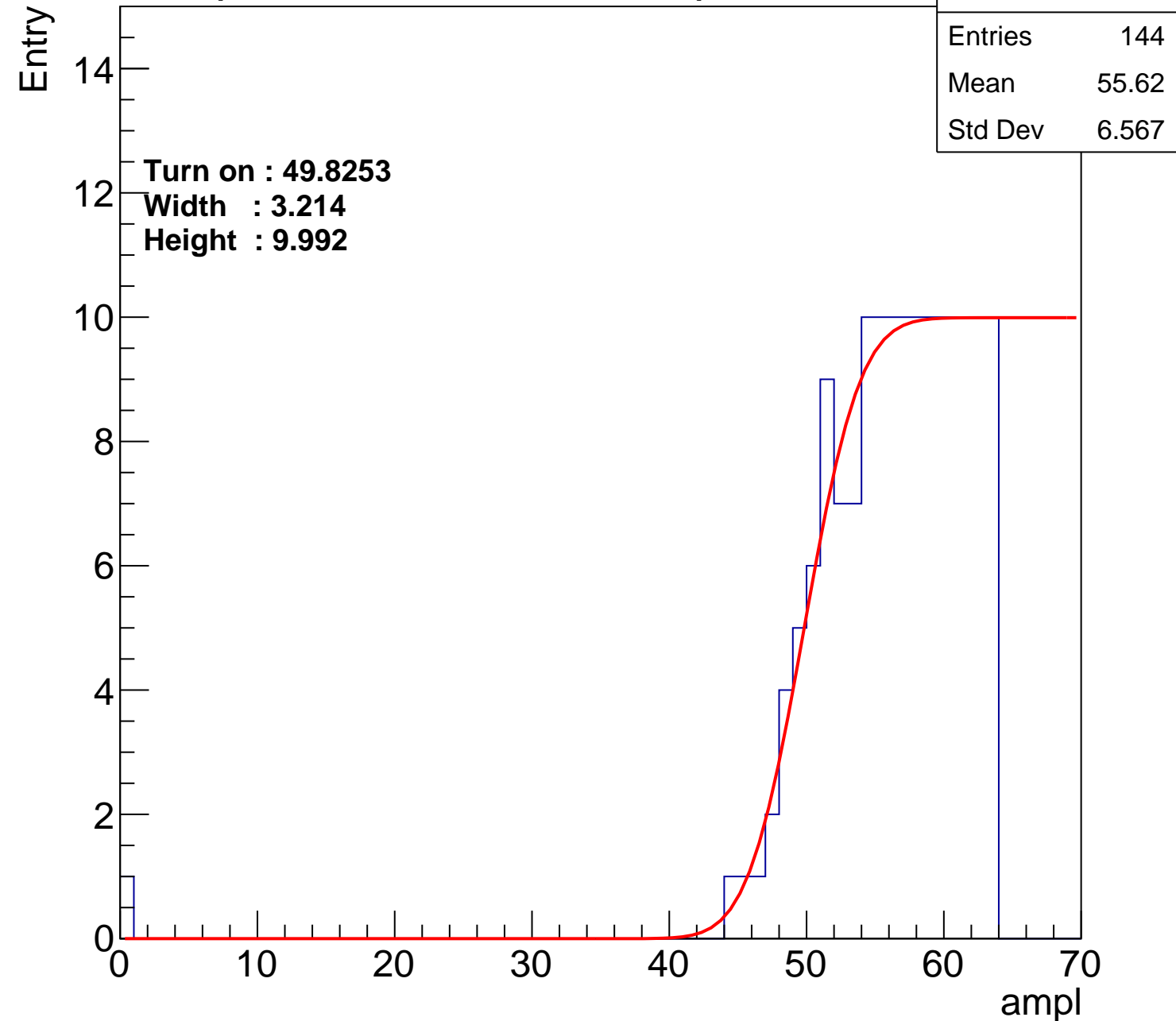
Width : 3.214

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch52

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.64
Std Dev	10.07

Turn on : 49.1486

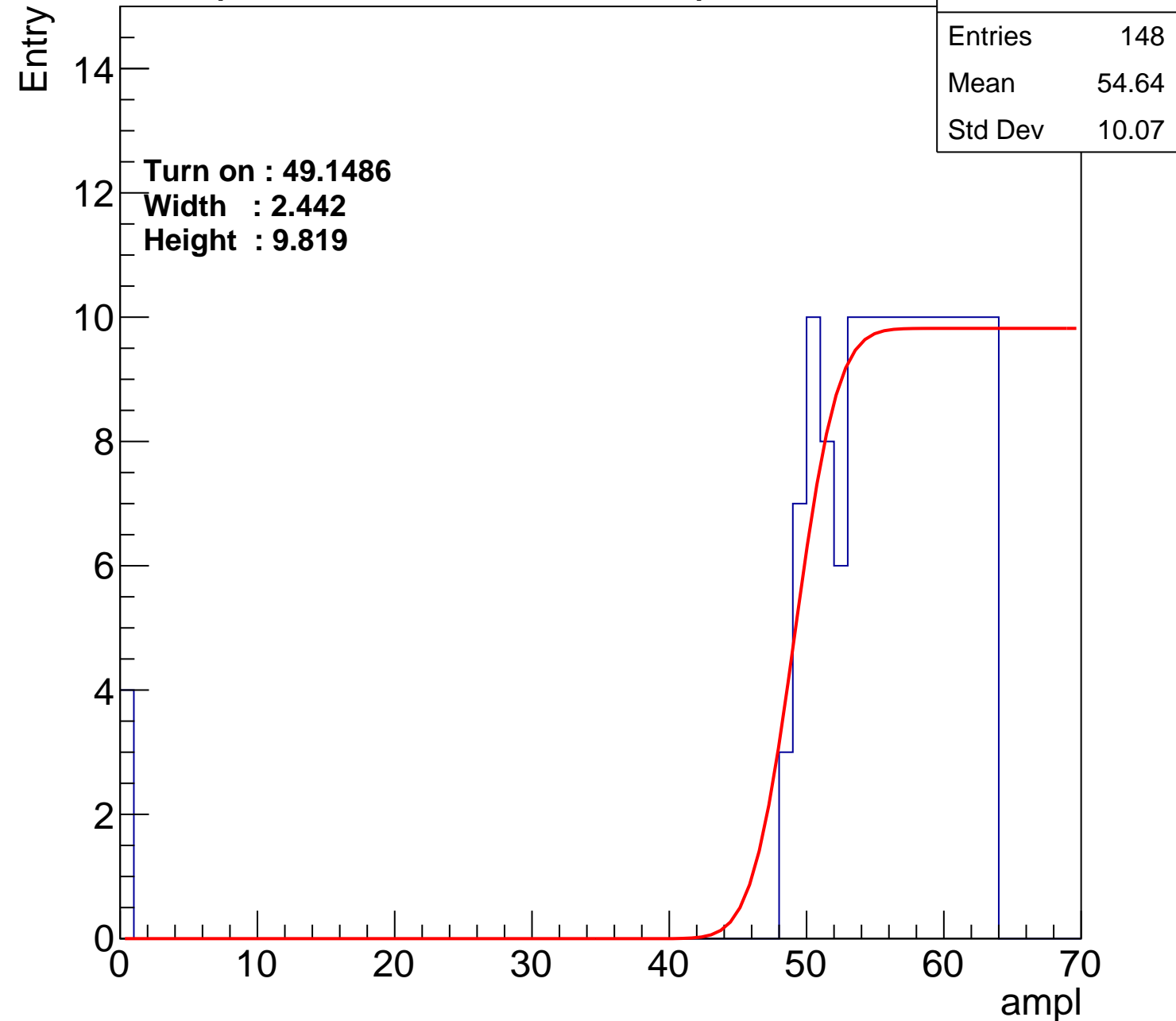
Width : 2.442

Height : 9.819

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch53

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	54.97
Std Dev	9.127

Turn on : 49.9013

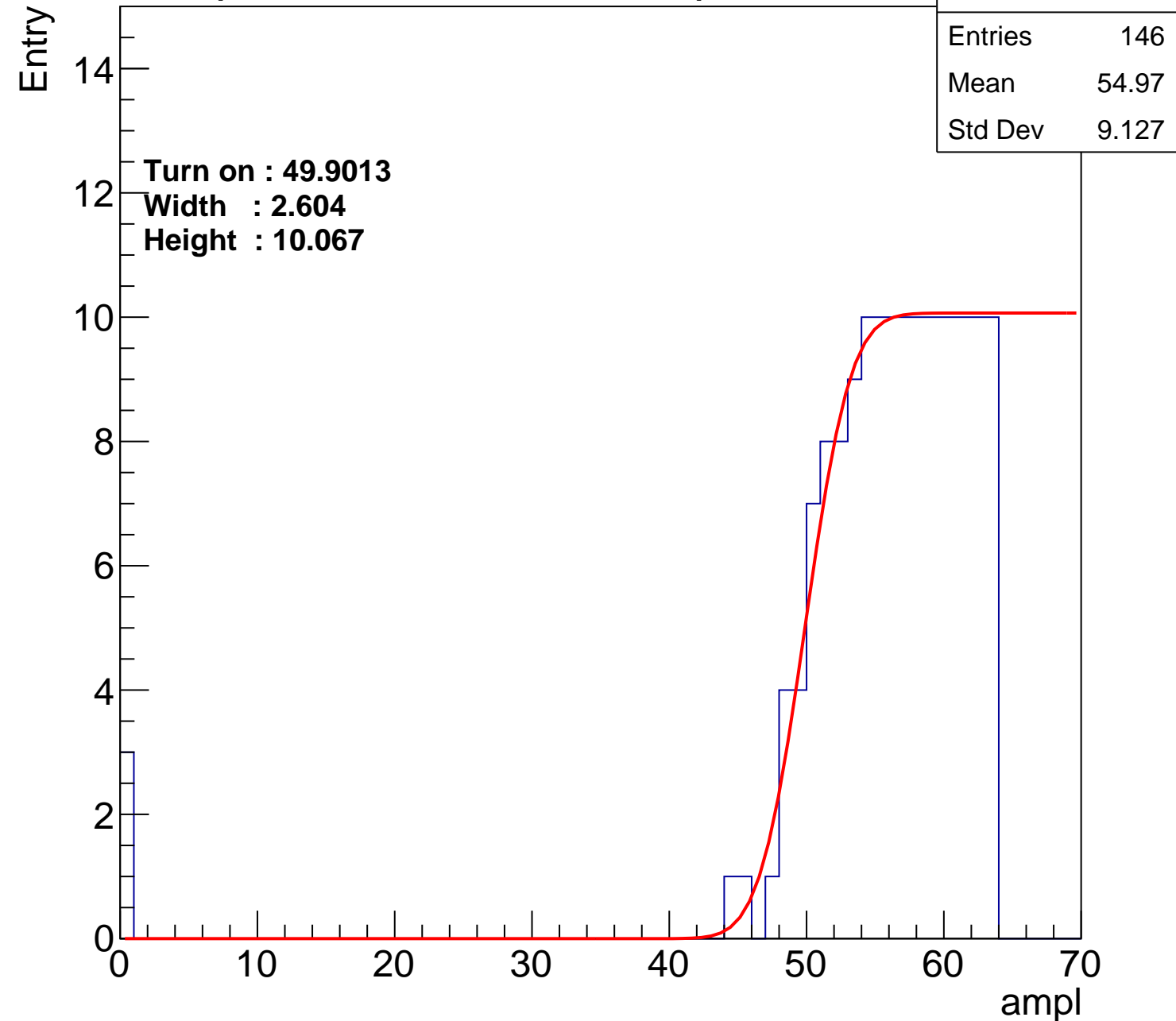
Width : 2.604

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch54

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	54.07
Std Dev	11.01

Turn on : 49.6864

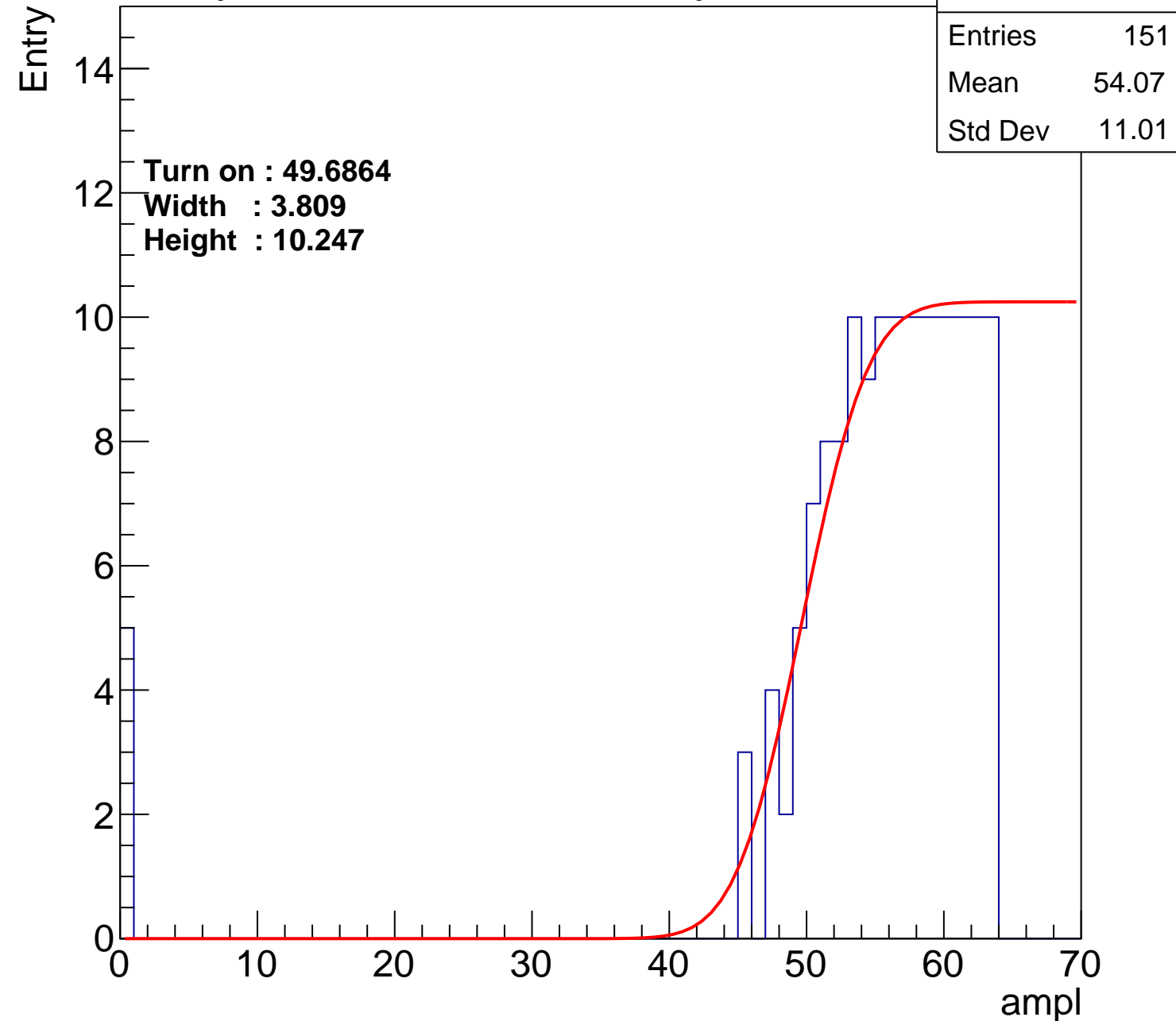
Width : 3.809

Height : 10.247

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch55

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.47
Std Dev	9.484

Turn on : 51.8434

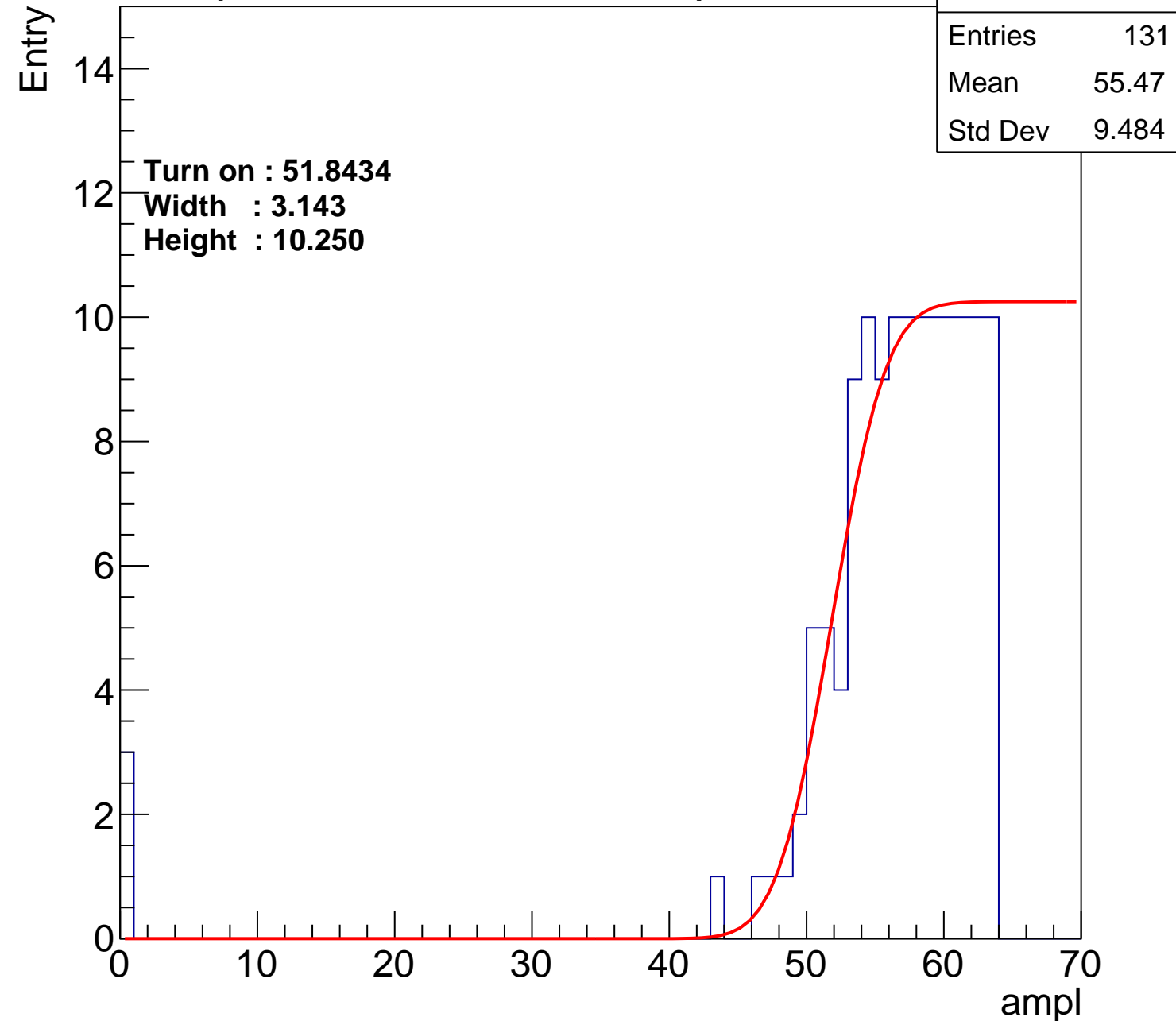
Width : 3.143

Height : 10.250

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch56

calib_packv5_040323_1717.root, FC#2, port C3

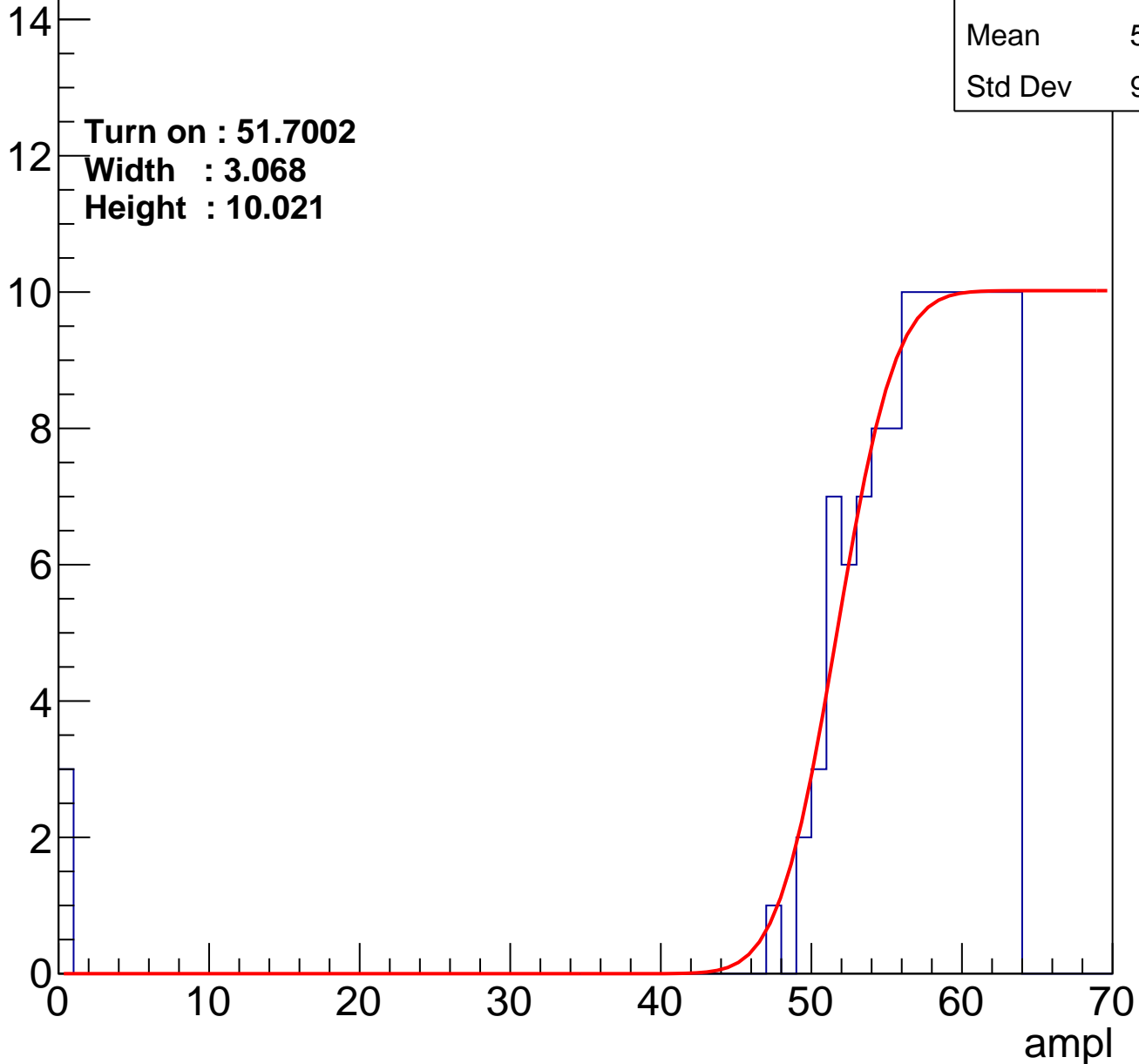
Entries	125
Mean	55.74
Std Dev	9.575

Turn on : 51.7002

Width : 3.068

Height : 10.021

Entry



B0L103S, U14-ch57

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	53.47
Std Dev	12.73

Turn on : 50.1124

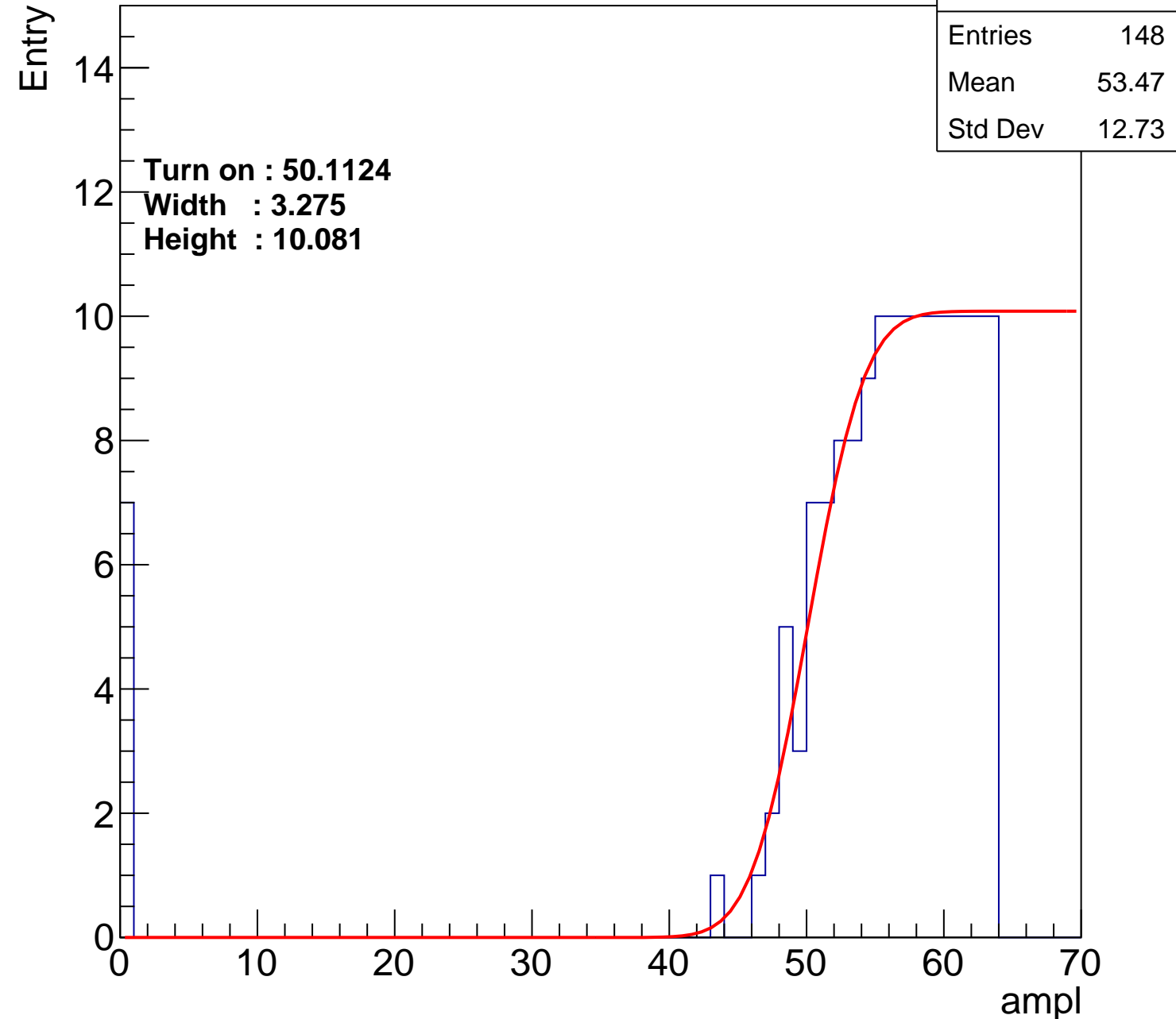
Width : 3.275

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_040323_1717.root, FC#2, port C3

Entries	163
Mean	54.56
Std Dev	7.836

Mean	54.56
------	-------

Std Dev	7.836
---------	-------

Width : 2.292

Height : 9.883



B0L103S, U14-ch59

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	55.37
Std Dev	7.914

Turn on : 49.8916

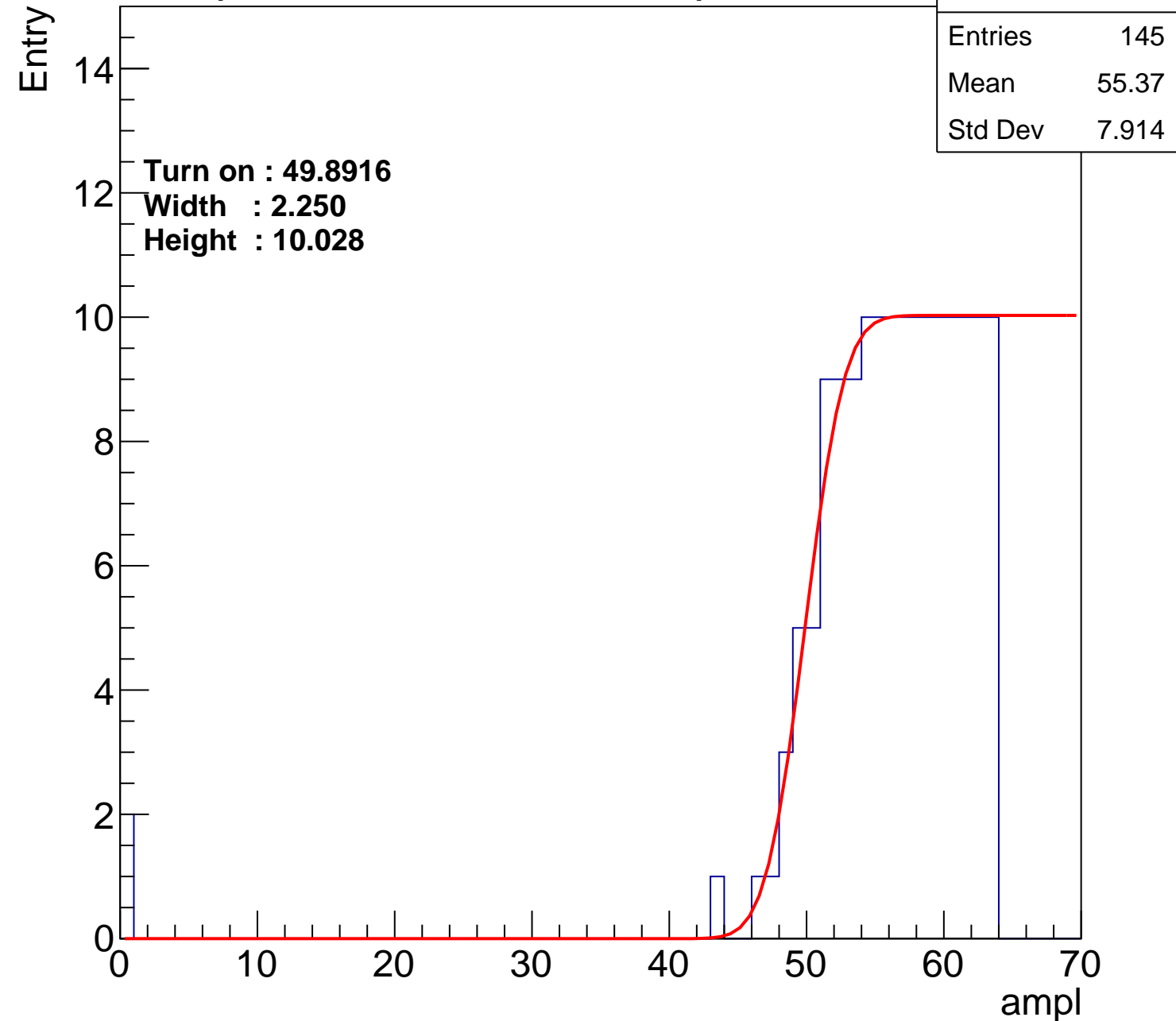
Width : 2.250

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch60

calib_packv5_040323_1717.root, FC#2, port C3

Entries	171
Mean	53.68
Std Dev	9.724

Turn on : 47.6643

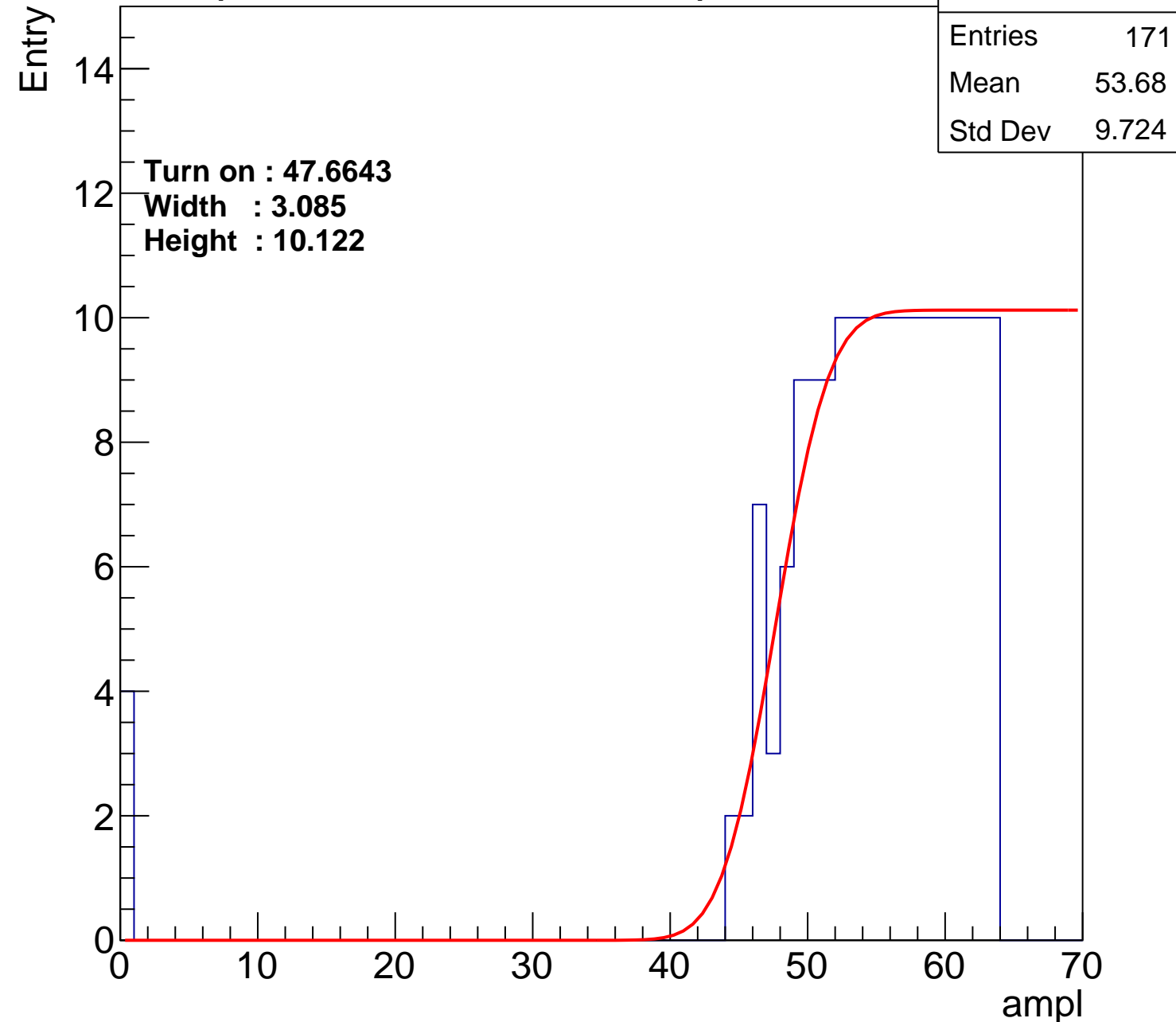
Width : 3.085

Height : 10.122

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch61

calib_packv5_040323_1717.root, FC#2, port C3

Entries	155
Mean	54.26
Std Dev	10.01

Turn on : 49.1532

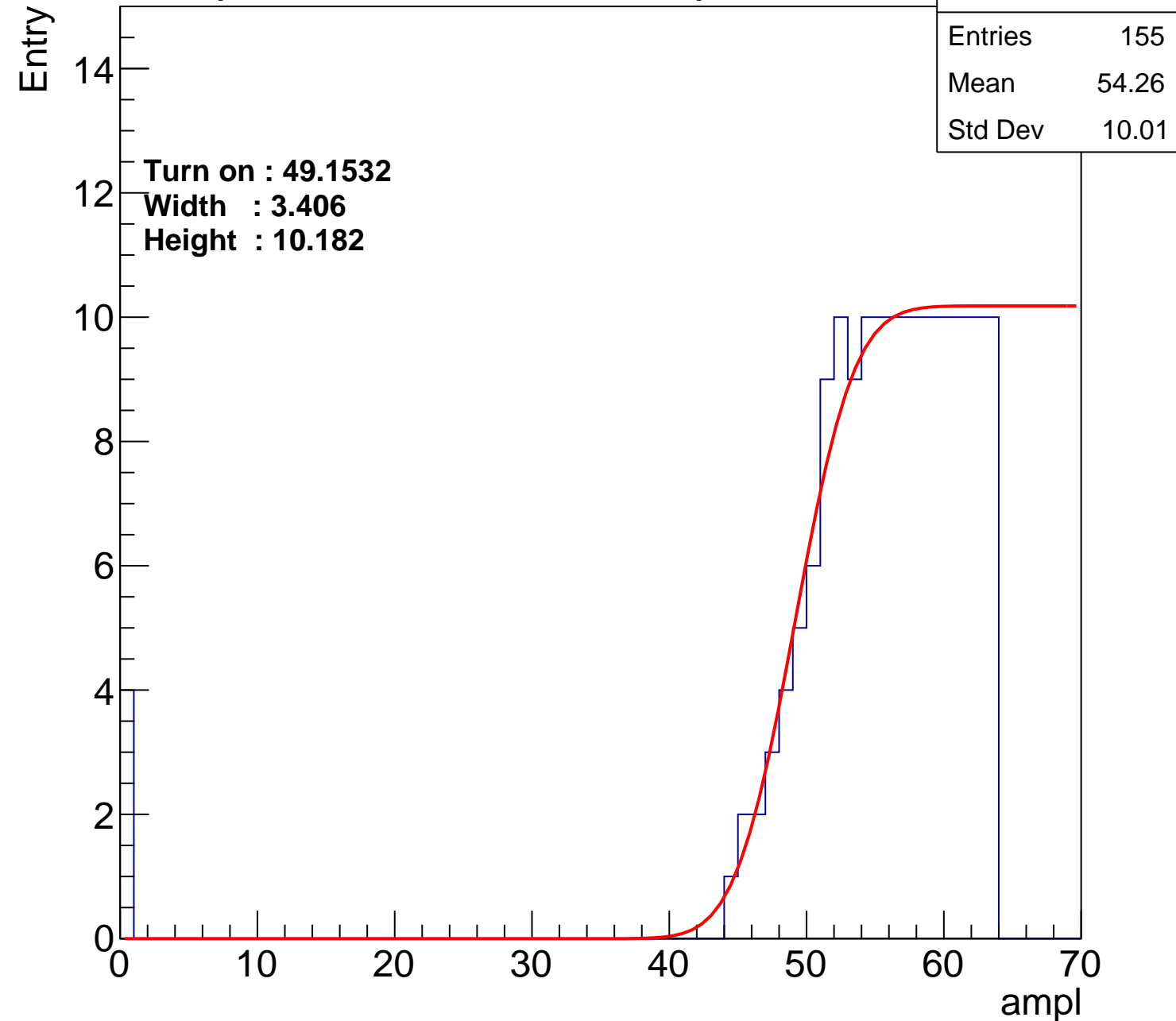
Width : 3.406

Height : 10.182

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch62

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.75
Std Dev	11.57

Turn on : 51.5319

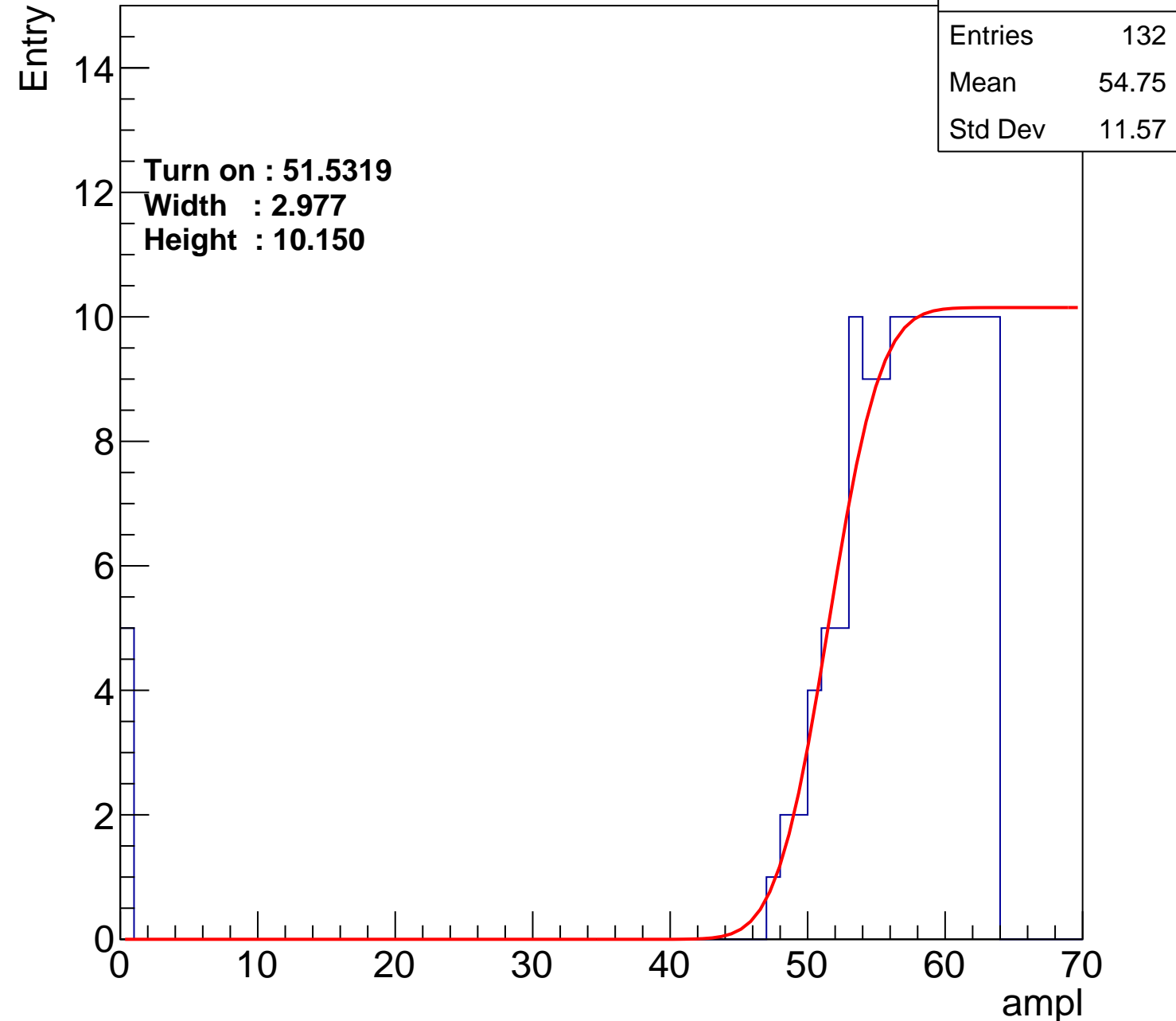
Width : 2.977

Height : 10.150

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch63

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	55.63
Std Dev	9.473

Turn on : 51.8607

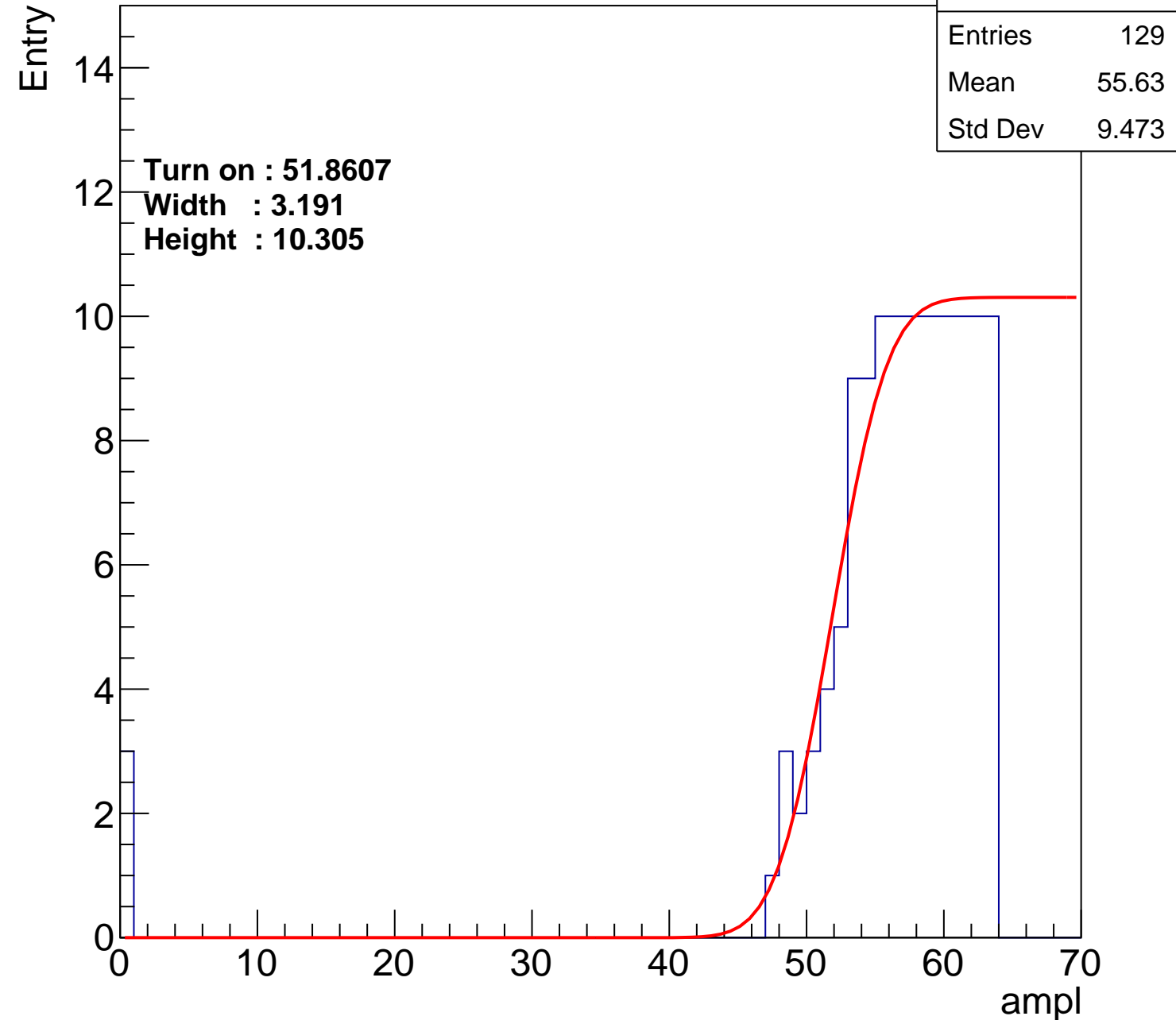
Width : 3.191

Height : 10.305

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch64

calib_packv5_040323_1717.root, FC#2, port C3

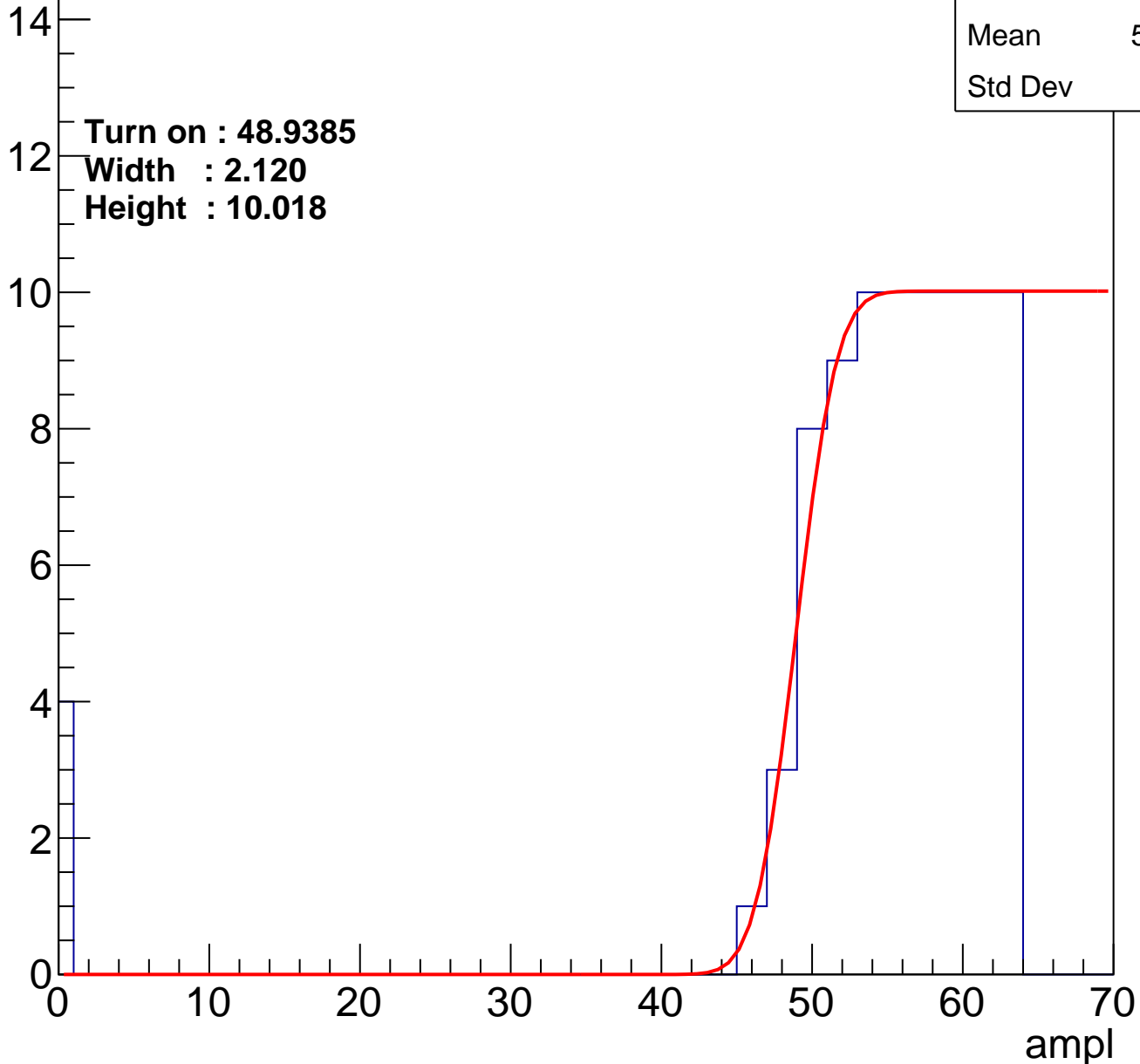
Entries	156
Mean	54.33
Std Dev	9.92

Turn on : 48.9385

Width : 2.120

Height : 10.018

Entry



B0L103S, U14-ch65

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	56.12
Std Dev	8.235

Turn on : 51.9441

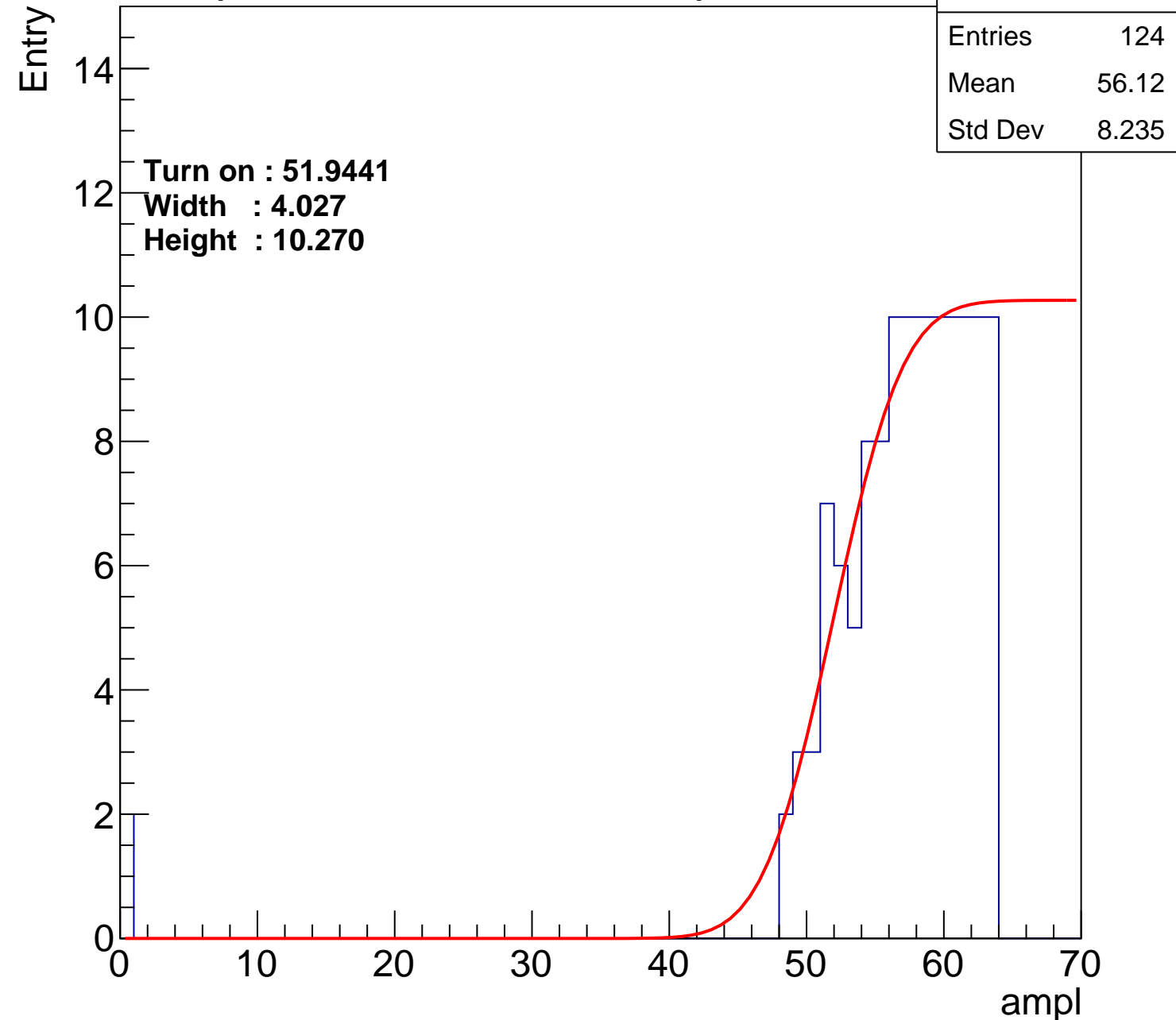
Width : 4.027

Height : 10.270

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch66

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	54.61
Std Dev	9.044

Turn on : 48.9669

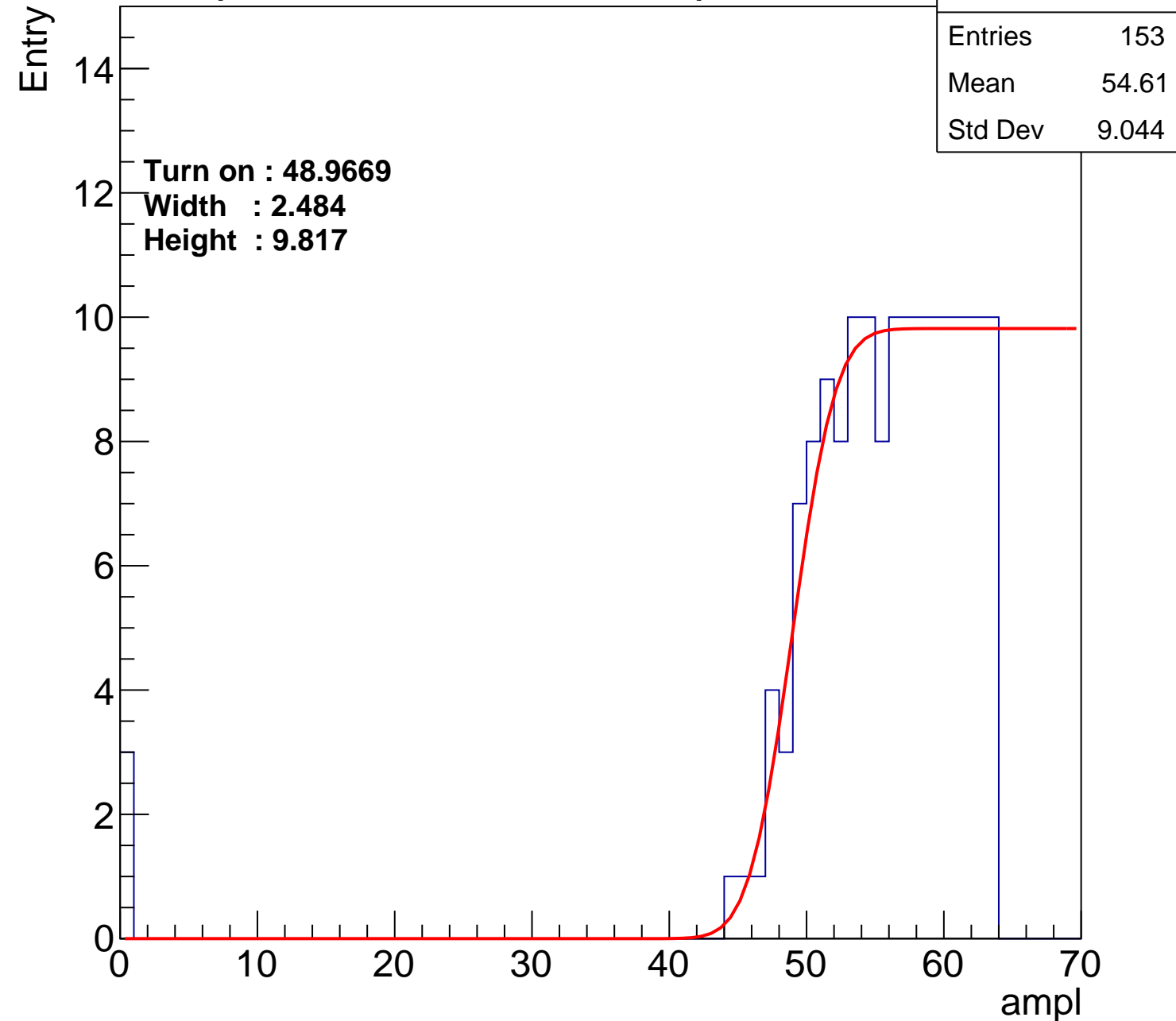
Width : 2.484

Height : 9.817

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch67

calib_packv5_040323_1717.root, FC#2, port C3

Entries	158
Mean	54.05
Std Dev	10.02

Turn on : 49.2899

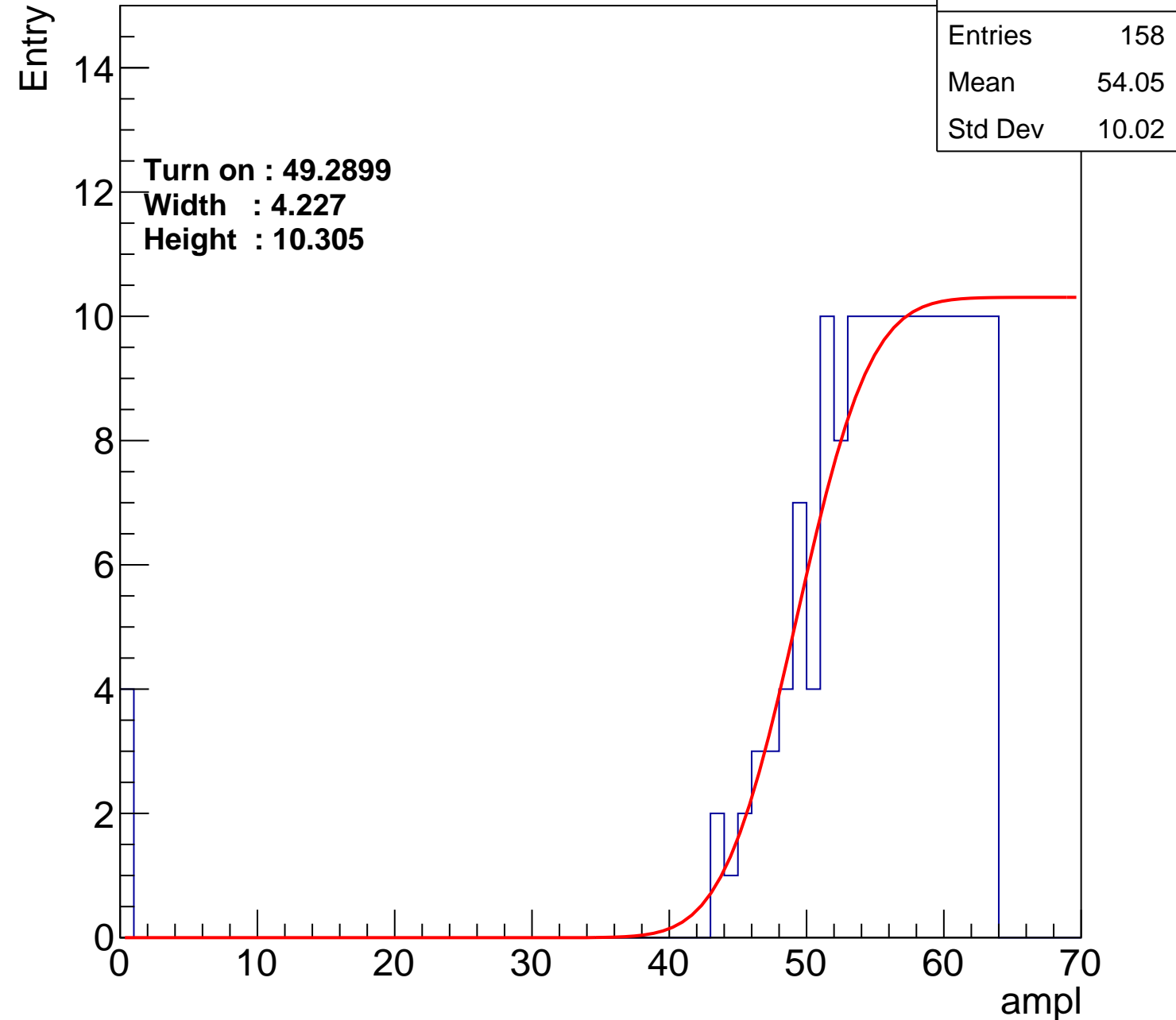
Width : 4.227

Height : 10.305

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch68

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.59
Std Dev	8.005

Turn on : 50.8015

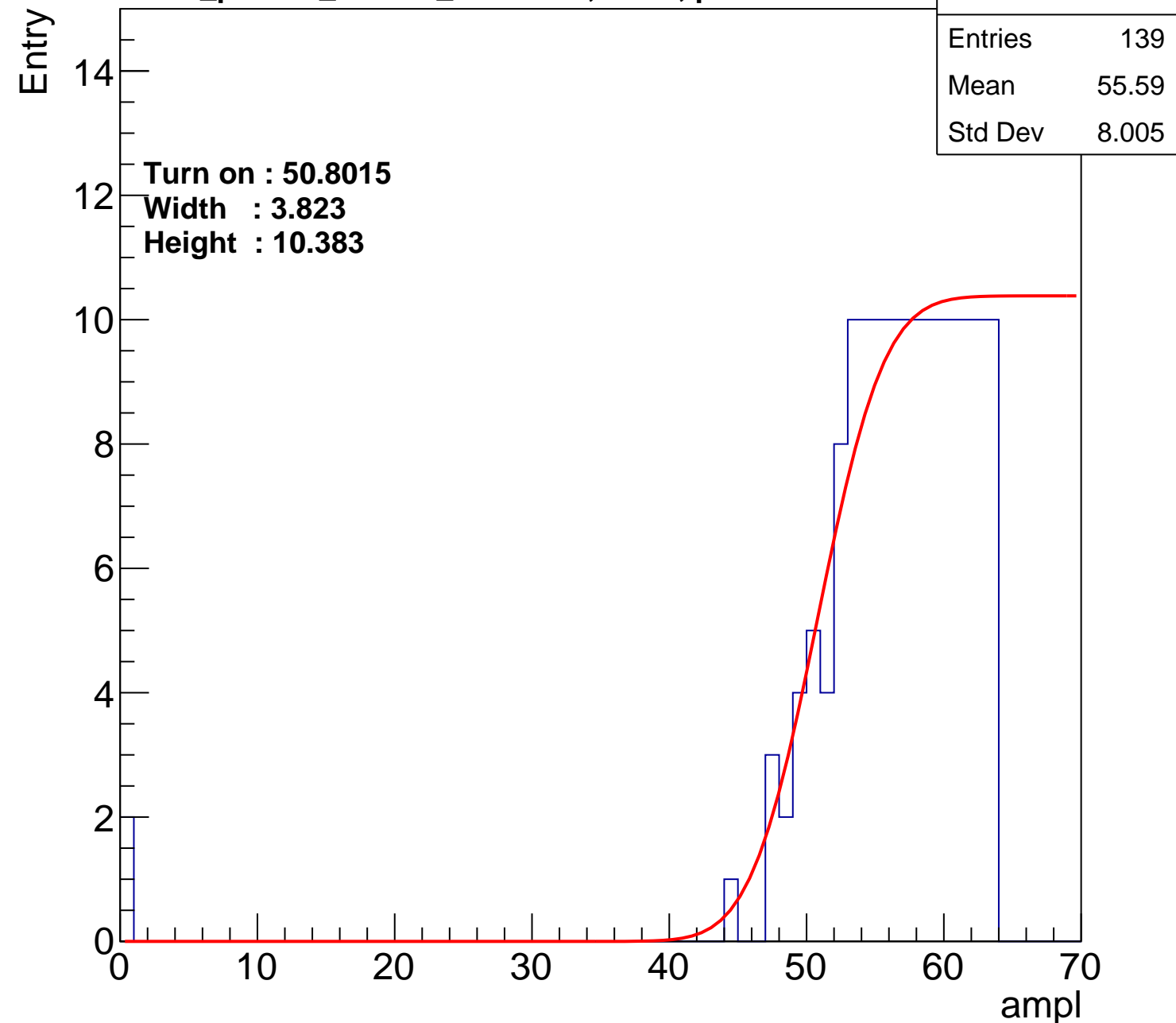
Width : 3.823

Height : 10.383

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch69

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.61
Std Dev	10.12

Turn on : 49.4325

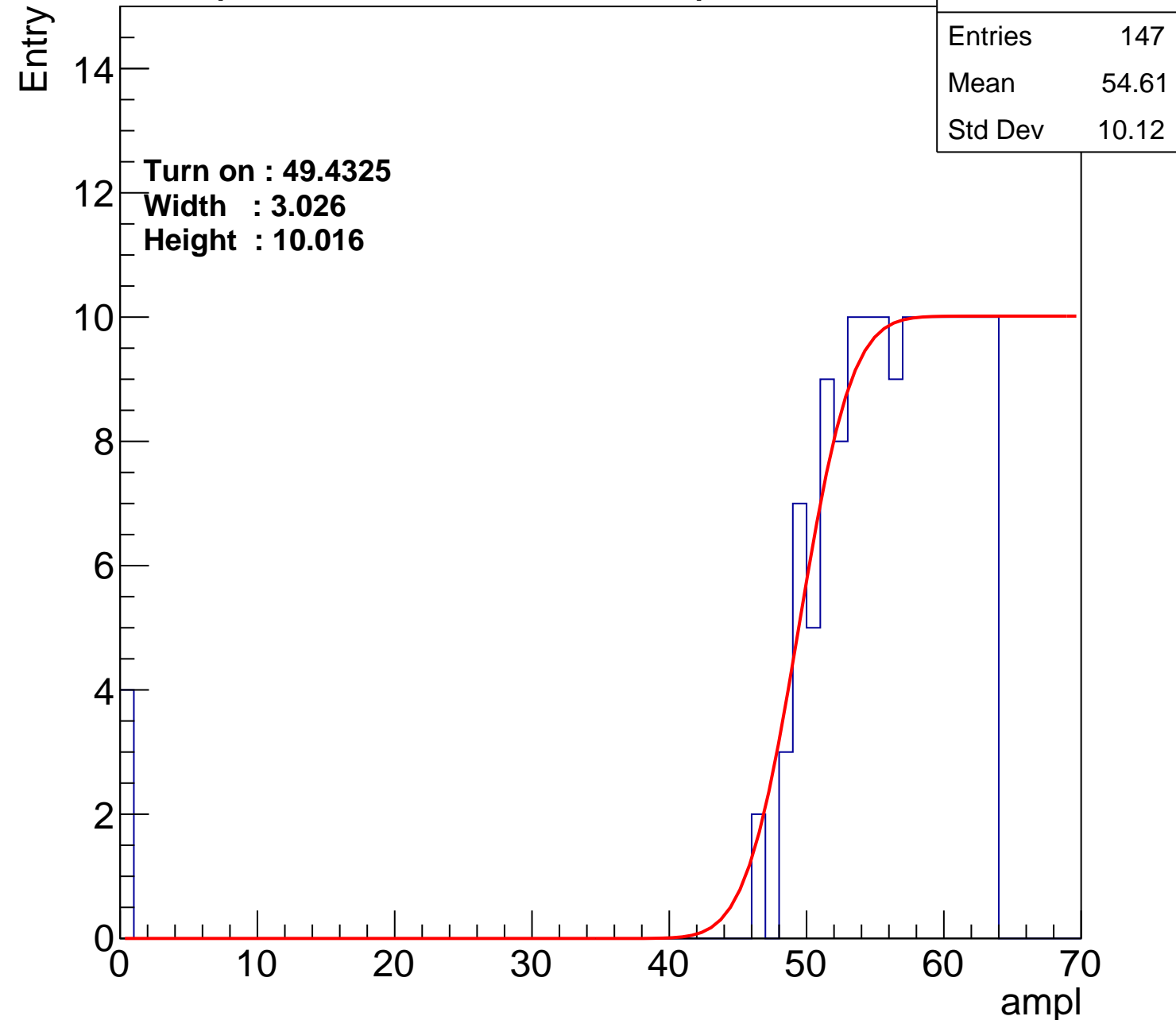
Width : 3.026

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch70

calib_packv5_040323_1717.root, FC#2, port C3

Turn on : 50.6780

Width : 2.448

Height : 10.083

Entries	139
Mean	55.61
Std Dev	7.973



B0L103S, U14-ch71

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	53.82
Std Dev	11.89

Turn on : 49.7114

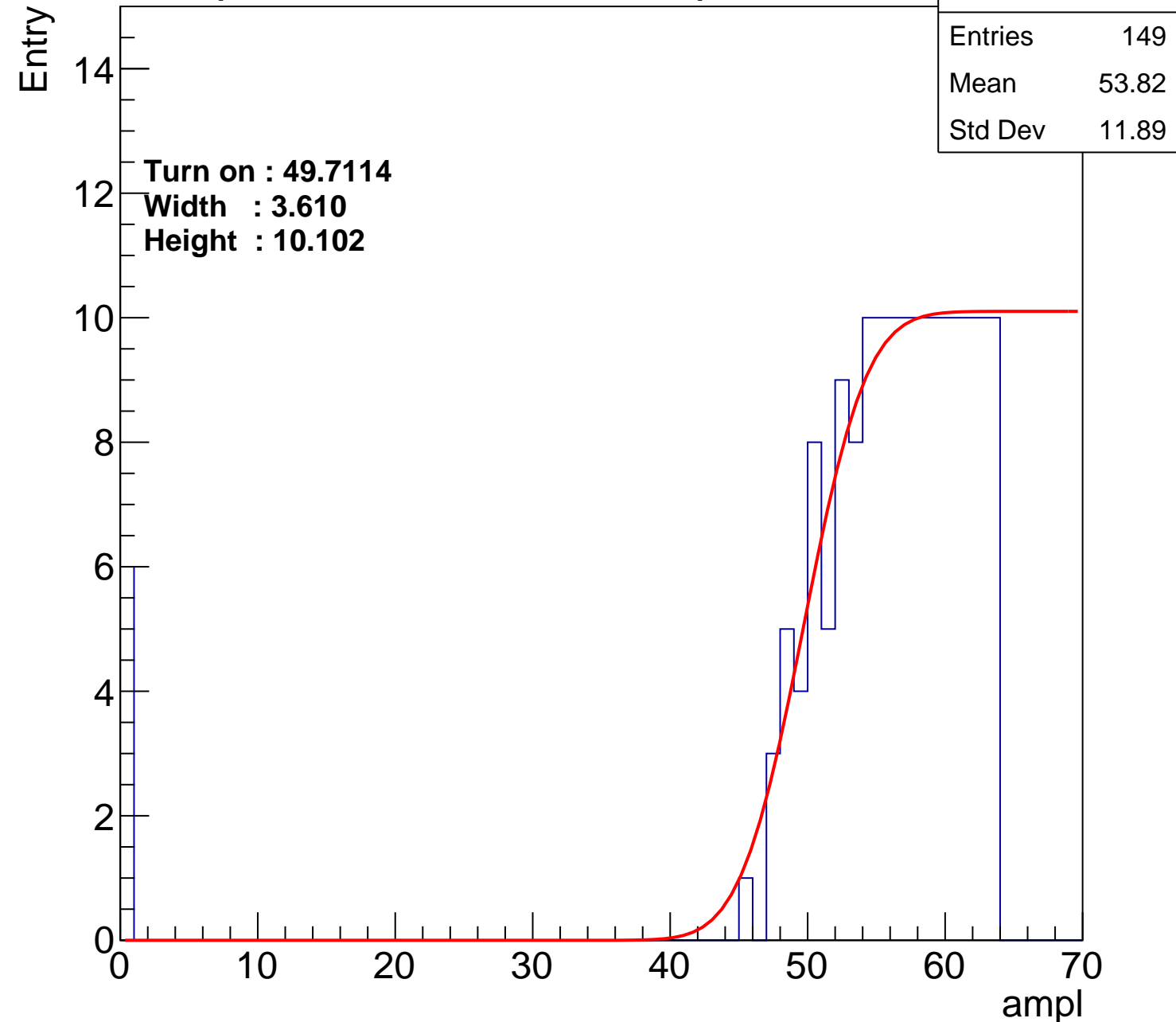
Width : 3.610

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch72

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	55.02
Std Dev	9.128

Turn on : 49.5235

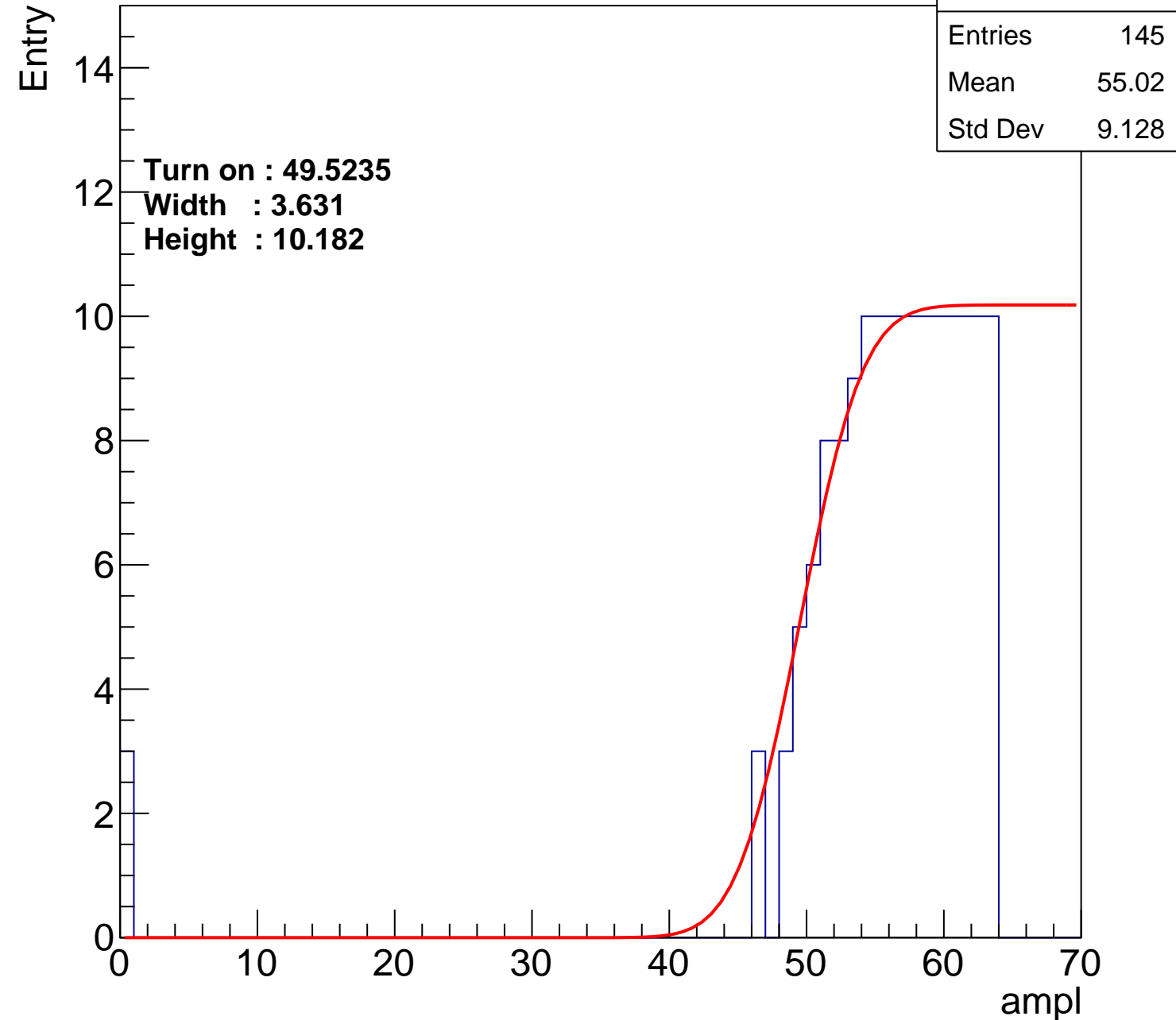
Width : 3.631

Height : 10.182

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch73

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.88
Std Dev	6.485

Turn on : 49.8498

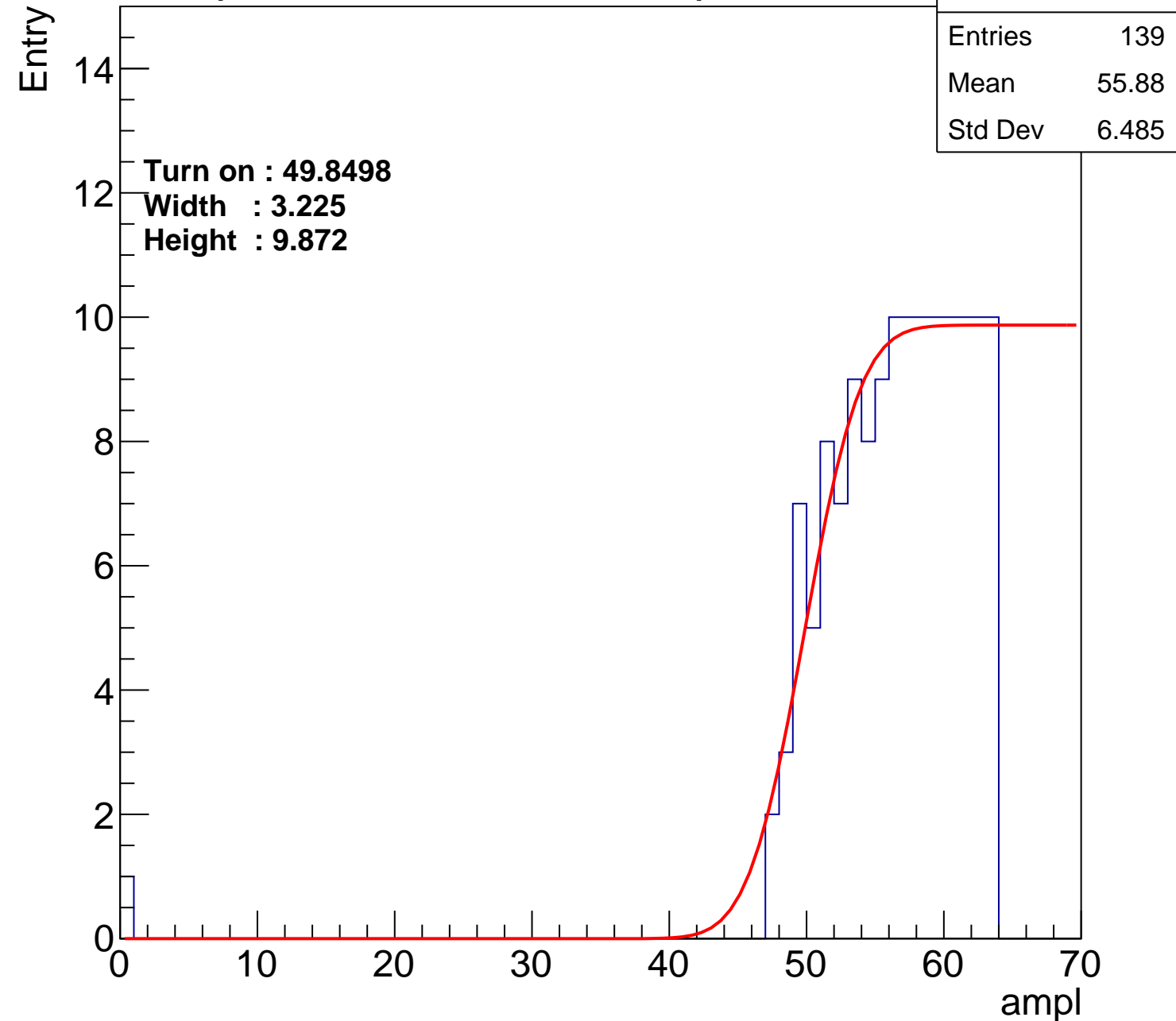
Width : 3.225

Height : 9.872

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch74

calib_packv5_040323_1717.root, FC#2, port C3

Entries	163
Mean	54.93
Std Dev	6.504

Turn on : 47.3260

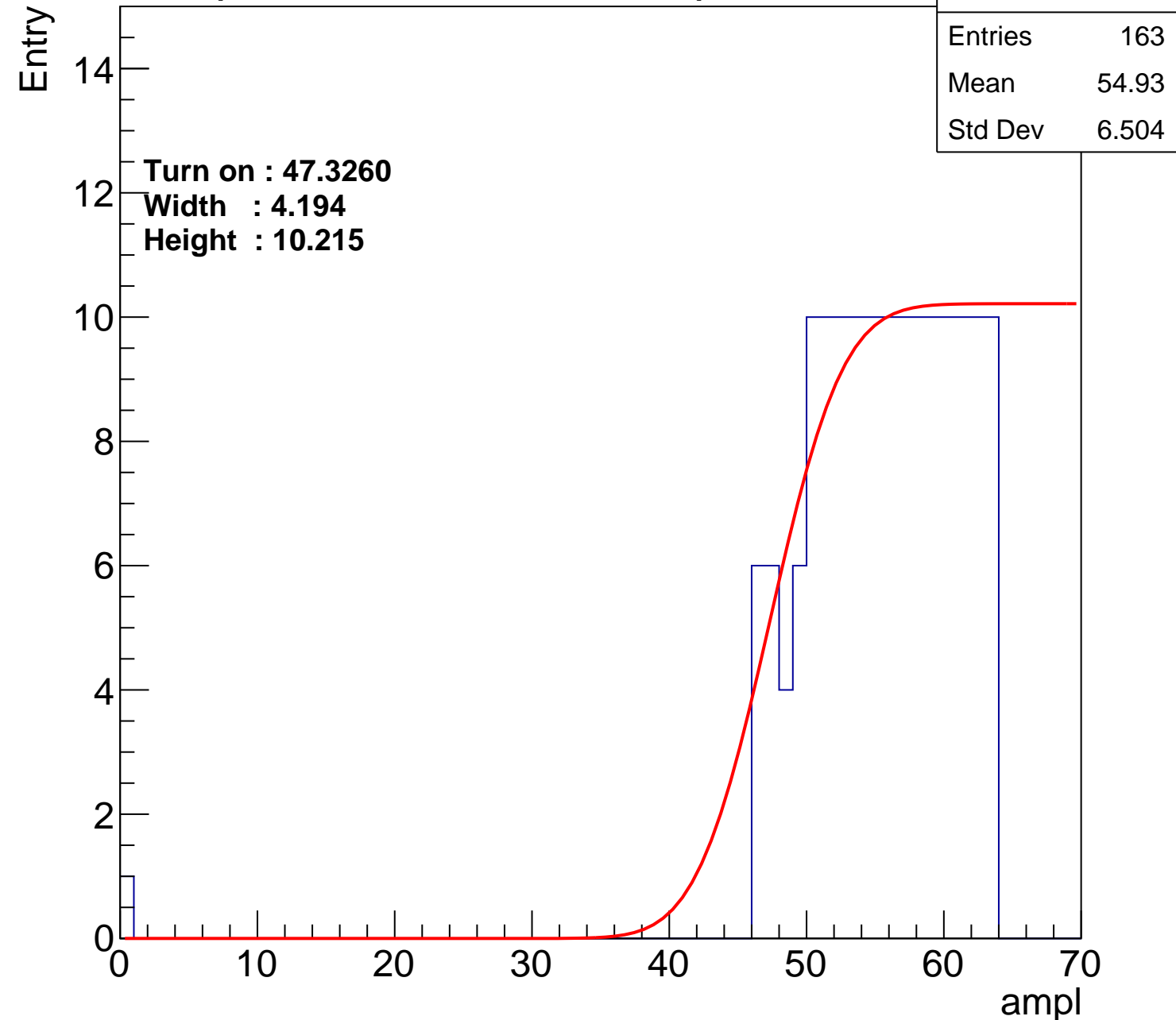
Width : 4.194

Height : 10.215

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch75

calib_packv5_040323_1717.root, FC#2, port C3

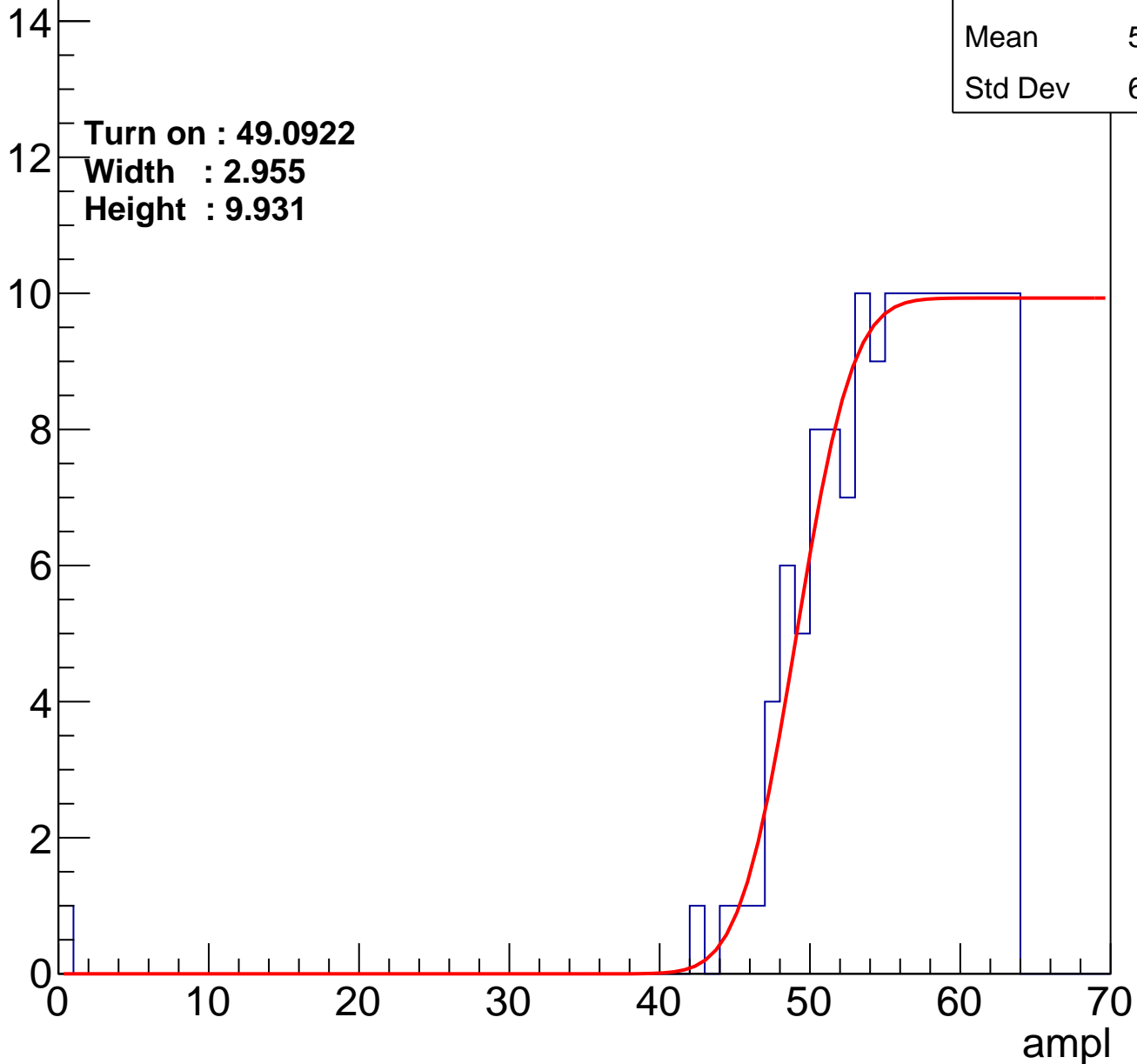
Entries	152
Mean	55.24
Std Dev	6.638

Turn on : 49.0922

Width : 2.955

Height : 9.931

Entry



B0L103S, U14-ch76

calib_packv5_040323_1717.root, FC#2, port C3

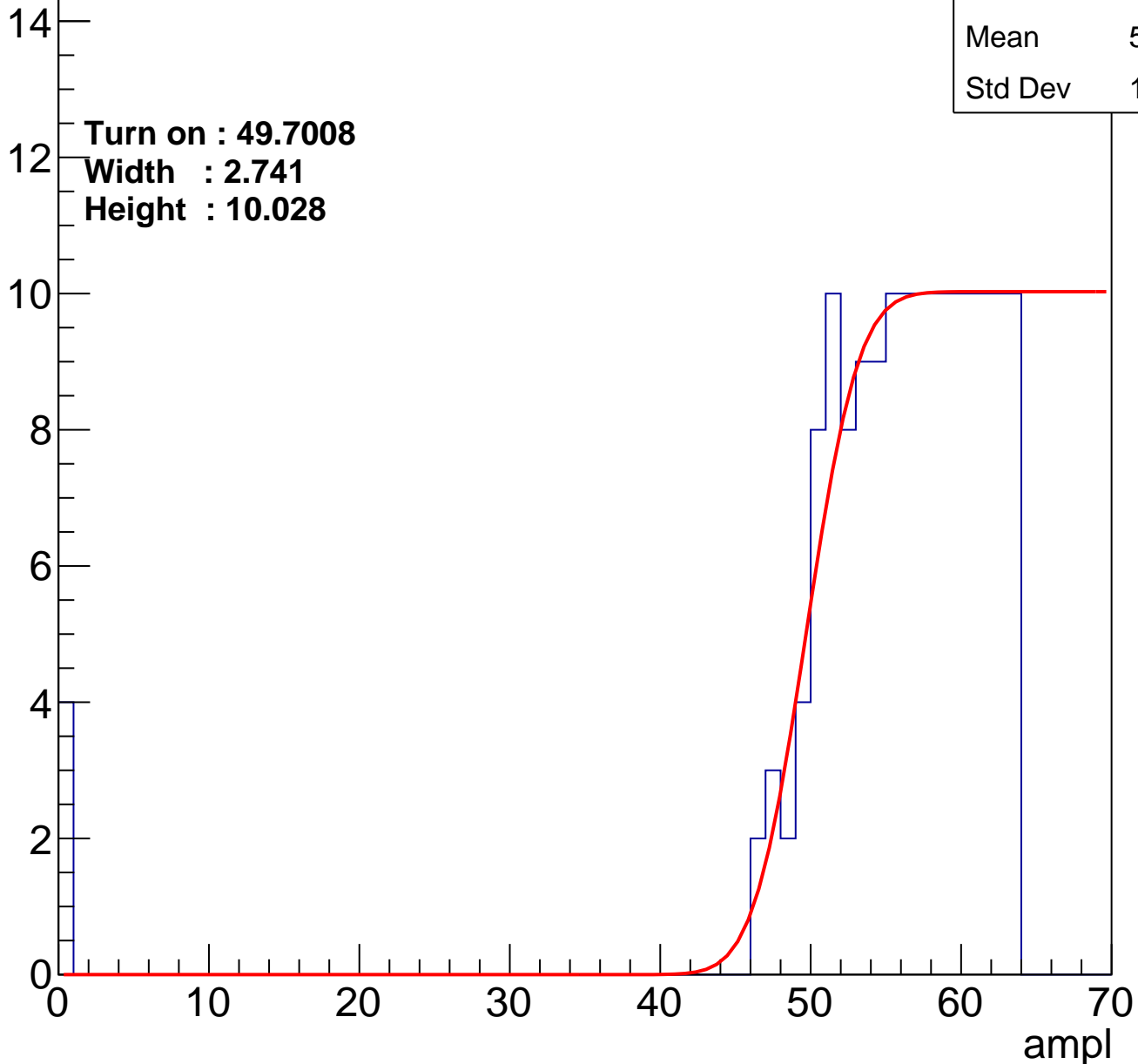
Entries	149
Mean	54.52
Std Dev	10.09

Turn on : 49.7008

Width : 2.741

Height : 10.028

Entry



B0L103S, U14-ch77

calib_packv5_040323_1717.root, FC#2, port C3

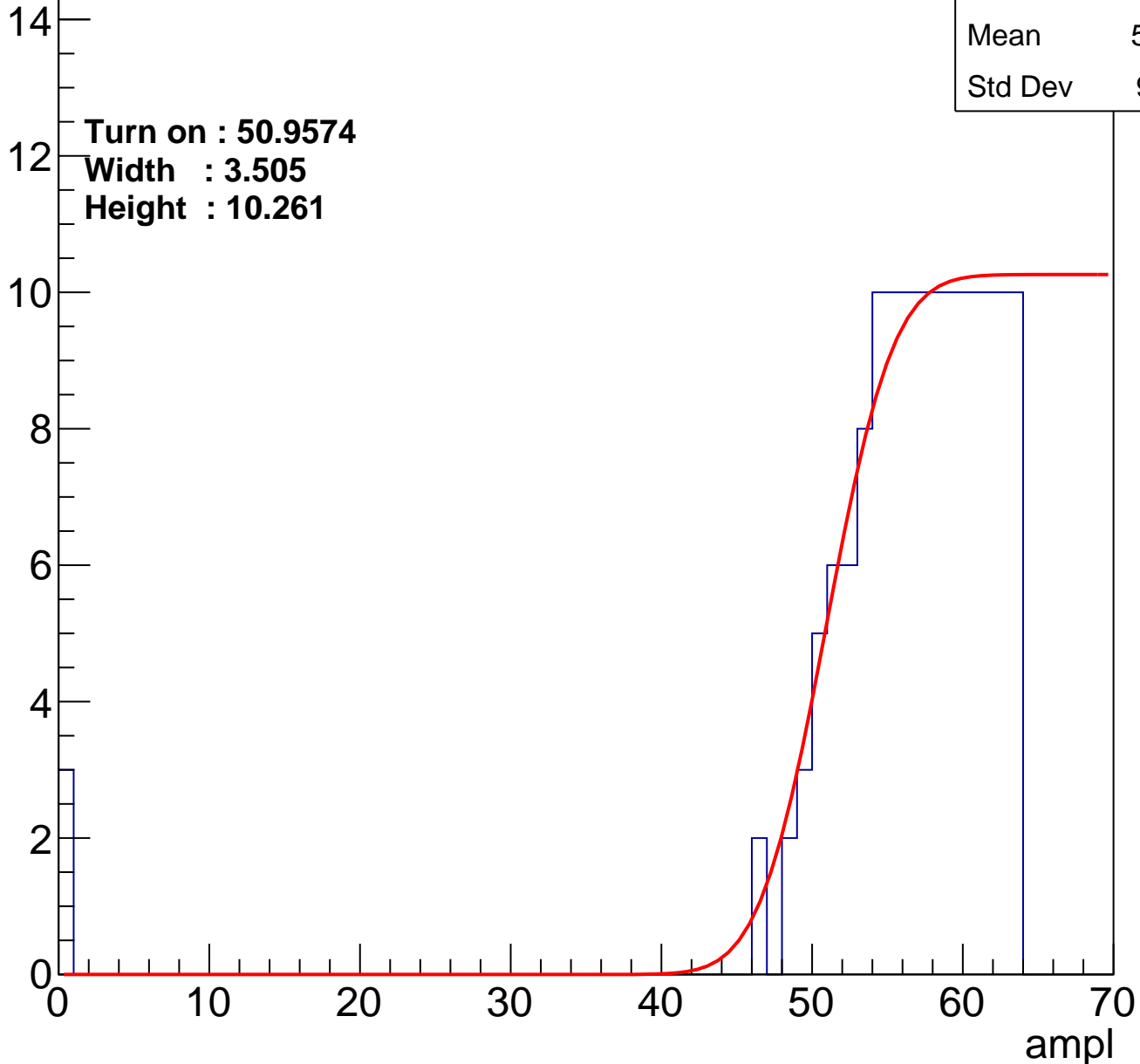
Entries	135
Mean	55.39
Std Dev	9.341

Turn on : 50.9574

Width : 3.505

Height : 10.261

Entry



B0L103S, U14-ch78

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.12
Std Dev	10.74

Turn on : 52.2615

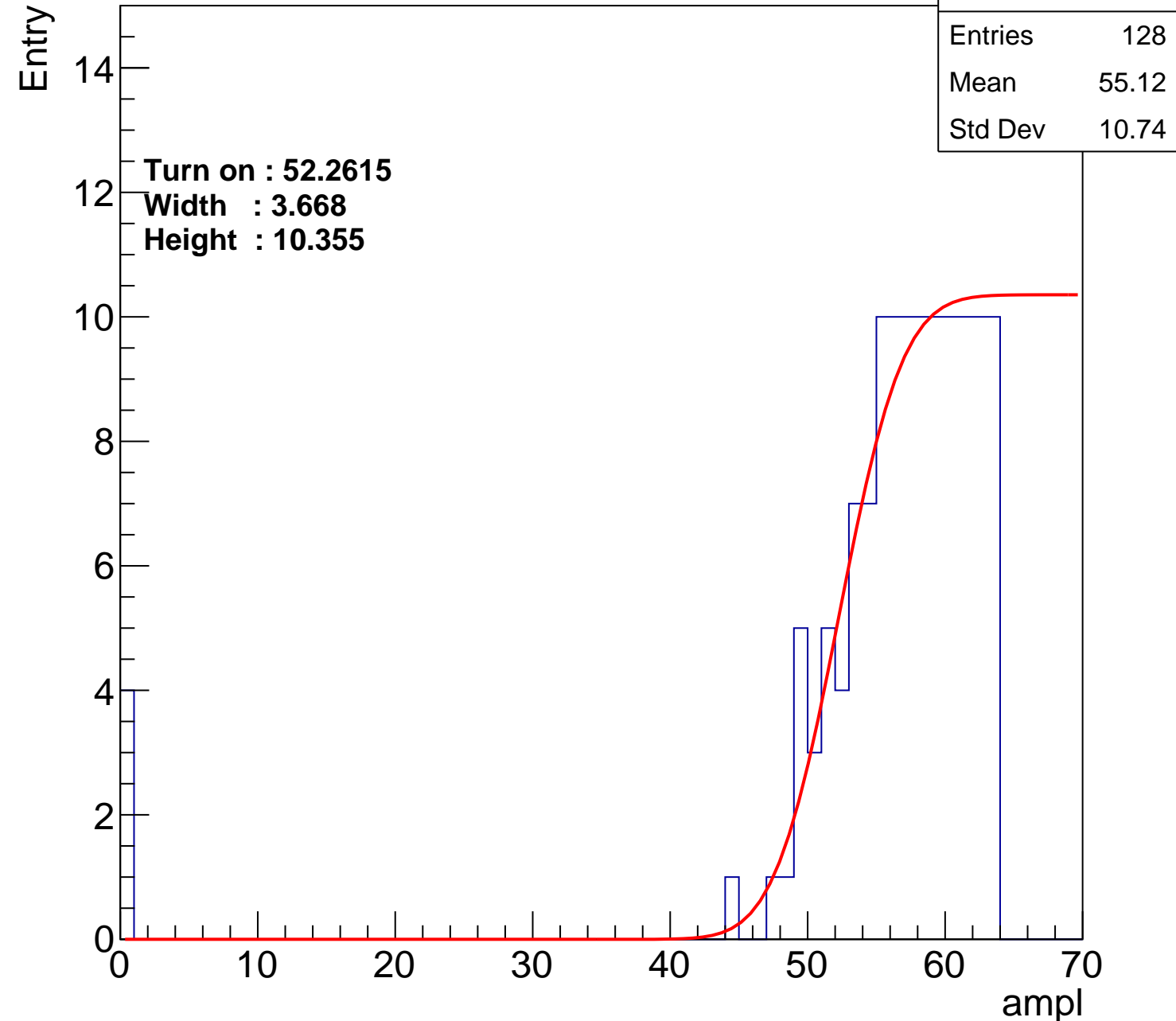
Width : 3.668

Height : 10.355

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch79

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.68
Std Dev	8.074

Turn on : 51.0629

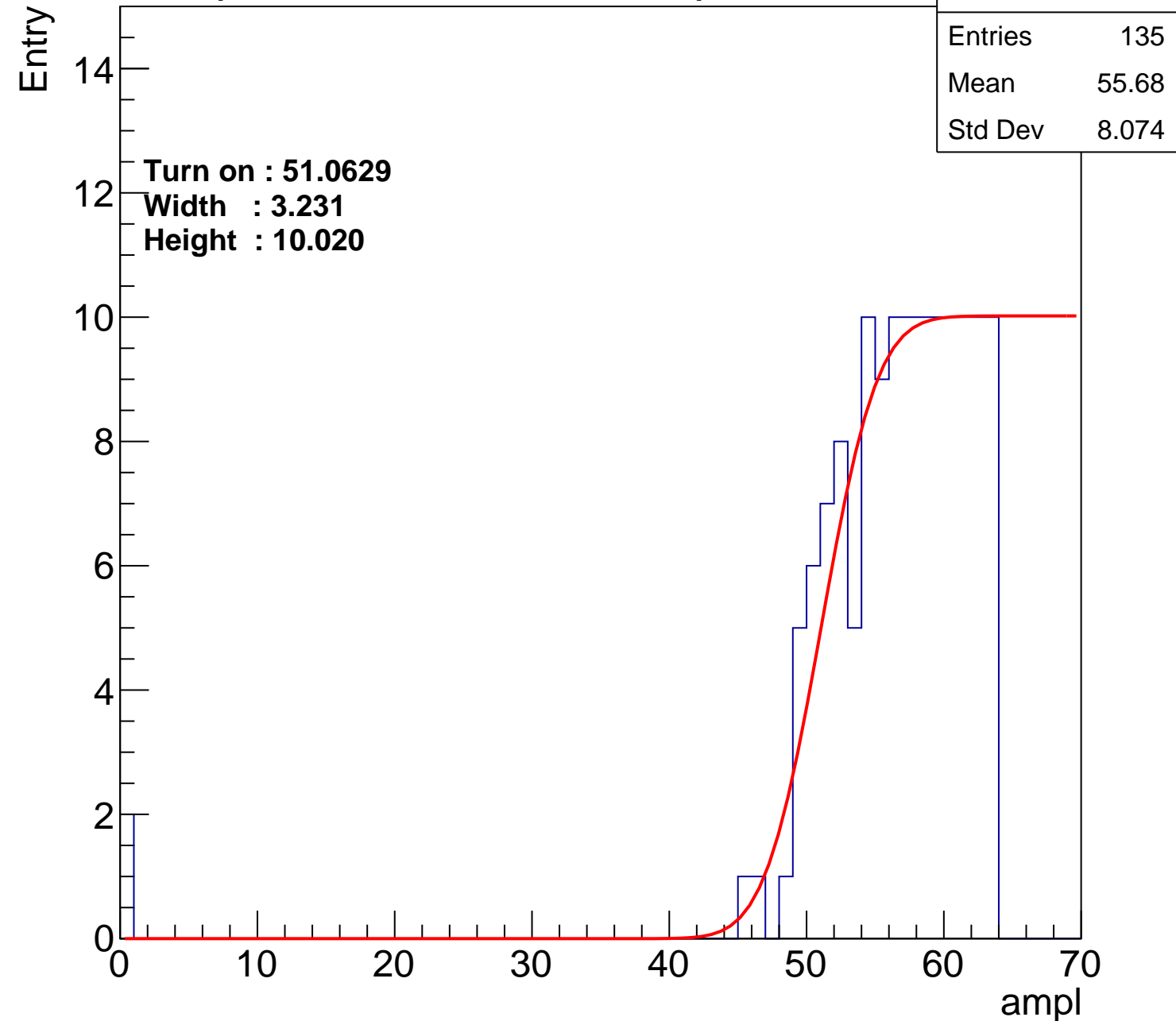
Width : 3.231

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch80

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	55.09
Std Dev	7.971

Turn on : 49.3871

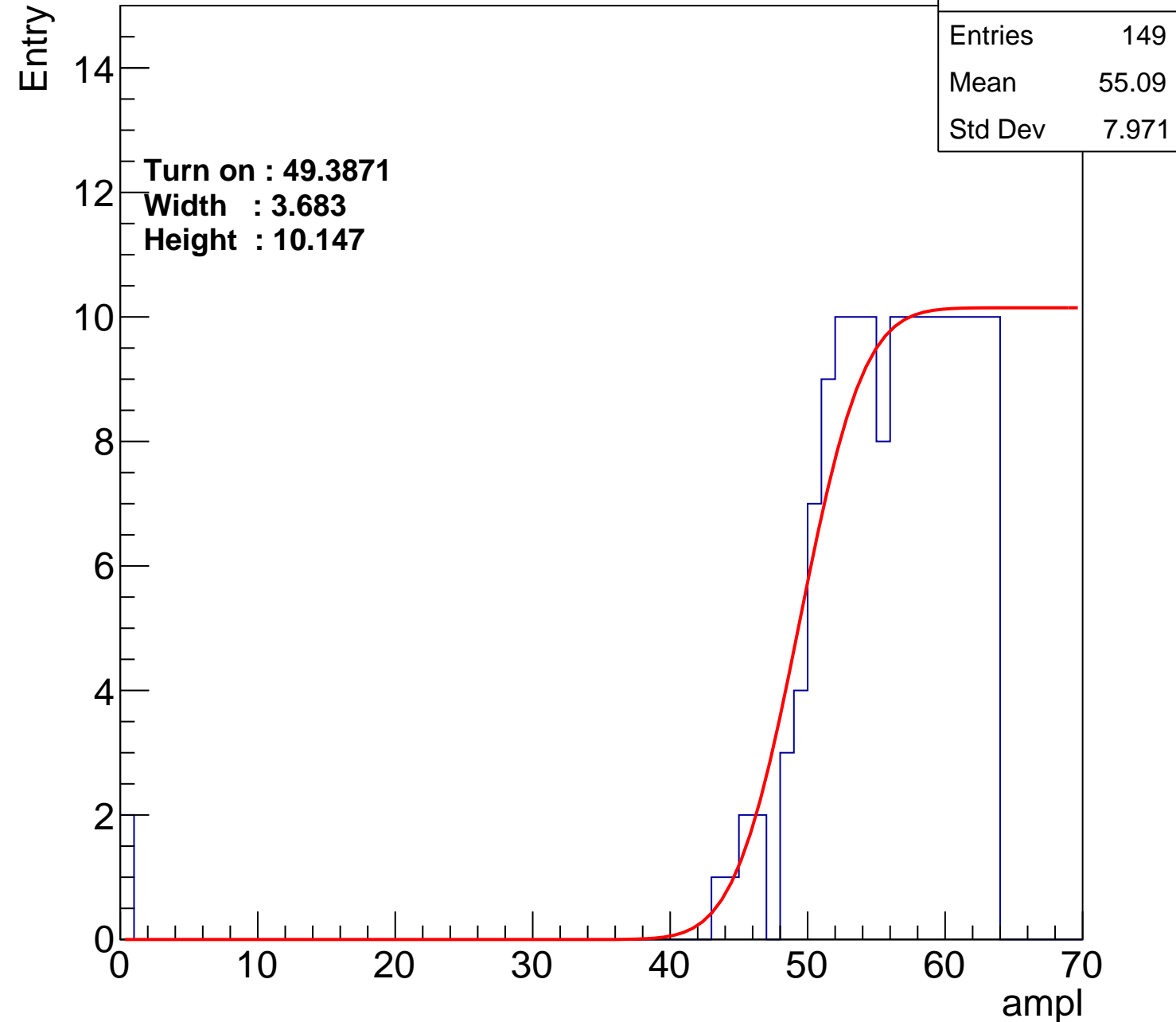
Width : 3.683

Height : 10.147

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch81

calib_packv5_040323_1717.root, FC#2, port C3

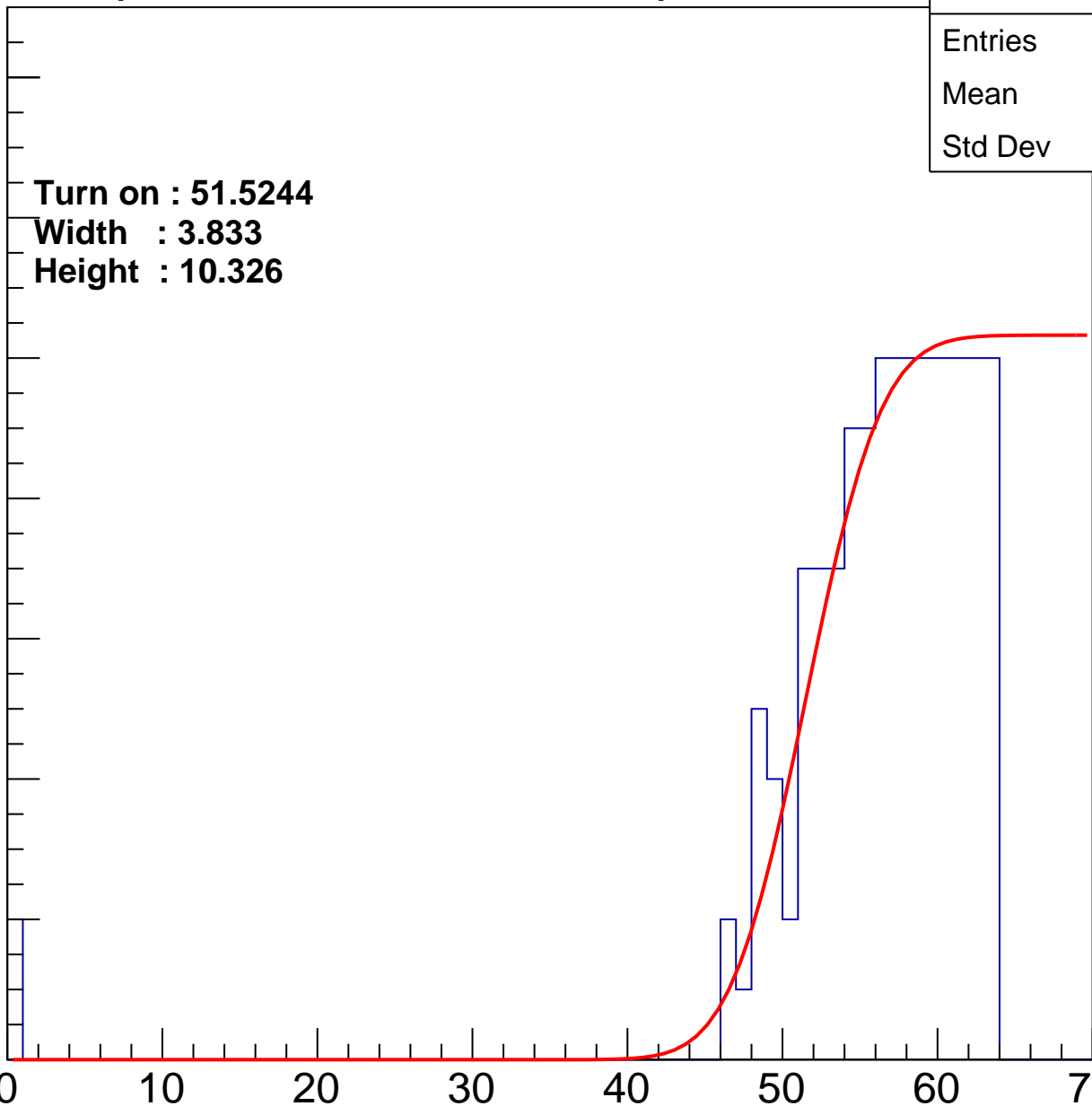
Entry

14
12
10
8
6
4
2
0

Turn on : 51.5244
Width : 3.833
Height : 10.326

Entries	135
Mean	55.61
Std Dev	8.127

ampl



B0L103S, U14-ch82

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	54.65
Std Dev	10.33

Turn on : 50.5854

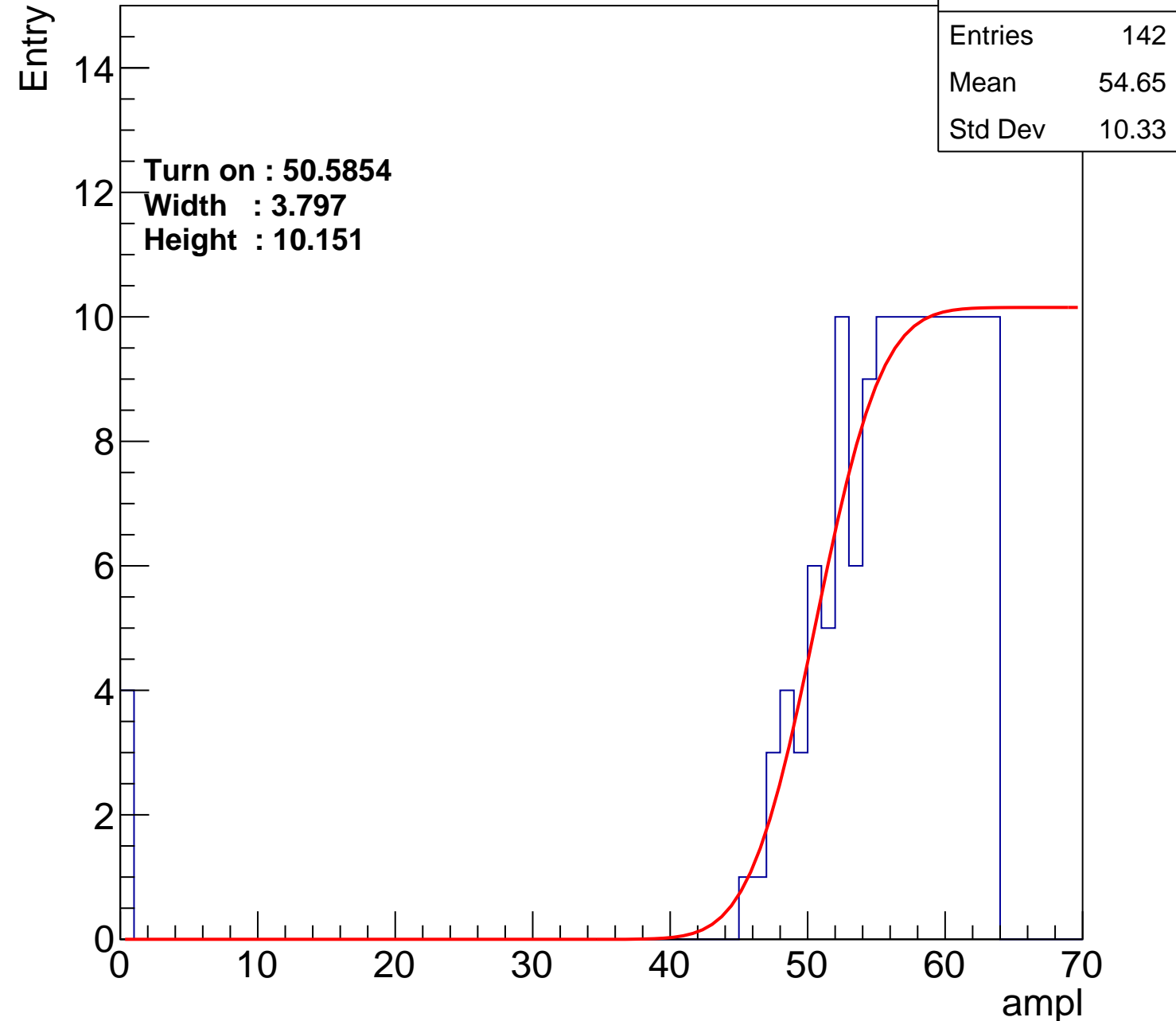
Width : 3.797

Height : 10.151

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch83

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.35
Std Dev	10.84

Turn on : 52.5092

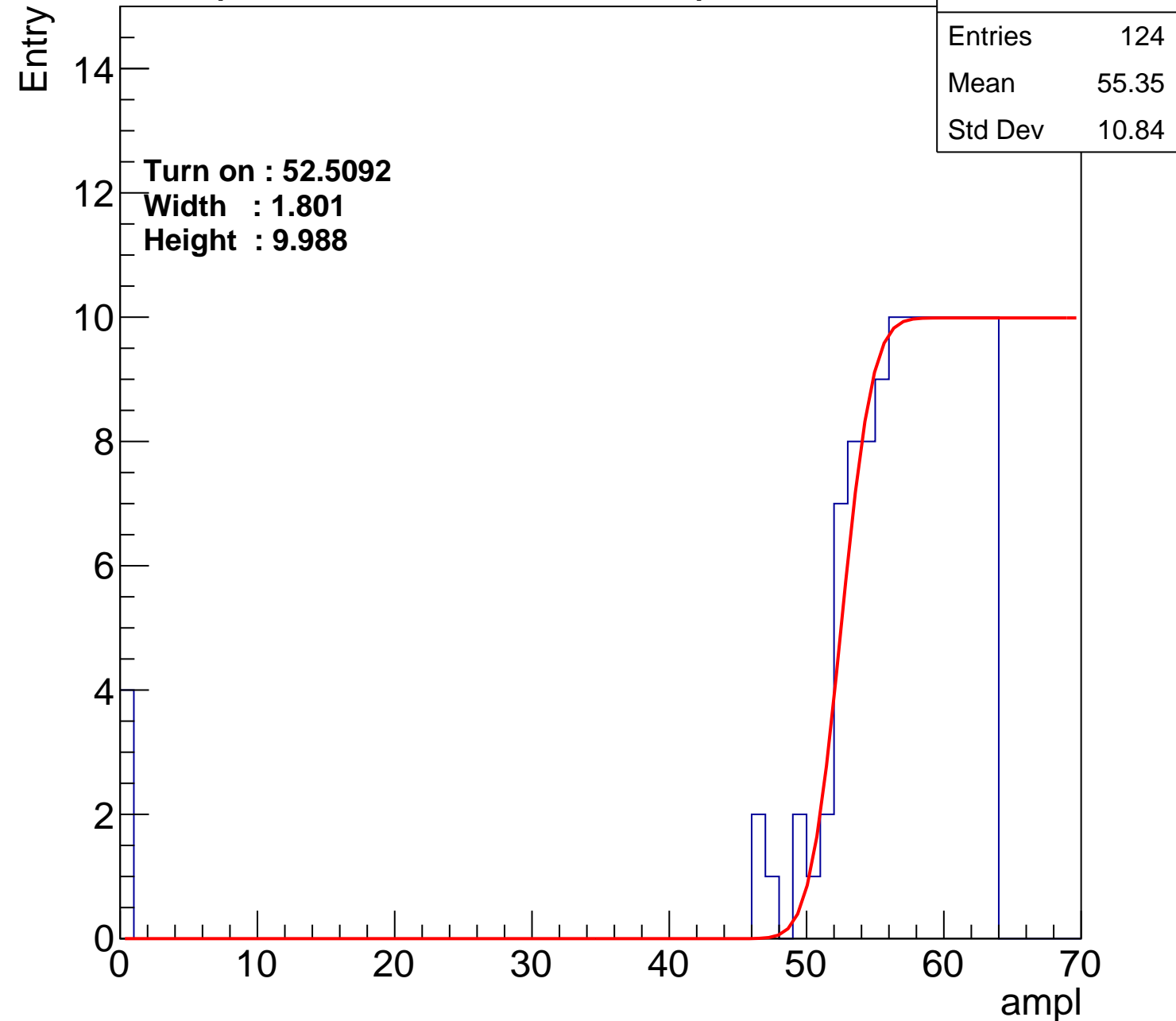
Width : 1.801

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch84

calib_packv5_040323_1717.root, FC#2, port C3

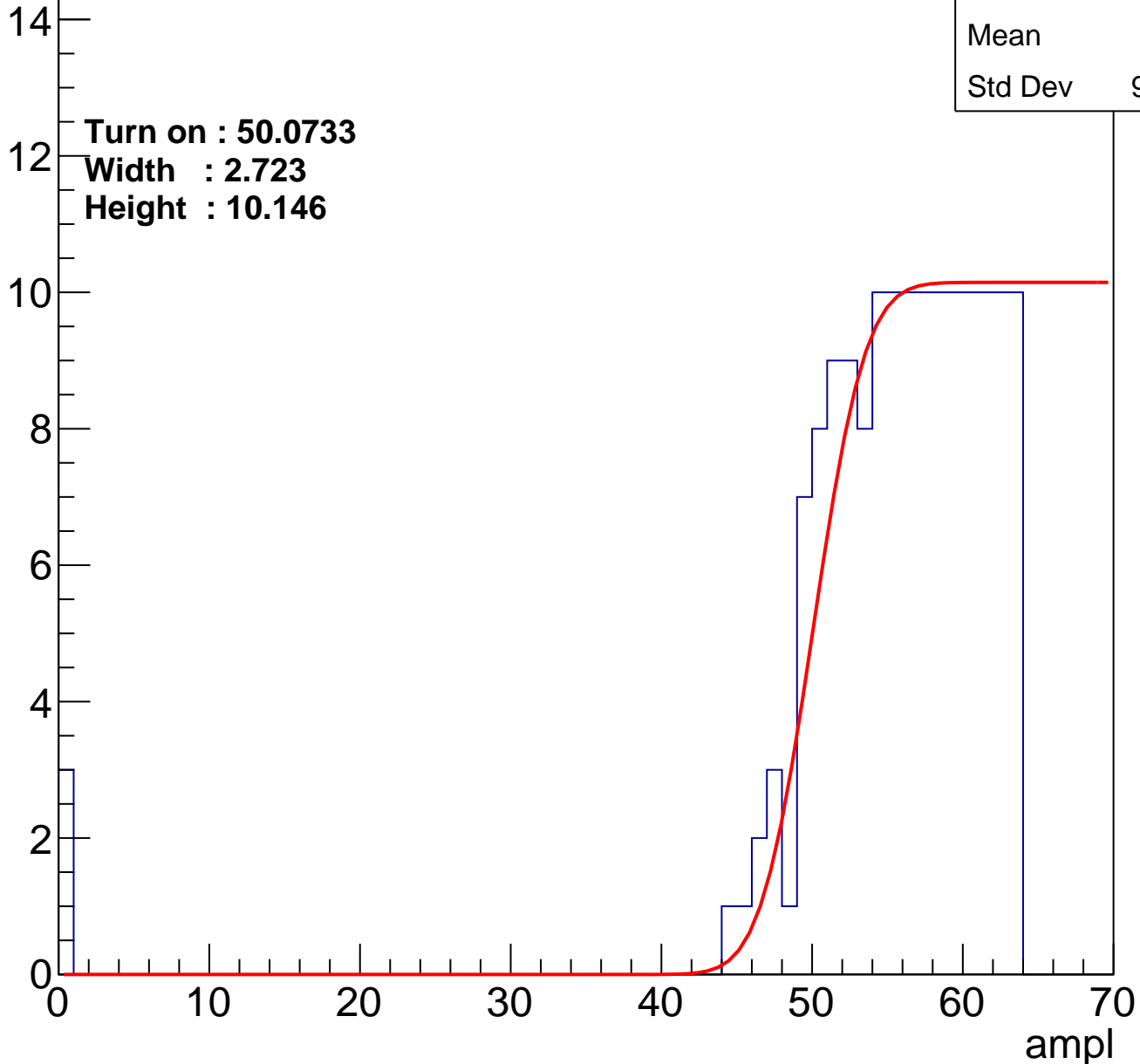
Entries	152
Mean	54.7
Std Dev	9.048

Turn on : 50.0733

Width : 2.723

Height : 10.146

Entry



B0L103S, U14-ch85

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	55.11
Std Dev	7.828

Turn on : 48.5824

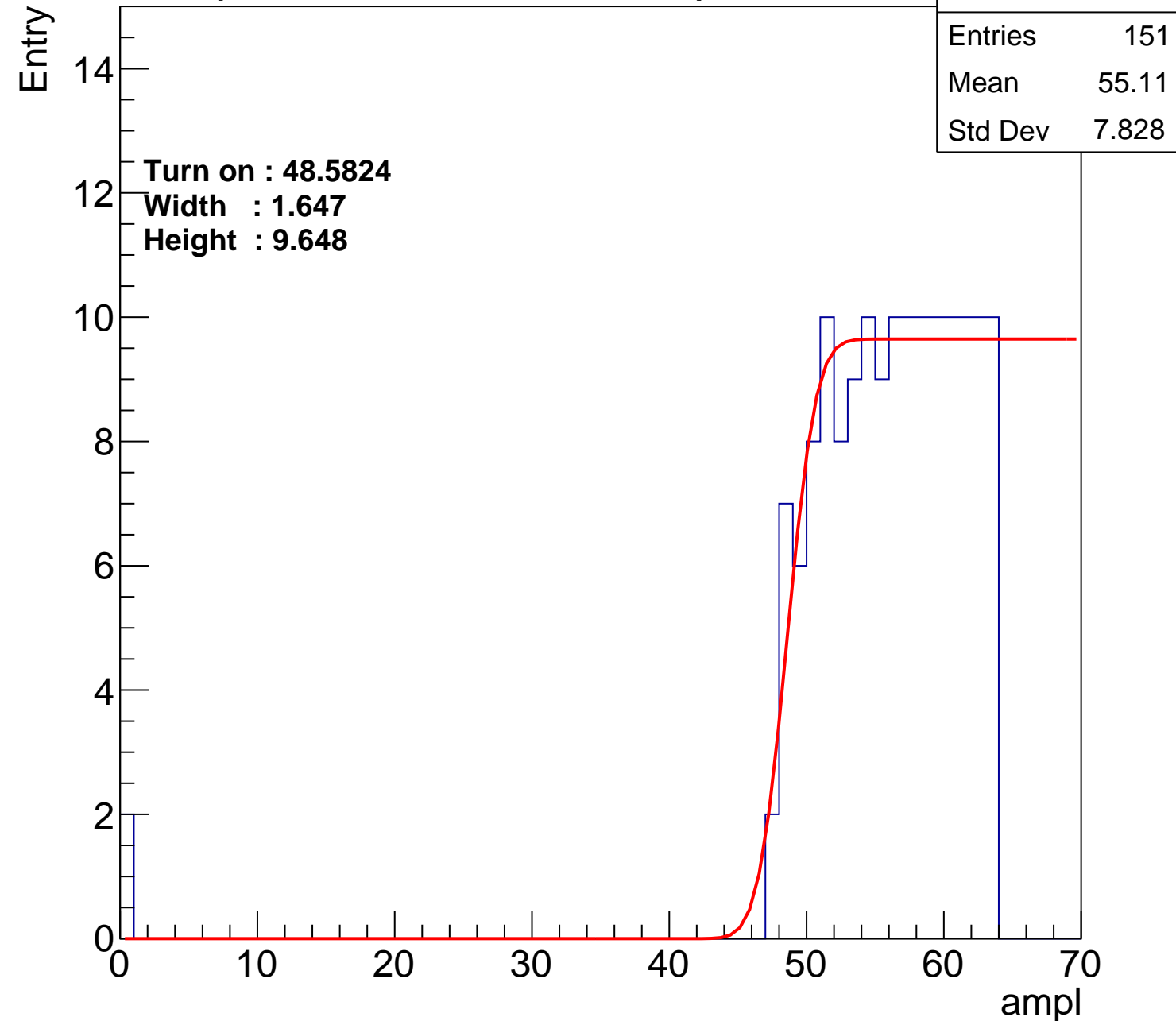
Width : 1.647

Height : 9.648

Entry

14
12
10
8
6
4
2
0

ampl



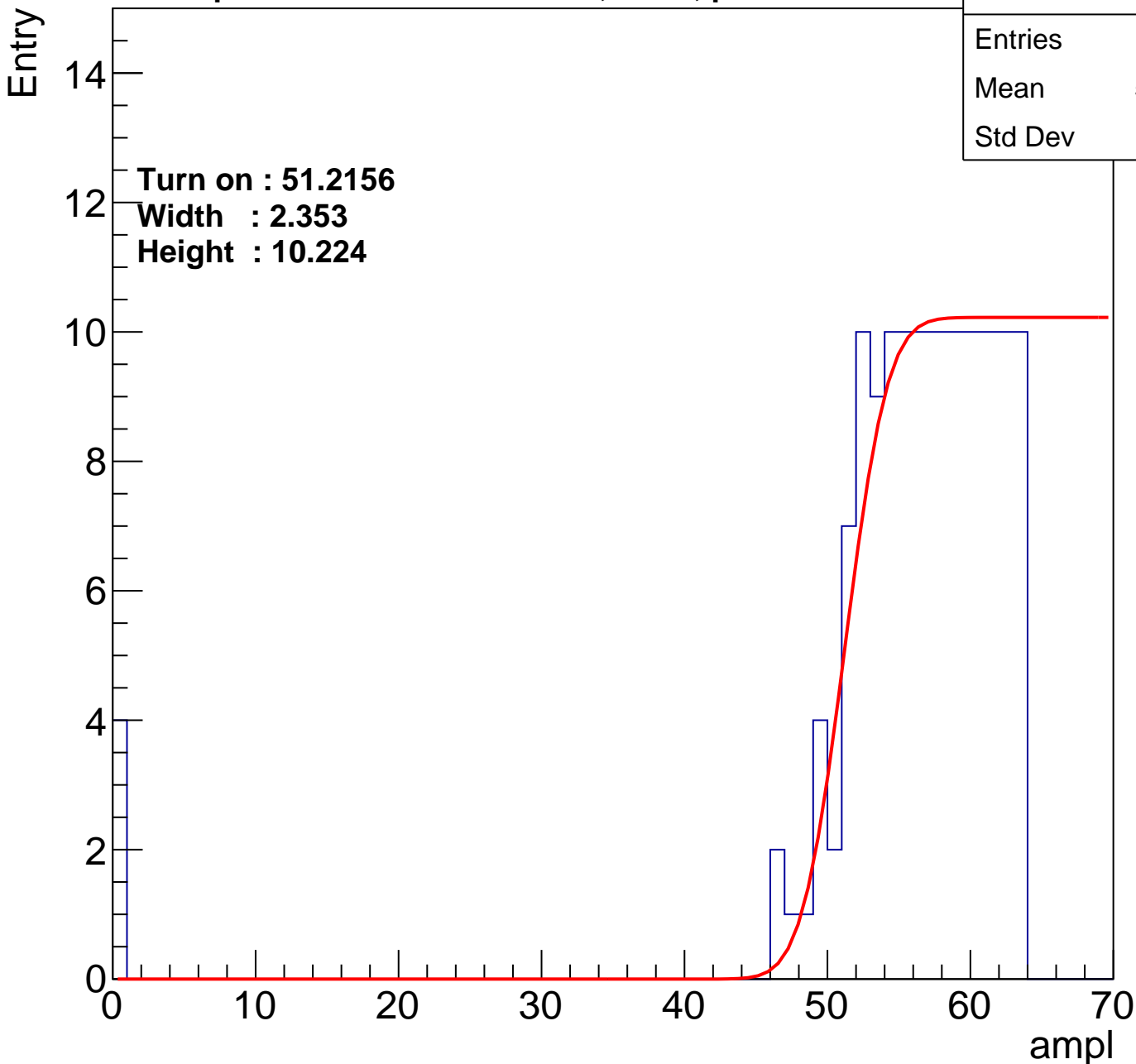
calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.91
Std Dev	10.3

Entries	140
Mean	54.91
Std Dev	10.3

Width : 2.353

Height : 10.224



B0L103S, U14-ch87

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	54.01
Std Dev	11.11

Turn on : 49.8968

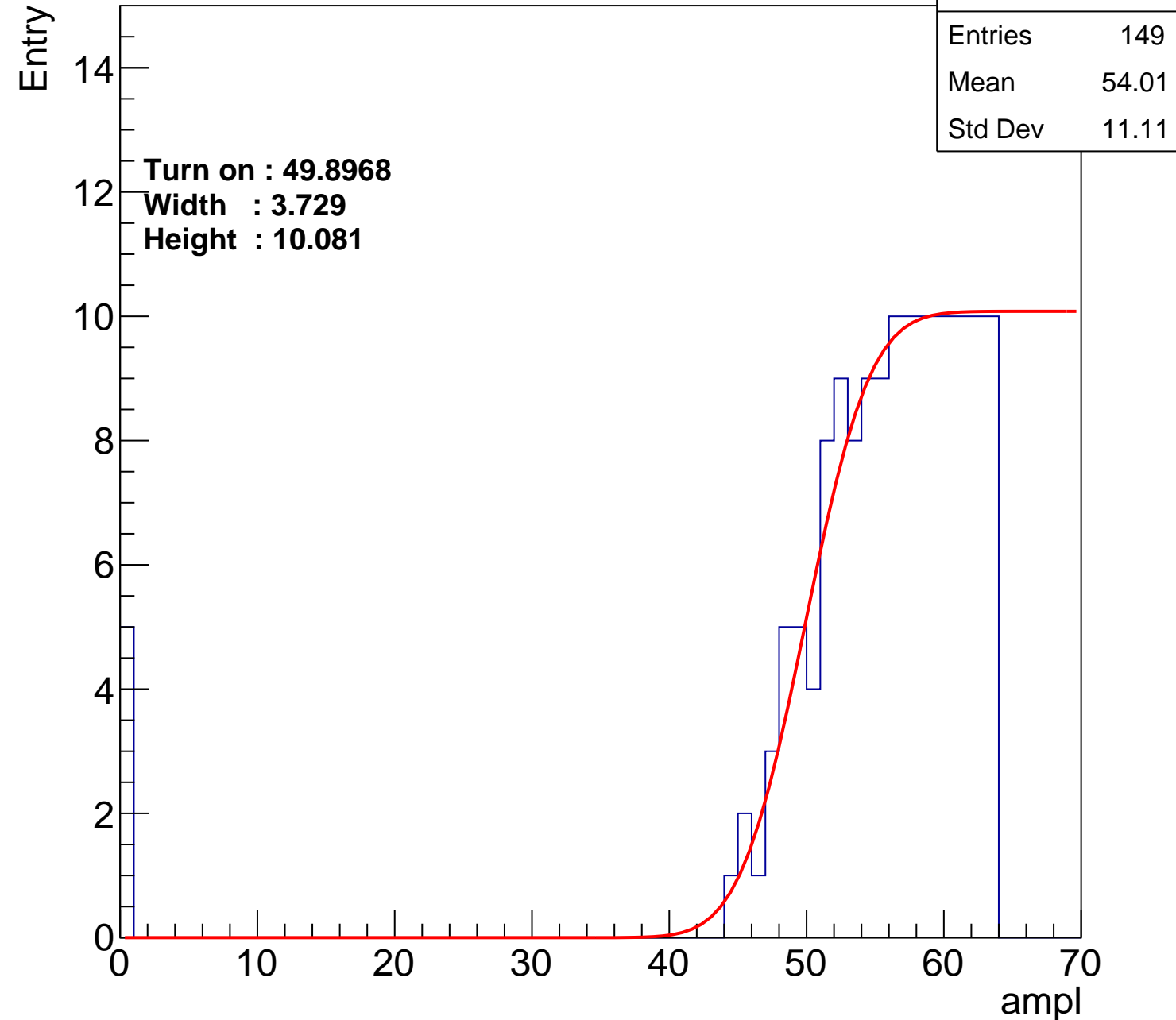
Width : 3.729

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch88

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.14
Std Dev	12.18

Turn on : 50.5182

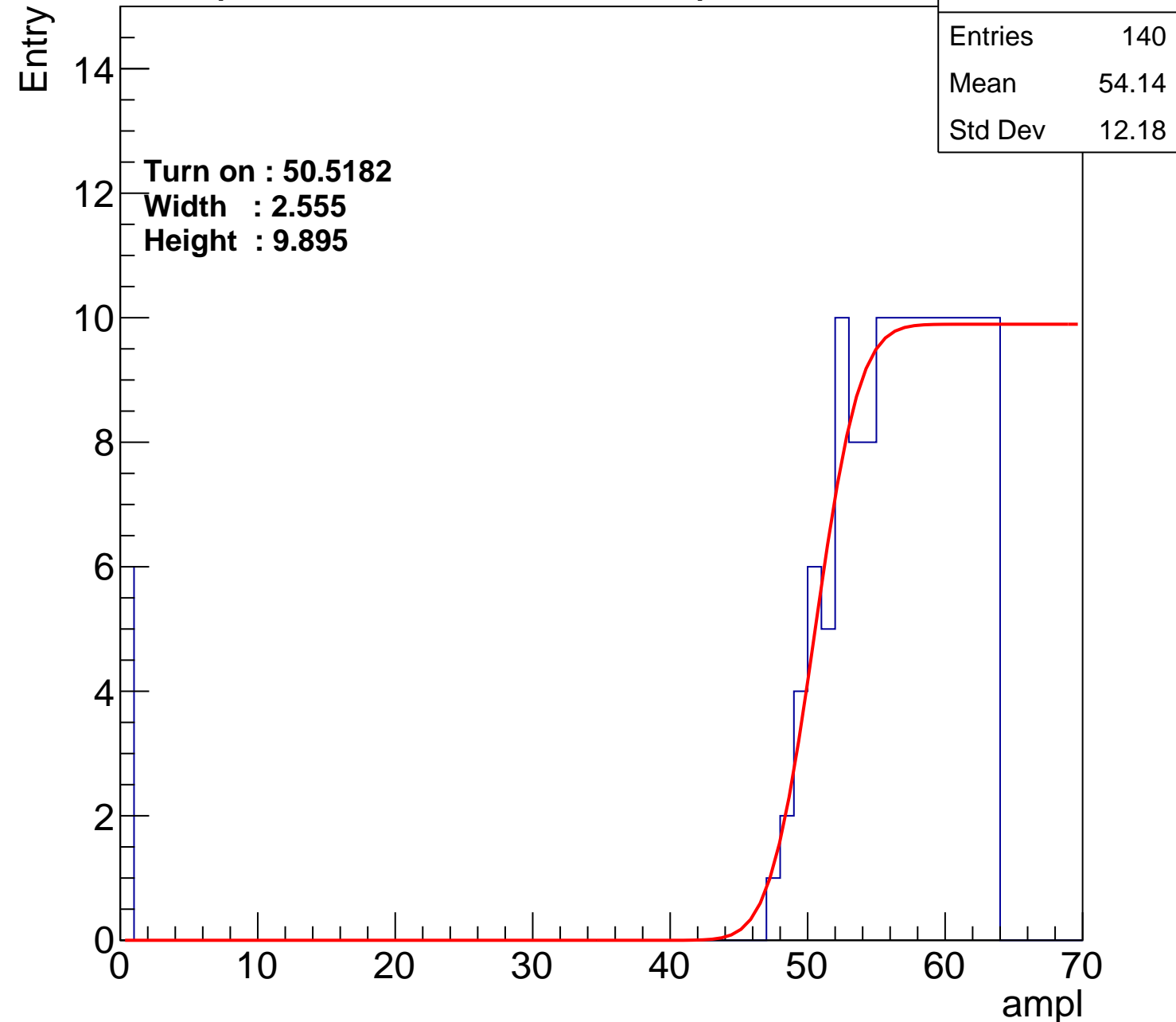
Width : 2.555

Height : 9.895

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_040323_1717.root, FC#2, port C3

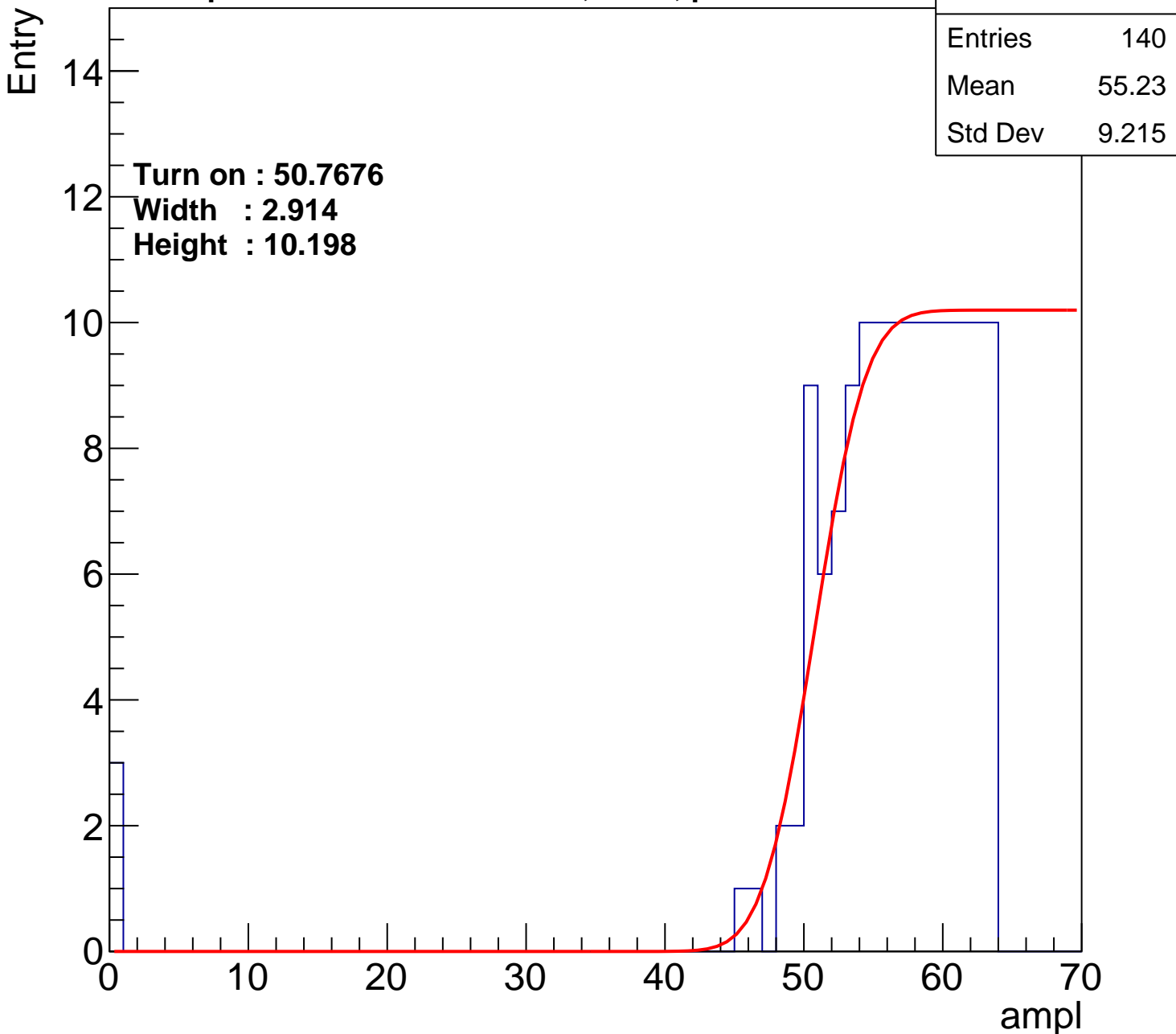
Entries	140
Mean	55.23
Std Dev	9.215

Mean	55.23
------	-------

Std Dev	9.215
---------	-------

Width : 2.914

Height : 10.198



B0L103S, U14-ch90

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.22
Std Dev	9.421

Turn on : 51.3479

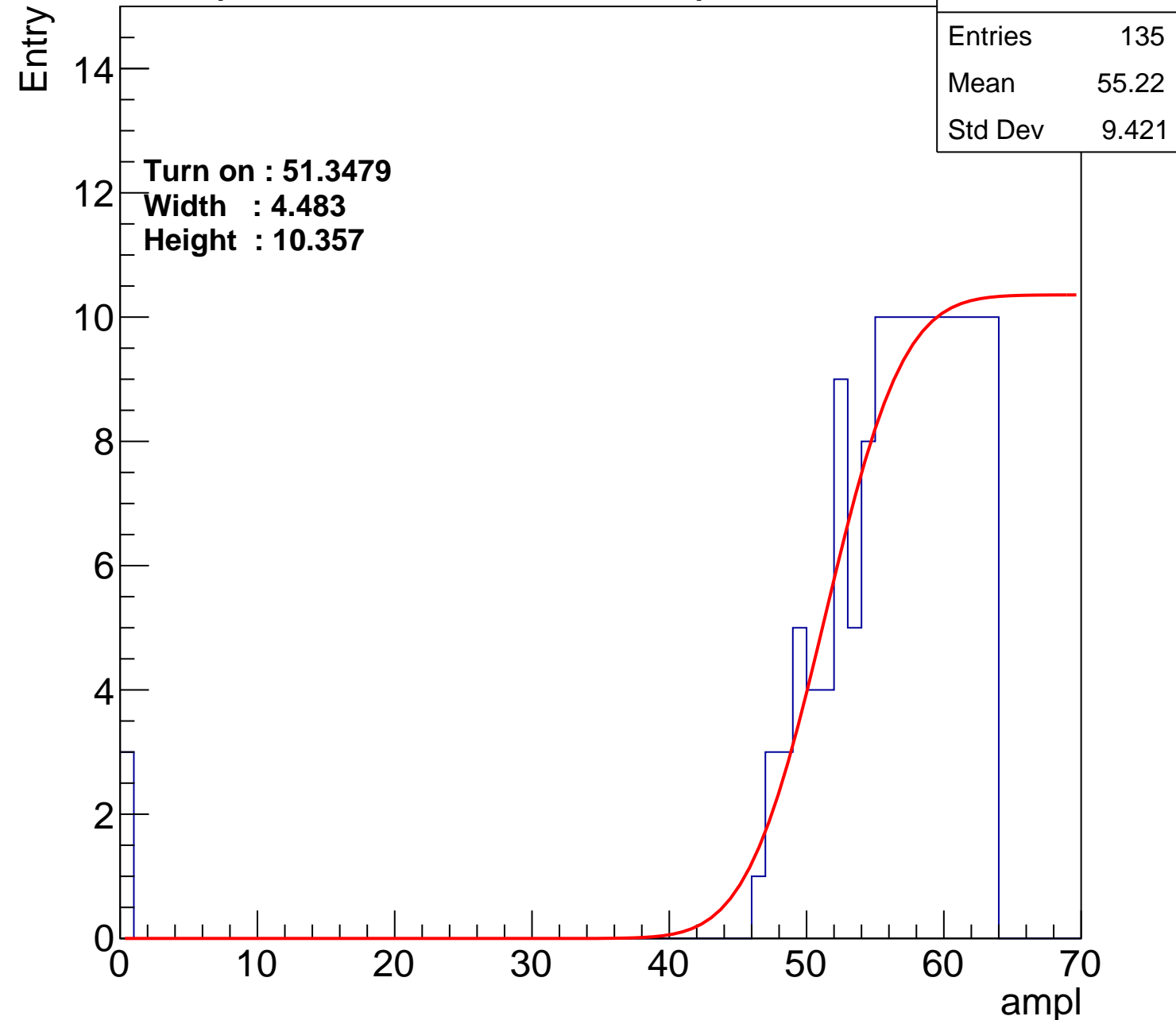
Width : 4.483

Height : 10.357

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch91

calib_packv5_040323_1717.root, FC#2, port C3

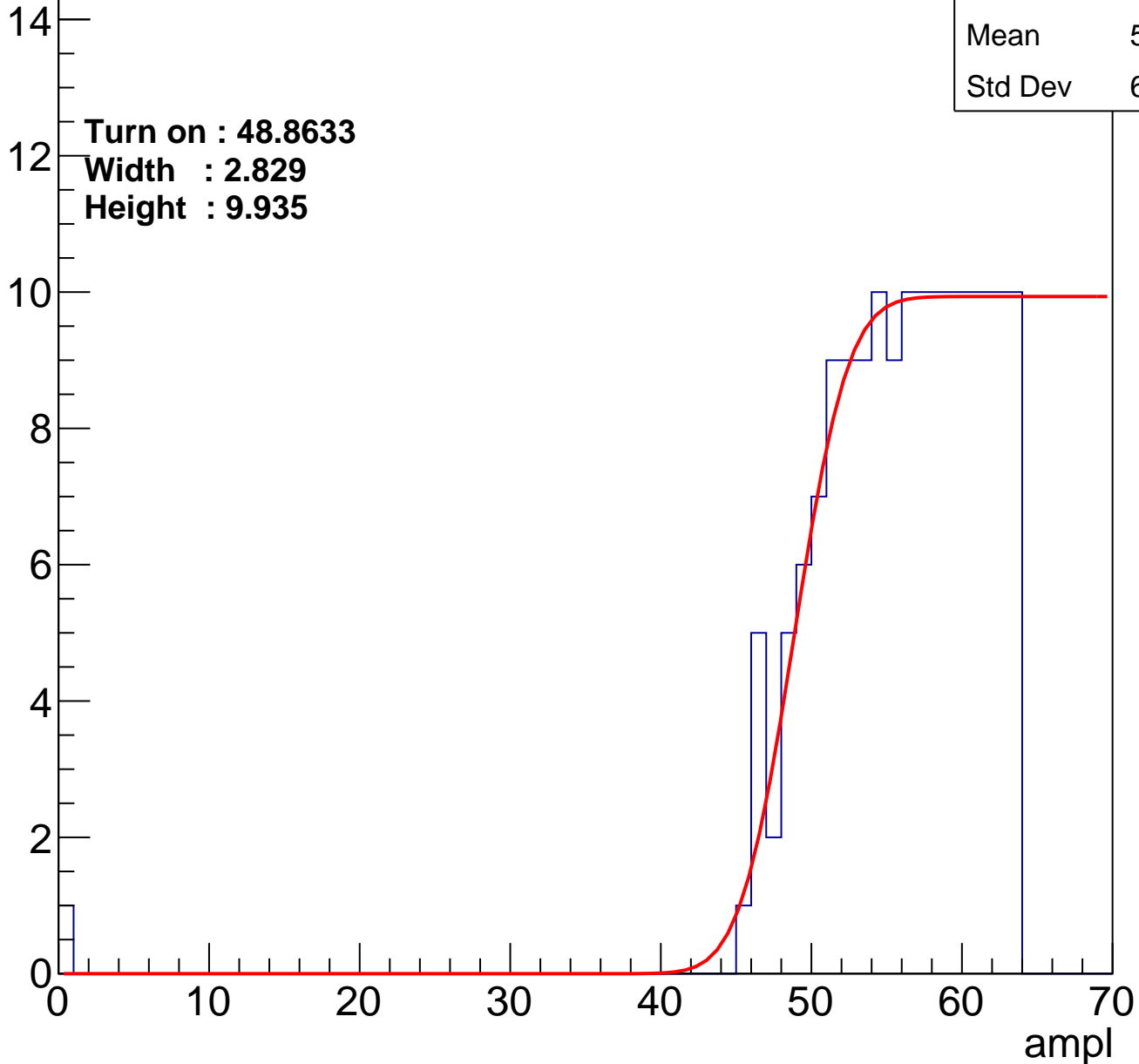
Entries	153
Mean	55.24
Std Dev	6.566

Turn on : 48.8633

Width : 2.829

Height : 9.935

Entry



B0L103S, U14-ch92

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	54.99
Std Dev	9.129

Turn on : 49.4072

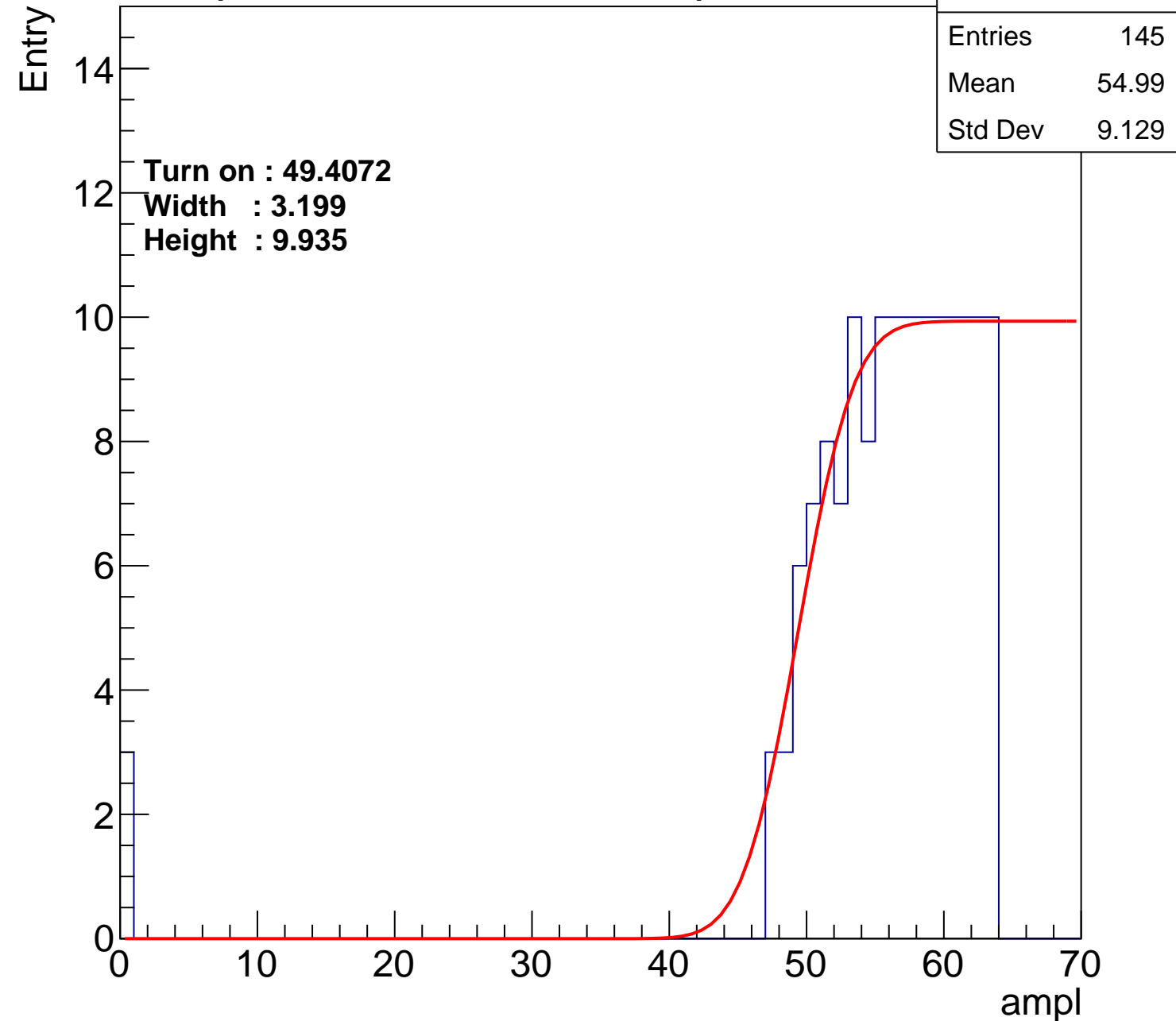
Width : 3.199

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch93

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.08
Std Dev	9.22

Turn on : 50.2093

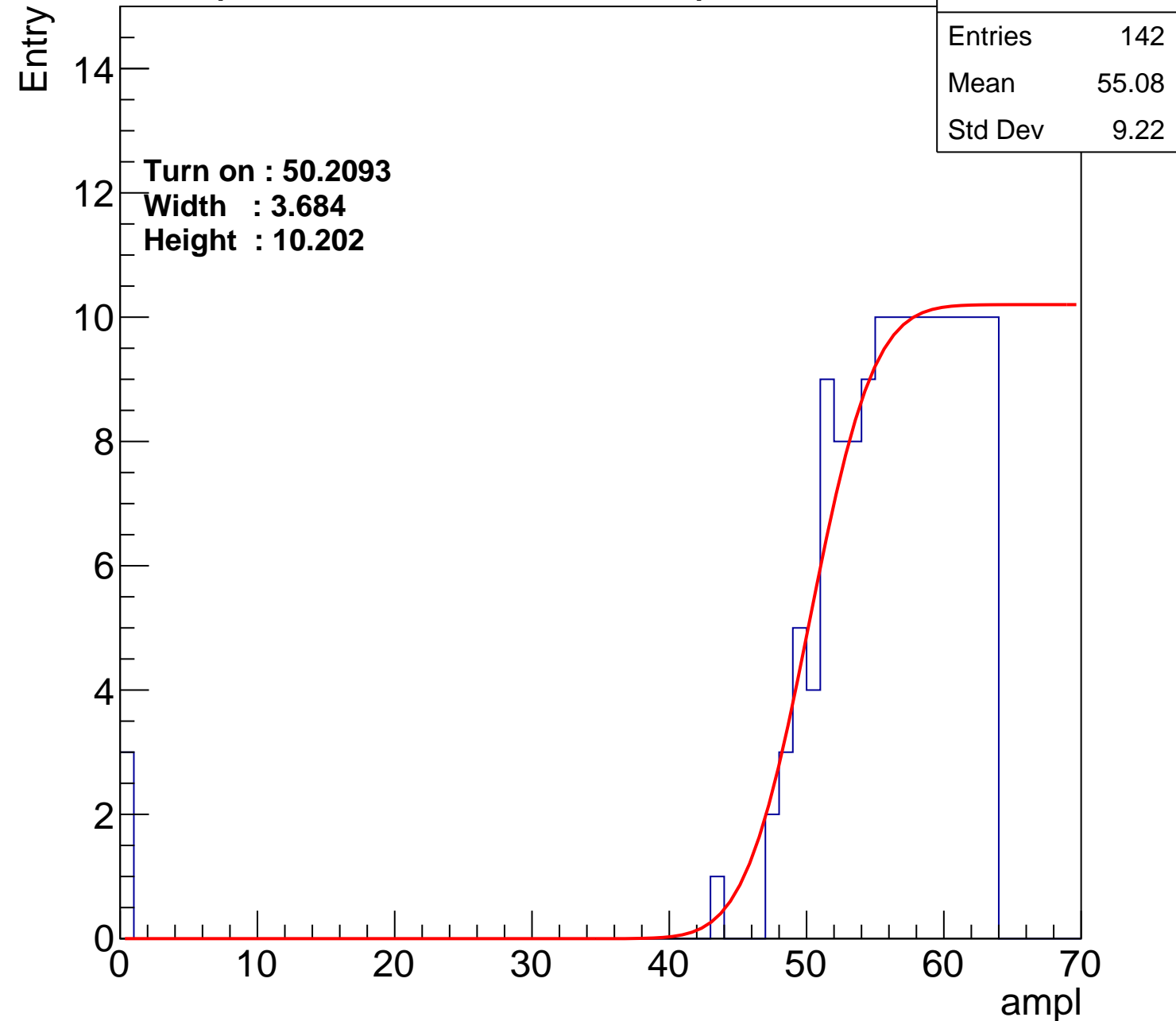
Width : 3.684

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch94

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.93
Std Dev	10.39

Turn on : 50.4932

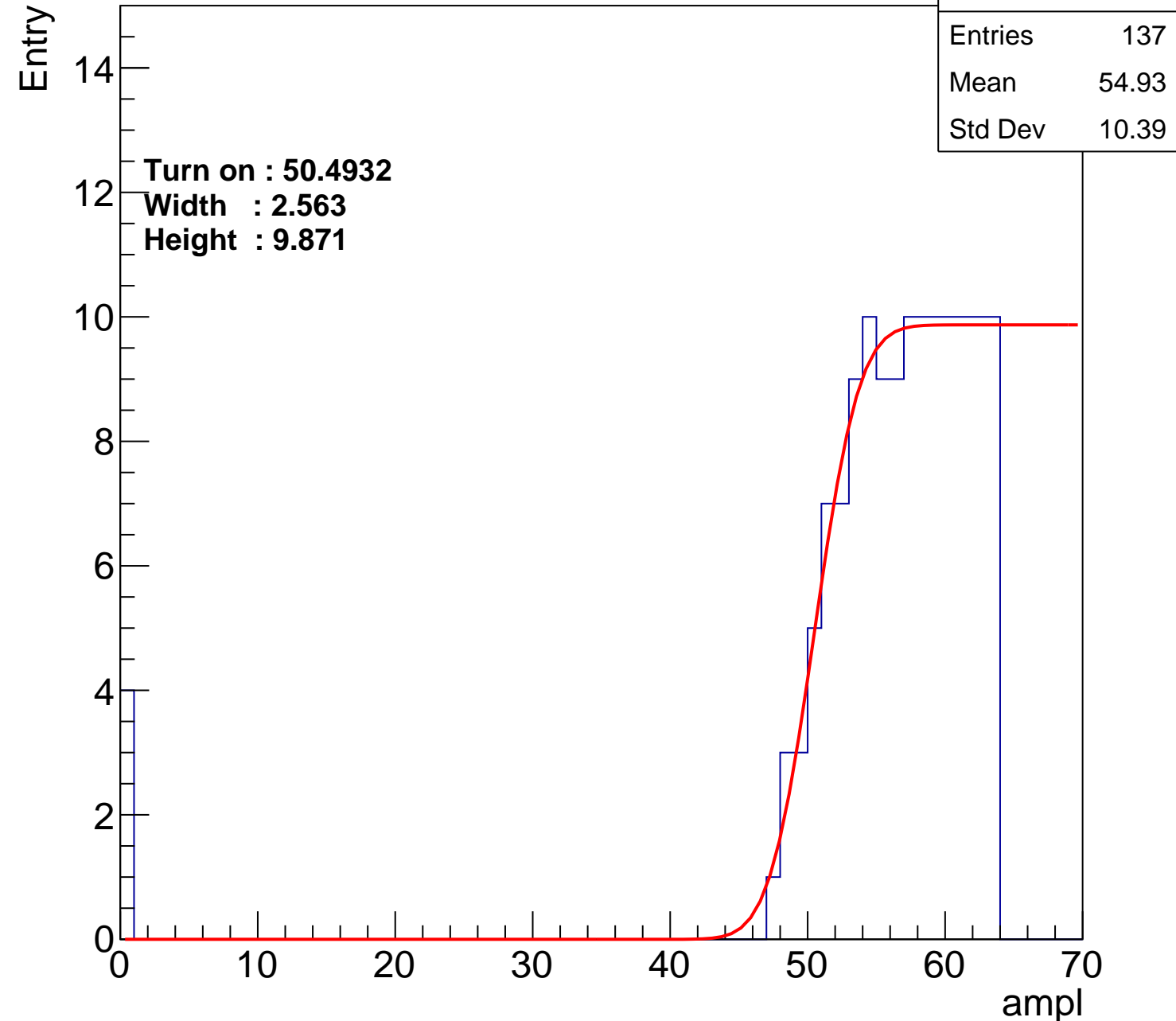
Width : 2.563

Height : 9.871

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch95

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	56.34
Std Dev	6.373

Turn on : 51.0004

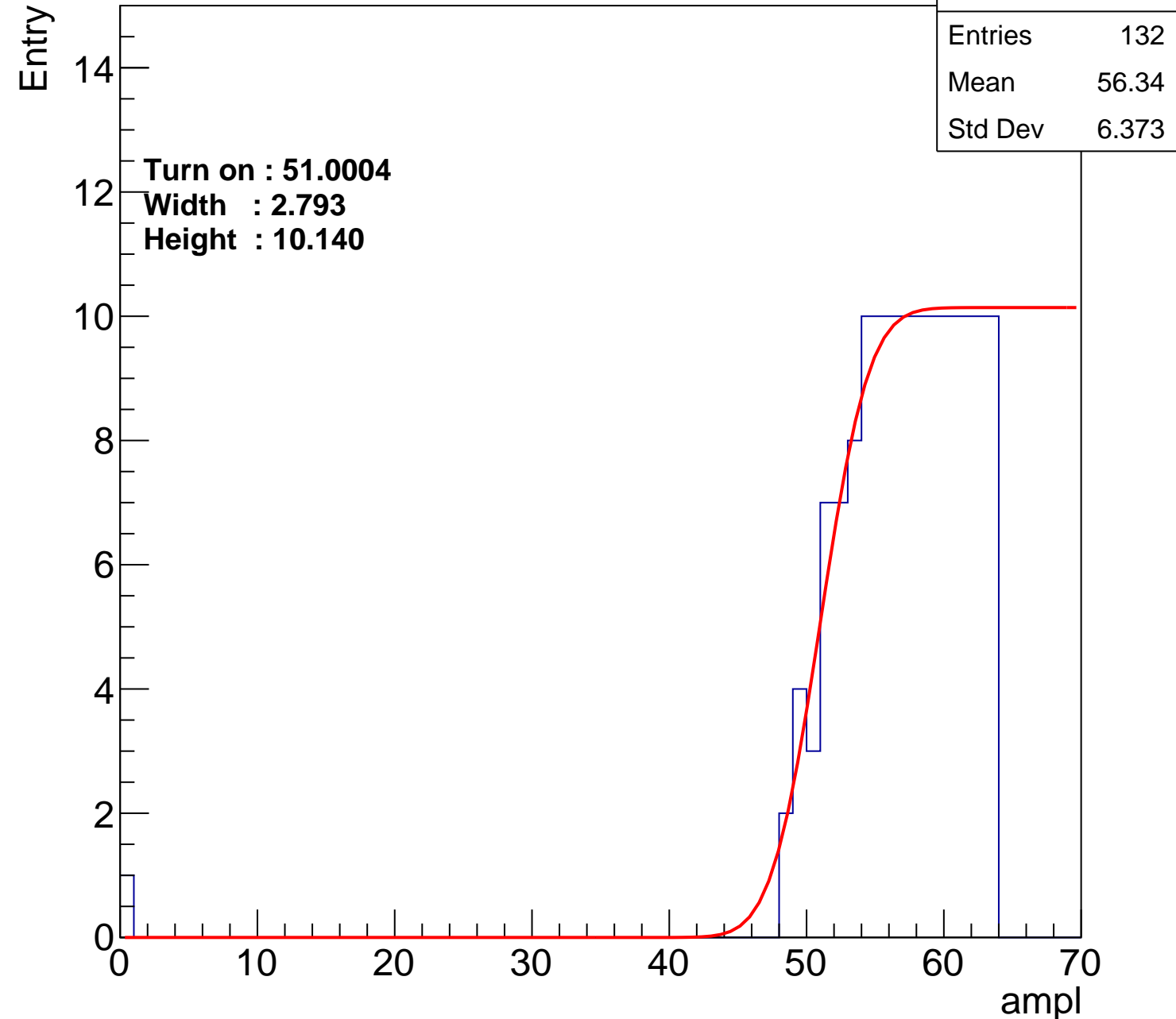
Width : 2.793

Height : 10.140

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch96

calib_packv5_040323_1717.root, FC#2, port C3

Entries	154
Mean	54.91
Std Dev	7.931

Turn on : 48.9078

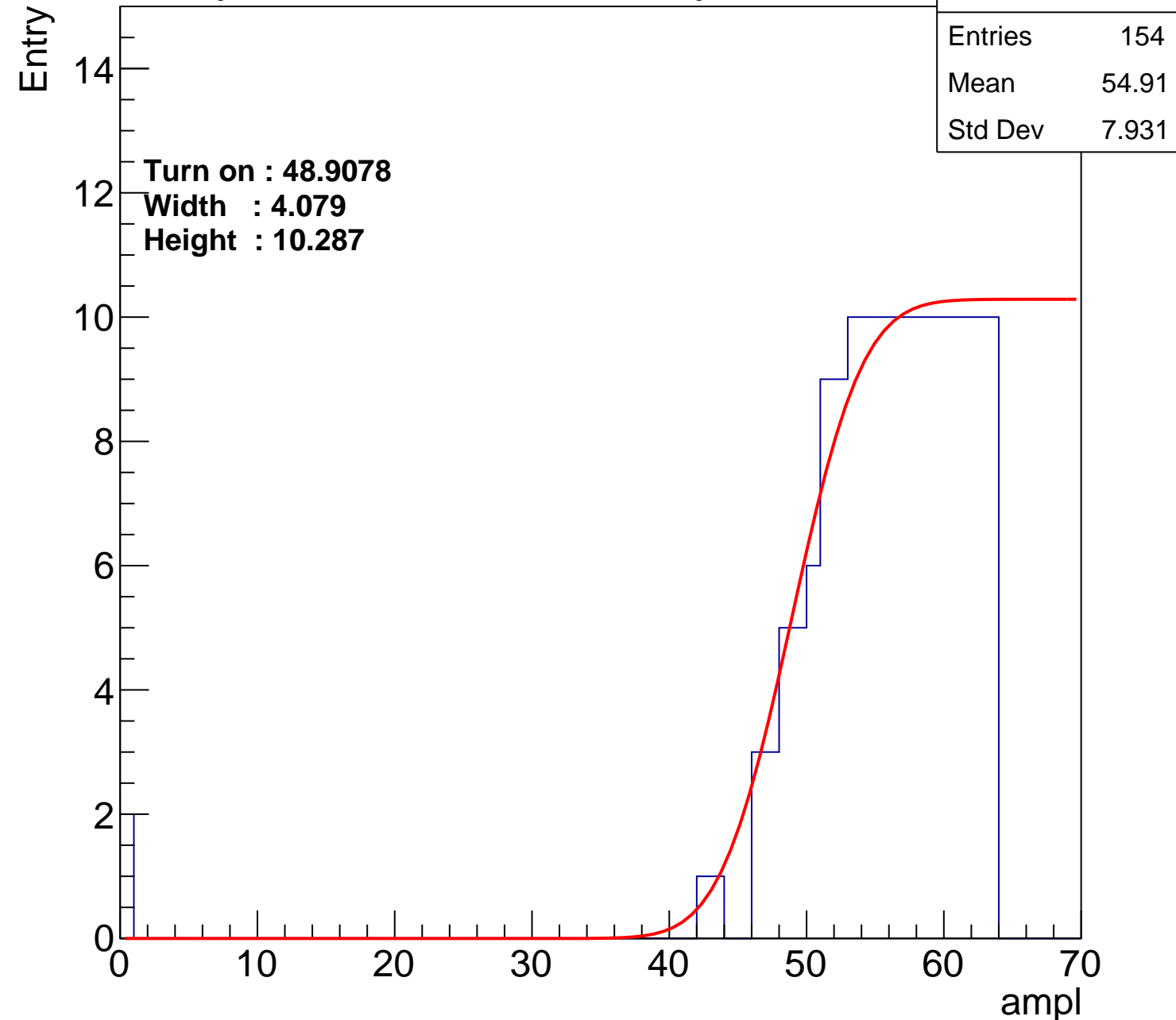
Width : 4.079

Height : 10.287

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch97

calib_packv5_040323_1717.root, FC#2, port C3

Entries	154
Mean	54.51
Std Dev	9.096

Turn on : 49.4078

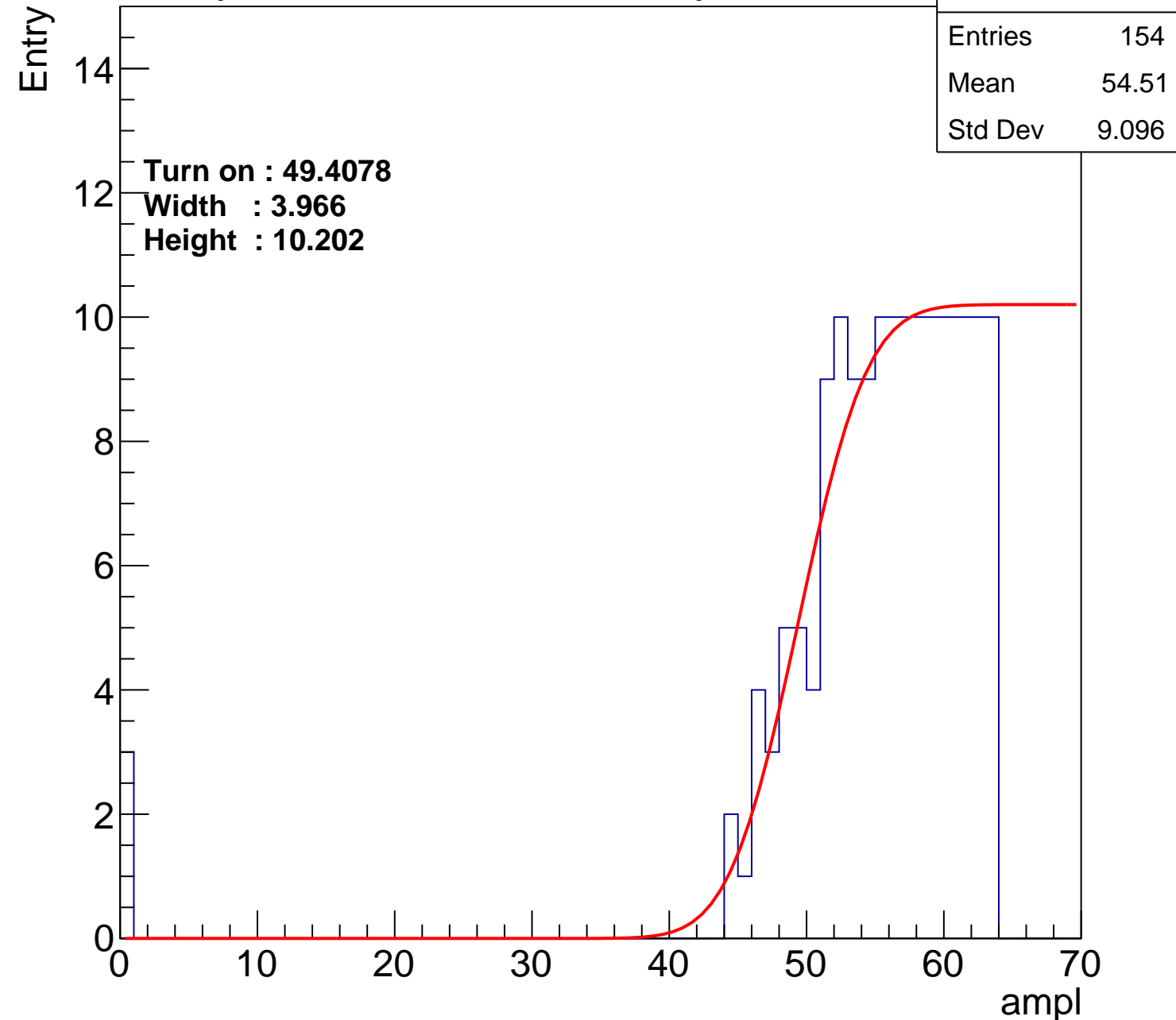
Width : 3.966

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch98

calib_packv5_040323_1717.root, FC#2, port C3

Entries	162
Mean	54.59
Std Dev	7.847

Turn on : 47.5610

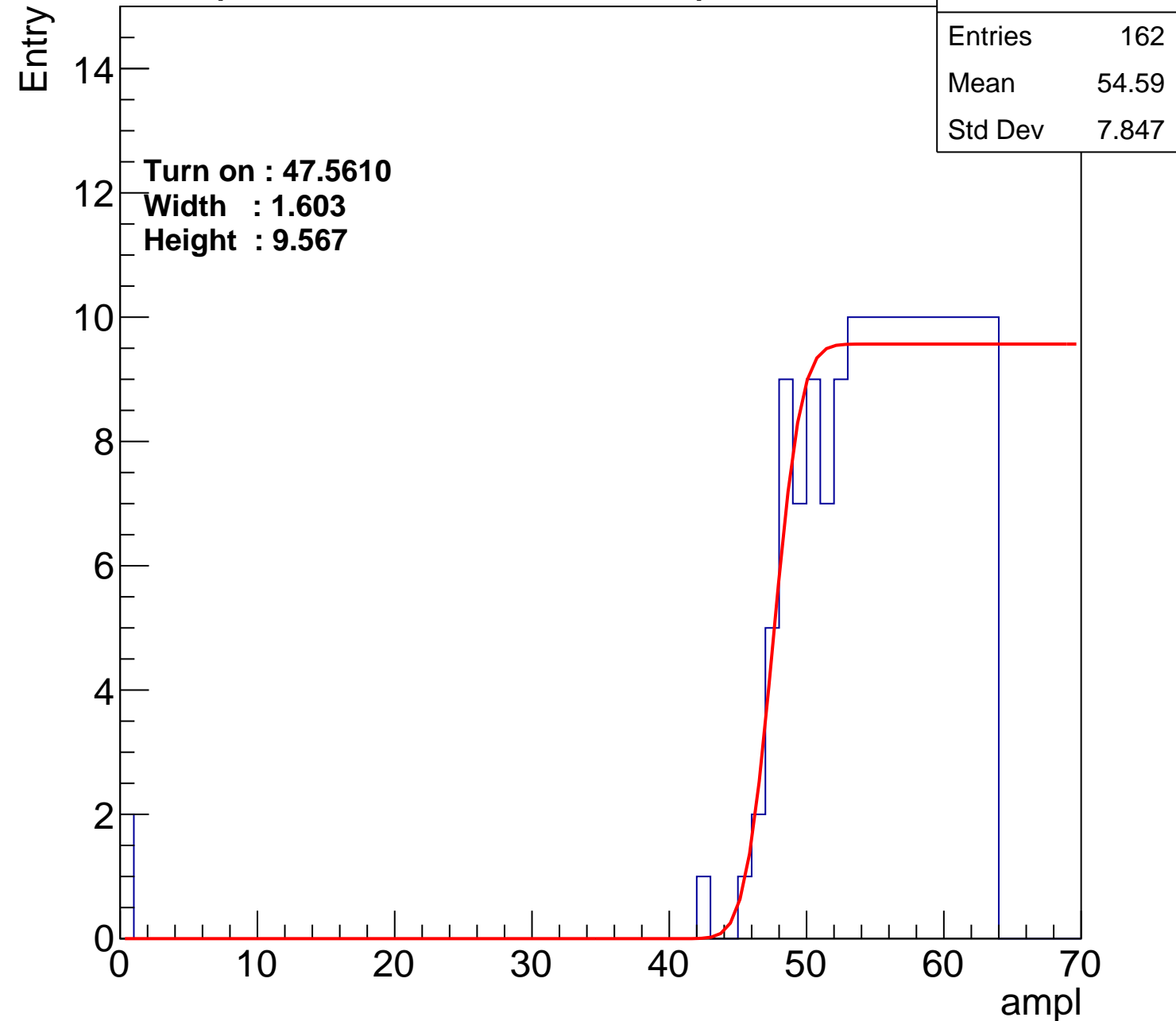
Width : 1.603

Height : 9.567

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch99

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.73
Std Dev	9.665

Turn on : 52.5632

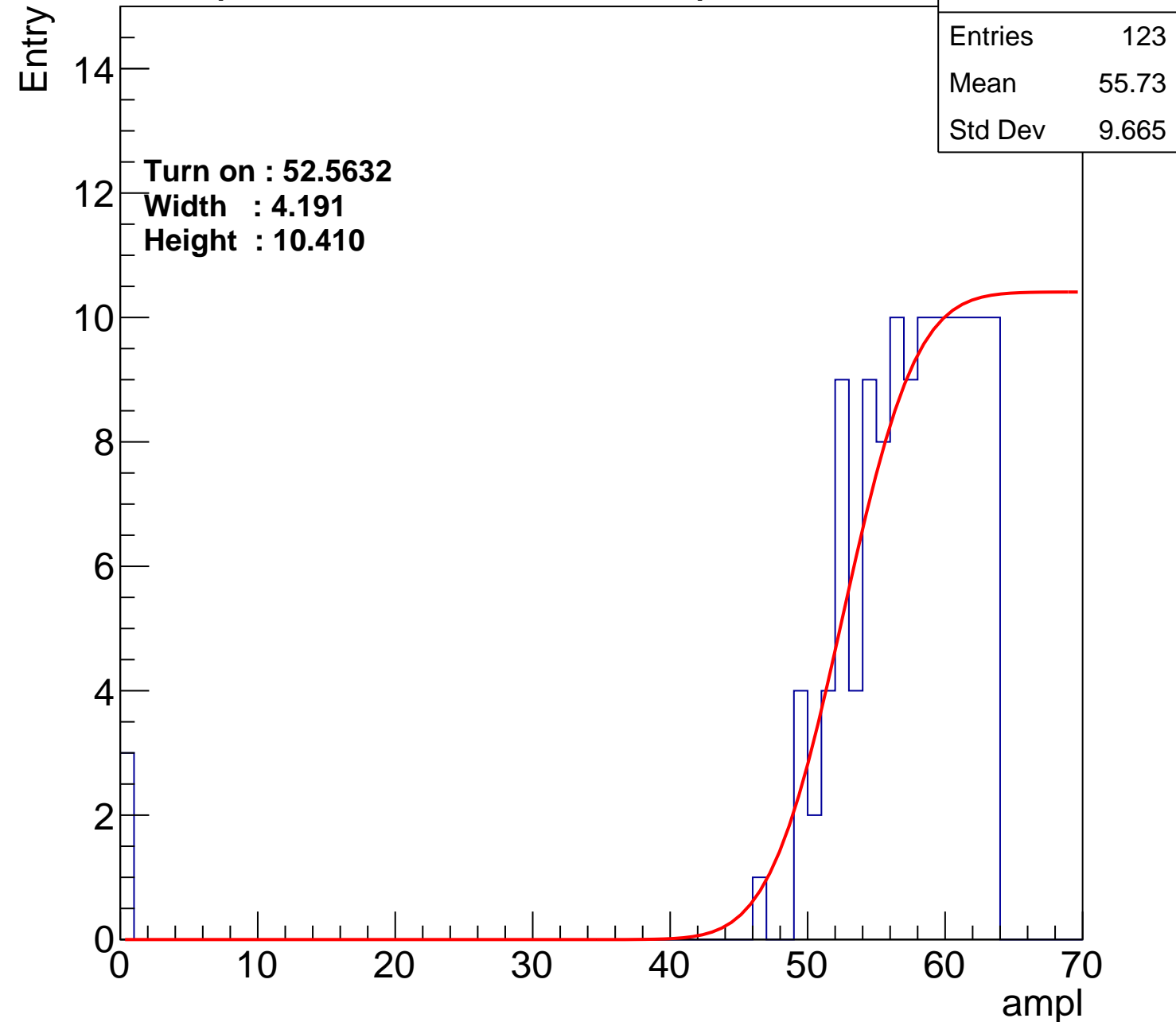
Width : 4.191

Height : 10.410

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch100

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.81
Std Dev	10.34

Turn on : 50.7556

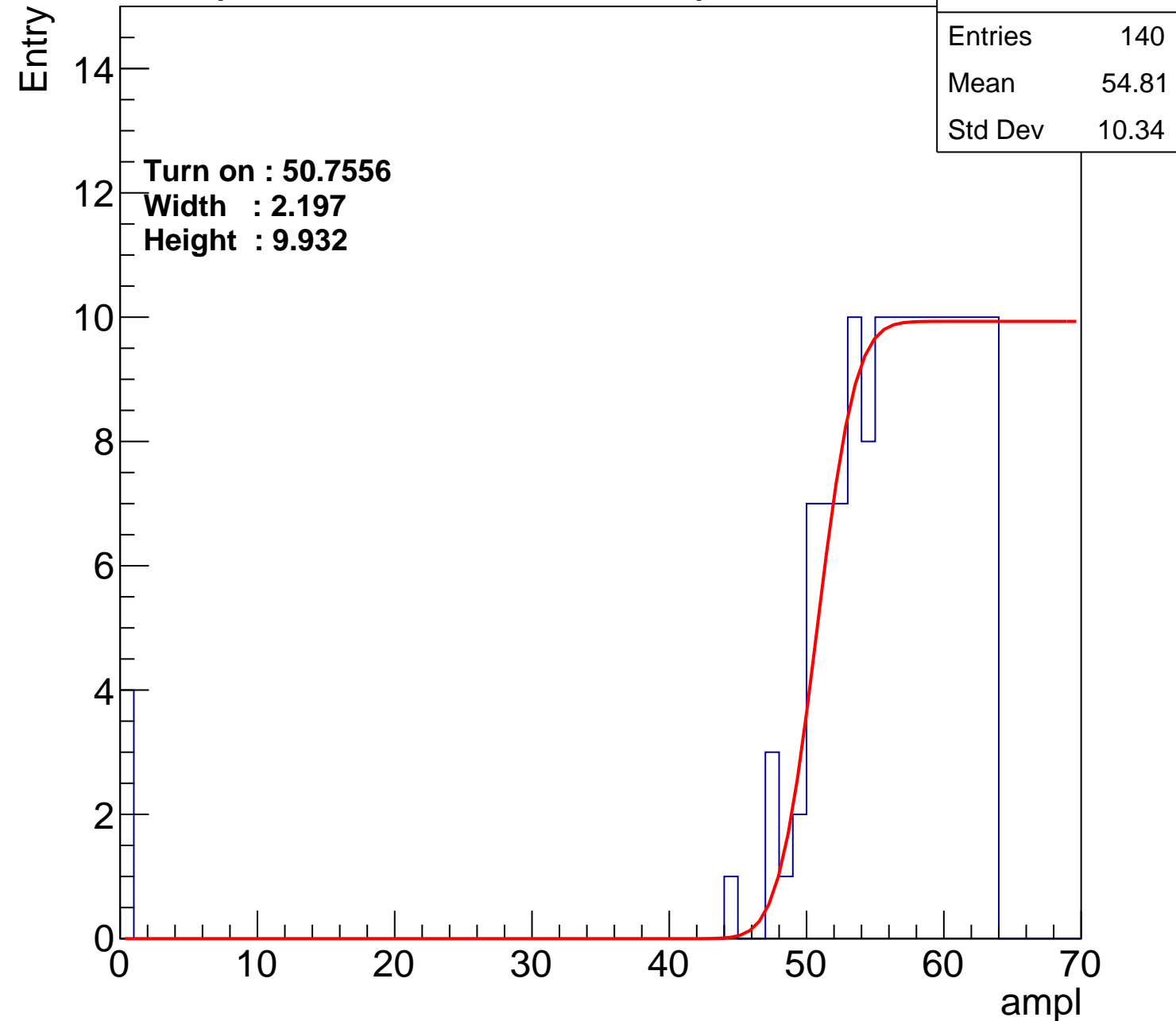
Width : 2.197

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch101

calib_packv5_040323_1717.root, FC#2, port C3

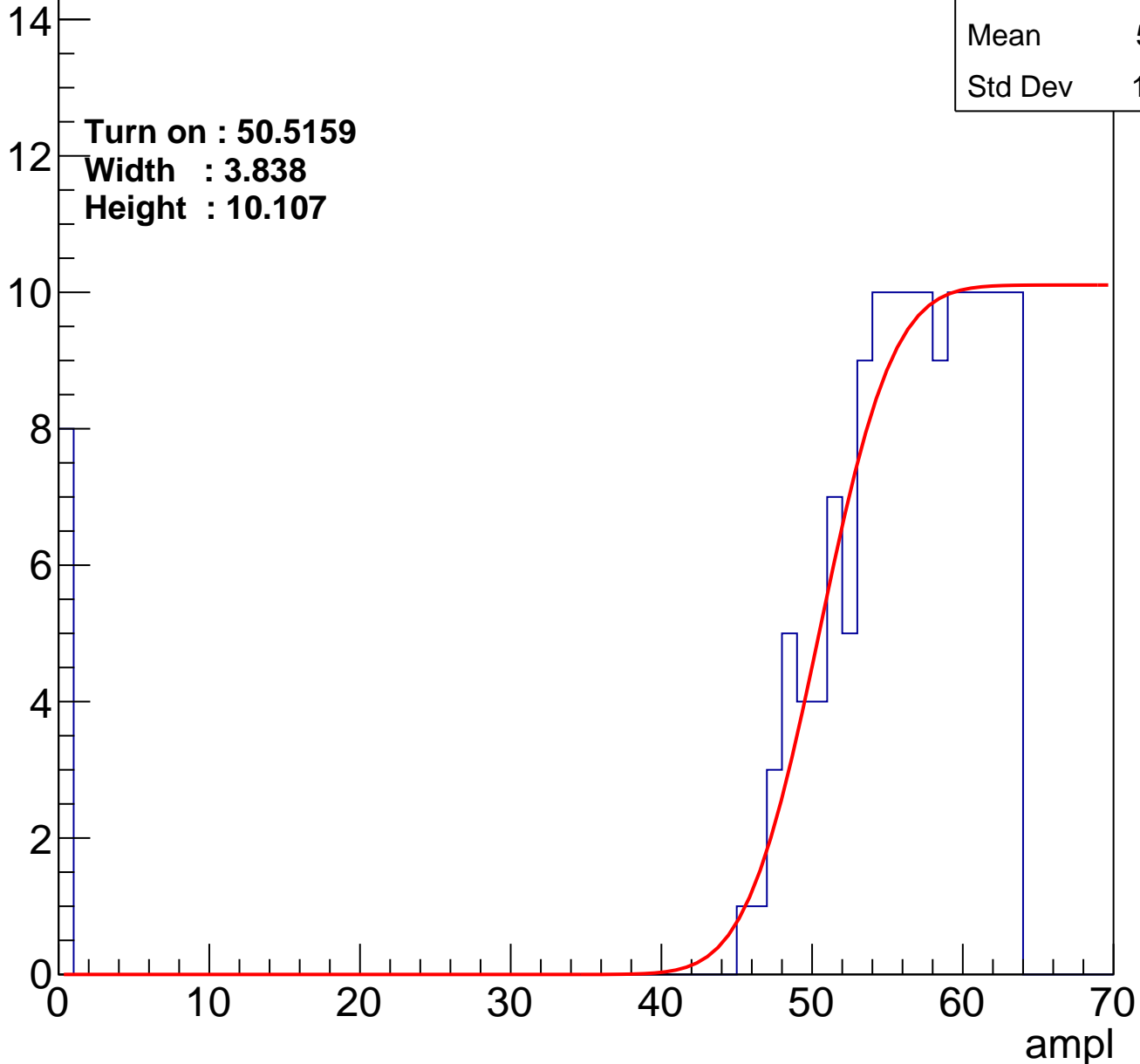
Entries	146
Mean	53.11
Std Dev	13.54

Turn on : 50.5159

Width : 3.838

Height : 10.107

Entry



B0L103S, U14-ch102

calib_packv5_040323_1717.root, FC#2, port C3

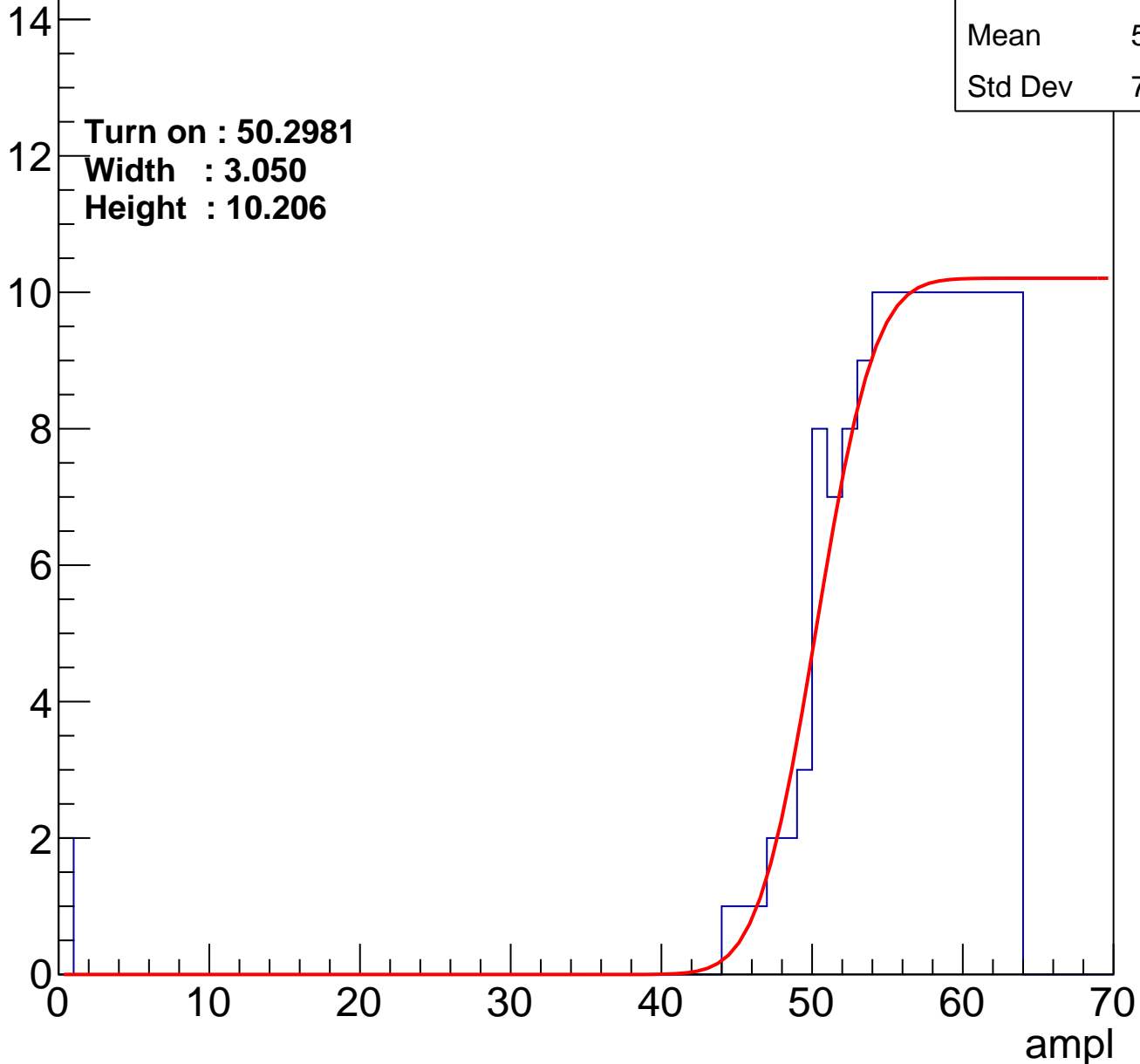
Entries	144
Mean	55.36
Std Dev	7.966

Turn on : 50.2981

Width : 3.050

Height : 10.206

Entry



B0L103S, U14-ch103

calib_packv5_040323_1717.root, FC#2, port C3

Entries	155
Mean	54.5
Std Dev	9.043

Turn on : 48.7355

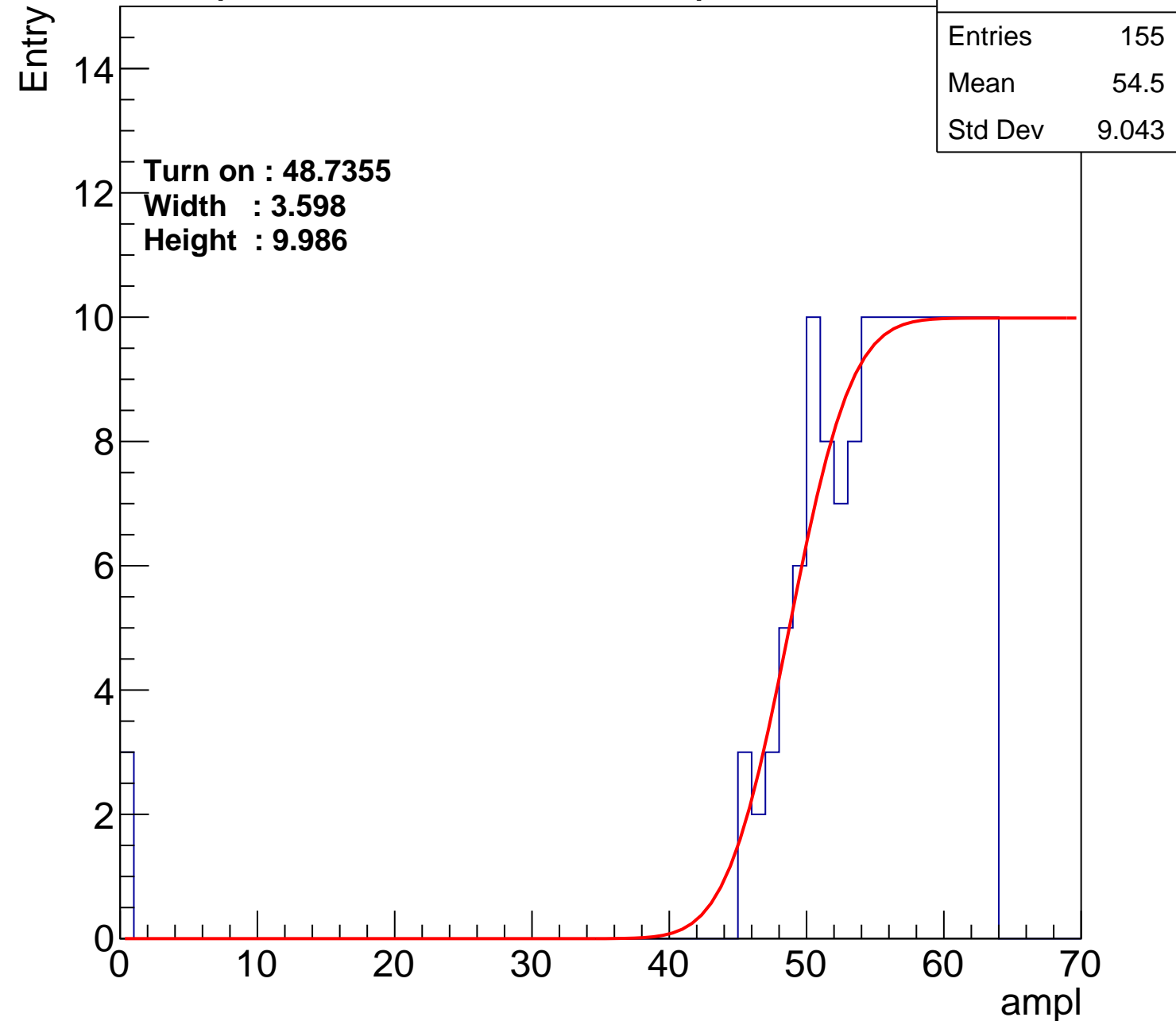
Width : 3.598

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch104

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	54.67
Std Dev	9.017

Turn on : 49.2587

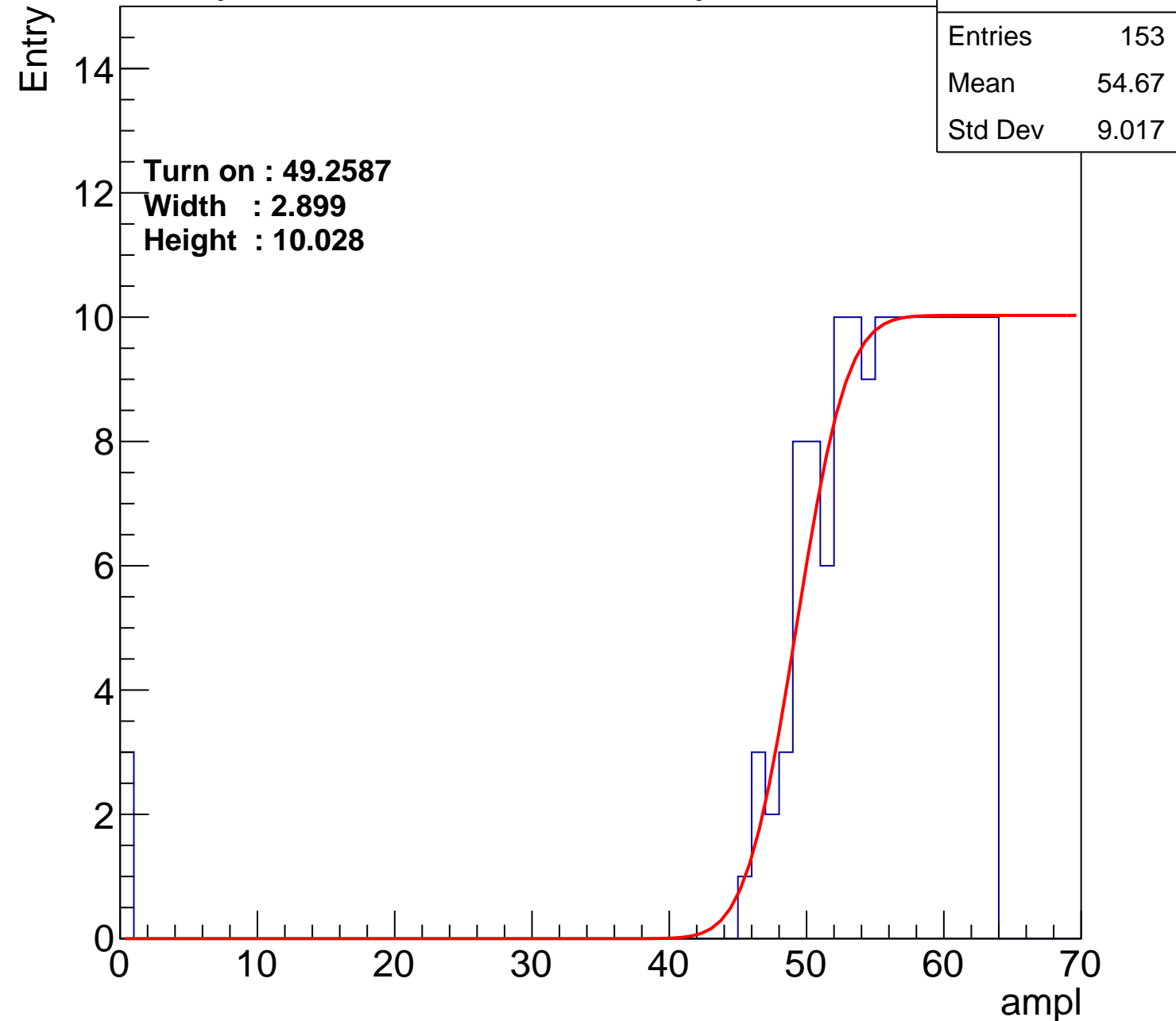
Width : 2.899

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch105

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	54.21
Std Dev	12.25

Turn on : 50.9080

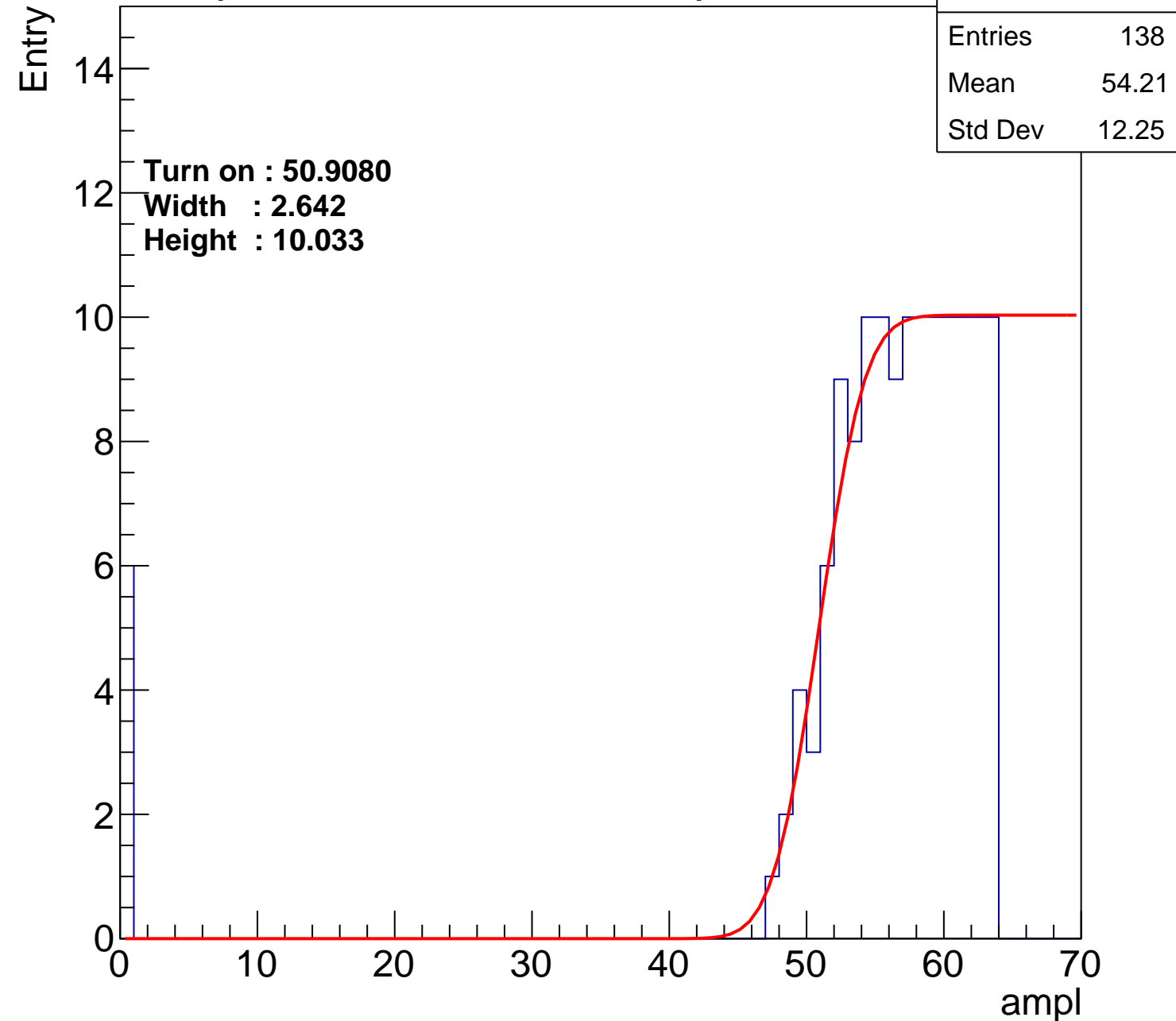
Width : 2.642

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch106

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.96
Std Dev	9.375

Turn on : 50.6120

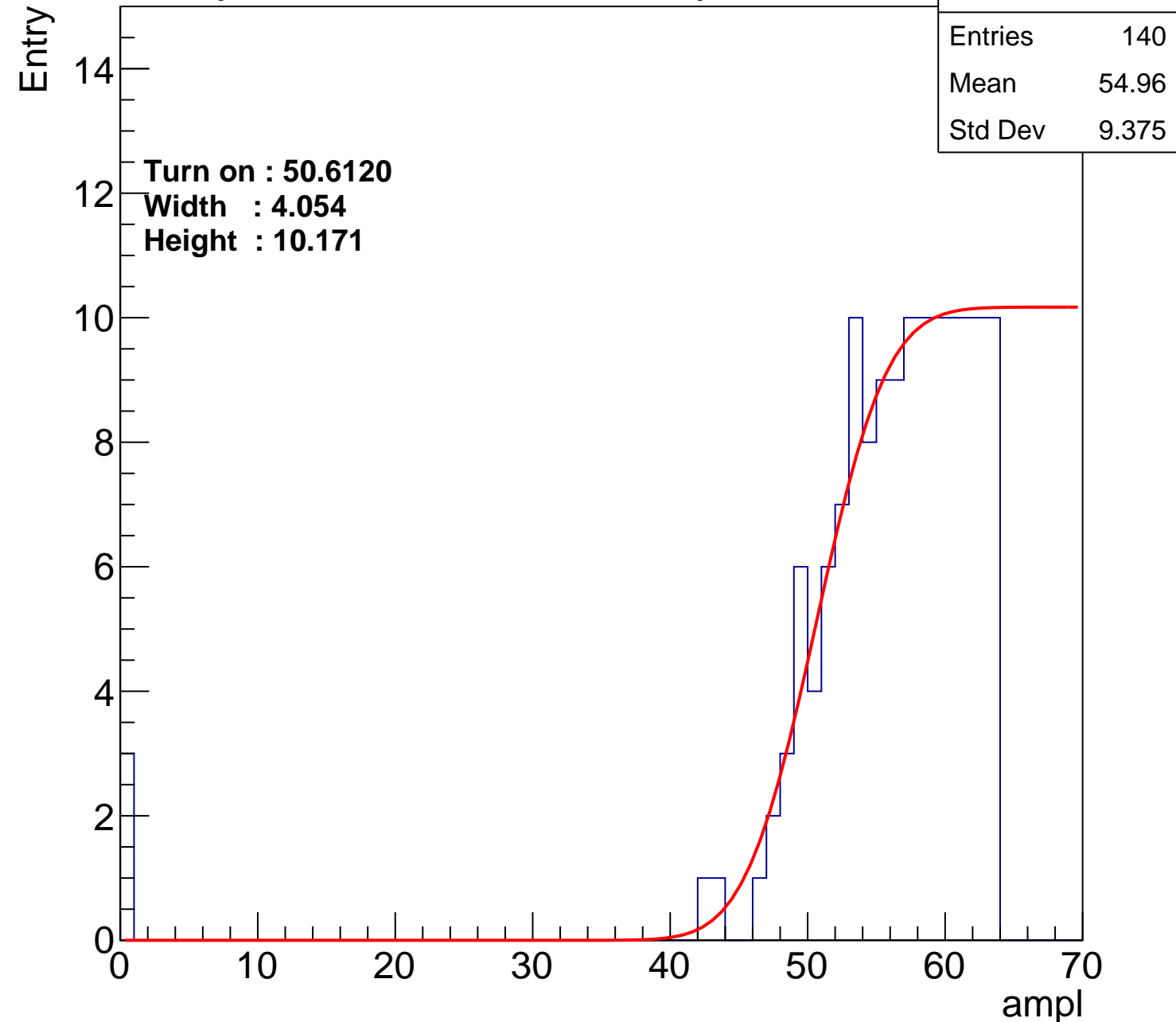
Width : 4.054

Height : 10.171

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch107

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	54.27
Std Dev	11.12

Turn on : 49.8487

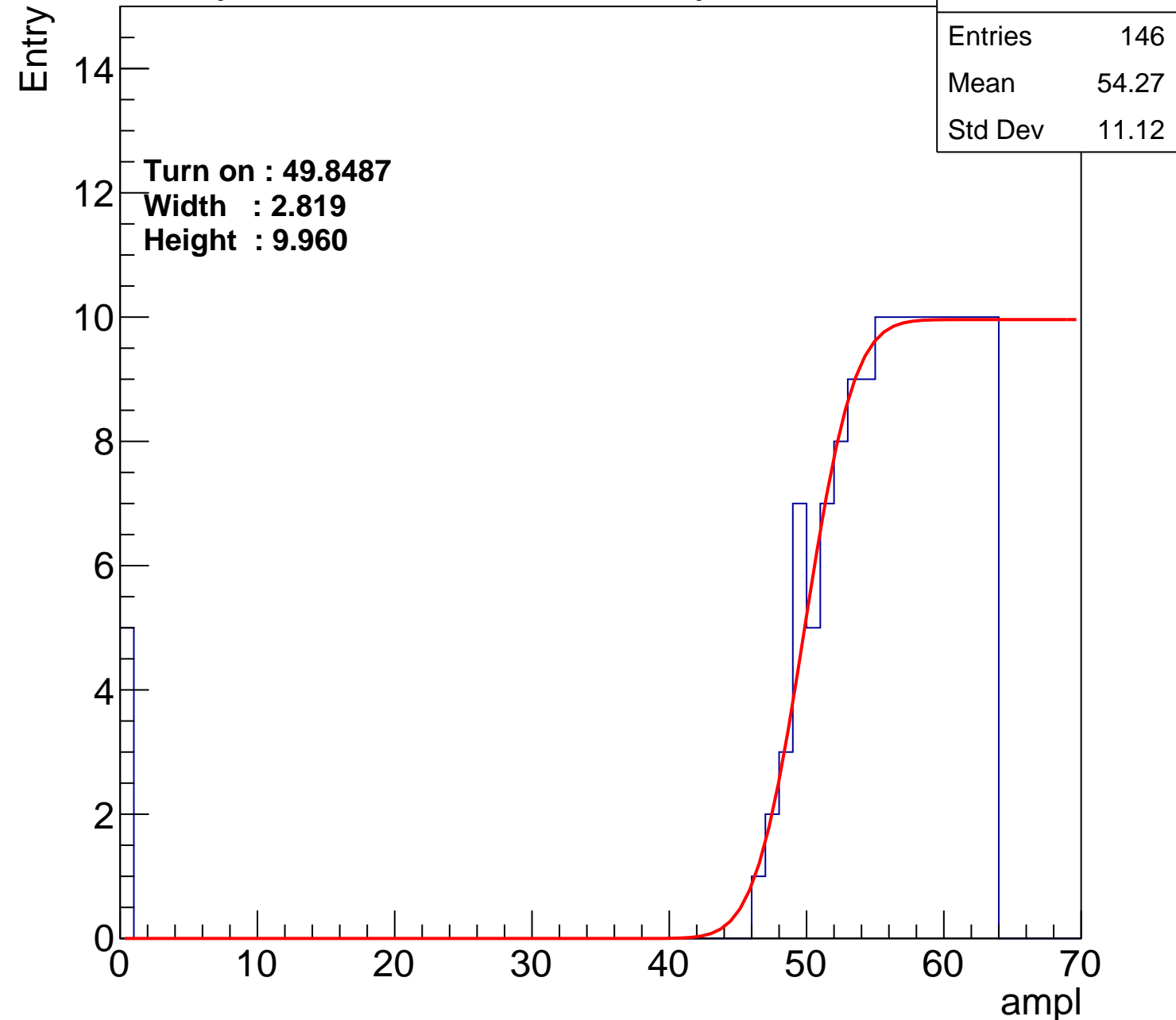
Width : 2.819

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch108

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	54.54
Std Dev	9.071

Turn on : 48.7534

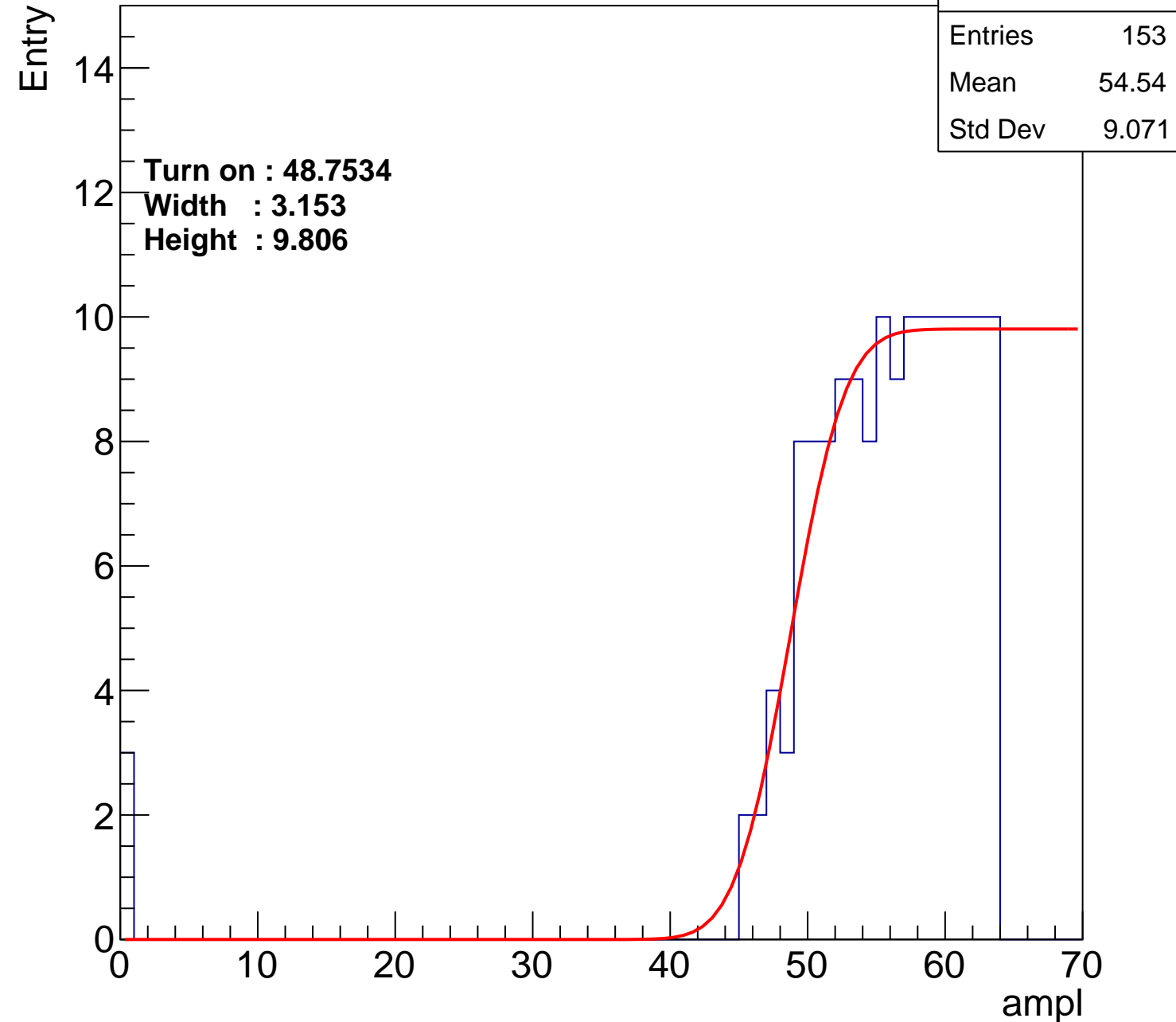
Width : 3.153

Height : 9.806

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch109

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	54.62
Std Dev	11.48

Turn on : 51.0904

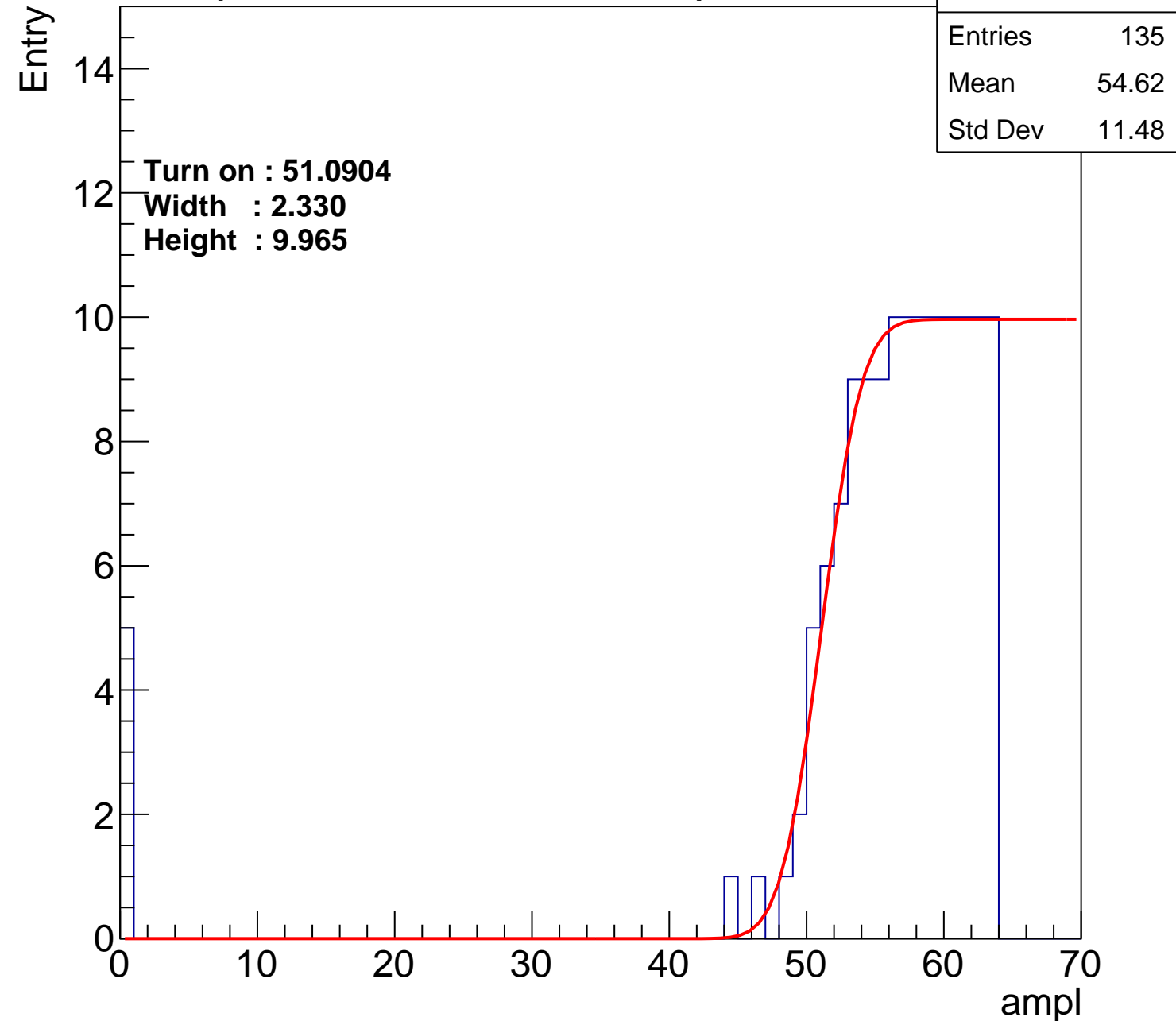
Width : 2.330

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch110

calib_packv5_040323_1717.root, FC#2, port C3

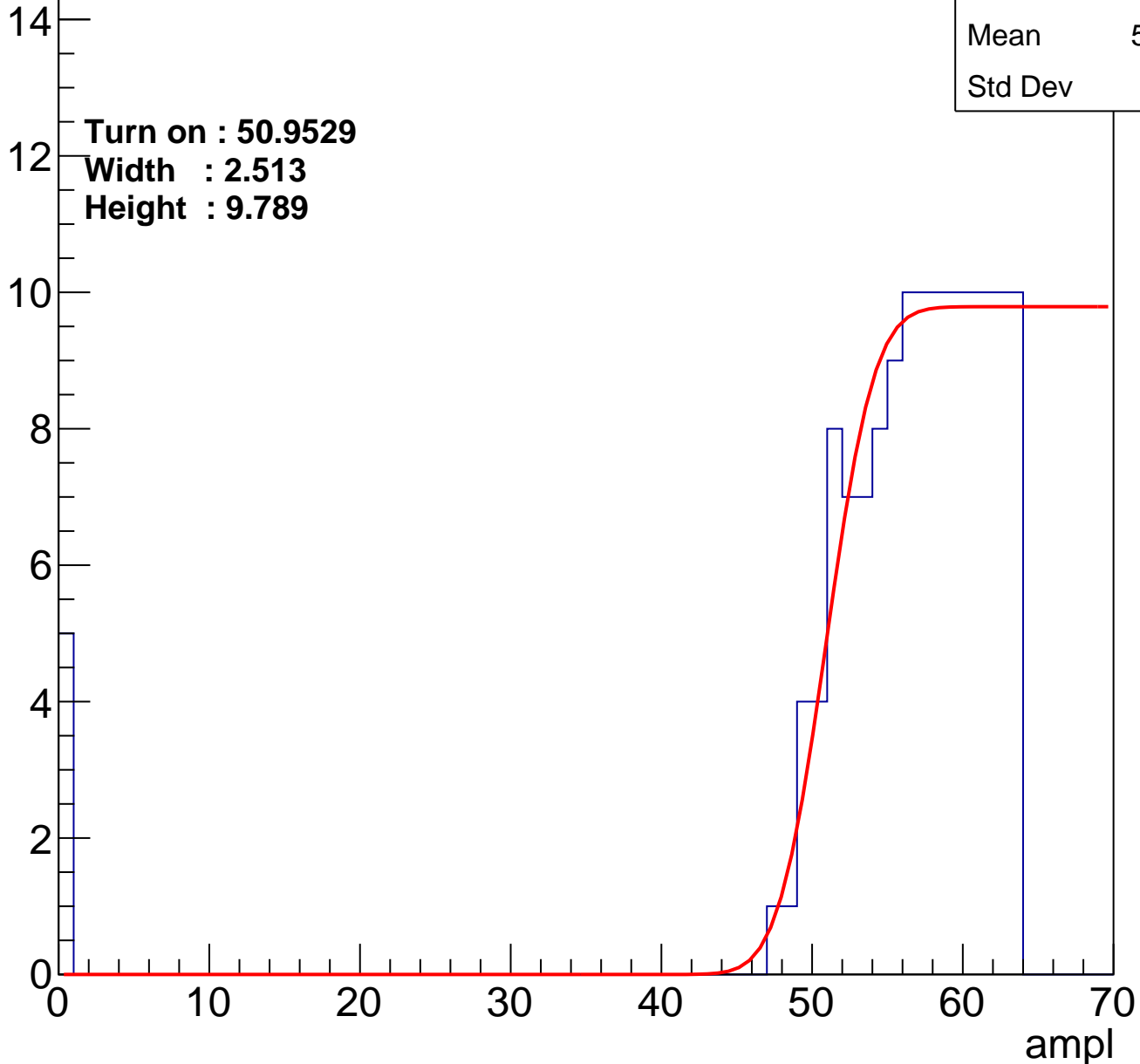
Entries	134
Mean	54.63
Std Dev	11.5

Turn on : 50.9529

Width : 2.513

Height : 9.789

Entry



B0L103S, U14-ch111

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.86
Std Dev	9.134

Turn on : 49.7393

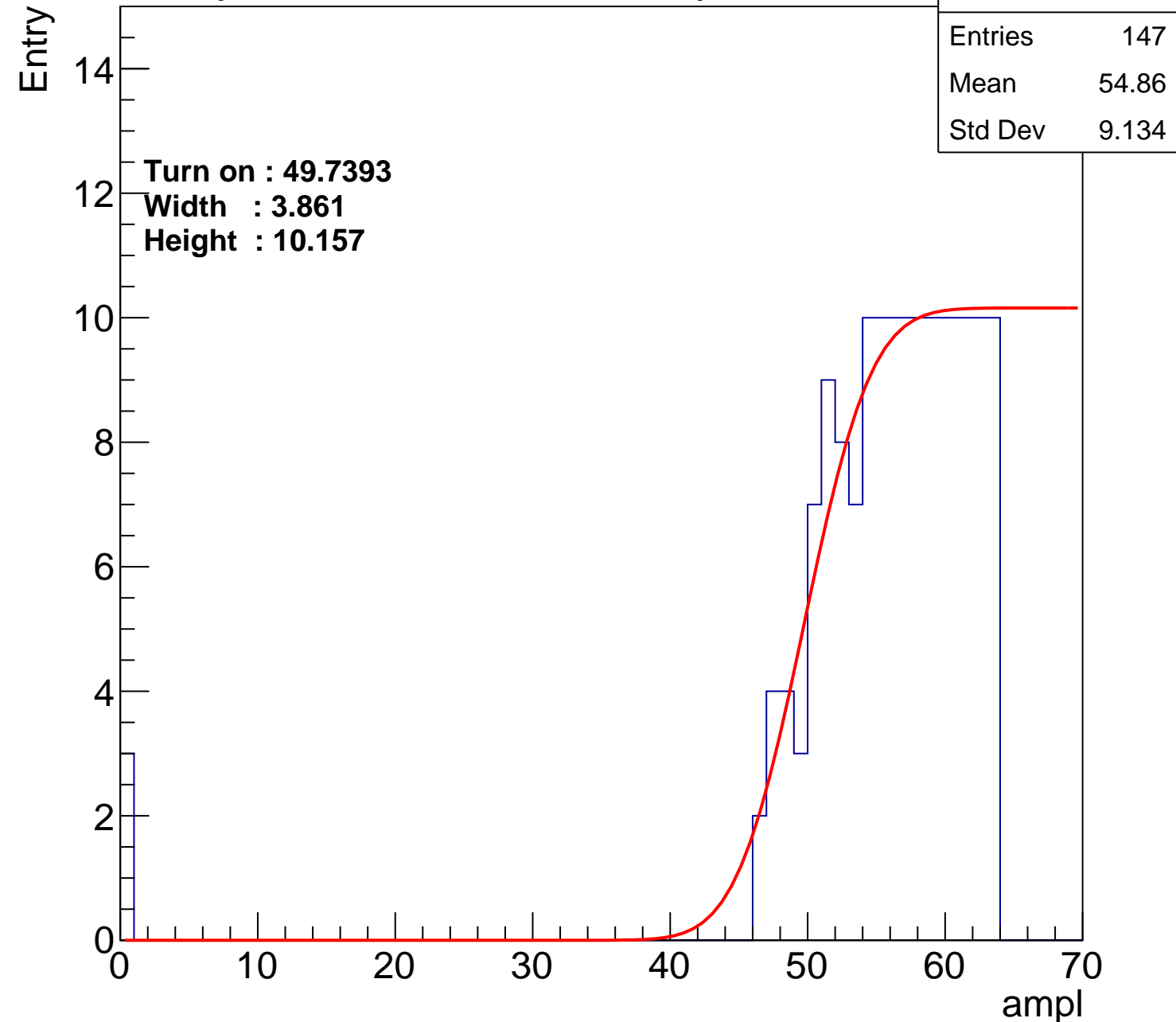
Width : 3.861

Height : 10.157

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch112

calib_packv5_040323_1717.root, FC#2, port C3

Entries	155
Mean	54.34
Std Dev	9.964

Turn on : 49.0954

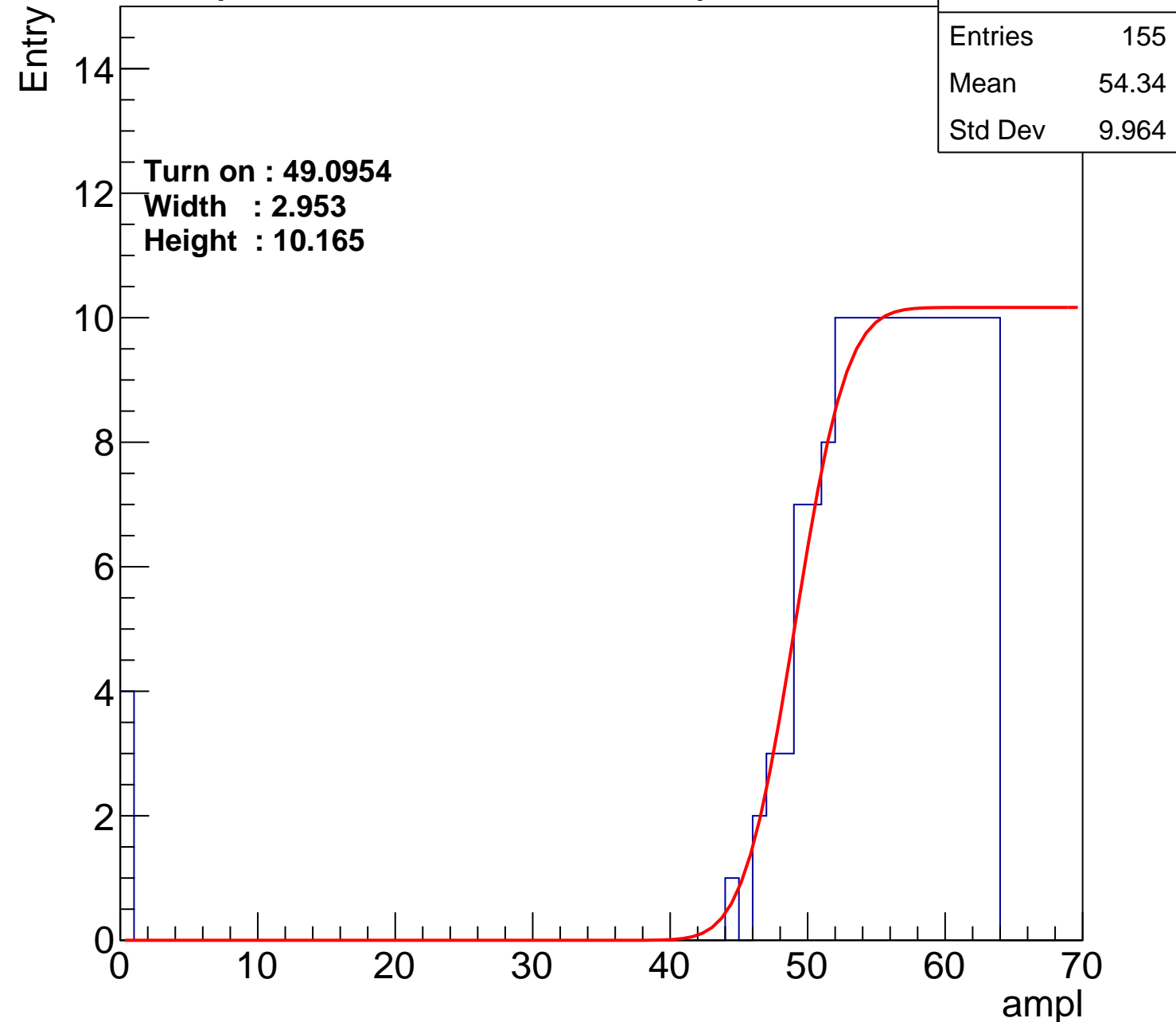
Width : 2.953

Height : 10.165

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch113

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	54.44
Std Dev	10.14

Turn on : 49.3098

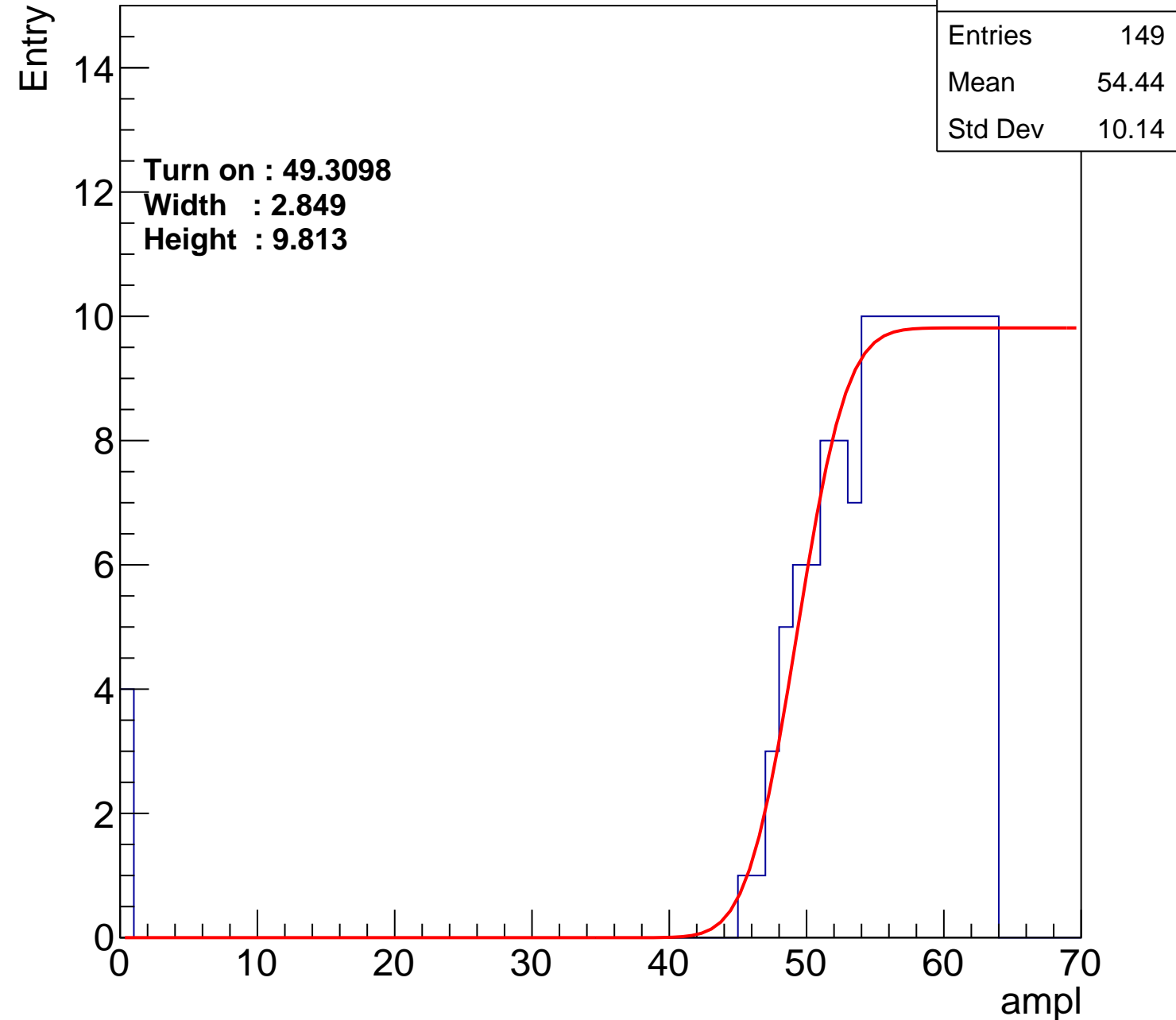
Width : 2.849

Height : 9.813

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch114

calib_packv5_040323_1717.root, FC#2, port C3

Entries	156
Mean	54.54
Std Dev	9.01

Turn on : 49.2361

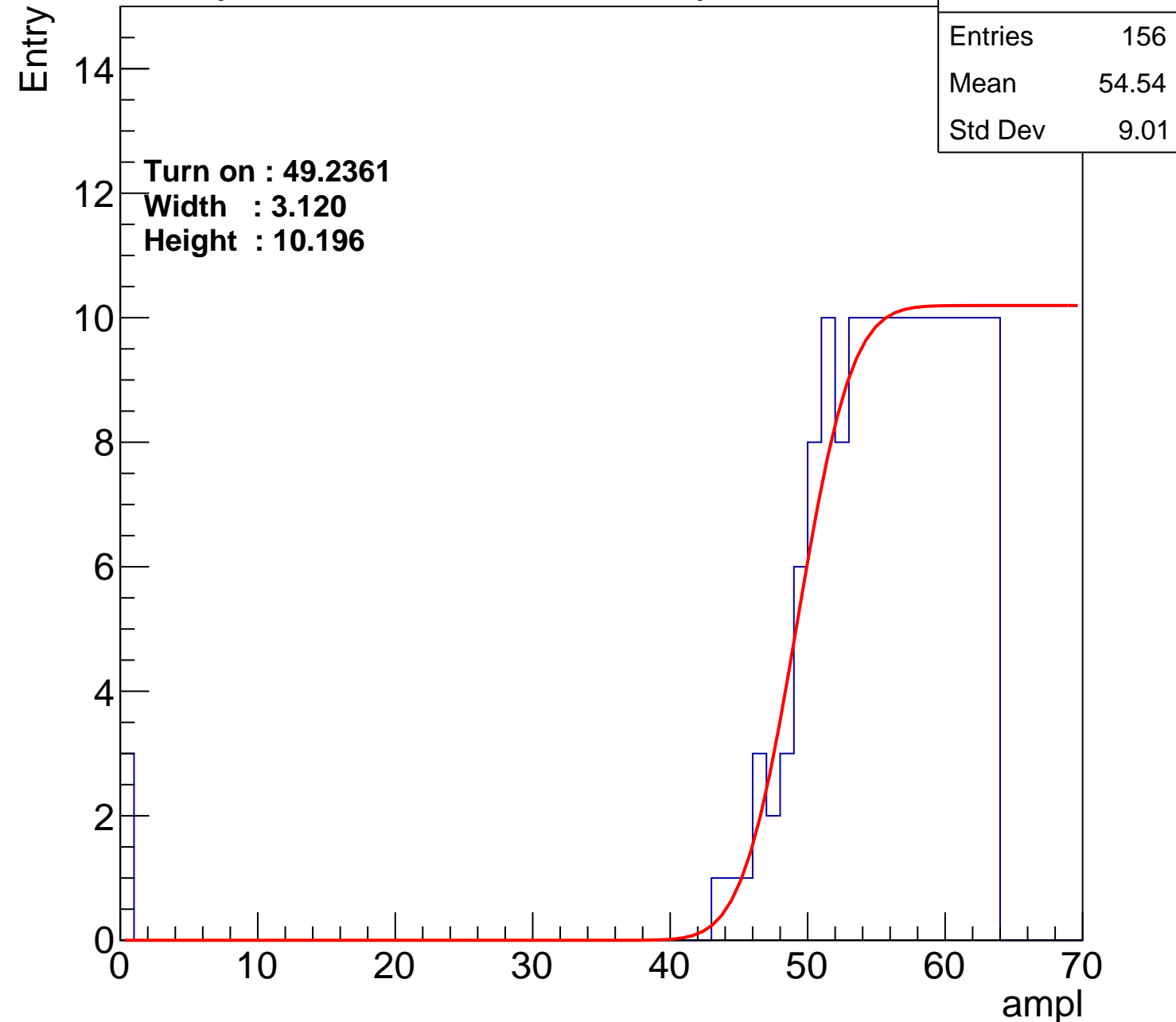
Width : 3.120

Height : 10.196

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch115

calib_packv5_040323_1717.root, FC#2, port C3

Entries	160
Mean	54.11
Std Dev	9.888

Turn on : 48.1615

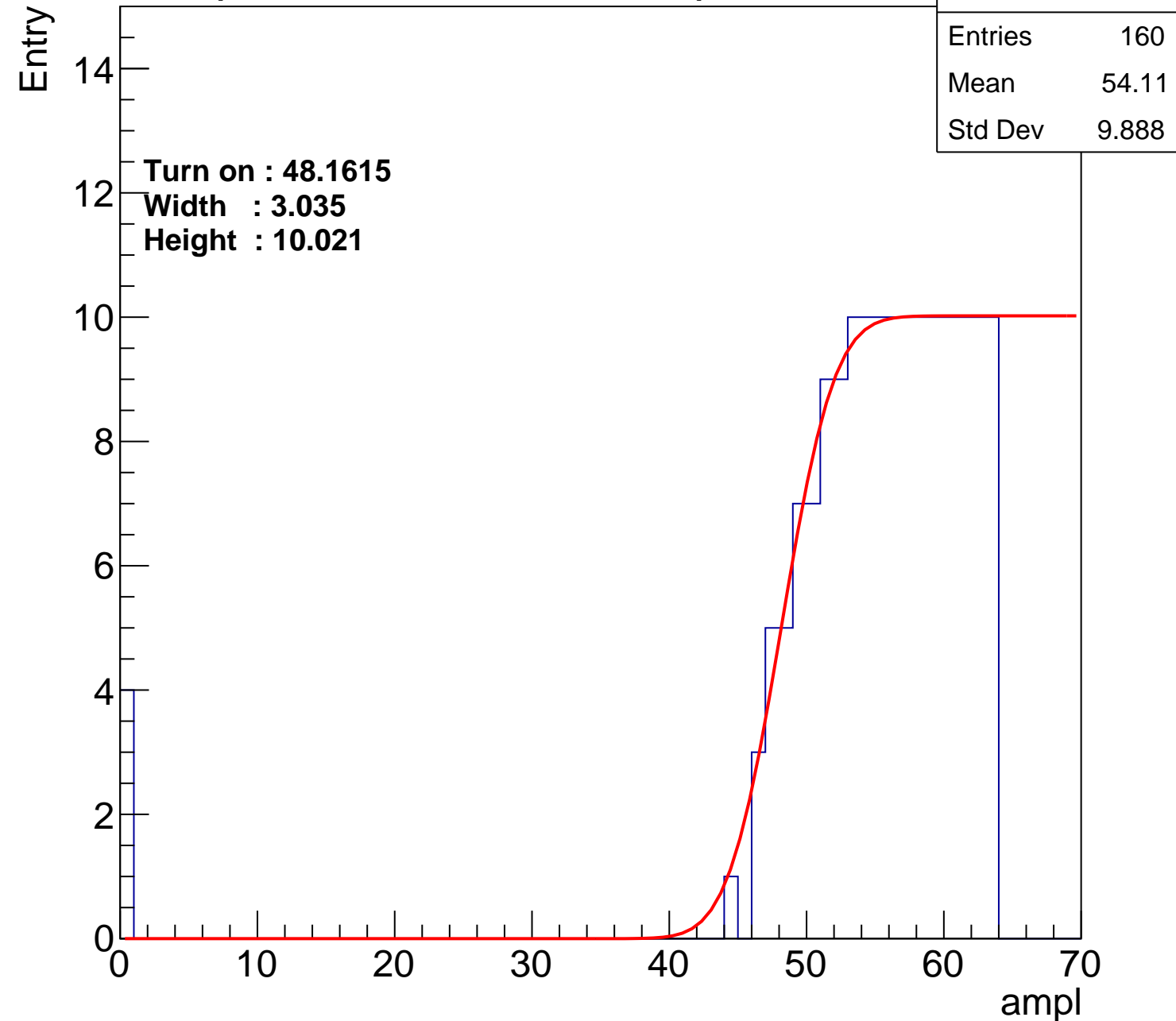
Width : 3.035

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch116

calib_packv5_040323_1717.root, FC#2, port C3

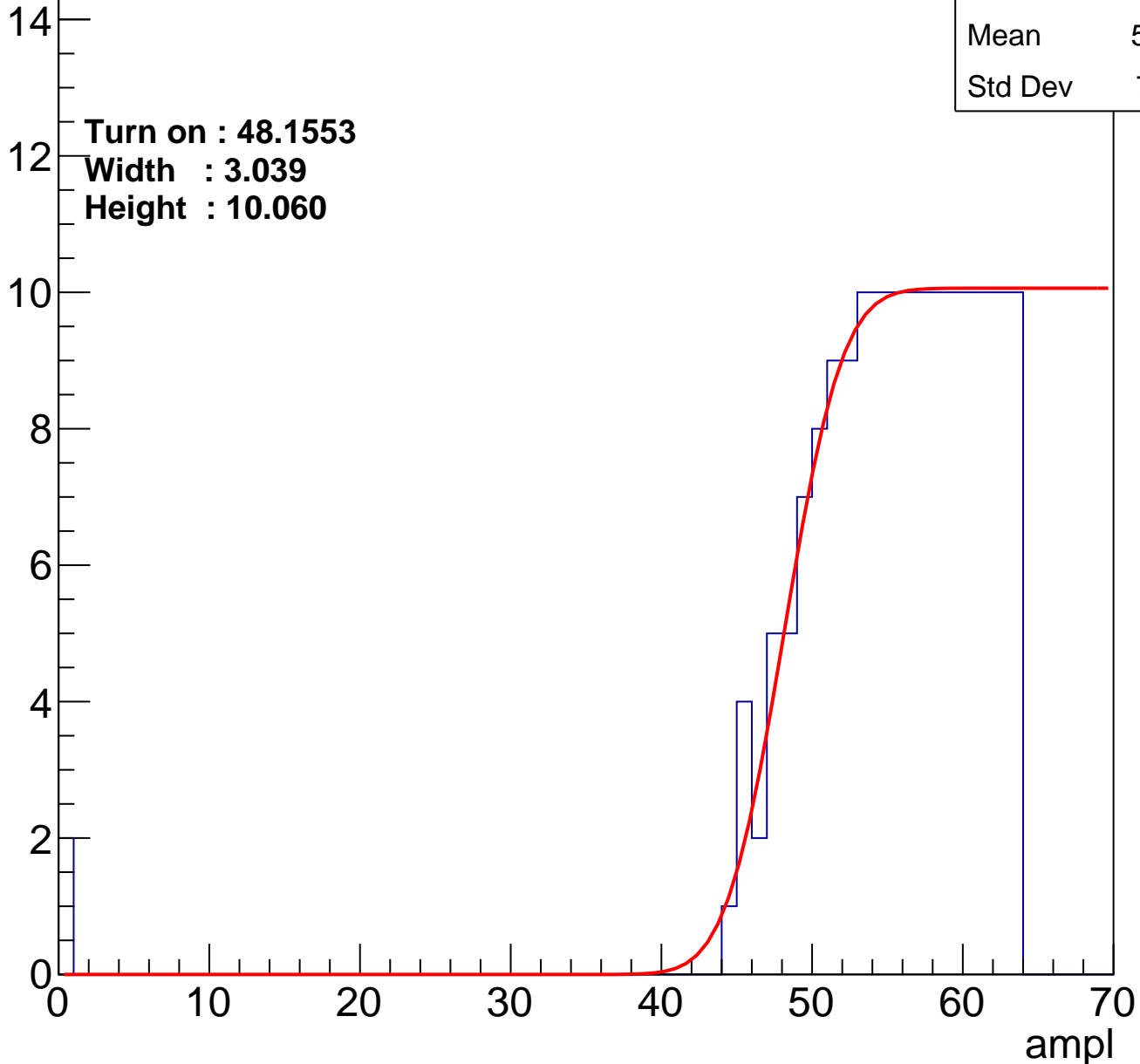
Entries	162
Mean	54.57
Std Dev	7.871

Turn on : 48.1553

Width : 3.039

Height : 10.060

Entry



B0L103S, U14-ch117

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.26
Std Dev	8.002

Turn on : 50.2780

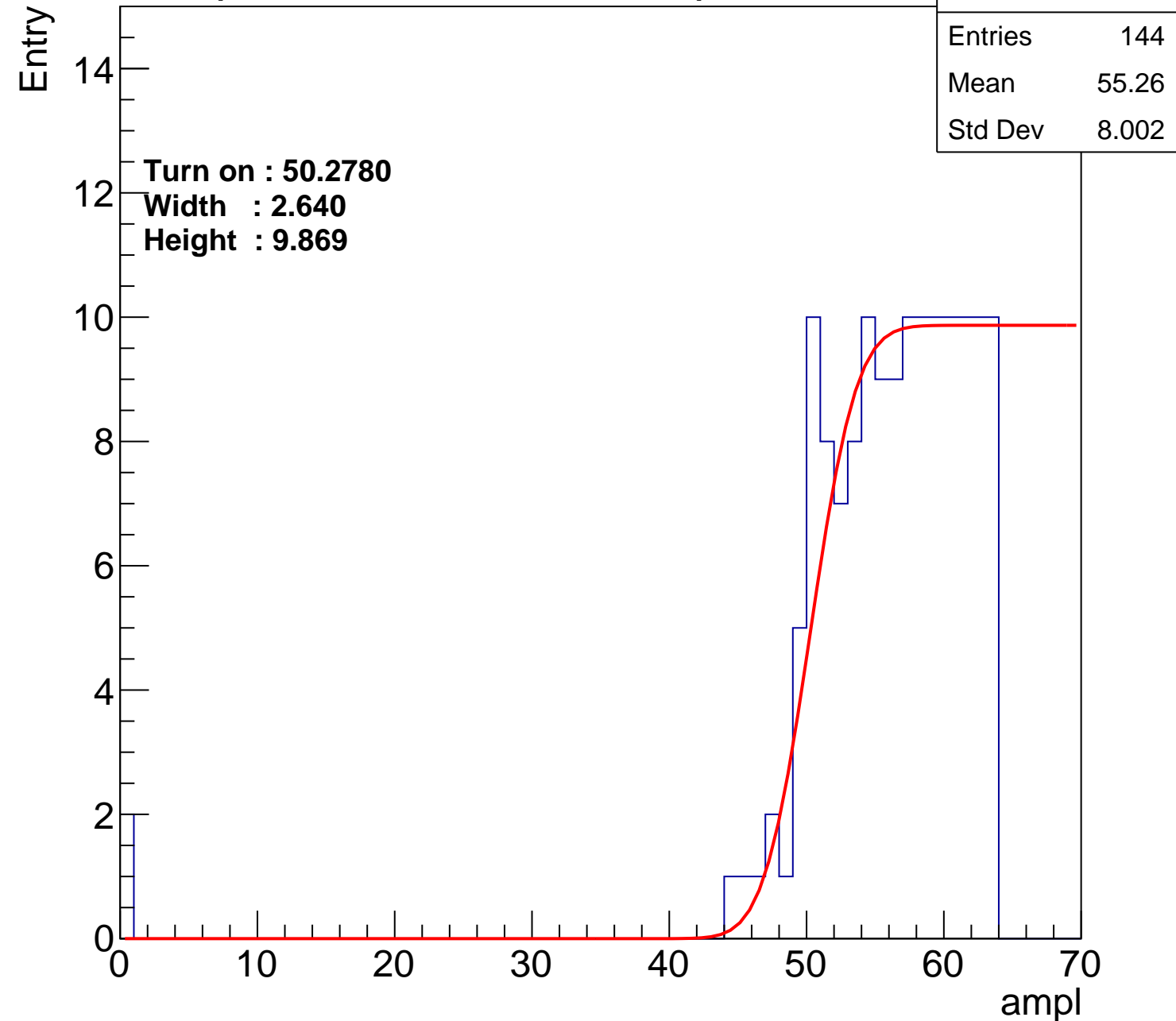
Width : 2.640

Height : 9.869

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch118

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	54.72
Std Dev	8.087

Turn on : 49.1639

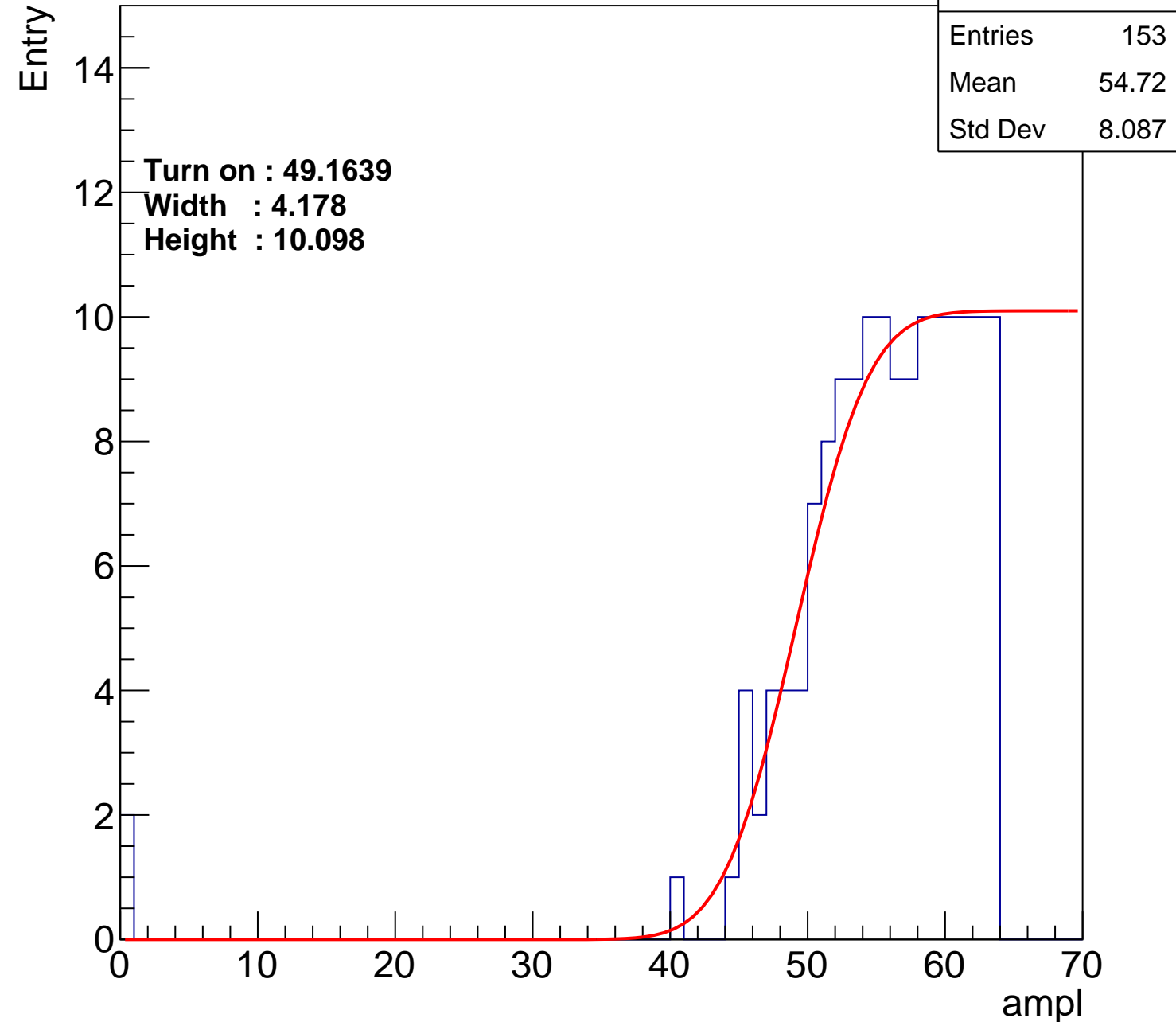
Width : 4.178

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch119

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	55.68
Std Dev	6.564

Turn on : 50.4003

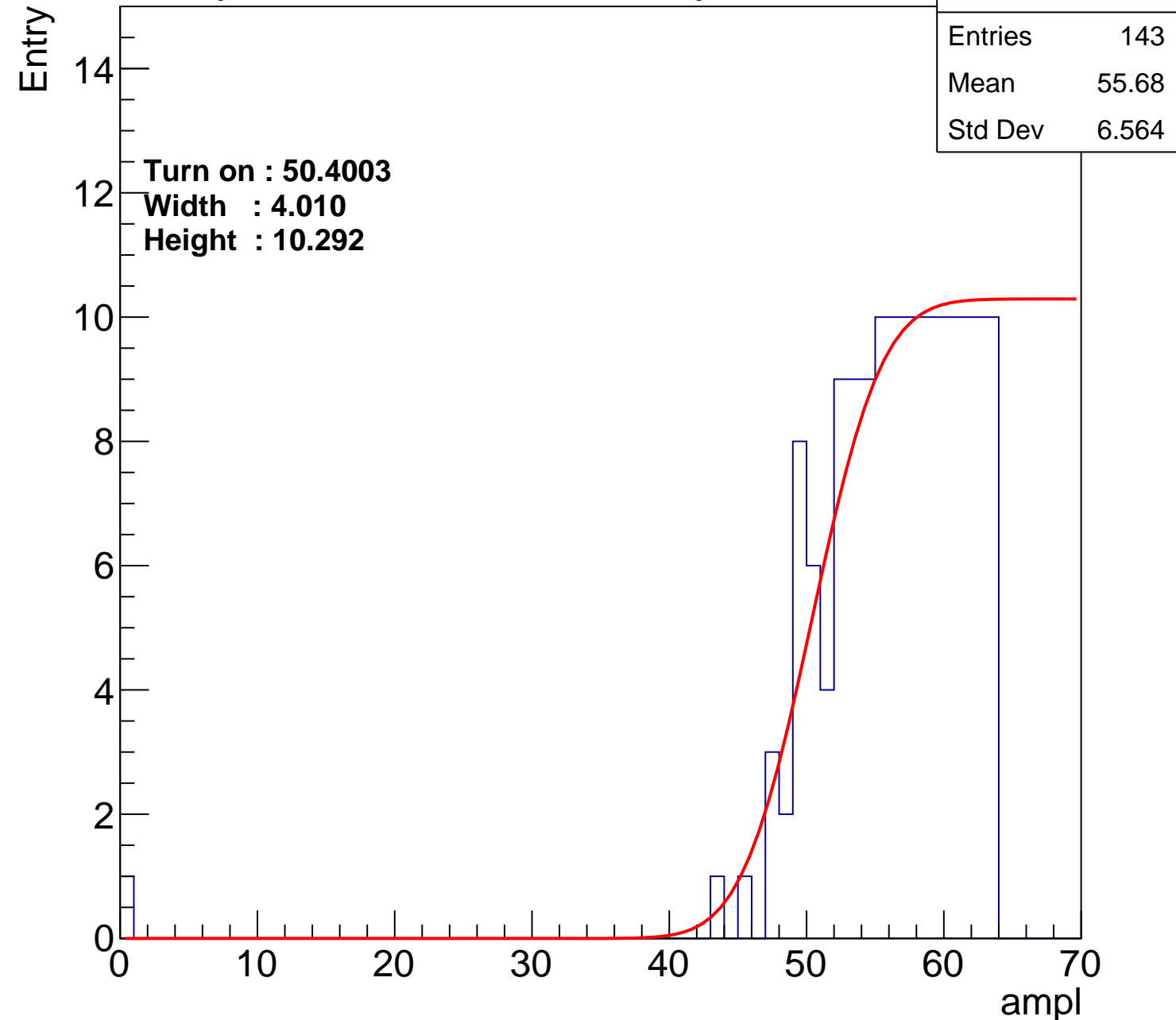
Width : 4.010

Height : 10.292

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch120

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	55.78
Std Dev	8.149

Turn on : 51.2195

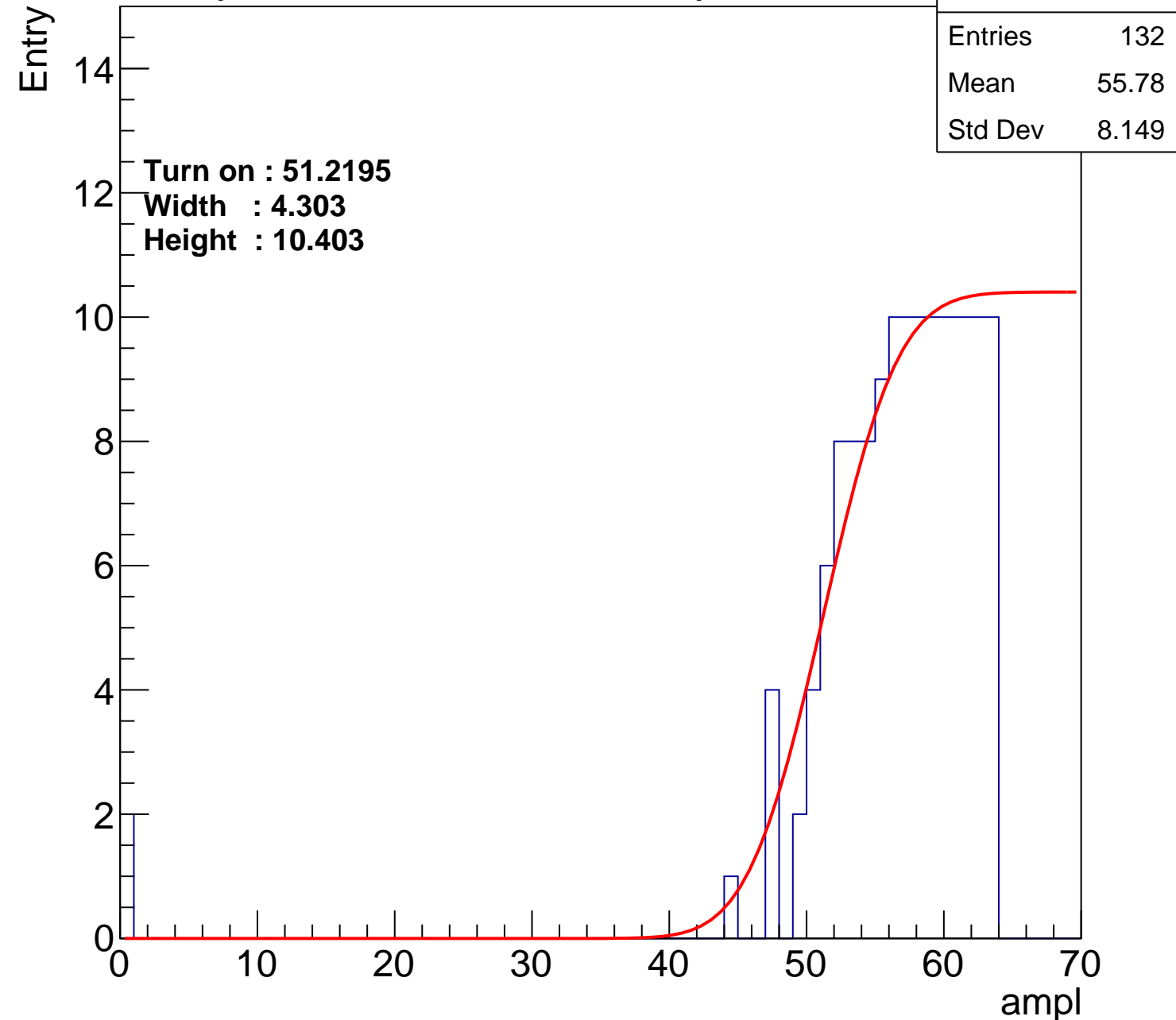
Width : 4.303

Height : 10.403

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch121

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	54.78
Std Dev	10.26

Turn on : 49.9307

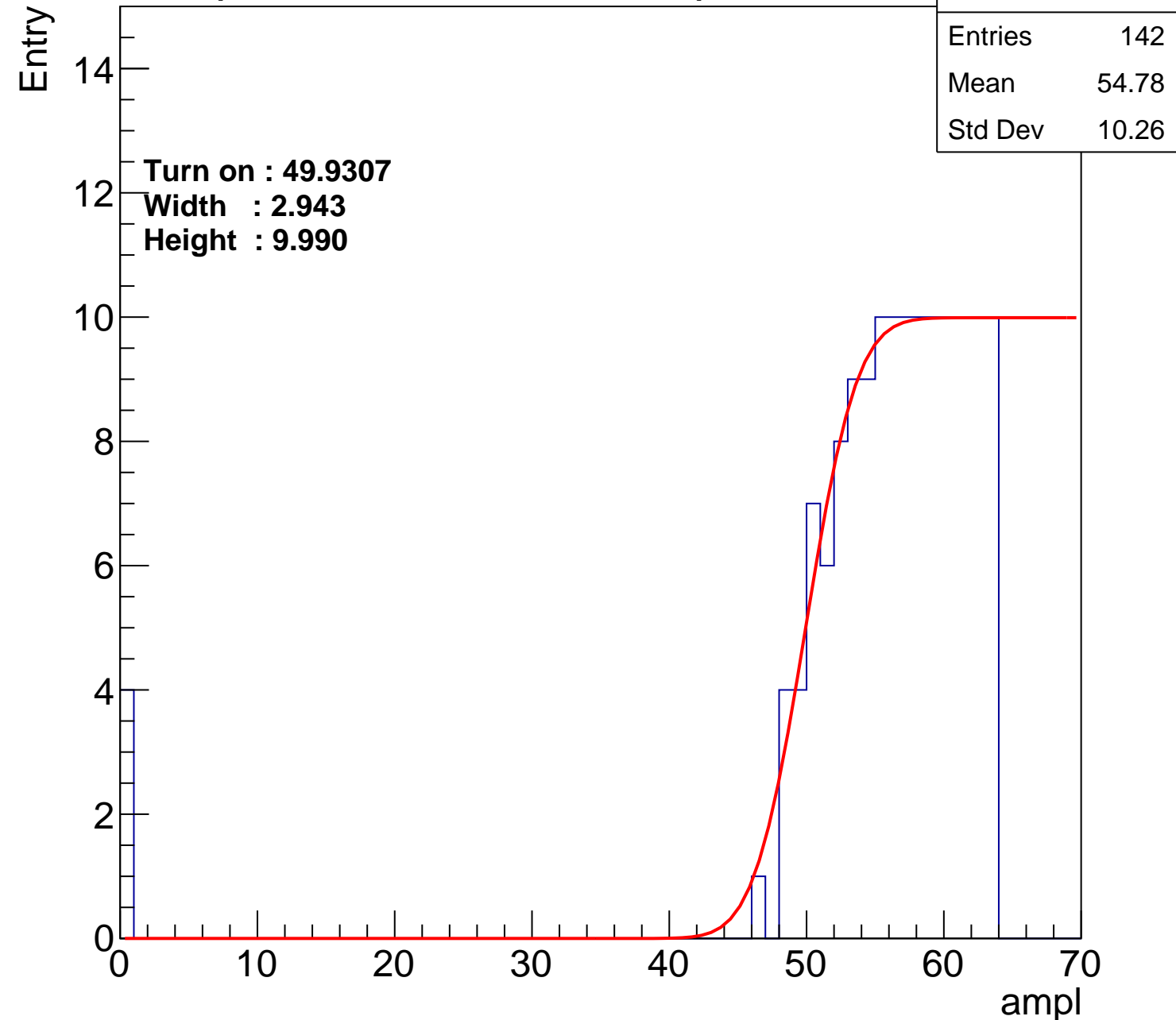
Width : 2.943

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch122

calib_packv5_040323_1717.root, FC#2, port C3

Entries	156
Mean	54.47
Std Dev	9.043

Turn on : 48.3249

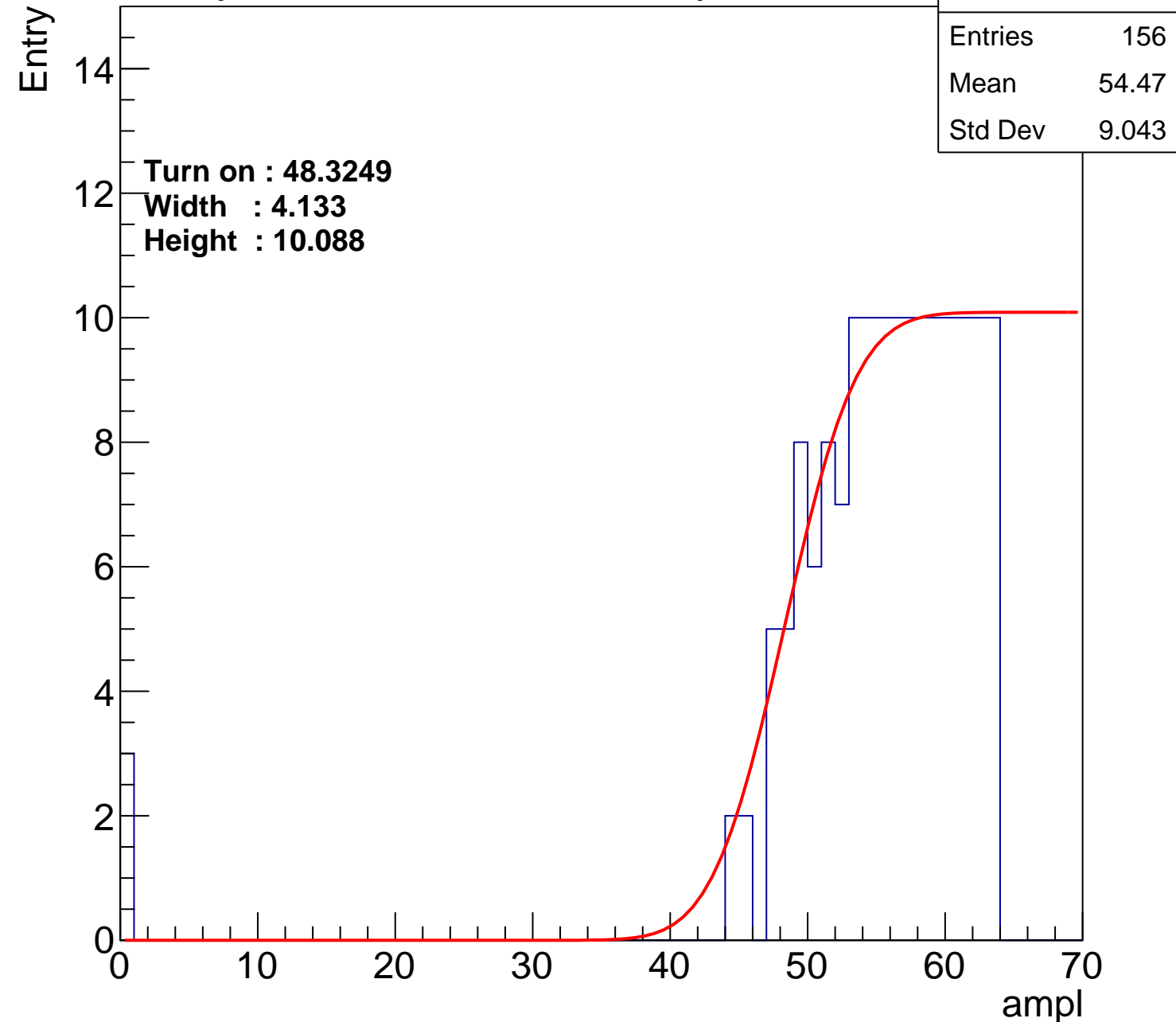
Width : 4.133

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch123

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.29
Std Dev	9.41

Turn on : 51.2677

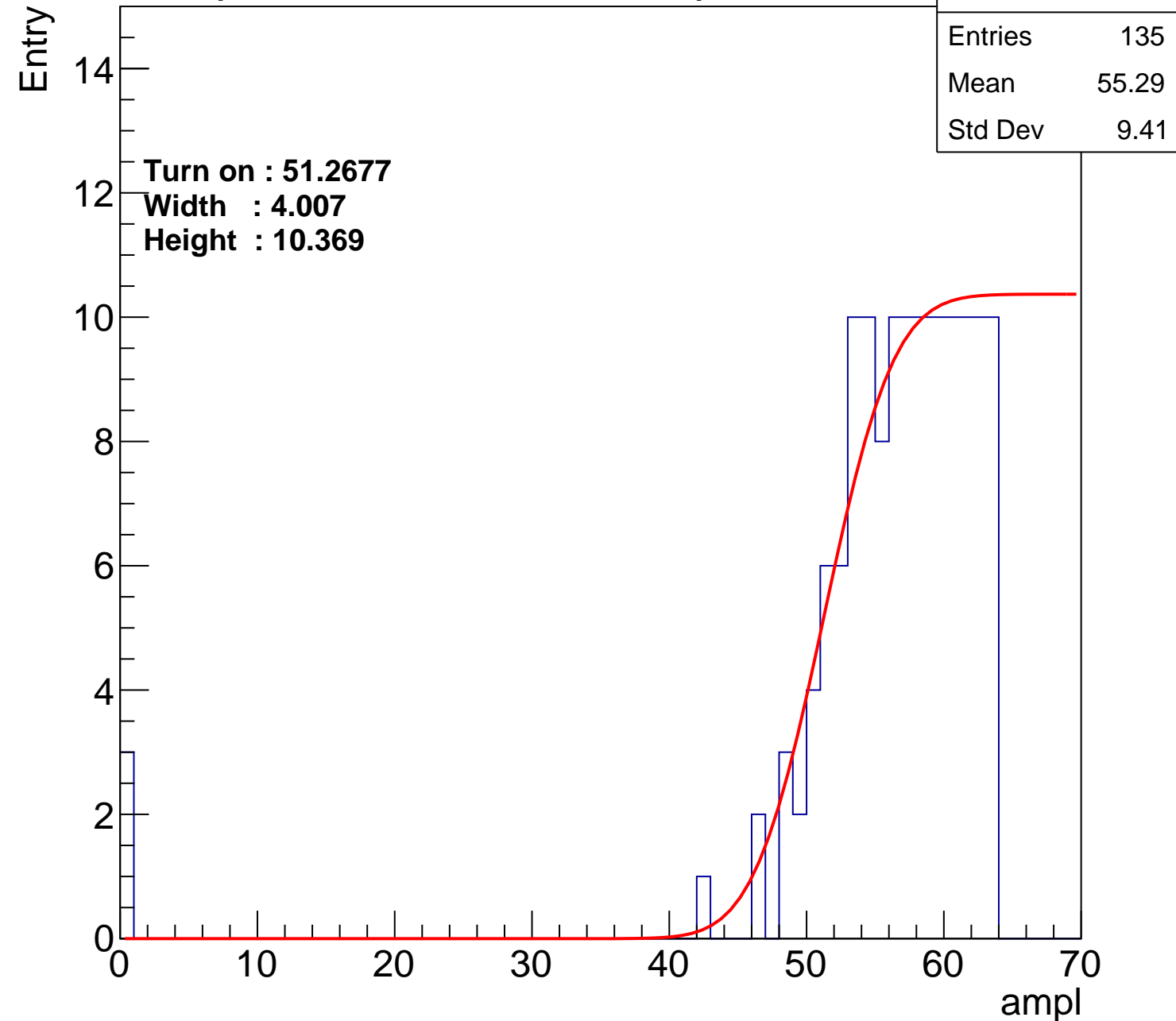
Width : 4.007

Height : 10.369

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch124

calib_packv5_040323_1717.root, FC#2, port C3

Entries	158
Mean	52.89
Std Dev	12.89

Turn on : 49.3174

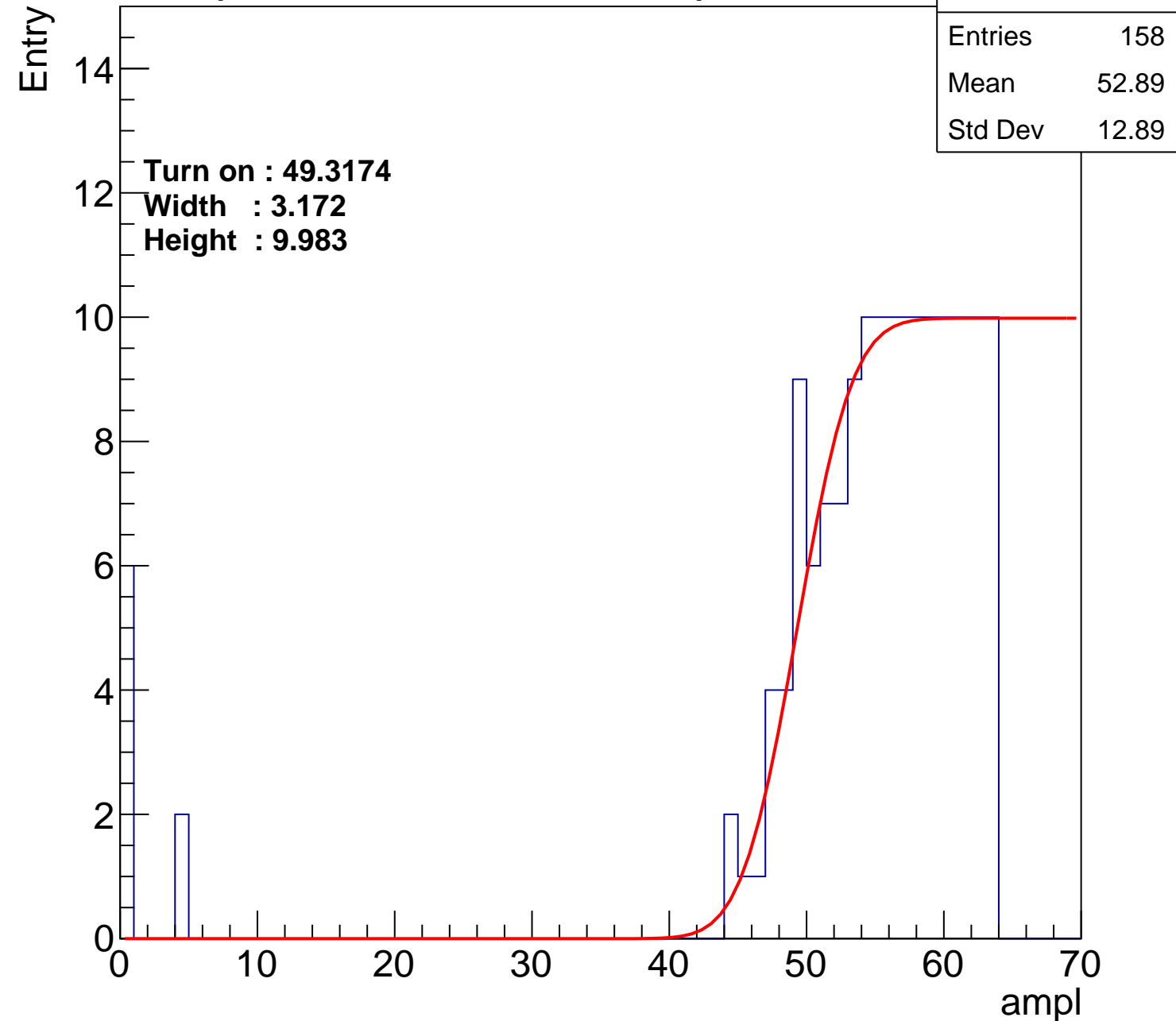
Width : 3.172

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch125

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	54.48
Std Dev	11.39

Turn on : 50.9074

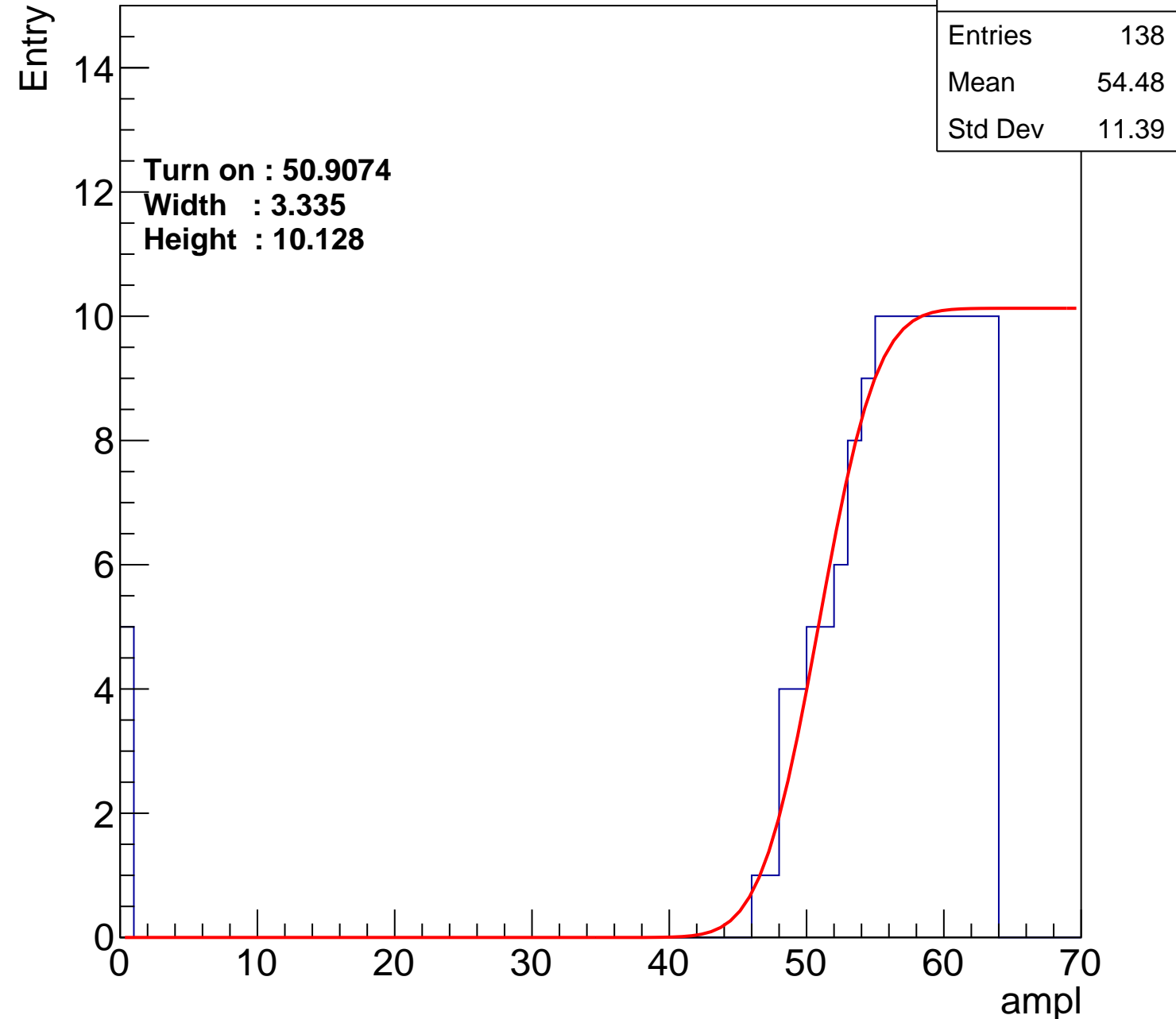
Width : 3.335

Height : 10.128

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch126

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	53.94
Std Dev	11.98

Turn on : 50.5462

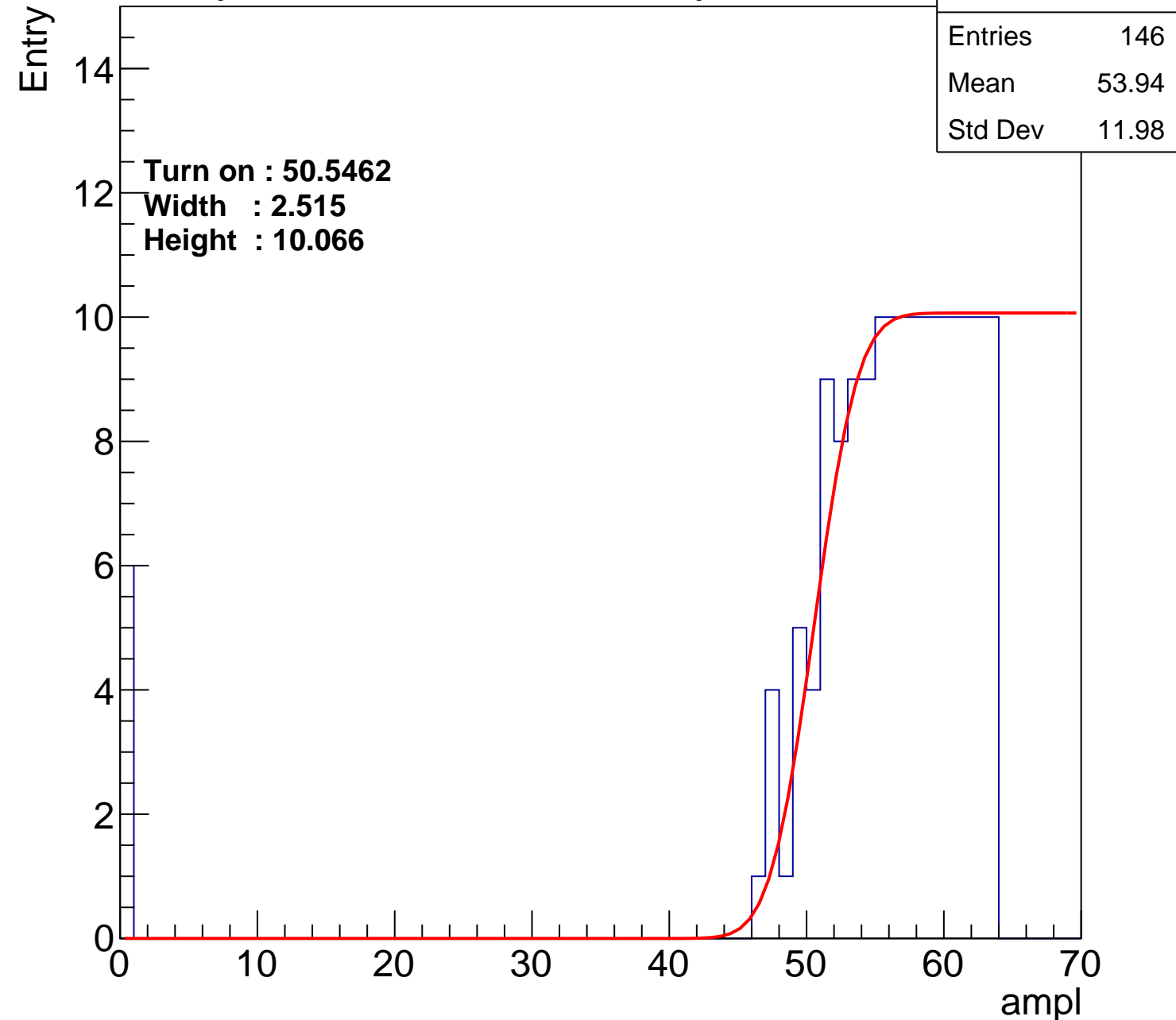
Width : 2.515

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	160
Mean	54.5
Std Dev	8.013

Turn on : 48.7717

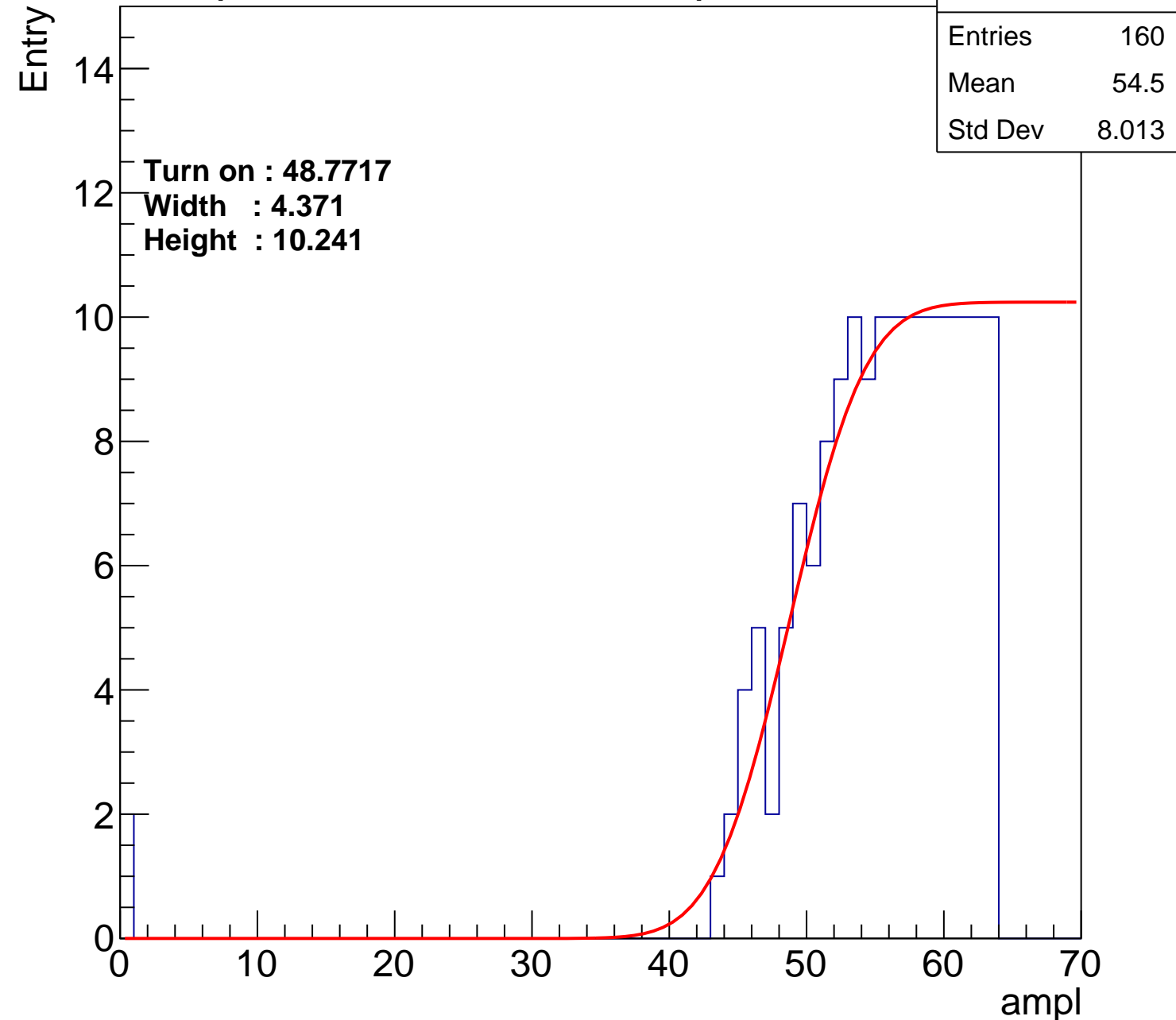
Width : 4.371

Height : 10.241

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U14-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	160
Mean	54.5
Std Dev	8.013

Turn on : 48.7717

Width : 4.371

Height : 10.241

Entry

14
12
10
8
6
4
2
0

ampl

