

B1L104S, U24-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	33.18
Std Dev	28.34

Turn on : 53.6281

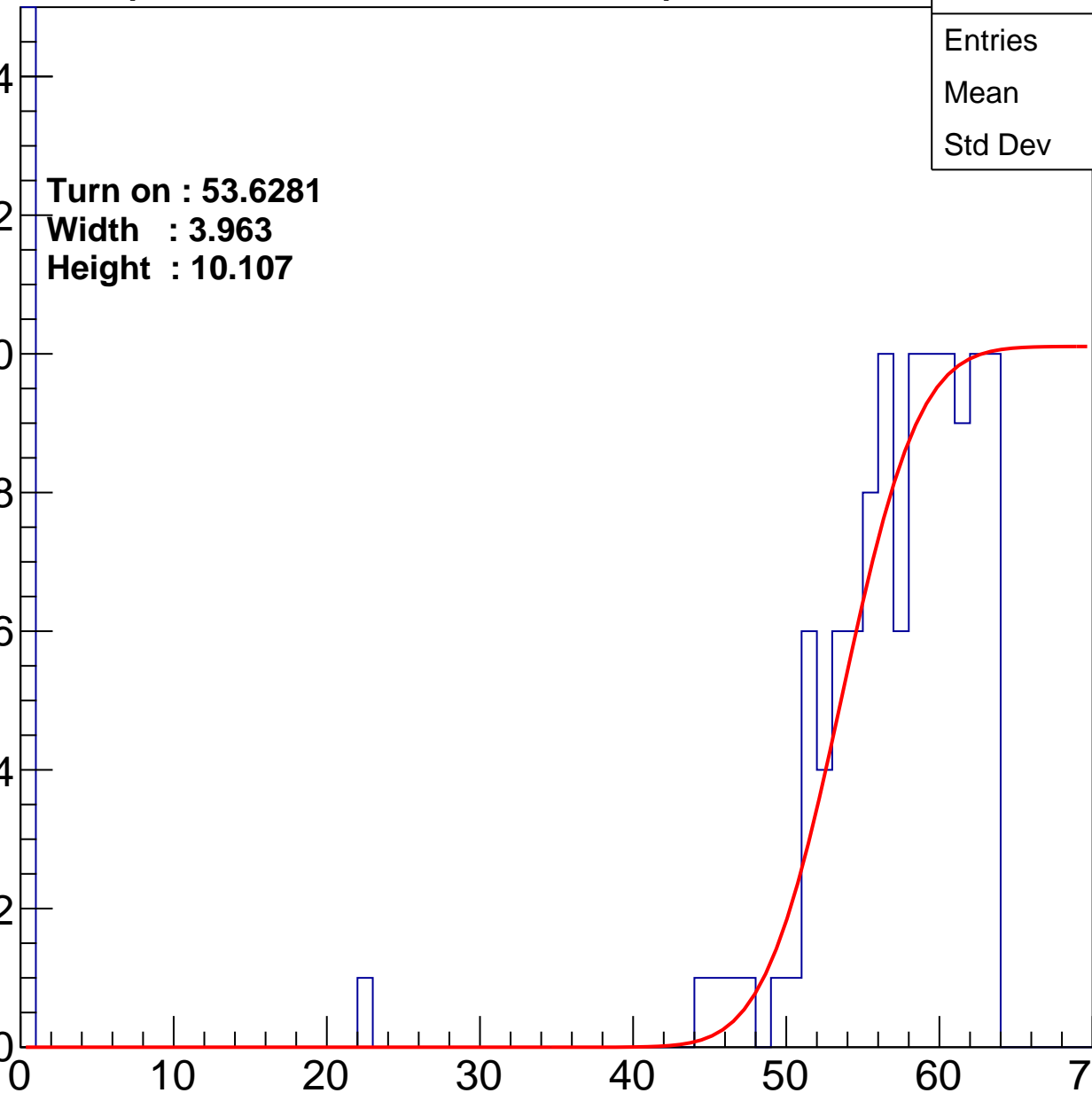
Width : 3.963

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	35.64
Std Dev	28.52

Turn on : 54.8461

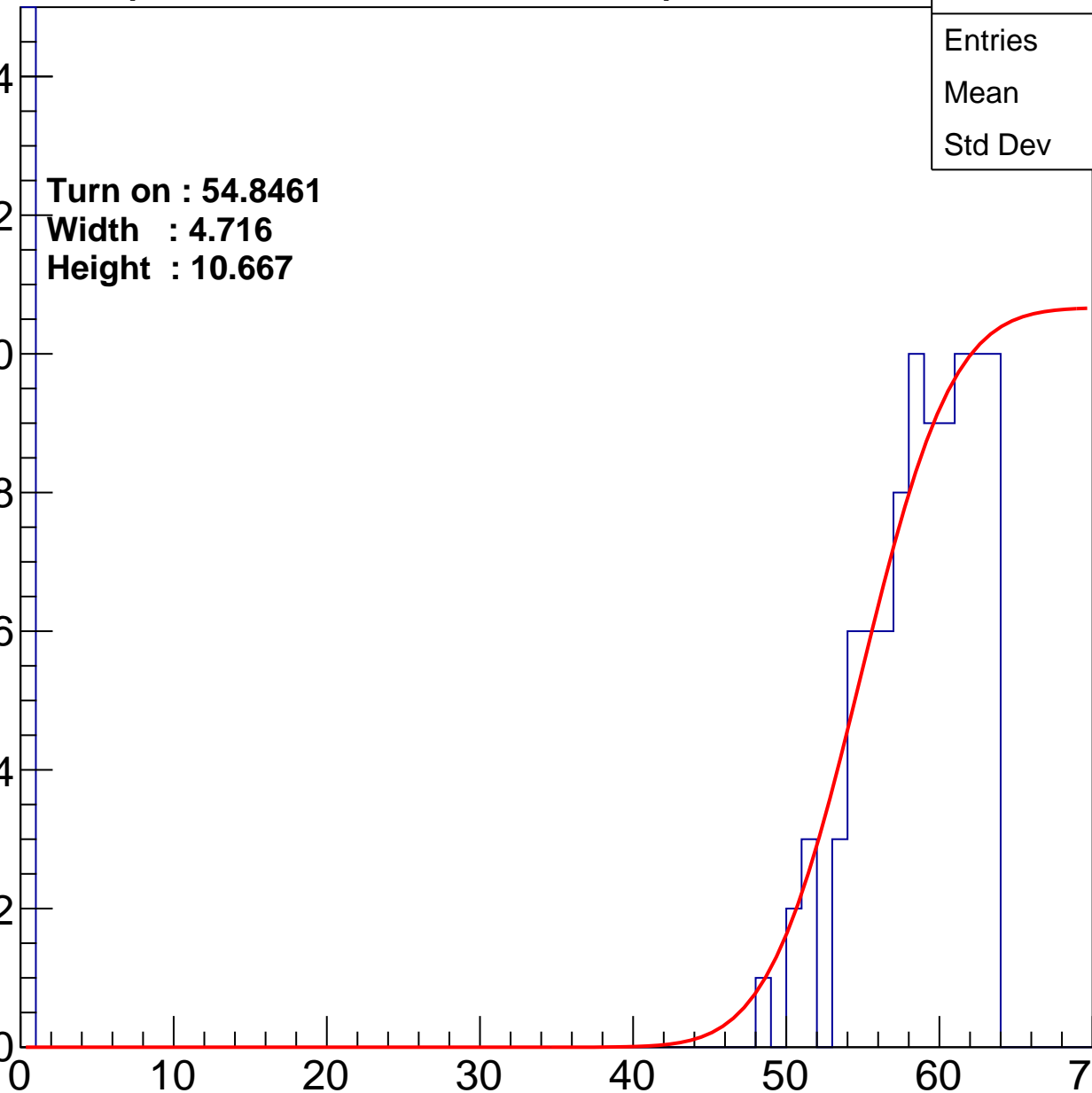
Width : 4.716

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	34.38
Std Dev	28.76

Turn on : 54.3473

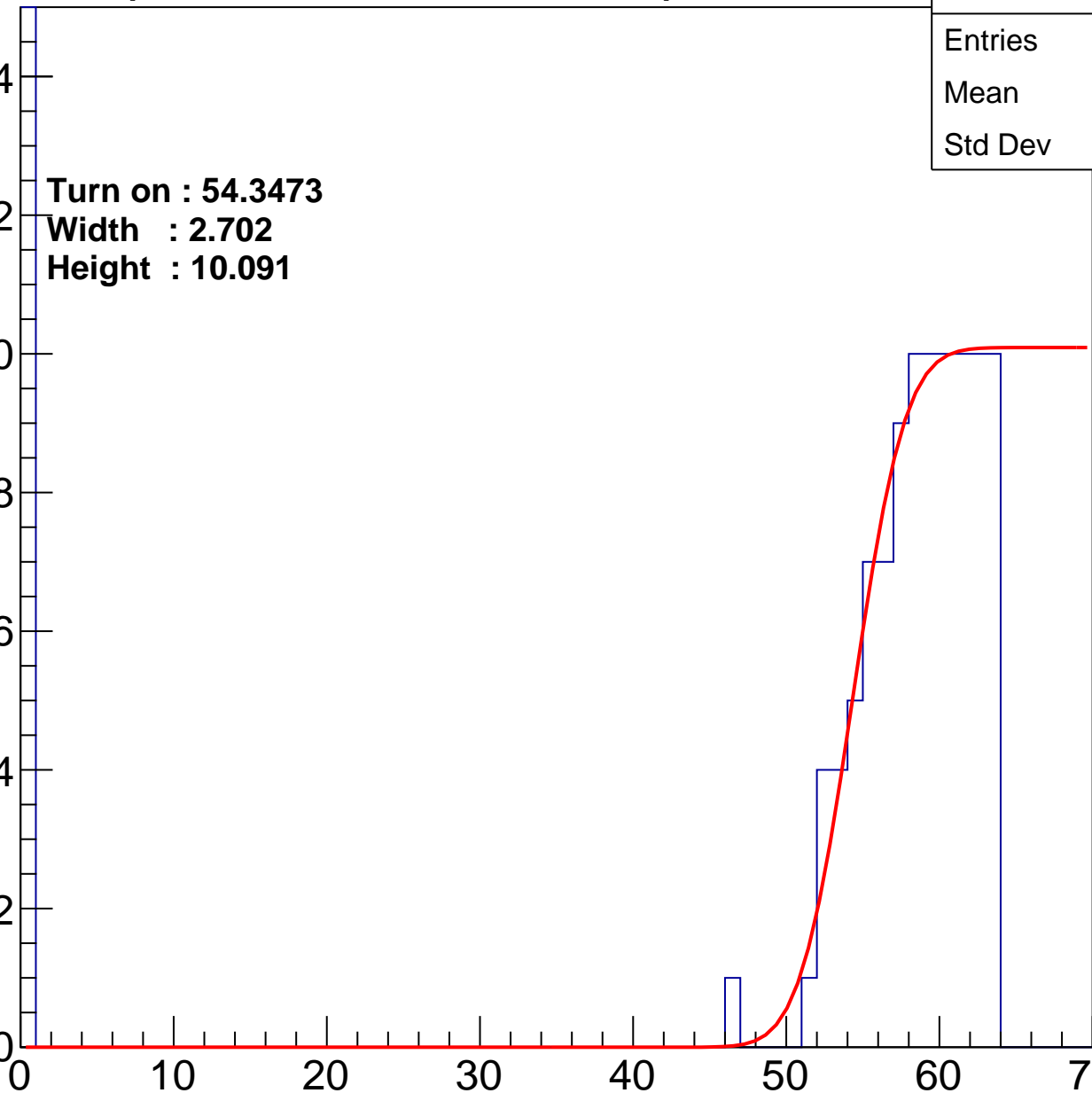
Width : 2.702

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	35.17
Std Dev	27.98

Turn on : 52.2052

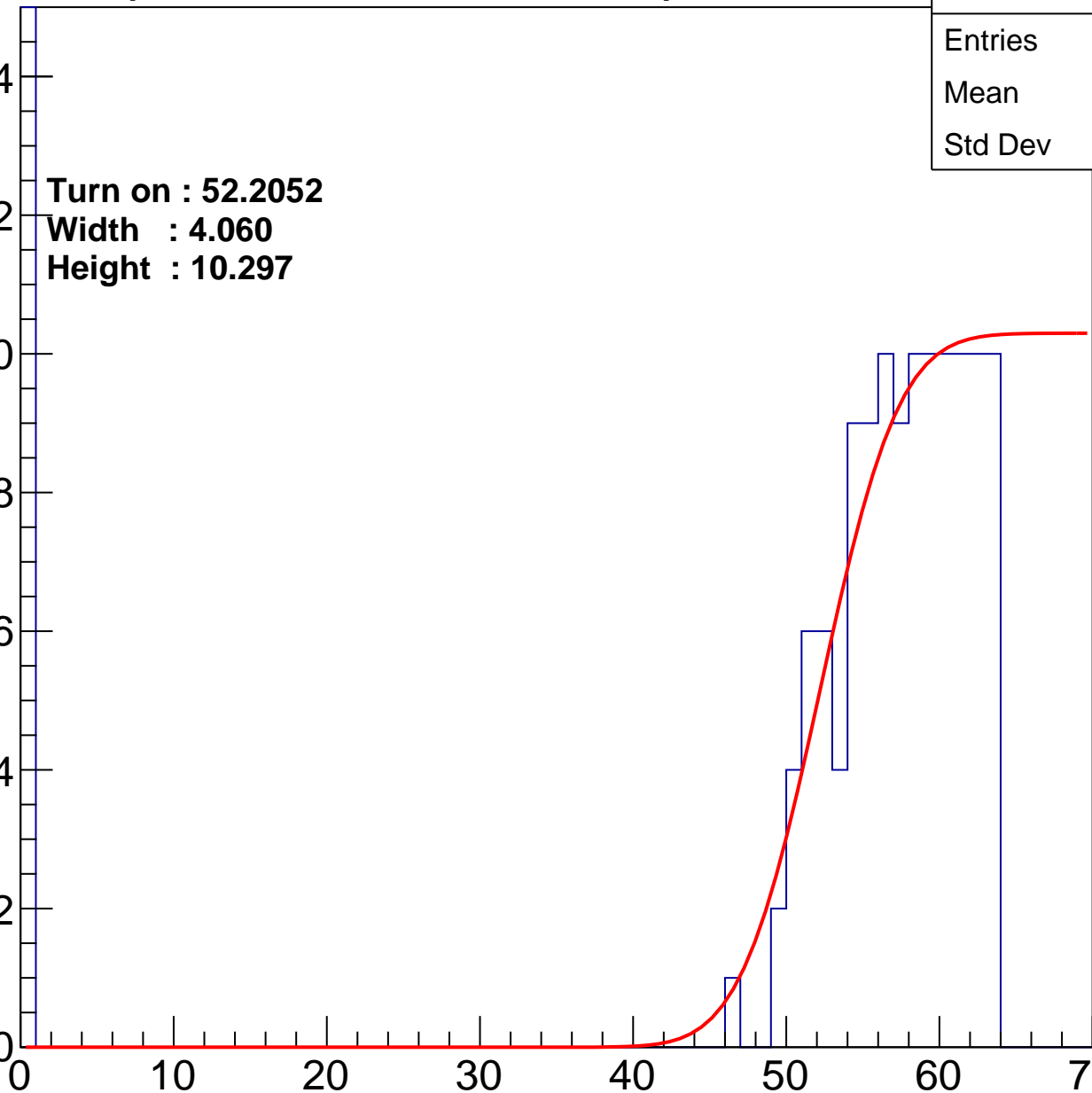
Width : 4.060

Height : 10.297

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	38.53
Std Dev	27.3

Turn on : 53.7848

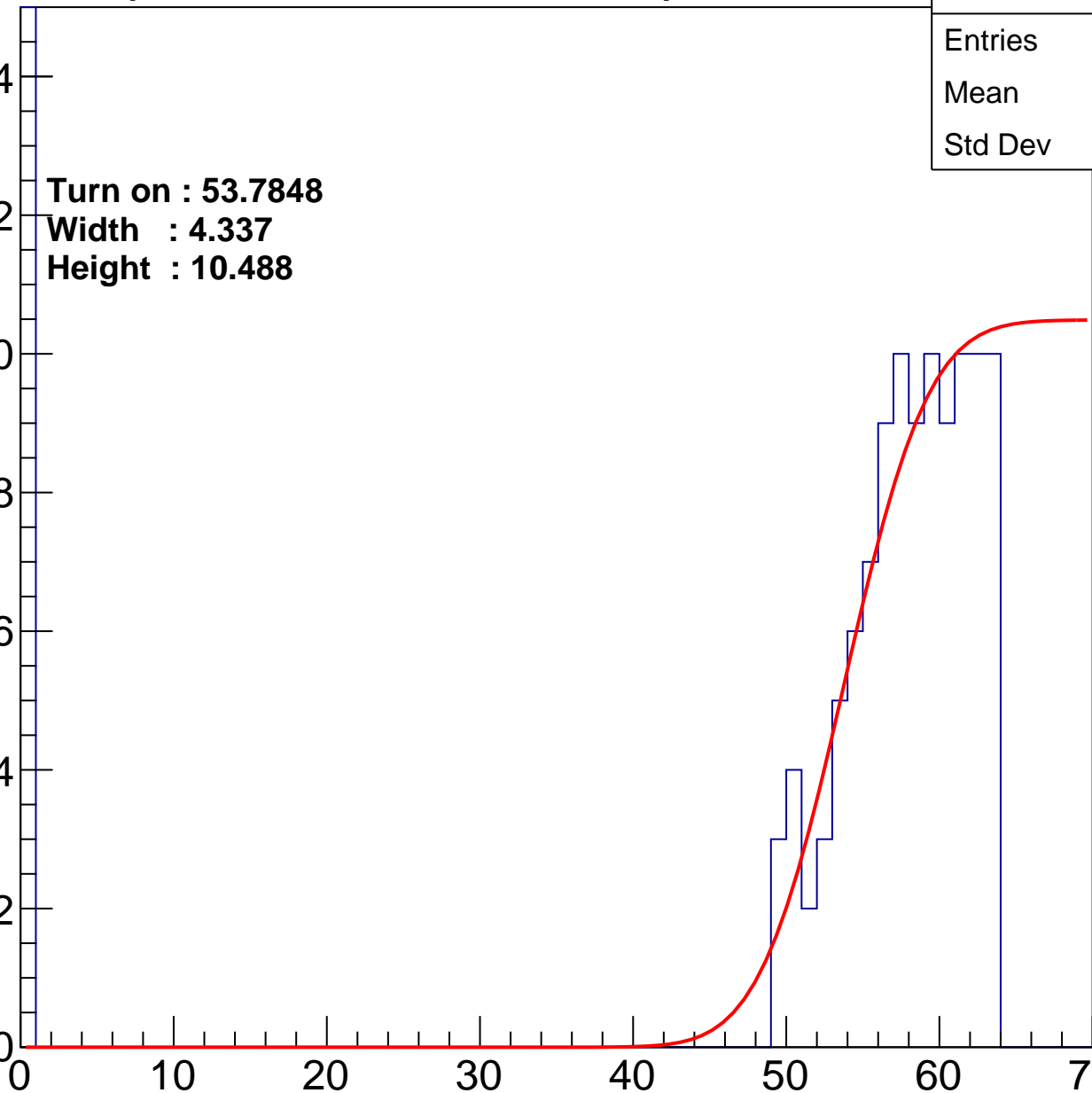
Width : 4.337

Height : 10.488

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	38.24
Std Dev	27.72

Turn on : 54.4124

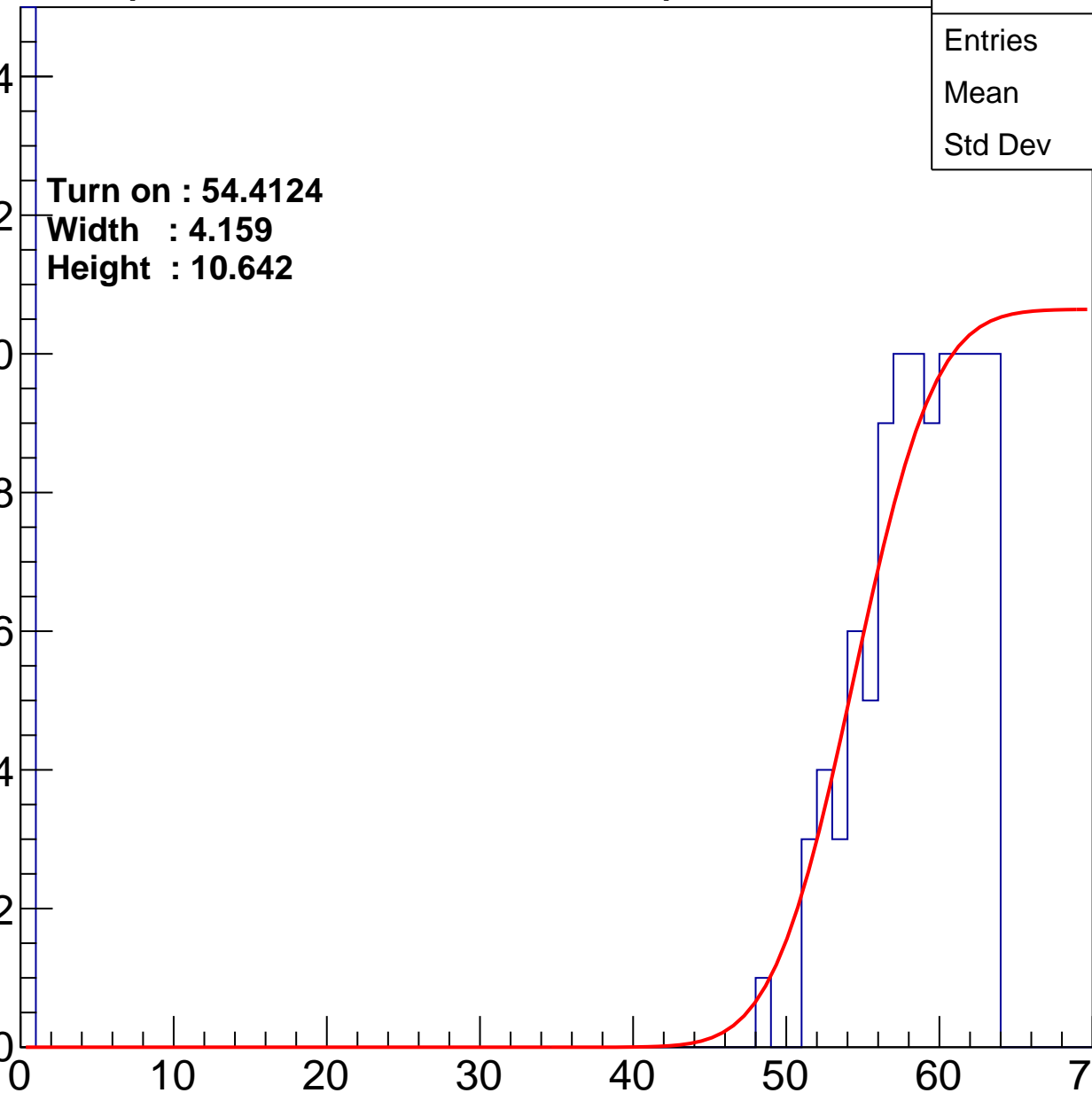
Width : 4.159

Height : 10.642

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	38.37
Std Dev	26.91

Turn on : 52.7027

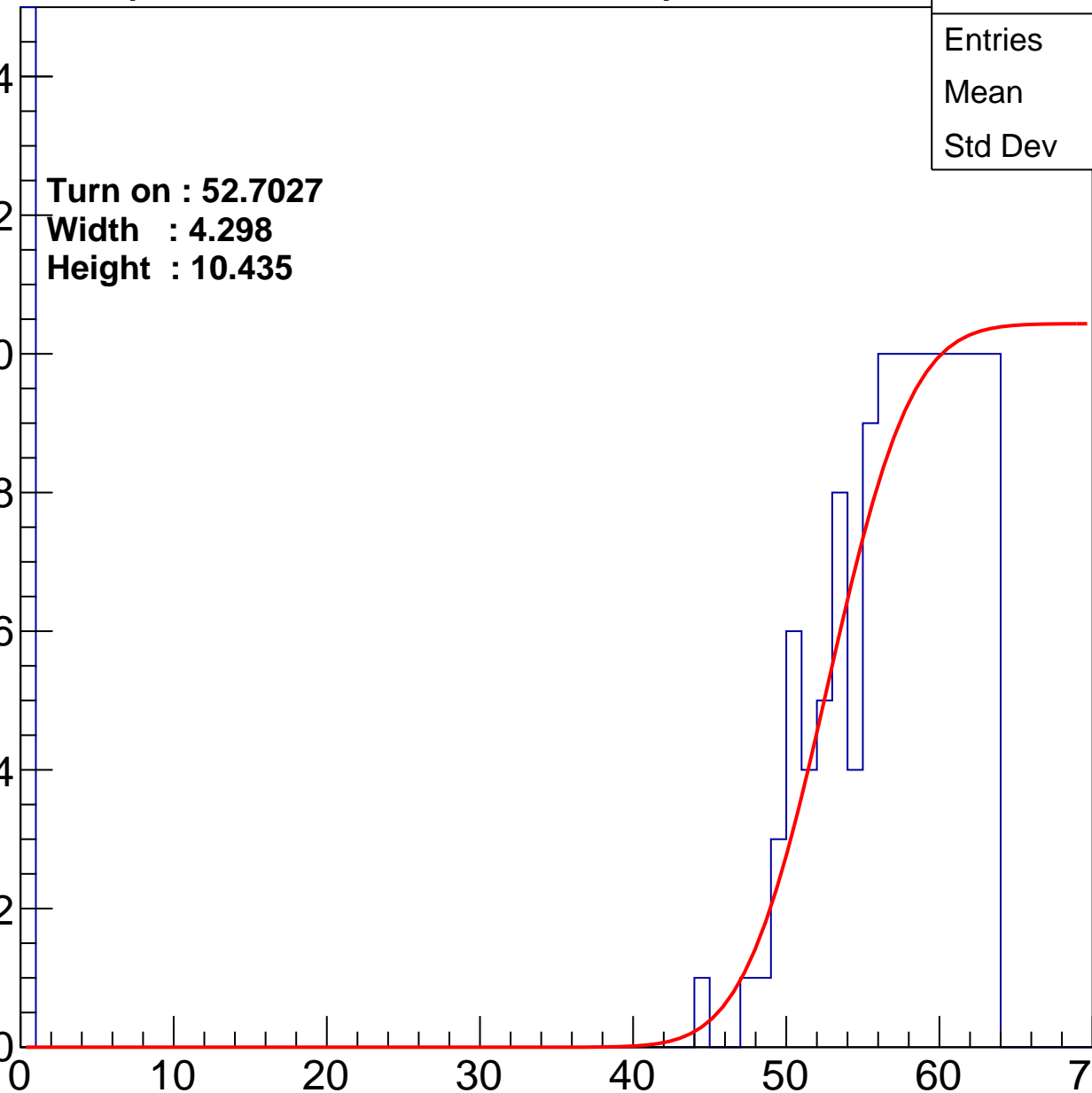
Width : 4.298

Height : 10.435

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	33.2
Std Dev	29.33

Turn on : 55.6752

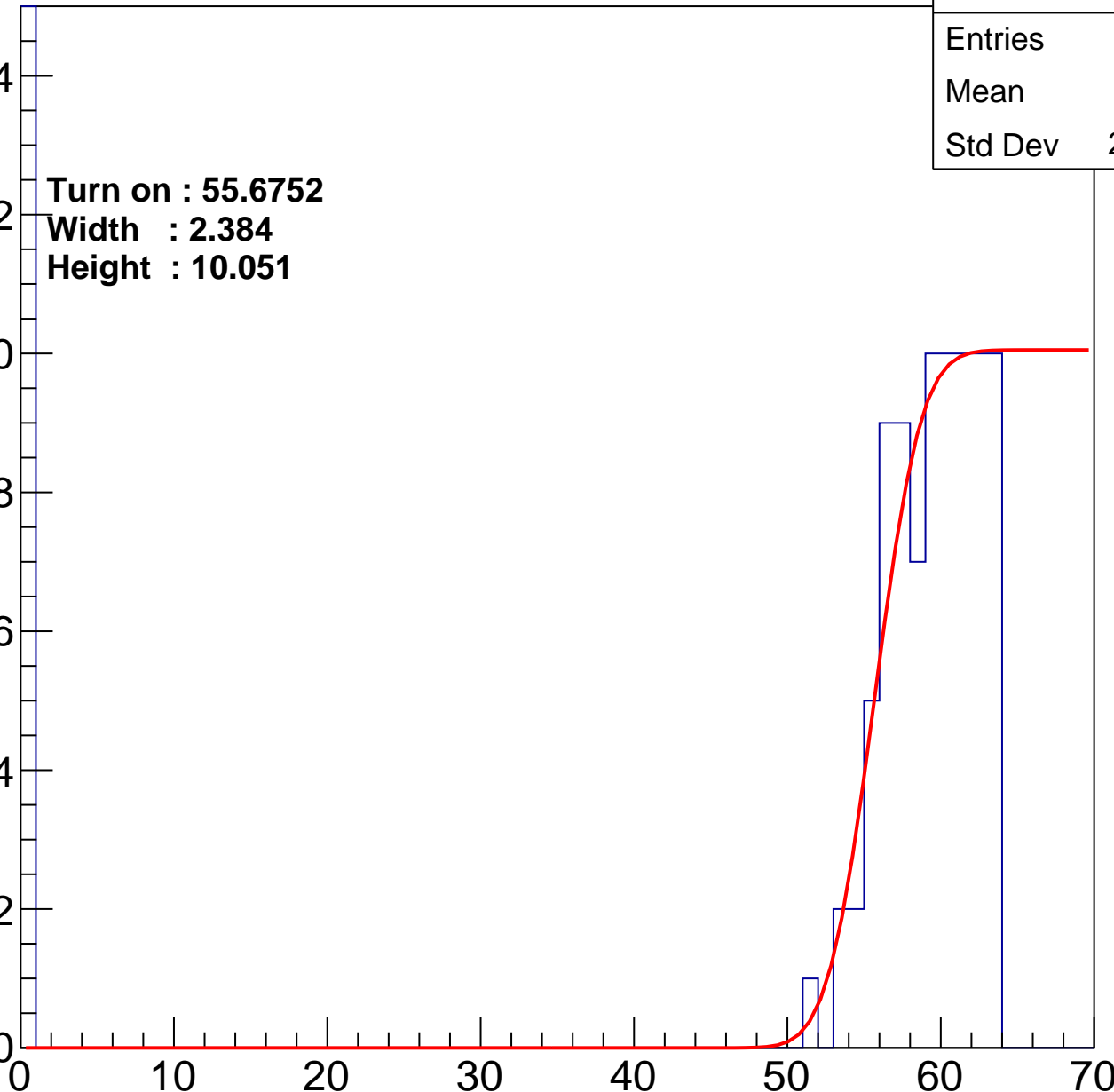
Width : 2.384

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	37.54
Std Dev	27.31

Turn on : 51.8936

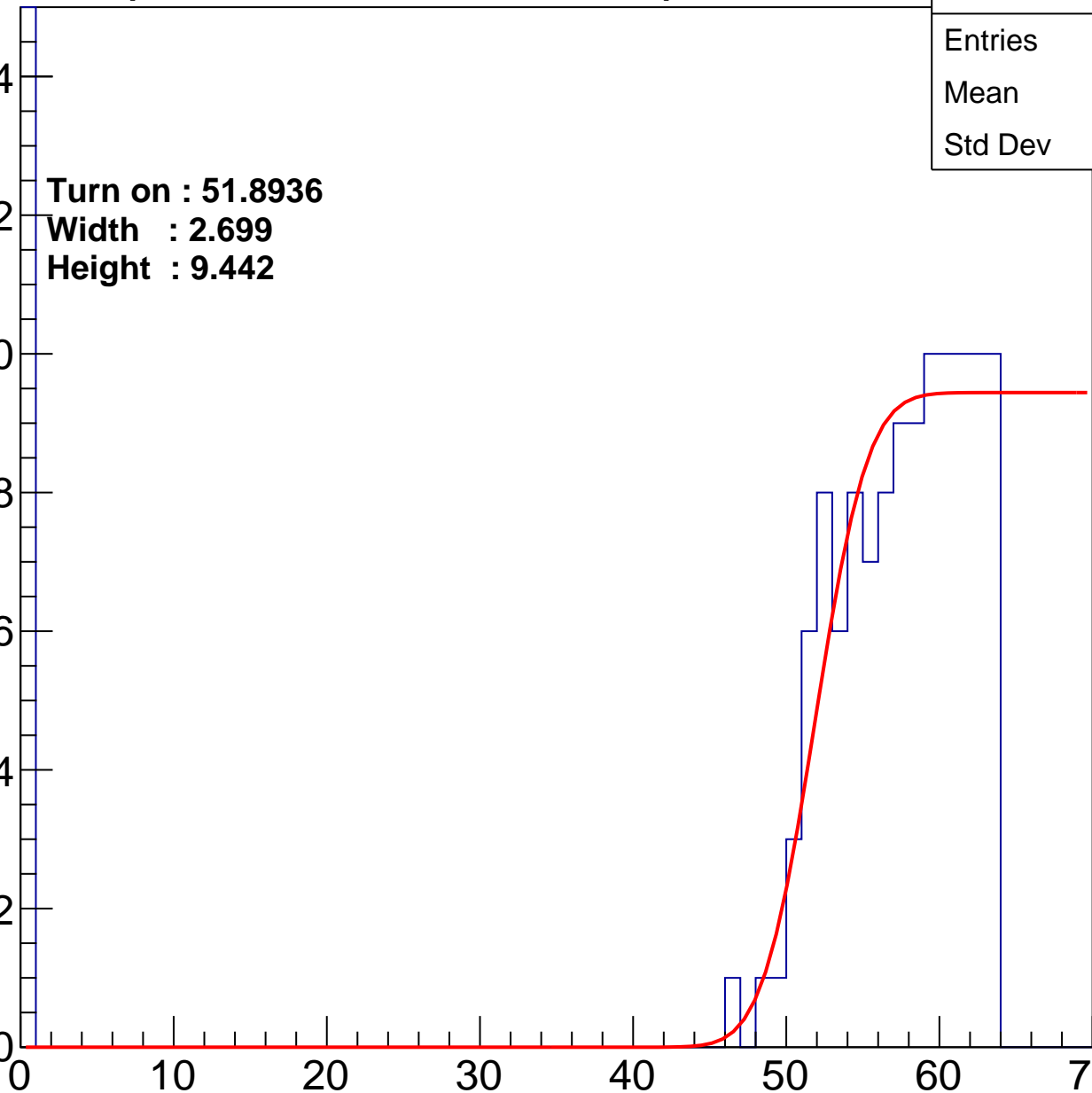
Width : 2.699

Height : 9.442

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch9

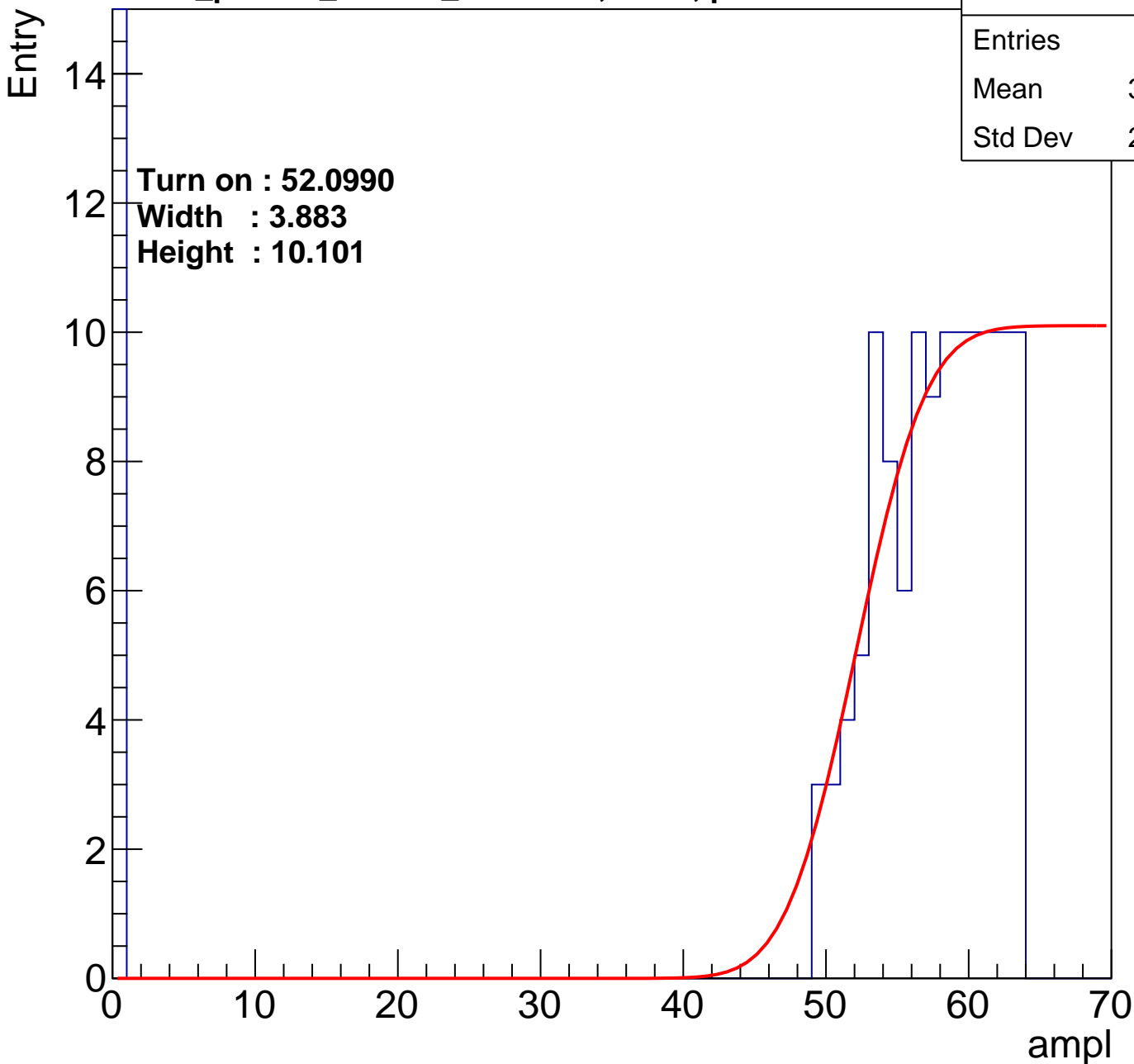
calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	37.12
Std Dev	27.52

Turn on : 52.0990

Width : 3.883

Height : 10.101



B1L104S, U24-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	32.8
Std Dev	29.13

Turn on : 54.8881

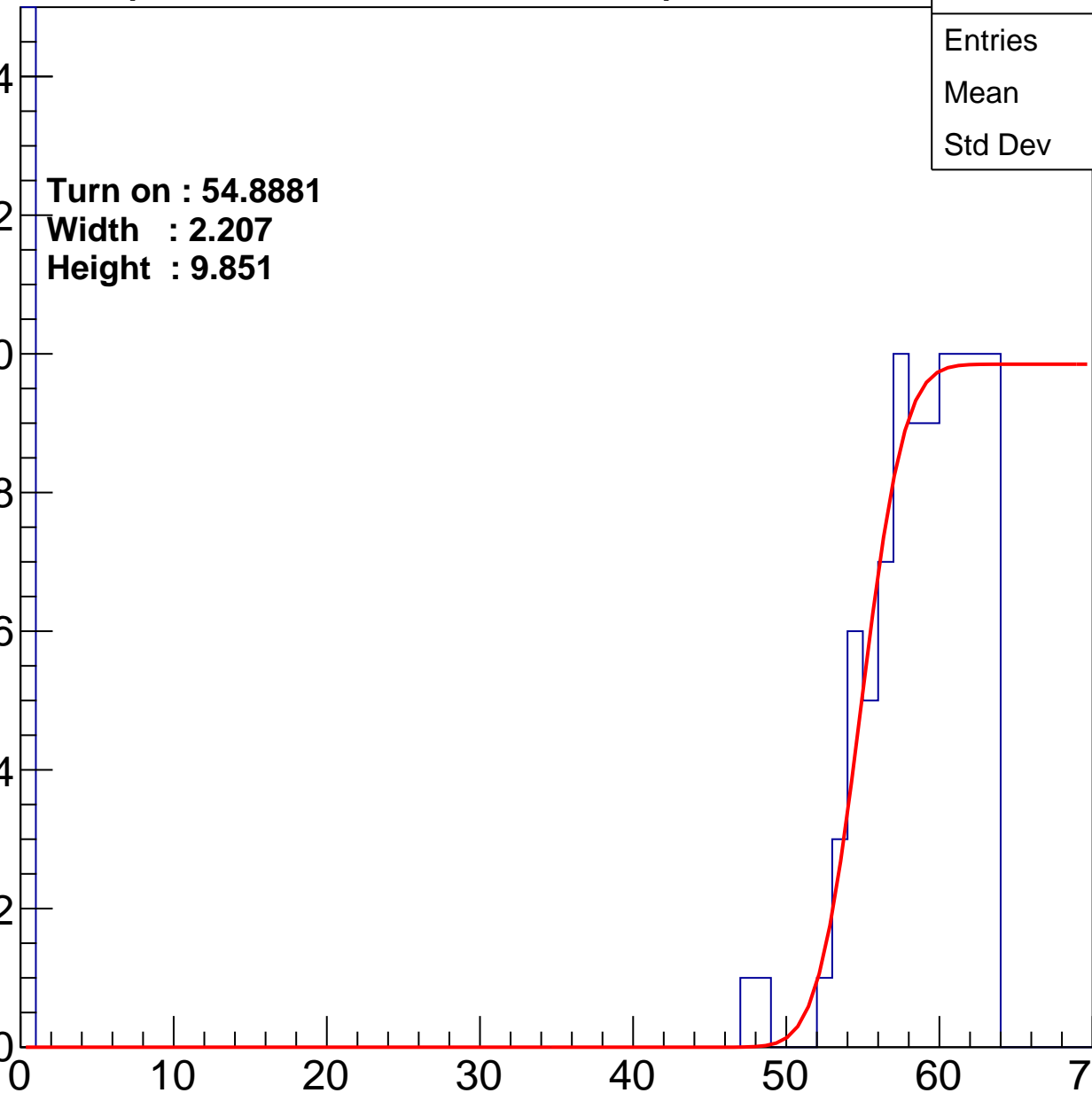
Width : 2.207

Height : 9.851

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	37.44
Std Dev	27.33

Turn on : 52.8475

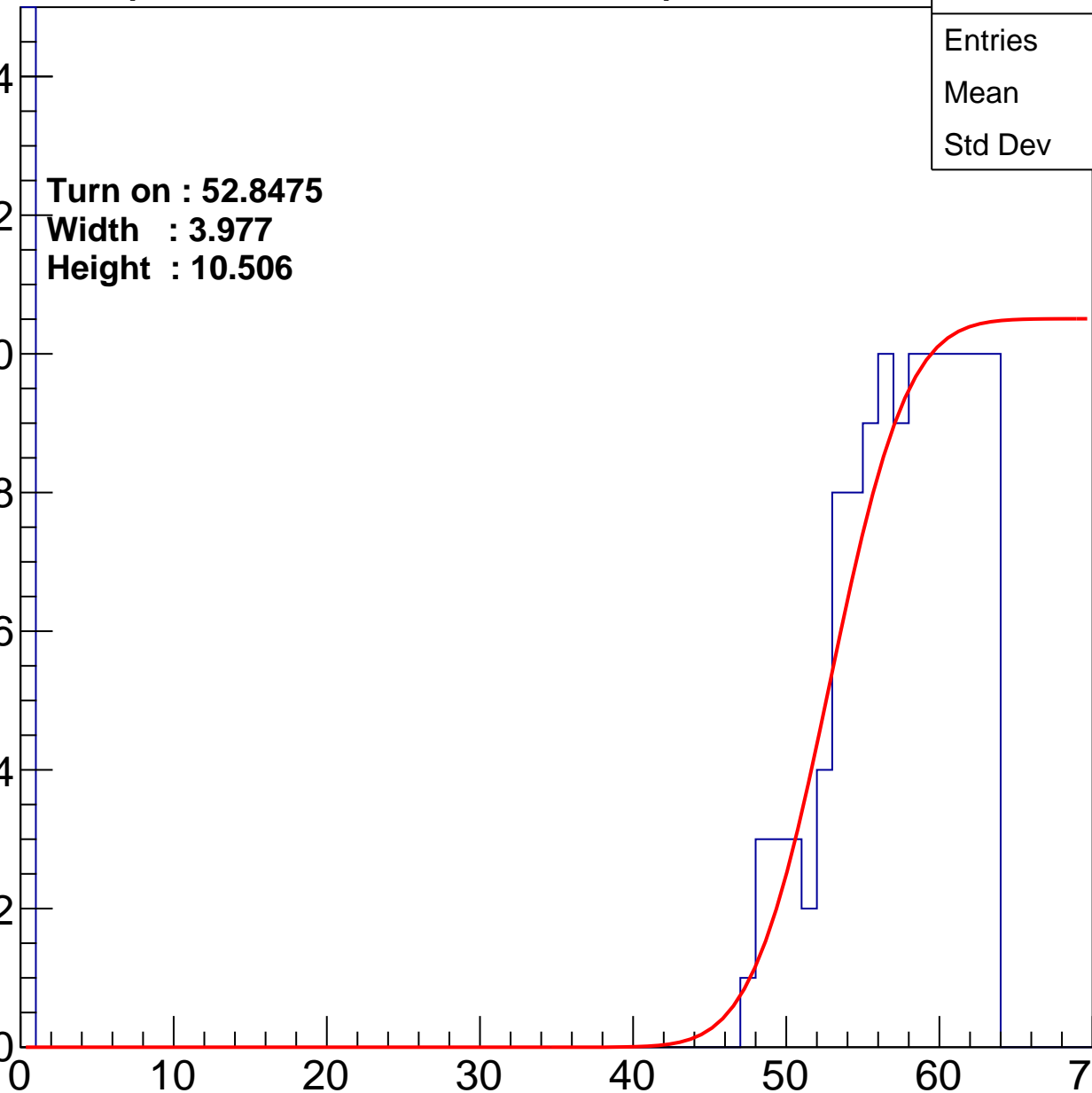
Width : 3.977

Height : 10.506

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	32.79
Std Dev	28.69

Turn on : 54.8736

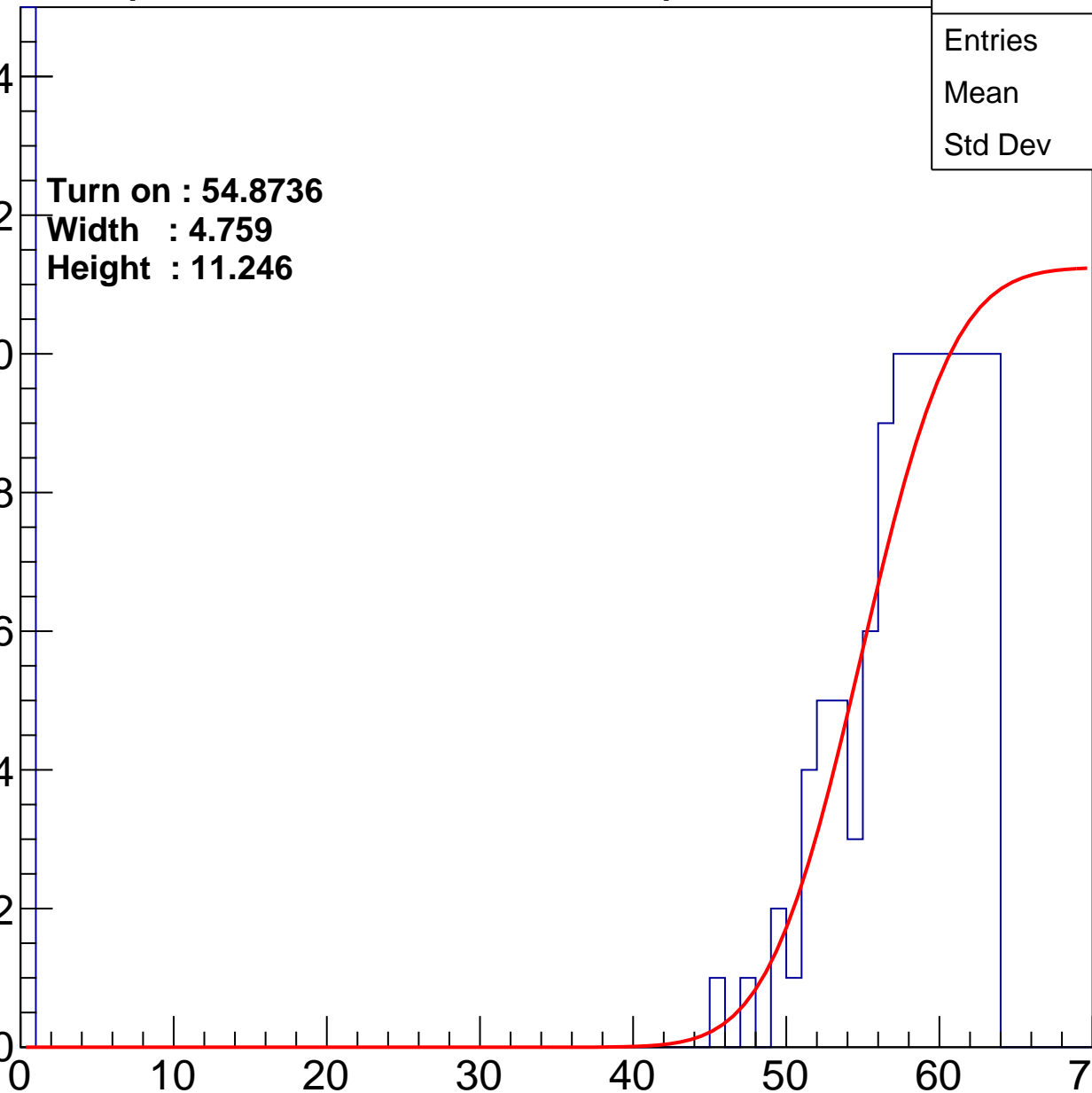
Width : 4.759

Height : 11.246

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	36.85
Std Dev	27.88

Turn on : 53.3556

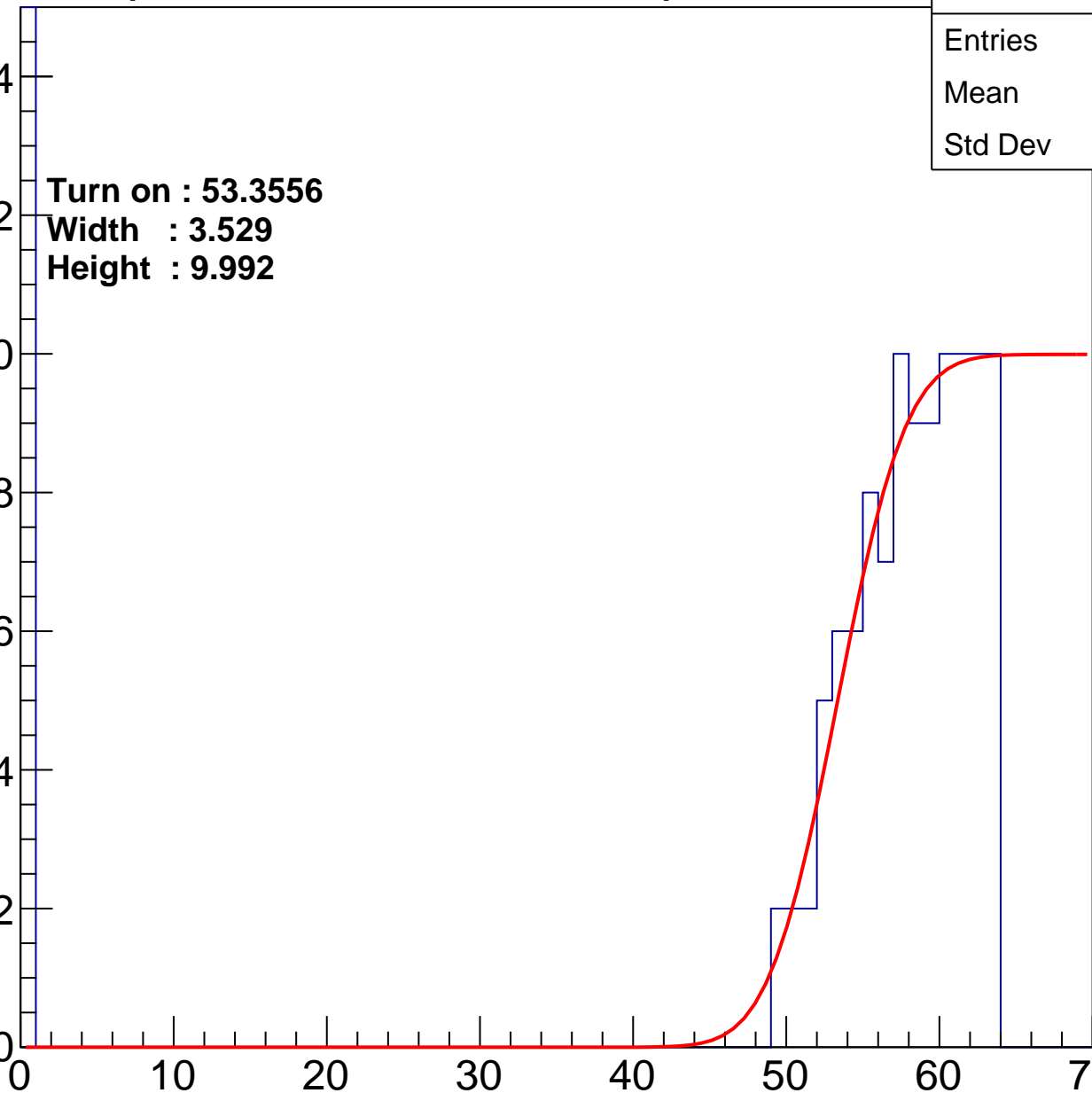
Width : 3.529

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch14

calib_packv5_033123_0516.root, FC#4, port A1

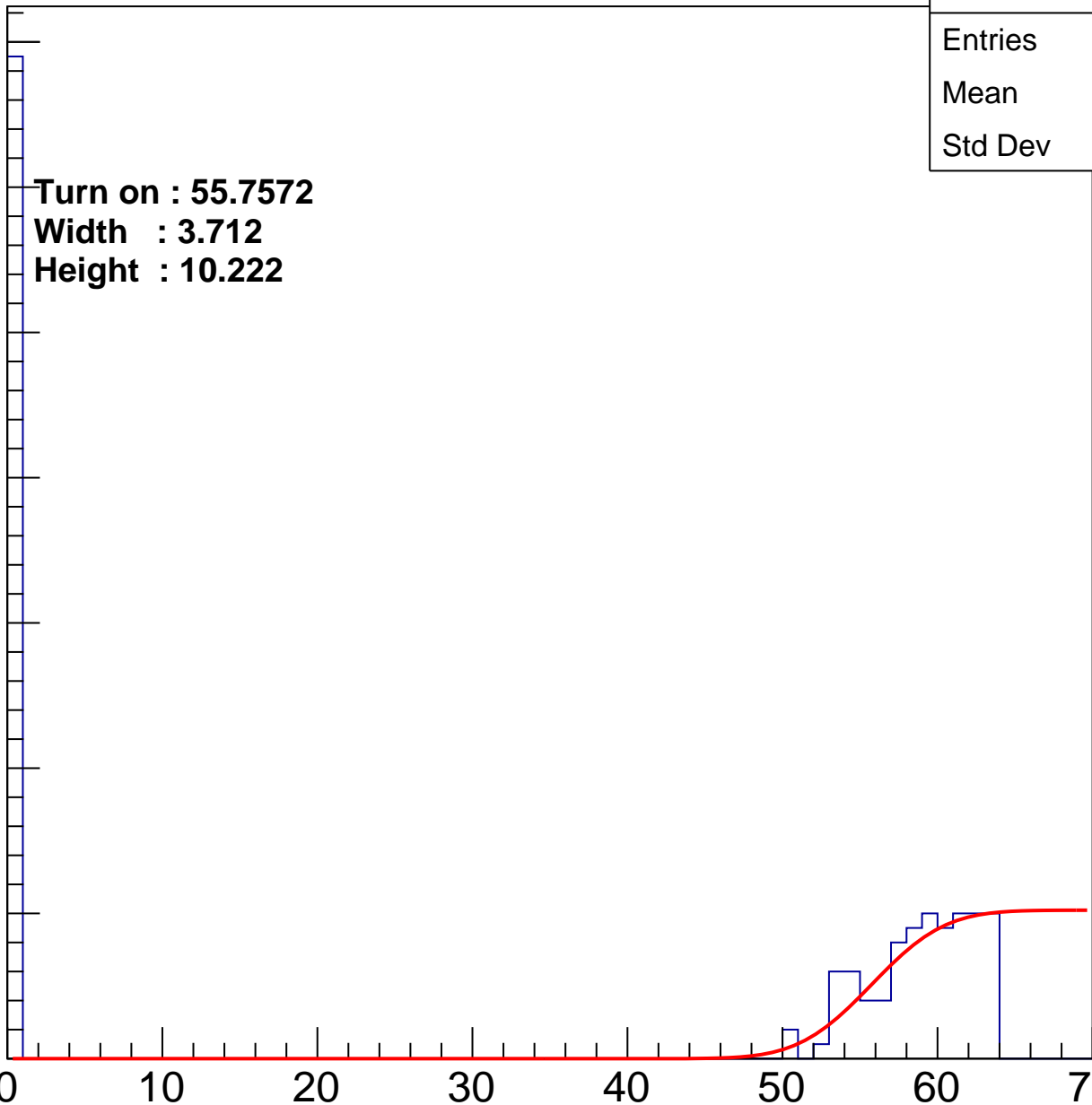
Entry

70
60
50
40
30
20
10
0

Turn on : 55.7572
Width : 3.712
Height : 10.222

Entries	158
Mean	32.95
Std Dev	29.12

ampl



B1L104S, U24-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entry

14

12

10

8

6

4

2

0

Turn on : 53.6728

Width : 2.675

Height : 9.946

Entries	176
Mean	35.4
Std Dev	28.24

ampl

0 10 20 30 40 50 60 70

B1L104S, U24-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	34.45
Std Dev	28.31

Turn on : 55.6034

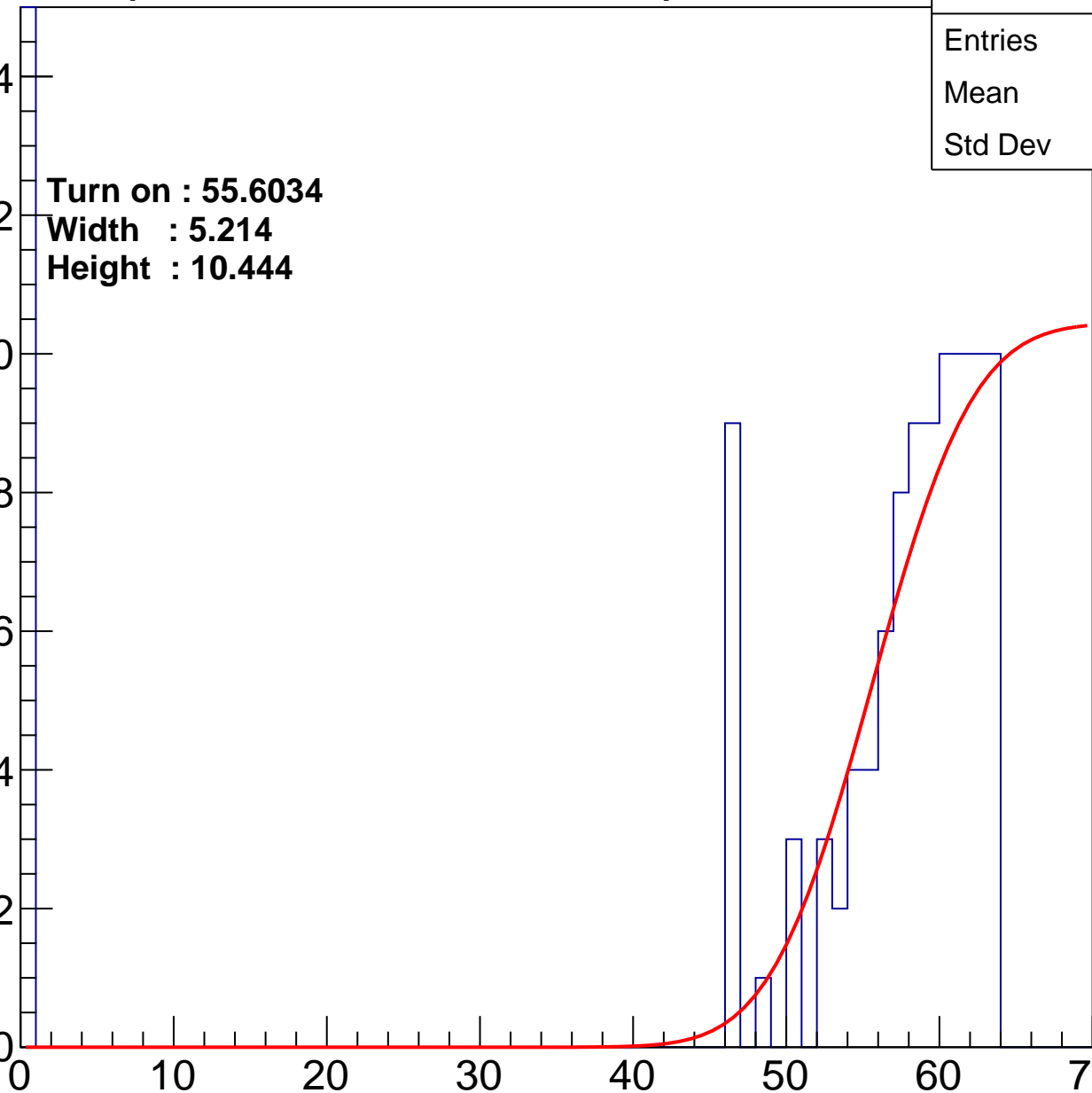
Width : 5.214

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	34.09
Std Dev	28.92

Turn on : 54.3474

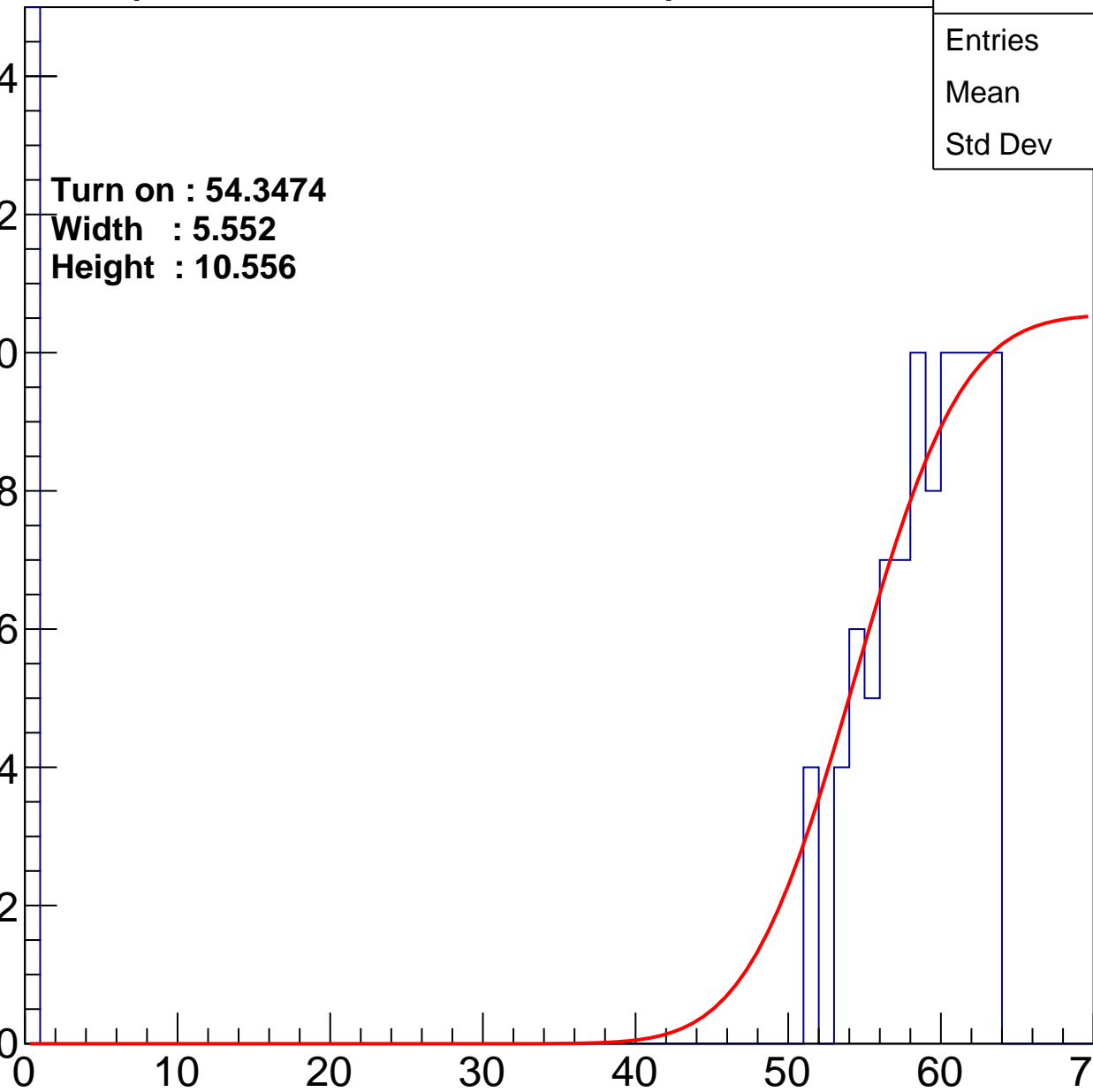
Width : 5.552

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch18

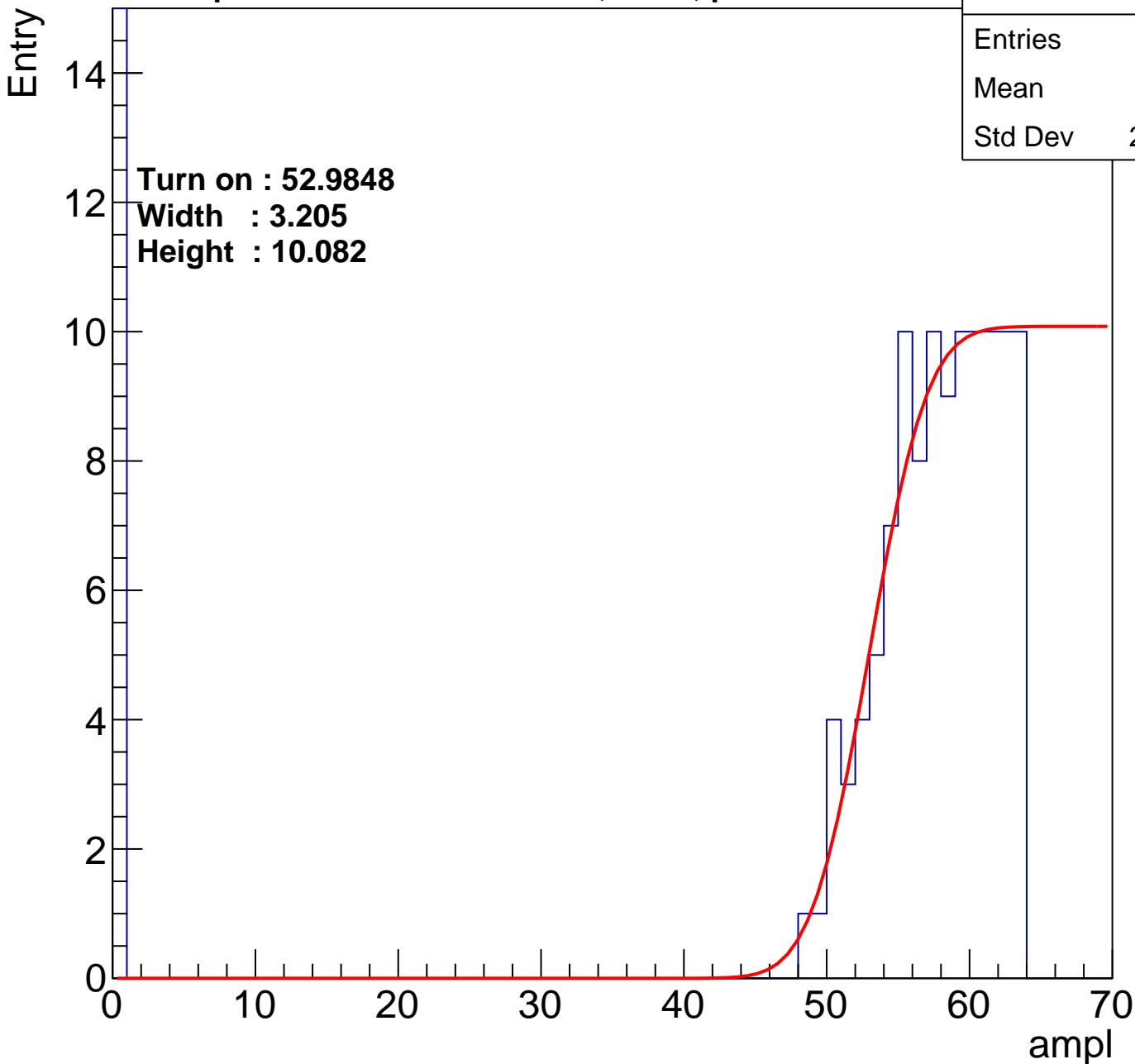
calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	35.2
Std Dev	28.18

Turn on : 52.9848

Width : 3.205

Height : 10.082



B1L104S, U24-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	33.94
Std Dev	28.67

Turn on : 53.0224

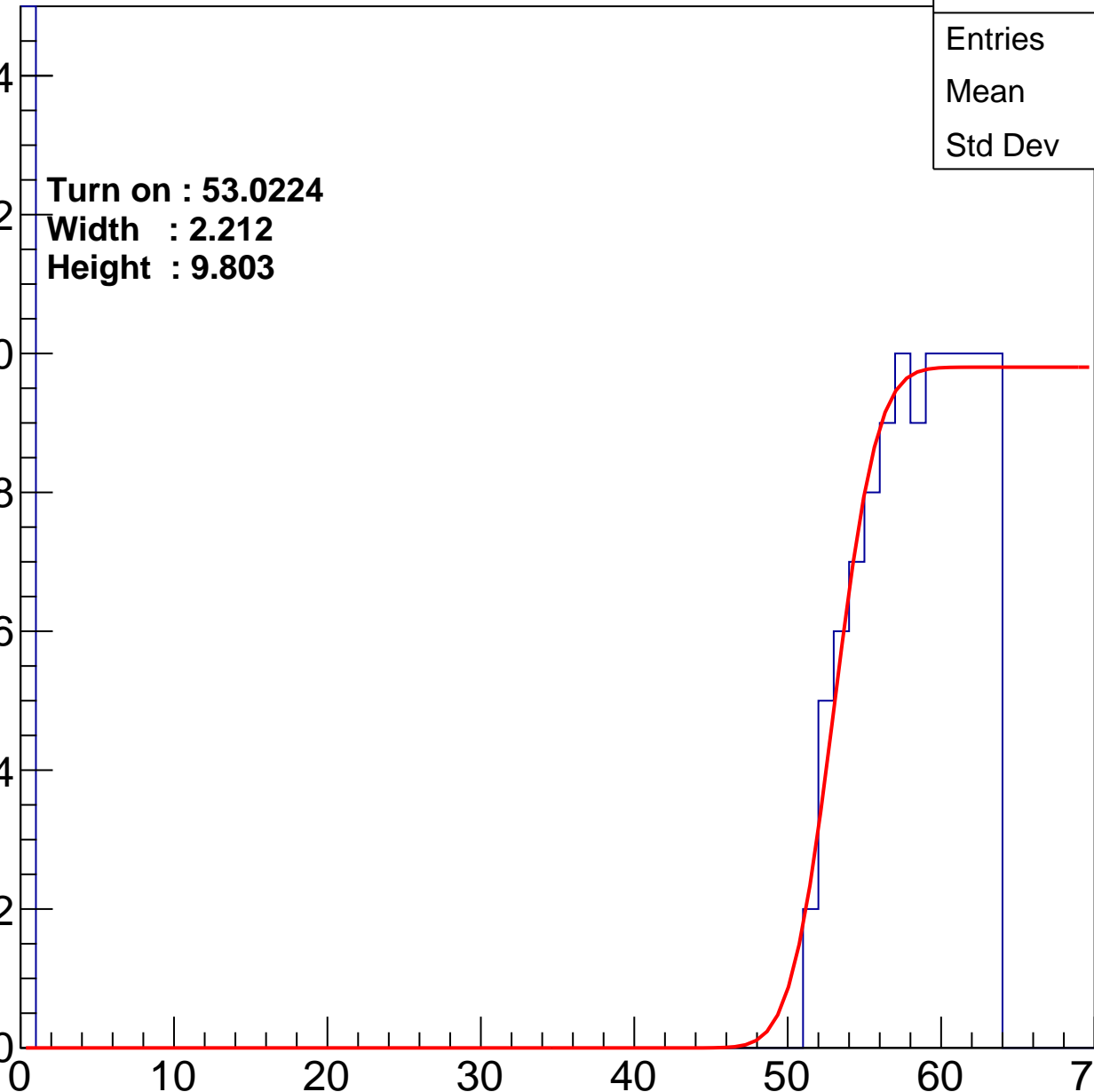
Width : 2.212

Height : 9.803

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	34.69
Std Dev	28.53

Turn on : 53.8977

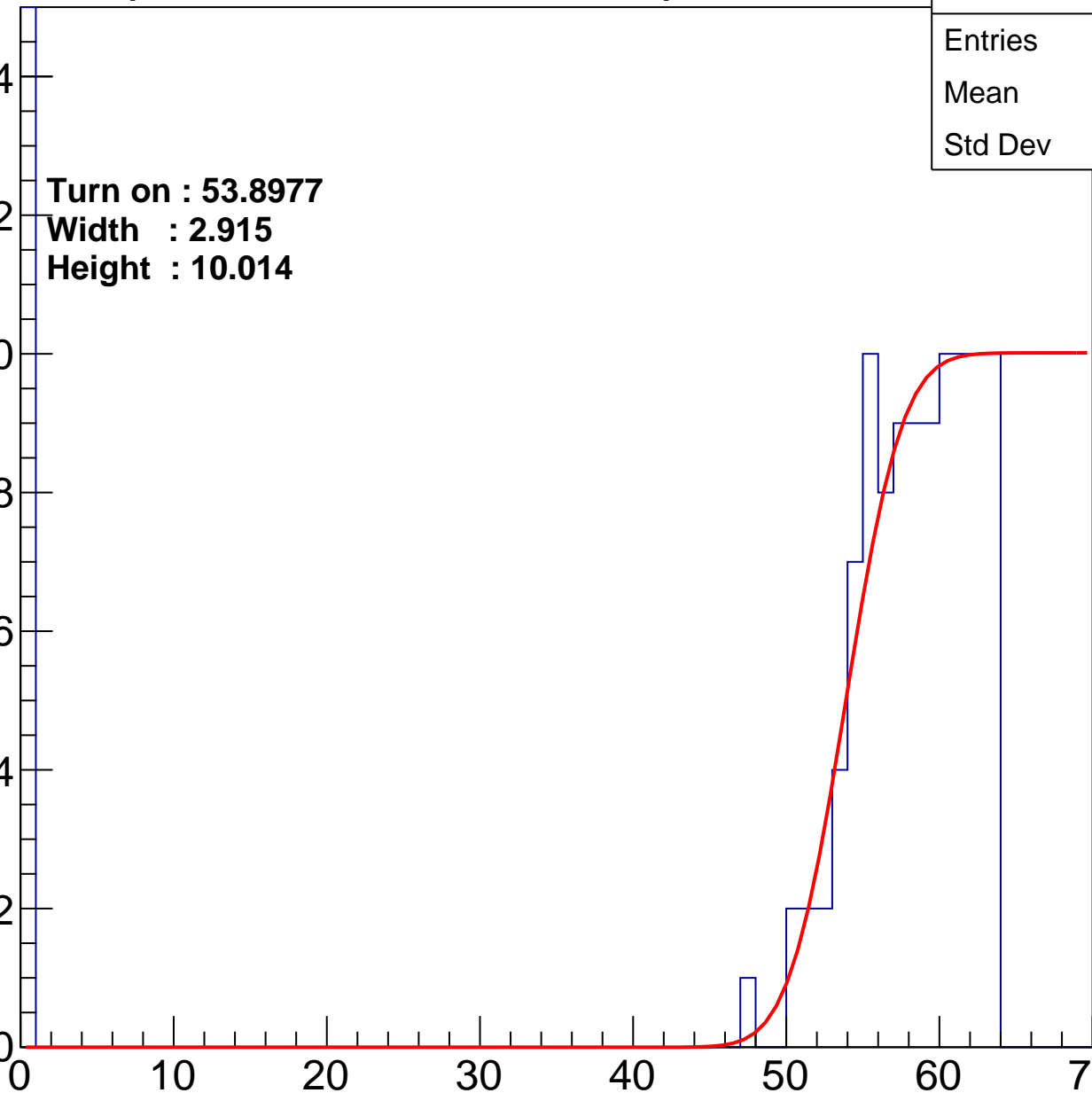
Width : 2.915

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch21

calib_packv5_033123_0516.root, FC#4, port A1

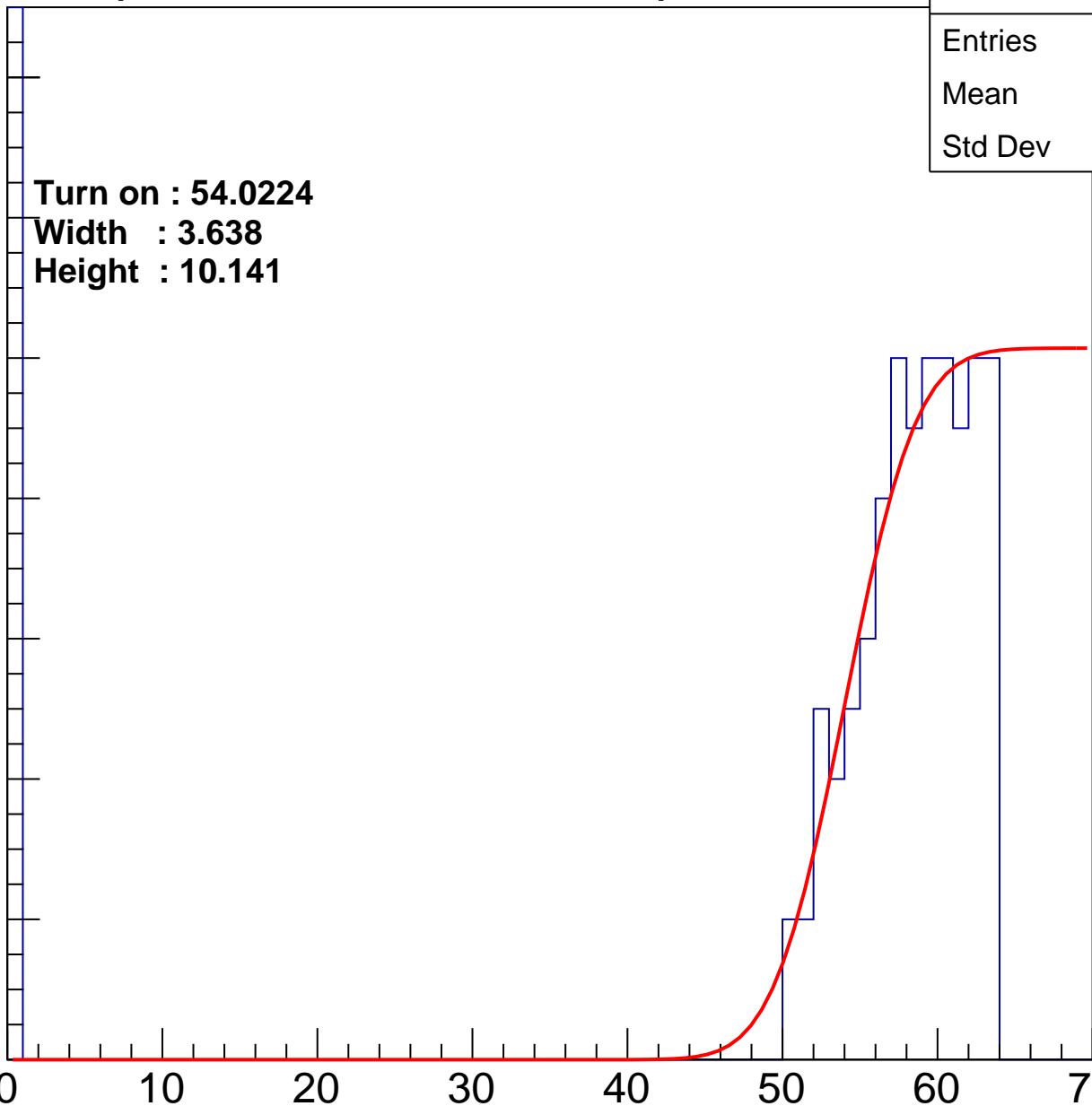
Entry

14
12
10
8
6
4
2
0

Turn on : 54.0224
Width : 3.638
Height : 10.141

Entries	187
Mean	31.03
Std Dev	29.06

ampl



B1L104S, U24-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	33.61
Std Dev	28.71

Turn on : 54.2397

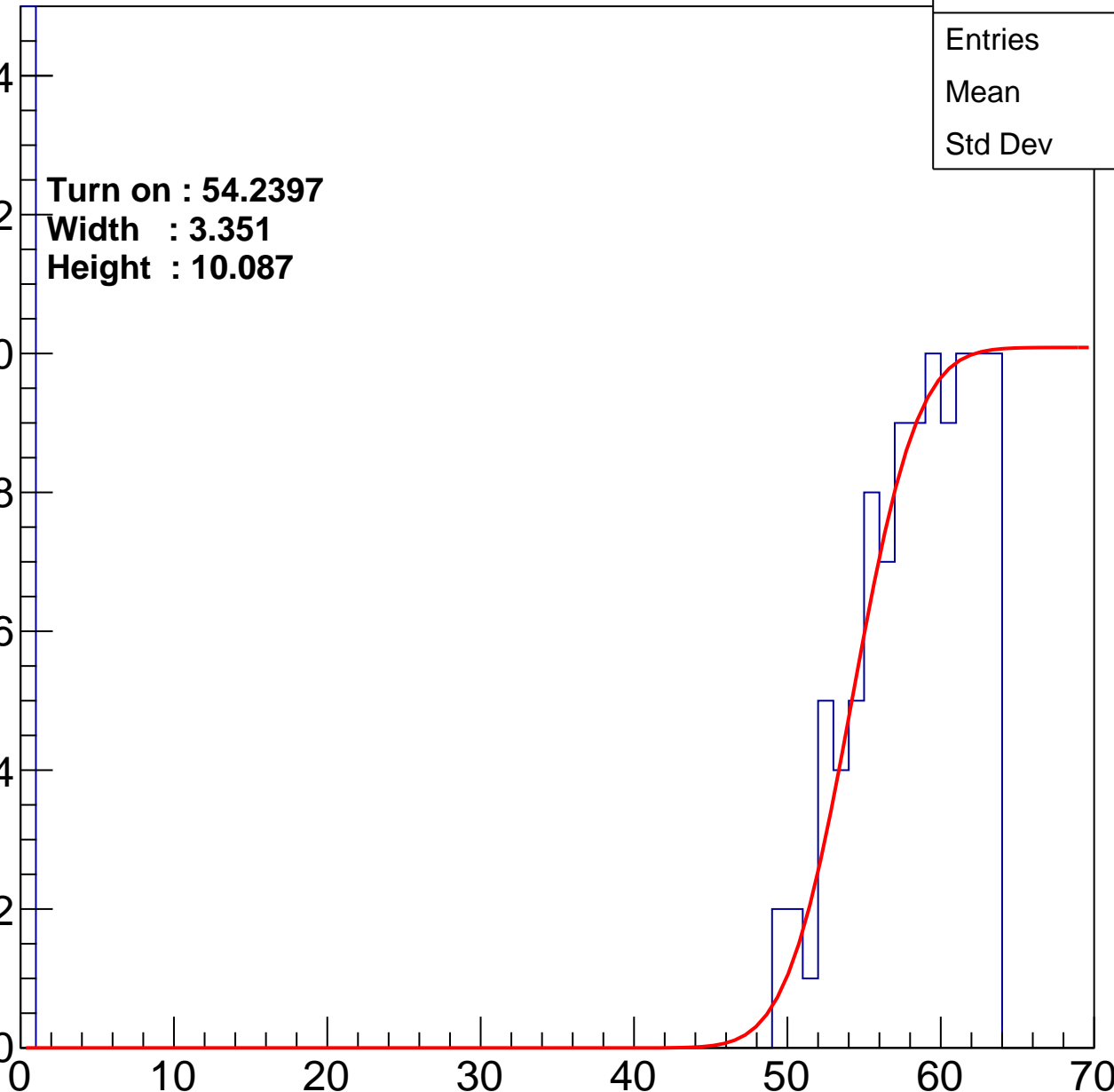
Width : 3.351

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	35.18
Std Dev	28.49

Turn on : 53.5953

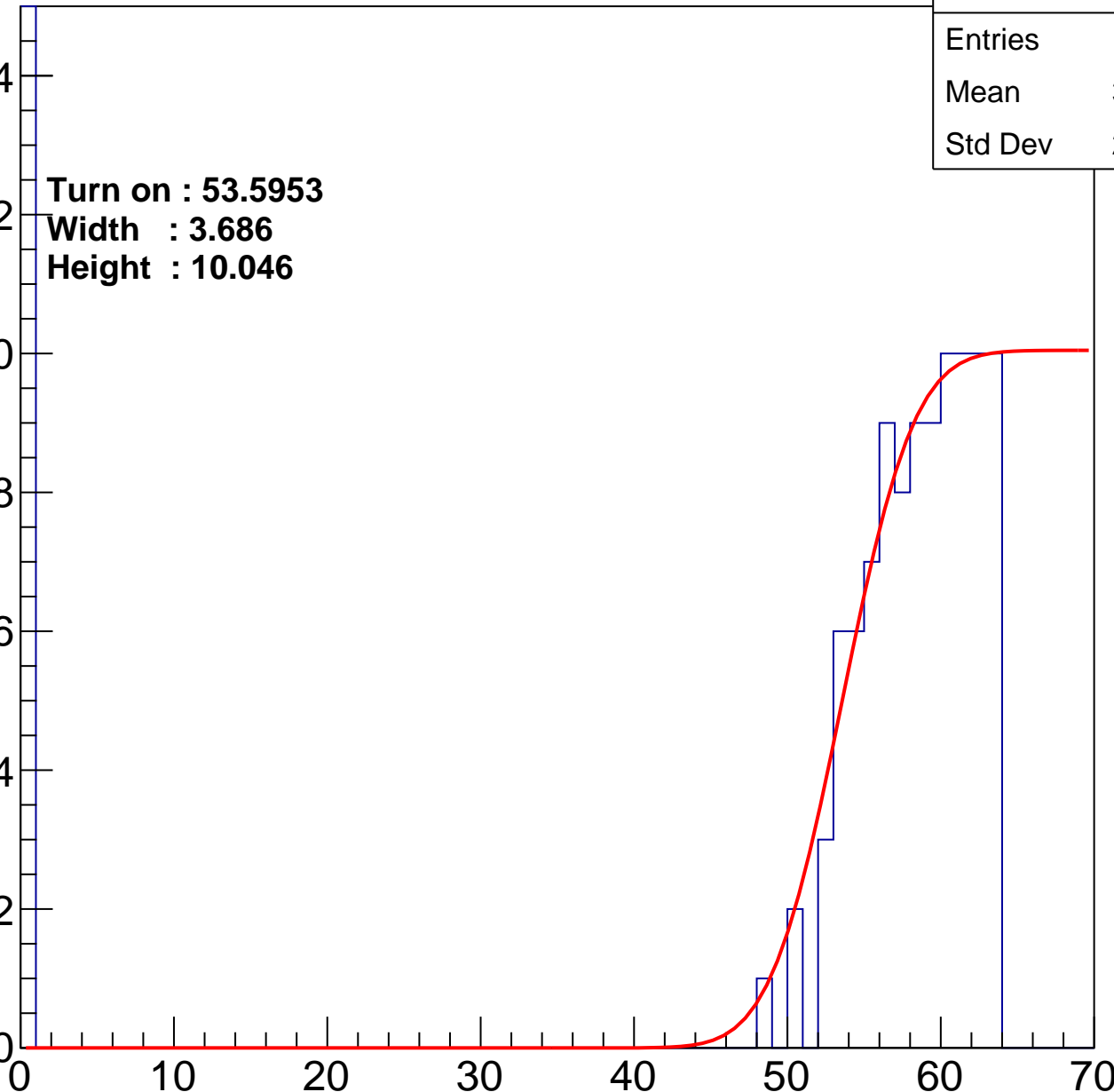
Width : 3.686

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	133
Mean	36.66
Std Dev	28.58

Turn on : 56.2977

Width : 3.725

Height : 10.222

Entry

50

40

30

20

10

0

0

10

20

30

40

50

60

70

ampl

0

B1L104S, U24-ch25

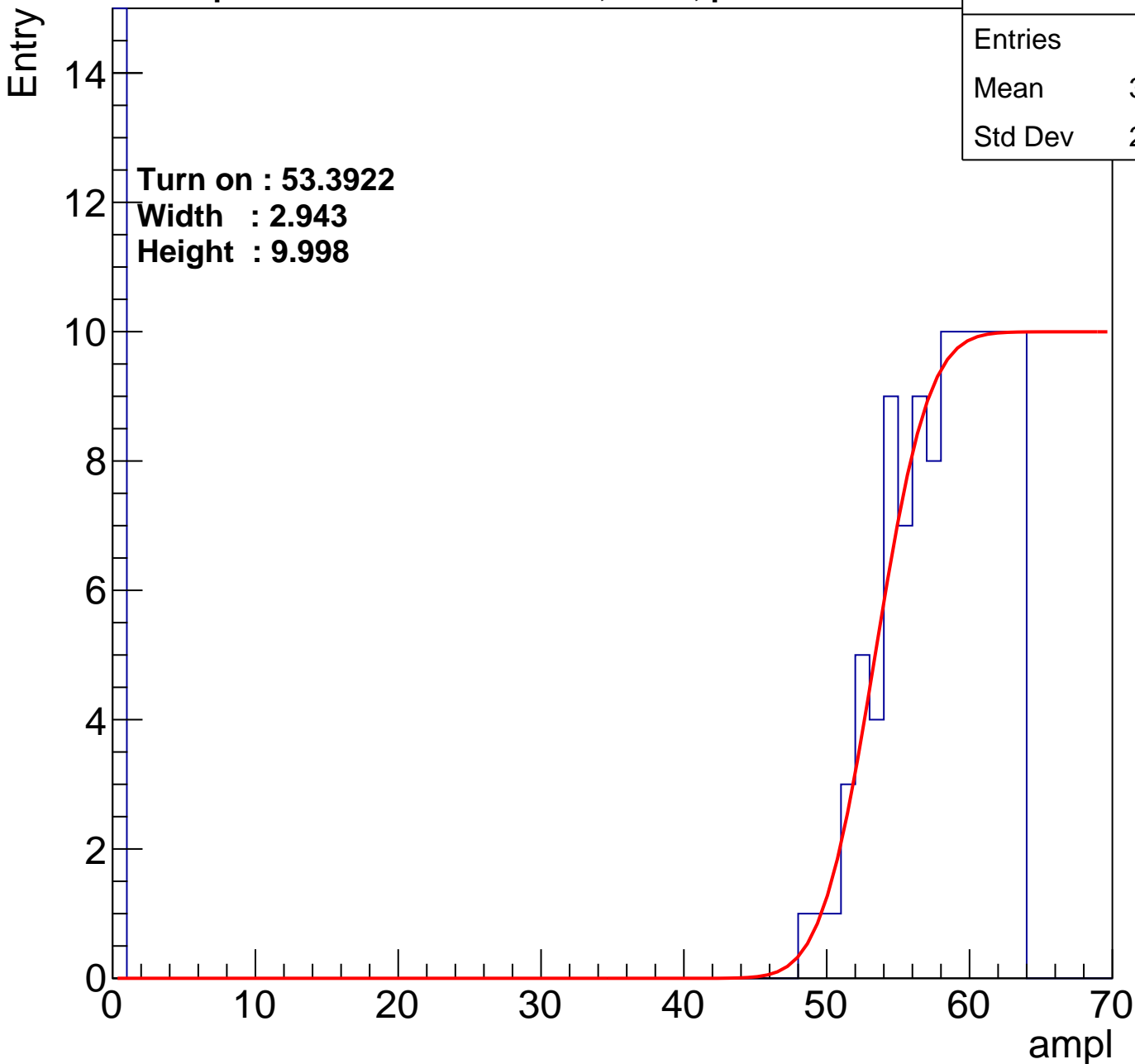
calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	36.66
Std Dev	27.93

Turn on : 53.3922

Width : 2.943

Height : 9.998



B1L104S, U24-ch26

calib_packv5_033123_0516.root, FC#4, port A1

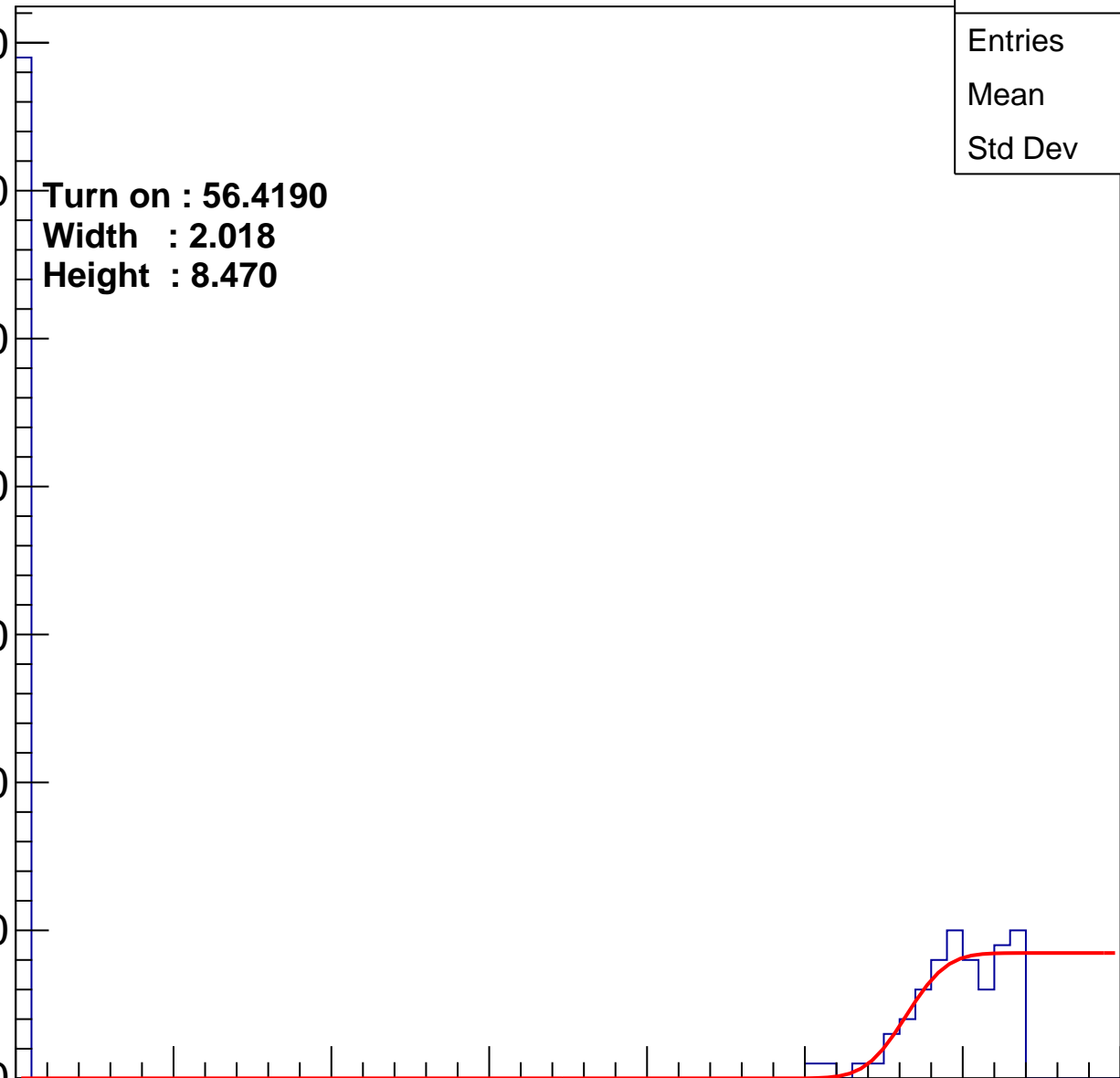
Entry

70
60
50
40
30
20
10
0

Turn on : 56.4190
Width : 2.018
Height : 8.470

Entries	137
Mean	29.39
Std Dev	29.68

ampl



B1L104S, U24-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	35.16
Std Dev	28.3

Turn on : 53.7865

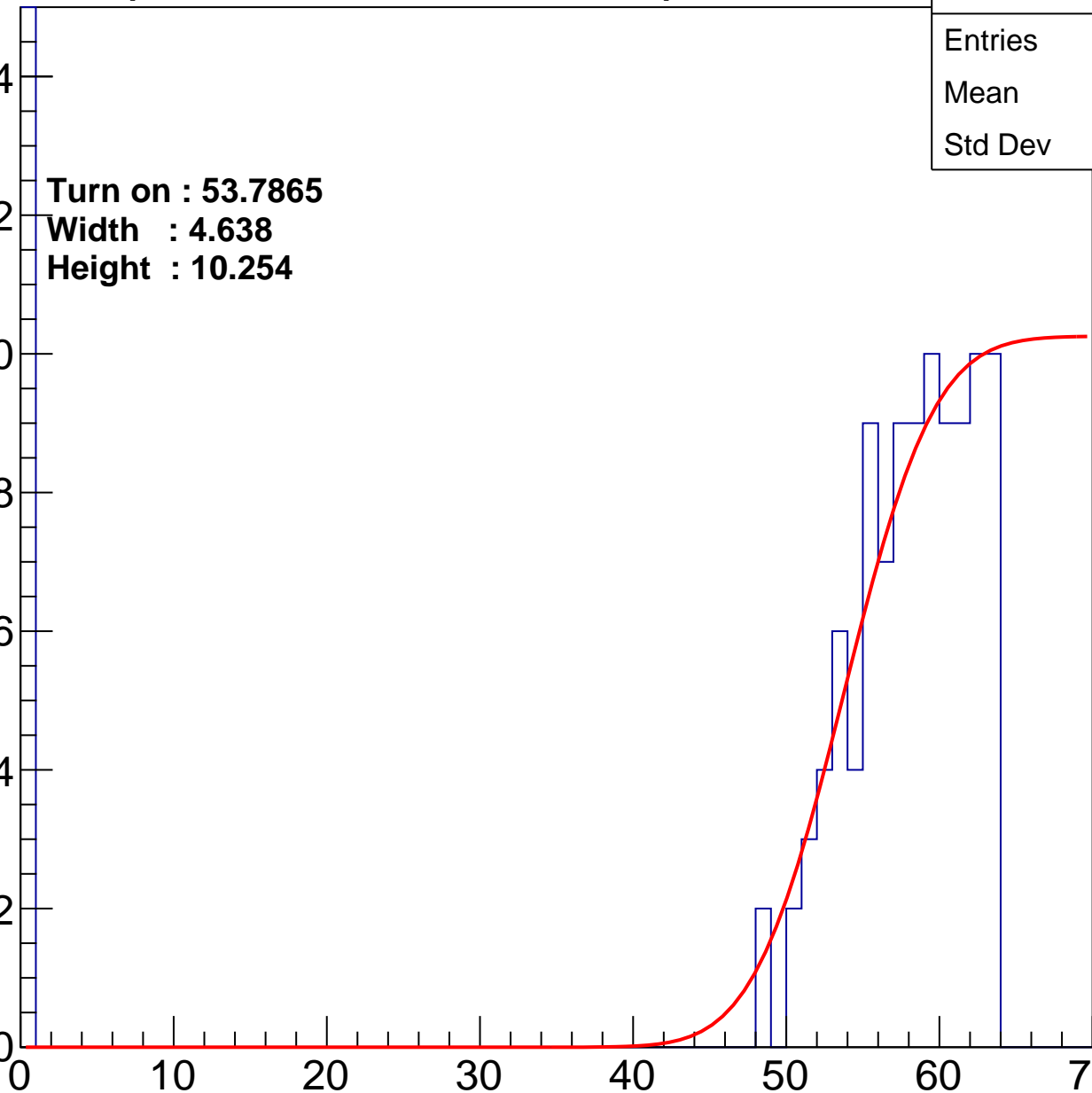
Width : 4.638

Height : 10.254

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	37.31
Std Dev	27.76

Turn on : 54.1033

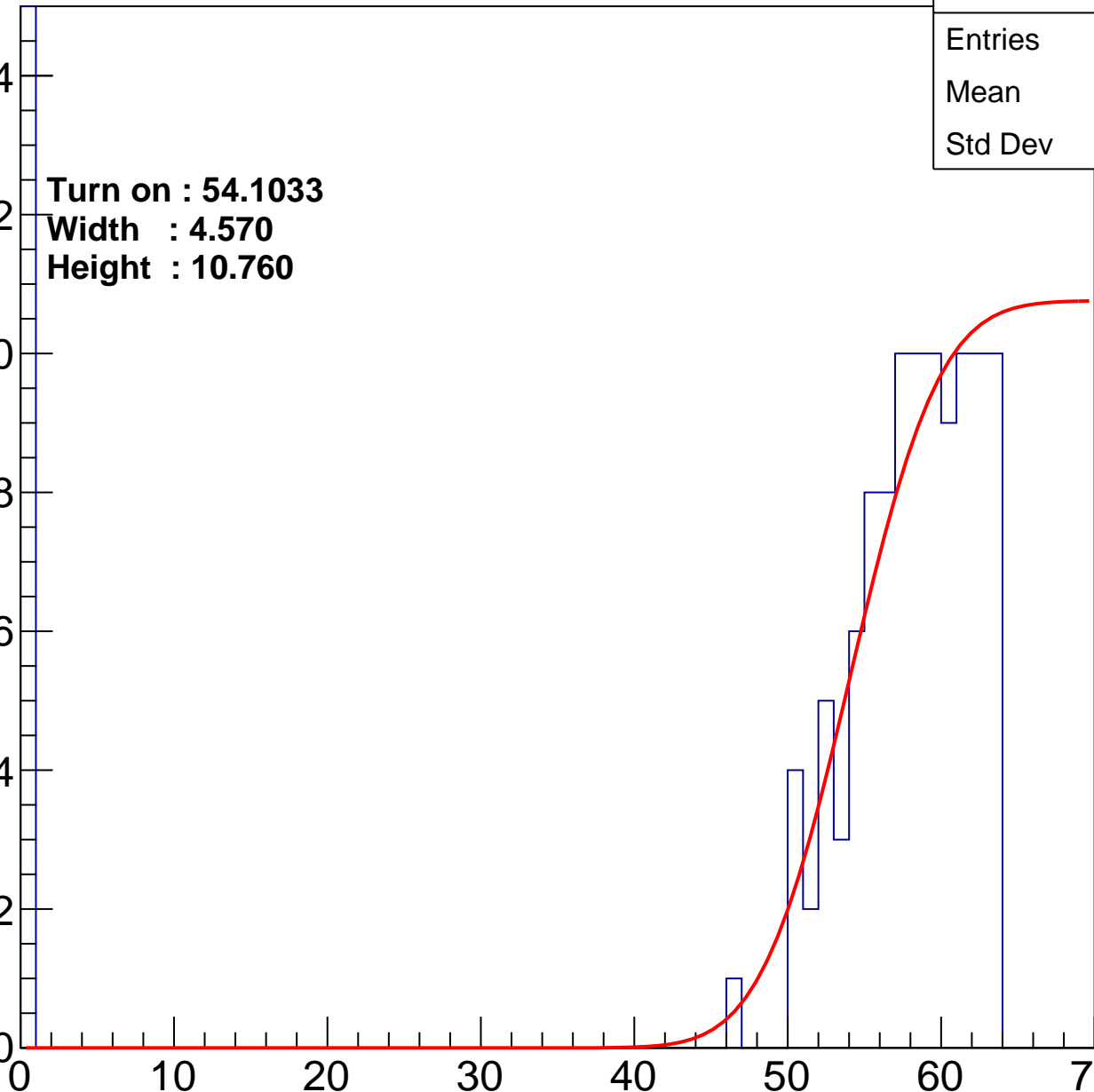
Width : 4.570

Height : 10.760

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	30.3
Std Dev	29.6

Turn on : 56.8624

Width : 2.982

Height : 9.889

Entry

70

60

50

40

30

20

10

0

0

10

20

30

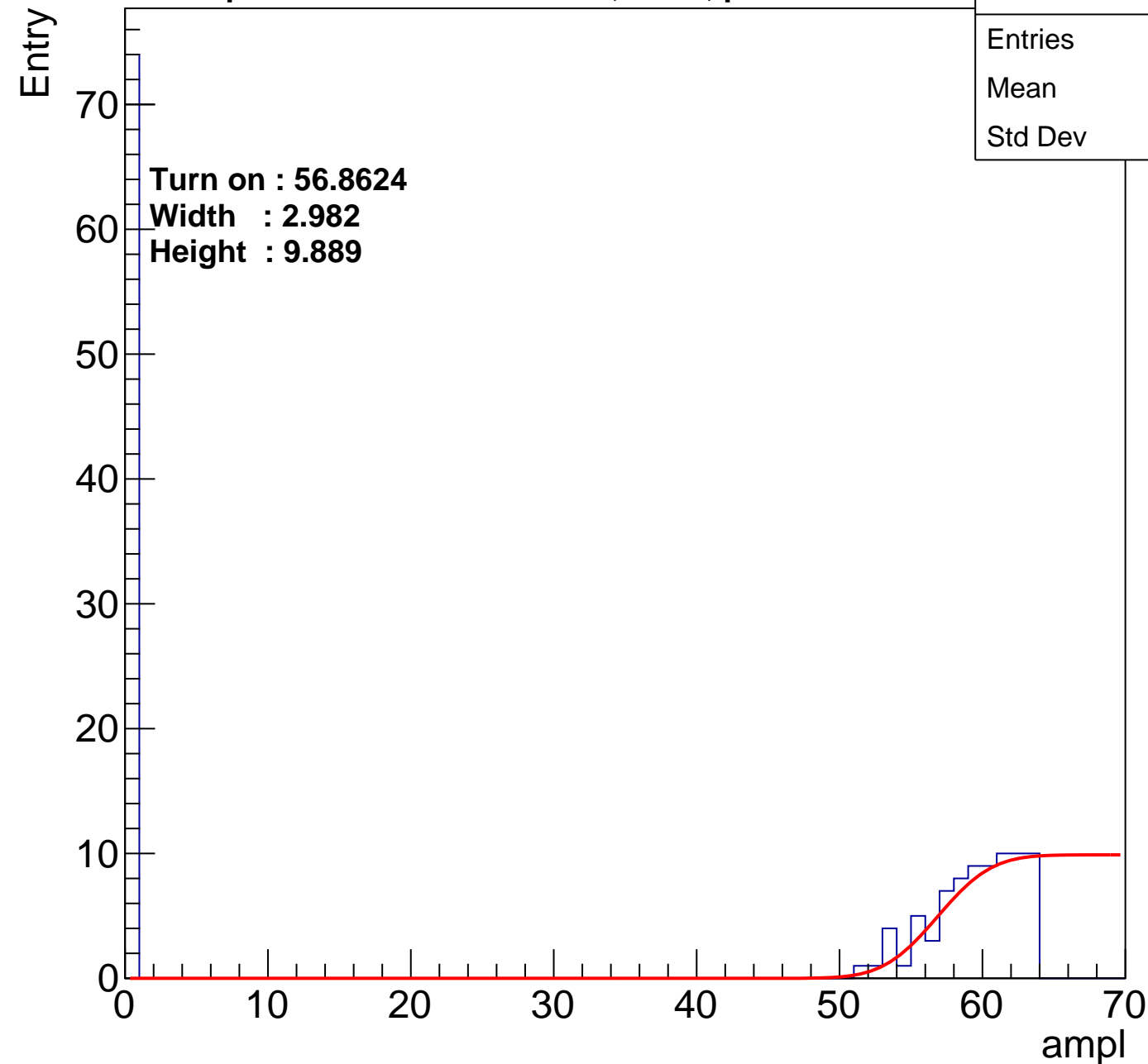
40

50

60

70

ampl



B1L104S, U24-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	39.24
Std Dev	26.84

Turn on : 52.9815

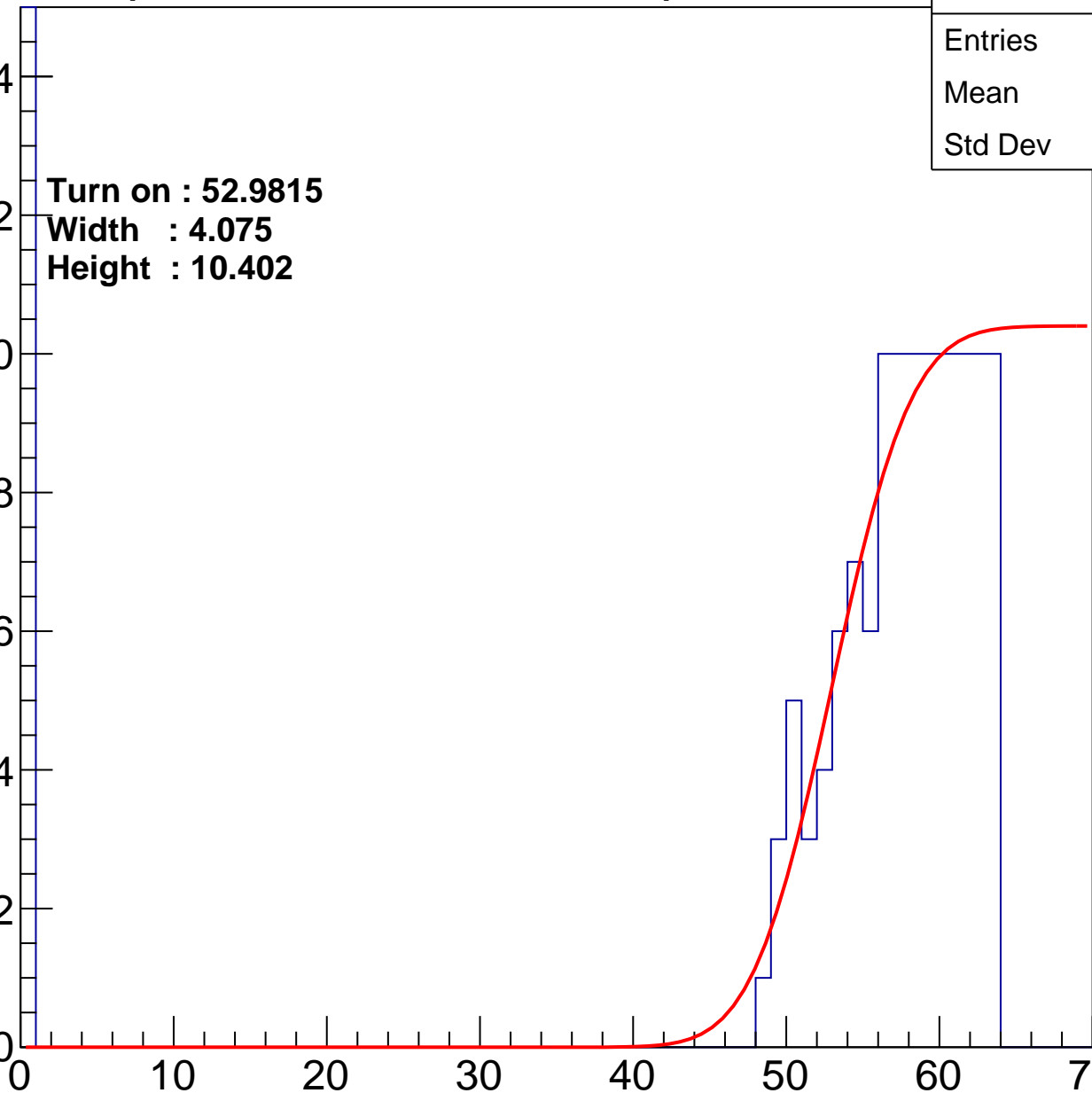
Width : 4.075

Height : 10.402

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	148
Mean	41.39
Std Dev	26.24

Turn on : 53.7745

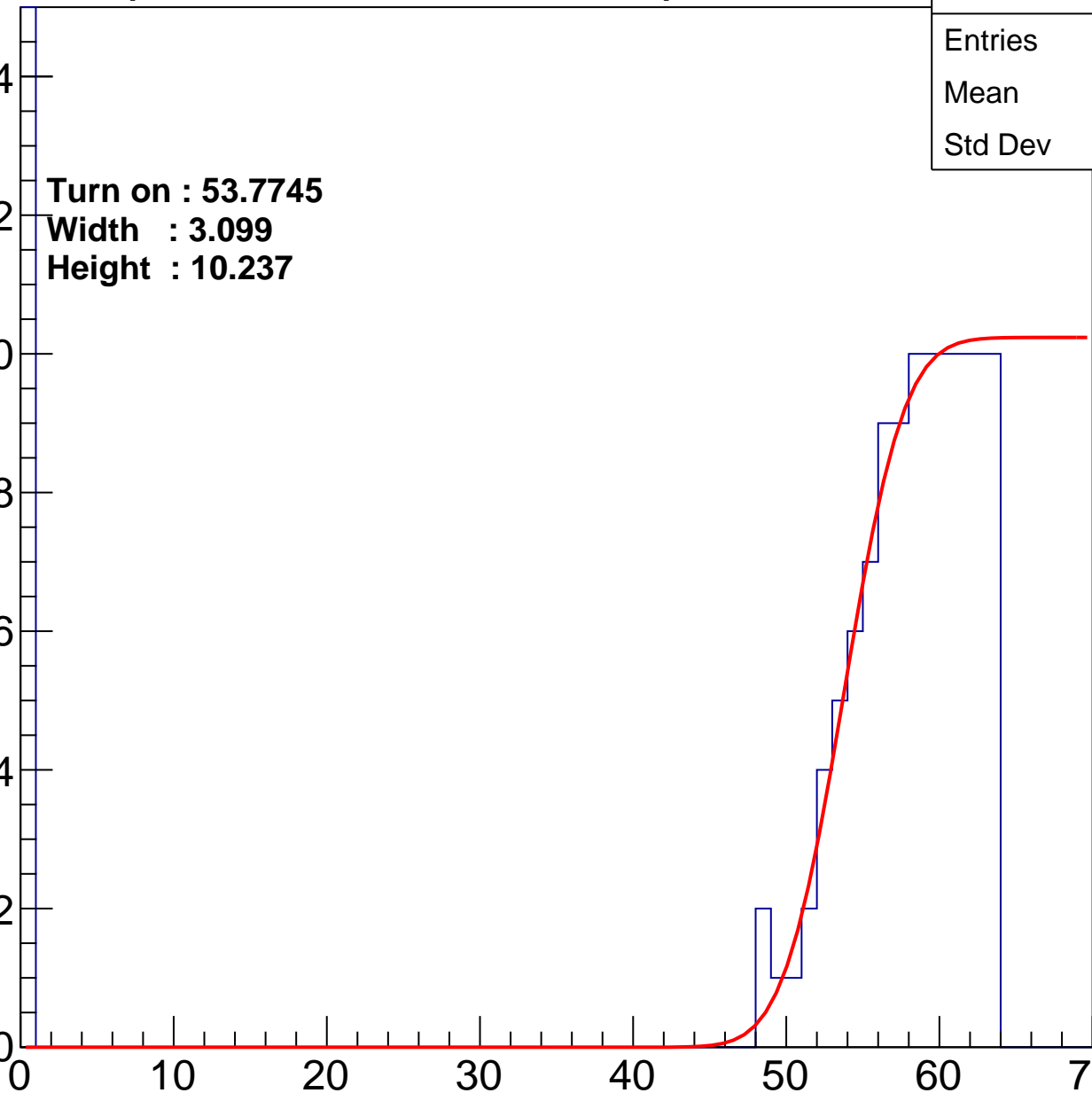
Width : 3.099

Height : 10.237

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	34.53
Std Dev	28.41

Turn on : 53.5602

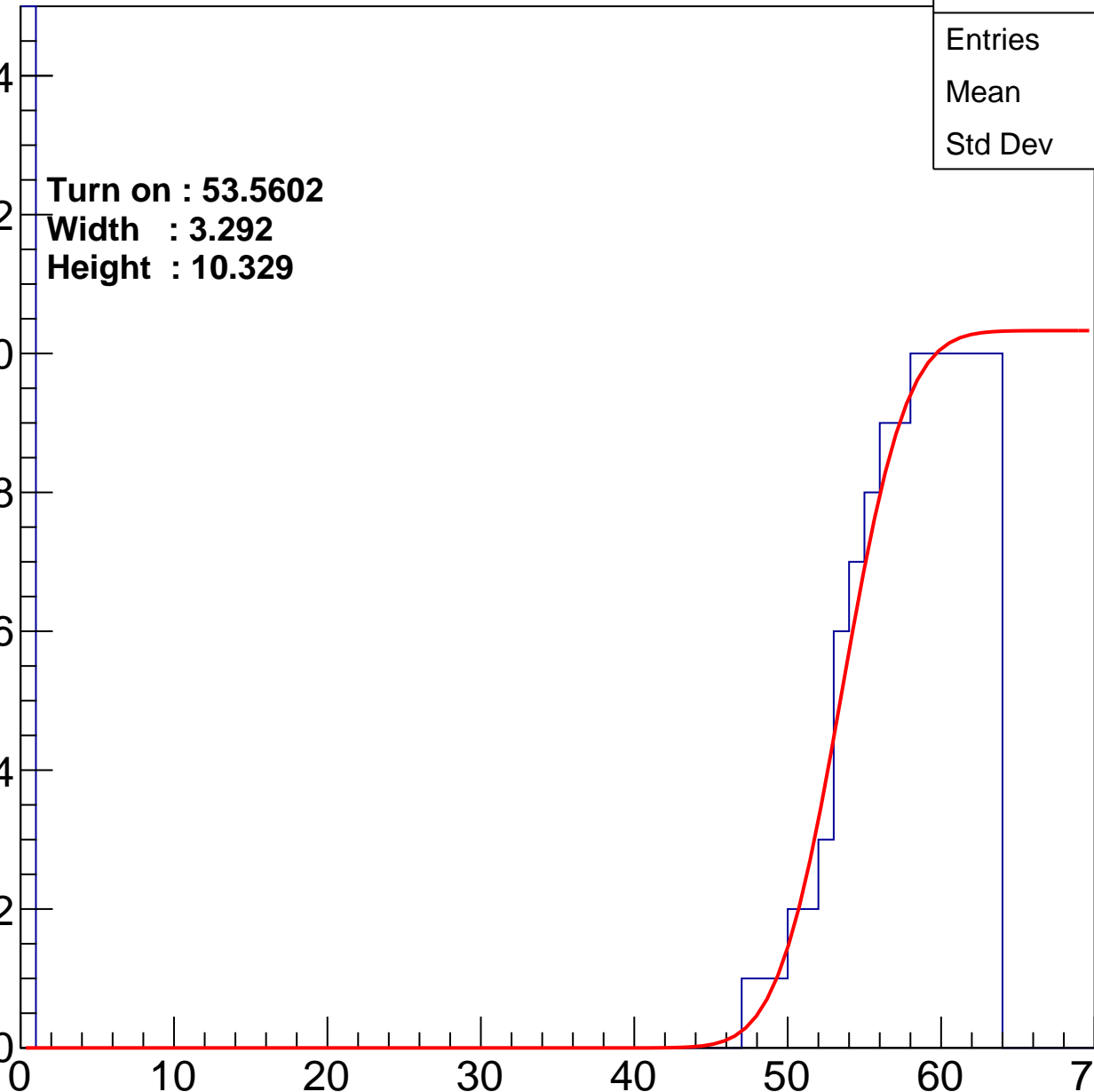
Width : 3.292

Height : 10.329

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch33

calib_packv5_033123_0516.root, FC#4, port A1

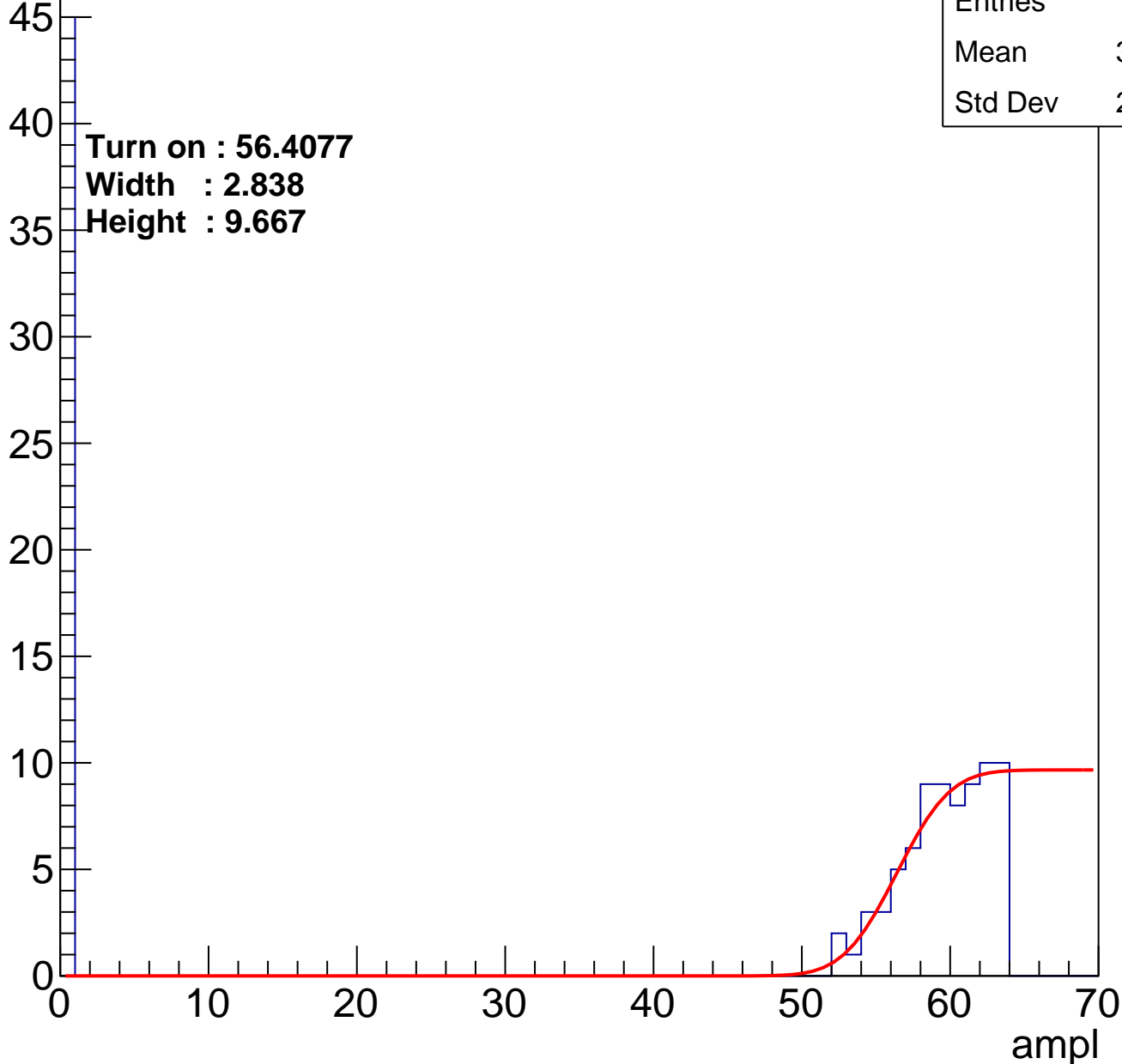
Entries	120
Mean	36.98
Std Dev	28.74

Turn on : 56.4077

Width : 2.838

Height : 9.667

Entry



B1L104S, U24-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	34.38
Std Dev	28.88

Turn on : 55.0790

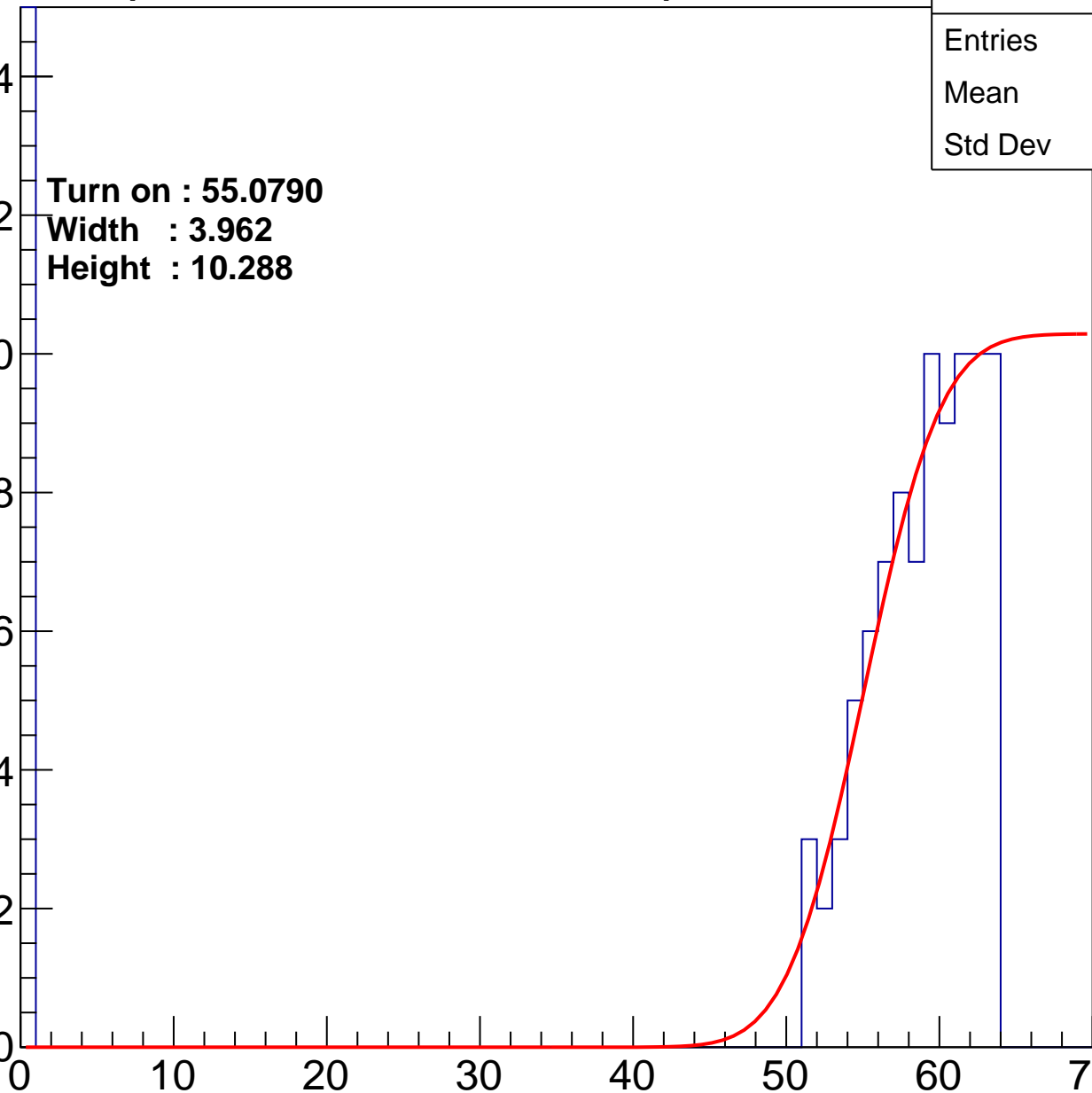
Width : 3.962

Height : 10.288

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch35

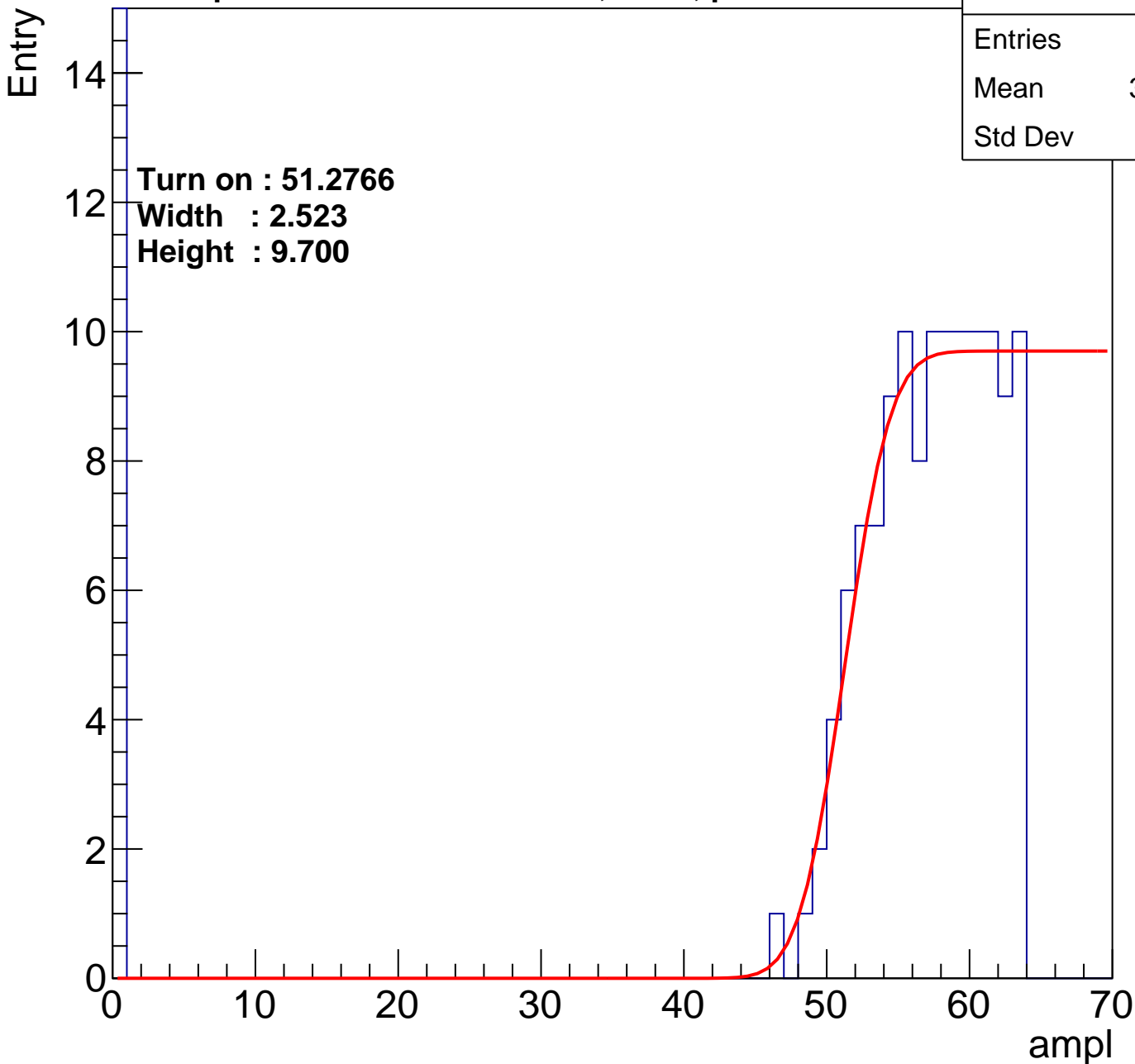
calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	38.55
Std Dev	26.8

Turn on : 51.2766

Width : 2.523

Height : 9.700



B1L104S, U24-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	36.68
Std Dev	28.01

Turn on : 53.9181

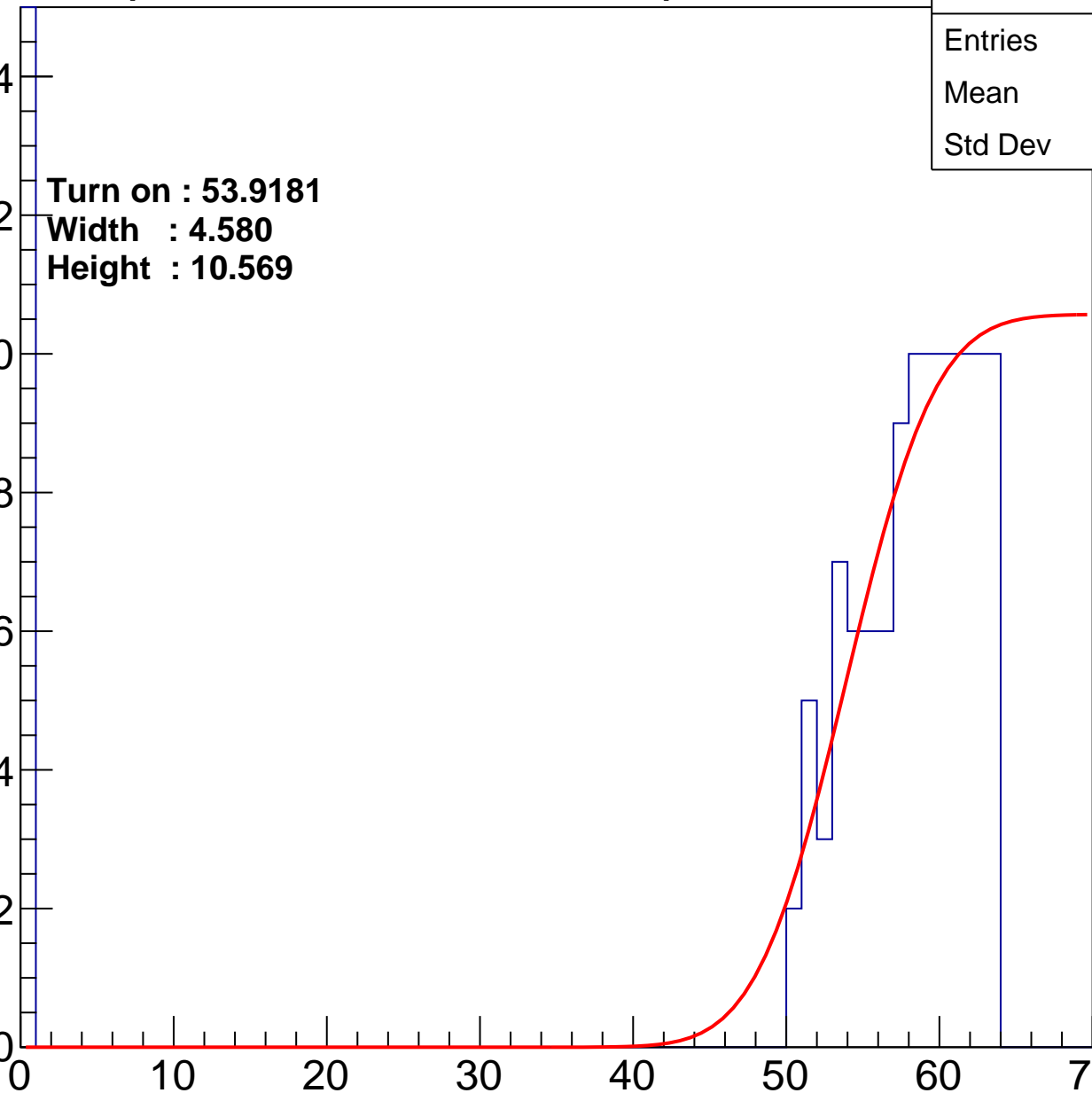
Width : 4.580

Height : 10.569

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	32.76
Std Dev	29

Turn on : 55.3846

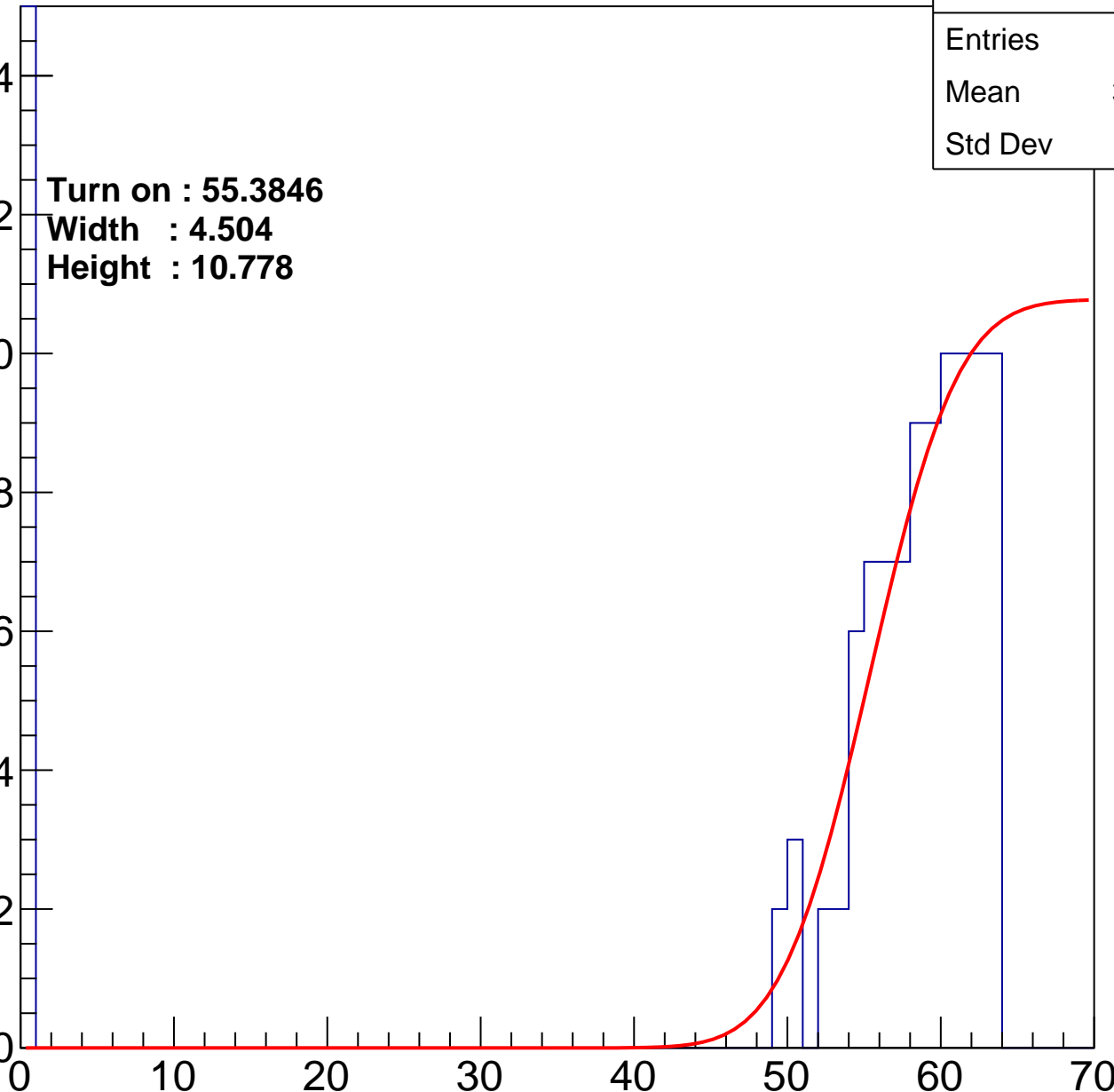
Width : 4.504

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch38

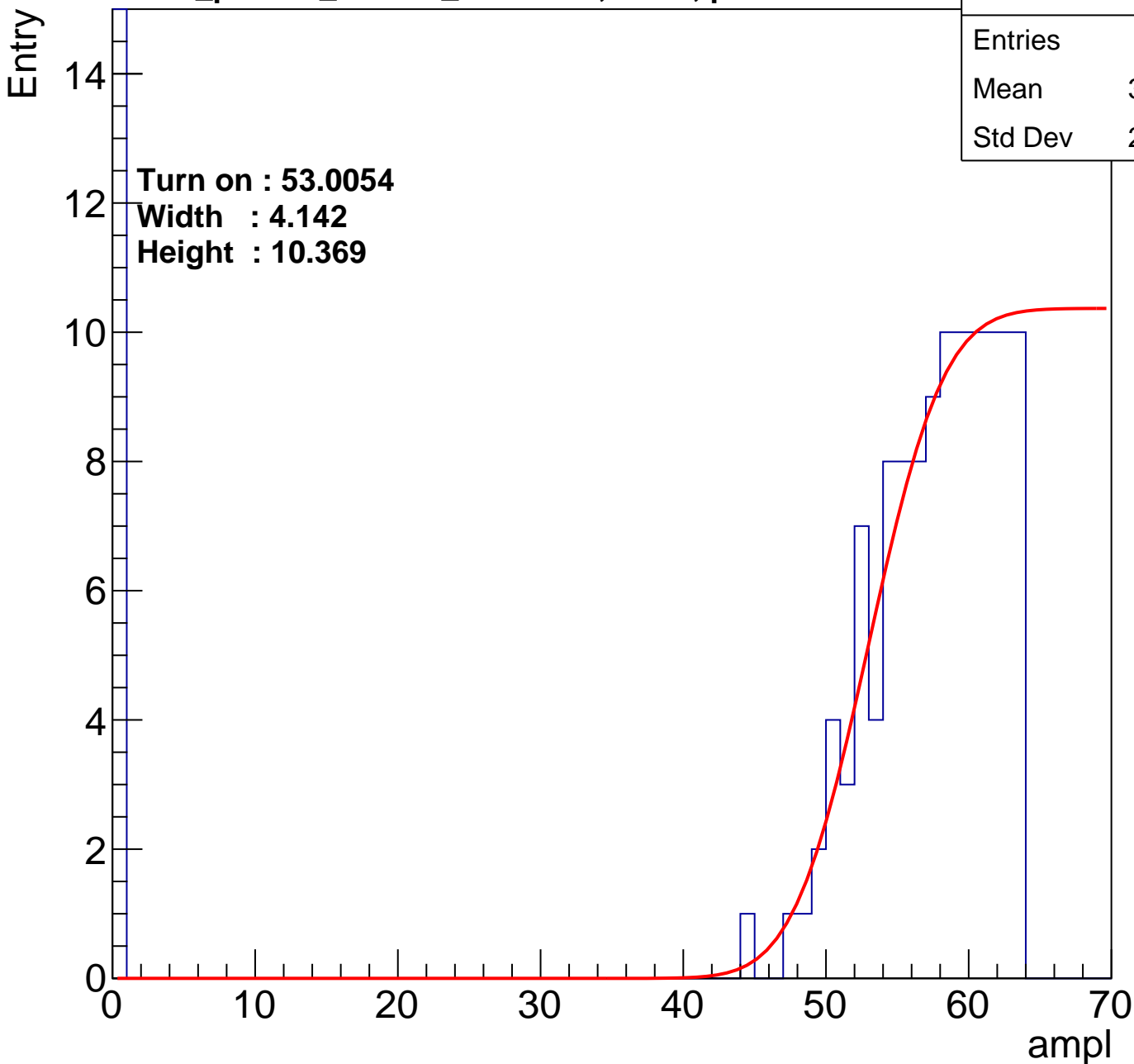
calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	37.24
Std Dev	27.44

Turn on : 53.0054

Width : 4.142

Height : 10.369



B1L104S, U24-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	33.55
Std Dev	28.62

Turn on : 54.6753

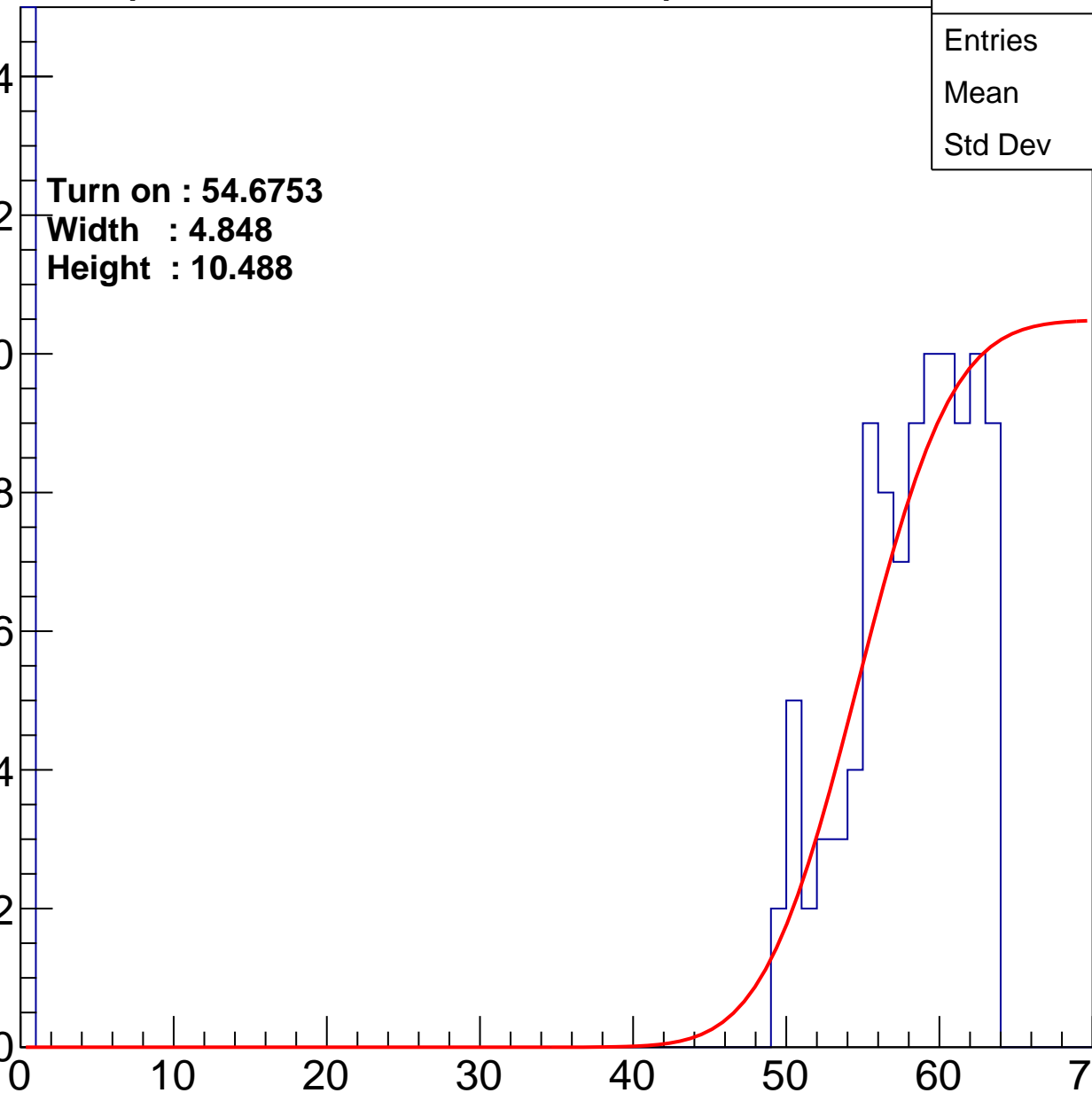
Width : 4.848

Height : 10.488

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	35.82
Std Dev	28.16

Turn on : 53.2168

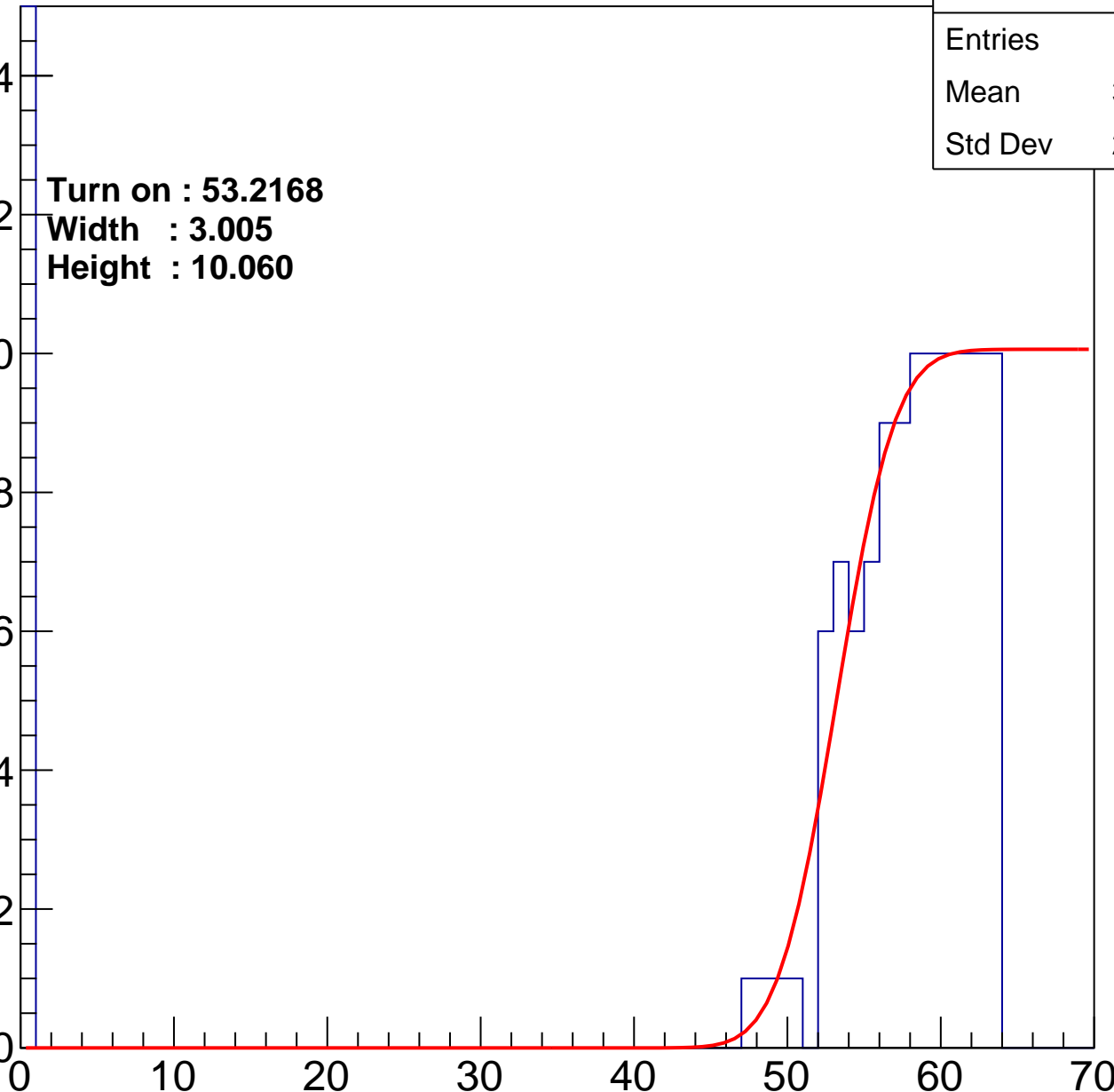
Width : 3.005

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch41

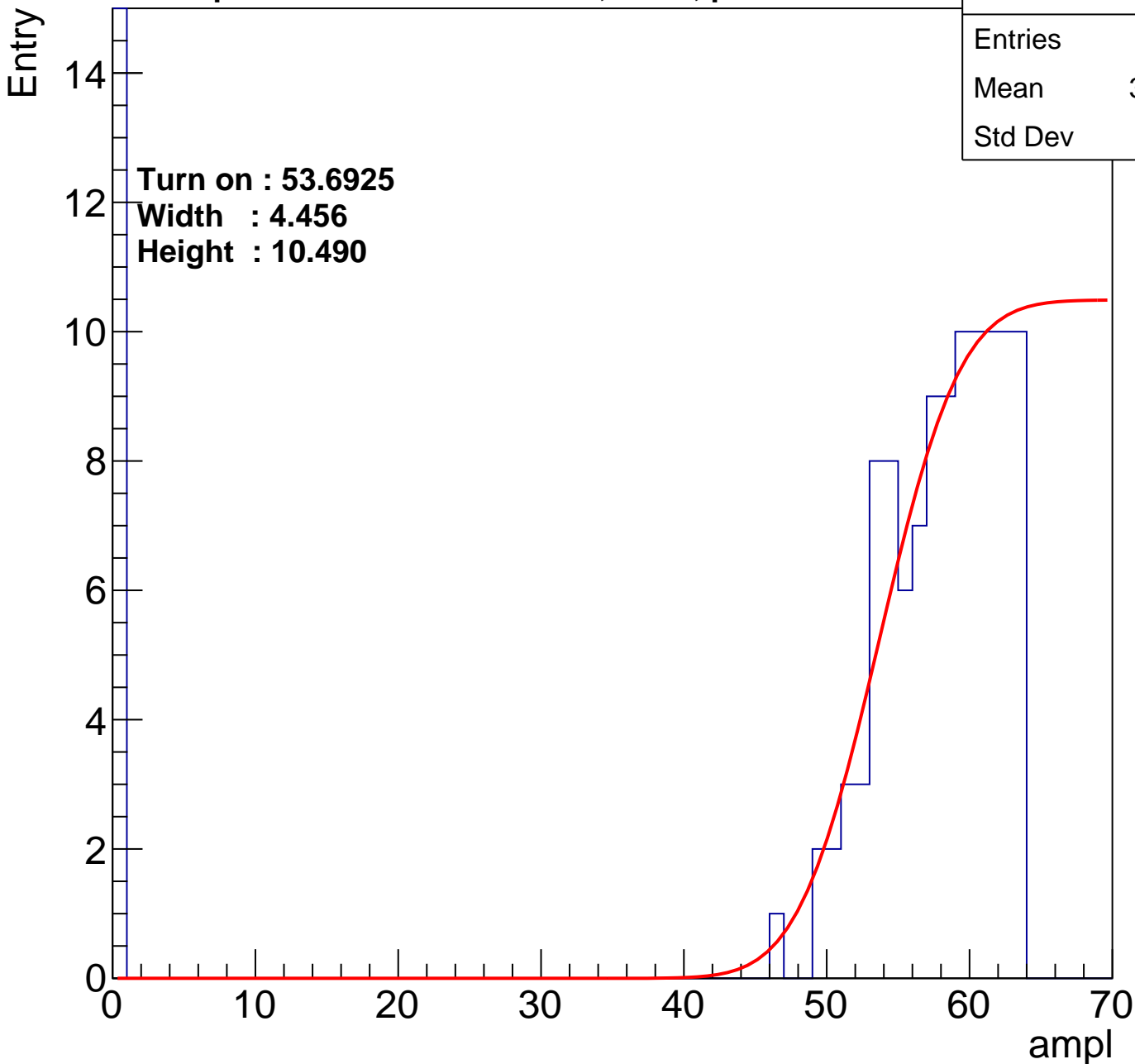
calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	33.78
Std Dev	28.5

Turn on : 53.6925

Width : 4.456

Height : 10.490



B1L104S, U24-ch42

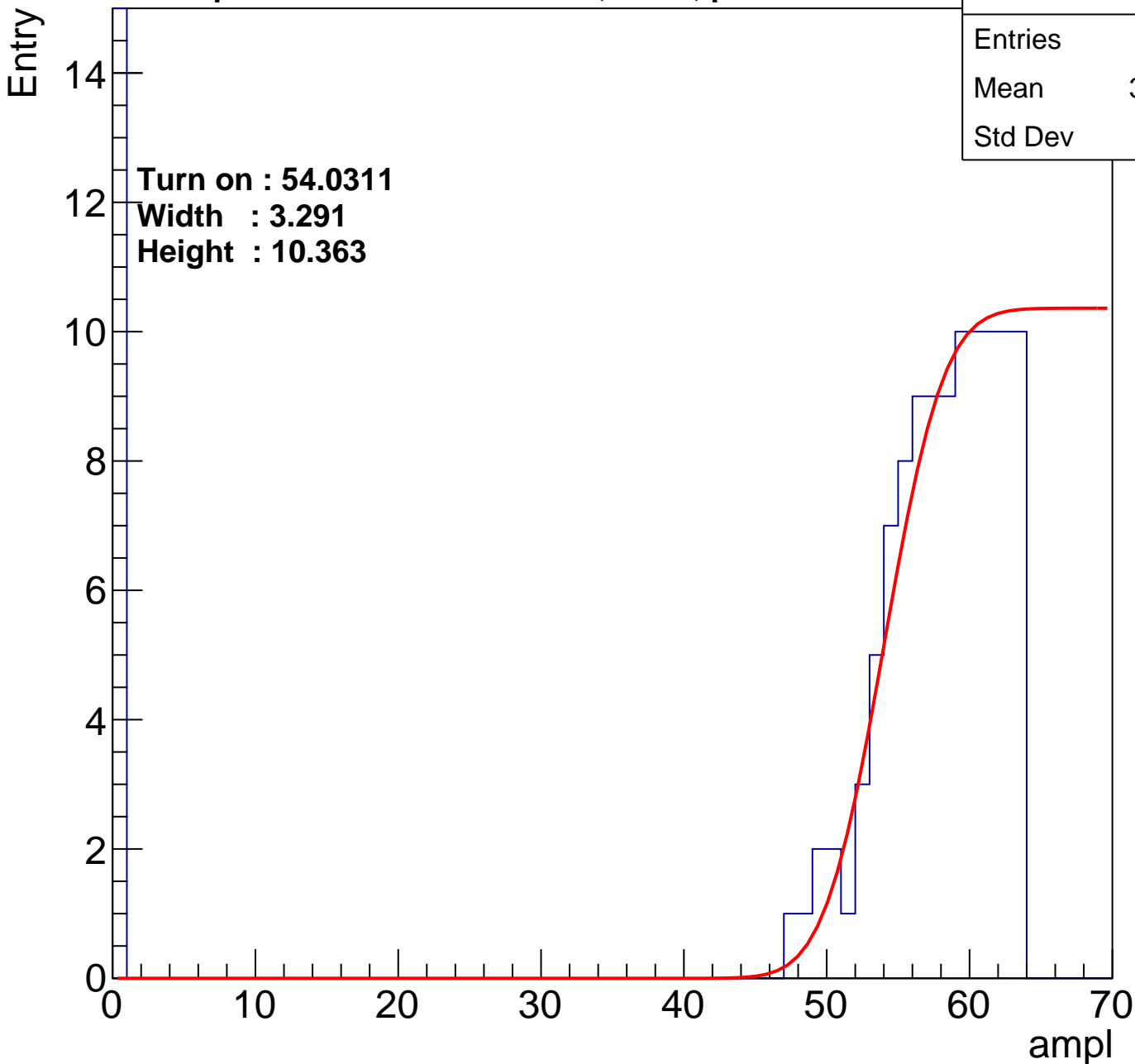
calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	31.02
Std Dev	28.9

Turn on : 54.0311

Width : 3.291

Height : 10.363



B1L104S, U24-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	37.69
Std Dev	27.32

Turn on : 53.3922

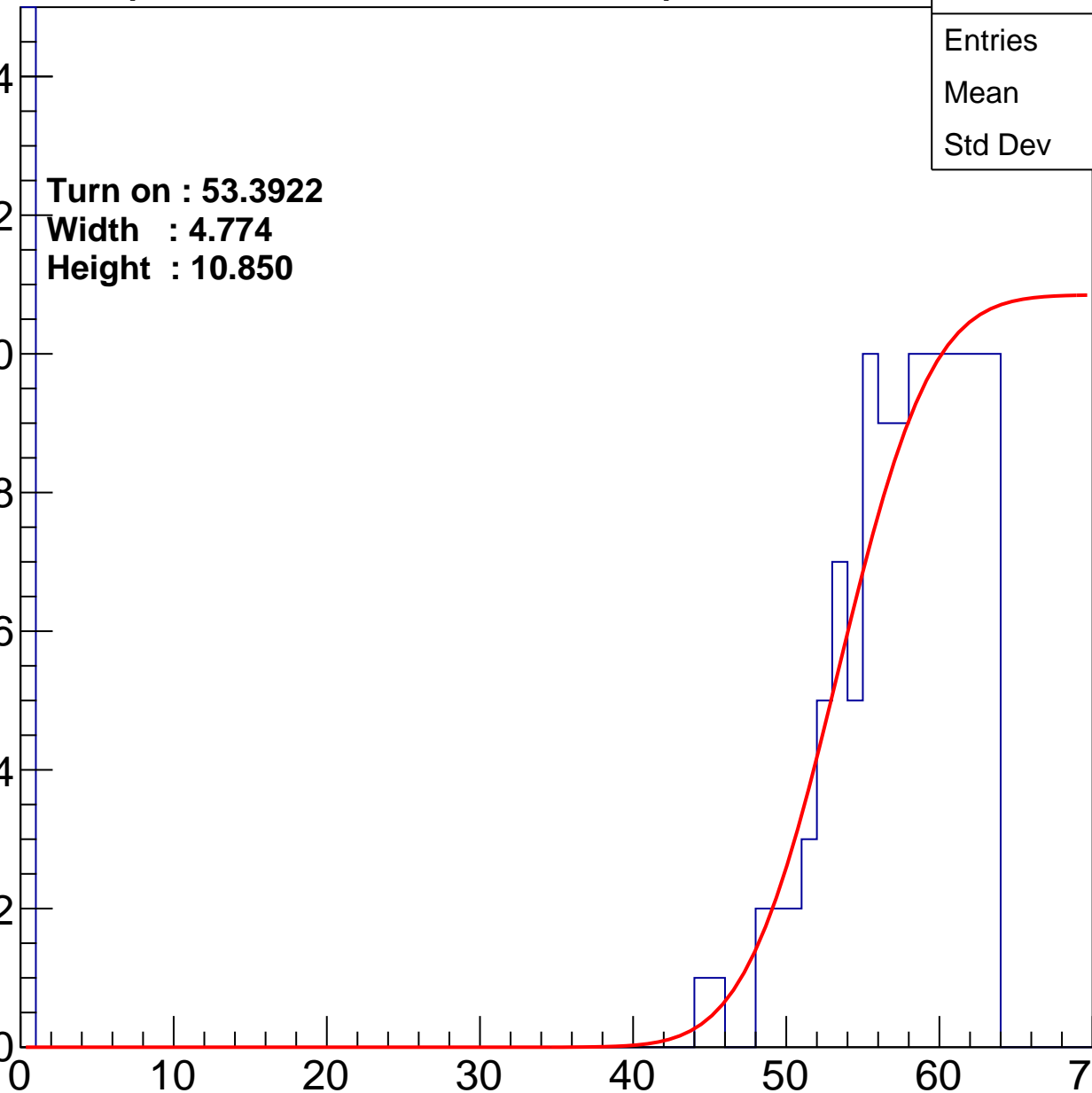
Width : 4.774

Height : 10.850

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	38.51
Std Dev	26.87

Turn on : 51.4161

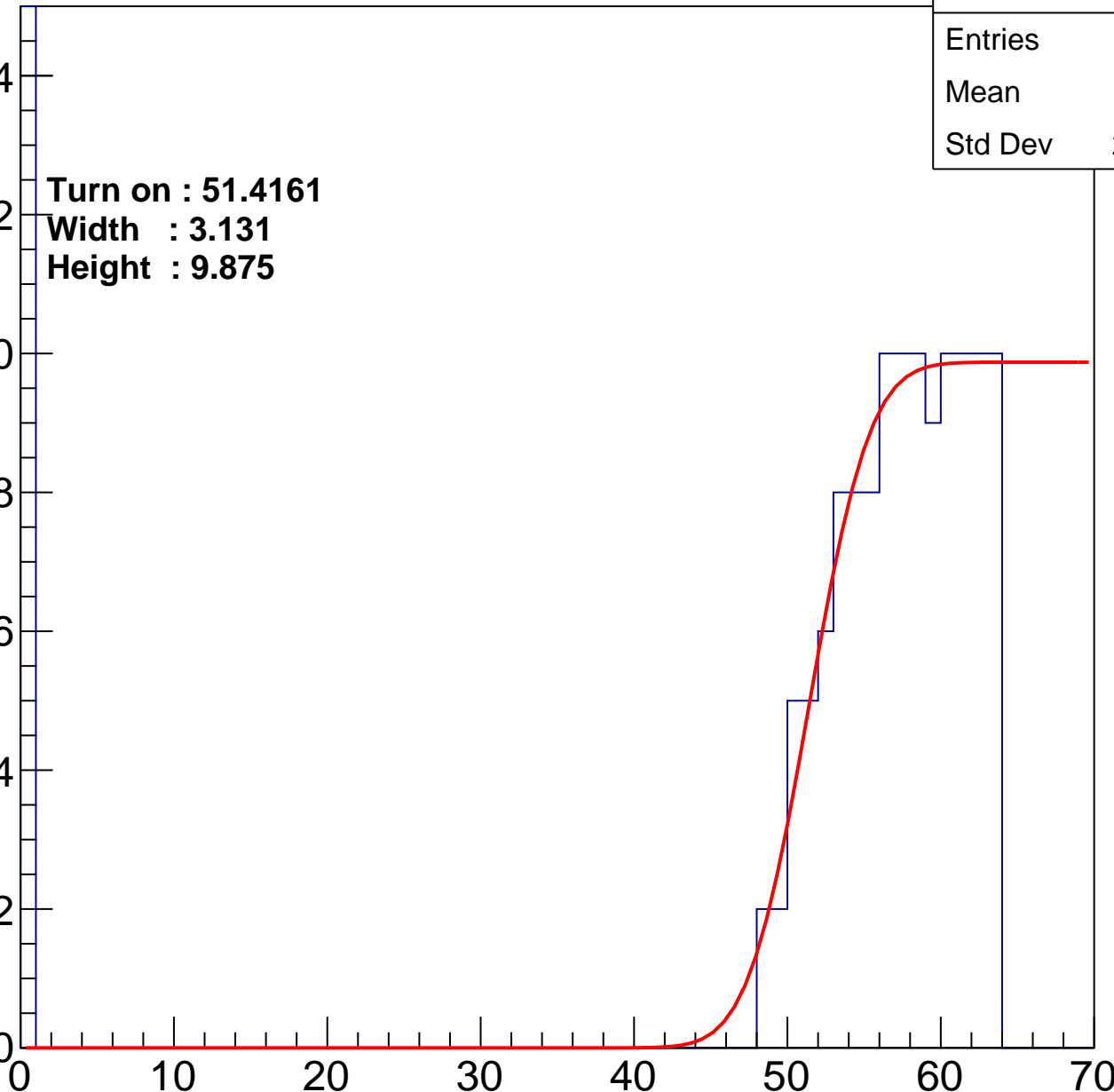
Width : 3.131

Height : 9.875

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch45

calib_packv5_033123_0516.root, FC#4, port A1

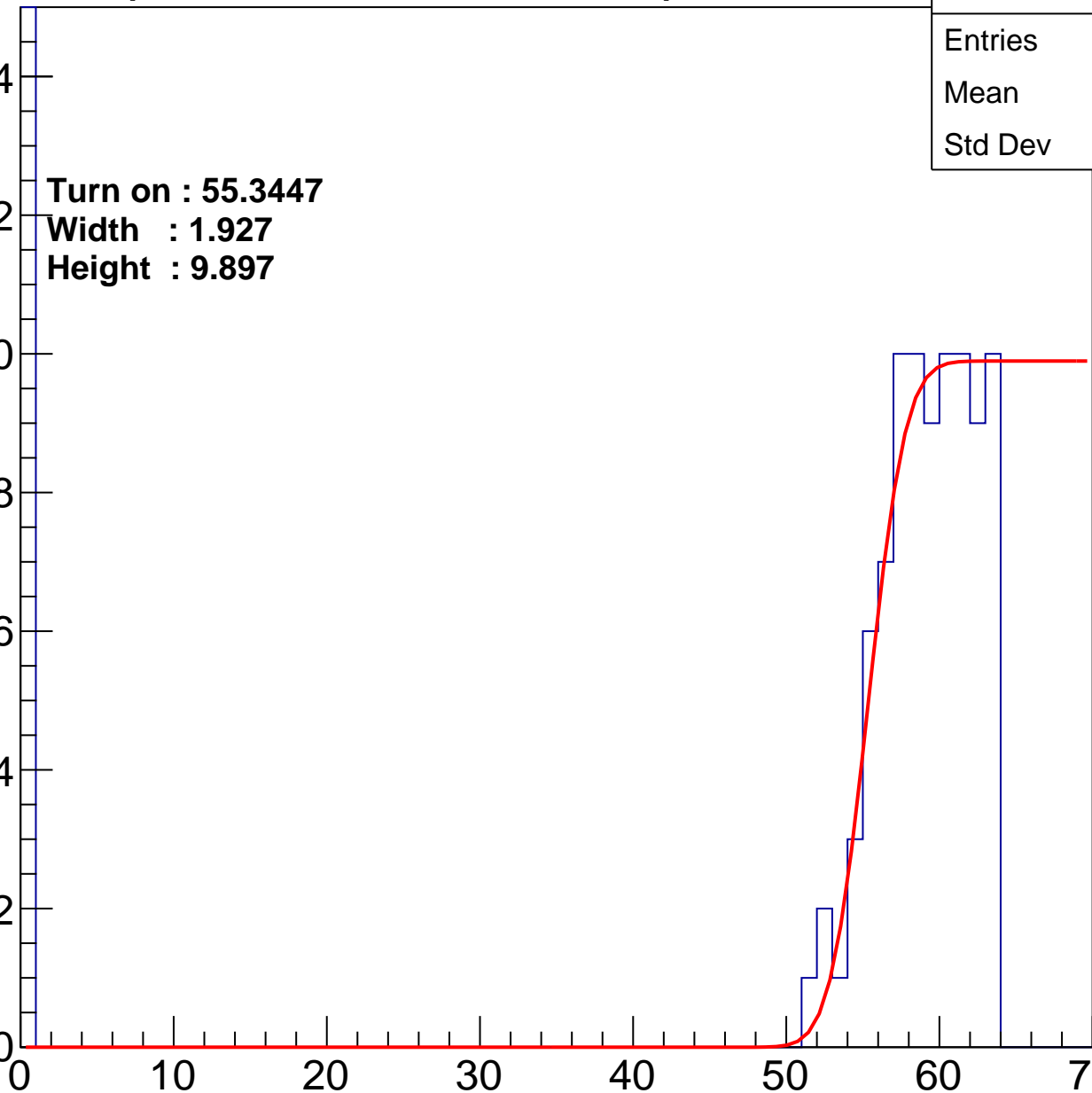
Entry

14
12
10
8
6
4
2
0

Turn on : 55.3447
Width : 1.927
Height : 9.897

Entries	146
Mean	35.42
Std Dev	28.85

ampl



B1L104S, U24-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.92
Std Dev	28.44

Turn on : 53.8234

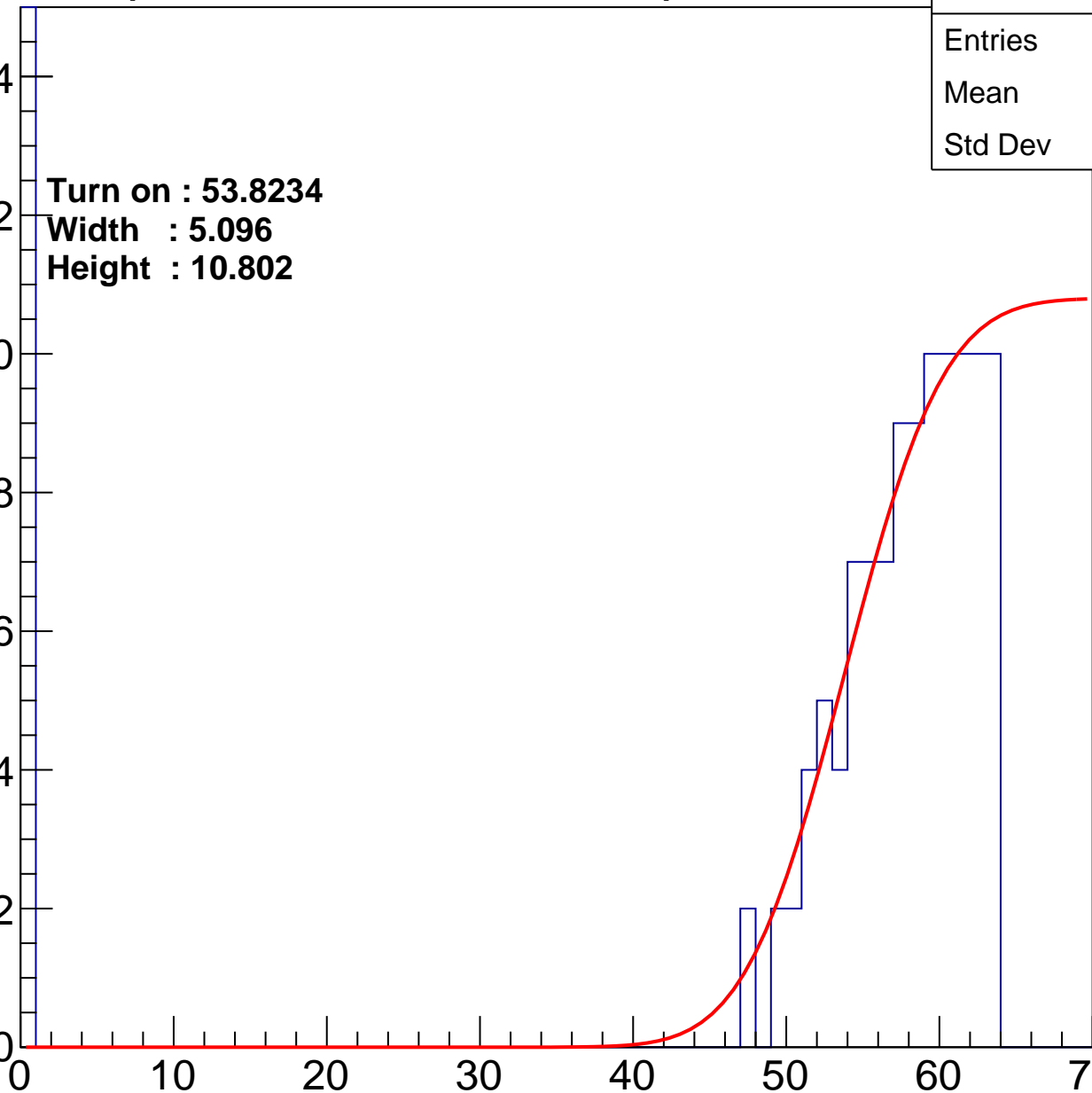
Width : 5.096

Height : 10.802

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	36.32
Std Dev	28.31

Turn on : 54.4683

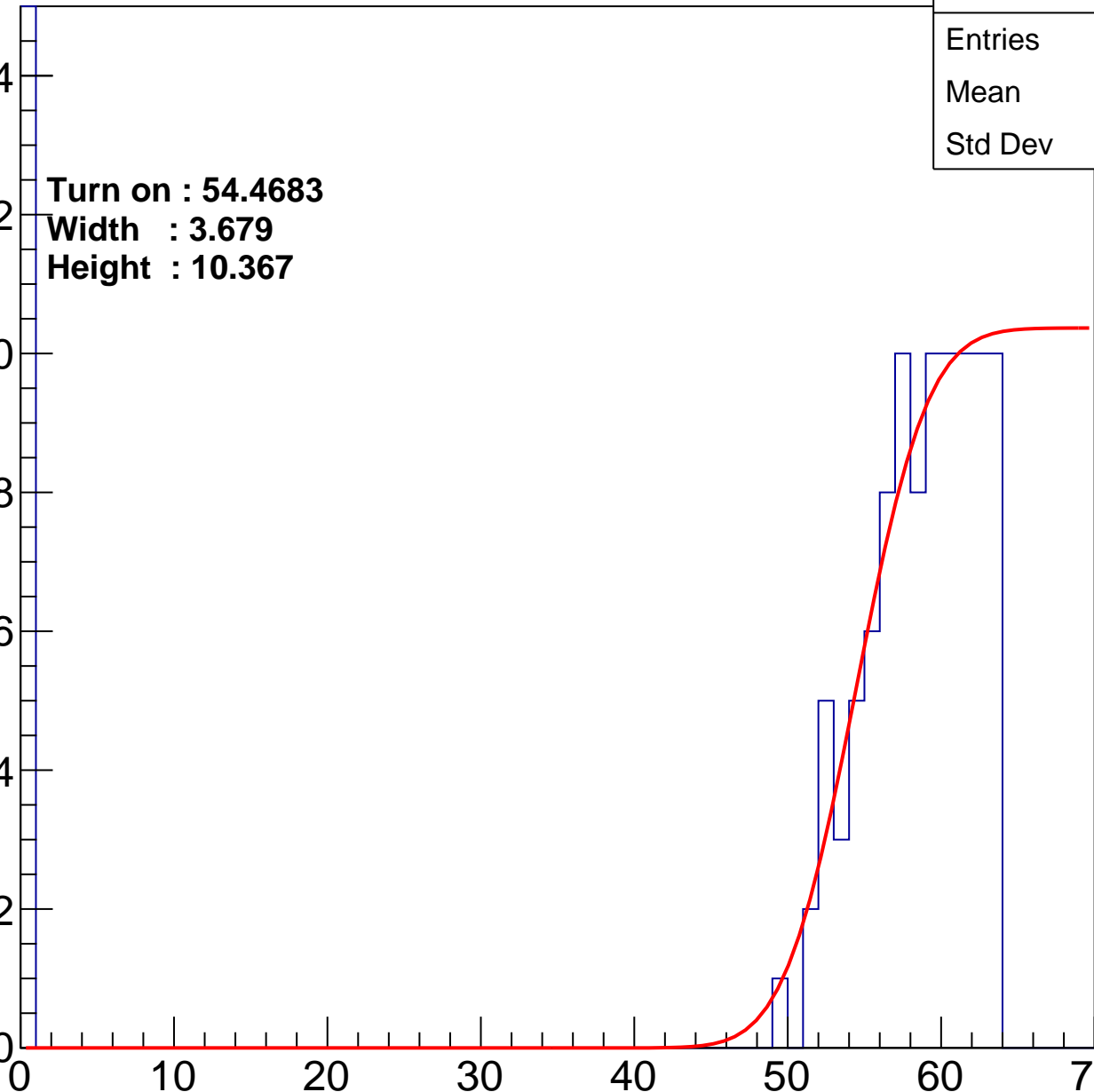
Width : 3.679

Height : 10.367

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	35.57
Std Dev	28.55

Turn on : 55.1231

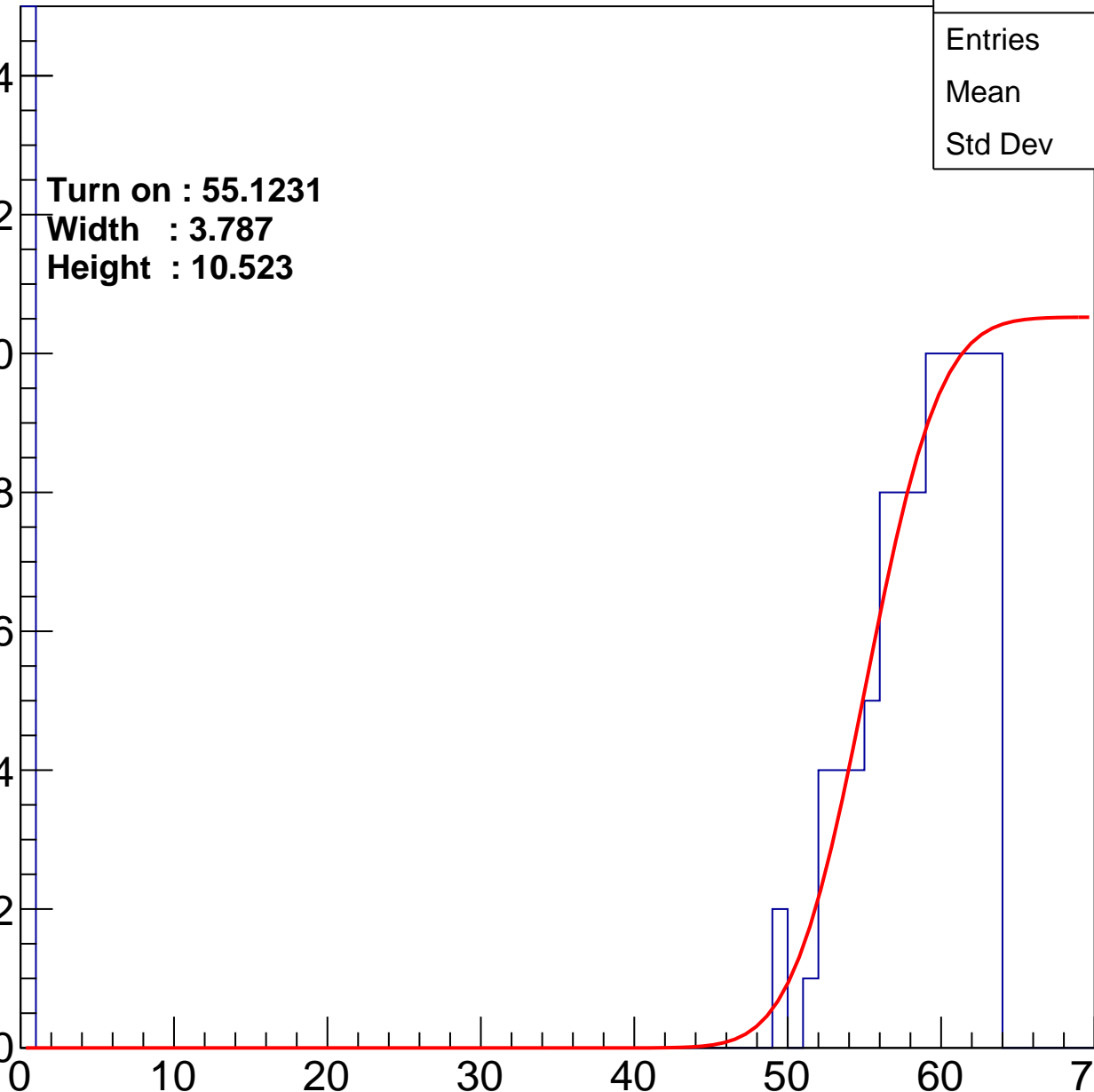
Width : 3.787

Height : 10.523

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch49

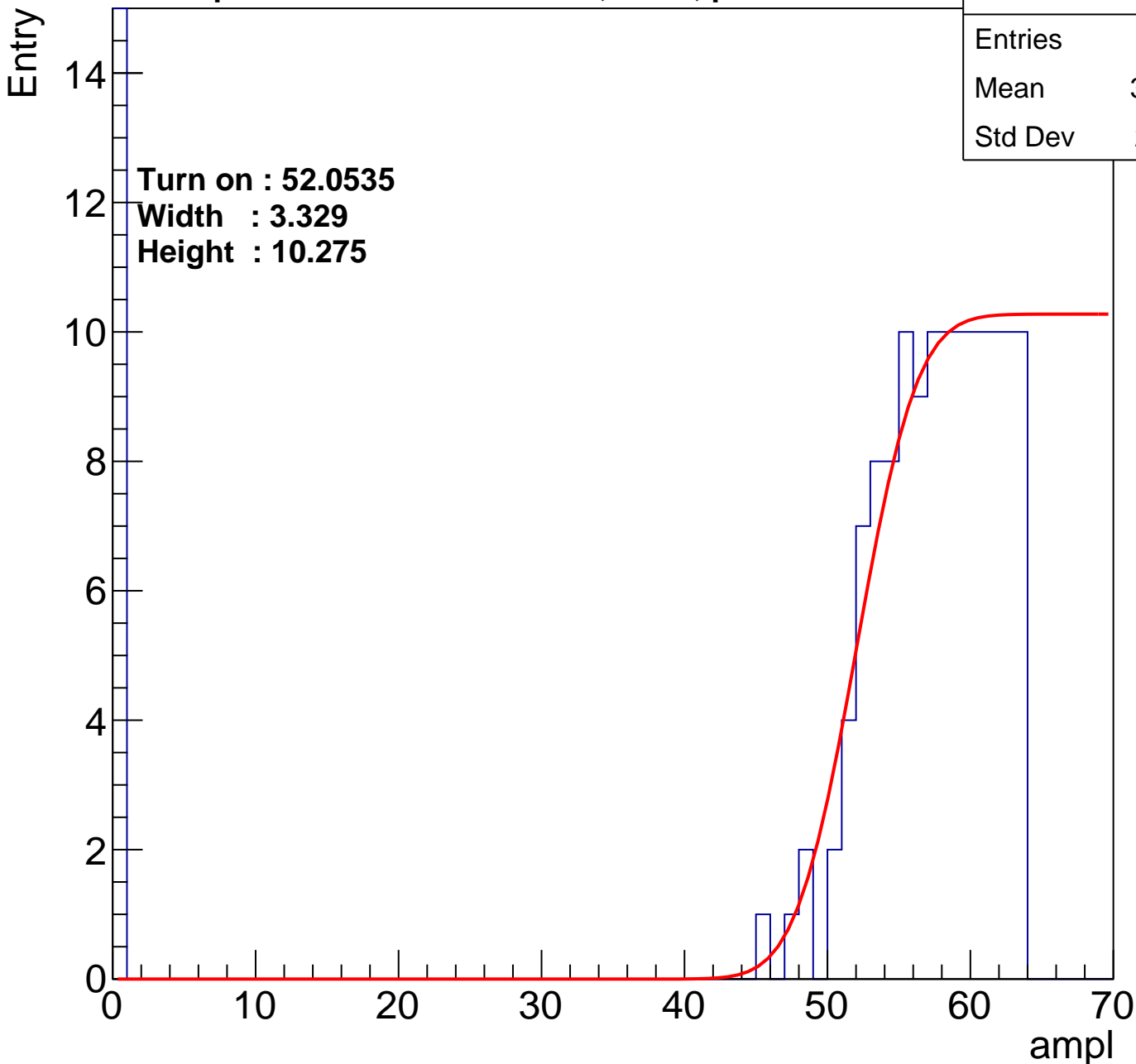
calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	33.33
Std Dev	28.31

Turn on : 52.0535

Width : 3.329

Height : 10.275



B1L104S, U24-ch50

calib_packv5_033123_0516.root, FC#4, port A1

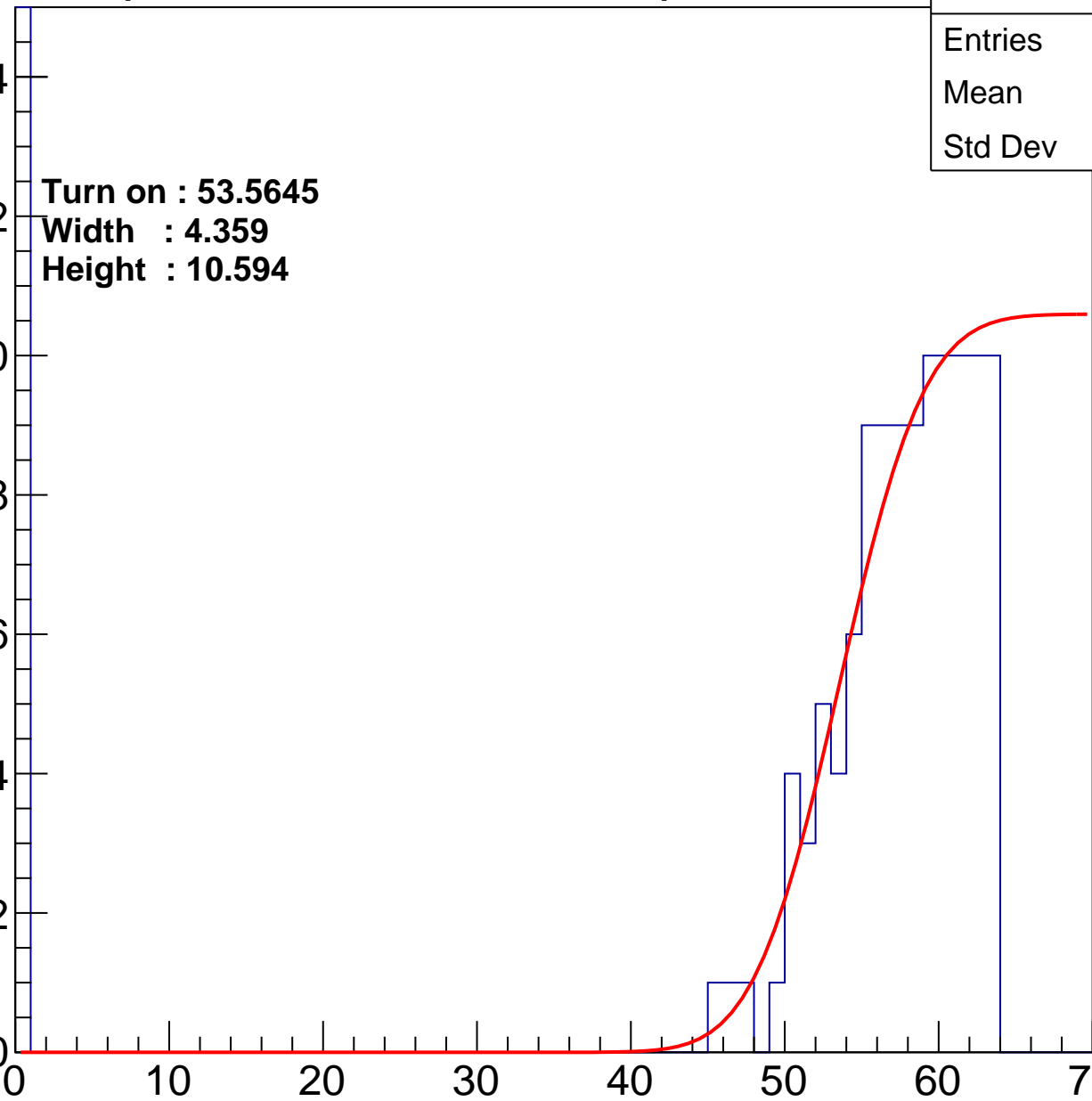
Entry

14
12
10
8
6
4
2
0

Turn on : 53.5645
Width : 4.359
Height : 10.594

Entries	211
Mean	30.43
Std Dev	28.76

ampl



B1L104S, U24-ch51

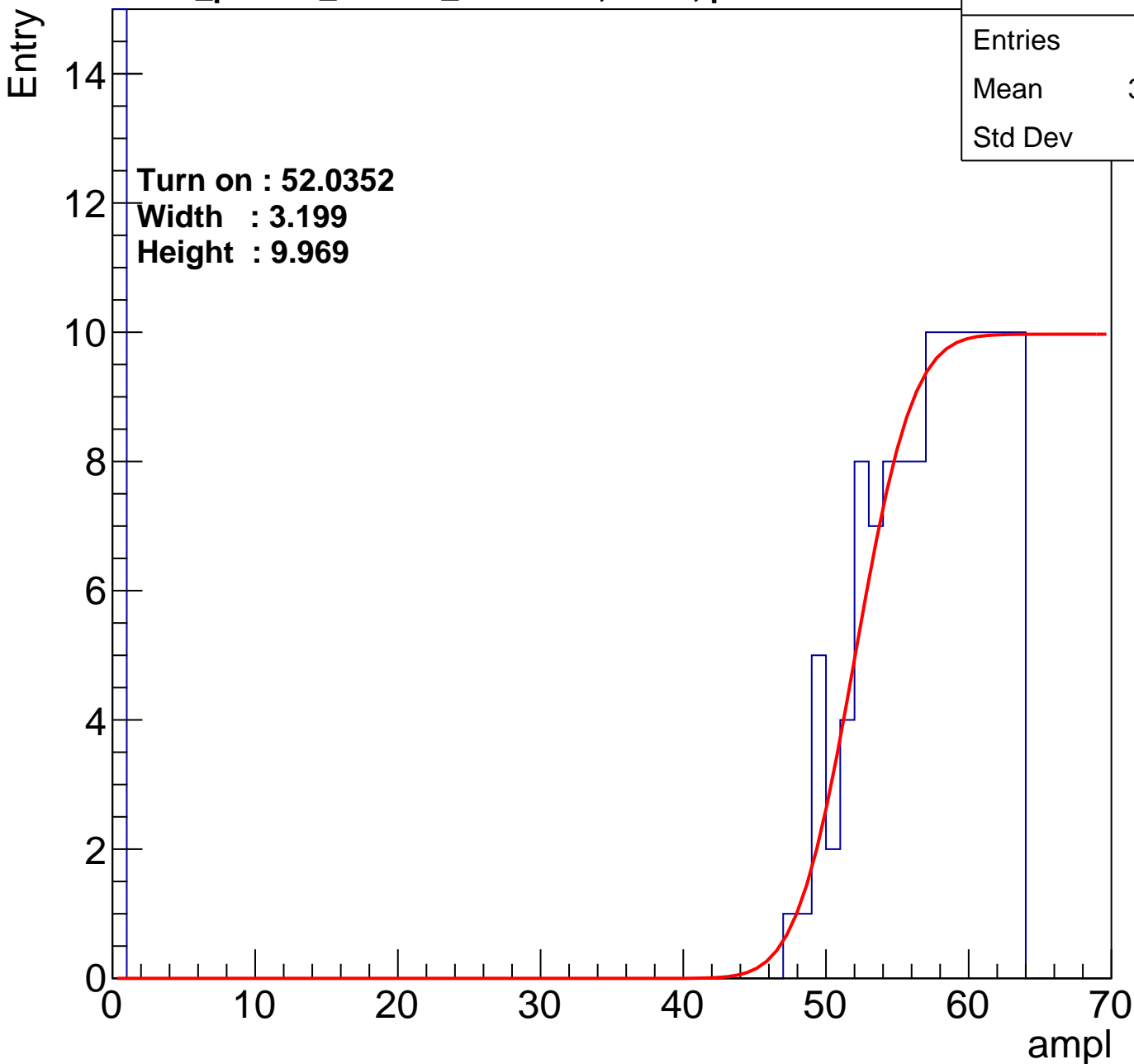
calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	39.49
Std Dev	26.5

Turn on : 52.0352

Width : 3.199

Height : 9.969



B1L104S, U24-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	28.83
Std Dev	29.45

Turn on : 56.3712

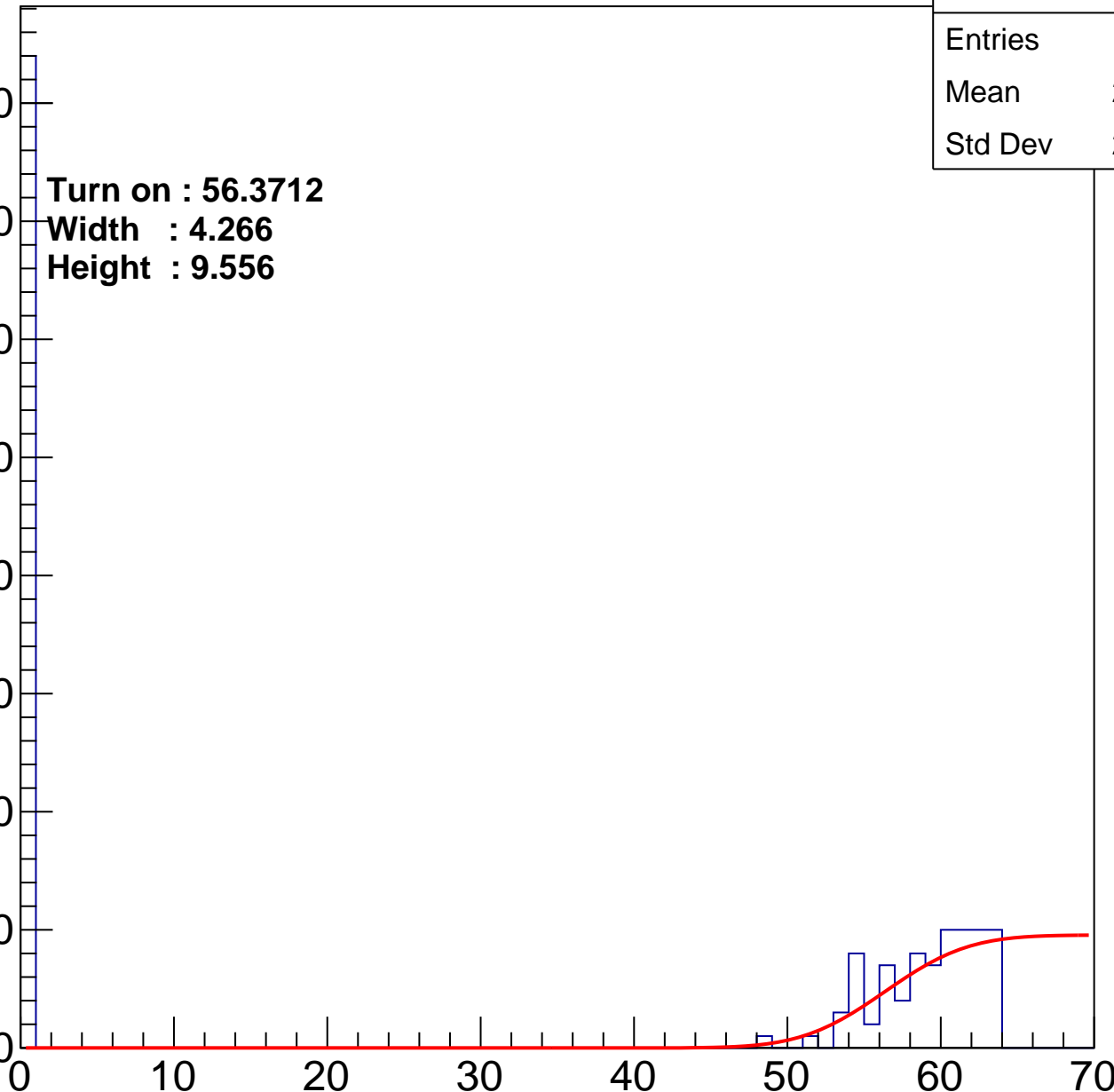
Width : 4.266

Height : 9.556

Entry

80
70
60
50
40
30
20
10
0

ampl



B1L104S, U24-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	35.35
Std Dev	28.6

Turn on : 55.0342

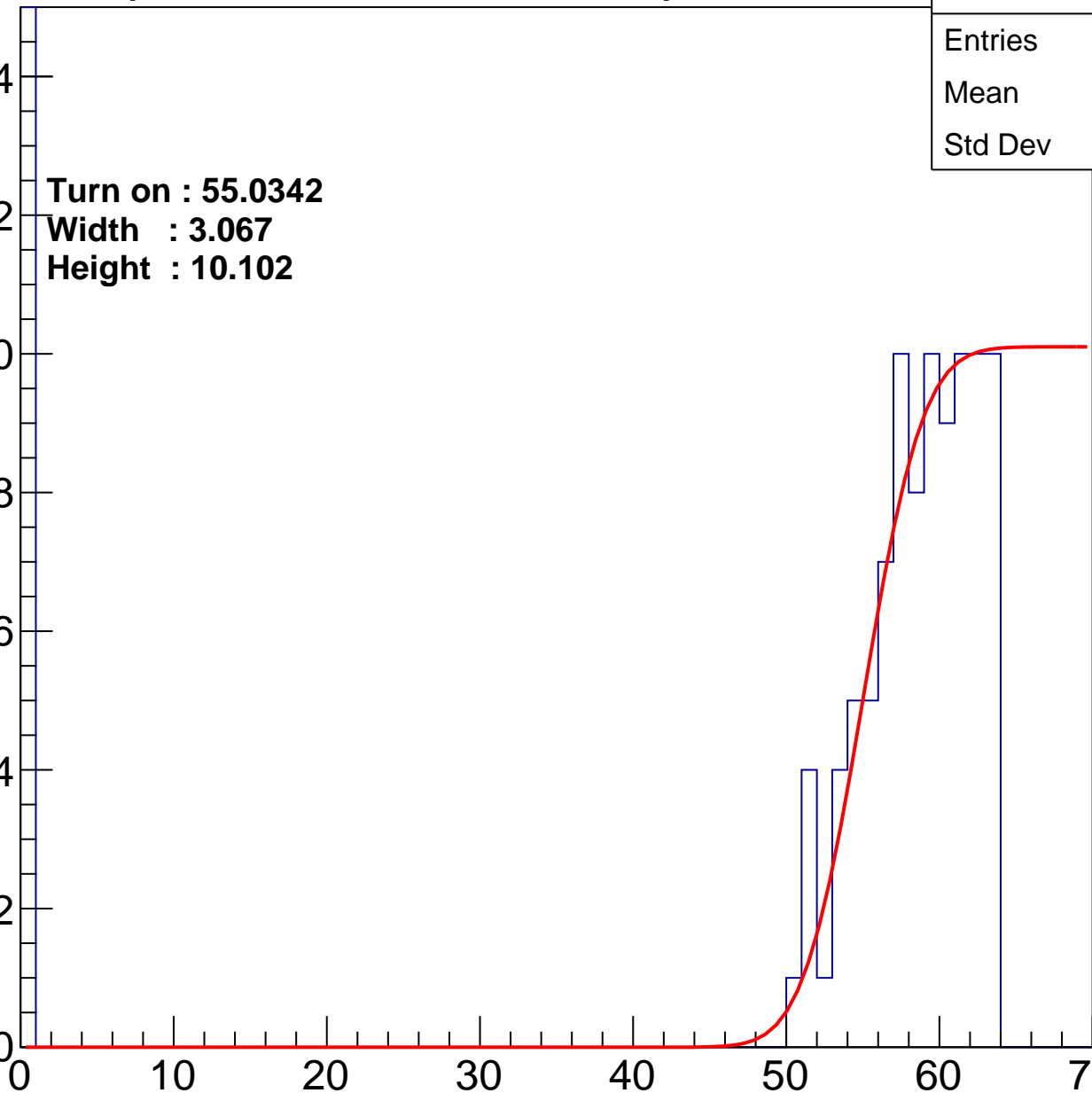
Width : 3.067

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	33.67
Std Dev	28.8

Turn on : 54.0579

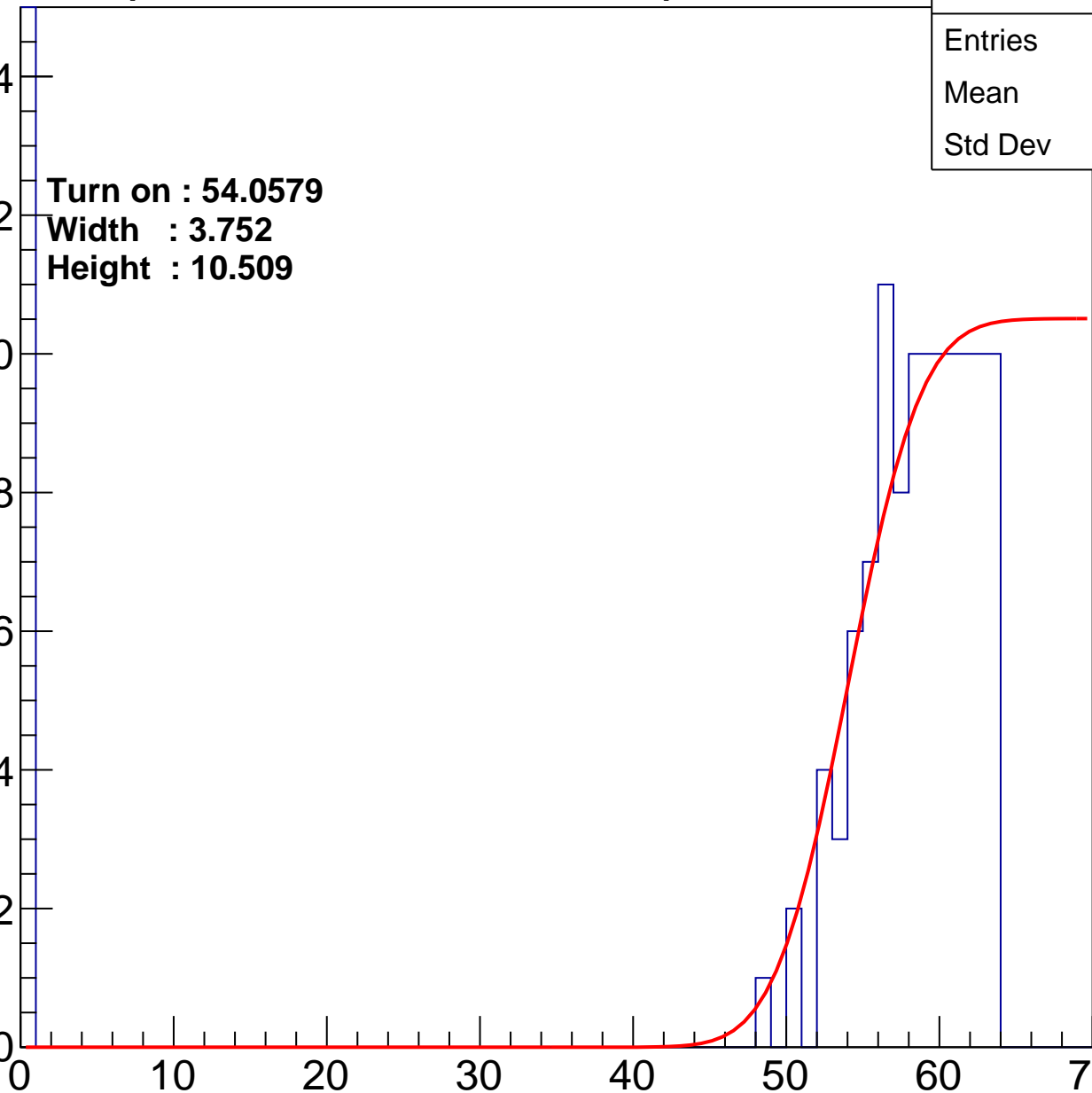
Width : 3.752

Height : 10.509

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	39.65
Std Dev	26.99

Turn on : 53.8796

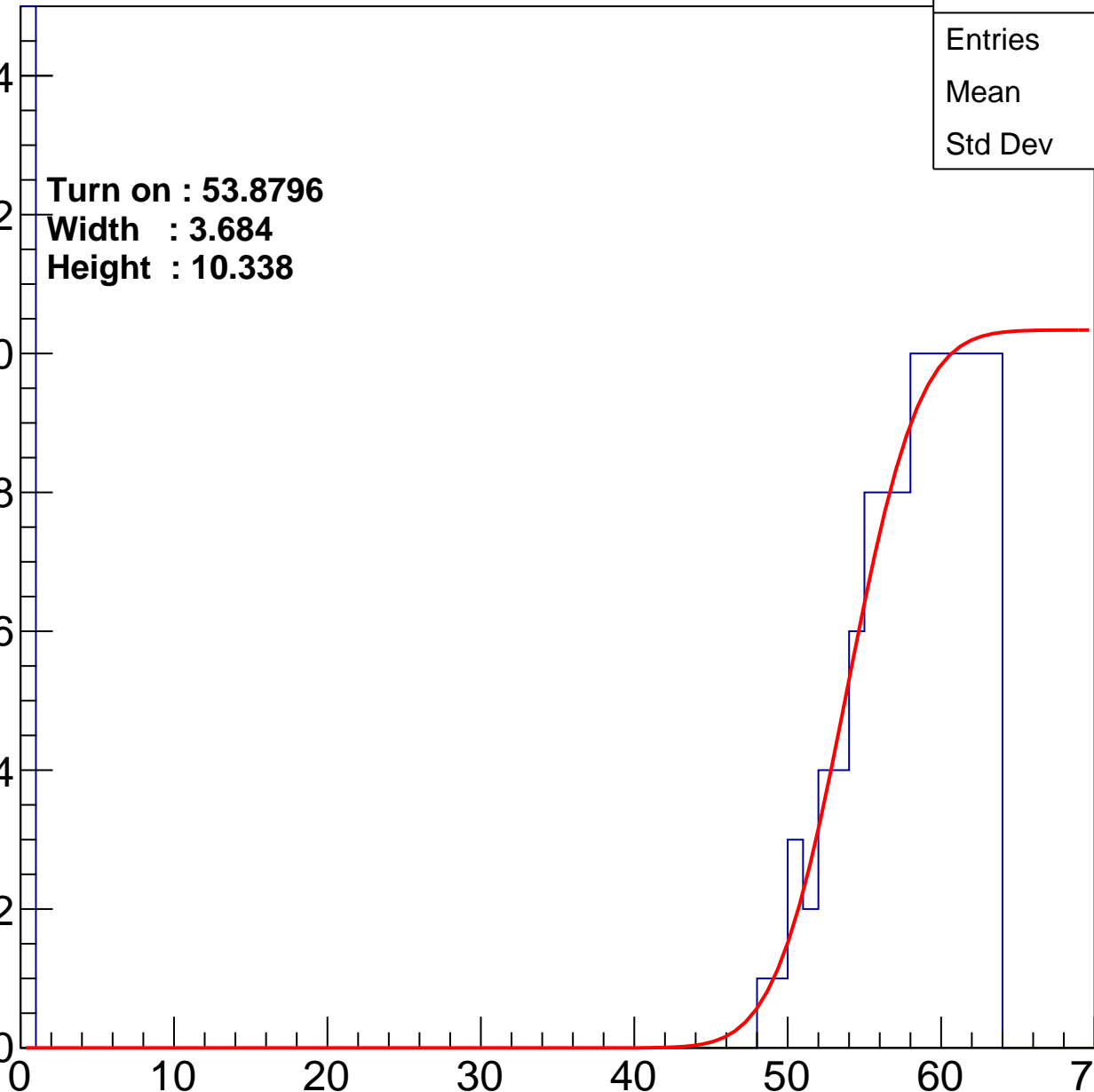
Width : 3.684

Height : 10.338

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch56

calib_packv5_033123_0516.root, FC#4, port A1

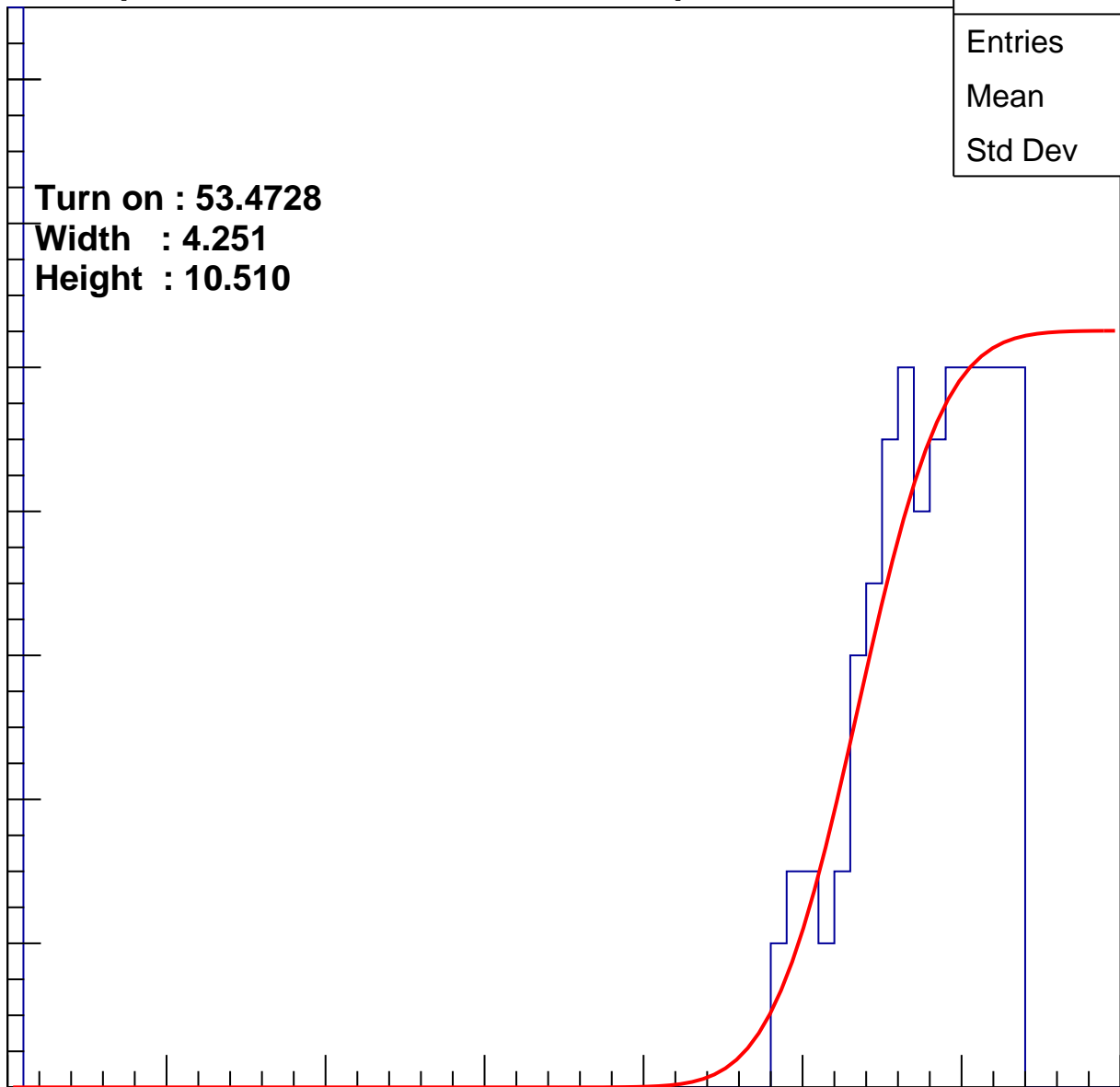
Entry

14
12
10
8
6
4
2
0

Turn on : 53.4728
Width : 4.251
Height : 10.510

Entries	196
Mean	32.81
Std Dev	28.57

ampl



B1L104S, U24-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.04
Std Dev	28.51

Turn on : 52.6736

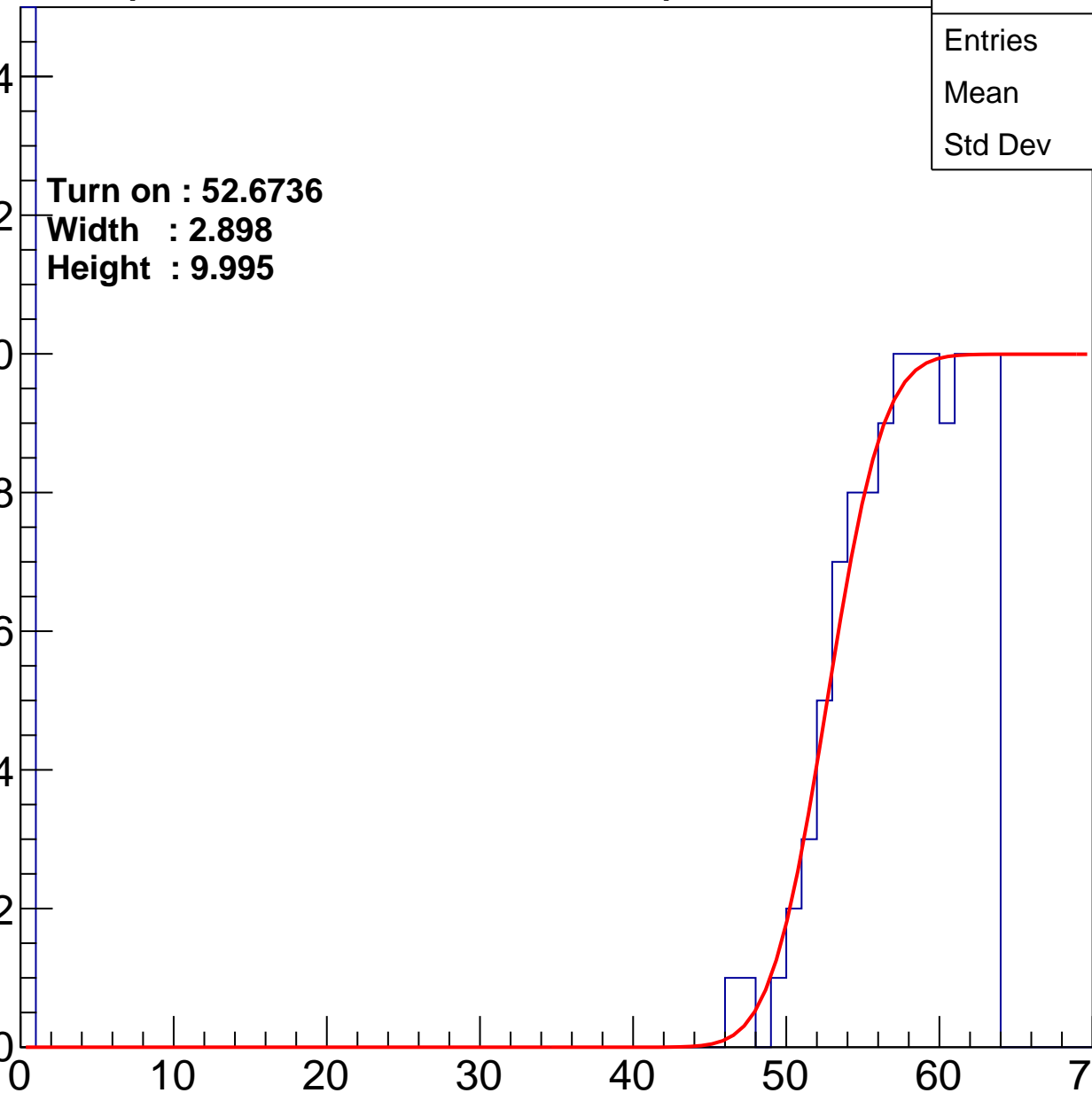
Width : 2.898

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch58

calib_packv5_033123_0516.root, FC#4, port A1

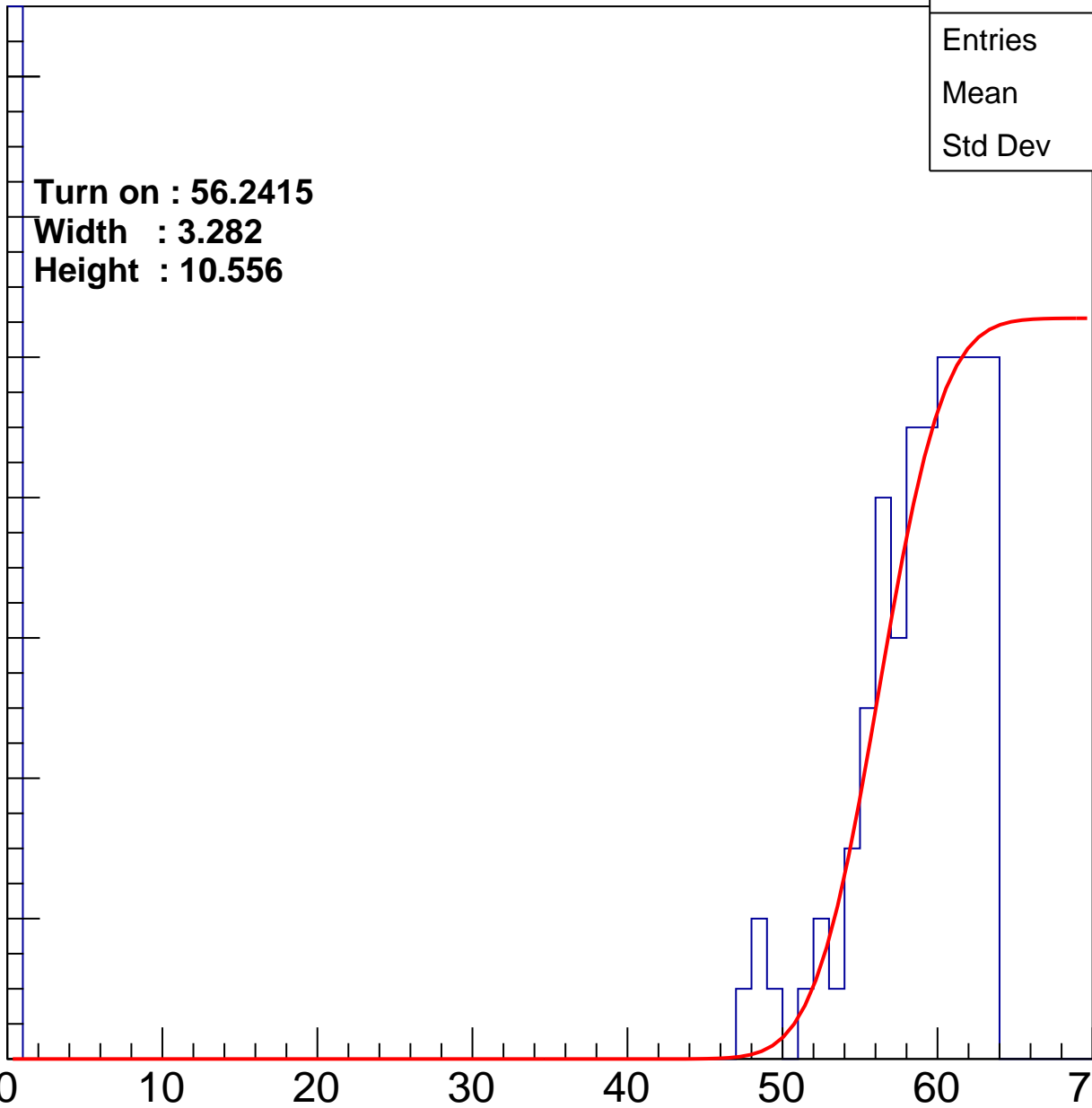
Entry

14
12
10
8
6
4
2
0

Turn on : 56.2415
Width : 3.282
Height : 10.556

Entries	155
Mean	33.16
Std Dev	29.07

ampl



B1L104S, U24-ch59

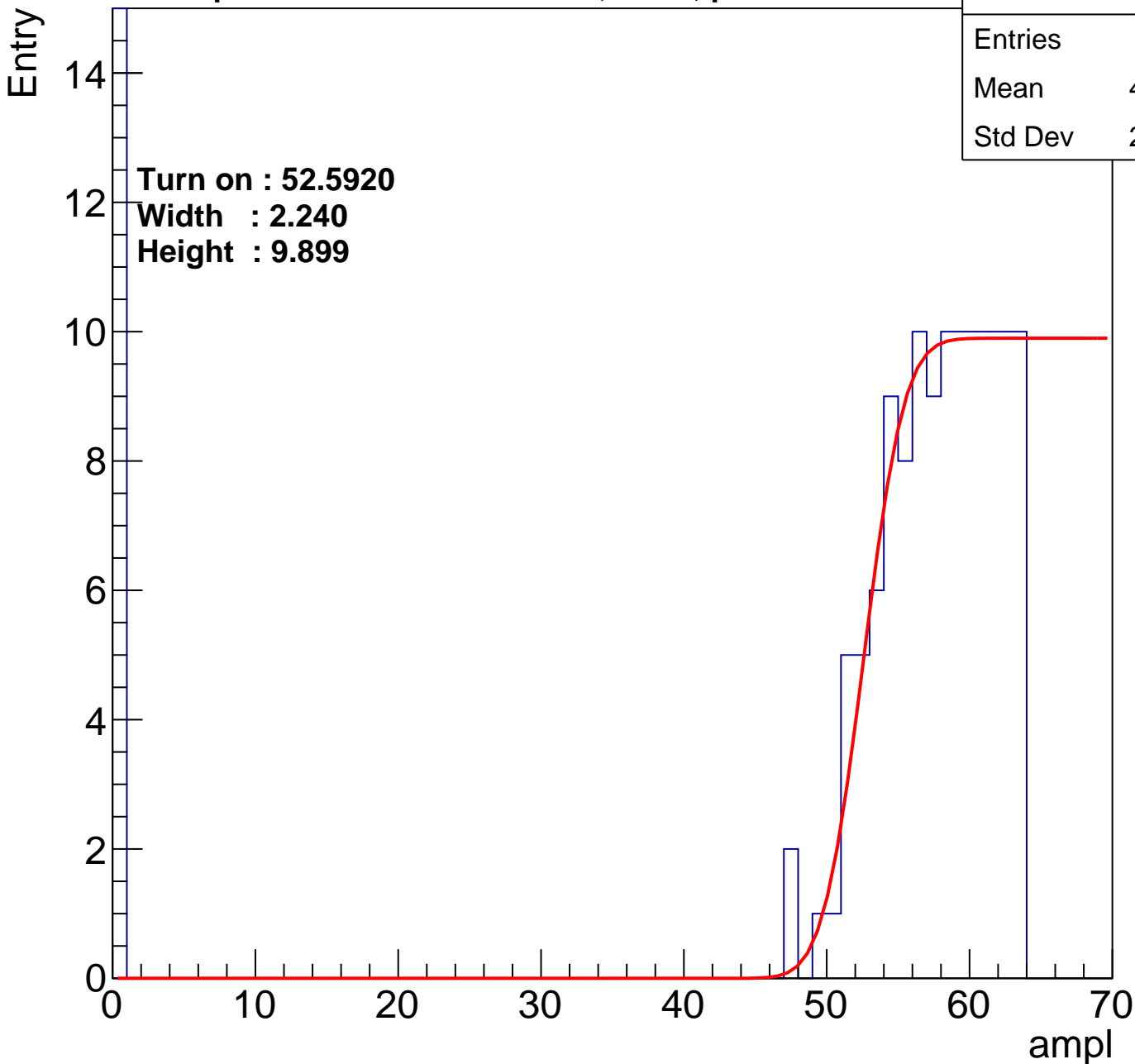
calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	40.09
Std Dev	26.52

Turn on : 52.5920

Width : 2.240

Height : 9.899



B1L104S, U24-ch60

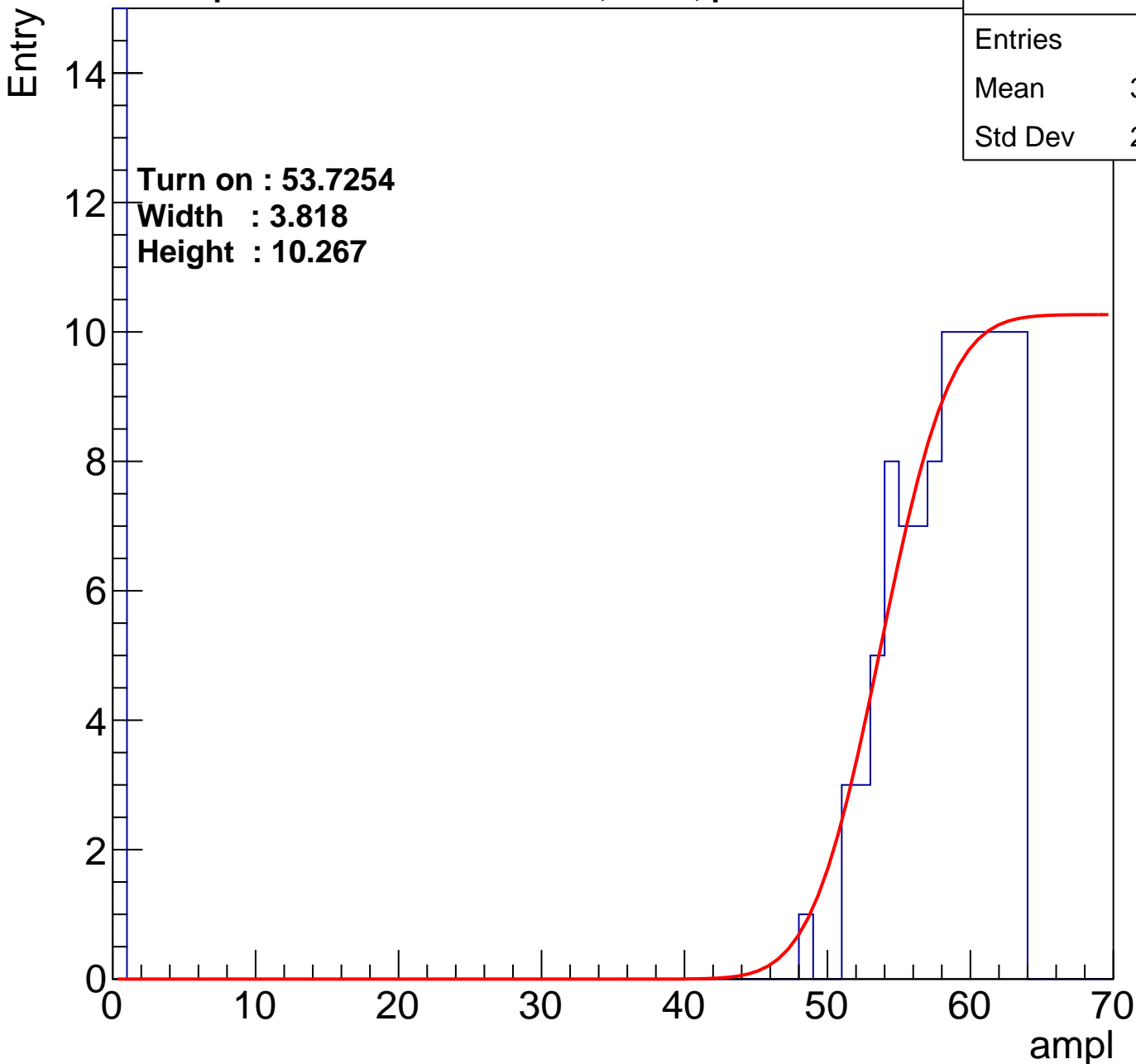
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.87
Std Dev	28.87

Turn on : 53.7254

Width : 3.818

Height : 10.267



B1L104S, U24-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	38.31
Std Dev	27.17

Turn on : 52.7663

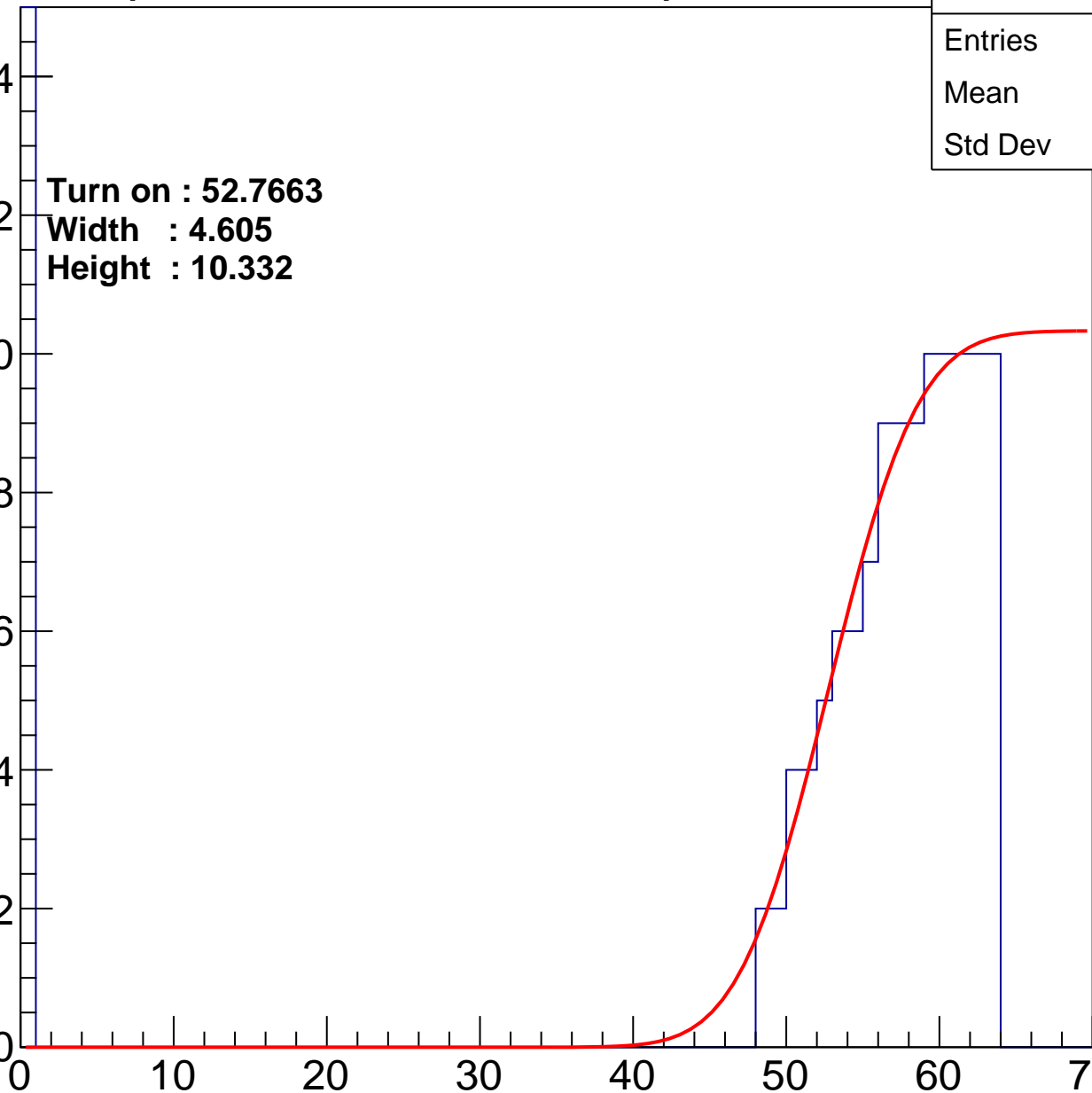
Width : 4.605

Height : 10.332

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch62

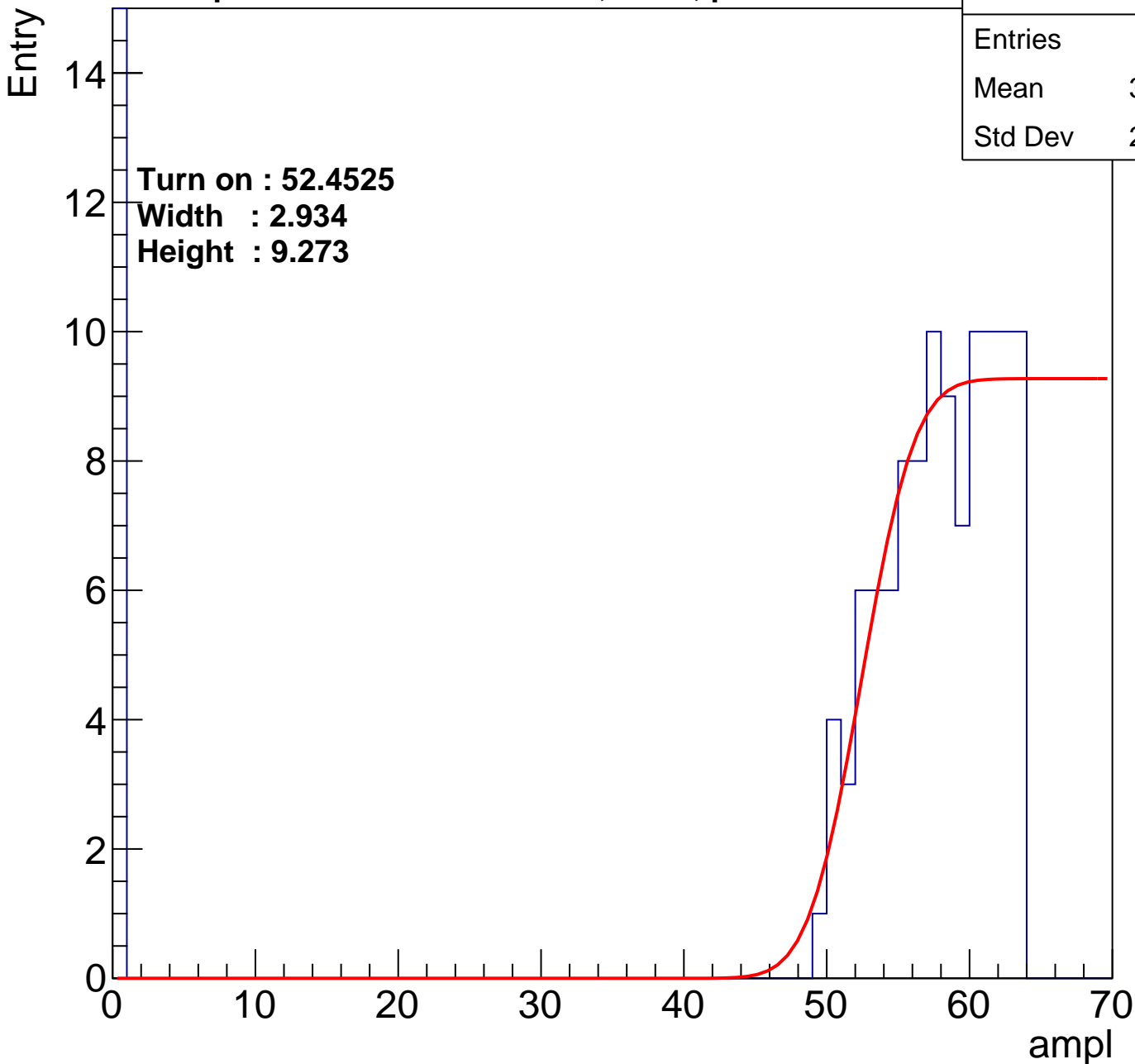
calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	32.68
Std Dev	28.62

Turn on : 52.4525

Width : 2.934

Height : 9.273



B1L104S, U24-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	149
Mean	36.05
Std Dev	28.5

Turn on : 54.8840

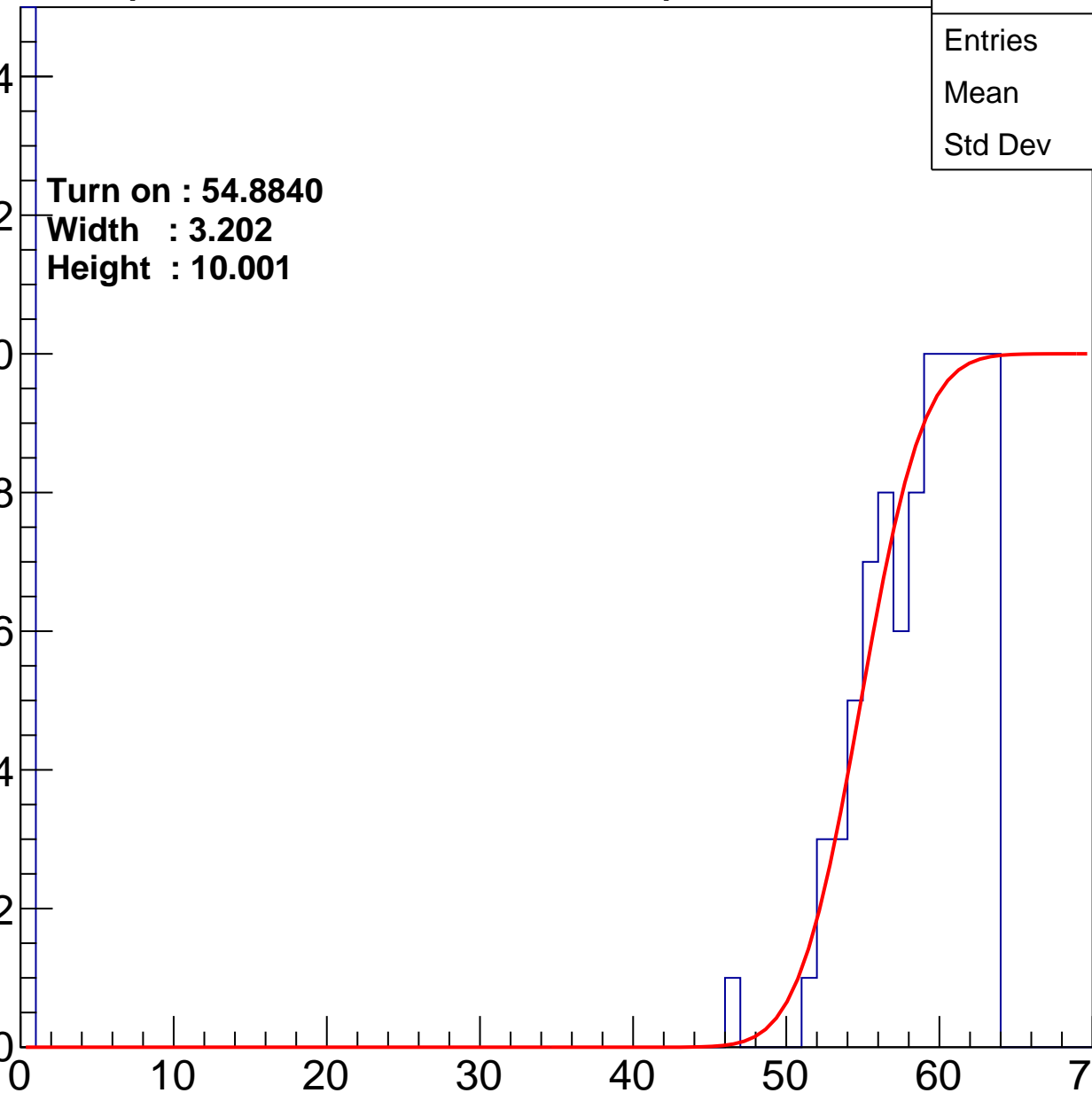
Width : 3.202

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	34.34
Std Dev	28.33

Turn on : 53.0185

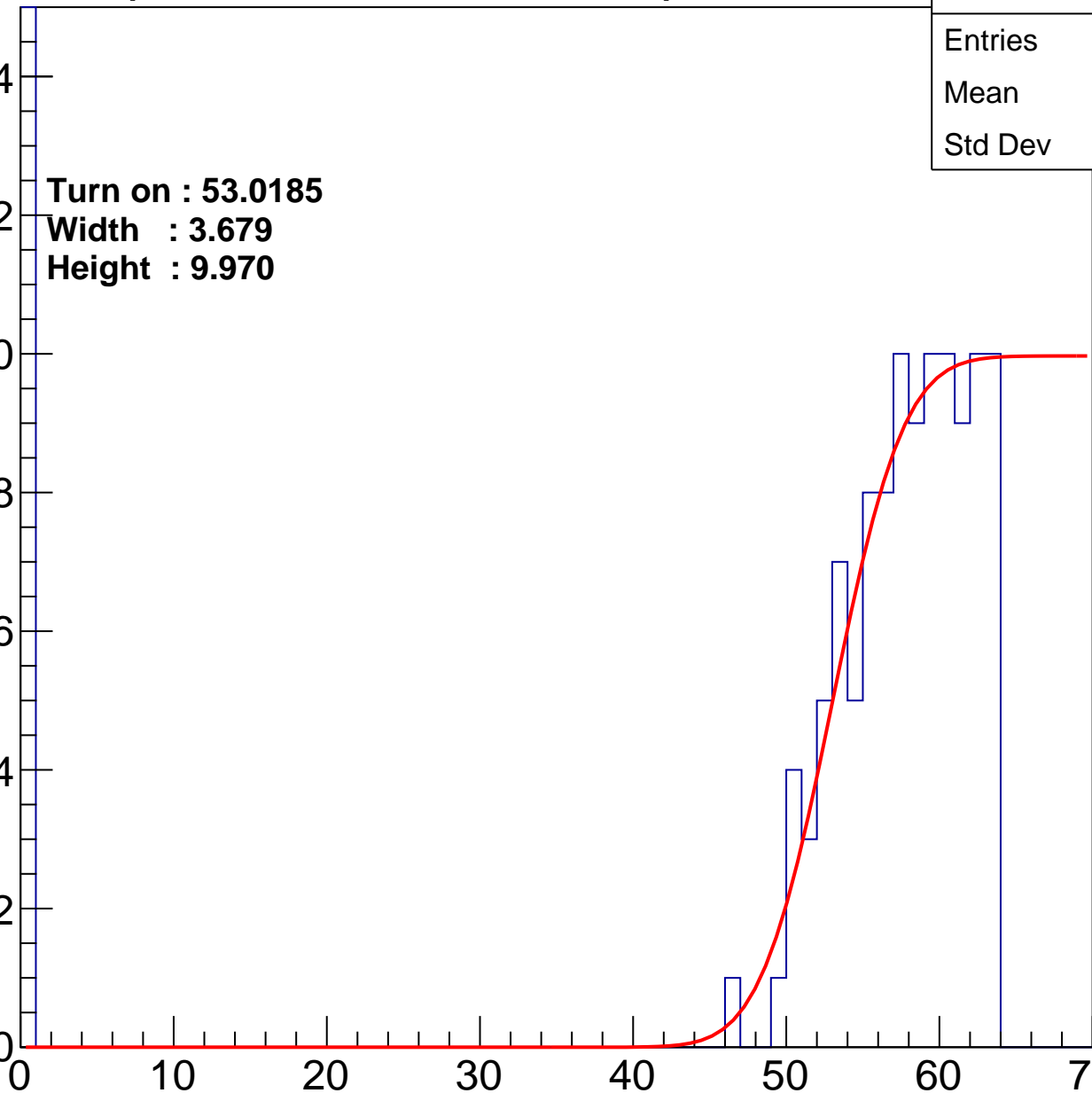
Width : 3.679

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	34.91
Std Dev	28.68

Turn on : 54.1646

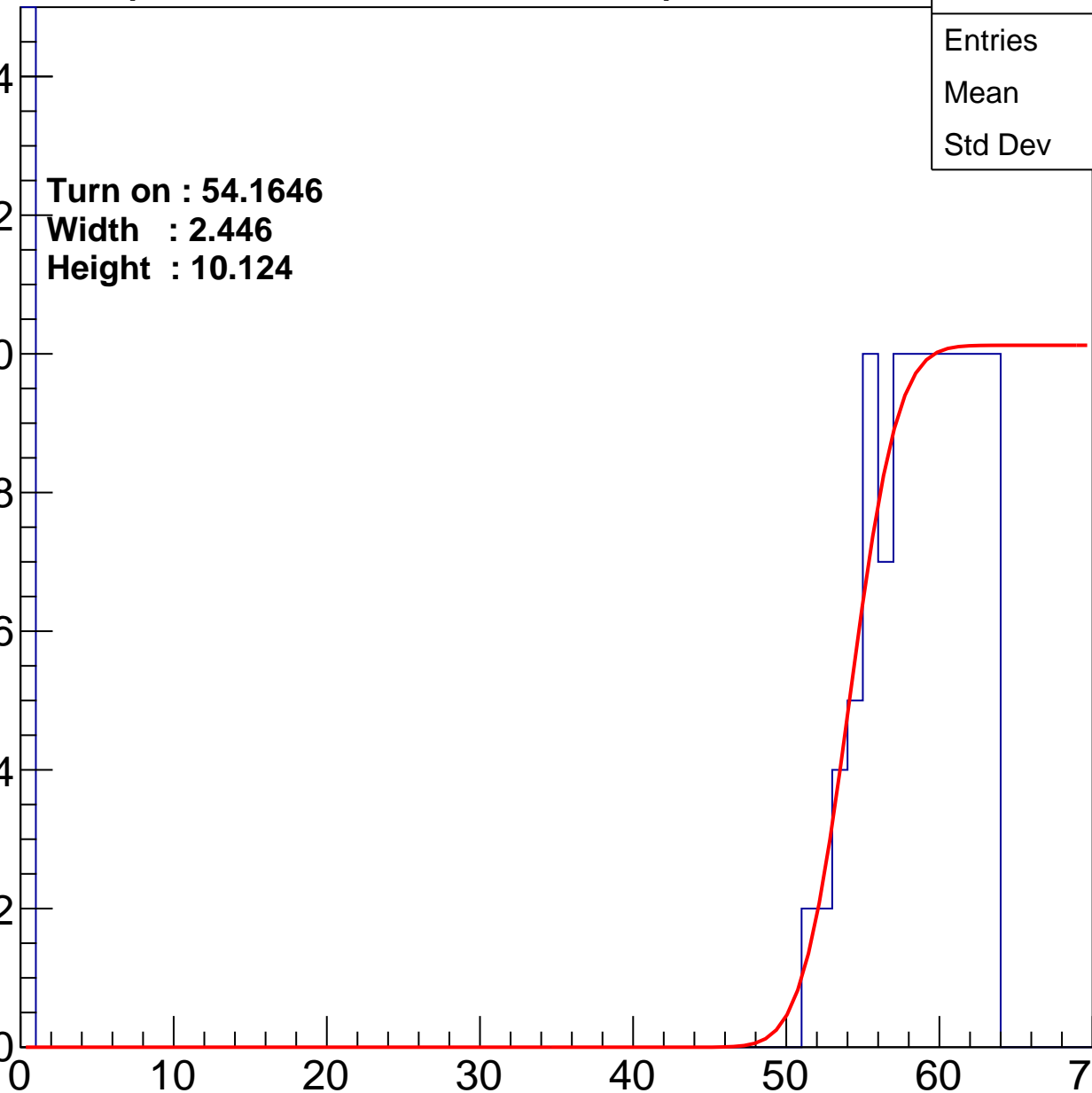
Width : 2.446

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	33.32
Std Dev	28.43

Turn on : 53.3913

Width : 4.227

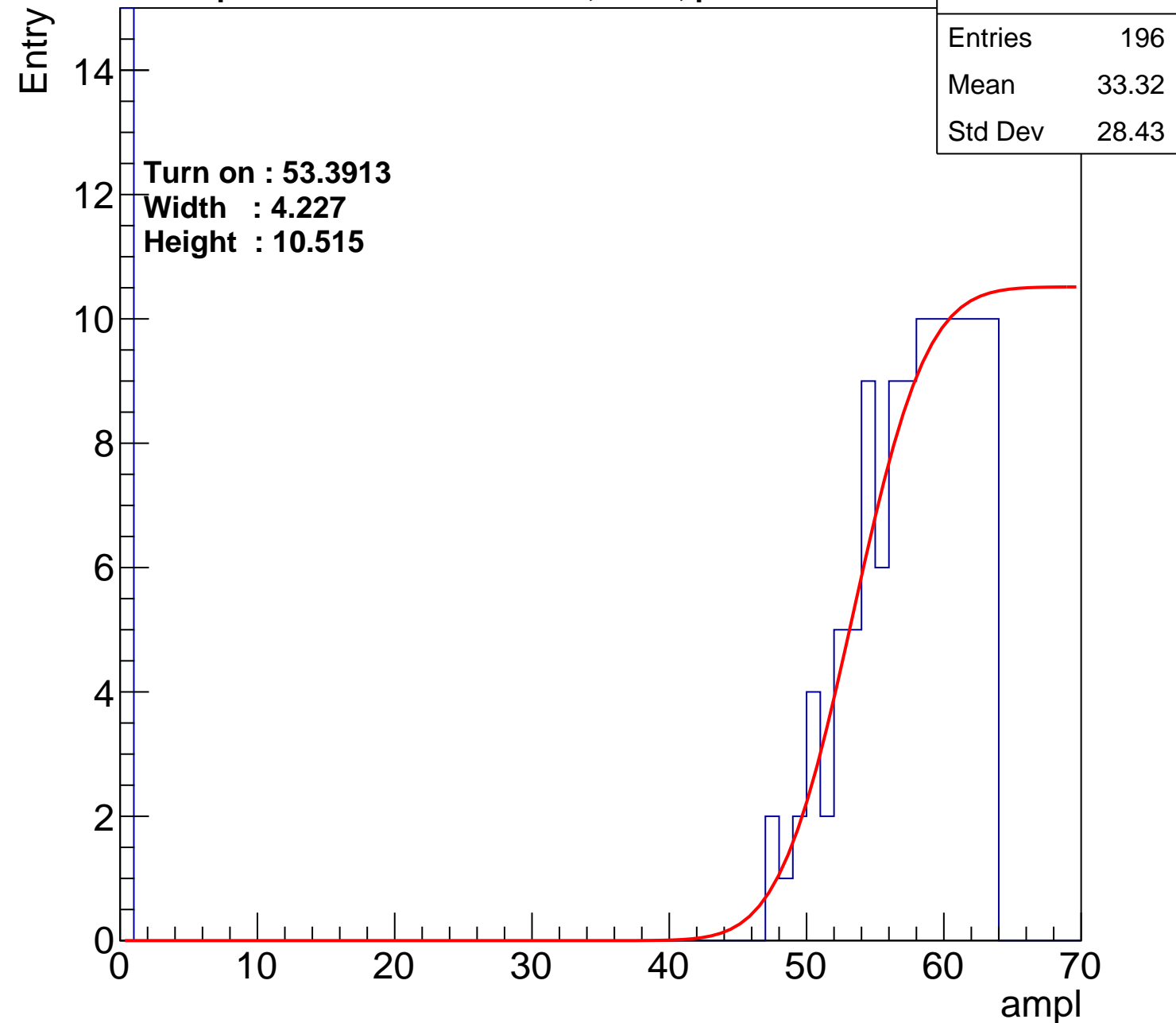
Height : 10.515

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U24-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	32.04
Std Dev	28.82

Turn on : 53.6872

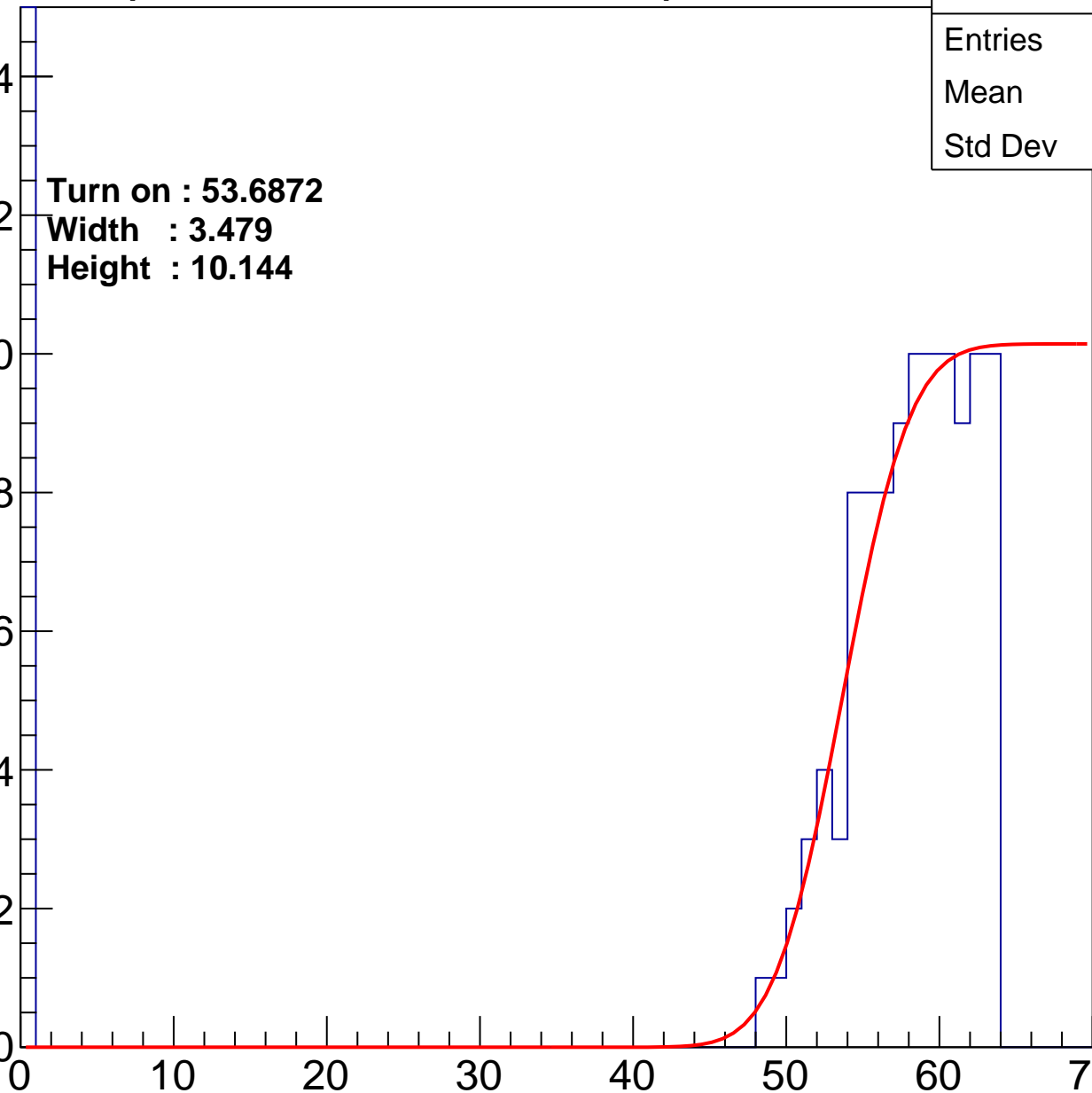
Width : 3.479

Height : 10.144

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	34.22
Std Dev	27.93

Turn on : 51.8294

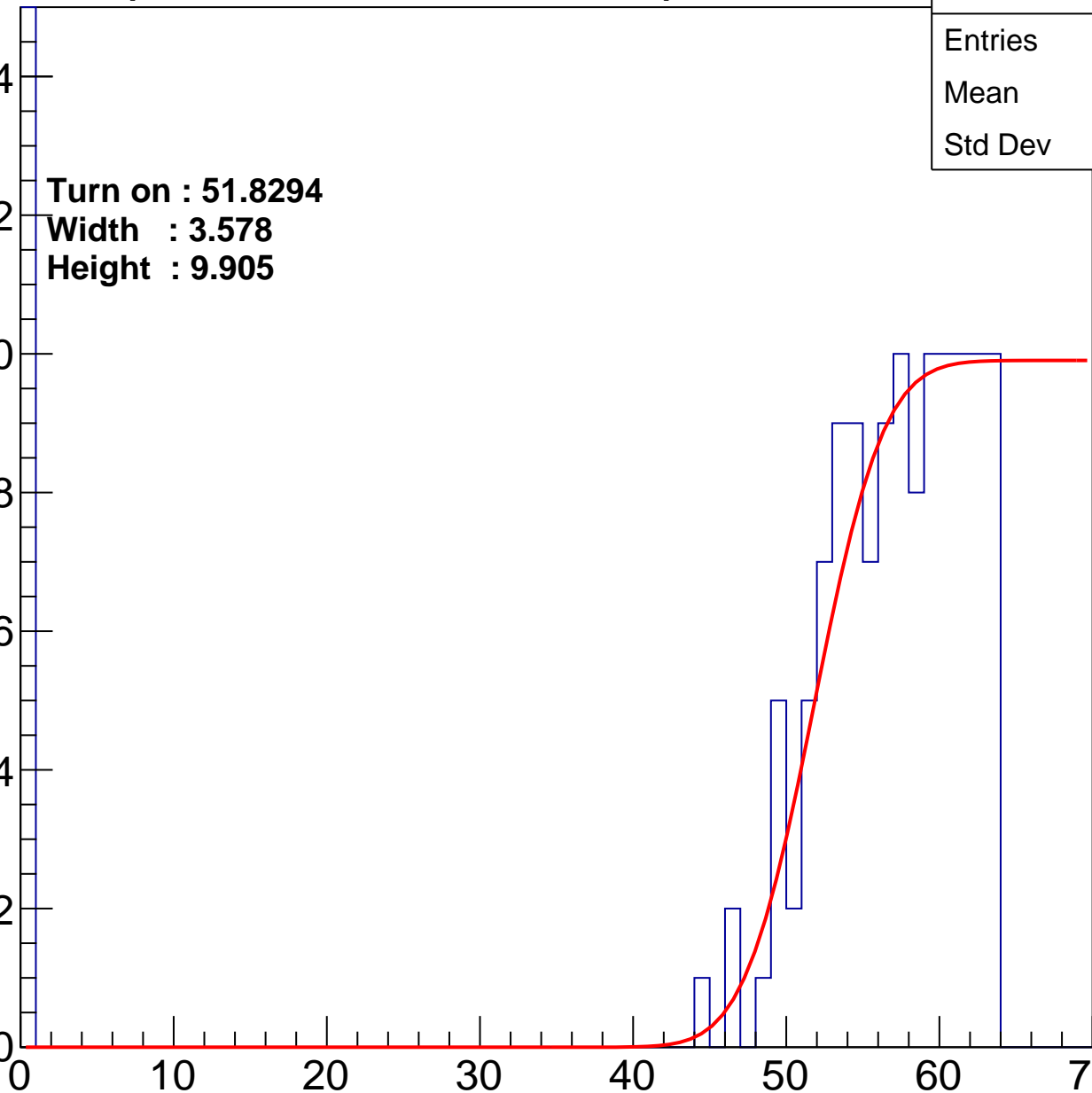
Width : 3.578

Height : 9.905

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch69

calib_packv5_033123_0516.root, FC#4, port A1

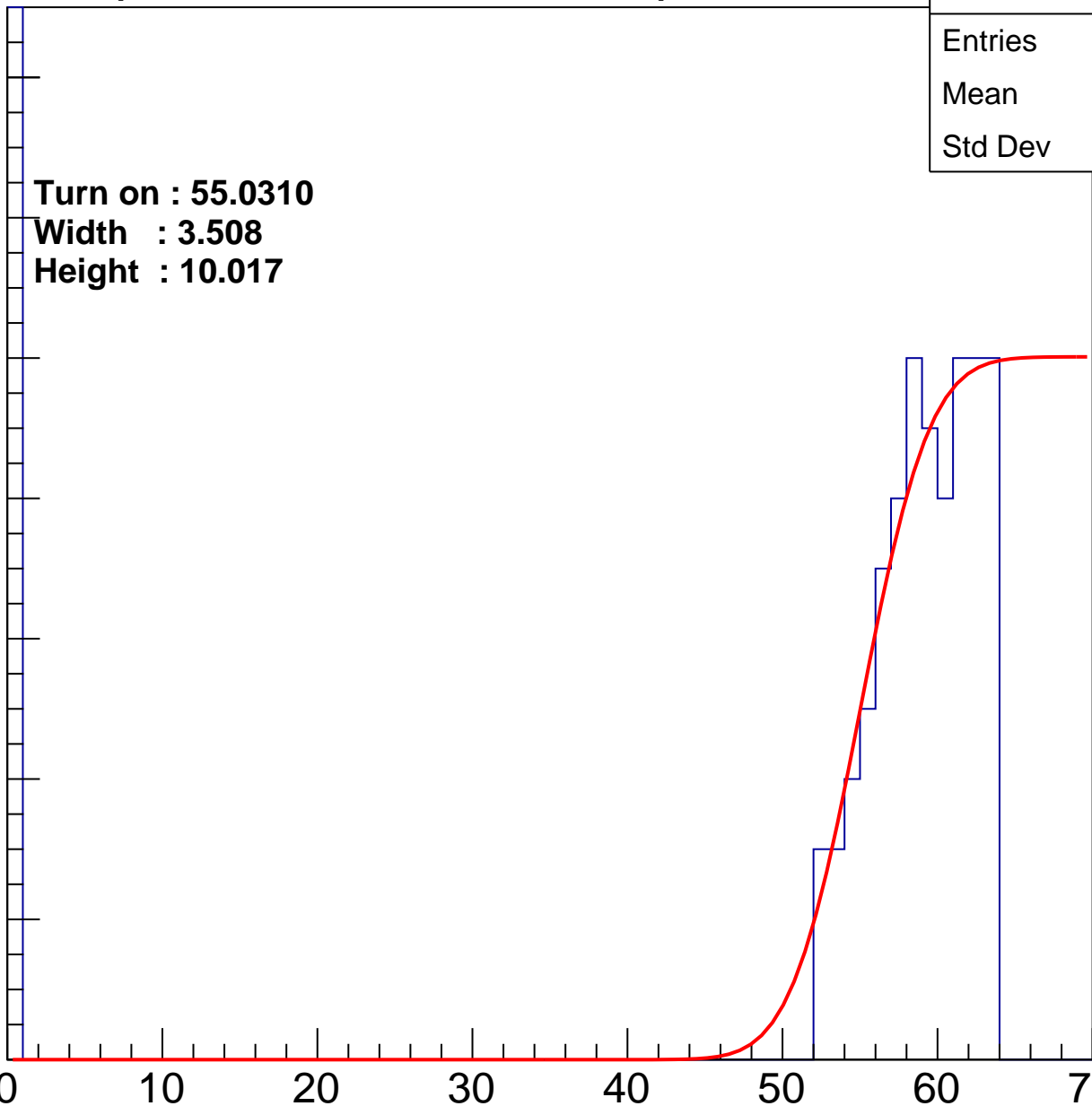
Entry

14
12
10
8
6
4
2
0

Turn on : 55.0310
Width : 3.508
Height : 10.017

Entries	166
Mean	30.75
Std Dev	29.39

ampl



B1L104S, U24-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	36.46
Std Dev	27.51

Turn on : 52.1025

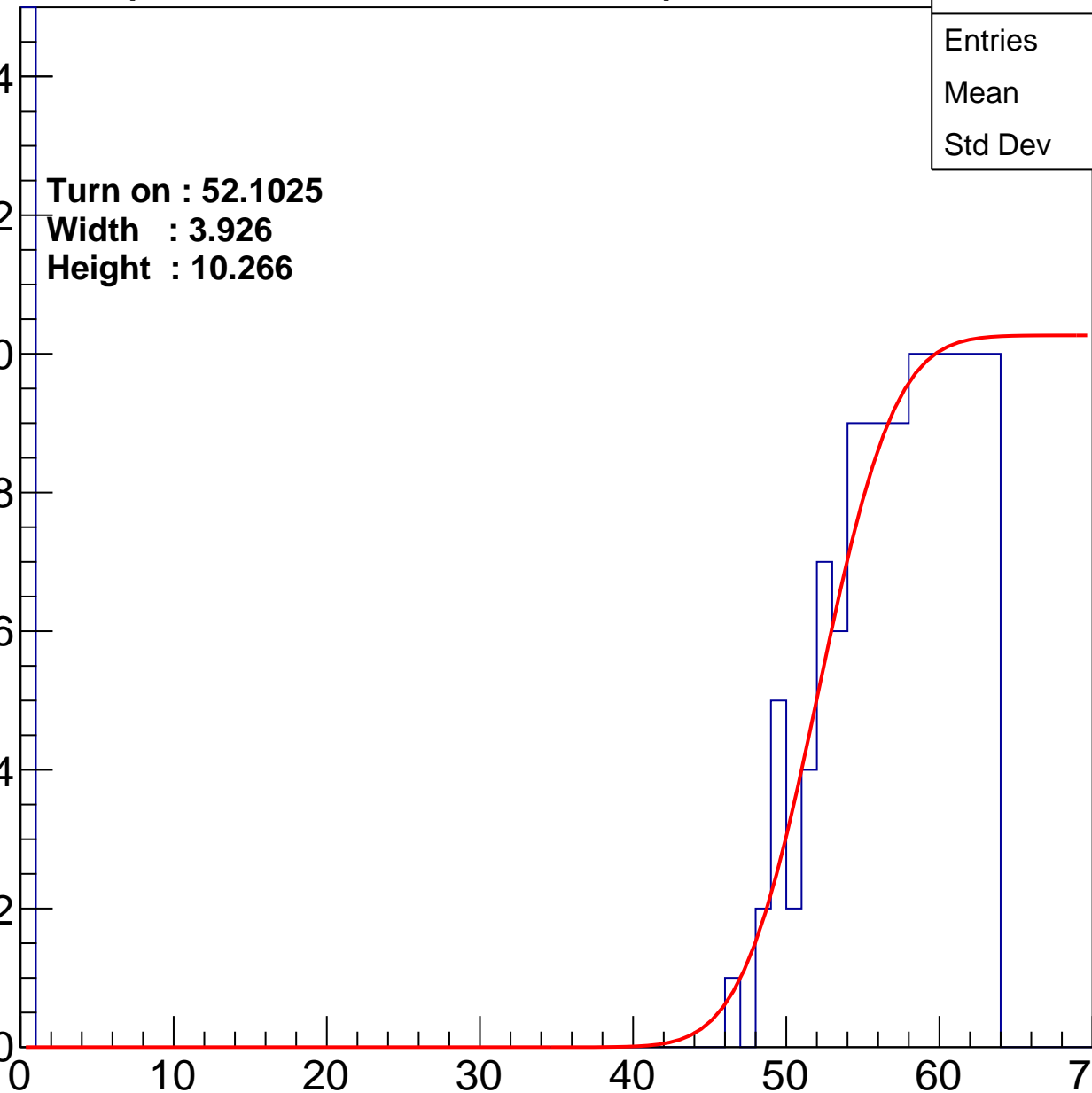
Width : 3.926

Height : 10.266

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	38.66
Std Dev	27.48

Turn on : 53.7127

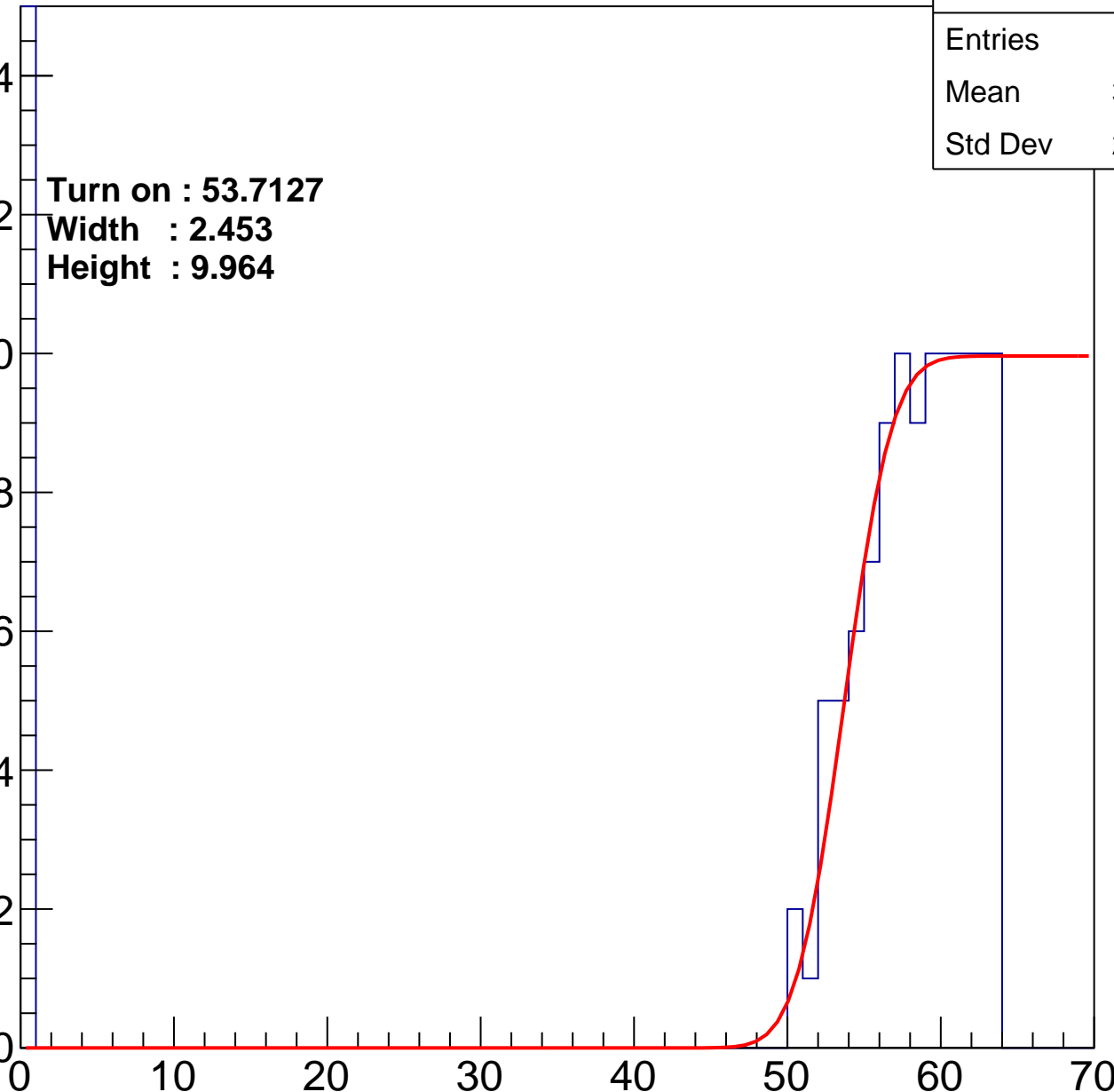
Width : 2.453

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	37.22
Std Dev	28

Turn on : 54.5652

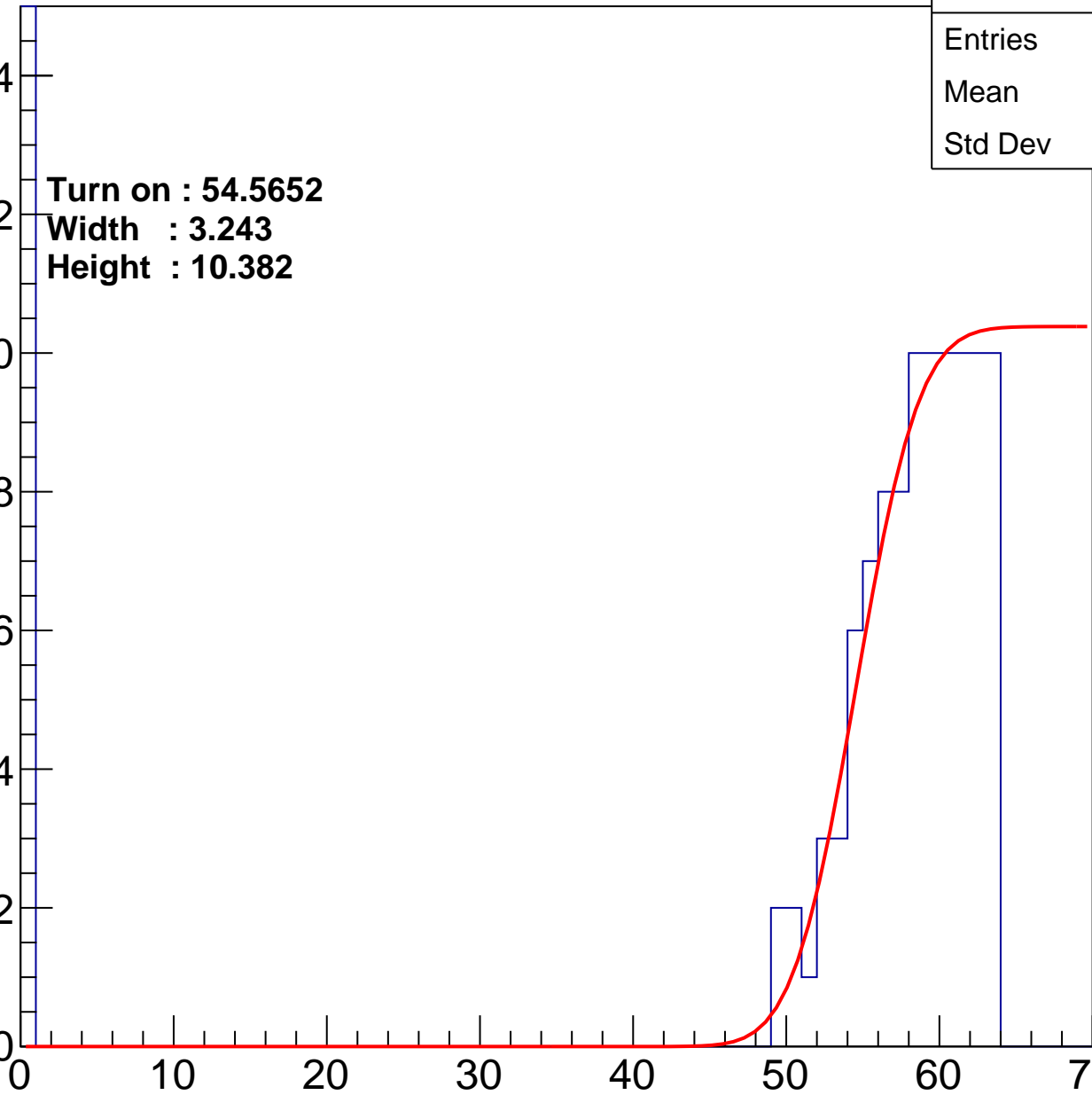
Width : 3.243

Height : 10.382

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	33.1
Std Dev	29.01

Turn on : 55.5256

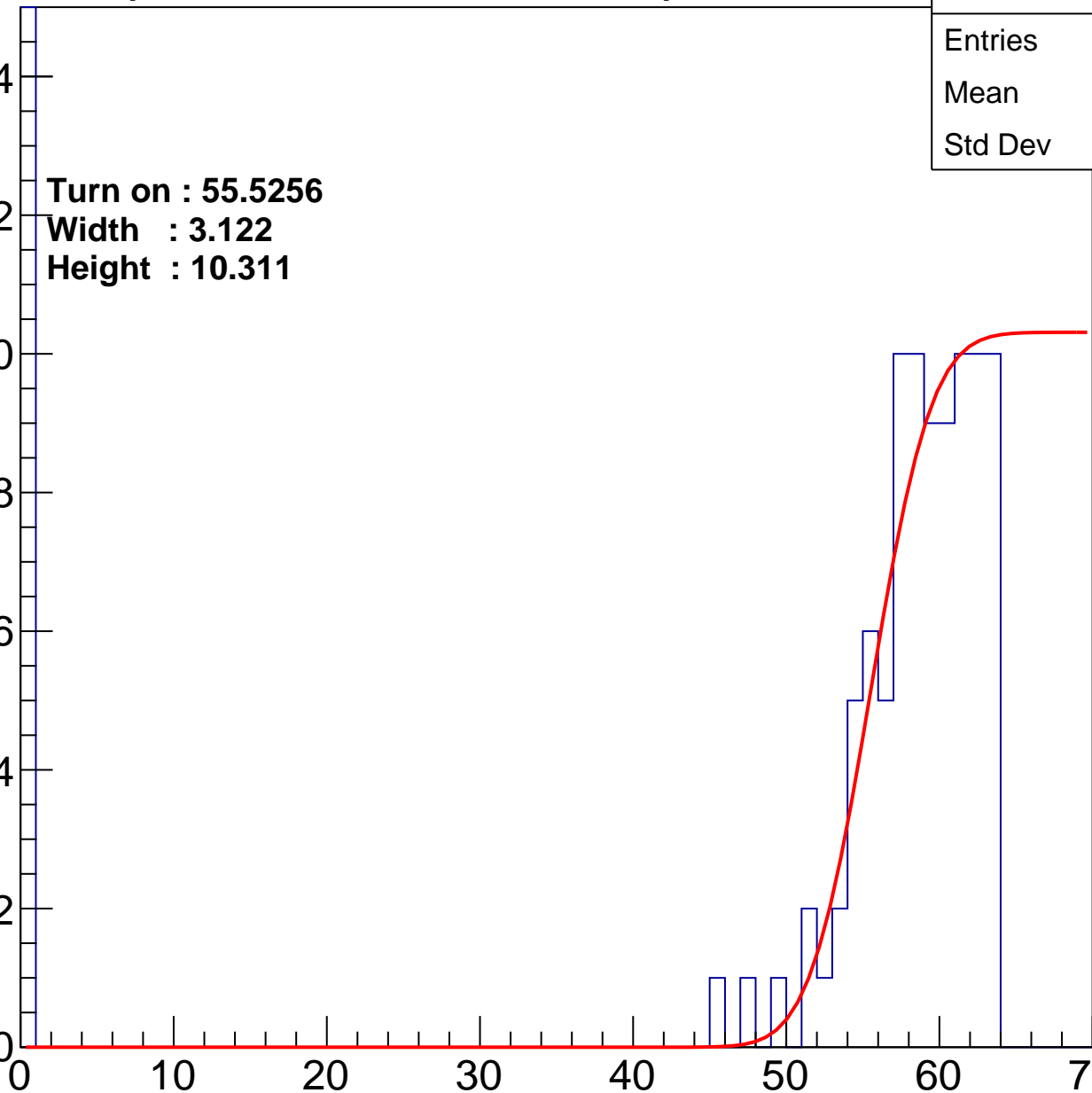
Width : 3.122

Height : 10.311

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	36.84
Std Dev	28.17

Turn on : 53.6861

Width : 1.958

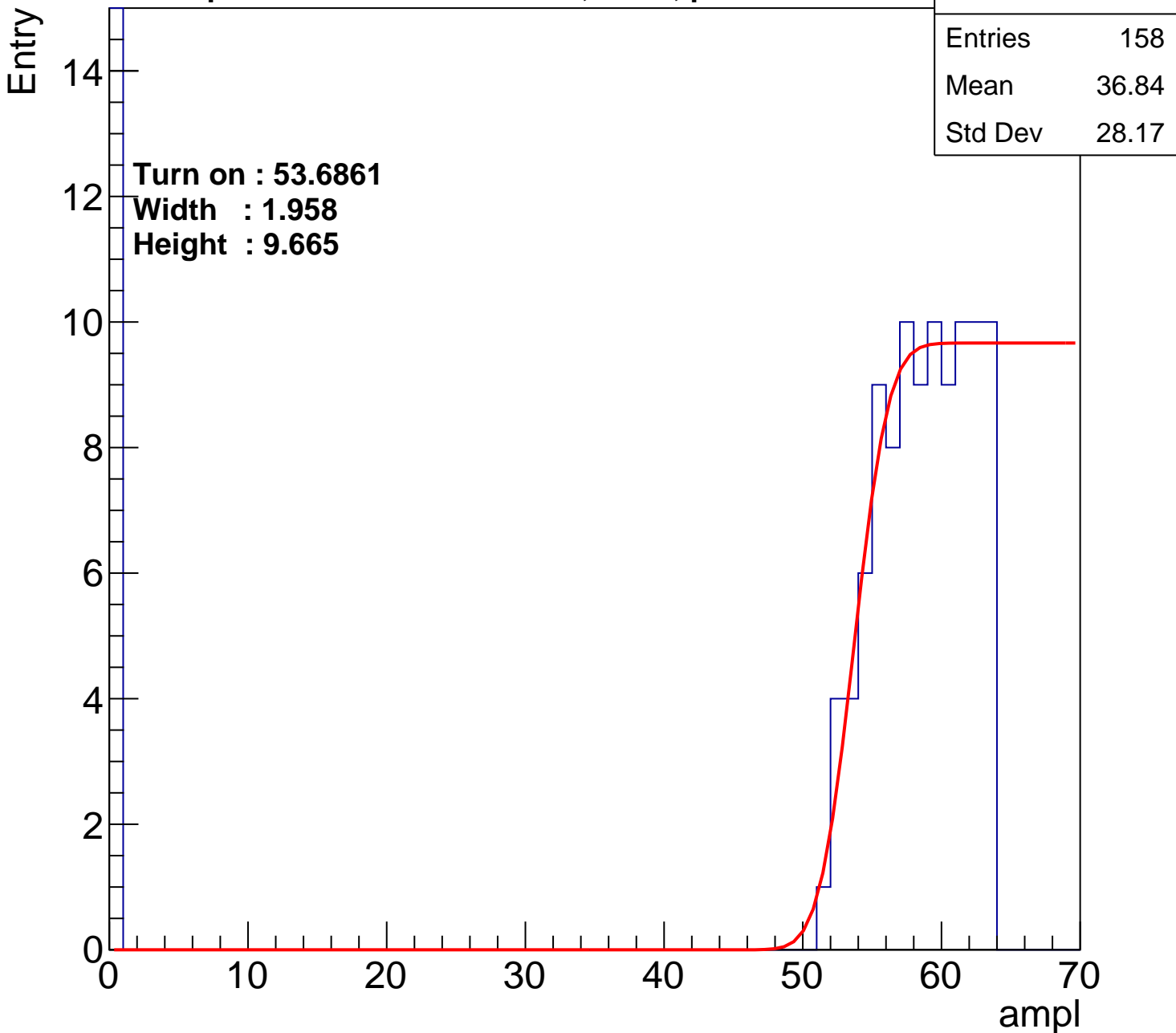
Height : 9.665

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U24-ch75

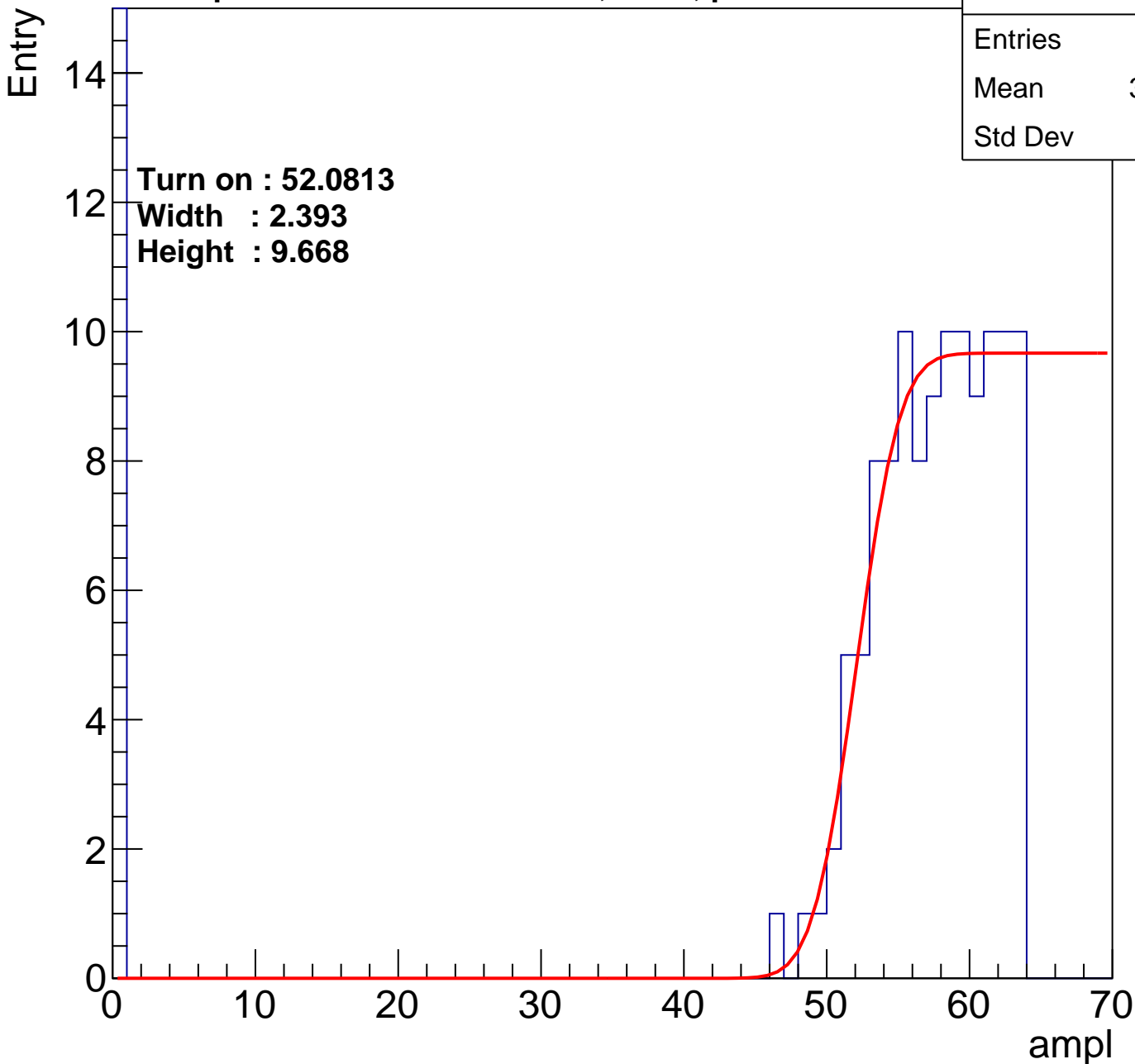
calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	34.33
Std Dev	28.2

Turn on : 52.0813

Width : 2.393

Height : 9.668



B1L104S, U24-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	34.56
Std Dev	28.32

Turn on : 53.4120

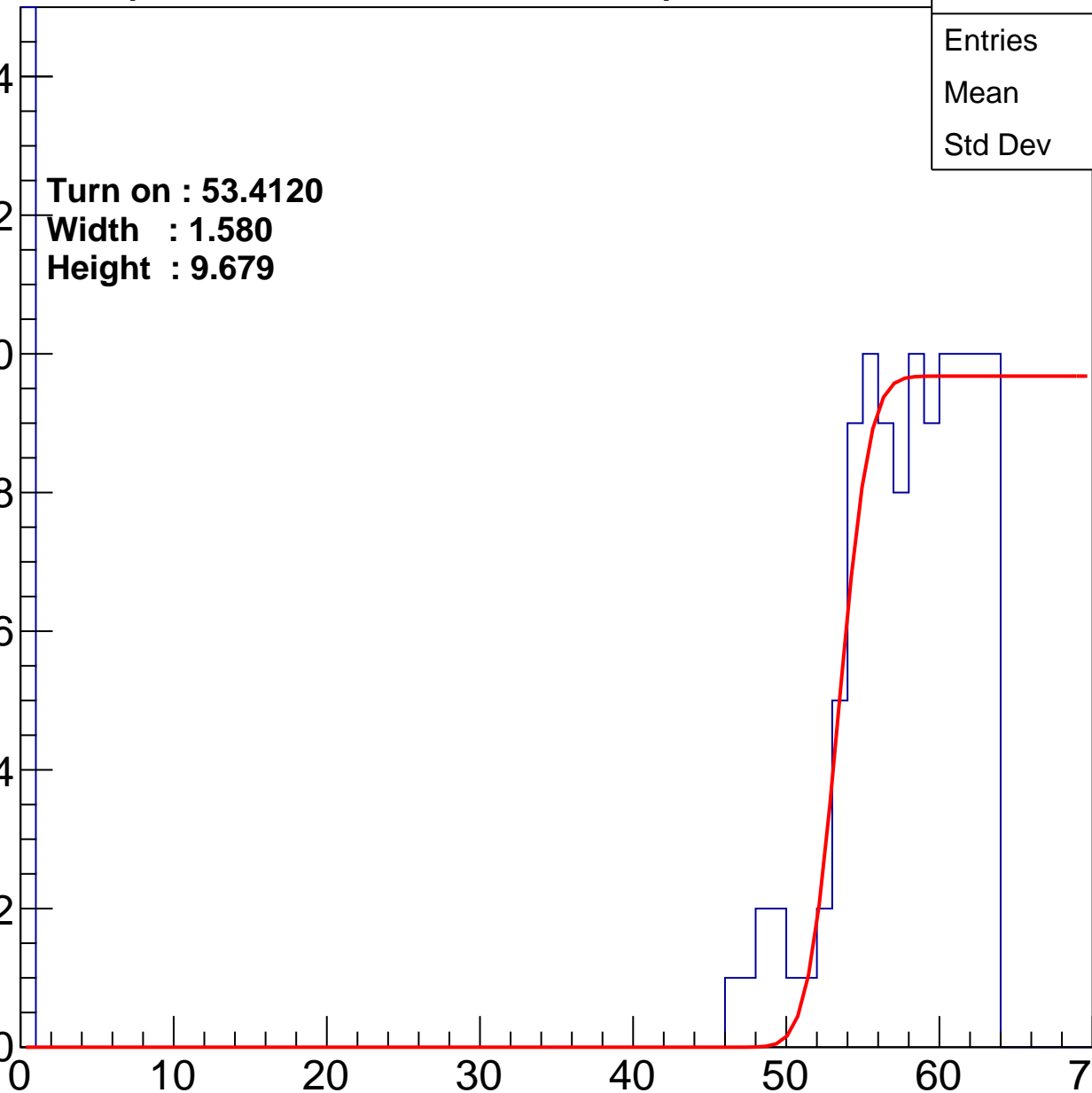
Width : 1.580

Height : 9.679

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	30.26
Std Dev	29.21

Turn on : 54.9876

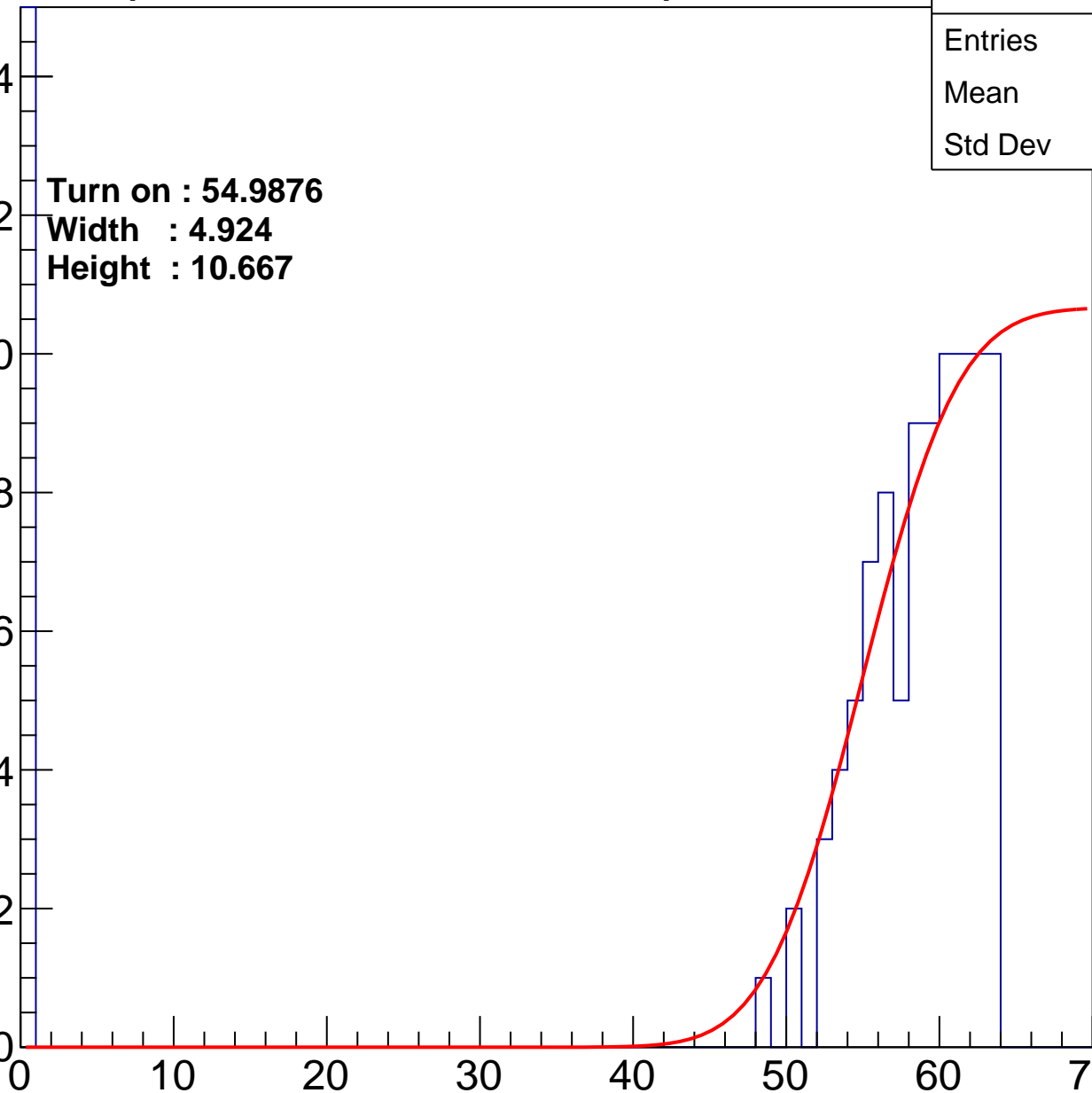
Width : 4.924

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	38.6
Std Dev	27.11

Turn on : 52.6675

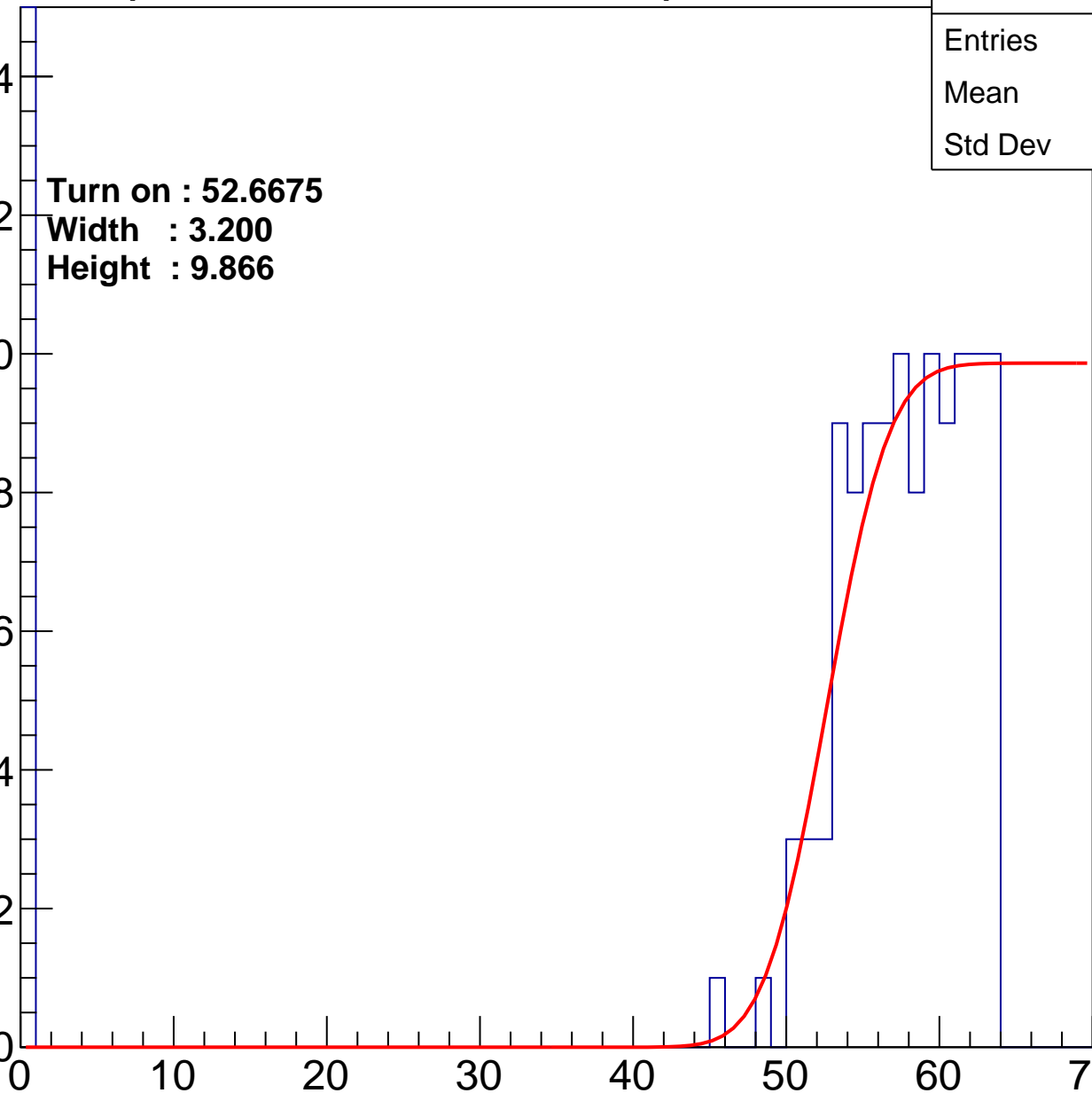
Width : 3.200

Height : 9.866

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch79

calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	36.14
Std Dev	28.38

Turn on : 54.5746

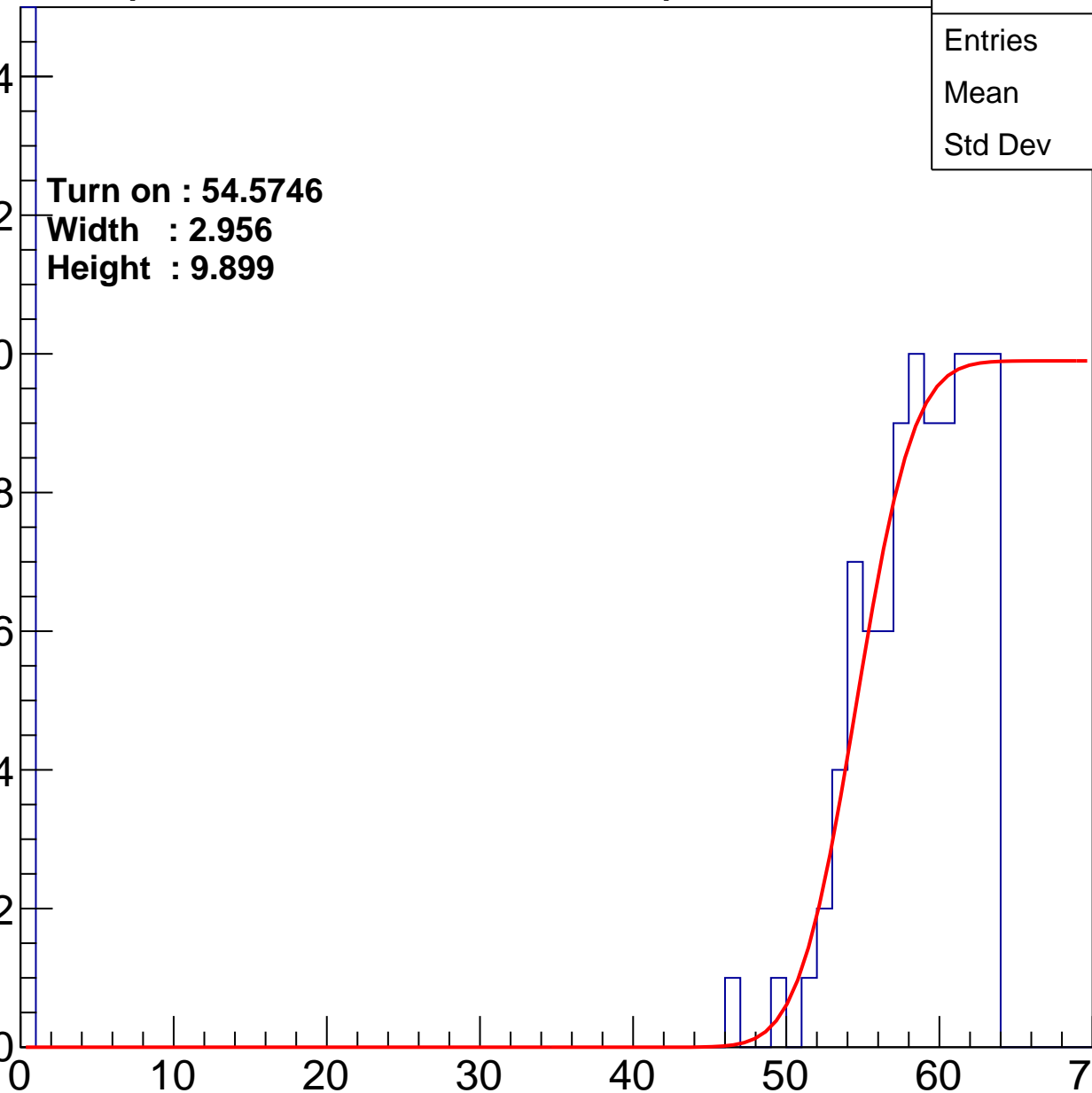
Width : 2.956

Height : 9.899

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	34.91
Std Dev	27.97

Turn on : 53.7065

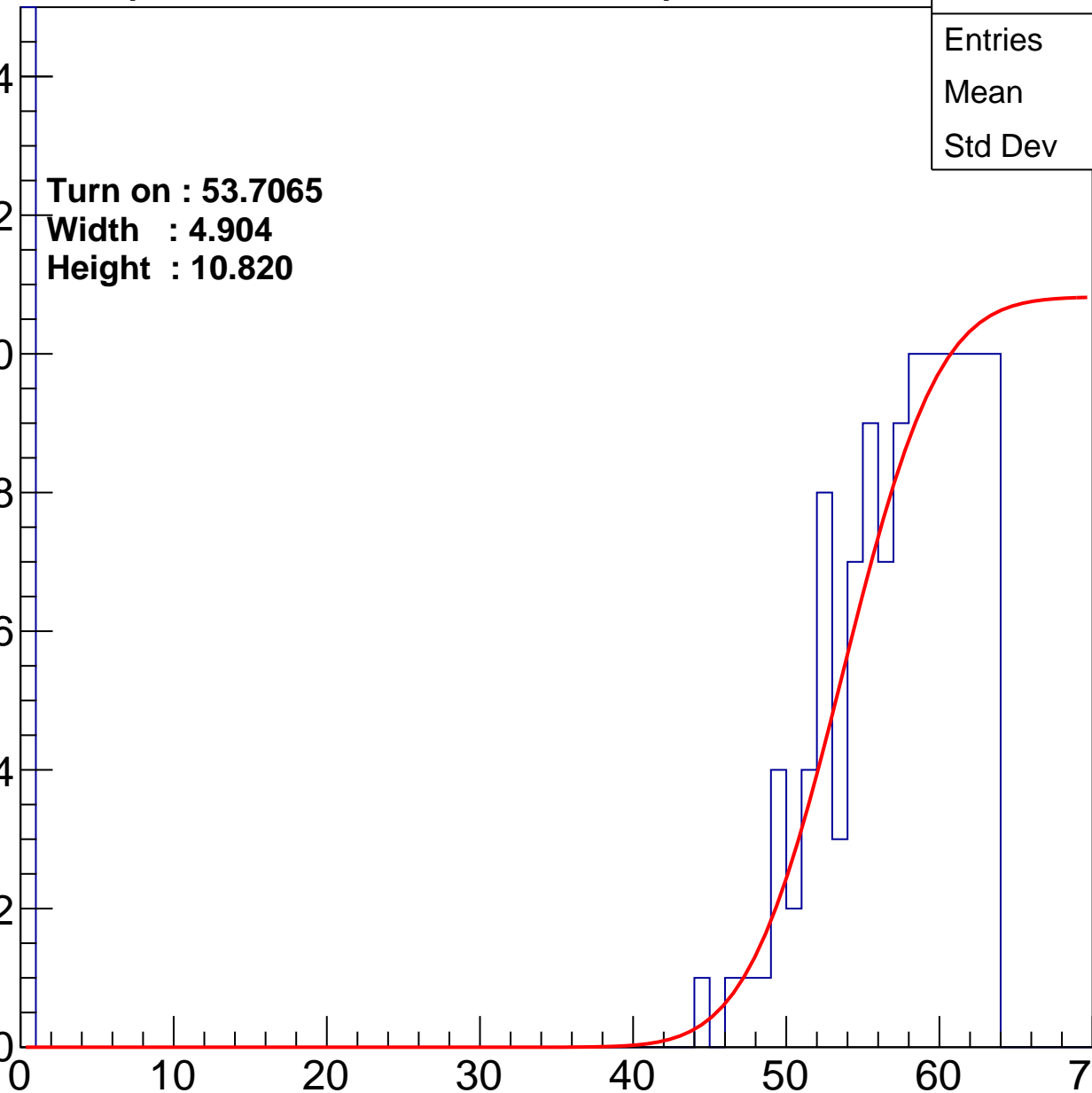
Width : 4.904

Height : 10.820

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch81

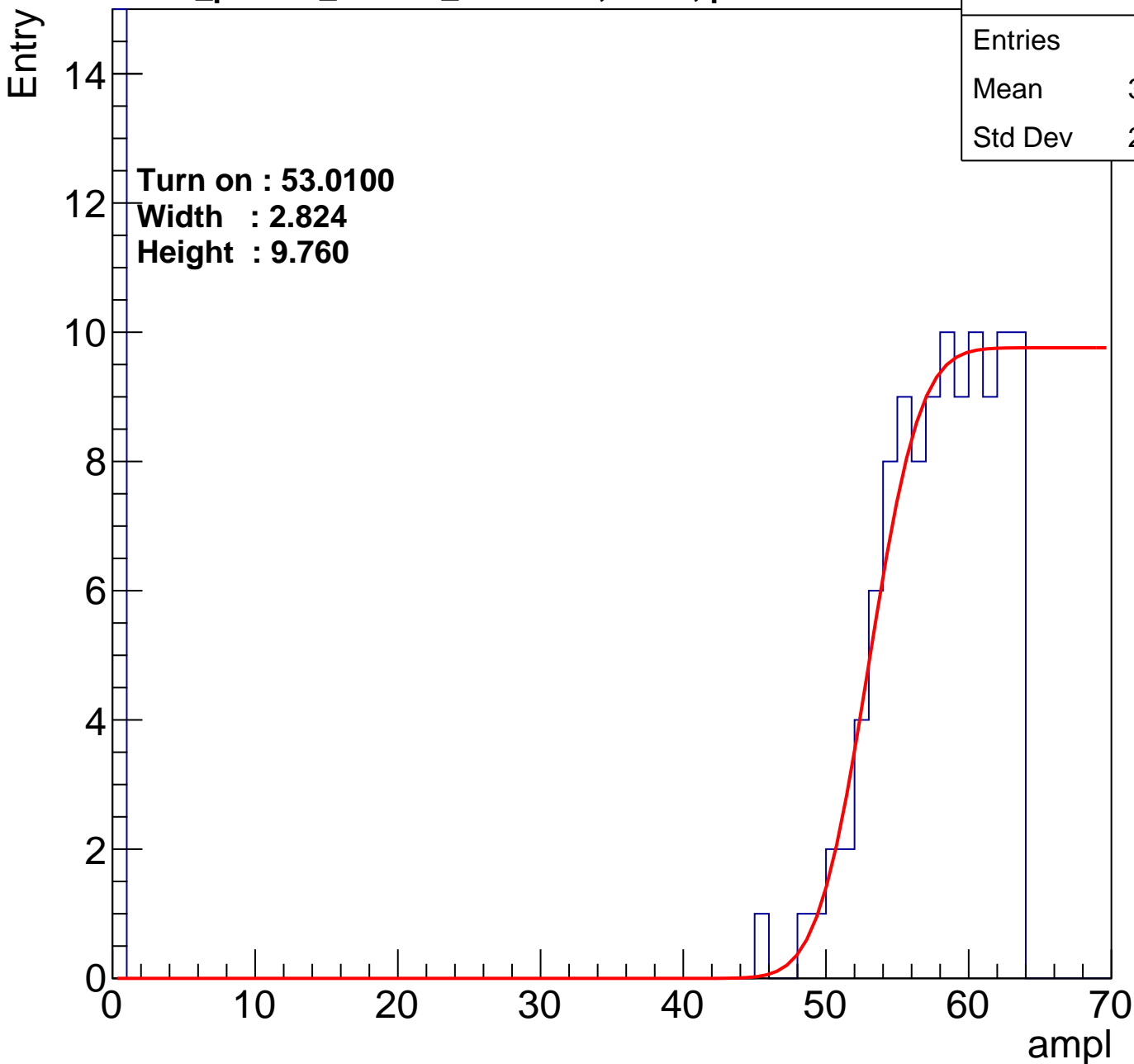
calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	37.76
Std Dev	27.48

Turn on : 53.0100

Width : 2.824

Height : 9.760



B1L104S, U24-ch82

calib_packv5_033123_0516.root, FC#4, port A1

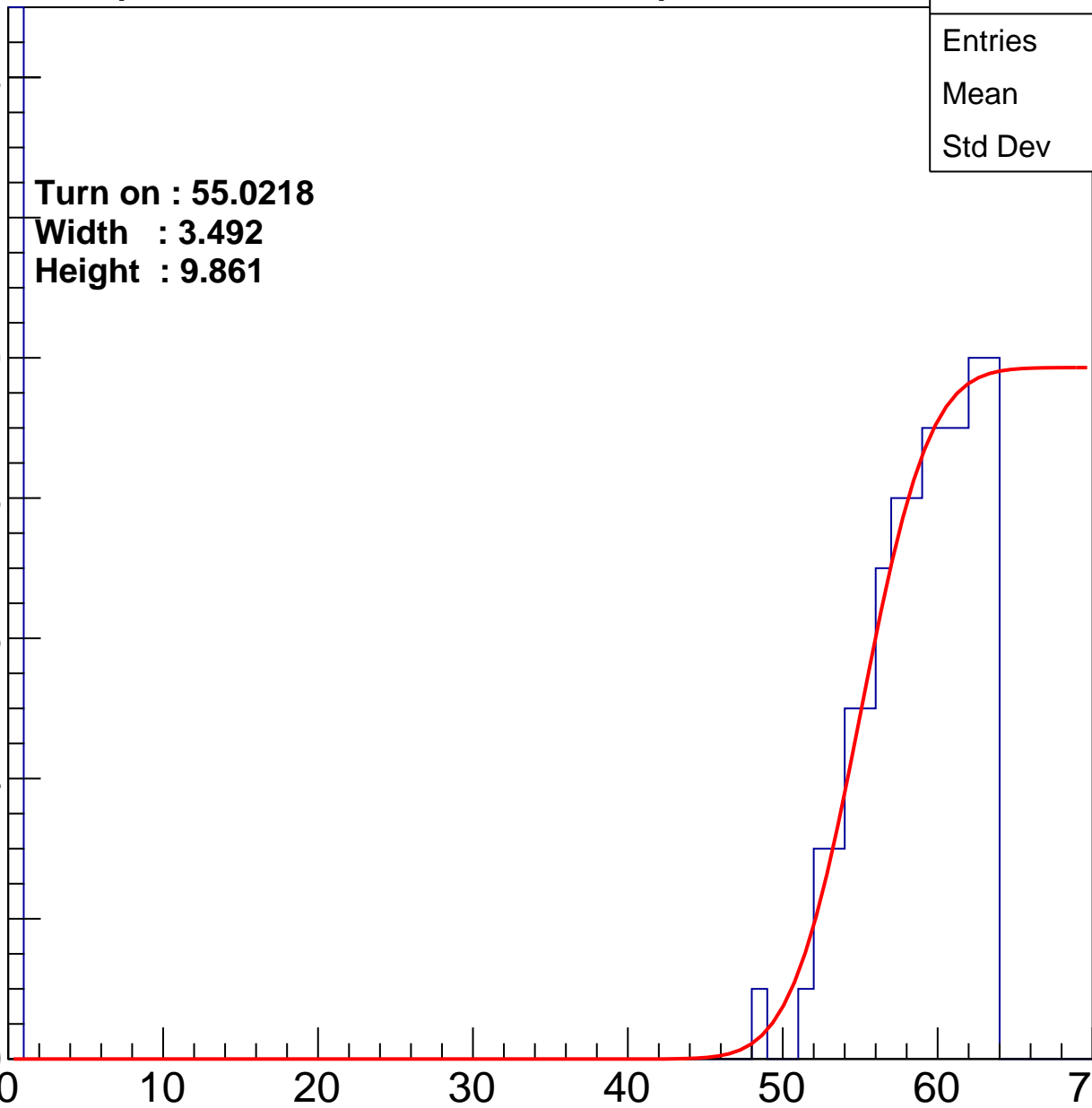
Entry

14
12
10
8
6
4
2
0

Turn on : 55.0218
Width : 3.492
Height : 9.861

Entries	160
Mean	32.13
Std Dev	29.17

ampl



B1L104S, U24-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	145
Mean	28.56
Std Dev	29.63

Turn on : 57.5548

Width : 3.599

Height : 9.111

Entry

70

60

50

40

30

20

10

0

0

10

20

30

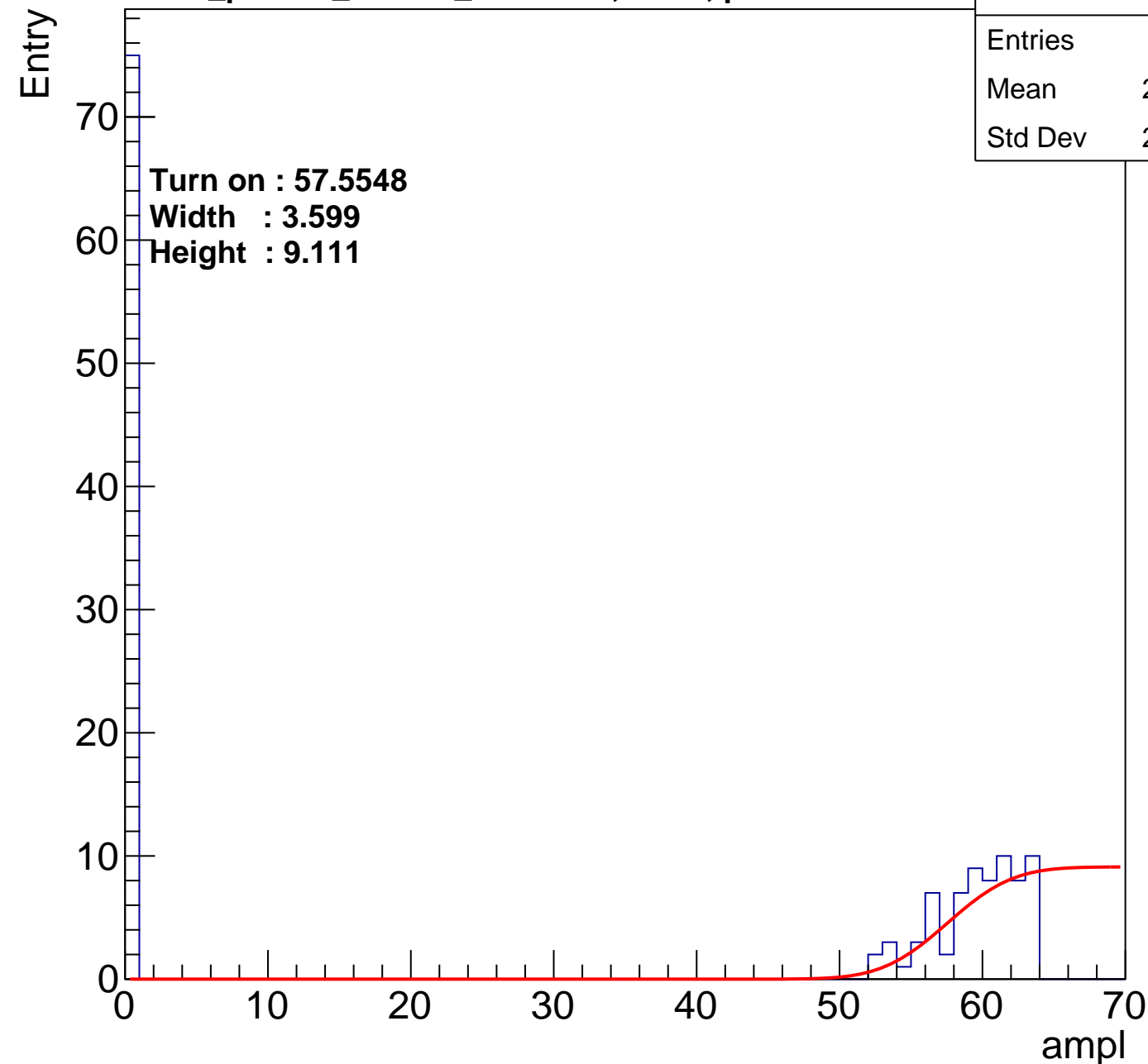
40

50

60

70

ampl



B1L104S, U24-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	27.73
Std Dev	29.01

Turn on : 53.6931

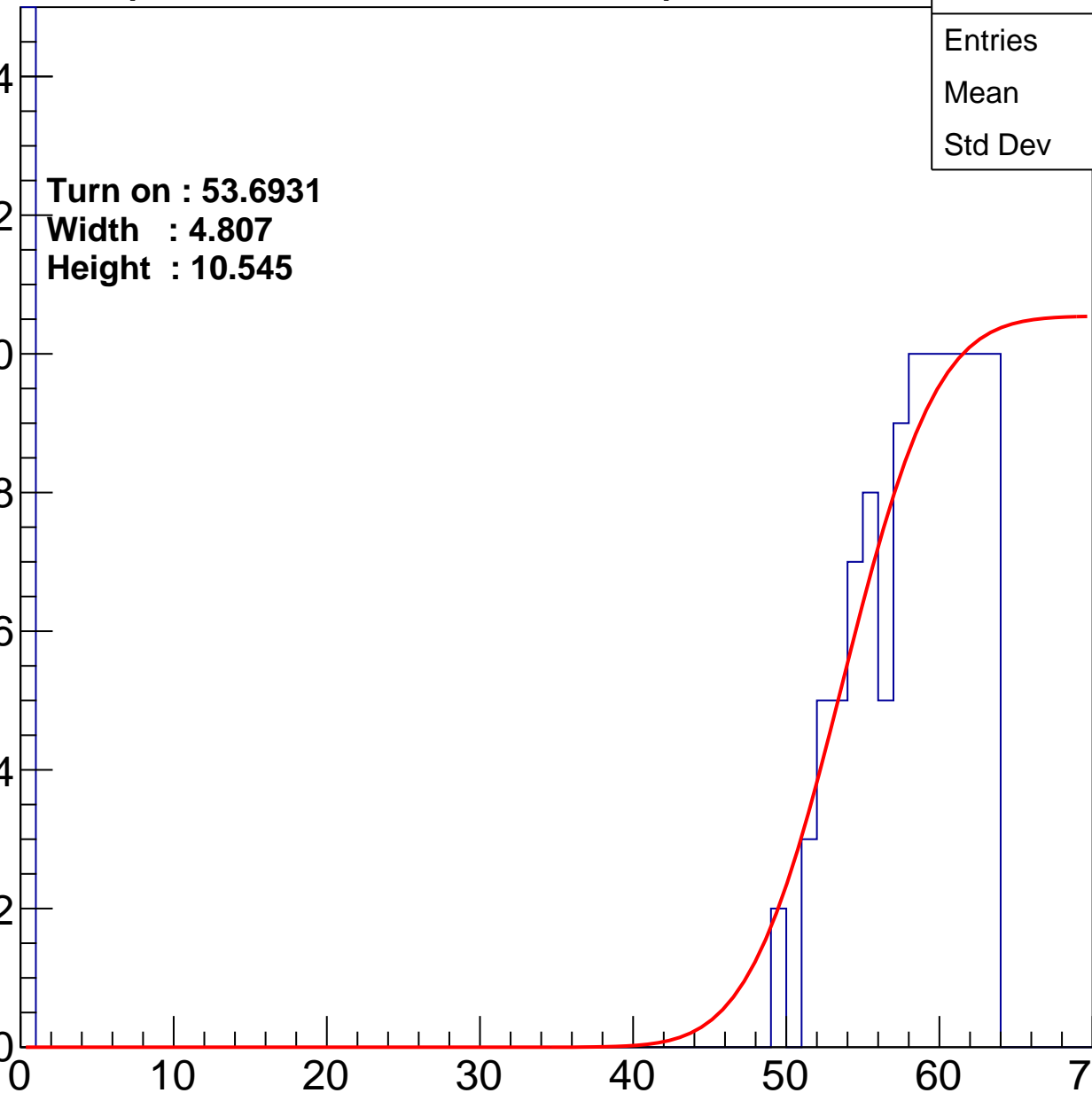
Width : 4.807

Height : 10.545

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch85

calib_packv5_033123_0516.root, FC#4, port A1

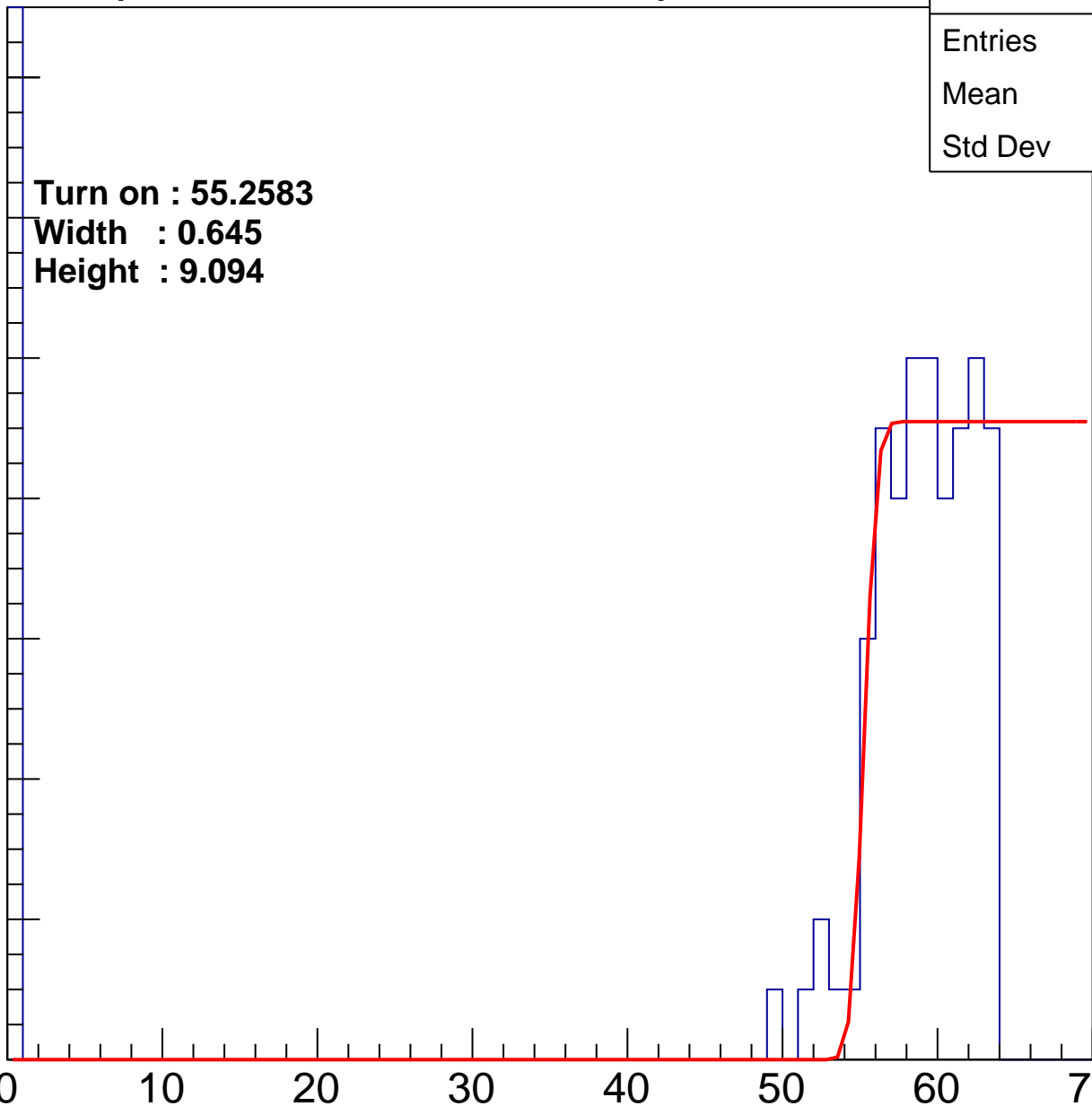
Entry

14
12
10
8
6
4
2
0

Turn on : 55.2583
Width : 0.645
Height : 9.094

Entries	157
Mean	31.76
Std Dev	29.32

ampl



B1L104S, U24-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.71
Std Dev	28.74

Turn on : 54.5310

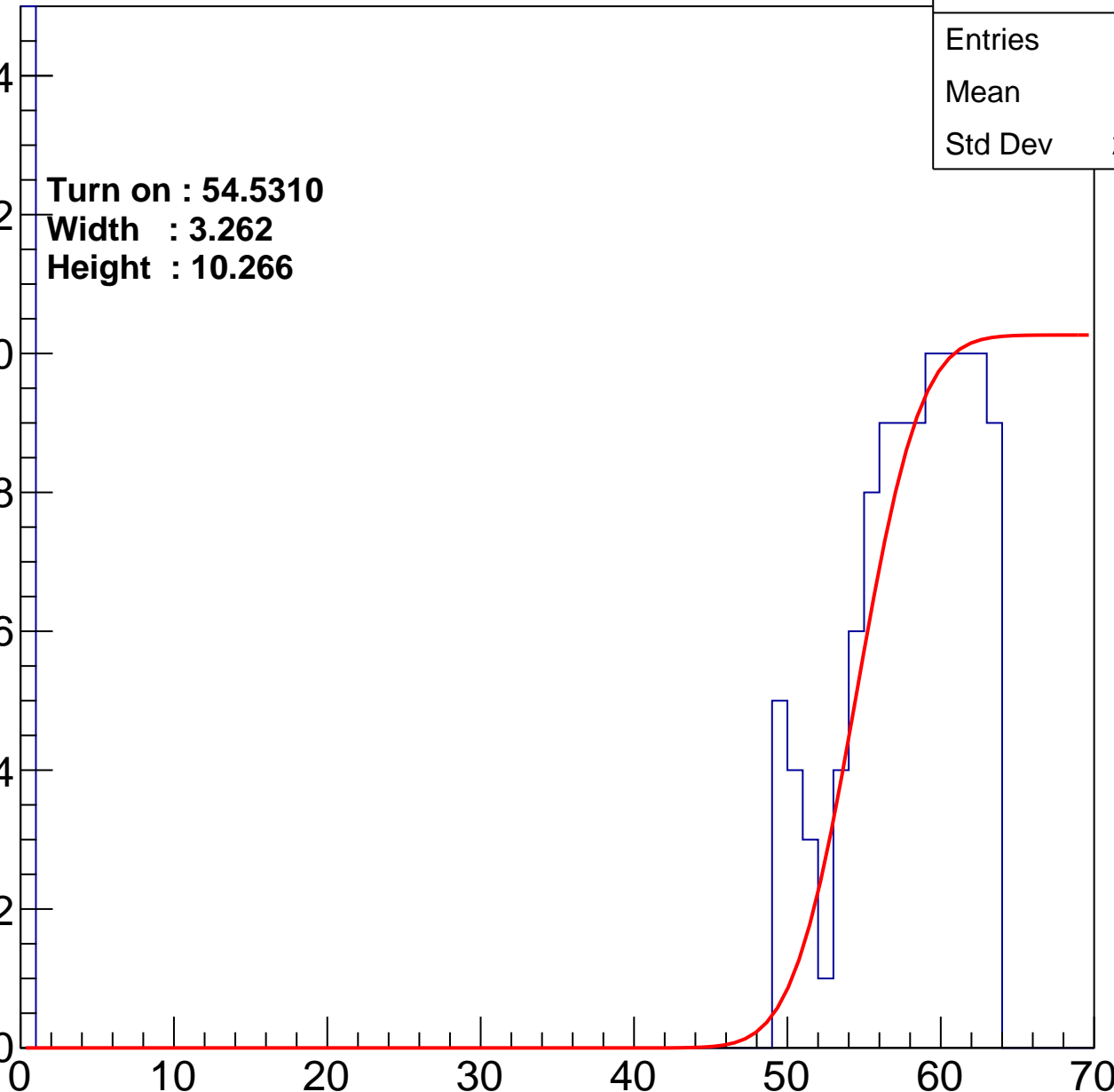
Width : 3.262

Height : 10.266

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	28.34
Std Dev	29.37

Turn on : 55.1775

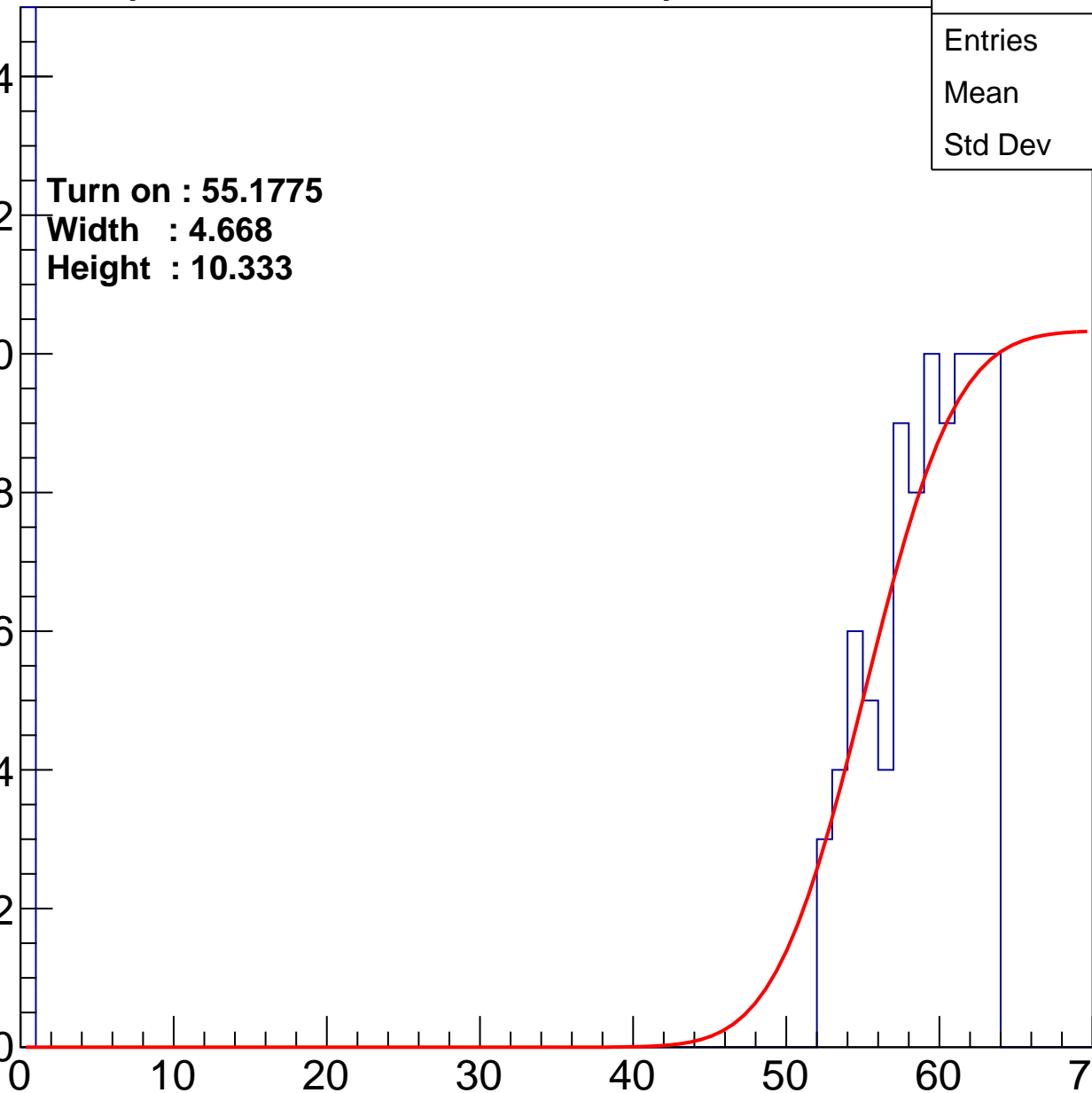
Width : 4.668

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch88

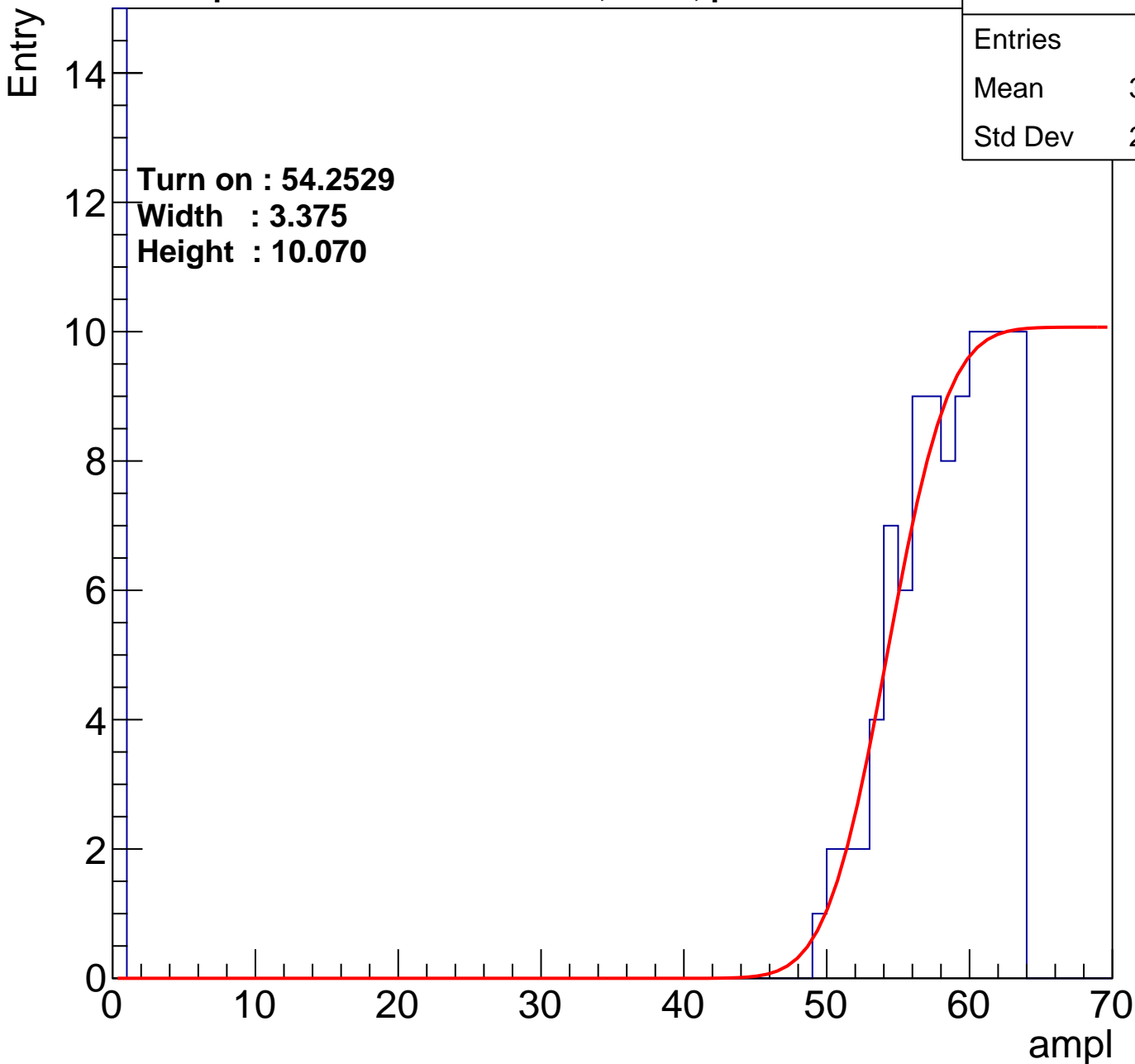
calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	31.23
Std Dev	29.06

Turn on : 54.2529

Width : 3.375

Height : 10.070



B1L104S, U24-ch89

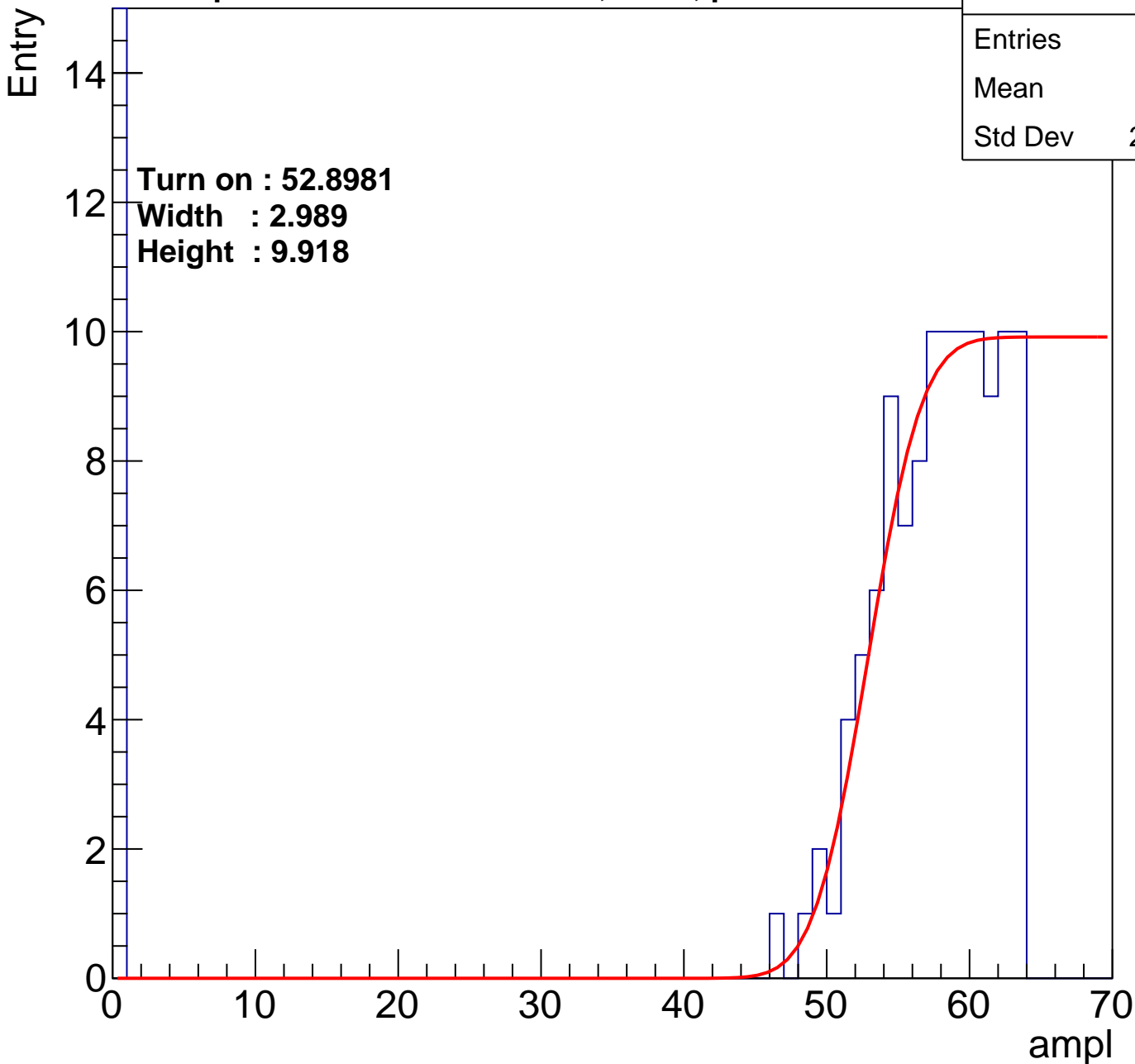
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	34.3
Std Dev	28.29

Turn on : 52.8981

Width : 2.989

Height : 9.918



B1L104S, U24-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	33.73
Std Dev	28.43

Turn on : 52.6297

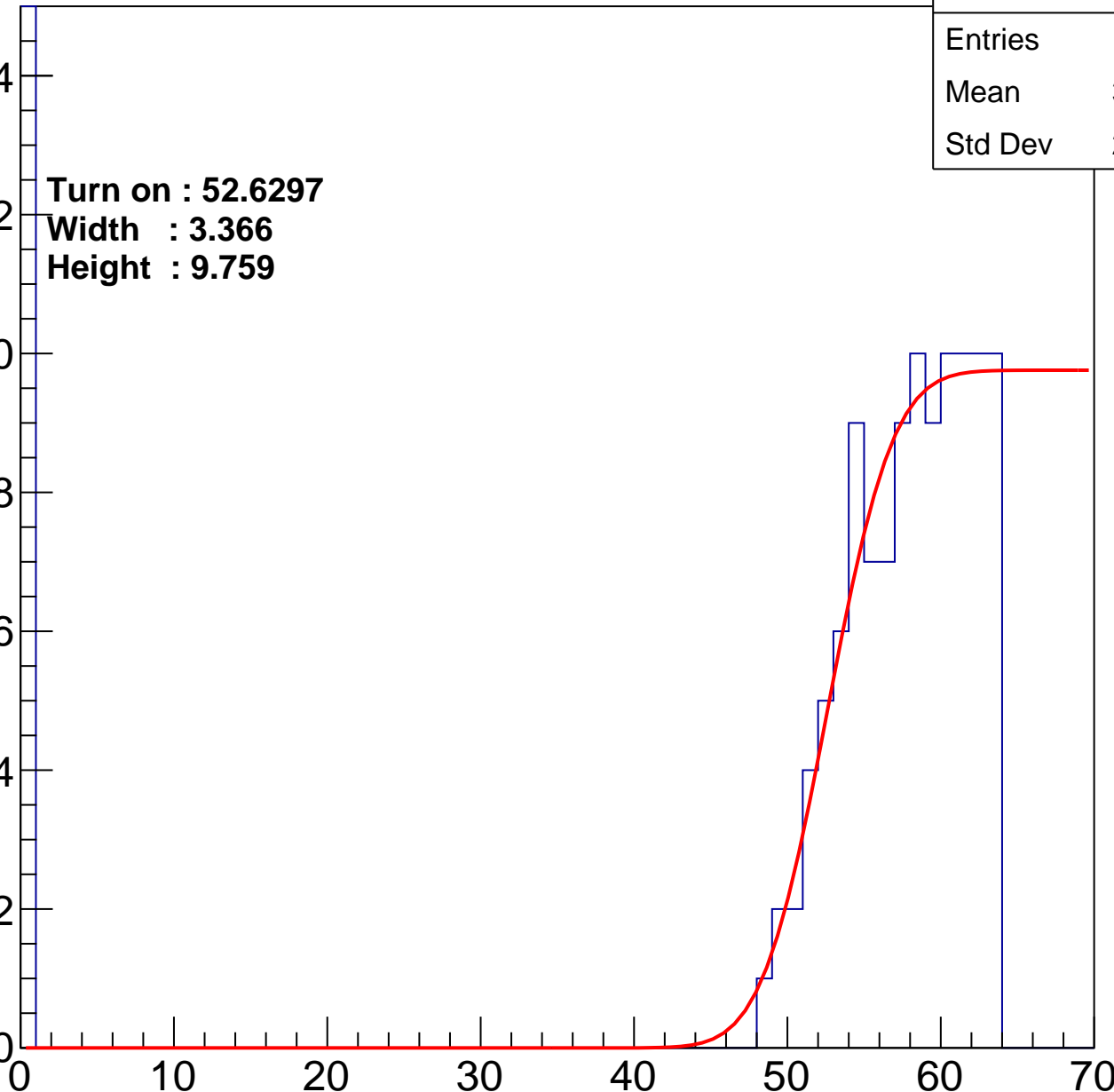
Width : 3.366

Height : 9.759

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch91

calib_packv5_033123_0516.root, FC#4, port A1

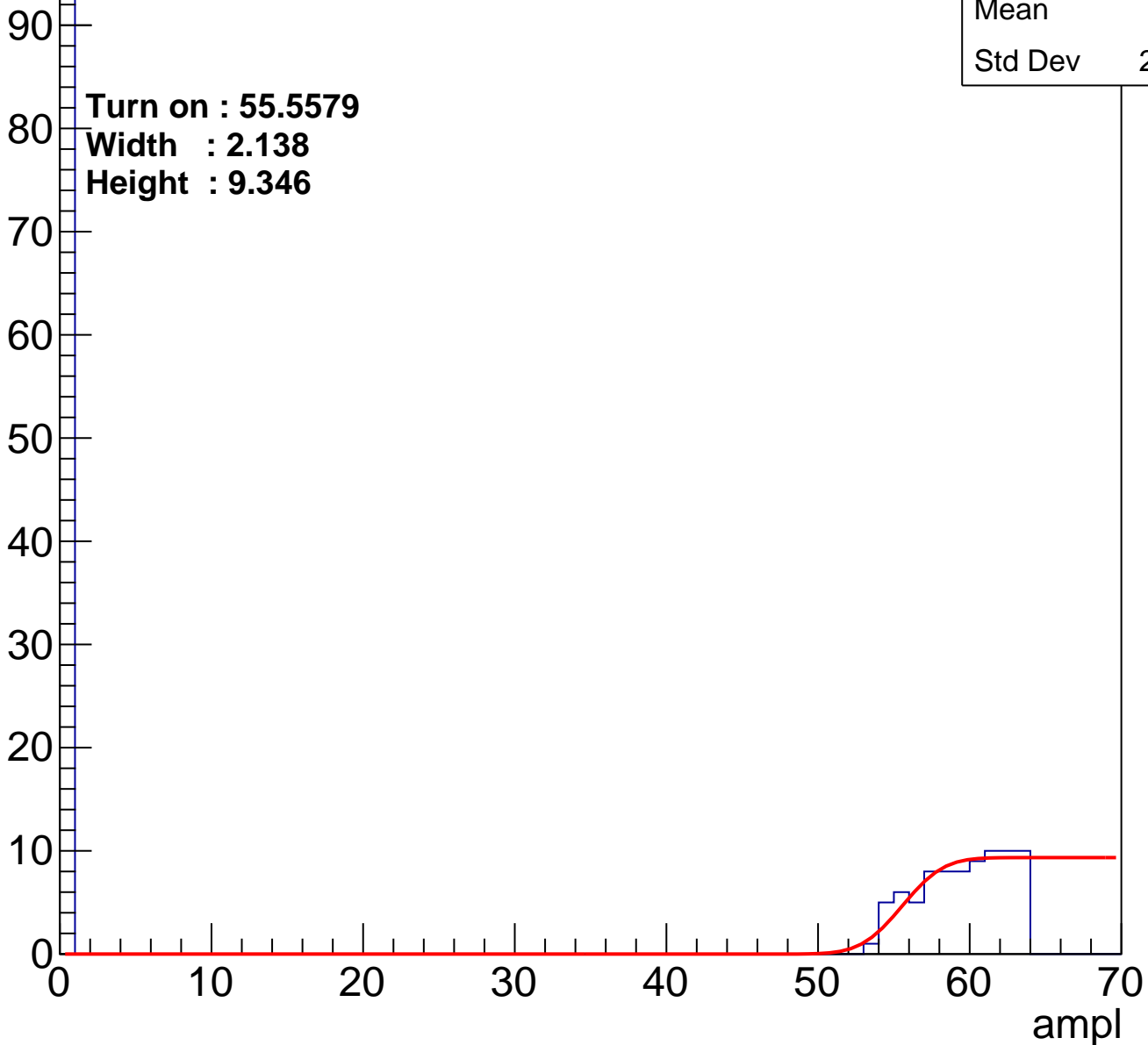
Entries	175
Mean	27
Std Dev	29.48

Turn on : 55.5579

Width : 2.138

Height : 9.346

Entry



B1L104S, U24-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	38.58
Std Dev	27.11

Turn on : 52.5582

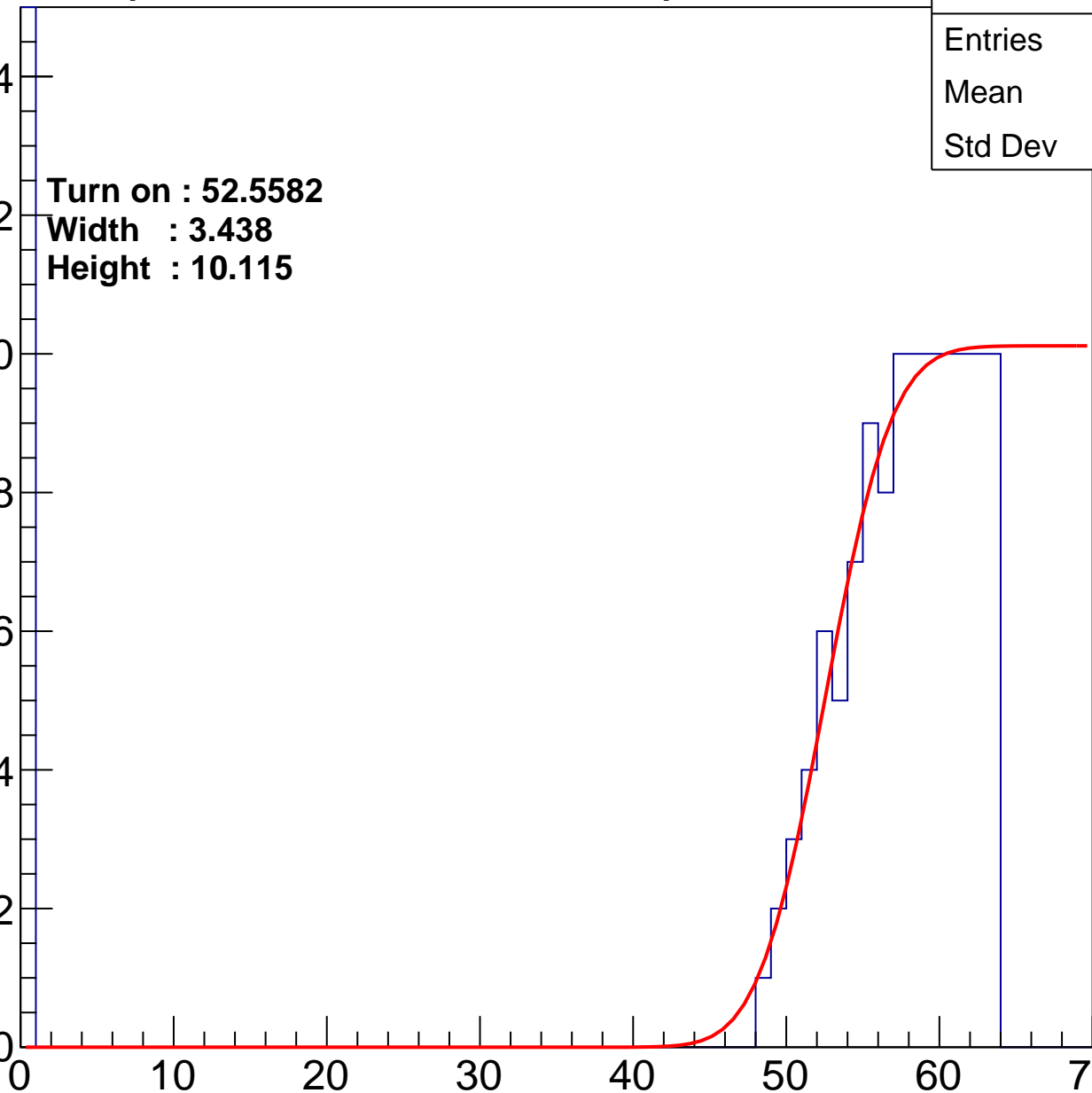
Width : 3.438

Height : 10.115

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	36.83
Std Dev	27.79

Turn on : 51.6954

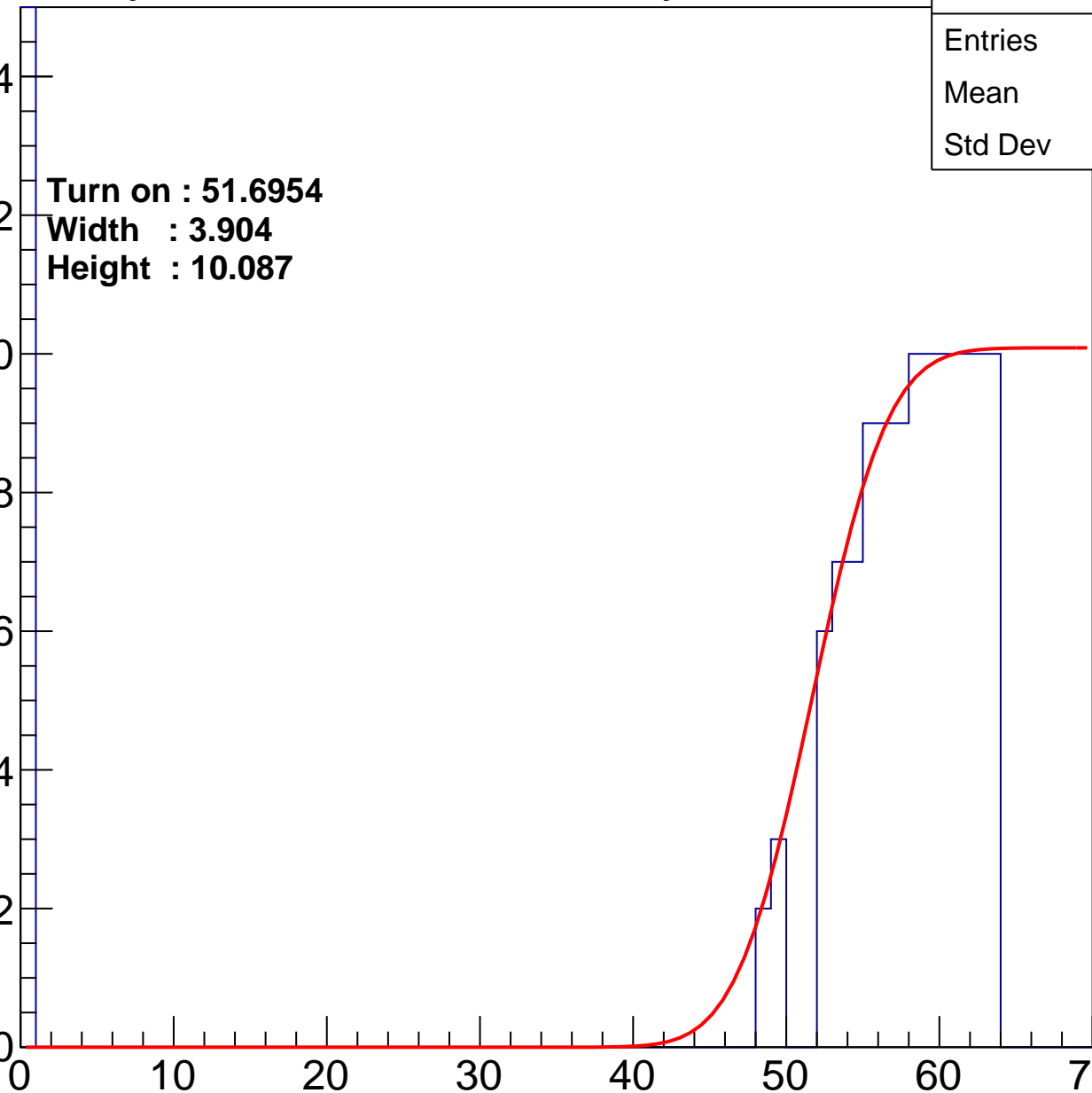
Width : 3.904

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	39.13
Std Dev	27.17

Turn on : 53.9404

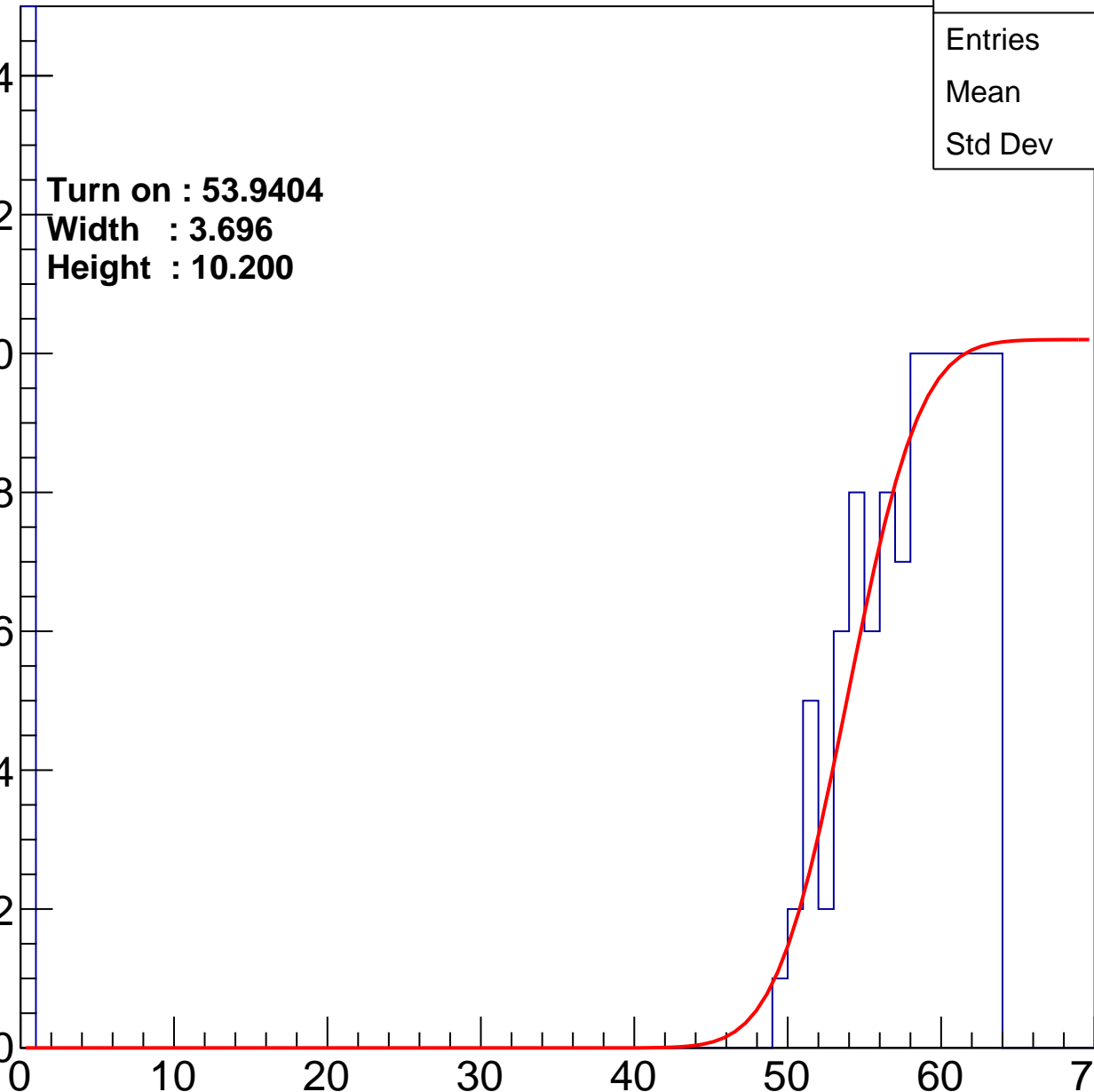
Width : 3.696

Height : 10.200

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	33.19
Std Dev	28.75

Turn on : 54.7222

Width : 3.579

Height : 10.436

Entry

14

12

10

8

6

4

2

0

0

10

20

30

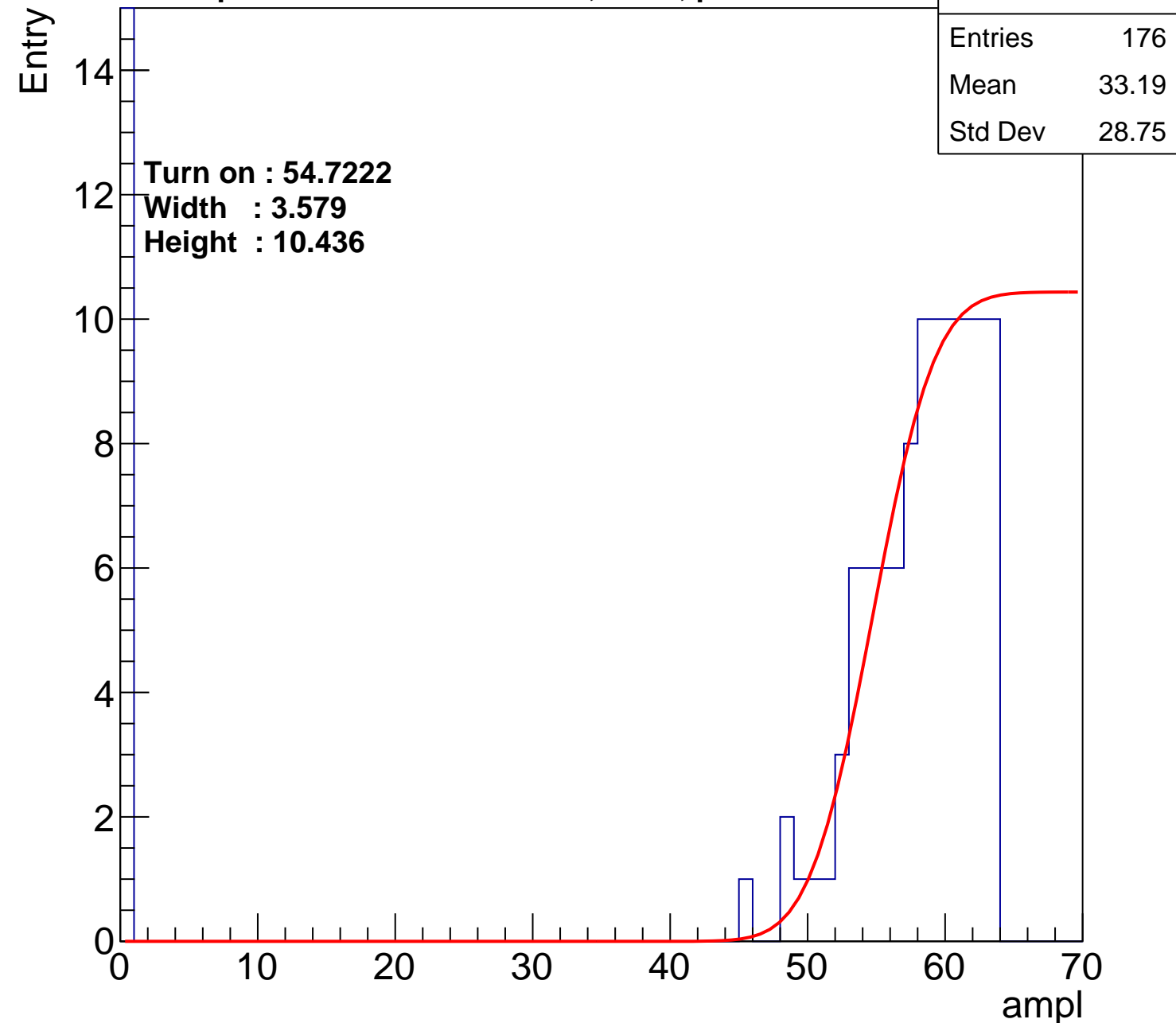
40

50

60

70

ampl



B1L104S, U24-ch96

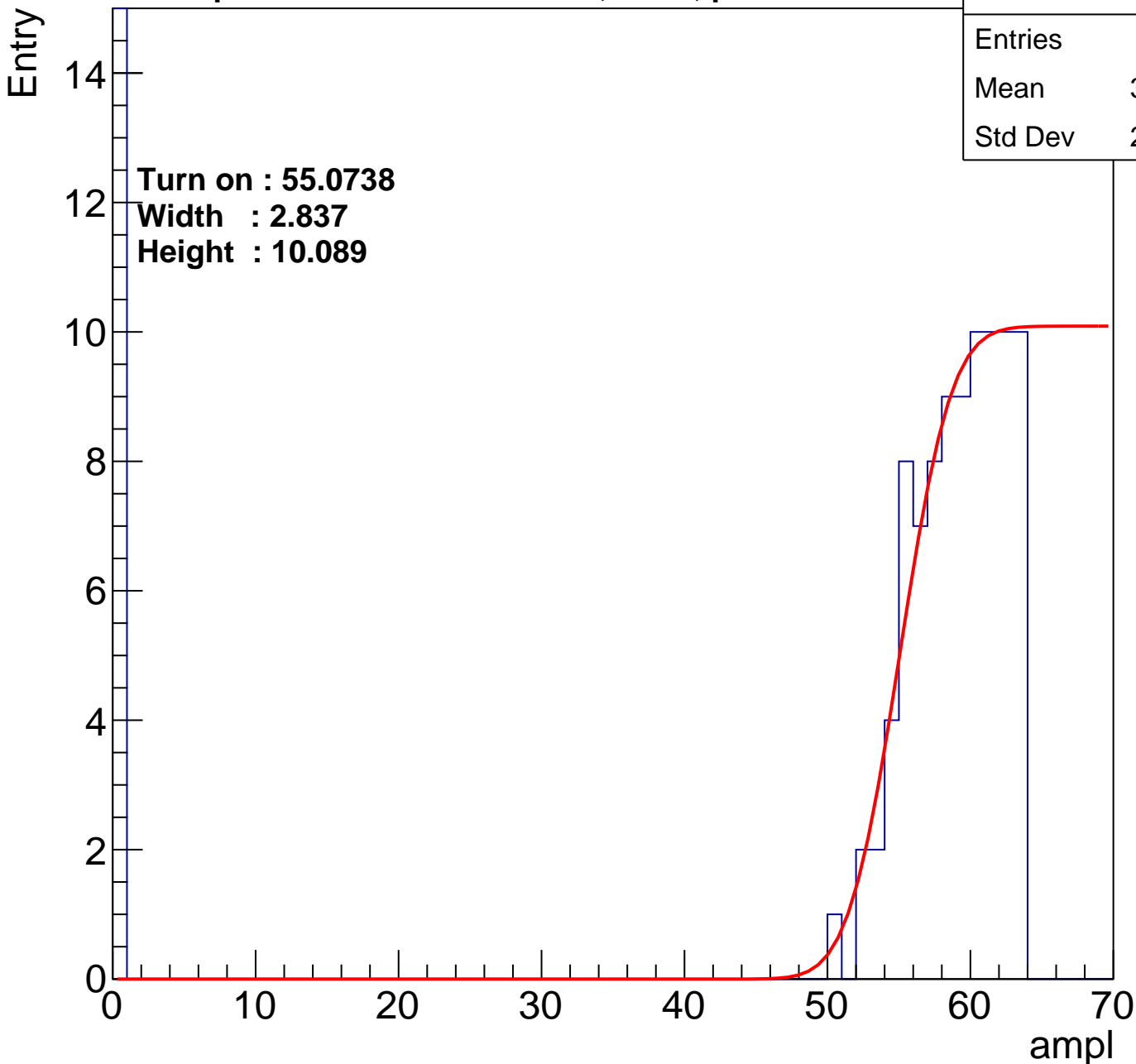
calib_packv5_033123_0516.root, FC#4, port A1

Entries	146
Mean	36.14
Std Dev	28.62

Turn on : 55.0738

Width : 2.837

Height : 10.089



B1L104S, U24-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	36.94
Std Dev	27.78

Turn on : 53.2580

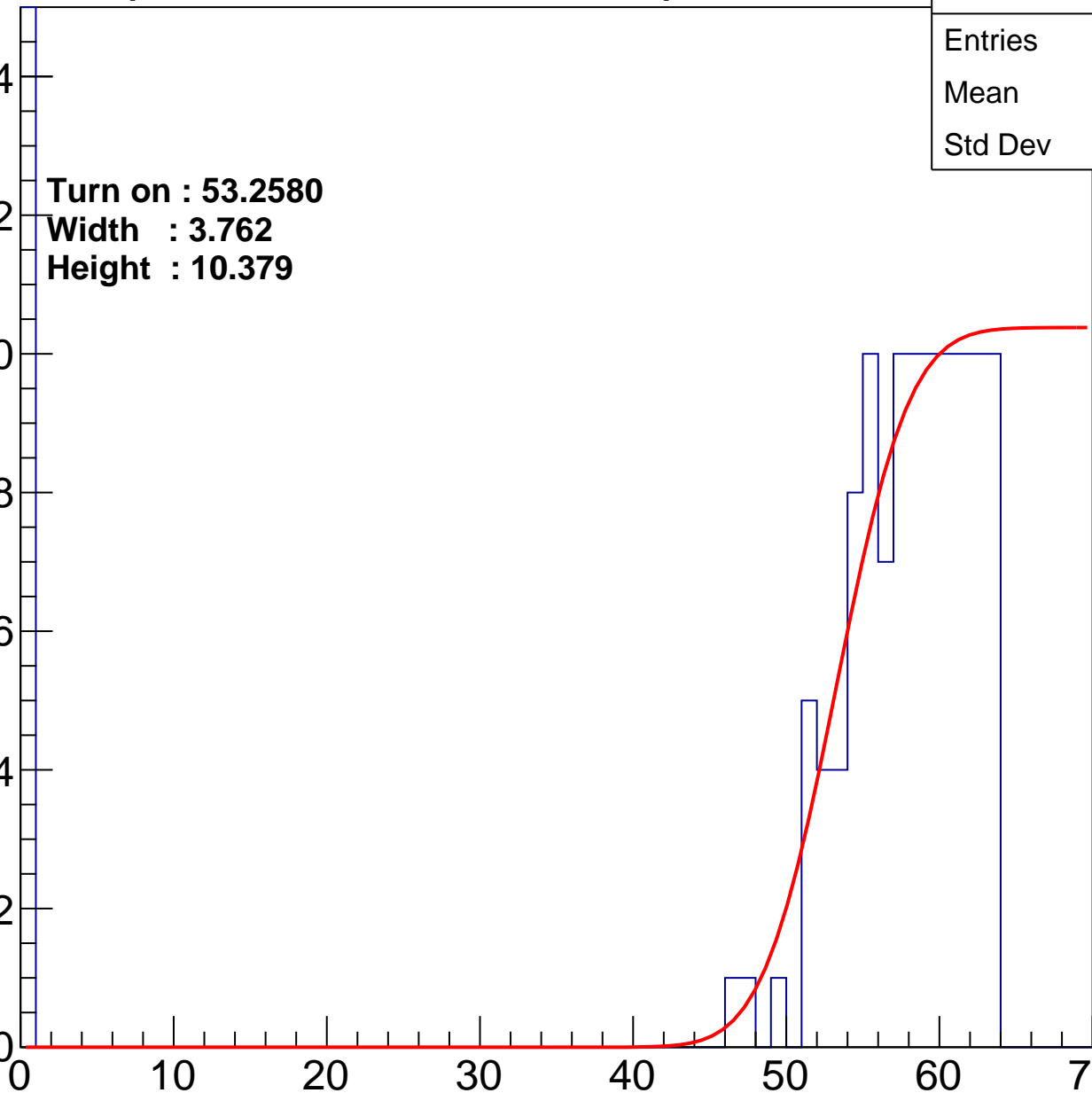
Width : 3.762

Height : 10.379

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch98

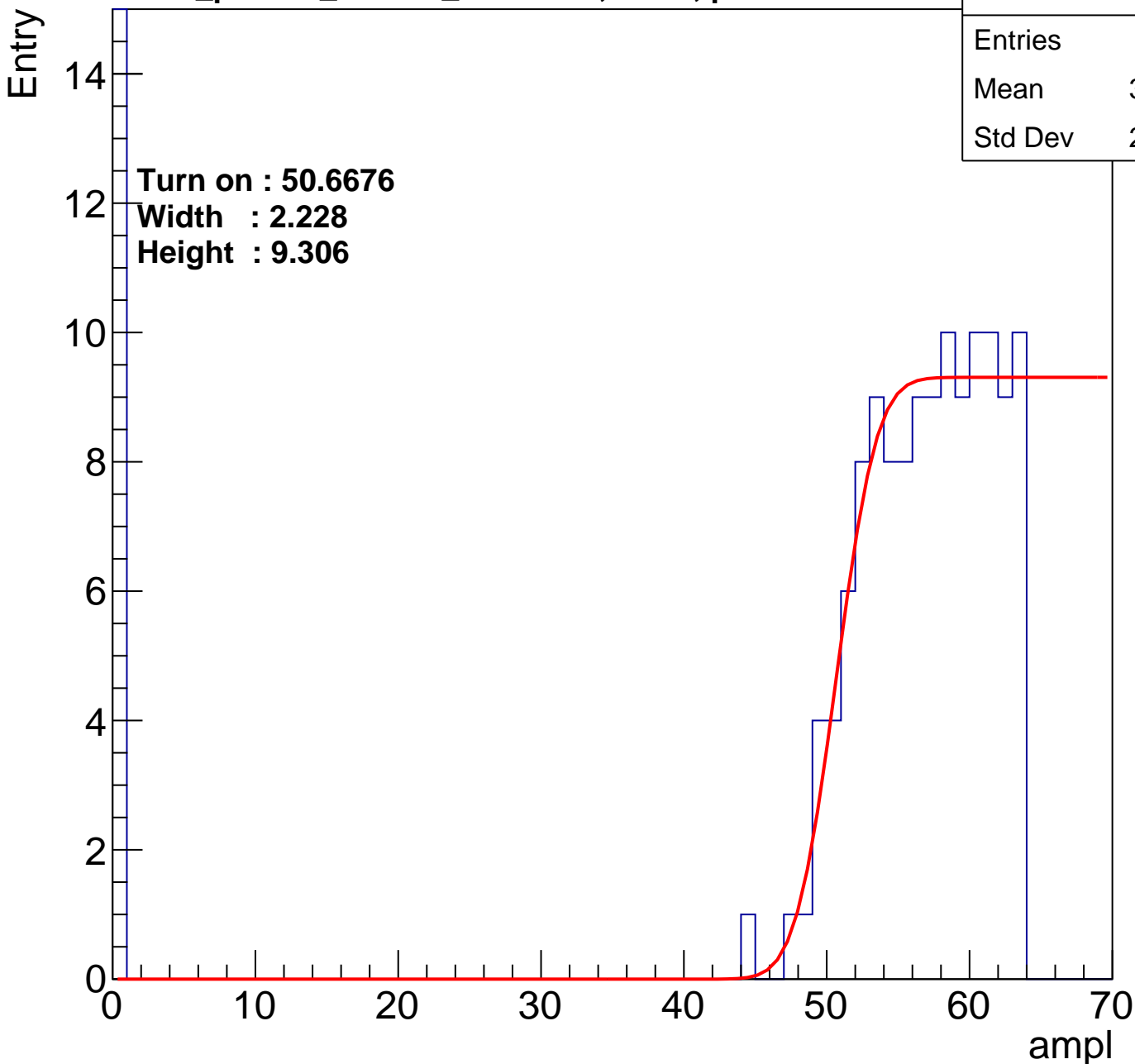
calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	36.57
Std Dev	27.29

Turn on : 50.6676

Width : 2.228

Height : 9.306



B1L104S, U24-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	29.77
Std Dev	29.23

Turn on : 55.0494

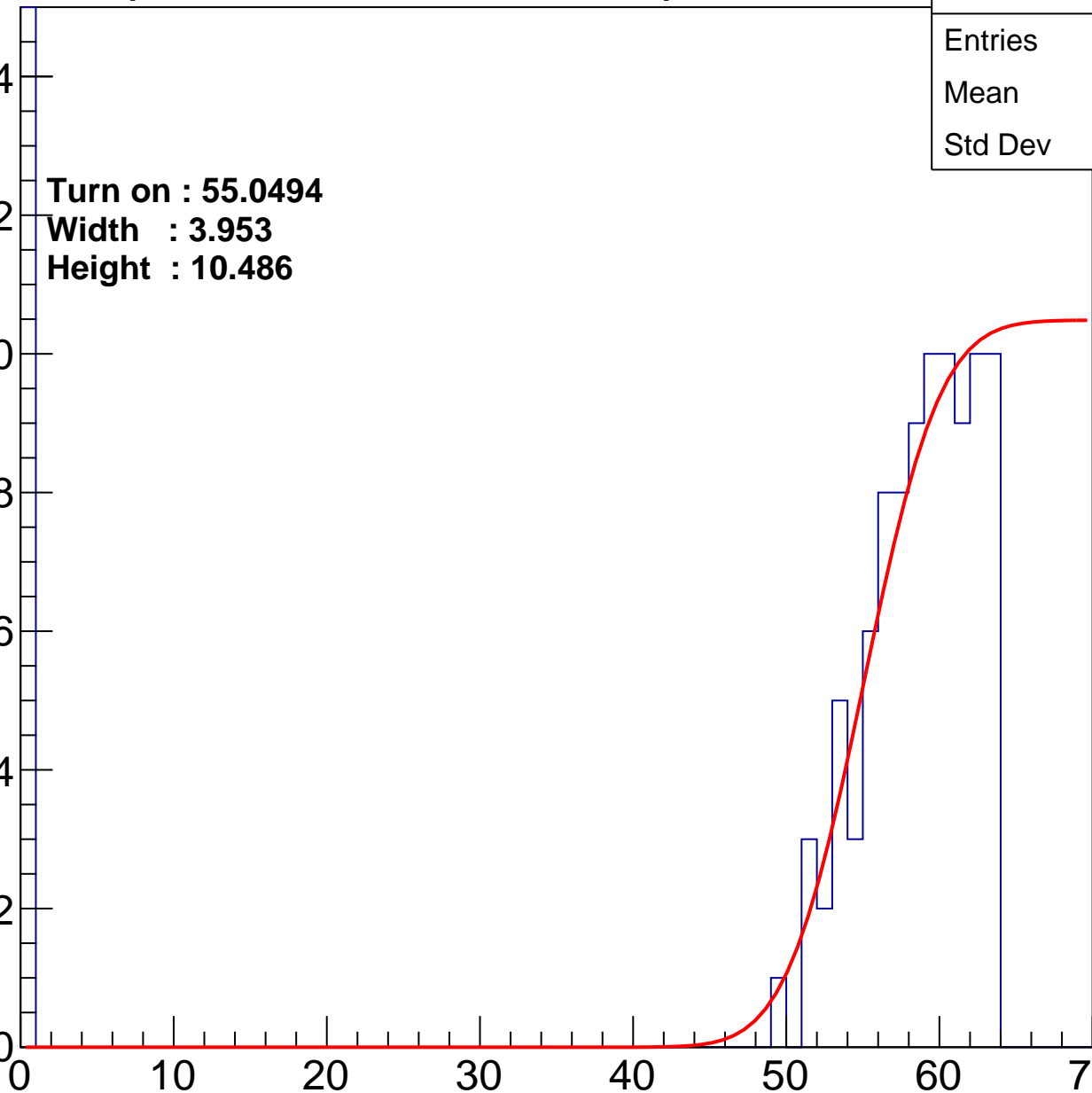
Width : 3.953

Height : 10.486

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	148
Mean	35.22
Std Dev	28.78

Turn on : 54.8677

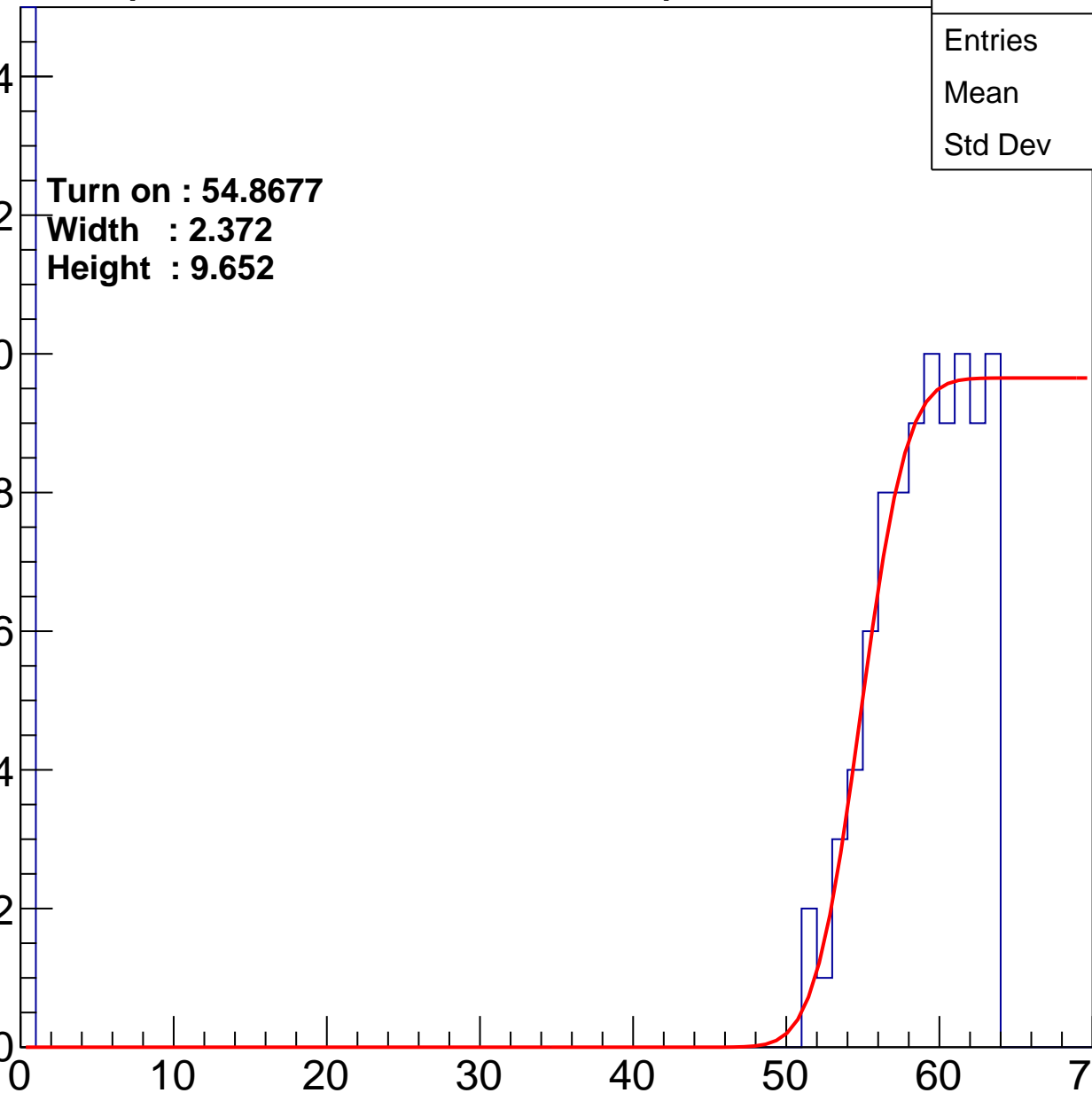
Width : 2.372

Height : 9.652

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	31.31
Std Dev	29.36

Turn on : 56.5533

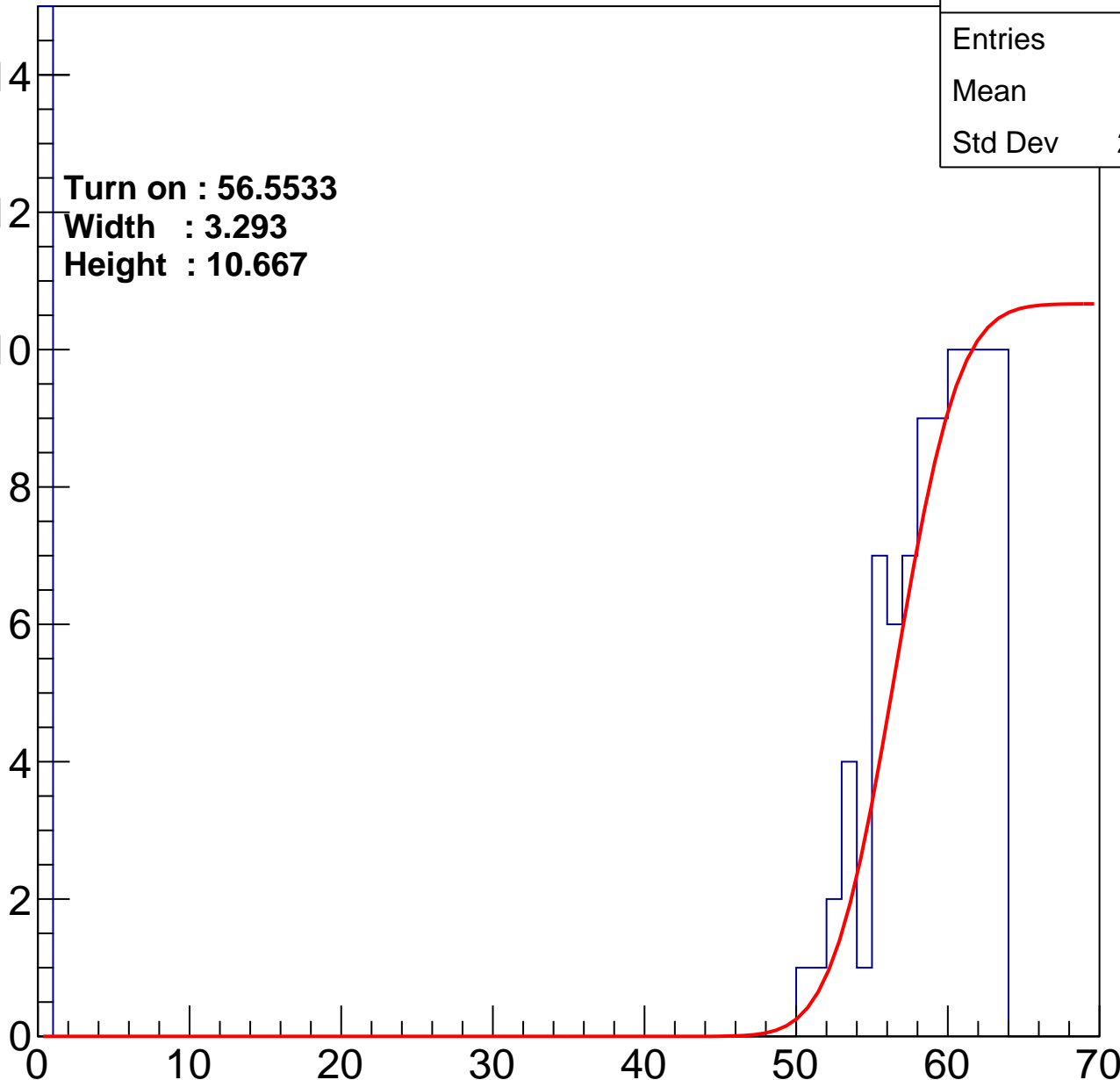
Width : 3.293

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	35.53
Std Dev	28

Turn on : 52.6793

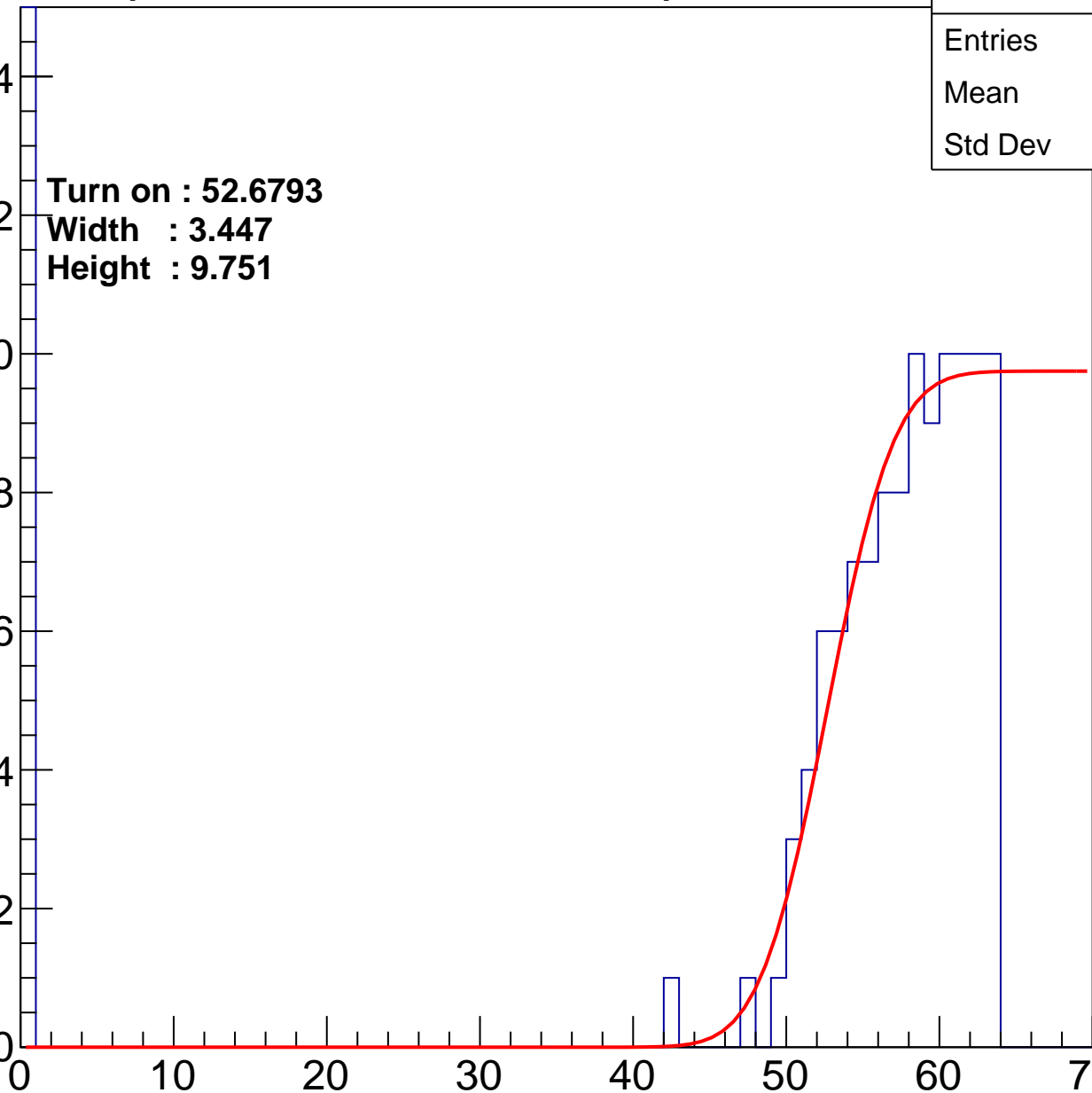
Width : 3.447

Height : 9.751

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	31.67
Std Dev	28.66

Turn on : 55.5757

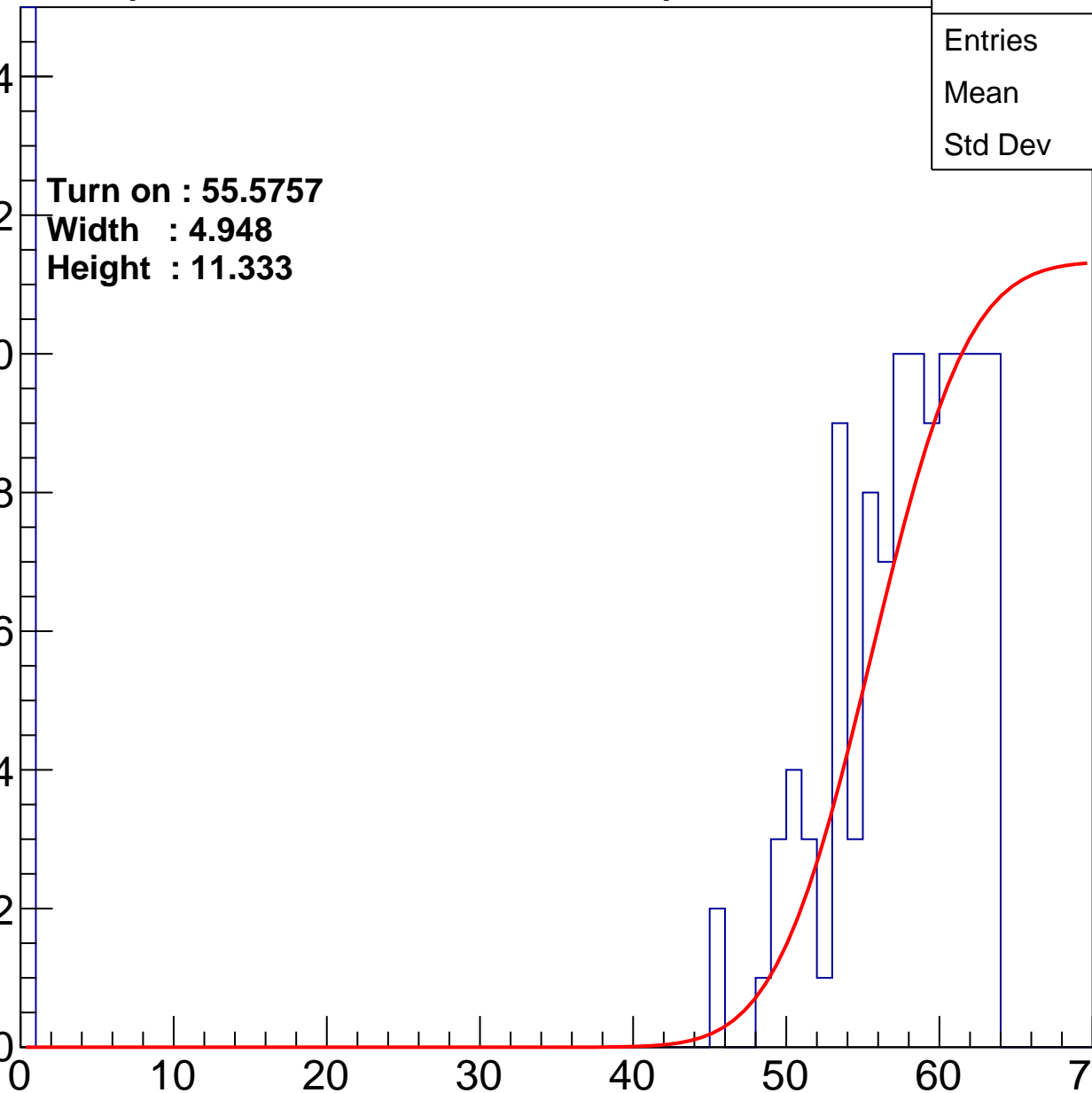
Width : 4.948

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	34.07
Std Dev	28.49

Turn on : 53.9029

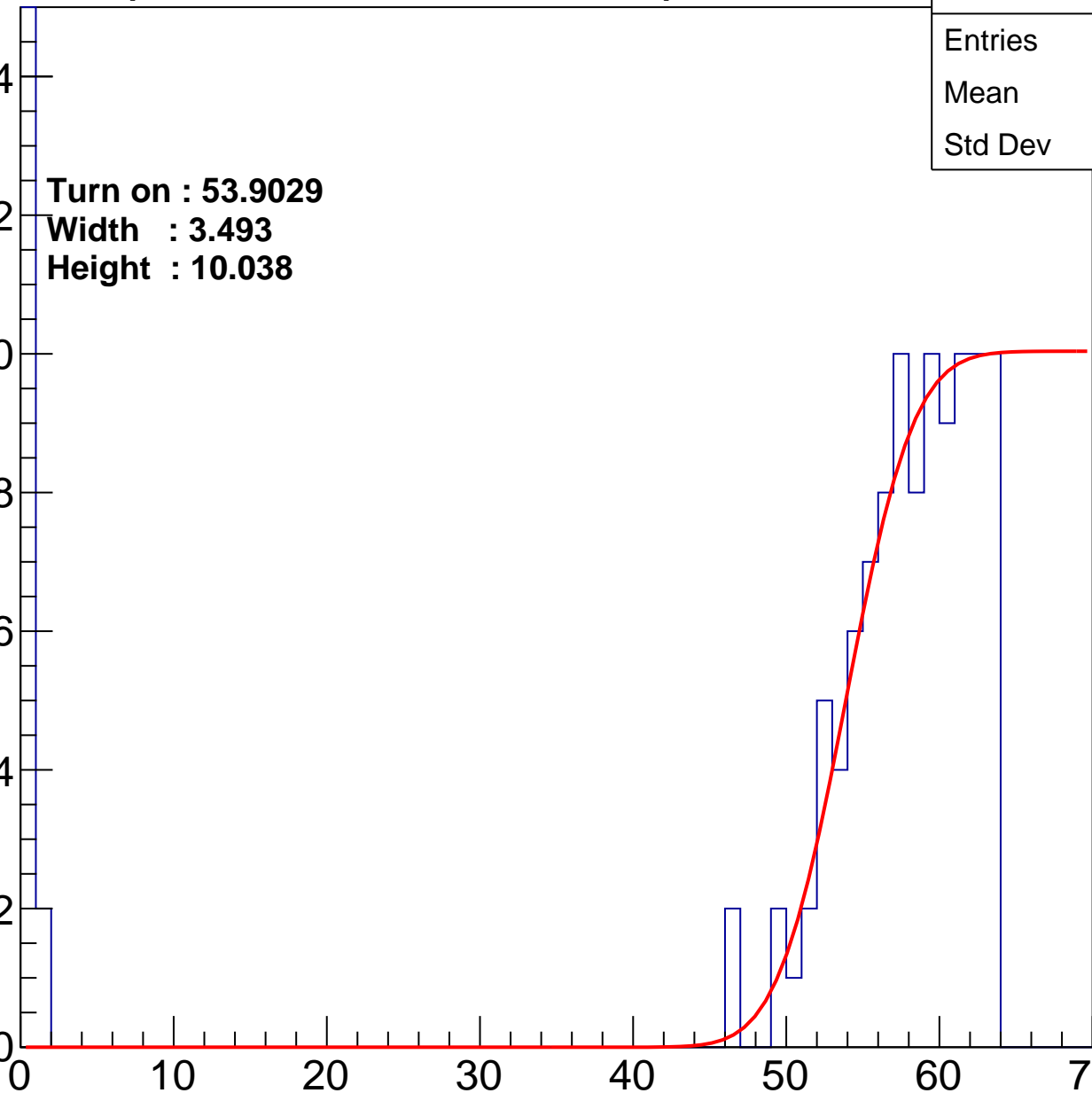
Width : 3.493

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	36.63
Std Dev	28.14

Turn on : 54.3097

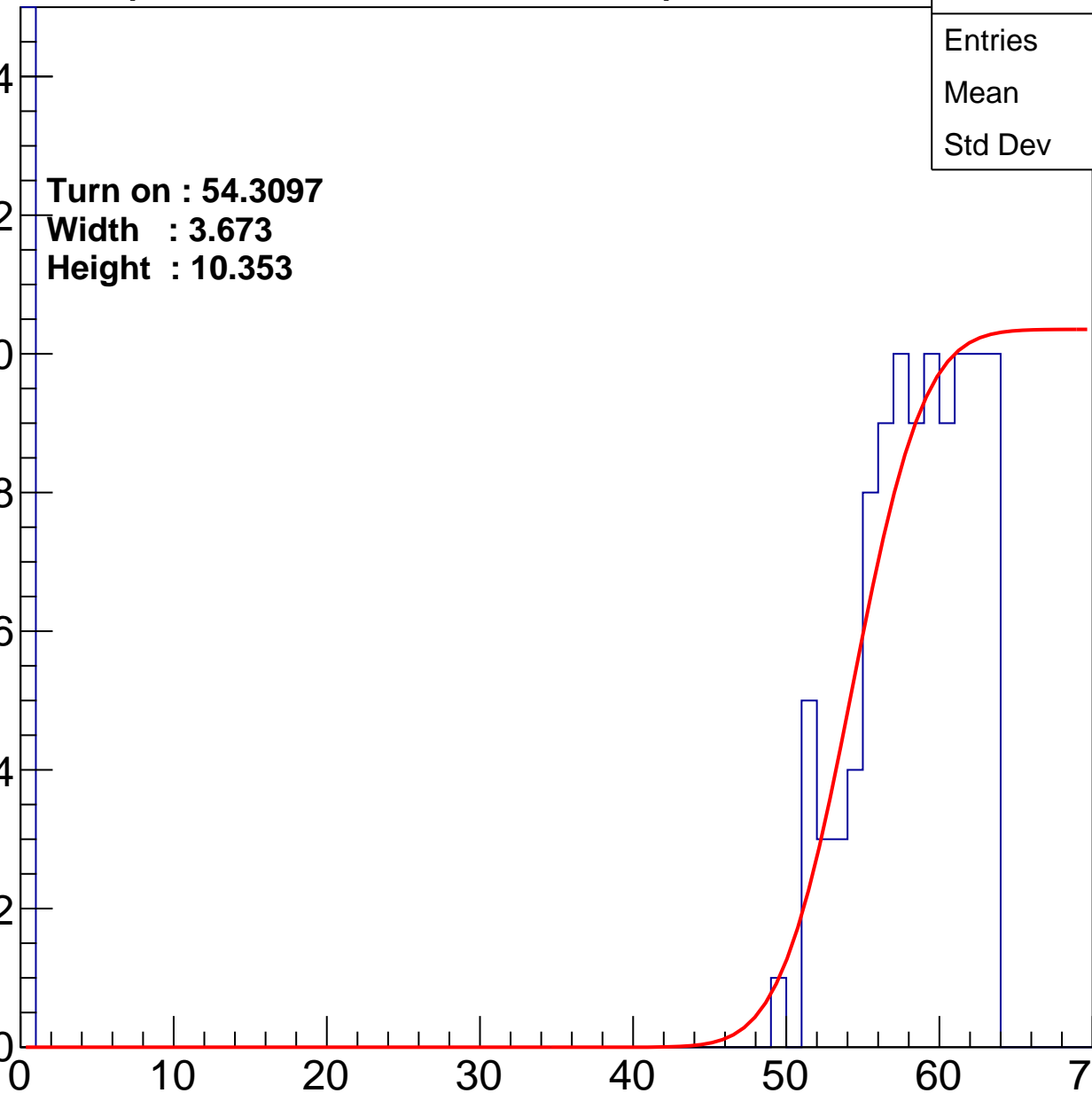
Width : 3.673

Height : 10.353

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.06
Std Dev	28.65

Turn on : 54.4441

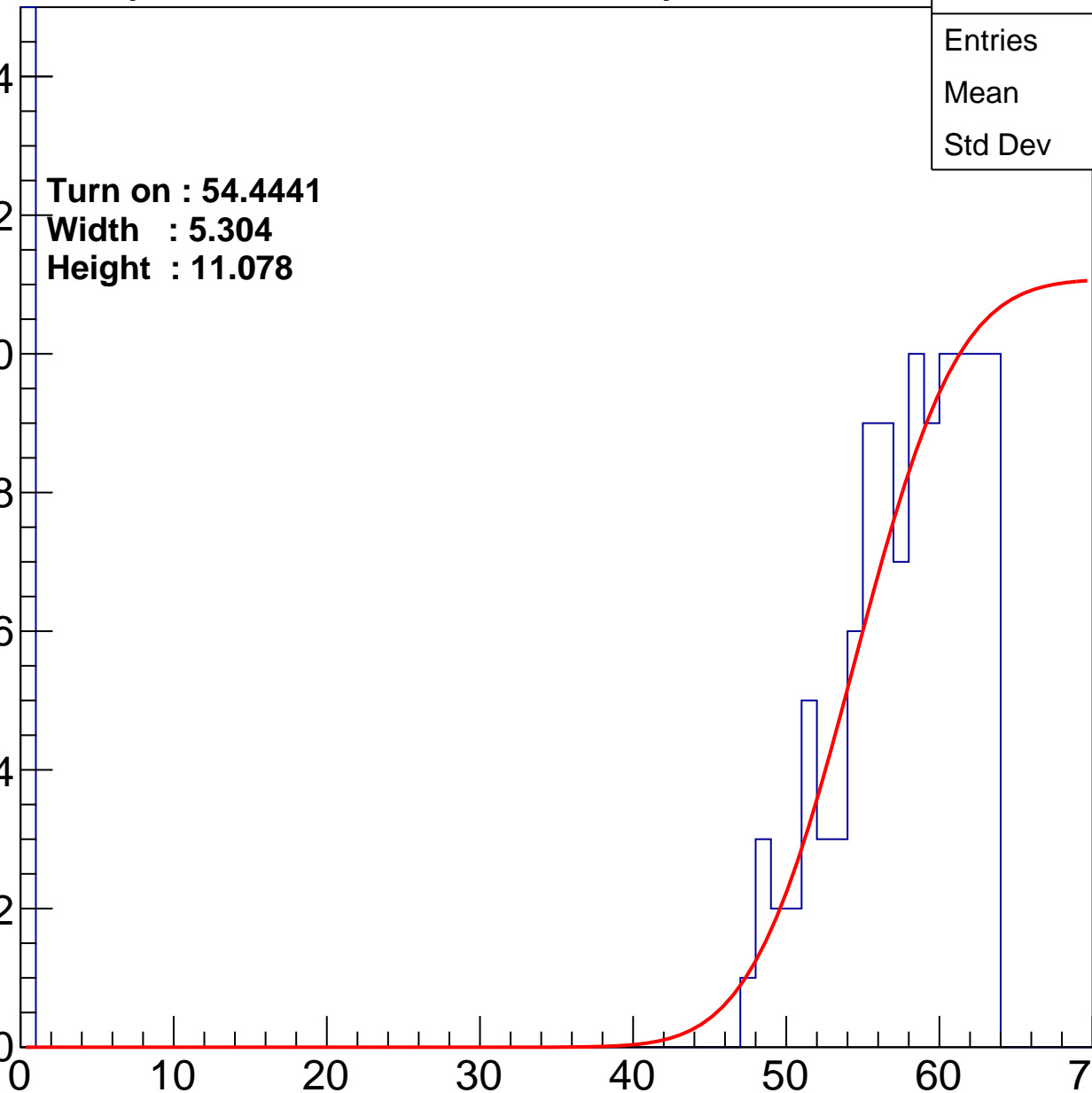
Width : 5.304

Height : 11.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	29.43
Std Dev	29.51

Turn on : 55.8127

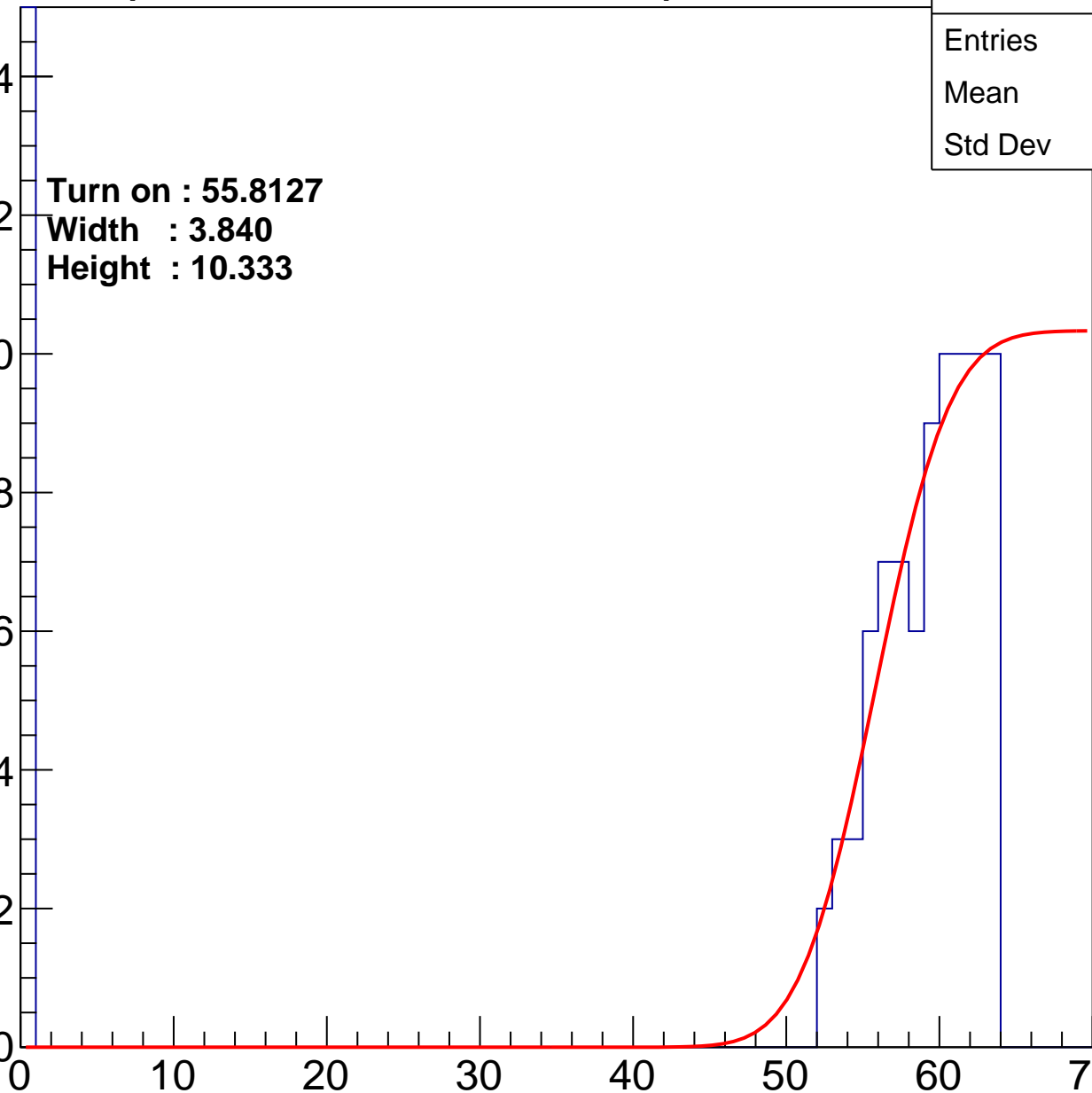
Width : 3.840

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	145
Mean	38.27
Std Dev	27.88

Turn on : 54.7125

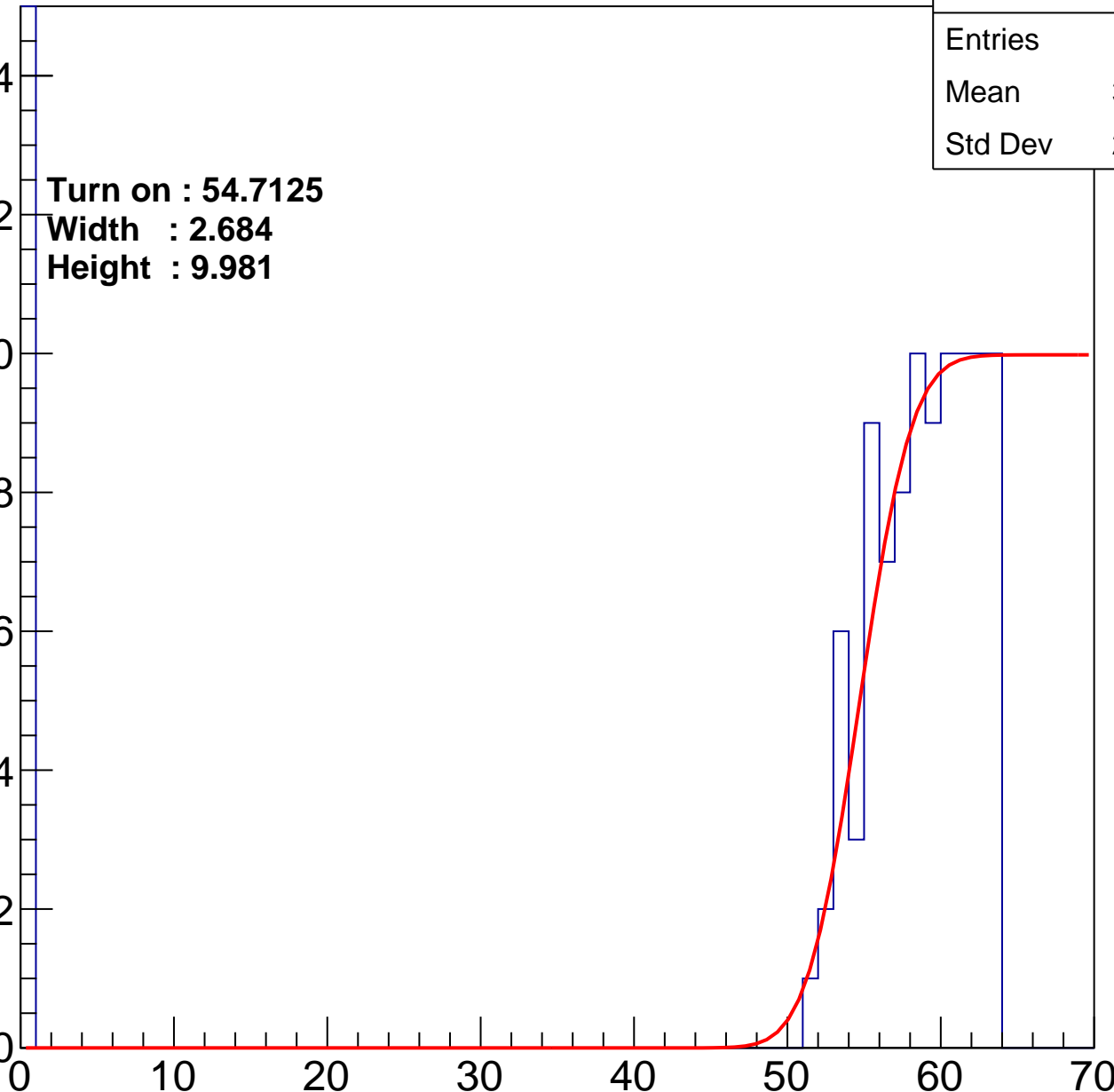
Width : 2.684

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch109

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	27.7
Std Dev	29.32

Turn on : 55.9497

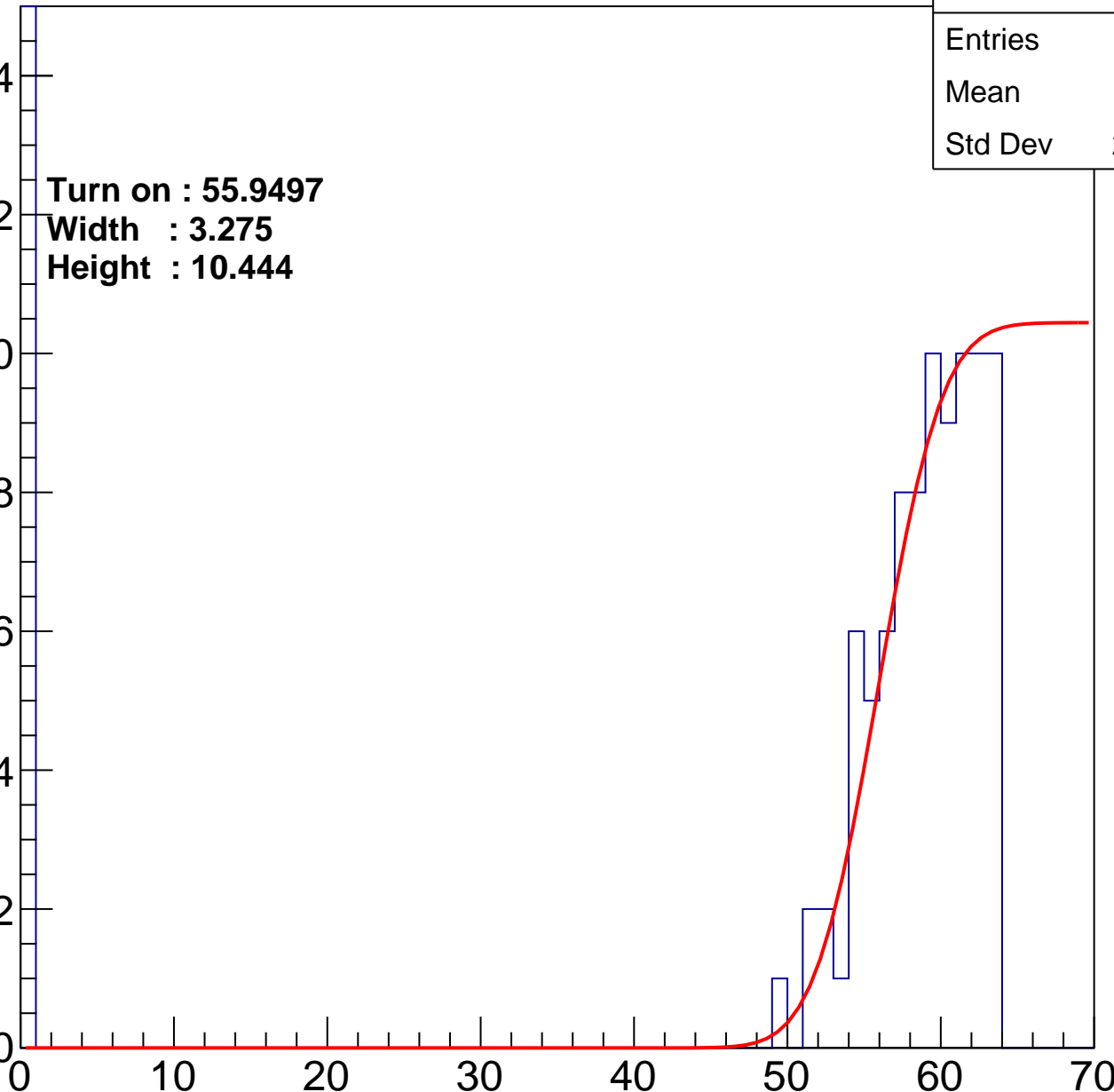
Width : 3.275

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	33.4
Std Dev	28.51

Turn on : 53.5138

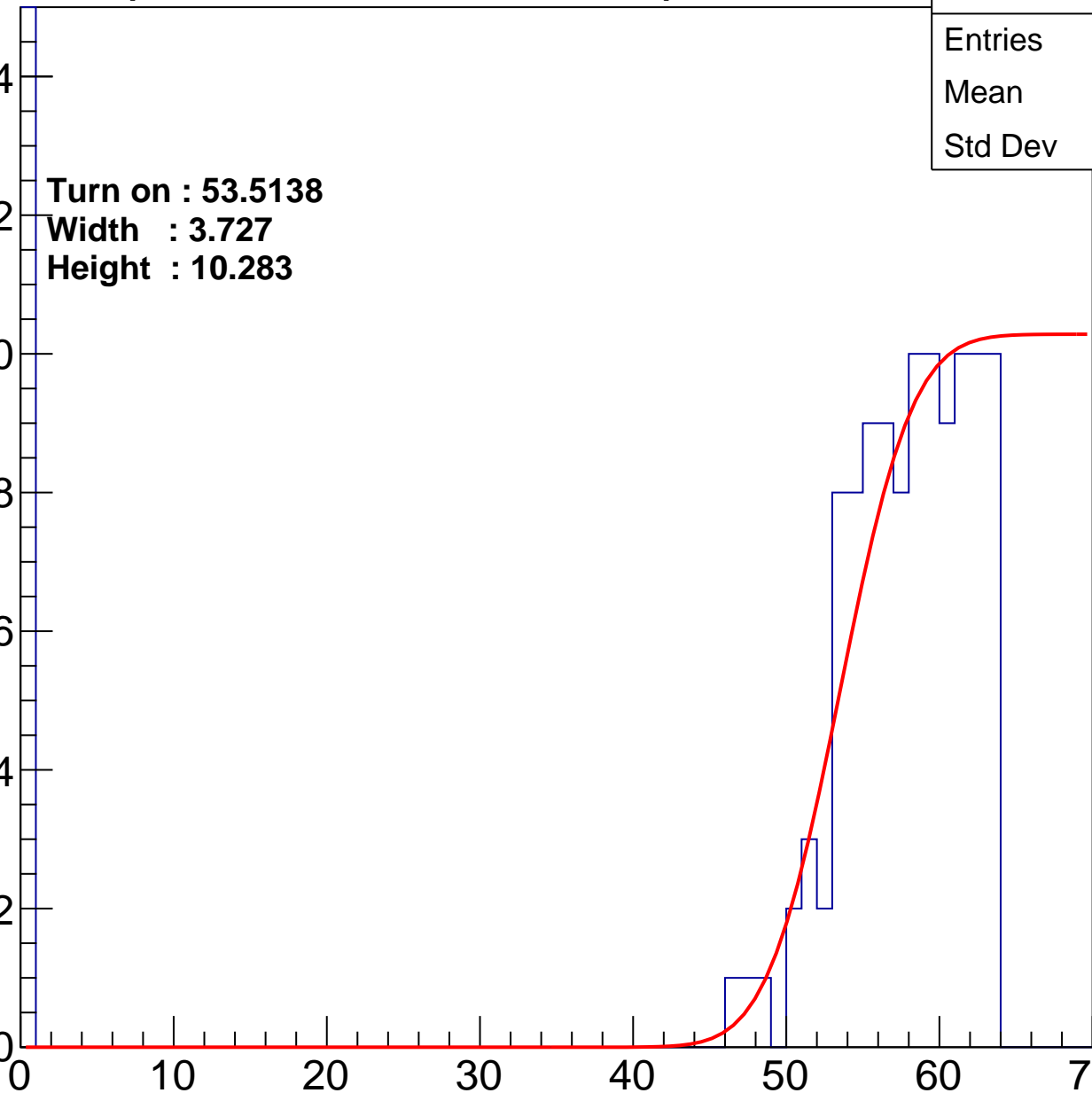
Width : 3.727

Height : 10.283

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	37.08
Std Dev	27.62

Turn on : 53.7497

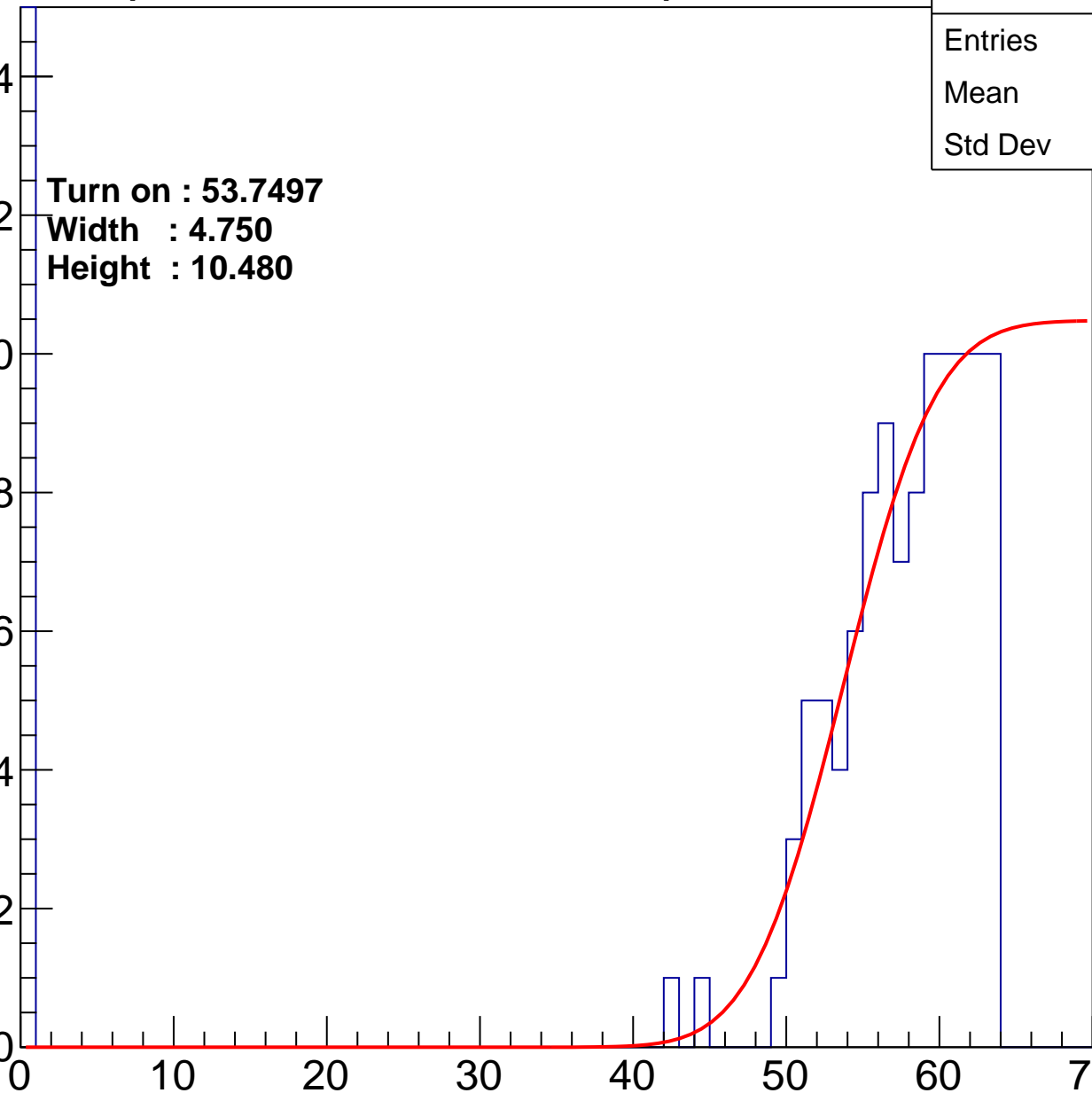
Width : 4.750

Height : 10.480

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	32.26
Std Dev	28.78

Turn on : 54.9207

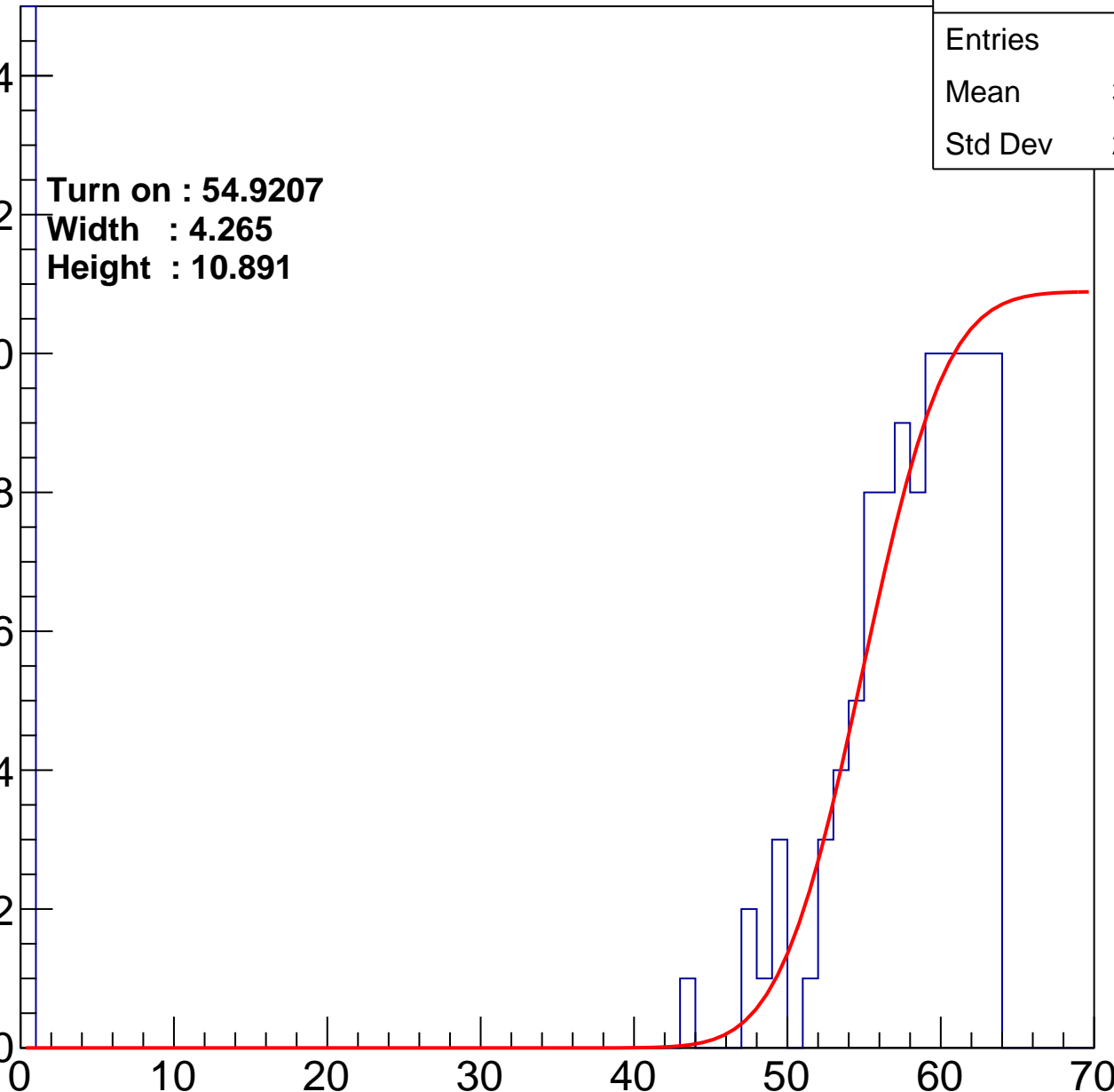
Width : 4.265

Height : 10.891

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch113

calib_packv5_033123_0516.root, FC#4, port A1

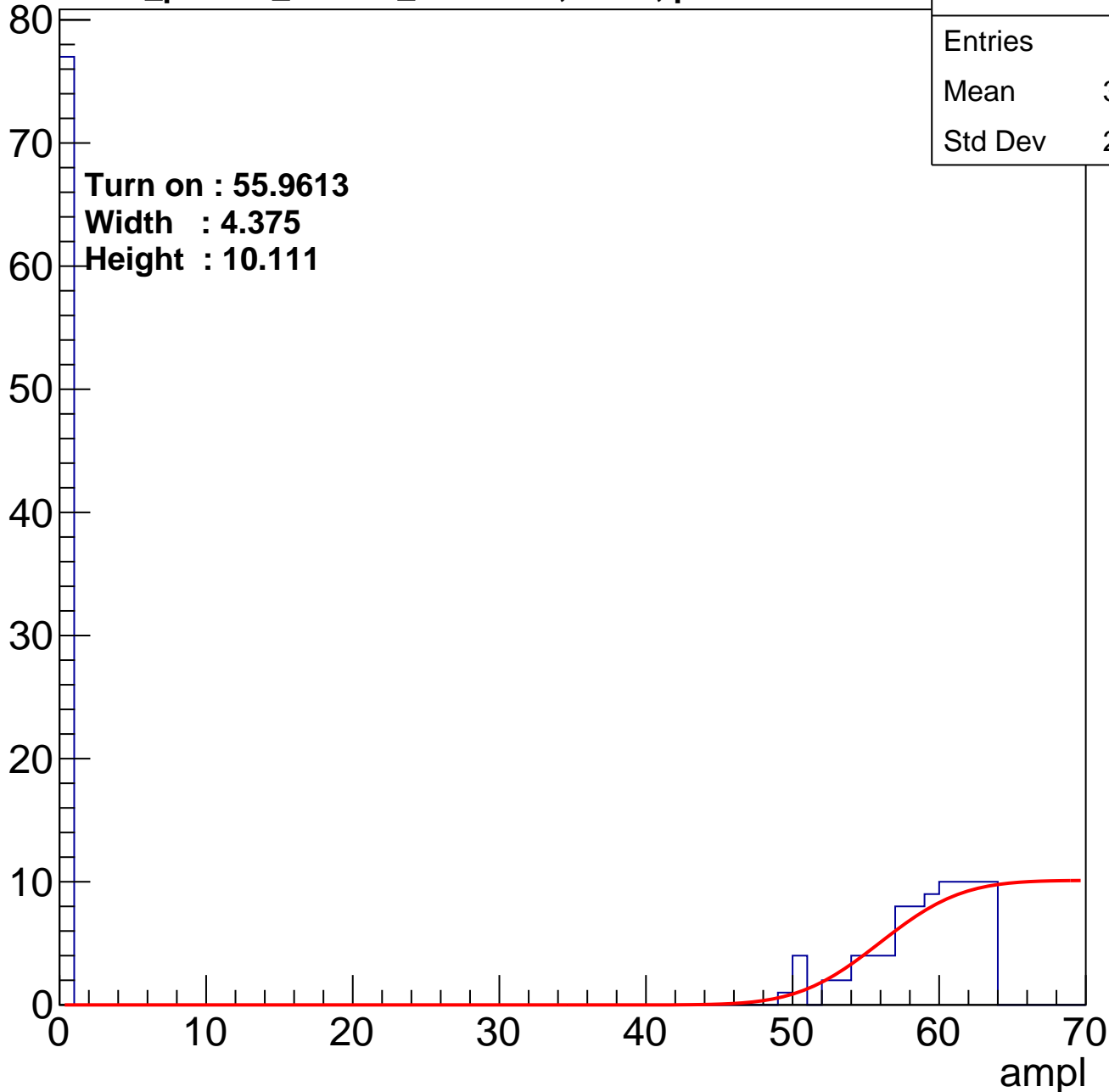
Entries	163
Mean	30.86
Std Dev	29.32

Turn on : 55.9613

Width : 4.375

Height : 10.111

Entry



B1L104S, U24-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	33.5
Std Dev	28.52

Turn on : 54.8078

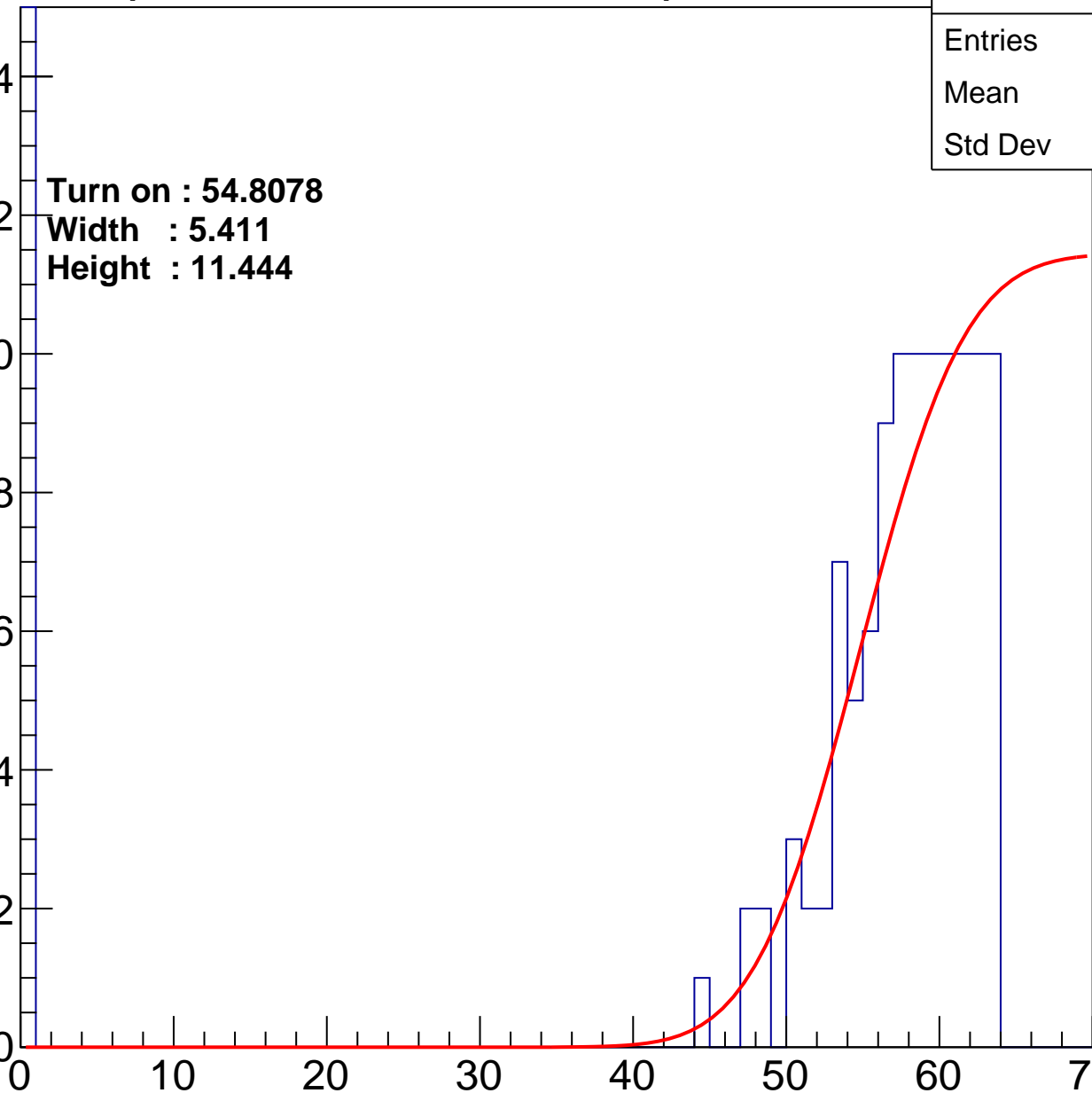
Width : 5.411

Height : 11.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	35.39
Std Dev	28.55

Turn on : 53.8804

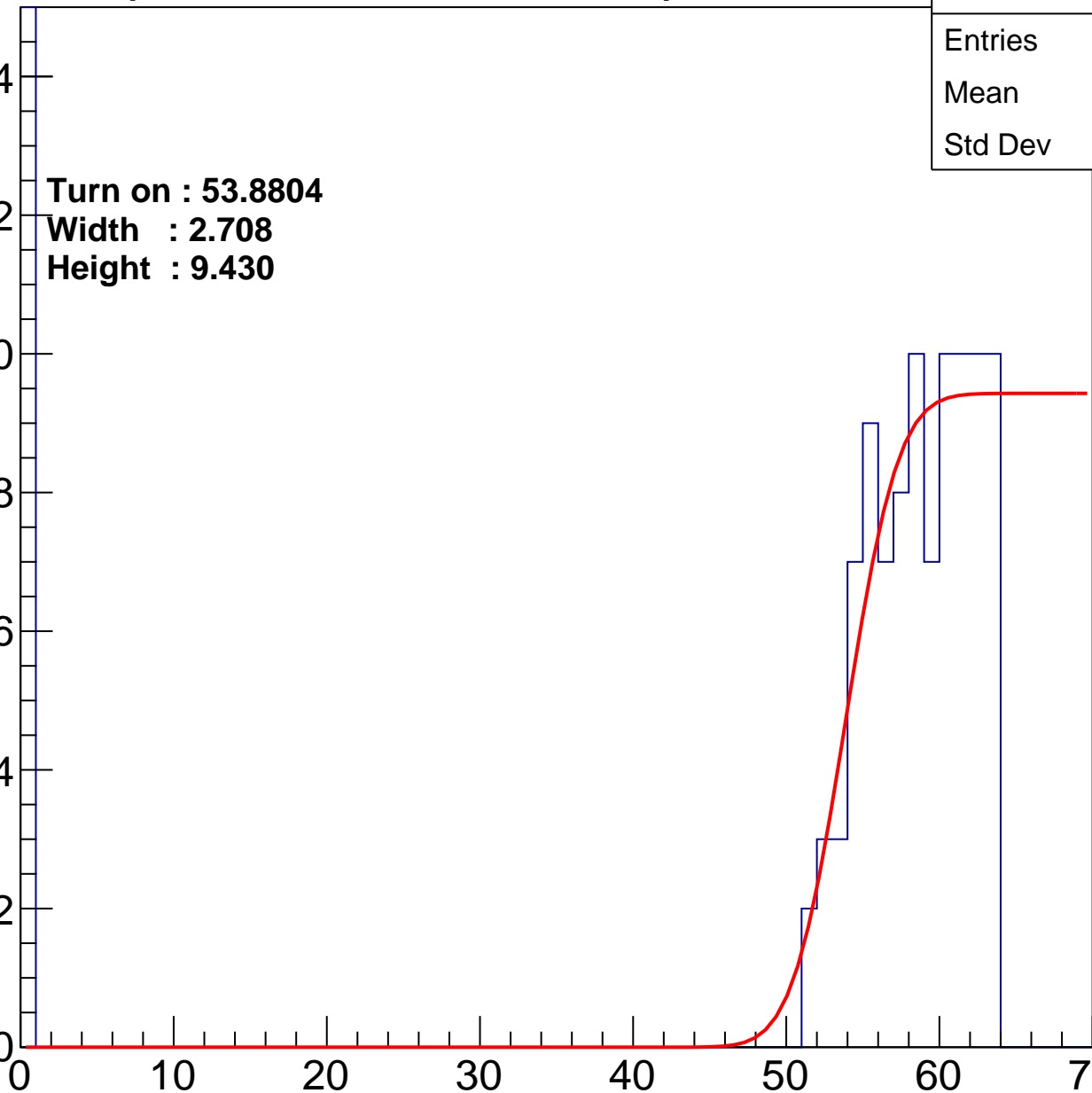
Width : 2.708

Height : 9.430

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	37.2
Std Dev	27.85

Turn on : 54.7969

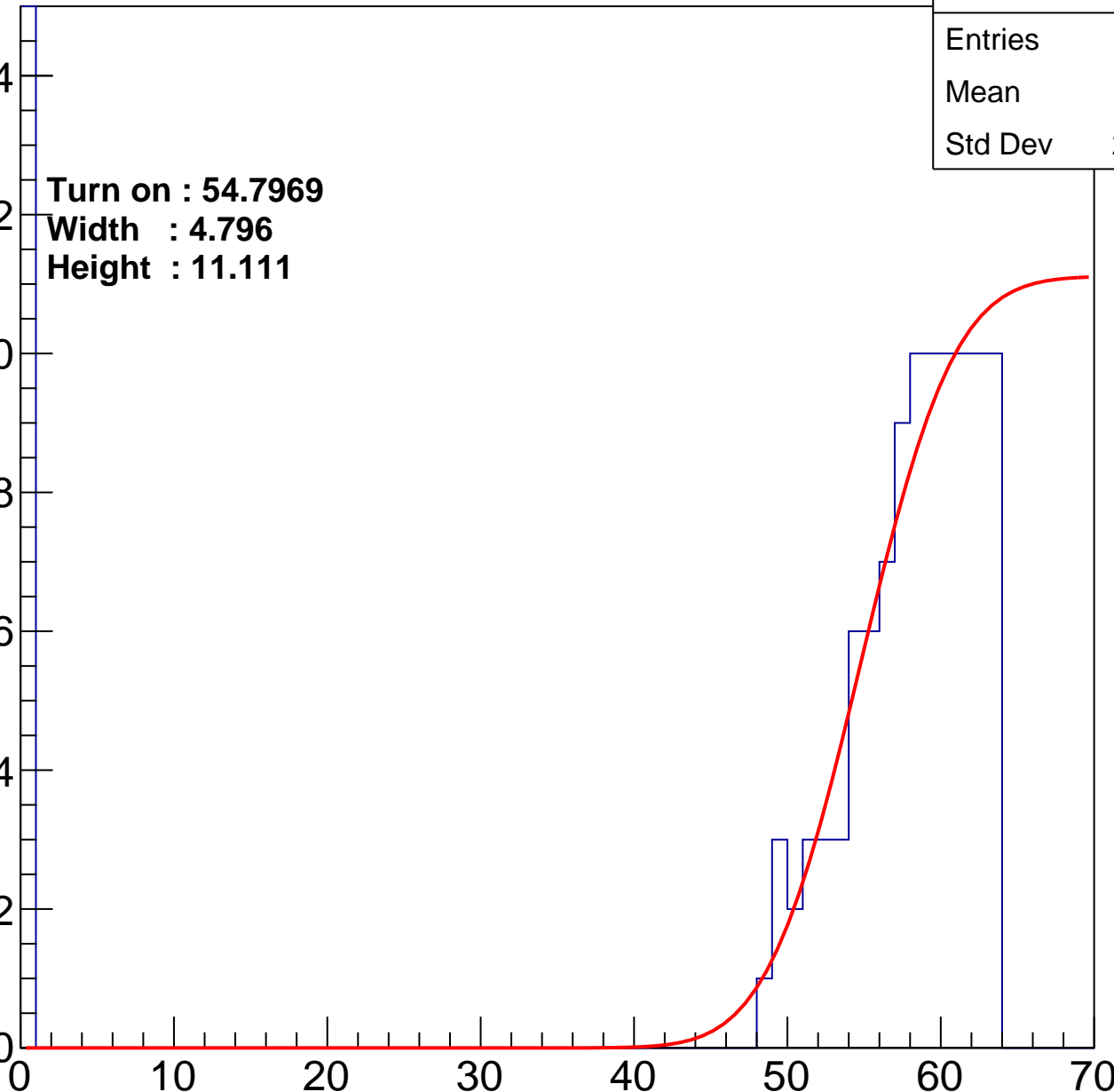
Width : 4.796

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl

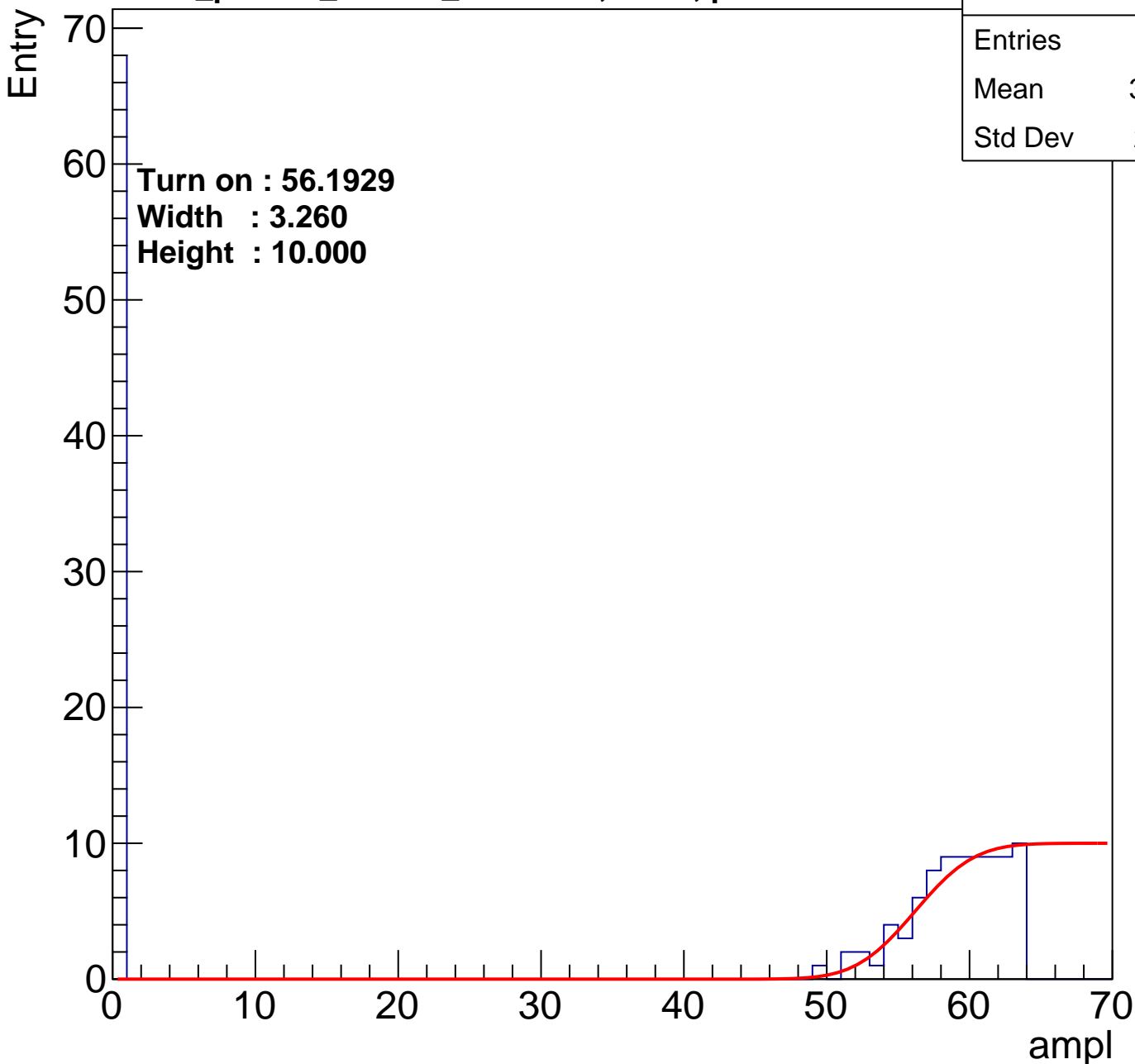


B1L104S, U24-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	32.07
Std Dev	29.31

Turn on : 56.1929
Width : 3.260
Height : 10.000



B1L104S, U24-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	30.16
Std Dev	29.39

Turn on : 54.9501

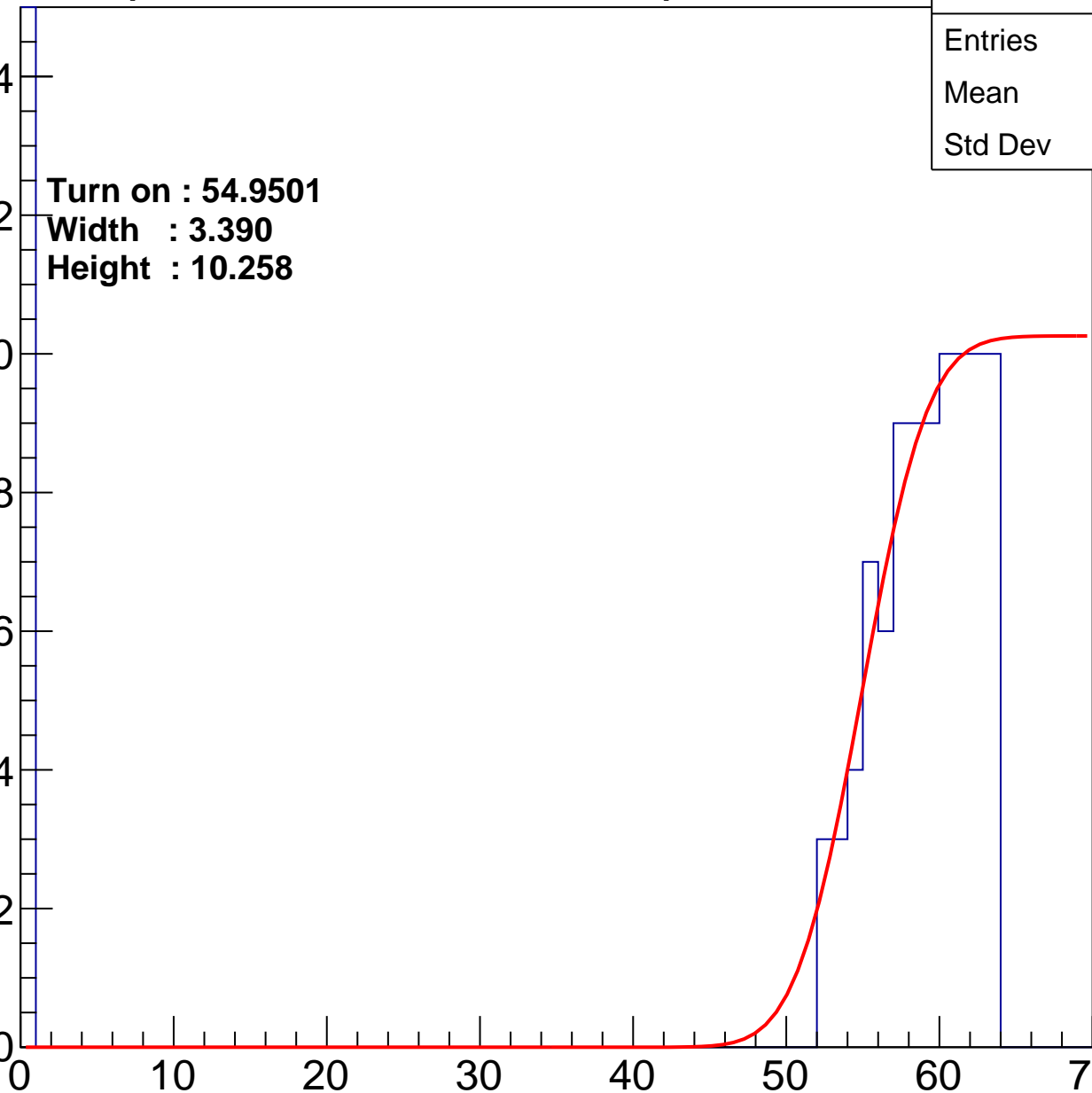
Width : 3.390

Height : 10.258

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	35.88
Std Dev	27.99

Turn on : 54.7673

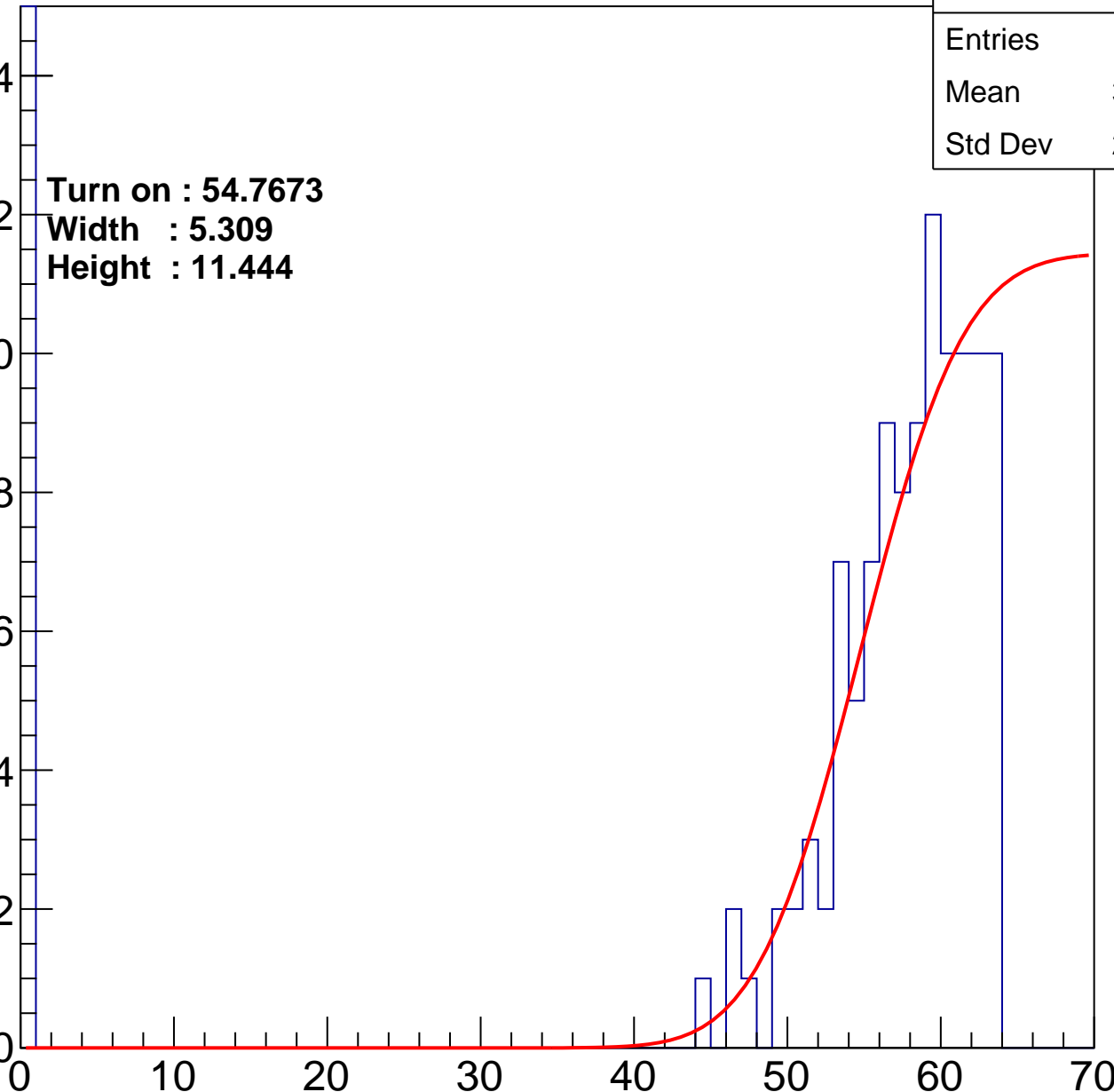
Width : 5.309

Height : 11.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	33.06
Std Dev	28.32

Turn on : 54.9606

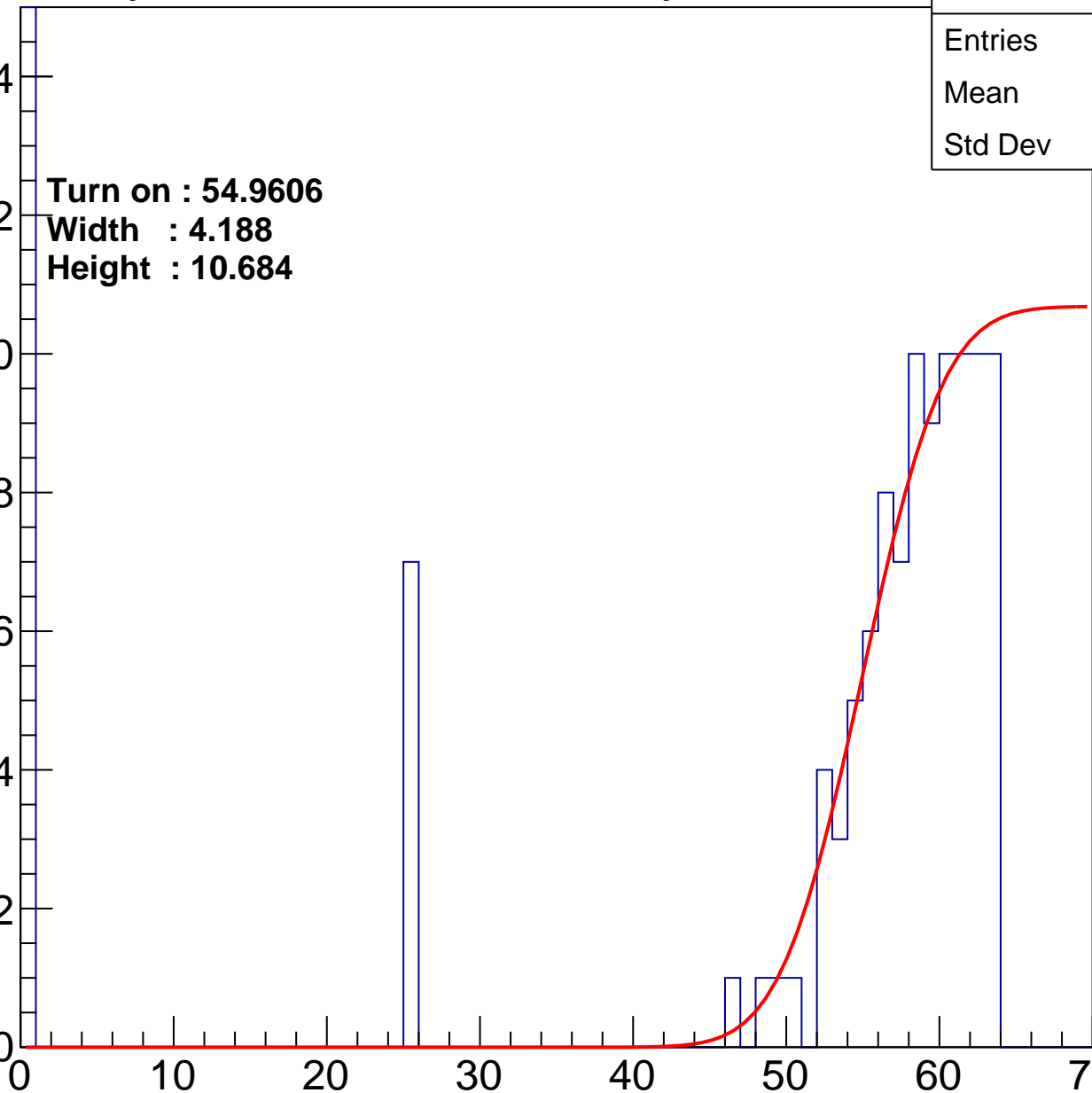
Width : 4.188

Height : 10.684

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	32.63
Std Dev	28.81

Turn on : 55.1178

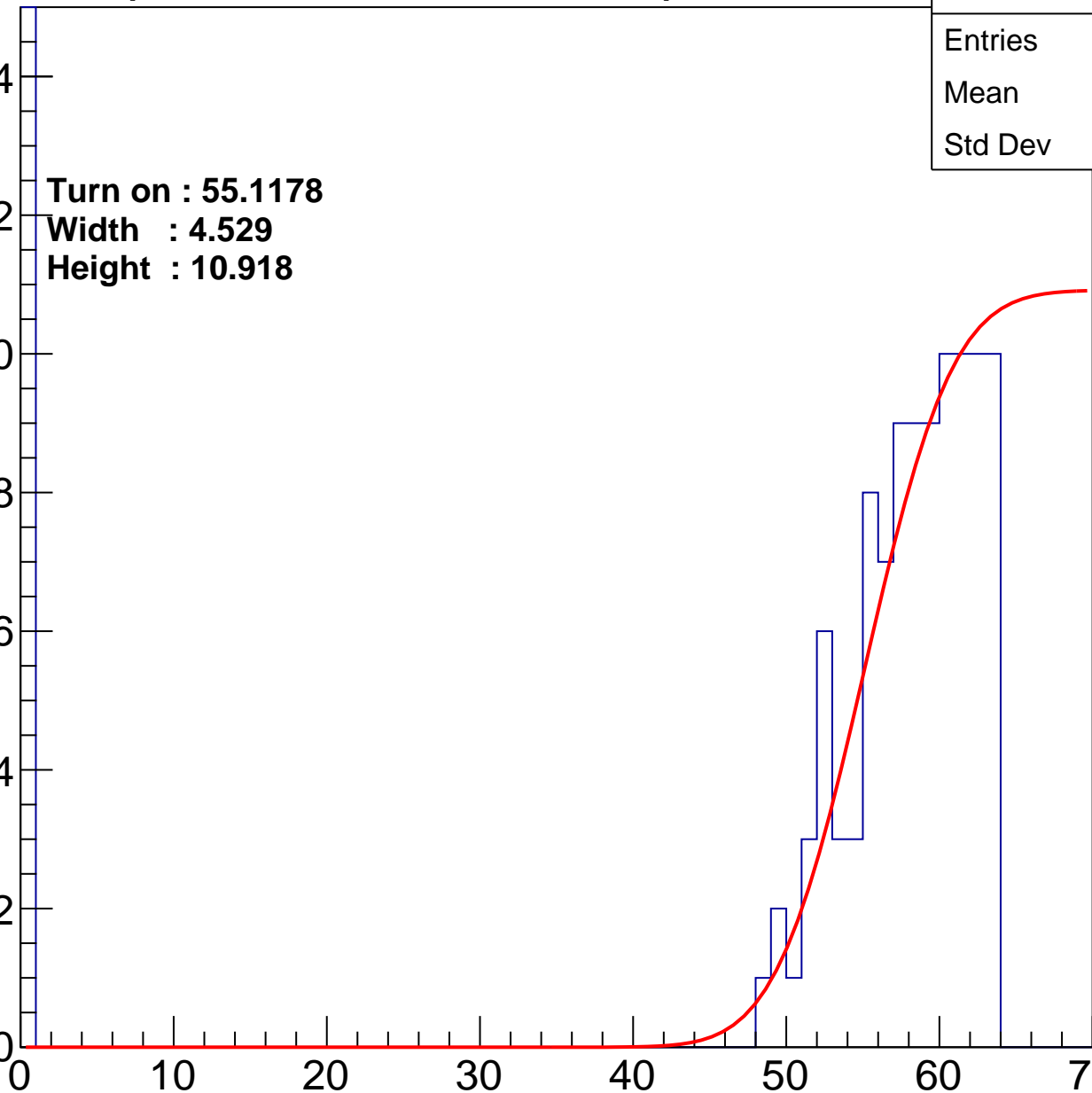
Width : 4.529

Height : 10.918

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	33.88
Std Dev	28.48

Turn on : 53.9467

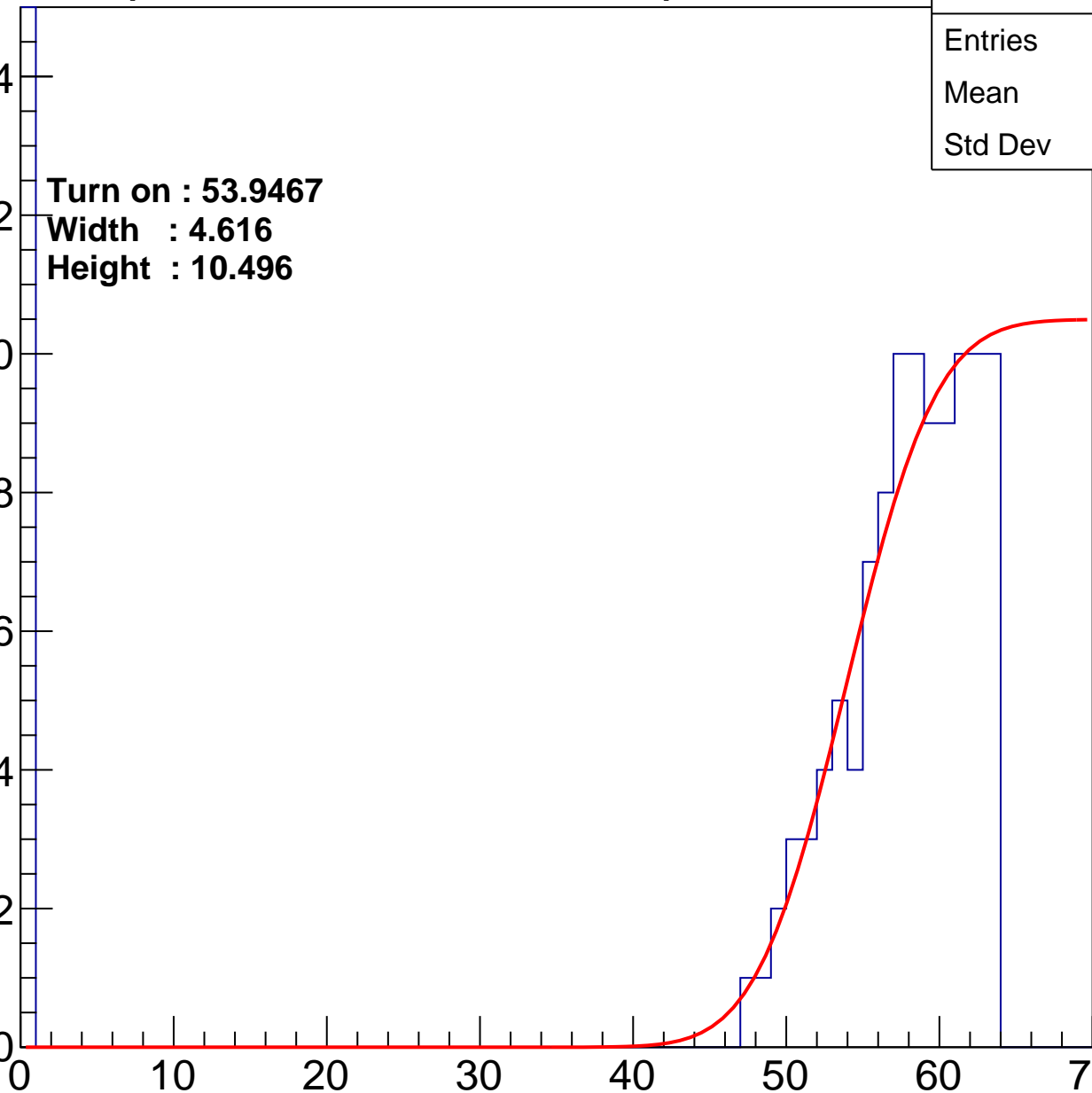
Width : 4.616

Height : 10.496

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	34.25
Std Dev	28.64

Turn on : 53.9098

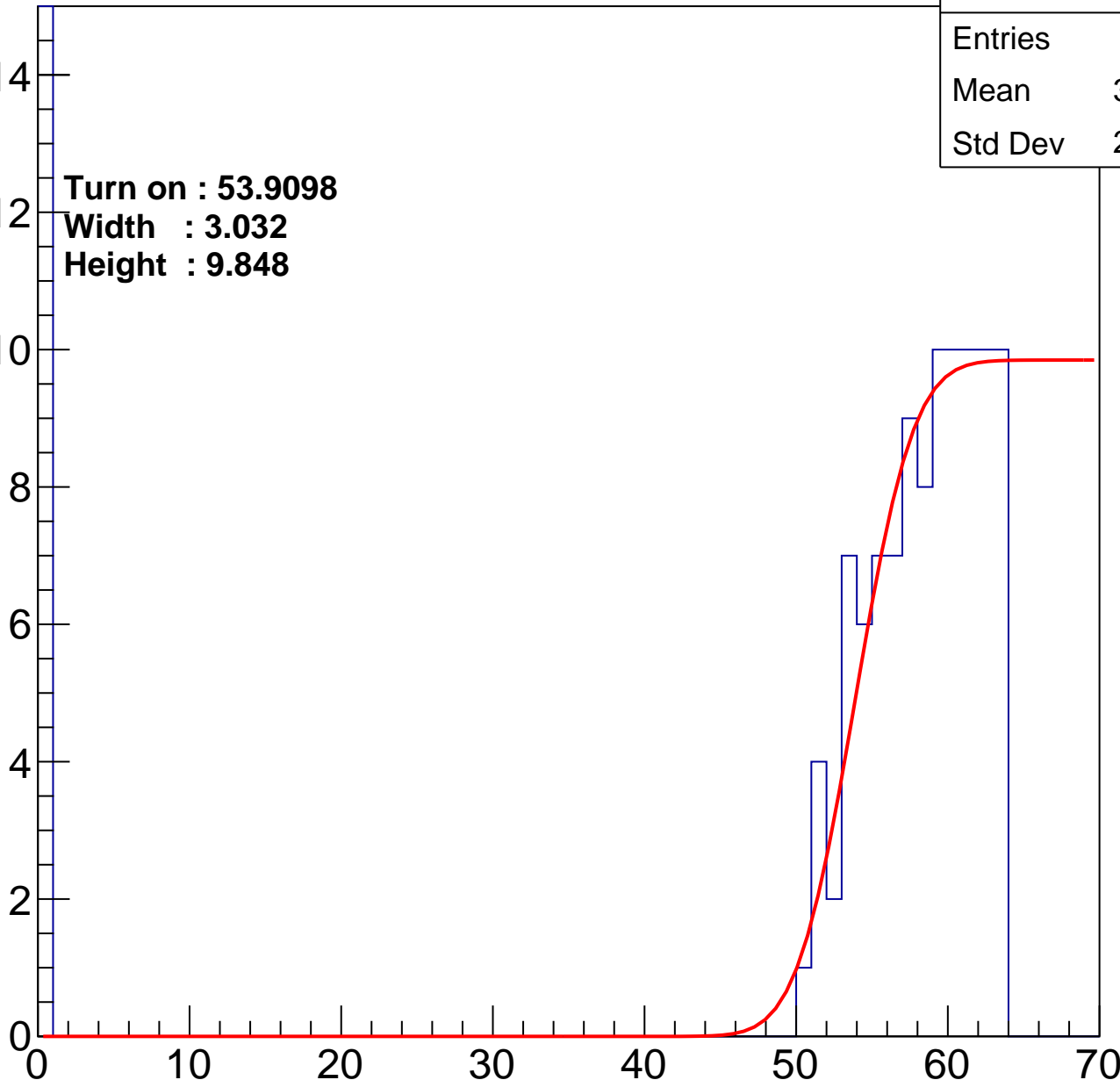
Width : 3.032

Height : 9.848

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	33.48
Std Dev	28.56

Turn on : 54.1381

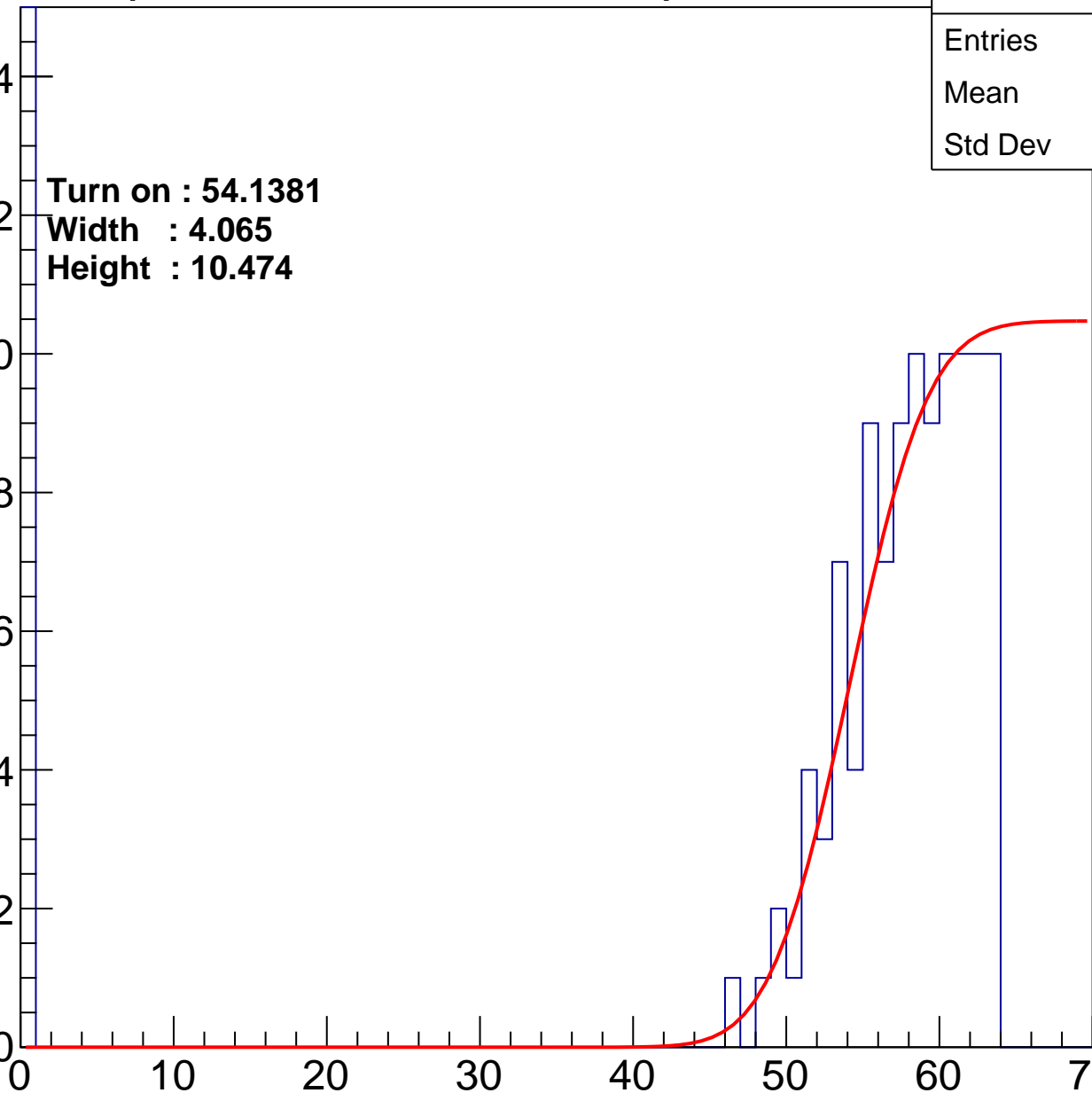
Width : 4.065

Height : 10.474

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	31.54
Std Dev	28.77

Turn on : 53.3043

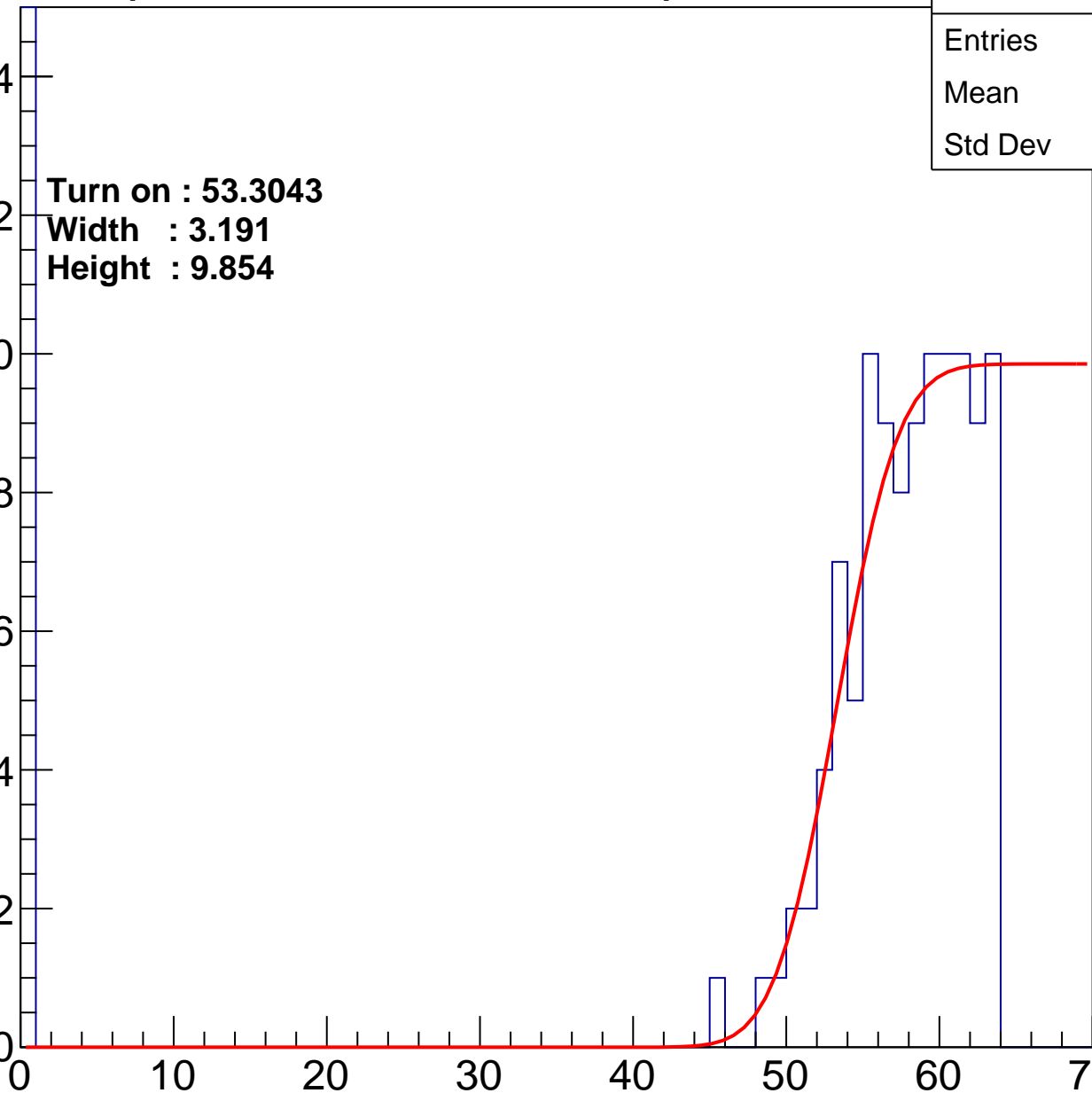
Width : 3.191

Height : 9.854

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	32.51
Std Dev	28.63

Turn on : 54.2943

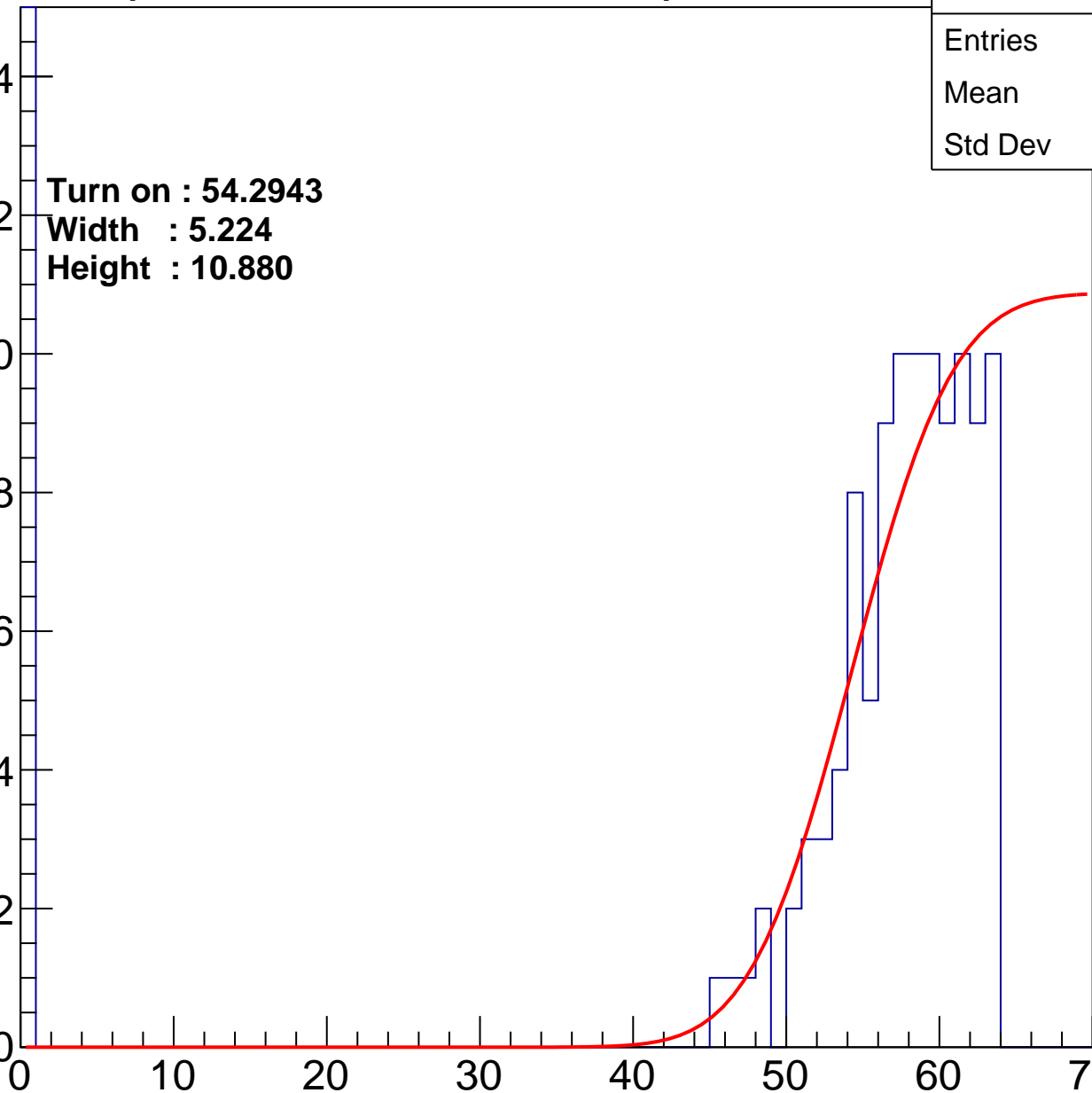
Width : 5.224

Height : 10.880

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U24-ch127

calib_packv5_033123_0516.root, FC#4, port A1

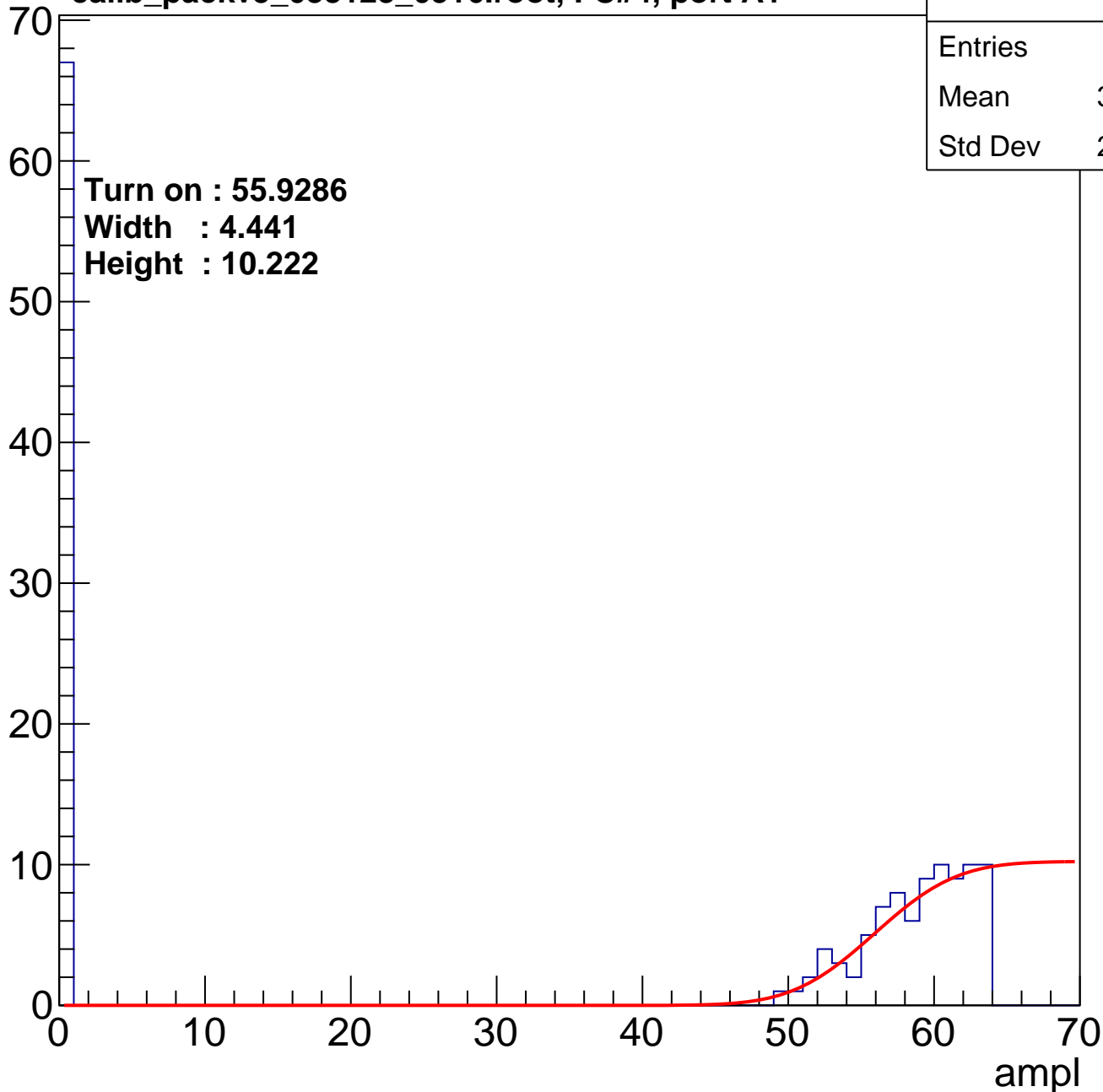
Entries	154
Mean	32.97
Std Dev	29.05

Turn on : 55.9286

Width : 4.441

Height : 10.222

Entry



B1L104S, U24-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	32.97
Std Dev	29.05

Turn on : 55.9286

Width : 4.441

Height : 10.222

Entry

