



# B0L103S, U20-ch0

calib\_packv5\_040323\_1717.root, FC#2, port C3

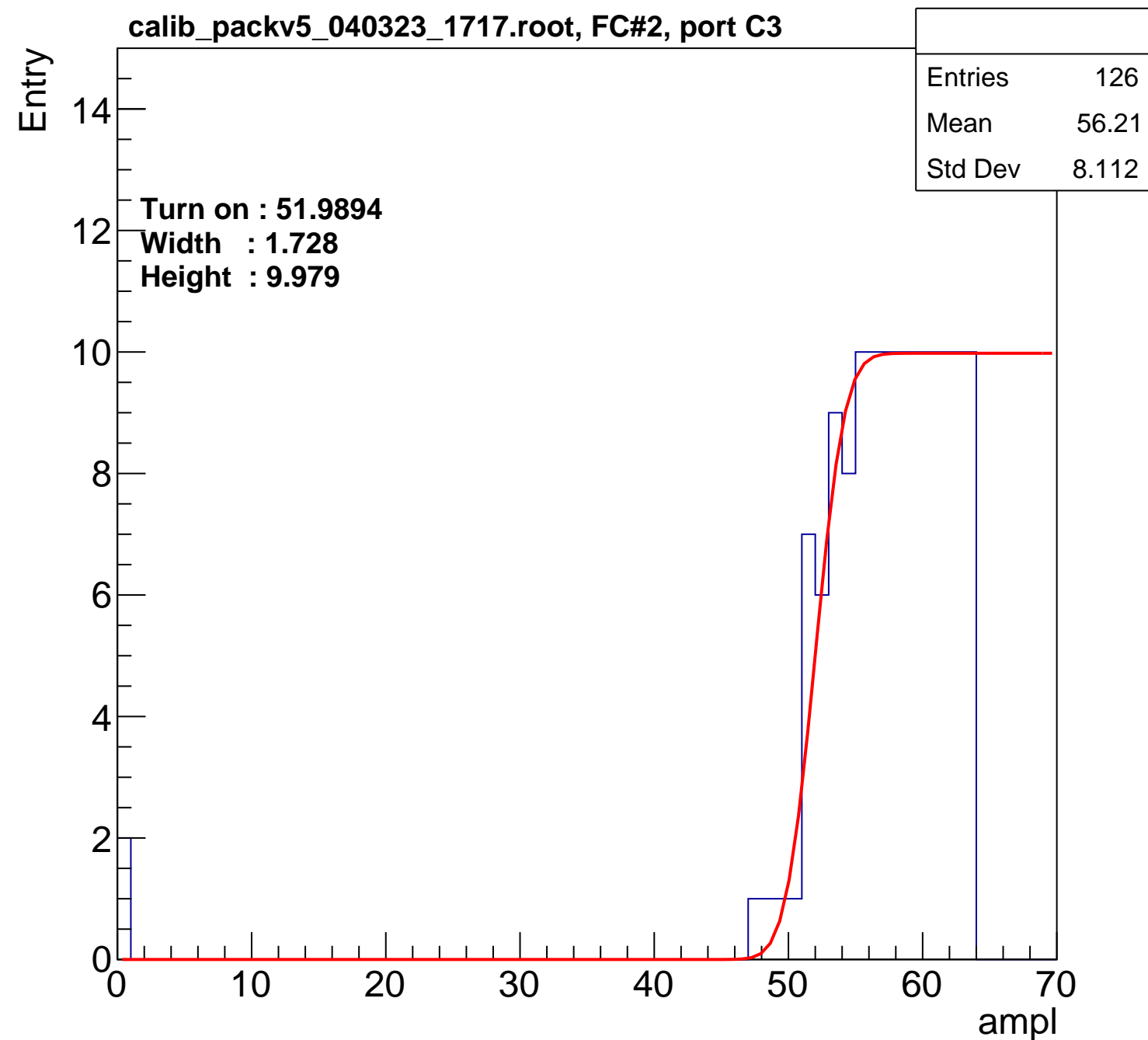
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 51.9894  
Width : 1.728  
Height : 9.979

Entries	126
Mean	56.21
Std Dev	8.112

ampl



# B0L103S, U20-ch1

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	147
Mean	54.31
Std Dev	11.07

Turn on : 50.4218

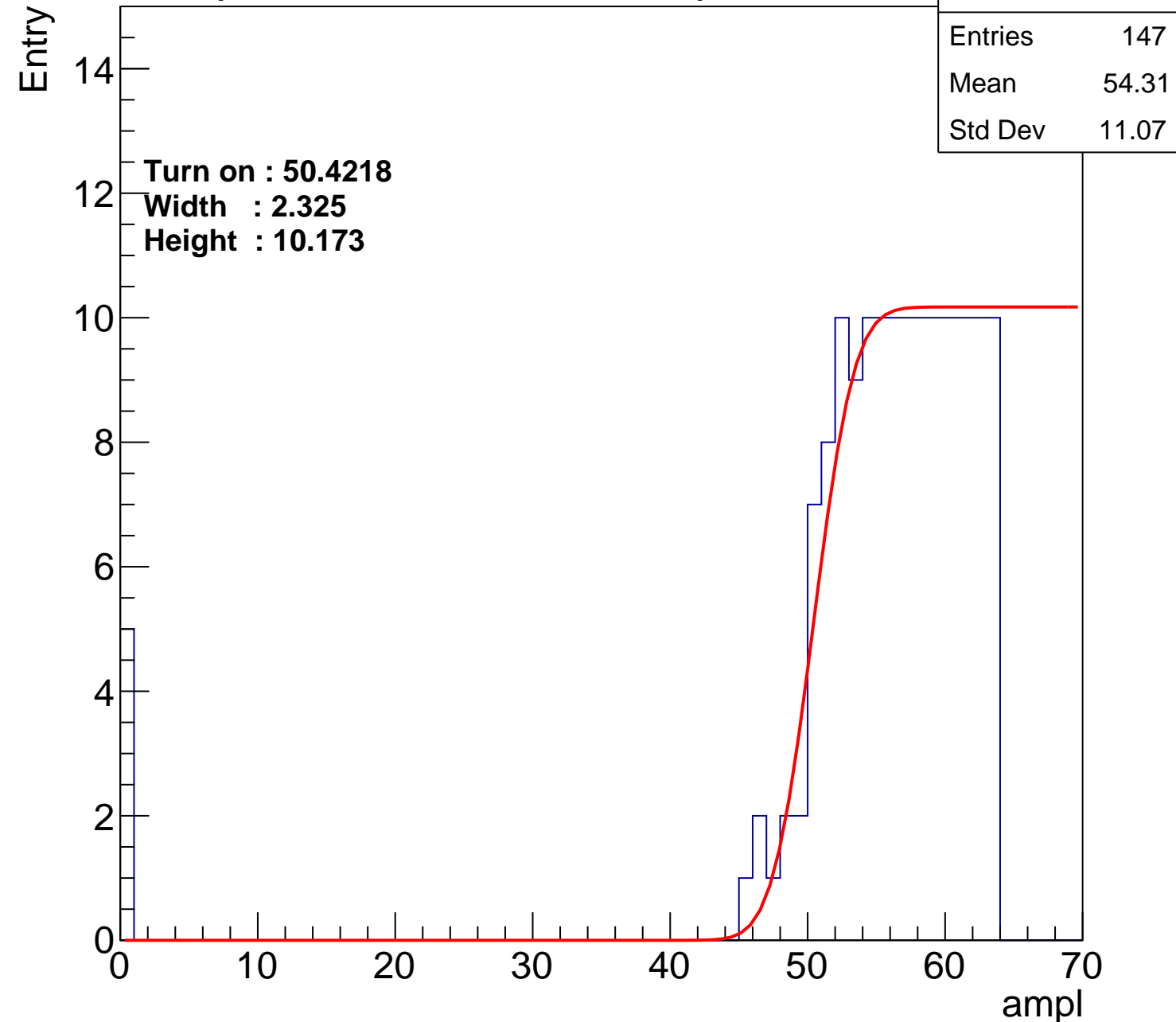
Width : 2.325

Height : 10.173

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch2

calib\_packv5\_040323\_1717.root, FC#2, port C3

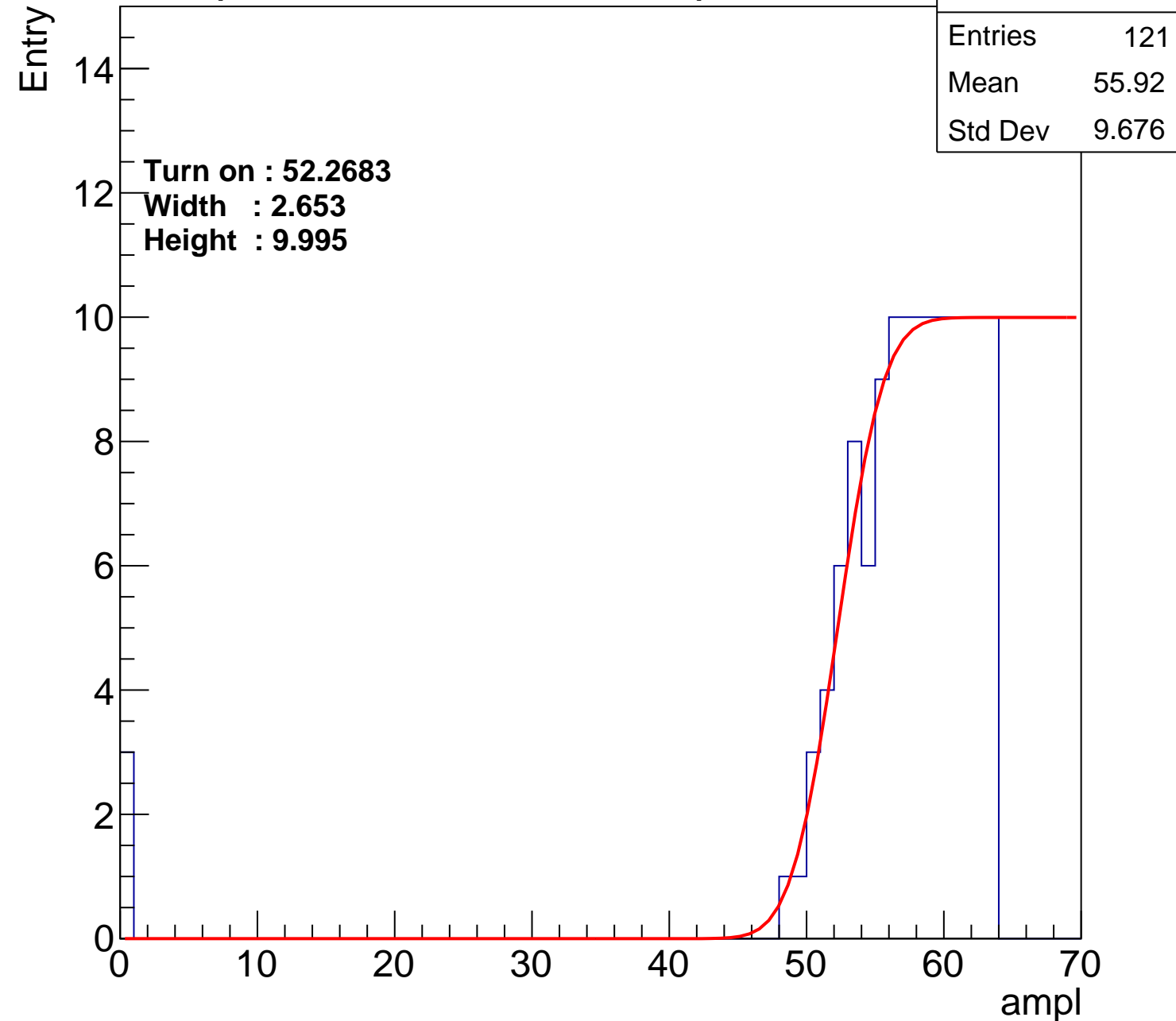
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 52.2683**  
**Width : 2.653**  
**Height : 9.995**

Entries	121
Mean	55.92
Std Dev	9.676

ampl



# B0L103S, U20-ch3

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

Turn on : 50.8538

Width : 1.808

Height : 10.067

Entries

135

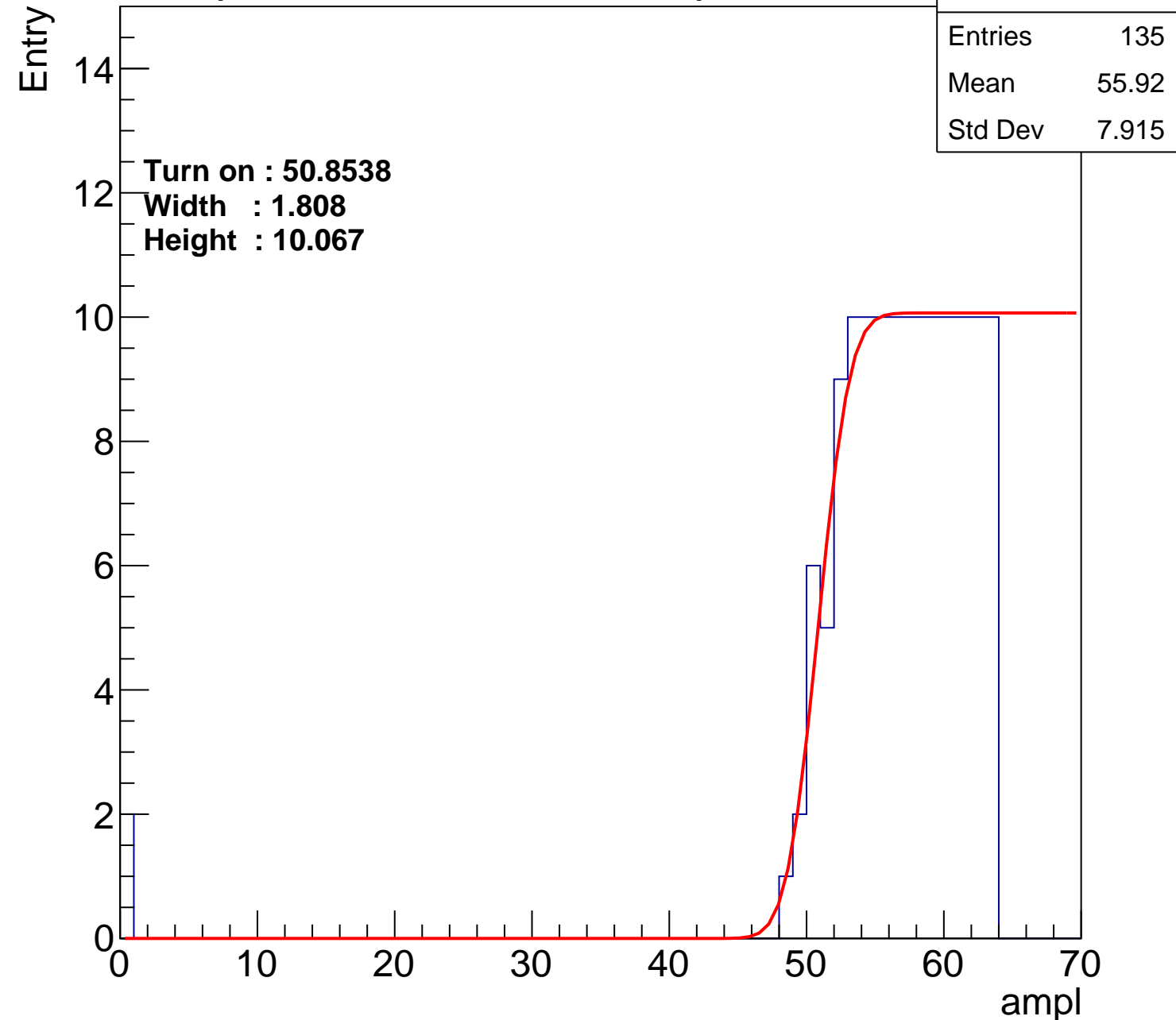
Mean

55.92

Std Dev

7.915

ampl



# B0L103S, U20-ch4

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	148
Mean	52.78
Std Dev	14.14

Turn on : 50.5903

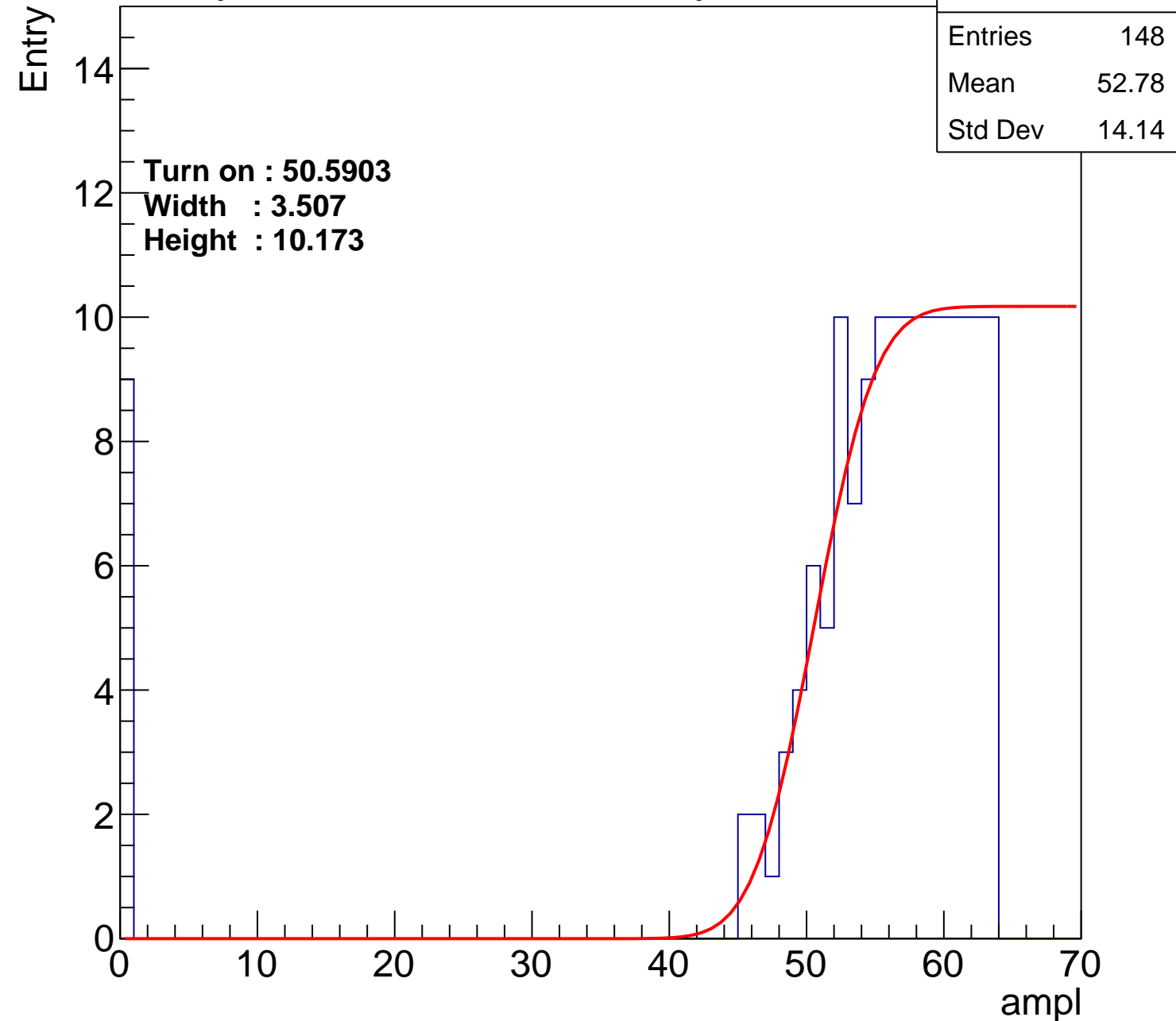
Width : 3.507

Height : 10.173

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch5

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.18
Std Dev	8.225

Turn on : 51.5177

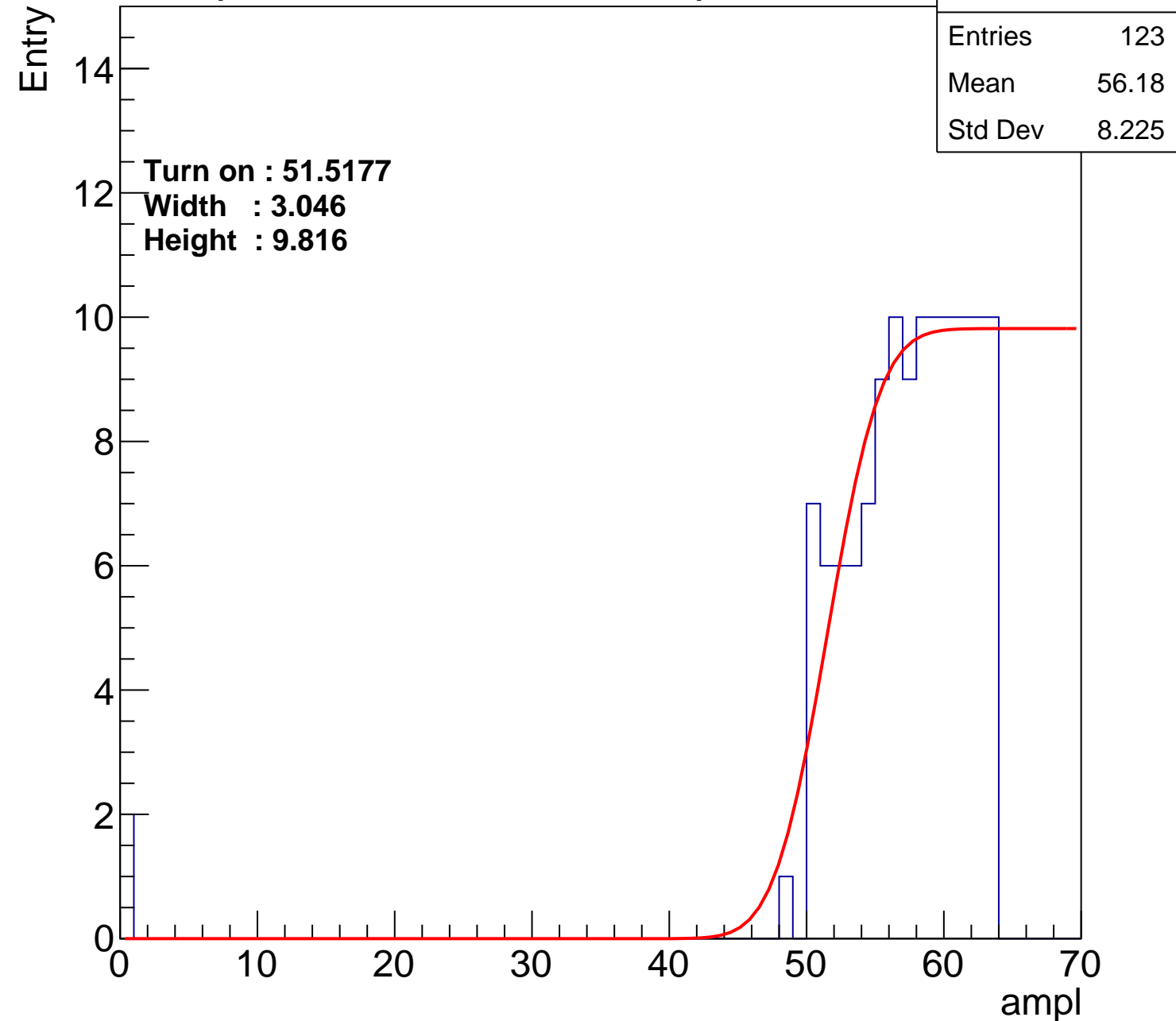
Width : 3.046

Height : 9.816

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch6

calib\_packv5\_040323\_1717.root, FC#2, port C3

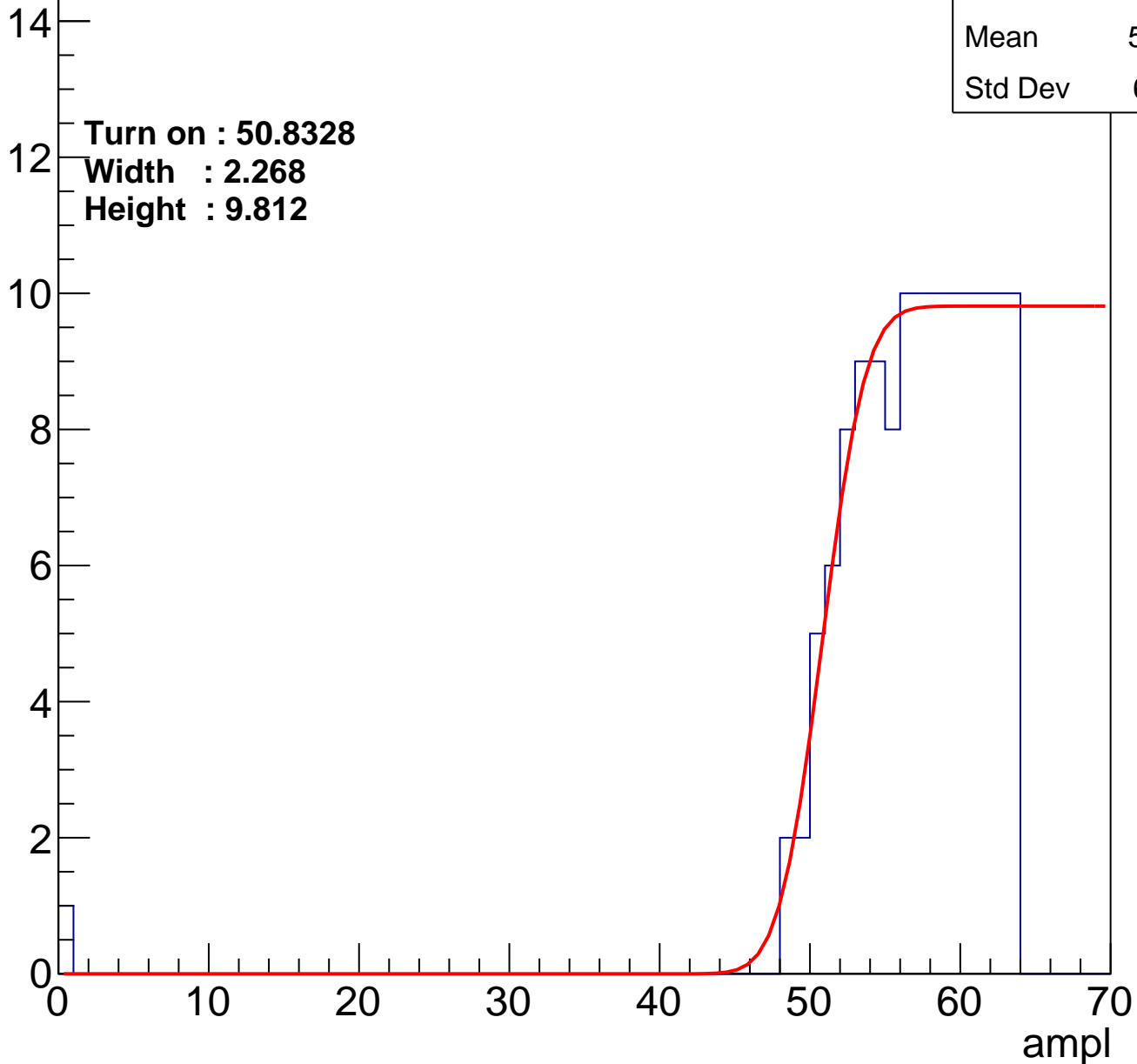
Entry

Entries	130
Mean	56.38
Std Dev	6.401

Turn on : 50.8328

Width : 2.268

Height : 9.812





# B0L103S, U20-ch7

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.83
Std Dev	8.118

Turn on : 51.2400

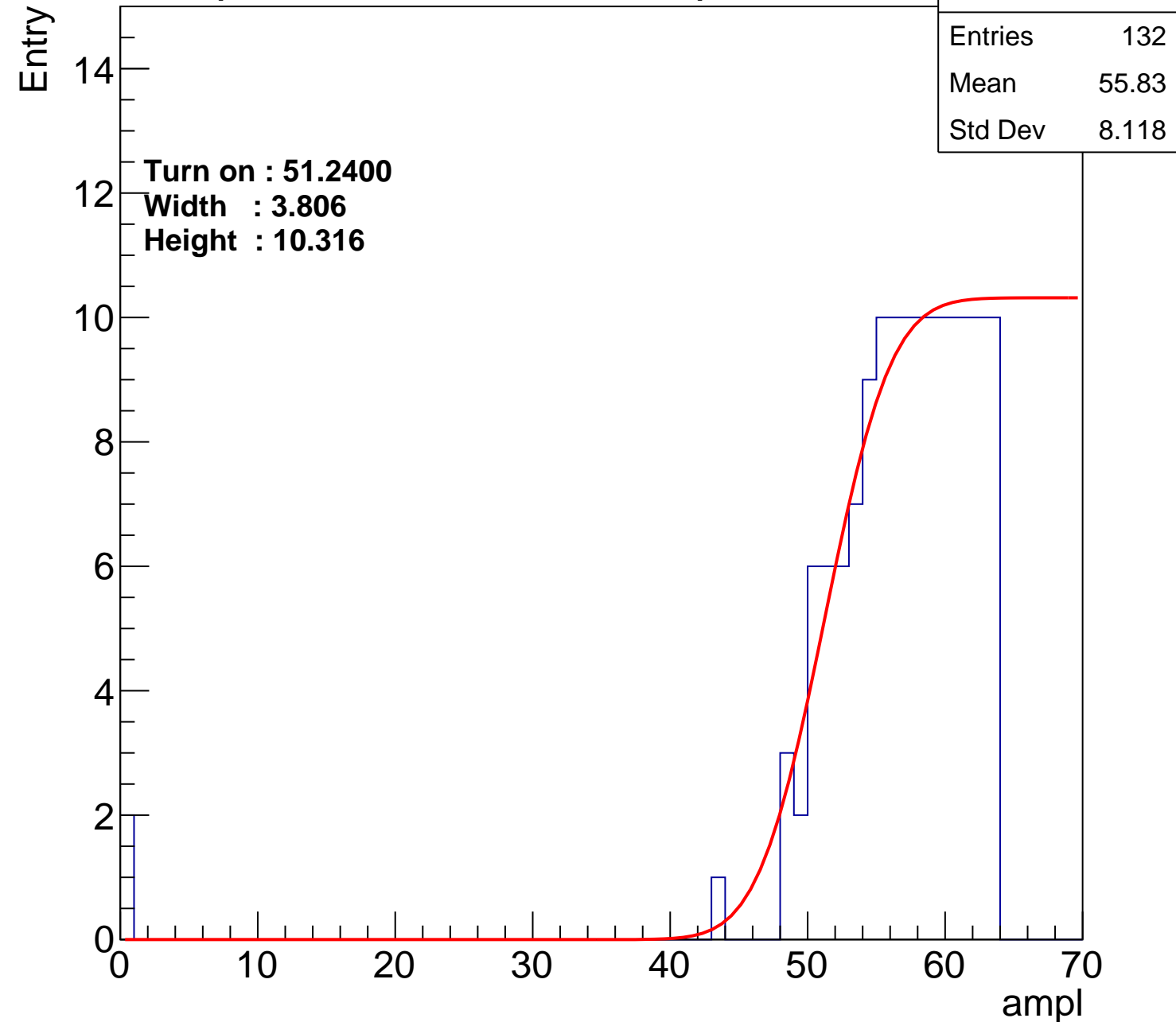
Width : 3.806

Height : 10.316

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch8

calib\_packv5\_040323\_1717.root, FC#2, port C3

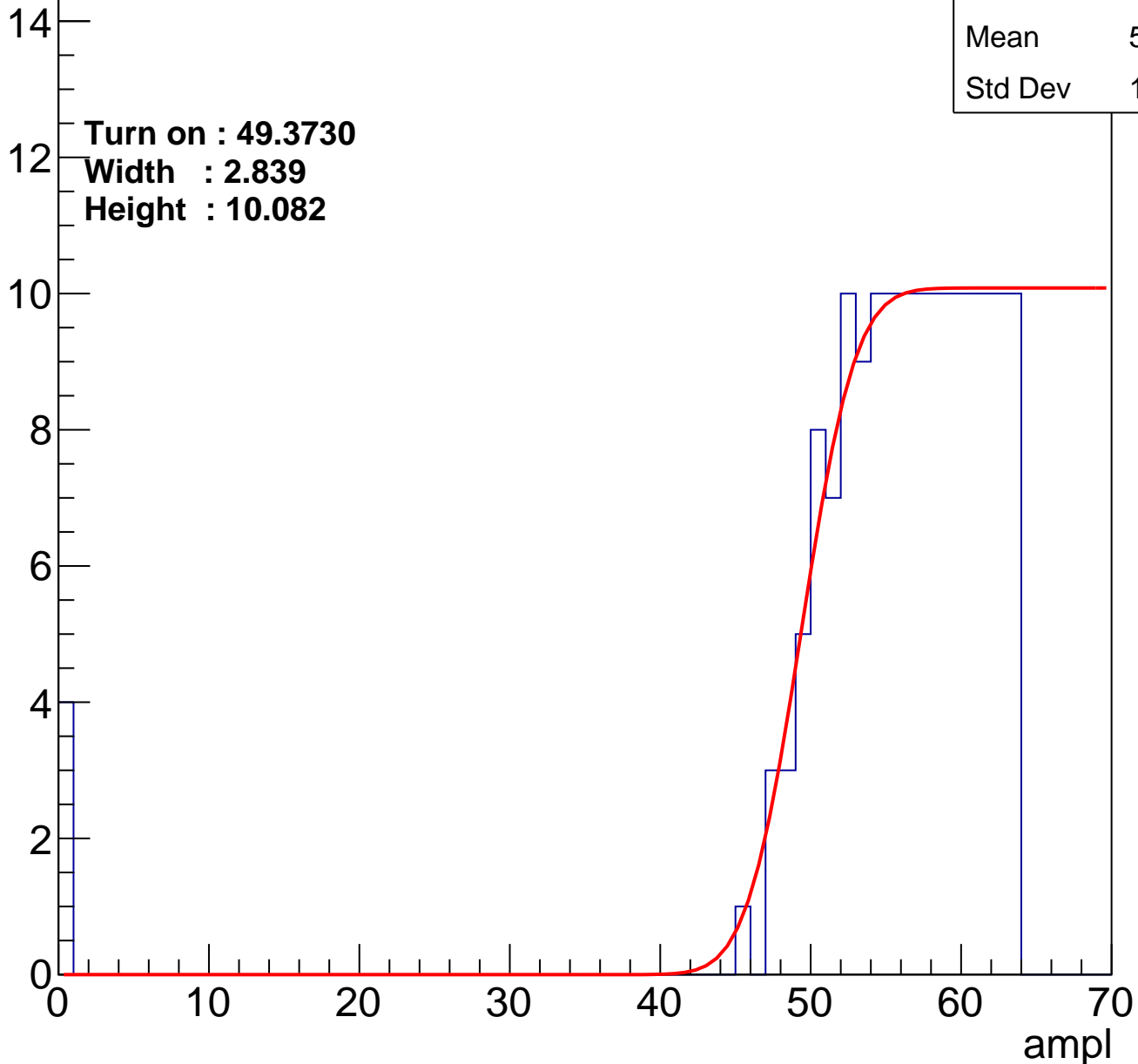
Entry

Entries	150
Mean	54.53
Std Dev	10.06

Turn on : 49.3730

Width : 2.839

Height : 10.082



# B0L103S, U20-ch9

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	57.14
Std Dev	6.599

Turn on : 53.9298

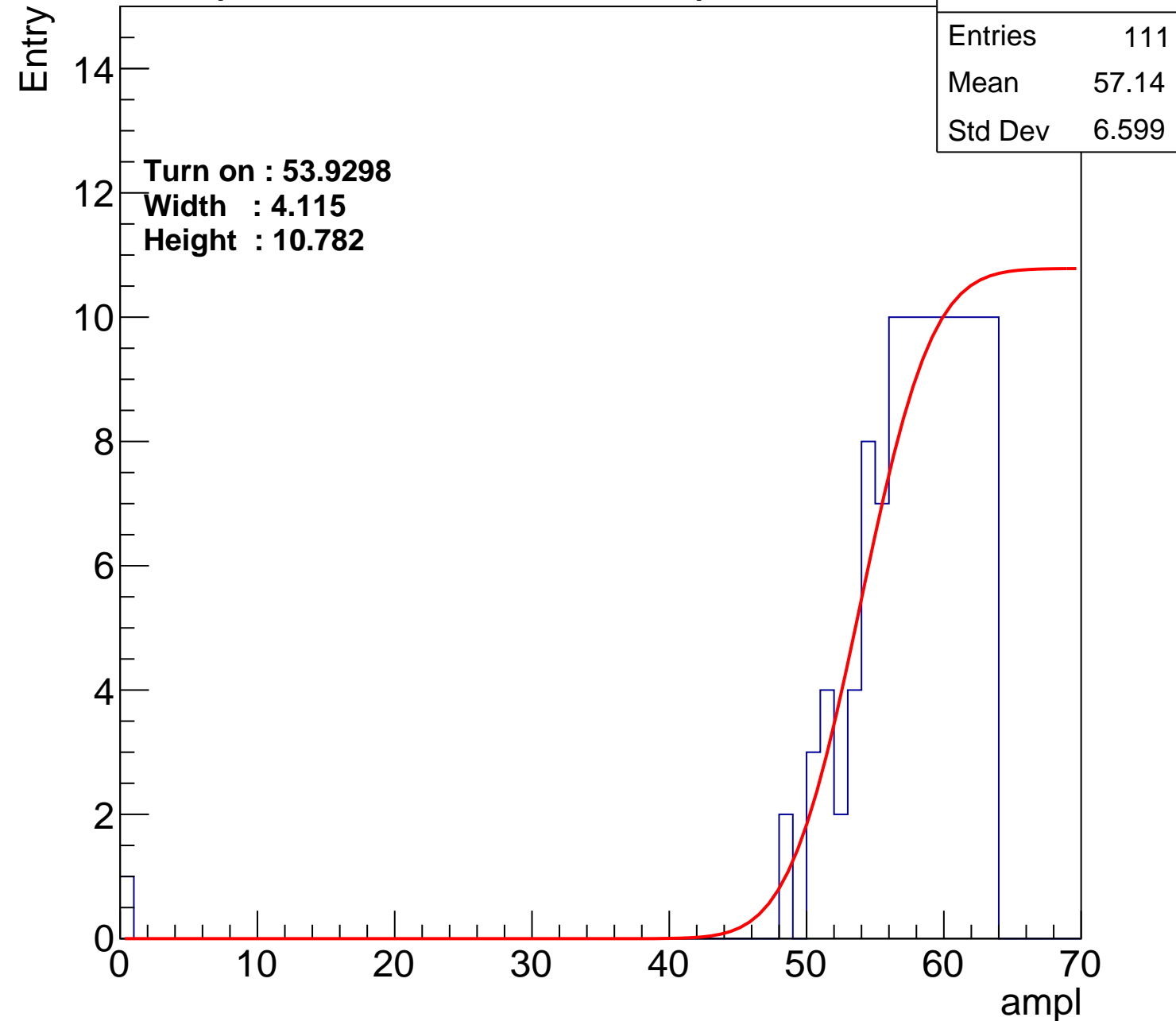
Width : 4.115

Height : 10.782

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch10

calib\_packv5\_040323\_1717.root, FC#2, port C3

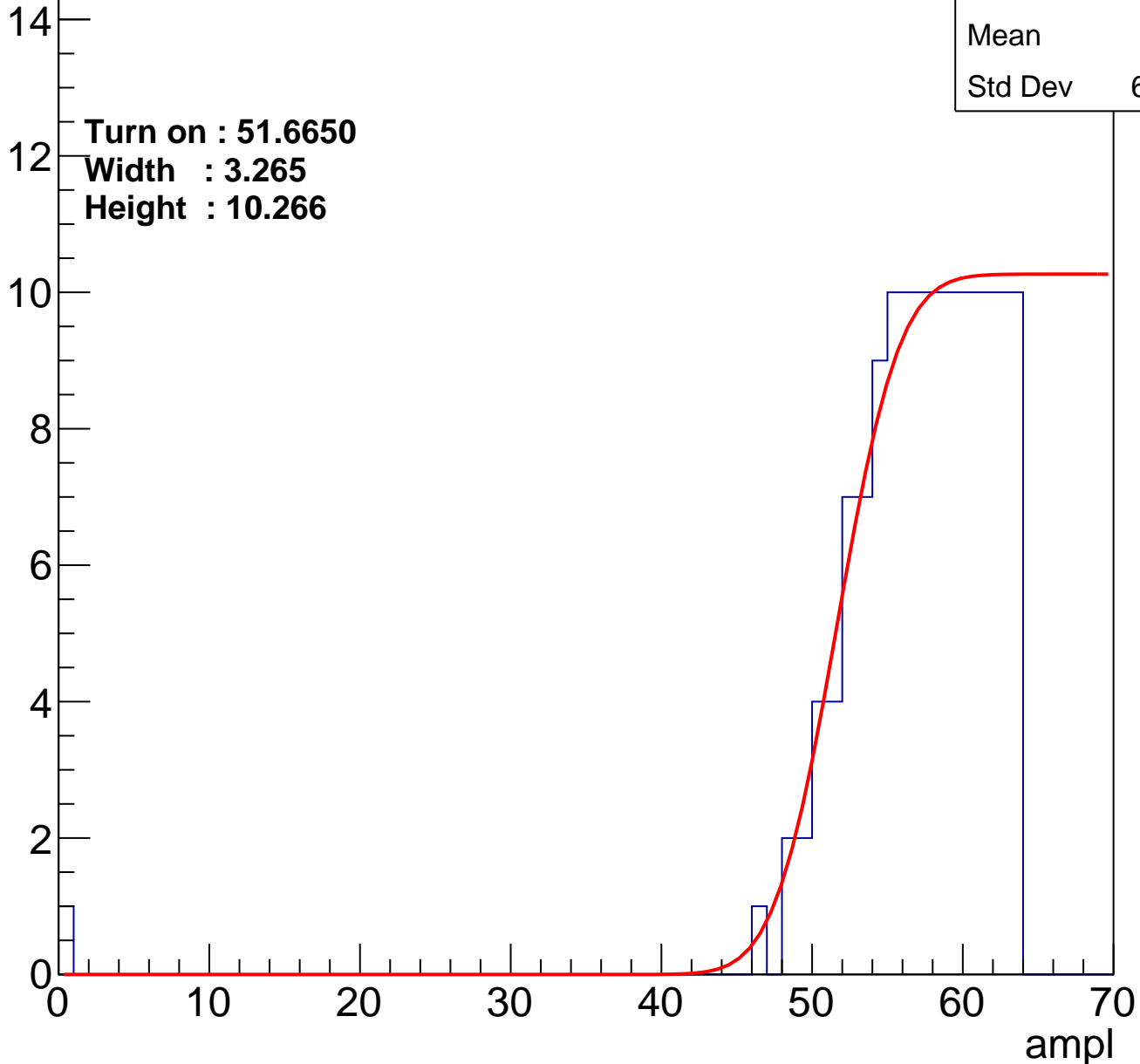
Entries	127
Mean	56.5
Std Dev	6.457

Turn on : 51.6650

Width : 3.265

Height : 10.266

Entry



# B0L103S, U20-ch11

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.76
Std Dev	9.654

Turn on : 52.6113

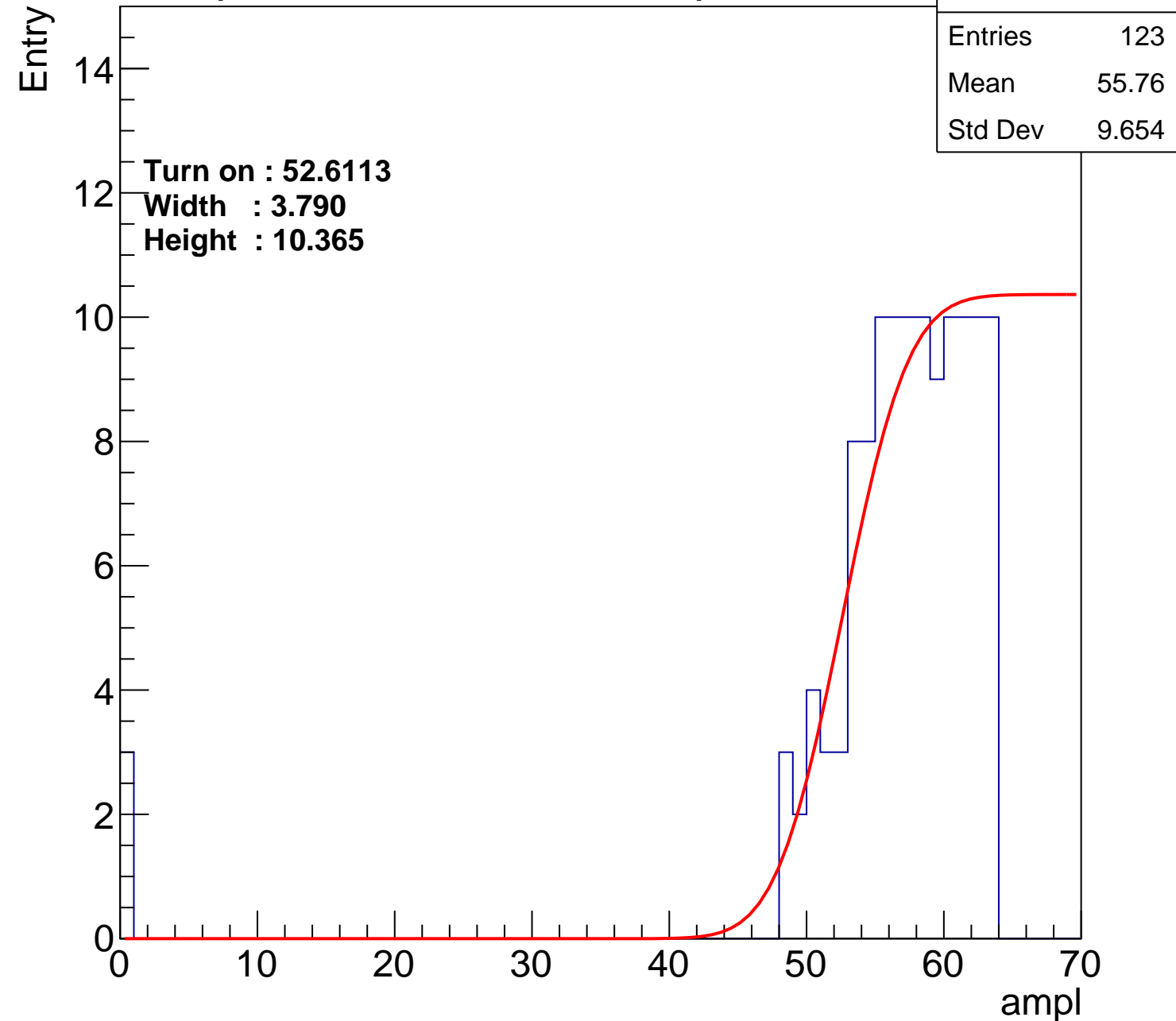
Width : 3.790

Height : 10.365

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch12

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.98
Std Dev	6.38

Turn on : 50.2163

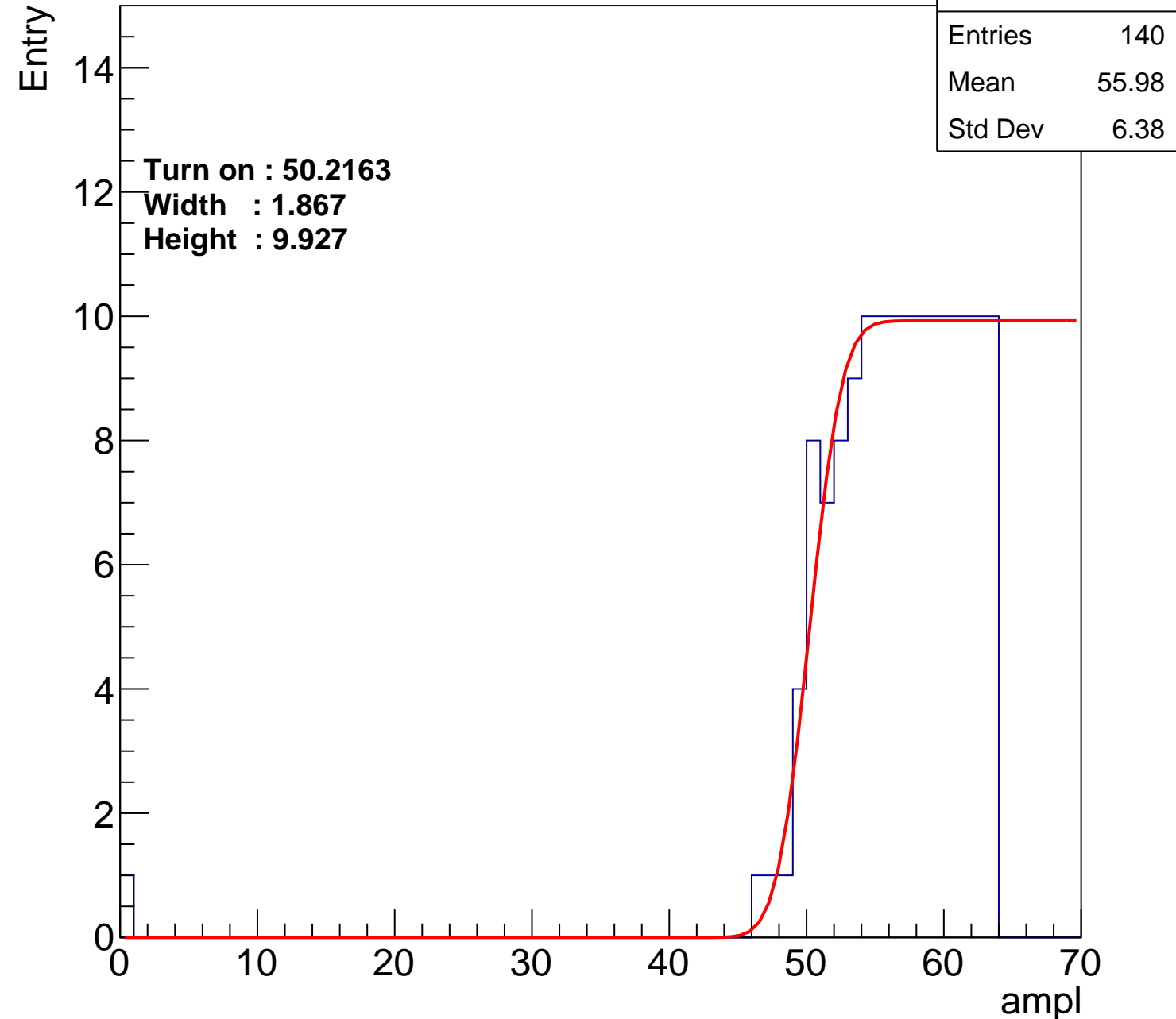
Width : 1.867

Height : 9.927

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch13

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	107
Mean	54.9
Std Dev	13.78

Turn on : 54.1468

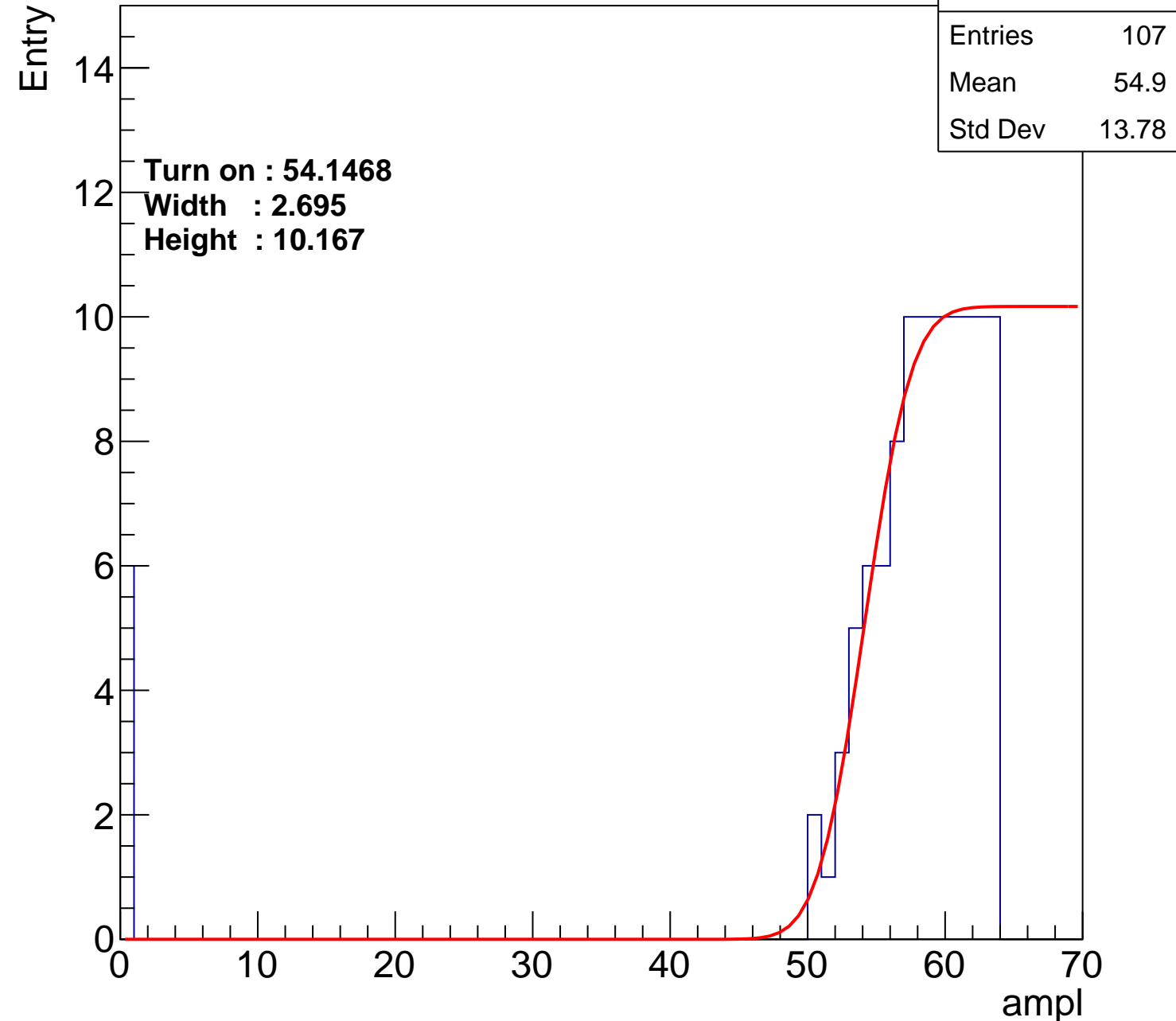
Width : 2.695

Height : 10.167

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch14

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.24
Std Dev	9.227

Turn on : 50.7435

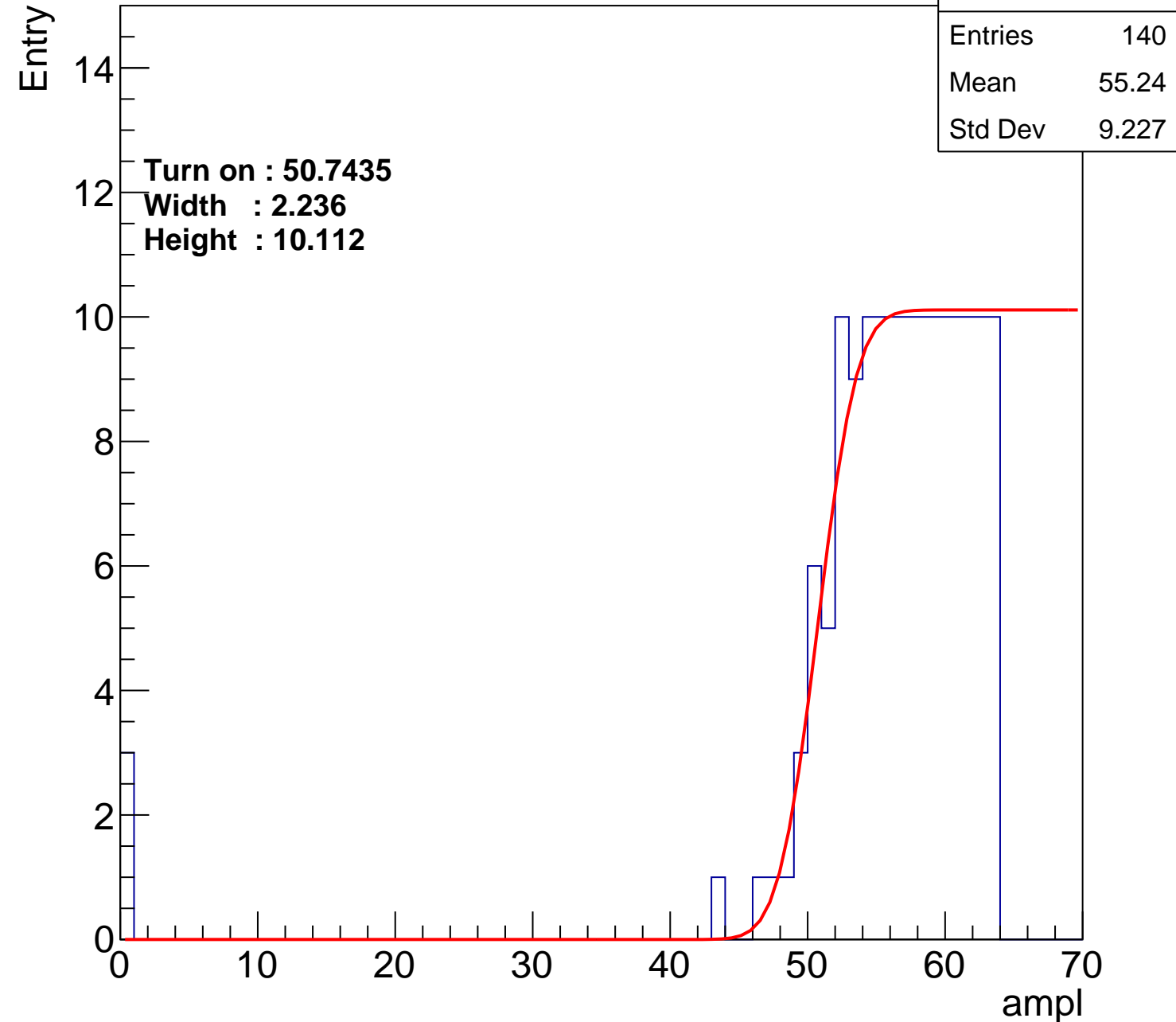
Width : 2.236

Height : 10.112

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch15

calib\_packv5\_040323\_1717.root, FC#2, port C3

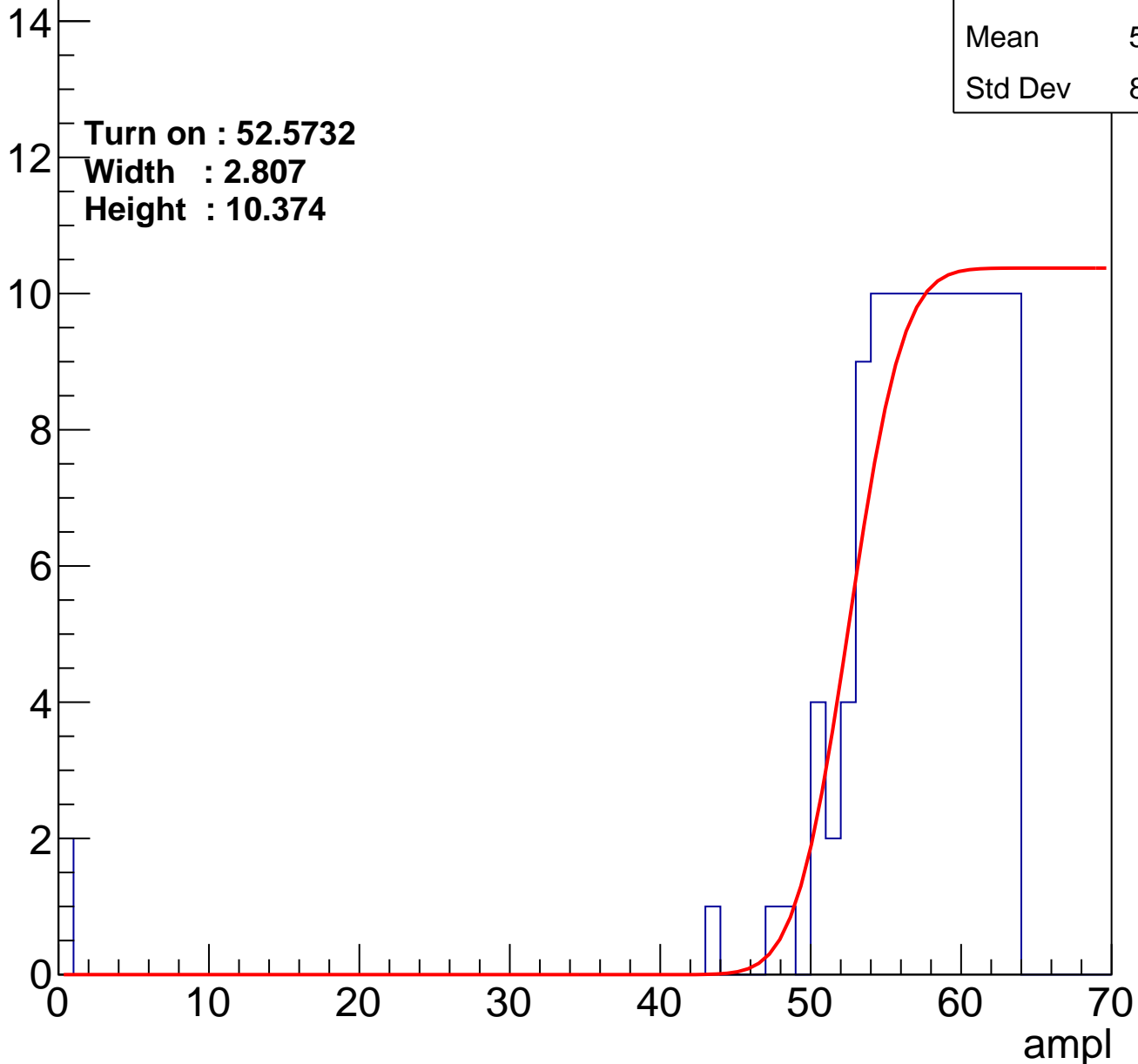
Entries	124
Mean	56.25
Std Dev	8.214

Turn on : 52.5732

Width : 2.807

Height : 10.374

Entry



# B0L103S, U20-ch16

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.4
Std Dev	9.289

Turn on : 50.8775

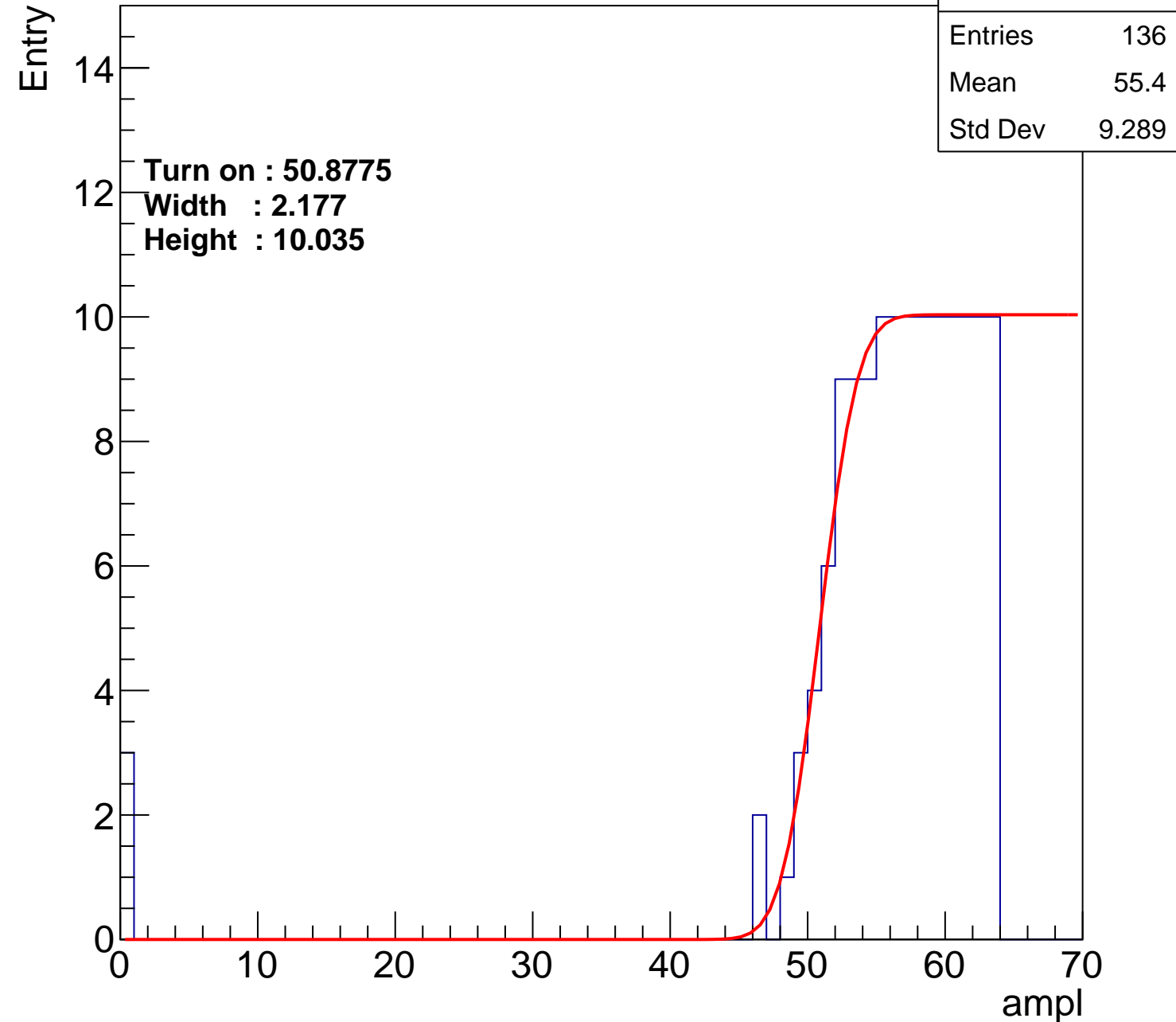
Width : 2.177

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch17

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	56.71
Std Dev	6.534

Turn on : 52.3668

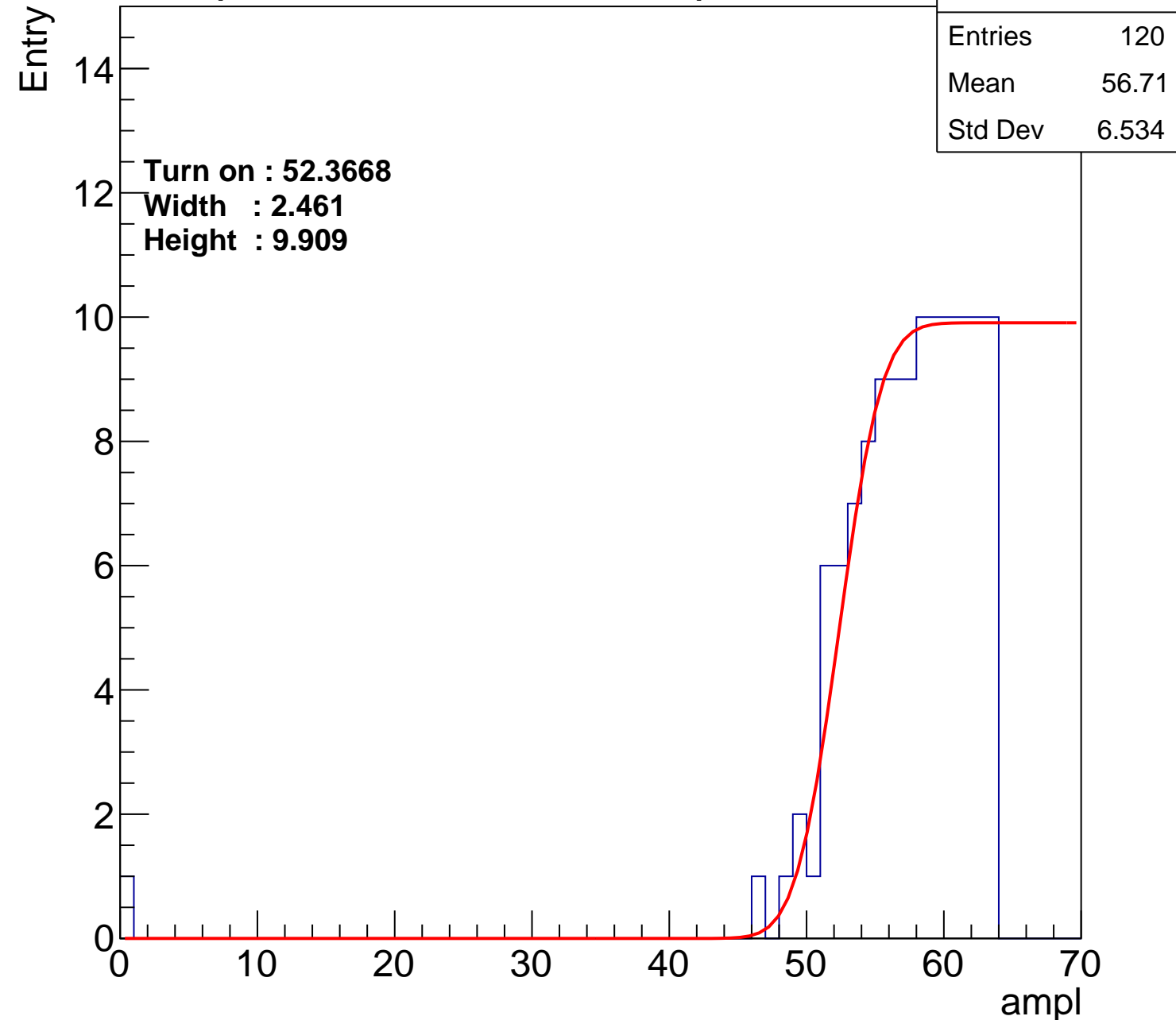
Width : 2.461

Height : 9.909

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch18

calib\_packv5\_040323\_1717.root, FC#2, port C3

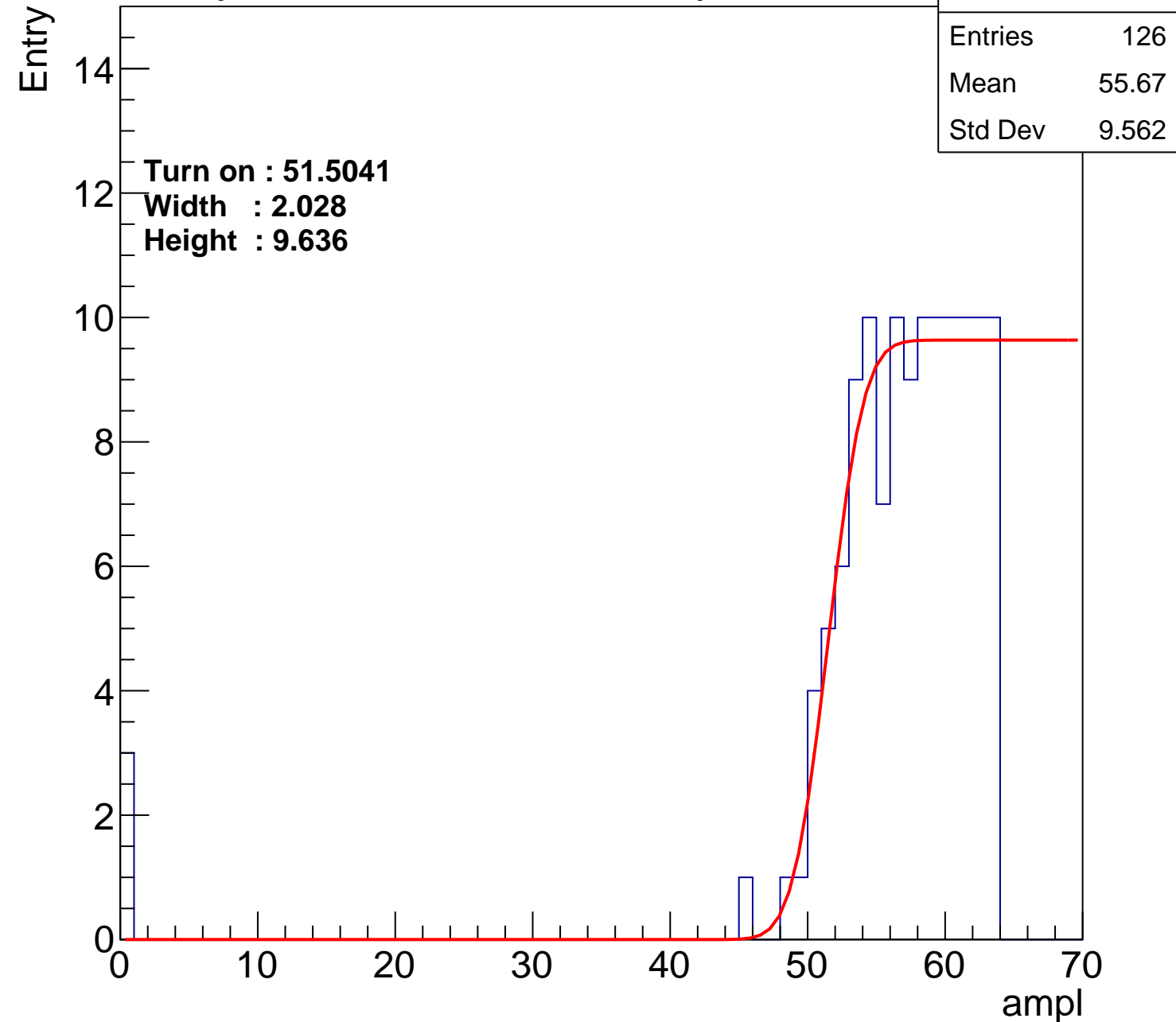
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 51.5041  
Width : 2.028  
Height : 9.636

Entries	126
Mean	55.67
Std Dev	9.562

ampl



# B0L103S, U20-ch19

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.77
Std Dev	9.608

Turn on : 52.1735

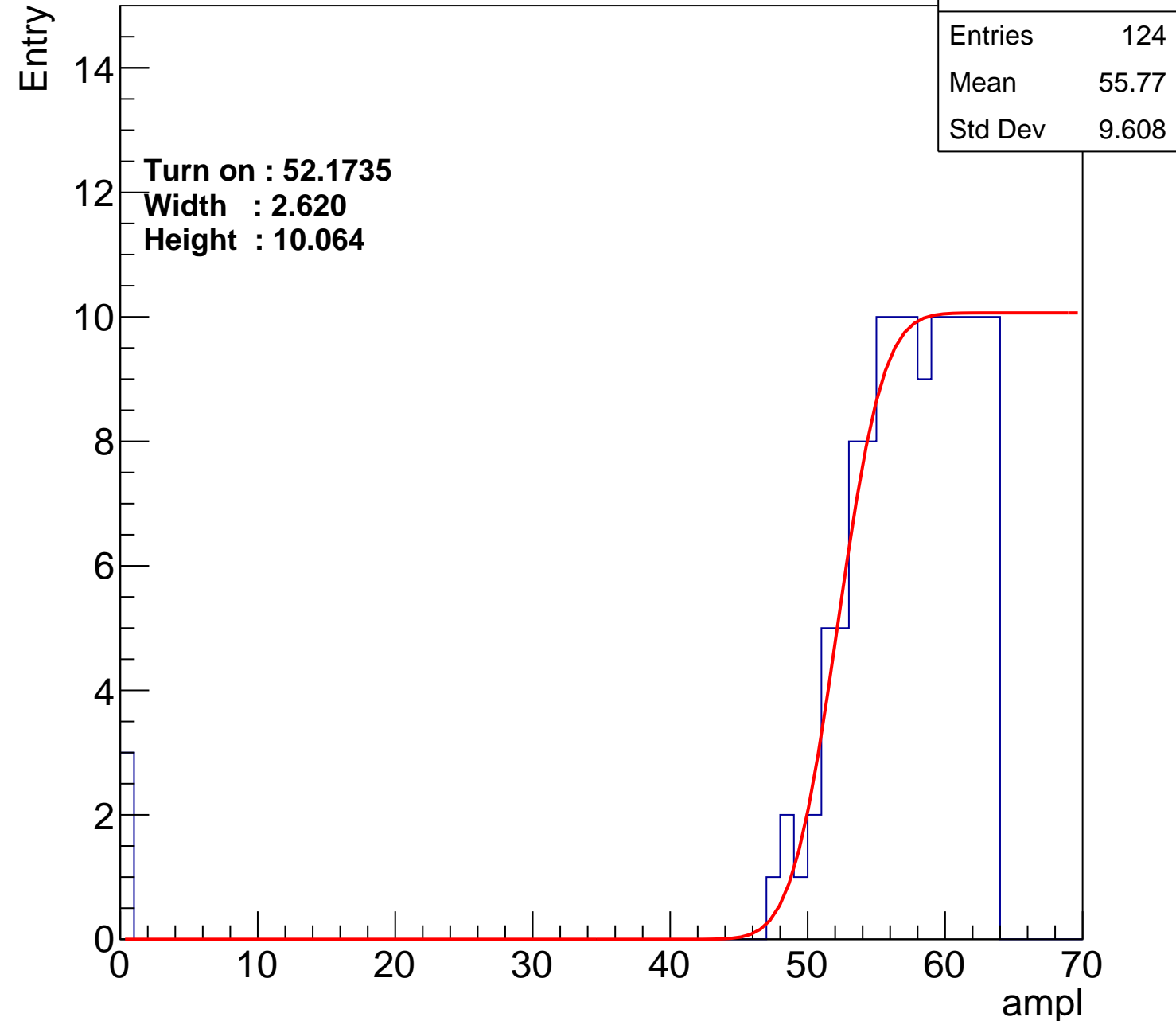
Width : 2.620

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch20

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	55.67
Std Dev	7.959

Turn on : 50.4652

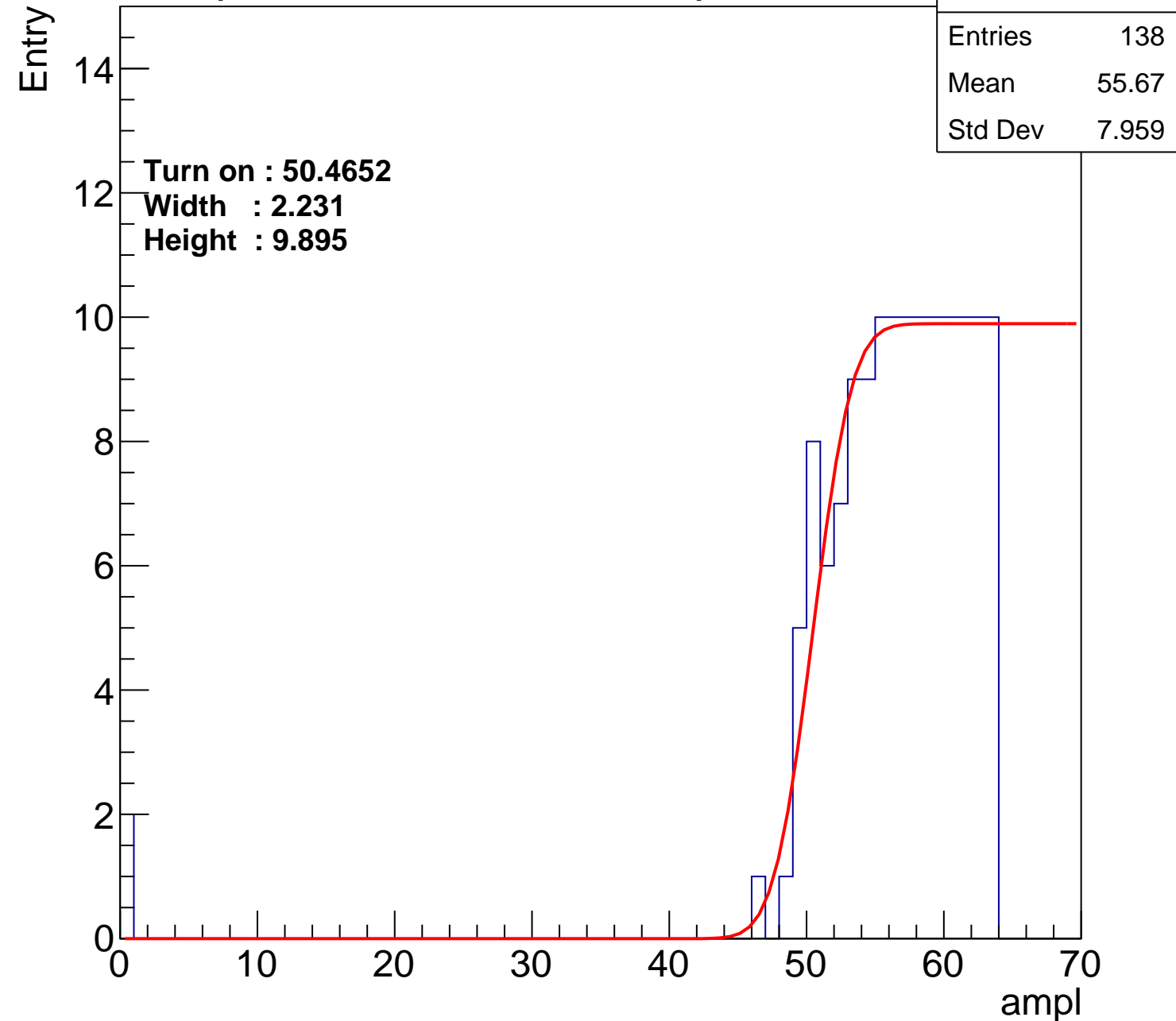
Width : 2.231

Height : 9.895

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch21

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.26
Std Dev	10.64

Turn on : 51.4192

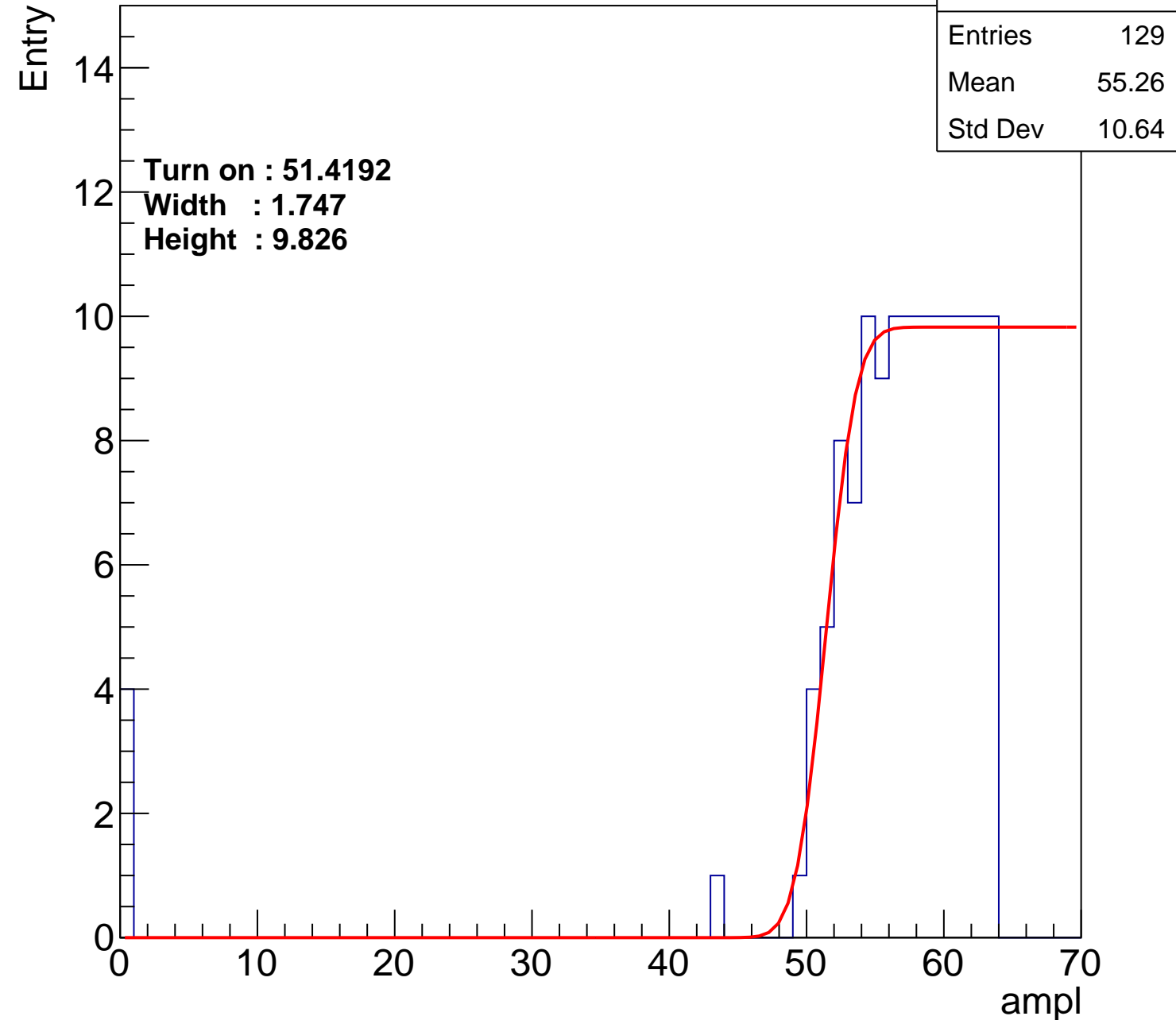
Width : 1.747

Height : 9.826

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch22

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	105
Mean	55.93
Std Dev	11.6

Turn on : 53.7014

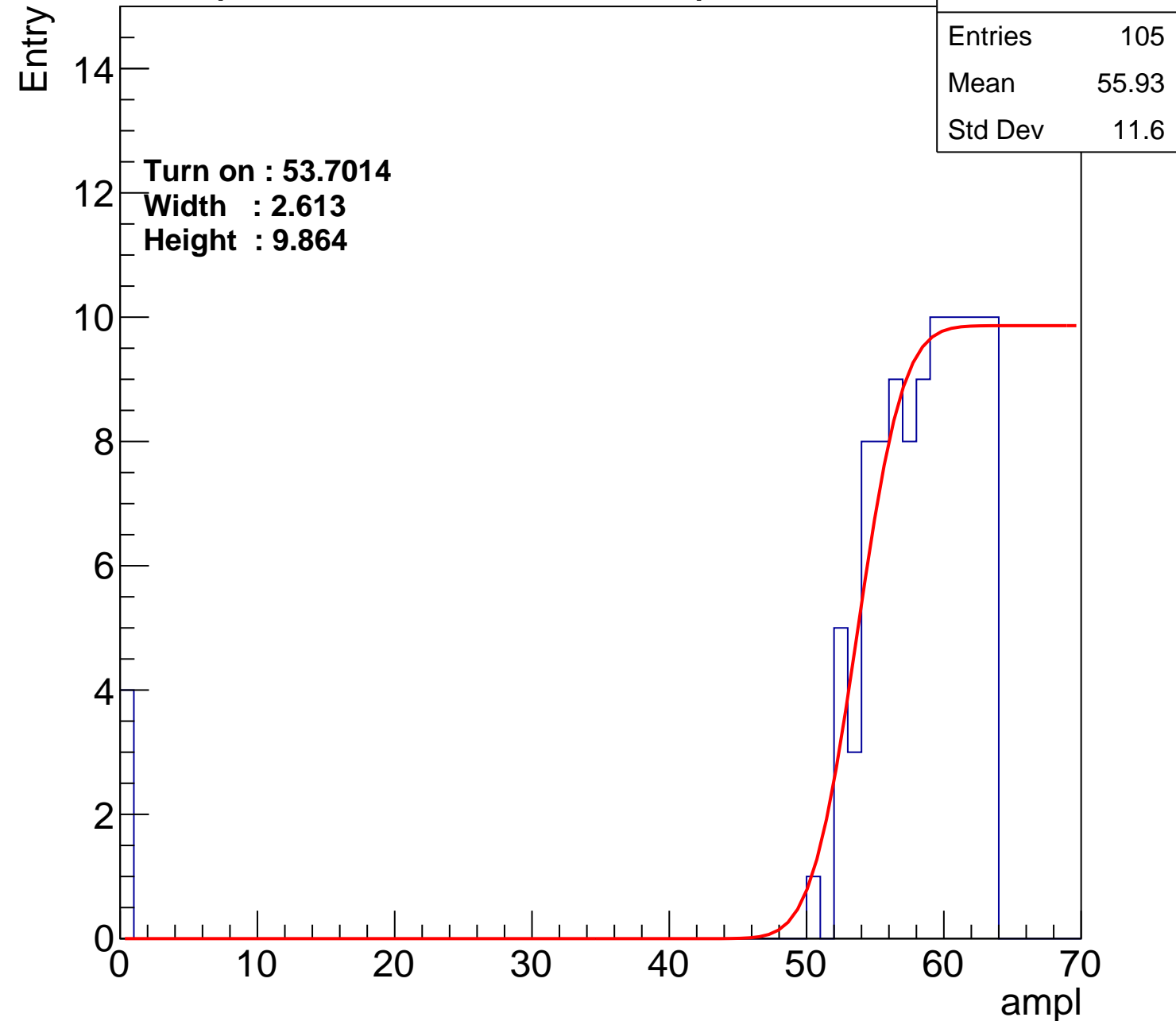
Width : 2.613

Height : 9.864

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch23

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	56.37
Std Dev	6.589

Turn on : 52.7582

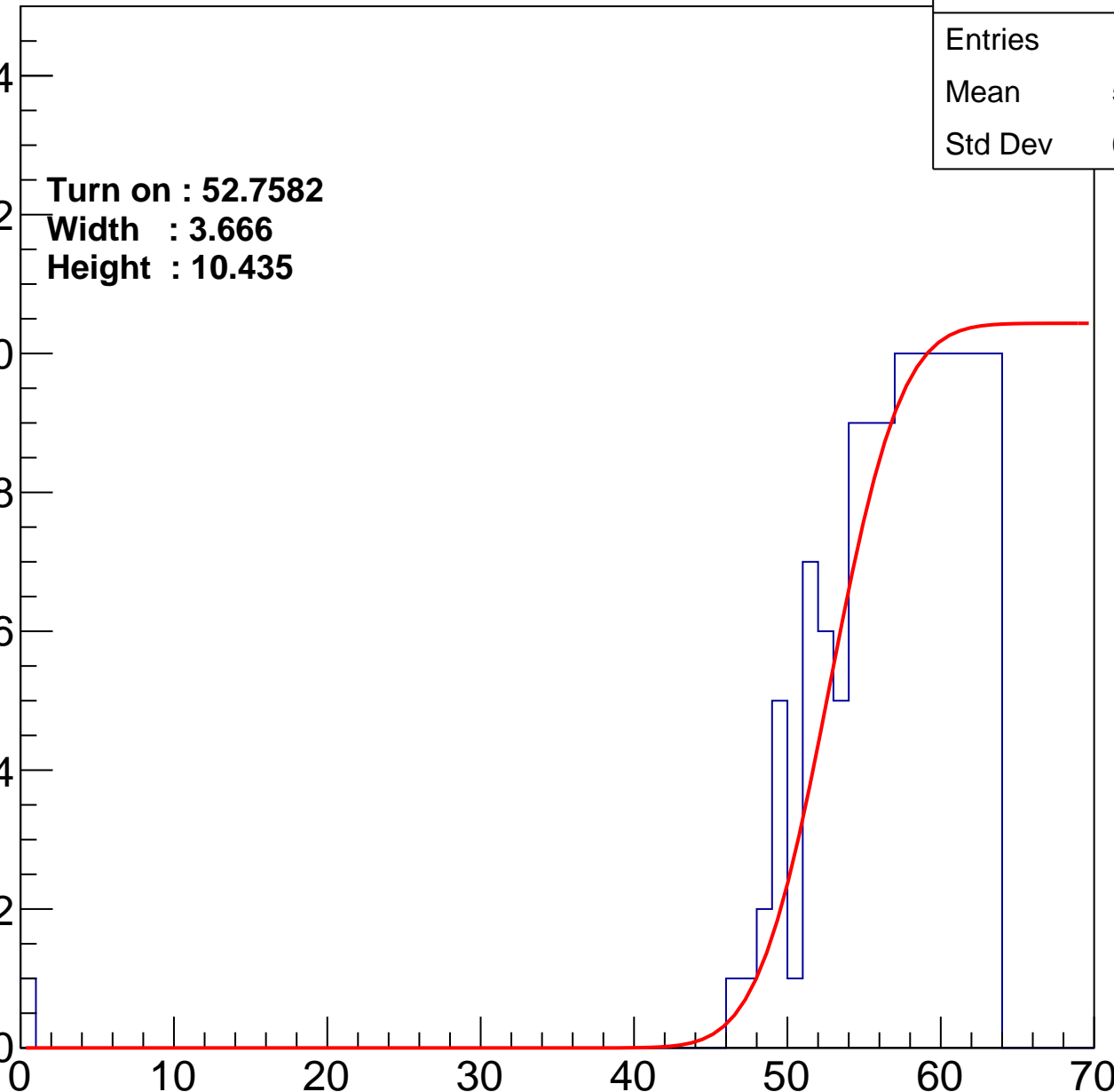
Width : 3.666

Height : 10.435

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch24

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	55.5
Std Dev	7.982

Turn on : 50.4293

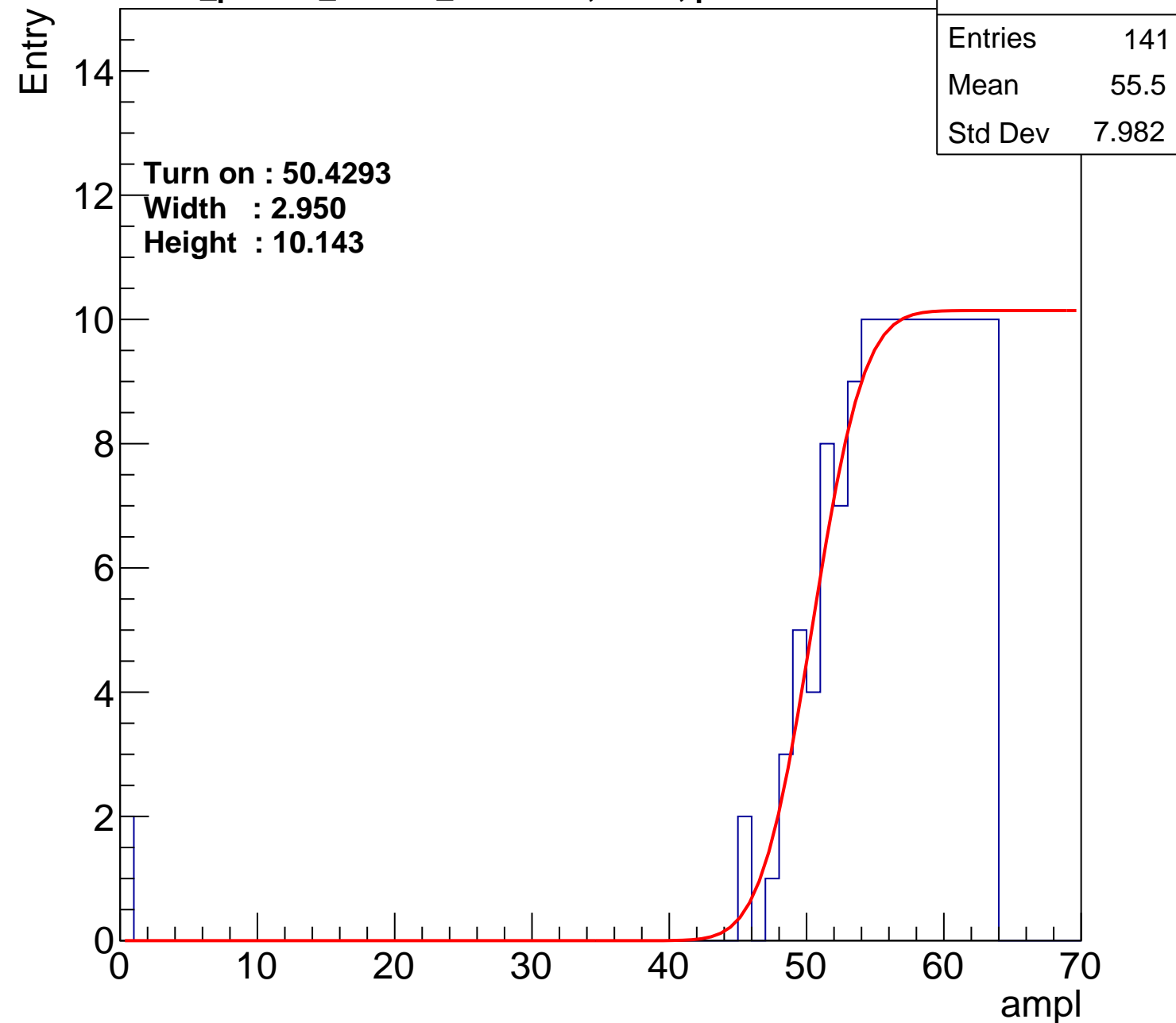
Width : 2.950

Height : 10.143

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch25

calib\_packv5\_040323\_1717.root, FC#2, port C3

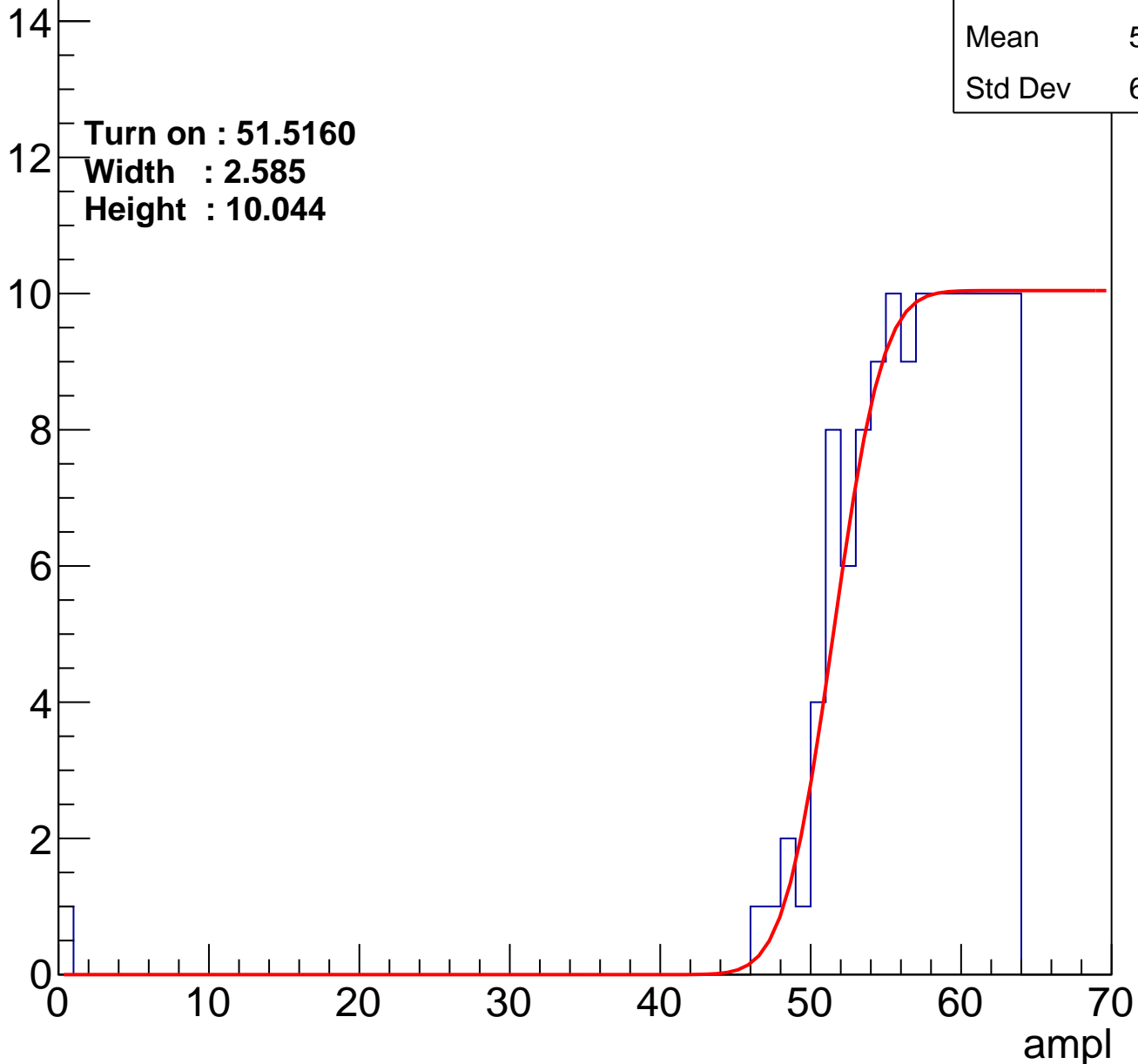
Entries	130
Mean	56.32
Std Dev	6.468

Turn on : 51.5160

Width : 2.585

Height : 10.044

Entry



# B0L103S, U20-ch26

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	56.17
Std Dev	6.626

Turn on : 51.7655

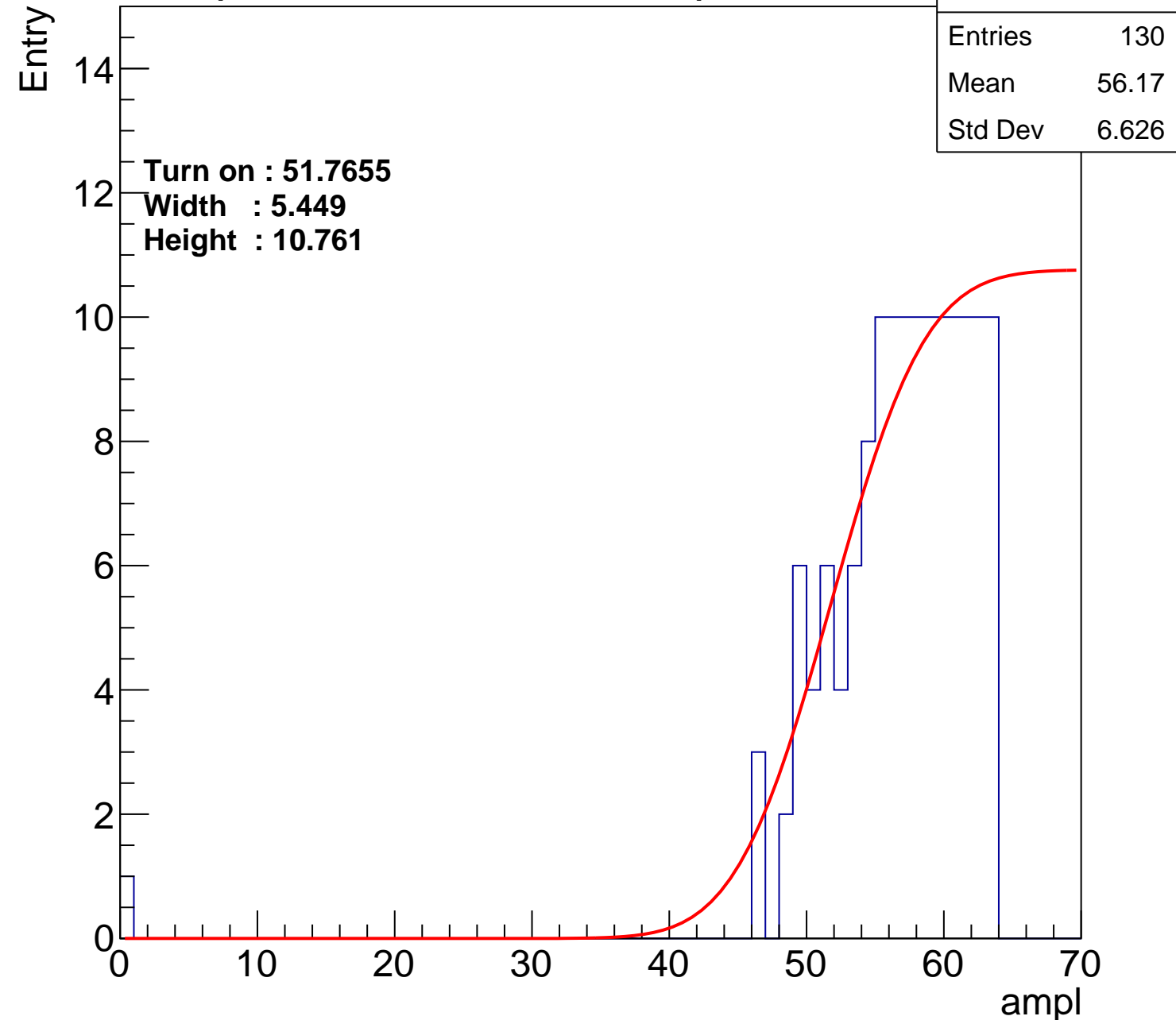
Width : 5.449

Height : 10.761

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch27

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	56.54
Std Dev	8.352

**Turn on : 52.9703**

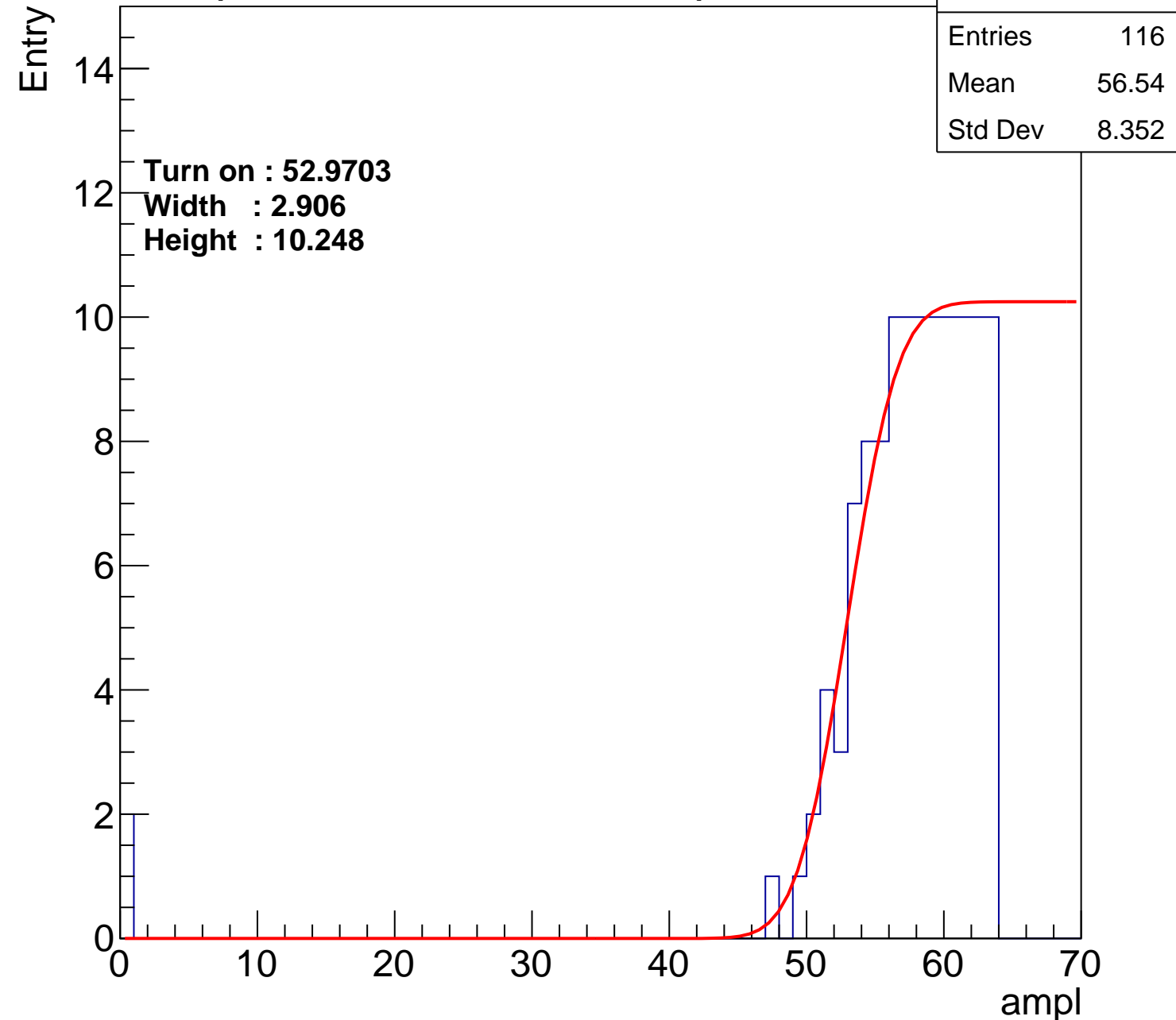
**Width : 2.906**

**Height : 10.248**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch28

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	55.61
Std Dev	8.018

Turn on : 50.8511

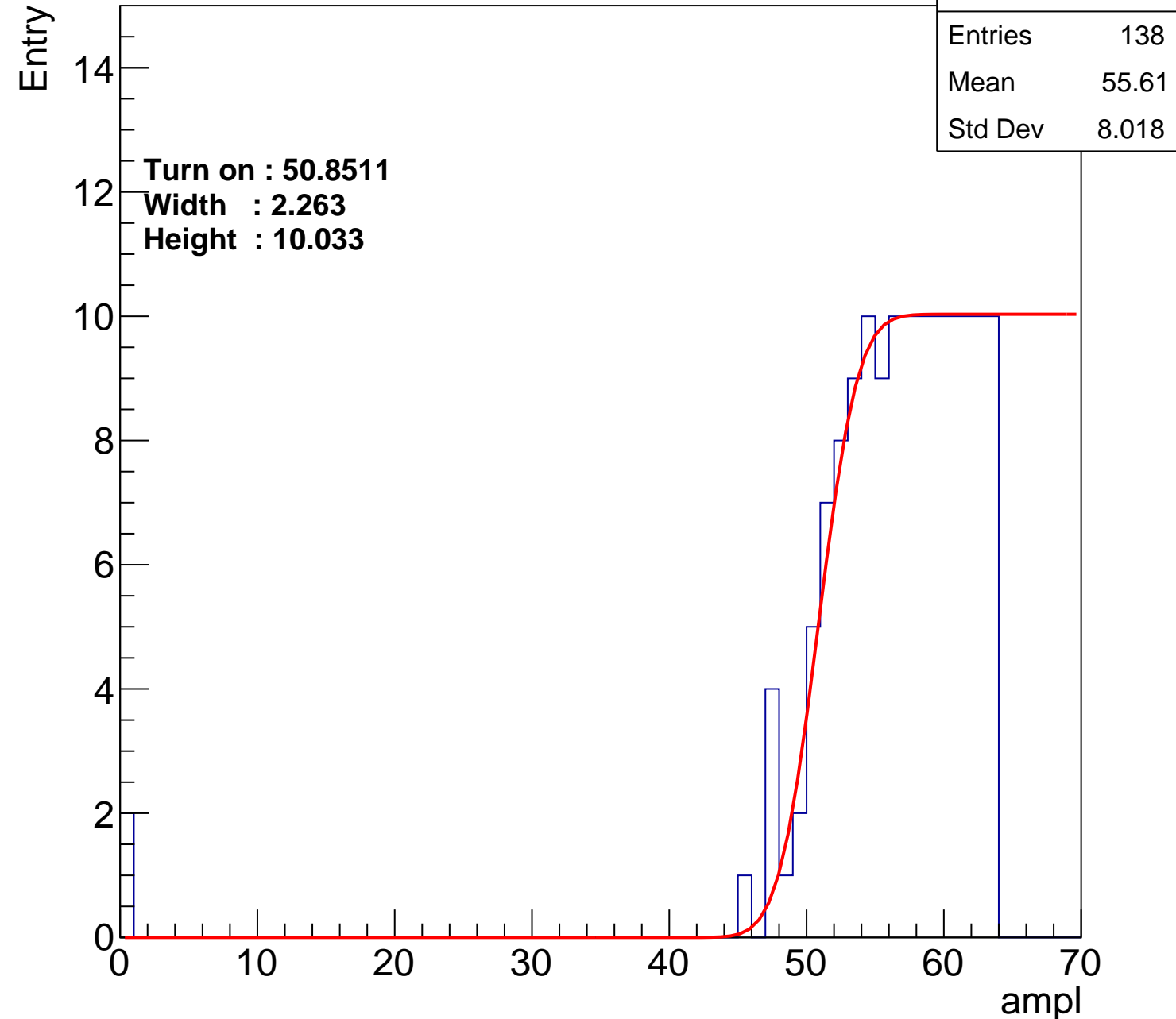
Width : 2.263

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch29

calib\_packv5\_040323\_1717.root, FC#2, port C3

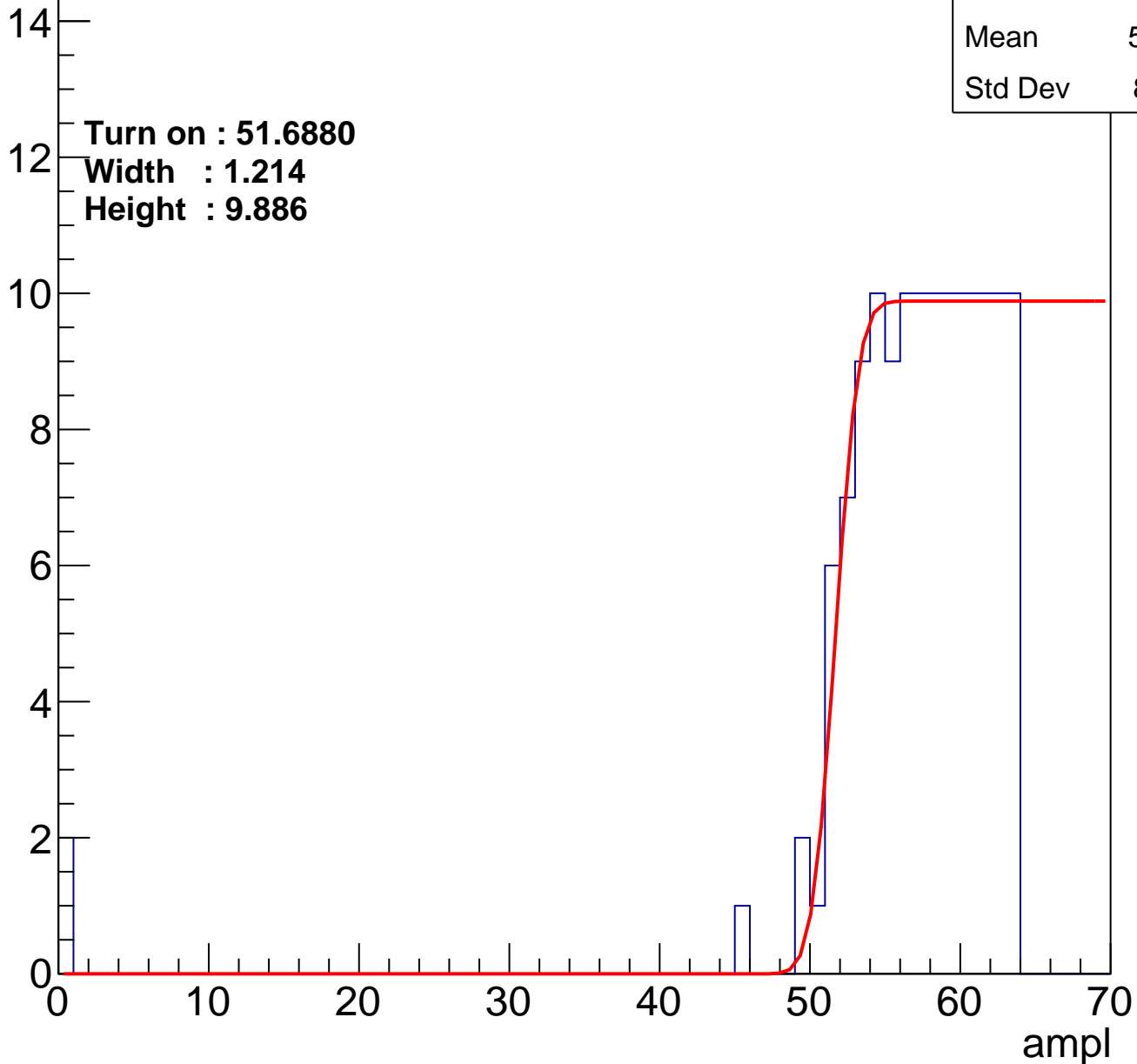
Entries	127
Mean	56.18
Std Dev	8.091

Turn on : 51.6880

Width : 1.214

Height : 9.886

Entry



# B0L103S, U20-ch30

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	142
Mean	54.69
Std Dev	10.33

Turn on : 50.8306

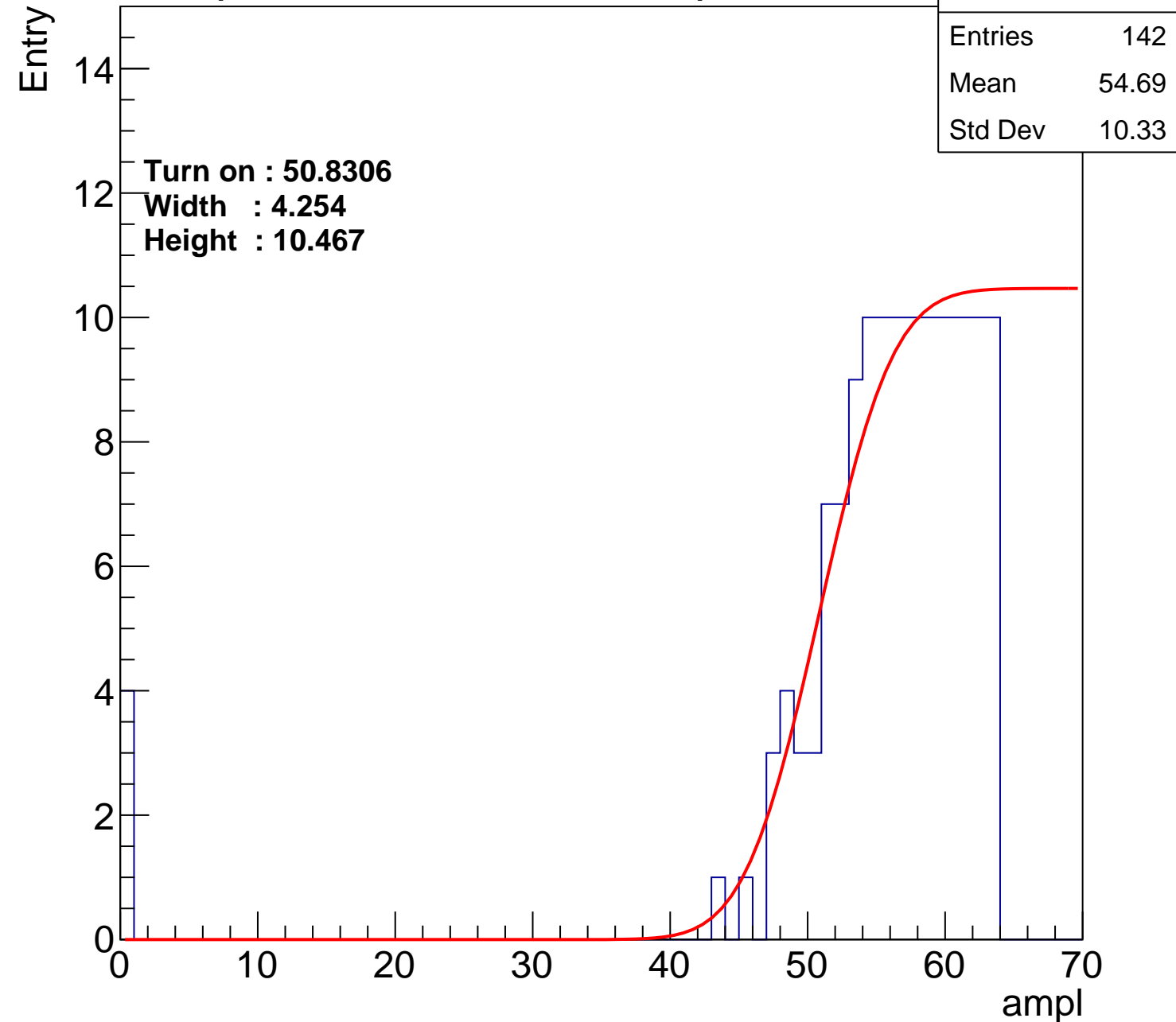
Width : 4.254

Height : 10.467

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch31

calib\_packv5\_040323\_1717.root, FC#2, port C3

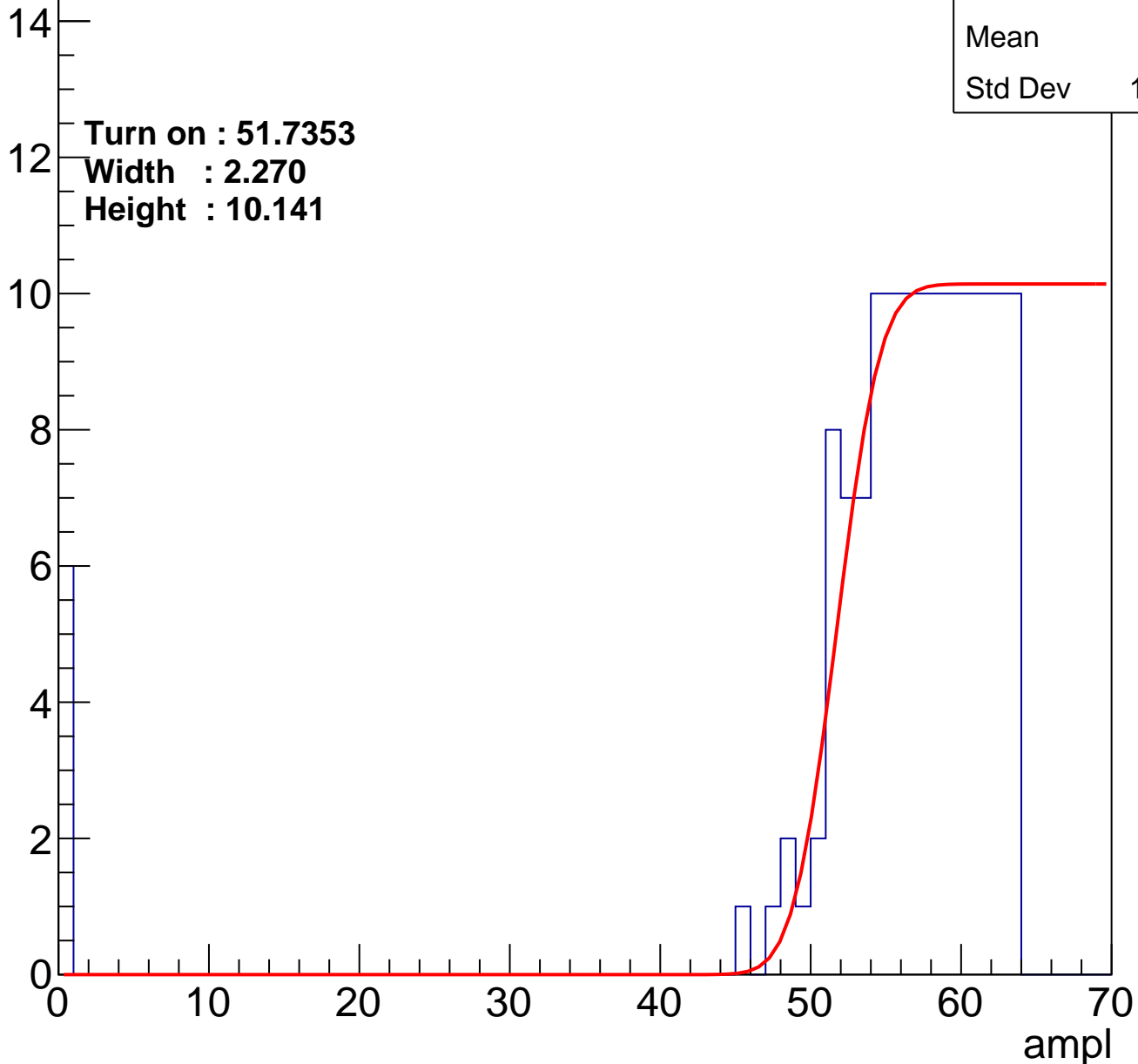
Entries	135
Mean	54.3
Std Dev	12.38

Turn on : 51.7353

Width : 2.270

Height : 10.141

Entry



# B0L103S, U20-ch32

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	55.22
Std Dev	9.295

Turn on : 50.9369

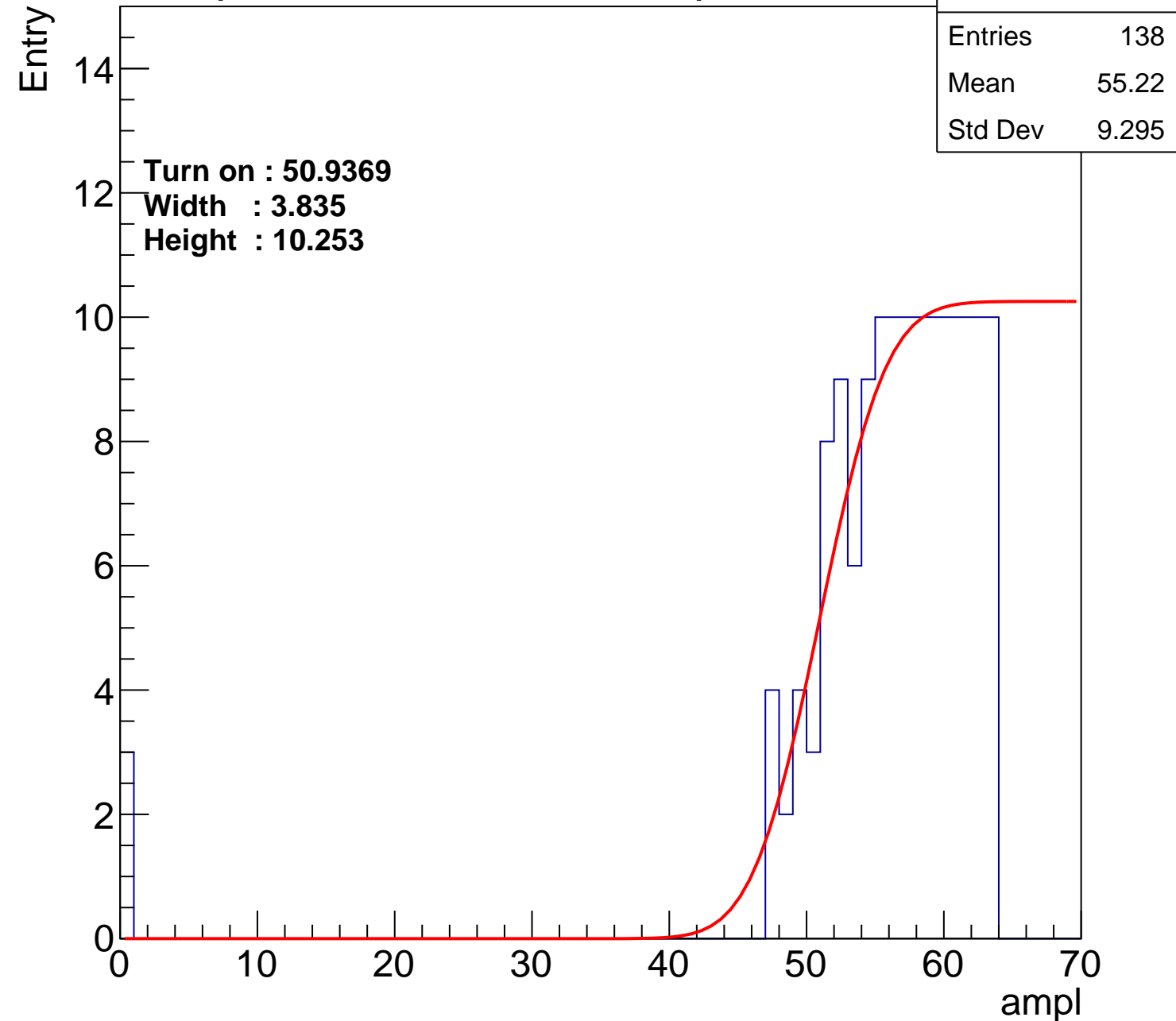
Width : 3.835

Height : 10.253

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch33

calib\_packv5\_040323\_1717.root, FC#2, port C3

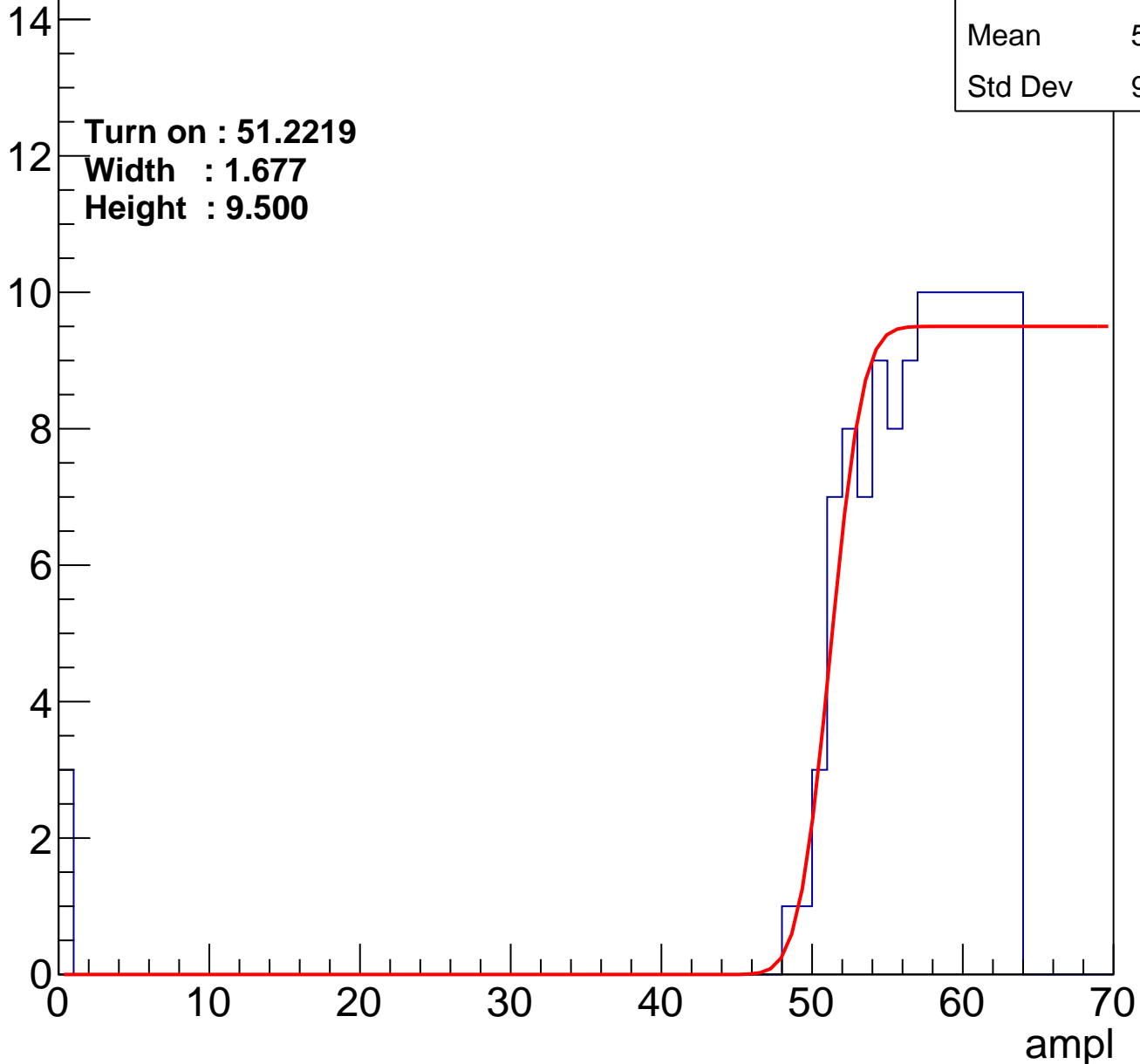
Entries	126
Mean	55.72
Std Dev	9.524

Turn on : 51.2219

Width : 1.677

Height : 9.500

Entry



# B0L103S, U20-ch34

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.95
Std Dev	8.061

Turn on : 50.8750

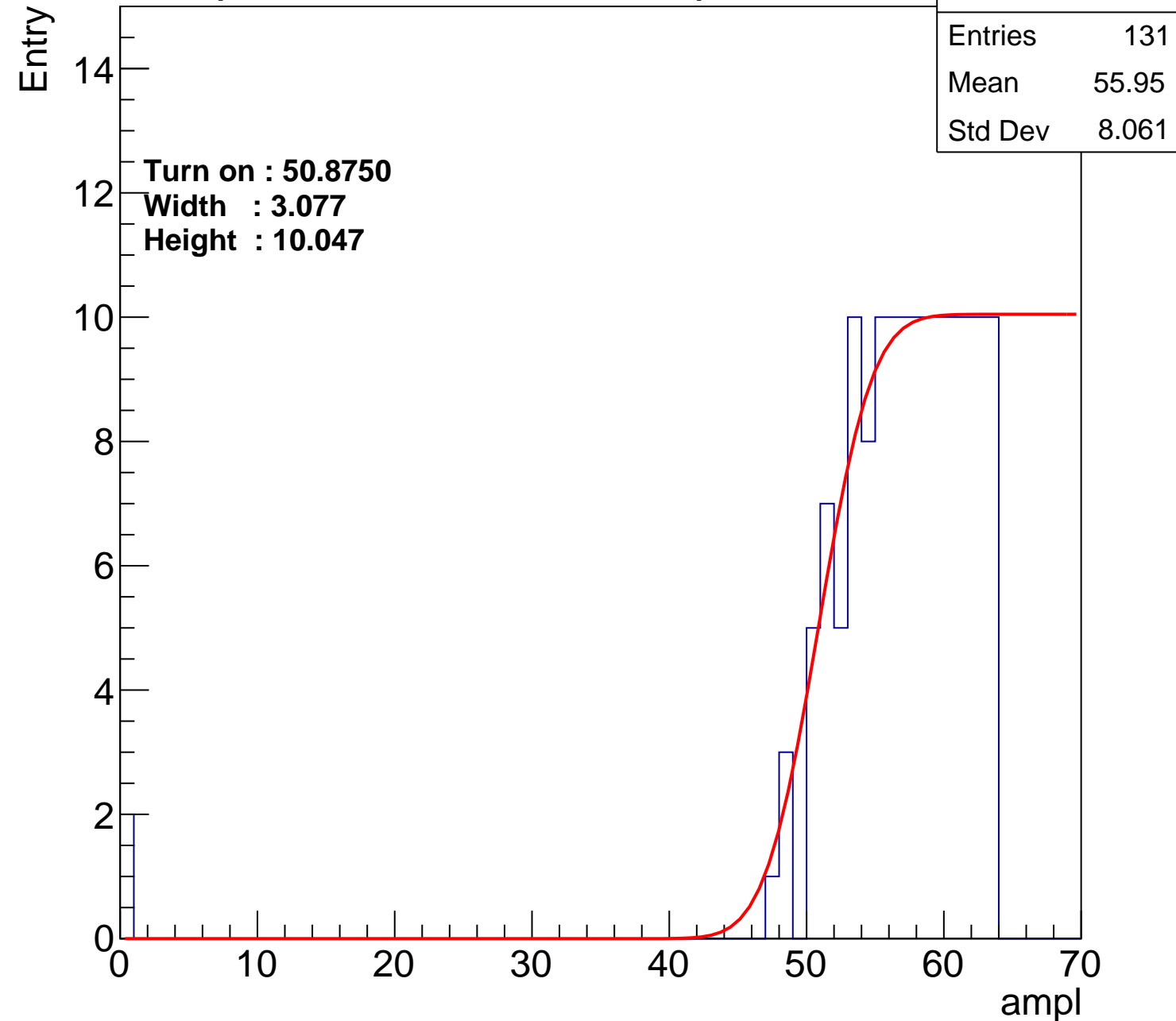
Width : 3.077

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch35

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	55.64
Std Dev	7.997

Turn on : 50.8037

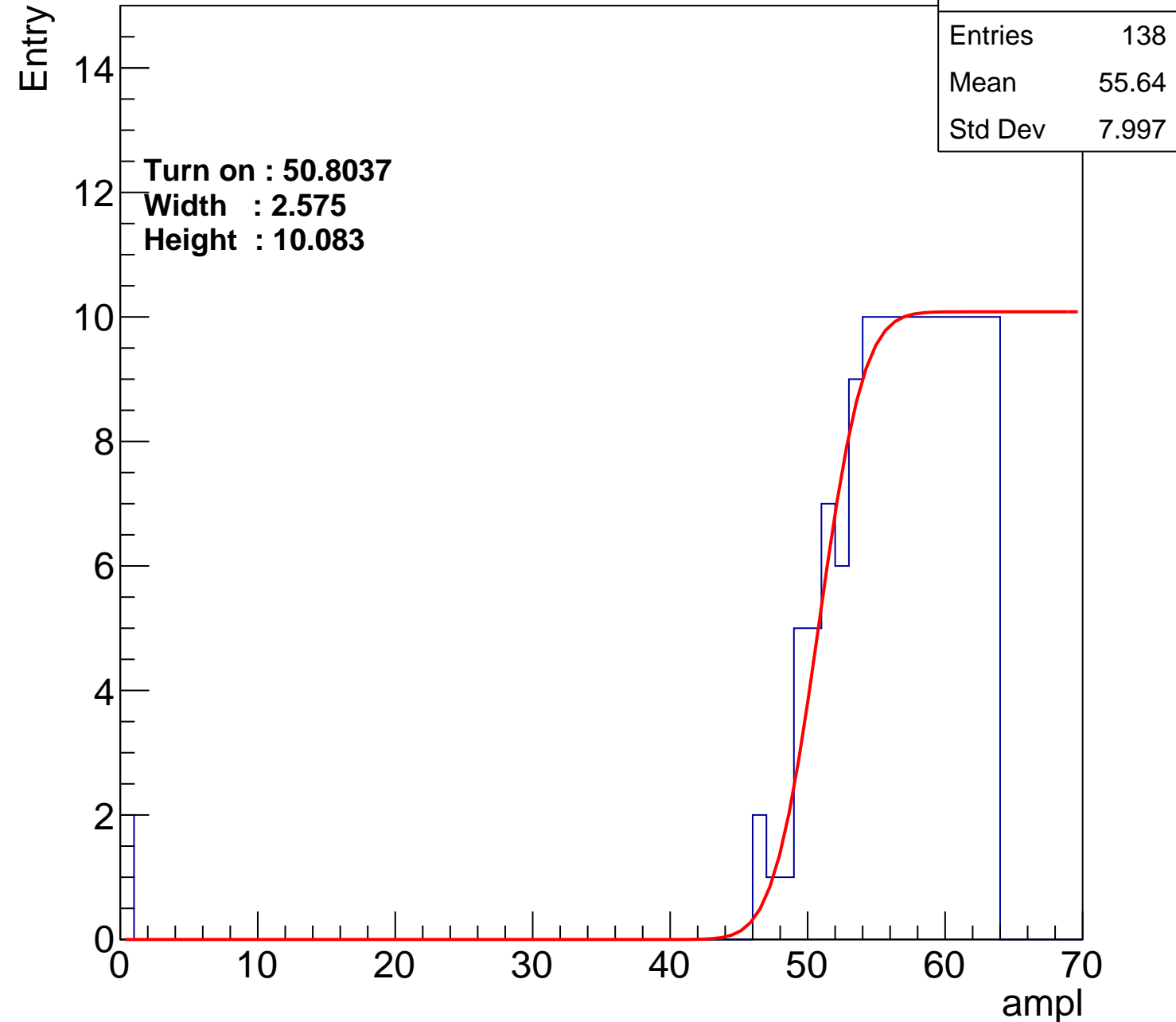
Width : 2.575

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch36

calib\_packv5\_040323\_1717.root, FC#2, port C3

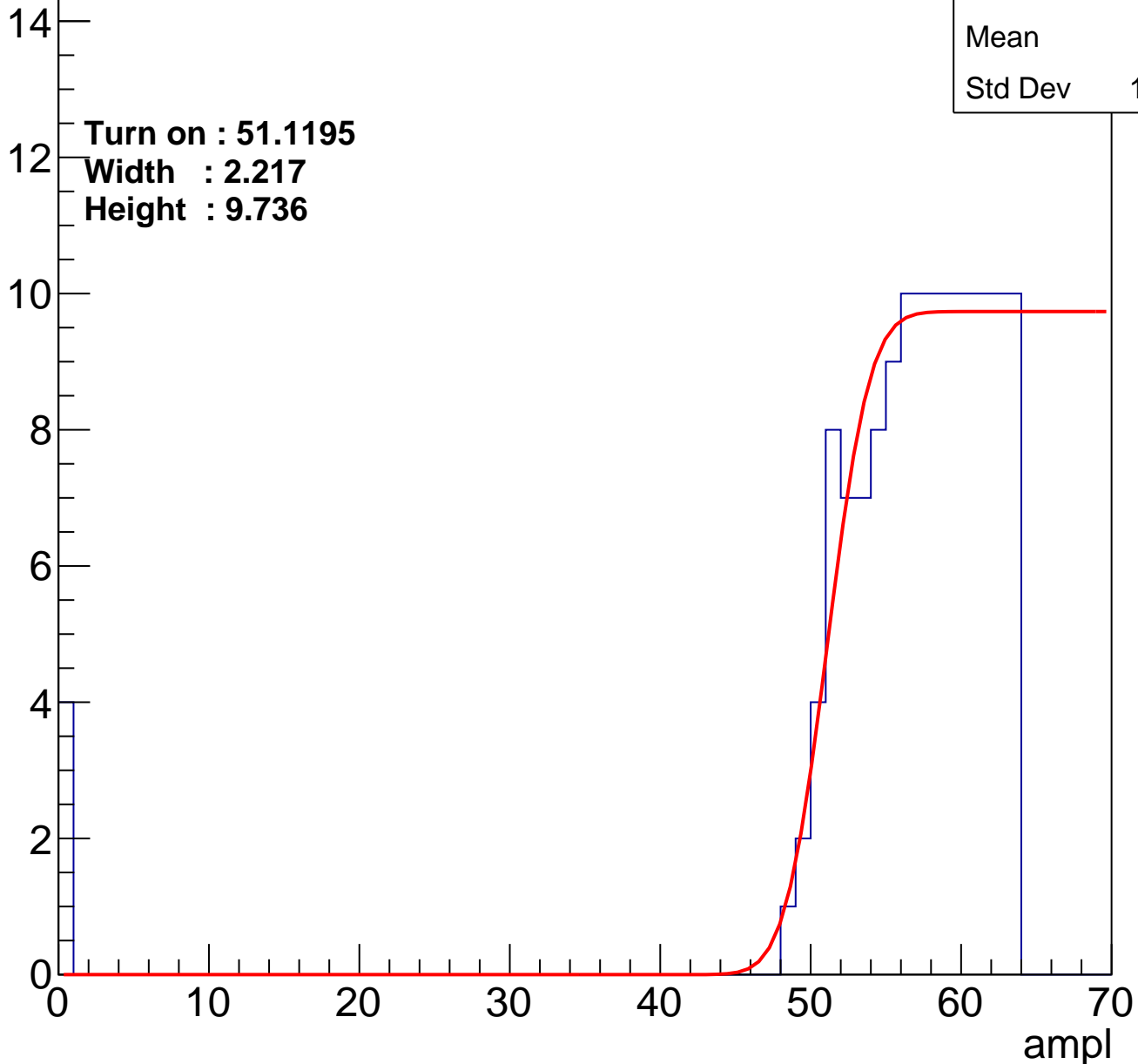
Entries	130
Mean	55.2
Std Dev	10.59

Turn on : 51.1195

Width : 2.217

Height : 9.736

Entry



# B0L103S, U20-ch37

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.68
Std Dev	8.175

Turn on : 51.3896

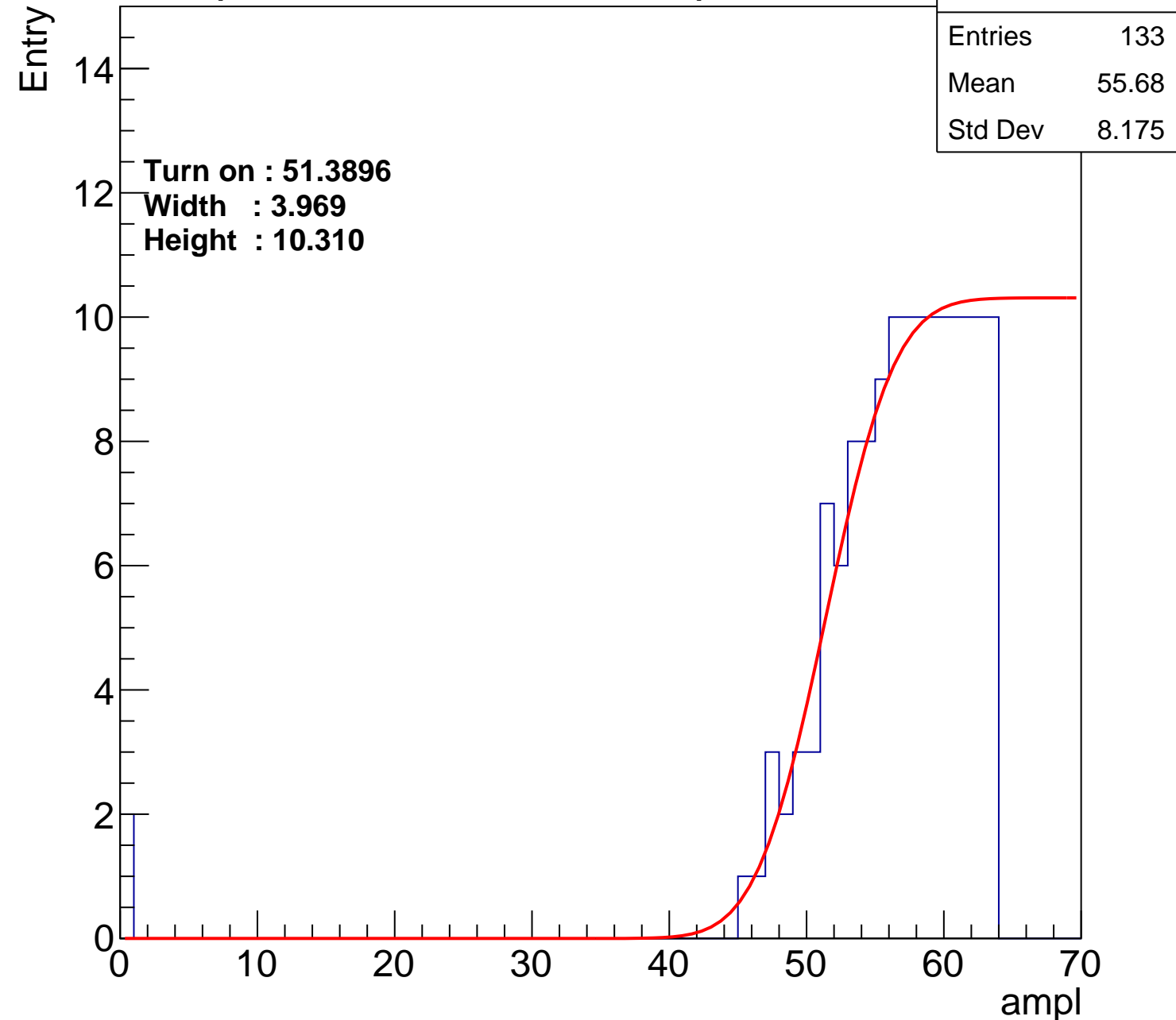
Width : 3.969

Height : 10.310

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch38

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	56.28
Std Dev	6.486

Turn on : 51.6356

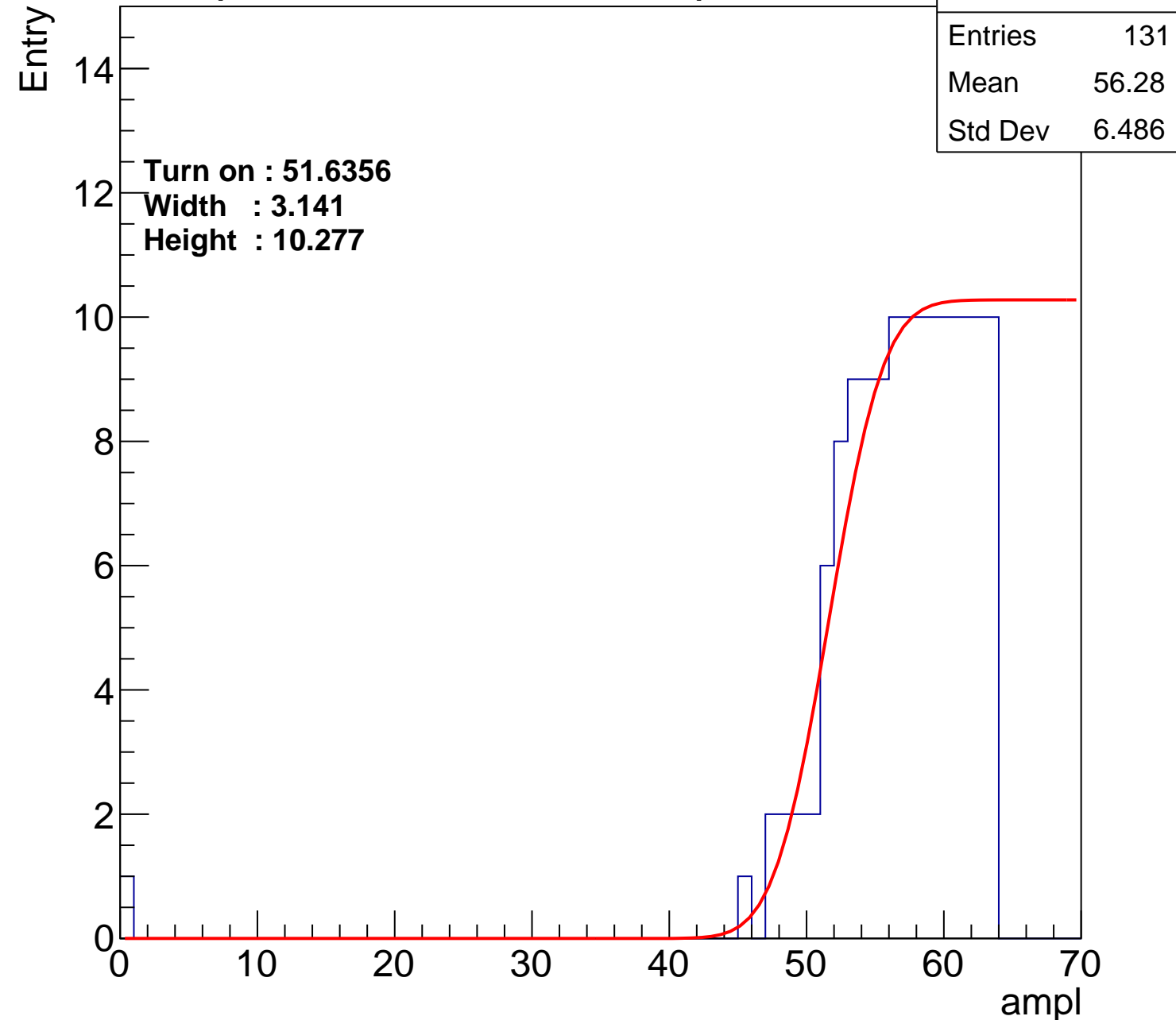
Width : 3.141

Height : 10.277

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch39

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.81
Std Dev	8.026

Turn on : 50.8865

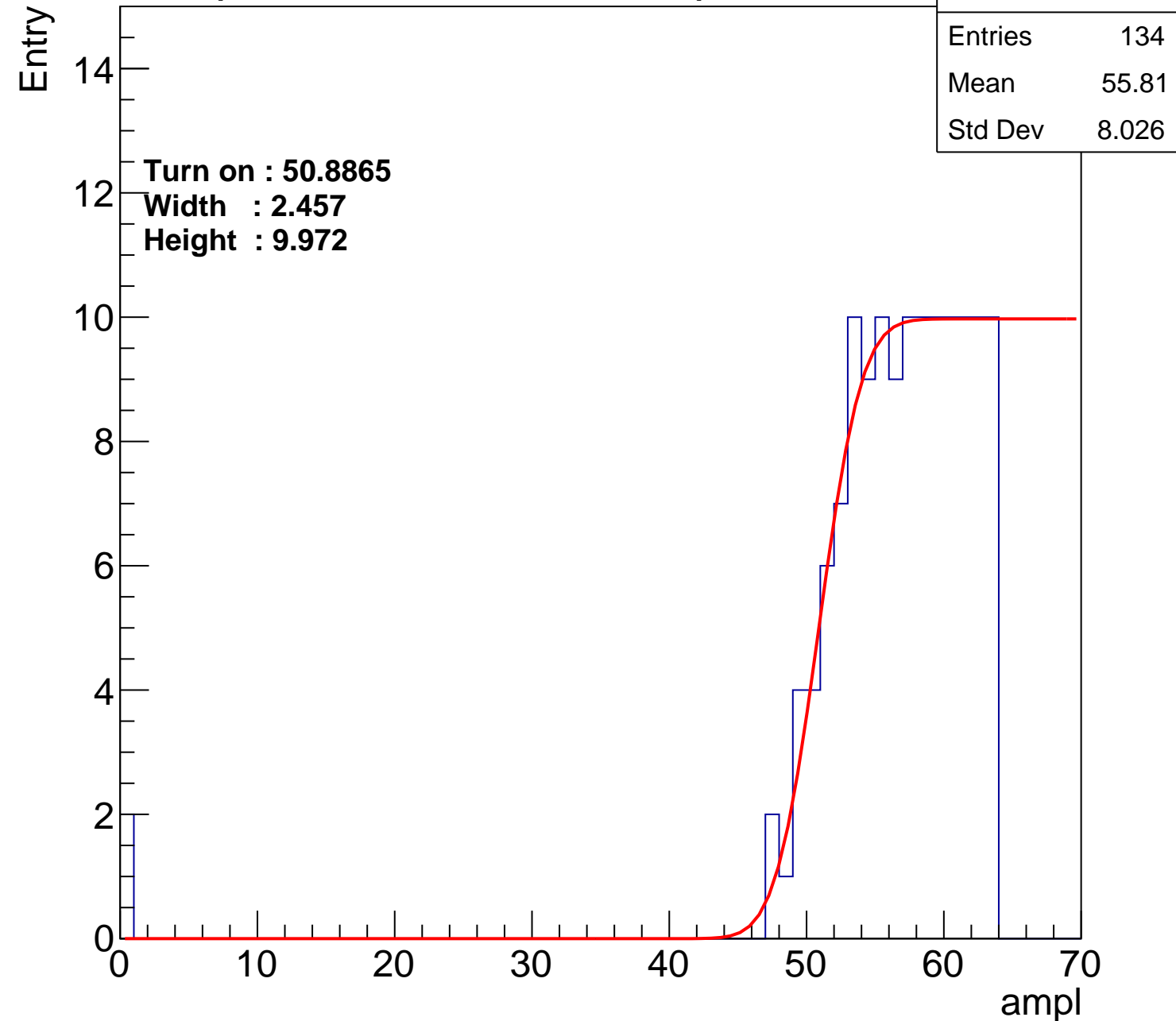
Width : 2.457

Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch40

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	54.79
Std Dev	10.29

Turn on : 50.2376

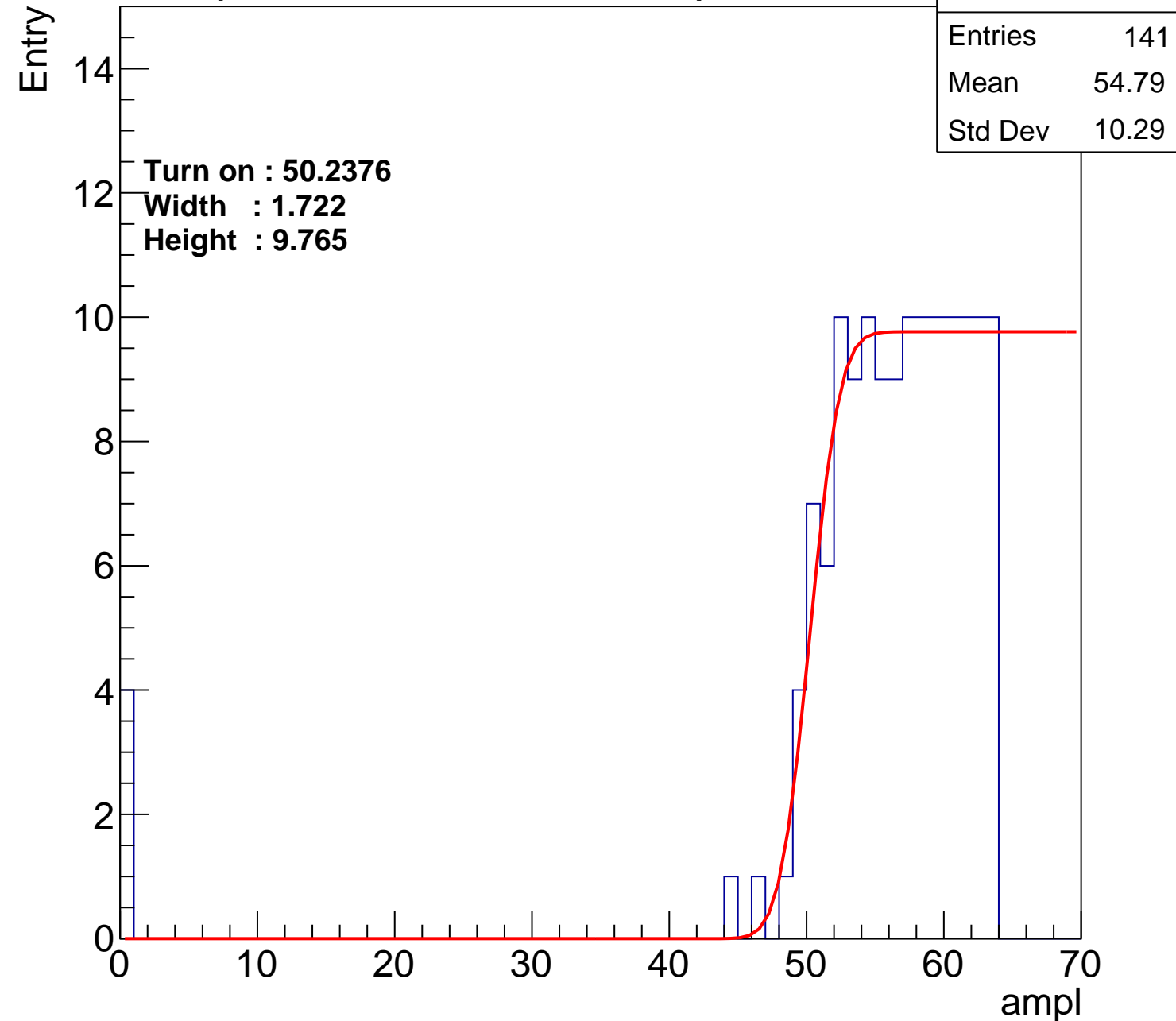
Width : 1.722

Height : 9.765

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch41

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.99
Std Dev	6.39

Turn on : 50.4642

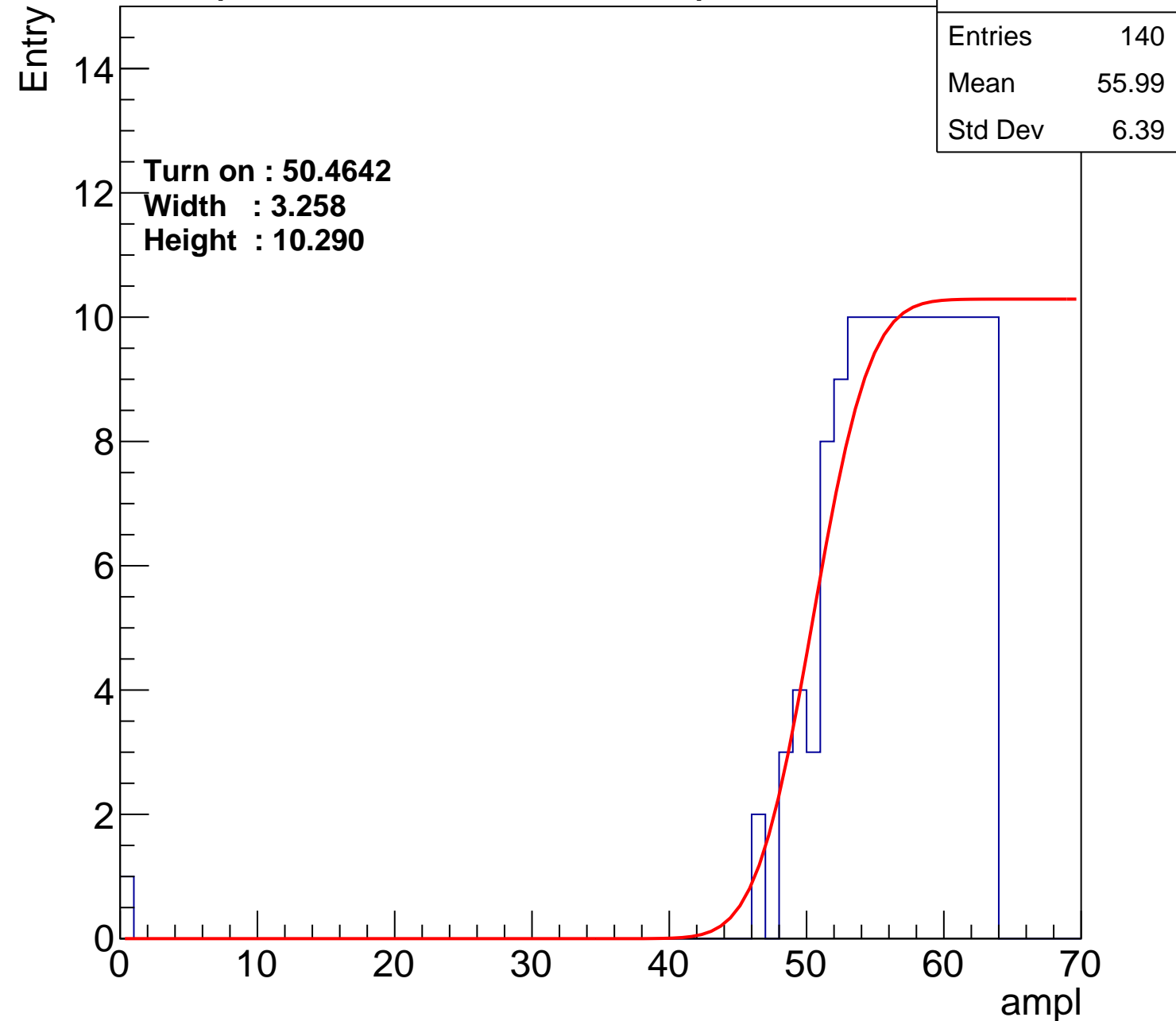
Width : 3.258

Height : 10.290

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch42

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	55.91
Std Dev	6.408

Turn on : 50.2734

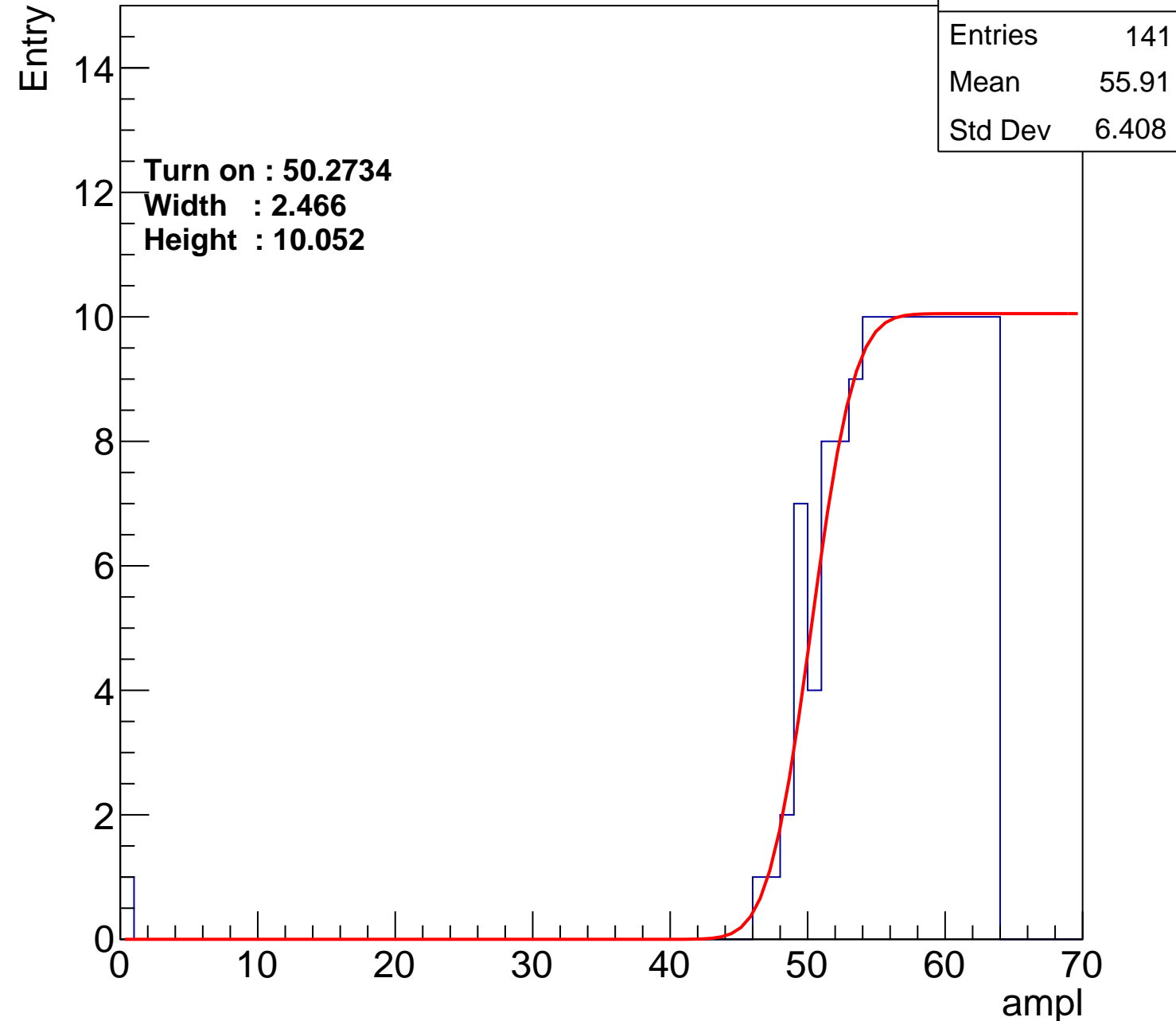
Width : 2.466

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch43

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.84
Std Dev	9.568

Turn on : 51.6966

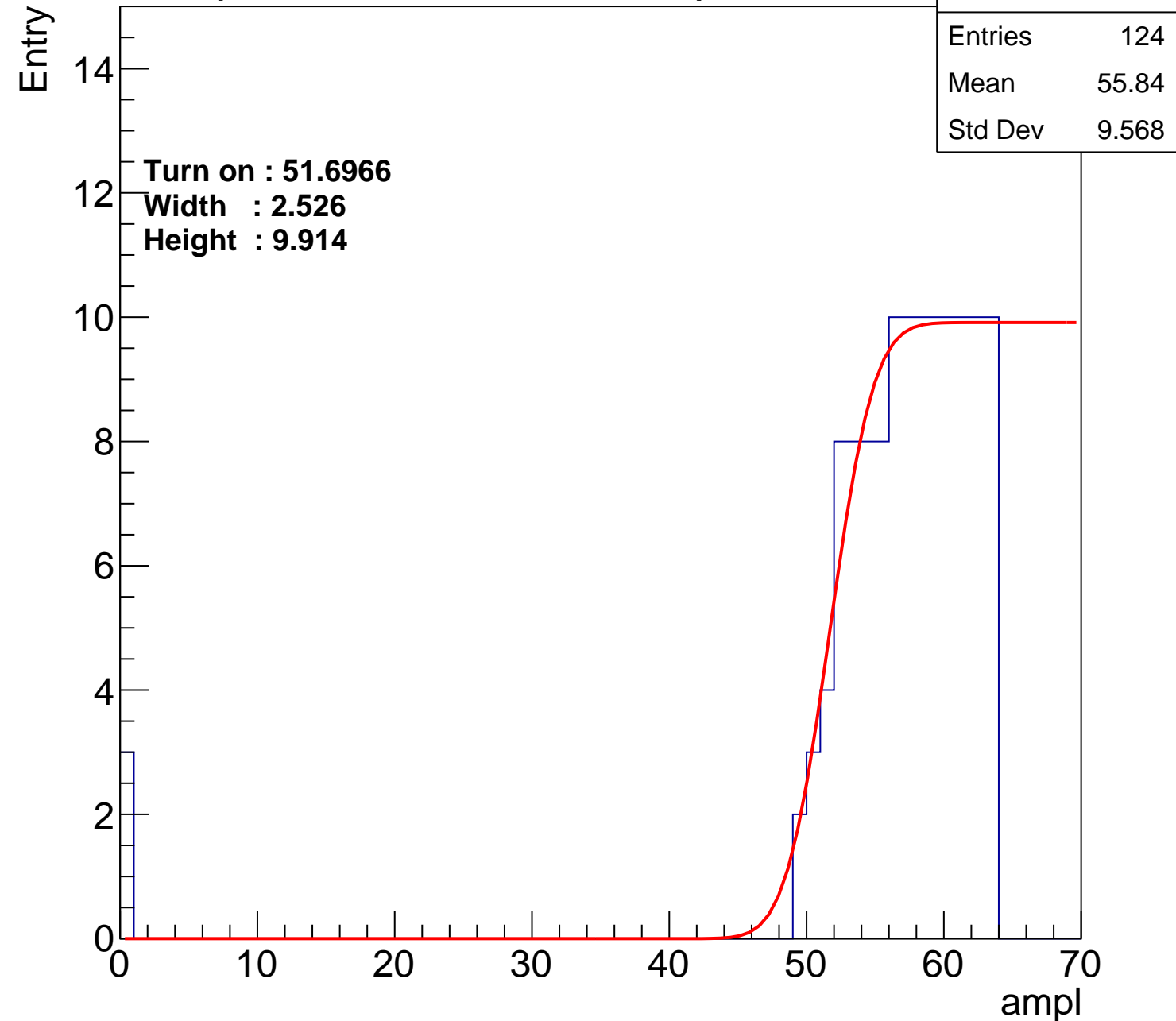
Width : 2.526

Height : 9.914

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch44

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.88
Std Dev	8.084

Turn on : 51.0674

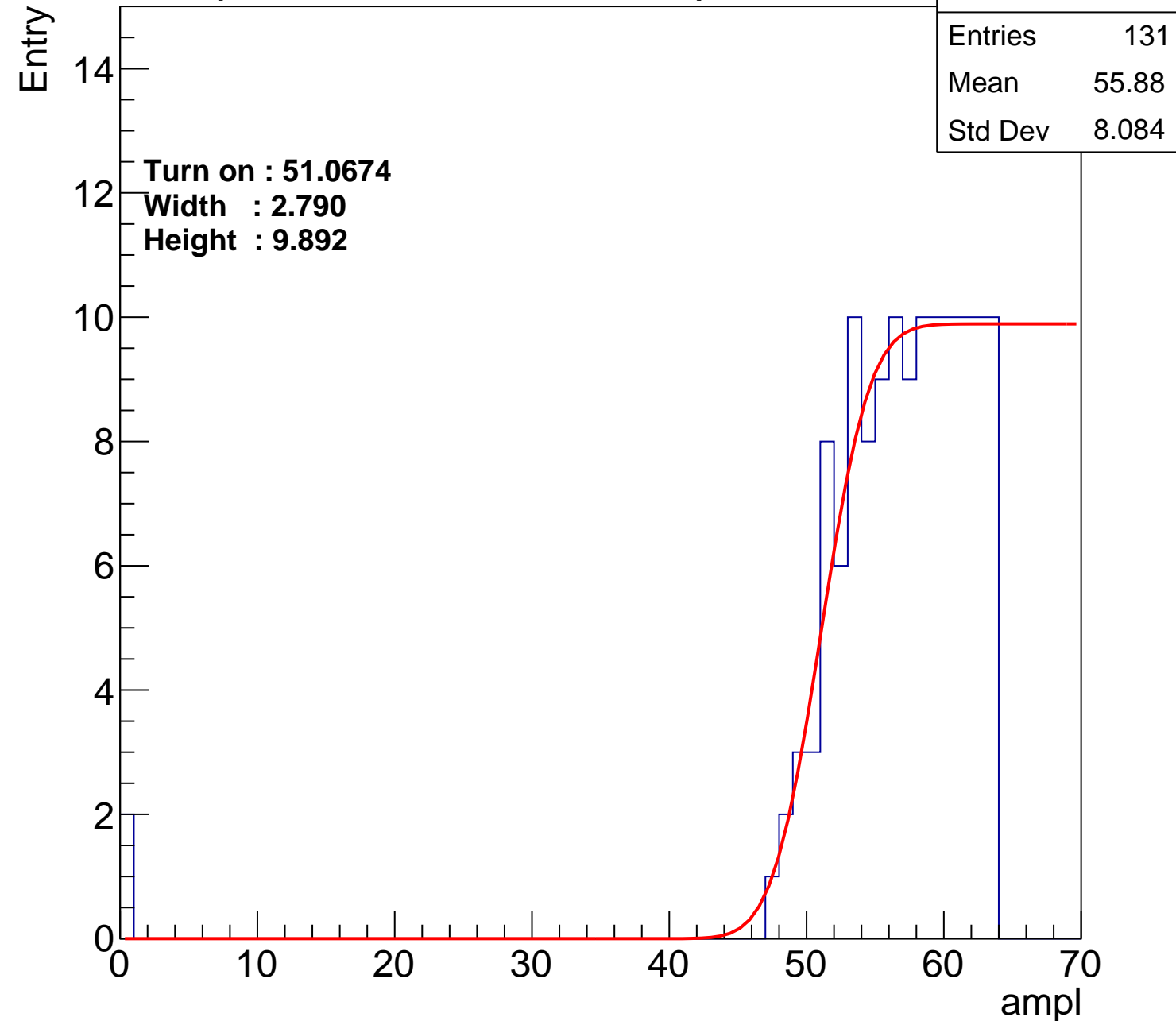
Width : 2.790

Height : 9.892

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch45

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	56.65
Std Dev	6.567

Turn on : 52.6115

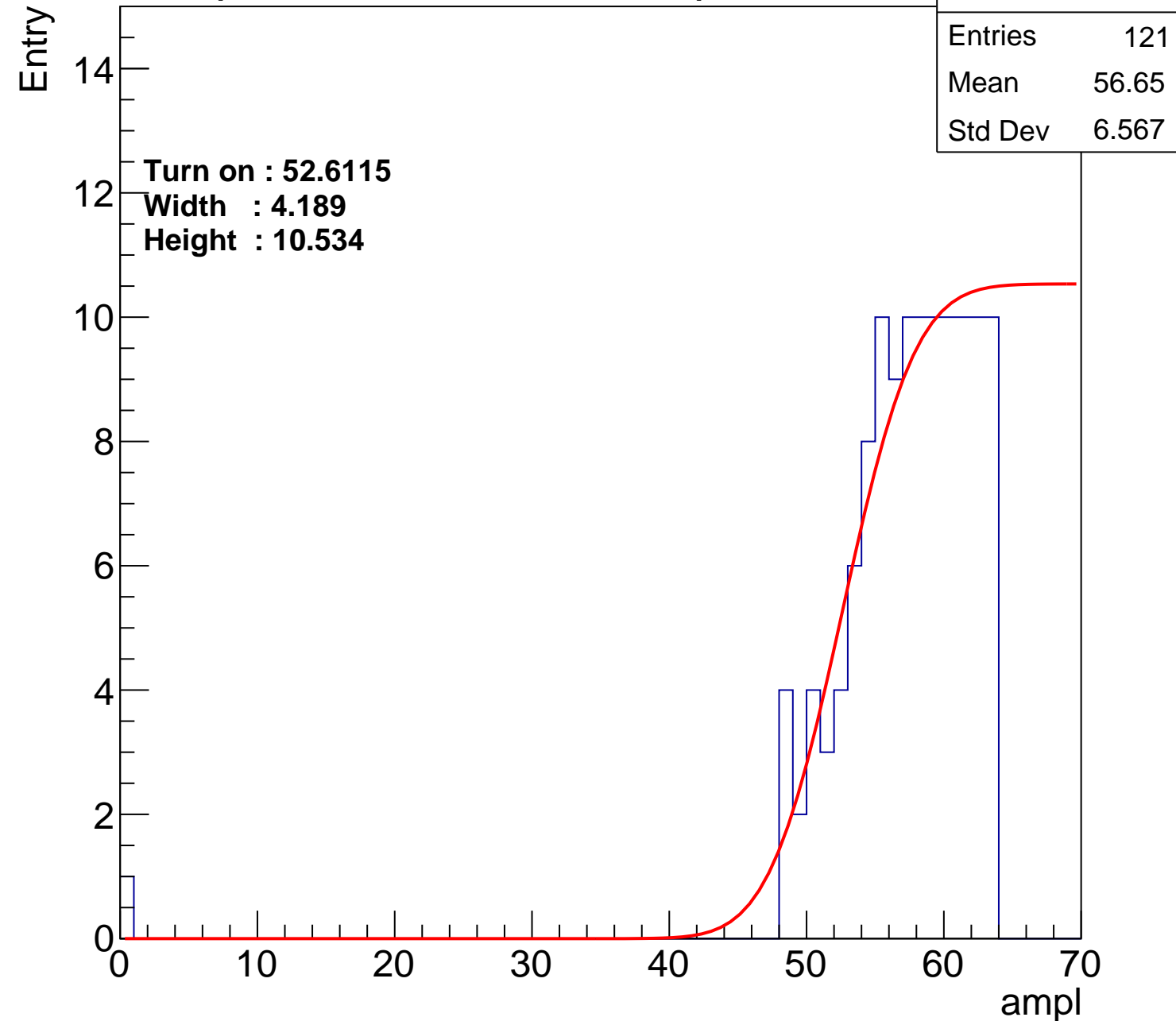
Width : 4.189

Height : 10.534

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch46

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.53
Std Dev	10.94

Turn on : 52.7003

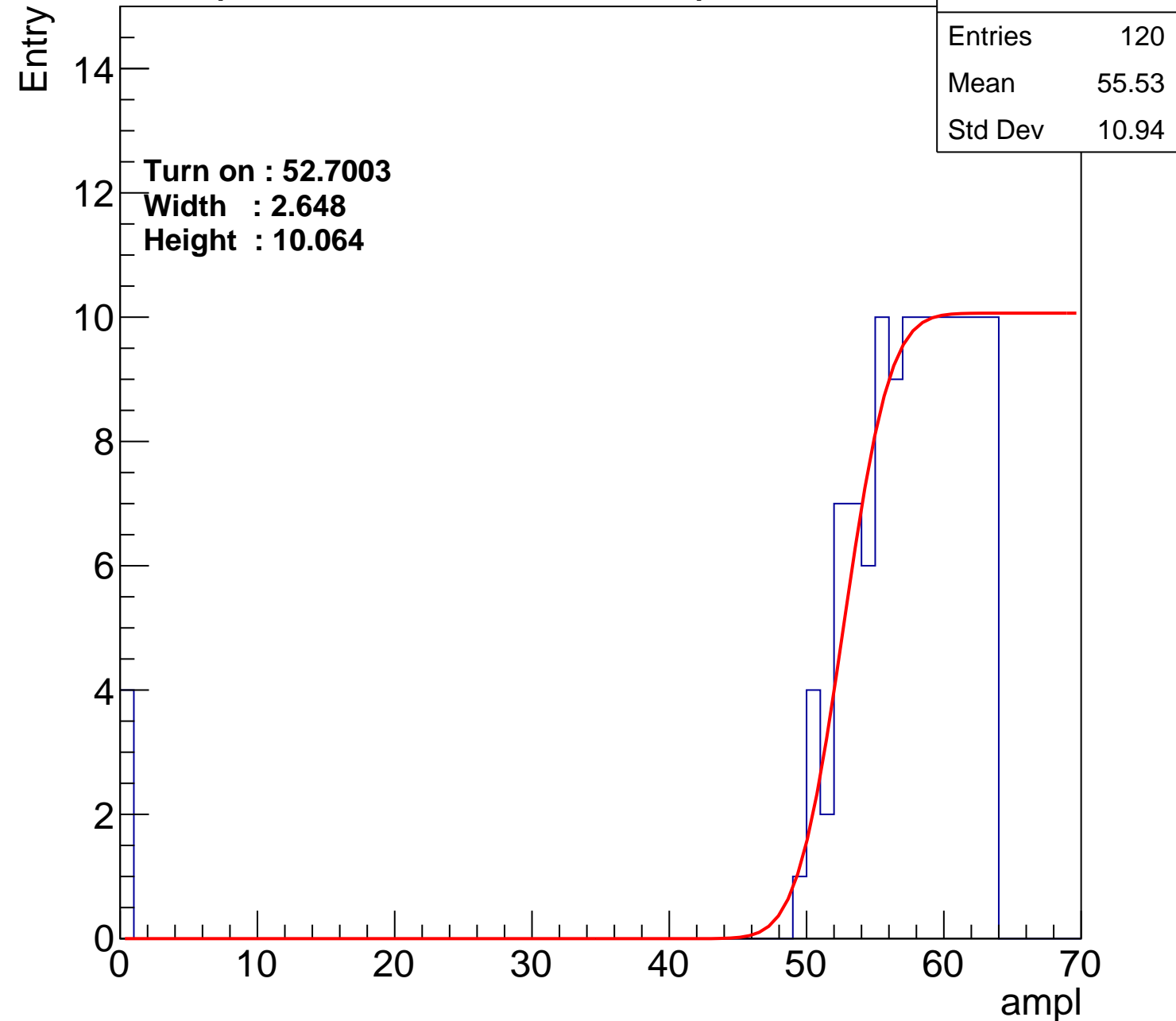
Width : 2.648

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch47

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.47
Std Dev	10.83

Turn on : 52.4548

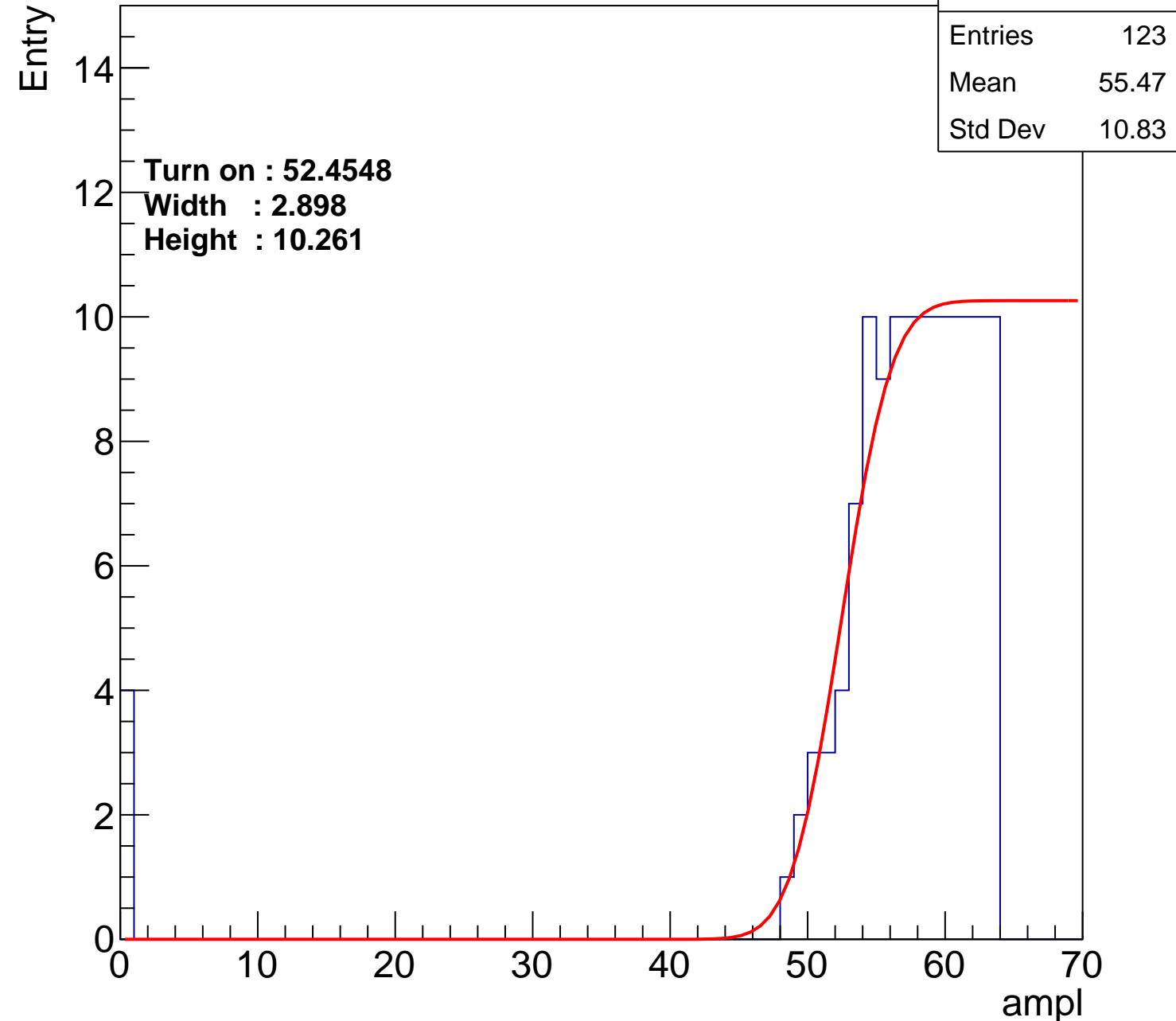
Width : 2.898

Height : 10.261

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch48

calib\_packv5\_040323\_1717.root, FC#2, port C3

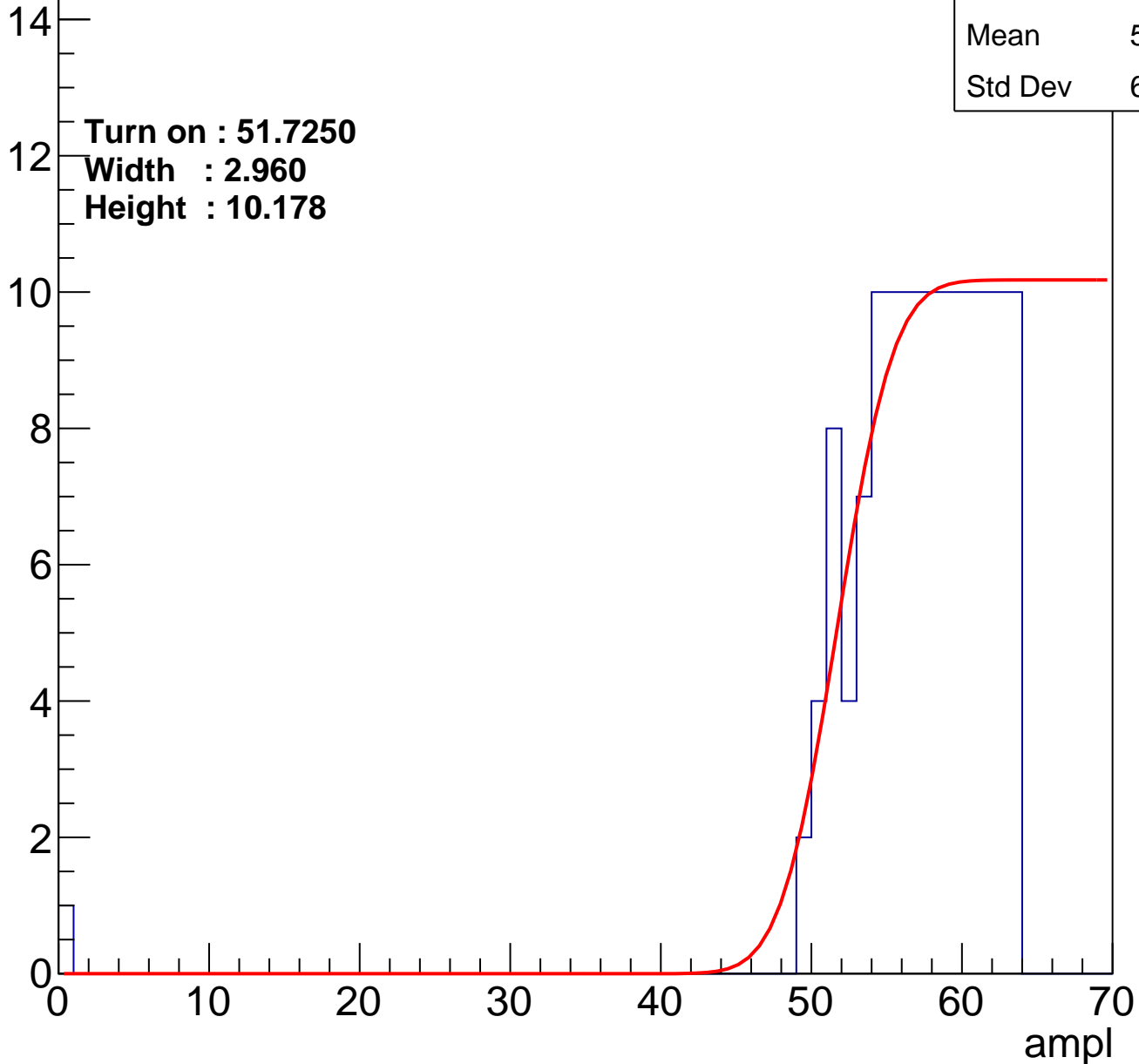
Entries	126
Mean	56.63
Std Dev	6.365

Turn on : 51.7250

Width : 2.960

Height : 10.178

Entry



# B0L103S, U20-ch49

calib\_packv5\_040323\_1717.root, FC#2, port C3

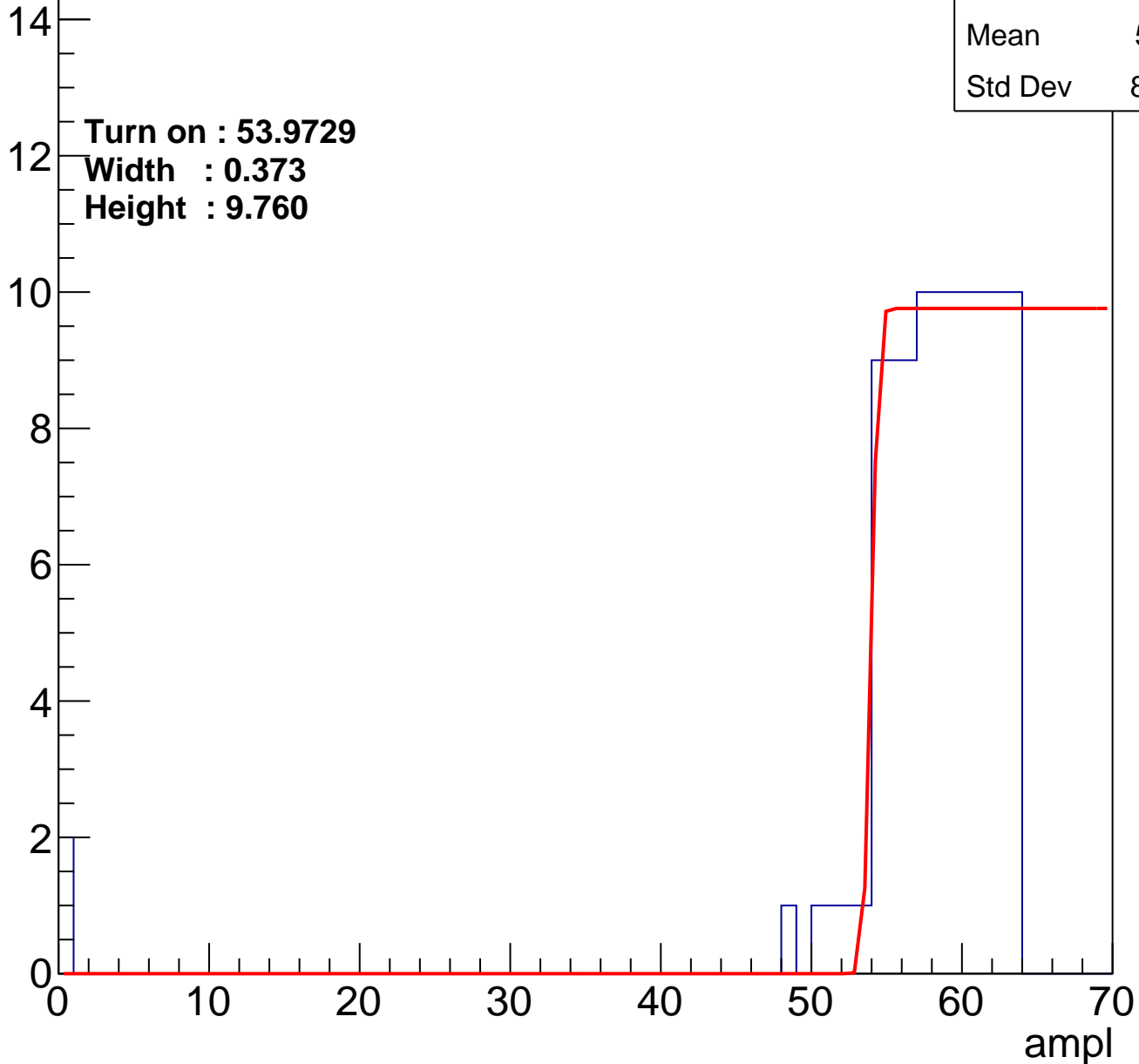
Entries	104
Mean	57.11
Std Dev	8.627

**Turn on : 53.9729**

**Width : 0.373**

**Height : 9.760**

Entry



# B0L103S, U20-ch50

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	160
Mean	54.31
Std Dev	8.99

Turn on : 47.9728

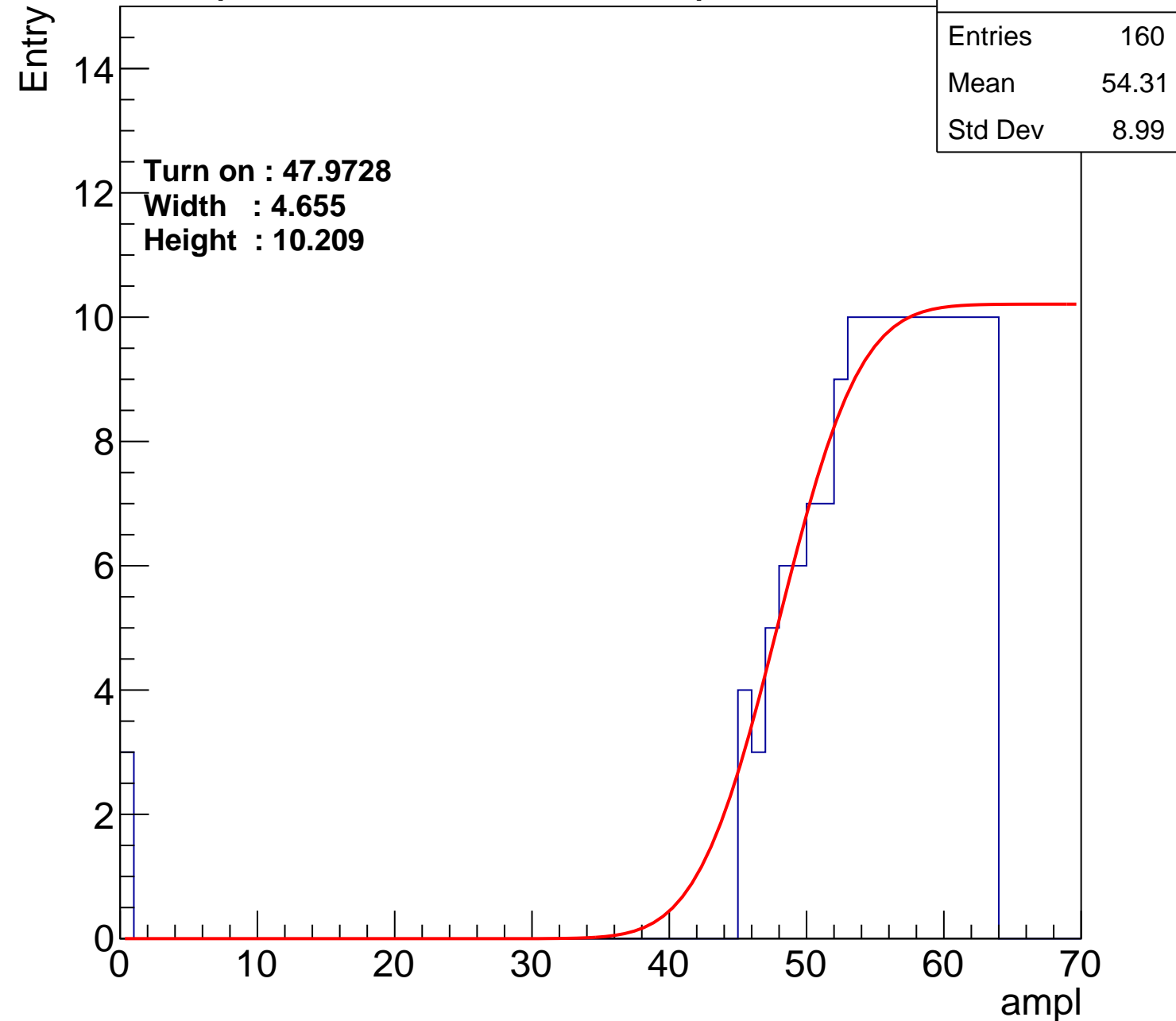
Width : 4.655

Height : 10.209

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch51

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	152
Mean	55.01
Std Dev	7.9

Turn on : 48.9856

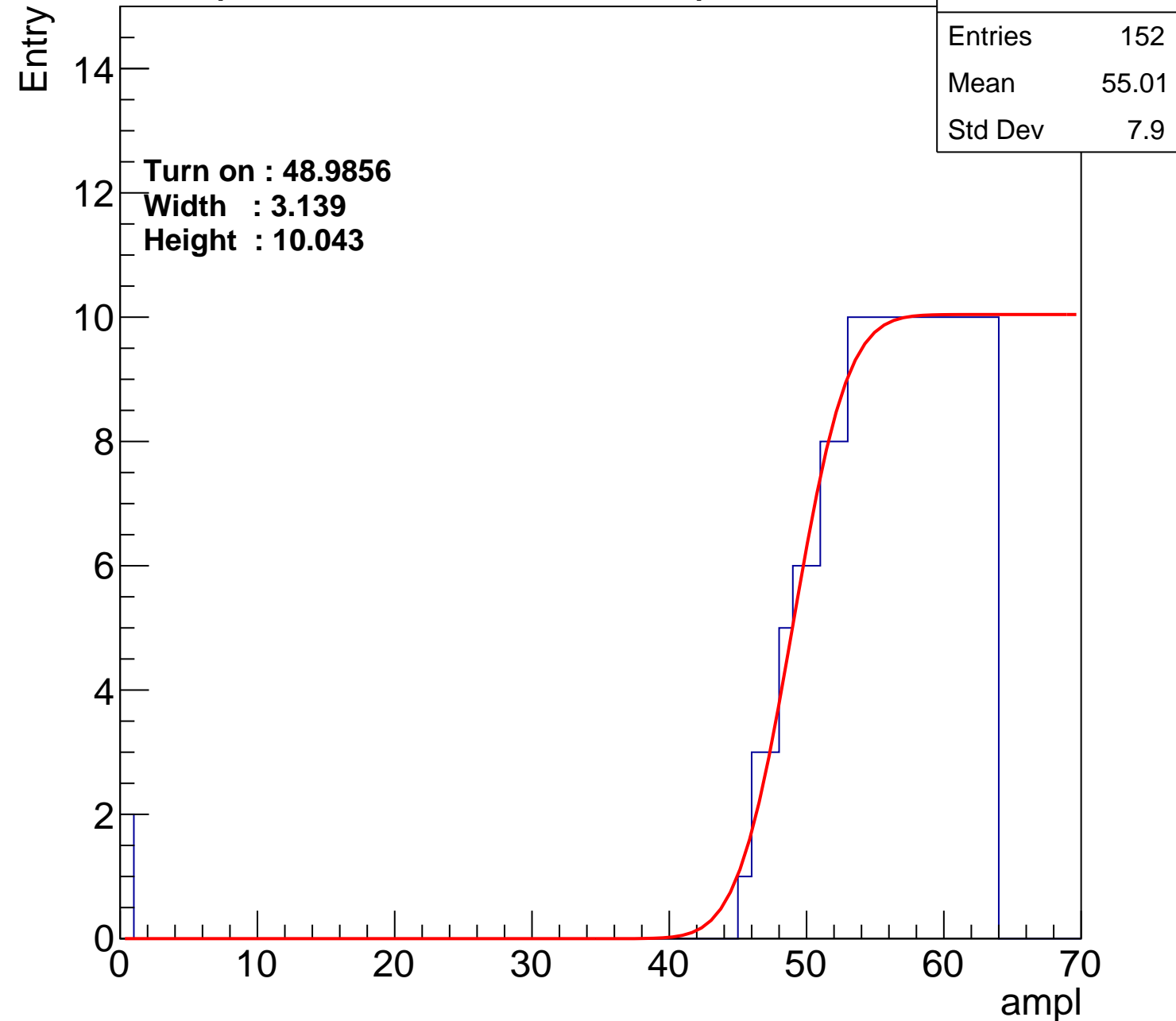
Width : 3.139

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch52

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	57.03
Std Dev	6.565

Turn on : 52.7049

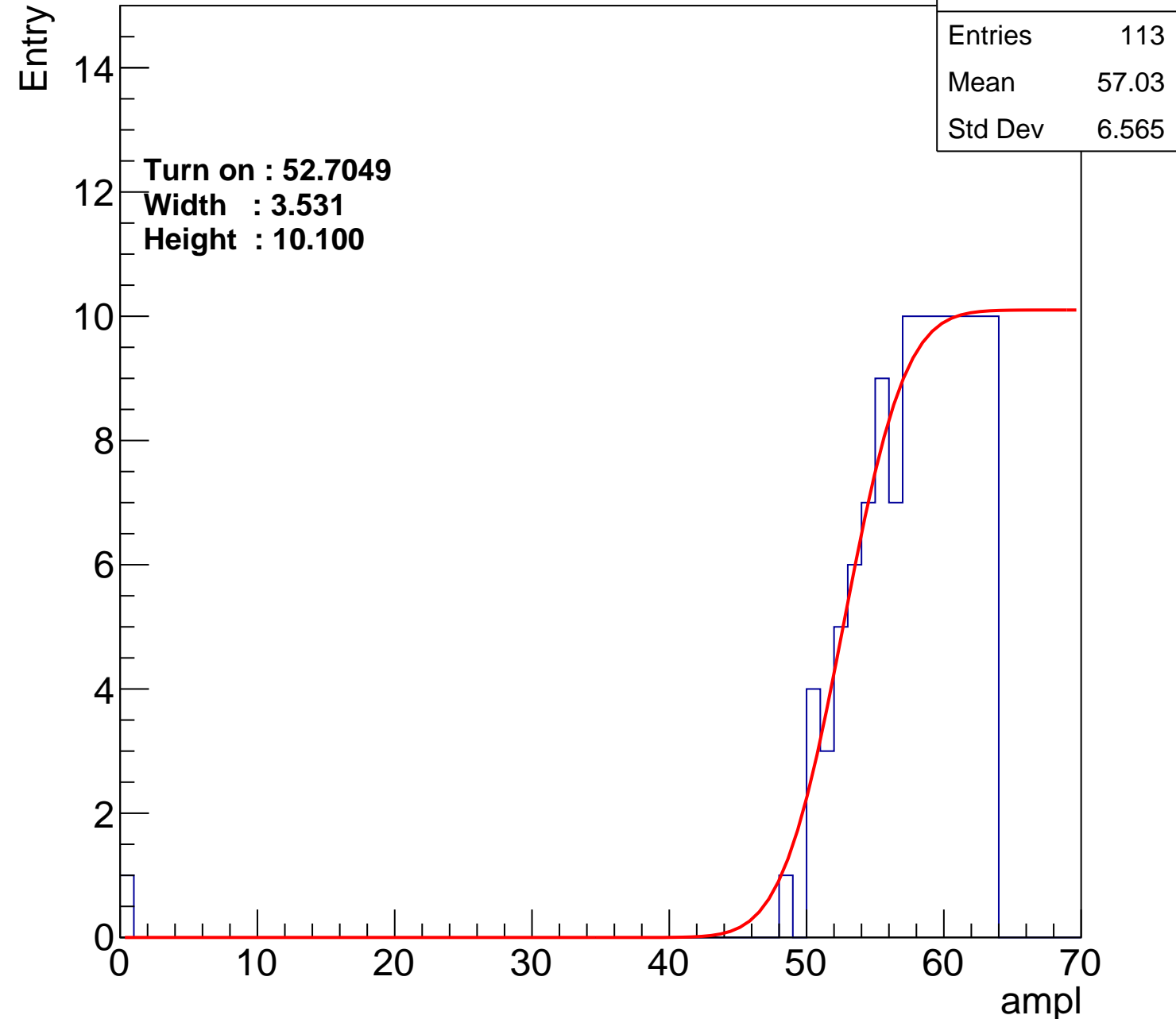
Width : 3.531

Height : 10.100

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch53

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	55.26
Std Dev	10.76

Turn on : 51.3133

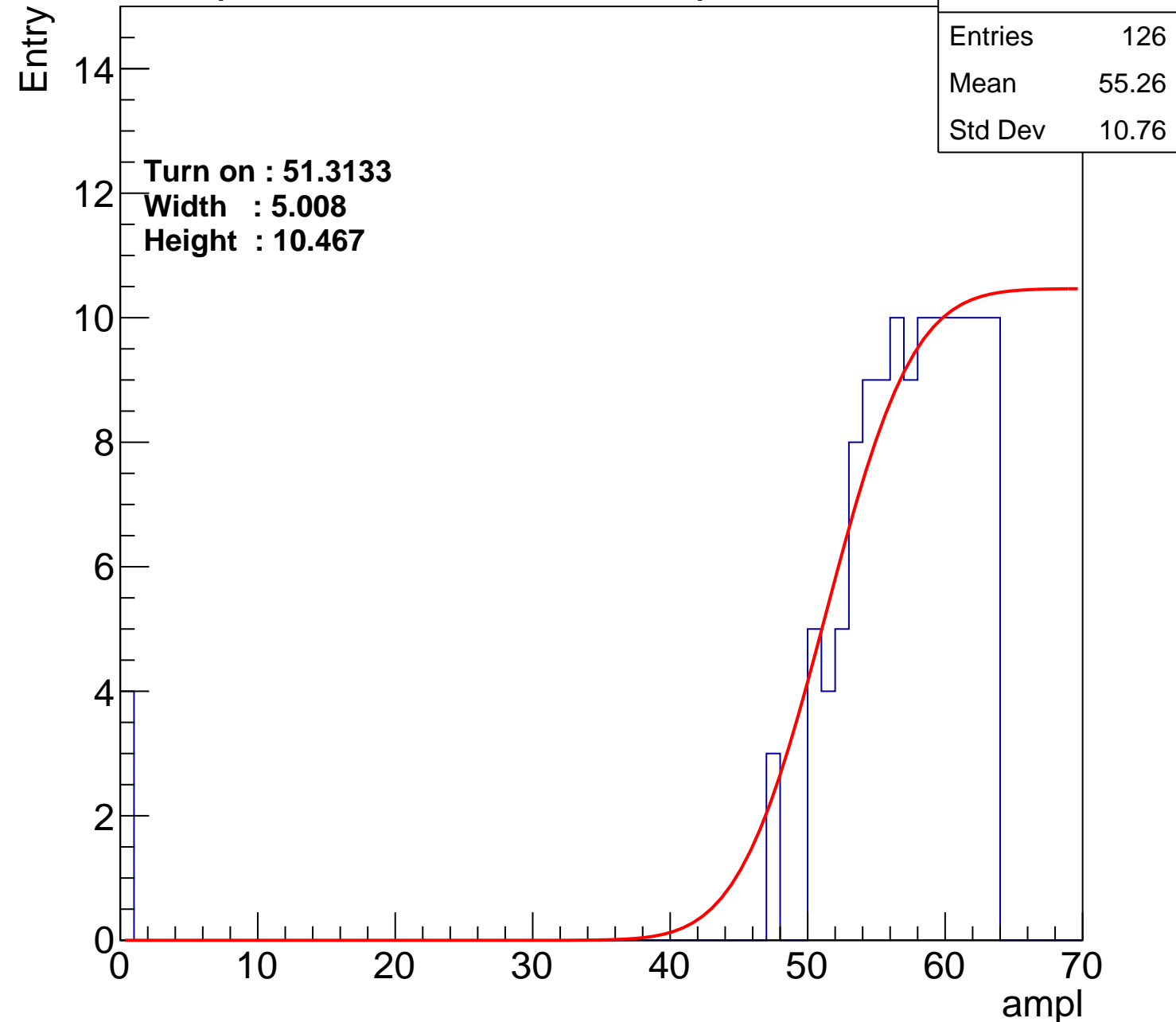
Width : 5.008

Height : 10.467

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch54

calib\_packv5\_040323\_1717.root, FC#2, port C3

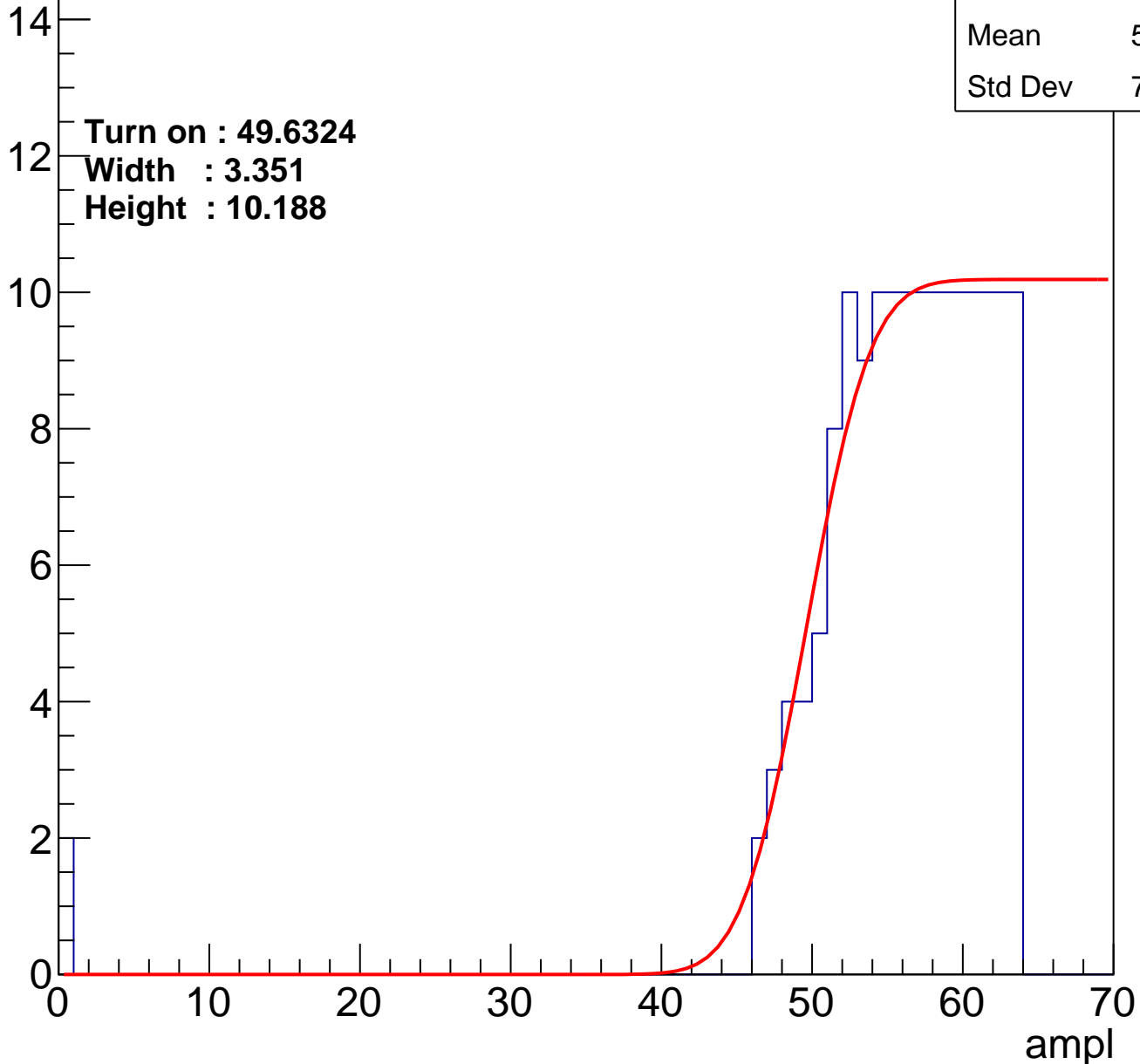
Entries	147
Mean	55.28
Std Dev	7.895

Turn on : 49.6324

Width : 3.351

Height : 10.188

Entry





# B0L103S, U20-ch55

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	57.15
Std Dev	6.571

Turn on : 52.7365

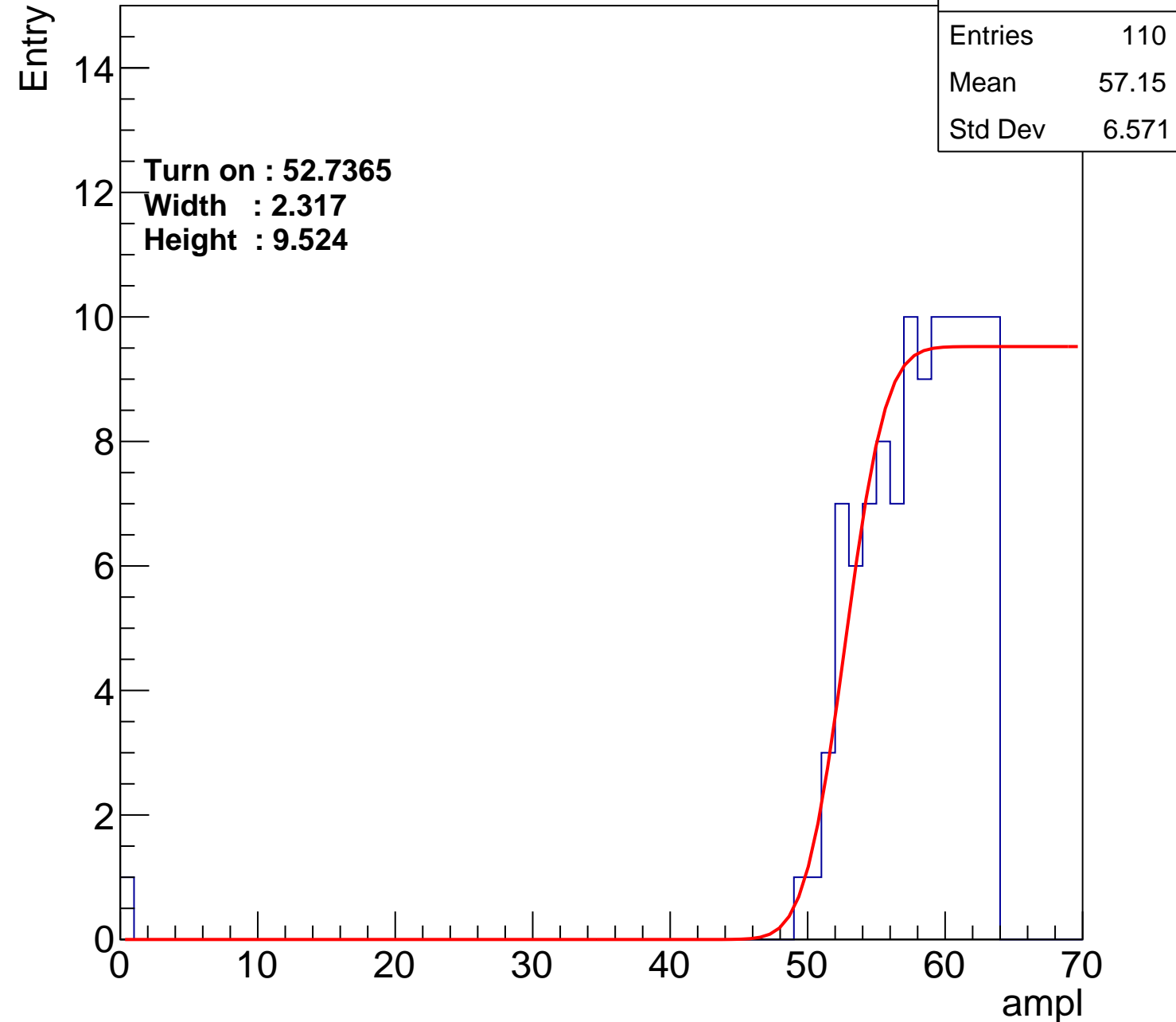
Width : 2.317

Height : 9.524

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch56

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	148
Mean	54.79
Std Dev	9.167

Turn on : 50.1559

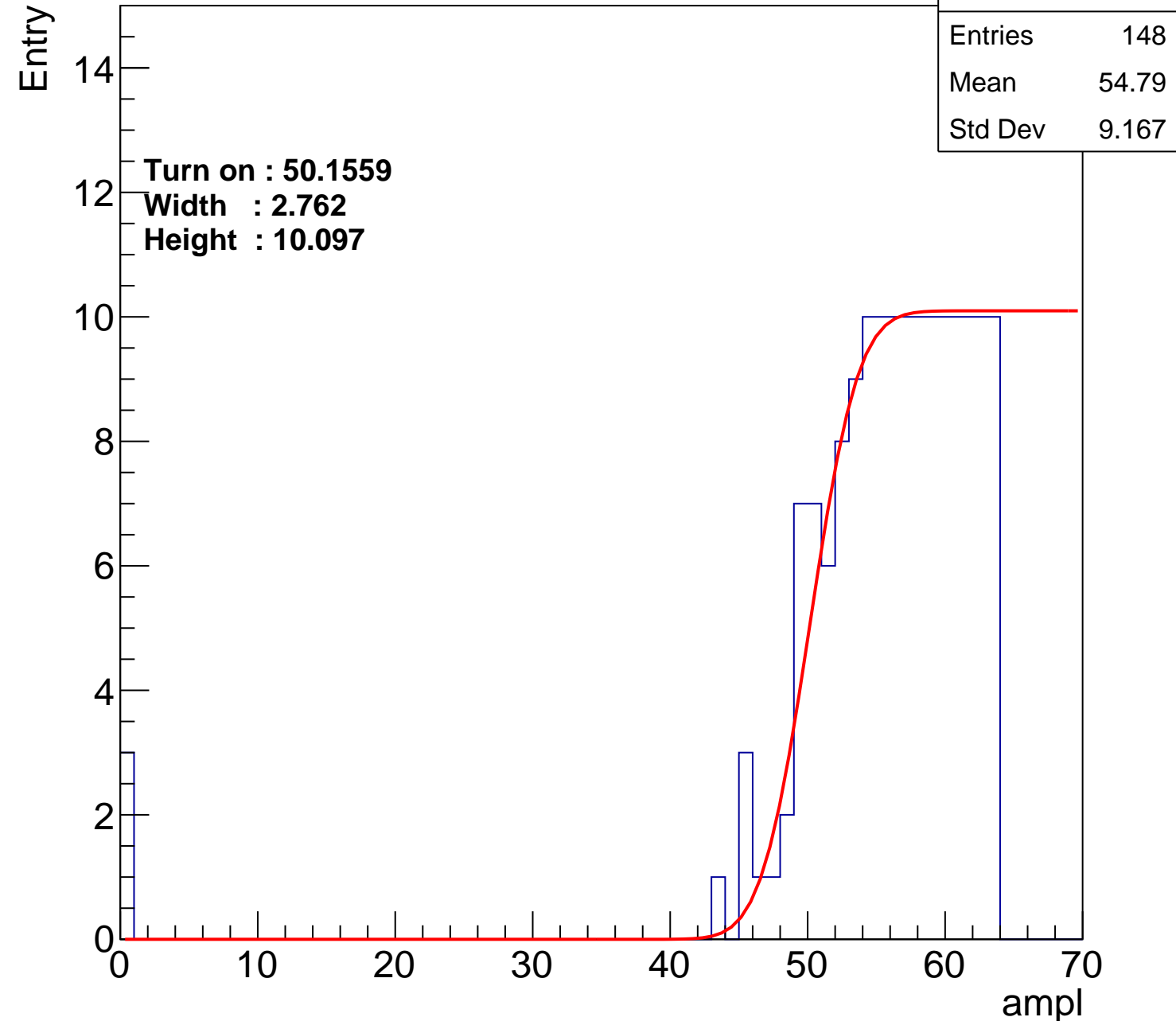
Width : 2.762

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch57

calib\_packv5\_040323\_1717.root, FC#2, port C3

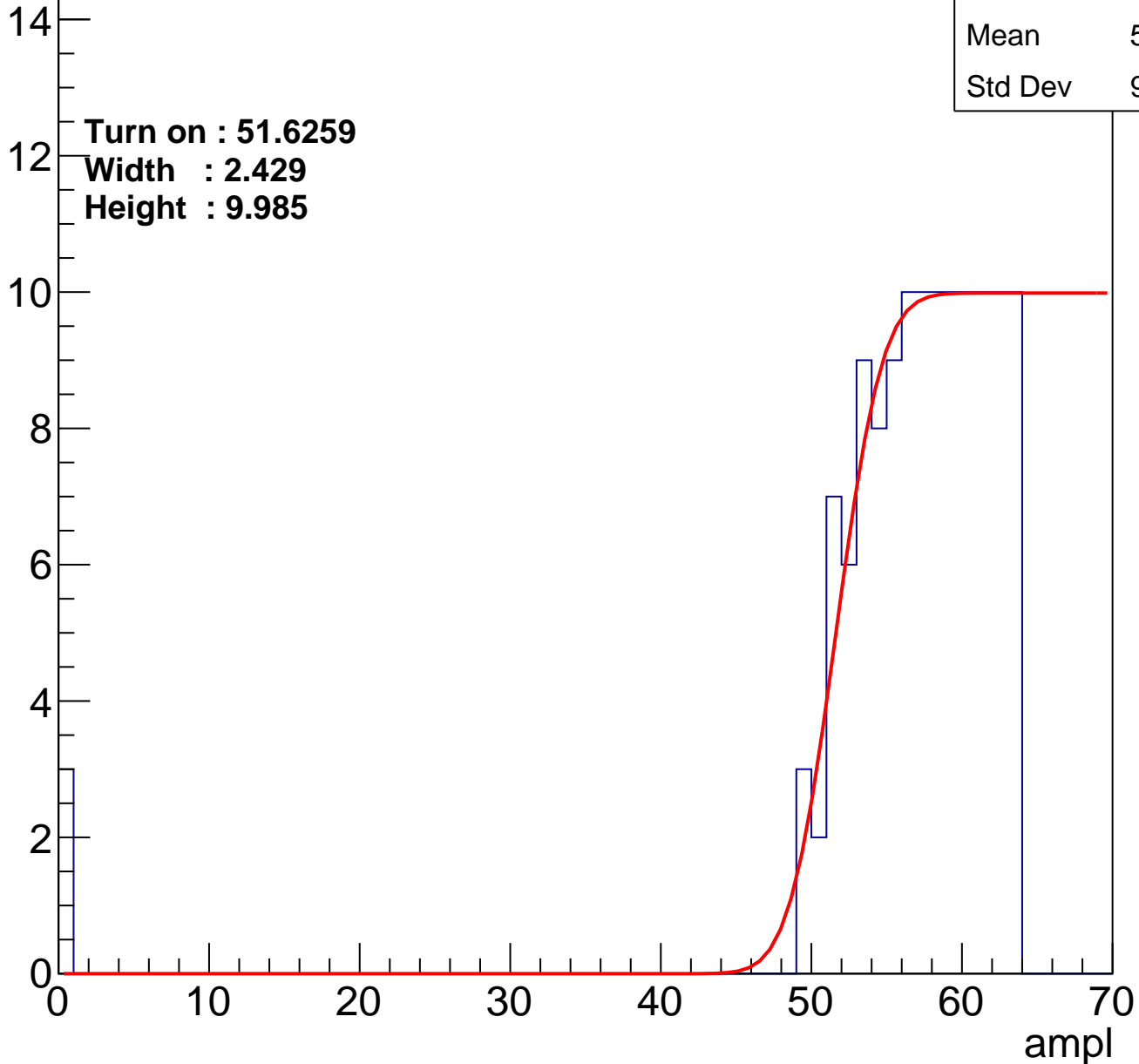
Entries	127
Mean	55.75
Std Dev	9.479

Turn on : 51.6259

Width : 2.429

Height : 9.985

Entry



# B0L103S, U20-ch58

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.83
Std Dev	8.039

Turn on : 50.6922

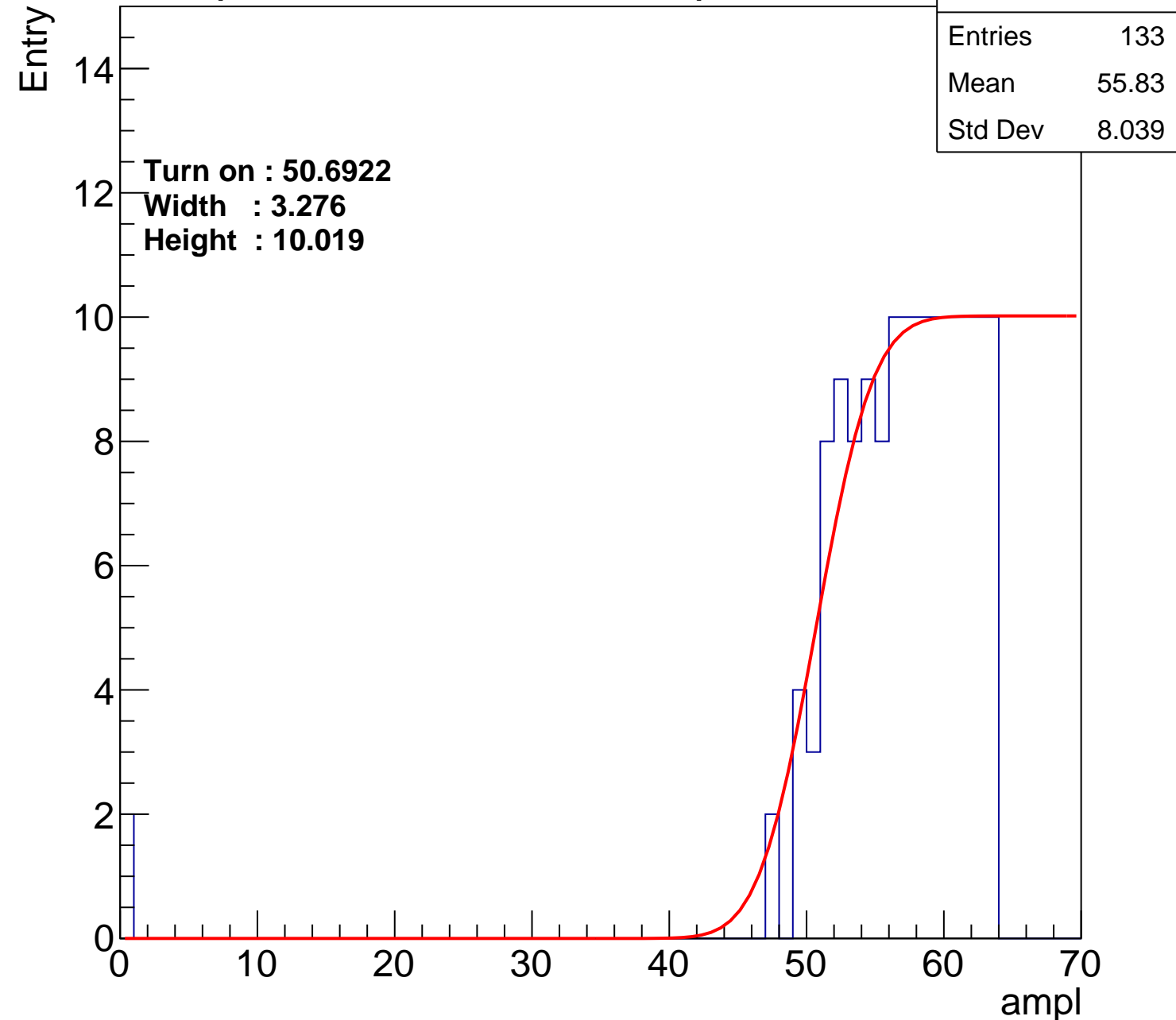
Width : 3.276

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch59

calib\_packv5\_040323\_1717.root, FC#2, port C3

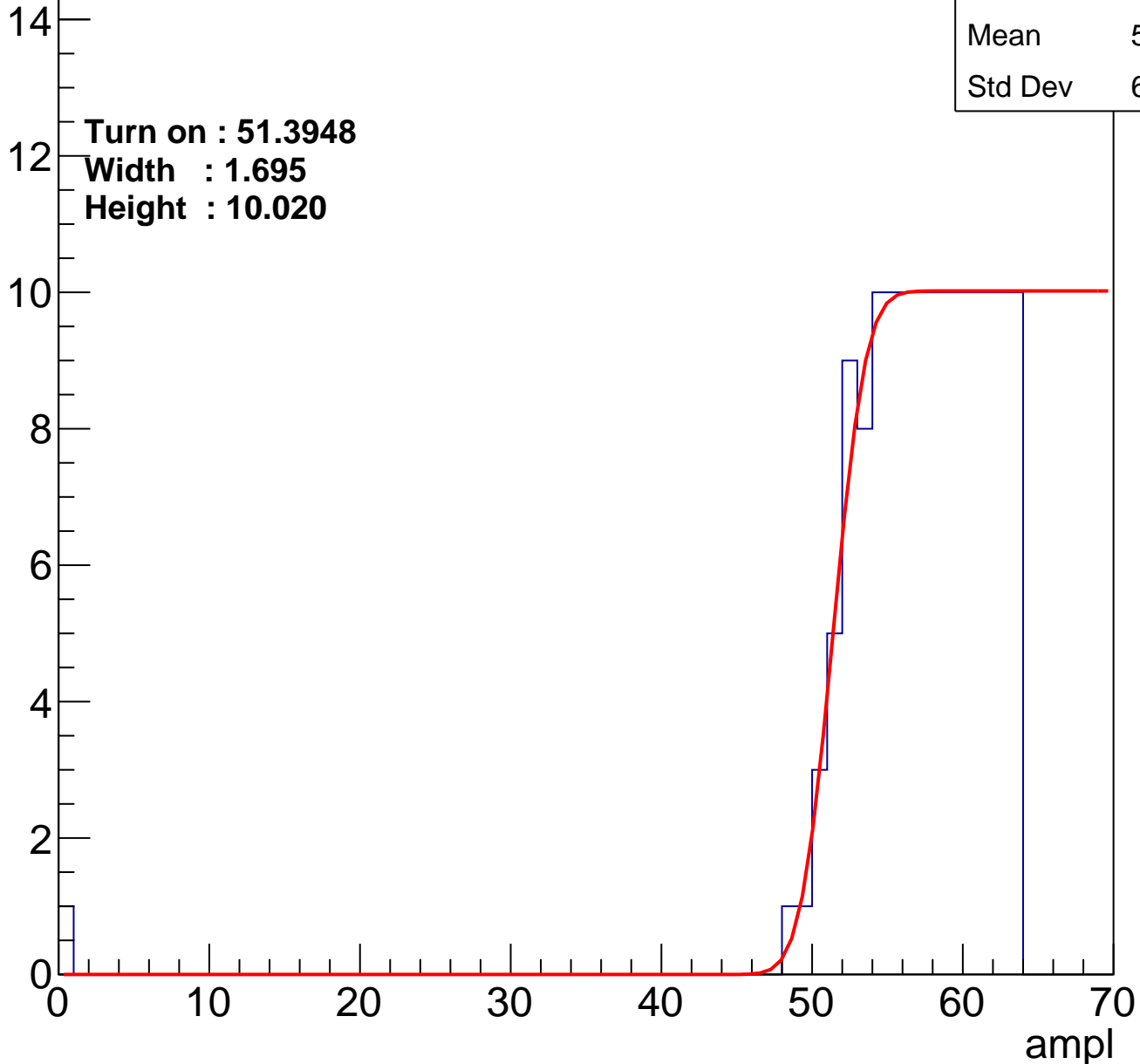
Entries	128
Mean	56.59
Std Dev	6.314

Turn on : 51.3948

Width : 1.695

Height : 10.020

Entry



# B0L103S, U20-ch60

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	54.01
Std Dev	12.06

Turn on : 50.9230

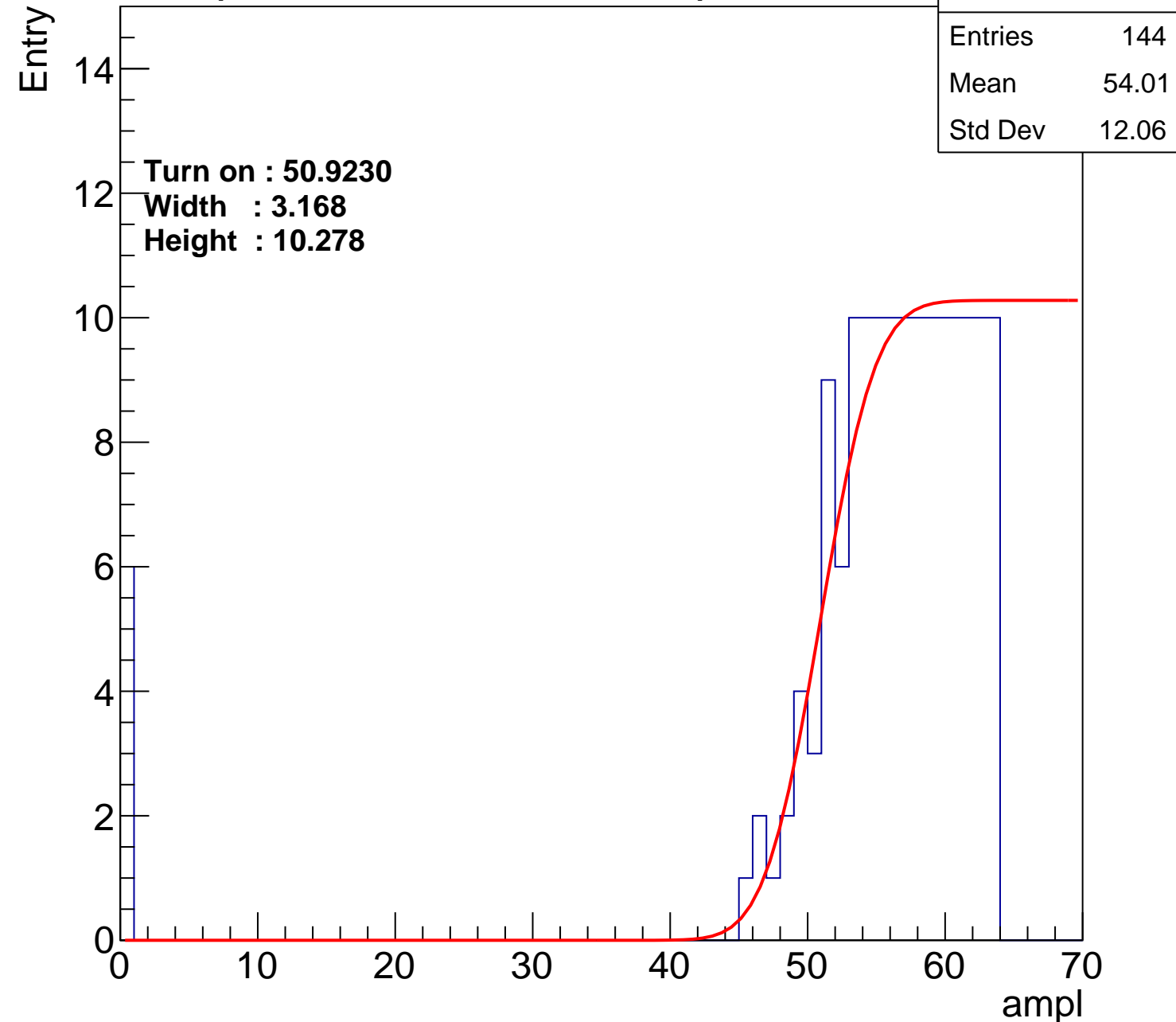
Width : 3.168

Height : 10.278

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch61

calib\_packv5\_040323\_1717.root, FC#2, port C3

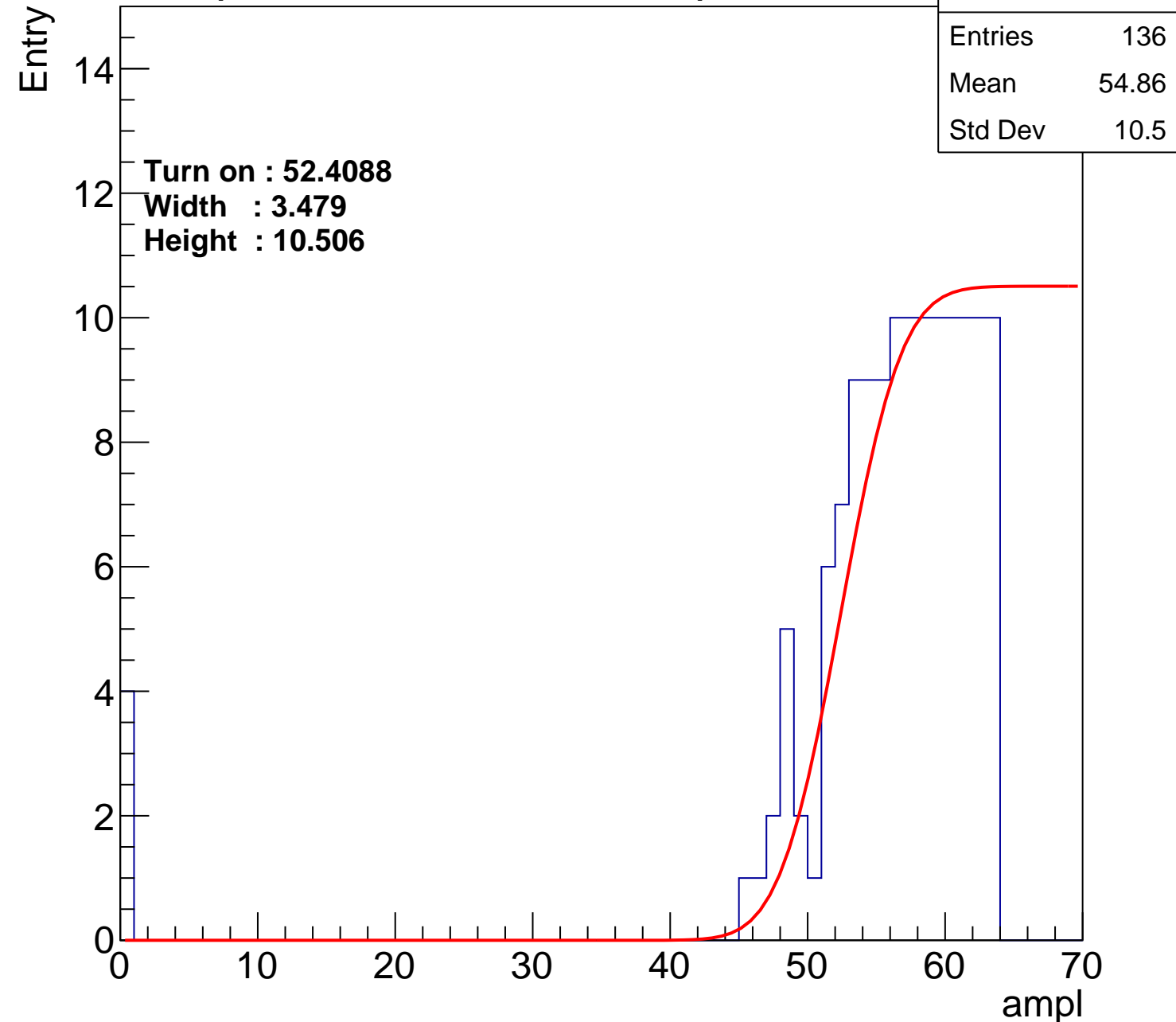
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 52.4088  
Width : 3.479  
Height : 10.506

Entries	136
Mean	54.86
Std Dev	10.5

ampl



# B0L103S, U20-ch62

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.04
Std Dev	10.4

Turn on : 51.0082

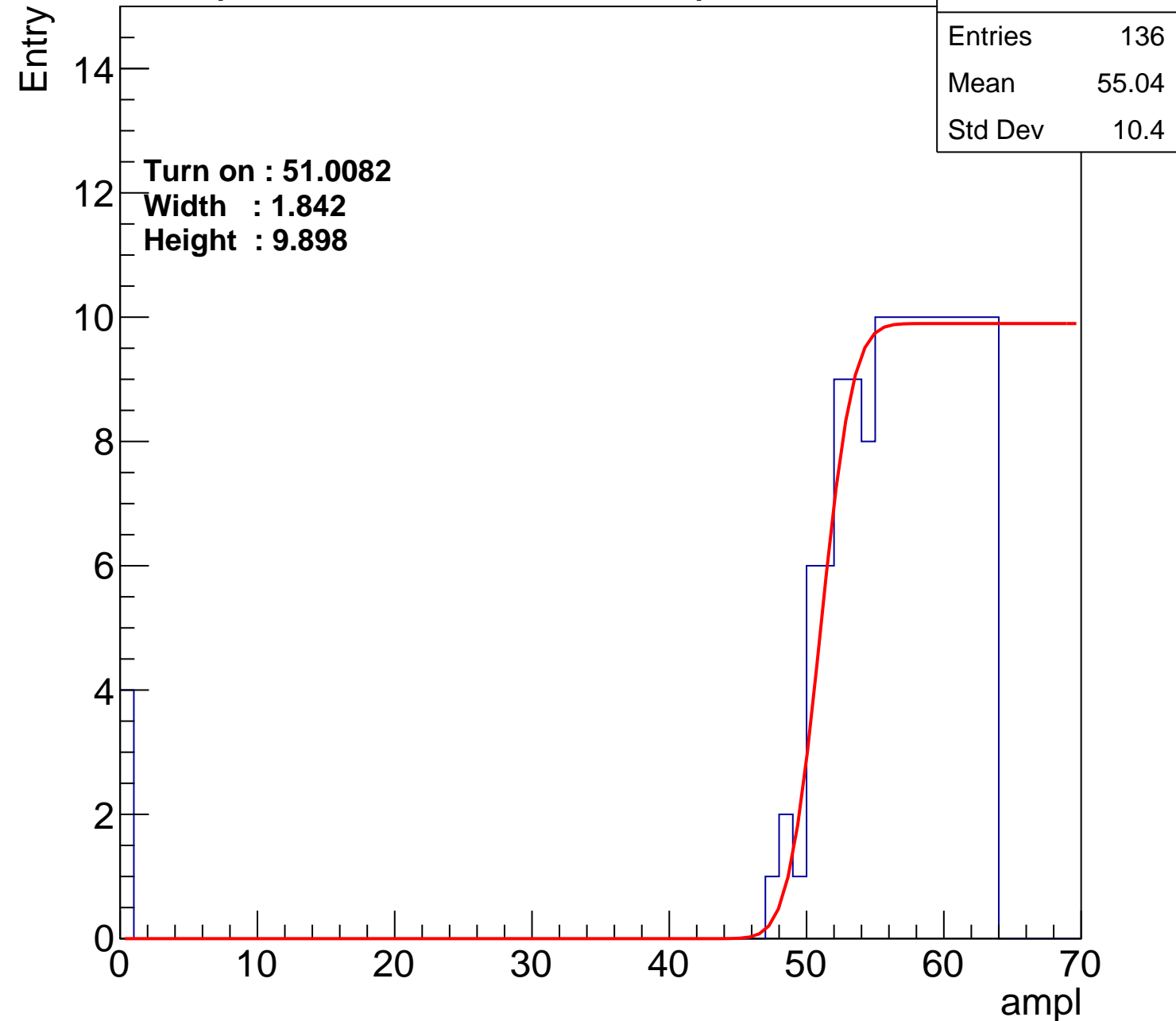
Width : 1.842

Height : 9.898

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch63

calib\_packv5\_040323\_1717.root, FC#2, port C3

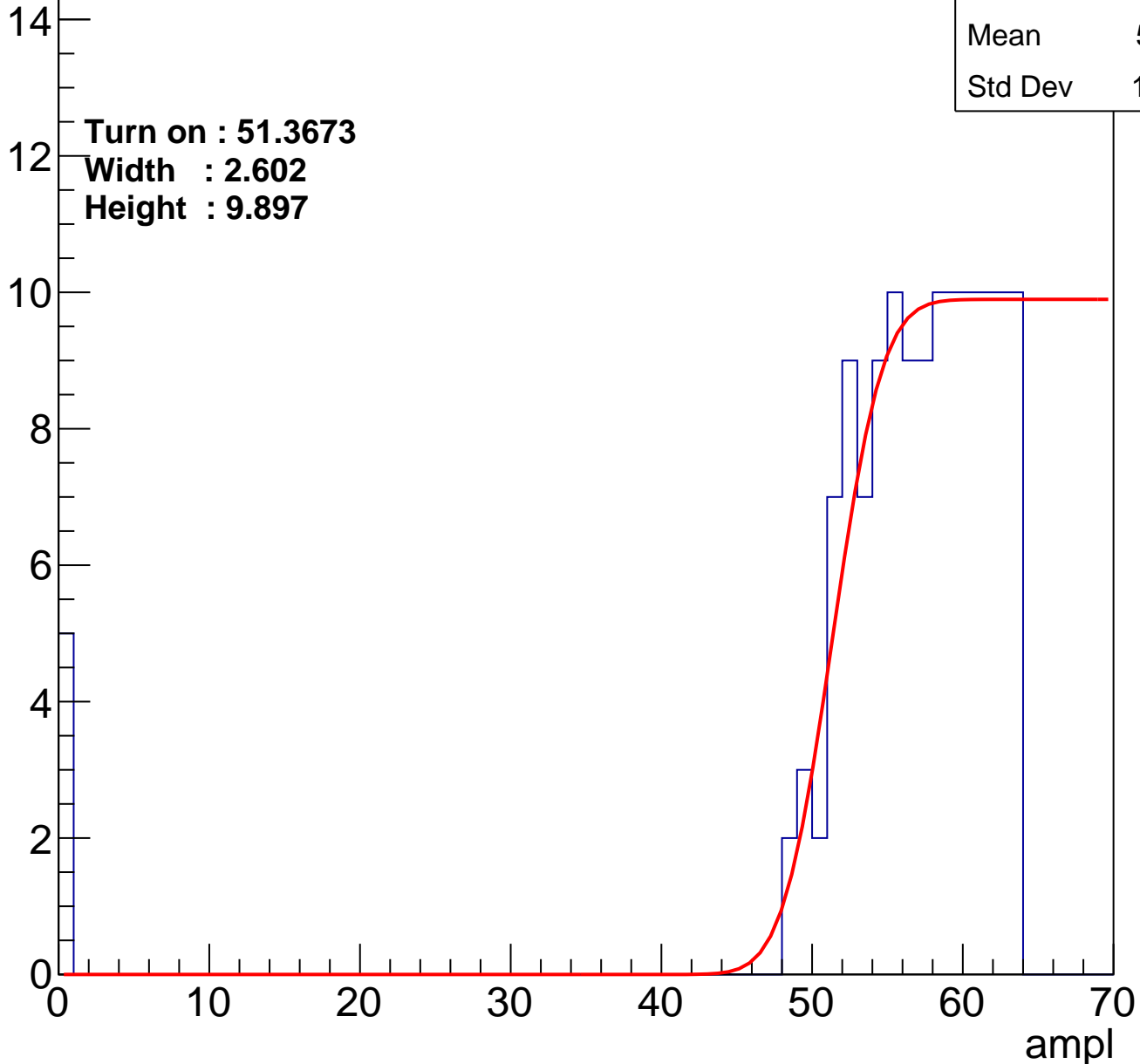
Entries	132
Mean	54.71
Std Dev	11.56

Turn on : 51.3673

Width : 2.602

Height : 9.897

Entry



# B0L103S, U20-ch64

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	55.28
Std Dev	7.928

Turn on : 50.9874

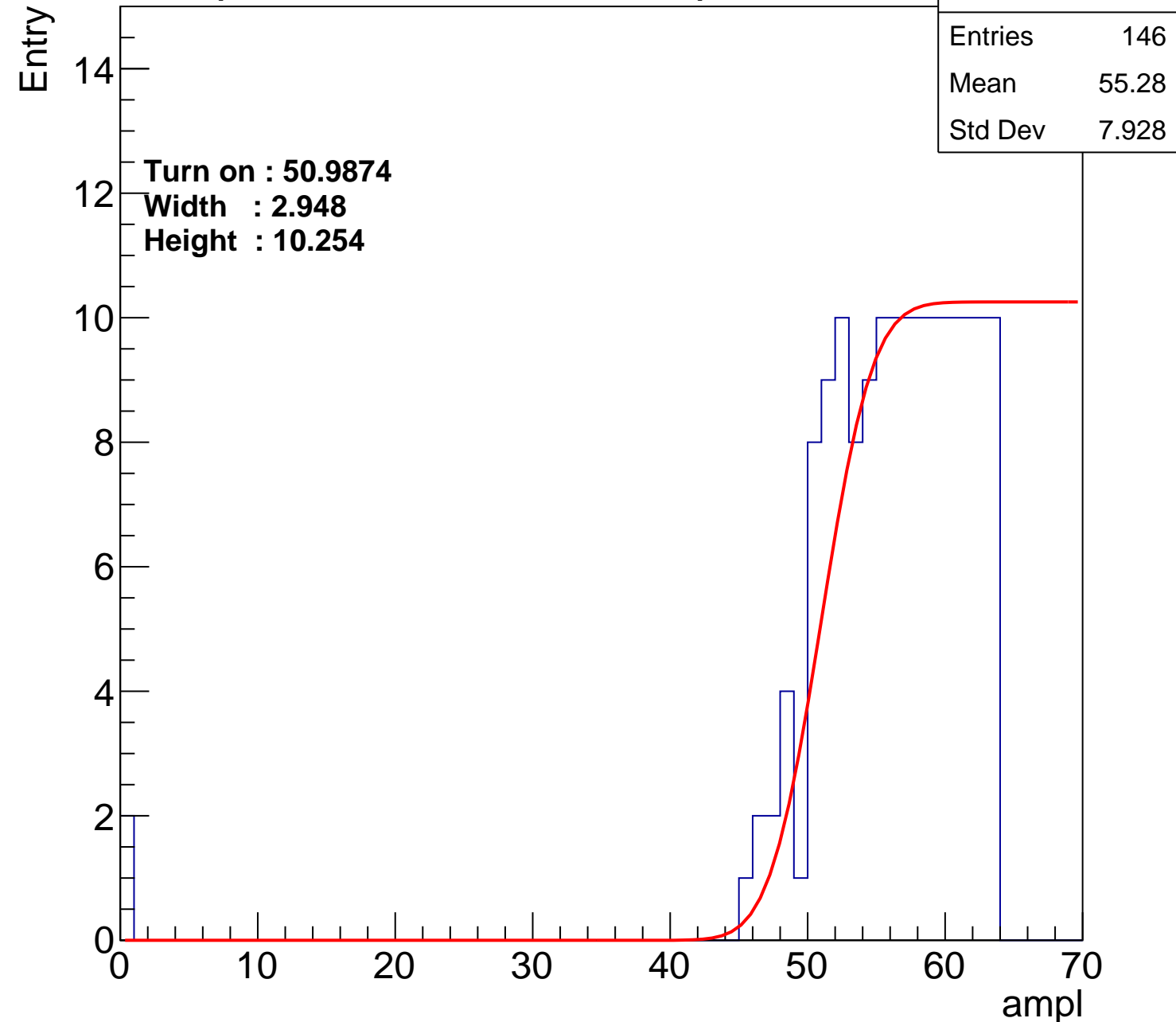
Width : 2.948

Height : 10.254

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch65

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

Turn on : 51.0700

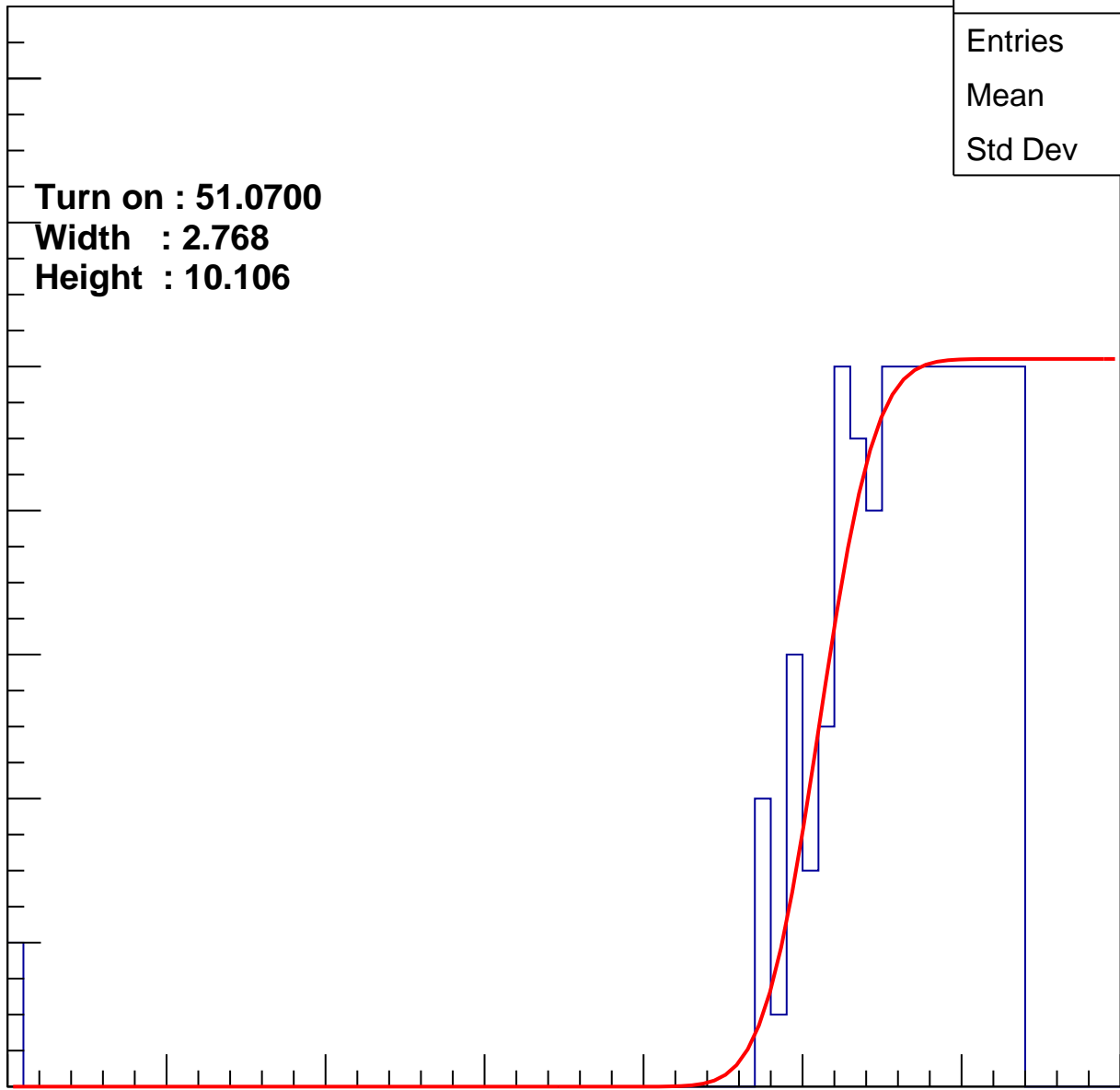
Width : 2.768

Height : 10.106

Entries	138
Mean	55.61
Std Dev	8.008

ampl

0 10 20 30 40 50 60 70



# B0L103S, U20-ch66

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	56.93
Std Dev	6.516

Turn on : 53.0370

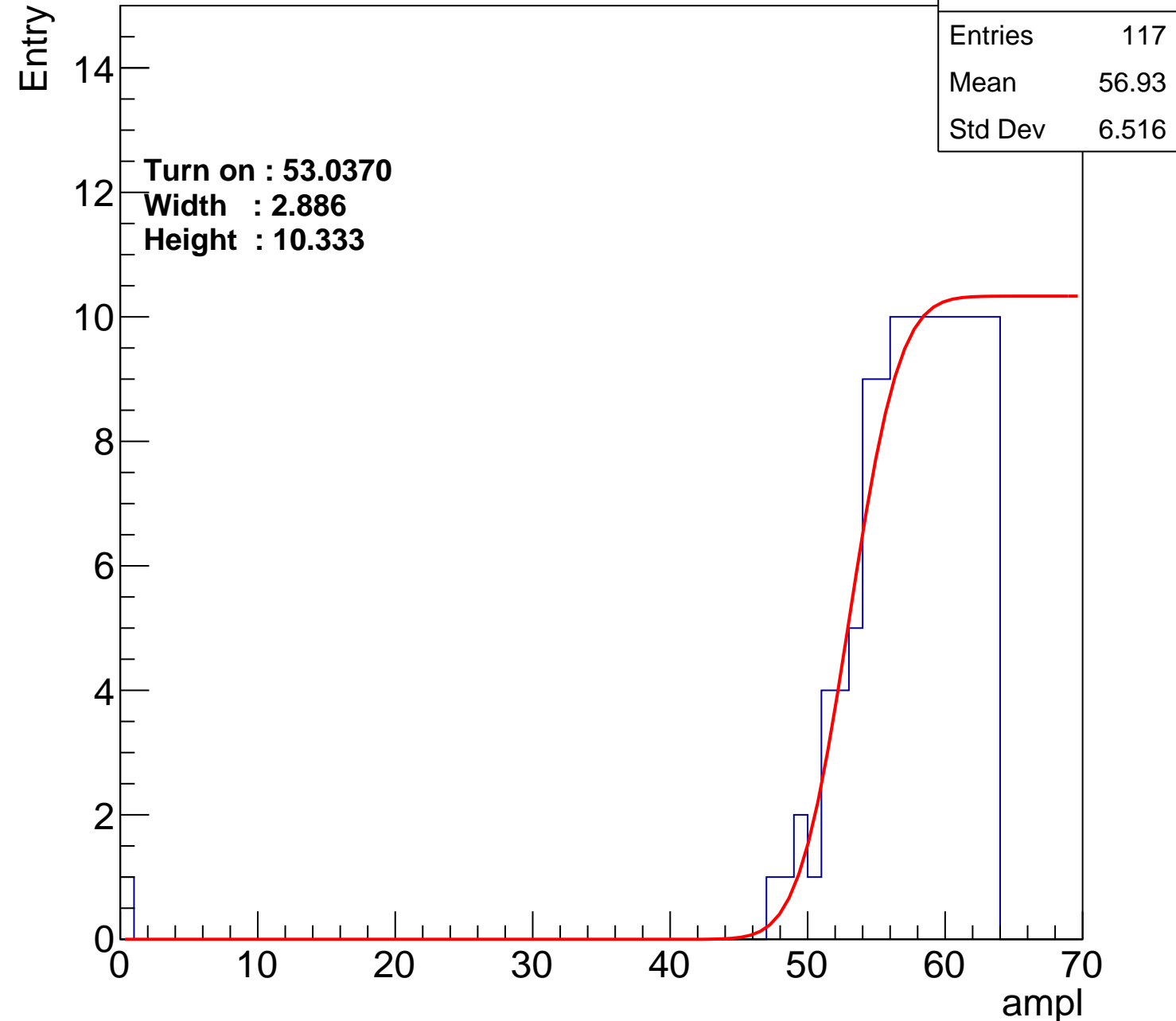
Width : 2.886

Height : 10.333

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch67

calib\_packv5\_040323\_1717.root, FC#2, port C3

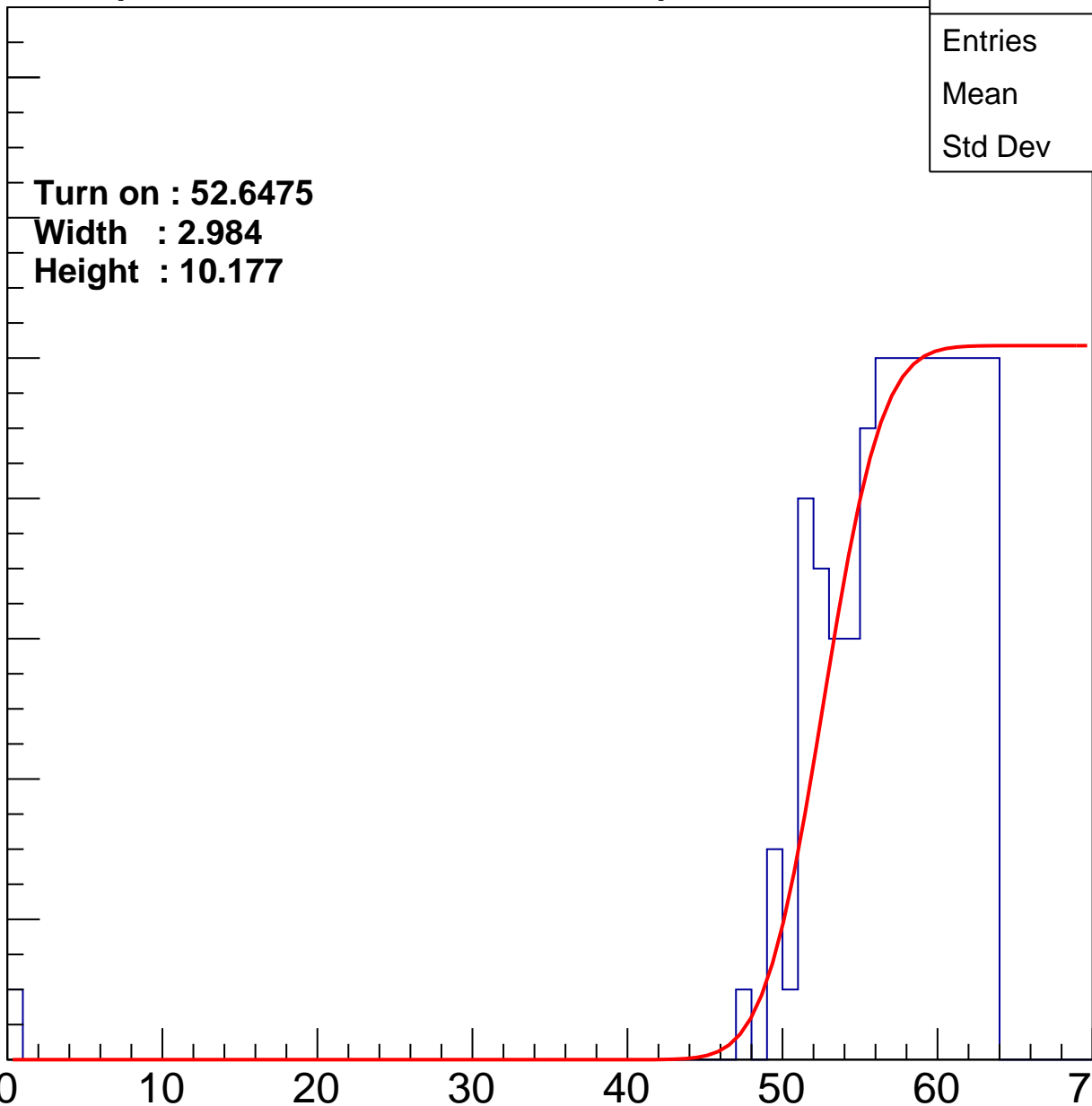
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 52.6475  
Width : 2.984  
Height : 10.177

Entries	122
Mean	56.66
Std Dev	6.494

ampl



# B0L103S, U20-ch68

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	54.78
Std Dev	13.14

Turn on : 53.3346

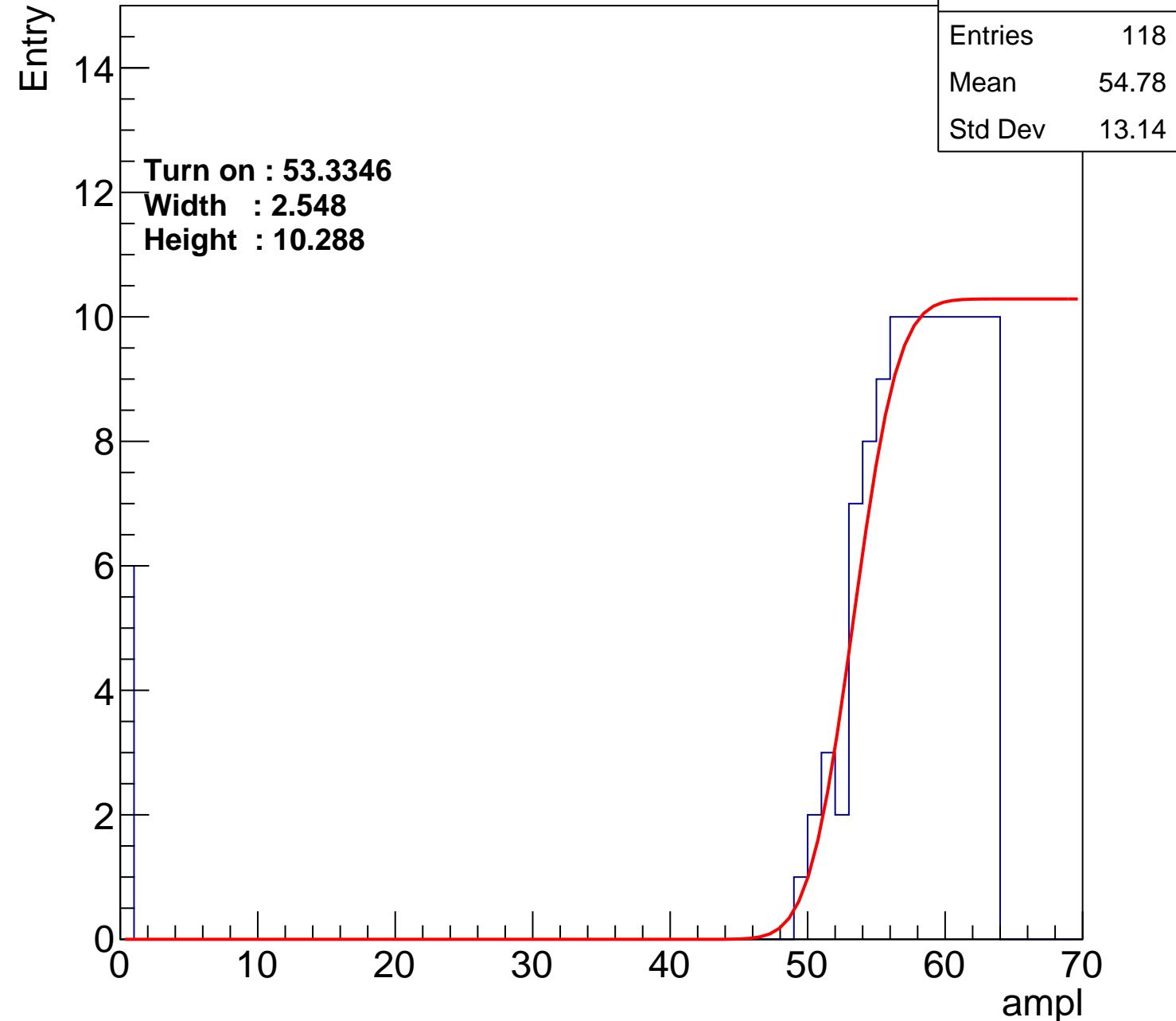
Width : 2.548

Height : 10.288

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch69

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.59
Std Dev	9.52

Turn on : 51.5641

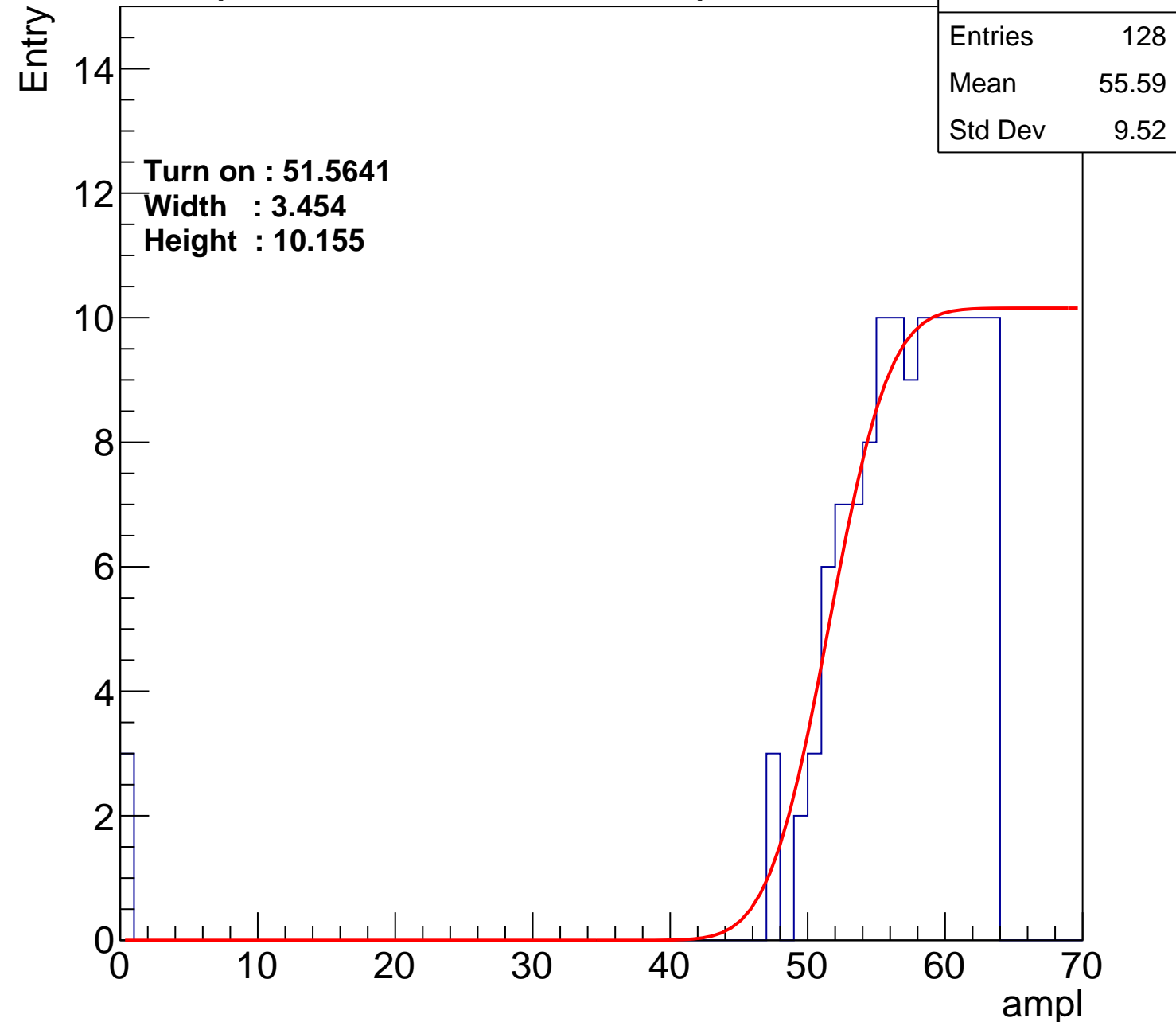
Width : 3.454

Height : 10.155

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch70

calib\_packv5\_040323\_1717.root, FC#2, port C3

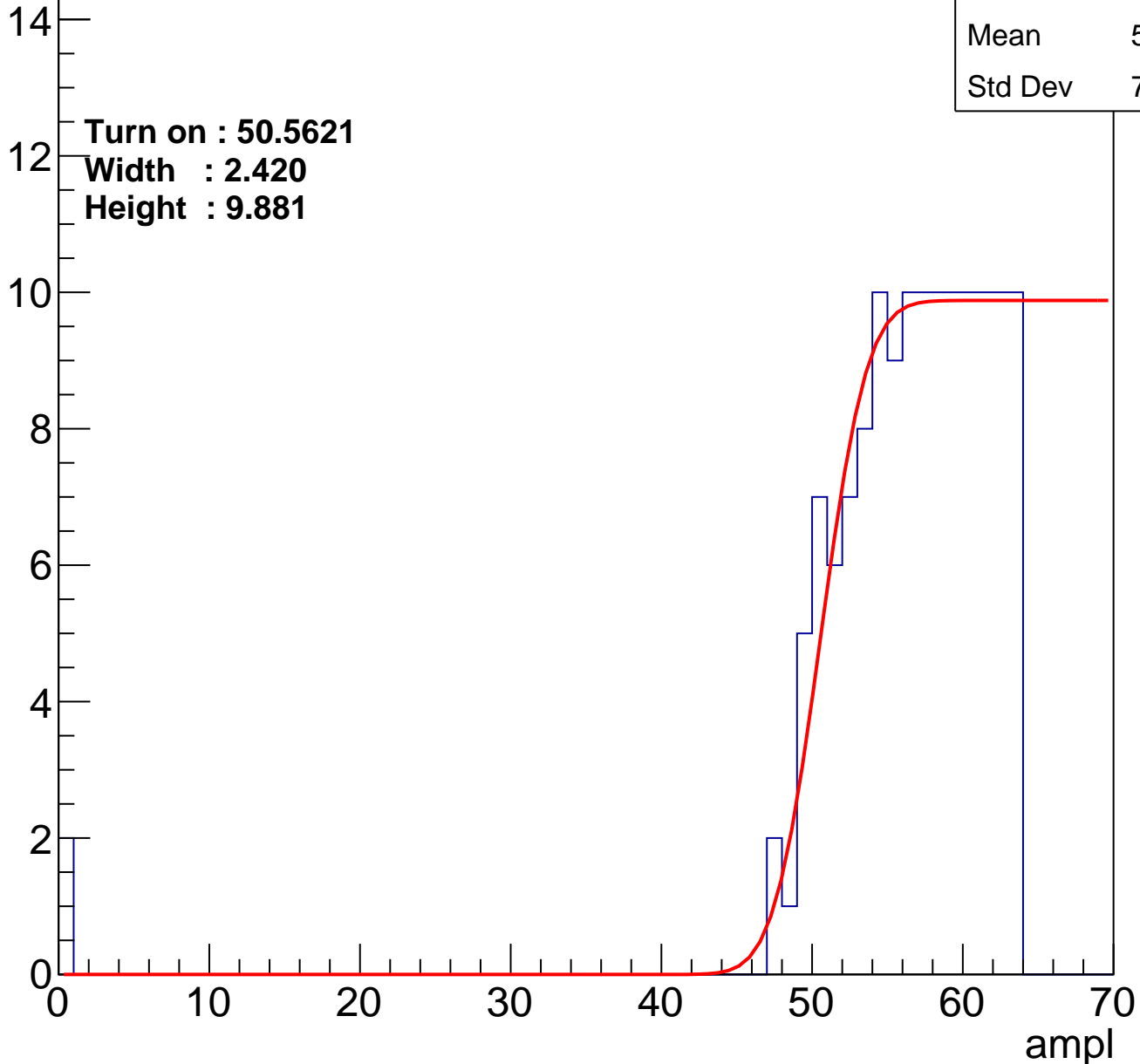
Entries	137
Mean	55.66
Std Dev	7.998

Turn on : 50.5621

Width : 2.420

Height : 9.881

Entry





# B0L103S, U20-ch71

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	56.23
Std Dev	6.508

Turn on : 51.2609

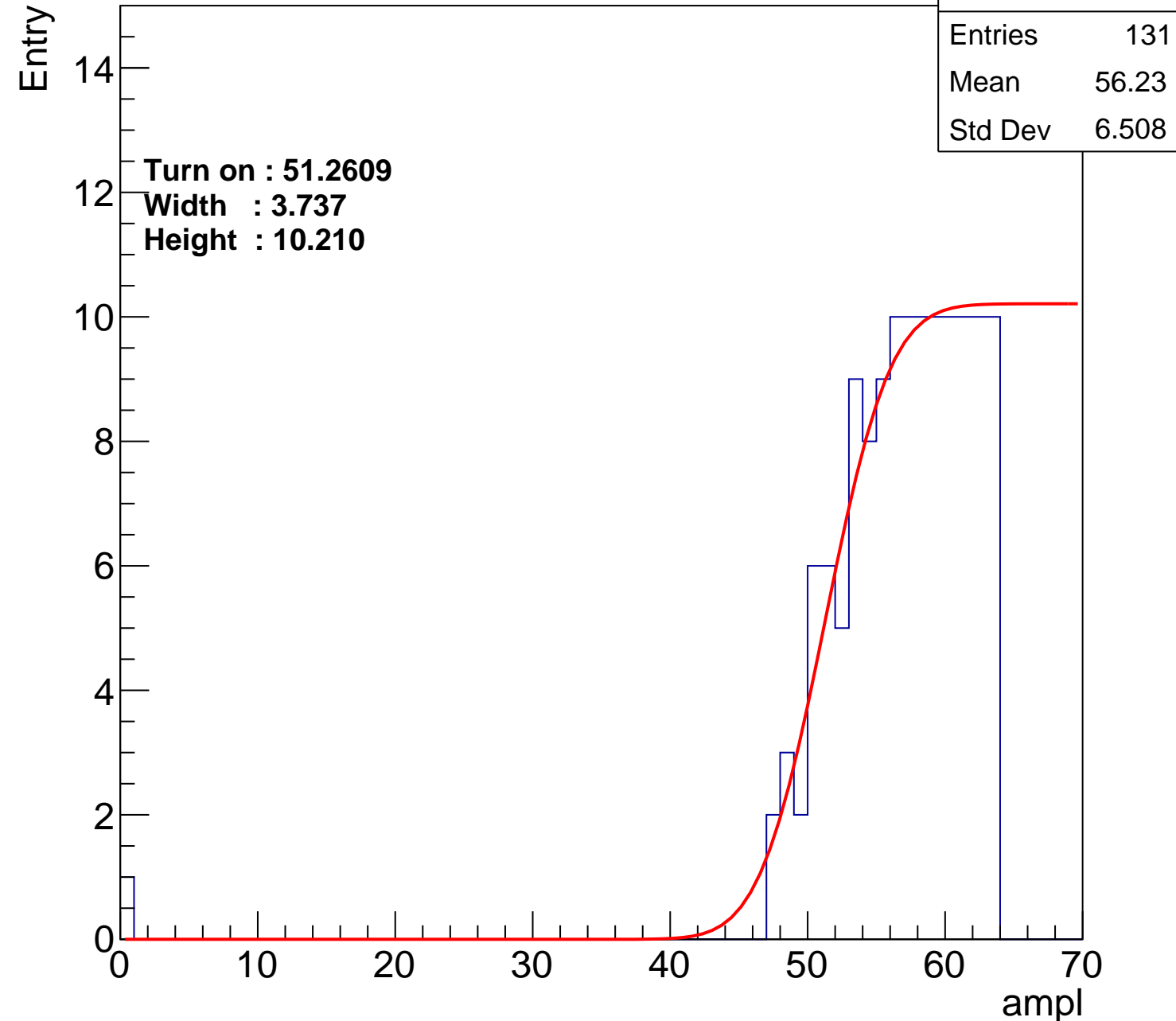
Width : 3.737

Height : 10.210

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch72

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	56.14
Std Dev	6.514

Turn on : 51.1362

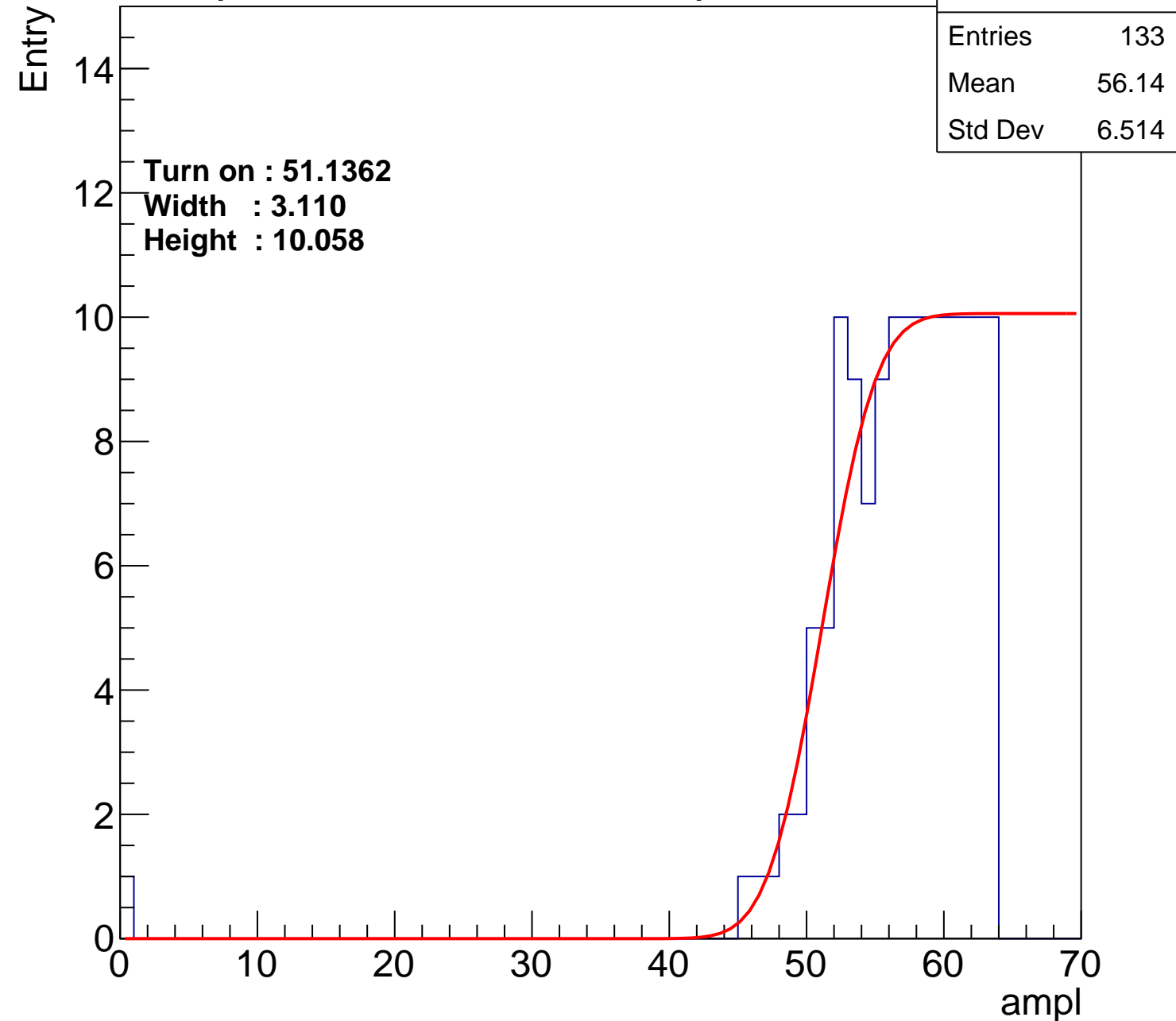
Width : 3.110

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch73

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.02
Std Dev	10.58

Turn on : 51.0861

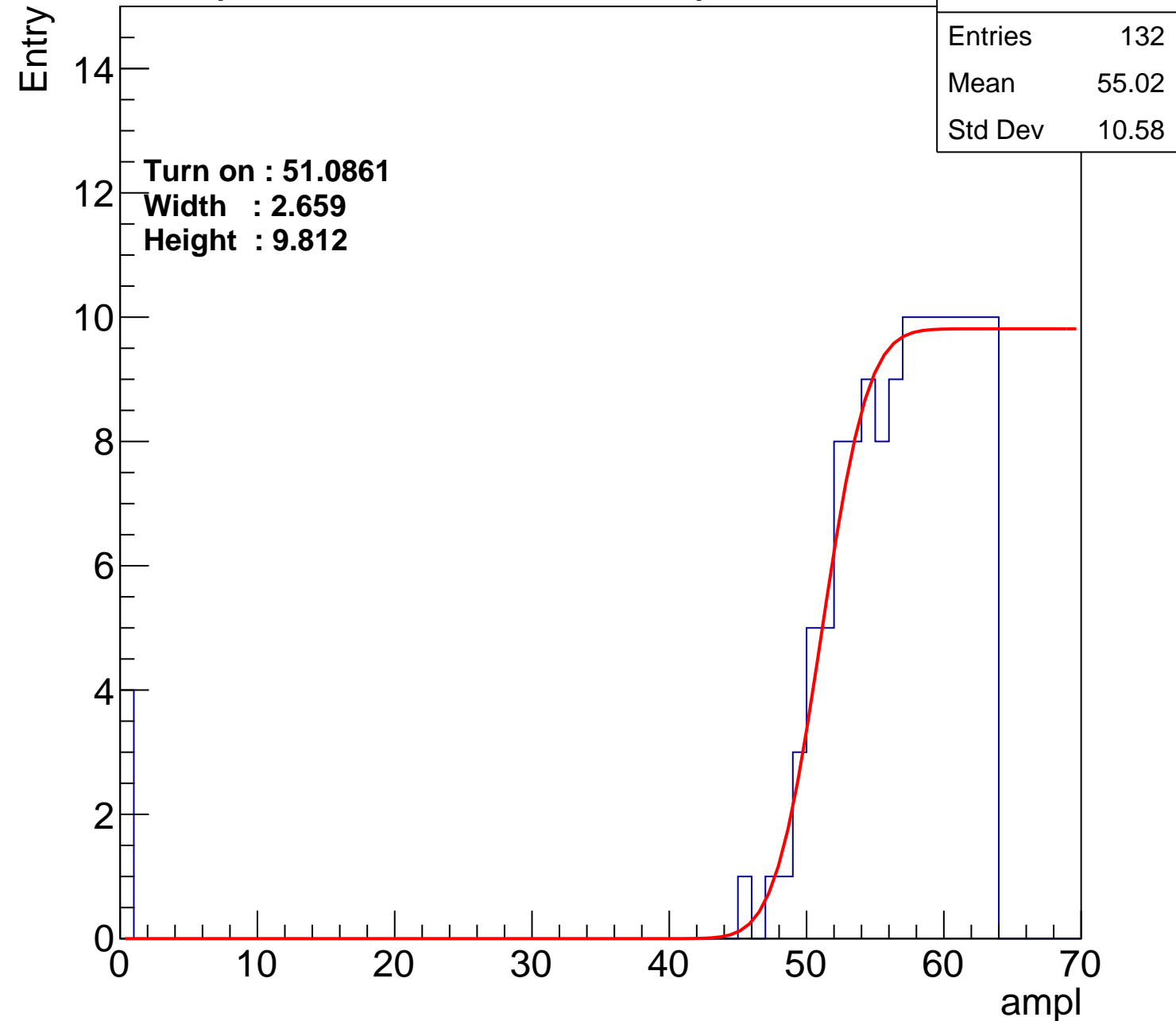
Width : 2.659

Height : 9.812

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch74

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.88
Std Dev	8.147

Turn on : 50.5876

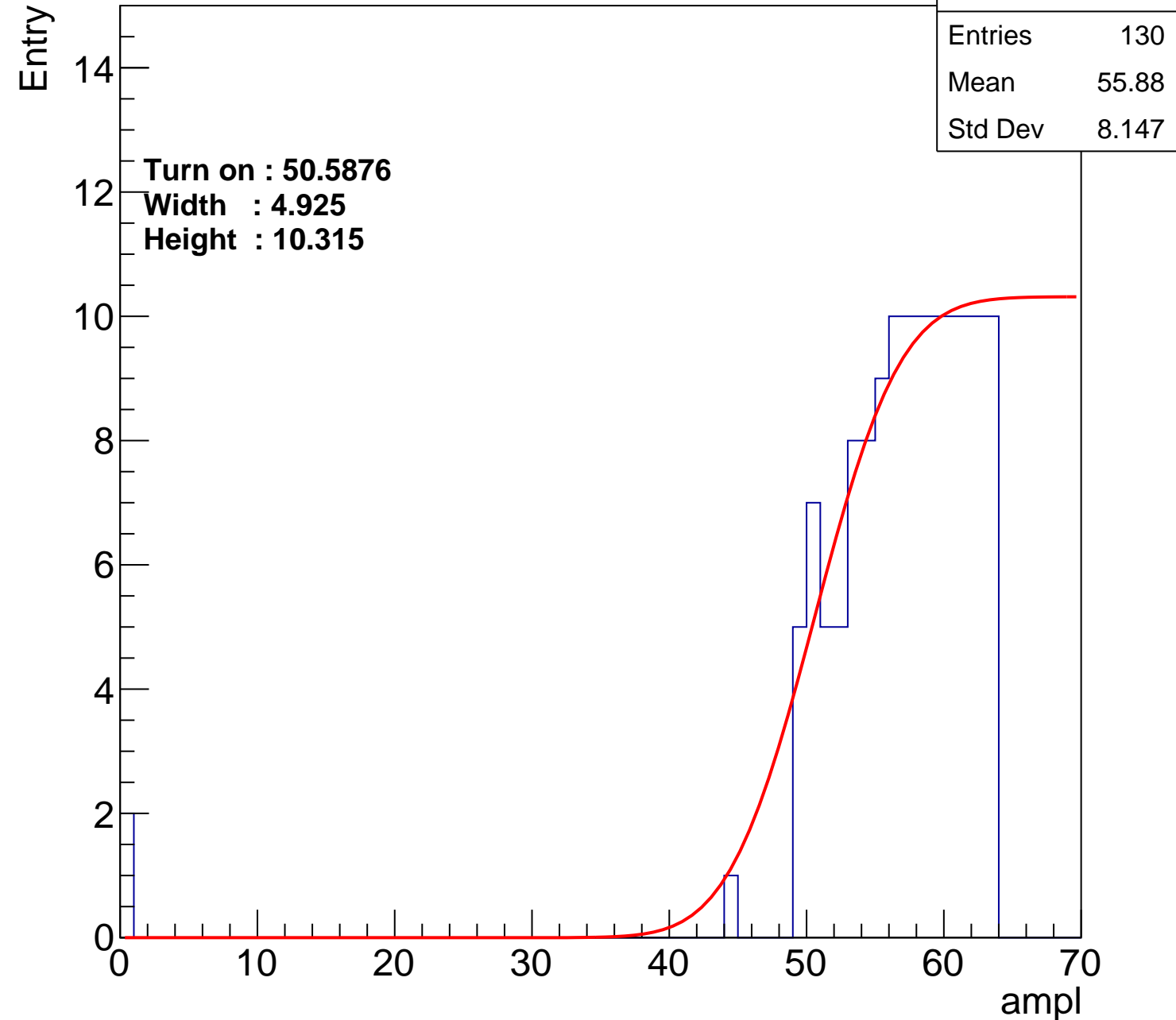
Width : 4.925

Height : 10.315

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch75

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	54.16
Std Dev	13.98

Turn on : 53.0505

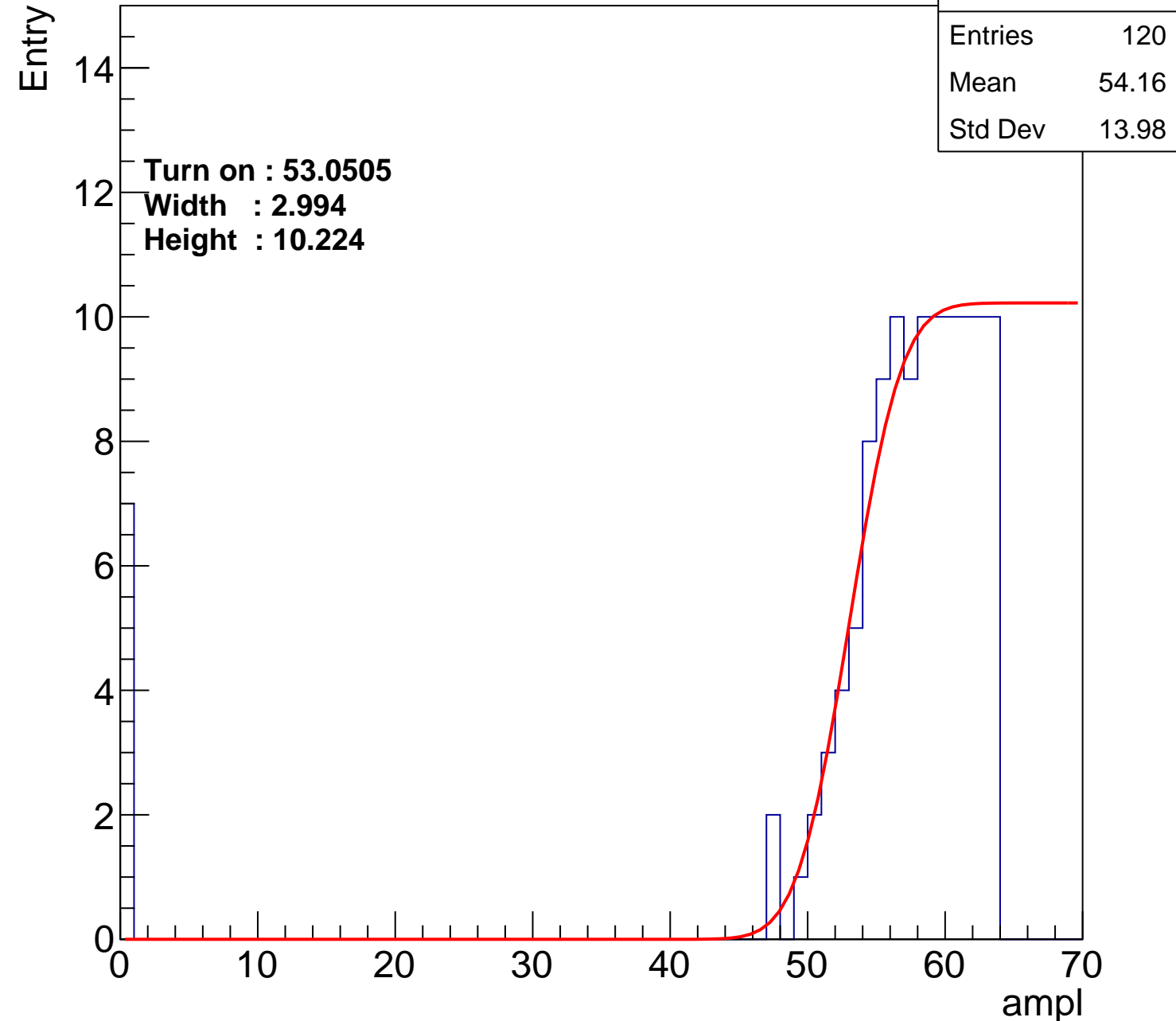
Width : 2.994

Height : 10.224

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch76

calib\_packv5\_040323\_1717.root, FC#2, port C3

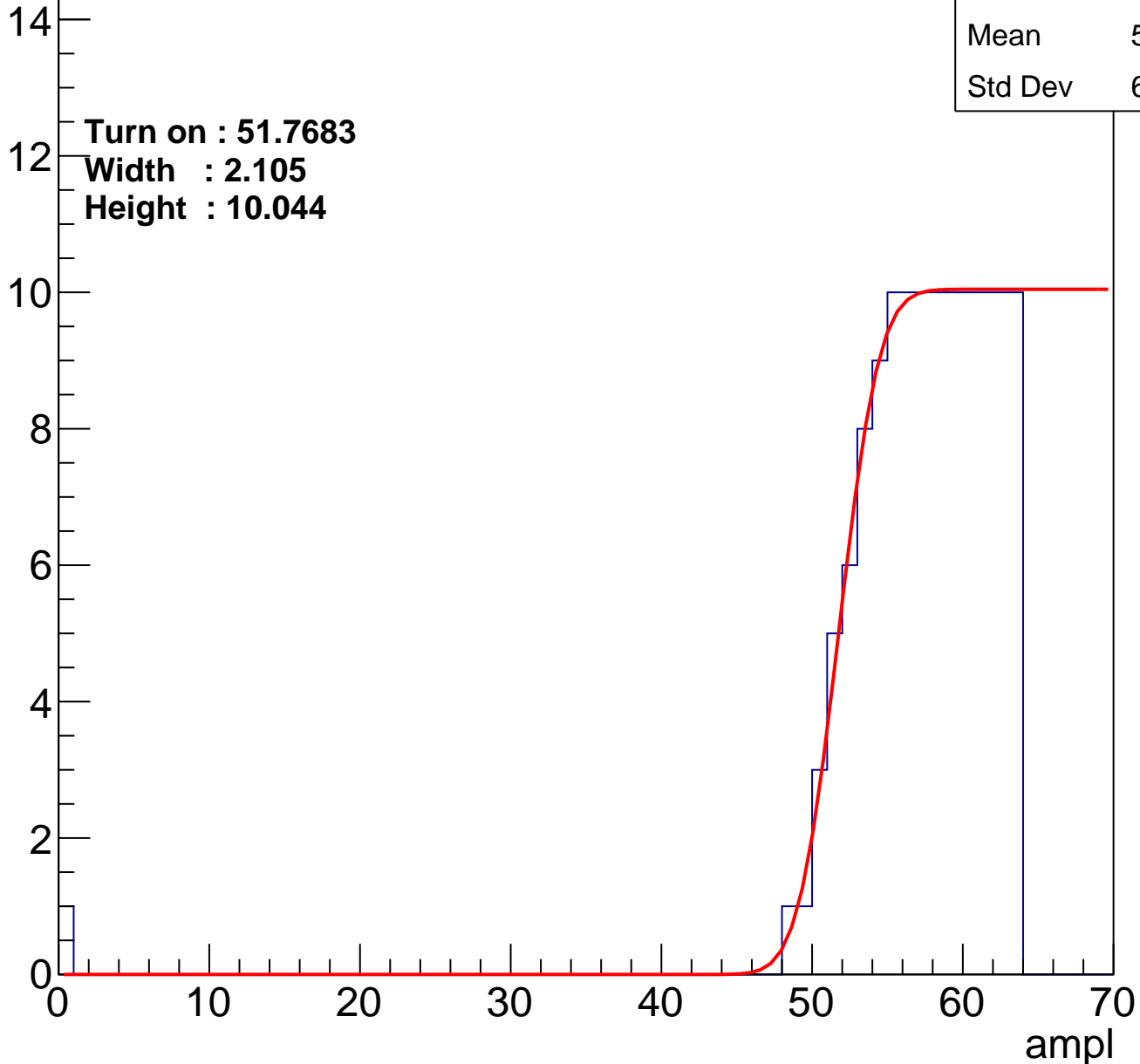
Entries	124
Mean	56.73
Std Dev	6.369

Turn on : 51.7683

Width : 2.105

Height : 10.044

Entry



# B0L103S, U20-ch77

calib\_packv5\_040323\_1717.root, FC#2, port C3

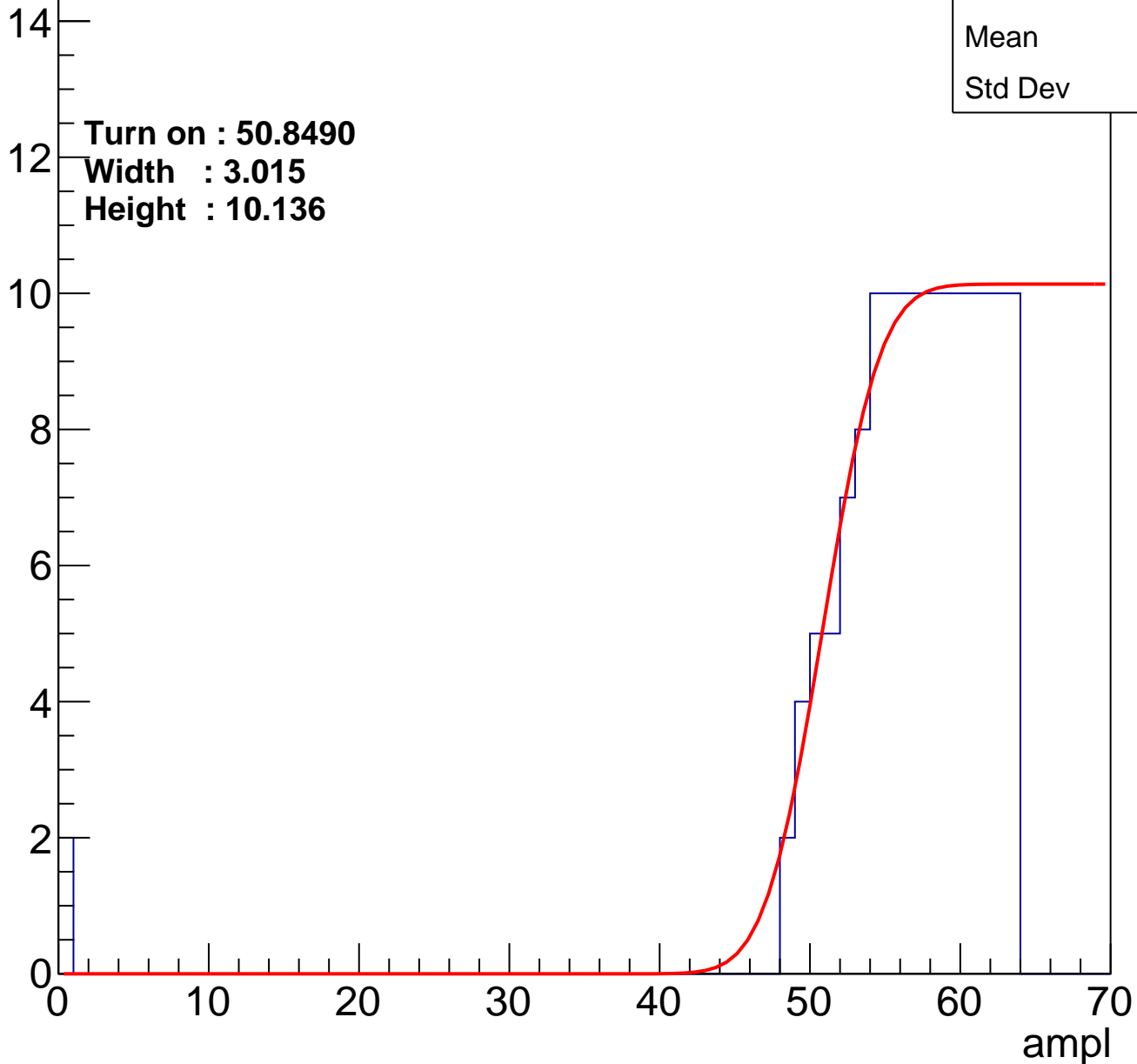
Entries	133
Mean	55.9
Std Dev	8.01

Turn on : 50.8490

Width : 3.015

Height : 10.136

Entry



# B0L103S, U20-ch78

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.42
Std Dev	10.84

Turn on : 52.5080

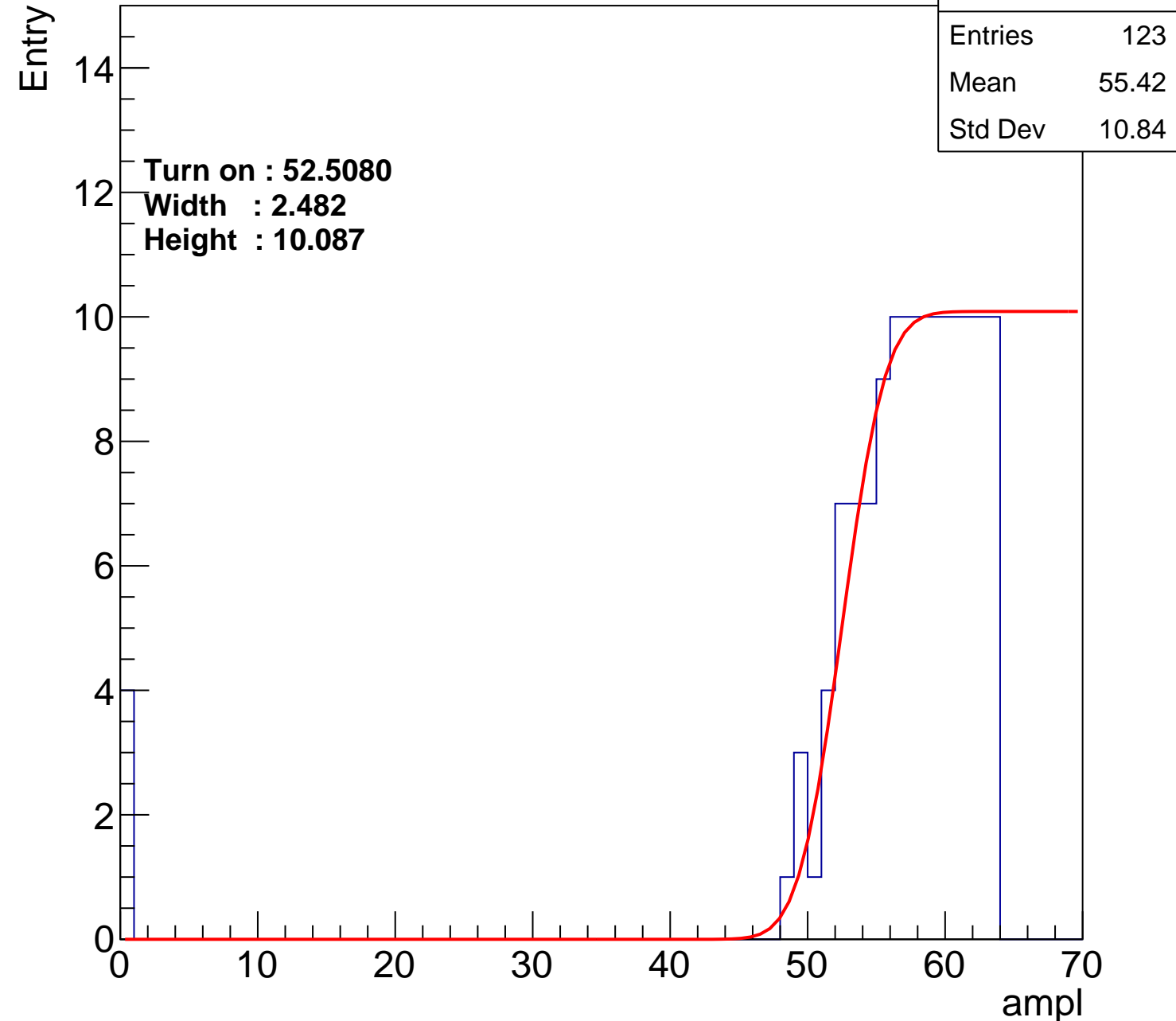
Width : 2.482

Height : 10.087

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch79

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	54.86
Std Dev	10.39

Turn on : 50.8671

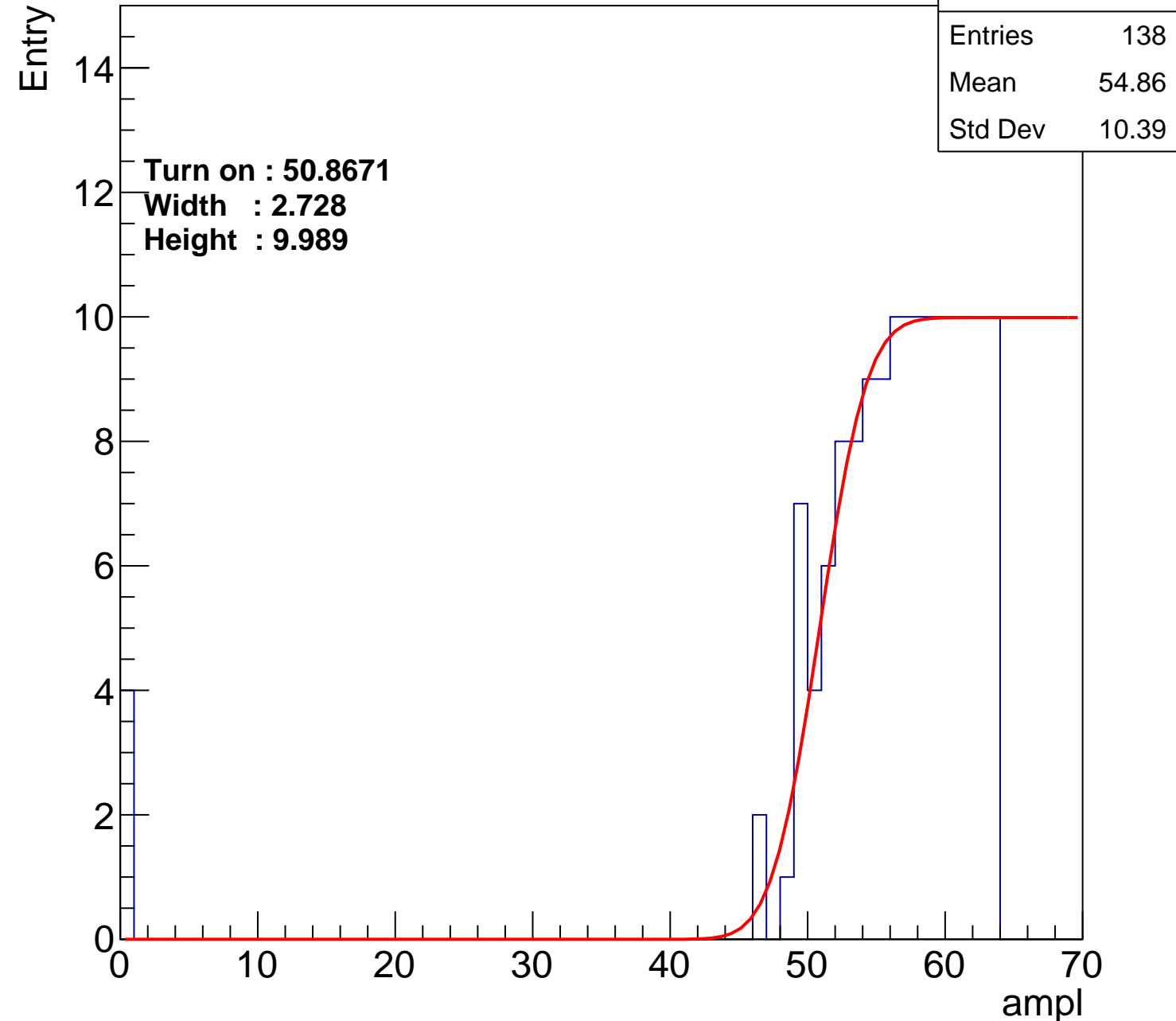
Width : 2.728

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch80

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.65
Std Dev	8.17

Turn on : 51.8507

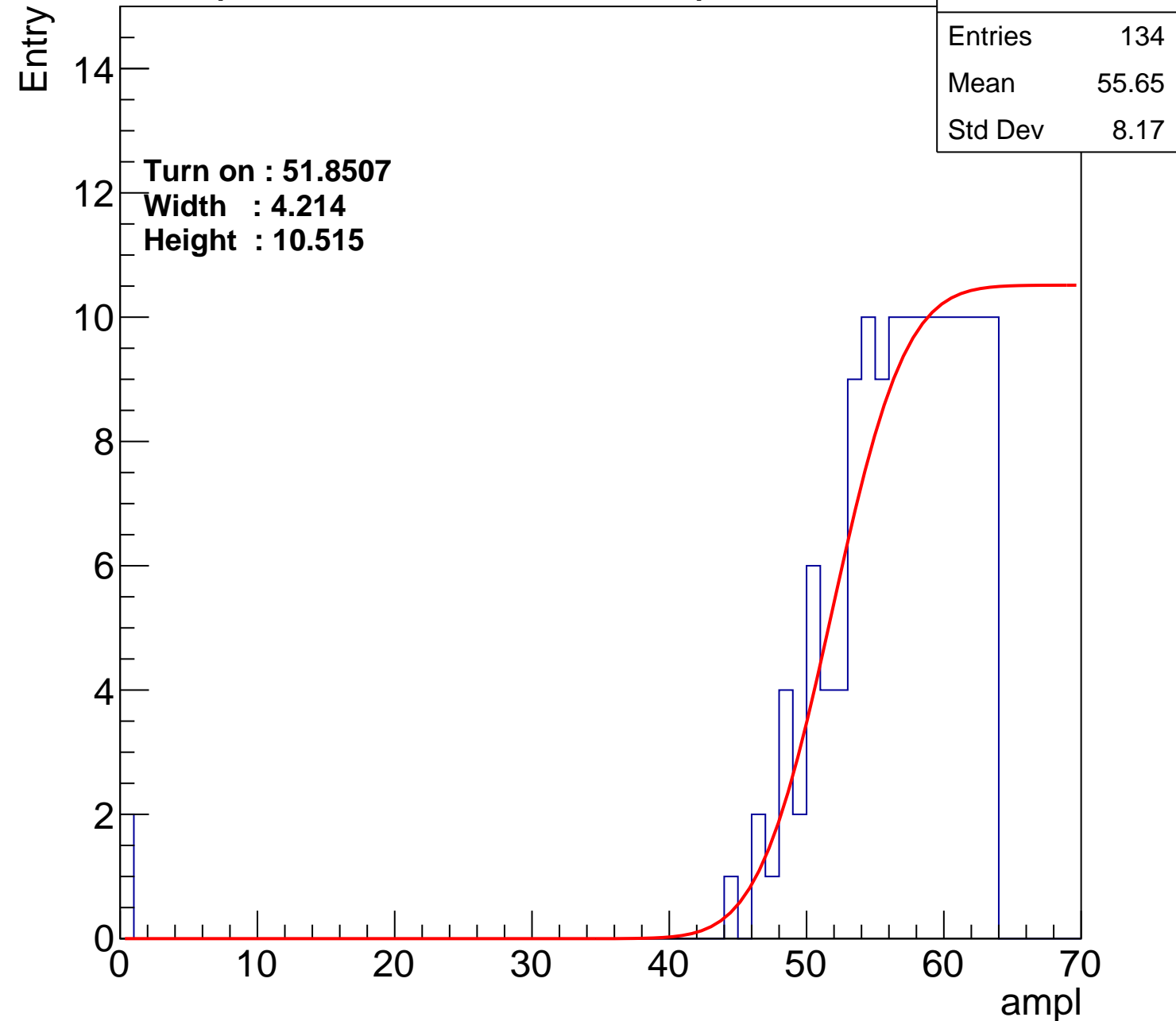
Width : 4.214

Height : 10.515

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch81

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.71
Std Dev	8.133

Turn on : 51.3079

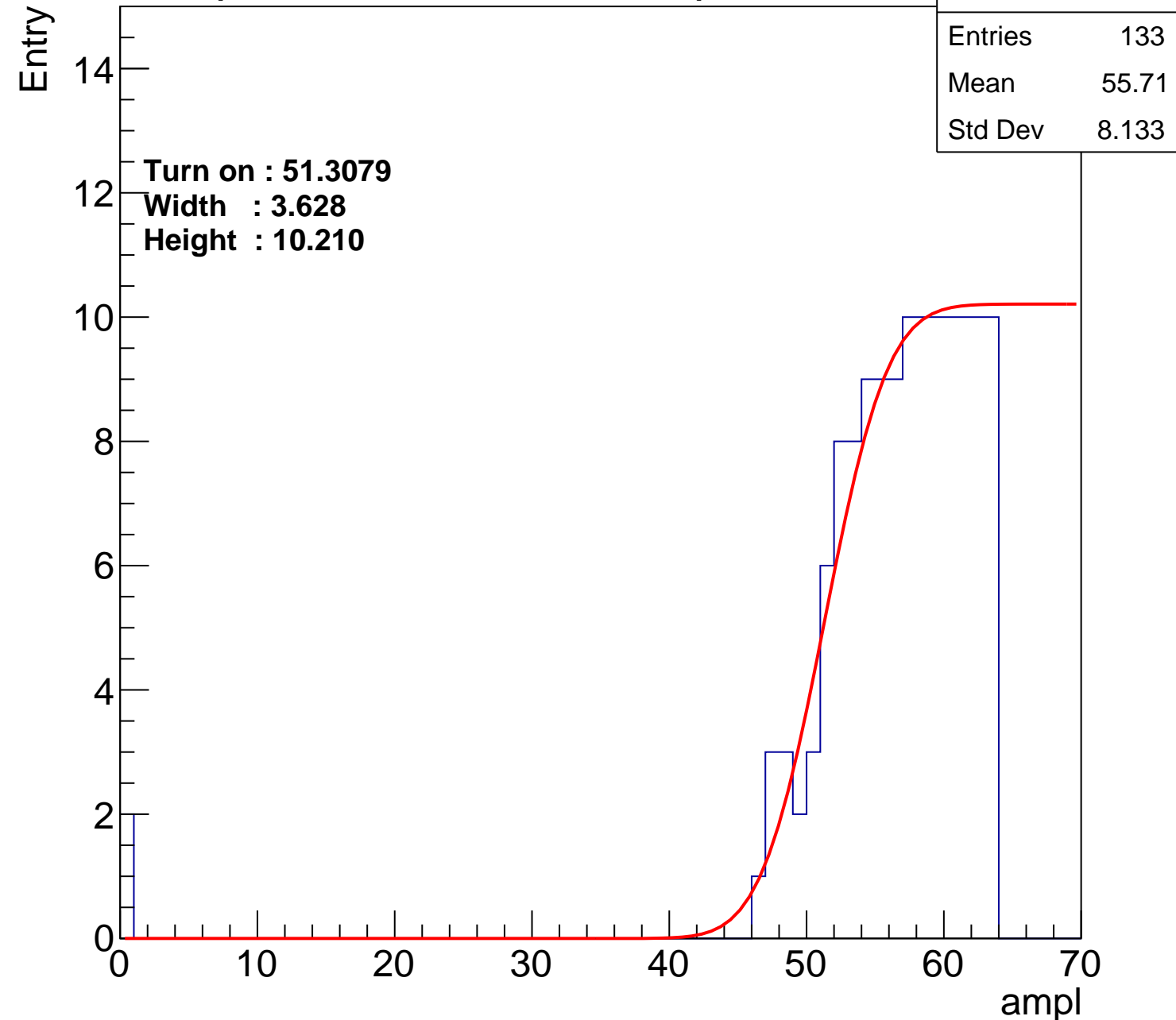
Width : 3.628

Height : 10.210

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch82

calib\_packv5\_040323\_1717.root, FC#2, port C3

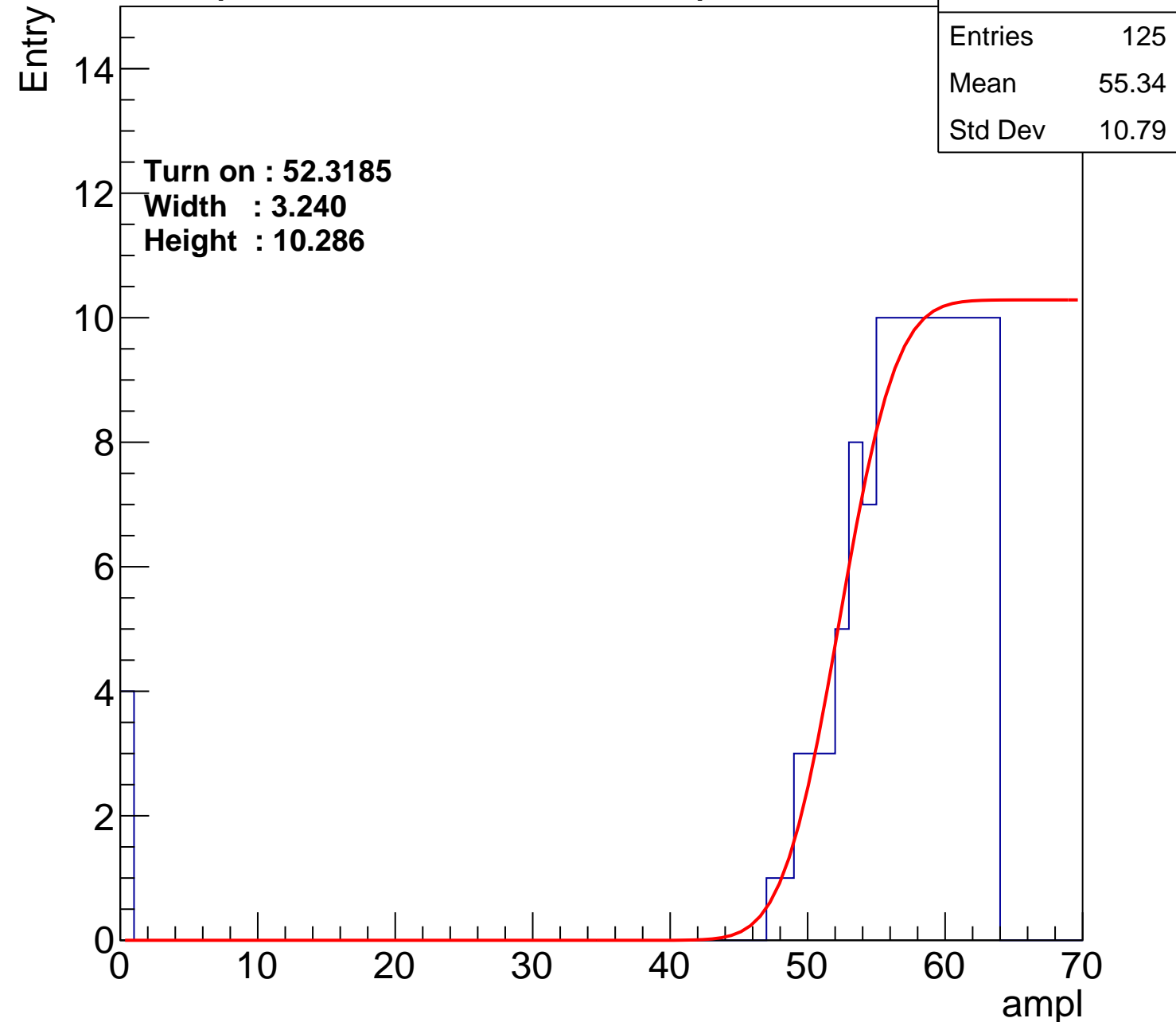
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 52.3185  
Width : 3.240  
Height : 10.286

Entries	125
Mean	55.34
Std Dev	10.79

ampl



# B0L103S, U20-ch83

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.18
Std Dev	10.58

Turn on : 51.6363

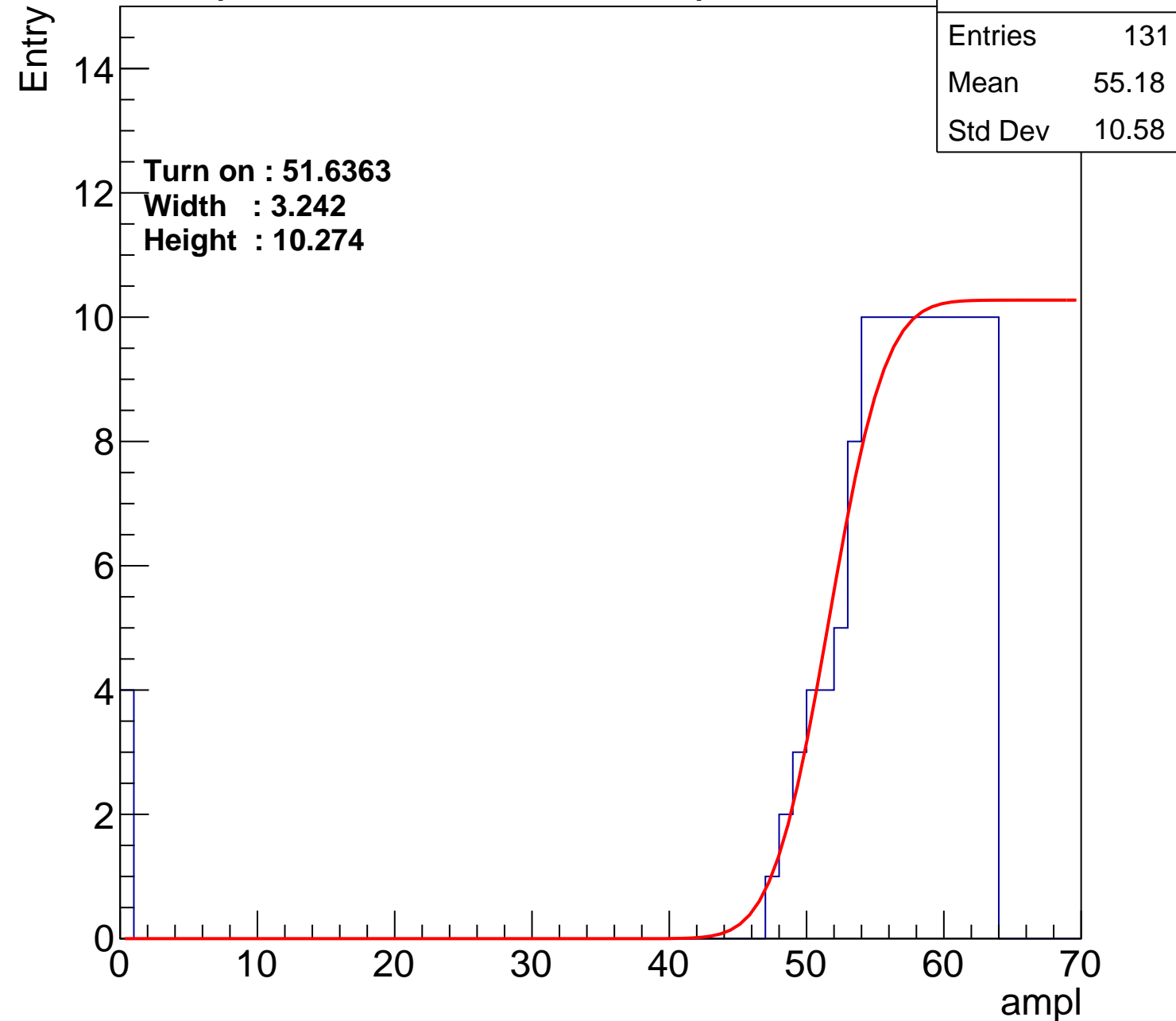
Width : 3.242

Height : 10.274

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch84

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	55.7
Std Dev	7.949

Turn on : 50.2735

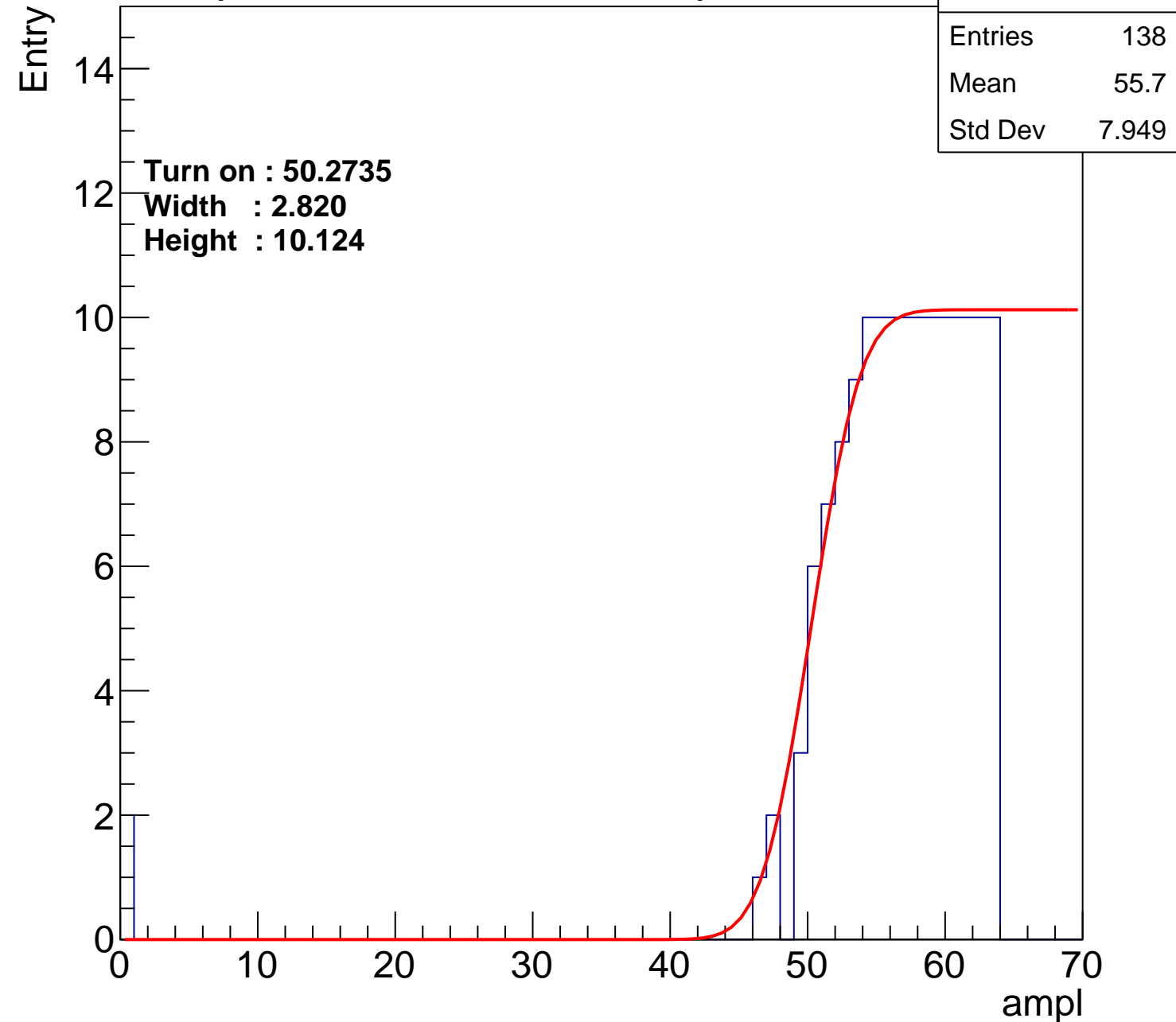
Width : 2.820

Height : 10.124

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch85

calib\_packv5\_040323\_1717.root, FC#2, port C3

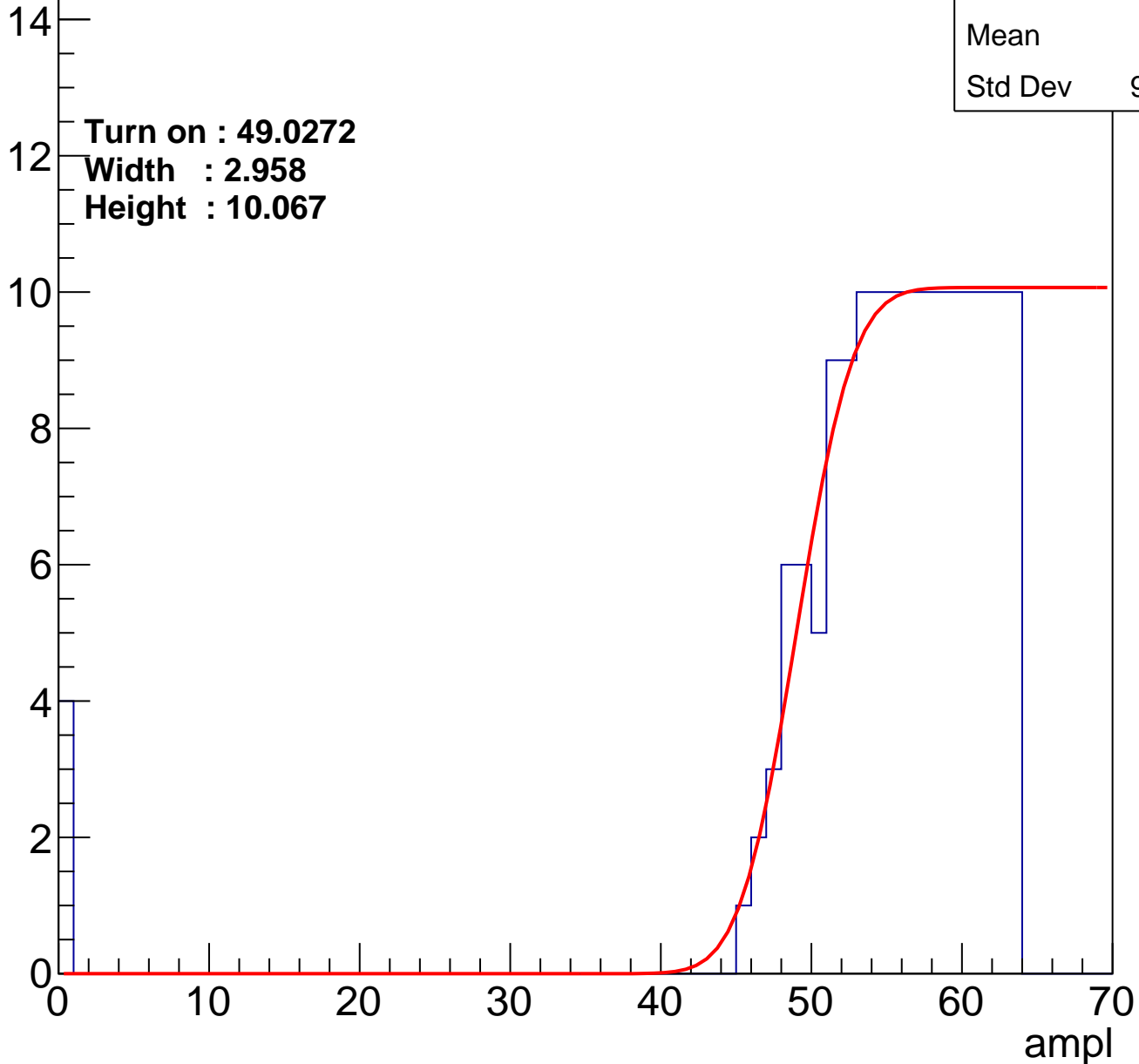
Entries	155
Mean	54.3
Std Dev	9.977

Turn on : 49.0272

Width : 2.958

Height : 10.067

Entry



# B0L103S, U20-ch86

calib\_packv5\_040323\_1717.root, FC#2, port C3

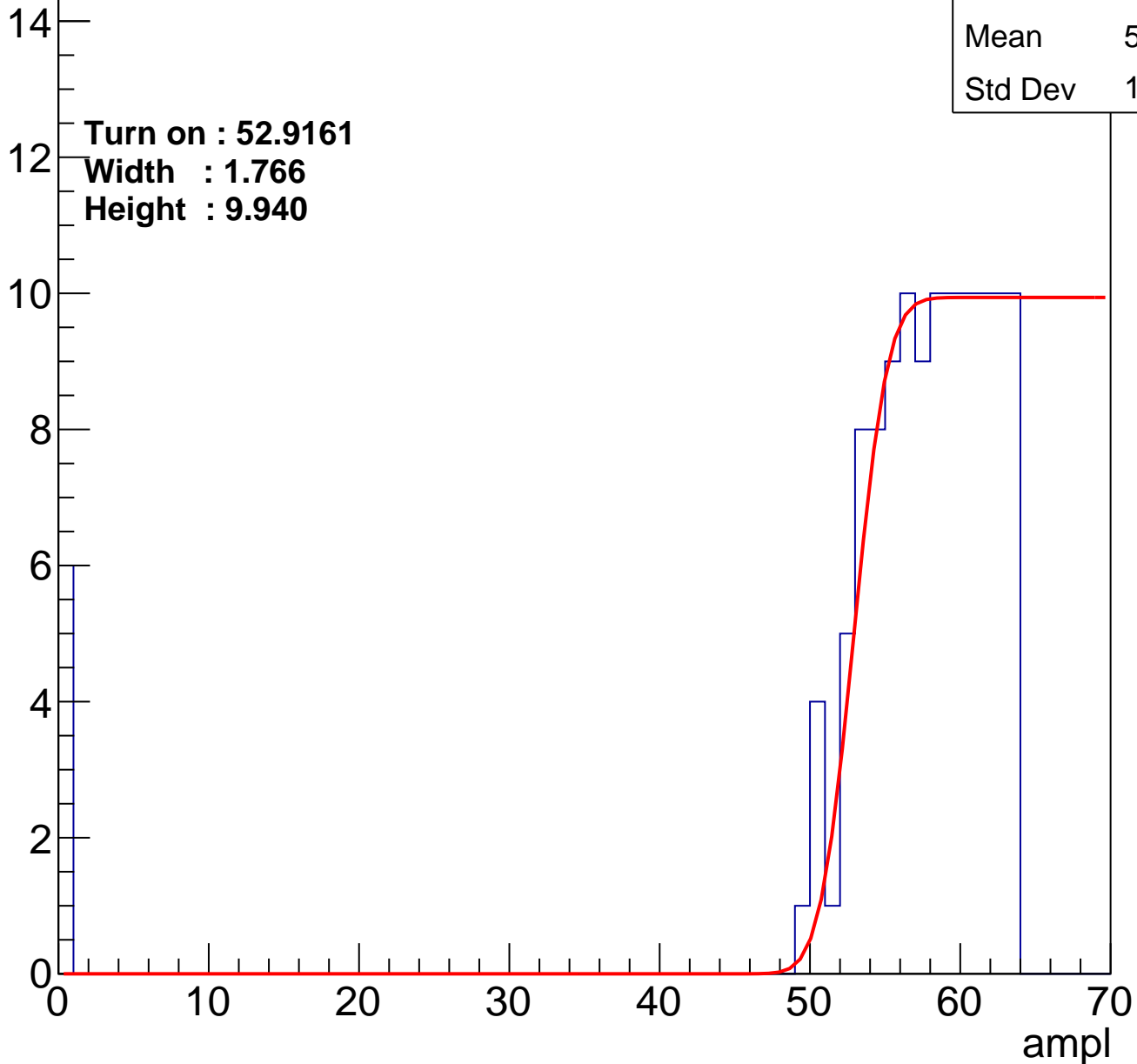
Entries	121
Mean	54.66
Std Dev	12.99

Turn on : 52.9161

Width : 1.766

Height : 9.940

Entry





# B0L103S, U20-ch87

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	54.79
Std Dev	11.75

Turn on : 52.2248

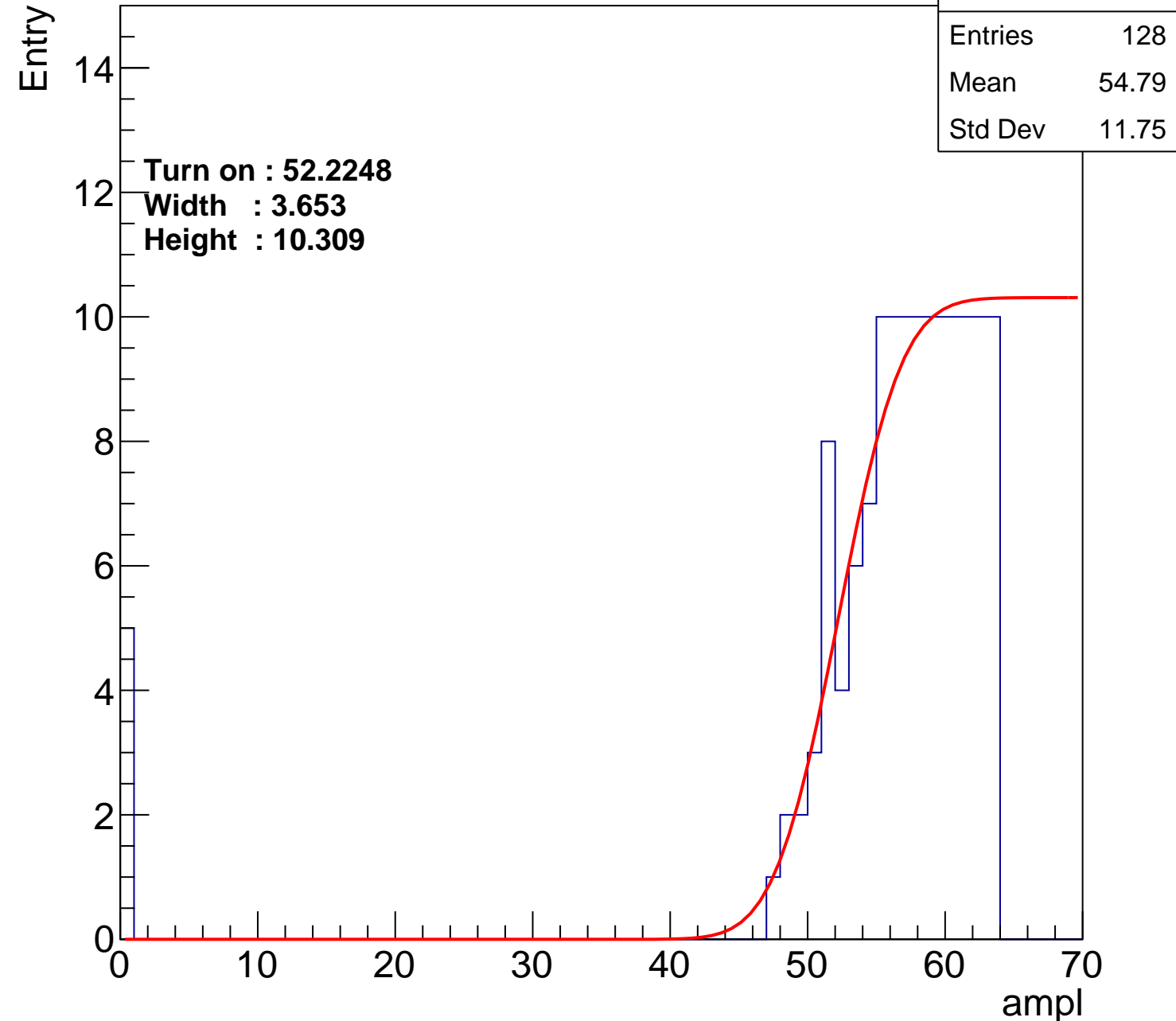
Width : 3.653

Height : 10.309

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch88

calib\_packv5\_040323\_1717.root, FC#2, port C3

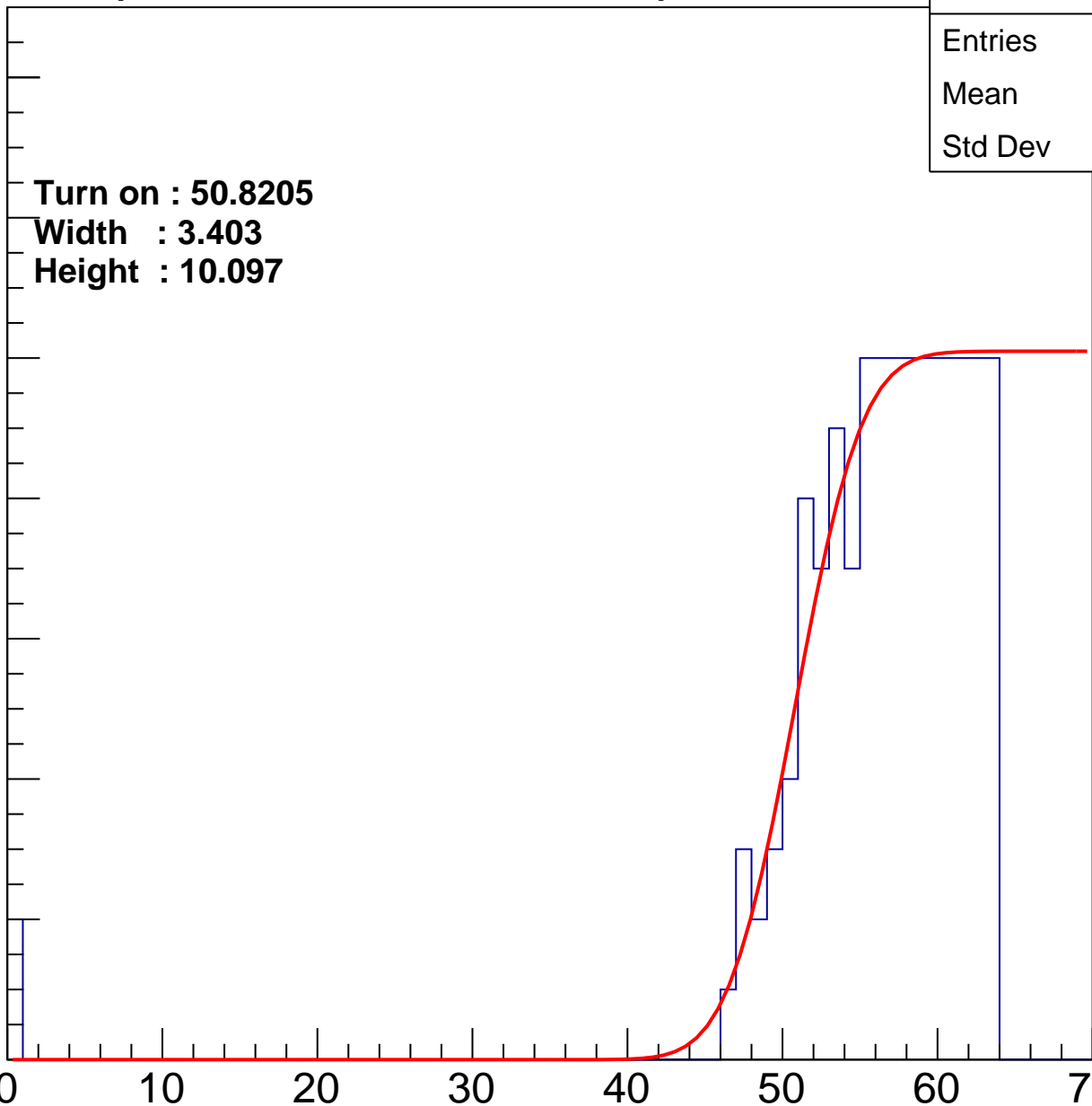
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 50.8205  
Width : 3.403  
Height : 10.097

Entries	136
Mean	55.64
Std Dev	8.066

ampl



# B0L103S, U20-ch89

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.08
Std Dev	10.64

Turn on : 52.2659

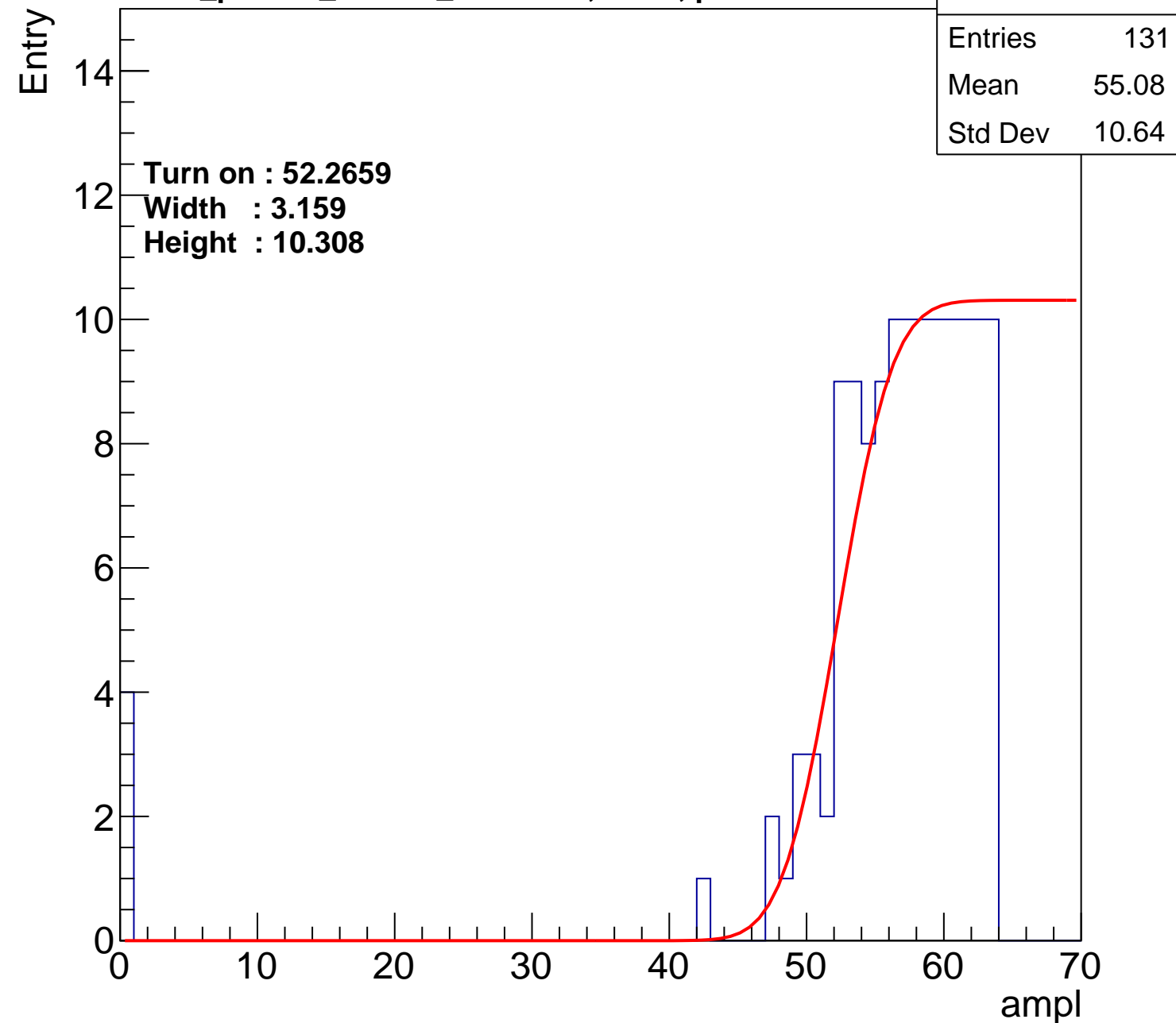
Width : 3.159

Height : 10.308

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch90

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	55.66
Std Dev	8.074

Turn on : 50.7533

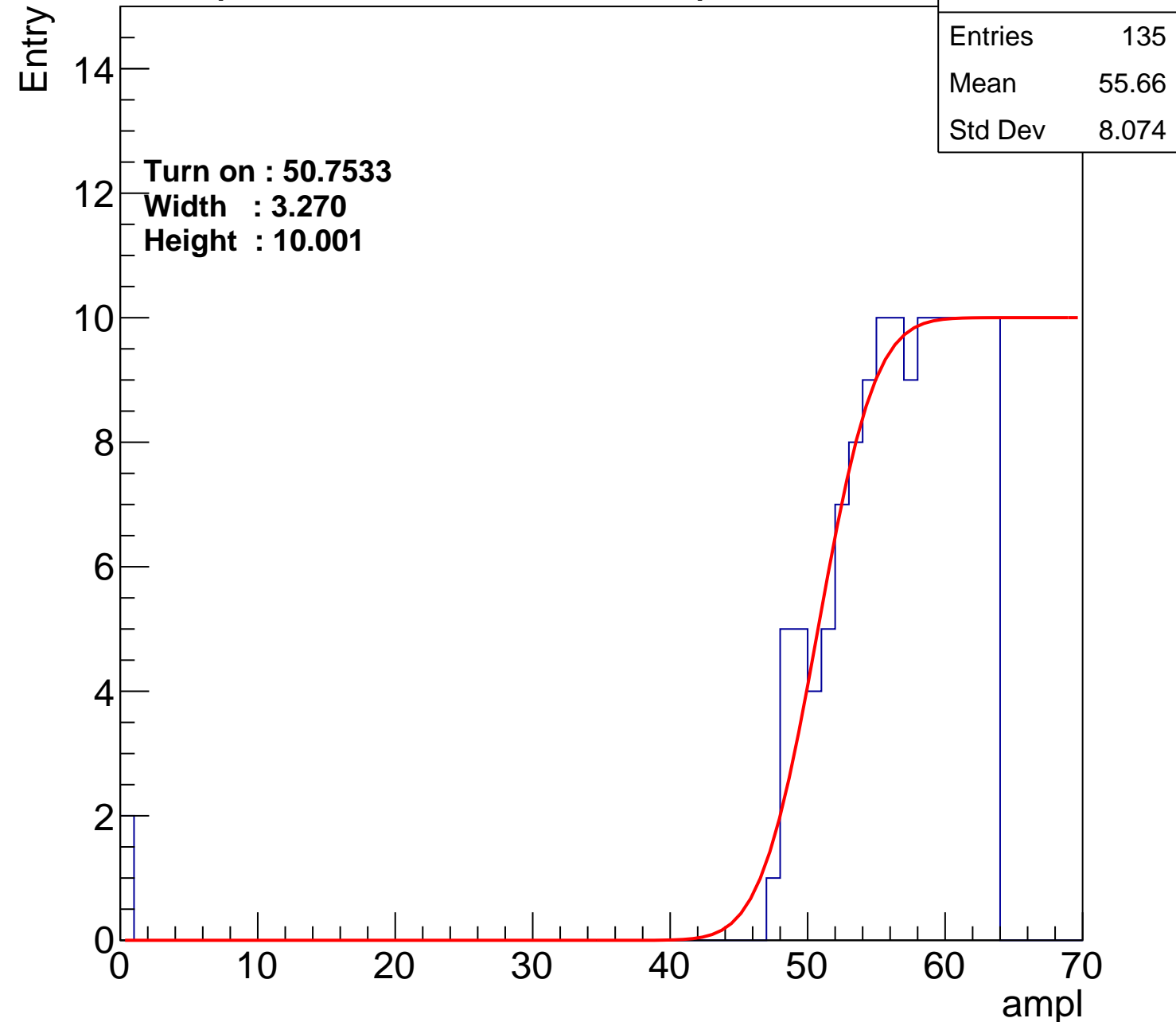
Width : 3.270

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch91

calib\_packv5\_040323\_1717.root, FC#2, port C3

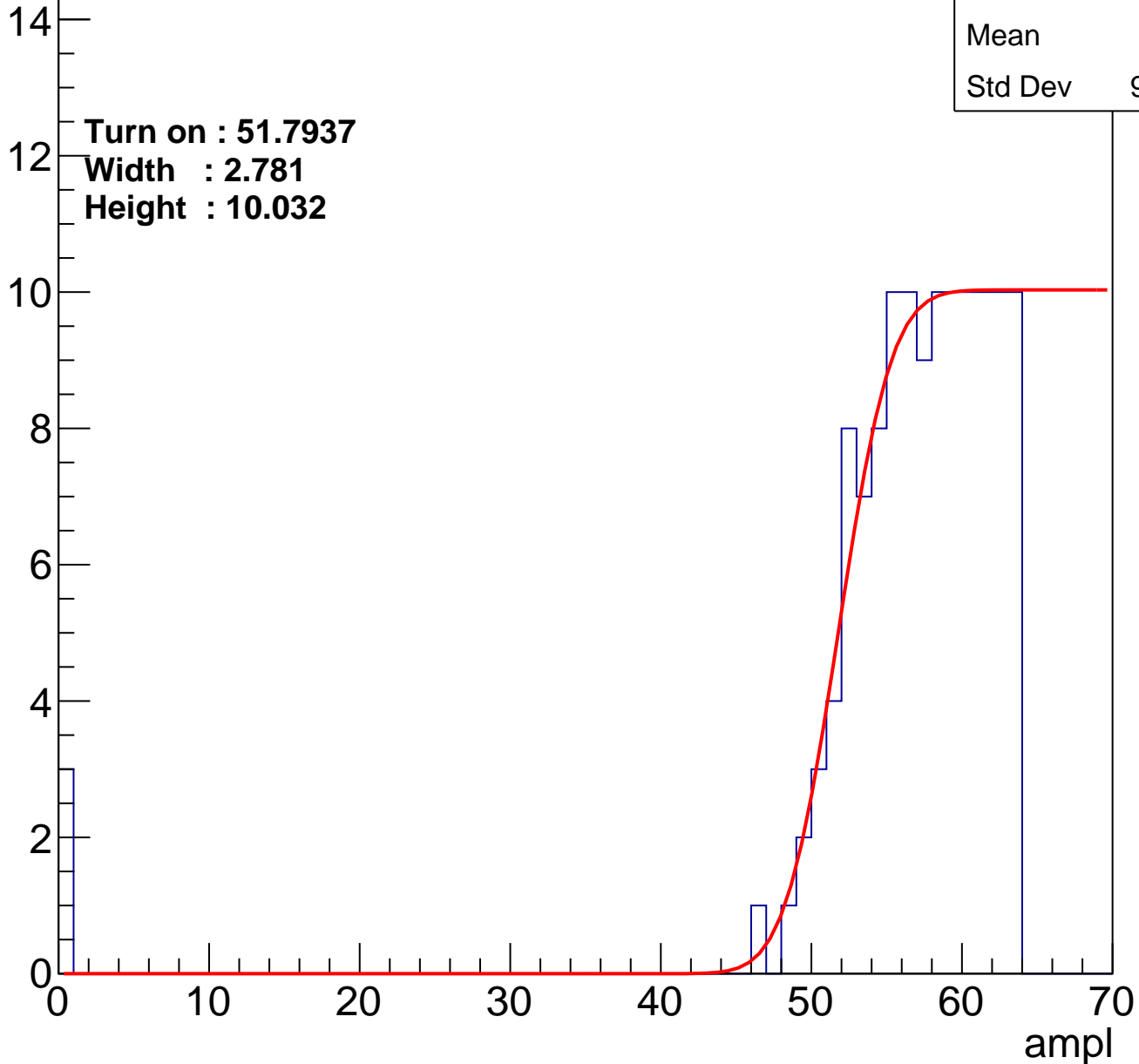
Entries	126
Mean	55.7
Std Dev	9.553

Turn on : 51.7937

Width : 2.781

Height : 10.032

Entry



# B0L103S, U20-ch92

calib\_packv5\_040323\_1717.root, FC#2, port C3

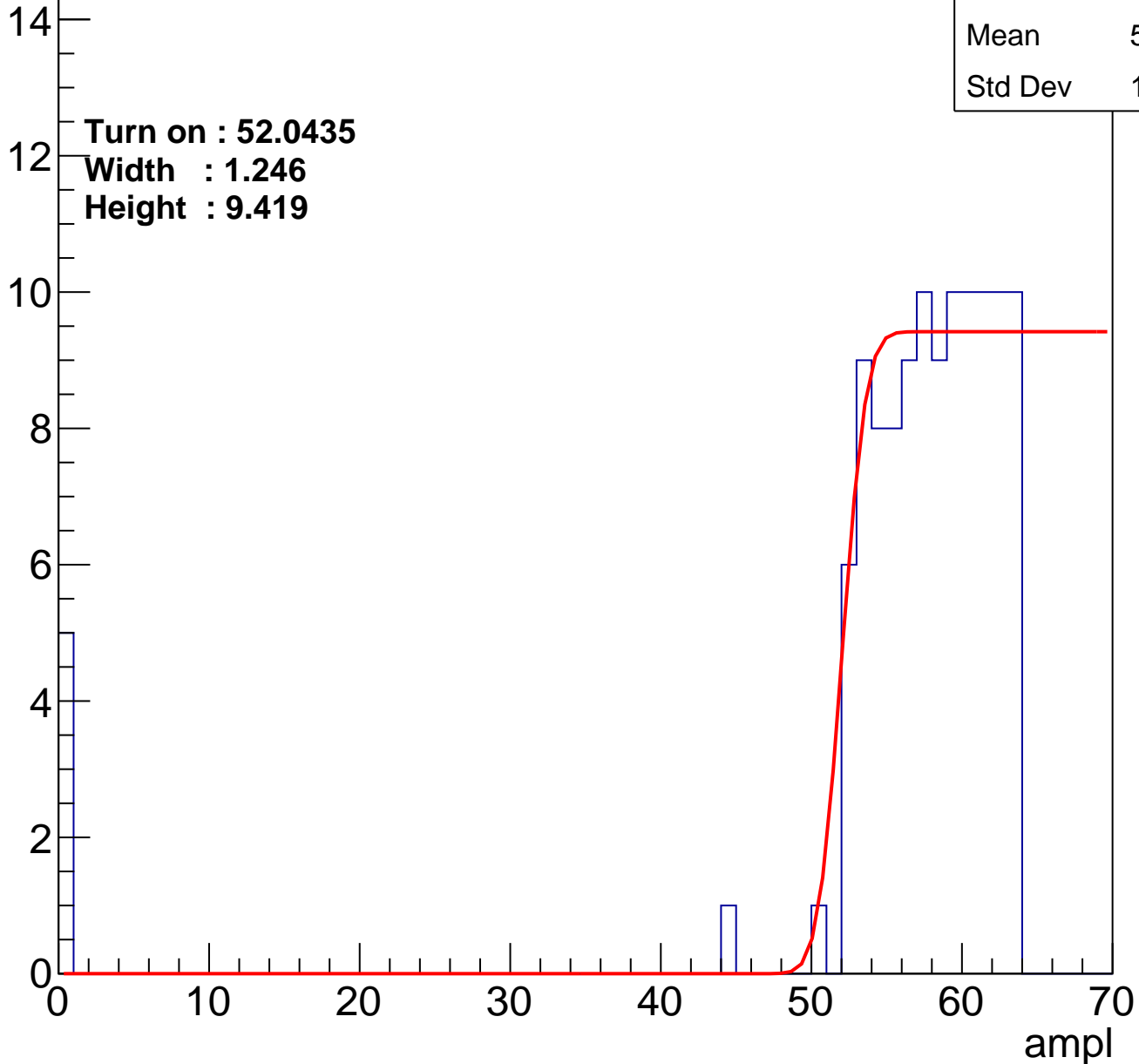
Entries	116
Mean	55.18
Std Dev	12.25

Turn on : 52.0435

Width : 1.246

Height : 9.419

Entry



# B0L103S, U20-ch93

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	54.61
Std Dev	10.29

Turn on : 50.6086

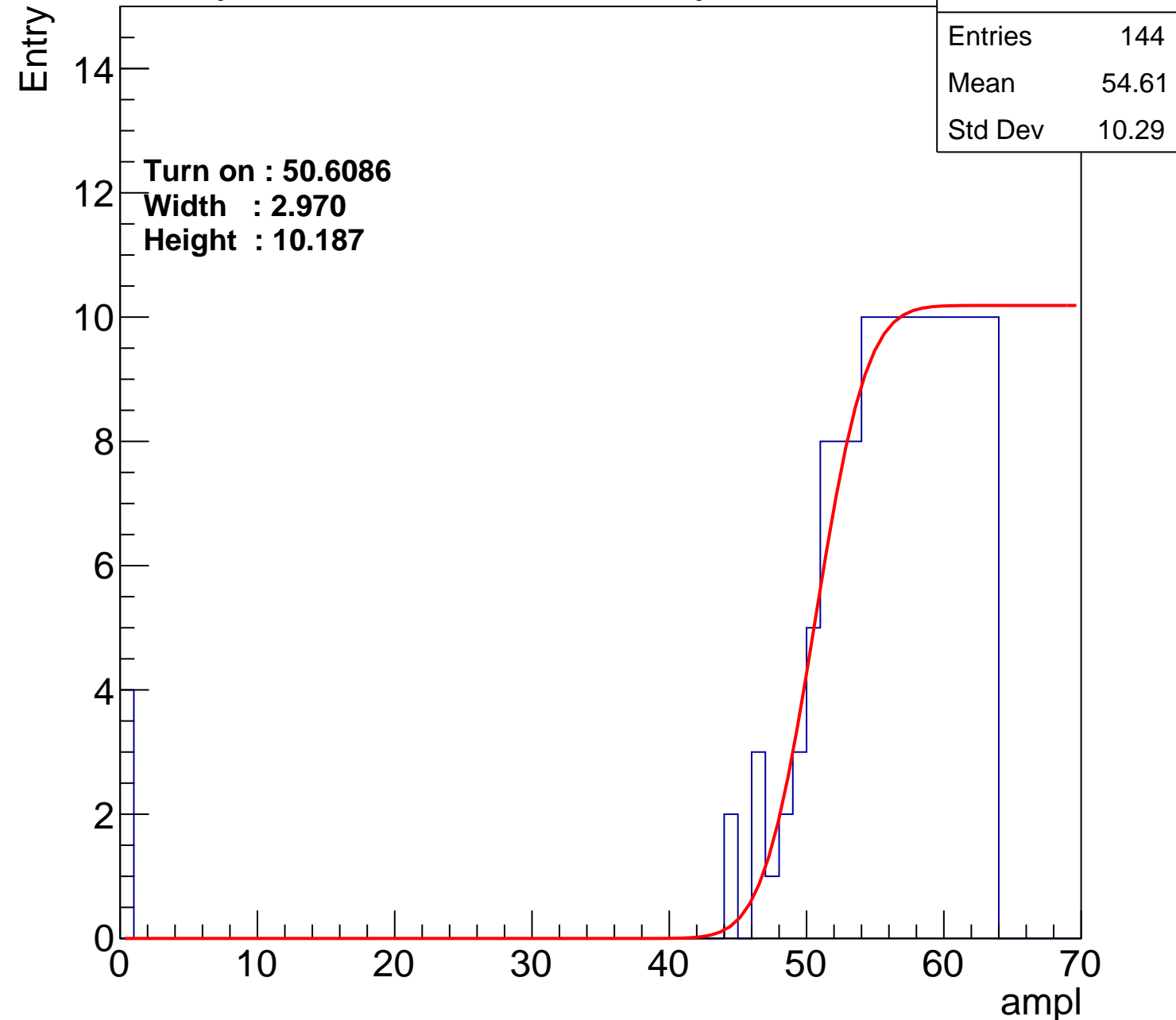
Width : 2.970

Height : 10.187

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch94

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	156
Mean	54.81
Std Dev	7.915

Turn on : 49.0301

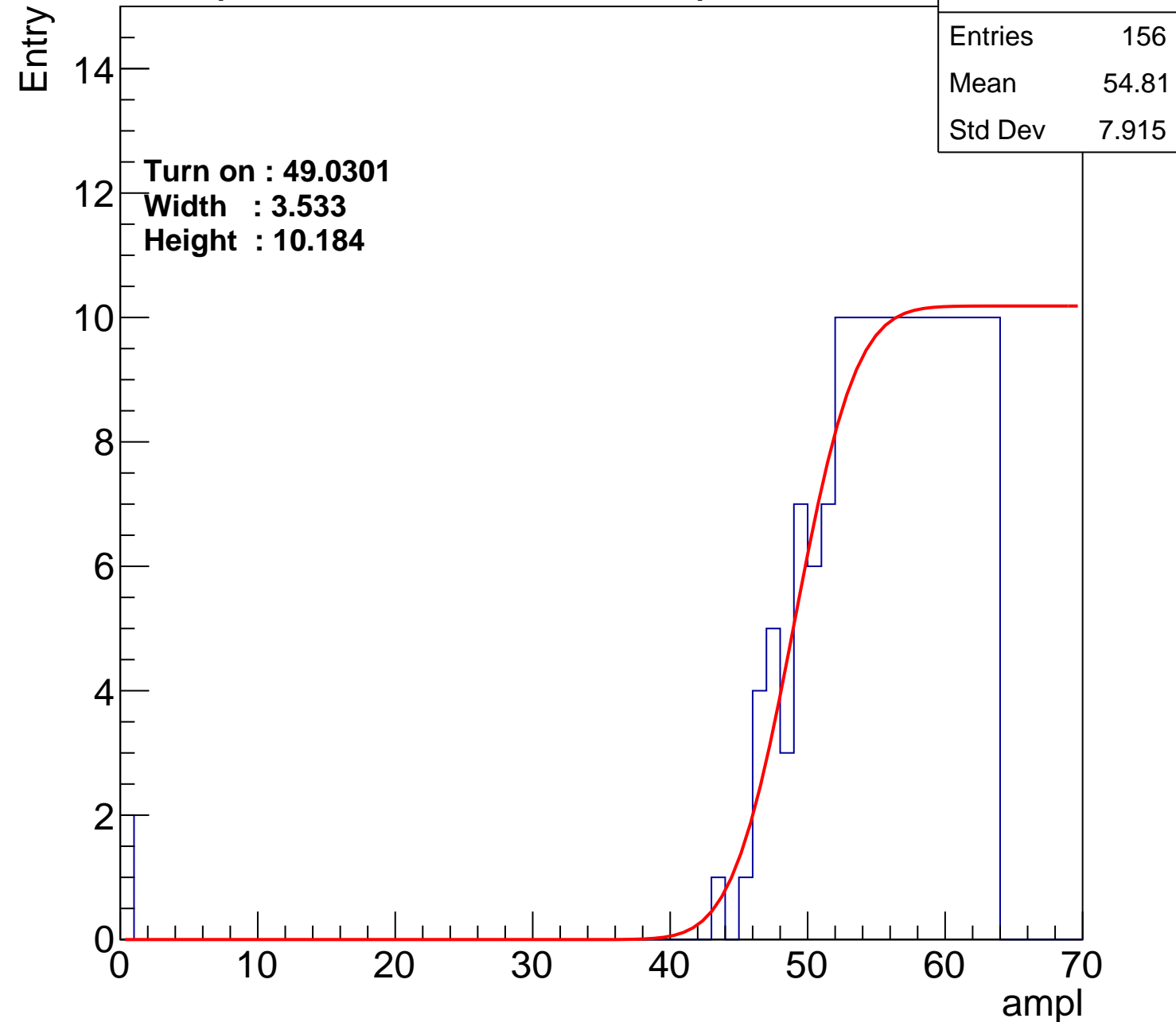
Width : 3.533

Height : 10.184

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch95

calib\_packv5\_040323\_1717.root, FC#2, port C3

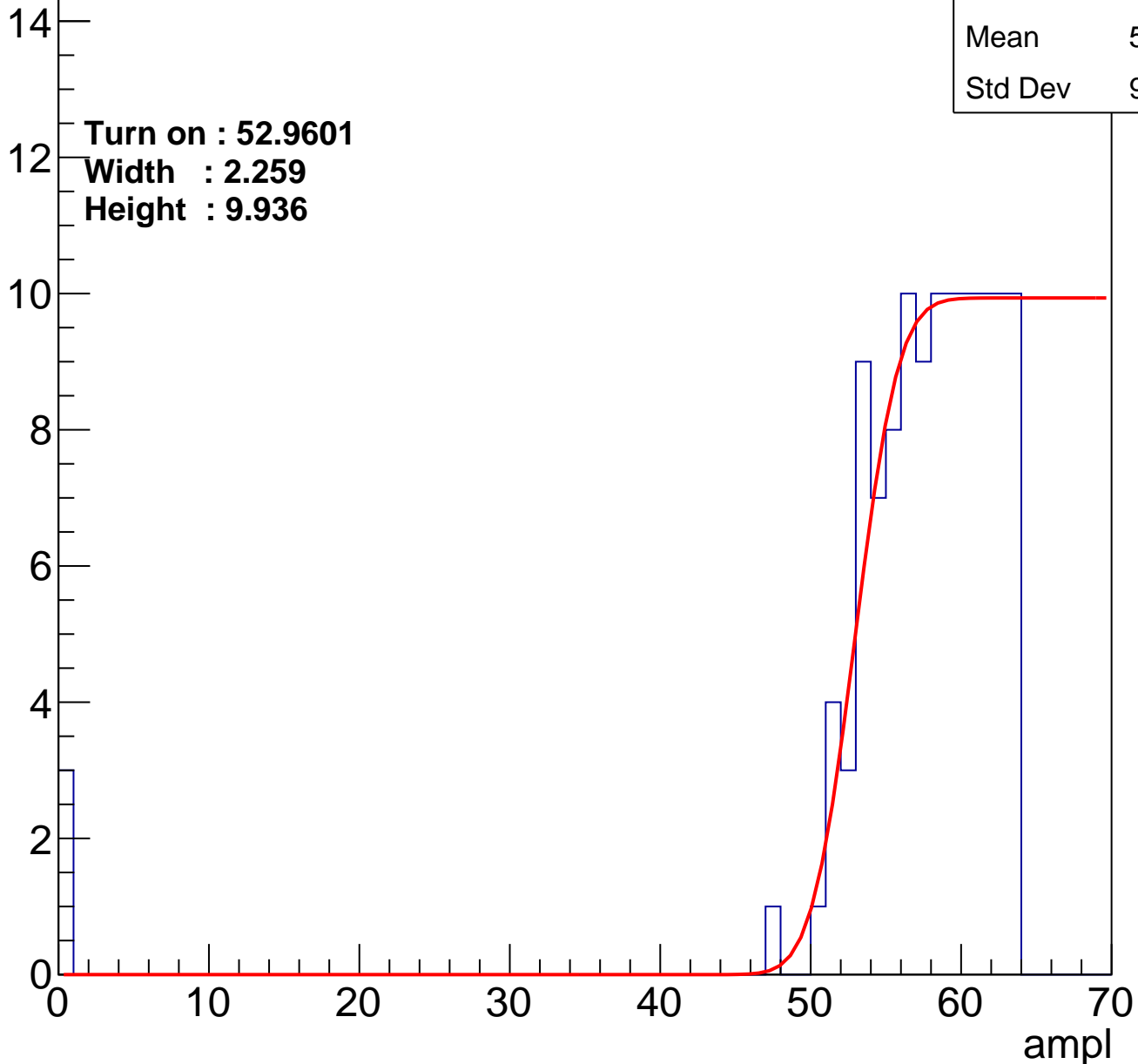
Entries	115
Mean	56.13
Std Dev	9.863

Turn on : 52.9601

Width : 2.259

Height : 9.936

Entry



# B0L103S, U20-ch96

calib\_packv5\_040323\_1717.root, FC#2, port C3

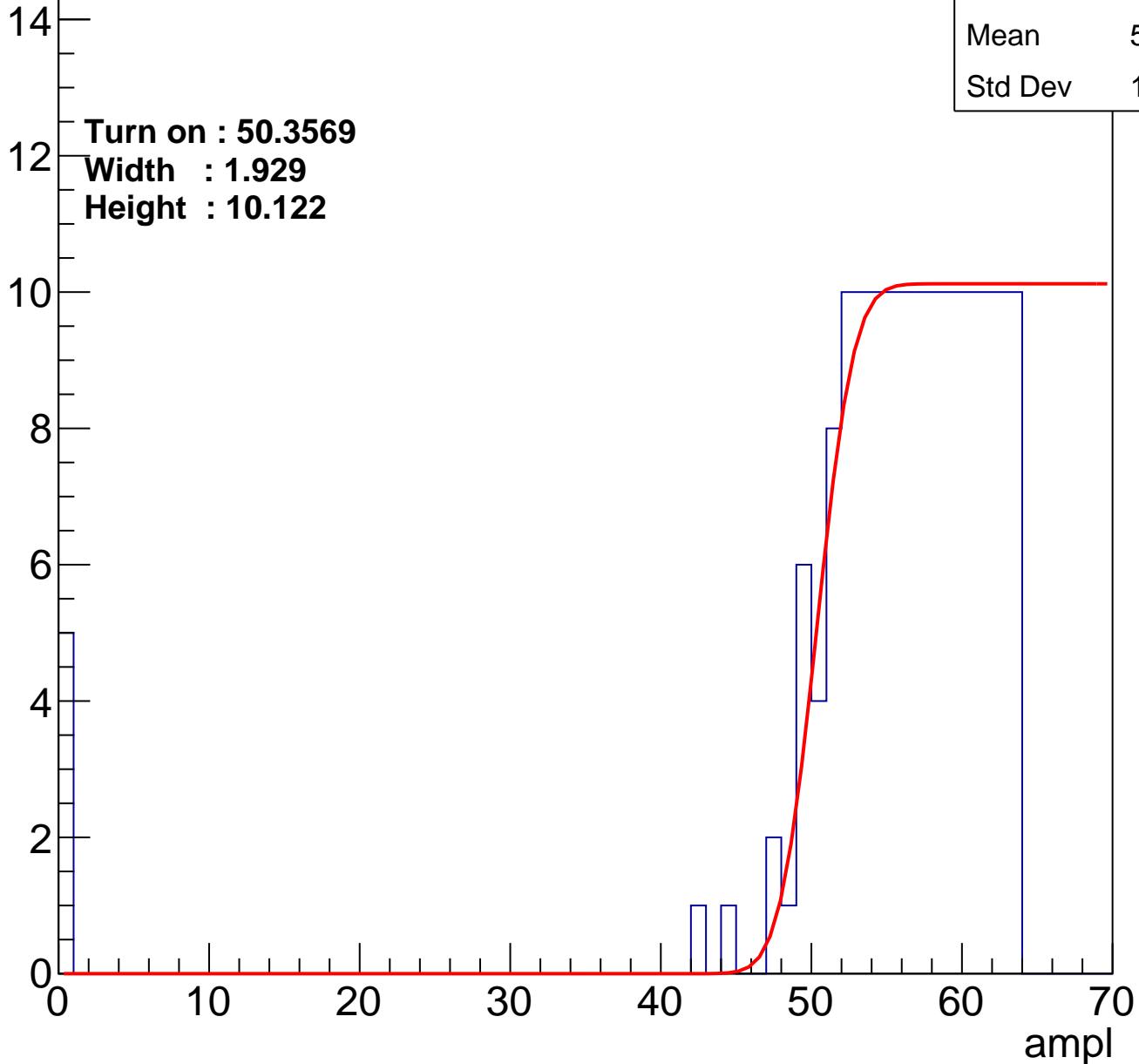
Entries	148
Mean	54.26
Std Dev	11.07

Turn on : 50.3569

Width : 1.929

Height : 10.122

Entry



# B0L103S, U20-ch97

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.98
Std Dev	9.656

Turn on : 52.7499

Width : 2.306

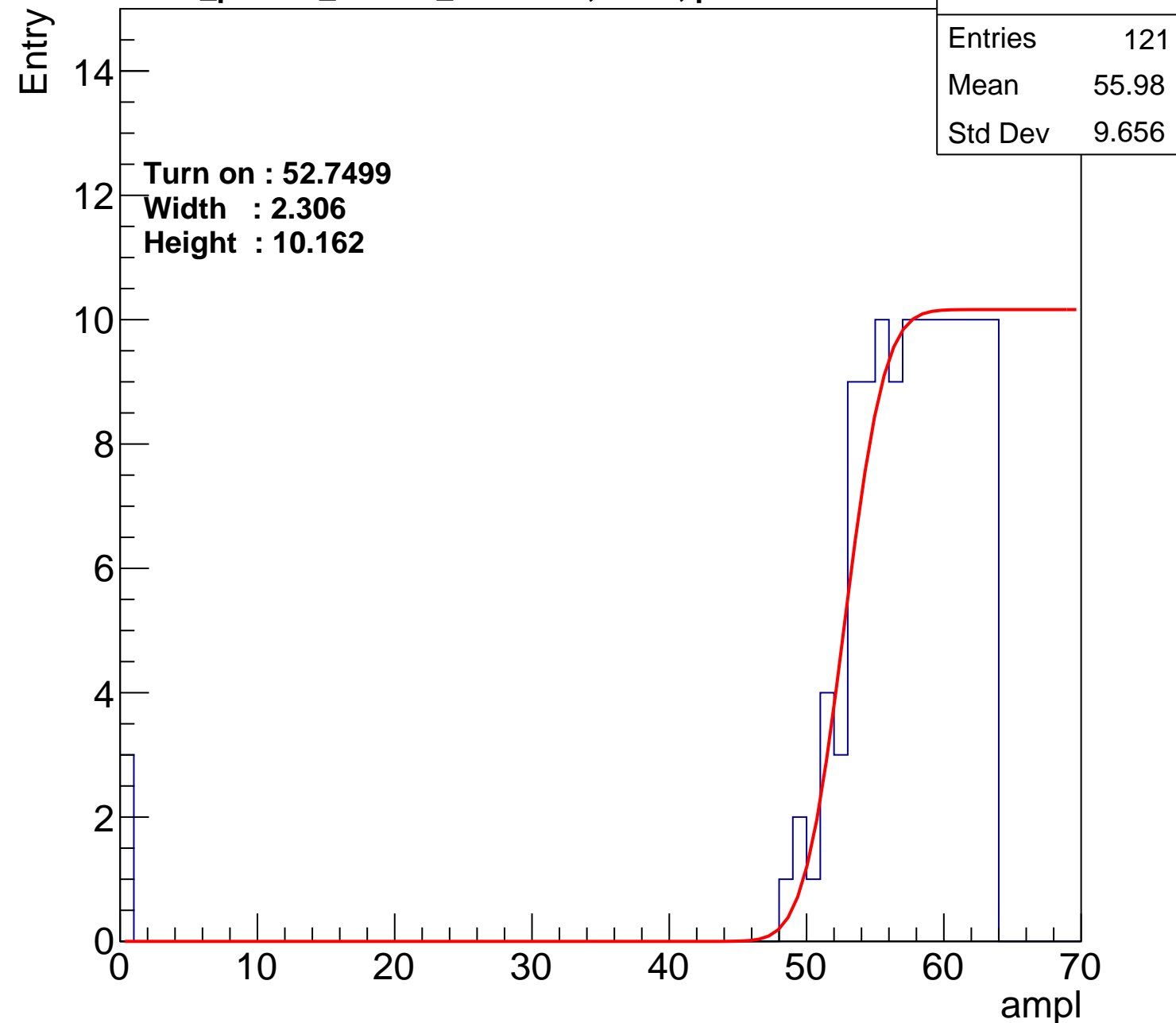
Height : 10.162

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U20-ch98

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	56.13
Std Dev	8.255

Turn on : 52.4820

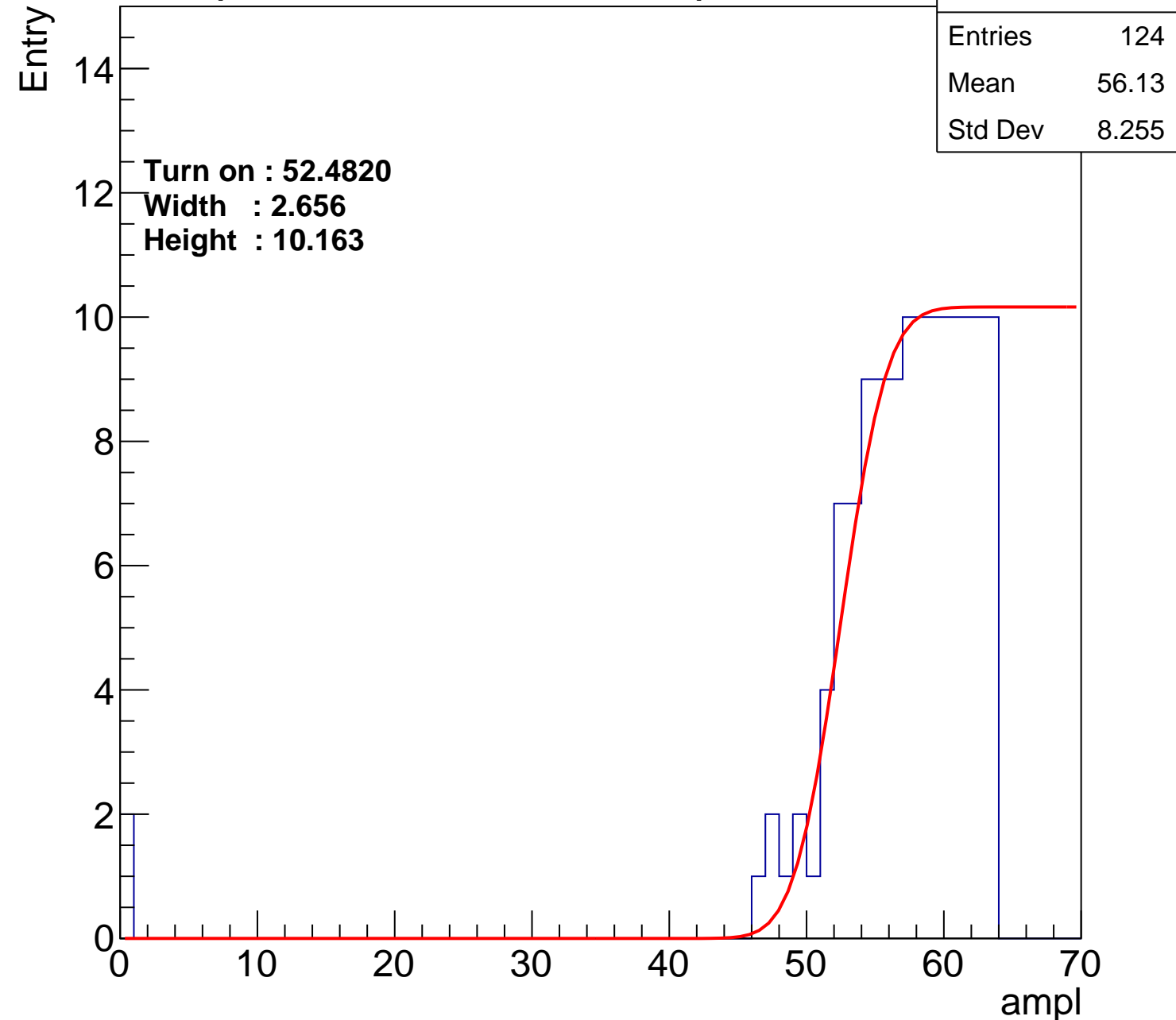
Width : 2.656

Height : 10.163

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch99

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.22
Std Dev	9.338

Turn on : 50.3777

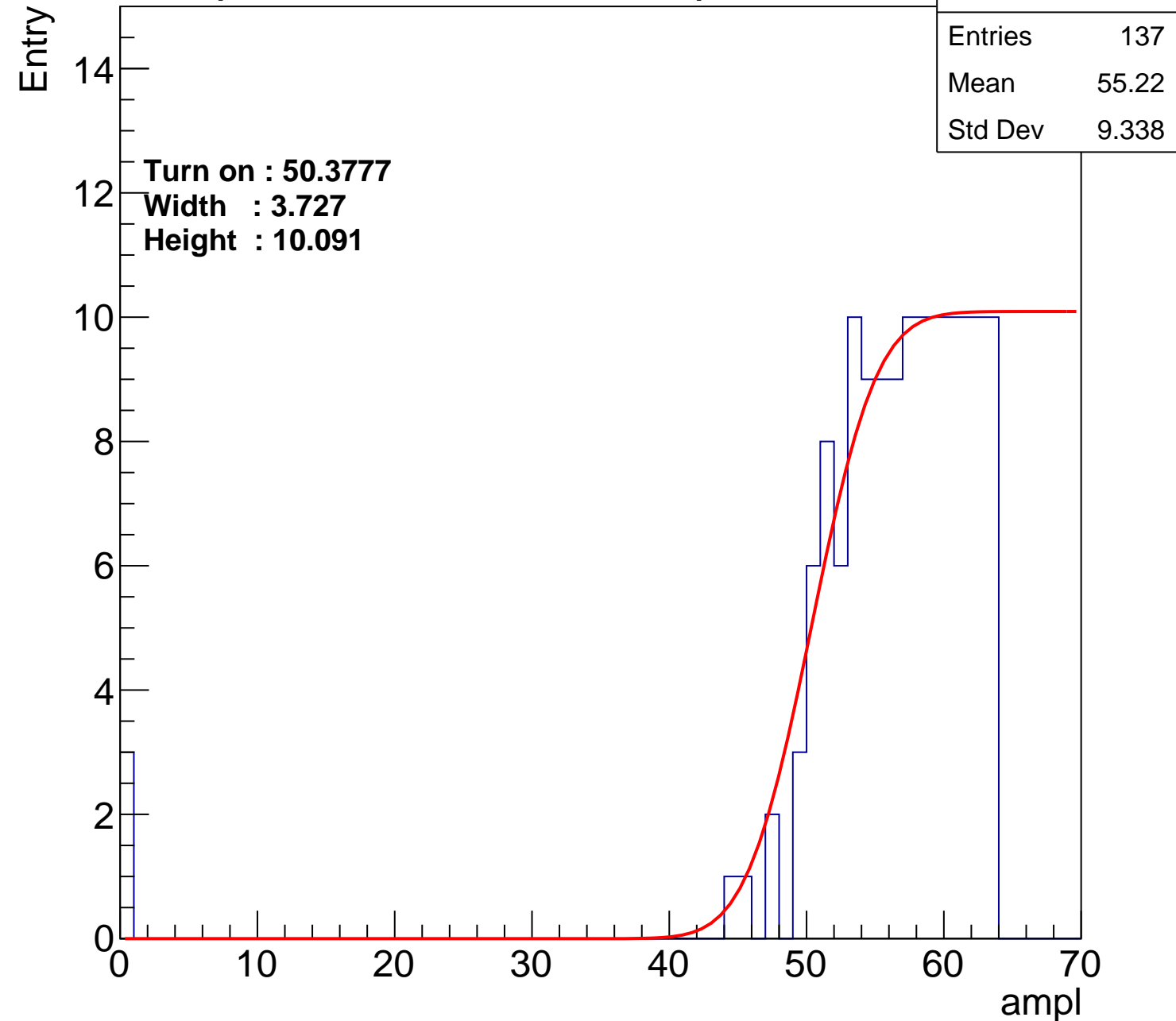
Width : 3.727

Height : 10.091

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch100

calib\_packv5\_040323\_1717.root, FC#2, port C3

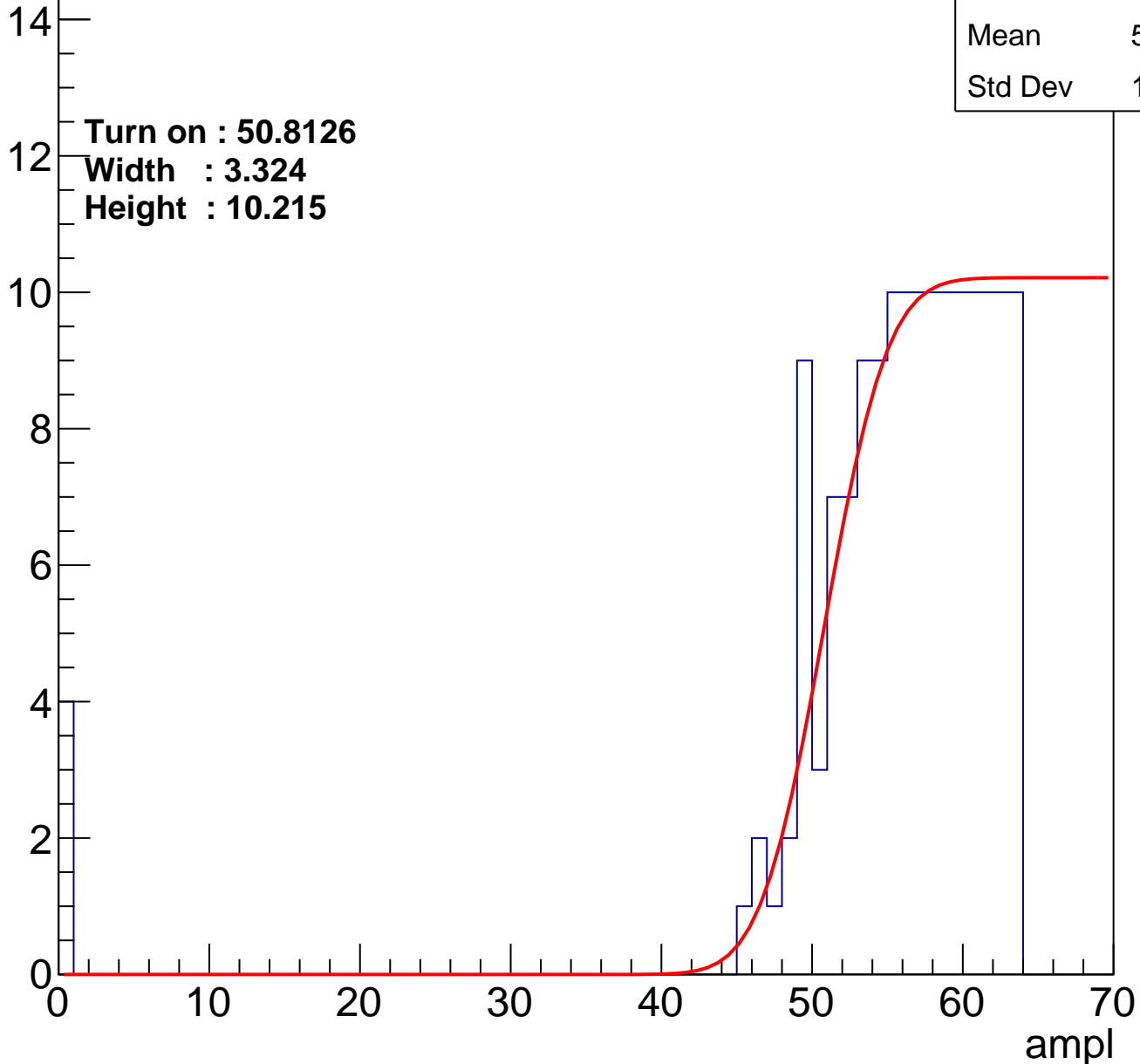
Entries	144
Mean	54.62
Std Dev	10.26

Turn on : 50.8126

Width : 3.324

Height : 10.215

Entry



# B0L103S, U20-ch101

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.82
Std Dev	6.476

Turn on : 52.4939

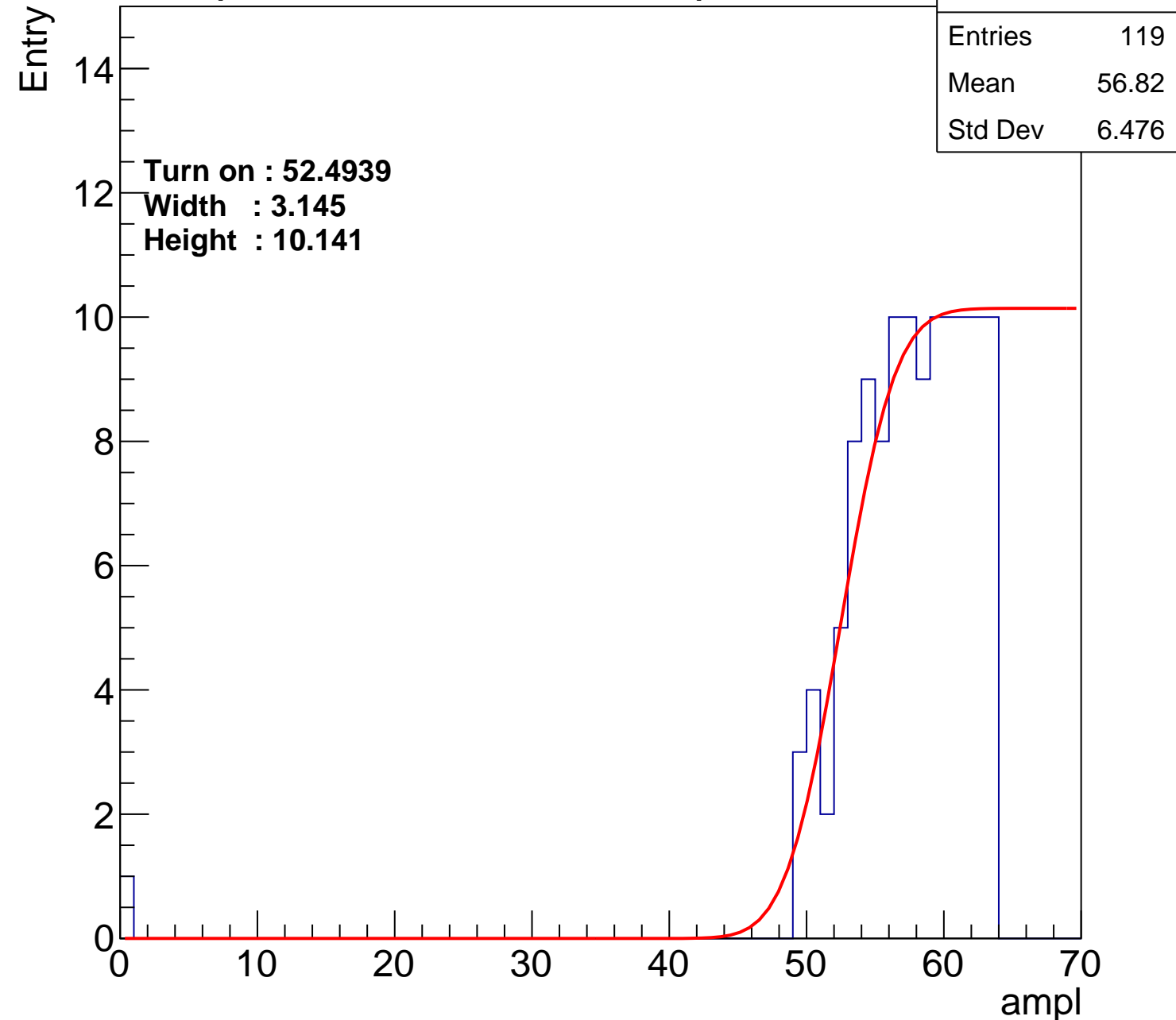
Width : 3.145

Height : 10.141

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch102

calib\_packv5\_040323\_1717.root, FC#2, port C3

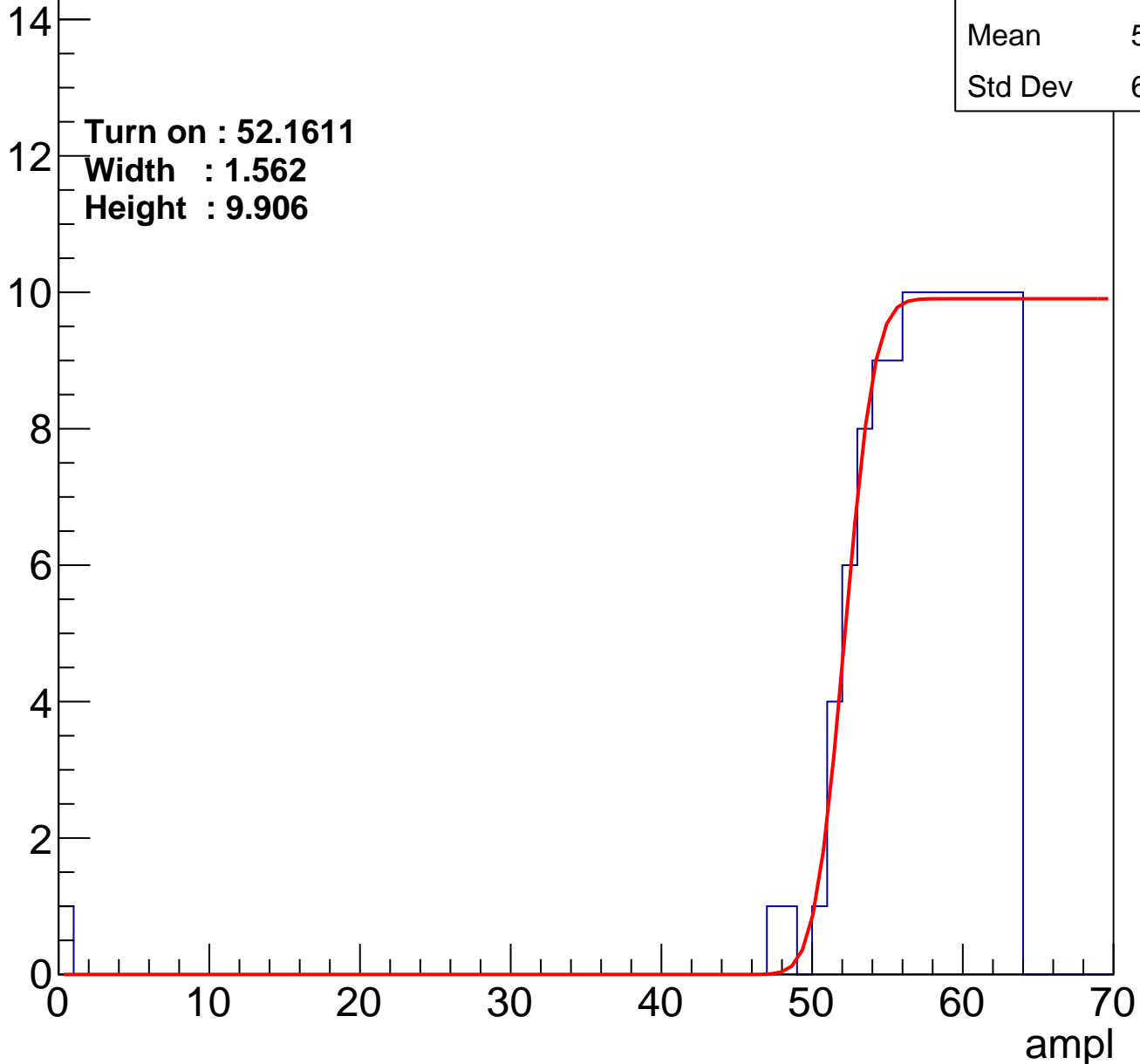
Entries	120
Mean	56.88
Std Dev	6.414

Turn on : 52.1611

Width : 1.562

Height : 9.906

Entry





# B0L103S, U20-ch103

calib\_packv5\_040323\_1717.root, FC#2, port C3

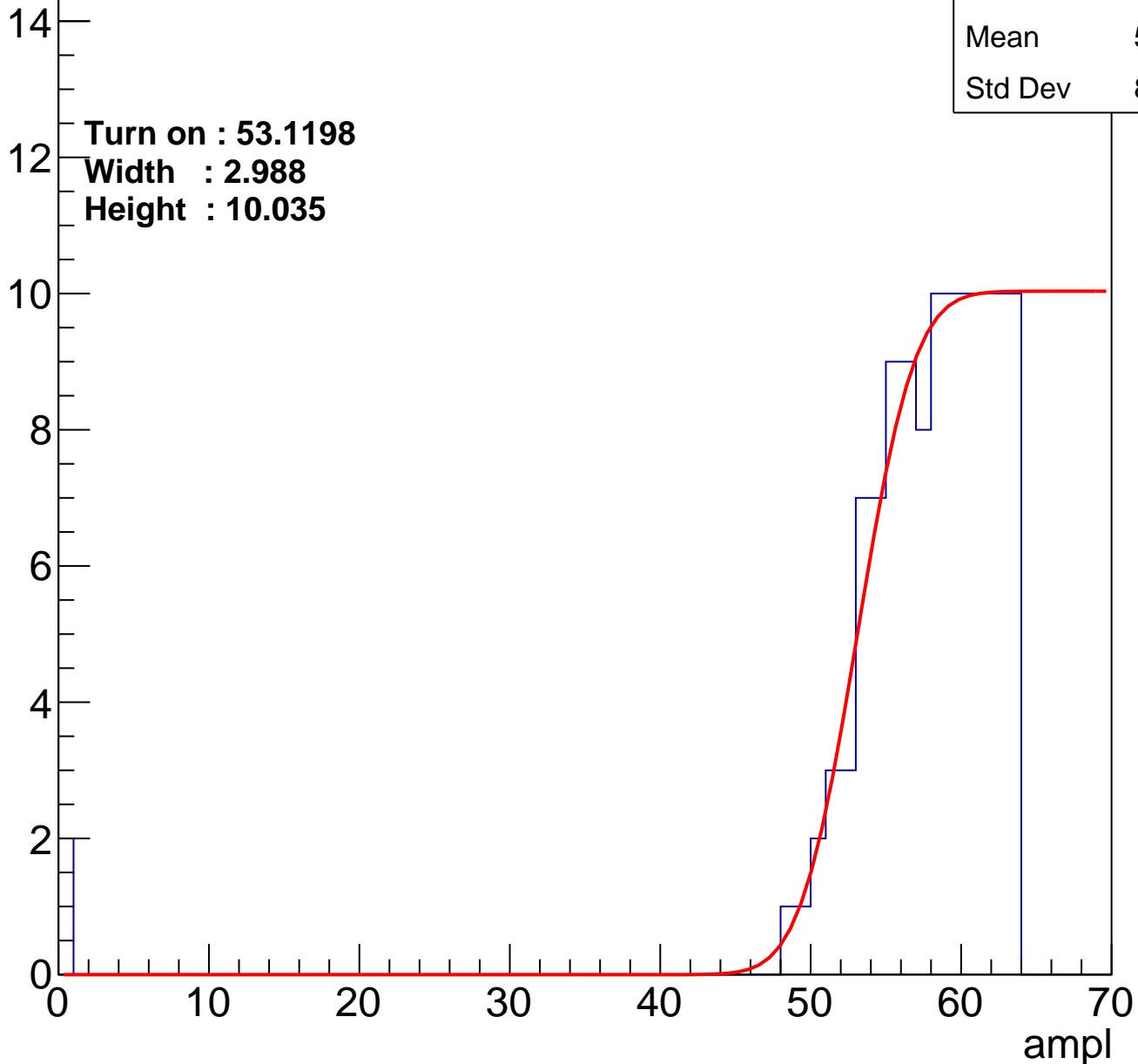
Entries	112
Mean	56.61
Std Dev	8.471

Turn on : 53.1198

Width : 2.988

Height : 10.035

Entry



# B0L103S, U20-ch104

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	56.21
Std Dev	8.292

Turn on : 52.0921

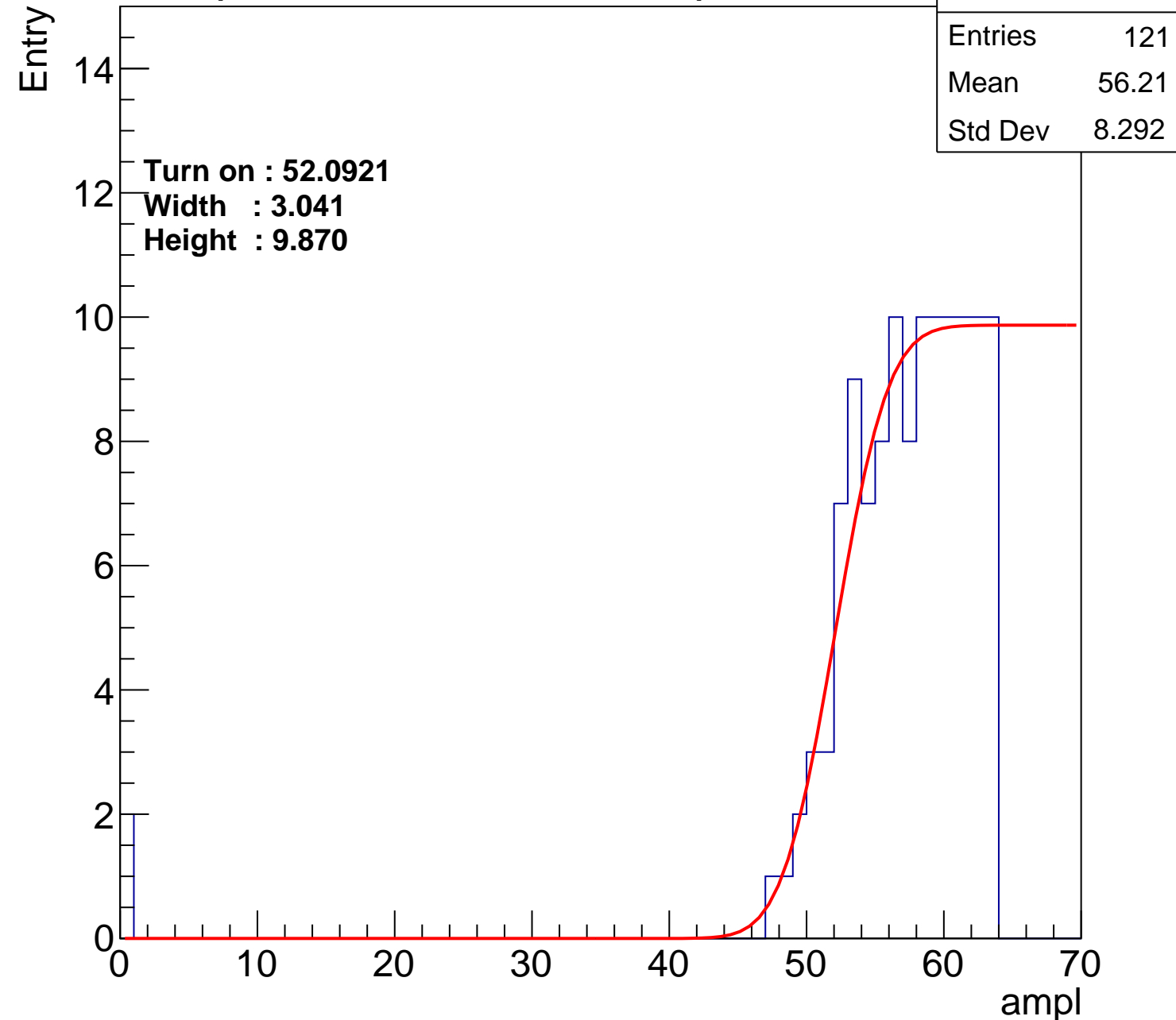
Width : 3.041

Height : 9.870

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch105

calib\_packv5\_040323\_1717.root, FC#2, port C3

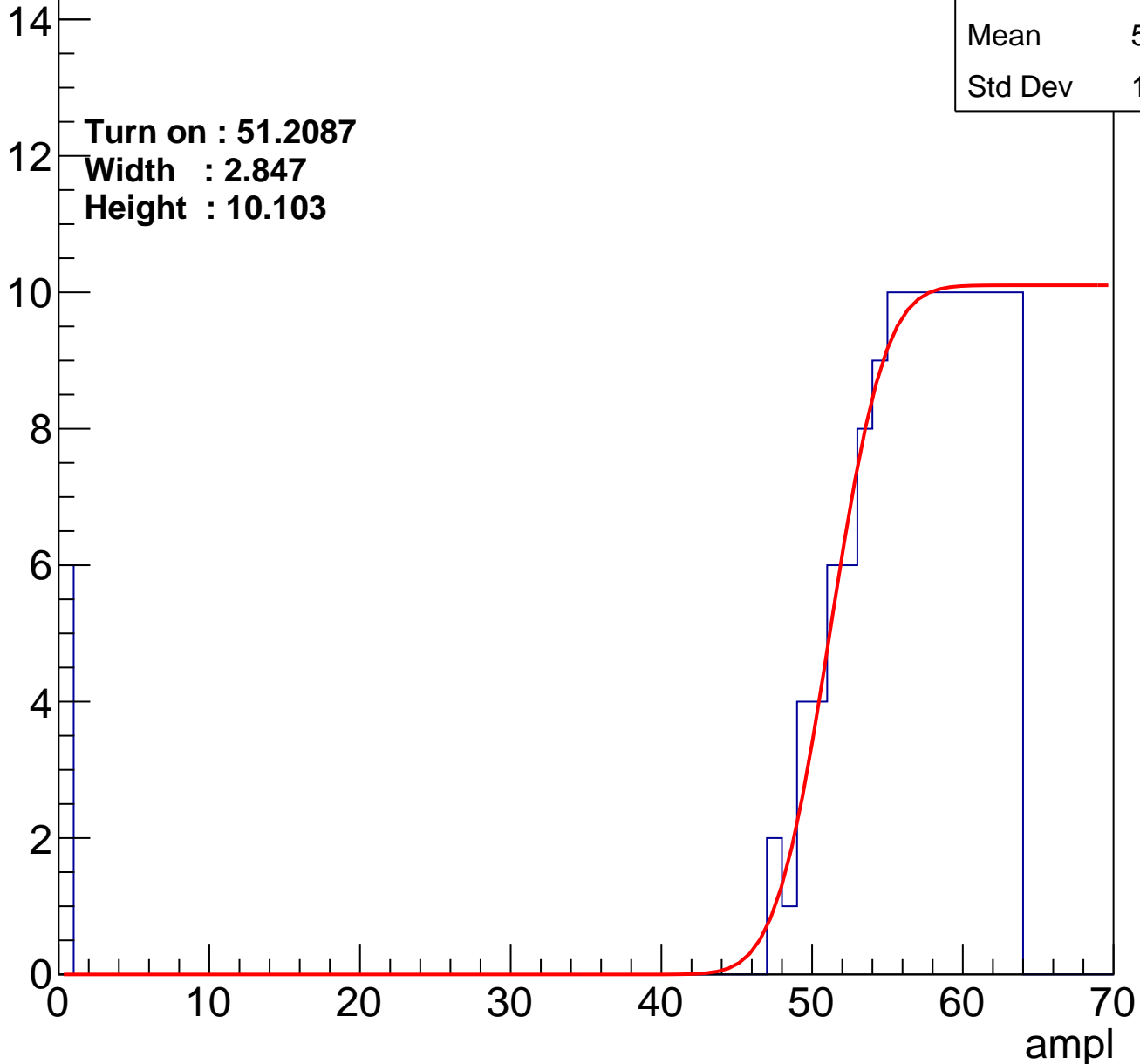
Entries	136
Mean	54.24
Std Dev	12.34

Turn on : 51.2087

Width : 2.847

Height : 10.103

Entry



# B0L103S, U20-ch106

calib\_packv5\_040323\_1717.root, FC#2, port C3

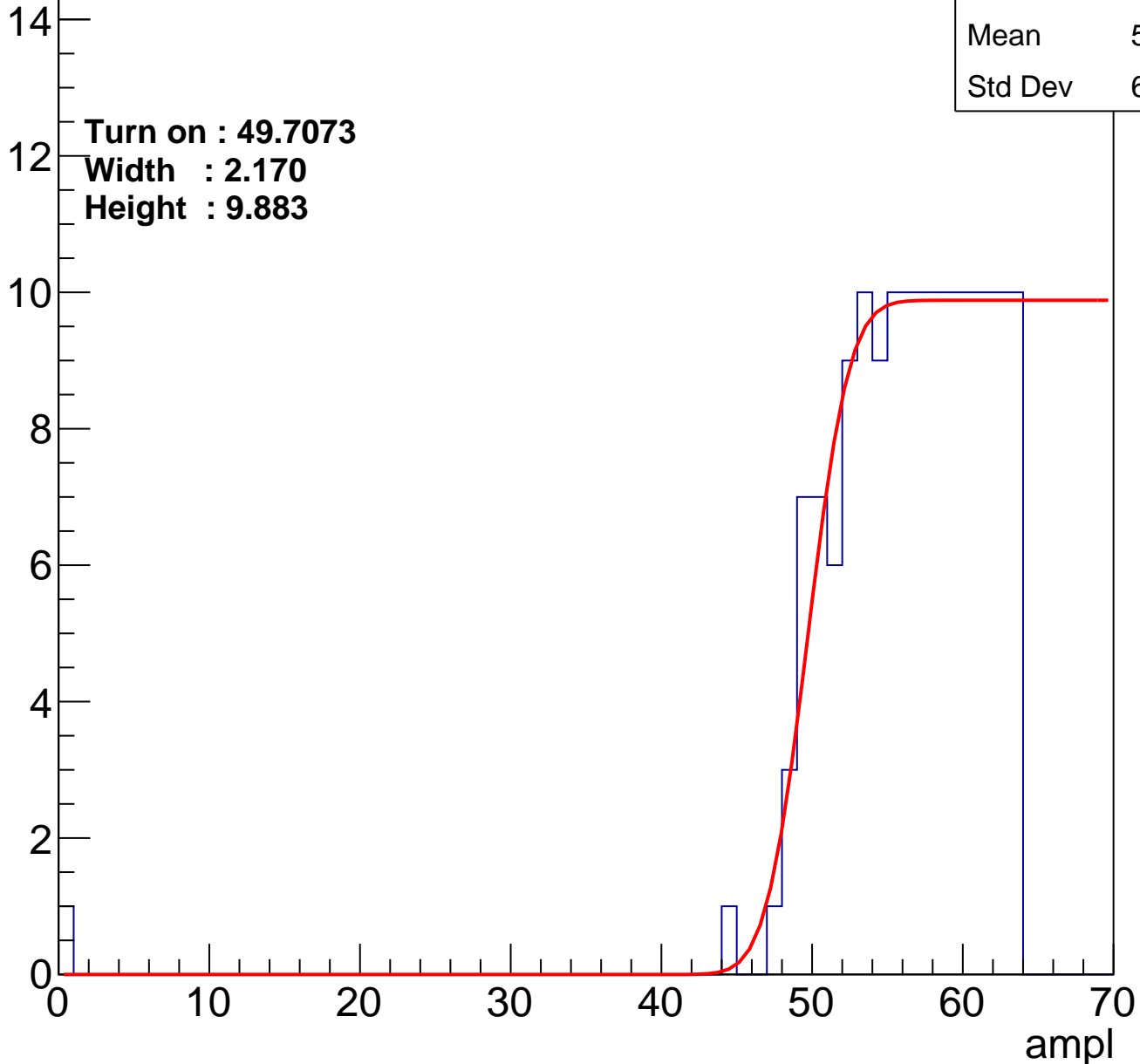
Entries	144
Mean	55.75
Std Dev	6.438

Turn on : 49.7073

Width : 2.170

Height : 9.883

Entry



# B0L103S, U20-ch107

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.46
Std Dev	8.028

Turn on : 50.6049

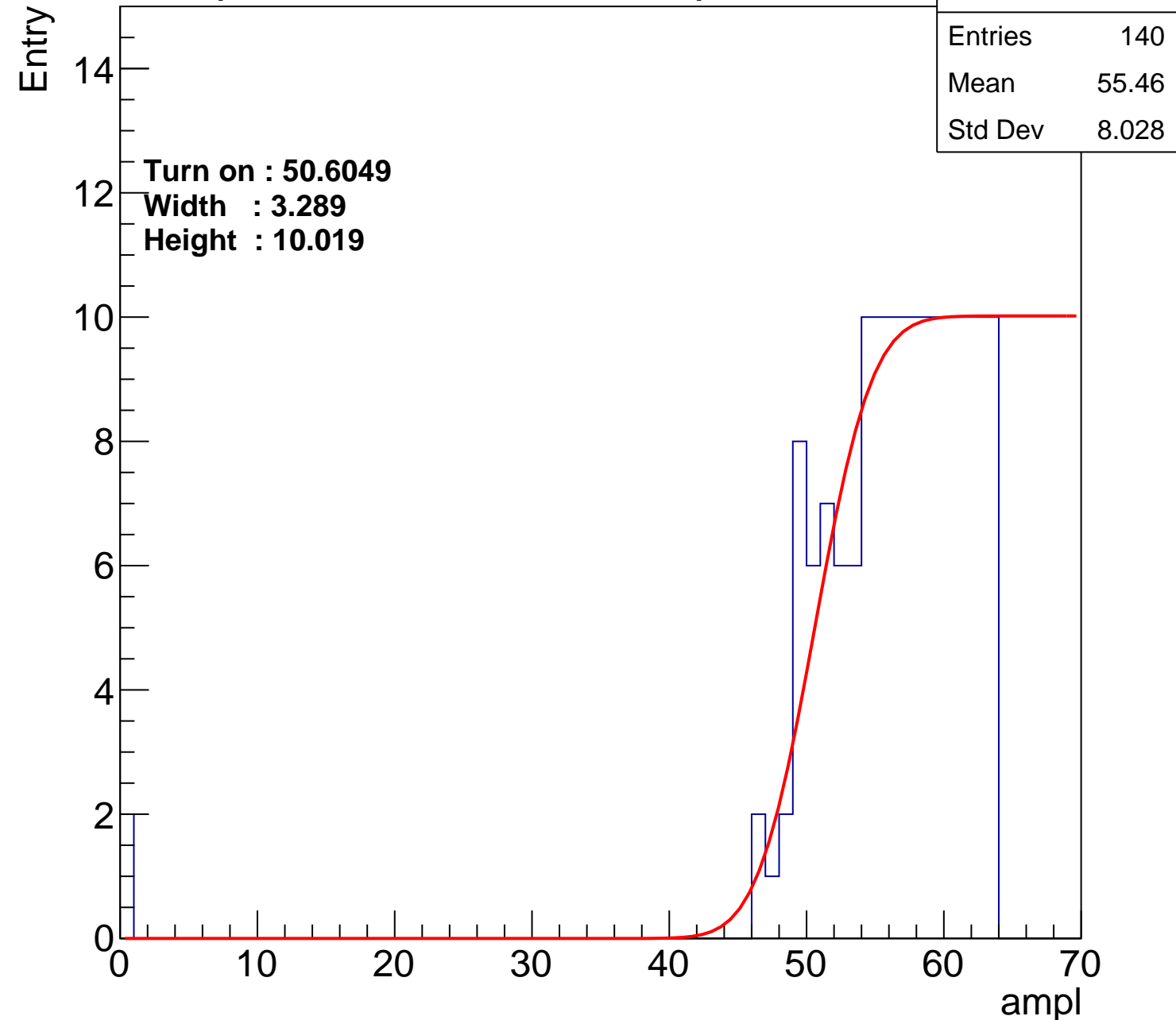
Width : 3.289

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch108

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	55.13
Std Dev	9.22

Turn on : 50.6302

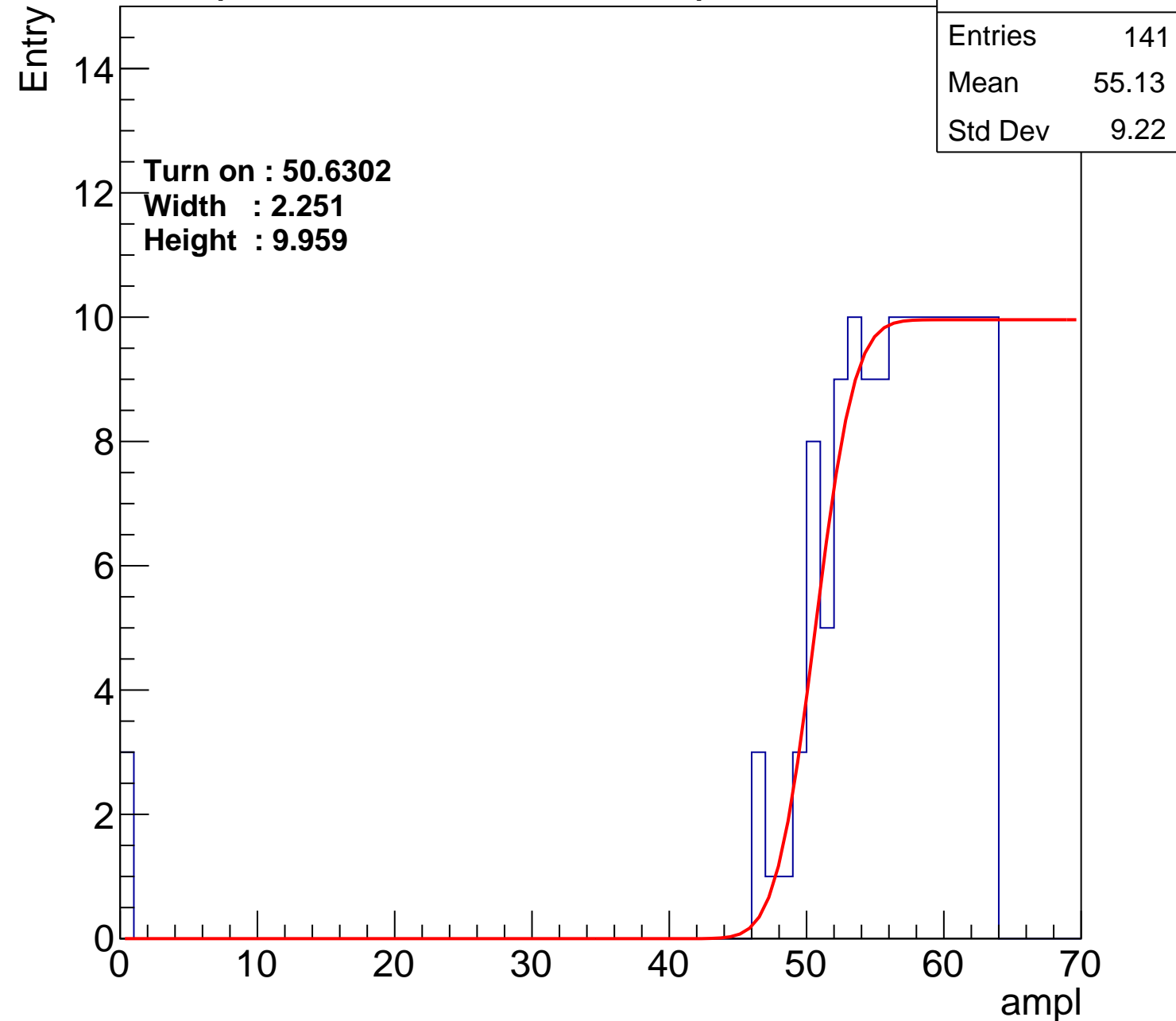
Width : 2.251

Height : 9.959

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch109

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.05
Std Dev	10.55

Turn on : 50.9606

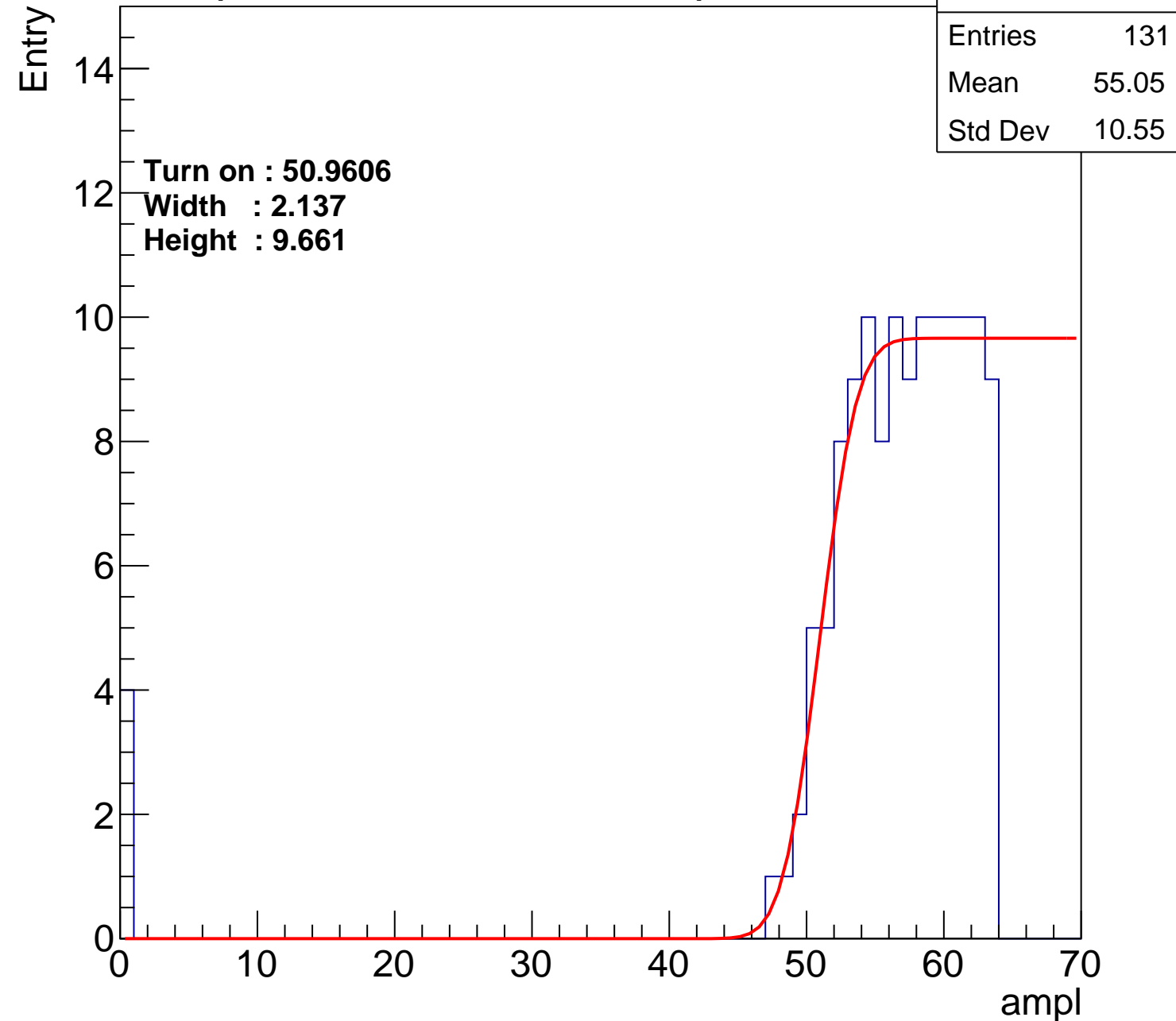
Width : 2.137

Height : 9.661

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch110

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	55.66
Std Dev	9.634

Turn on : 52.4481

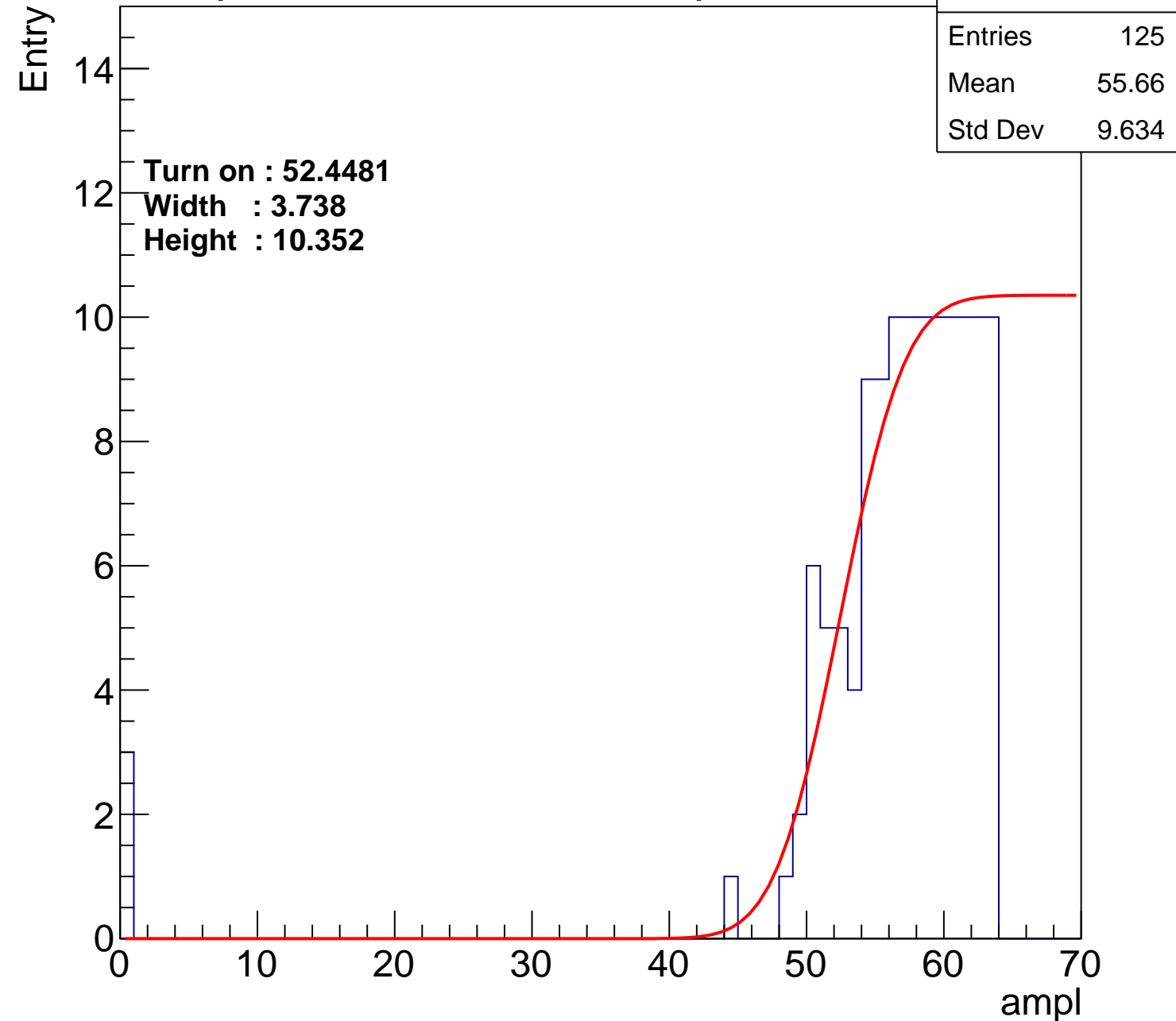
Width : 3.738

Height : 10.352

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch111

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.09
Std Dev	10.55

Turn on : 51.1319

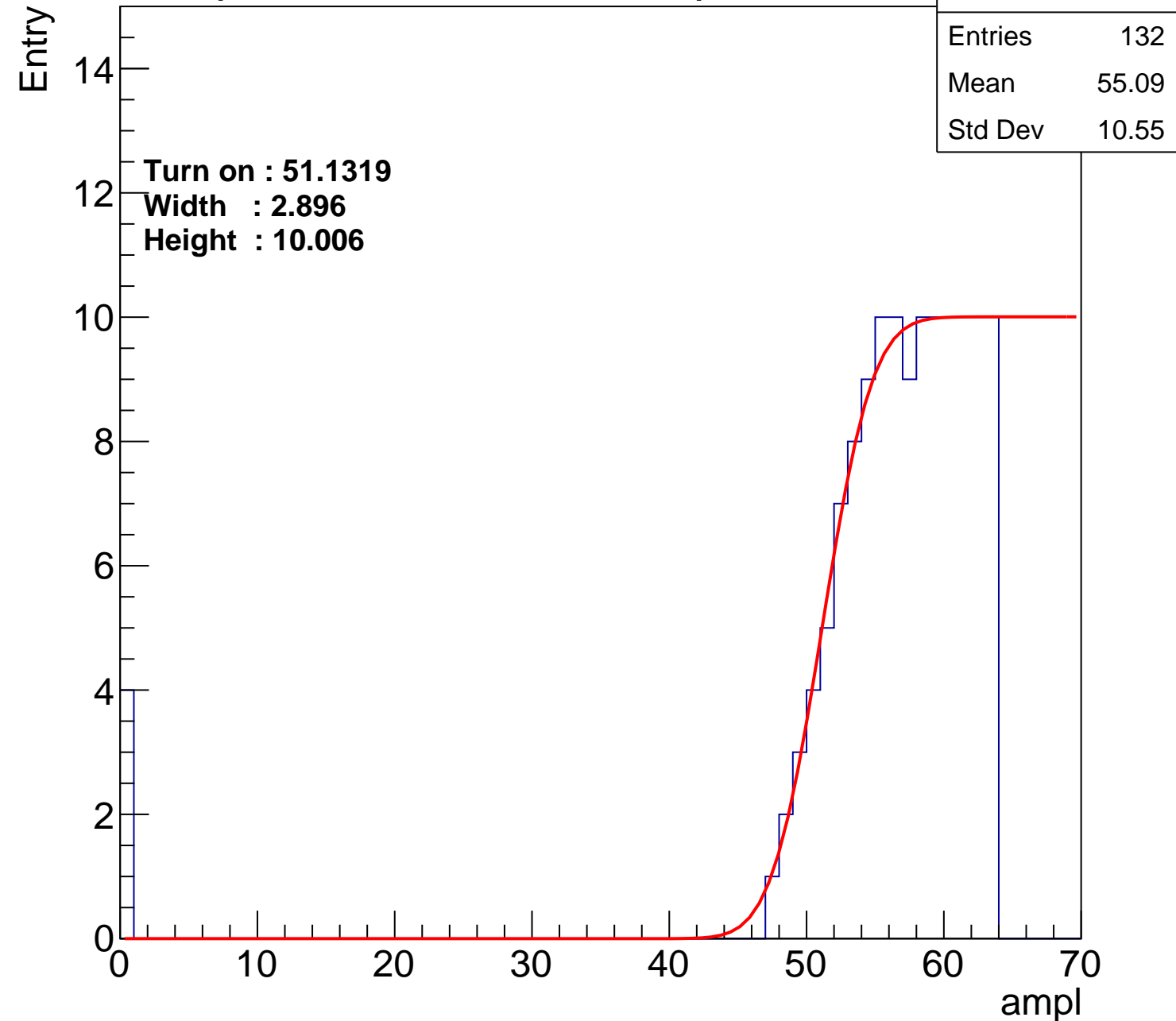
Width : 2.896

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch112

calib\_packv5\_040323\_1717.root, FC#2, port C3

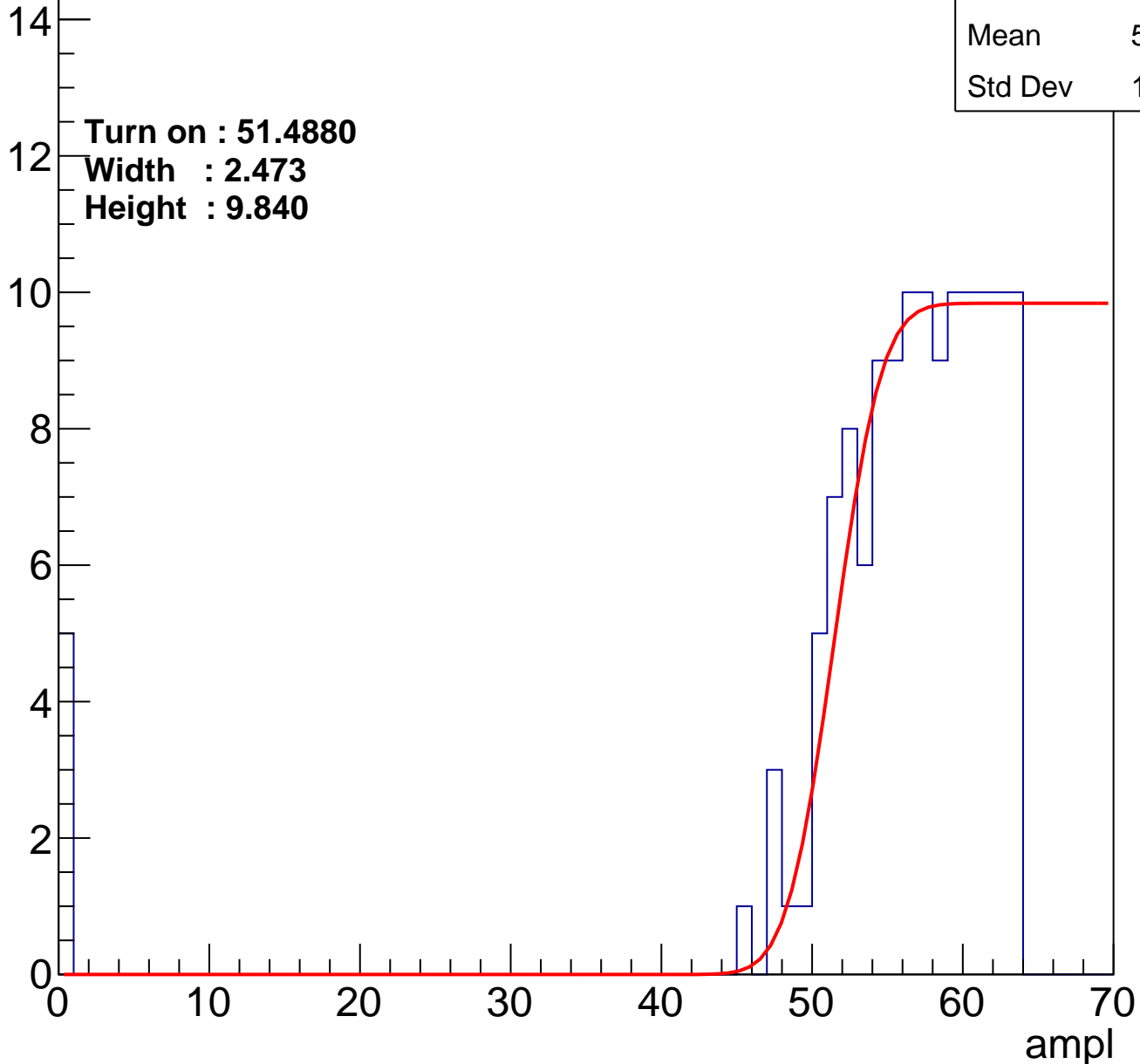
Entries	134
Mean	54.53
Std Dev	11.54

Turn on : 51.4880

Width : 2.473

Height : 9.840

Entry



# B0L103S, U20-ch113

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	55.22
Std Dev	10.72

Turn on : 51.8480

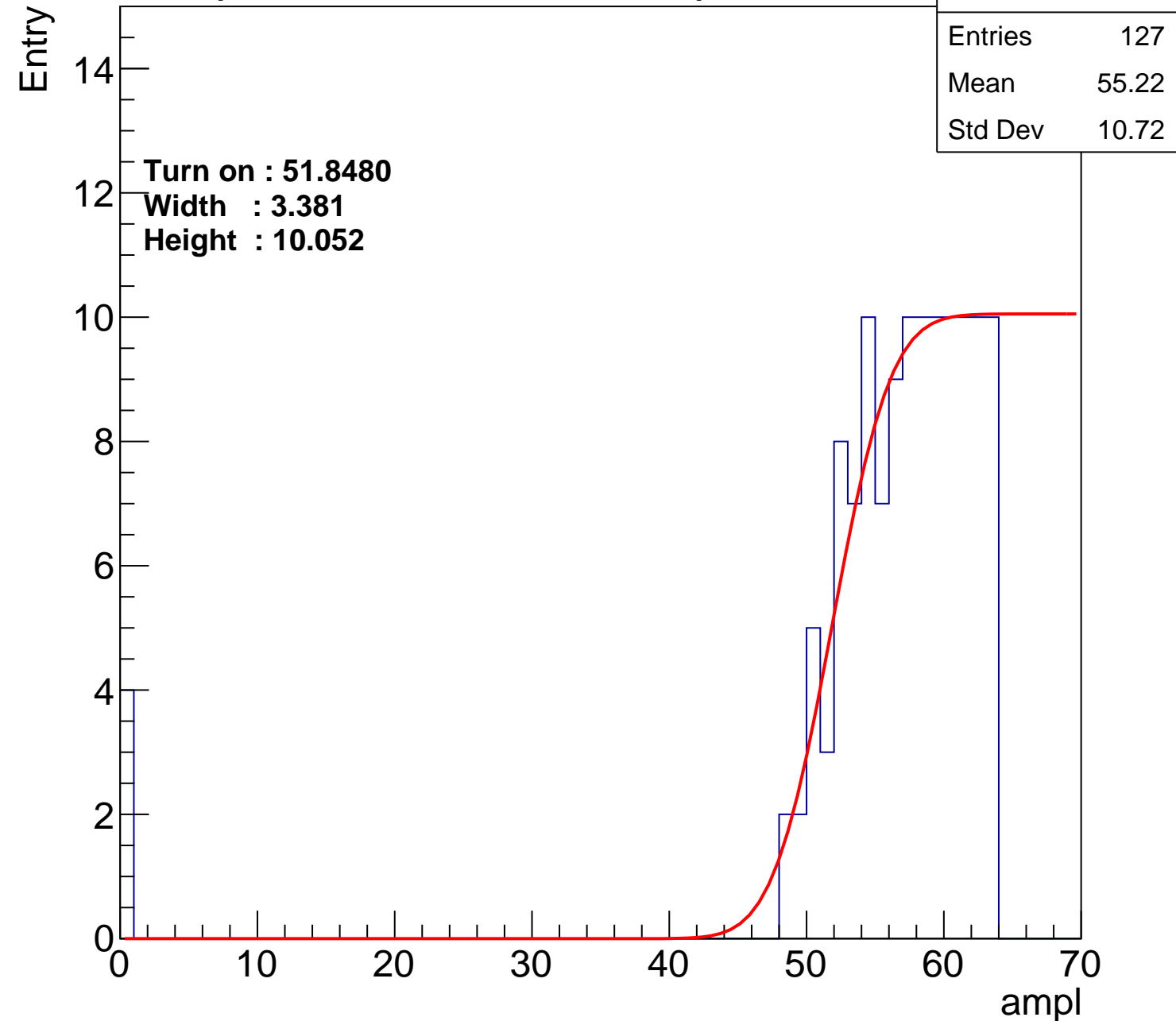
Width : 3.381

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch114

calib\_packv5\_040323\_1717.root, FC#2, port C3

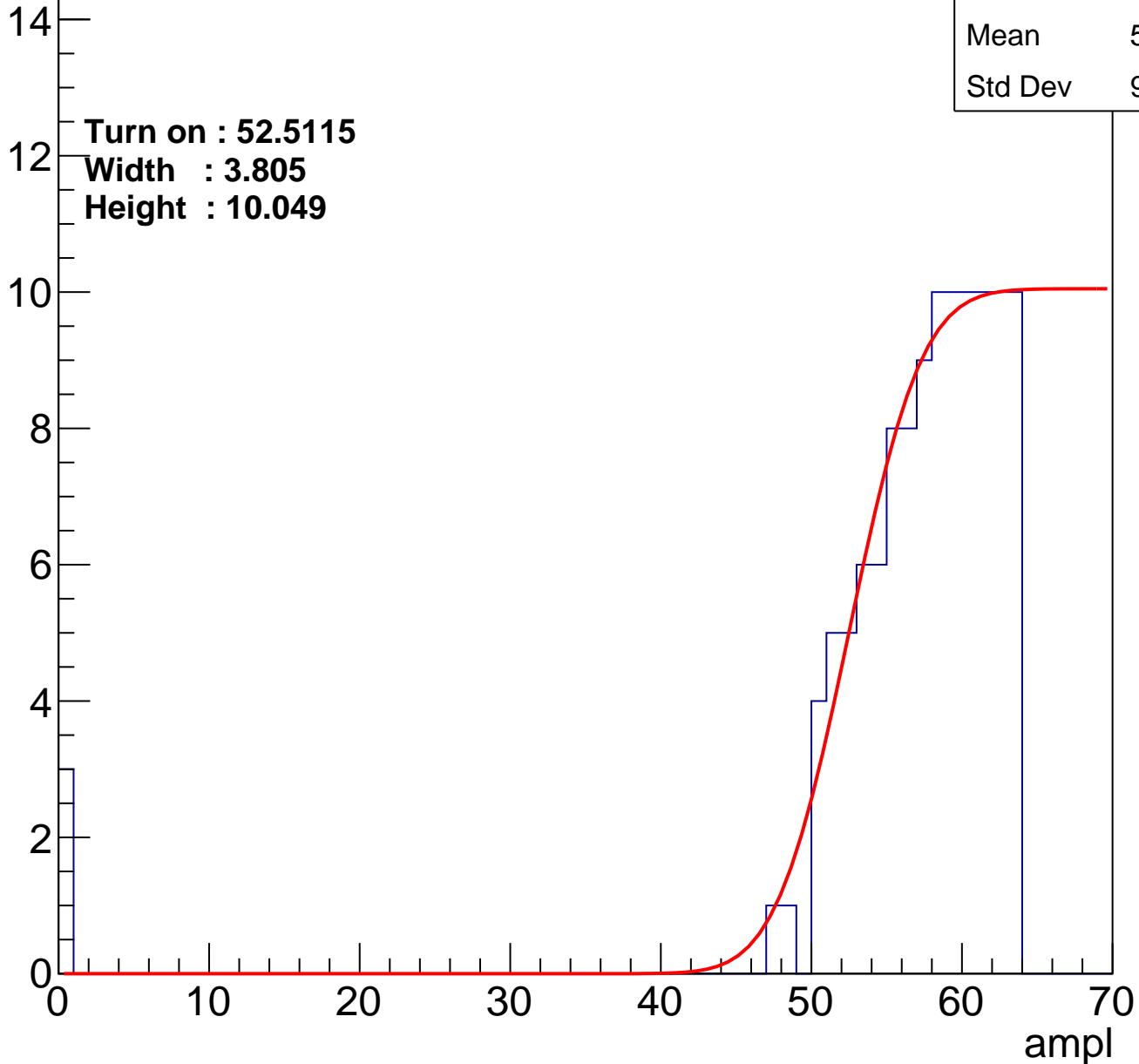
Entries	116
Mean	55.89
Std Dev	9.907

Turn on : 52.5115

Width : 3.805

Height : 10.049

Entry



# B0L103S, U20-ch115

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.46
Std Dev	10.92

Turn on : 52.6289

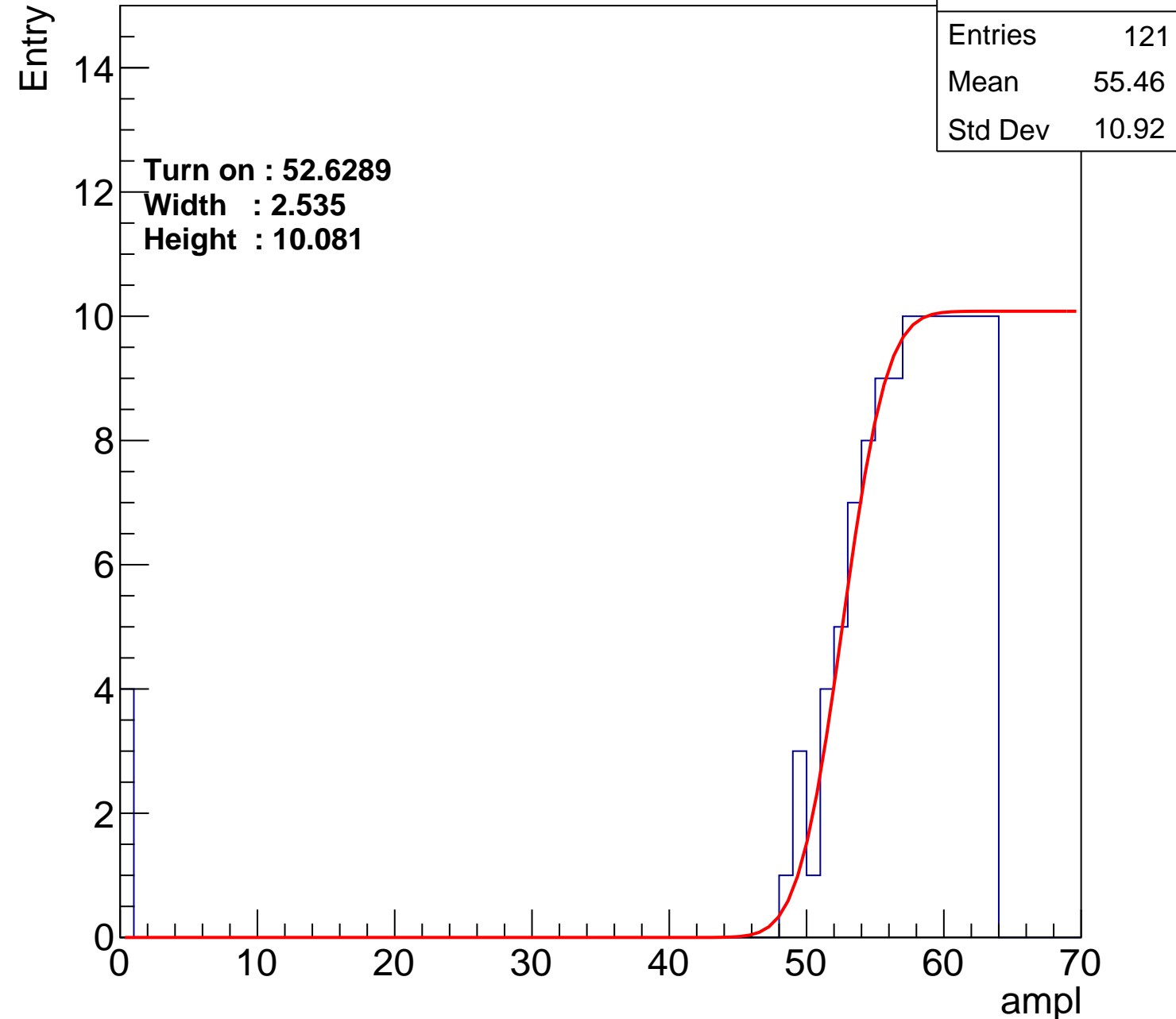
Width : 2.535

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch116

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	56.01
Std Dev	10.02

Turn on : 53.9186

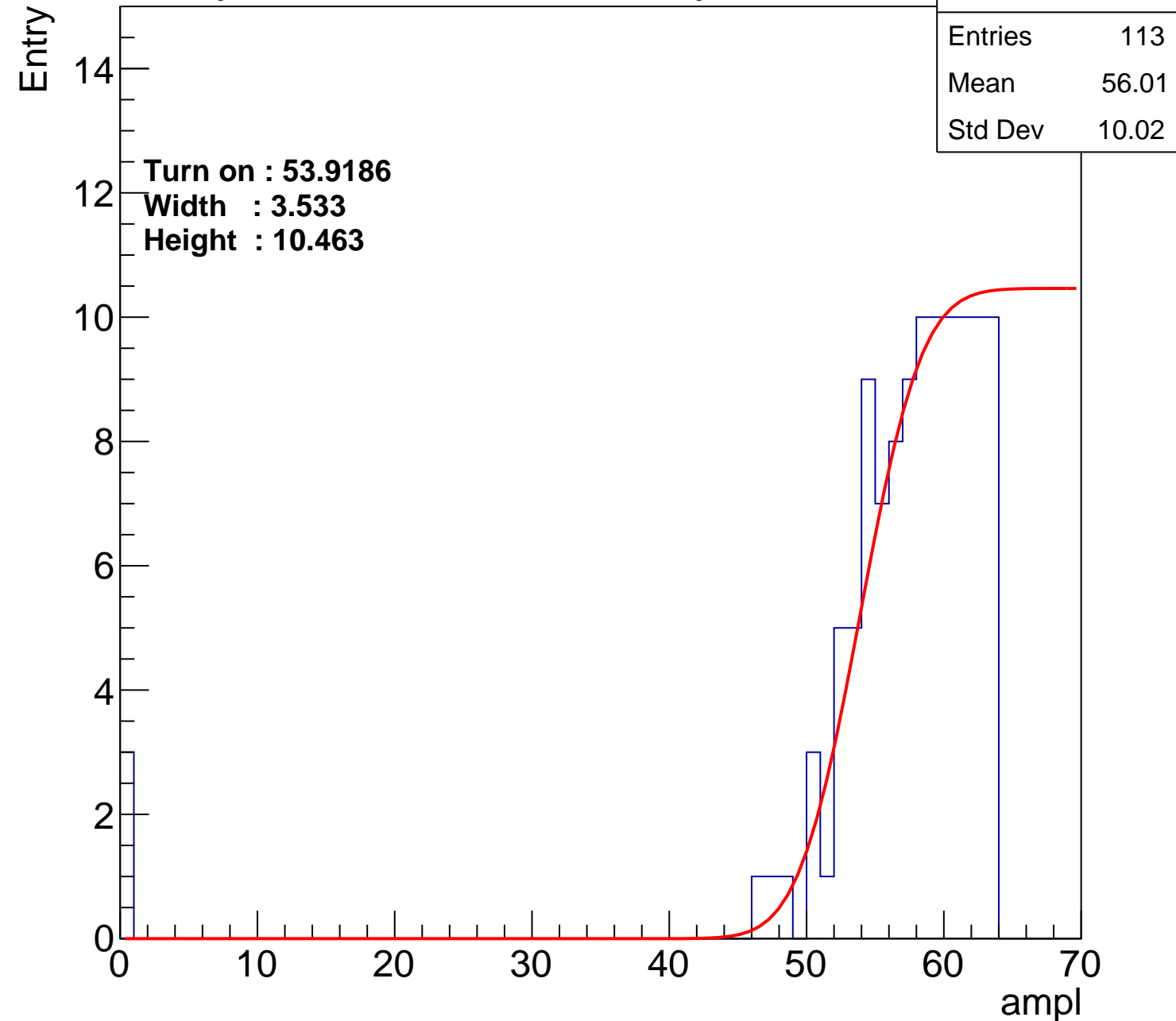
Width : 3.533

Height : 10.463

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch117

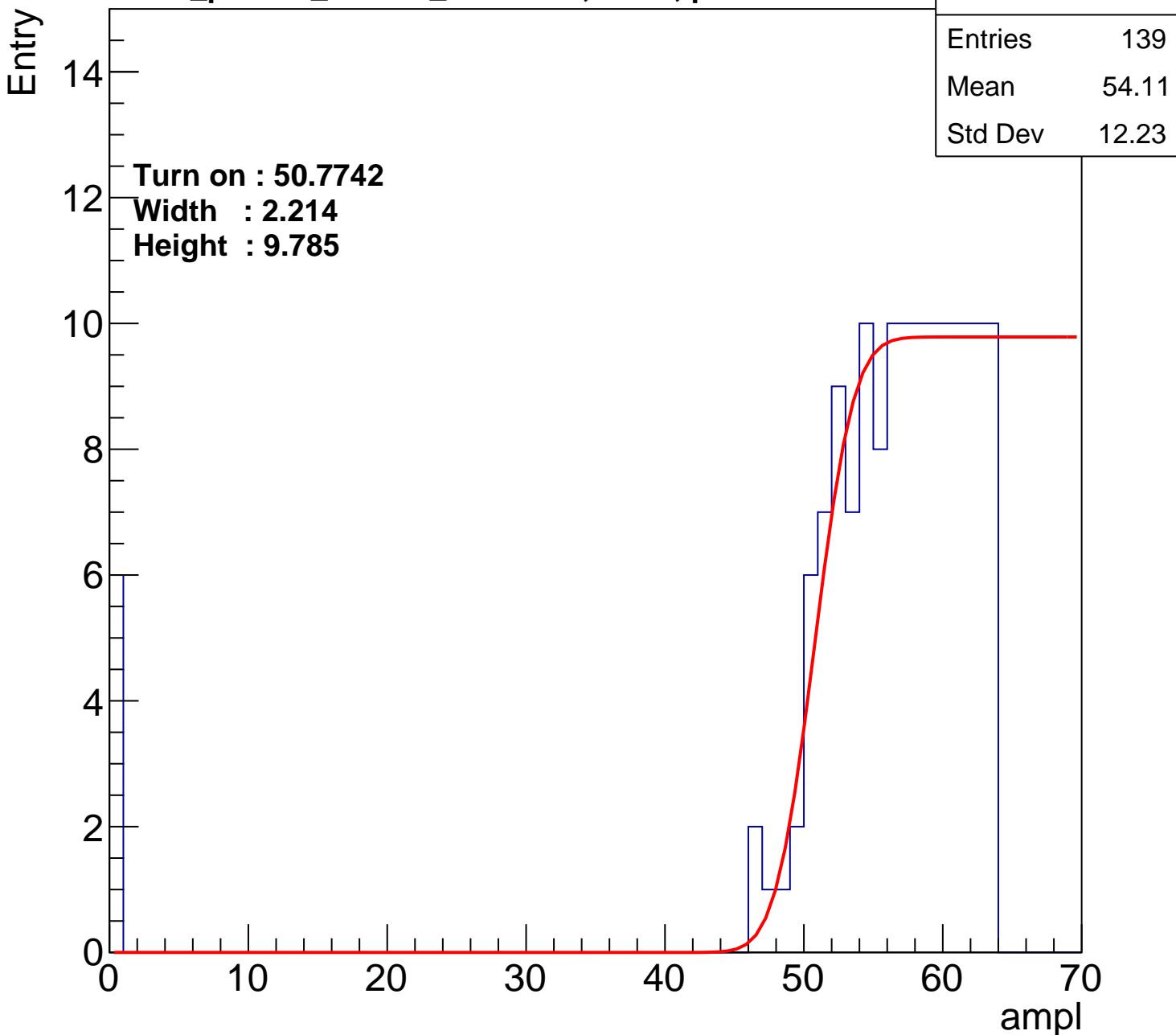
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

**Turn on : 50.7742**

**Width : 2.214**

**Height : 9.785**

Entries	139
Mean	54.11
Std Dev	12.23



# B0L103S, U20-ch118

calib\_packv5\_040323\_1717.root, FC#2, port C3

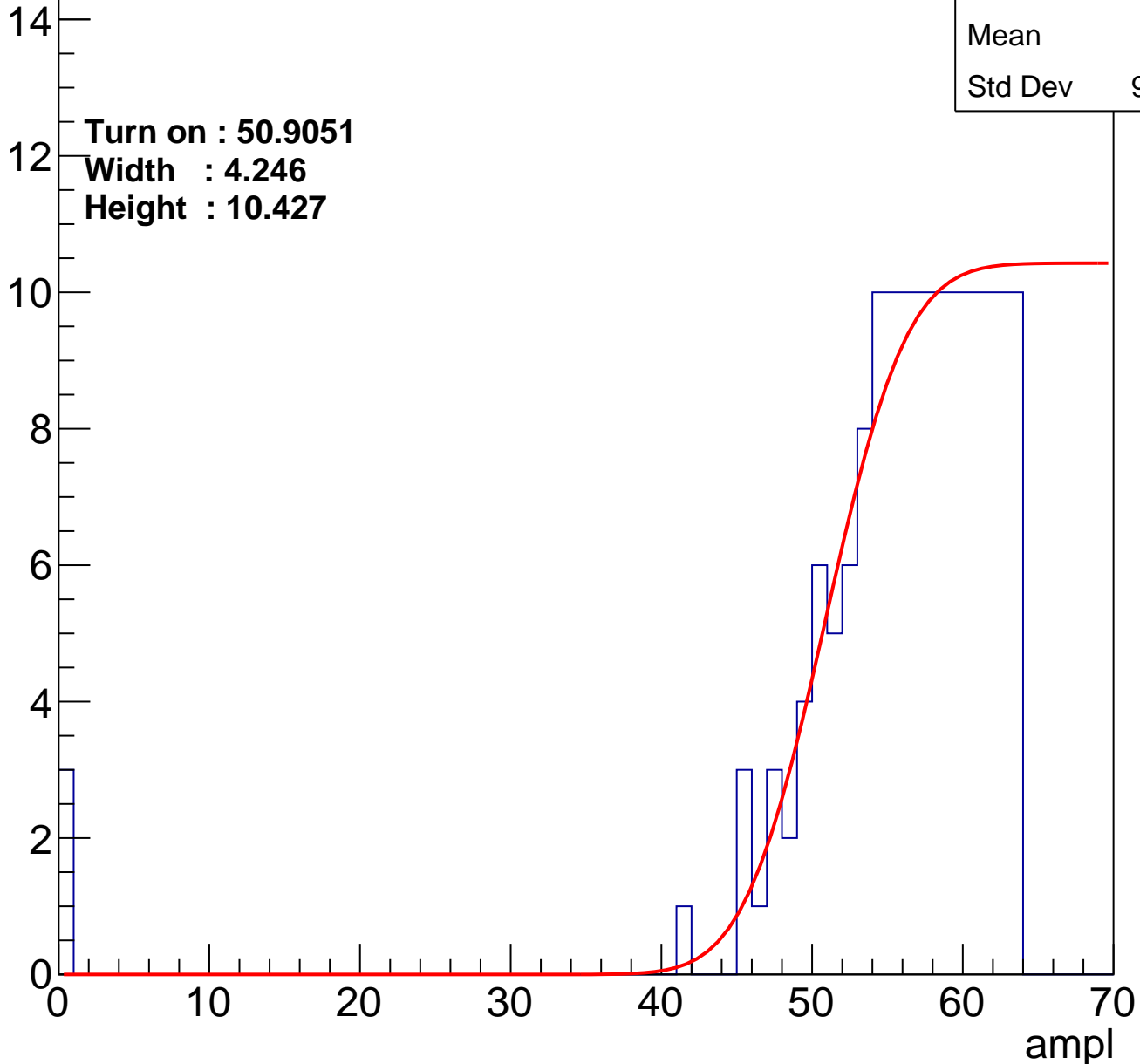
Entries	142
Mean	54.9
Std Dev	9.364

Turn on : 50.9051

Width : 4.246

Height : 10.427

Entry





# B0L103S, U20-ch119

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	114
Mean	56.11
Std Dev	9.899

Turn on : 53.0705

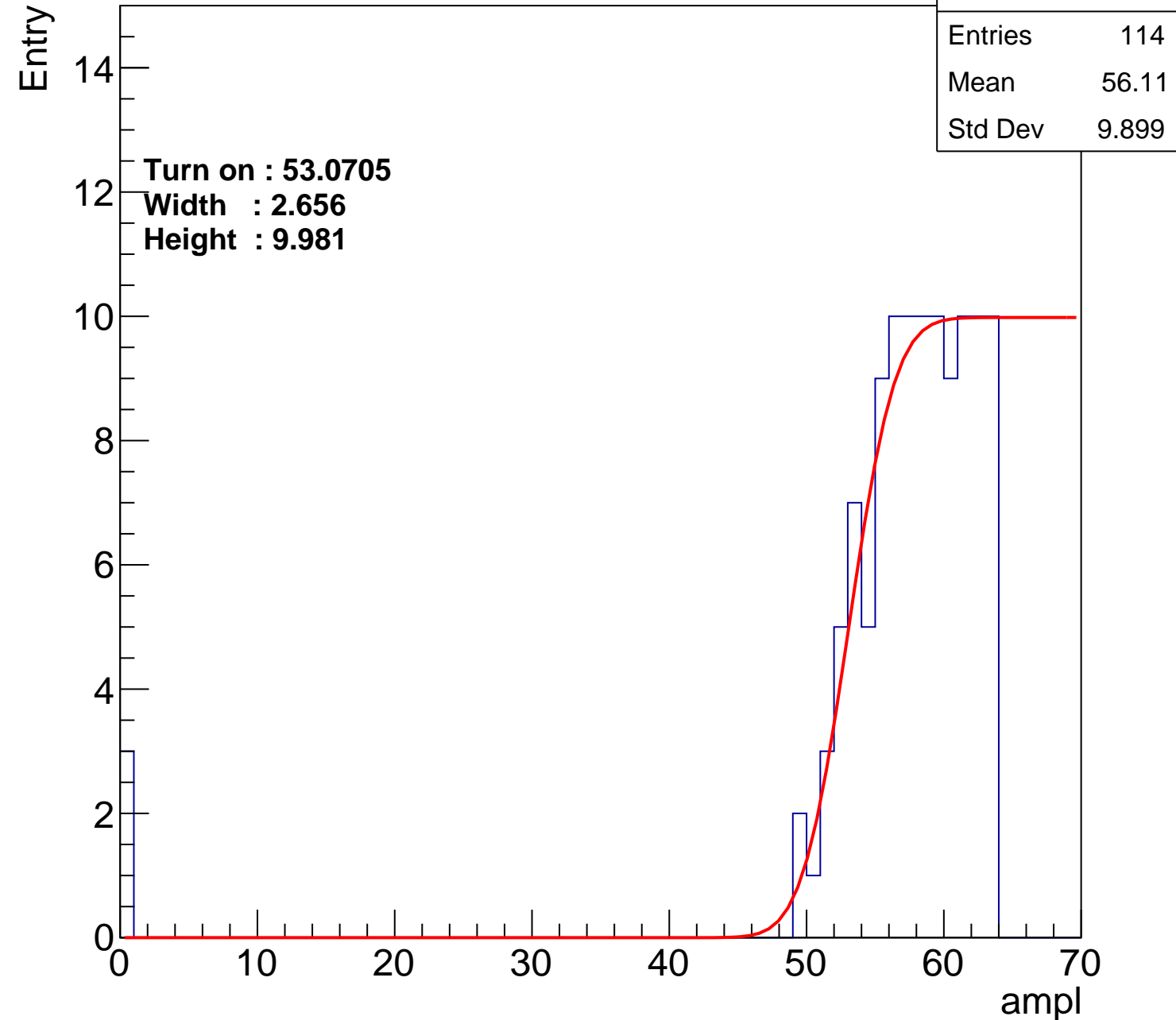
Width : 2.656

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch120

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	56.28
Std Dev	8.447

Turn on : 52.2317

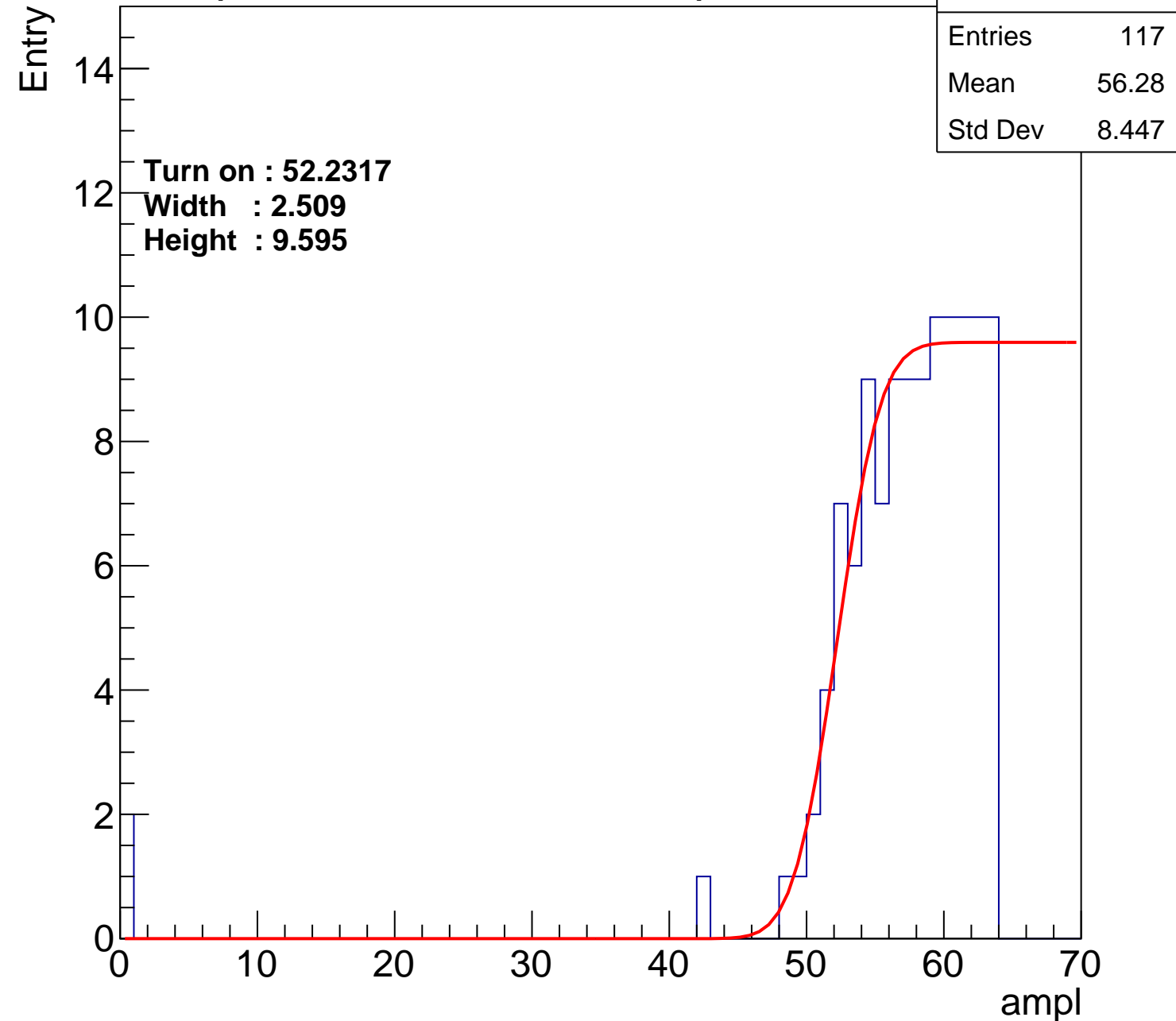
Width : 2.509

Height : 9.595

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch121

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	54.66
Std Dev	10.23

Turn on : 50.1135

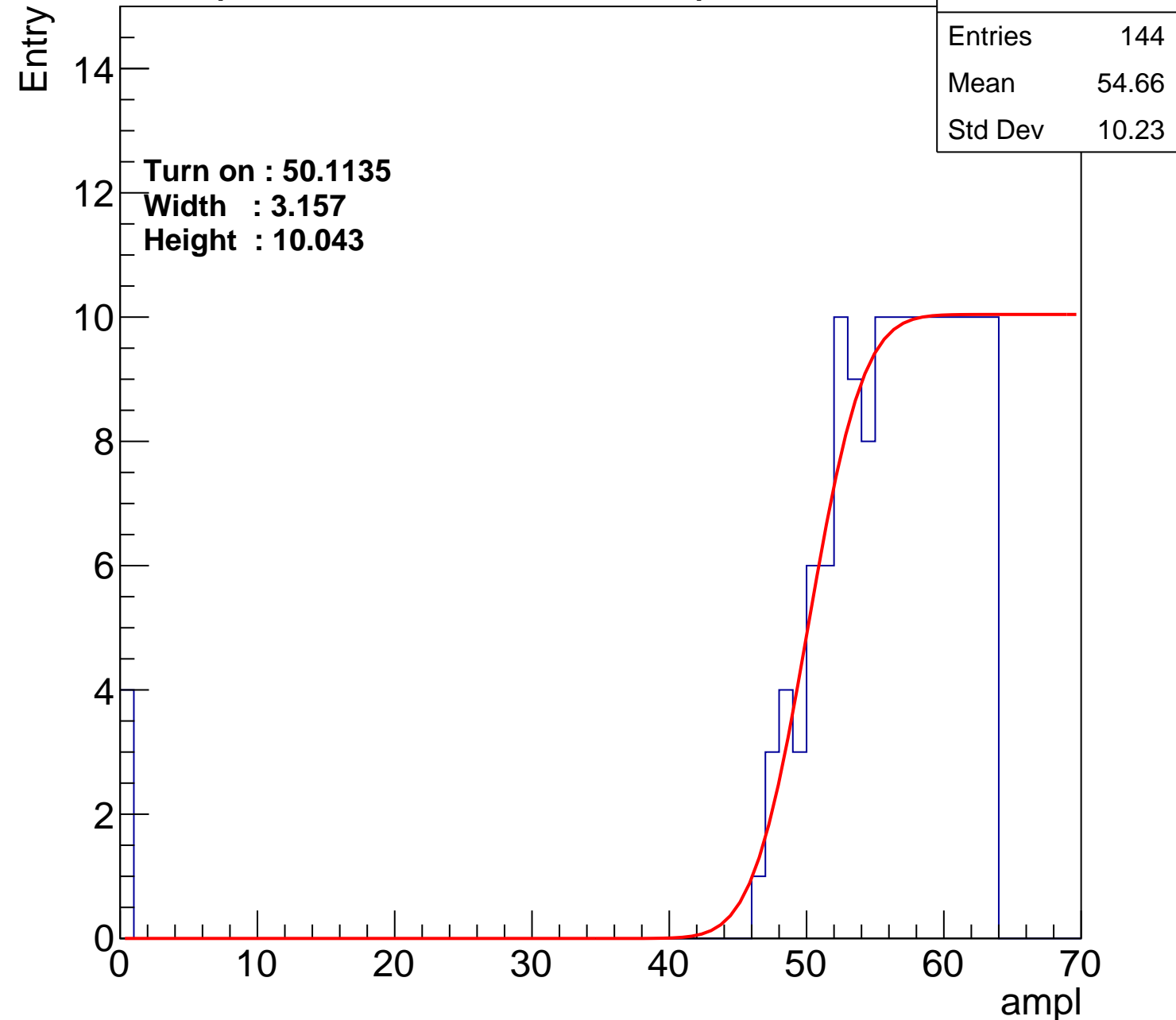
Width : 3.157

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch122

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	55.39
Std Dev	9.329

Turn on : 50.6826

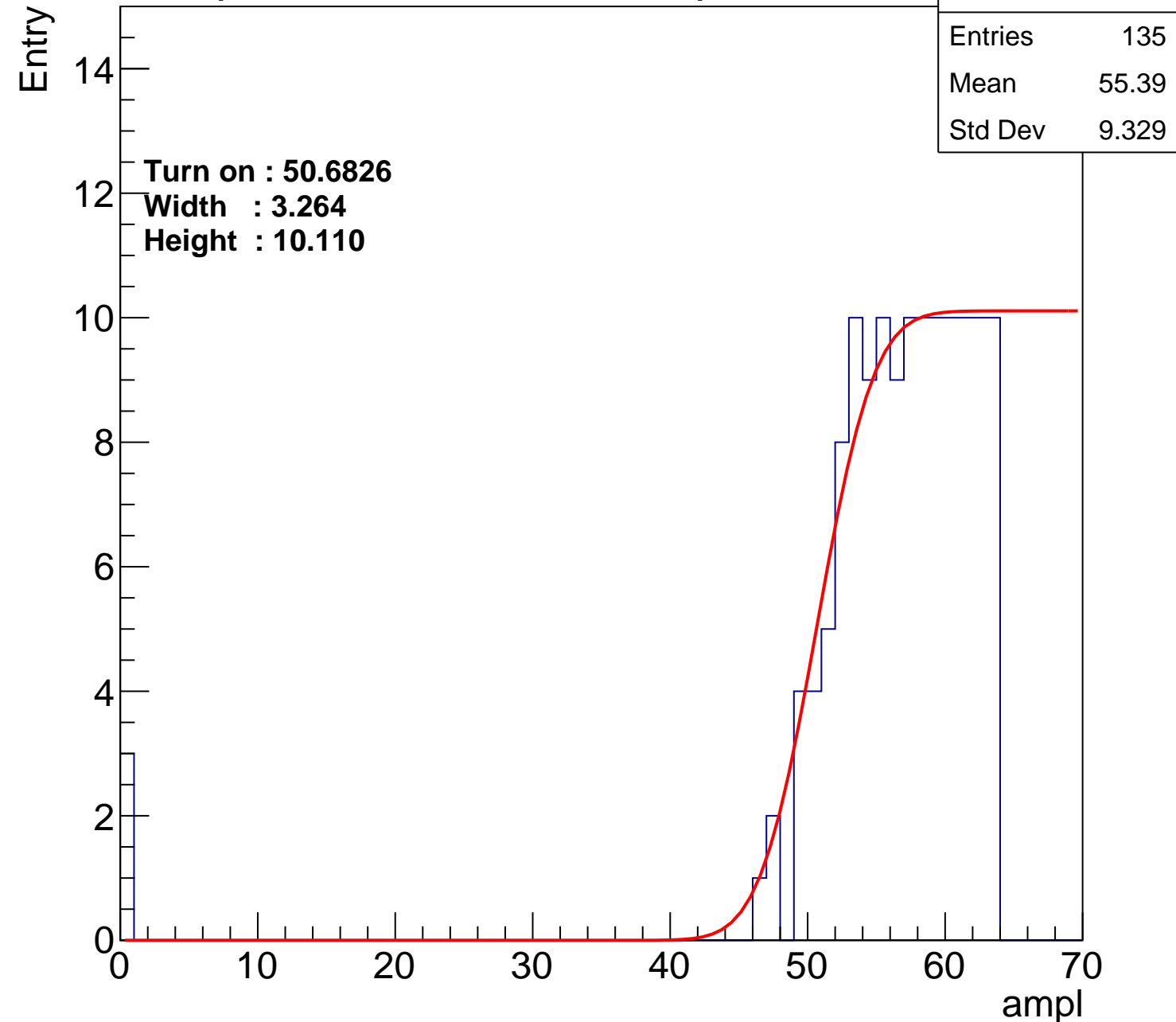
Width : 3.264

Height : 10.110

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch123

calib\_packv5\_040323\_1717.root, FC#2, port C3

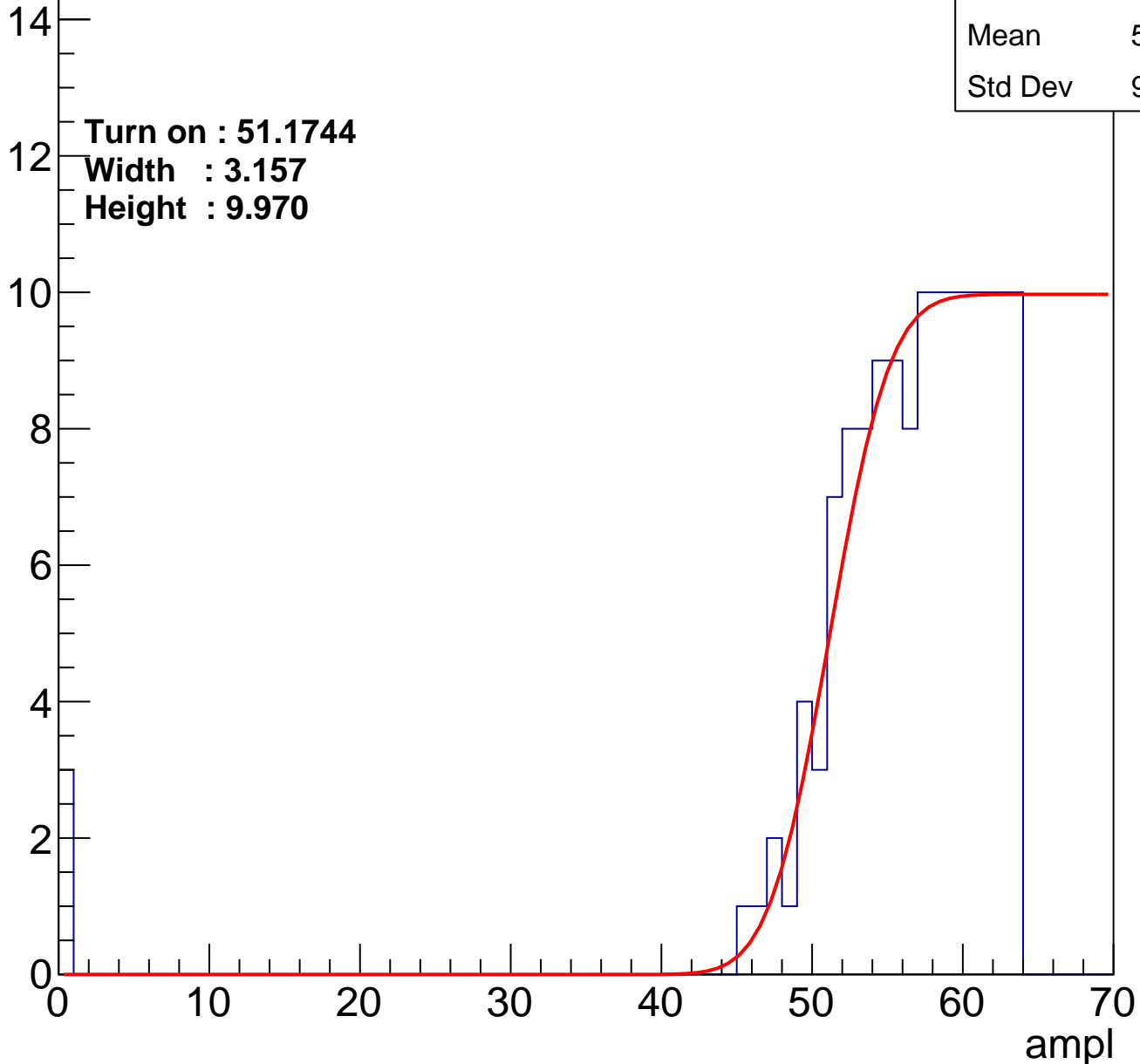
Entries	134
Mean	55.26
Std Dev	9.426

Turn on : 51.1744

Width : 3.157

Height : 9.970

Entry



# B0L103S, U20-ch124

calib\_packv5\_040323\_1717.root, FC#2, port C3

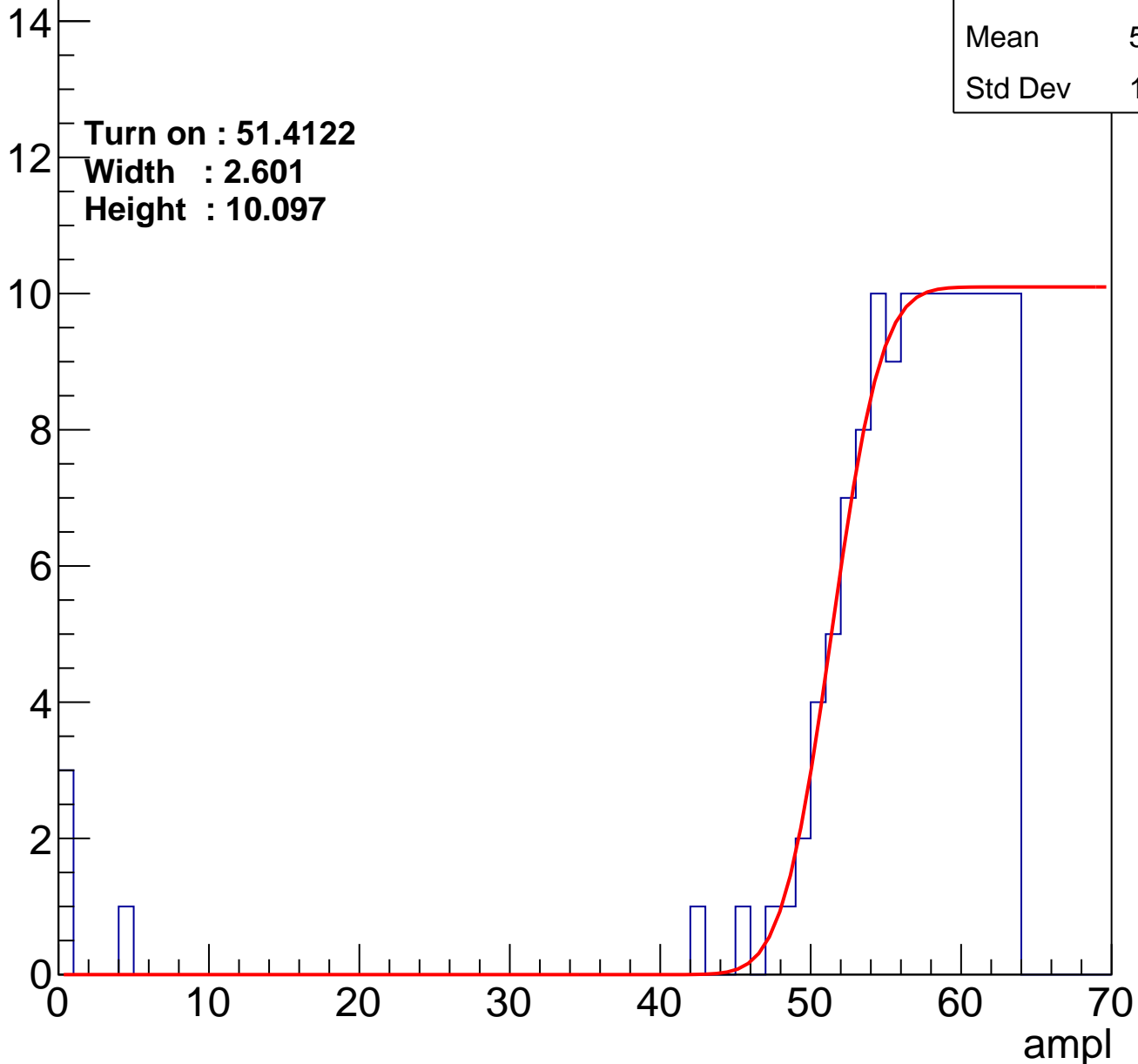
Entries	133
Mean	55.05
Std Dev	10.43

Turn on : 51.4122

Width : 2.601

Height : 10.097

Entry



# B0L103S, U20-ch125

calib\_packv5\_040323\_1717.root, FC#2, port C3

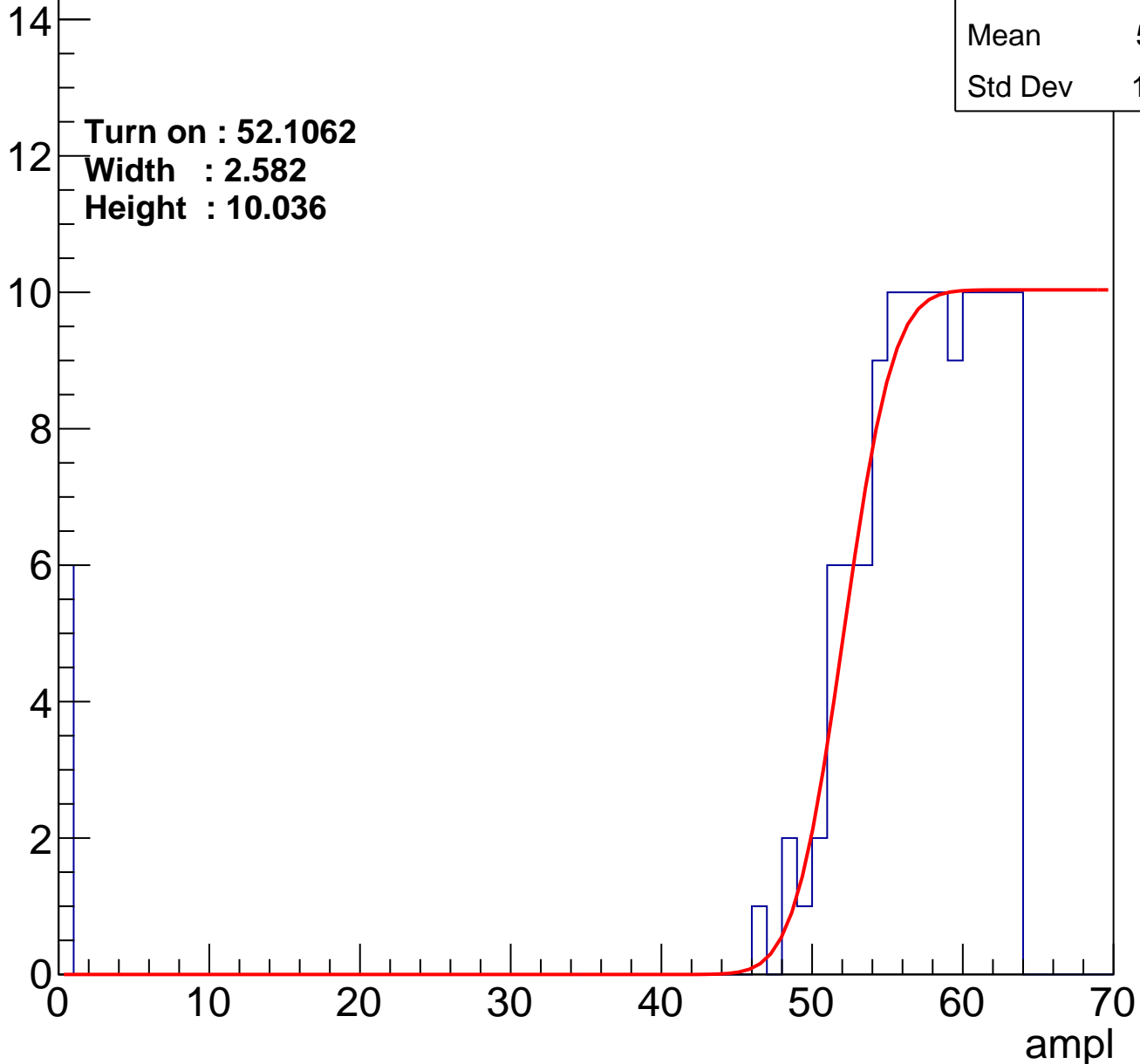
Entries	128
Mean	54.41
Std Dev	12.68

Turn on : 52.1062

Width : 2.582

Height : 10.036

Entry



# B0L103S, U20-ch126

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.6
Std Dev	9.39

Turn on : 51.2972

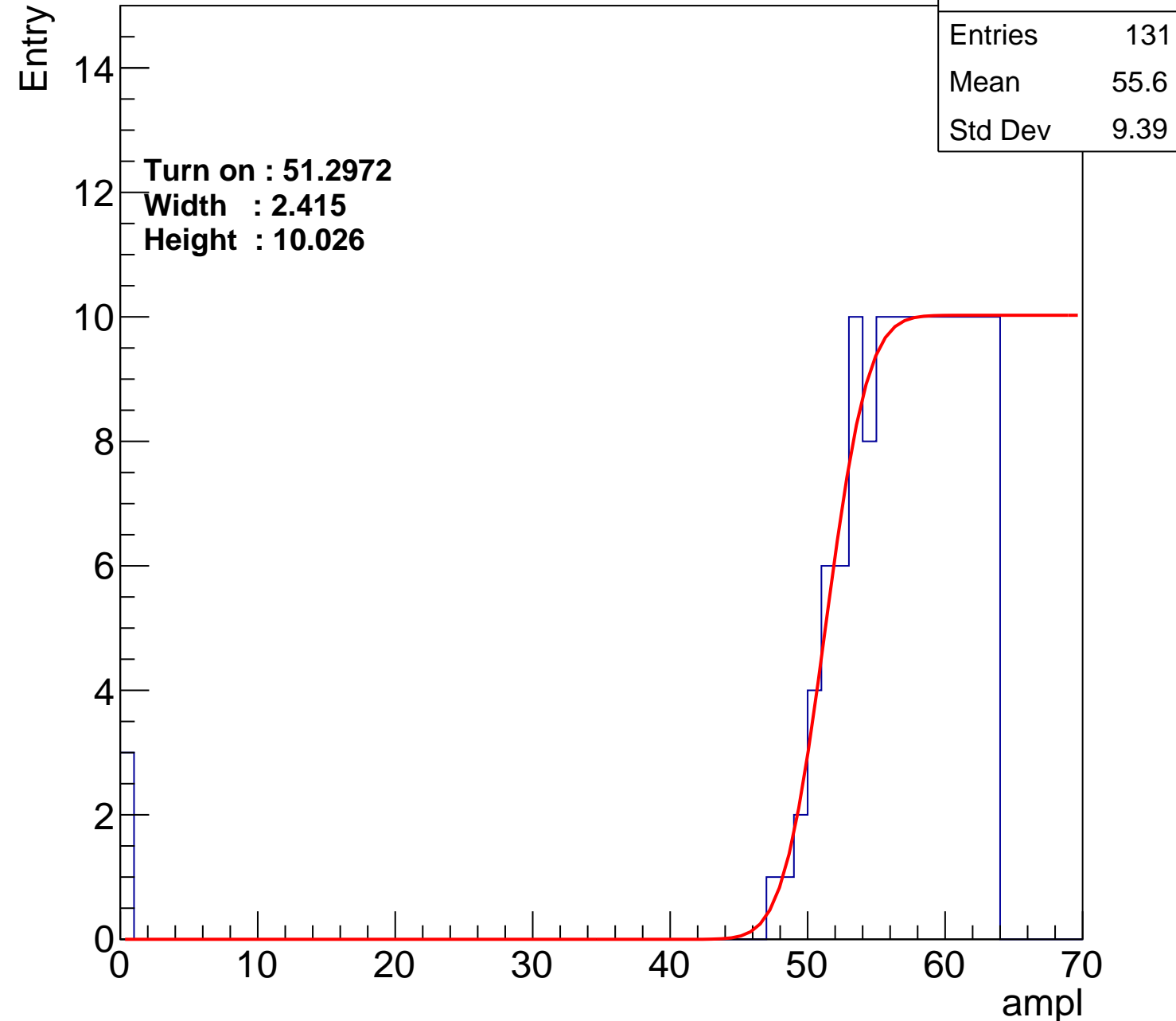
Width : 2.415

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U20-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	142
Mean	54.32
Std Dev	11.27

Turn on : 50.4295

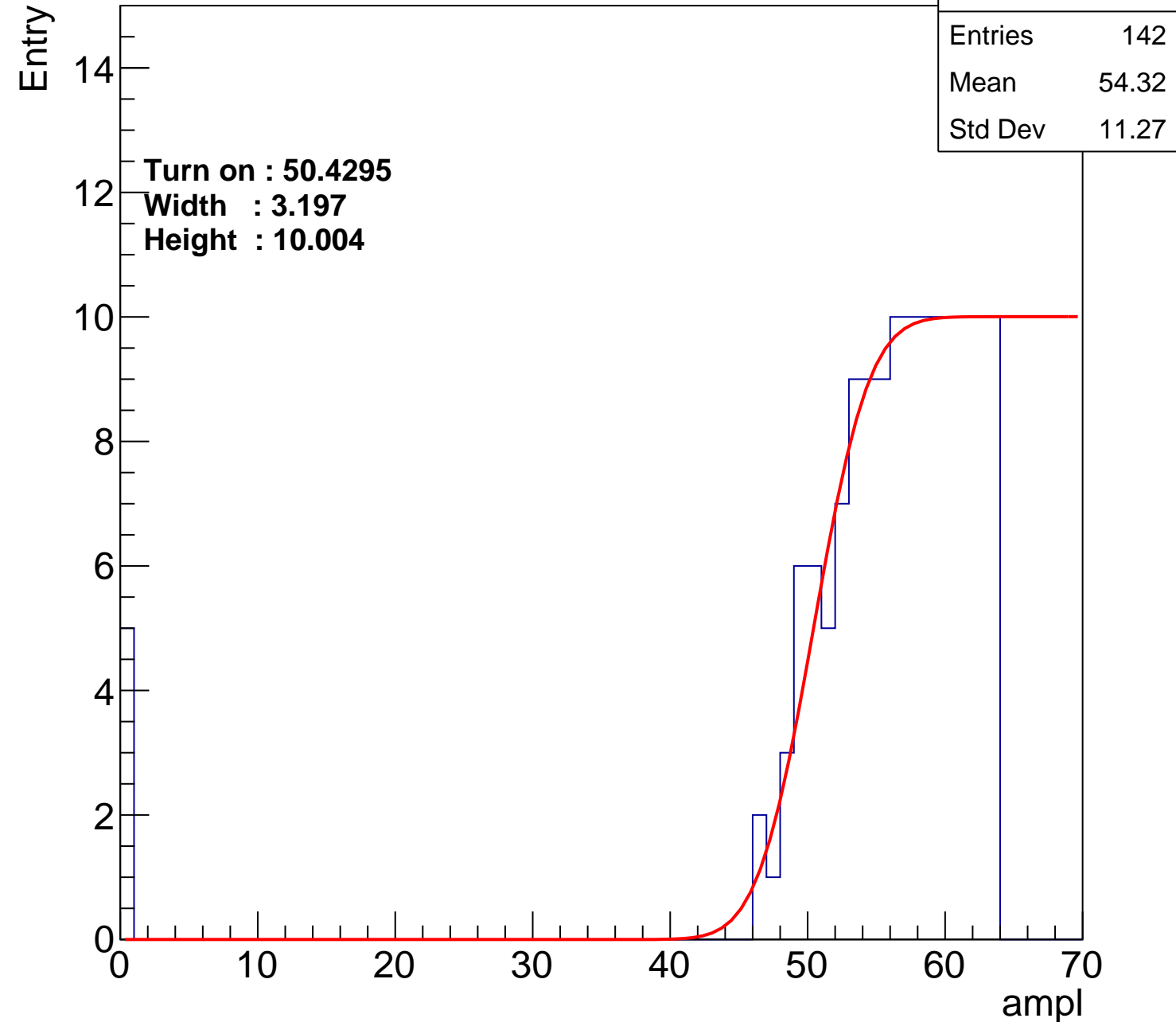
Width : 3.197

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U20-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	142
Mean	54.32
Std Dev	11.27

Turn on : 50.4295

Width : 3.197

Height : 10.004

Entry

