

B1L001S, U25-ch0

calib_packv5_042523_0143.root, FC#2, port C2

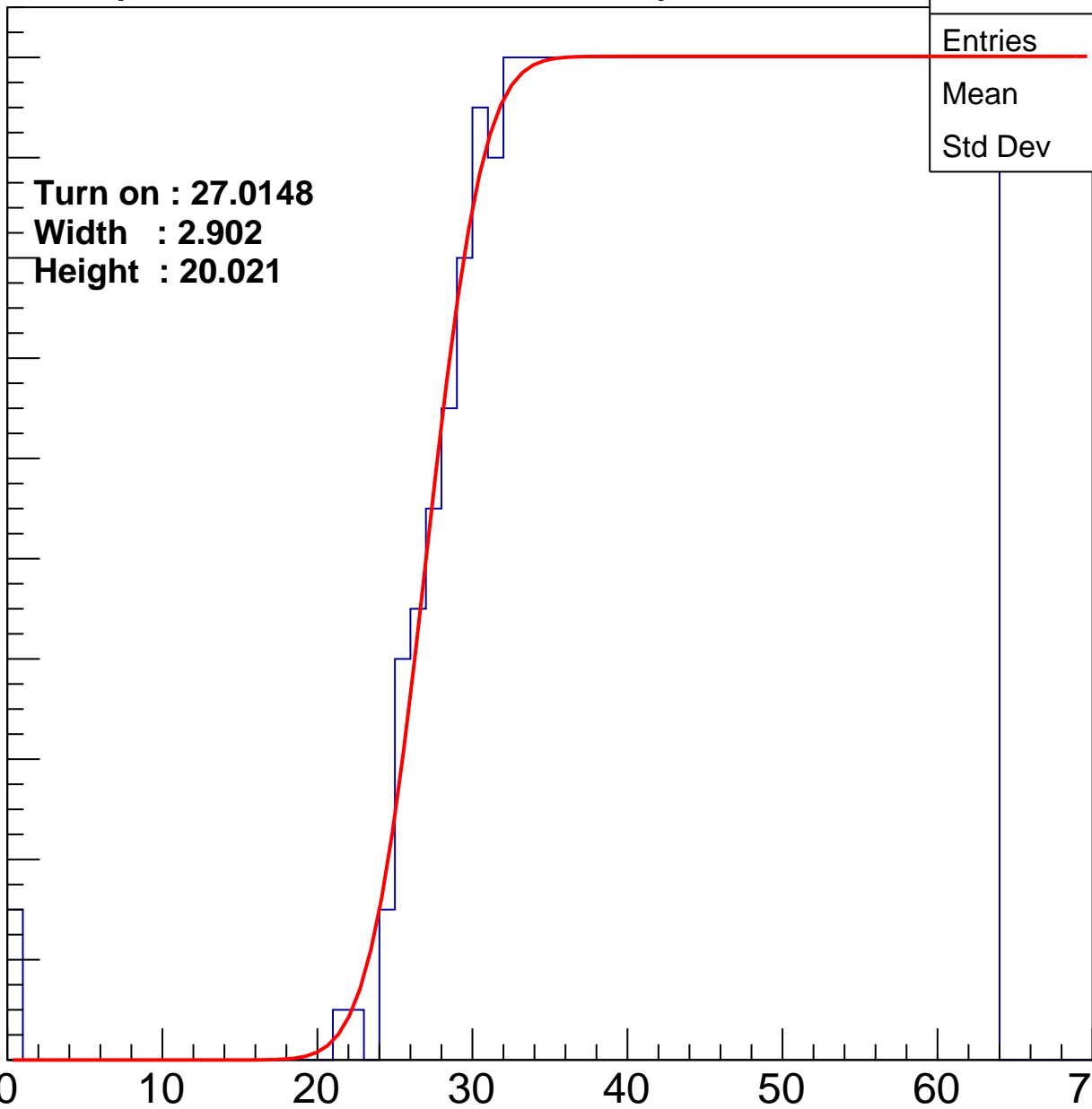
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0148
Width : 2.902
Height : 20.021

Entries	742
Mean	44.75
Std Dev	11.18

ampl



B1L001S, U25-ch1

calib_packv5_042523_0143.root, FC#2, port C2

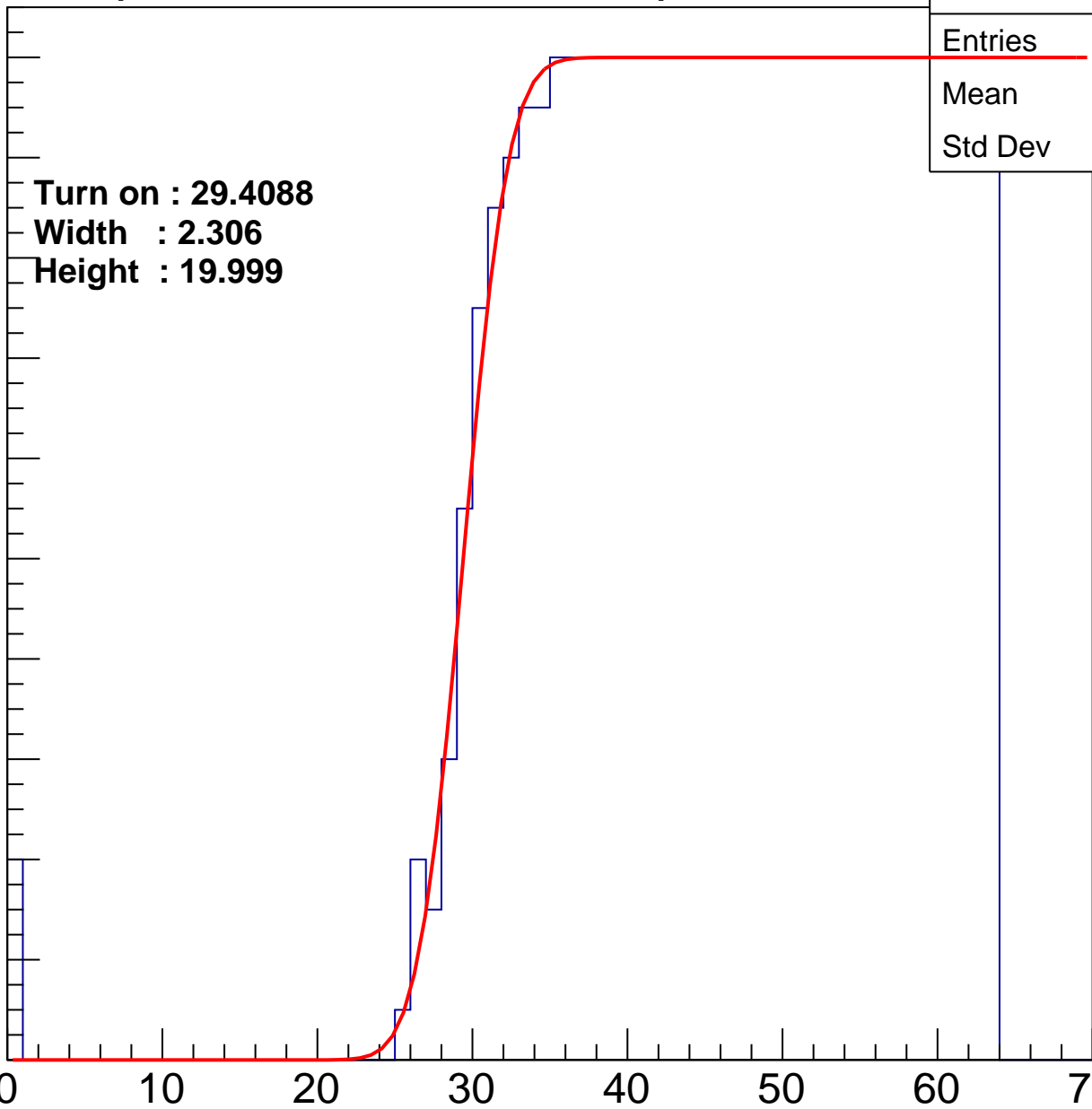
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.4088
Width : 2.306
Height : 19.999

Entries	697
Mean	45.83
Std Dev	10.69

ampl



B1L001S, U25-ch2

calib_packv5_042523_0143.root, FC#2, port C2

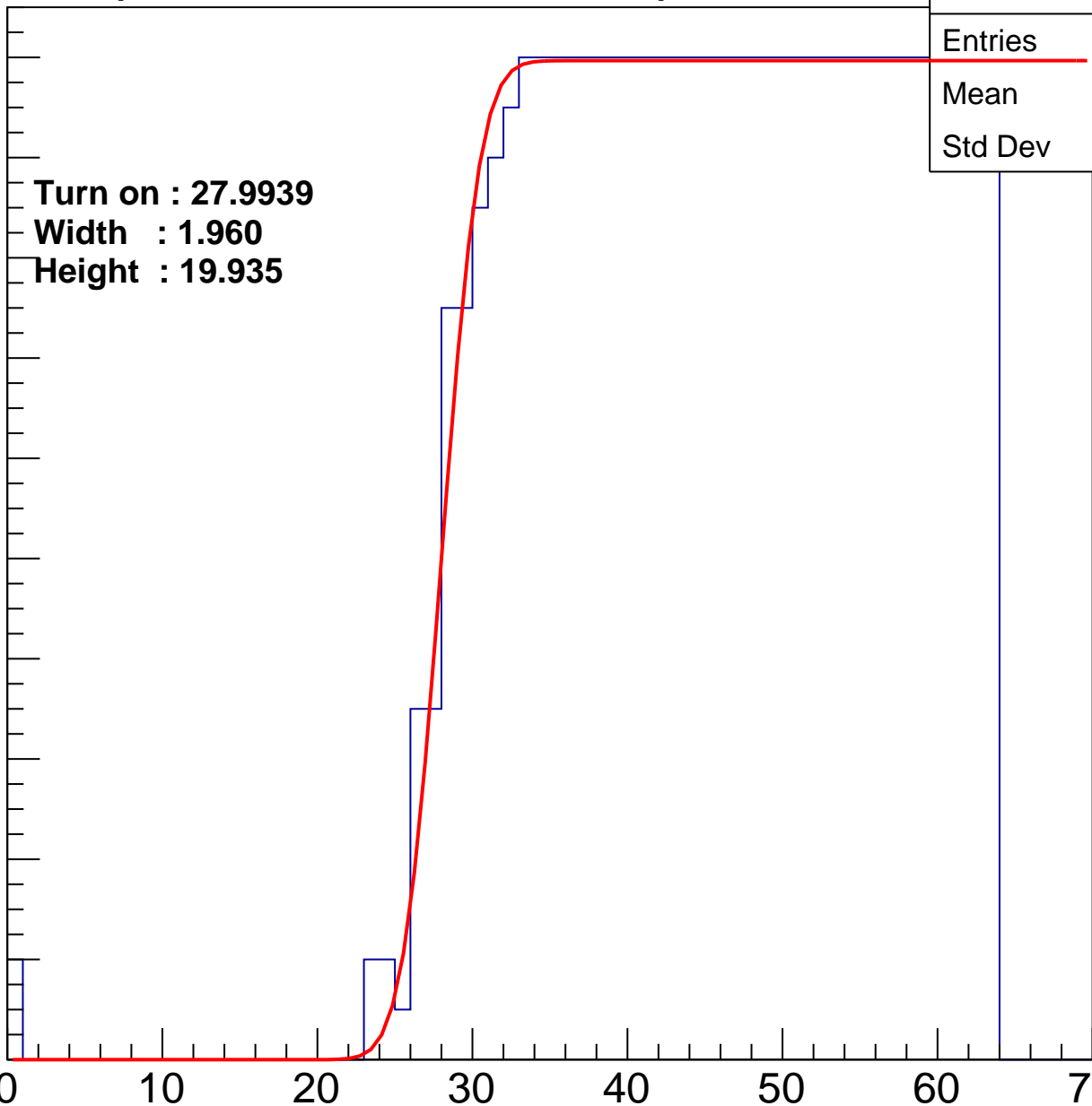
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9939
Width : 1.960
Height : 19.935

Entries	725
Mean	45.22
Std Dev	10.83

ampl



B1L001S, U25-ch3

calib_packv5_042523_0143.root, FC#2, port C2

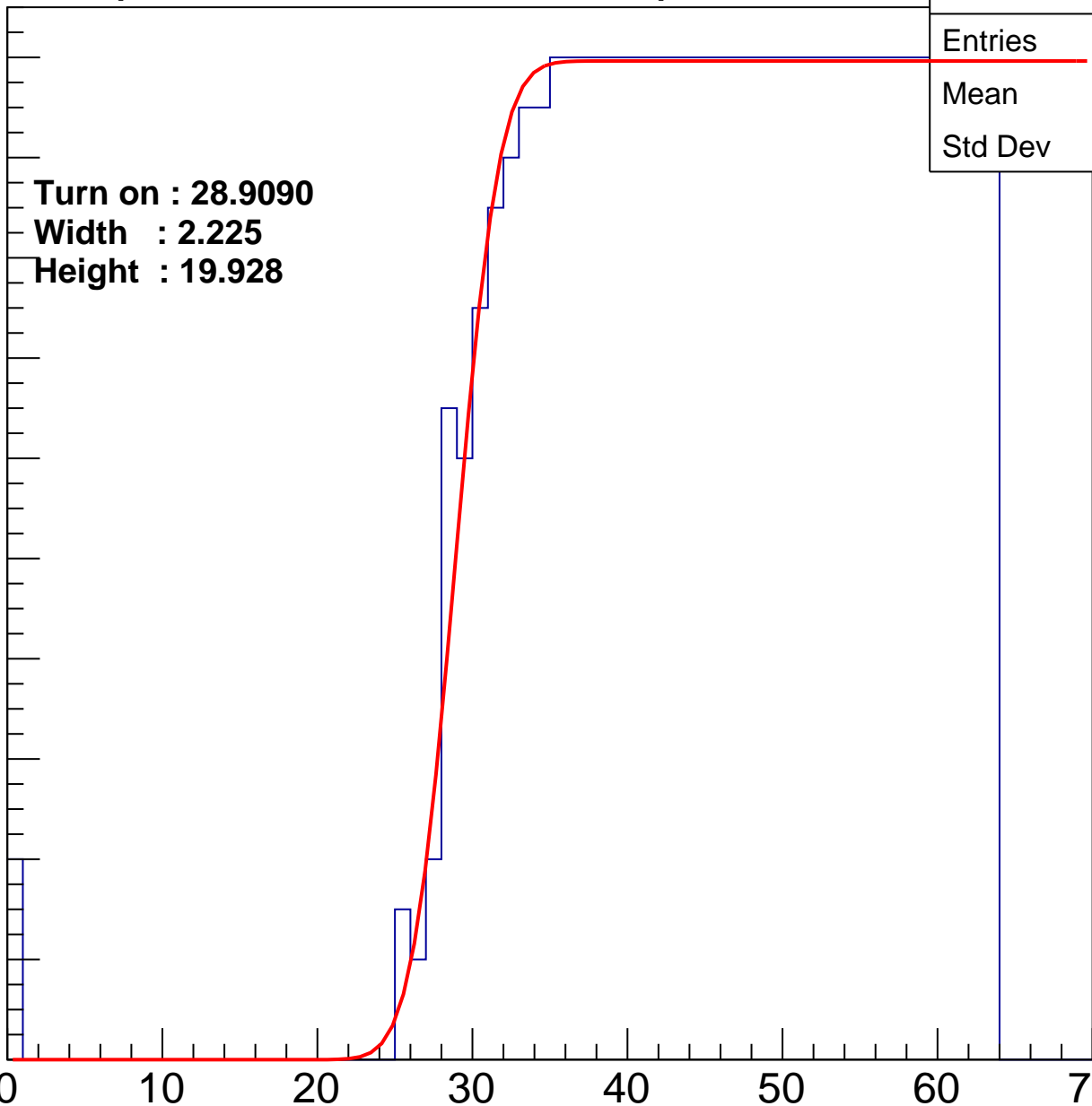
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.9090
Width : 2.225
Height : 19.928

Entries	706
Mean	45.6
Std Dev	10.81

ampl



B1L001S, U25-ch4

calib_packv5_042523_0143.root, FC#2, port C2

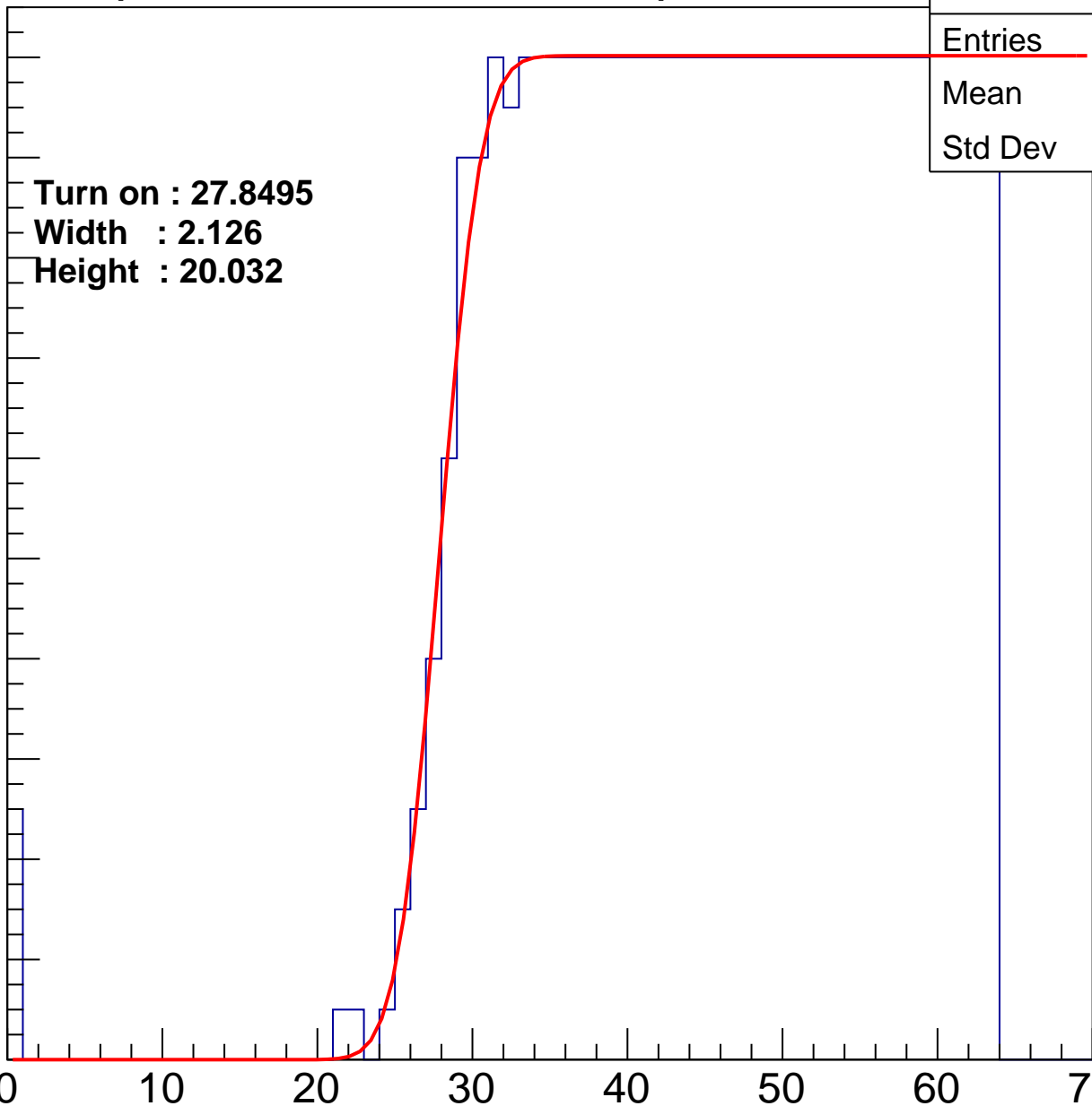
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8495
Width : 2.126
Height : 20.032

Entries	731
Mean	44.97
Std Dev	11.2

ampl



B1L001S, U25-ch5

calib_packv5_042523_0143.root, FC#2, port C2

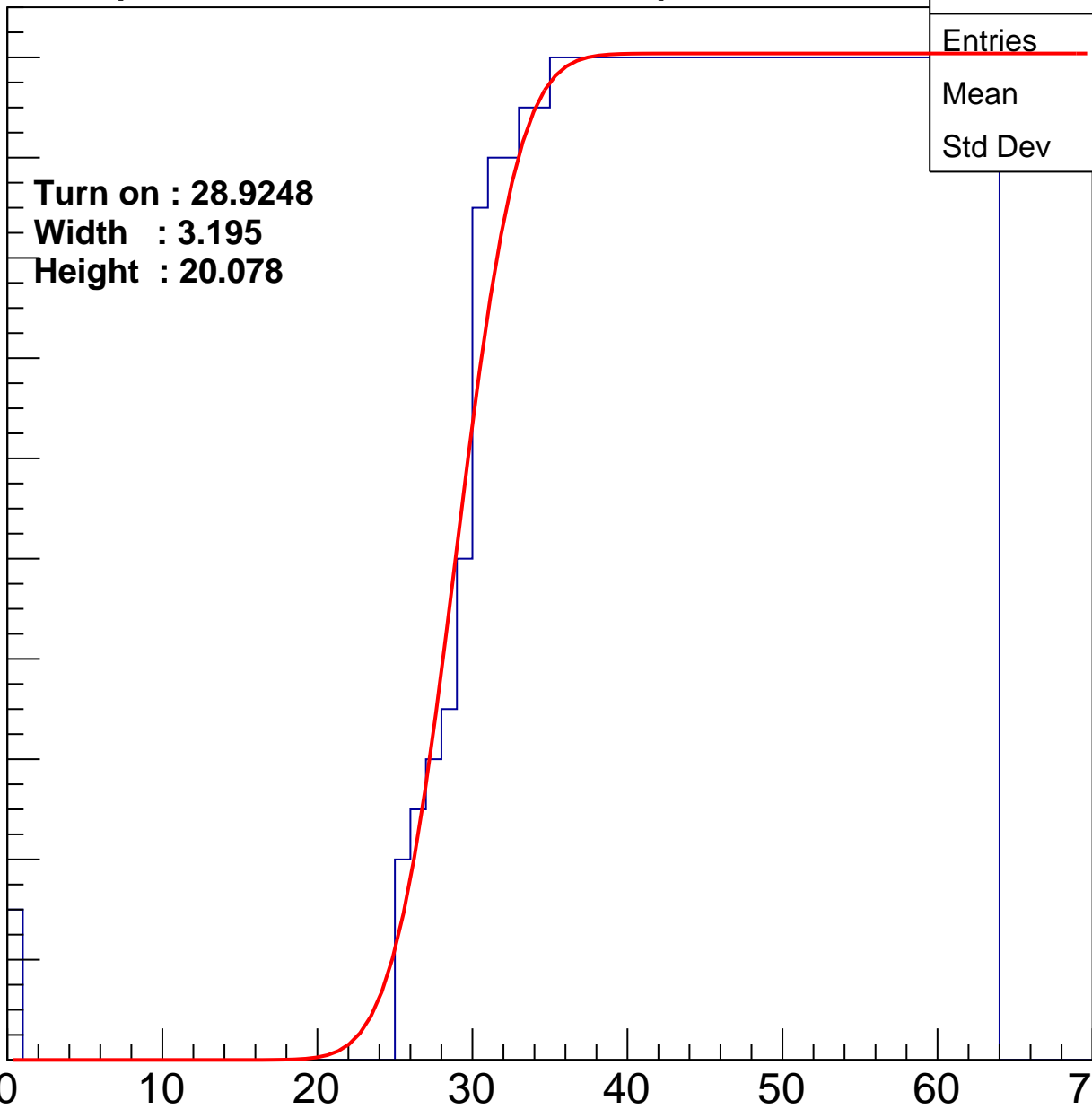
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.9248
Width : 3.195
Height : 20.078

Entries	706
Mean	45.63
Std Dev	10.71

ampl



B1L001S, U25-ch6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

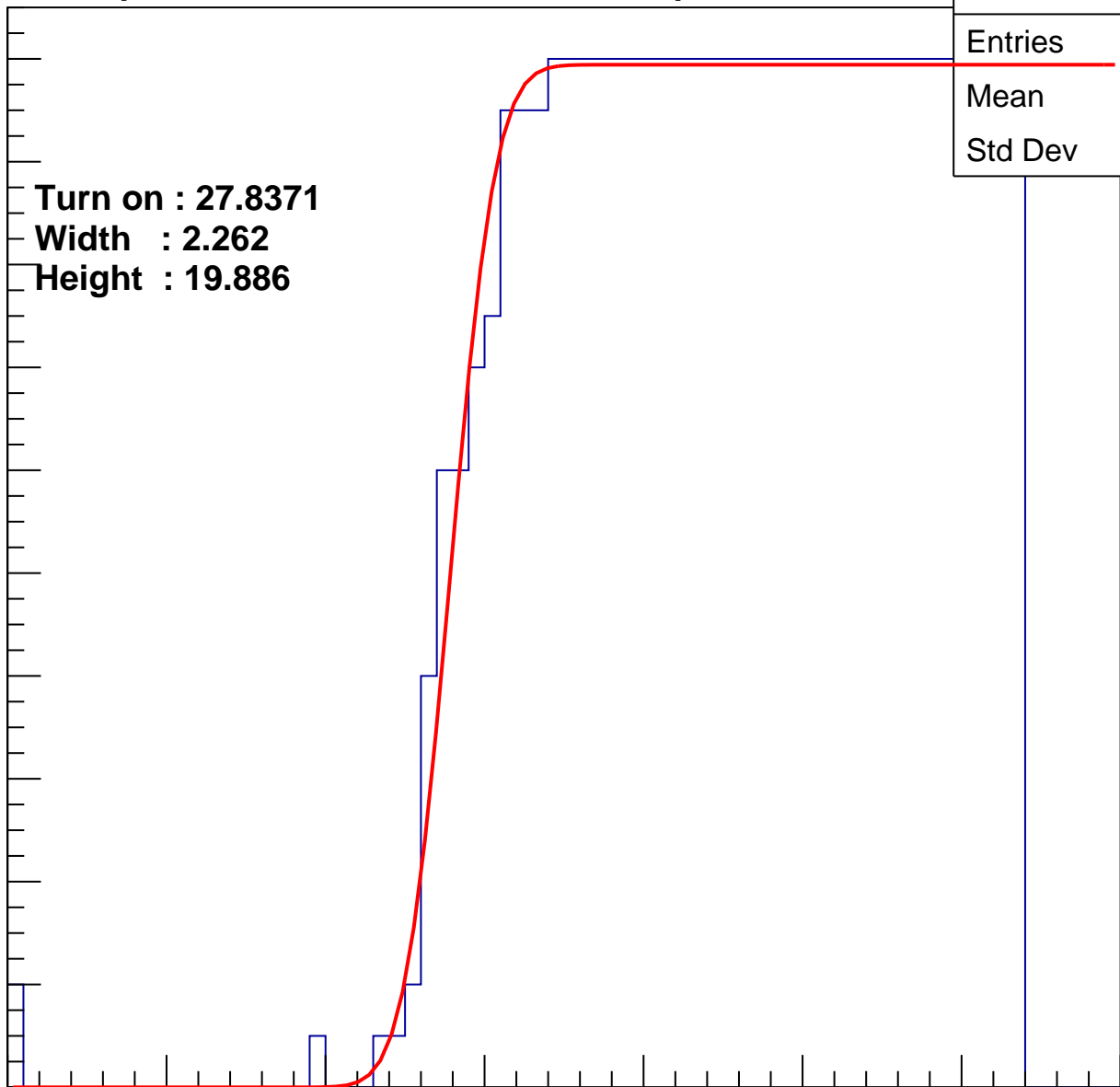
20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8371
Width : 2.262
Height : 19.886

Entries	725
Mean	45.19
Std Dev	10.87

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch7

calib_packv5_042523_0143.root, FC#2, port C2

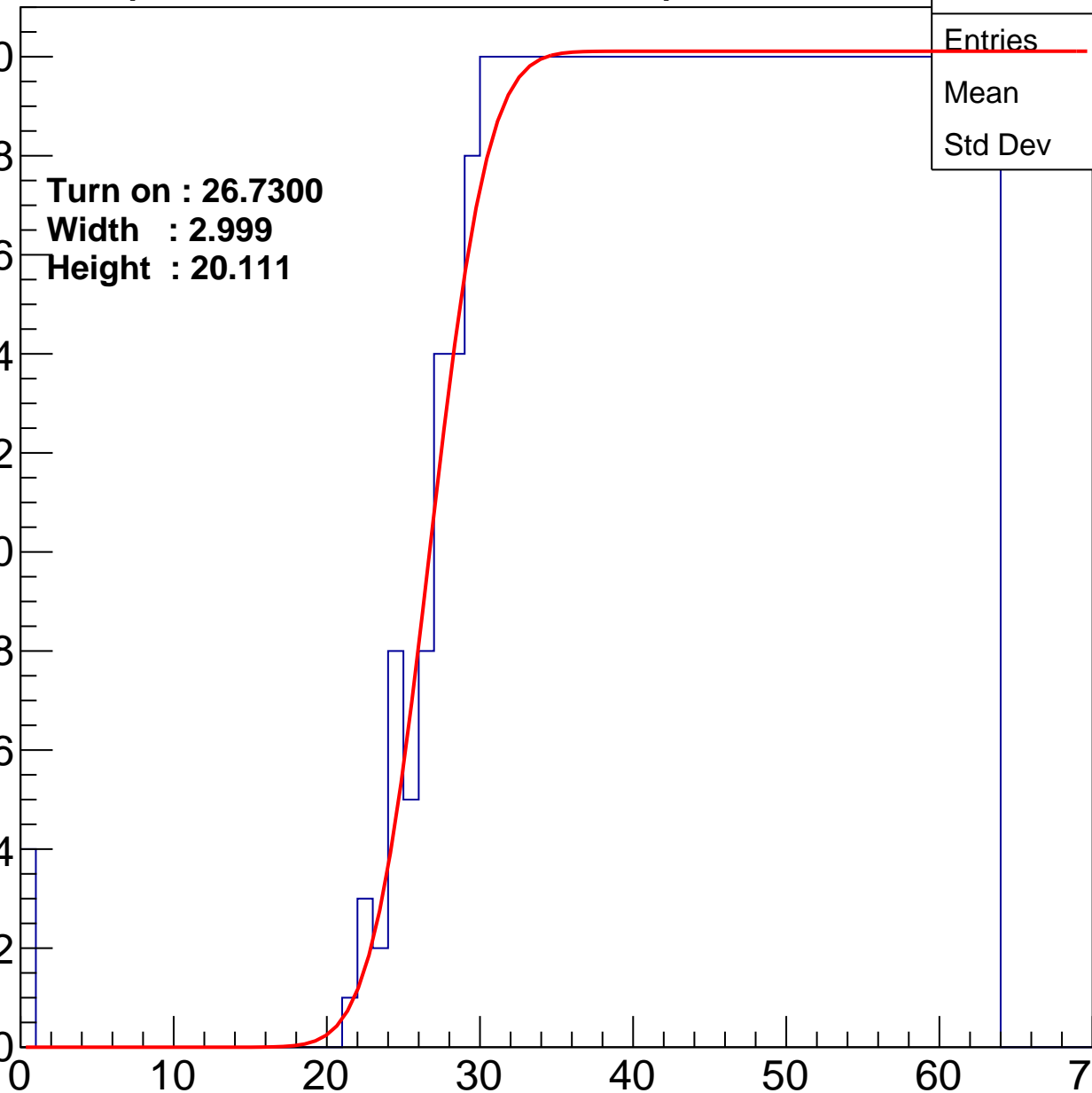
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7300
Width : 2.999
Height : 20.111

Entries	757
Mean	44.35
Std Dev	11.46

ampl



B1L001S, U25-ch8

calib_packv5_042523_0143.root, FC#2, port C2

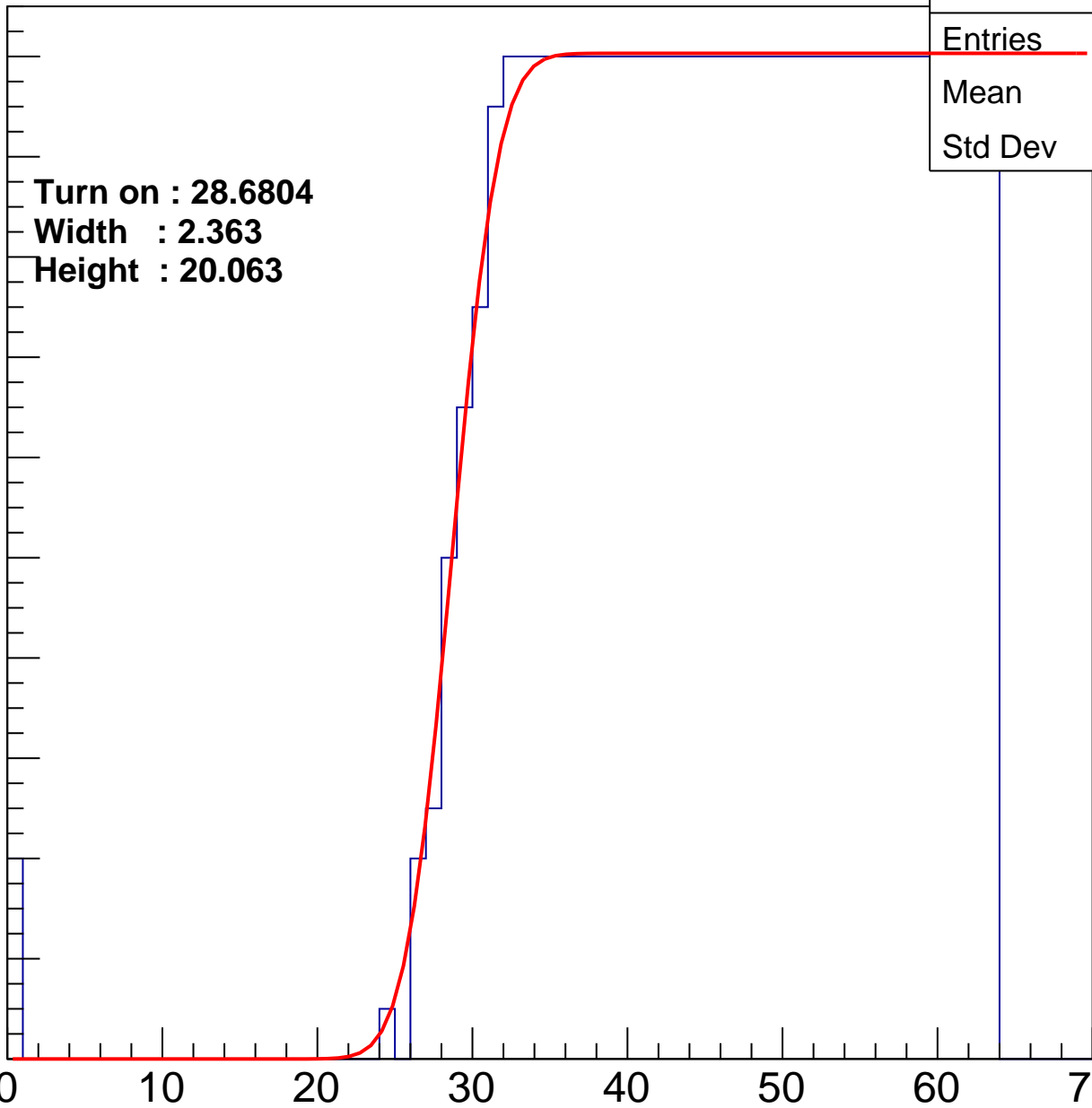
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6804
Width : 2.363
Height : 20.063

Entries	711
Mean	45.51
Std Dev	10.82

ampl



B1L001S, U25-ch9

calib_packv5_042523_0143.root, FC#2, port C2

Entry

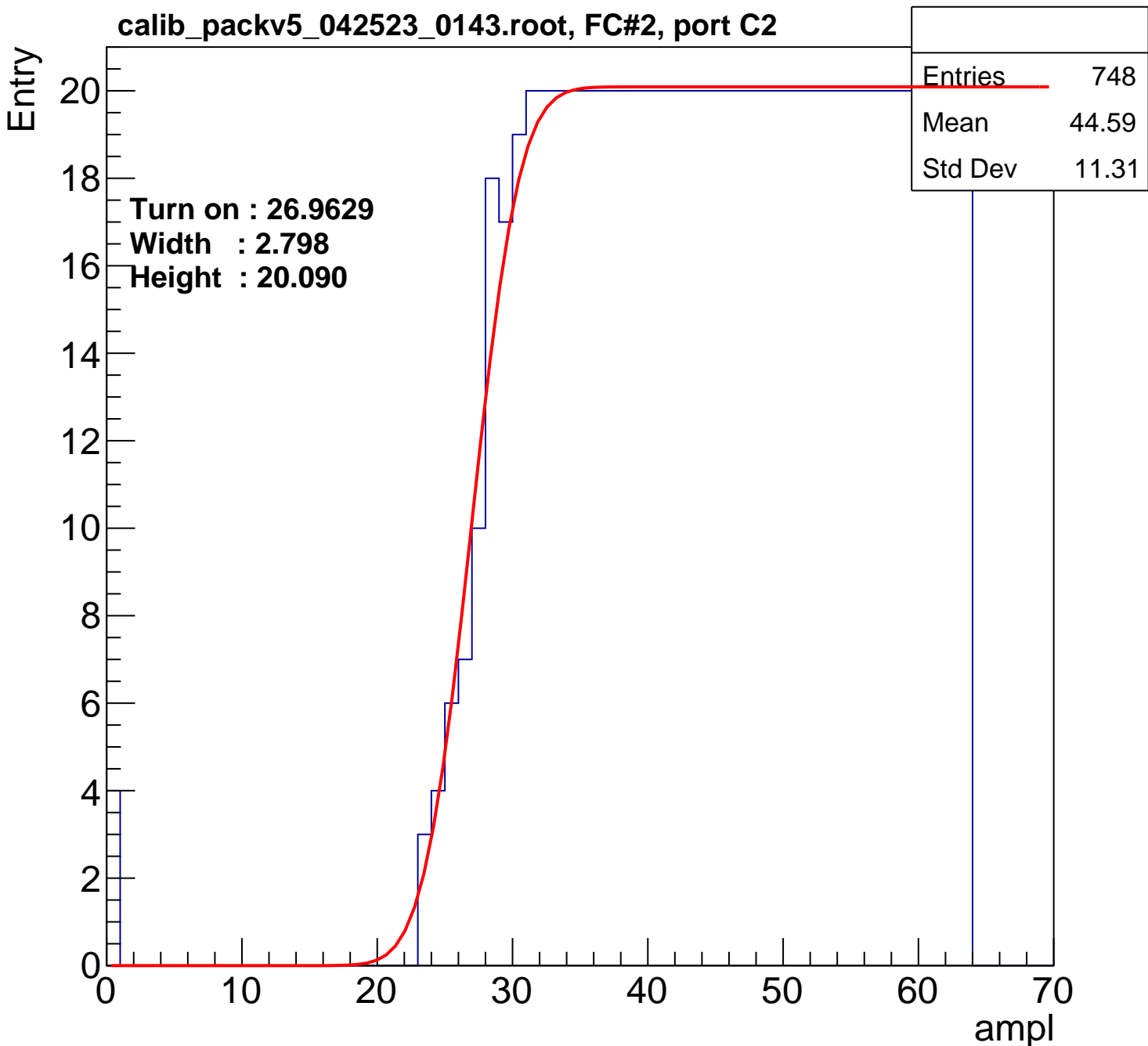
20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9629
Width : 2.798
Height : 20.090

Entries	748
Mean	44.59
Std Dev	11.31

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch10

calib_packv5_042523_0143.root, FC#2, port C2

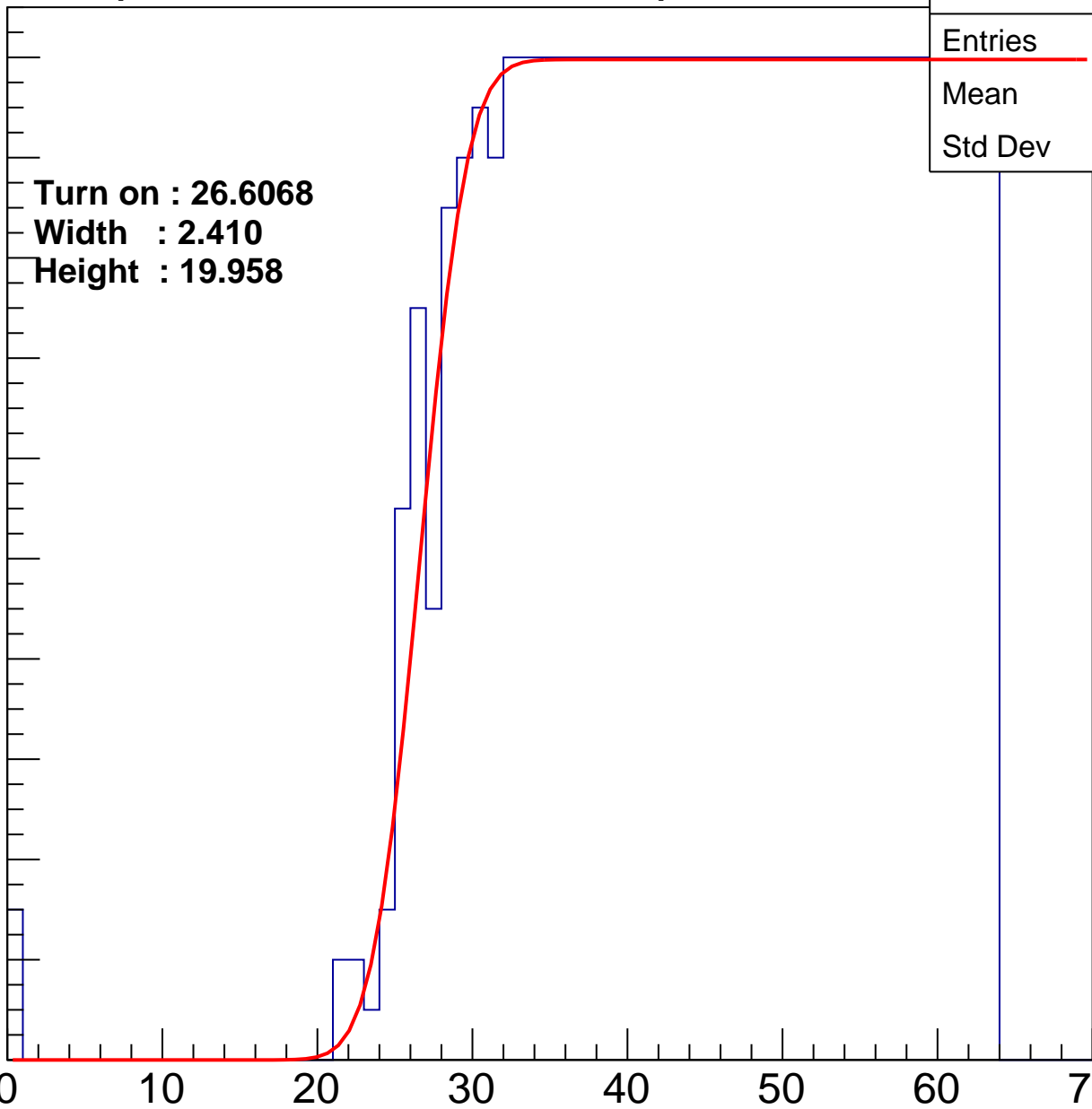
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6068
Width : 2.410
Height : 19.958

Entries	758
Mean	44.35
Std Dev	11.39

ampl



B1L001S, U25-ch11

calib_packv5_042523_0143.root, FC#2, port C2

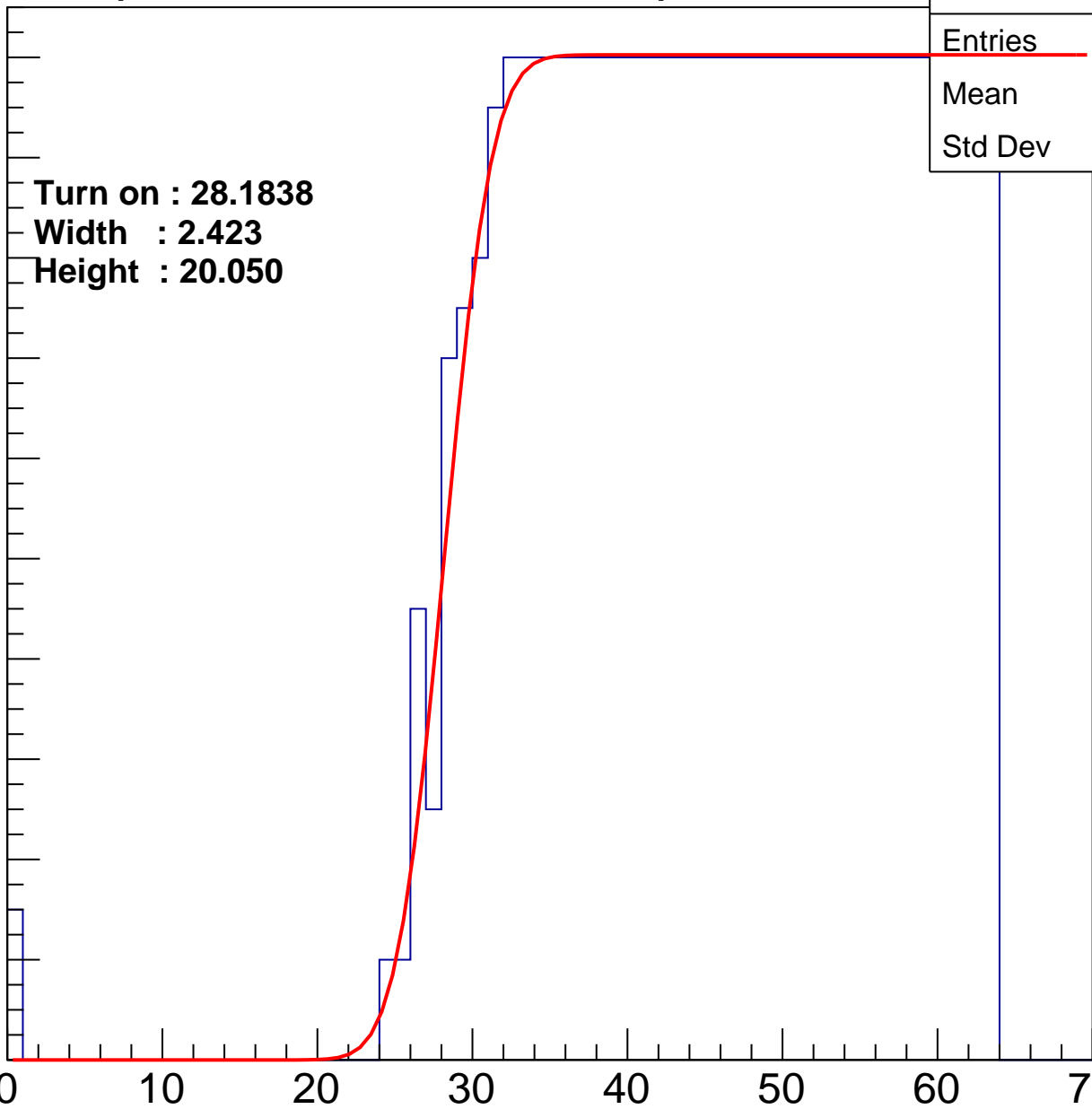
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1838
Width : 2.423
Height : 20.050

Entries	725
Mean	45.19
Std Dev	10.91

ampl



B1L001S, U25-ch12

calib_packv5_042523_0143.root, FC#2, port C2

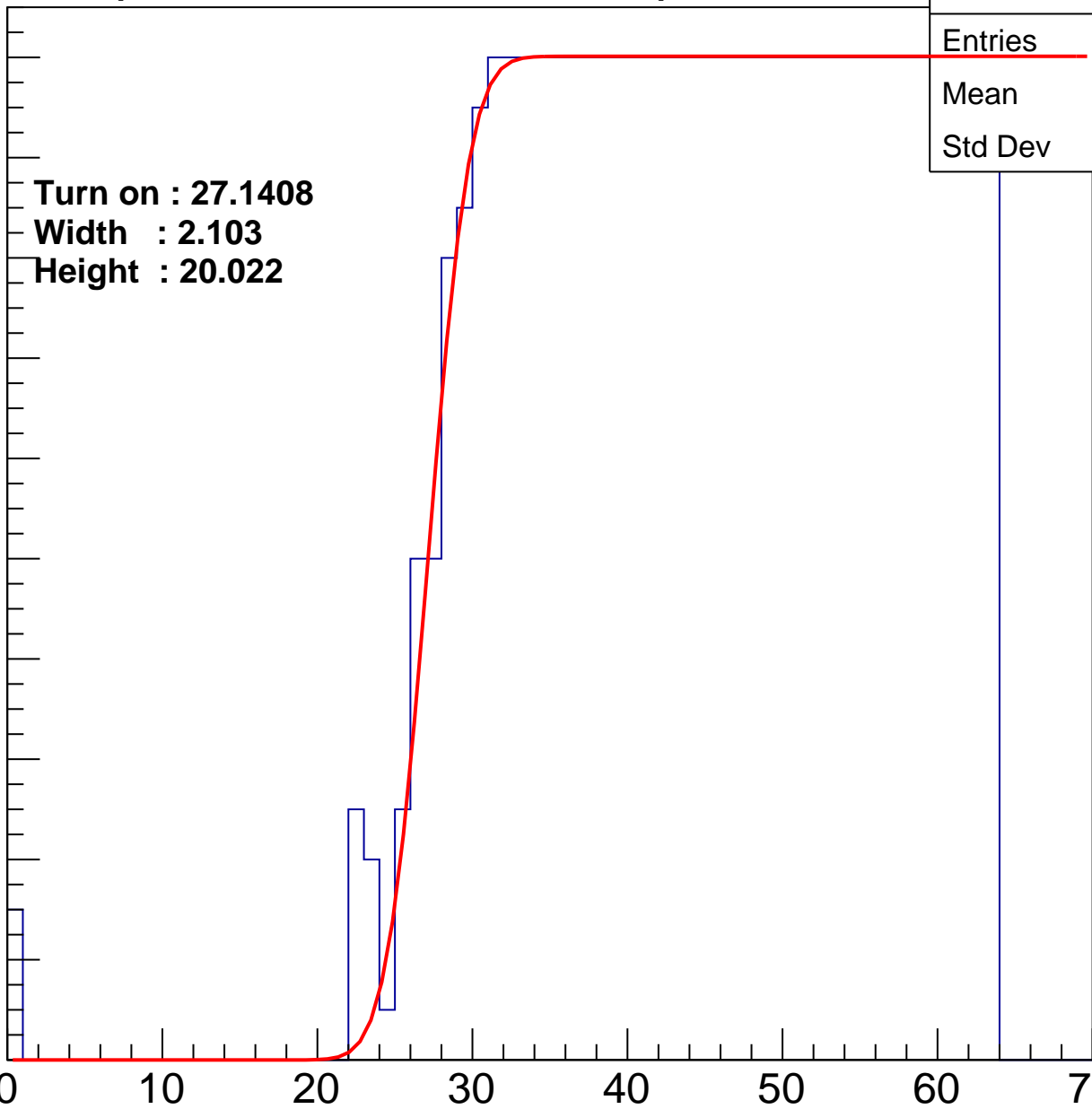
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1408
Width : 2.103
Height : 20.022

Entries	750
Mean	44.55
Std Dev	11.29

ampl



B1L001S, U25-ch13

calib_packv5_042523_0143.root, FC#2, port C2

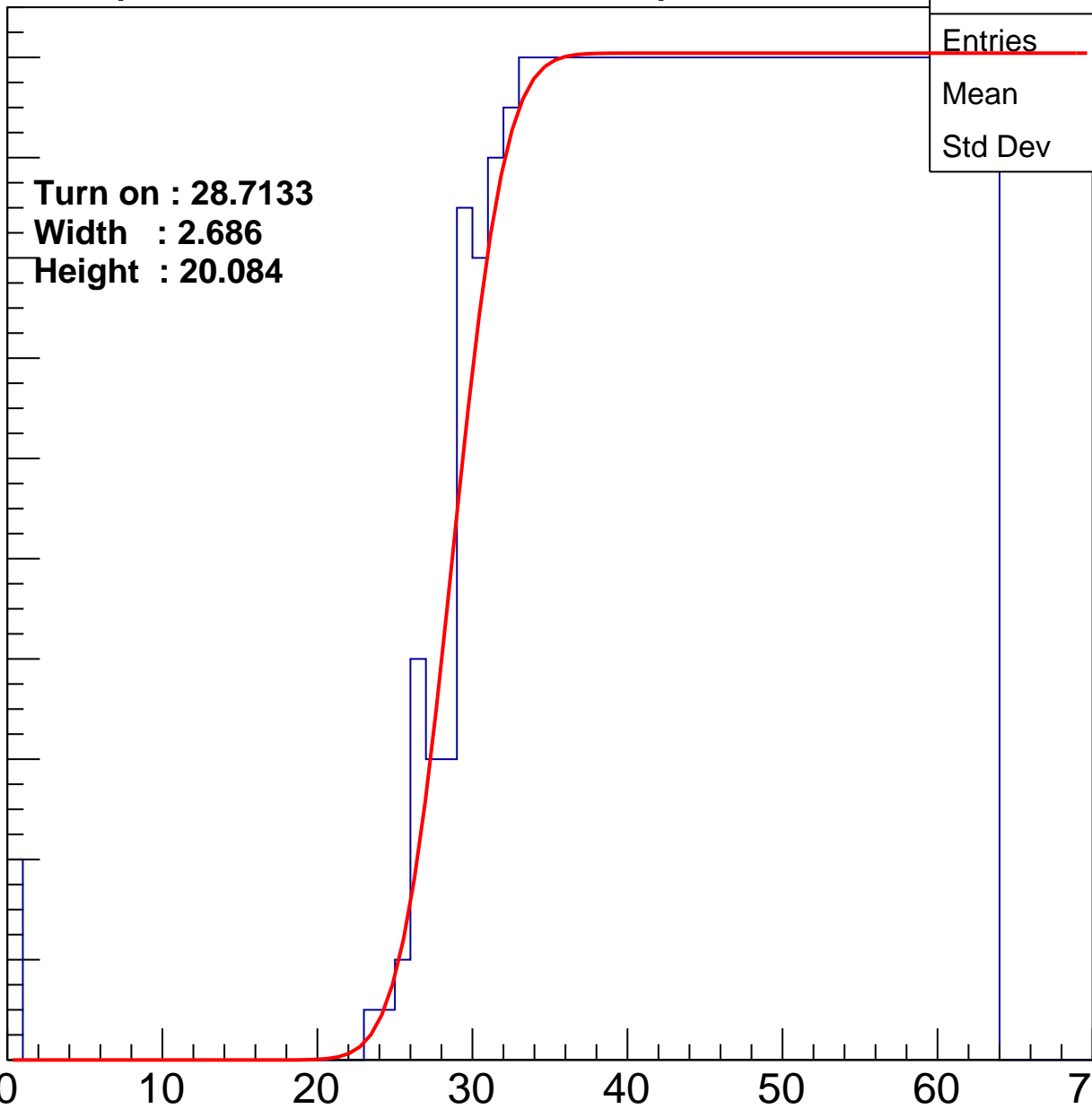
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.7133
Width : 2.686
Height : 20.084

Entries	718
Mean	45.31
Std Dev	10.96

ampl



B1L001S, U25-ch14

calib_packv5_042523_0143.root, FC#2, port C2

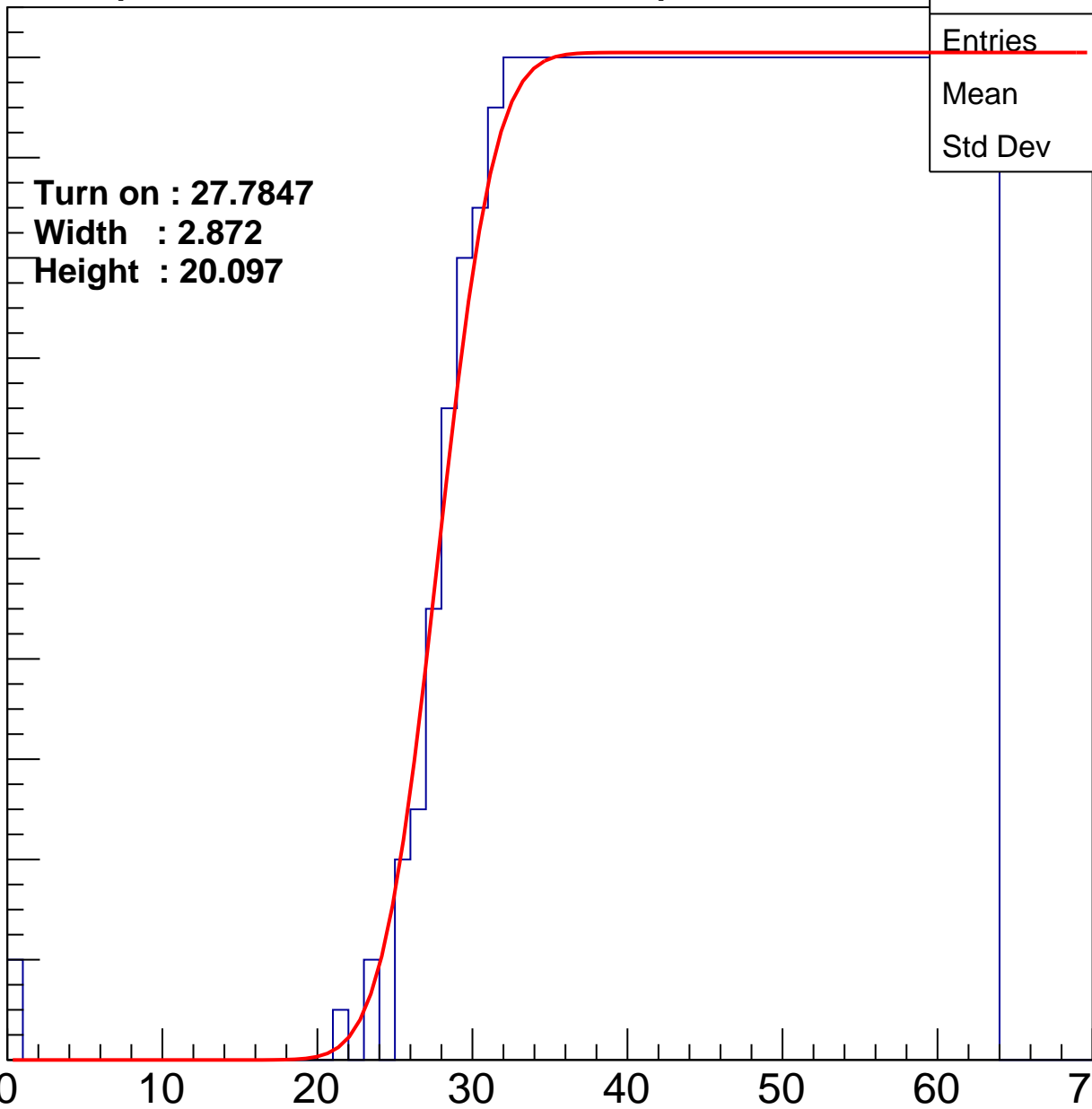
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7847
Width : 2.872
Height : 20.097

Entries	728
Mean	45.15
Std Dev	10.86

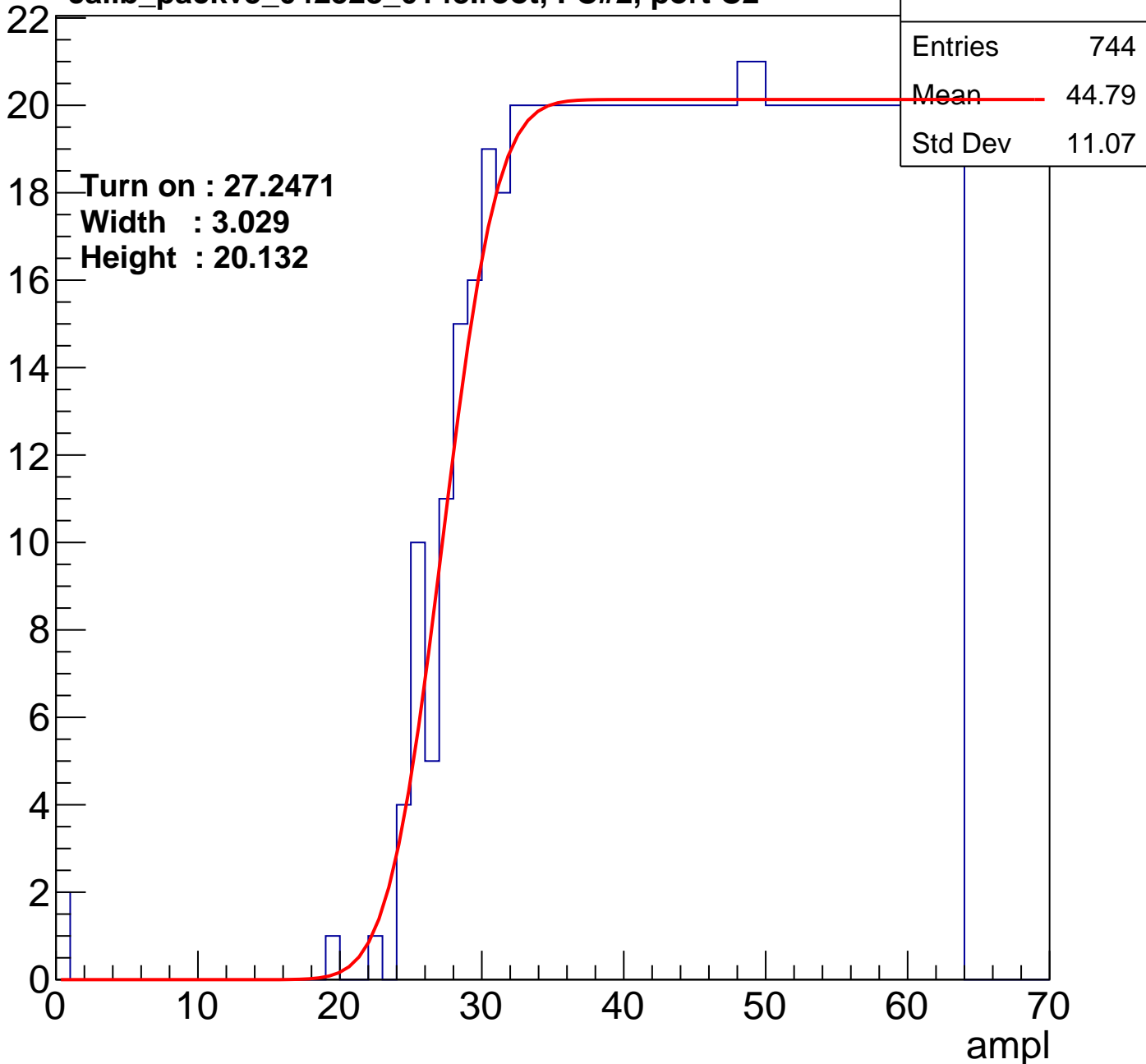
ampl



B1L001S, U25-ch15

calib_packv5_042523_0143.root, FC#2, port C2

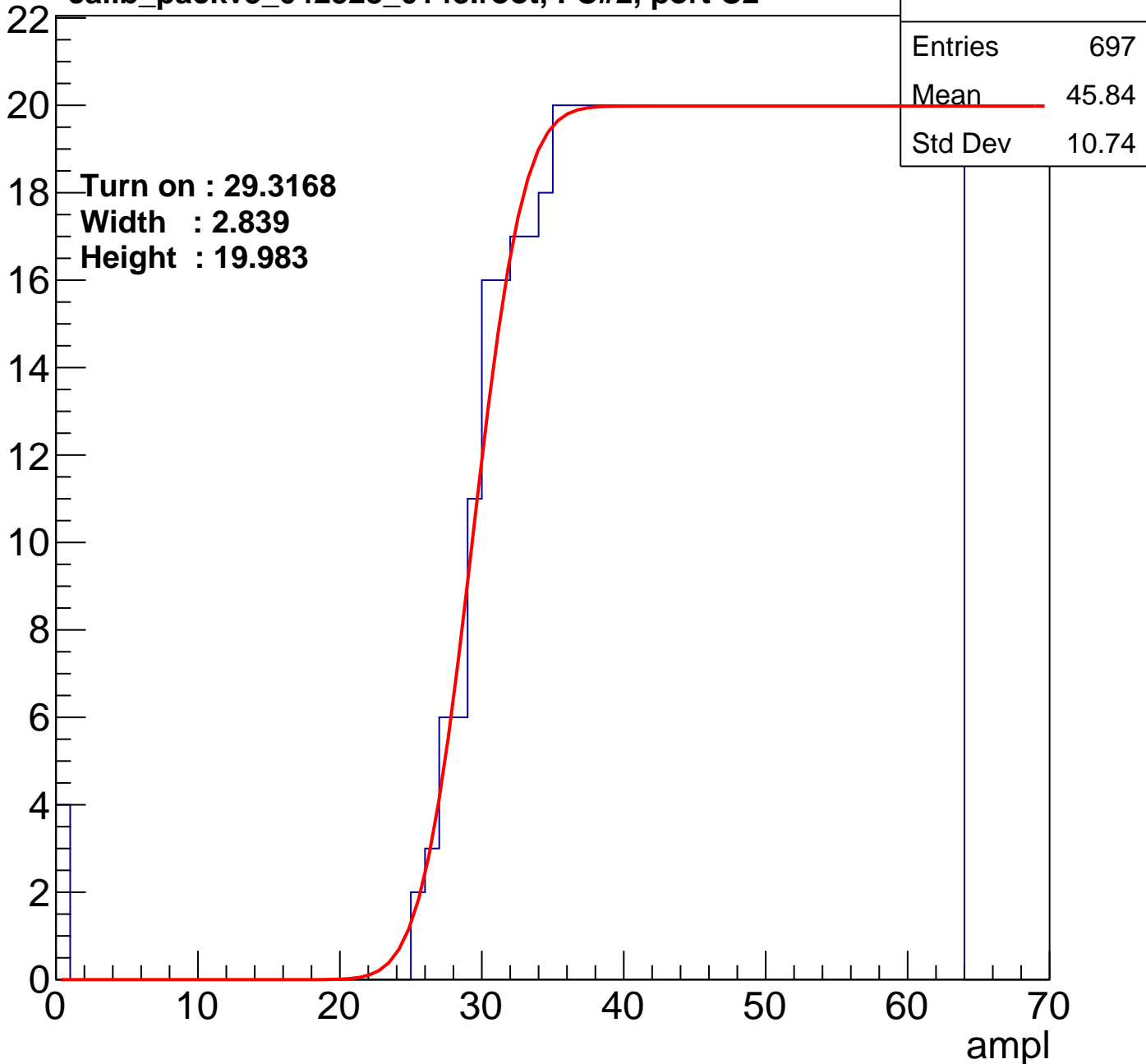
Entry



B1L001S, U25-ch16

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch17

calib_packv5_042523_0143.root, FC#2, port C2

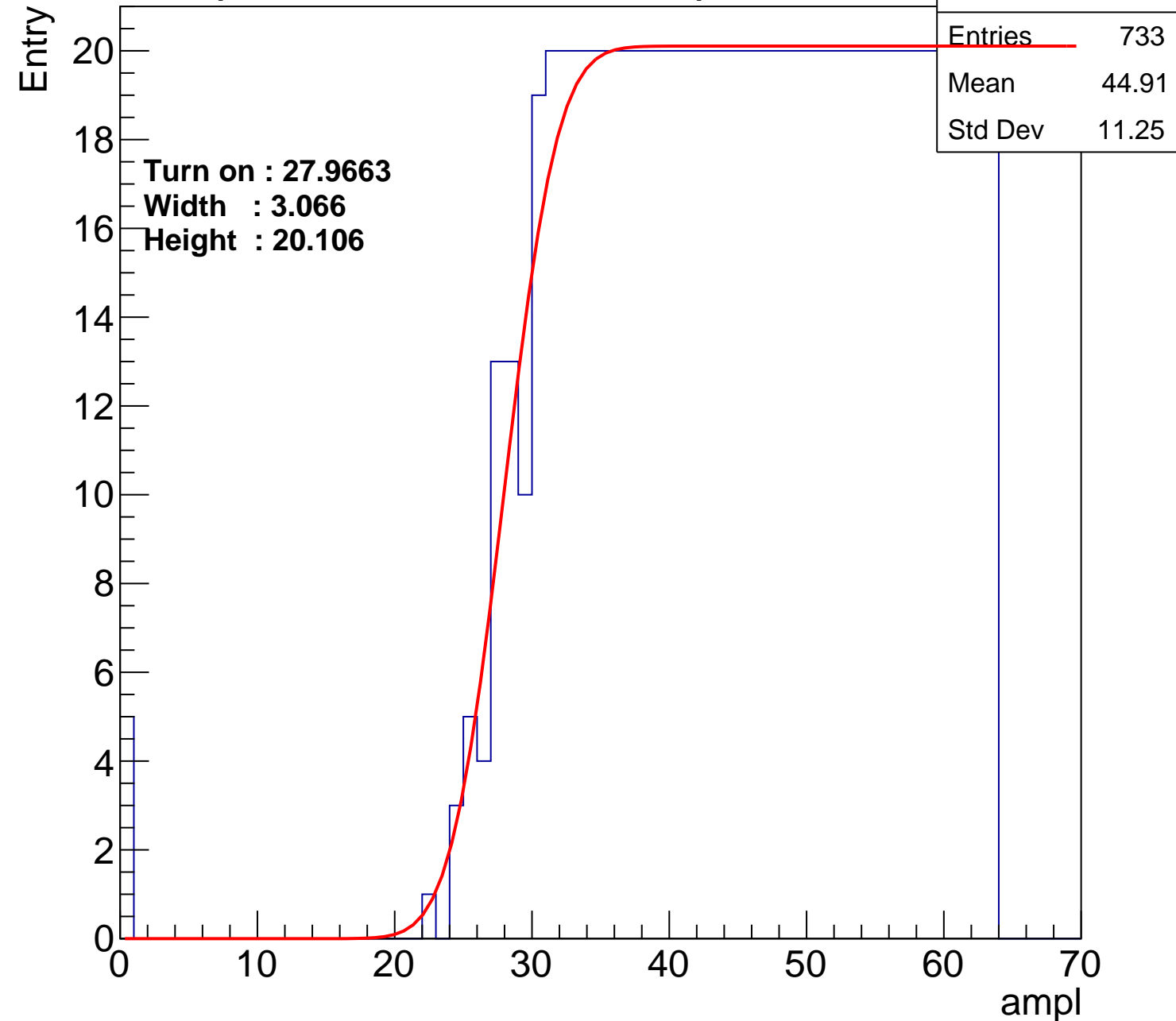
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9663
Width : 3.066
Height : 20.106

Entries	733
Mean	44.91
Std Dev	11.25

ampl



B1L001S, U25-ch18

calib_packv5_042523_0143.root, FC#2, port C2

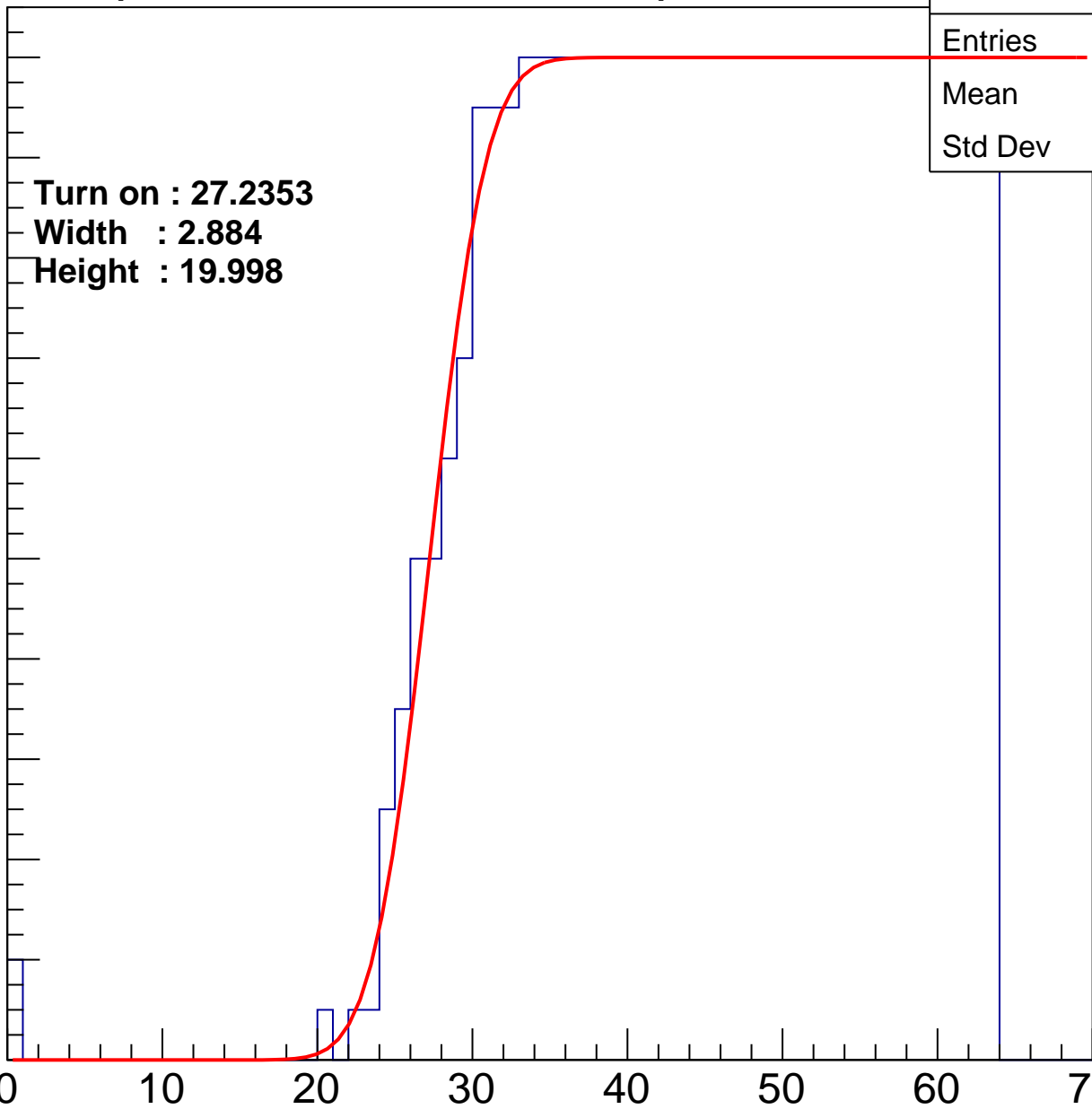
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2353
Width : 2.884
Height : 19.998

Entries	740
Mean	44.81
Std Dev	11.09

ampl



B1L001S, U25-ch19

calib_packv5_042523_0143.root, FC#2, port C2

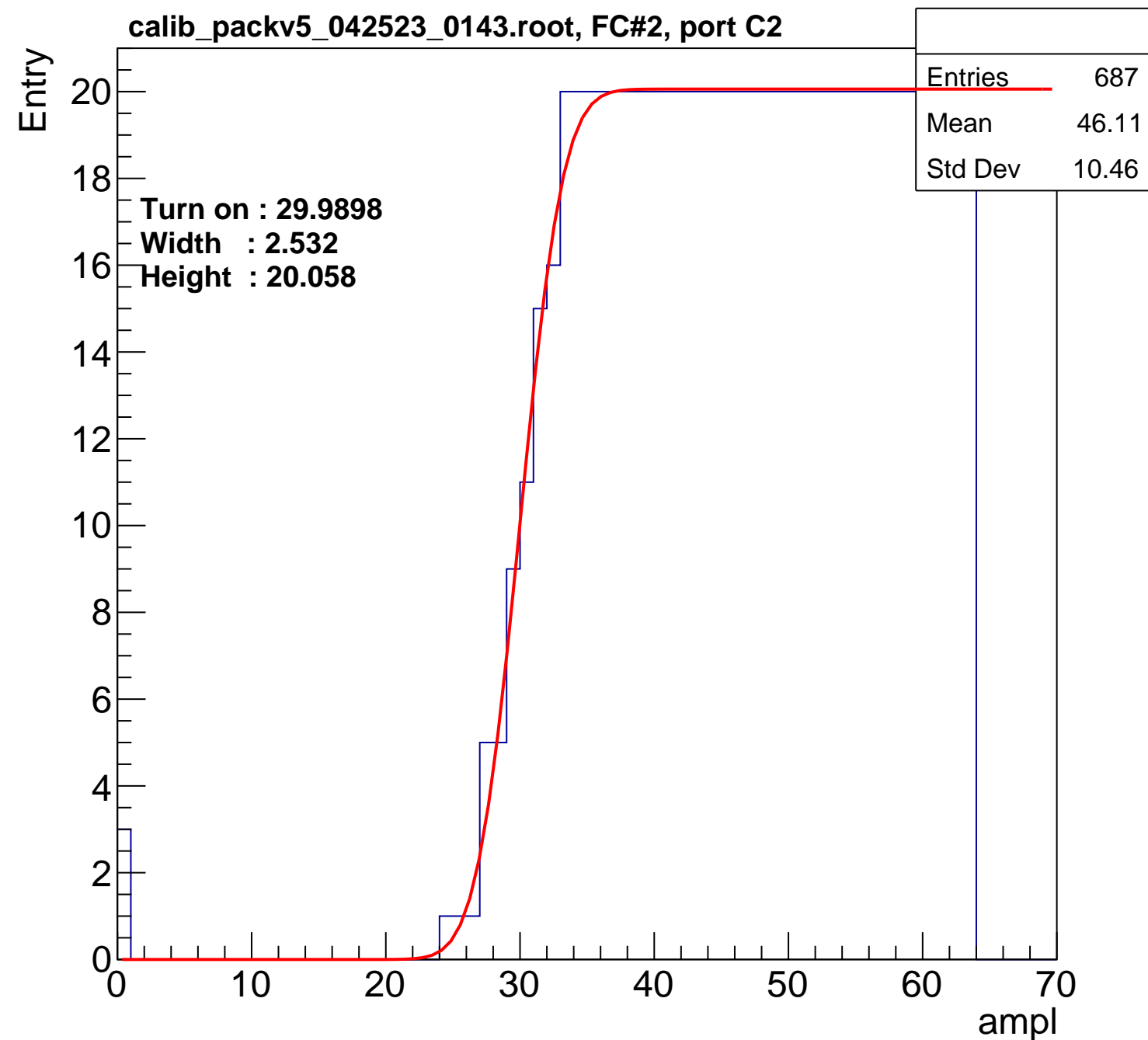
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.9898
Width : 2.532
Height : 20.058

Entries	687
Mean	46.11
Std Dev	10.46

ampl



B1L001S, U25-ch20

calib_packv5_042523_0143.root, FC#2, port C2

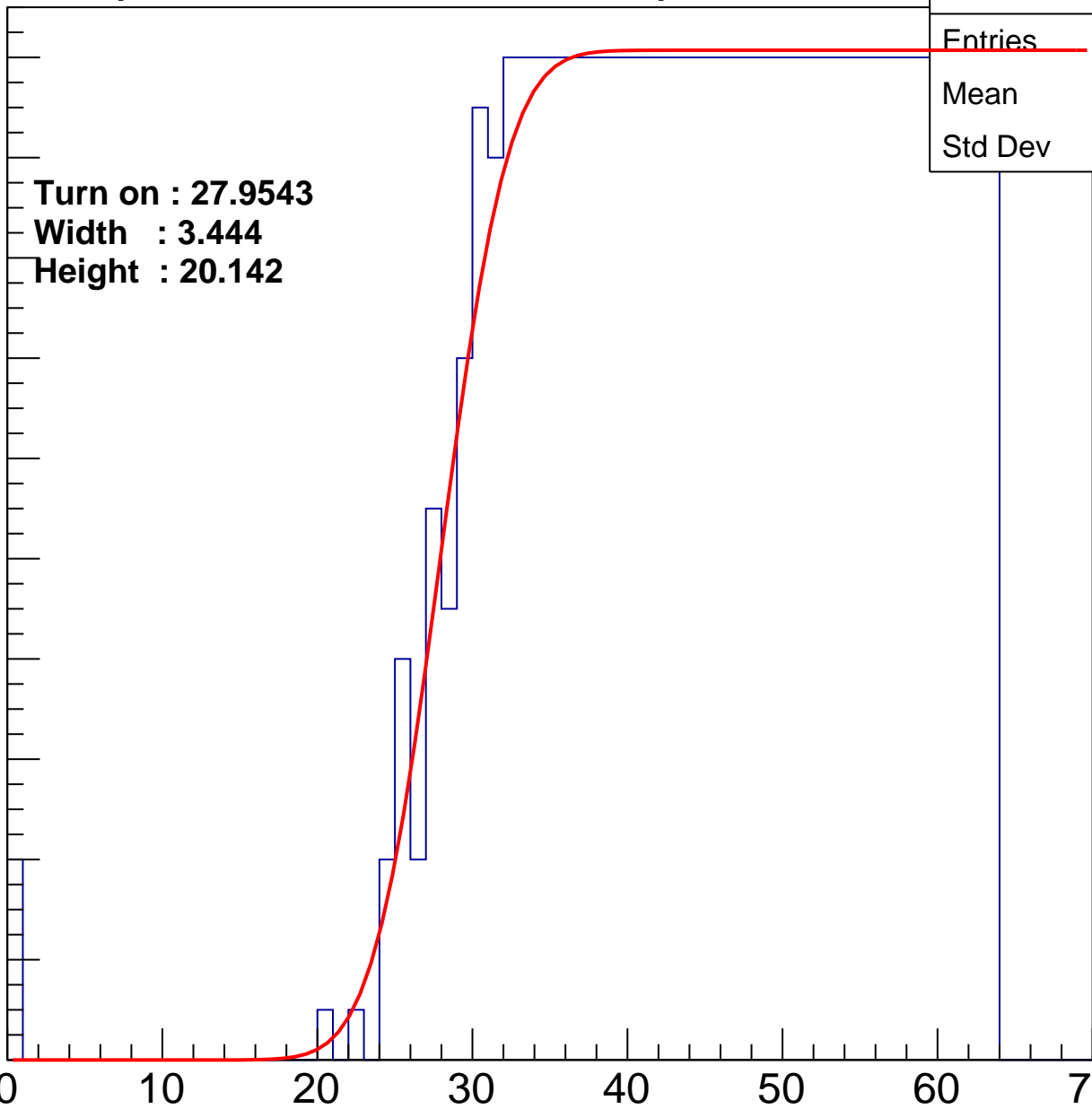
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9543
Width : 3.444
Height : 20.142

Entries	733
Mean	44.92
Std Dev	11.19

ampl



B1L001S, U25-ch21

calib_packv5_042523_0143.root, FC#2, port C2

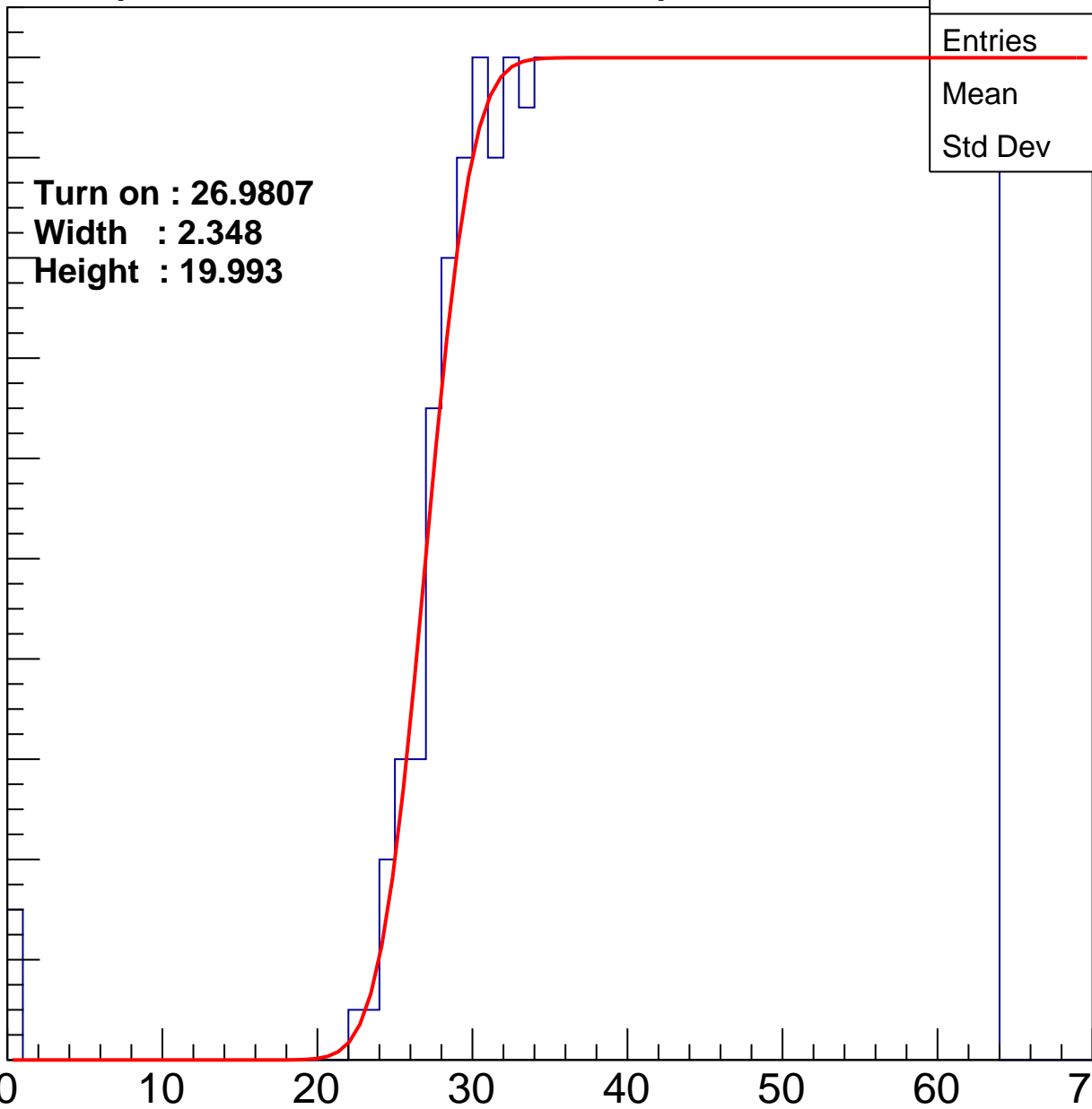
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9807
Width : 2.348
Height : 19.993

Entries	745
Mean	44.69
Std Dev	11.19

ampl



B1L001S, U25-ch22

calib_packv5_042523_0143.root, FC#2, port C2

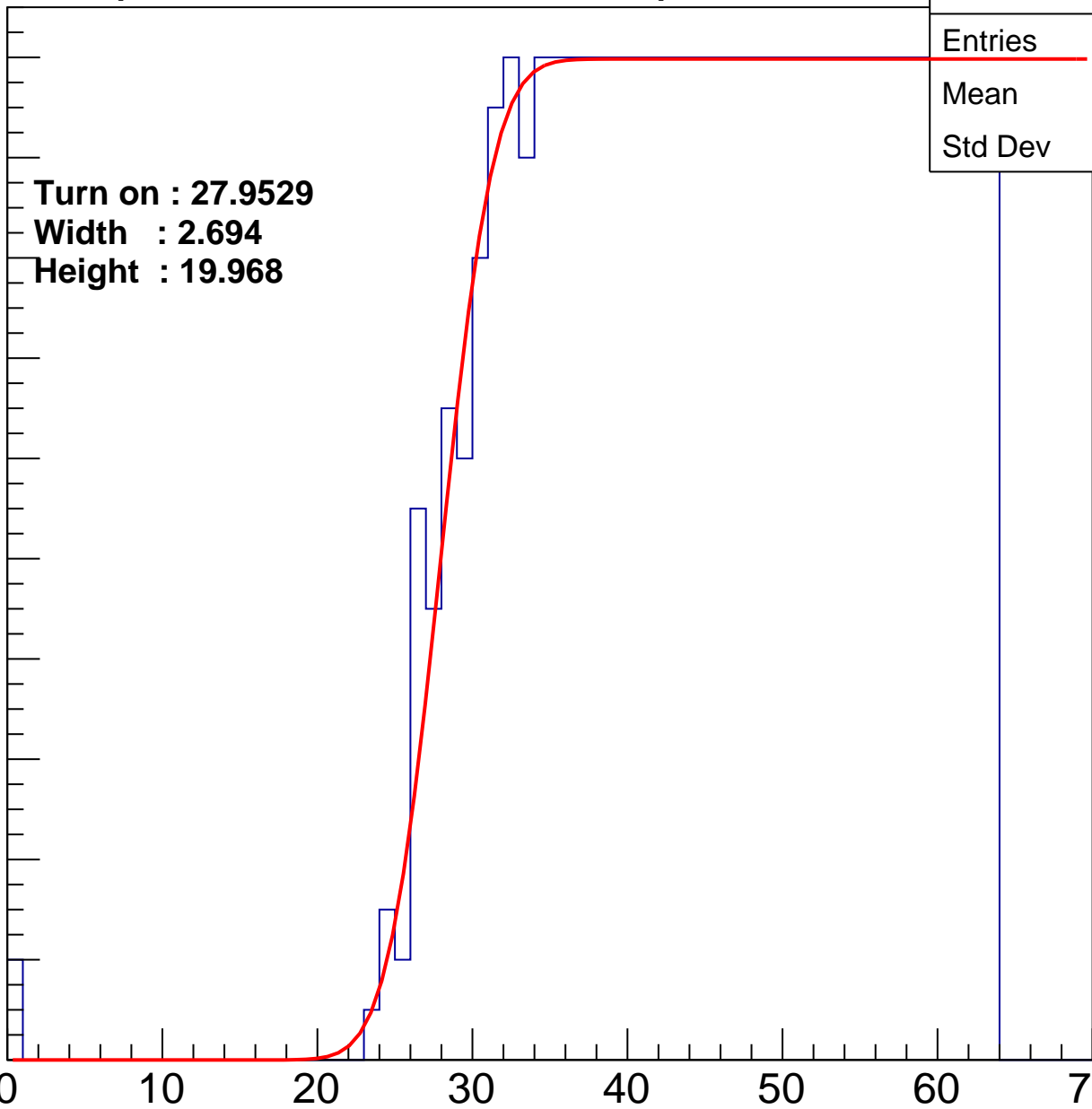
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9529
Width : 2.694
Height : 19.968

Entries	726
Mean	45.16
Std Dev	10.88

ampl



B1L001S, U25-ch23

calib_packv5_042523_0143.root, FC#2, port C2

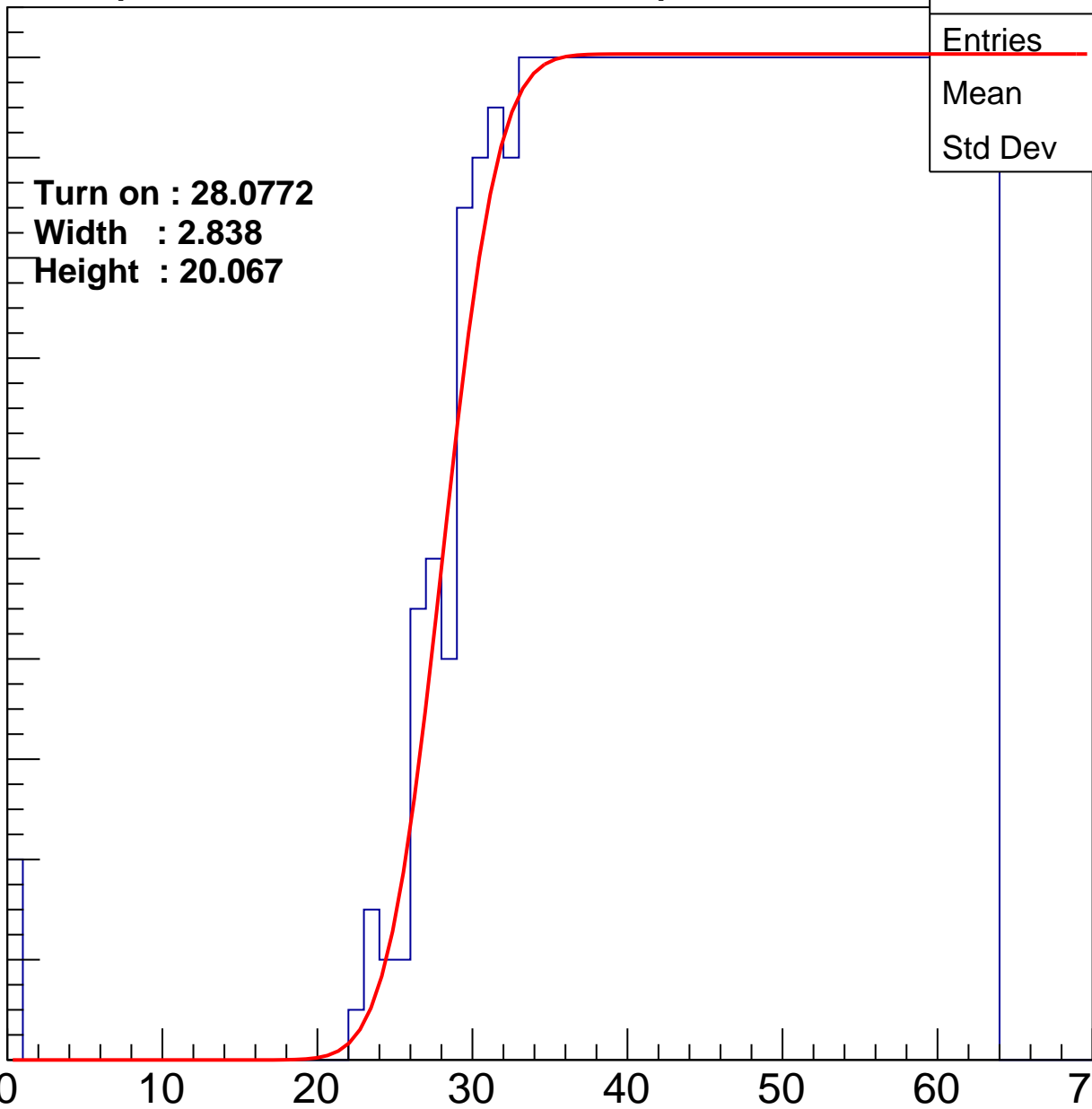
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0772
Width : 2.838
Height : 20.067

Entries	731
Mean	44.97
Std Dev	11.15

ampl



B1L001S, U25-ch24

calib_packv5_042523_0143.root, FC#2, port C2

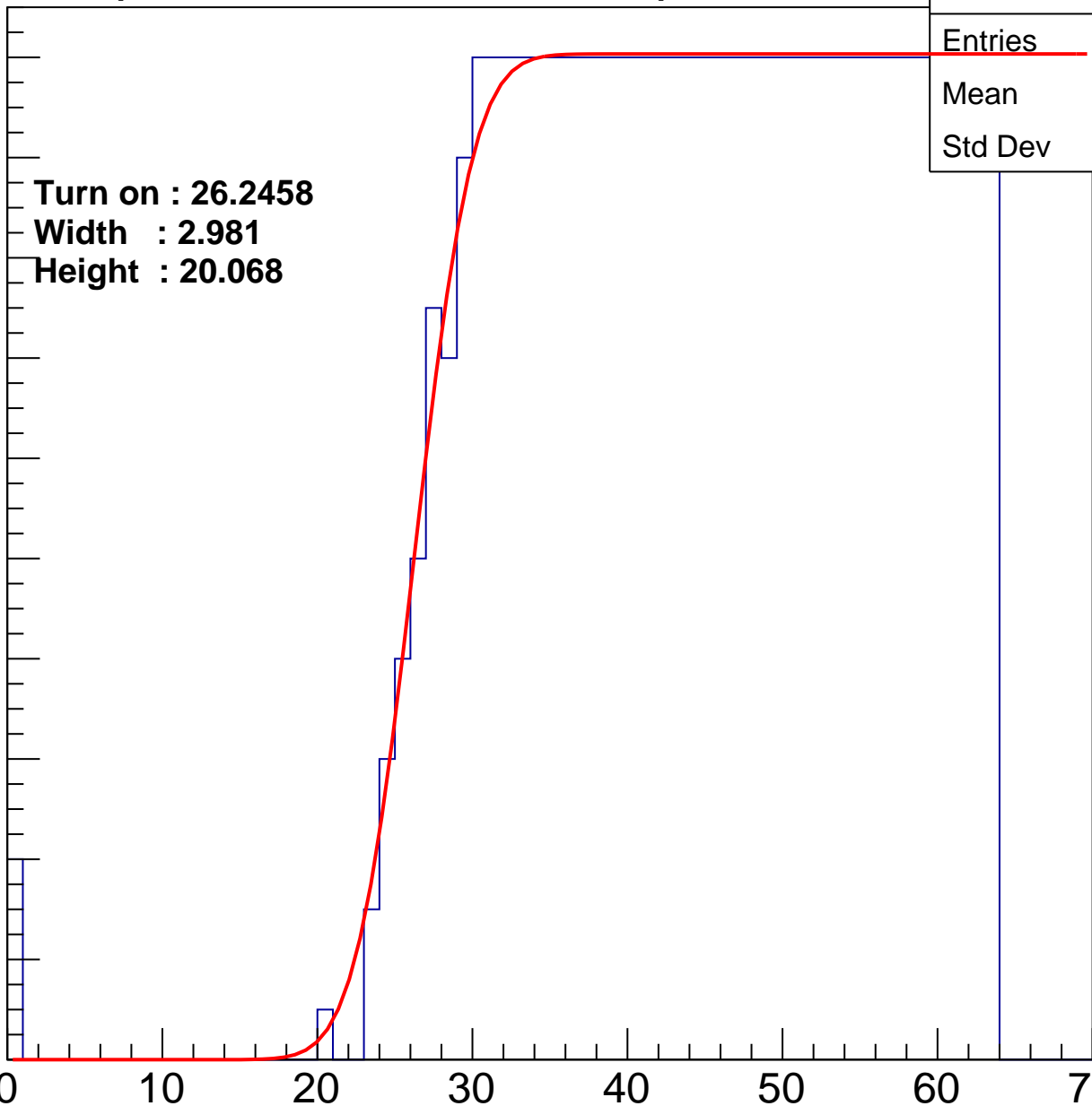
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2458
Width : 2.981
Height : 20.068

Entries	759
Mean	44.31
Std Dev	11.46

ampl



B1L001S, U25-ch25

calib_packv5_042523_0143.root, FC#2, port C2

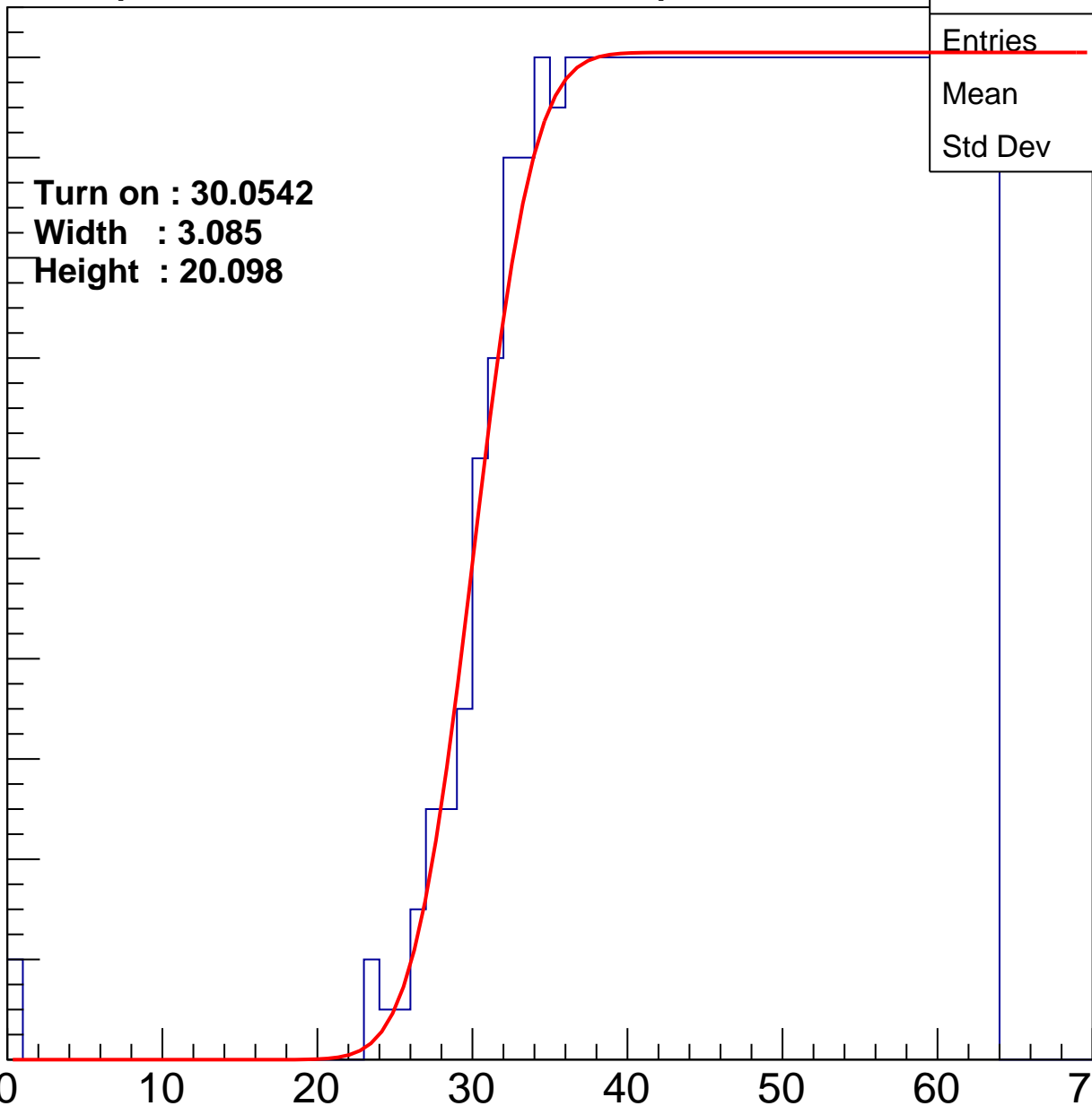
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 30.0542
Width : 3.085
Height : 20.098

Entries	687
Mean	46.11
Std Dev	10.39

ampl



B1L001S, U25-ch26

calib_packv5_042523_0143.root, FC#2, port C2

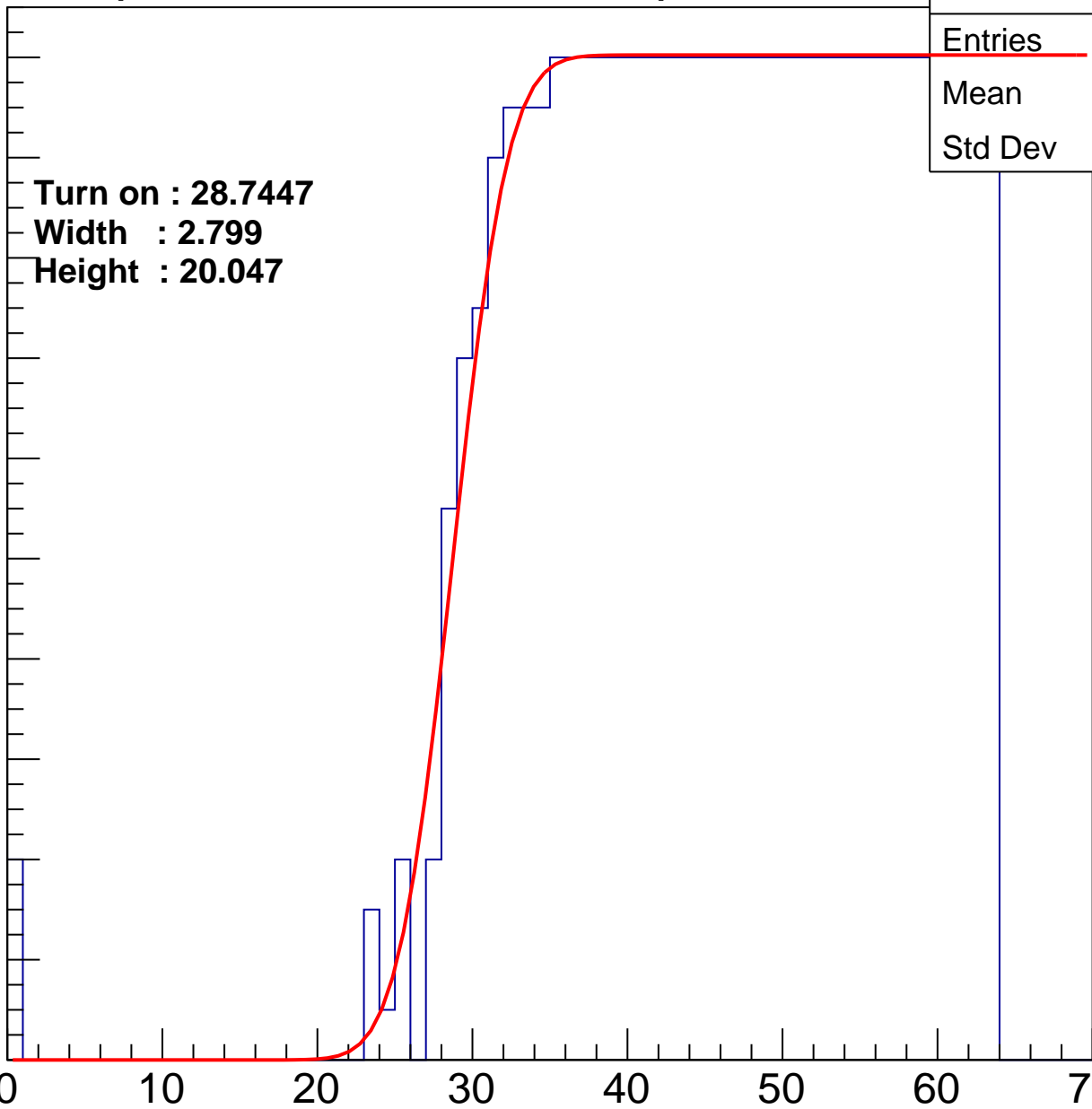
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.7447
Width : 2.799
Height : 20.047

Entries	711
Mean	45.46
Std Dev	10.9

ampl



B1L001S, U25-ch27

calib_packv5_042523_0143.root, FC#2, port C2

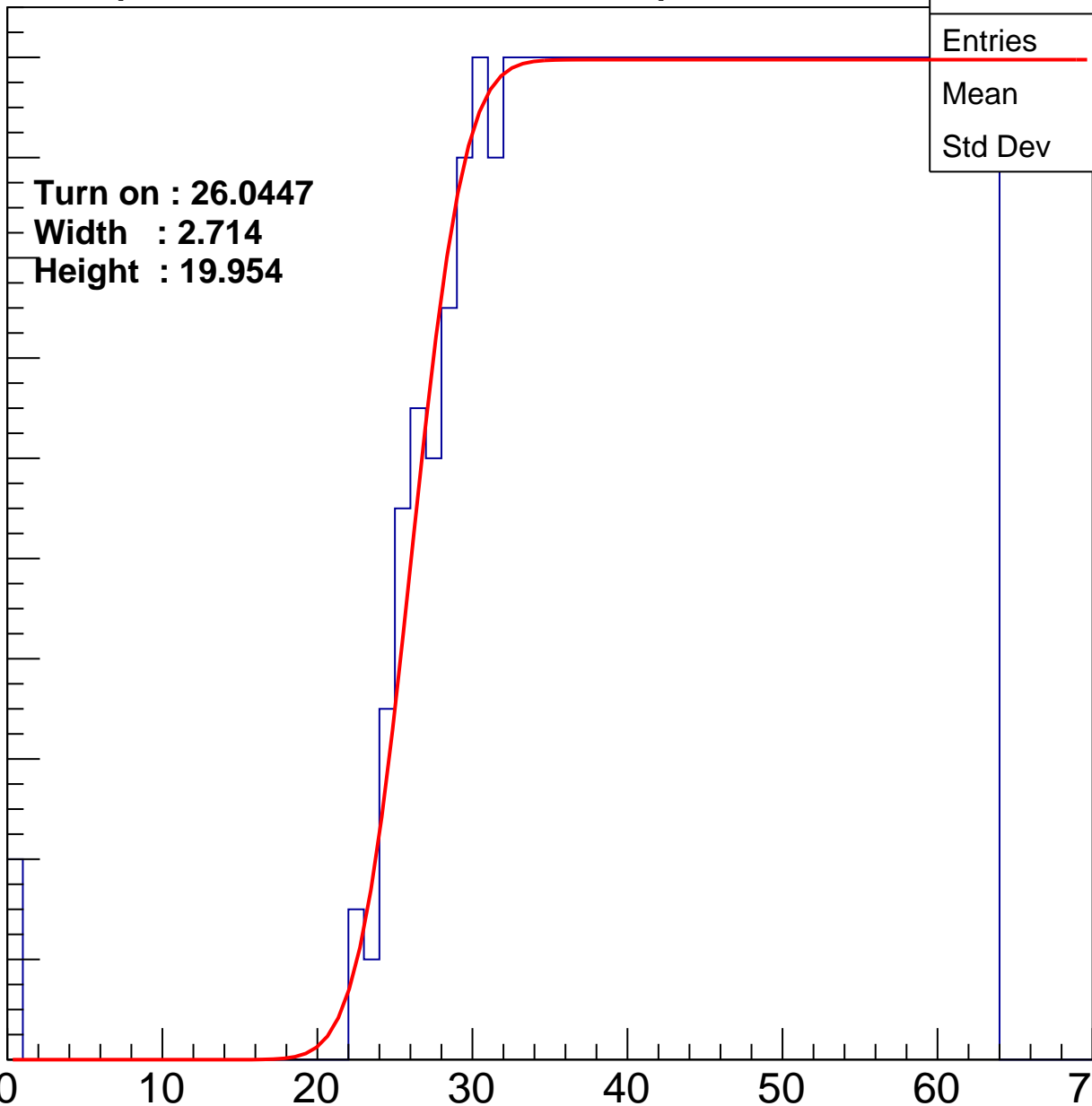
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0447
Width : 2.714
Height : 19.954

Entries	763
Mean	44.19
Std Dev	11.55

ampl



B1L001S, U25-ch28

calib_packv5_042523_0143.root, FC#2, port C2

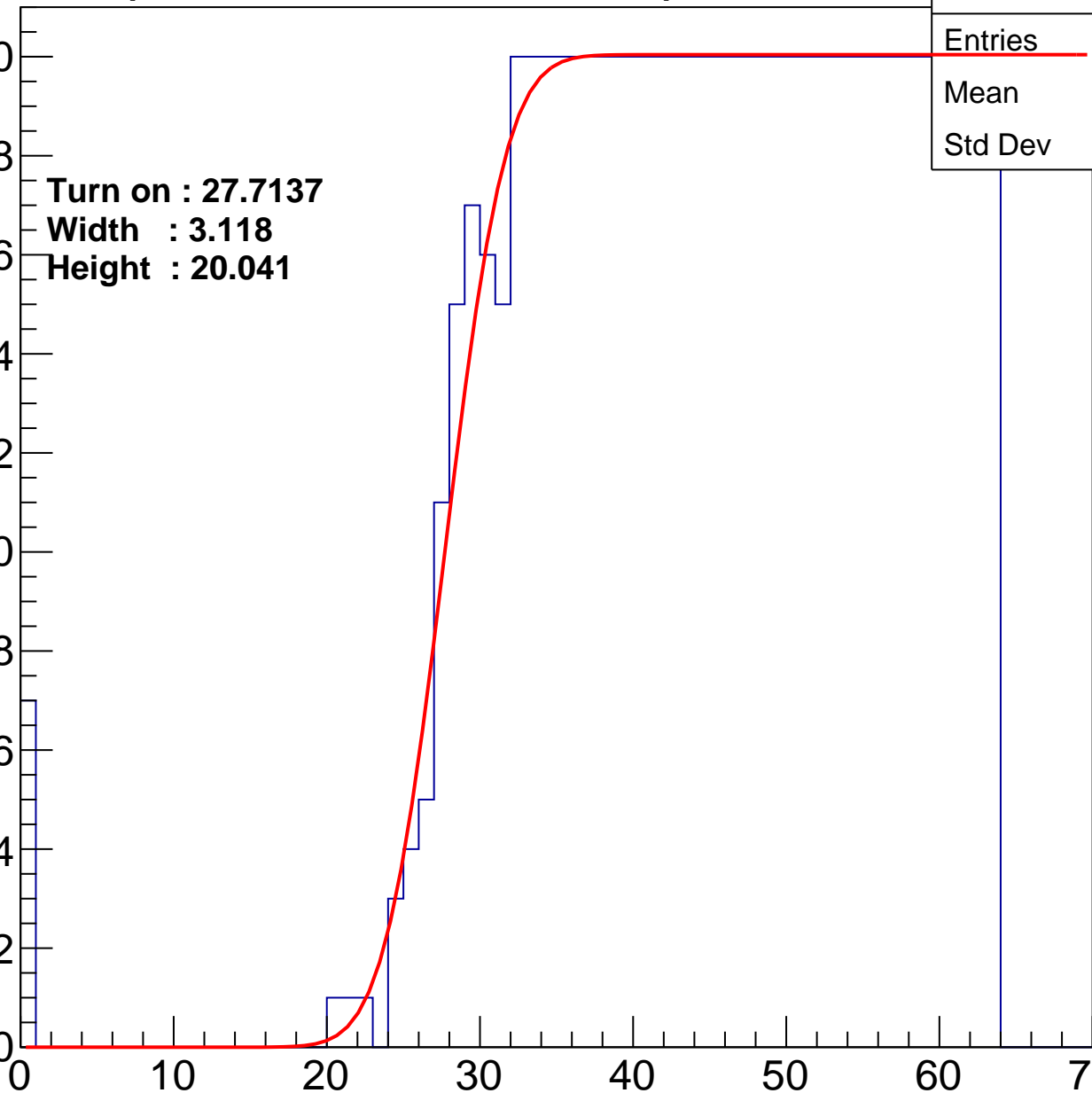
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7137
Width : 3.118
Height : 20.041

Entries	736
Mean	44.73
Std Dev	11.54

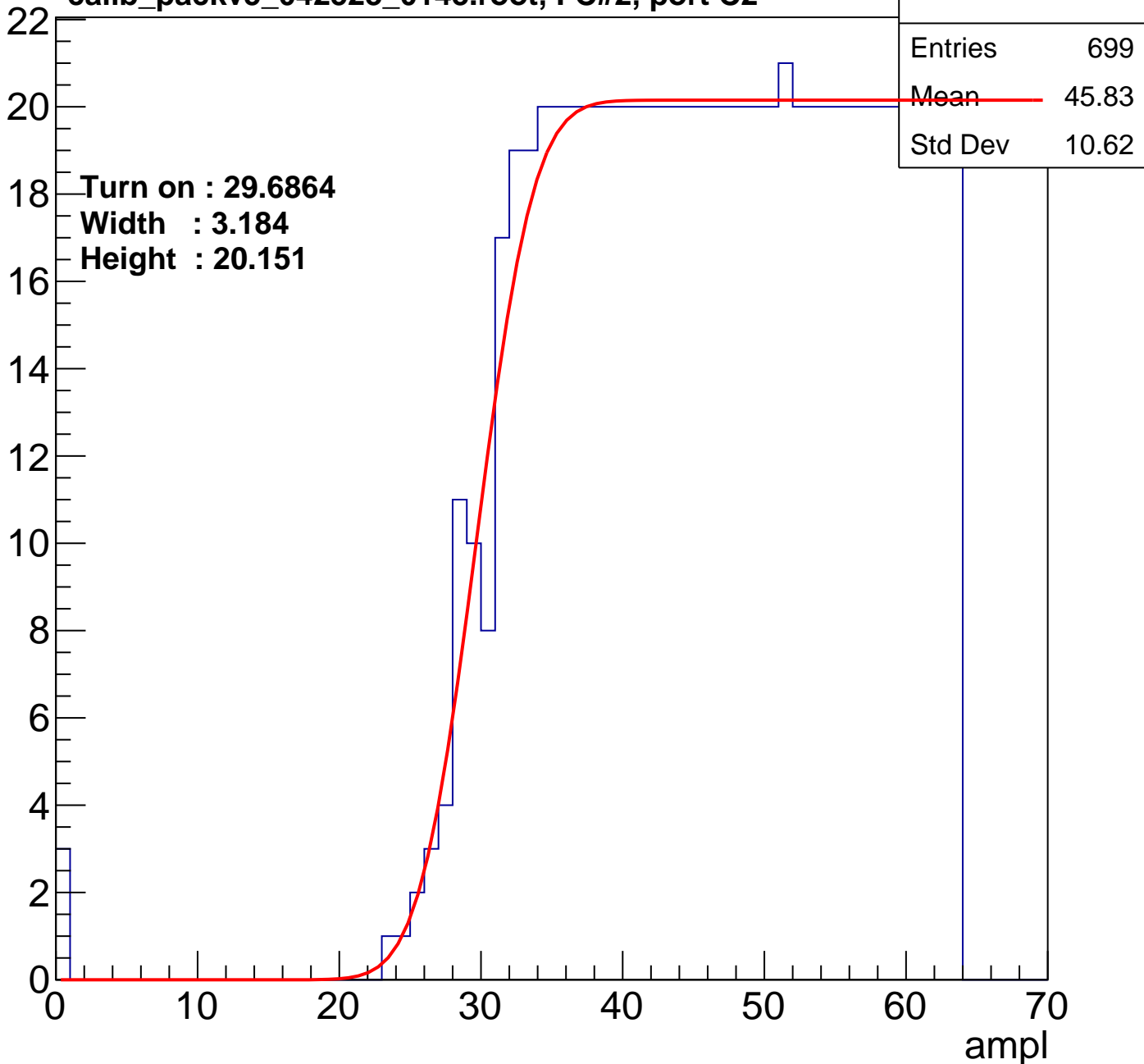
ampl



B1L001S, U25-ch29

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch30

calib_packv5_042523_0143.root, FC#2, port C2

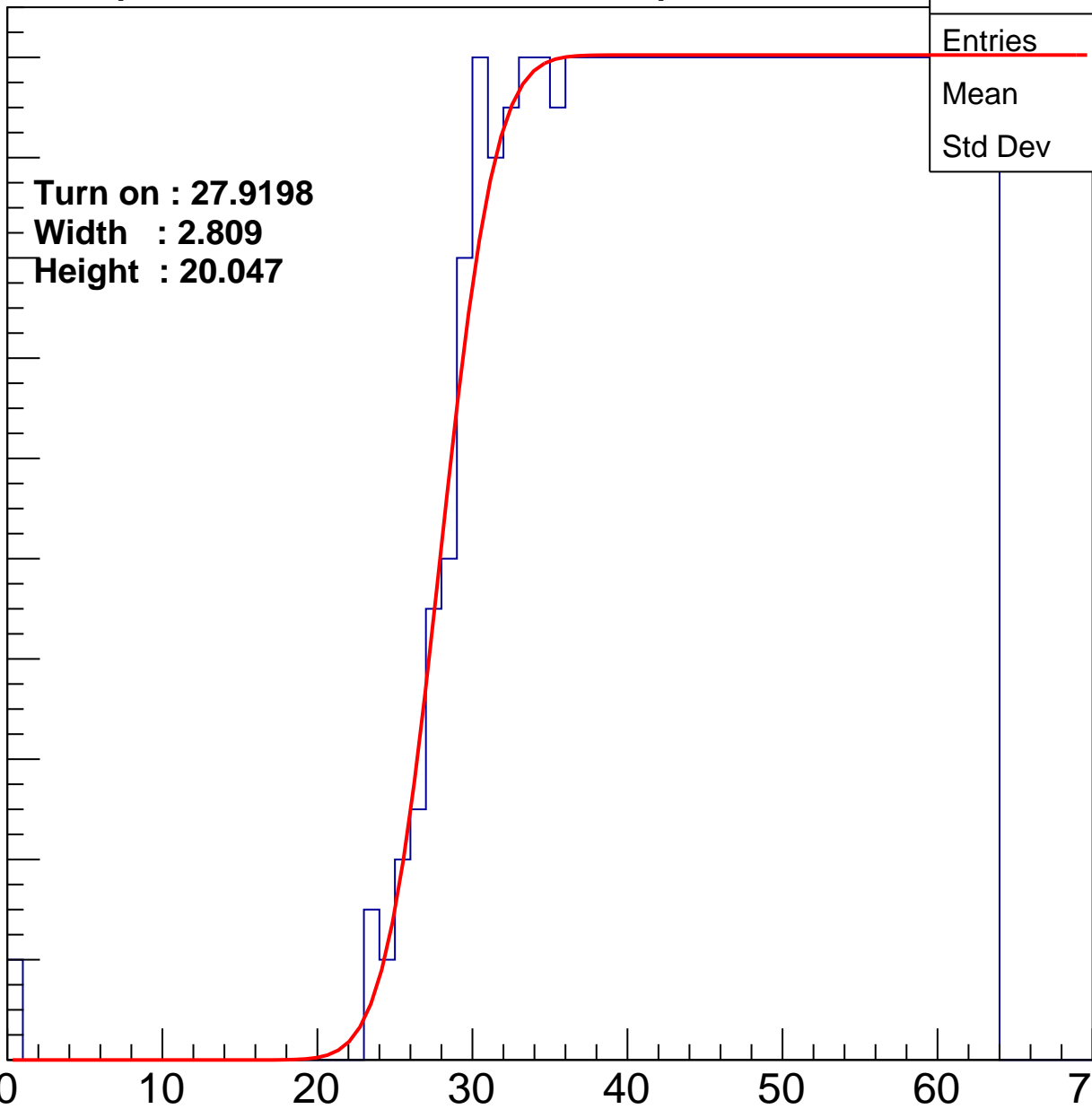
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9198
Width : 2.809
Height : 20.047

Entries	727
Mean	45.15
Std Dev	10.88

ampl



B1L001S, U25-ch31

calib_packv5_042523_0143.root, FC#2, port C2

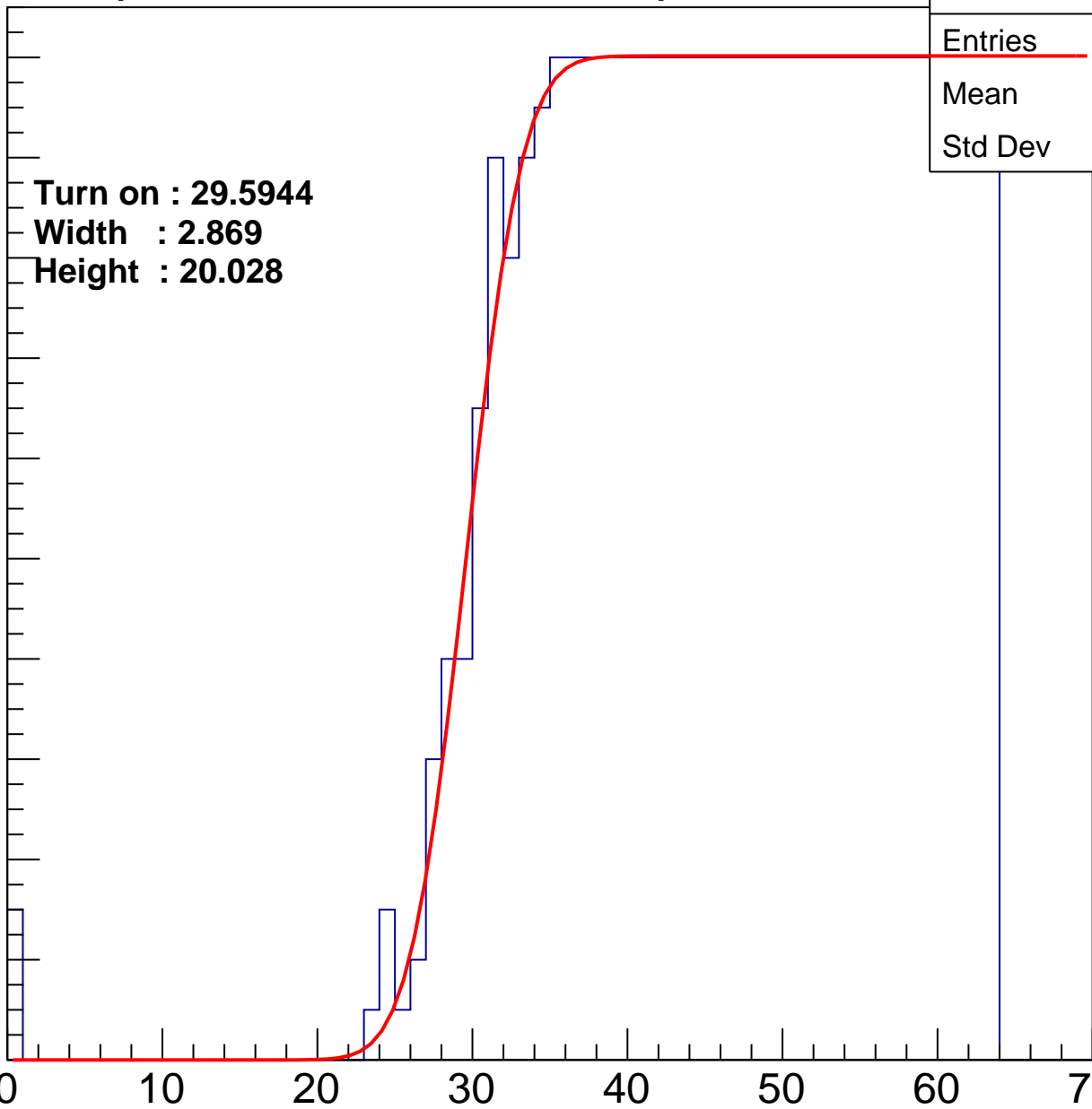
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.5944
Width : 2.869
Height : 20.028

Entries	696
Mean	45.85
Std Dev	10.64

ampl



B1L001S, U25-ch32

calib_packv5_042523_0143.root, FC#2, port C2

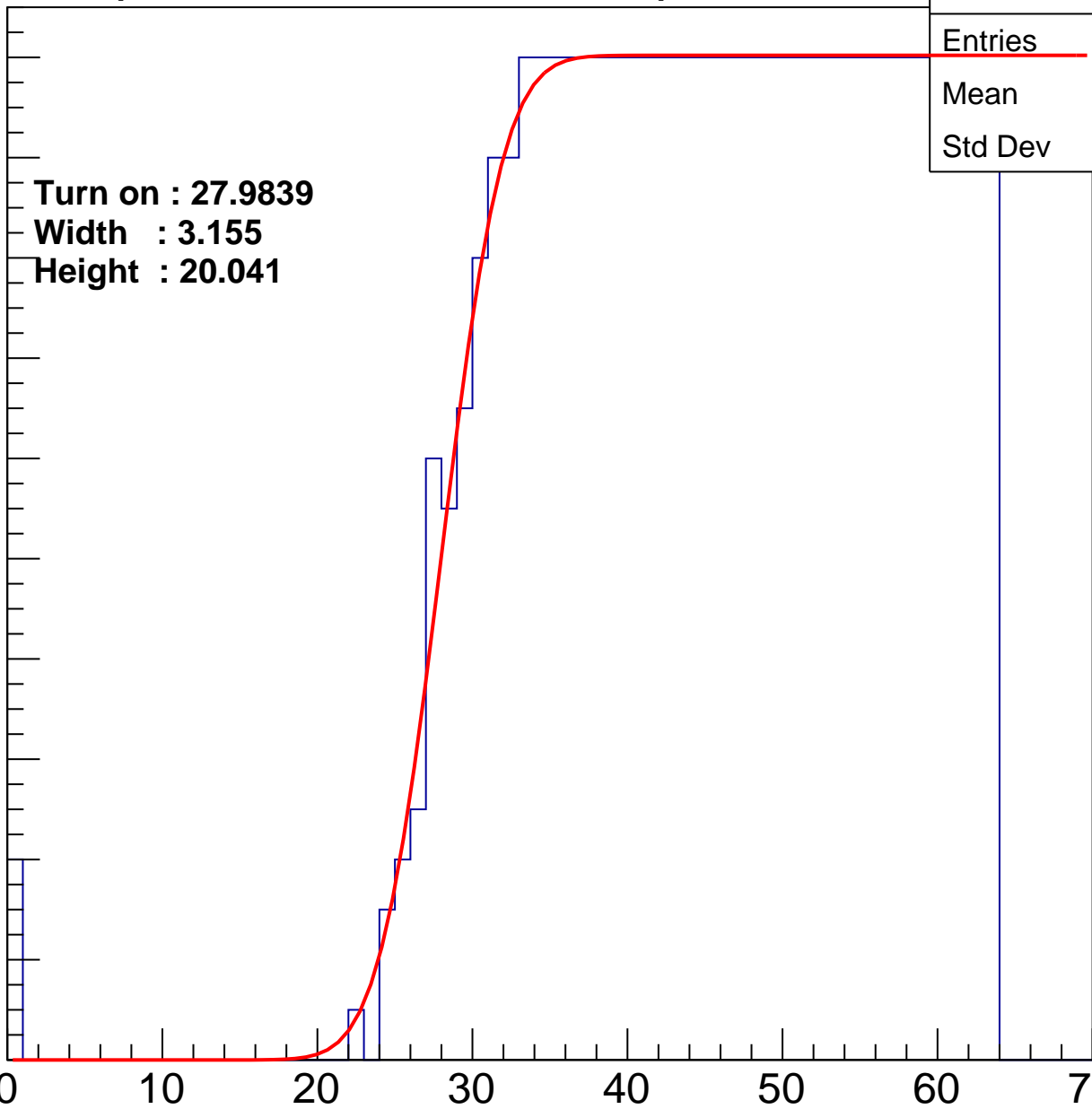
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9839
Width : 3.155
Height : 20.041

Entries	725
Mean	45.11
Std Dev	11.09

ampl



B1L001S, U25-ch33

calib_packv5_042523_0143.root, FC#2, port C2

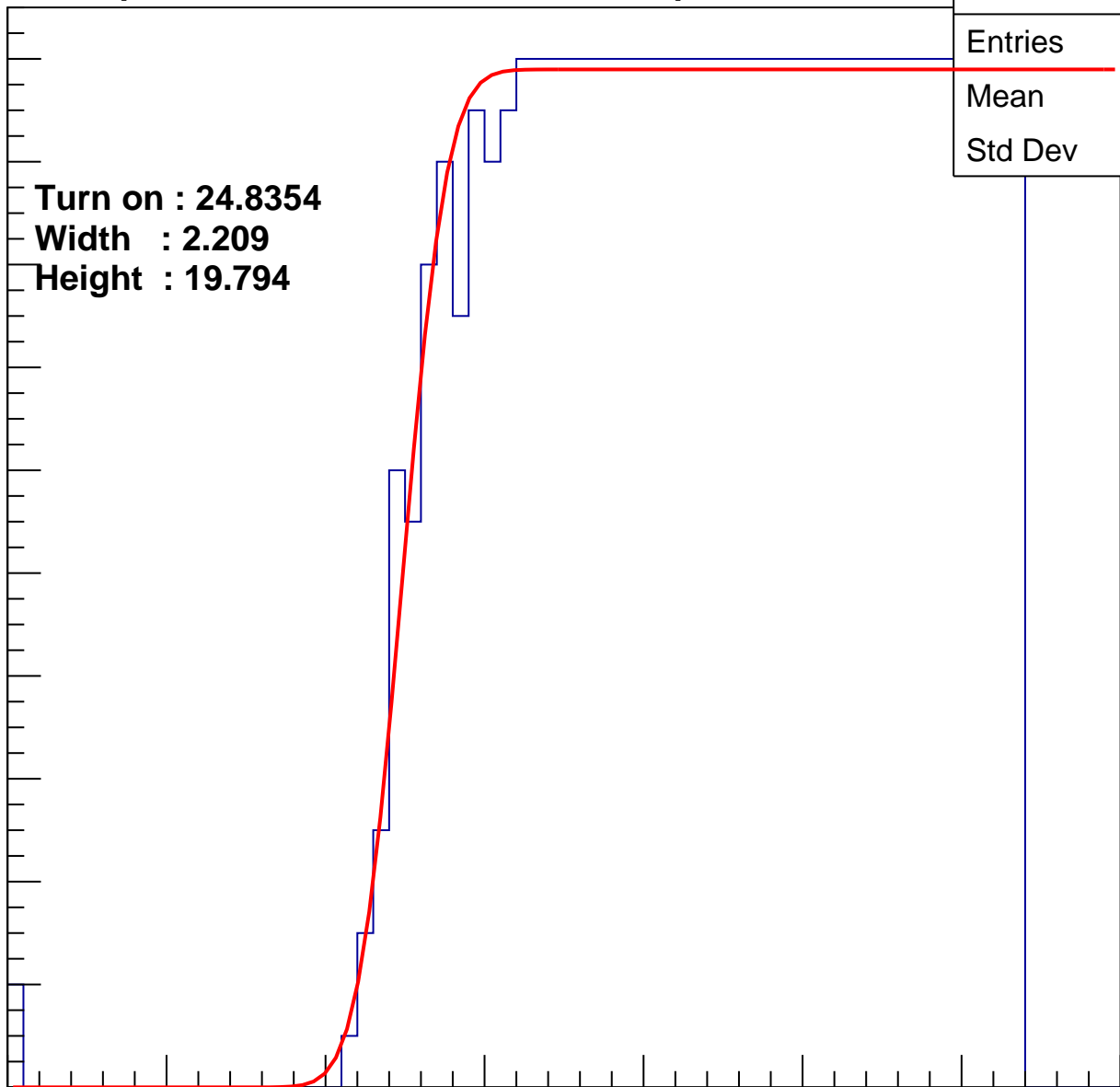
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8354
Width : 2.209
Height : 19.794

Entries	779
Mean	43.86
Std Dev	11.58

ampl



B1L001S, U25-ch34

calib_packv5_042523_0143.root, FC#2, port C2

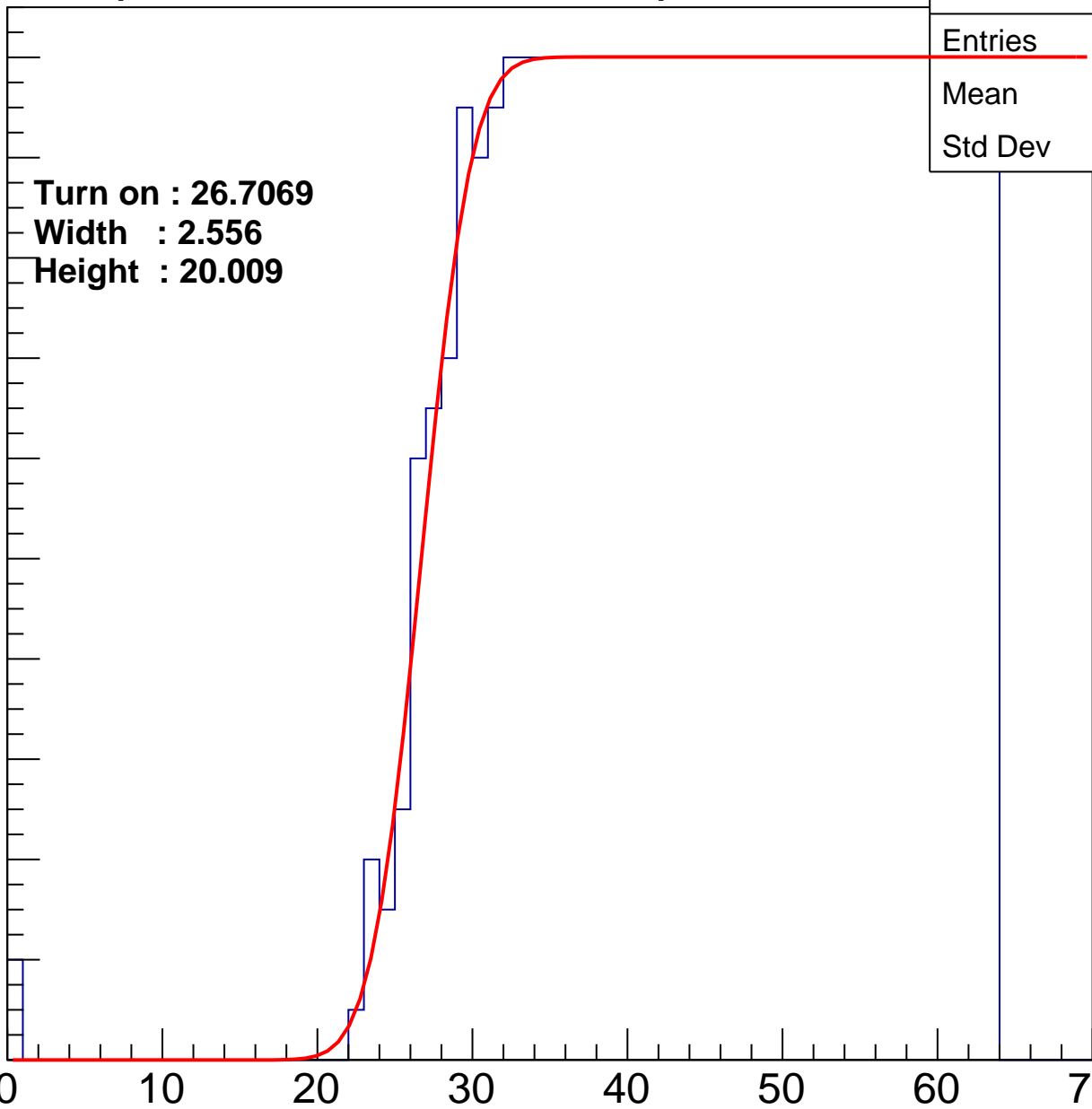
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7069
Width : 2.556
Height : 20.009

Entries	750
Mean	44.59
Std Dev	11.17

ampl



B1L001S, U25-ch35

calib_packv5_042523_0143.root, FC#2, port C2

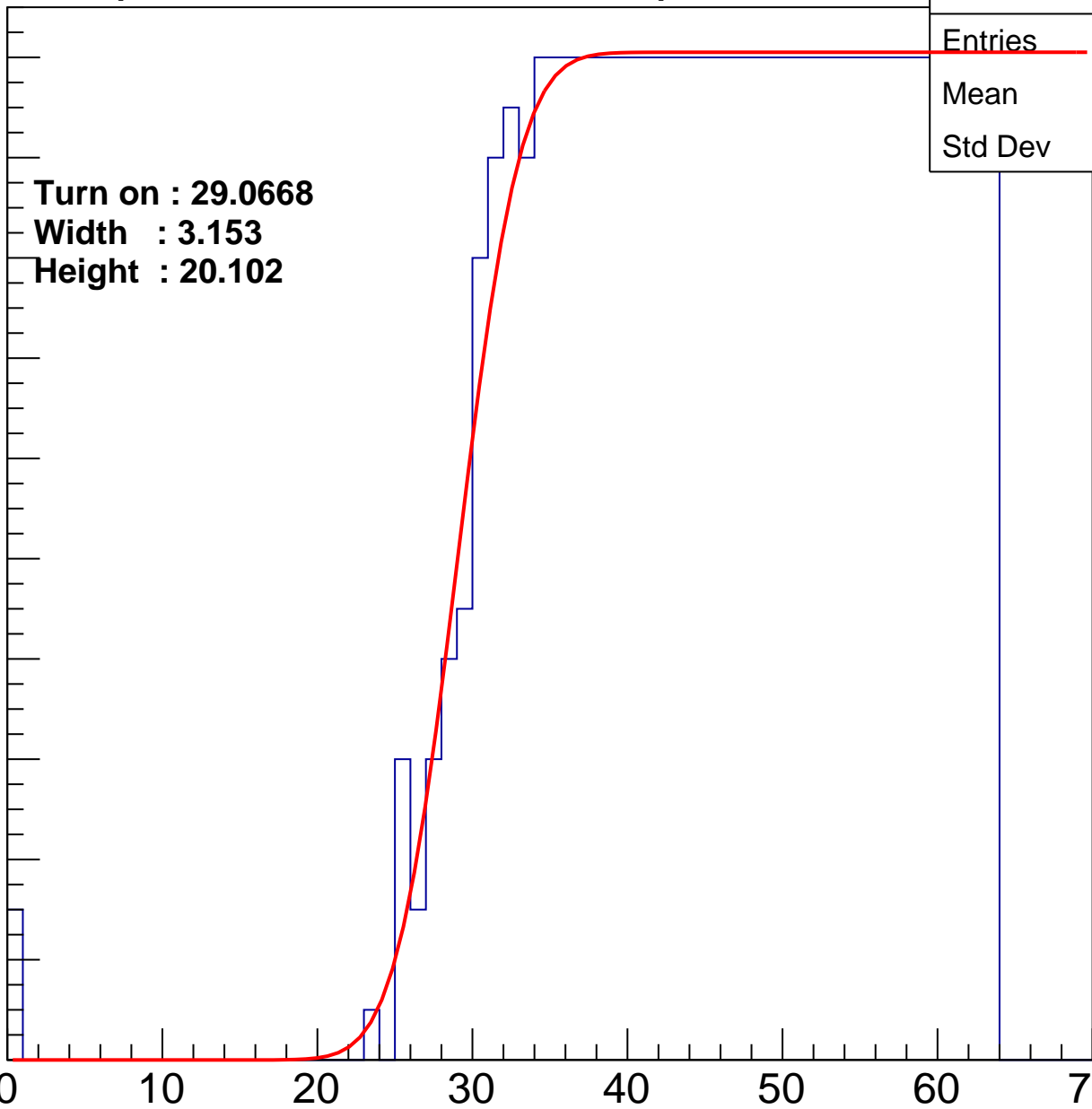
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.0668
Width : 3.153
Height : 20.102

Entries	707
Mean	45.6
Std Dev	10.74

ampl



B1L001S, U25-ch36

calib_packv5_042523_0143.root, FC#2, port C2

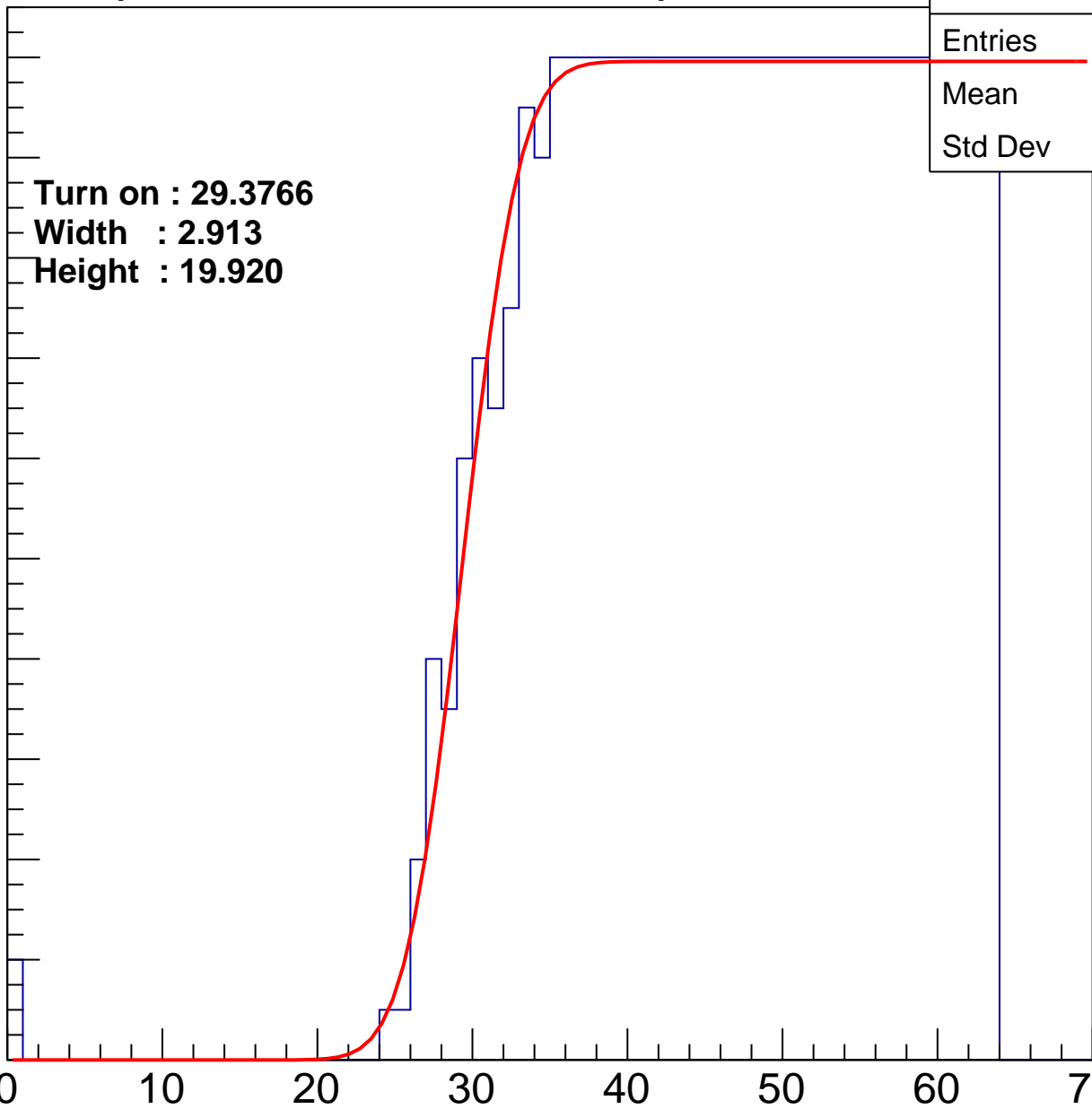
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.3766
Width : 2.913
Height : 19.920

Entries	694
Mean	45.93
Std Dev	10.5

ampl



B1L001S, U25-ch37

calib_packv5_042523_0143.root, FC#2, port C2

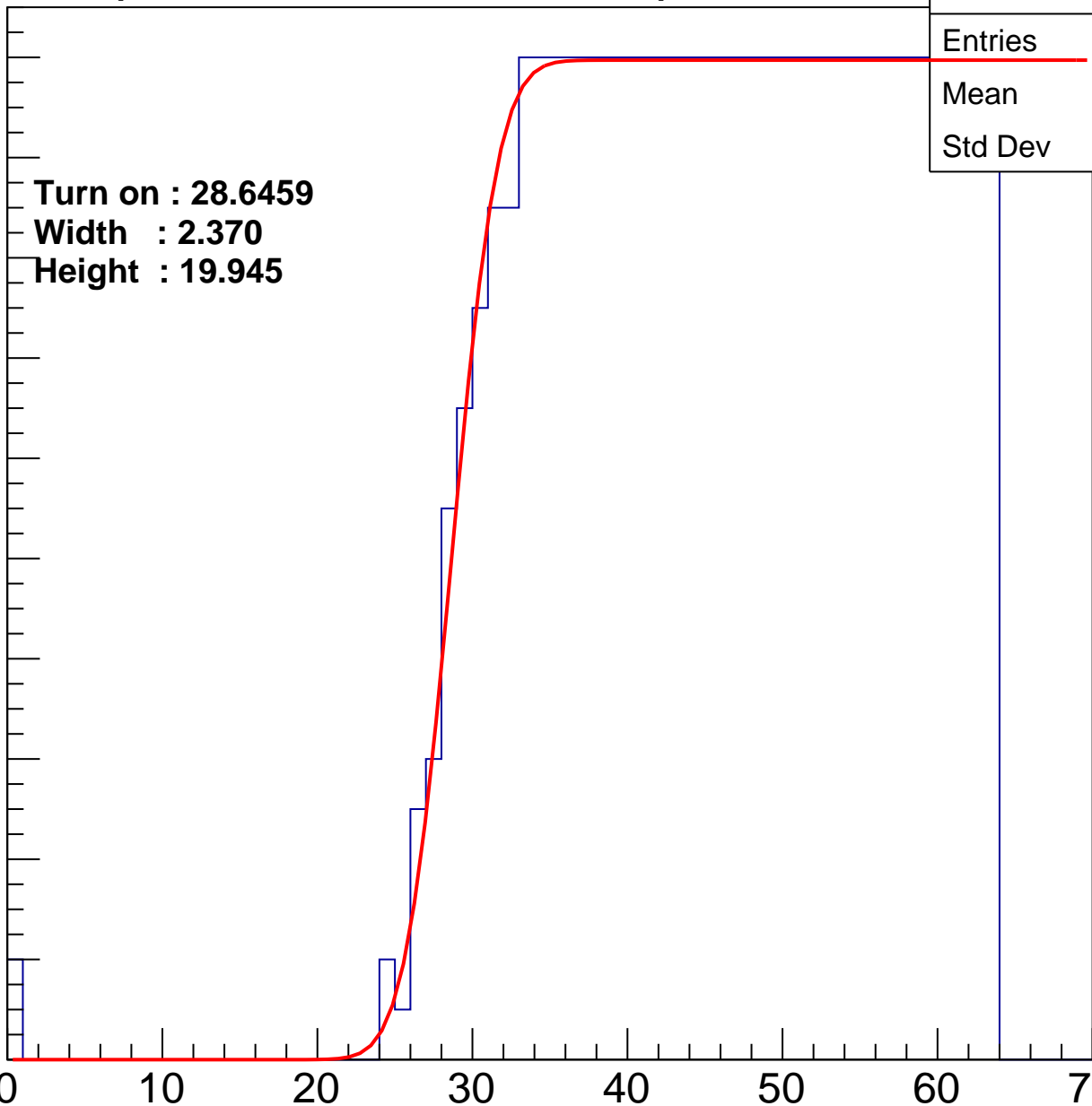
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6459
Width : 2.370
Height : 19.945

Entries	709
Mean	45.6
Std Dev	10.63

ampl



B1L001S, U25-ch38

calib_packv5_042523_0143.root, FC#2, port C2

Entry

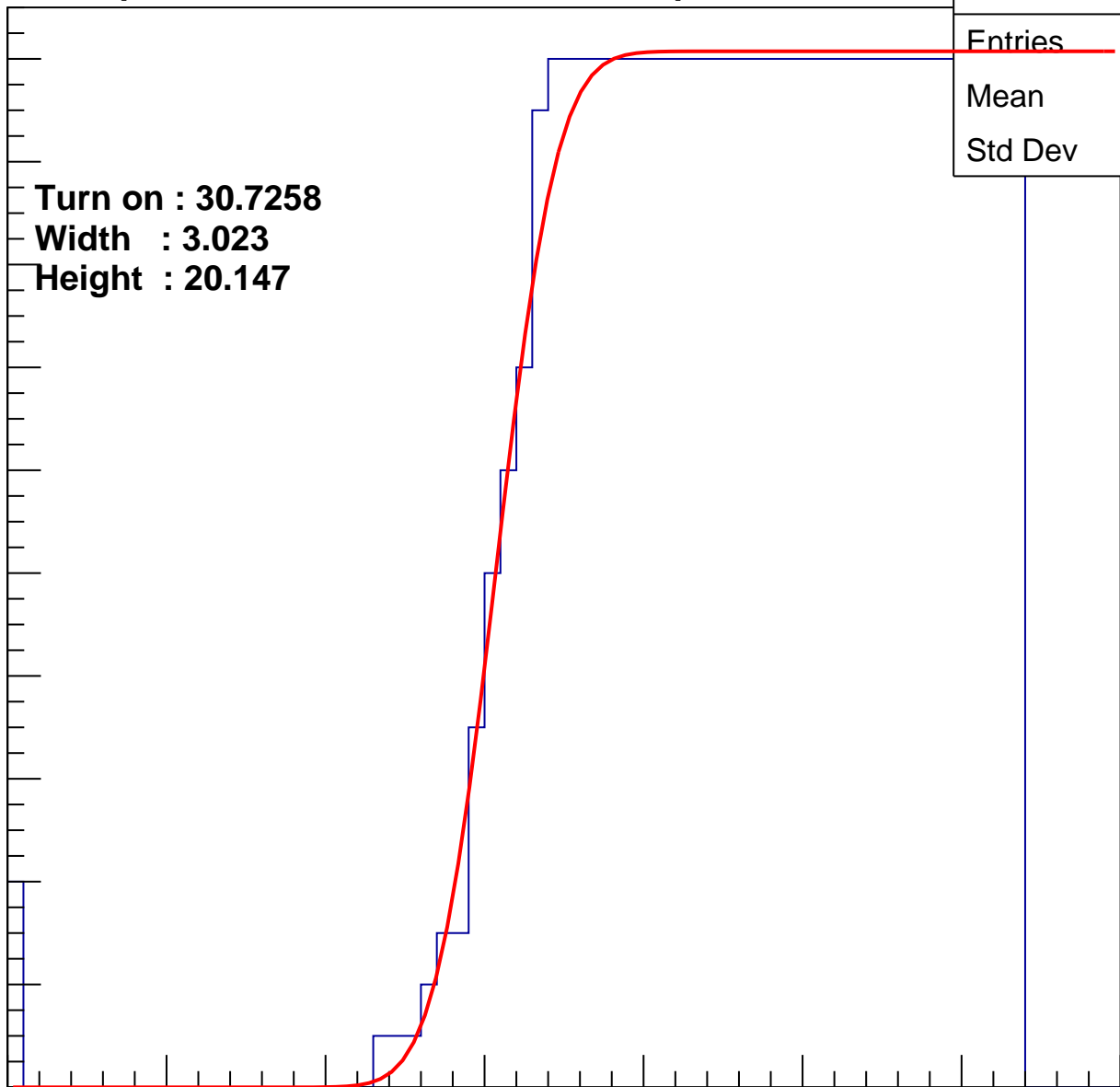
20
18
16
14
12
10
8
6
4
2
0

Turn on : 30.7258
Width : 3.023
Height : 20.147

Entries	677
Mean	46.29
Std Dev	10.5

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch39

calib_packv5_042523_0143.root, FC#2, port C2

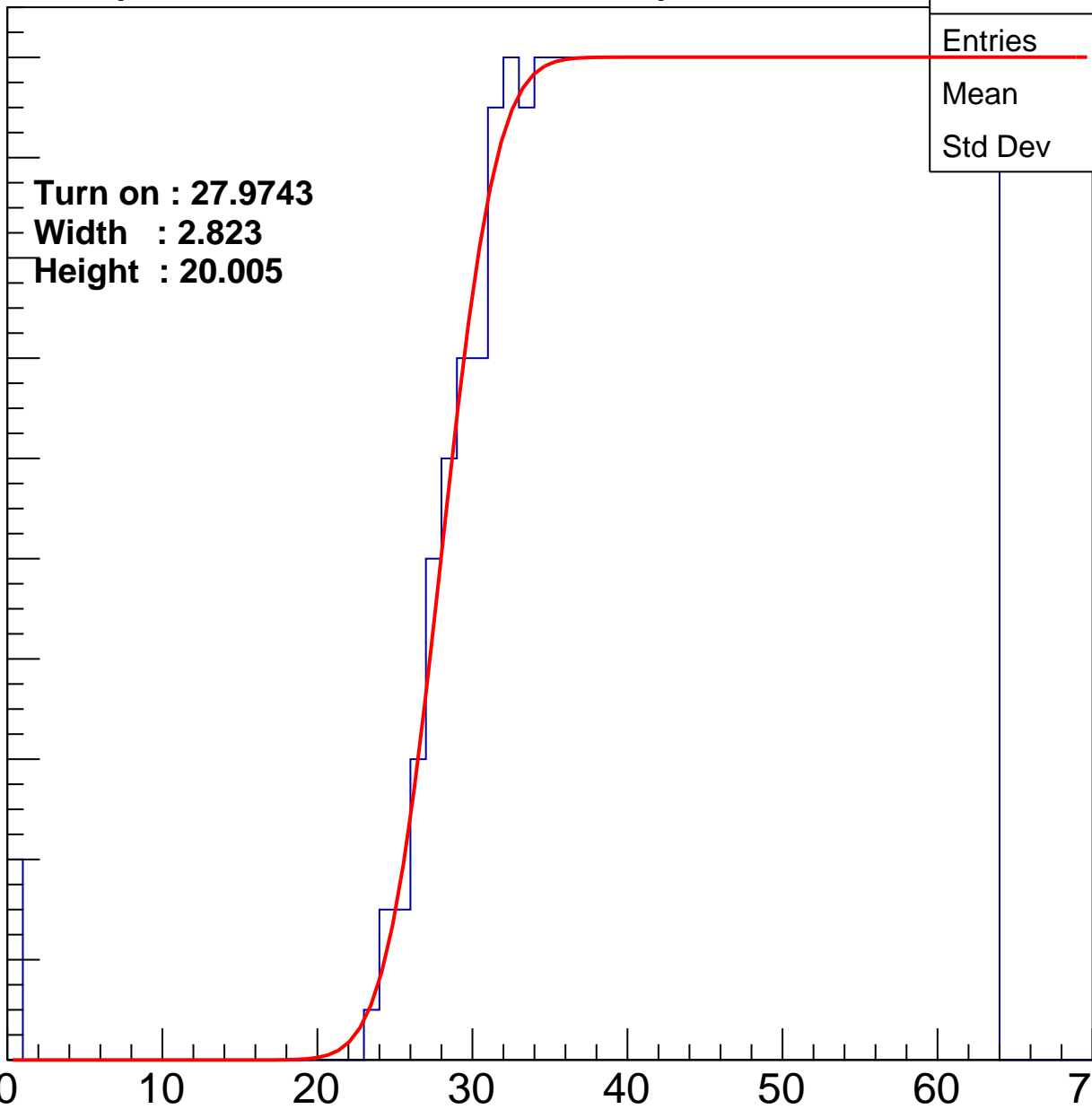
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9743
Width : 2.823
Height : 20.005

Entries	725
Mean	45.12
Std Dev	11.07

ampl



B1L001S, U25-ch40

calib_packv5_042523_0143.root, FC#2, port C2

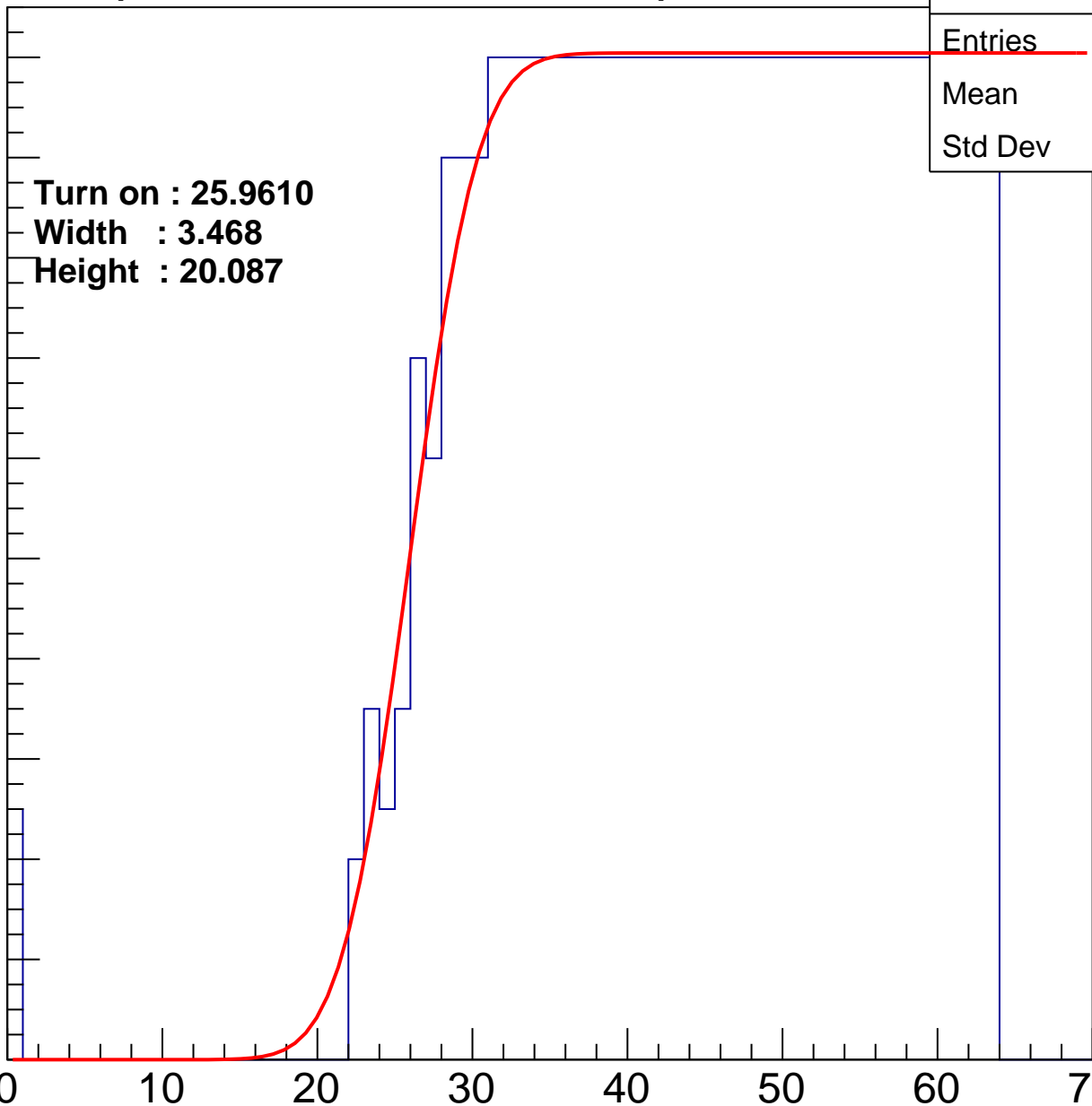
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9610
Width : 3.468
Height : 20.087

Entries	768
Mean	44.03
Std Dev	11.7

ampl



B1L001S, U25-ch41

calib_packv5_042523_0143.root, FC#2, port C2

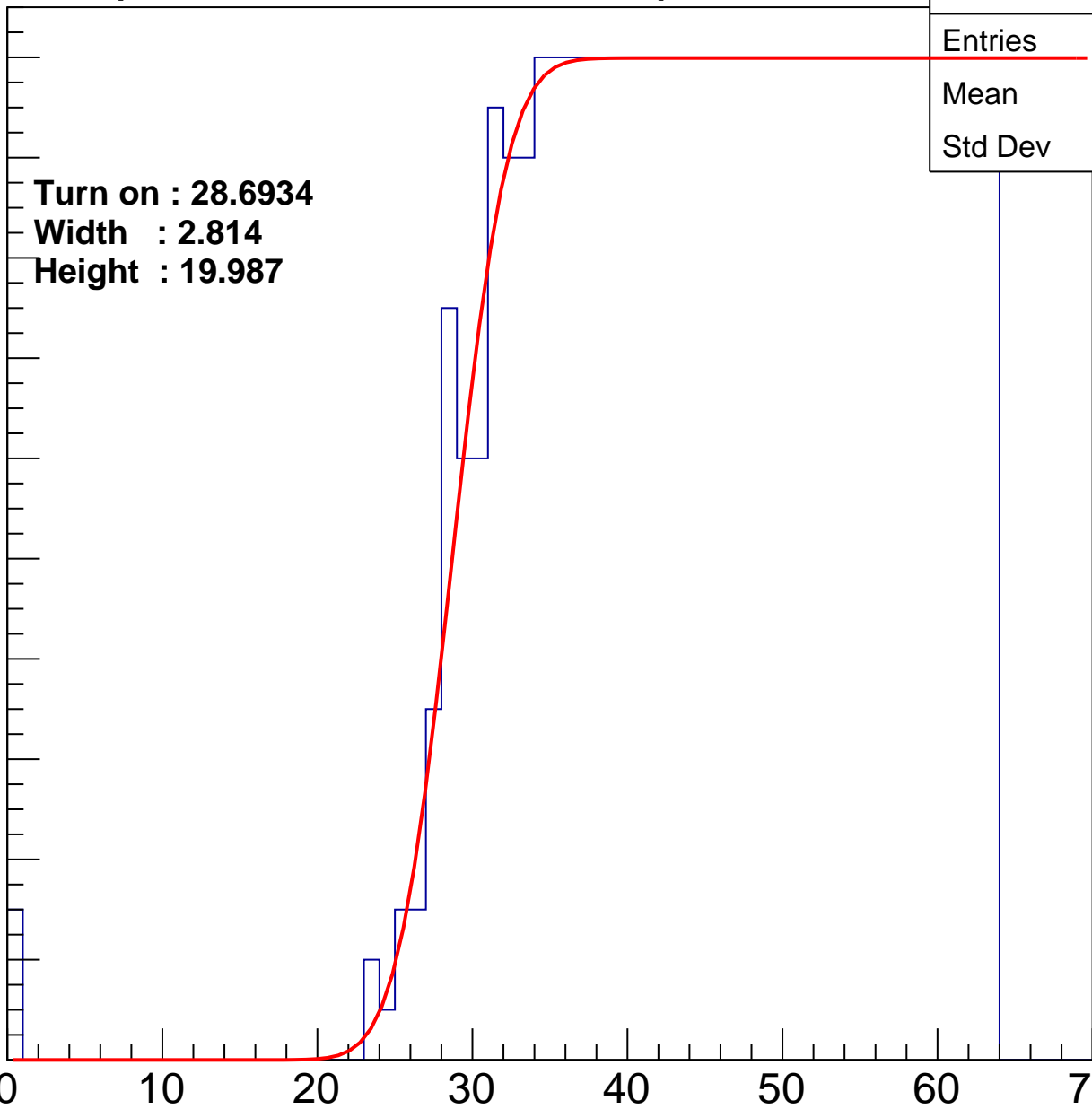
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6934
Width : 2.814
Height : 19.987

Entries	713
Mean	45.44
Std Dev	10.83

ampl



B1L001S, U25-ch42

calib_packv5_042523_0143.root, FC#2, port C2

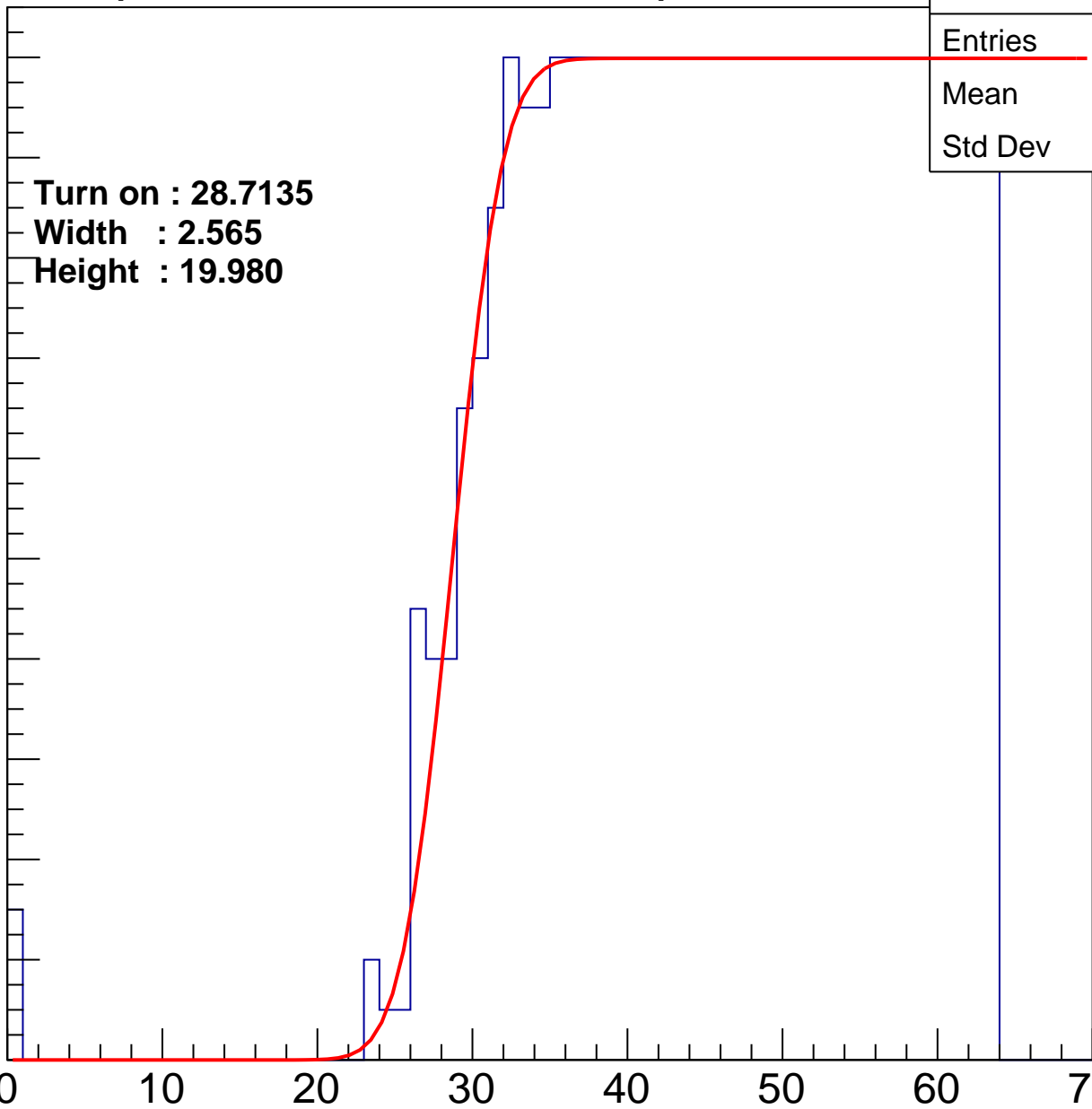
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.7135
Width : 2.565
Height : 19.980

Entries	714
Mean	45.41
Std Dev	10.85

ampl



B1L001S, U25-ch43

calib_packv5_042523_0143.root, FC#2, port C2

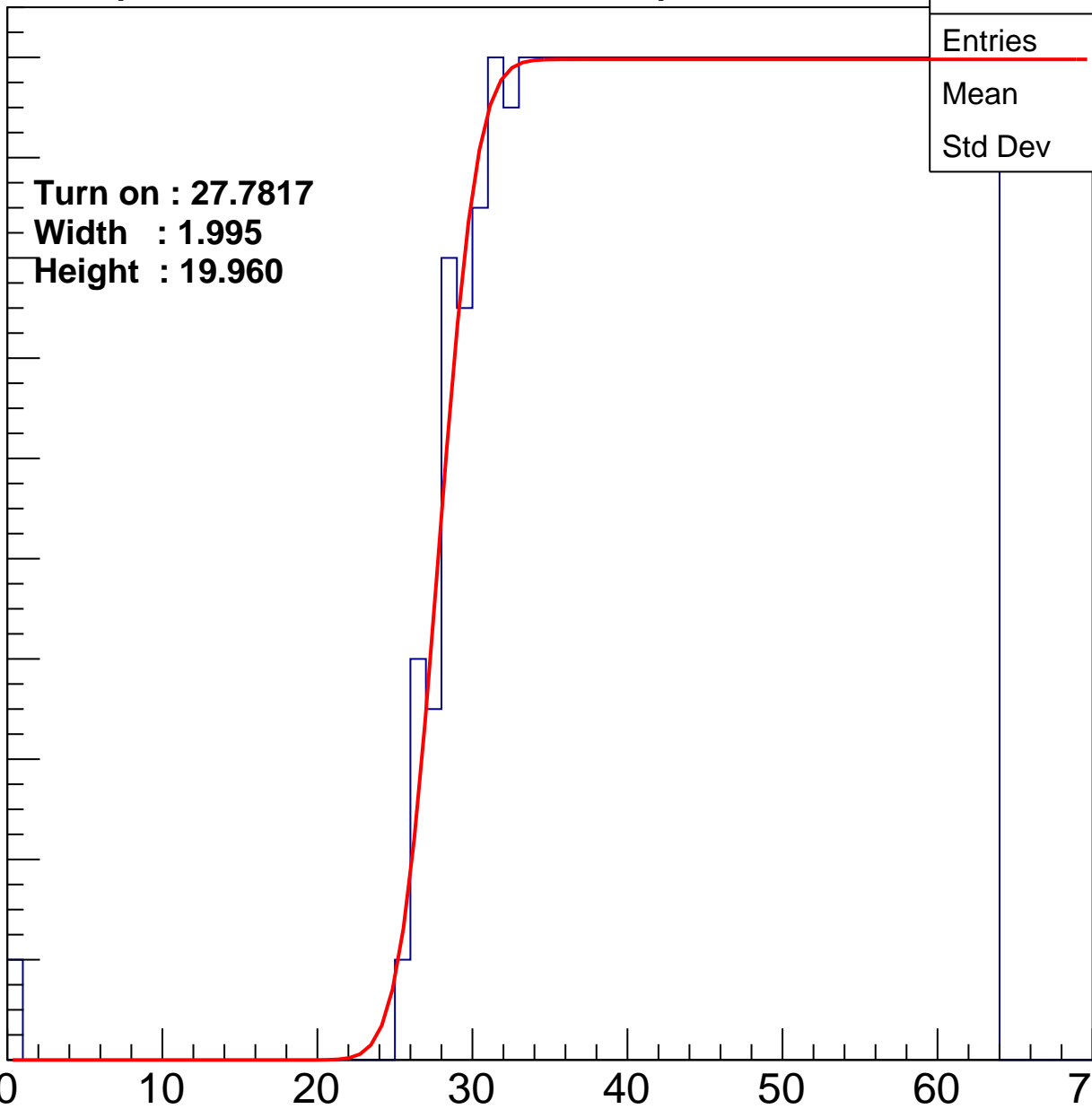
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7817
Width : 1.995
Height : 19.960

Entries	726
Mean	45.22
Std Dev	10.79

ampl



B1L001S, U25-ch44

calib_packv5_042523_0143.root, FC#2, port C2

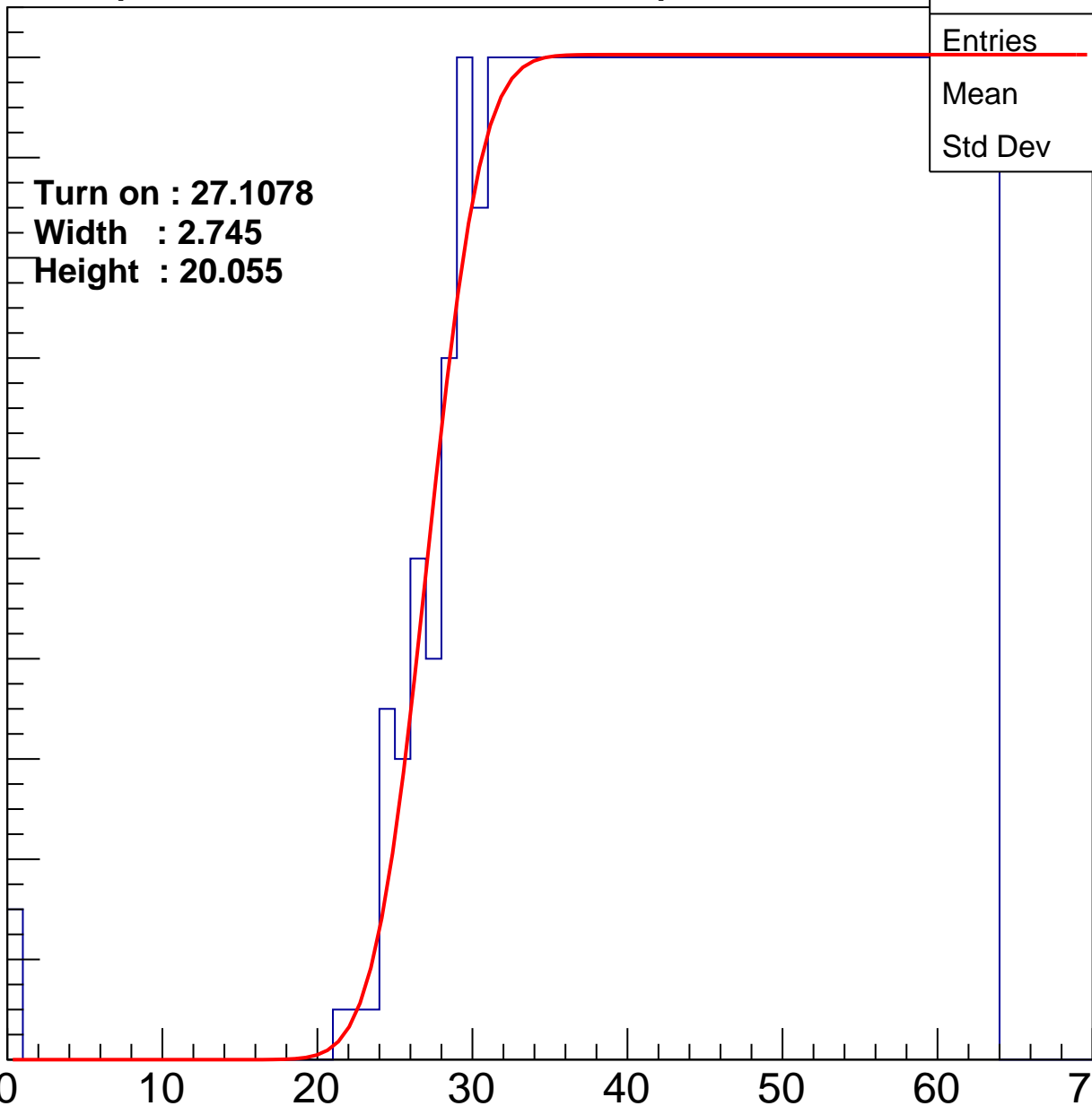
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1078
Width : 2.745
Height : 20.055

Entries	748
Mean	44.6
Std Dev	11.25

ampl



B1L001S, U25-ch45

calib_packv5_042523_0143.root, FC#2, port C2

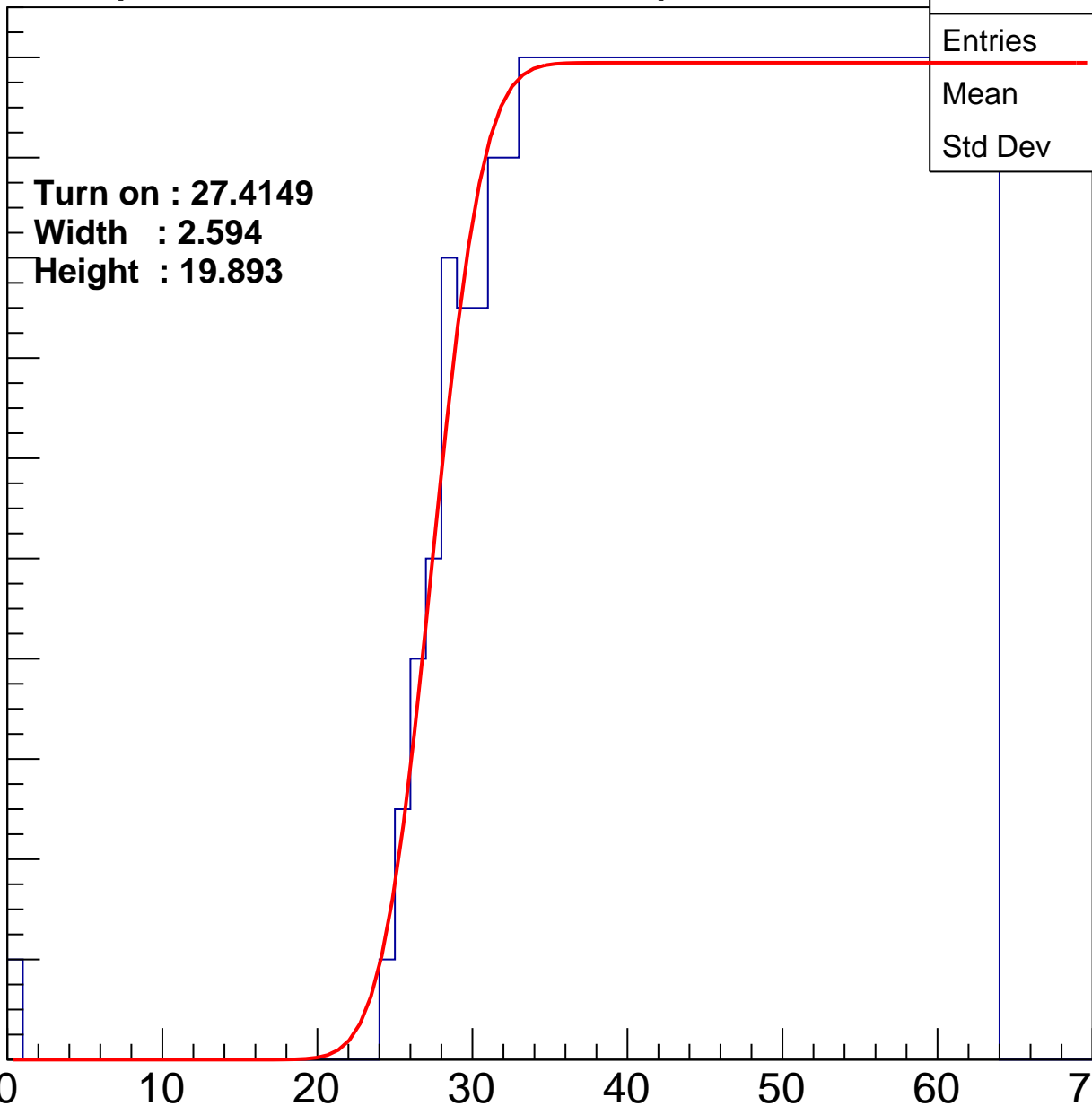
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4149
Width : 2.594
Height : 19.893

Entries	729
Mean	45.1
Std Dev	10.9

ampl



B1L001S, U25-ch46

calib_packv5_042523_0143.root, FC#2, port C2

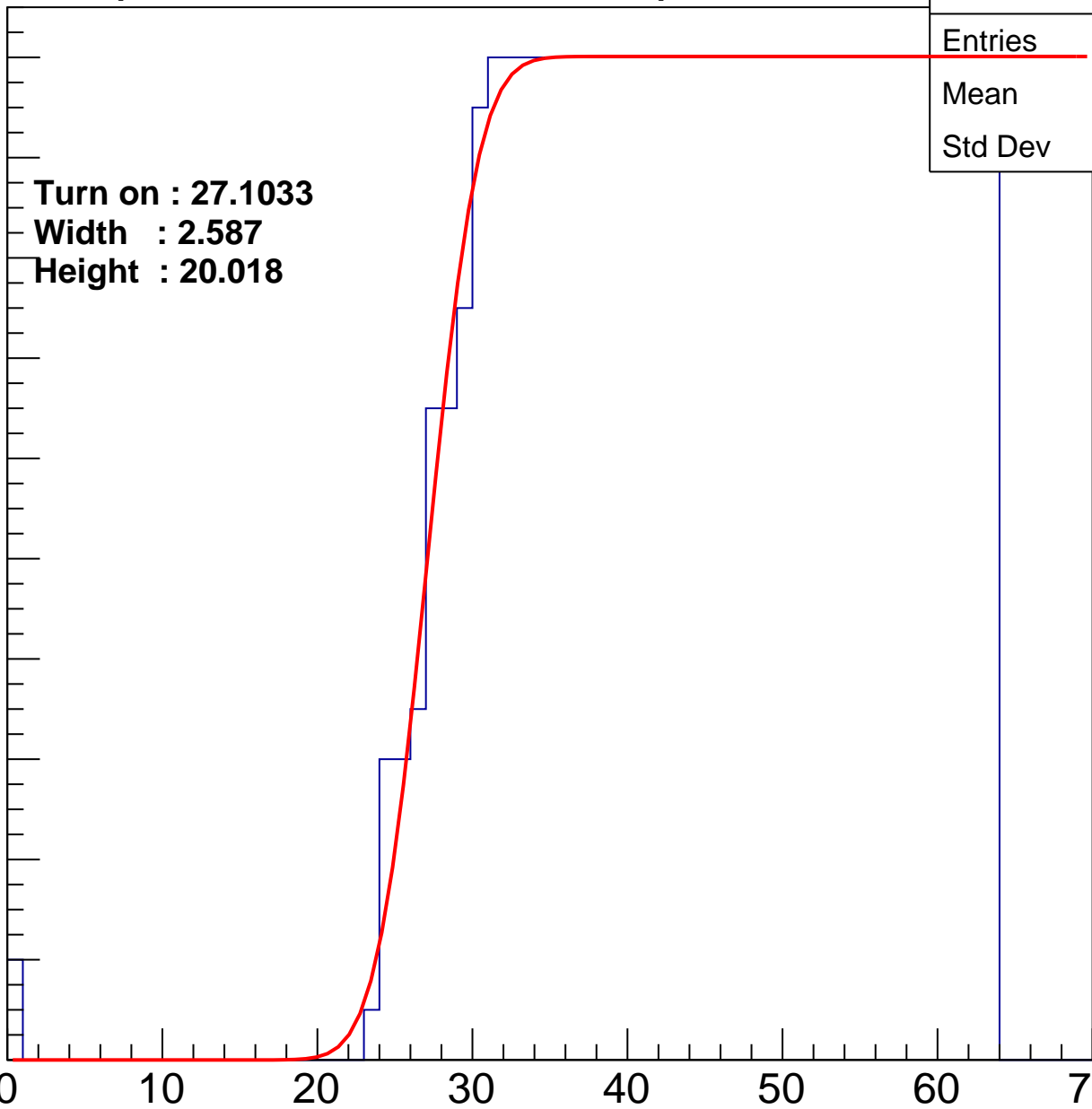
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1033
Width : 2.587
Height : 20.018

Entries	742
Mean	44.8
Std Dev	11.05

ampl



B1L001S, U25-ch47

calib_packv5_042523_0143.root, FC#2, port C2

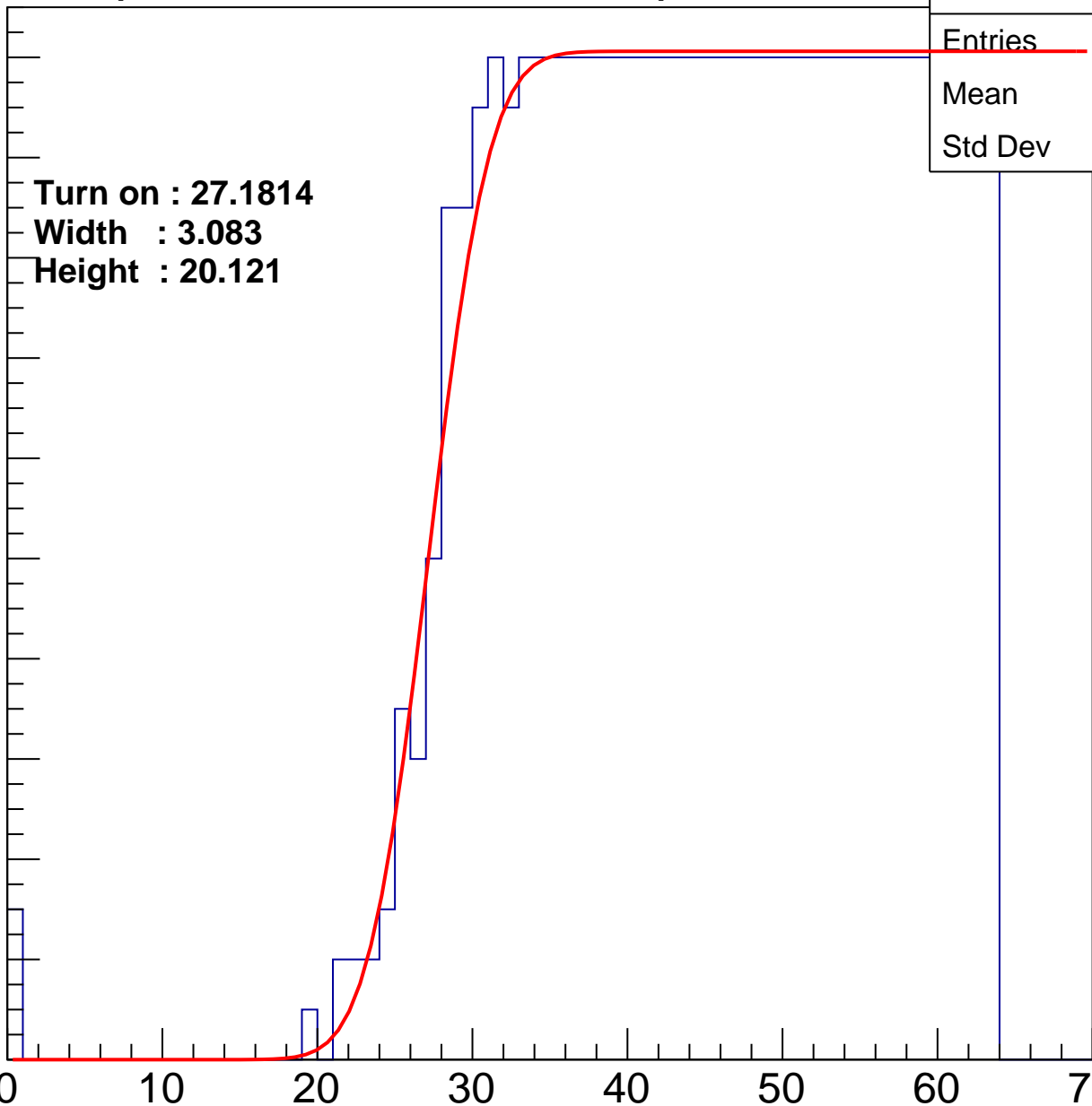
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1814
Width : 3.083
Height : 20.121

Entries	748
Mean	44.59
Std Dev	11.28

ampl



B1L001S, U25-ch48

calib_packv5_042523_0143.root, FC#2, port C2

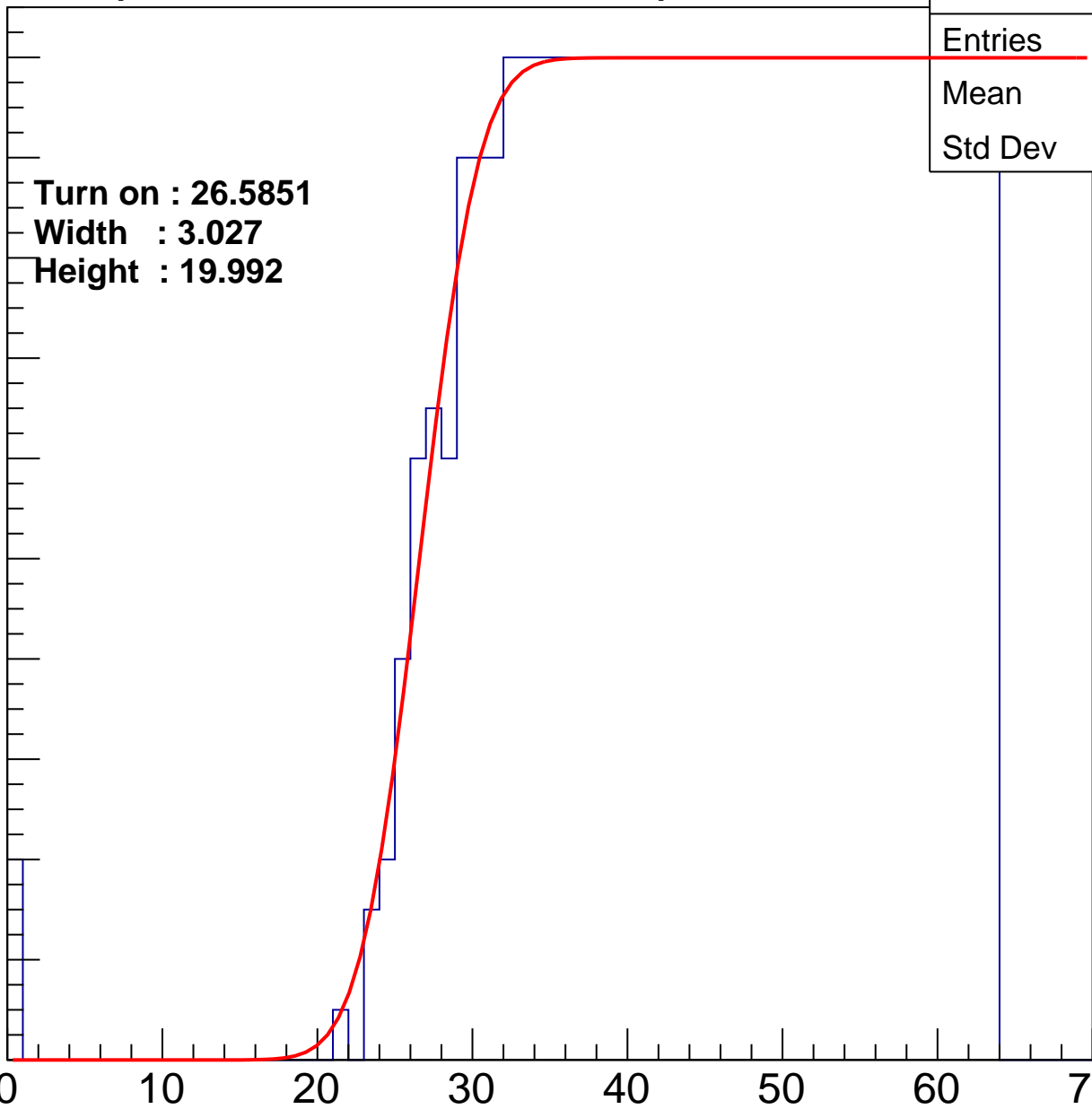
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5851
Width : 3.027
Height : 19.992

Entries	751
Mean	44.48
Std Dev	11.4

ampl



B1L001S, U25-ch49

calib_packv5_042523_0143.root, FC#2, port C2

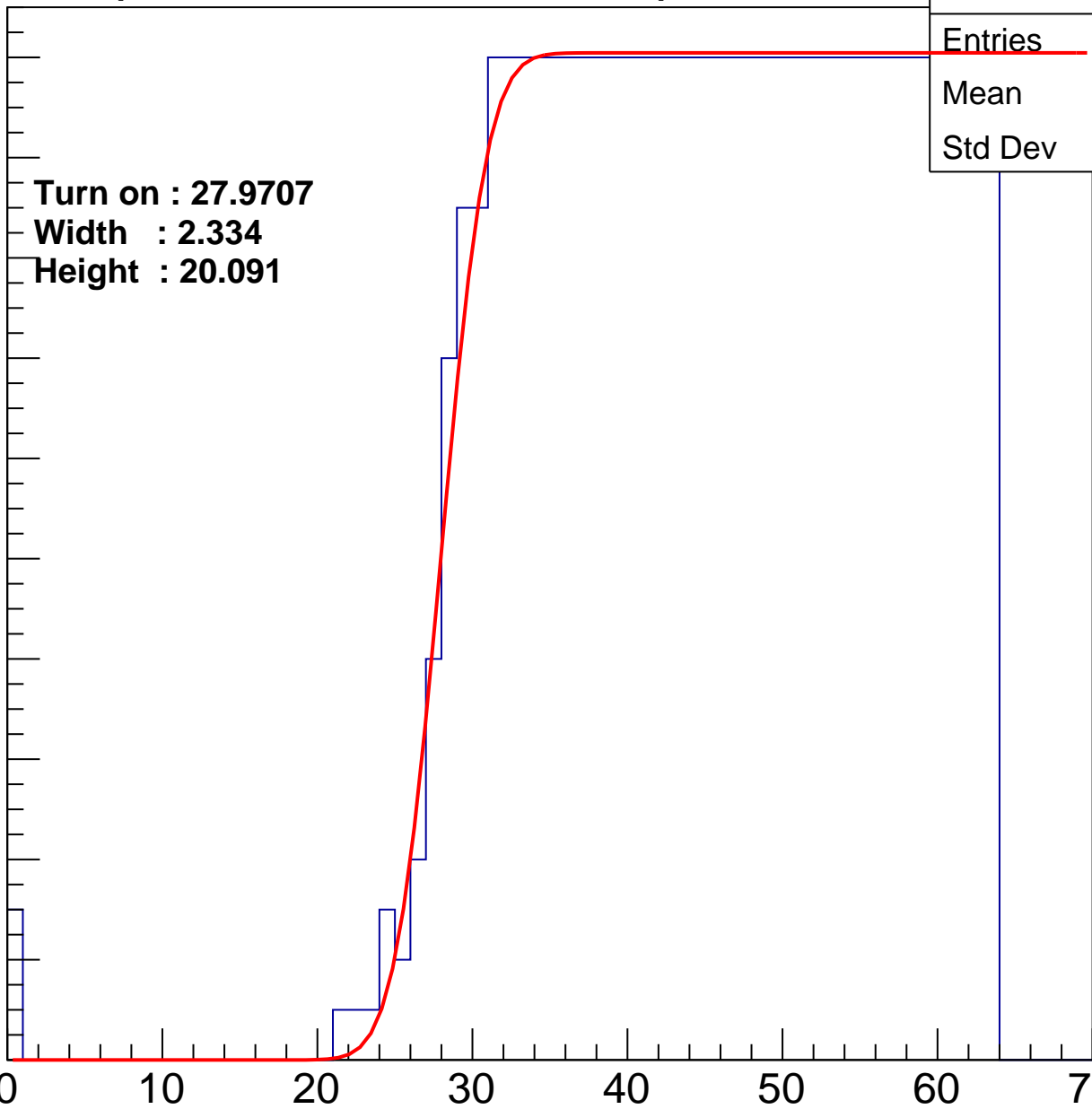
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9707
Width : 2.334
Height : 20.091

Entries	731
Mean	45.04
Std Dev	11

ampl



B1L001S, U25-ch50

calib_packv5_042523_0143.root, FC#2, port C2

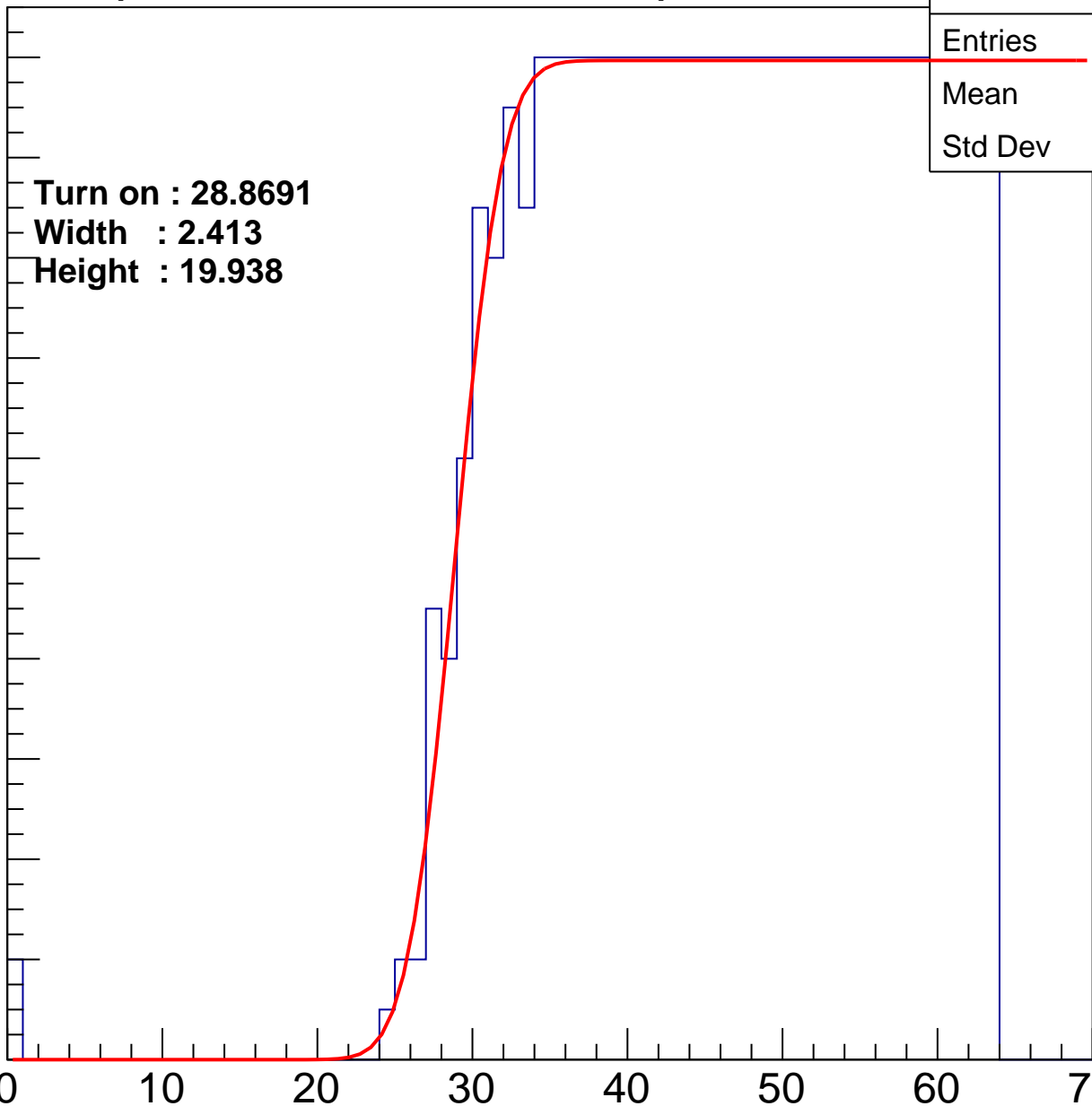
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.8691
Width : 2.413
Height : 19.938

Entries	705
Mean	45.7
Std Dev	10.58

ampl



B1L001S, U25-ch51

calib_packv5_042523_0143.root, FC#2, port C2

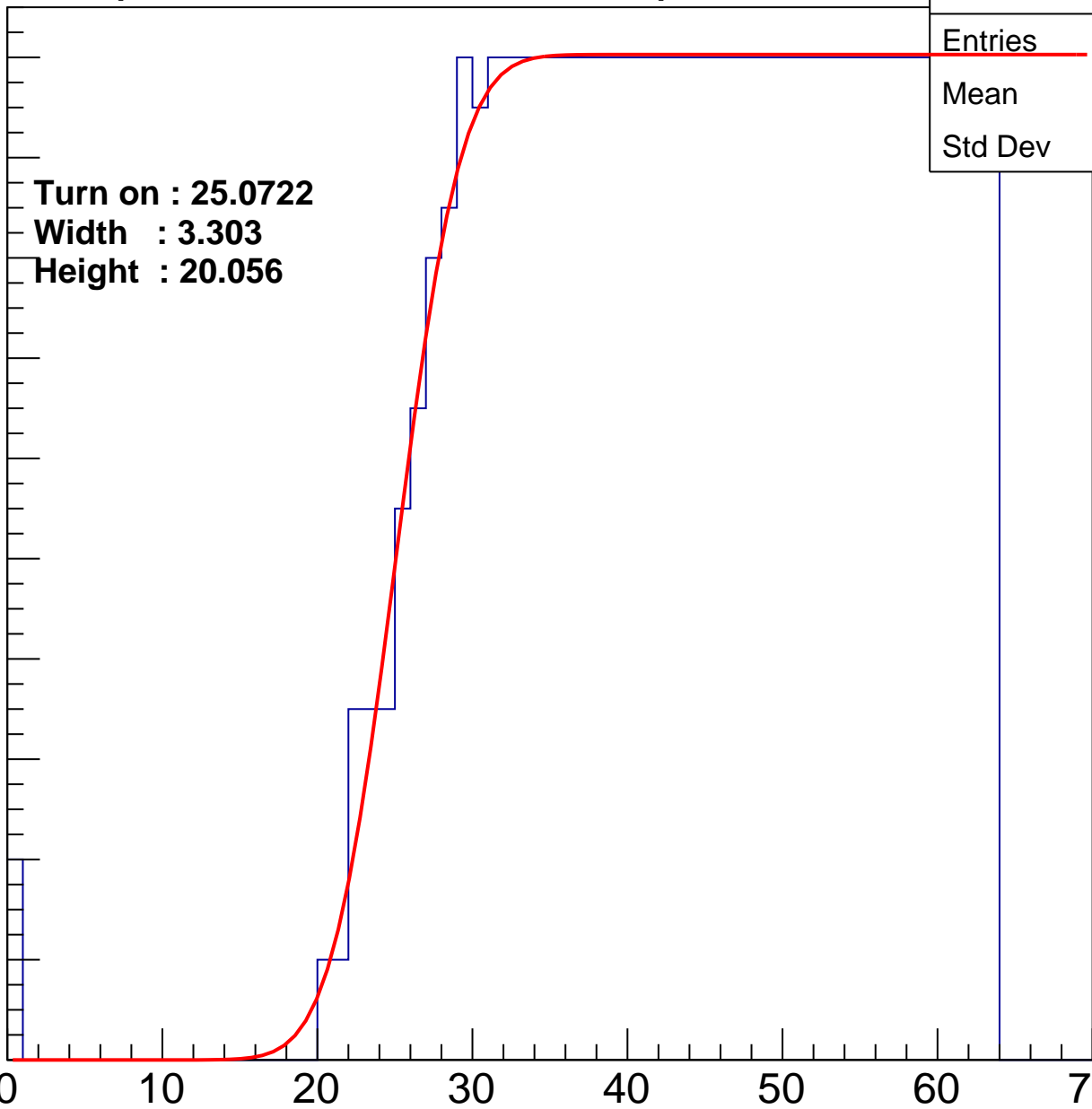
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0722
Width : 3.303
Height : 20.056

Entries	785
Mean	43.64
Std Dev	11.85

ampl



B1L001S, U25-ch52

calib_packv5_042523_0143.root, FC#2, port C2

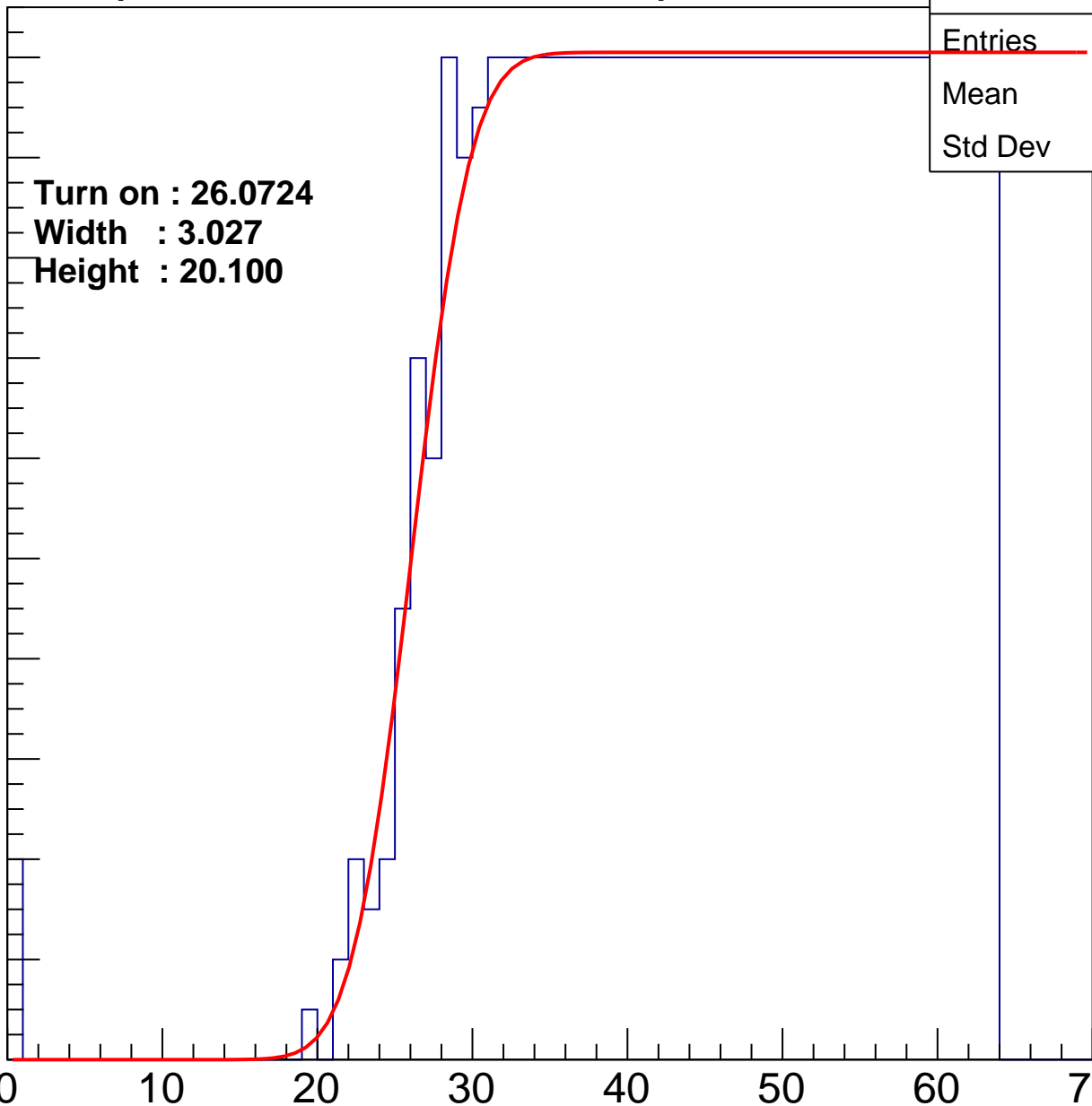
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0724
Width : 3.027
Height : 20.100

Entries	770
Mean	44.02
Std Dev	11.63

ampl



B1L001S, U25-ch53

calib_packv5_042523_0143.root, FC#2, port C2

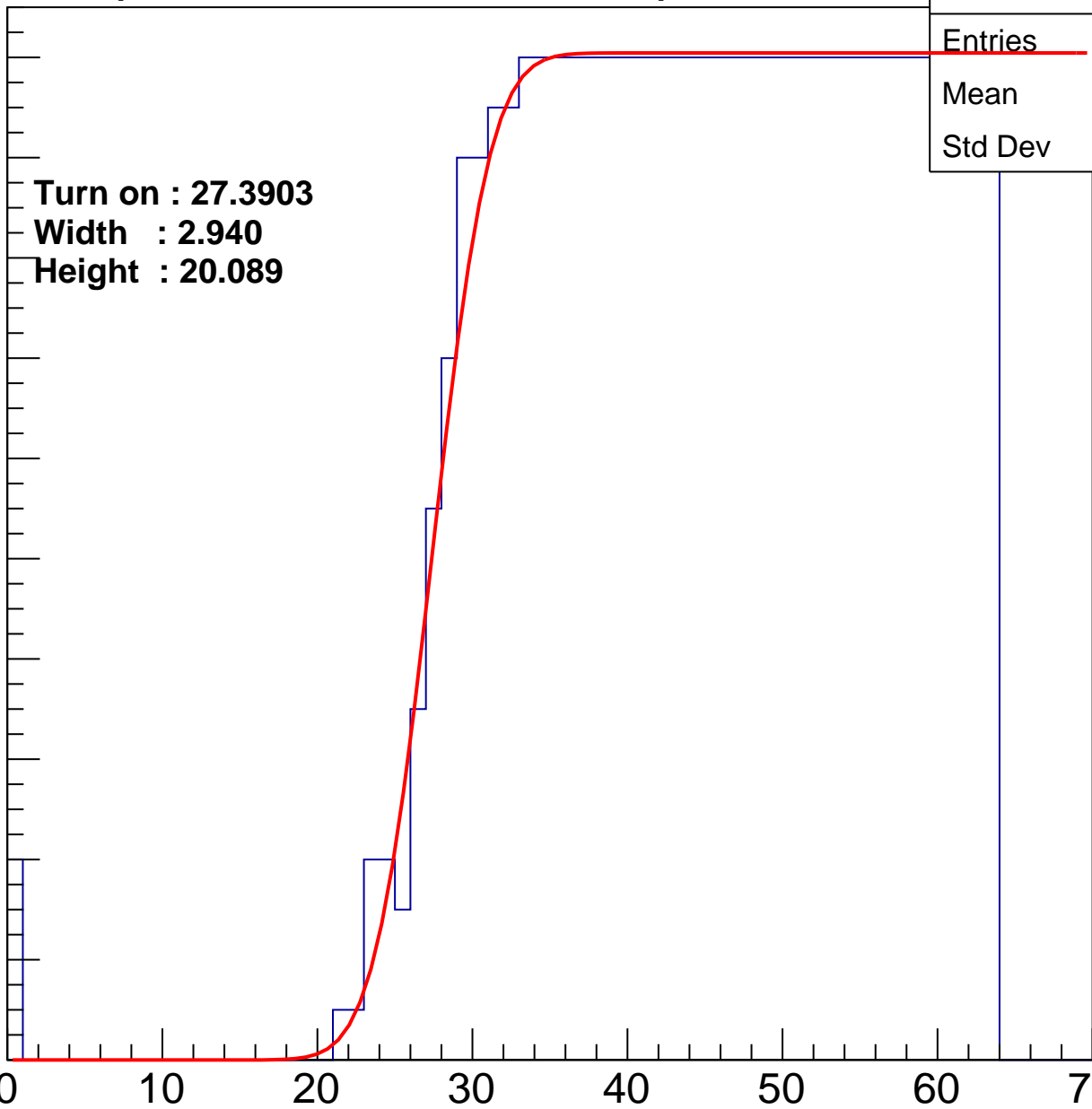
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3903
Width : 2.940
Height : 20.089

Entries	743
Mean	44.68
Std Dev	11.3

ampl



B1L001S, U25-ch54

calib_packv5_042523_0143.root, FC#2, port C2

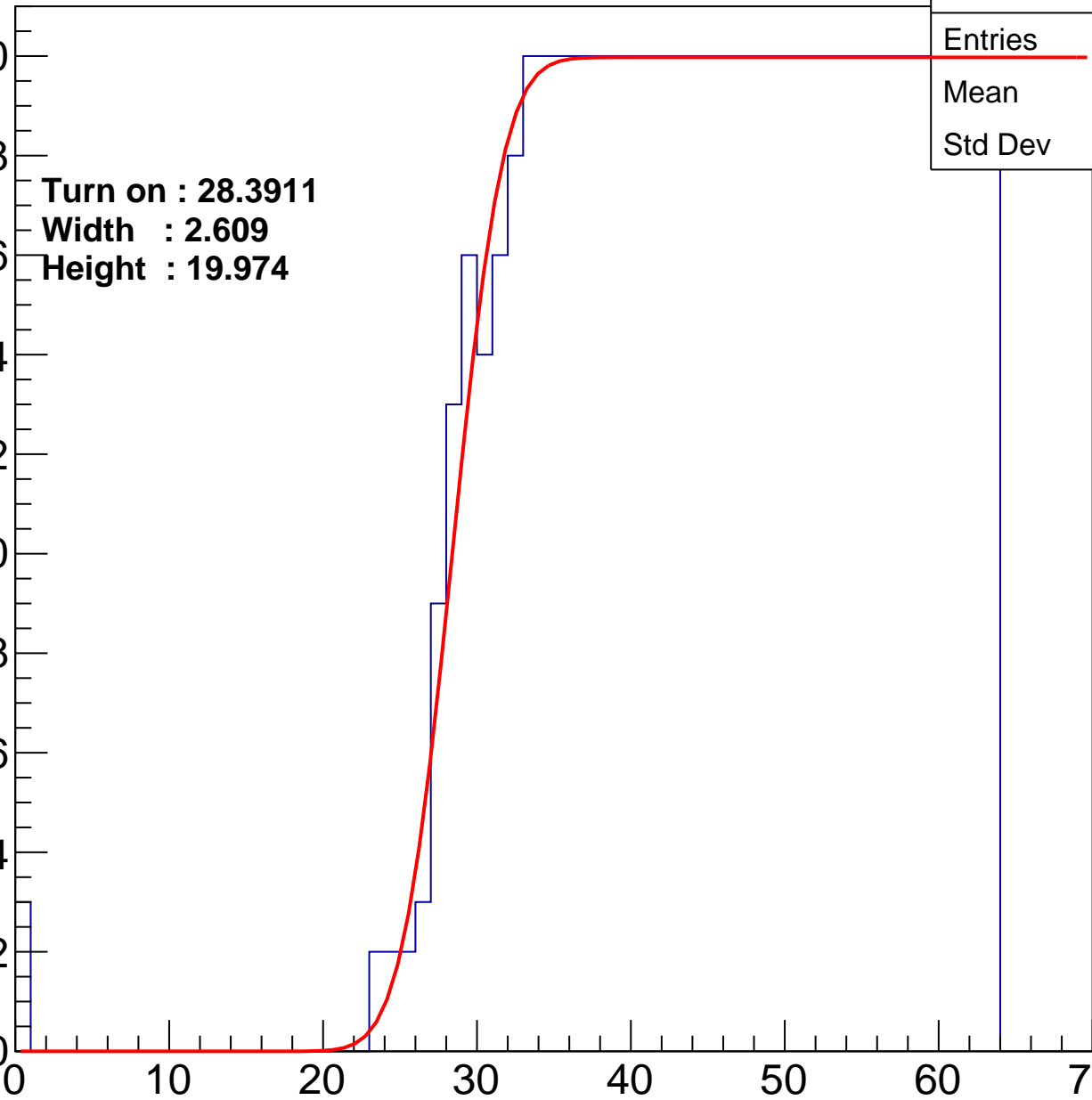
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3911
Width : 2.609
Height : 19.974

Entries	718
Mean	45.33
Std Dev	10.88

ampl



B1L001S, U25-ch55

calib_packv5_042523_0143.root, FC#2, port C2

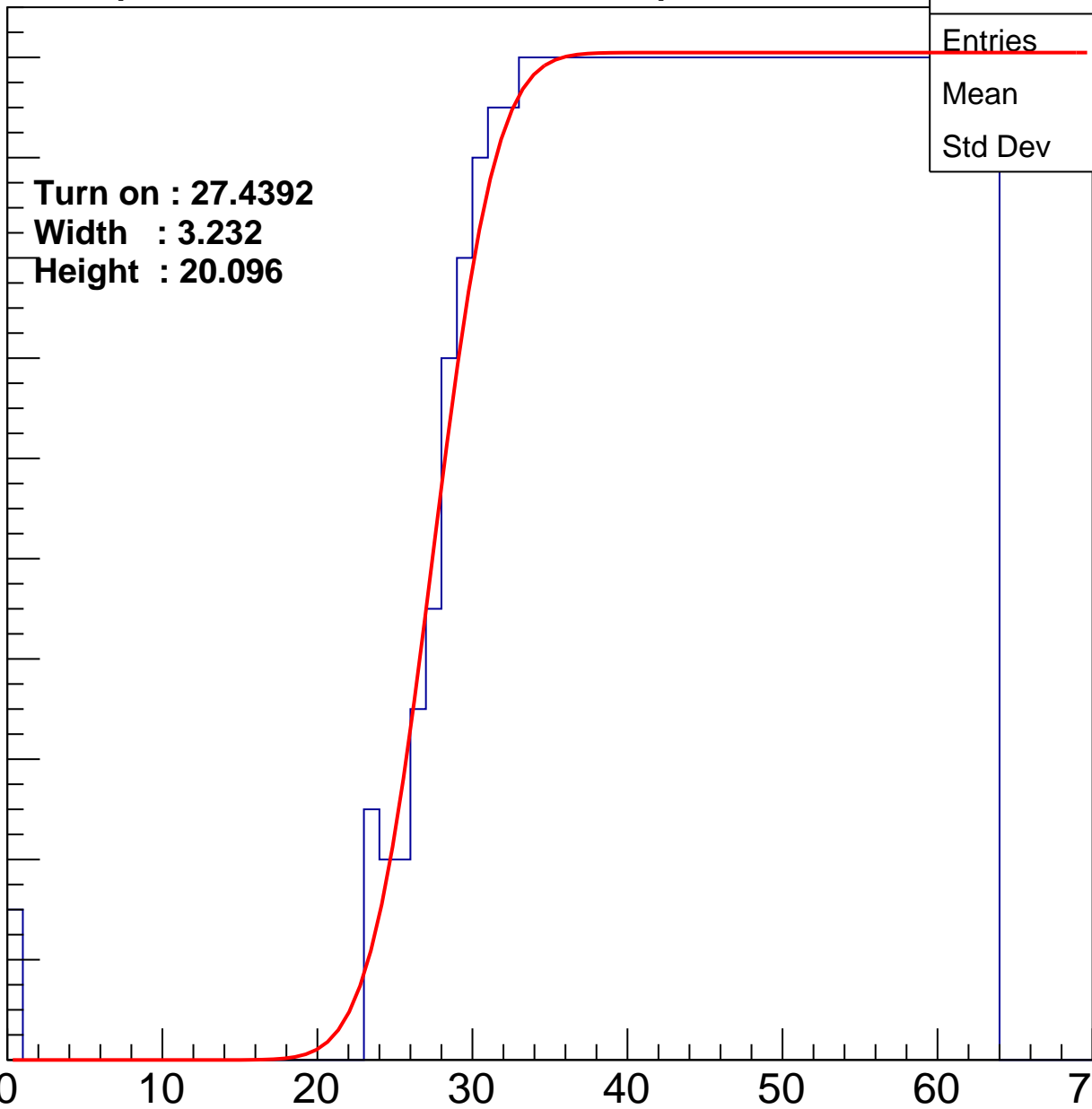
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4392
Width : 3.232
Height : 20.096

Entries	738
Mean	44.84
Std Dev	11.14

ampl



B1L001S, U25-ch56

calib_packv5_042523_0143.root, FC#2, port C2

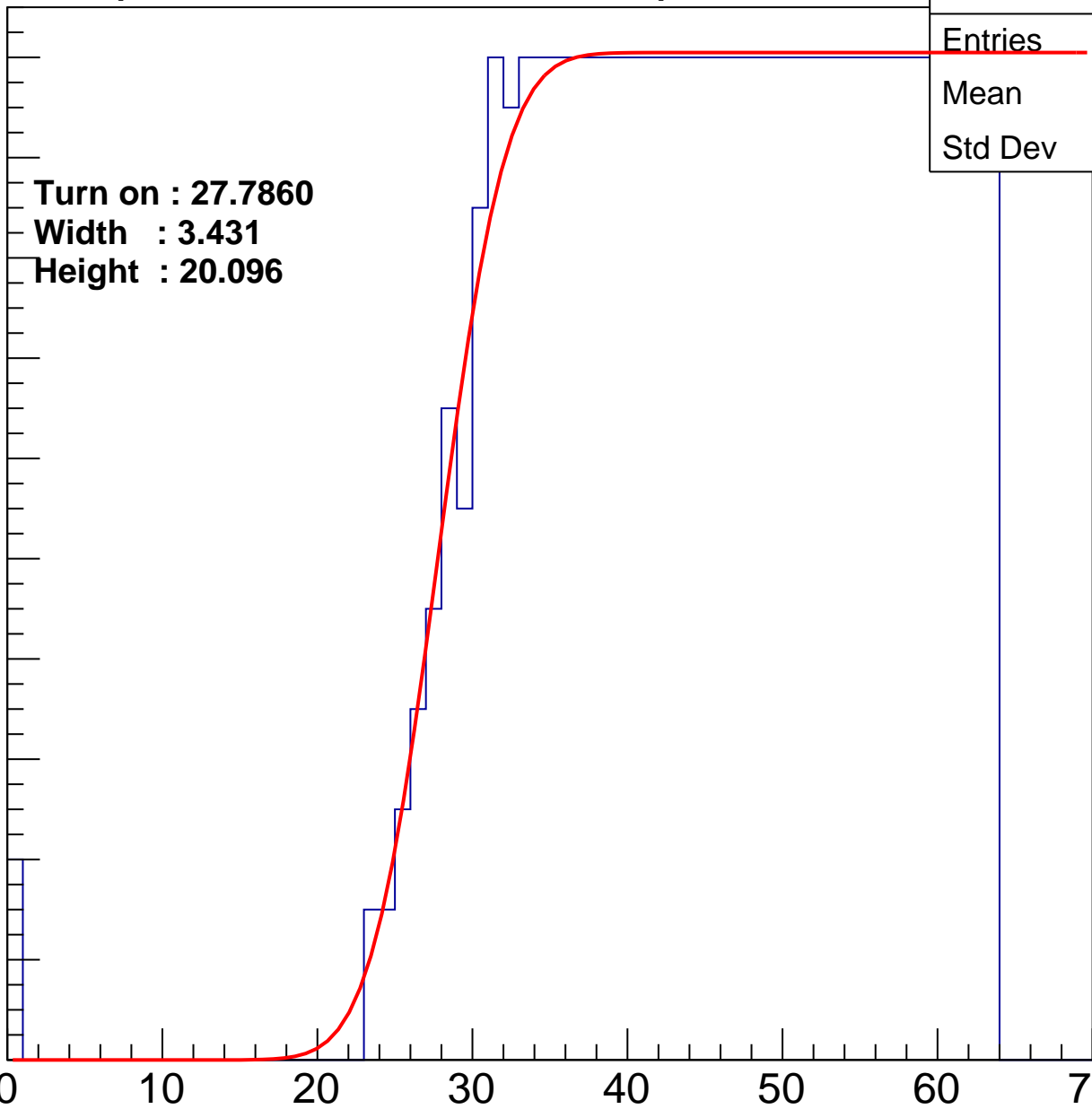
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7860
Width : 3.431
Height : 20.096

Entries	731
Mean	44.97
Std Dev	11.16

ampl



B1L001S, U25-ch57

calib_packv5_042523_0143.root, FC#2, port C2

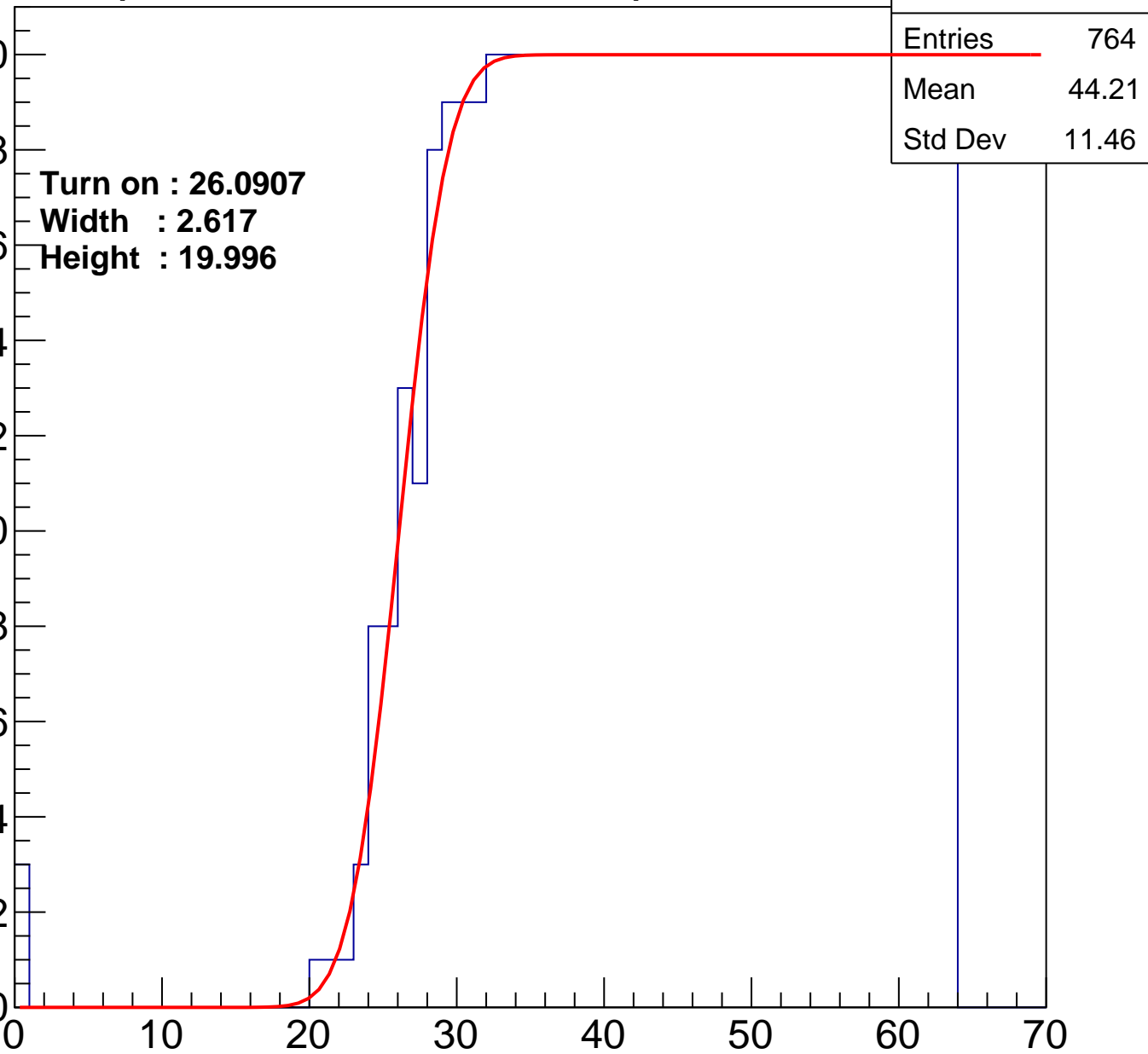
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0907
Width : 2.617
Height : 19.996

Entries	764
Mean	44.21
Std Dev	11.46

ampl



B1L001S, U25-ch58

calib_packv5_042523_0143.root, FC#2, port C2

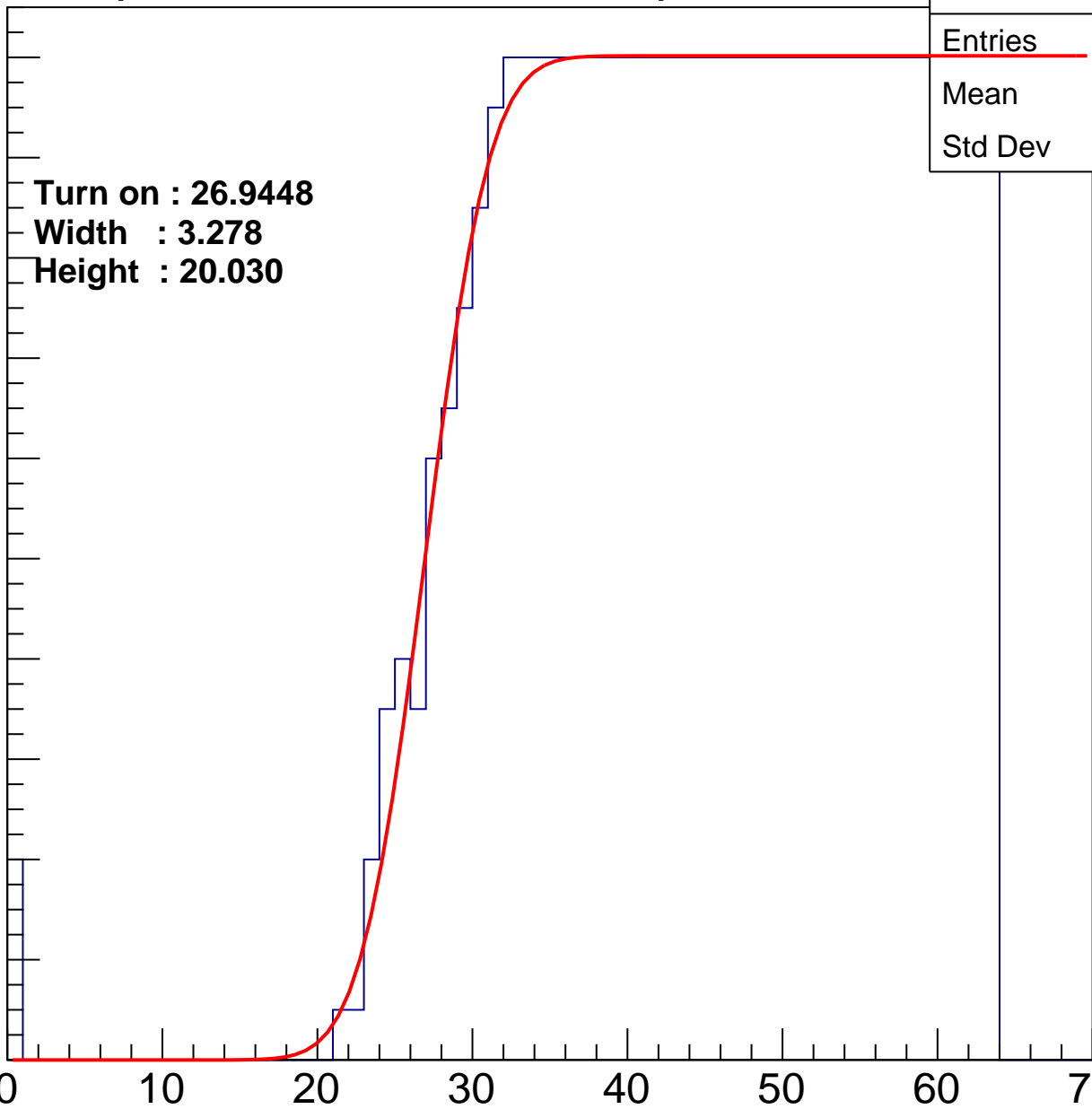
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9448
Width : 3.278
Height : 20.030

Entries	748
Mean	44.53
Std Dev	11.41

ampl



B1L001S, U25-ch59

calib_packv5_042523_0143.root, FC#2, port C2

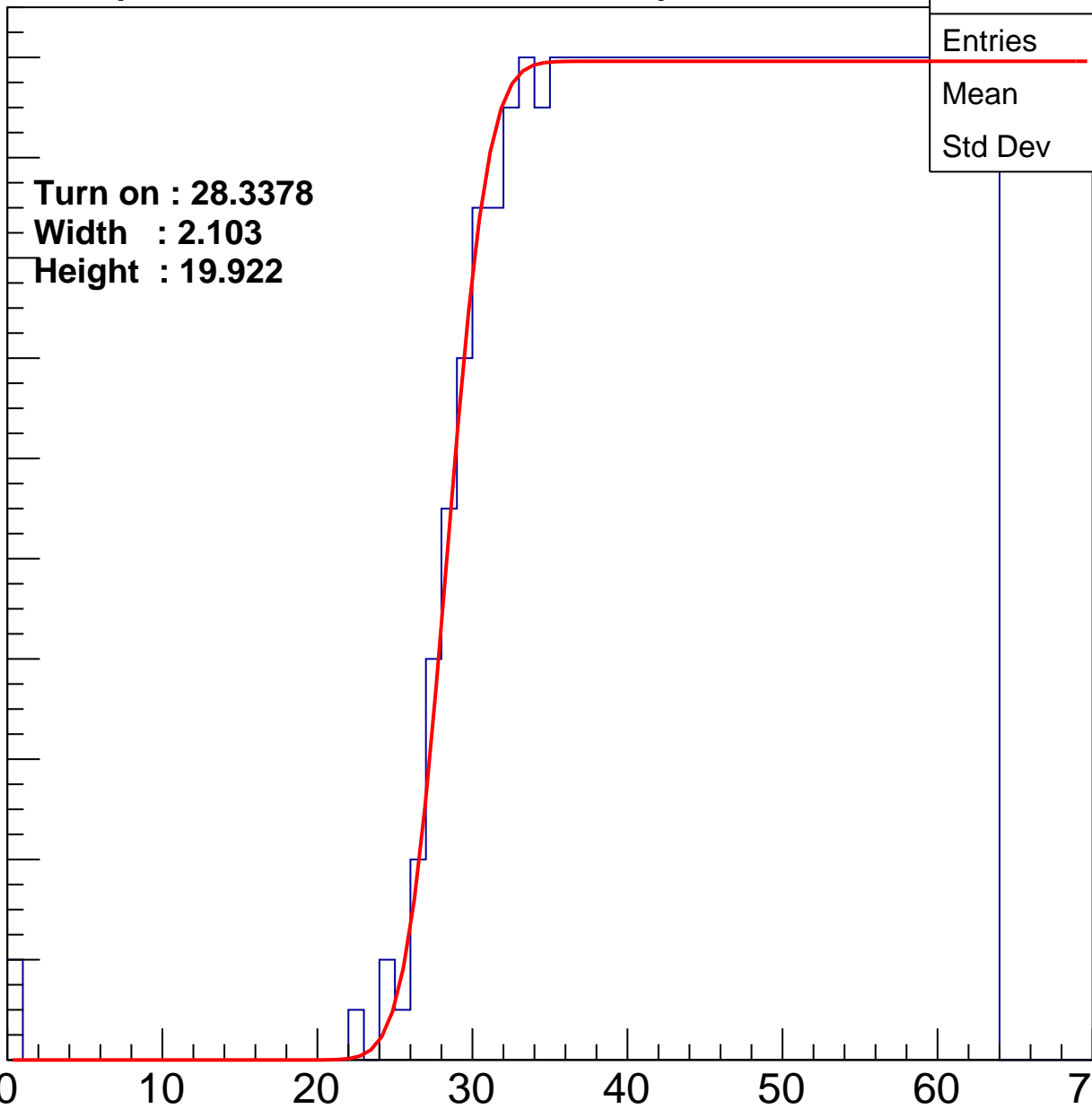
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3378
Width : 2.103
Height : 19.922

Entries	715
Mean	45.45
Std Dev	10.71

ampl



B1L001S, U25-ch60

calib_packv5_042523_0143.root, FC#2, port C2

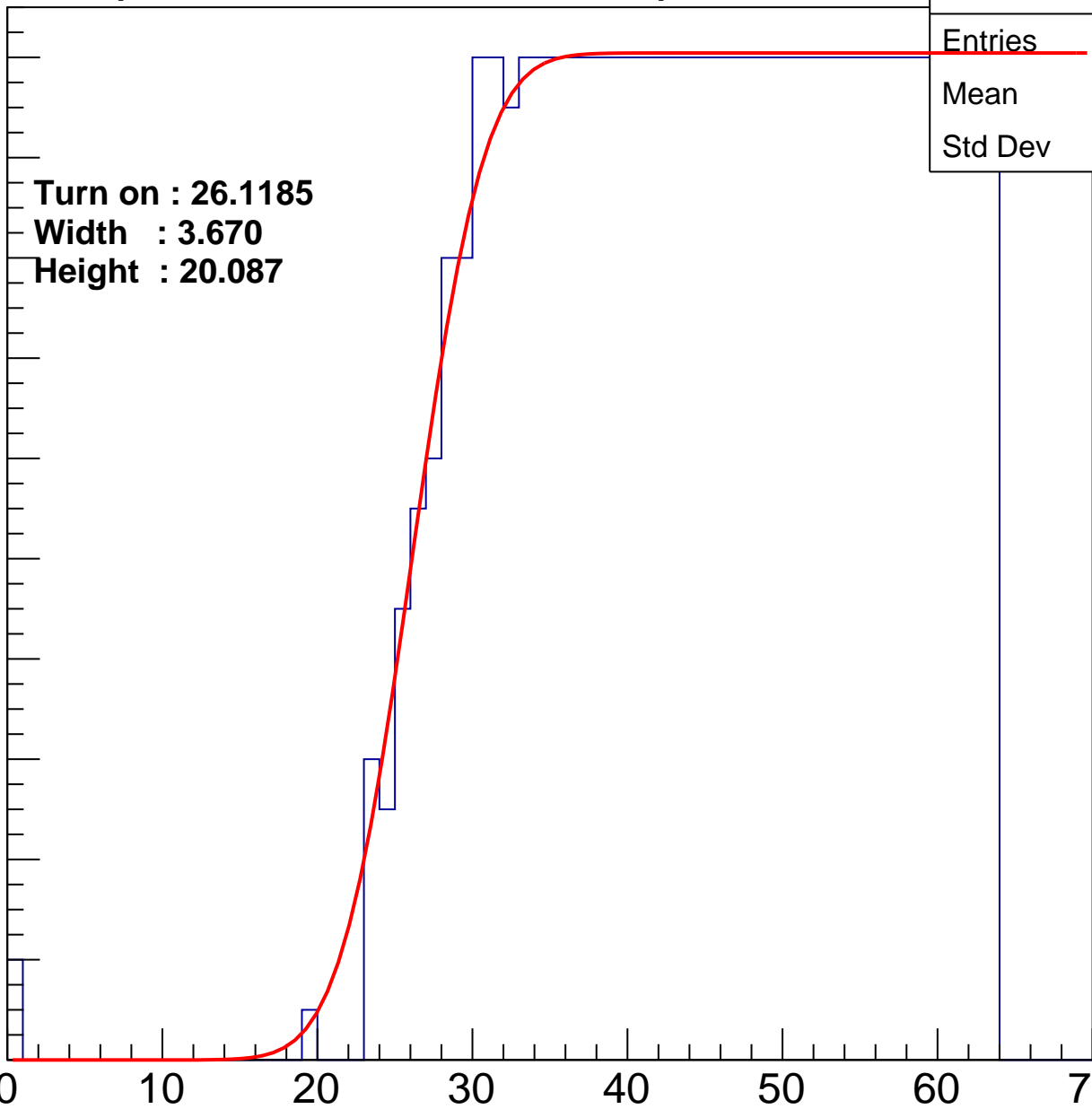
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1185
Width : 3.670
Height : 20.087

Entries	757
Mean	44.4
Std Dev	11.29

ampl



B1L001S, U25-ch61

calib_packv5_042523_0143.root, FC#2, port C2

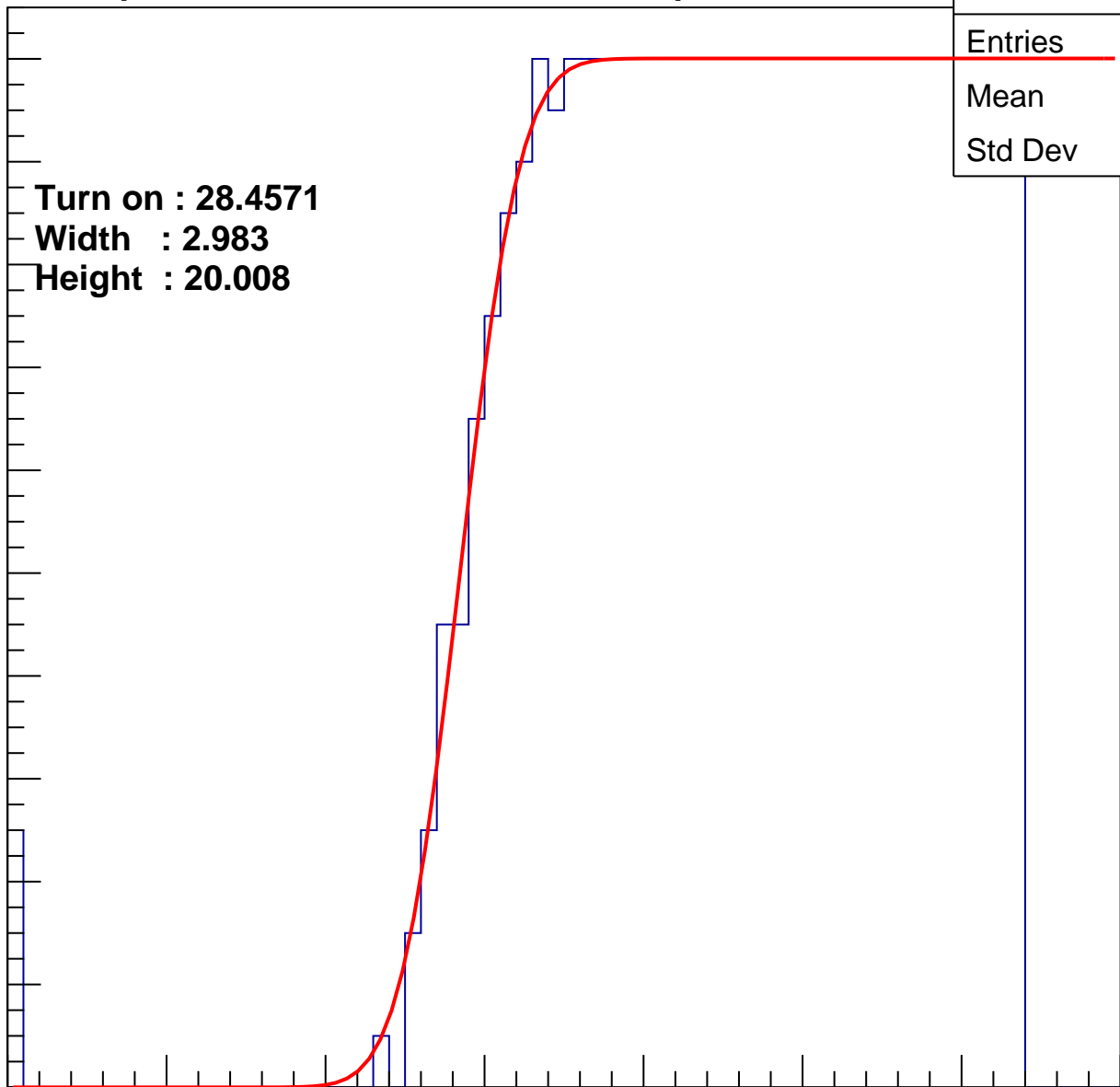
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4571
Width : 2.983
Height : 20.008

Entries	714
Mean	45.35
Std Dev	11.05

ampl



B1L001S, U25-ch62

calib_packv5_042523_0143.root, FC#2, port C2

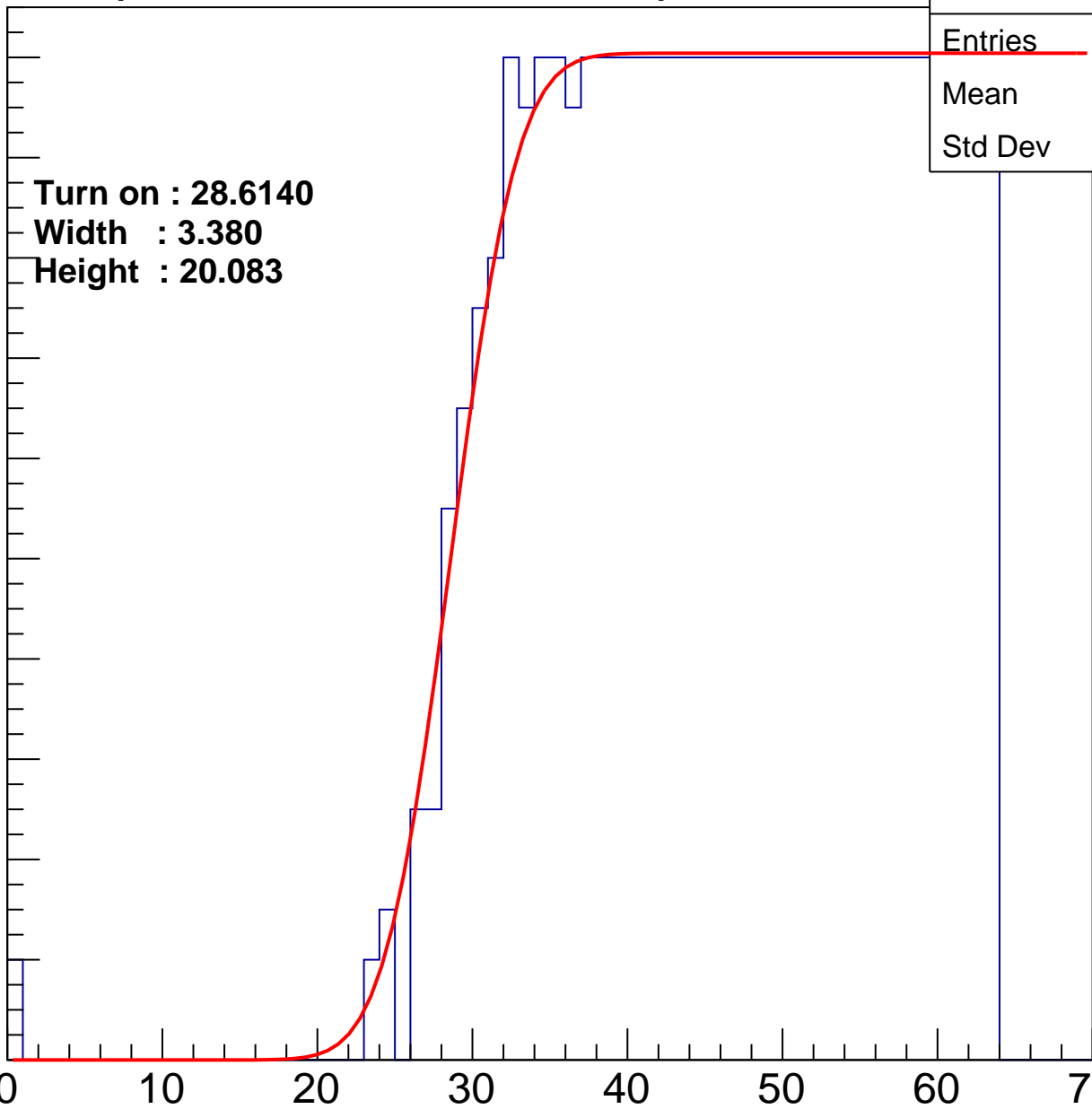
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6140
Width : 3.380
Height : 20.083

Entries	710
Mean	45.56
Std Dev	10.67

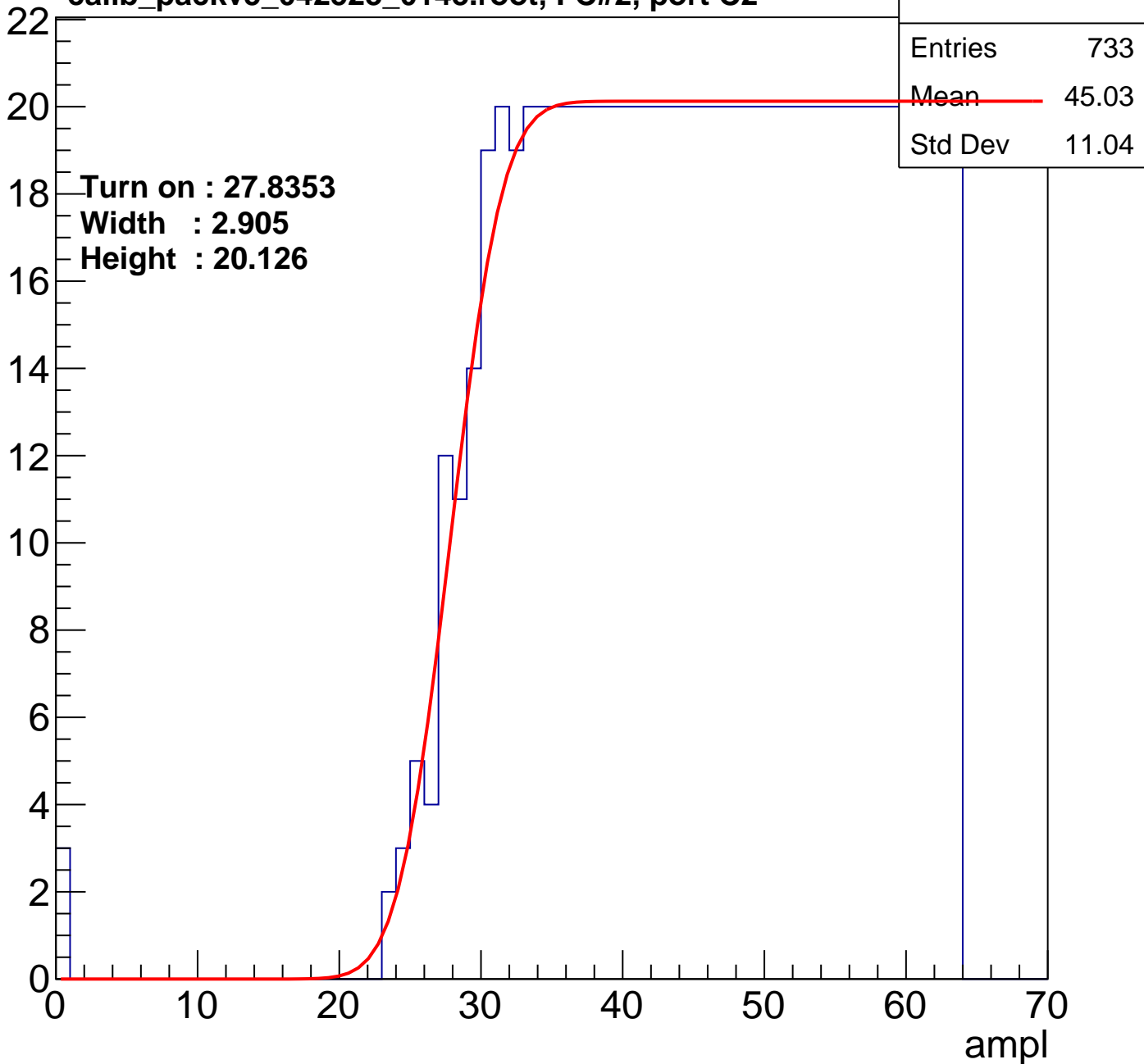
ampl



B1L001S, U25-ch63

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch64

calib_packv5_042523_0143.root, FC#2, port C2

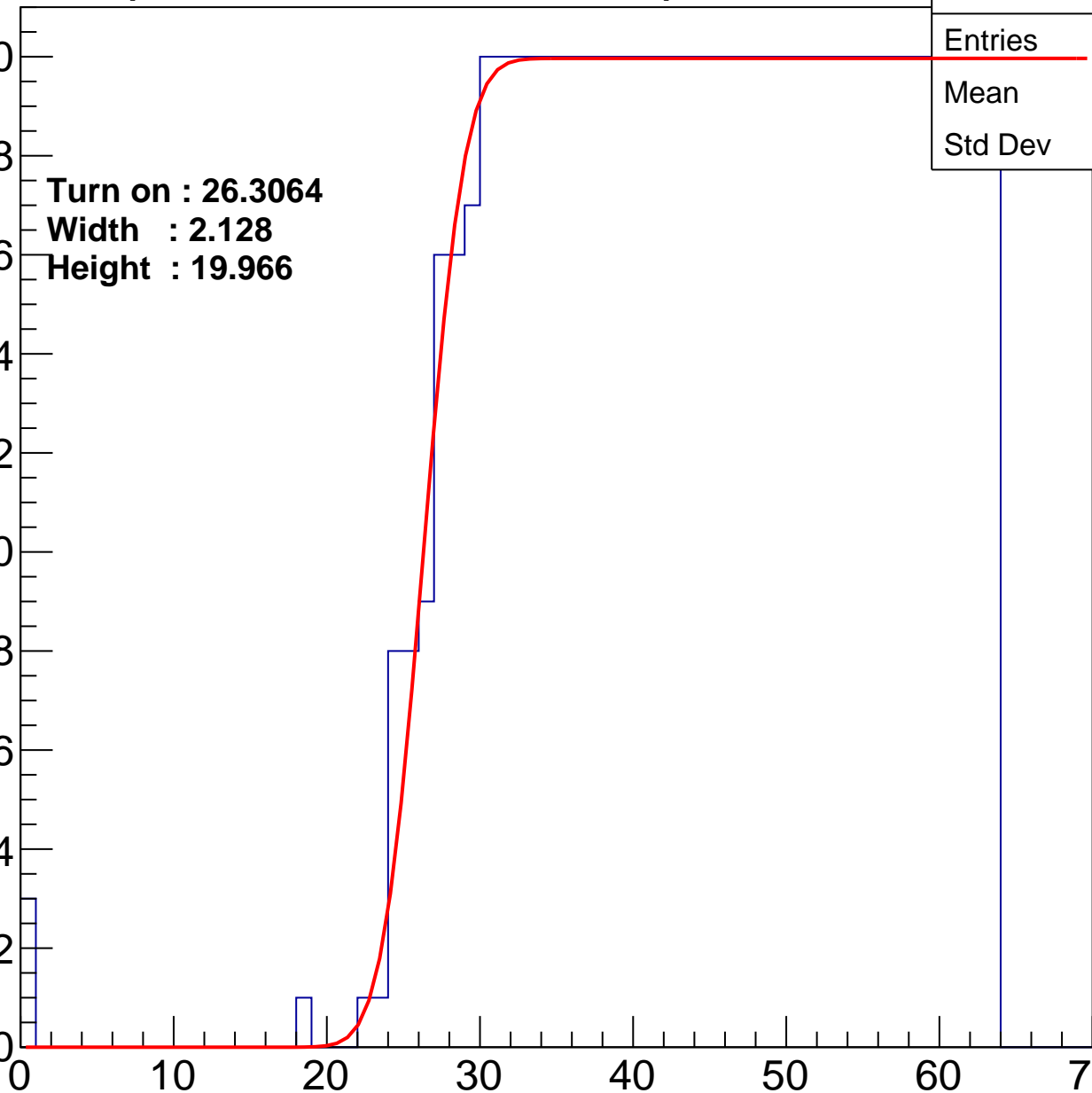
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3064
Width : 2.128
Height : 19.966

Entries	760
Mean	44.32
Std Dev	11.39

ampl



B1L001S, U25-ch65

calib_packv5_042523_0143.root, FC#2, port C2

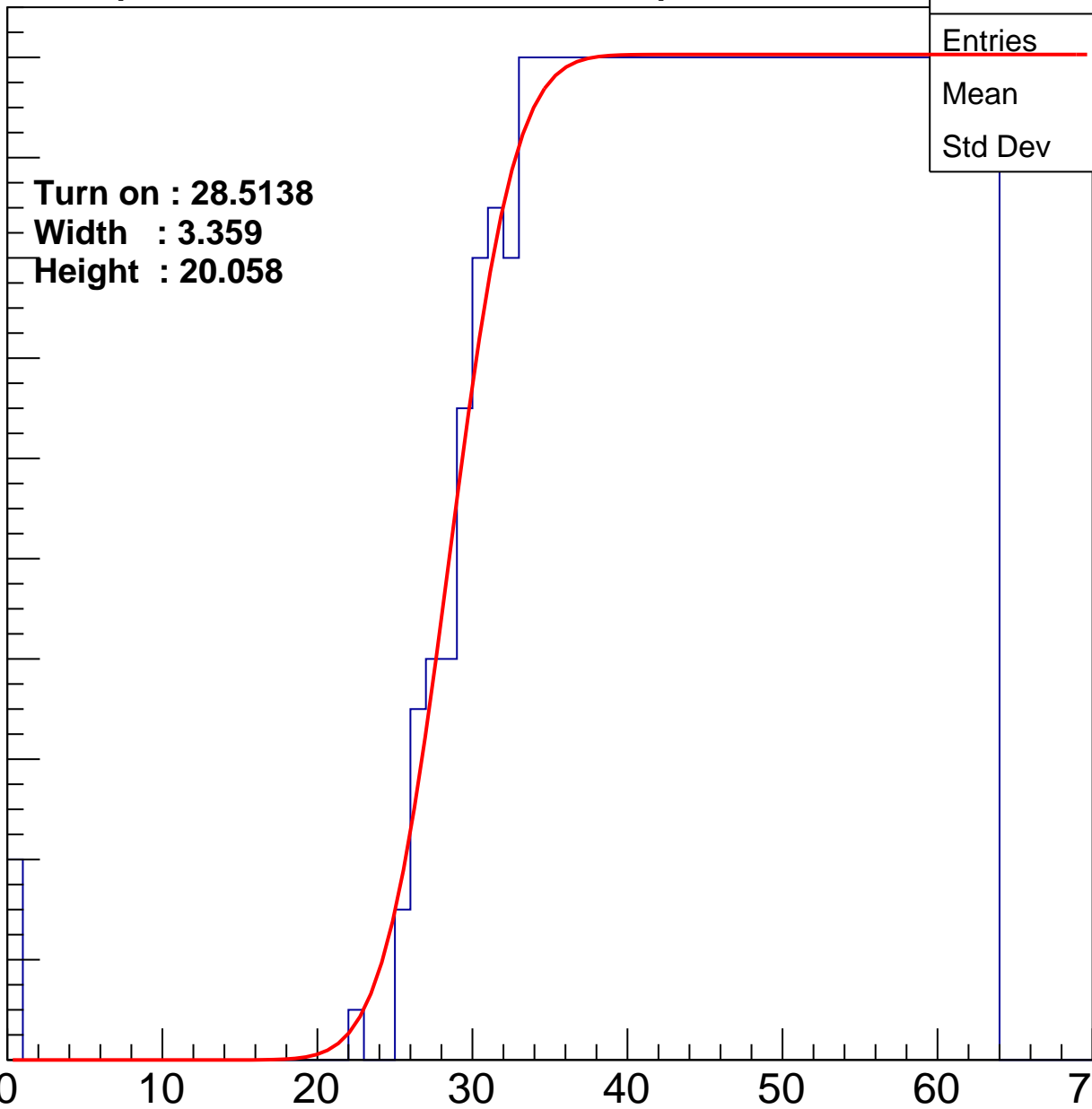
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5138
Width : 3.359
Height : 20.058

Entries	713
Mean	45.41
Std Dev	10.94

ampl



B1L001S, U25-ch66

calib_packv5_042523_0143.root, FC#2, port C2

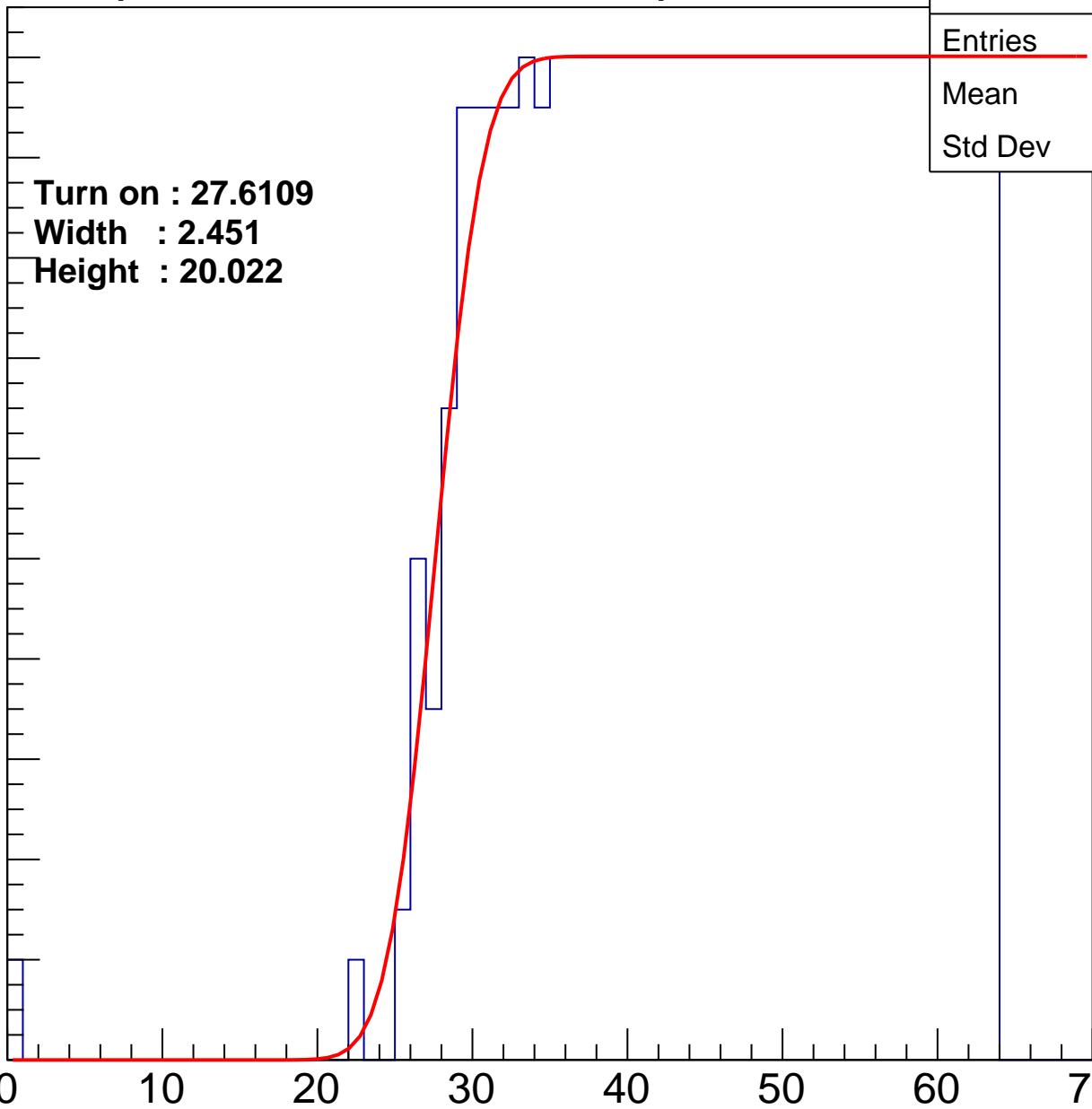
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6109
Width : 2.451
Height : 20.022

Entries	732
Mean	45.05
Std Dev	10.91

ampl



B1L001S, U25-ch67

calib_packv5_042523_0143.root, FC#2, port C2

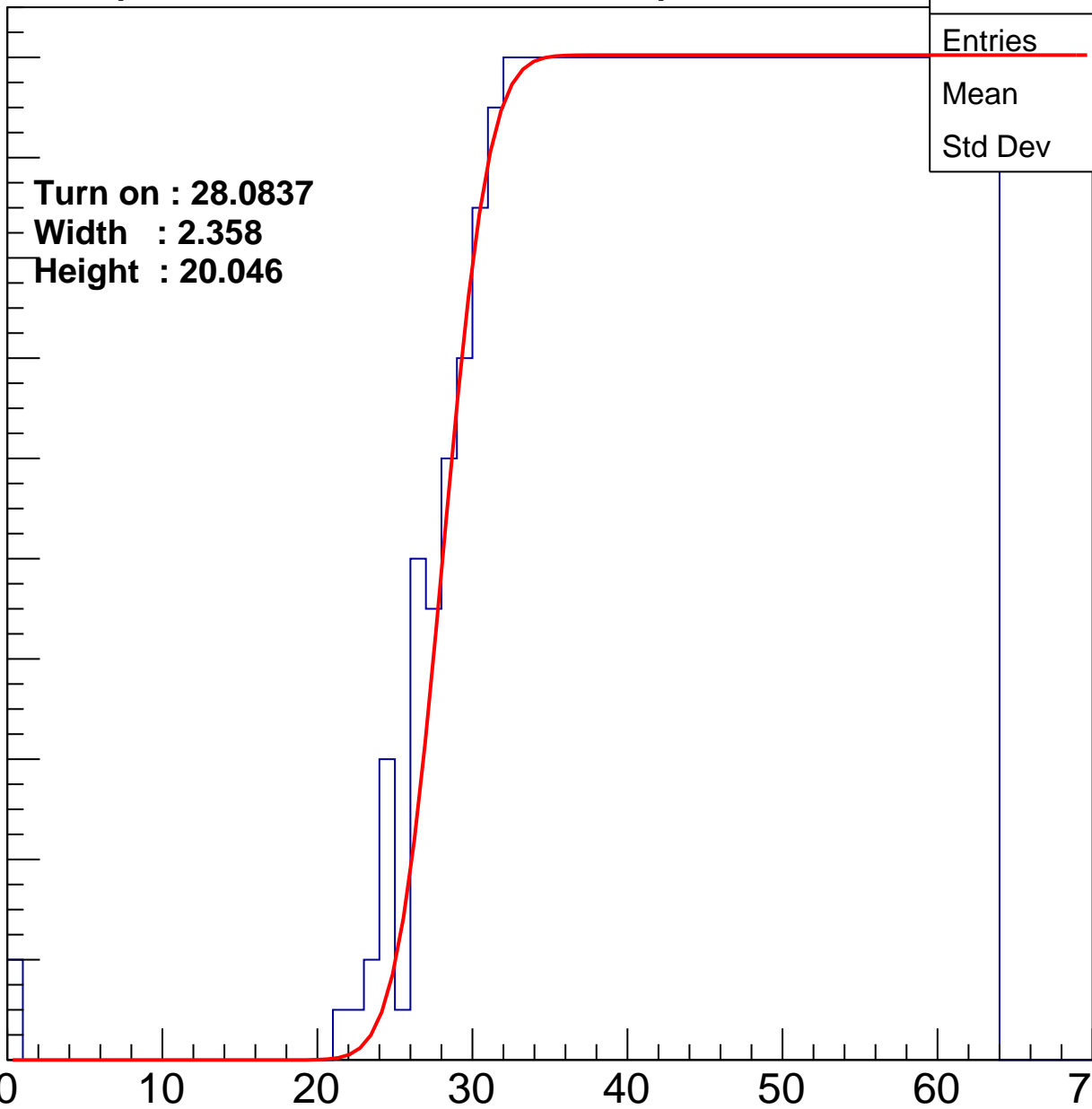
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0837
Width : 2.358
Height : 20.046

Entries	734
Mean	44.96
Std Dev	11

ampl



B1L001S, U25-ch68

calib_packv5_042523_0143.root, FC#2, port C2

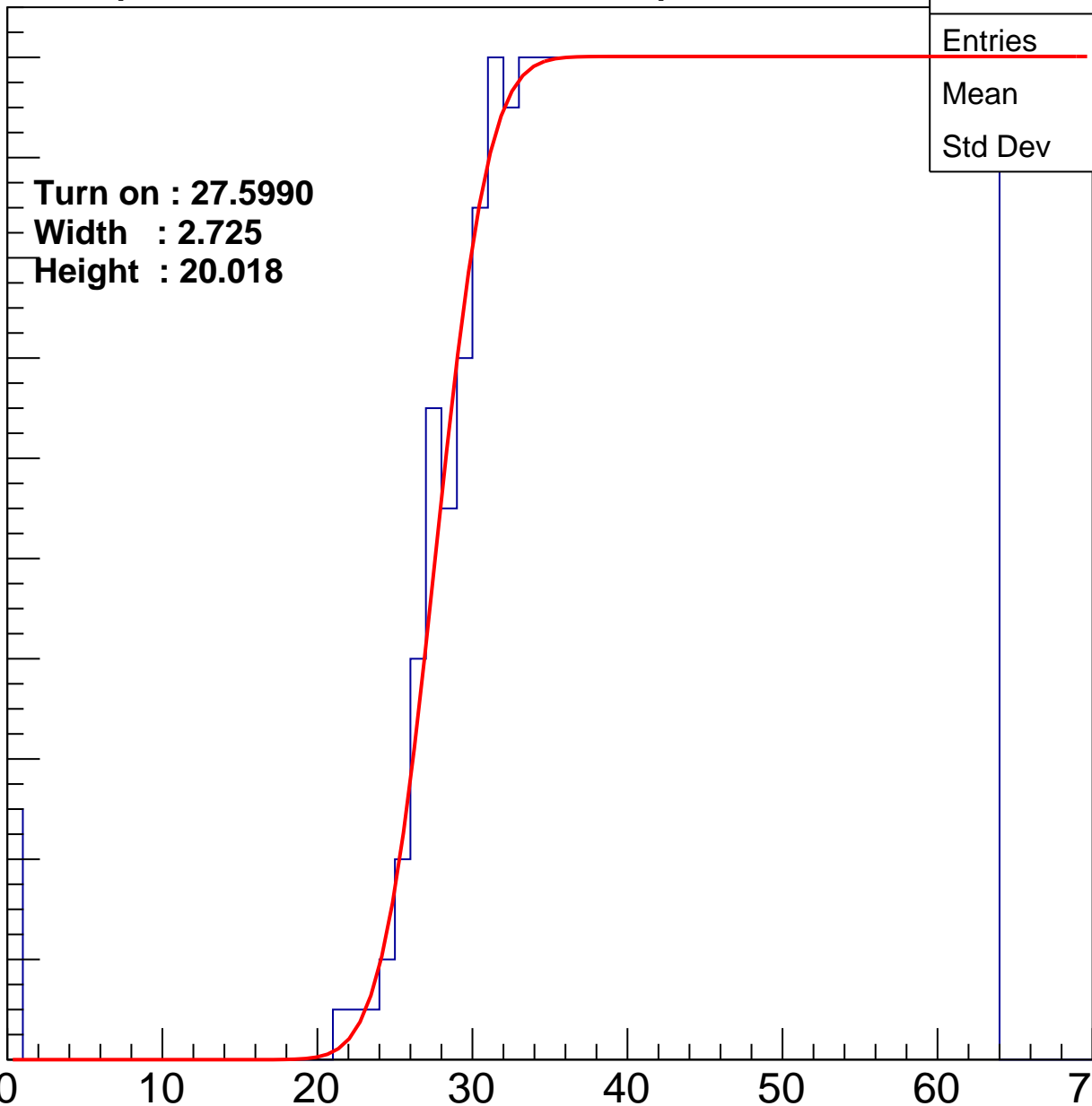
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5990
Width : 2.725
Height : 20.018

Entries	736
Mean	44.82
Std Dev	11.31

ampl



B1L001S, U25-ch69

calib_packv5_042523_0143.root, FC#2, port C2

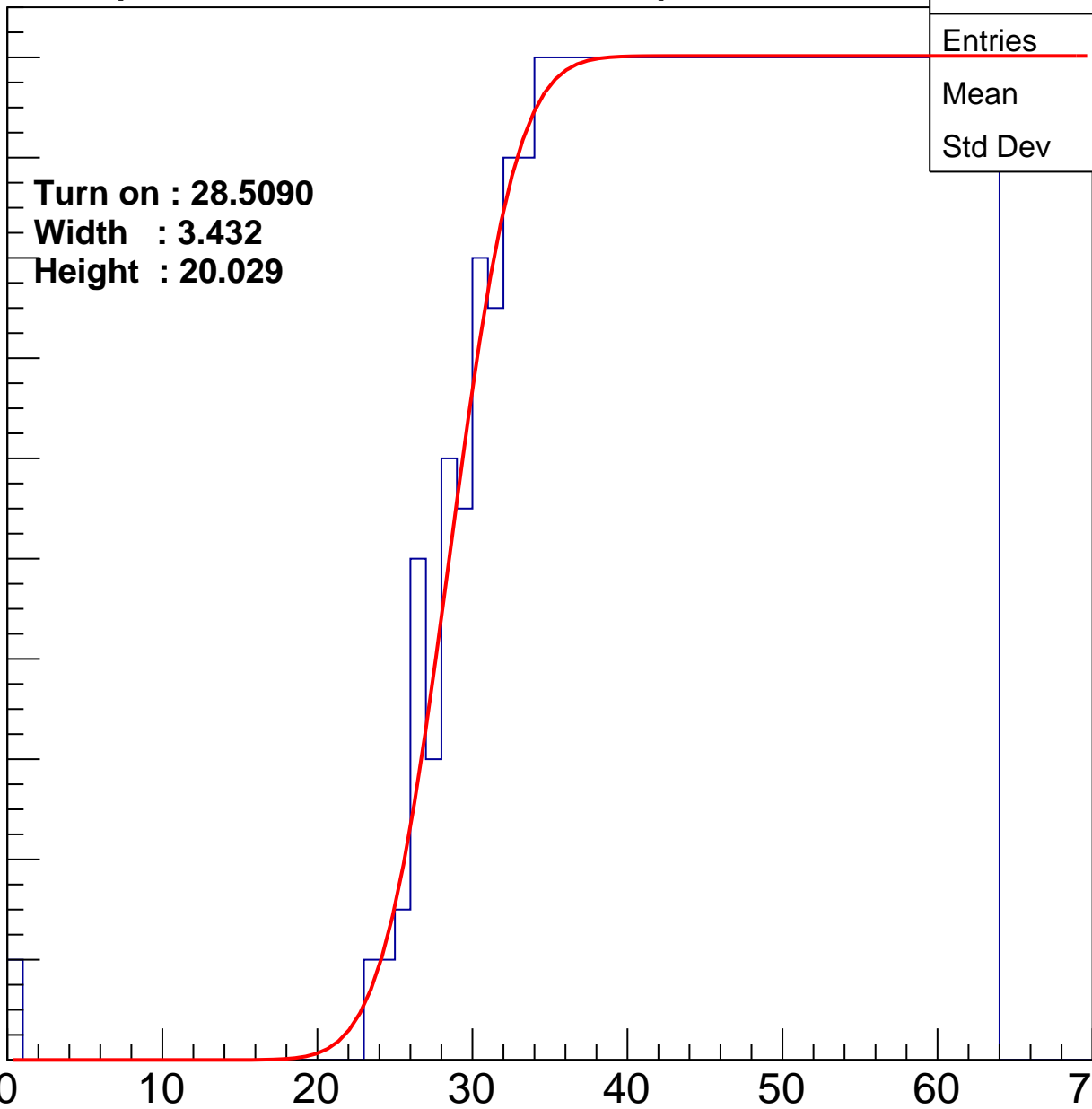
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5090
Width : 3.432
Height : 20.029

Entries	715
Mean	45.4
Std Dev	10.79

ampl



B1L001S, U25-ch70

calib_packv5_042523_0143.root, FC#2, port C2

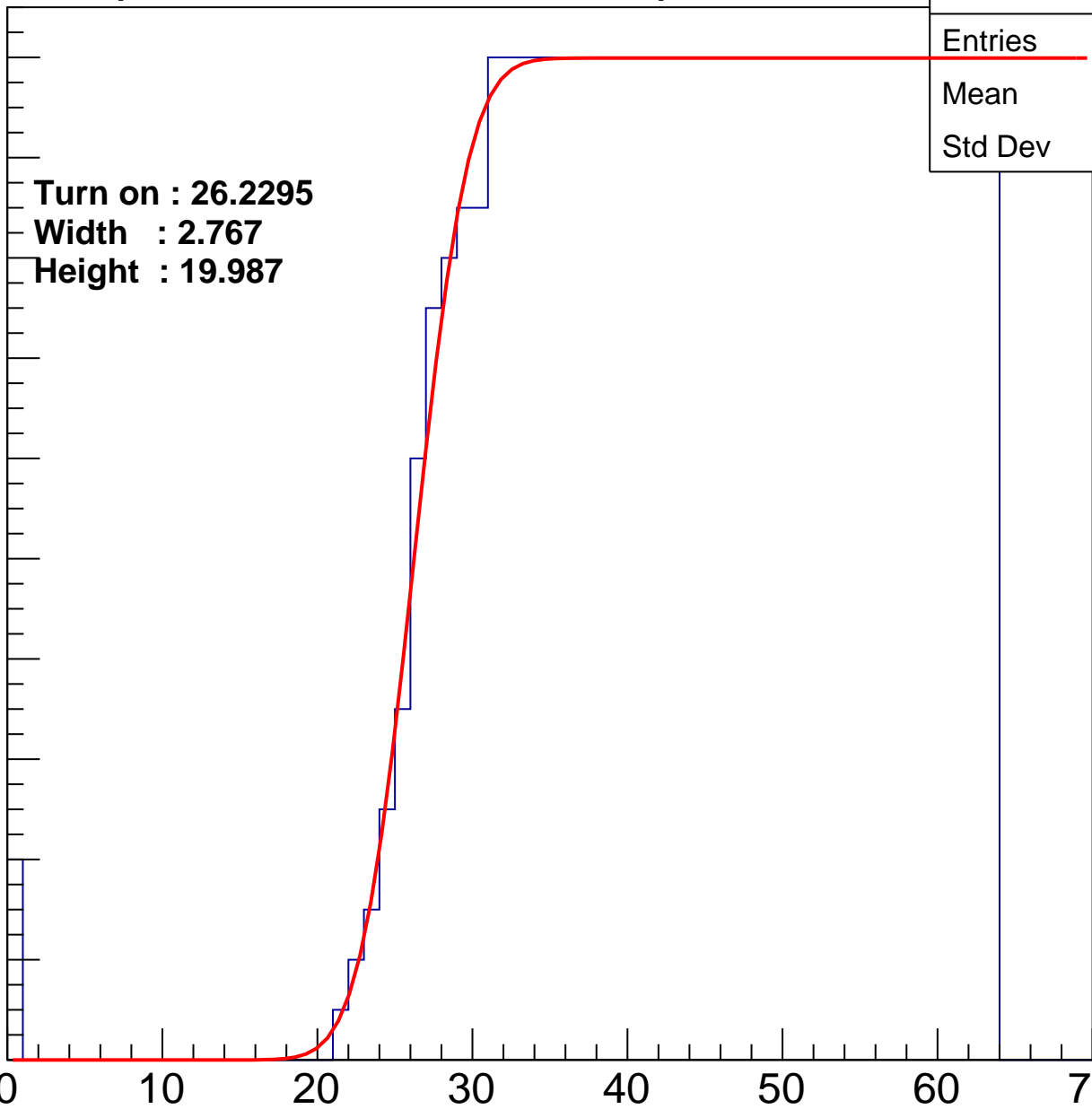
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2295
Width : 2.767
Height : 19.987

Entries	759
Mean	44.29
Std Dev	11.49

ampl



B1L001S, U25-ch71

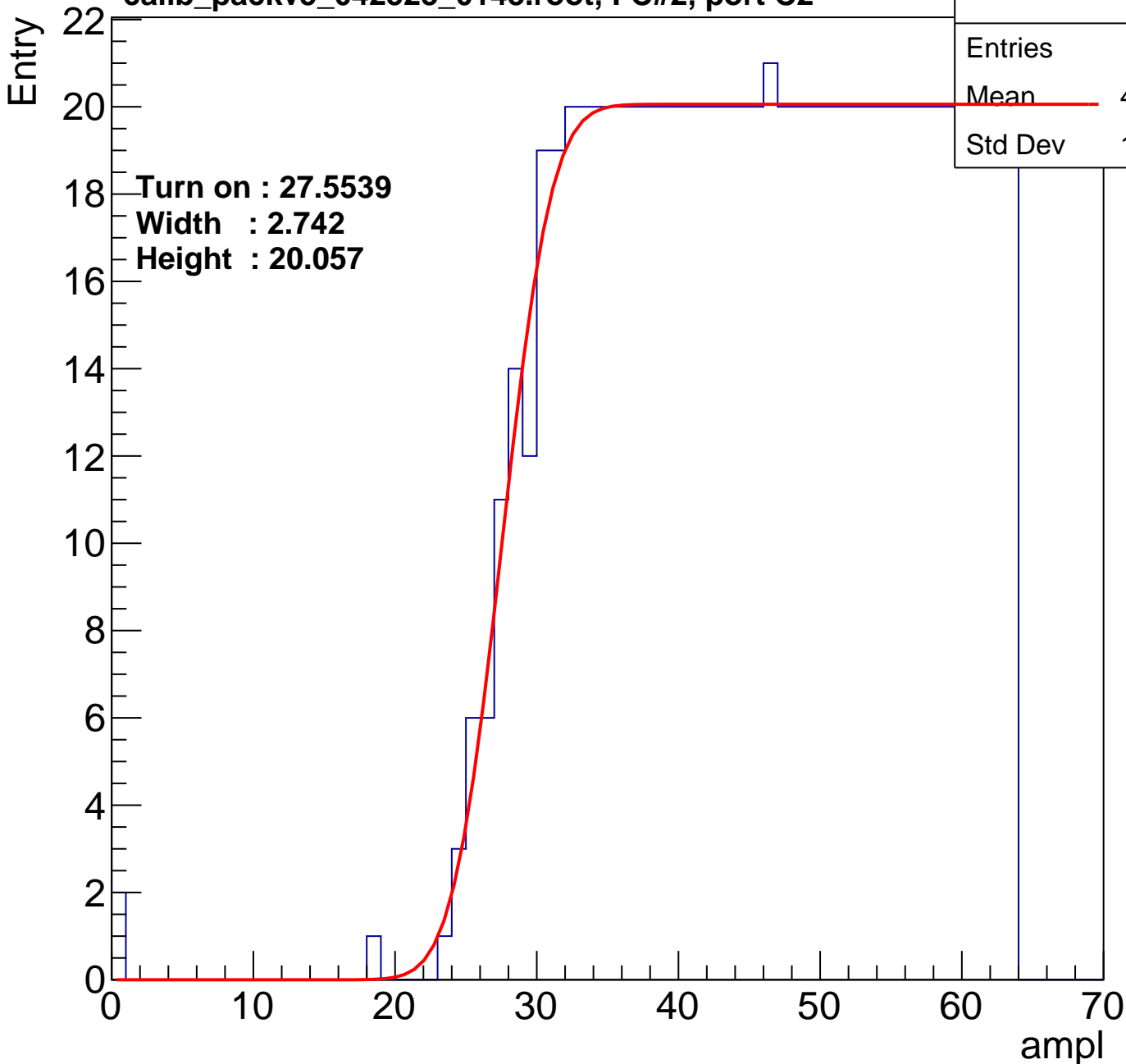
calib_packv5_042523_0143.root, FC#2, port C2

Entries	735
Mean	44.98
Std Dev	10.97

Turn on : 27.5539

Width : 2.742

Height : 20.057



B1L001S, U25-ch72

calib_packv5_042523_0143.root, FC#2, port C2

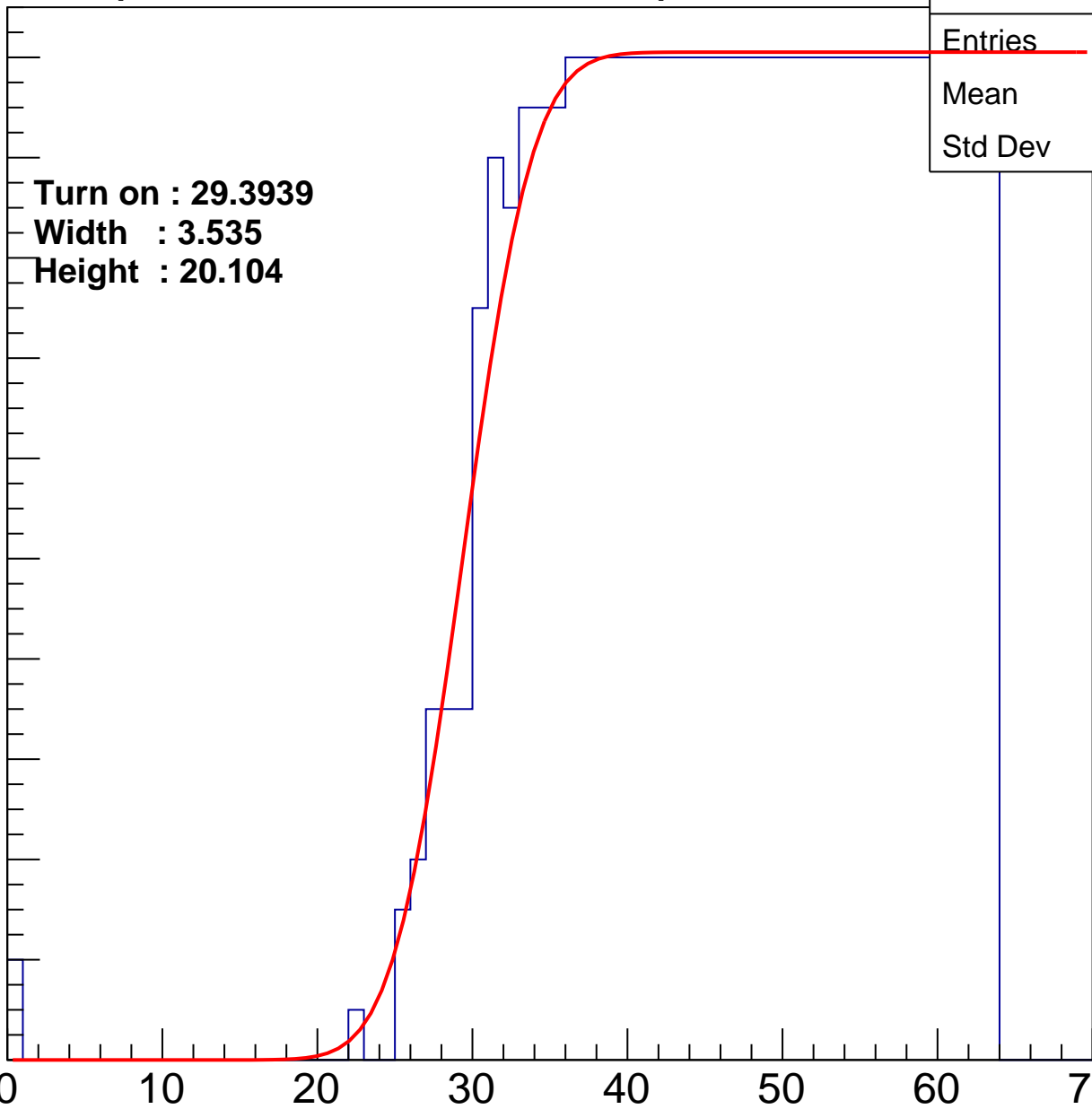
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.3939
Width : 3.535
Height : 20.104

Entries	698
Mean	45.84
Std Dev	10.53

ampl



B1L001S, U25-ch73

calib_packv5_042523_0143.root, FC#2, port C2

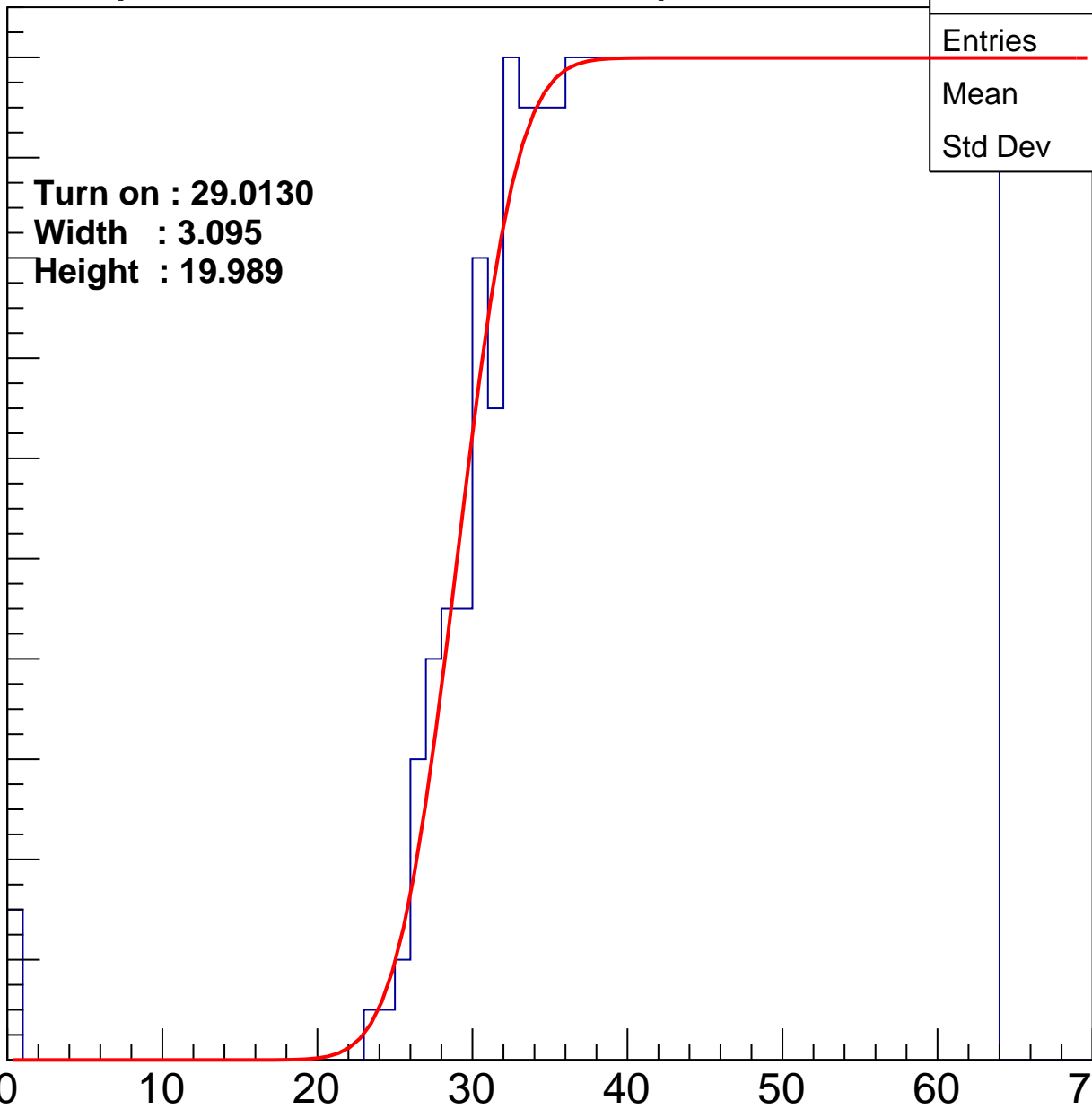
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.0130
Width : 3.095
Height : 19.989

Entries	705
Mean	45.62
Std Dev	10.75

ampl



B1L001S, U25-ch74

calib_packv5_042523_0143.root, FC#2, port C2

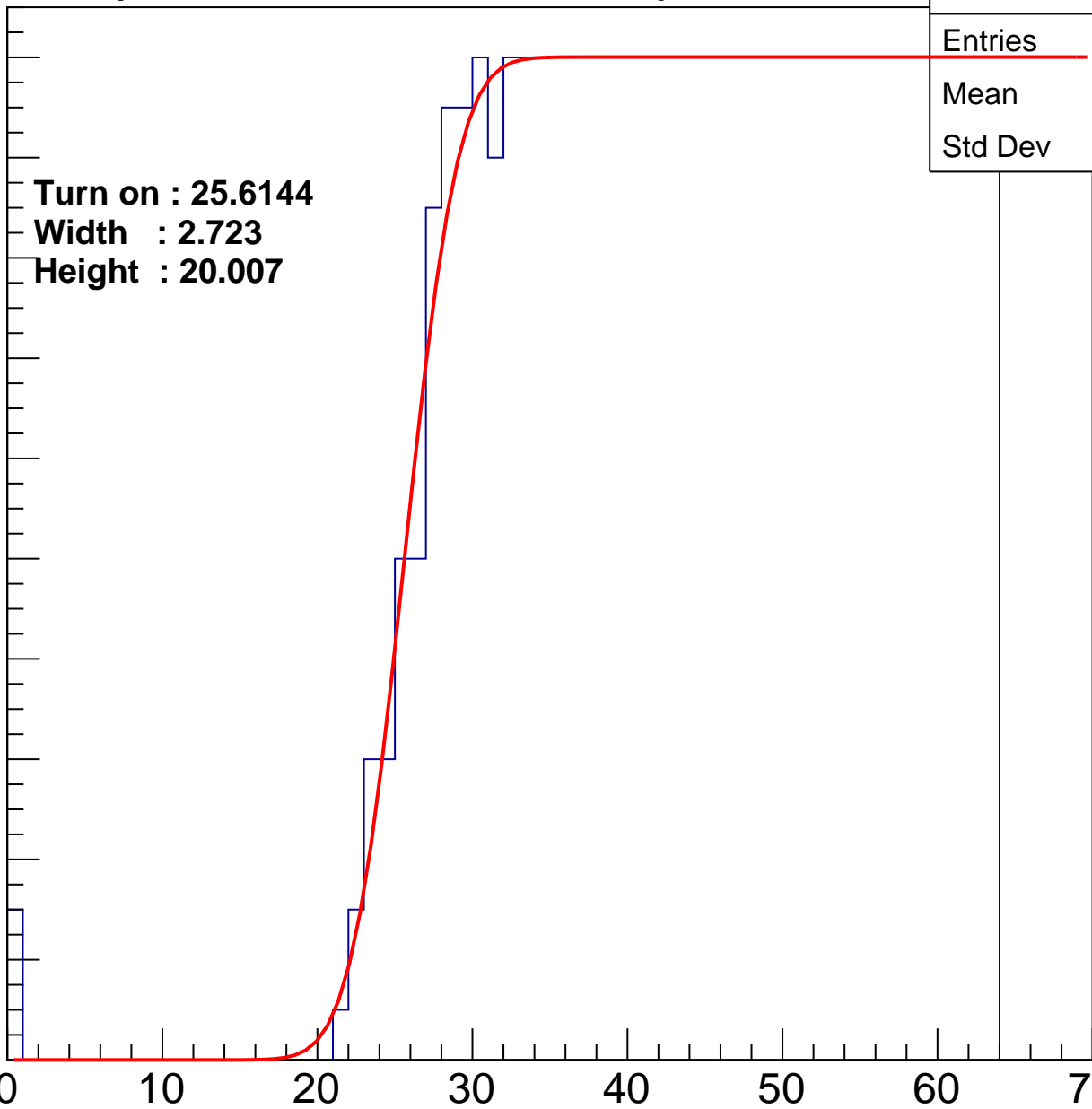
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6144
Width : 2.723
Height : 20.007

Entries	772
Mean	44.01
Std Dev	11.56

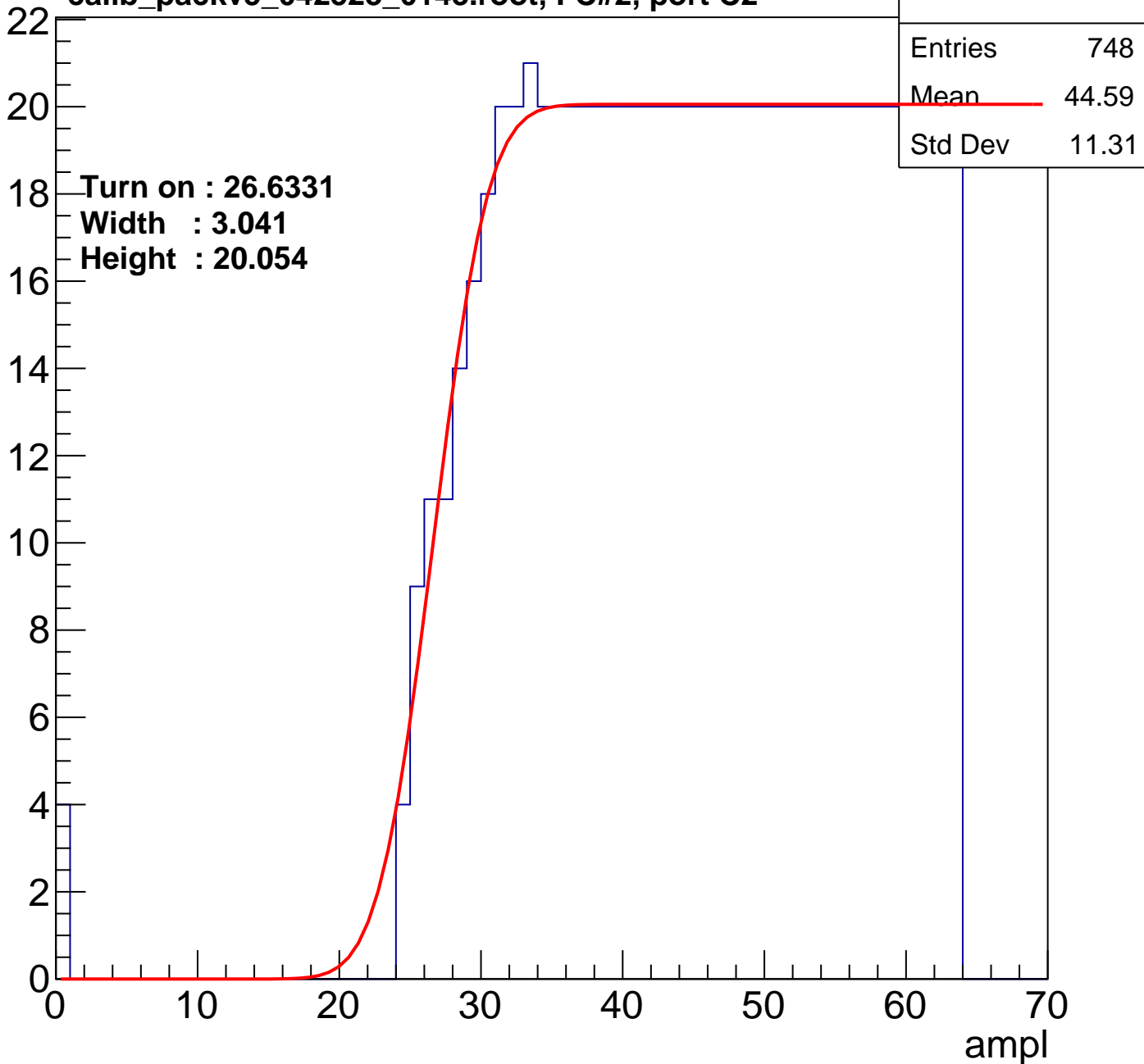
ampl



B1L001S, U25-ch75

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch76

calib_packv5_042523_0143.root, FC#2, port C2

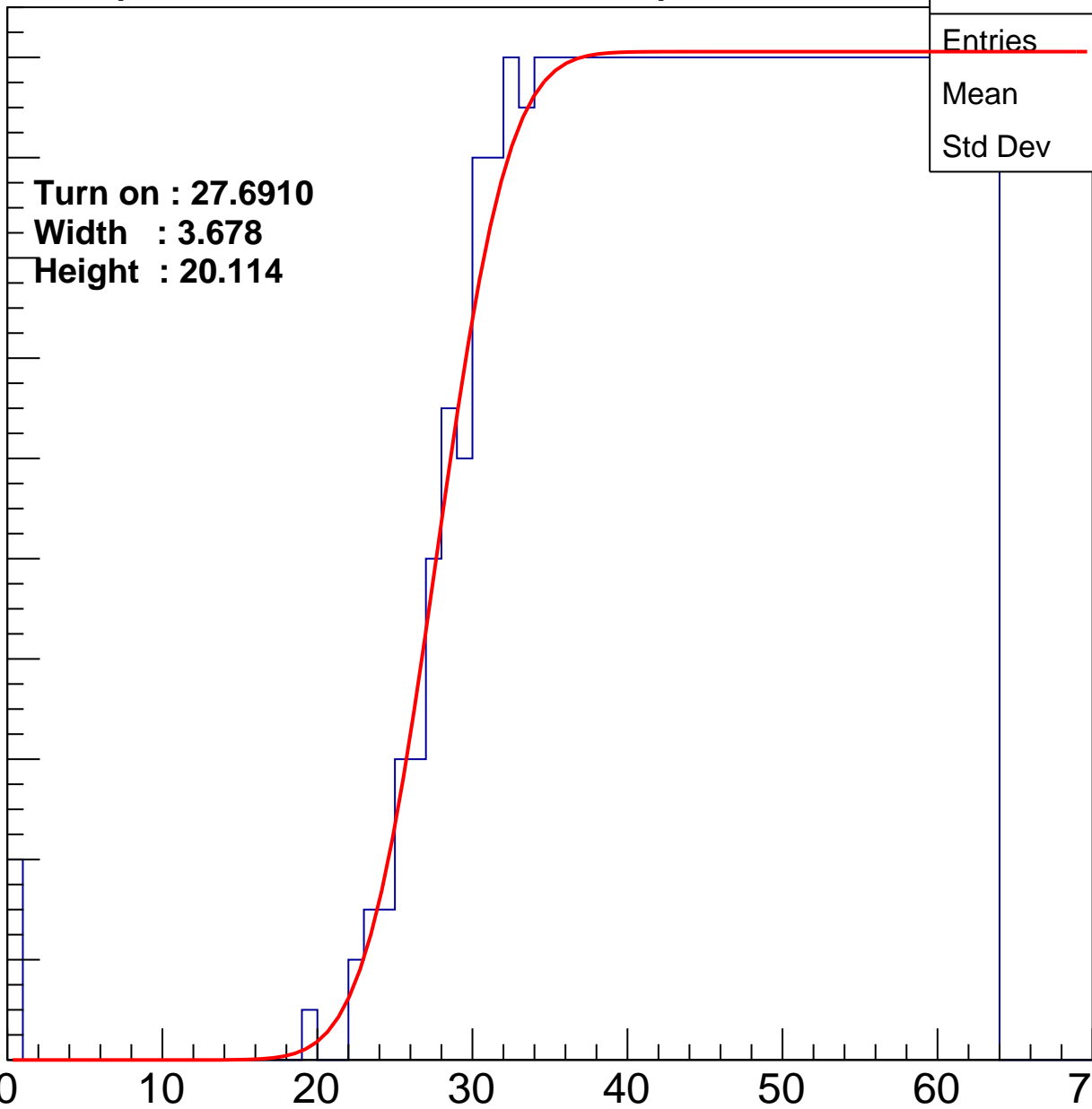
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6910
Width : 3.678
Height : 20.114

Entries	735
Mean	44.84
Std Dev	11.26

ampl



B1L001S, U25-ch77

calib_packv5_042523_0143.root, FC#2, port C2

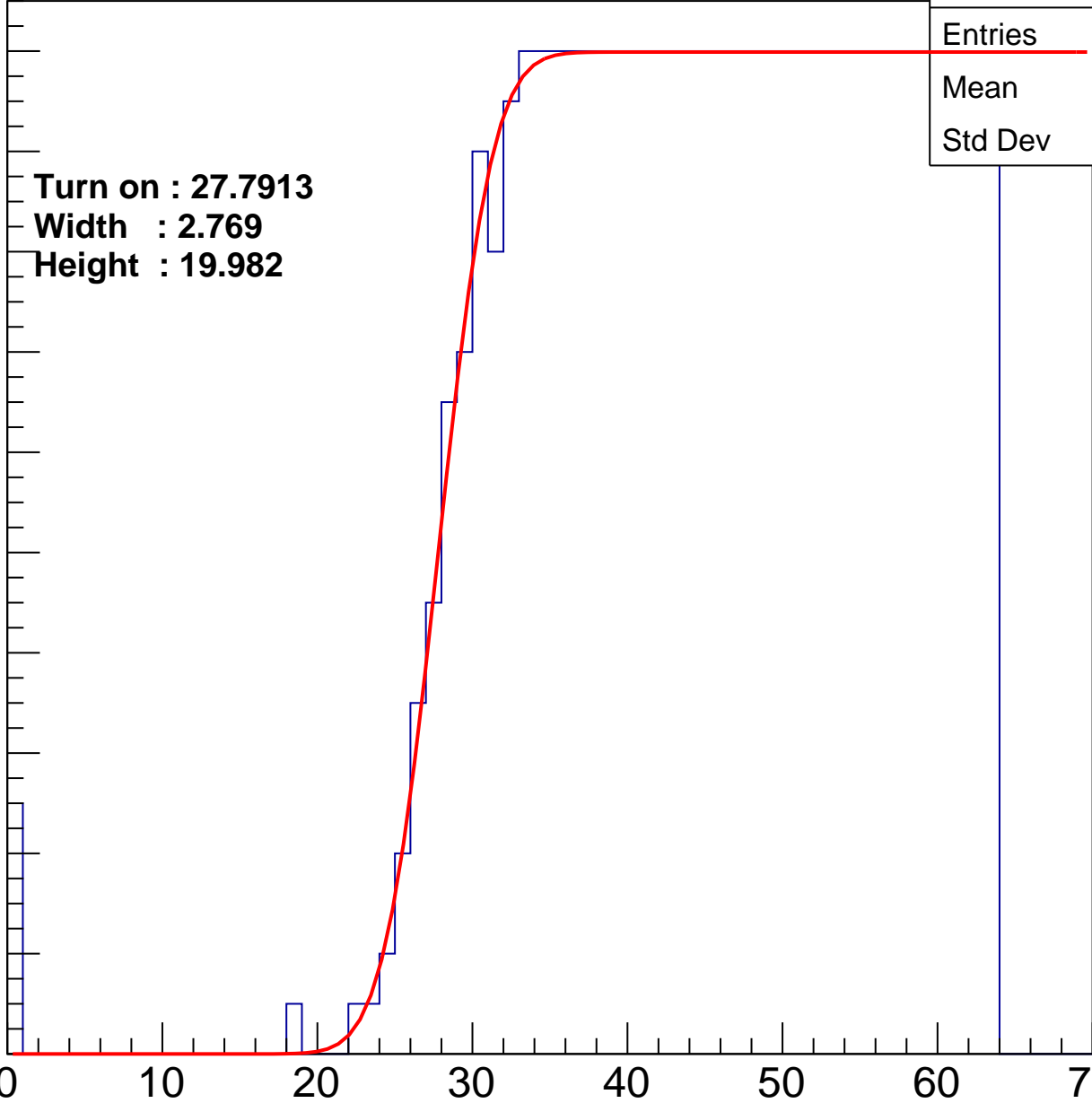
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7913
Width : 2.769
Height : 19.982

Entries	730
Mean	44.95
Std Dev	11.27

ampl



B1L001S, U25-ch78

calib_packv5_042523_0143.root, FC#2, port C2

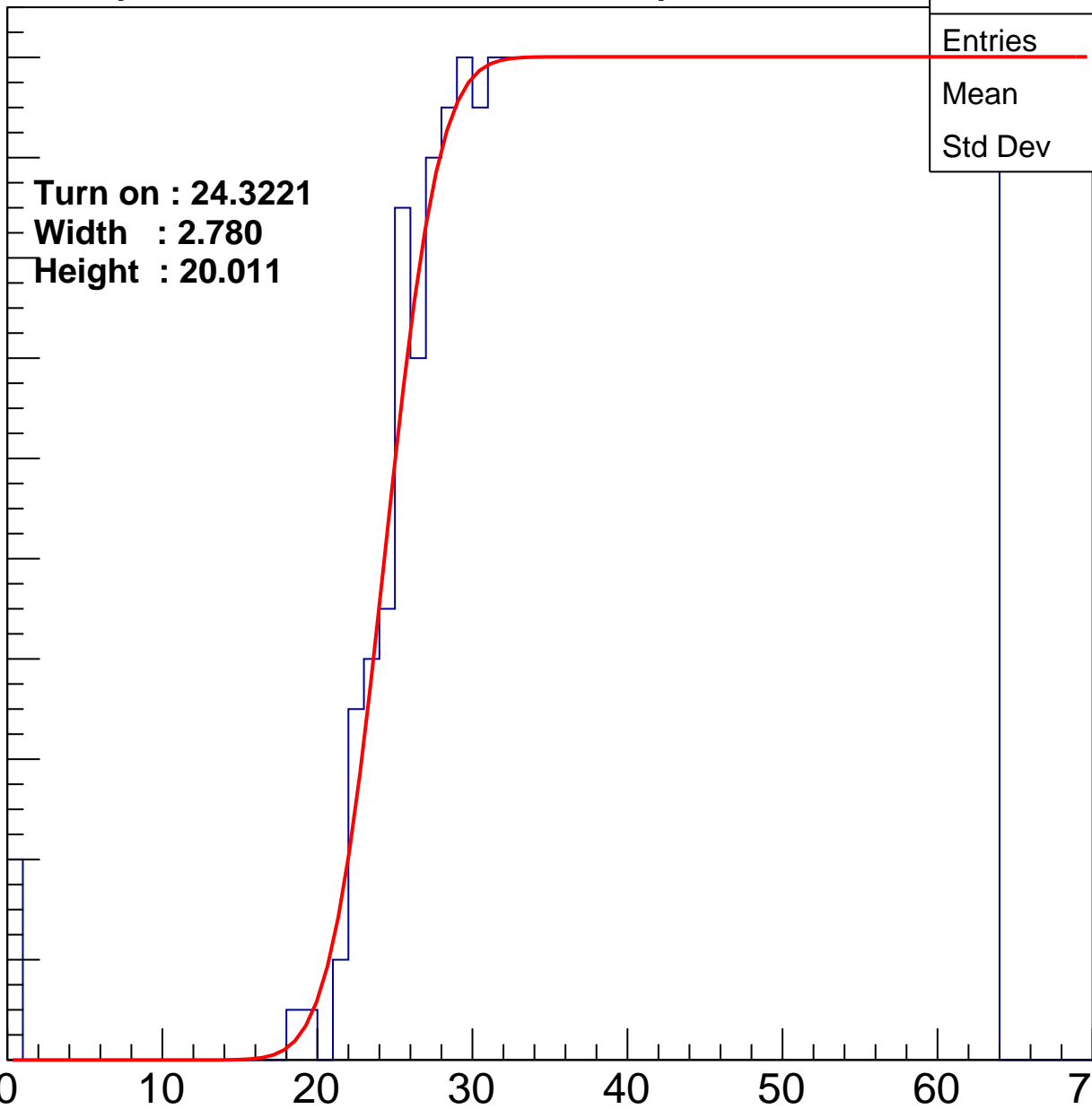
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.3221
Width : 2.780
Height : 20.011

Entries	799
Mean	43.32
Std Dev	12

ampl



B1L001S, U25-ch79

calib_packv5_042523_0143.root, FC#2, port C2

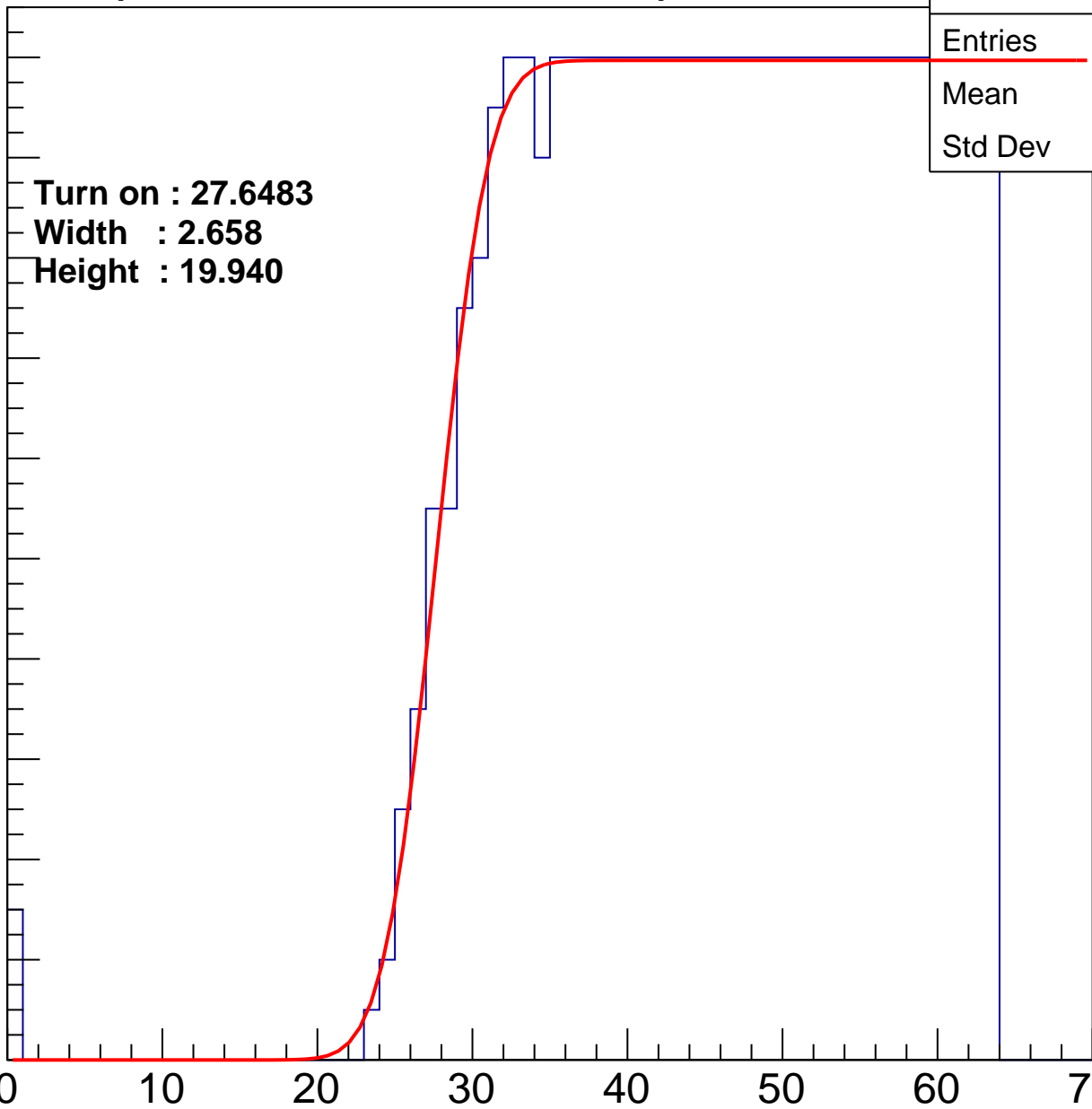
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6483
Width : 2.658
Height : 19.940

Entries	728
Mean	45.08
Std Dev	11.01

ampl



B1L001S, U25-ch80

calib_packv5_042523_0143.root, FC#2, port C2

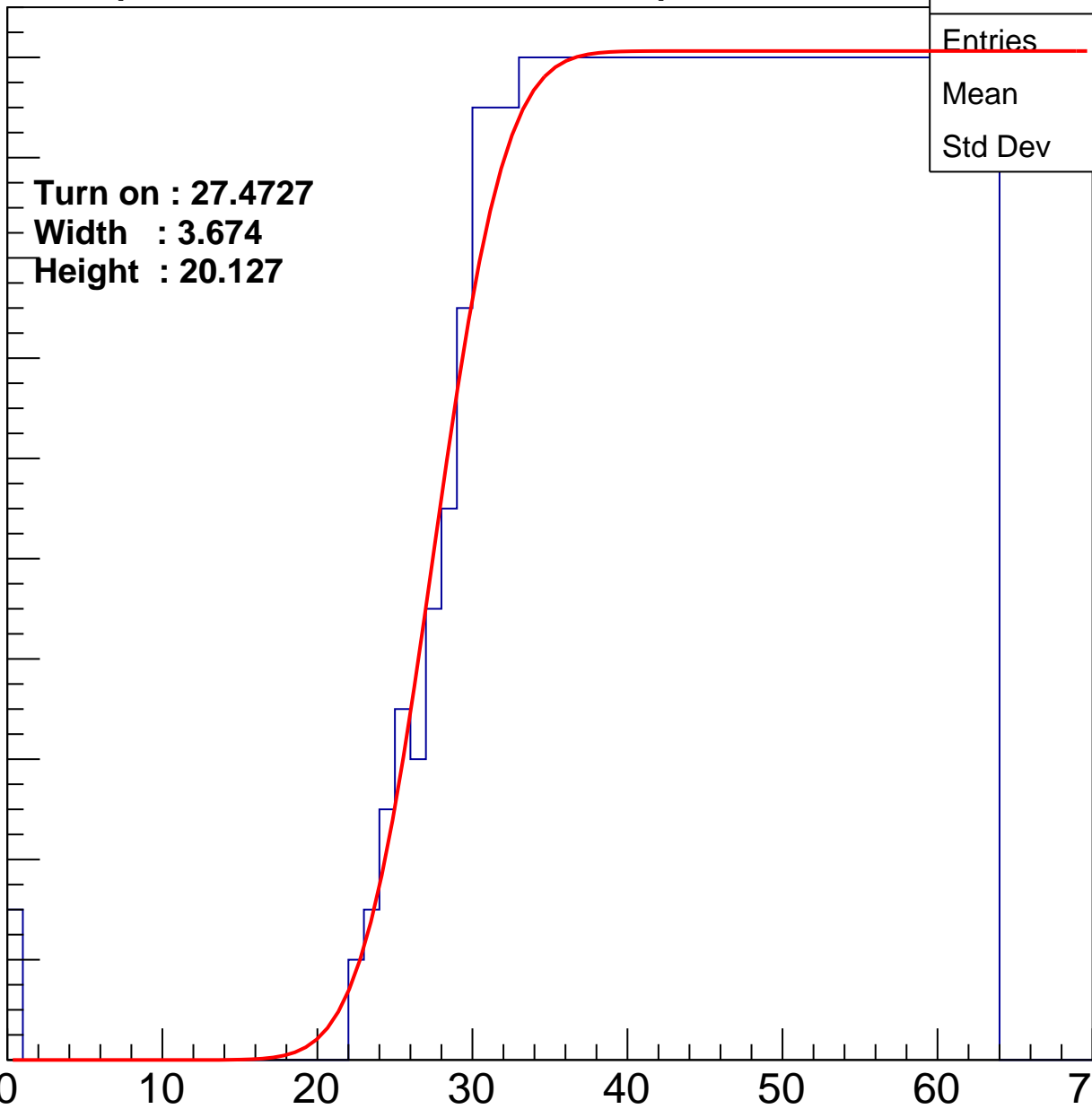
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4727
Width : 3.674
Height : 20.127

Entries	738
Mean	44.82
Std Dev	11.17

ampl



B1L001S, U25-ch81

calib_packv5_042523_0143.root, FC#2, port C2

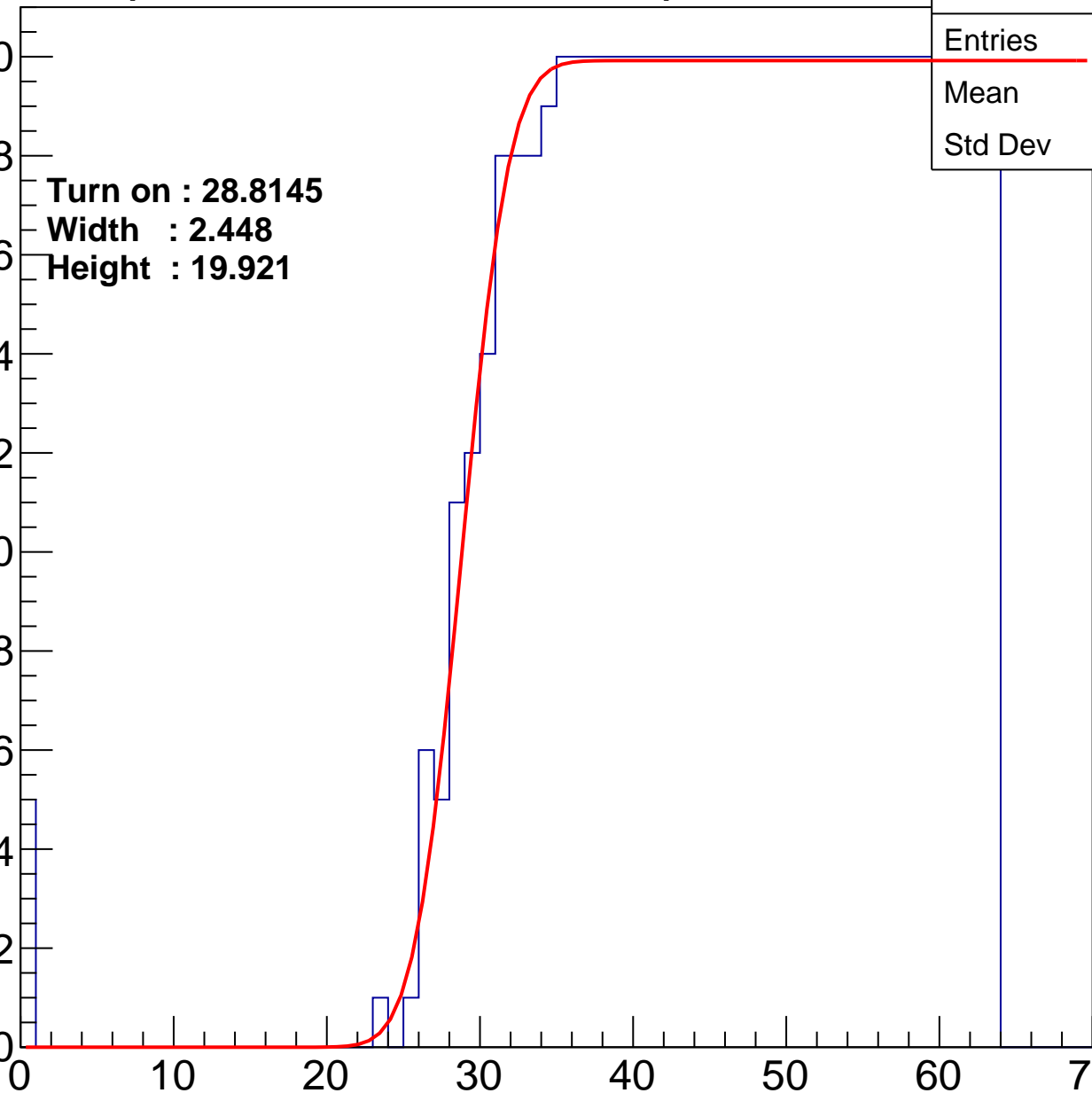
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.8145
Width : 2.448
Height : 19.921

Entries	708
Mean	45.49
Std Dev	10.98

ampl

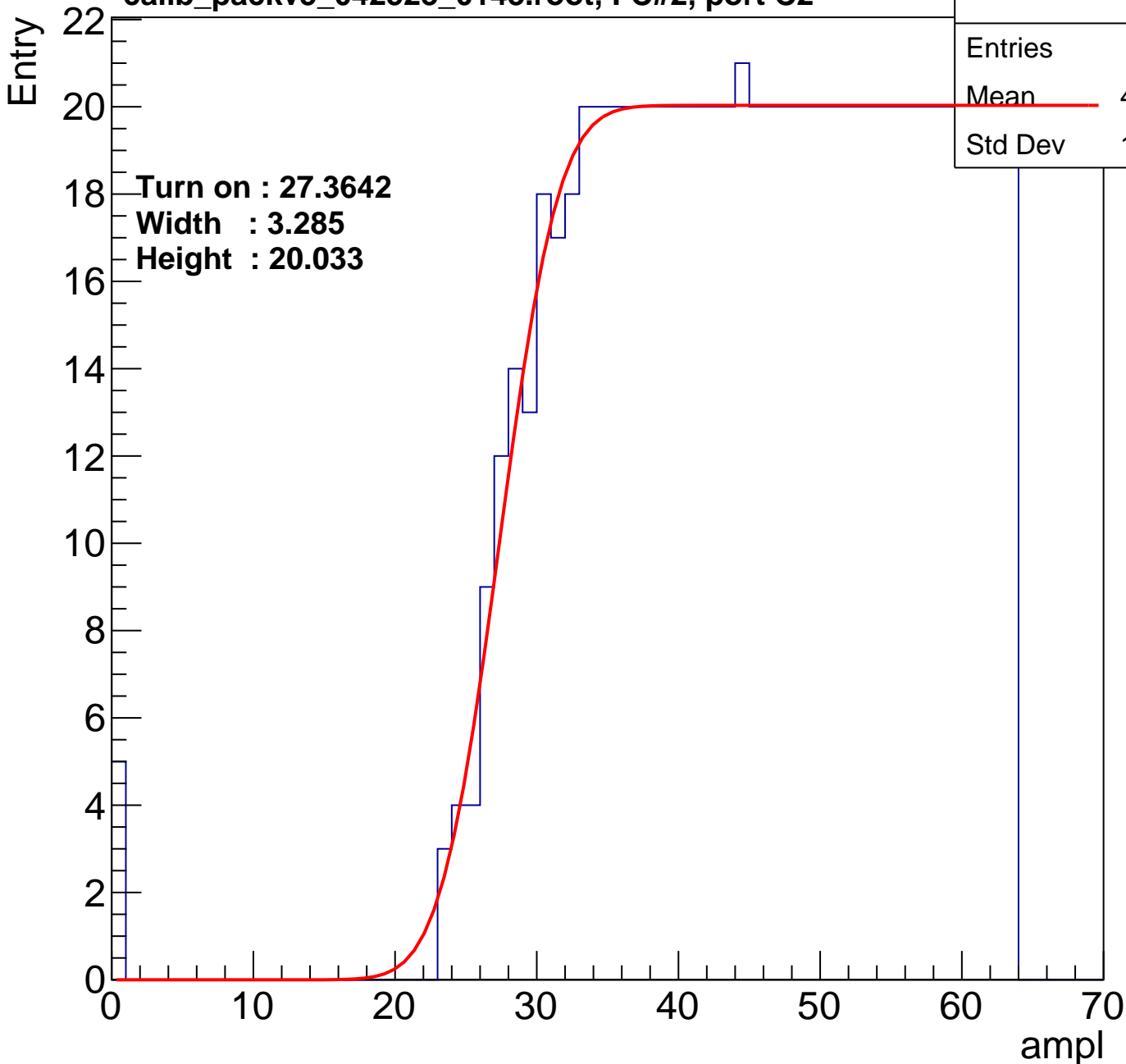


B1L001S, U25-ch82

calib_packv5_042523_0143.root, FC#2, port C2

Entries	738
Mean	44.77
Std Dev	11.35

Turn on : 27.3642
Width : 3.285
Height : 20.033



B1L001S, U25-ch83

calib_packv5_042523_0143.root, FC#2, port C2

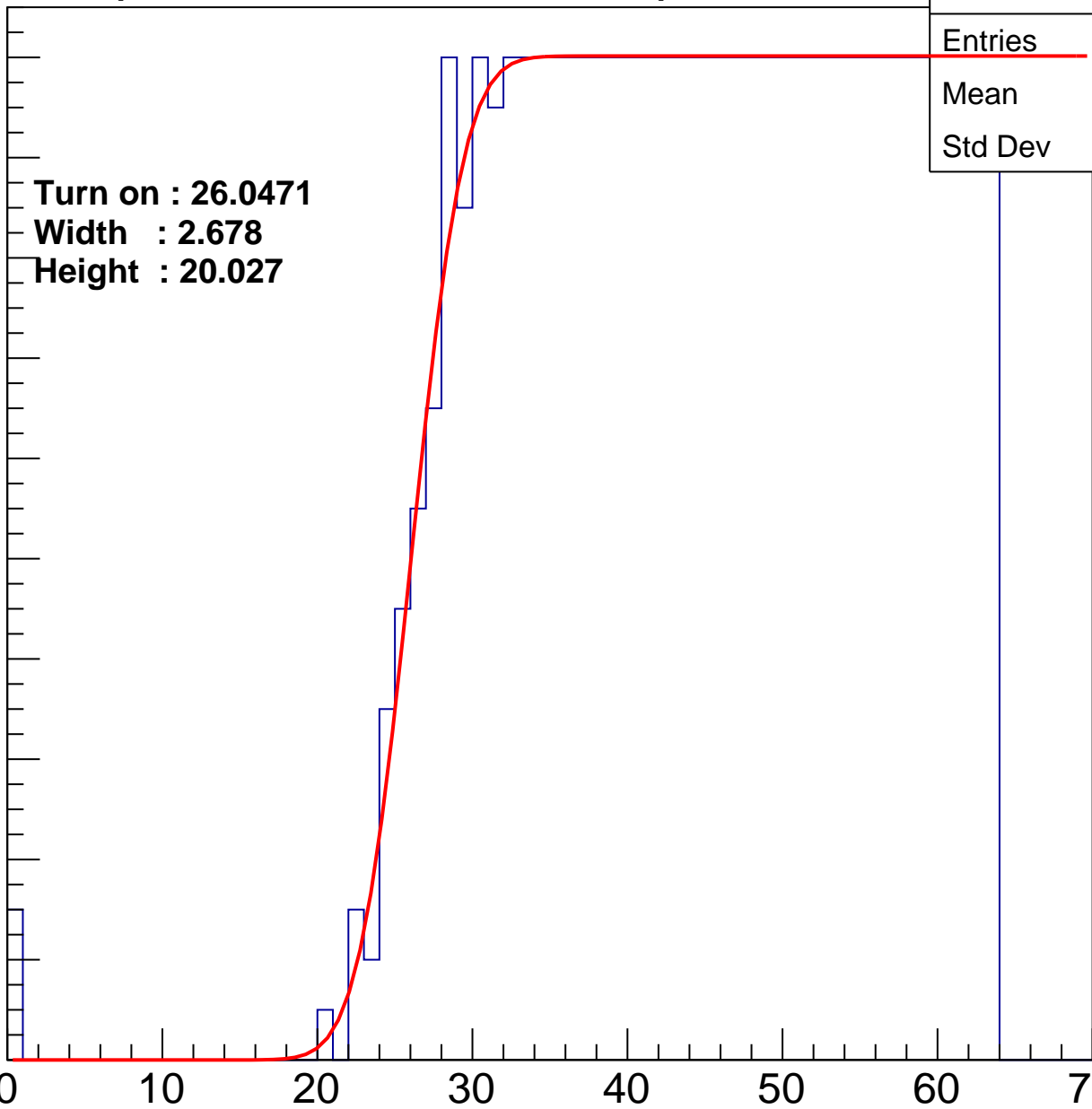
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0471
Width : 2.678
Height : 20.027

Entries	765
Mean	44.19
Std Dev	11.46

ampl



B1L001S, U25-ch84

calib_packv5_042523_0143.root, FC#2, port C2

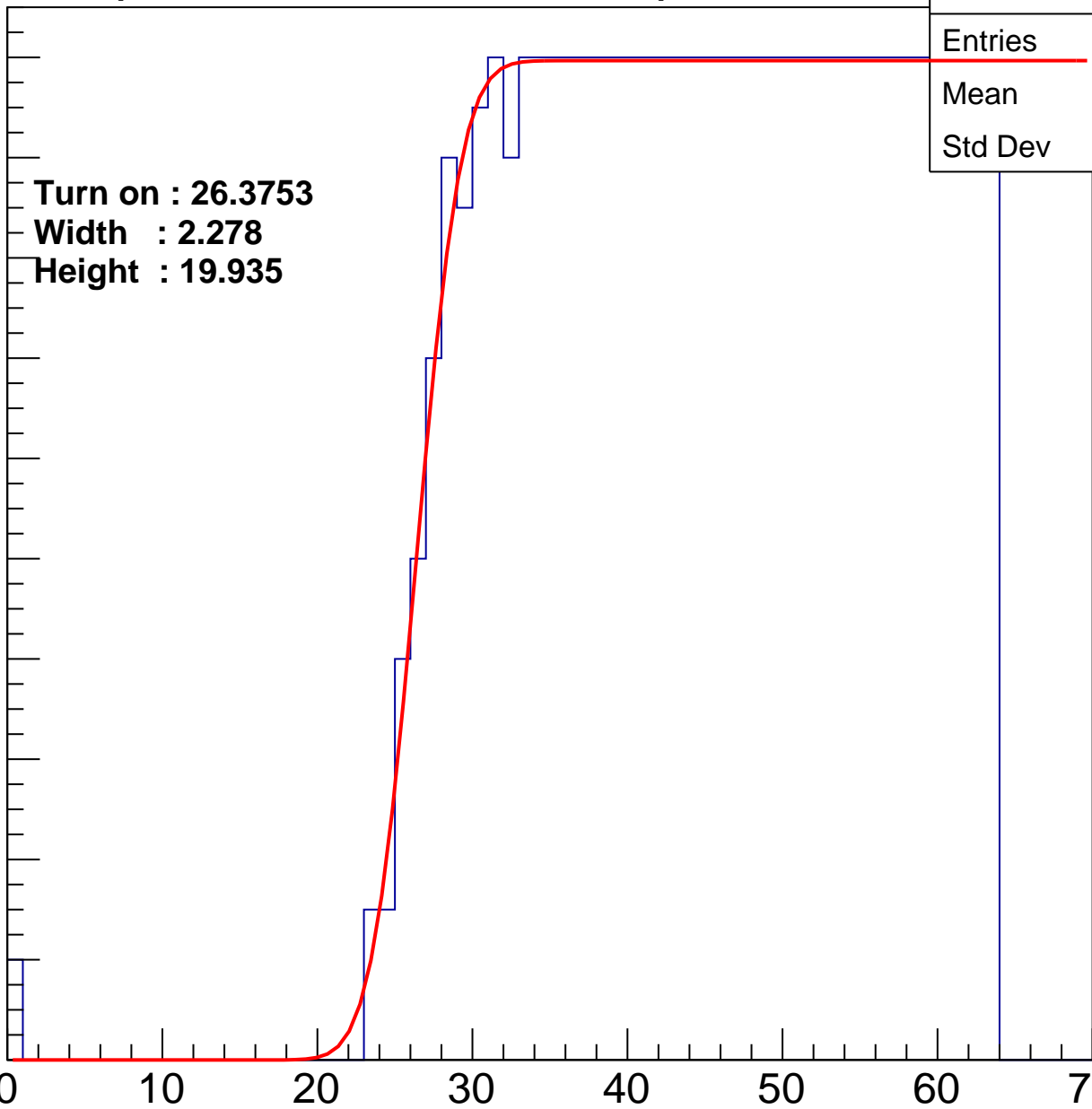
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3753
Width : 2.278
Height : 19.935

Entries	752
Mean	44.55
Std Dev	11.18

ampl



B1L001S, U25-ch85

calib_packv5_042523_0143.root, FC#2, port C2

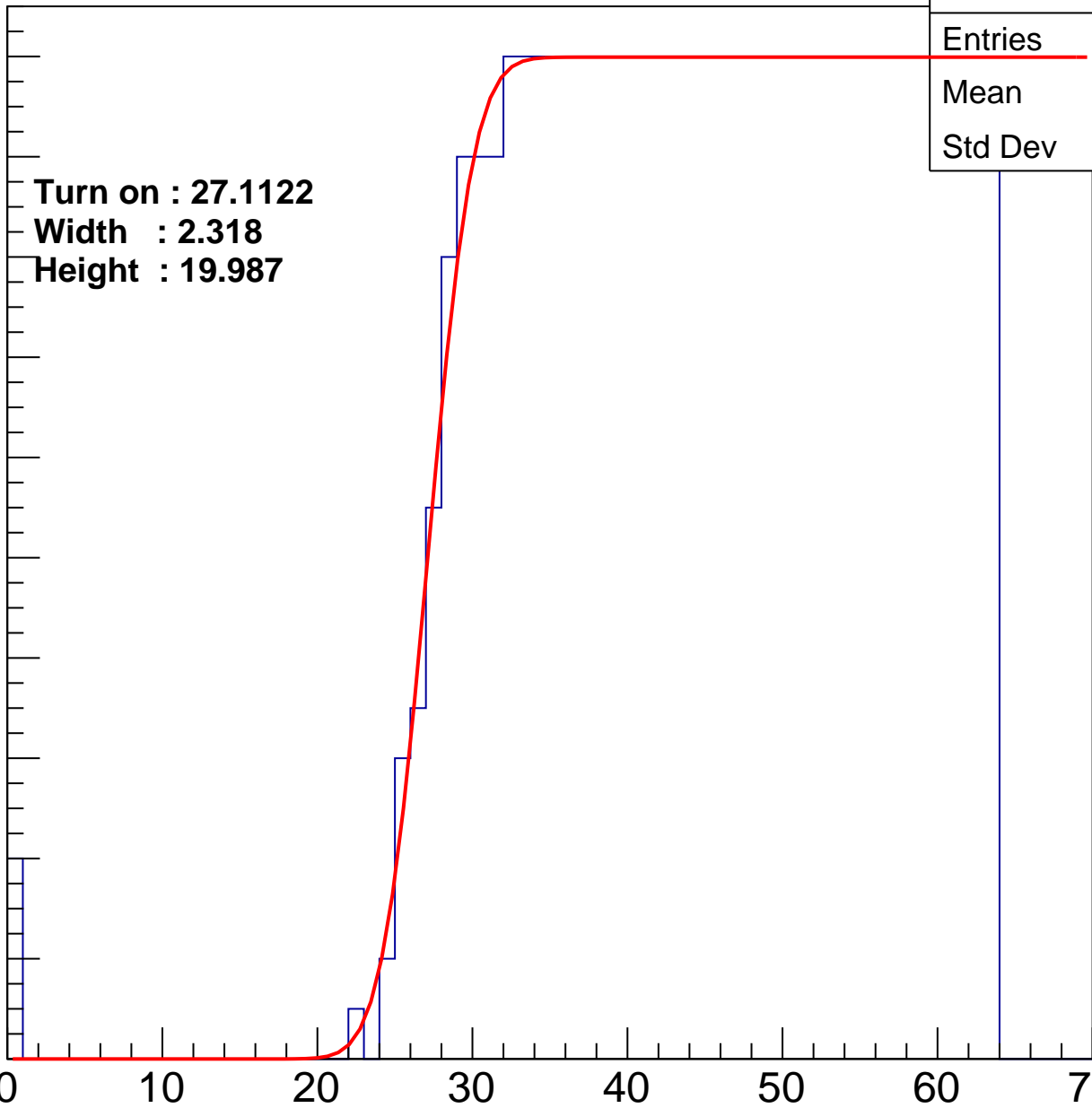
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1122
Width : 2.318
Height : 19.987

Entries	741
Mean	44.76
Std Dev	11.22

ampl



B1L001S, U25-ch86

calib_packv5_042523_0143.root, FC#2, port C2

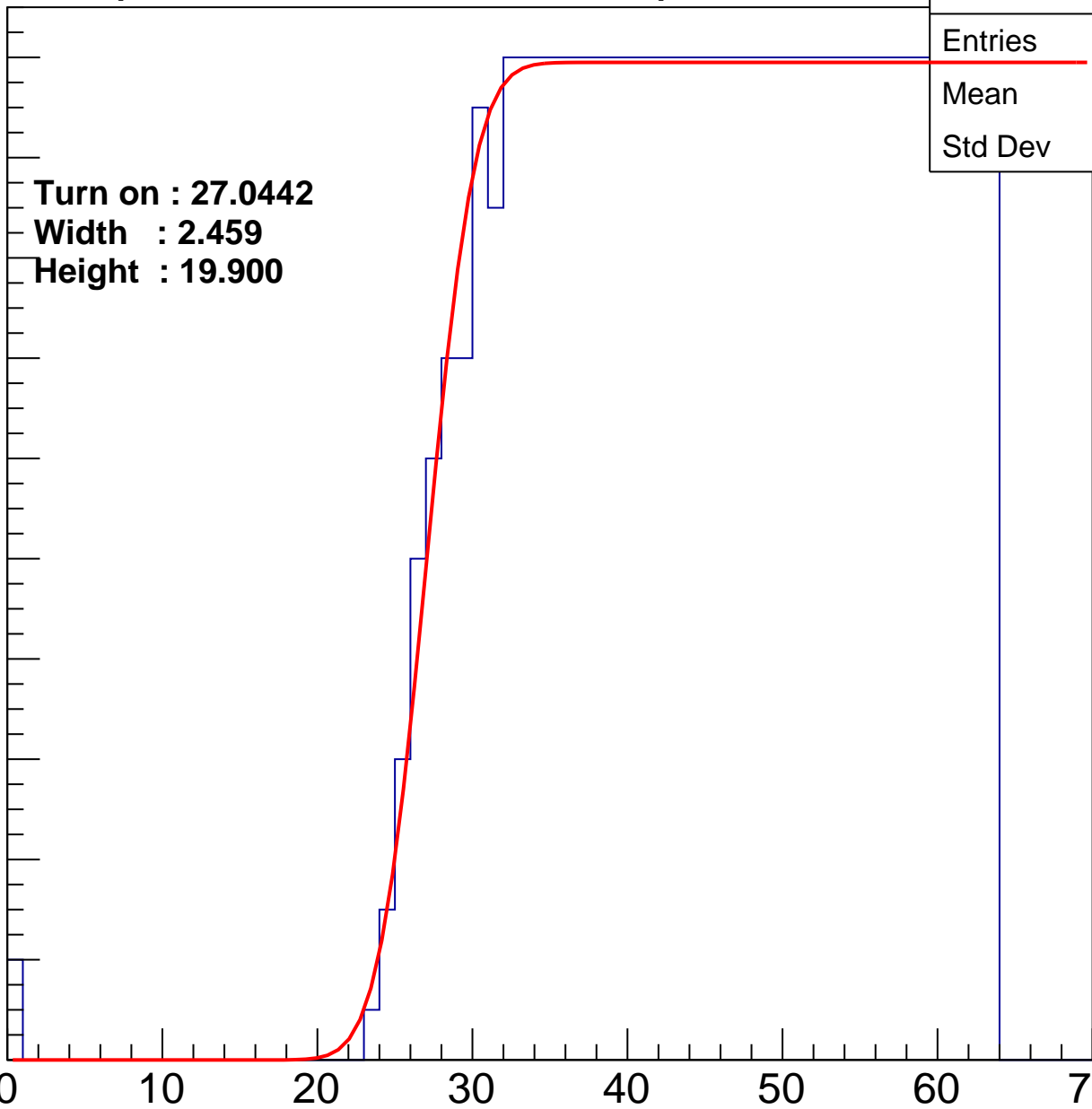
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0442
Width : 2.459
Height : 19.900

Entries	738
Mean	44.88
Std Dev	11.02

ampl



B1L001S, U25-ch87

calib_packv5_042523_0143.root, FC#2, port C2

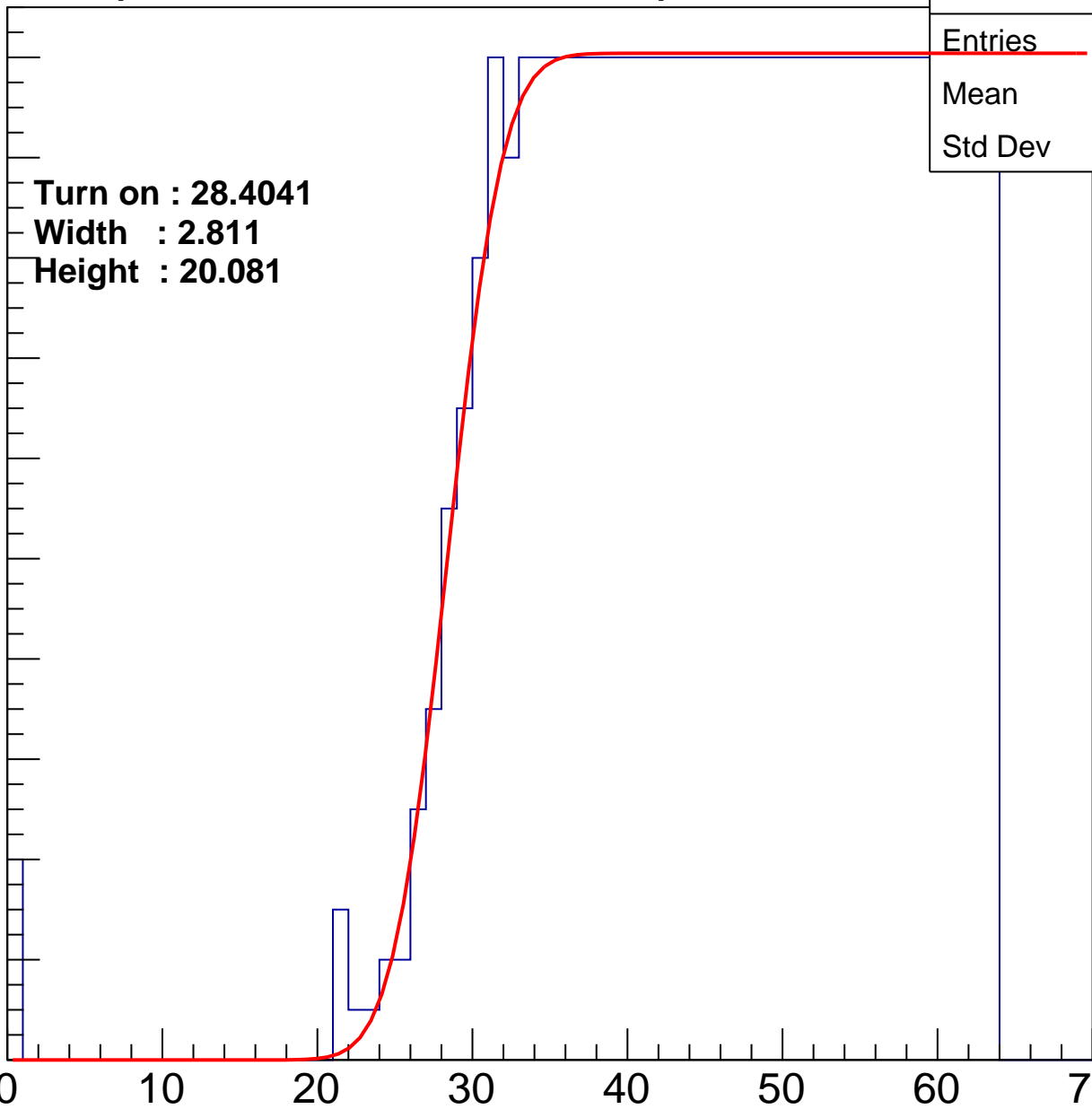
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4041
Width : 2.811
Height : 20.081

Entries	723
Mean	45.15
Std Dev	11.08

ampl



B1L001S, U25-ch88

calib_packv5_042523_0143.root, FC#2, port C2

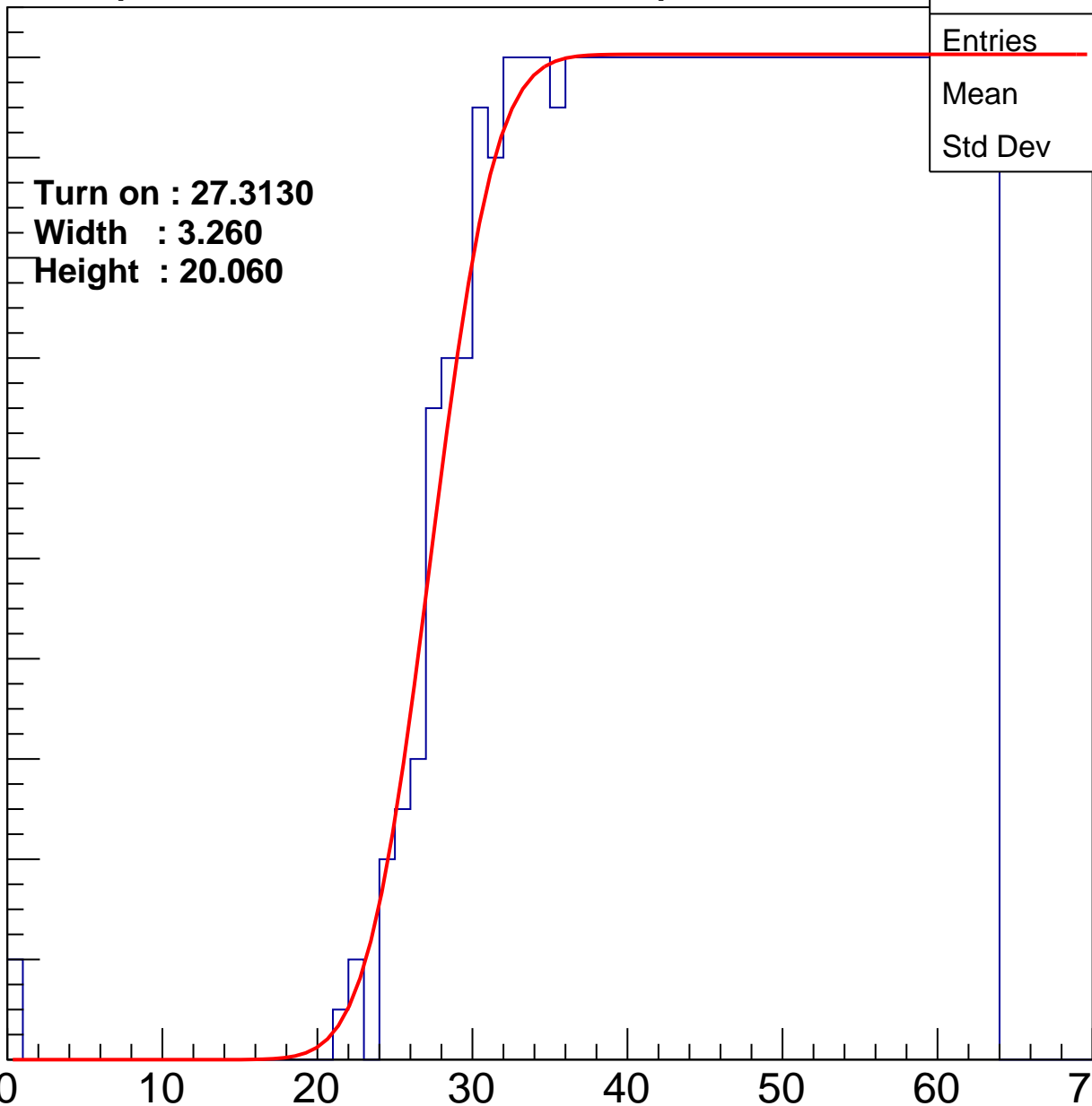
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3130
Width : 3.260
Height : 20.060

Entries	737
Mean	44.89
Std Dev	11.03

ampl



B1L001S, U25-ch89

calib_packv5_042523_0143.root, FC#2, port C2

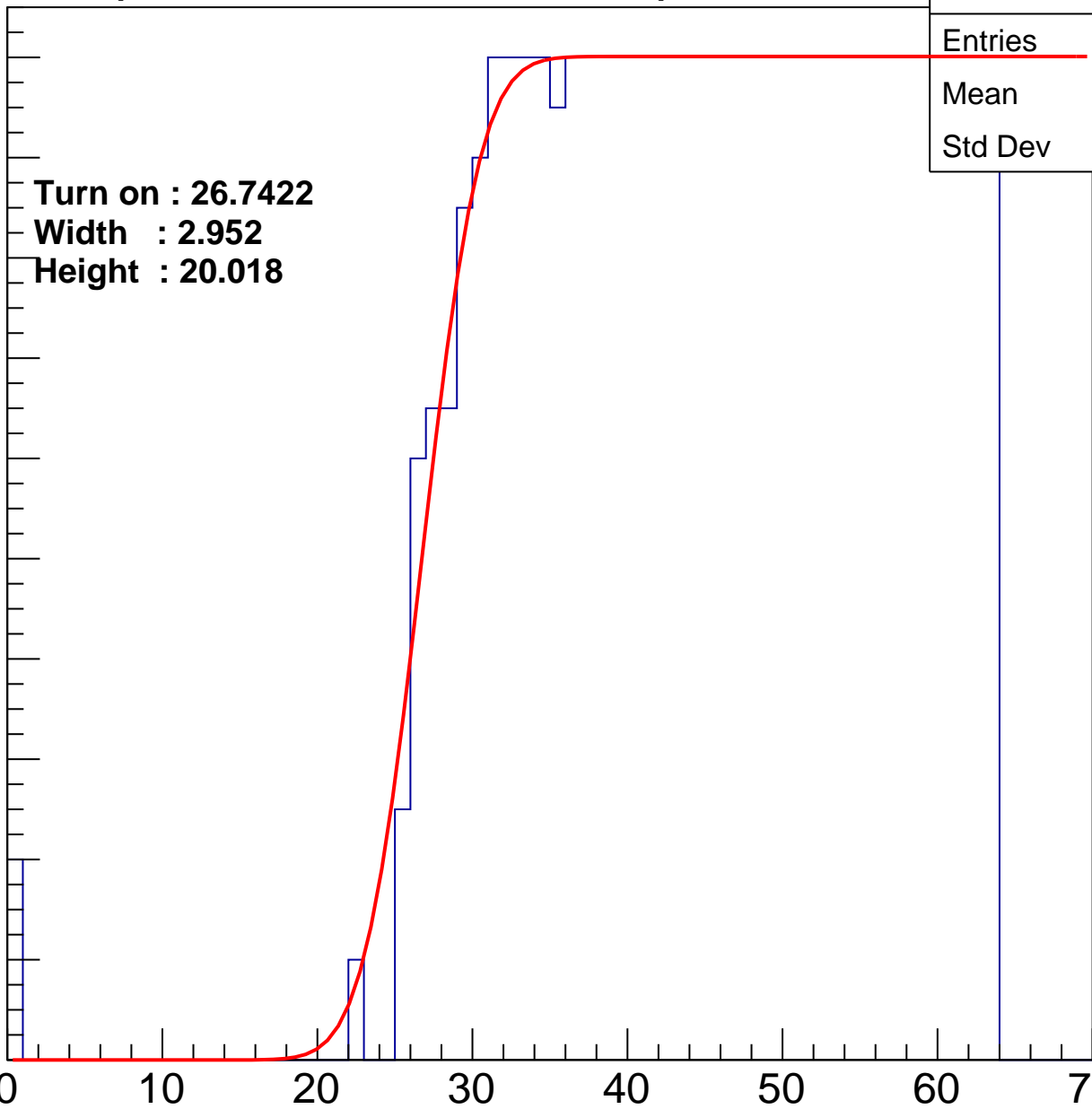
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7422
Width : 2.952
Height : 20.018

Entries	743
Mean	44.7
Std Dev	11.26

ampl



B1L001S, U25-ch90

calib_packv5_042523_0143.root, FC#2, port C2

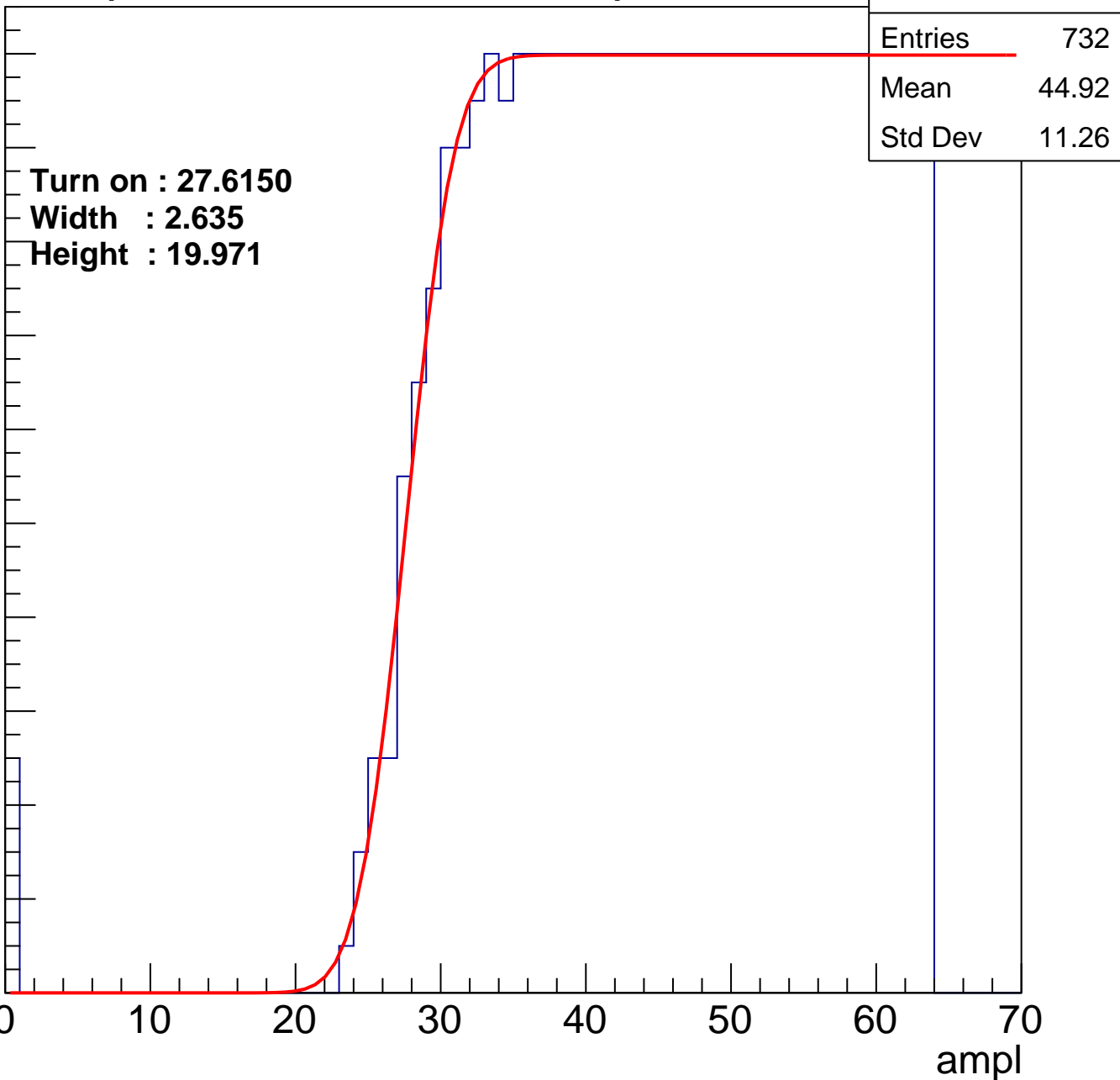
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6150
Width : 2.635
Height : 19.971

Entries	732
Mean	44.92
Std Dev	11.26

ampl



B1L001S, U25-ch91

calib_packv5_042523_0143.root, FC#2, port C2

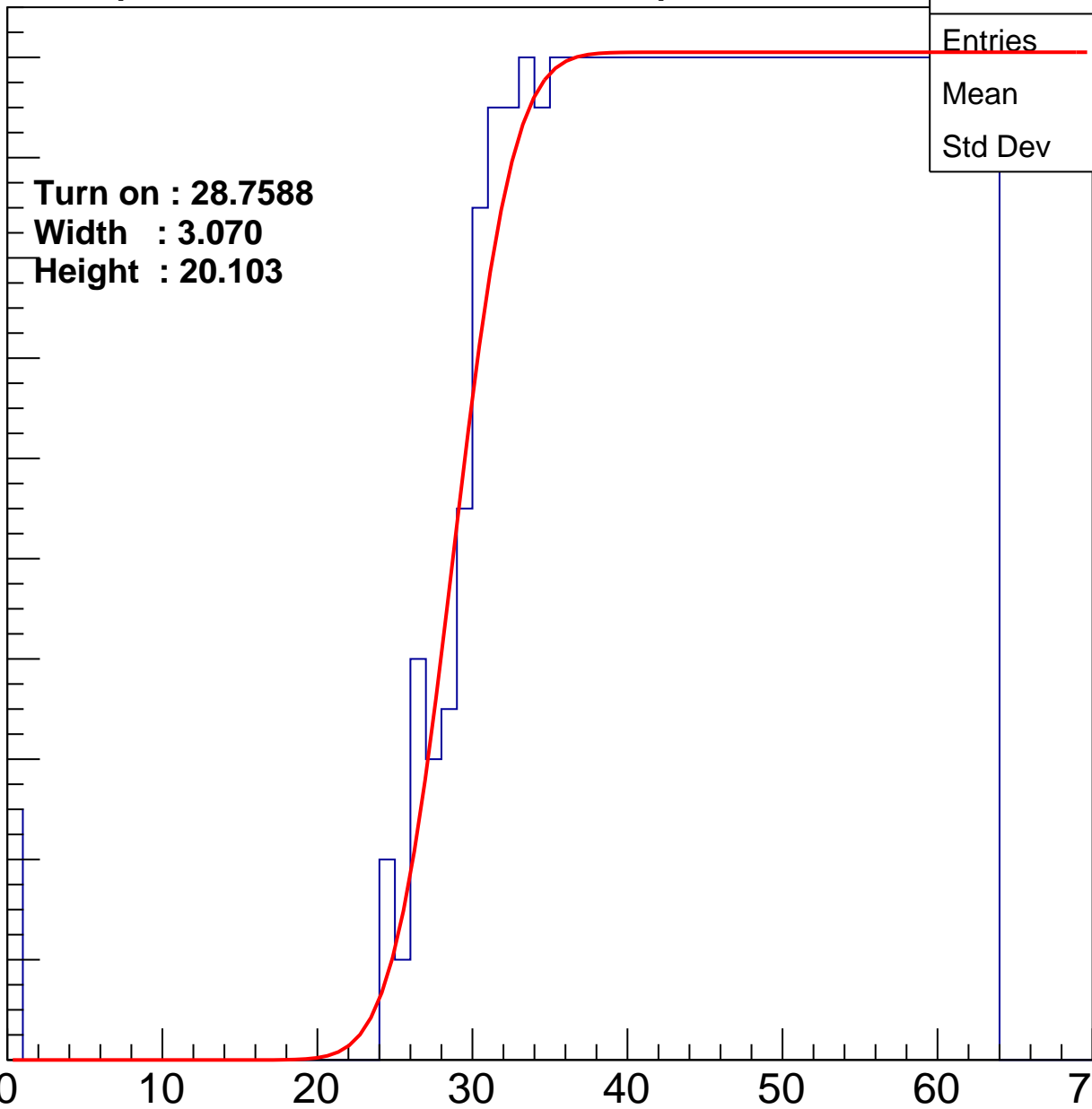
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.7588
Width : 3.070
Height : 20.103

Entries	717
Mean	45.28
Std Dev	11.09

ampl



B1L001S, U25-ch92

calib_packv5_042523_0143.root, FC#2, port C2

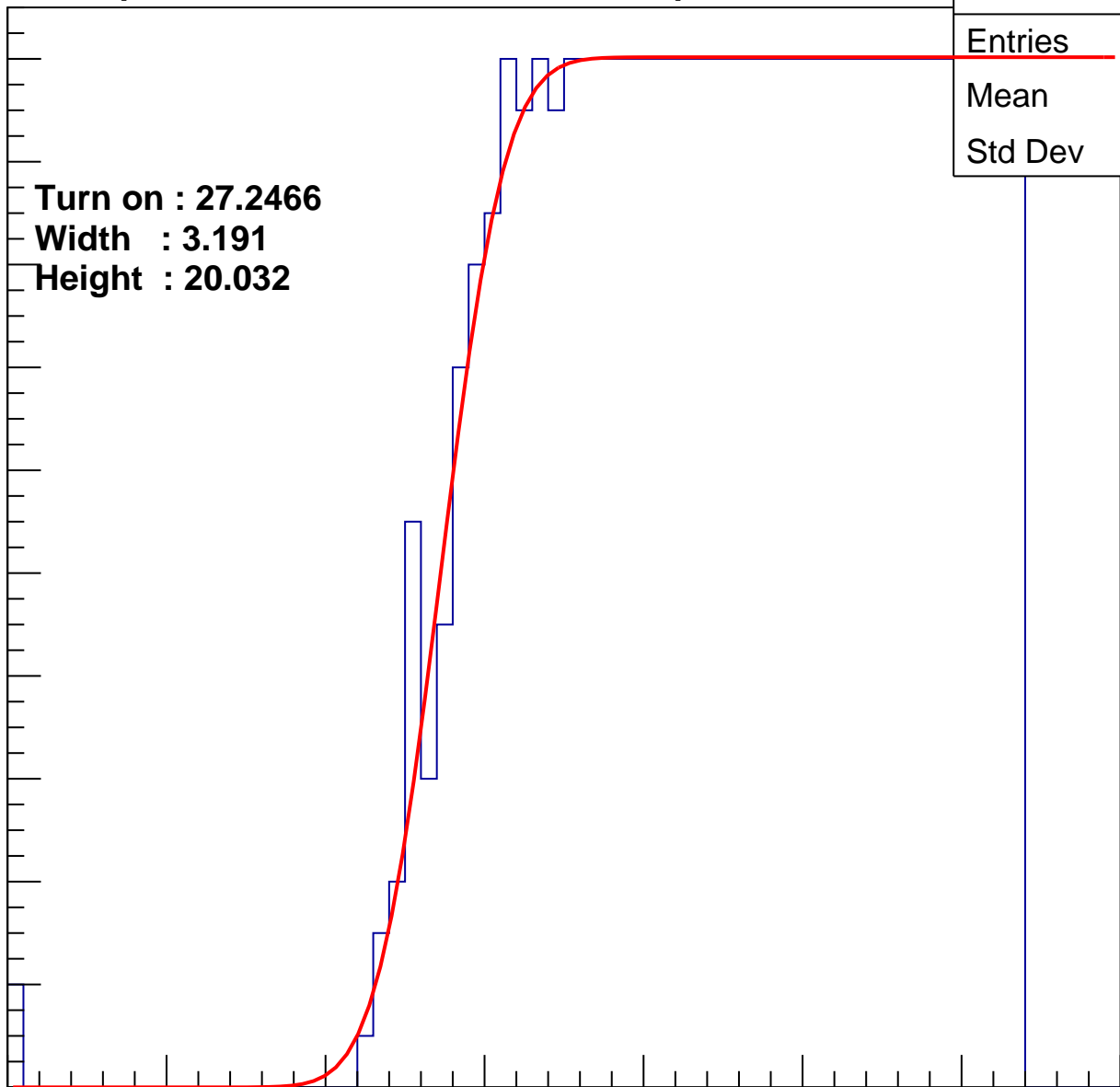
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2466
Width : 3.191
Height : 20.032

Entries	741
Mean	44.78
Std Dev	11.11

ampl



B1L001S, U25-ch93

calib_packv5_042523_0143.root, FC#2, port C2

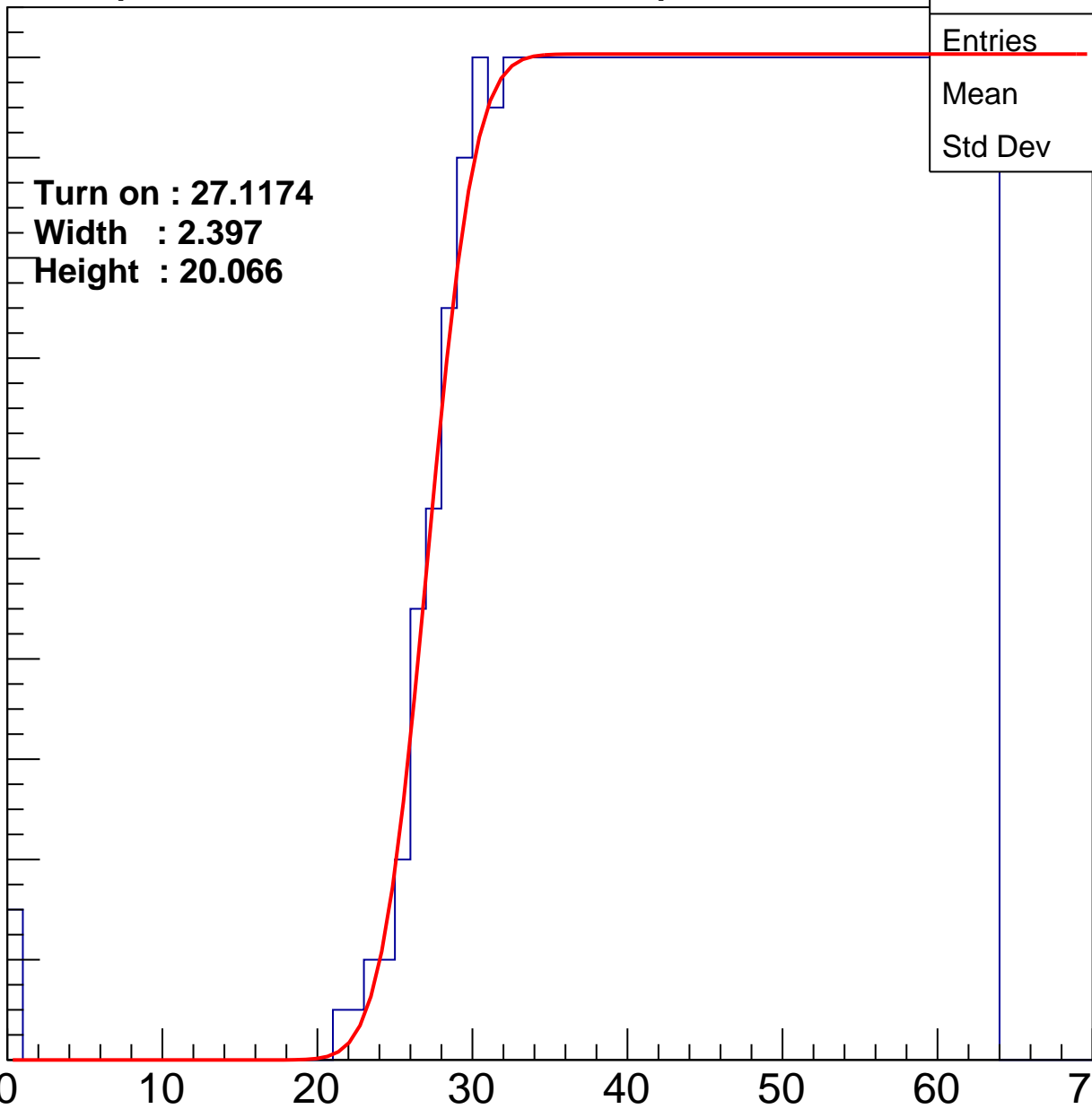
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1174
Width : 2.397
Height : 20.066

Entries	745
Mean	44.7
Std Dev	11.18

ampl



B1L001S, U25-ch94

calib_packv5_042523_0143.root, FC#2, port C2

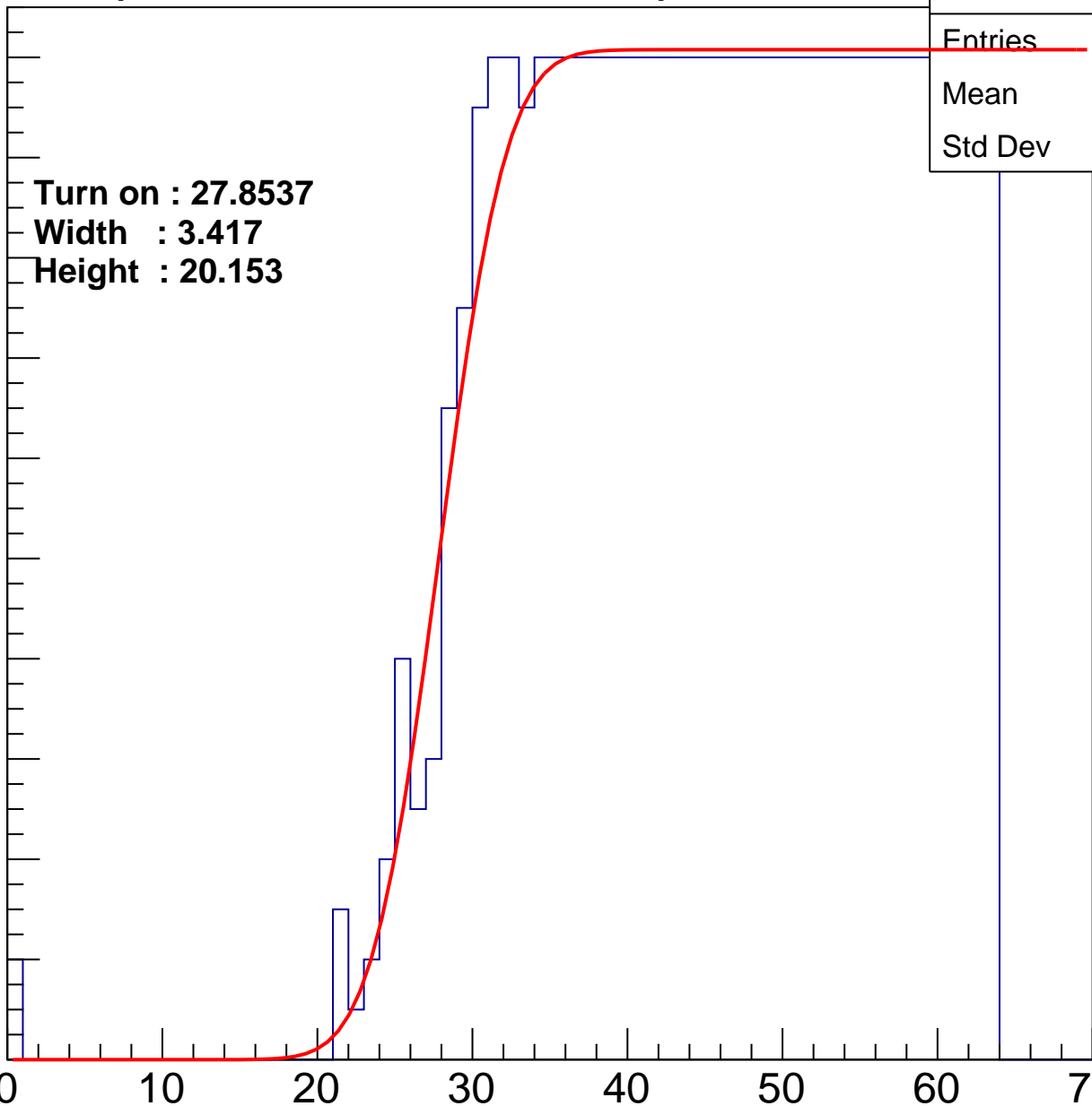
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8537
Width : 3.417
Height : 20.153

Entries	737
Mean	44.88
Std Dev	11.06

ampl



B1L001S, U25-ch95

calib_packv5_042523_0143.root, FC#2, port C2

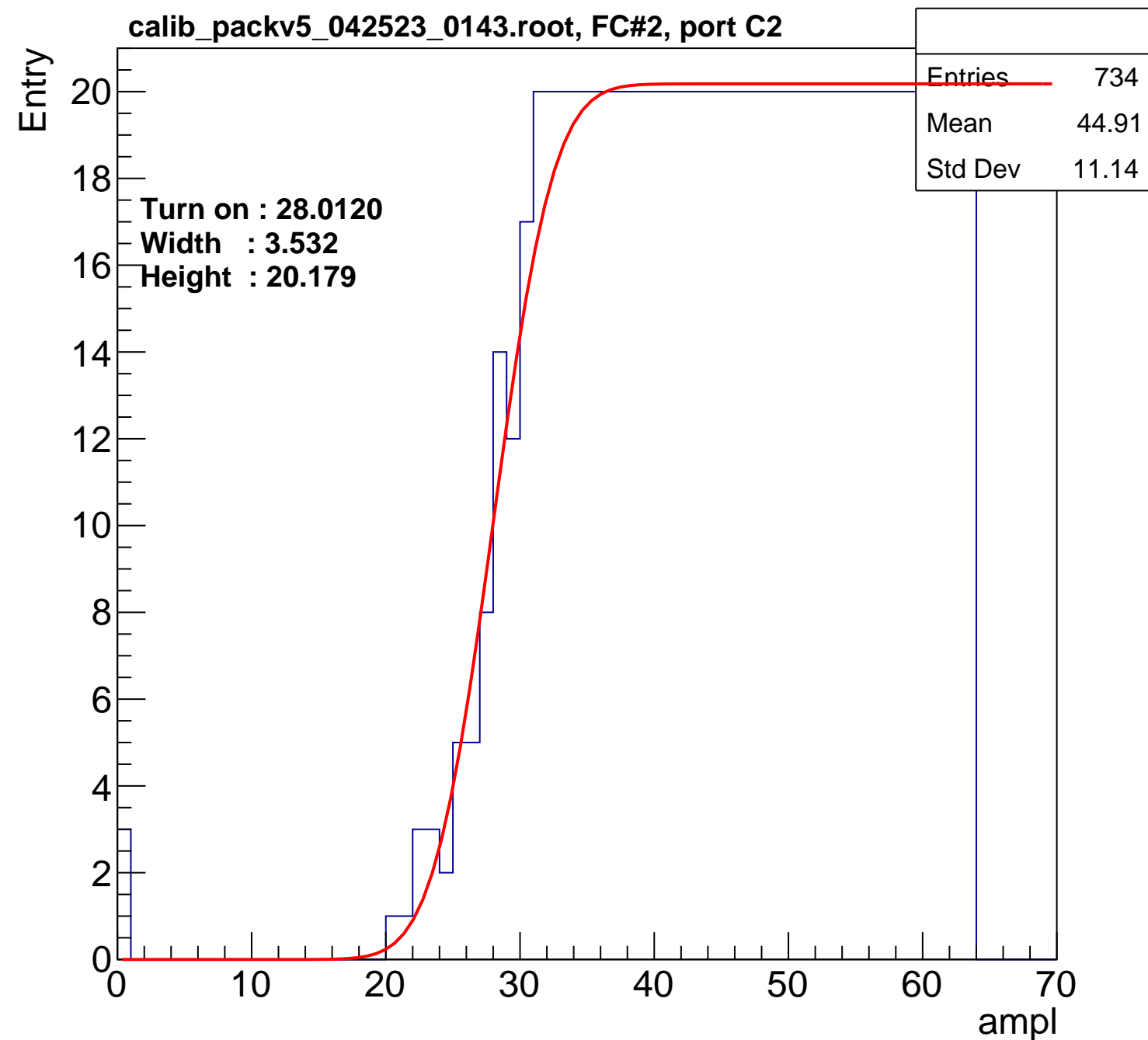
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0120
Width : 3.532
Height : 20.179

Entries	734
Mean	44.91
Std Dev	11.14

ampl



B1L001S, U25-ch96

calib_packv5_042523_0143.root, FC#2, port C2

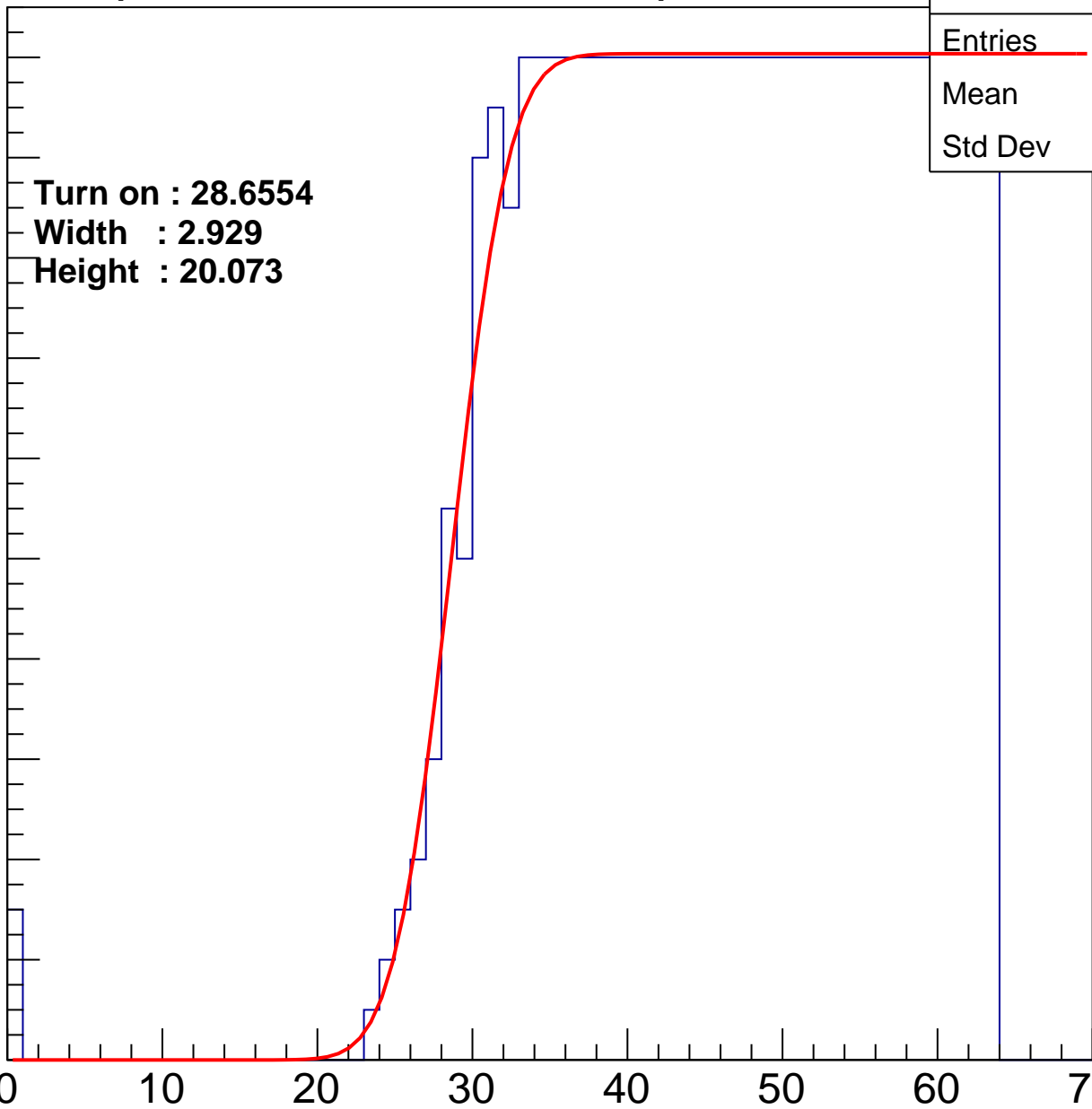
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6554
Width : 2.929
Height : 20.073

Entries	714
Mean	45.44
Std Dev	10.81

ampl



B1L001S, U25-ch97

calib_packv5_042523_0143.root, FC#2, port C2

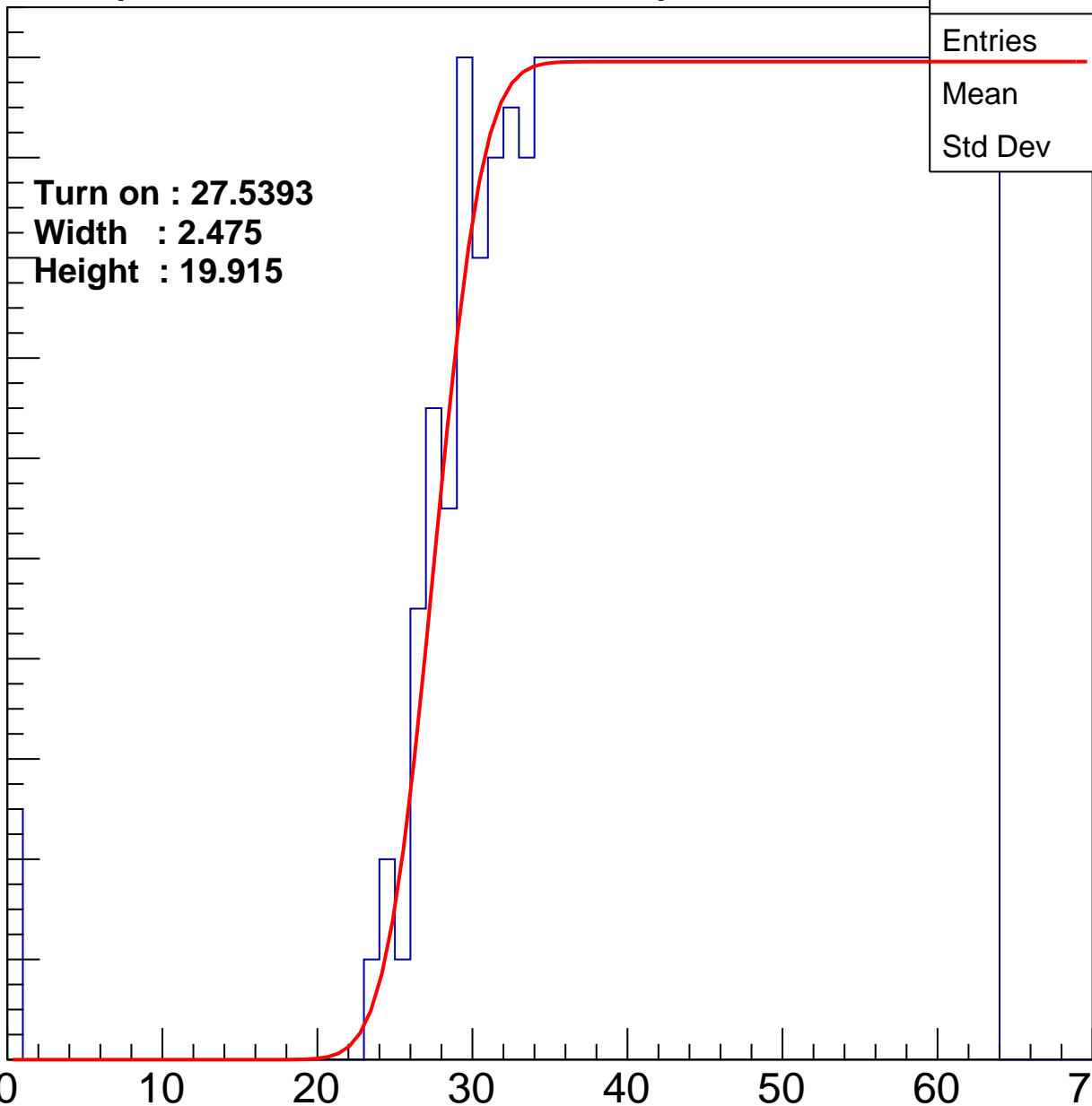
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5393
Width : 2.475
Height : 19.915

Entries	737
Mean	44.78
Std Dev	11.33

ampl



B1L001S, U25-ch98

calib_packv5_042523_0143.root, FC#2, port C2

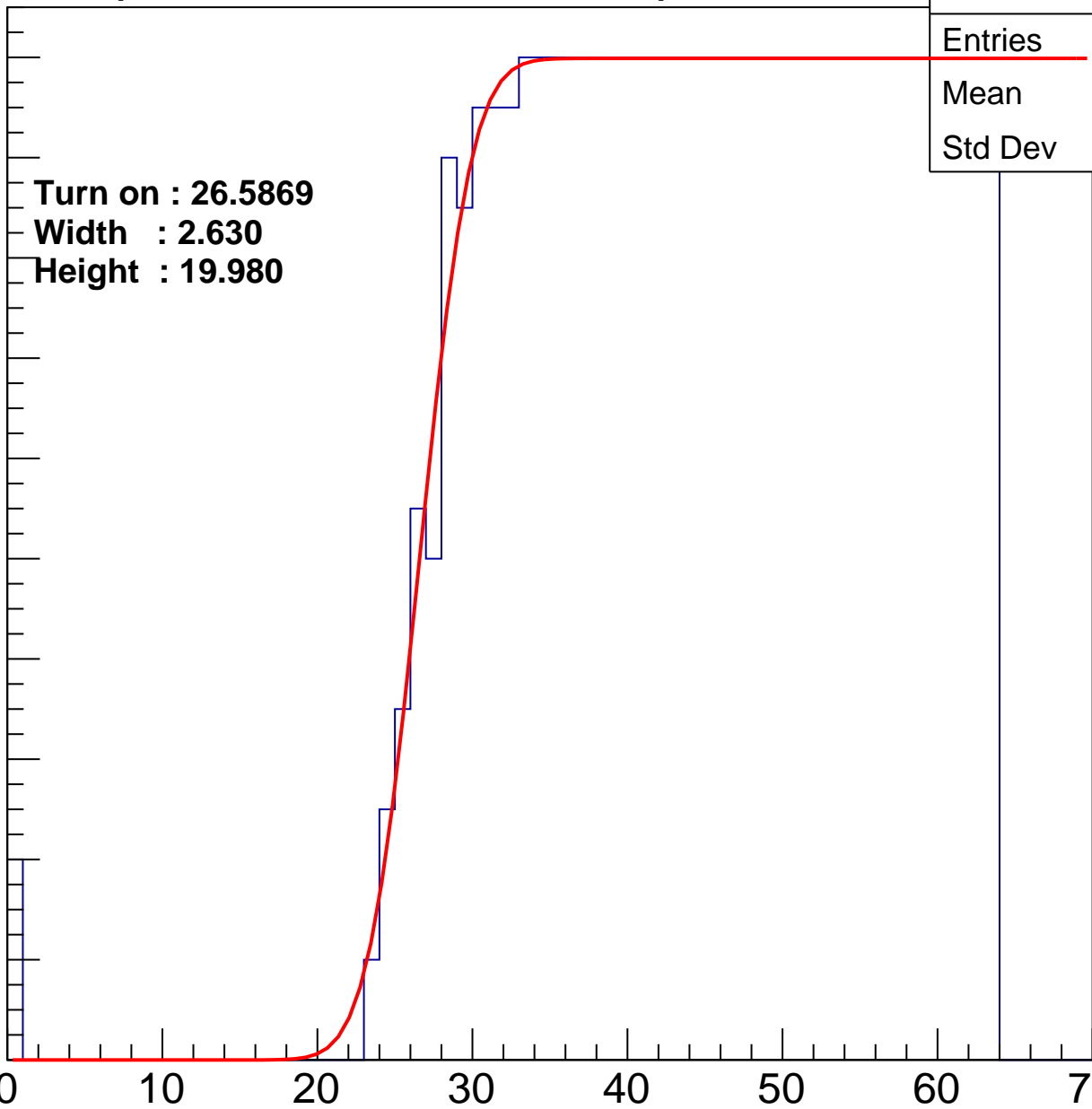
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5869
Width : 2.630
Height : 19.980

Entries	751
Mean	44.5
Std Dev	11.37

ampl



B1L001S, U25-ch99

calib_packv5_042523_0143.root, FC#2, port C2

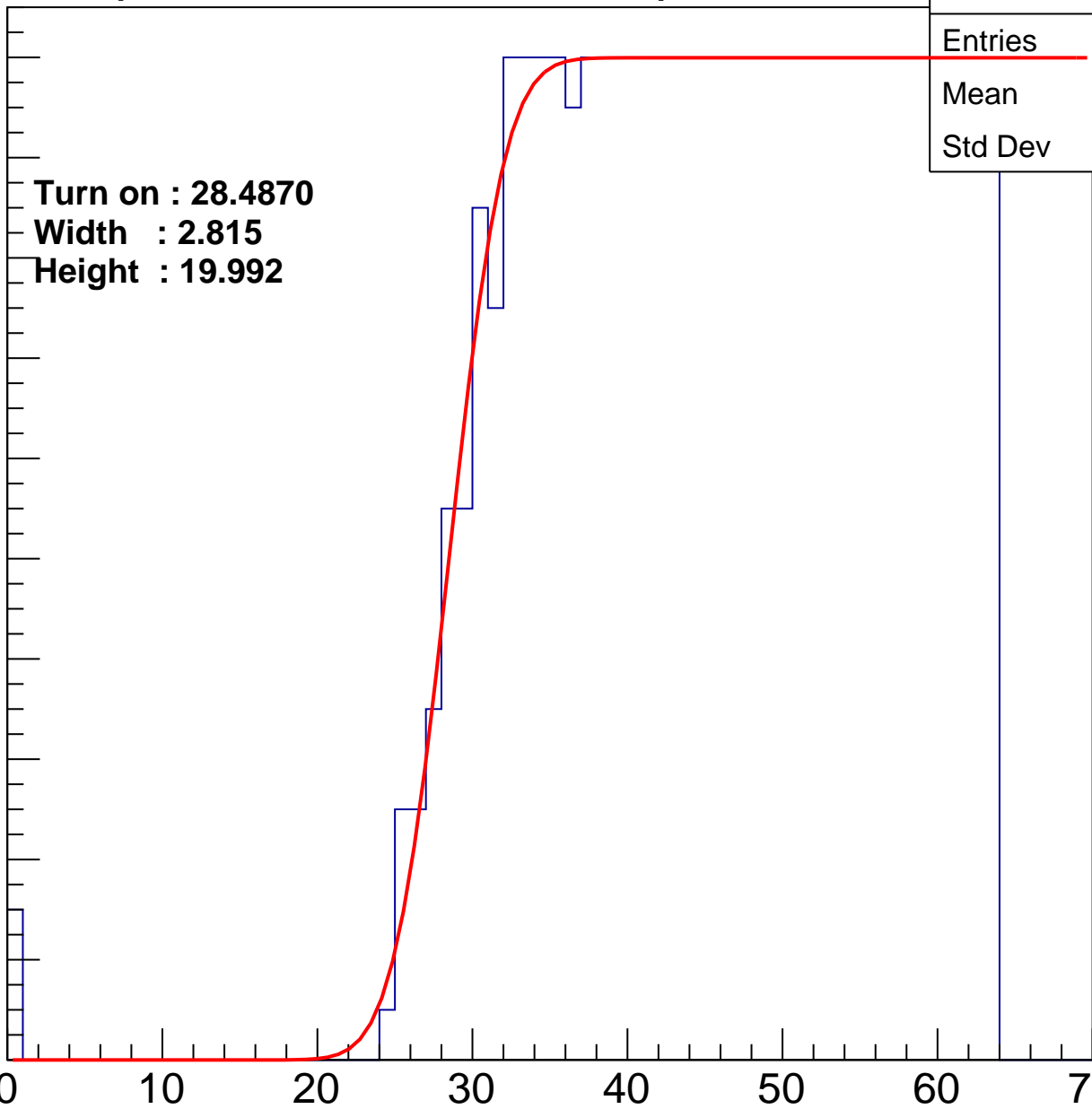
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4870
Width : 2.815
Height : 19.992

Entries	714
Mean	45.43
Std Dev	10.83

ampl



B1L001S, U25-ch100

calib_packv5_042523_0143.root, FC#2, port C2

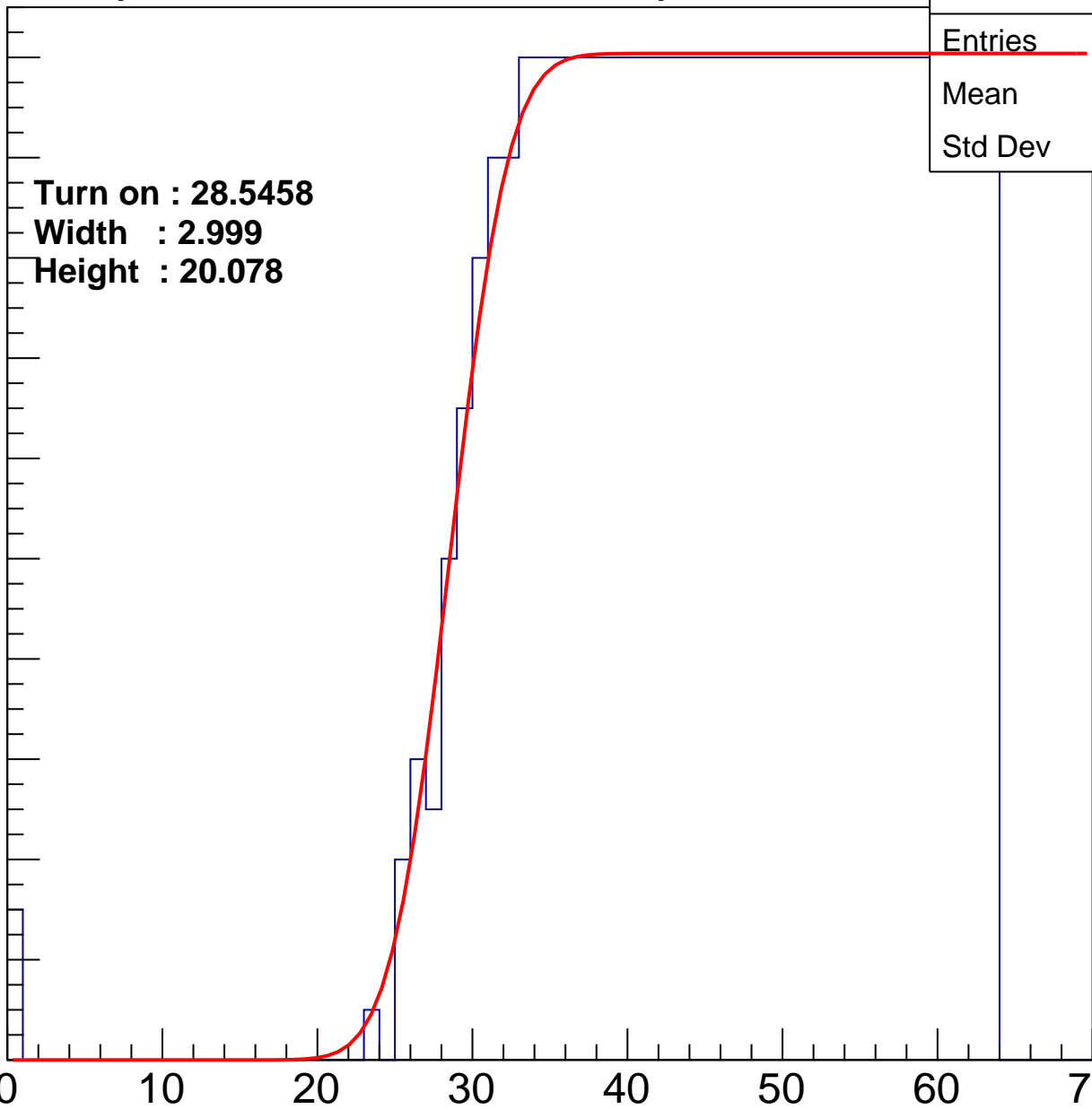
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5458
Width : 2.999
Height : 20.078

Entries	714
Mean	45.44
Std Dev	10.81

ampl



B1L001S, U25-ch101

calib_packv5_042523_0143.root, FC#2, port C2

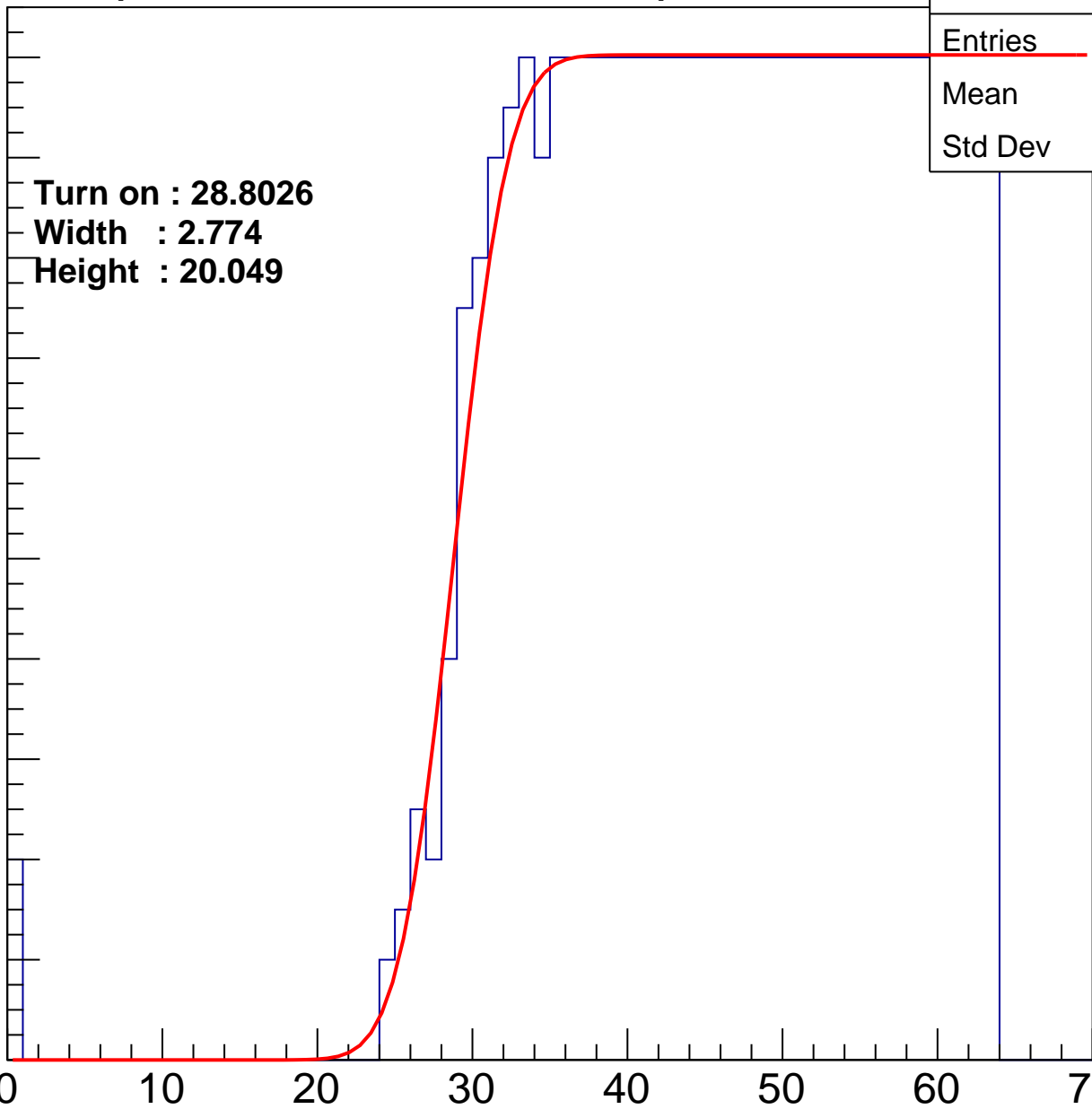
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.8026
Width : 2.774
Height : 20.049

Entries	712
Mean	45.45
Std Dev	10.9

ampl



B1L001S, U25-ch102

calib_packv5_042523_0143.root, FC#2, port C2

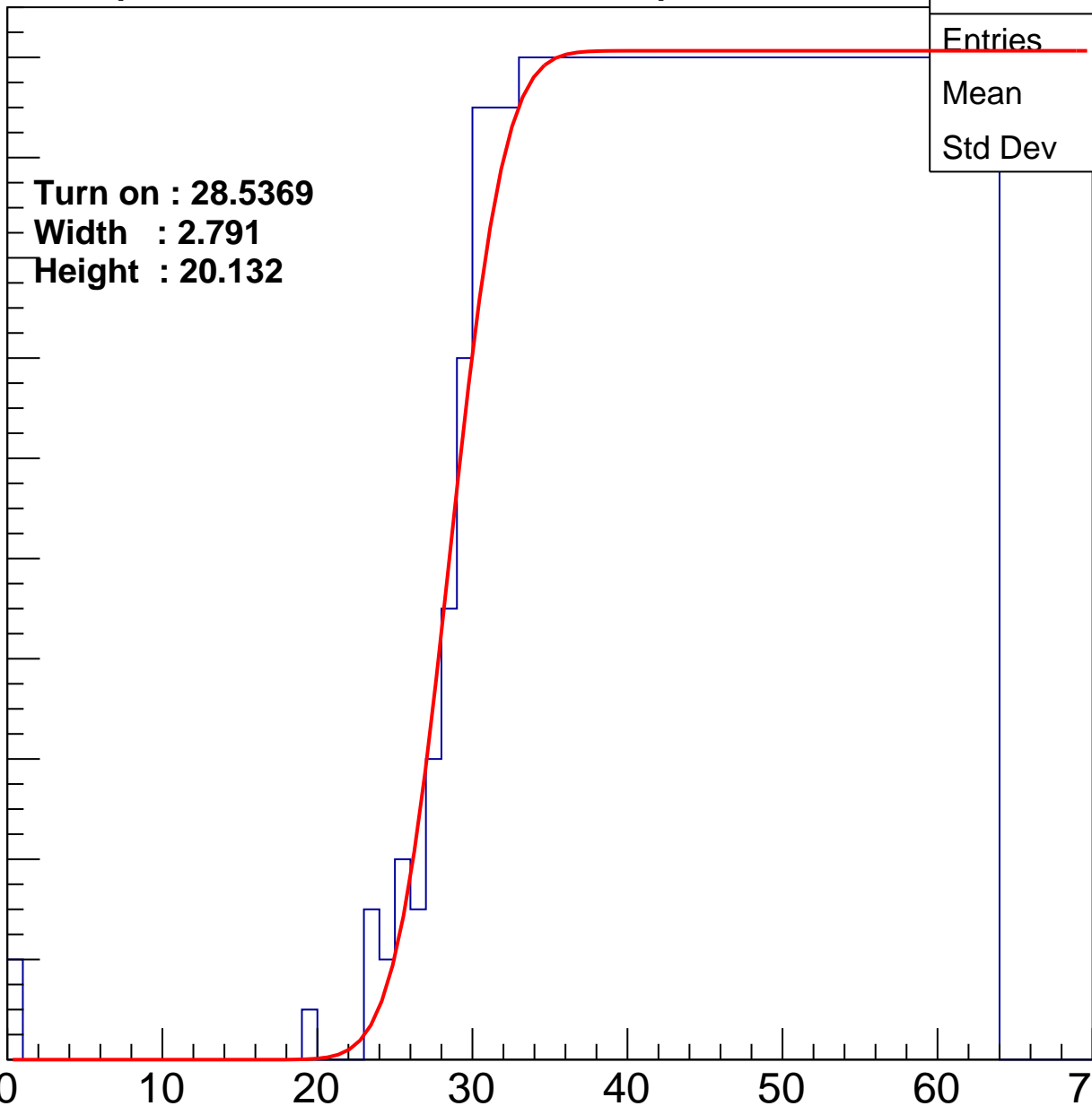
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5369
Width : 2.791
Height : 20.132

Entries	721
Mean	45.3
Std Dev	10.81

ampl



B1L001S, U25-ch103

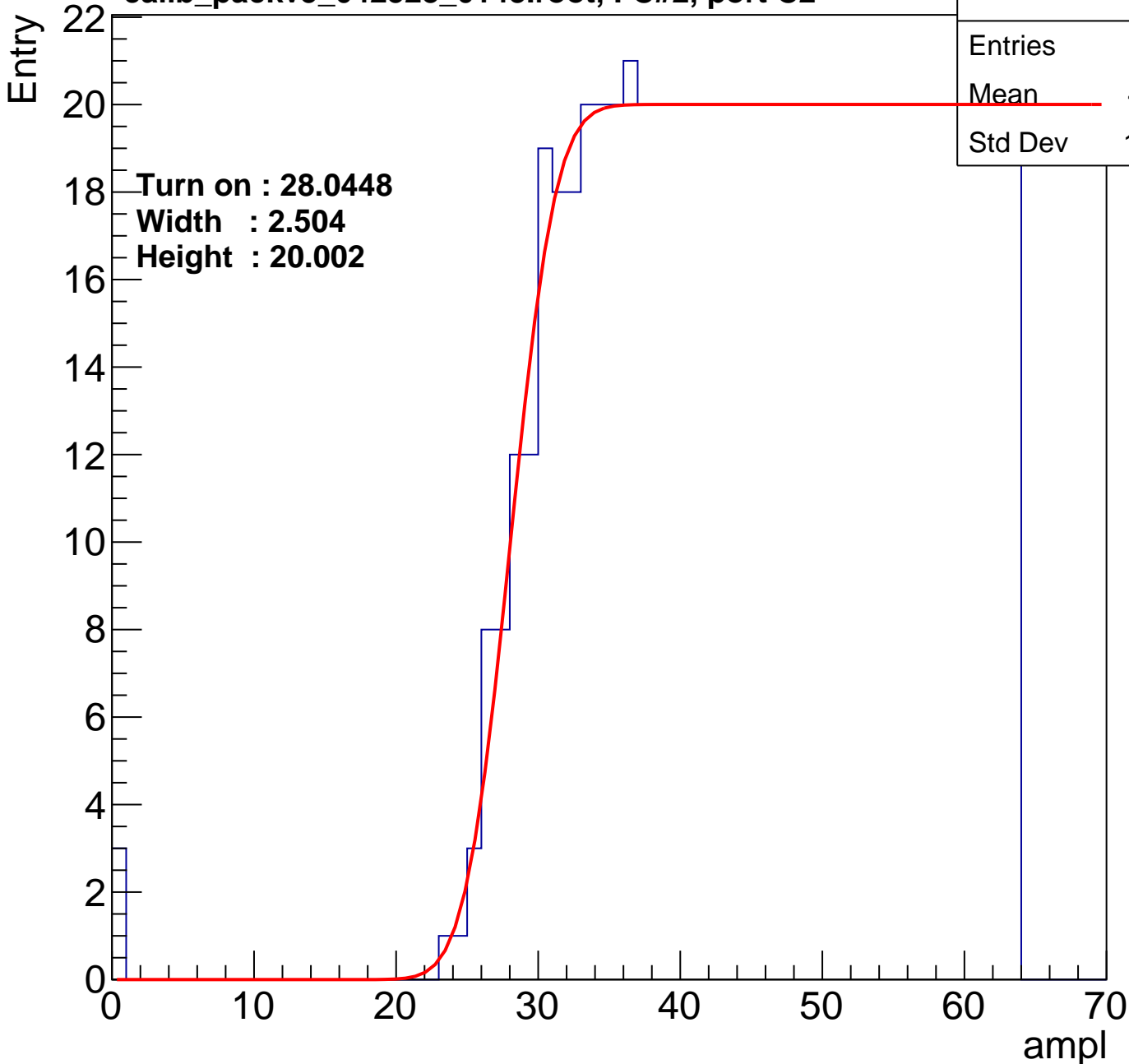
calib_packv5_042523_0143.root, FC#2, port C2

Entries	724
Mean	45.21
Std Dev	10.92

Turn on : 28.0448

Width : 2.504

Height : 20.002



B1L001S, U25-ch104

calib_packv5_042523_0143.root, FC#2, port C2

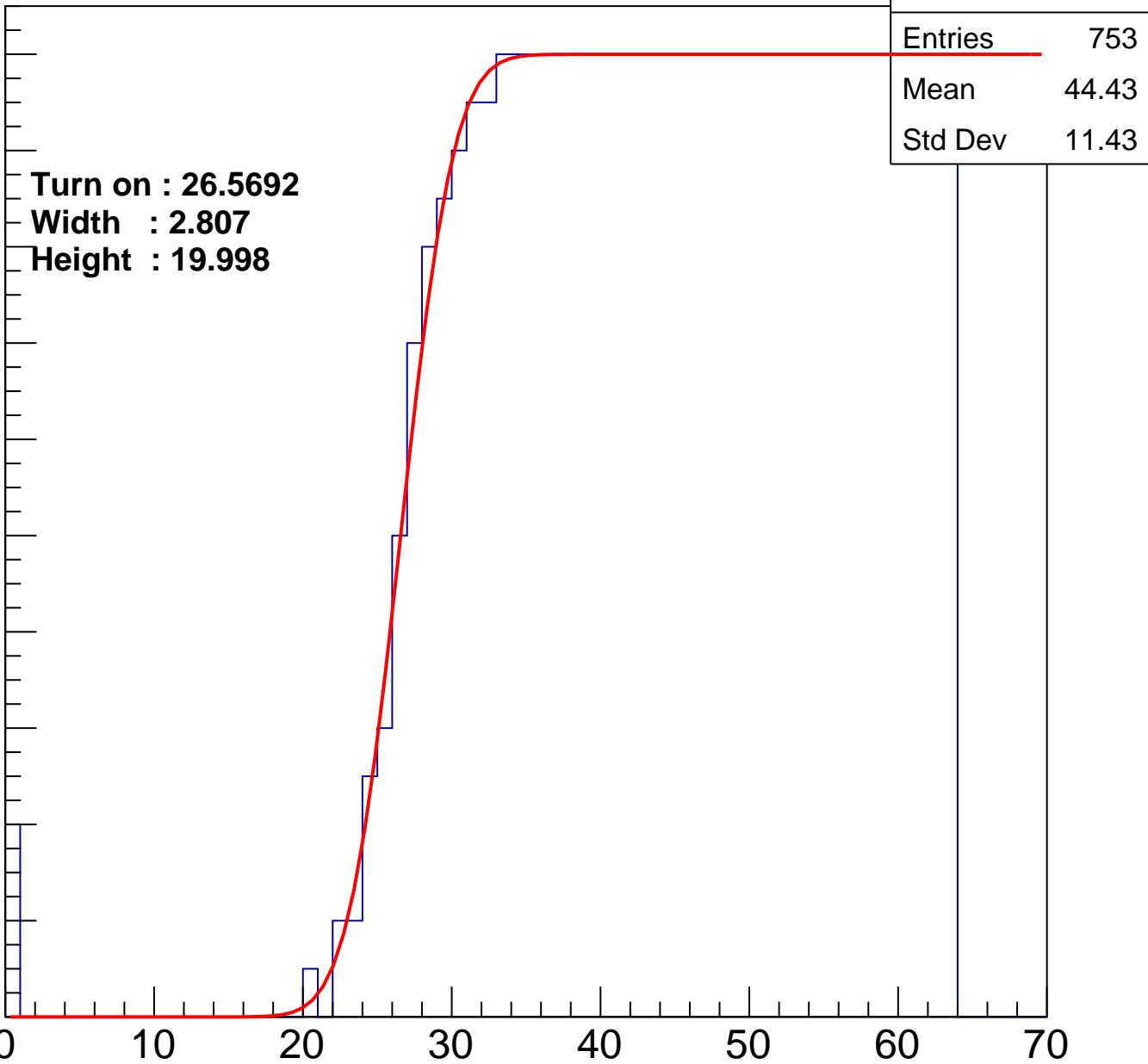
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5692
Width : 2.807
Height : 19.998

Entries	753
Mean	44.43
Std Dev	11.43

ampl



B1L001S, U25-ch105

calib_packv5_042523_0143.root, FC#2, port C2

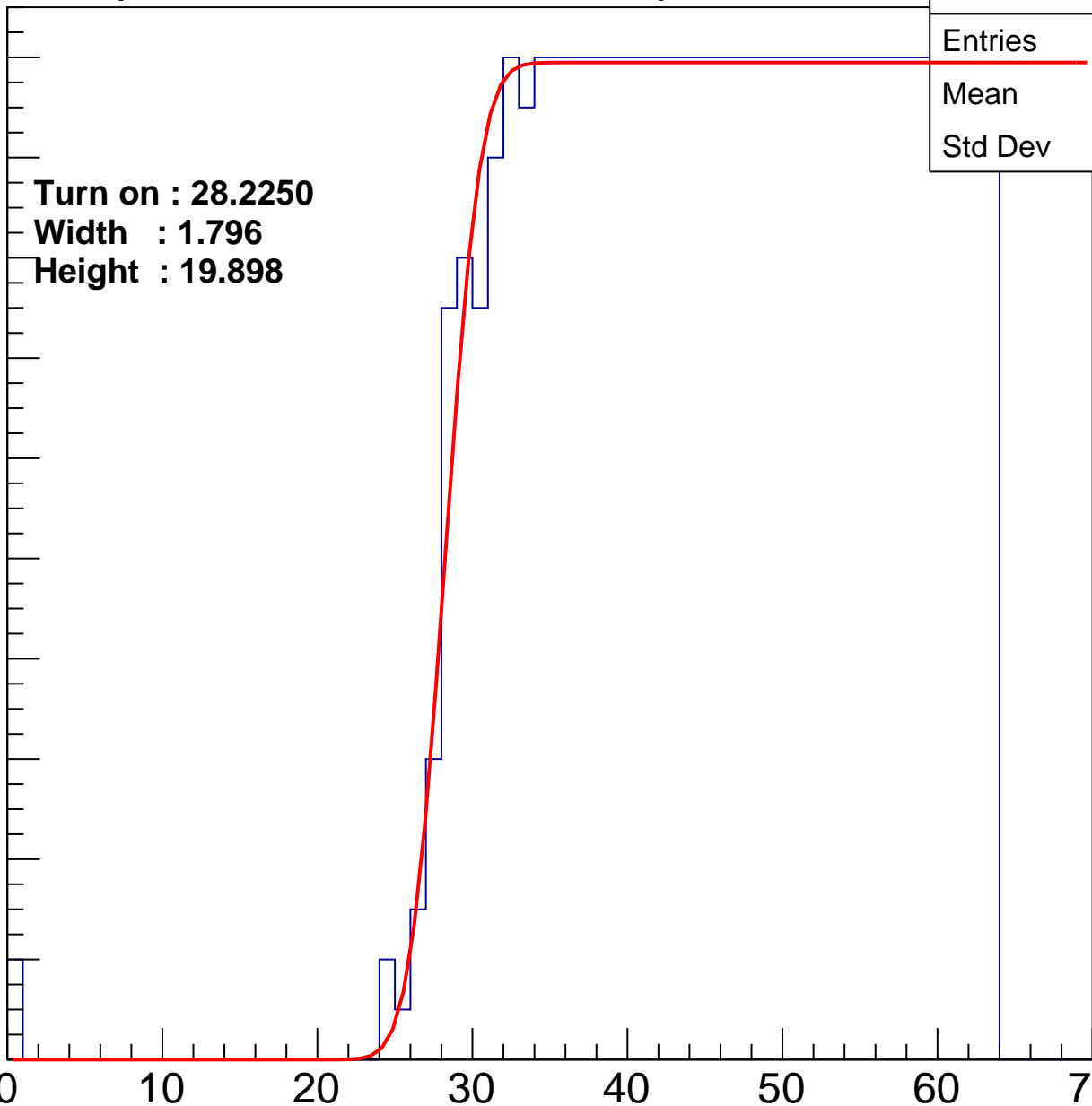
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.2250
Width : 1.796
Height : 19.898

Entries	717
Mean	45.43
Std Dev	10.69

ampl



B1L001S, U25-ch106

calib_packv5_042523_0143.root, FC#2, port C2

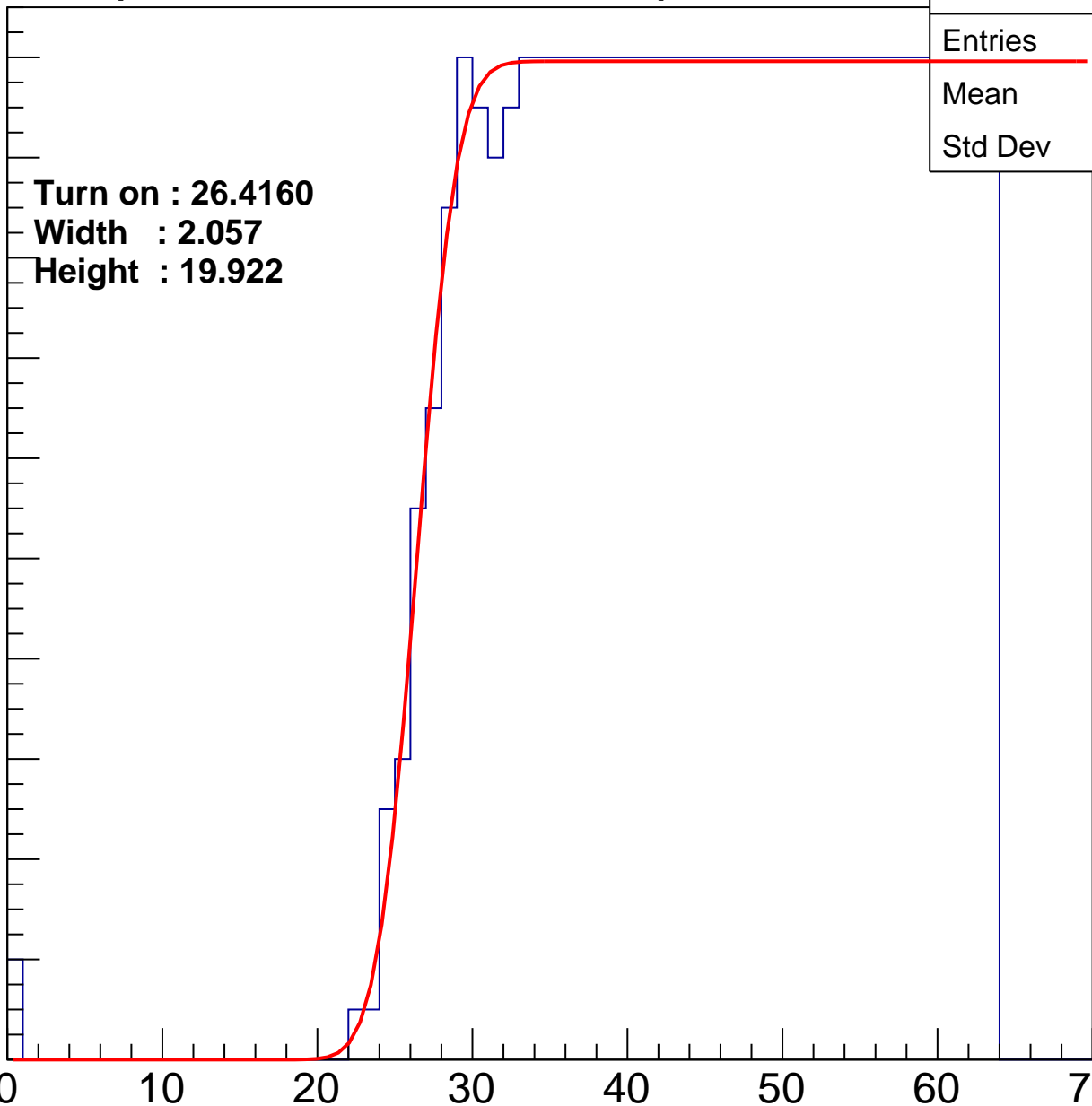
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4160
Width : 2.057
Height : 19.922

Entries	752
Mean	44.55
Std Dev	11.18

ampl



B1L001S, U25-ch107

calib_packv5_042523_0143.root, FC#2, port C2

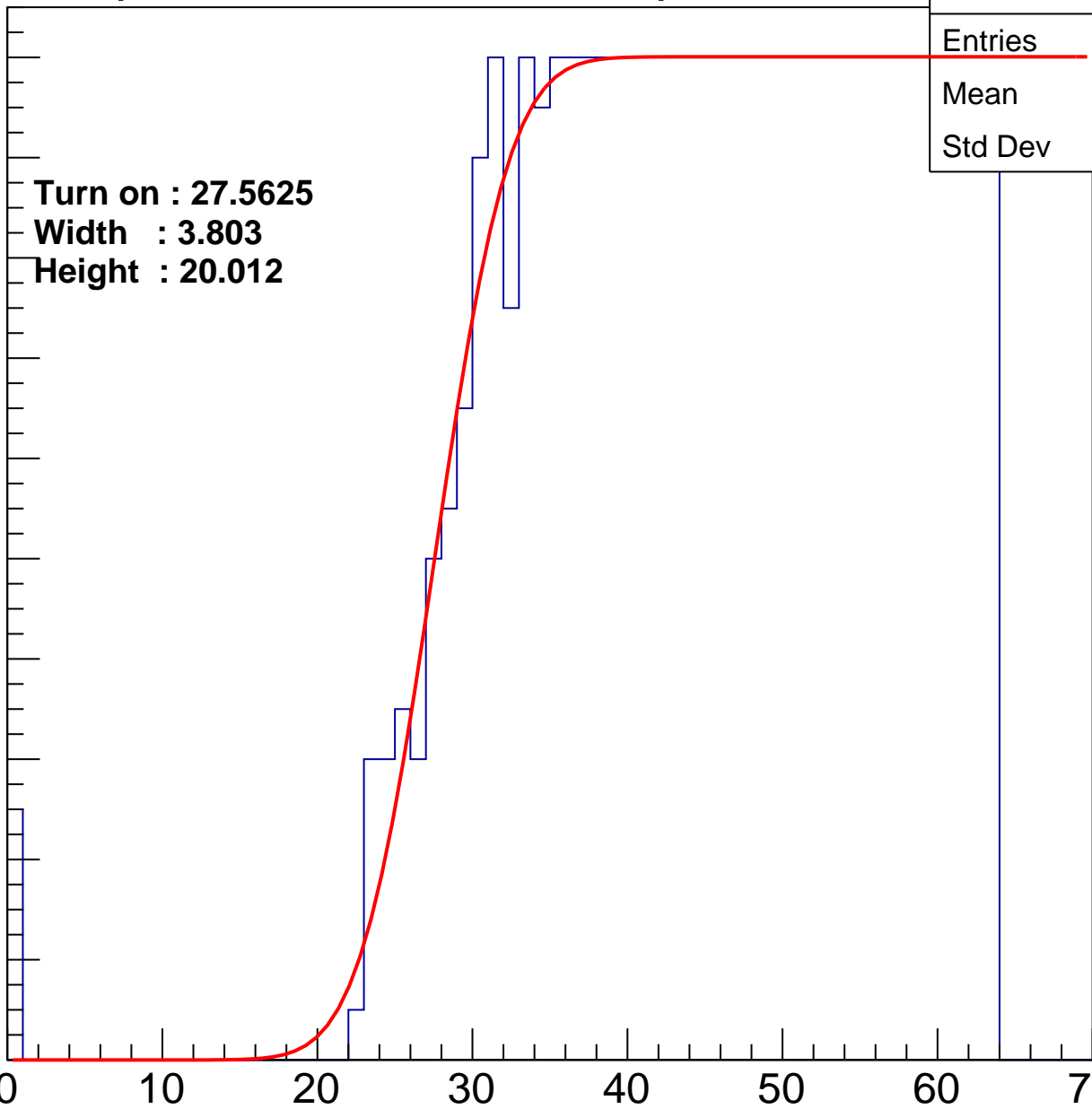
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5625
Width : 3.803
Height : 20.012

Entries	737
Mean	44.72
Std Dev	11.44

ampl



B1L001S, U25-ch108

calib_packv5_042523_0143.root, FC#2, port C2

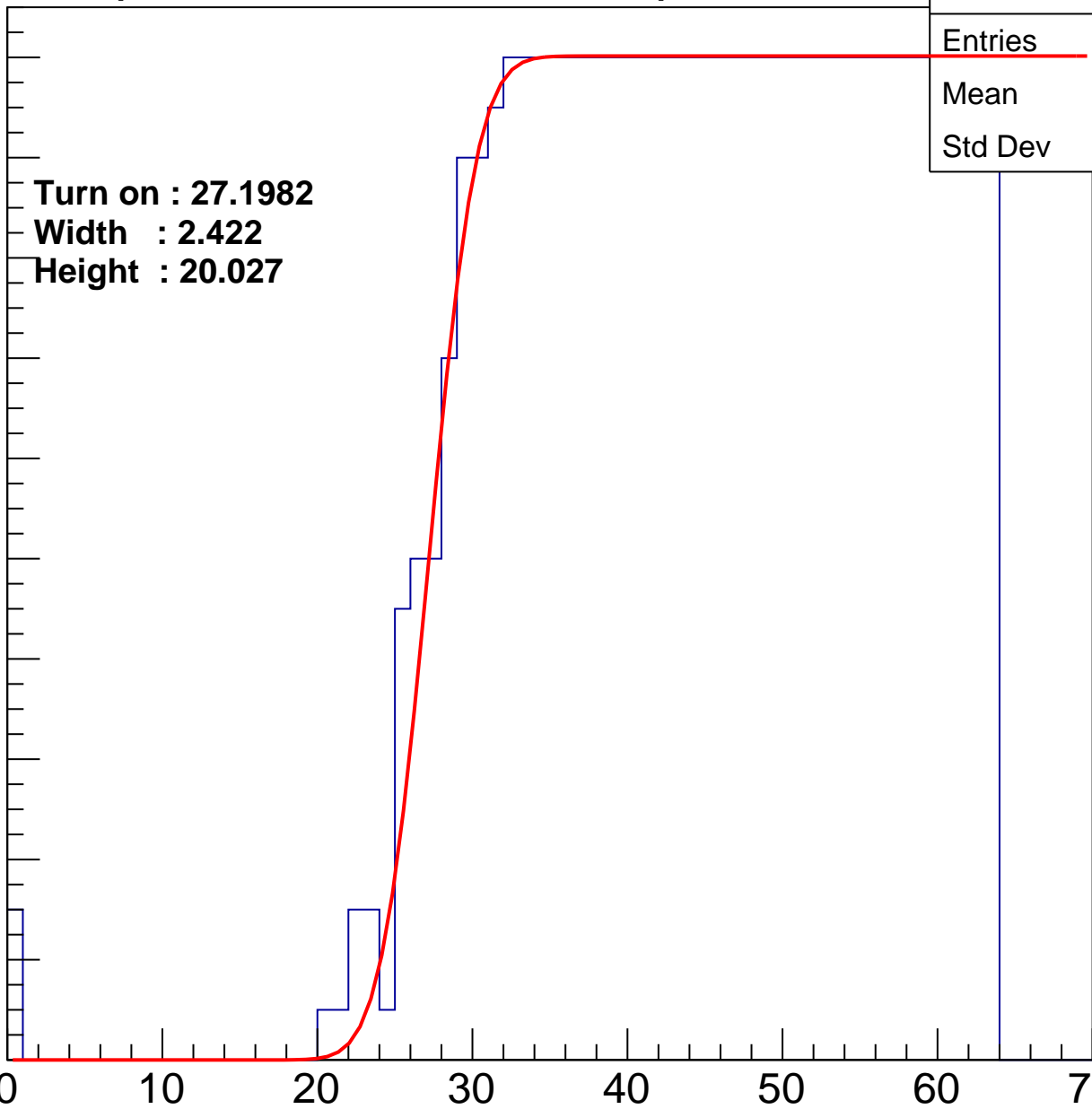
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1982
Width : 2.422
Height : 20.027

Entries	750
Mean	44.53
Std Dev	11.32

ampl



B1L001S, U25-ch109

calib_packv5_042523_0143.root, FC#2, port C2

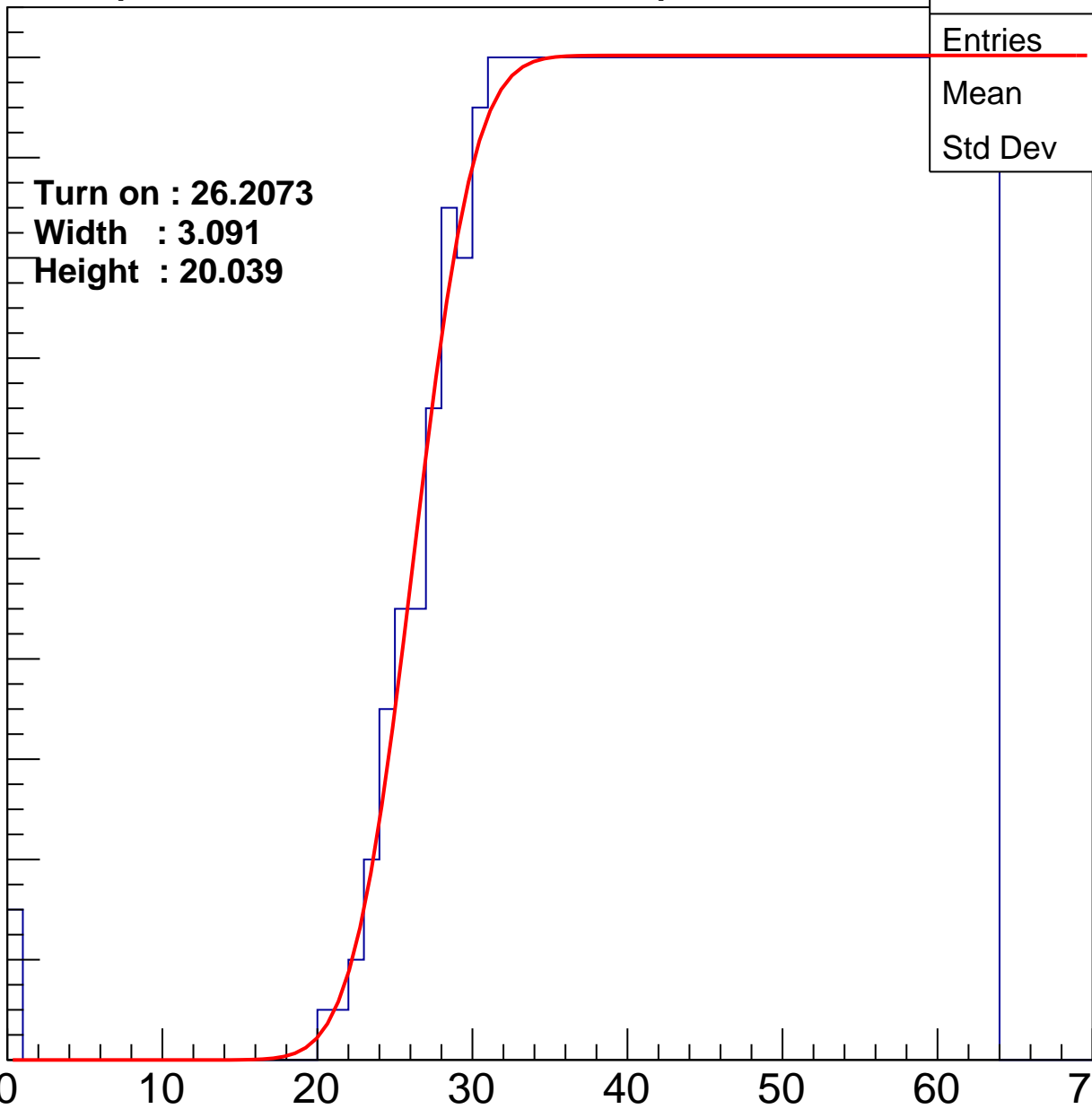
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2073
Width : 3.091
Height : 20.039

Entries	761
Mean	44.26
Std Dev	11.45

ampl



B1L001S, U25-ch110

calib_packv5_042523_0143.root, FC#2, port C2

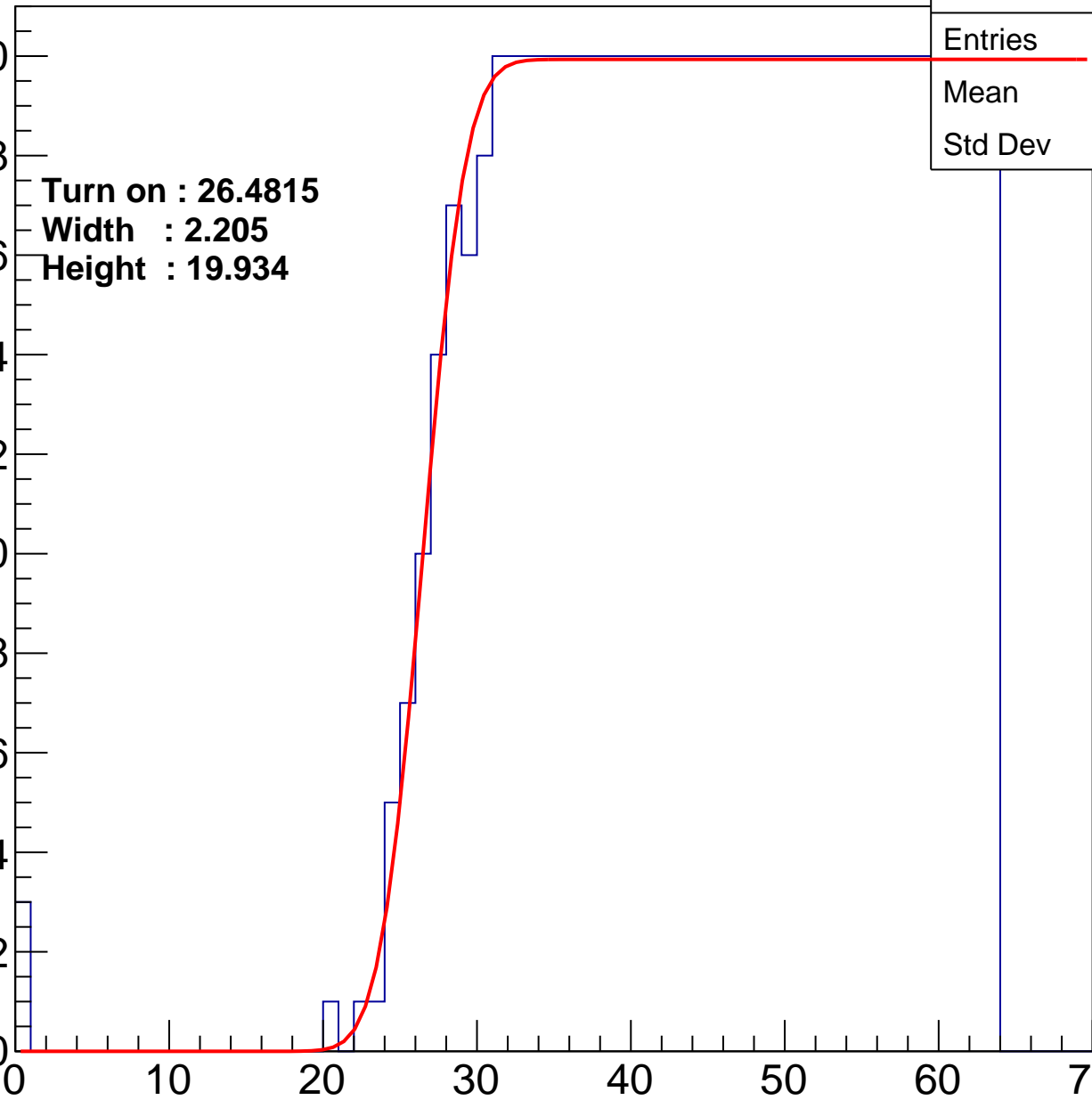
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4815
Width : 2.205
Height : 19.934

Entries	753
Mean	44.49
Std Dev	11.3

ampl



B1L001S, U25-ch111

calib_packv5_042523_0143.root, FC#2, port C2

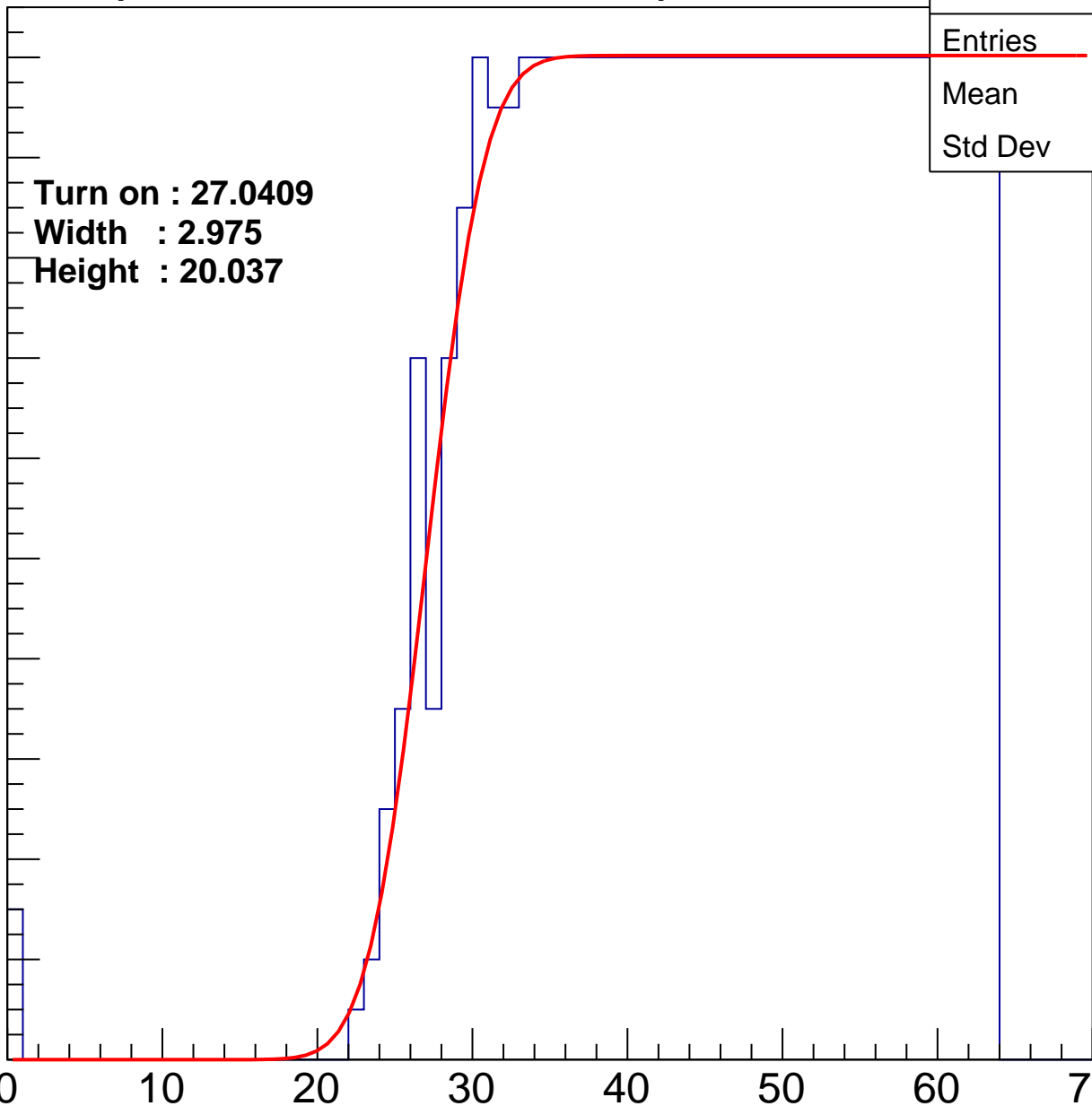
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0409
Width : 2.975
Height : 20.037

Entries	748
Mean	44.6
Std Dev	11.26

ampl



B1L001S, U25-ch112

calib_packv5_042523_0143.root, FC#2, port C2

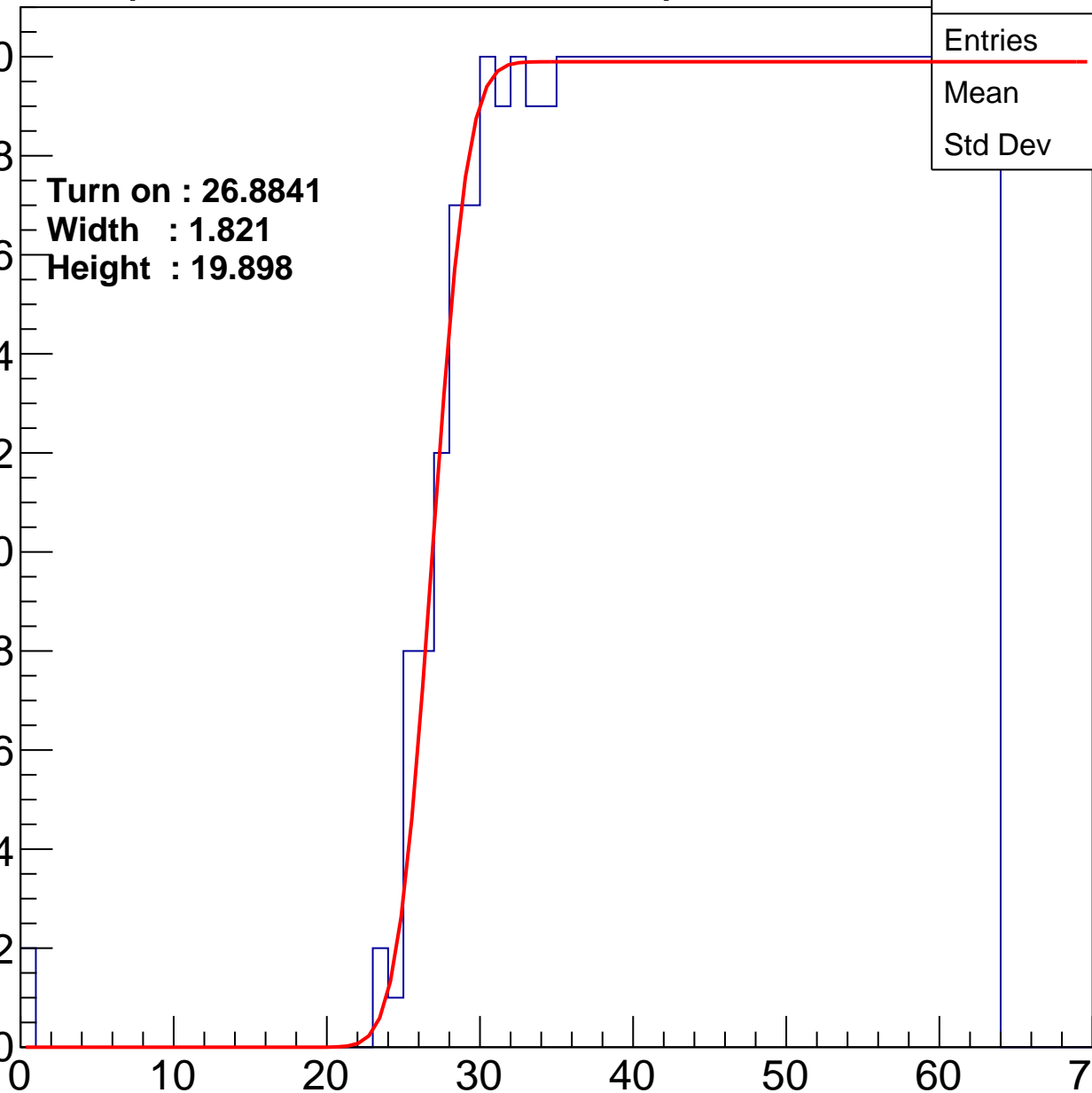
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8841
Width : 1.821
Height : 19.898

Entries	744
Mean	44.75
Std Dev	11.07

ampl



B1L001S, U25-ch113

calib_packv5_042523_0143.root, FC#2, port C2

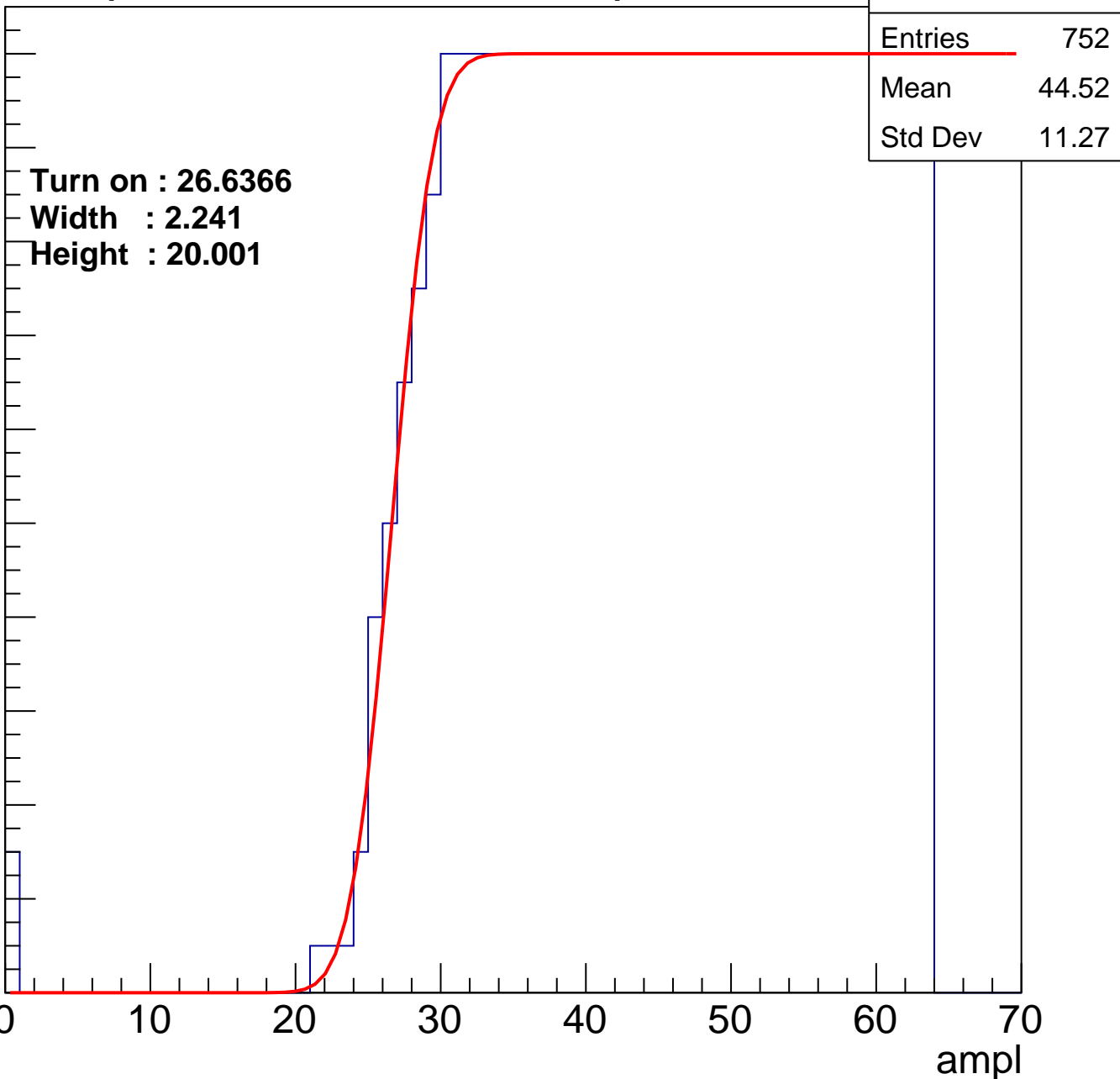
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6366
Width : 2.241
Height : 20.001

Entries	752
Mean	44.52
Std Dev	11.27

ampl



B1L001S, U25-ch114

calib_packv5_042523_0143.root, FC#2, port C2

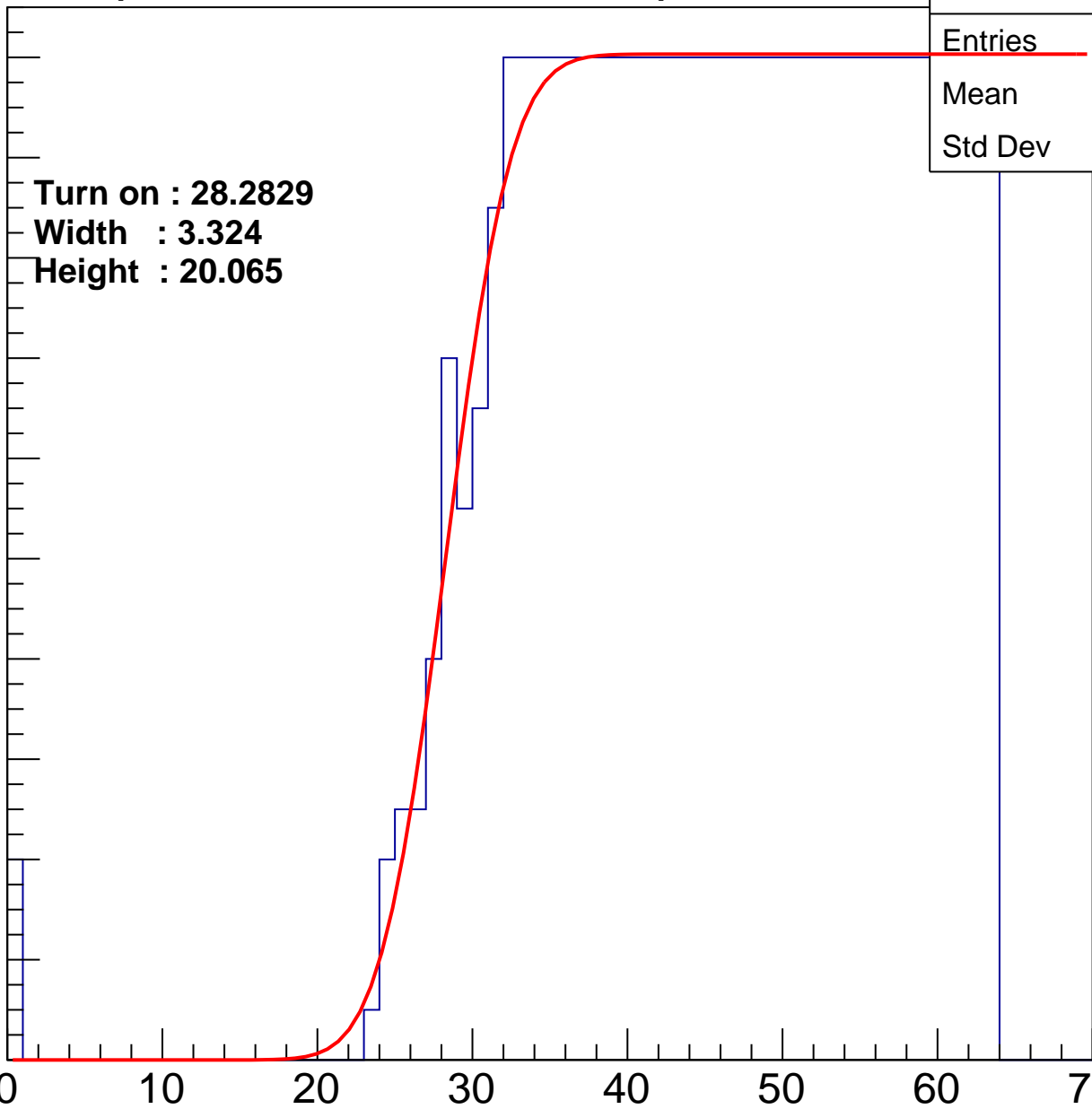
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.2829
Width : 3.324
Height : 20.065

Entries	722
Mean	45.18
Std Dev	11.07

ampl



B1L001S, U25-ch115

calib_packv5_042523_0143.root, FC#2, port C2

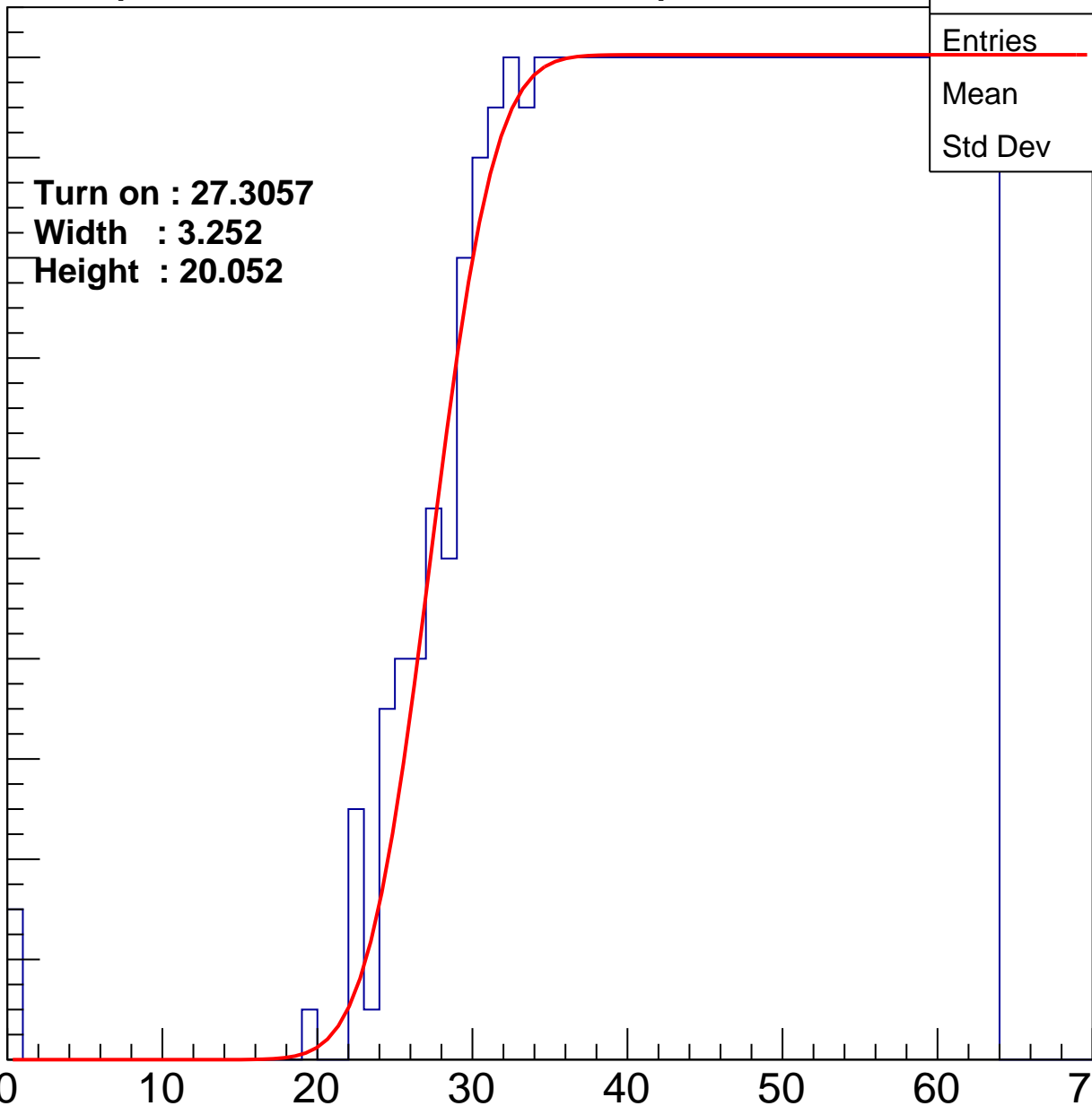
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3057
Width : 3.252
Height : 20.052

Entries	746
Mean	44.59
Std Dev	11.32

ampl



B1L001S, U25-ch116

calib_packv5_042523_0143.root, FC#2, port C2

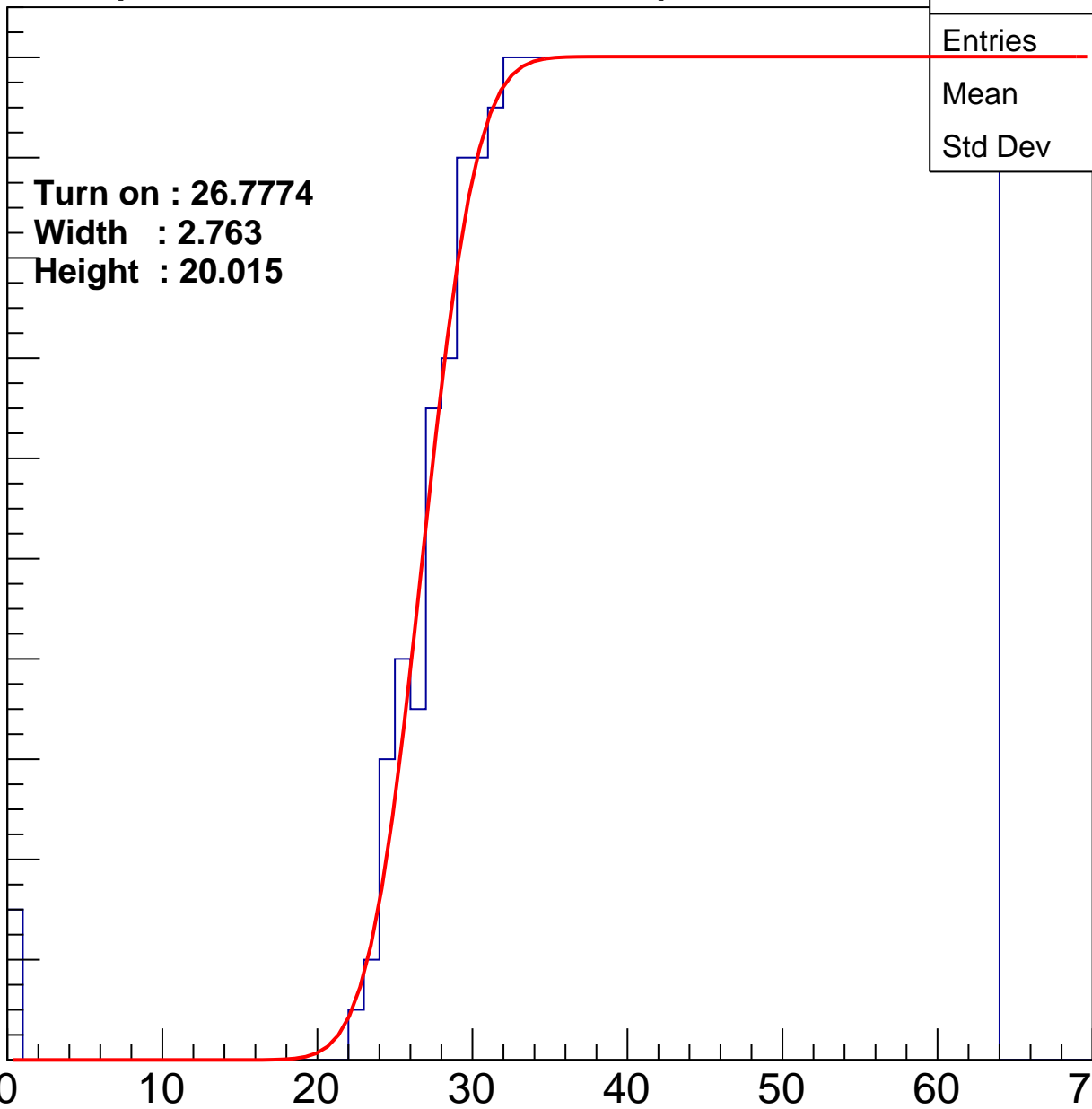
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7774
Width : 2.763
Height : 20.015

Entries	749
Mean	44.58
Std Dev	11.26

ampl



B1L001S, U25-ch117

calib_packv5_042523_0143.root, FC#2, port C2

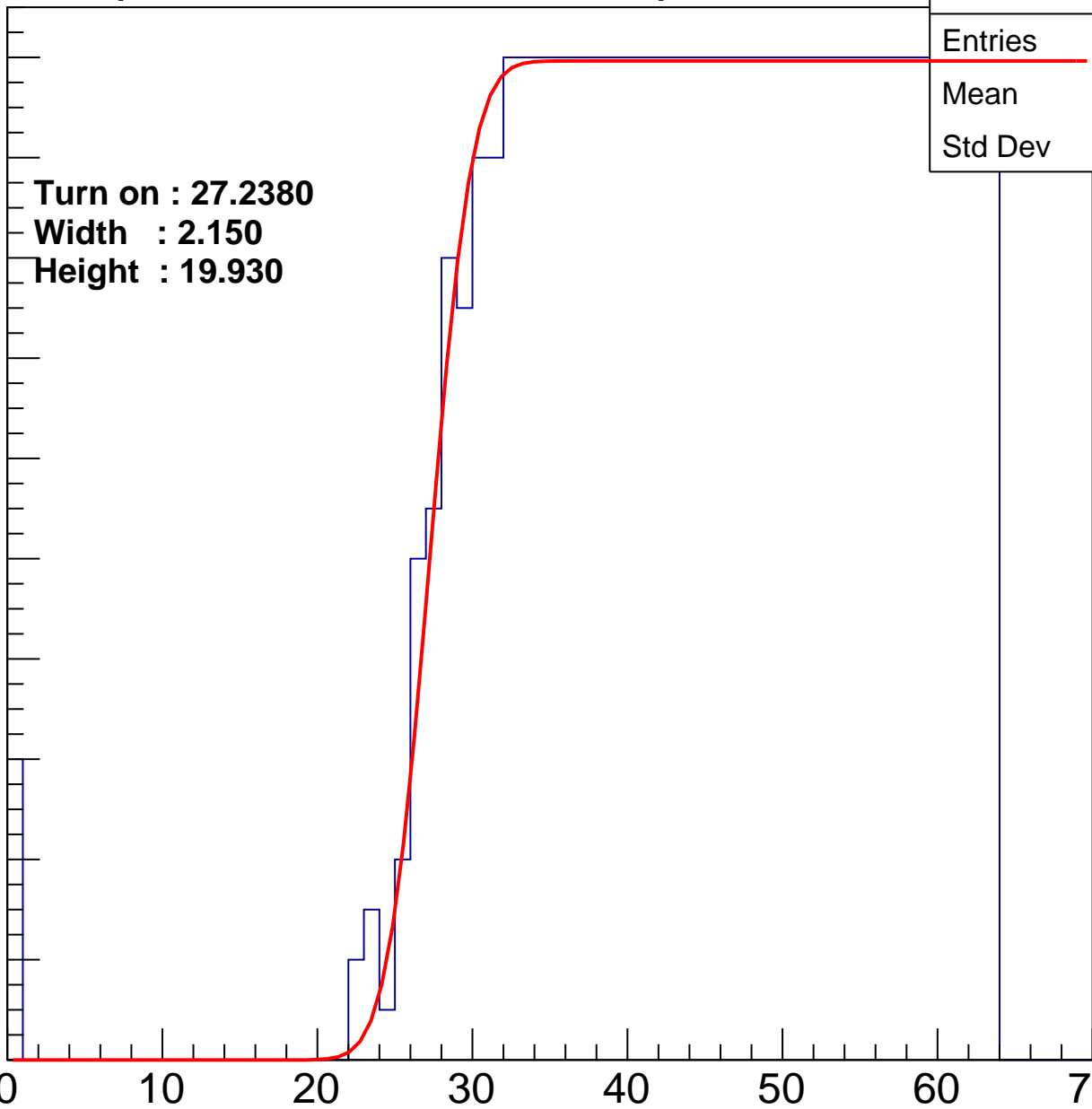
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2380
Width : 2.150
Height : 19.930

Entries	744
Mean	44.59
Std Dev	11.5

ampl



B1L001S, U25-ch118

calib_packv5_042523_0143.root, FC#2, port C2

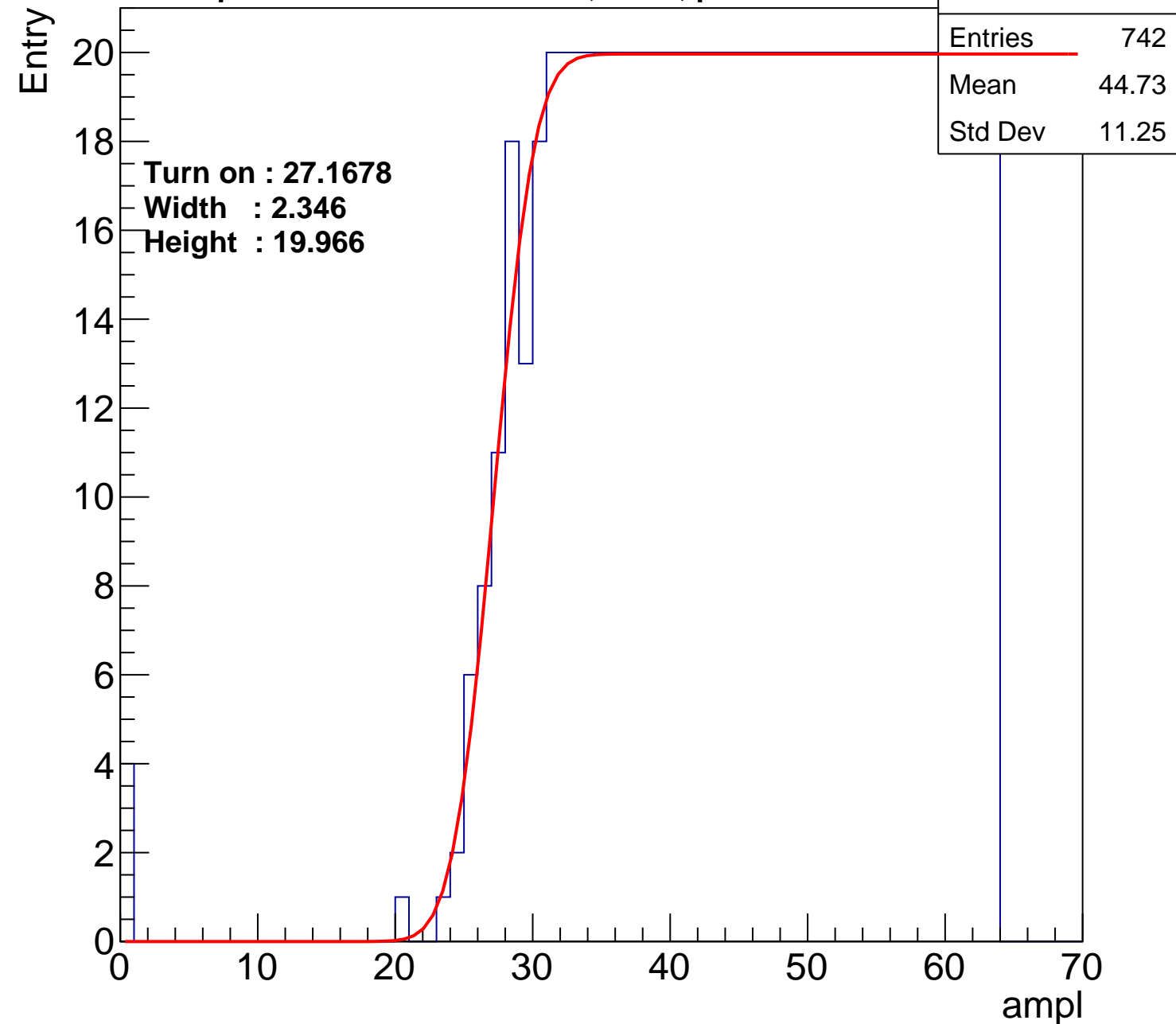
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1678
Width : 2.346
Height : 19.966

Entries	742
Mean	44.73
Std Dev	11.25

ampl



B1L001S, U25-ch119

calib_packv5_042523_0143.root, FC#2, port C2

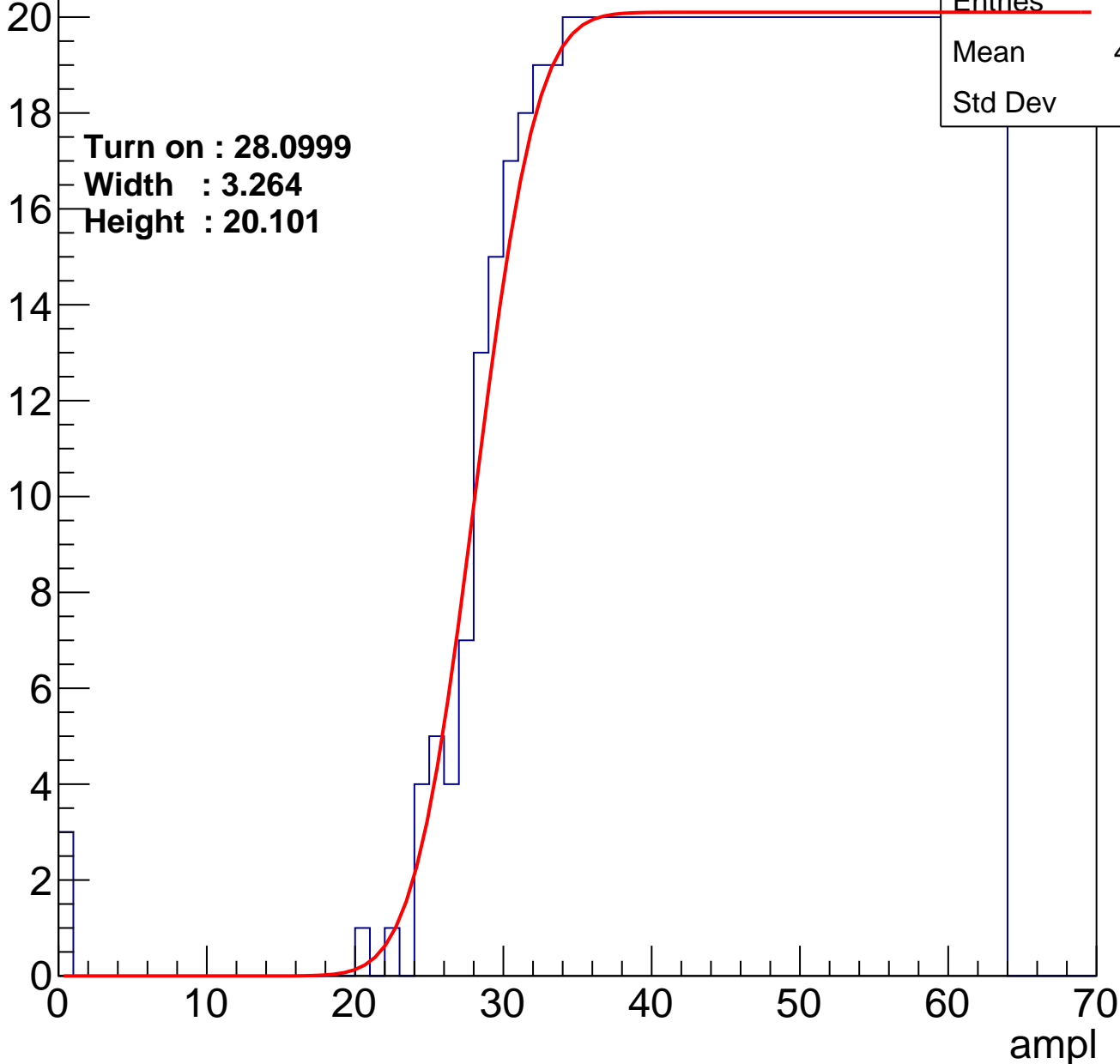
Entries	726
Mean	45.12
Std Dev	11

Turn on : 28.0999

Width : 3.264

Height : 20.101

Entry



B1L001S, U25-ch120

calib_packv5_042523_0143.root, FC#2, port C2

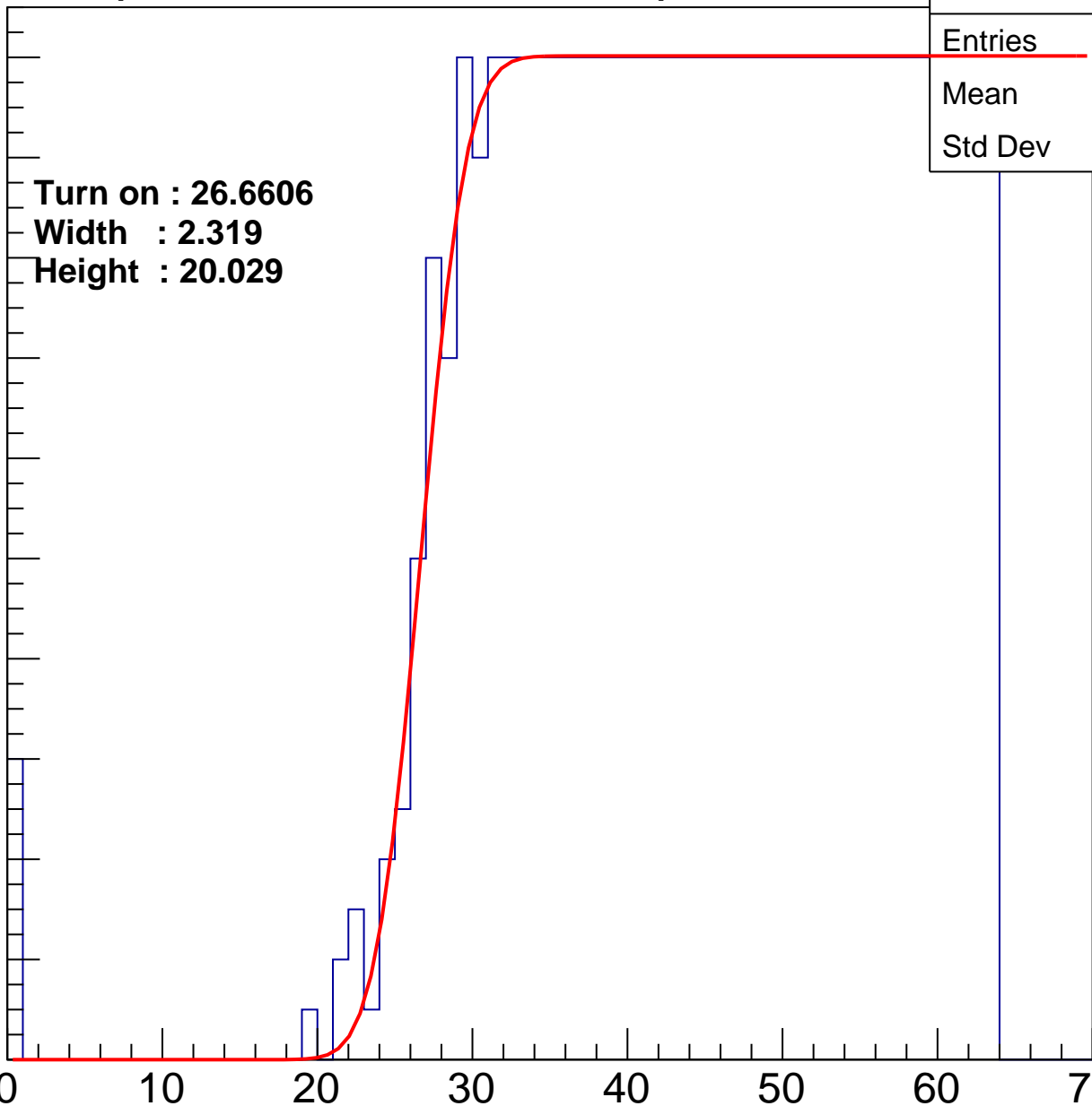
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6606
Width : 2.319
Height : 20.029

Entries	760
Mean	44.2
Std Dev	11.69

ampl



B1L001S, U25-ch121

calib_packv5_042523_0143.root, FC#2, port C2

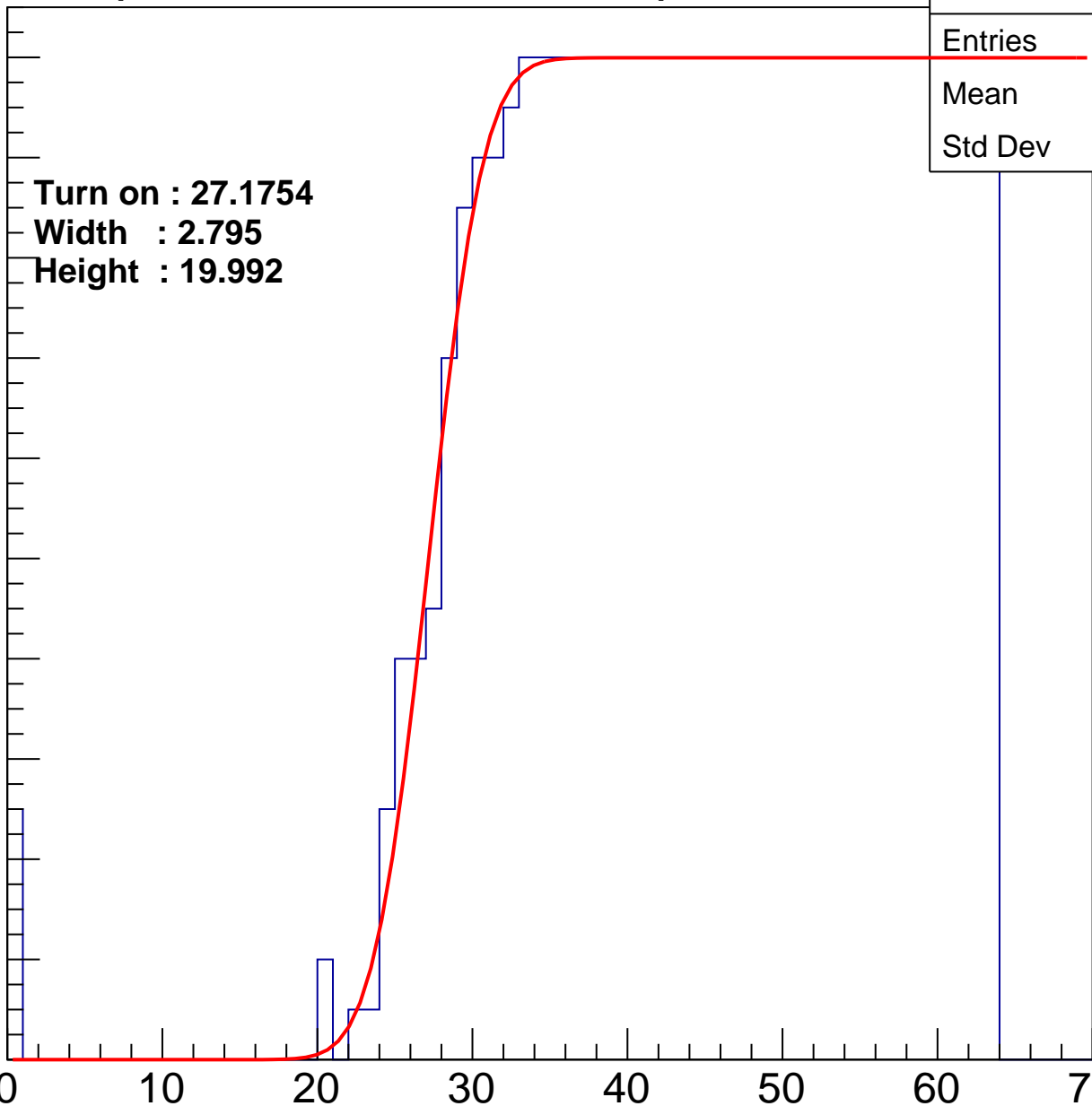
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1754
Width : 2.795
Height : 19.992

Entries	745
Mean	44.57
Std Dev	11.46

ampl



B1L001S, U25-ch122

calib_packv5_042523_0143.root, FC#2, port C2

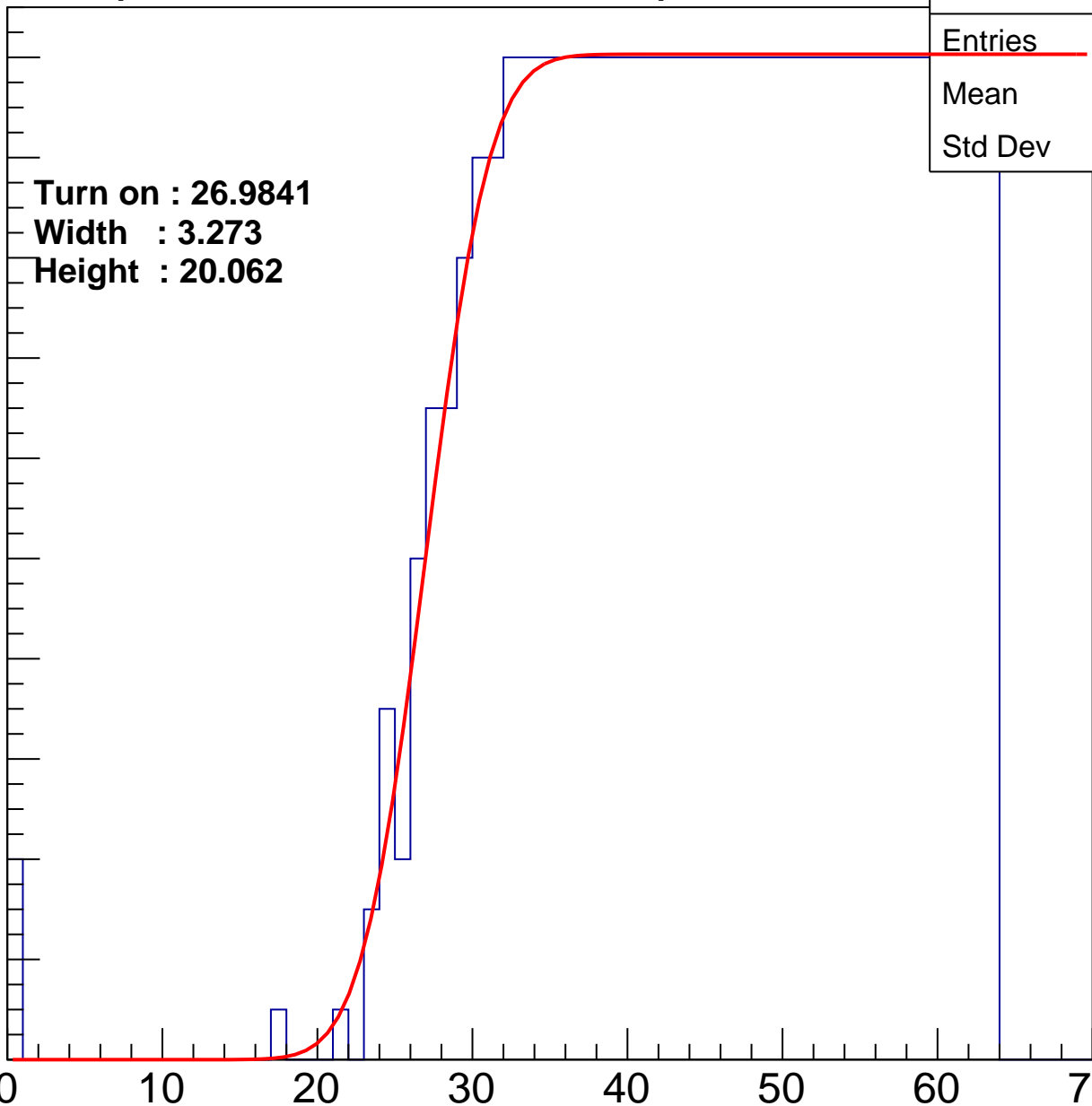
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9841
Width : 3.273
Height : 20.062

Entries	748
Mean	44.53
Std Dev	11.4

ampl



B1L001S, U25-ch123

calib_packv5_042523_0143.root, FC#2, port C2

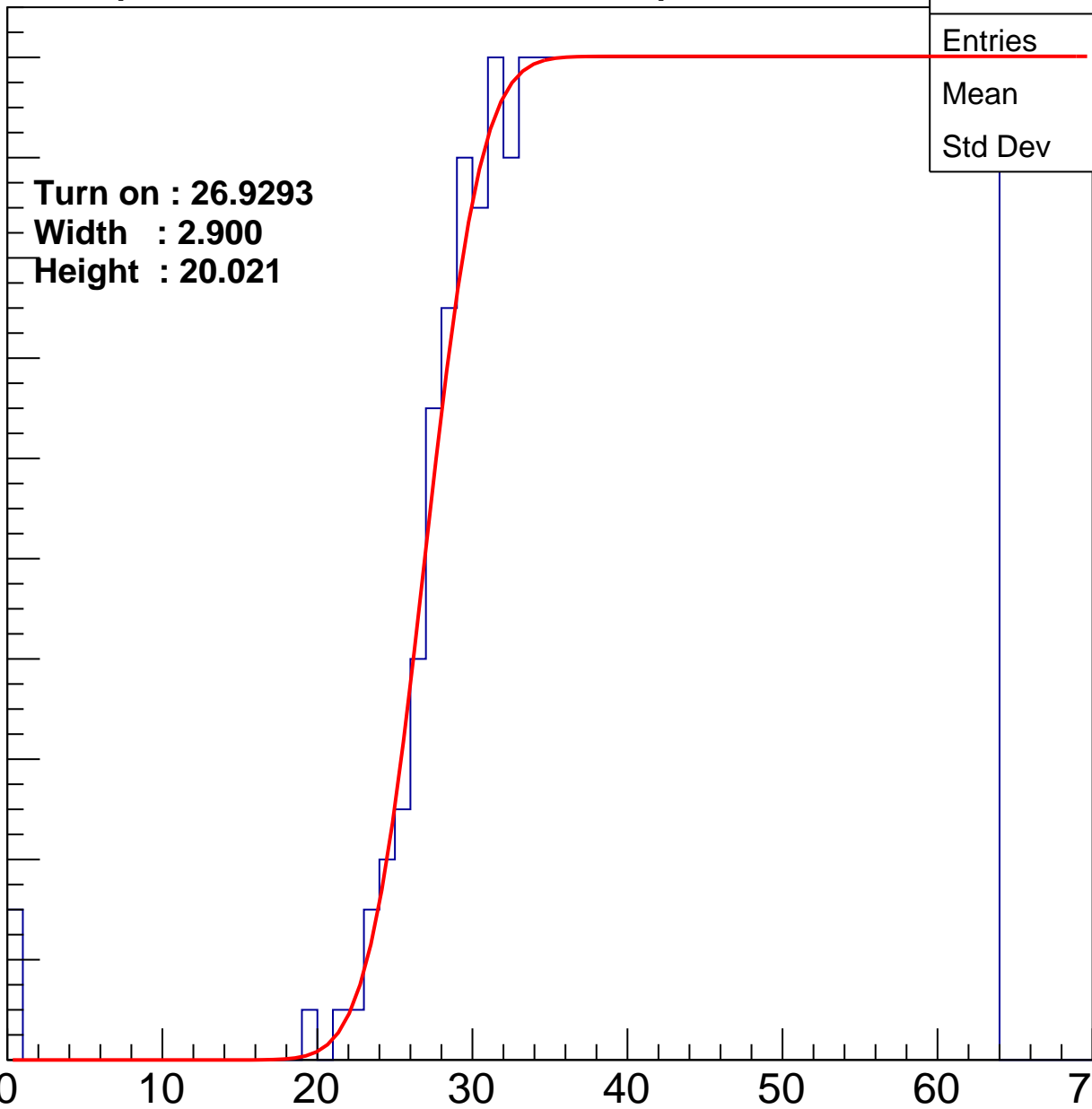
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9293
Width : 2.900
Height : 20.021

Entries	747
Mean	44.6
Std Dev	11.28

ampl



B1L001S, U25-ch124

calib_packv5_042523_0143.root, FC#2, port C2

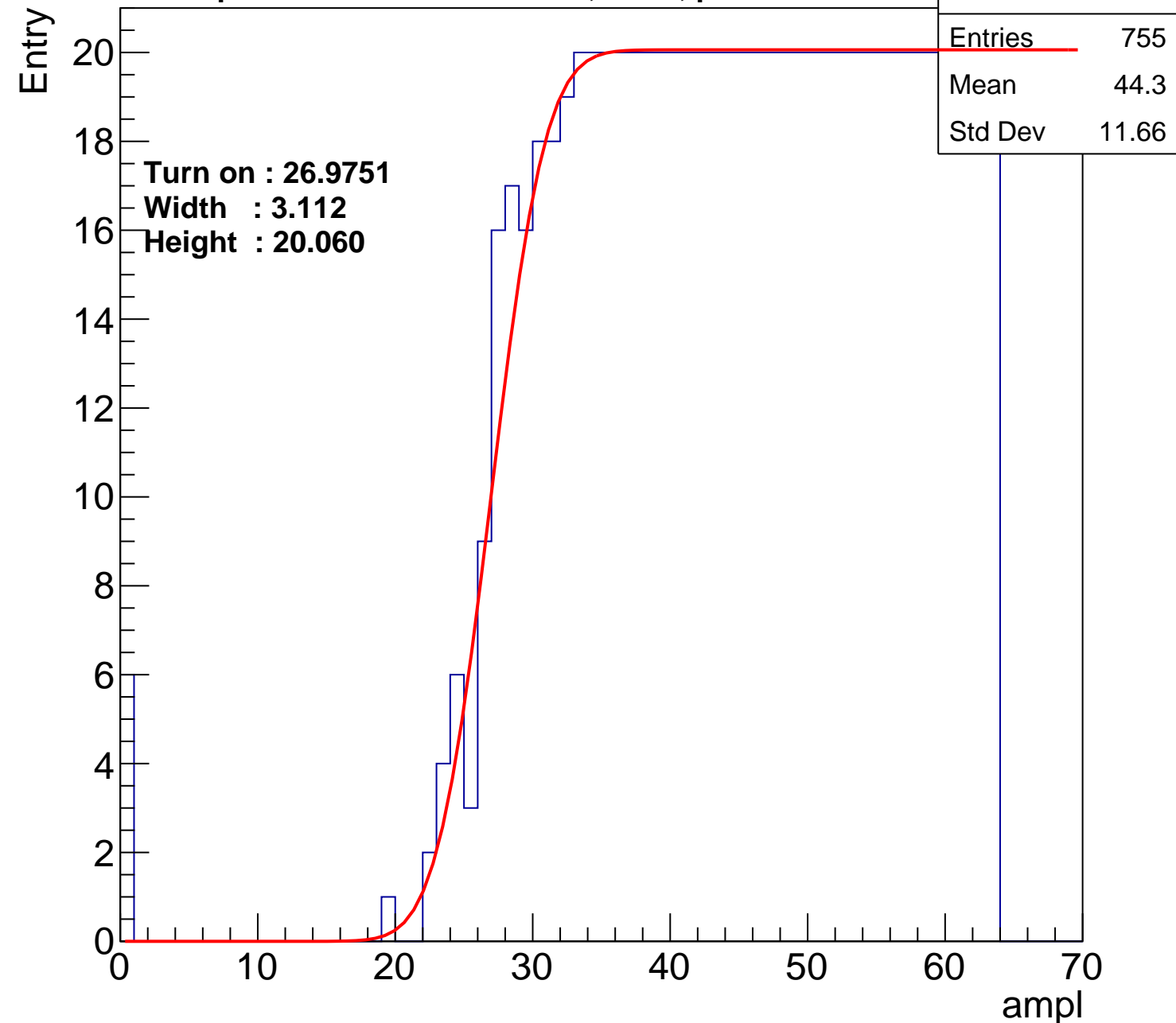
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9751
Width : 3.112
Height : 20.060

Entries	755
Mean	44.3
Std Dev	11.66

ampl



B1L001S, U25-ch125

calib_packv5_042523_0143.root, FC#2, port C2

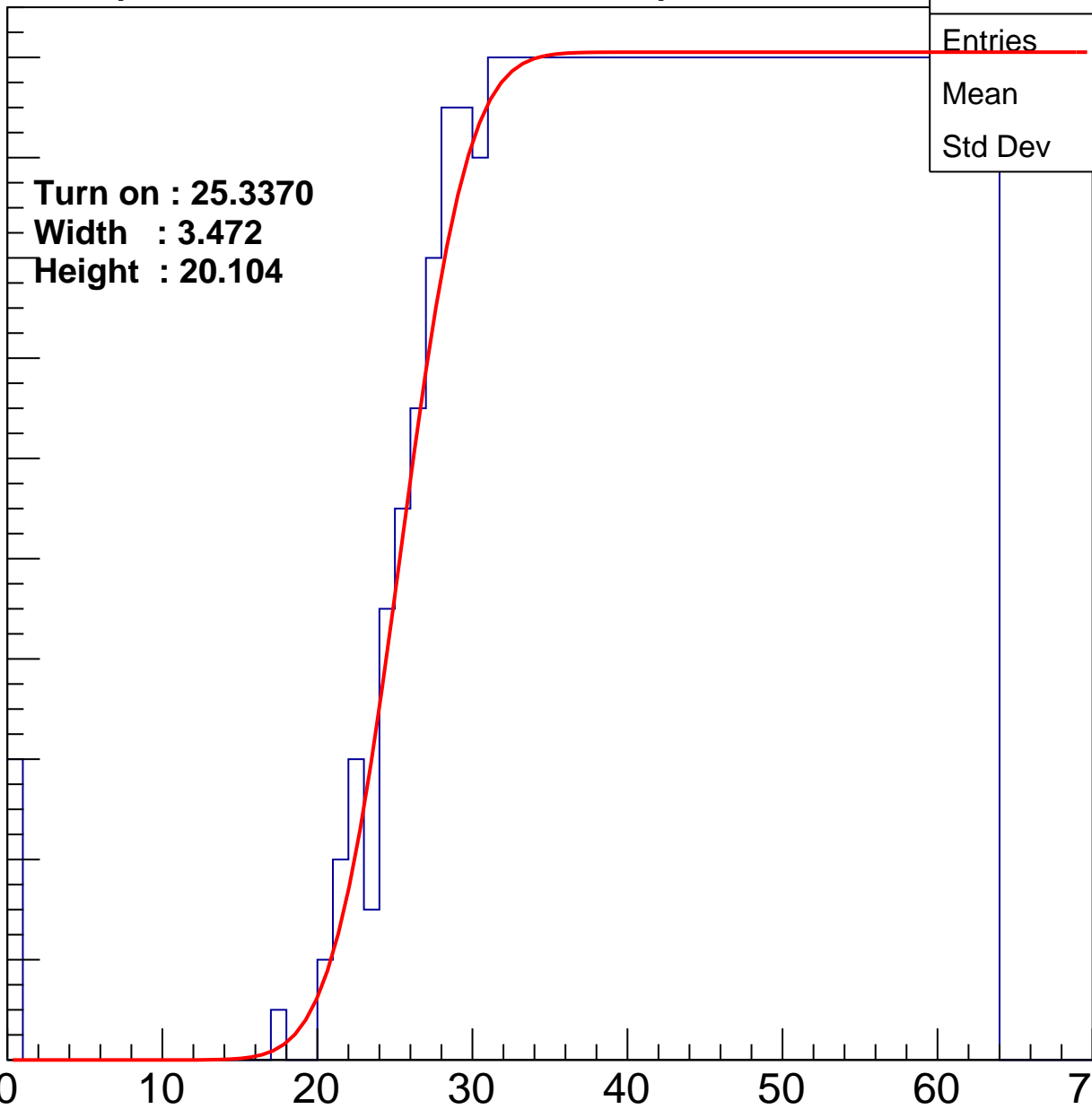
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3370
Width : 3.472
Height : 20.104

Entries	787
Mean	43.51
Std Dev	12.06

ampl



B1L001S, U25-ch126

calib_packv5_042523_0143.root, FC#2, port C2

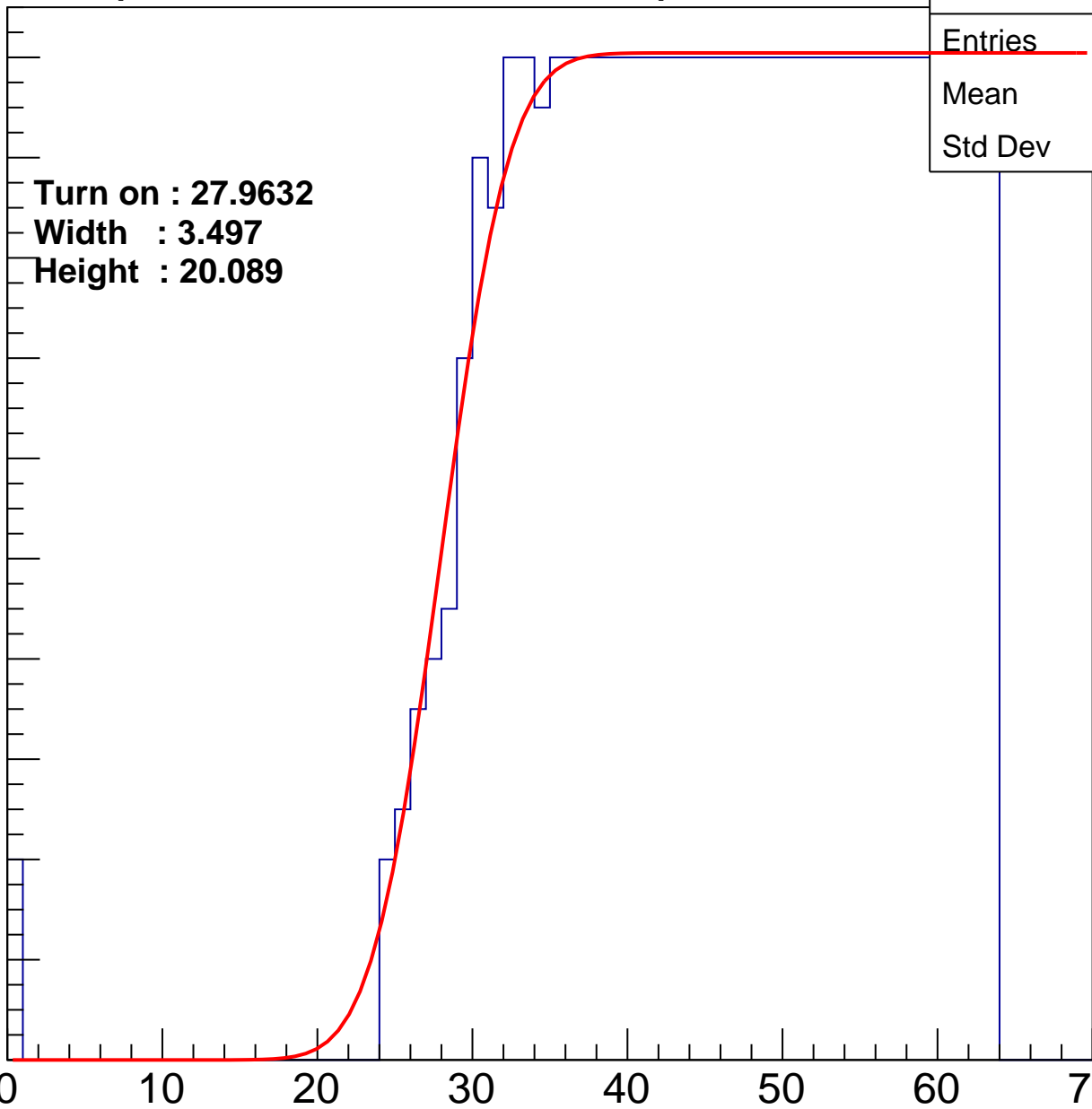
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9632
Width : 3.497
Height : 20.089

Entries	725
Mean	45.12
Std Dev	11.08

ampl



B1L001S, U25-ch127

calib_packv5_042523_0143.root, FC#2, port C2

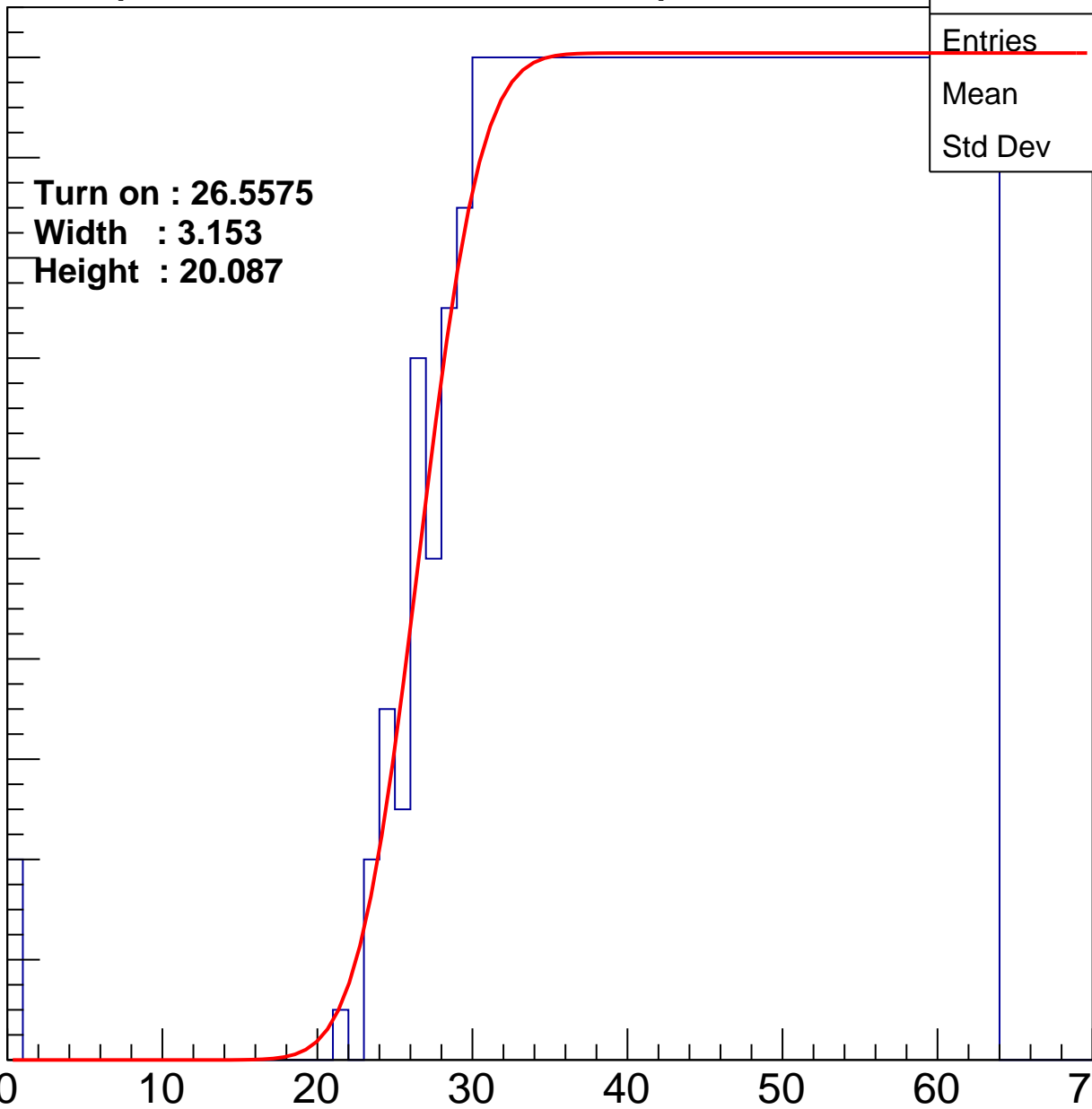
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5575
Width : 3.153
Height : 20.087

Entries	757
Mean	44.35
Std Dev	11.46

ampl



B1L001S, U25-ch127

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5575
Width : 3.153
Height : 20.087

Entries	757
Mean	44.35
Std Dev	11.46

ampl

