

B1L104S, U26-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	31.45
Std Dev	28.57

Turn on : 55.0843

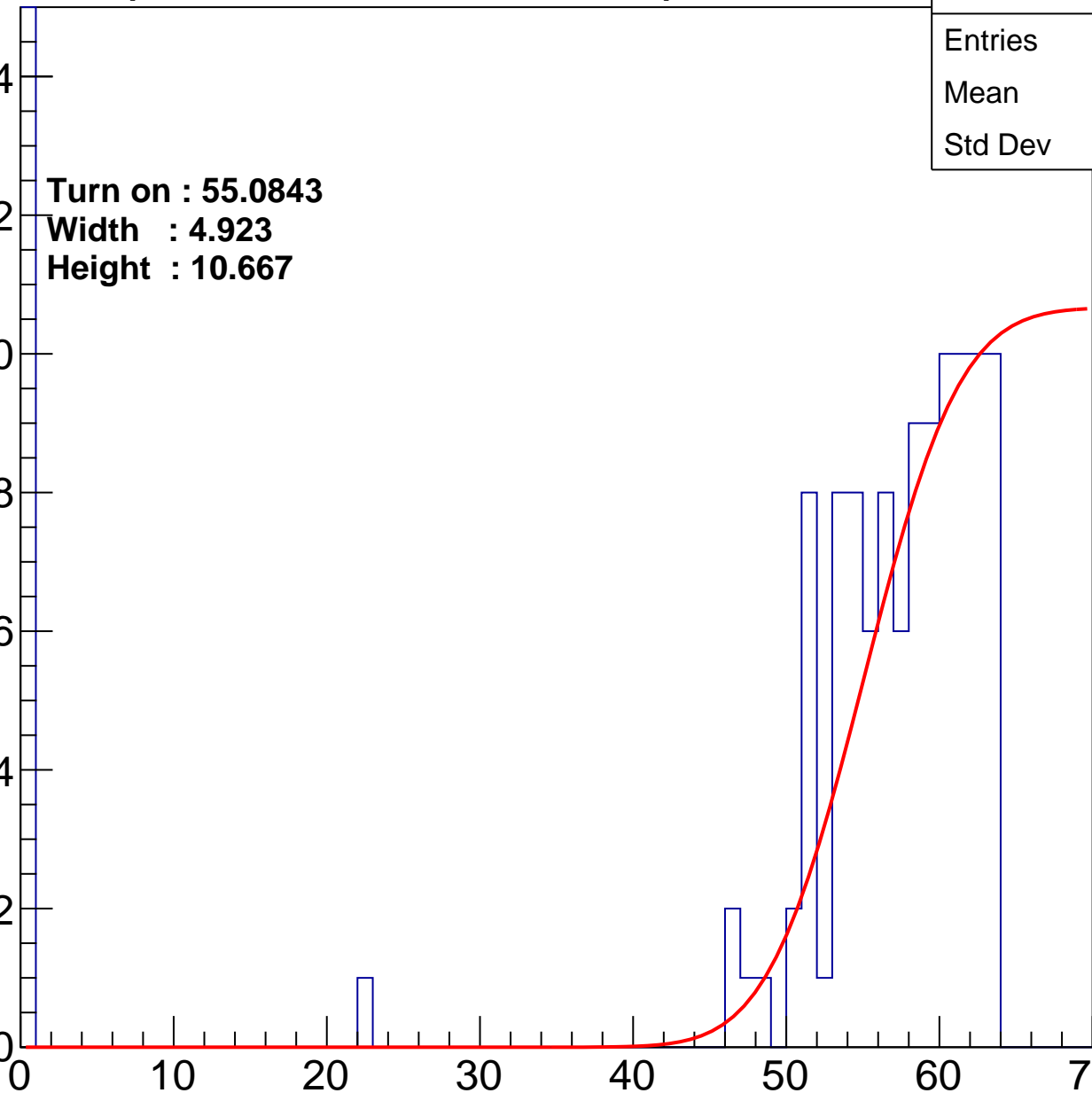
Width : 4.923

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	36.88
Std Dev	27.69

Turn on : 53.0380

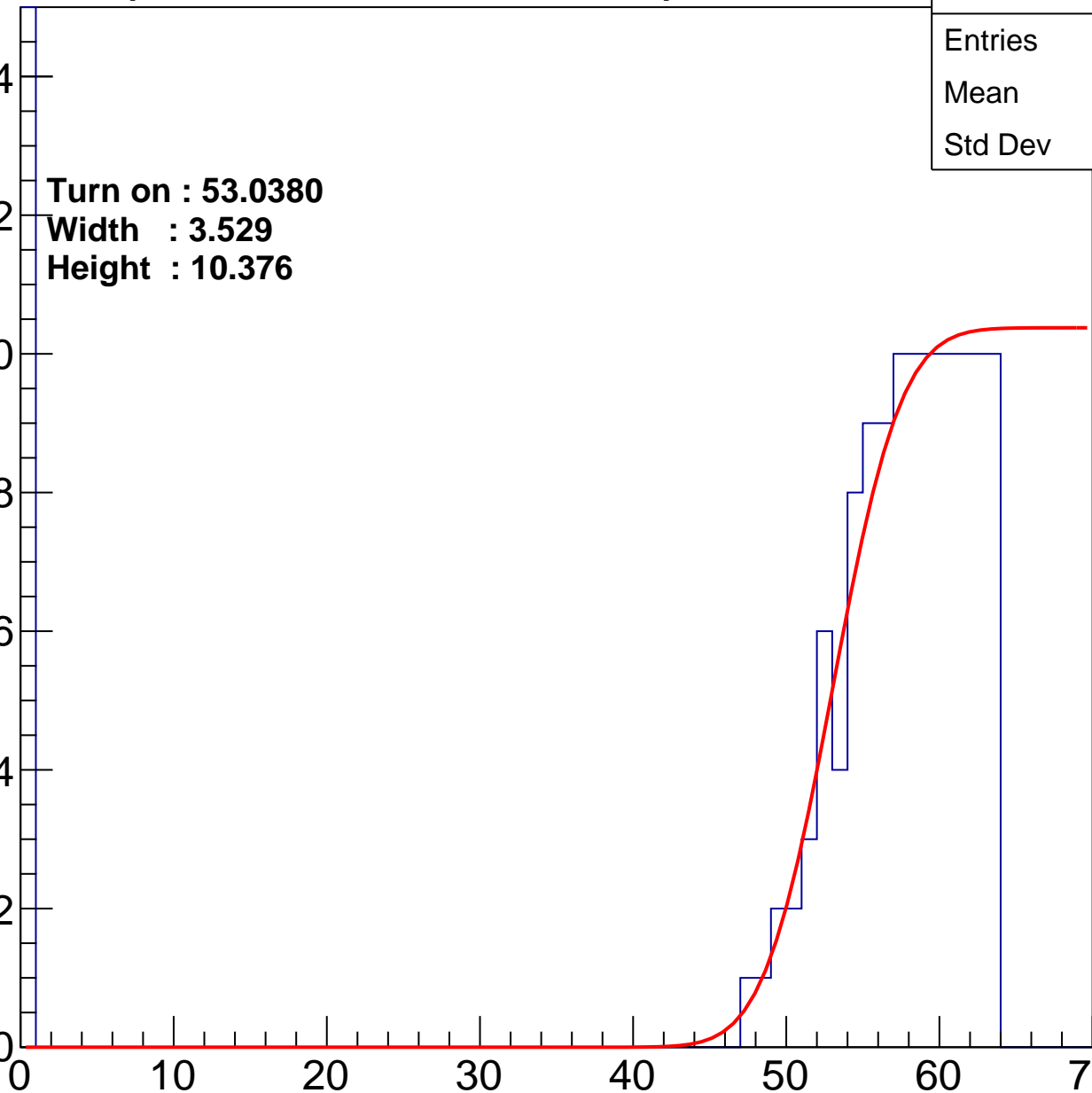
Width : 3.529

Height : 10.376

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	31.88
Std Dev	29.1

Turn on : 55.2719

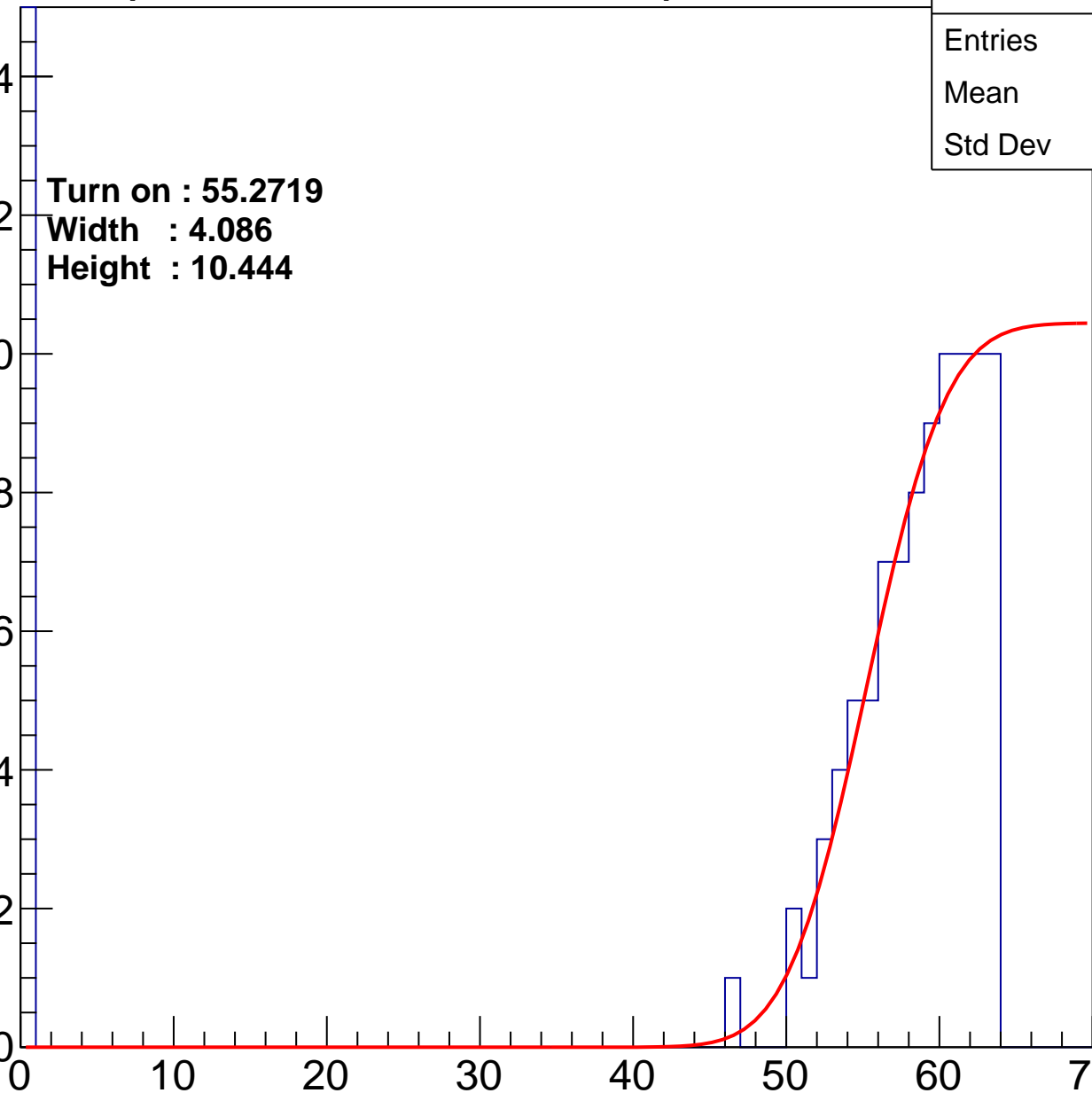
Width : 4.086

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	34.38
Std Dev	28.94

Turn on : 55.5777

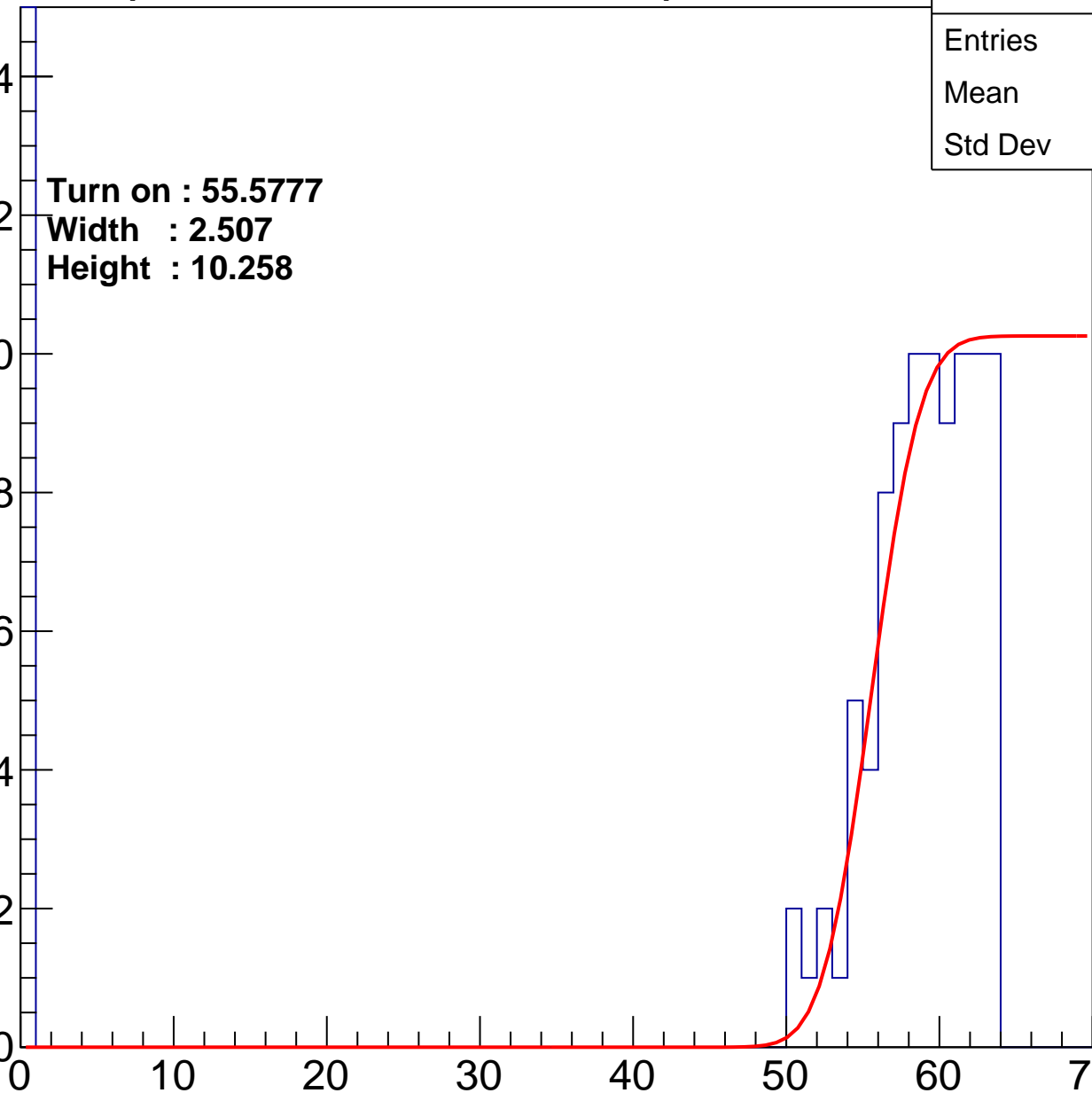
Width : 2.507

Height : 10.258

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	36.16
Std Dev	28.12

Turn on : 54.2760

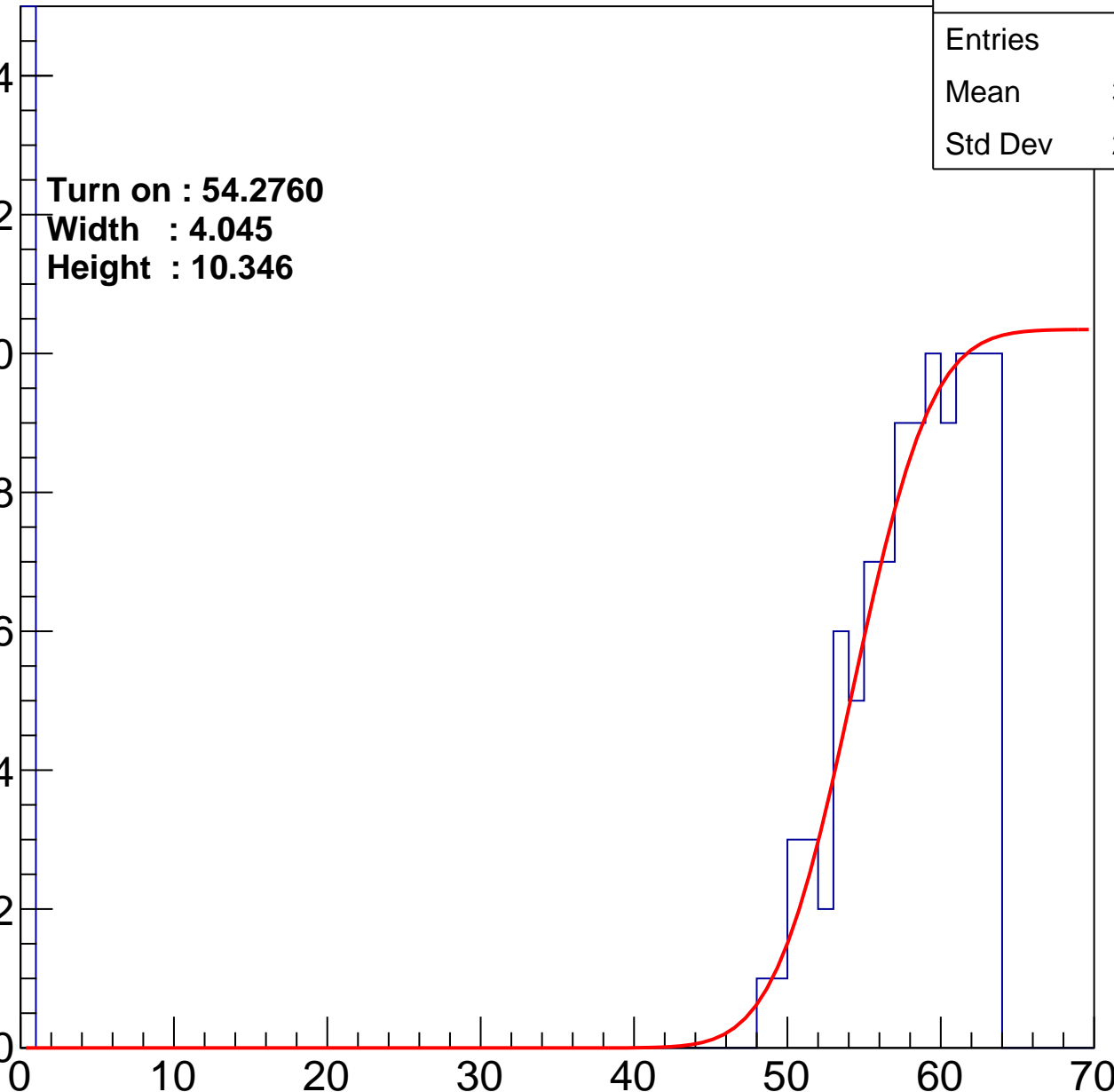
Width : 4.045

Height : 10.346

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	36.06
Std Dev	27.9

Turn on : 52.5068

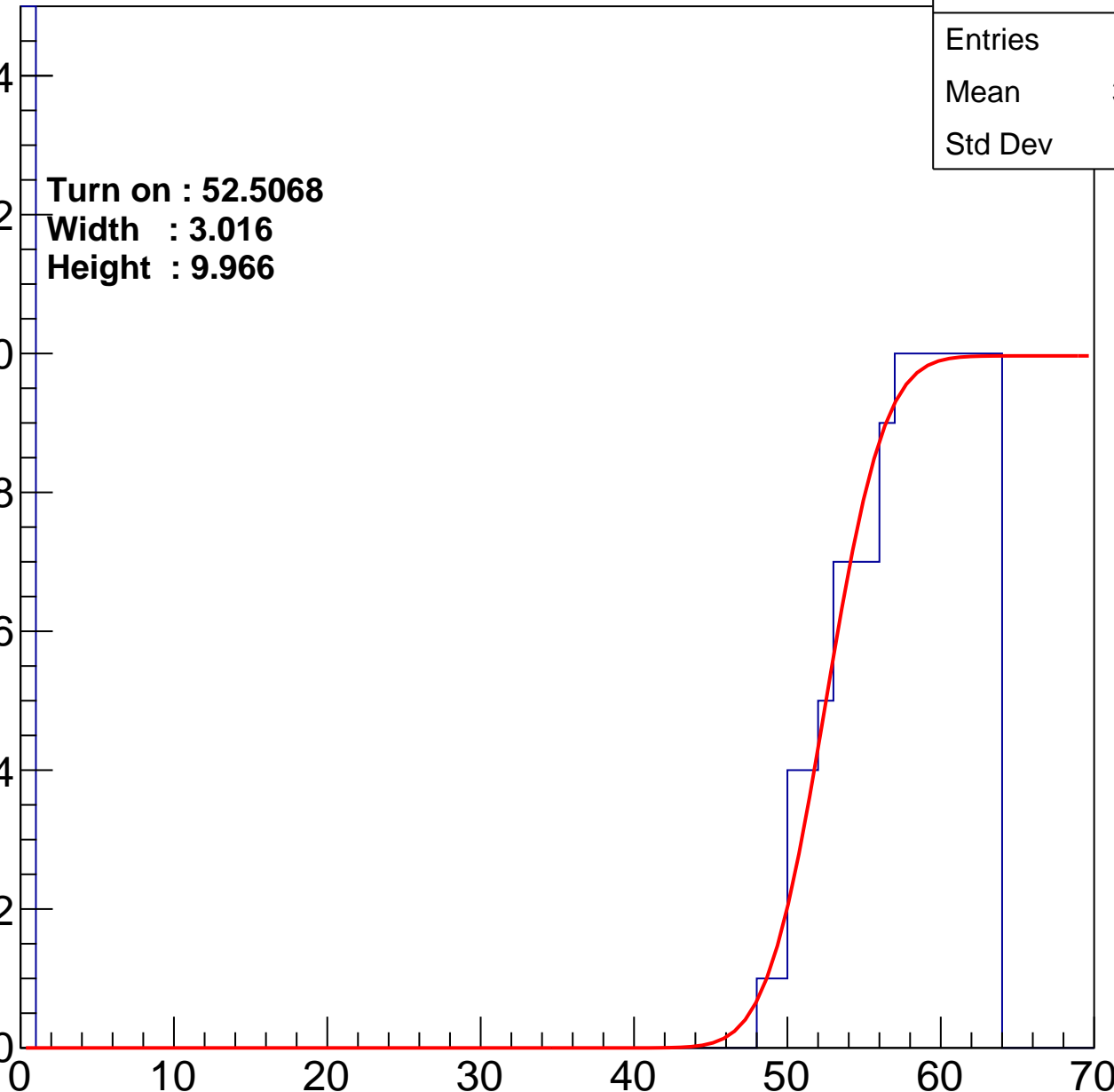
Width : 3.016

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	130
Mean	39.52
Std Dev	27.45

Turn on : 55.5956

Width : 4.275

Height : 10.222

Entry

40
35
30
25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70

B1L104S, U26-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	147
Mean	38.22
Std Dev	27.62

Turn on : 55.9059

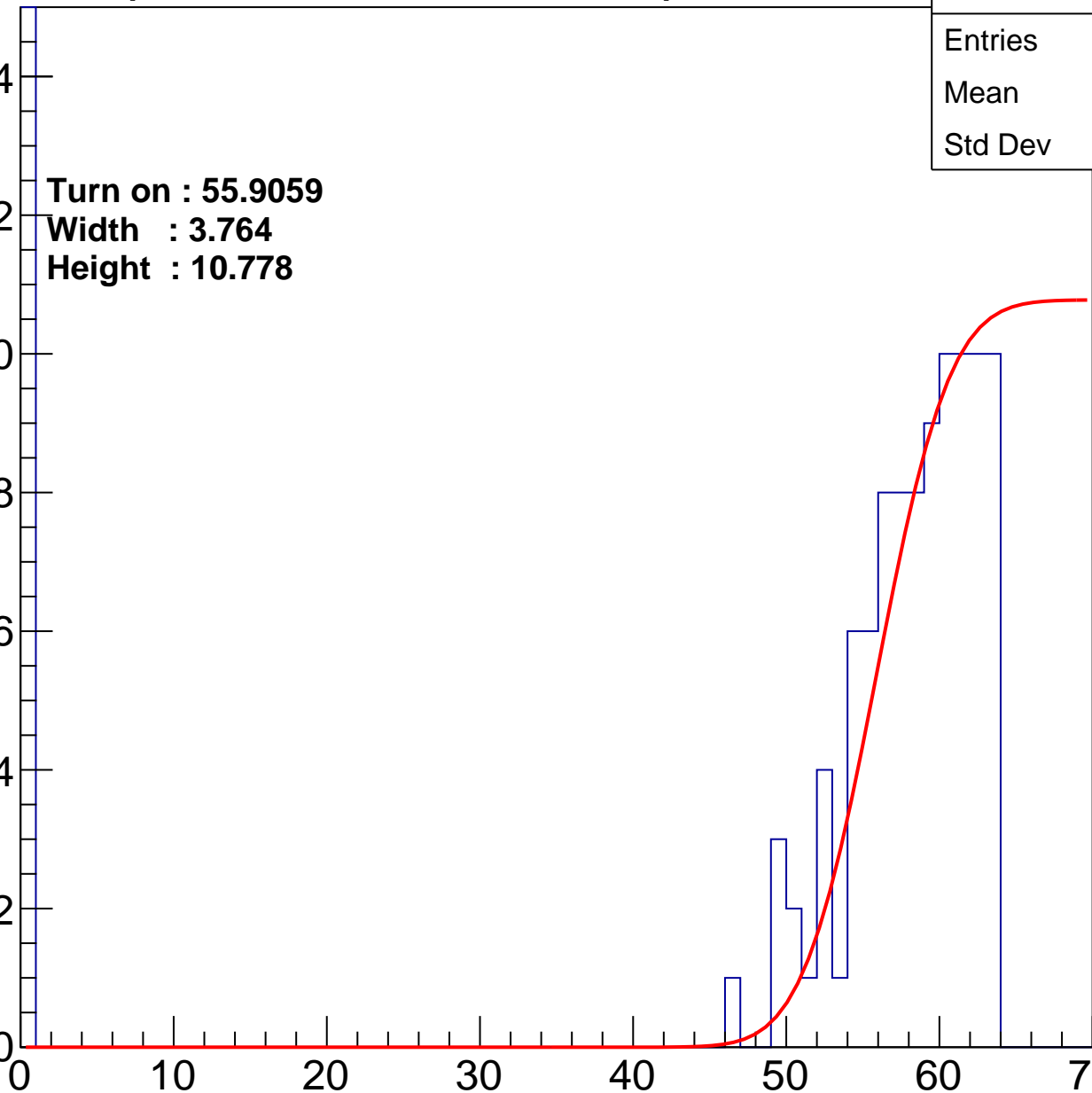
Width : 3.764

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch8

calib_packv5_033123_0516.root, FC#4, port A1

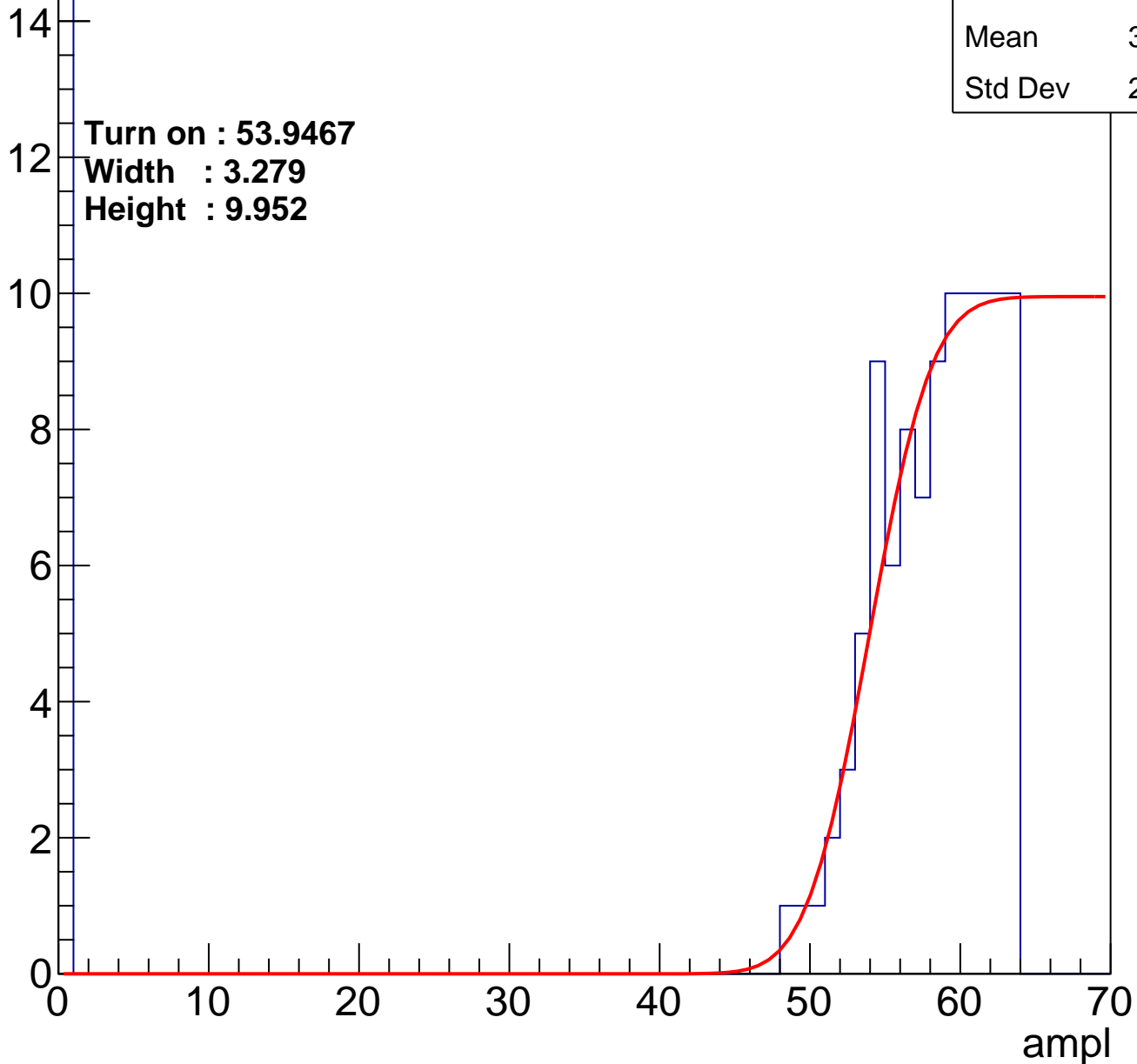
Entry

Entries	159
Mean	37.14
Std Dev	27.92

Turn on : 53.9467

Width : 3.279

Height : 9.952



B1L104S, U26-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	142
Mean	39.41
Std Dev	27.42

Turn on : 54.8443

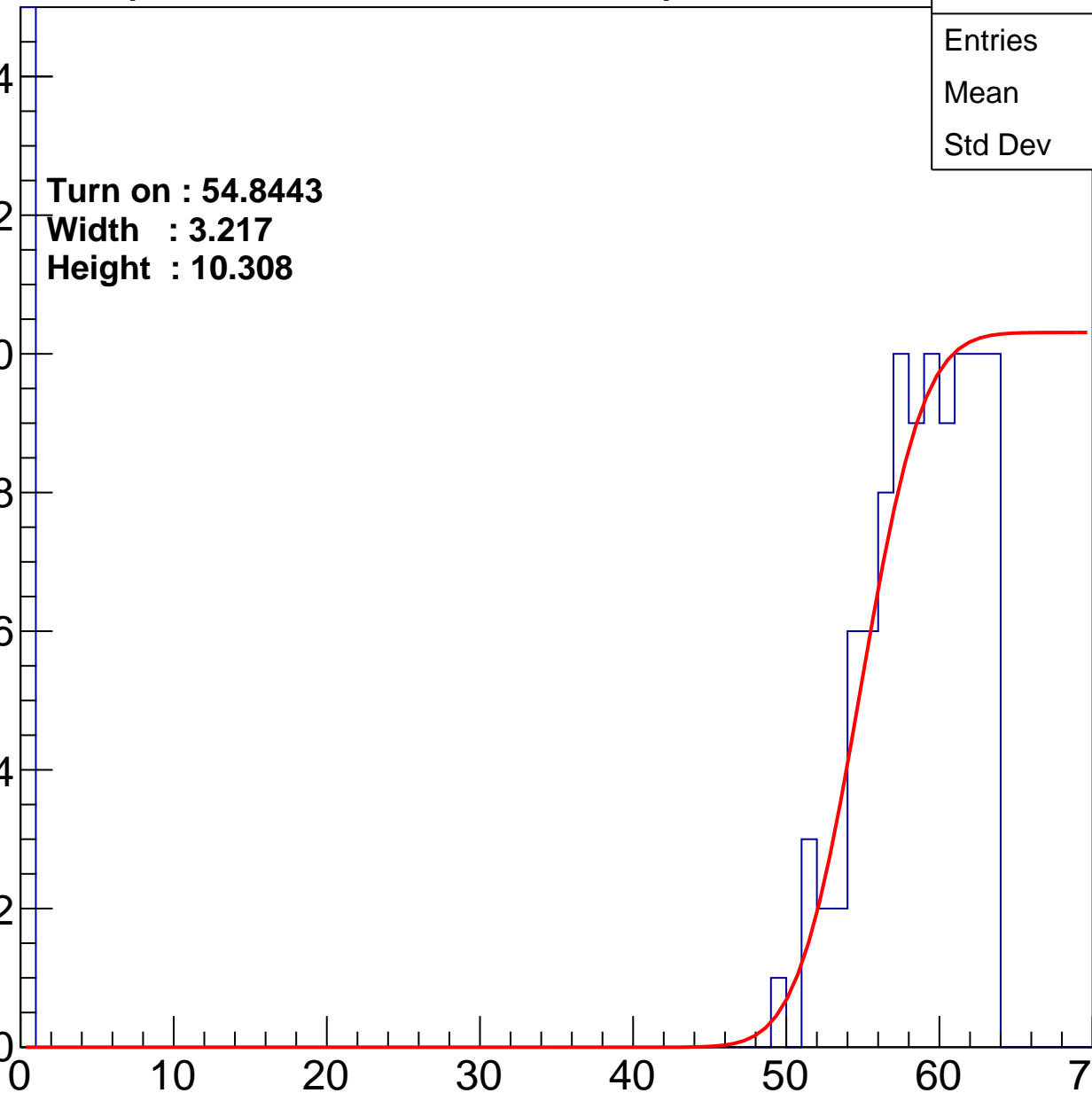
Width : 3.217

Height : 10.308

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	34.14
Std Dev	28.36

Turn on : 54.0677

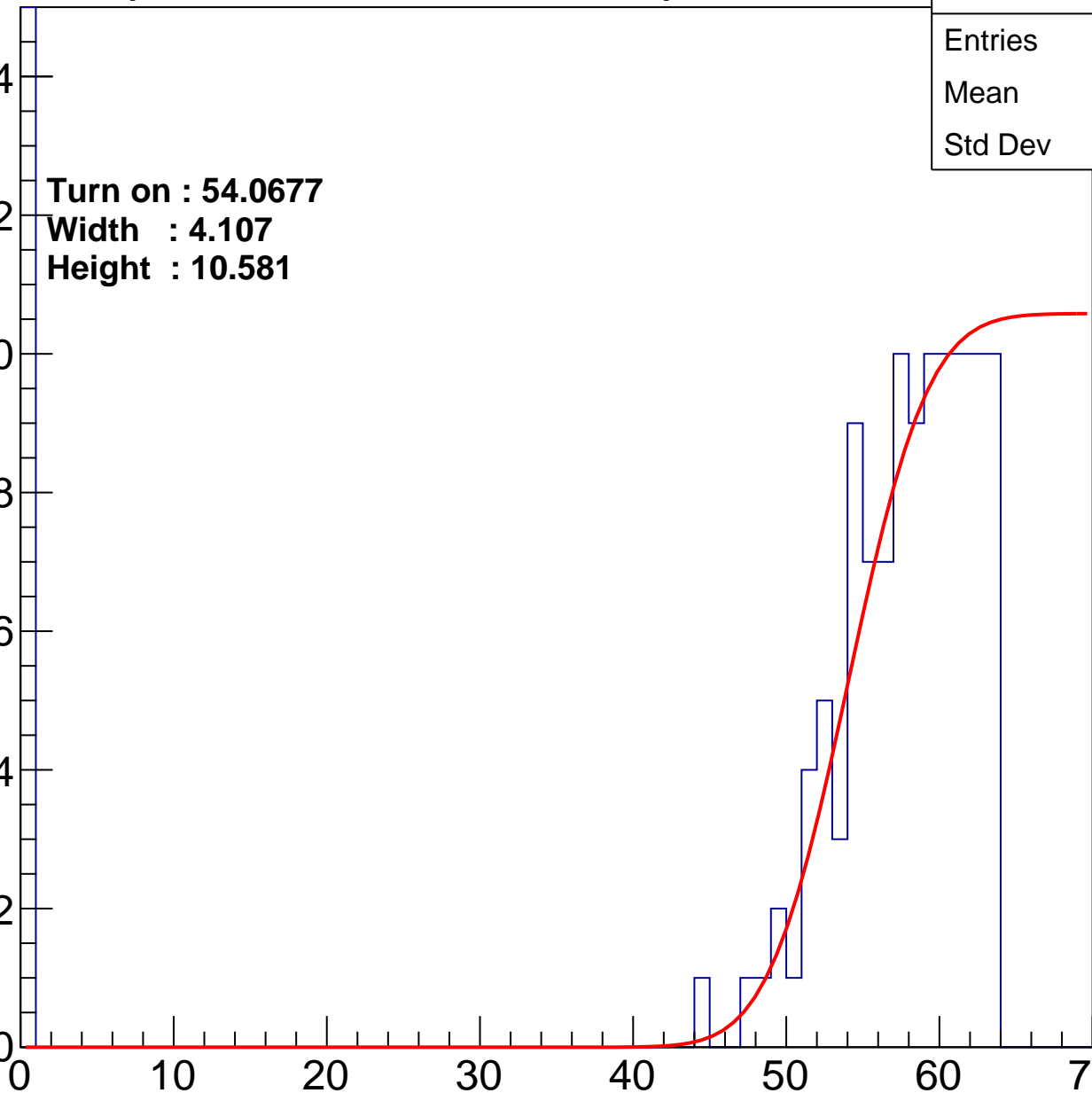
Width : 4.107

Height : 10.581

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	39.26
Std Dev	27.05

Turn on : 53.5447

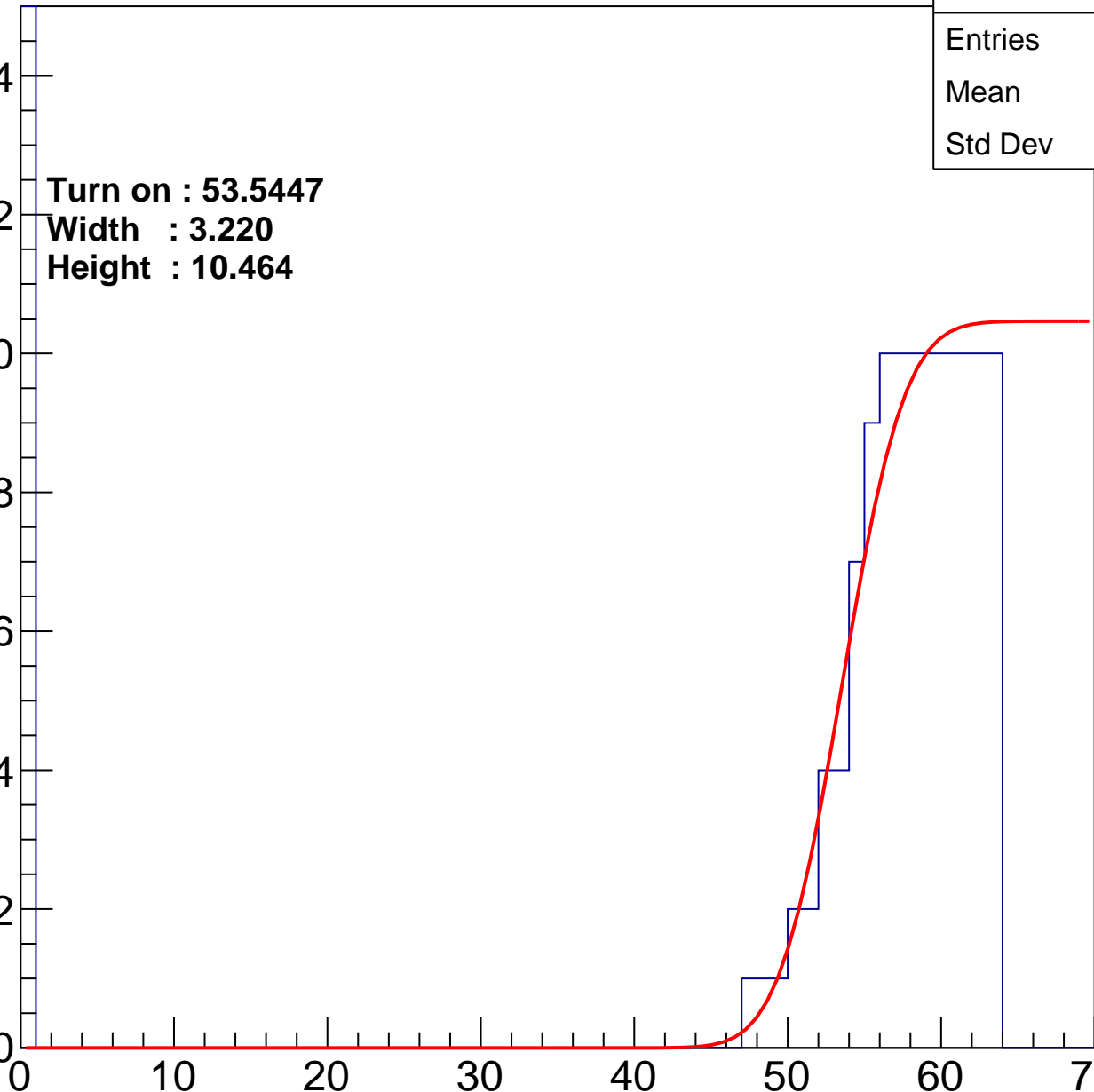
Width : 3.220

Height : 10.464

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch12

calib_packv5_033123_0516.root, FC#4, port A1

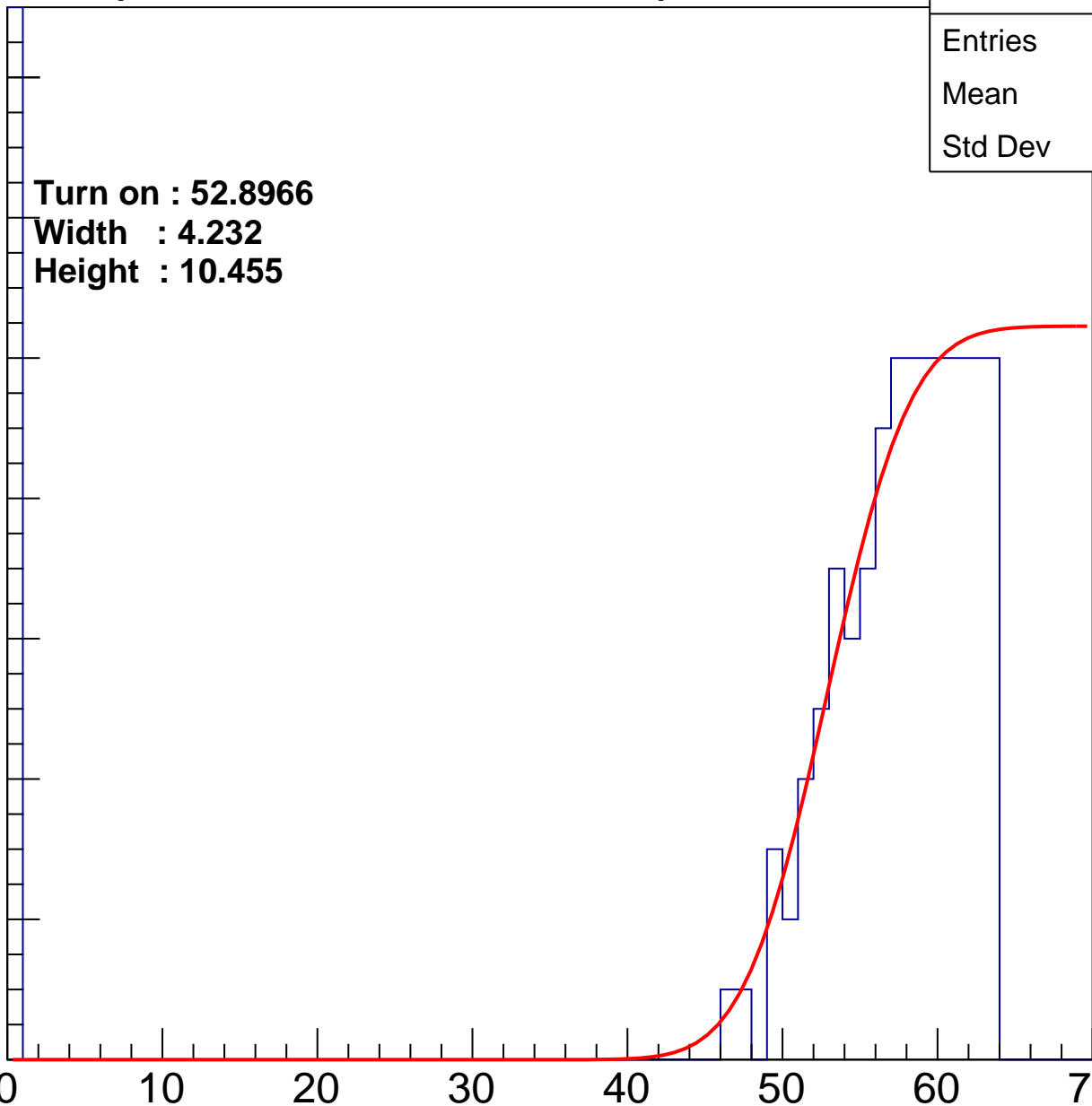
Entry

14
12
10
8
6
4
2
0

Turn on : 52.8966
Width : 4.232
Height : 10.455

Entries	175
Mean	37.65
Std Dev	27.39

ampl



B1L104S, U26-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	142
Mean	39.7
Std Dev	27.2

Turn on : 54.6126

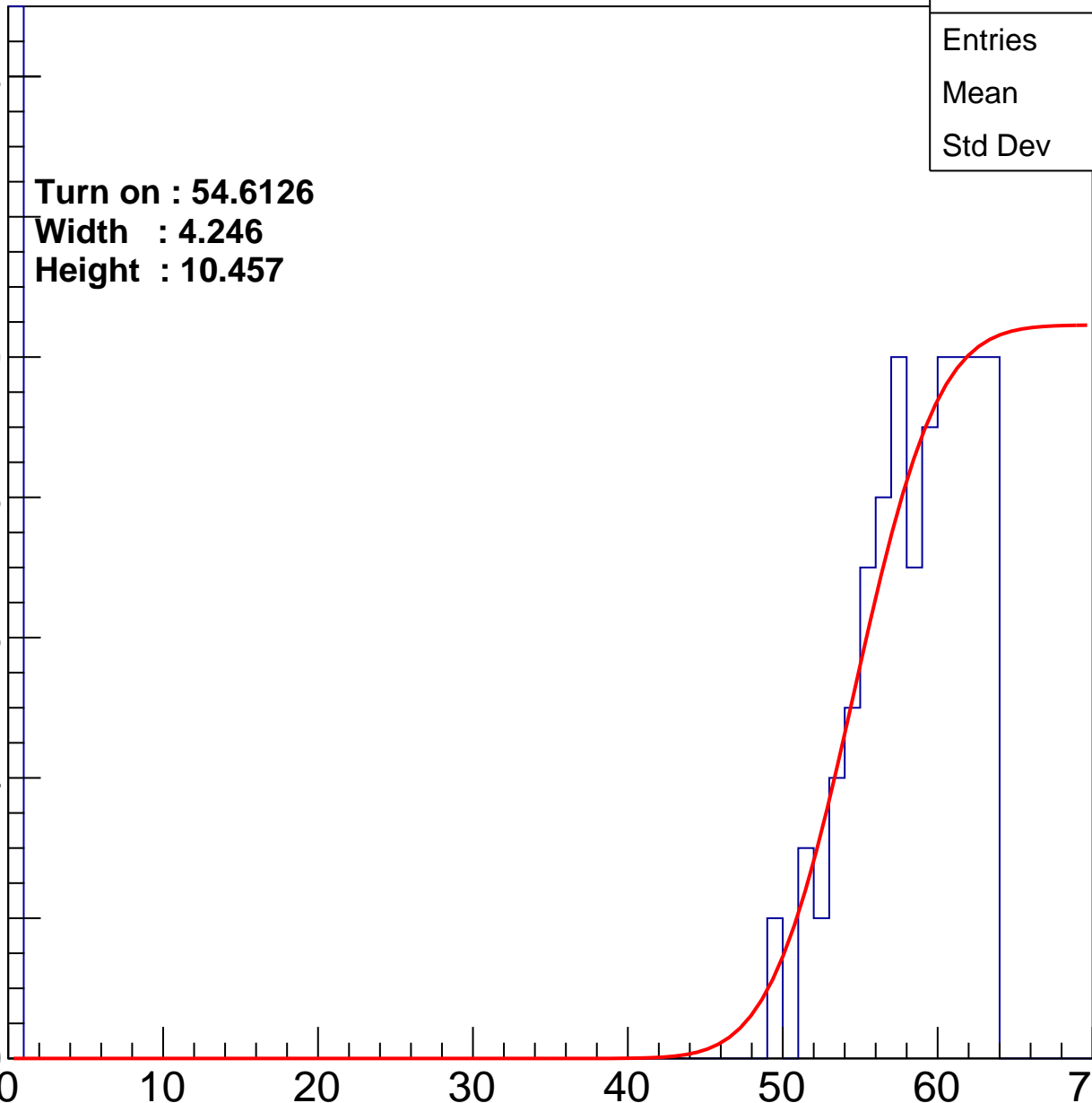
Width : 4.246

Height : 10.457

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	34.61
Std Dev	28.53

Turn on : 53.9739

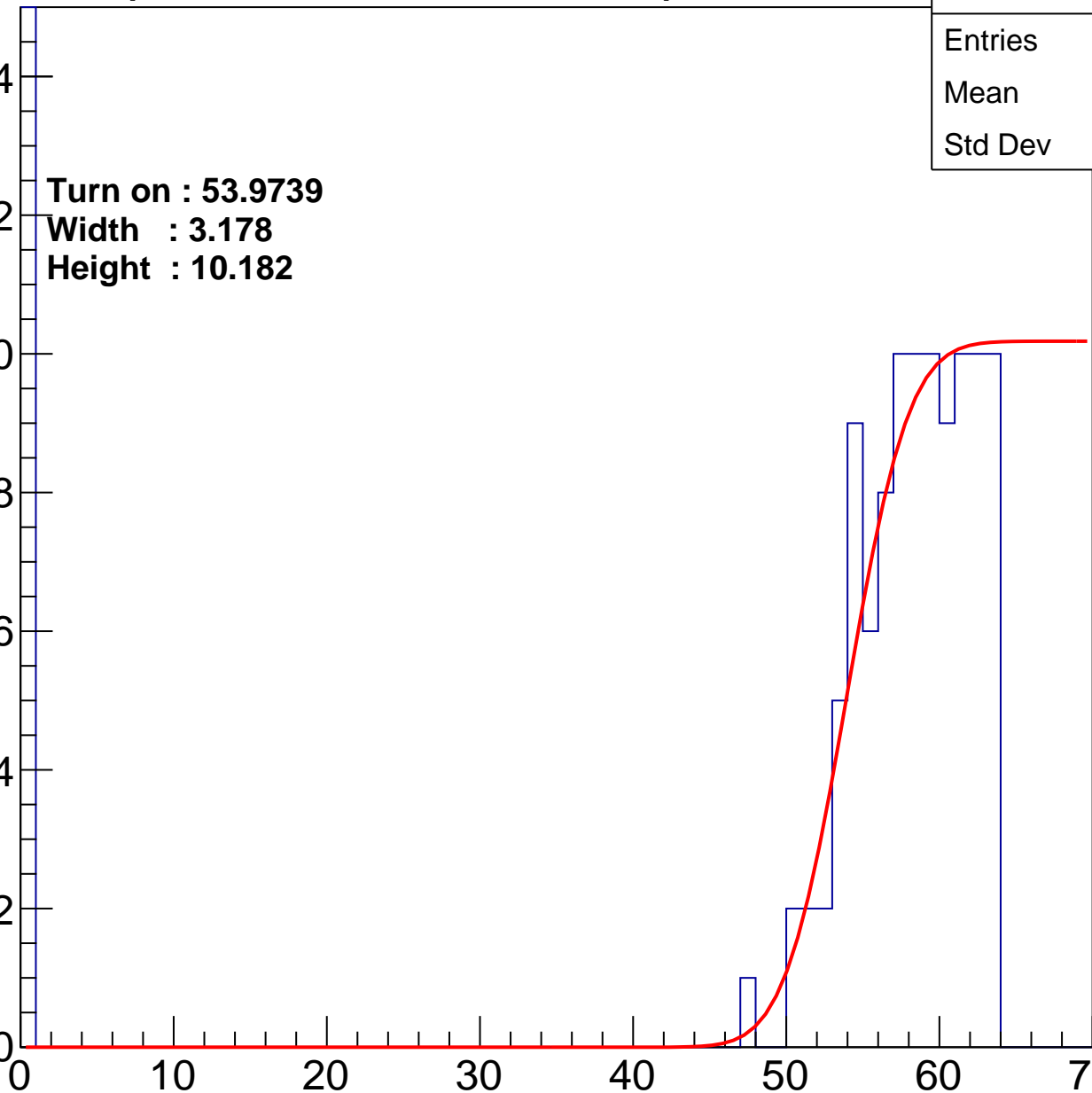
Width : 3.178

Height : 10.182

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	33.81
Std Dev	28.61

Turn on : 54.0249

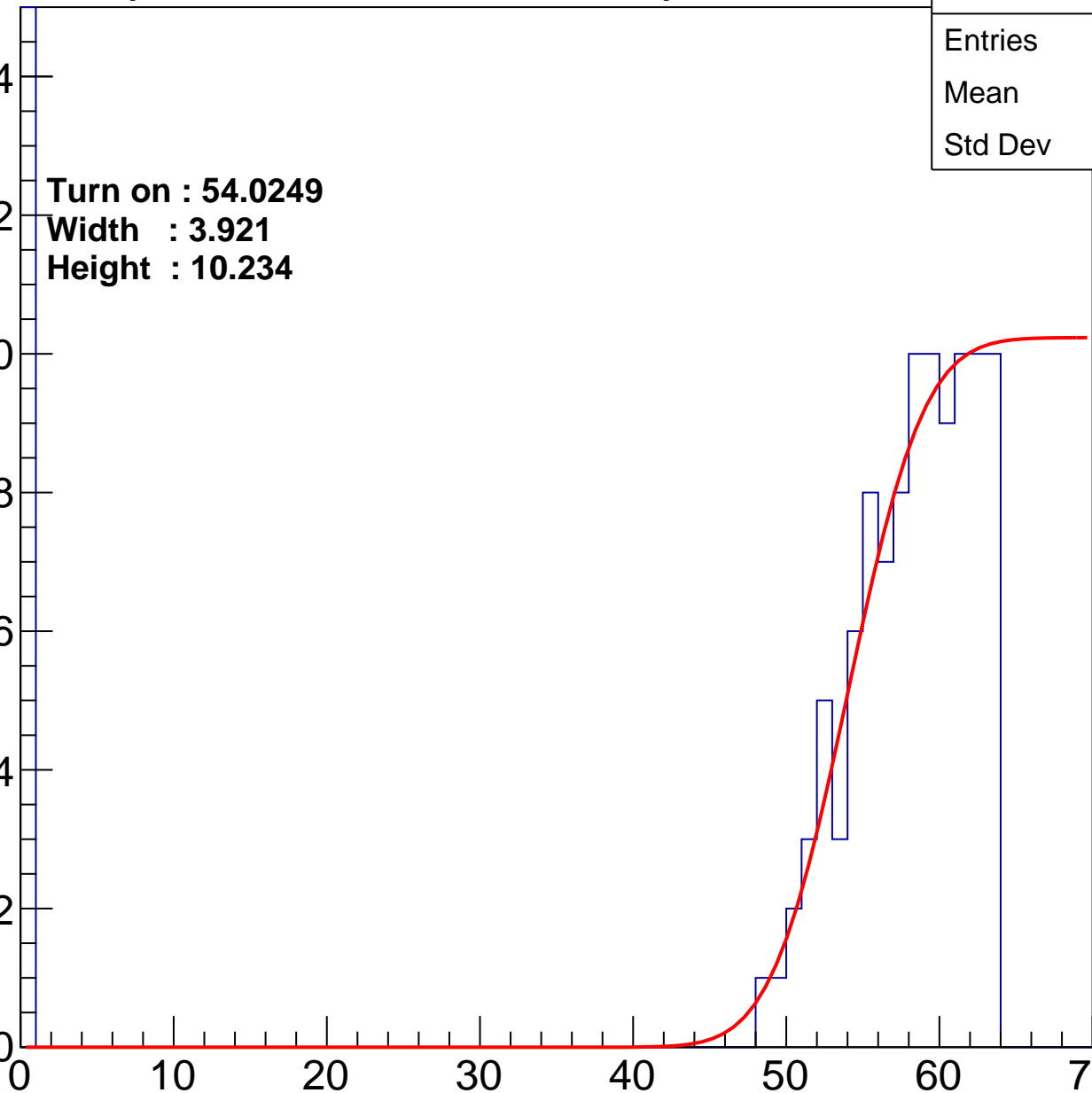
Width : 3.921

Height : 10.234

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	32.33
Std Dev	28.37

Turn on : 53.1865

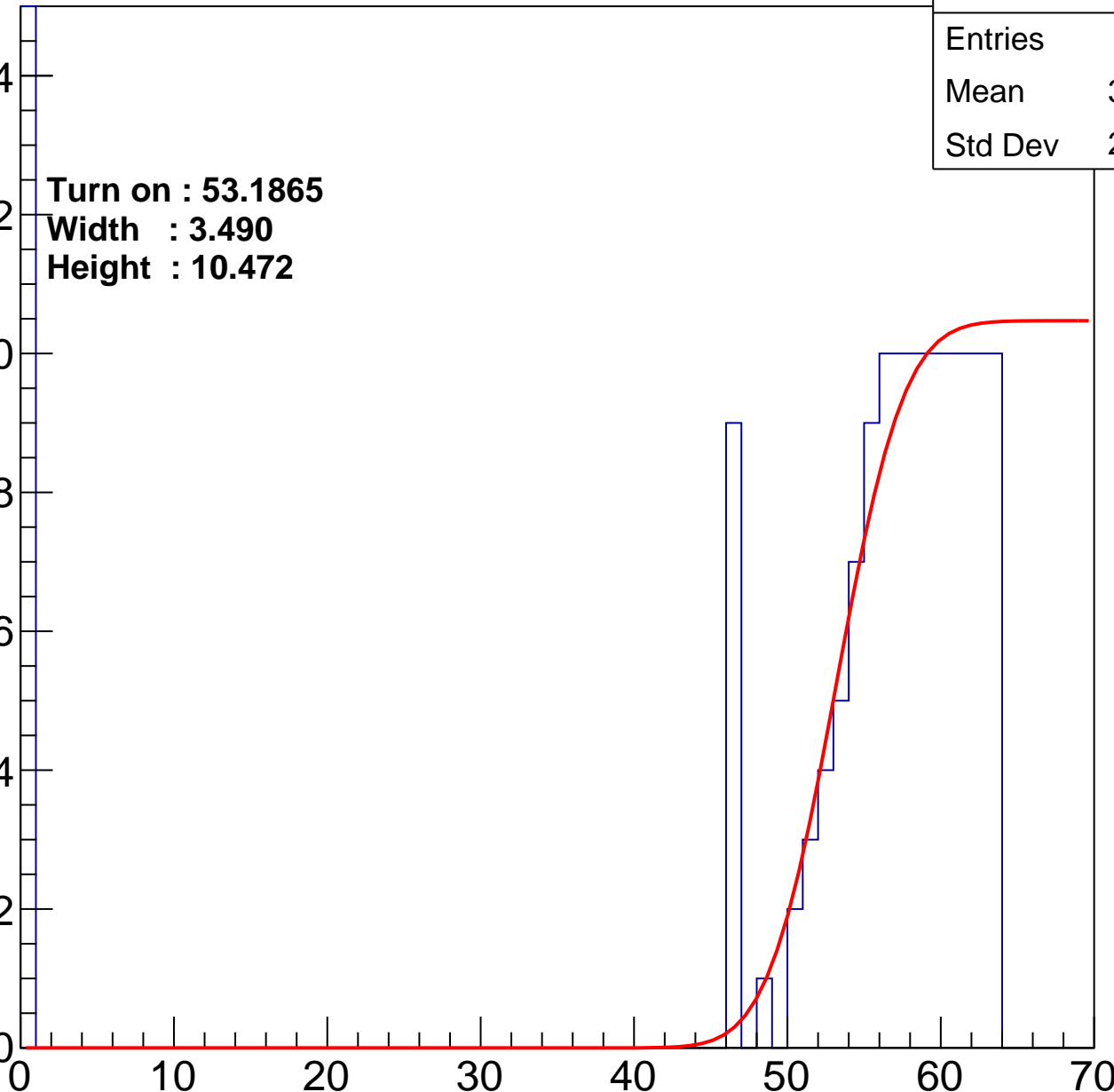
Width : 3.490

Height : 10.472

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch17

calib_packv5_033123_0516.root, FC#4, port A1

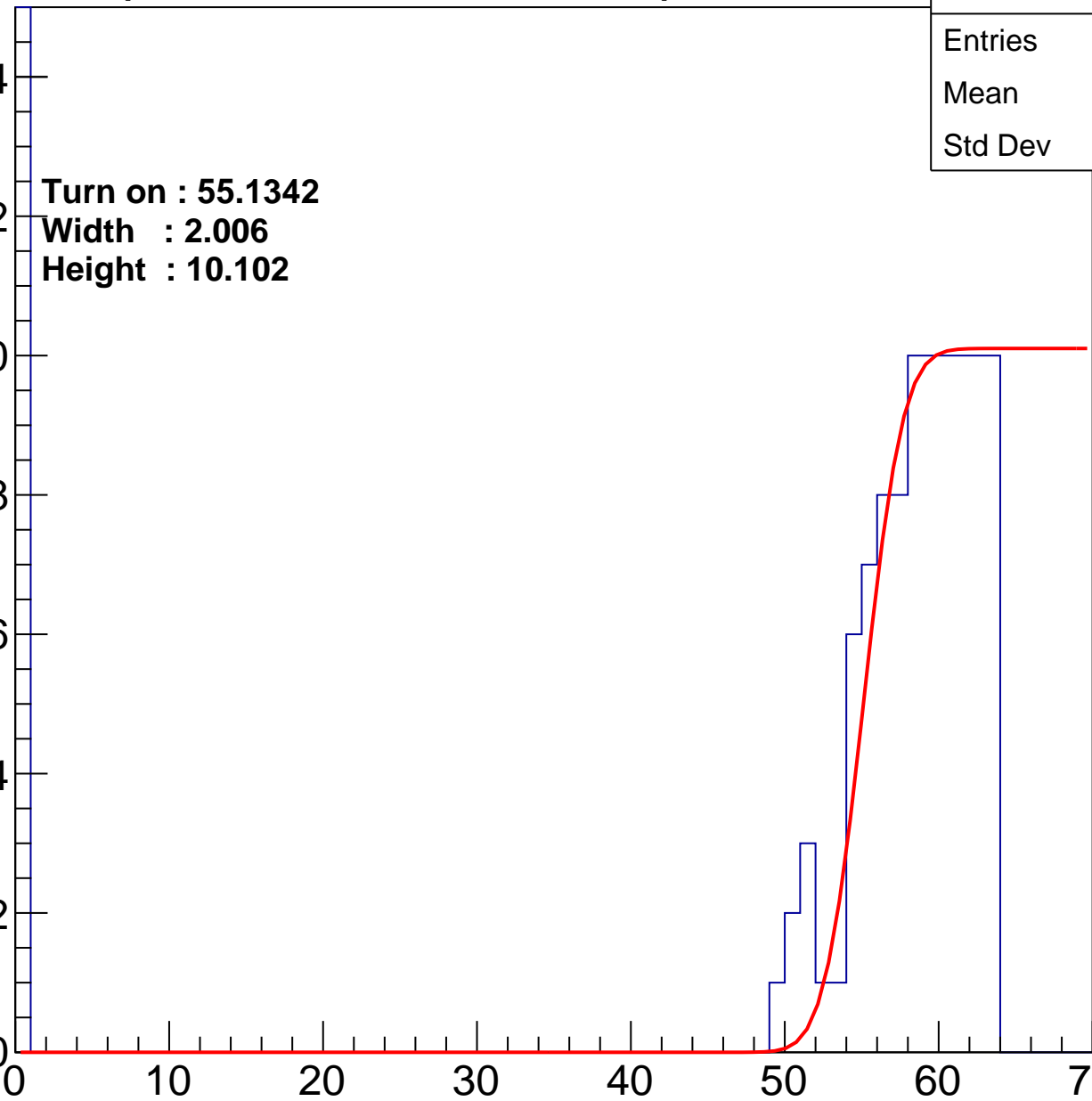
Entry

14
12
10
8
6
4
2
0

Turn on : 55.1342
Width : 2.006
Height : 10.102

Entries	173
Mean	32.66
Std Dev	29.02

ampl



B1L104S, U26-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	145
Mean	39.01
Std Dev	27.57

Turn on : 54.6279

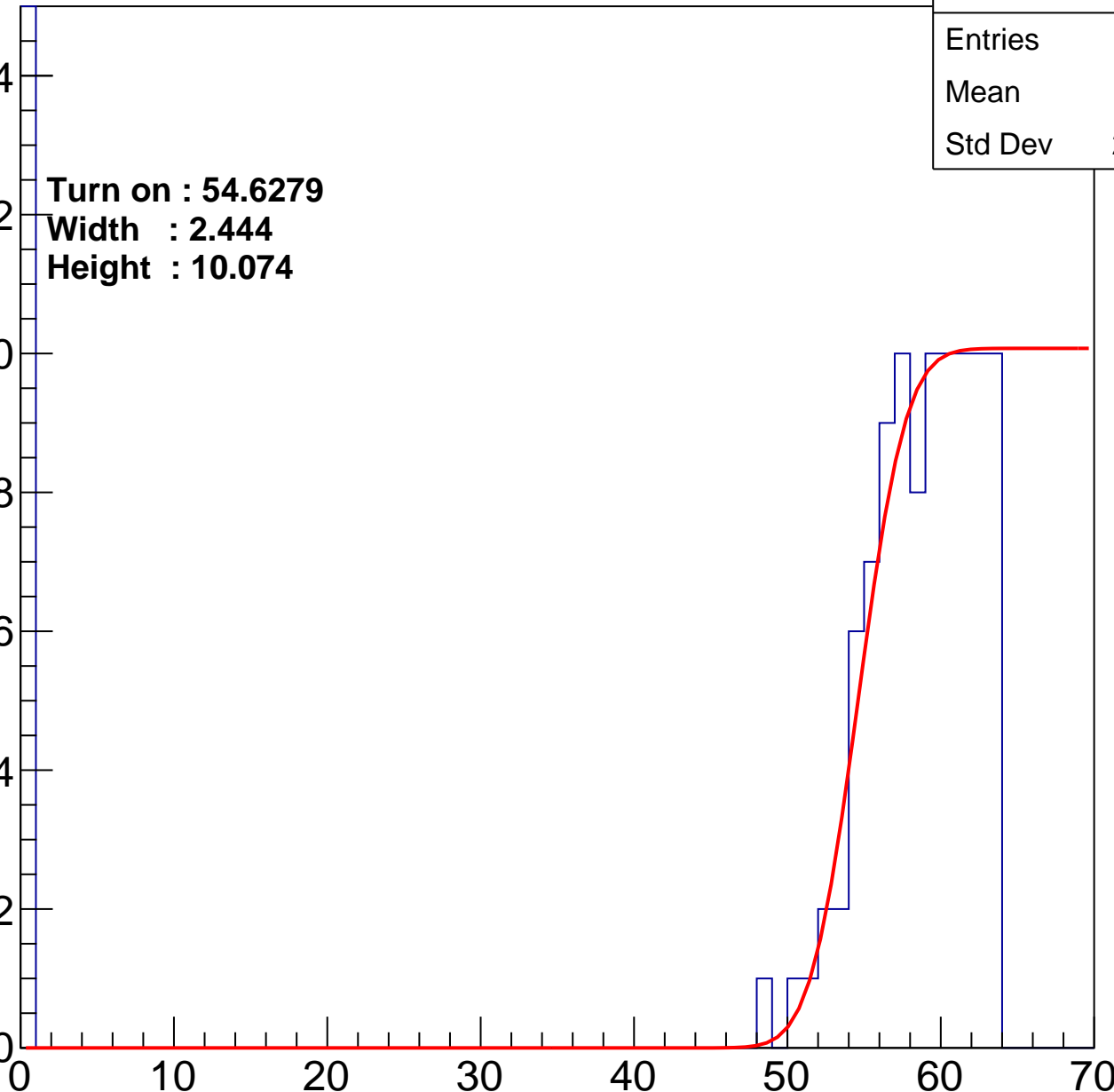
Width : 2.444

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	40.74
Std Dev	26.16

Turn on : 52.8375

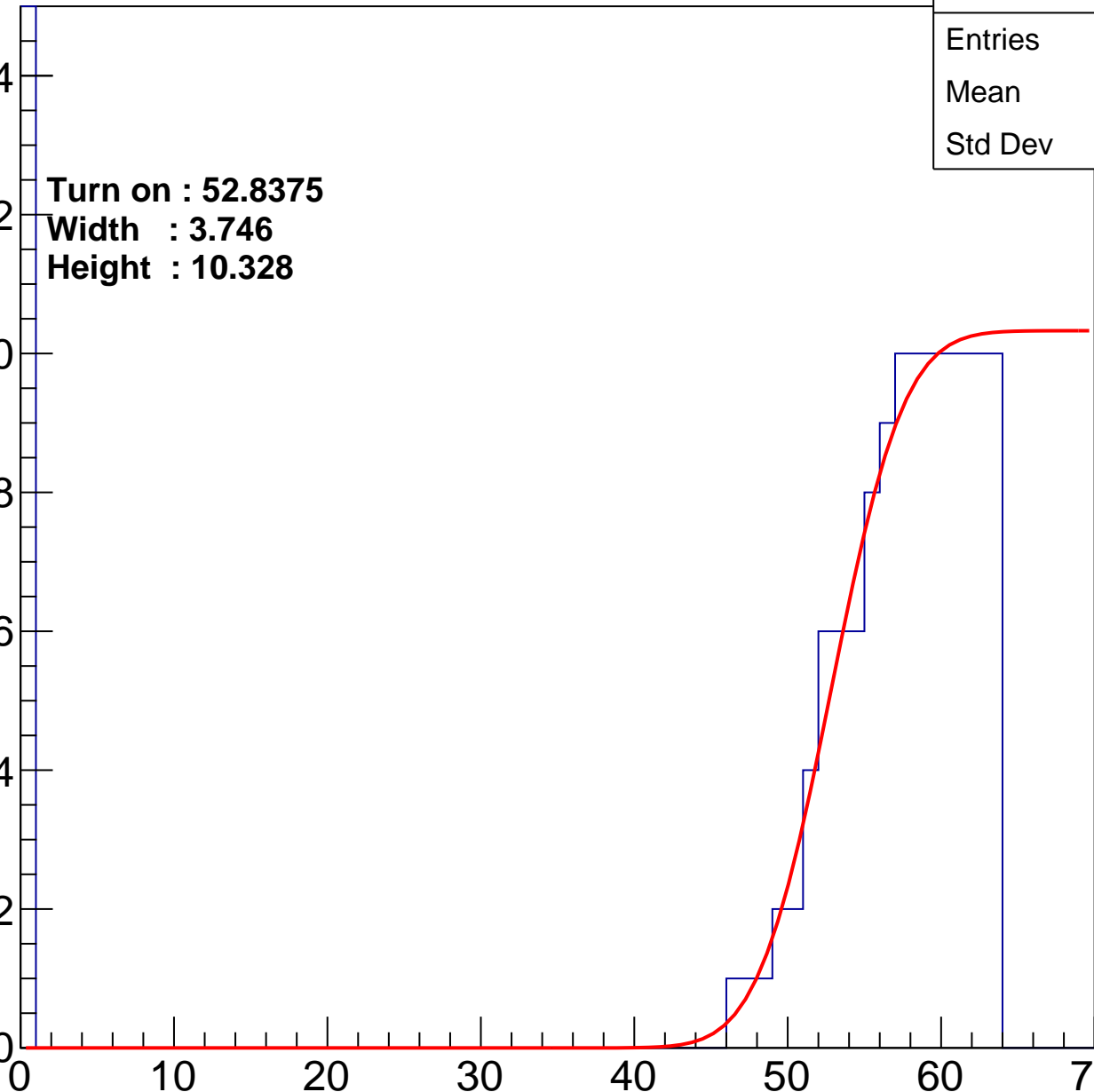
Width : 3.746

Height : 10.328

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	147
Mean	37.99
Std Dev	27.82

Turn on : 54.1162

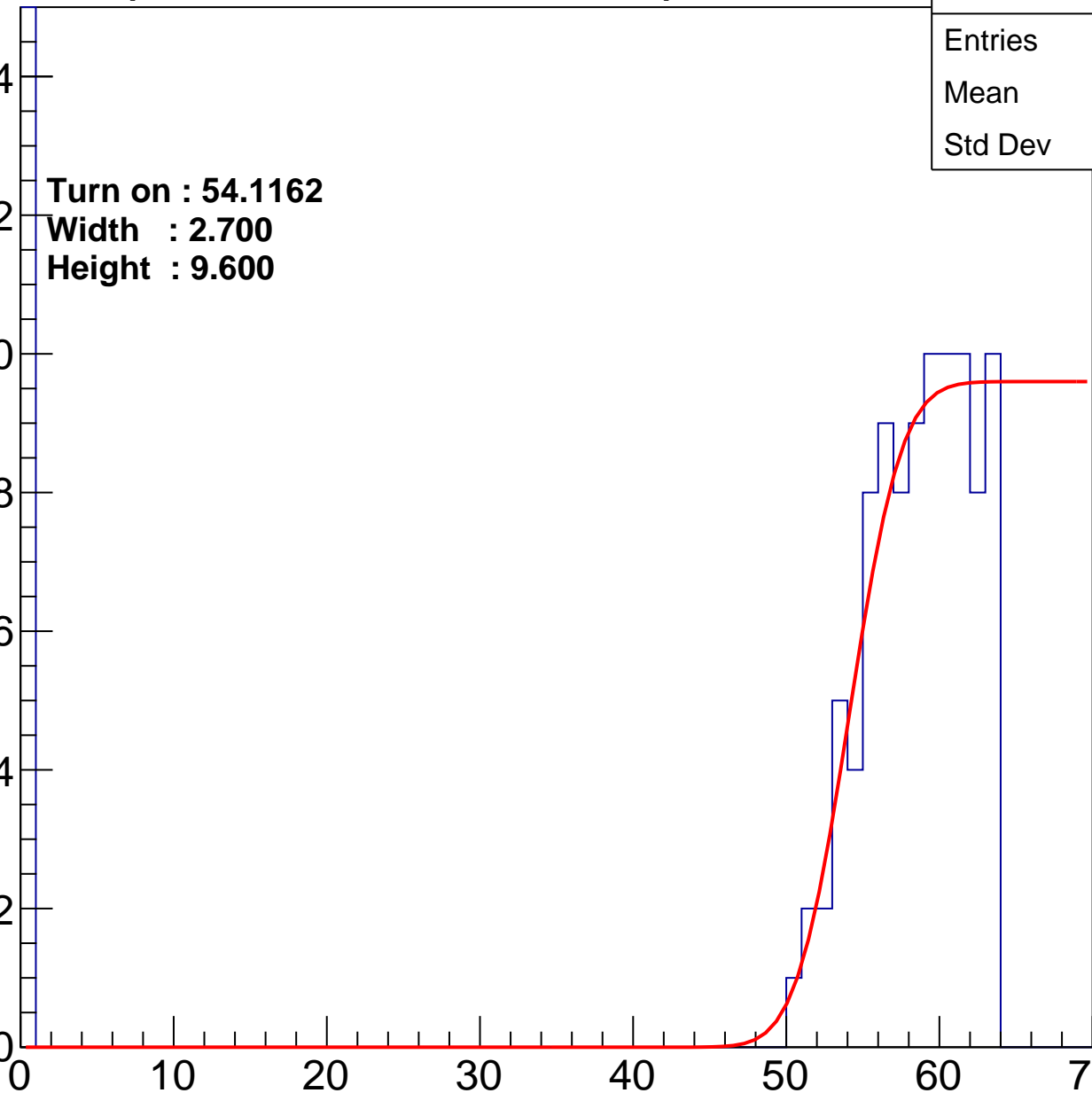
Width : 2.700

Height : 9.600

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	135
Mean	40.13
Std Dev	27.13

Turn on : 55.9053

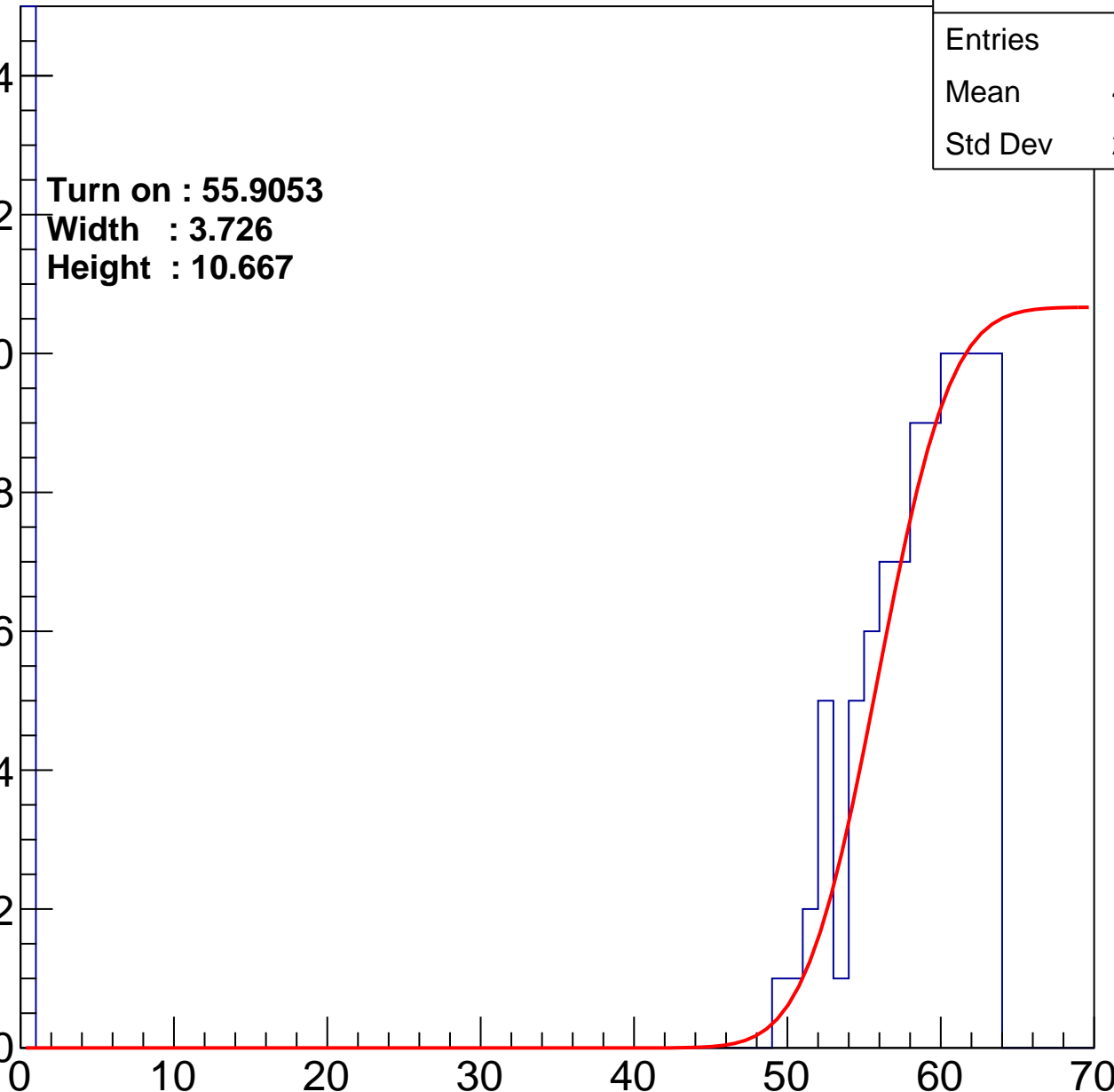
Width : 3.726

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	38.95
Std Dev	26.9

Turn on : 54.1503

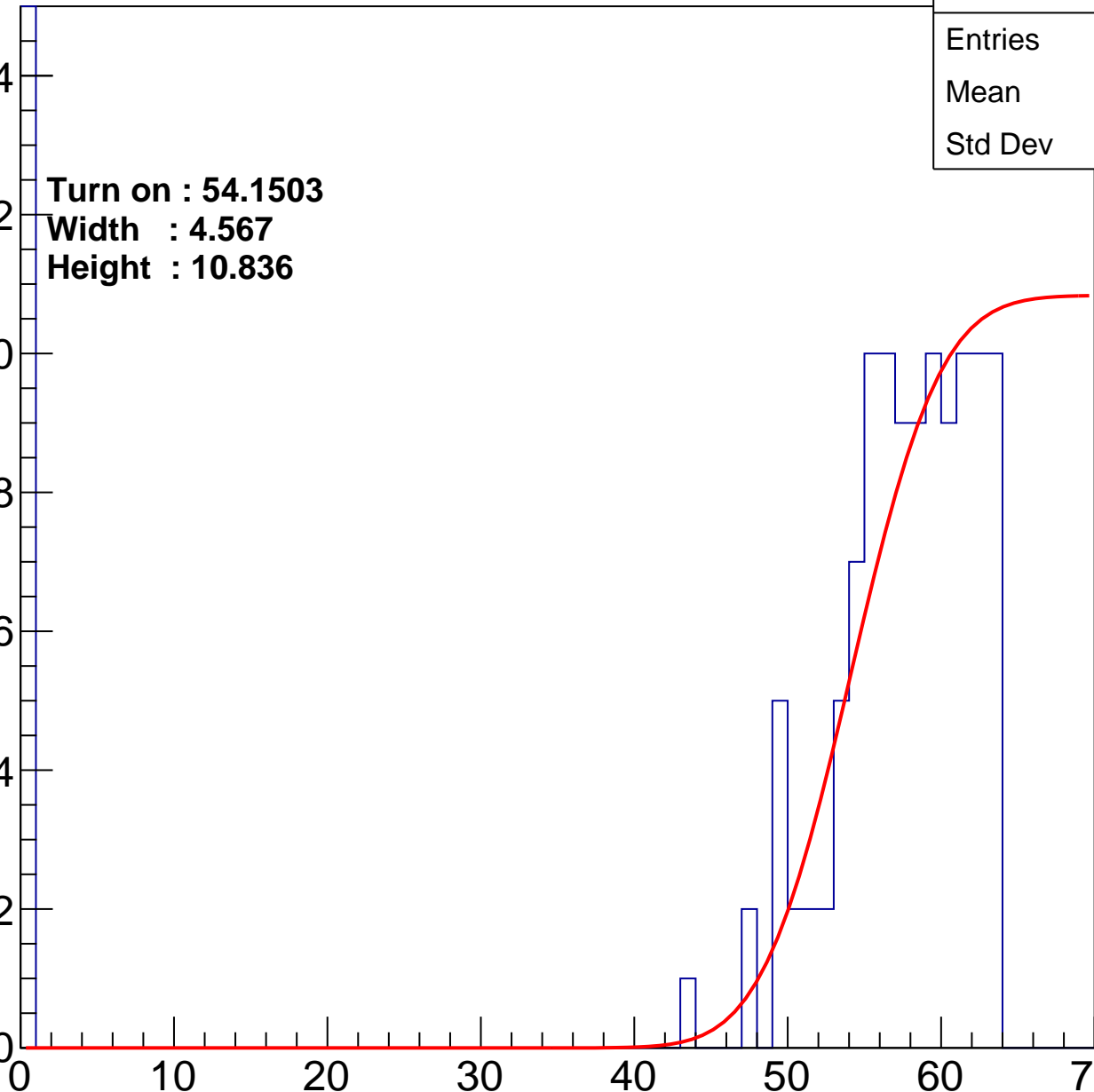
Width : 4.567

Height : 10.836

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	31.14
Std Dev	29.36

Turn on : 55.5389

Width : 4.380

Height : 10.000

Entry

70

60

50

40

30

20

10

0

0

10

20

30

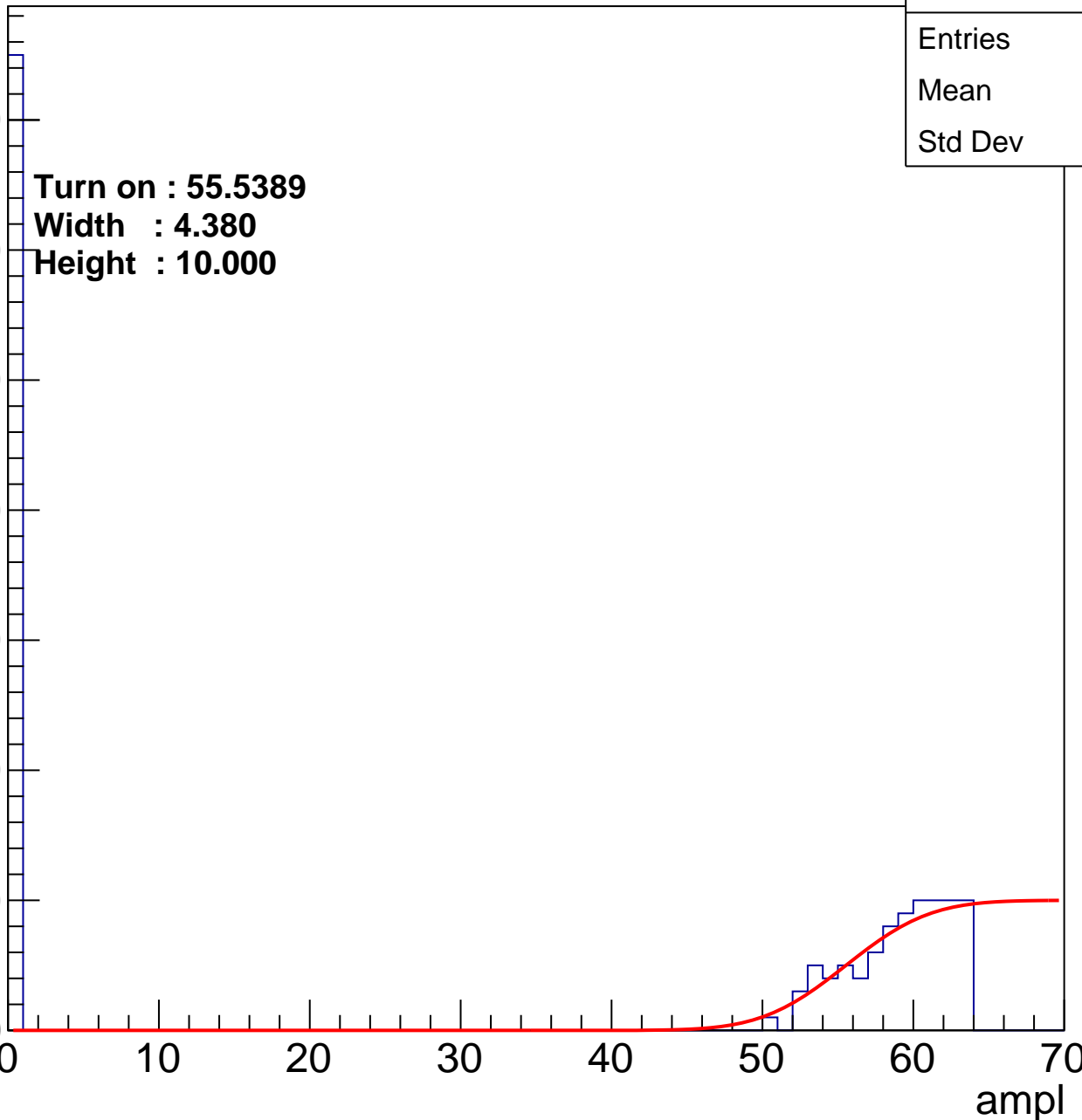
40

50

60

70

ampl



B1L104S, U26-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	134
Mean	40.81
Std Dev	26.78

Turn on : 54.5955

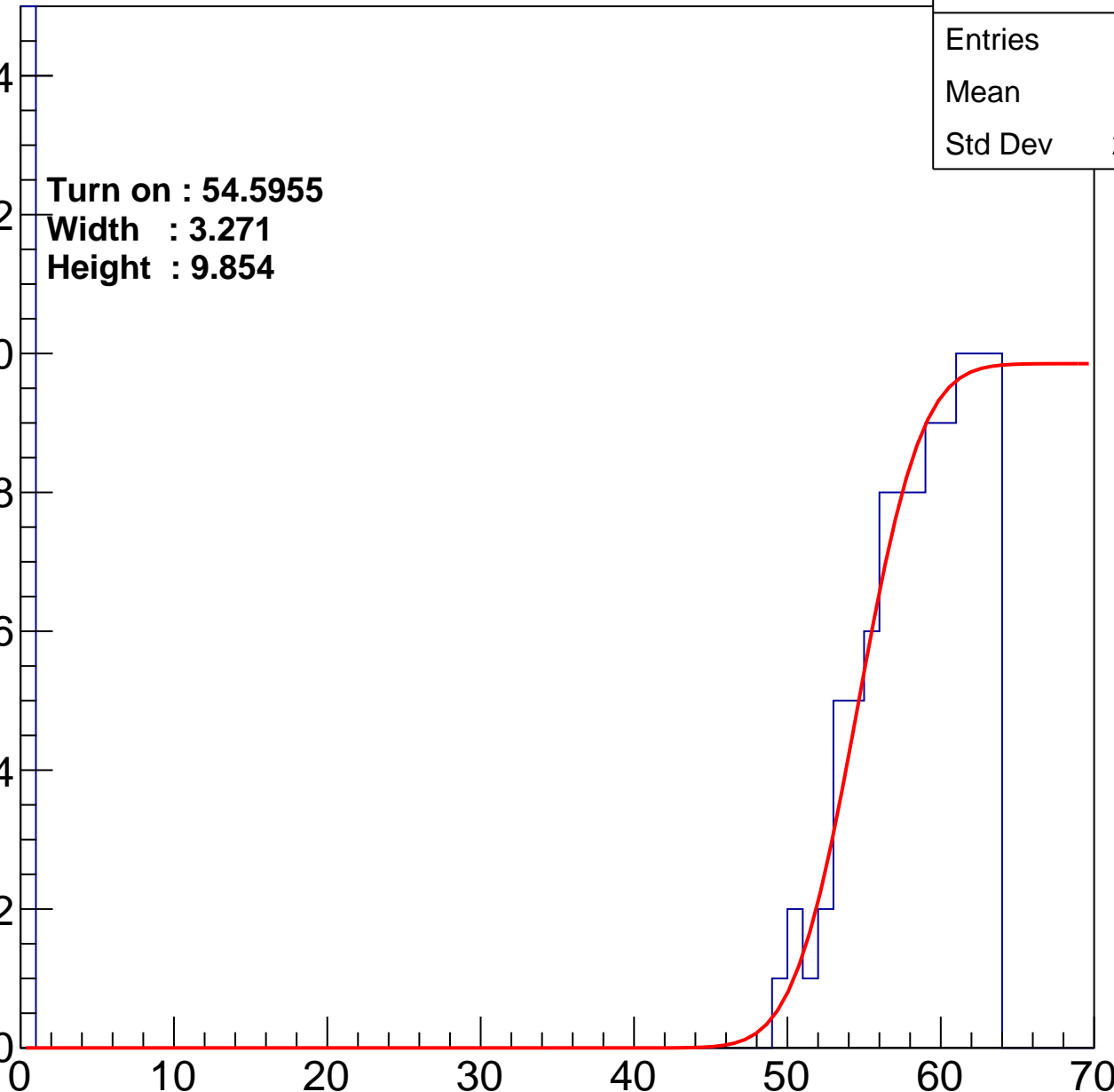
Width : 3.271

Height : 9.854

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch25

calib_packv5_033123_0516.root, FC#4, port A1

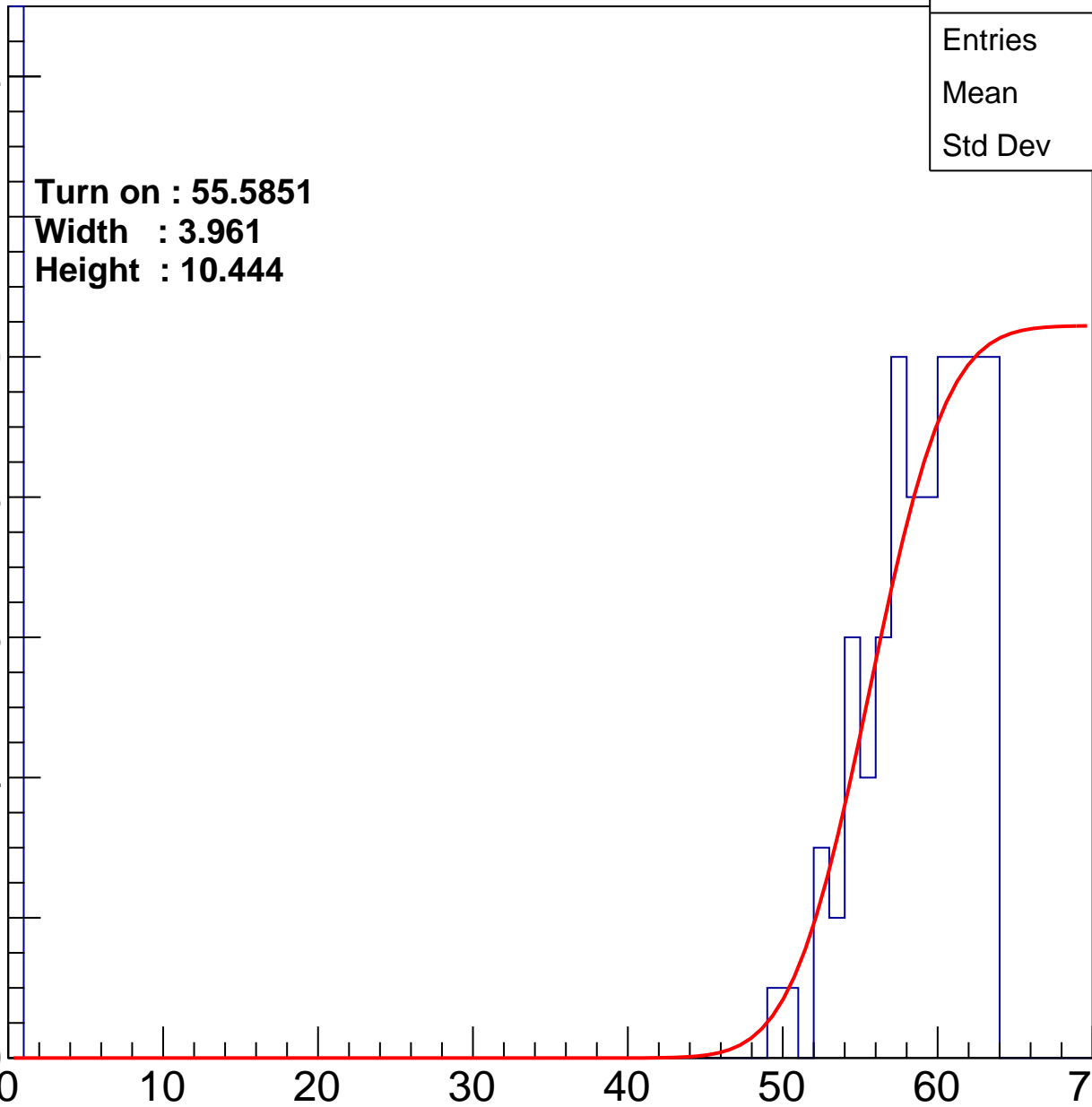
Entry

14
12
10
8
6
4
2
0

Turn on : 55.5851
Width : 3.961
Height : 10.444

Entries	143
Mean	36.41
Std Dev	28.49

ampl



B1L104S, U26-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	33.04
Std Dev	28.71

Turn on : 53.9426

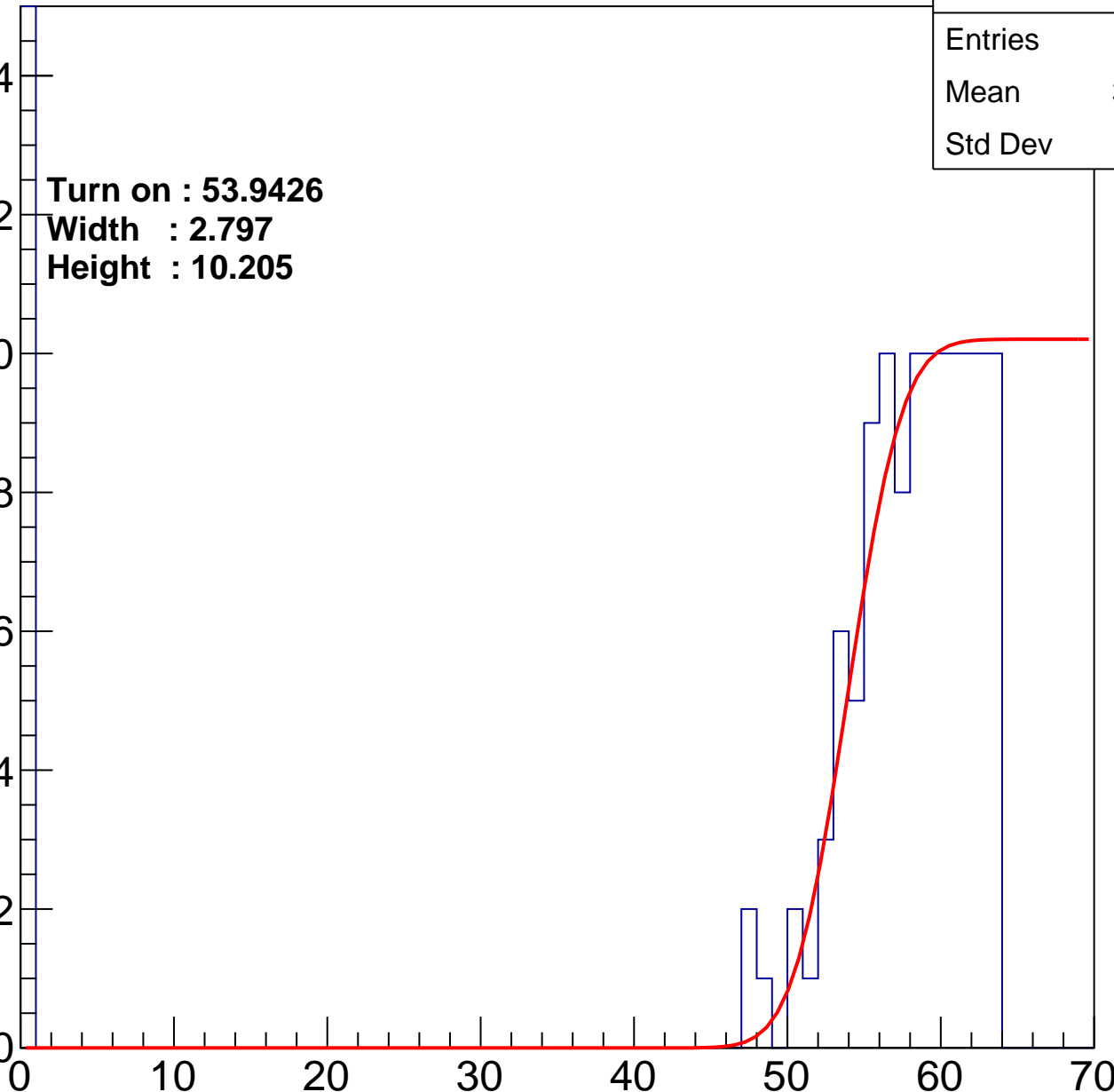
Width : 2.797

Height : 10.205

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	32.98
Std Dev	29.32

Turn on : 55.6754

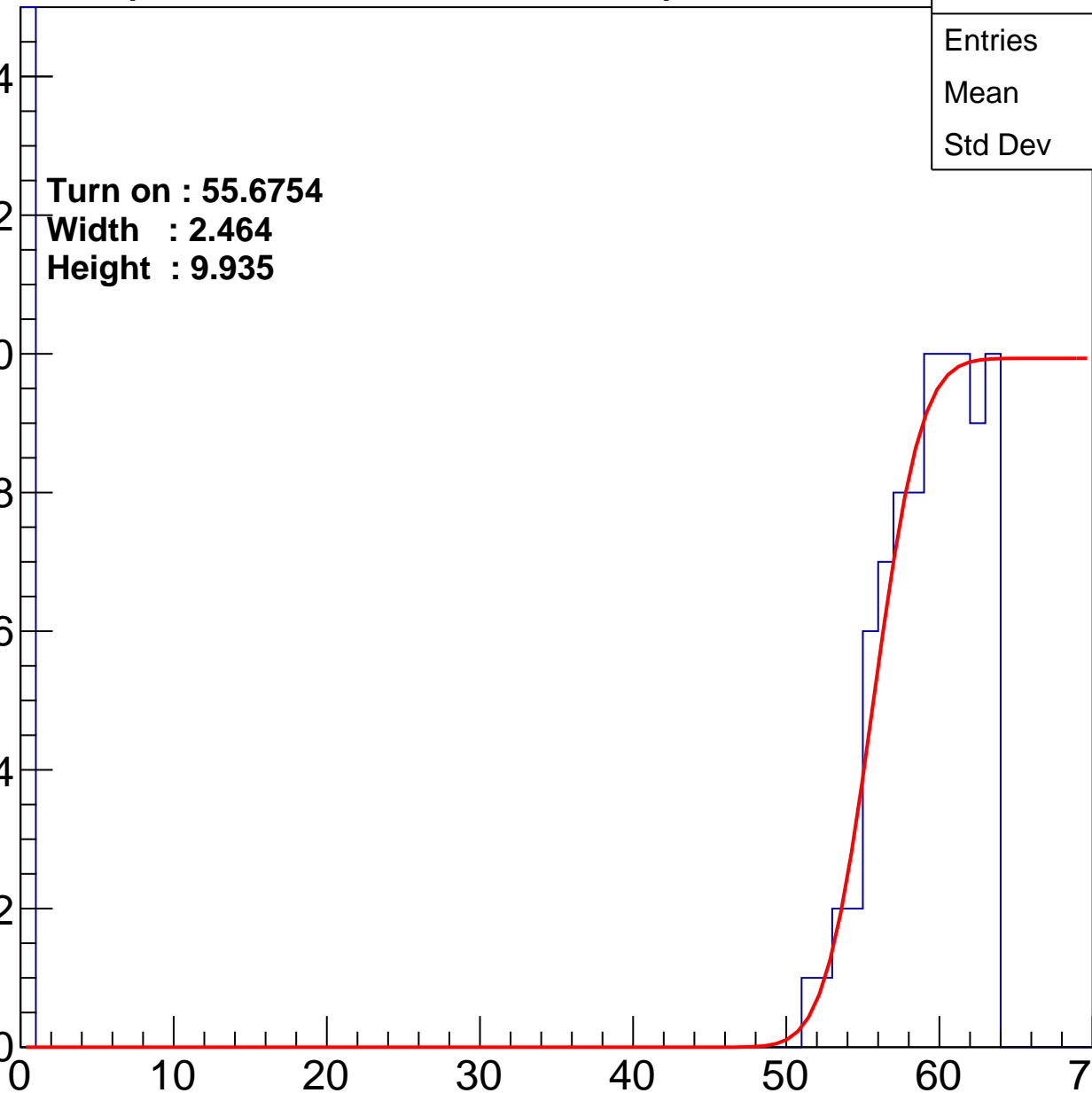
Width : 2.464

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.49
Std Dev	28.86

Turn on : 53.8379

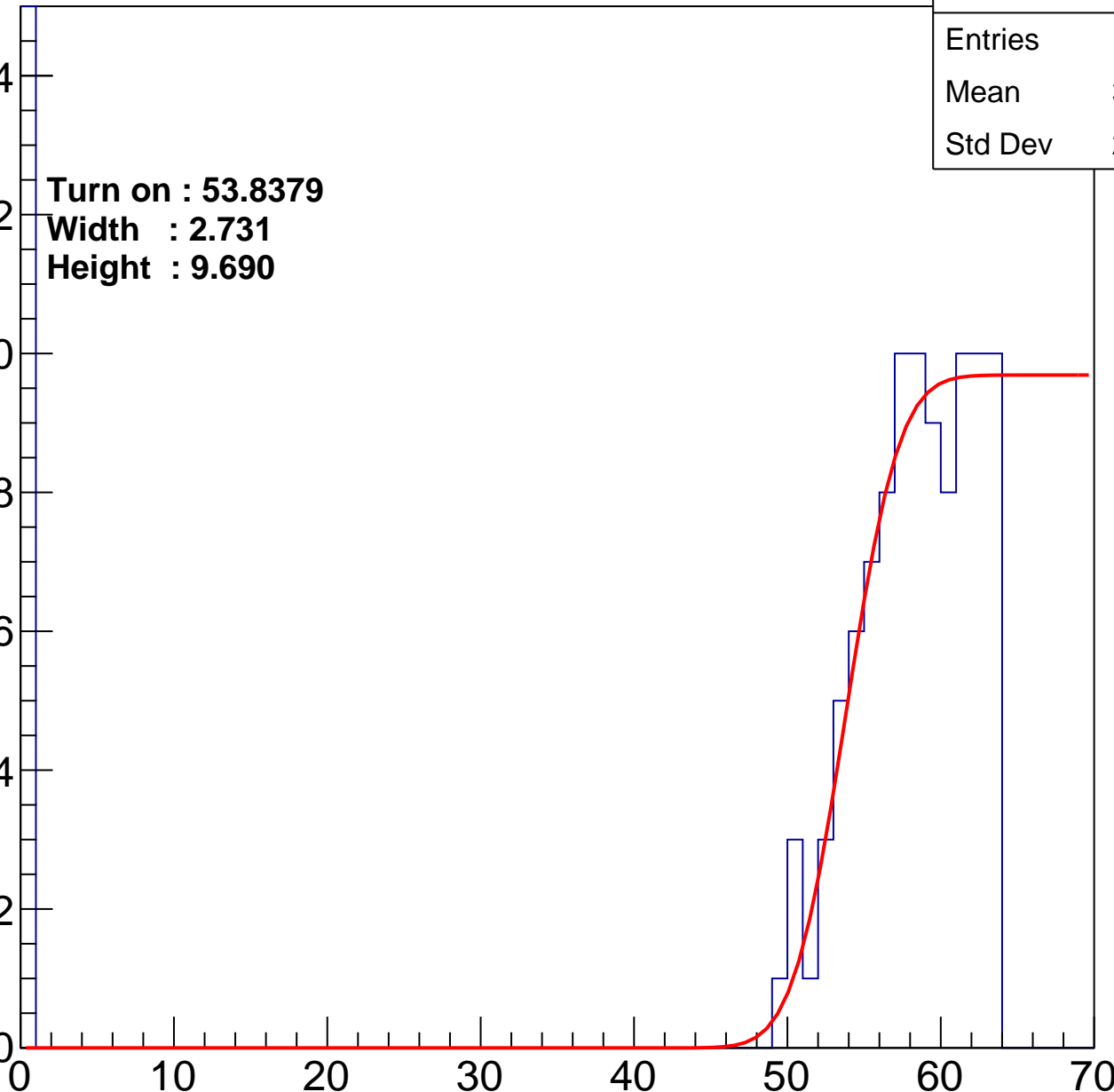
Width : 2.731

Height : 9.690

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	35.47
Std Dev	28.49

Turn on : 55.2095

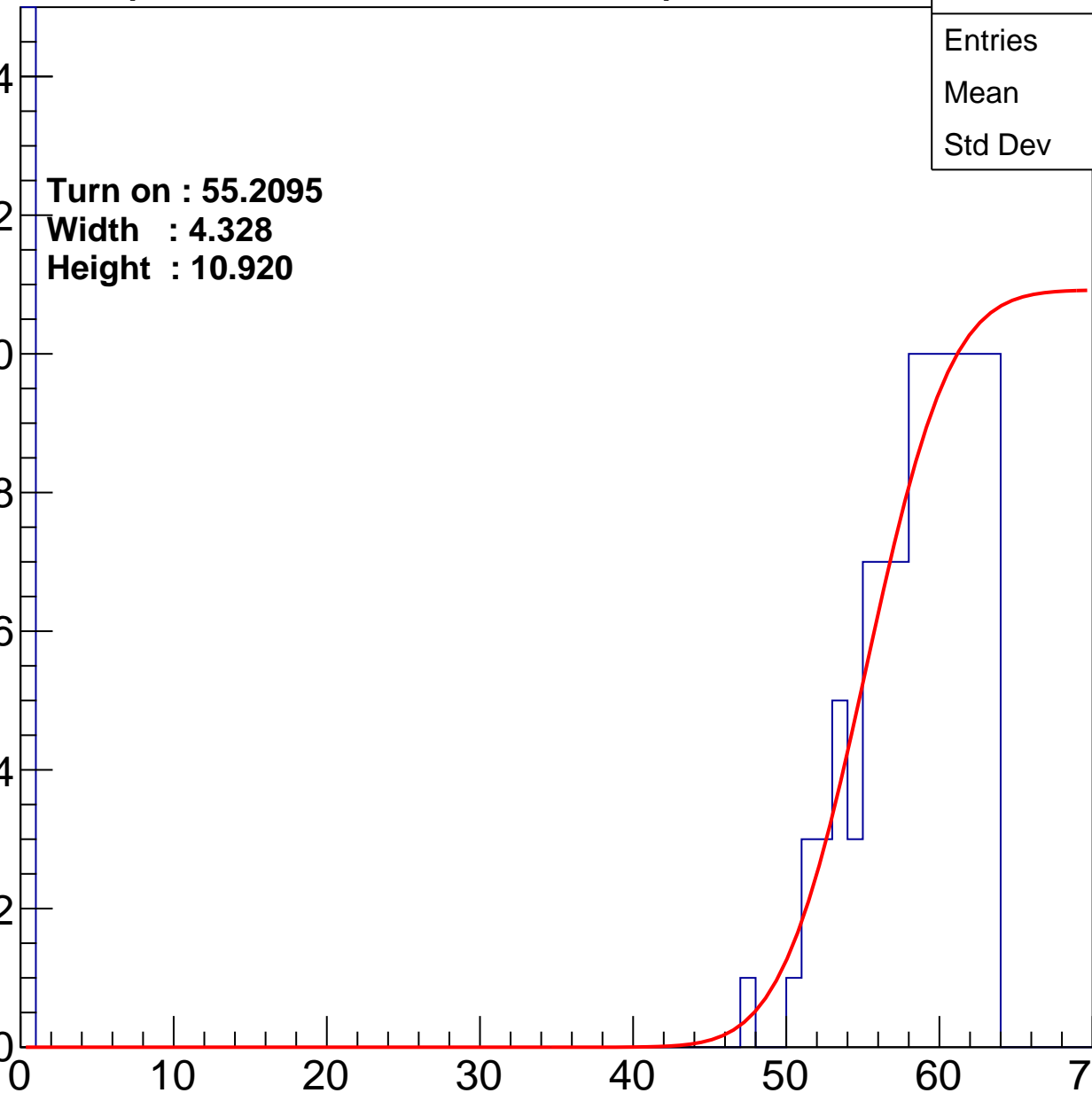
Width : 4.328

Height : 10.920

Entry

14
12
10
8
6
4
2
0

ampl

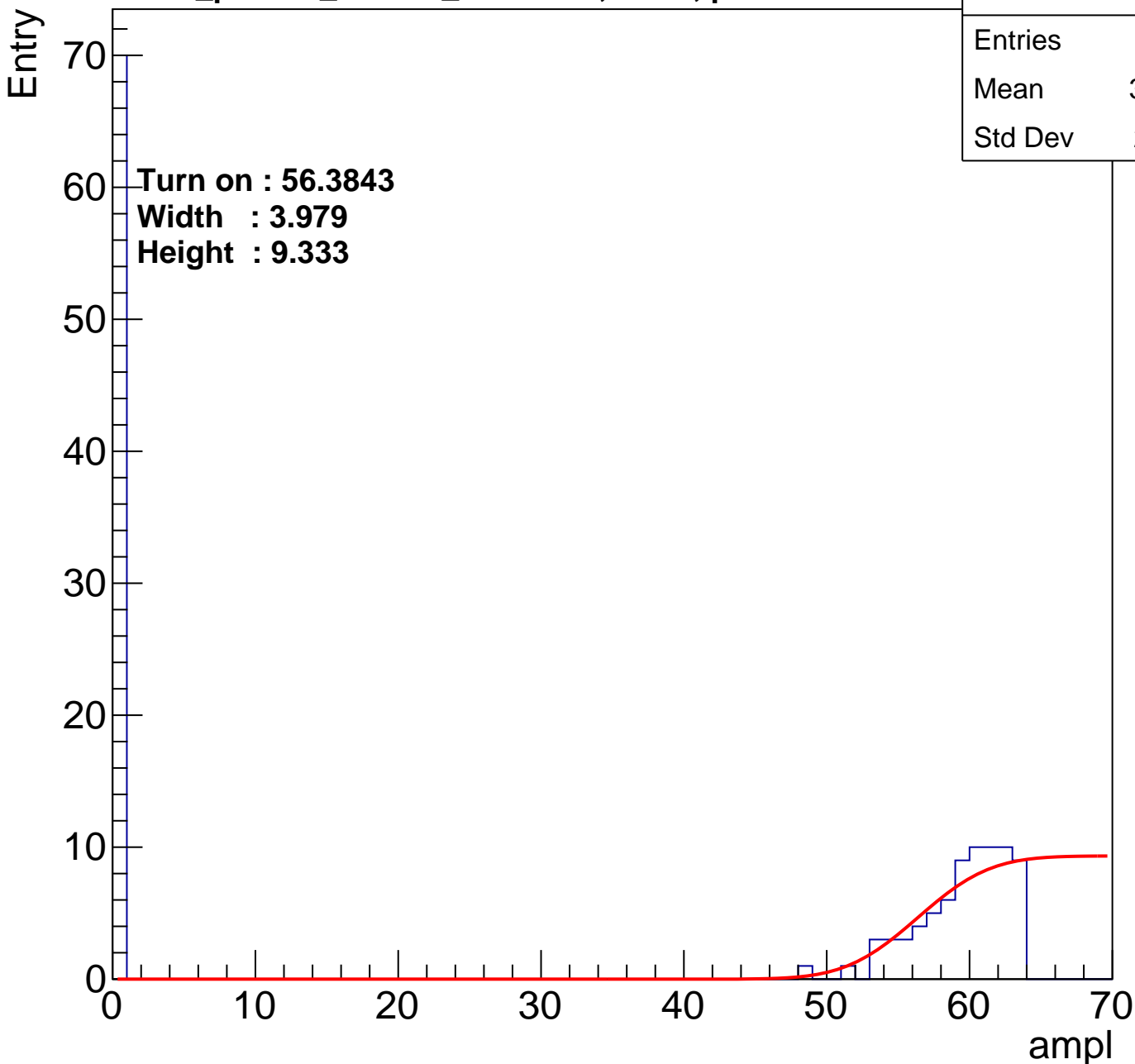


B1L104S, U26-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	144
Mean	30.35
Std Dev	29.61

Turn on : 56.3843
Width : 3.979
Height : 9.333



B1L104S, U26-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	38.5
Std Dev	27.63

Turn on : 53.9154

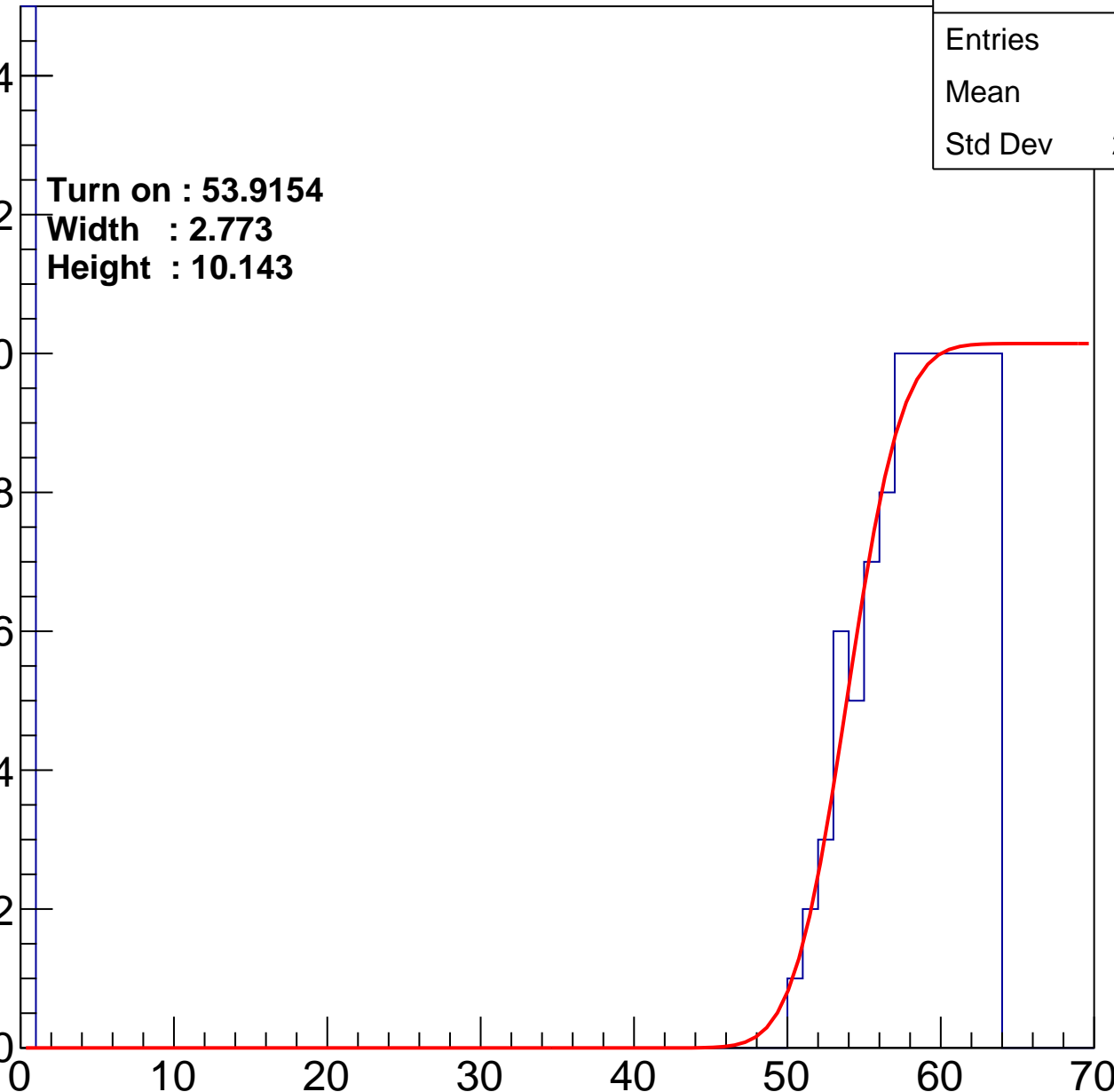
Width : 2.773

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	146
Mean	38.74
Std Dev	27.67

Turn on : 54.5050

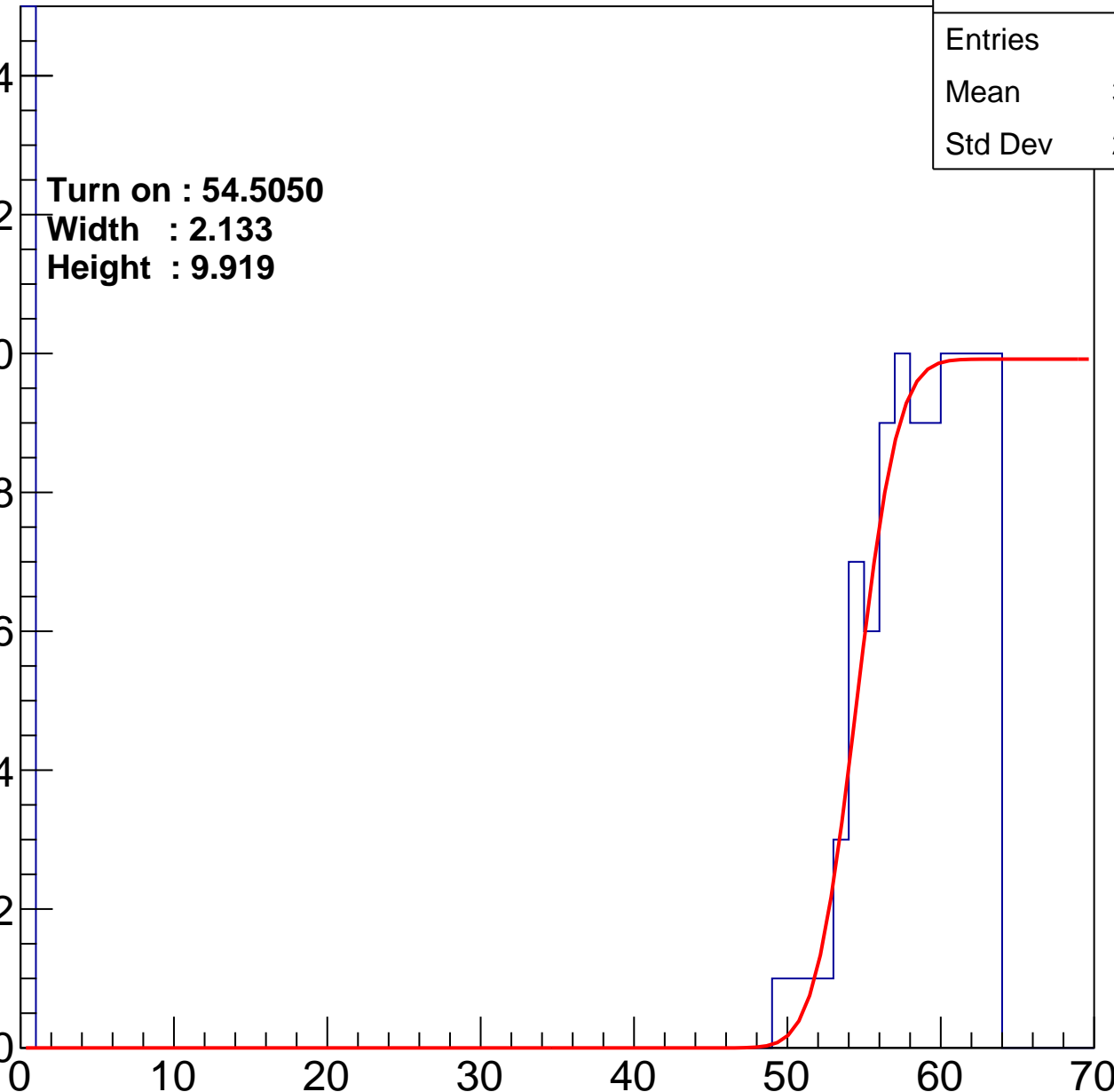
Width : 2.133

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	35.11
Std Dev	28.45

Turn on : 53.4003

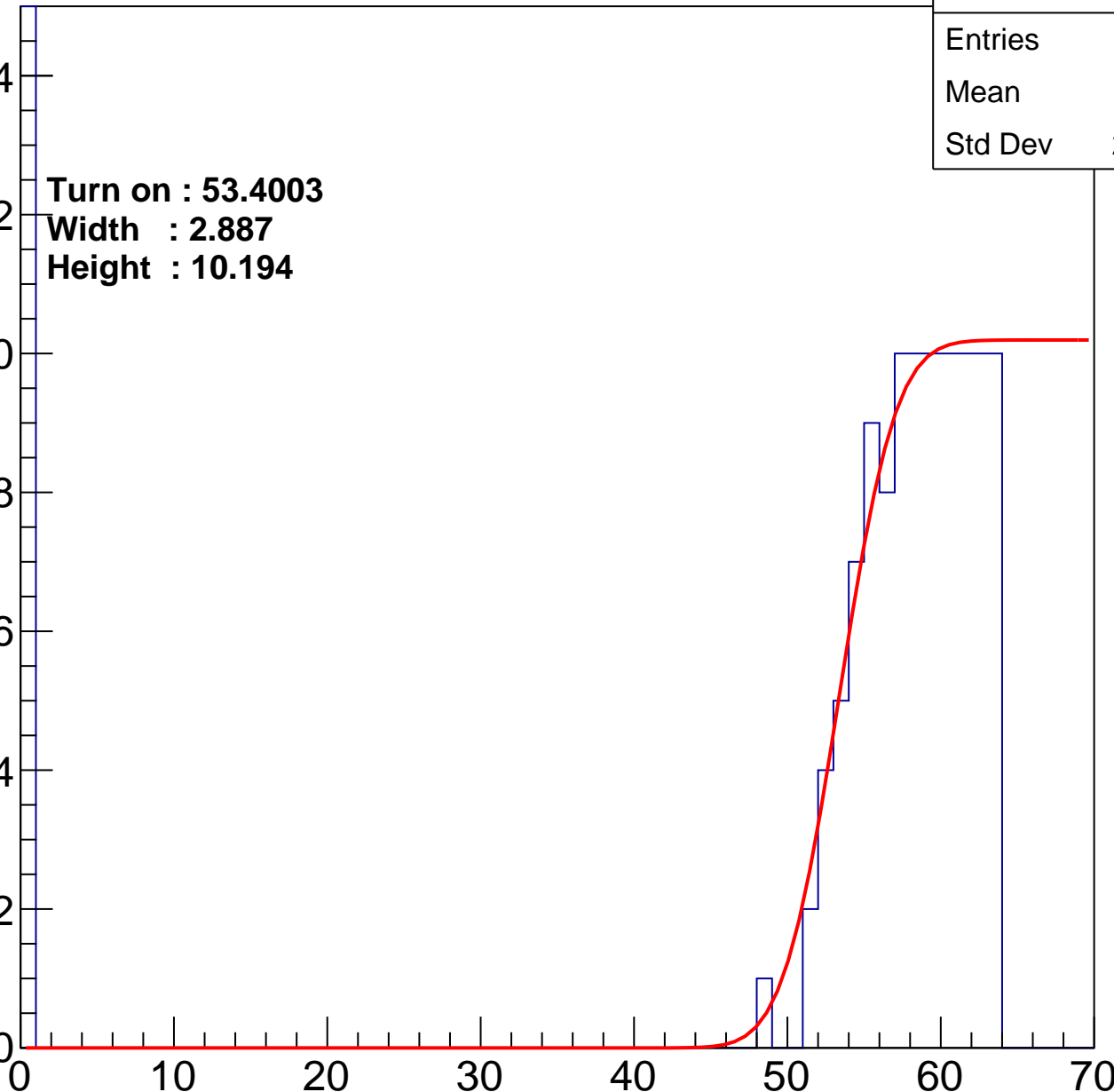
Width : 2.887

Height : 10.194

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	40.12
Std Dev	25.99

Turn on : 51.7502

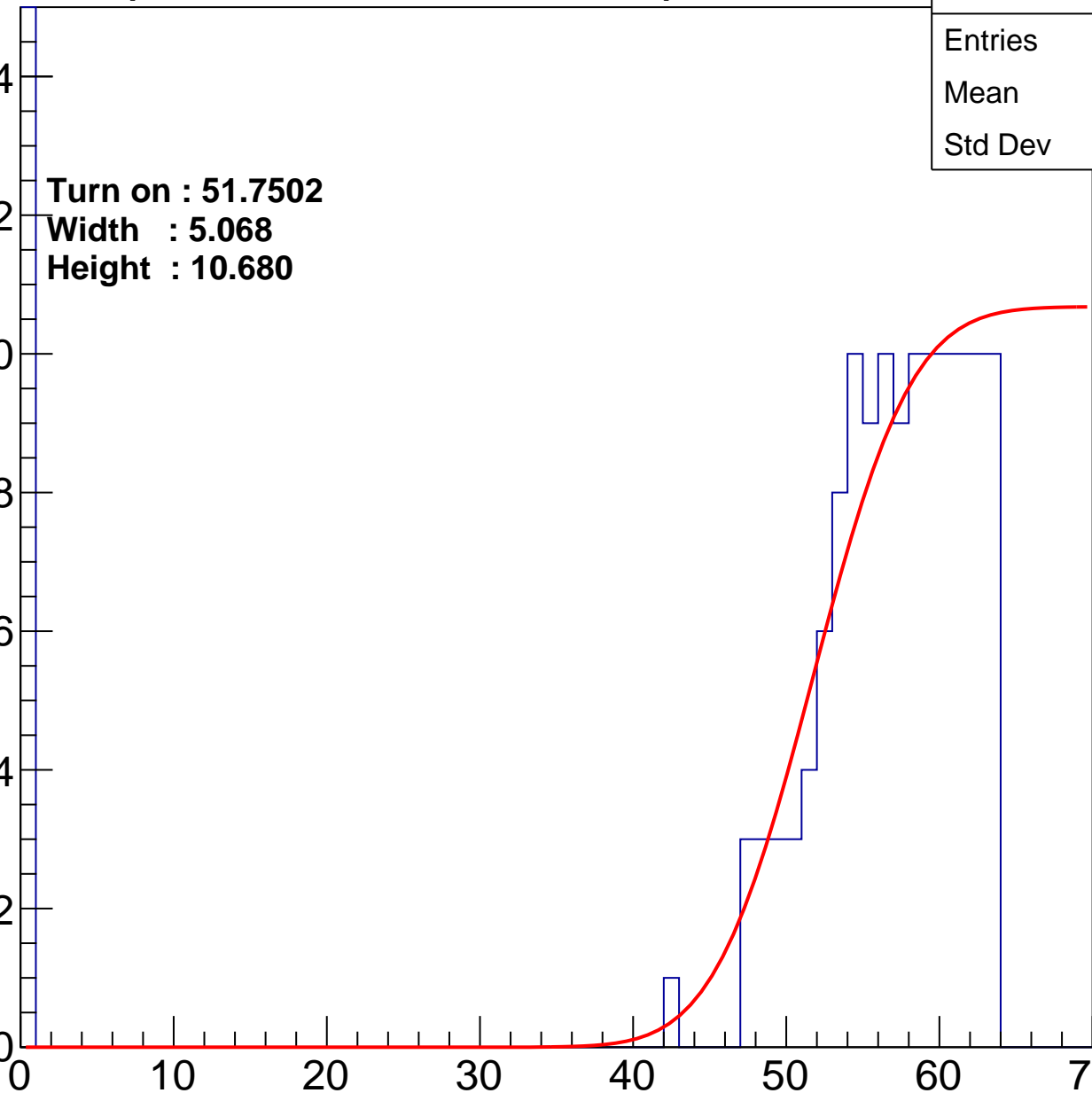
Width : 5.068

Height : 10.680

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch35

calib_packv5_033123_0516.root, FC#4, port A1

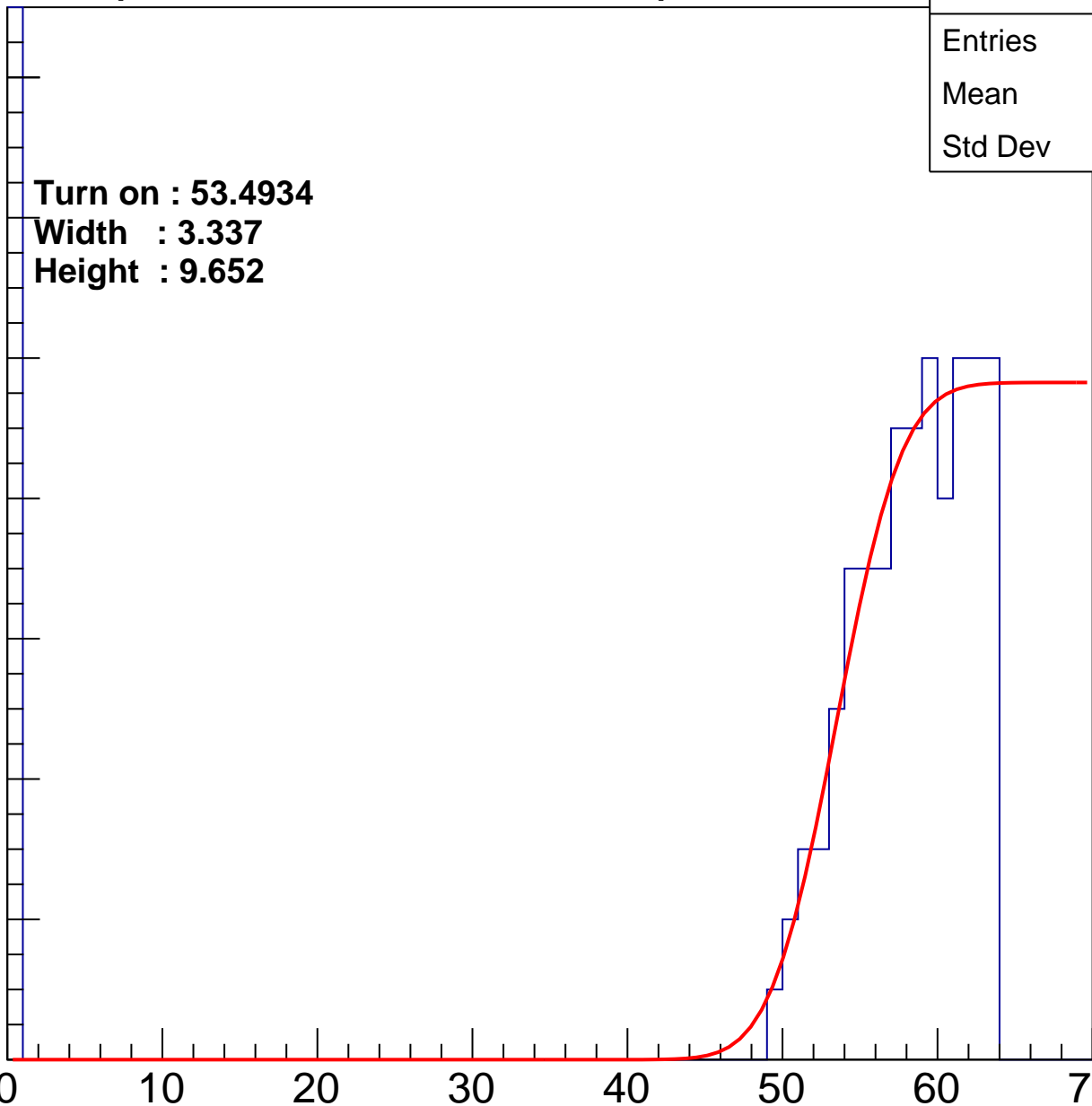
Entry

14
12
10
8
6
4
2
0

Turn on : 53.4934
Width : 3.337
Height : 9.652

Entries	165
Mean	35.41
Std Dev	28.33

ampl



B1L104S, U26-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	33.8
Std Dev	29

Turn on : 54.6959

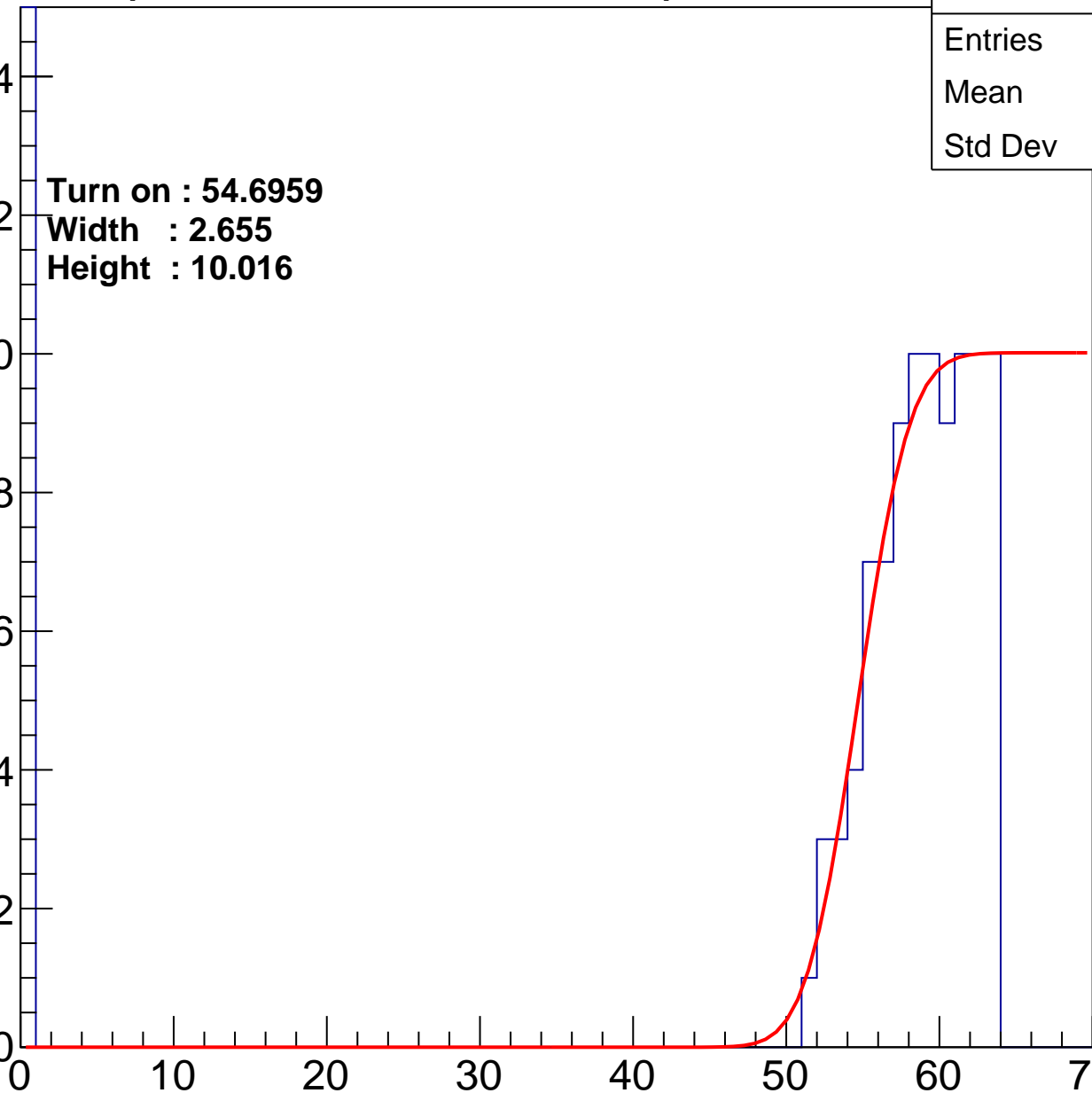
Width : 2.655

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	36.22
Std Dev	28.32

Turn on : 54.7009

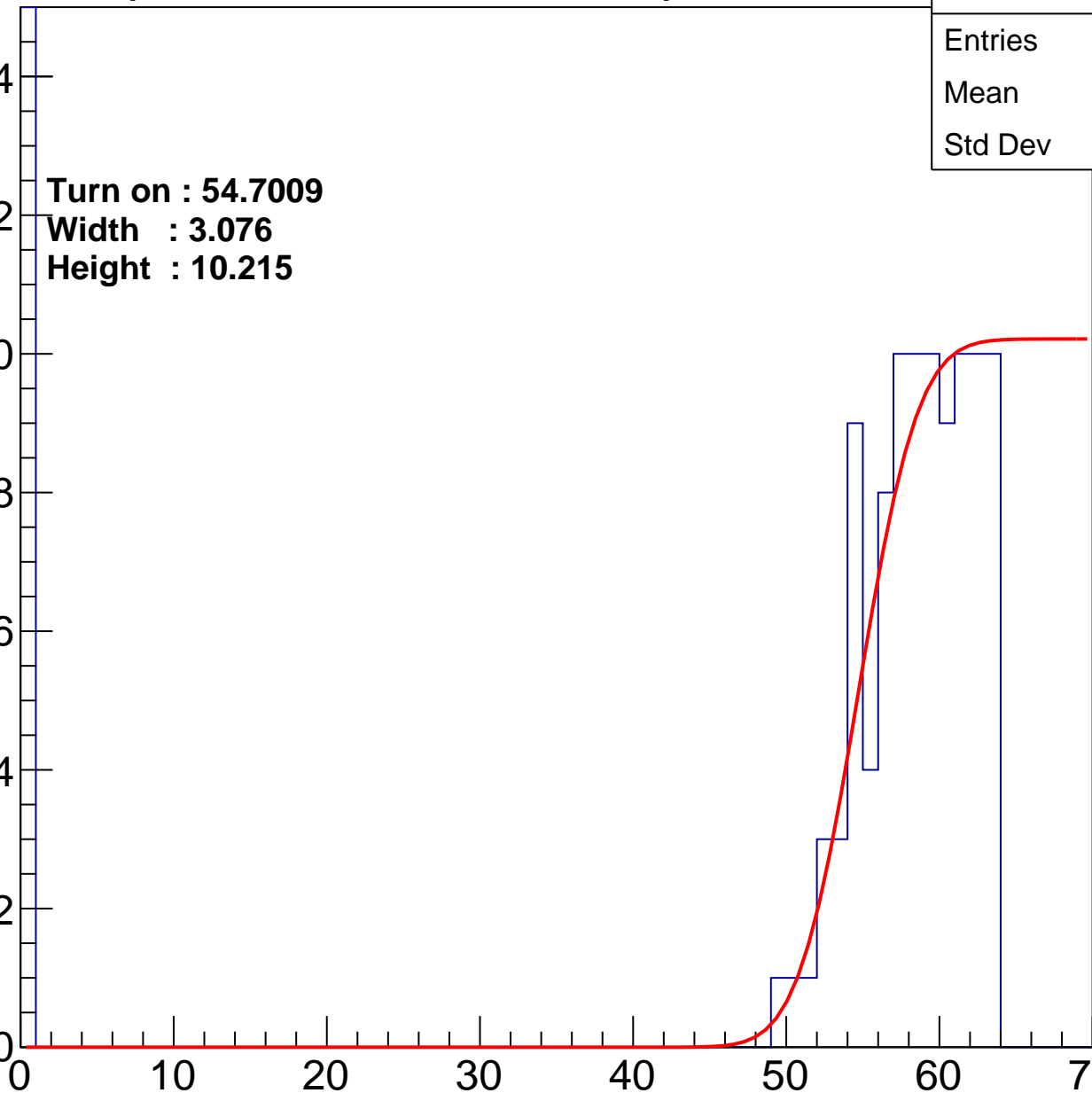
Width : 3.076

Height : 10.215

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	39.06
Std Dev	26.75

Turn on : 51.5321

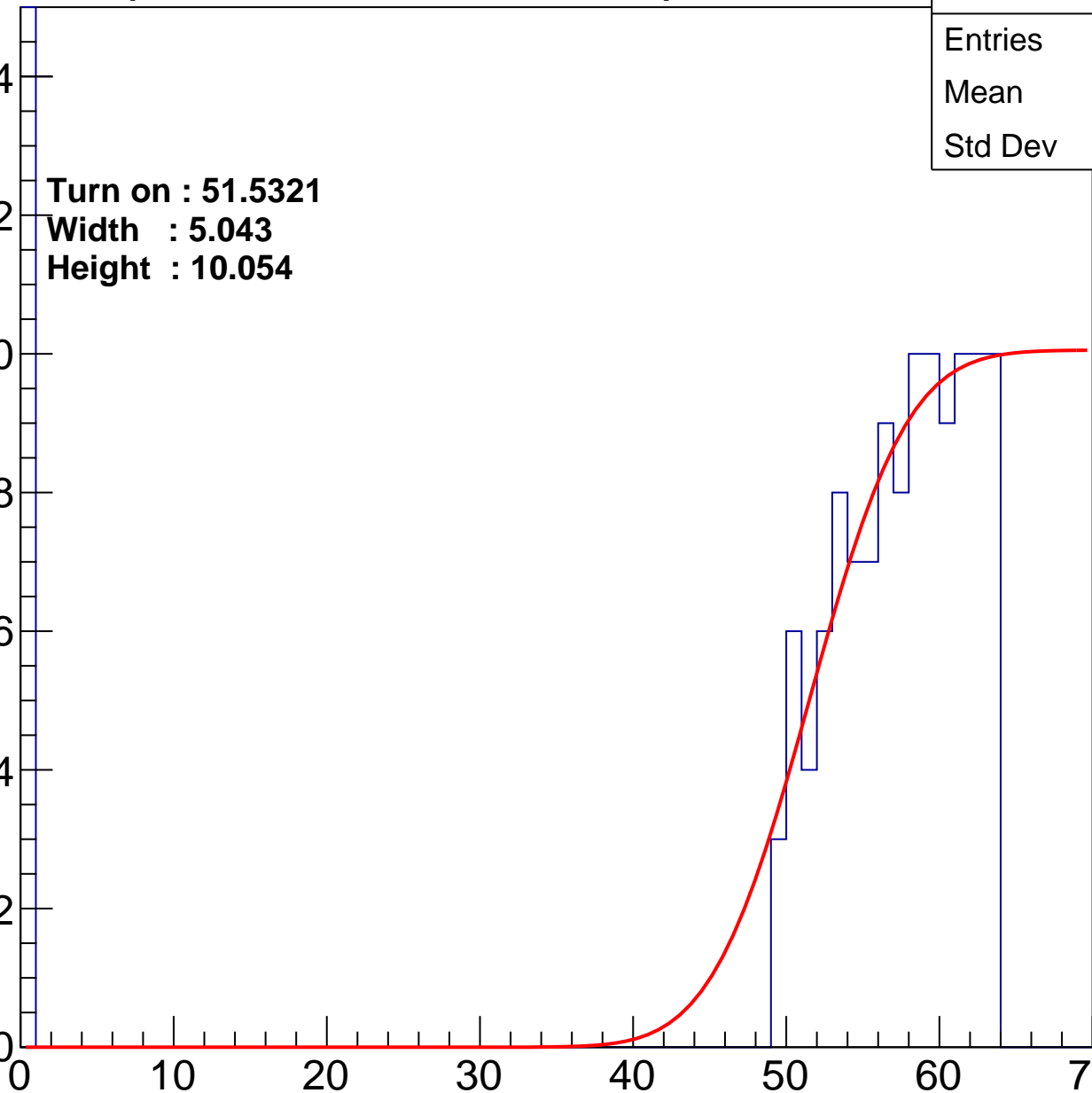
Width : 5.043

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	125
Mean	35.44
Std Dev	29.04

Turn on : 56.9654

Width : 2.943

Height : 9.667

Entry

50

40

30

20

10

0

0

10

20

30

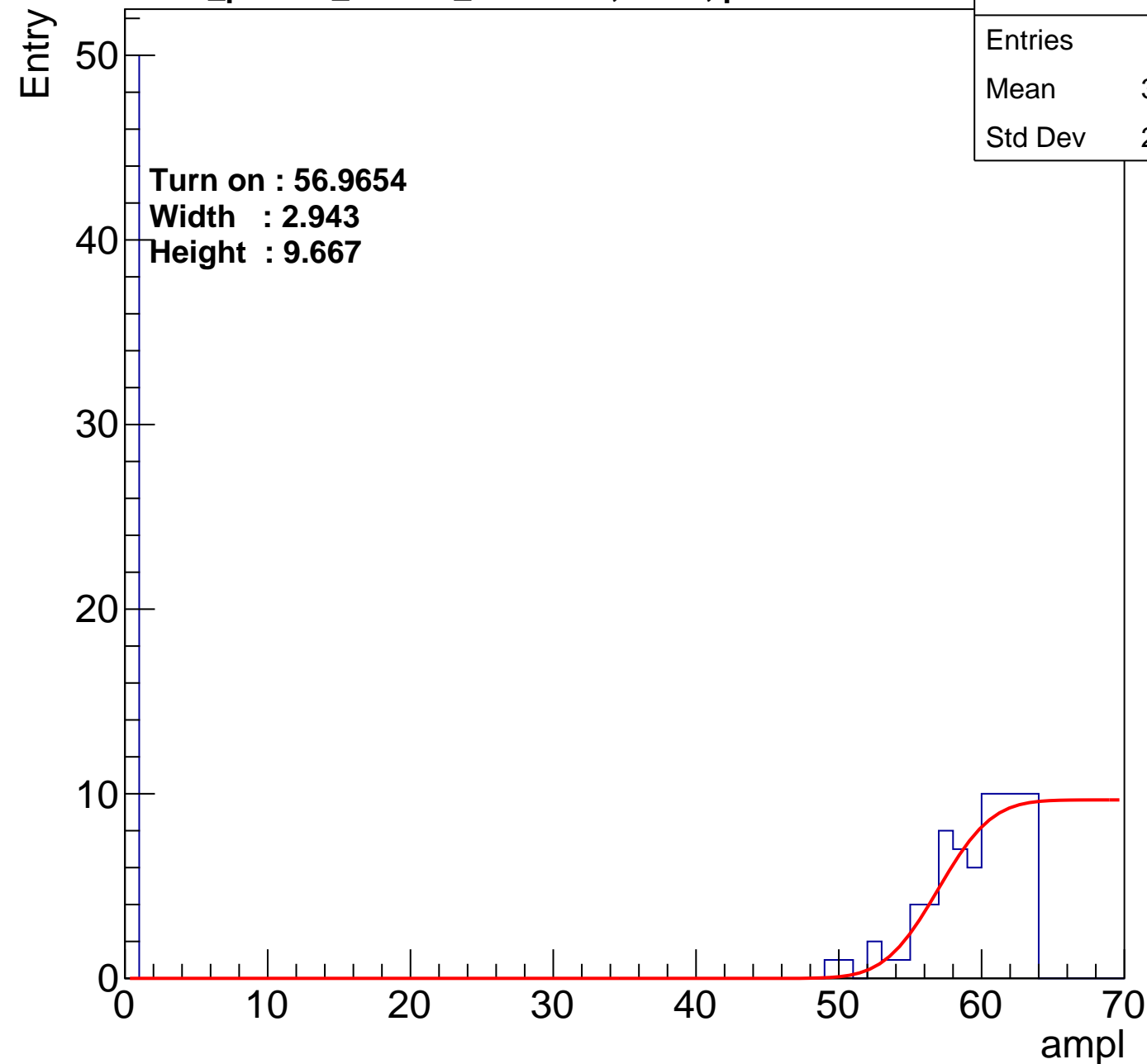
40

50

60

70

ampl



B1L104S, U26-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	31.98
Std Dev	29.12

Turn on : 55.4111

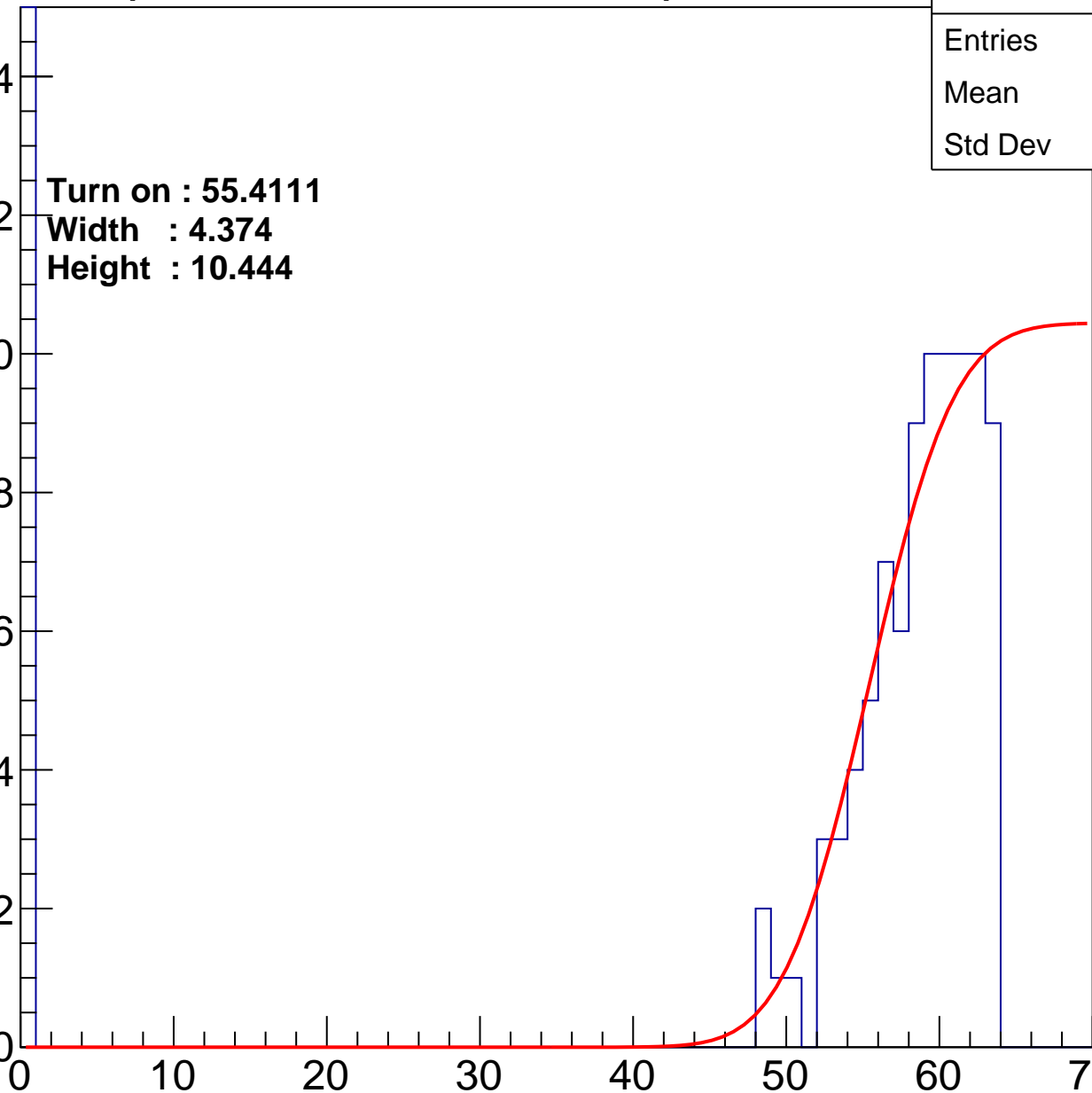
Width : 4.374

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	33.92
Std Dev	28.82

Turn on : 54.6129

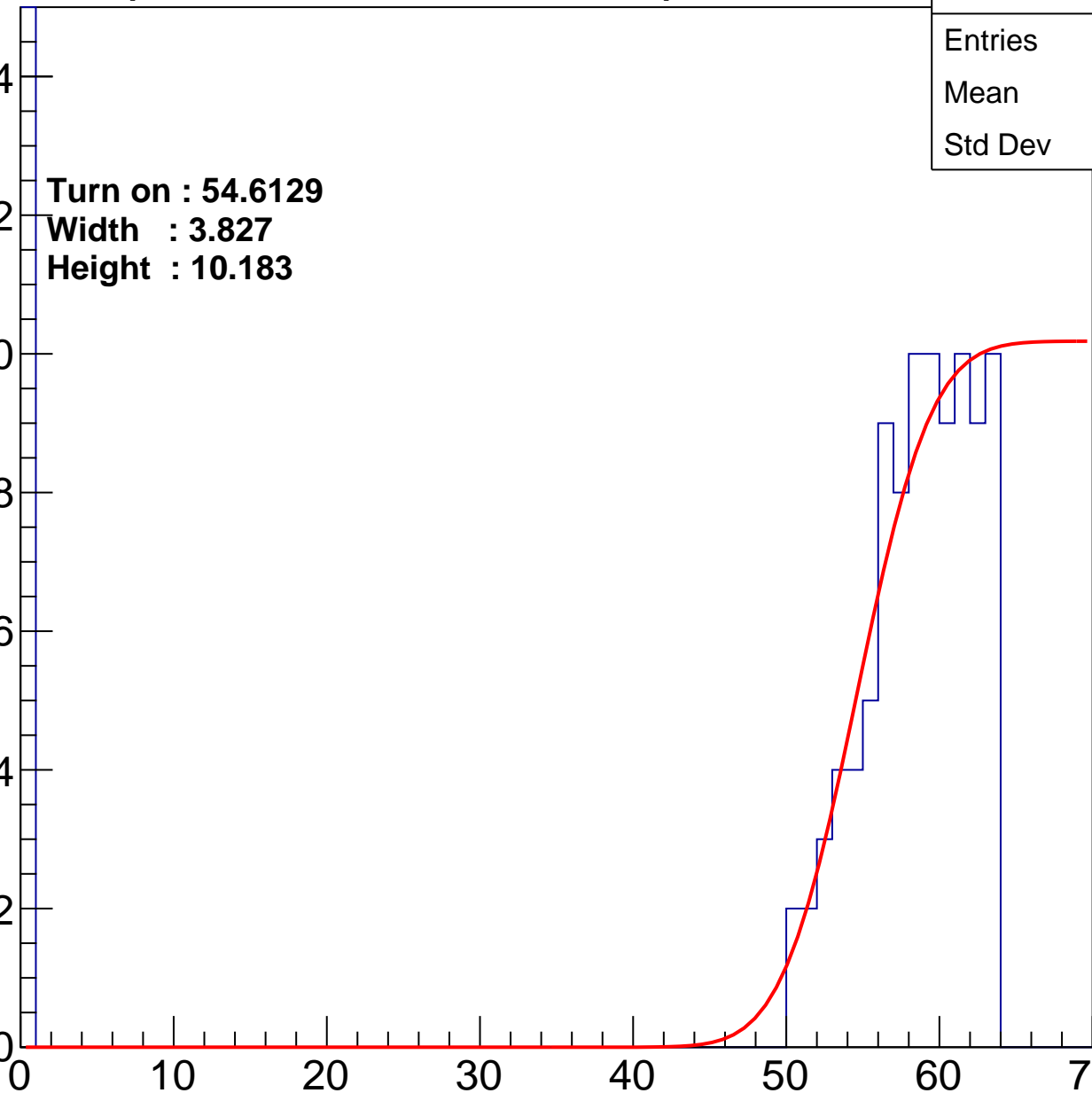
Width : 3.827

Height : 10.183

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	40.38
Std Dev	25.82

Turn on : 51.3790

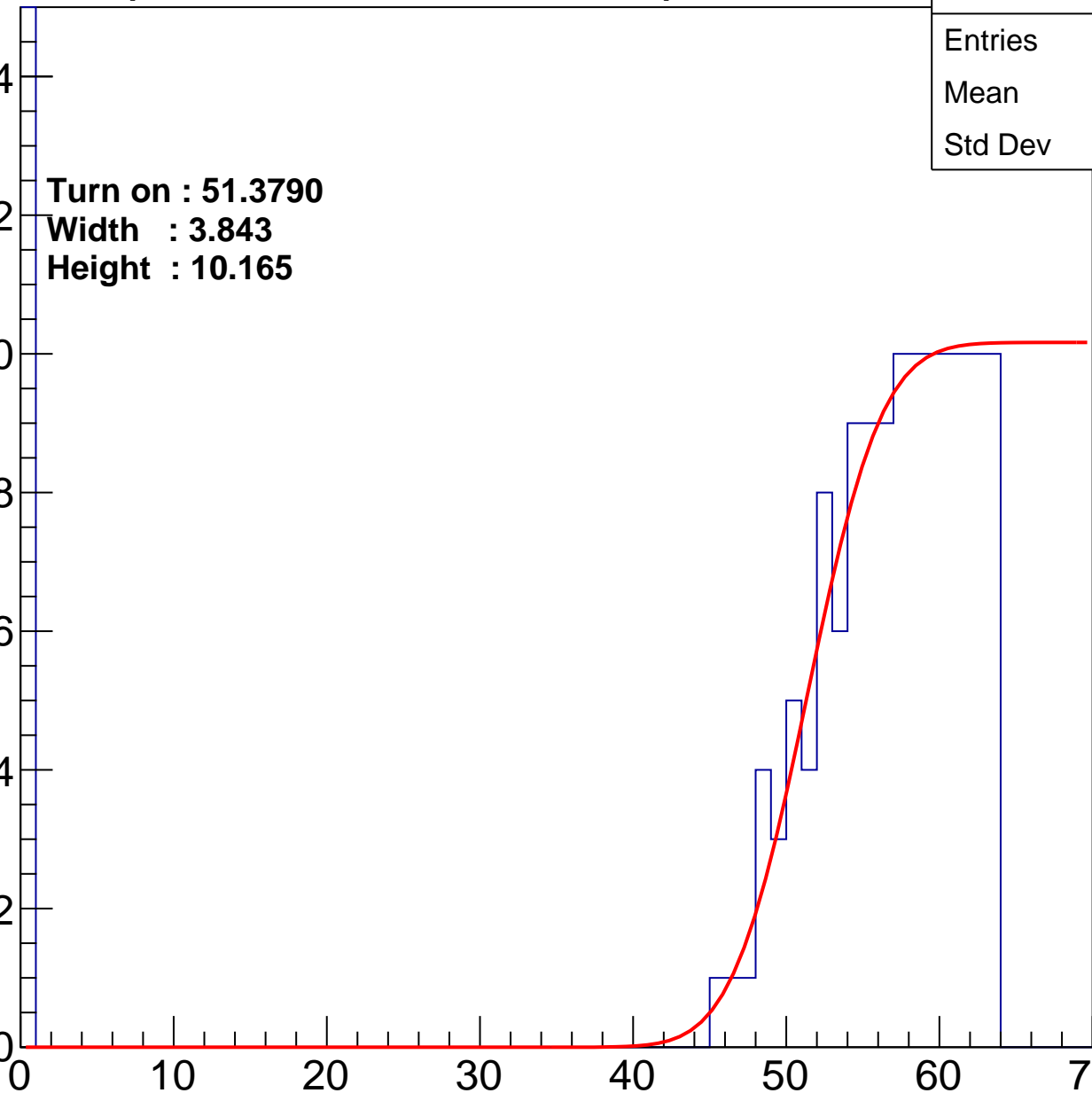
Width : 3.843

Height : 10.165

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	34.16
Std Dev	28.73

Turn on : 54.8701

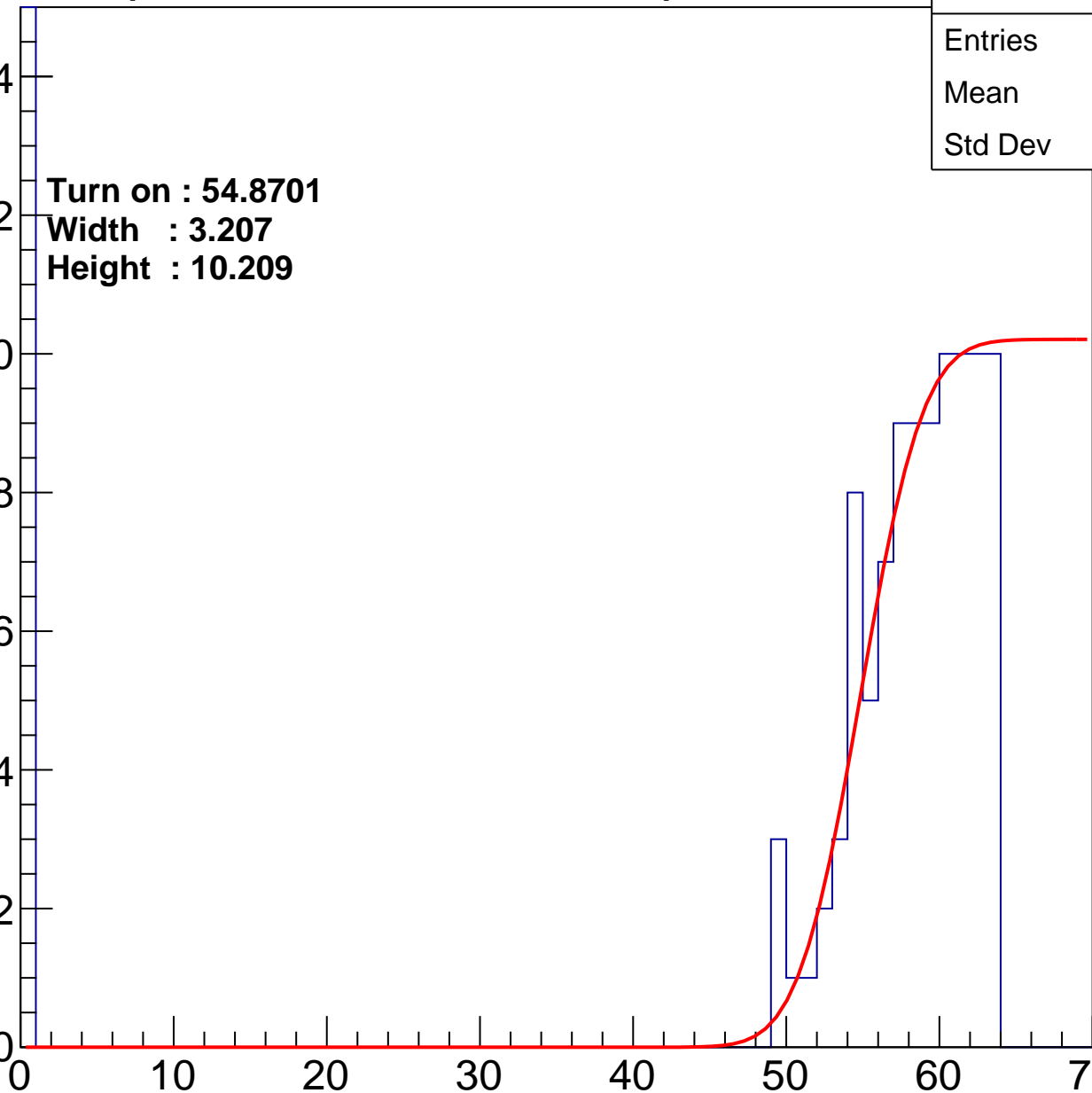
Width : 3.207

Height : 10.209

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	35.98
Std Dev	28.12

Turn on : 53.9987

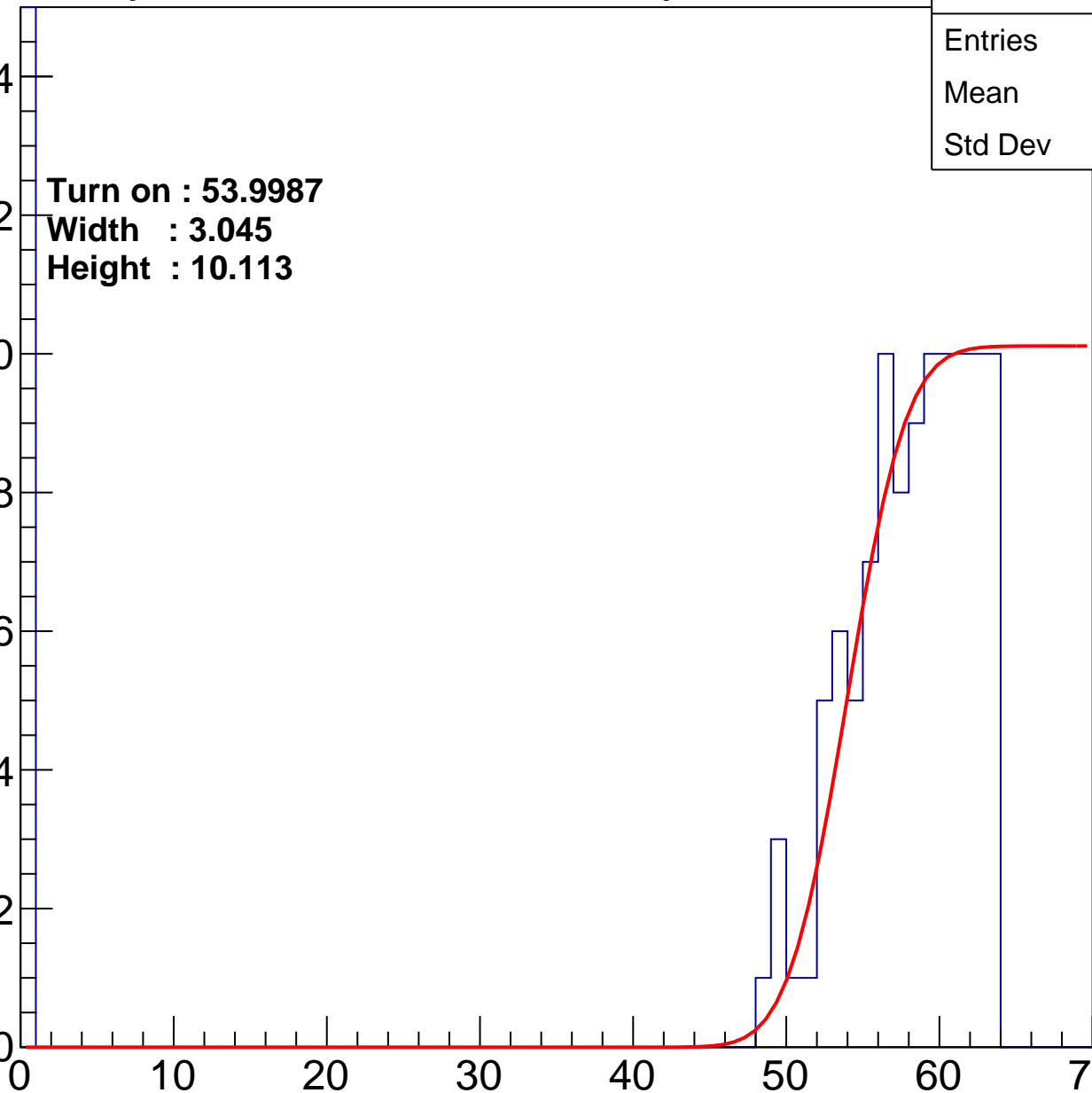
Width : 3.045

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	36.47
Std Dev	27.88

Turn on : 52.3959

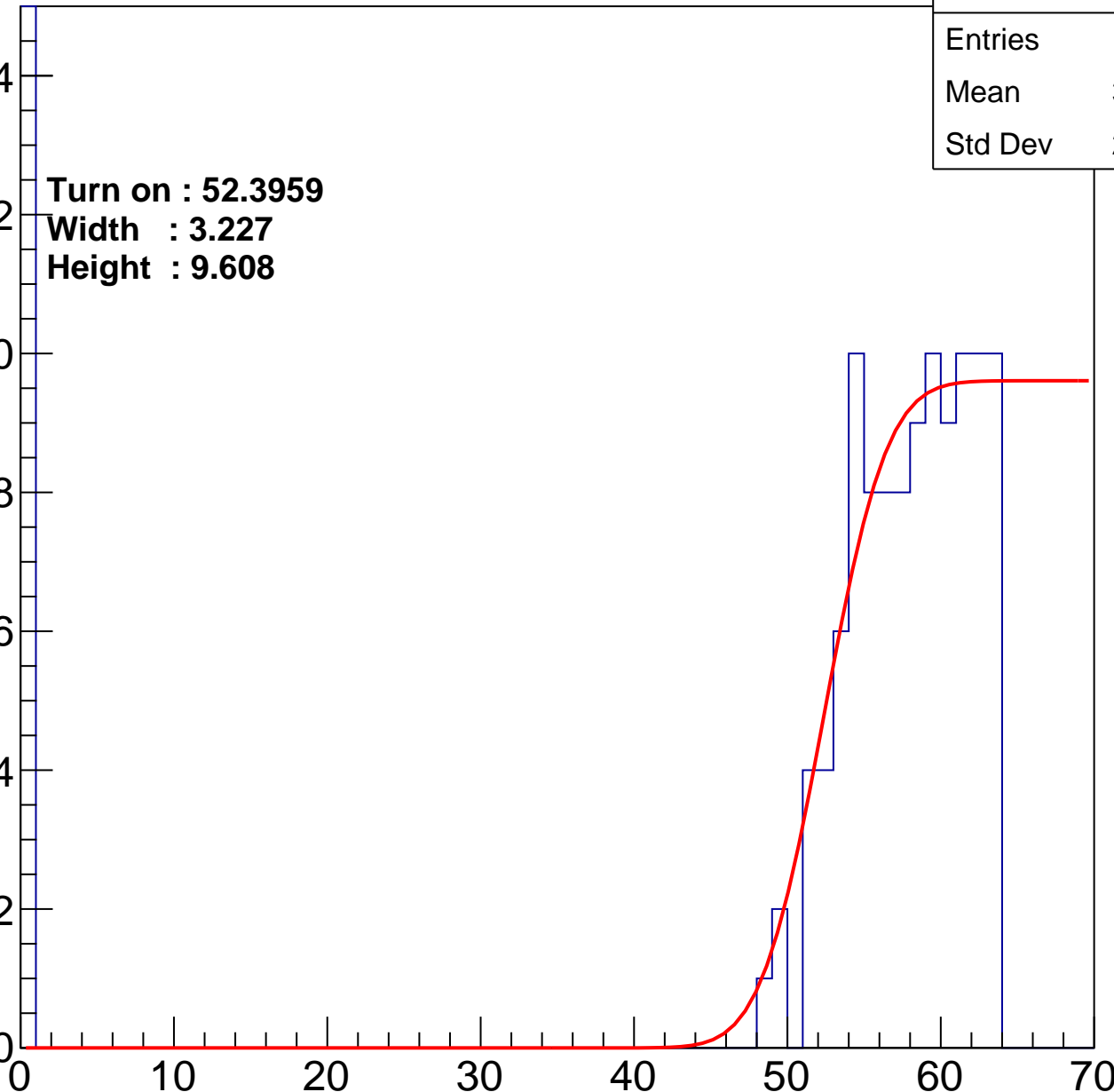
Width : 3.227

Height : 9.608

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	148
Mean	33.95
Std Dev	28.94

Turn on : 54.9799

Width : 2.672

Height : 9.206

Entry

60

50

40

30

20

10

0

0

10

20

30

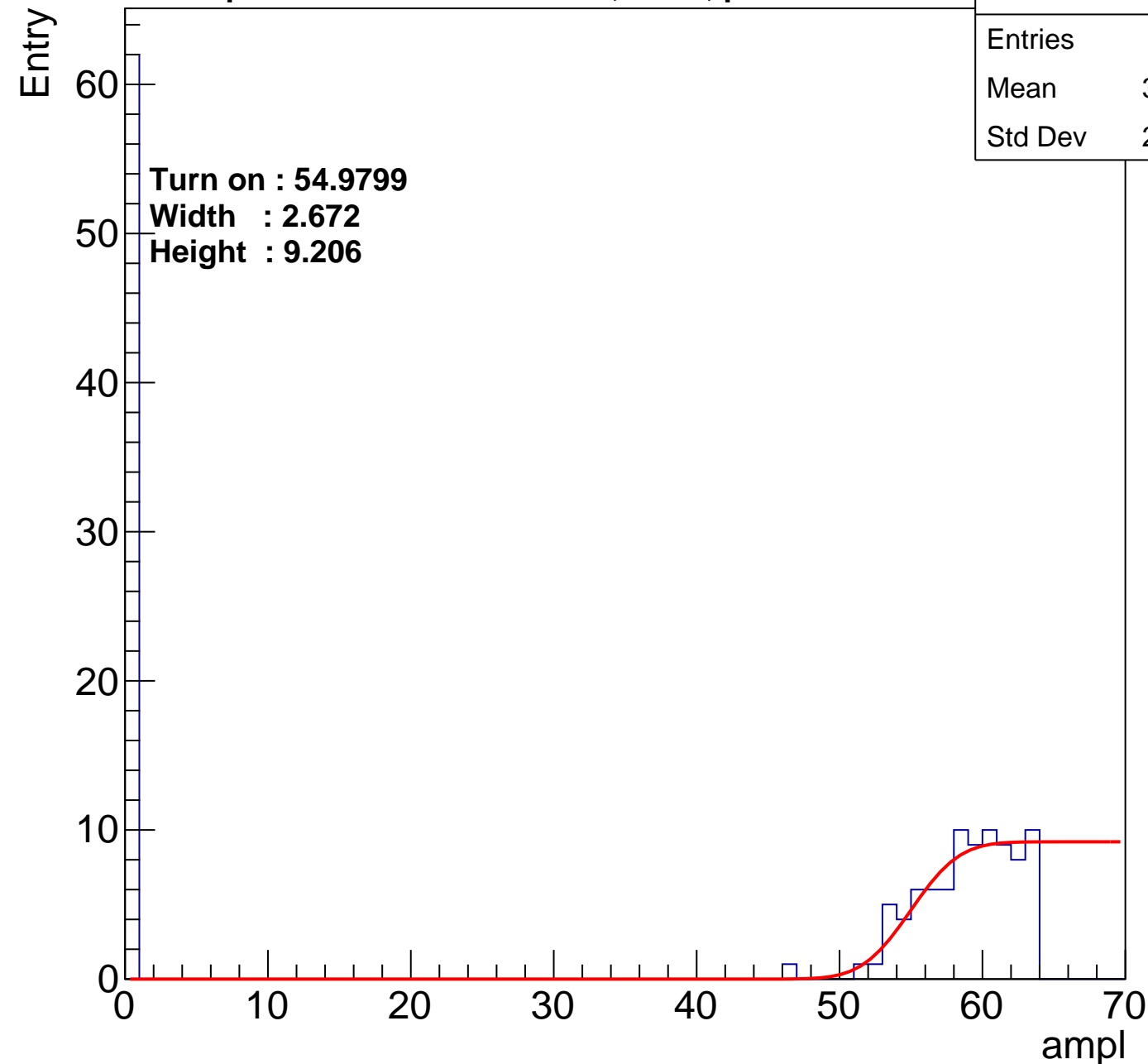
40

50

60

70

ampl



B1L104S, U26-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	38.08
Std Dev	26.93

Turn on : 51.5539

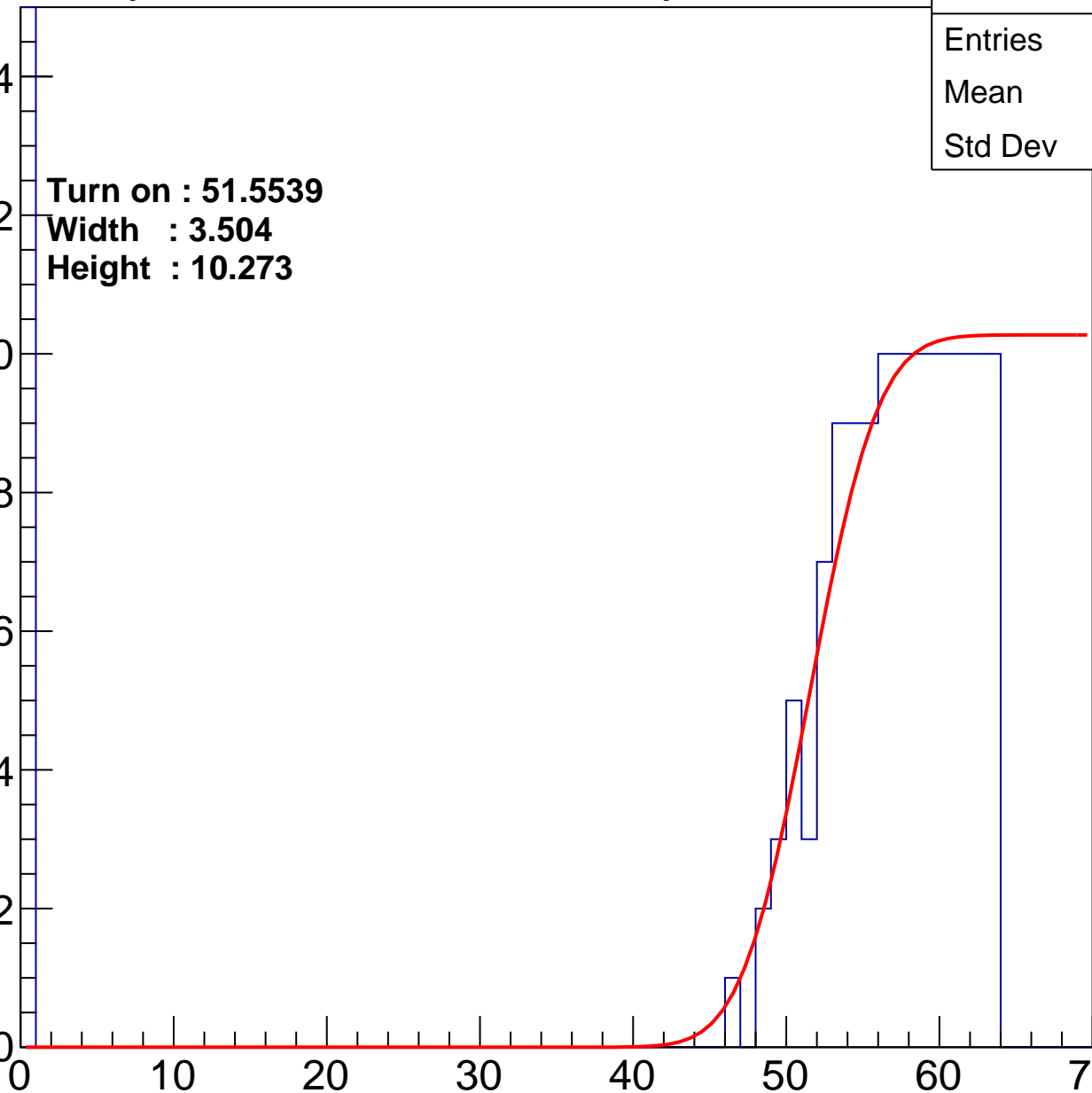
Width : 3.504

Height : 10.273

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	36.88
Std Dev	27.68

Turn on : 55.2581

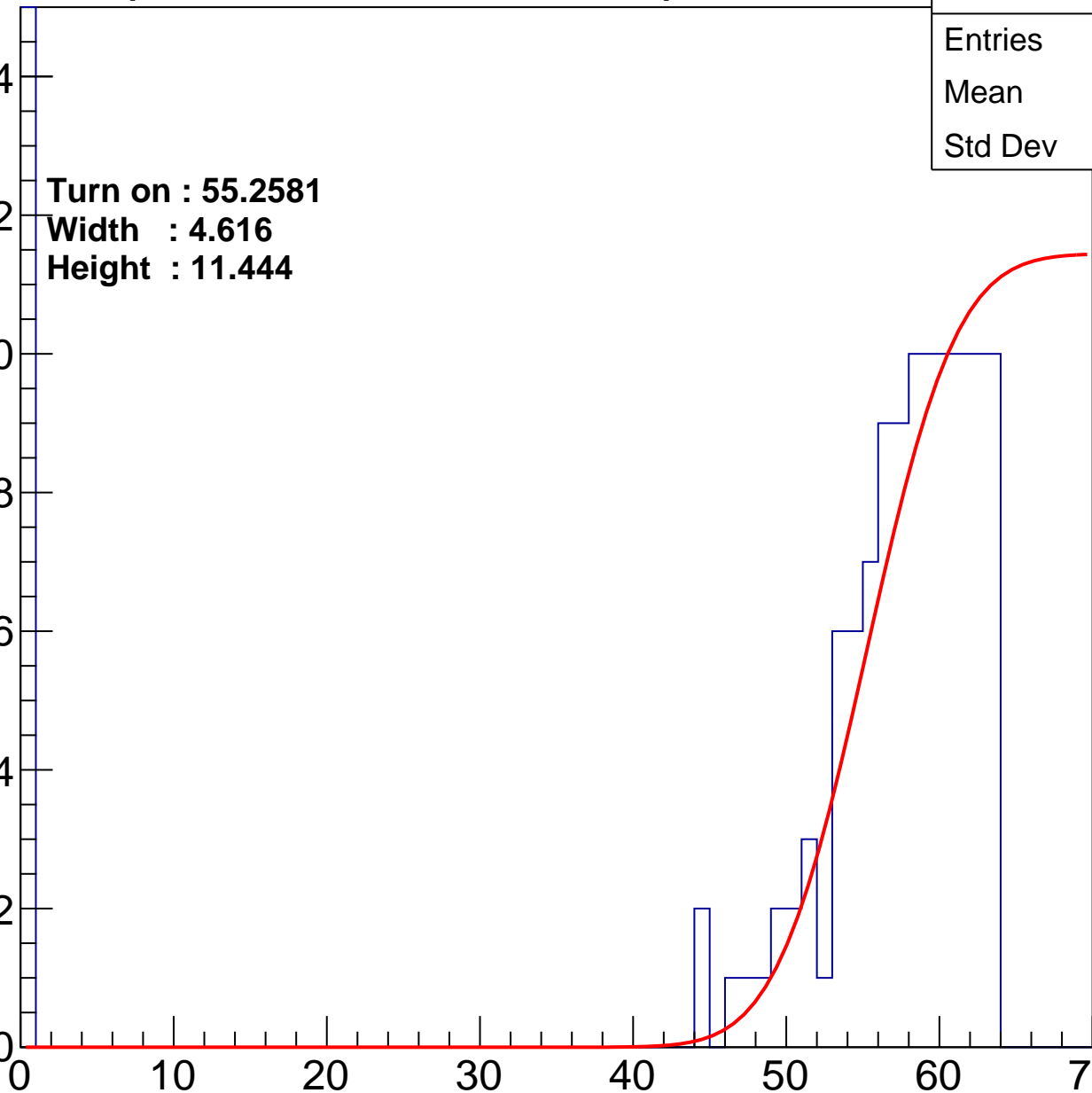
Width : 4.616

Height : 11.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch49

calib_packv5_033123_0516.root, FC#4, port A1

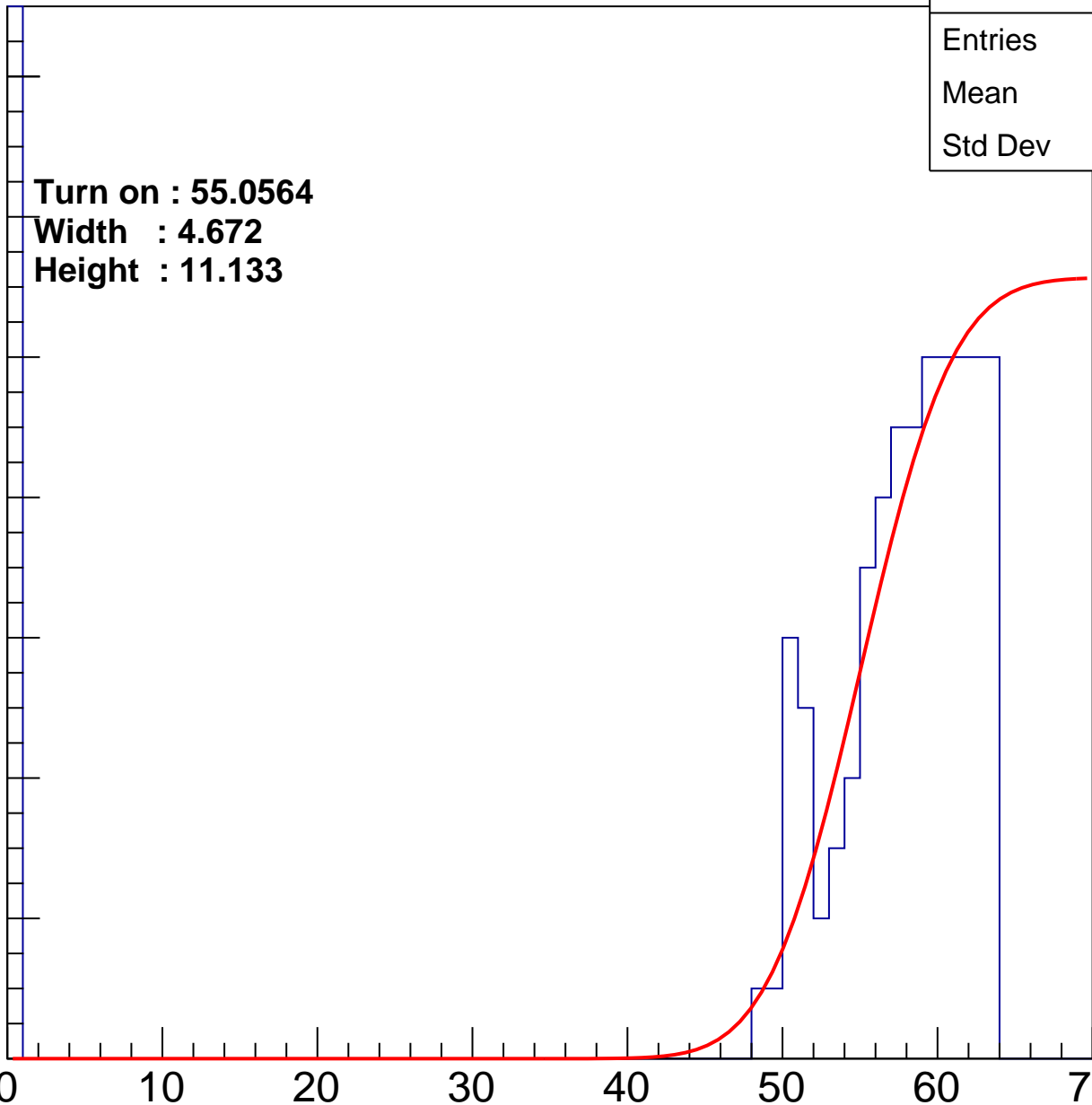
Entry

14
12
10
8
6
4
2
0

Turn on : 55.0564
Width : 4.672
Height : 11.133

Entries	188
Mean	32.18
Std Dev	28.76

ampl



B1L104S, U26-ch50

calib_packv5_033123_0516.root, FC#4, port A1

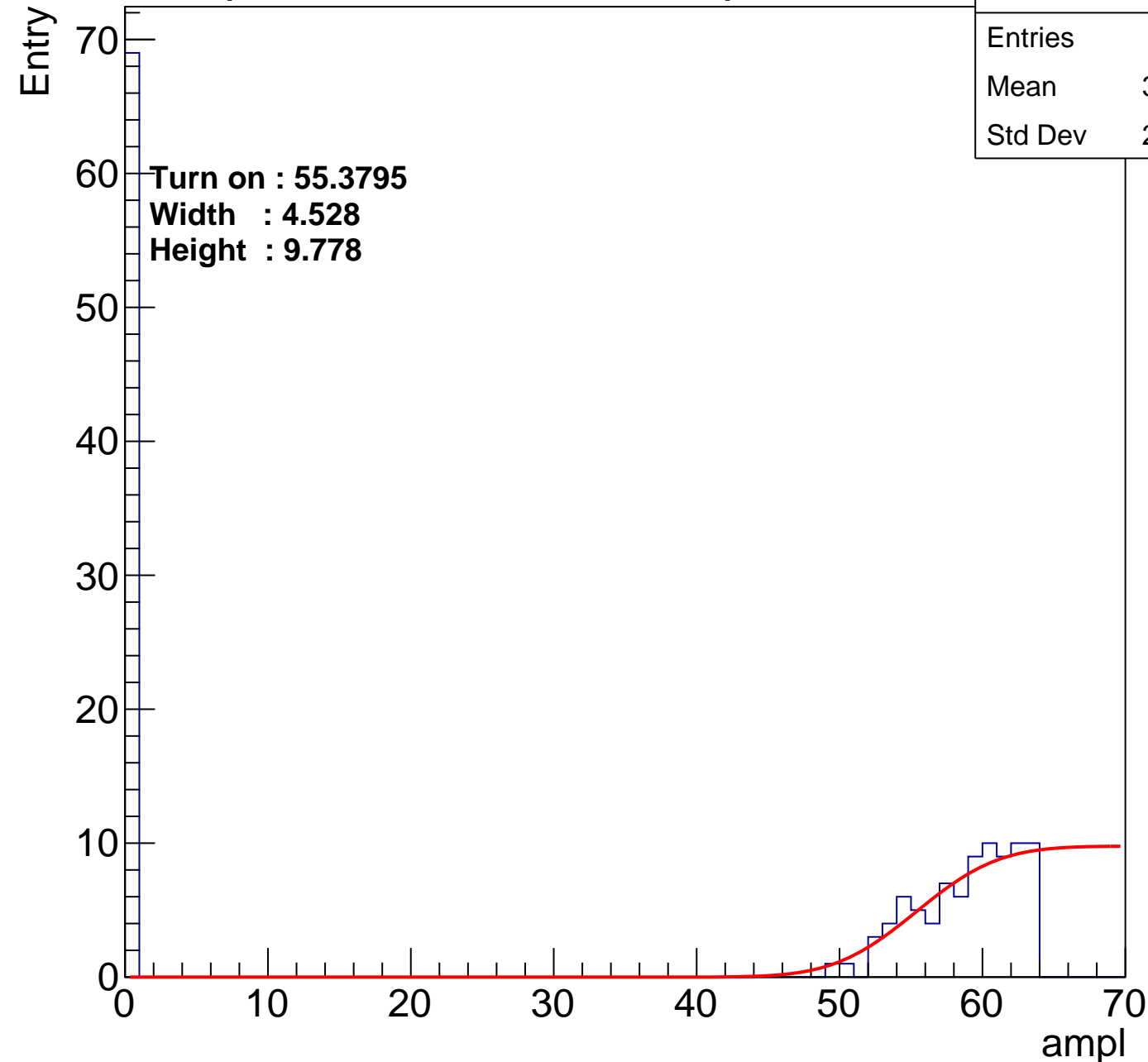
Entries	154
Mean	32.25
Std Dev	29.18

Entry

70
60
50
40
30
20
10
0

Turn on : 55.3795
Width : 4.528
Height : 9.778

ampl



B1L104S, U26-ch51

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	33.4
Std Dev	28.81

Turn on : 54.4385

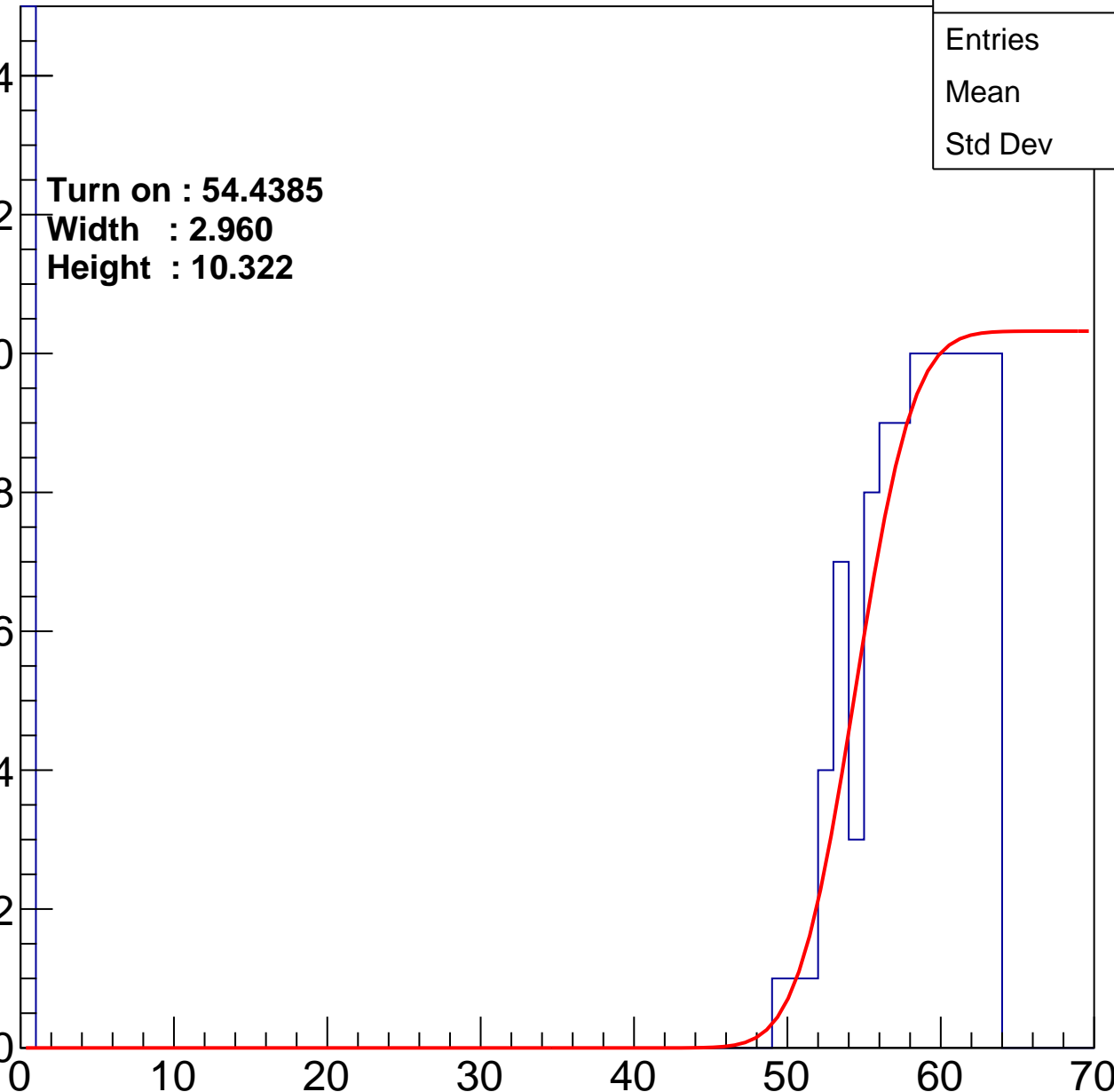
Width : 2.960

Height : 10.322

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	34.26
Std Dev	28.22

Turn on : 54.0743

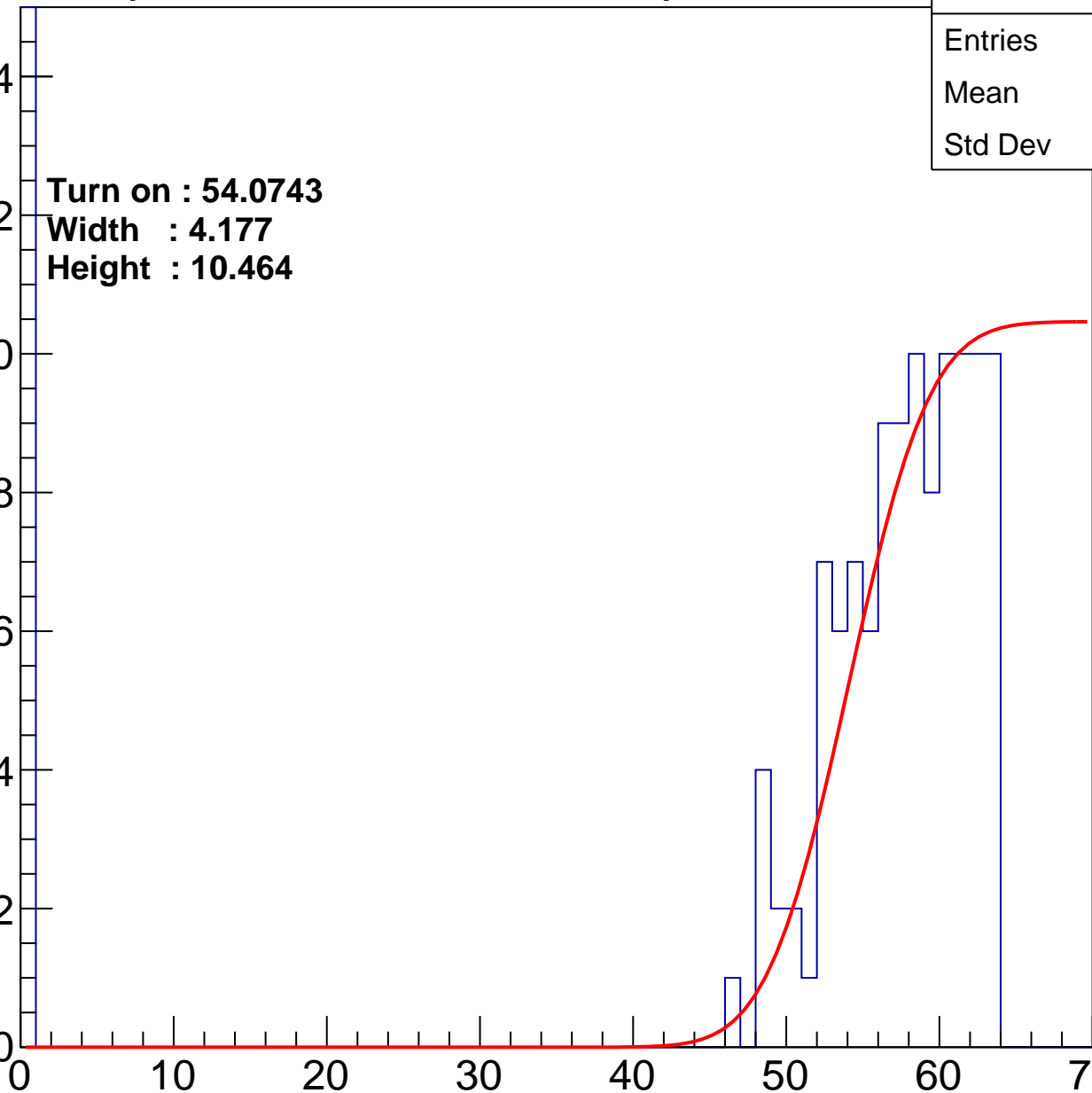
Width : 4.177

Height : 10.464

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	40.82
Std Dev	26.08

Turn on : 51.8677

Width : 3.402

Height : 9.722

Entry

14

12

10

8

6

4

2

0

0

10

20

30

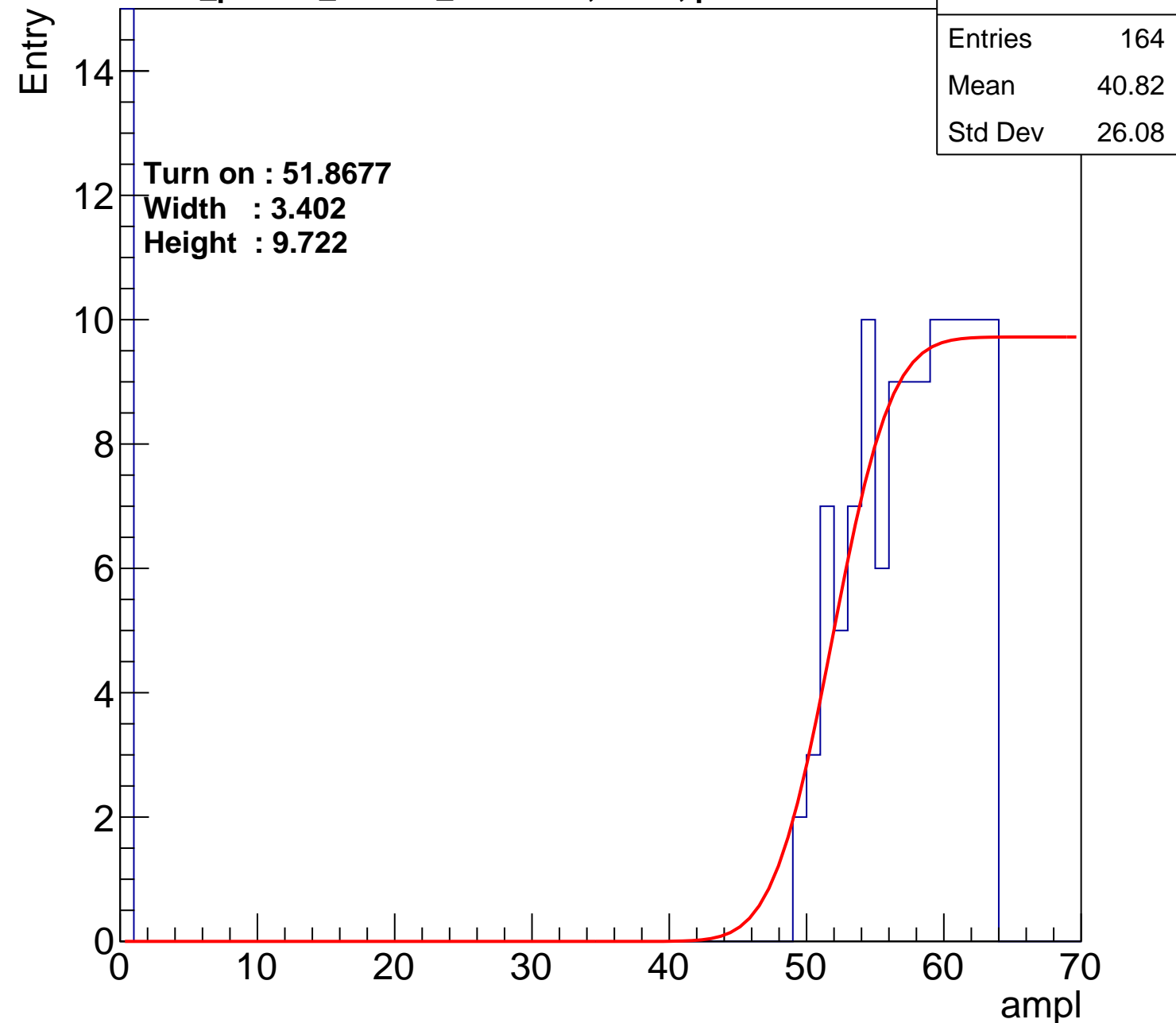
40

50

60

70

ampl



B1L104S, U26-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	30.2
Std Dev	29.16

Turn on : 53.7299

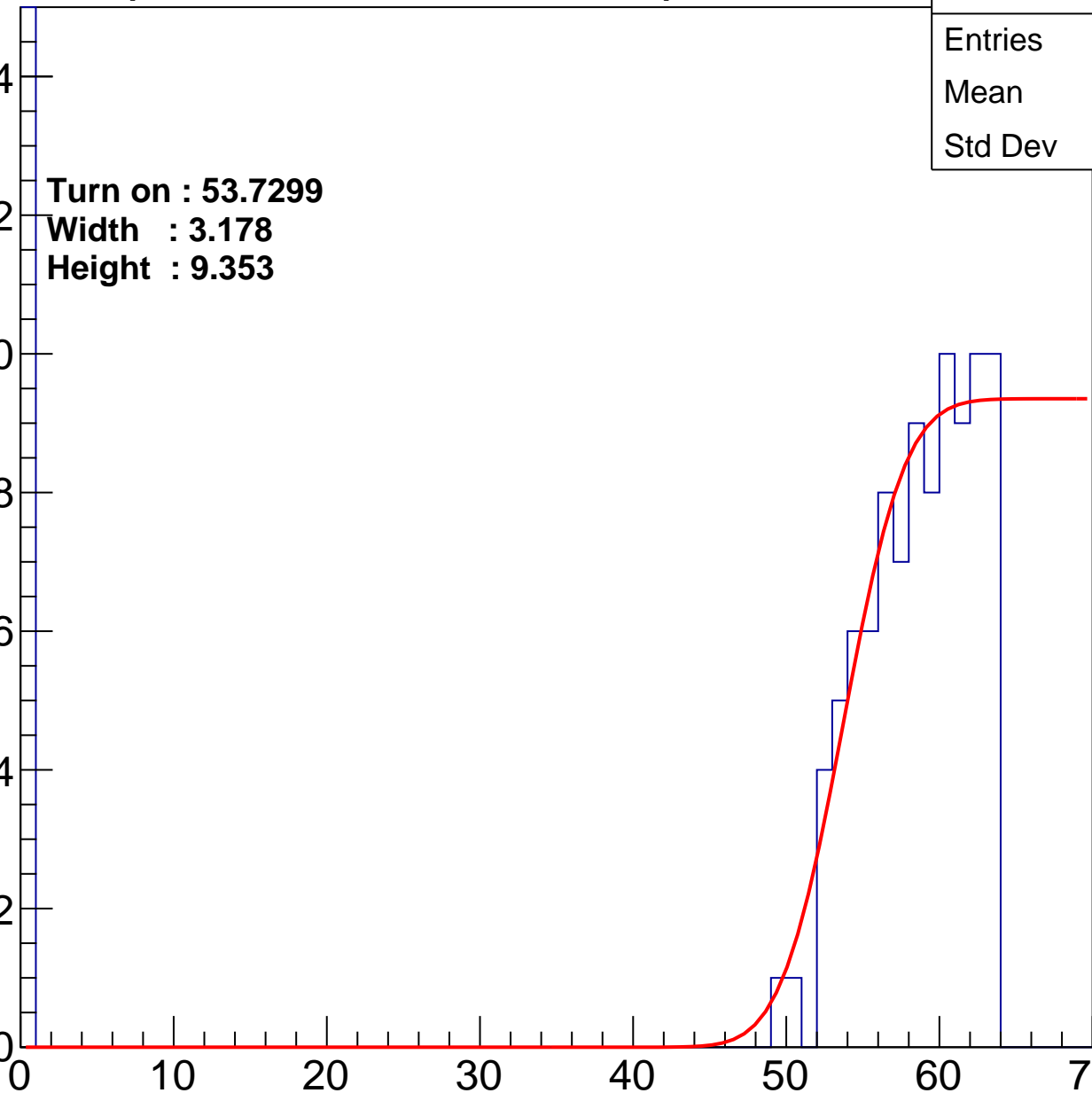
Width : 3.178

Height : 9.353

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch55

calib_packv5_033123_0516.root, FC#4, port A1

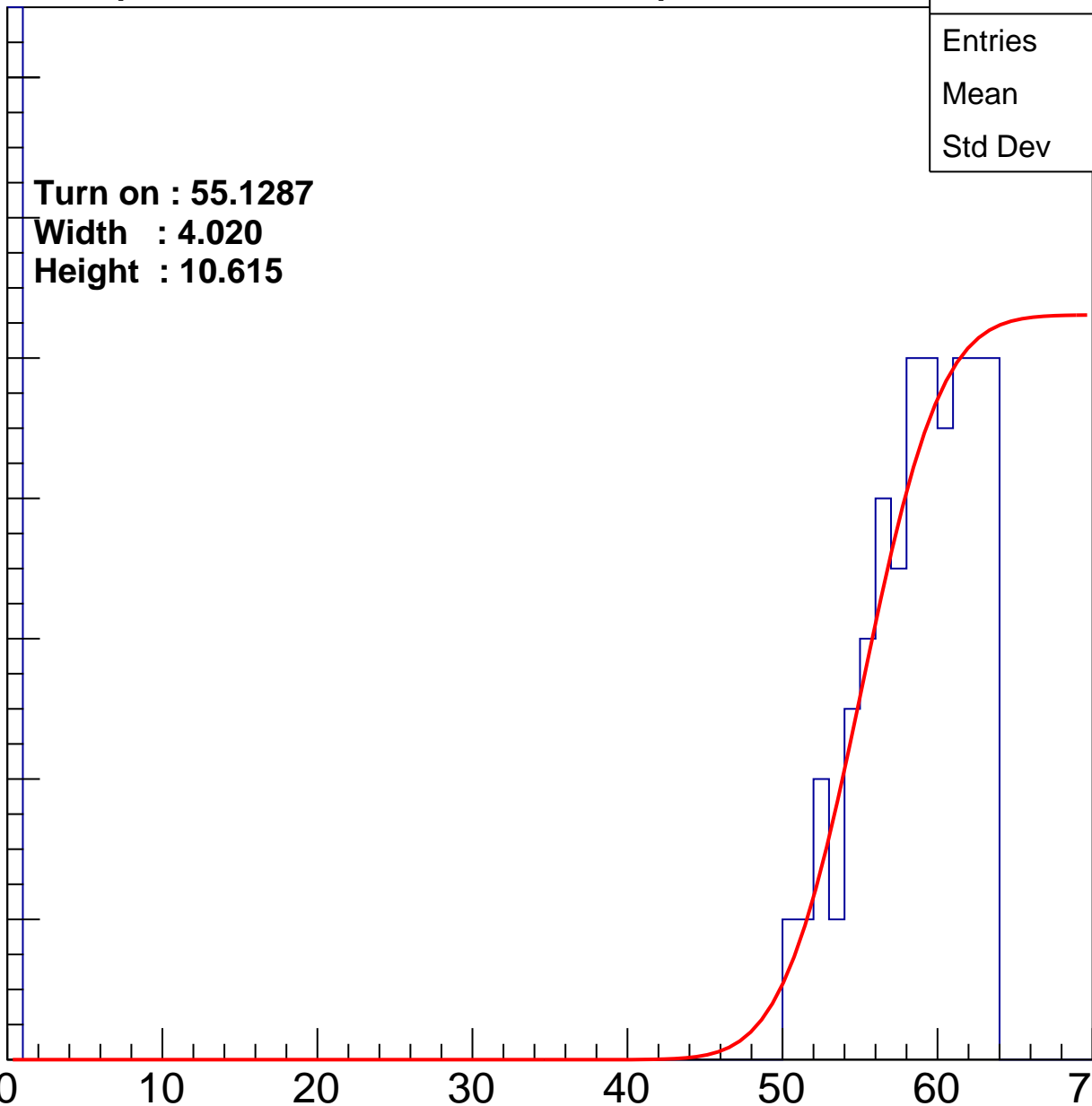
Entry

14
12
10
8
6
4
2
0

Turn on : 55.1287
Width : 4.020
Height : 10.615

Entries	135
Mean	40.99
Std Dev	26.75

ampl



B1L104S, U26-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	149
Mean	37.7
Std Dev	27.78

Turn on : 55.4038

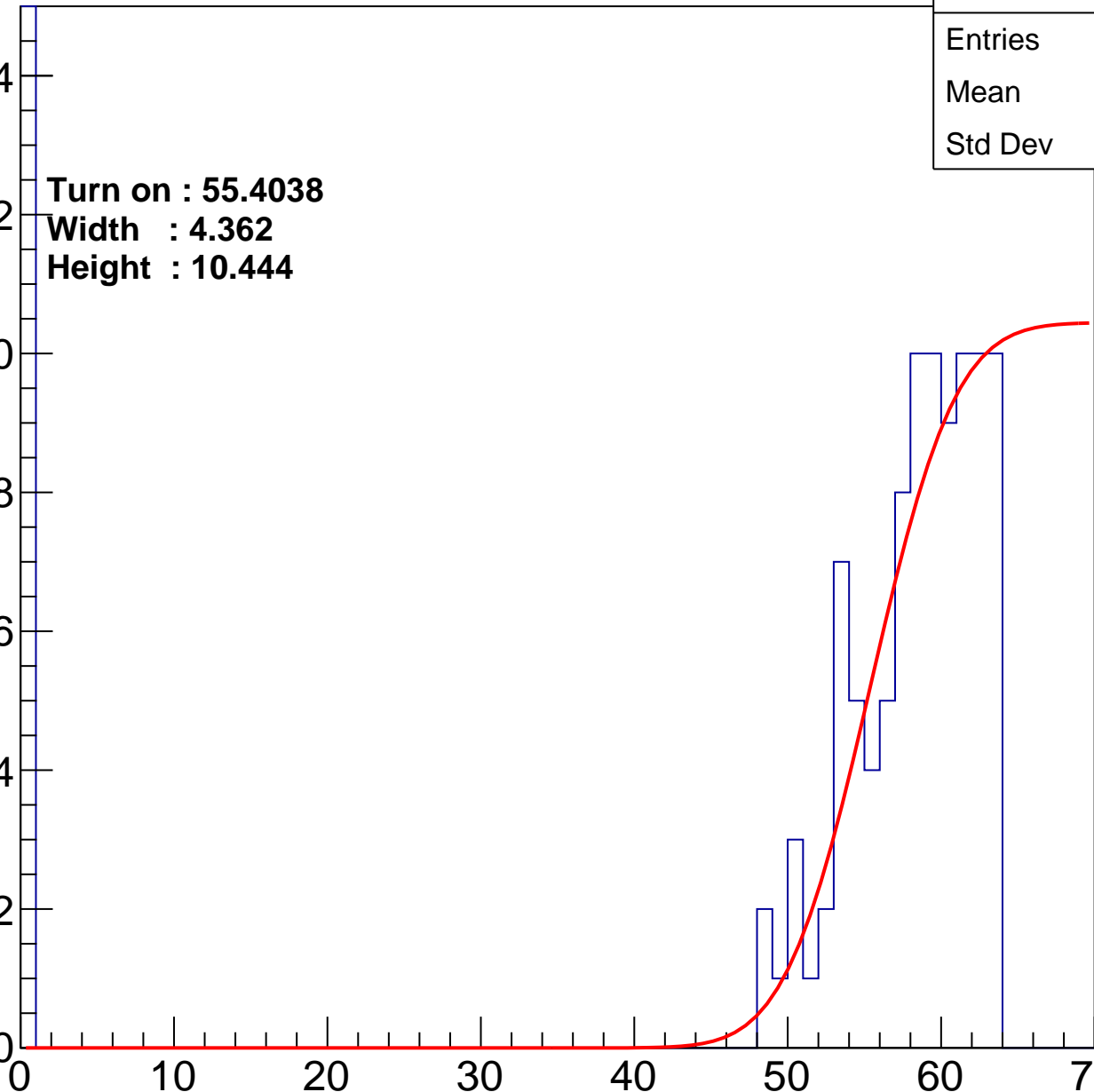
Width : 4.362

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch57

calib_packv5_033123_0516.root, FC#4, port A1

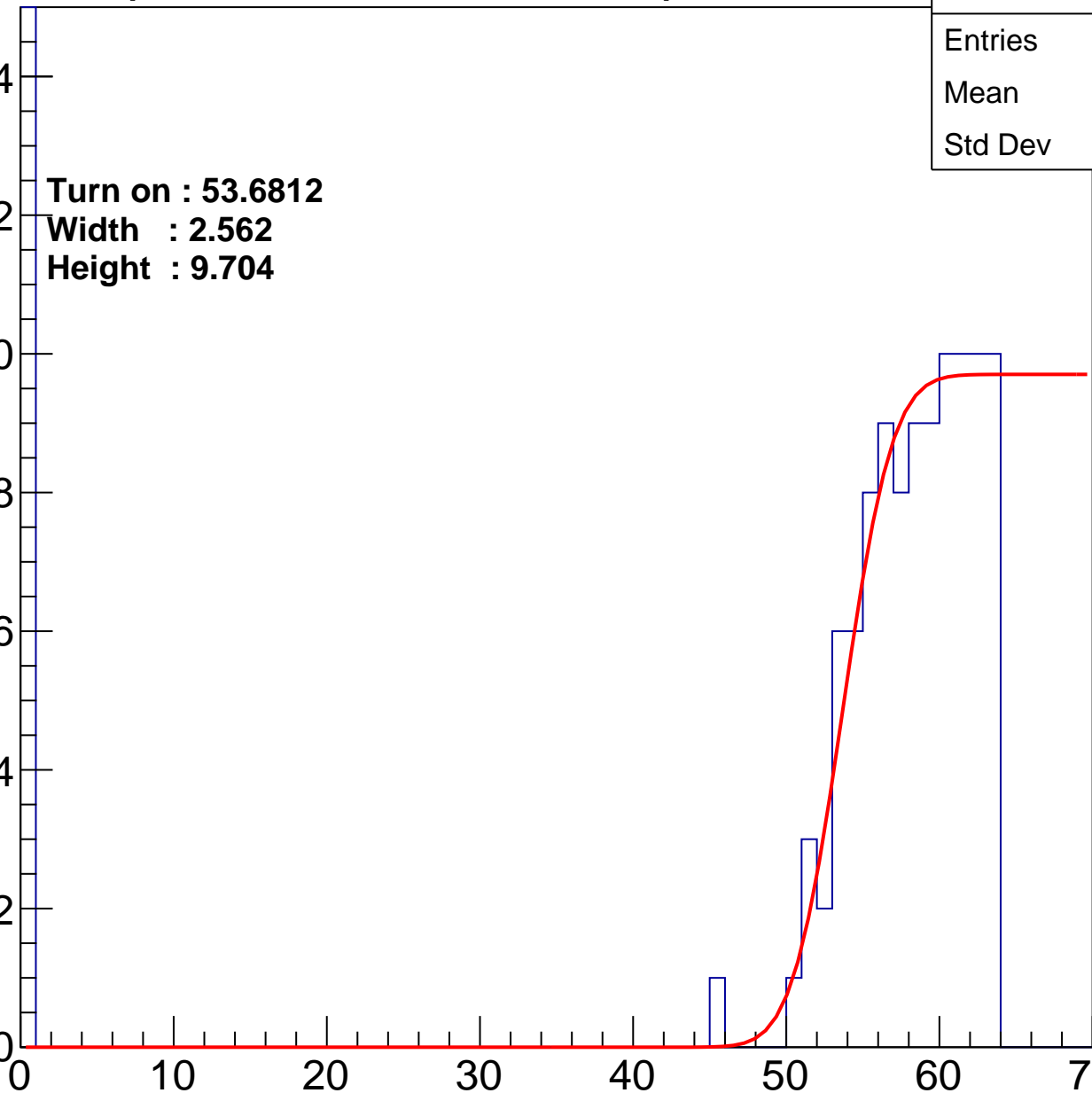
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6812
Width : 2.562
Height : 9.704

Entries	158
Mean	37.39
Std Dev	27.86

ampl



B1L104S, U26-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	32.82
Std Dev	28.92

Turn on : 55.7683

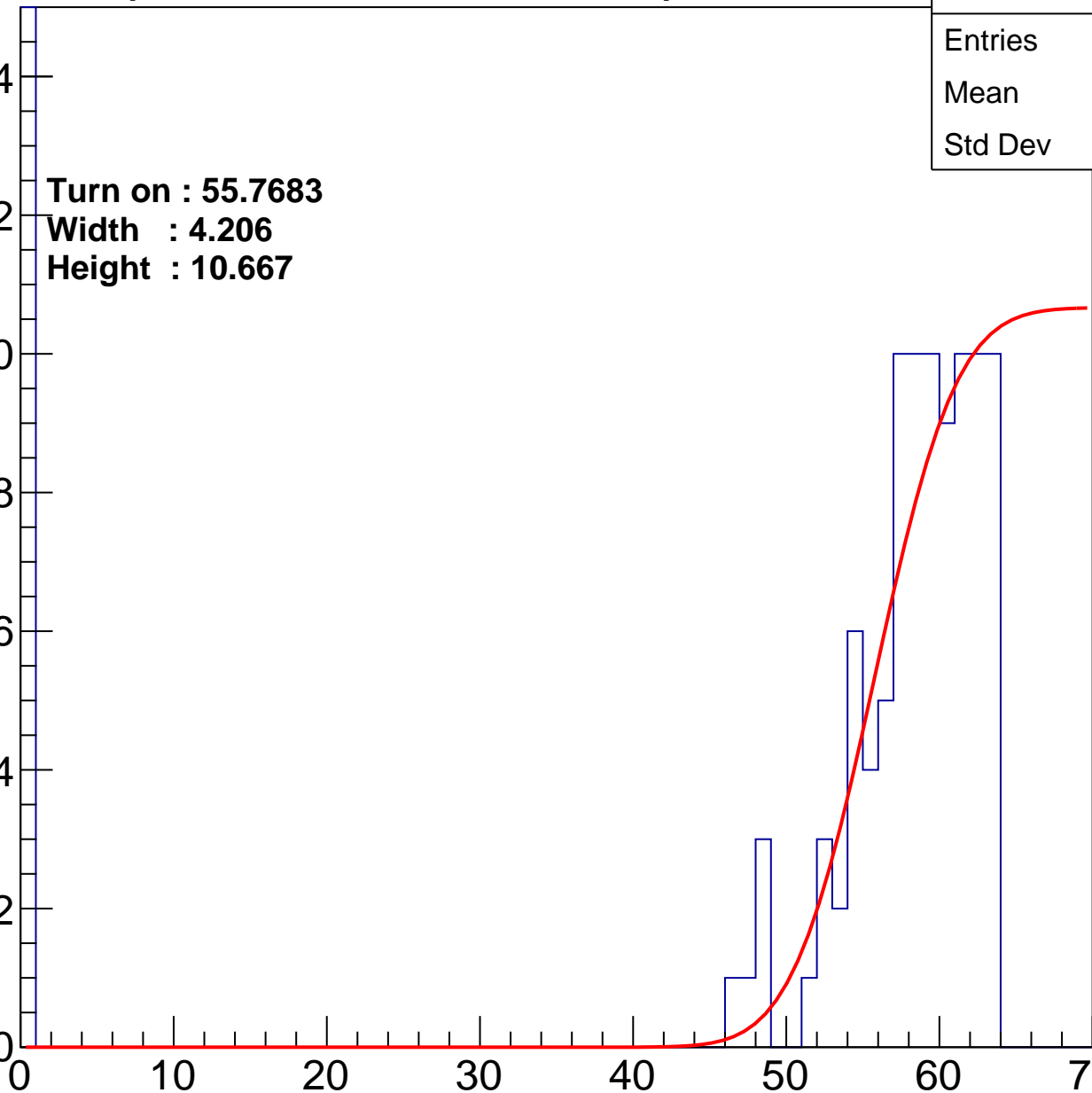
Width : 4.206

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl

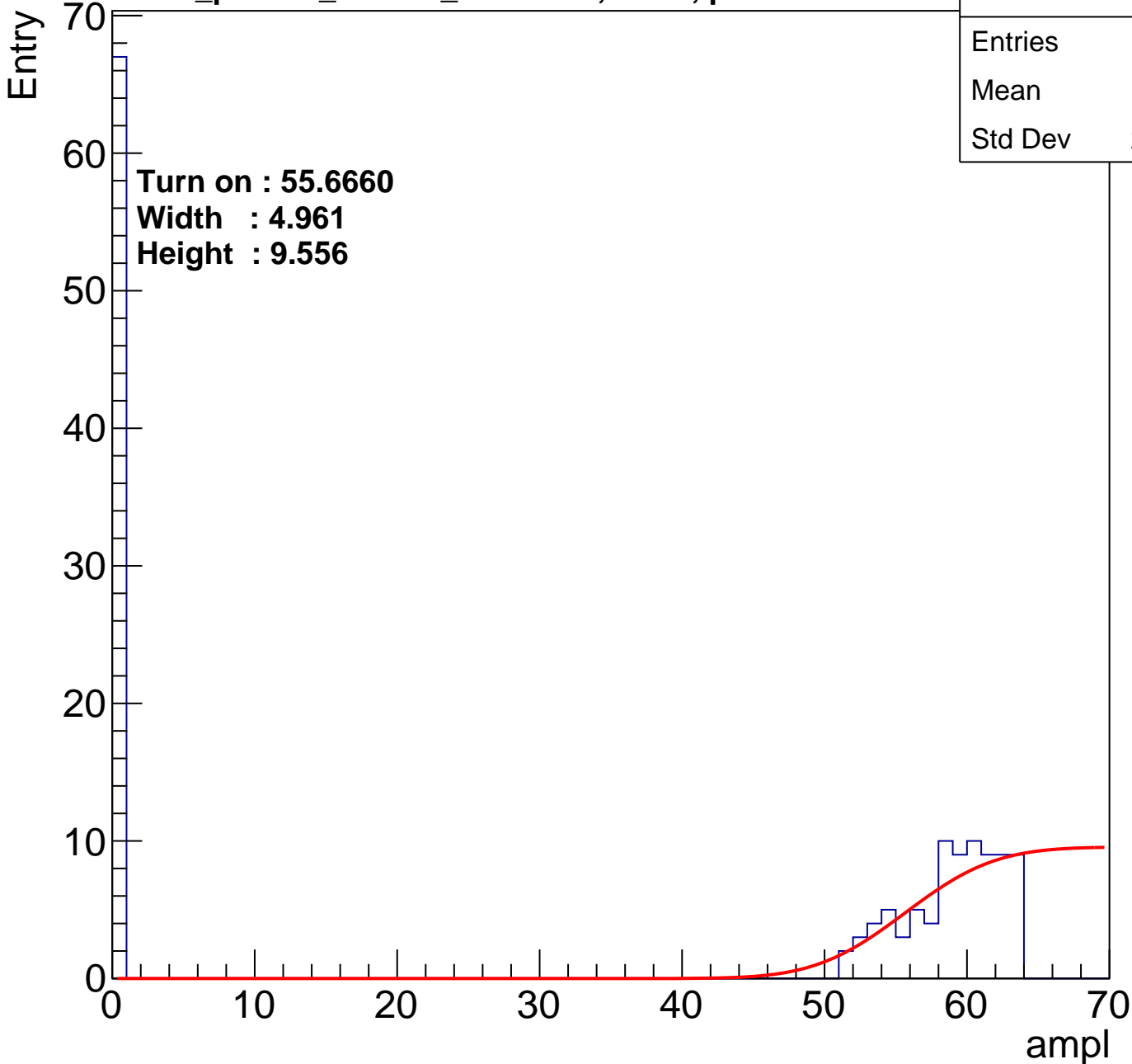


B1L104S, U26-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	149
Mean	32.2
Std Dev	29.21

Turn on : 55.6660
Width : 4.961
Height : 9.556



B1L104S, U26-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	35.78
Std Dev	28.54

Turn on : 55.7362

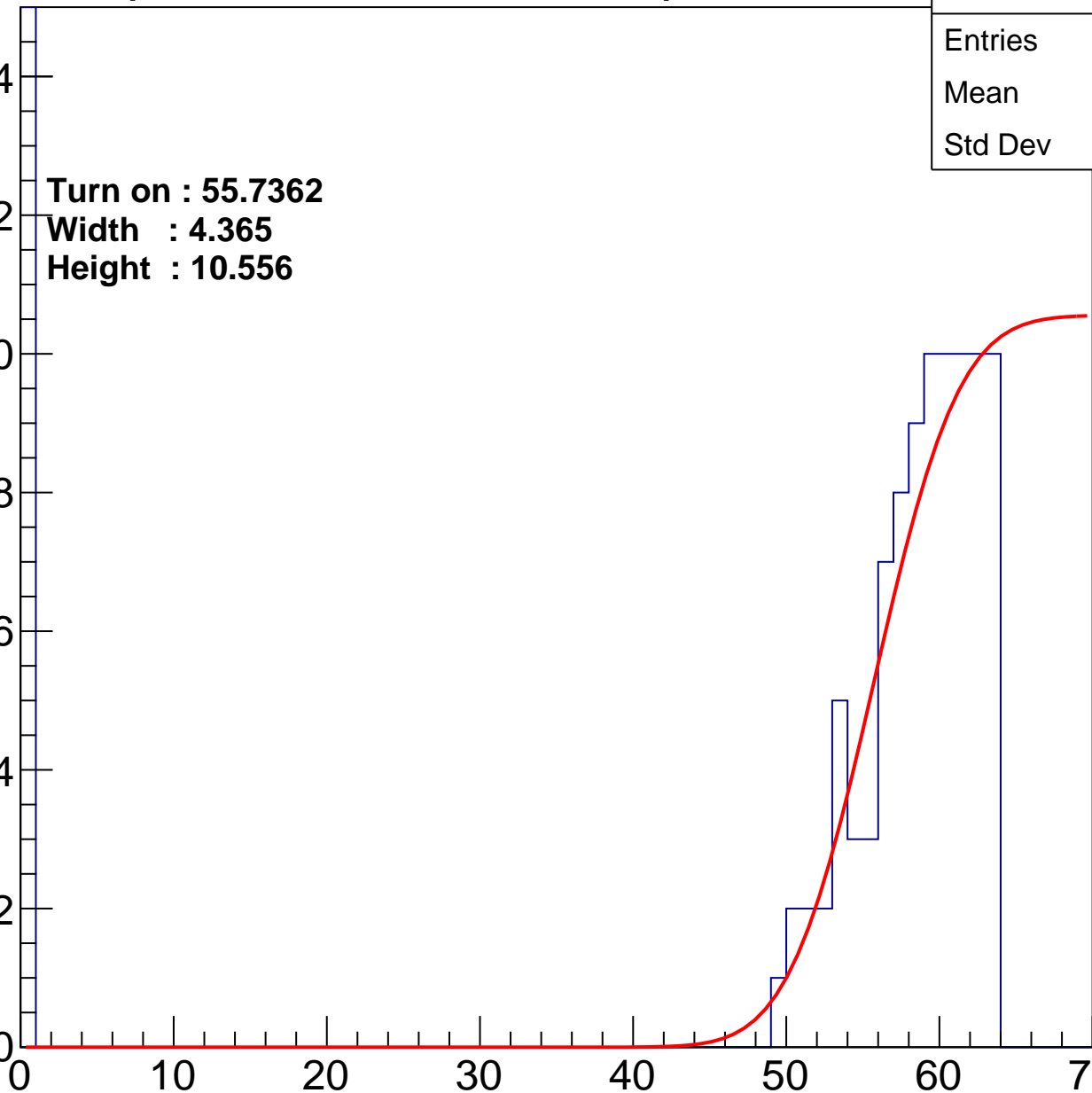
Width : 4.365

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch61

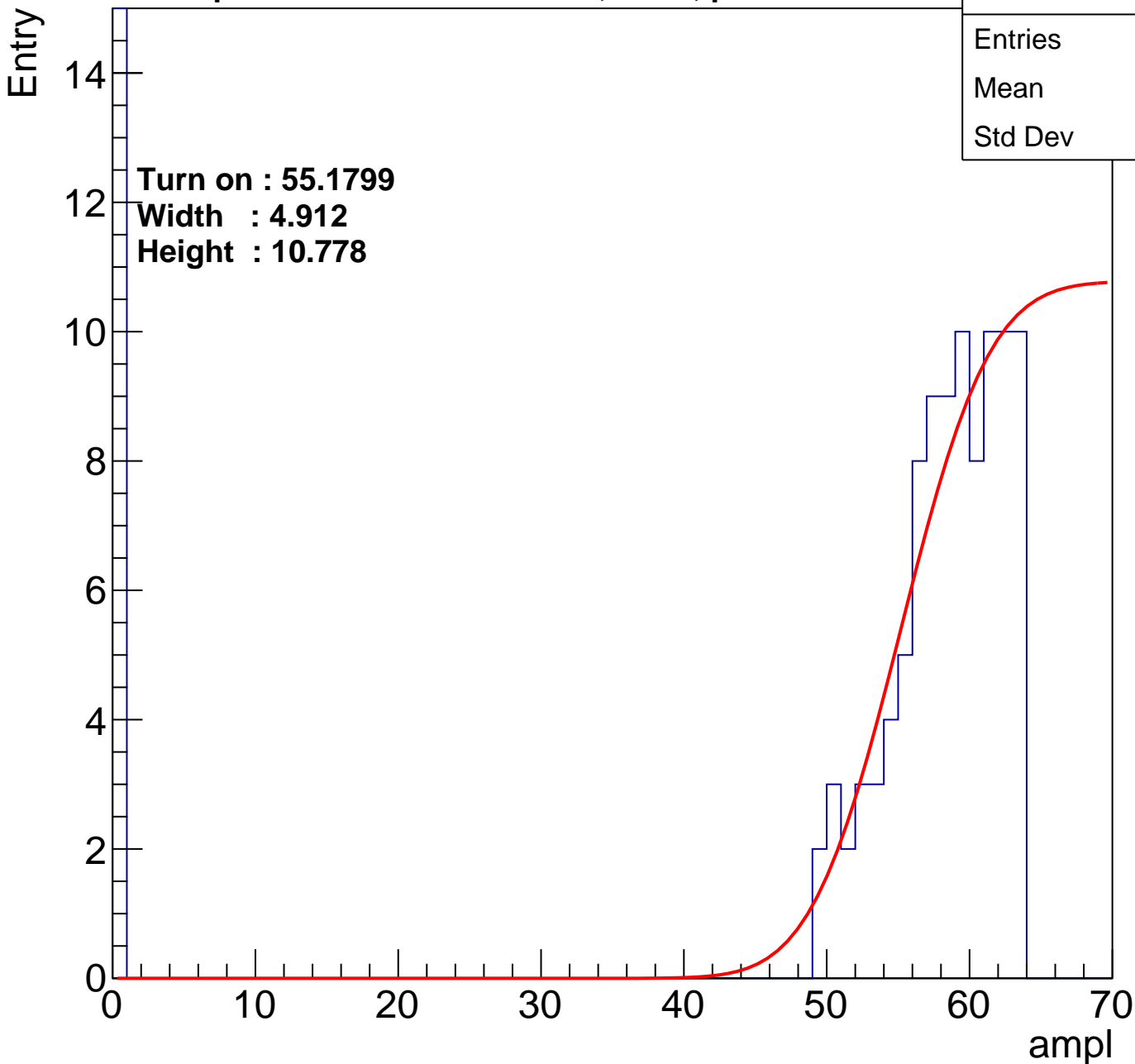
calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	36.4
Std Dev	28.2

Turn on : 55.1799

Width : 4.912

Height : 10.778



B1L104S, U26-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	32.3
Std Dev	28.81

Turn on : 55.1882

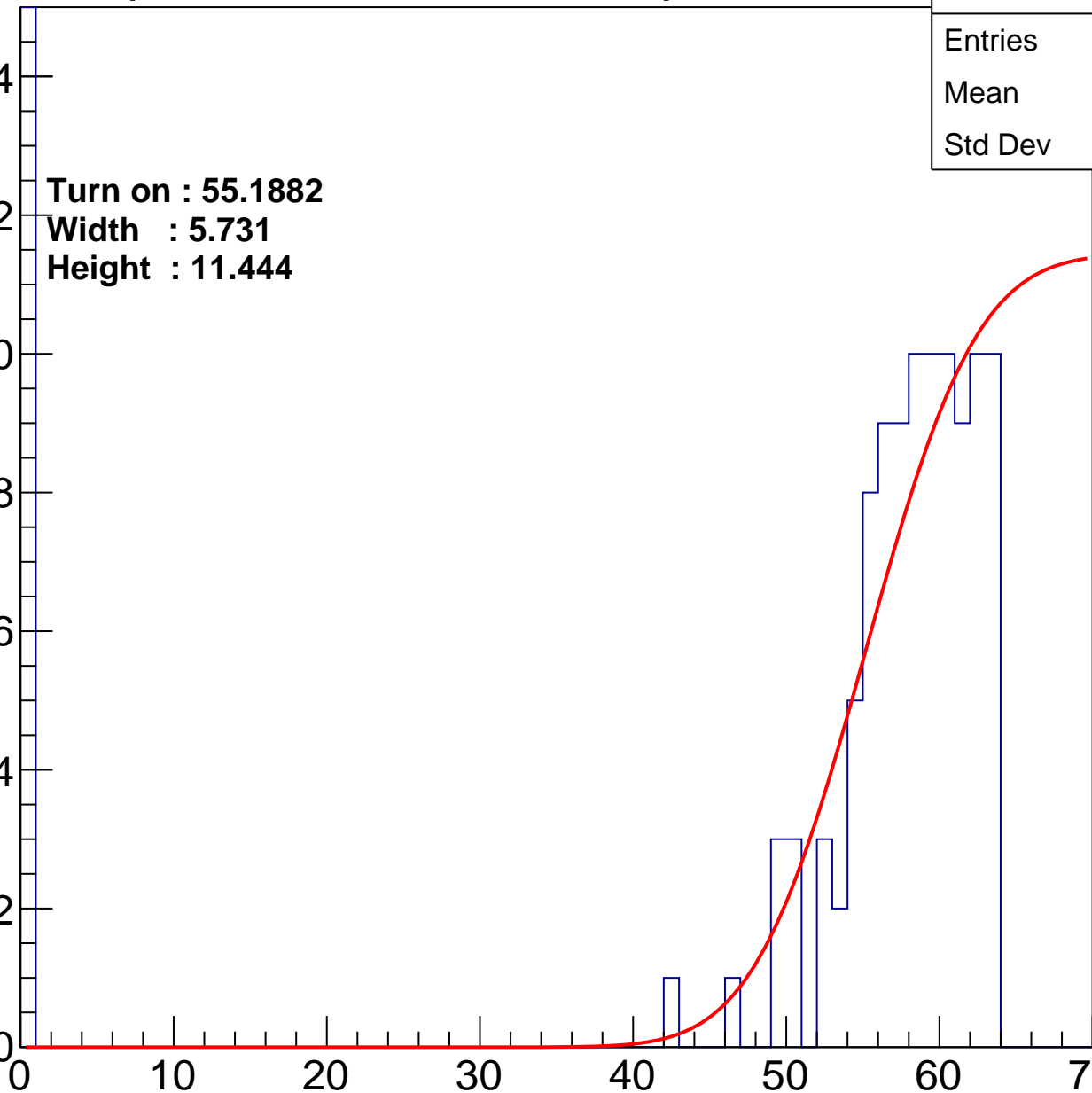
Width : 5.731

Height : 11.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch63

calib_packv5_033123_0516.root, FC#4, port A1

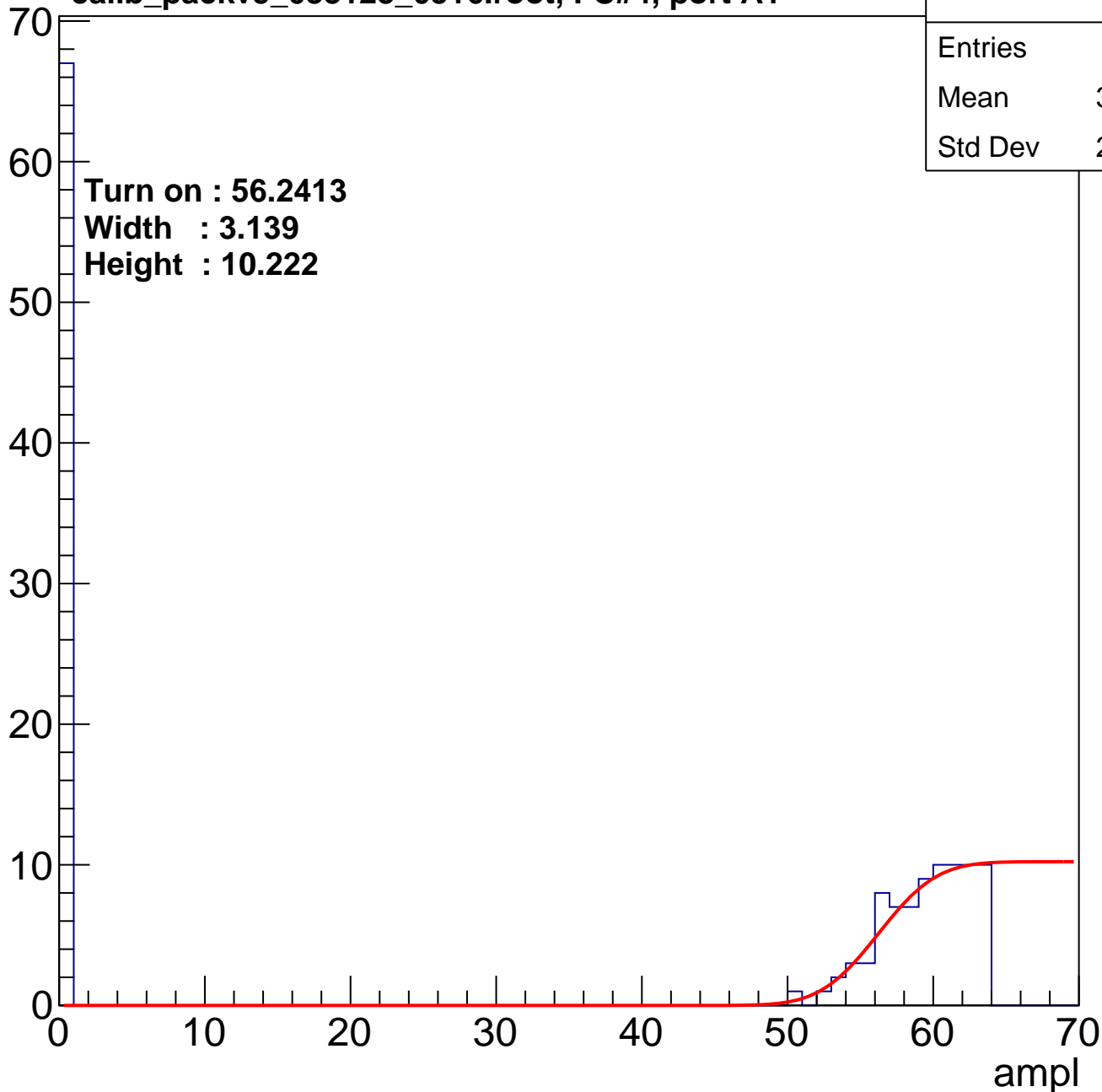
Entries	148
Mean	32.29
Std Dev	29.45

Turn on : 56.2413

Width : 3.139

Height : 10.222

Entry



B1L104S, U26-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	35.4
Std Dev	28.29

Turn on : 52.9401

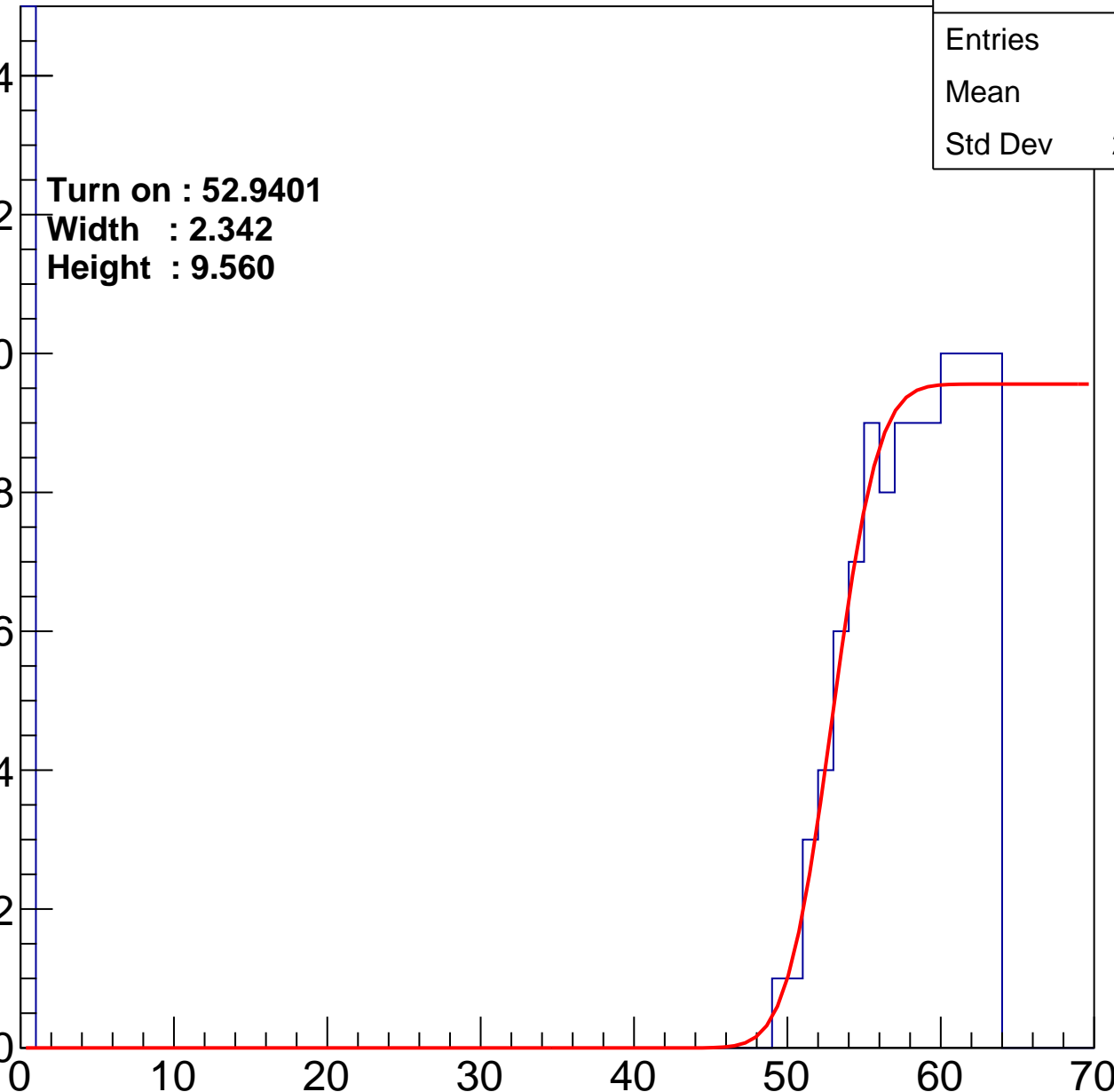
Width : 2.342

Height : 9.560

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	38.18
Std Dev	27.41

Turn on : 53.0798

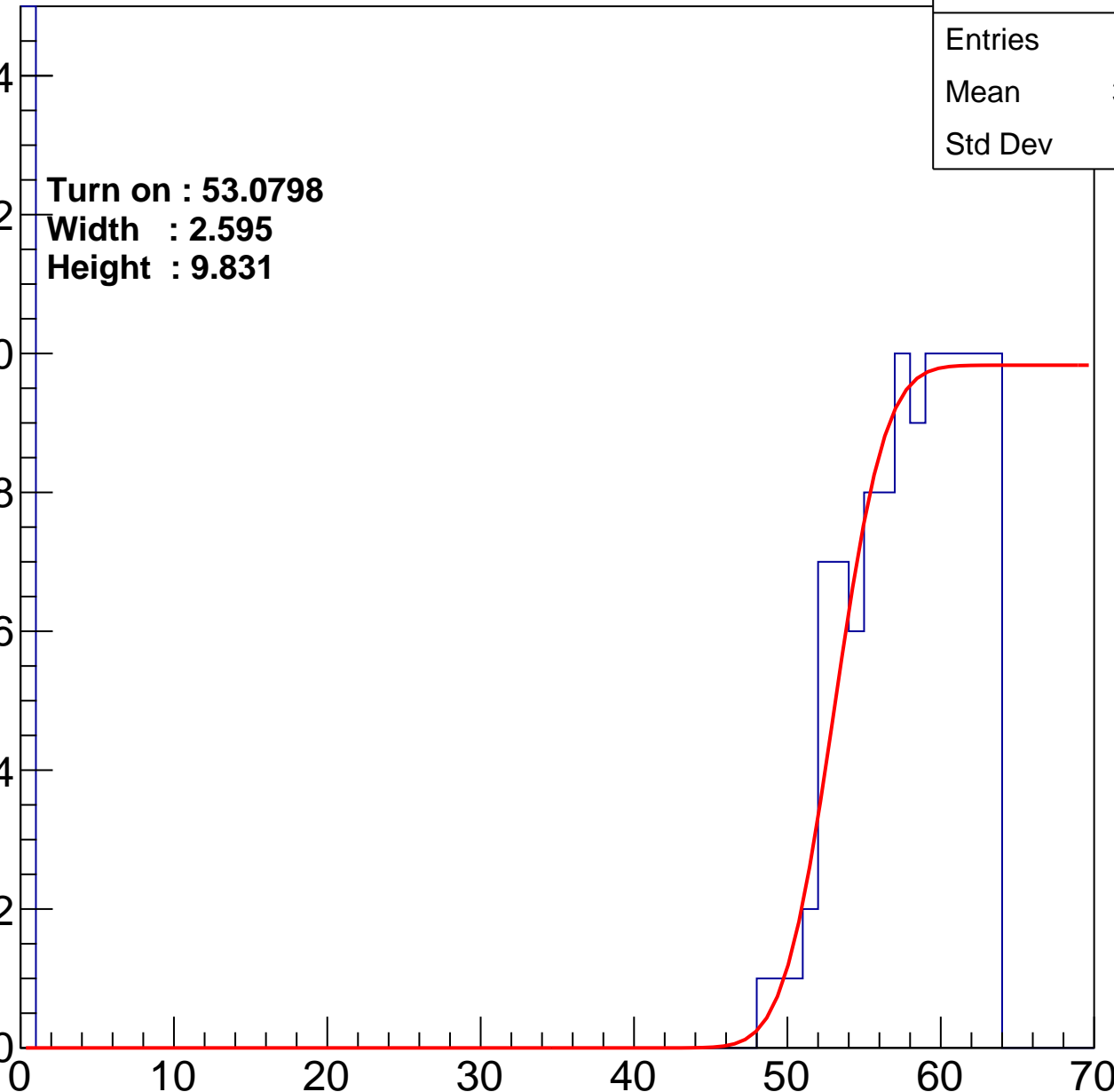
Width : 2.595

Height : 9.831

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	34.64
Std Dev	28.27

Turn on : 54.6555

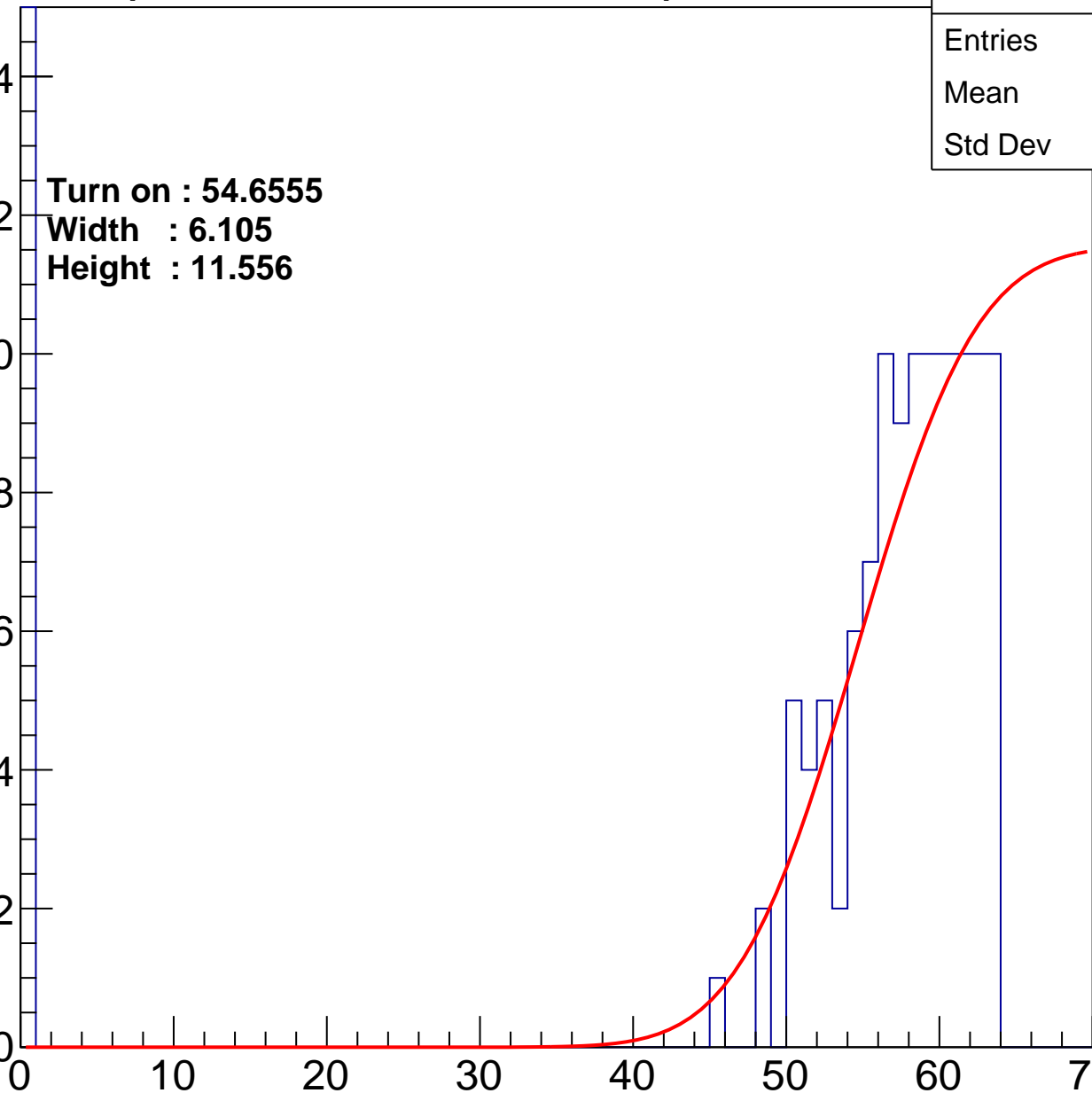
Width : 6.105

Height : 11.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	40.69
Std Dev	26.16

Turn on : 52.3781

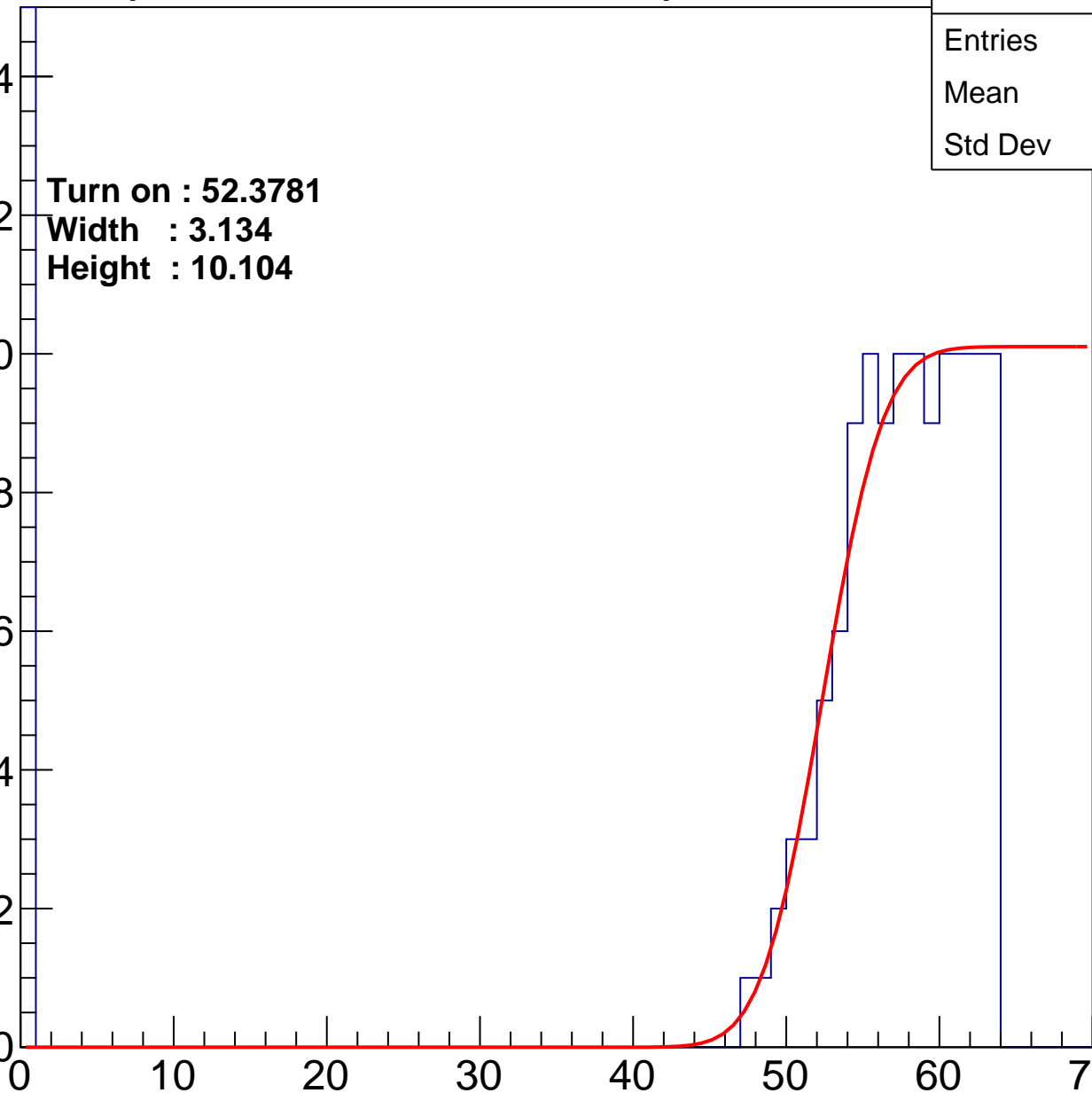
Width : 3.134

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	35.24
Std Dev	28.15

Turn on : 53.1546

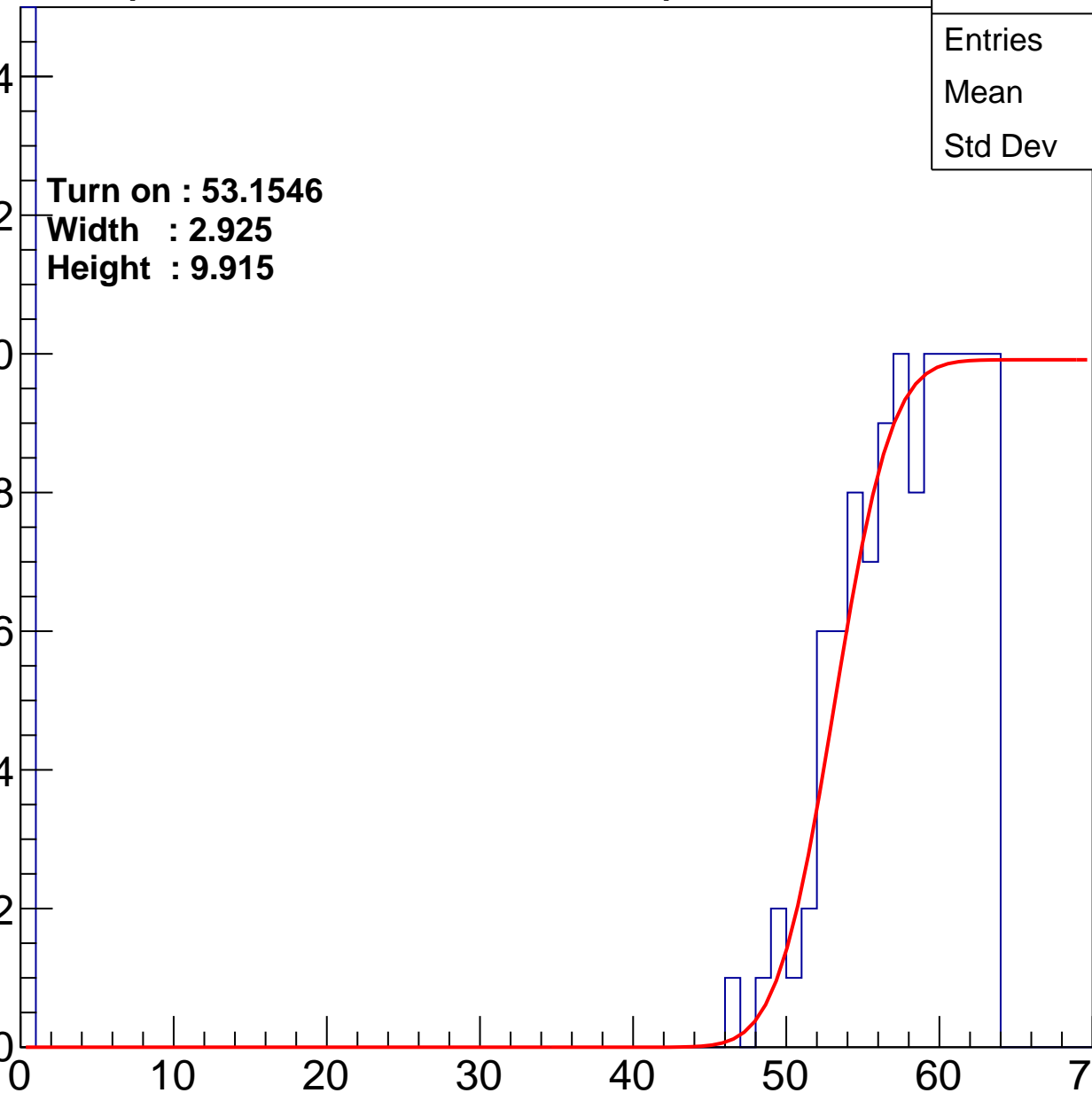
Width : 2.925

Height : 9.915

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch69

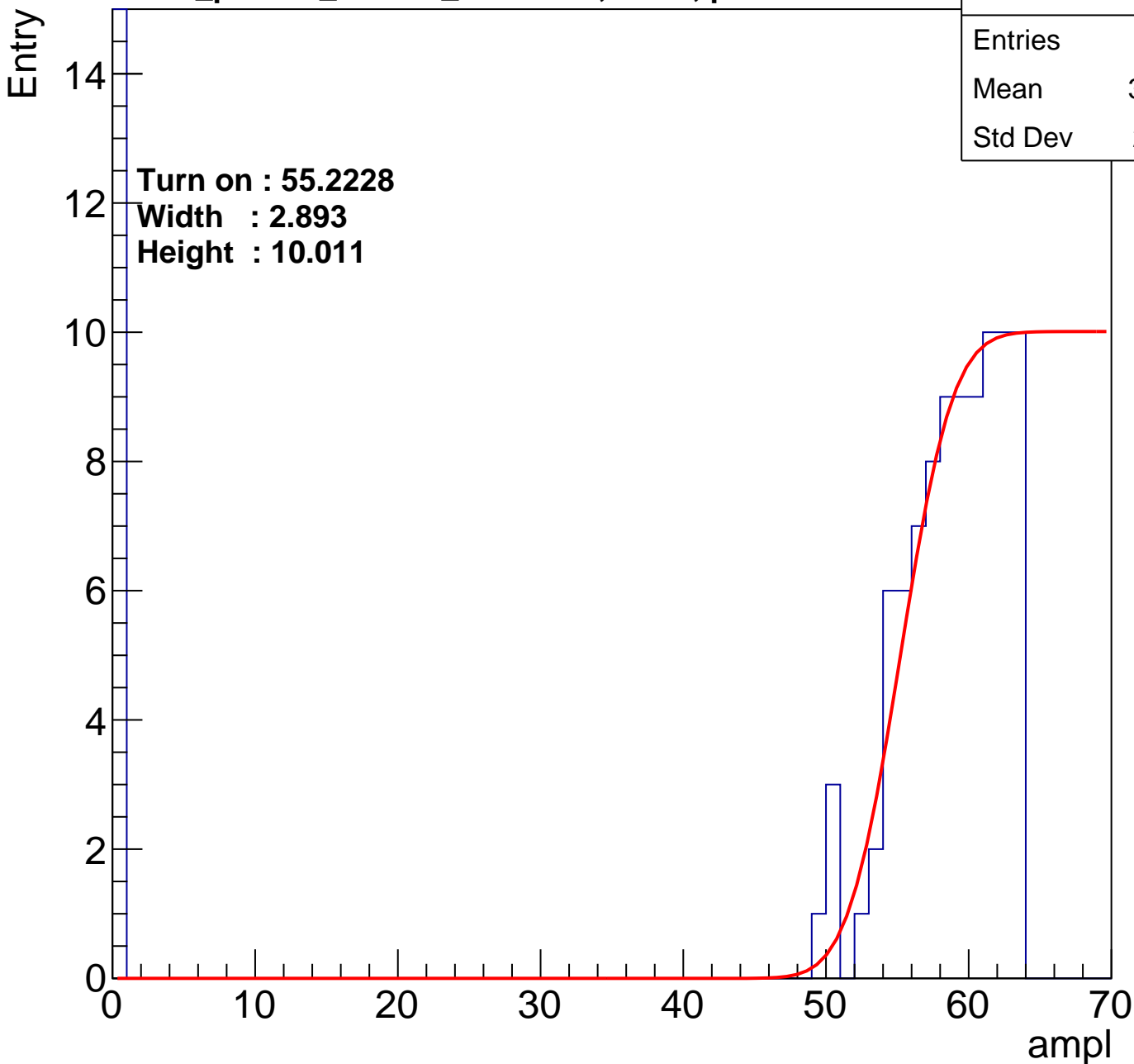
calib_packv5_033123_0516.root, FC#4, port A1

Entries	143
Mean	37.15
Std Dev	28.21

Turn on : 55.2228

Width : 2.893

Height : 10.011



B1L104S, U26-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	35.31
Std Dev	28.57

Turn on : 54.6824

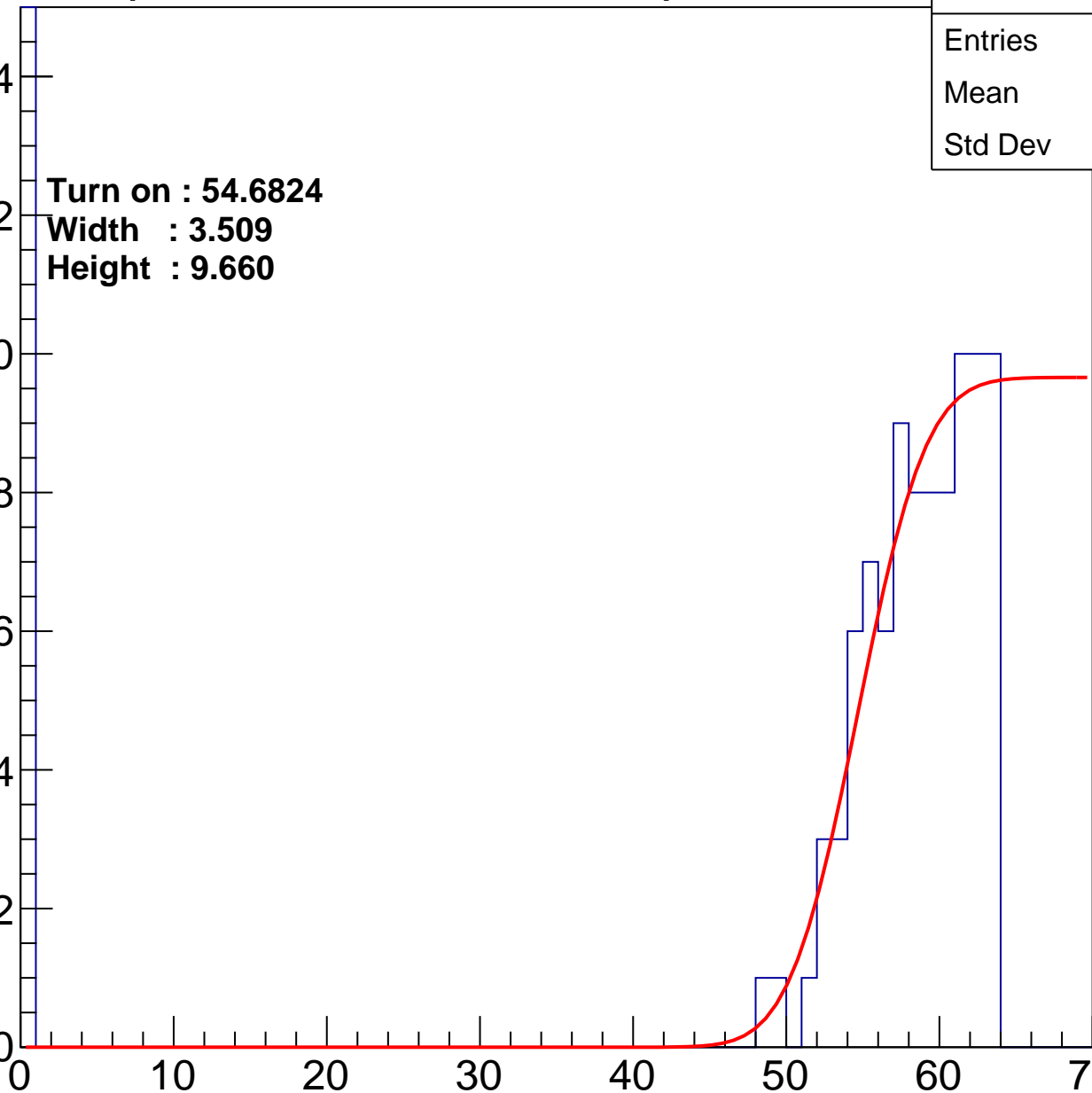
Width : 3.509

Height : 9.660

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	137
Mean	36.39
Std Dev	28.58

Turn on : 55.9875

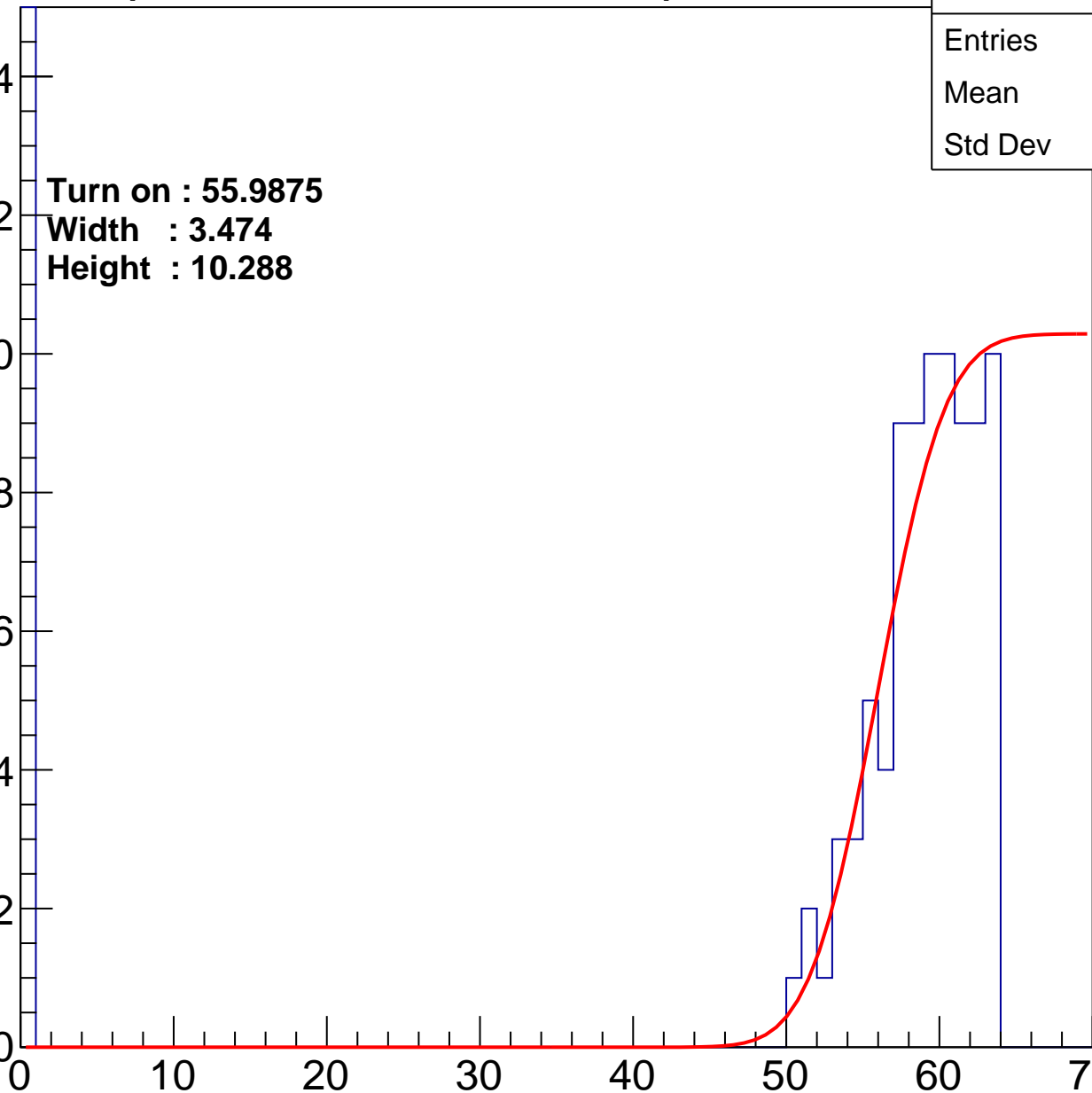
Width : 3.474

Height : 10.288

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch72

calib_packv5_033123_0516.root, FC#4, port A1

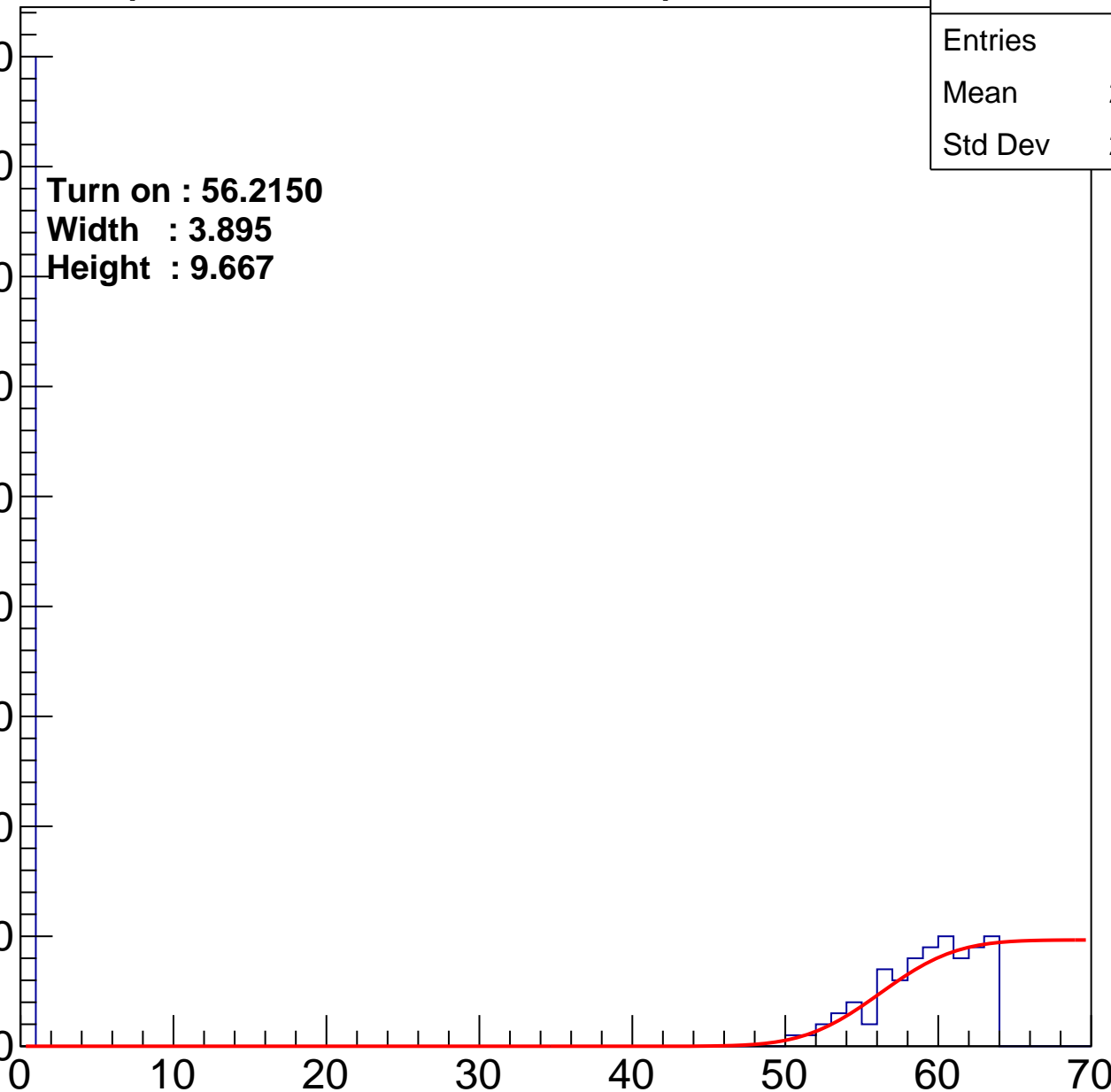
Entry

90
80
70
60
50
40
30
20
10
0

Turn on : 56.2150
Width : 3.895
Height : 9.667

Entries	170
Mean	27.62
Std Dev	29.38

ampl



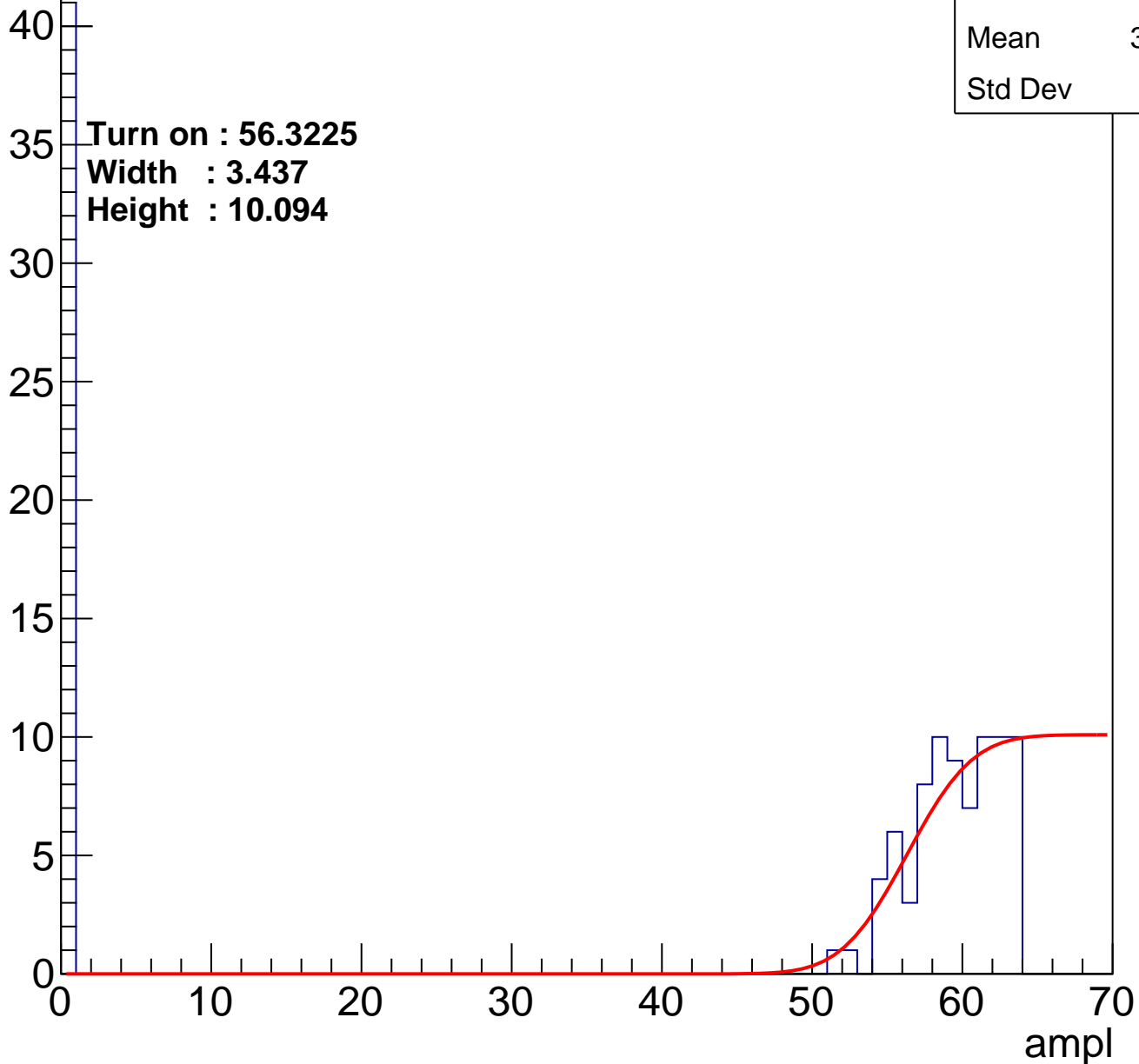
B1L104S, U26-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	120
Mean	38.87
Std Dev	28.1

Turn on : 56.3225
Width : 3.437
Height : 10.094

Entry



B1L104S, U26-ch74

calib_packv5_033123_0516.root, FC#4, port A1

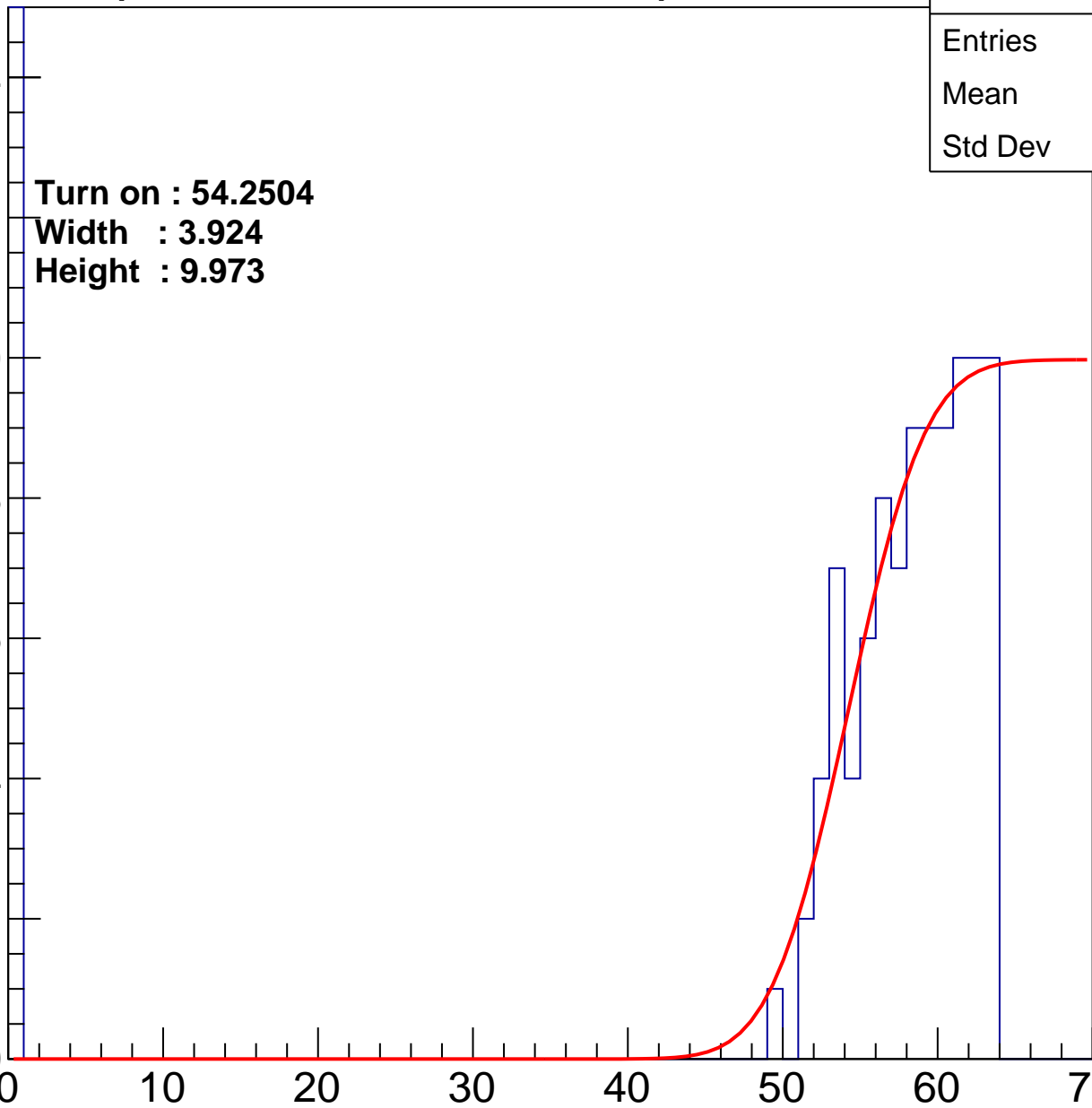
Entry

14
12
10
8
6
4
2
0

Turn on : 54.2504
Width : 3.924
Height : 9.973

Entries	160
Mean	34.85
Std Dev	28.59

ampl



B1L104S, U26-ch75

calib_packv5_033123_0516.root, FC#4, port A1

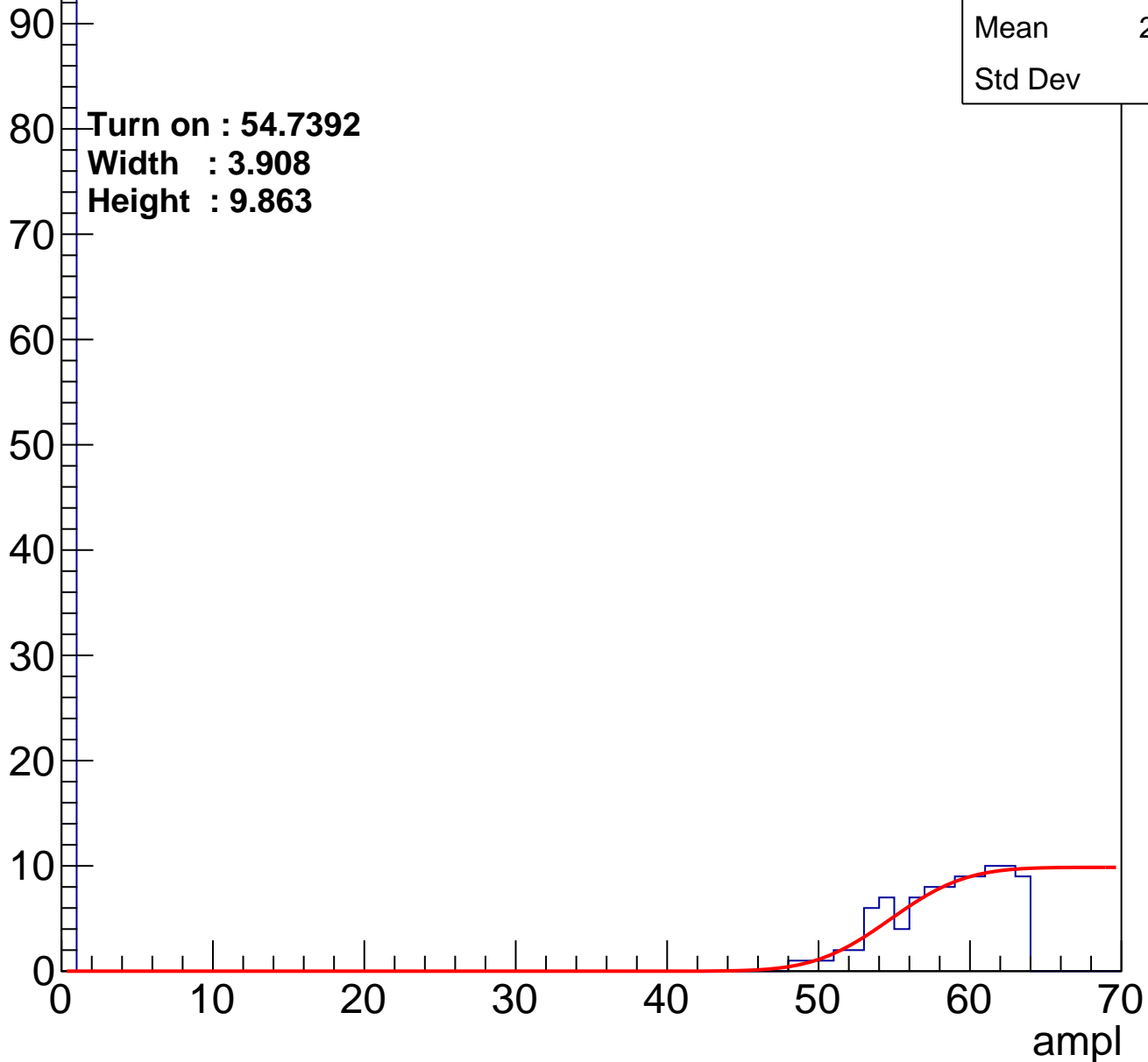
Entries	187
Mean	29.14
Std Dev	29.1

Turn on : 54.7392

Width : 3.908

Height : 9.863

Entry



B1L104S, U26-ch76

calib_packv5_033123_0516.root, FC#4, port A1

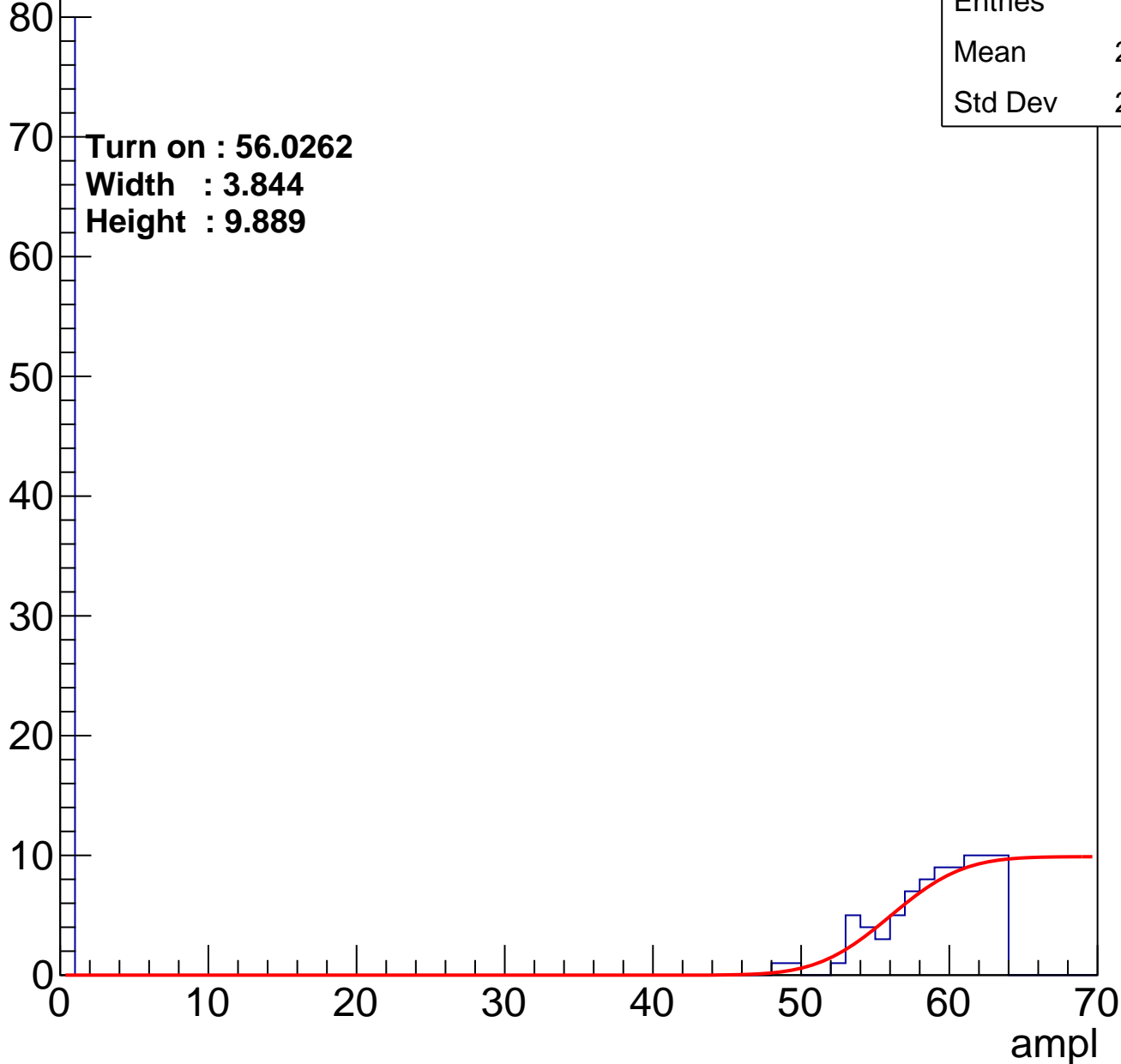
Entries	163
Mean	29.87
Std Dev	29.43

Turn on : 56.0262

Width : 3.844

Height : 9.889

Entry



B1L104S, U26-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	31.45
Std Dev	29.24

Turn on : 55.5363

Width : 3.088

Height : 9.427

Entry

70

60

50

40

30

20

10

0

0

10

20

30

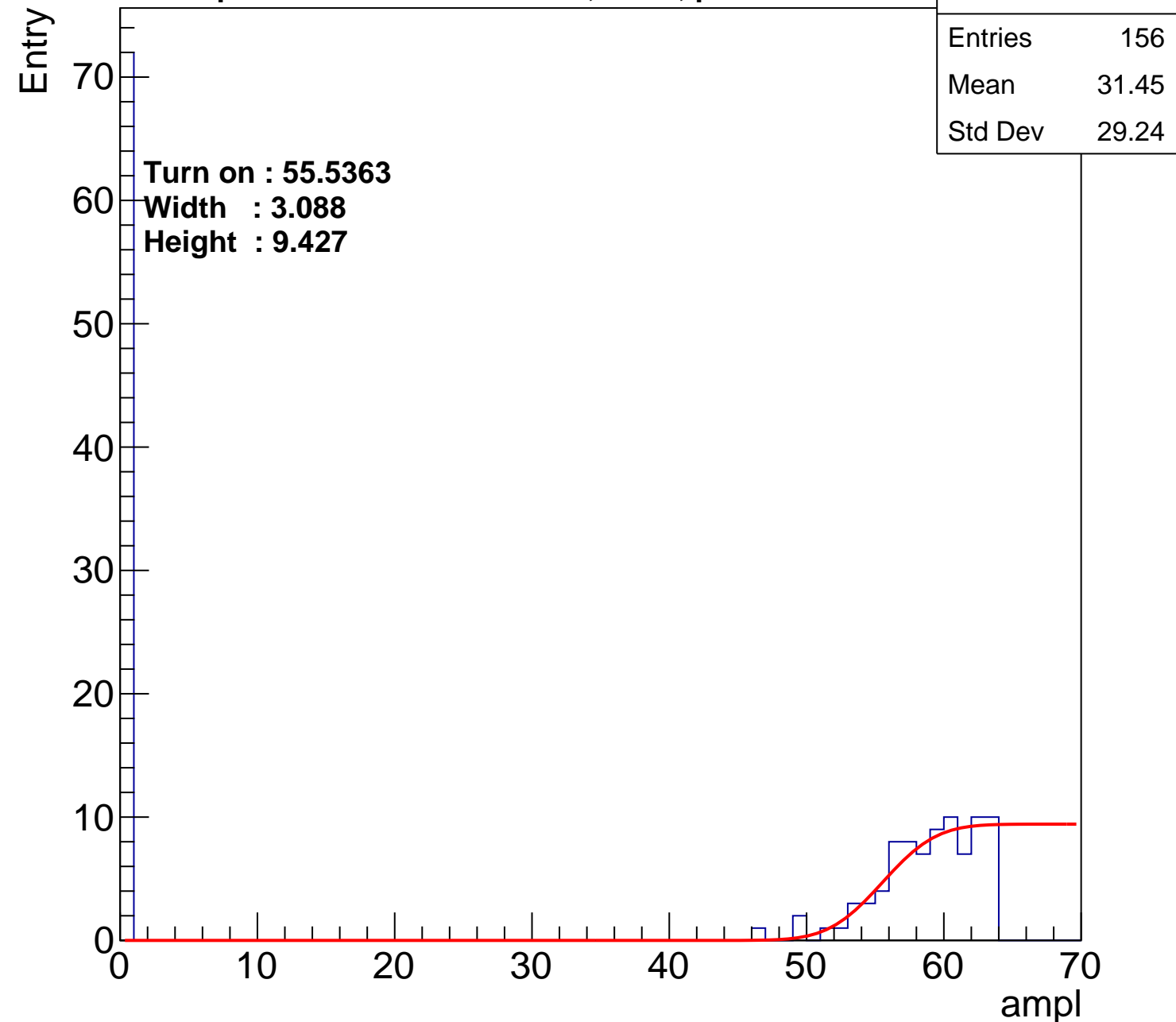
40

50

60

70

ampl



B1L104S, U26-ch78

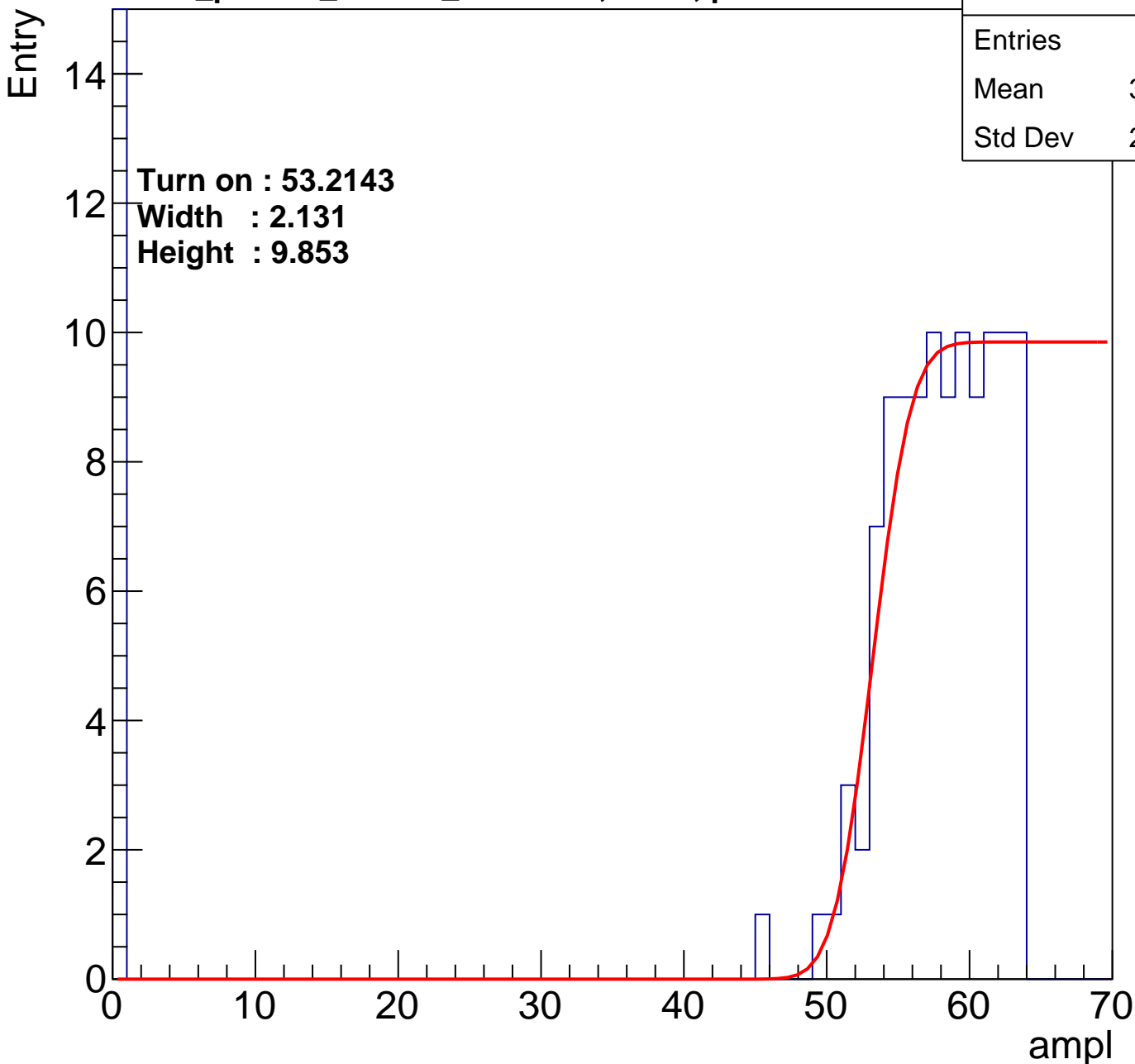
calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	33.72
Std Dev	28.53

Turn on : 53.2143

Width : 2.131

Height : 9.853



B1L104S, U26-ch79

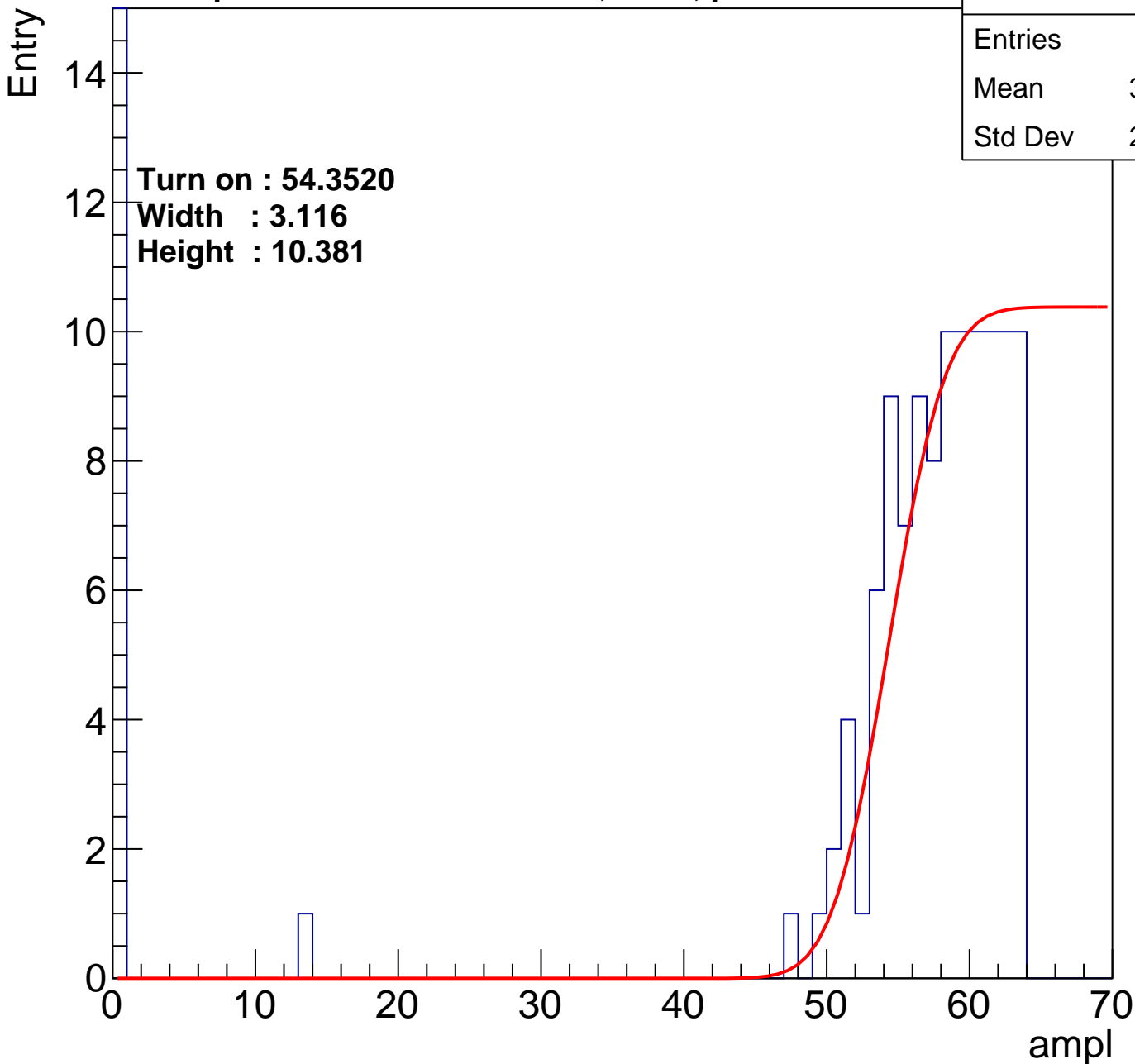
calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	34.12
Std Dev	28.45

Turn on : 54.3520

Width : 3.116

Height : 10.381



B1L104S, U26-ch80

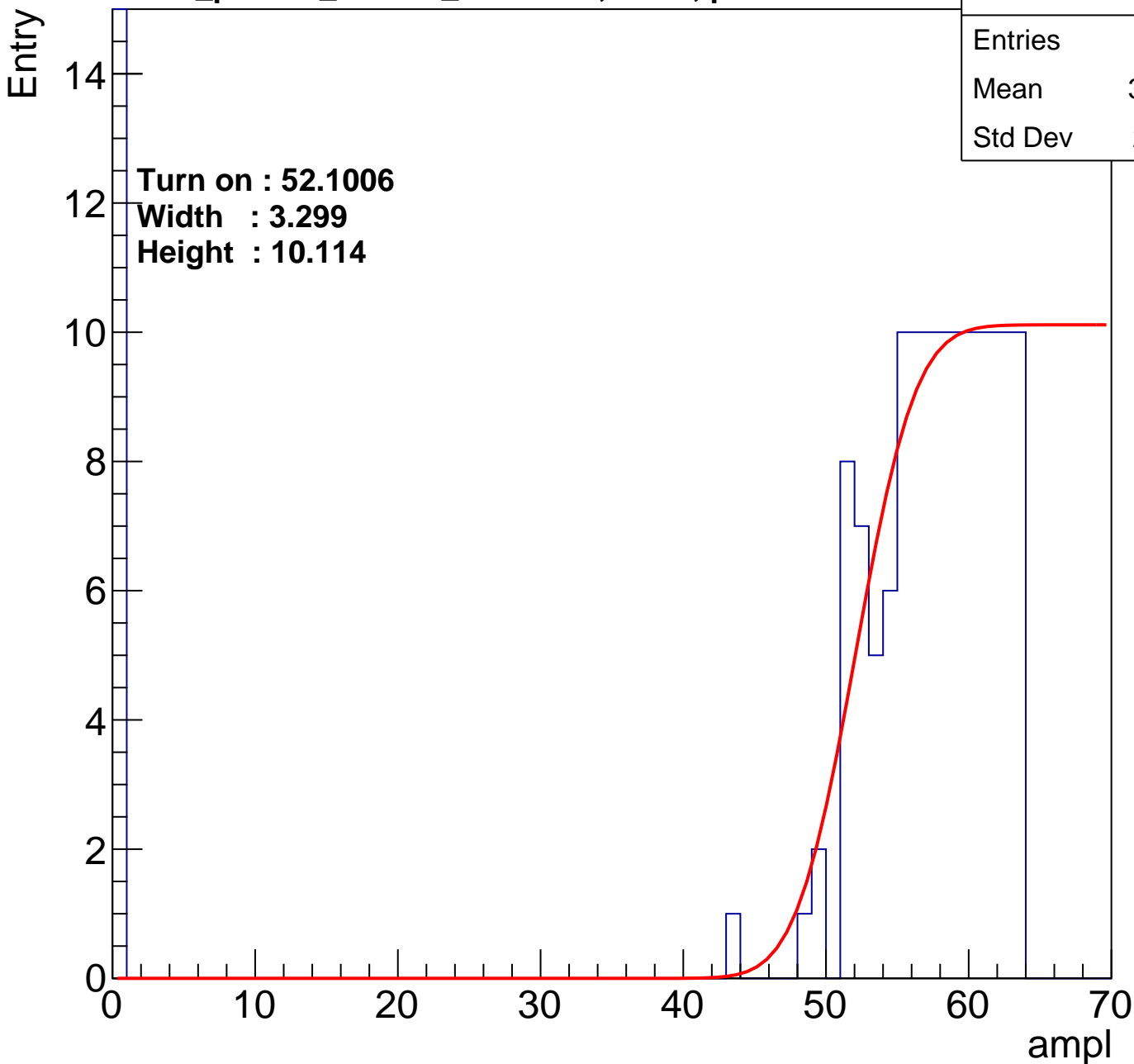
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	35.54
Std Dev	27.91

Turn on : 52.1006

Width : 3.299

Height : 10.114



B1L104S, U26-ch81

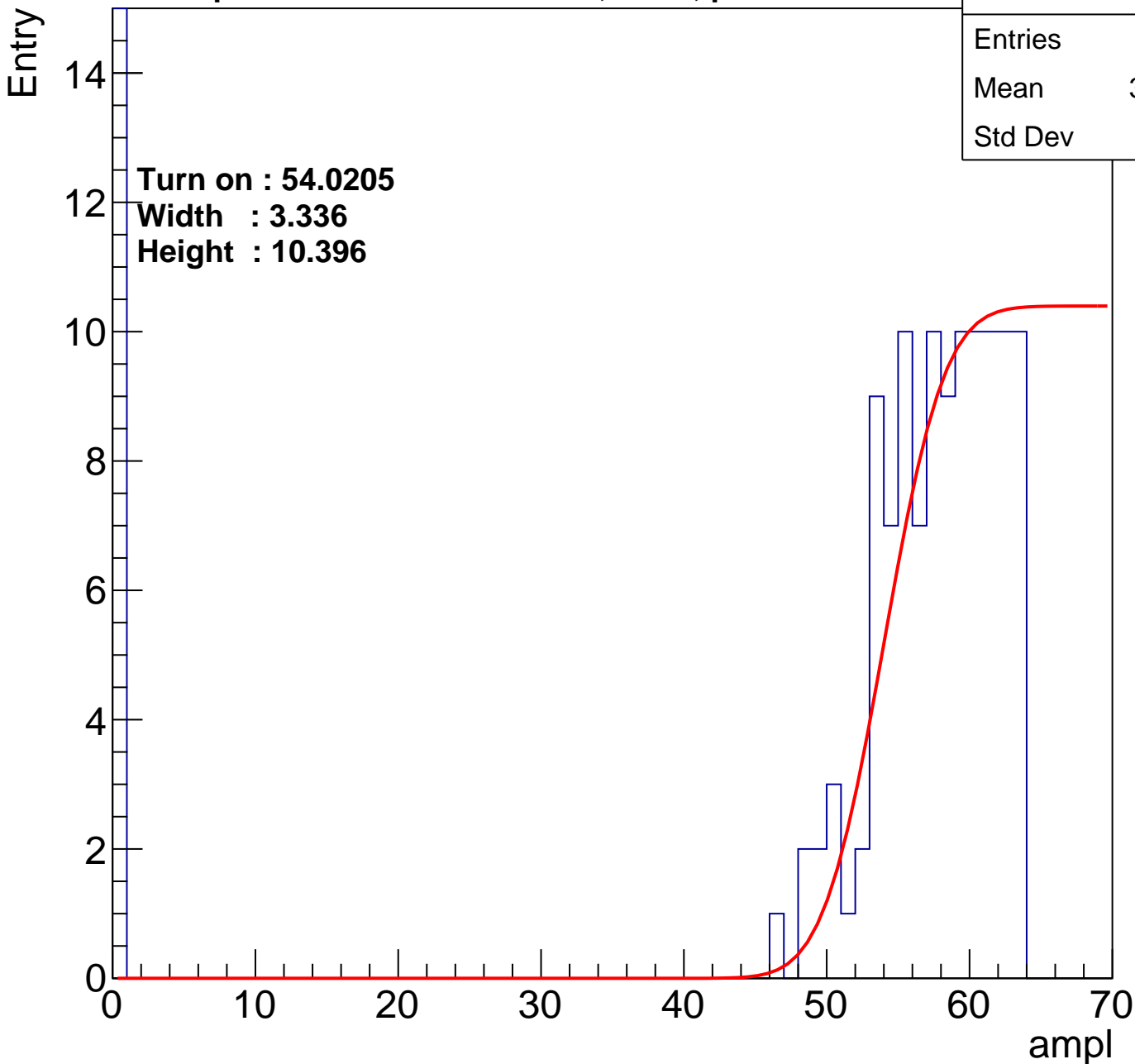
calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	38.37
Std Dev	27.2

Turn on : 54.0205

Width : 3.336

Height : 10.396



B1L104S, U26-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	31.41
Std Dev	28.45

Turn on : 51.7550

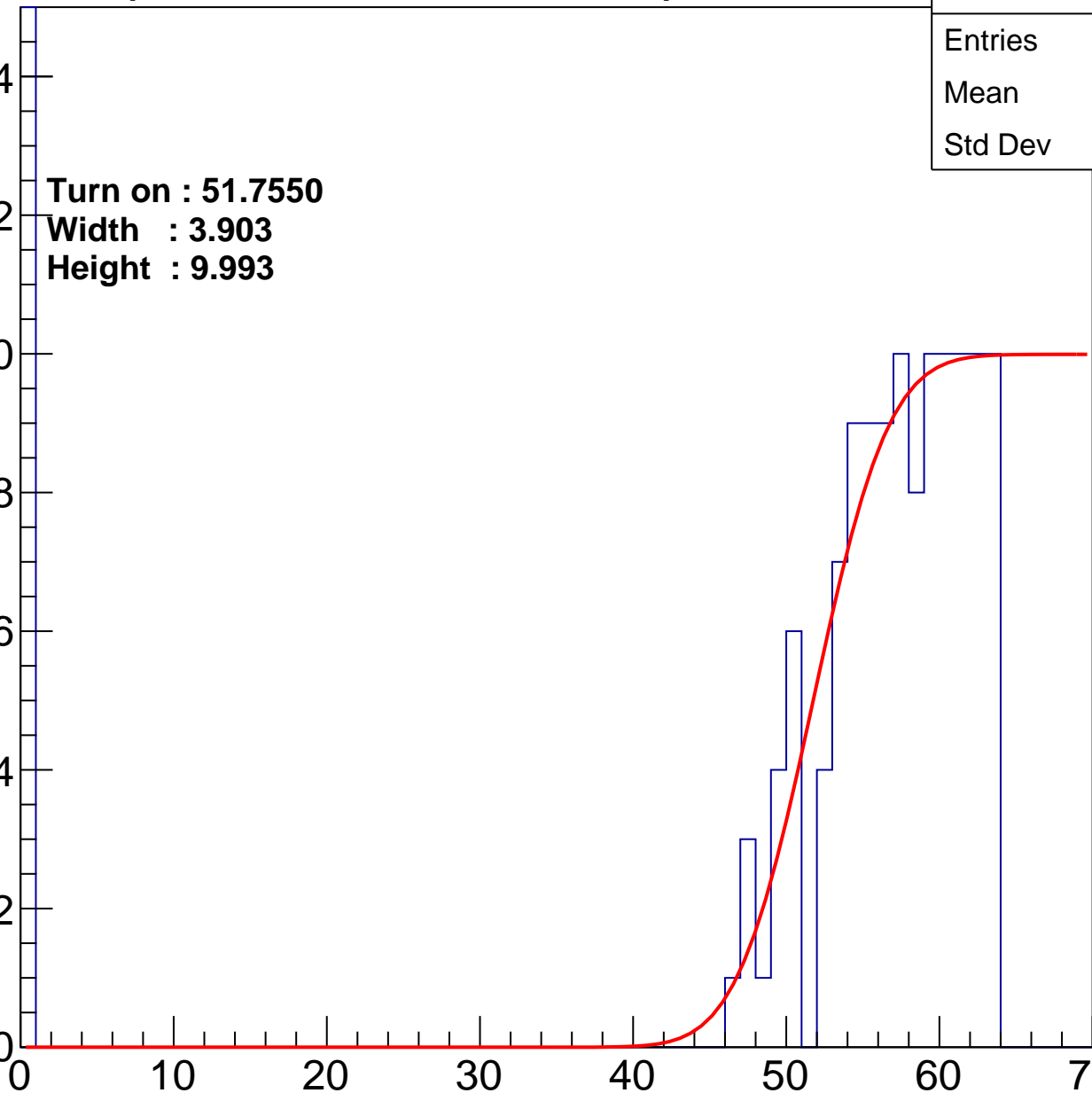
Width : 3.903

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	41.18
Std Dev	25.7

Turn on : 53.1566

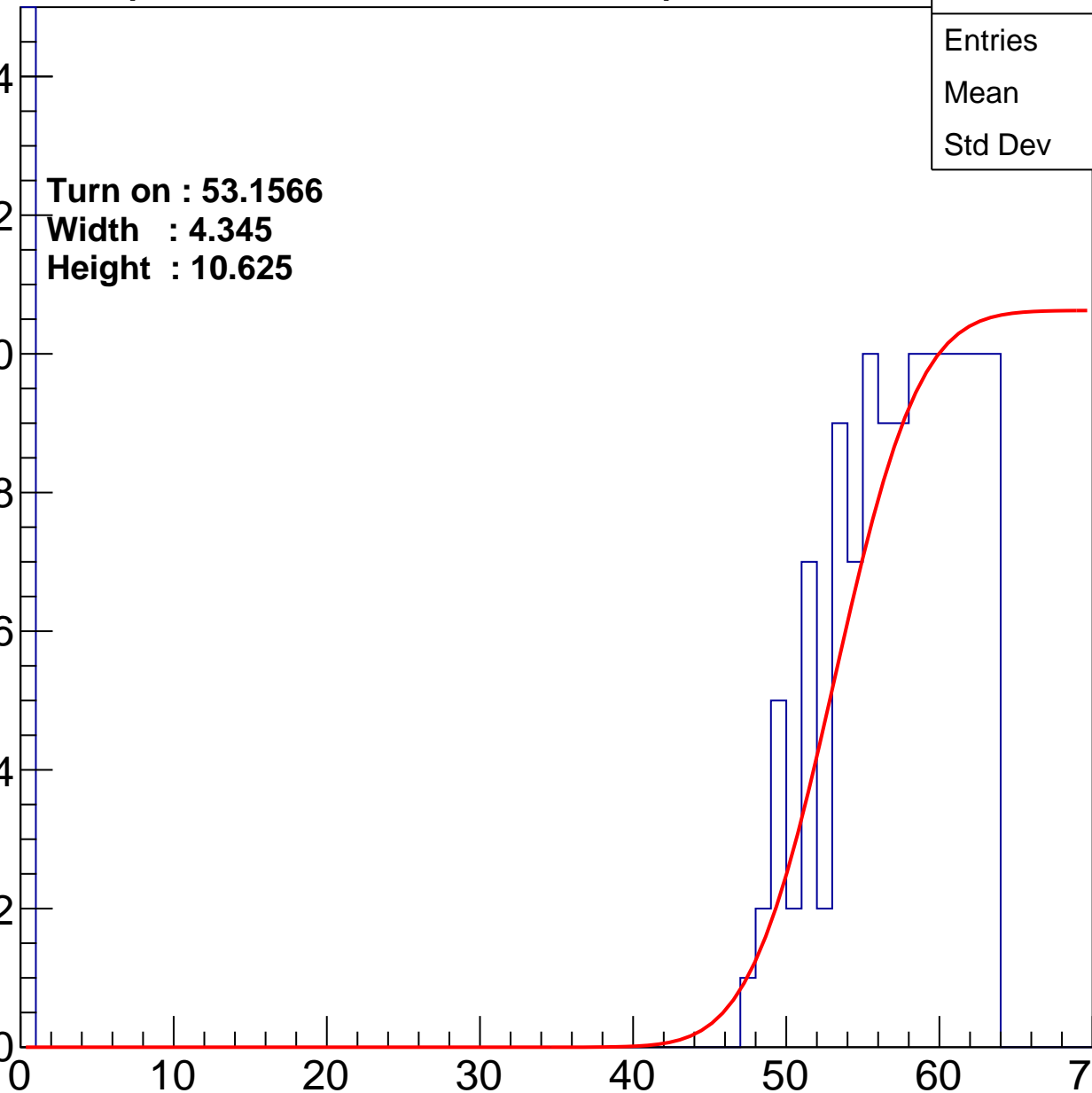
Width : 4.345

Height : 10.625

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch84

calib_packv5_033123_0516.root, FC#4, port A1

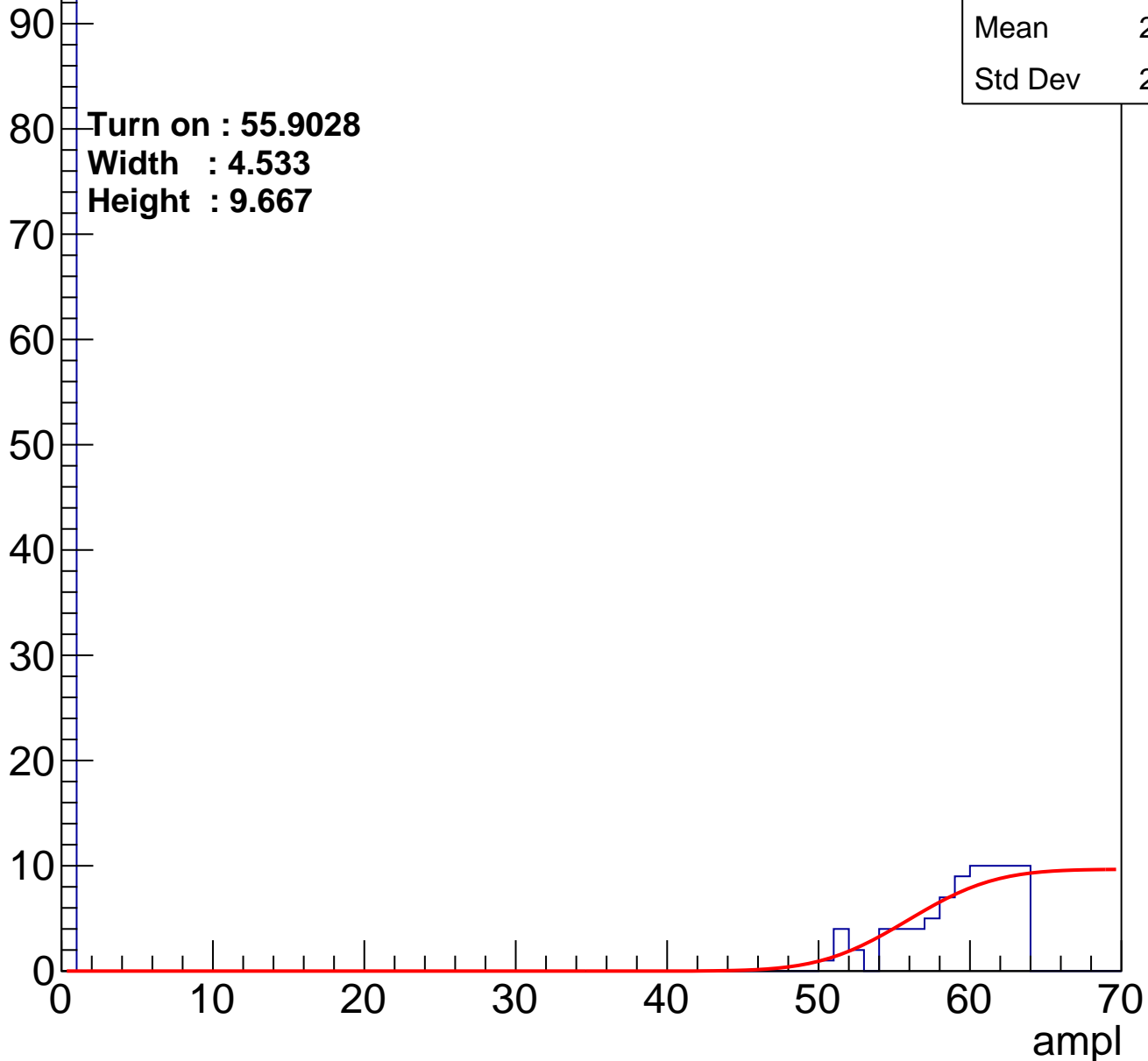
Entries	173
Mean	27.17
Std Dev	29.39

Turn on : 55.9028

Width : 4.533

Height : 9.667

Entry



B1L104S, U26-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	34.09
Std Dev	28.62

Turn on : 53.6344

Width : 3.578

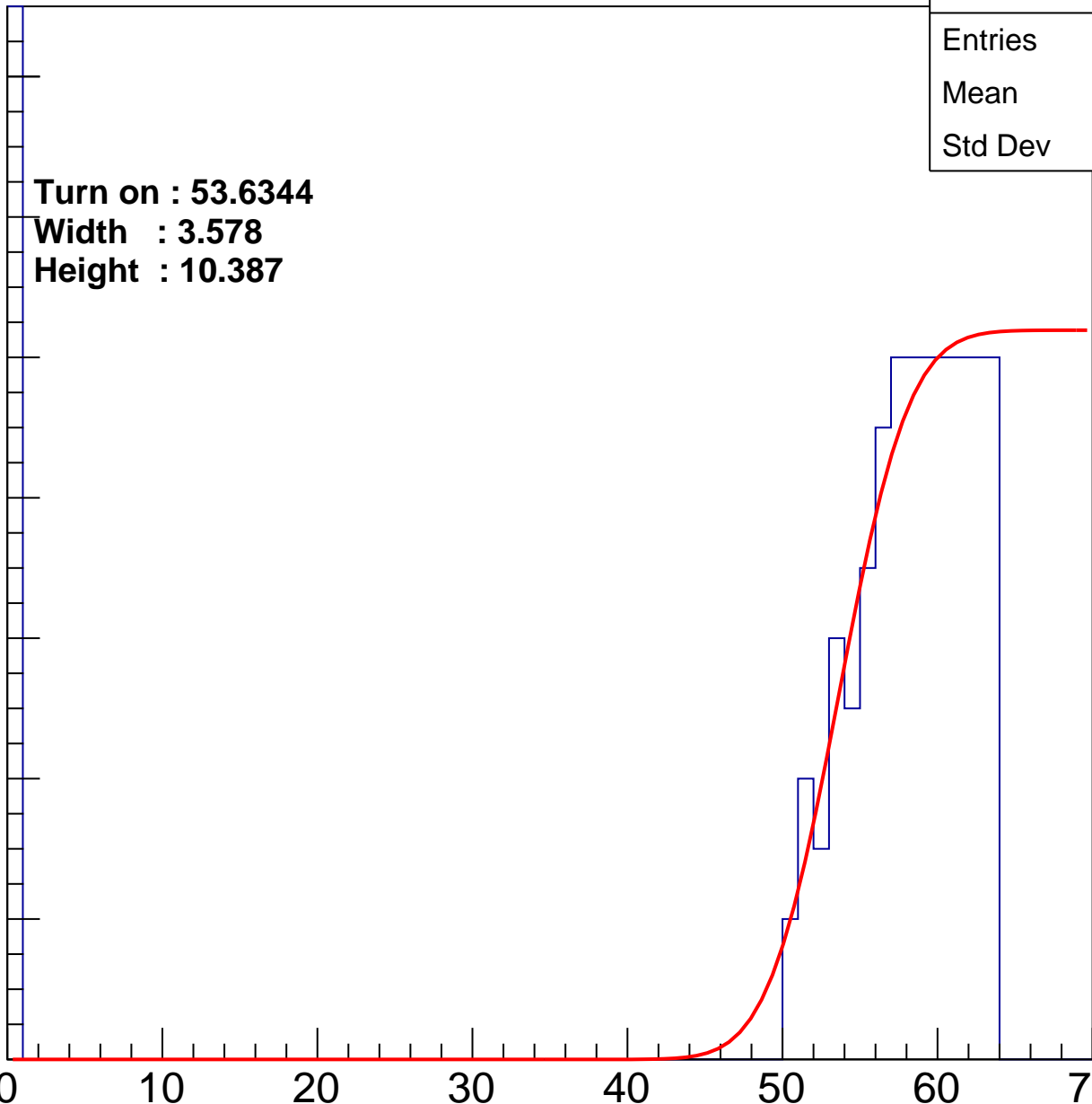
Height : 10.387

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U26-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.1
Std Dev	28.31

Turn on : 52.8243

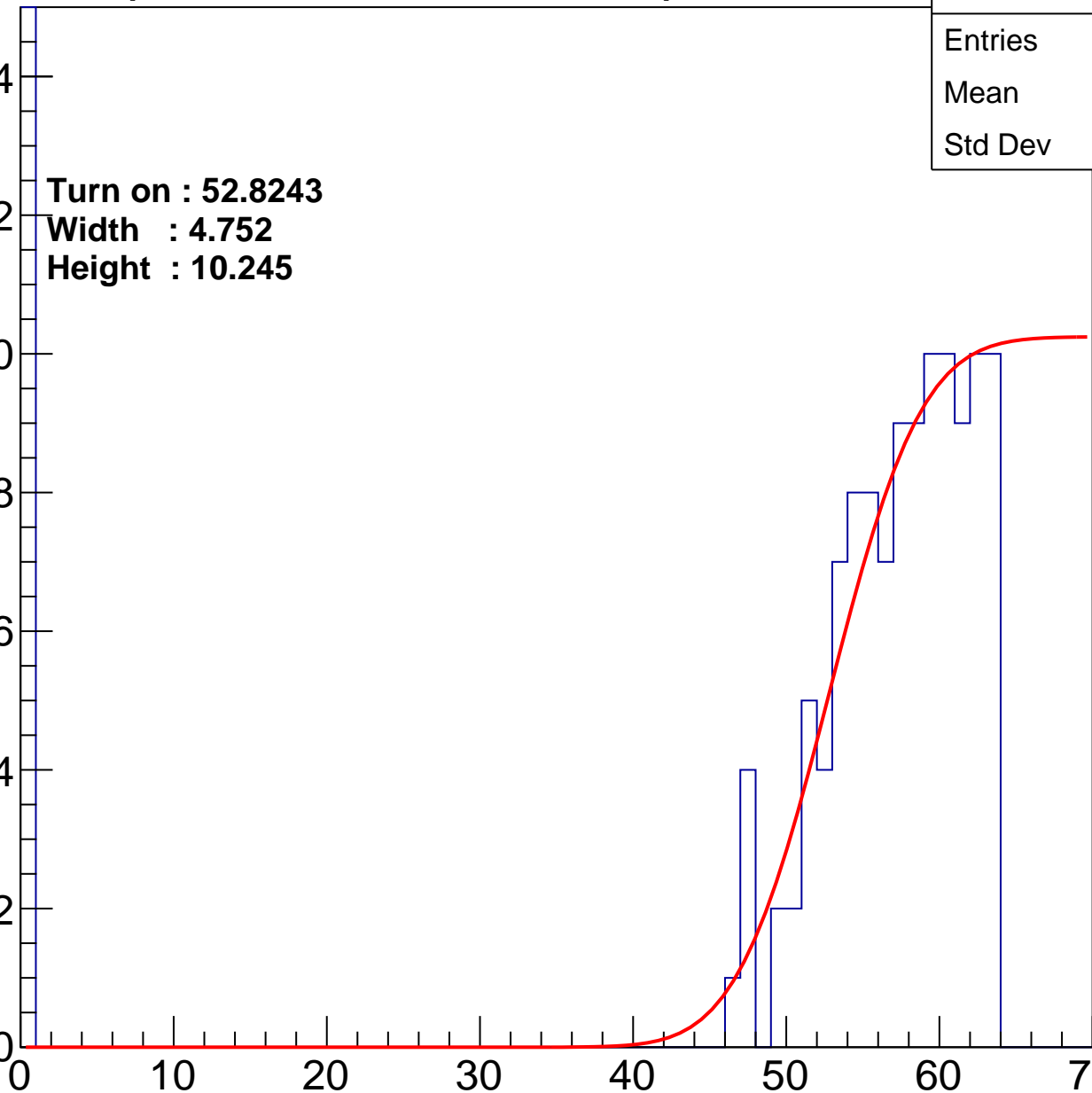
Width : 4.752

Height : 10.245

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch87

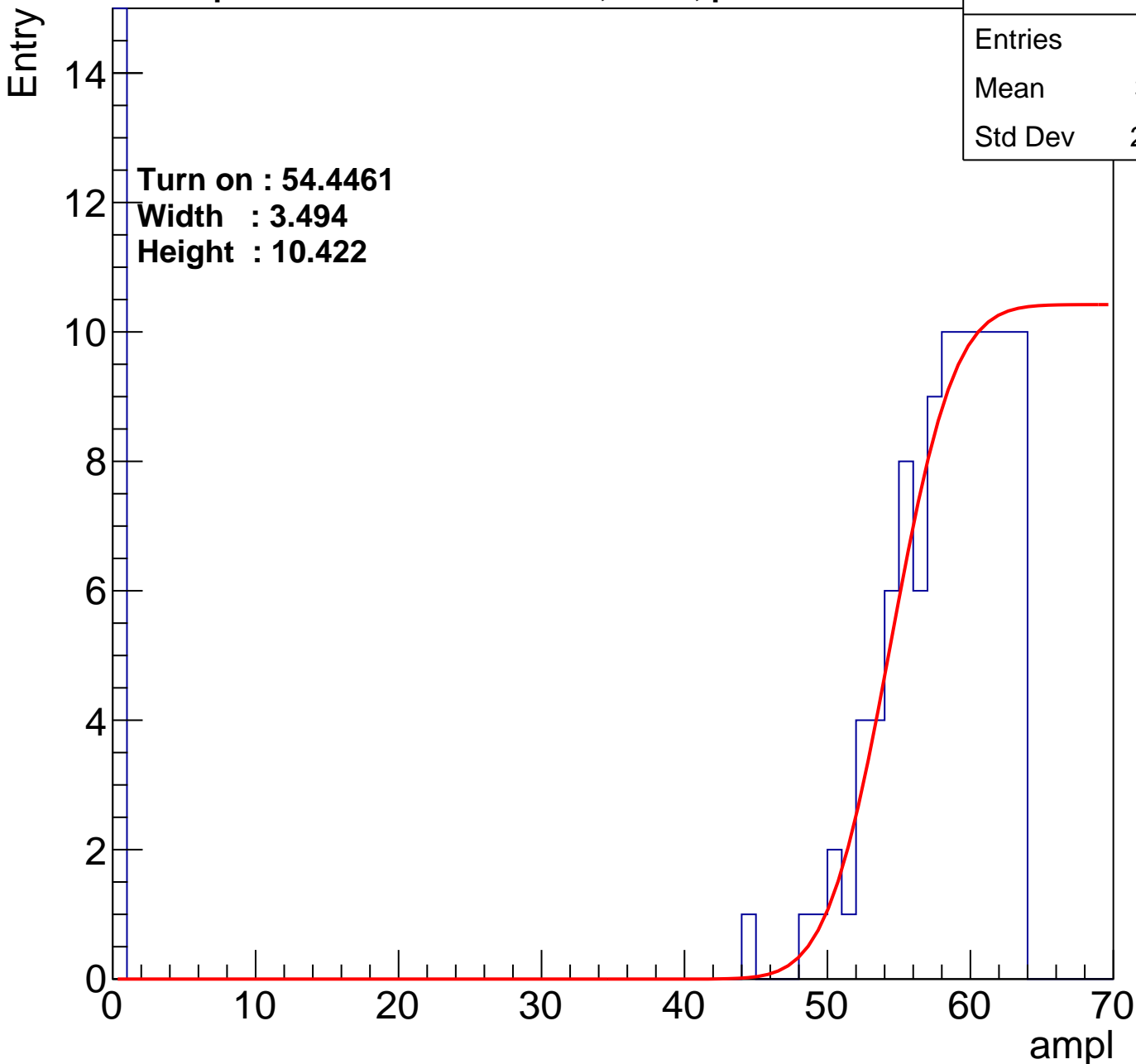
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	31.51
Std Dev	28.93

Turn on : 54.4461

Width : 3.494

Height : 10.422



B1L104S, U26-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	35.78
Std Dev	28.17

Turn on : 53.7879

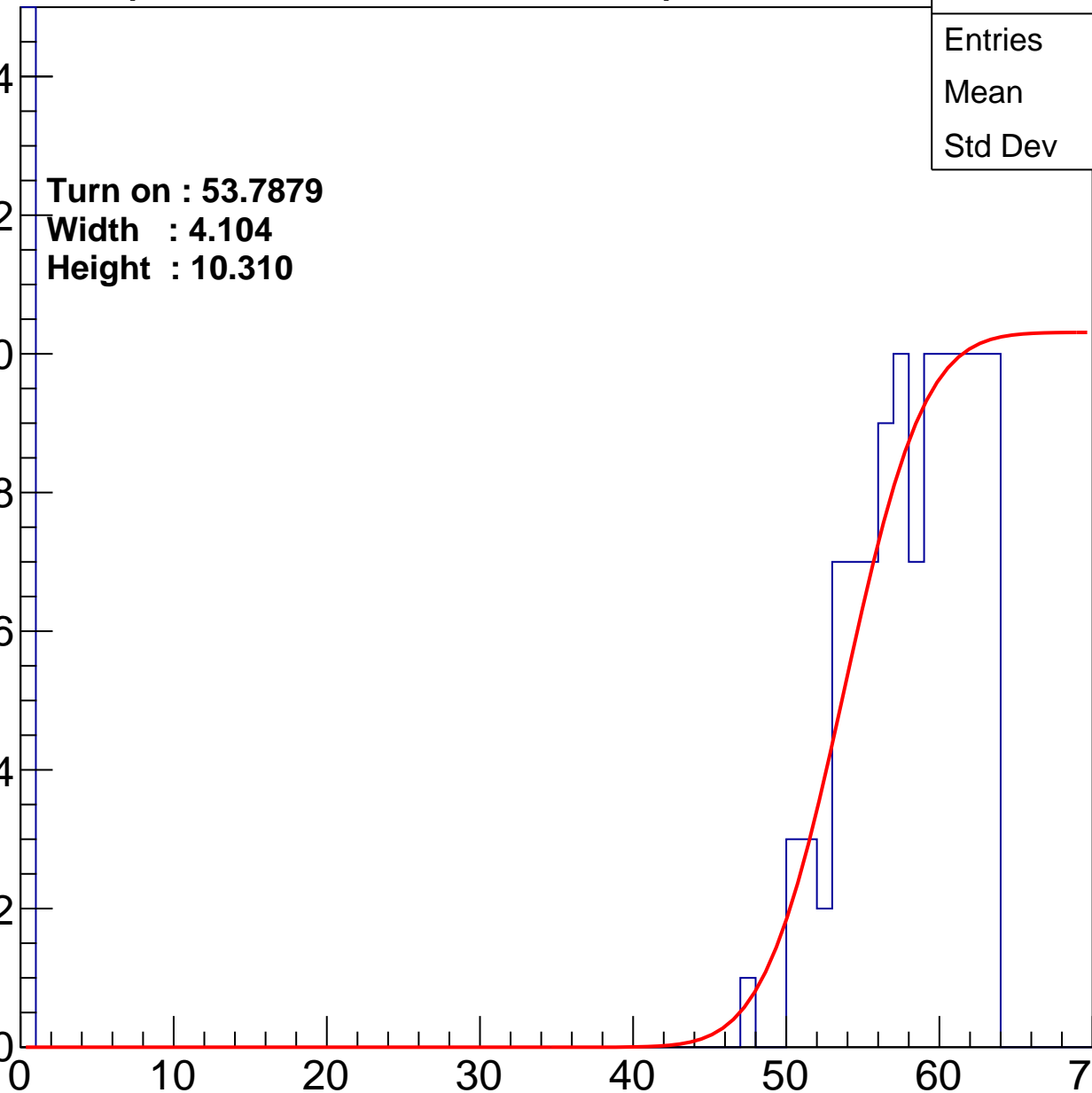
Width : 4.104

Height : 10.310

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	33.08
Std Dev	28.14

Turn on : 52.7233

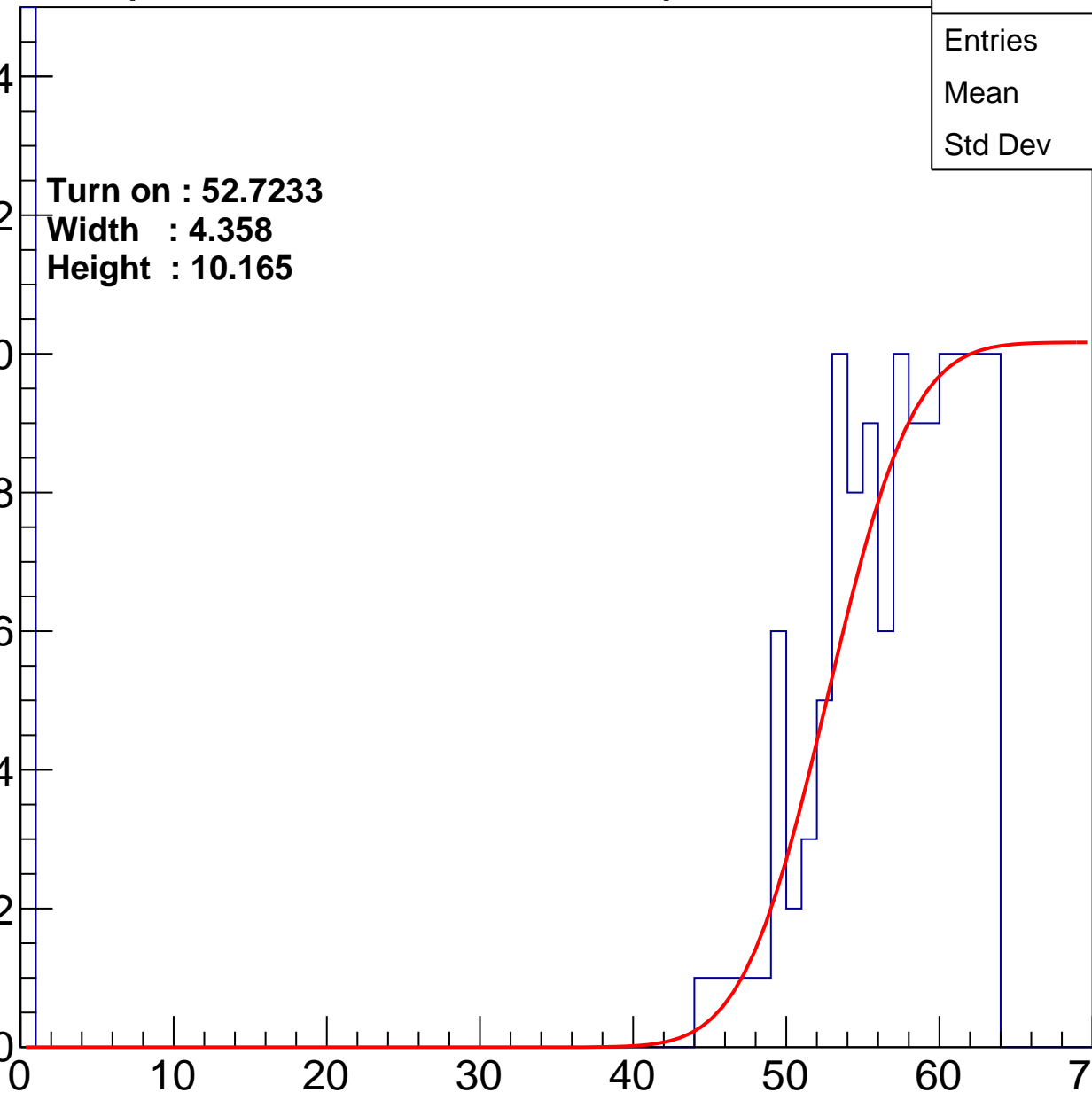
Width : 4.358

Height : 10.165

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	38.19
Std Dev	26.9

Turn on : 51.9458

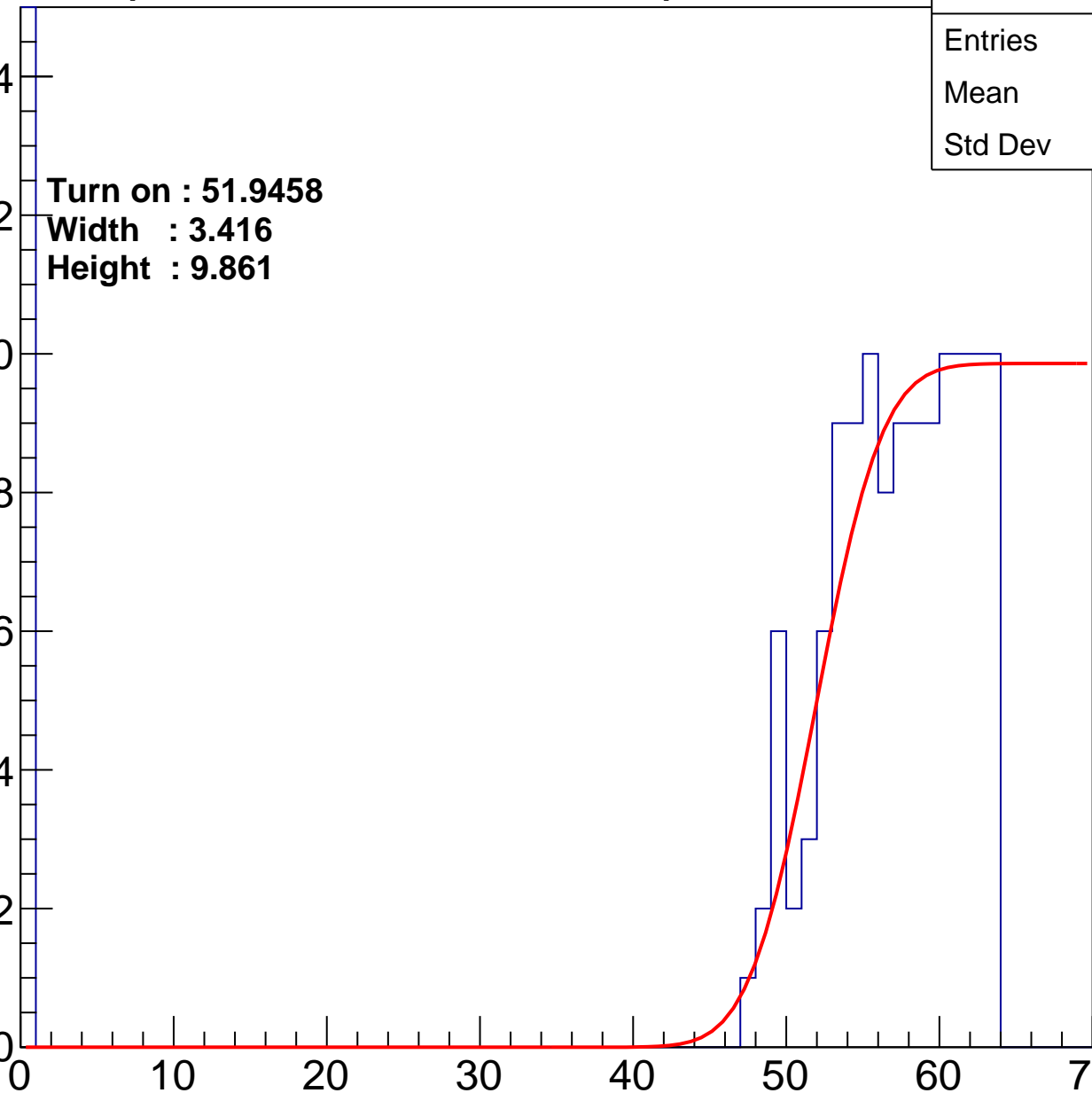
Width : 3.416

Height : 9.861

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	33.04
Std Dev	29.12

Turn on : 55.2169

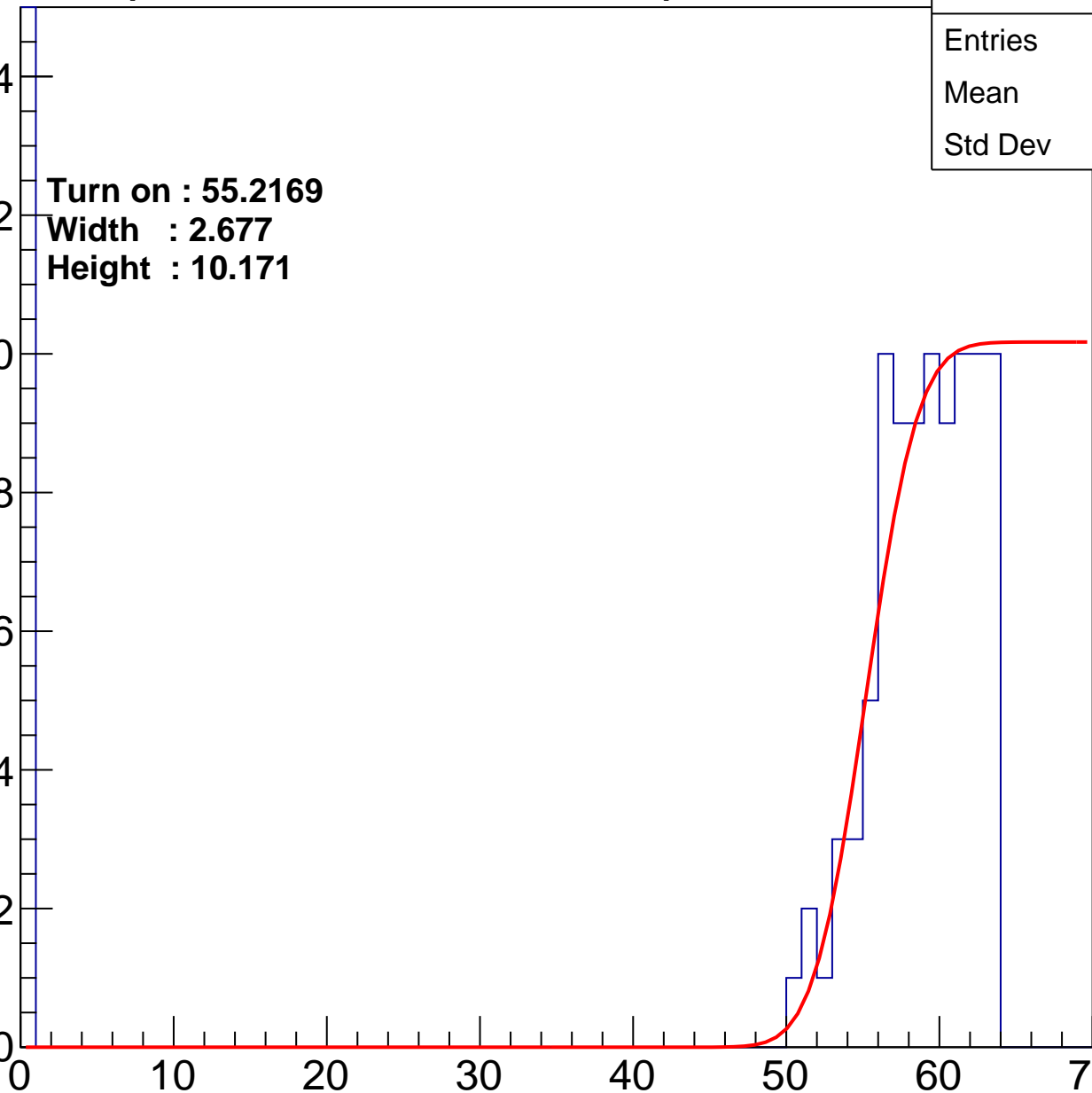
Width : 2.677

Height : 10.171

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	30.58
Std Dev	28.88

Turn on : 53.4831

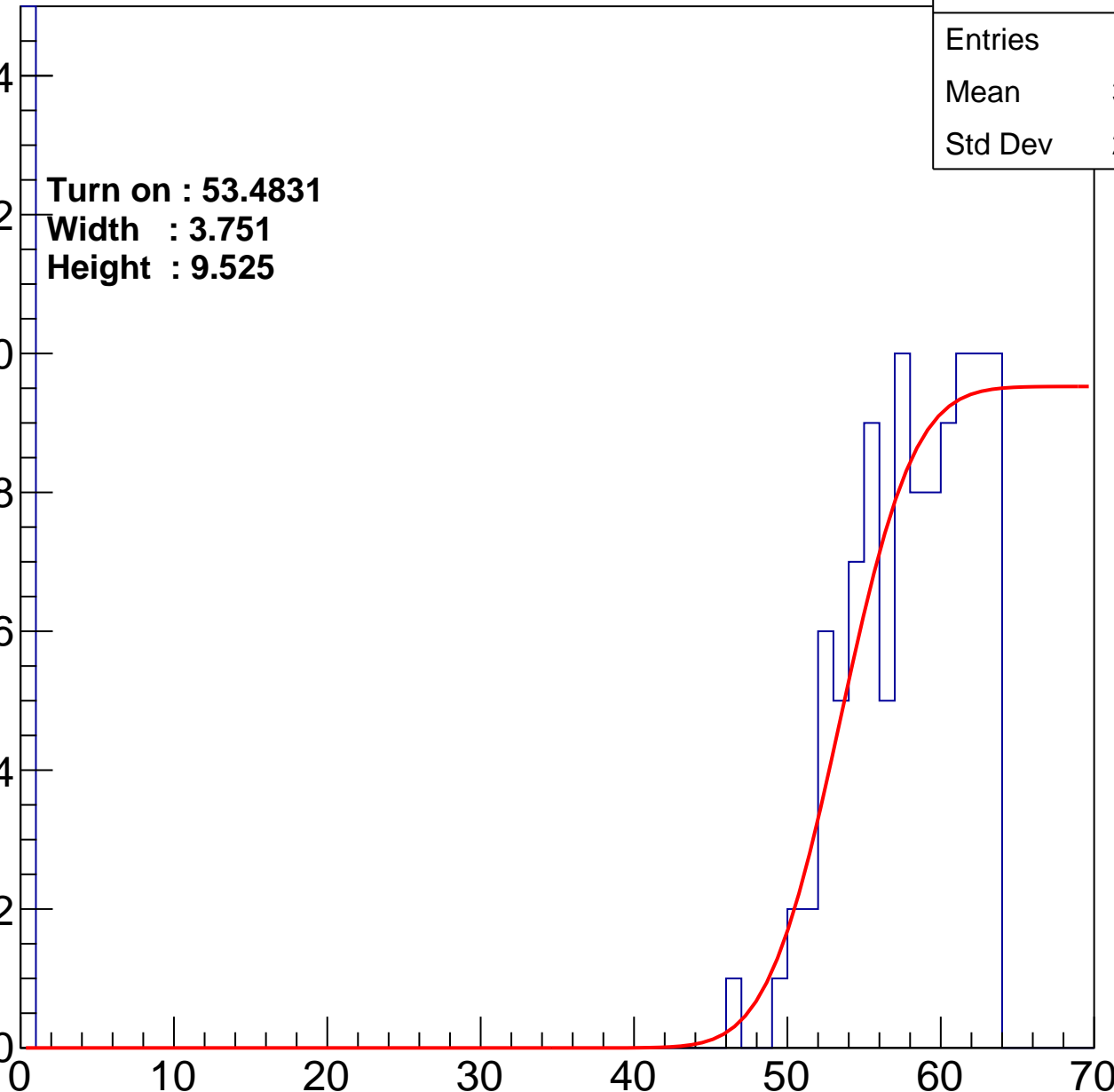
Width : 3.751

Height : 9.525

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	32.38
Std Dev	29.26

Turn on : 55.7936

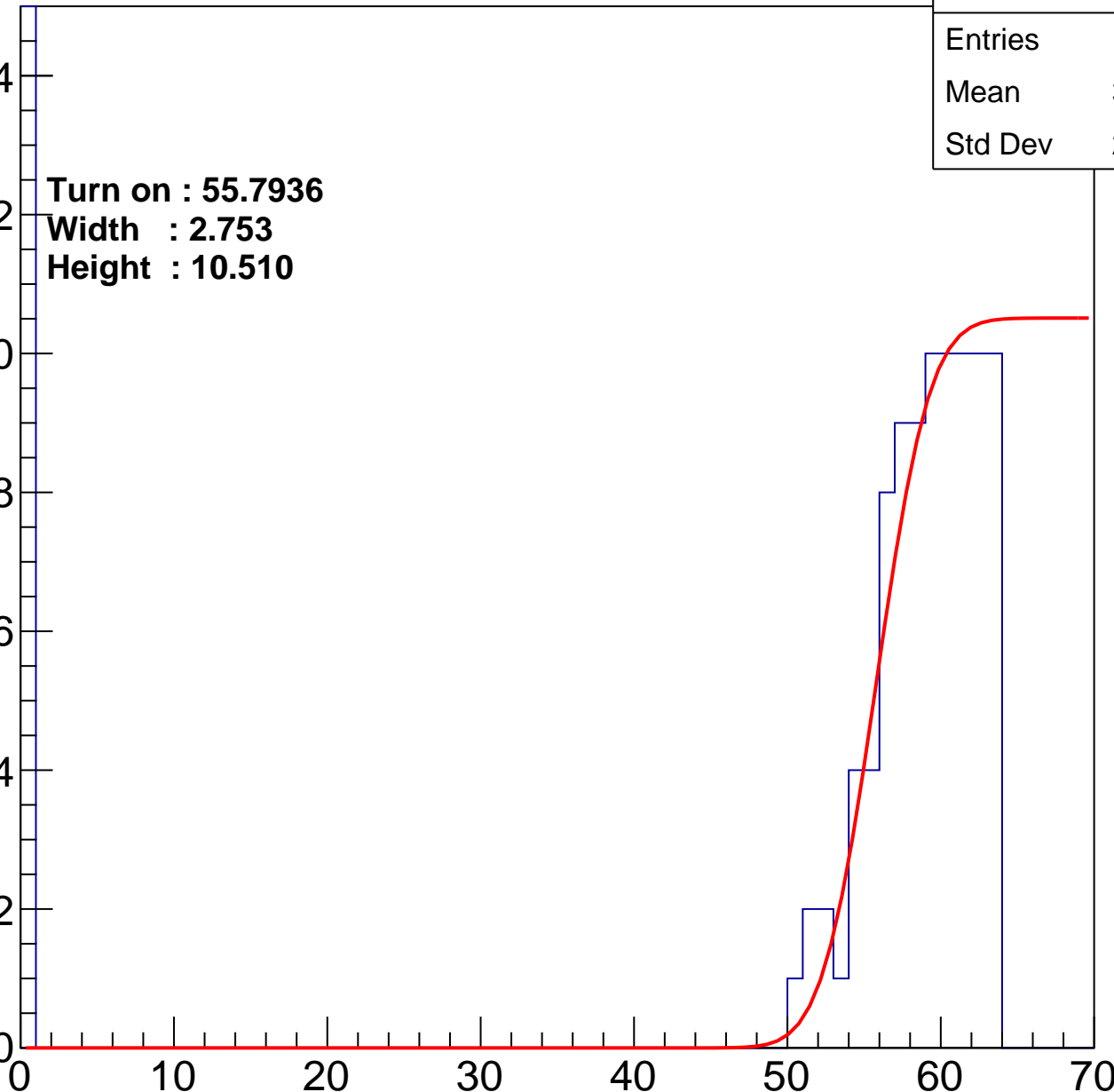
Width : 2.753

Height : 10.510

Entry

14
12
10
8
6
4
2
0

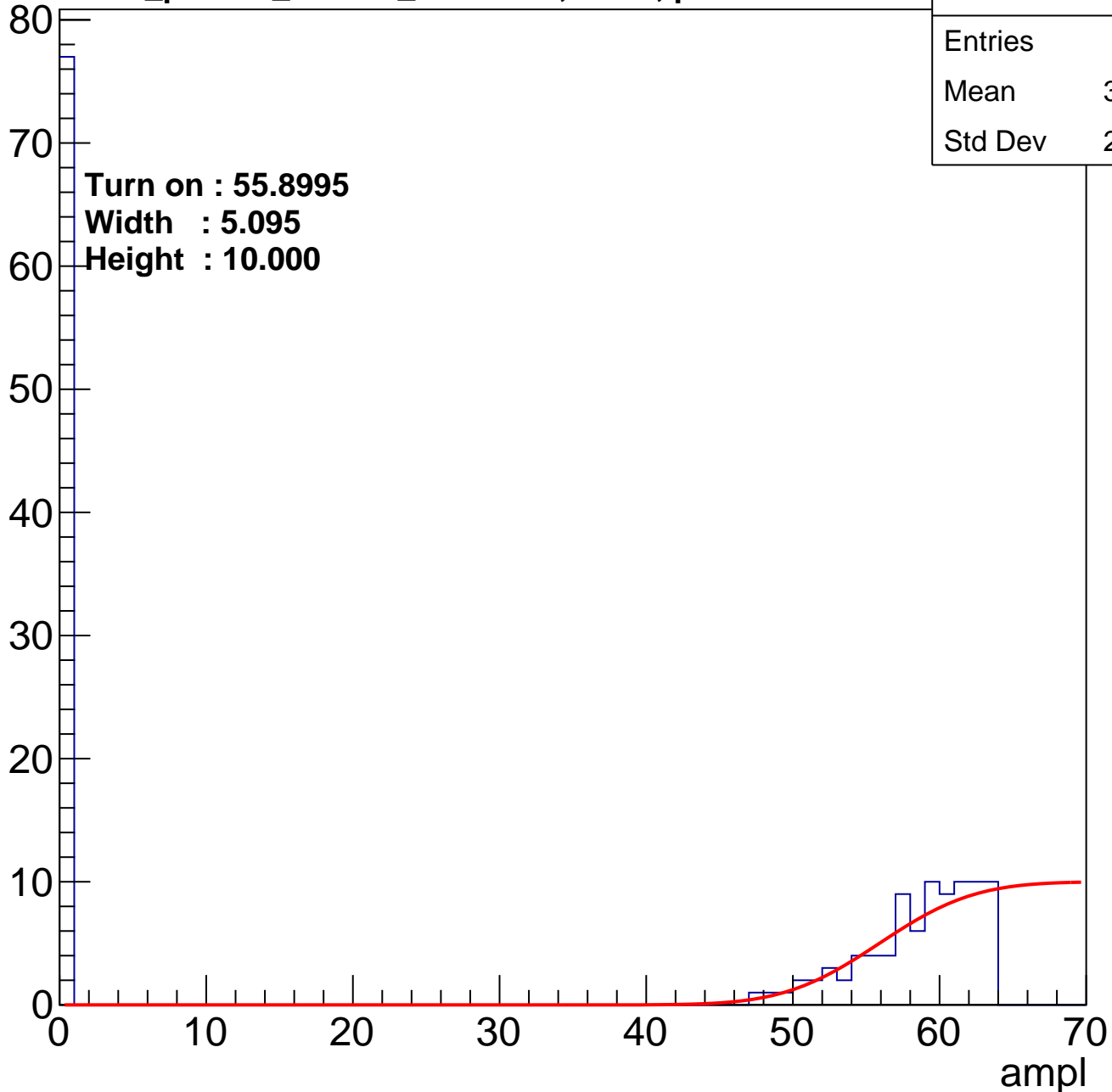
ampl



B1L104S, U26-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entry



B1L104S, U26-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	34.19
Std Dev	28.75

Turn on : 54.9695

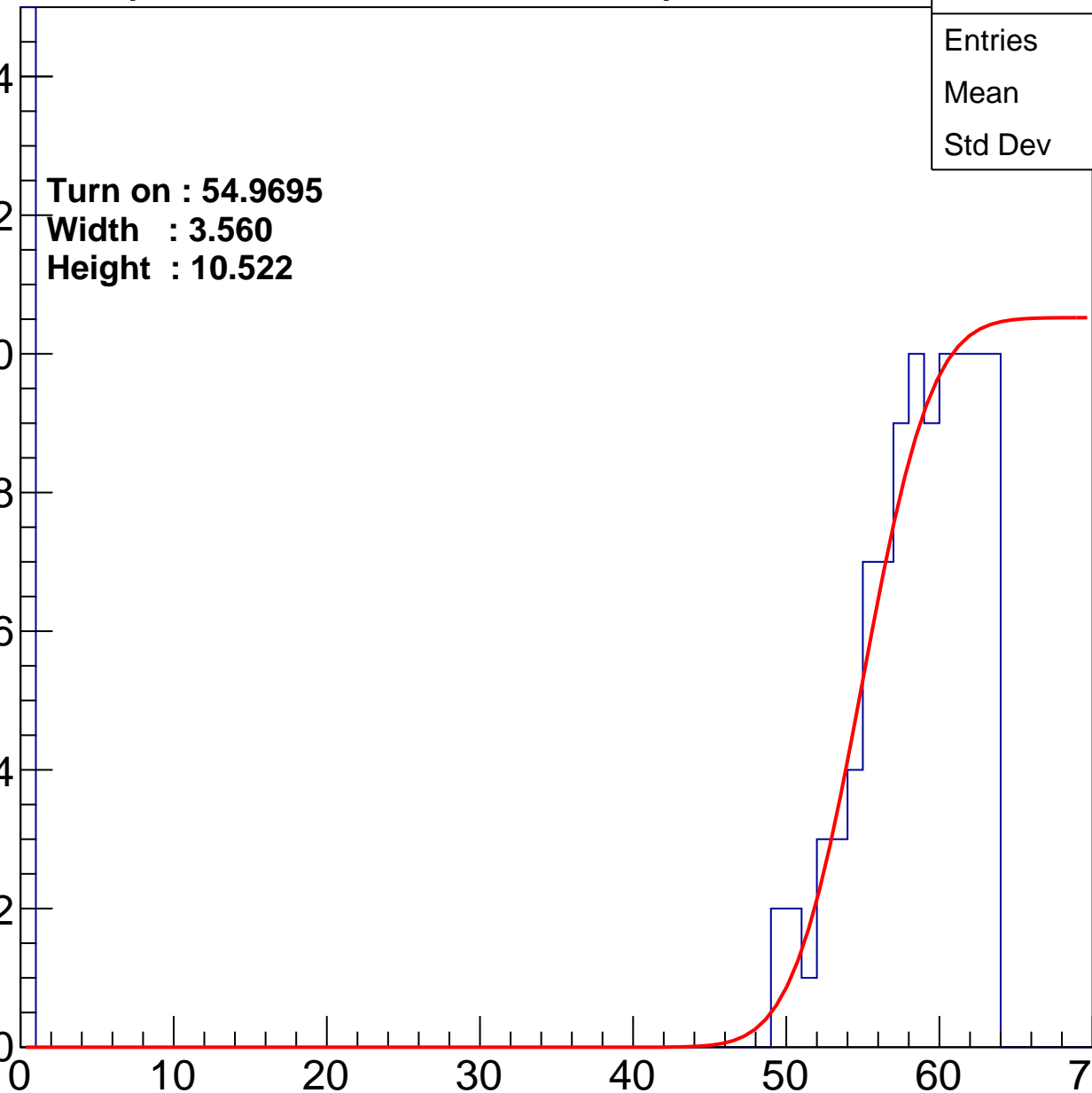
Width : 3.560

Height : 10.522

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	30.26
Std Dev	28.95

Turn on : 53.9646

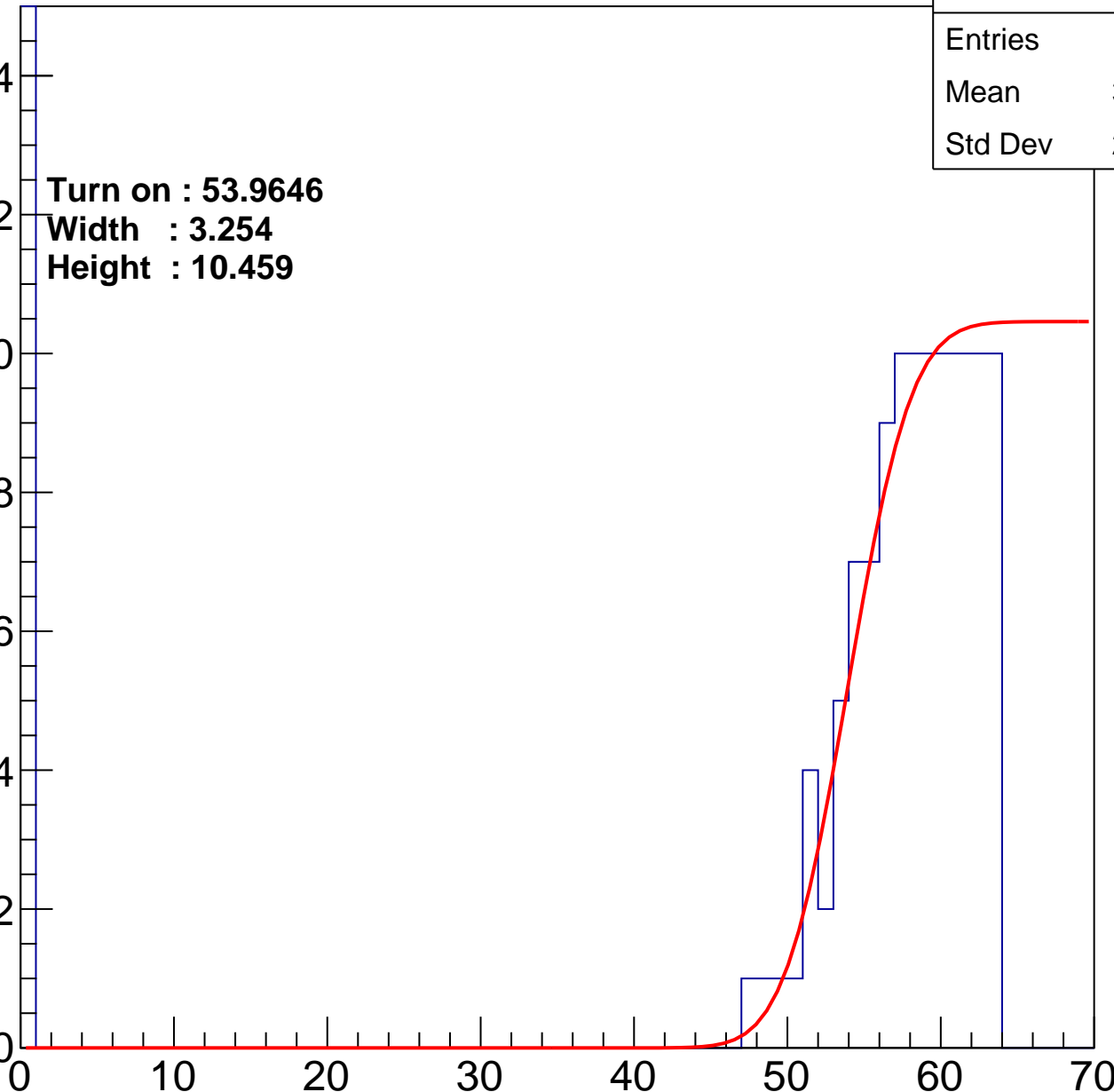
Width : 3.254

Height : 10.459

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	28.03
Std Dev	29.1

Turn on : 54.2974

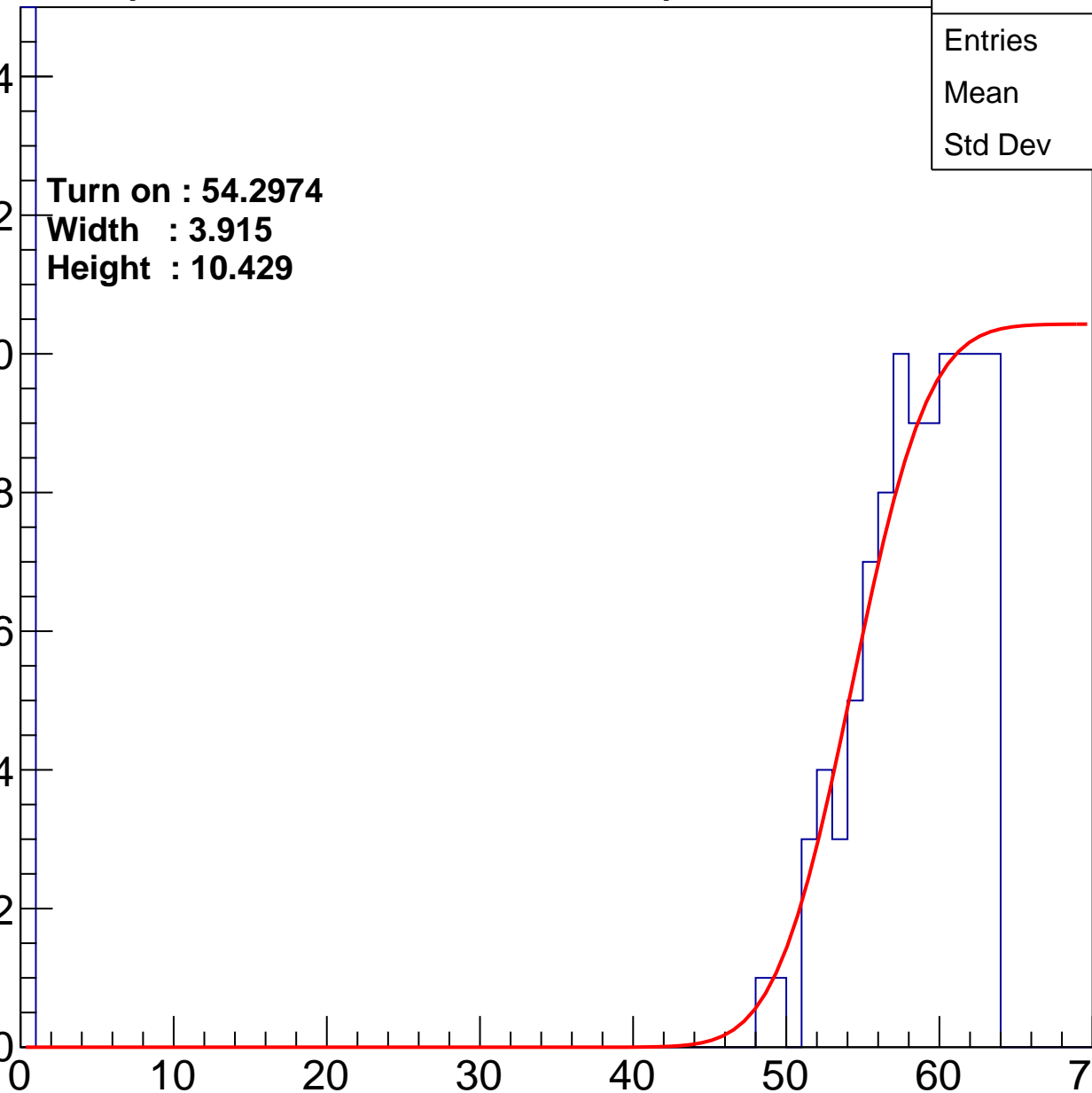
Width : 3.915

Height : 10.429

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch98

calib_packv5_033123_0516.root, FC#4, port A1

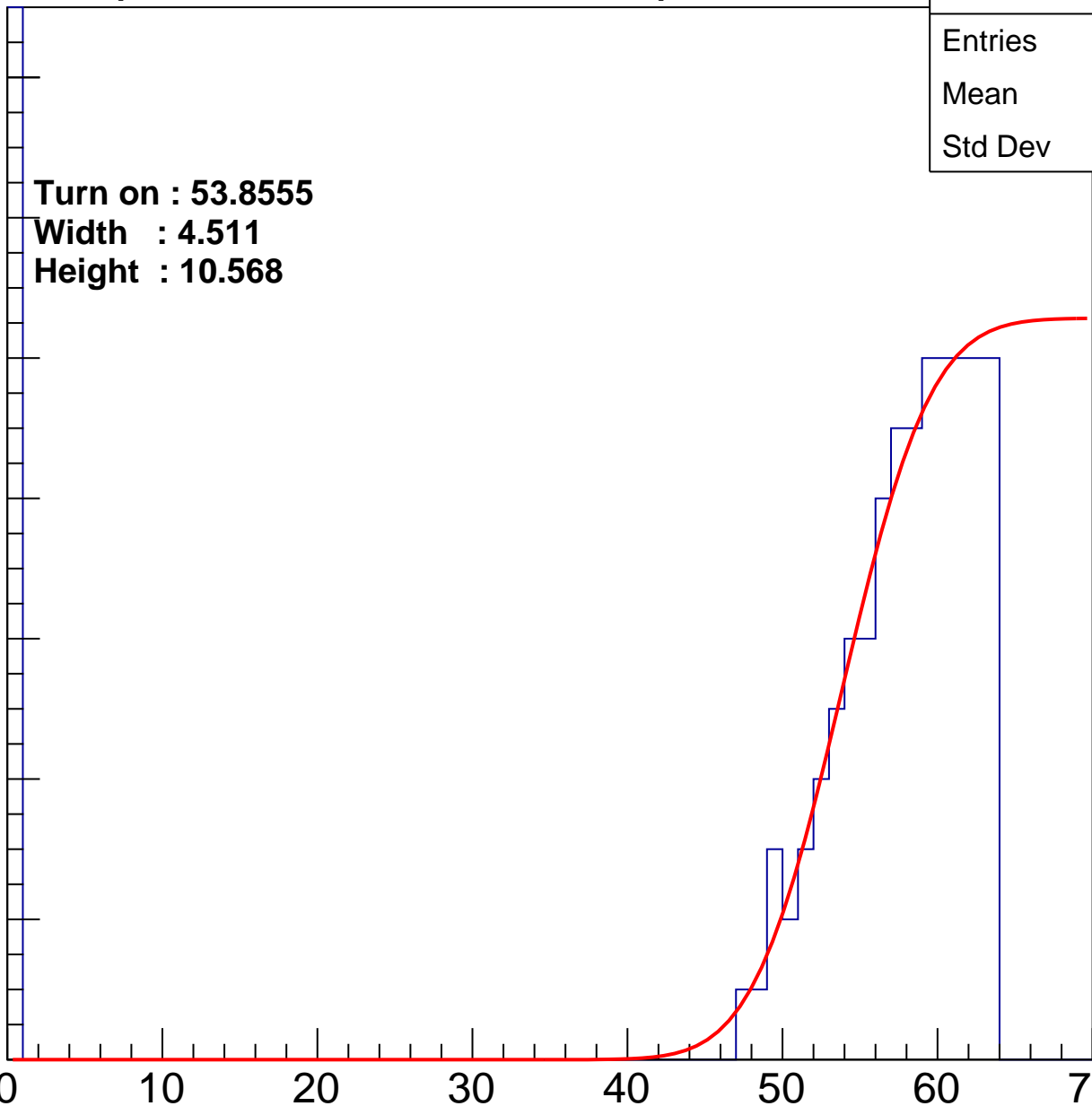
Entry

14
12
10
8
6
4
2
0

Turn on : 53.8555
Width : 4.511
Height : 10.568

Entries	165
Mean	37.3
Std Dev	27.65

ampl



B1L104S, U26-ch99

calib_packv5_033123_0516.root, FC#4, port A1

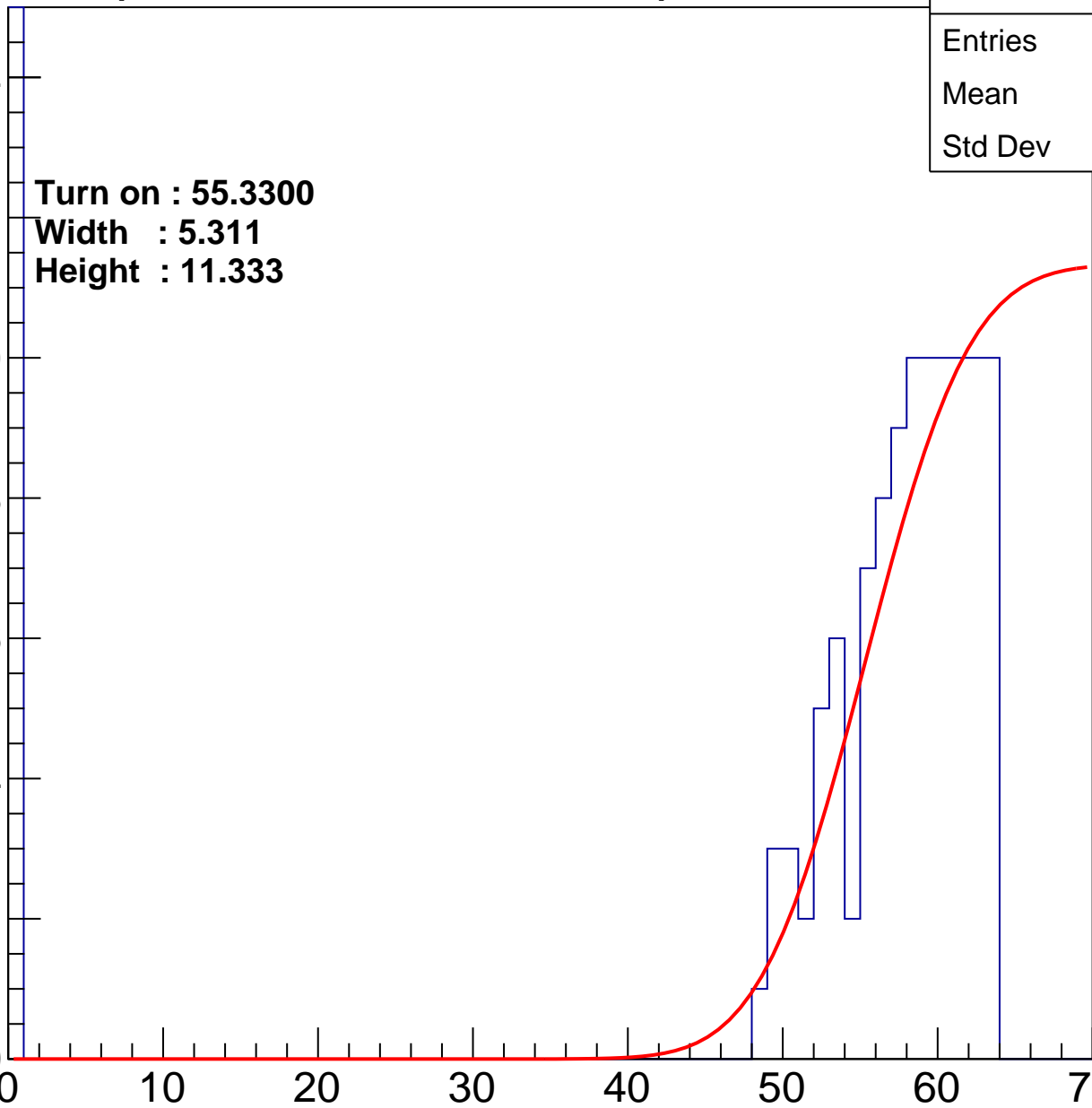
Entry

14
12
10
8
6
4
2
0

Turn on : 55.3300
Width : 5.311
Height : 11.333

Entries	168
Mean	36.36
Std Dev	27.98

ampl



B1L104S, U26-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	28.93
Std Dev	29.36

Turn on : 55.9972

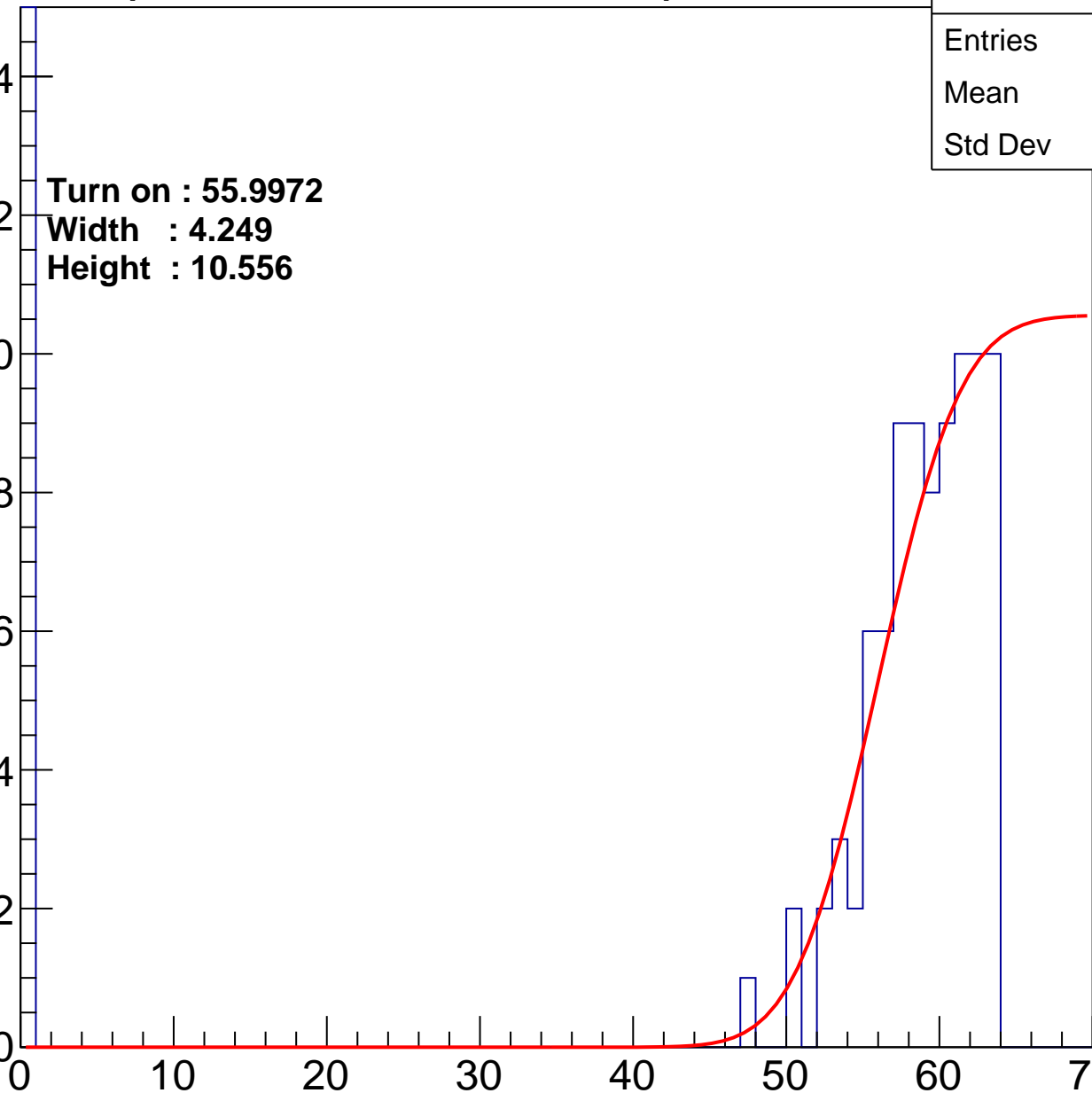
Width : 4.249

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch101

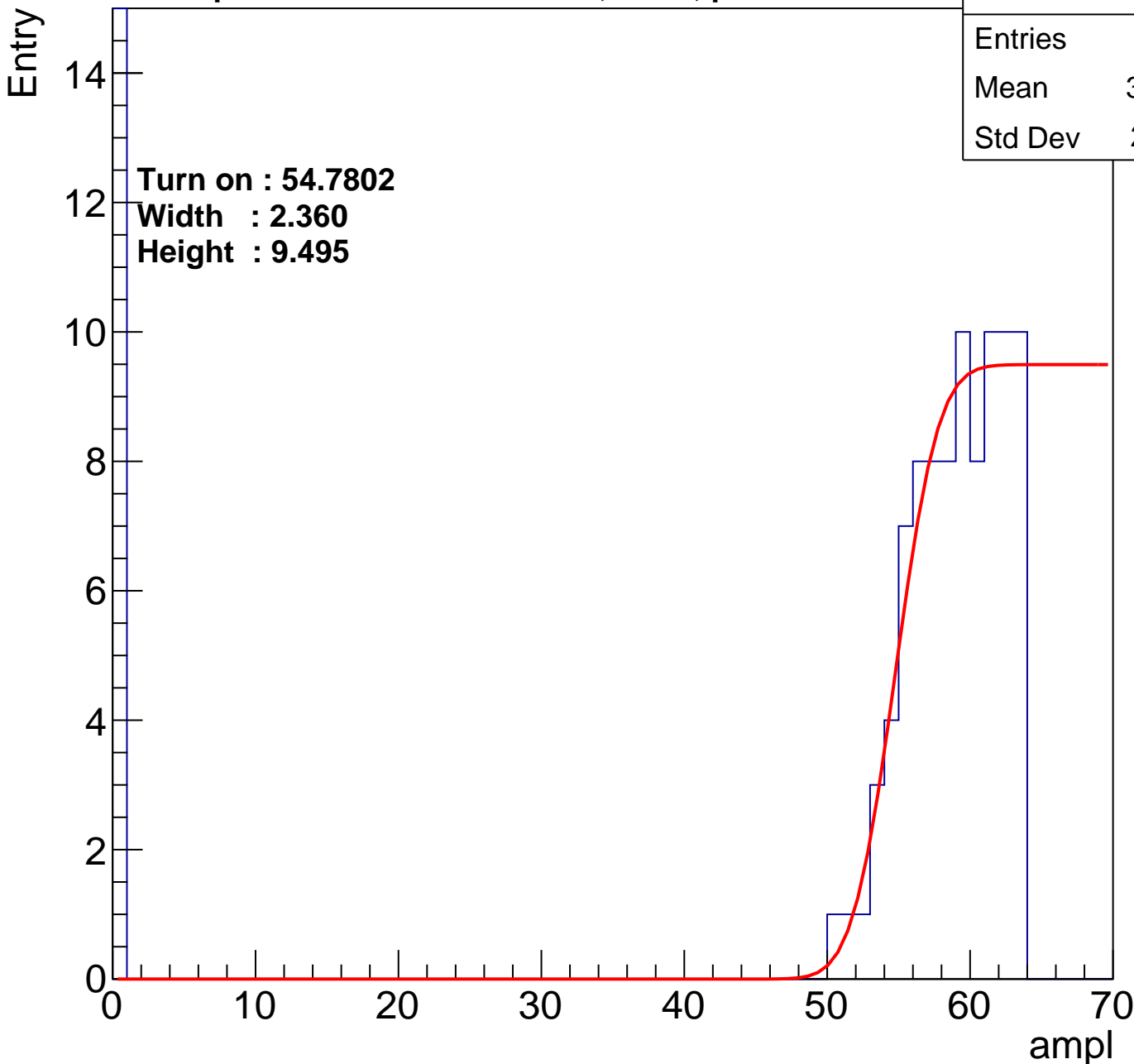
calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	32.37
Std Dev	29.21

Turn on : 54.7802

Width : 2.360

Height : 9.495



B1L104S, U26-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	34.4
Std Dev	28.22

Turn on : 53.9120

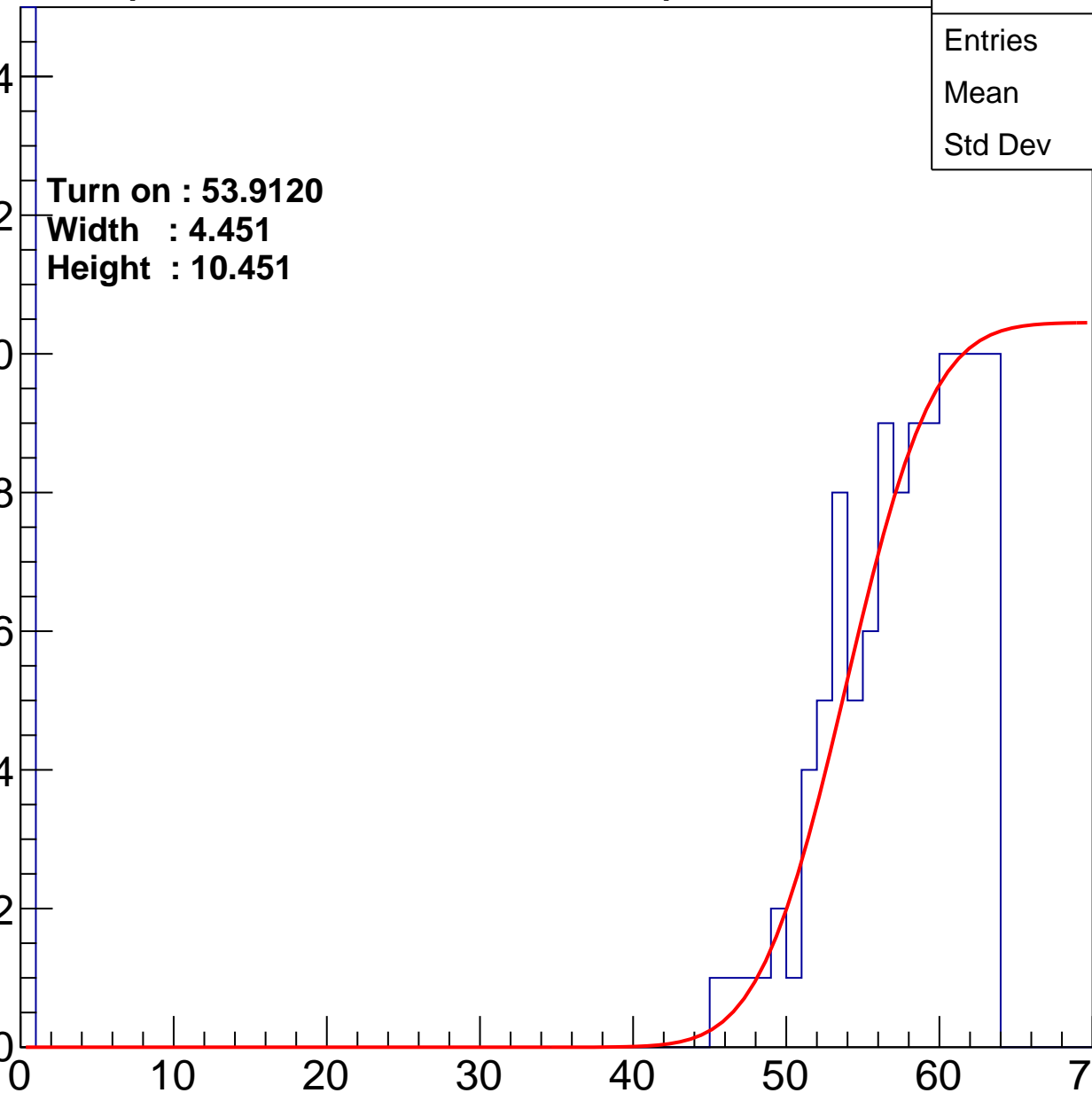
Width : 4.451

Height : 10.451

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	33.37
Std Dev	28.68

Turn on : 53.9540

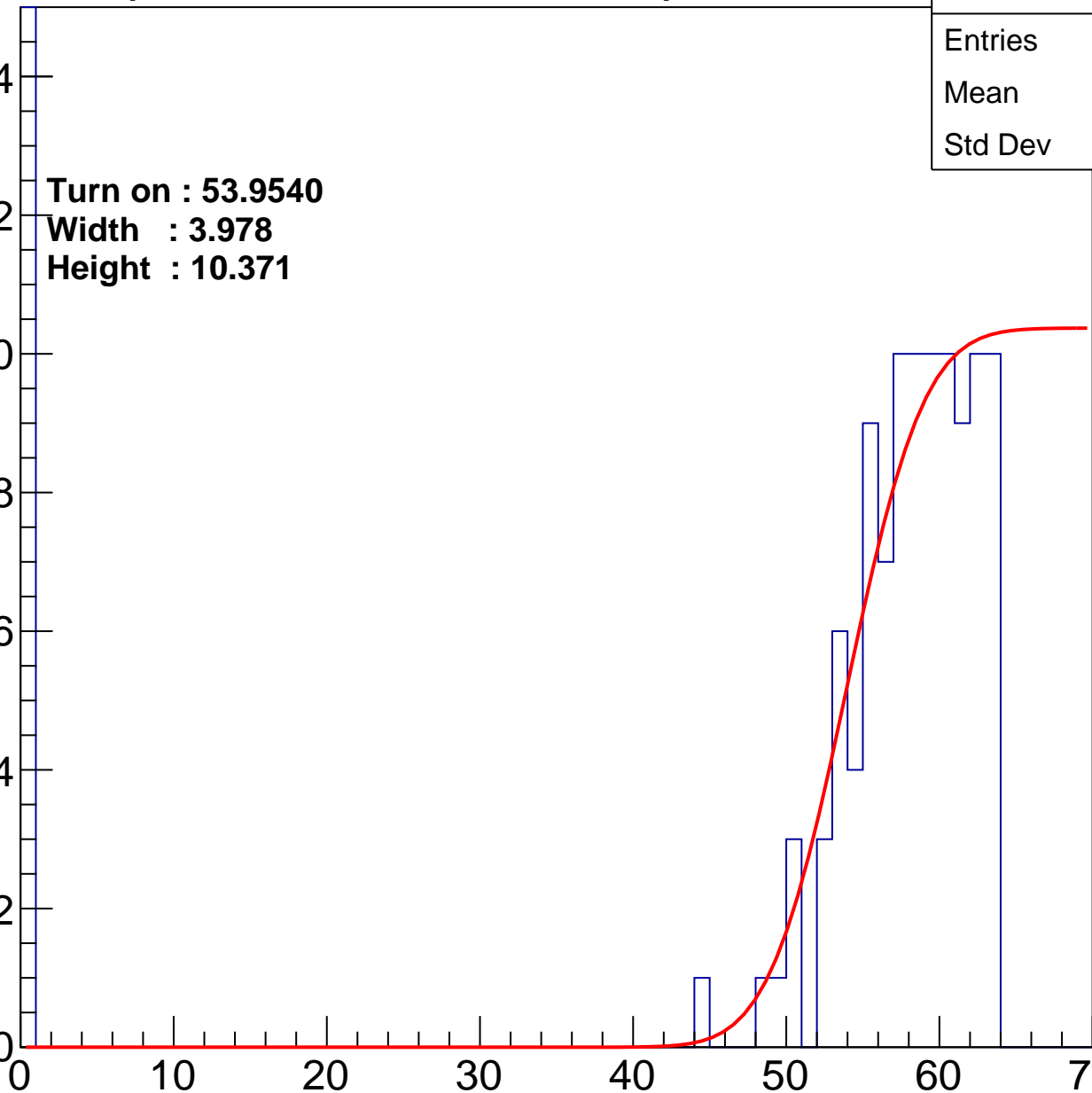
Width : 3.978

Height : 10.371

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	34.96
Std Dev	28.59

Turn on : 55.3527

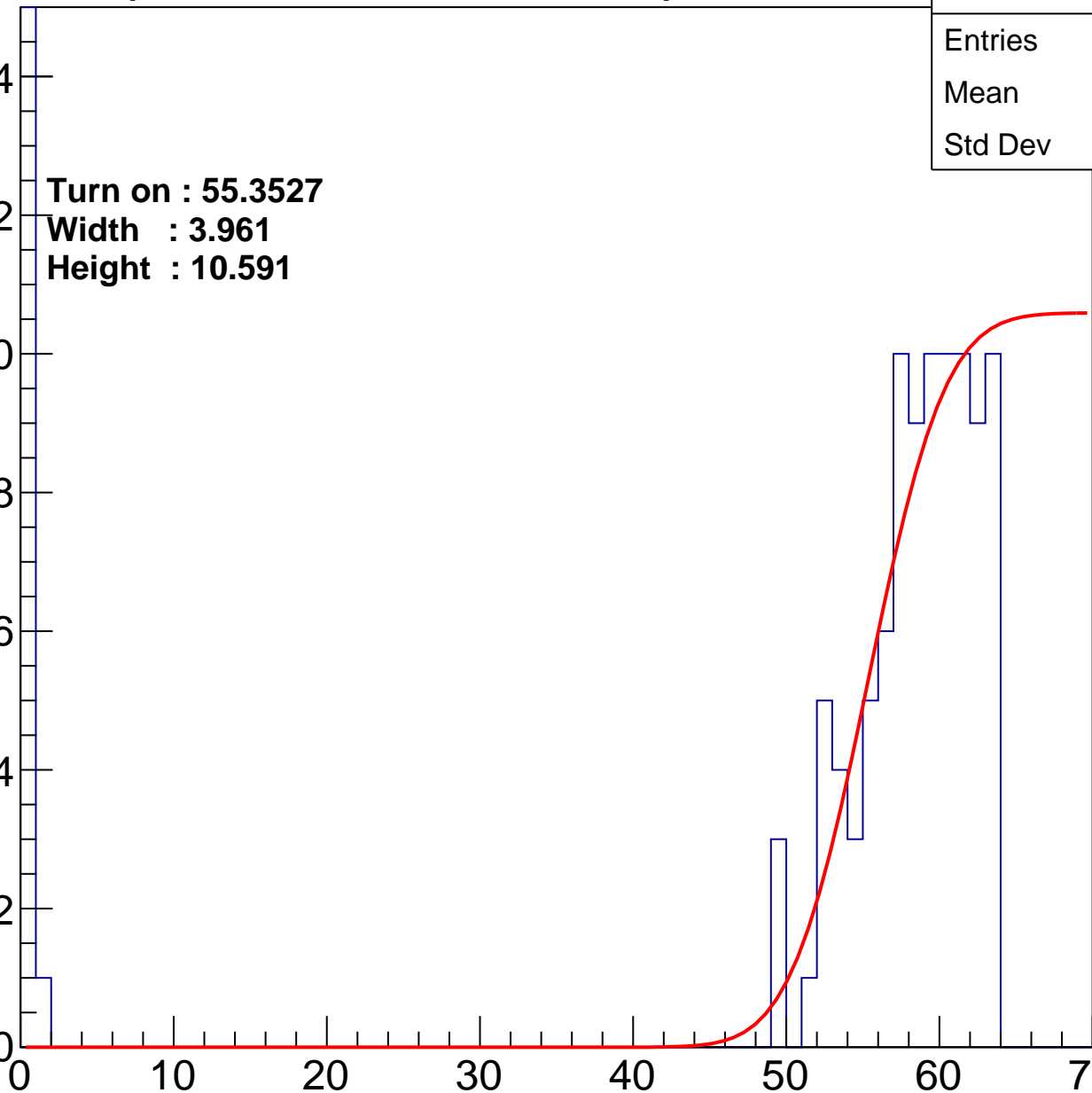
Width : 3.961

Height : 10.591

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch105

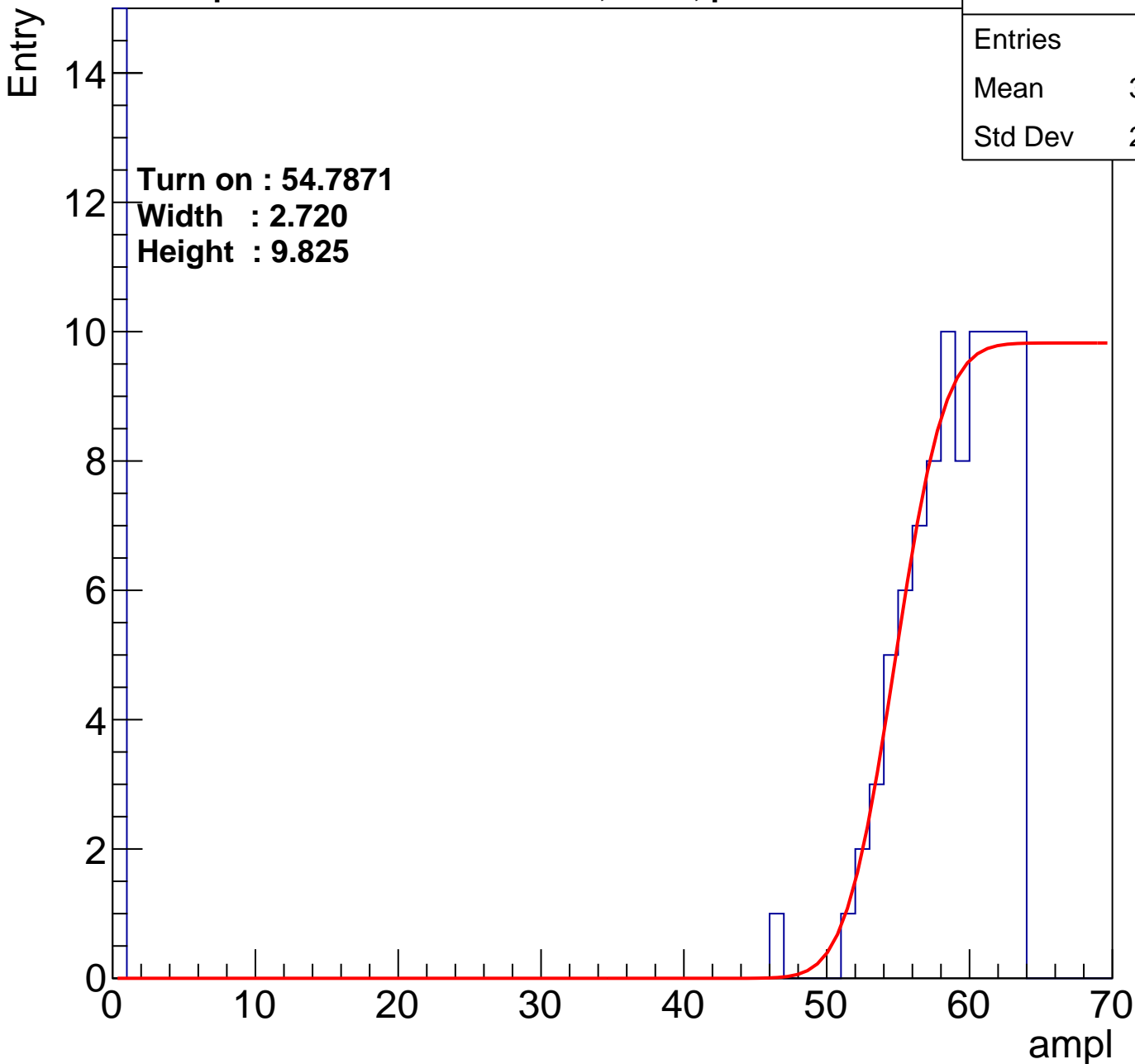
calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	33.46
Std Dev	29.04

Turn on : 54.7871

Width : 2.720

Height : 9.825



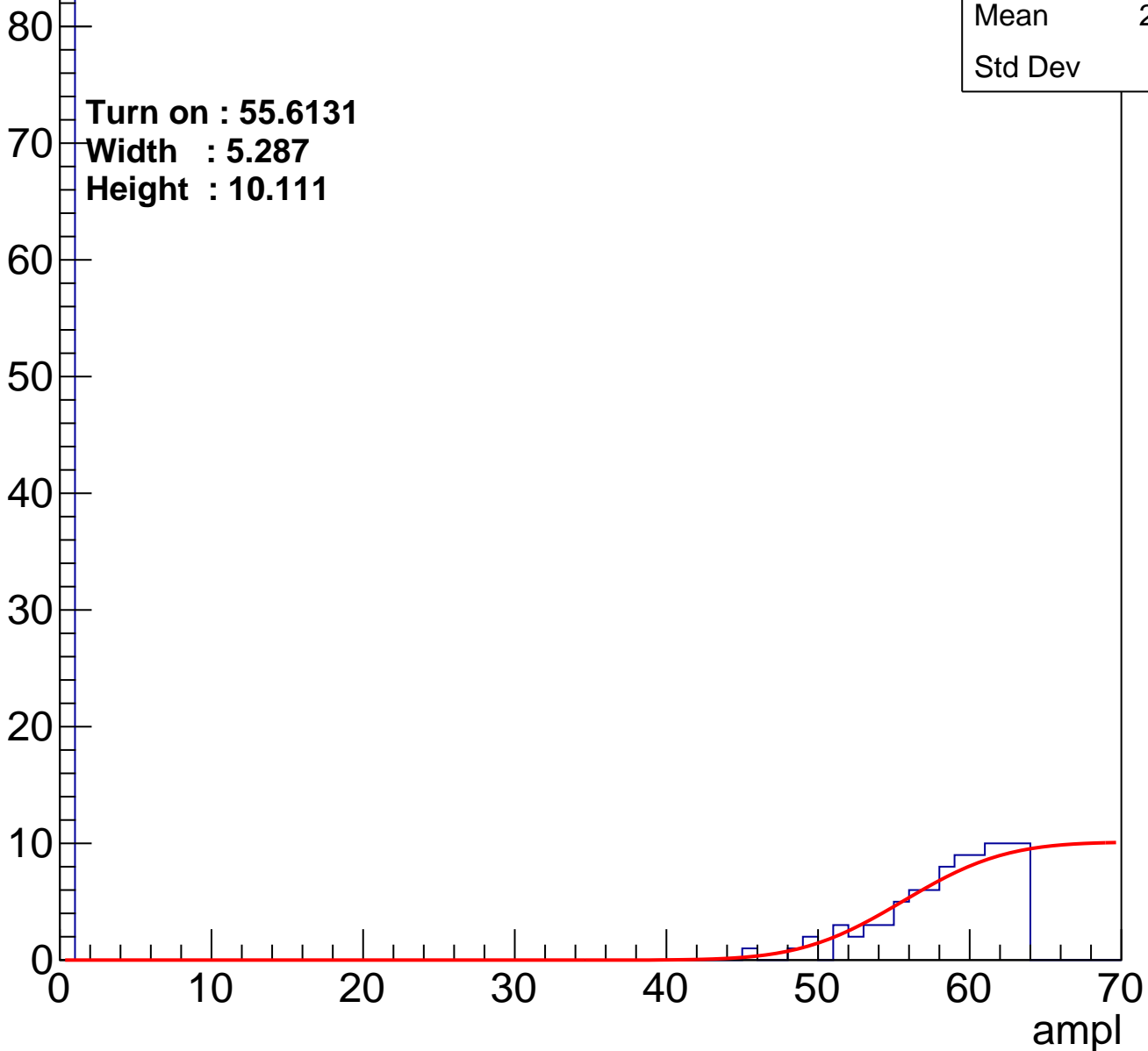
B1L104S, U26-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	29.75
Std Dev	29.2

Turn on : 55.6131
Width : 5.287
Height : 10.111

Entry



B1L104S, U26-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	33.39
Std Dev	28.57

Turn on : 53.4412

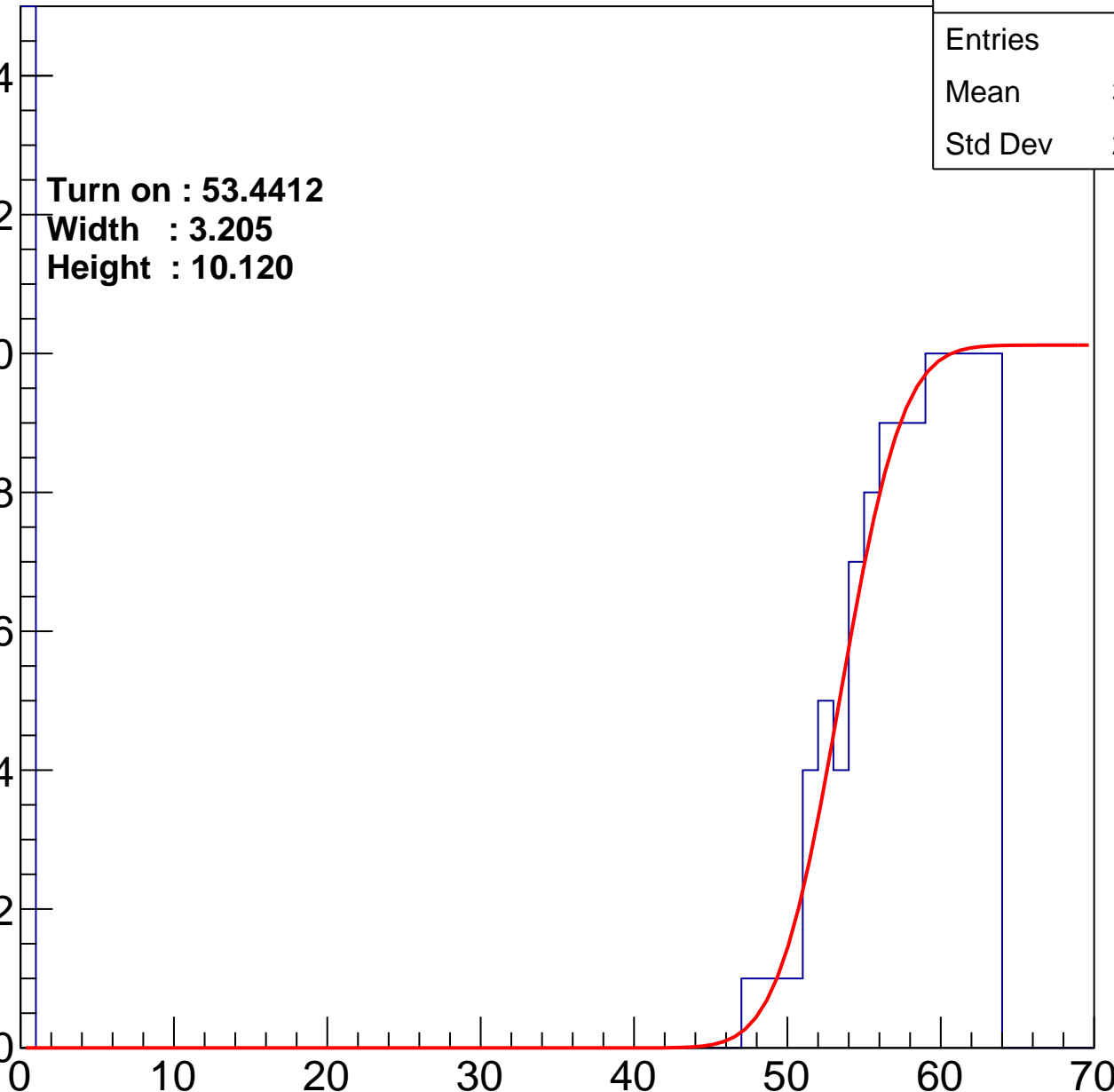
Width : 3.205

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	30.81
Std Dev	29.41

Turn on : 56.0975

Width : 3.366

Height : 10.111

Entry

70

60

50

40

30

20

10

0

0

10

20

30

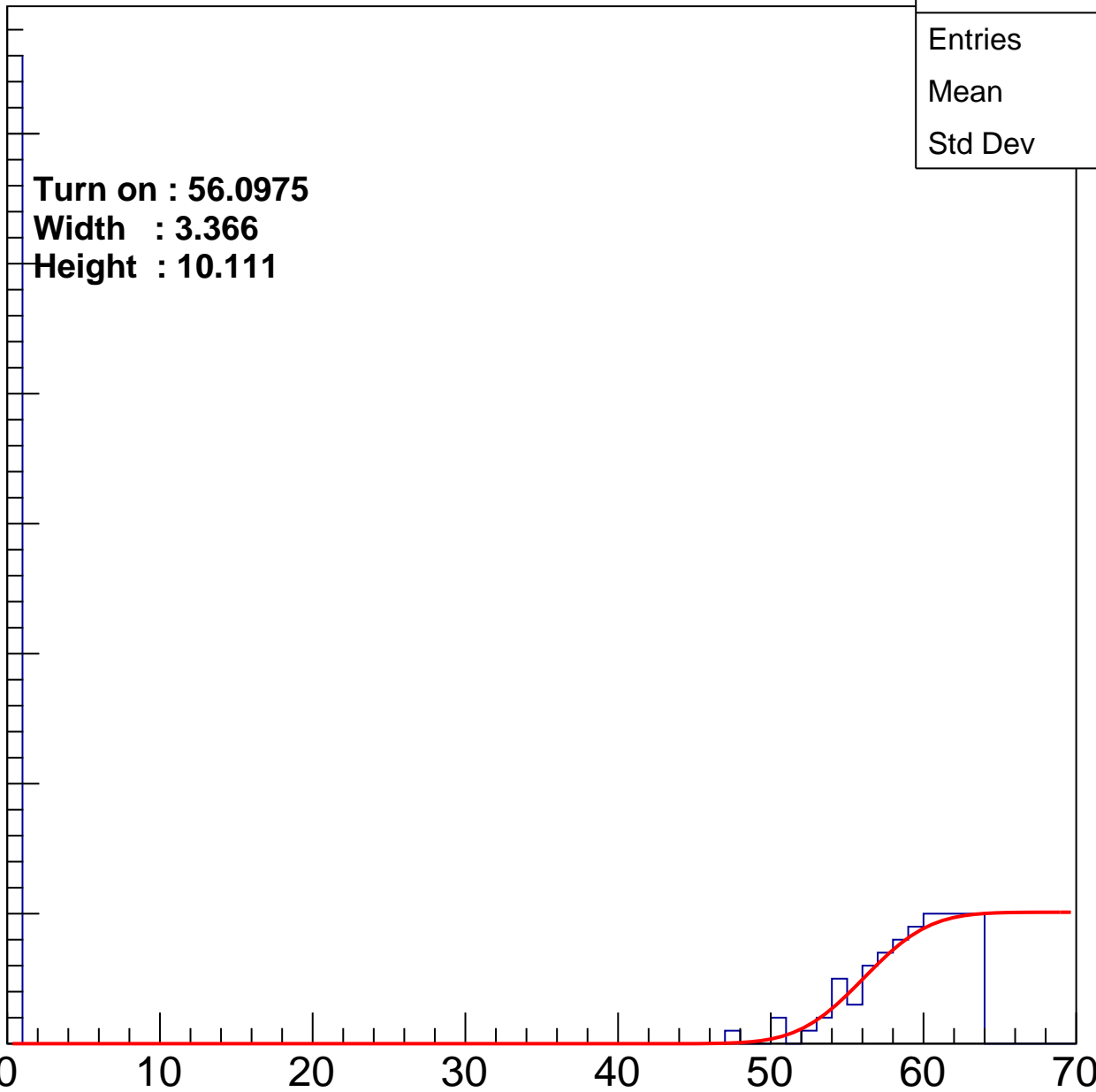
40

50

60

70

ampl



B1L104S, U26-ch109

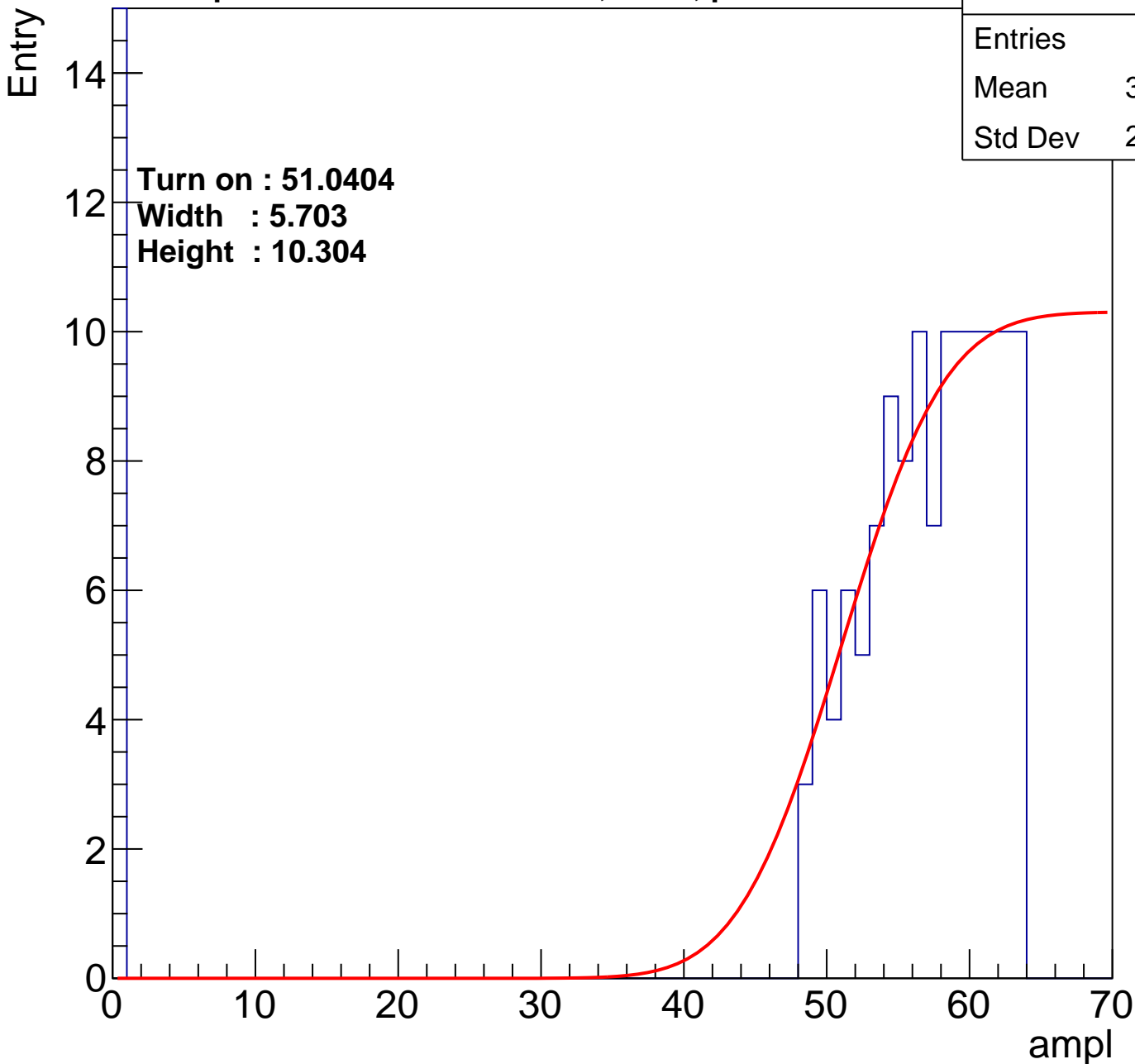
calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	39.17
Std Dev	26.46

Turn on : 51.0404

Width : 5.703

Height : 10.304



B1L104S, U26-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	34.94
Std Dev	28.08

Turn on : 54.3130

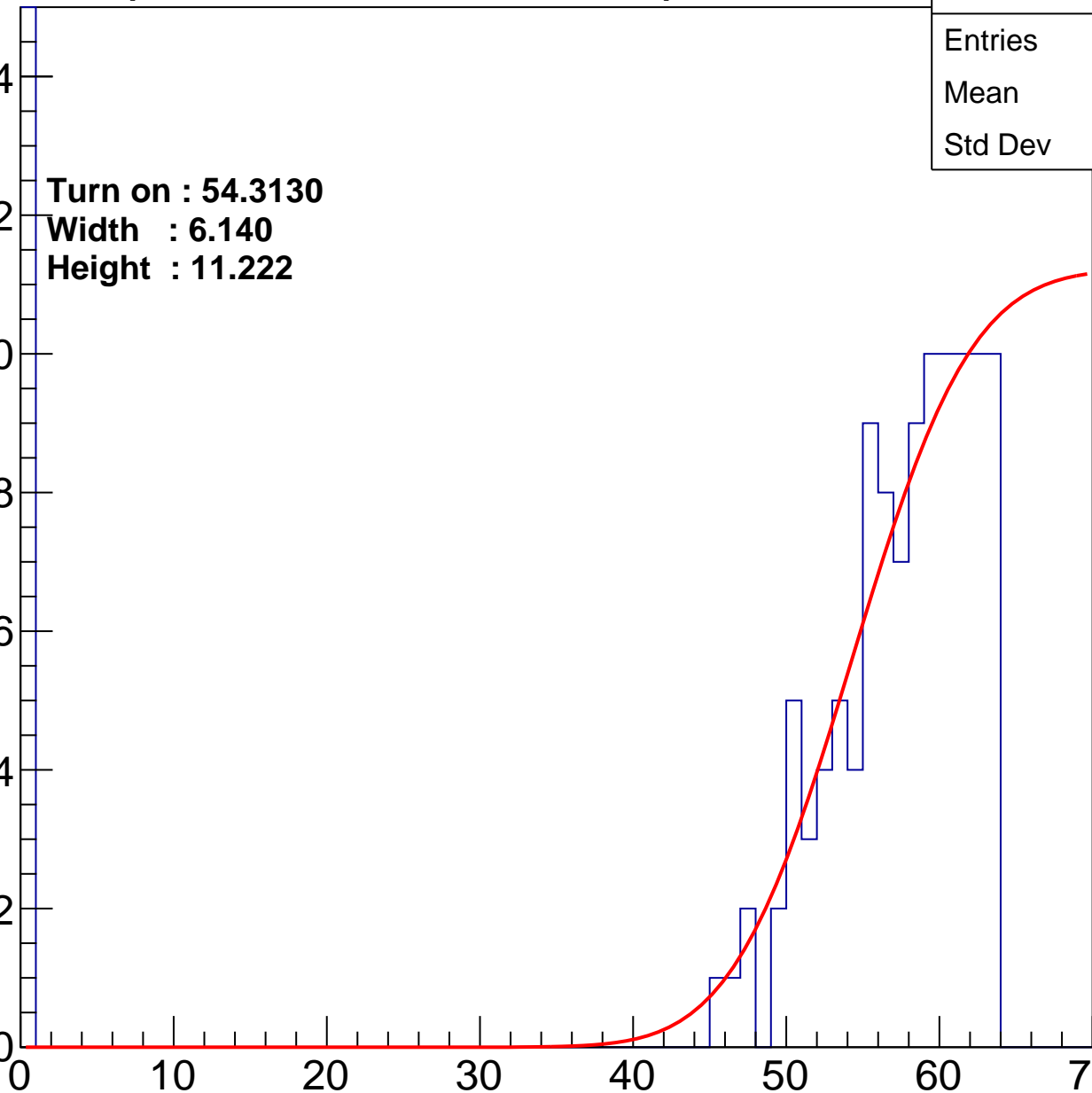
Width : 6.140

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch111

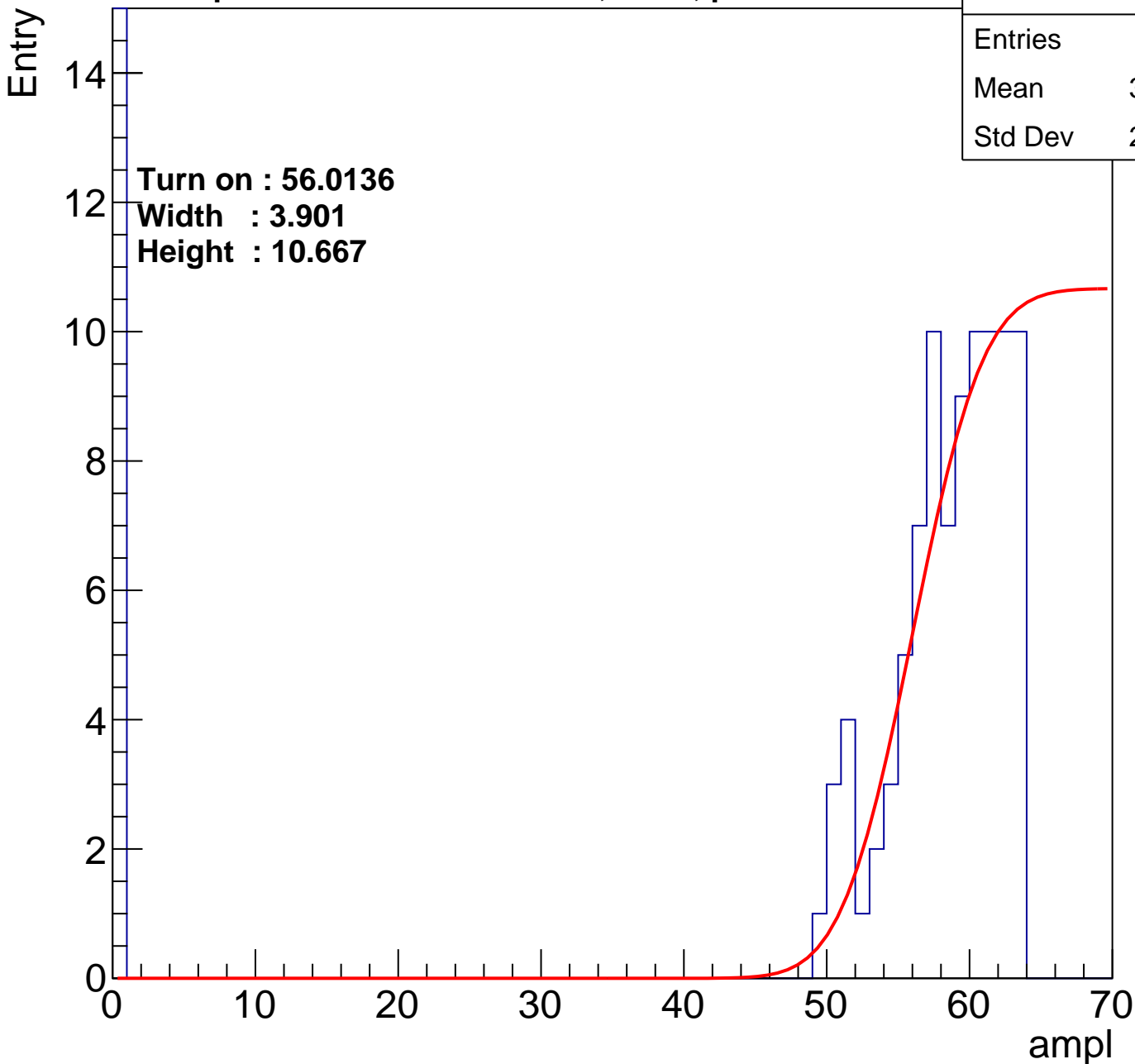
calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	33.69
Std Dev	28.89

Turn on : 56.0136

Width : 3.901

Height : 10.667



B1L104S, U26-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	147
Mean	30.16
Std Dev	29.63

Turn on : 56.1552

Width : 3.583

Height : 9.556

Entry

70

60

50

40

30

20

10

0

0

10

20

30

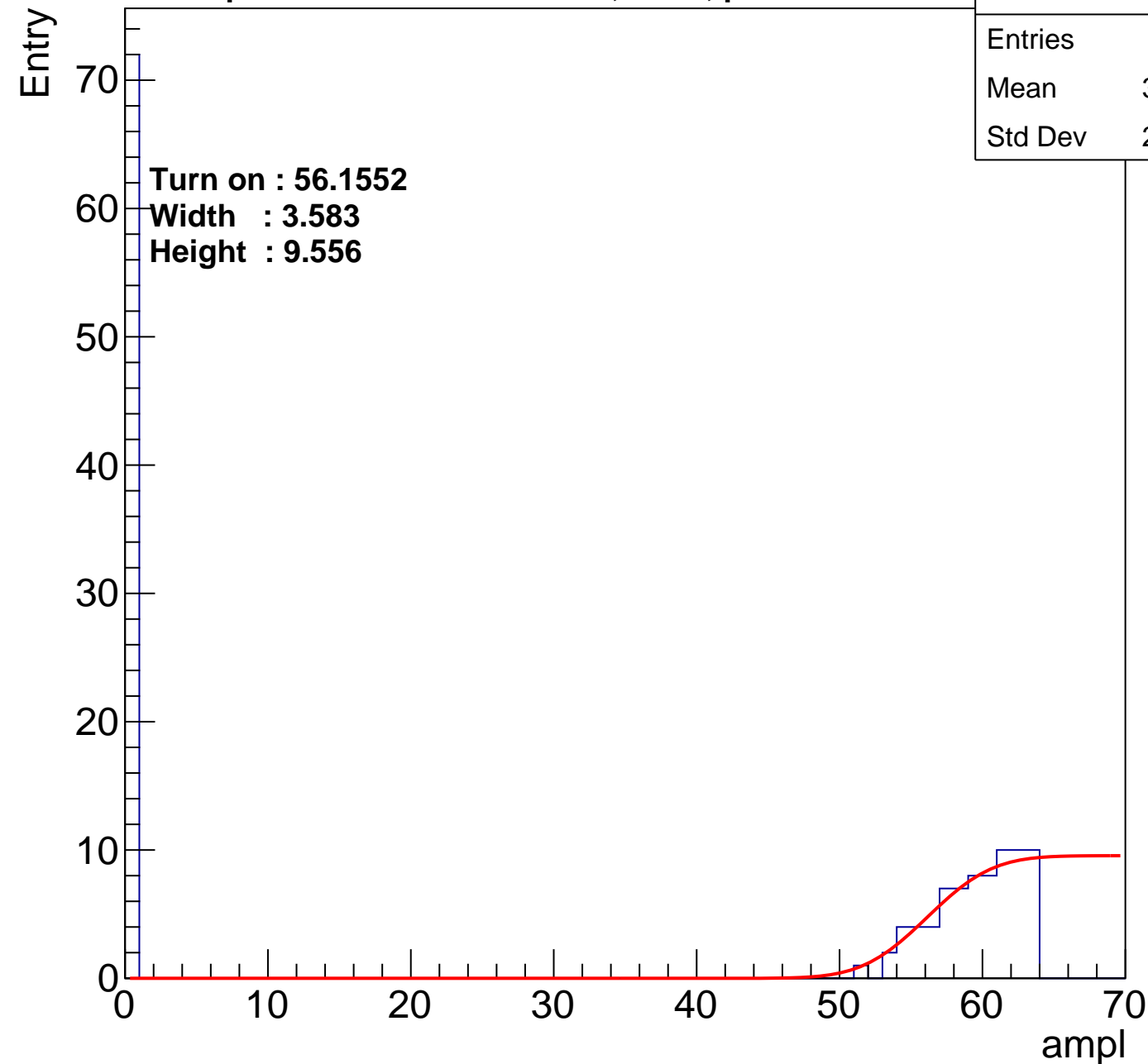
40

50

60

70

ampl



B1L104S, U26-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	149
Mean	33.81
Std Dev	29.05

Turn on : 56.5535

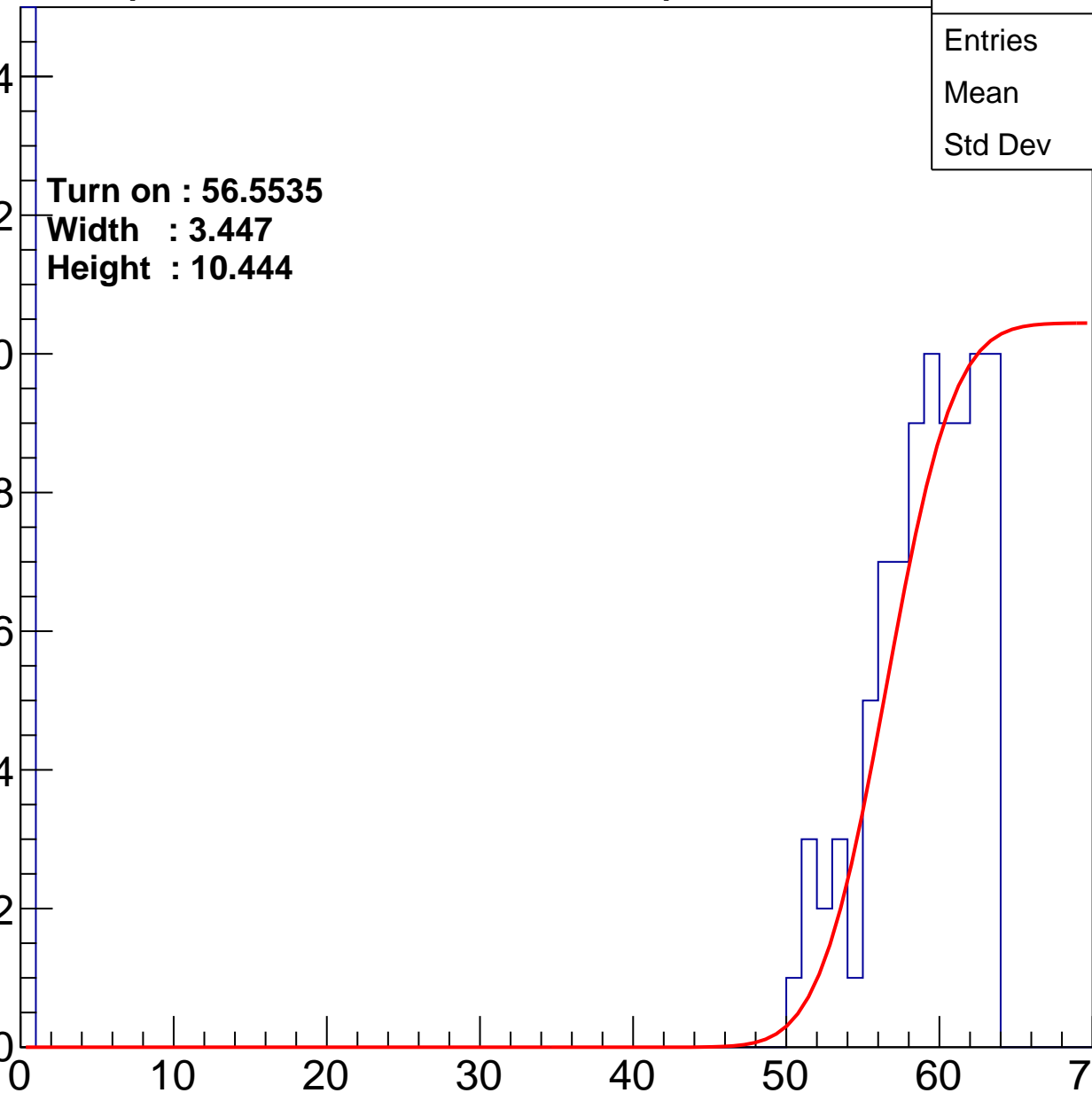
Width : 3.447

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	33.38
Std Dev	28.53

Turn on : 53.9376

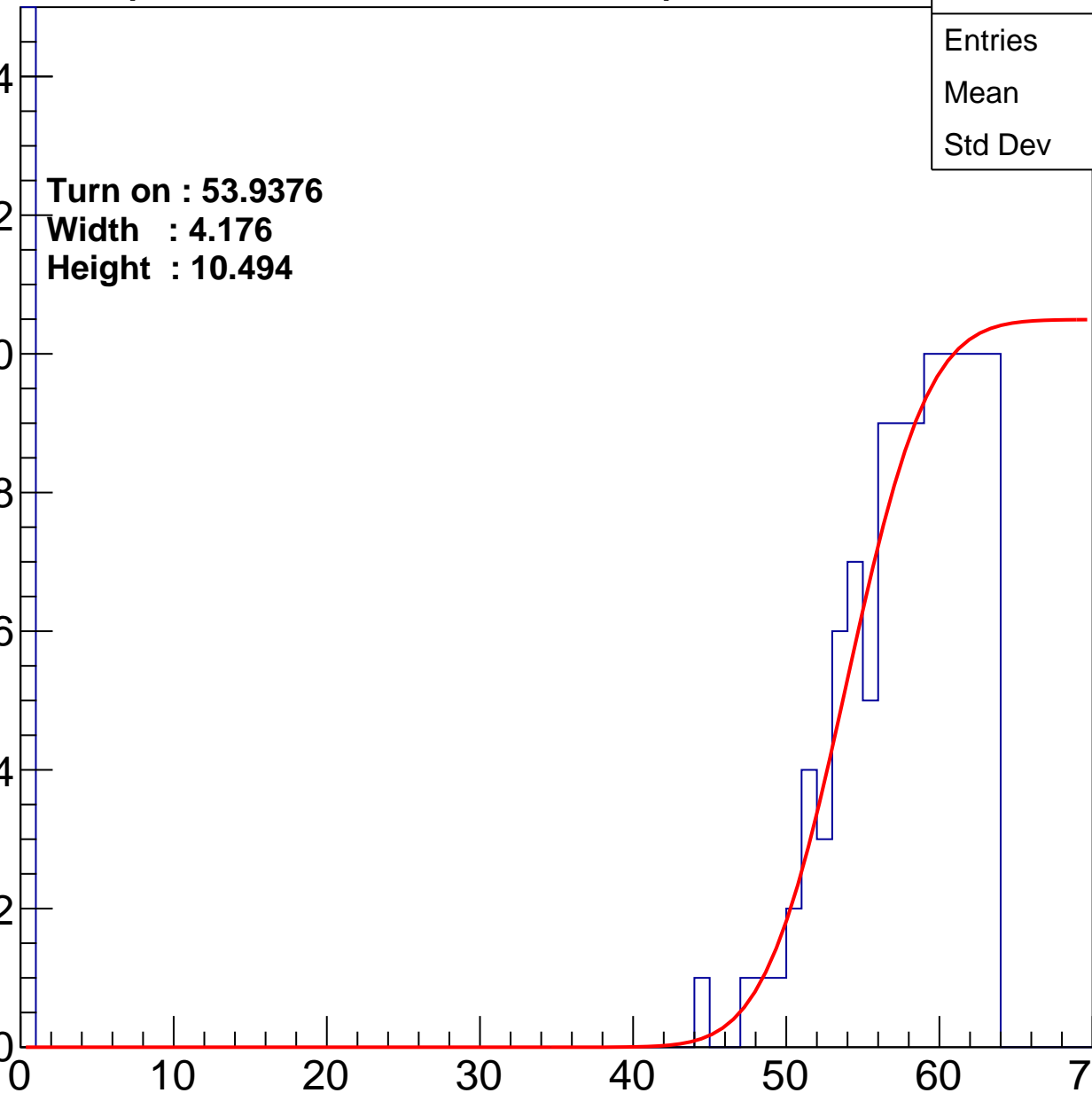
Width : 4.176

Height : 10.494

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch115

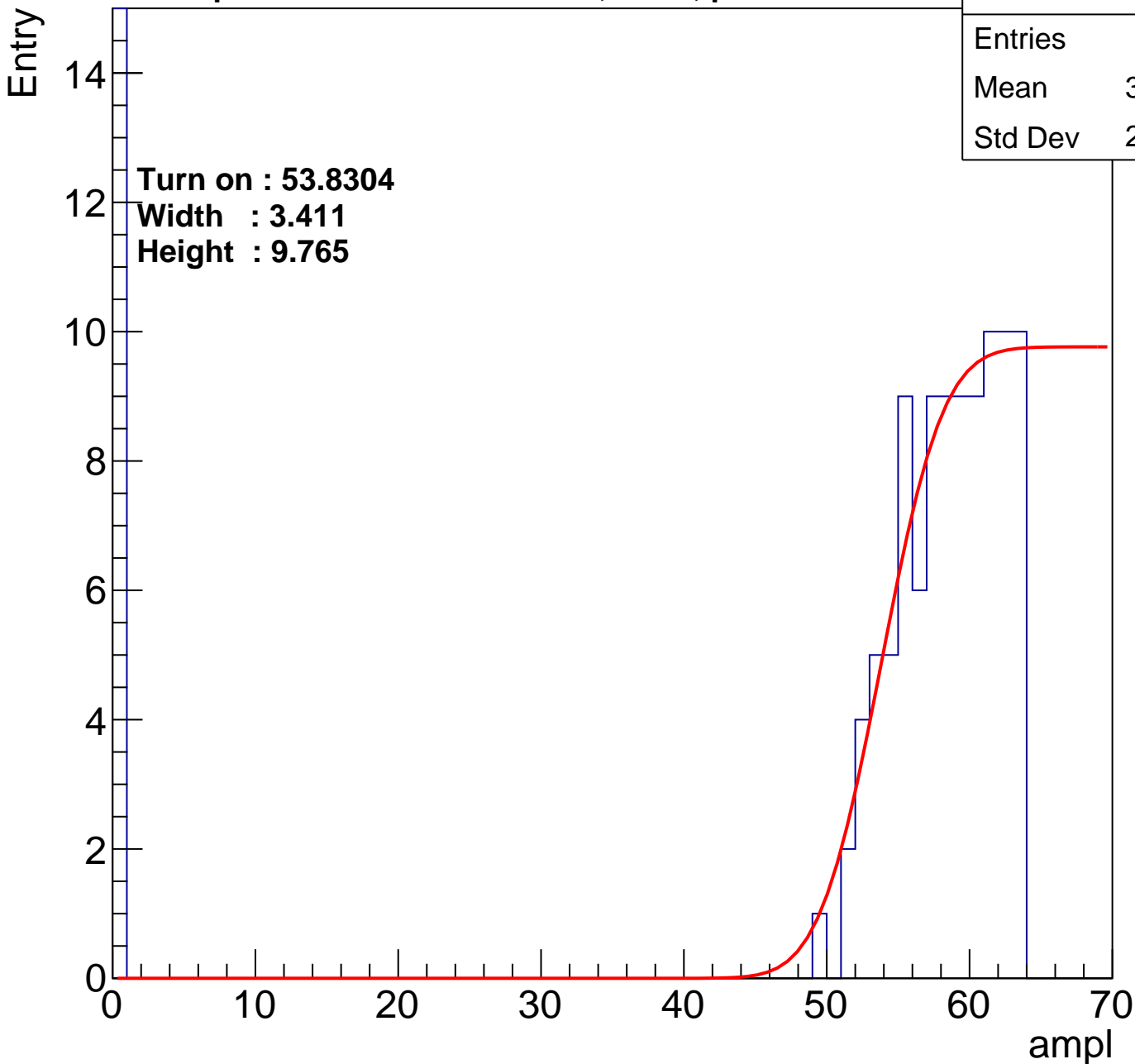
calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	37.69
Std Dev	27.86

Turn on : 53.8304

Width : 3.411

Height : 9.765



B1L104S, U26-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	143
Mean	34.01
Std Dev	29.05

Turn on : 56.7642

Width : 1.544

Height : 9.910

Entry

60

50

40

30

20

10

0

0

10

20

30

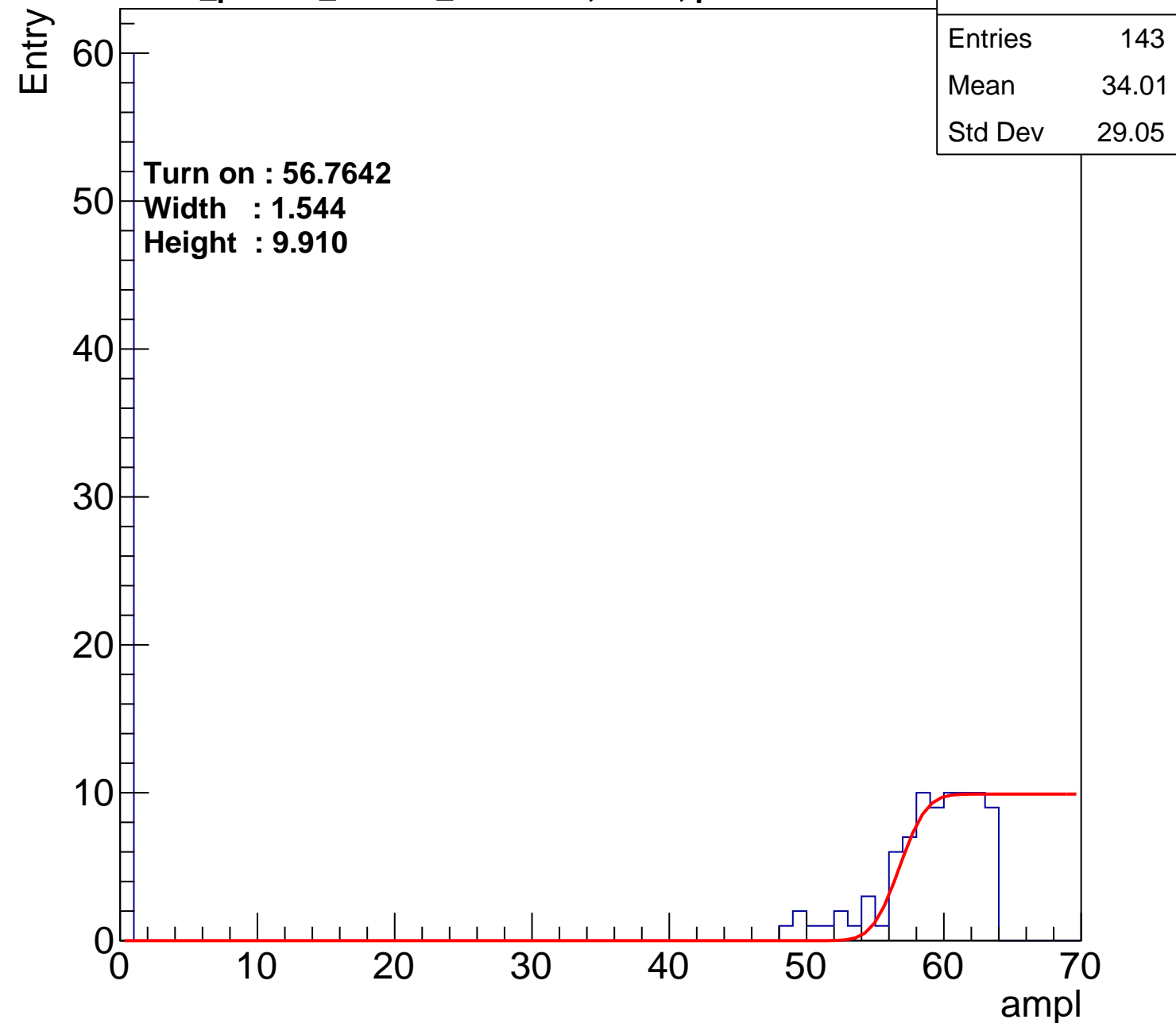
40

50

60

70

ampl



B1L104S, U26-ch117

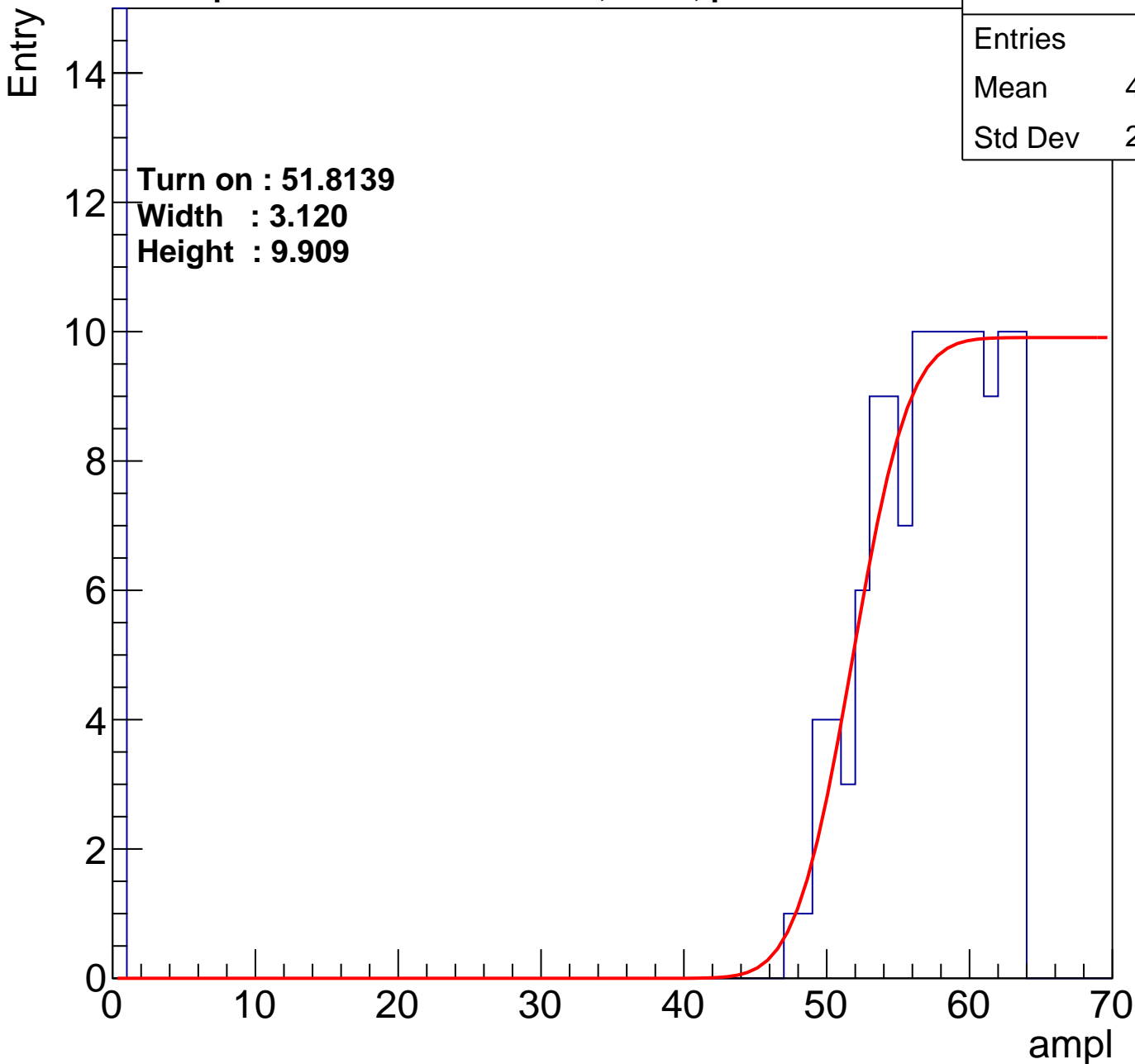
calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	40.95
Std Dev	25.82

Turn on : 51.8139

Width : 3.120

Height : 9.909



B1L104S, U26-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	32.74
Std Dev	29.03

Turn on : 54.1688

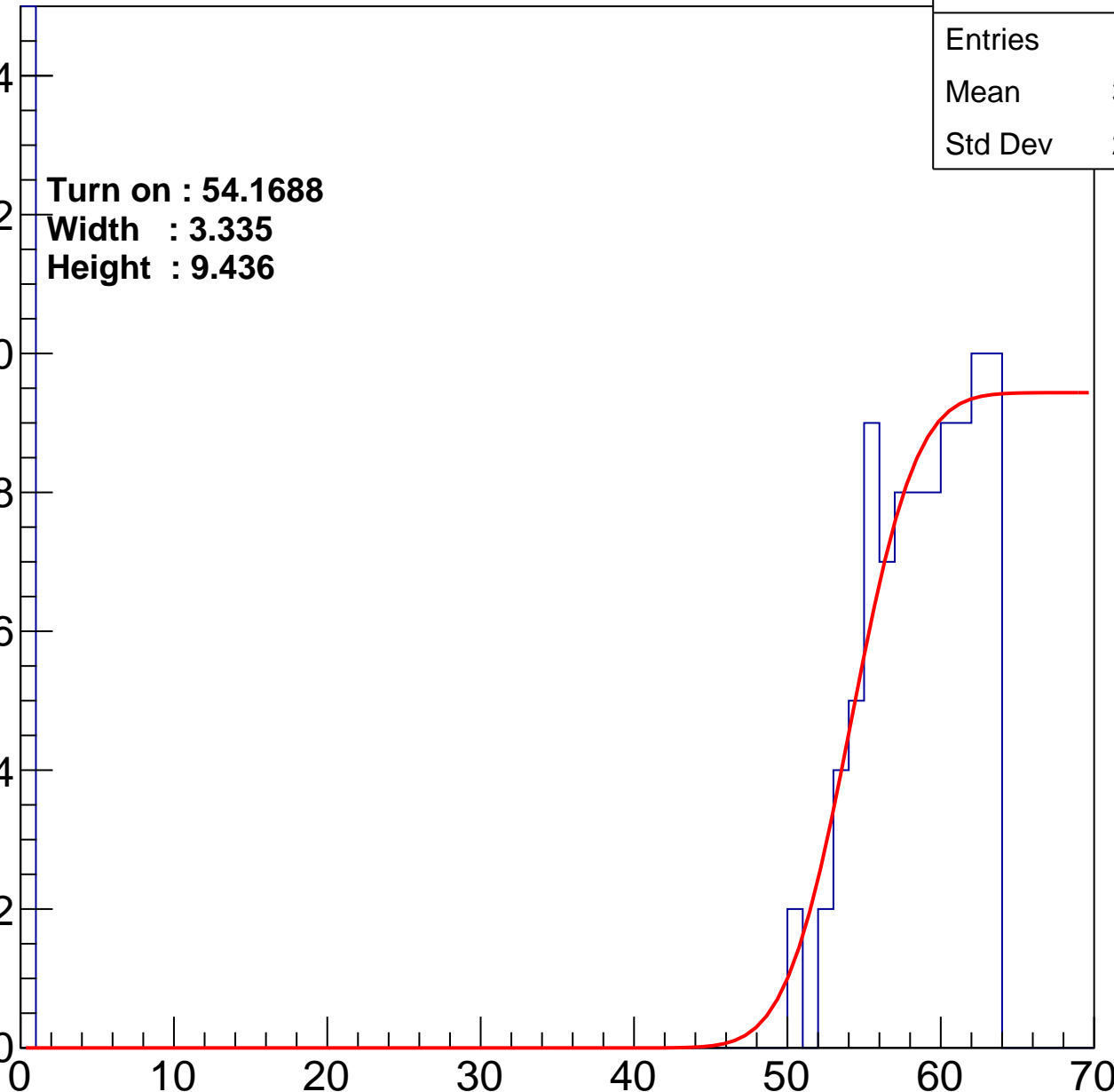
Width : 3.335

Height : 9.436

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	26.6
Std Dev	29.15

Turn on : 55.1931

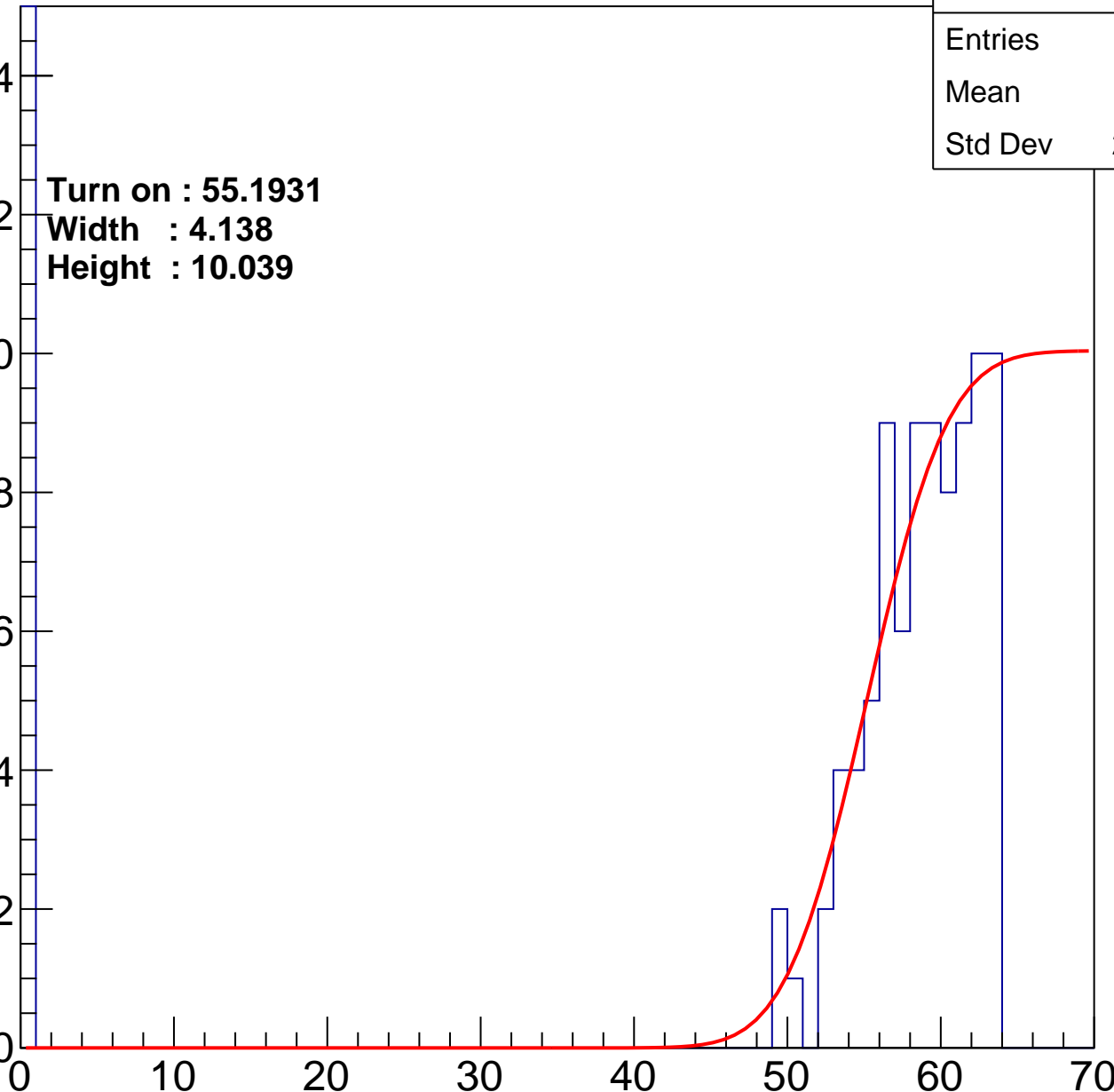
Width : 4.138

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	223
Mean	29.9
Std Dev	27.93

Turn on : 53.6599

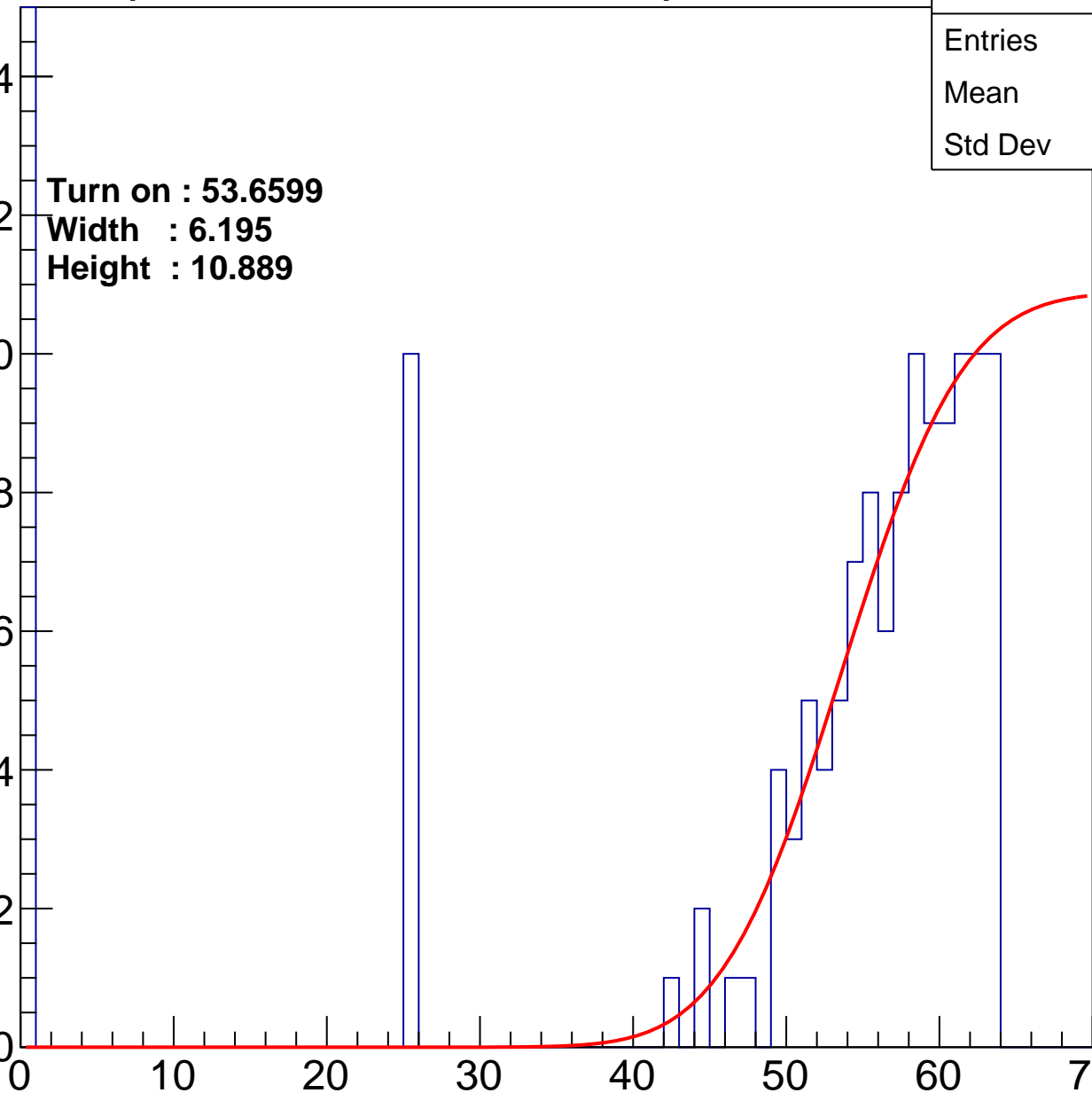
Width : 6.195

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch121

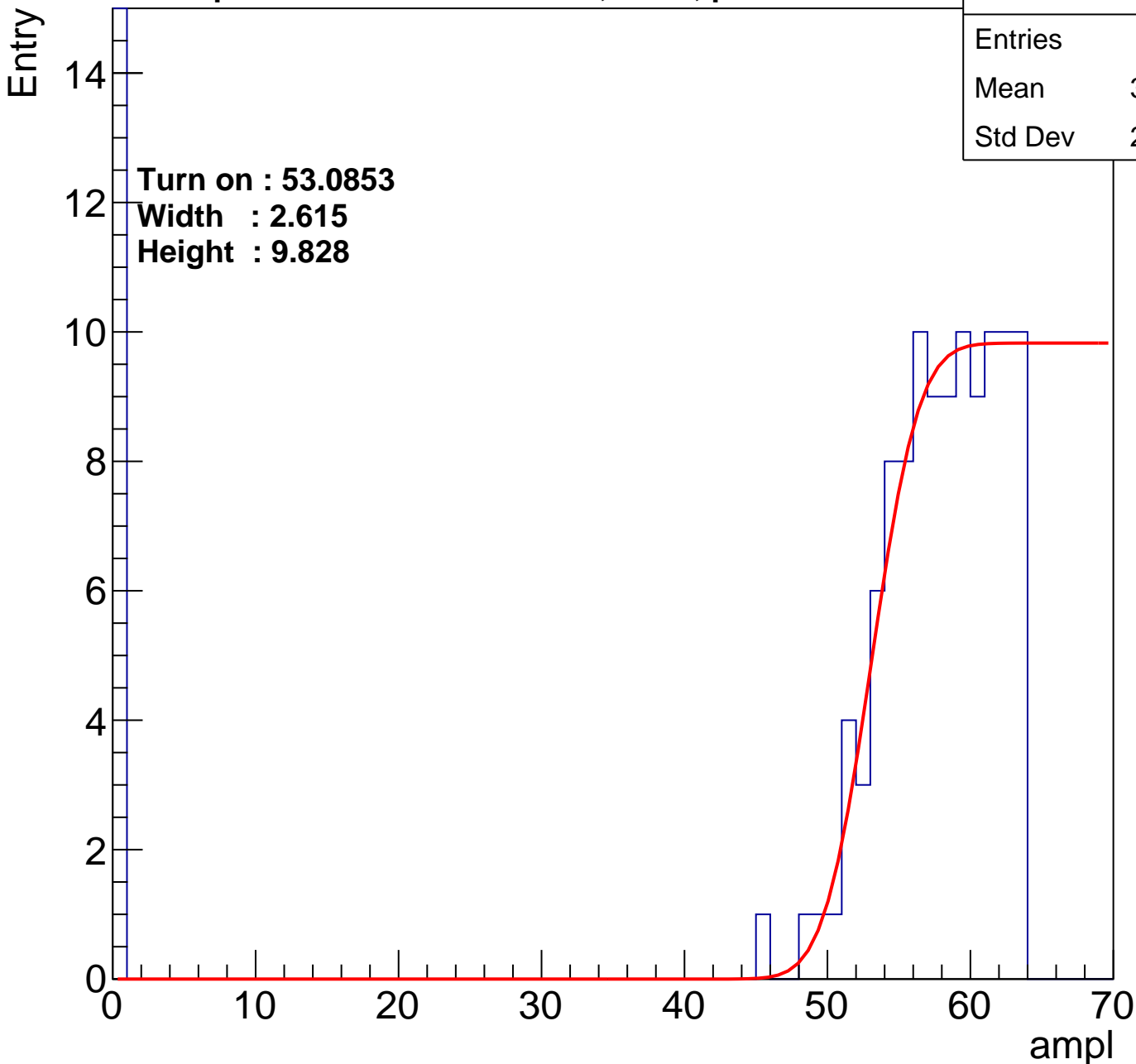
calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	39.54
Std Dev	26.85

Turn on : 53.0853

Width : 2.615

Height : 9.828



B1L104S, U26-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	34.62
Std Dev	28.39

Turn on : 55.4678

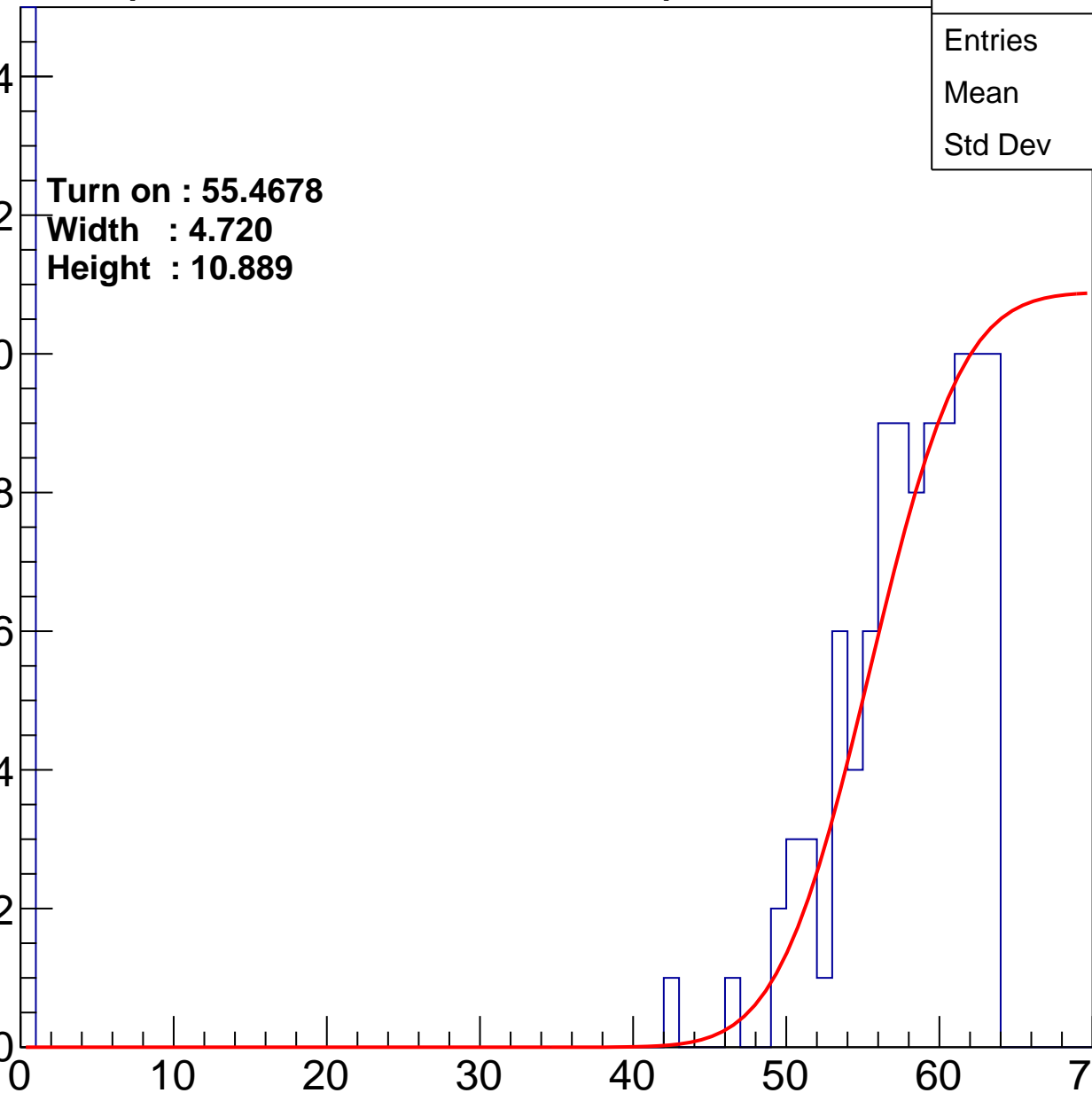
Width : 4.720

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch123

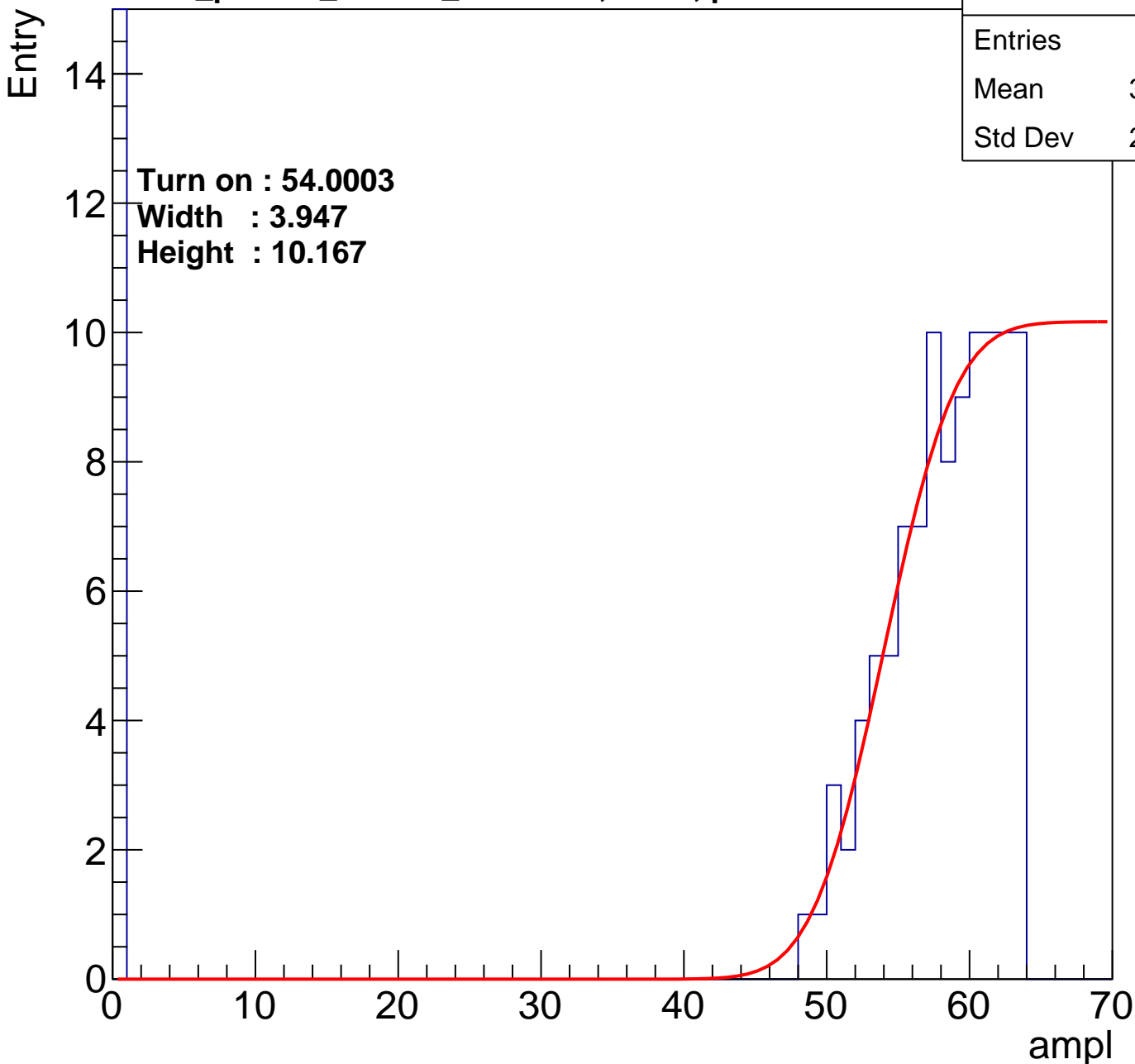
calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	37.07
Std Dev	27.88

Turn on : 54.0003

Width : 3.947

Height : 10.167



B1L104S, U26-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	32.11
Std Dev	28.74

Turn on : 53.7820

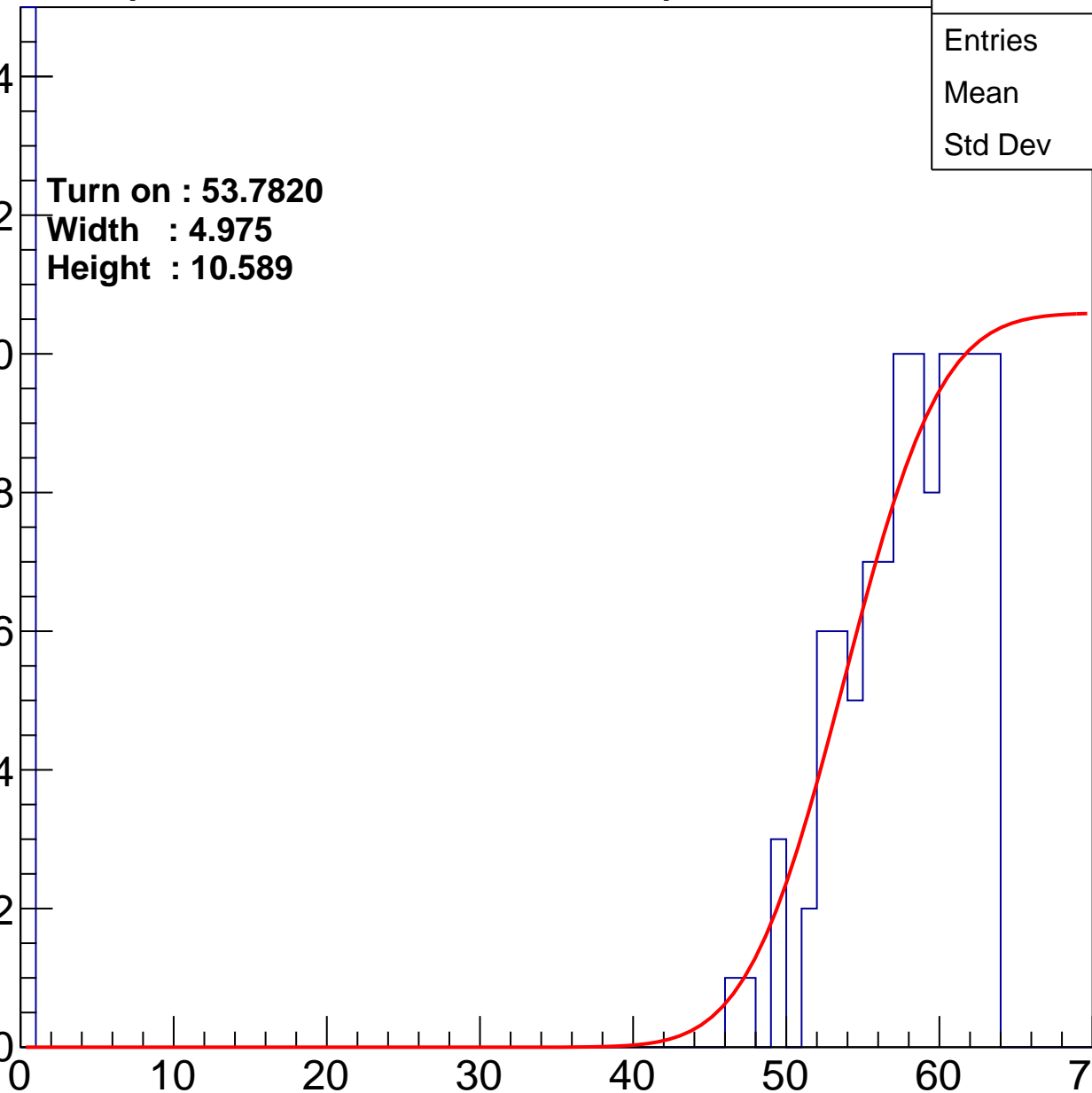
Width : 4.975

Height : 10.589

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	35.54
Std Dev	28.07

Turn on : 54.7100

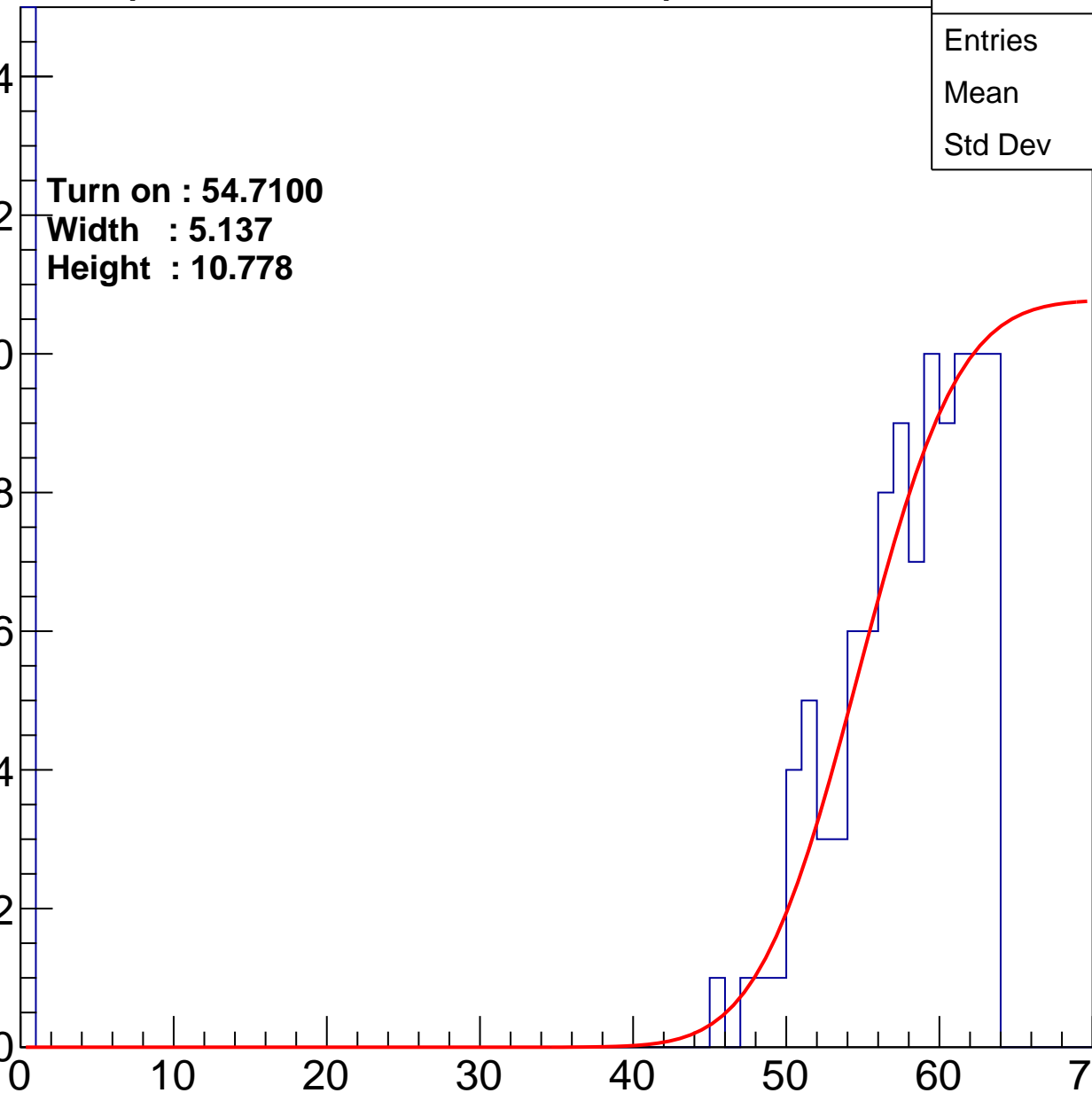
Width : 5.137

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



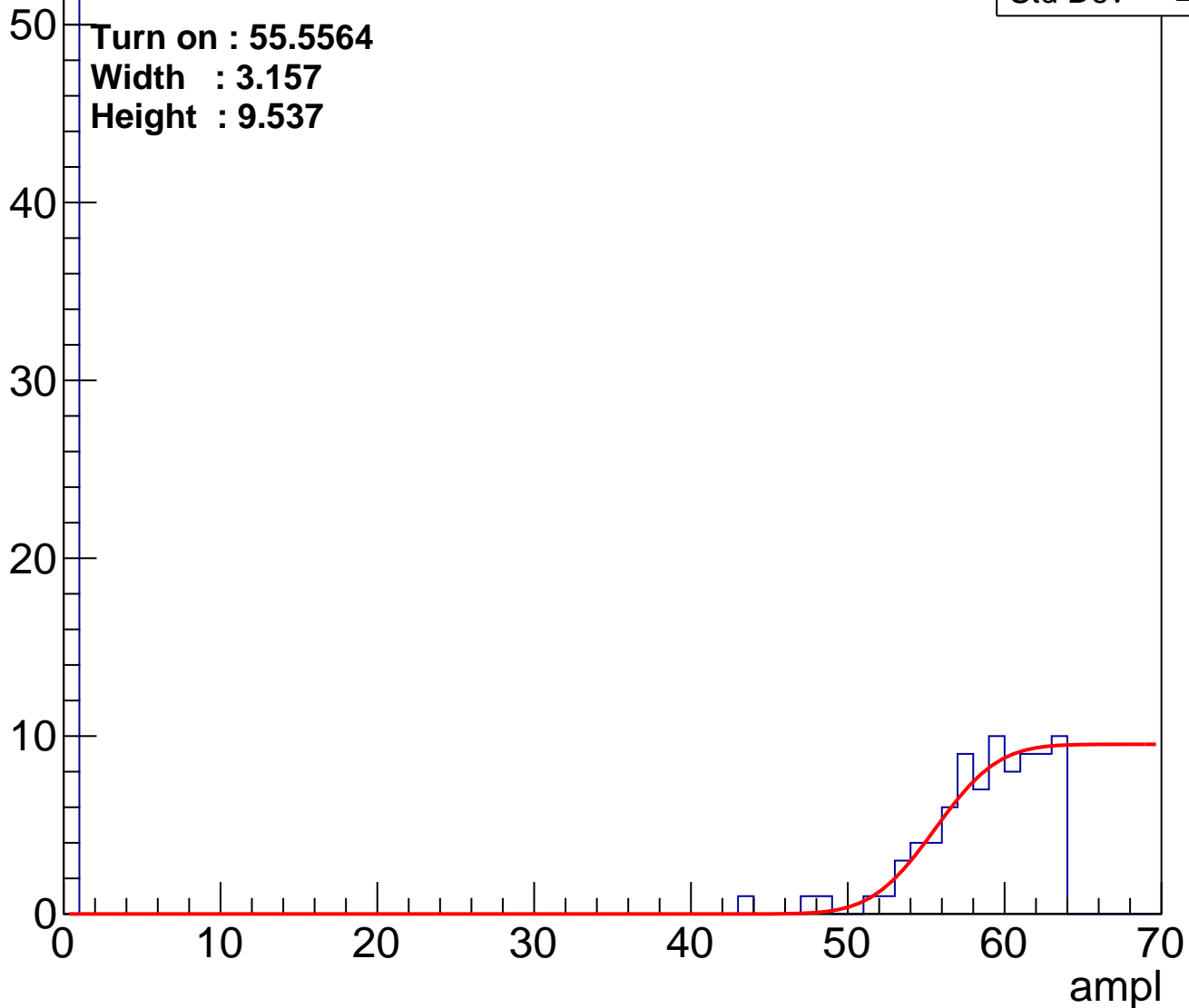
B1L104S, U26-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	141
Mean	34.74
Std Dev	28.77

Turn on : 55.5564
Width : 3.157
Height : 9.537

Entry



B1L104S, U26-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	34.03
Std Dev	28.45

Turn on : 54.9681

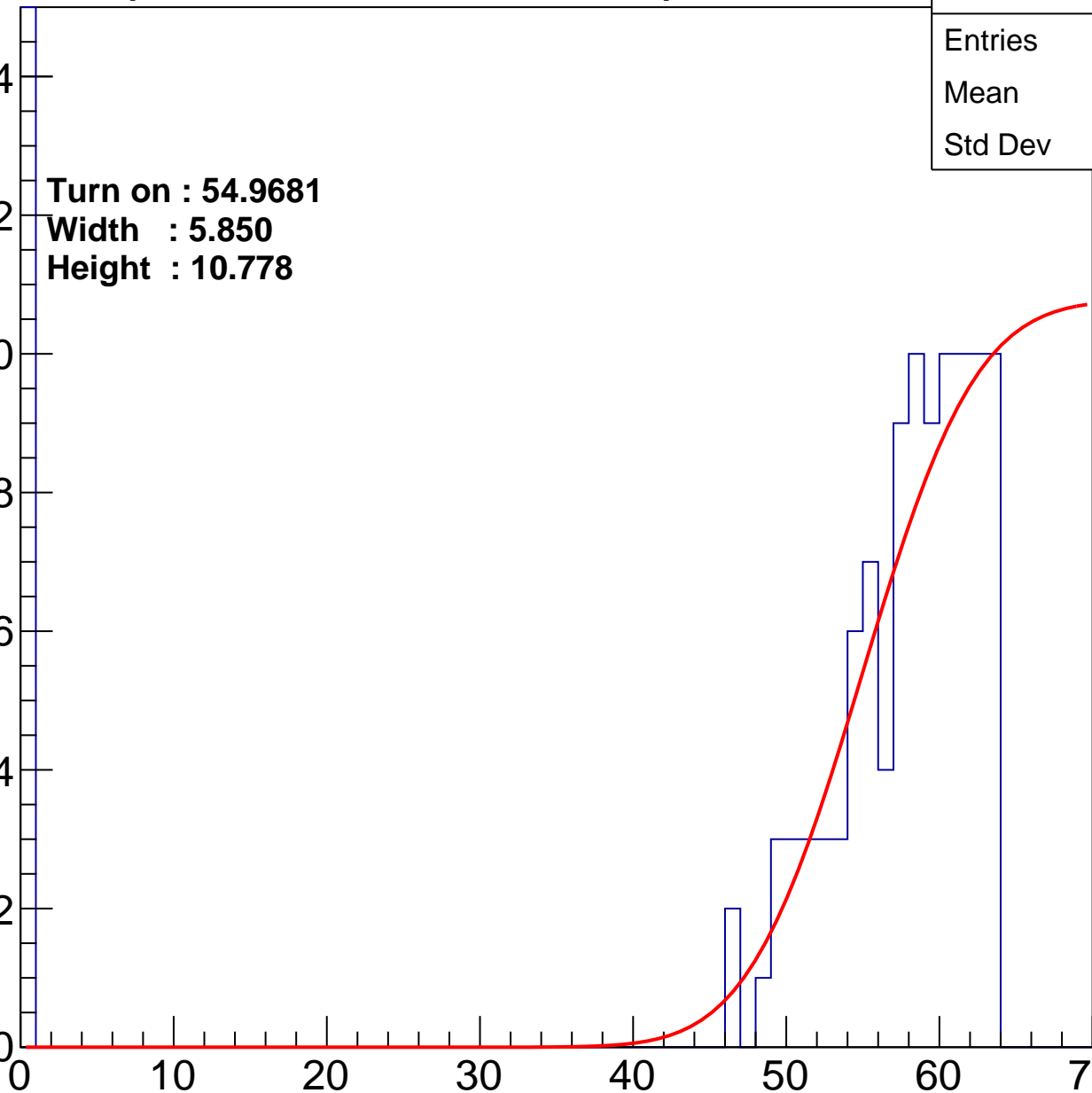
Width : 5.850

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U26-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	34.03
Std Dev	28.45

Turn on : 54.9681

Width : 5.850

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl

