

B1L103S, U2-ch0, adc0

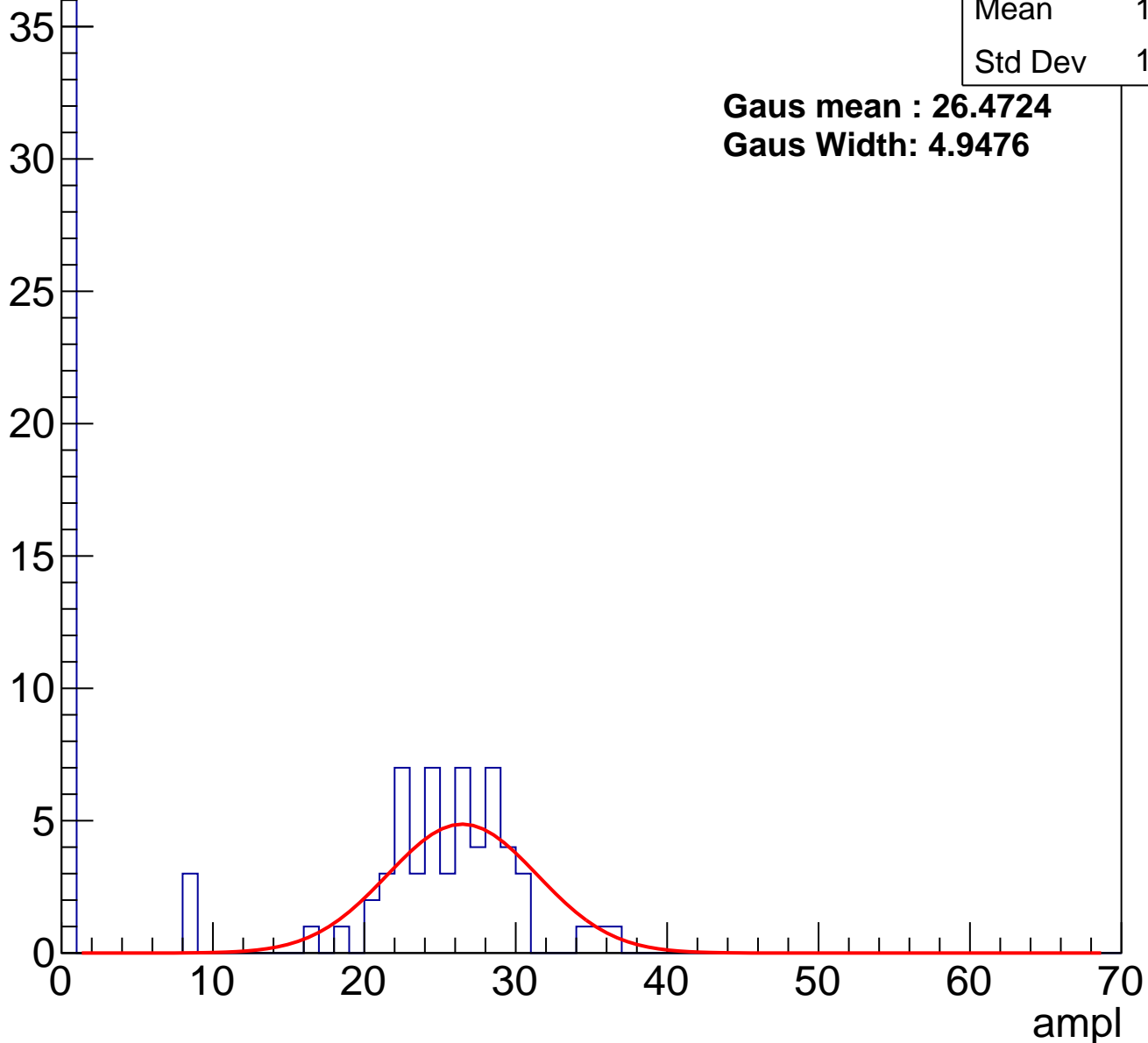
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	14.99
Std Dev	12.69

Gaus mean : 26.4724

Gaus Width: 4.9476

Entry



B1L103S, U2-ch0, adc1

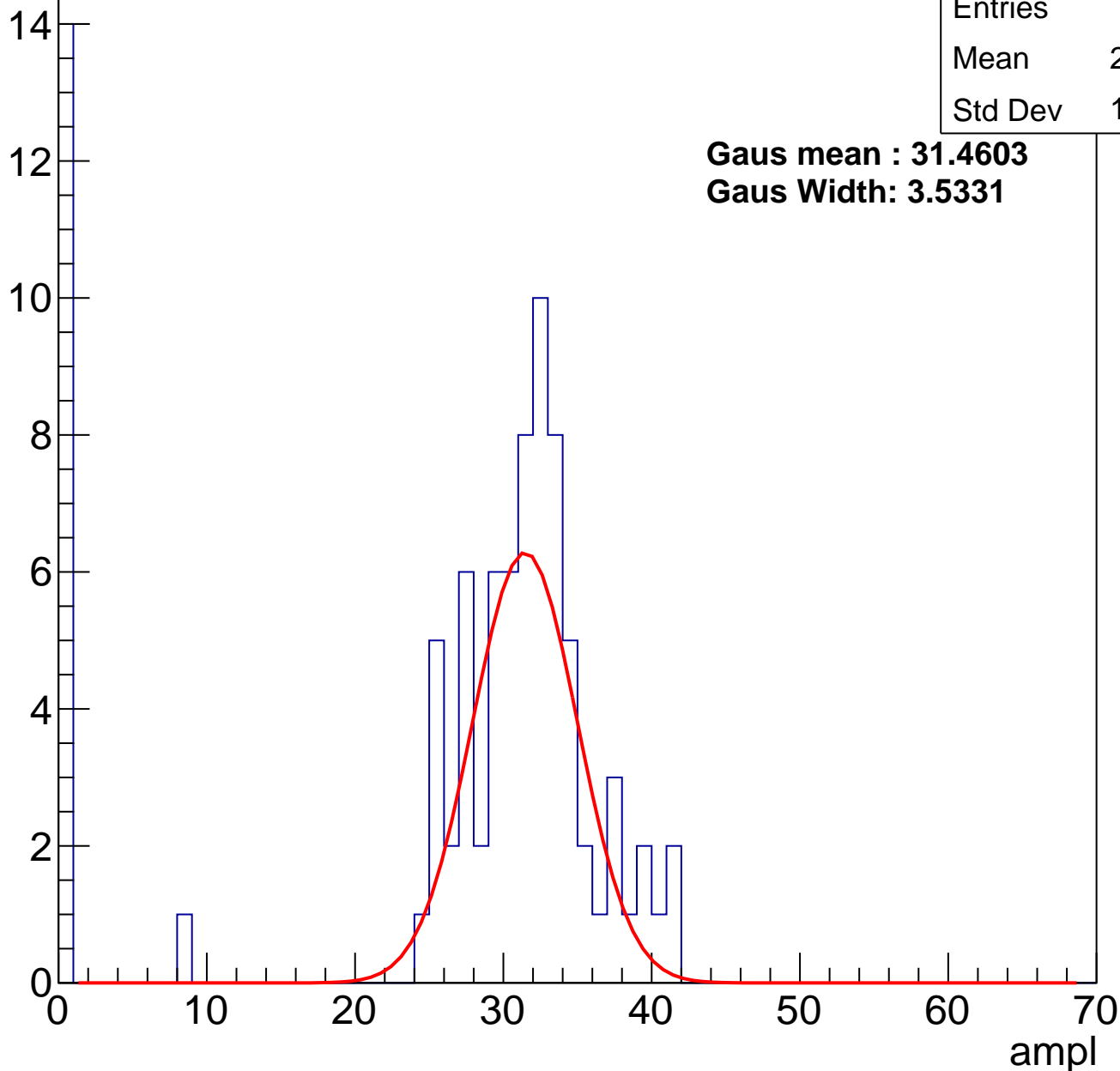
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	26.02
Std Dev	12.29

Gaus mean : 31.4603

Gaus Width: 3.5331

Entry



B1L103S, U2-ch0, adc2

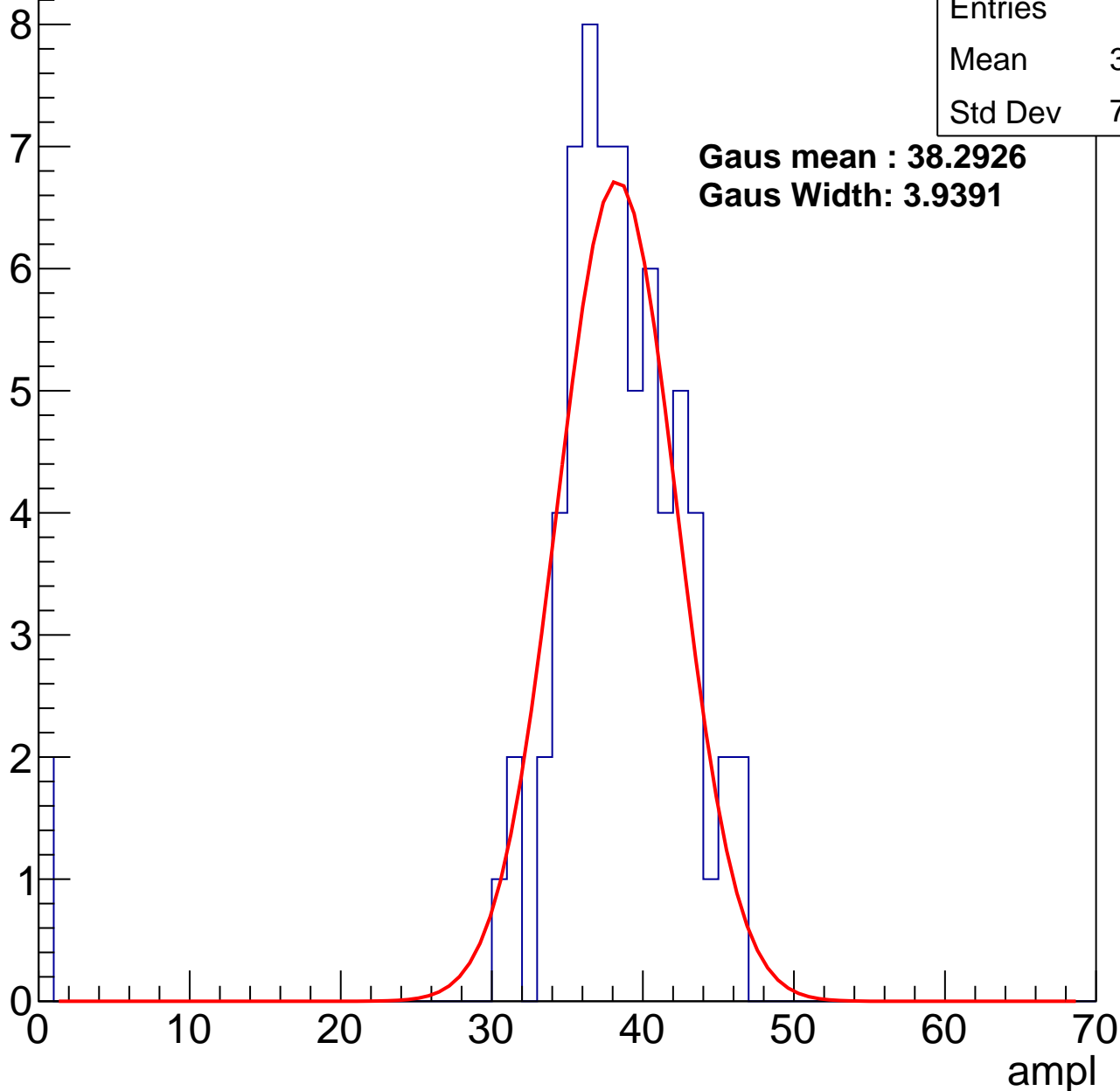
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	37.09
Std Dev	7.338

Gaus mean : 38.2926

Gaus Width: 3.9391

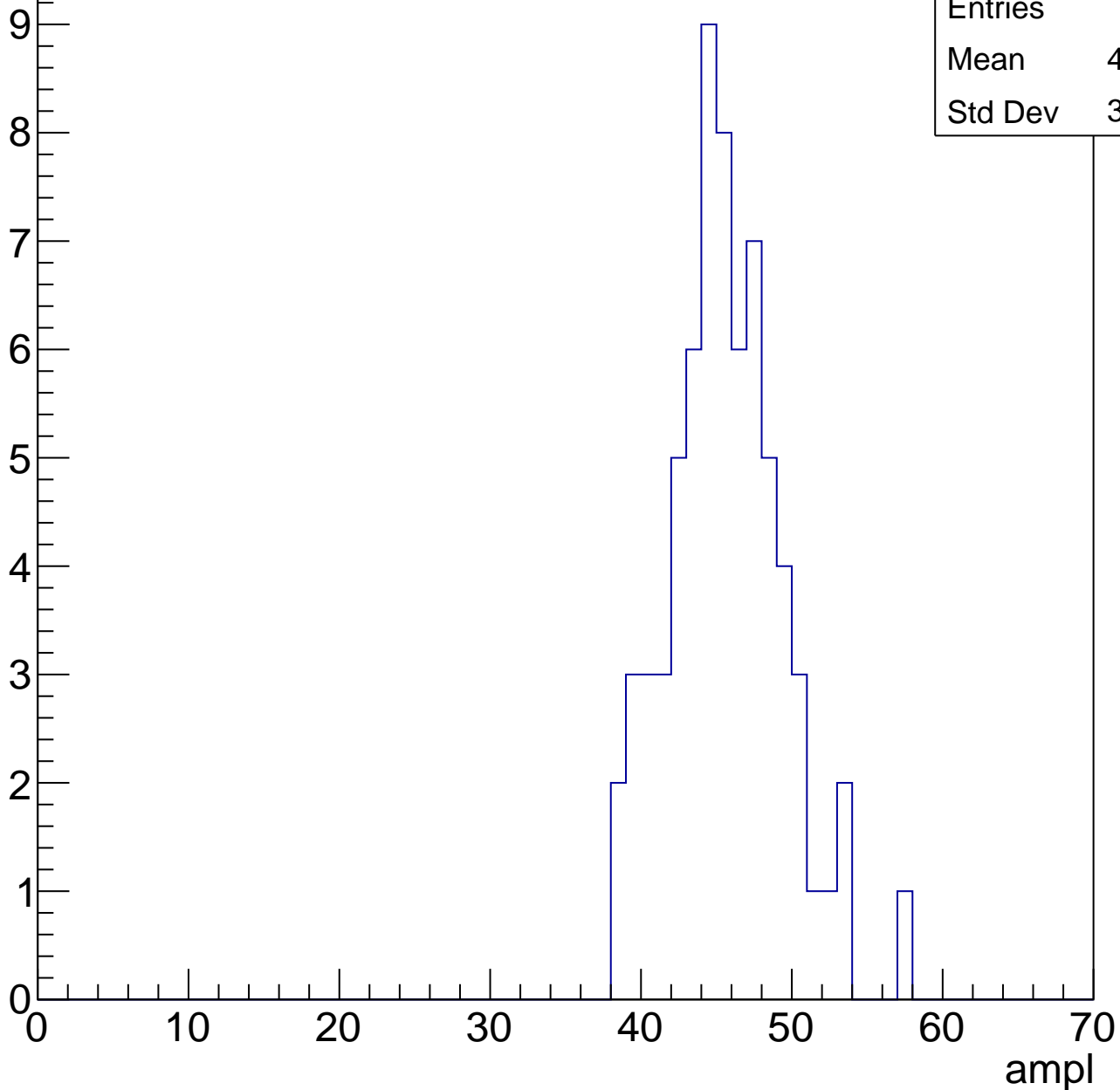


B1L103S, U2-ch0, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	45.17
Std Dev	3.769

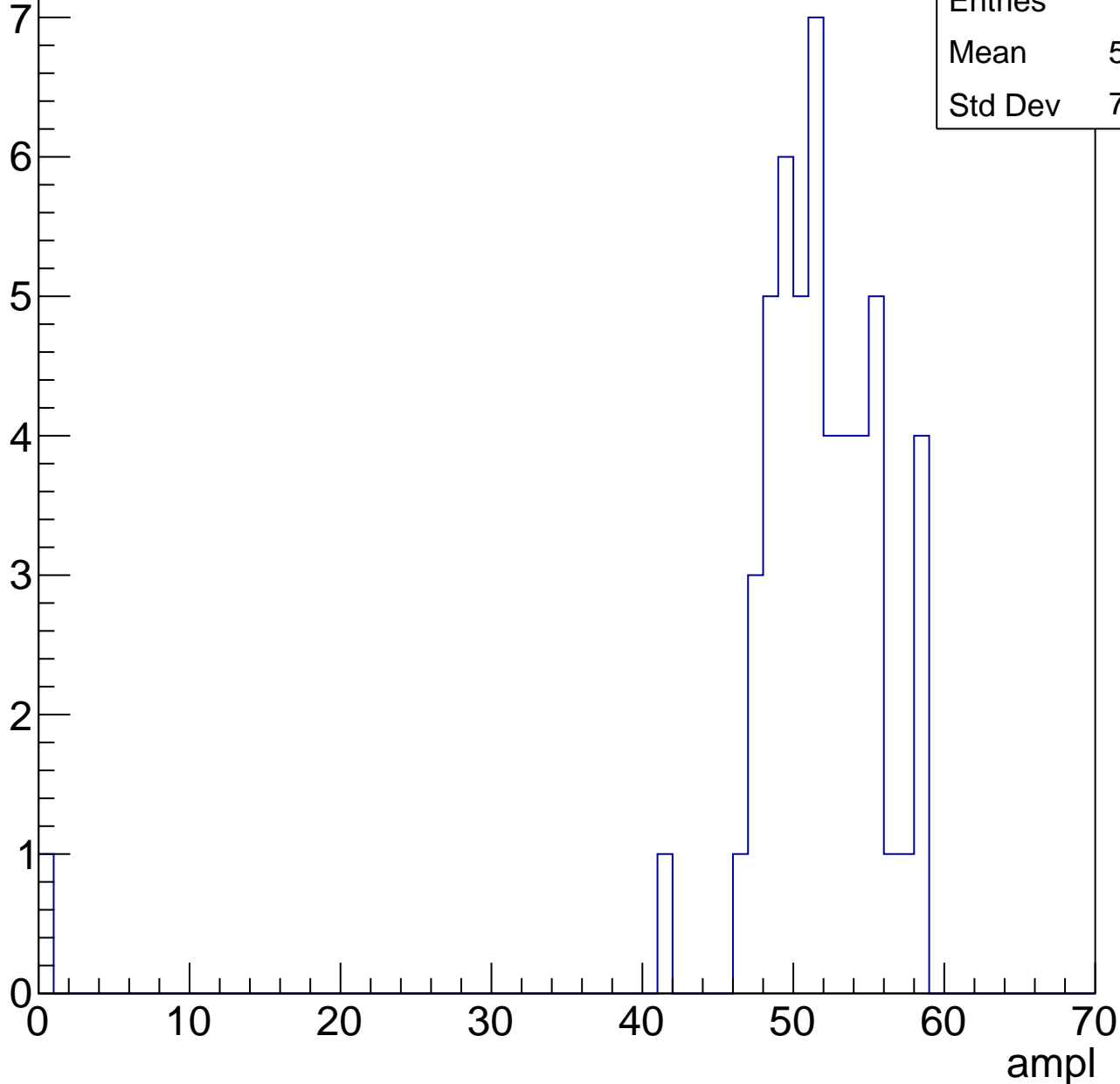


B1L103S, U2-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	50.48
Std Dev	7.878

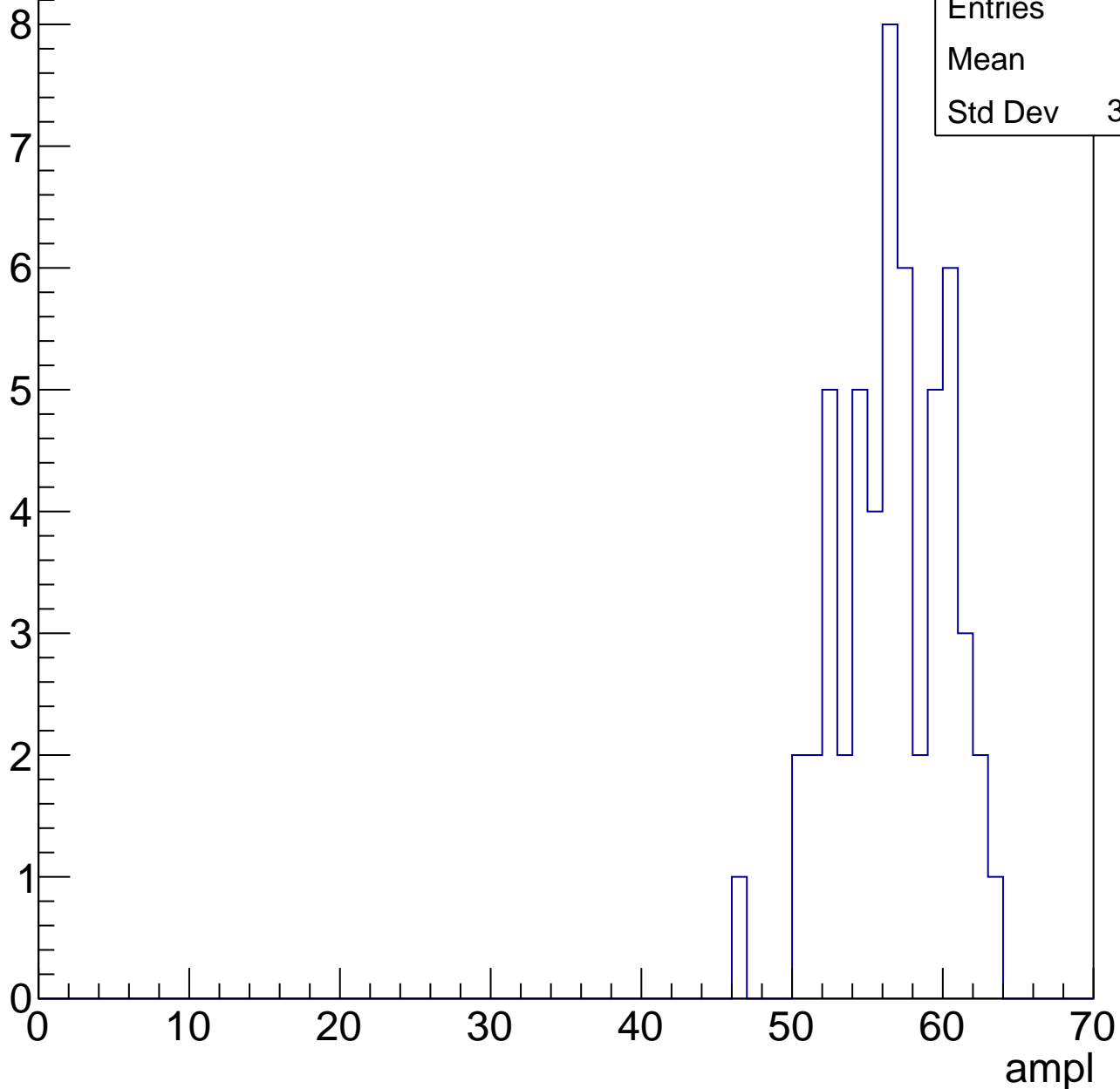


B1L103S, U2-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.2
Std Dev	3.582

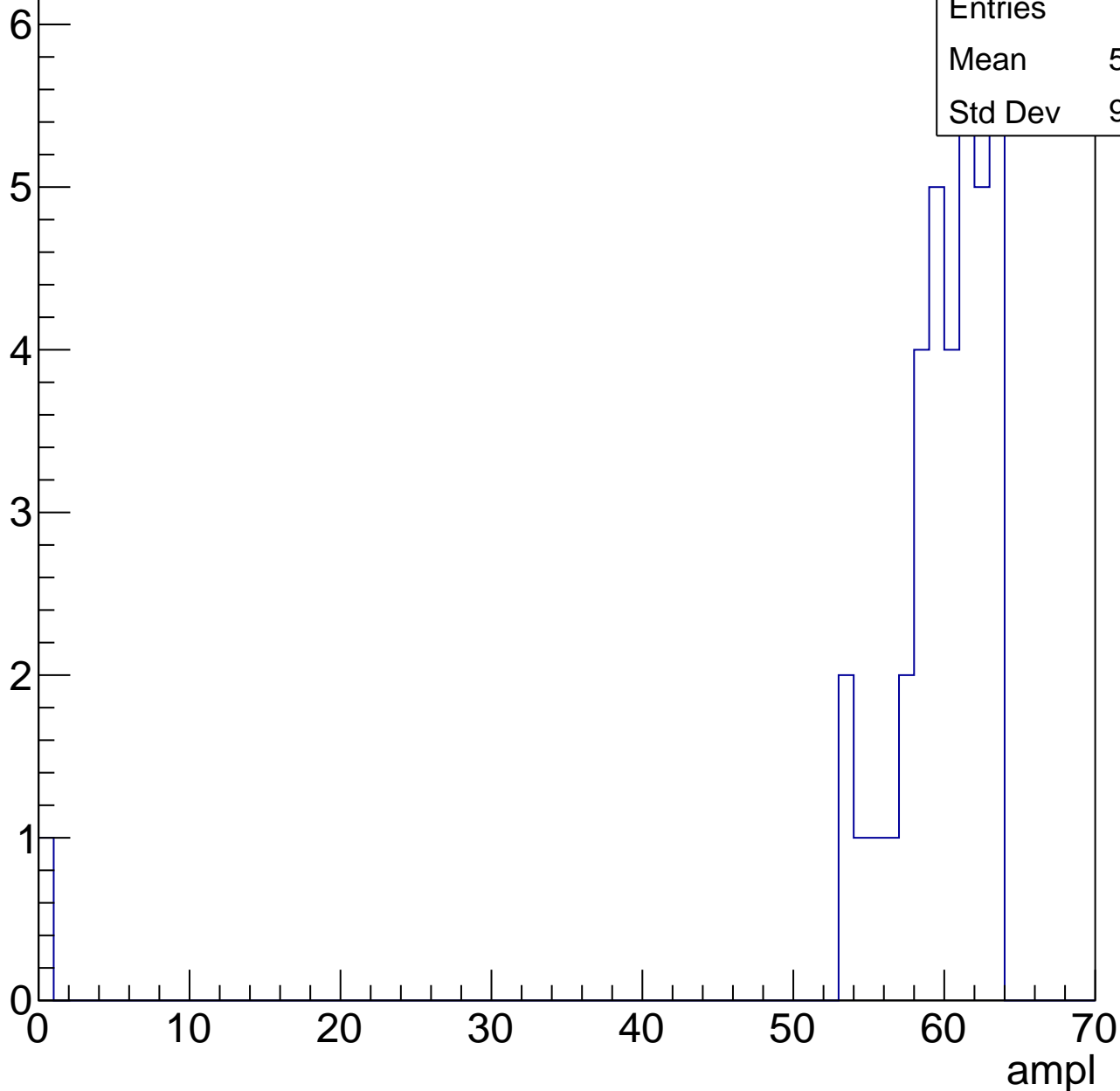


B1L103S, U2-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

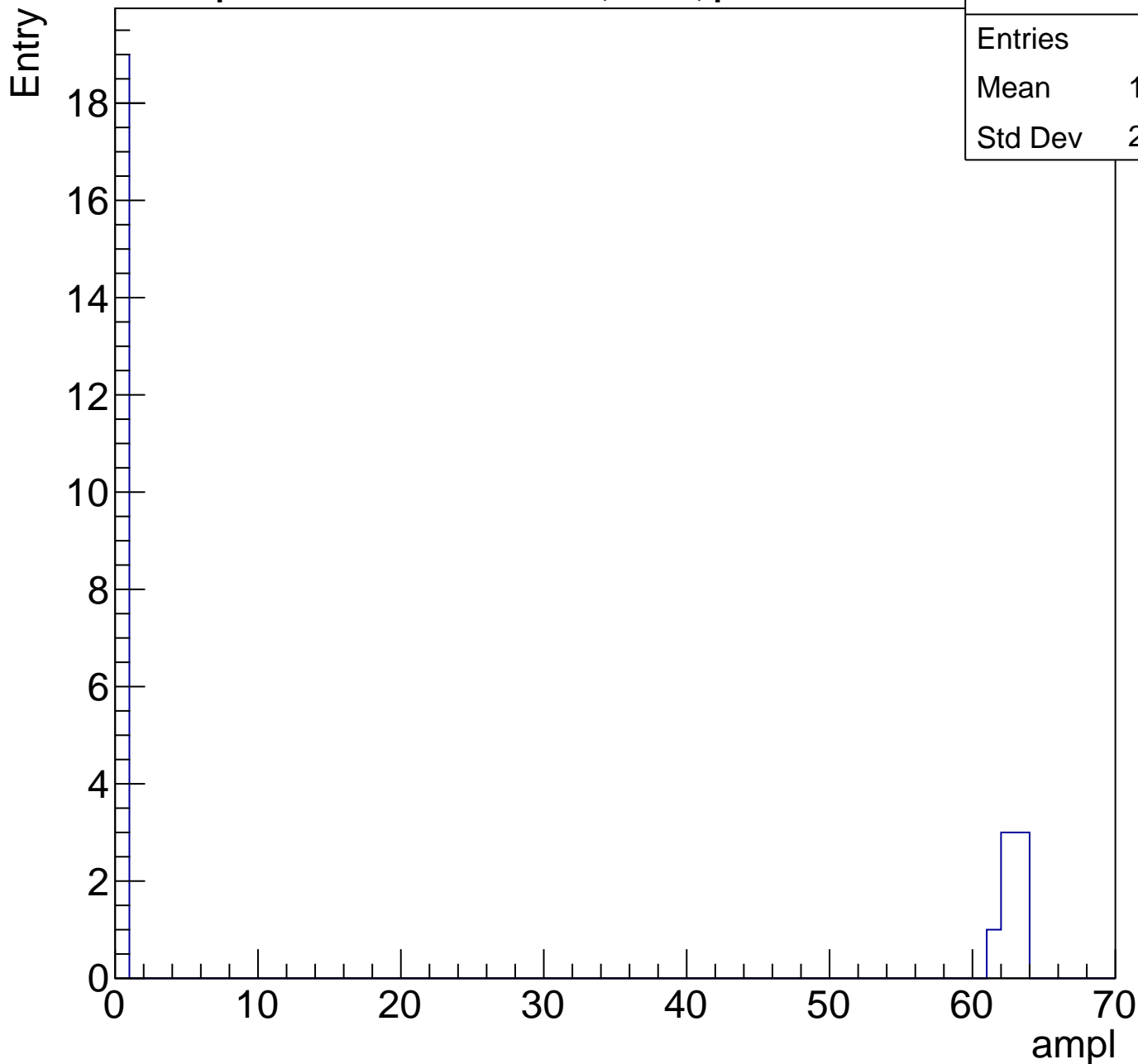
Entries	38
Mean	58.05
Std Dev	9.934



B1L103S, U2-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	16.77
Std Dev	27.63



B1L103S, U2-ch1, adc0

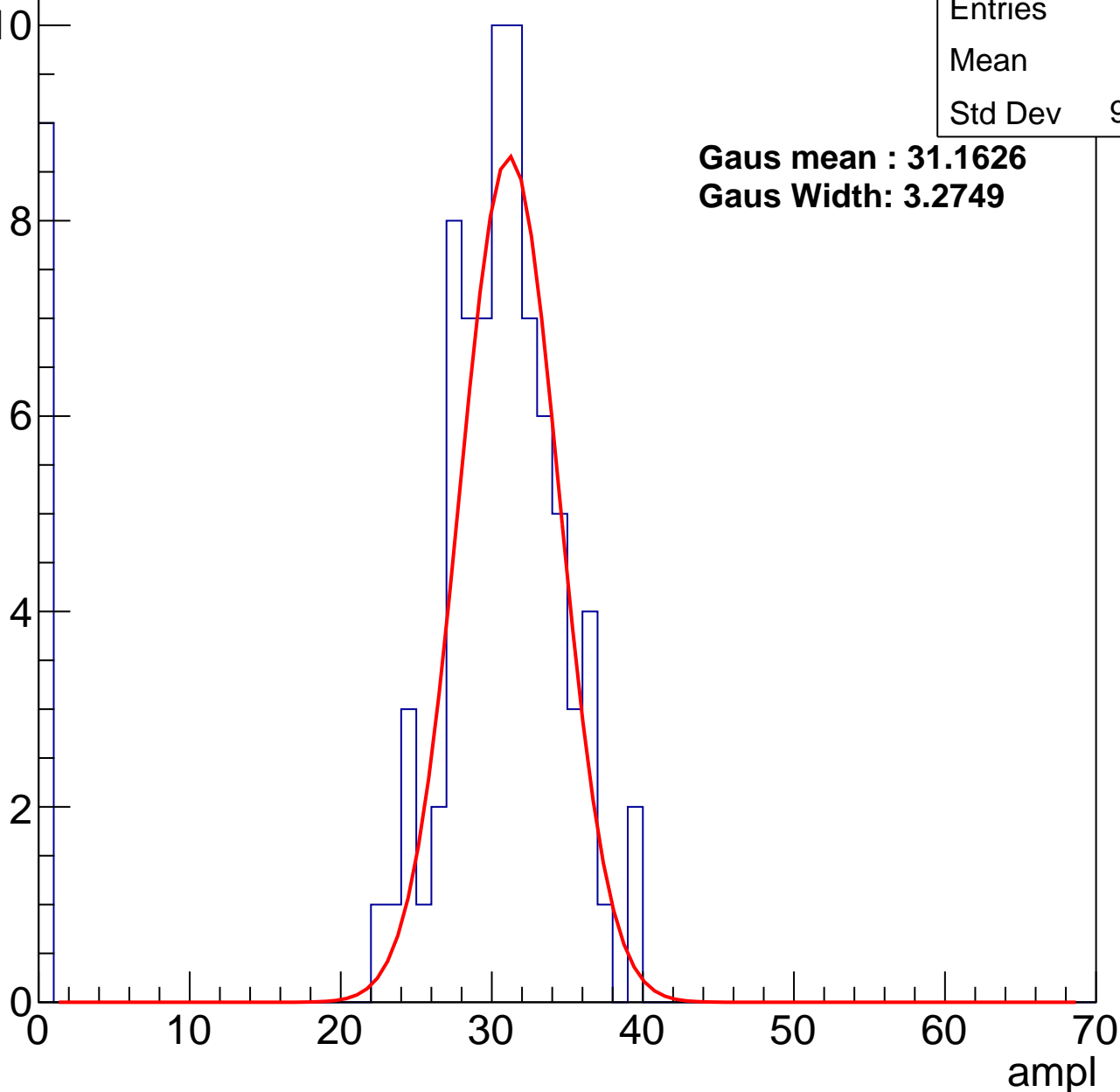
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	27.3
Std Dev	9.857

Gaus mean : 31.1626

Gaus Width: 3.2749



B1L103S, U2-ch1, adc1

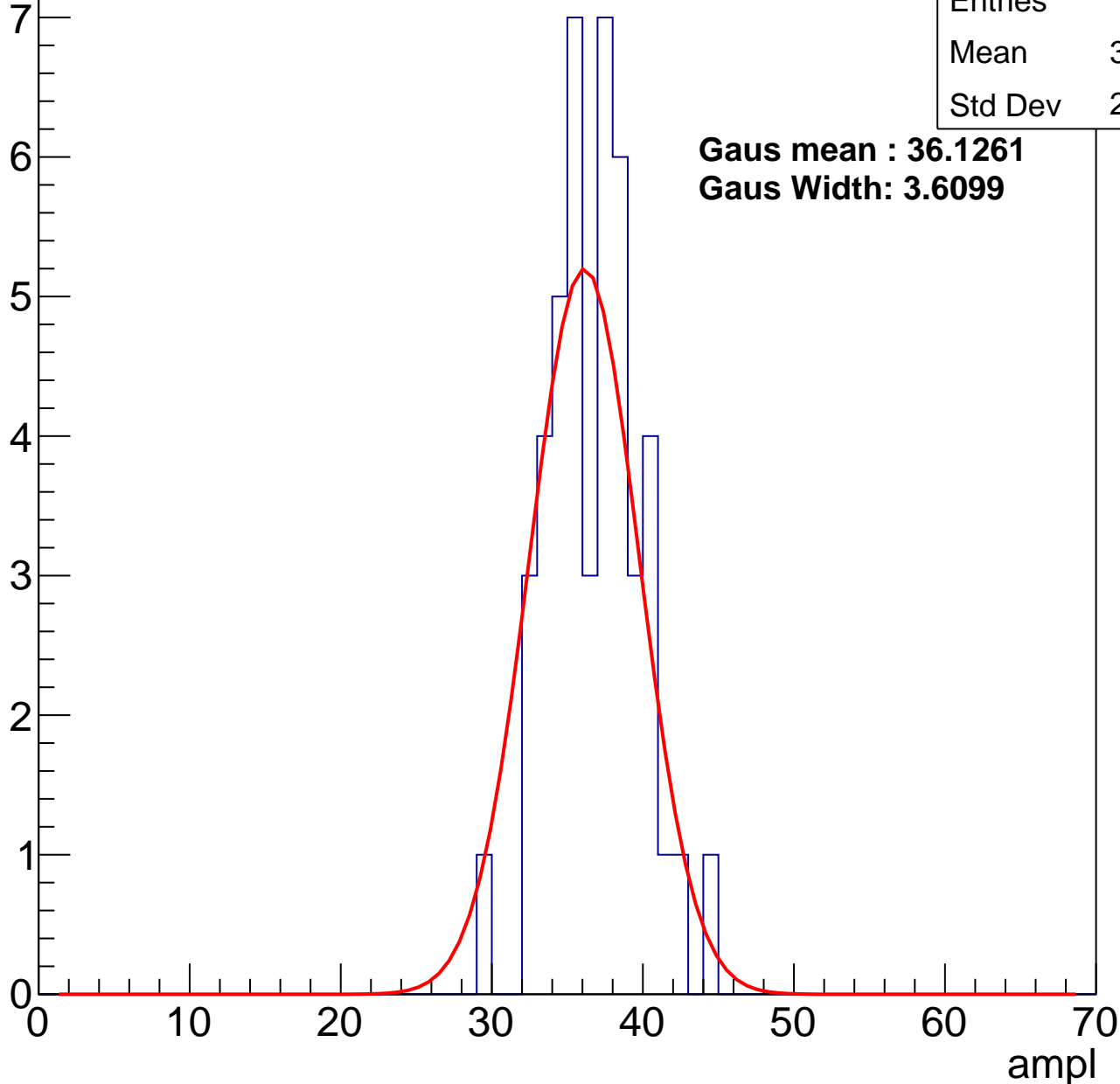
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	36.33
Std Dev	2.957

Gaus mean : 36.1261

Gaus Width: 3.6099



B1L103S, U2-ch1, adc2

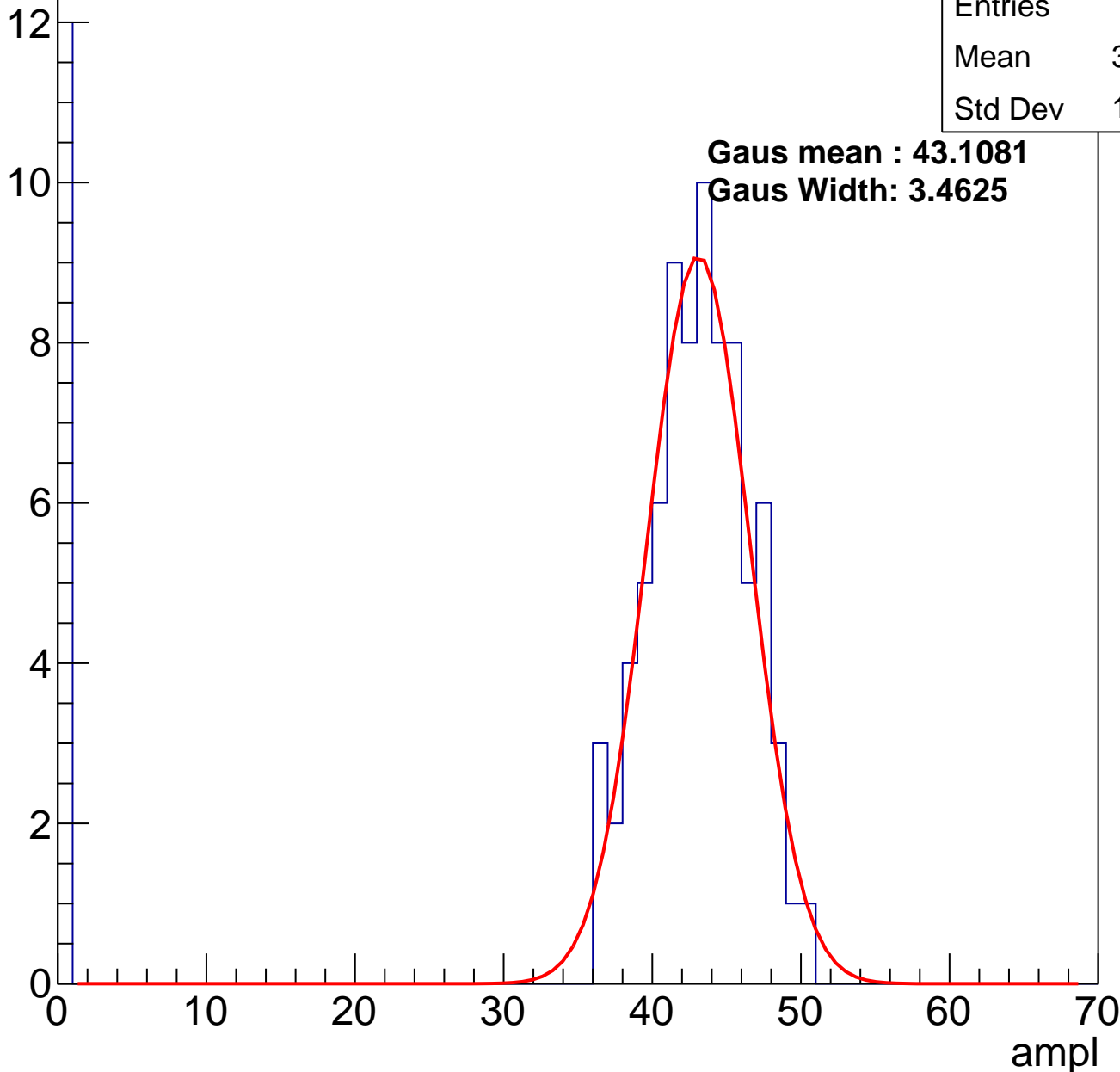
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	37.04
Std Dev	14.75

Gaus mean : 43.1081

Gaus Width: 3.4625

Entry

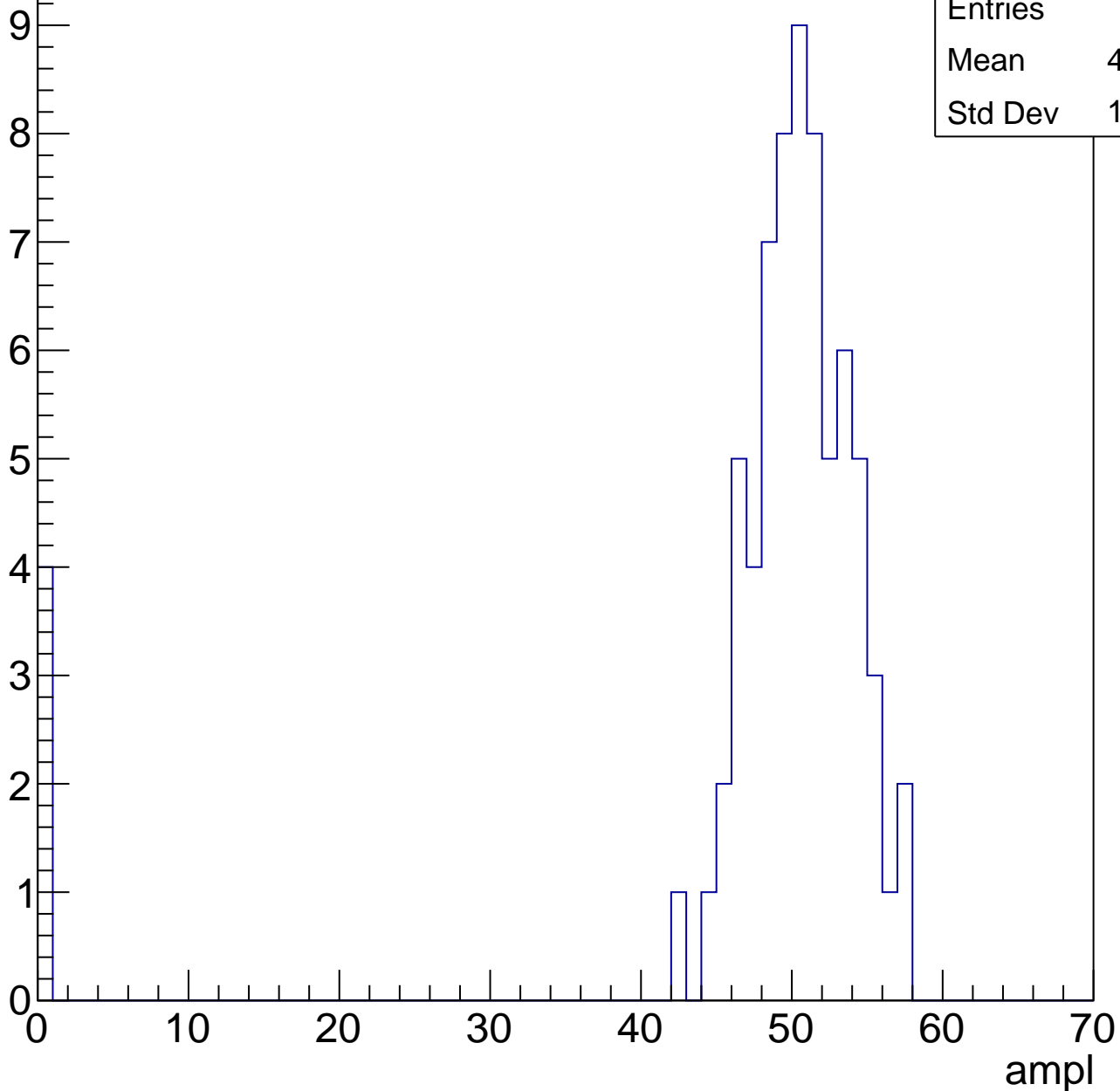


B1L103S, U2-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	47.37
Std Dev	11.97

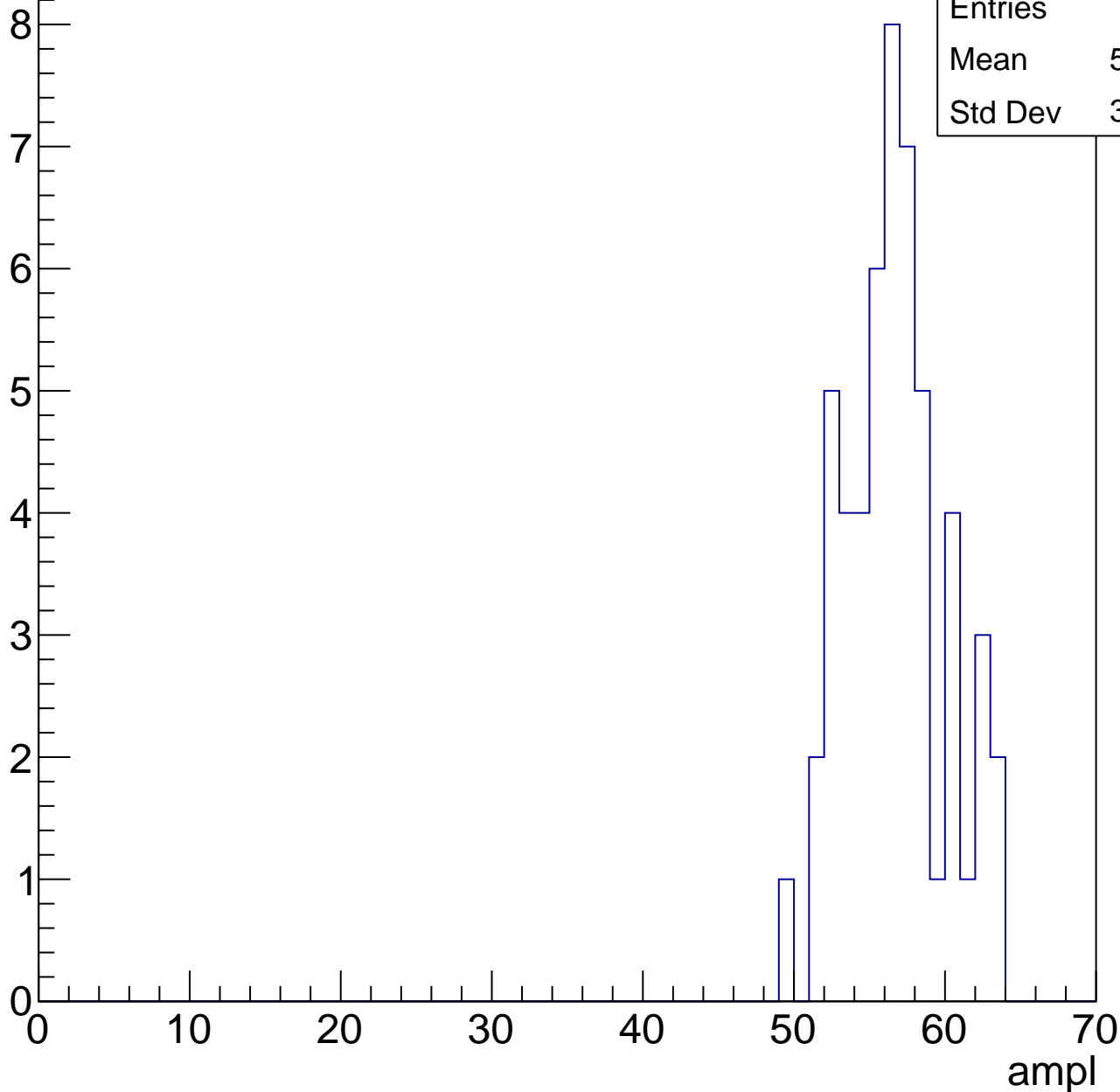


B1L103S, U2-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	56.19
Std Dev	3.268



B1L103S, U2-ch1, adc5

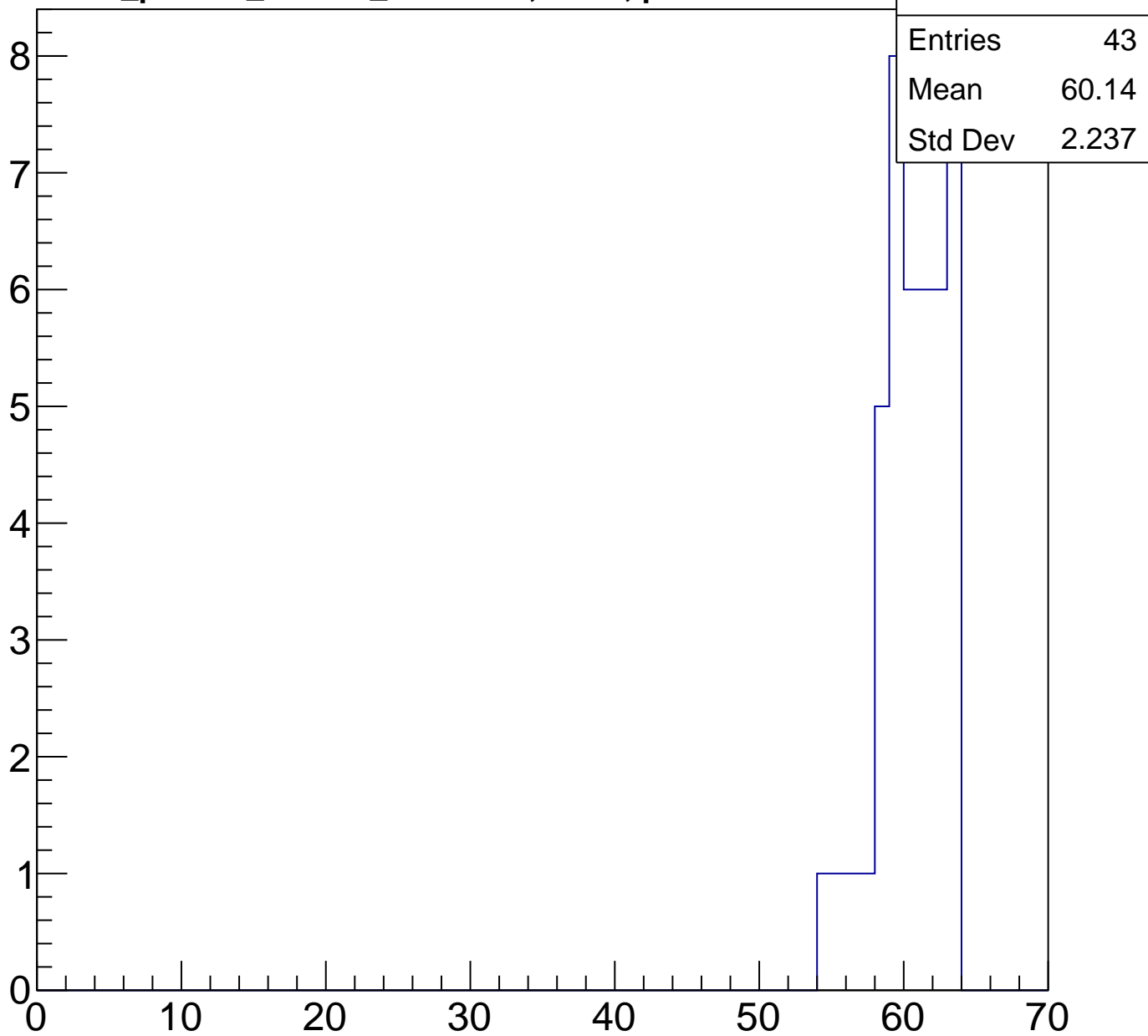
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	60.14
Std Dev	2.237

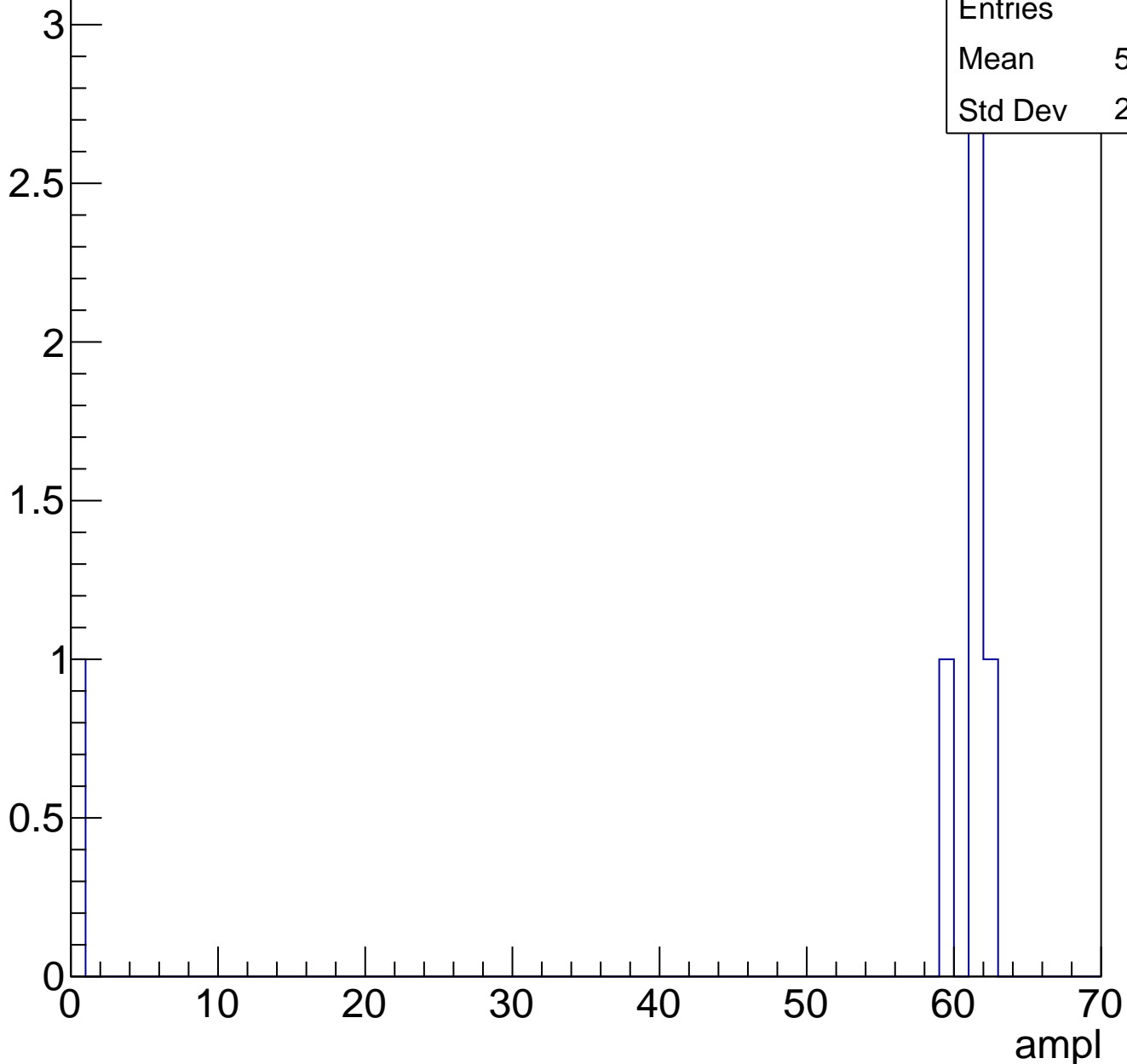
ampl



B1L103S, U2-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

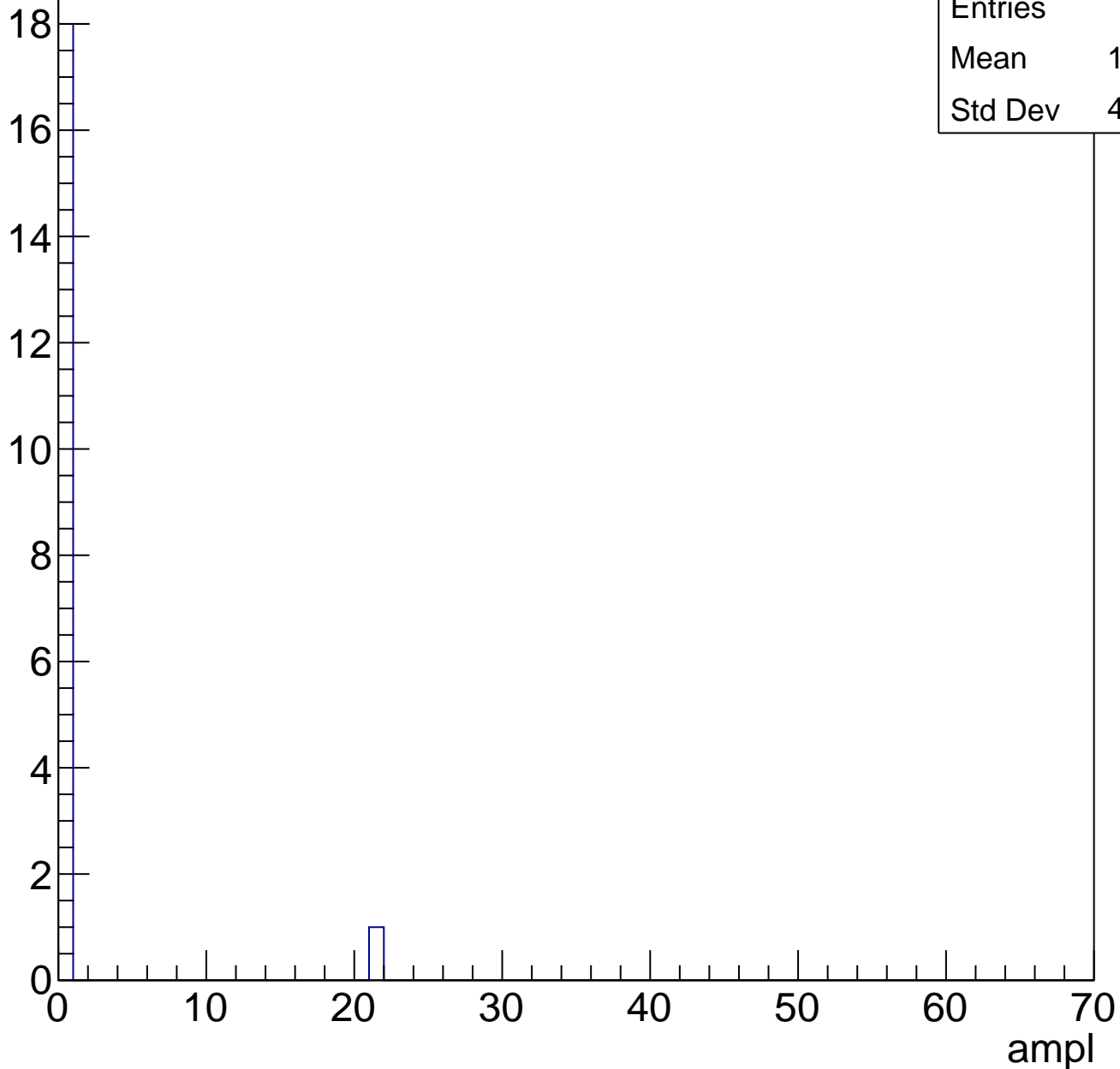


B1L103S, U2-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U2-ch2, adc0

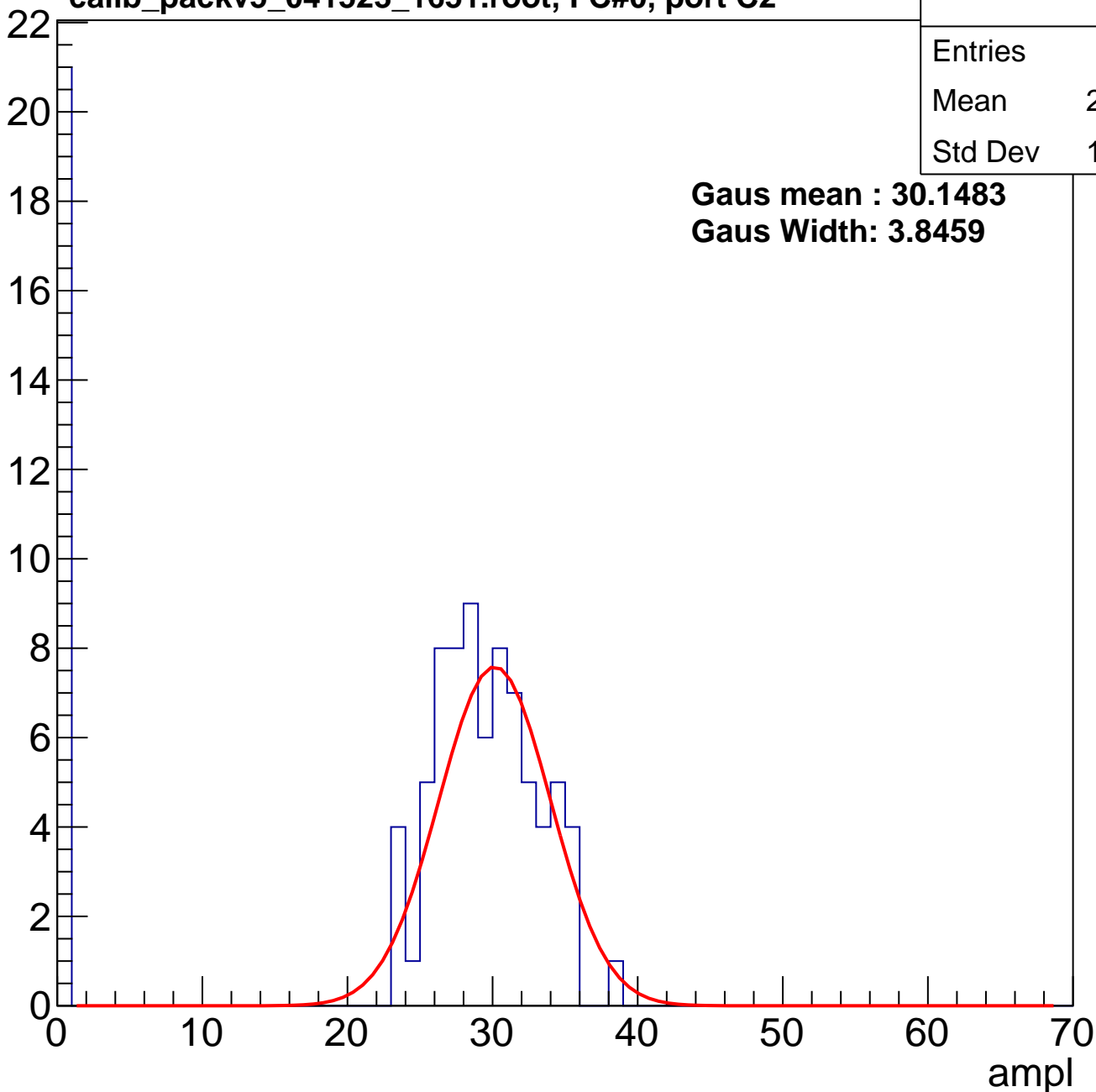
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	22.79
Std Dev	12.42

Gaus mean : 30.1483

Gaus Width: 3.8459

Entry



B1L103S, U2-ch2, adc1

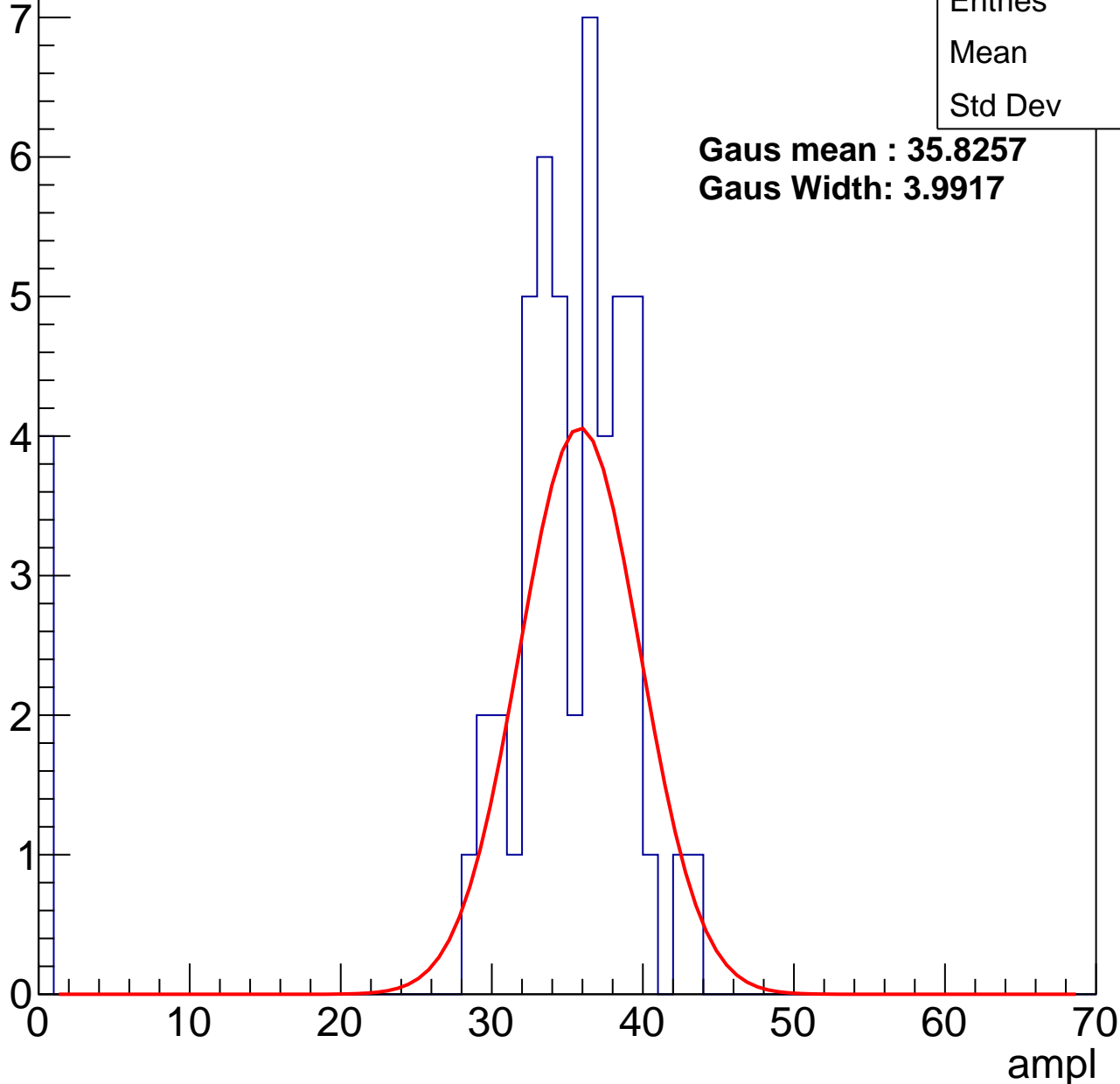
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	32.4
Std Dev	9.9

Gaus mean : 35.8257

Gaus Width: 3.9917



B1L103S, U2-ch2, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	35.01
Std Dev	14.67

Gaus mean : 41.0514

Gaus Width: 4.3674

Entry

10

8

6

4

2

0

0

10

20

30

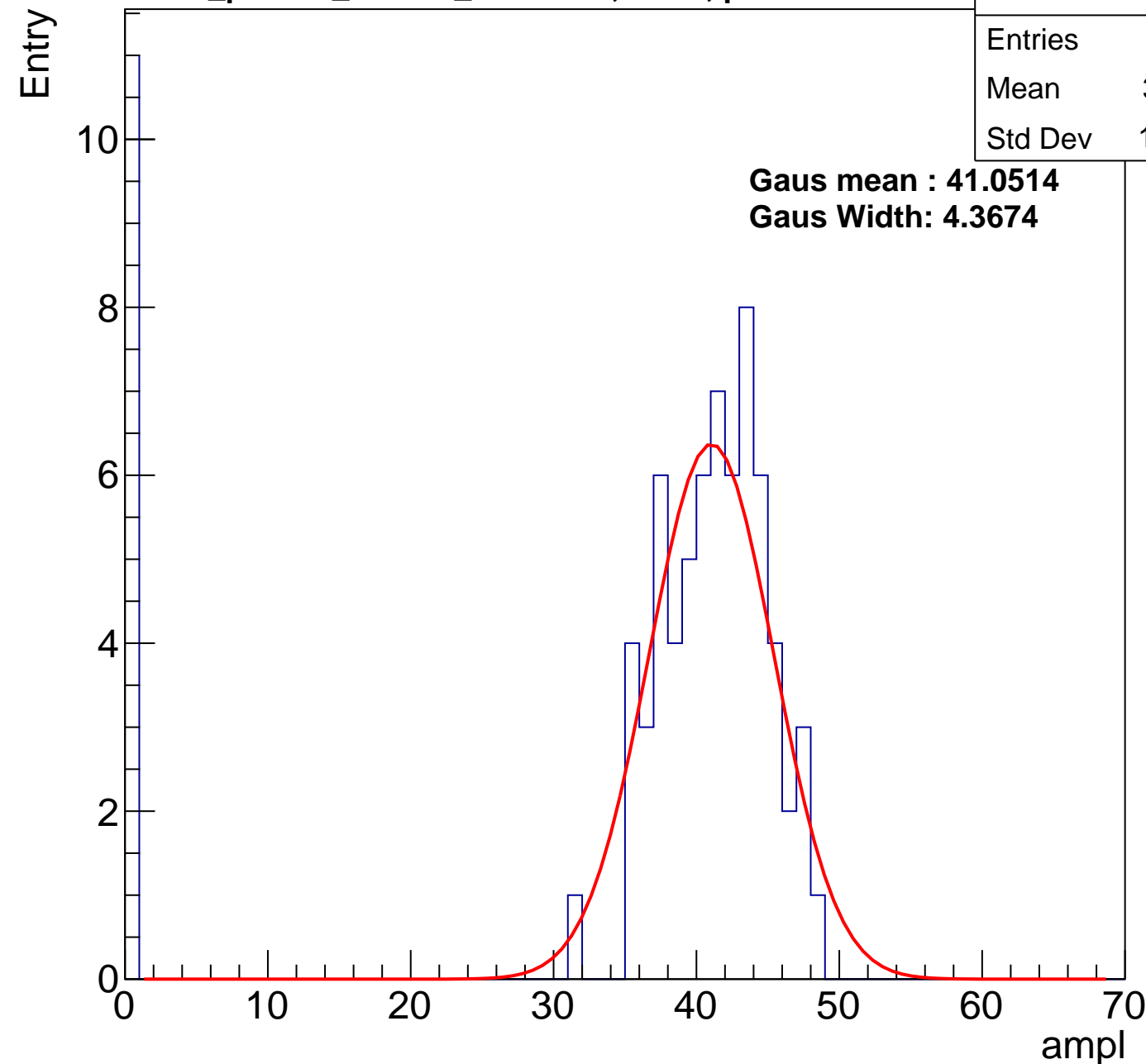
40

50

60

70

ampl

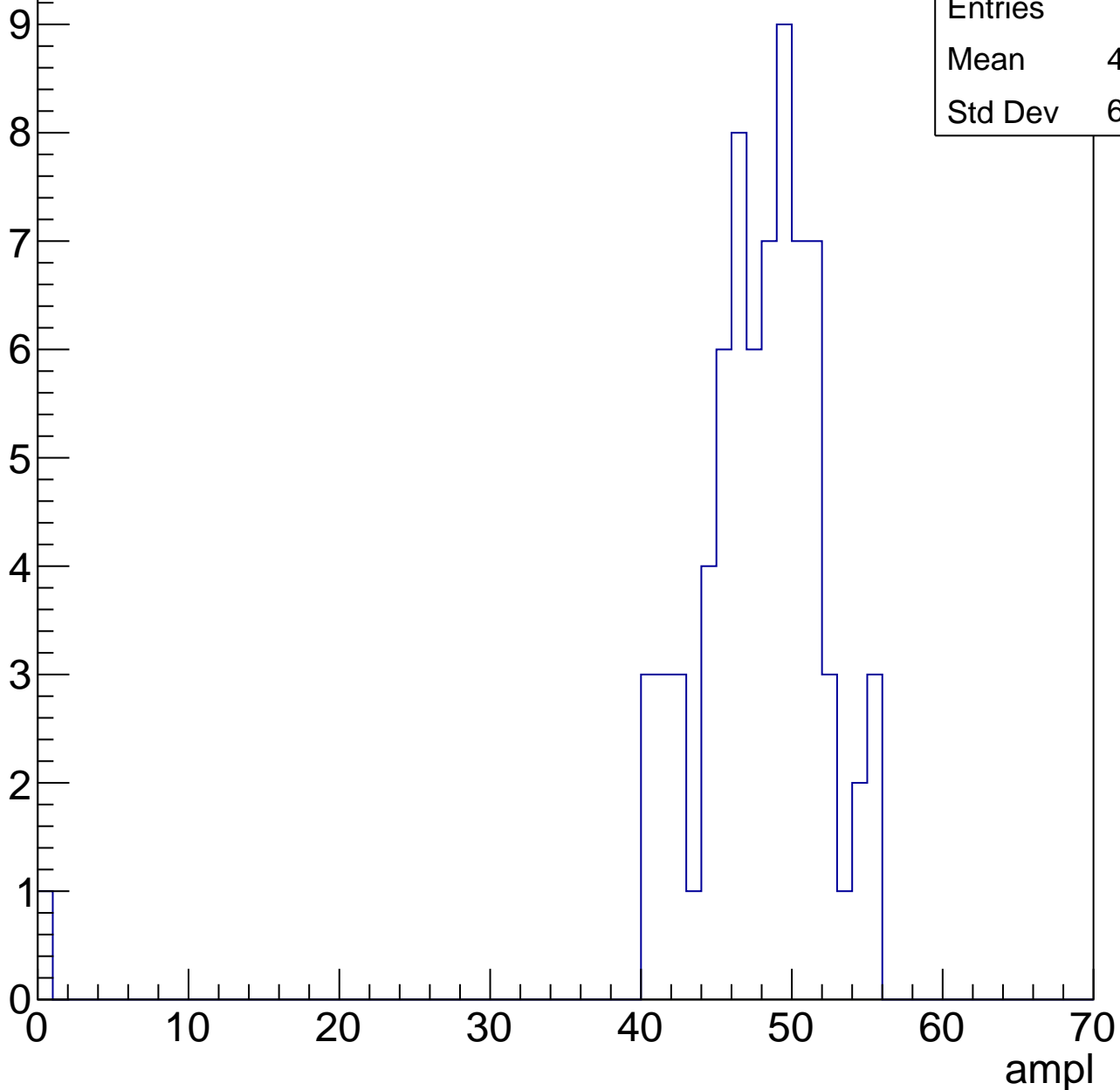


B1L103S, U2-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	46.95
Std Dev	6.619

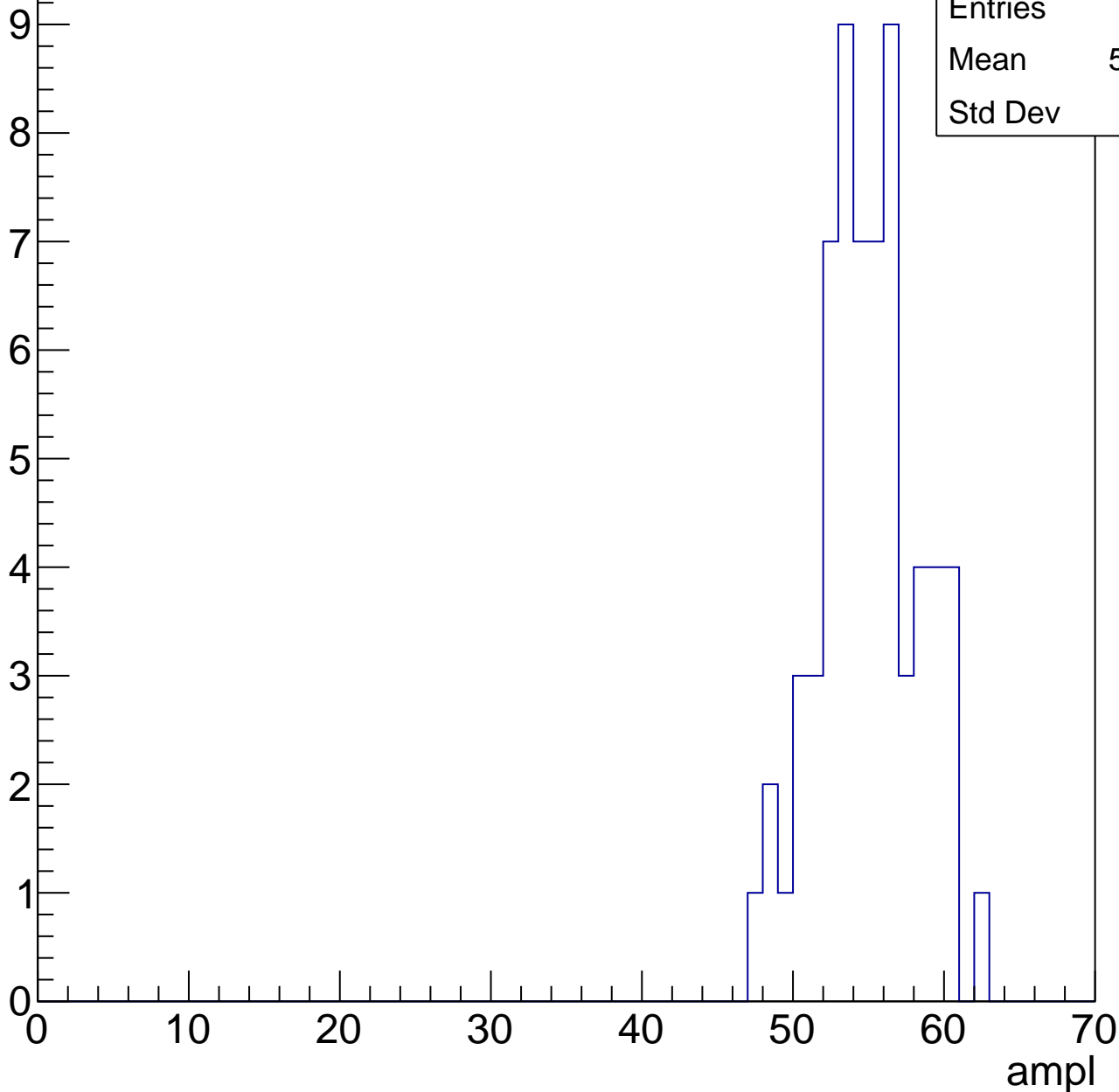


B1L103S, U2-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	54.52
Std Dev	3.24

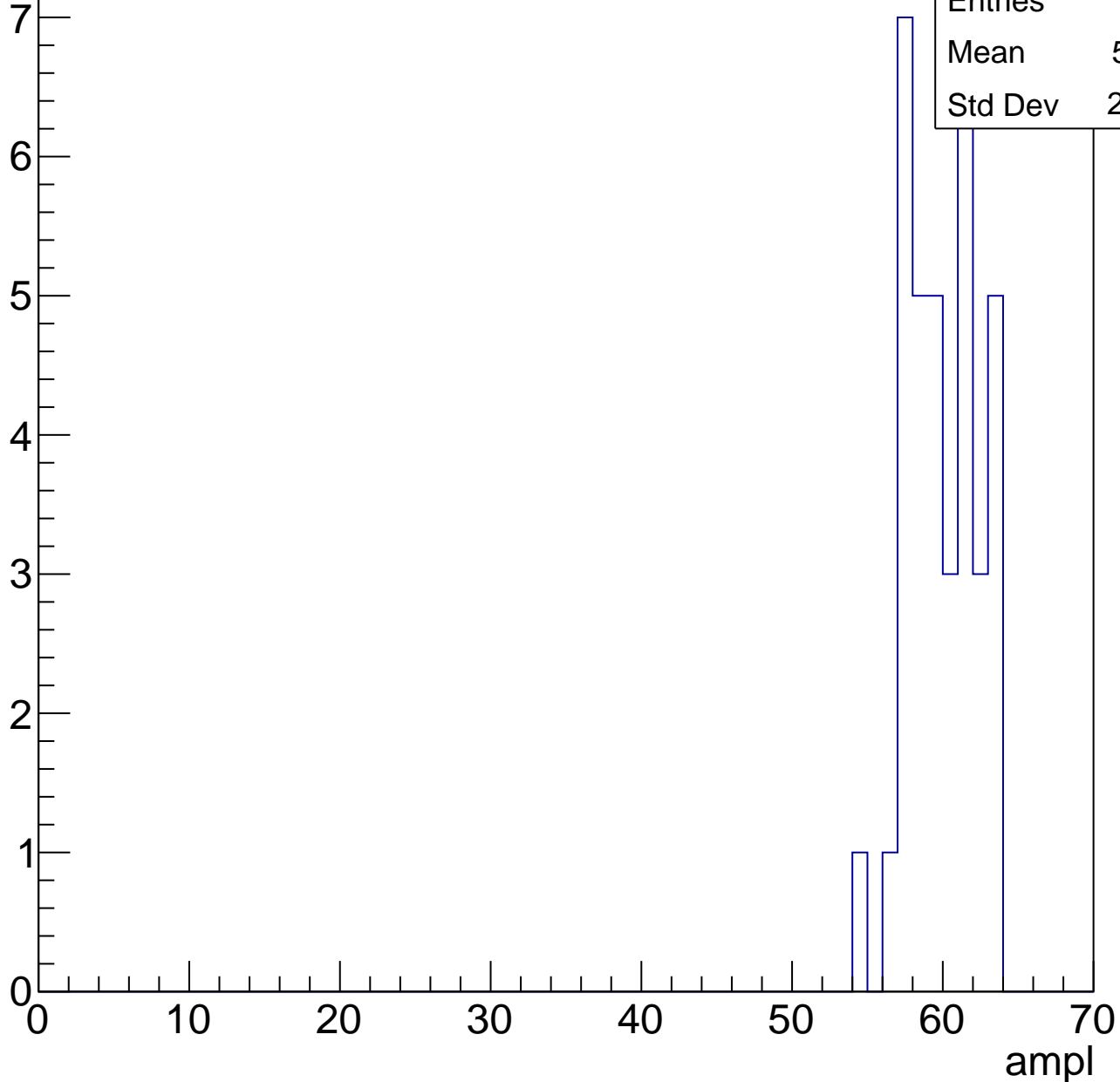


B1L103S, U2-ch2, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	59.51
Std Dev	2.297

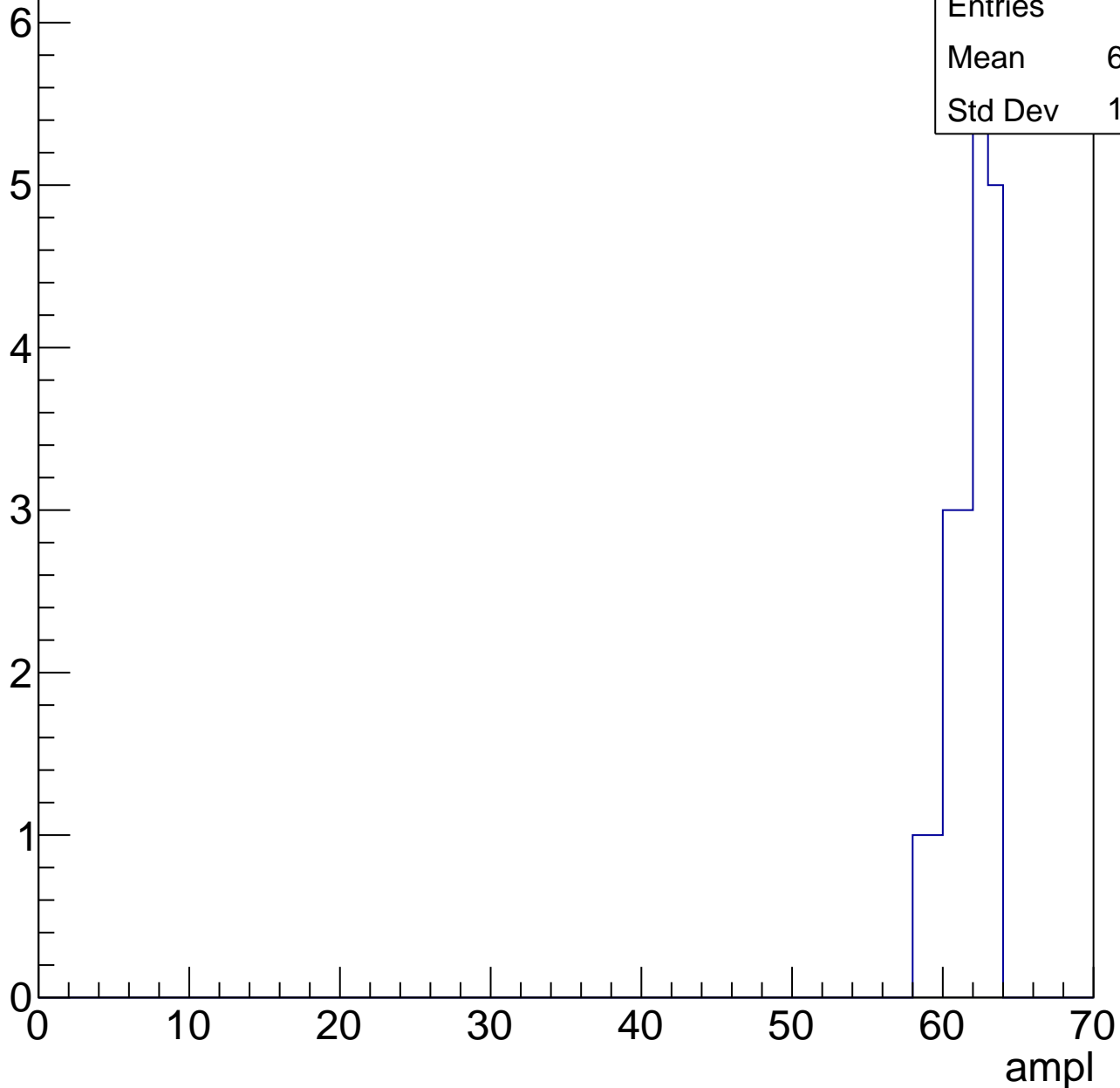


B1L103S, U2-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

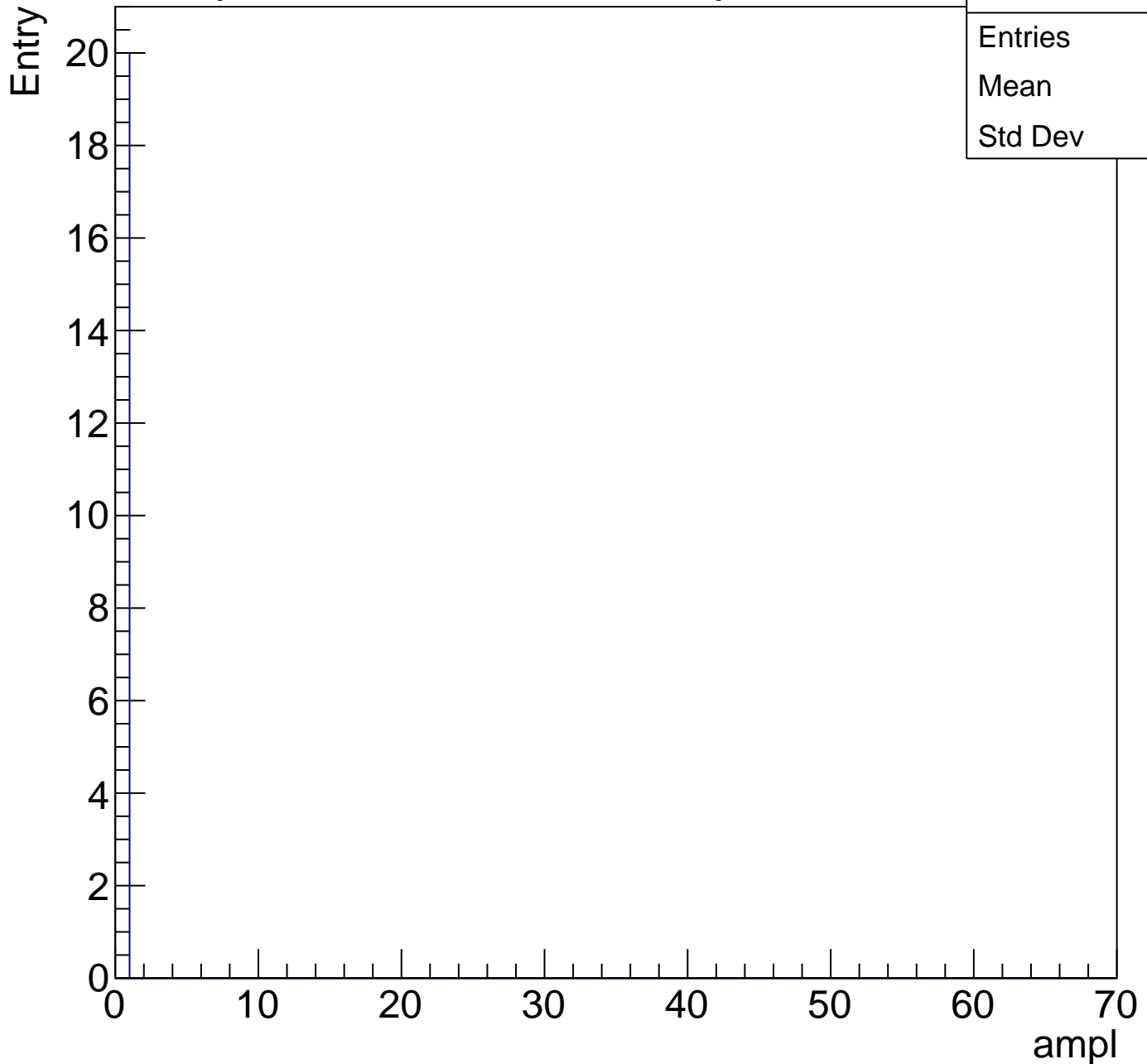
Entries	19
Mean	61.42
Std Dev	1.426



B1L103S, U2-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	0
Std Dev	0



B1L103S, U2-ch3, adc0

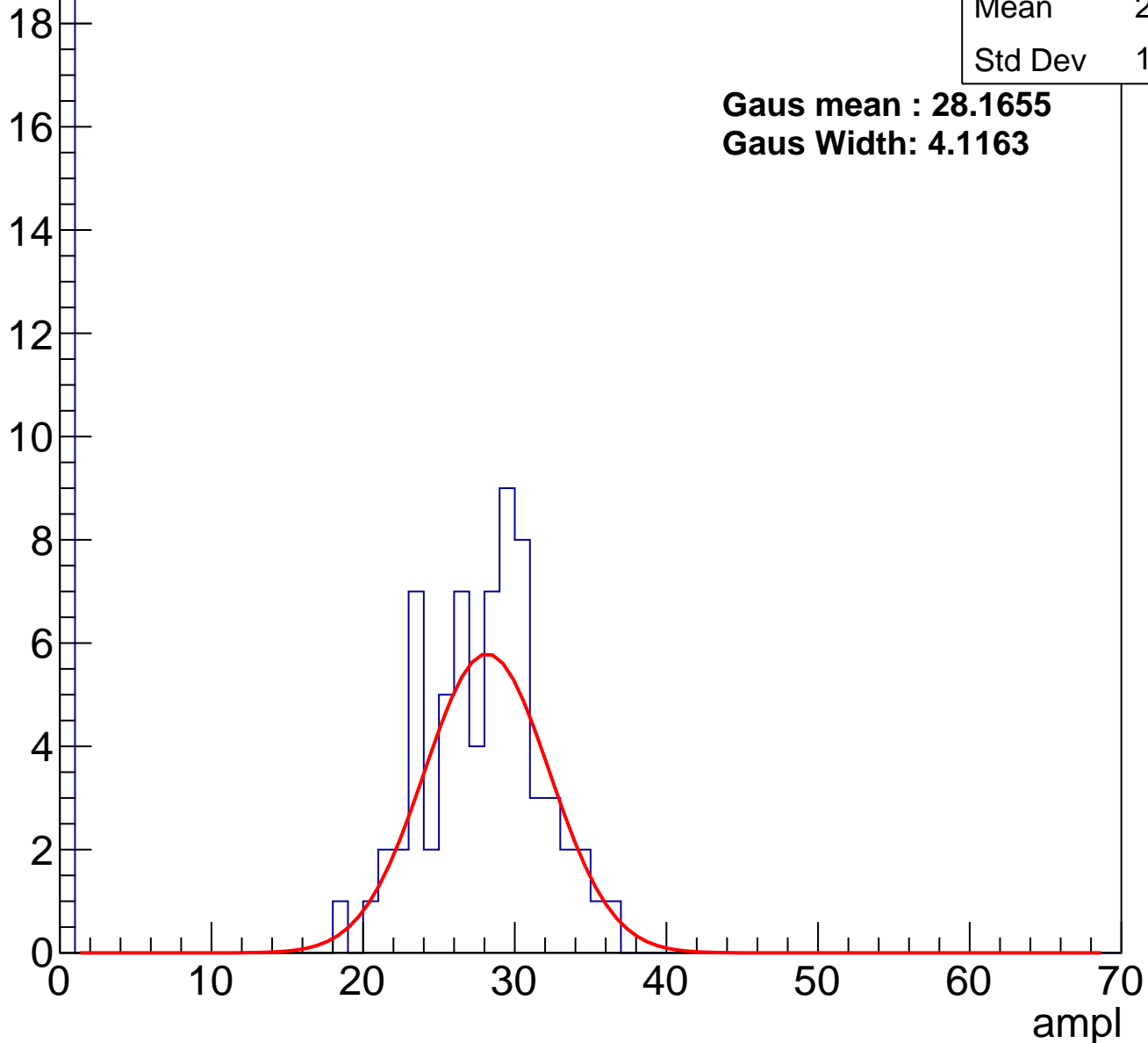
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	21.38
Std Dev	11.86

Gaus mean : 28.1655

Gaus Width: 4.1163

Entry



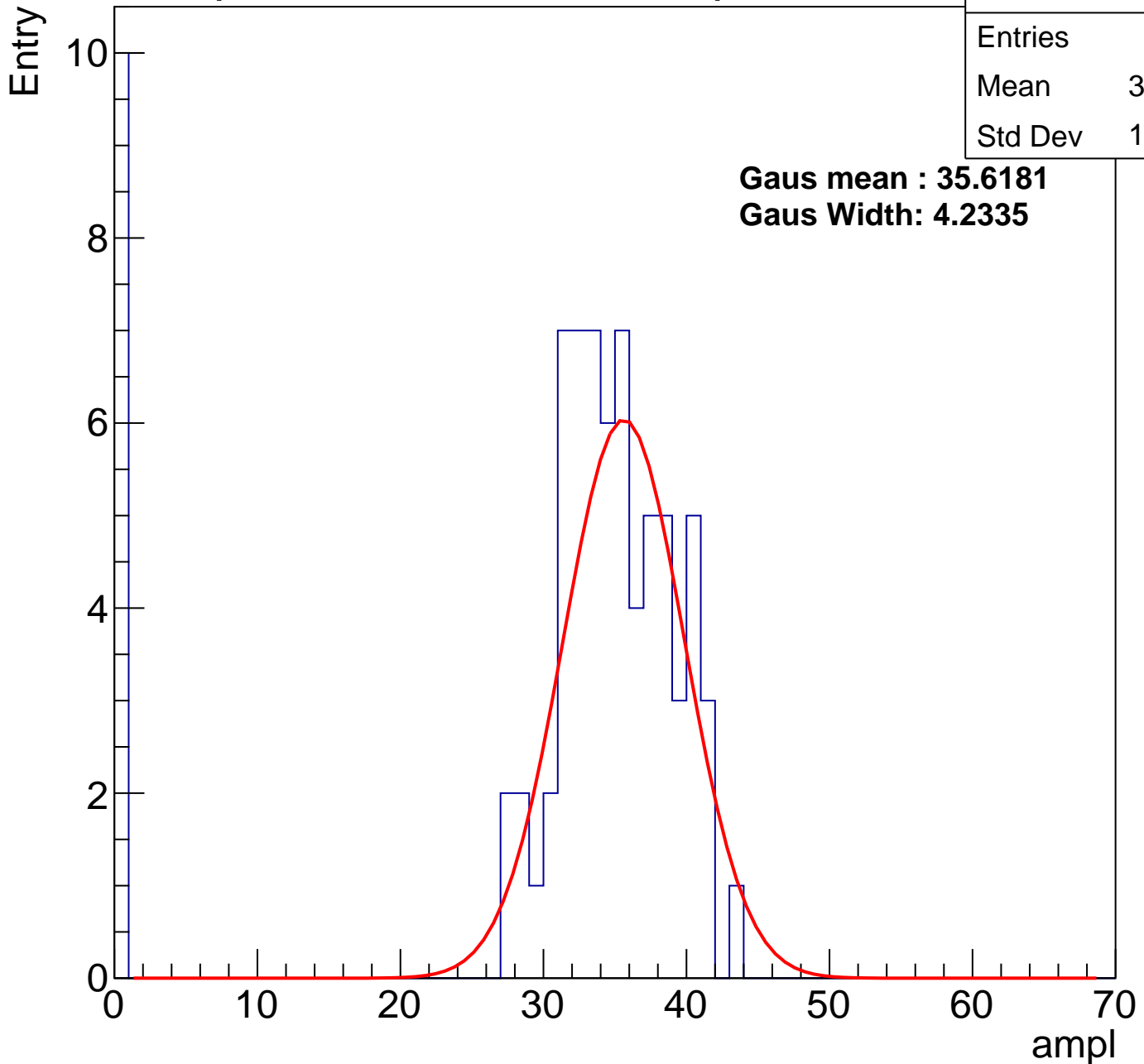
B1L103S, U2-ch3, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	30.16
Std Dev	12.16

Gaus mean : 35.6181

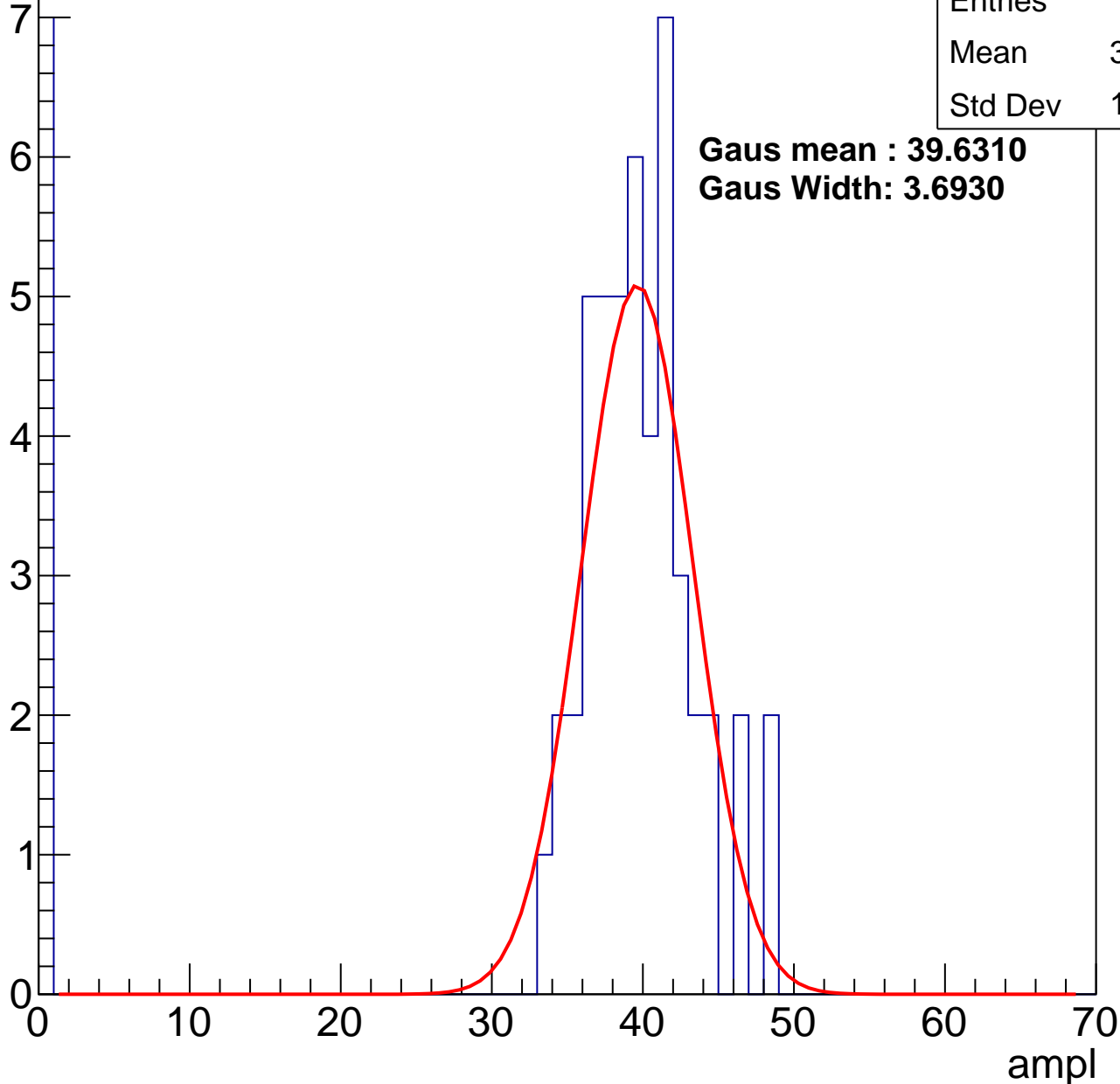
Gaus Width: 4.2335



B1L103S, U2-ch3, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

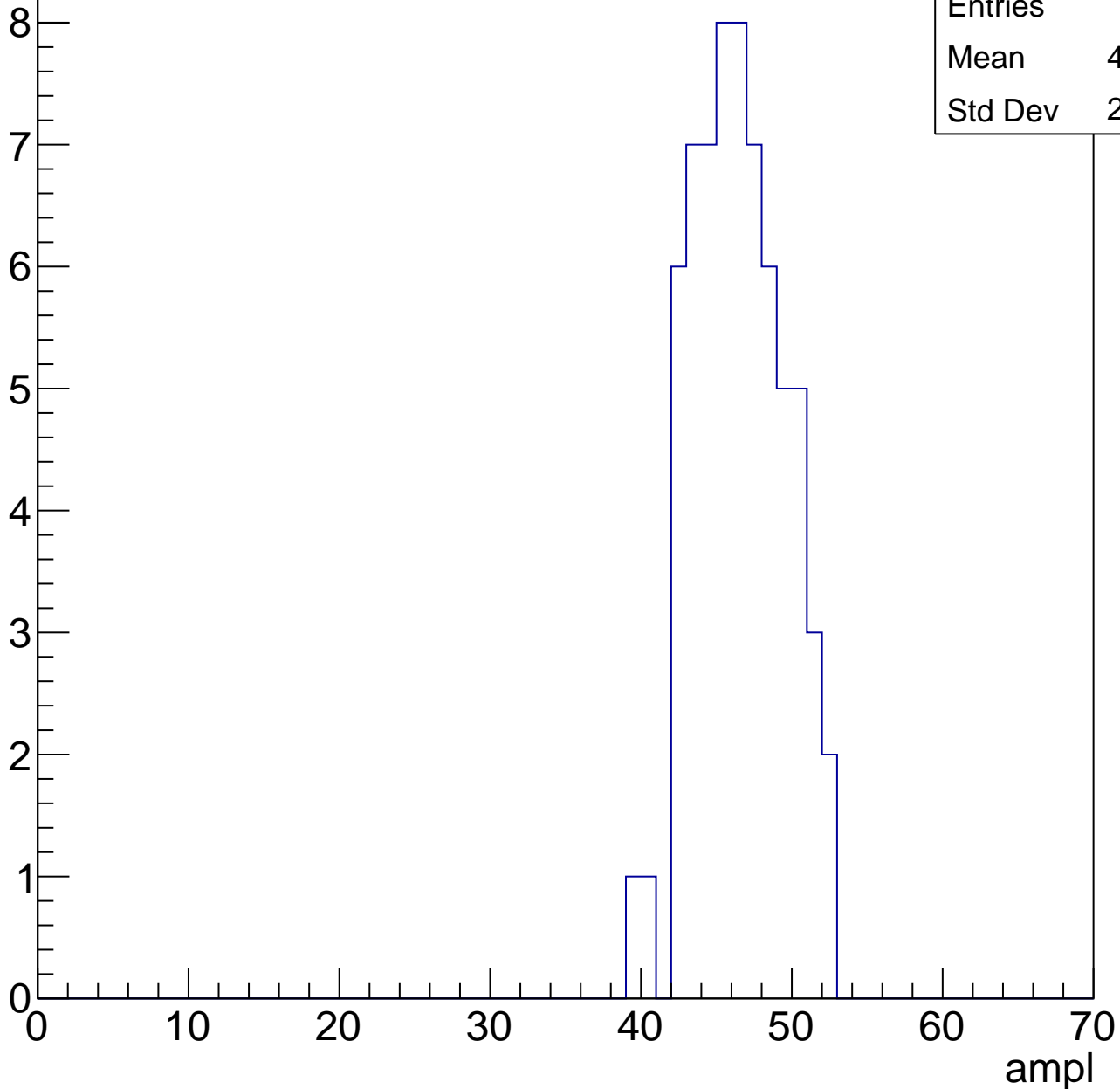


B1L103S, U2-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	46.02
Std Dev	2.972



B1L103S, U2-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

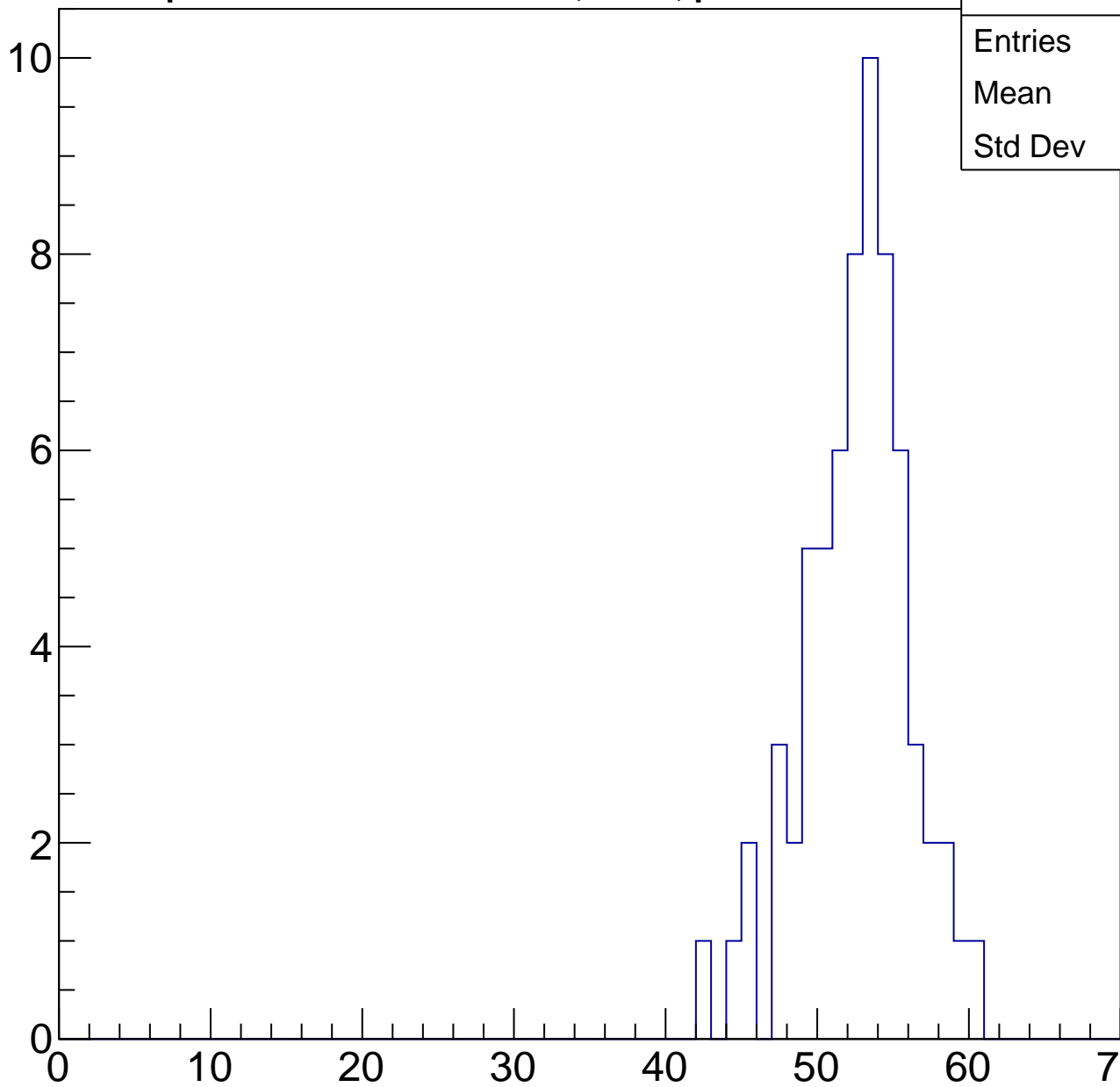
Entries	66
Mean	52.11
Std Dev	3.521

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

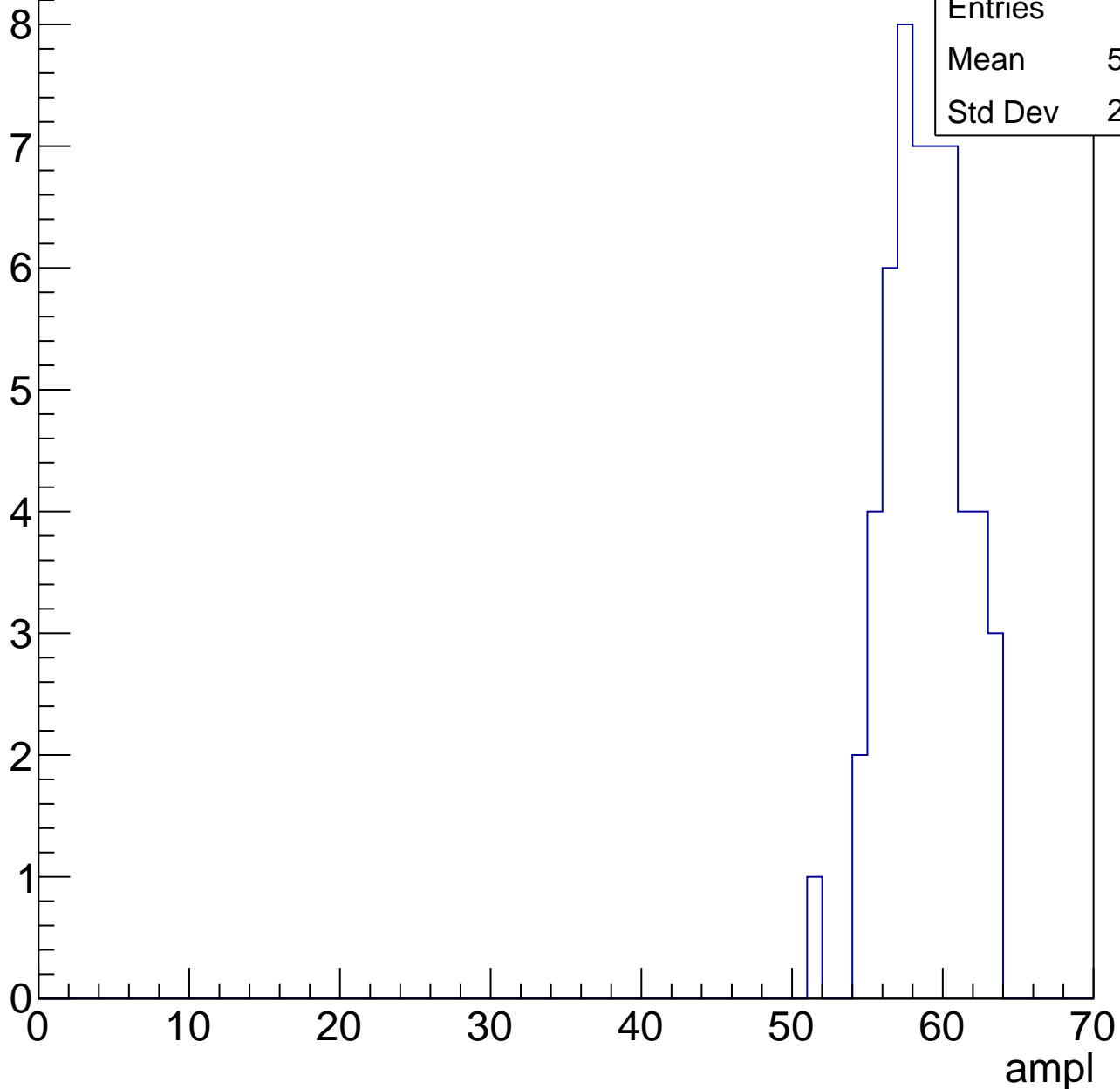
ampl



B1L103S, U2-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

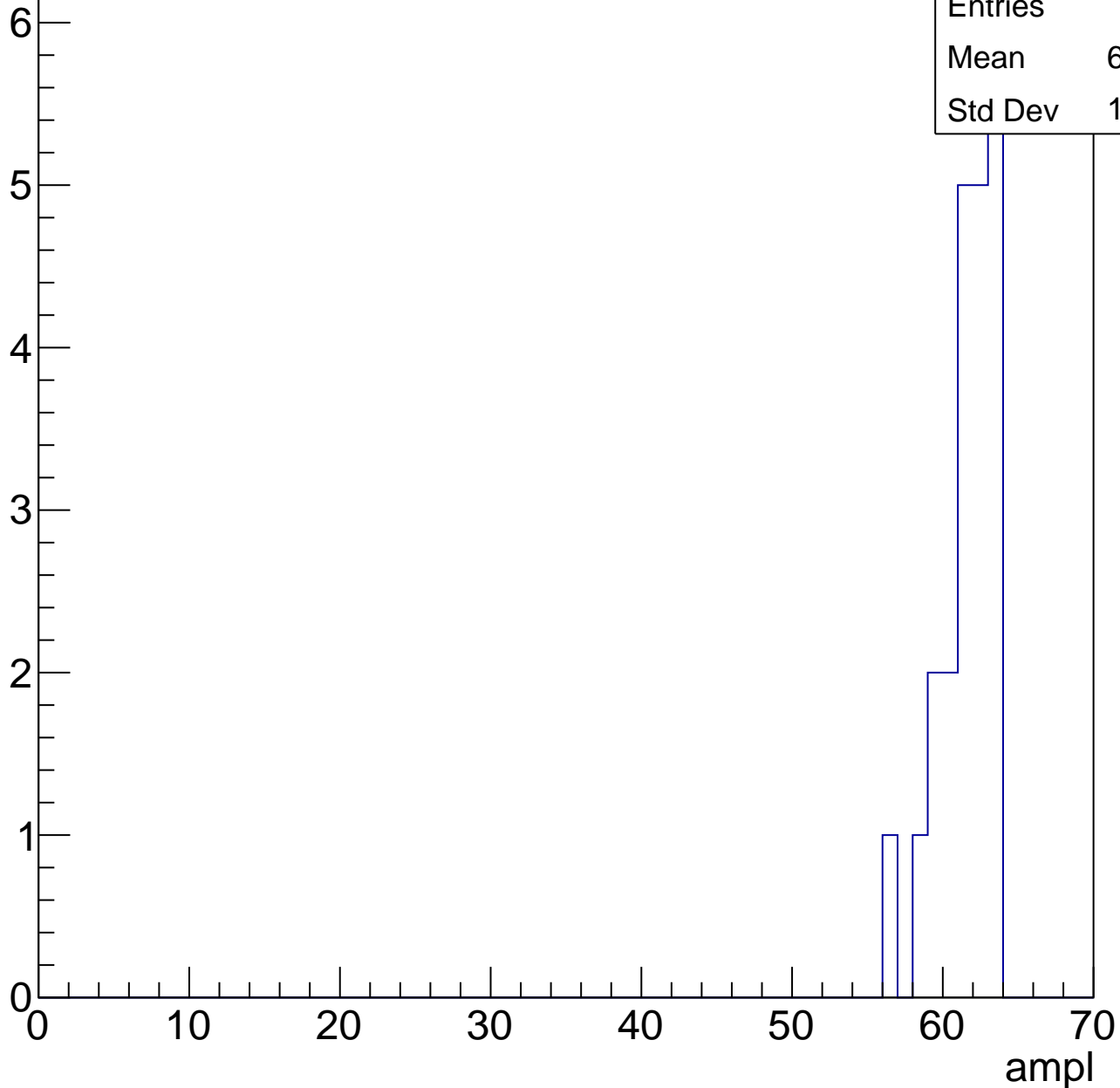


B1L103S, U2-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	61.14
Std Dev	1.816

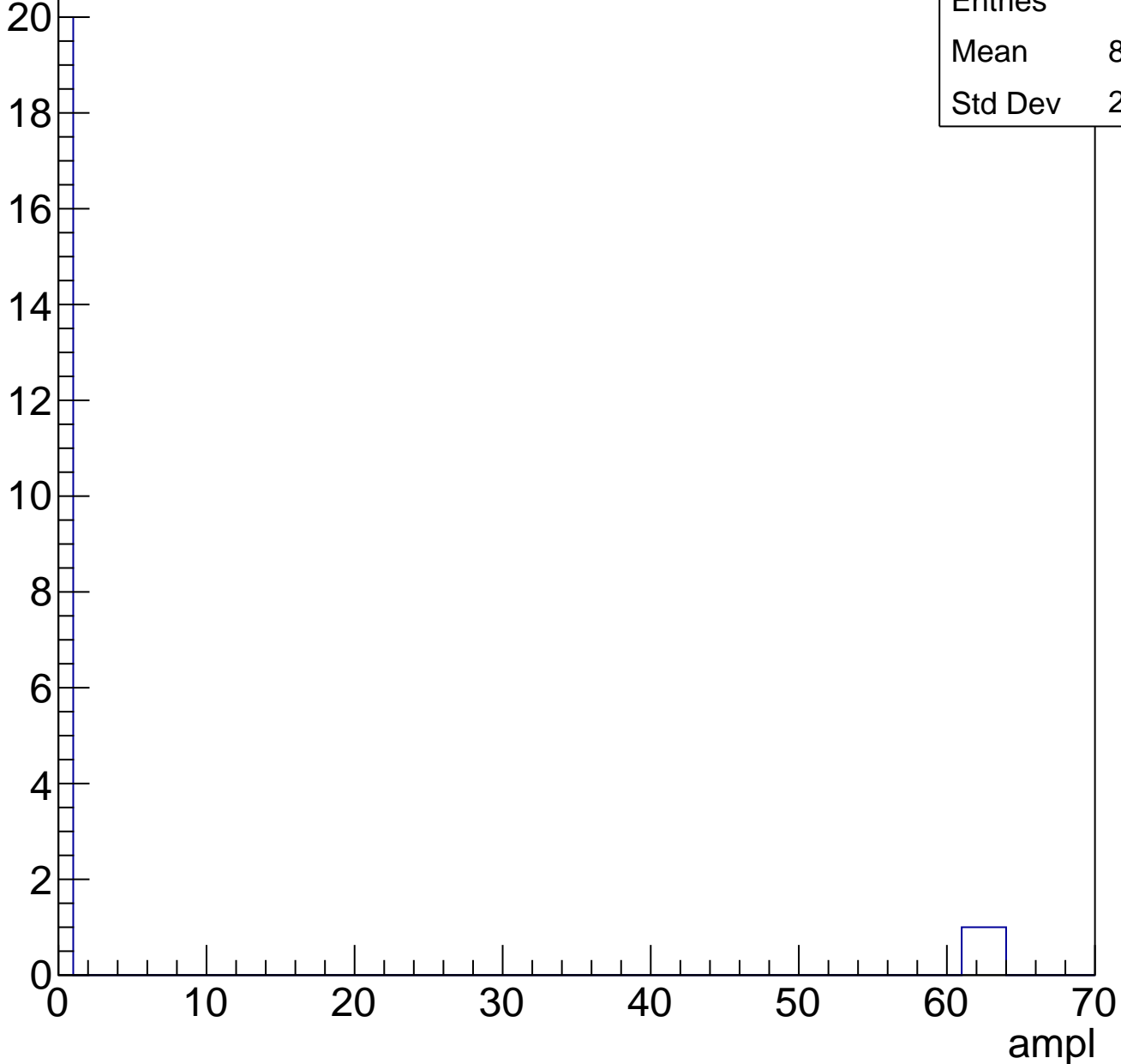


B1L103S, U2-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	8.087
Std Dev	20.88

Entry



B1L103S, U2-ch4, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	24.19
Std Dev	11.47

Gaus mean : 29.7730

Gaus Width: 3.7478

Entry

12

10

8

6

4

2

0

0

10

20

30

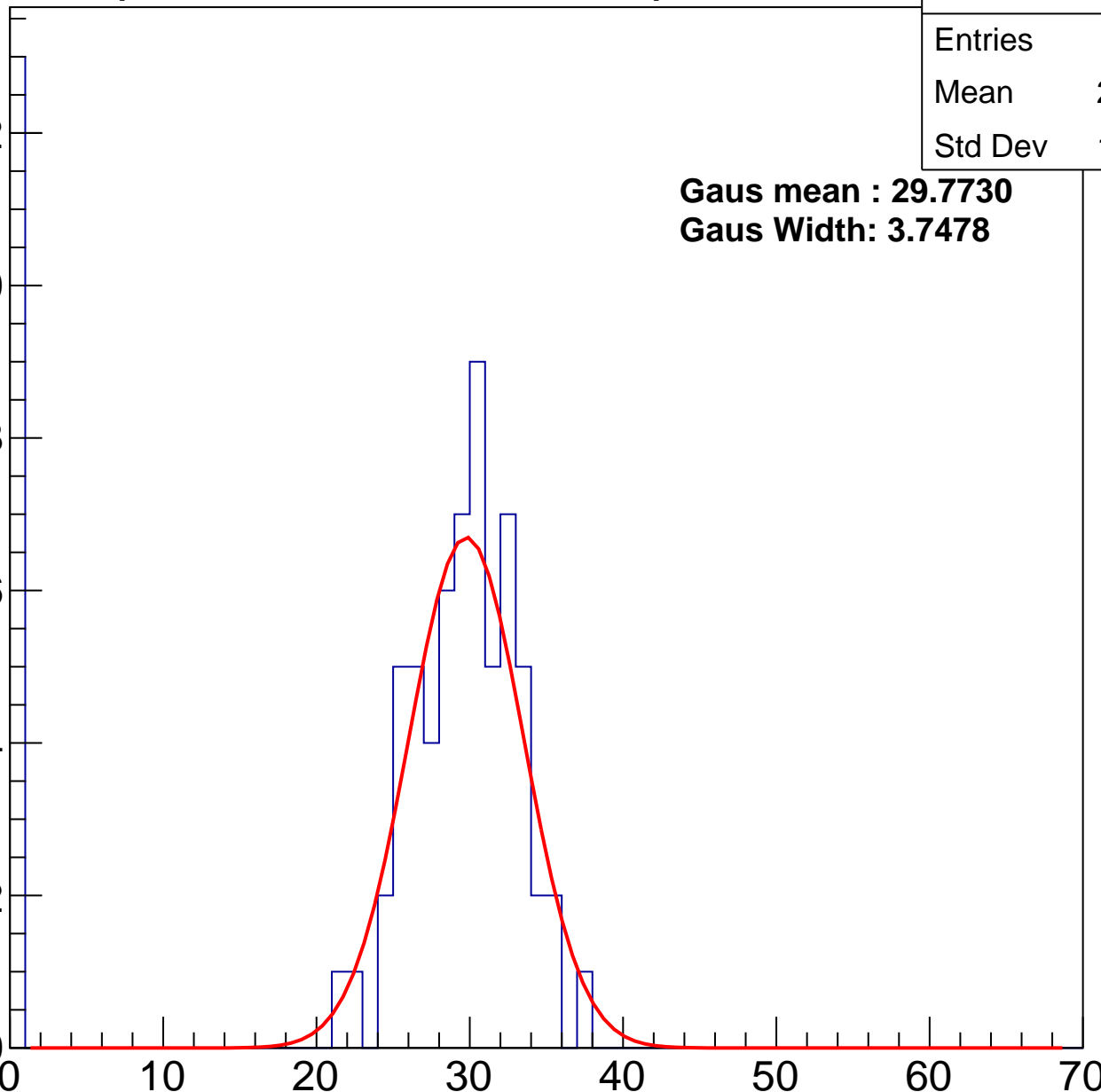
40

50

60

70

ampl



B1L103S, U2-ch4, adc1

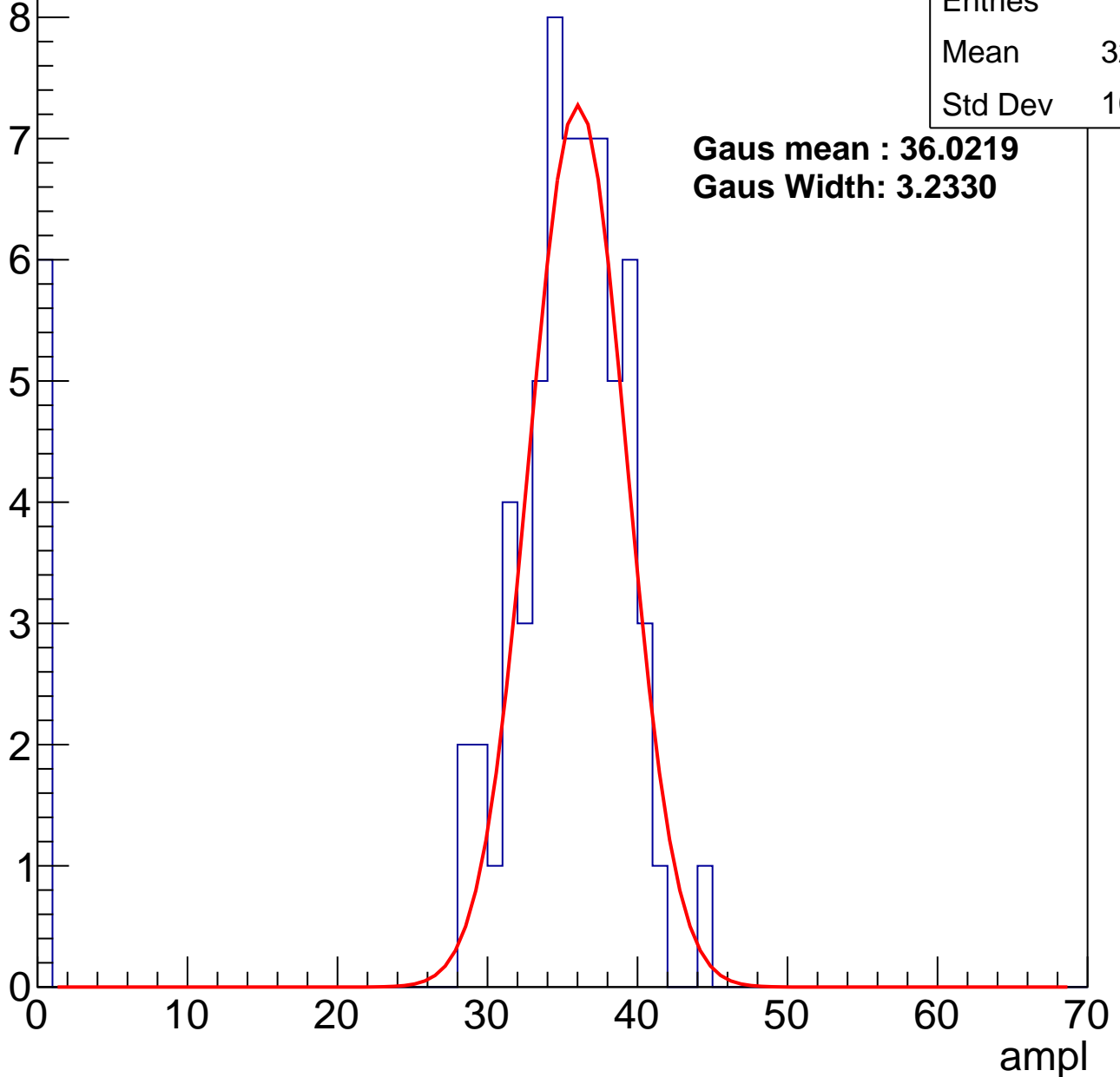
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	32.15
Std Dev	10.49

Gaus mean : 36.0219

Gaus Width: 3.2330



B1L103S, U2-ch4, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	35.33
Std Dev	15.32

Gaus mean : 41.7501

Gaus Width: 3.4569

Entry

10

8

6

4

2

0

0

10

20

30

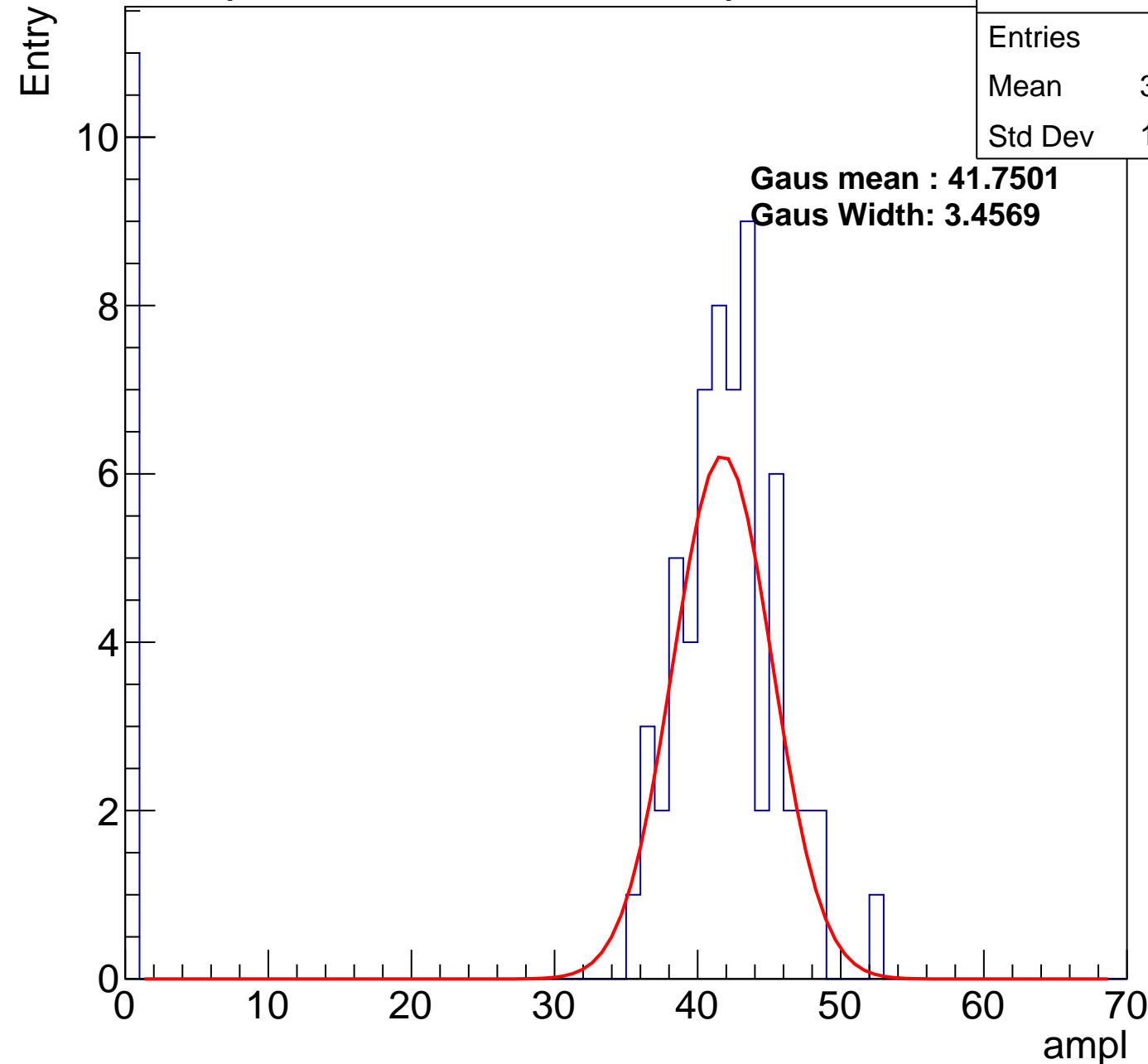
40

50

60

70

ampl

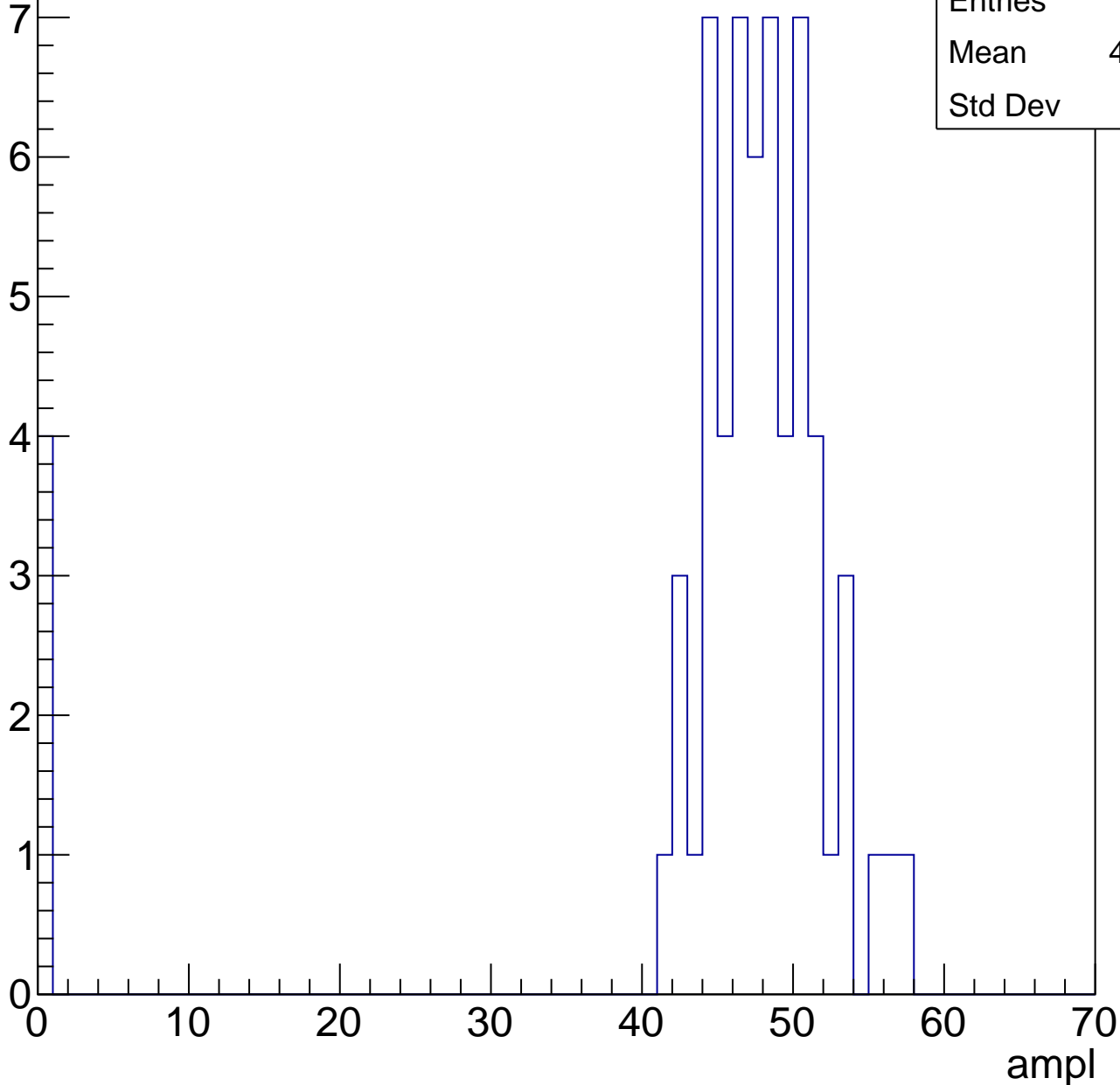


B1L103S, U2-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	44.63
Std Dev	12.2

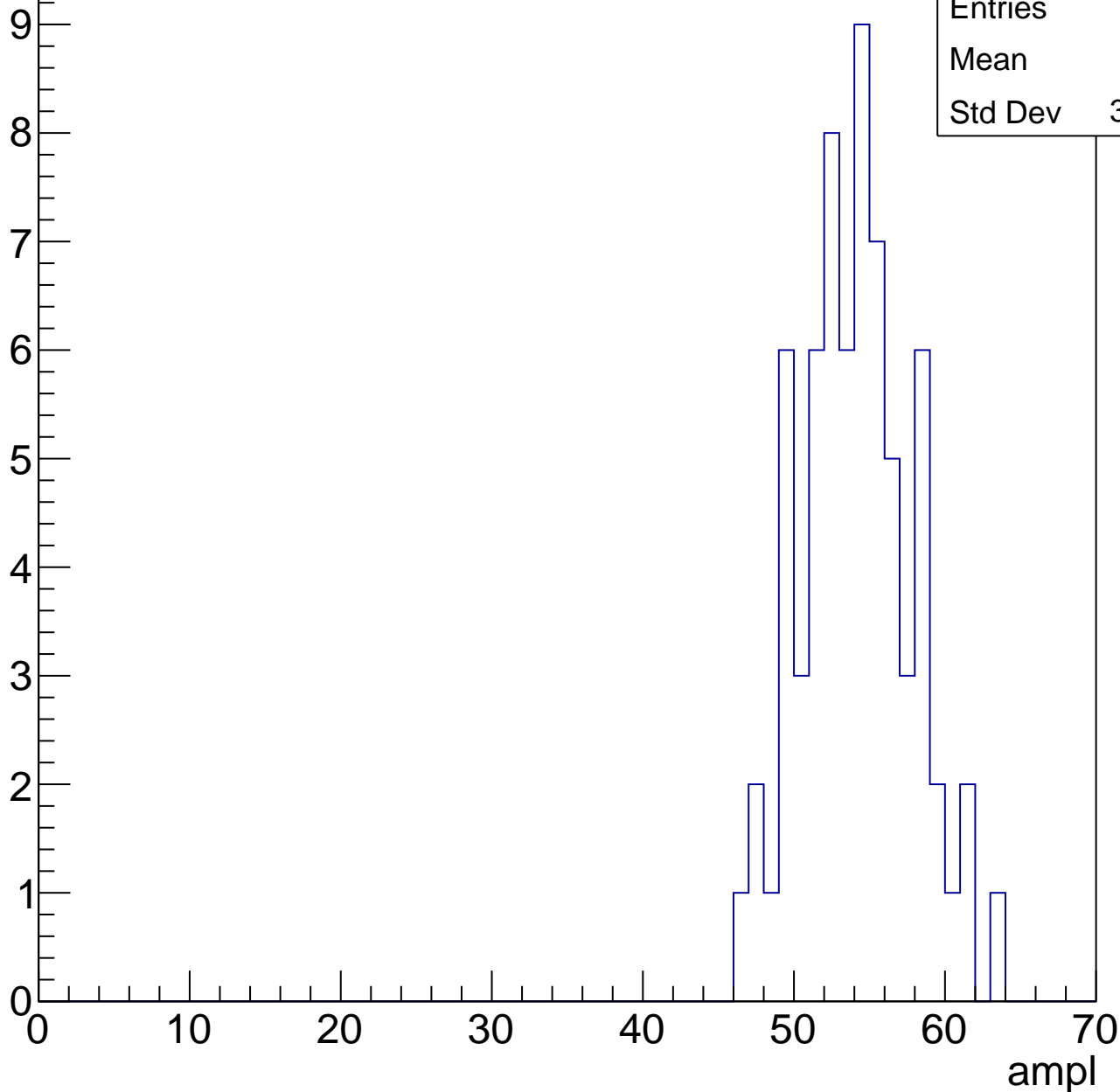


B1L103S, U2-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

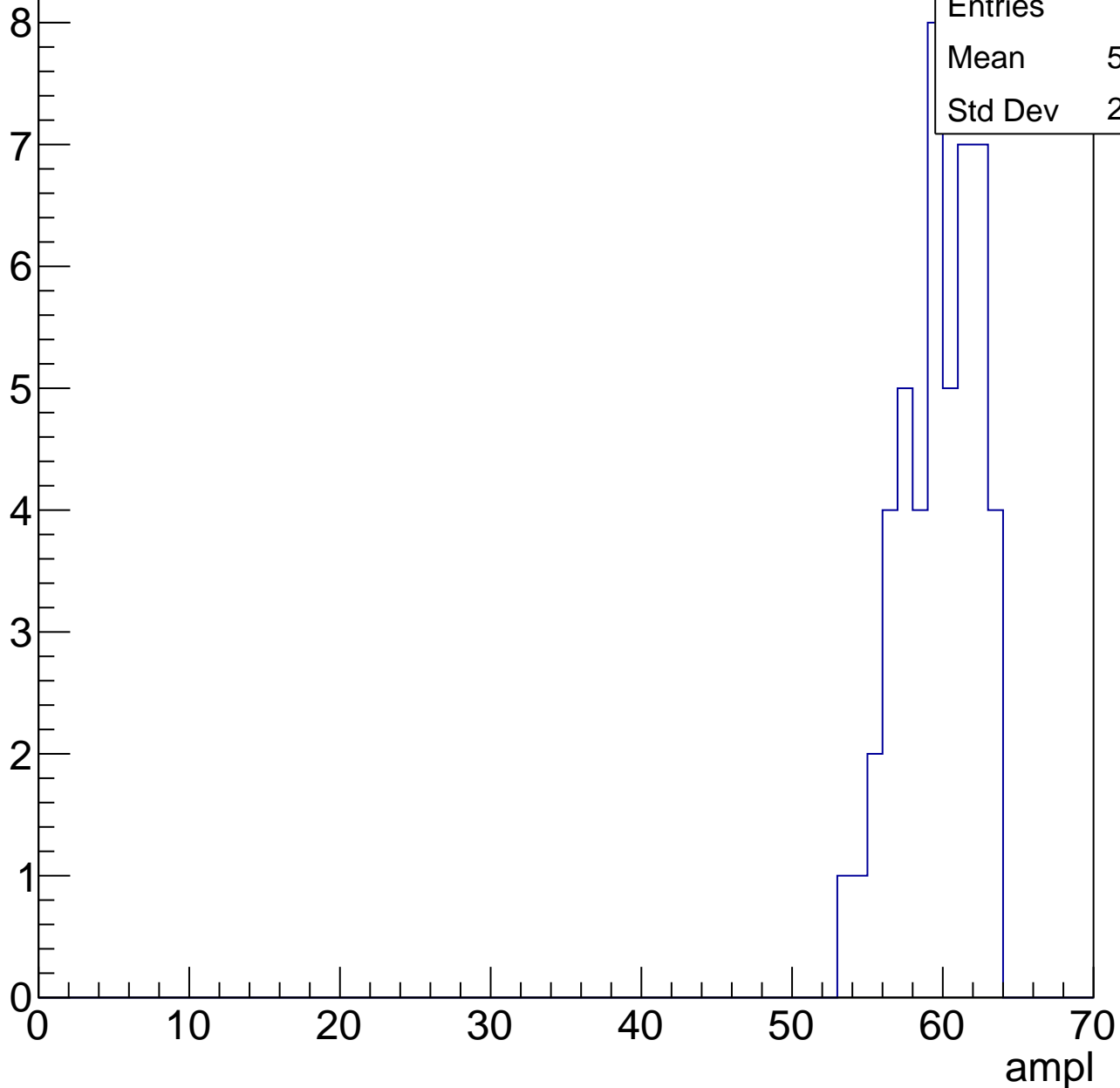
Entries	69
Mean	53.7
Std Dev	3.605



B1L103S, U2-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

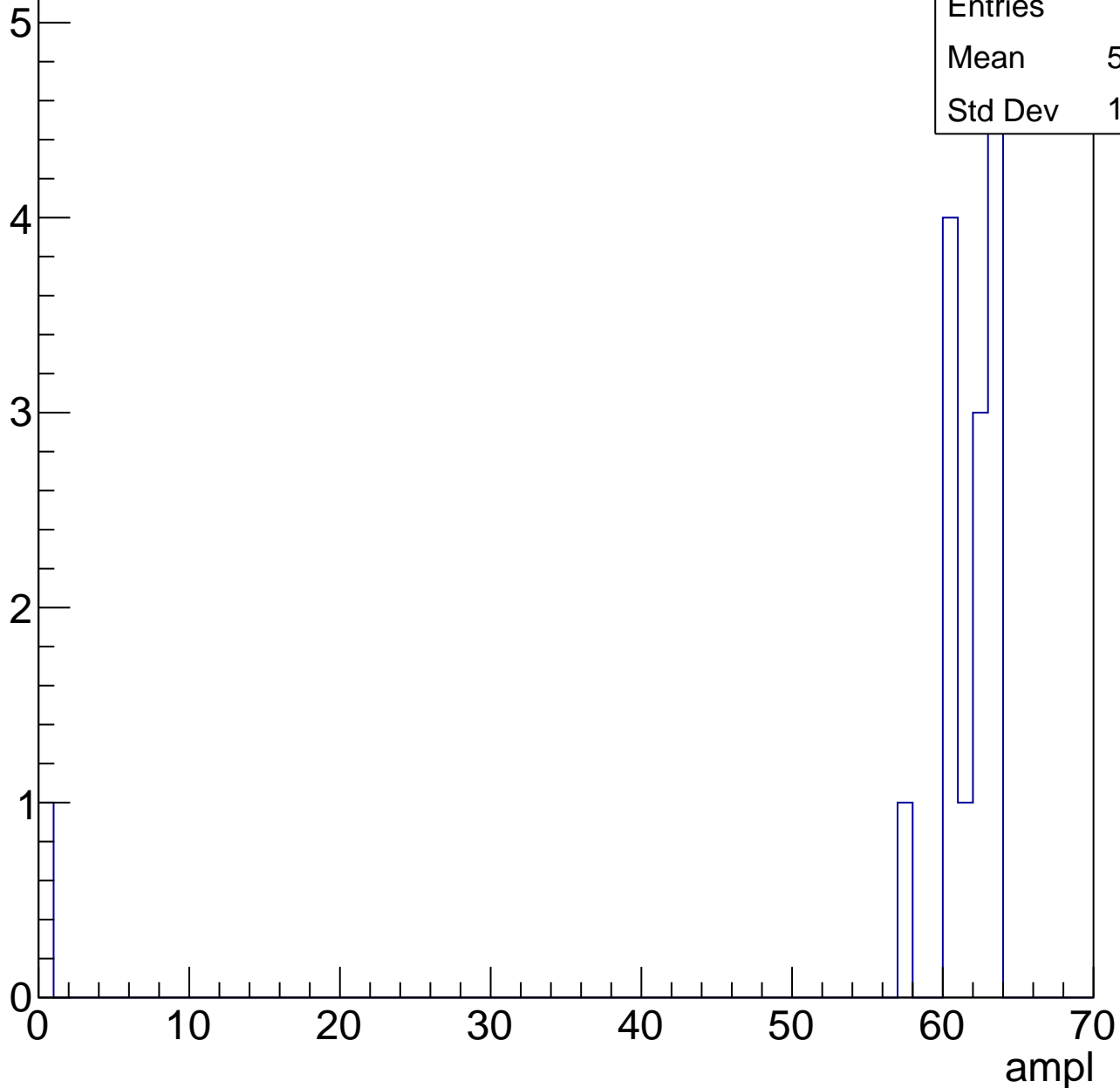


B1L103S, U2-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.27
Std Dev	15.39



B1L103S, U2-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

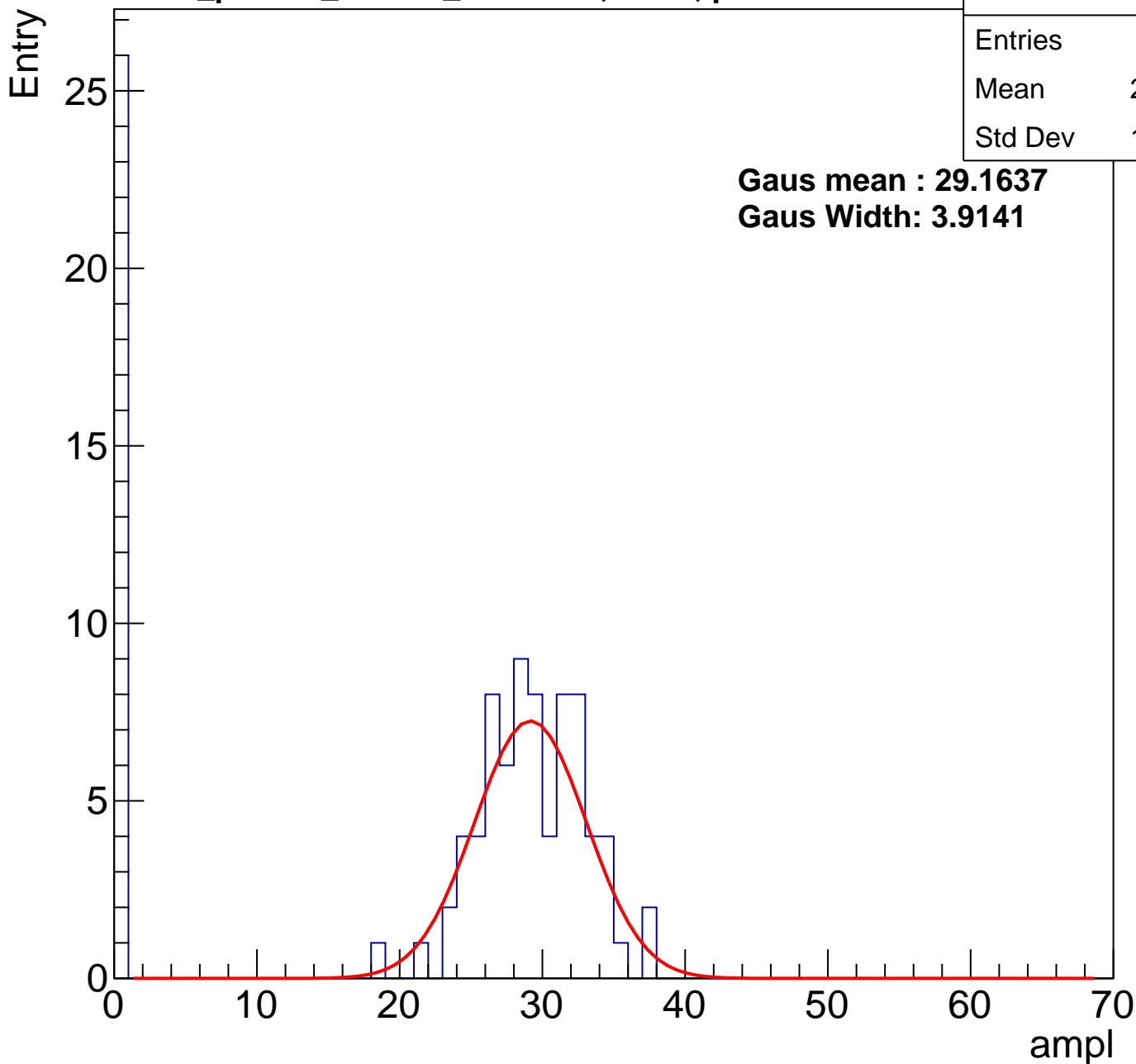
B1L103S, U2-ch5, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	21.36
Std Dev	13.03

Gaus mean : 29.1637

Gaus Width: 3.9141



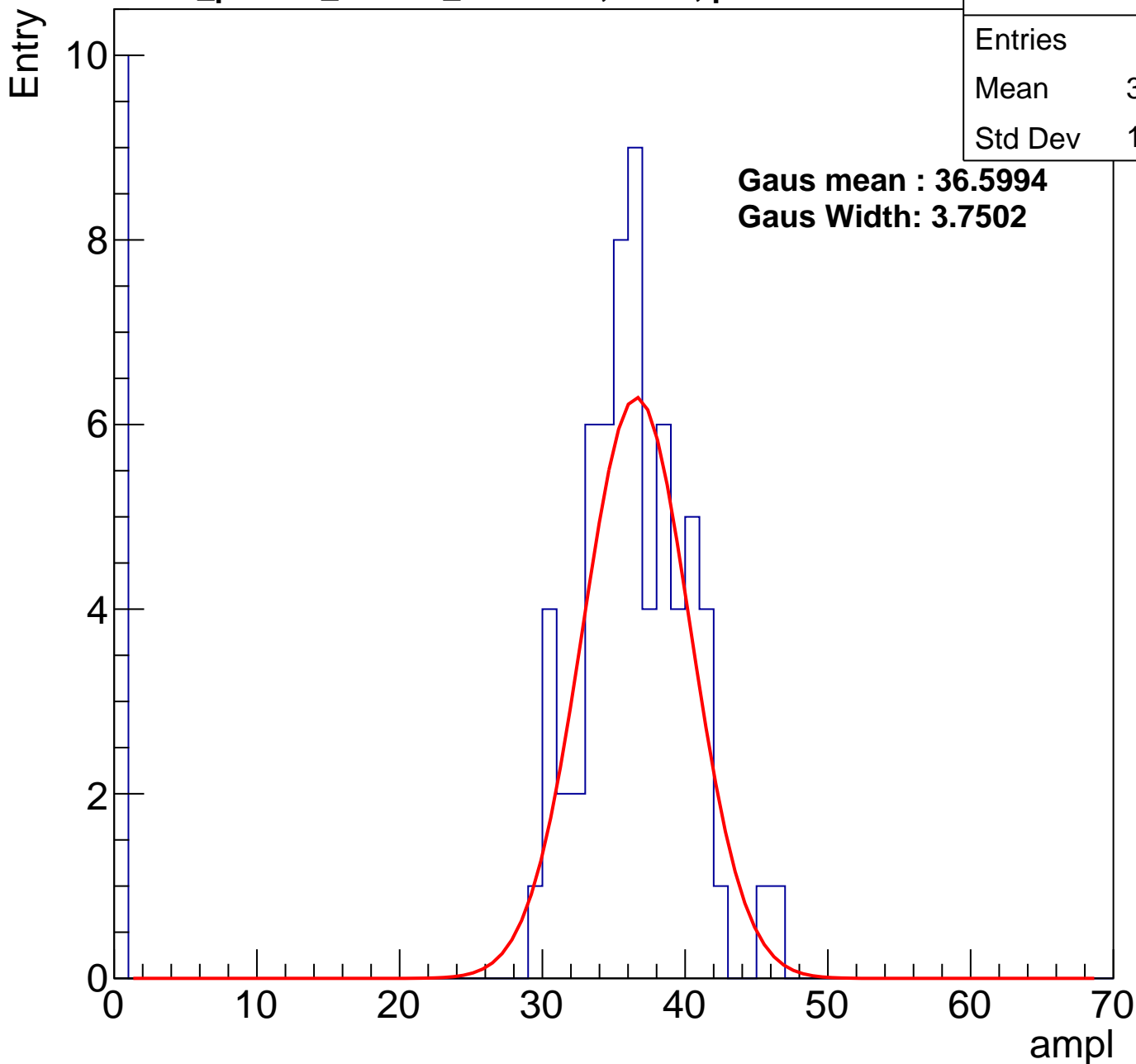
B1L103S, U2-ch5, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.22
Std Dev	12.78

Gaus mean : 36.5994

Gaus Width: 3.7502



B1L103S, U2-ch5, adc2

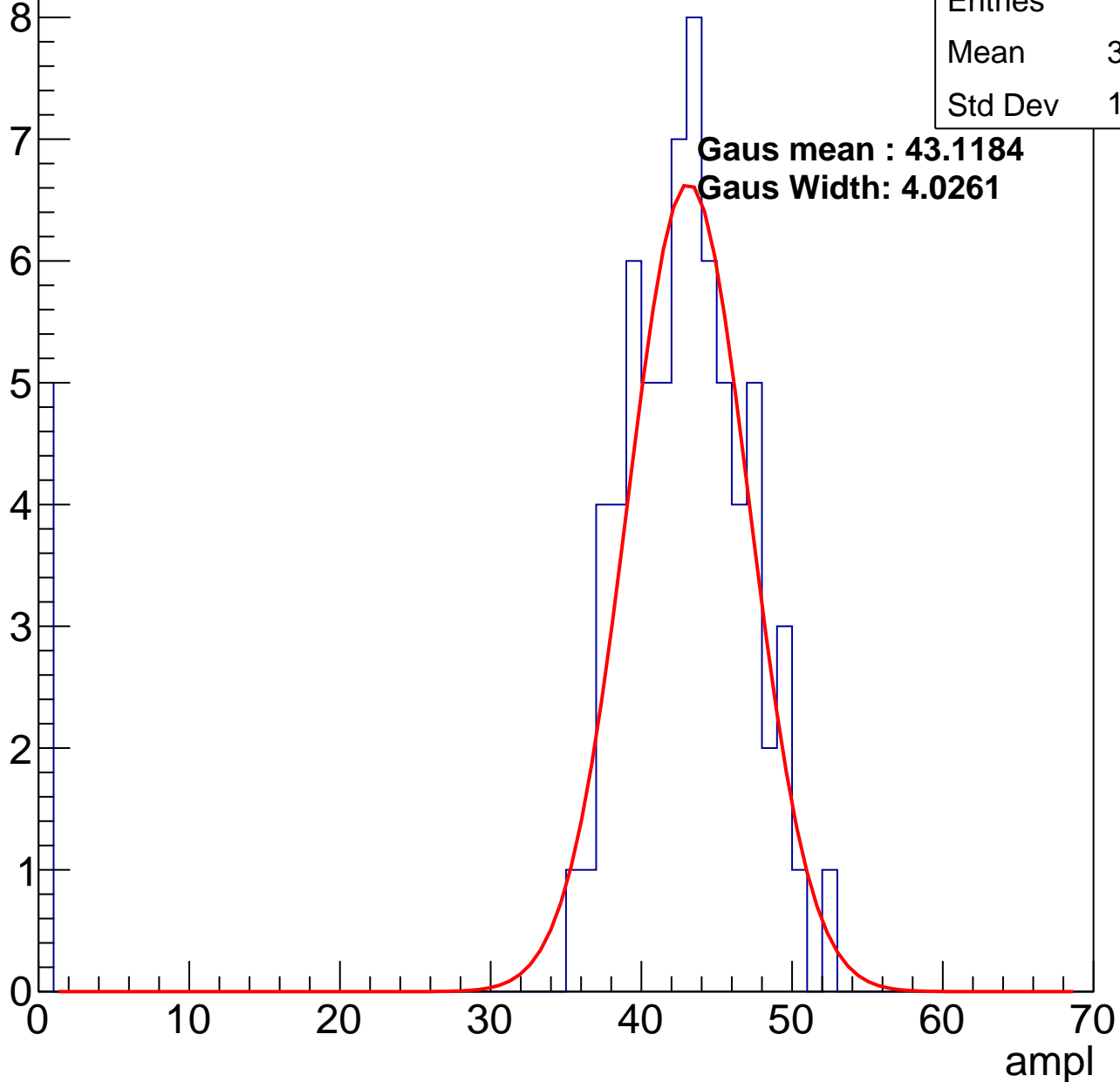
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	39.74
Std Dev	11.36

Gaus mean : 43.1184

Gaus Width: 4.0261



B1L103S, U2-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 61

Mean 48.43

Std Dev 3.341

ampl

0

10

20

30

40

50

60

70

7

6

5

4

3

2

1

0

10

20

30

40

50

60

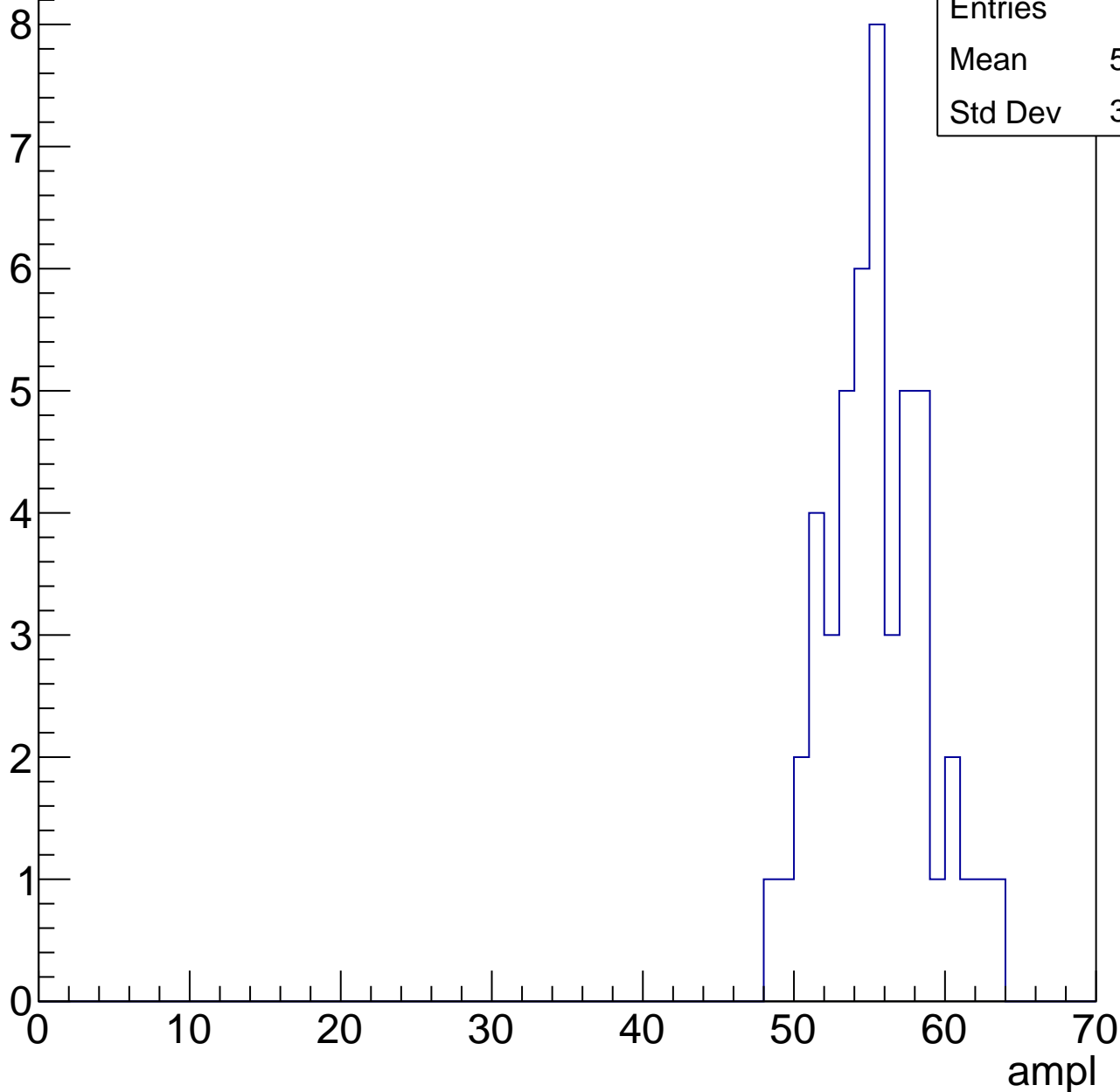
70

B1L103S, U2-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

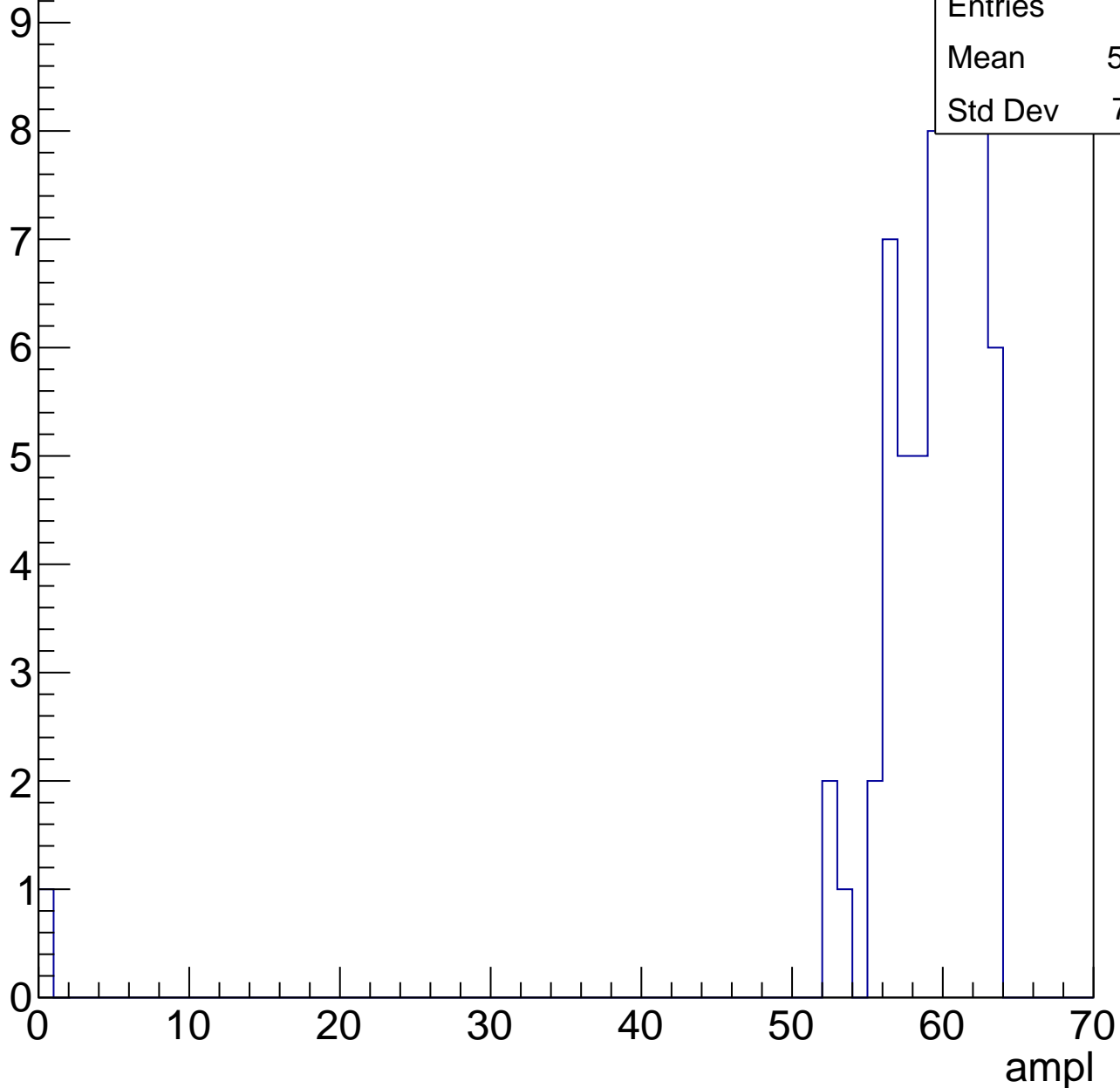
Entries	49
Mean	54.98
Std Dev	3.304



B1L103S, U2-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

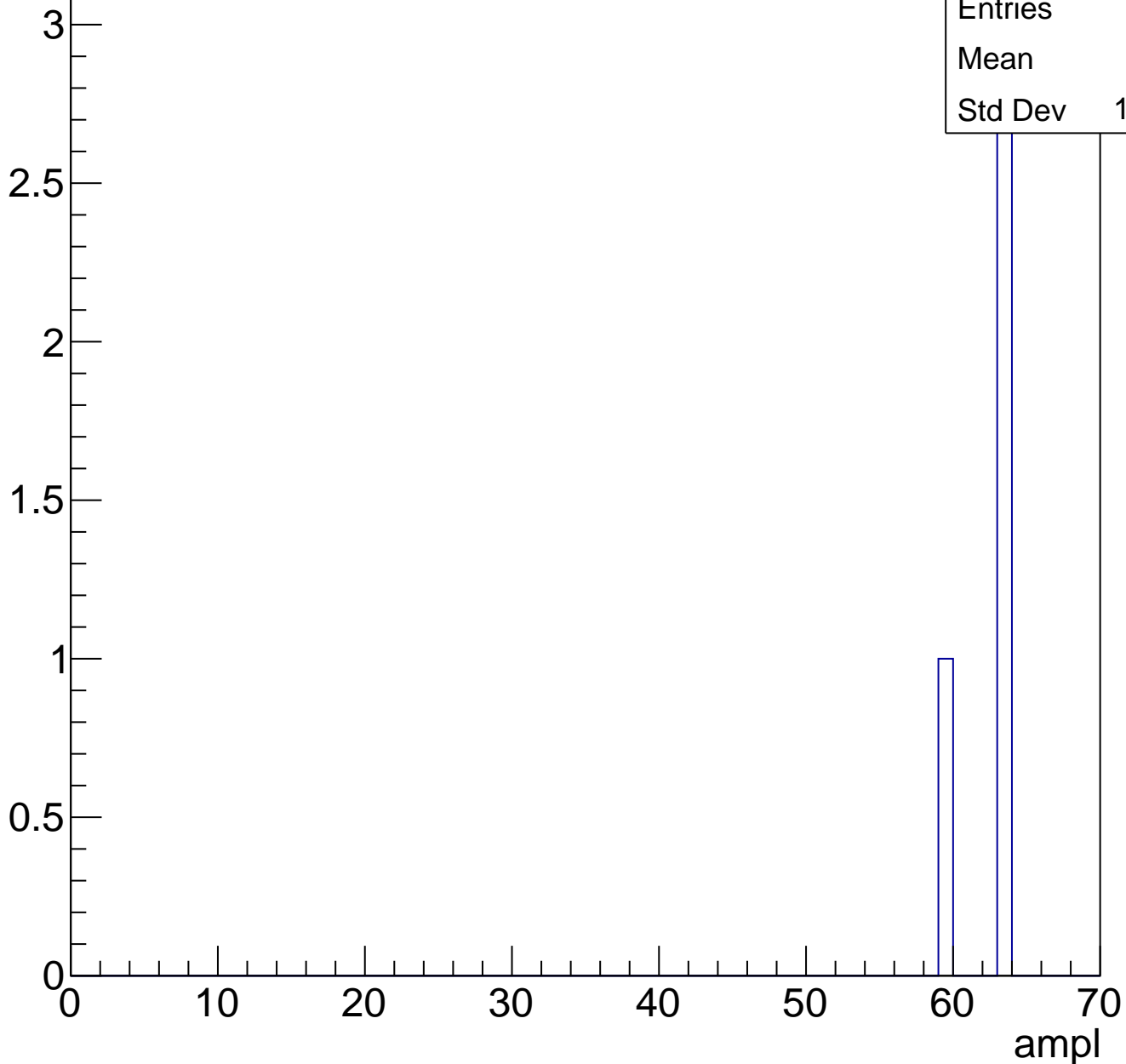
Entry



B1L103S, U2-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U2-ch6, adc0

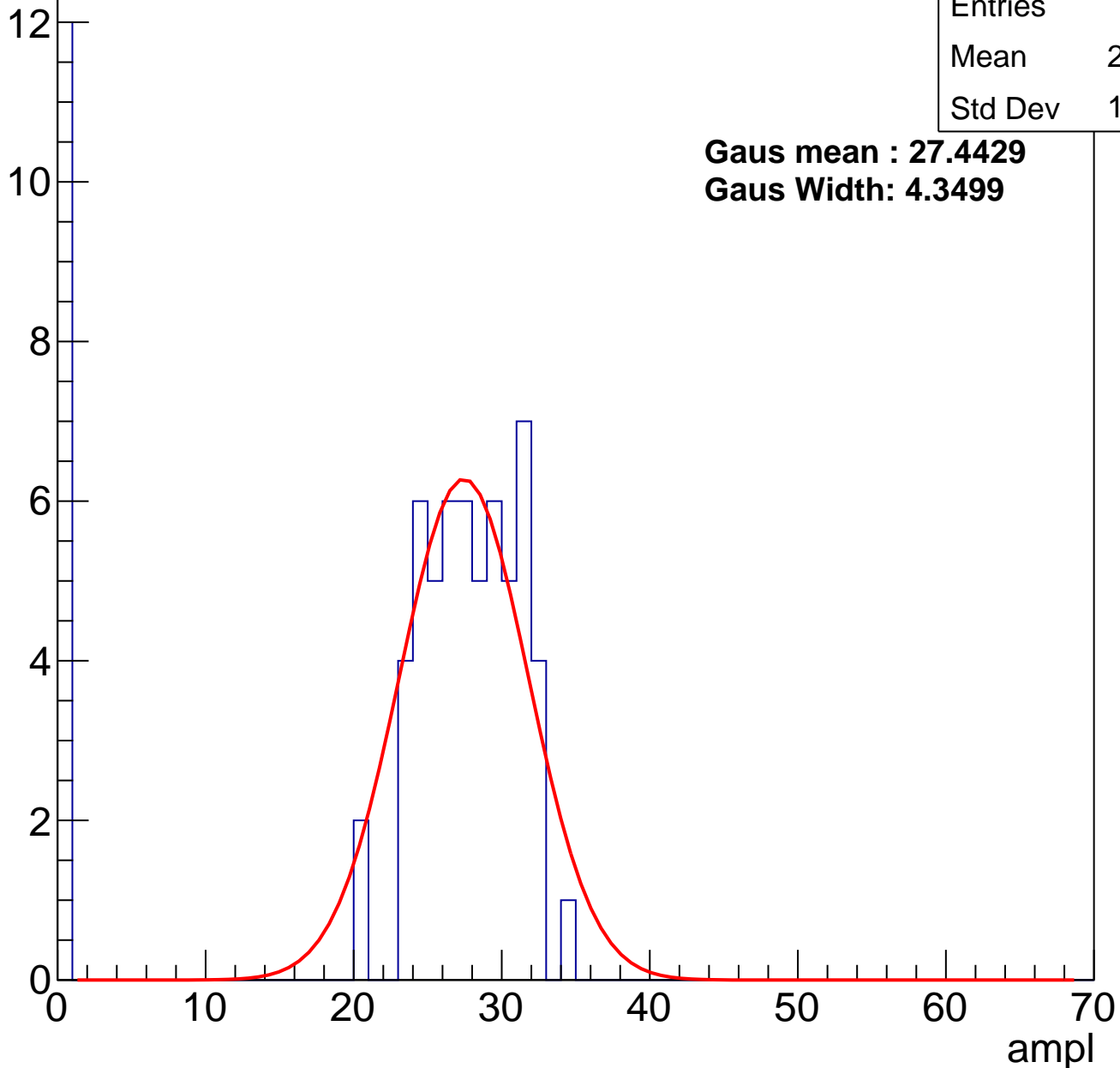
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	22.64
Std Dev	10.78

Gaus mean : 27.4429

Gaus Width: 4.3499

Entry



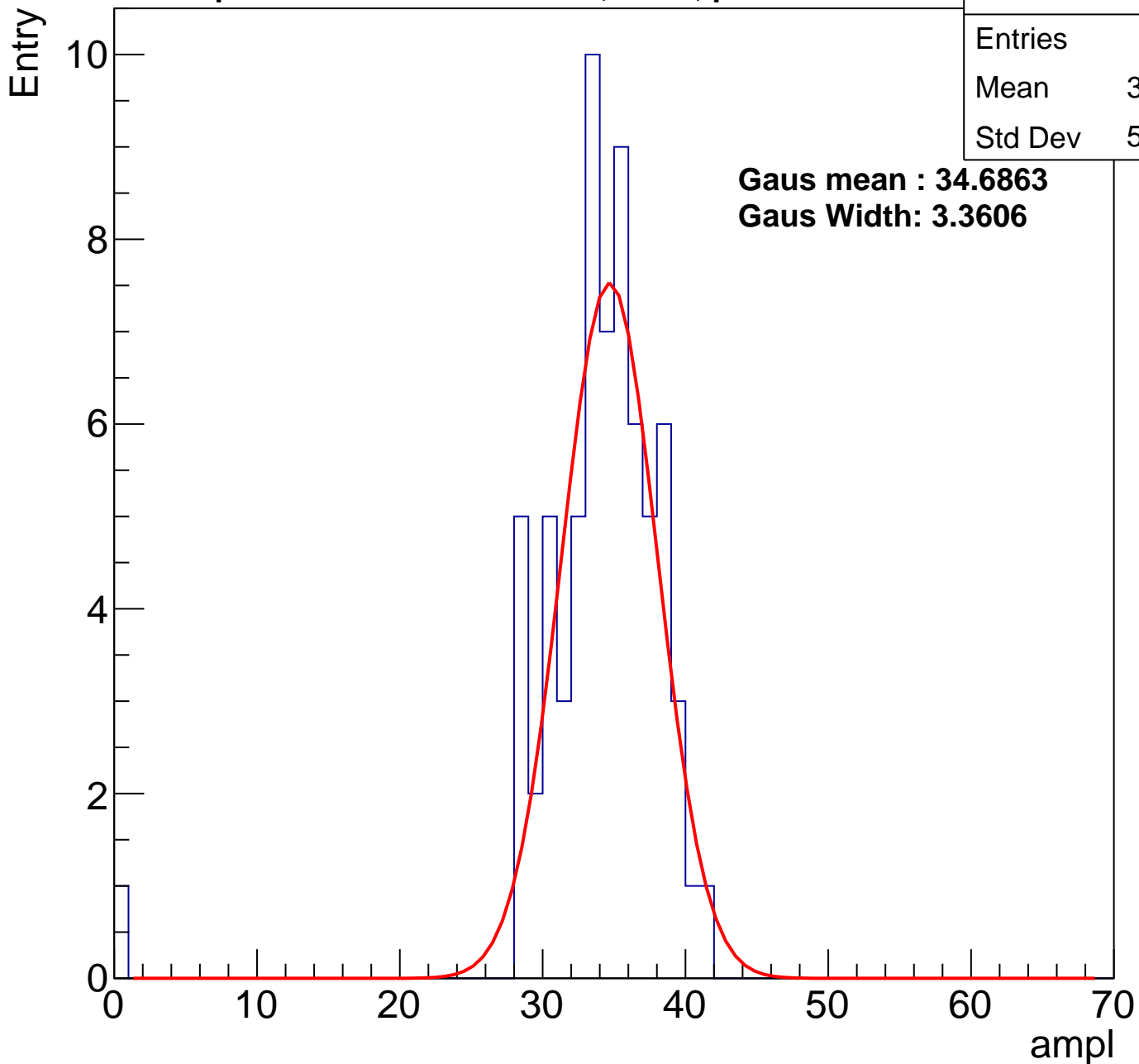
B1L103S, U2-ch6, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	33.49
Std Dev	5.163

Gaus mean : 34.6863

Gaus Width: 3.3606



B1L103S, U2-ch6, adc2

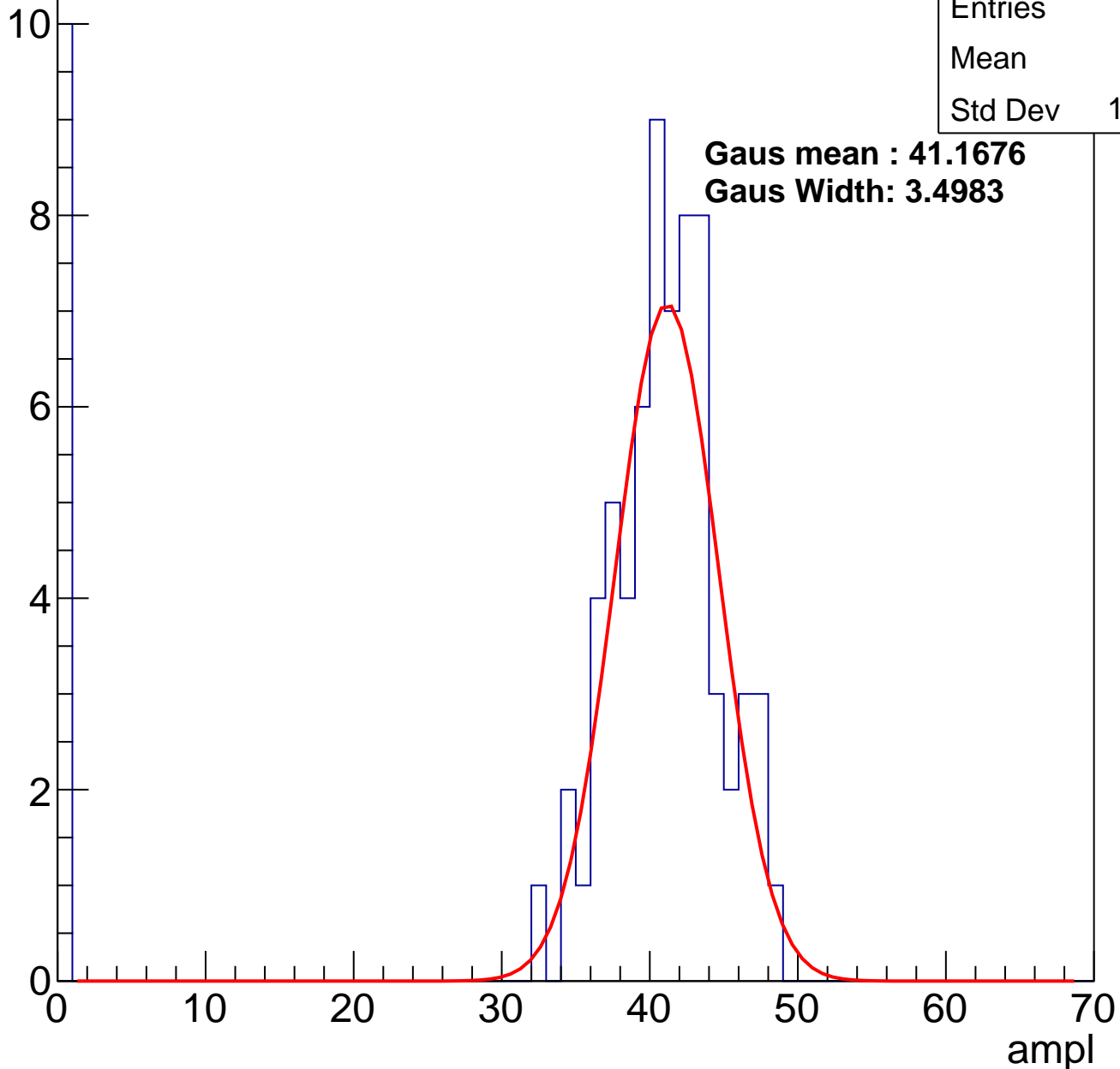
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	35.4
Std Dev	14.05

Gaus mean : 41.1676

Gaus Width: 3.4983

Entry

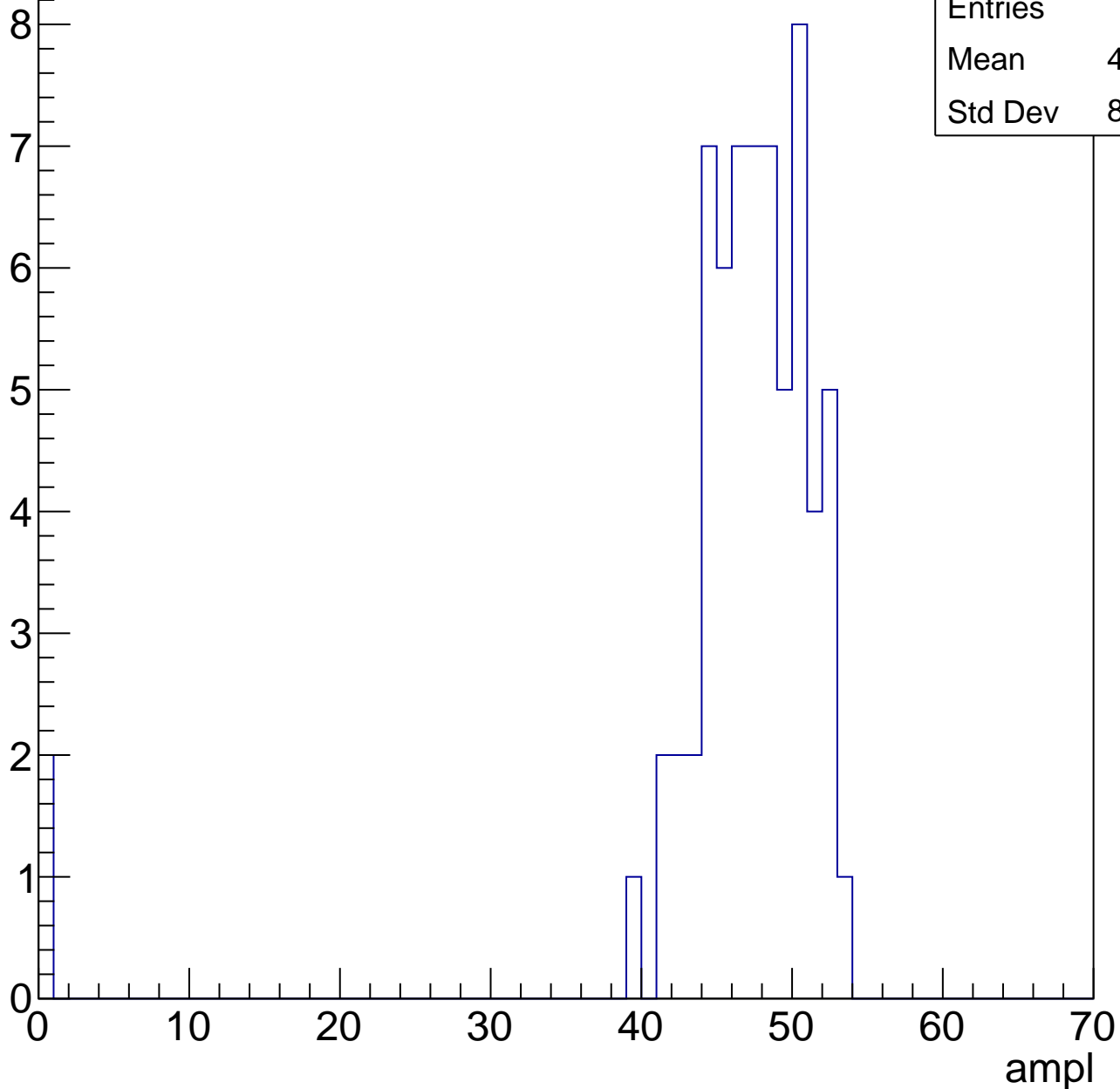


B1L103S, U2-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

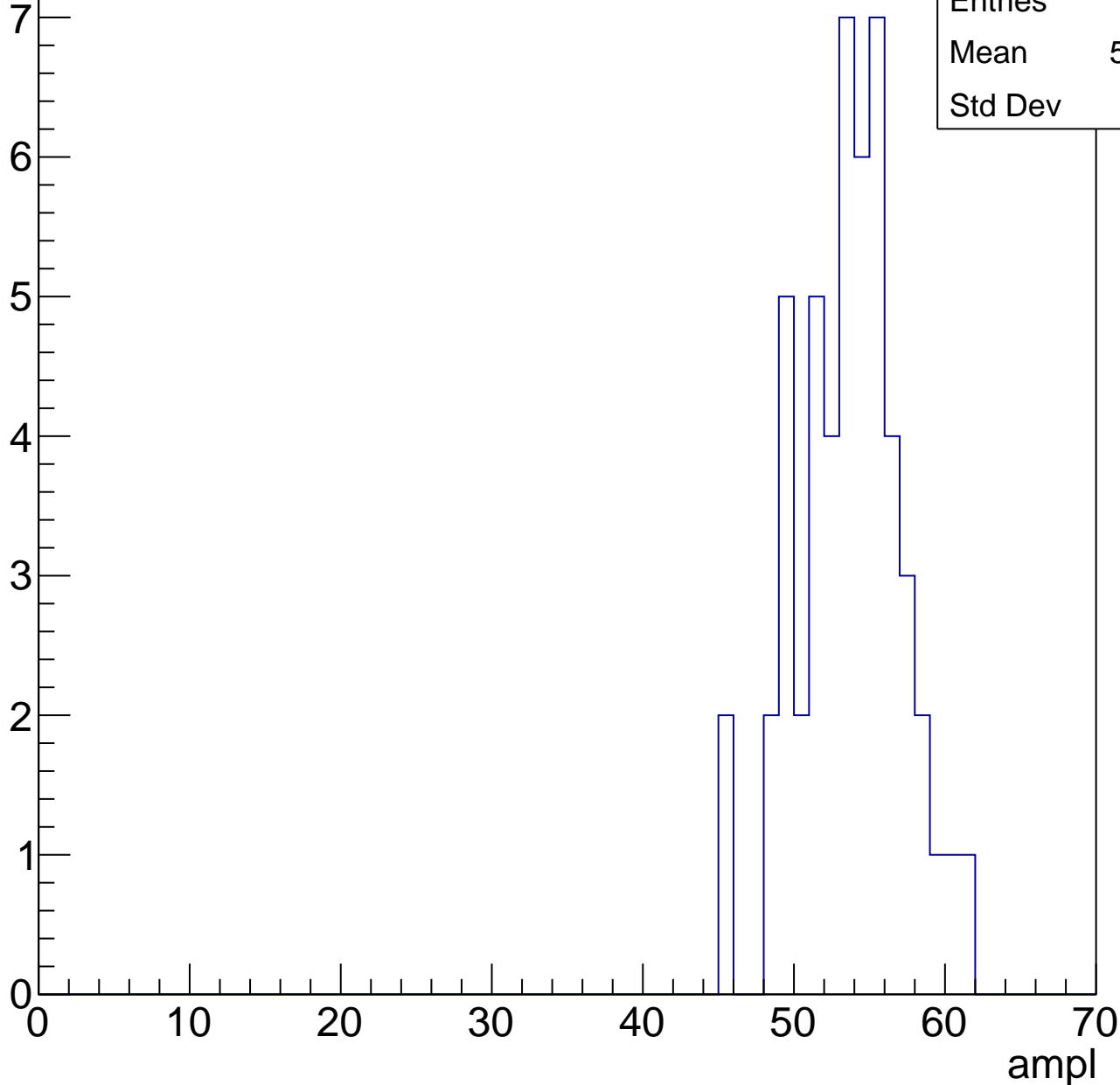
Entries	66
Mean	45.73
Std Dev	8.658



B1L103S, U2-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

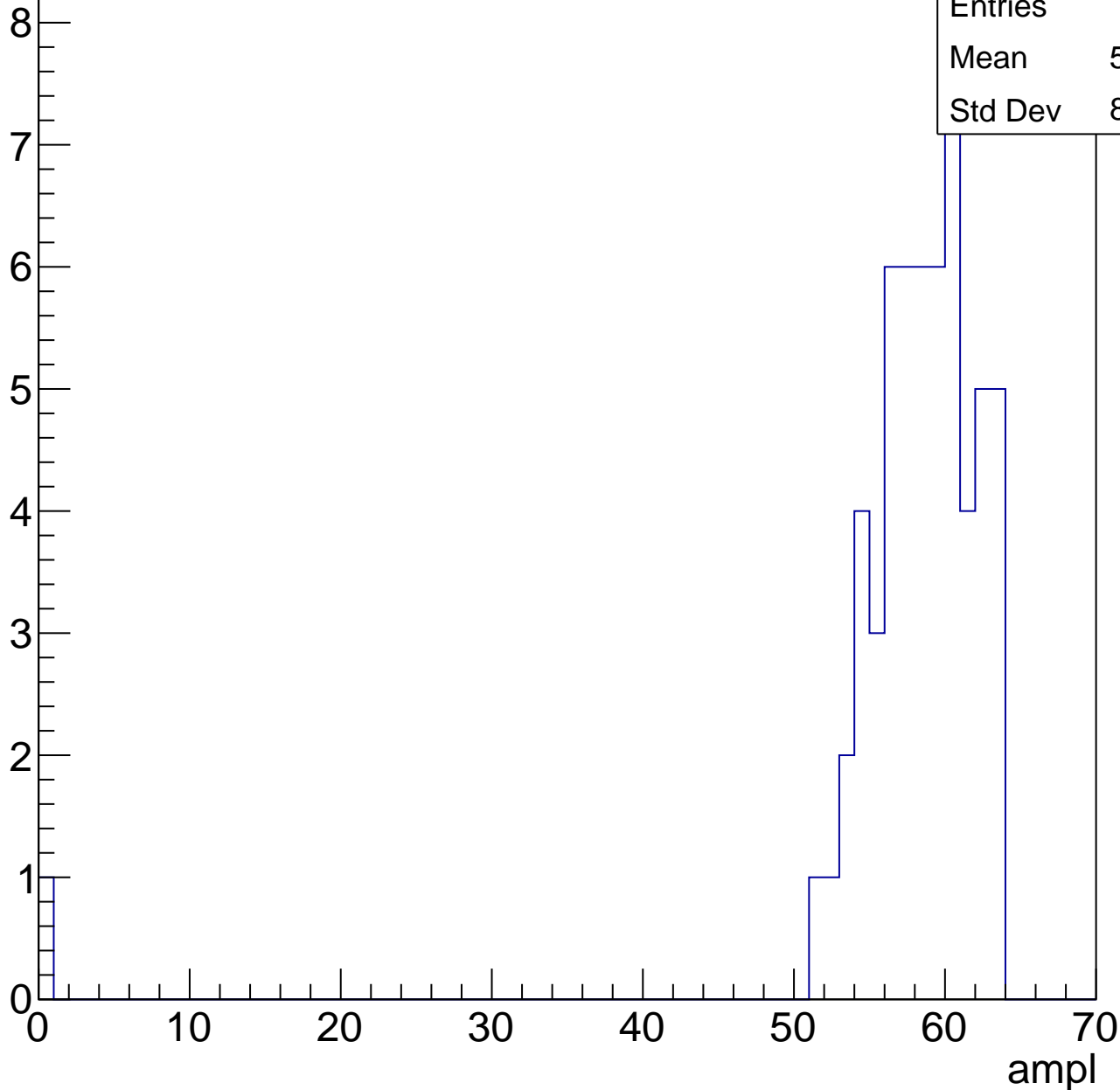


B1L103S, U2-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.22
Std Dev	8.162

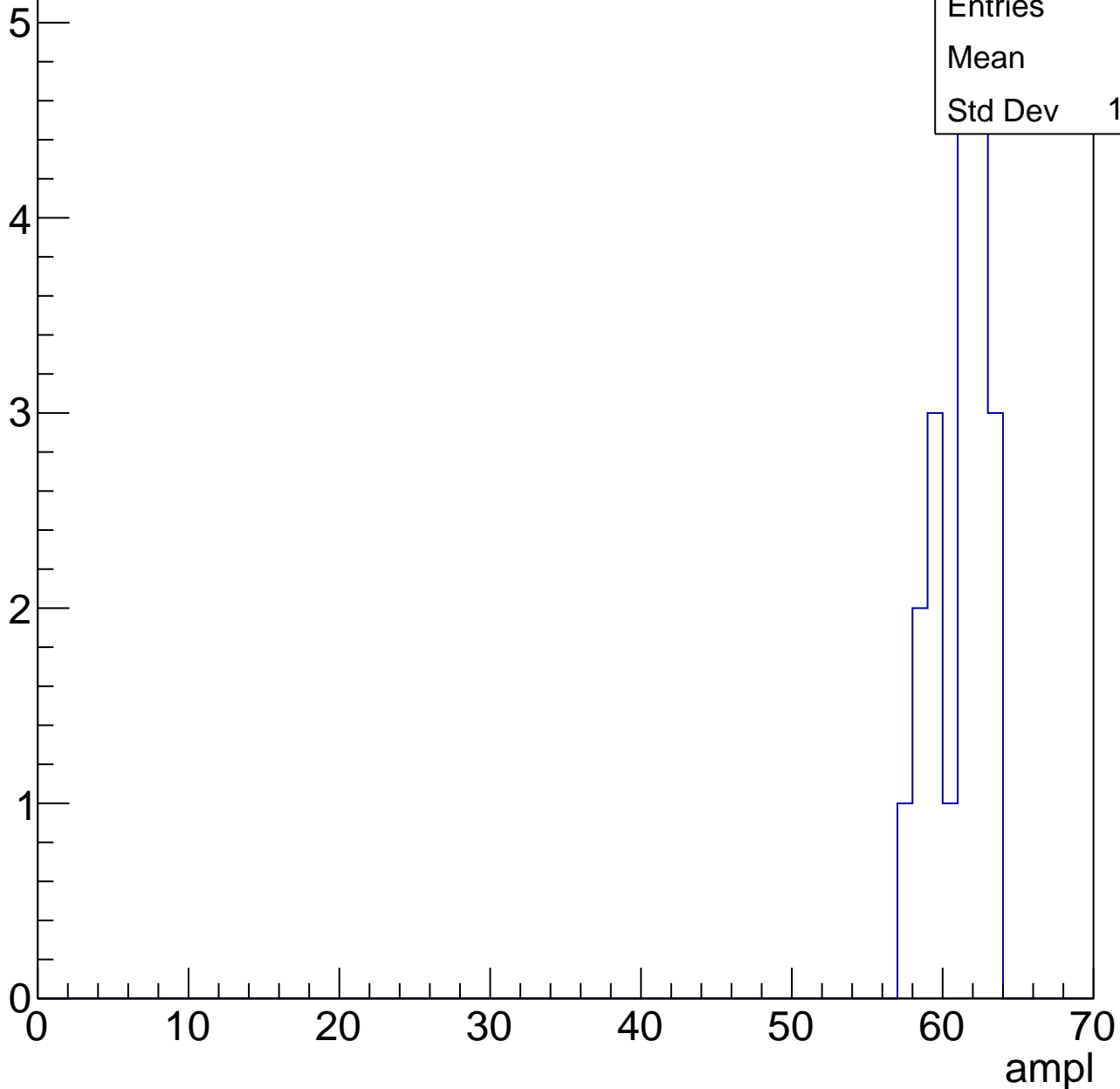


B1L103S, U2-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	60.7
Std Dev	1.764

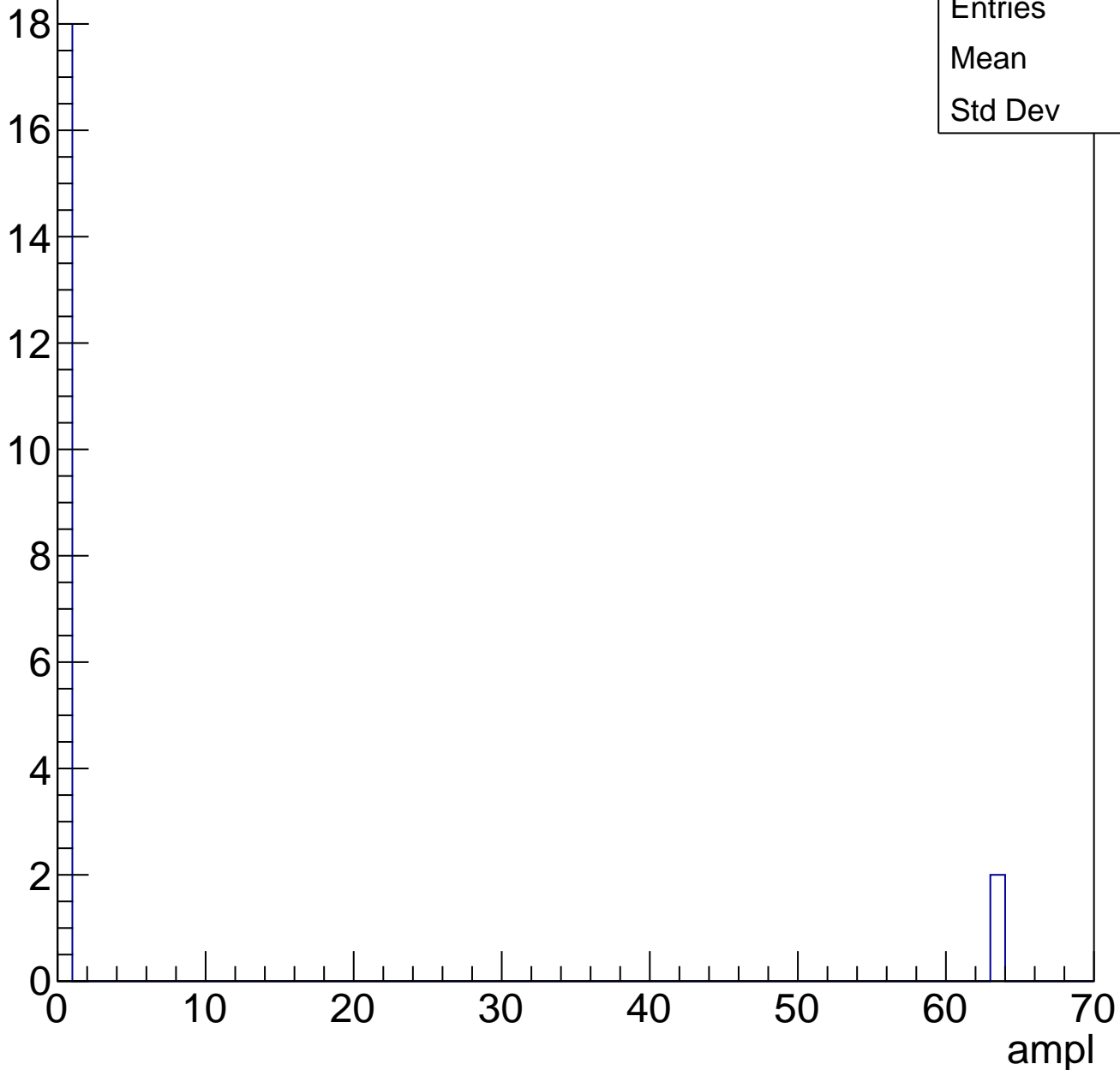


B1L103S, U2-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.3
Std Dev	18.9

Entry



B1L103S, U2-ch7, adc0

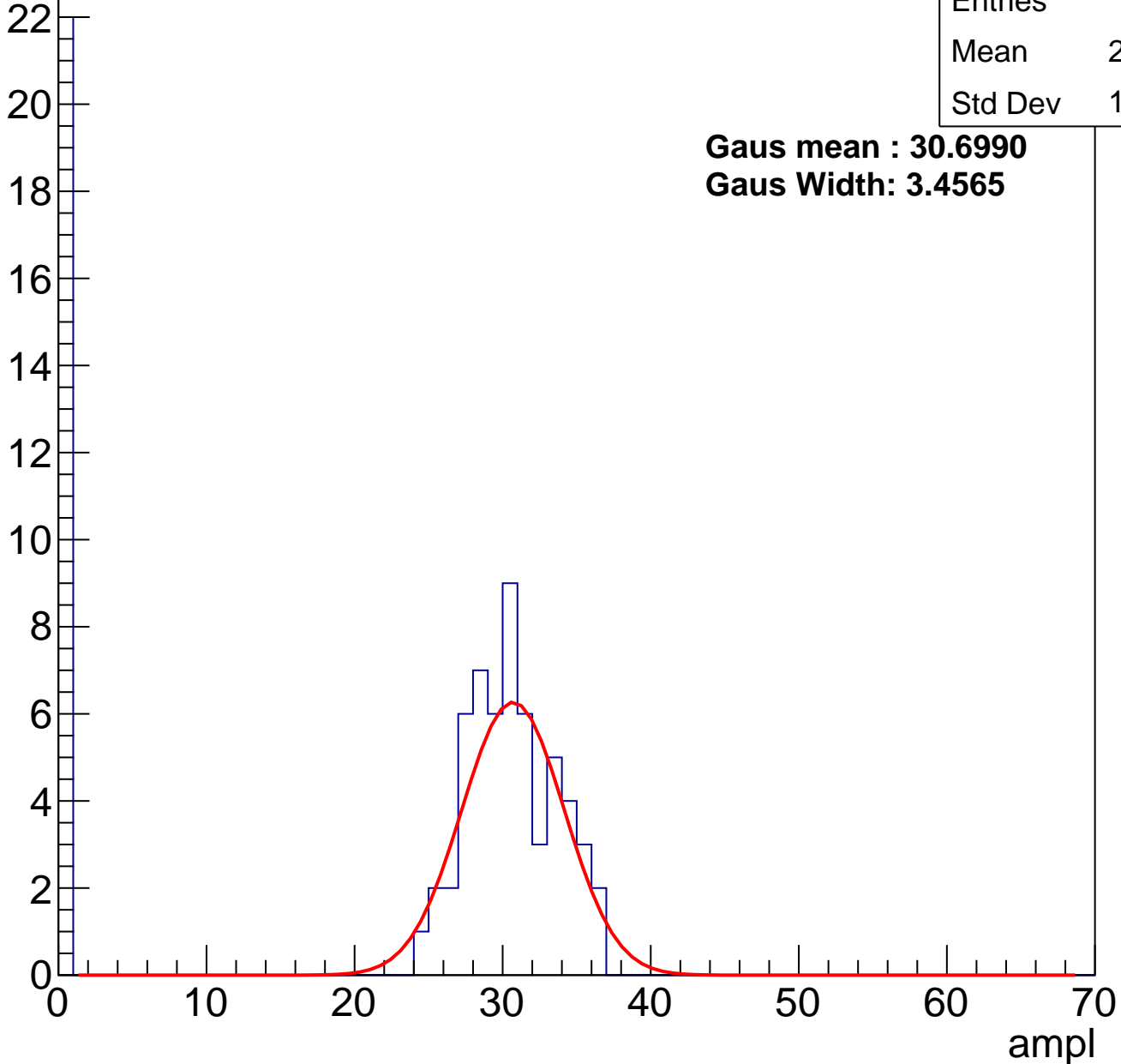
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	21.64
Std Dev	13.79

Gaus mean : 30.6990

Gaus Width: 3.4565

Entry



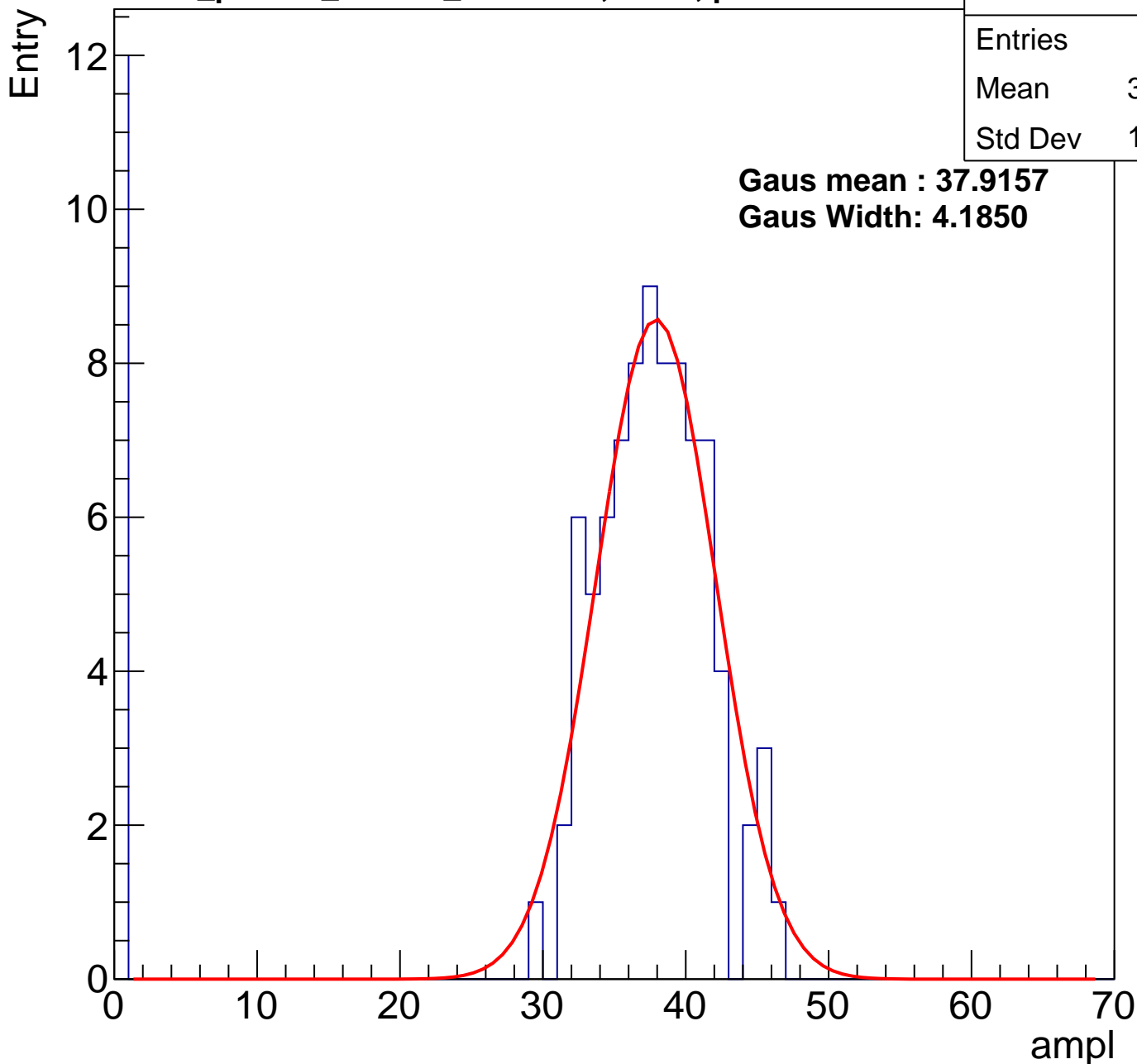
B1L103S, U2-ch7, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	32.69
Std Dev	12.82

Gaus mean : 37.9157

Gaus Width: 4.1850



B1L103S, U2-ch7, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	40.21
Std Dev	12.73

Gaus mean : 44.3325

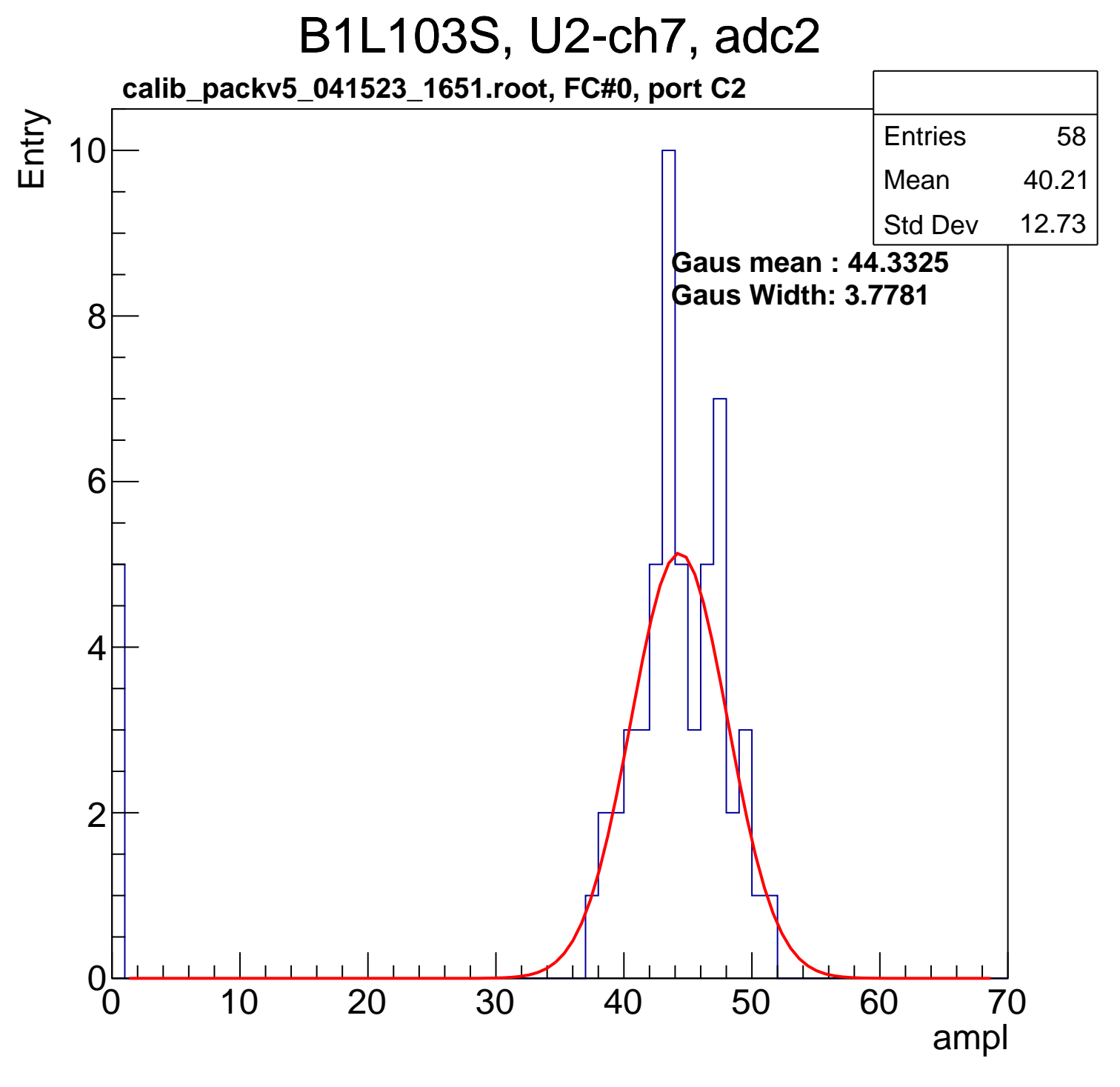
Gaus Width: 3.7781

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

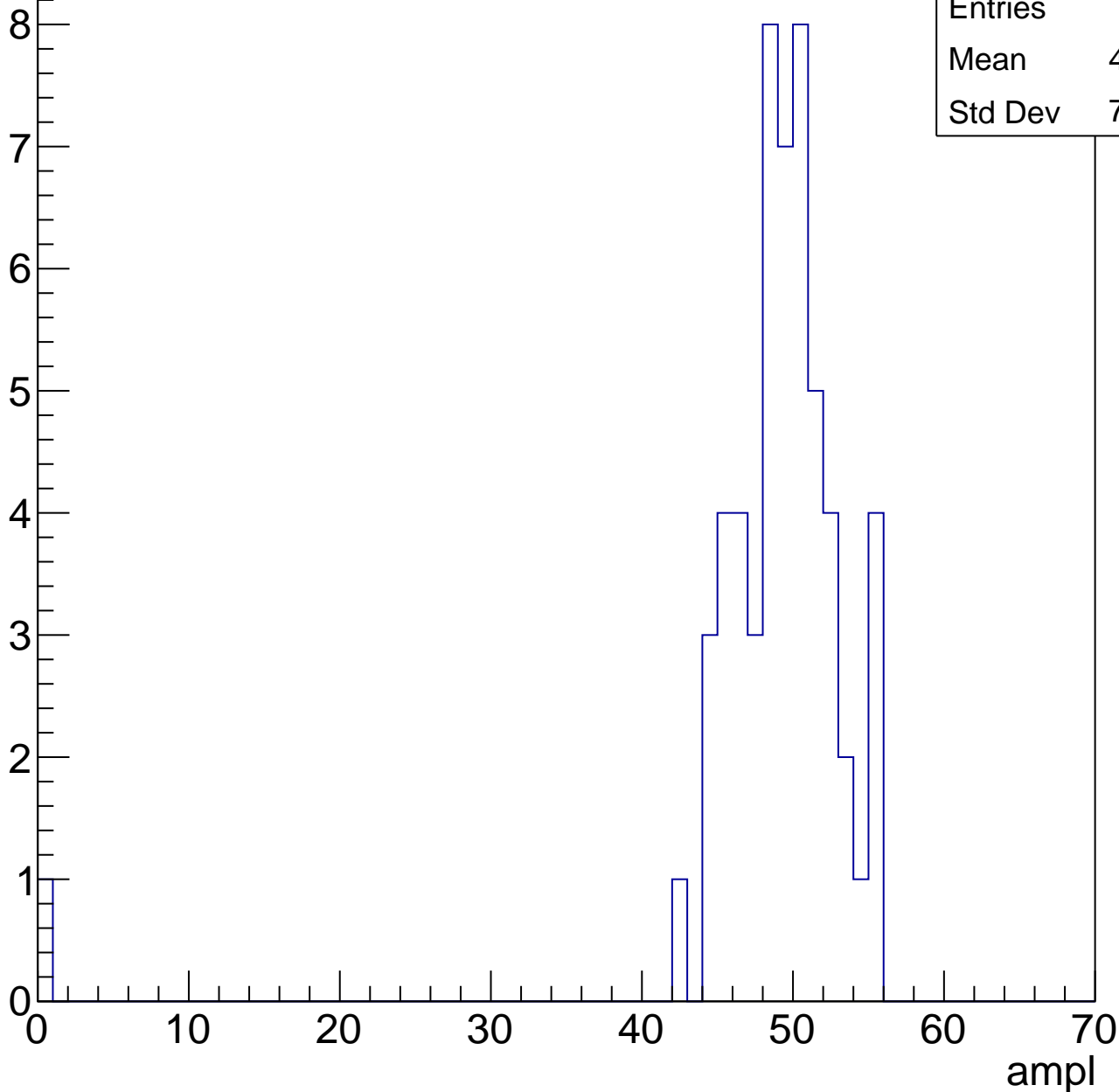


B1L103S, U2-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	48.16
Std Dev	7.226

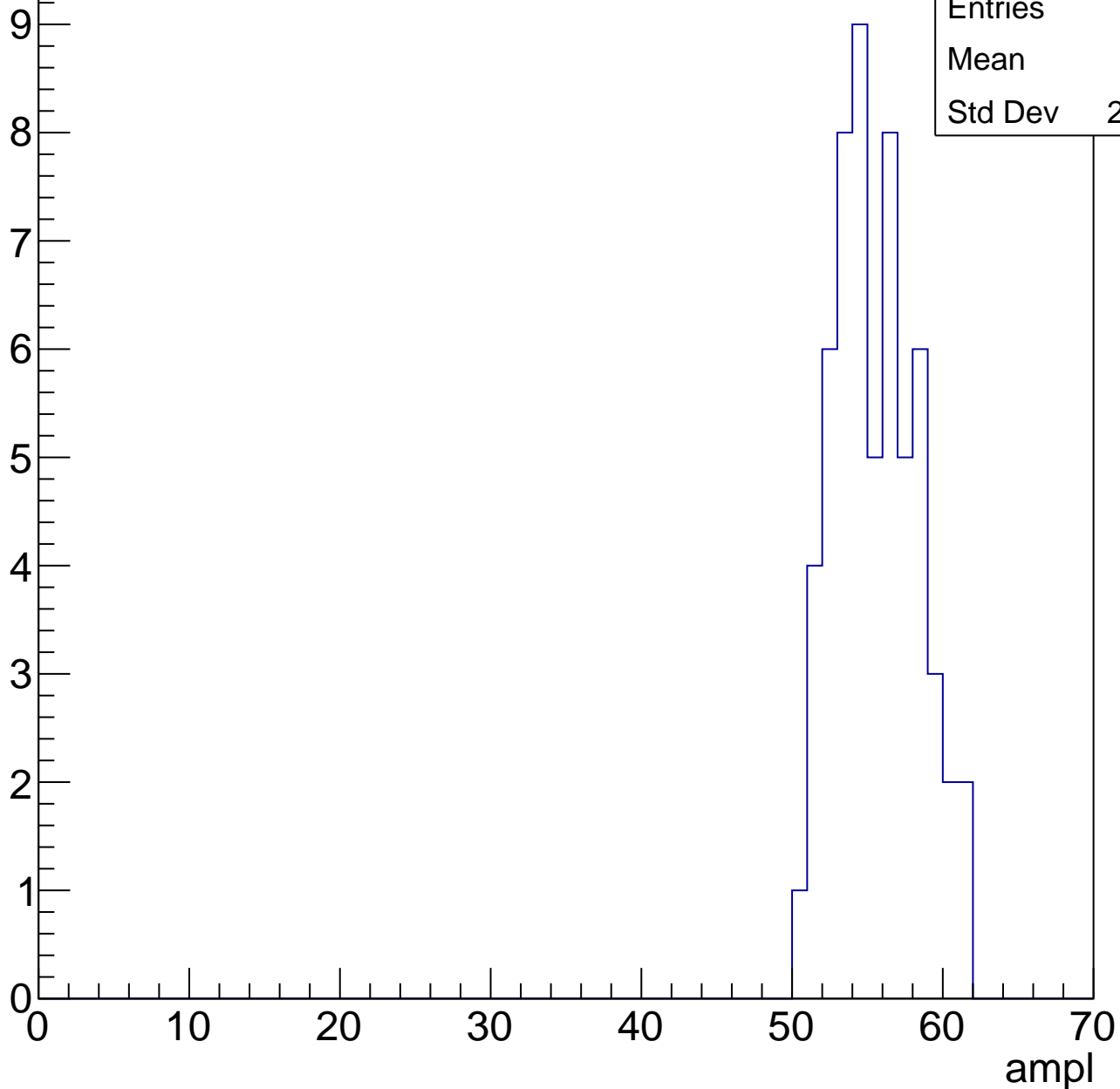


B1L103S, U2-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	55.1
Std Dev	2.717



B1L103S, U2-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

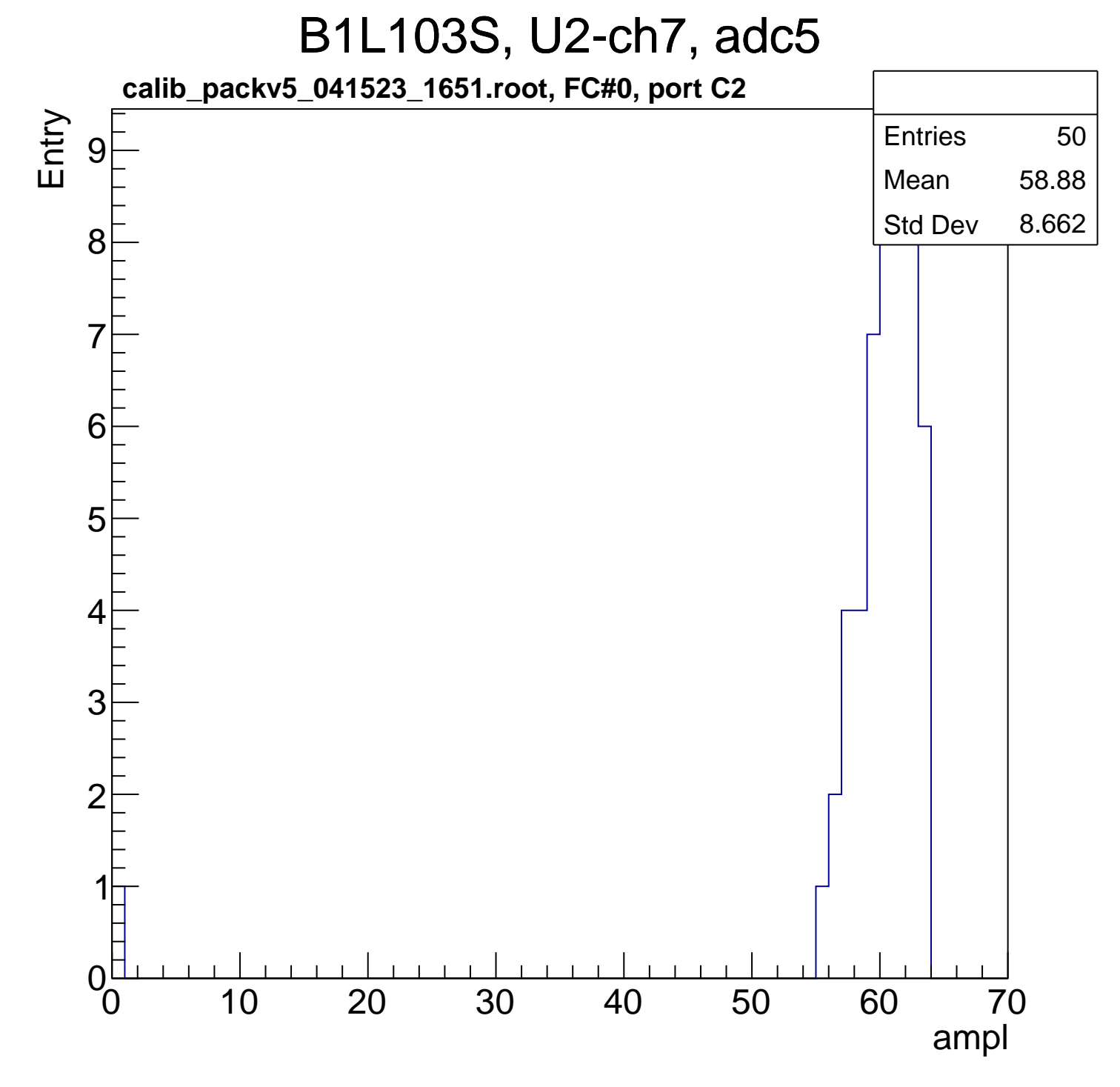
Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.88
Std Dev	8.662

ampl

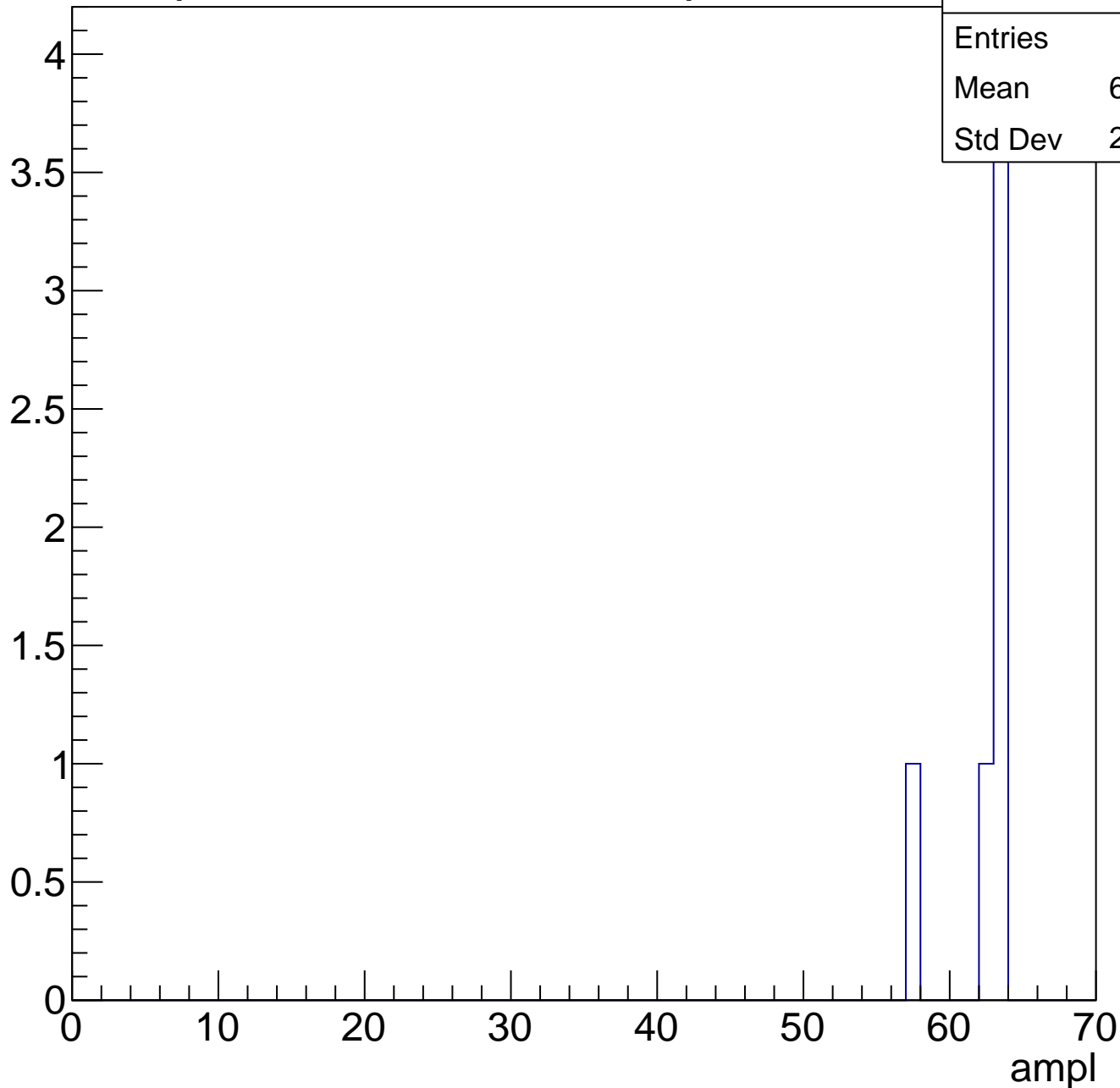
0 10 20 30 40 50 60 70



B1L103S, U2-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

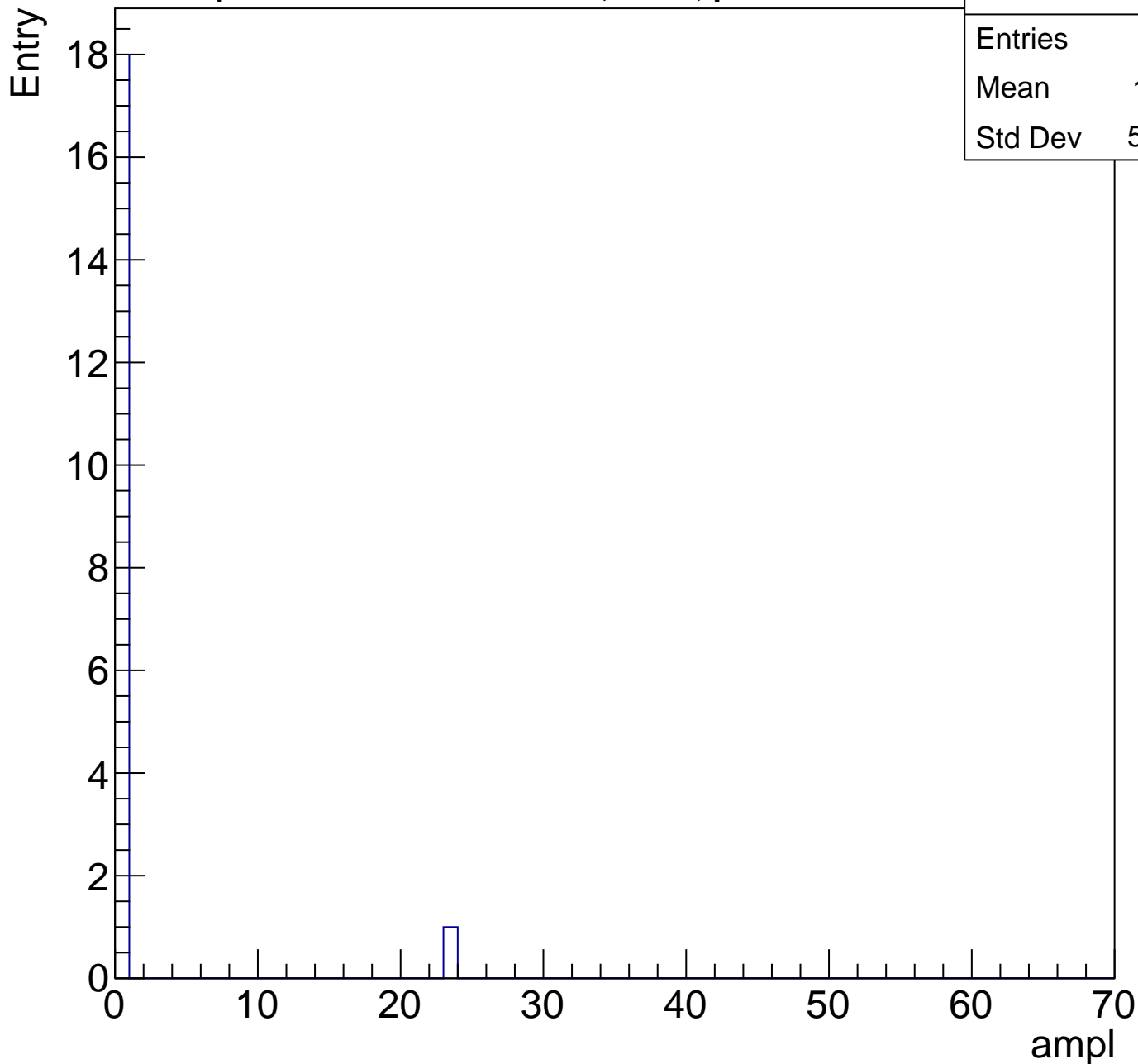
Entry



B1L103S, U2-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136



B1L103S, U2-ch8, adc0

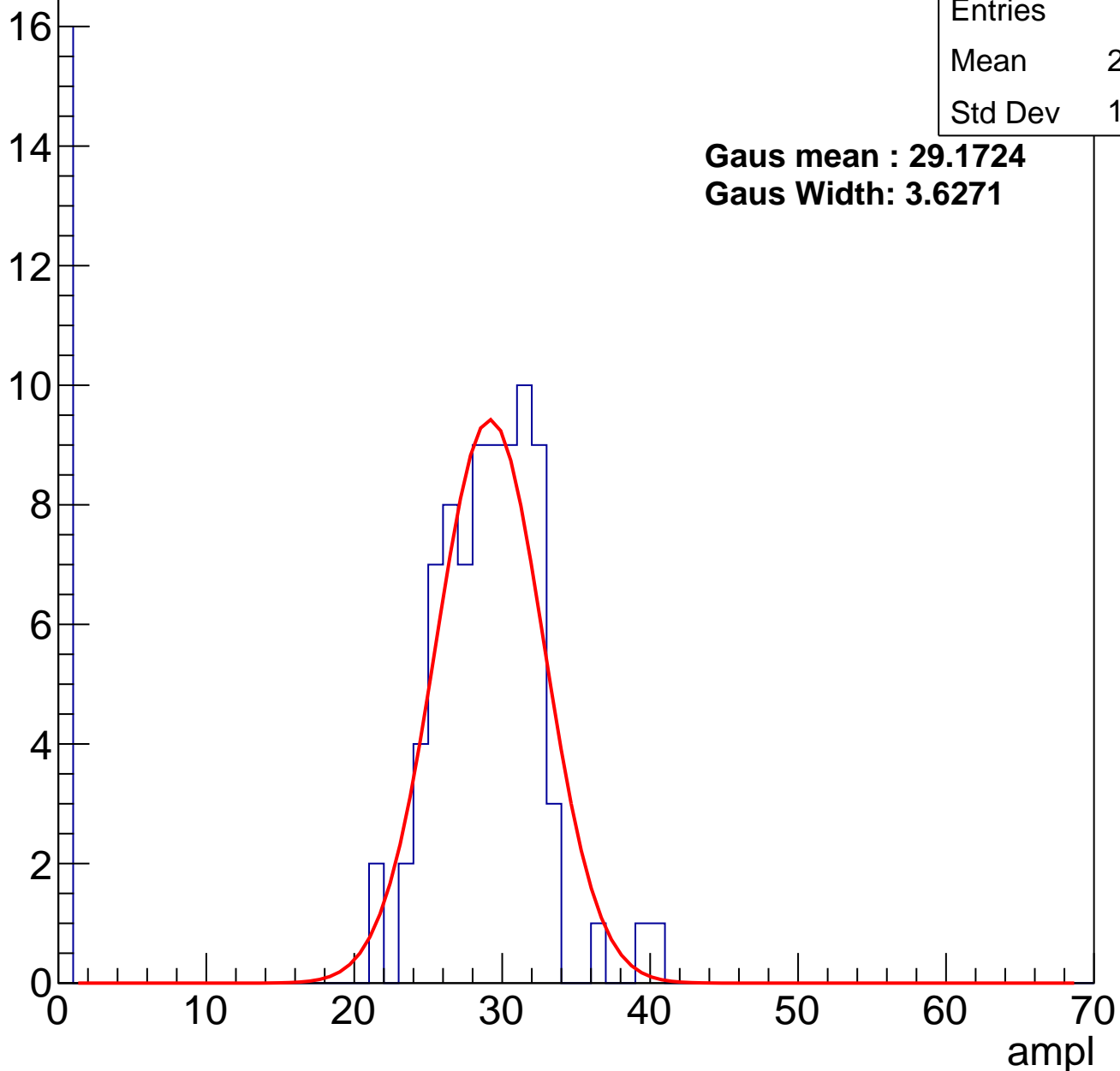
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	23.99
Std Dev	11.05

Gaus mean : 29.1724

Gaus Width: 3.6271

Entry



B1L103S, U2-ch8, adc1

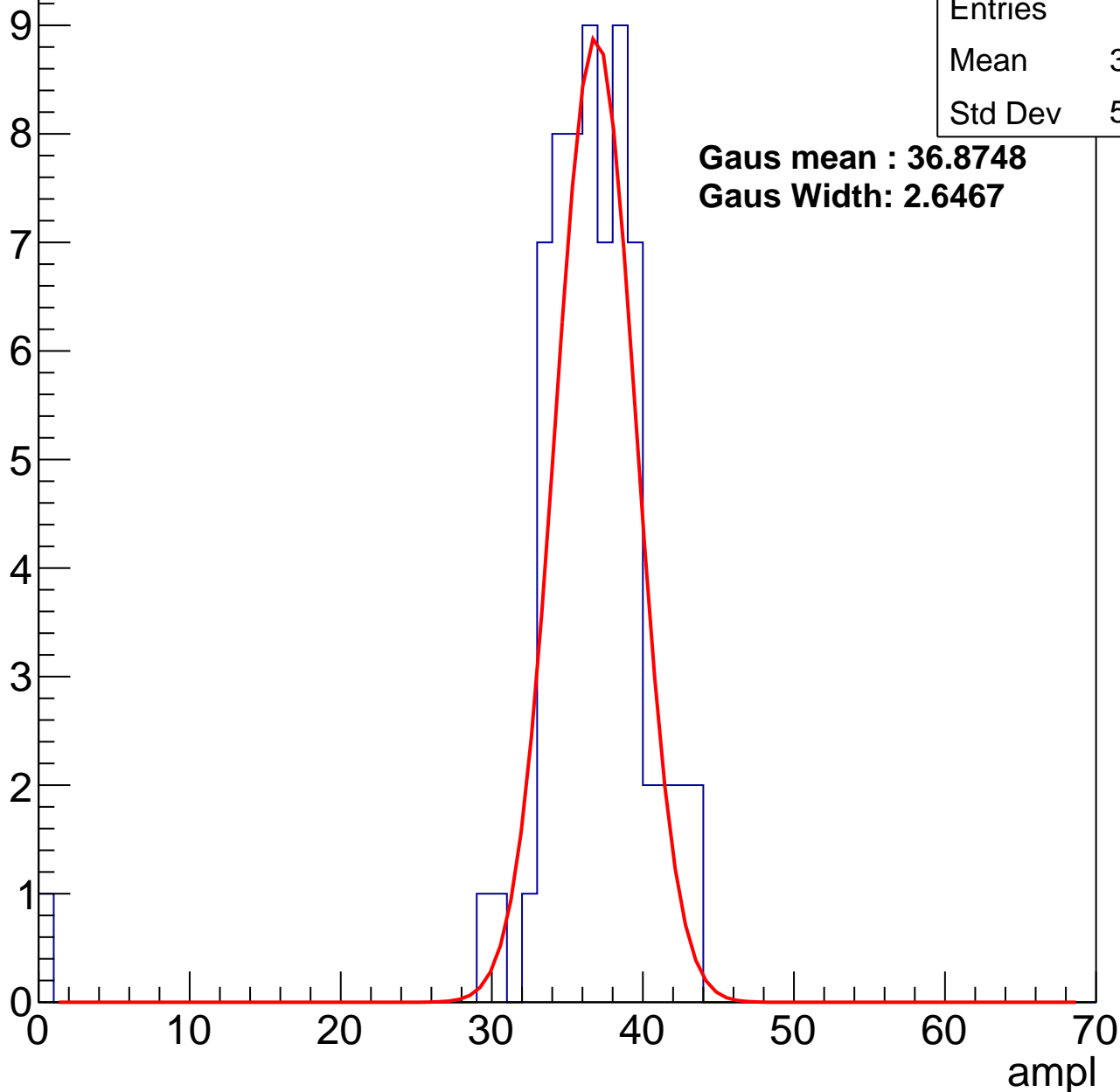
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.88
Std Dev	5.265

Gaus mean : 36.8748

Gaus Width: 2.6467



B1L103S, U2-ch8, adc2

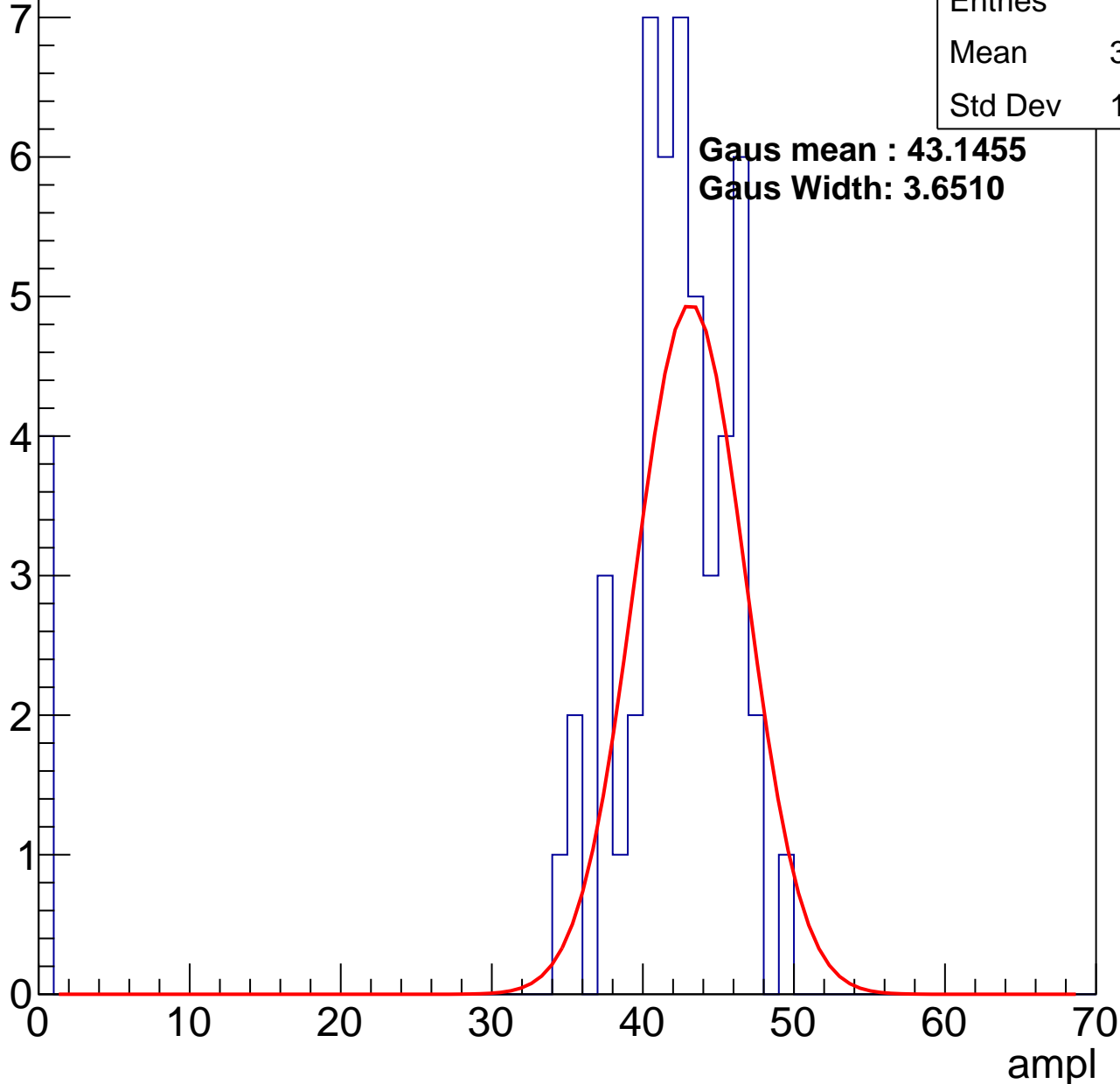
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	38.83
Std Dev	11.44

Gaus mean : 43.1455

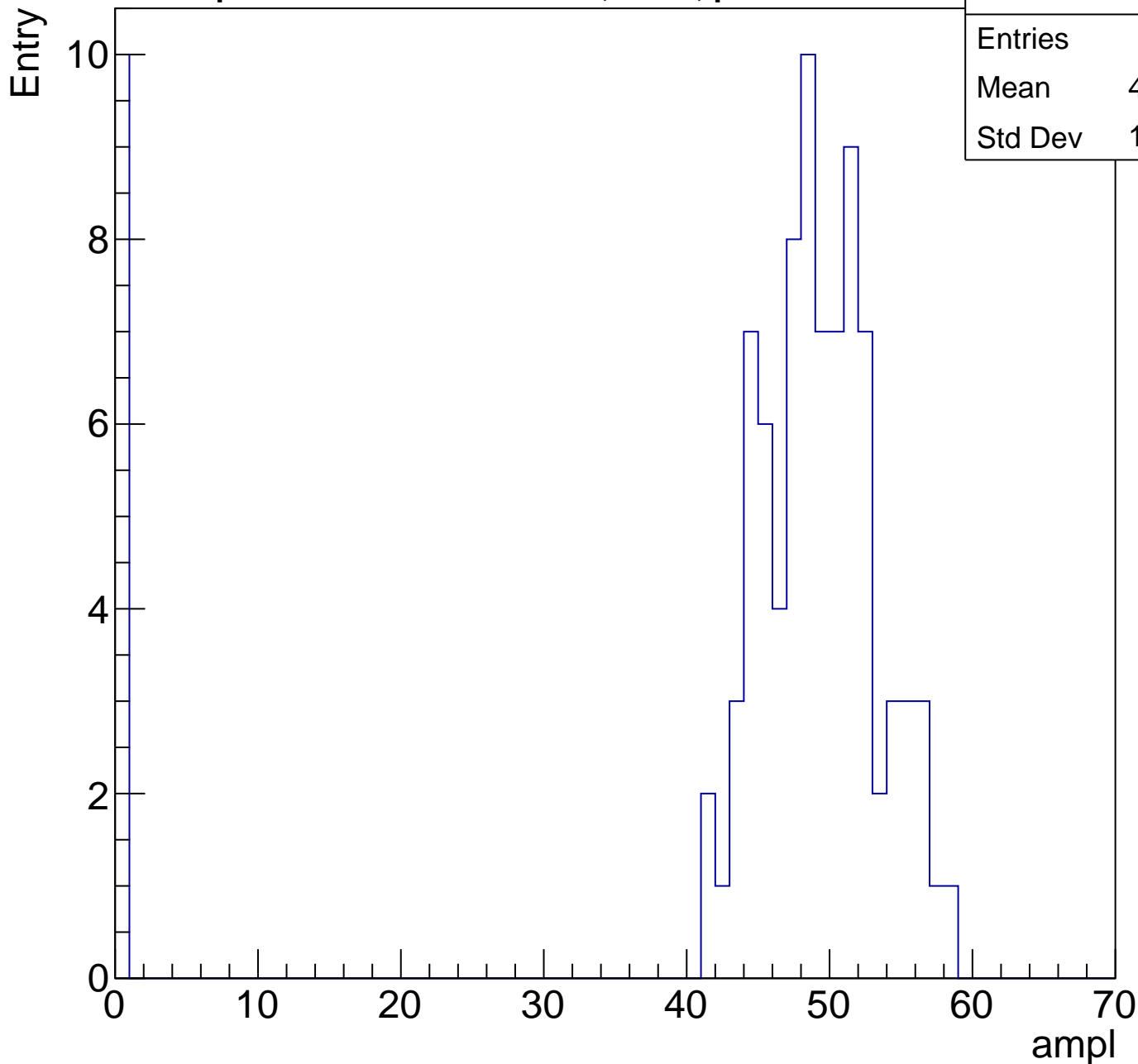
Gaus Width: 3.6510



B1L103S, U2-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	43.65
Std Dev	15.49

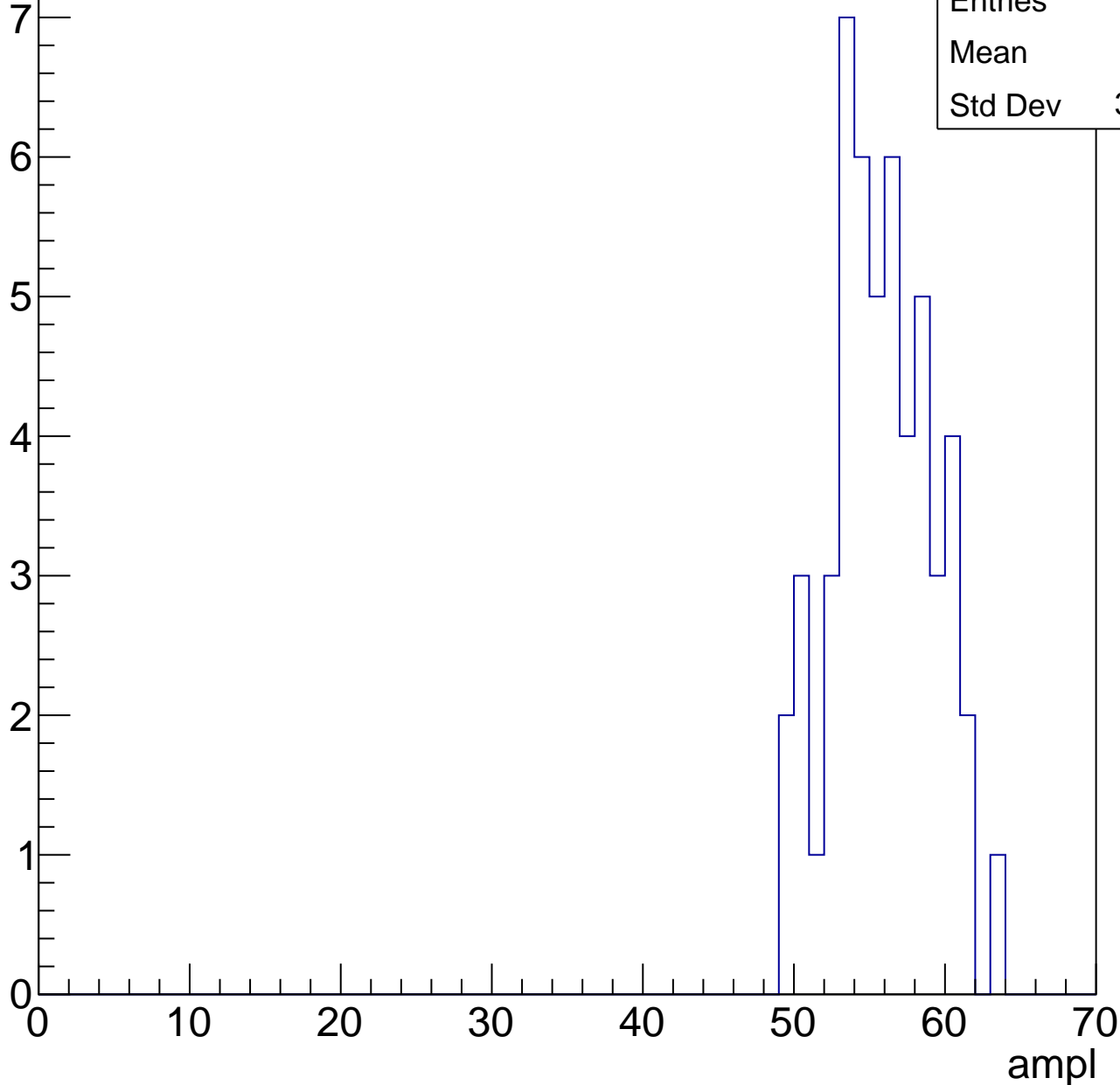


B1L103S, U2-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	55.4
Std Dev	3.301

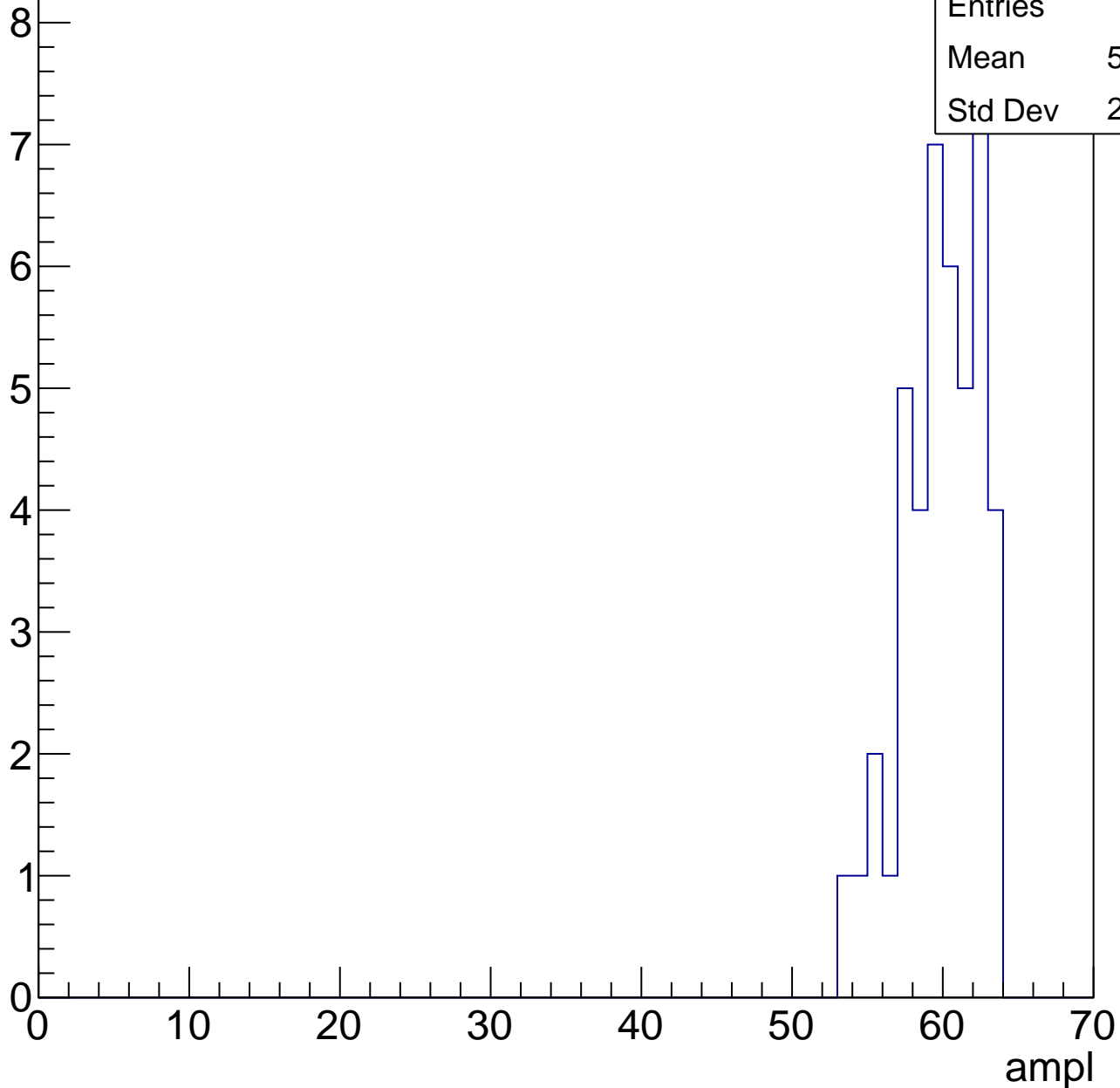


B1L103S, U2-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	59.45
Std Dev	2.518

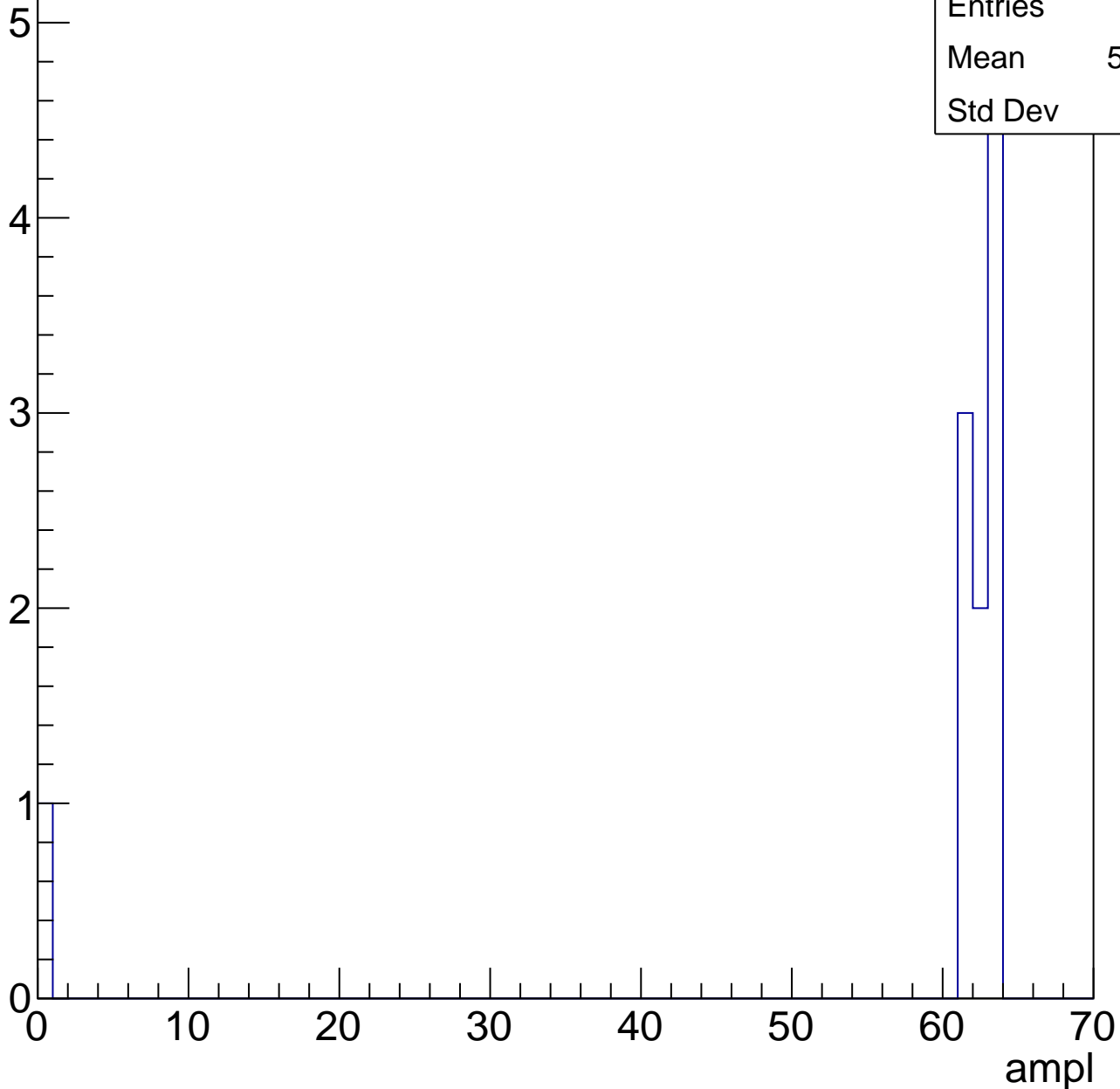


B1L103S, U2-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	56.55
Std Dev	17.9

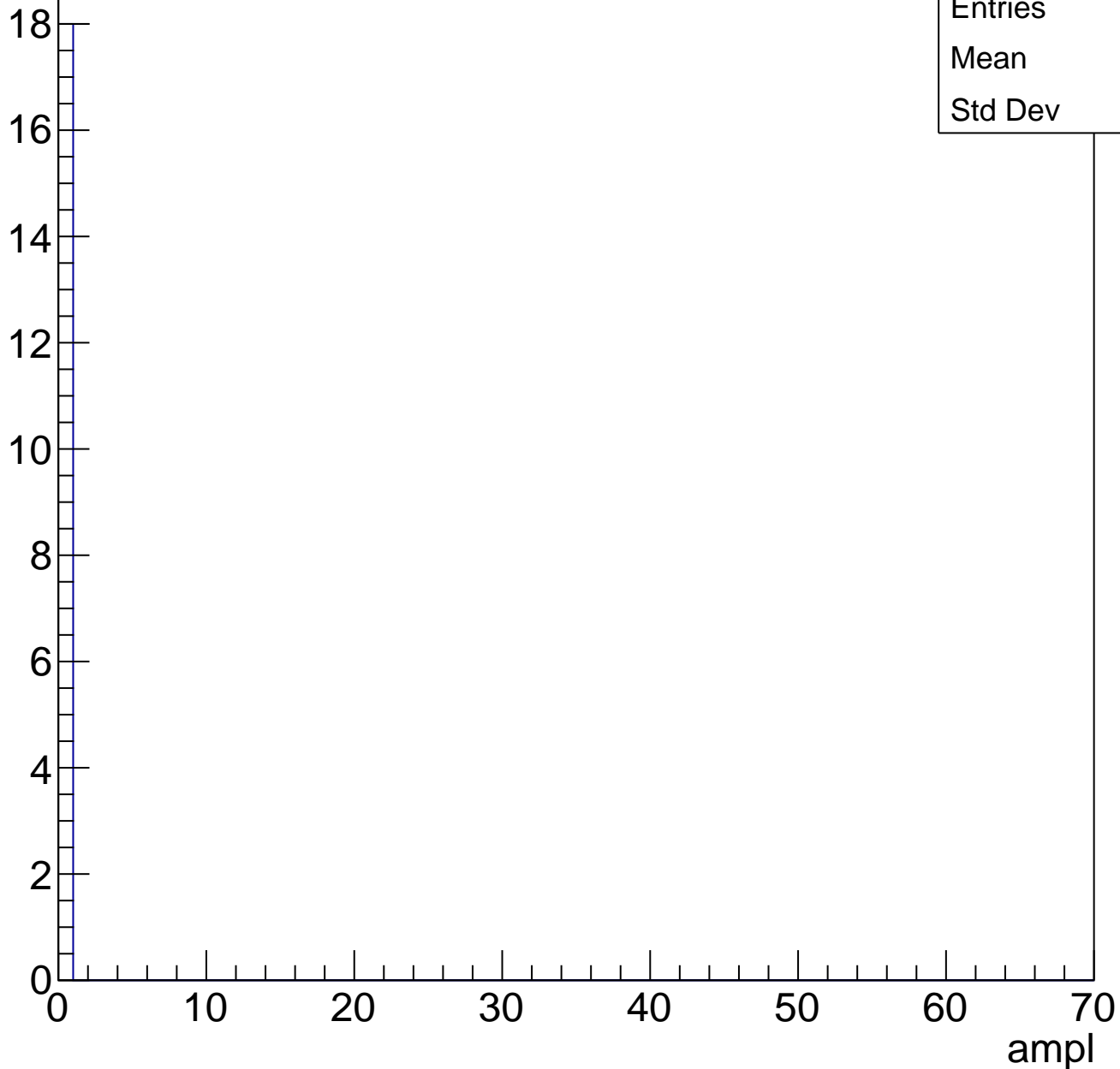


B1L103S, U2-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0

Entry



B1L103S, U2-ch9, adc0

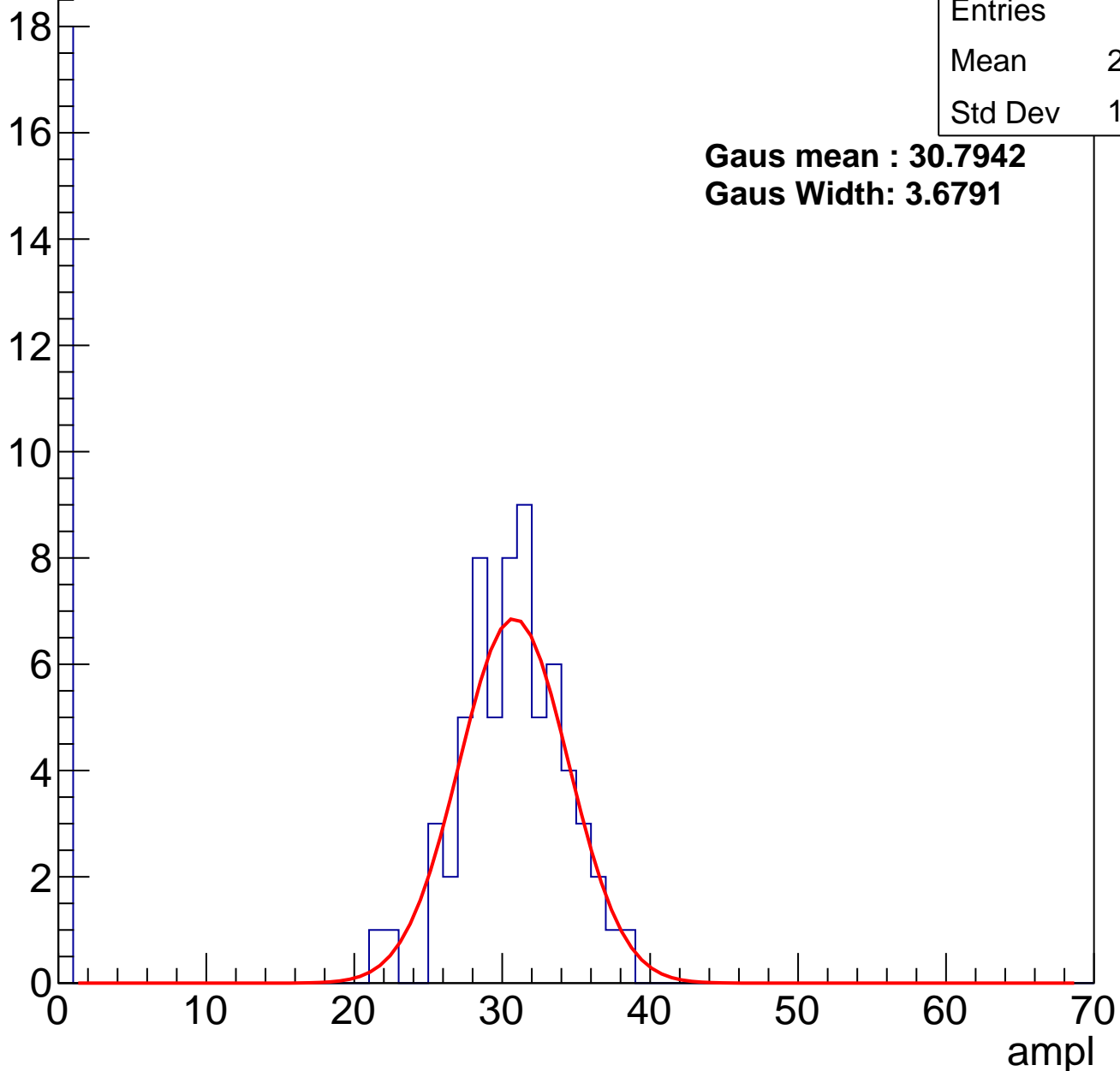
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.65
Std Dev	12.89

Gaus mean : 30.7942

Gaus Width: 3.6791

Entry



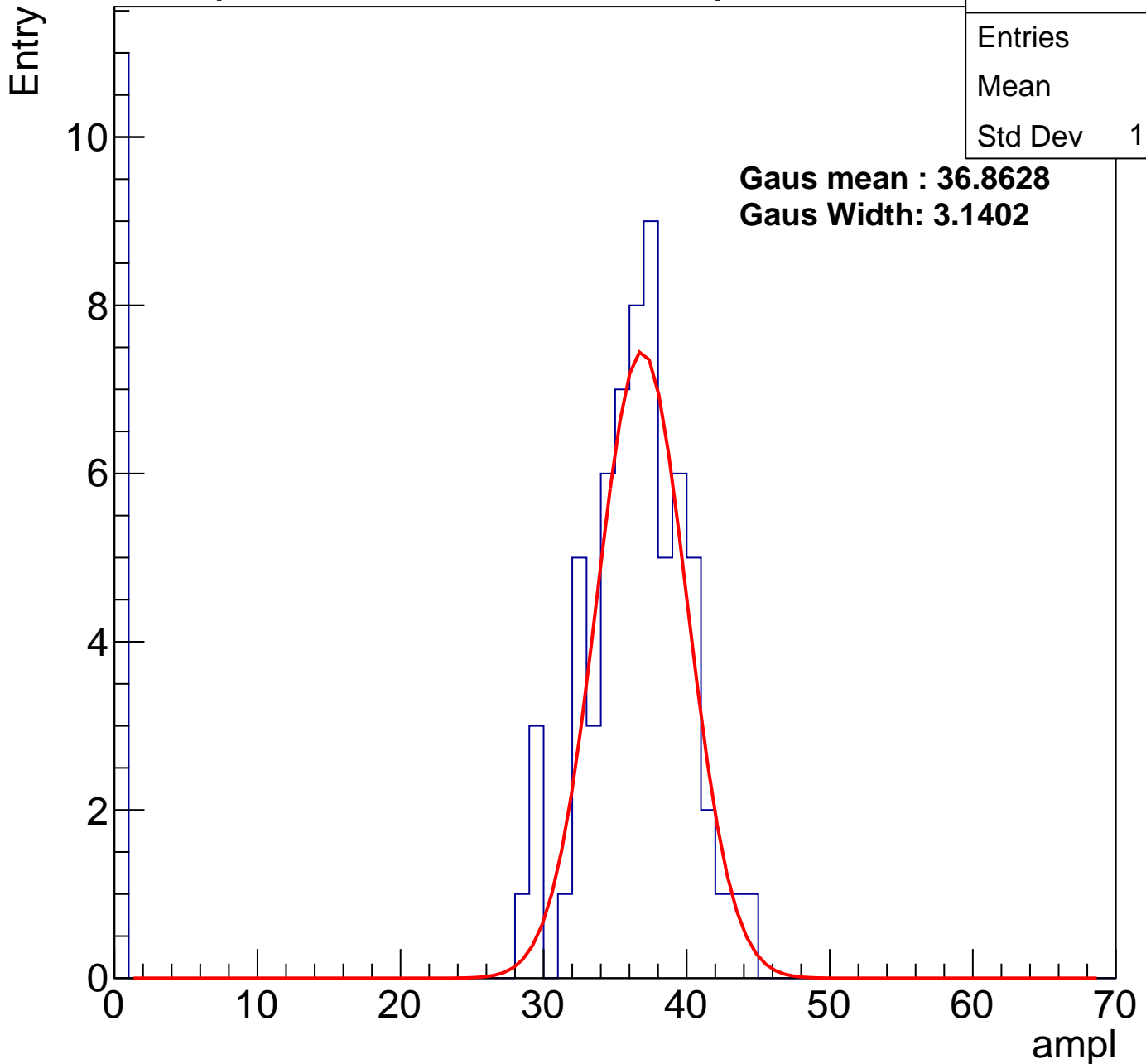
B1L103S, U2-ch9, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	30.8
Std Dev	13.15

Gaus mean : 36.8628

Gaus Width: 3.1402



B1L103S, U2-ch9, adc2

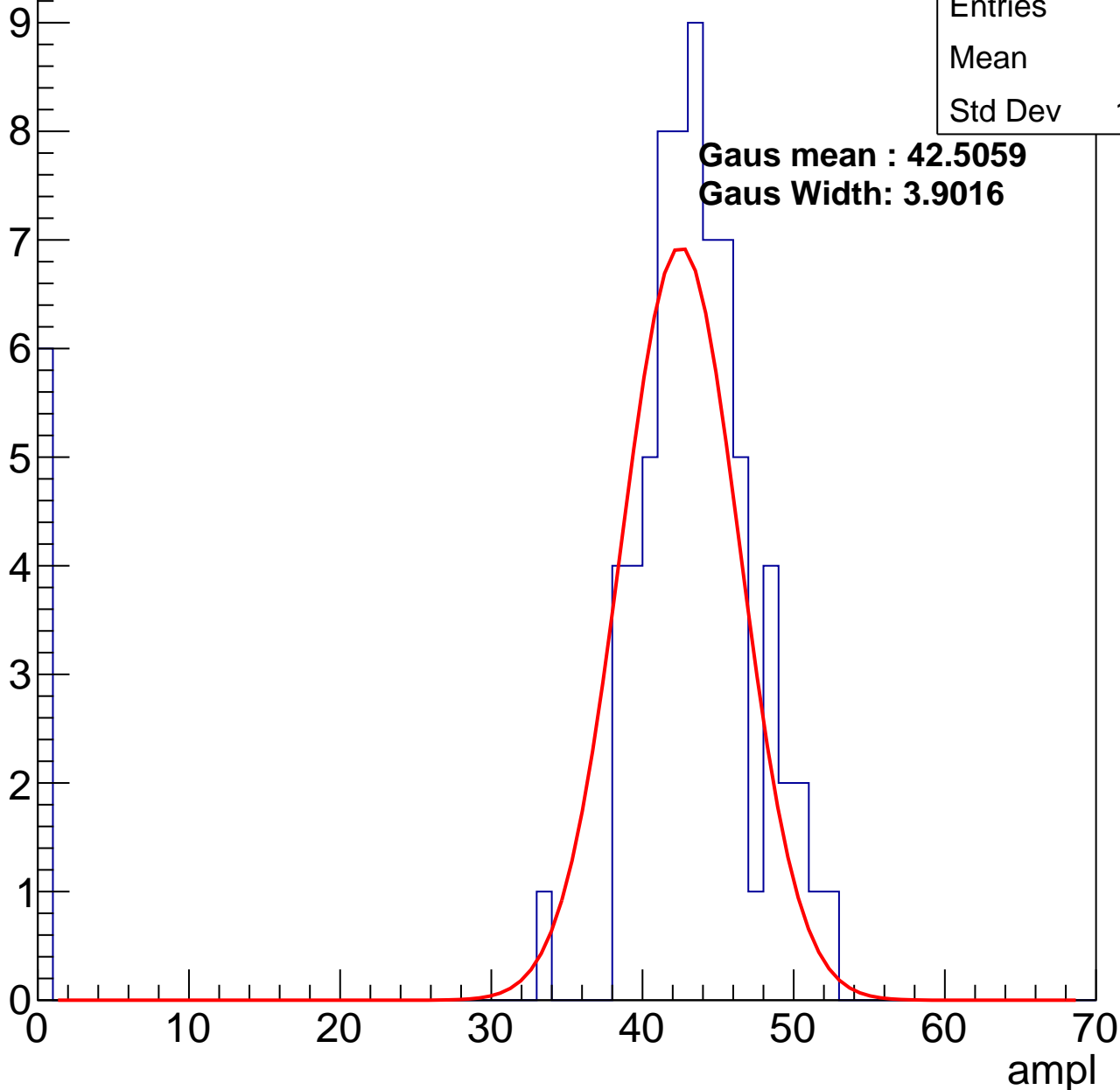
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	39.8
Std Dev	12.21

Gaus mean : 42.5059

Gaus Width: 3.9016

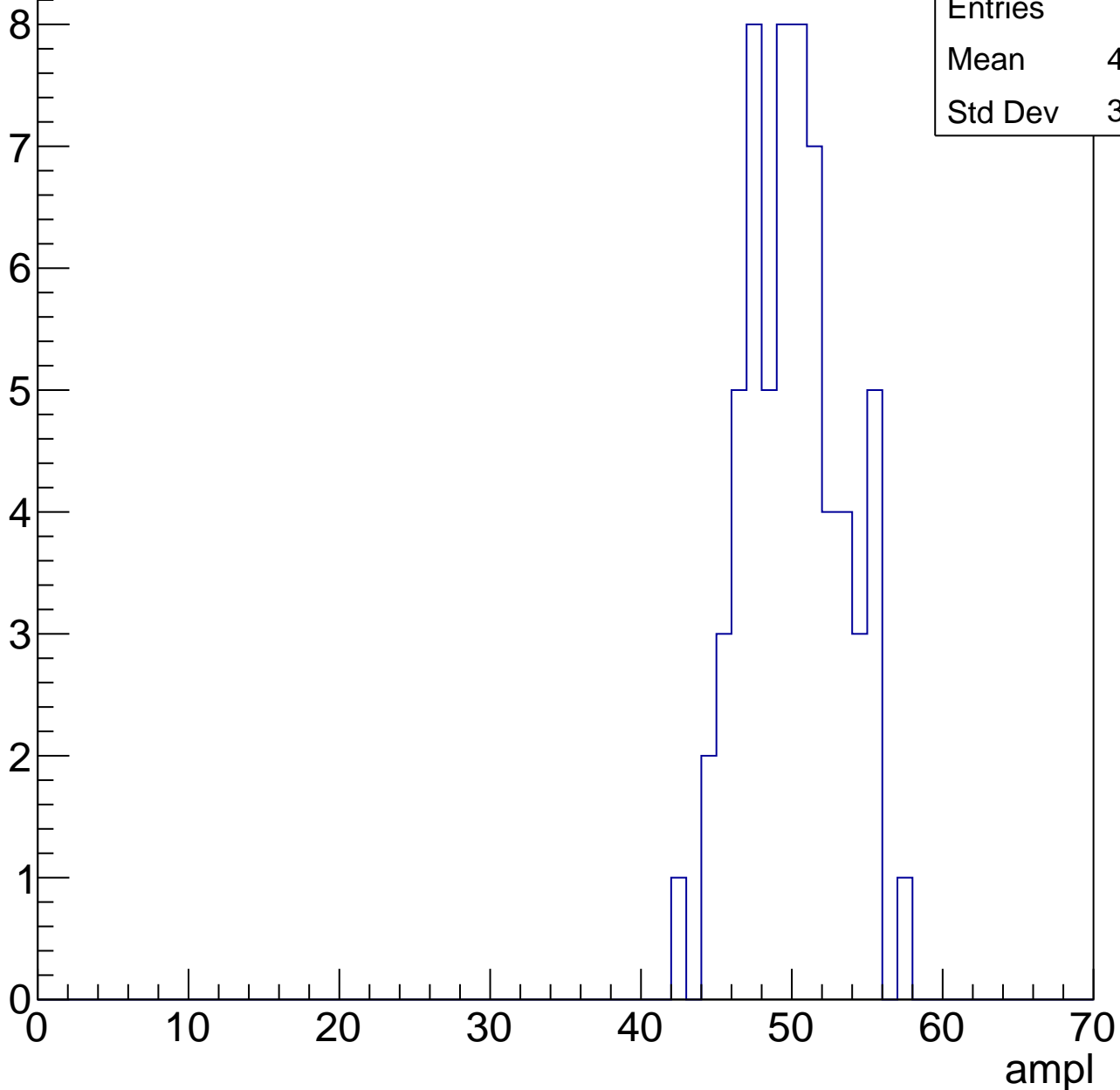


B1L103S, U2-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.59
Std Dev	3.215

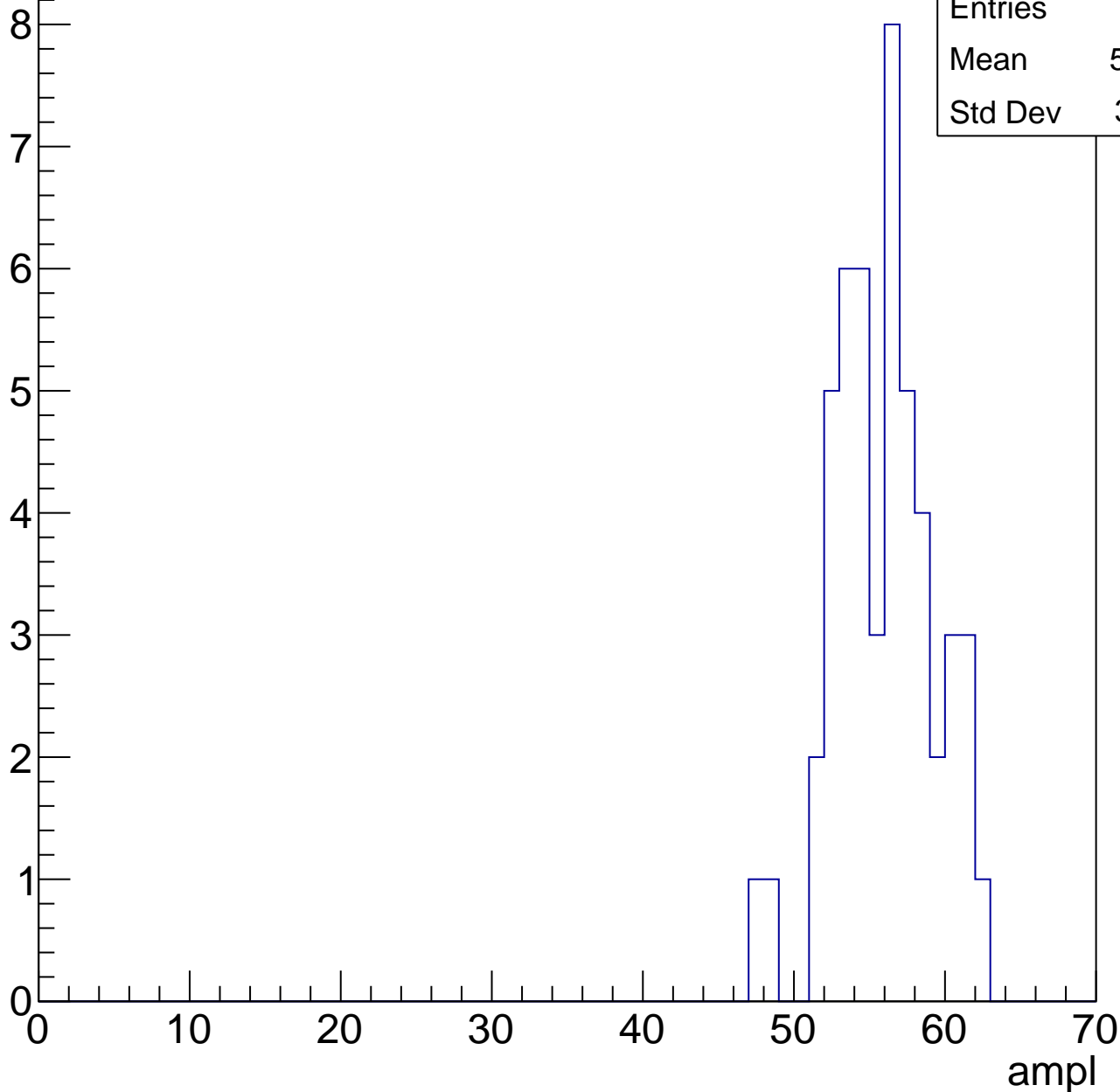


B1L103S, U2-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.44
Std Dev	3.281

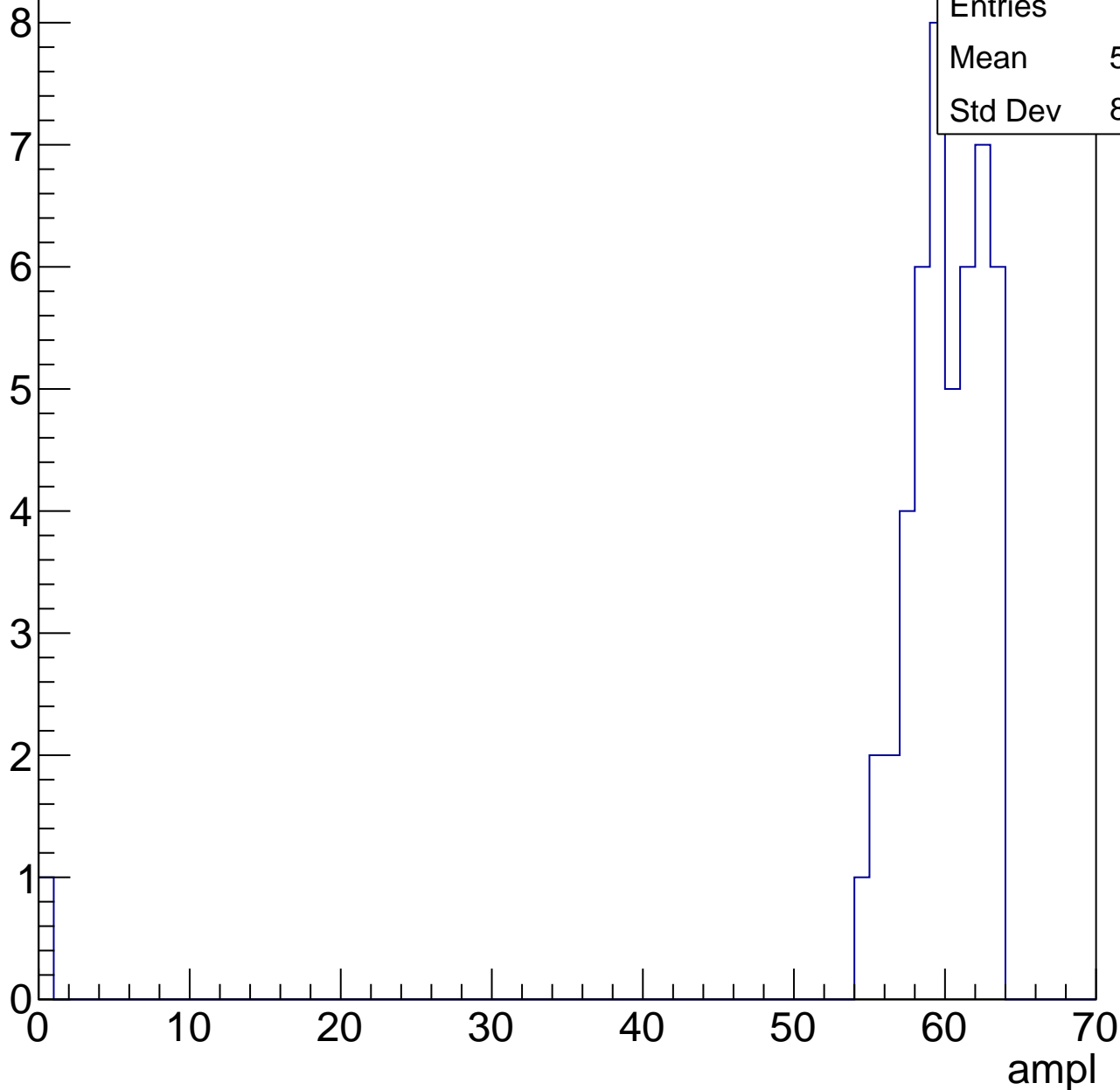


B1L103S, U2-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

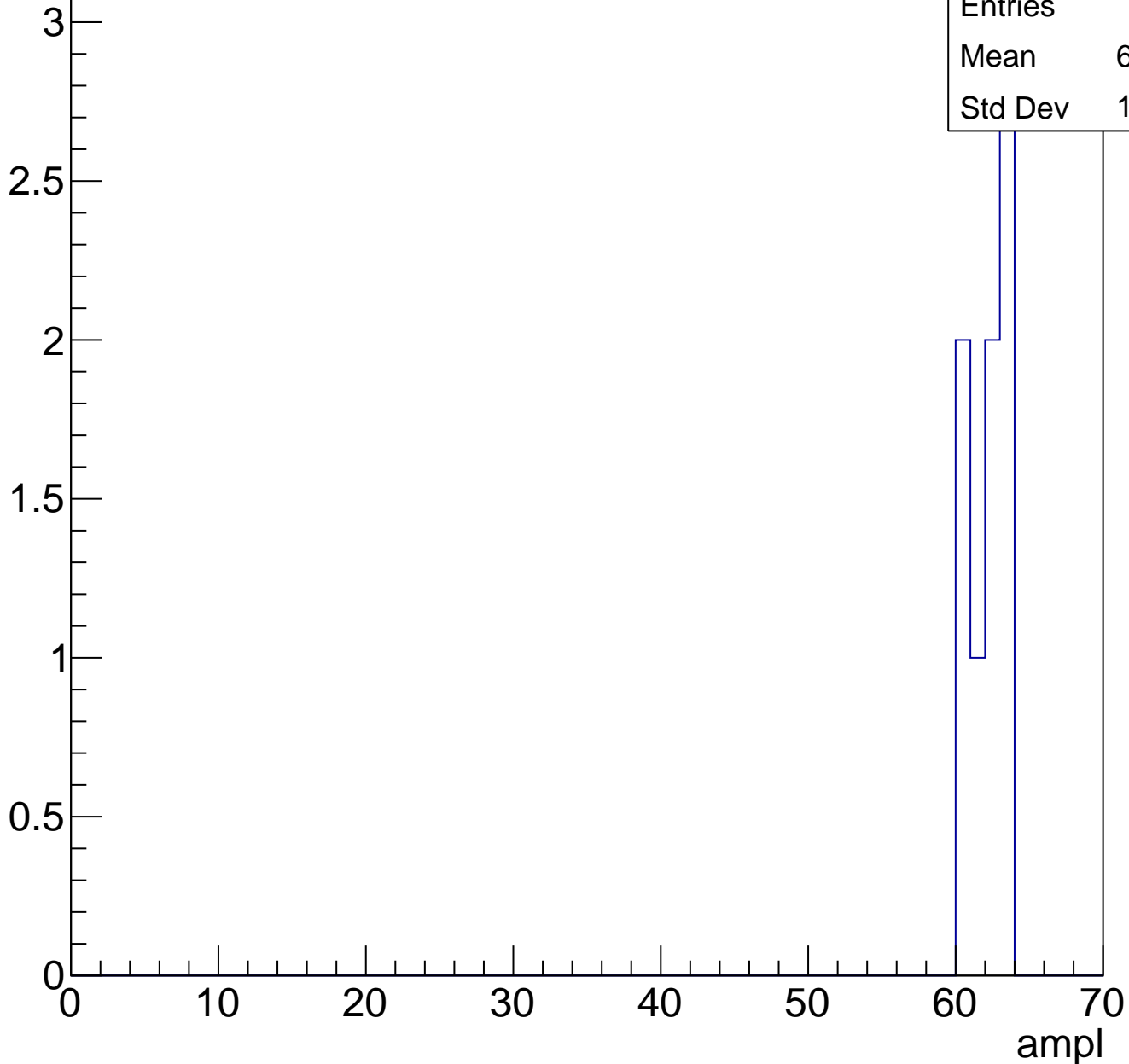
Entries	48
Mean	58.38
Std Dev	8.836



B1L103S, U2-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U2-ch10, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	23.95
Std Dev	11.58

Gaus mean : 29.1410

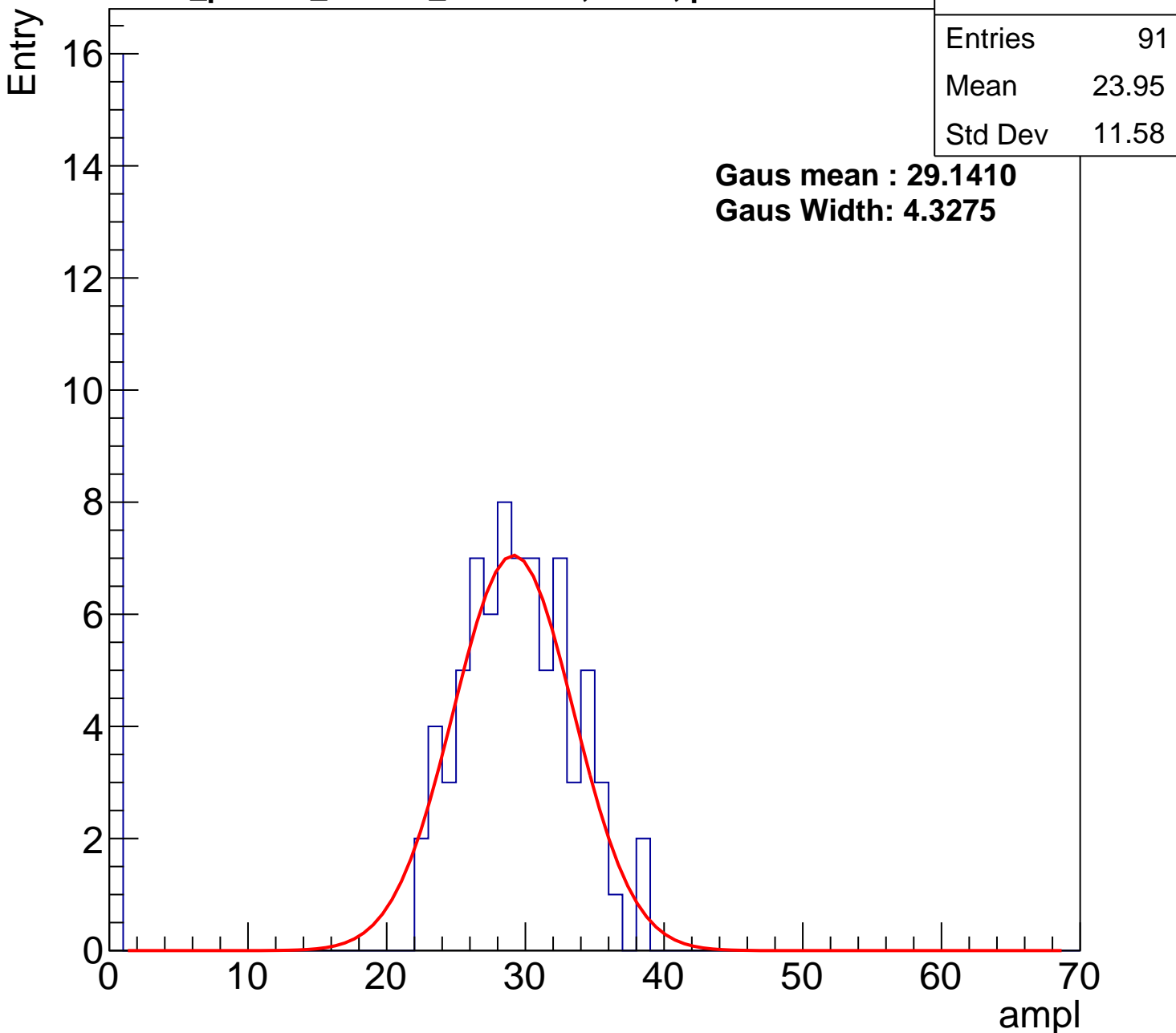
Gaus Width: 4.3275

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch10, adc1

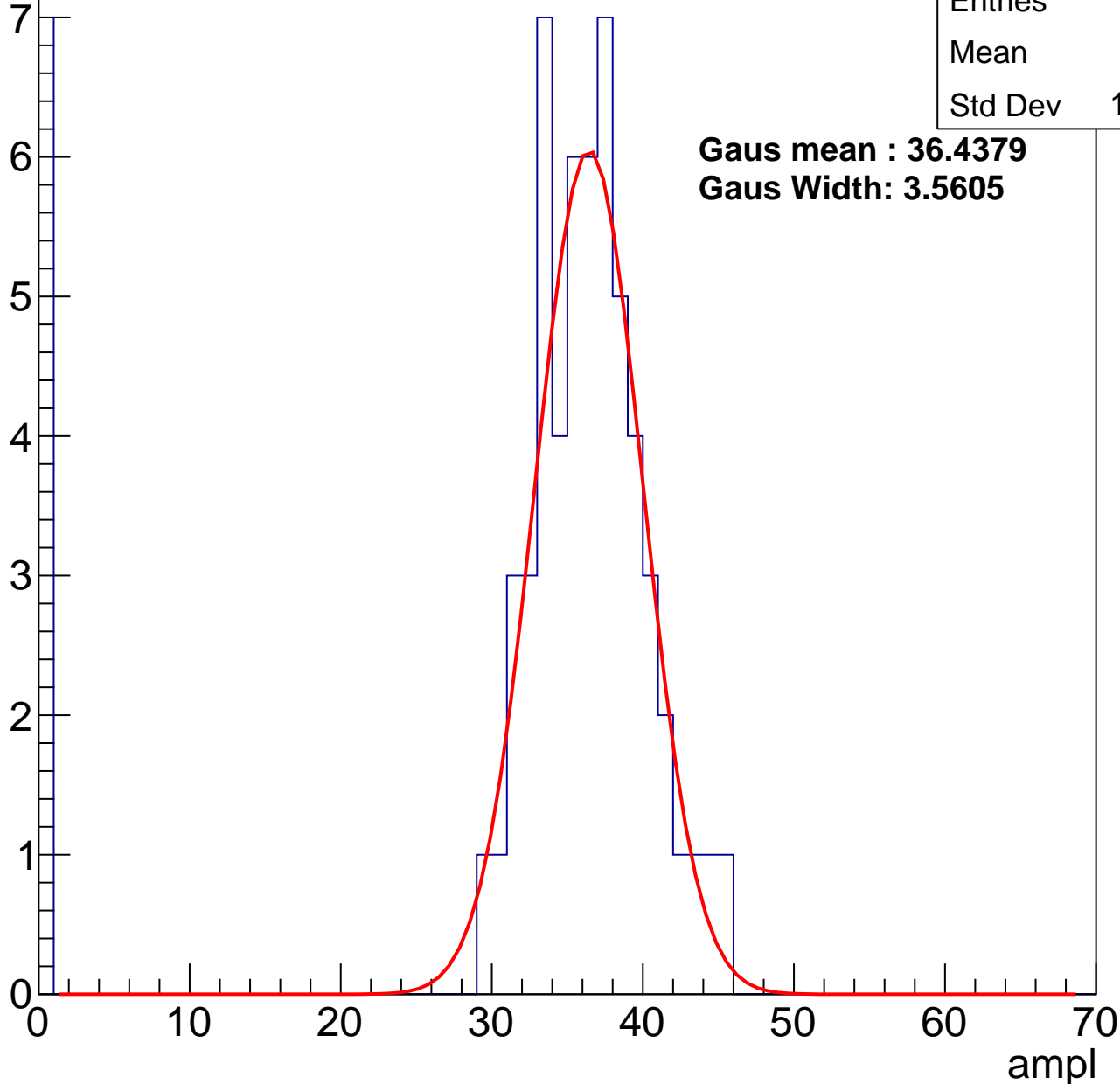
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	32.1
Std Dev	11.82

Gaus mean : 36.4379

Gaus Width: 3.5605



B1L103S, U2-ch10, adc2

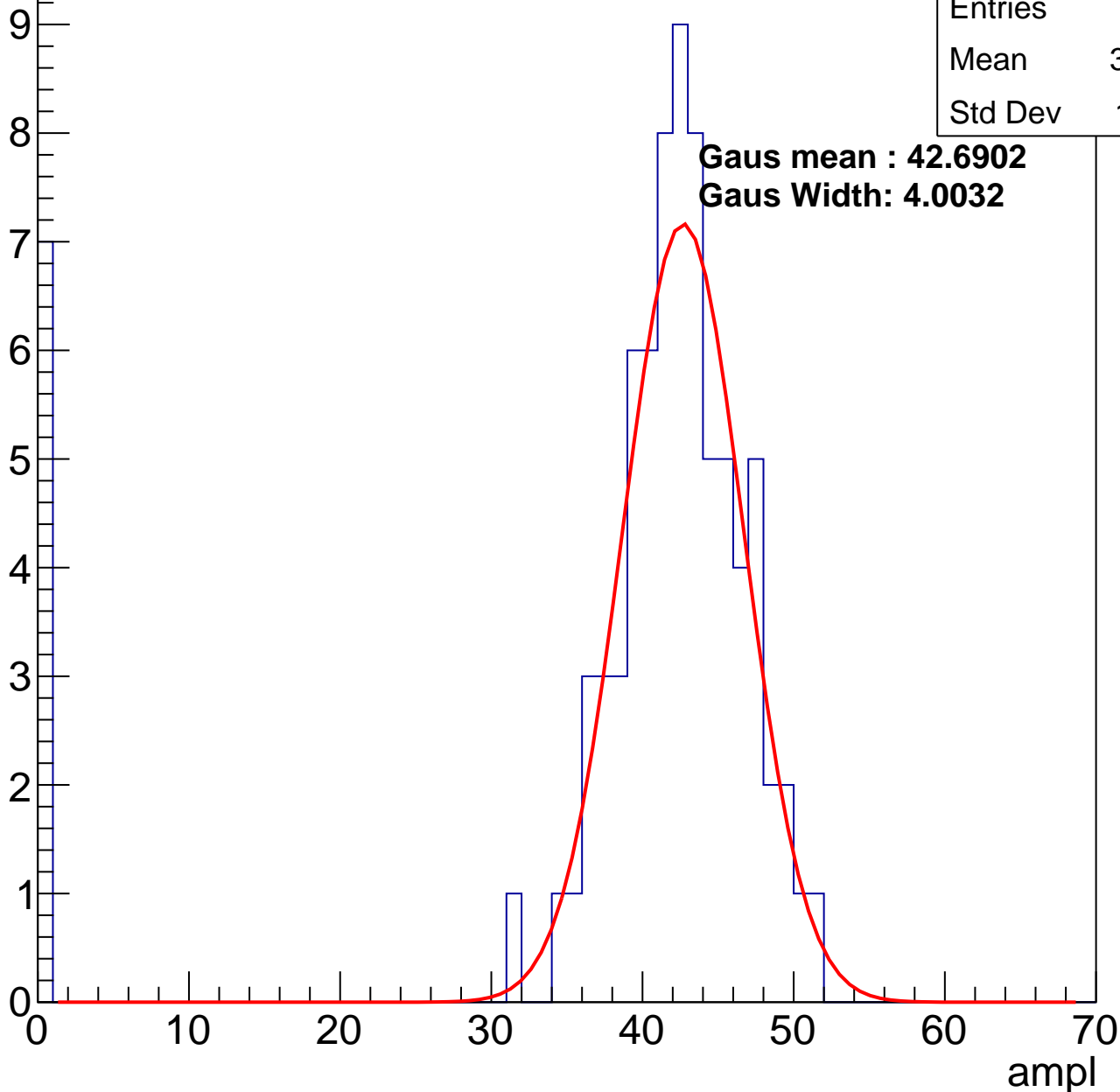
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	38.47
Std Dev	12.41

Gaus mean : 42.6902

Gaus Width: 4.0032

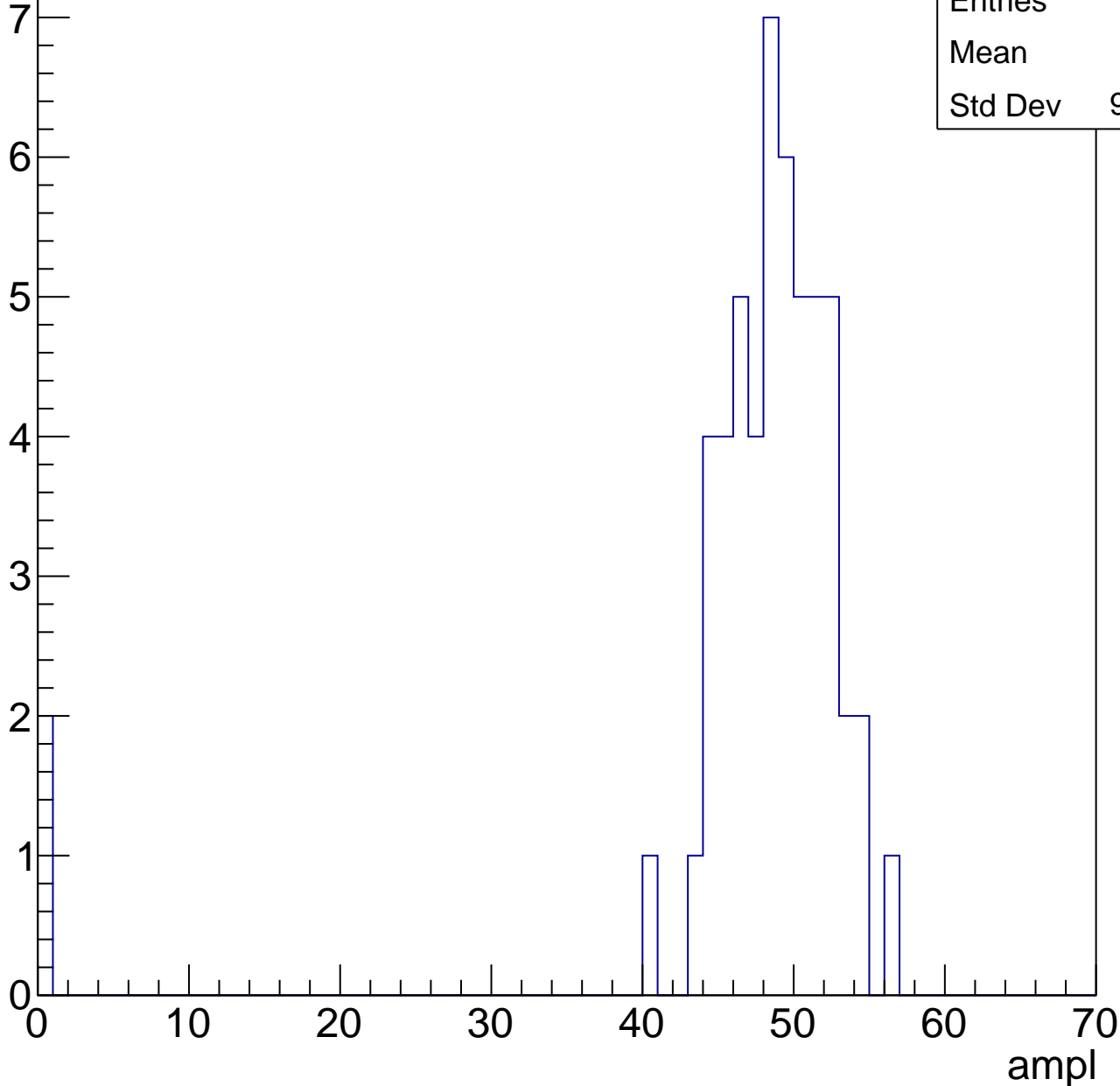


B1L103S, U2-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	46.7
Std Dev	9.687

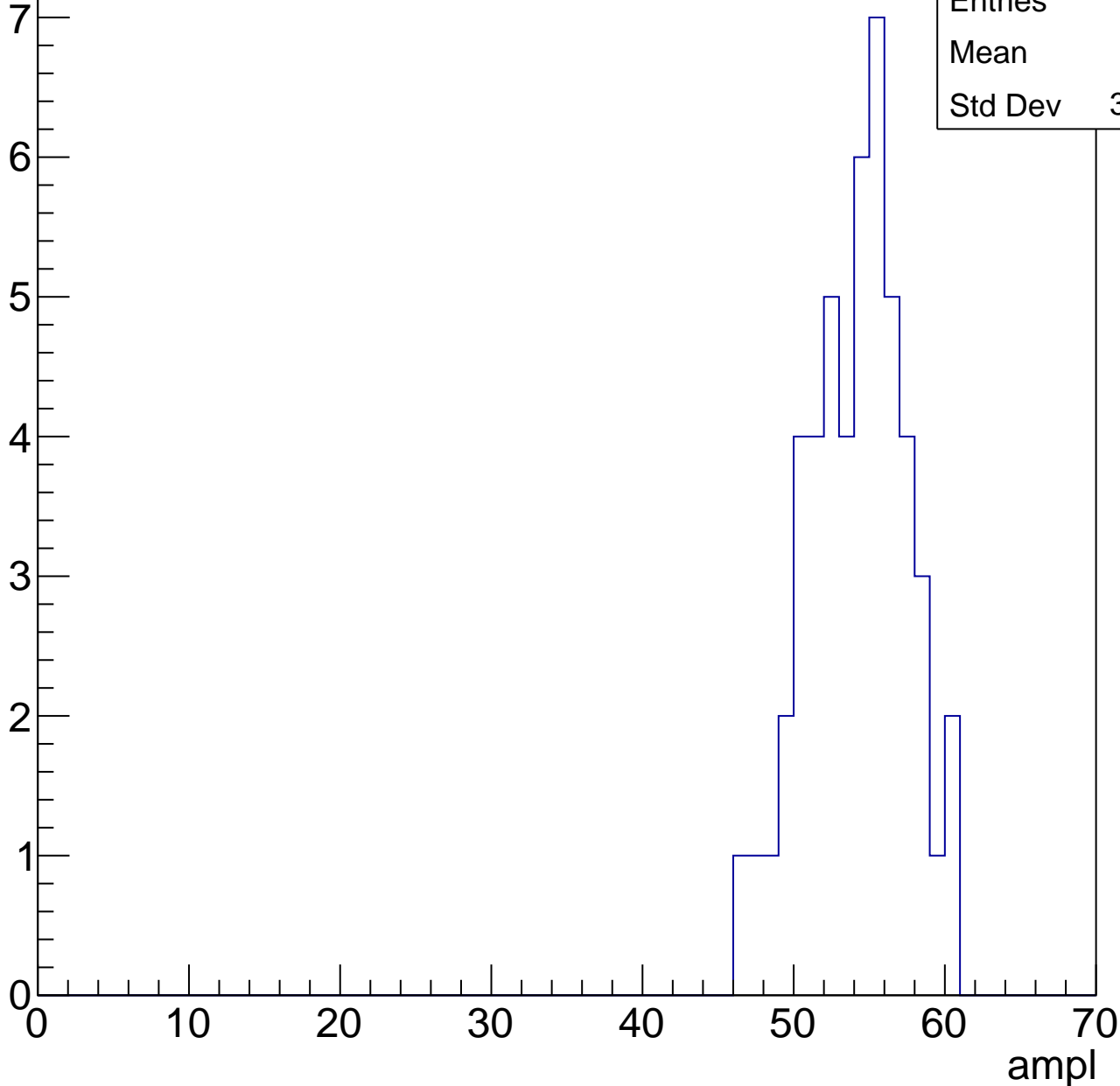


B1L103S, U2-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

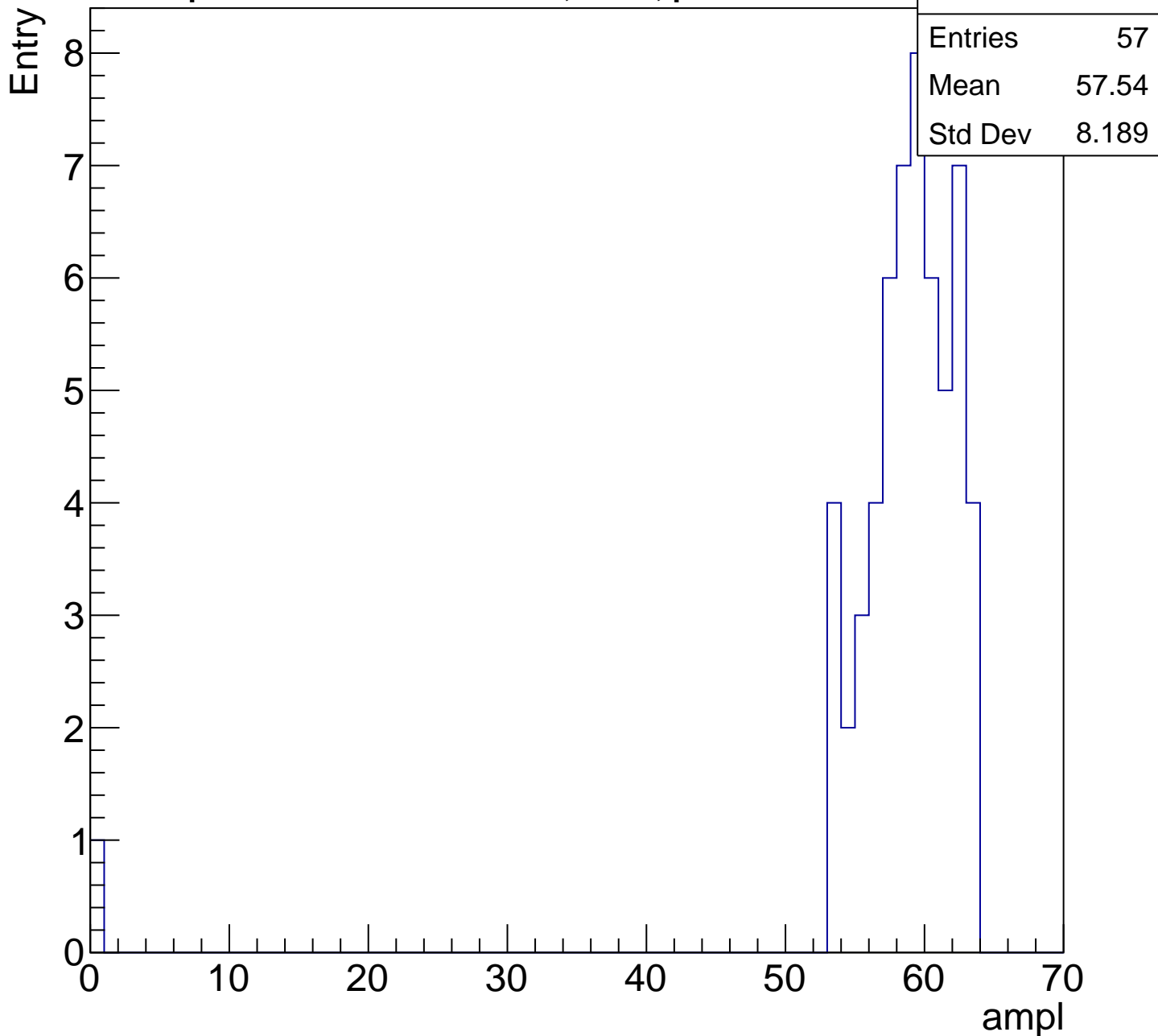
Entry

Entries	50
Mean	53.7
Std Dev	3.245



B1L103S, U2-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

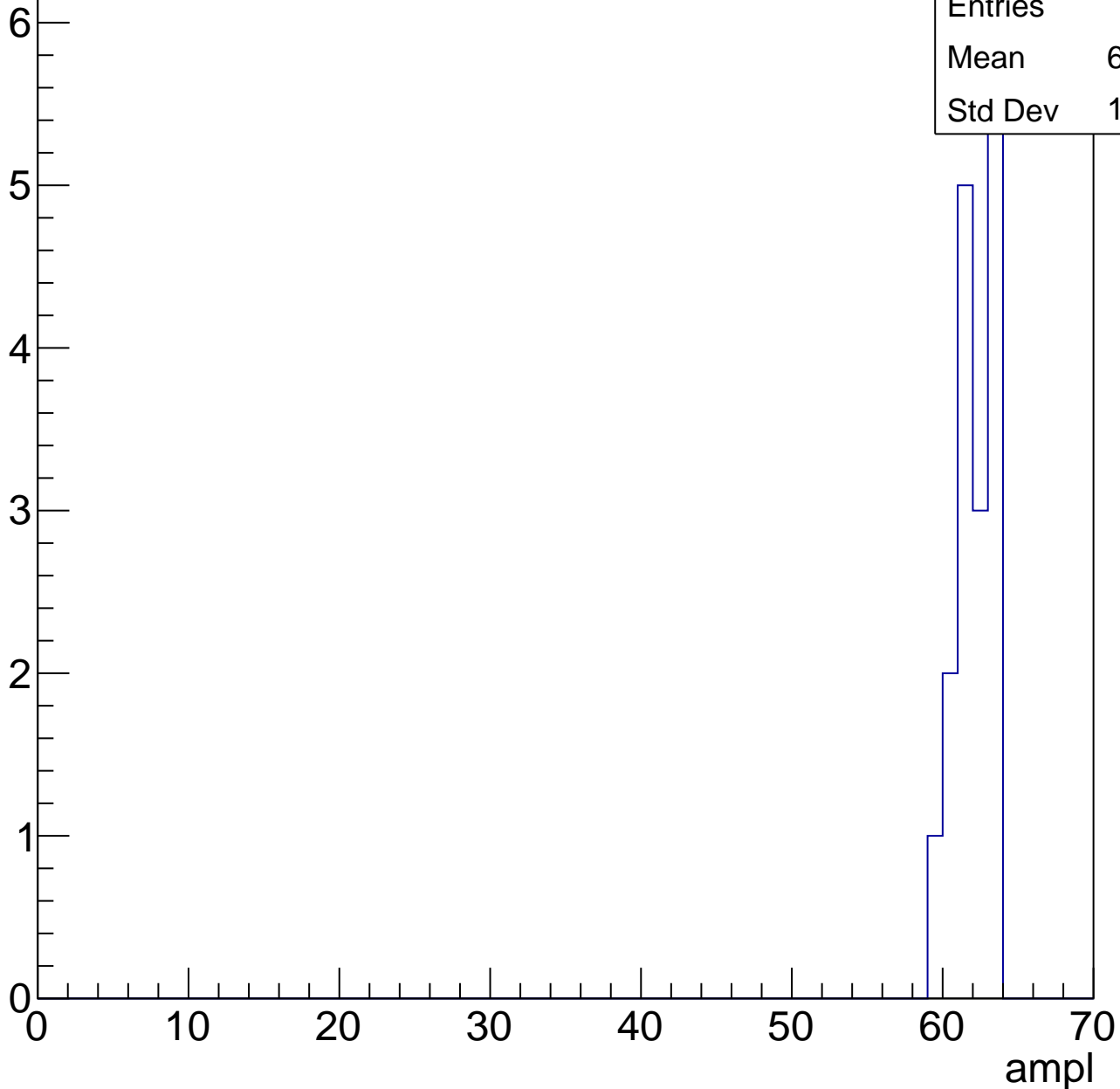


B1L103S, U2-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.65
Std Dev	1.234



B1L103S, U2-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U2-ch11, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	20.87
Std Dev	11.84

Gaus mean : 28.4414

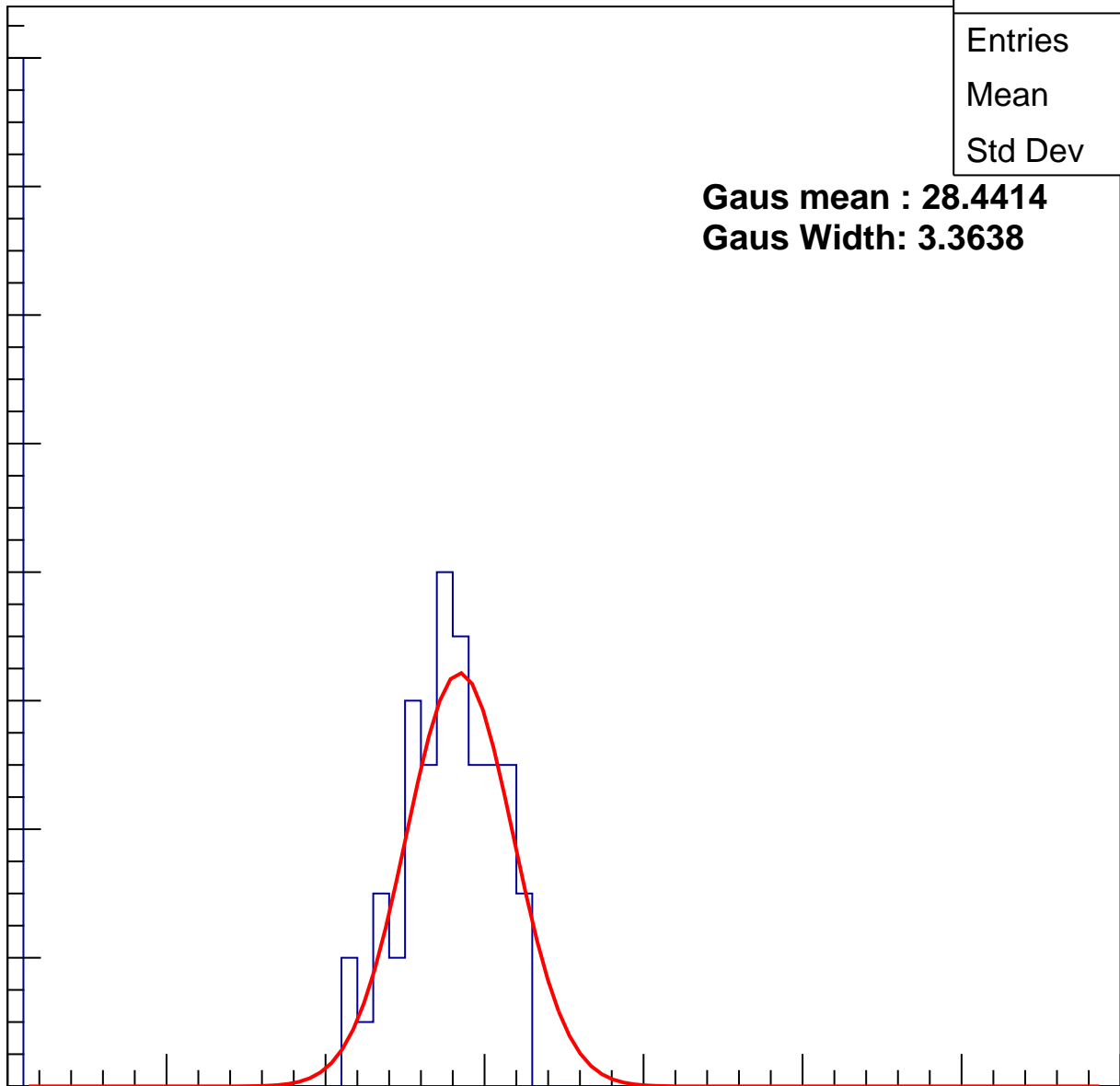
Gaus Width: 3.3638

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch11, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	31.22
Std Dev	9.33

Gaus mean : 34.0304

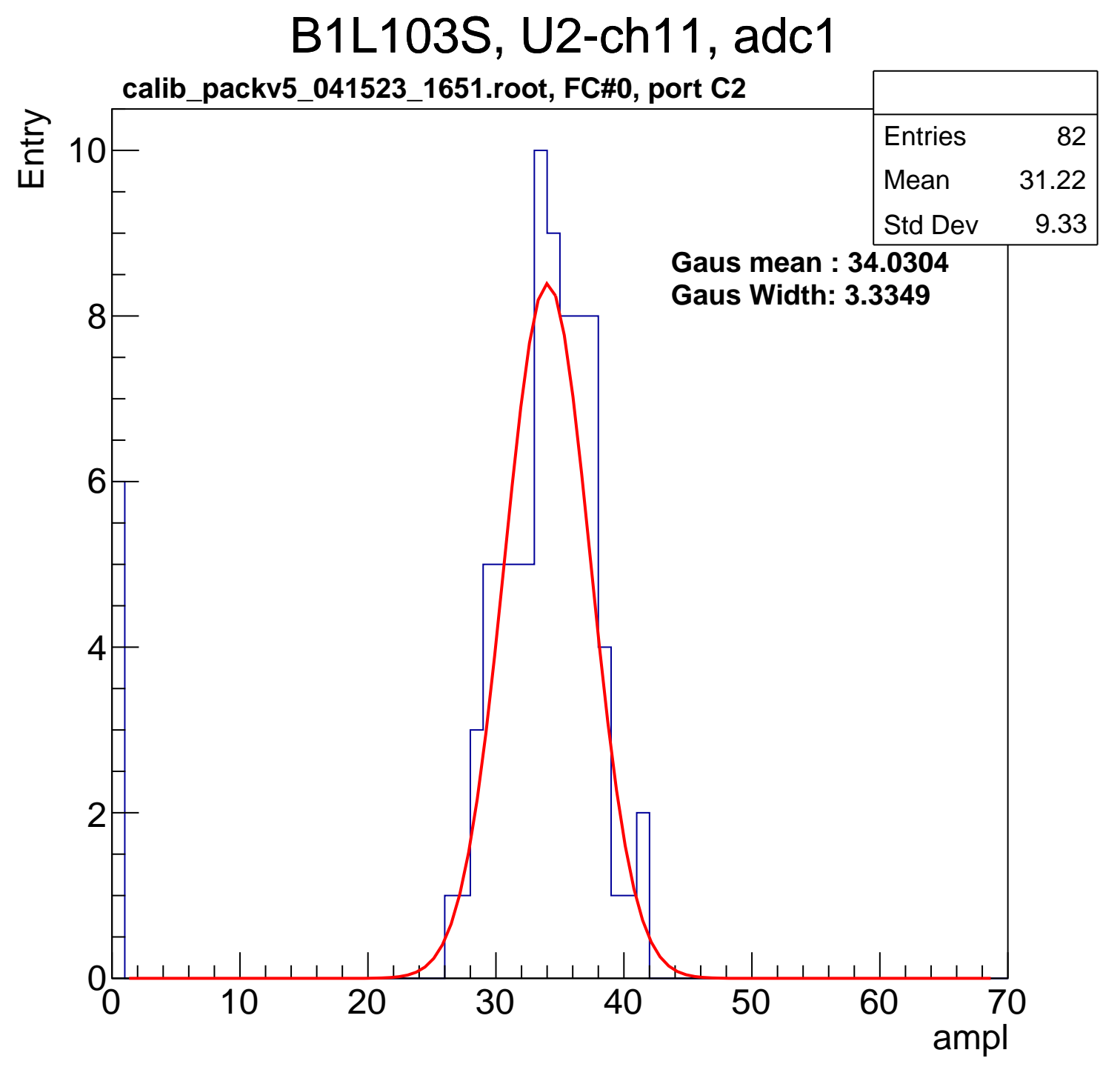
Gaus Width: 3.3349

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch11, adc2

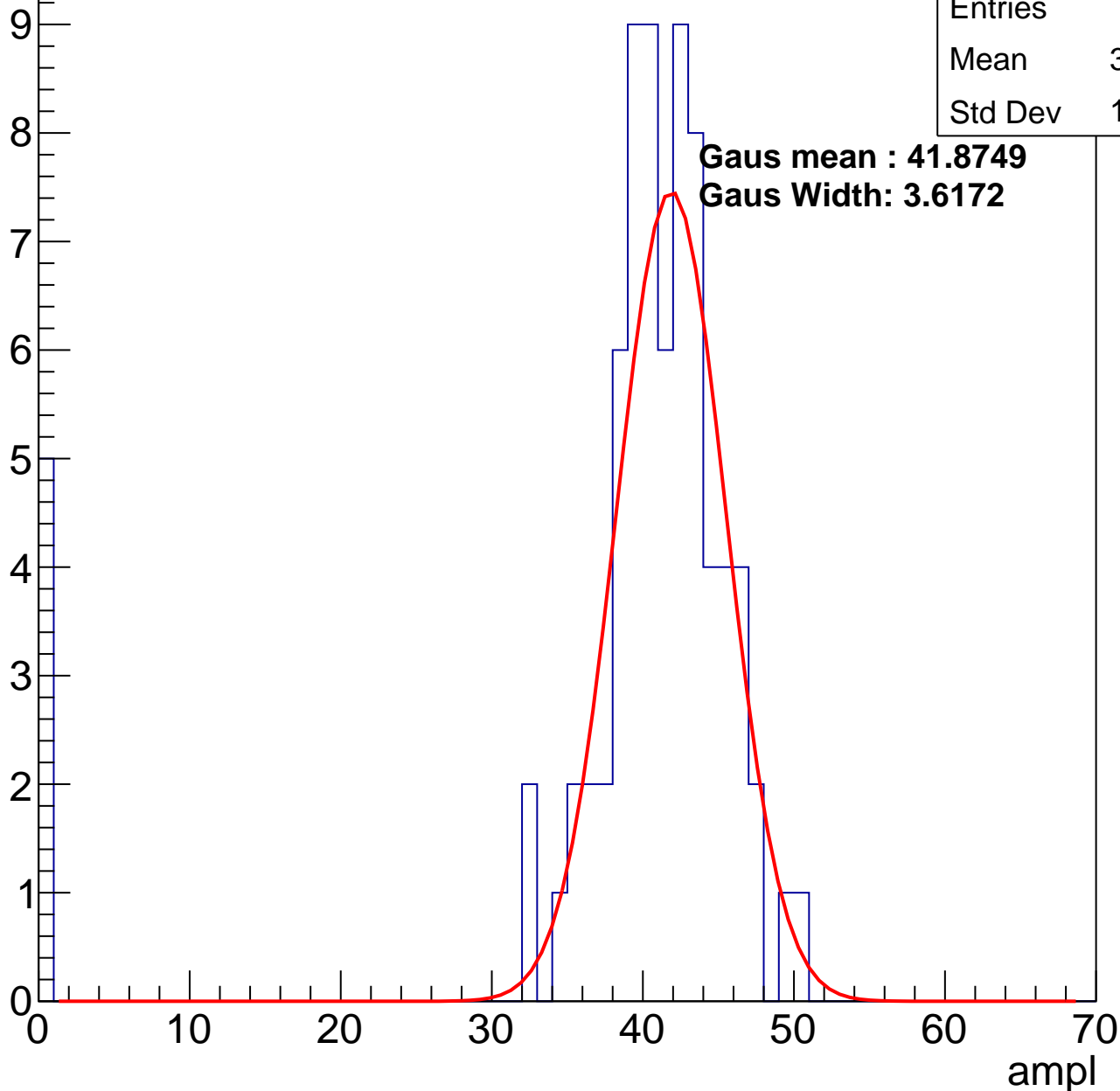
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	38.36
Std Dev	10.69

Gaus mean : 41.8749

Gaus Width: 3.6172

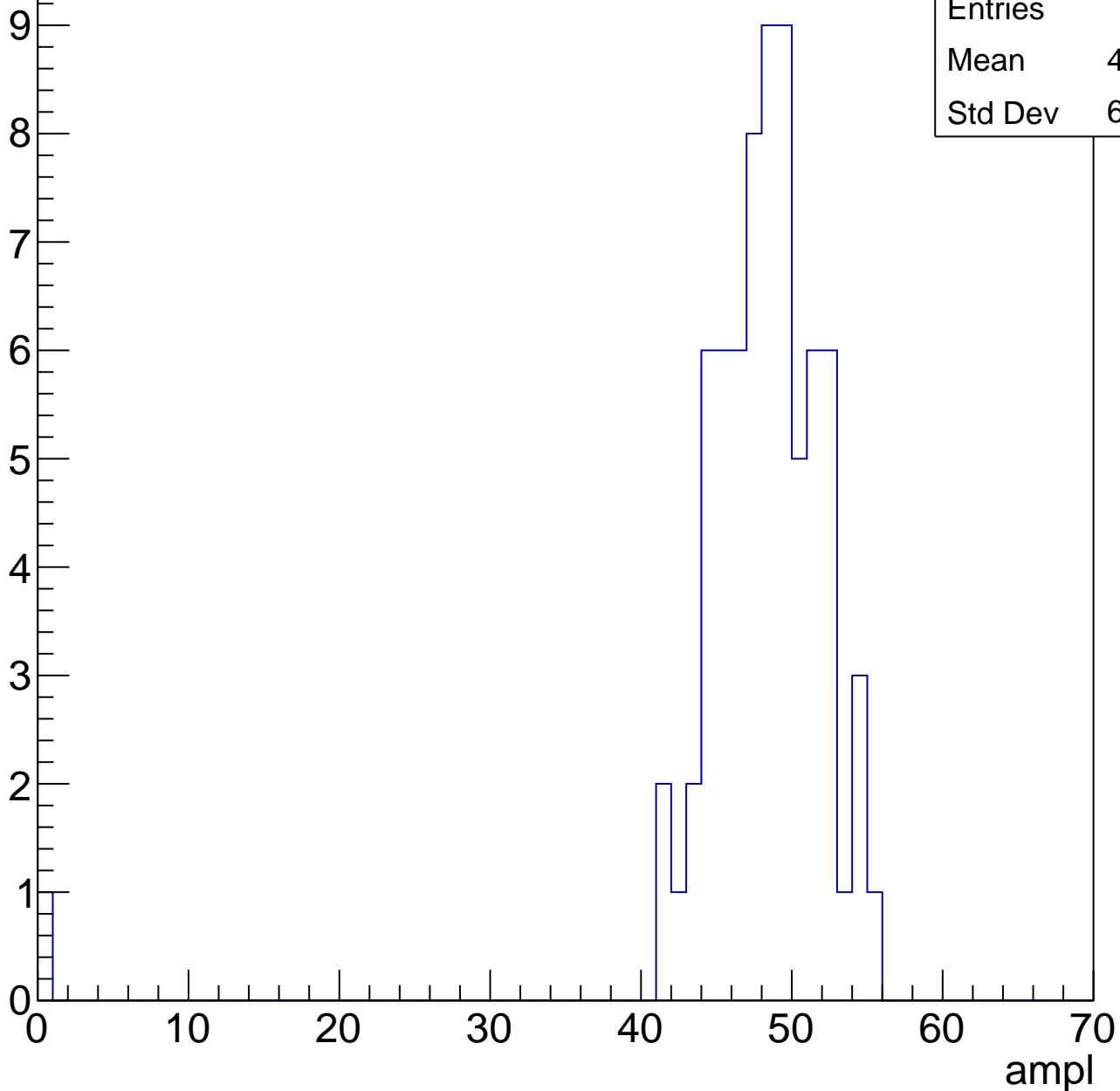


B1L103S, U2-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	47.32
Std Dev	6.452

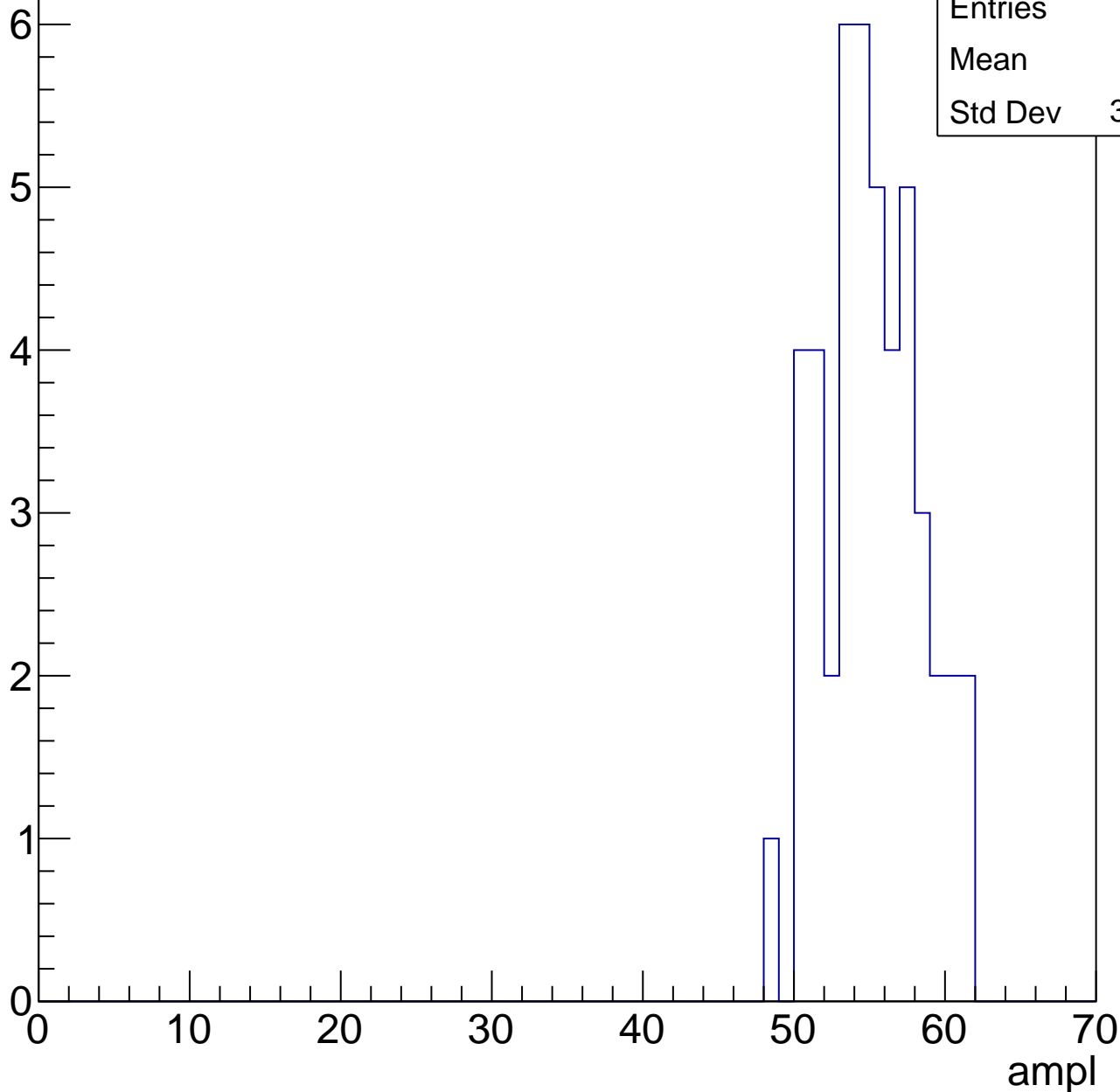


B1L103S, U2-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	54.7
Std Dev	3.168

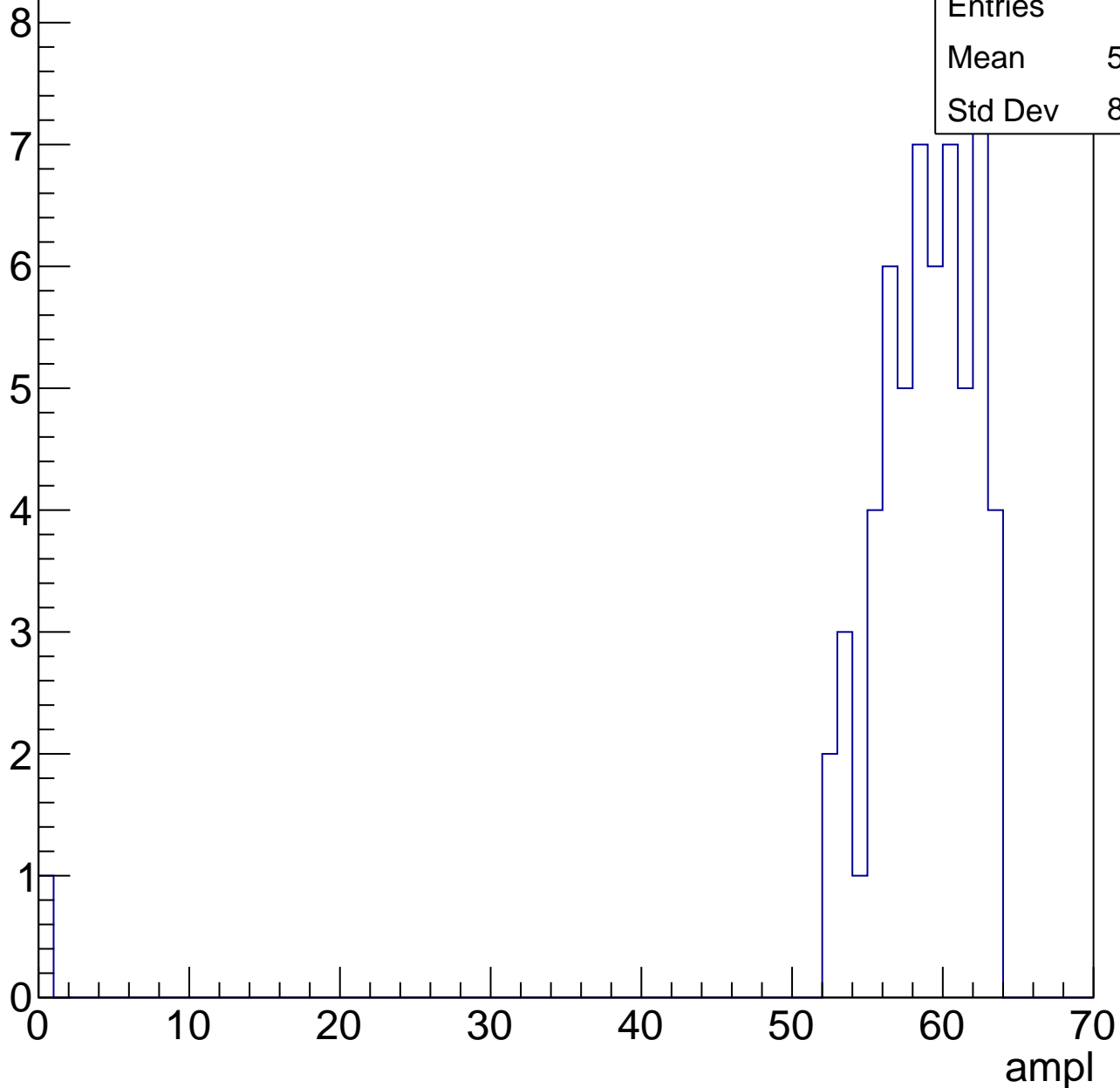


B1L103S, U2-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.47
Std Dev	8.112

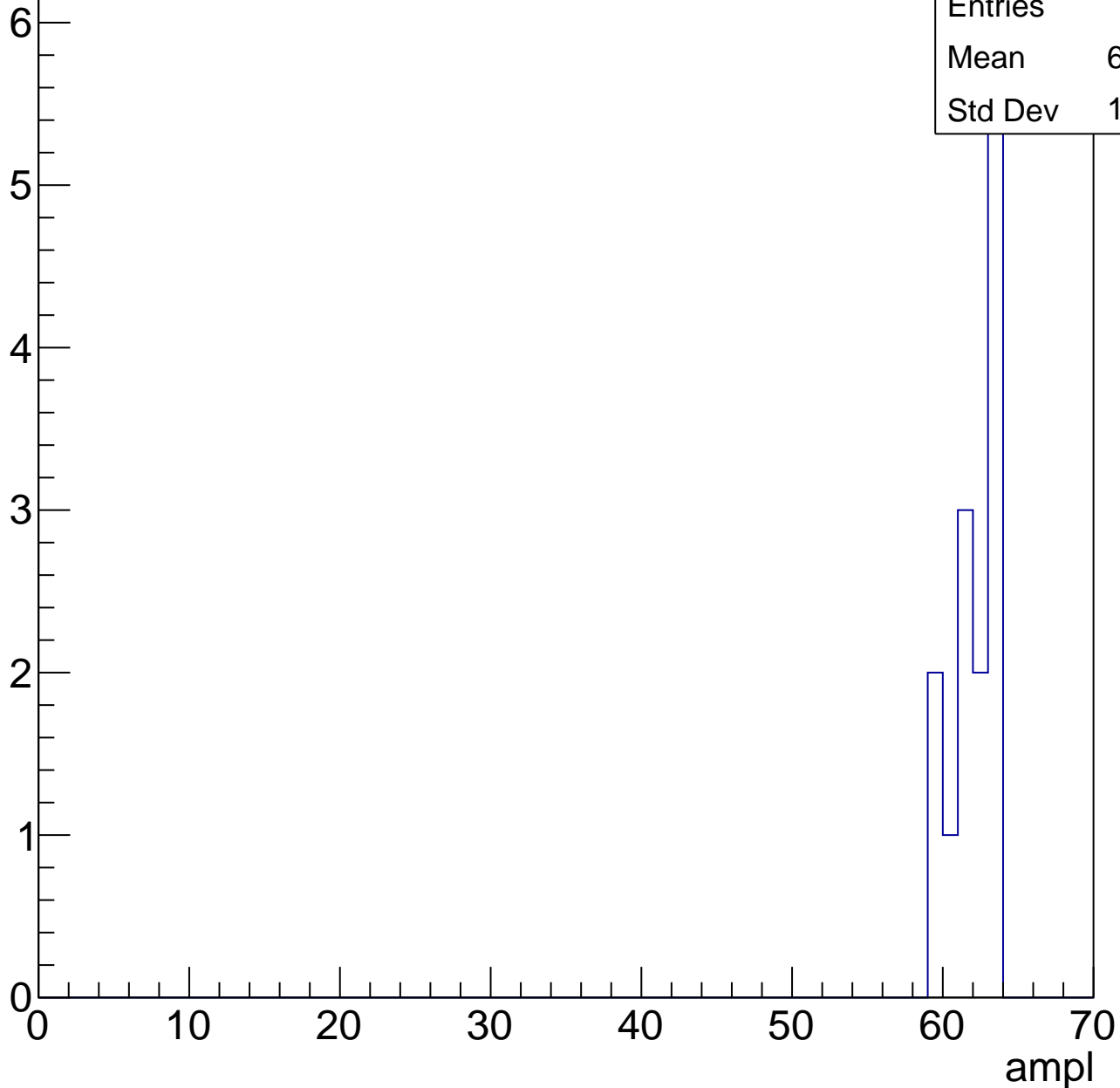


B1L103S, U2-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.64
Std Dev	1.445



B1L103S, U2-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch12, adc0

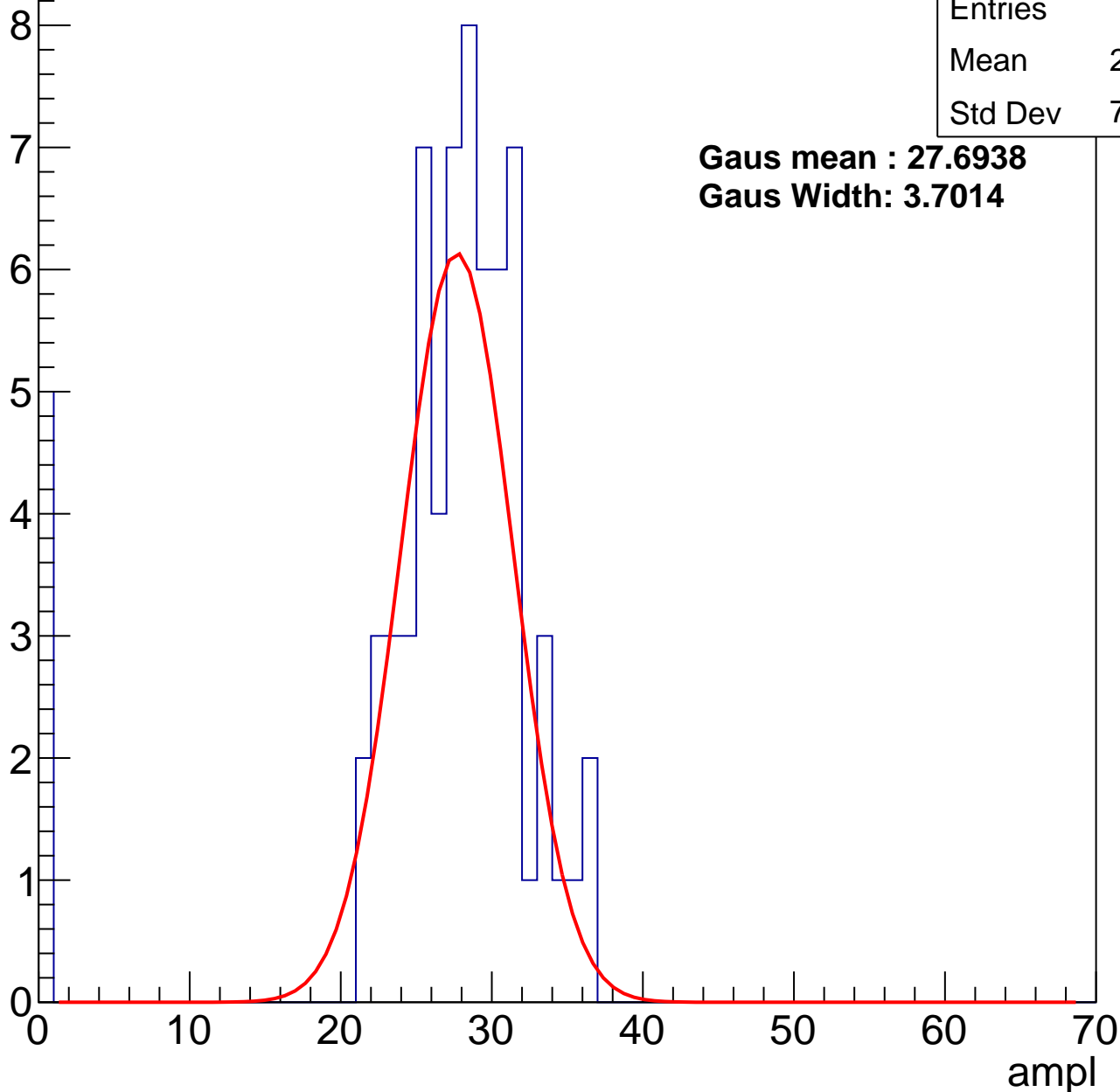
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	25.86
Std Dev	7.999

Gaus mean : 27.6938

Gaus Width: 3.7014



B1L103S, U2-ch12, adc1

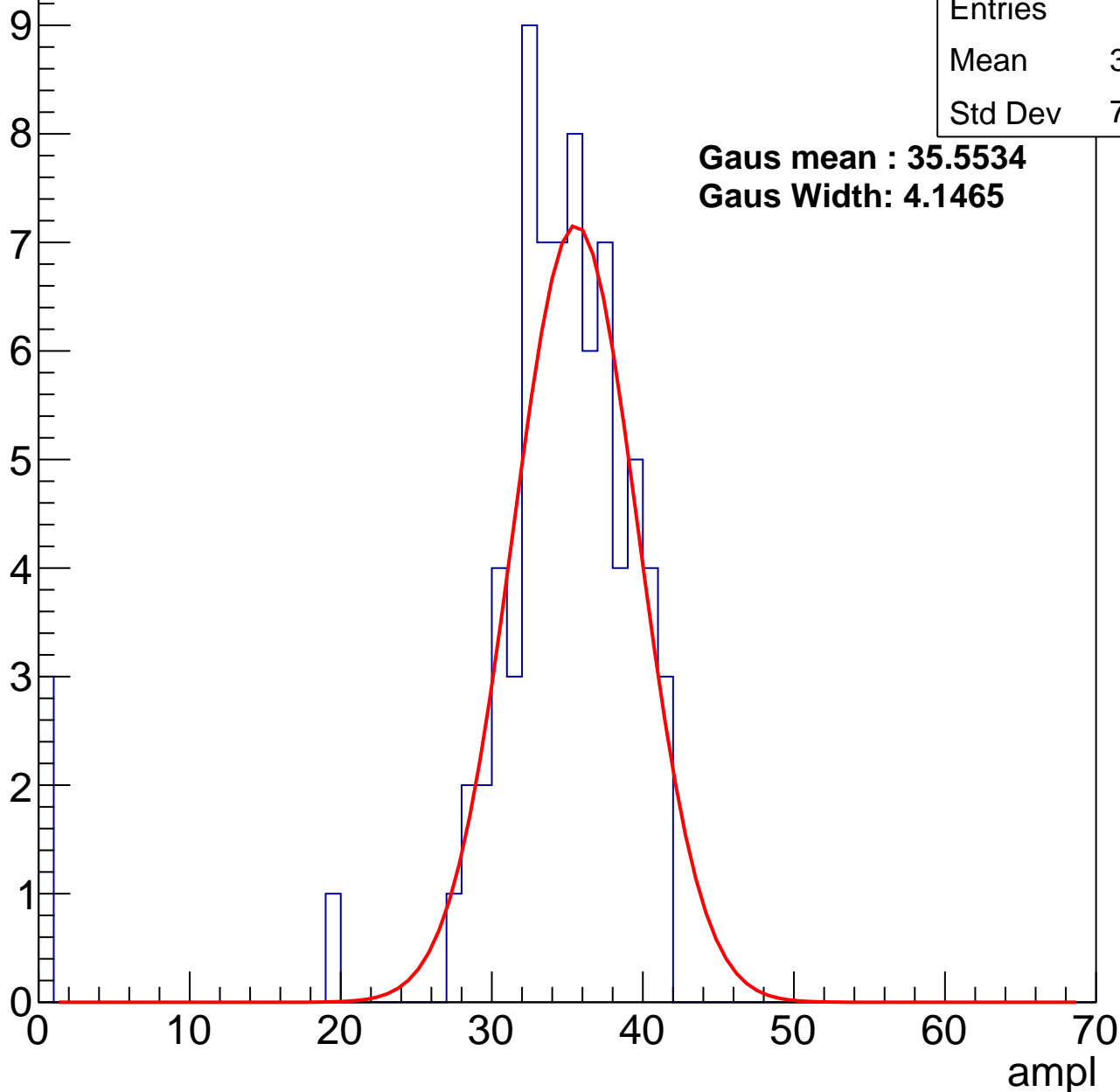
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	33.09
Std Dev	7.704

Gaus mean : 35.5534

Gaus Width: 4.1465



B1L103S, U2-ch12, adc2

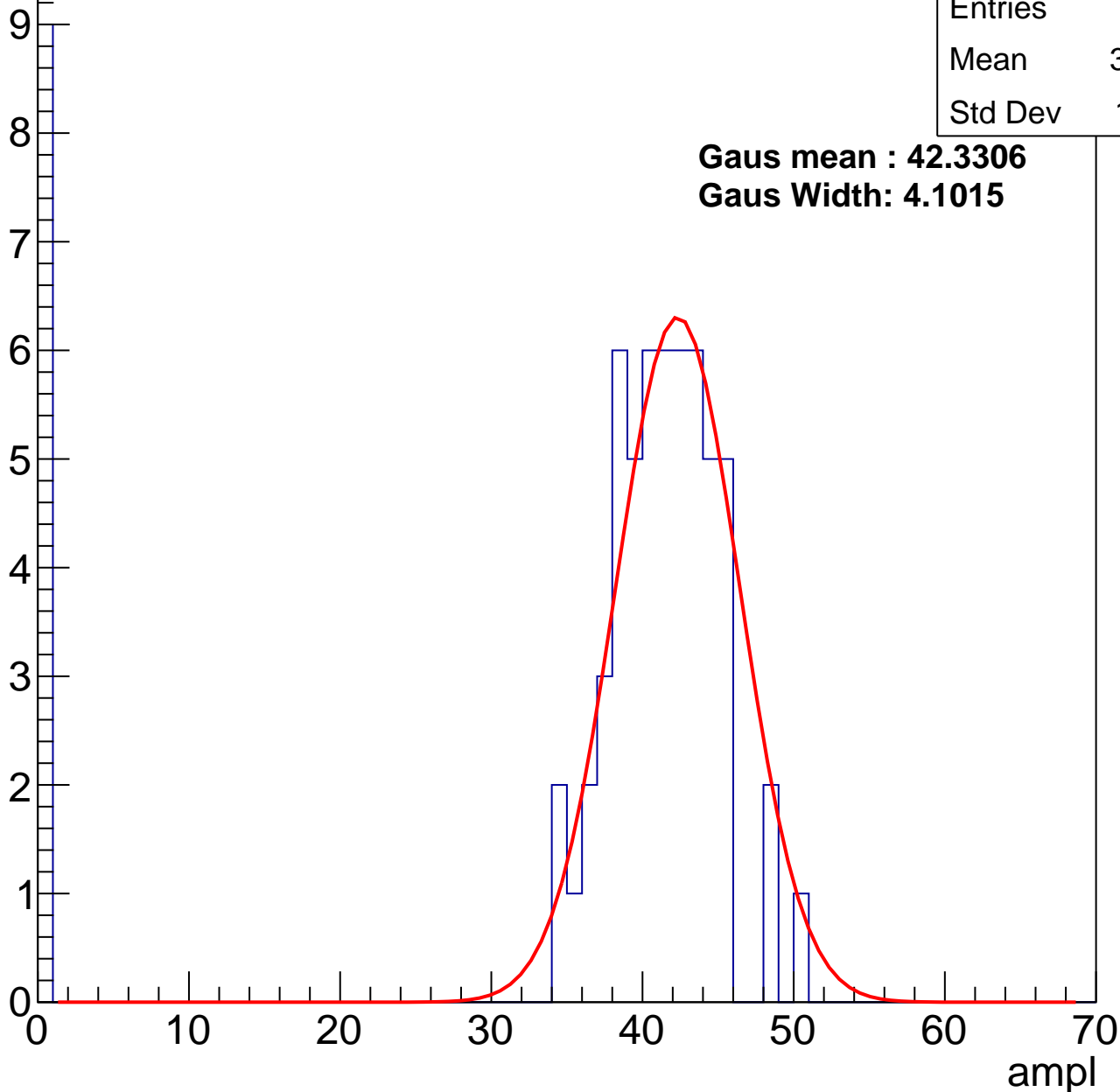
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	35.32
Std Dev	14.51

Gaus mean : 42.3306

Gaus Width: 4.1015

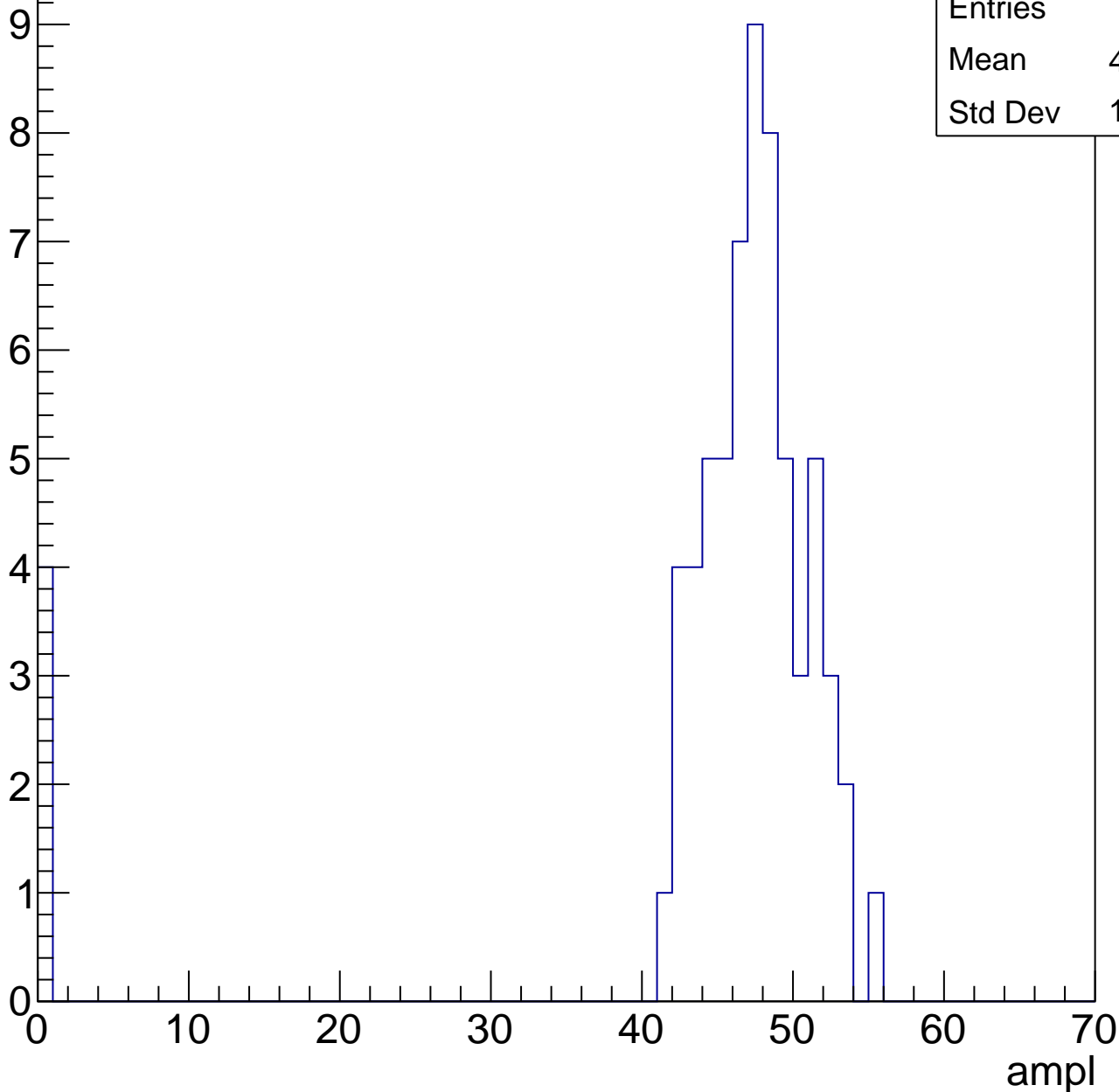


B1L103S, U2-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	44.27
Std Dev	11.65

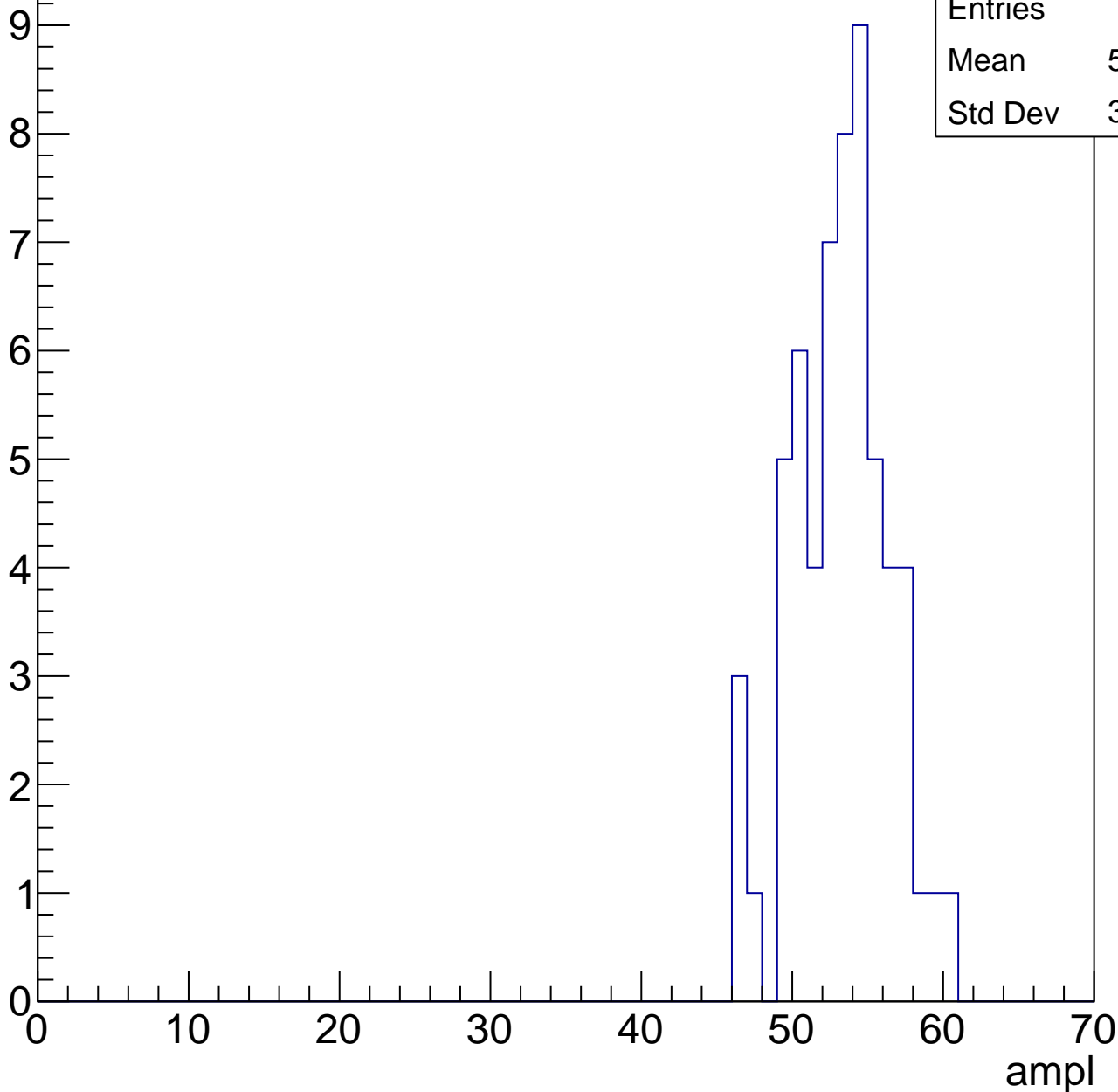


B1L103S, U2-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	52.75
Std Dev	3.122

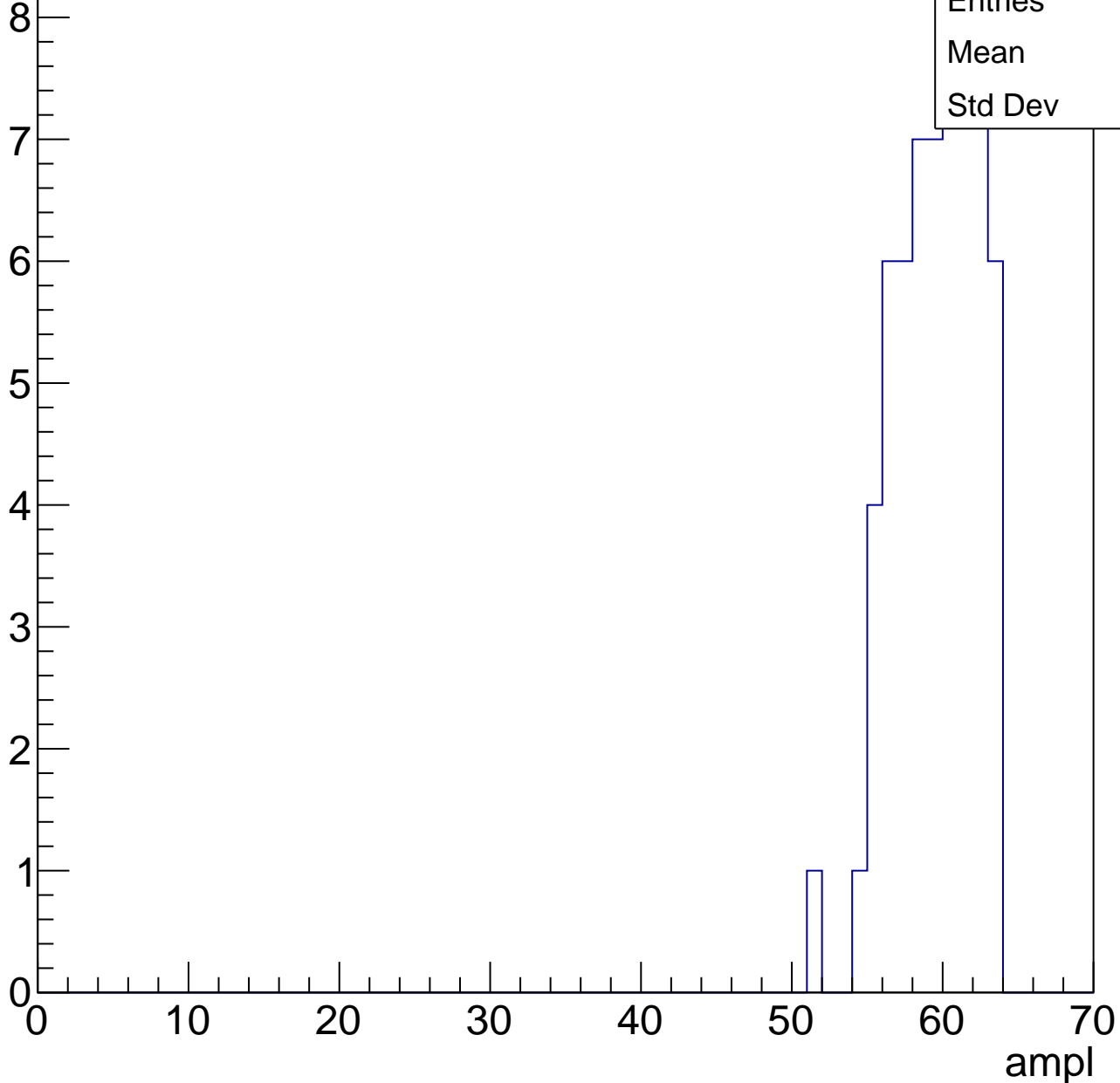


B1L103S, U2-ch12, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

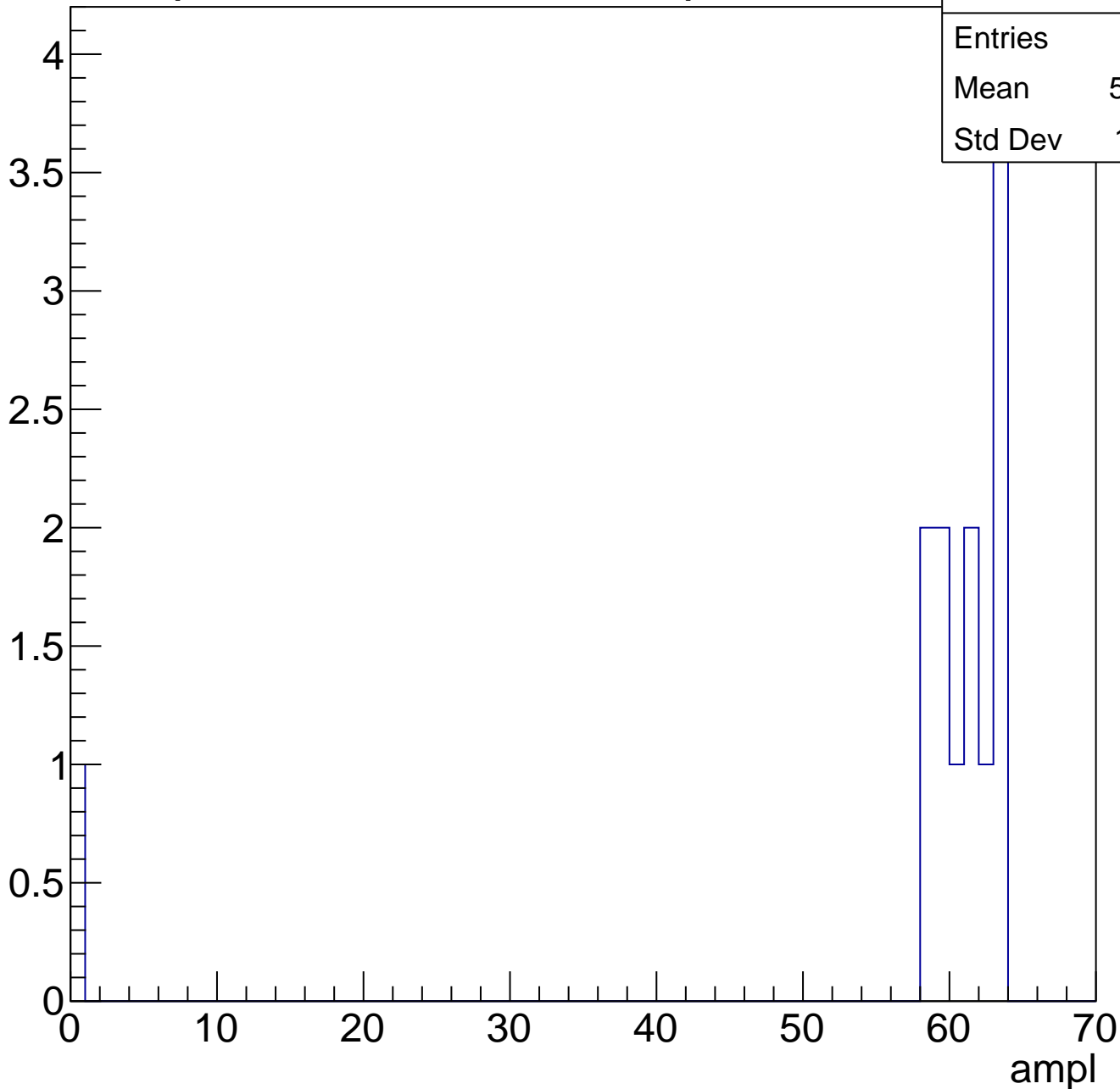
Entries	62
Mean	59.1
Std Dev	2.68



B1L103S, U2-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

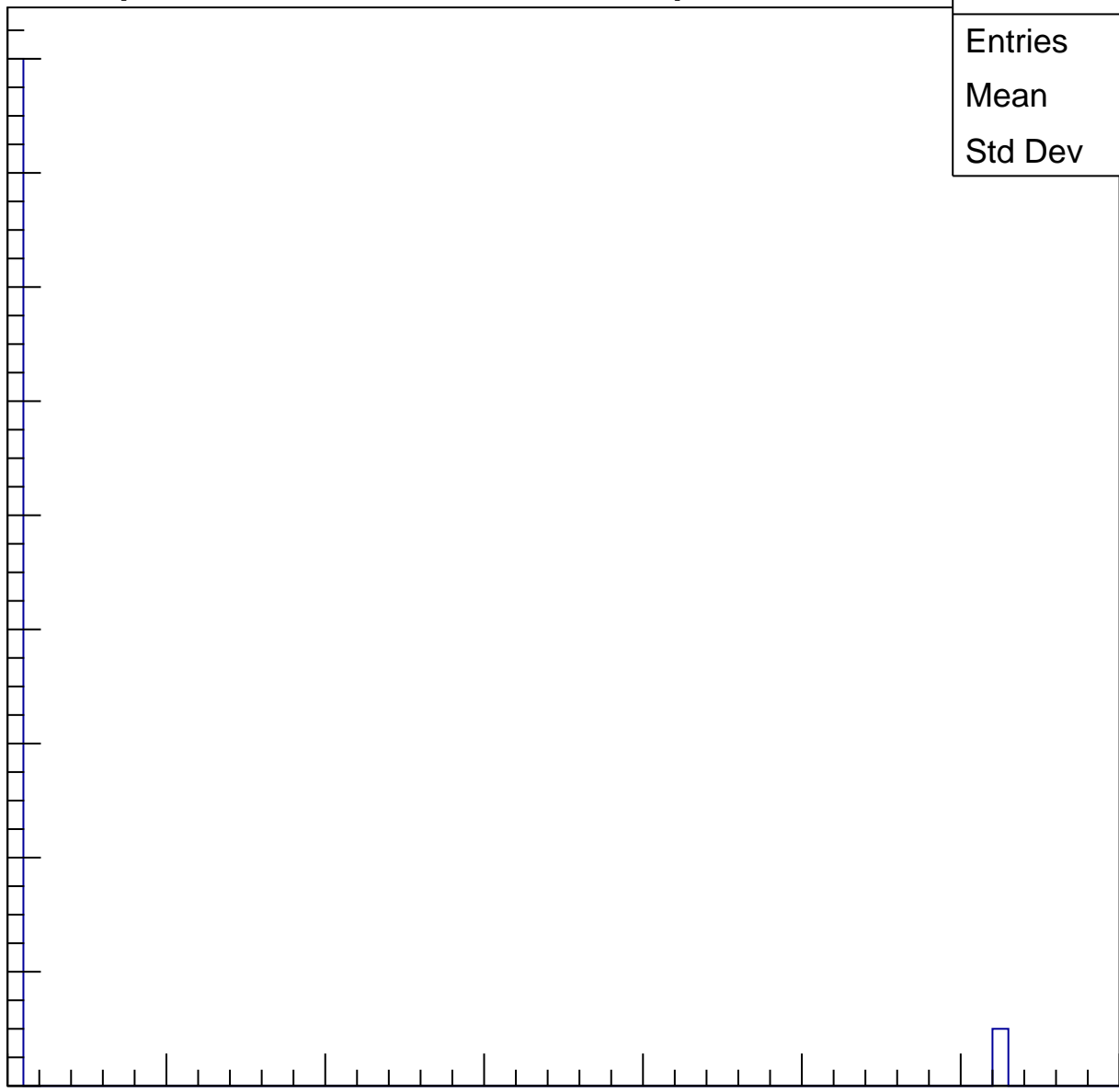
Entries	19
Mean	3.263
Std Dev	13.84

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch13, adc0

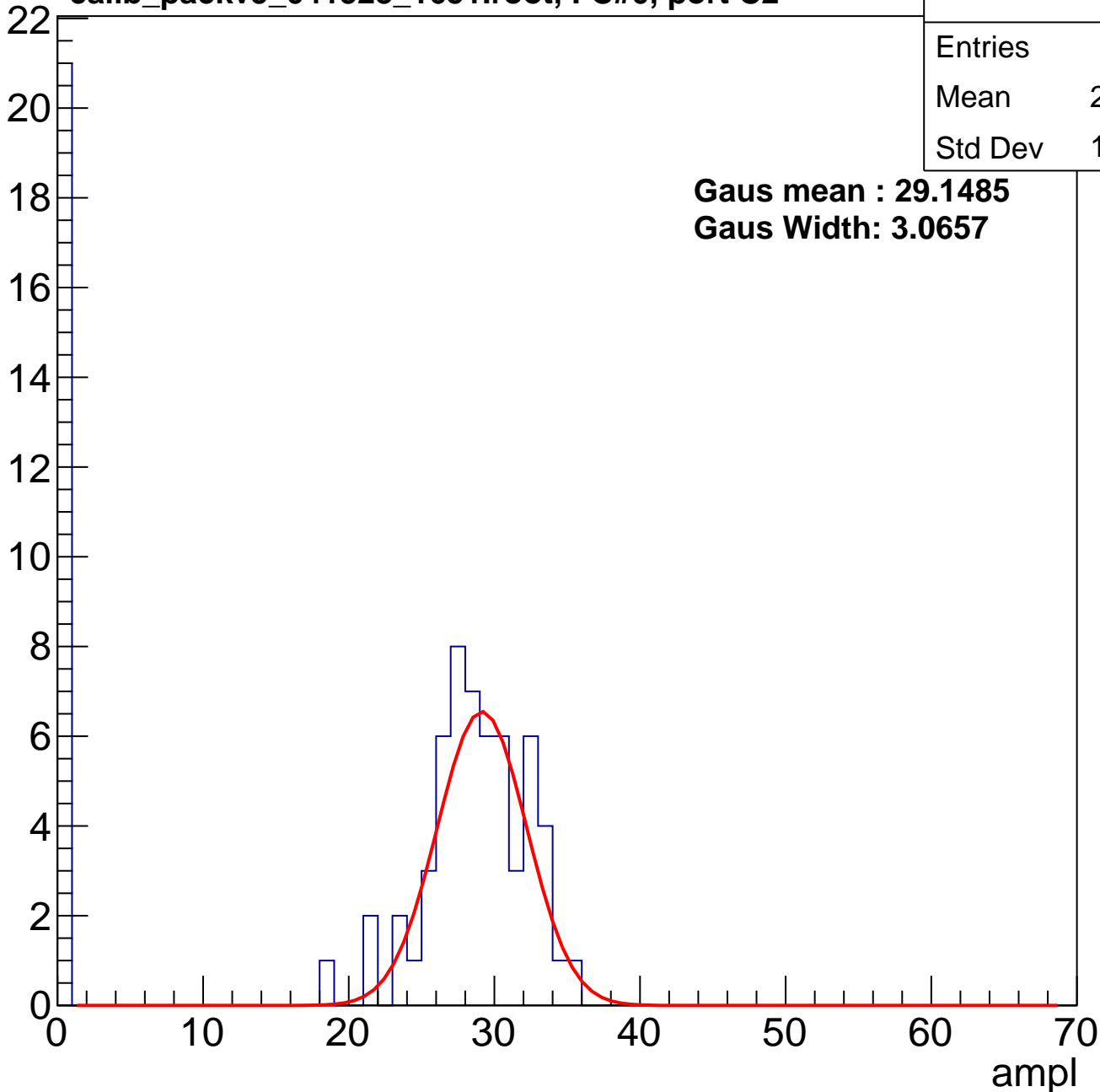
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	20.68
Std Dev	12.88

Gaus mean : 29.1485

Gaus Width: 3.0657

Entry



B1L103S, U2-ch13, adc1

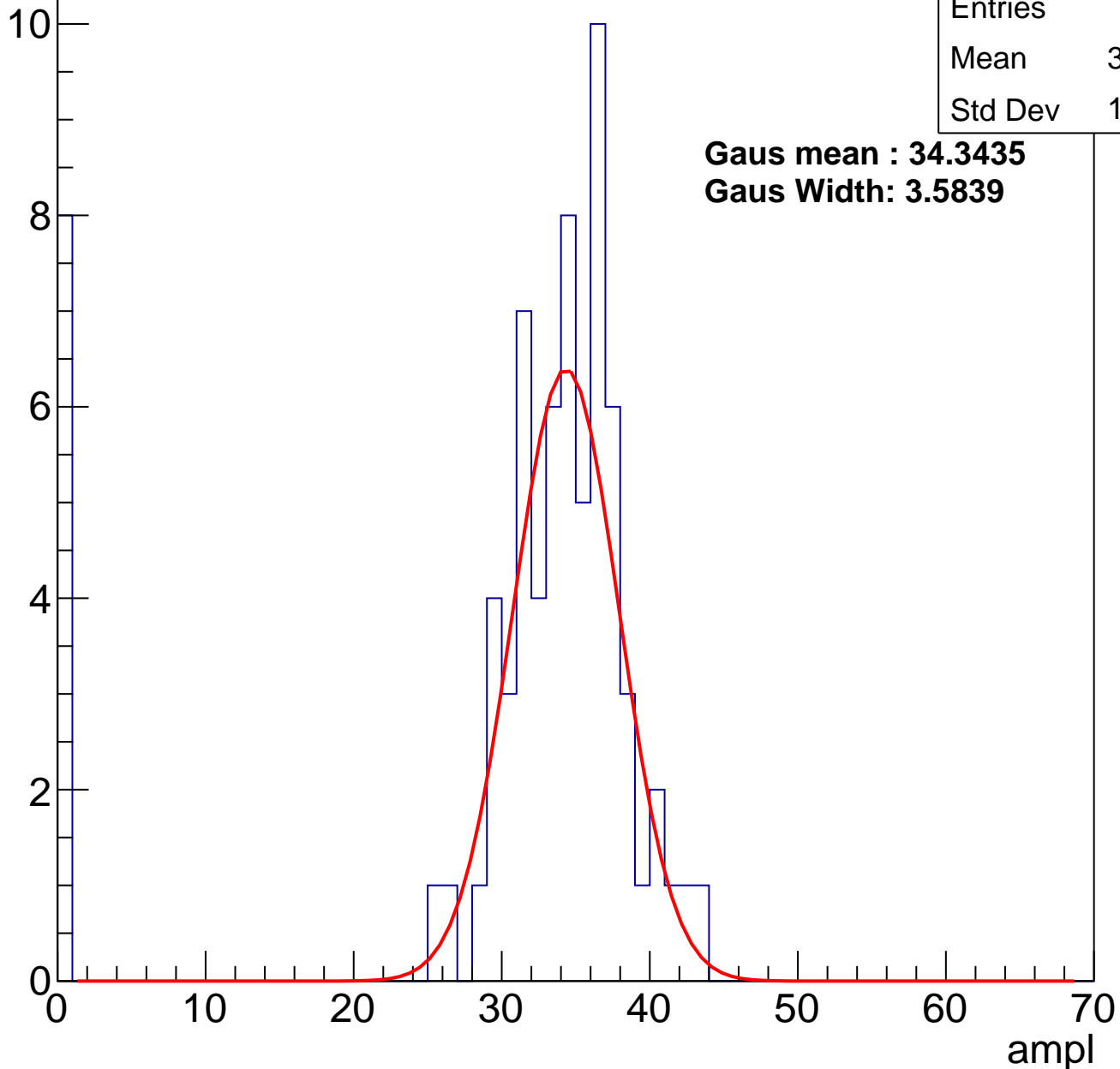
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	30.36
Std Dev	11.18

Gaus mean : 34.3435

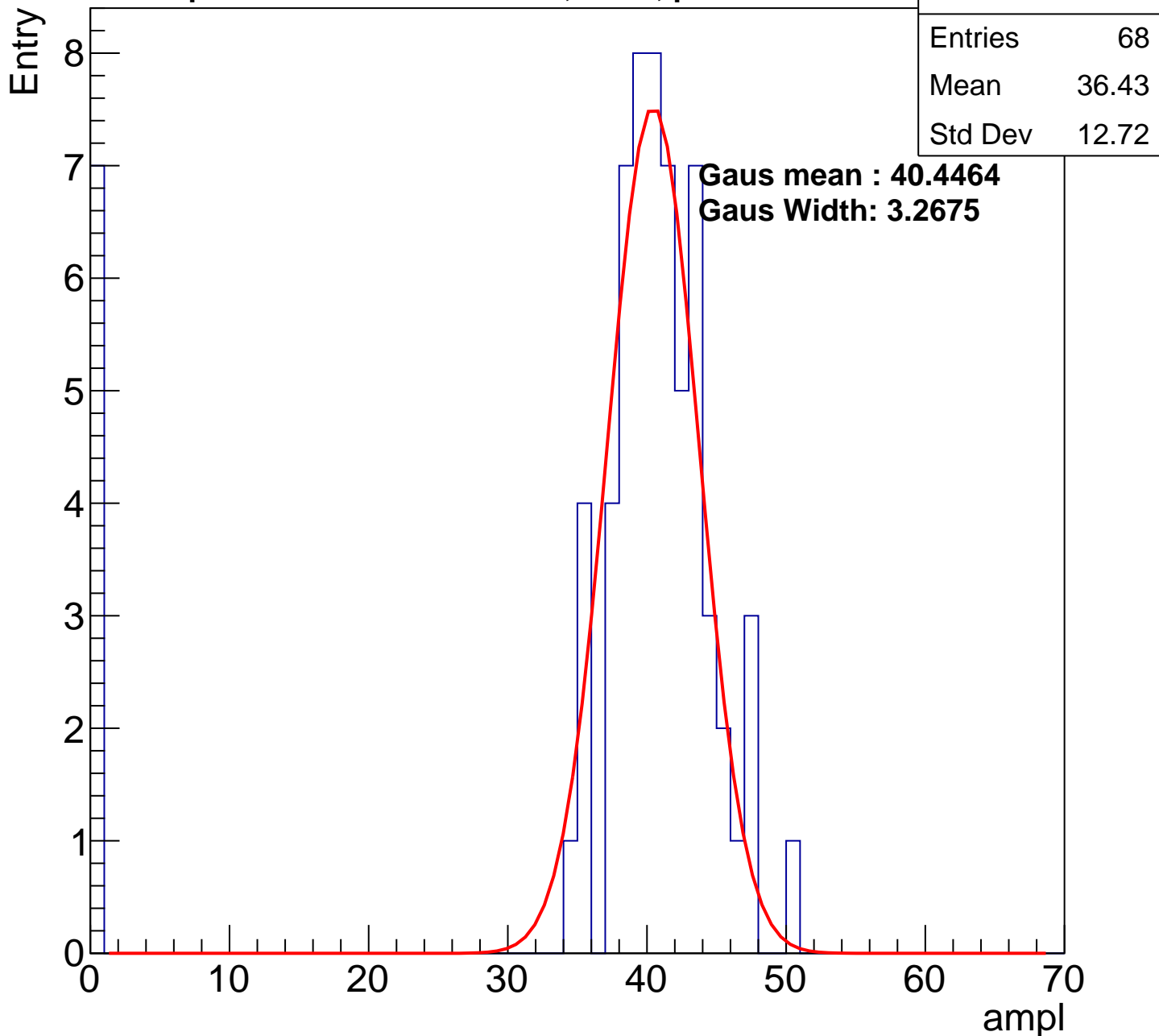
Gaus Width: 3.5839

Entry



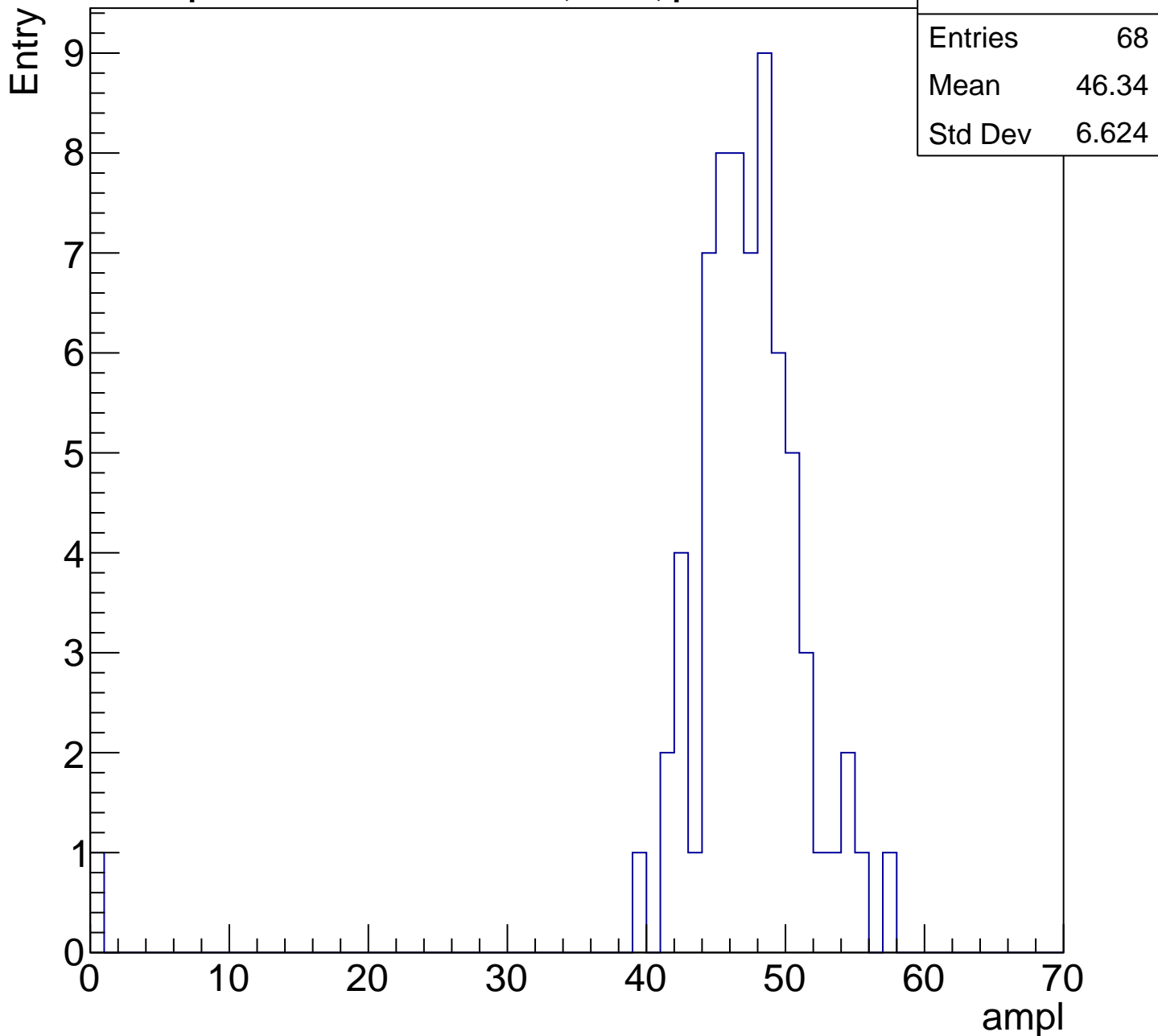
B1L103S, U2-ch13, adc2

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

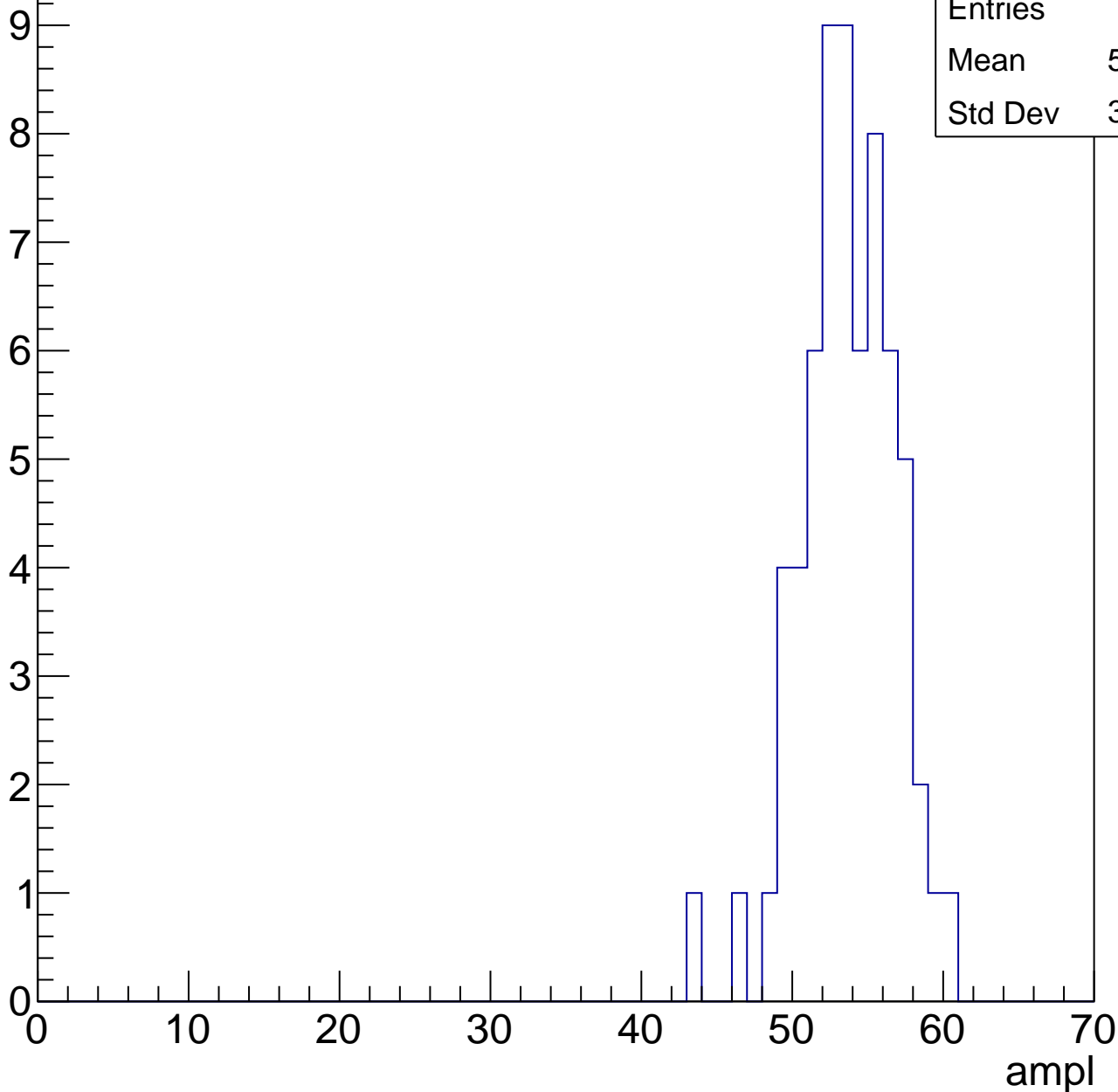


B1L103S, U2-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	53.19
Std Dev	3.092

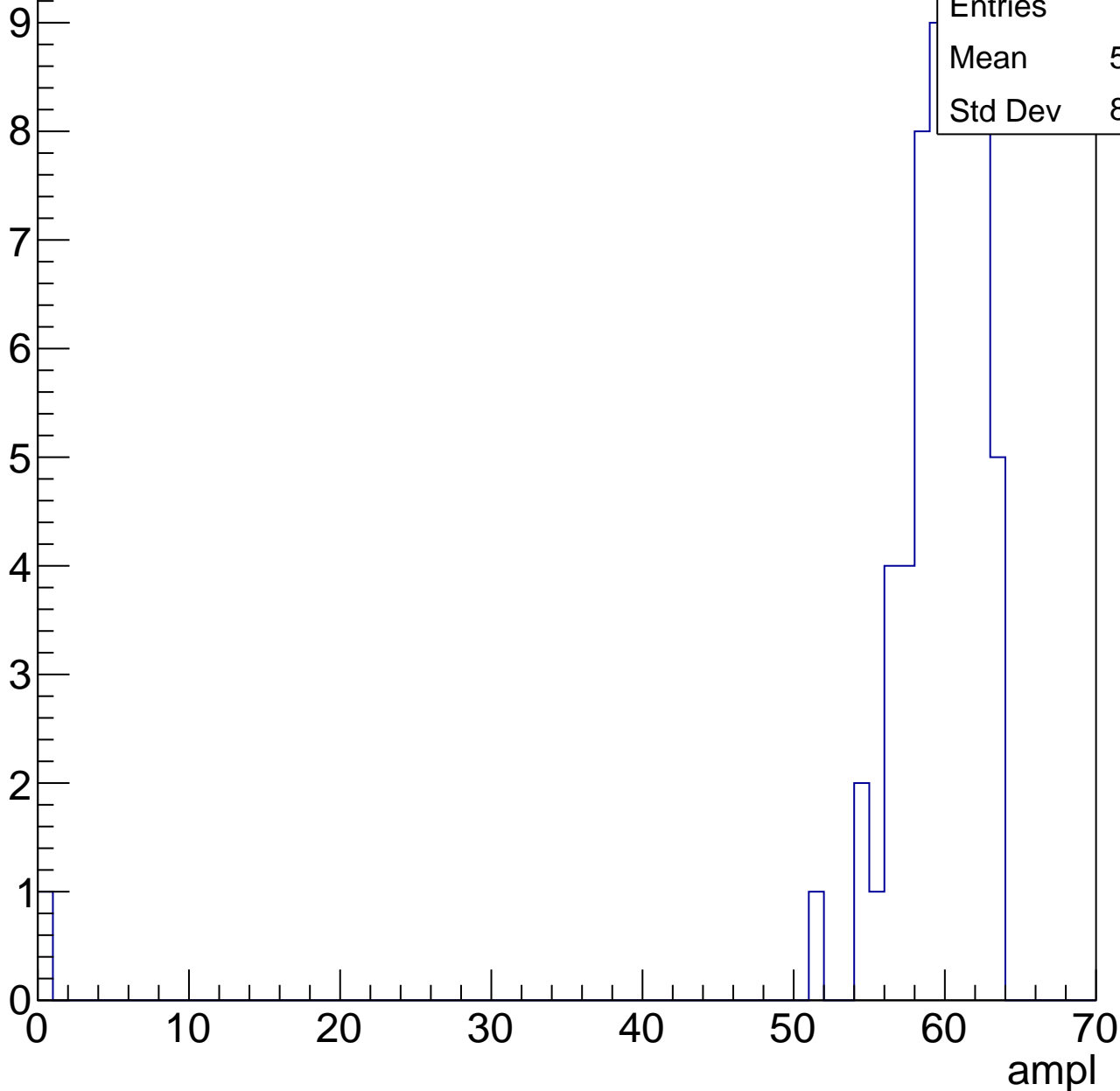


B1L103S, U2-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	58.35
Std Dev	8.002

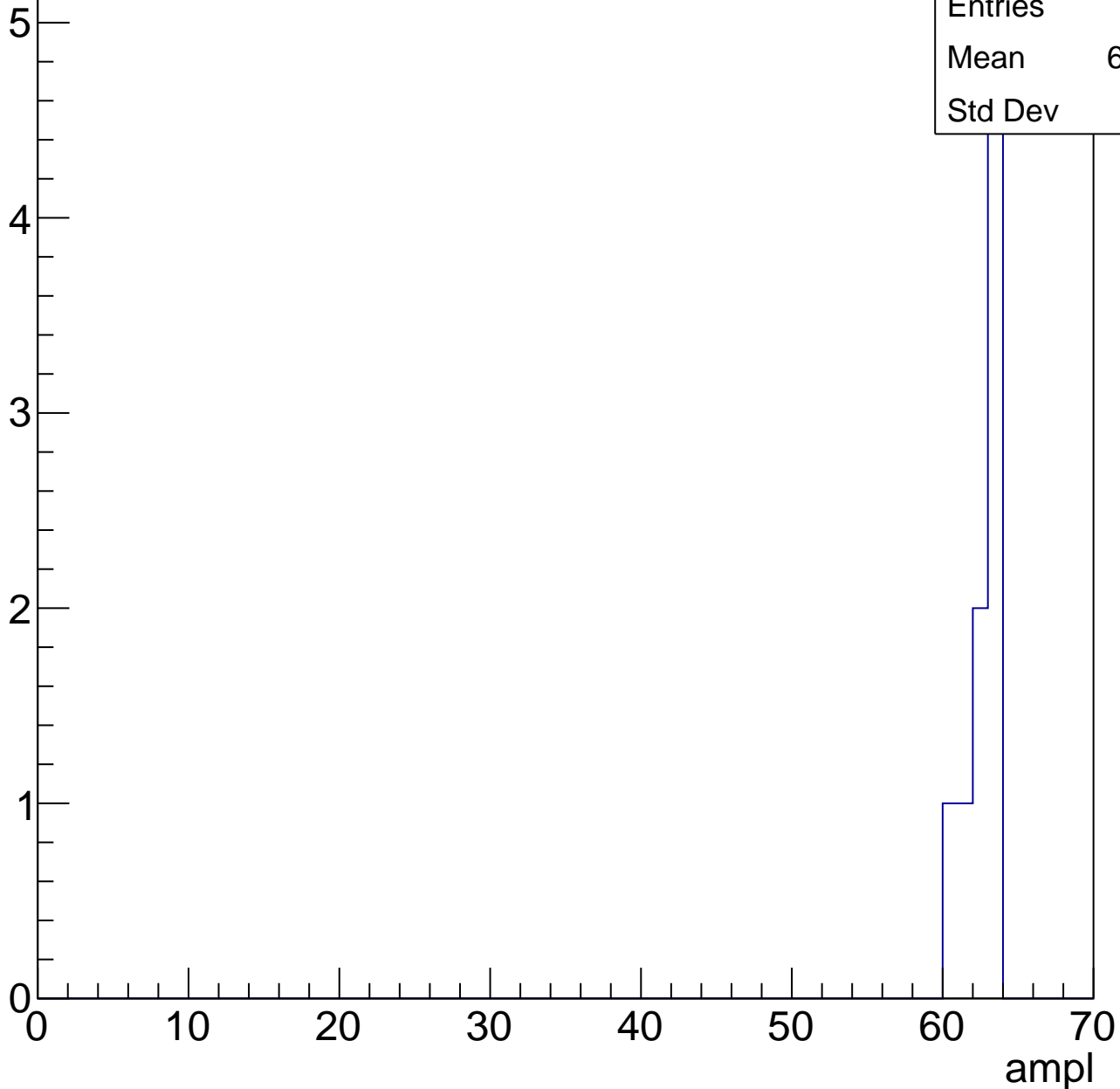


B1L103S, U2-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	62.22
Std Dev	1.03



B1L103S, U2-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch14, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	21.56
Std Dev	12.67

Gaus mean : 29.8167

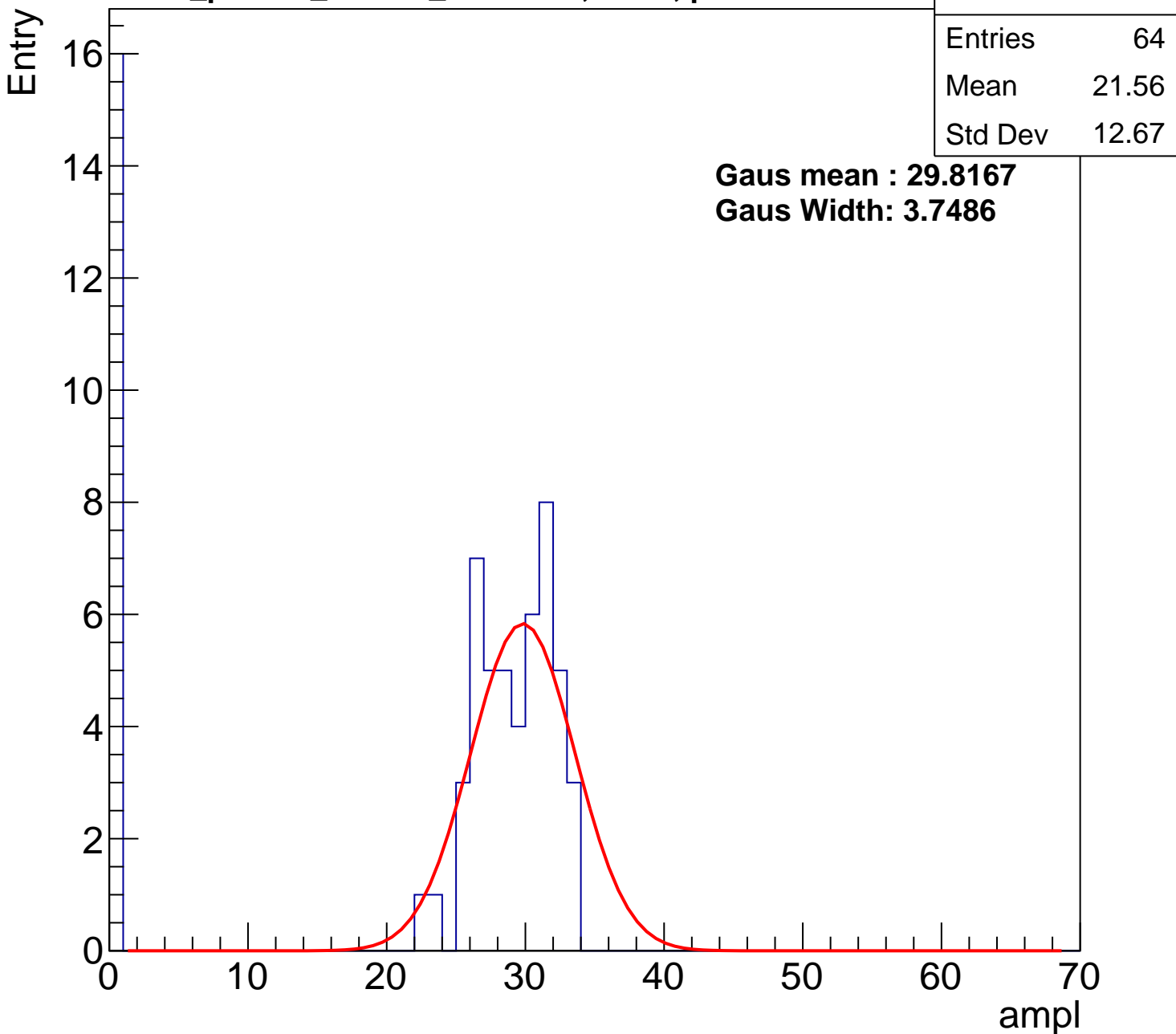
Gaus Width: 3.7486

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch14, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	30.33
Std Dev	13.19

Gaus mean : 36.1212

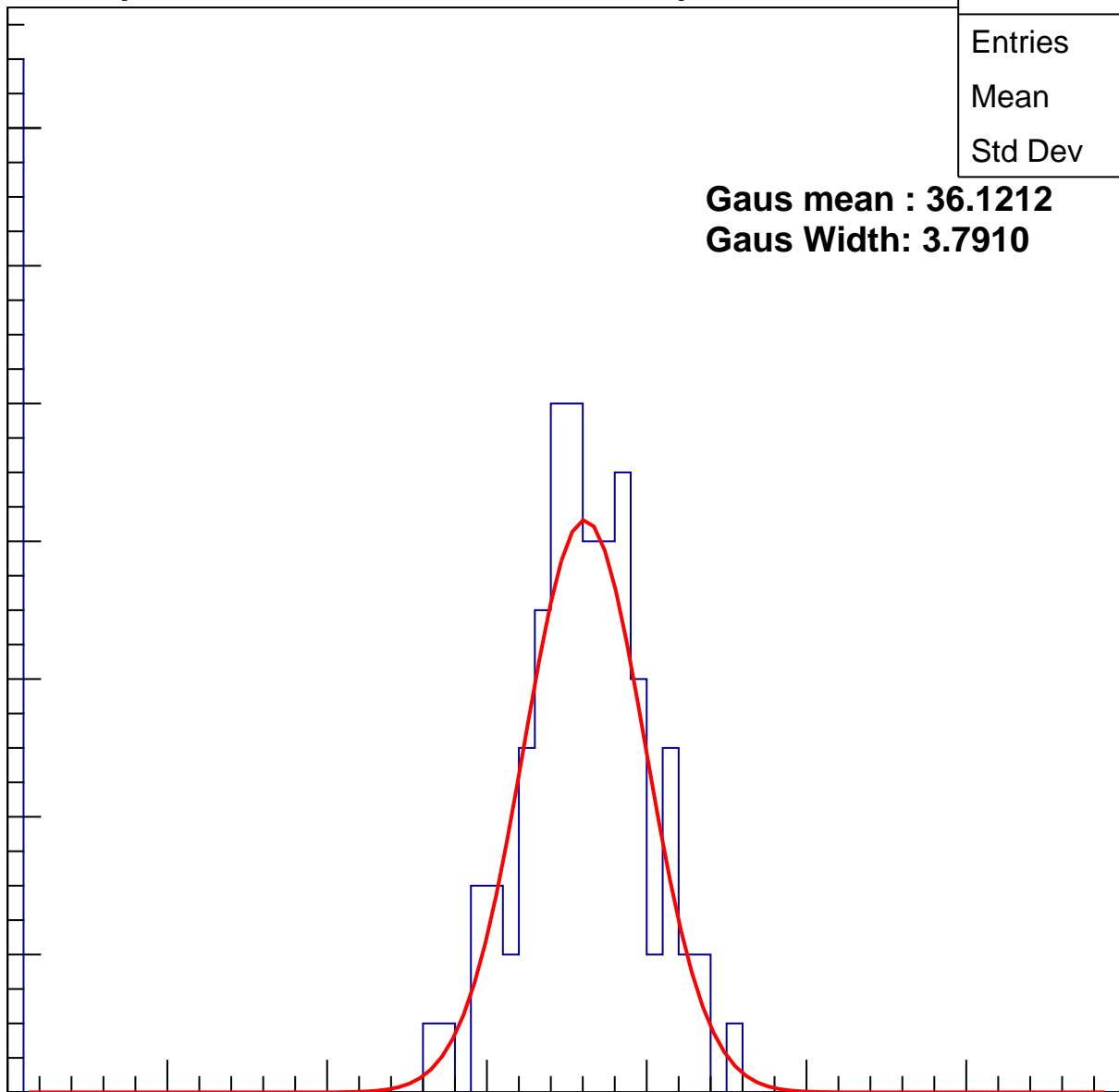
Gaus Width: 3.7910

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch14, adc2

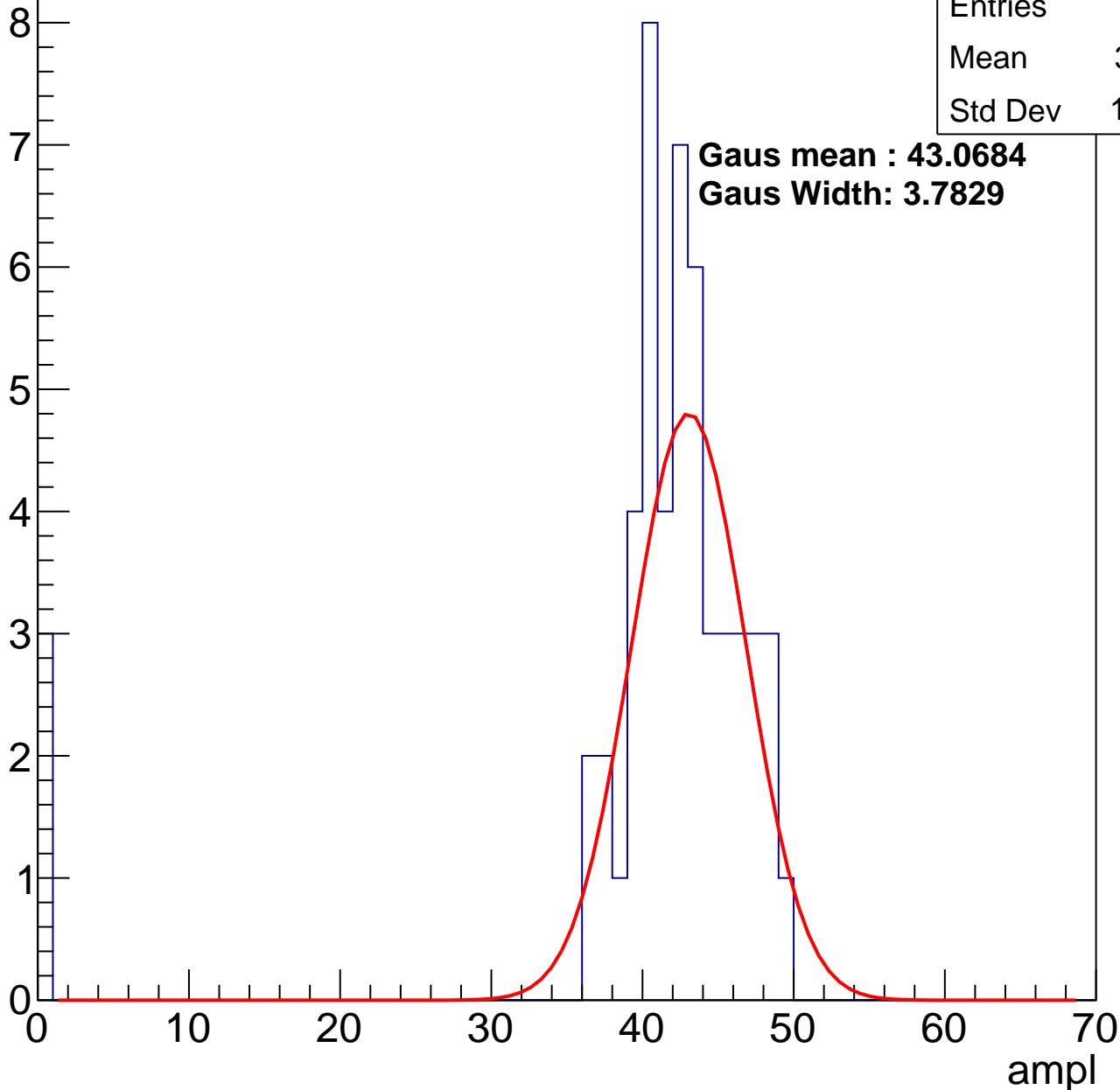
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	39.91
Std Dev	10.27

Gaus mean : 43.0684

Gaus Width: 3.7829

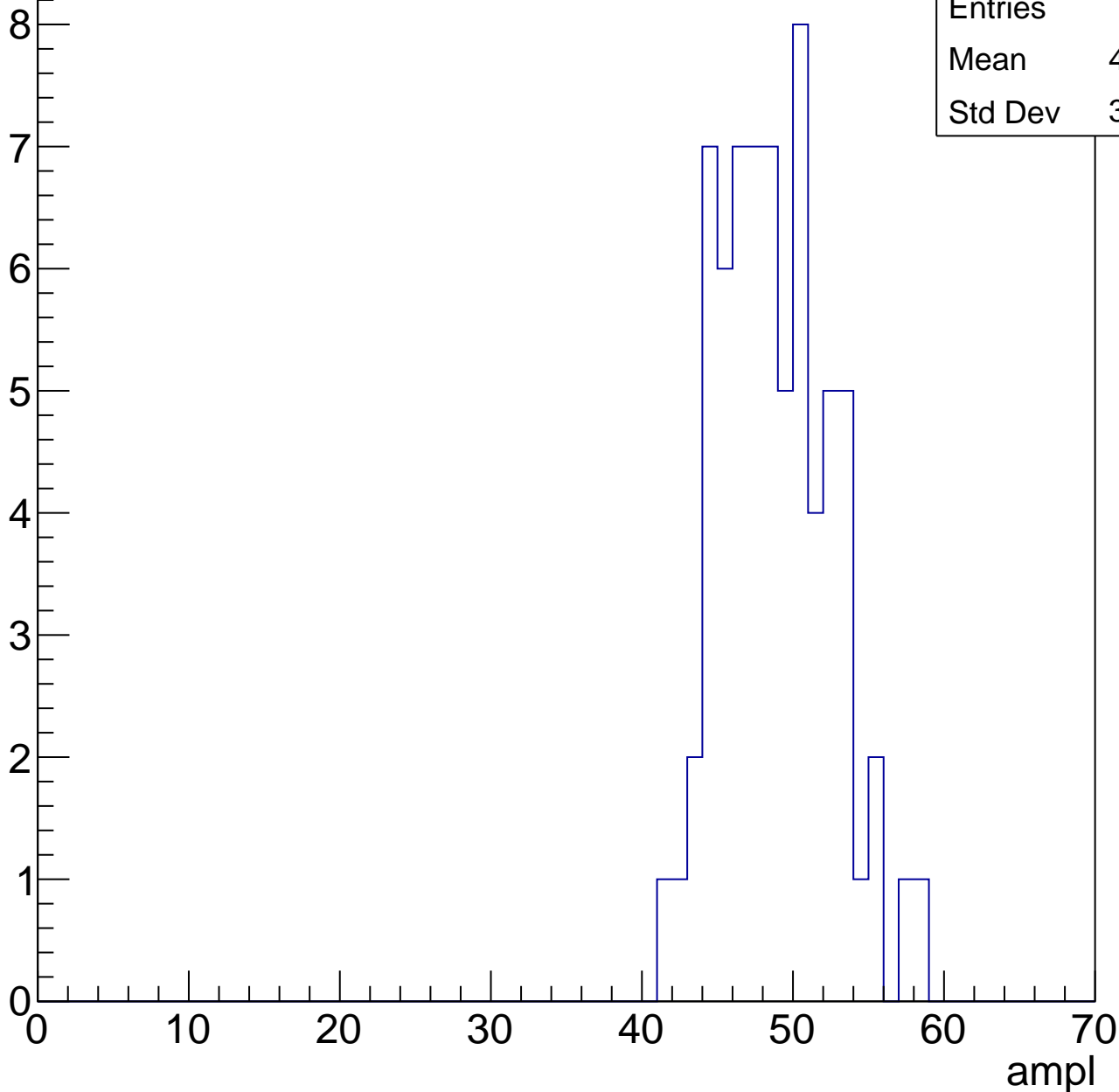


B1L103S, U2-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	48.39
Std Dev	3.623

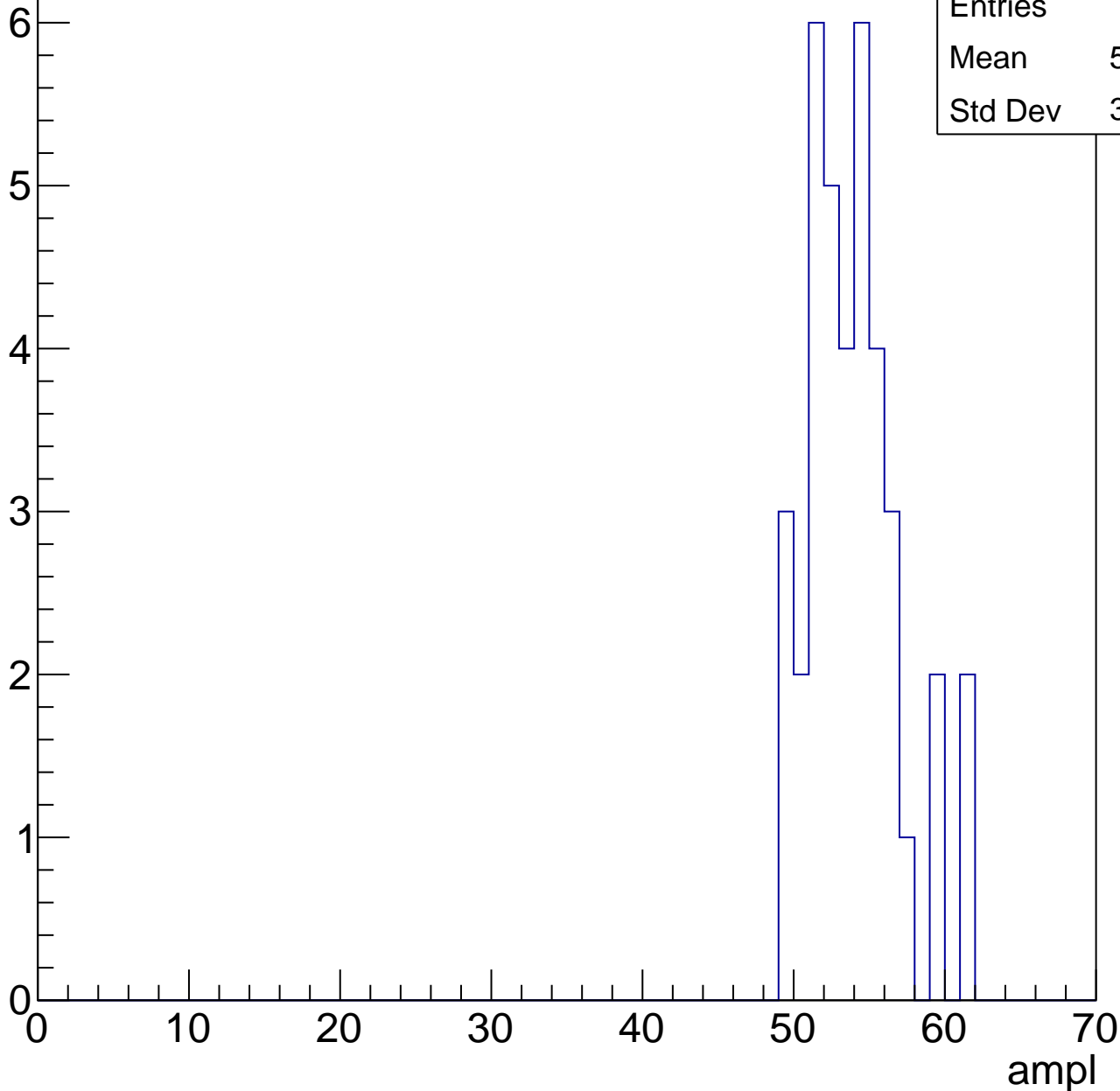


B1L103S, U2-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	53.53
Std Dev	3.033

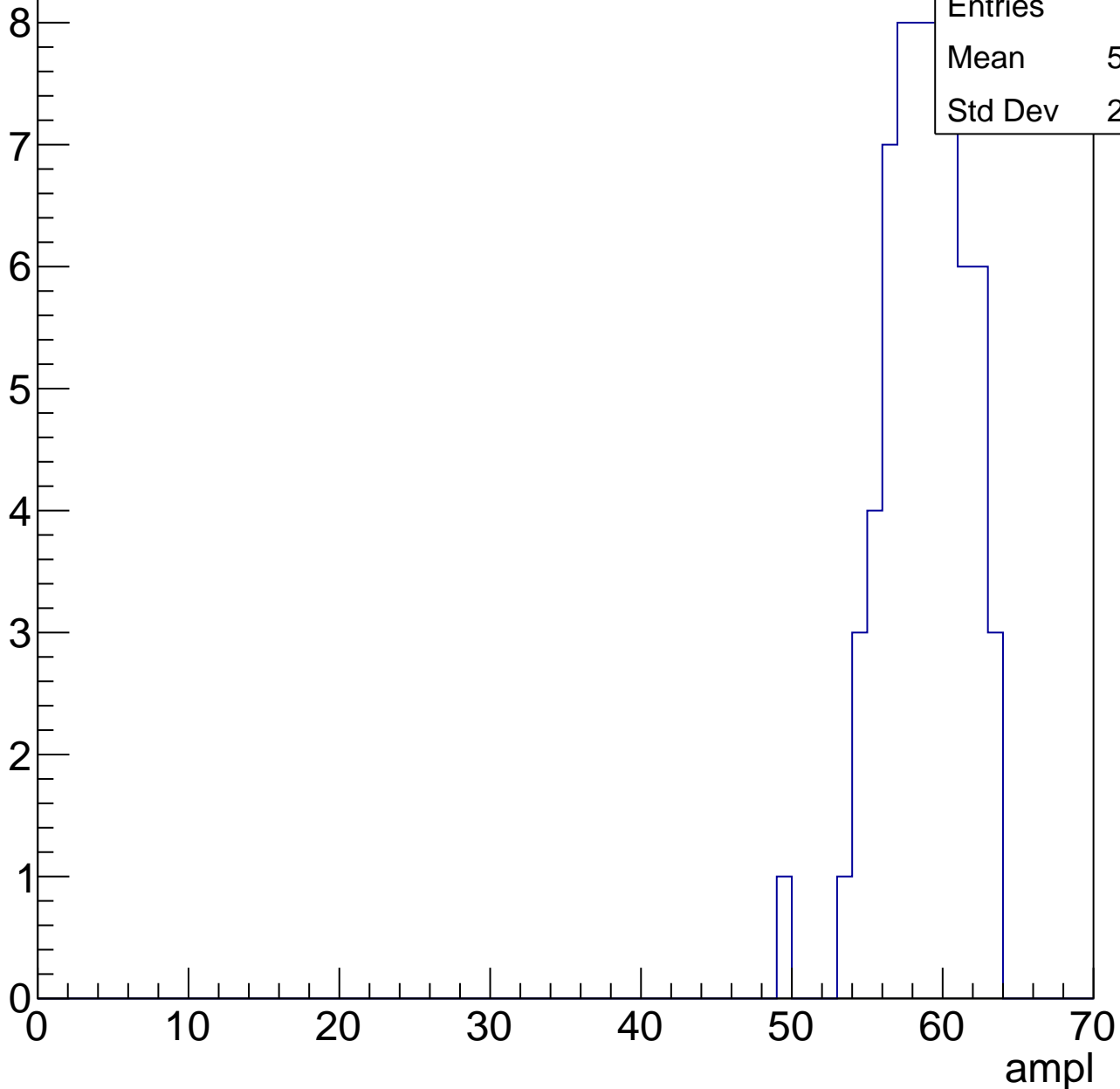


B1L103S, U2-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	58.33
Std Dev	2.772

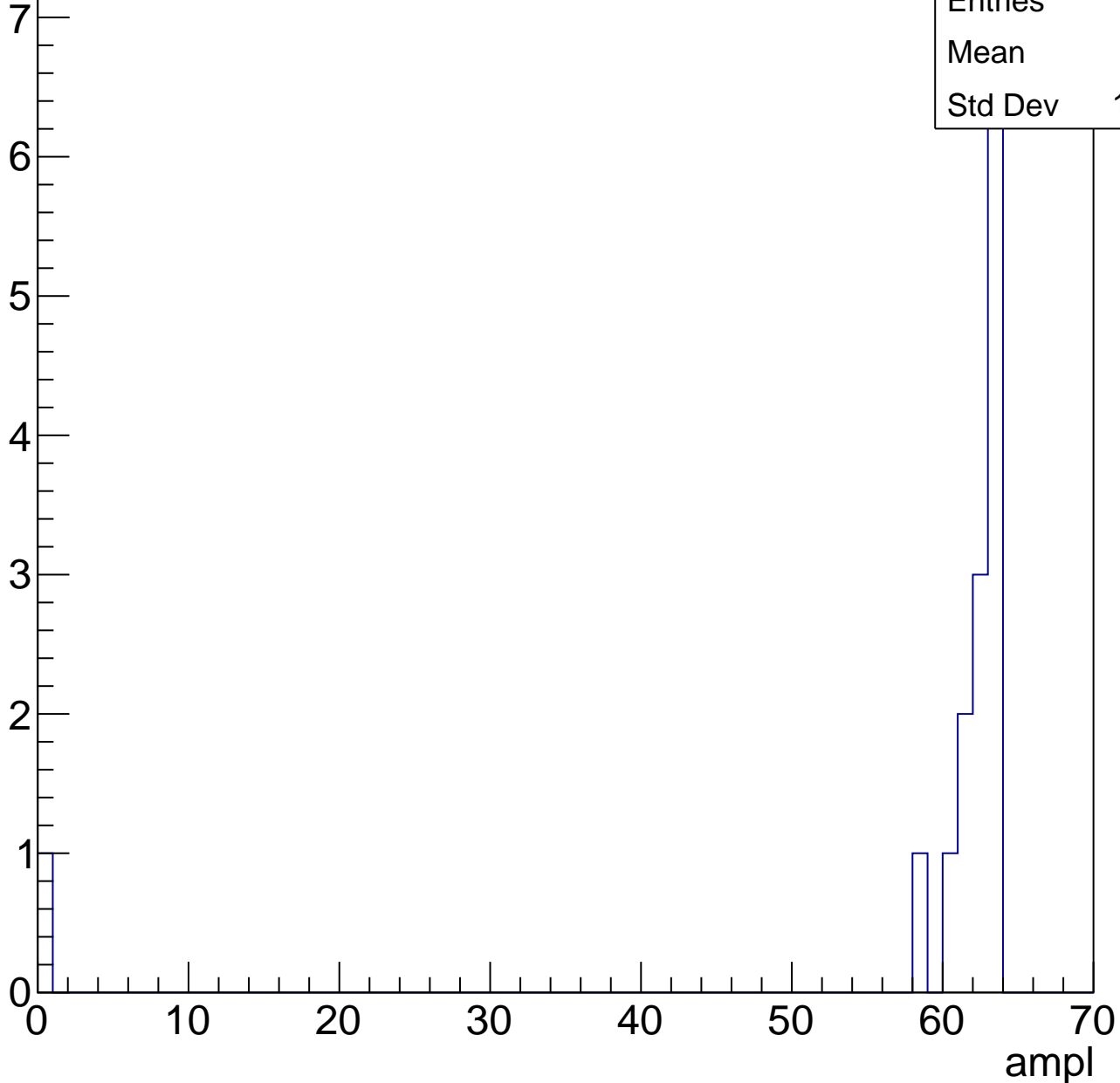


B1L103S, U2-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.8
Std Dev	15.51

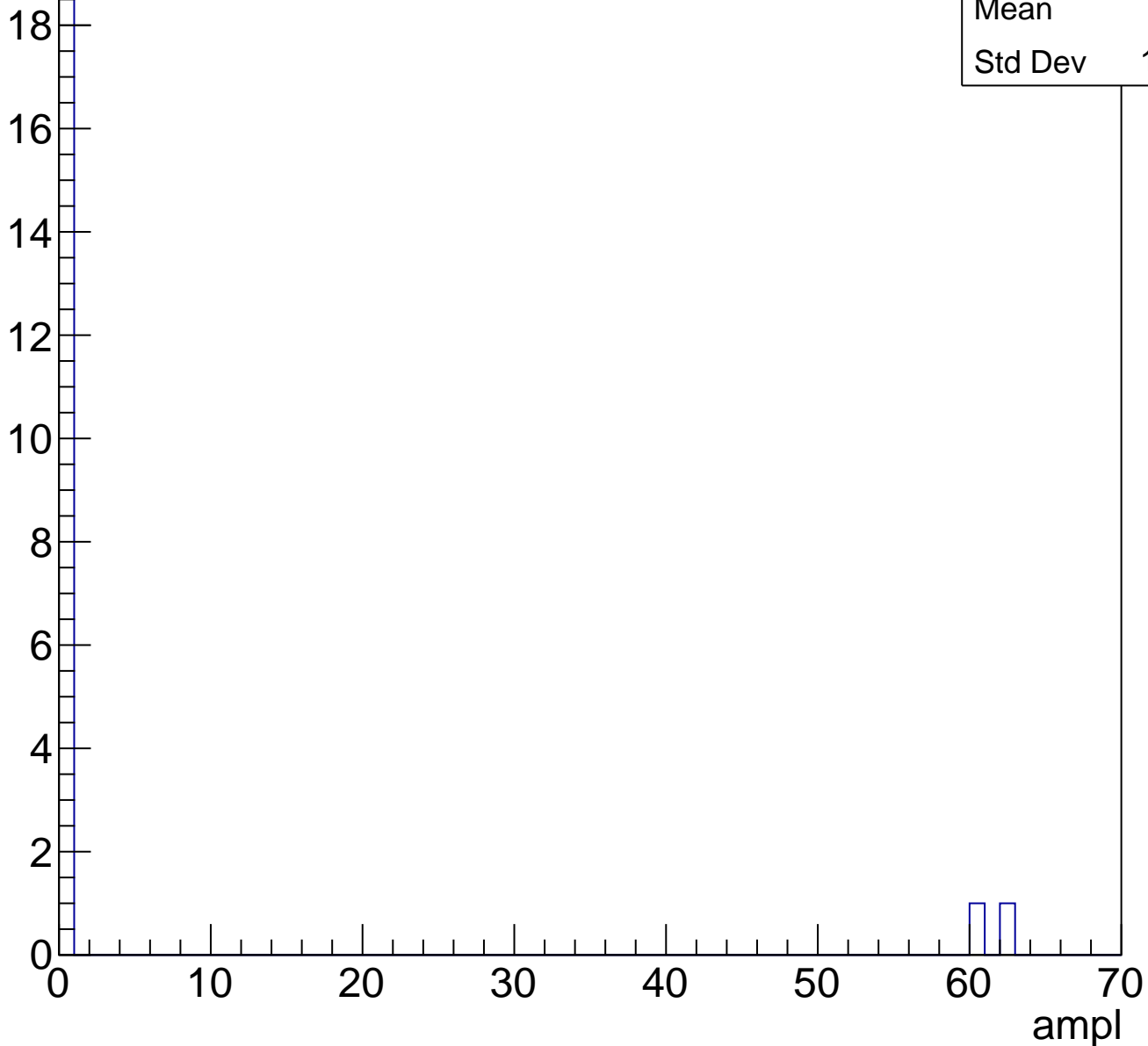


B1L103S, U2-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91

Entry



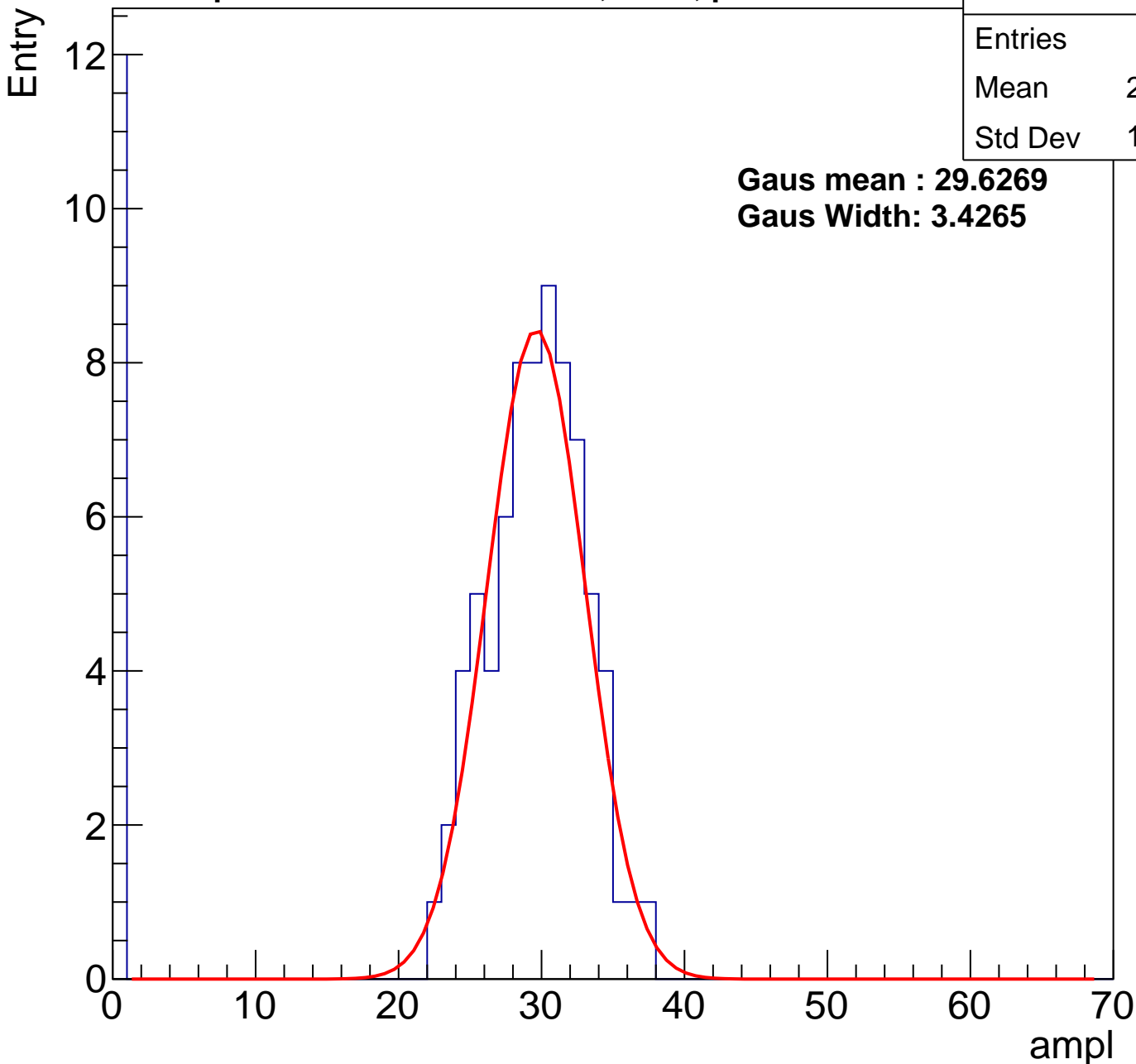
B1L103S, U2-ch15, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	25.14
Std Dev	10.57

Gaus mean : 29.6269

Gaus Width: 3.4265



B1L103S, U2-ch15, adc1

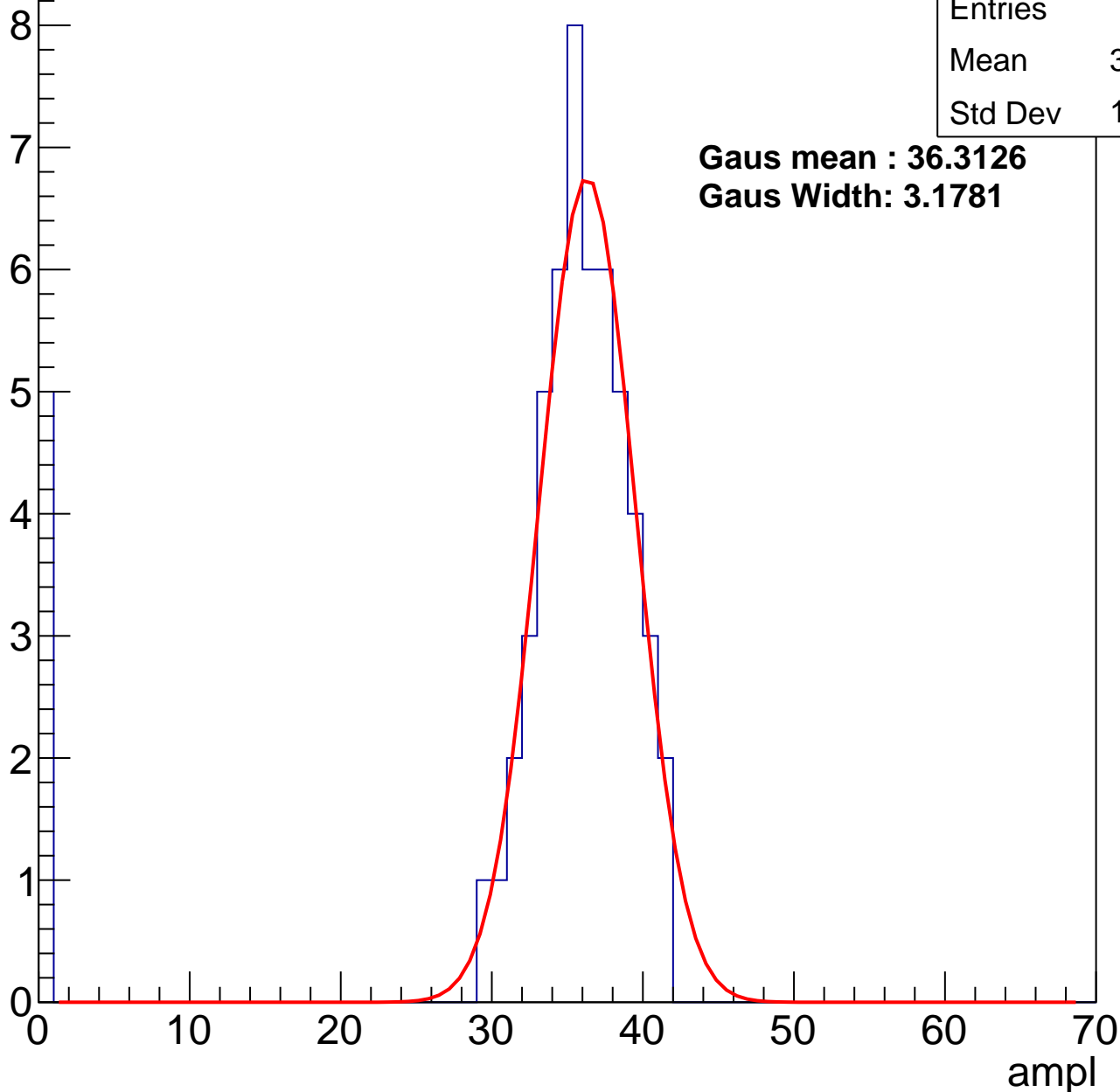
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	32.49
Std Dev	10.43

Gaus mean : 36.3126

Gaus Width: 3.1781



B1L103S, U2-ch15, adc2

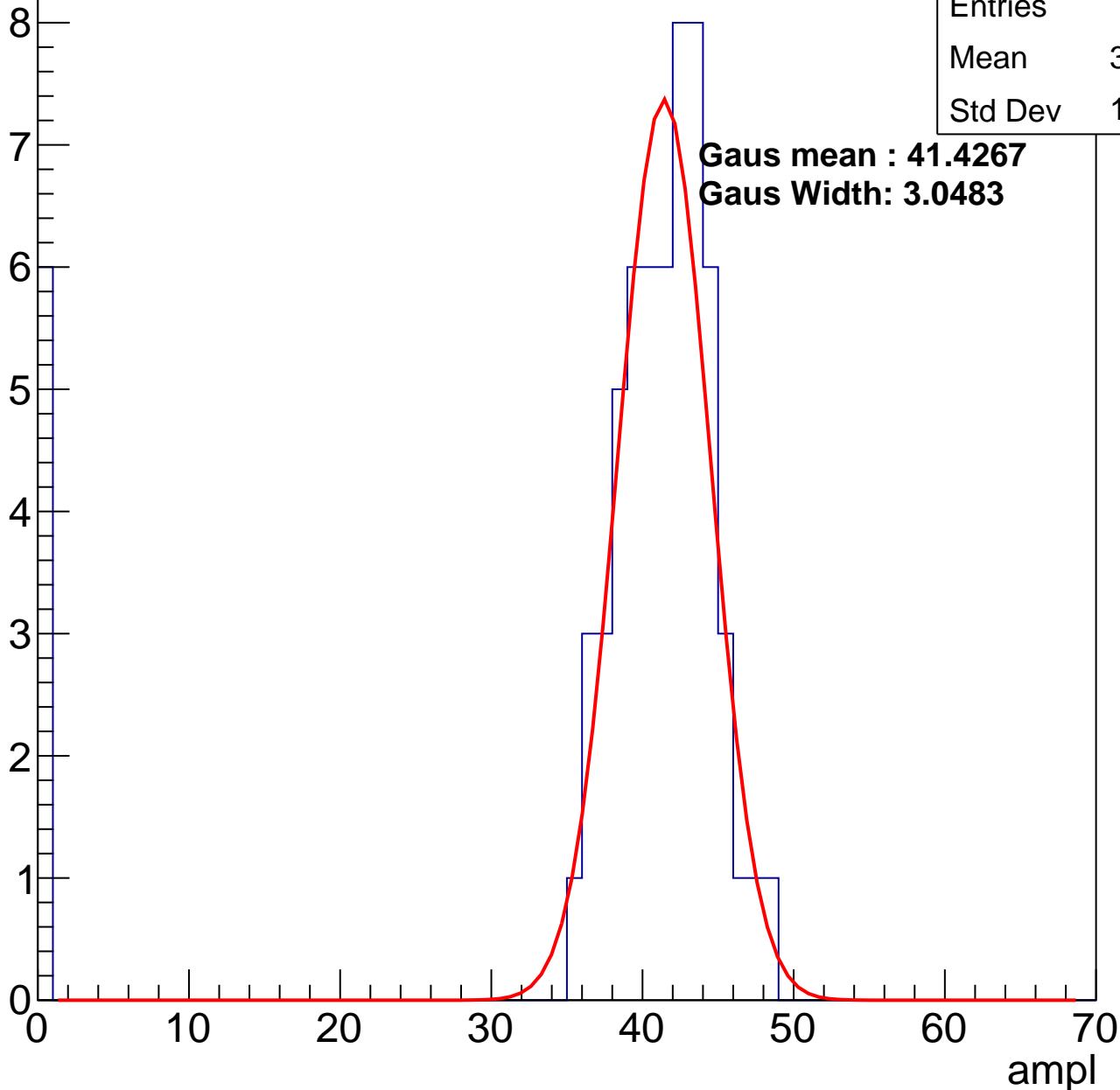
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.25
Std Dev	12.29

Gaus mean : 41.4267

Gaus Width: 3.0483

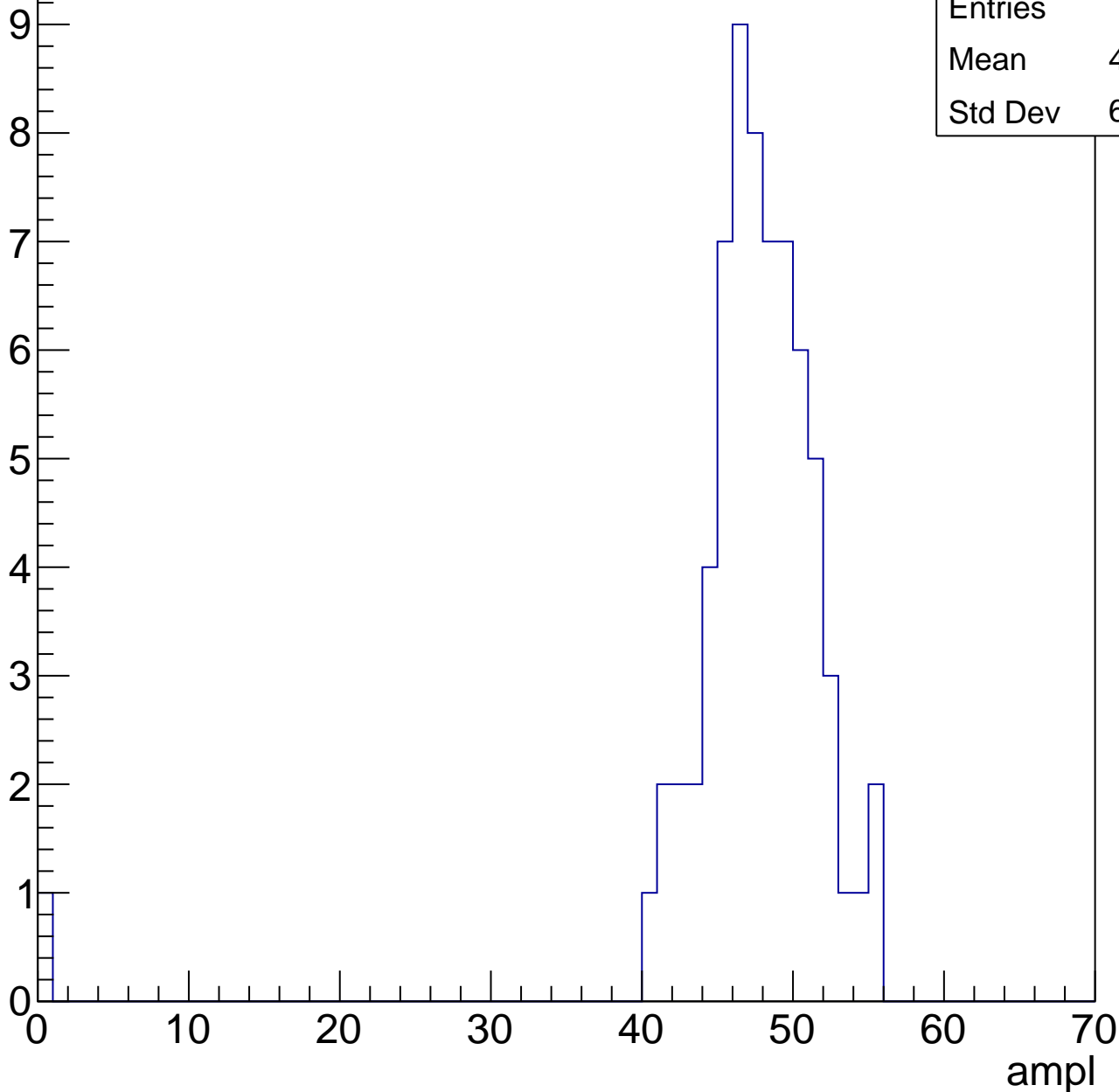


B1L103S, U2-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	46.76
Std Dev	6.573

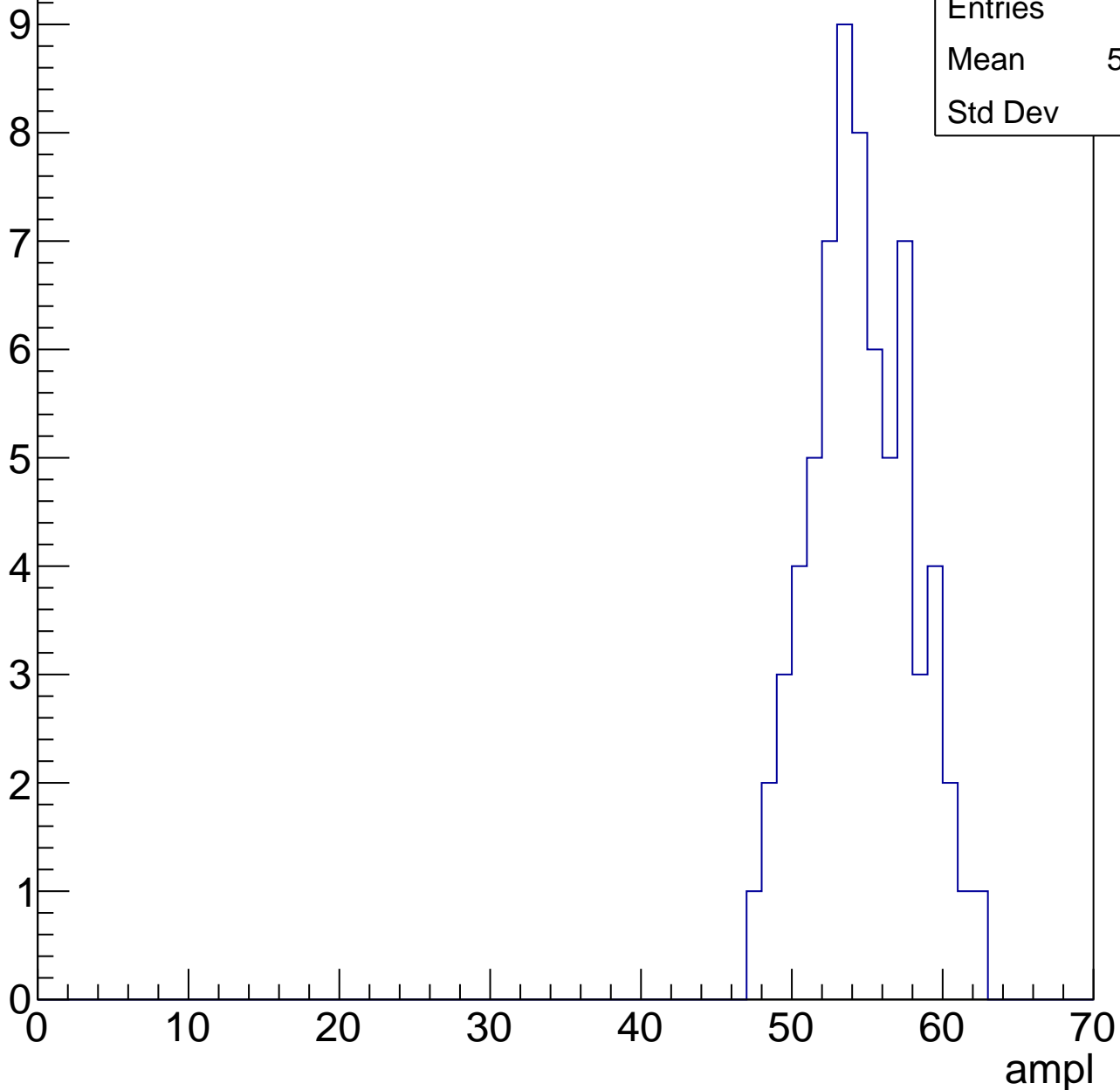


B1L103S, U2-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	54.12
Std Dev	3.35

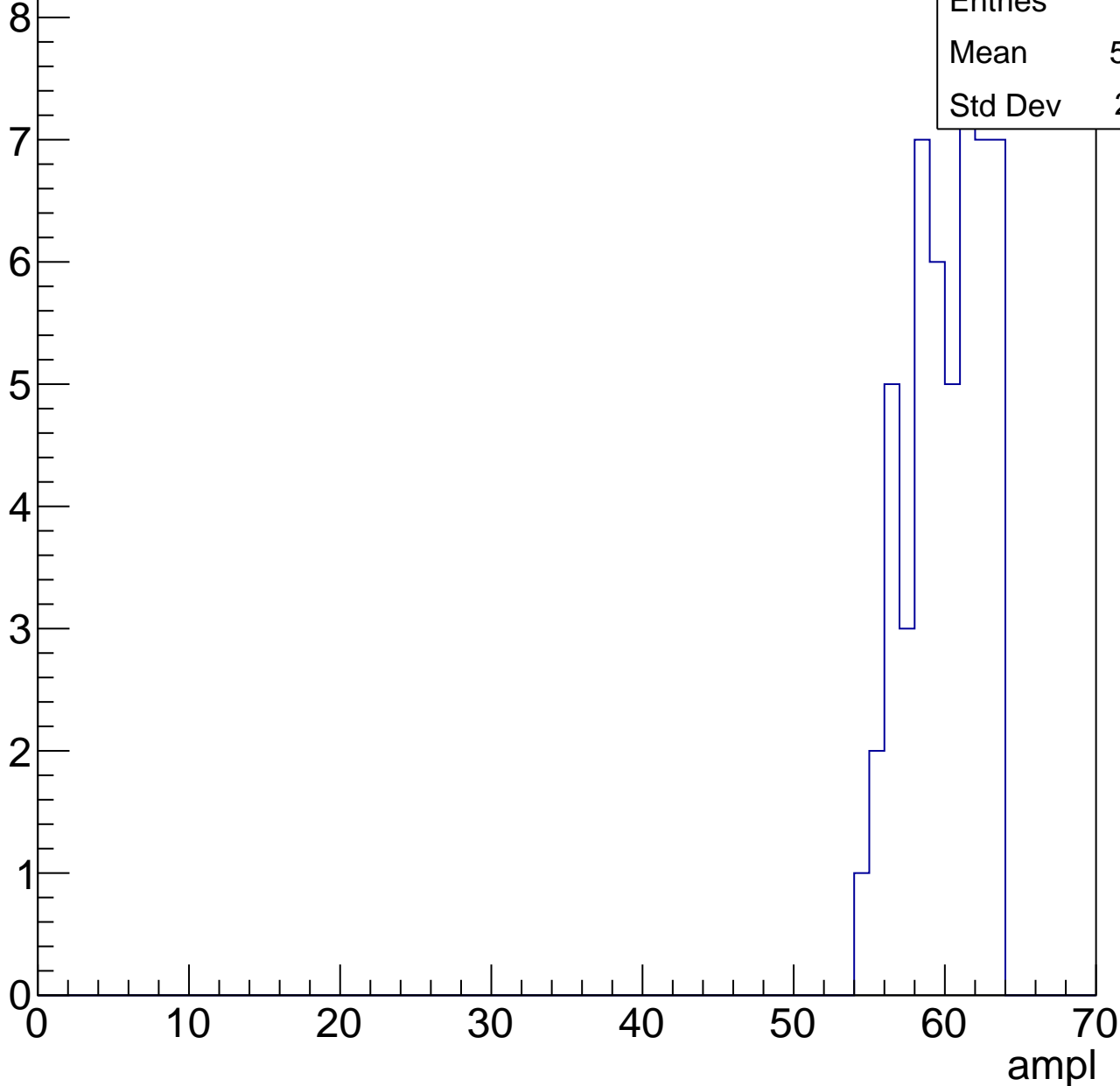


B1L103S, U2-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

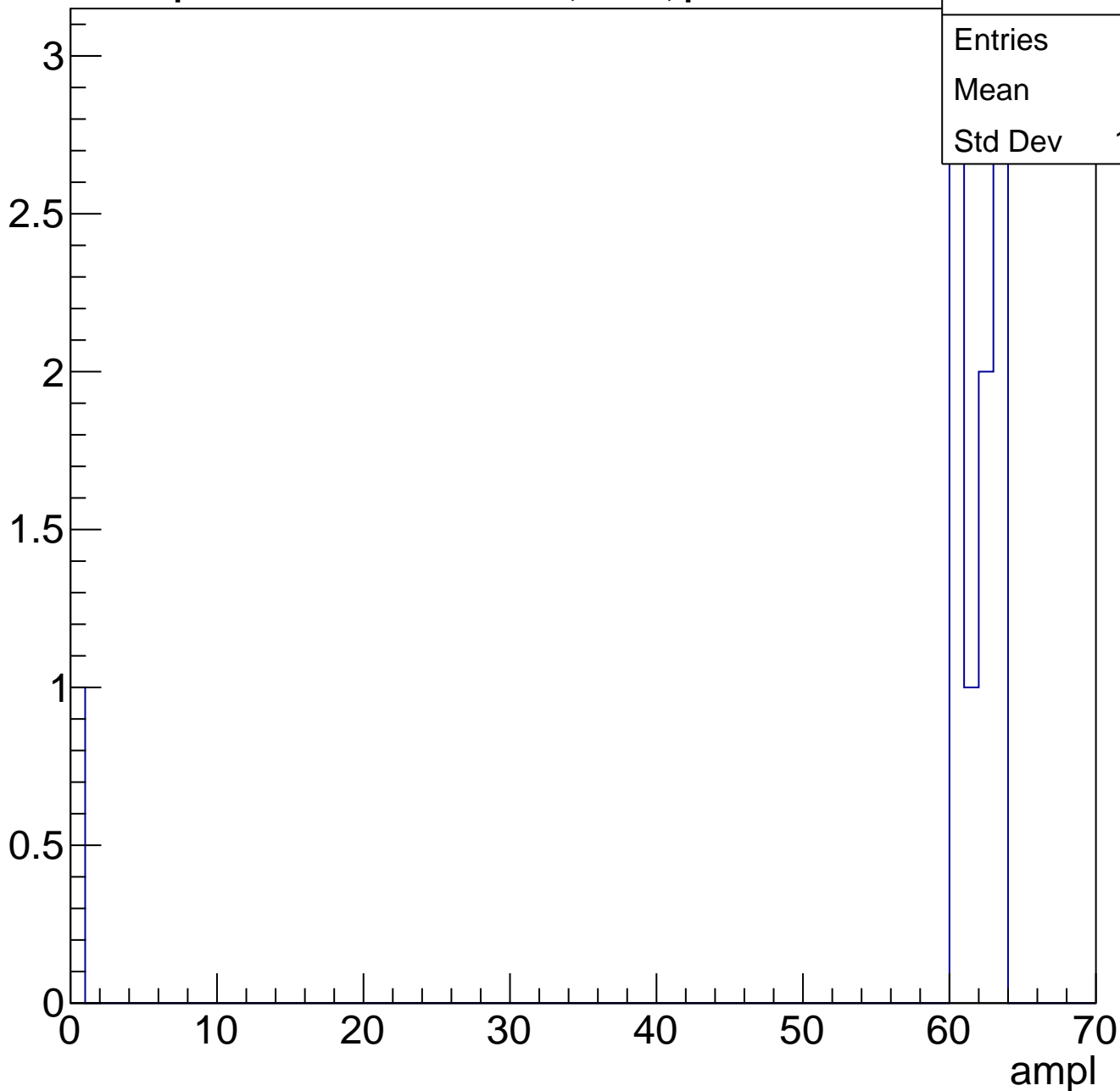
Entries	51
Mean	59.57
Std Dev	2.491



B1L103S, U2-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch16, adc0

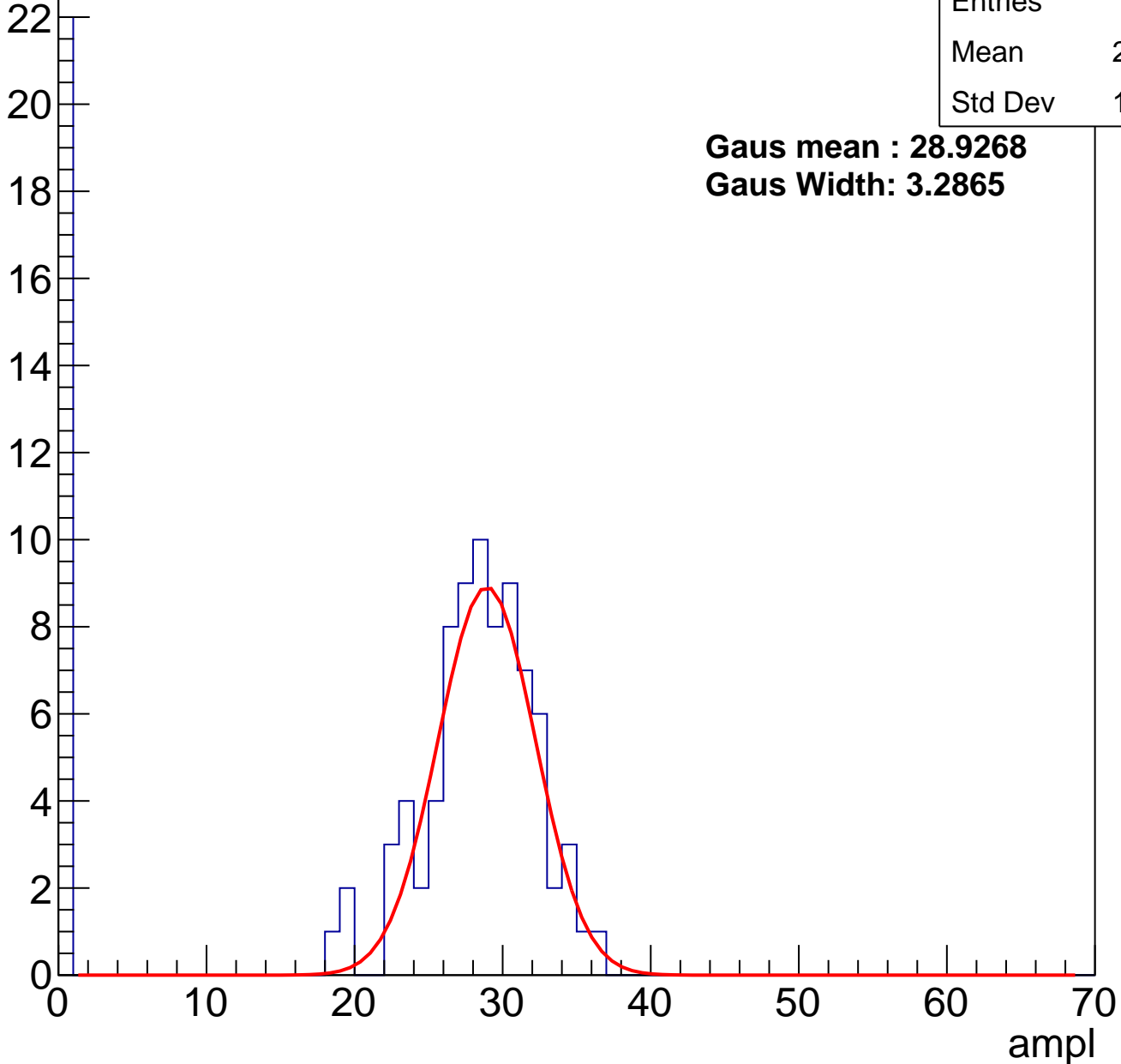
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	21.99
Std Dev	11.97

Gaus mean : 28.9268

Gaus Width: 3.2865

Entry



B1L103S, U2-ch16, adc1

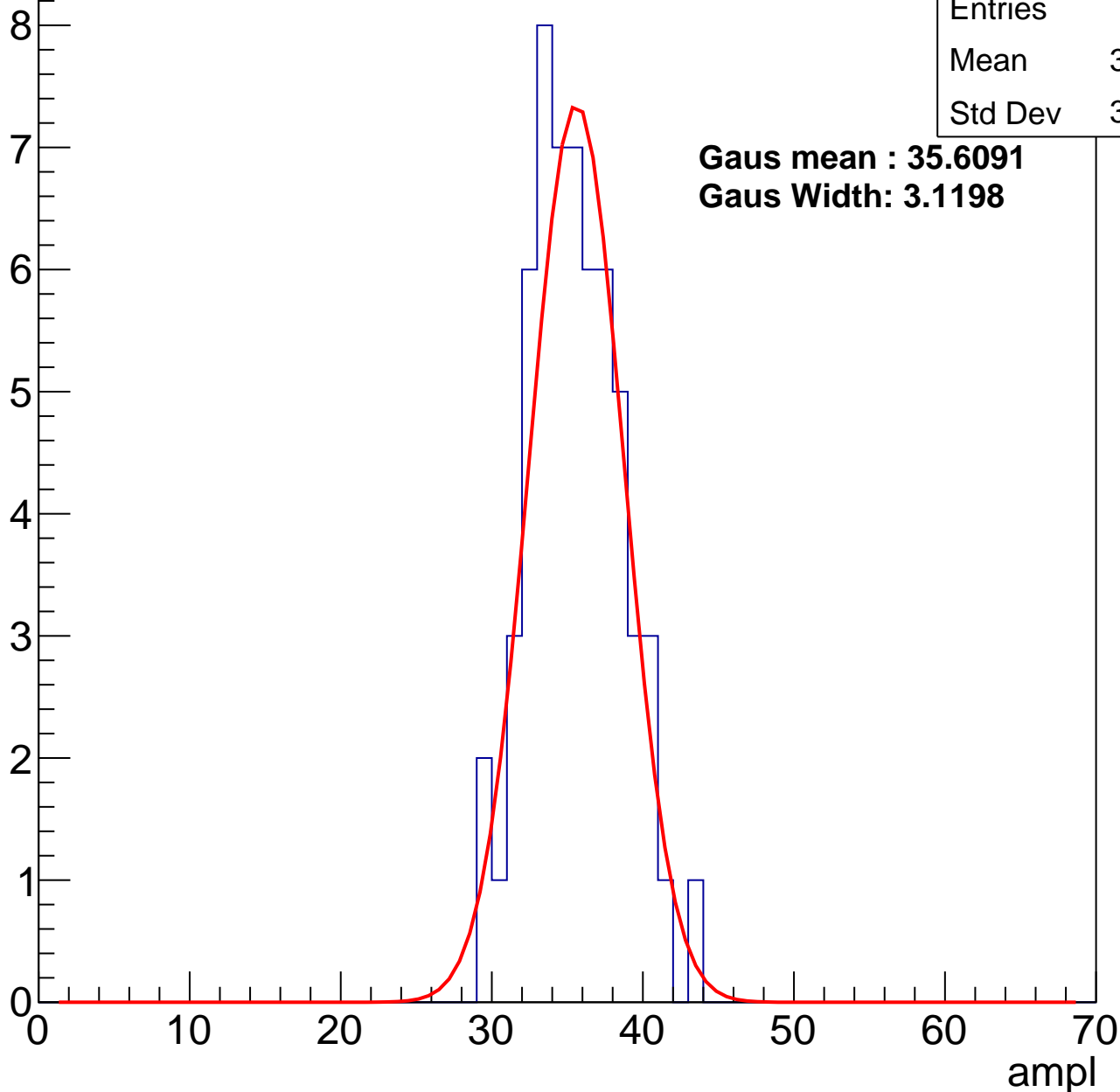
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.07
Std Dev	3.013

Gaus mean : 35.6091

Gaus Width: 3.1198



B1L103S, U2-ch16, adc2

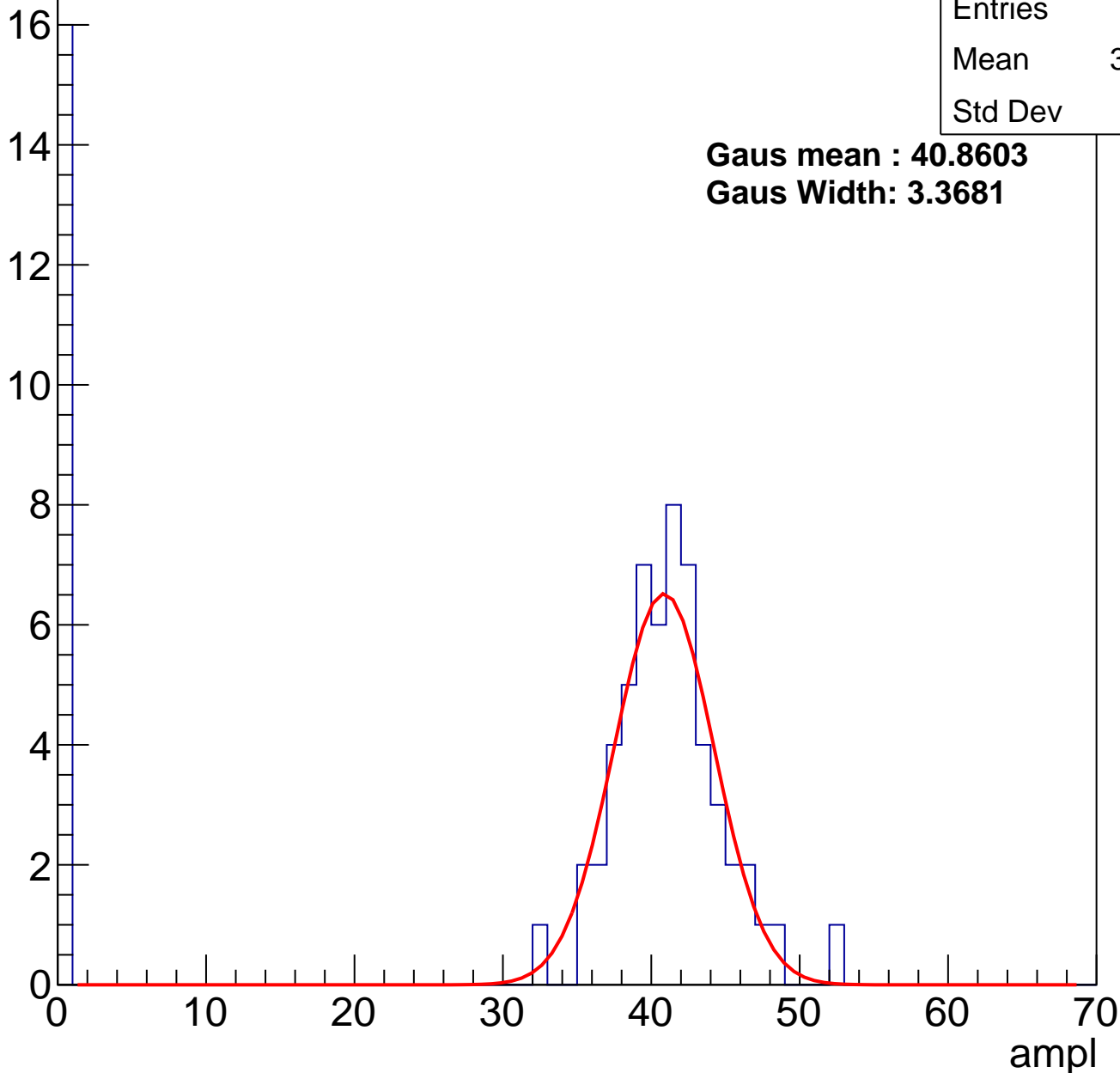
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	31.67
Std Dev	17.2

Gaus mean : 40.8603

Gaus Width: 3.3681

Entry

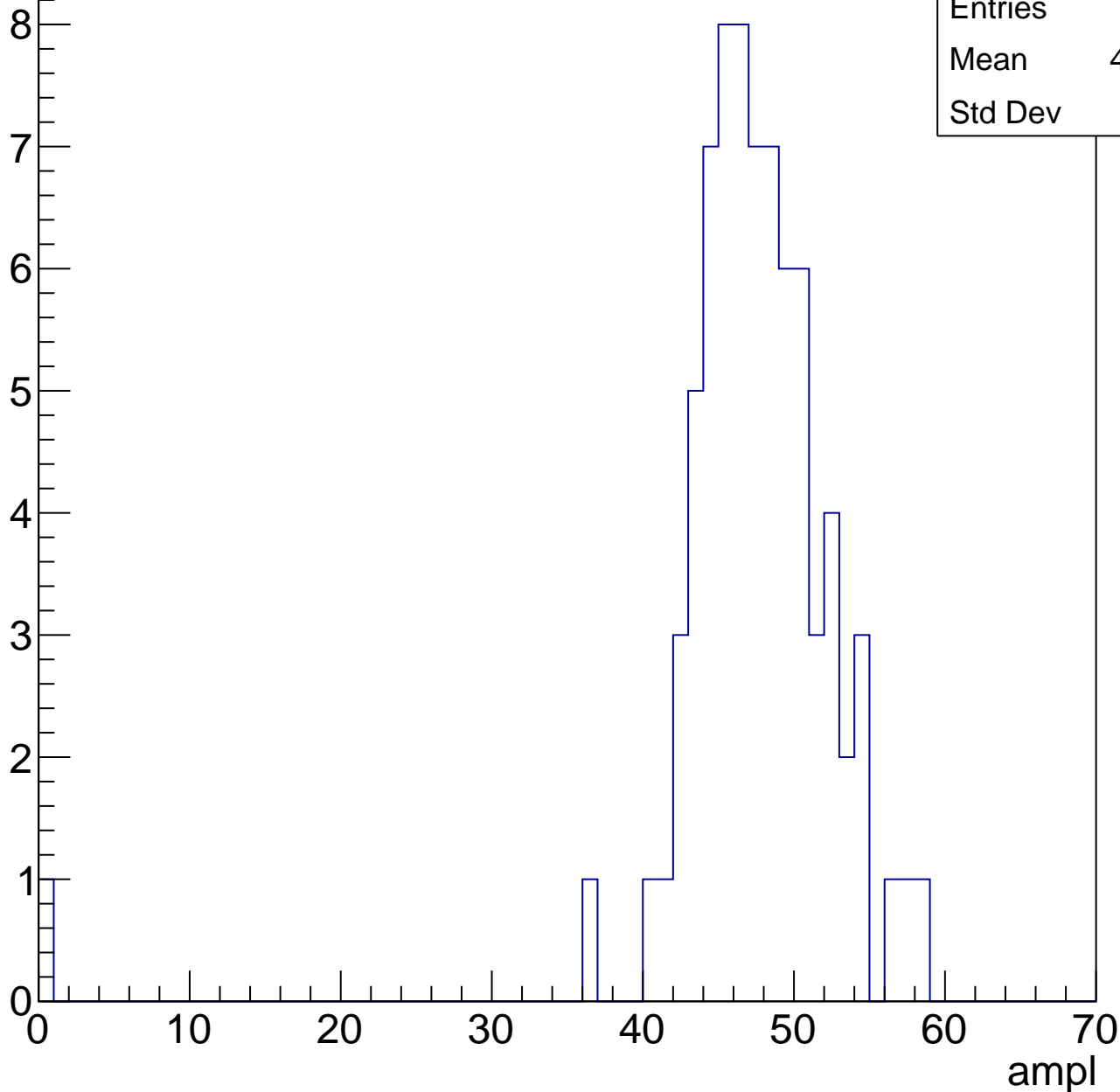


B1L103S, U2-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

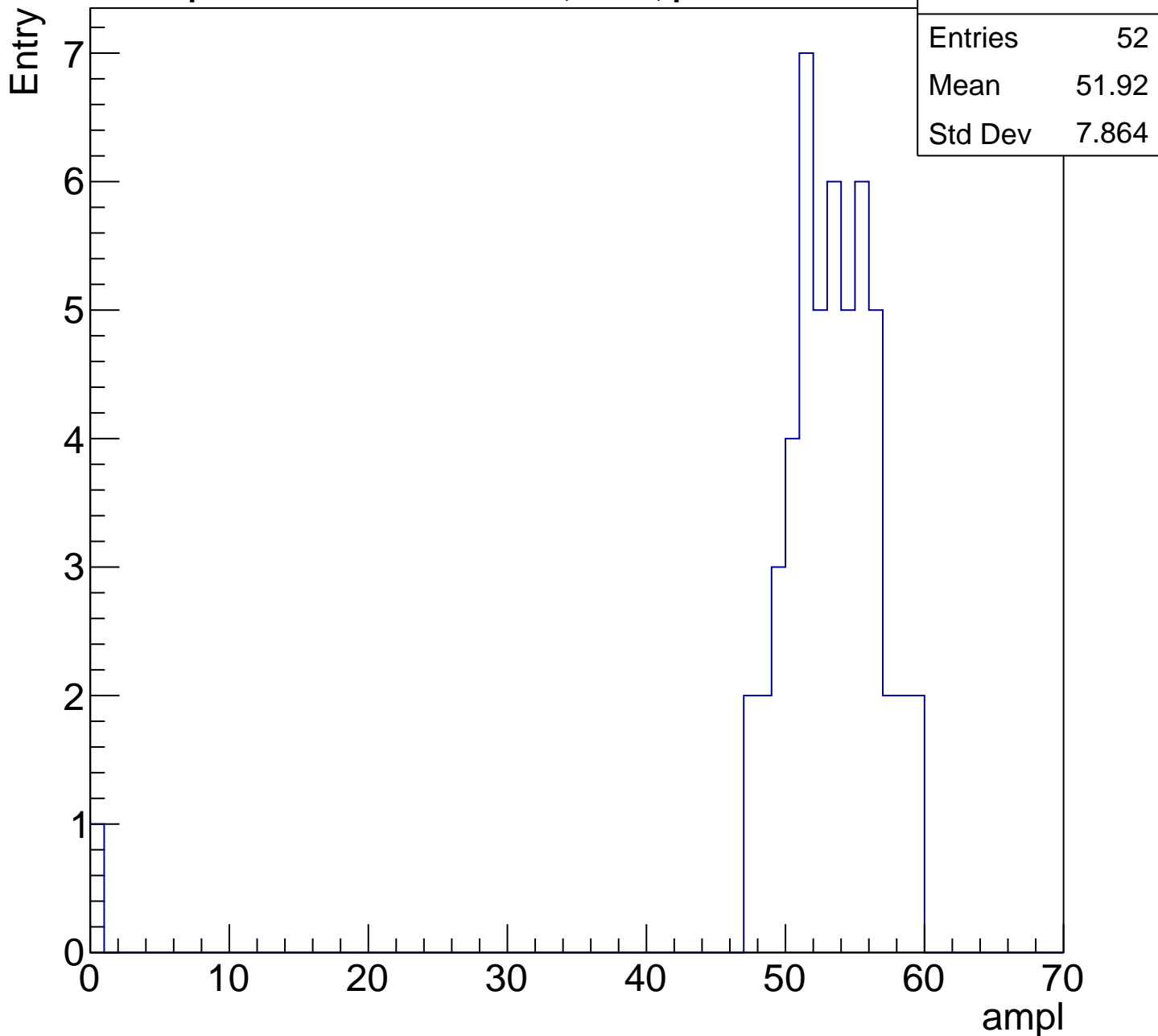
Entry

Entries	76
Mean	46.75
Std Dev	6.71



B1L103S, U2-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch16, adc5

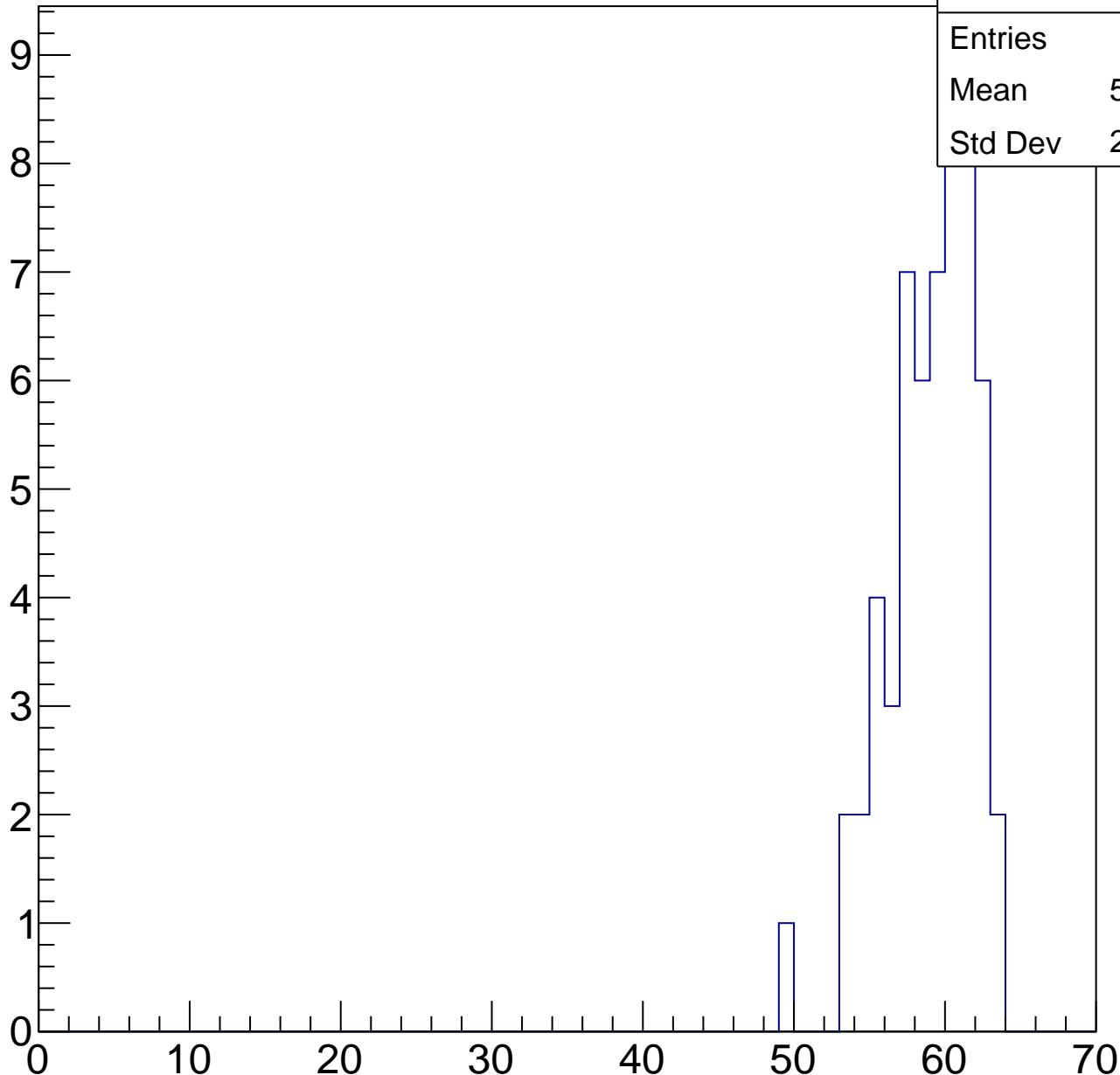
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.54
Std Dev	2.847

ampl

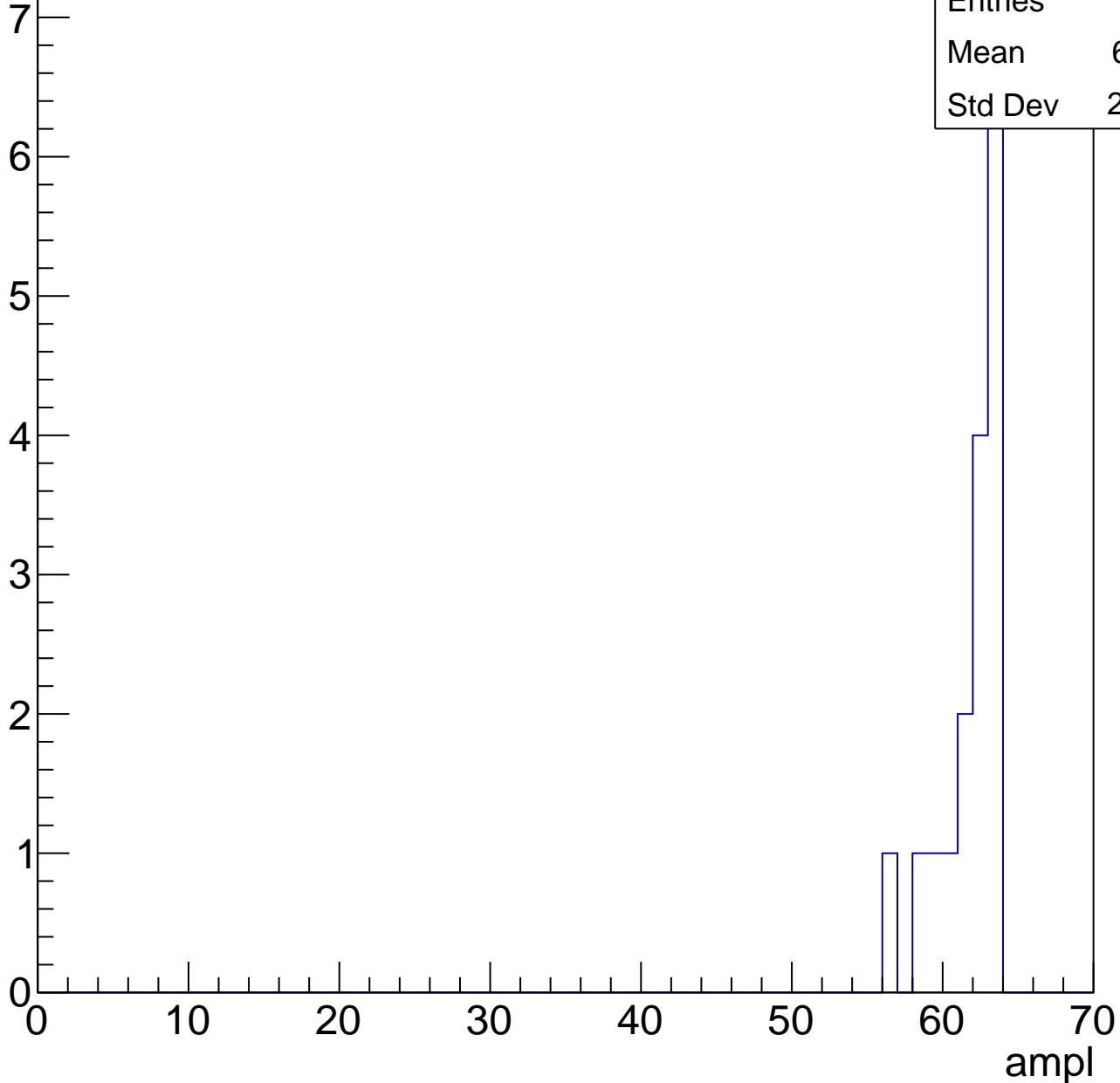


B1L103S, U2-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.41
Std Dev	2.002



B1L103S, U2-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	3.316
Std Dev	14.07

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch17, adc0

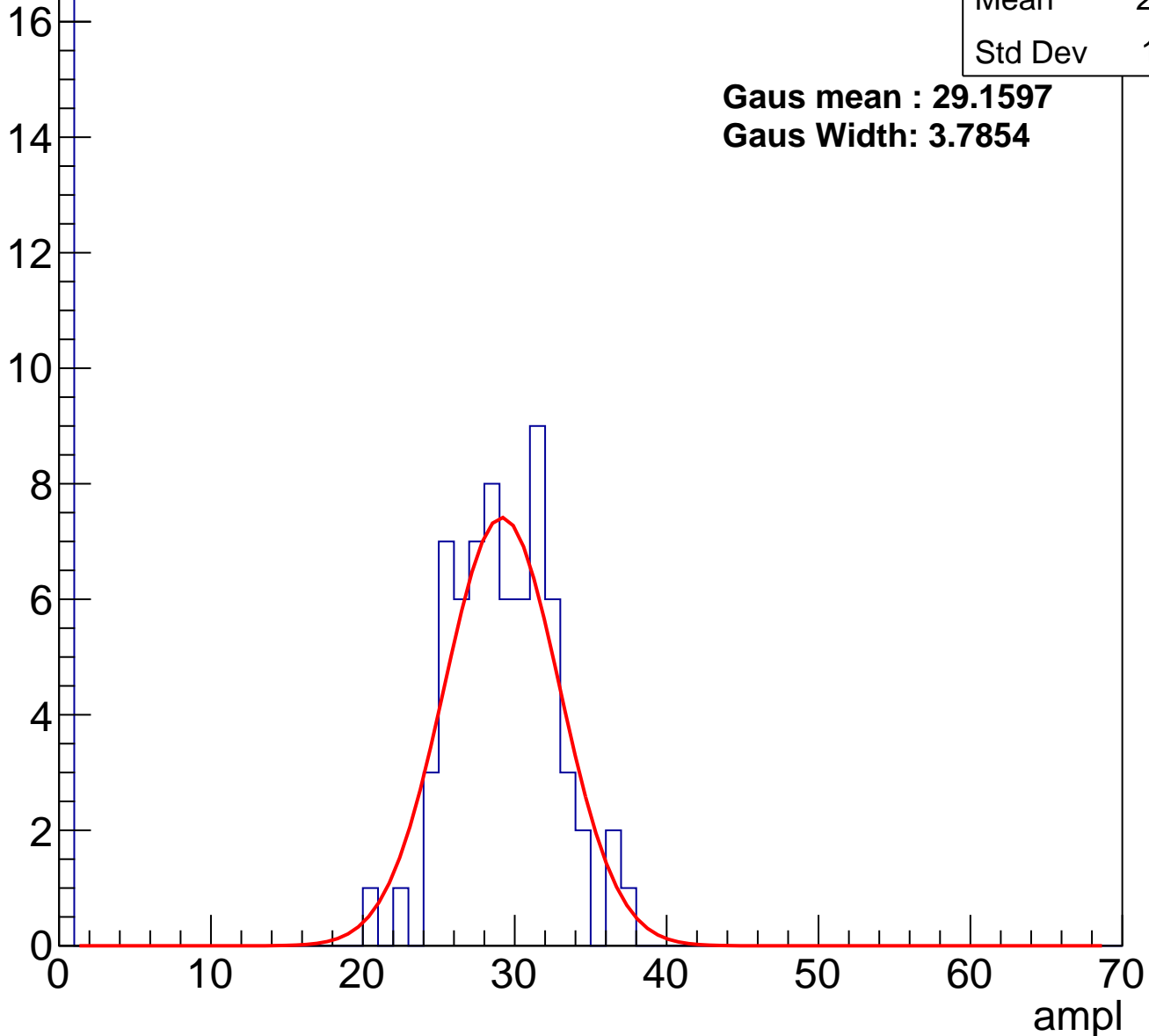
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	23.05
Std Dev	11.91

Gaus mean : 29.1597

Gaus Width: 3.7854

Entry



B1L103S, U2-ch17, adc1

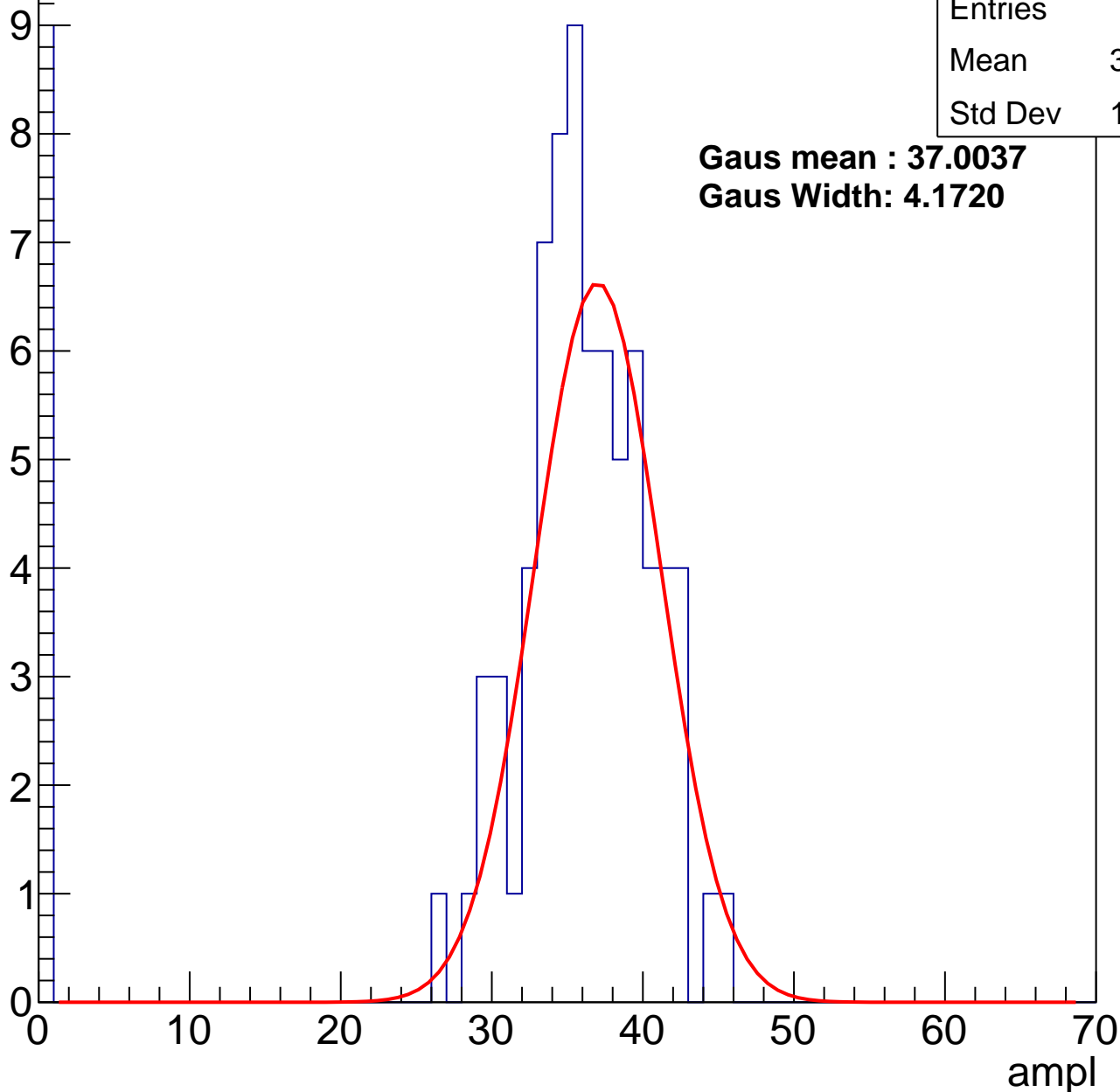
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	31.94
Std Dev	11.74

Gaus mean : 37.0037

Gaus Width: 4.1720



B1L103S, U2-ch17, adc2

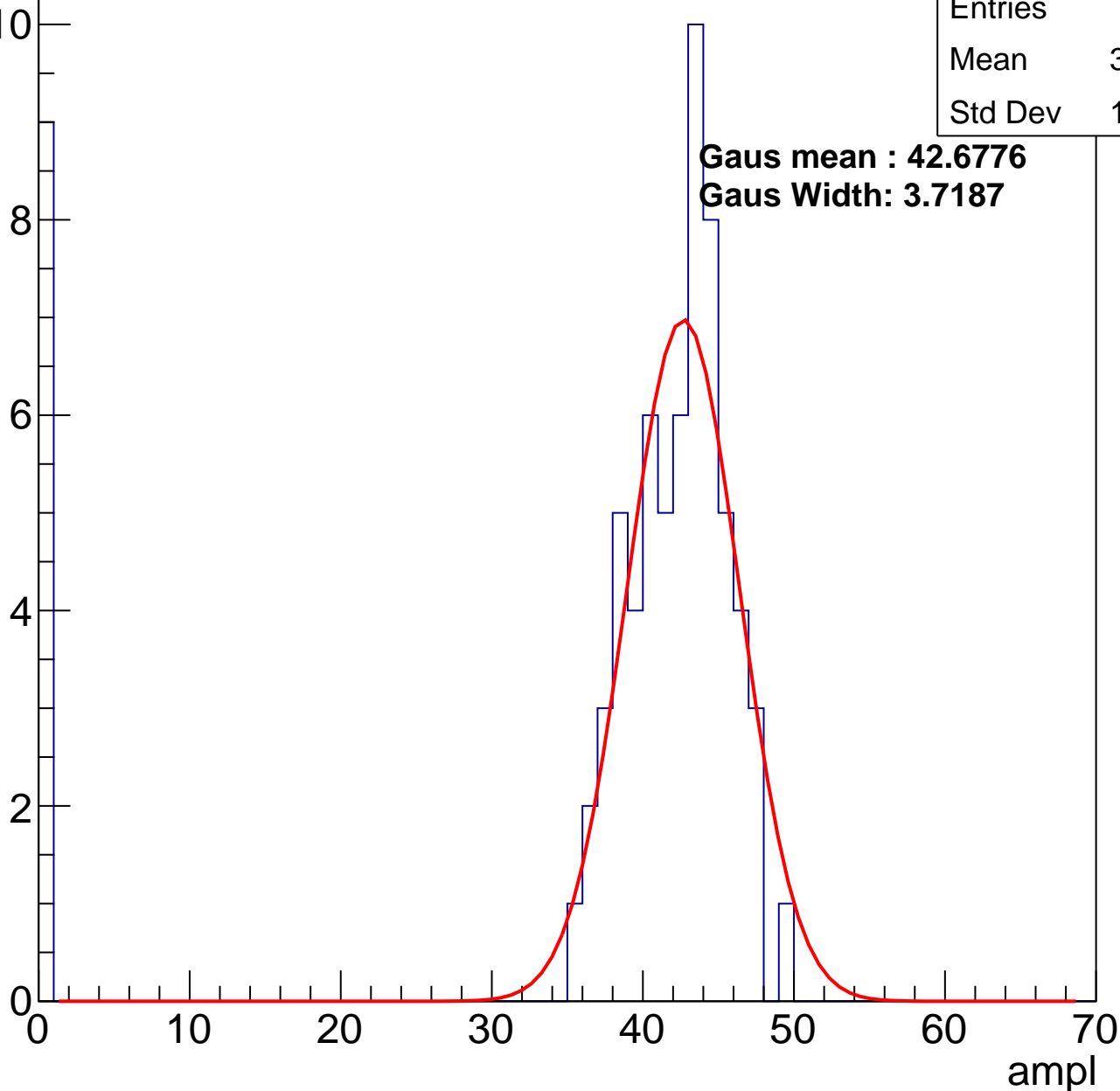
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	36.69
Std Dev	14.17

Gaus mean : 42.6776

Gaus Width: 3.7187

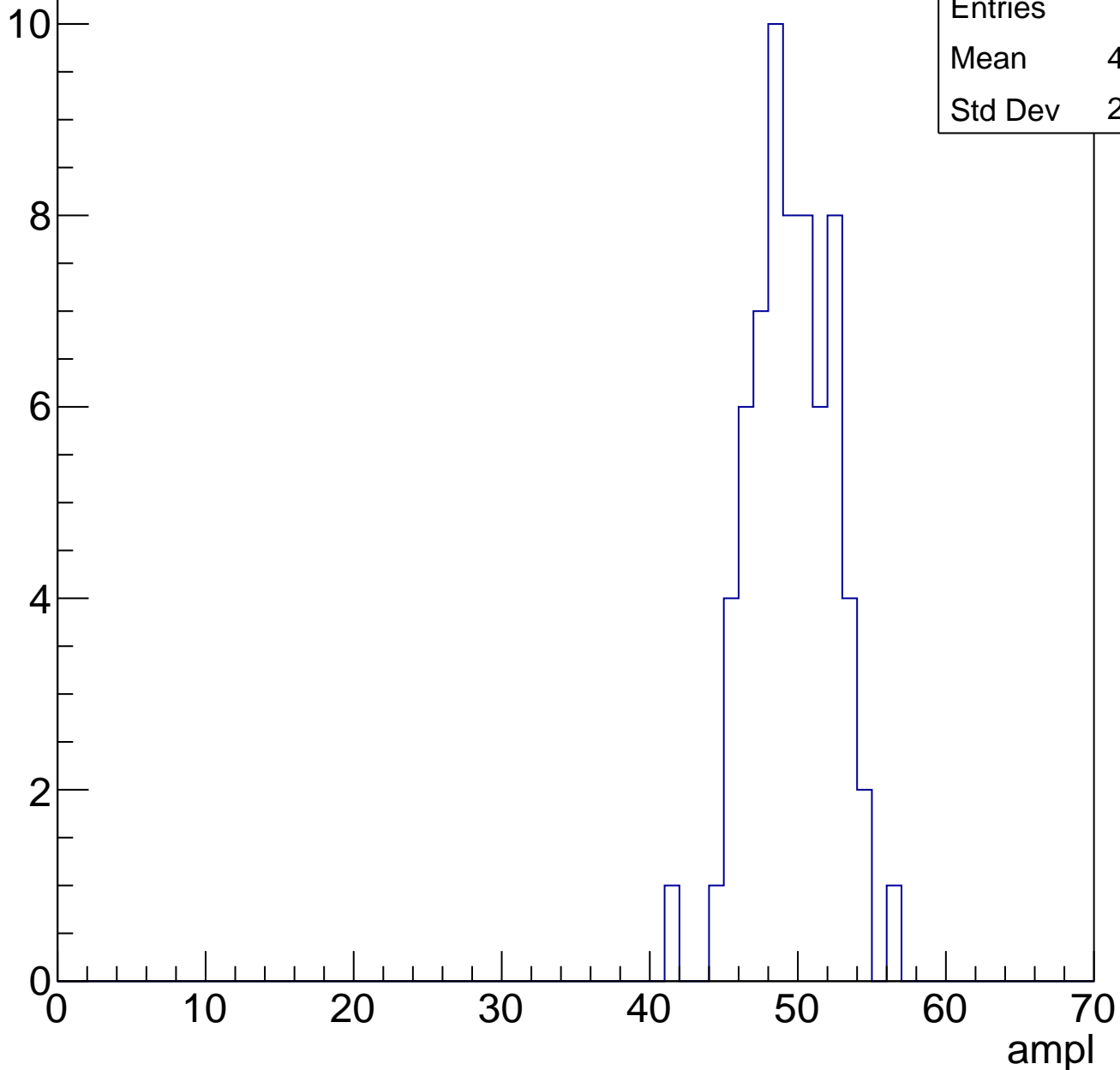


B1L103S, U2-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	49.09
Std Dev	2.784

Entry

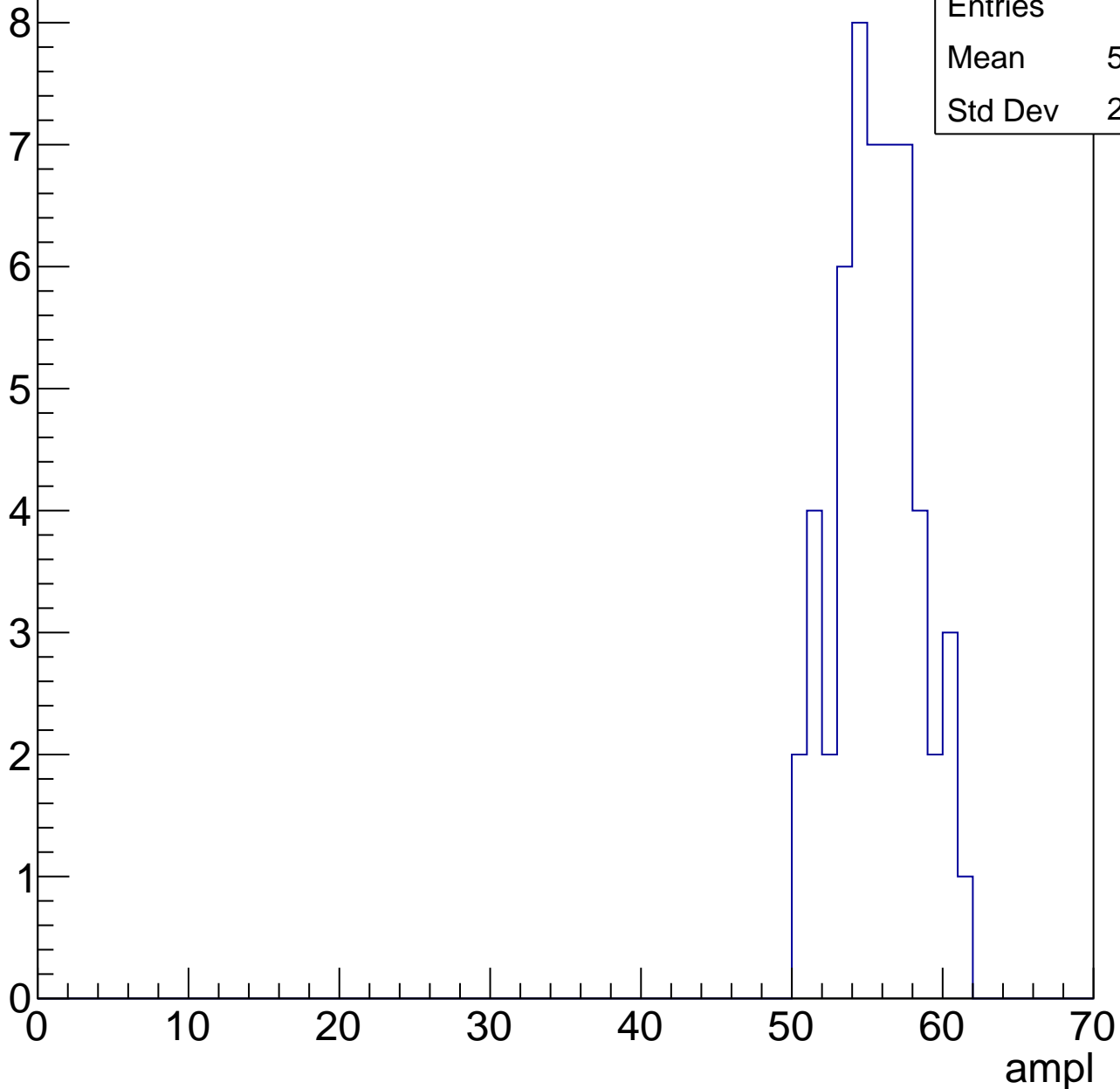


B1L103S, U2-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	55.19
Std Dev	2.664

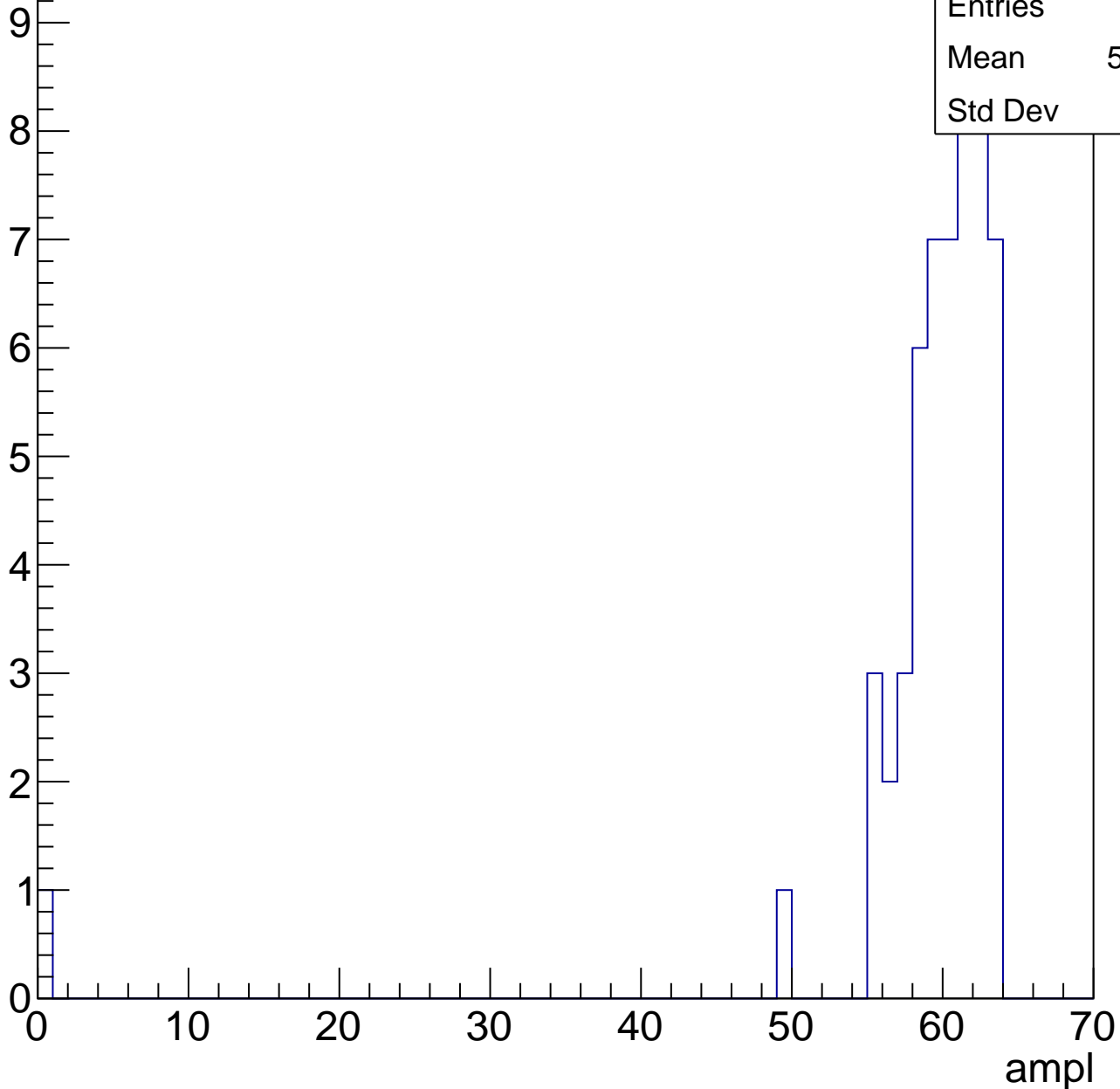


B1L103S, U2-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	58.59
Std Dev	8.48



B1L103S, U2-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

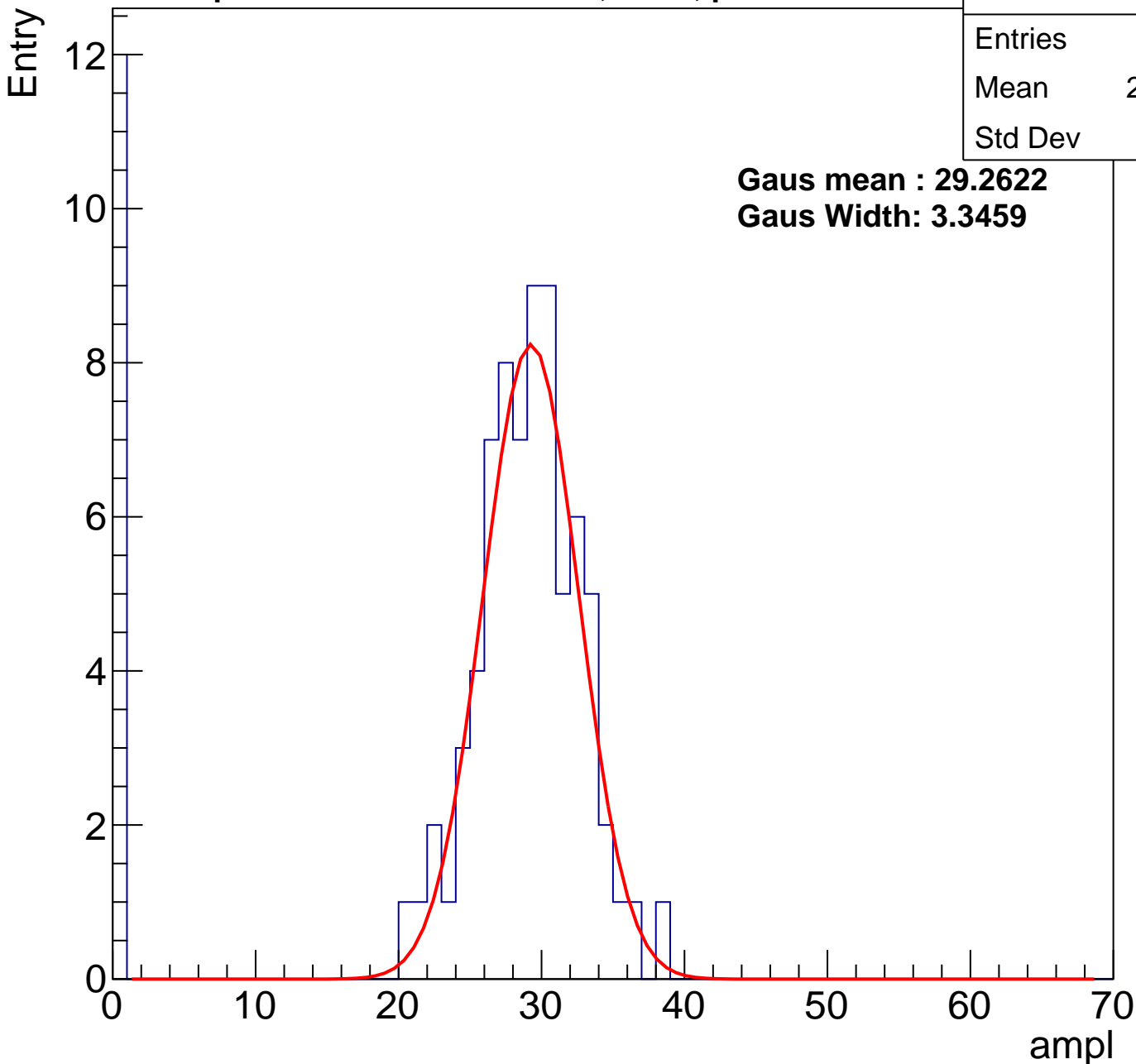
B1L103S, U2-ch18, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	24.64
Std Dev	10.5

Gaus mean : 29.2622

Gaus Width: 3.3459



B1L103S, U2-ch18, adc1

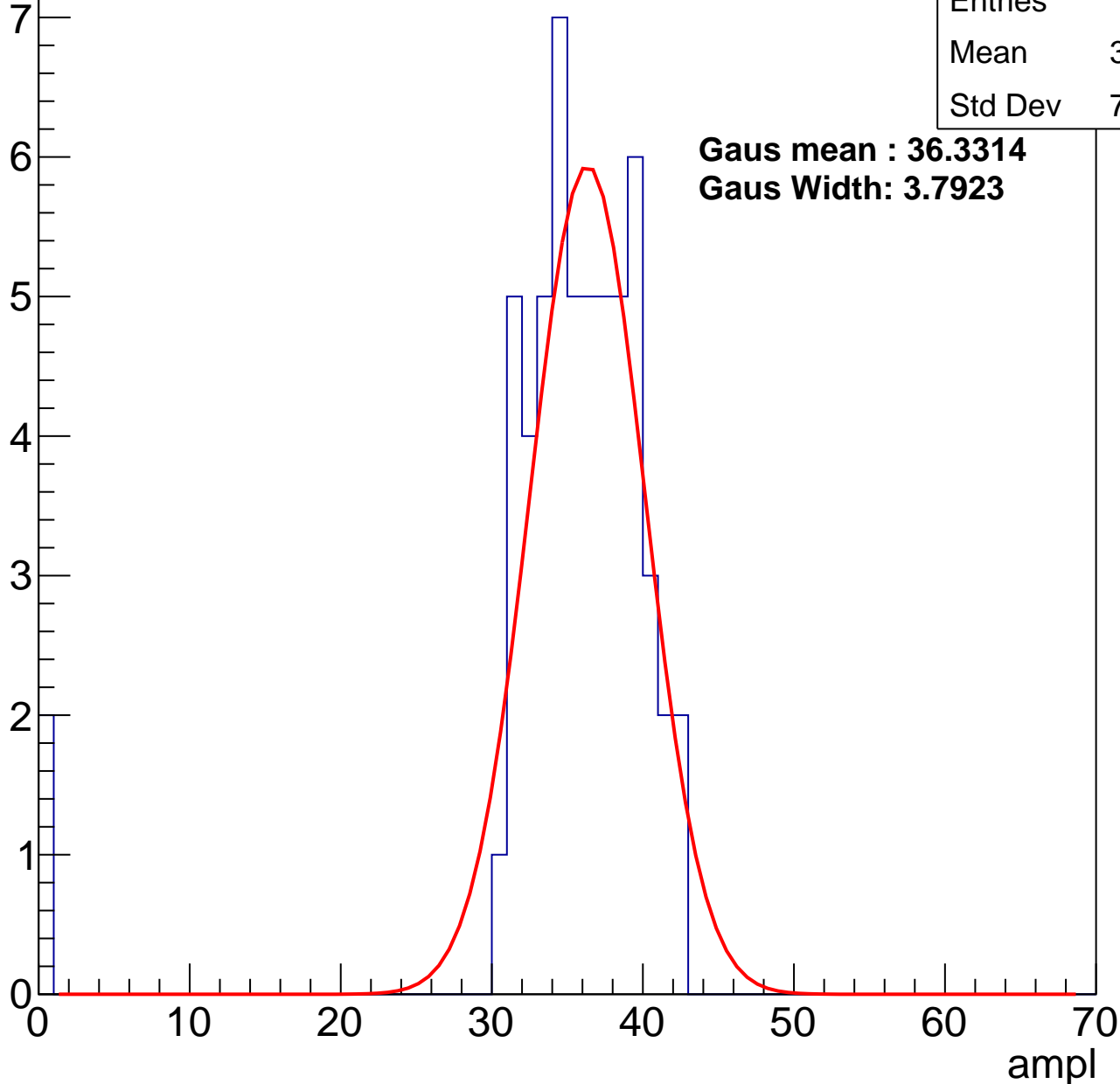
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	34.49
Std Dev	7.272

Gaus mean : 36.3314

Gaus Width: 3.7923



B1L103S, U2-ch18, adc2

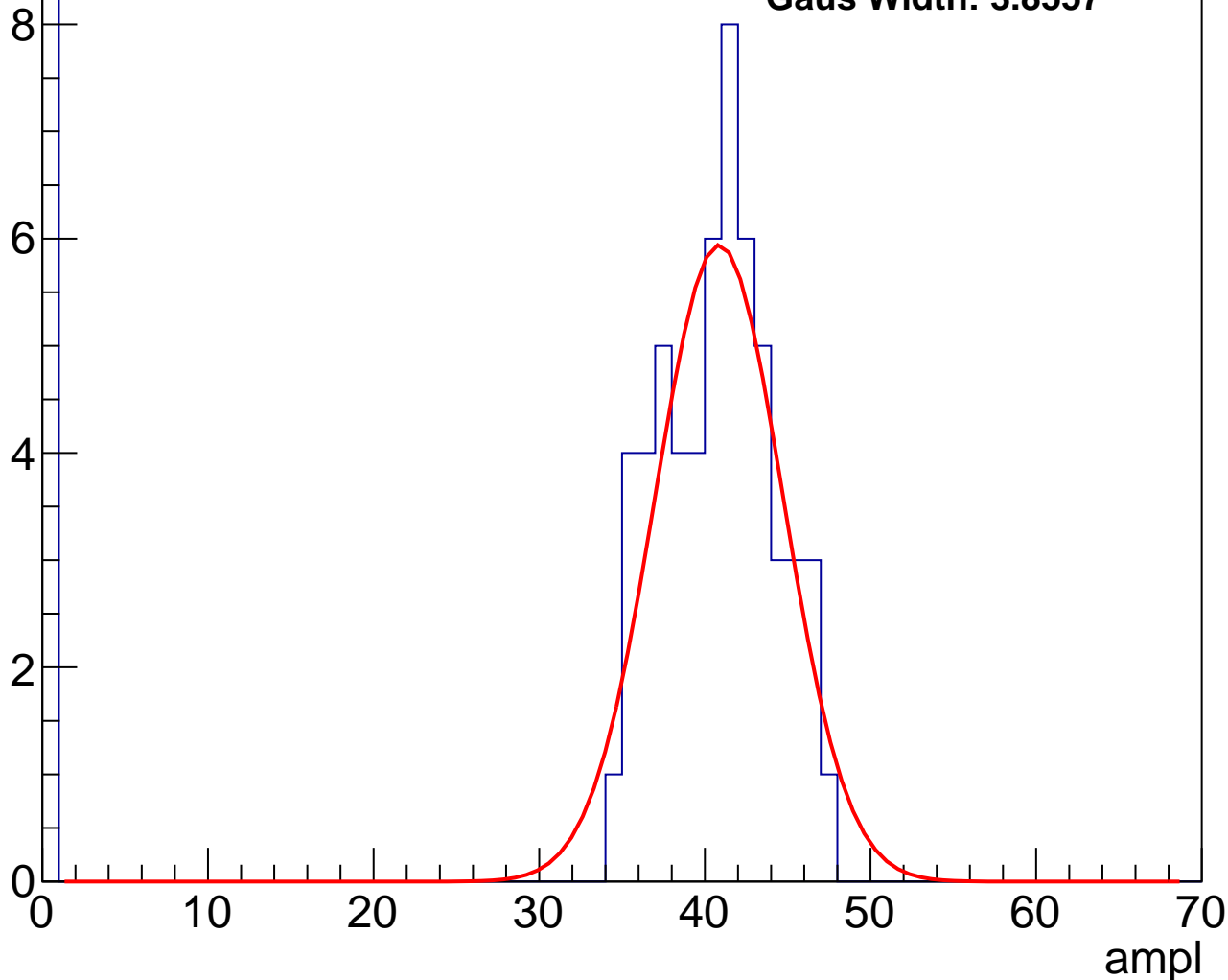
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	34.3
Std Dev	14.68

Gaus mean : 40.8586

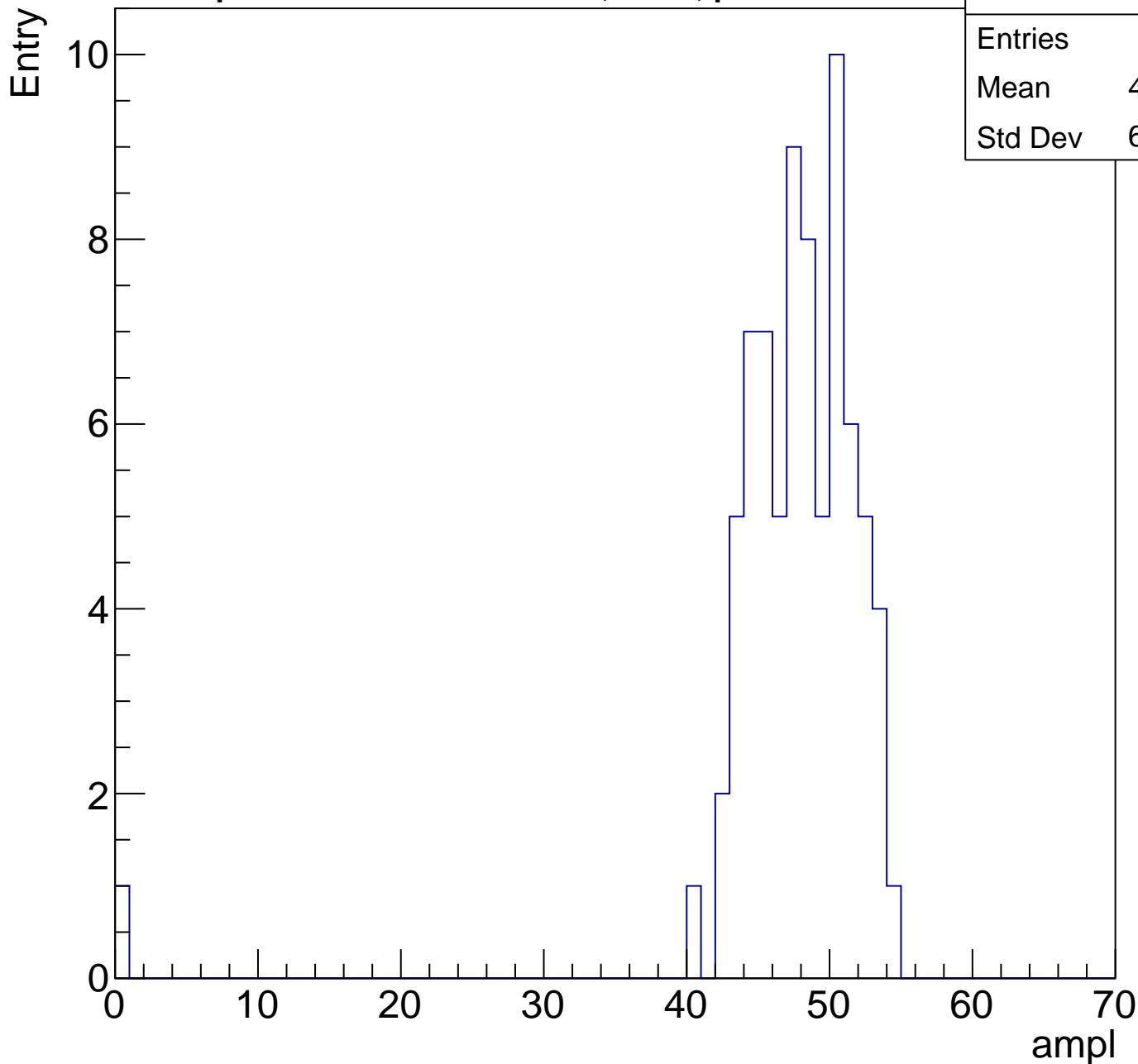
Gaus Width: 3.8557



B1L103S, U2-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	47.05
Std Dev	6.299

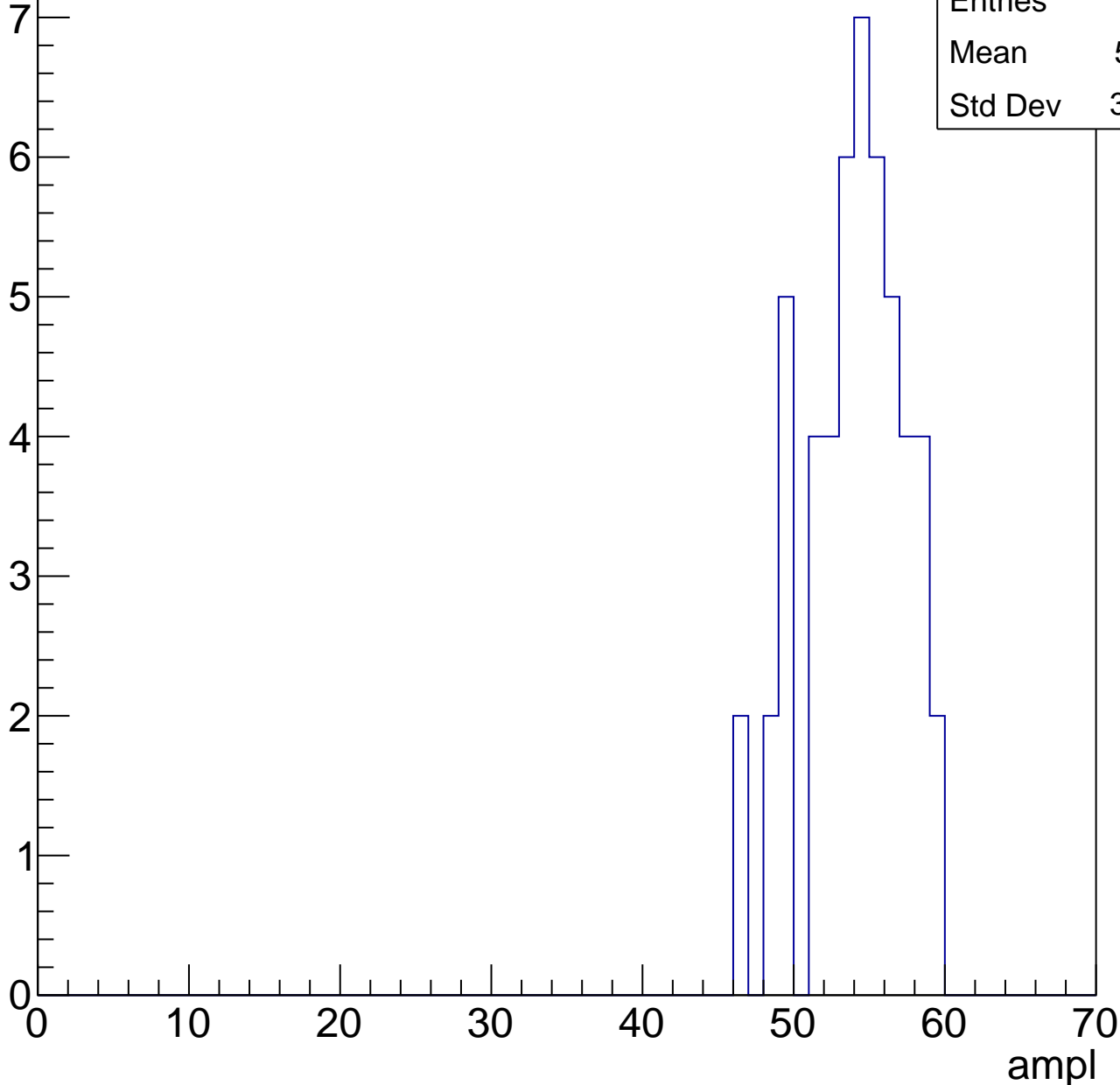


B1L103S, U2-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	53.51
Std Dev	3.274

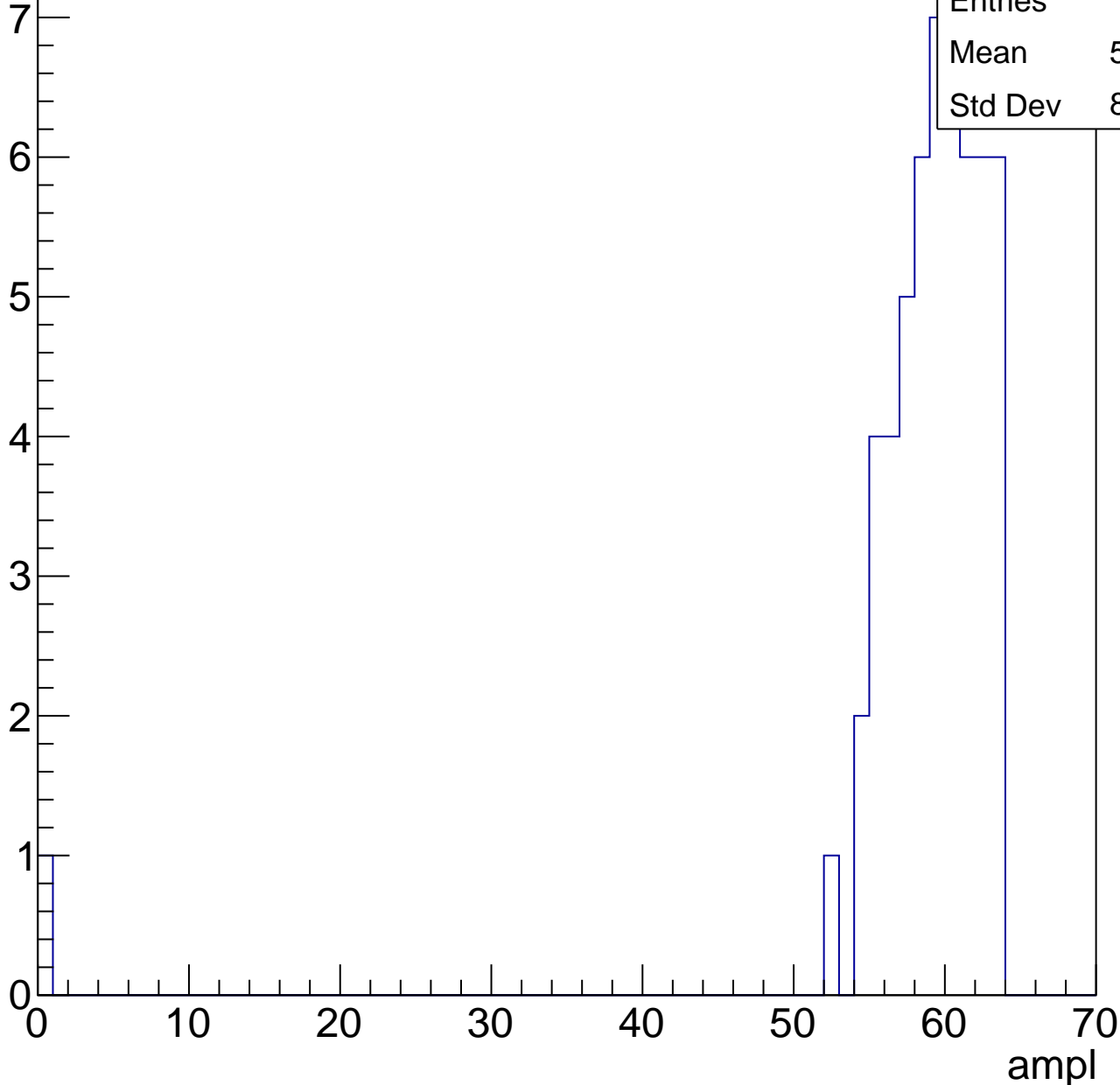


B1L103S, U2-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

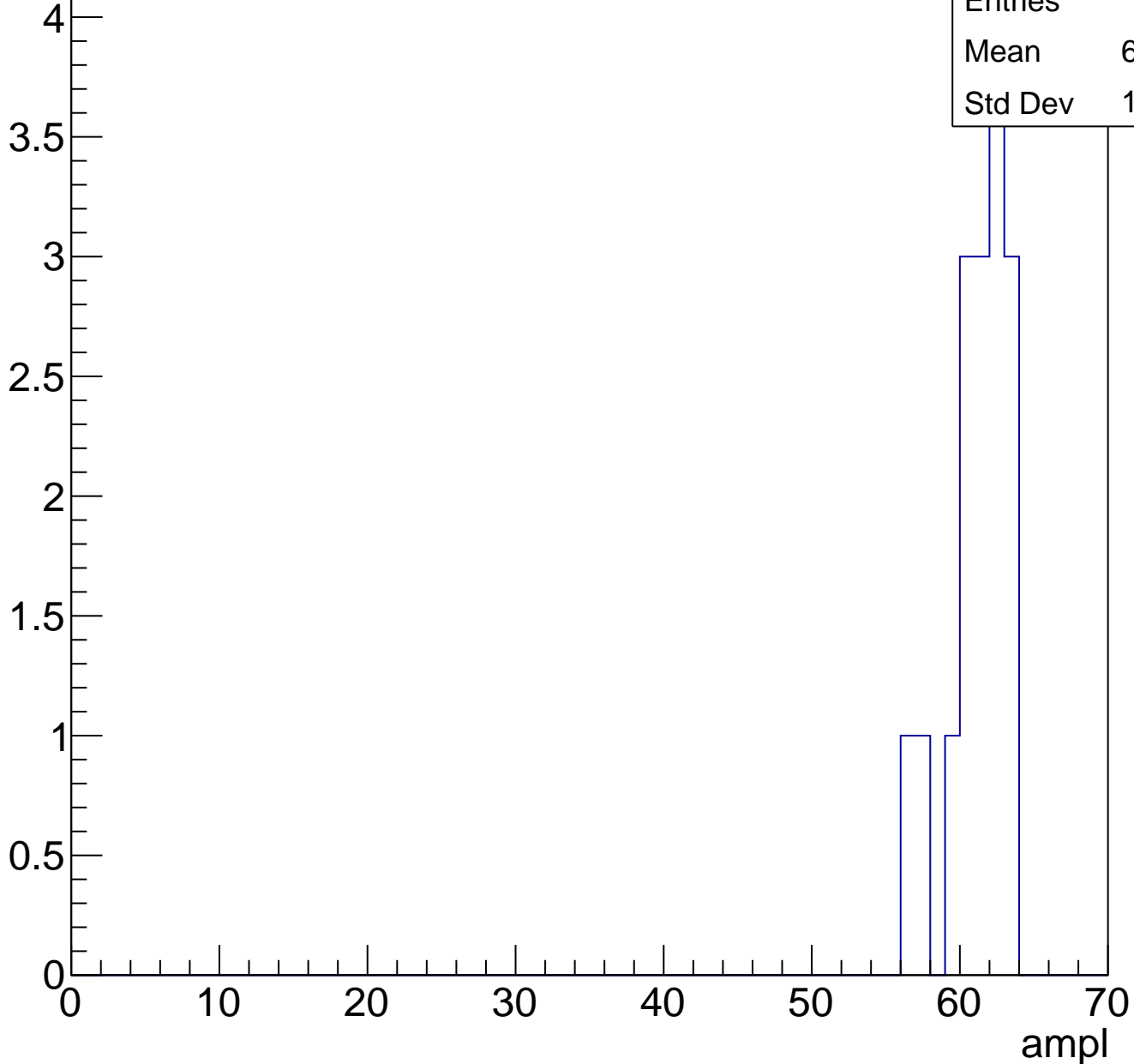
Entries	55
Mean	57.93
Std Dev	8.338



B1L103S, U2-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

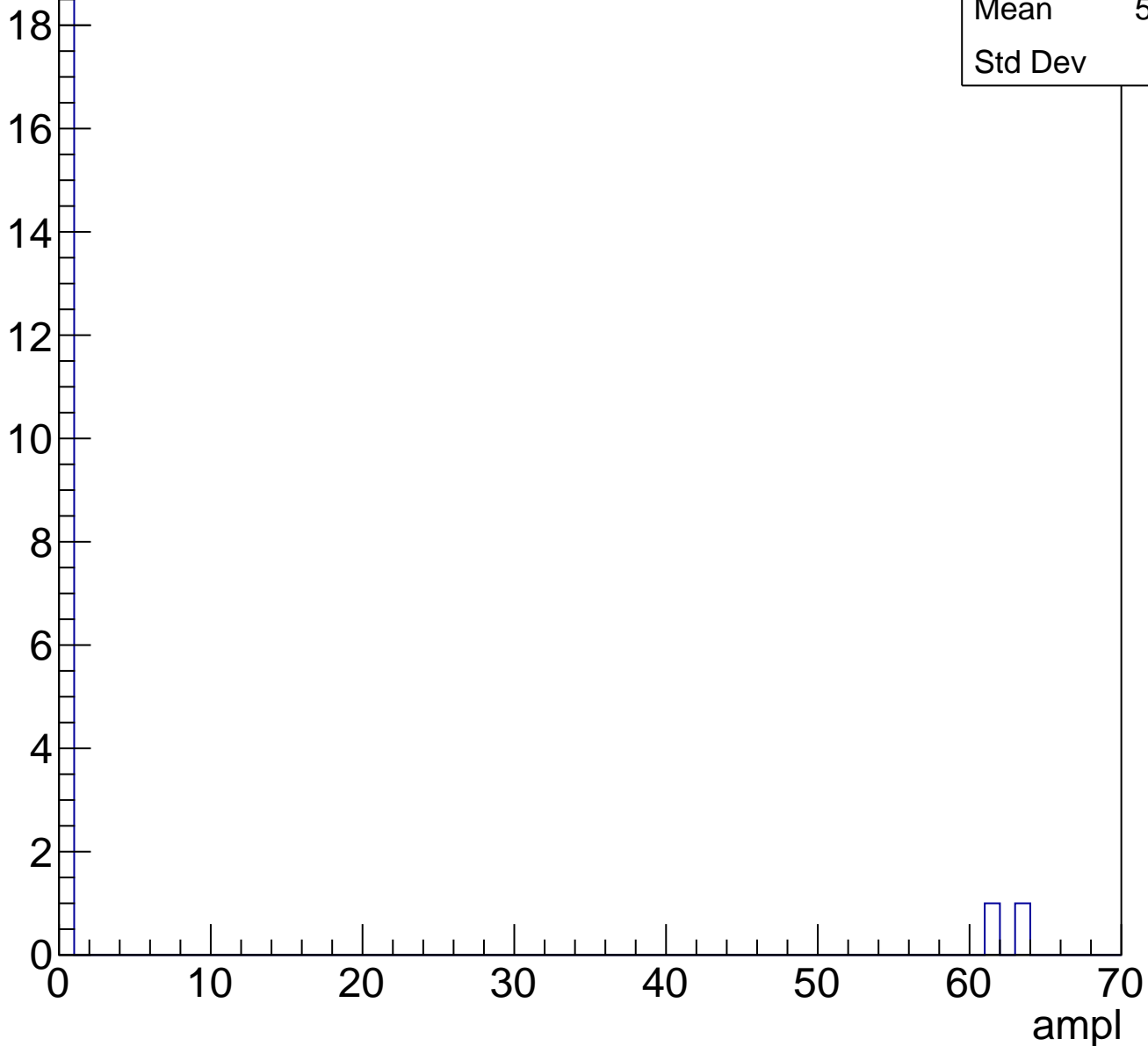
Entry



B1L103S, U2-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	5.905
Std Dev	18.2

B1L103S, U2-ch19, adc0

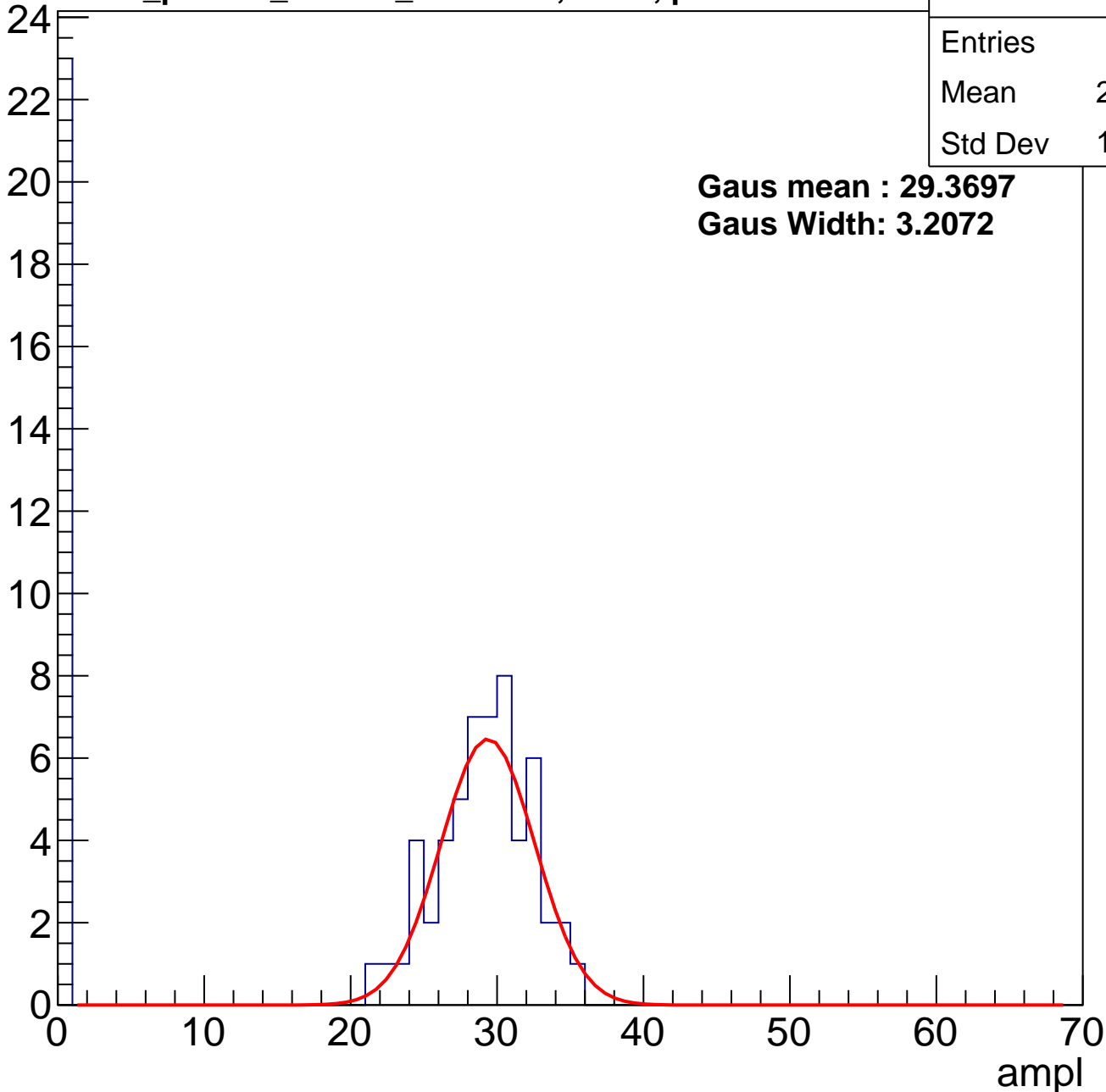
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	20.19
Std Dev	13.32

Gaus mean : 29.3697

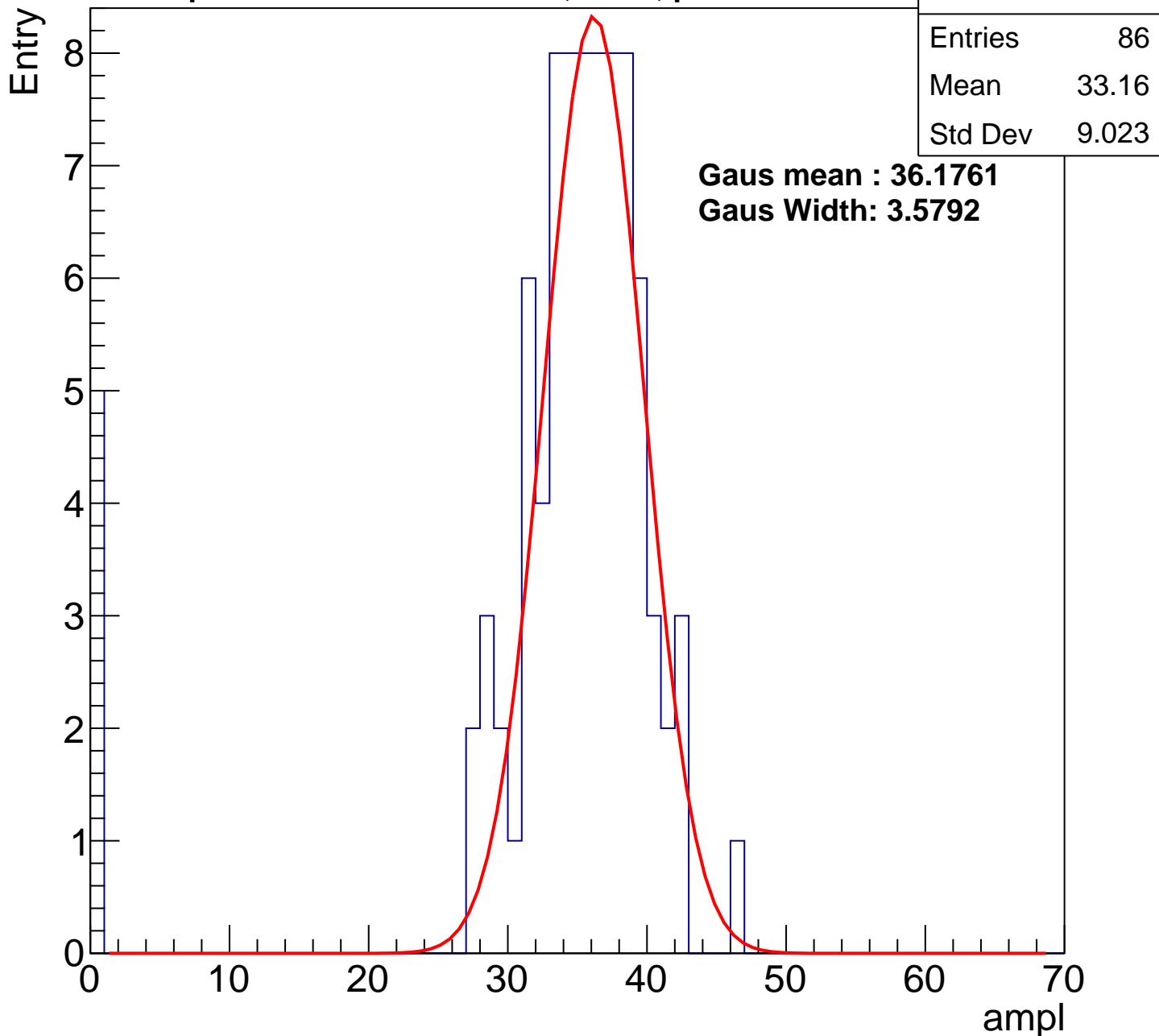
Gaus Width: 3.2072

Entry



B1L103S, U2-ch19, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch19, adc2

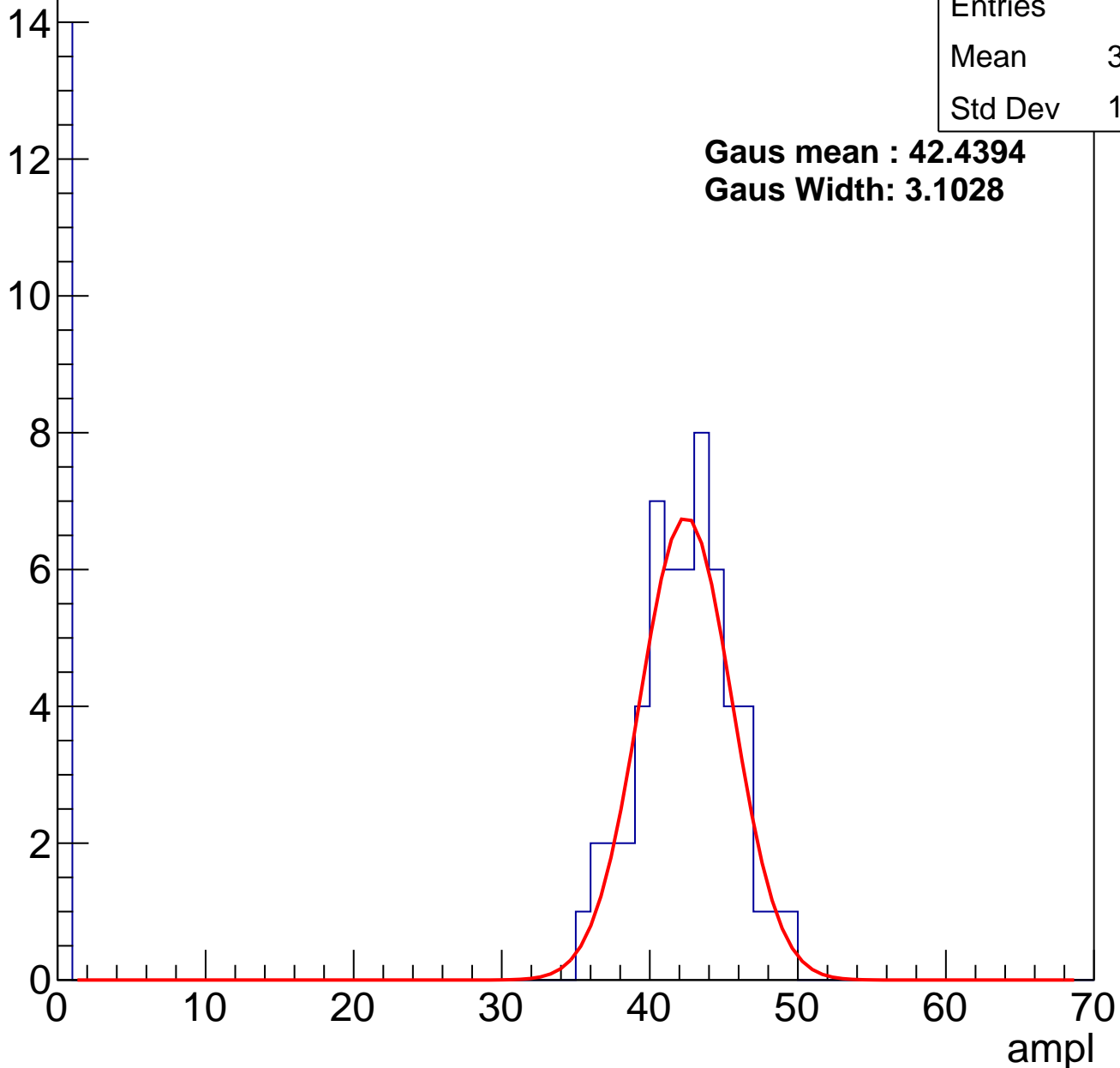
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	33.43
Std Dev	17.09

Gaus mean : 42.4394

Gaus Width: 3.1028

Entry

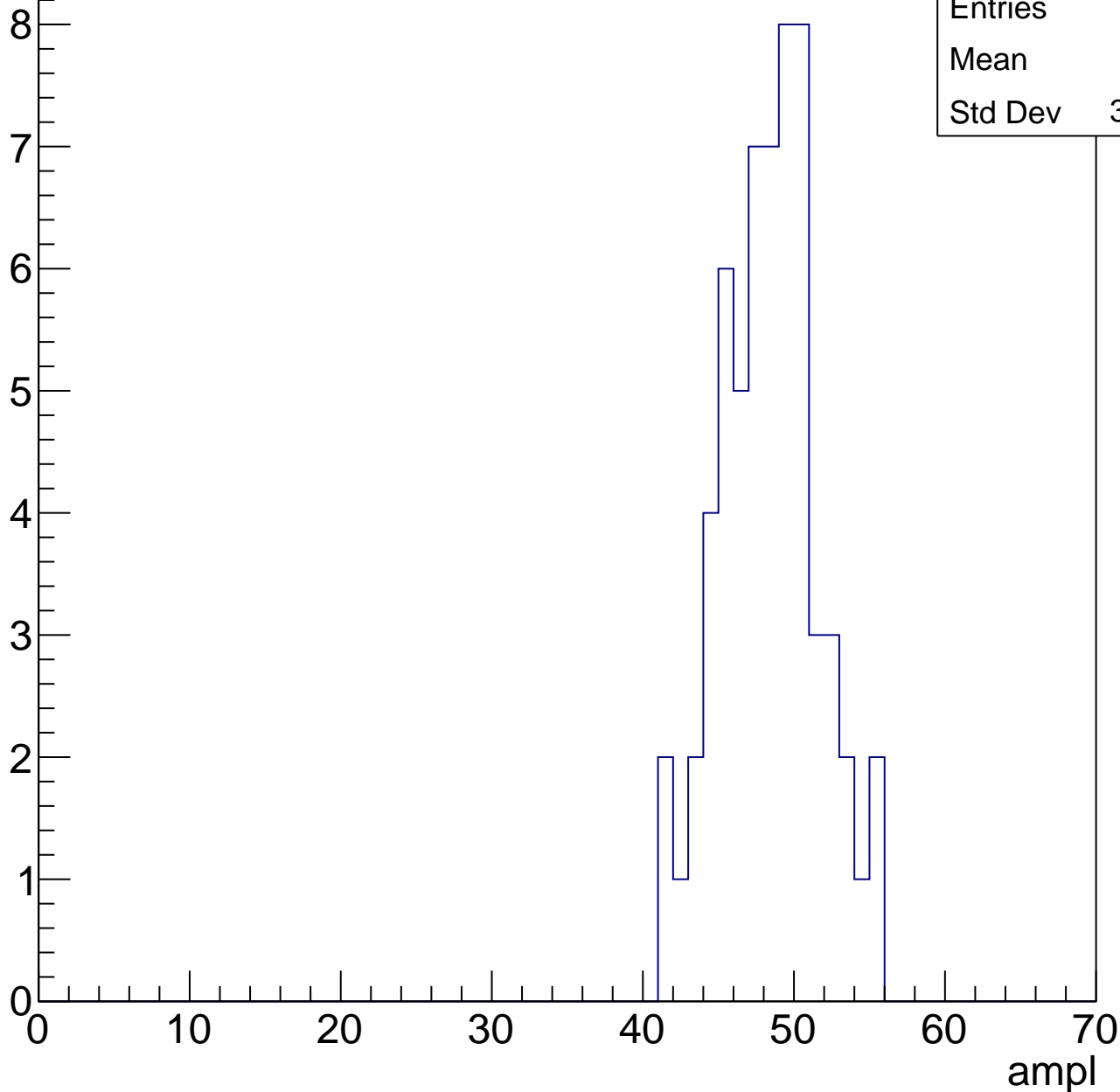


B1L103S, U2-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

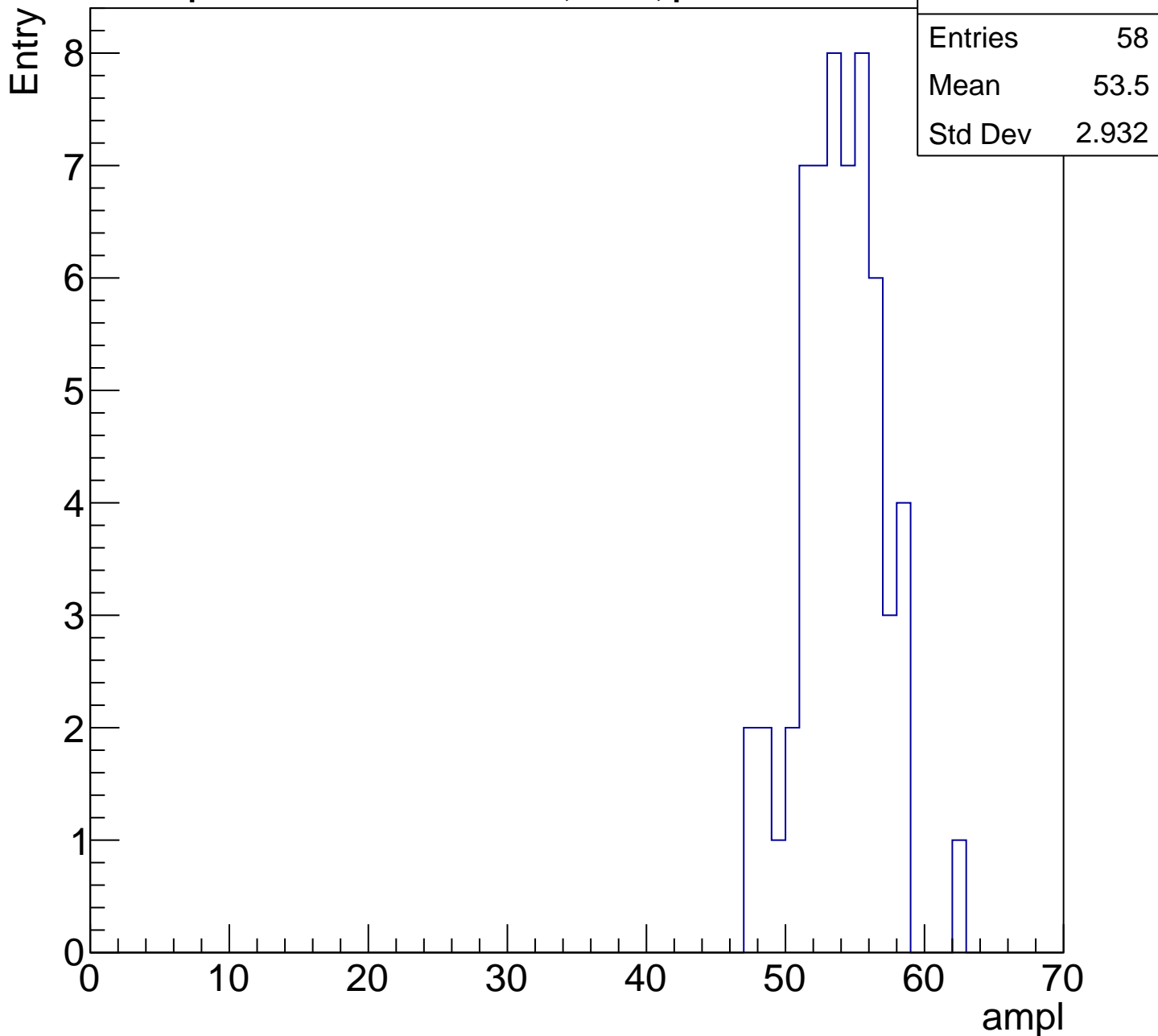
Entry

Entries	61
Mean	47.9
Std Dev	3.207



B1L103S, U2-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

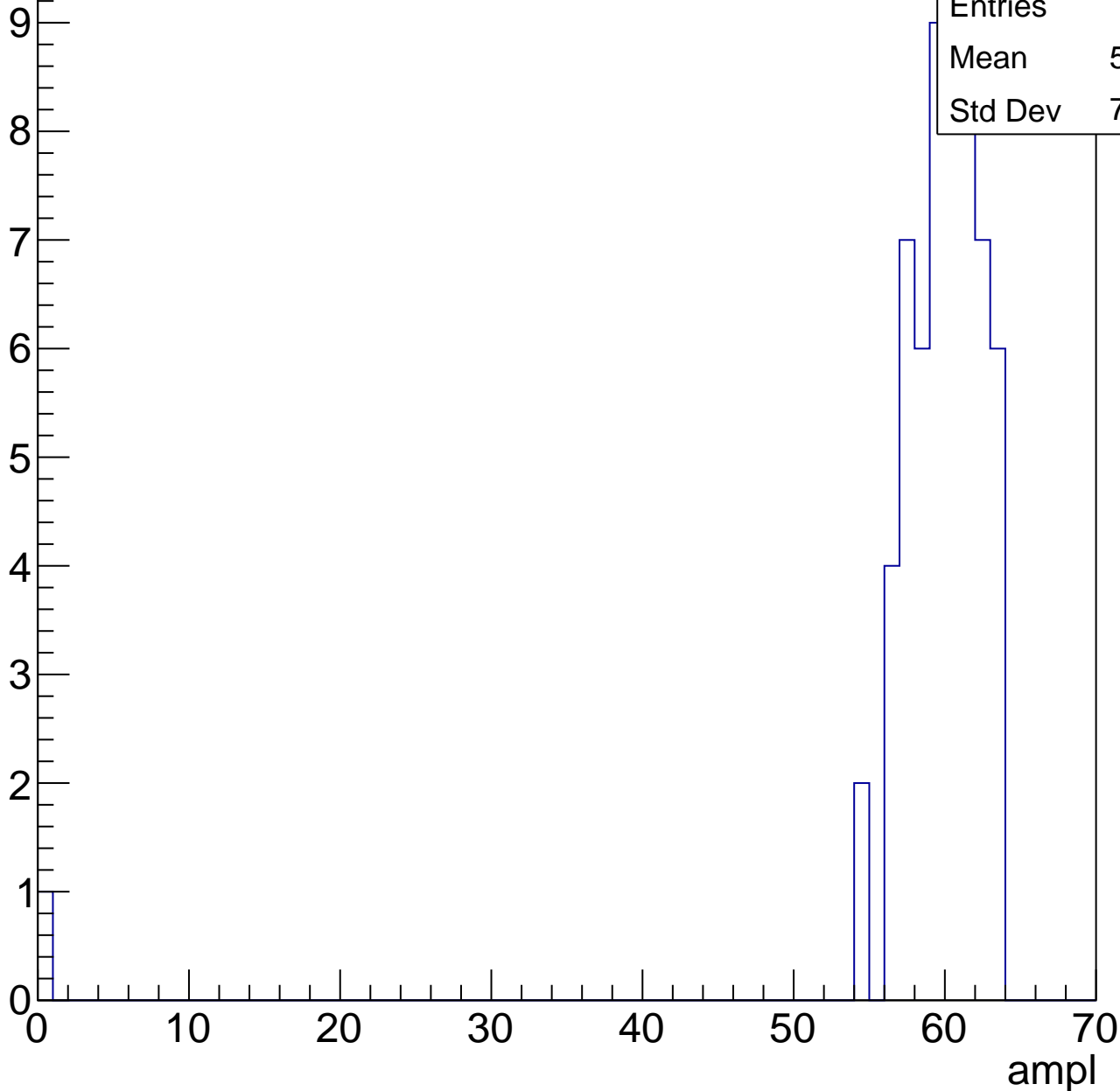


B1L103S, U2-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

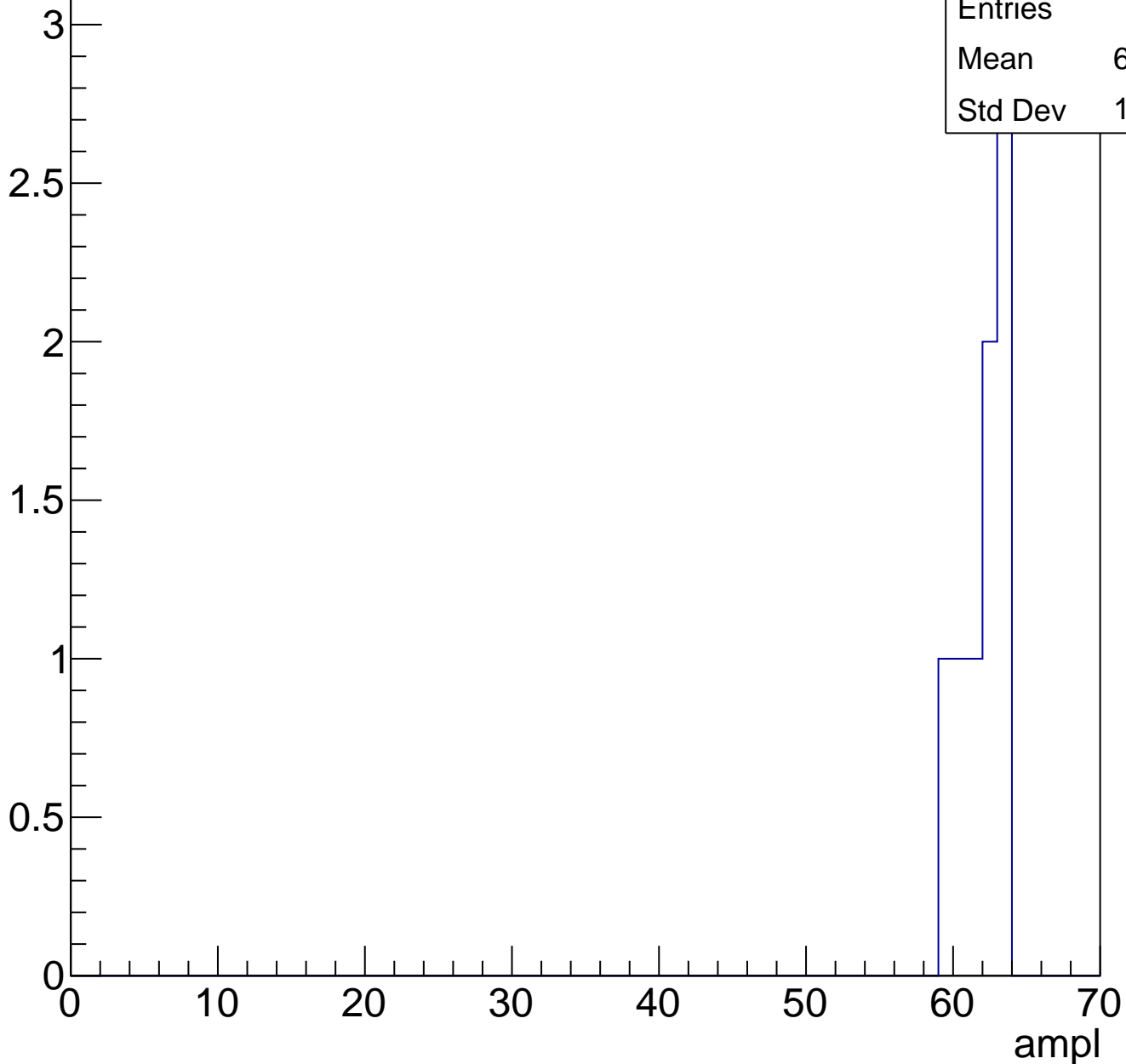
Entries	60
Mean	58.52
Std Dev	7.949



B1L103S, U2-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	3.15
Std Dev	13.73

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch20, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	23.44
Std Dev	10.56

Gaus mean : 29.0529

Gaus Width: 4.5915

Entry

12

10

8

6

4

2

0

0

10

20

30

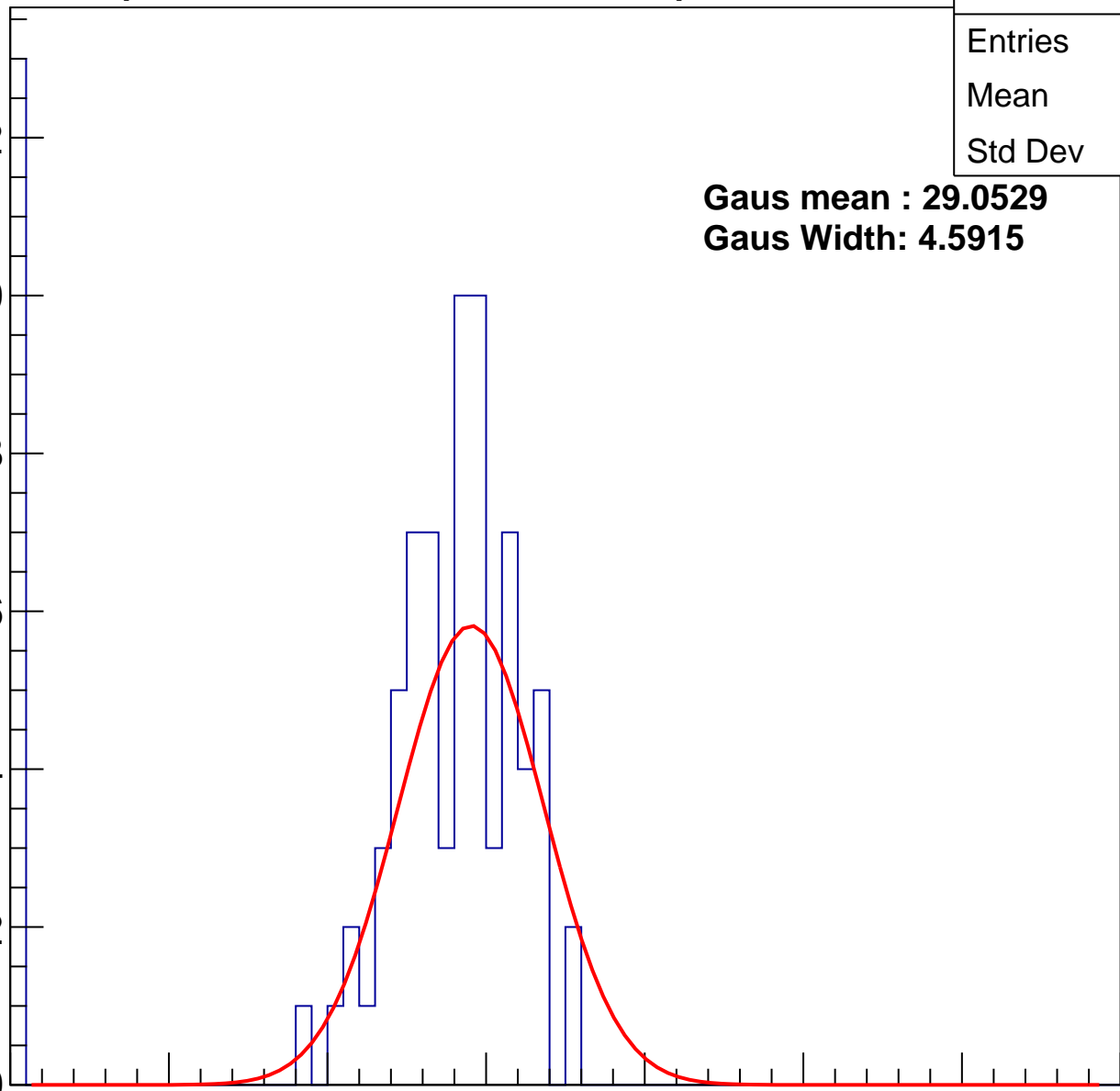
40

50

60

70

ampl



B1L103S, U2-ch20, adc1

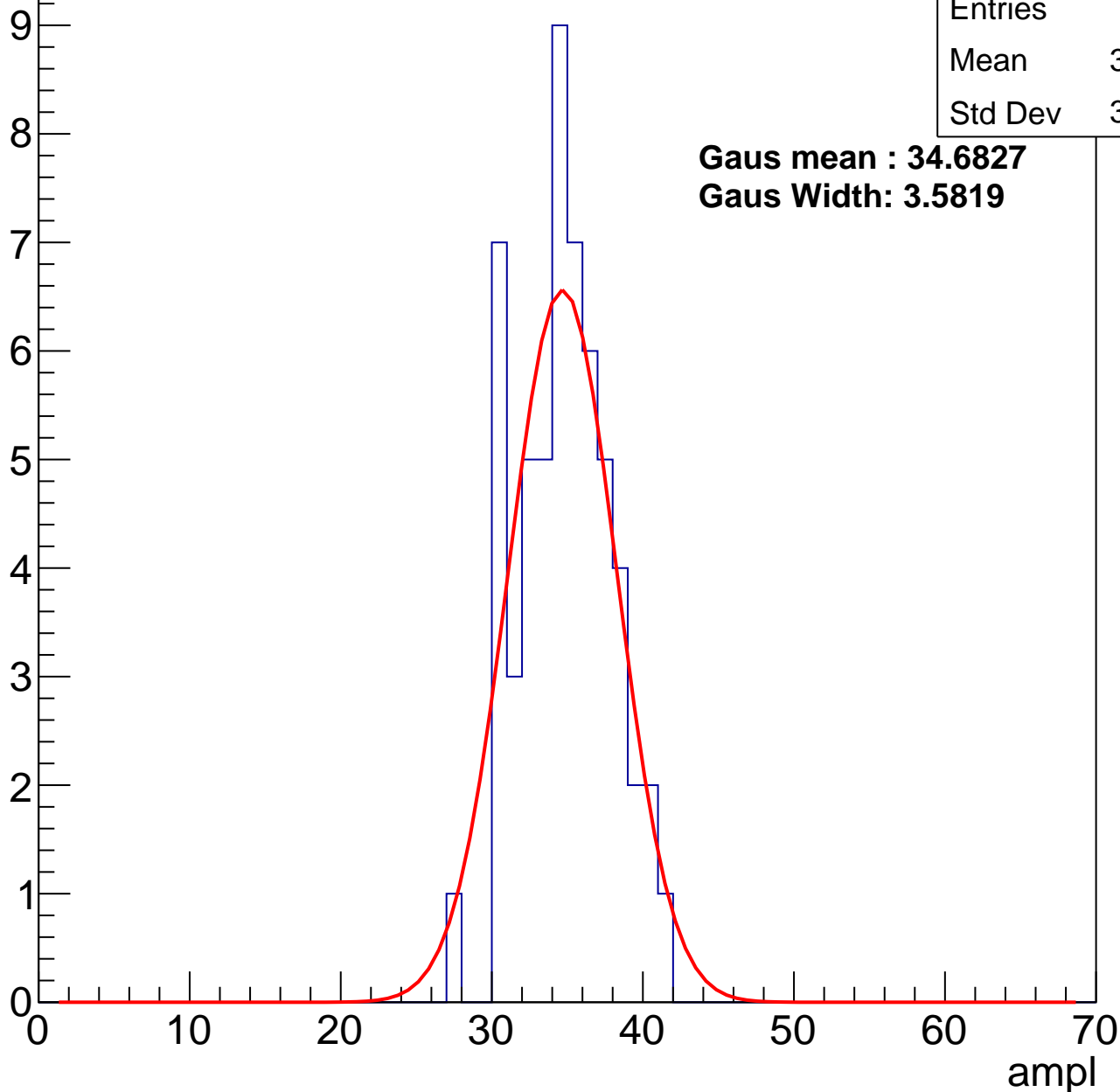
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	34.35
Std Dev	3.012

Gaus mean : 34.6827

Gaus Width: 3.5819



B1L103S, U2-ch20, adc2

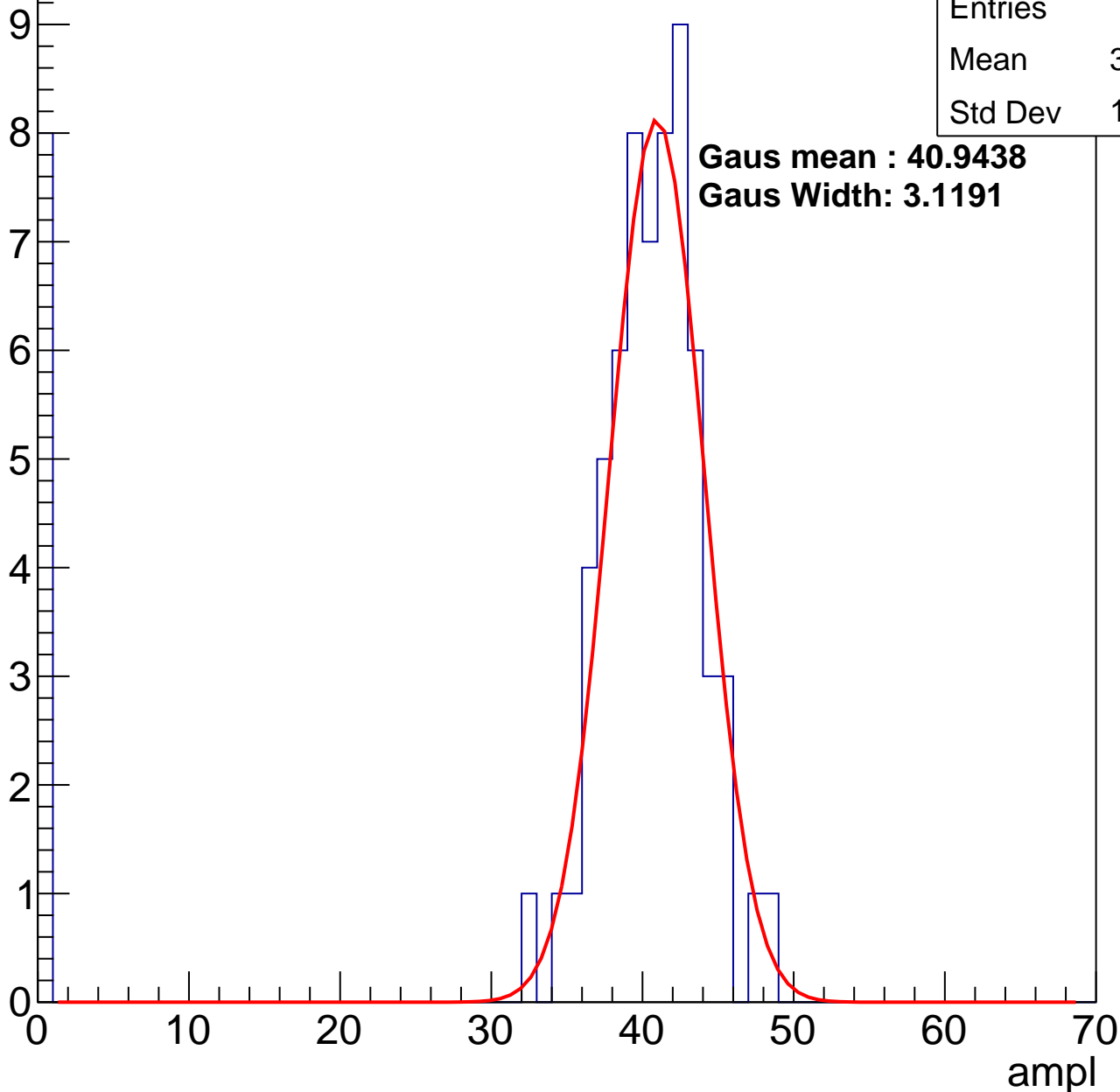
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.78
Std Dev	12.97

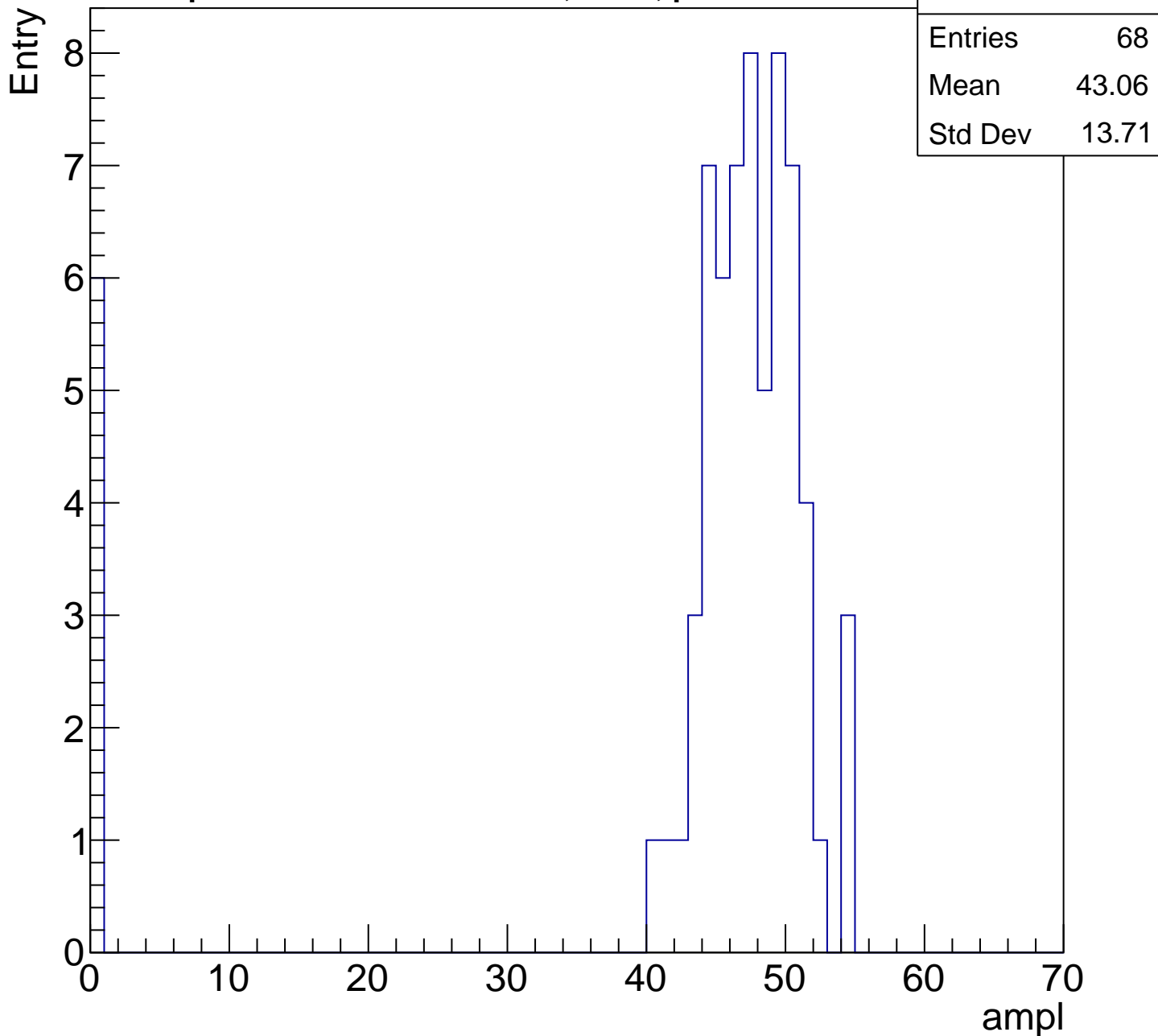
Gaus mean : 40.9438

Gaus Width: 3.1191



B1L103S, U2-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

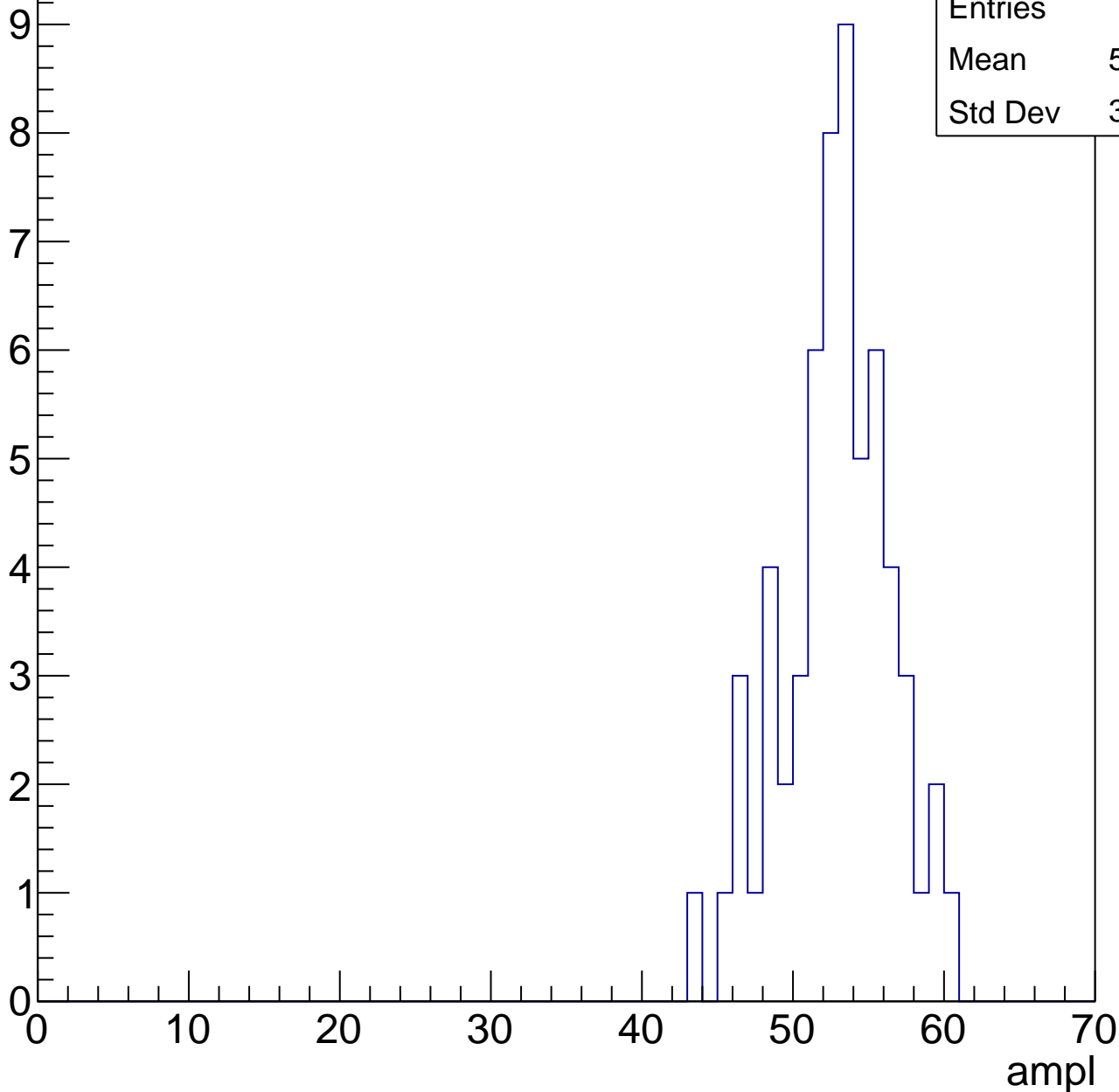


B1L103S, U2-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	52.38
Std Dev	3.592



B1L103S, U2-ch20, adc5

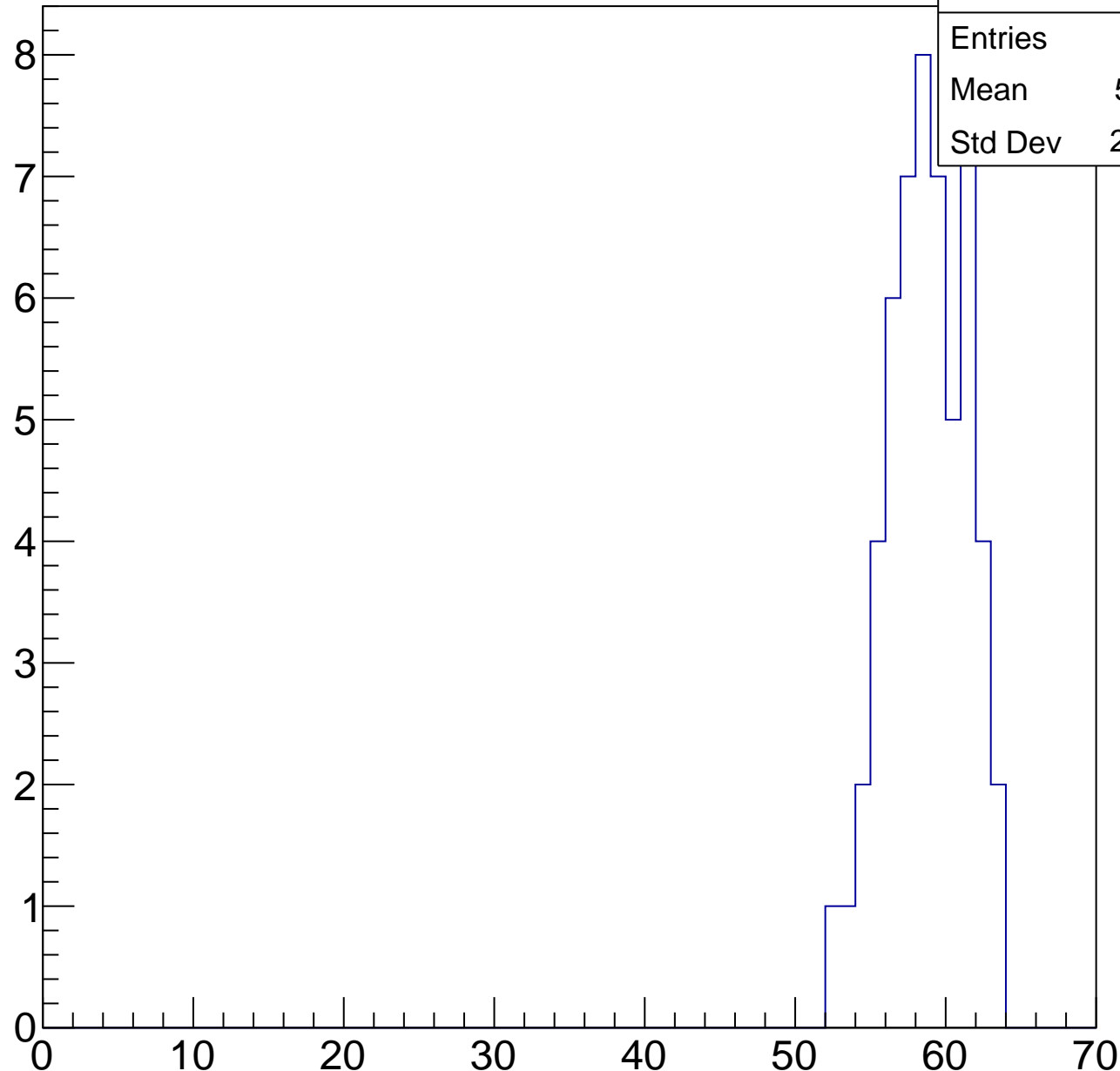
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	55
Mean	58.31
Std Dev	2.586

ampl

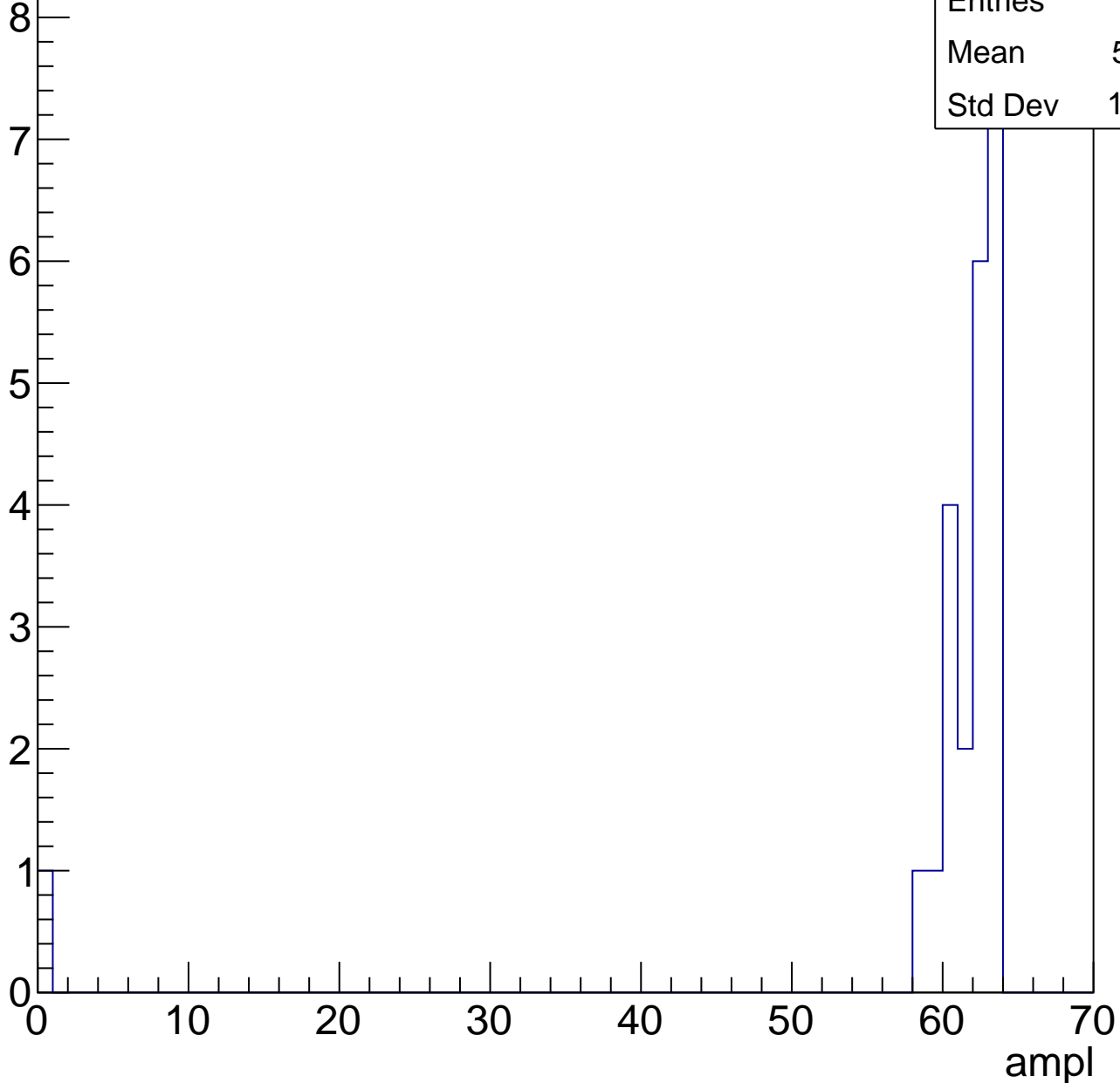


B1L103S, U2-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.91
Std Dev	12.64



B1L103S, U2-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U2-ch21, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	23.95
Std Dev	11.56

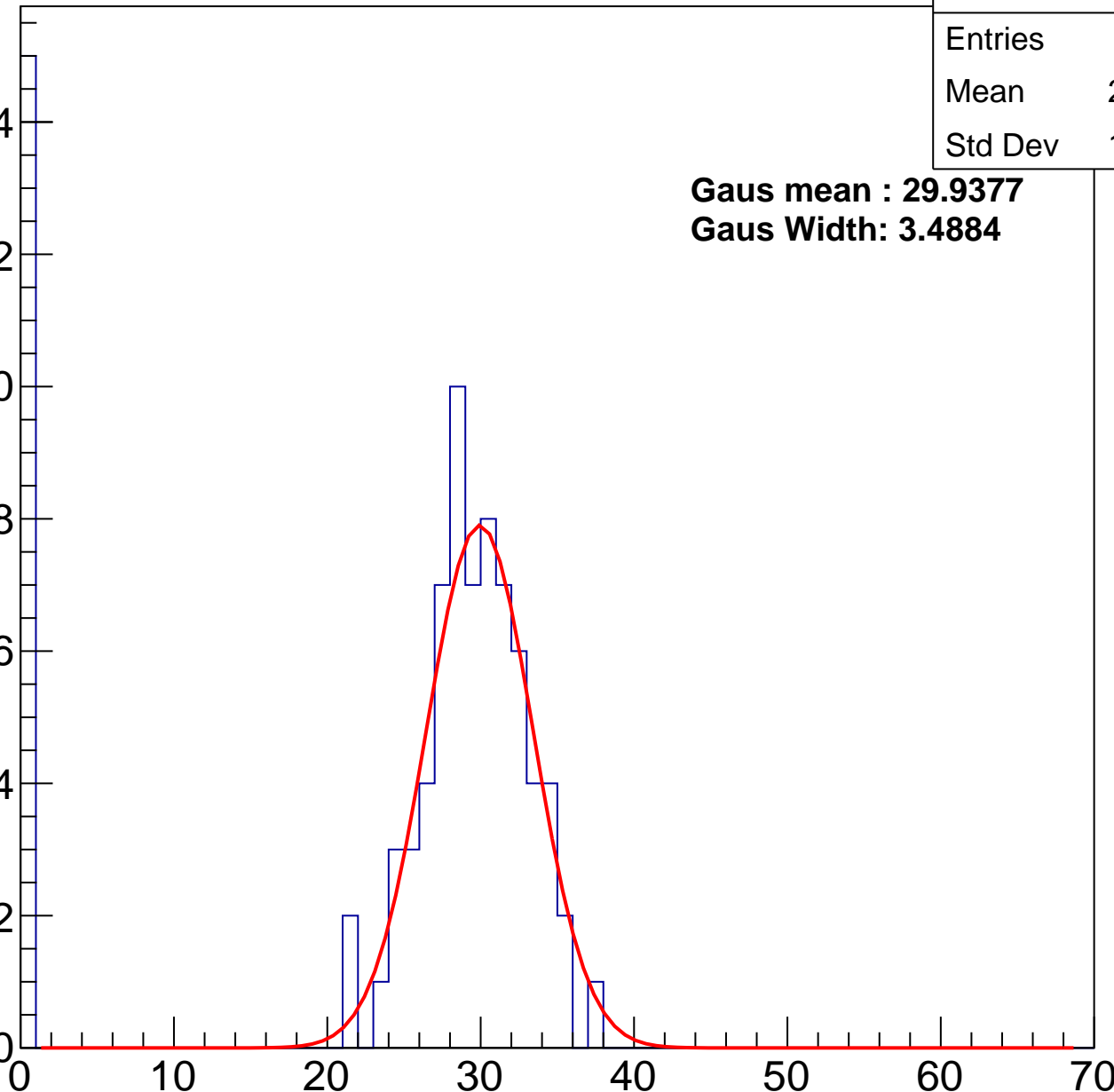
Gaus mean : 29.9377

Gaus Width: 3.4884

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch21, adc1

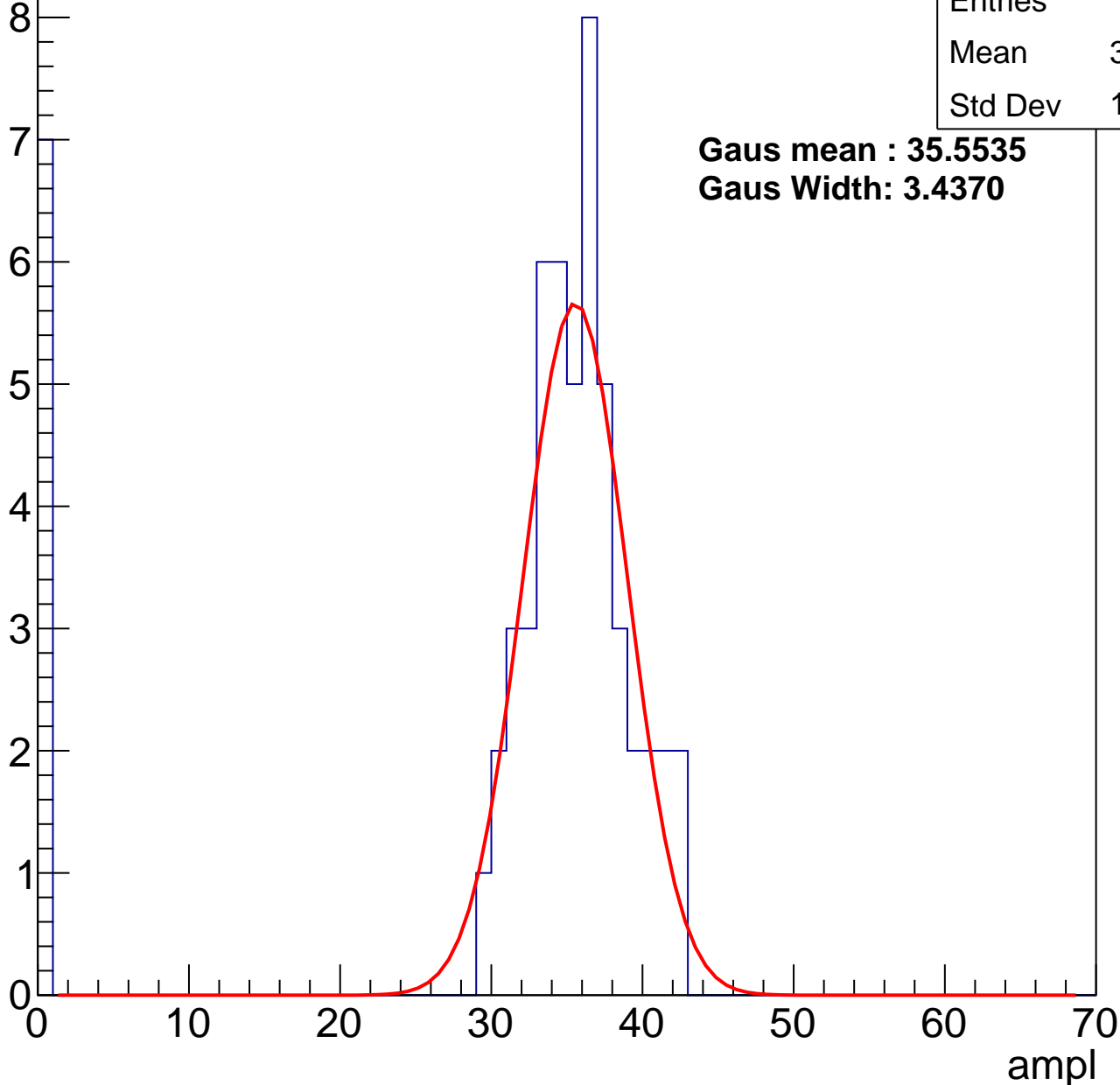
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	30.98
Std Dev	11.96

Gaus mean : 35.5535

Gaus Width: 3.4370



B1L103S, U2-ch21, adc2

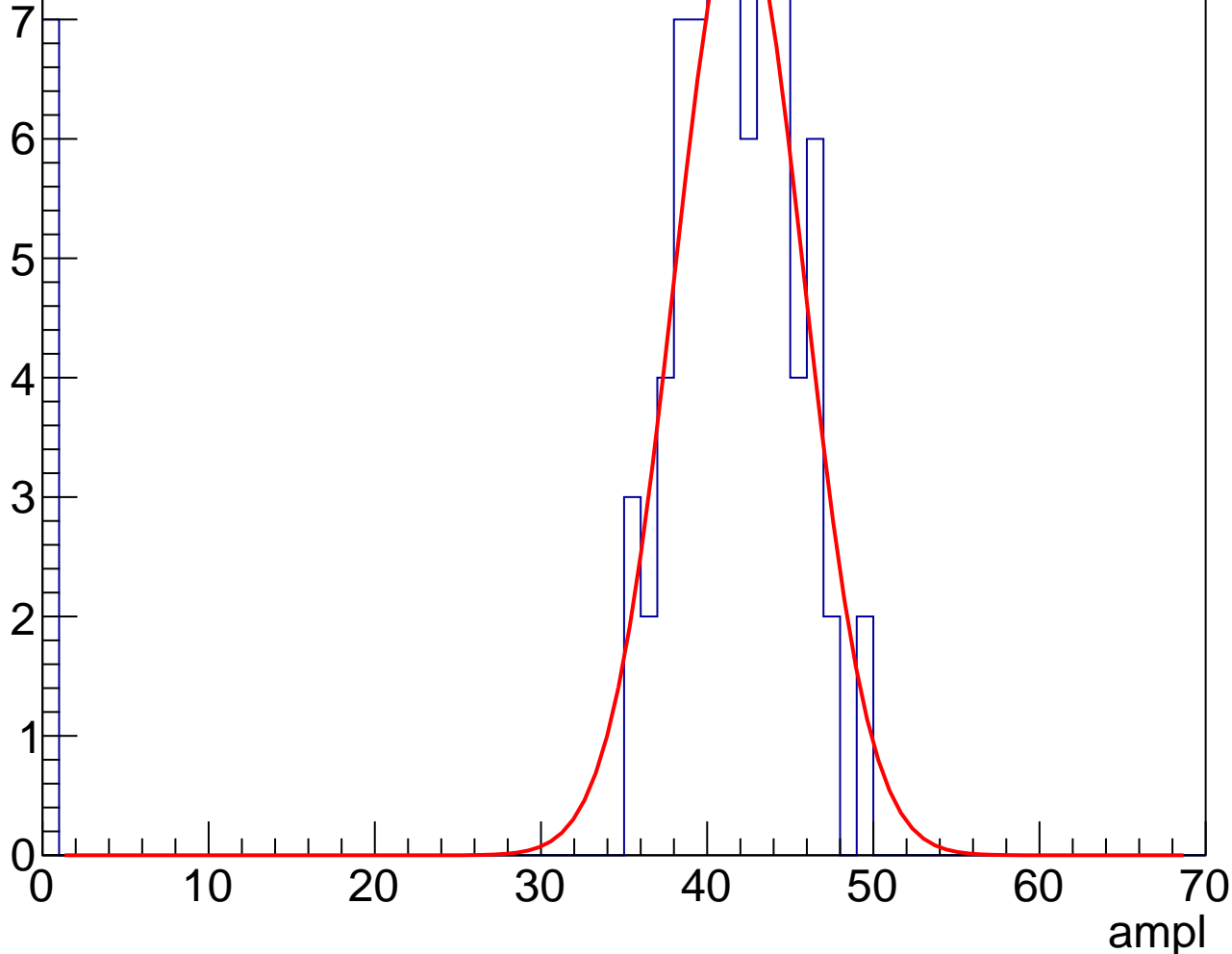
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	37.96
Std Dev	11.95

Gaus mean : 41.9290

Gaus Width: 3.8979

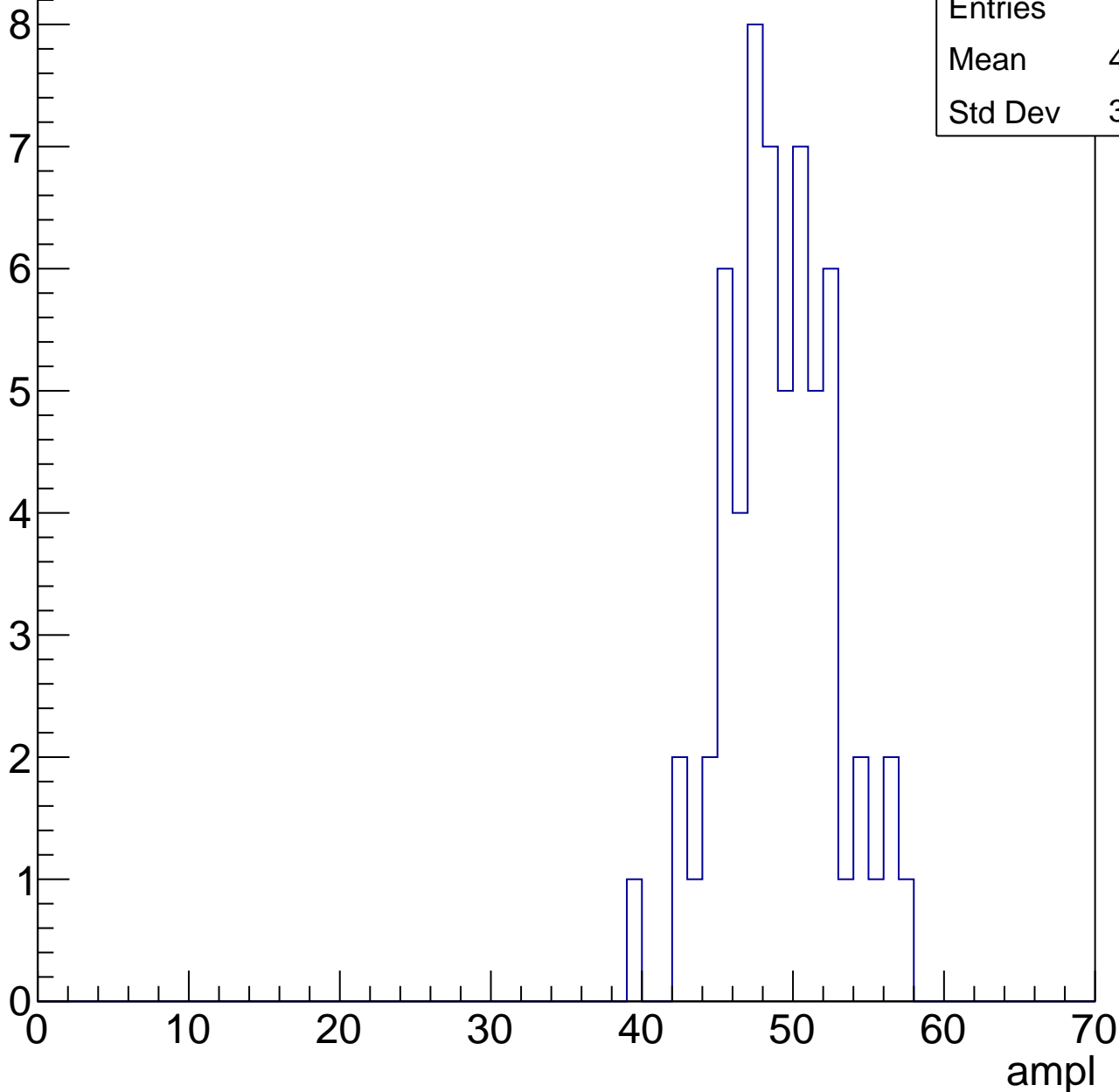


B1L103S, U2-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	48.64
Std Dev	3.603

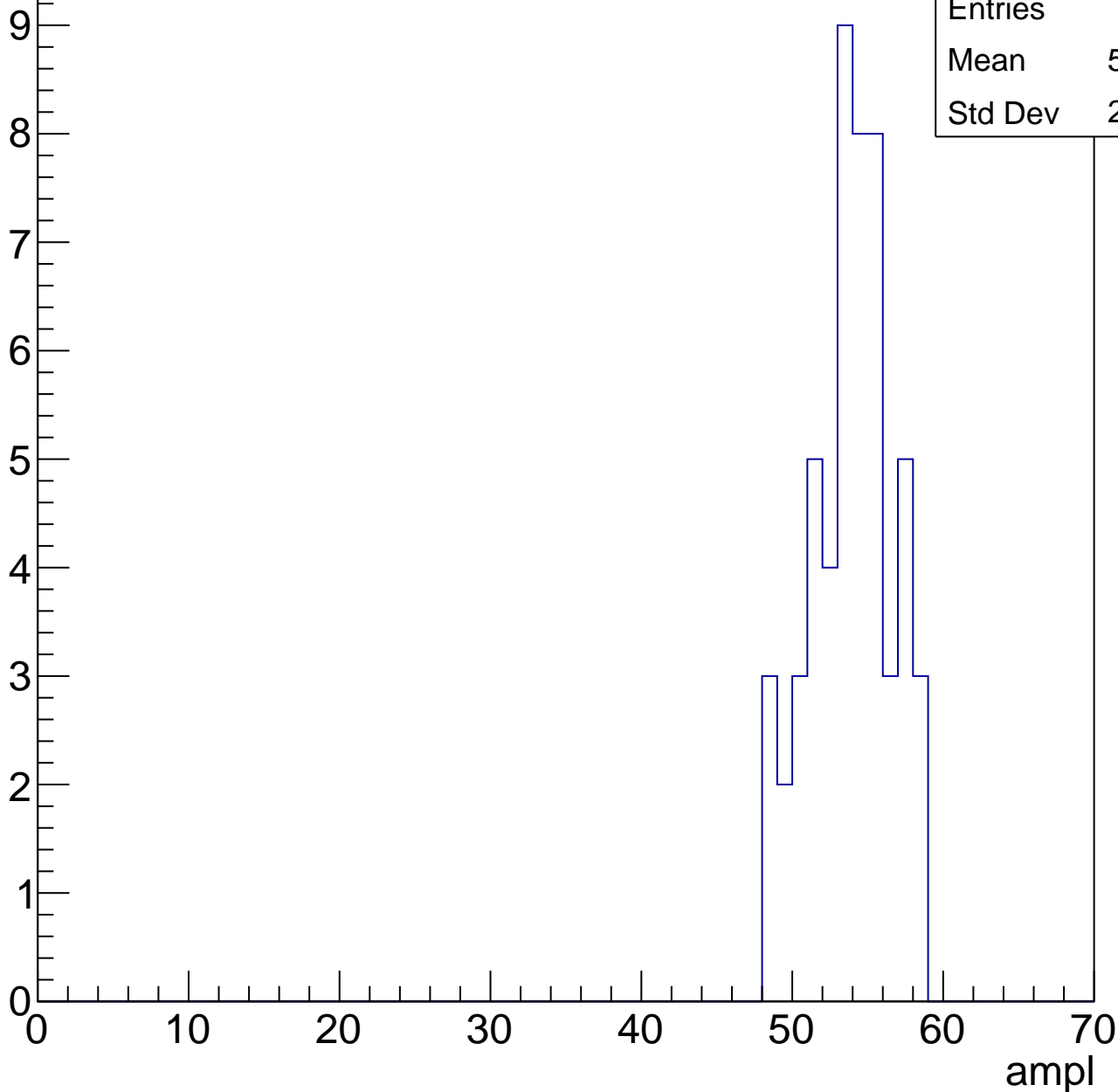


B1L103S, U2-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	53.42
Std Dev	2.645

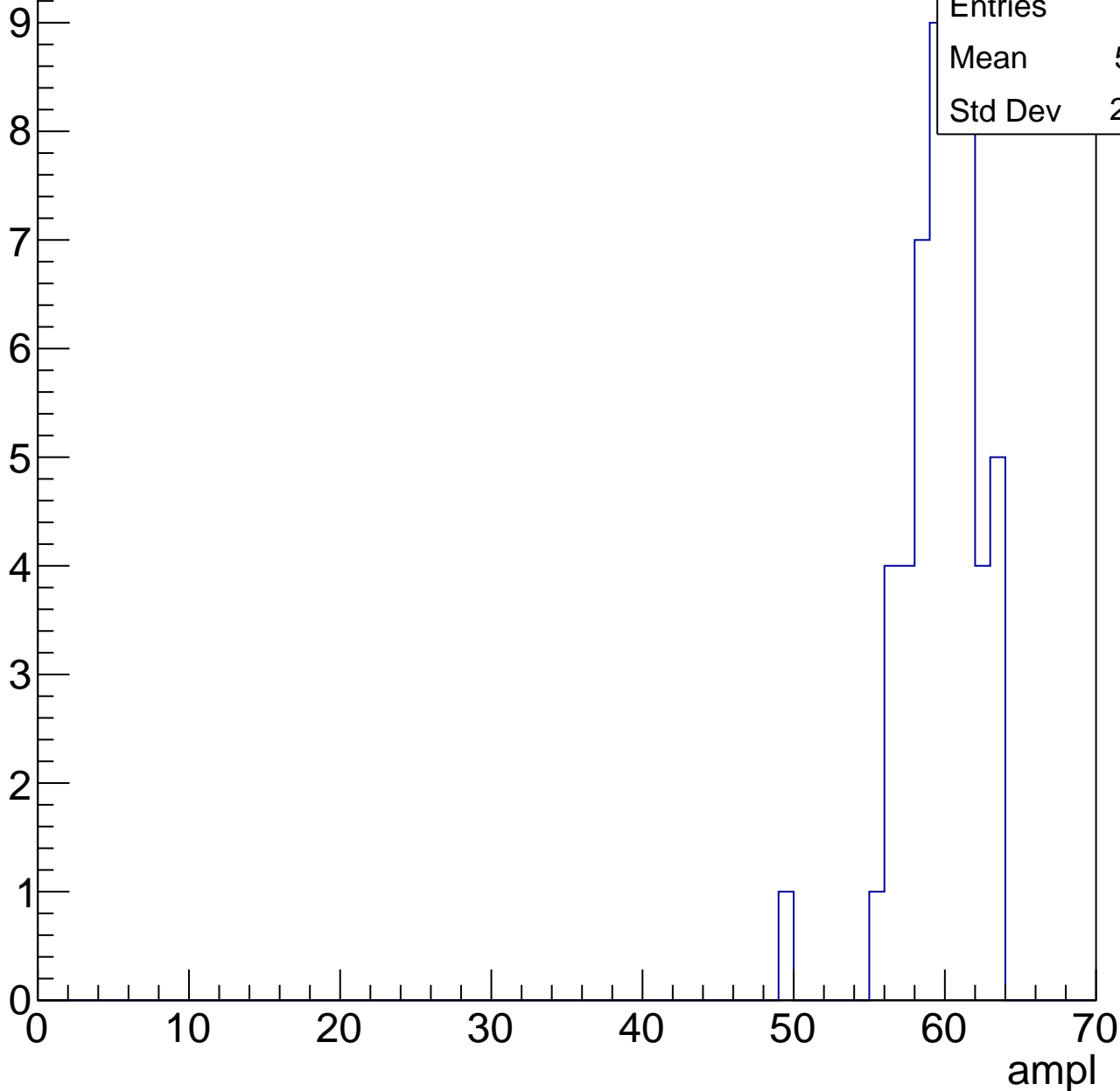


B1L103S, U2-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	59.31
Std Dev	2.508

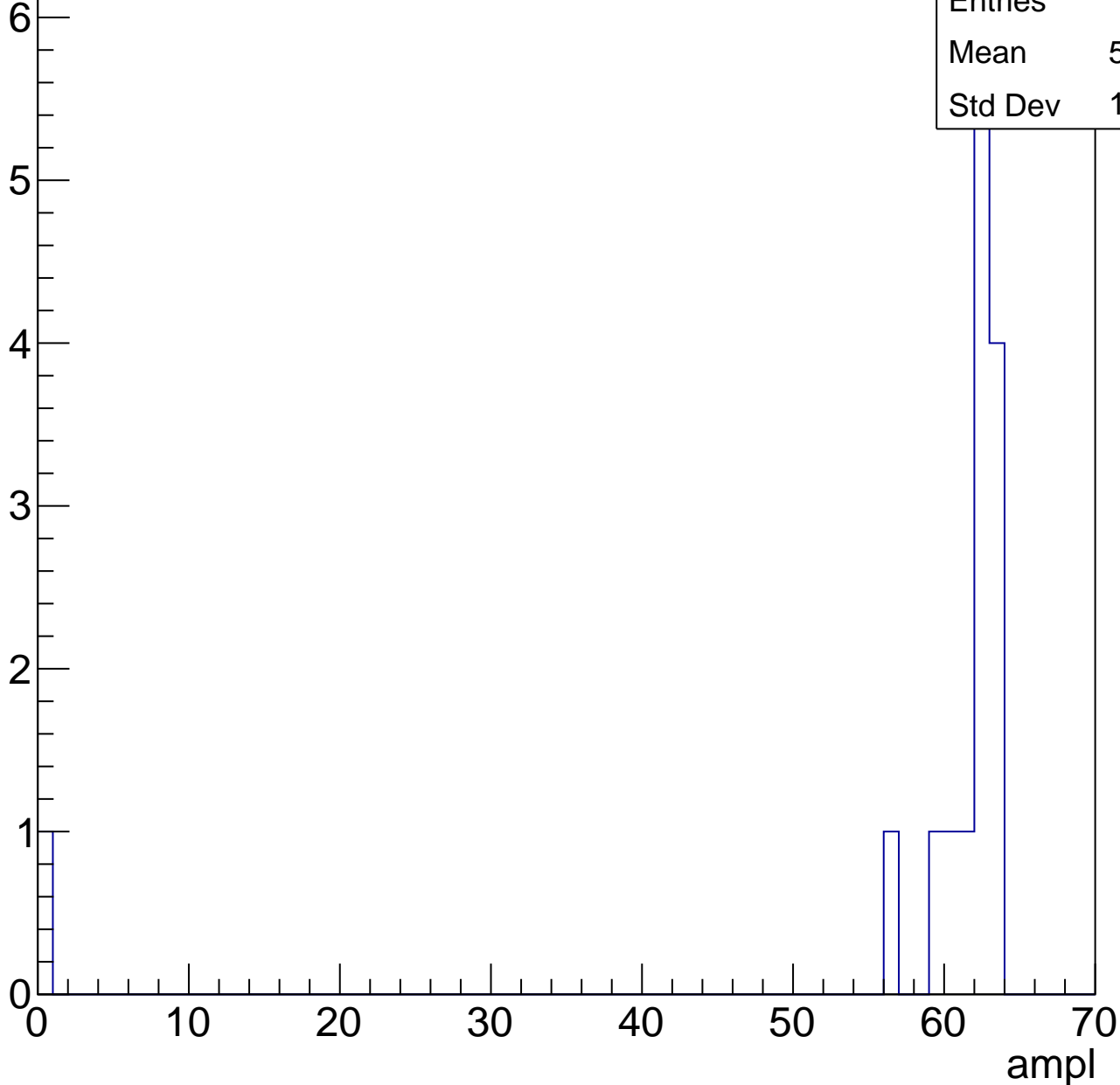


B1L103S, U2-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

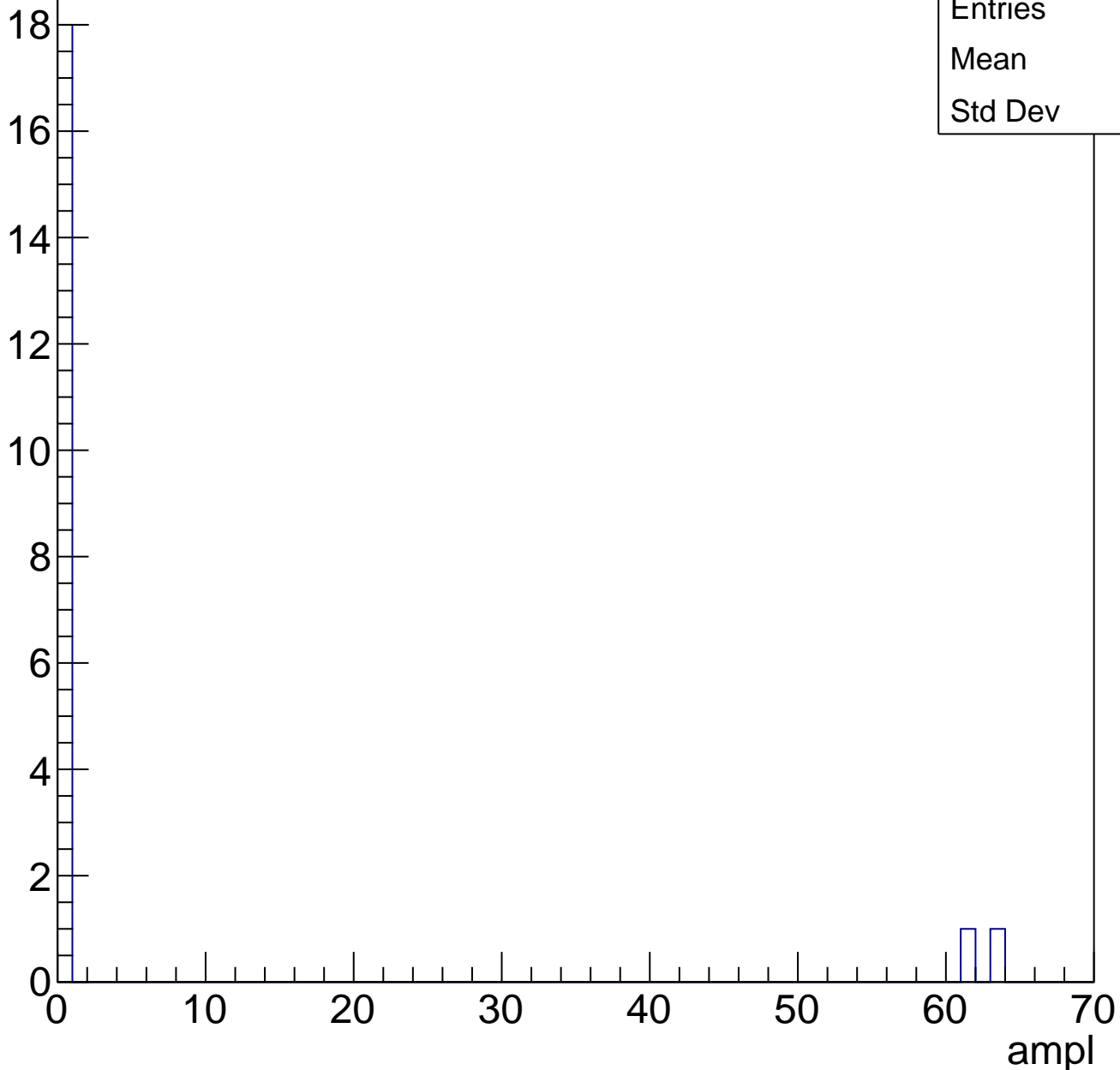
Entries	15
Mean	57.33
Std Dev	15.43



B1L103S, U2-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	6.2
Std Dev	18.6

B1L103S, U2-ch22, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	23.16
Std Dev	9.71

Gaus mean : 27.2337

Gaus Width: 3.8626

Entry

12

10

8

6

4

2

0

0

10

20

30

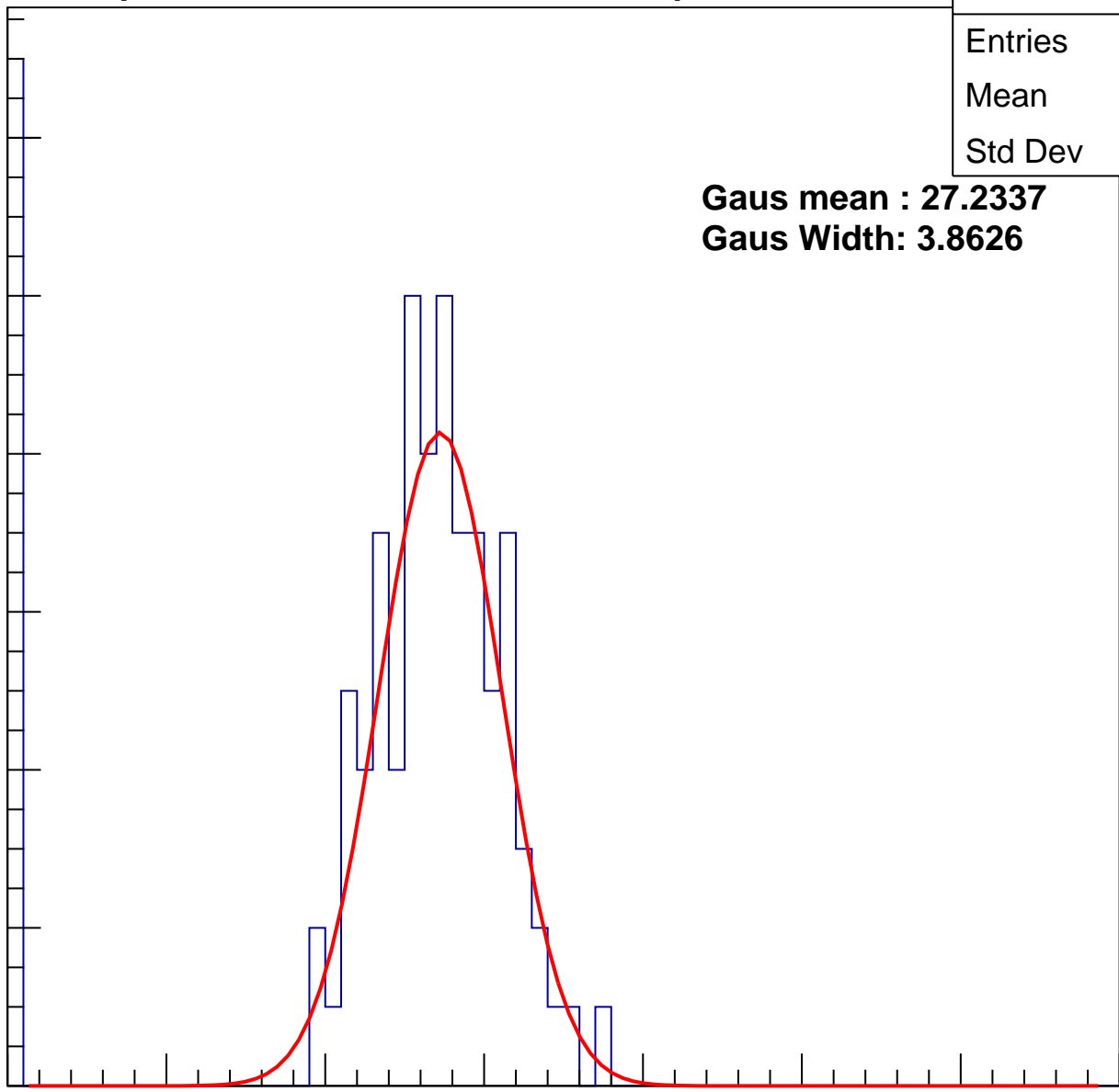
40

50

60

70

ampl



B1L103S, U2-ch22, adc1

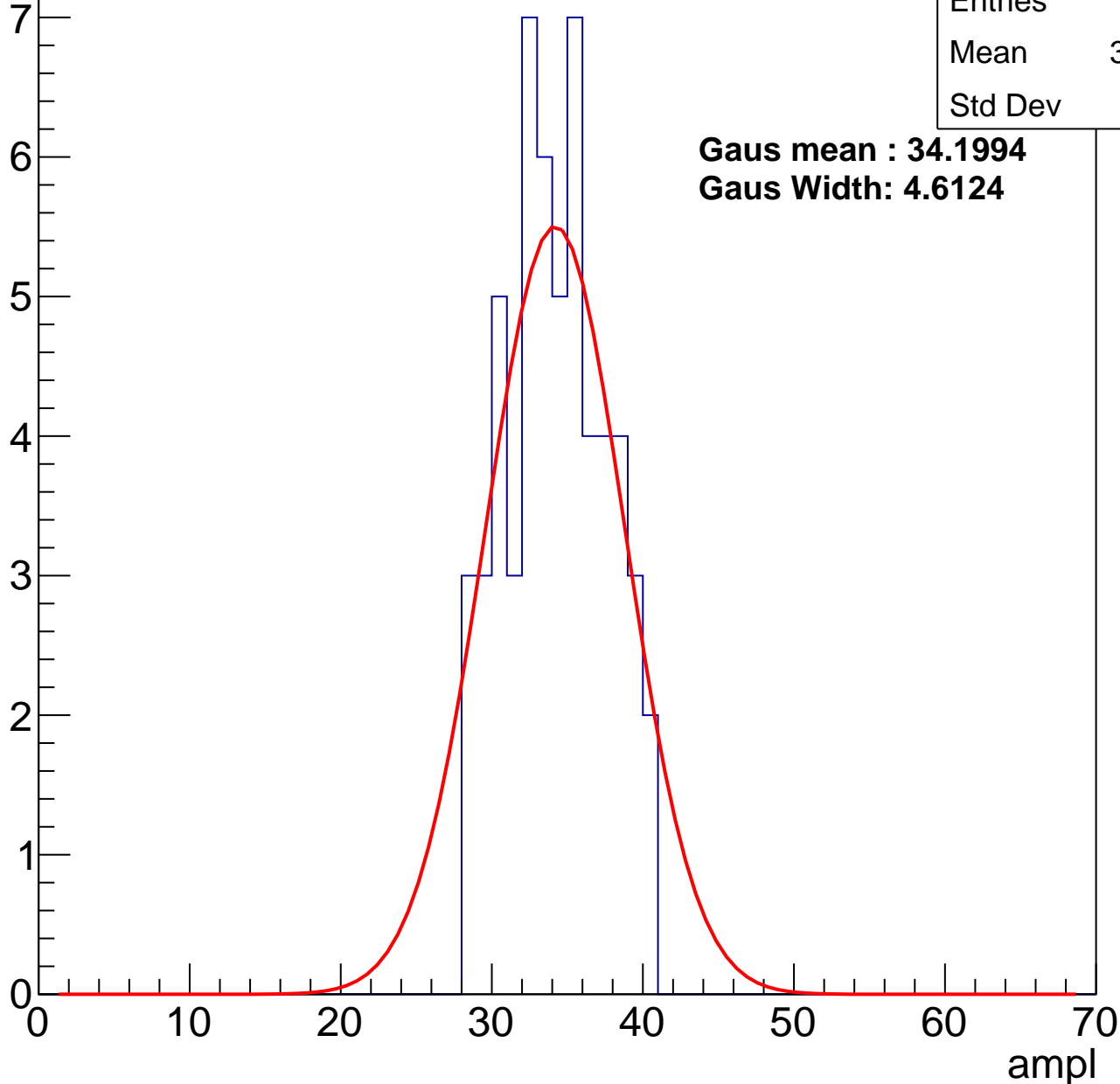
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	33.79
Std Dev	3.25

Gaus mean : 34.1994

Gaus Width: 4.6124



B1L103S, U2-ch22, adc2

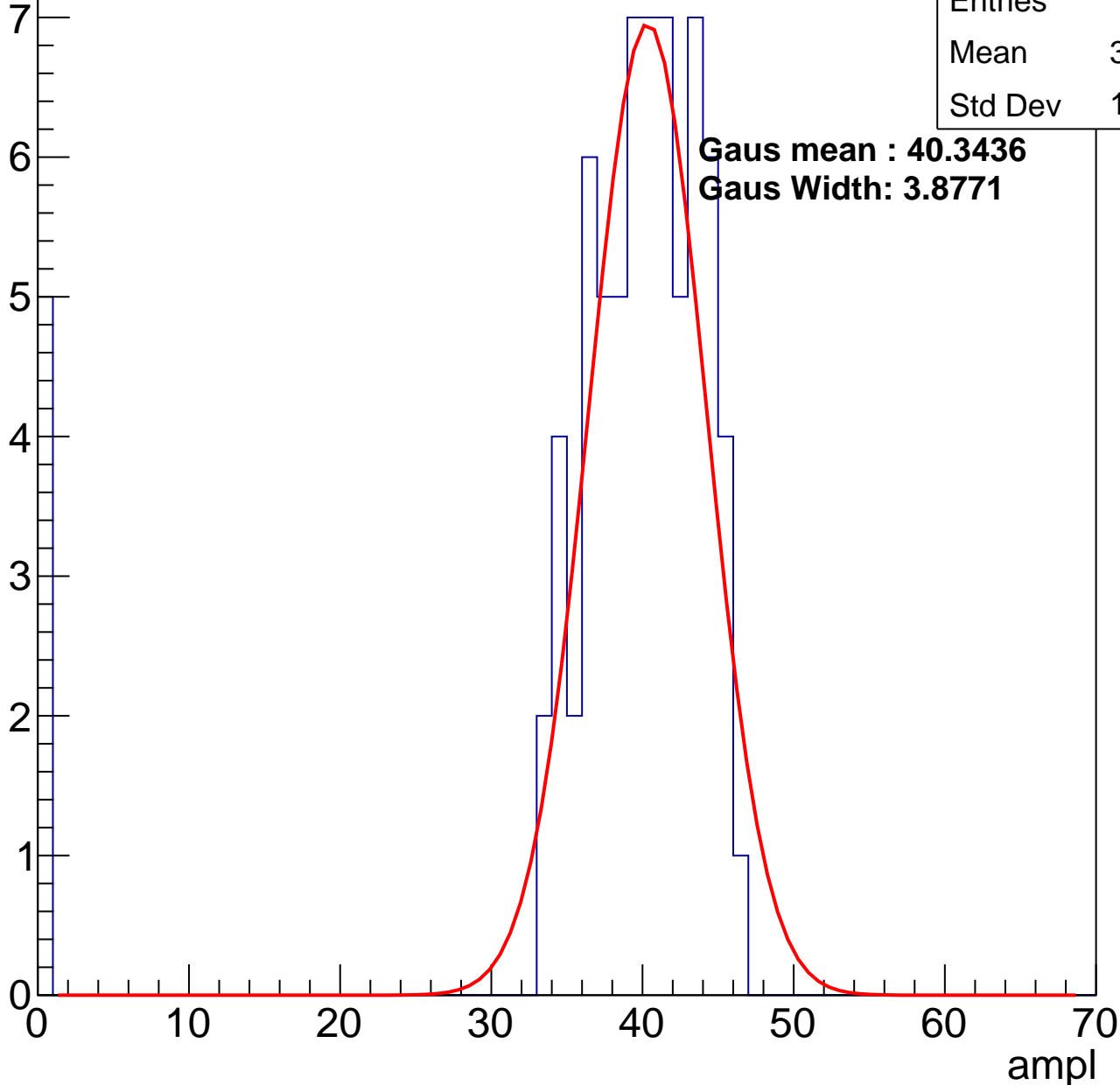
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	37.04
Std Dev	10.56

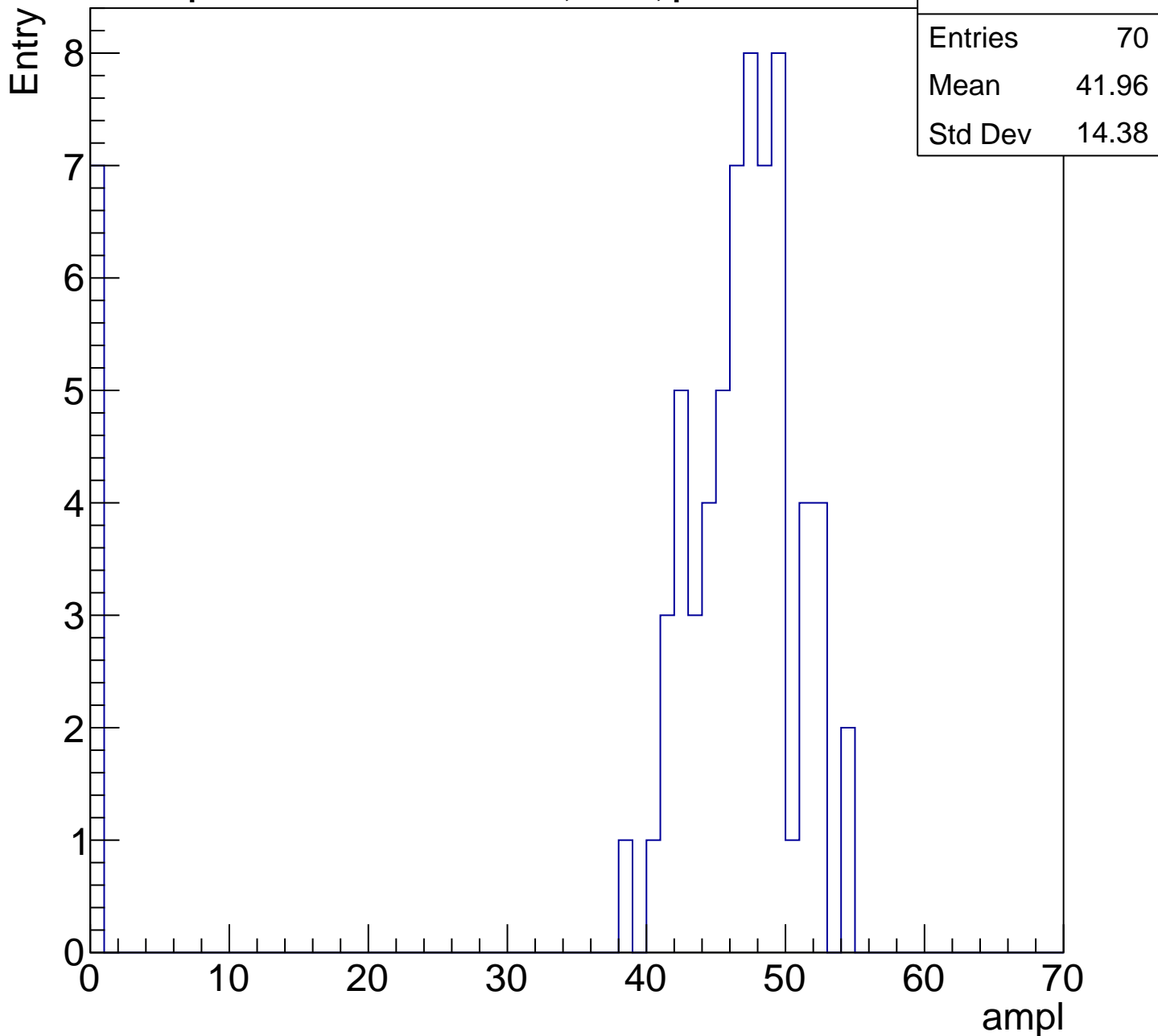
Gaus mean : 40.3436

Gaus Width: 3.8771



B1L103S, U2-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	51.44
Std Dev	7.215

Entry

10

8

6

4

2

0

0

10

20

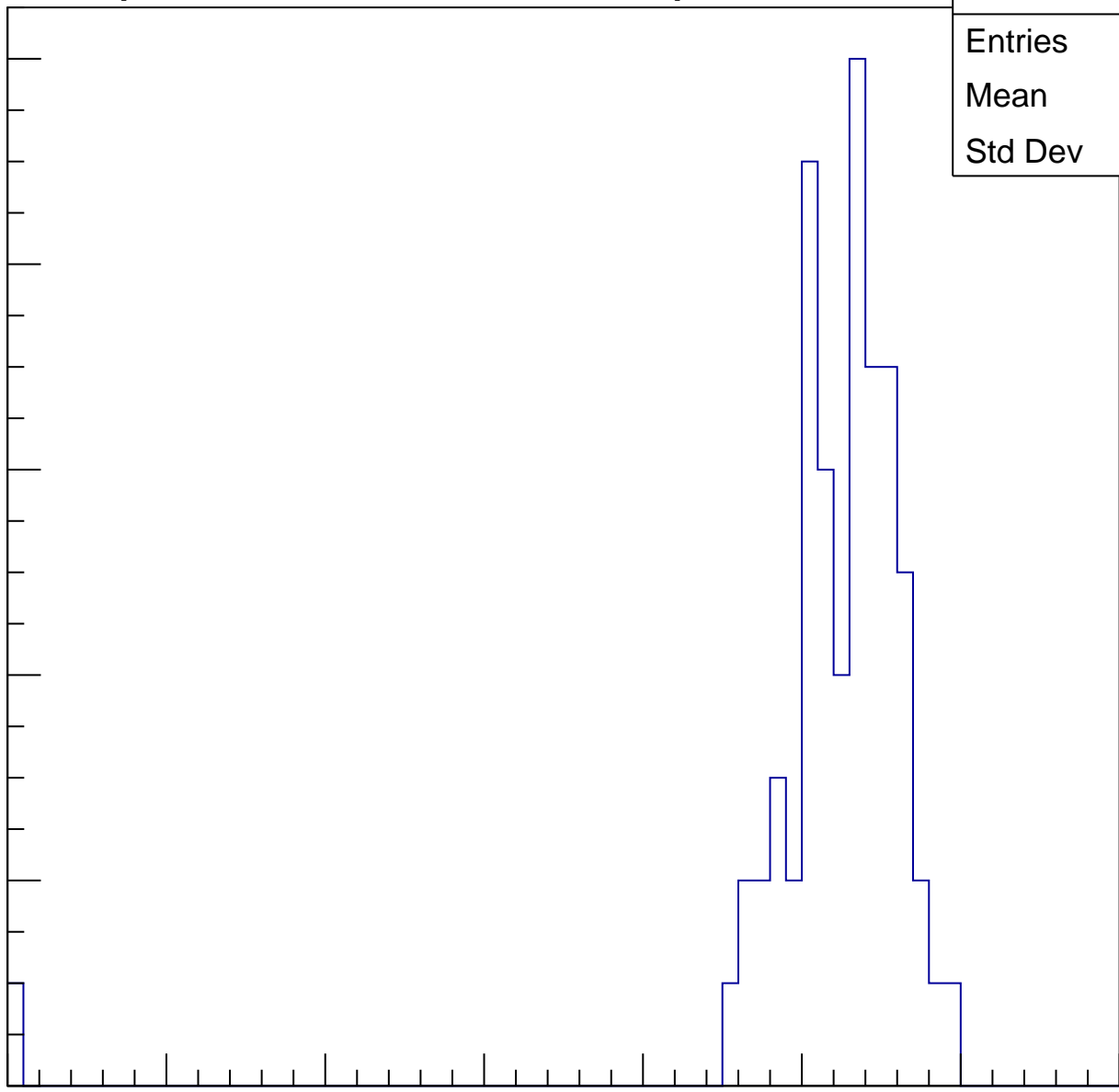
30

40

50

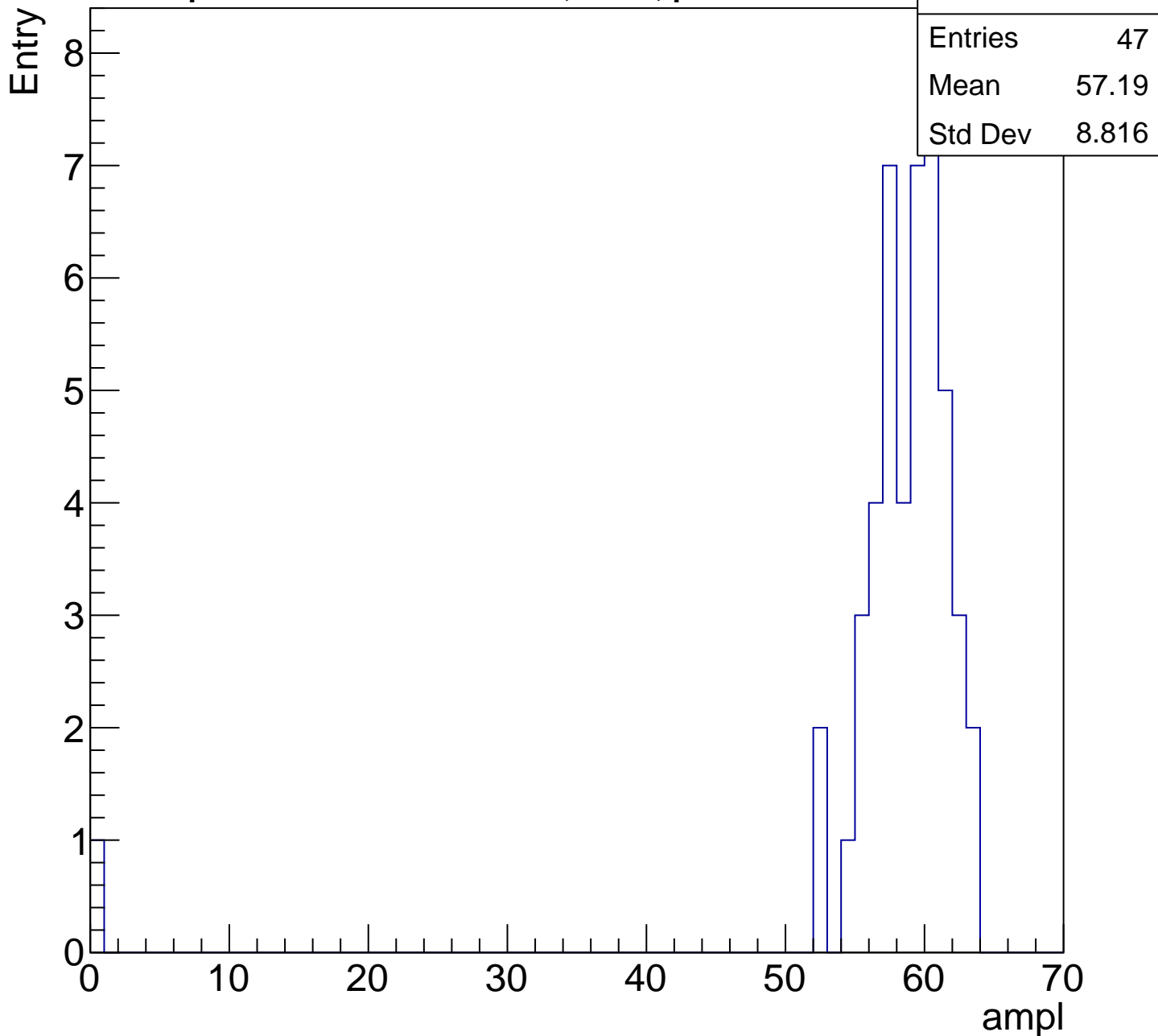
60

ampl



B1L103S, U2-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

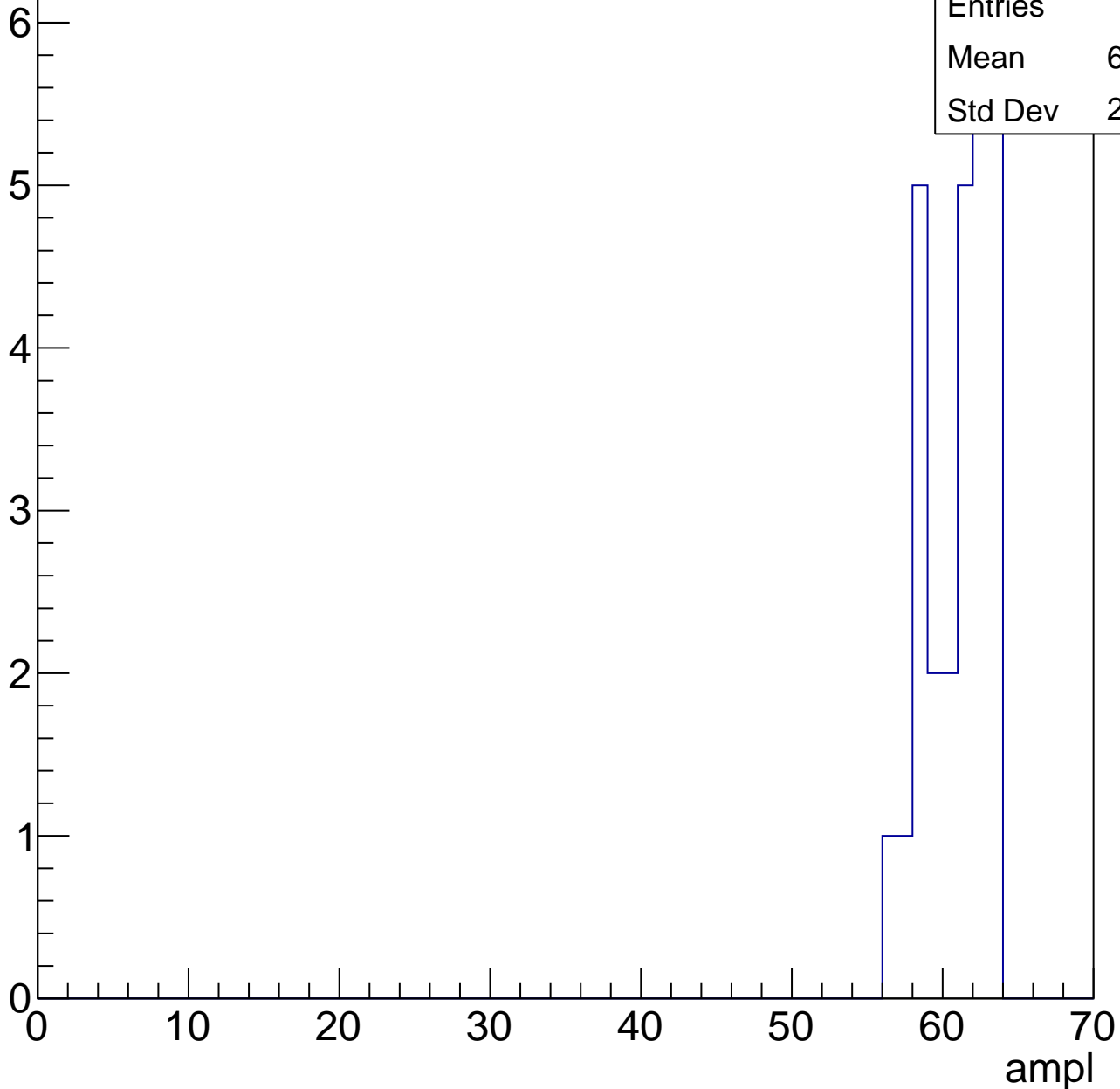


B1L103S, U2-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	60.57
Std Dev	2.078

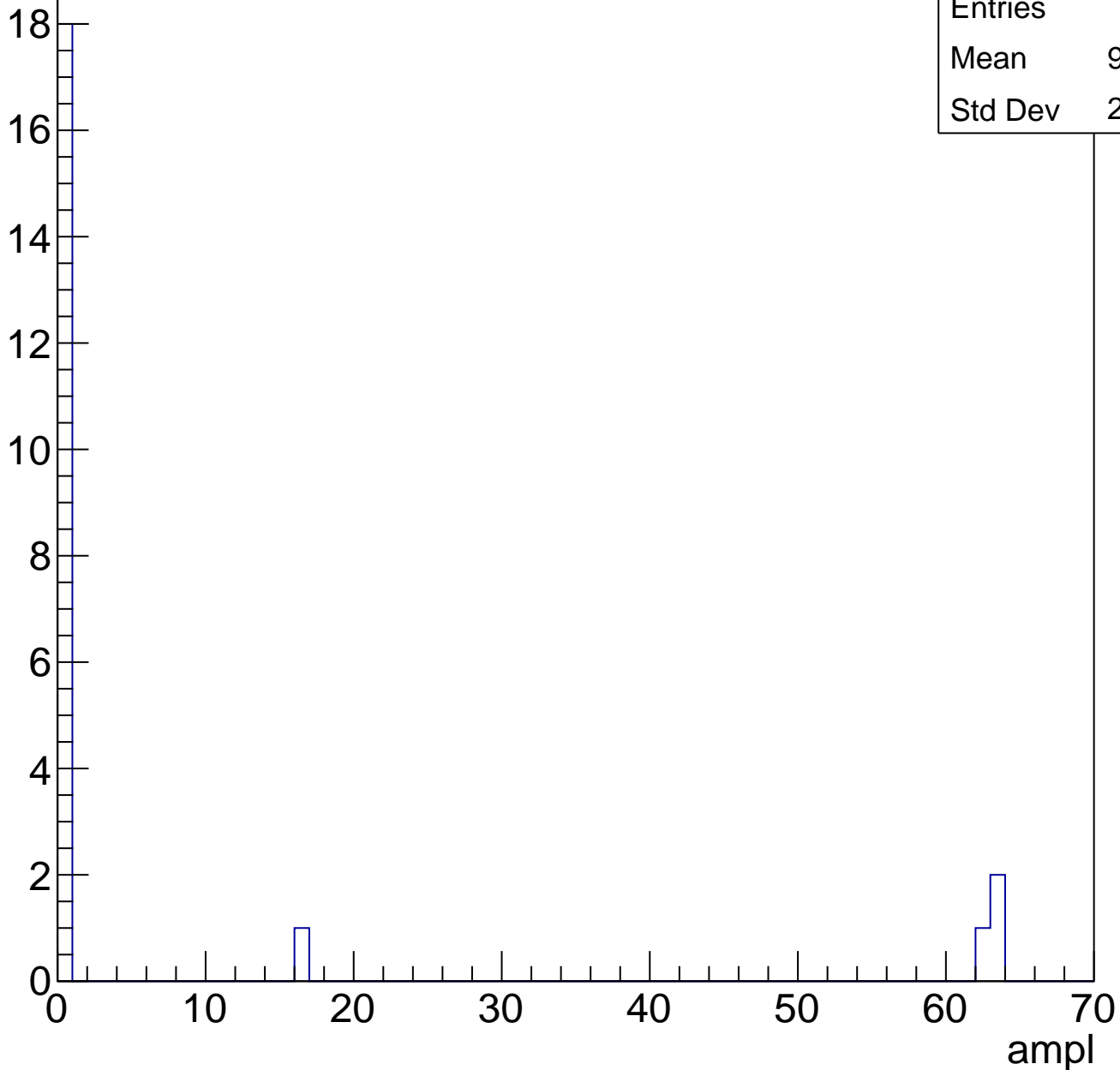


B1L103S, U2-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	9.273
Std Dev	21.48

Entry



B1L103S, U2-ch23, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.14
Std Dev	11.46

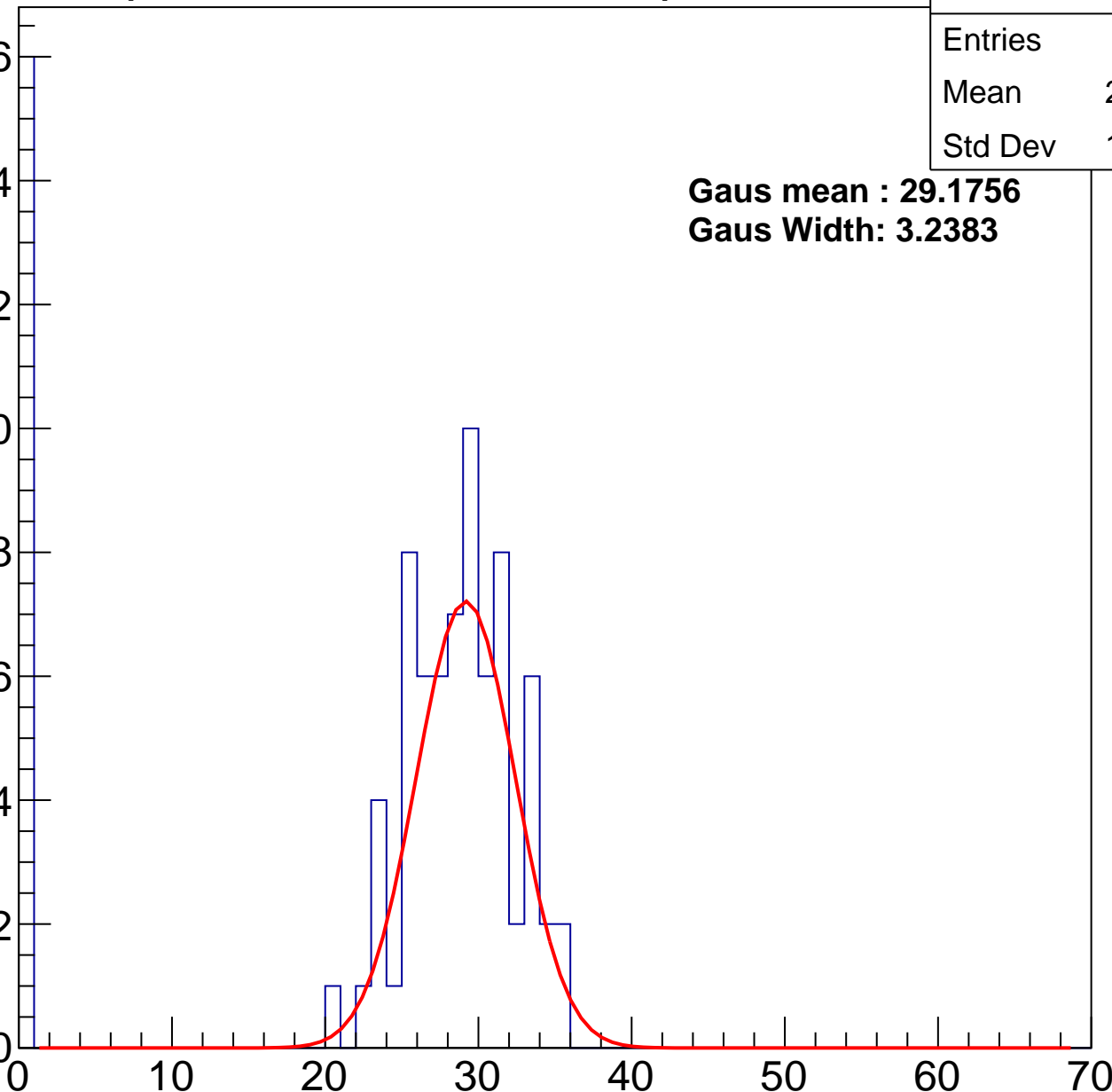
Gaus mean : 29.1756

Gaus Width: 3.2383

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch23, adc1

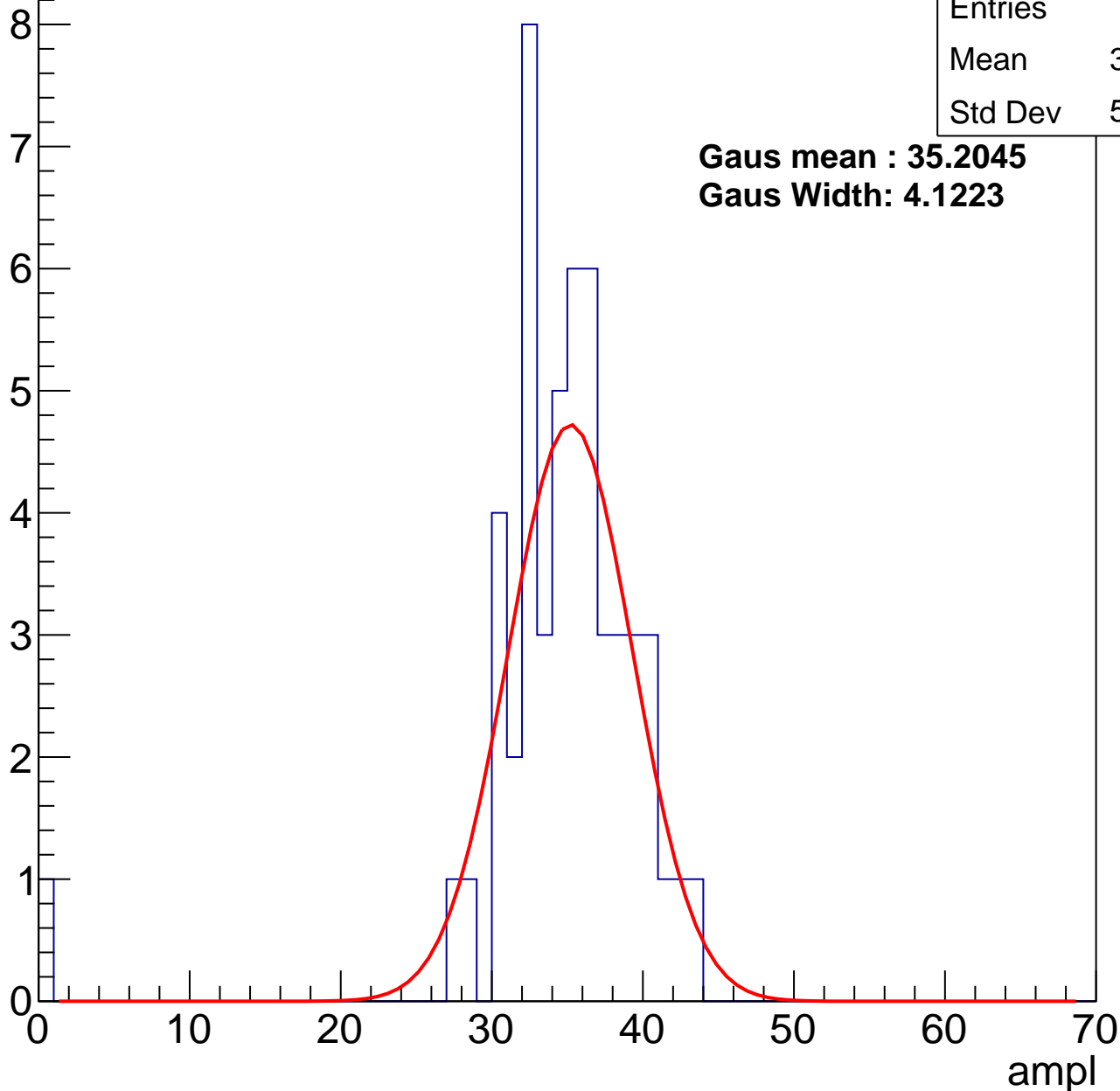
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	34.15
Std Dev	5.947

Gaus mean : 35.2045

Gaus Width: 4.1223



B1L103S, U2-ch23, adc2

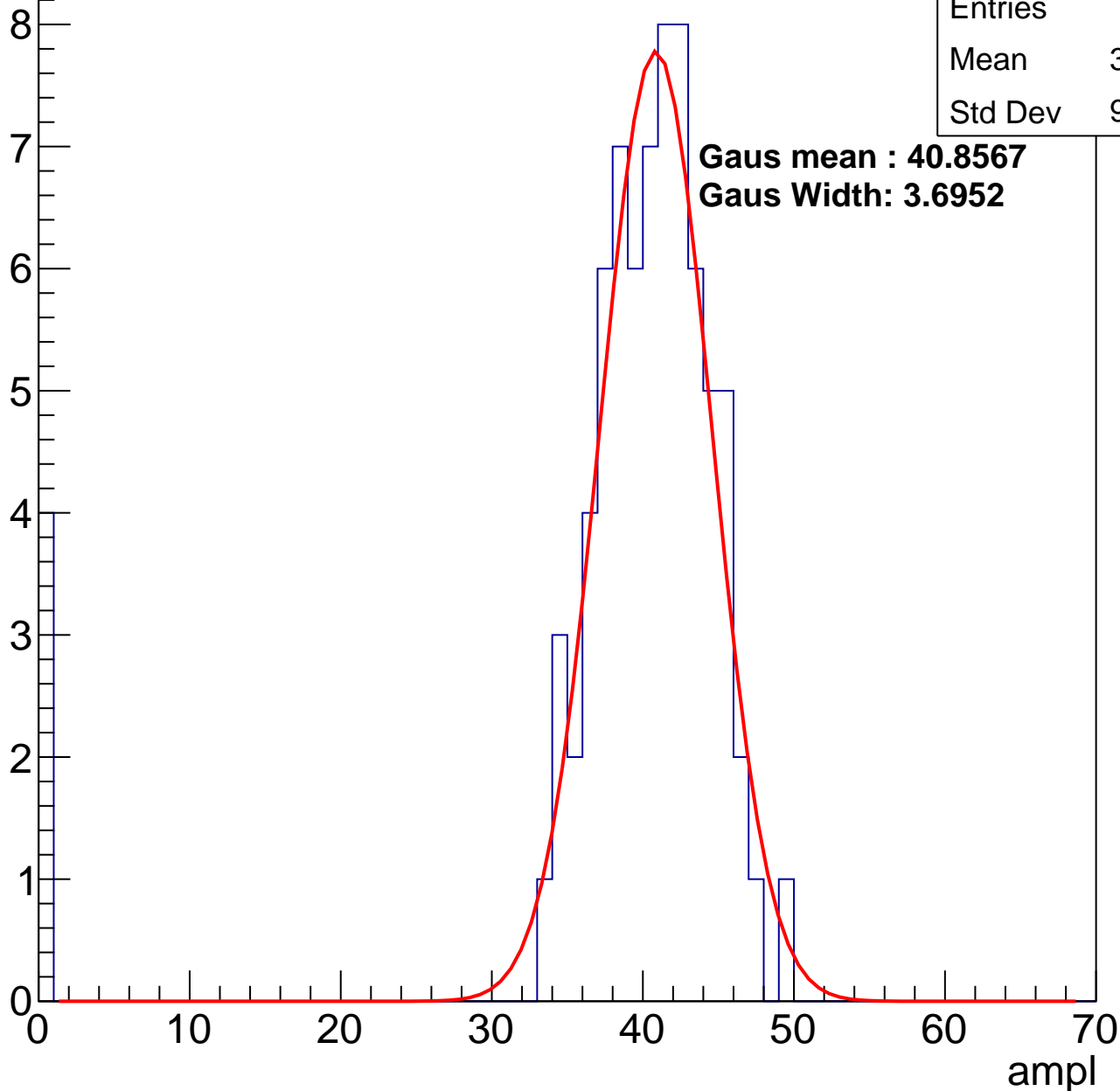
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	38.24
Std Dev	9.613

Gaus mean : 40.8567

Gaus Width: 3.6952



B1L103S, U2-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	42.18
Std Dev	15.61

Entry

10

8

6

4

2

0

0

10

20

30

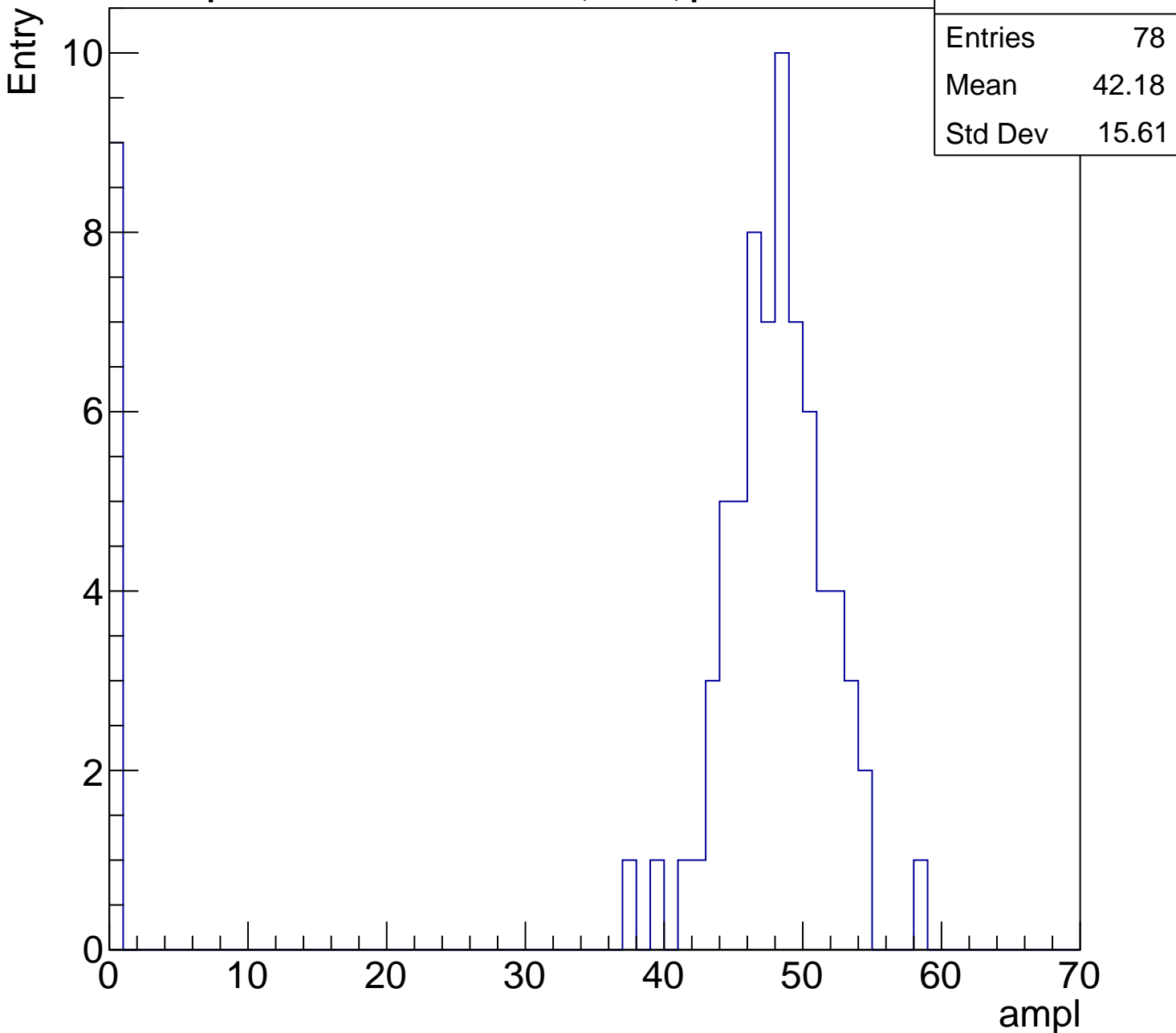
40

50

60

70

ampl

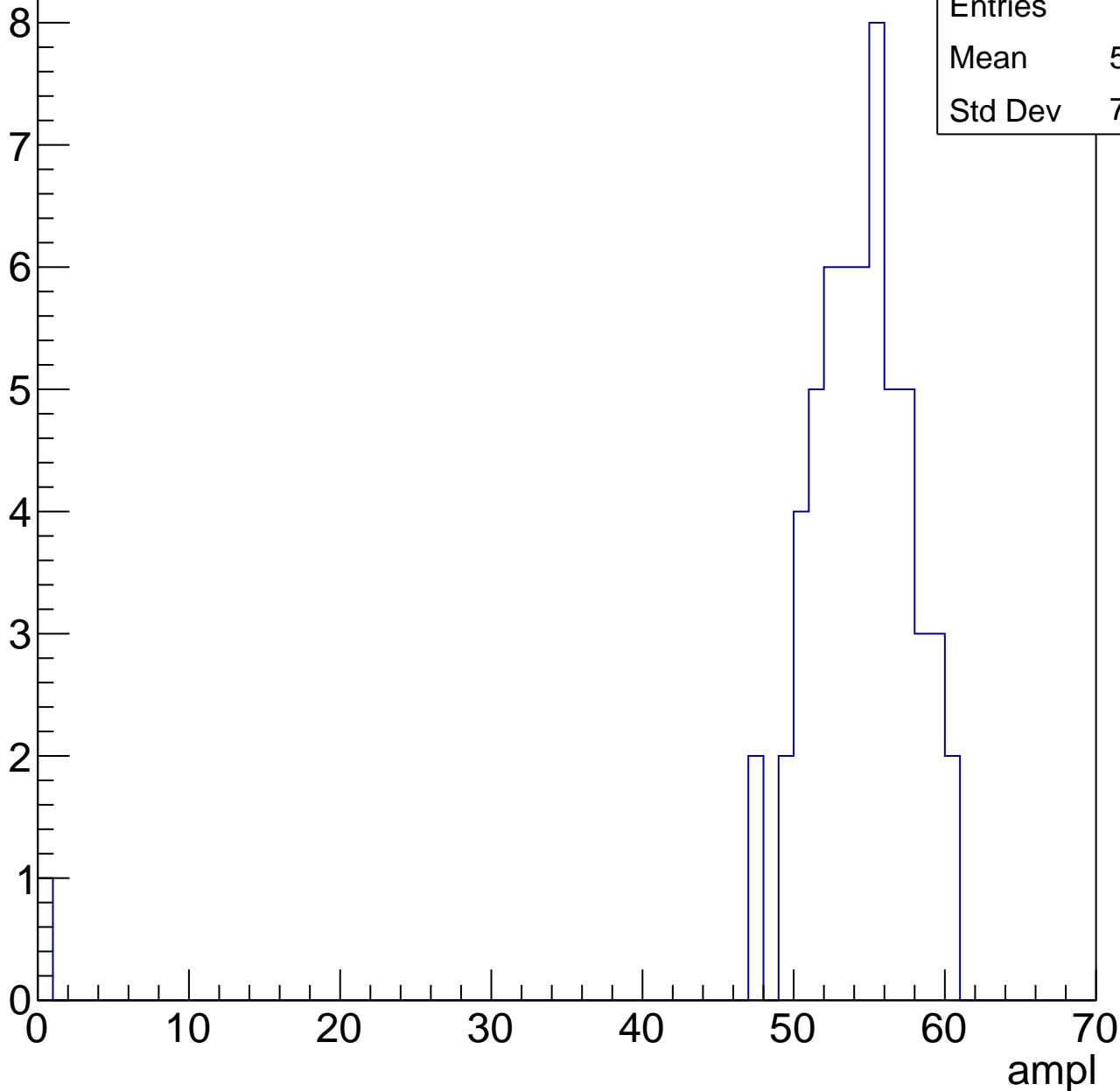


B1L103S, U2-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.05
Std Dev	7.678

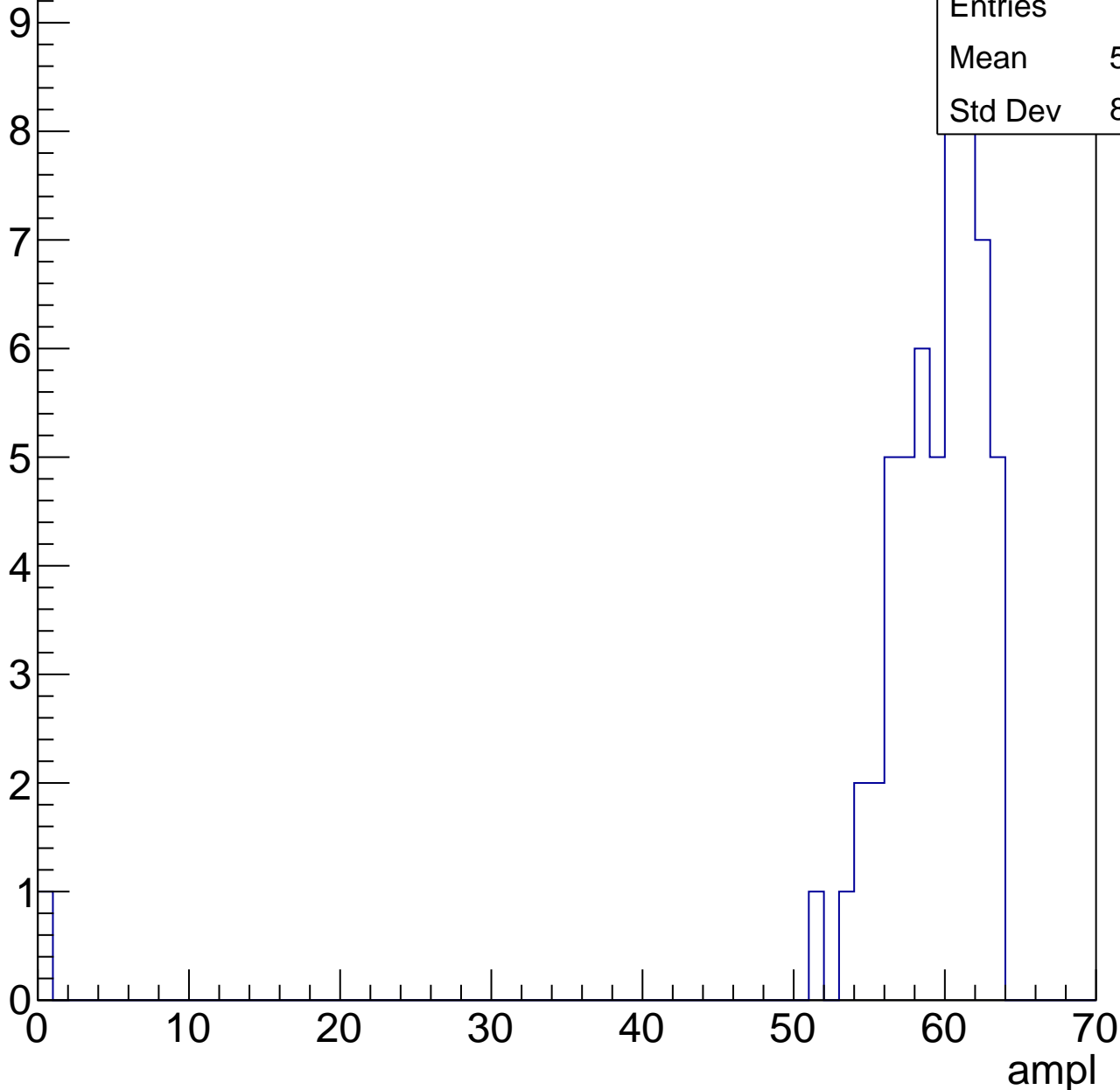


B1L103S, U2-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

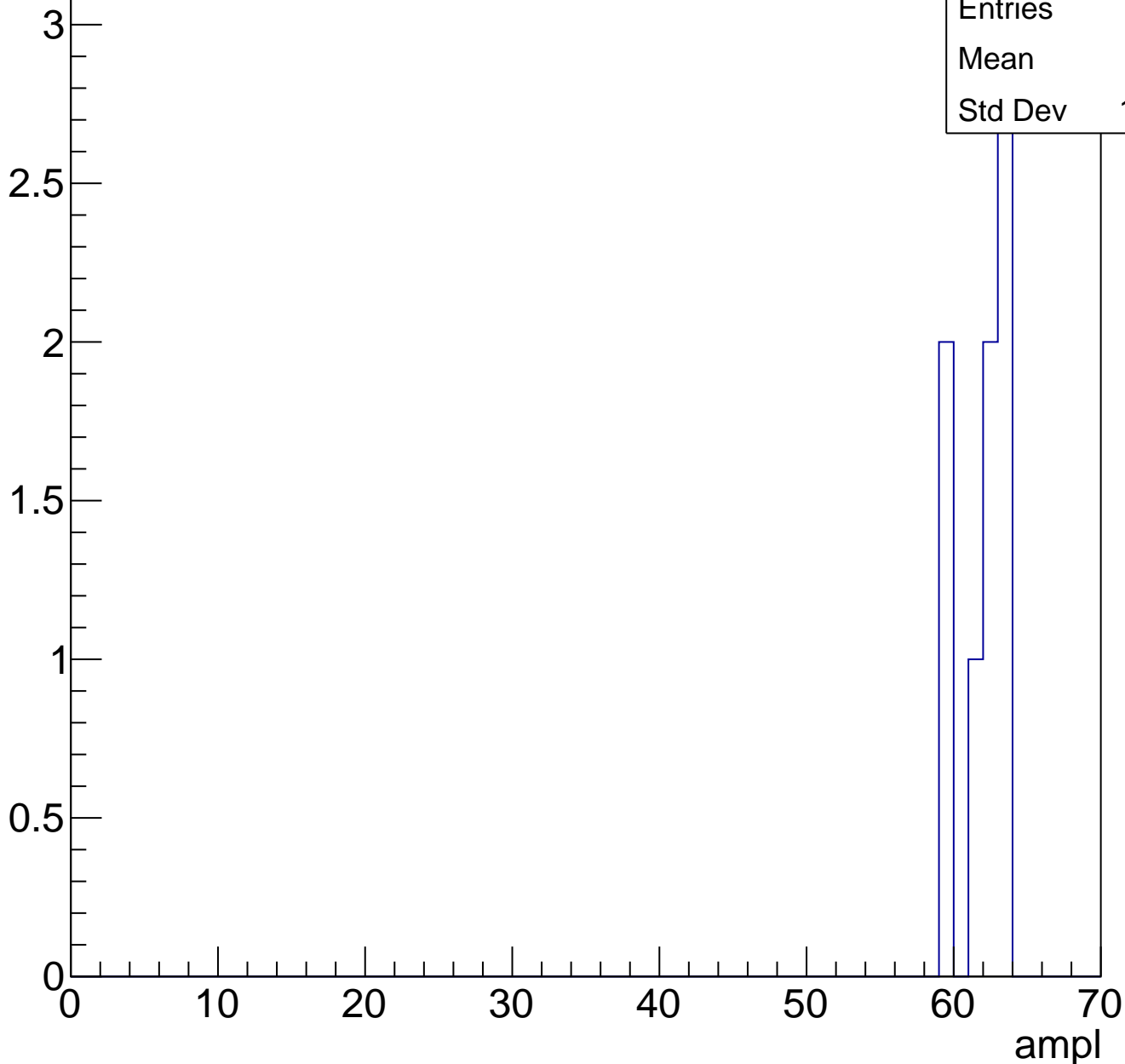
Entries	57
Mean	58.04
Std Dev	8.238



B1L103S, U2-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

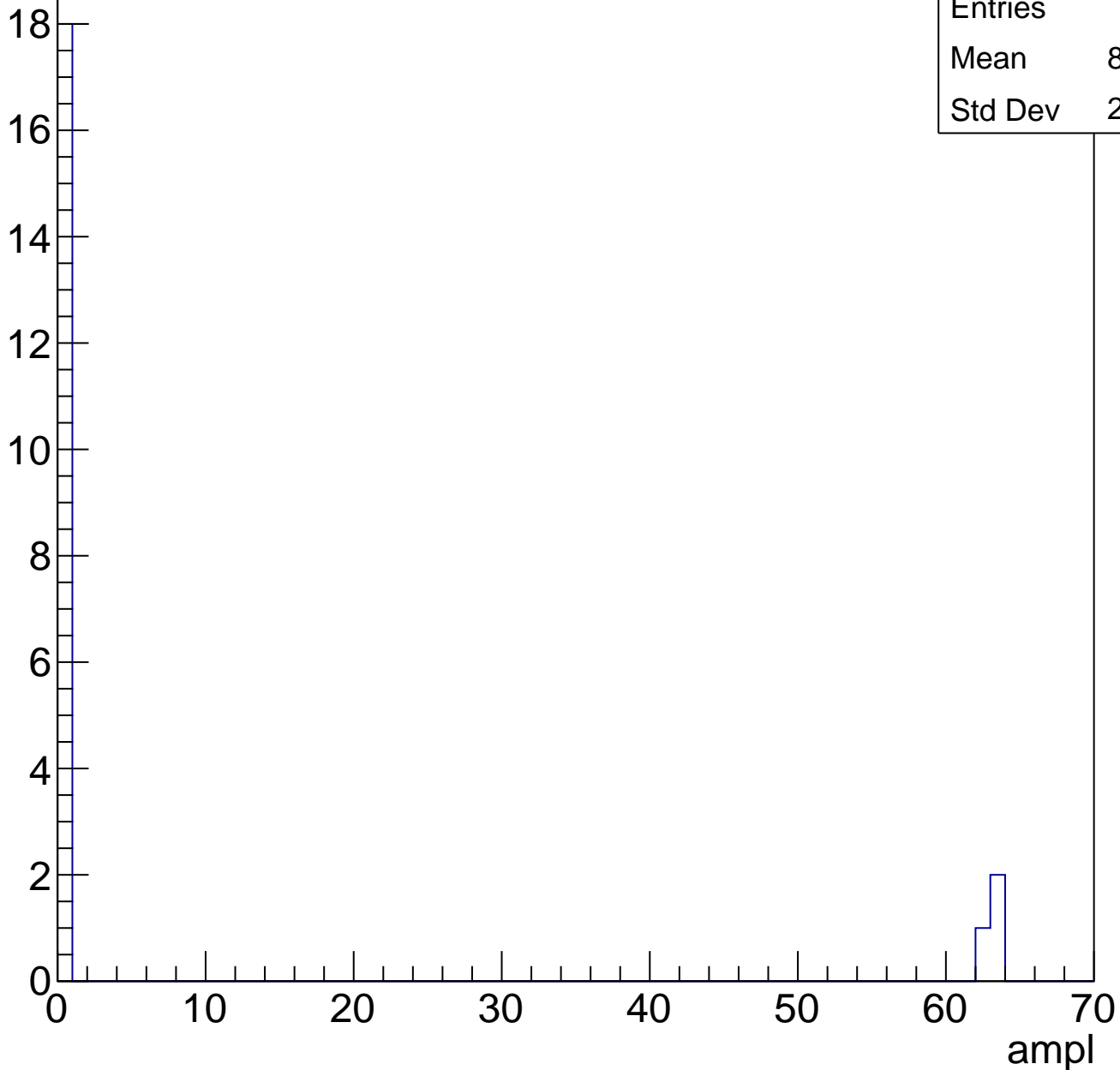


B1L103S, U2-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	8.952
Std Dev	21.93

Entry



B1L103S, U2-ch24, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	23.45
Std Dev	10.52

Gaus mean : 28.6294

Gaus Width: 3.3477

Entry

12

10

8

6

4

2

0

0

10

20

30

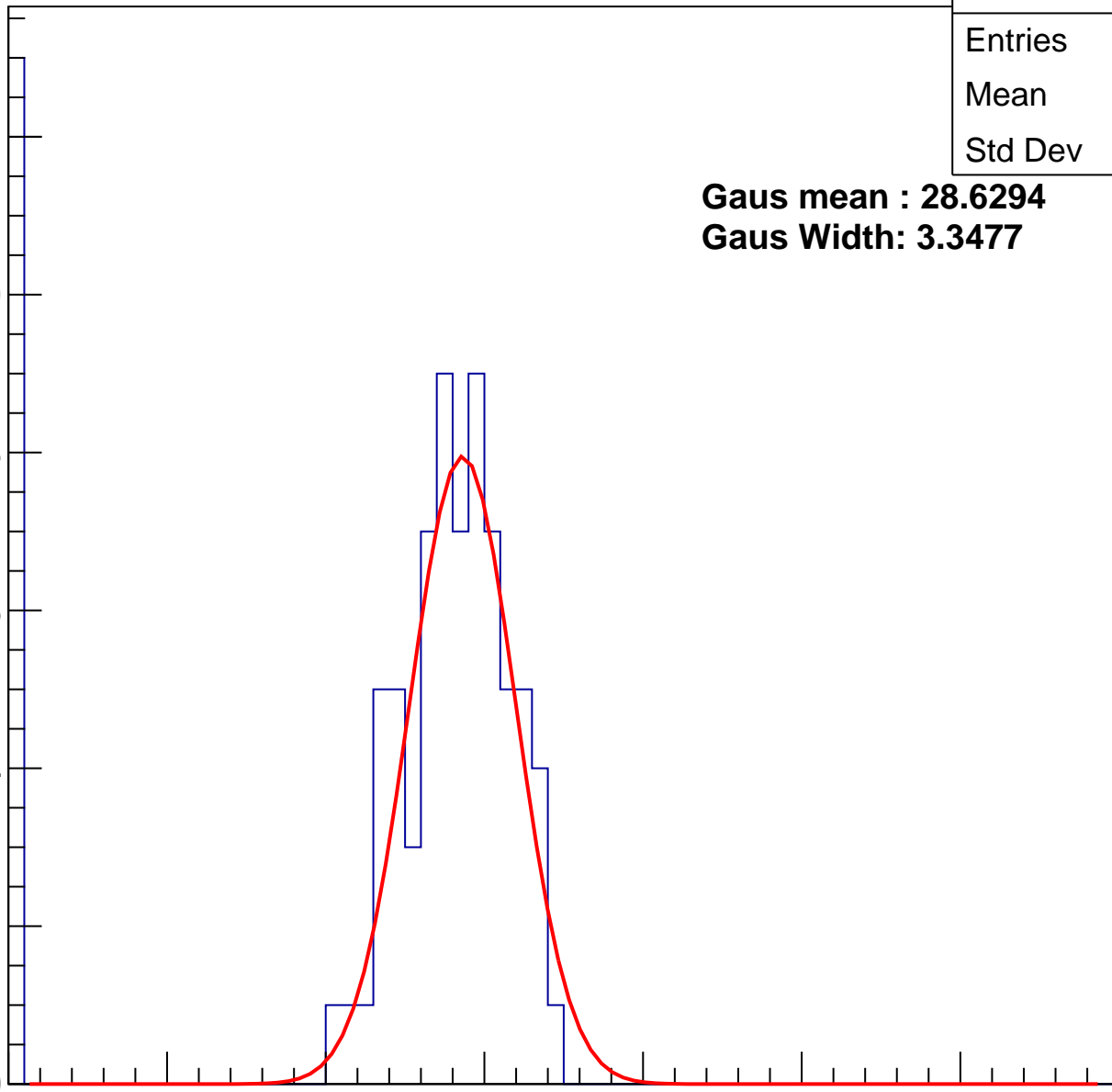
40

50

60

70

ampl



B1L103S, U2-ch24, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	34.17
Std Dev	7.388

Gaus mean : 36.0043

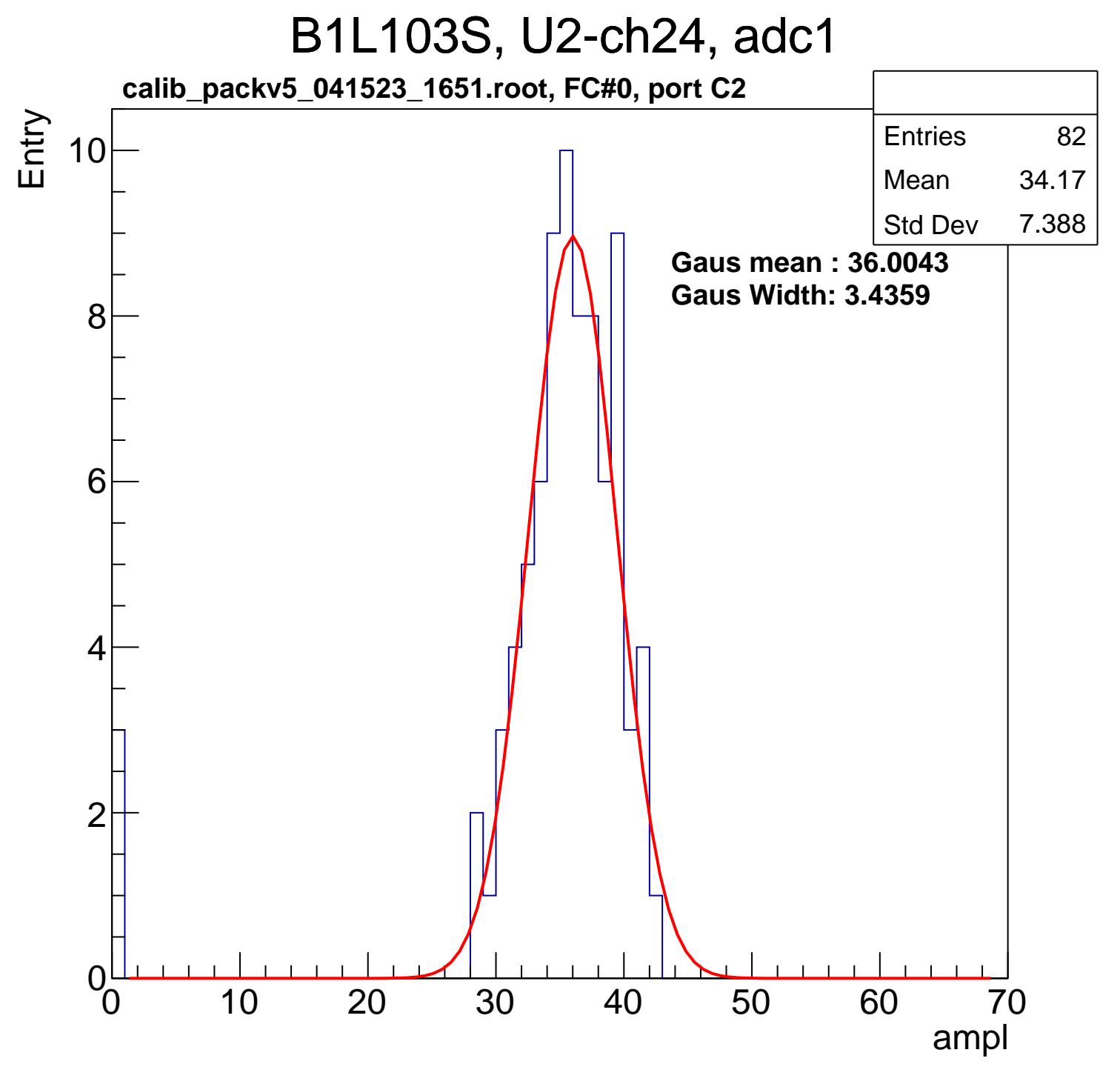
Gaus Width: 3.4359

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch24, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	55
Mean	33.44
Std Dev	16.93

Gaus mean : 41.7939

Gaus Width: 3.4141

Entry

10

8

6

4

2

0

0

10

20

30

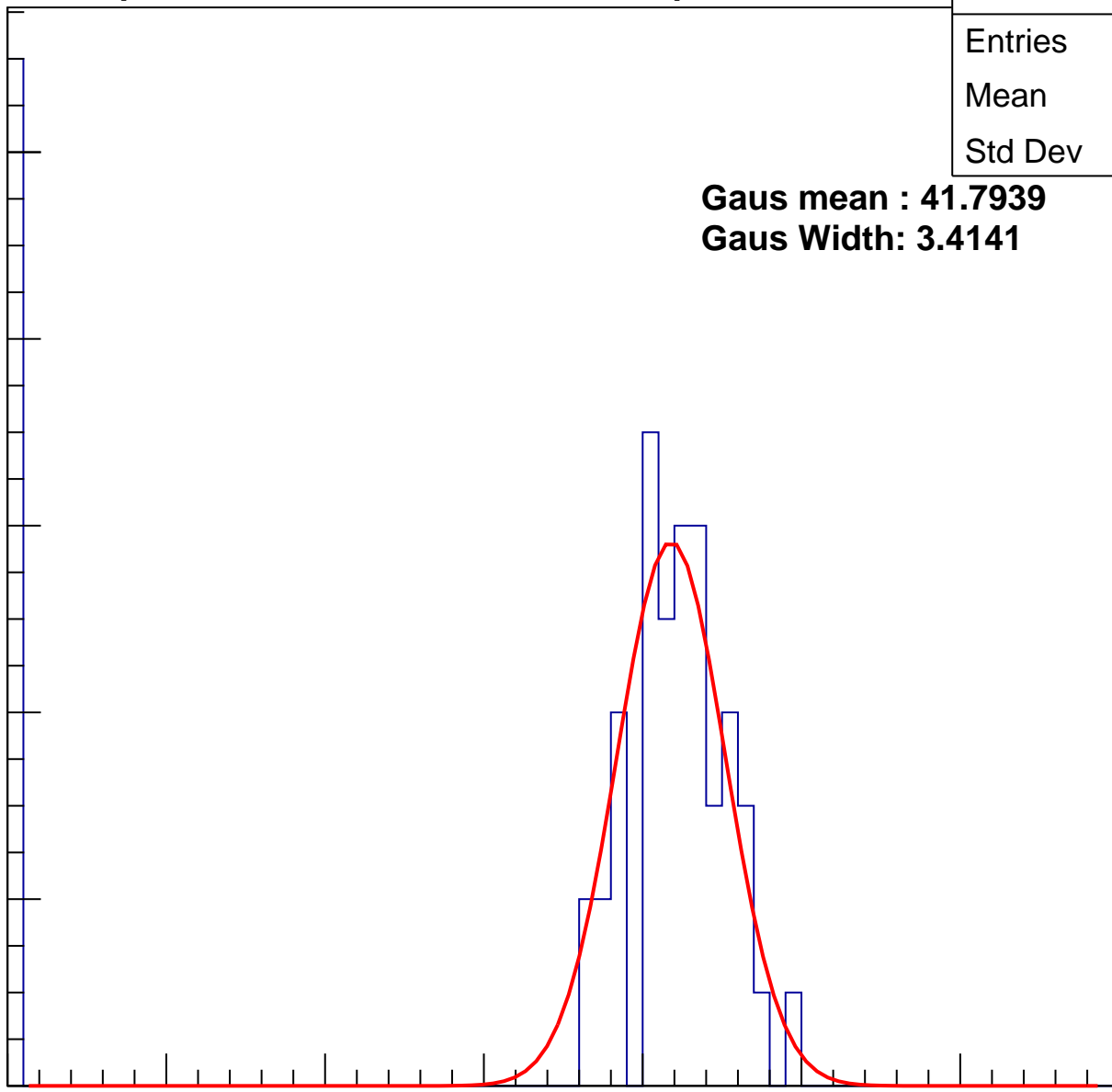
40

50

60

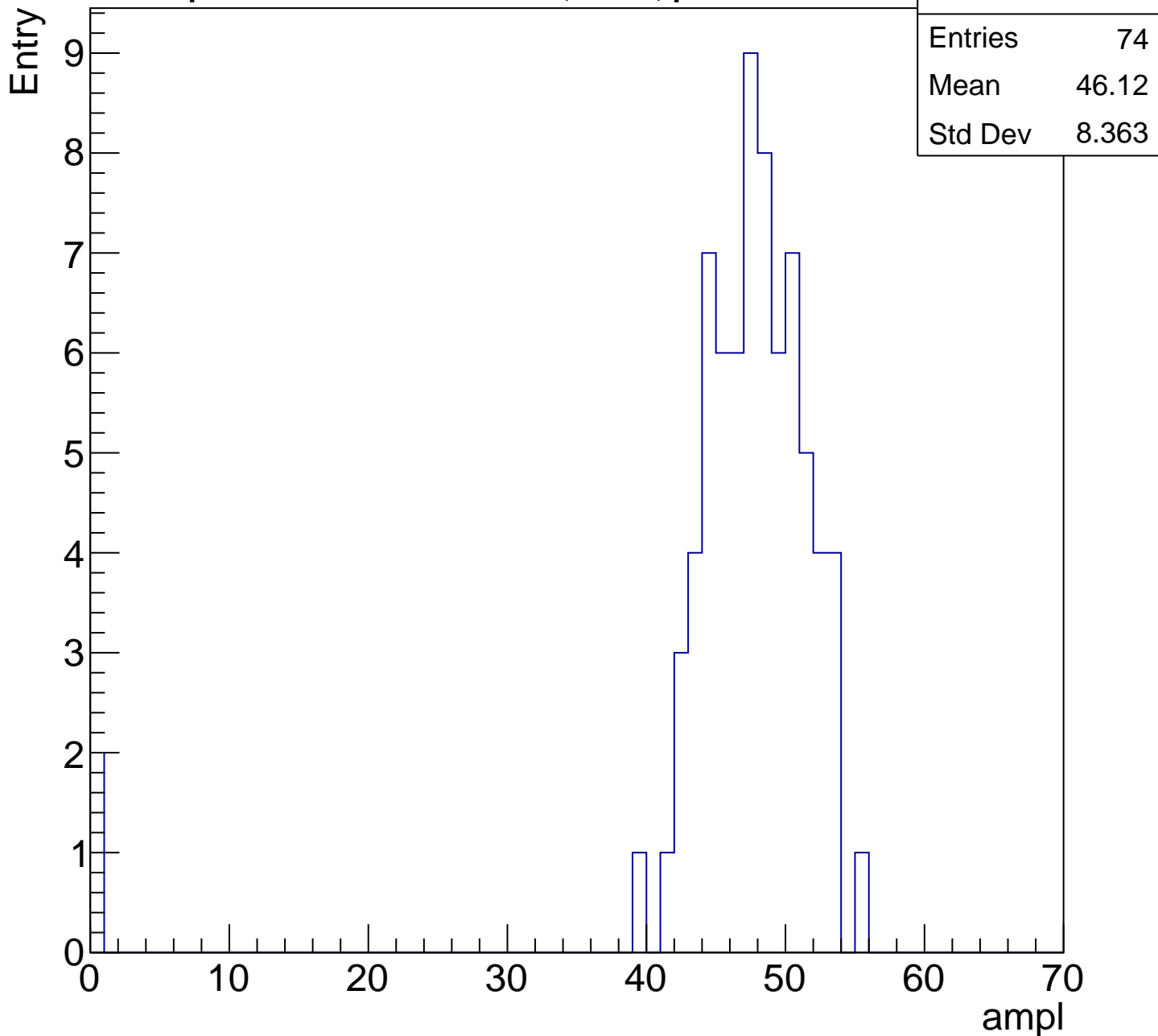
70

ampl



B1L103S, U2-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

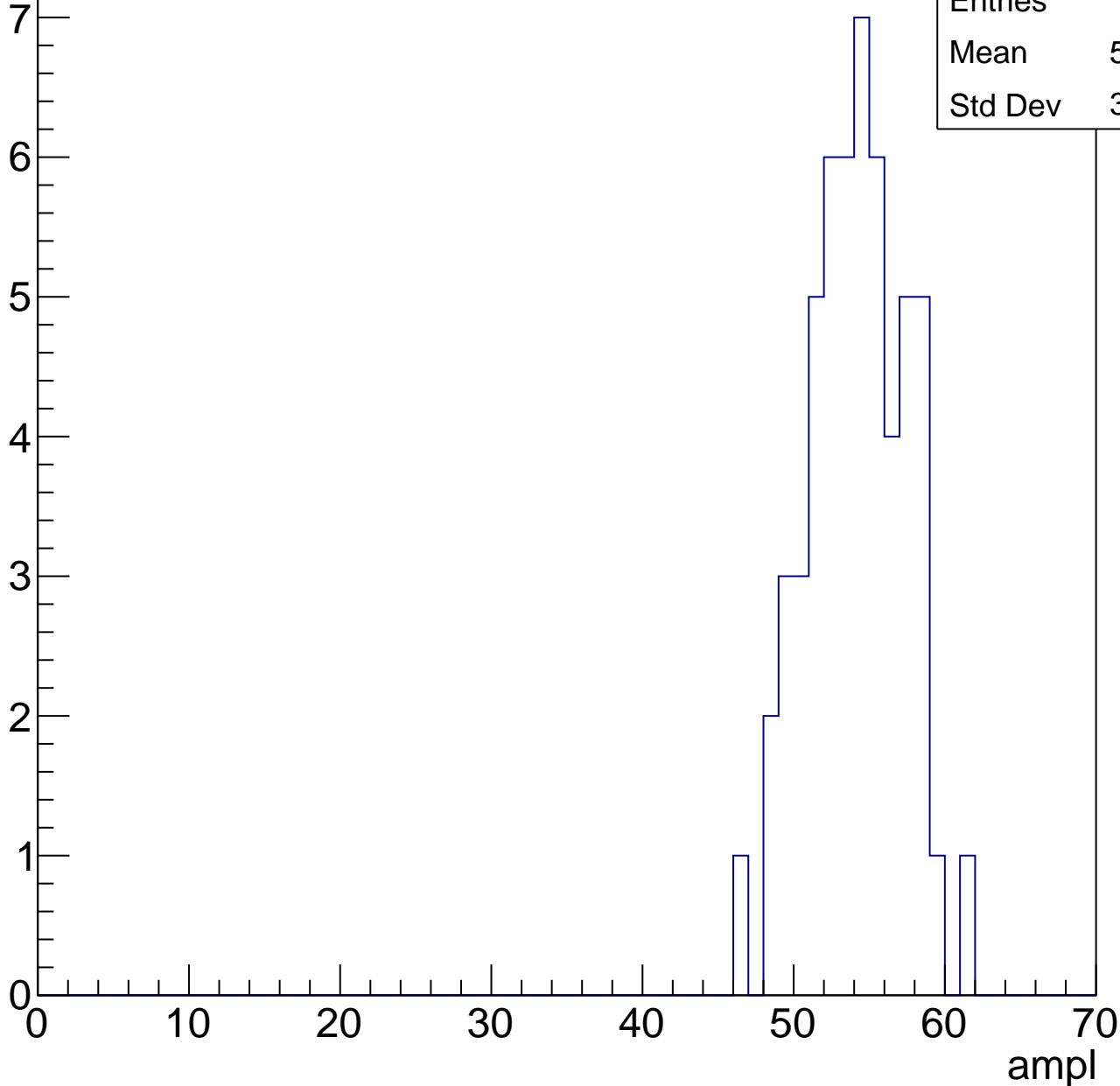


B1L103S, U2-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.65
Std Dev	3.158



B1L103S, U2-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 52

Mean 57.58

Std Dev 8.422

8

6

4

2

0

0

10

20

30

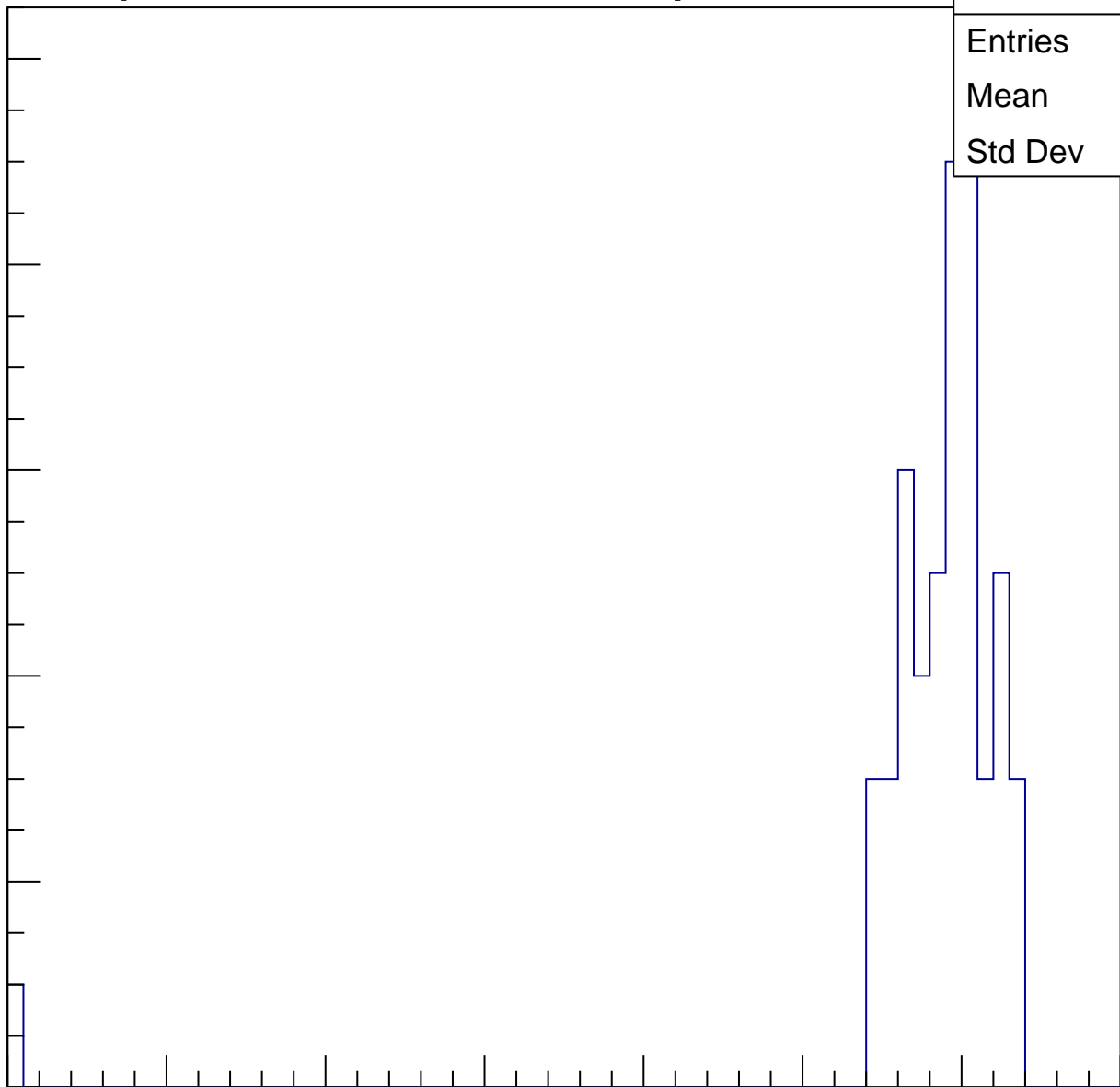
40

50

60

70

ampl

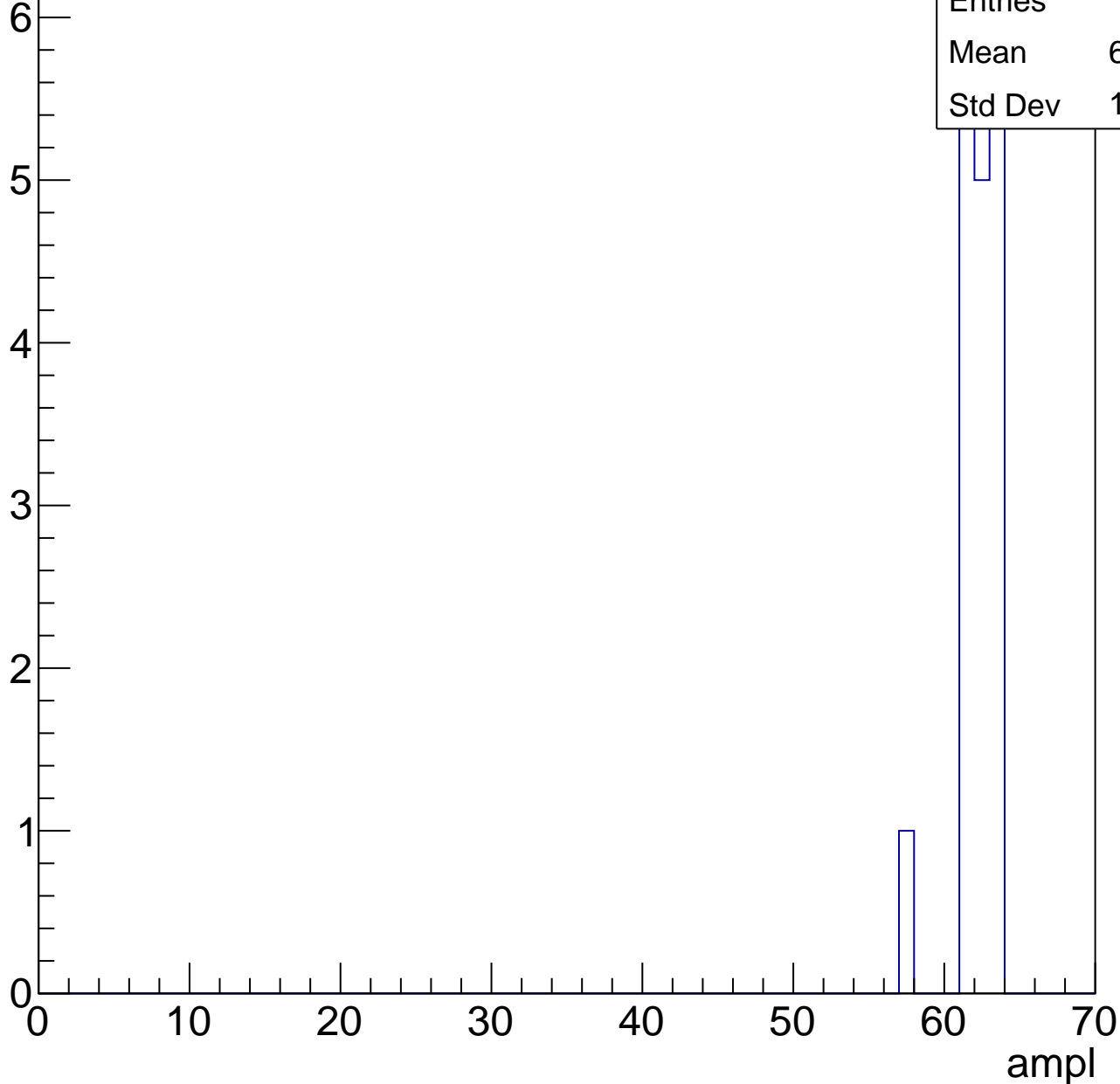


B1L103S, U2-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.72
Std Dev	1.407



B1L103S, U2-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



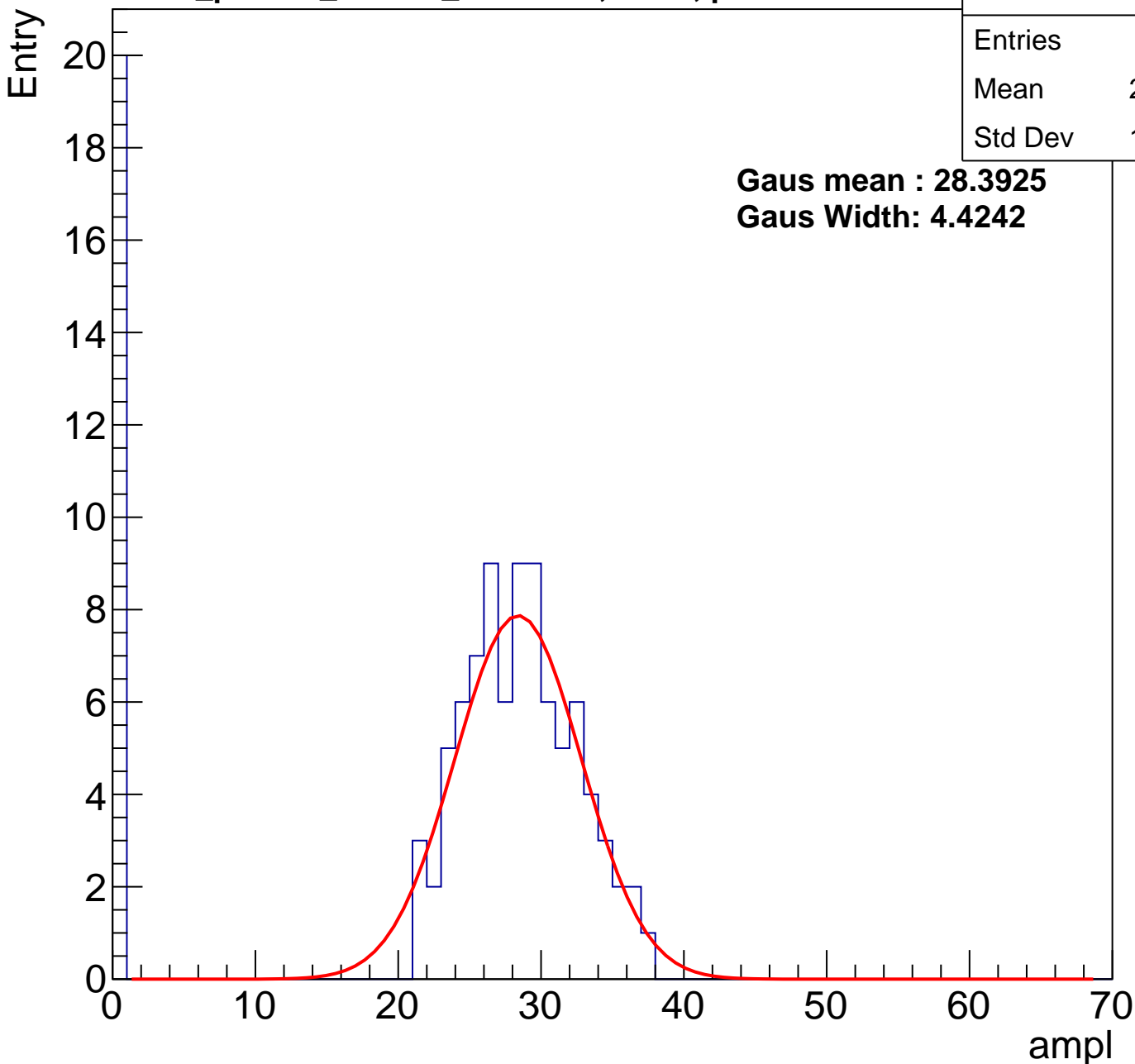
B1L103S, U2-ch25, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	22.76
Std Dev	11.56

Gaus mean : 28.3925

Gaus Width: 4.4242



B1L103S, U2-ch25, adc1

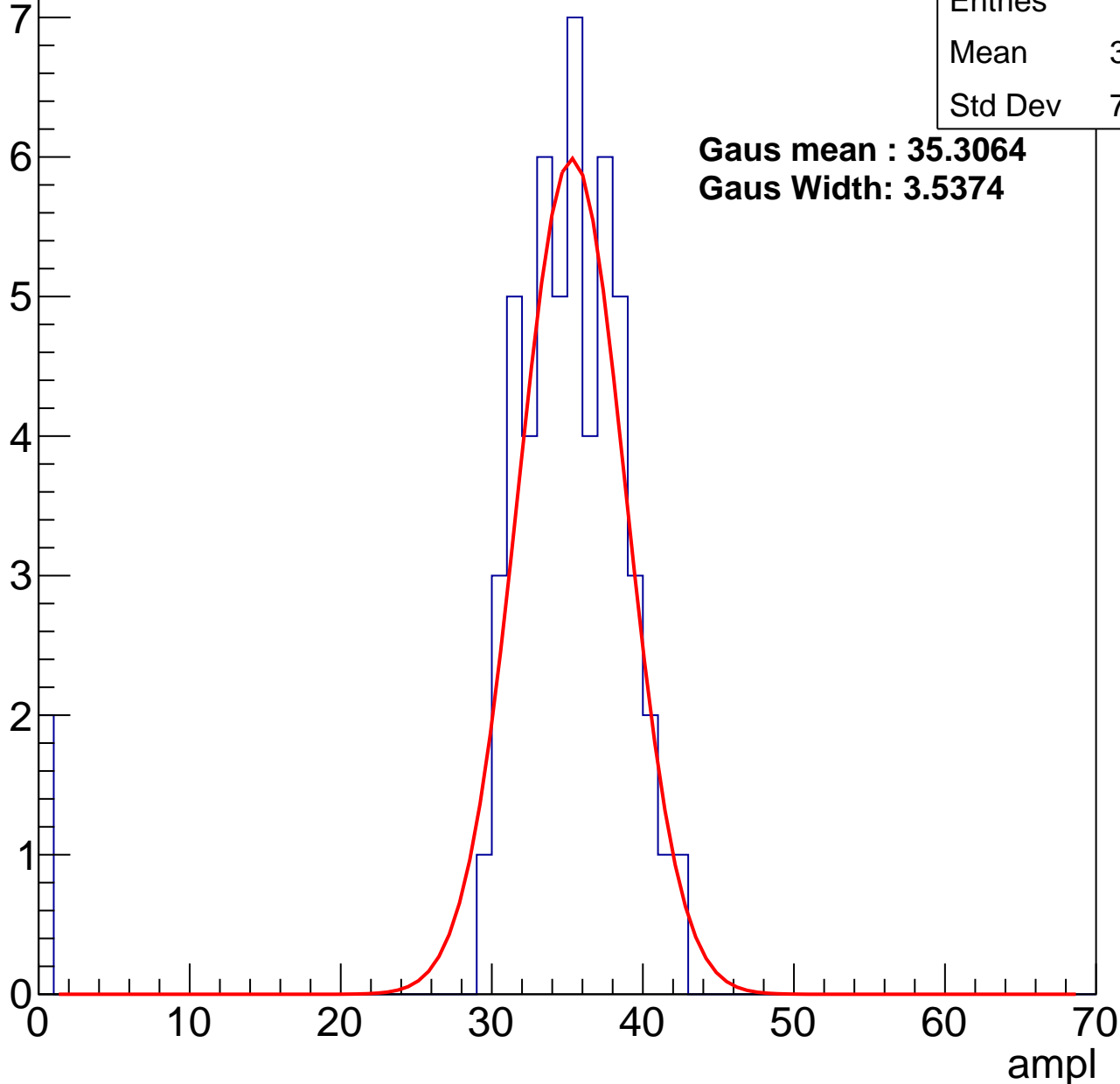
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	33.65
Std Dev	7.214

Gaus mean : 35.3064

Gaus Width: 3.5374



B1L103S, U2-ch25, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	79
Mean	34.18
Std Dev	15.44

Gaus mean : 41.4588

Gaus Width: 3.2858

0

2

4

6

8

10

12

Entry

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	34.18
Std Dev	15.44

Gaus mean : 41.4588

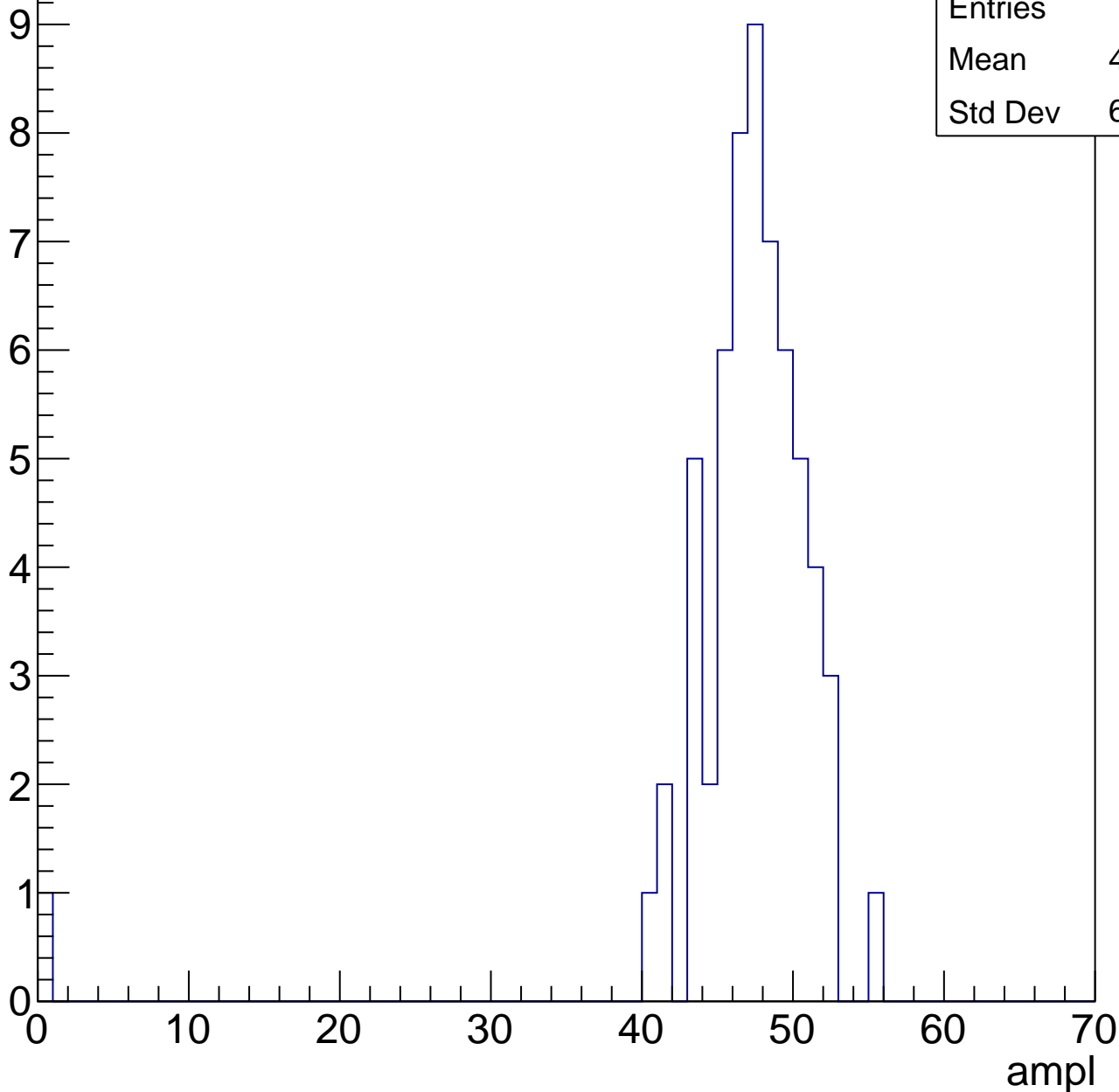
Gaus Width: 3.2858

B1L103S, U2-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	46.35
Std Dev	6.725

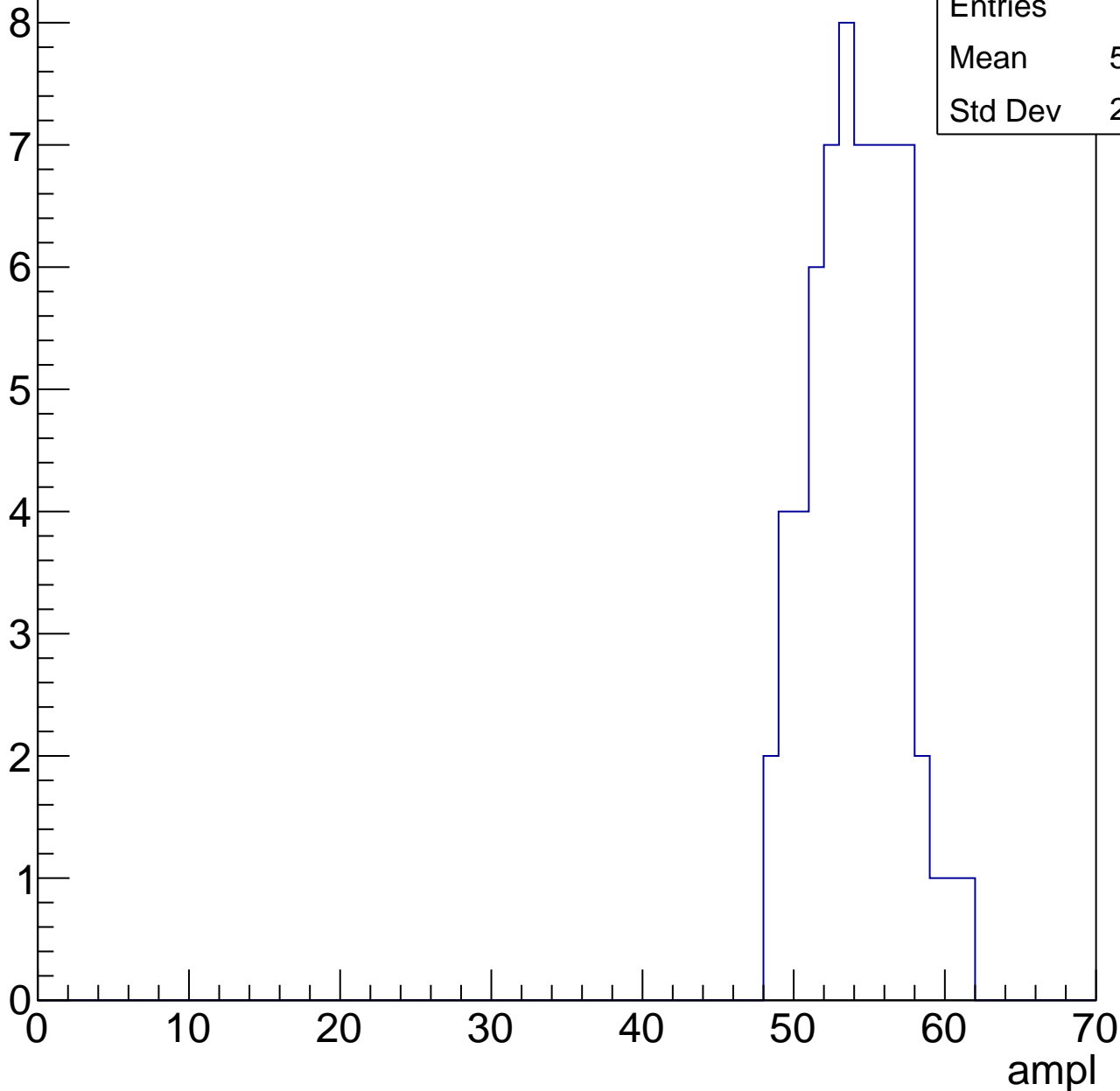


B1L103S, U2-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	53.69
Std Dev	2.957



B1L103S, U2-ch25, adc5

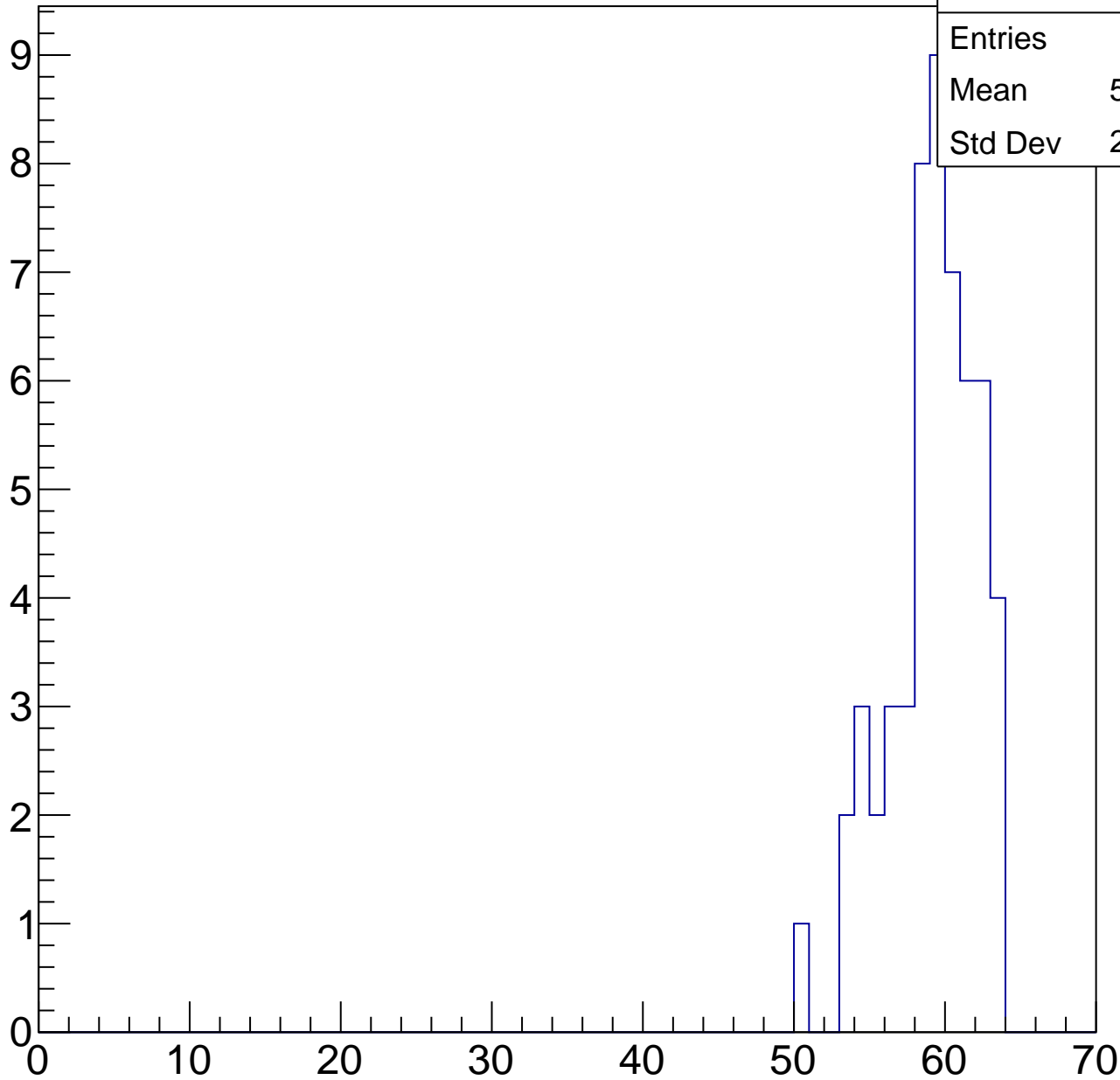
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.74
Std Dev	2.894

ampl

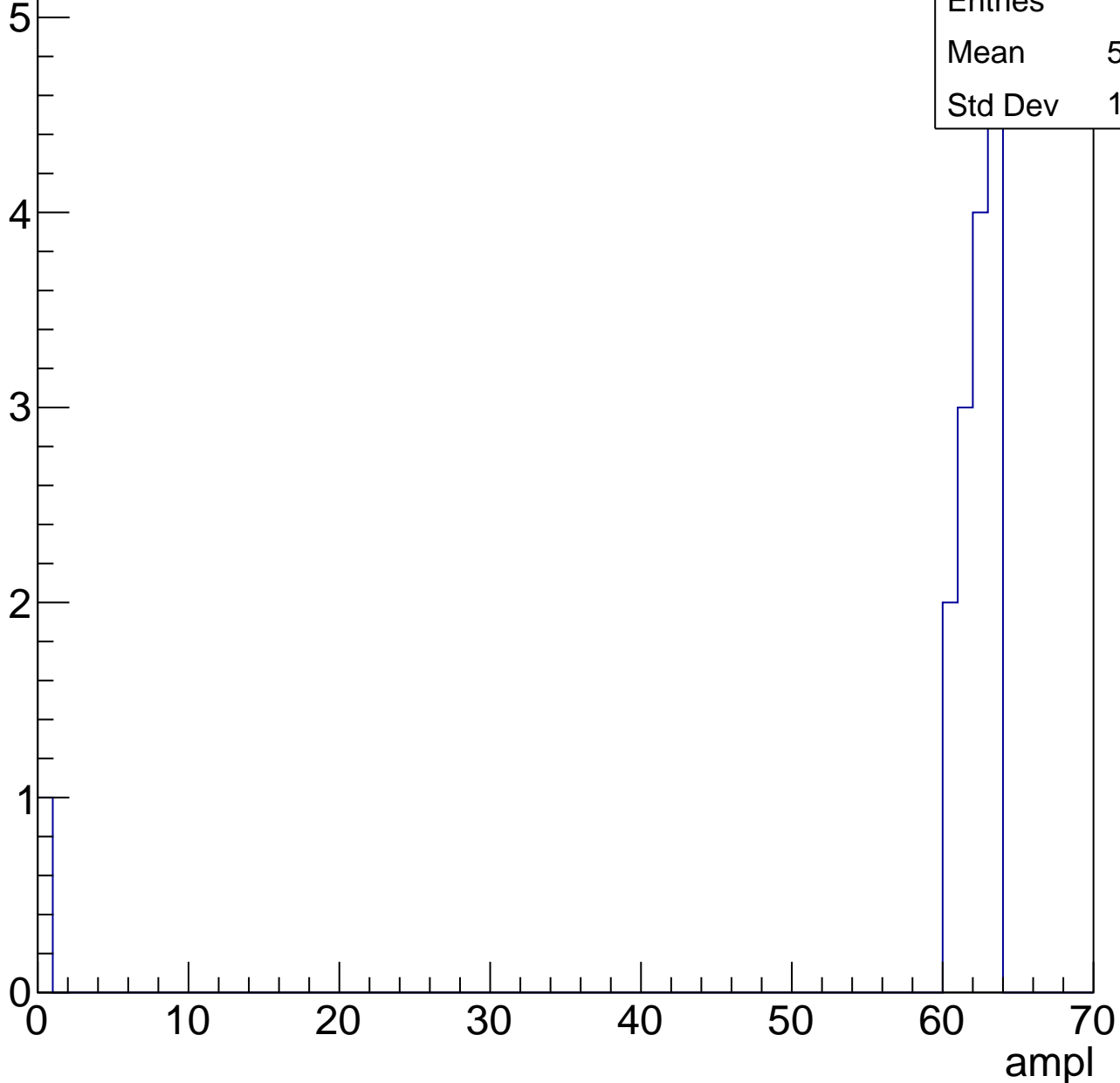


B1L103S, U2-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

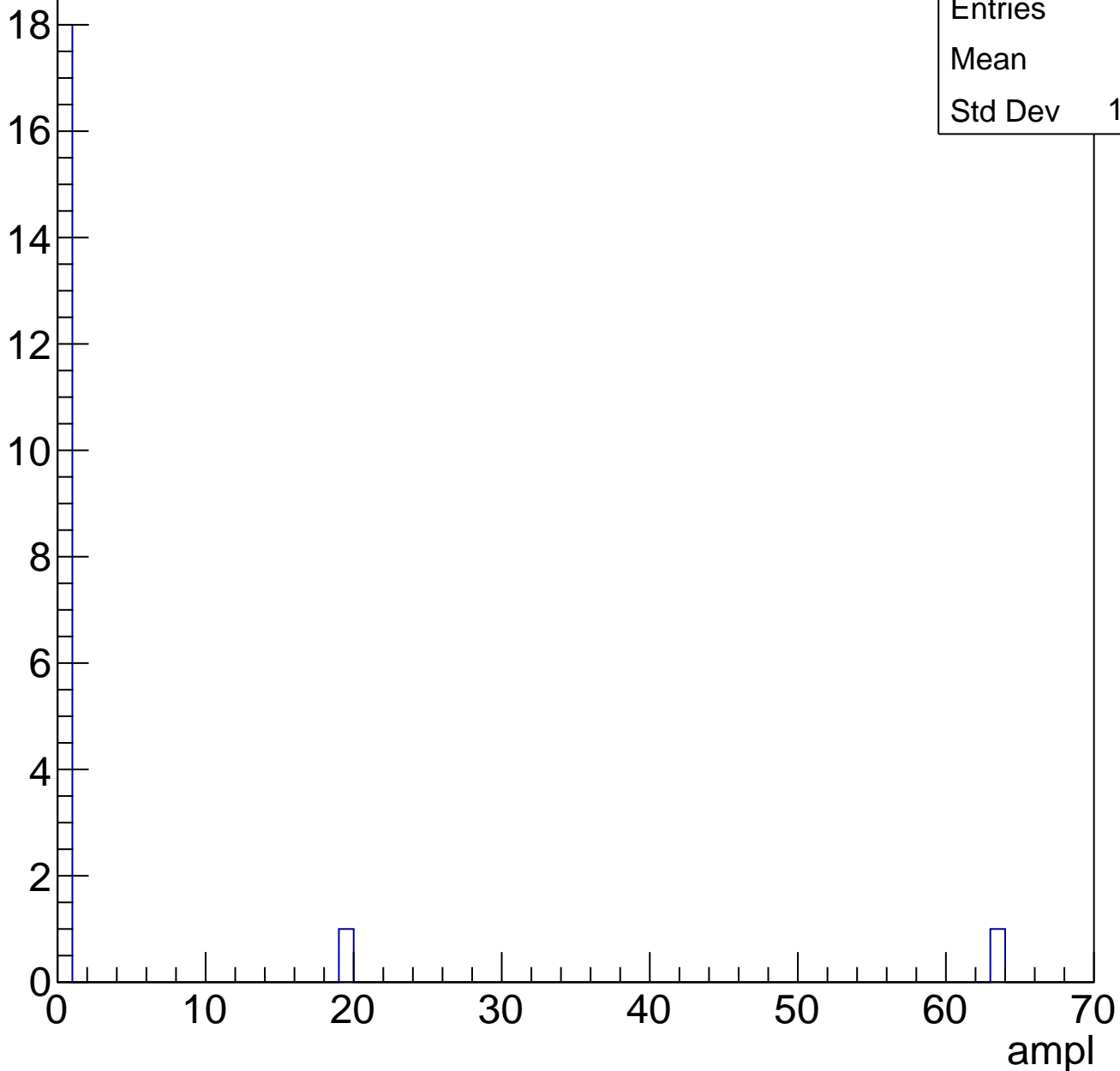
Entries	15
Mean	57.73
Std Dev	15.46



B1L103S, U2-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	4.1
Std Dev	14.13

B1L103S, U2-ch26, adc0

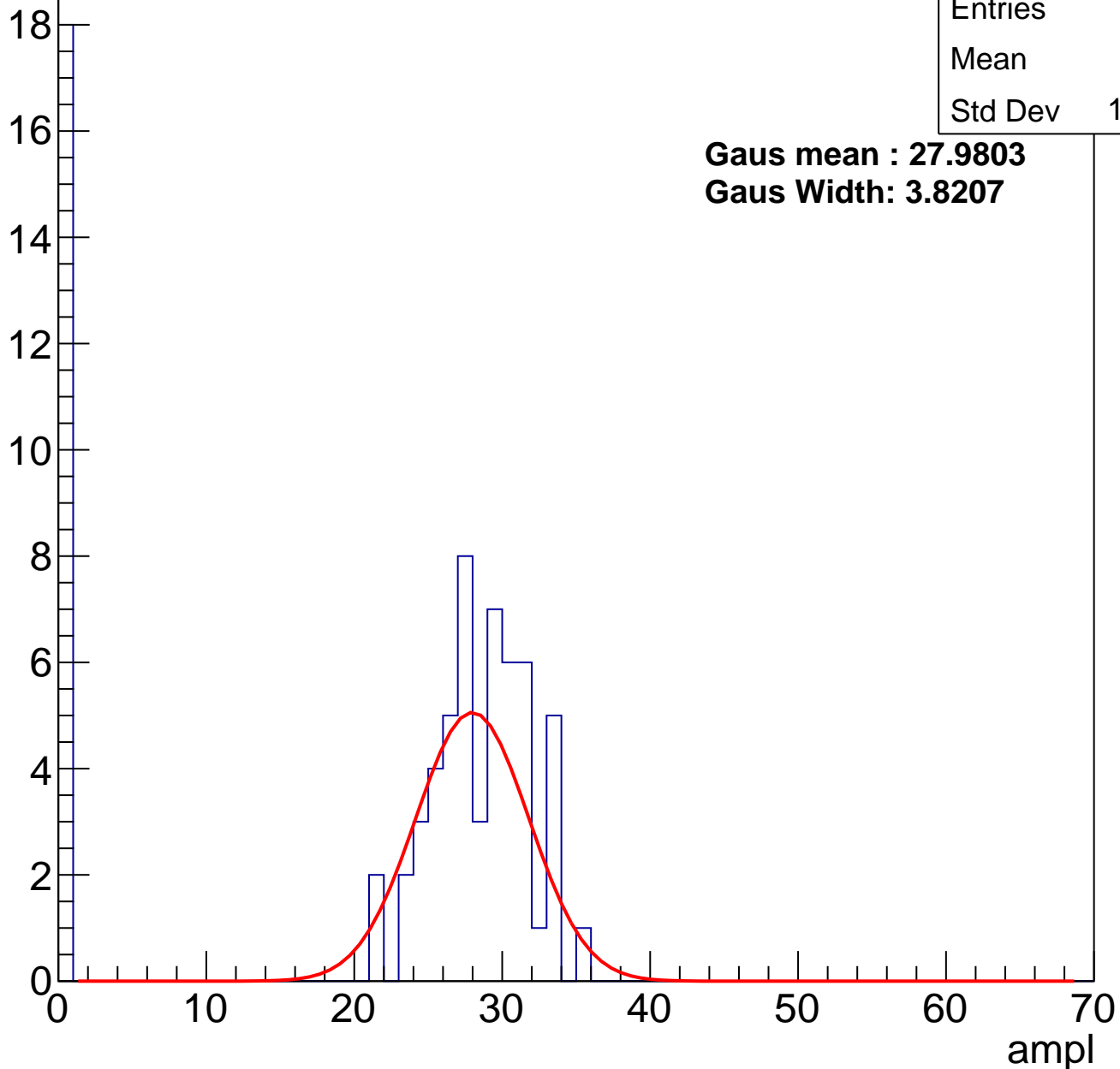
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	21
Std Dev	12.54

Gaus mean : 27.9803

Gaus Width: 3.8207

Entry



B1L103S, U2-ch26, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	30.79
Std Dev	12.04

Gaus mean : 35.8791

Gaus Width: 4.4558

Entry

12

10

8

6

4

2

0

0

10

20

30

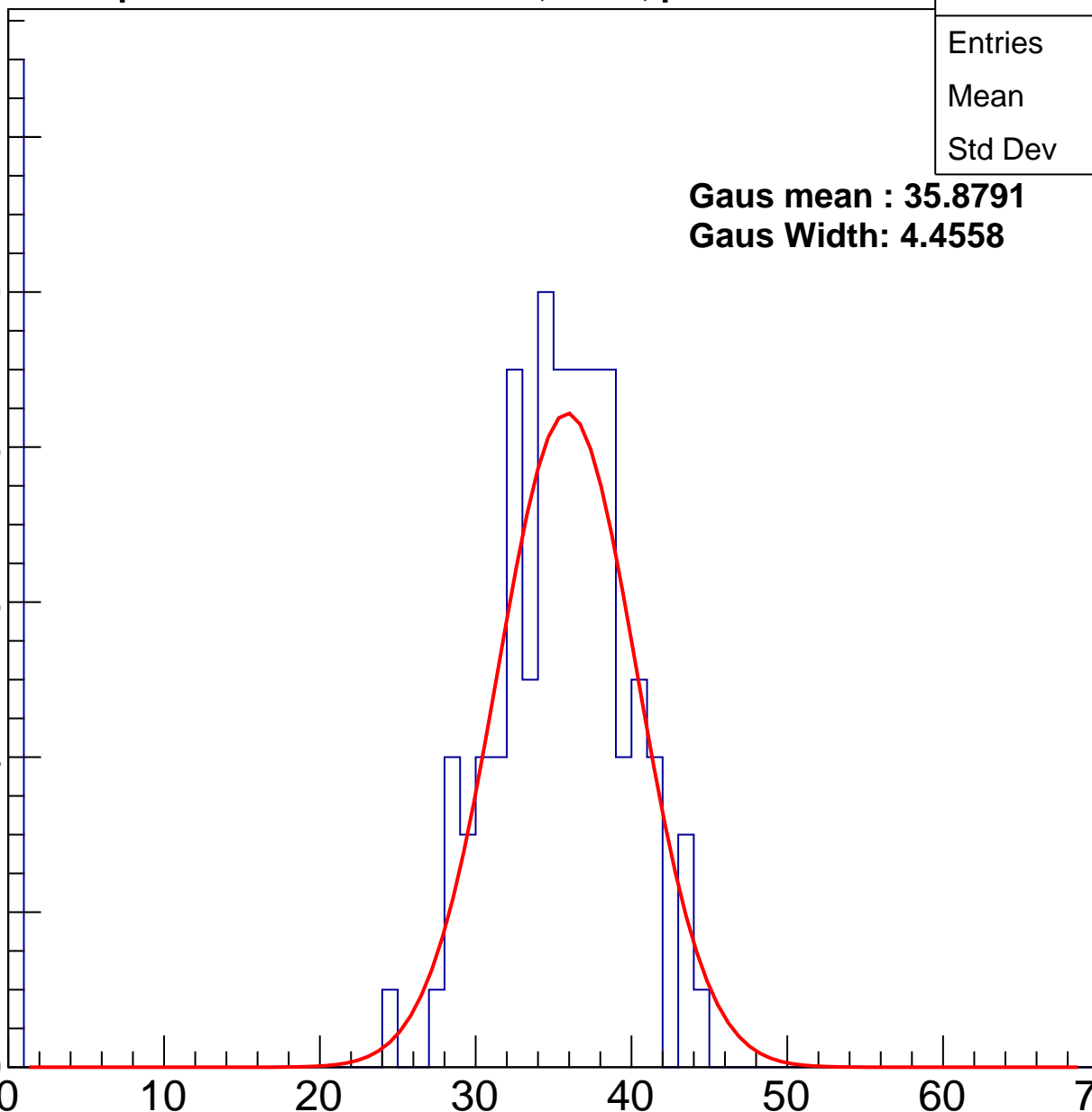
40

50

60

70

ampl



B1L103S, U2-ch26, adc2

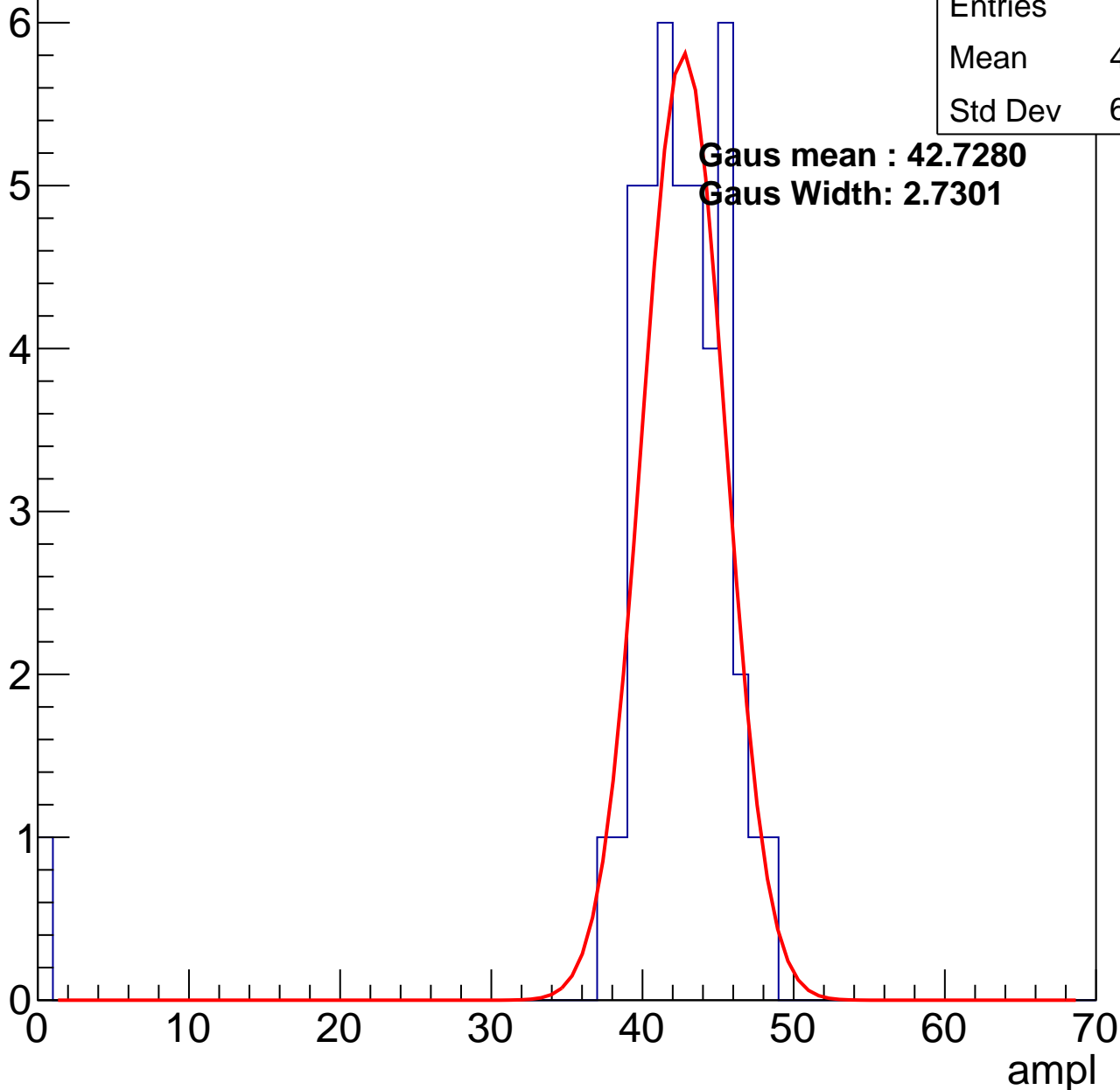
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	41.26
Std Dev	6.854

Gaus mean : 42.7280

Gaus Width: 2.7301

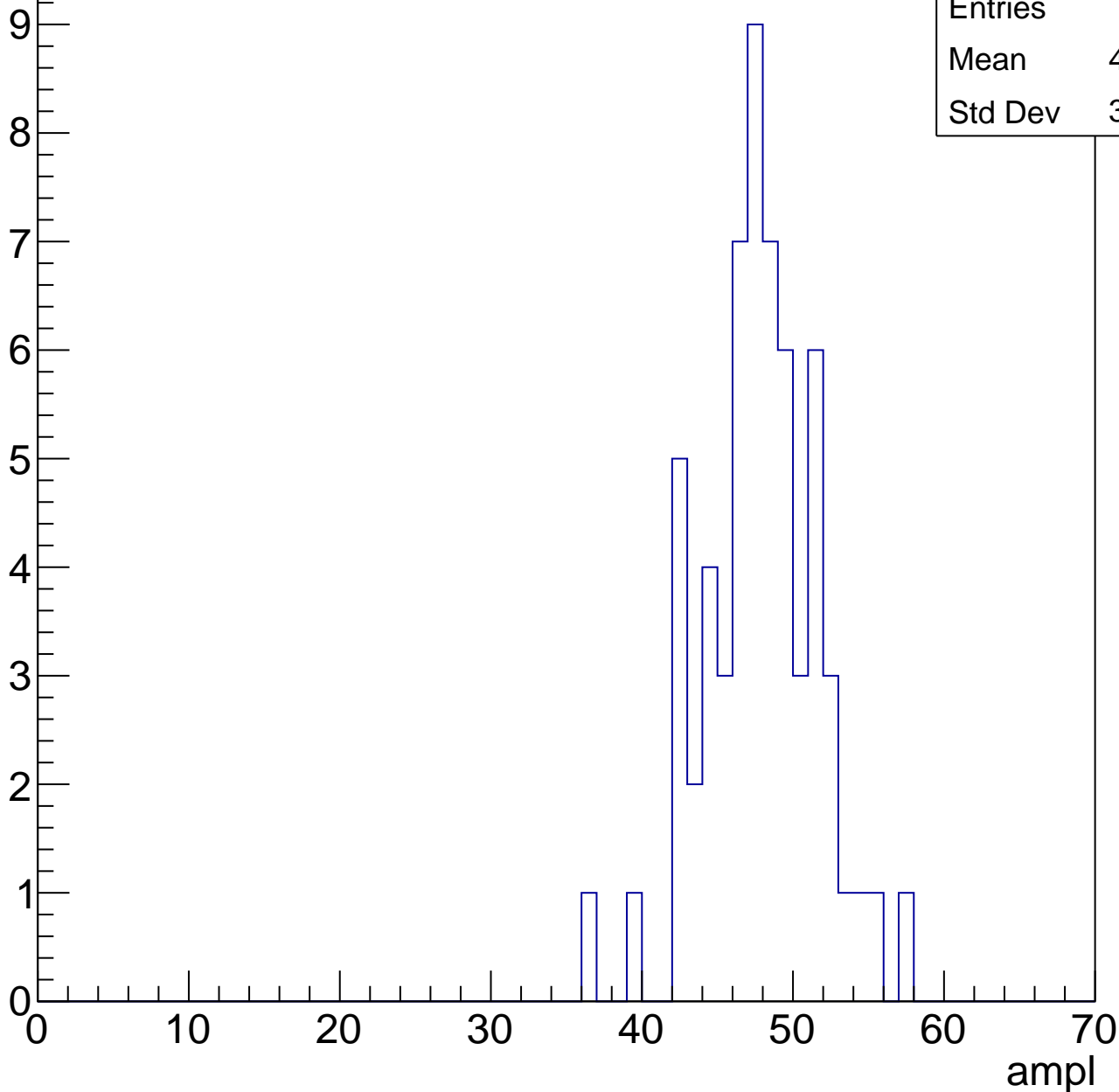


B1L103S, U2-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	47.34
Std Dev	3.772

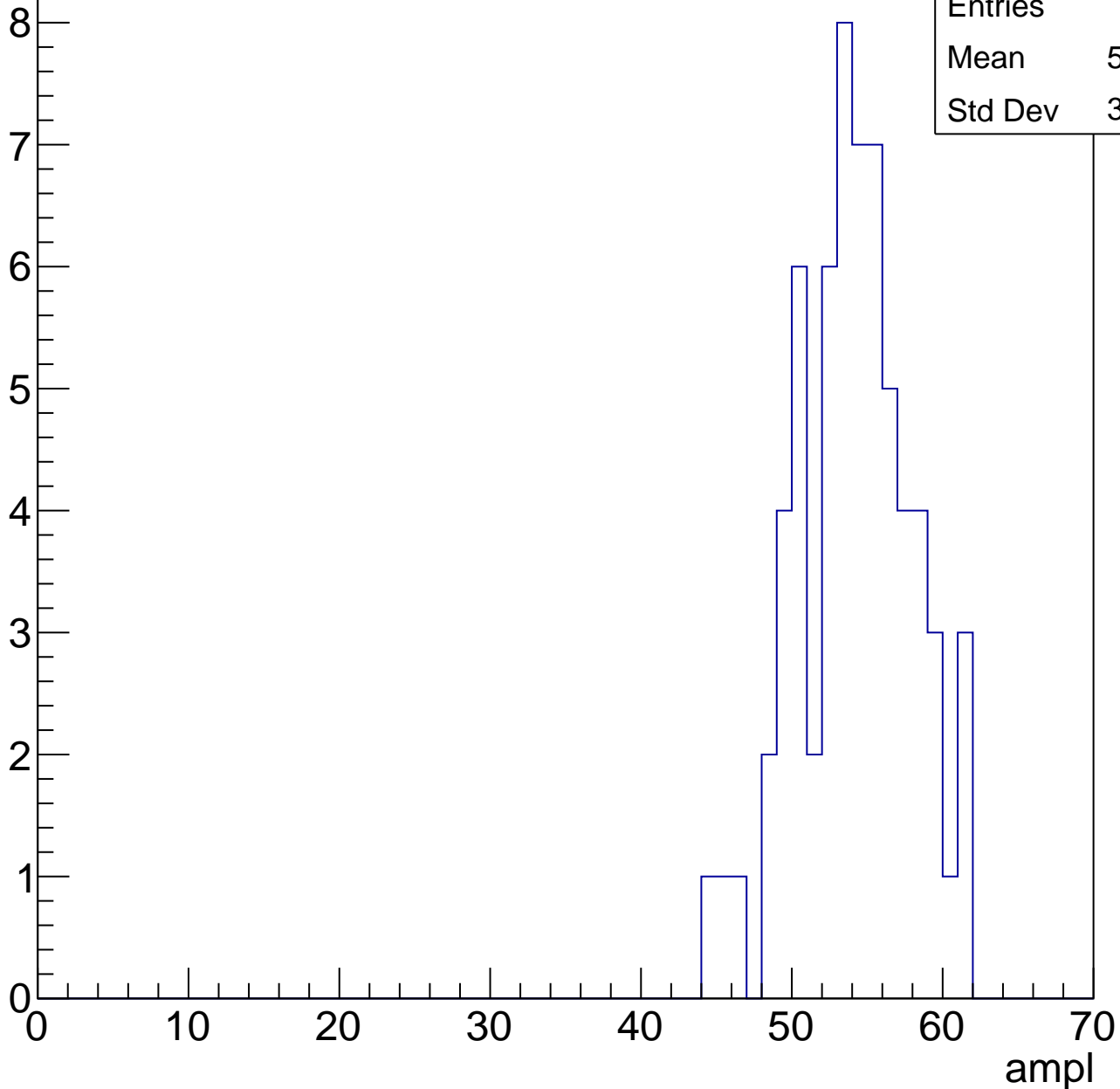


B1L103S, U2-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	53.66
Std Dev	3.804

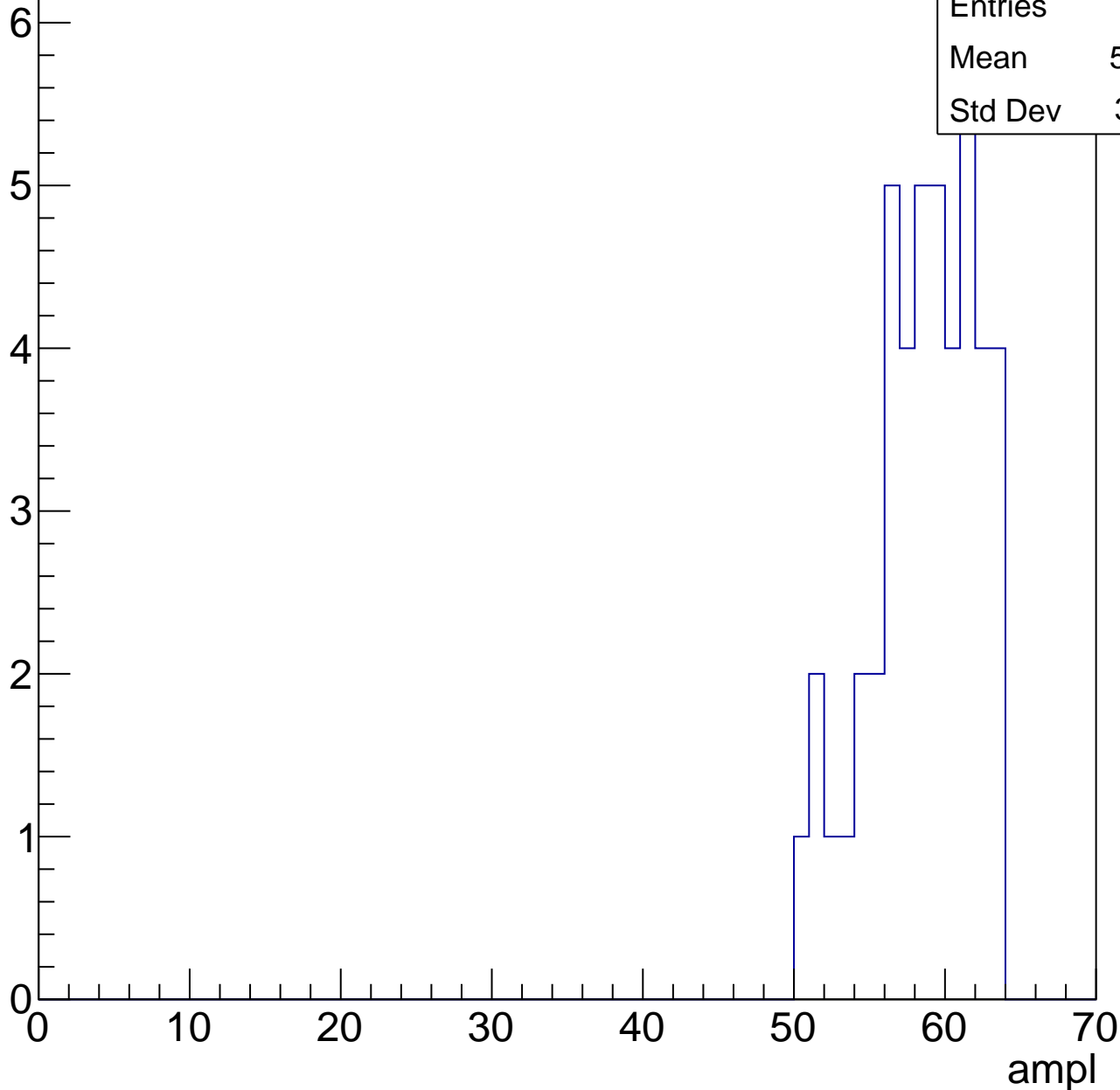


B1L103S, U2-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.13
Std Dev	3.411

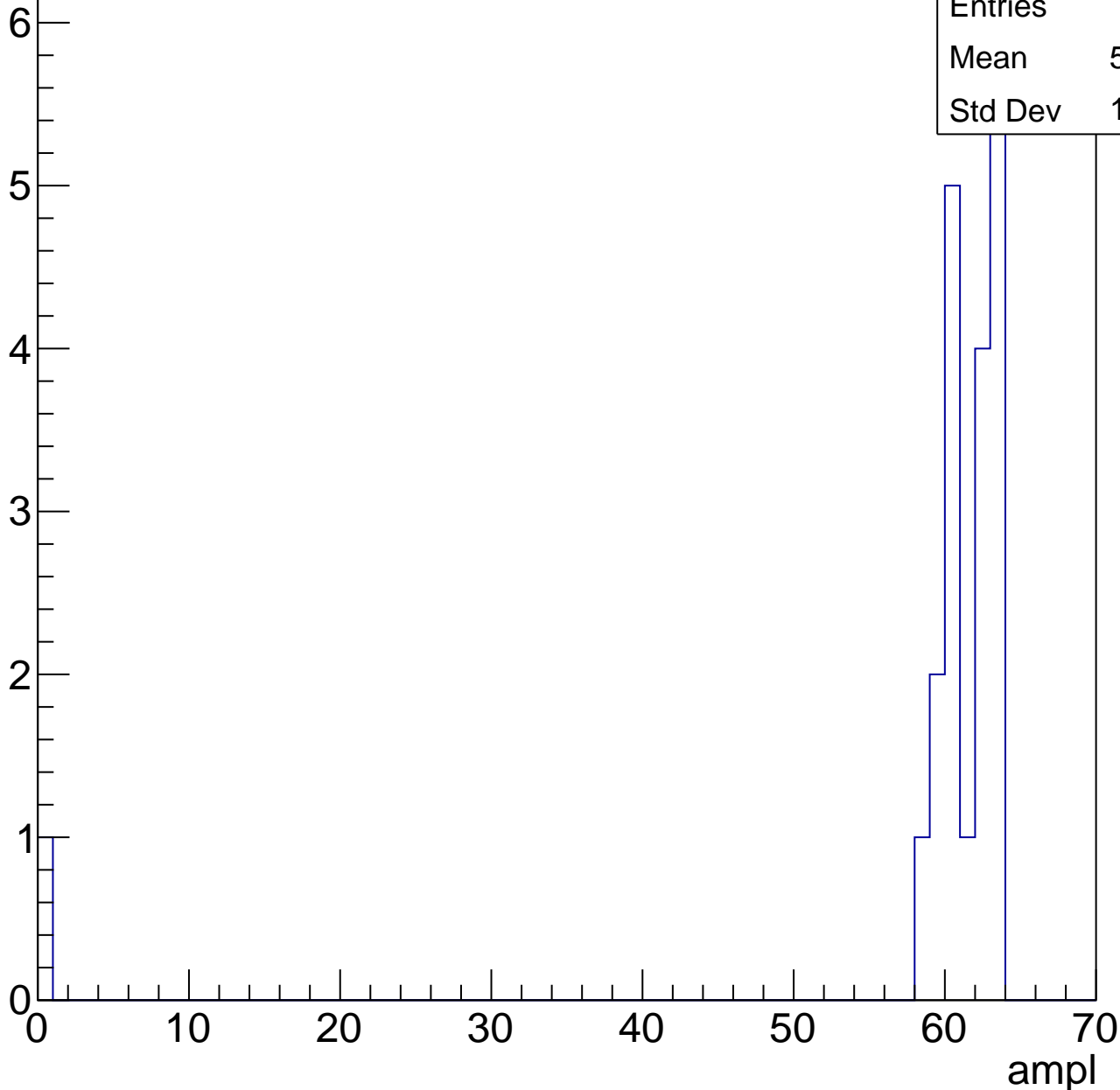


B1L103S, U2-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

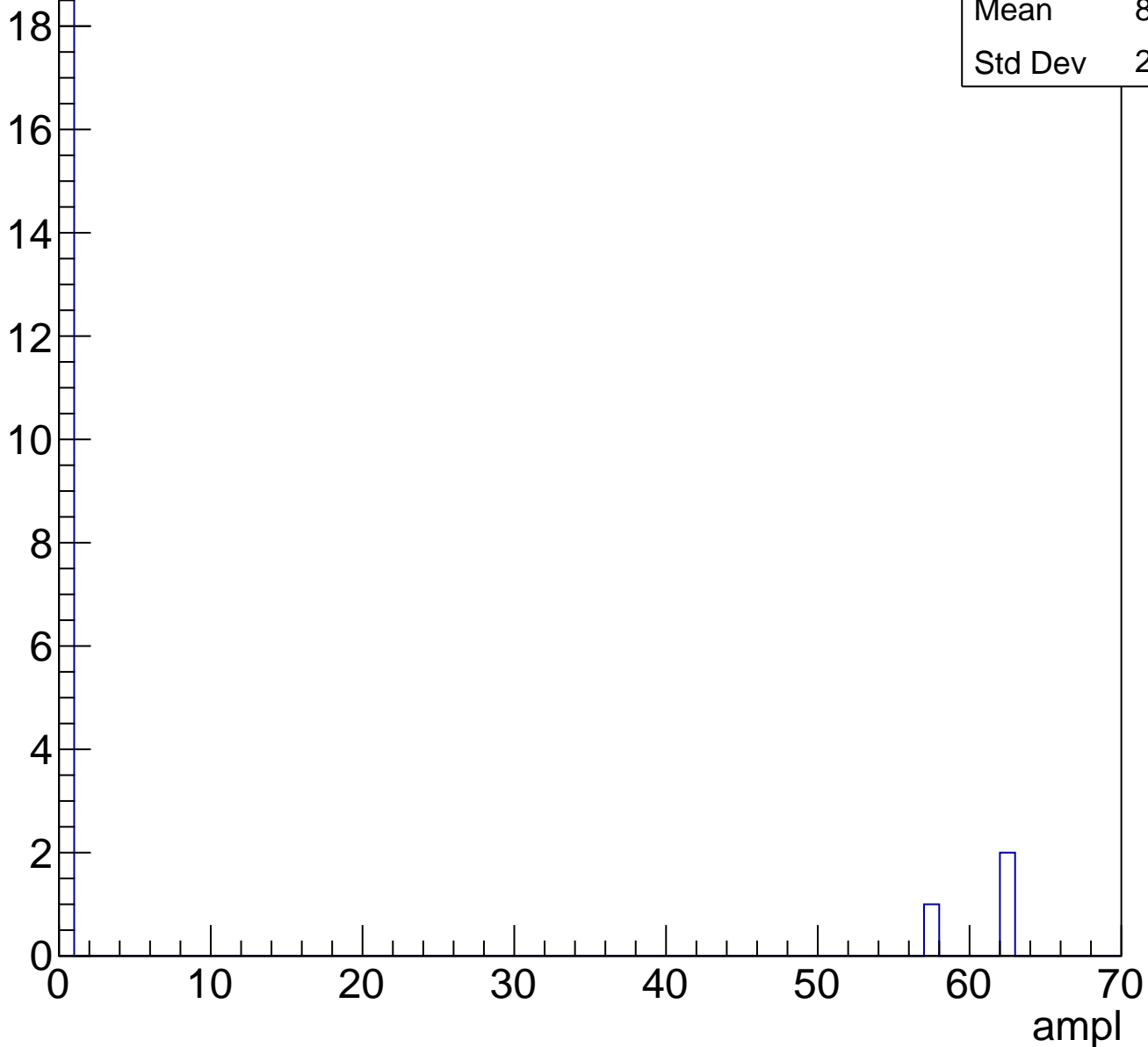
Entries	20
Mean	58.15
Std Dev	13.43



B1L103S, U2-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch27, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	25.65
Std Dev	10.93

Gaus mean : 29.9671

Gaus Width: 3.8438

Entry

12

10

8

6

4

2

0

0

10

20

30

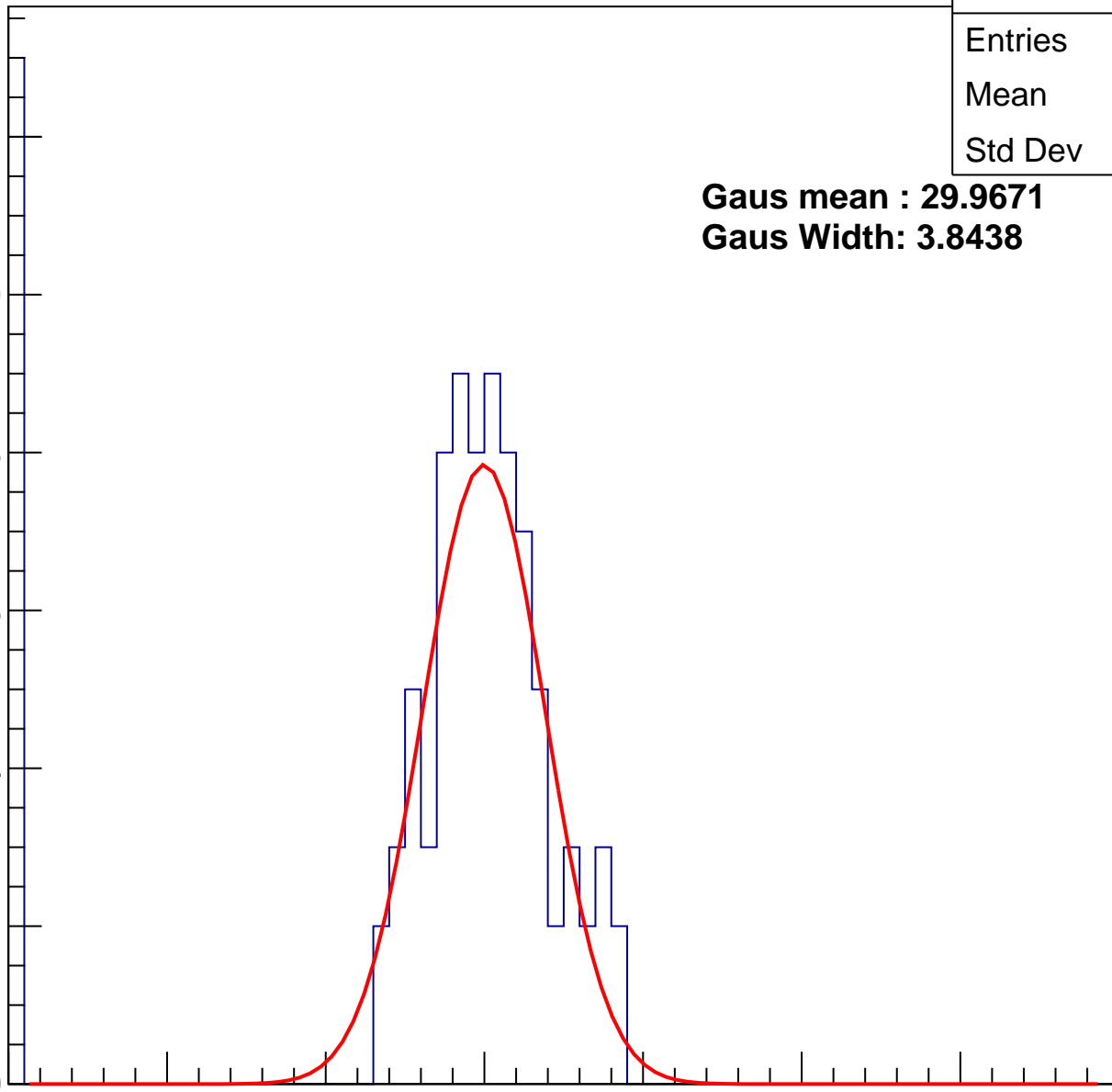
40

50

60

70

ampl



B1L103S, U2-ch27, adc1

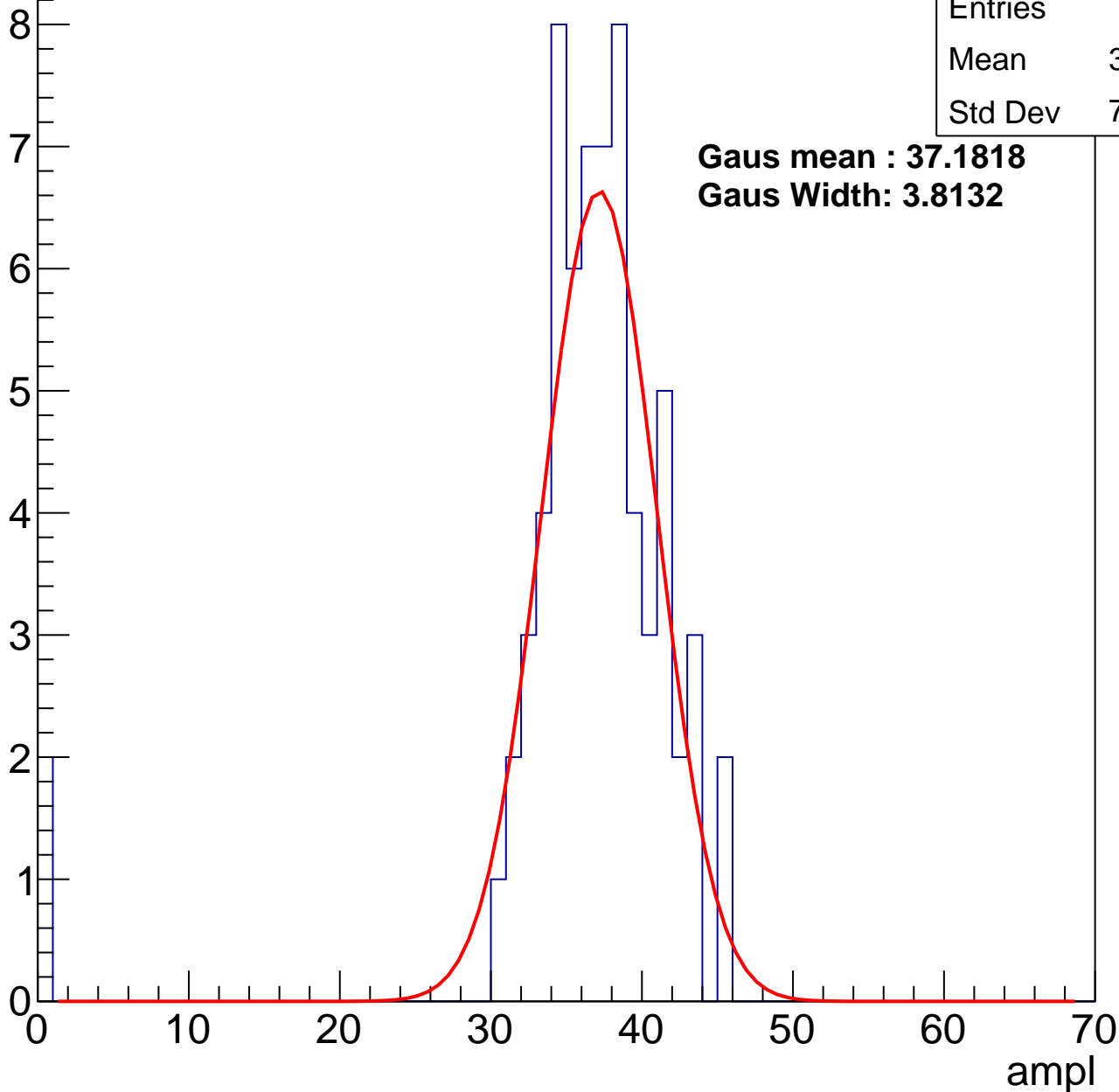
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.84
Std Dev	7.152

Gaus mean : 37.1818

Gaus Width: 3.8132



B1L103S, U2-ch27, adc2

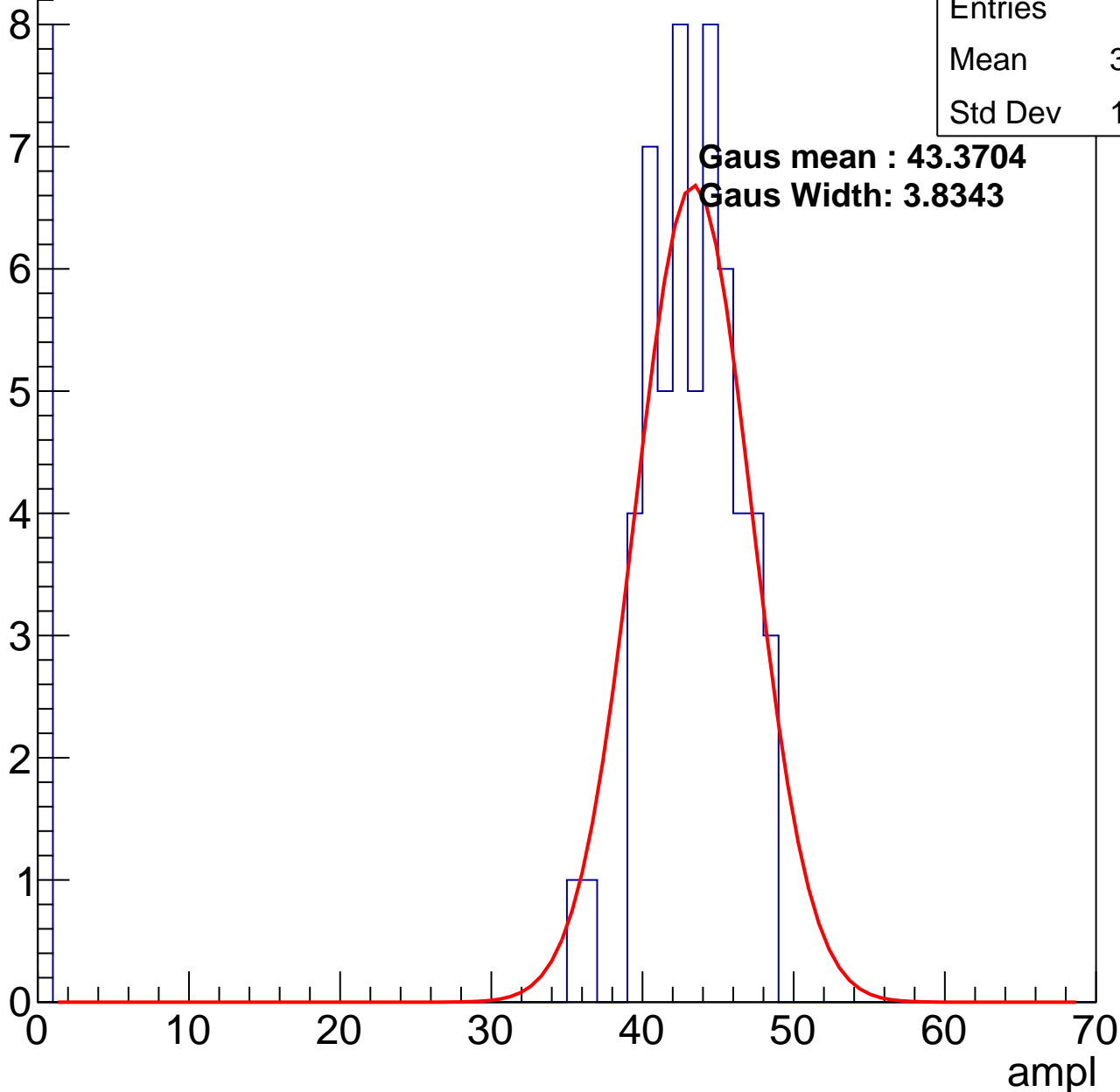
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.52
Std Dev	14.44

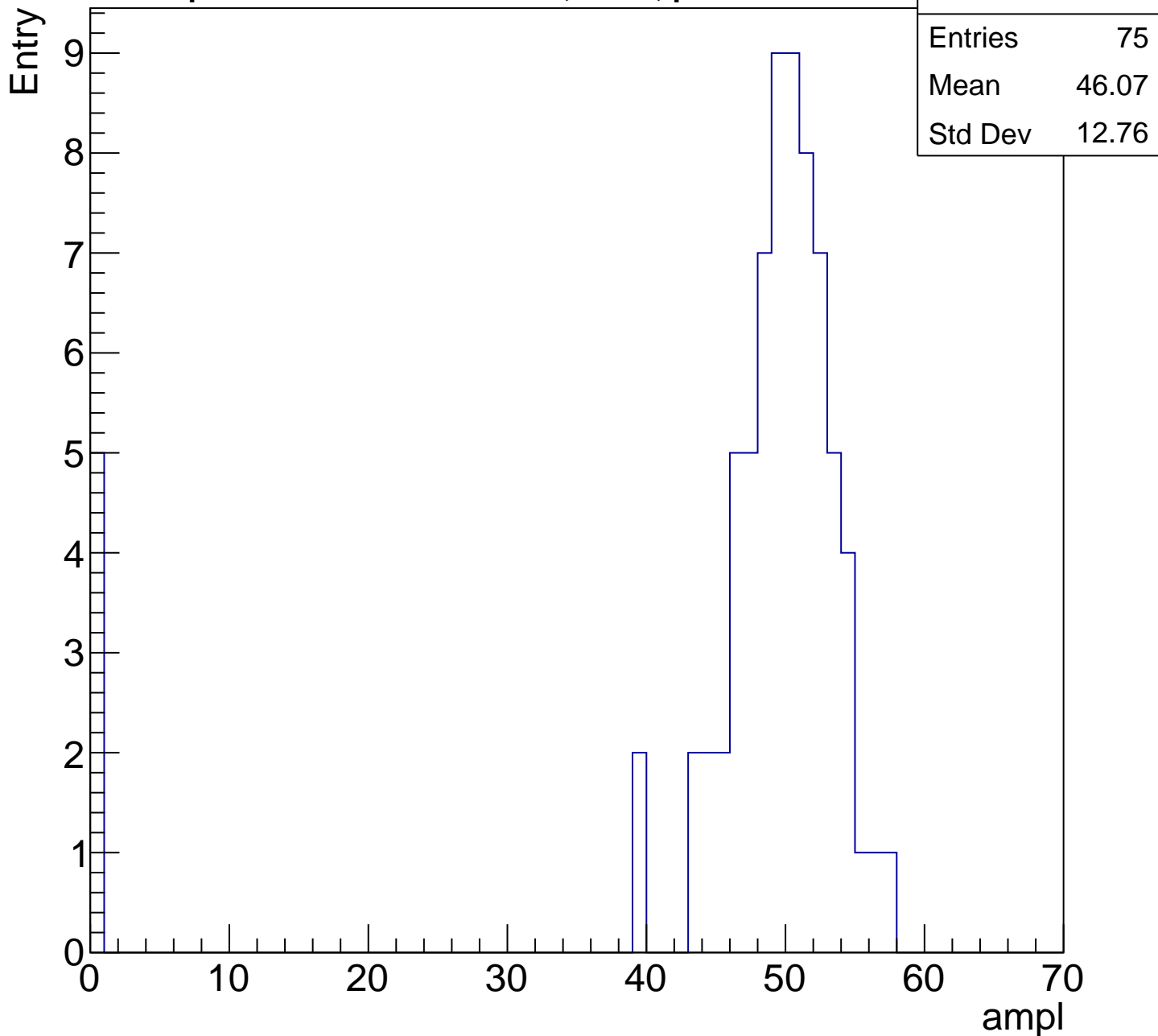
Gaus mean : 43.3704

Gaus Width: 3.8343



B1L103S, U2-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

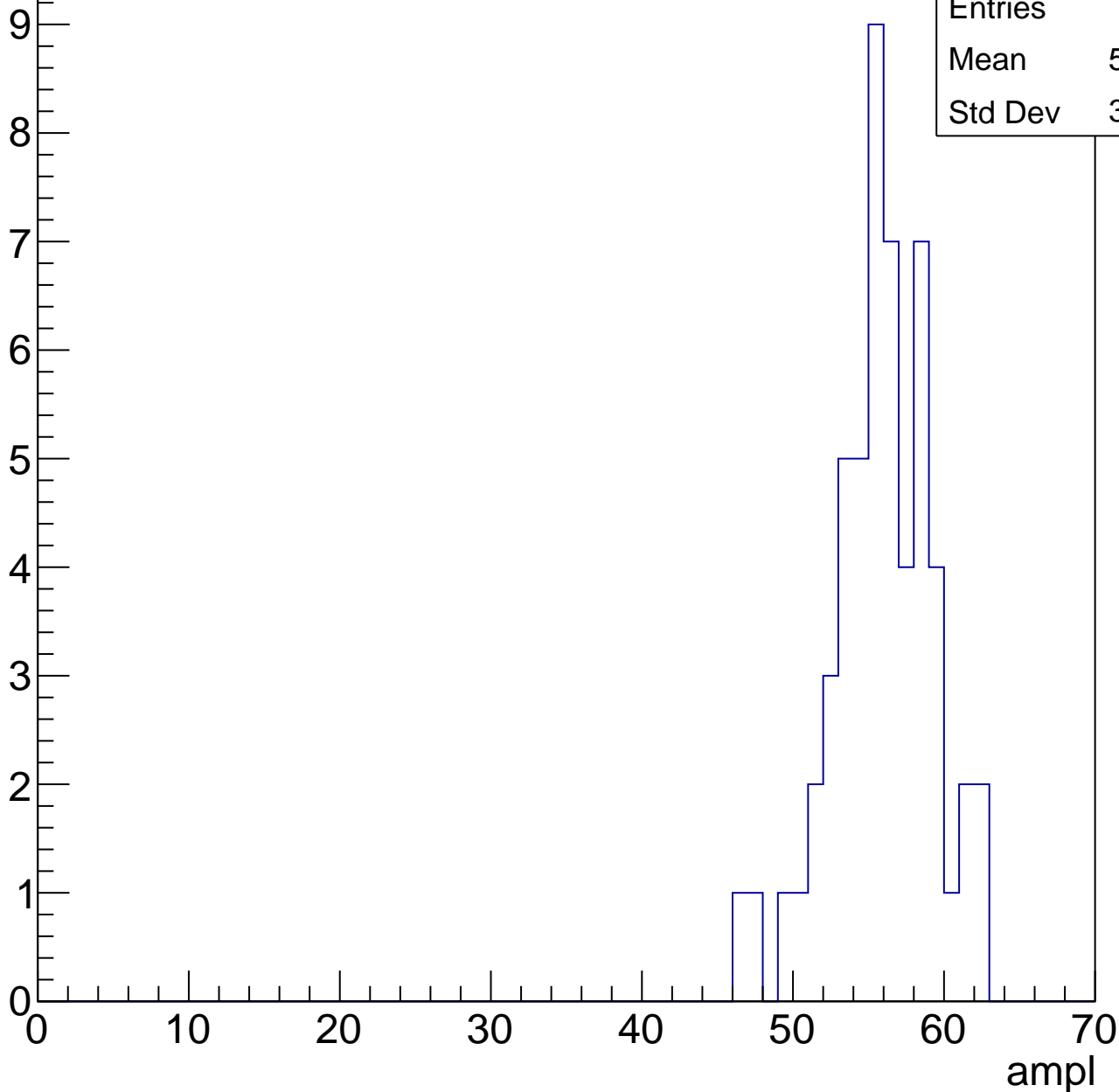


B1L103S, U2-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.42
Std Dev	3.372



B1L103S, U2-ch27, adc5

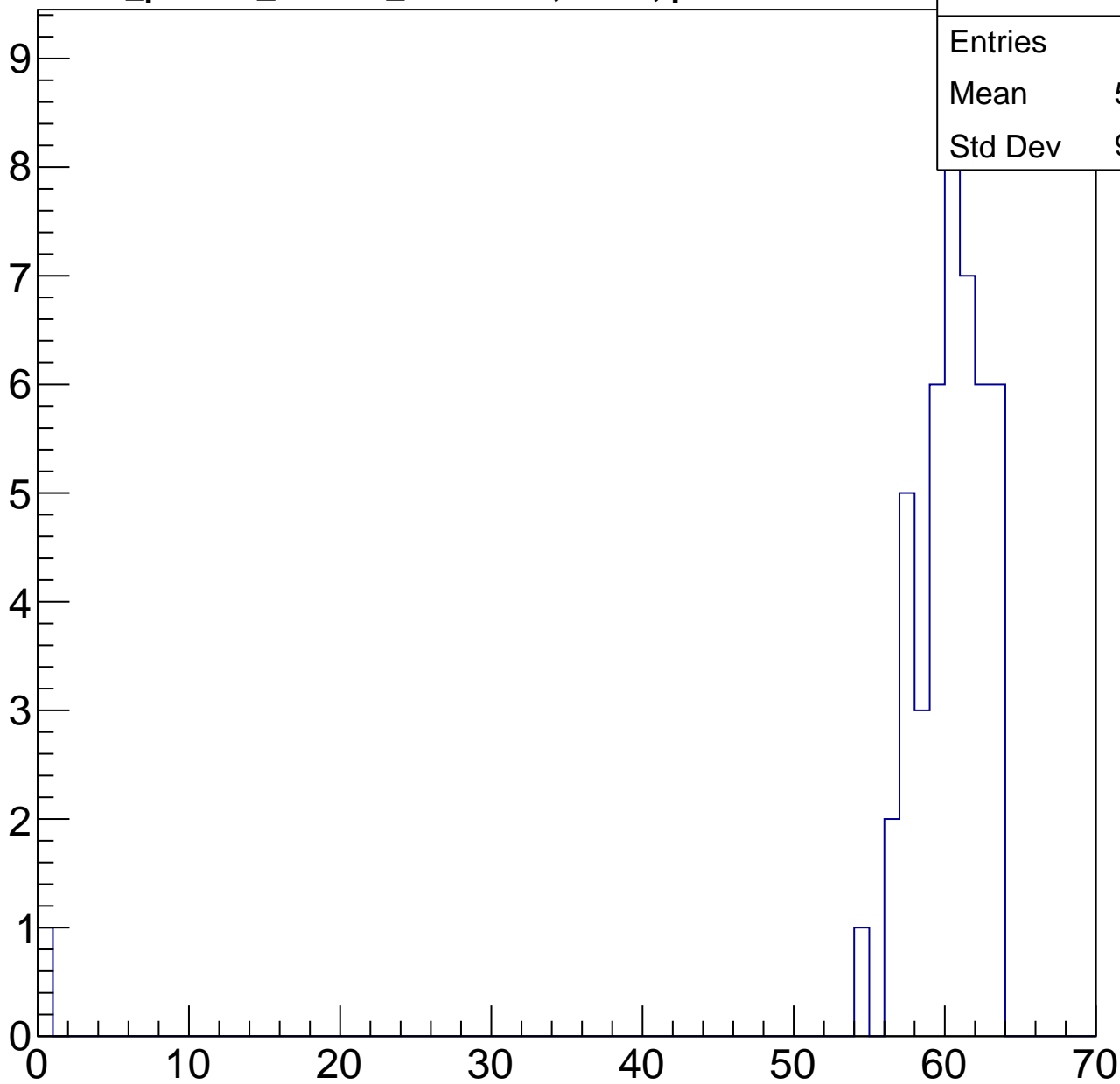
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.61
Std Dev	9.001

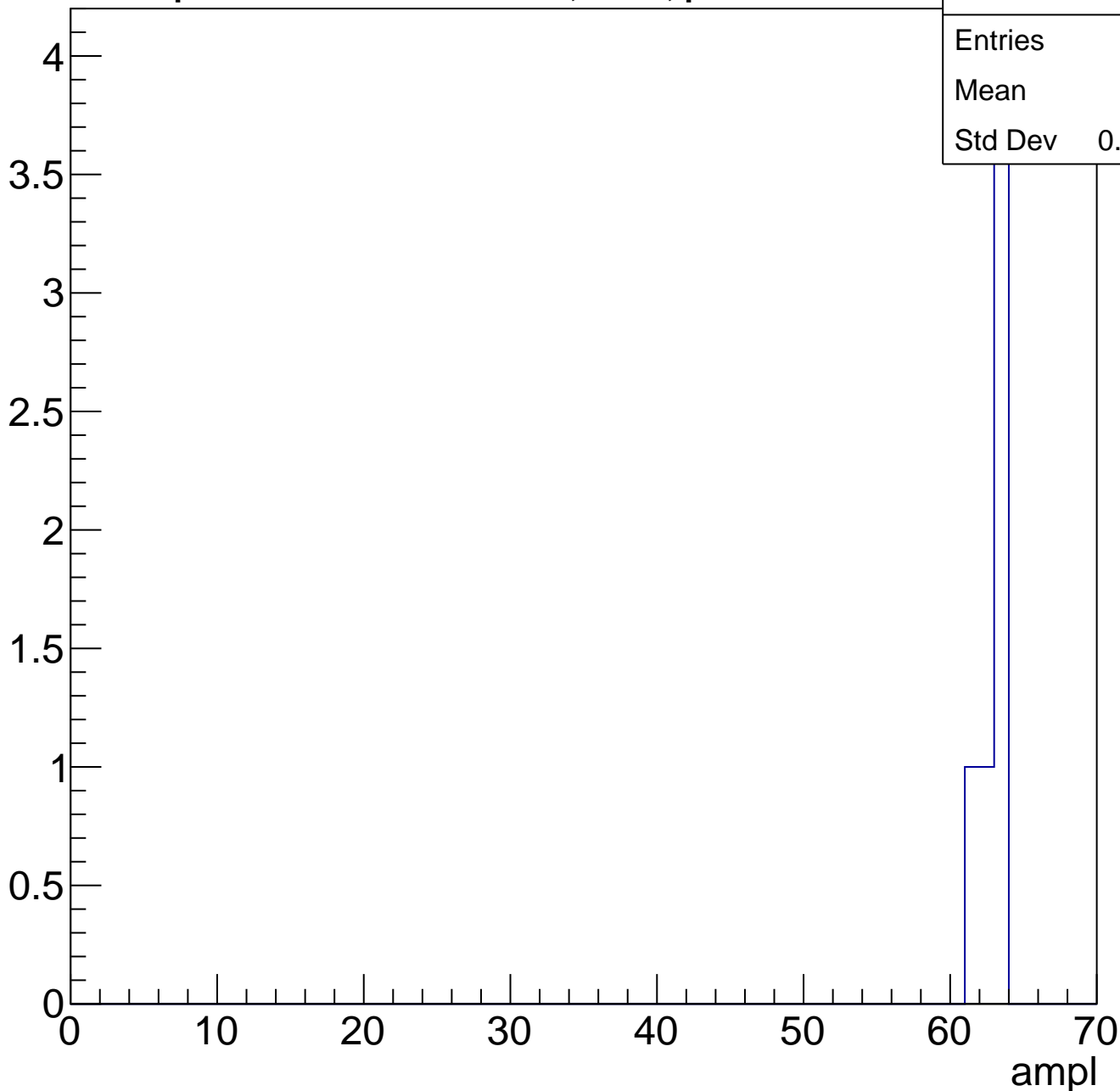
ampl



B1L103S, U2-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

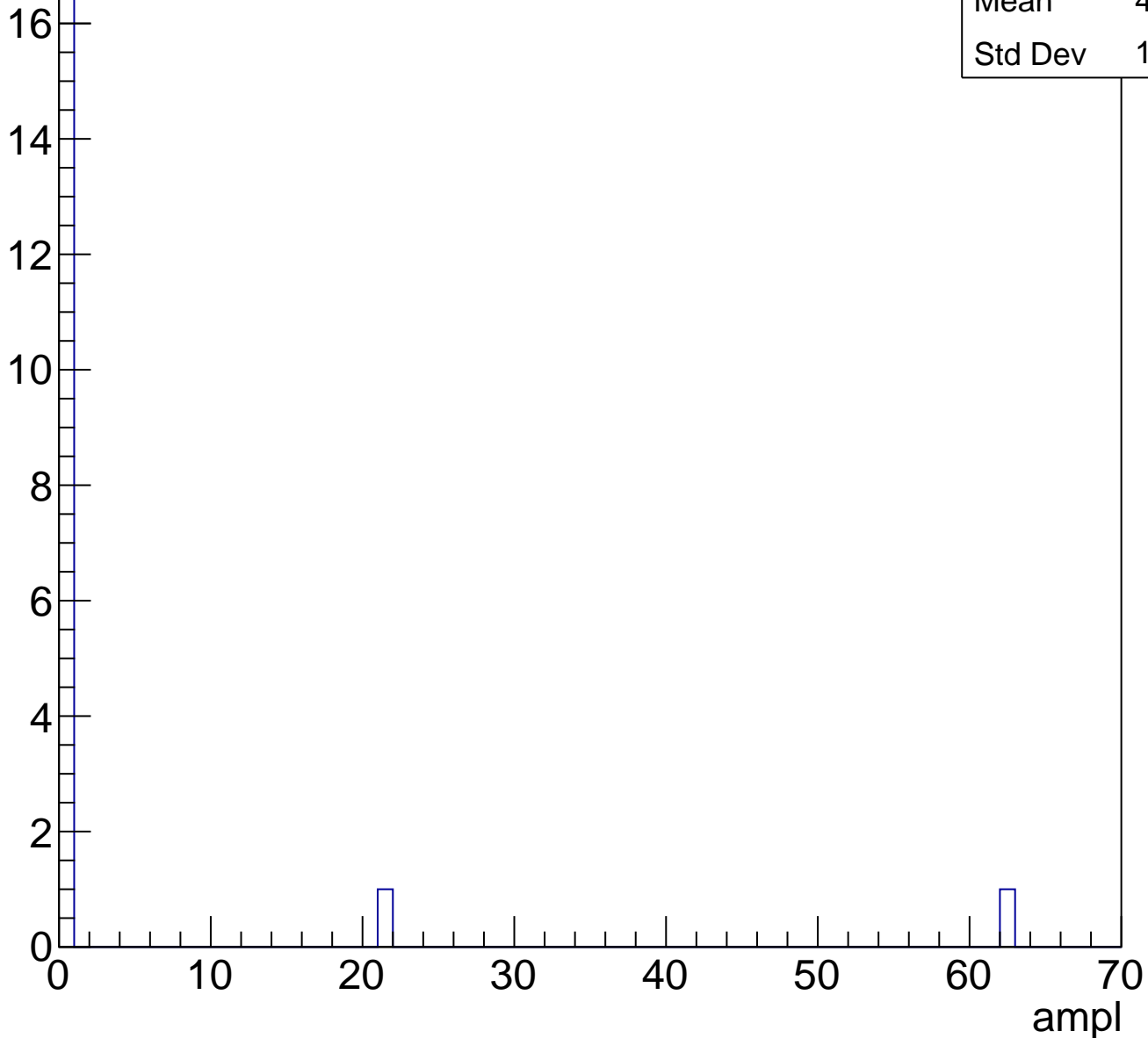


B1L103S, U2-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.368
Std Dev	14.37

Entry



B1L103S, U2-ch28, adc0

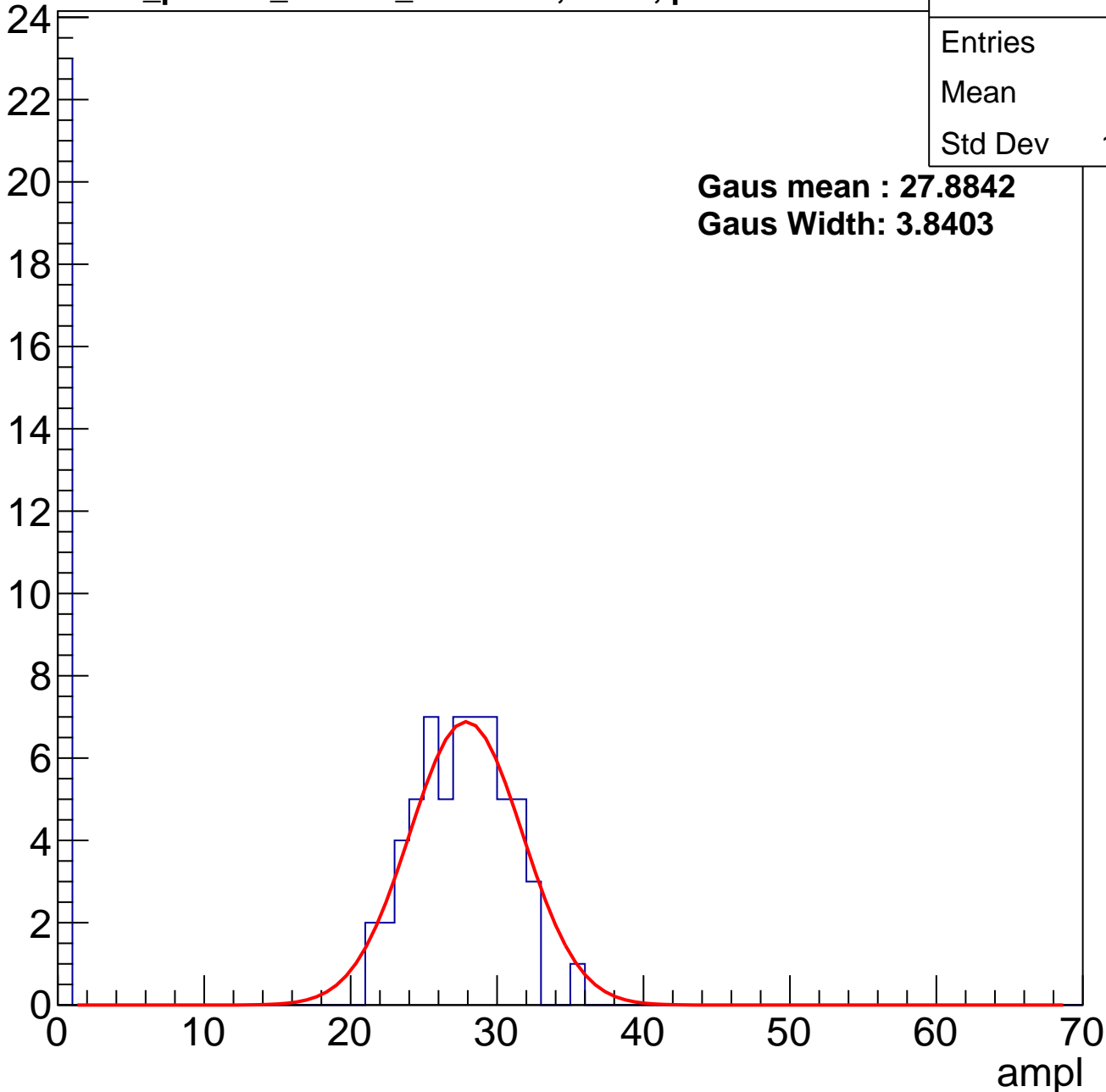
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	19.6
Std Dev	12.41

Gaus mean : 27.8842

Gaus Width: 3.8403

Entry



B1L103S, U2-ch28, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	29.28
Std Dev	12.7

Gaus mean : 34.3257

Gaus Width: 3.4372

Entry

12

10

8

6

4

2

0

0

10

20

30

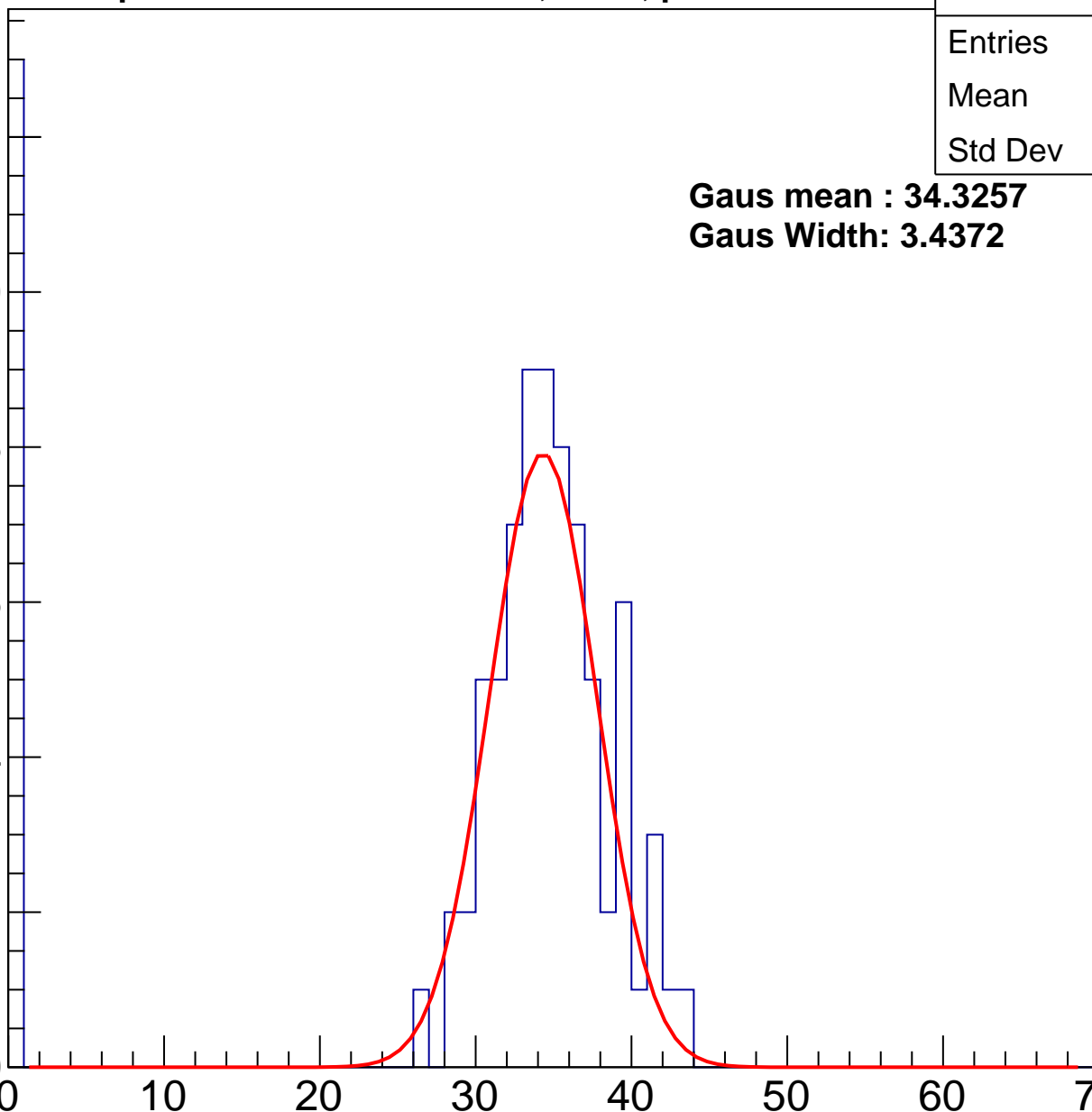
40

50

60

70

ampl



B1L103S, U2-ch28, adc2

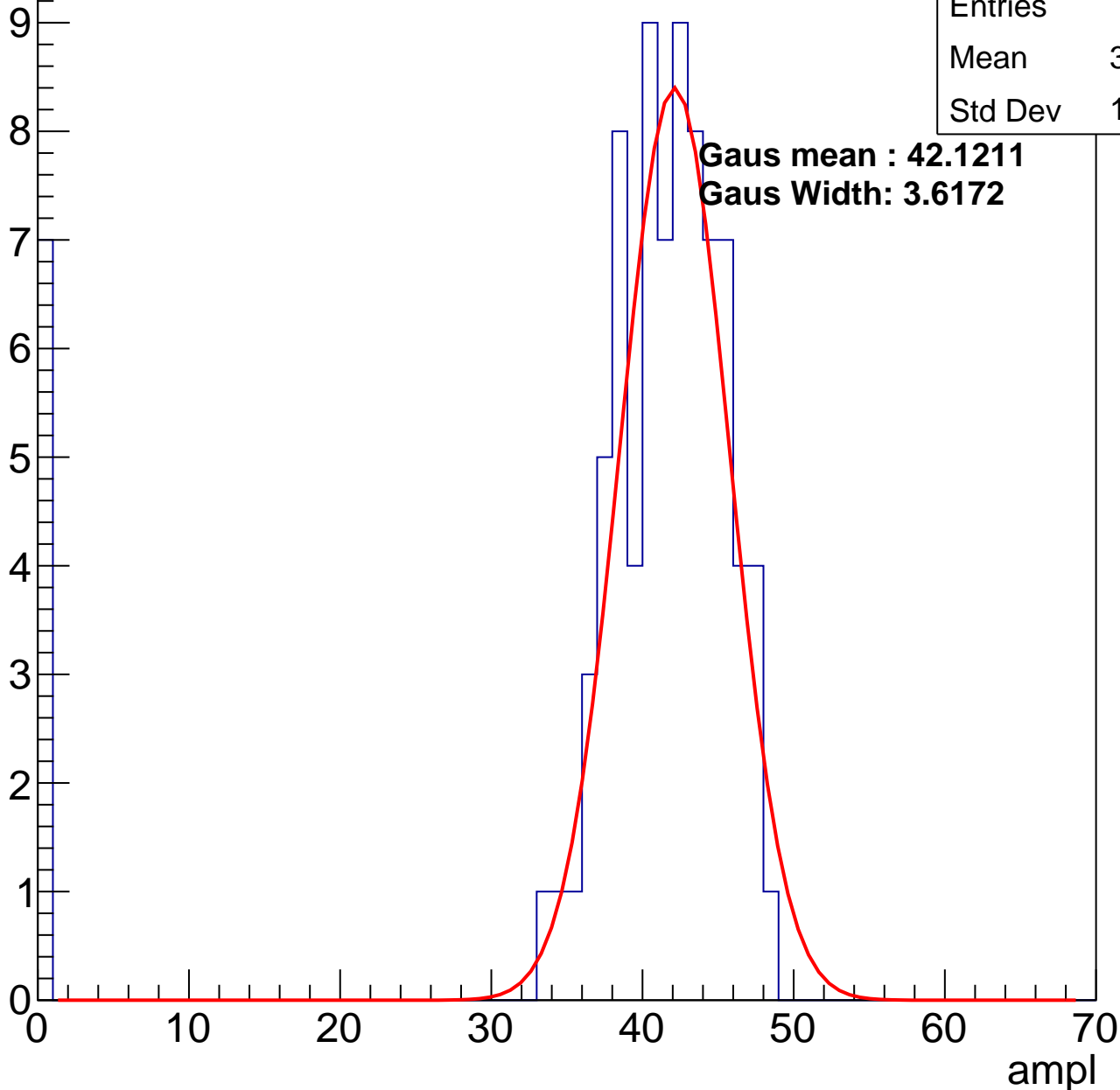
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	37.99
Std Dev	11.76

Gaus mean : 42.1211

Gaus Width: 3.6172

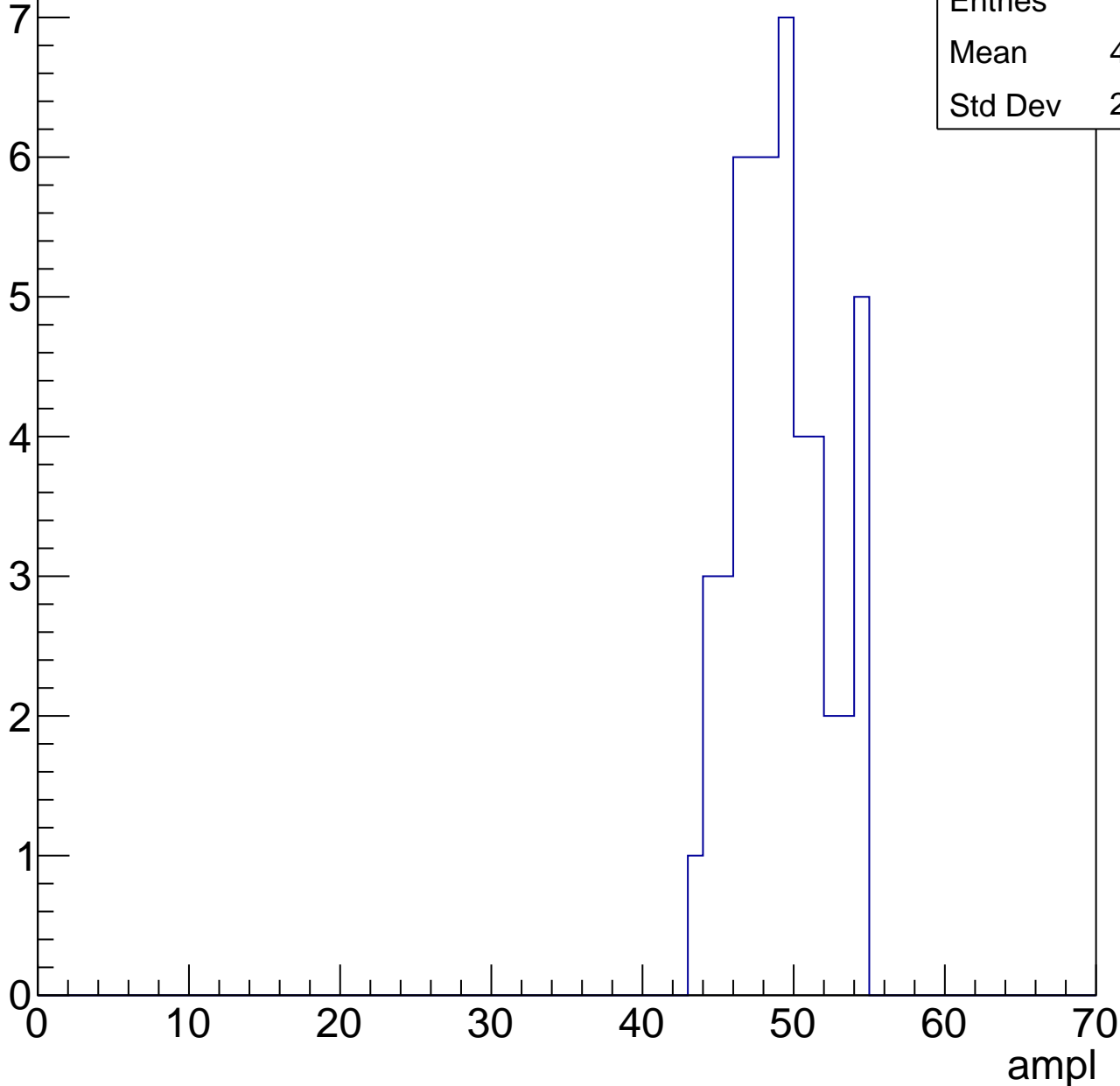


B1L103S, U2-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	48.63
Std Dev	2.967

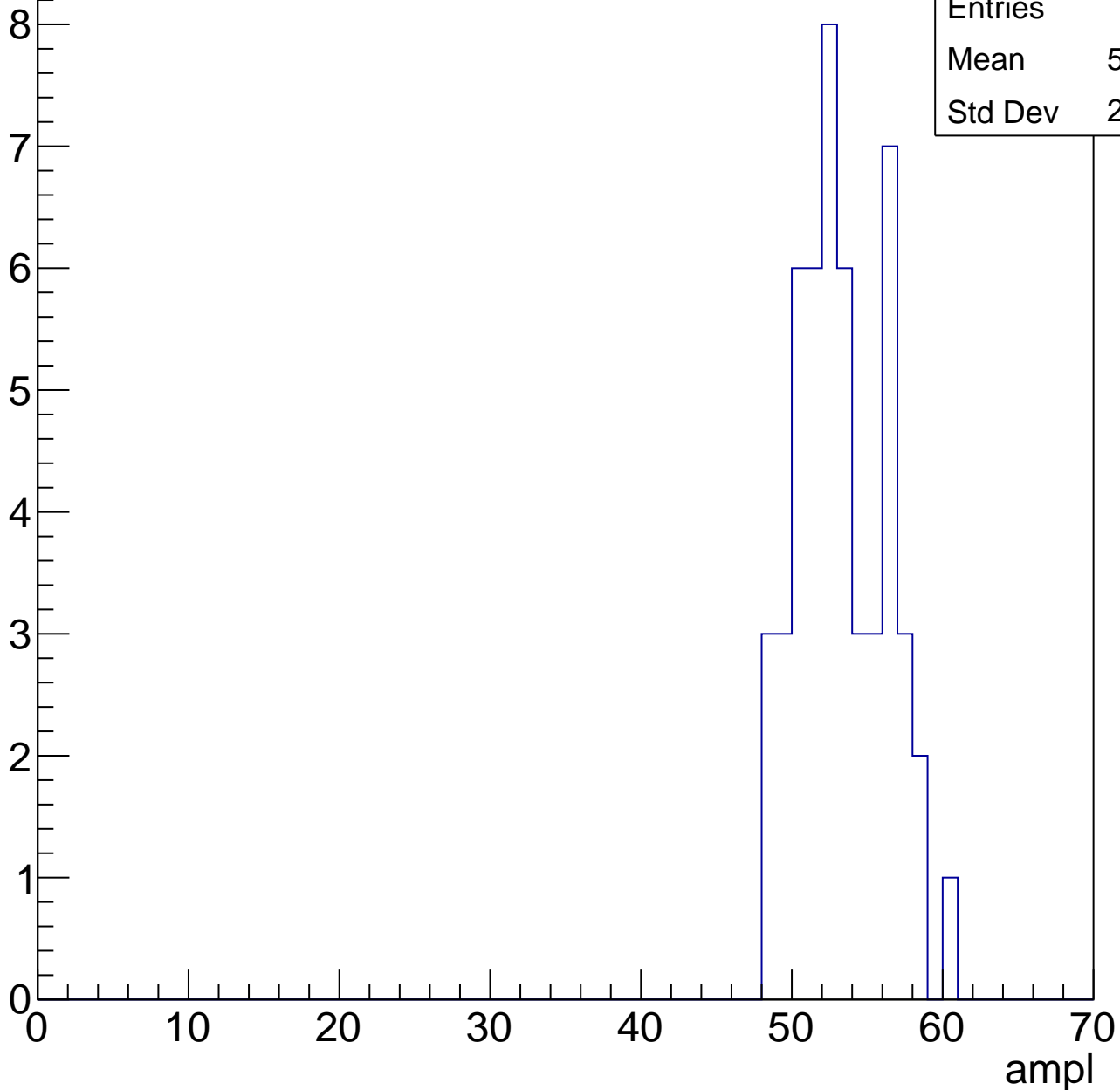


B1L103S, U2-ch28, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	52.88
Std Dev	2.915

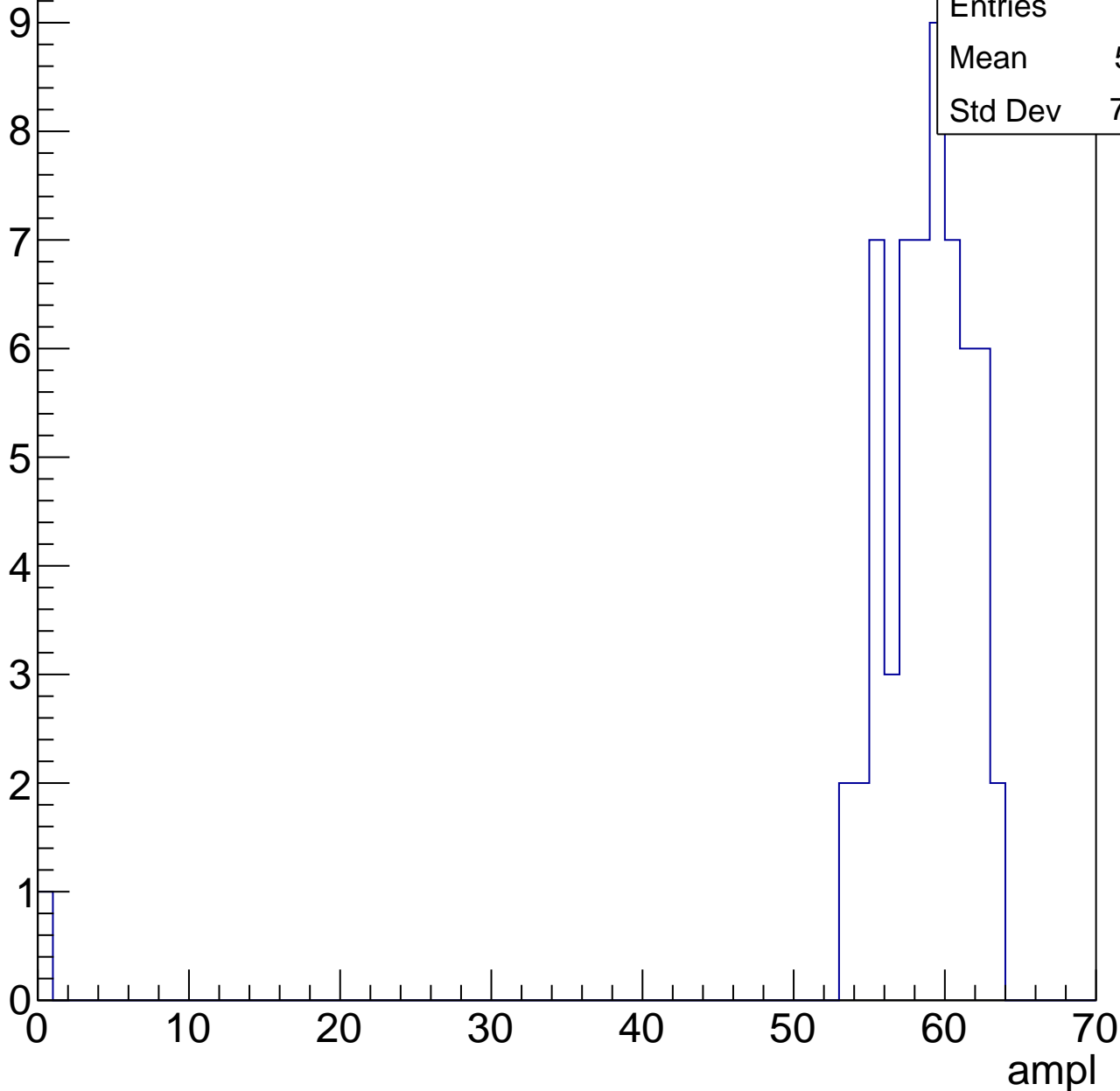


B1L103S, U2-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.41
Std Dev	7.966

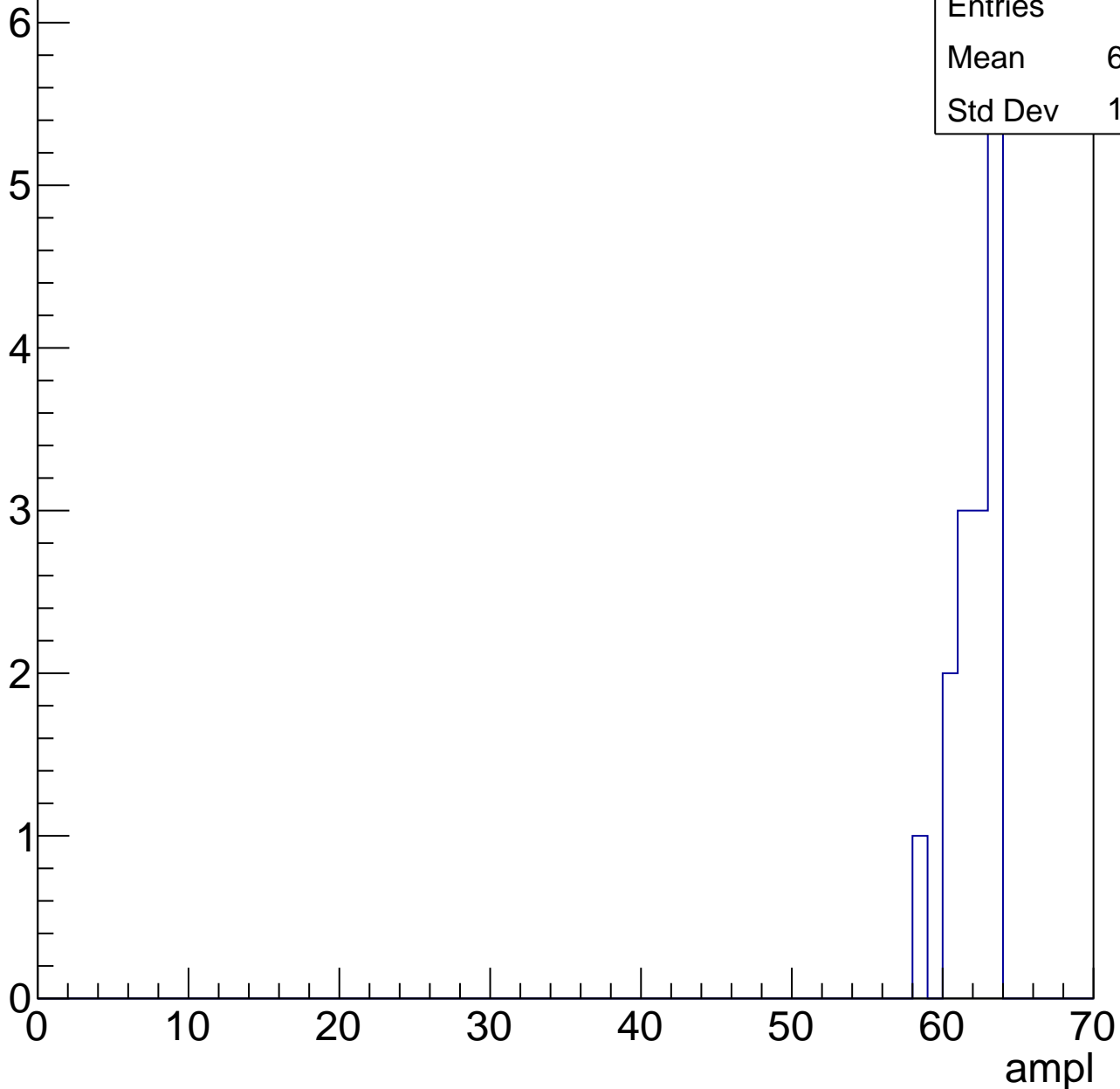


B1L103S, U2-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.67
Std Dev	1.445

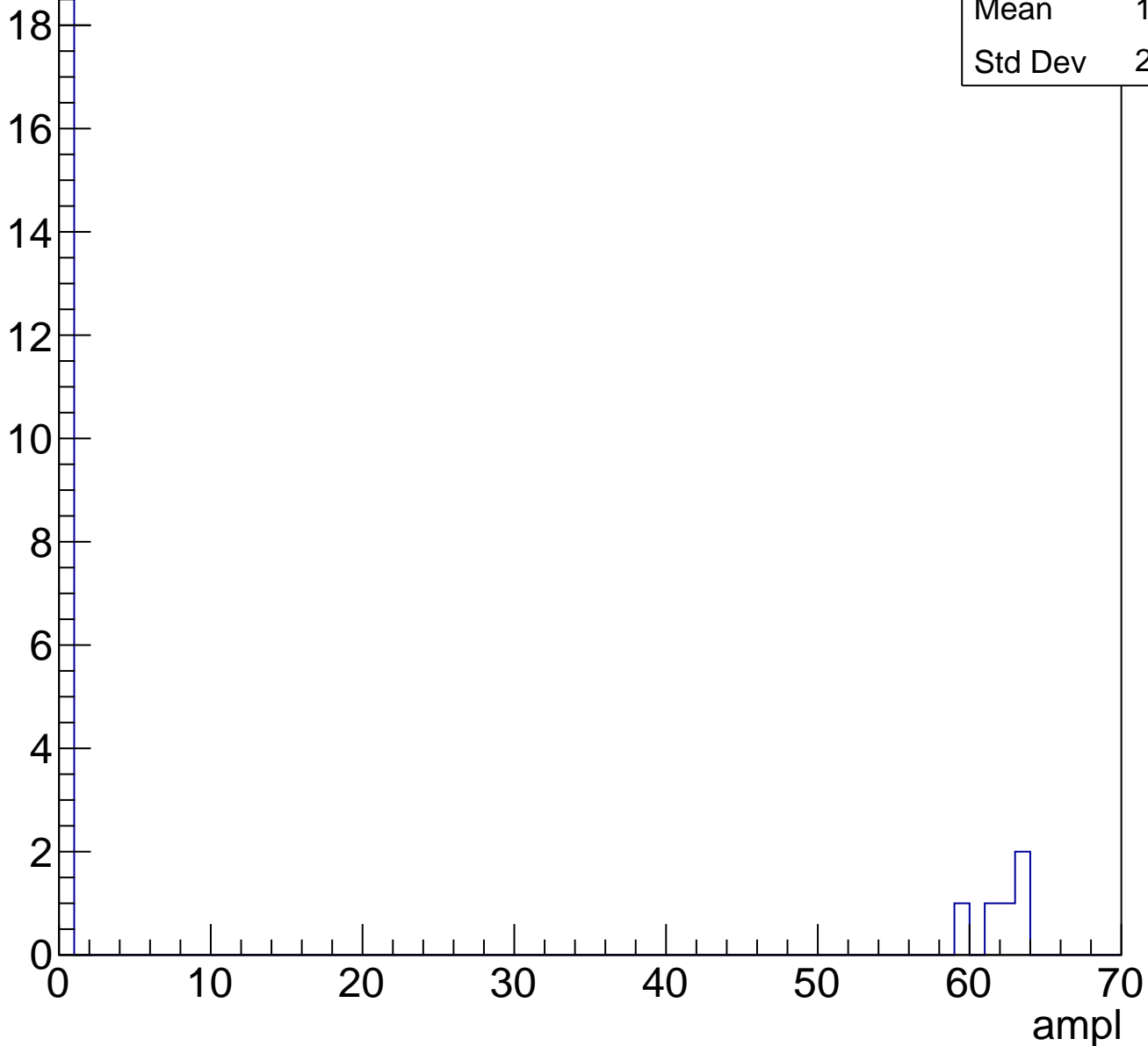


B1L103S, U2-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	12.83
Std Dev	25.03

Entry



B1L103S, U2-ch29, adc0

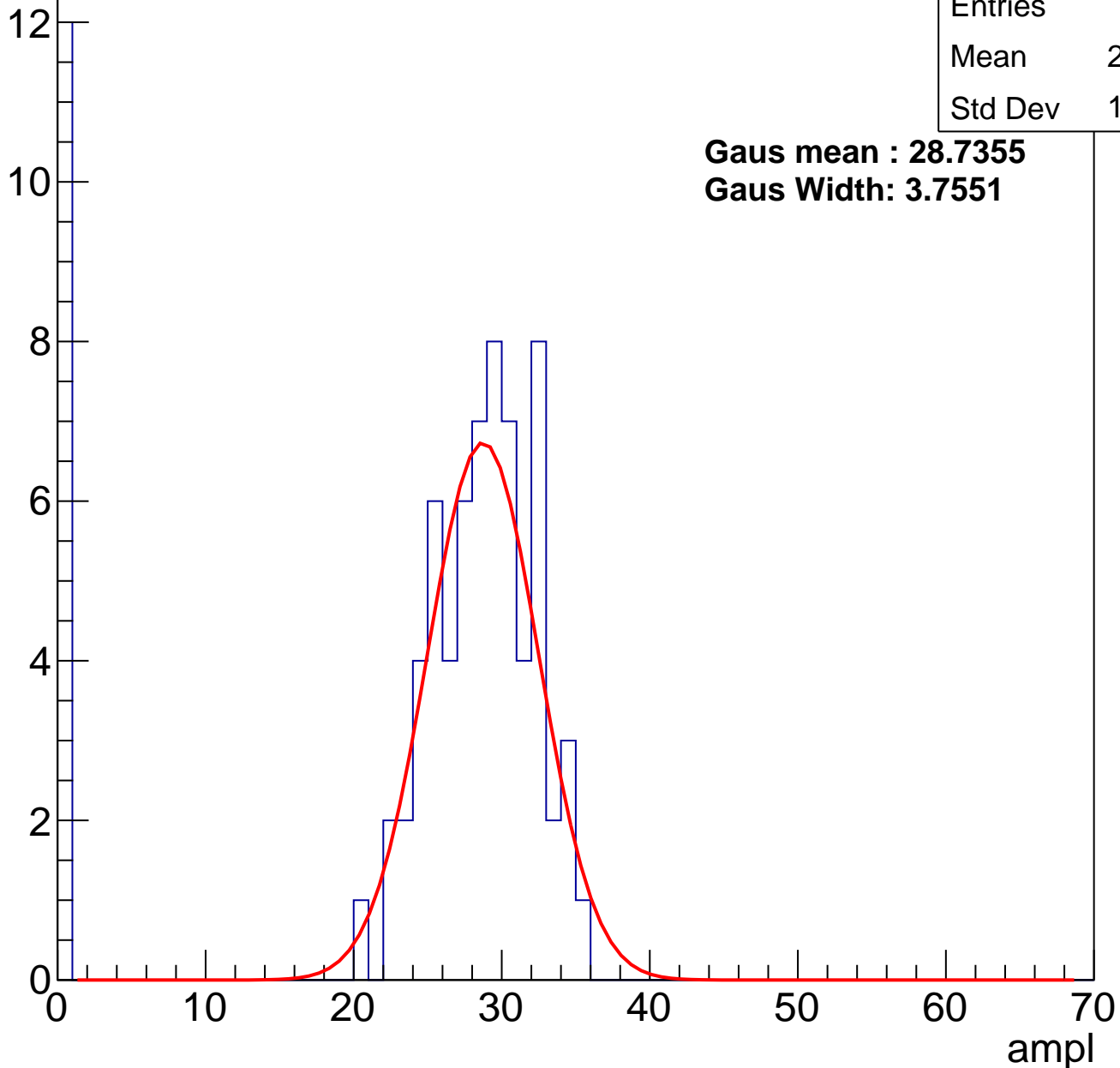
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	23.94
Std Dev	10.73

Gaus mean : 28.7355

Gaus Width: 3.7551

Entry



B1L103S, U2-ch29, adc1

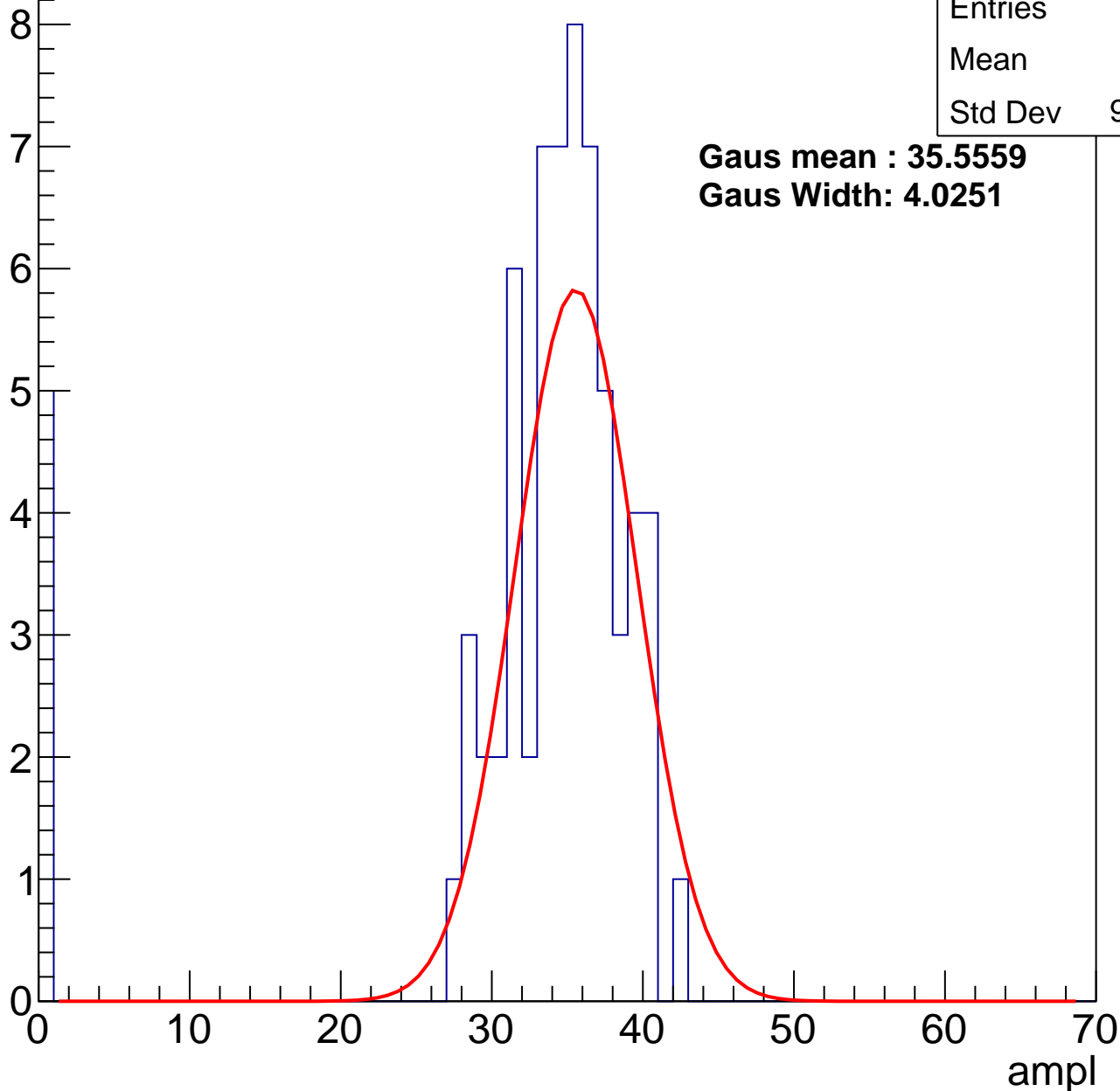
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	31.9
Std Dev	9.642

Gaus mean : 35.5559

Gaus Width: 4.0251



B1L103S, U2-ch29, adc2

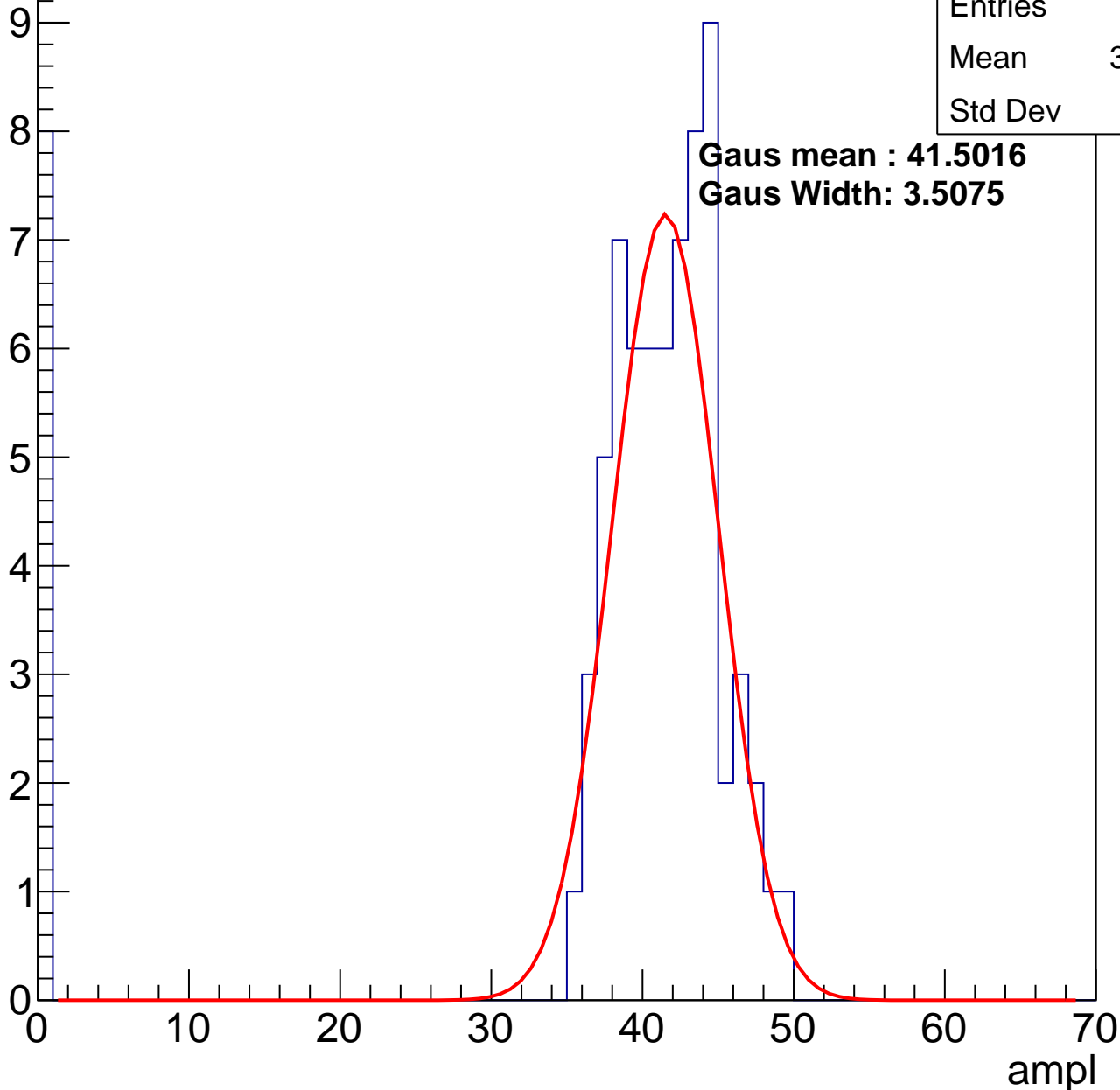
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	36.89
Std Dev	13.1

Gaus mean : 41.5016

Gaus Width: 3.5075

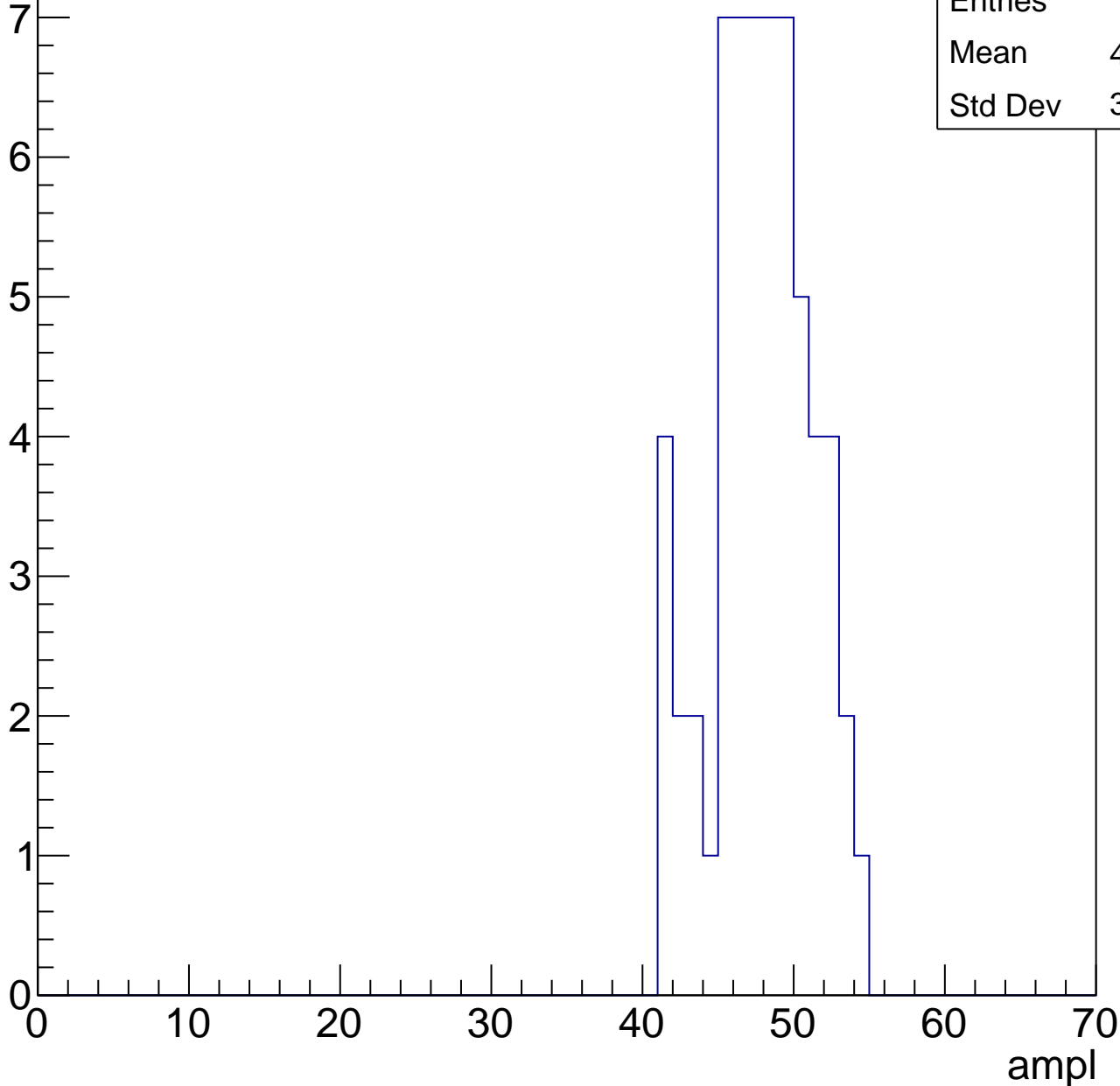


B1L103S, U2-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	47.42
Std Dev	3.226

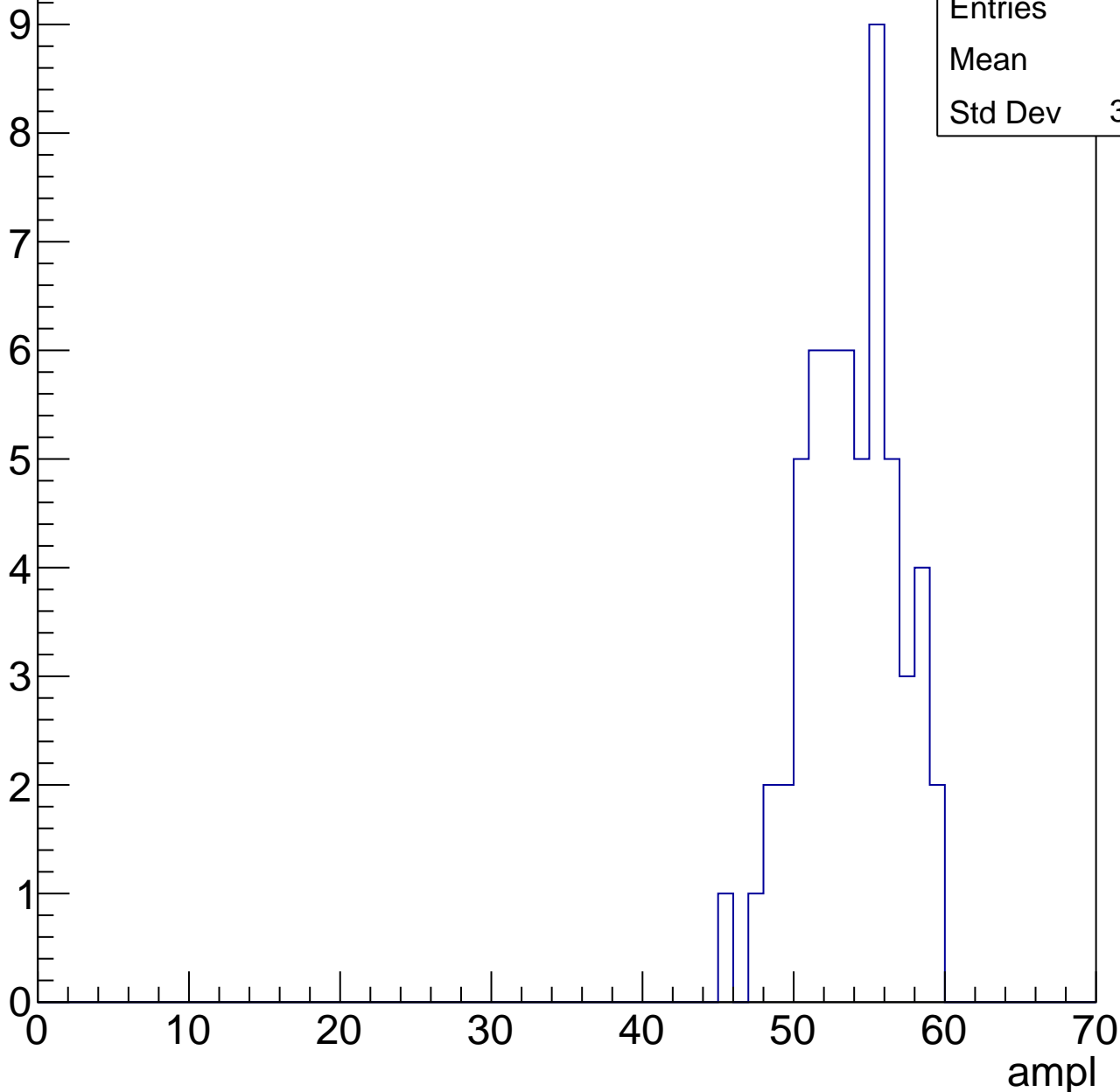


B1L103S, U2-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	53.3
Std Dev	3.129



B1L103S, U2-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	59
Mean	57.85
Std Dev	8.08

ampl

0

10

20

30

40

50

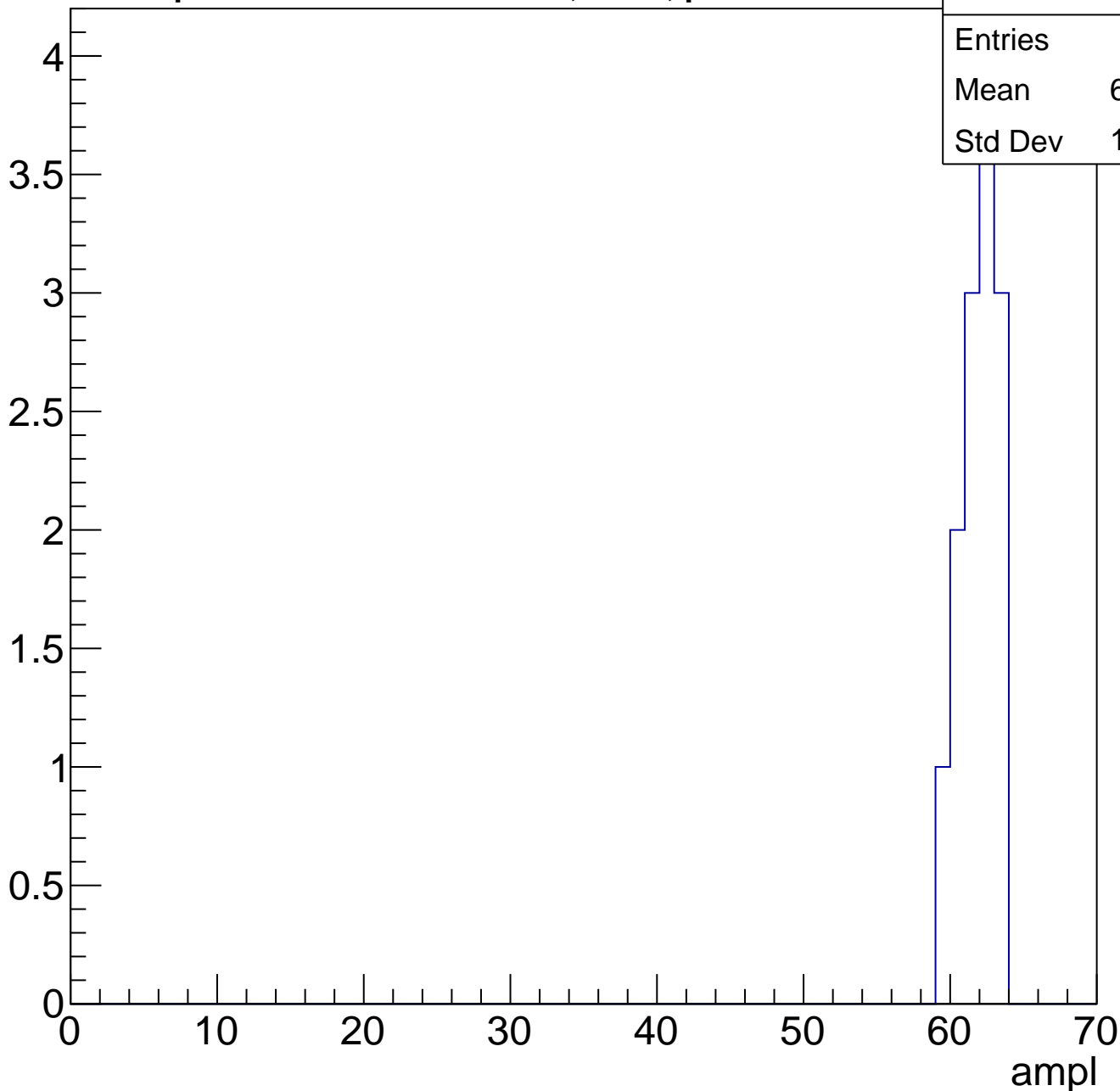
60

70

B1L103S, U2-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U2-ch30, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	21.97
Std Dev	10.48

Gaus mean : 27.1340

Gaus Width: 3.8364

Entry

12

10

8

6

4

2

0

0

10

20

30

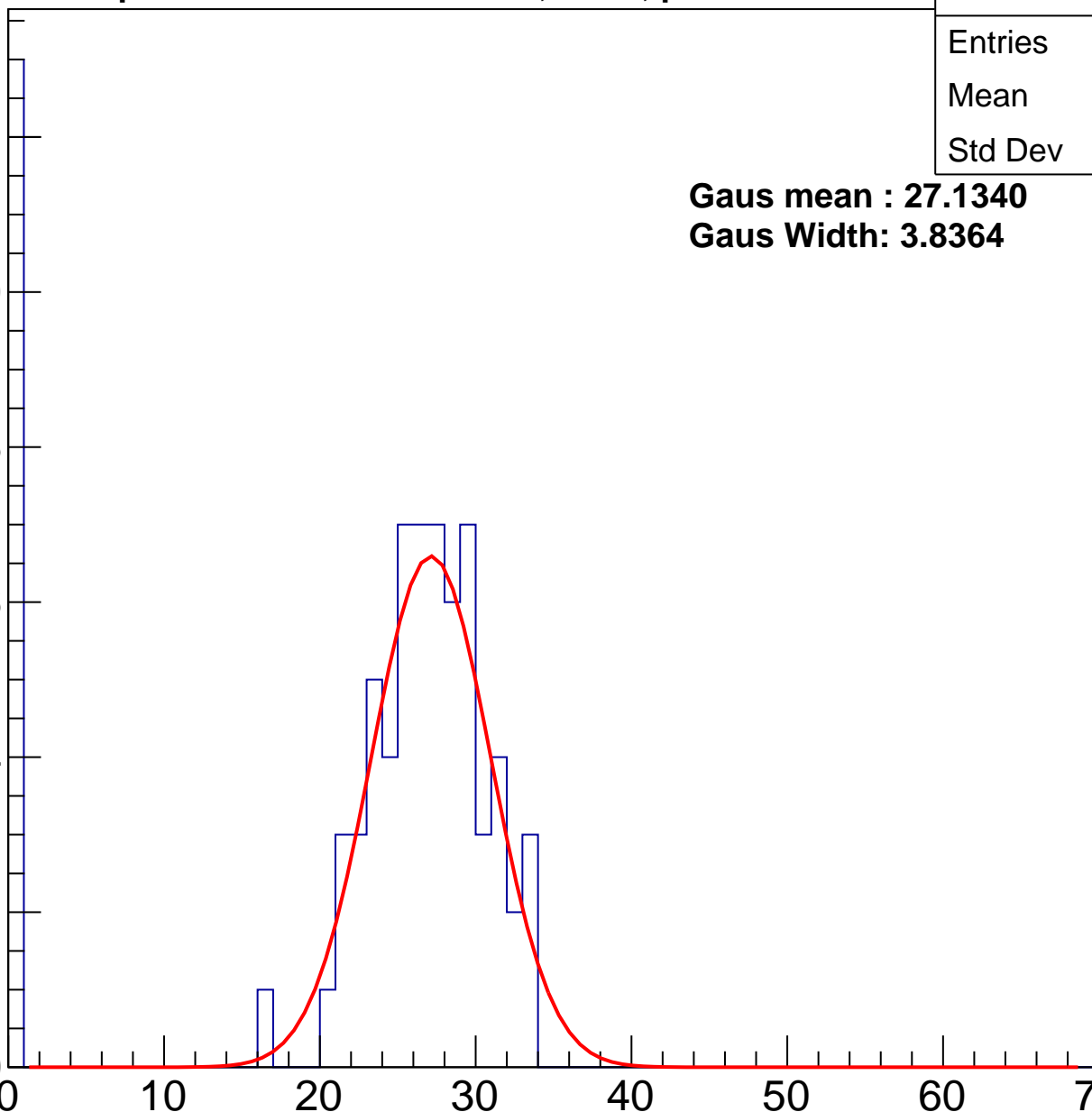
40

50

60

70

ampl



B1L103S, U2-ch30, adc1

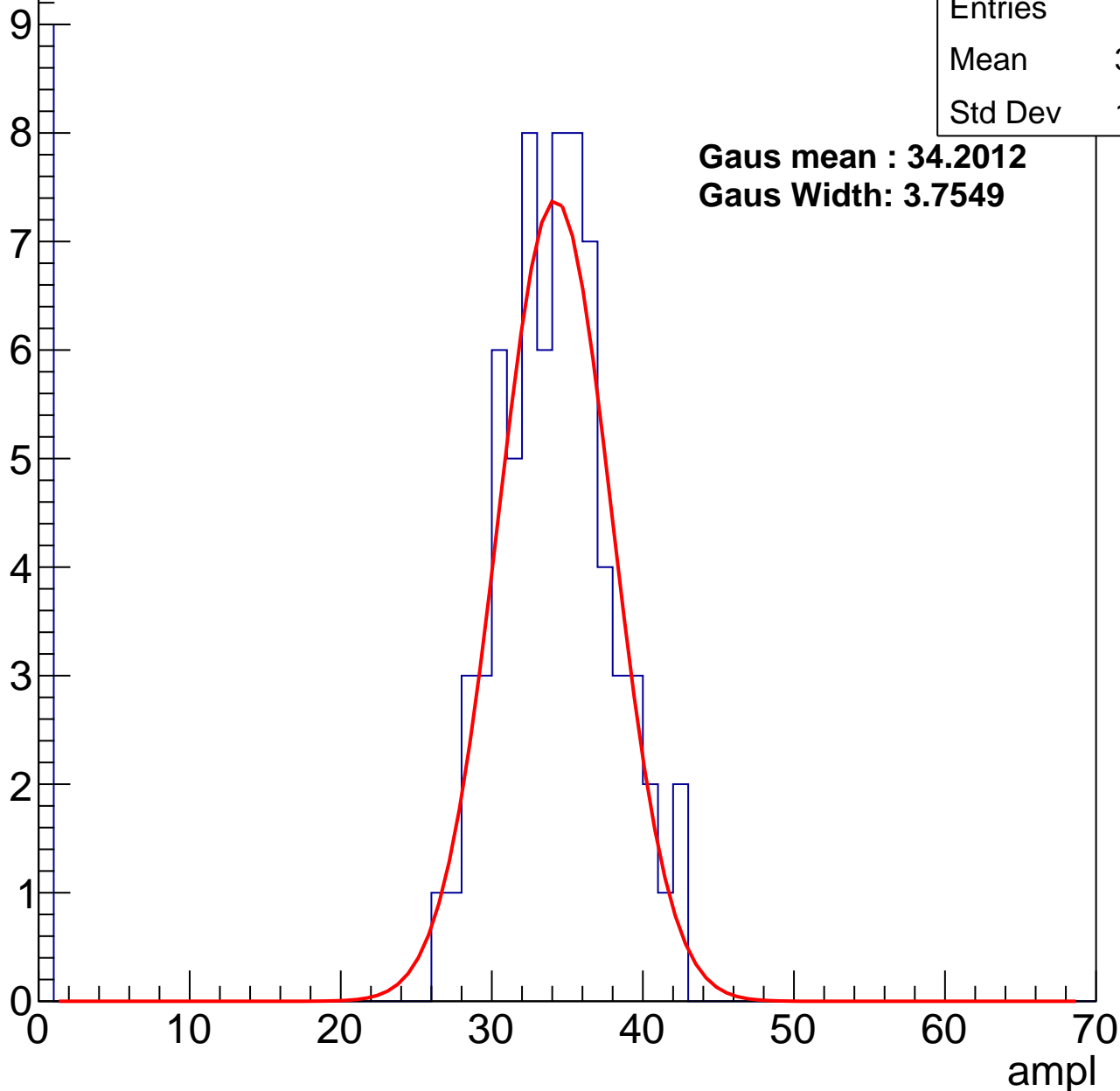
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	30.01
Std Dev	11.21

Gaus mean : 34.2012

Gaus Width: 3.7549



B1L103S, U2-ch30, adc2

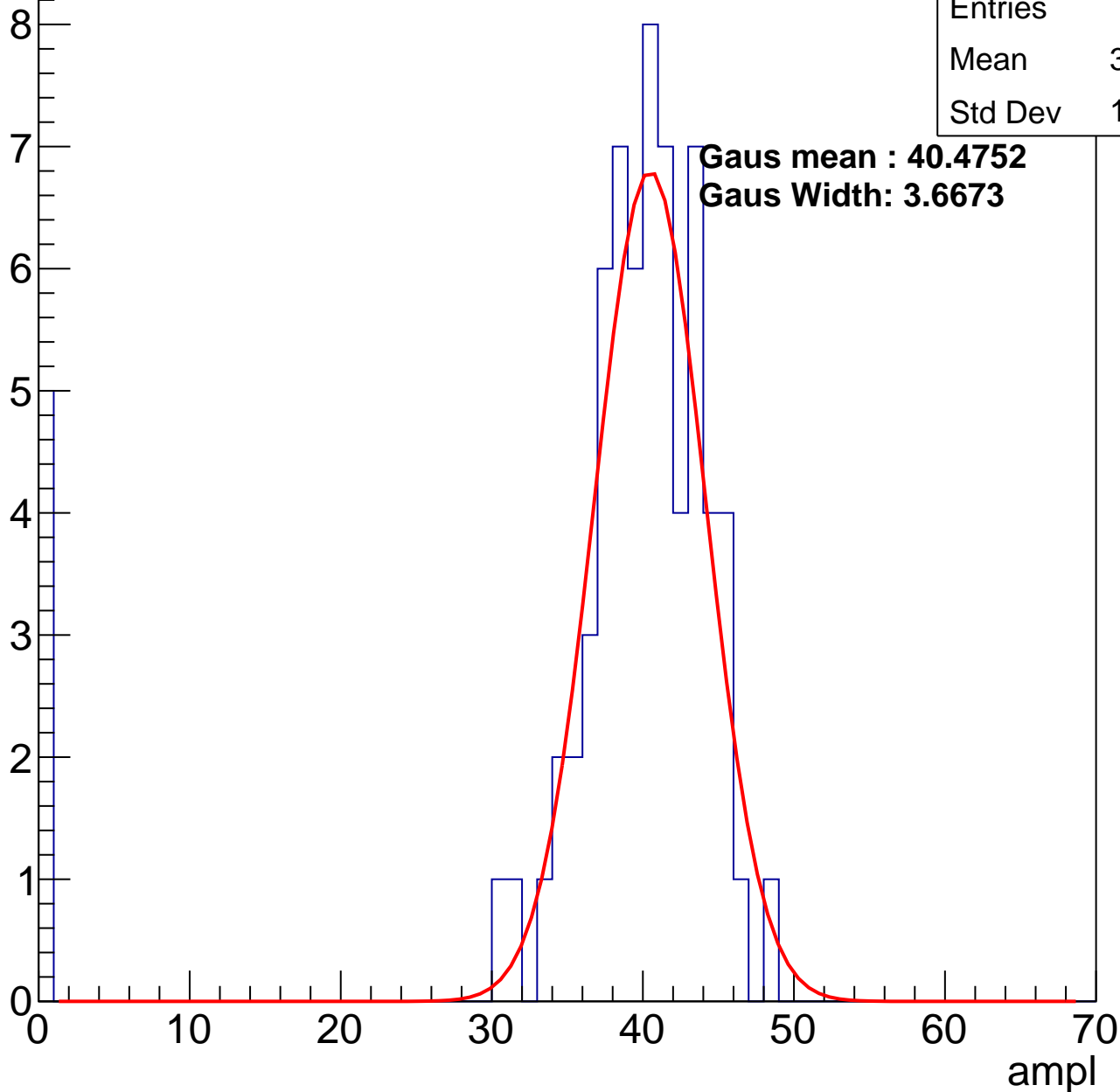
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.97
Std Dev	10.82

Gaus mean : 40.4752

Gaus Width: 3.6673

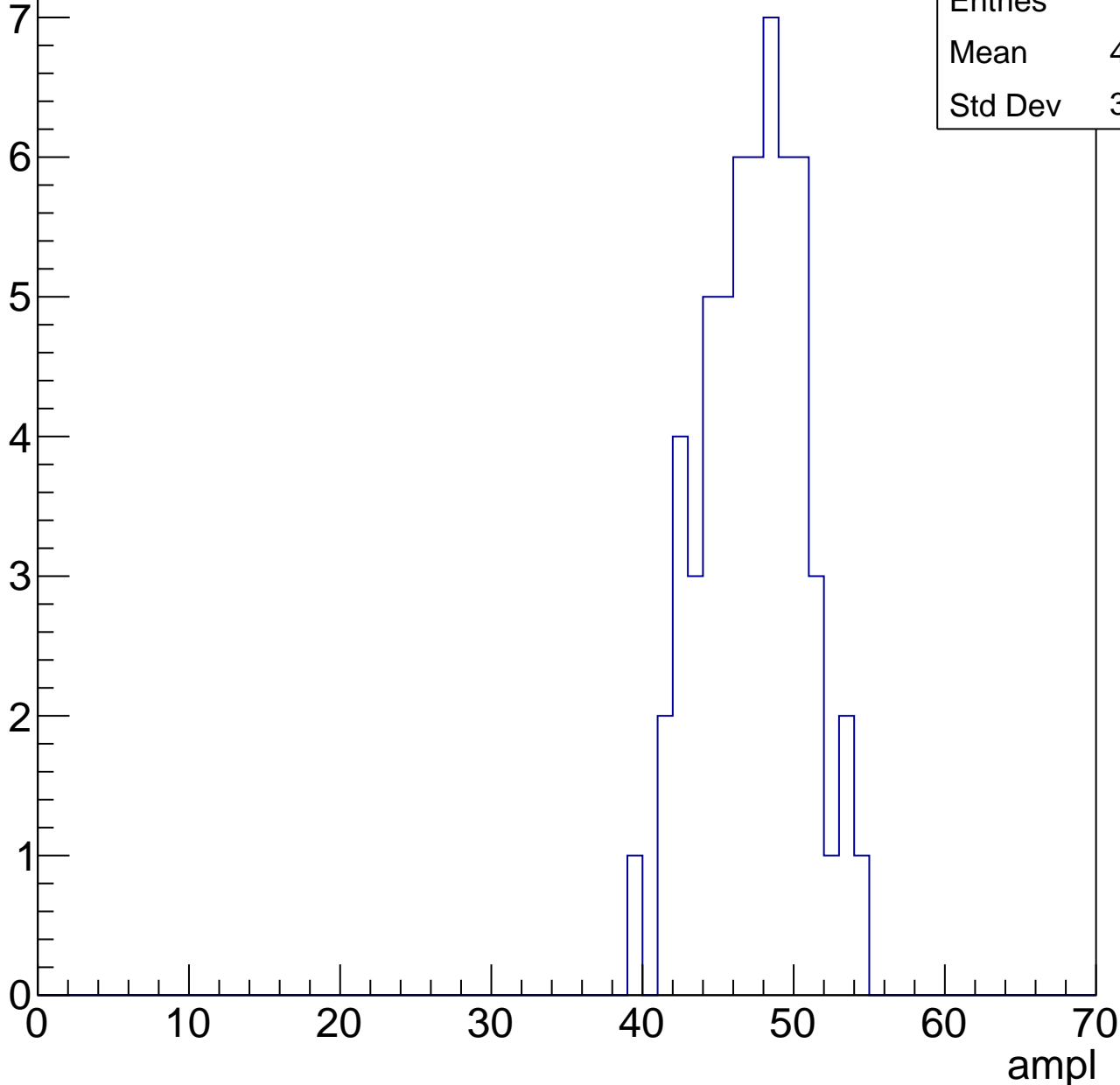


B1L103S, U2-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	46.83
Std Dev	3.302

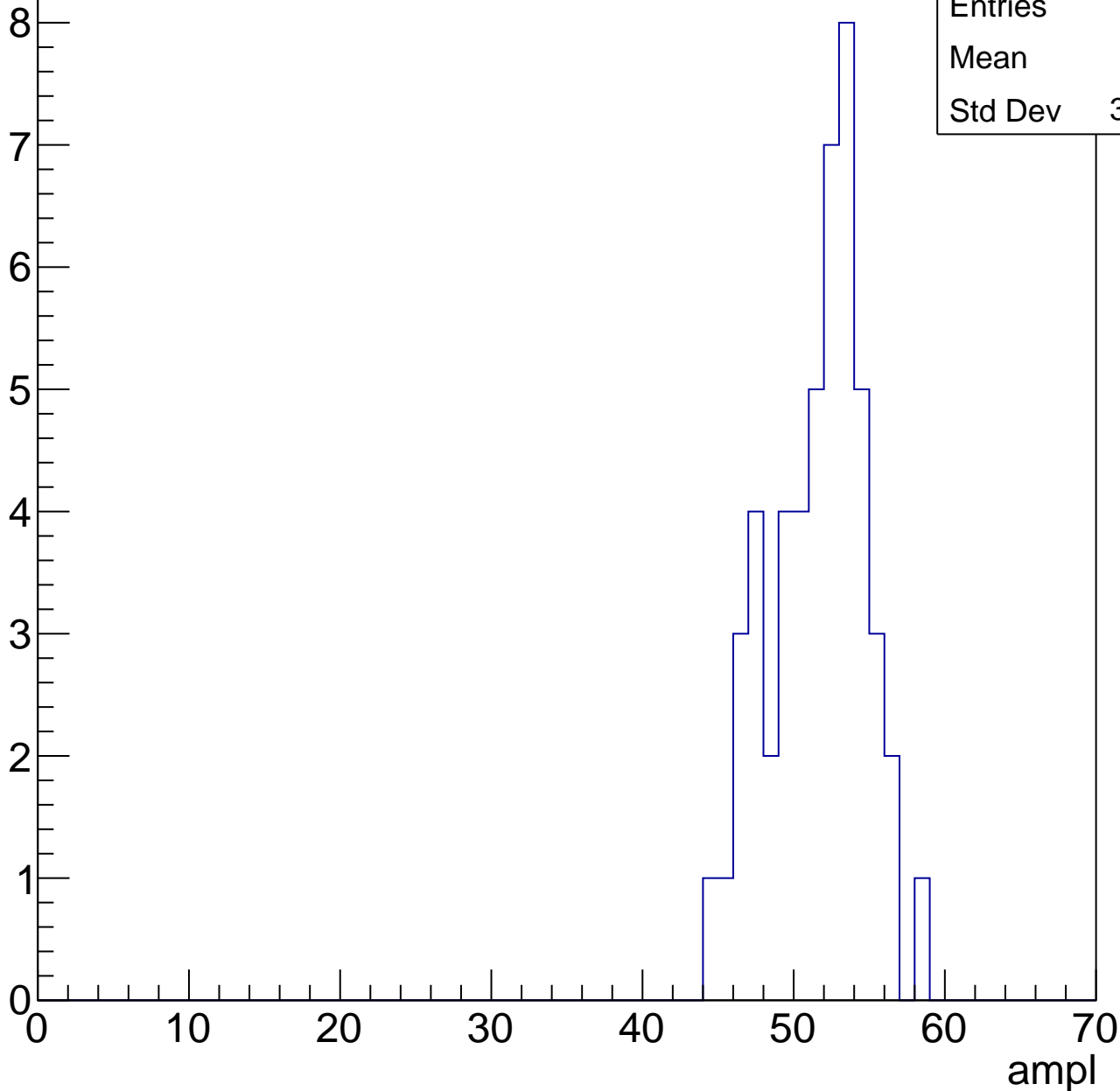


B1L103S, U2-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

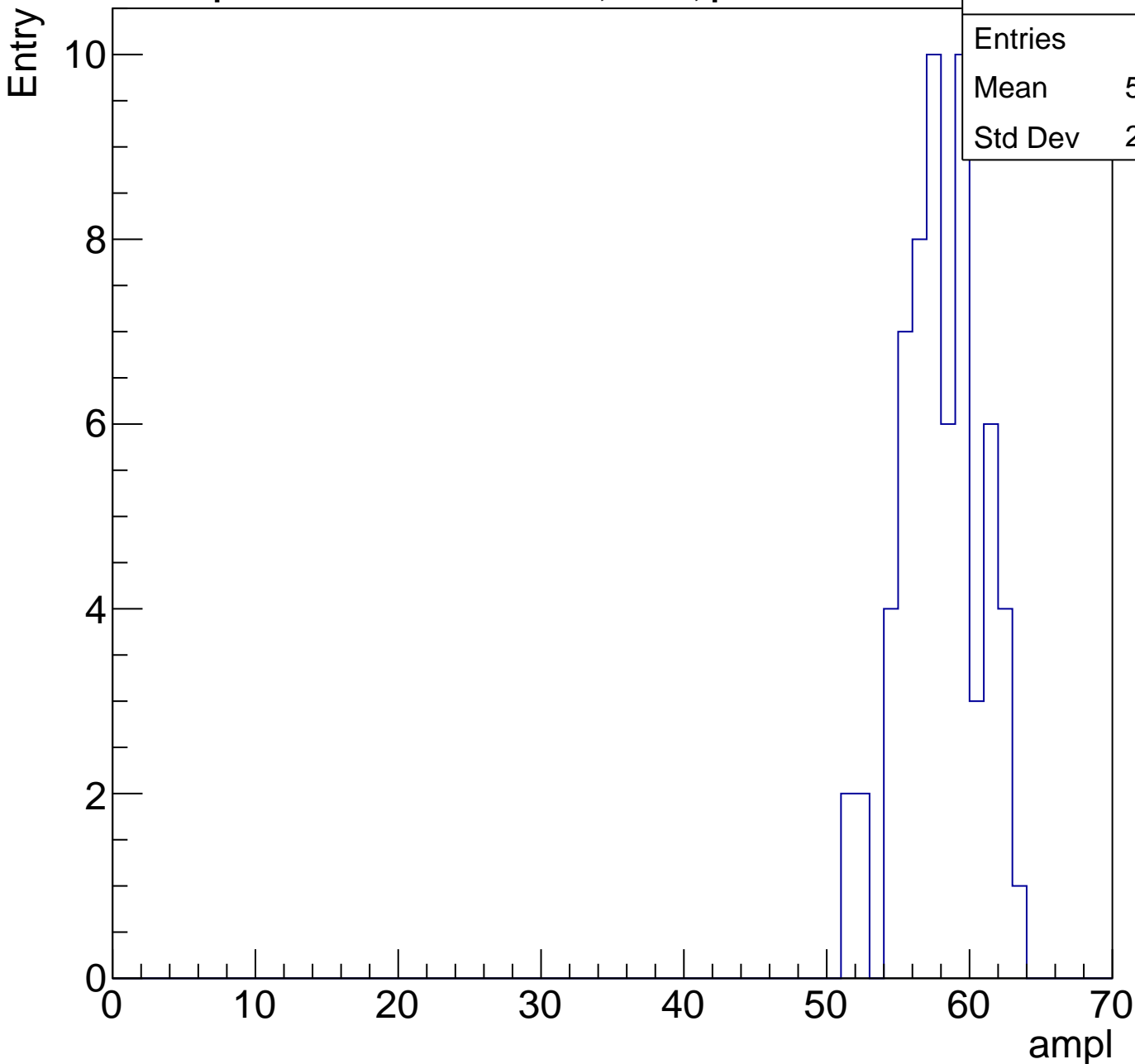
Entries	50
Mean	51.1
Std Dev	3.138



B1L103S, U2-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	57.46
Std Dev	2.776



B1L103S, U2-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	27
Mean	61.22
Std Dev	1.618

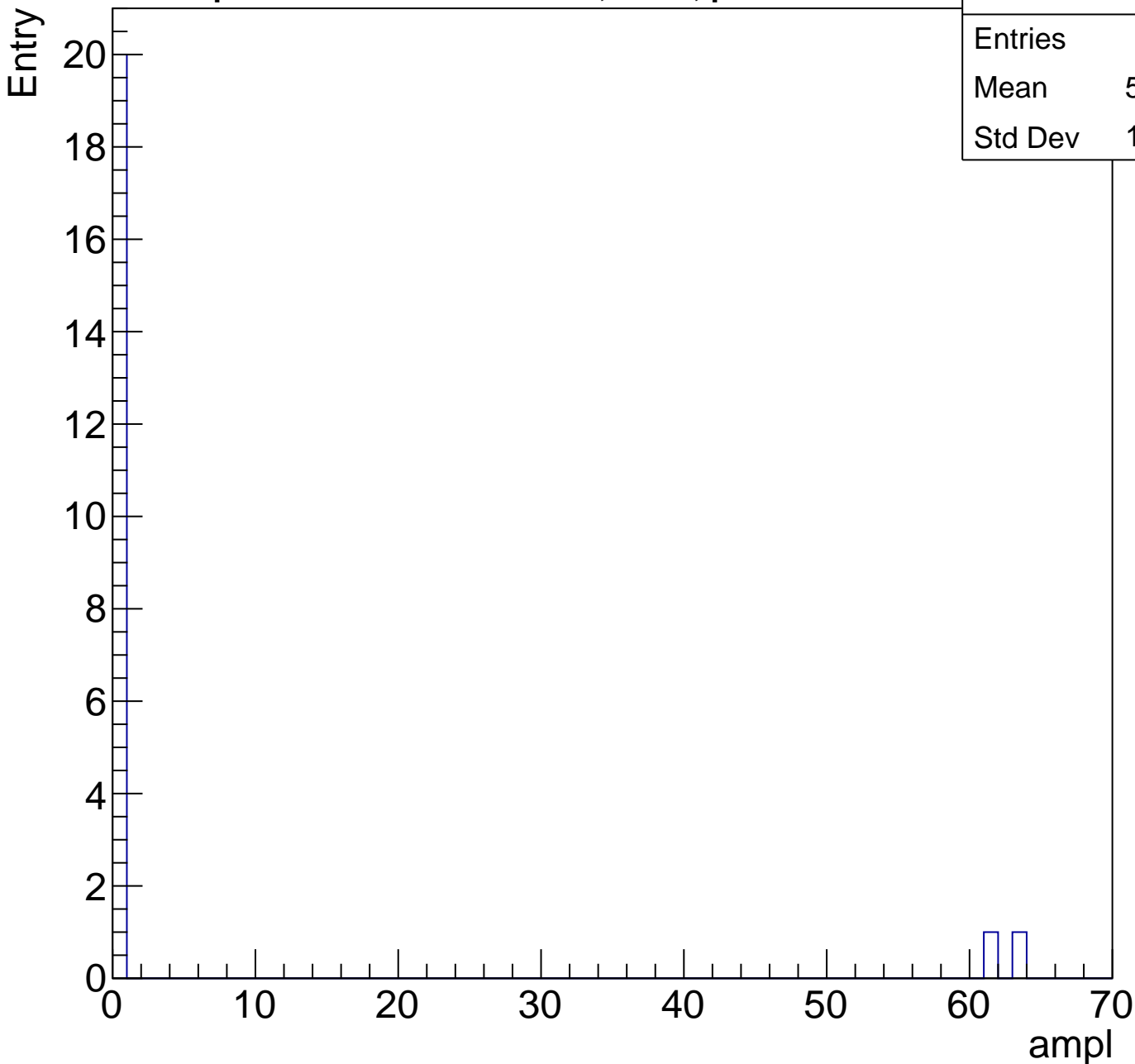
ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	5.636
Std Dev	17.83



B1L103S, U2-ch31, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	24.56
Std Dev	10.09

Gaus mean : 28.4269

Gaus Width: 3.3101

Entry

10

8

6

4

2

0

0

10

20

30

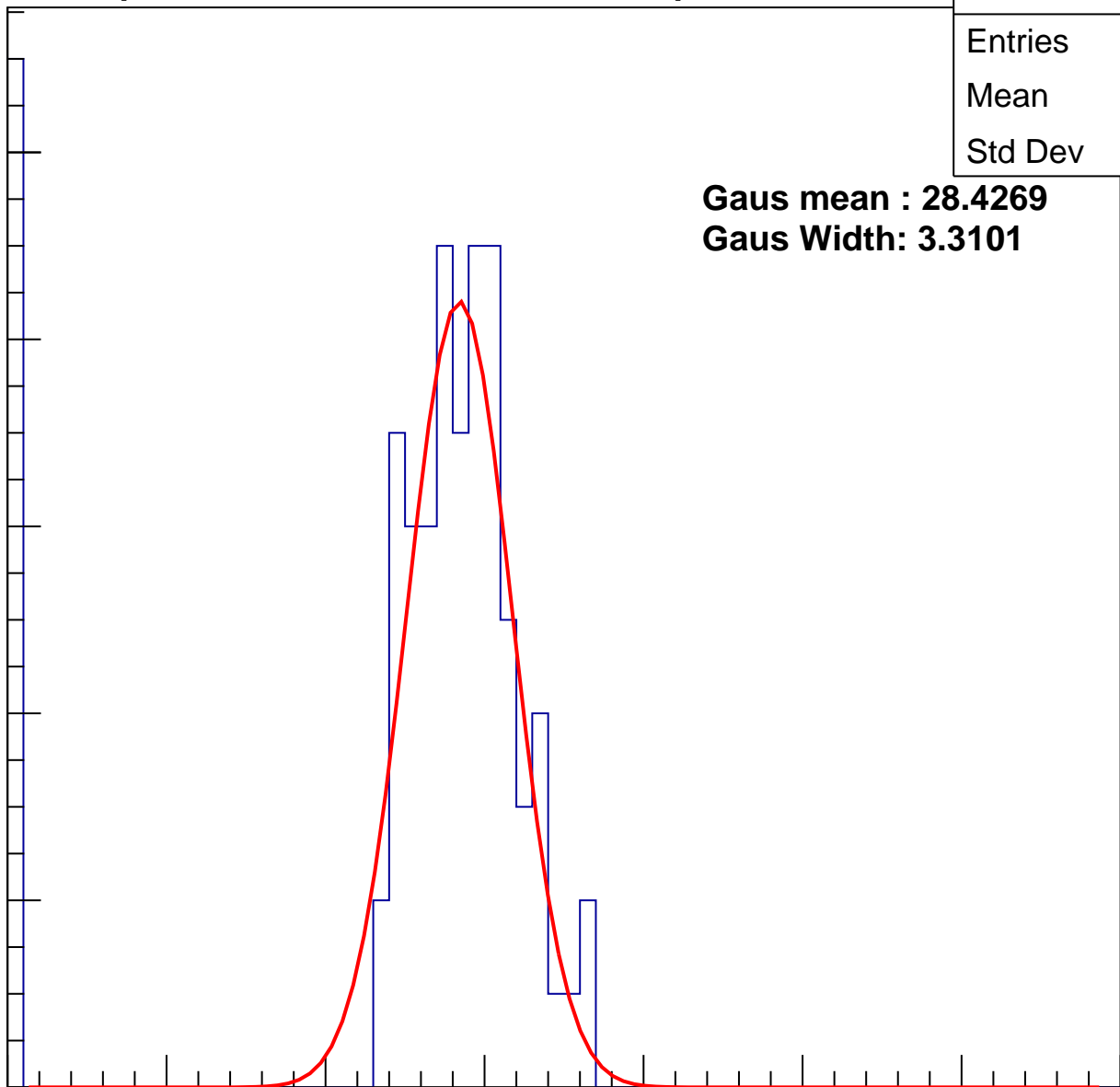
40

50

60

70

ampl



B1L103S, U2-ch31, adc1

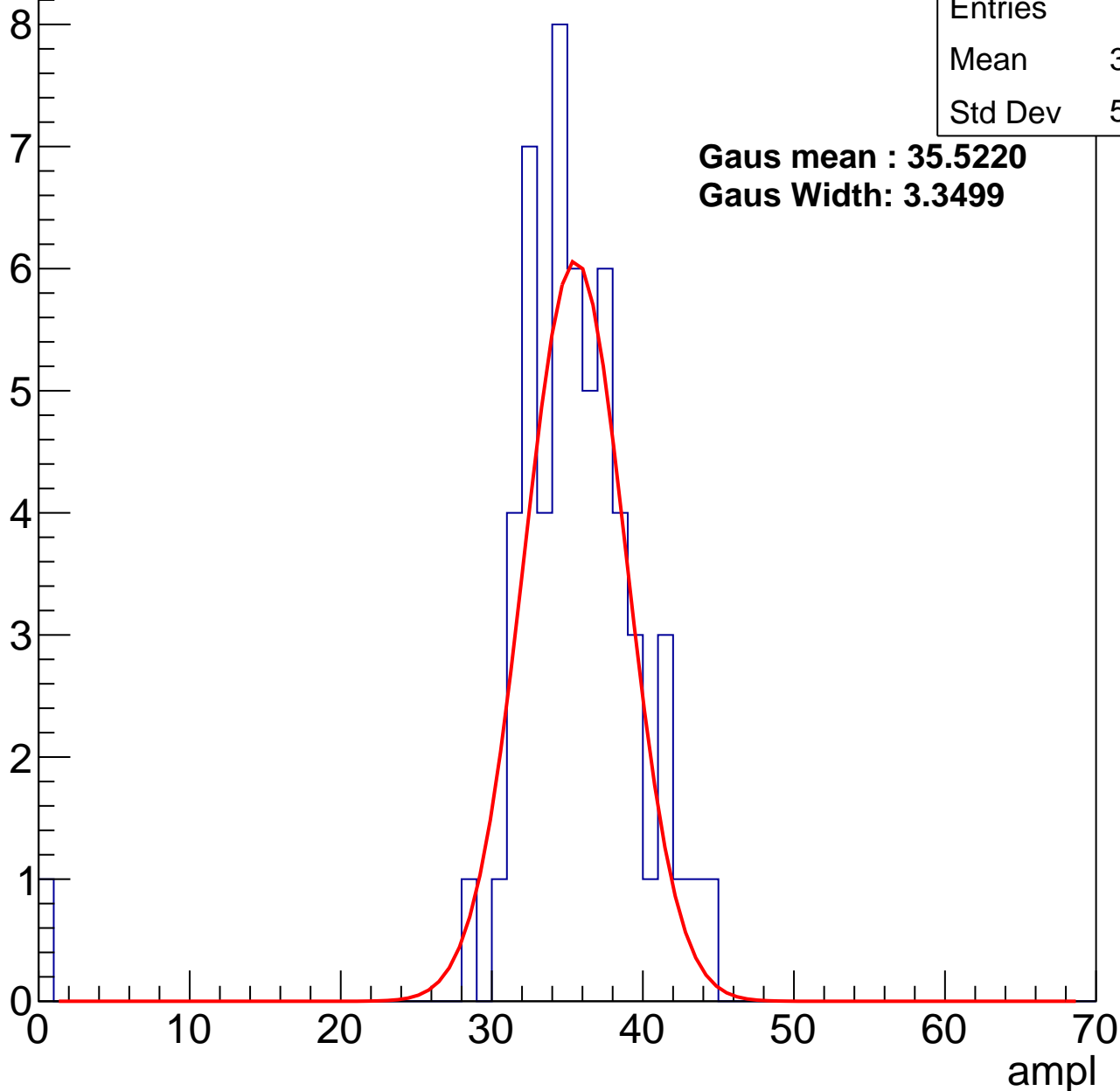
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	34.79
Std Dev	5.754

Gaus mean : 35.5220

Gaus Width: 3.3499



B1L103S, U2-ch31, adc2

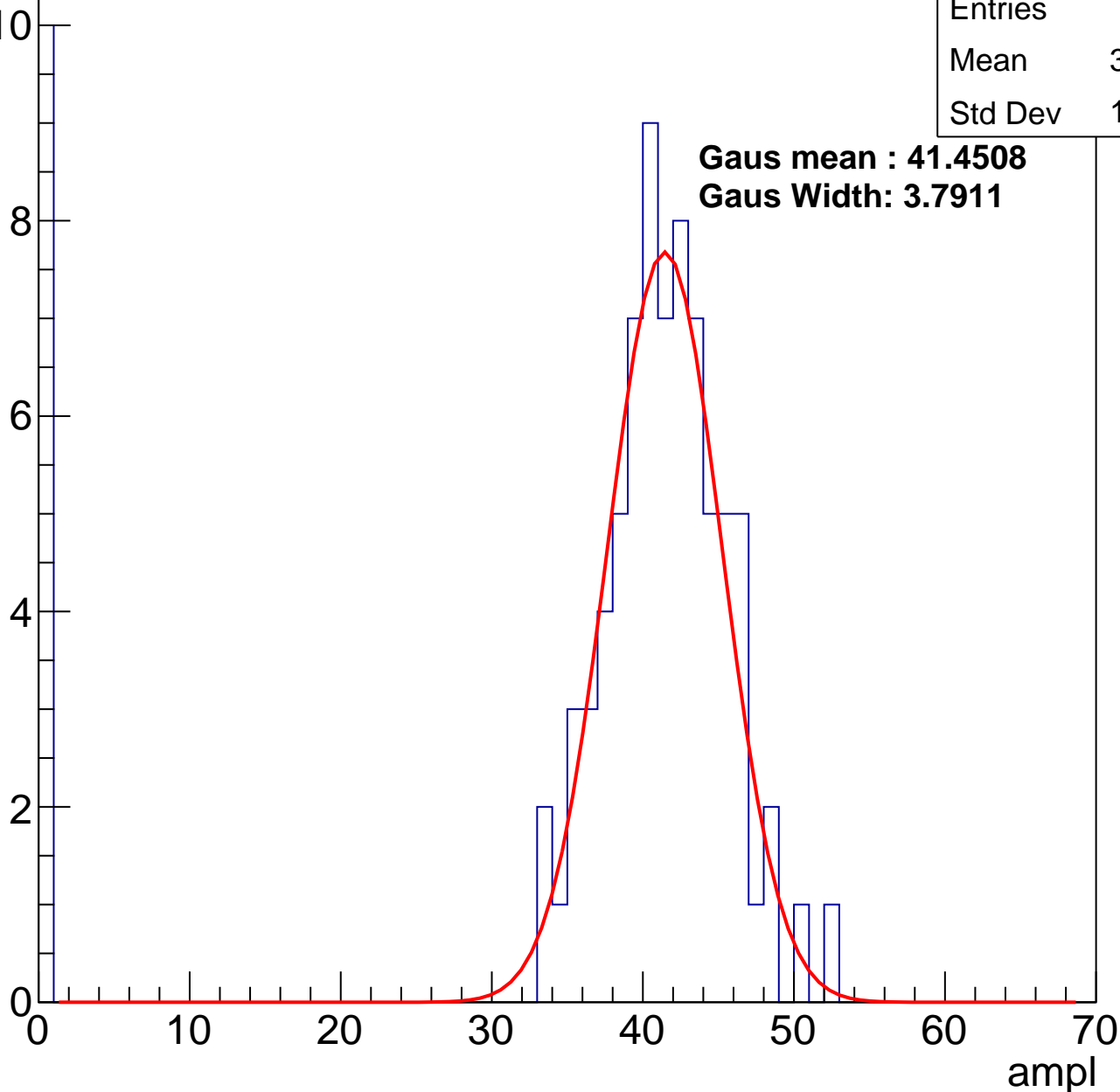
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	36.37
Std Dev	13.68

Gaus mean : 41.4508

Gaus Width: 3.7911

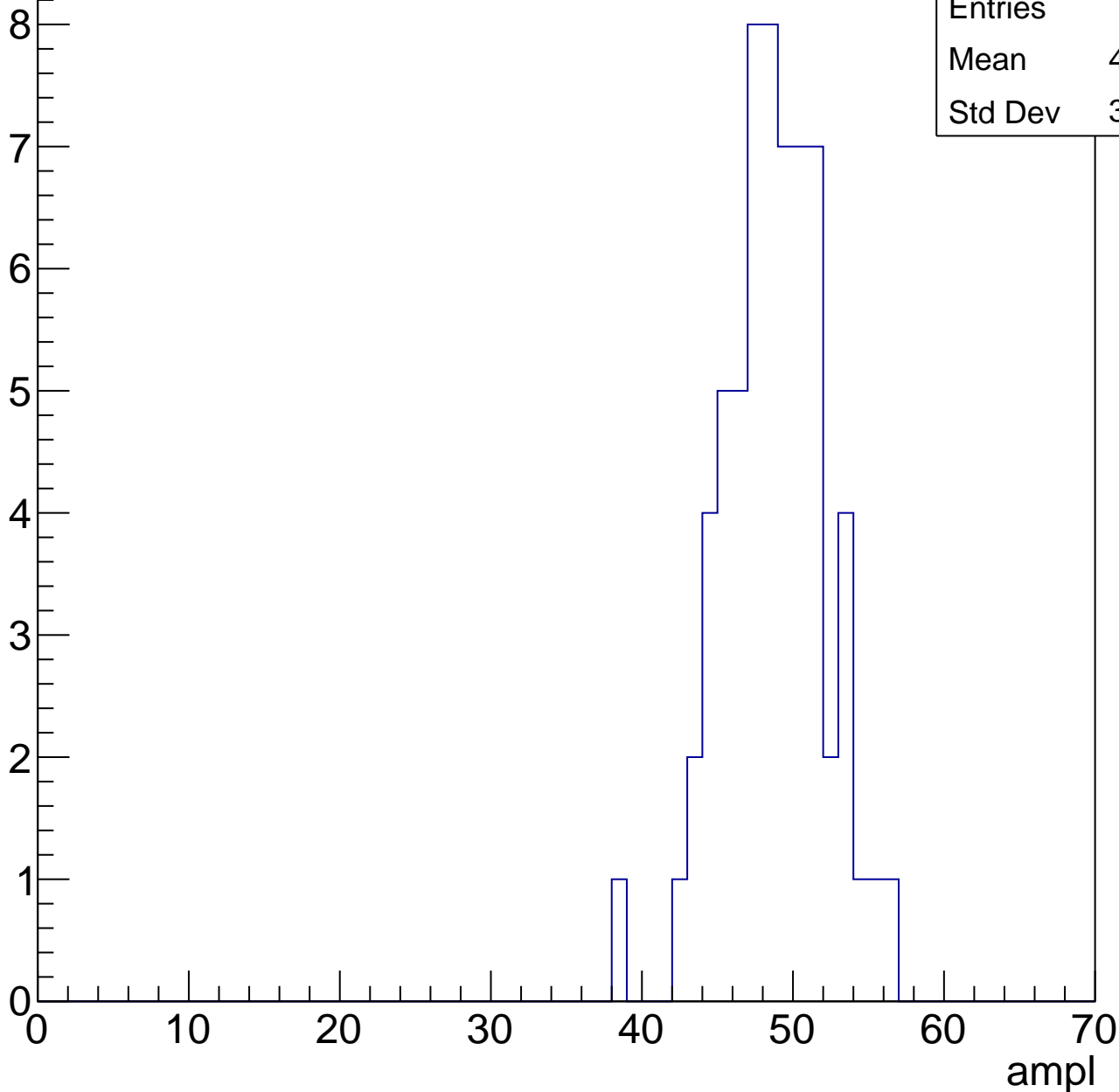


B1L103S, U2-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	48.25
Std Dev	3.302

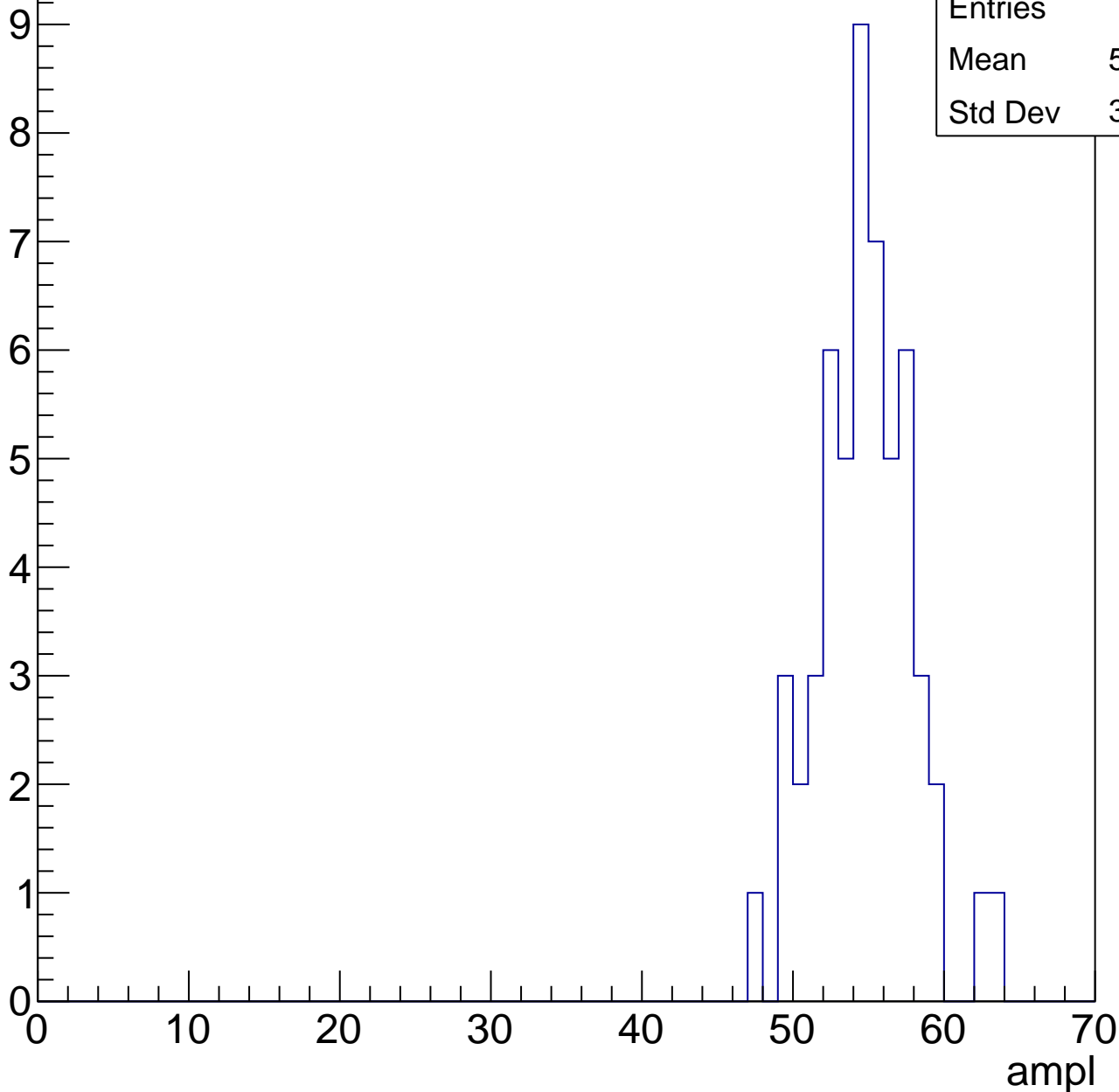


B1L103S, U2-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	54.33
Std Dev	3.133



B1L103S, U2-ch31, adc5

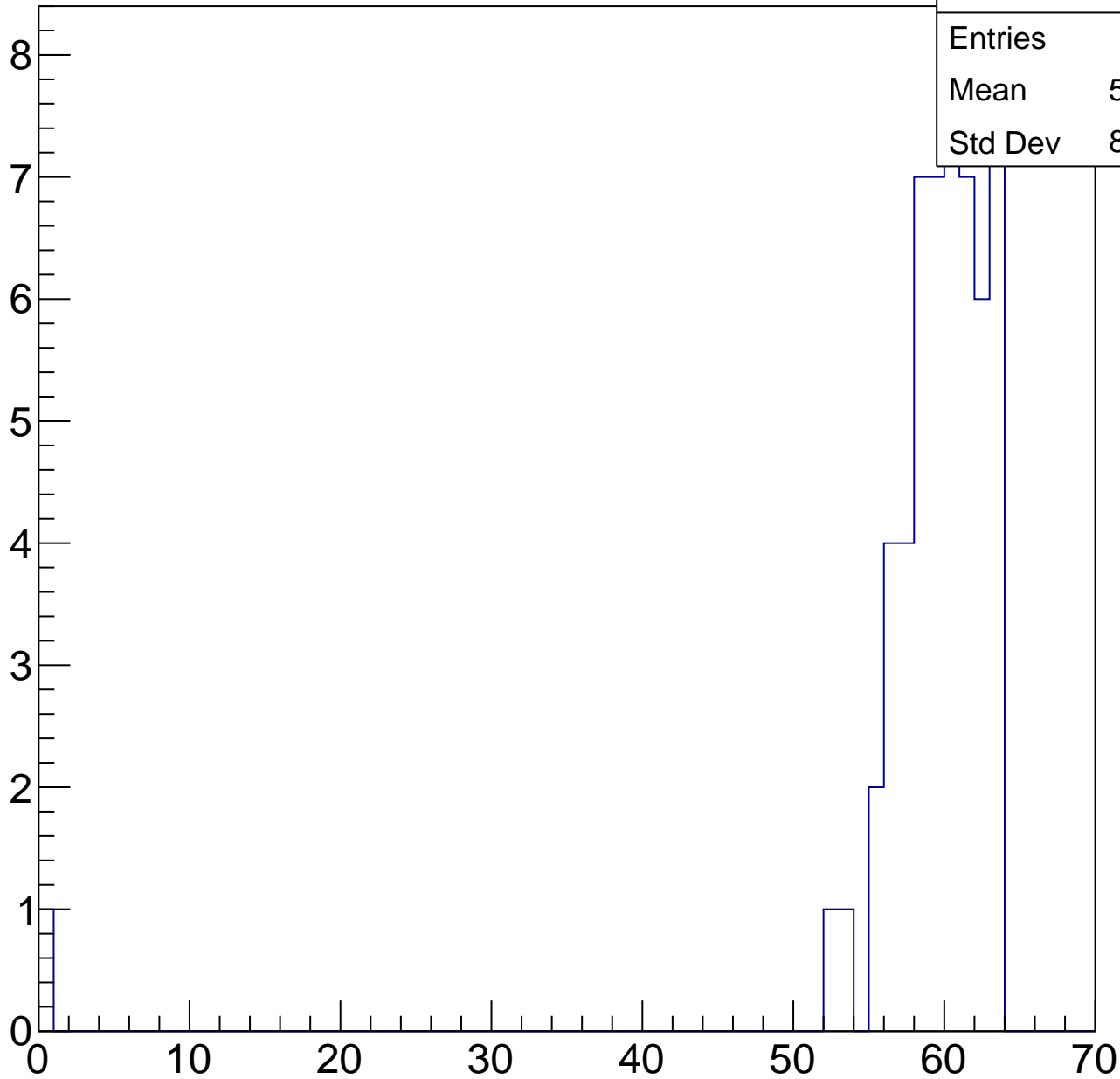
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.38
Std Dev	8.295

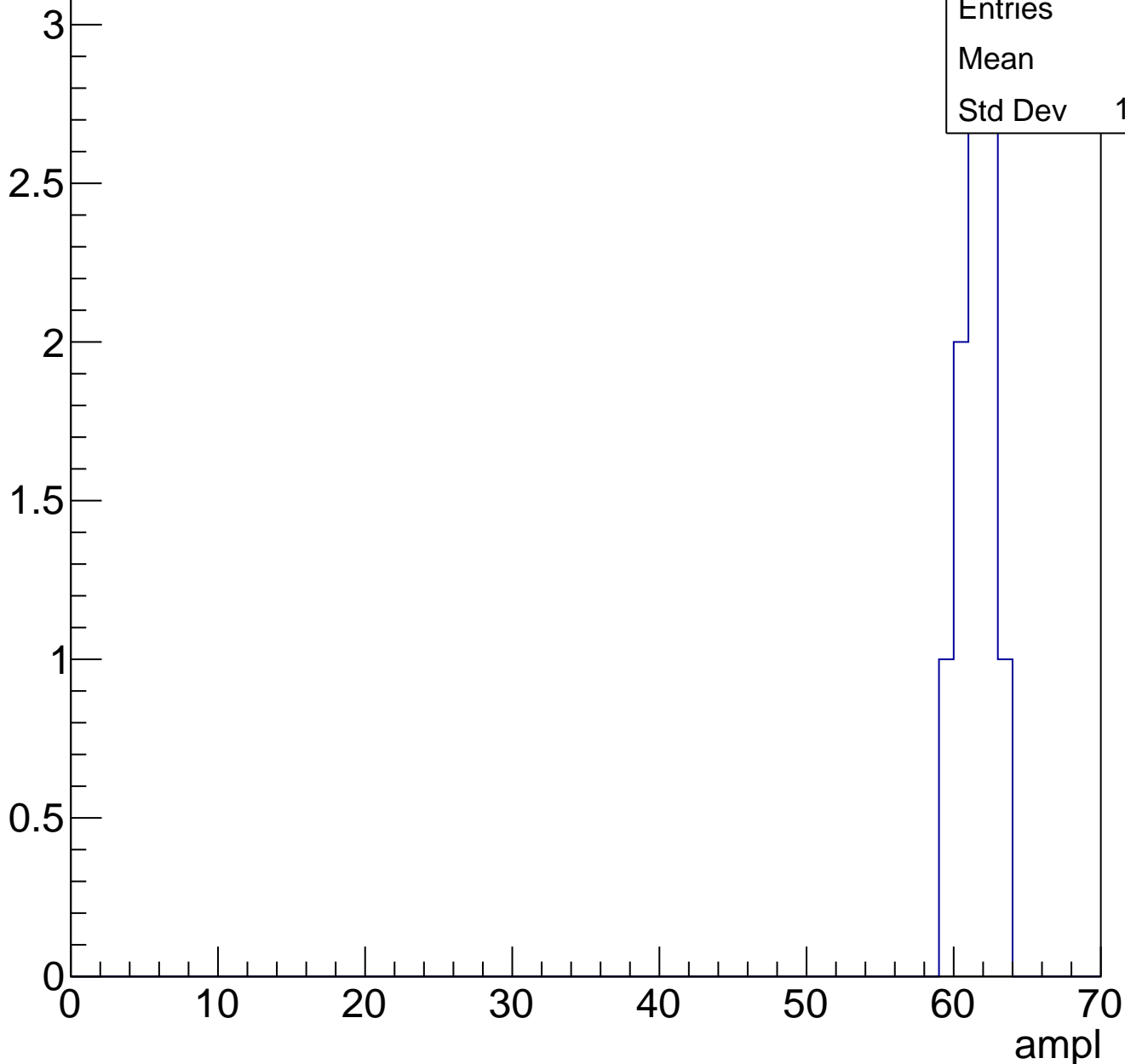
ampl



B1L103S, U2-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

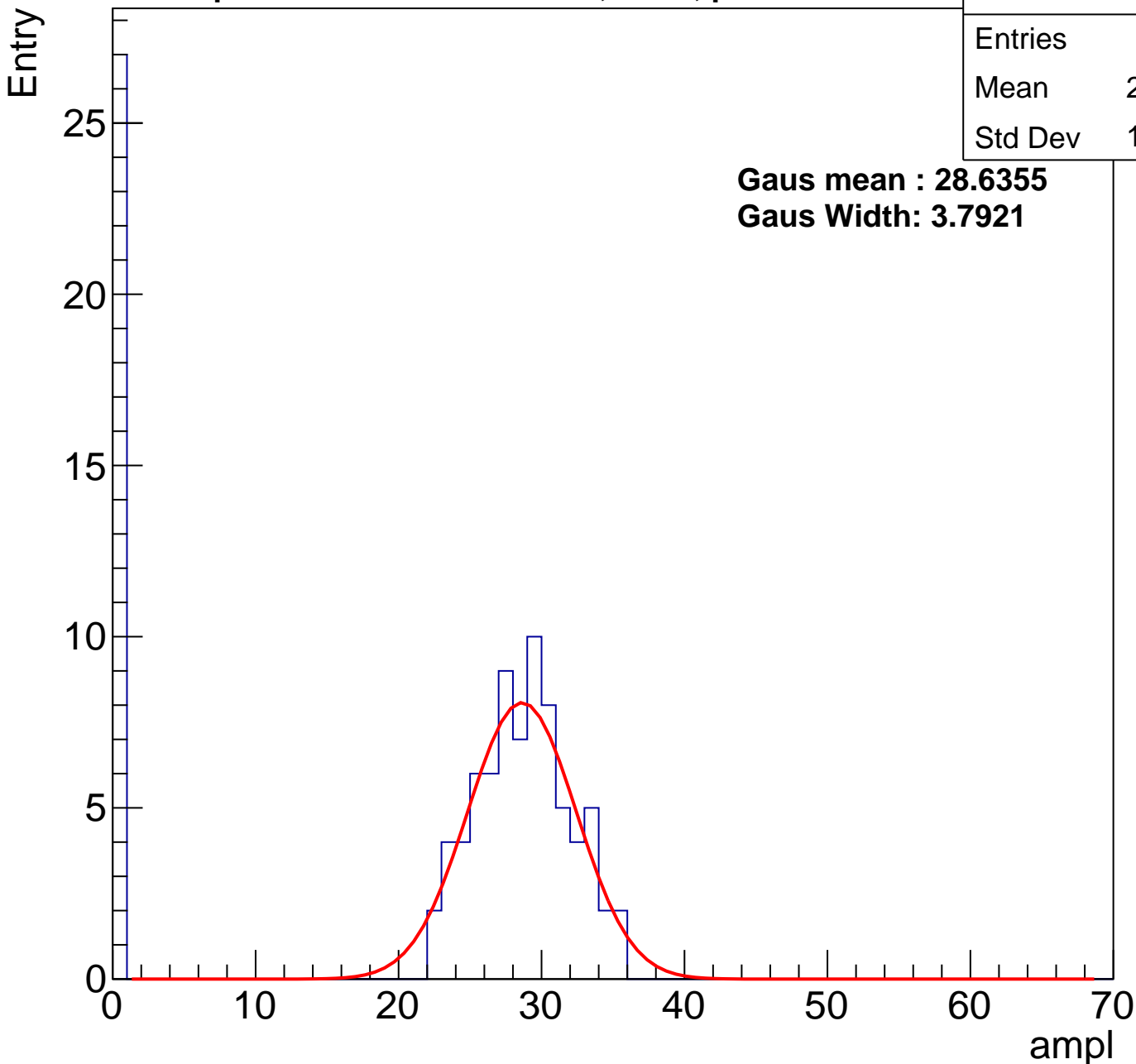
B1L103S, U2-ch32, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	20.72
Std Dev	12.82

Gaus mean : 28.6355

Gaus Width: 3.7921



B1L103S, U2-ch32, adc1

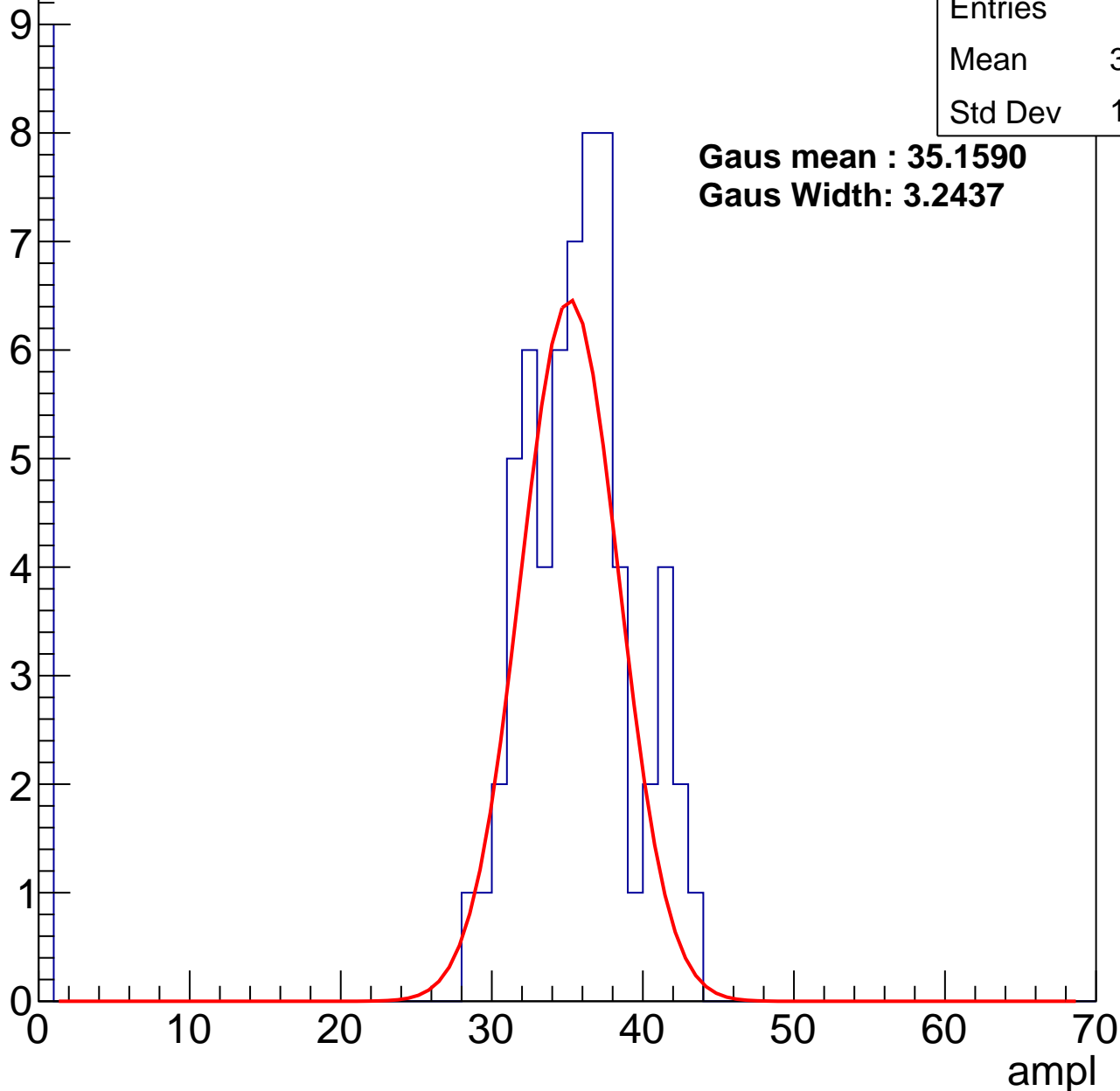
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30.86
Std Dev	12.19

Gaus mean : 35.1590

Gaus Width: 3.2437



B1L103S, U2-ch32, adc2

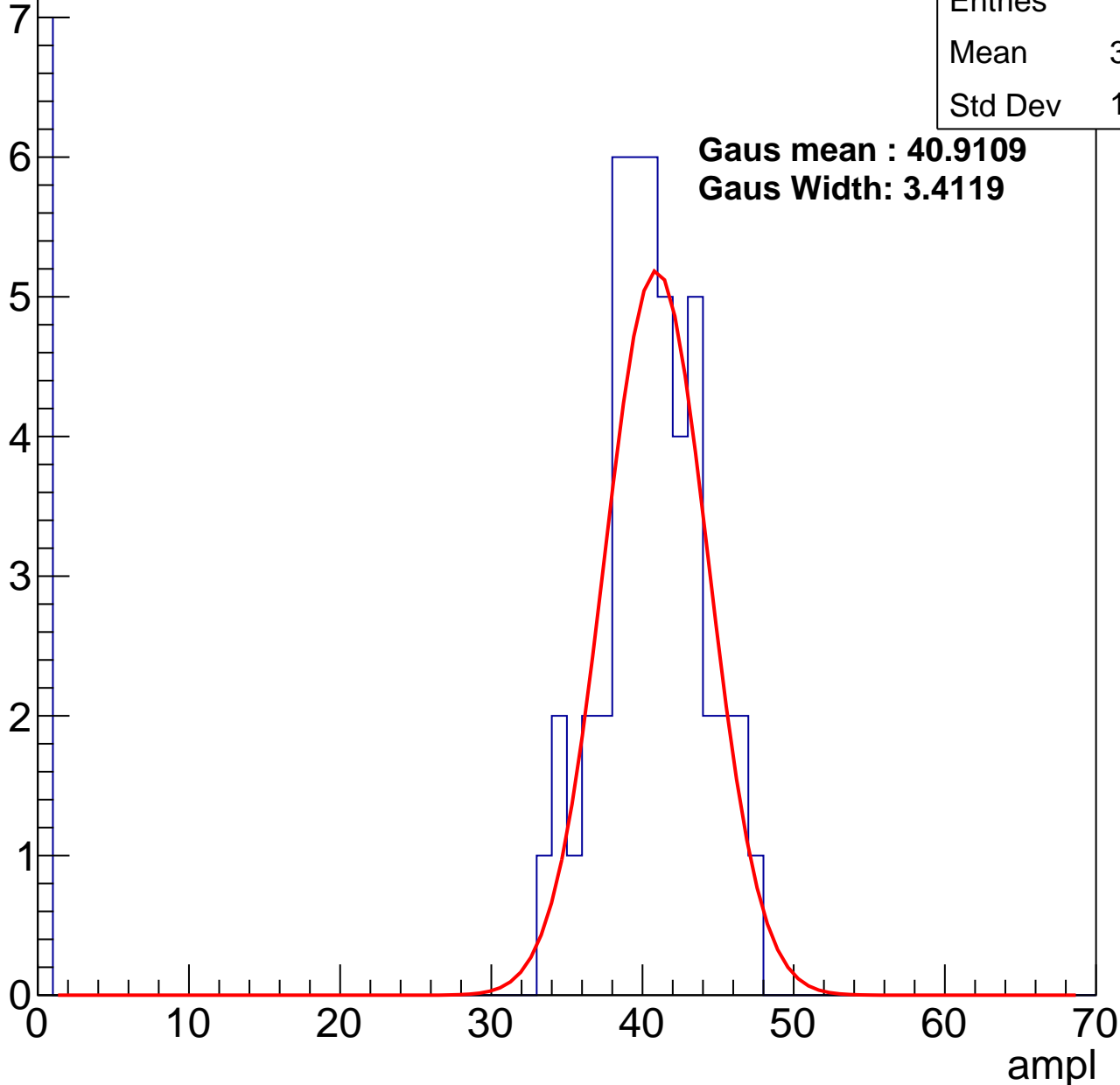
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	34.98
Std Dev	13.83

Gaus mean : 40.9109

Gaus Width: 3.4119

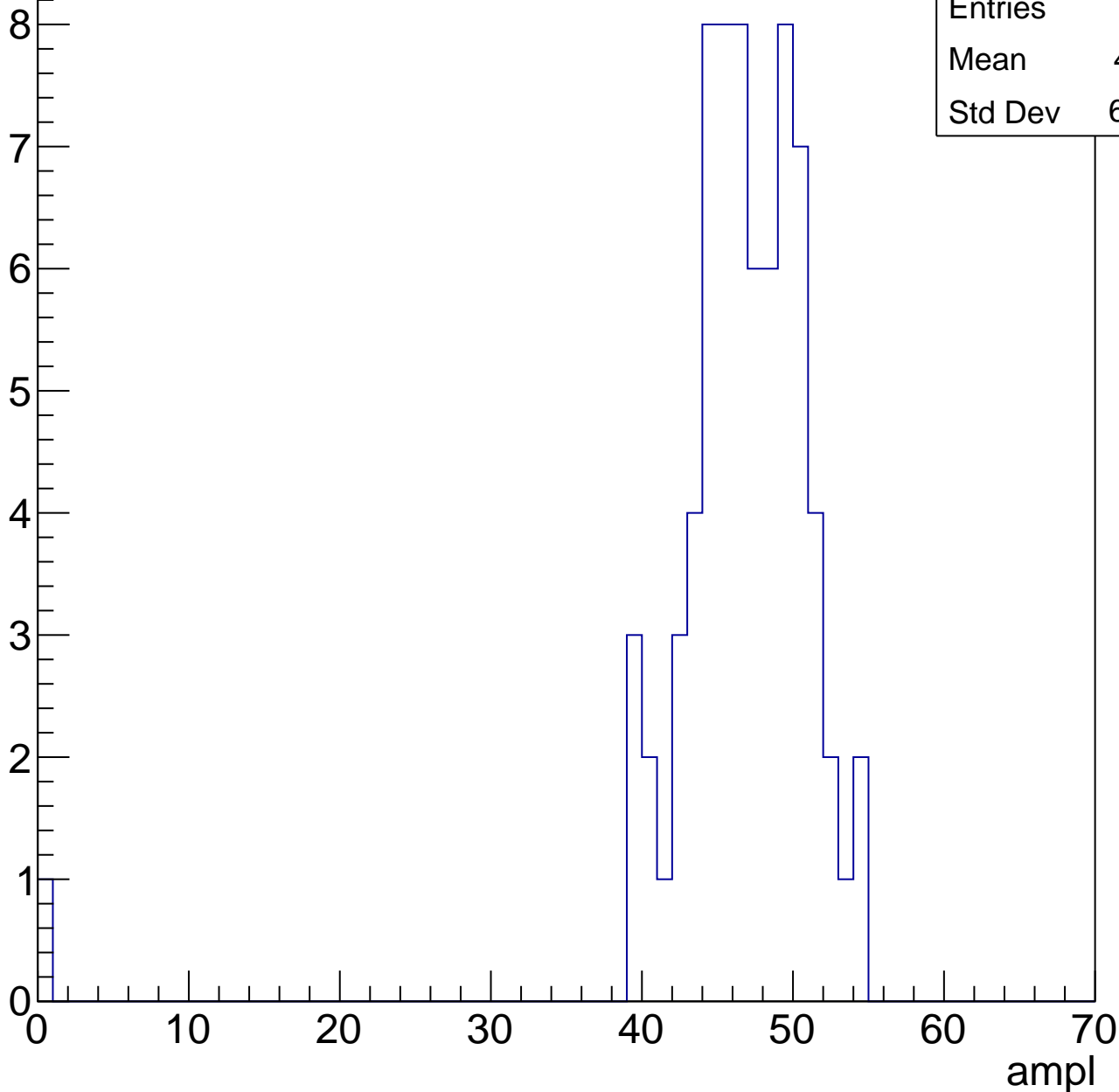


B1L103S, U2-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	45.91
Std Dev	6.416

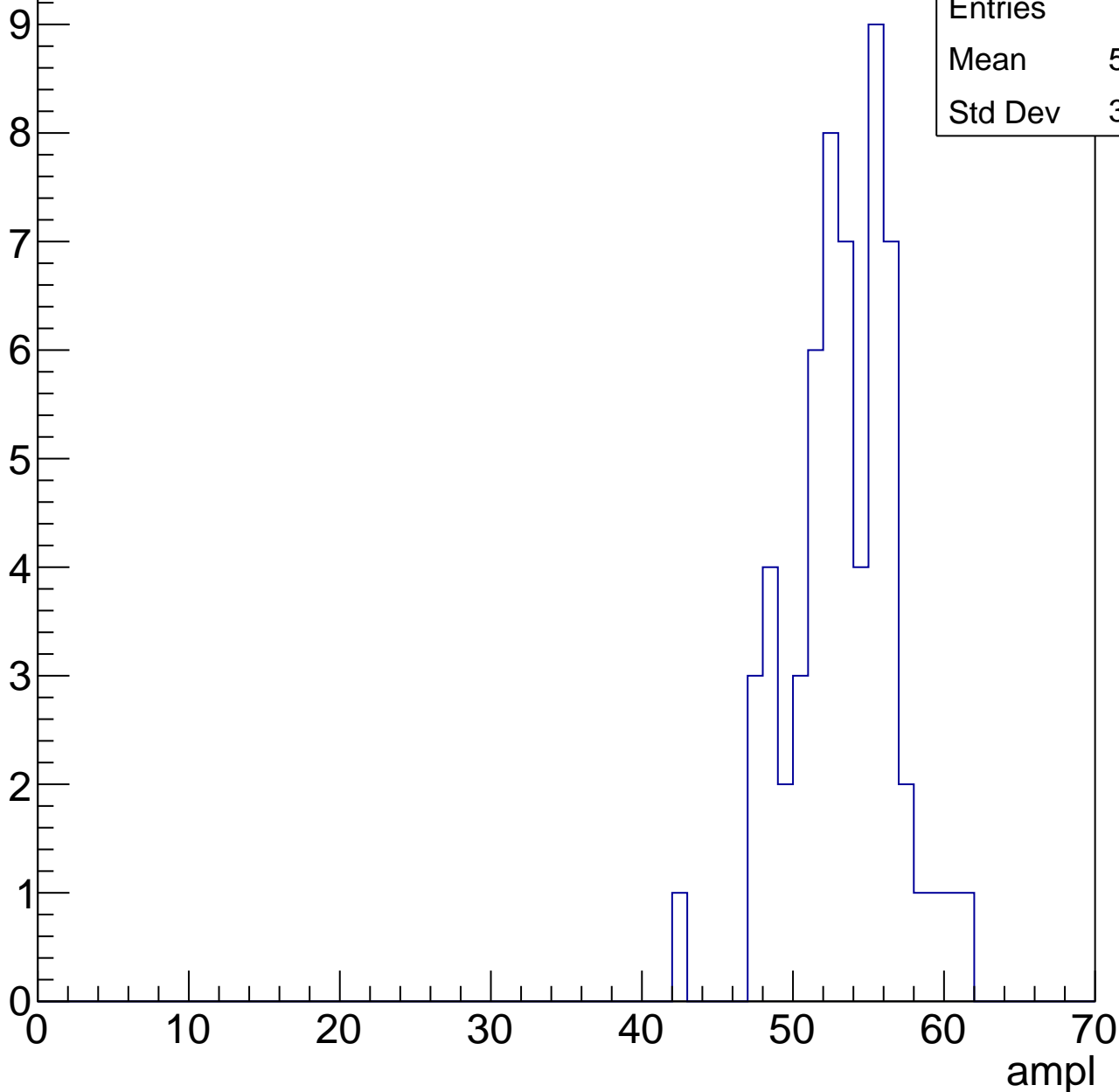


B1L103S, U2-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

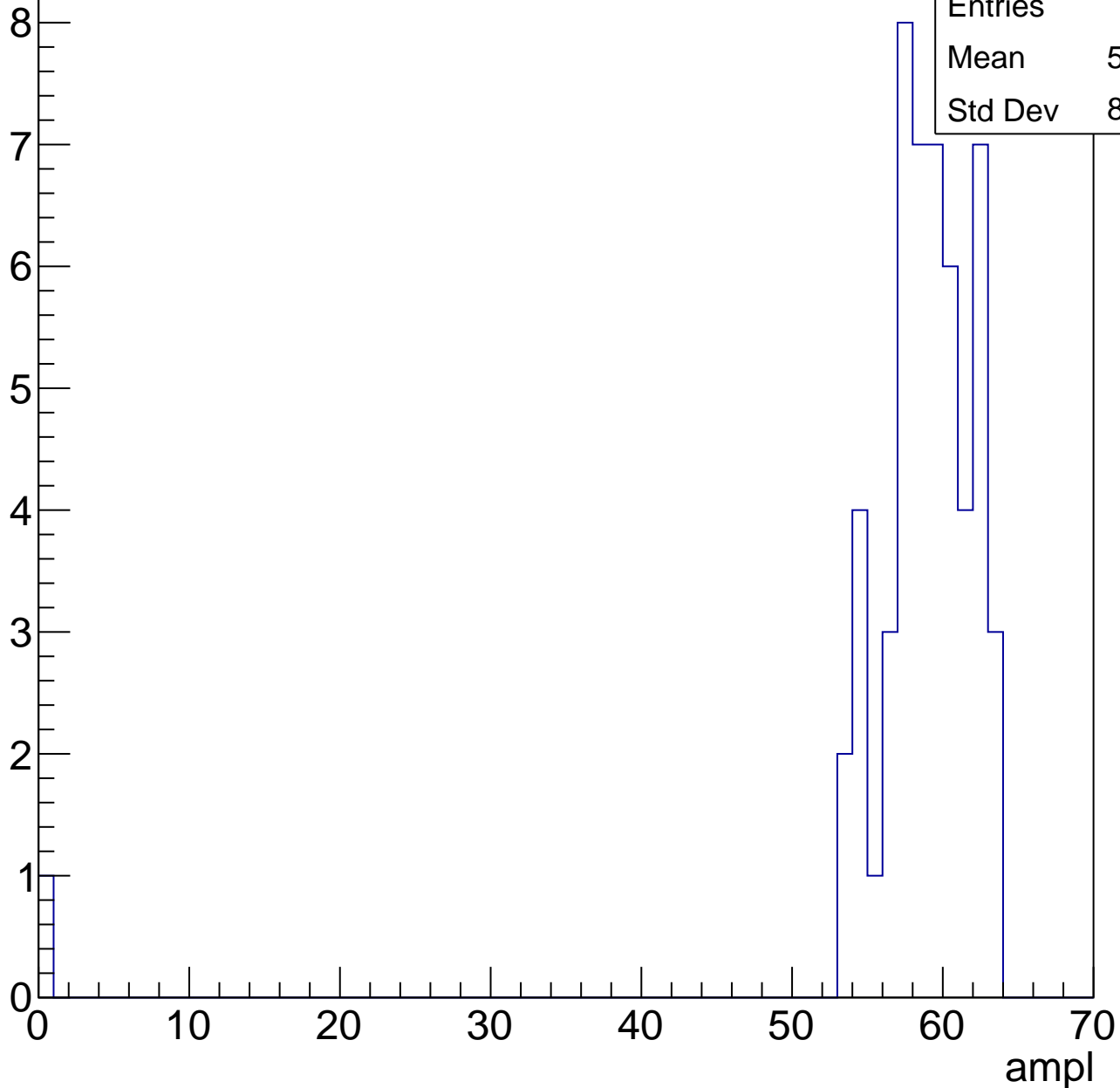
Entries	60
Mean	52.85
Std Dev	3.482



B1L103S, U2-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

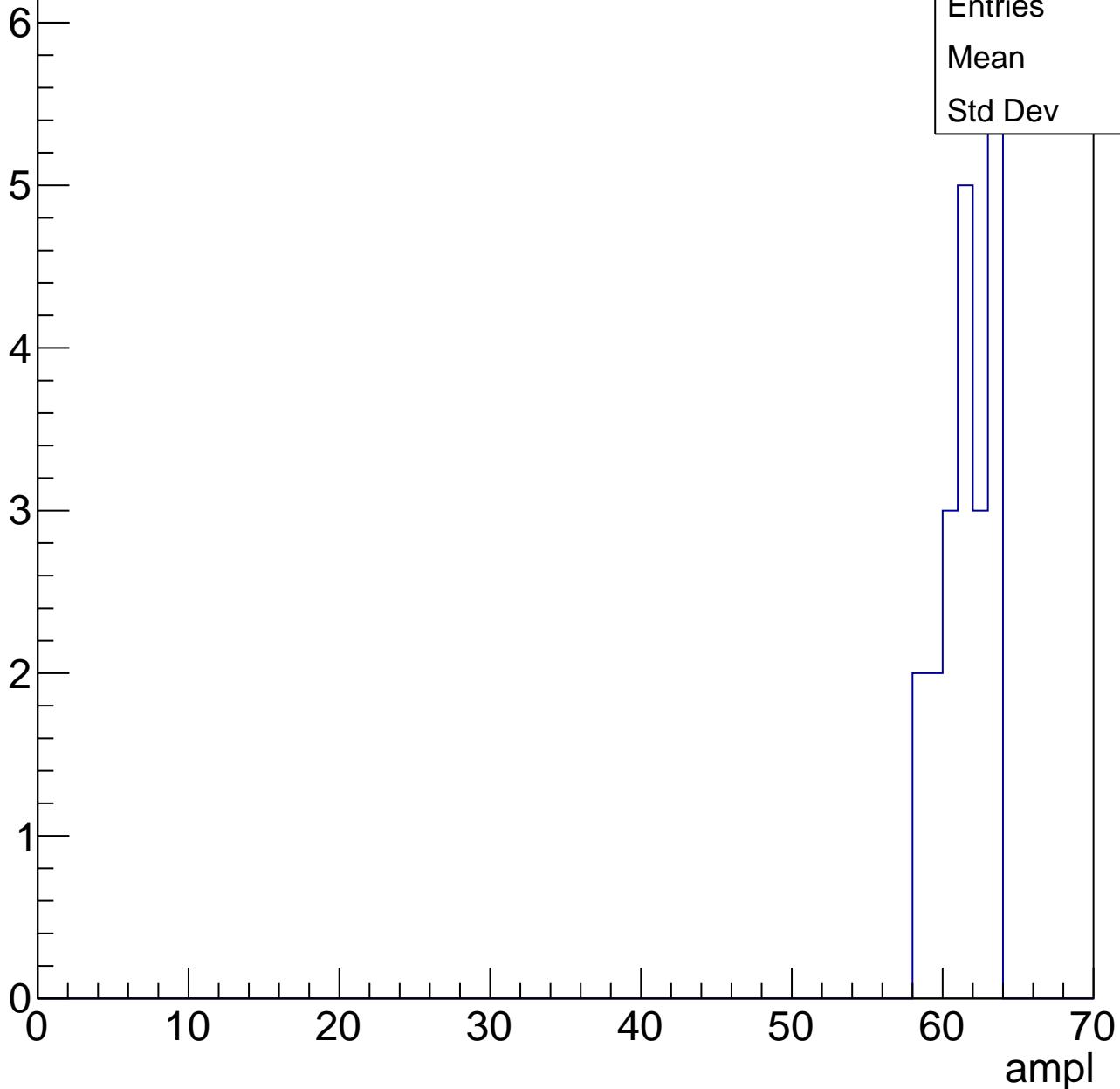


B1L103S, U2-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	61.1
Std Dev	1.63

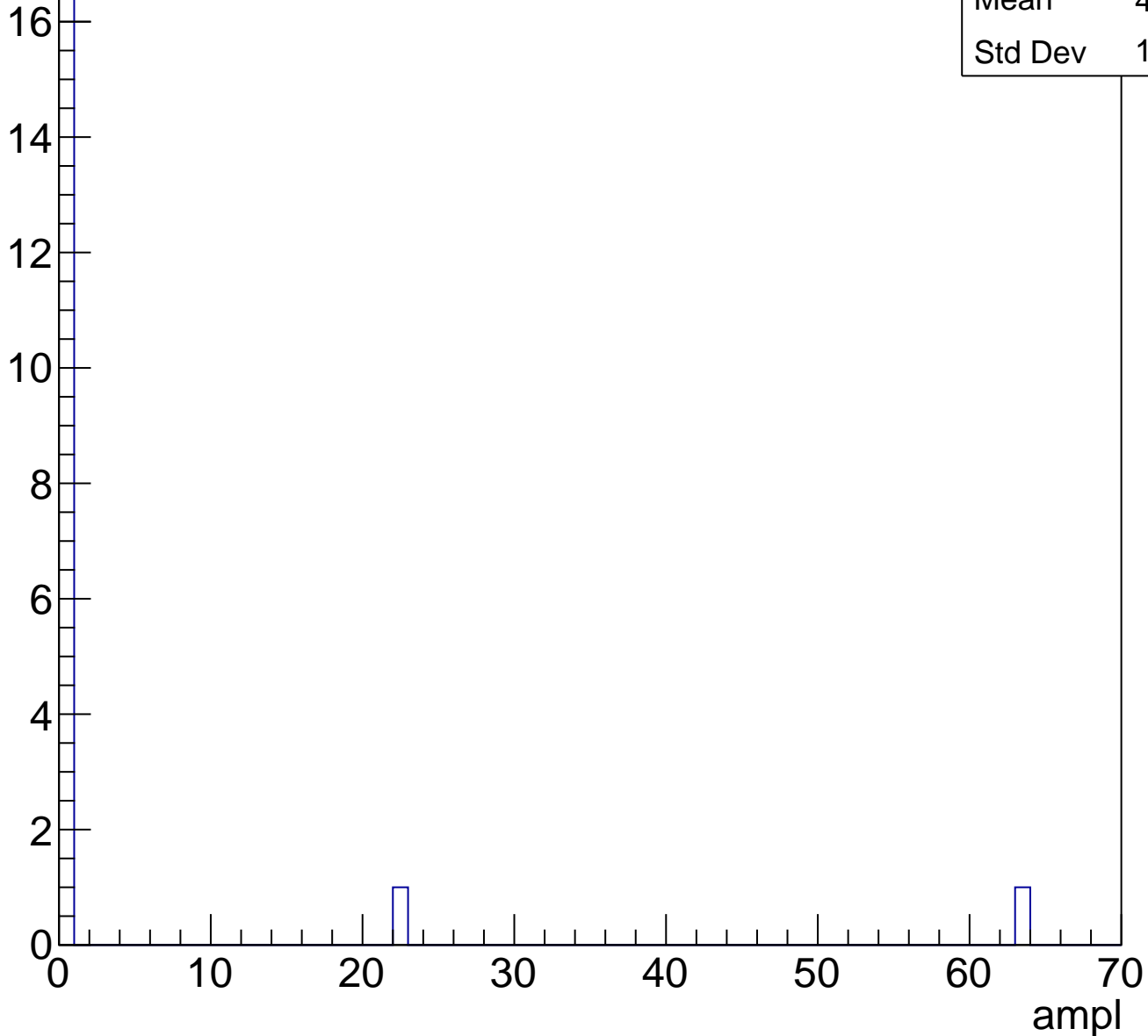


B1L103S, U2-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.474
Std Dev	14.64

Entry



B1L103S, U2-ch33, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	23.76
Std Dev	9.276

Gaus mean : 27.4457

Gaus Width: 4.0859

Entry

10

8

6

4

2

0

0

10

20

30

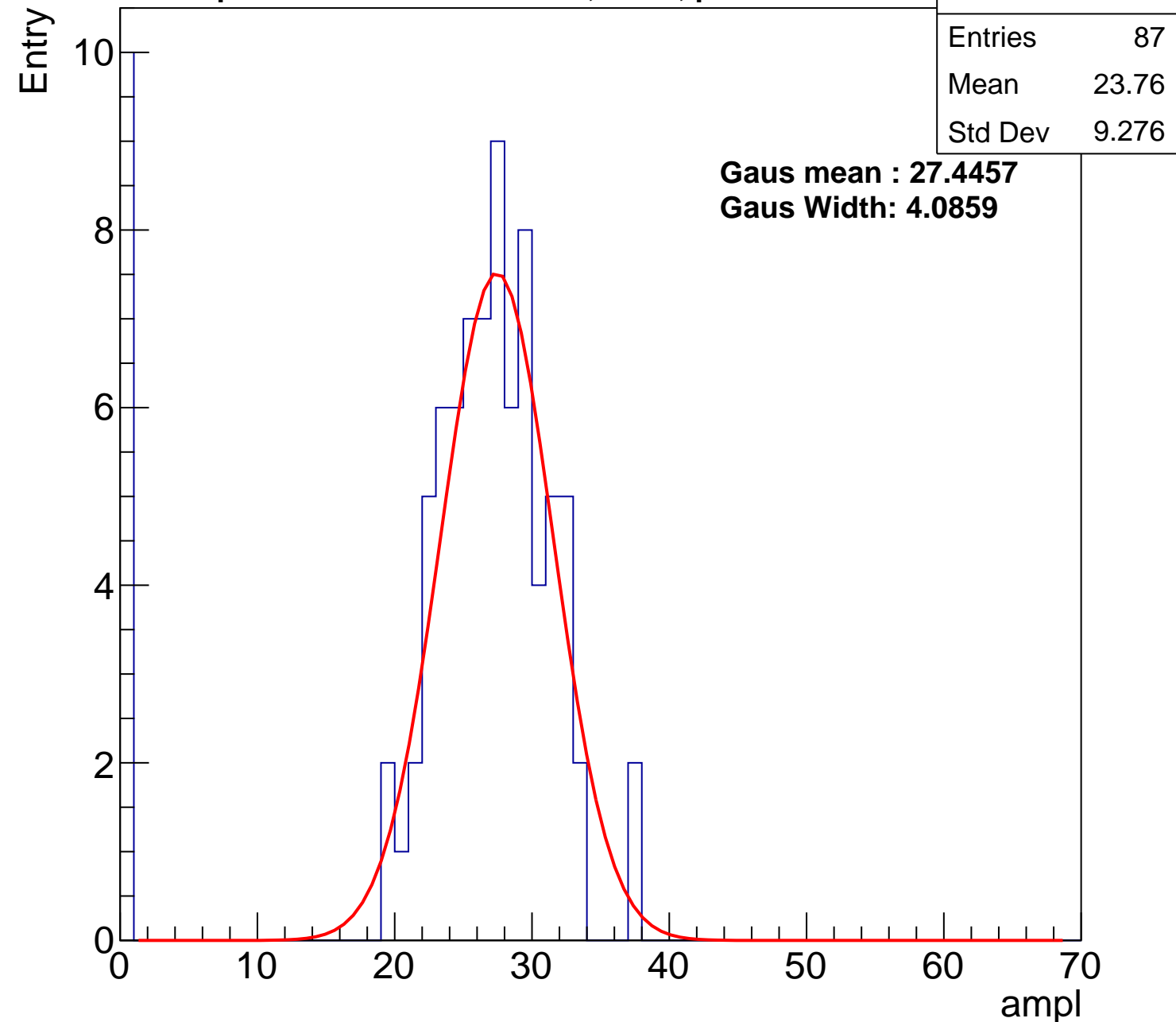
40

50

60

70

ampl



B1L103S, U2-ch33, adc1

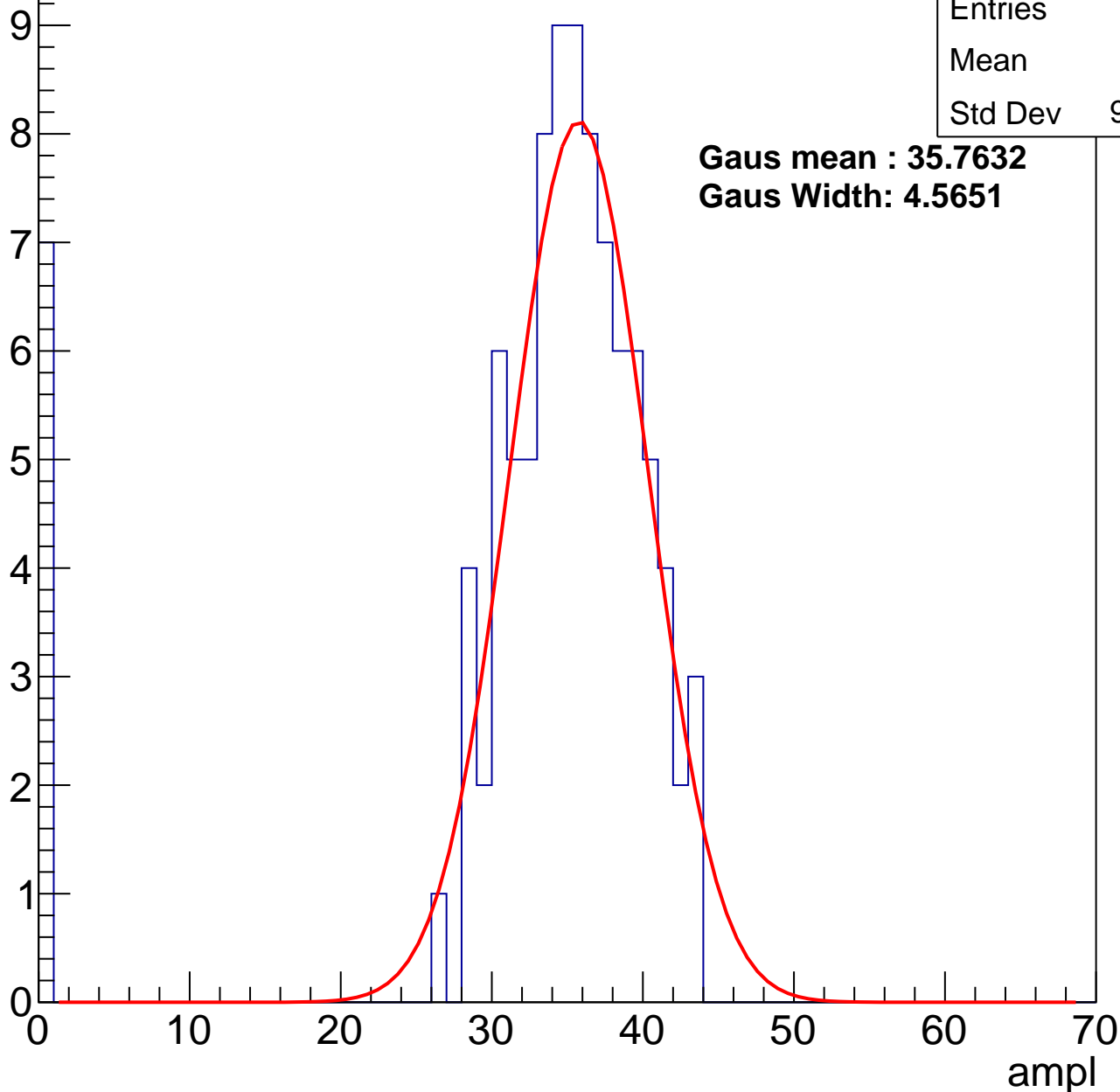
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	97
Mean	32.6
Std Dev	9.857

Gaus mean : 35.7632

Gaus Width: 4.5651



B1L103S, U2-ch33, adc2

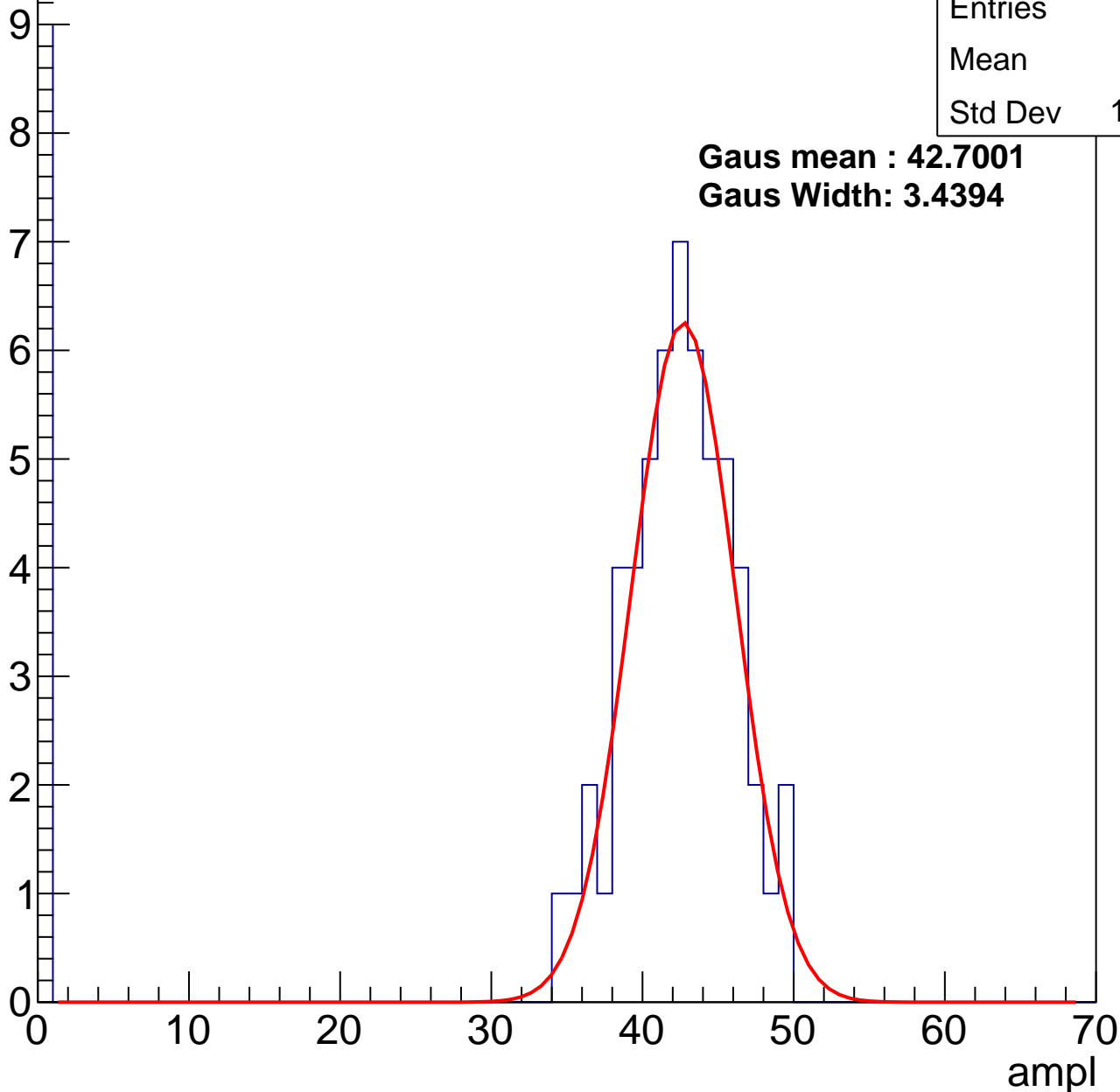
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.2
Std Dev	14.86

Gaus mean : 42.7001

Gaus Width: 3.4394

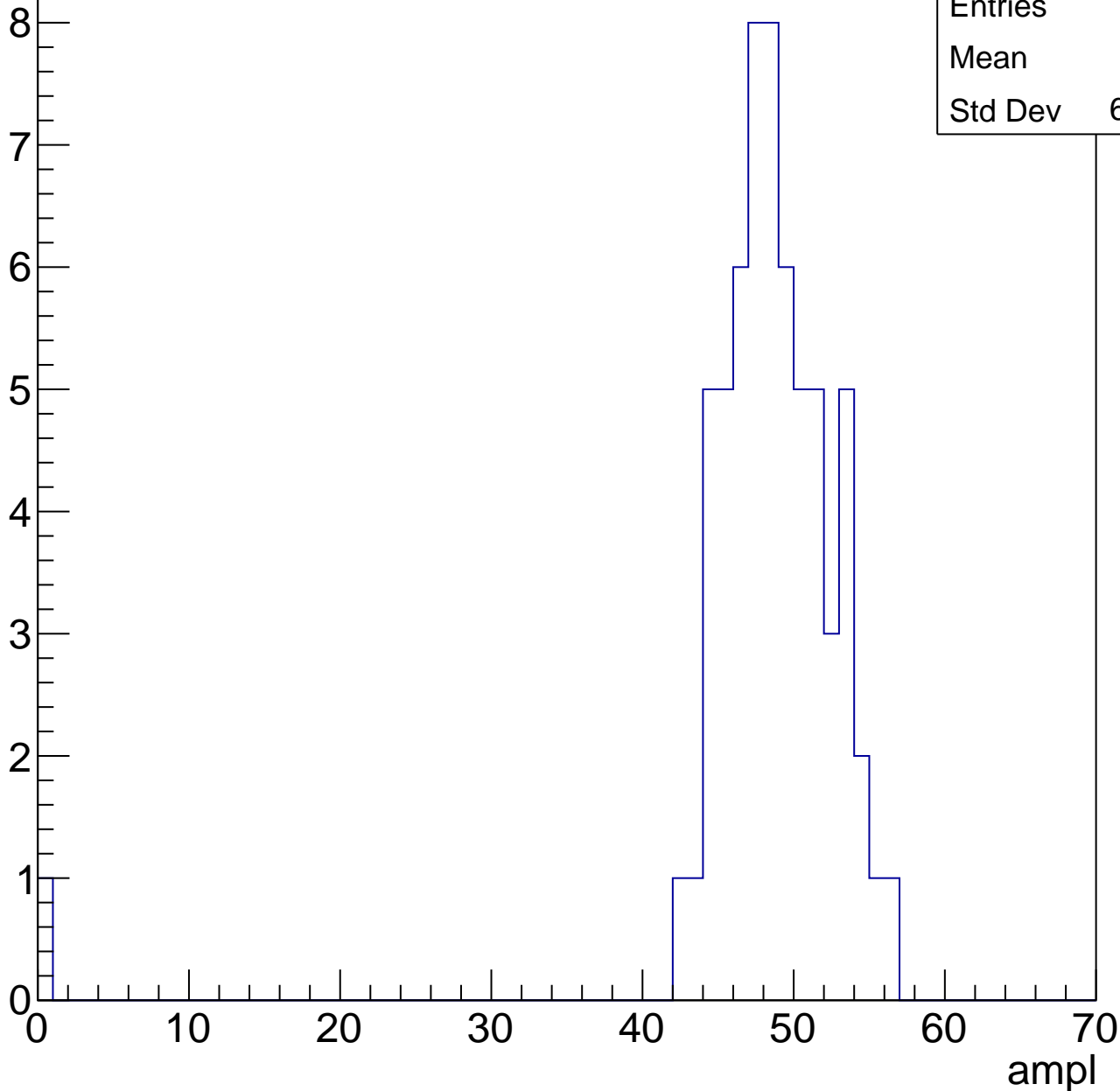


B1L103S, U2-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.7
Std Dev	6.837



B1L103S, U2-ch33, adc4

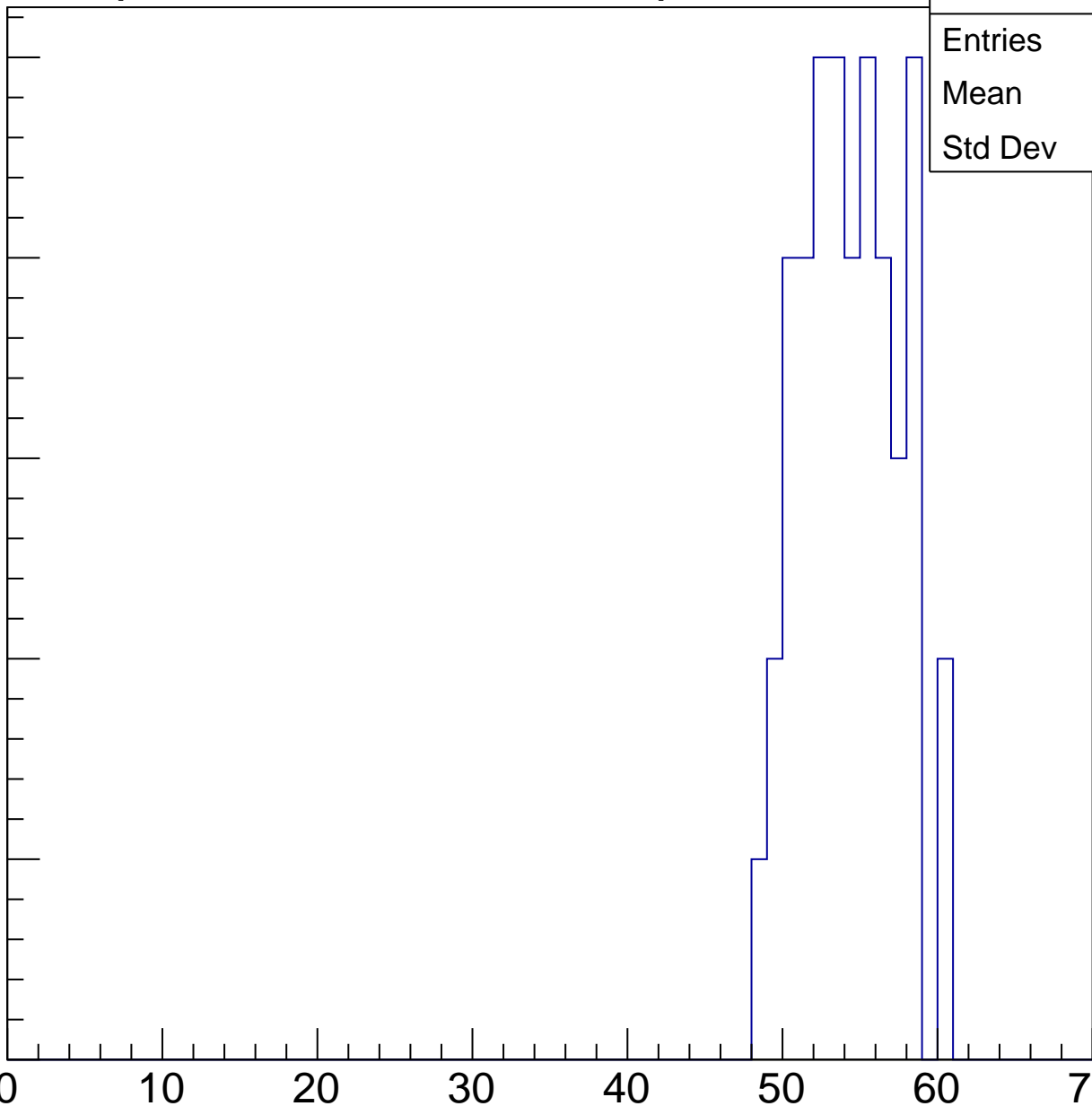
calib_packv5_041523_1651.root, FC#0, port C2

Entry

5
4
3
2
1
0

Entries	44
Mean	53.89
Std Dev	3.054

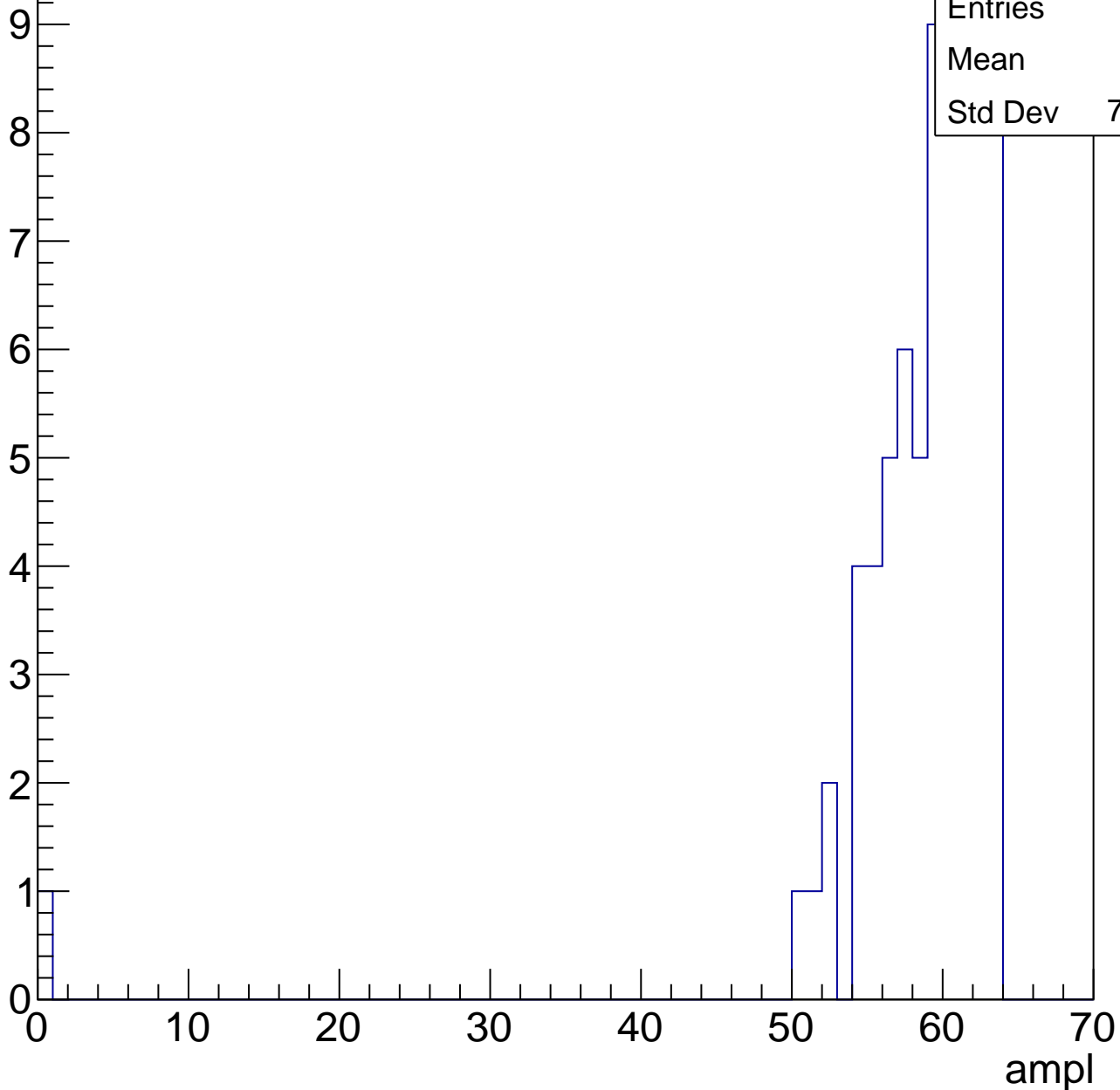
ampl



B1L103S, U2-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

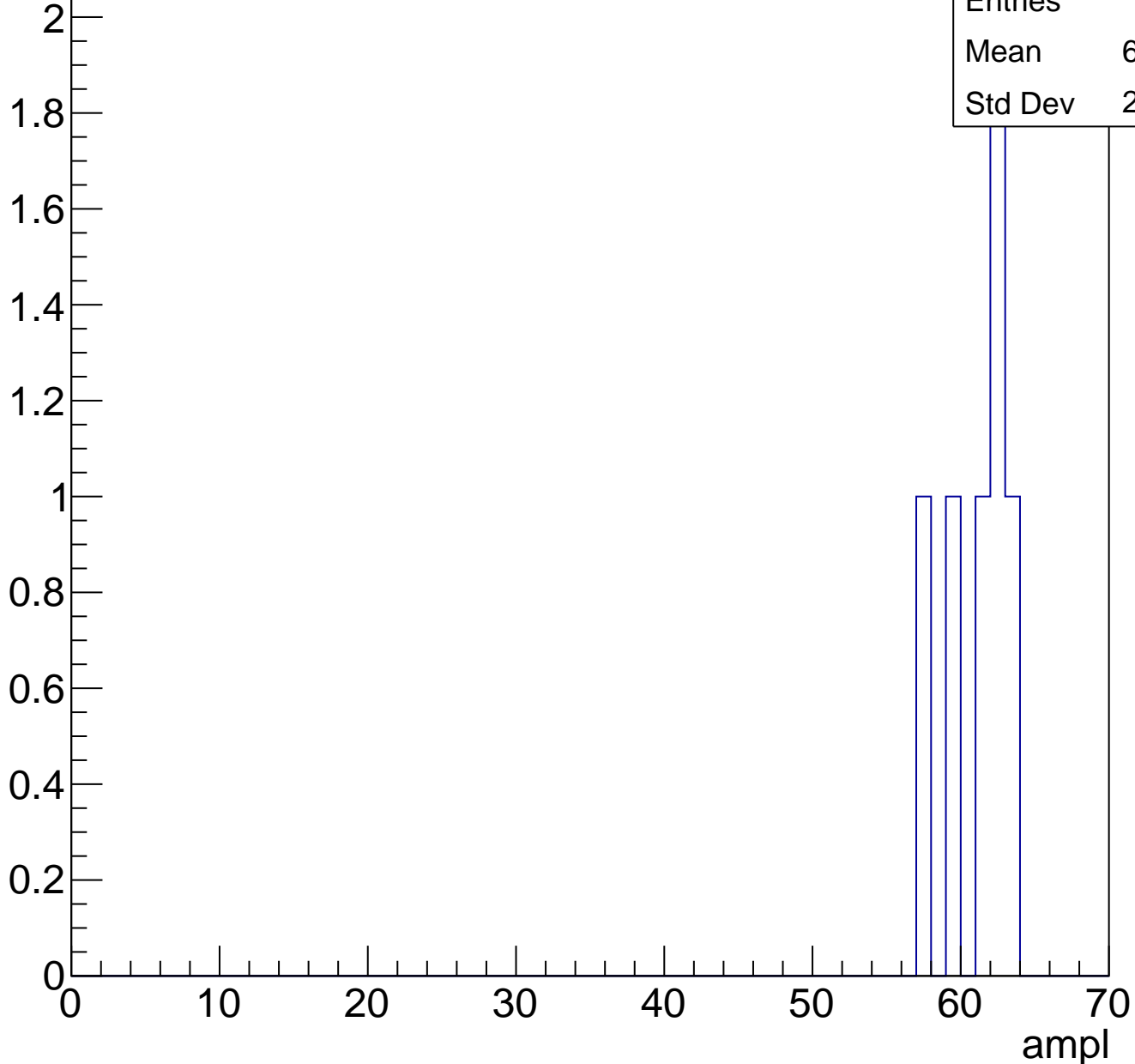
Entry



B1L103S, U2-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	6
Mean	60.67
Std Dev	2.055

B1L103S, U2-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch34, adc0

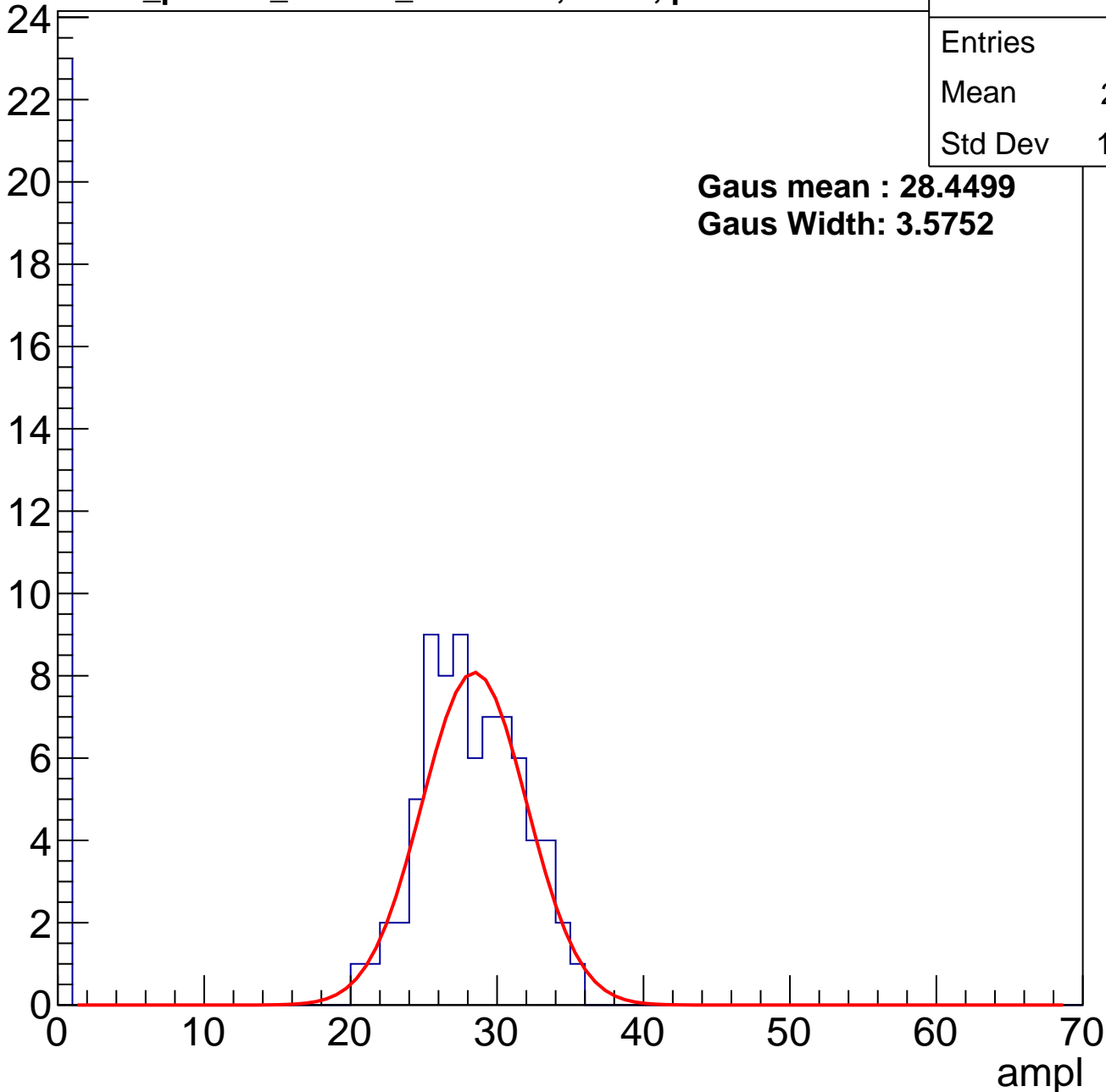
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	21.21
Std Dev	12.17

Gaus mean : 28.4499

Gaus Width: 3.5752

Entry



B1L103S, U2-ch34, adc1

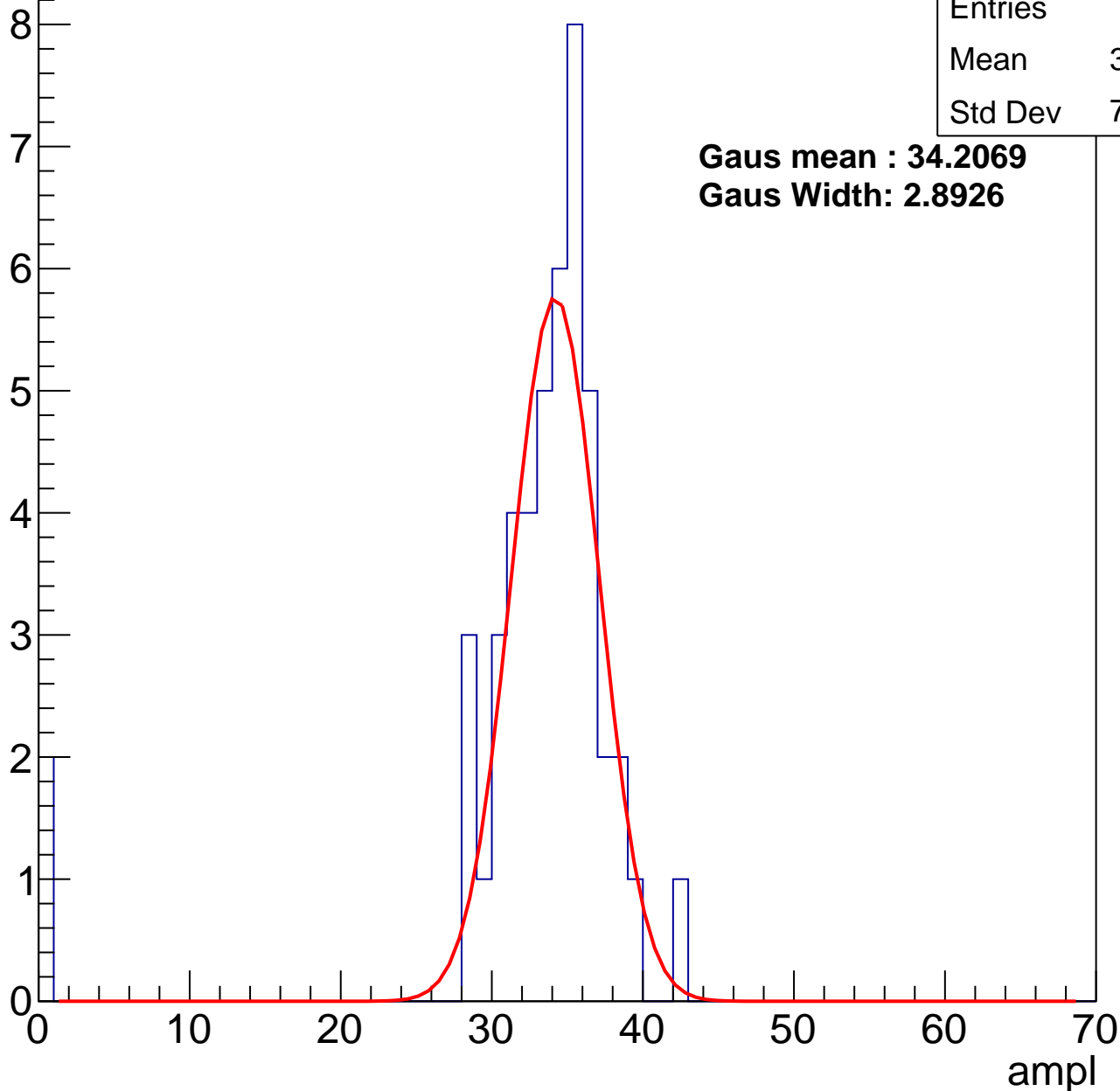
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	32.23
Std Dev	7.395

Gaus mean : 34.2069

Gaus Width: 2.8926



B1L103S, U2-ch34, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	33.63
Std Dev	14.45

Gaus mean : 40.7187

Gaus Width: 3.5953

Entry

10

8

6

4

2

0

0

10

20

30

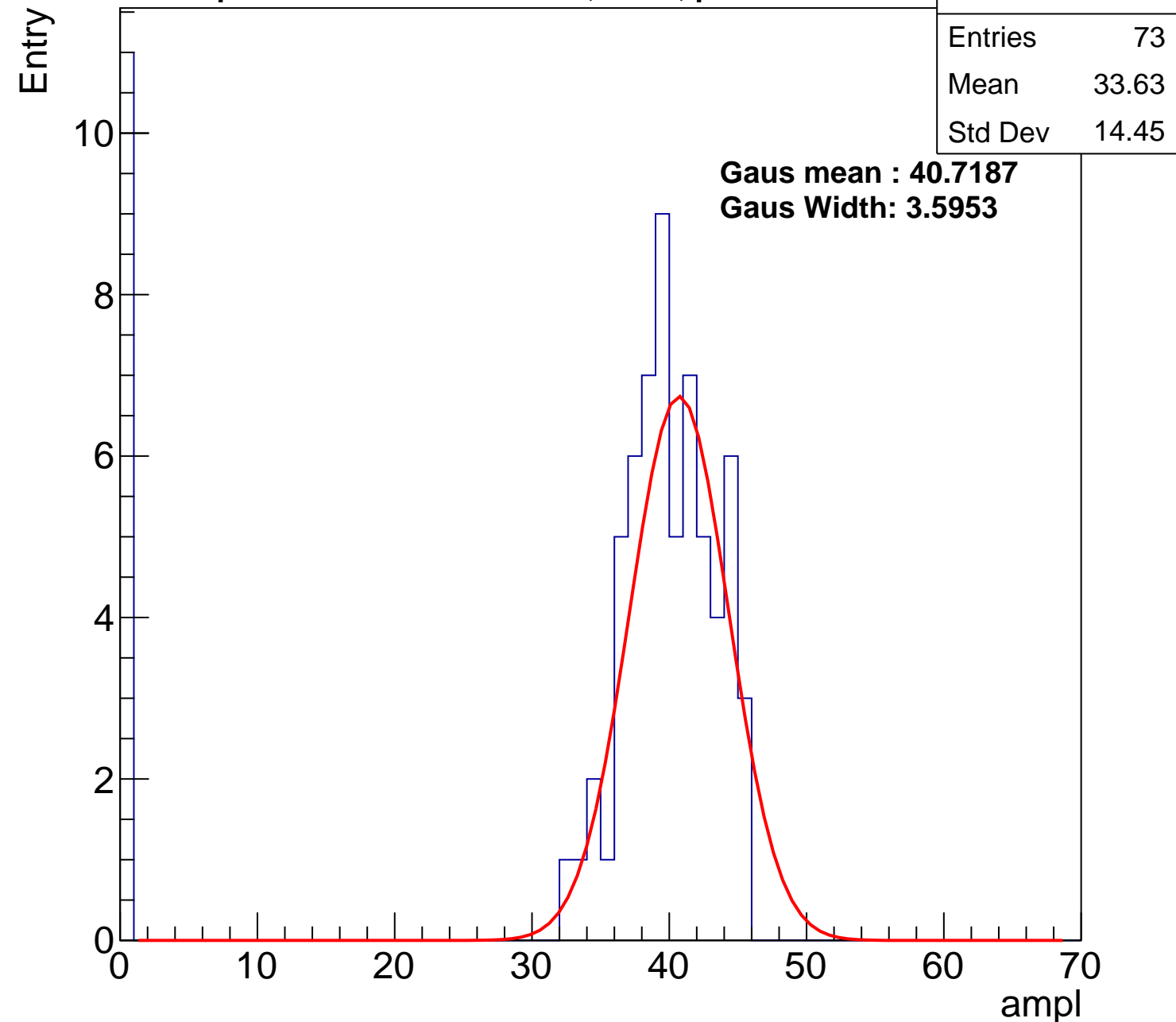
40

50

60

70

ampl



B1L103S, U2-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	43.26
Std Dev	9.73

Entry

10

8

6

4

2

0

0

10

20

30

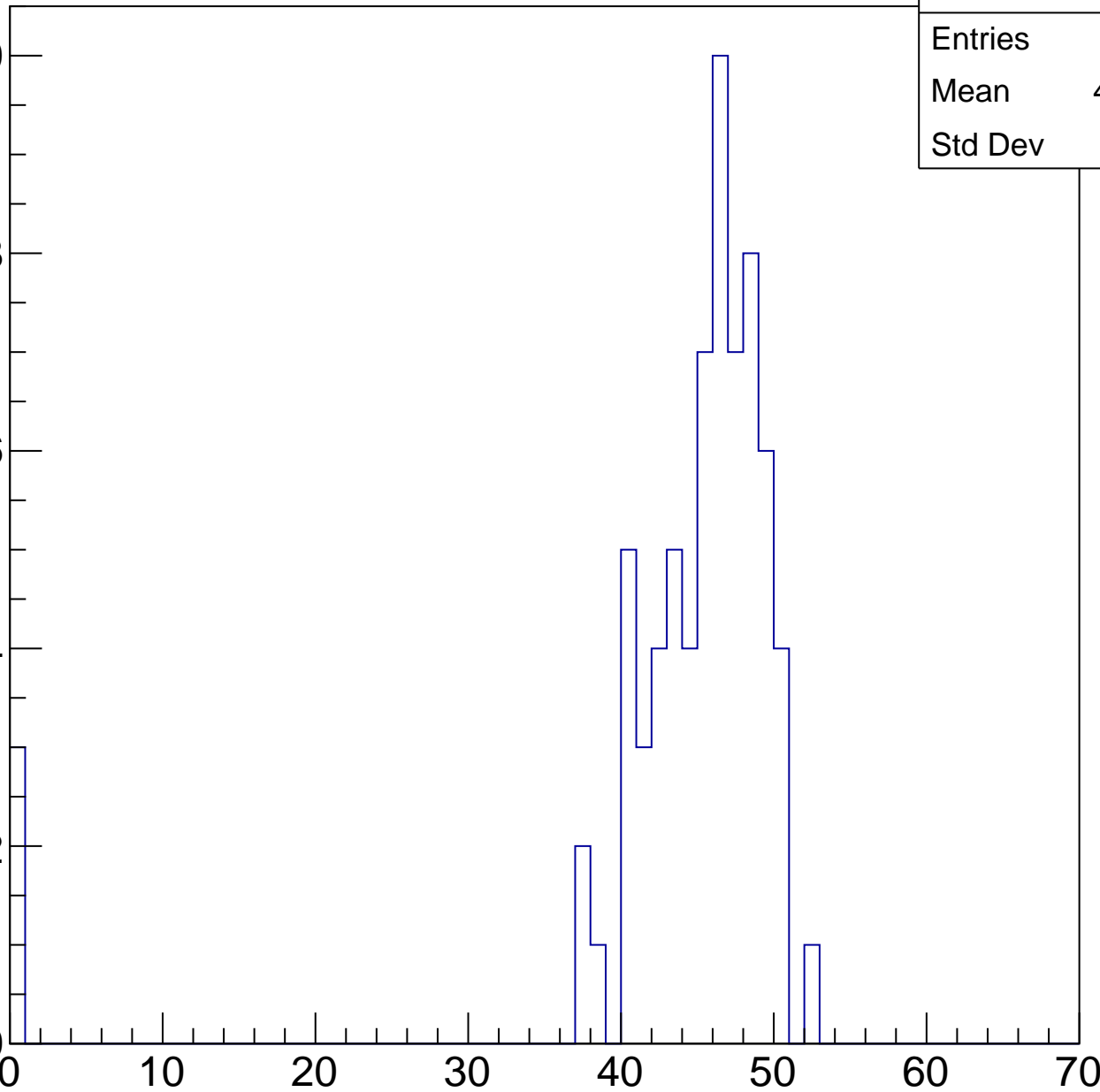
40

50

60

70

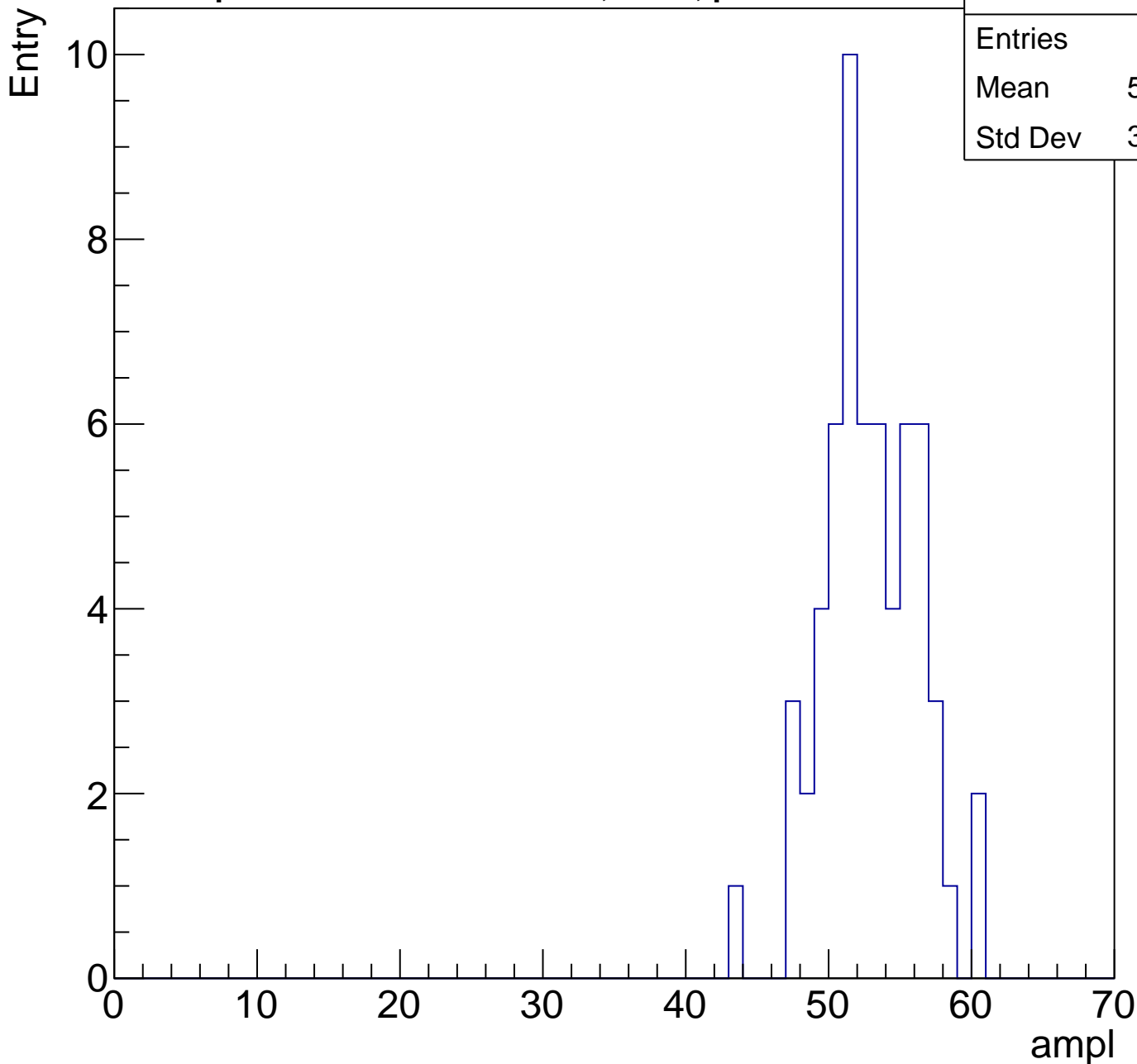
ampl



B1L103S, U2-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	60
Mean	52.45
Std Dev	3.309



B1L103S, U2-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	57.23
Std Dev	7.51

ampl

0

10

20

30

40

50

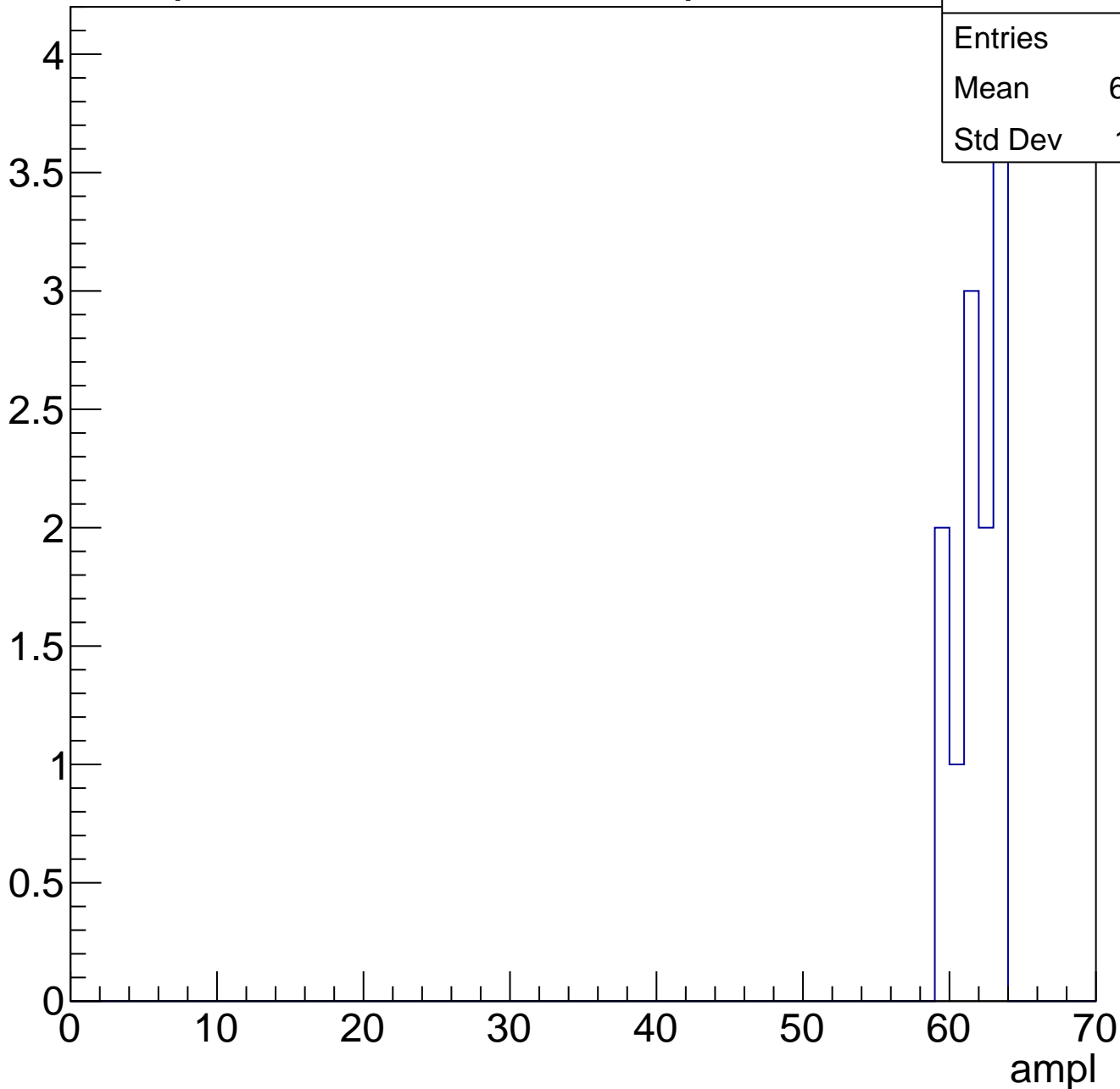
60

70

B1L103S, U2-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	22
Mean	8.5
Std Dev	21.39

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch35, adc0

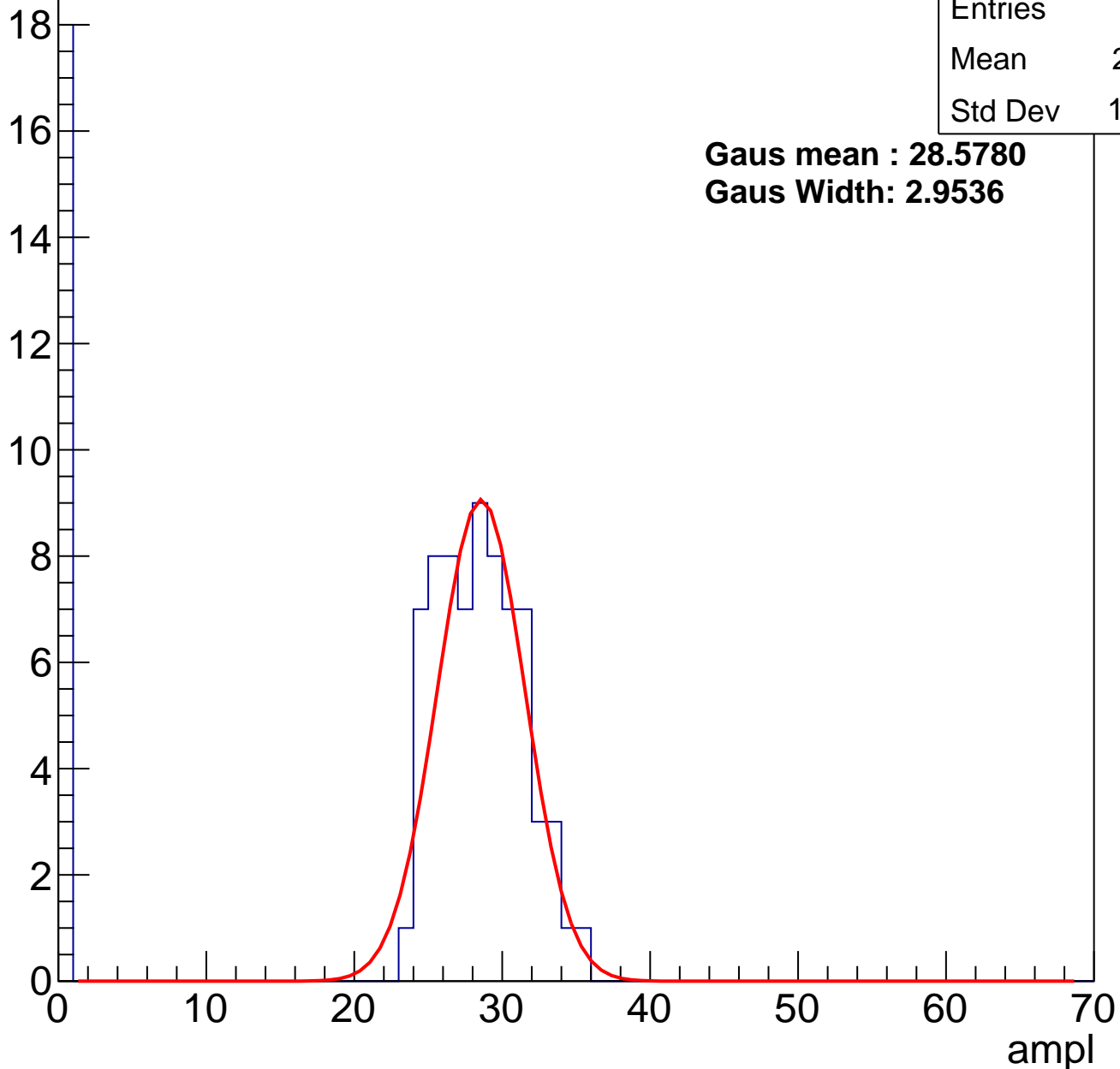
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	22.31
Std Dev	11.59

Gaus mean : 28.5780

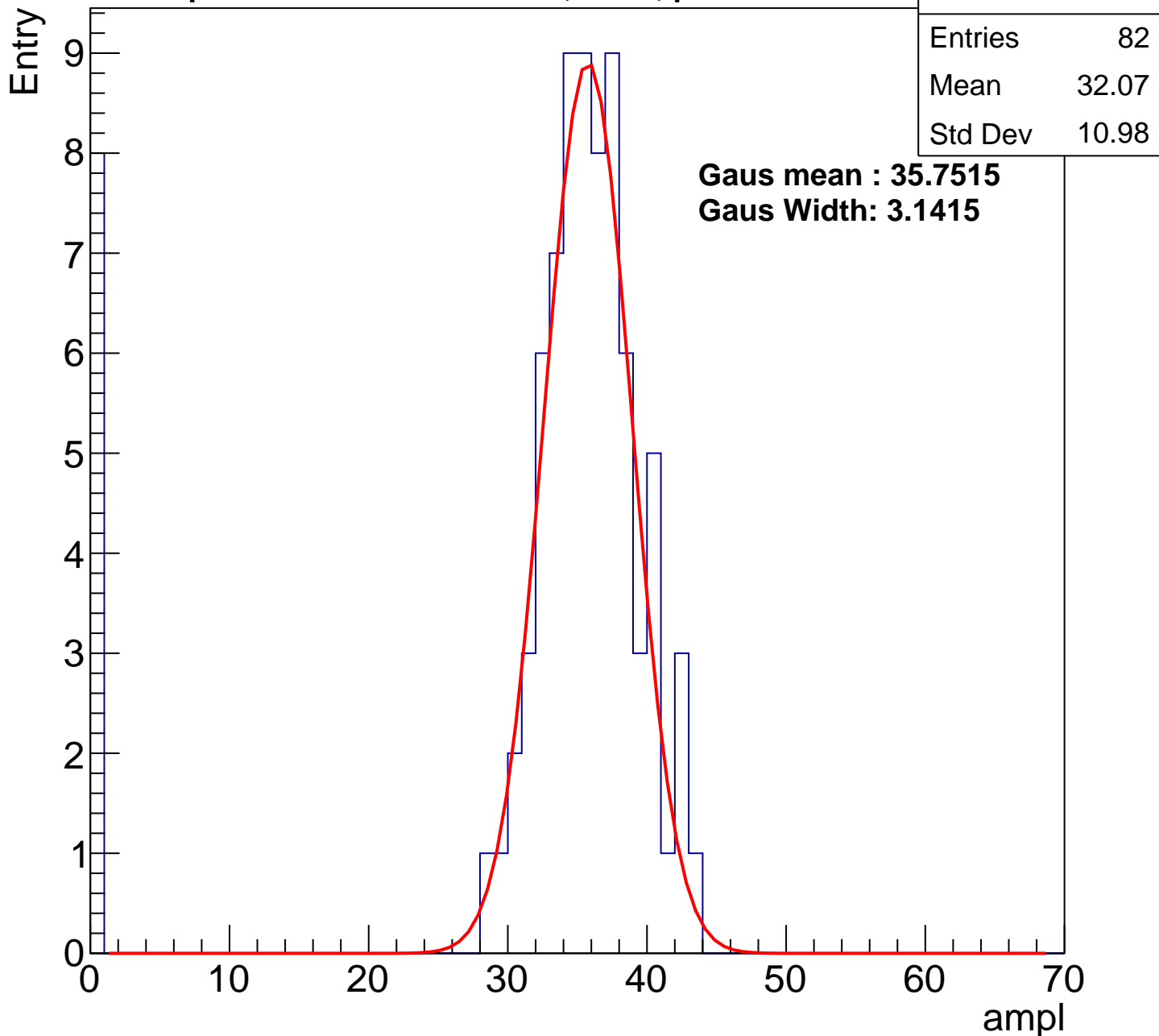
Gaus Width: 2.9536

Entry



B1L103S, U2-ch35, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch35, adc2

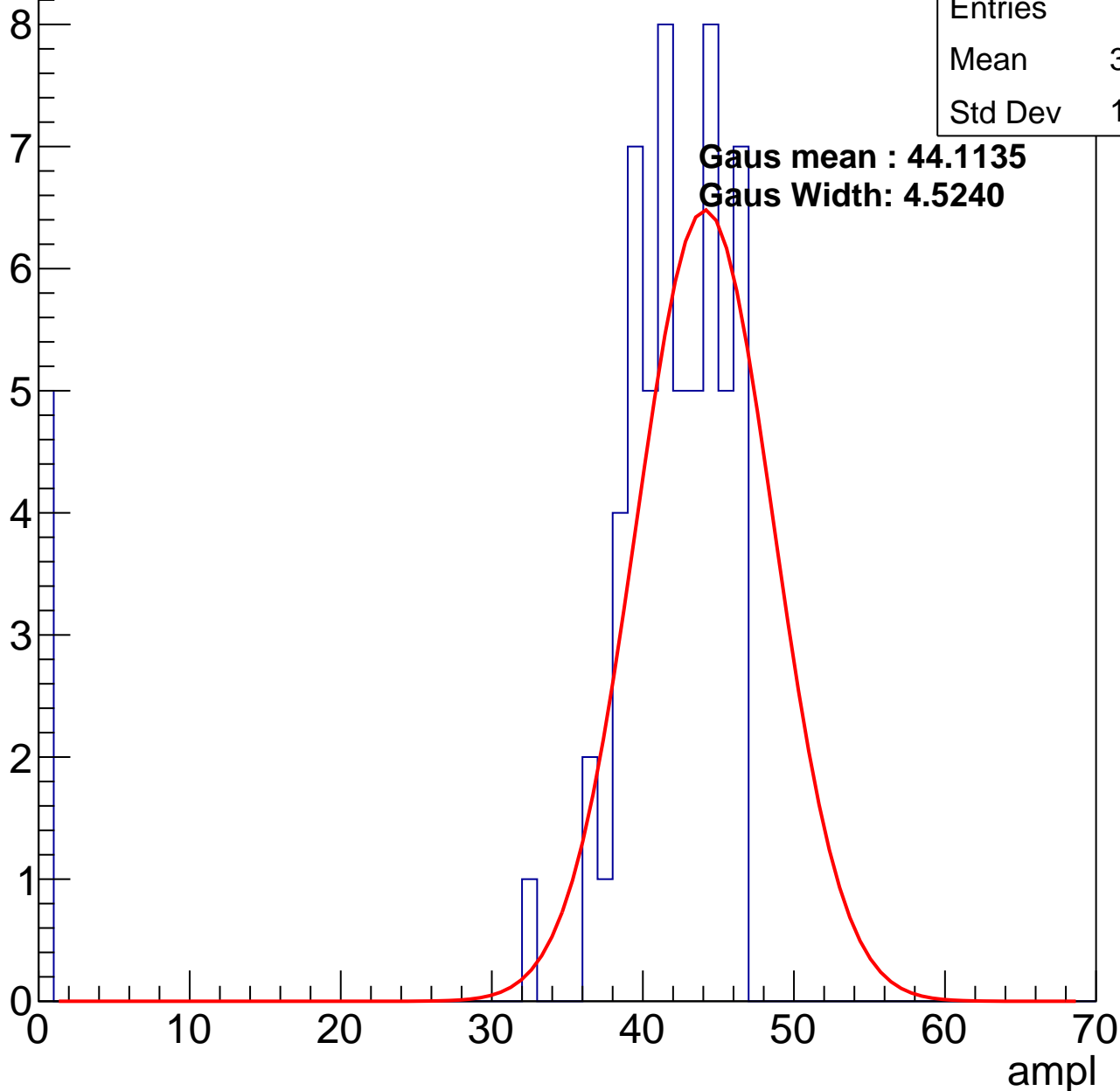
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	38.38
Std Dev	11.64

Gaus mean : 44.1135

Gaus Width: 4.5240

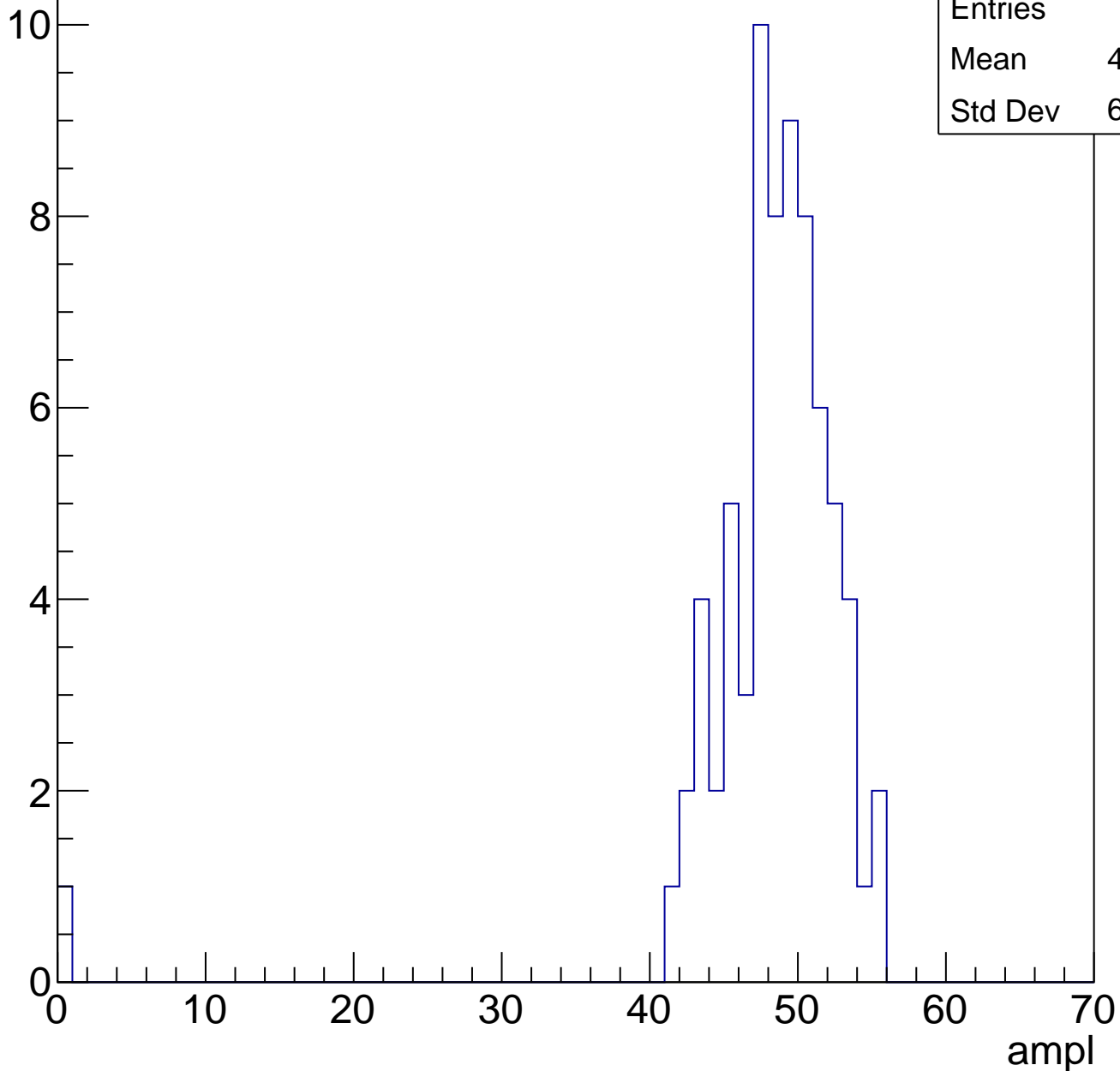


B1L103S, U2-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

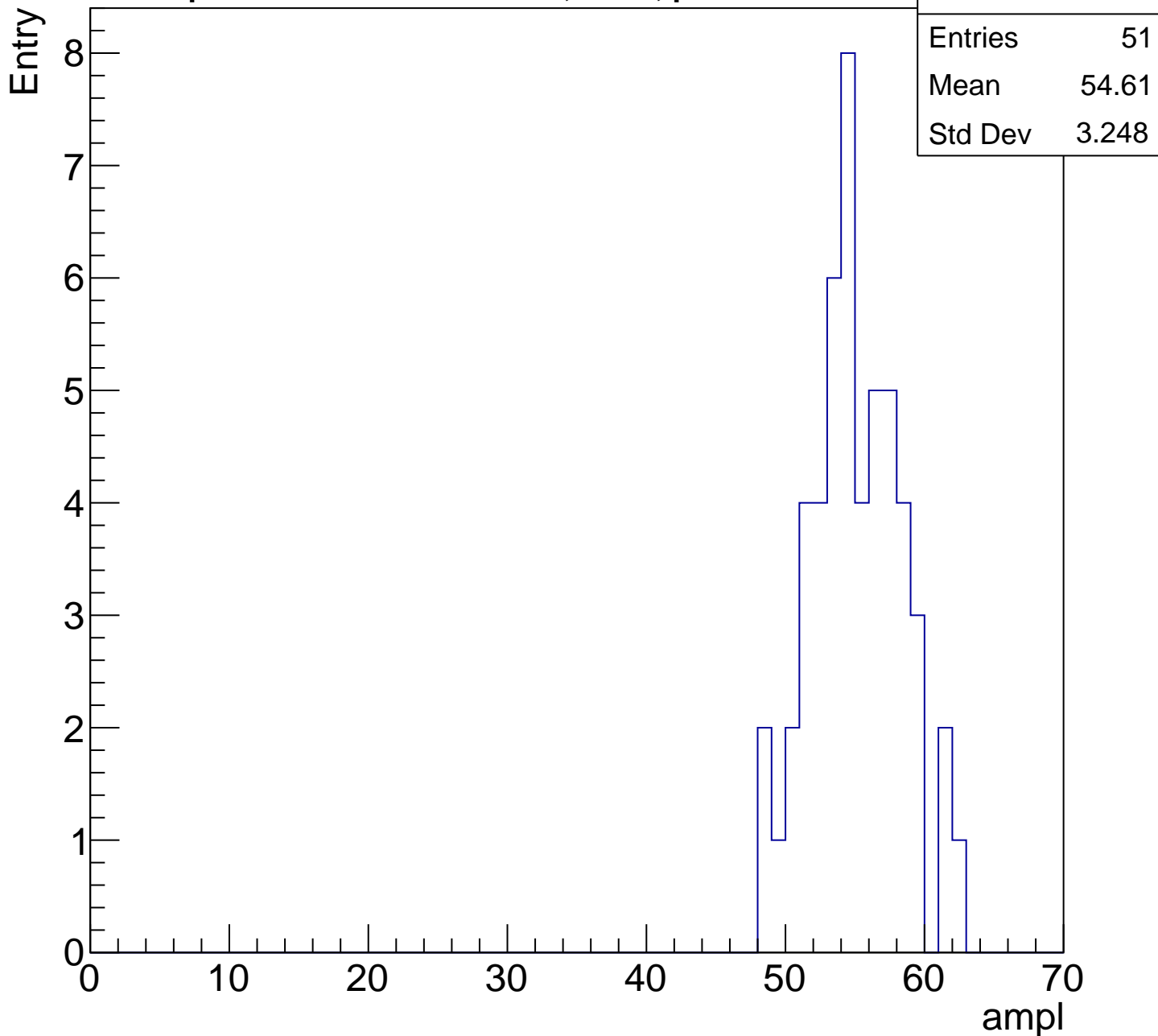
Entries	71
Mean	47.68
Std Dev	6.528

Entry



B1L103S, U2-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch35, adc5

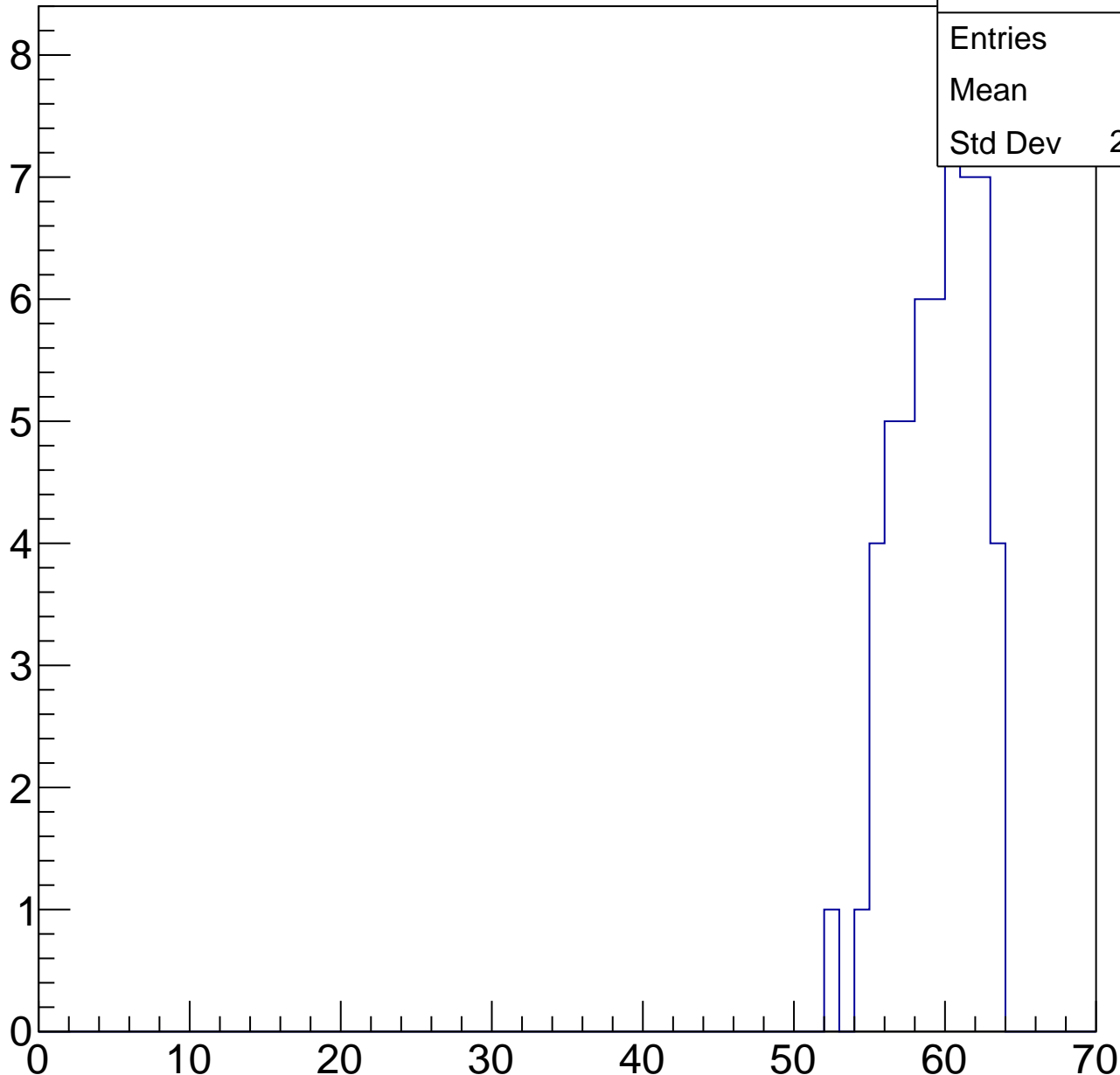
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	59
Std Dev	2.625

ampl

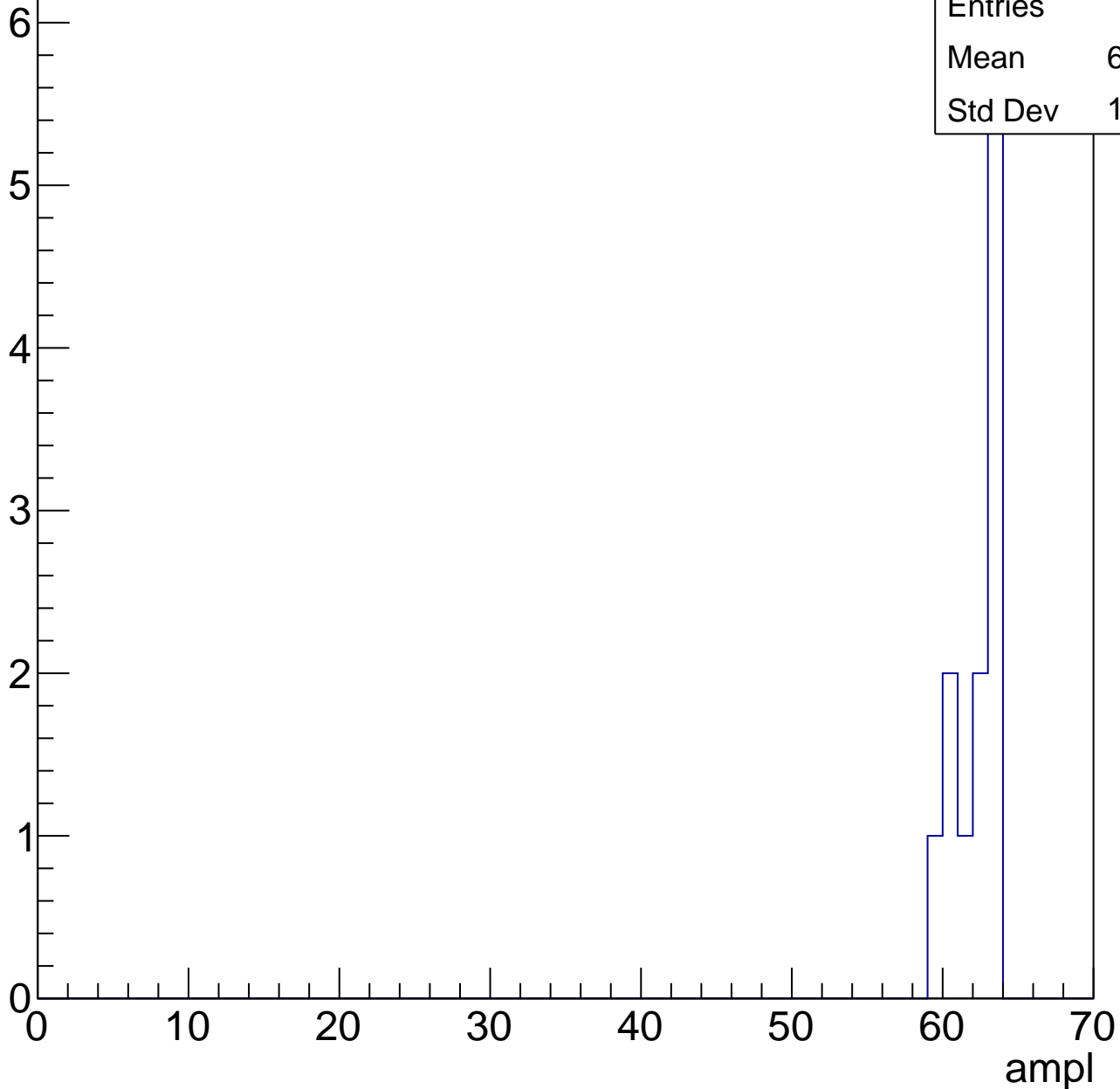


B1L103S, U2-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.83
Std Dev	1.404

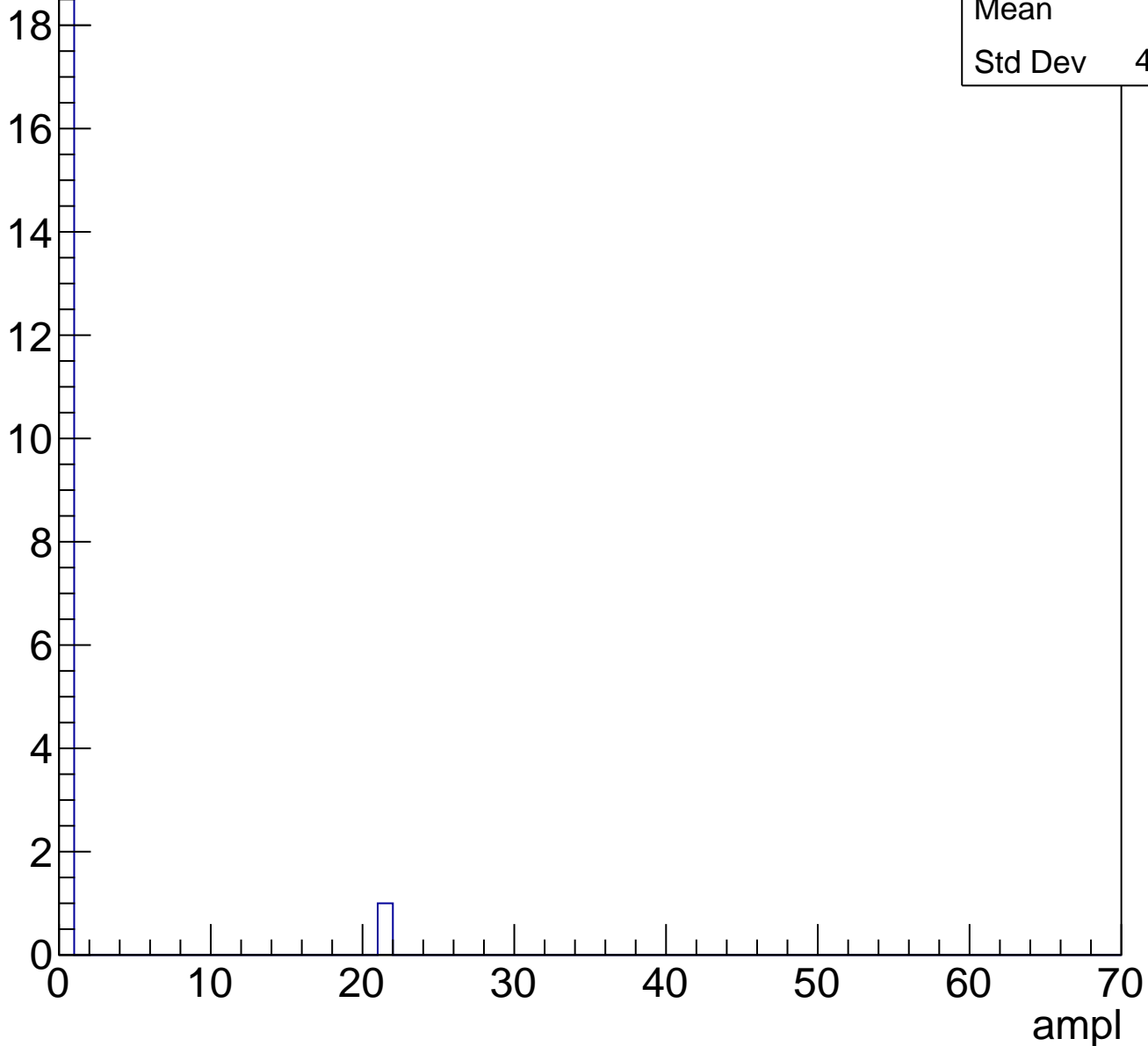


B1L103S, U2-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	1.05
Std Dev	4.577

Entry



B1L103S, U2-ch36, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	17.95
Std Dev	13.95

Gaus mean : 28.8024

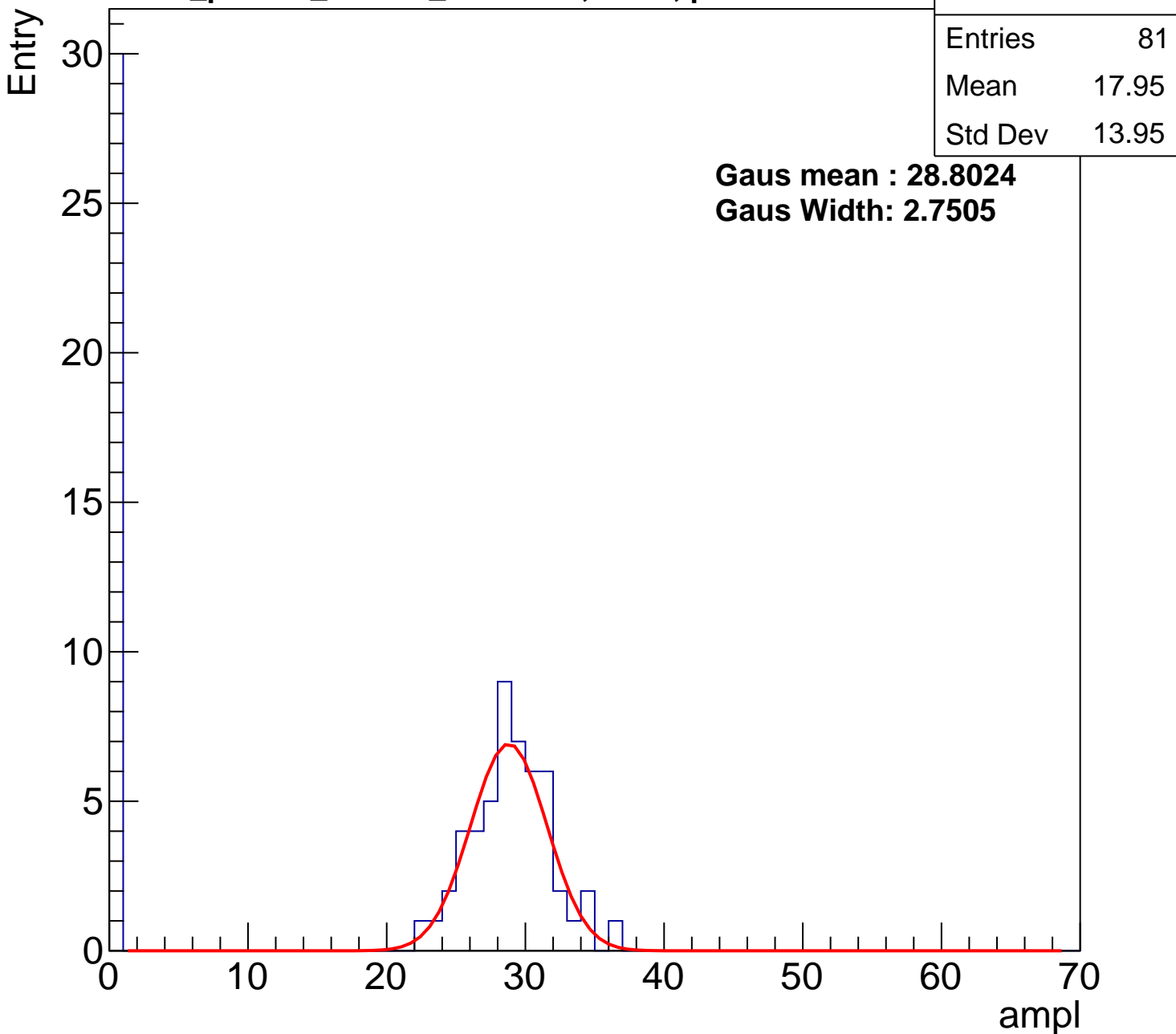
Gaus Width: 2.7505

Entry

30
25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch36, adc1

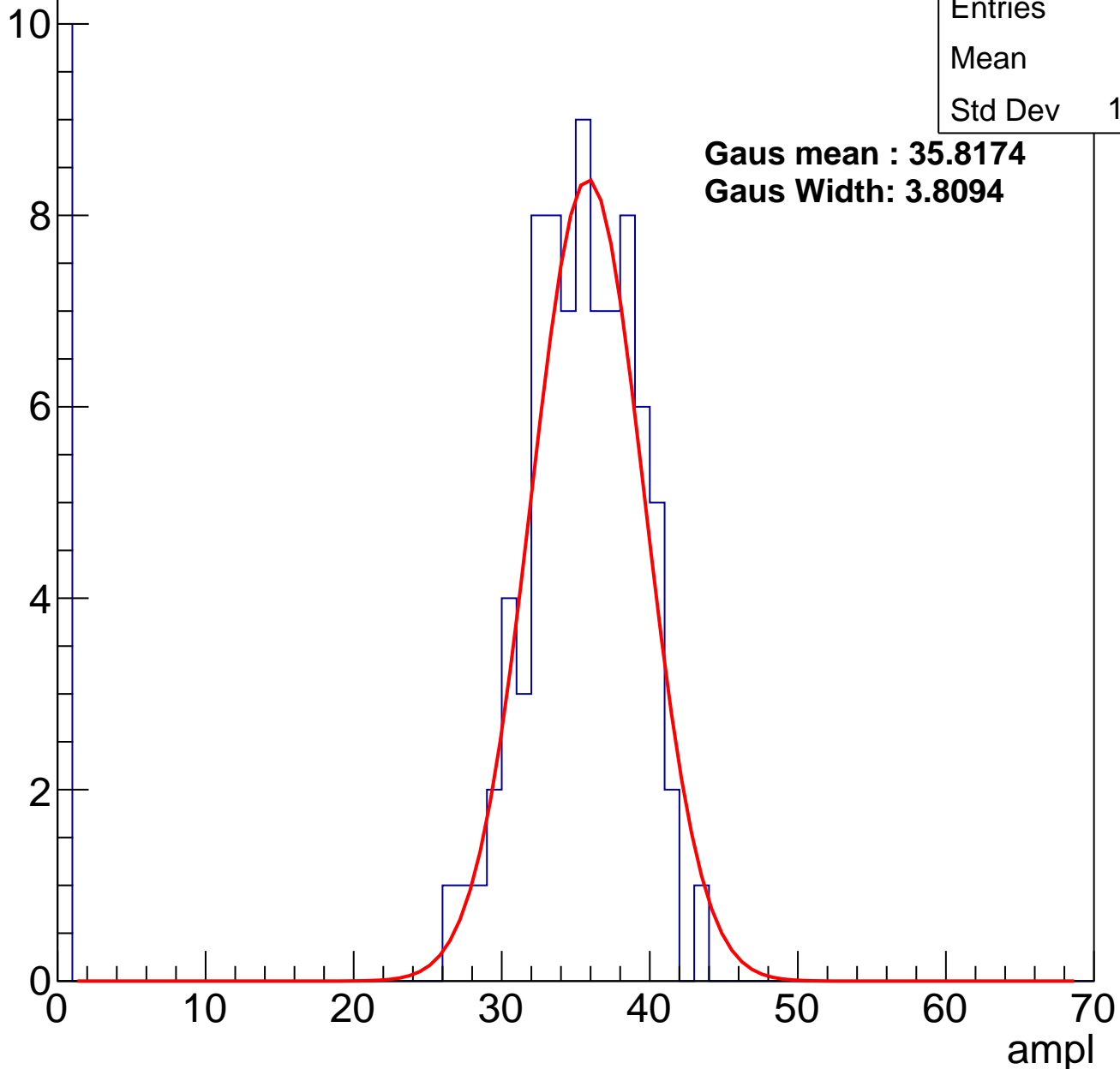
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	31.1
Std Dev	11.48

Gaus mean : 35.8174

Gaus Width: 3.8094

Entry



B1L103S, U2-ch36, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

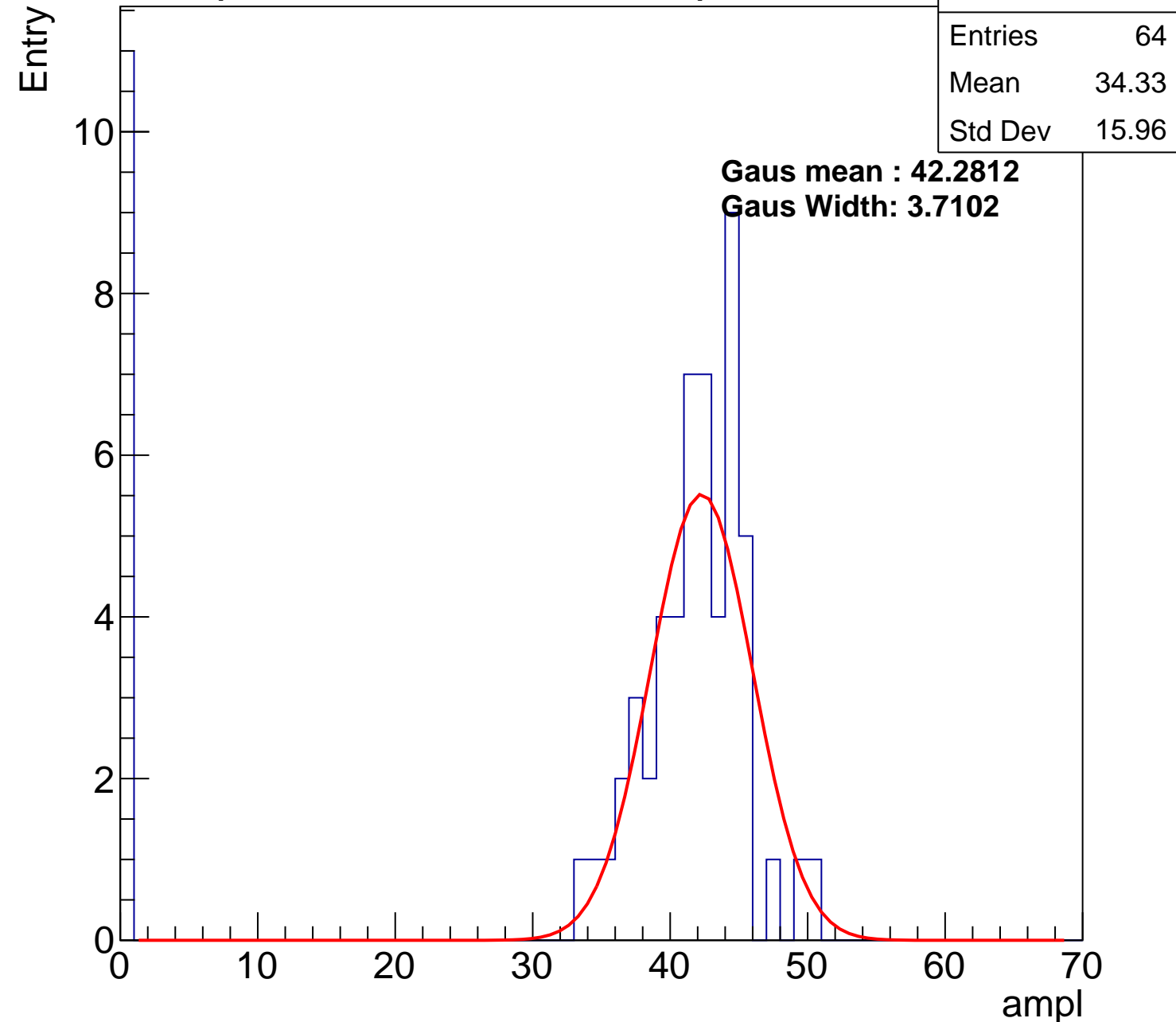
70

ampl

Entries	64
Mean	34.33
Std Dev	15.96

Gaus mean : 42.2812

Gaus Width: 3.7102



B1L103S, U2-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

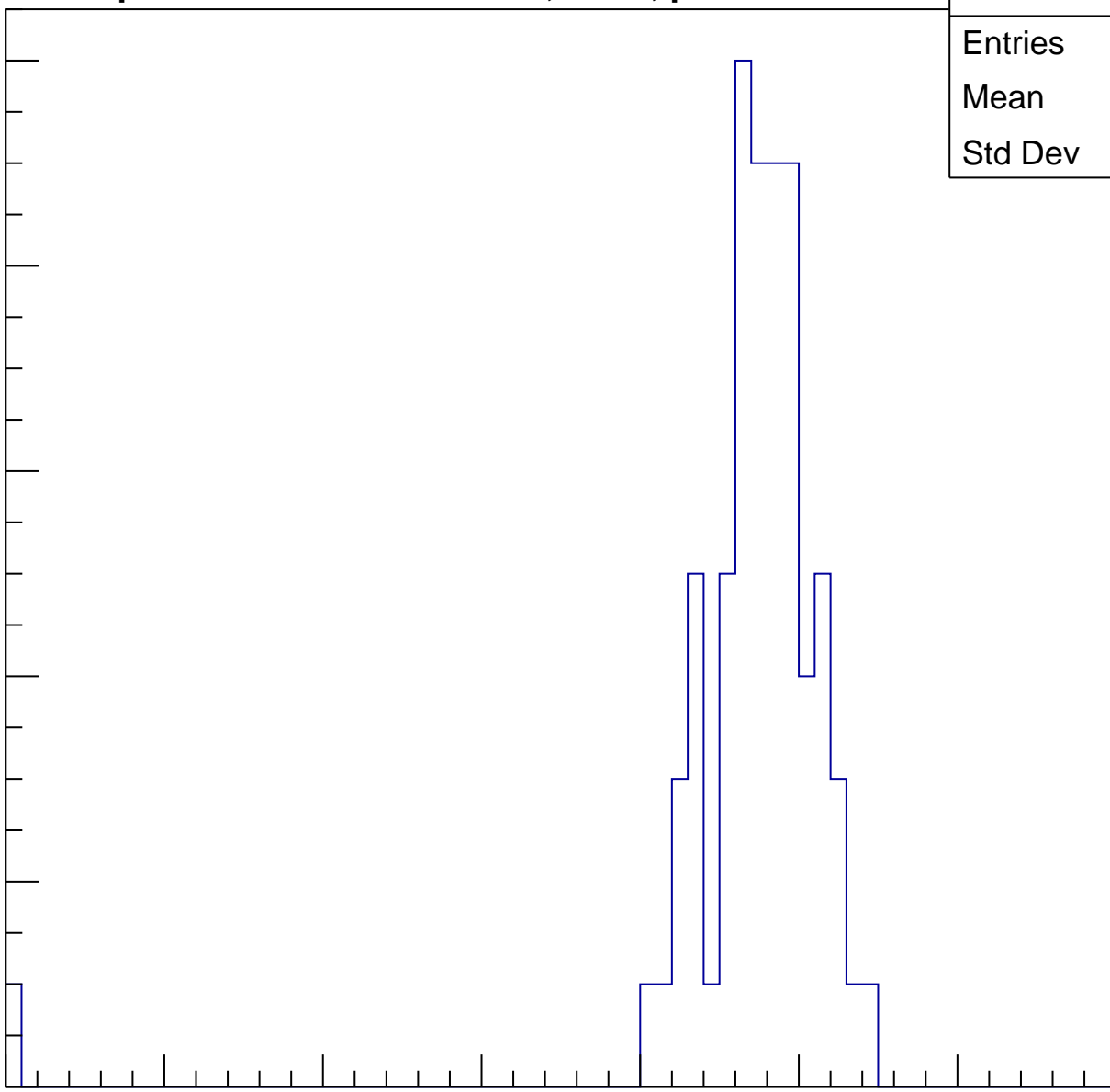
Entries	68
Mean	46.54
Std Dev	6.411

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

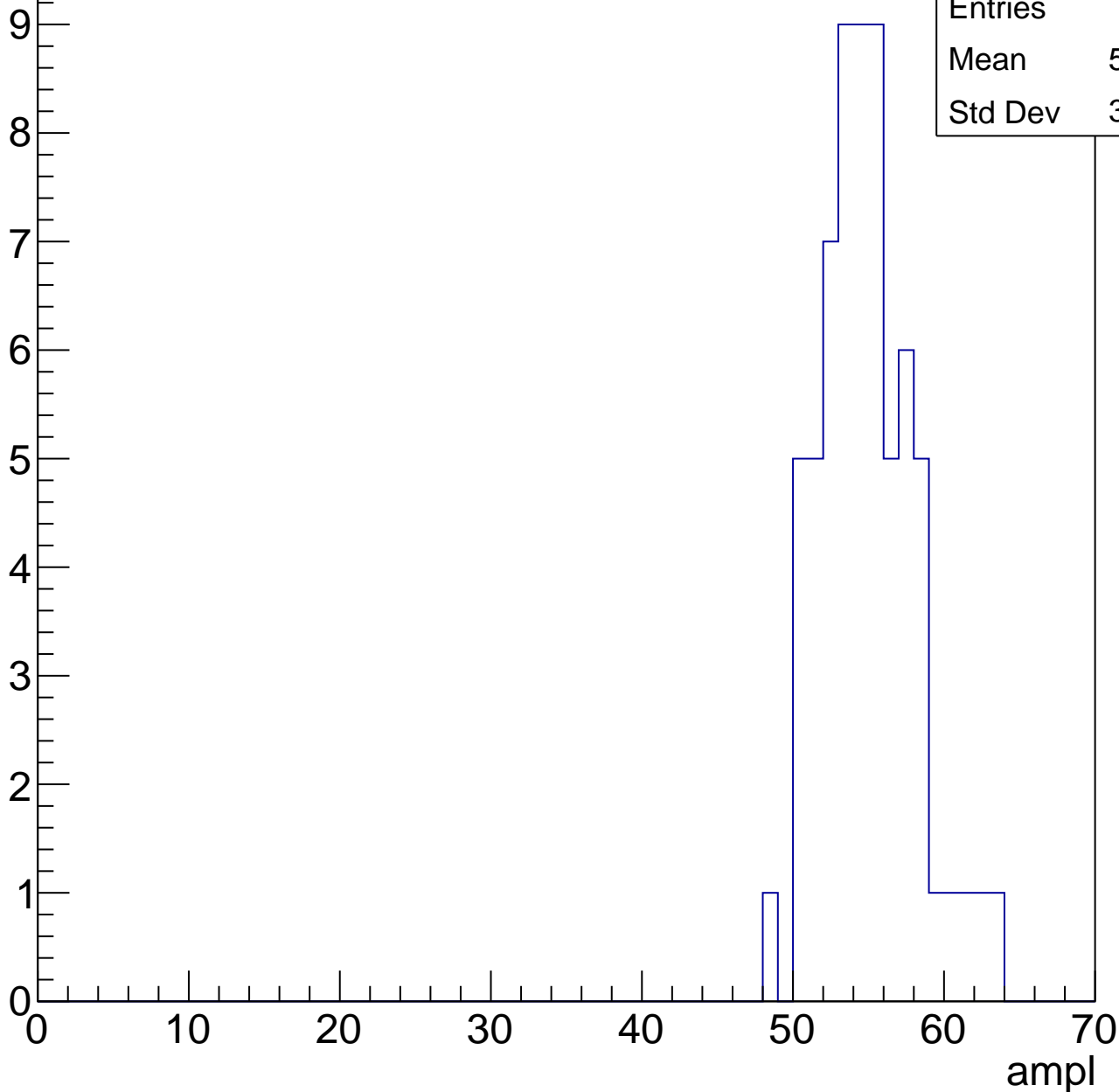


B1L103S, U2-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	54.42
Std Dev	3.025



B1L103S, U2-ch36, adc5

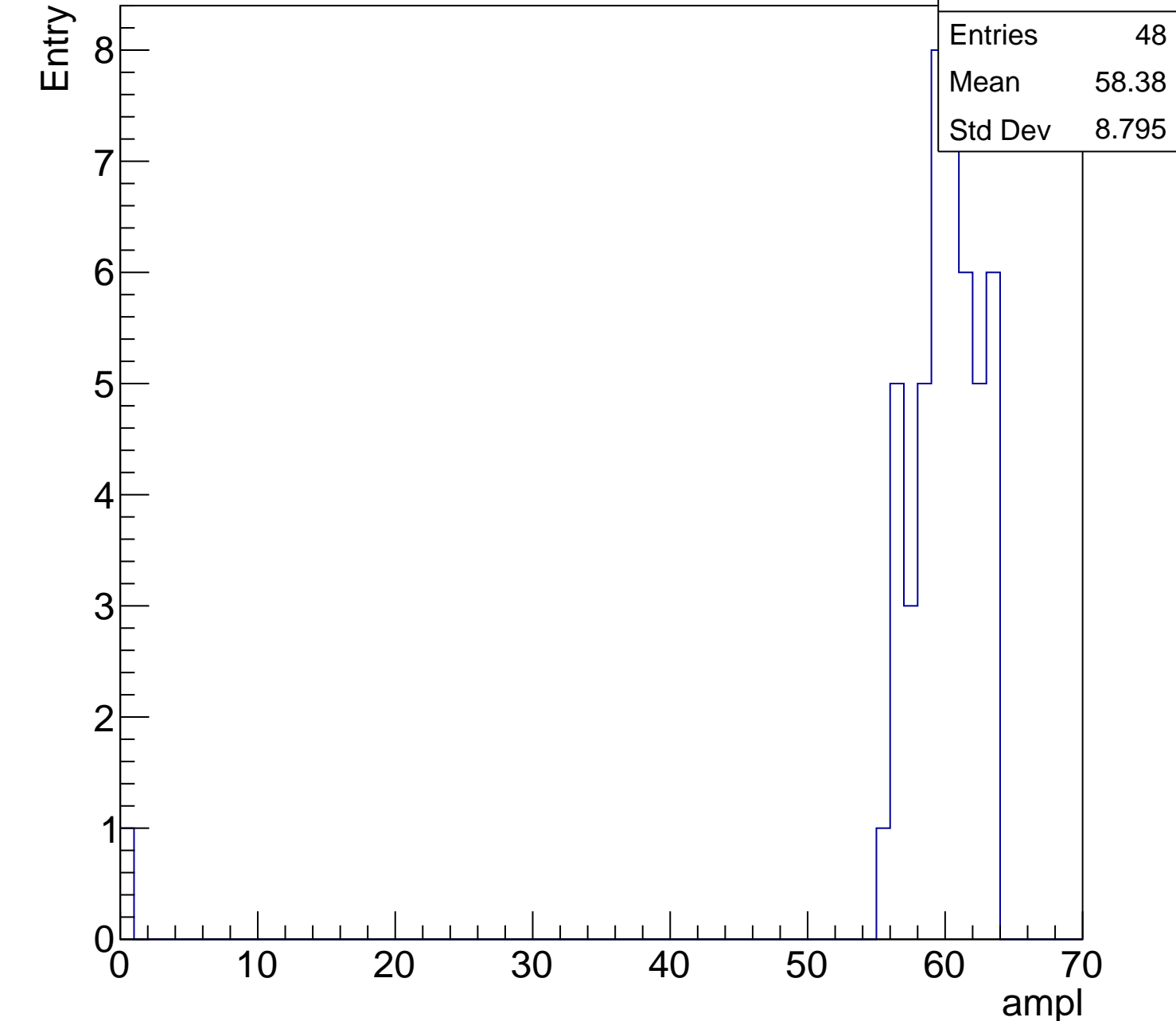
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.38
Std Dev	8.795

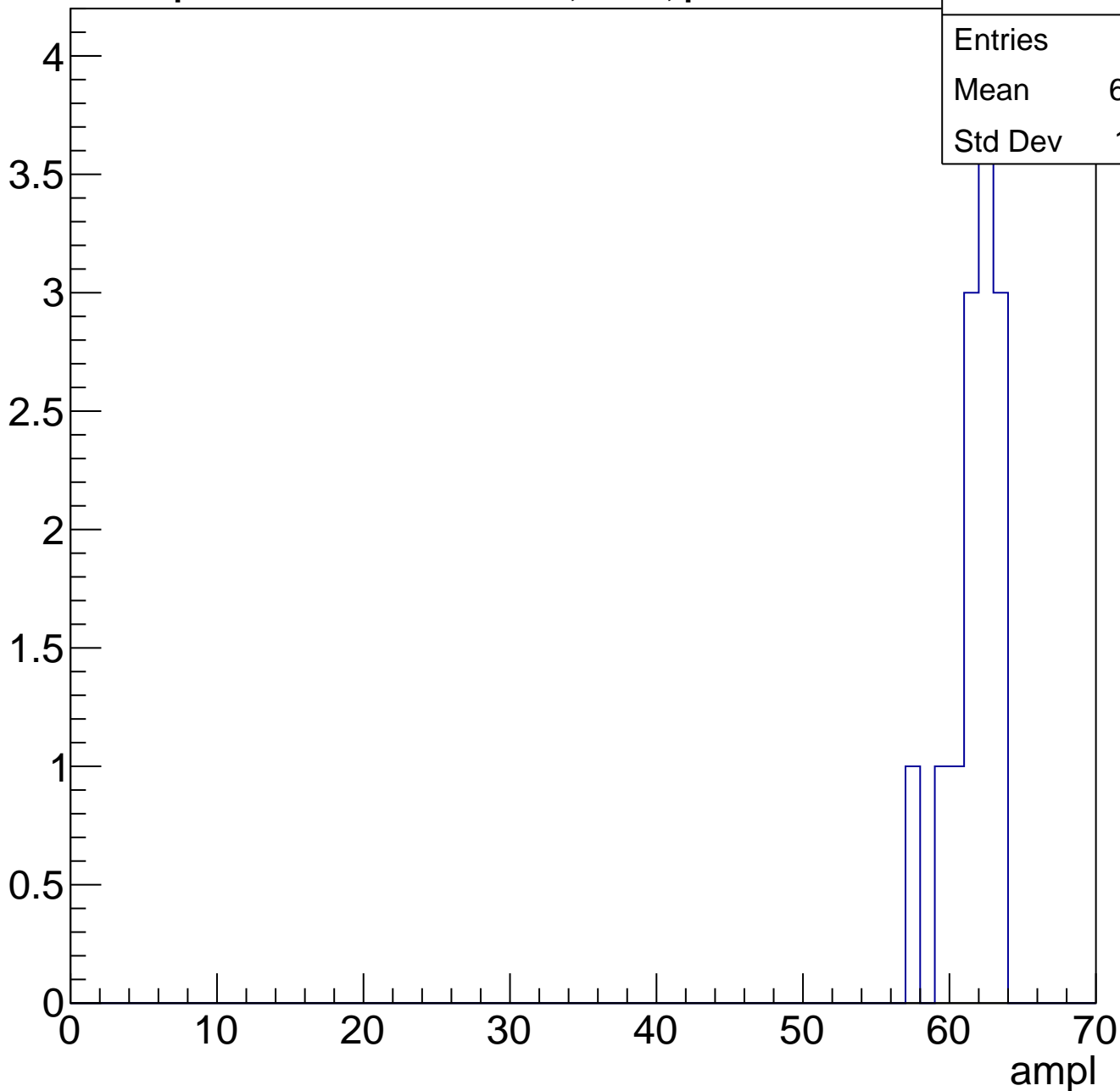
ampl



B1L103S, U2-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch37, adc0

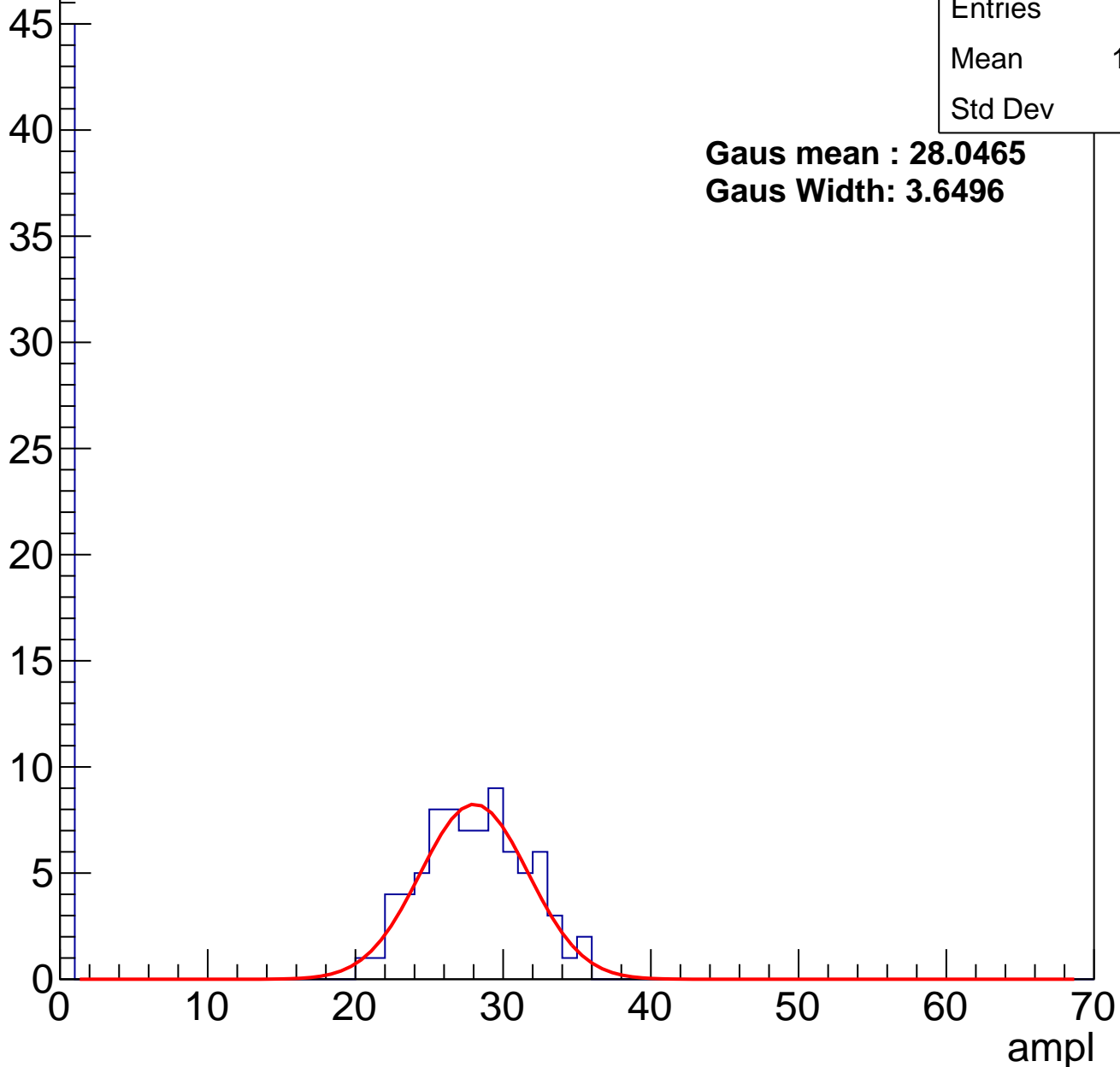
calib_packv5_041523_1651.root, FC#0, port C2

Entries	122
Mean	17.42
Std Dev	13.6

Gaus mean : 28.0465

Gaus Width: 3.6496

Entry



B1L103S, U2-ch37, adc1

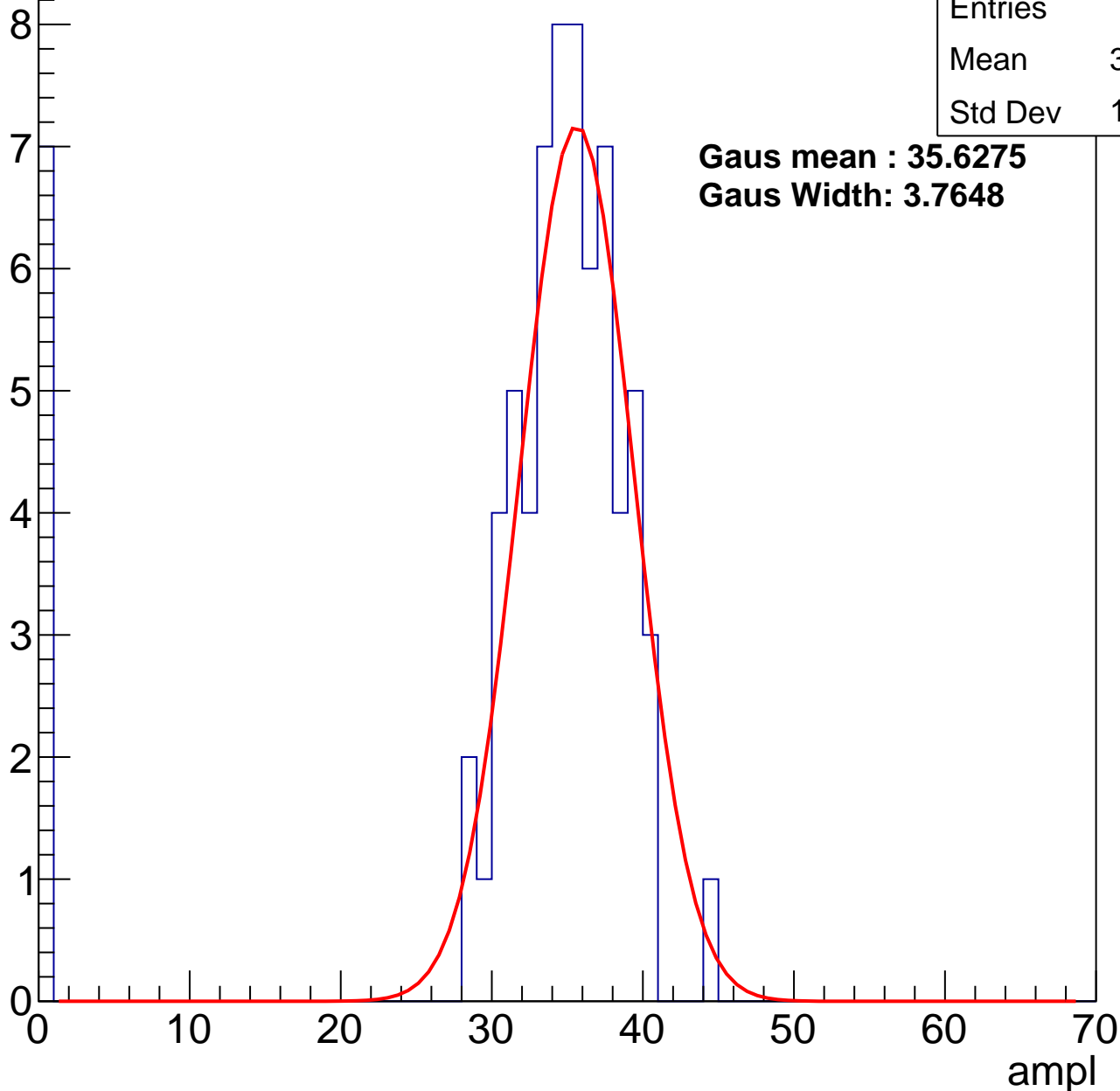
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	31.35
Std Dev	10.74

Gaus mean : 35.6275

Gaus Width: 3.7648



B1L103S, U2-ch37, adc2

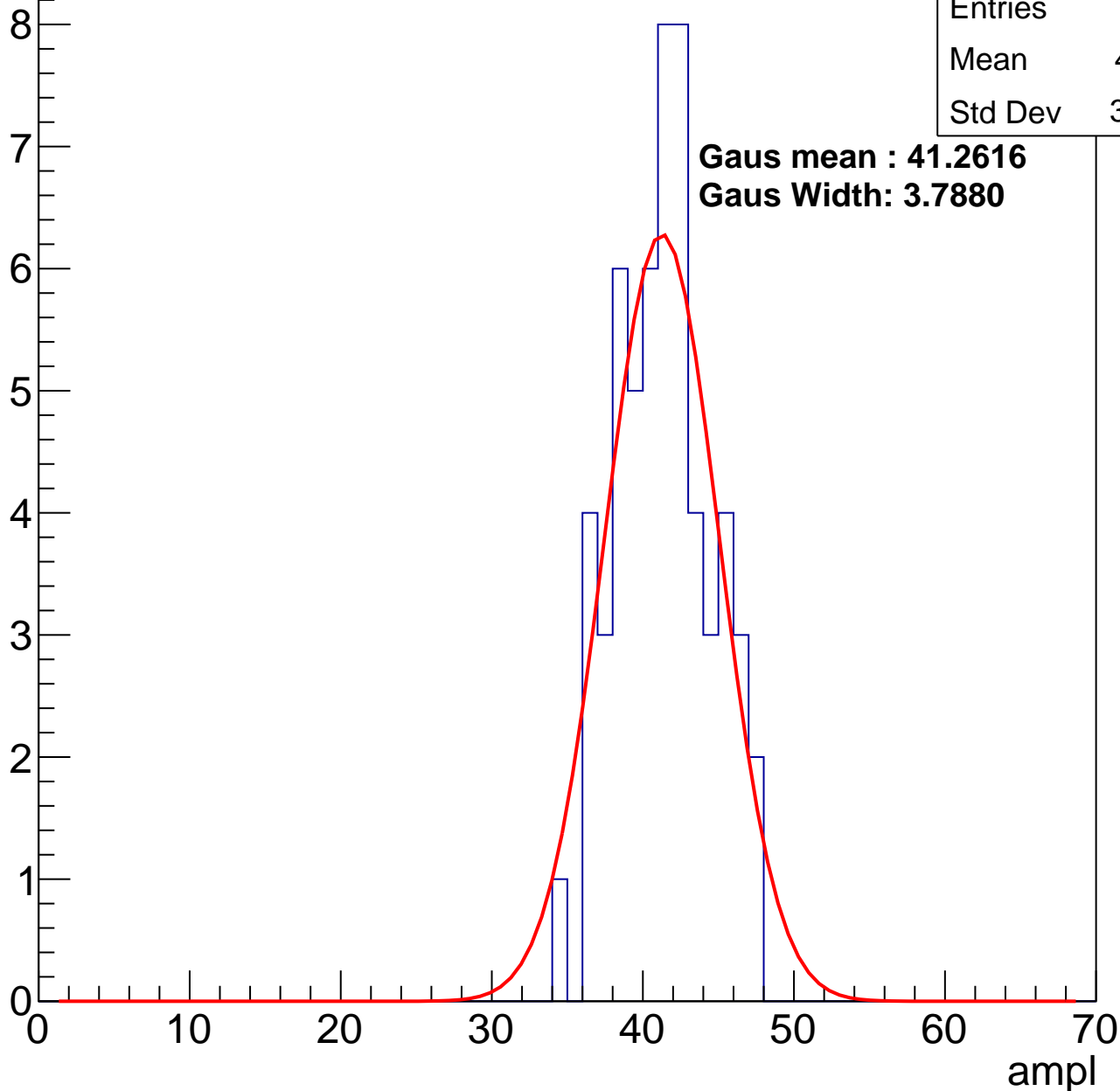
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	40.91
Std Dev	3.074

Gaus mean : 41.2616

Gaus Width: 3.7880



B1L103S, U2-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	45.63
Std Dev	9.751

Entry

10

8

6

4

2

0

0

10

20

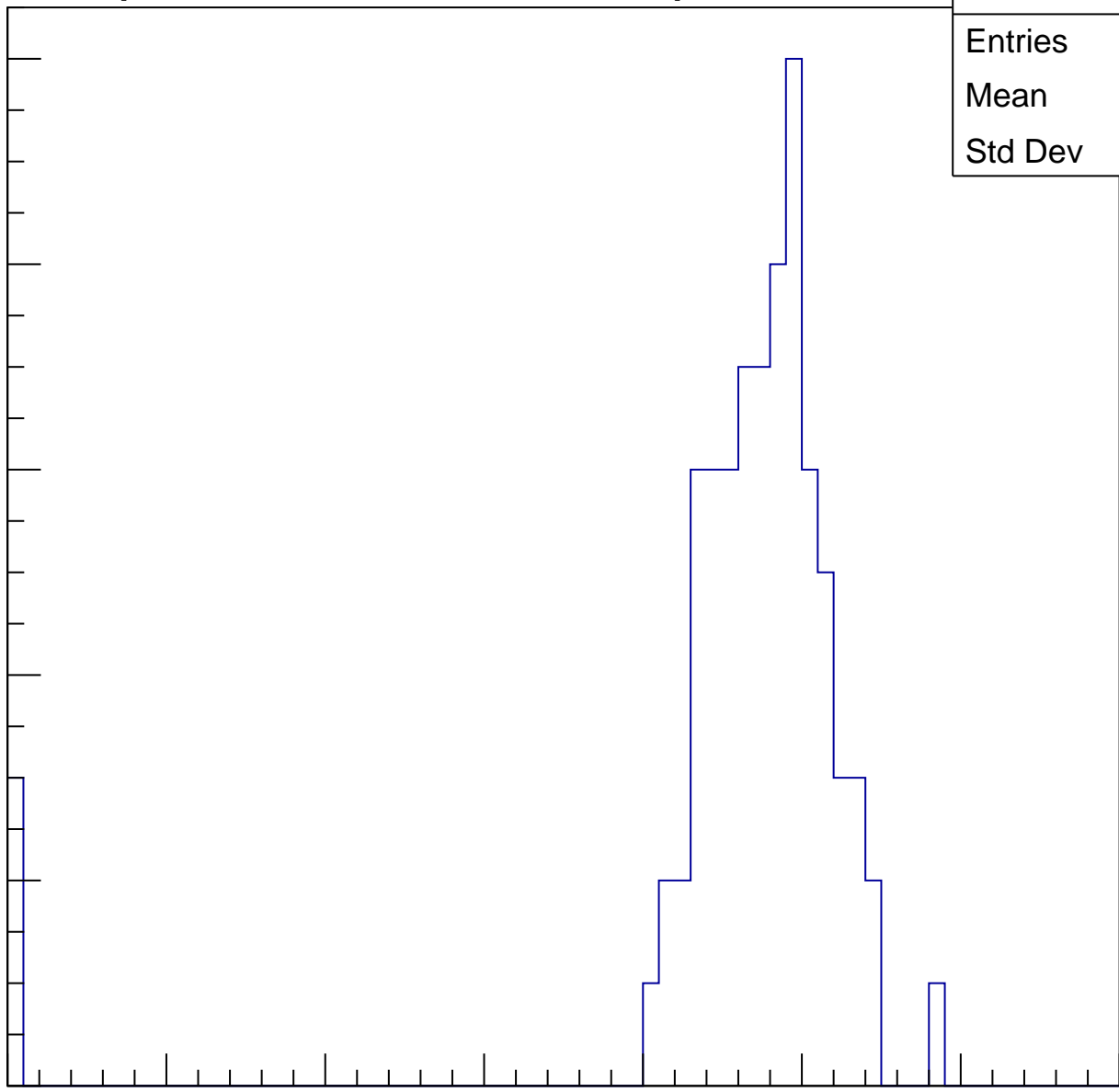
30

40

50

60

ampl

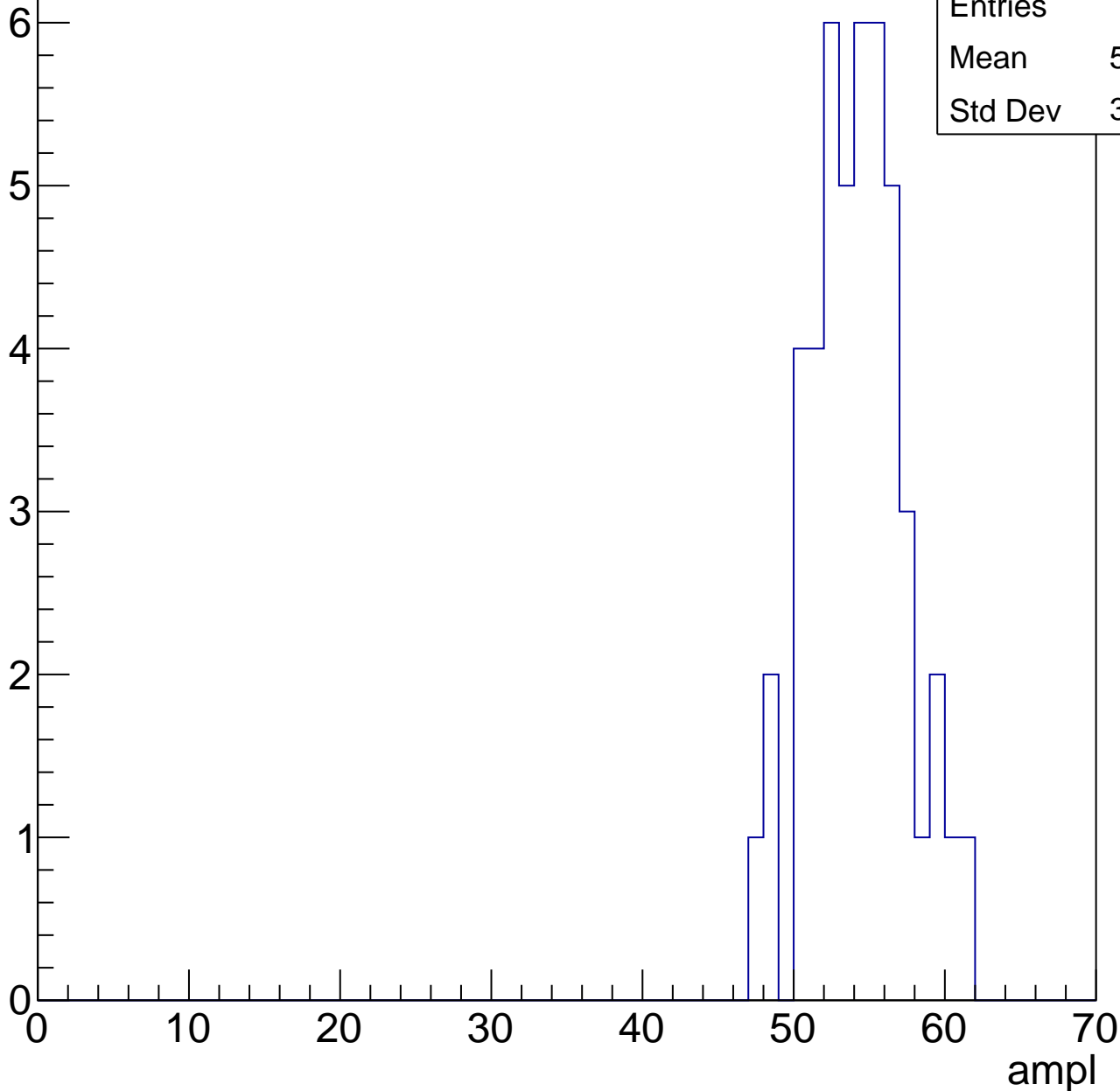


B1L103S, U2-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	53.74
Std Dev	3.097

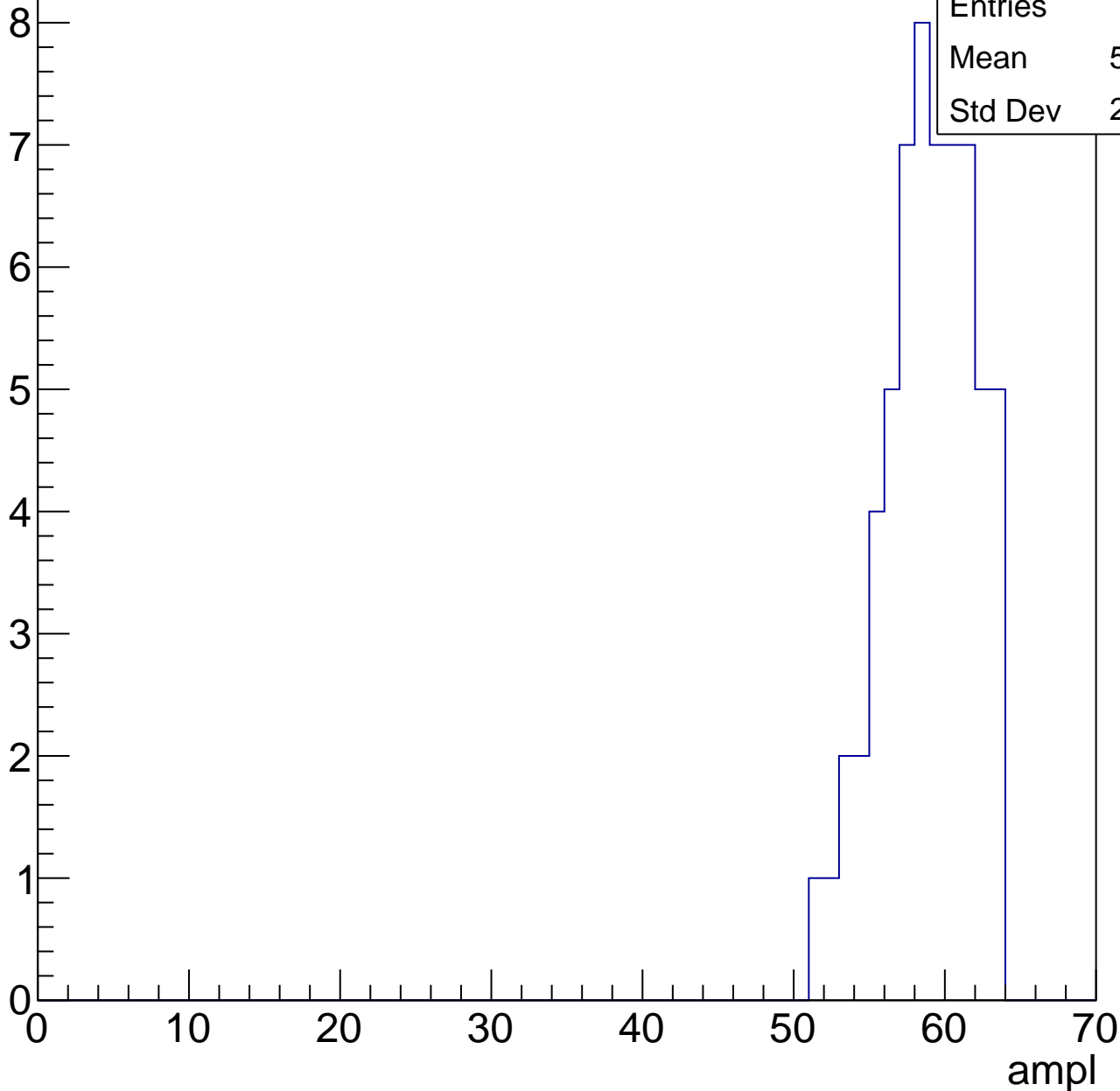


B1L103S, U2-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	58.44
Std Dev	2.923

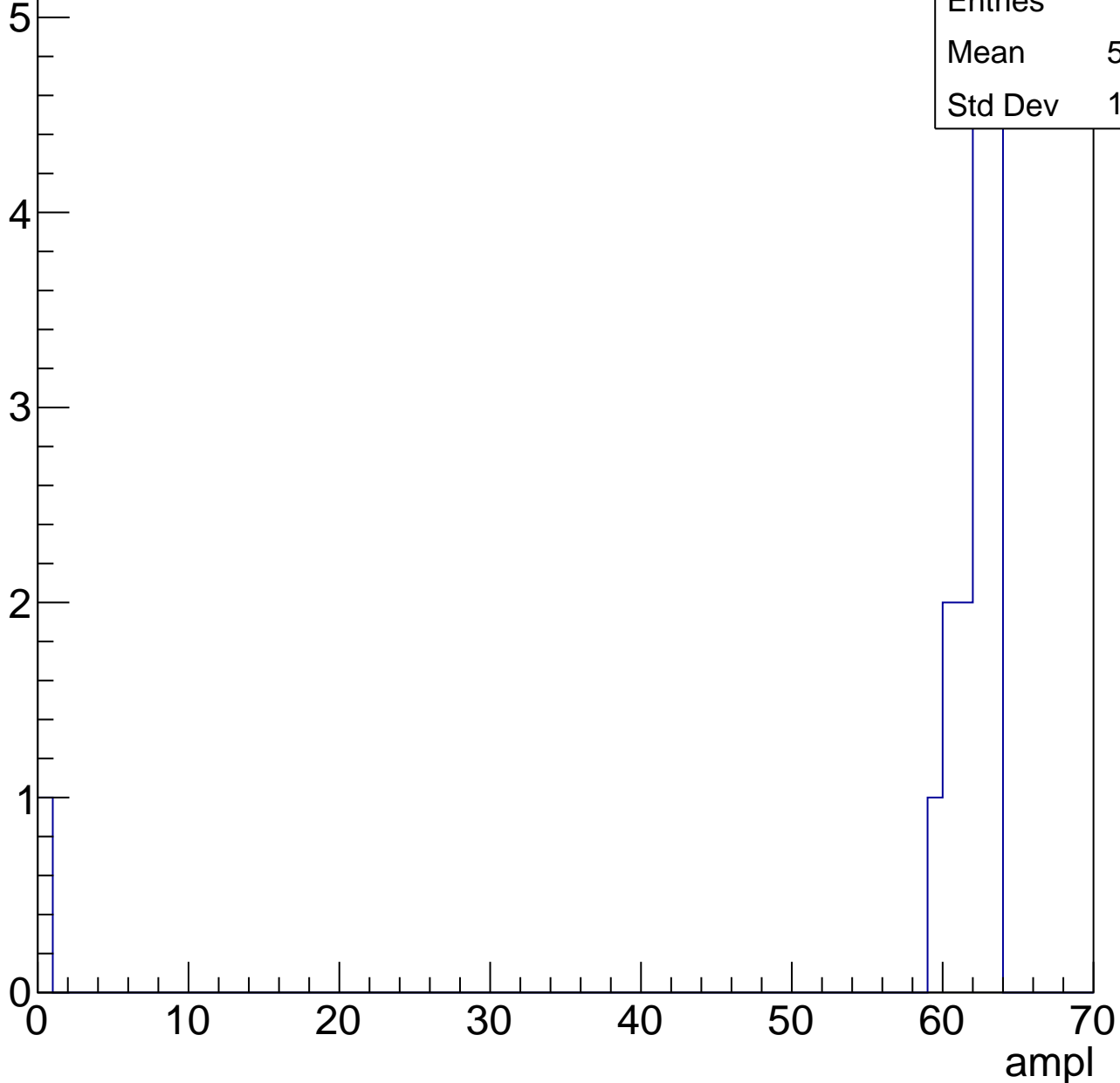


B1L103S, U2-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.88
Std Dev	14.99



B1L103S, U2-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

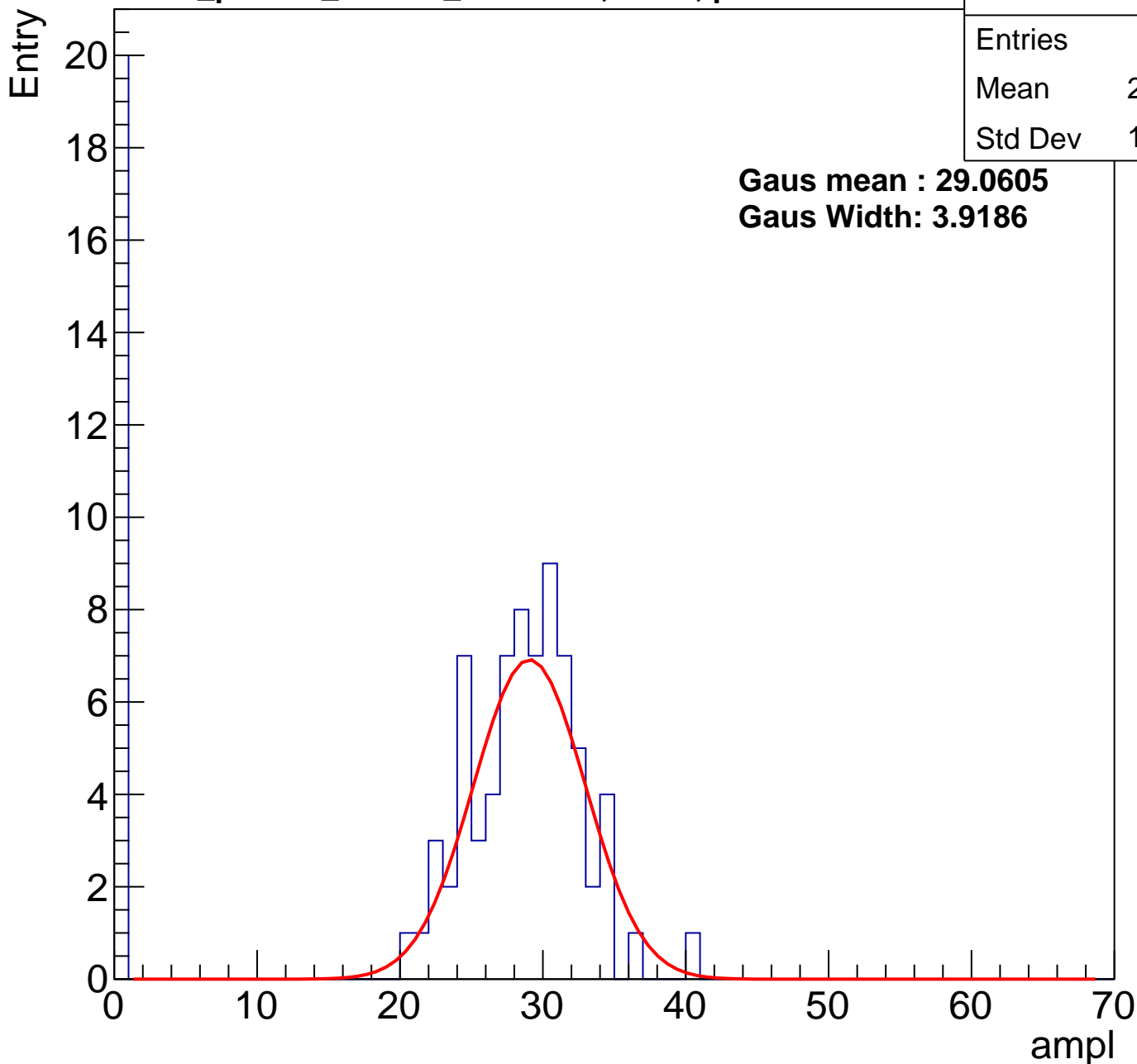
B1L103S, U2-ch38, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	22.18
Std Dev	12.15

Gaus mean : 29.0605

Gaus Width: 3.9186



B1L103S, U2-ch38, adc1

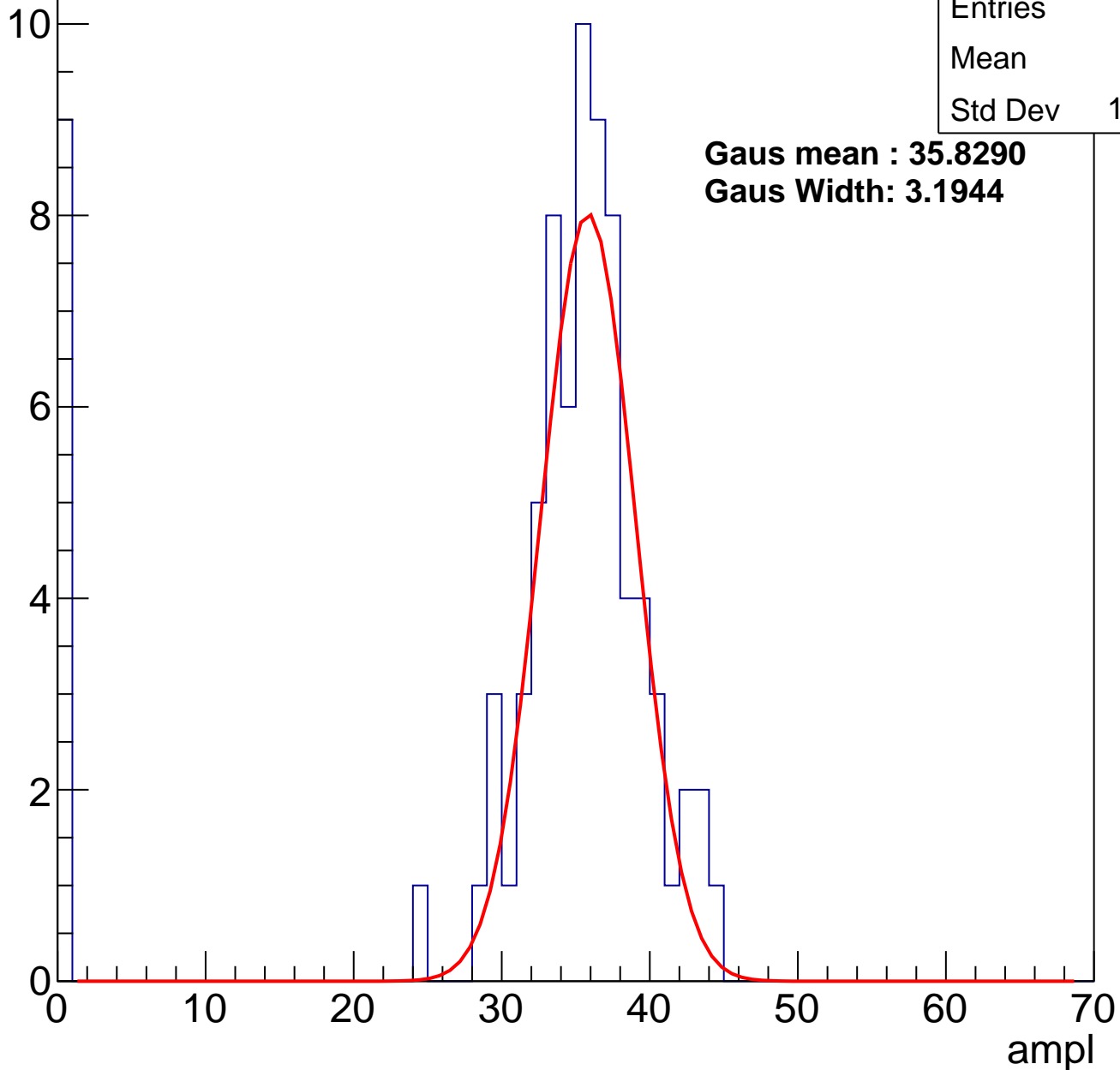
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	31.4
Std Dev	11.64

Gaus mean : 35.8290

Gaus Width: 3.1944

Entry



B1L103S, U2-ch38, adc2

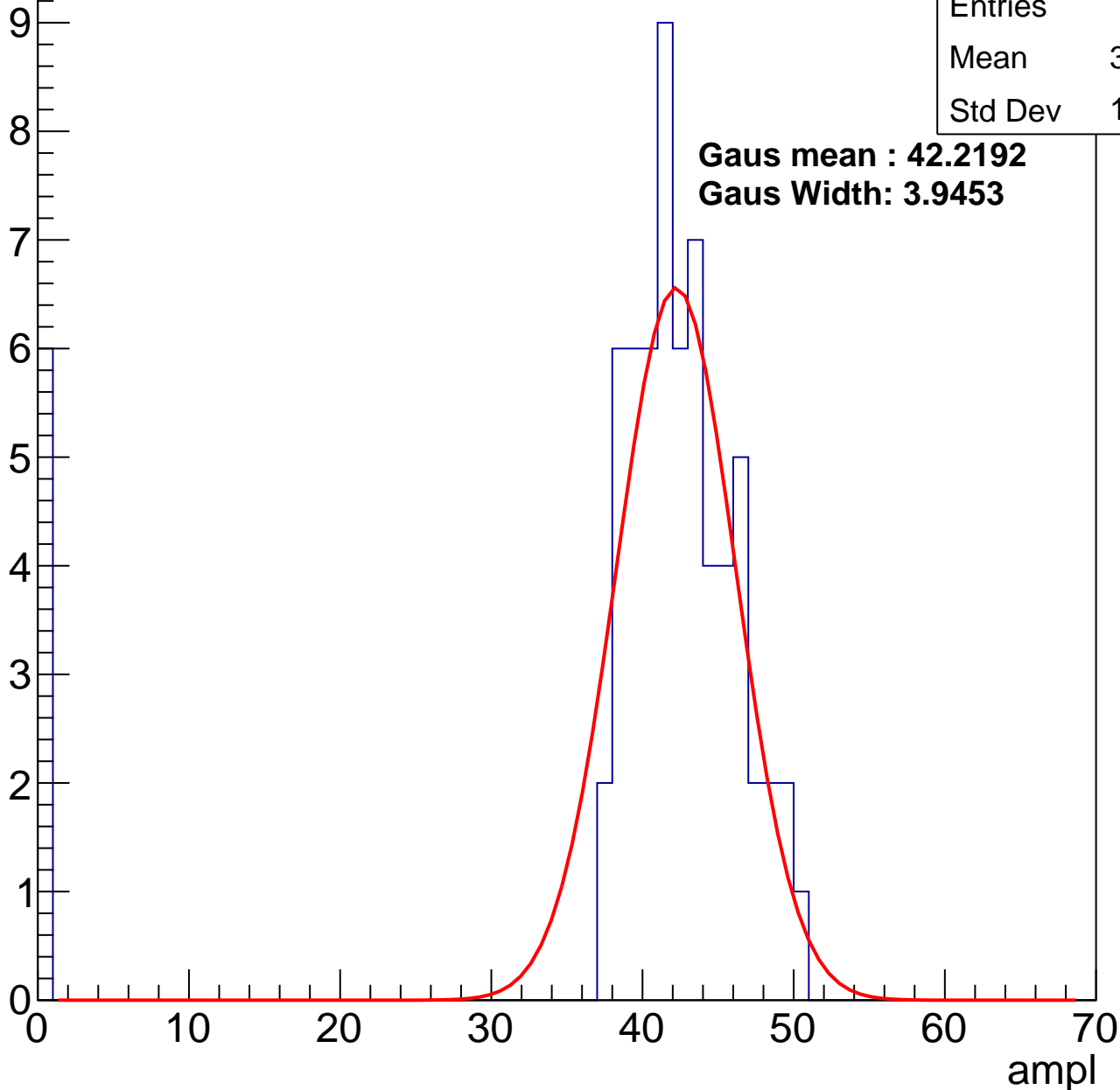
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.56
Std Dev	12.39

Gaus mean : 42.2192

Gaus Width: 3.9453

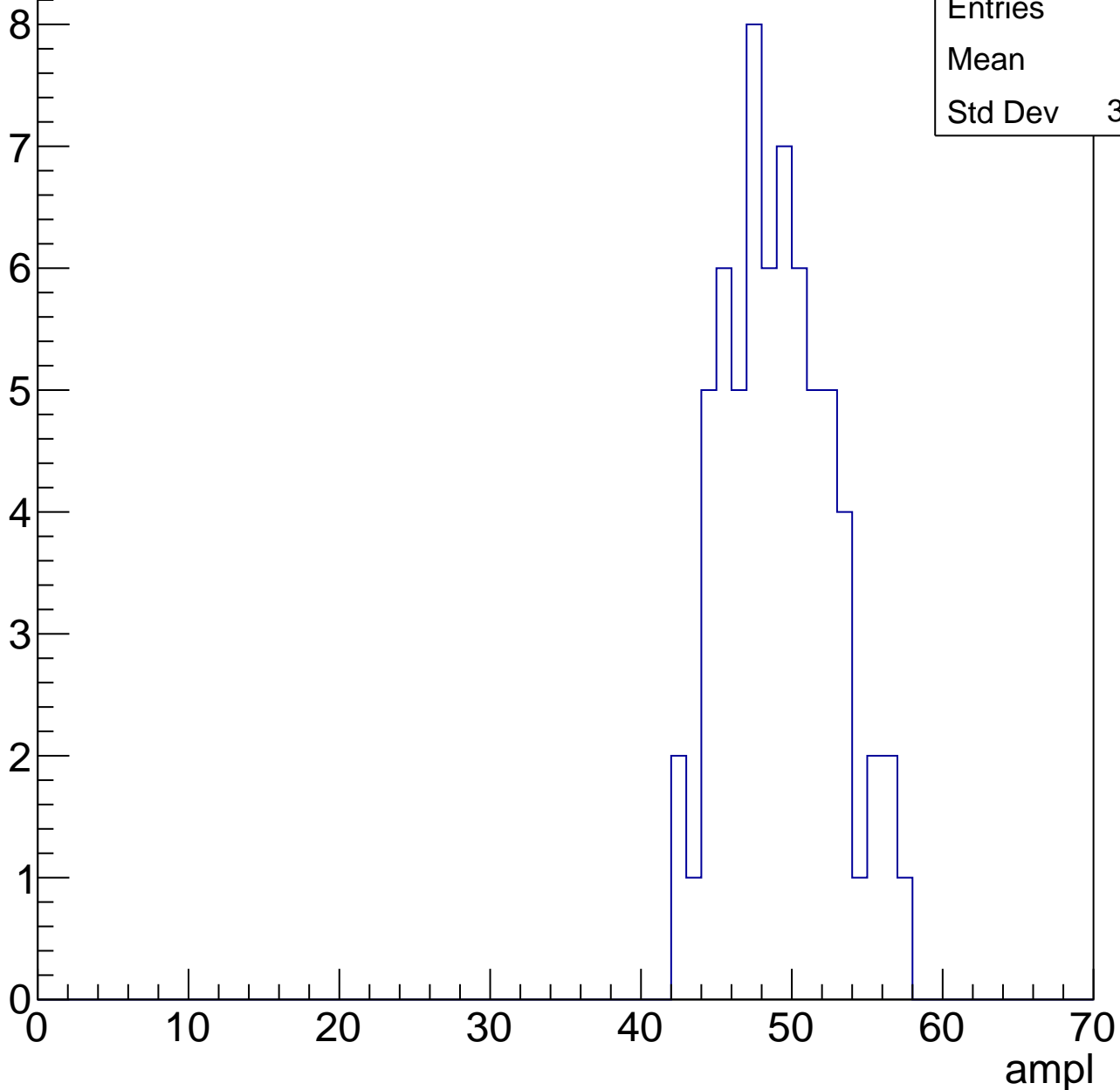


B1L103S, U2-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	48.7
Std Dev	3.538



B1L103S, U2-ch38, adc4

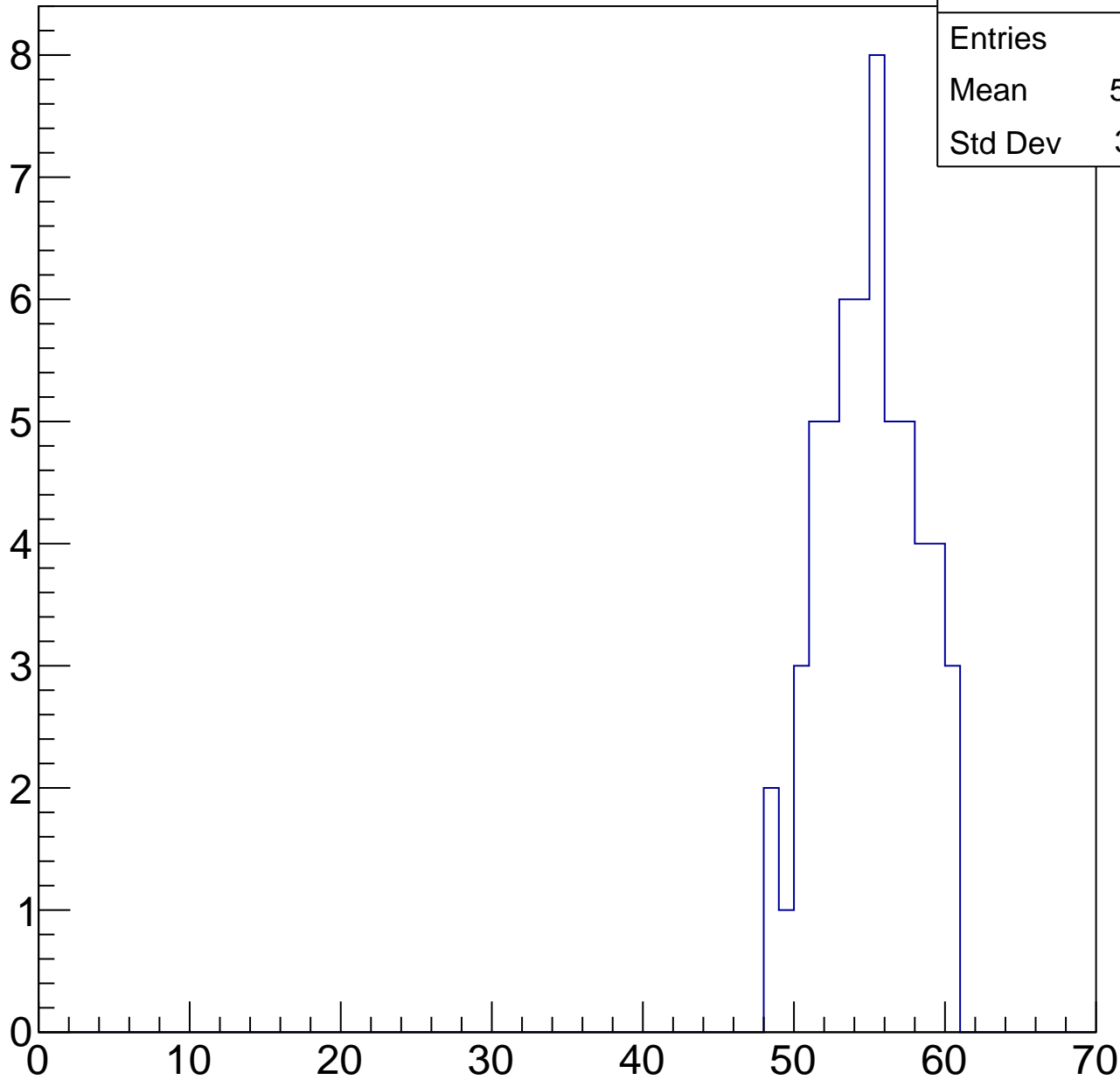
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	57
Mean	54.47
Std Dev	3.101

ampl

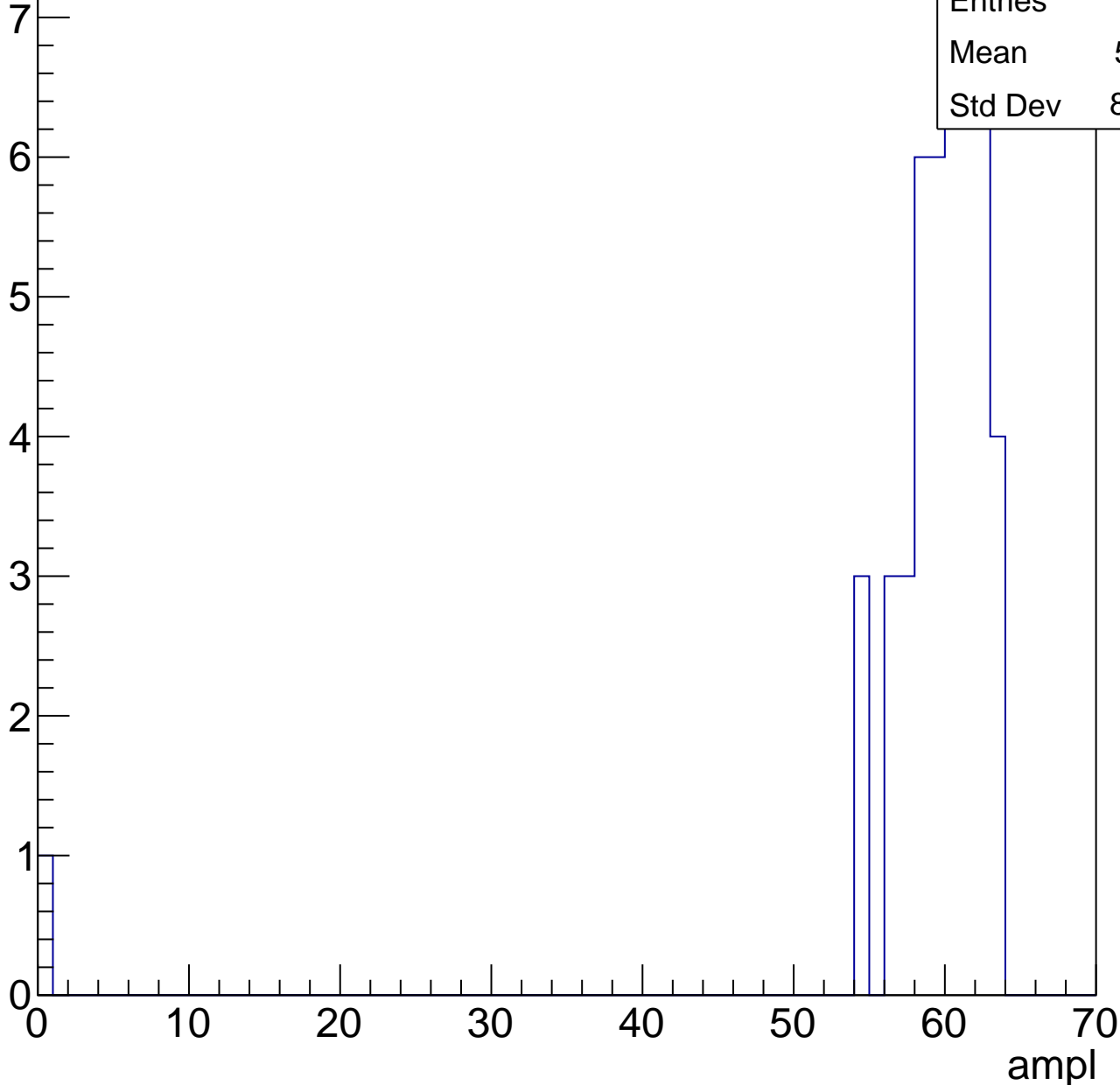


B1L103S, U2-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

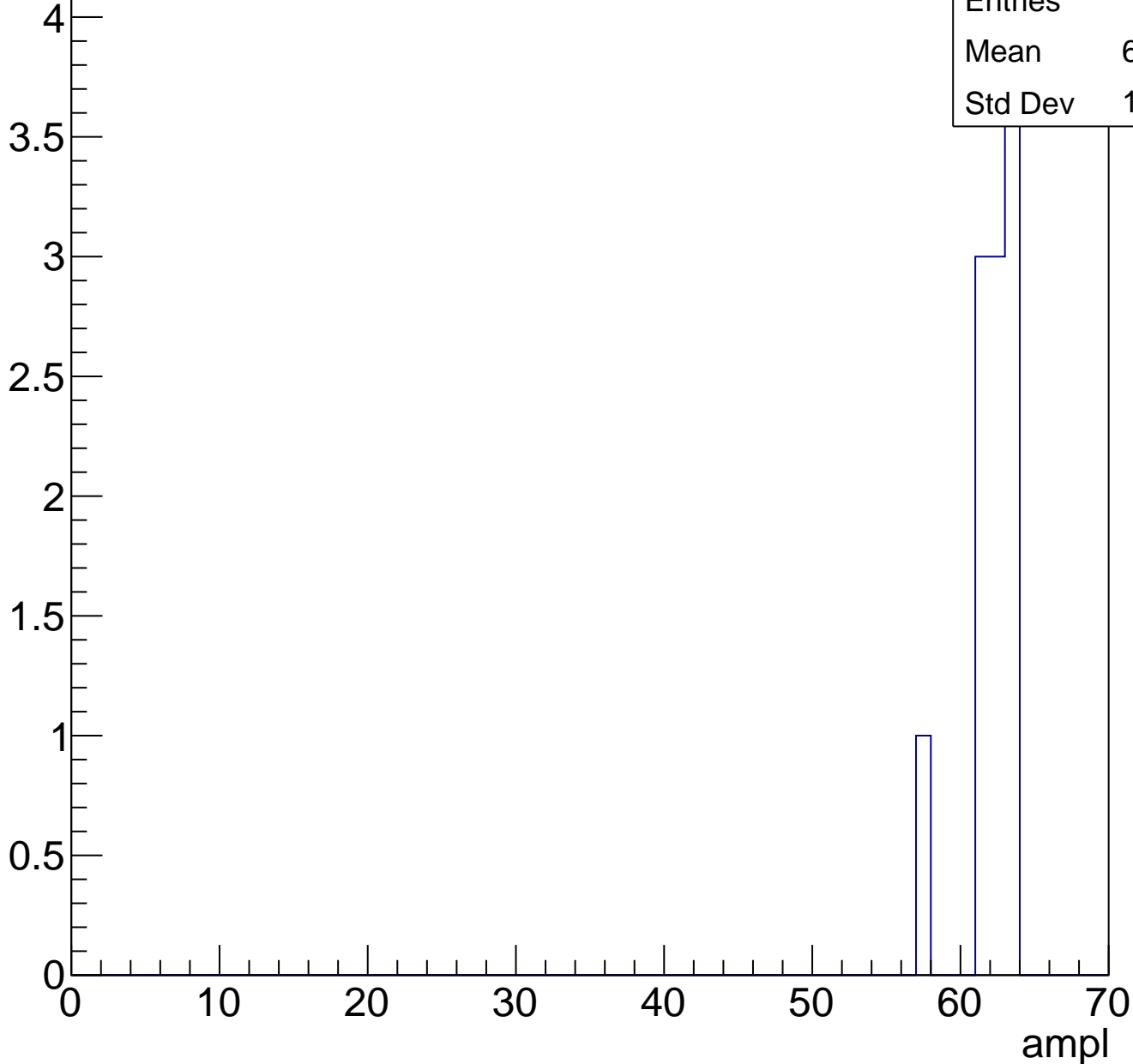
Entries	47
Mean	58.21
Std Dev	8.913



B1L103S, U2-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U2-ch39, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	22.22
Std Dev	13.97

Gaus mean : 30.8530

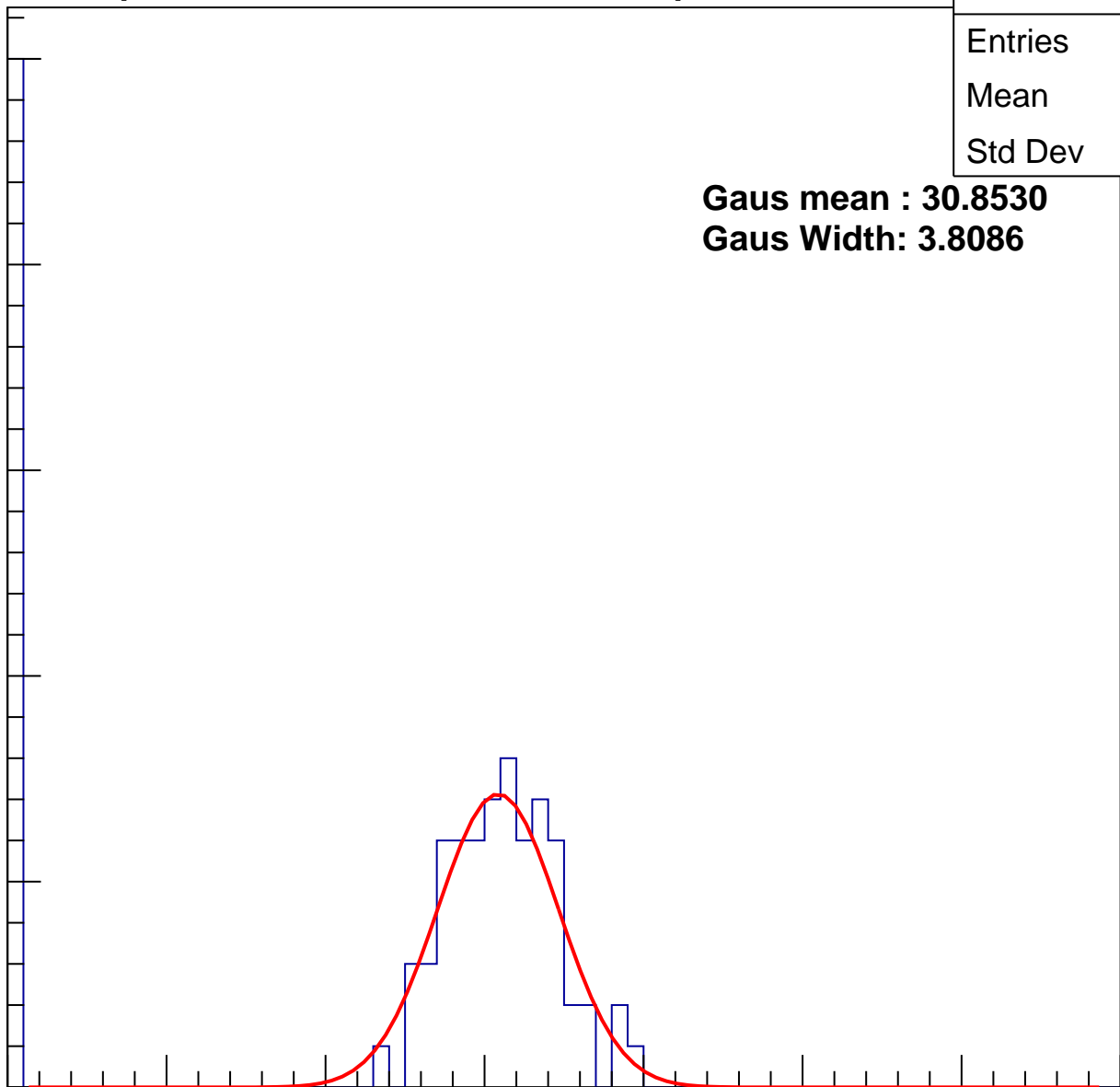
Gaus Width: 3.8086

Entry

25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch39, adc1

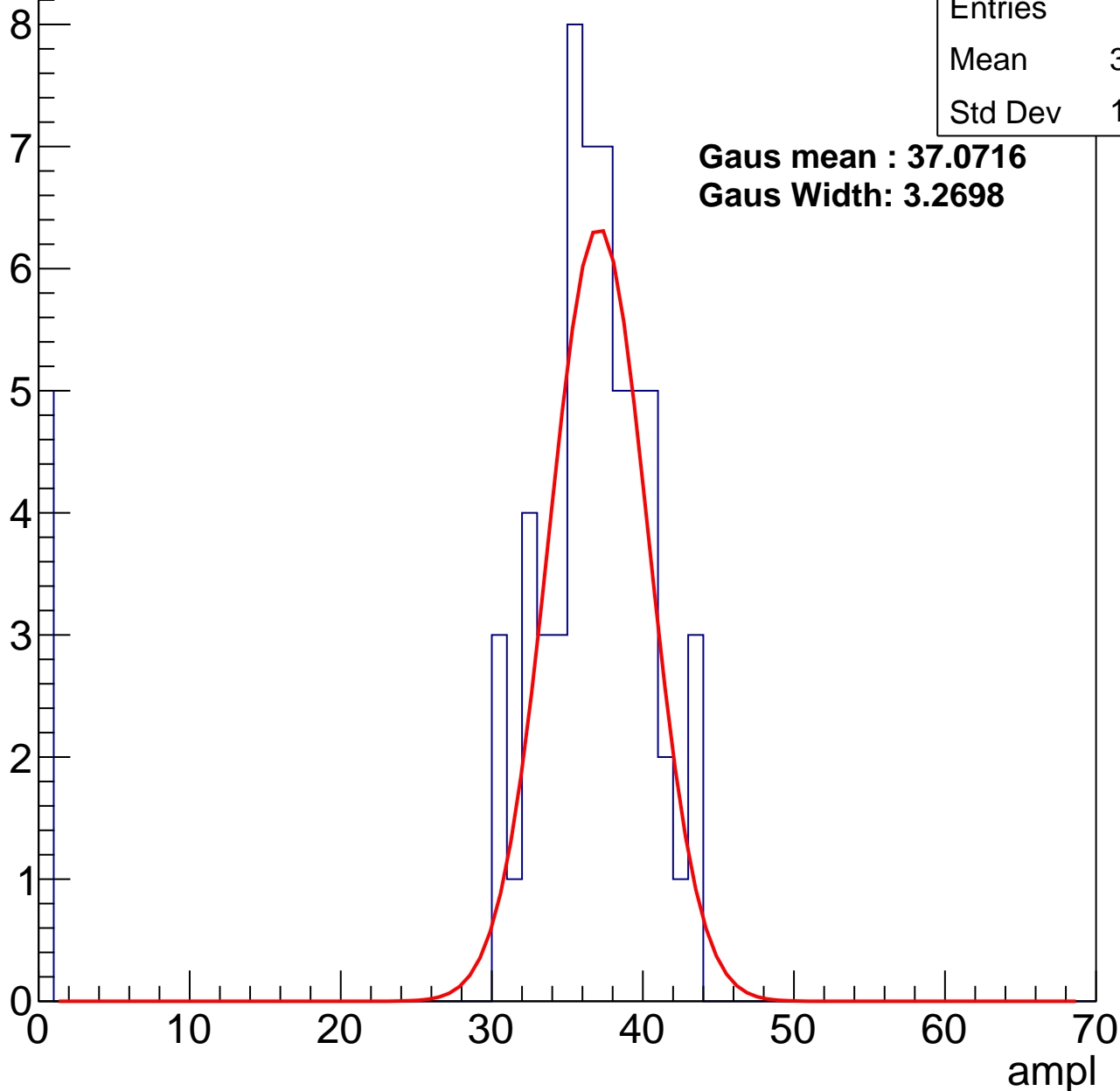
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	33.53
Std Dev	10.42

Gaus mean : 37.0716

Gaus Width: 3.2698



B1L103S, U2-ch39, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	36.75
Std Dev	15.47

Gaus mean : 43.5730

Gaus Width: 4.0341

Entry

10

8

6

4

2

0

0

10

20

30

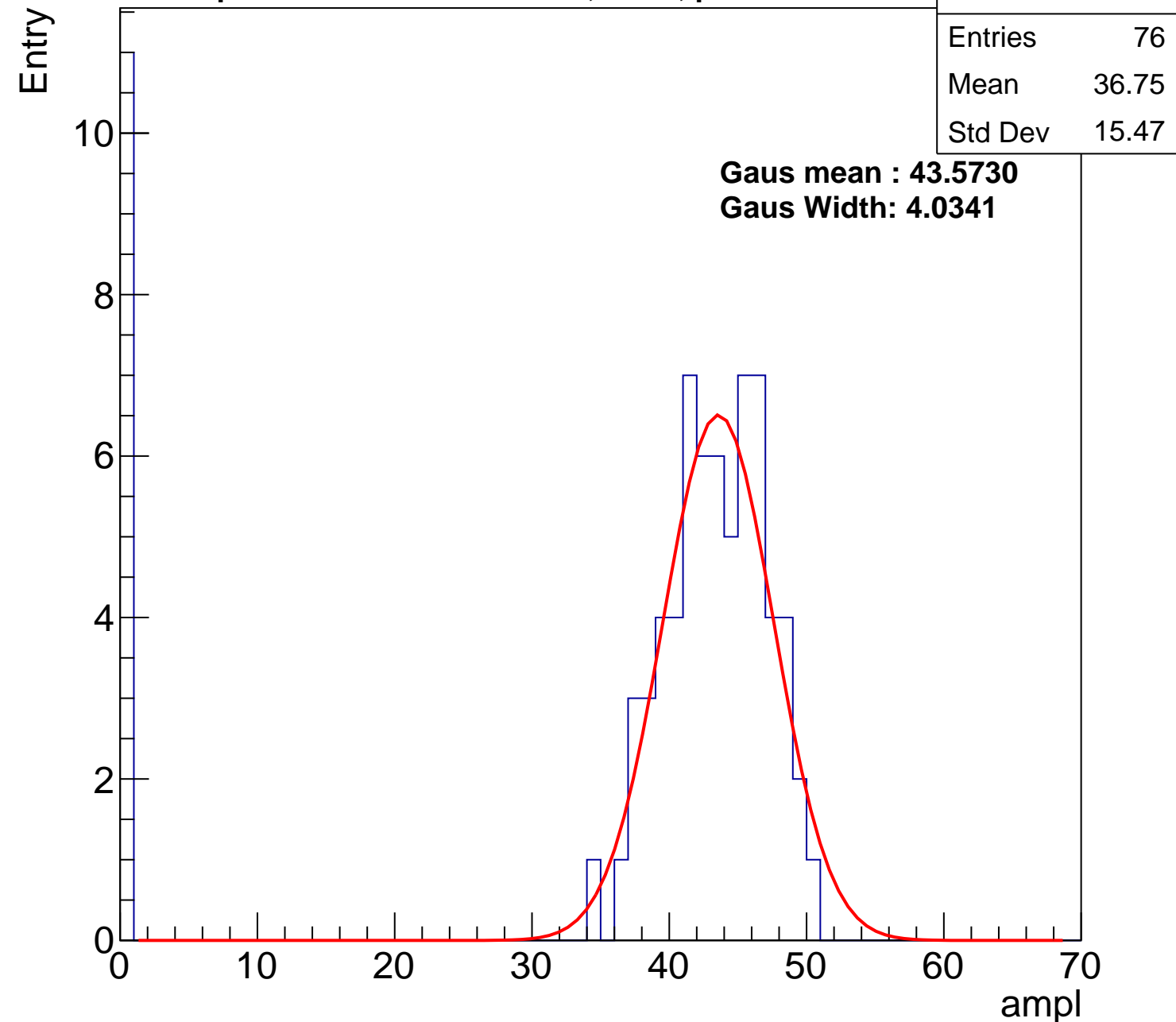
40

50

60

70

ampl

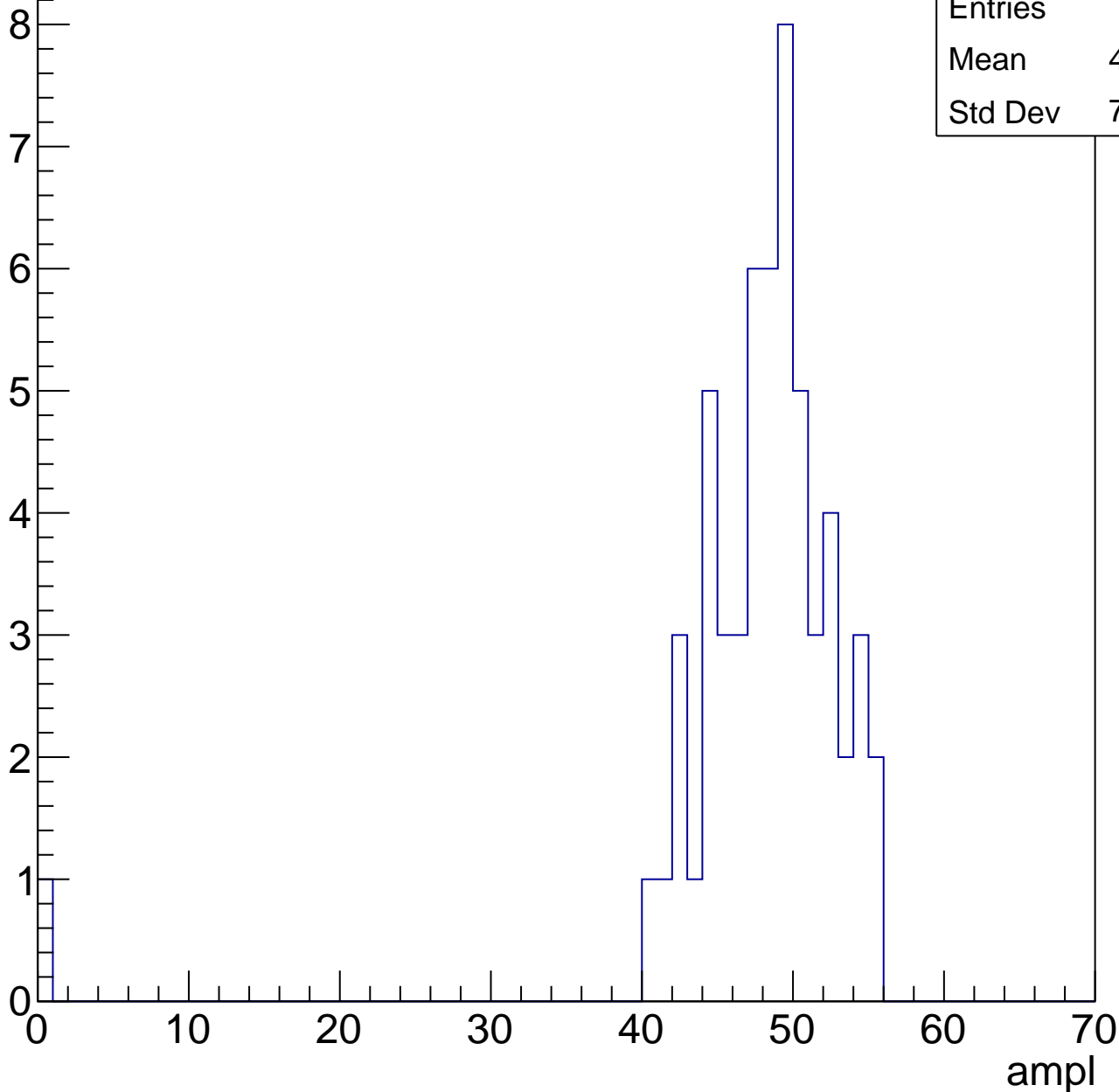


B1L103S, U2-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

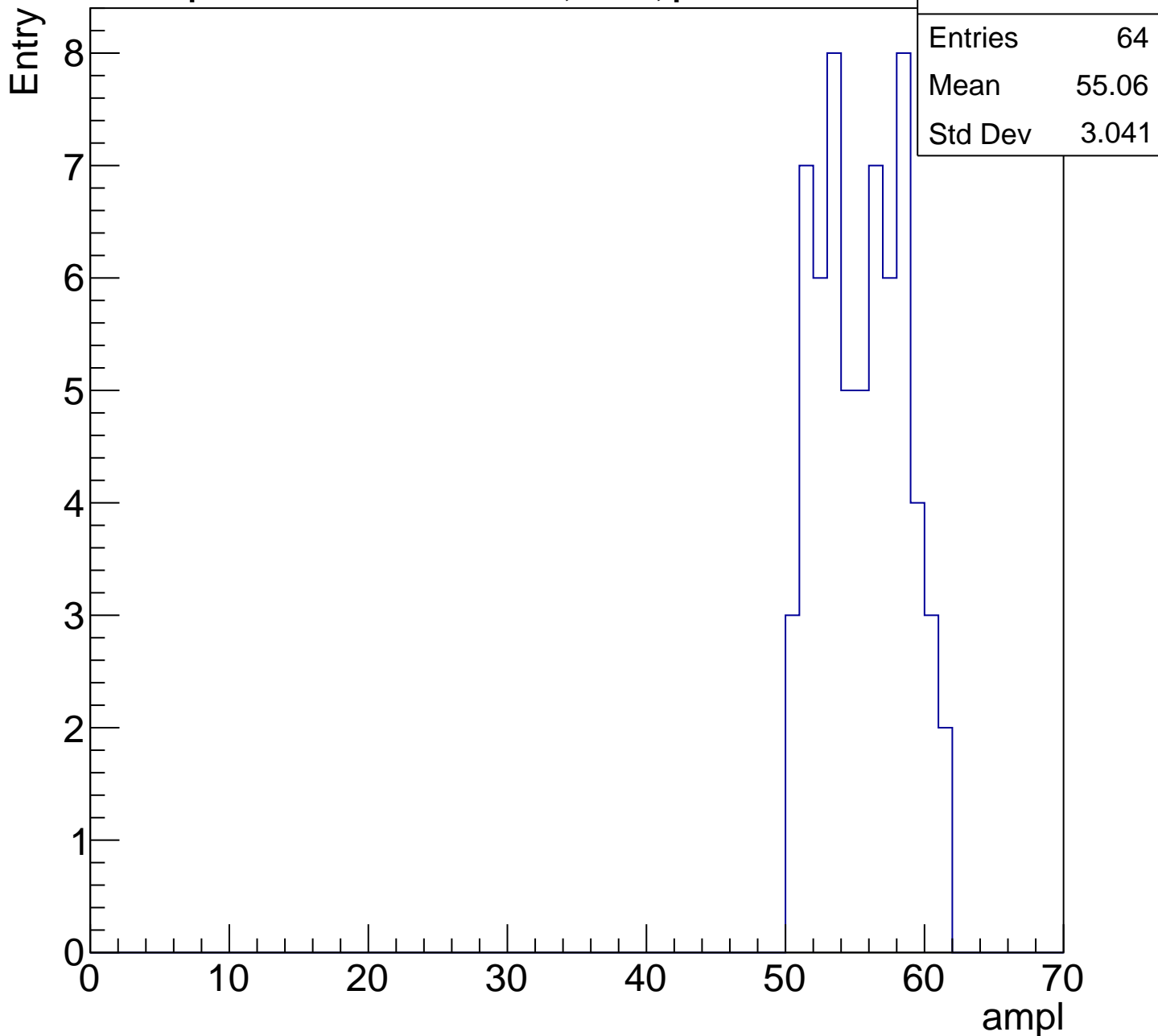
Entry

Entries	57
Mean	47.26
Std Dev	7.278



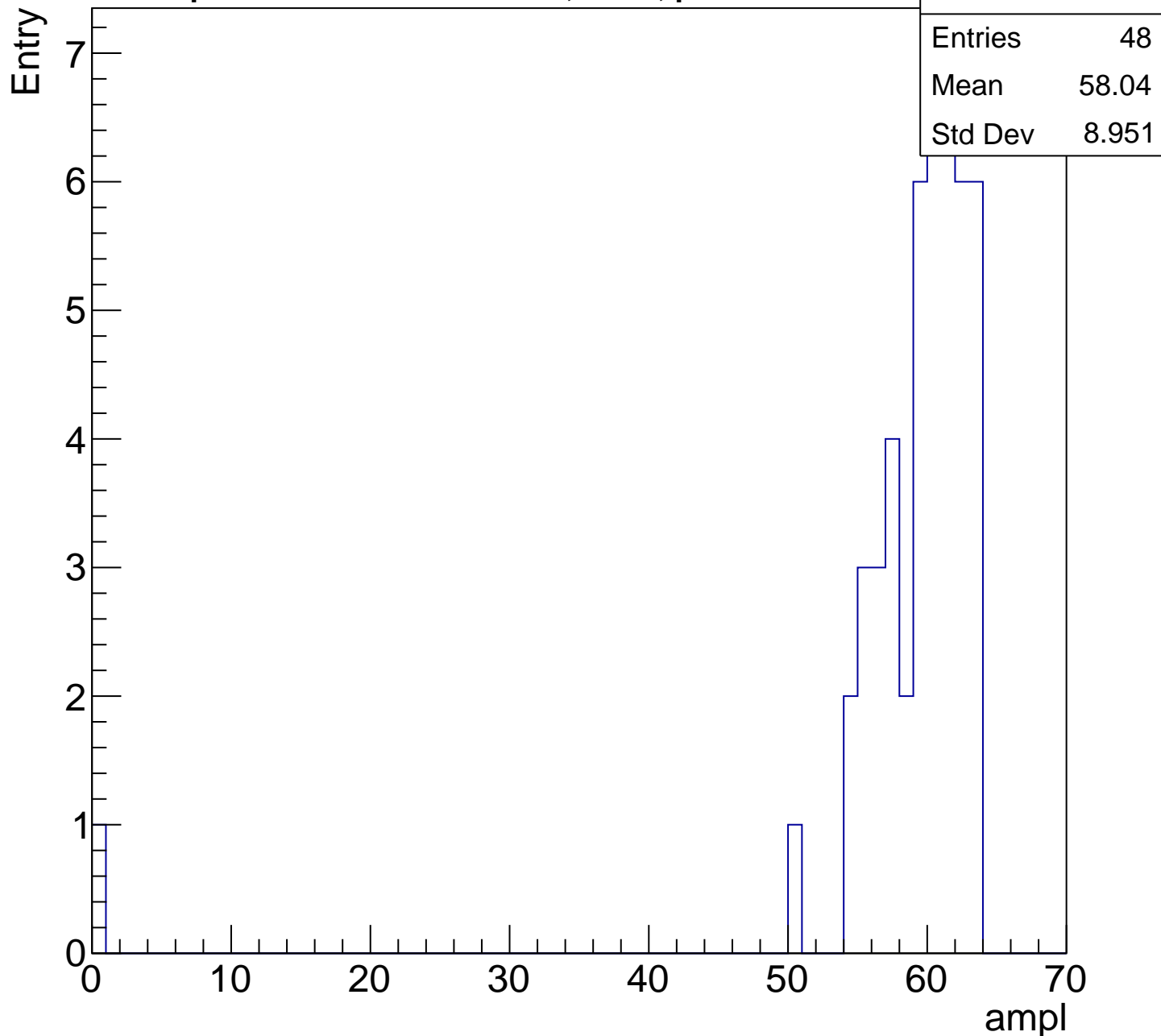
B1L103S, U2-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch39, adc5

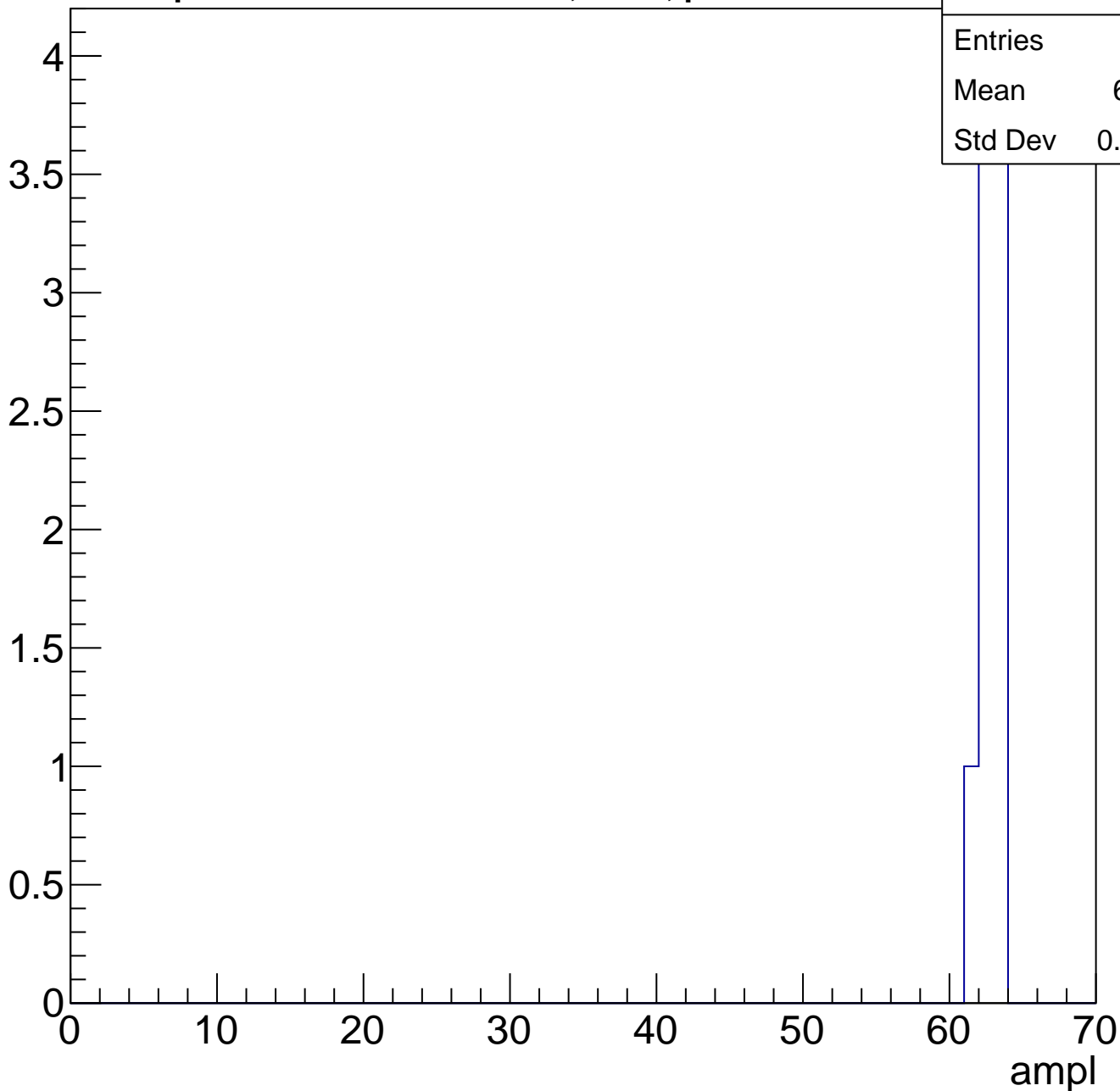
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U2-ch40, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	24.34
Std Dev	10.08

Gaus mean : 28.4272

Gaus Width: 3.1565

Entry

10

8

6

4

2

0

0

10

20

30

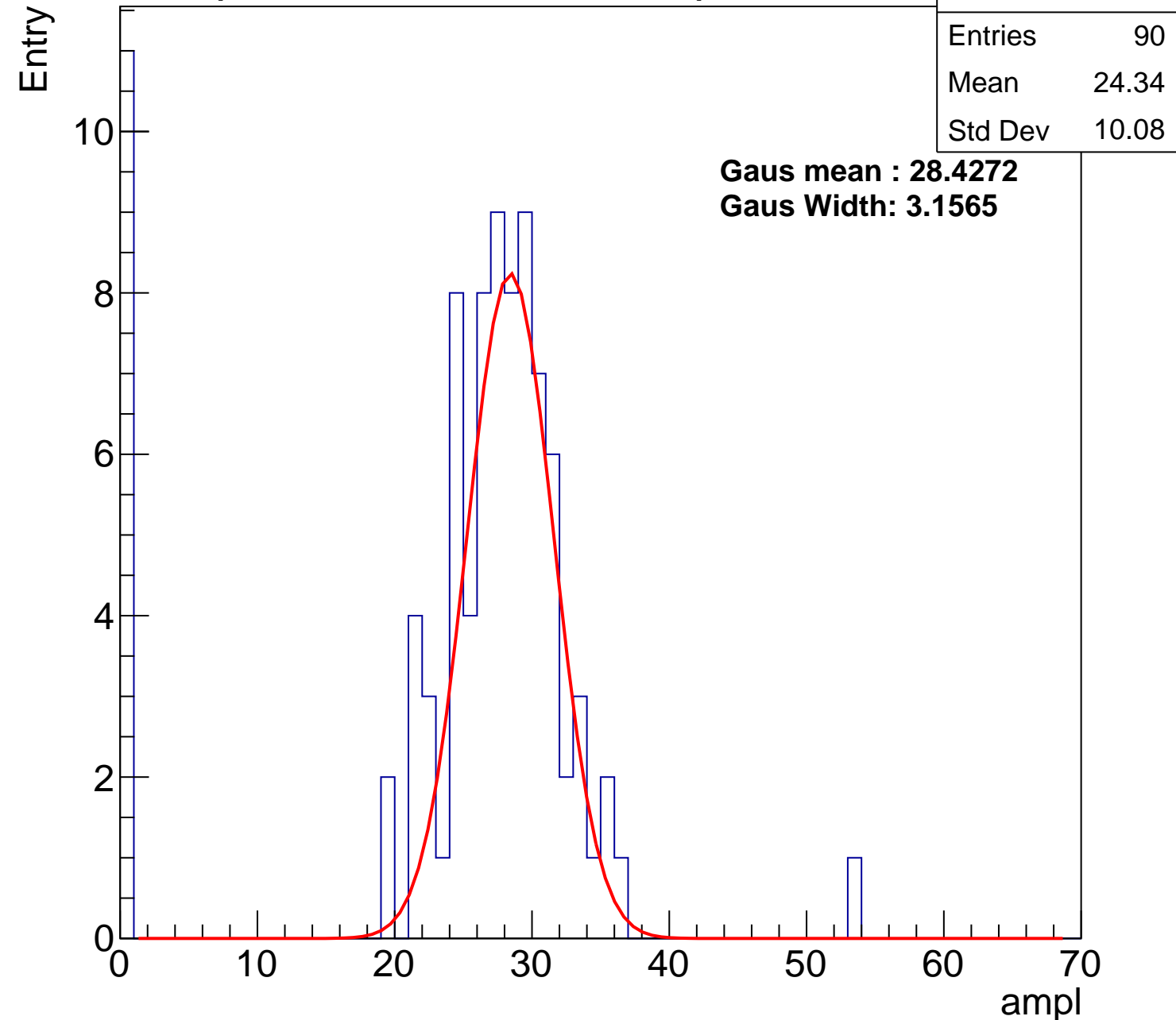
40

50

60

70

ampl



B1L103S, U2-ch40, adc1

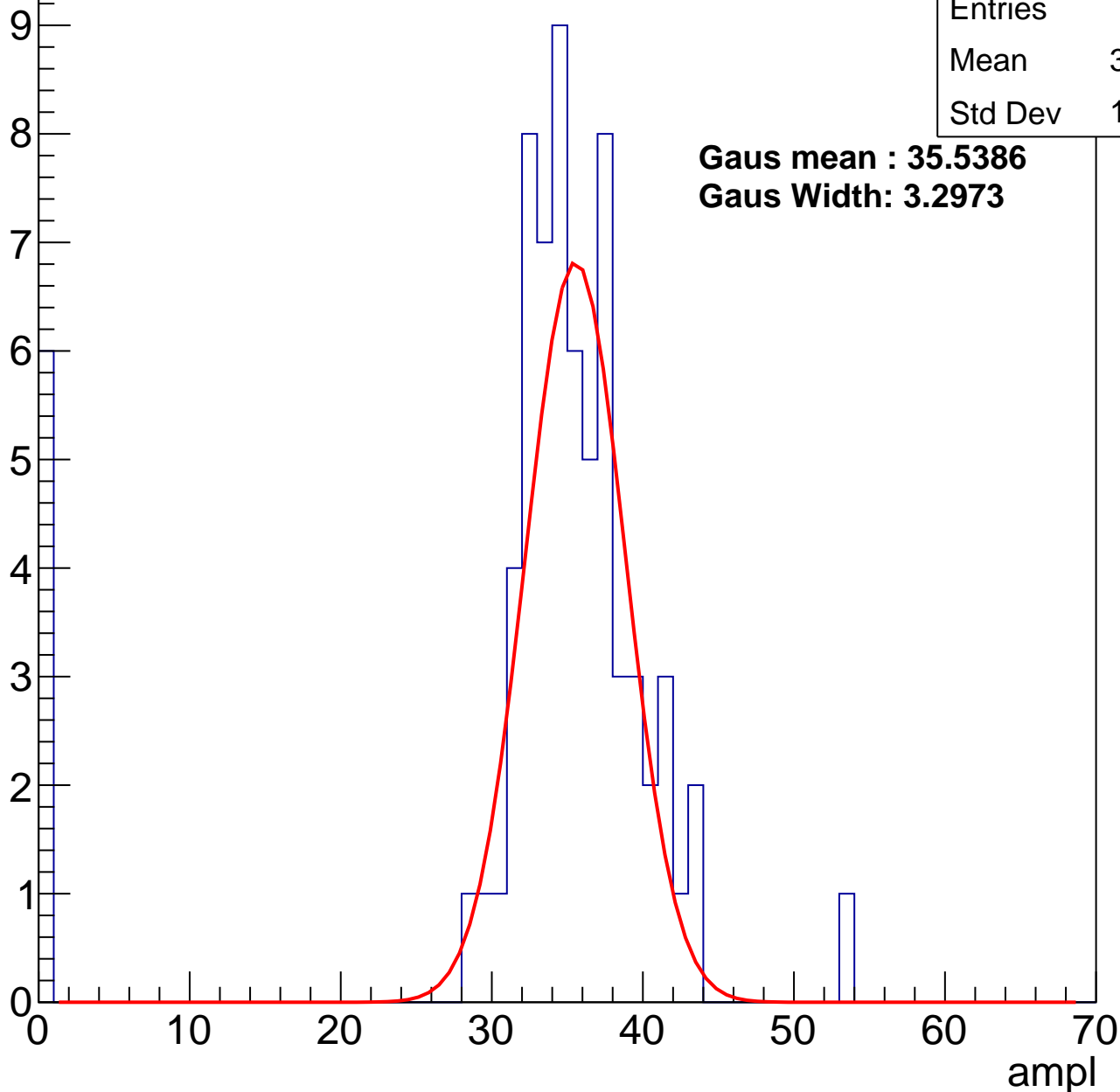
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	32.46
Std Dev	10.58

Gaus mean : 35.5386

Gaus Width: 3.2973



B1L103S, U2-ch40, adc2

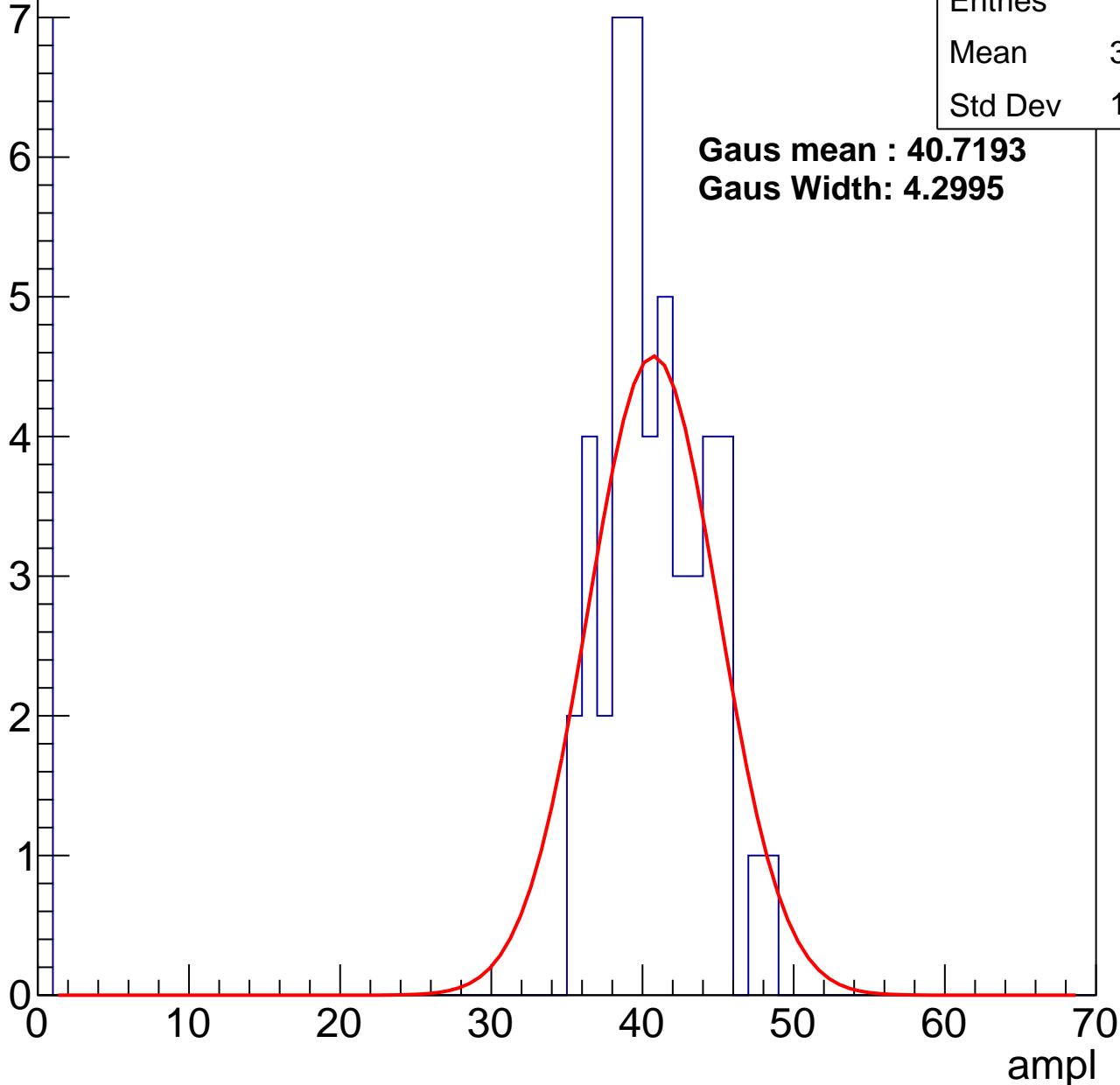
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	35.15
Std Dev	13.89

Gaus mean : 40.7193

Gaus Width: 4.2995

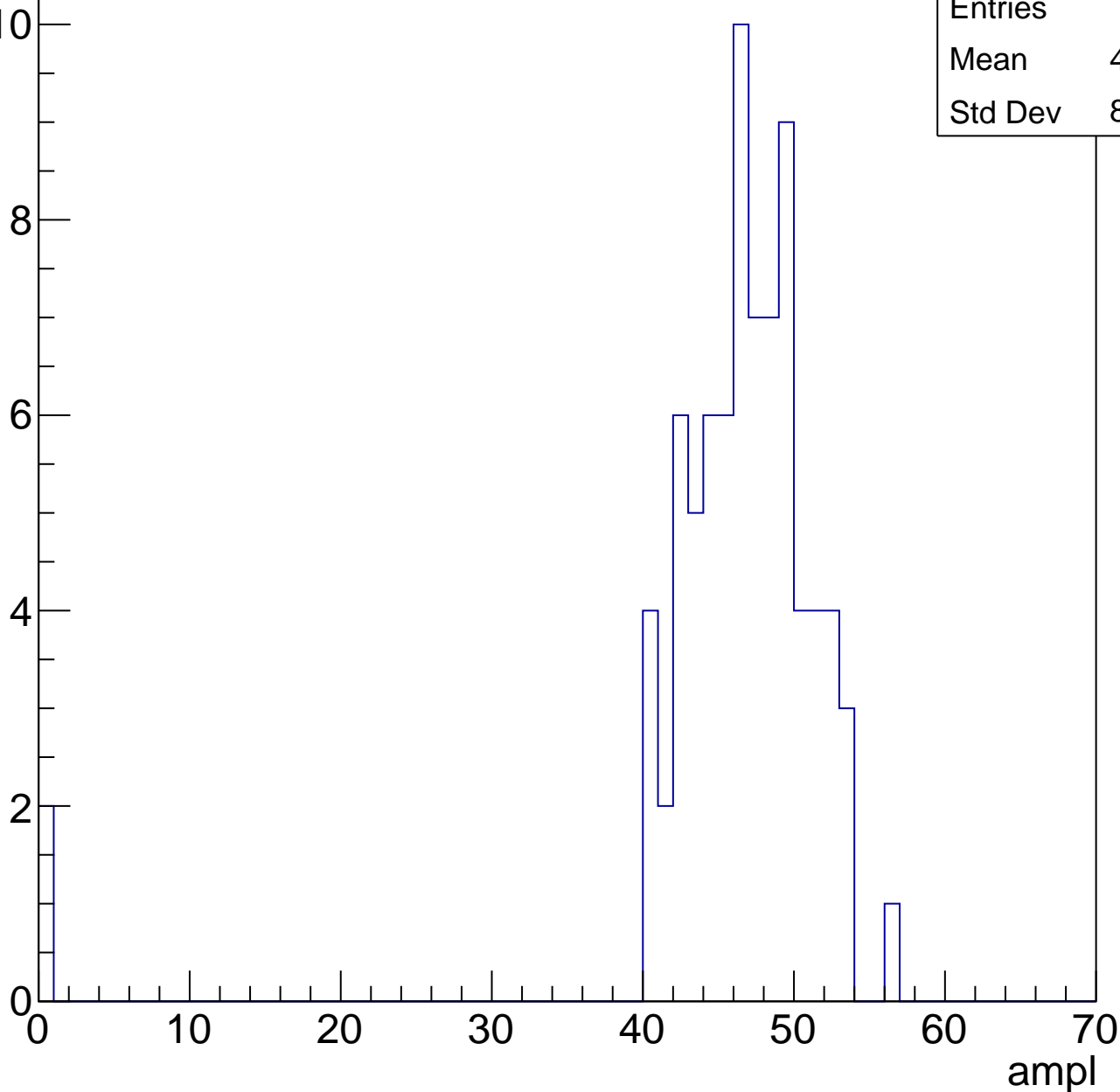


B1L103S, U2-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	45.45
Std Dev	8.096

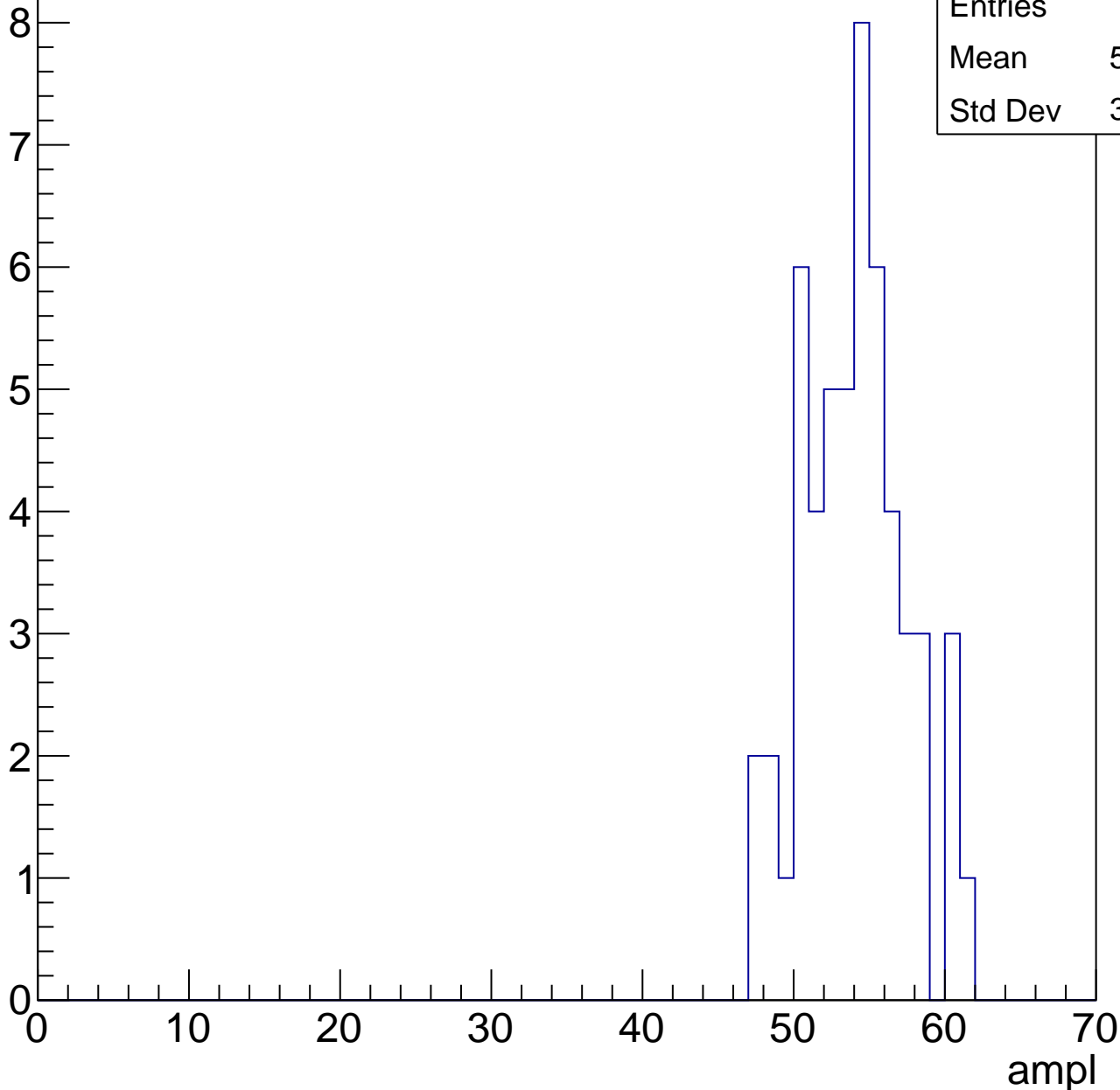


B1L103S, U2-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	53.58
Std Dev	3.356



B1L103S, U2-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

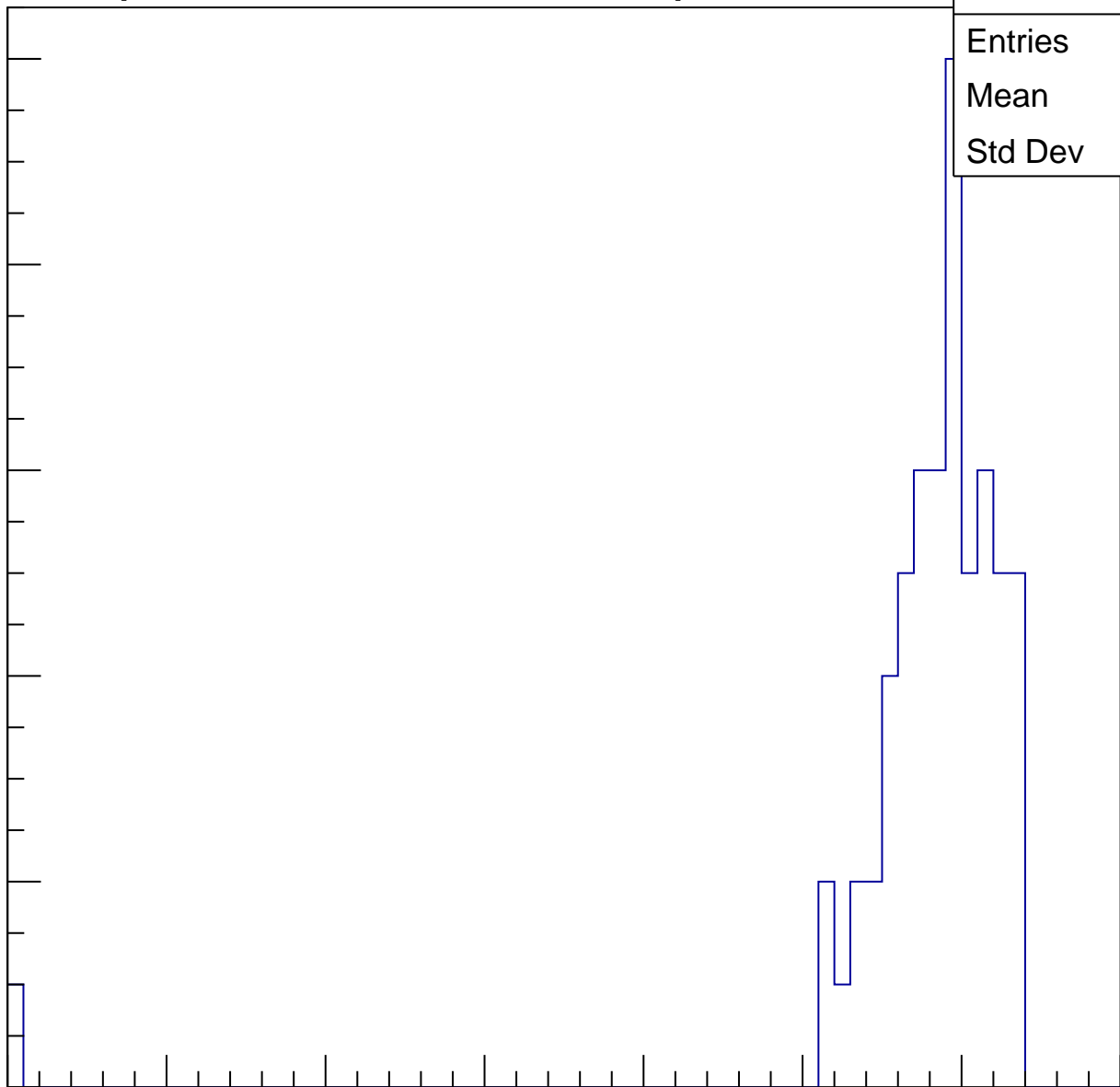
Entries	60
Mean	57.32
Std Dev	8.065

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

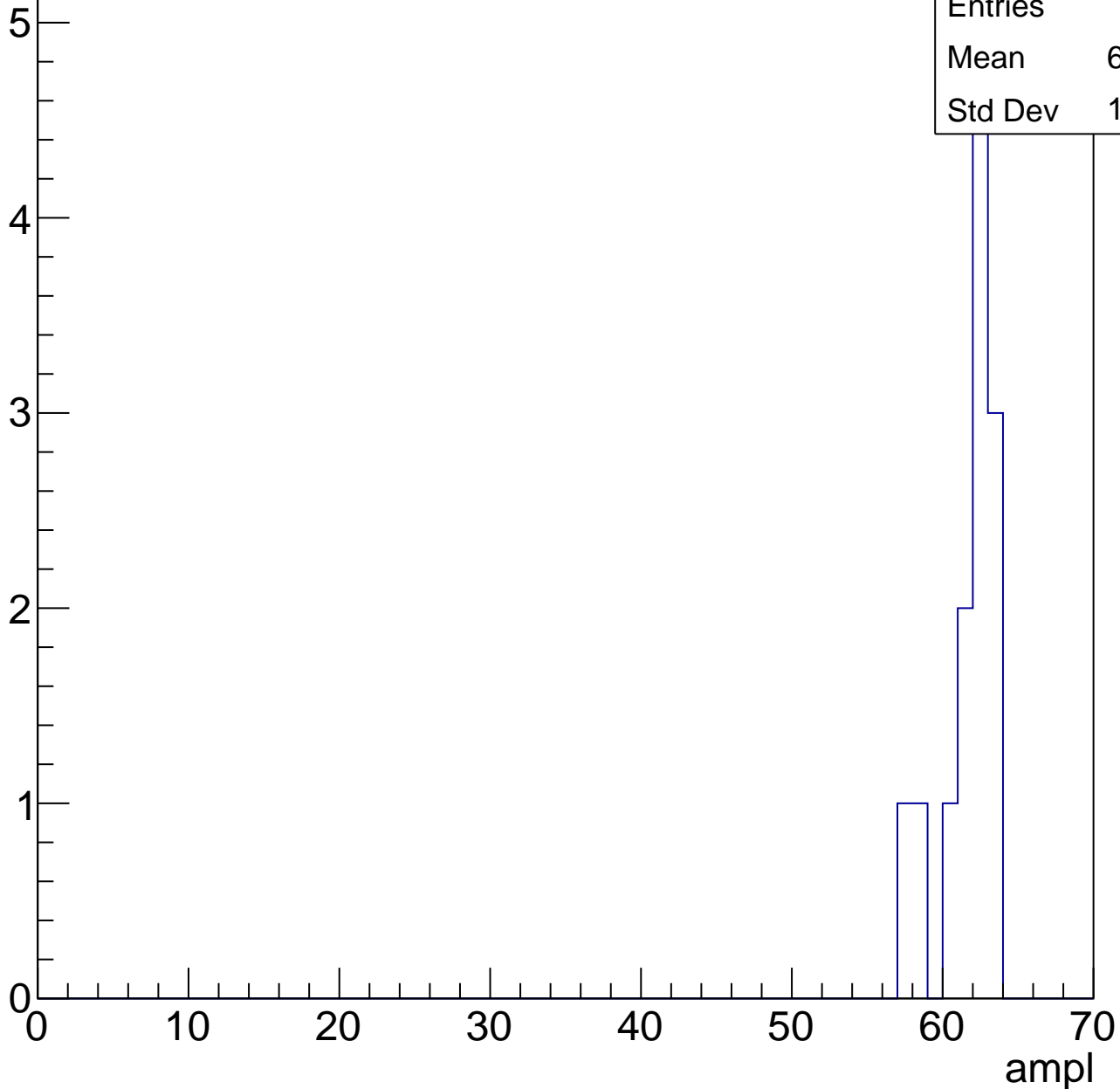


B1L103S, U2-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.23
Std Dev	1.804

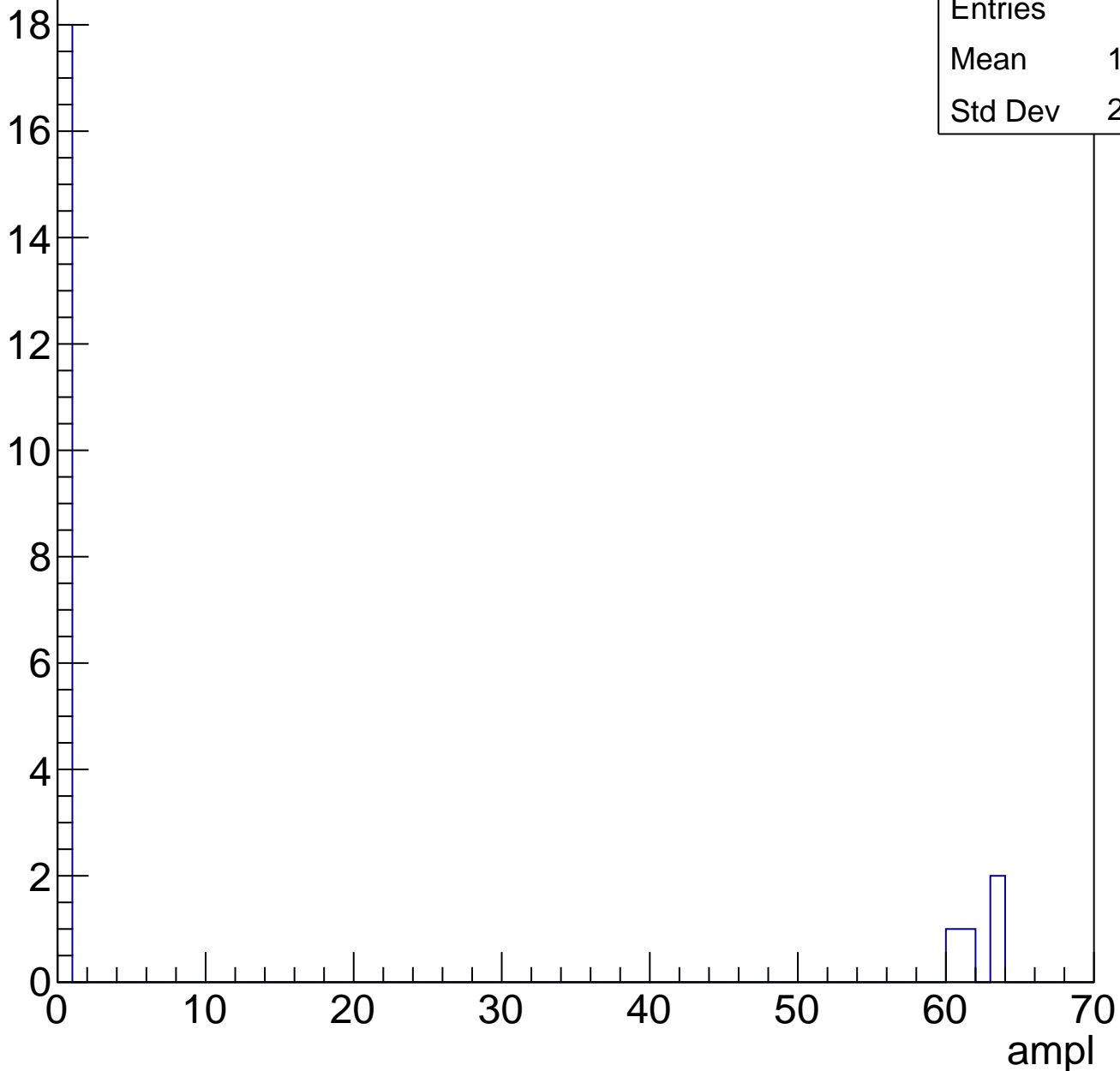


B1L103S, U2-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	11.23
Std Dev	23.82

Entry



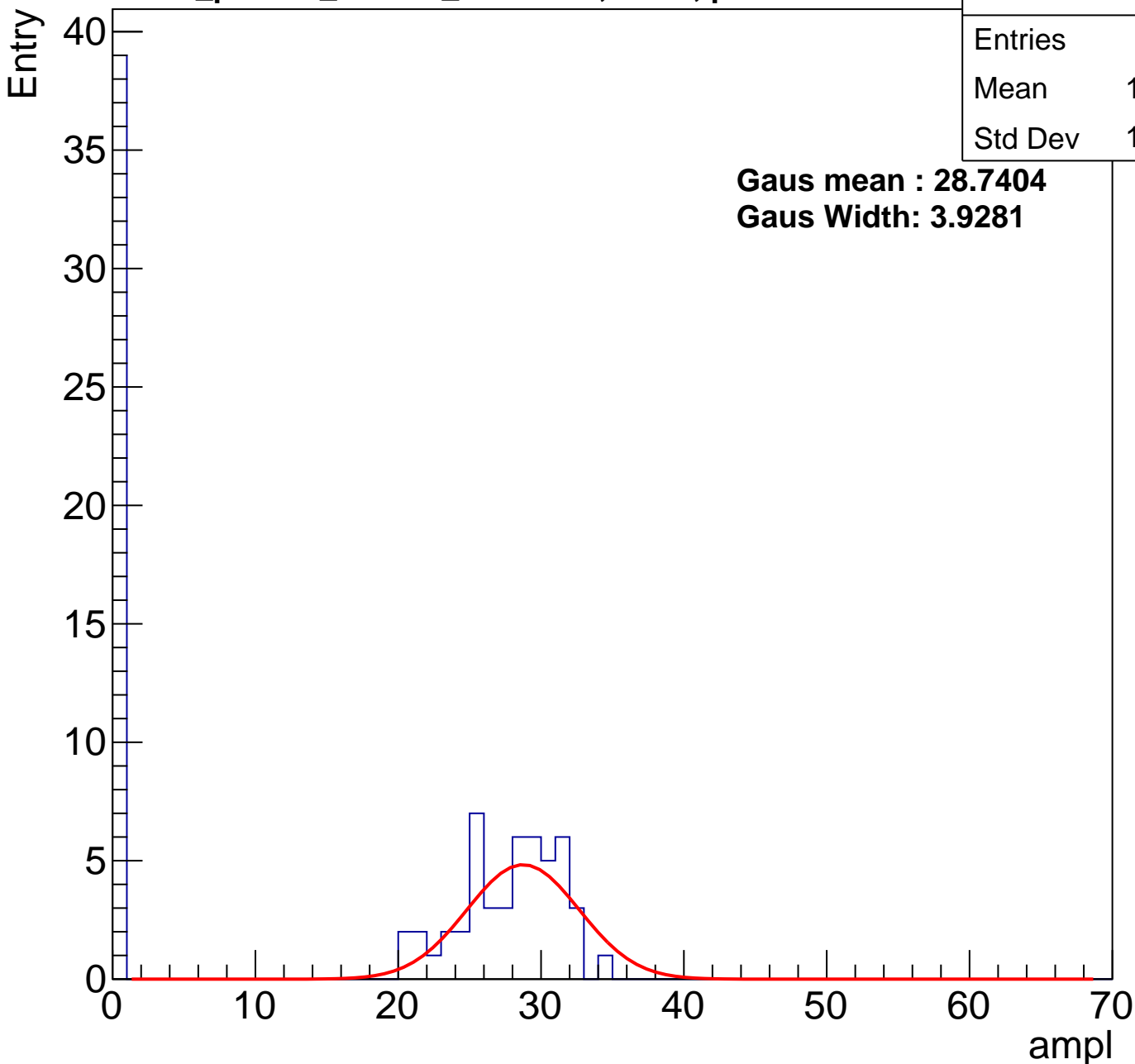
B1L103S, U2-ch41, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	15.23
Std Dev	13.82

Gaus mean : 28.7404

Gaus Width: 3.9281



B1L103S, U2-ch41, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	30.8
Std Dev	11.89

Gaus mean : 35.3734

Gaus Width: 4.2152

Entry

10

8

6

4

2

0

0

10

20

30

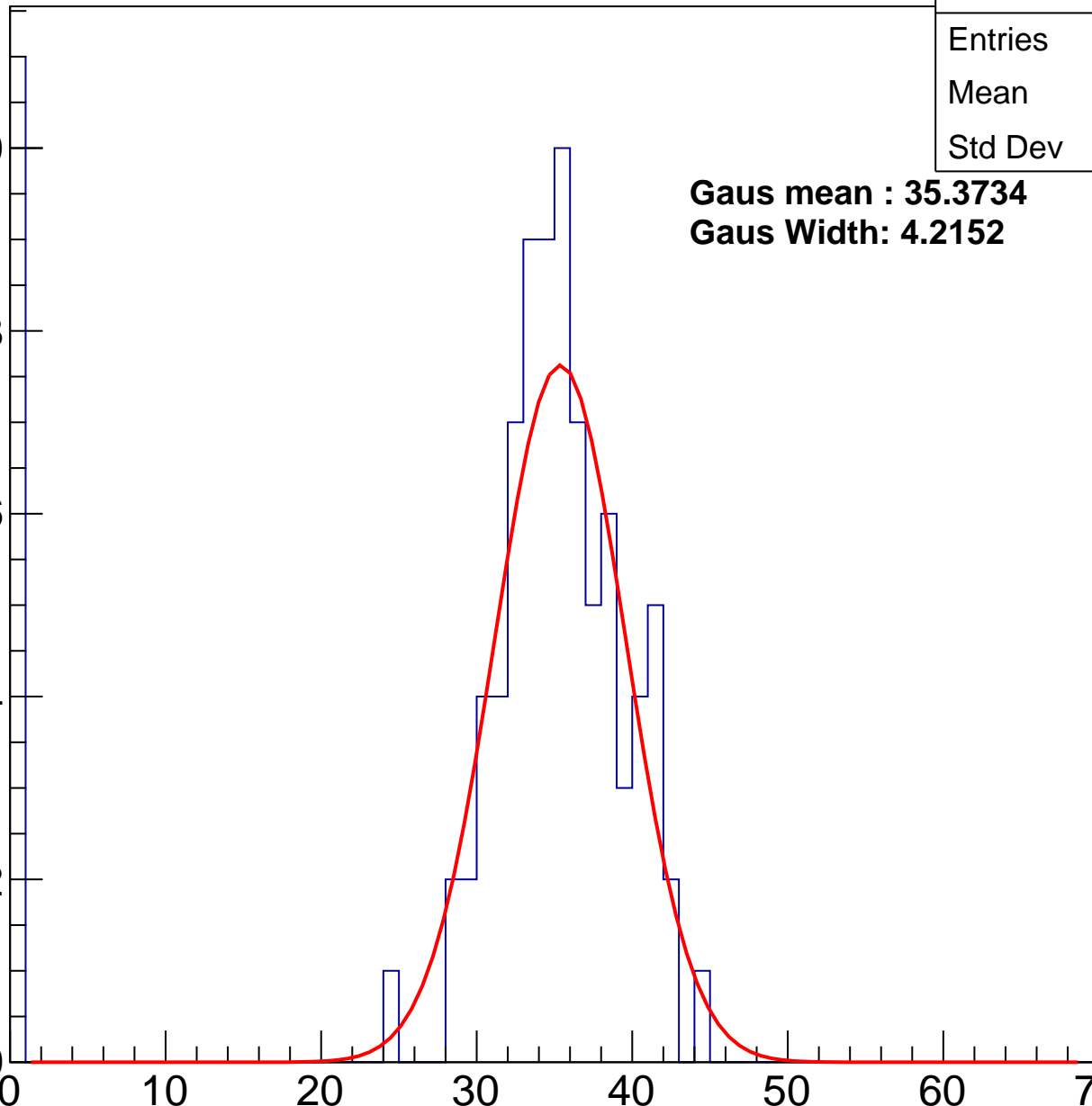
40

50

60

70

ampl



B1L103S, U2-ch41, adc2

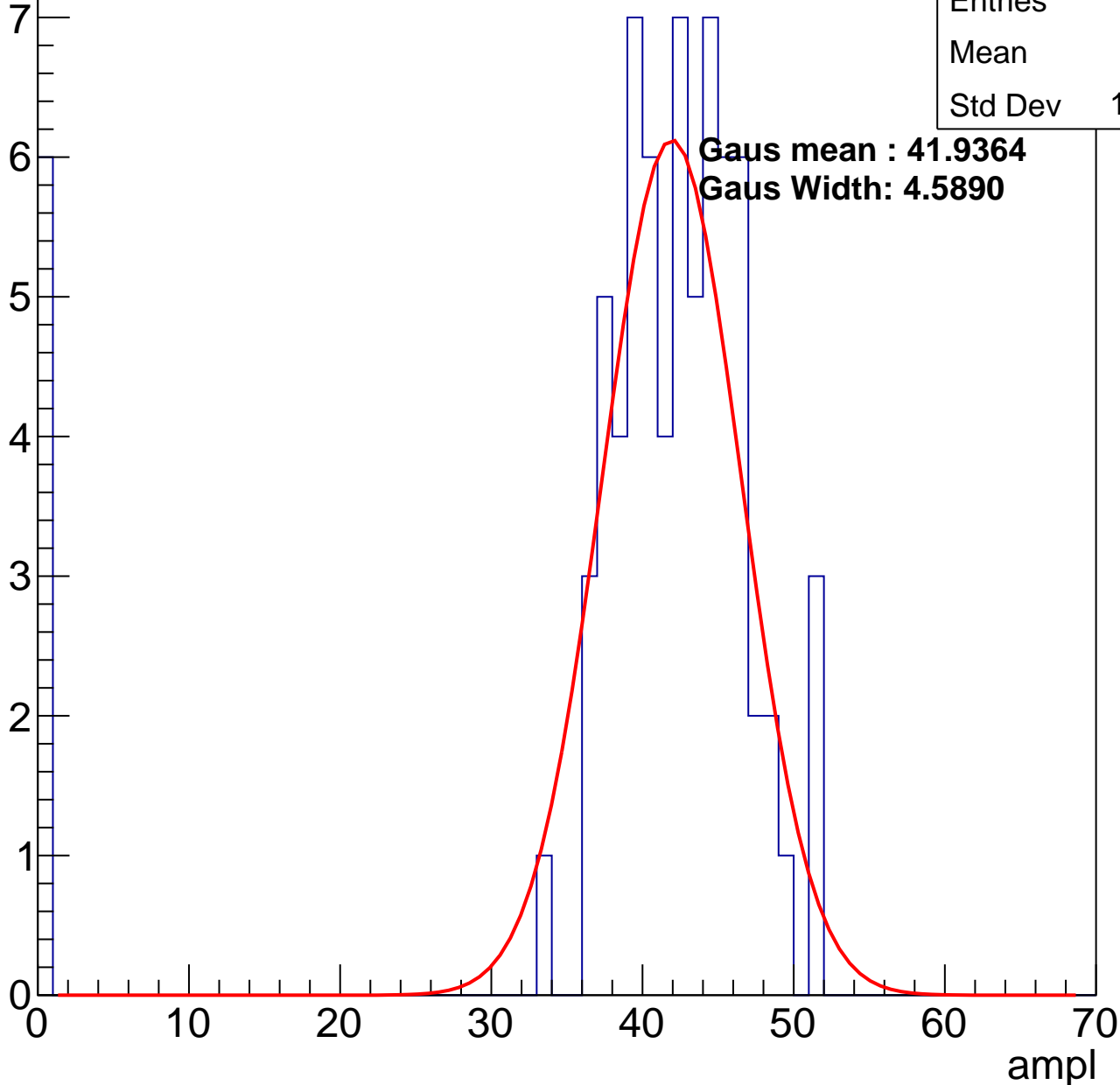
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	38.8
Std Dev	12.05

Gaus mean : 41.9364

Gaus Width: 4.5890

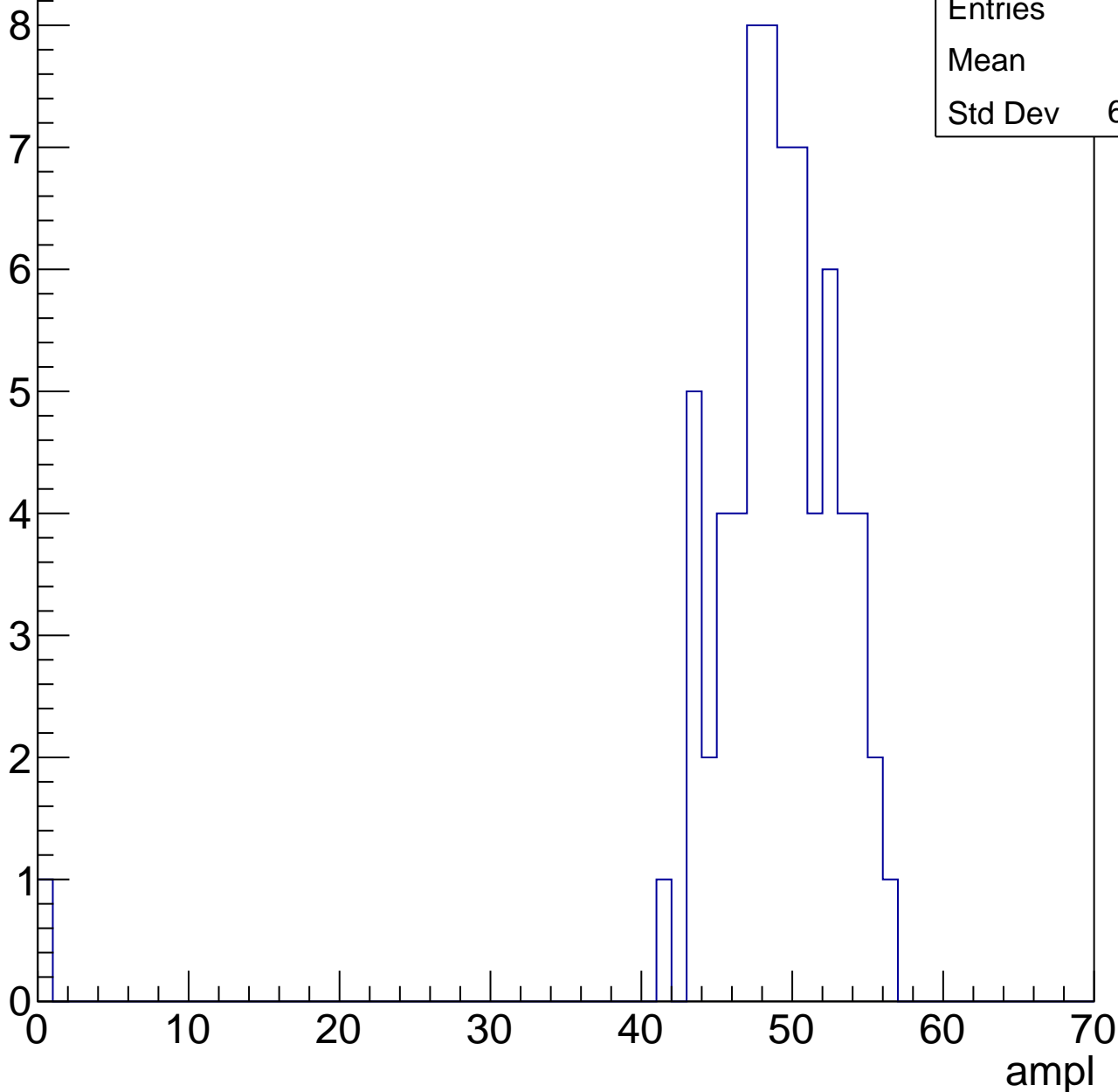


B1L103S, U2-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

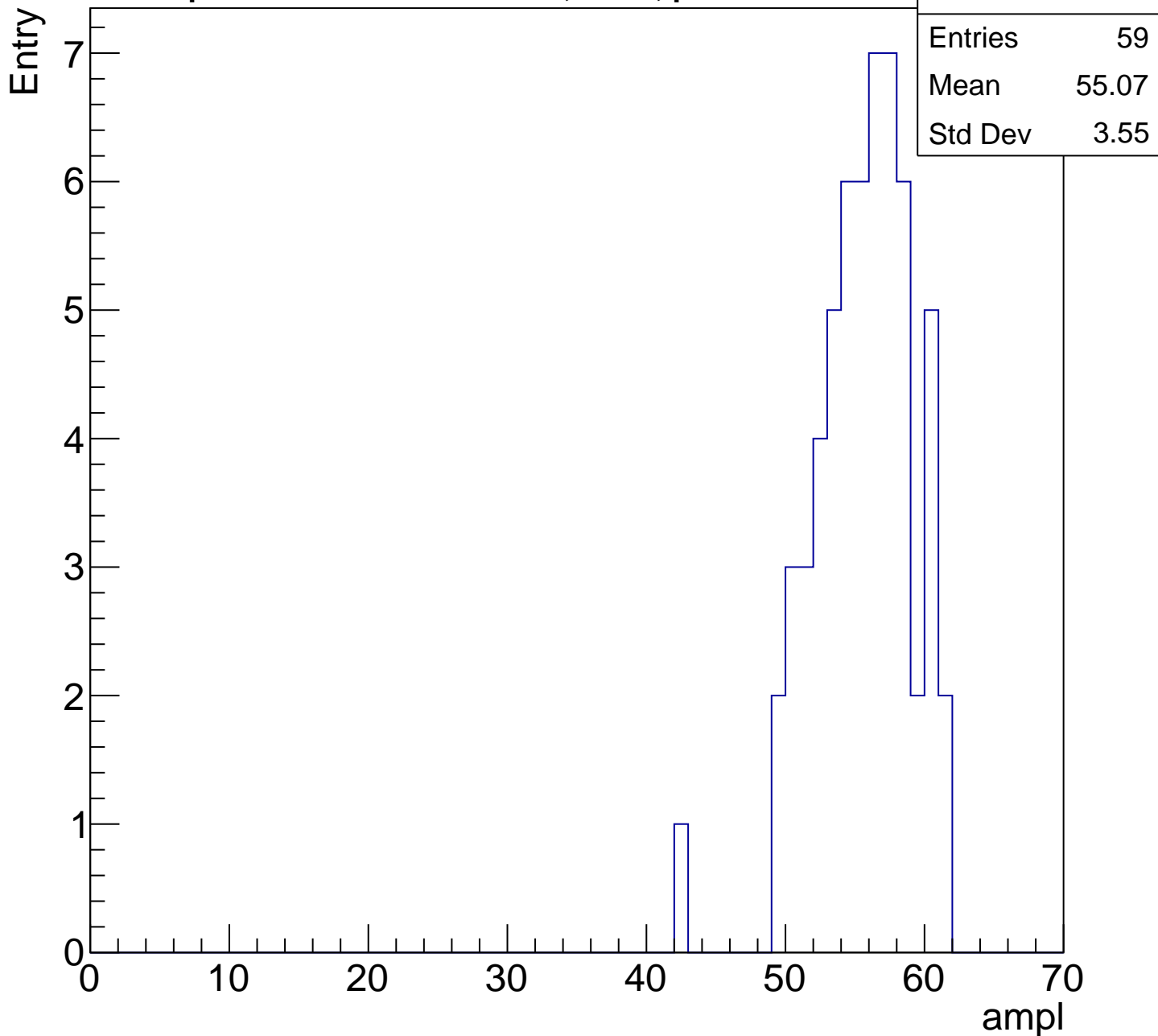
Entry

Entries	68
Mean	48.1
Std Dev	6.798



B1L103S, U2-ch41, adc4

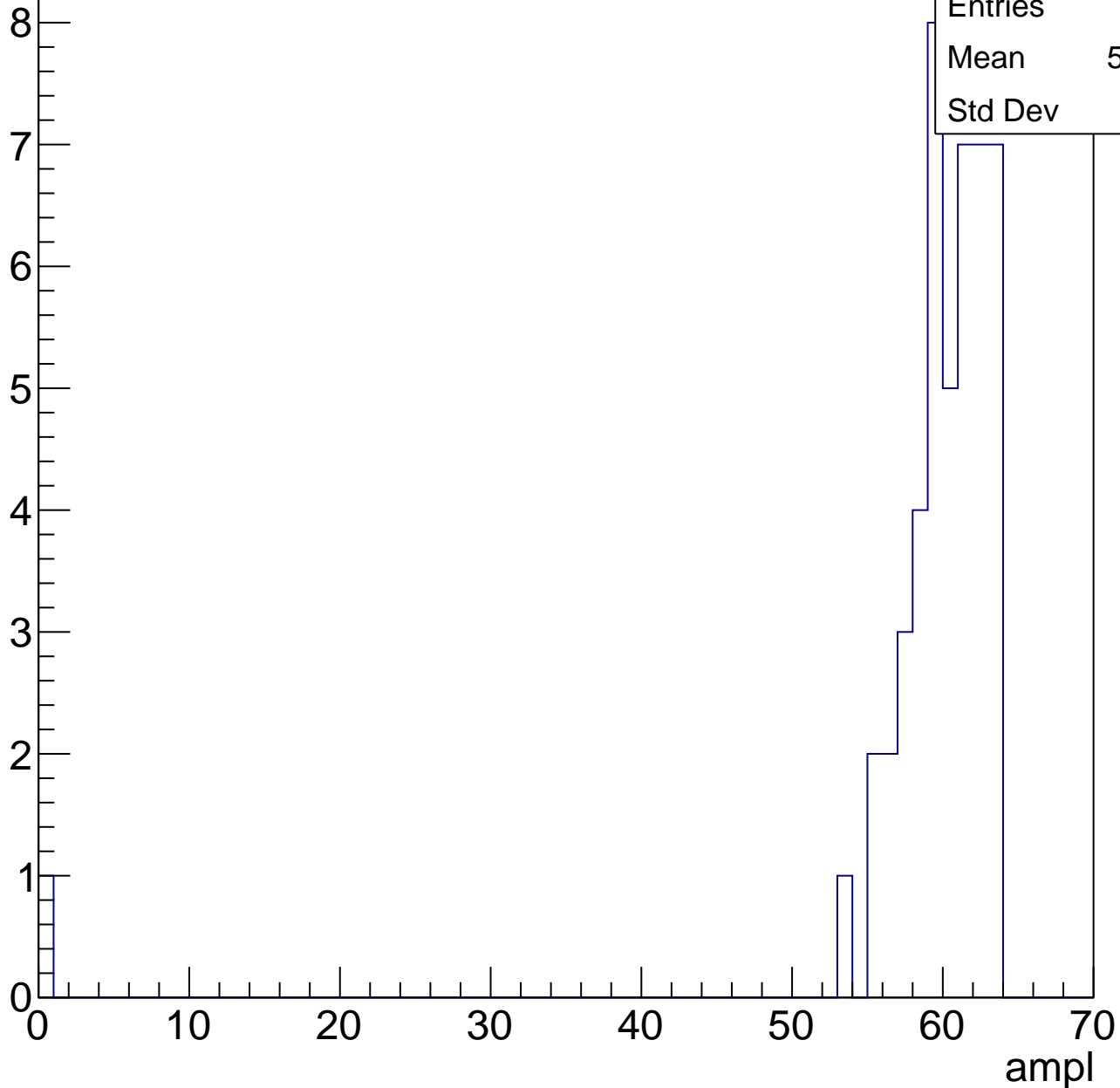
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



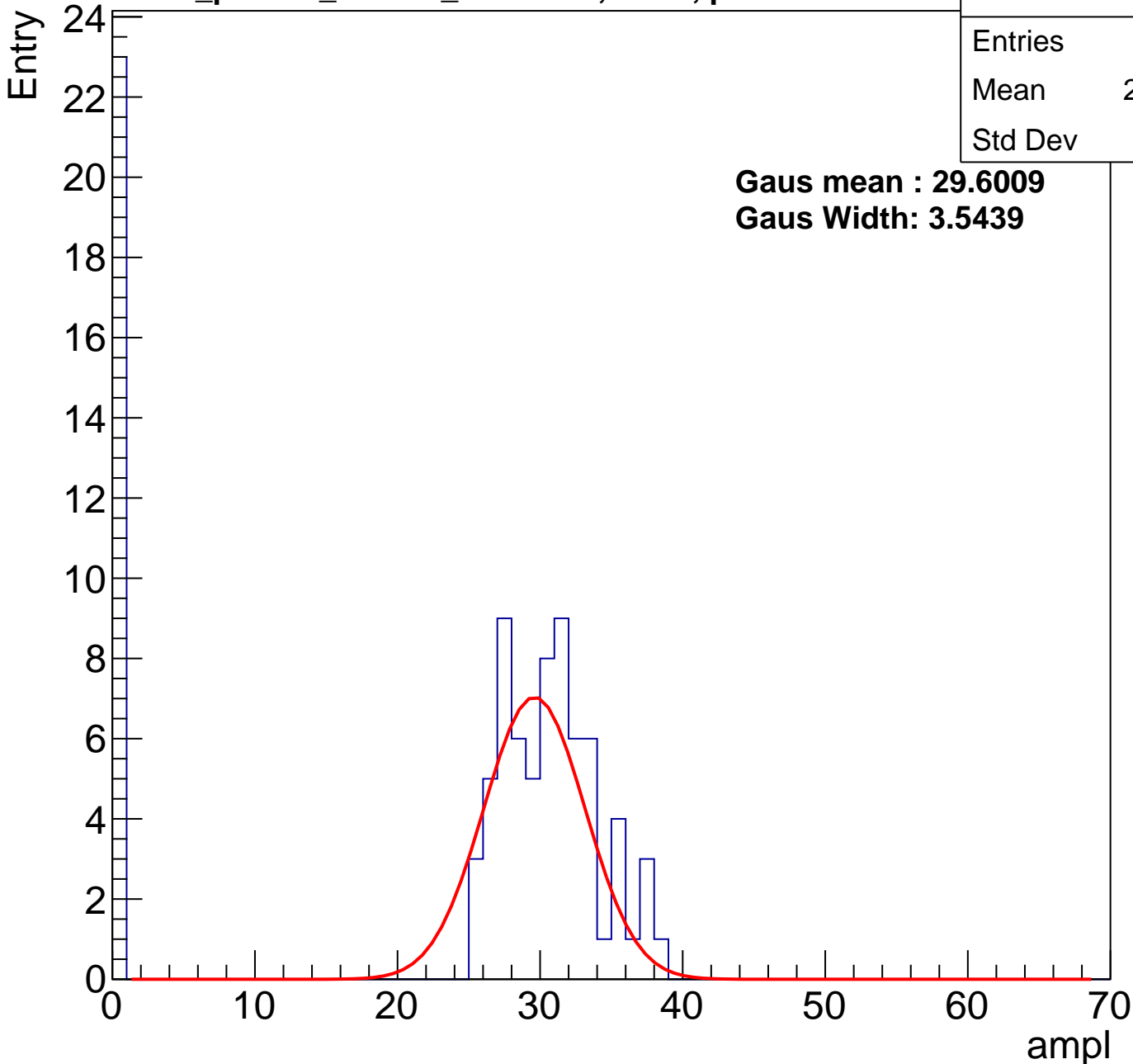
B1L103S, U2-ch42, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	22.54
Std Dev	13.5

Gaus mean : 29.6009

Gaus Width: 3.5439



B1L103S, U2-ch42, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	29.32
Std Dev	14.58

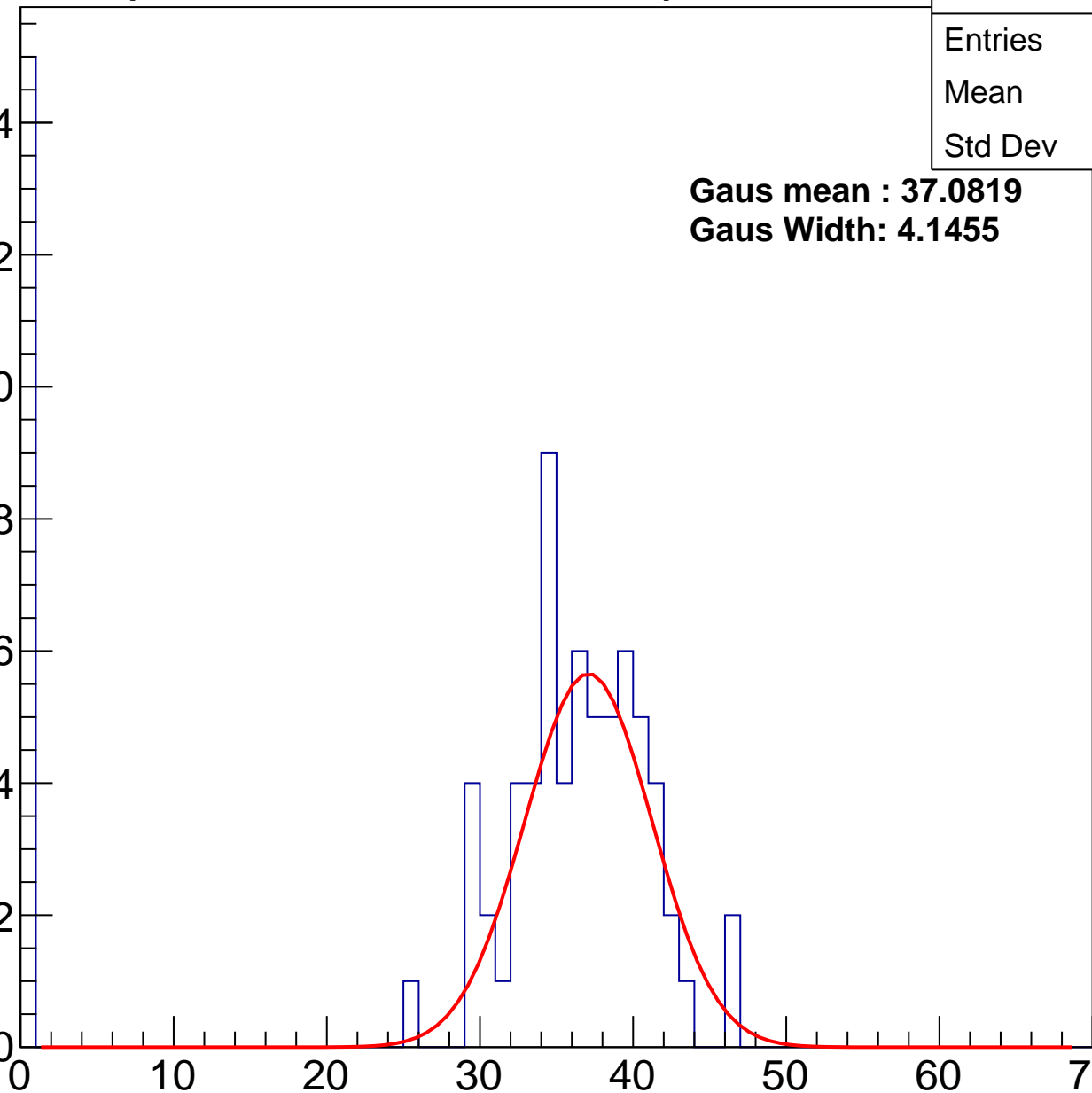
Gaus mean : 37.0819

Gaus Width: 4.1455

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch42, adc2

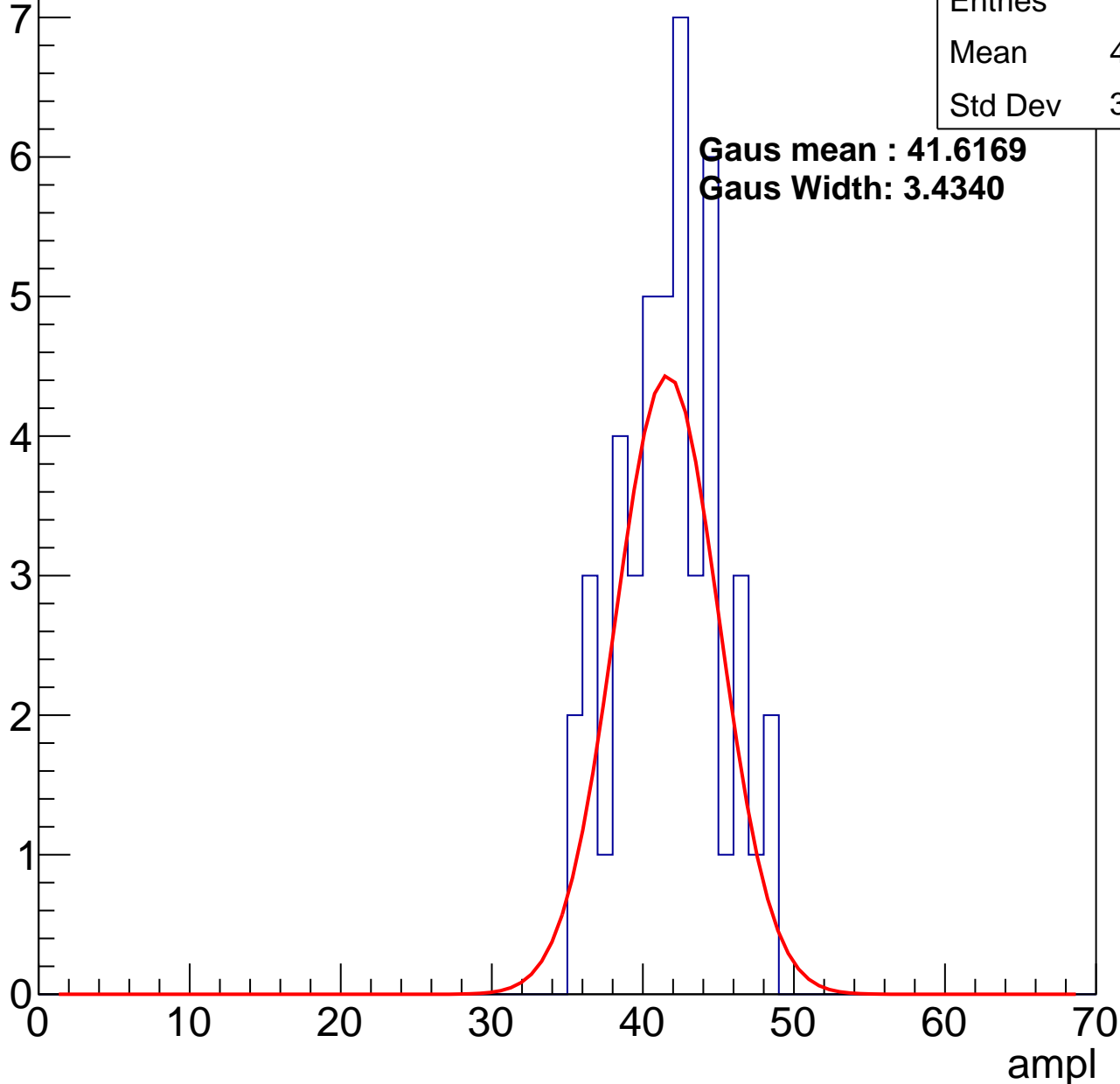
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	41.35
Std Dev	3.325

Gaus mean : 41.6169

Gaus Width: 3.4340

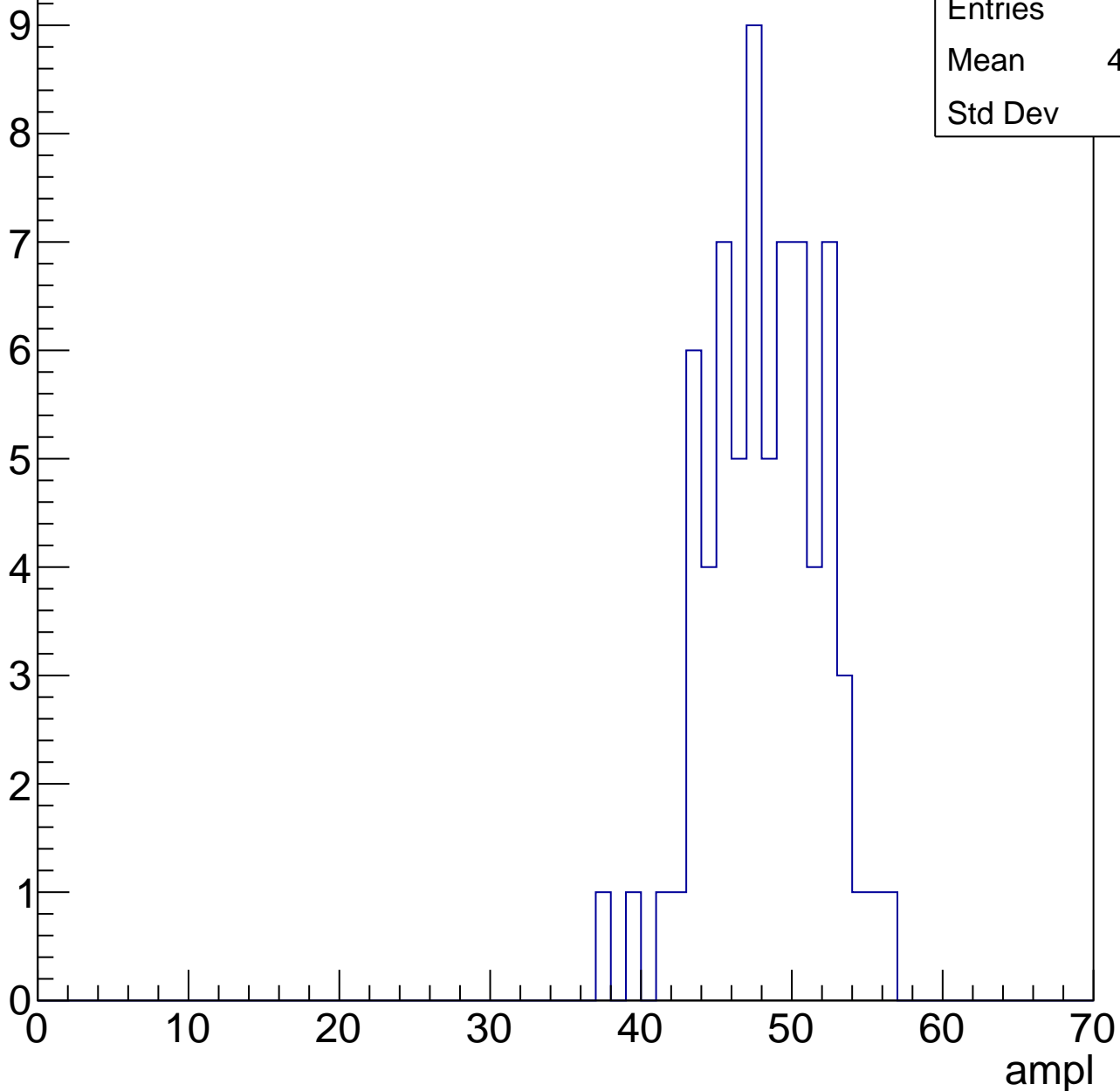


B1L103S, U2-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

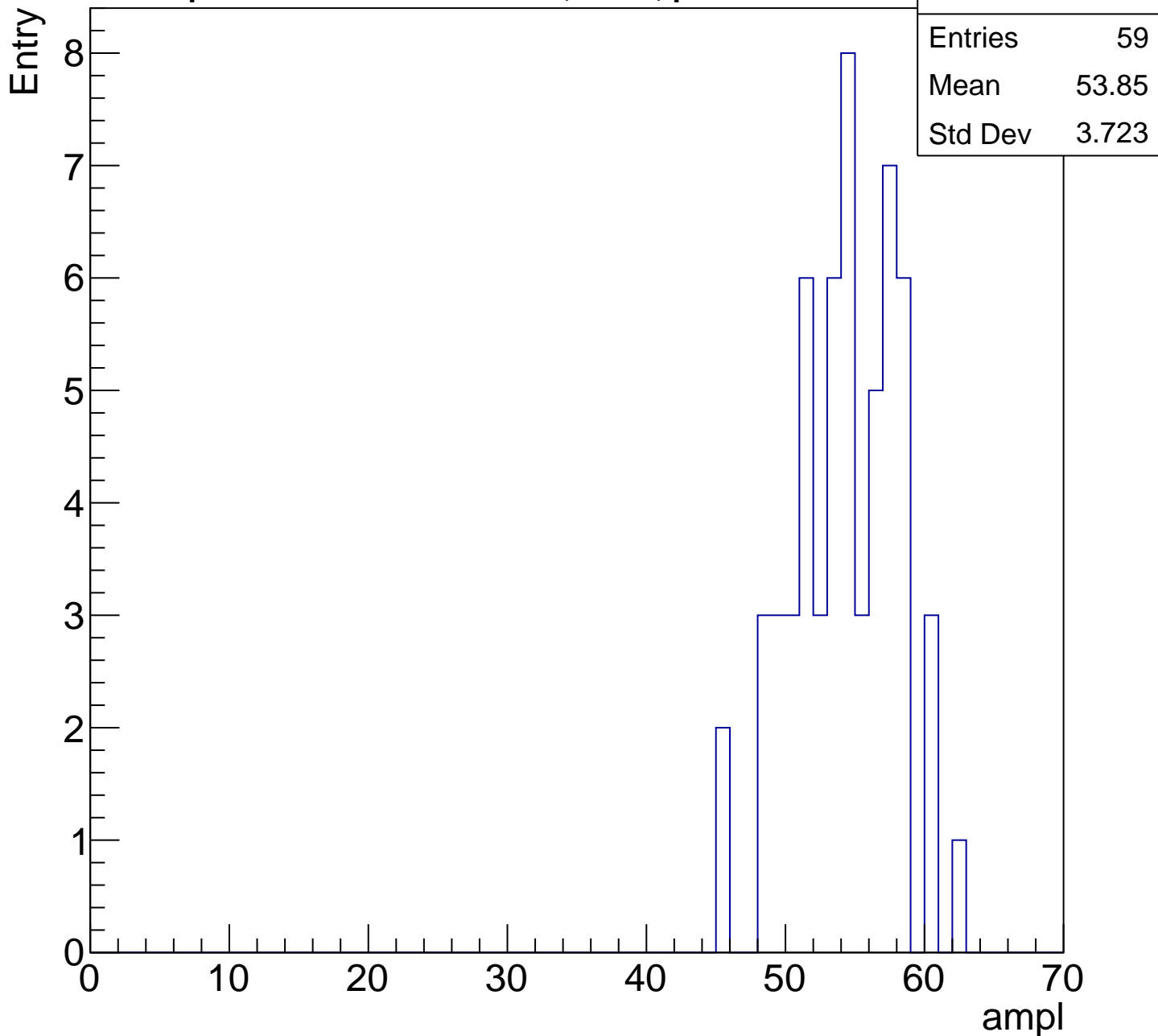
Entry

Entries	71
Mean	47.69
Std Dev	3.74



B1L103S, U2-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 54

Mean 59.06

Std Dev 2.656

8

6

4

2

0

0

10

20

30

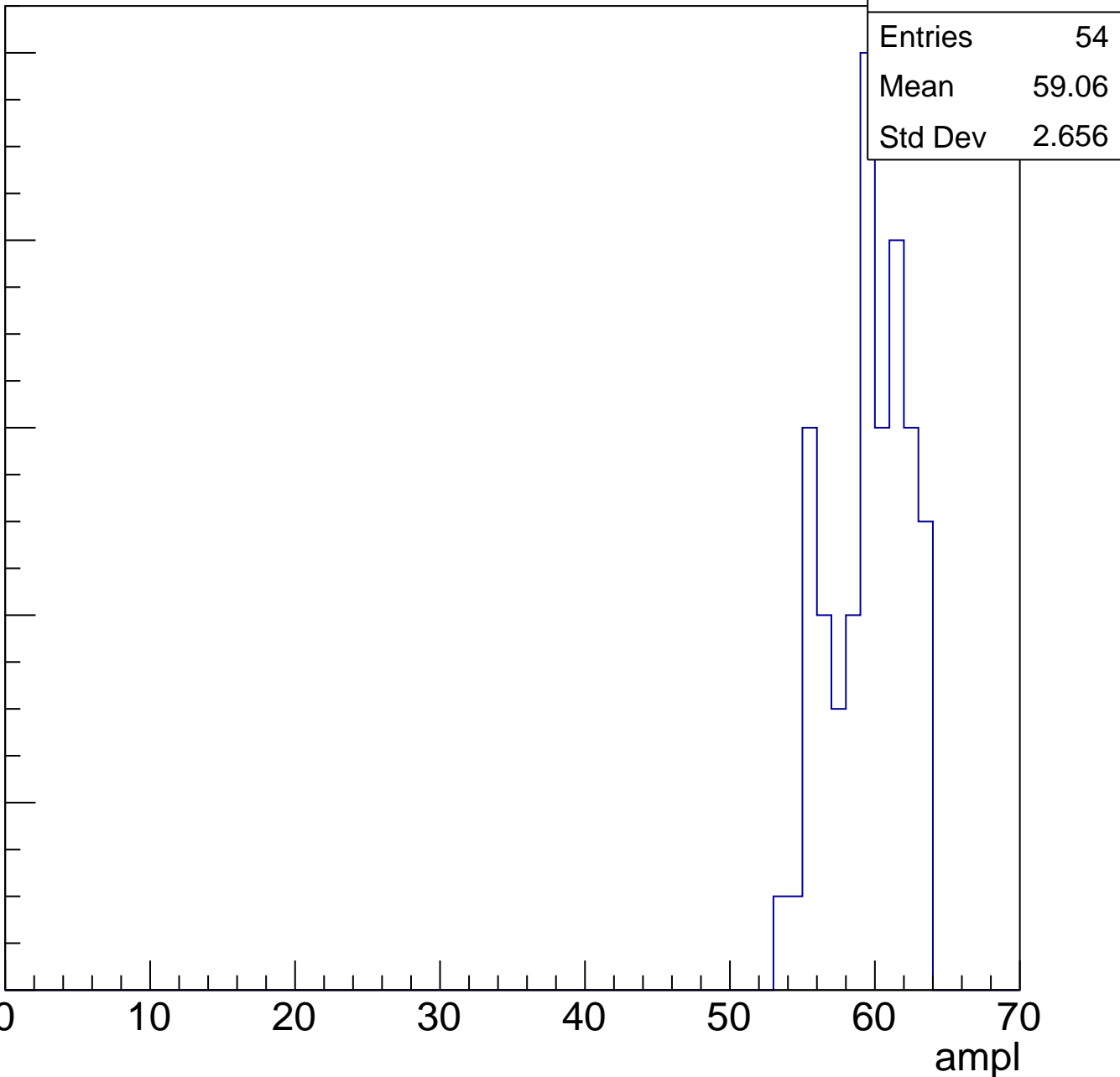
40

50

60

70

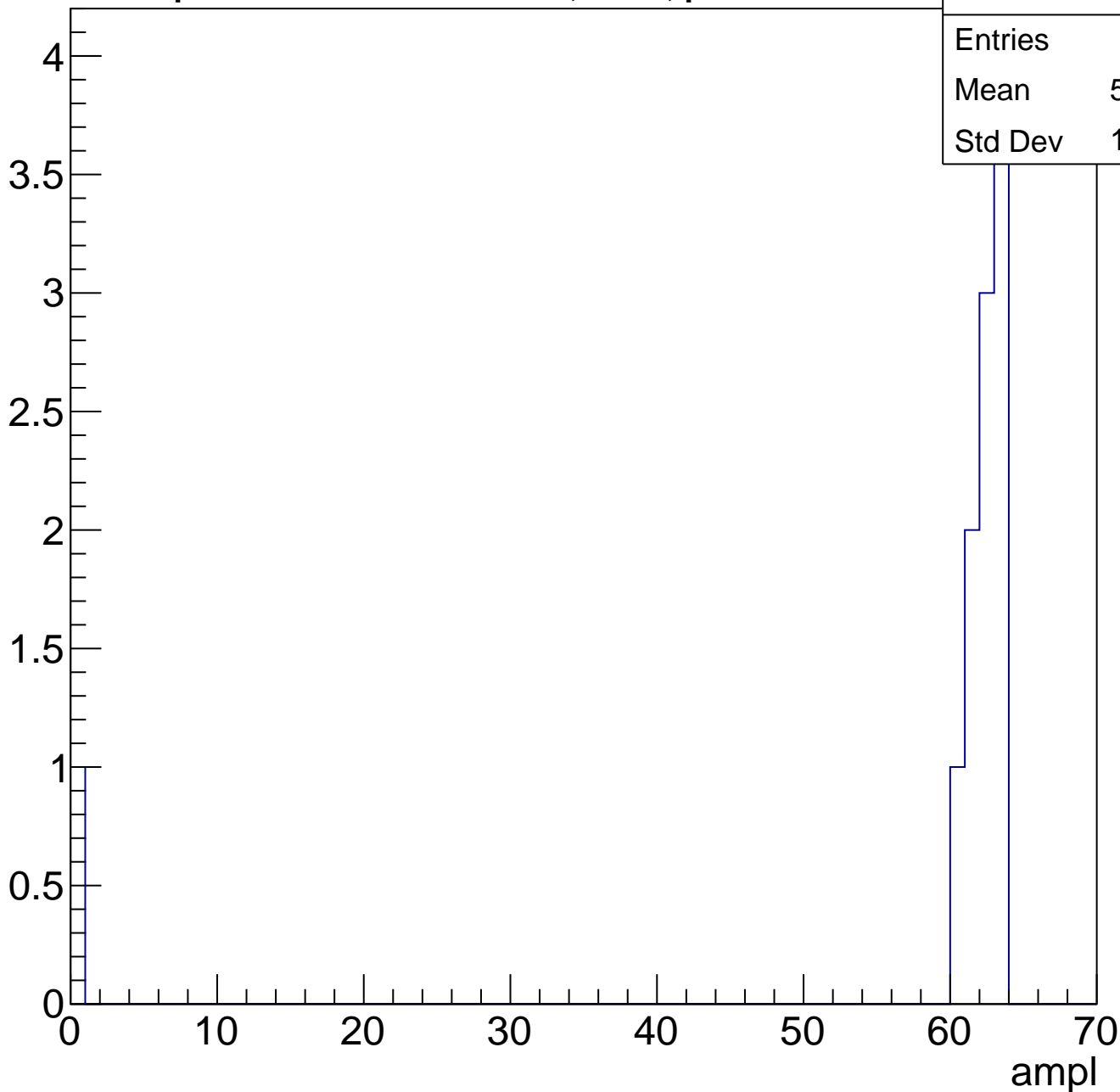
ampl



B1L103S, U2-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	3.15
Std Dev	13.73

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch43, adc0

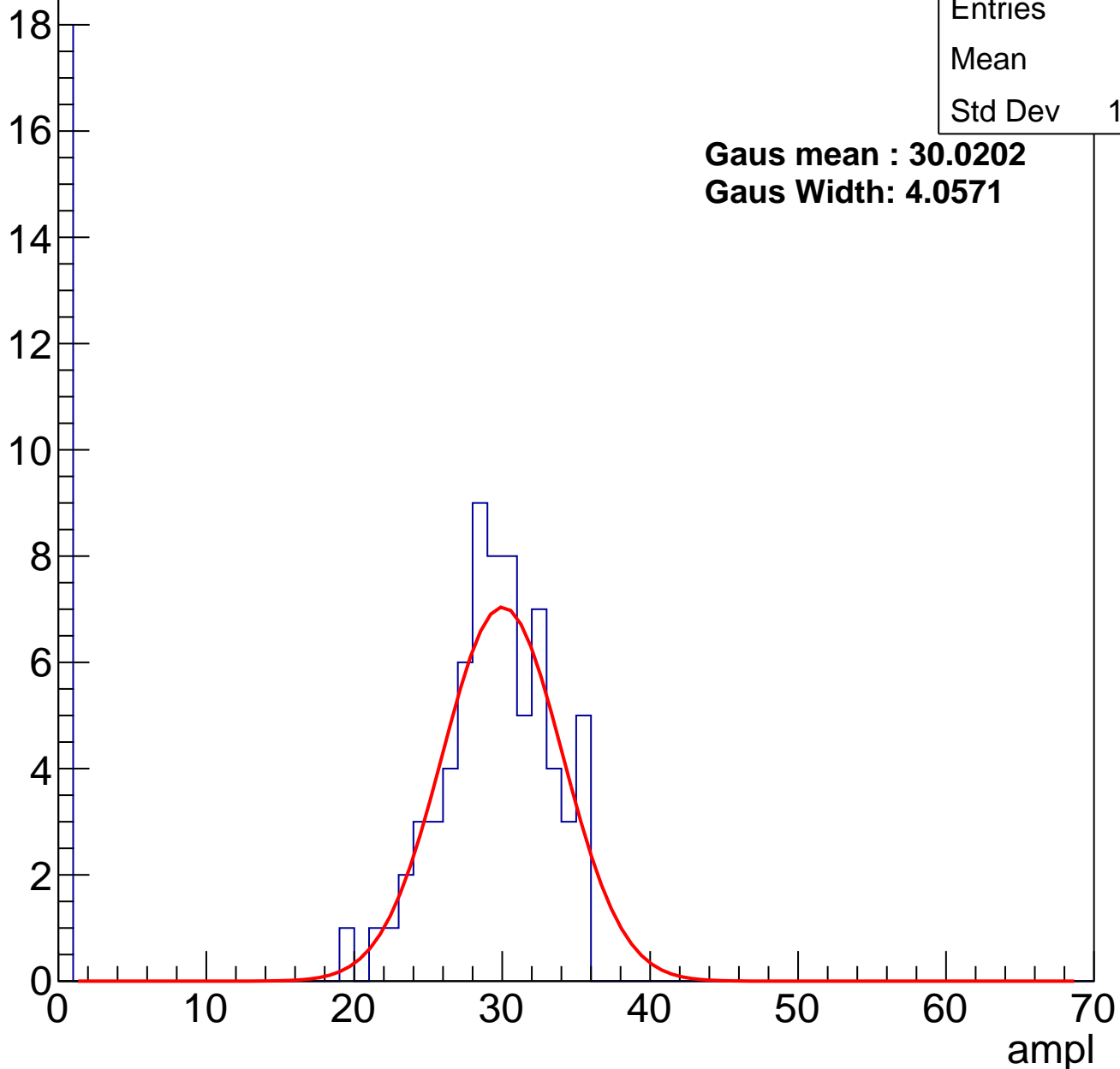
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	23.1
Std Dev	12.14

Gaus mean : 30.0202

Gaus Width: 4.0571

Entry



B1L103S, U2-ch43, adc1

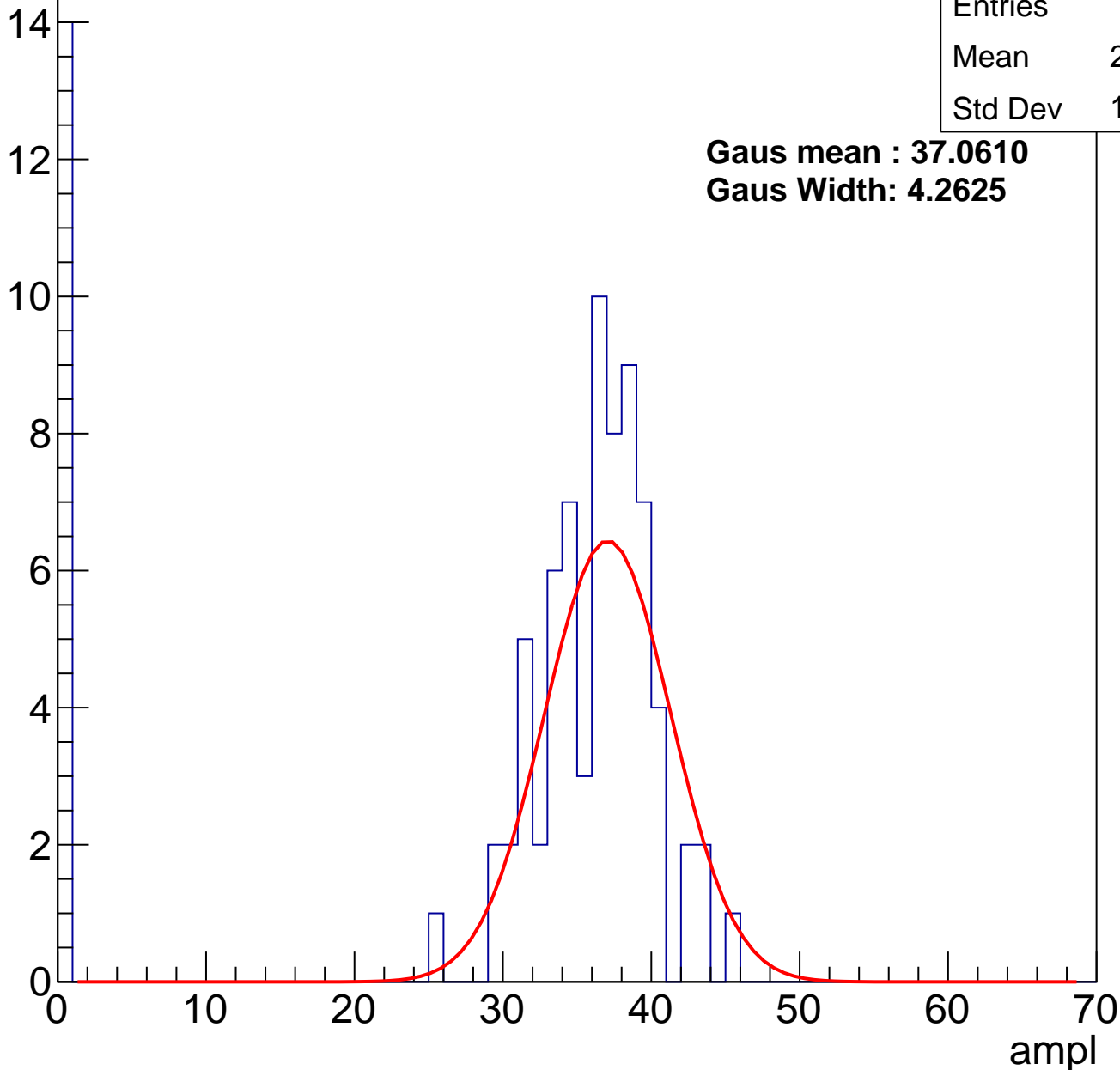
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	29.99
Std Dev	13.73

Gaus mean : 37.0610

Gaus Width: 4.2625

Entry



B1L103S, U2-ch43, adc2

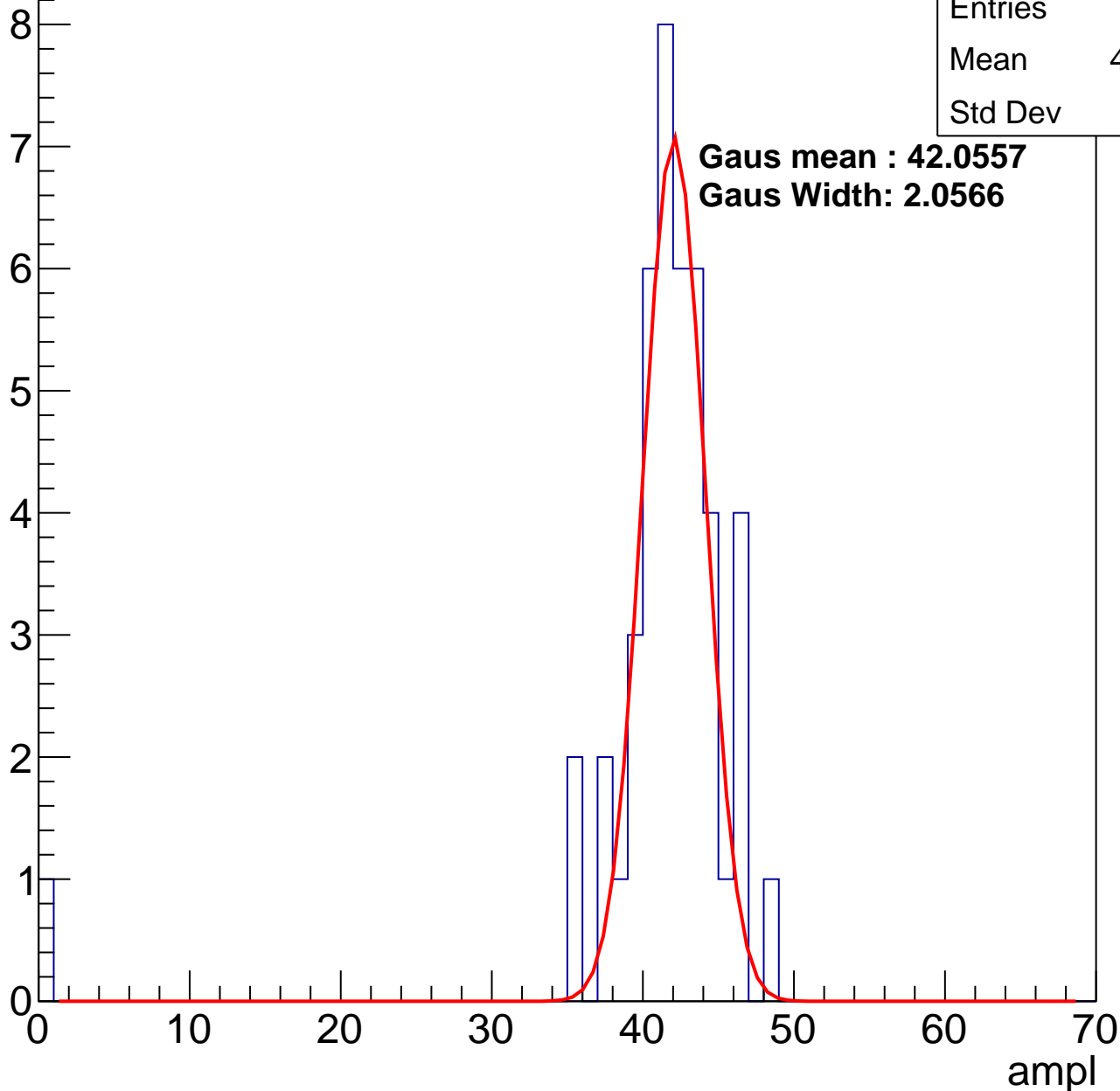
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	40.67
Std Dev	6.73

Gaus mean : 42.0557

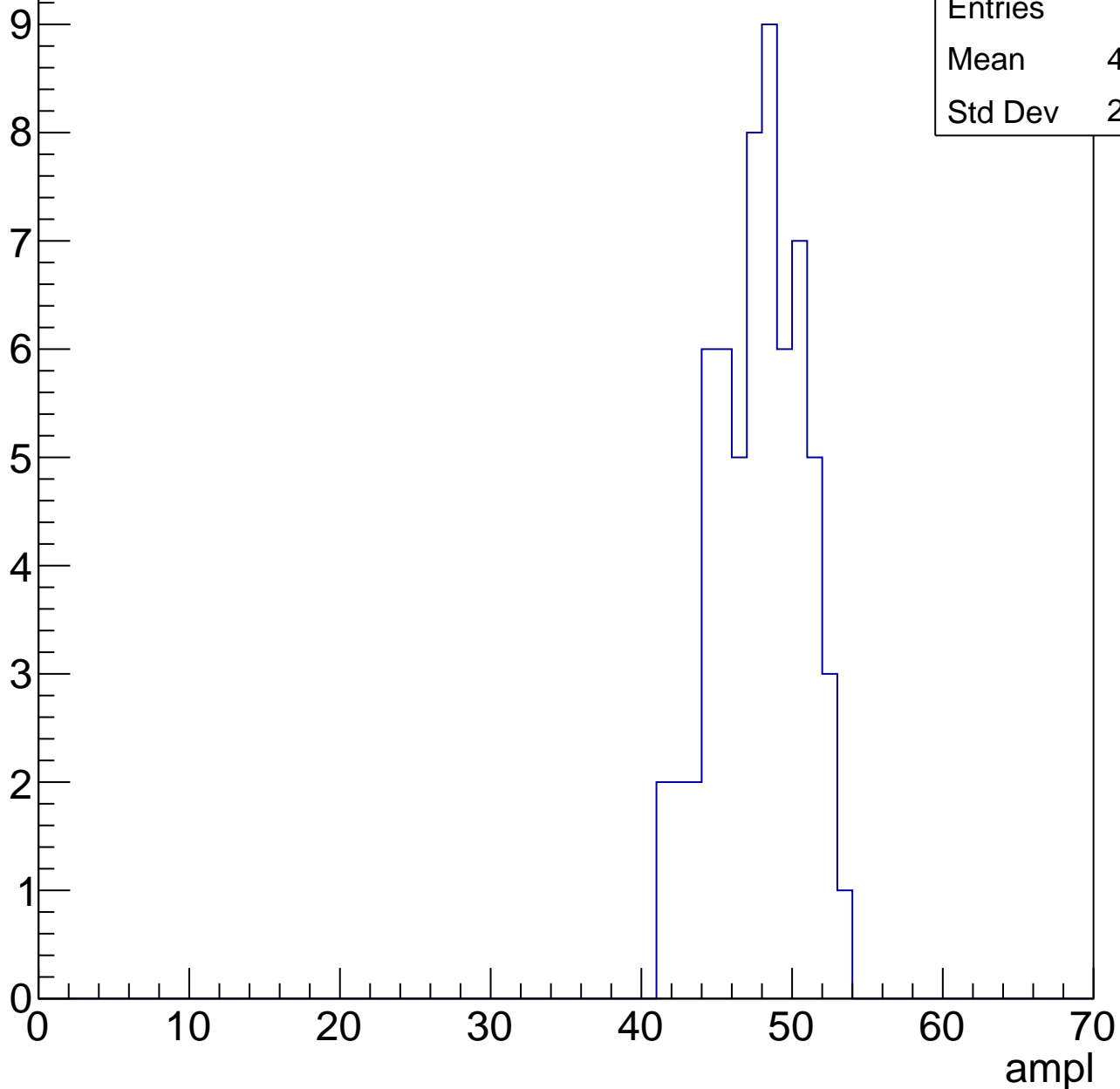
Gaus Width: 2.0566



B1L103S, U2-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

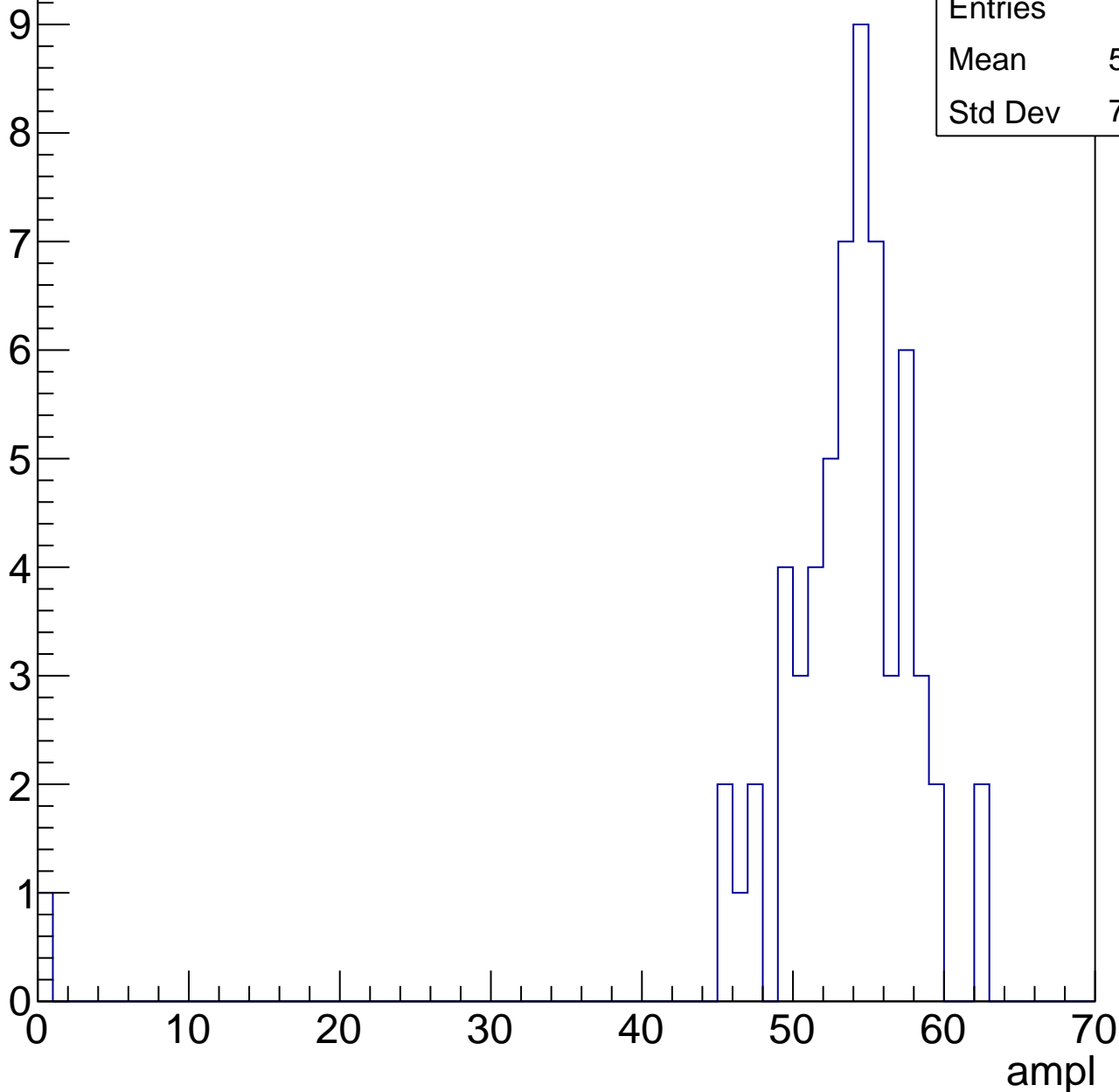


B1L103S, U2-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	52.59
Std Dev	7.713

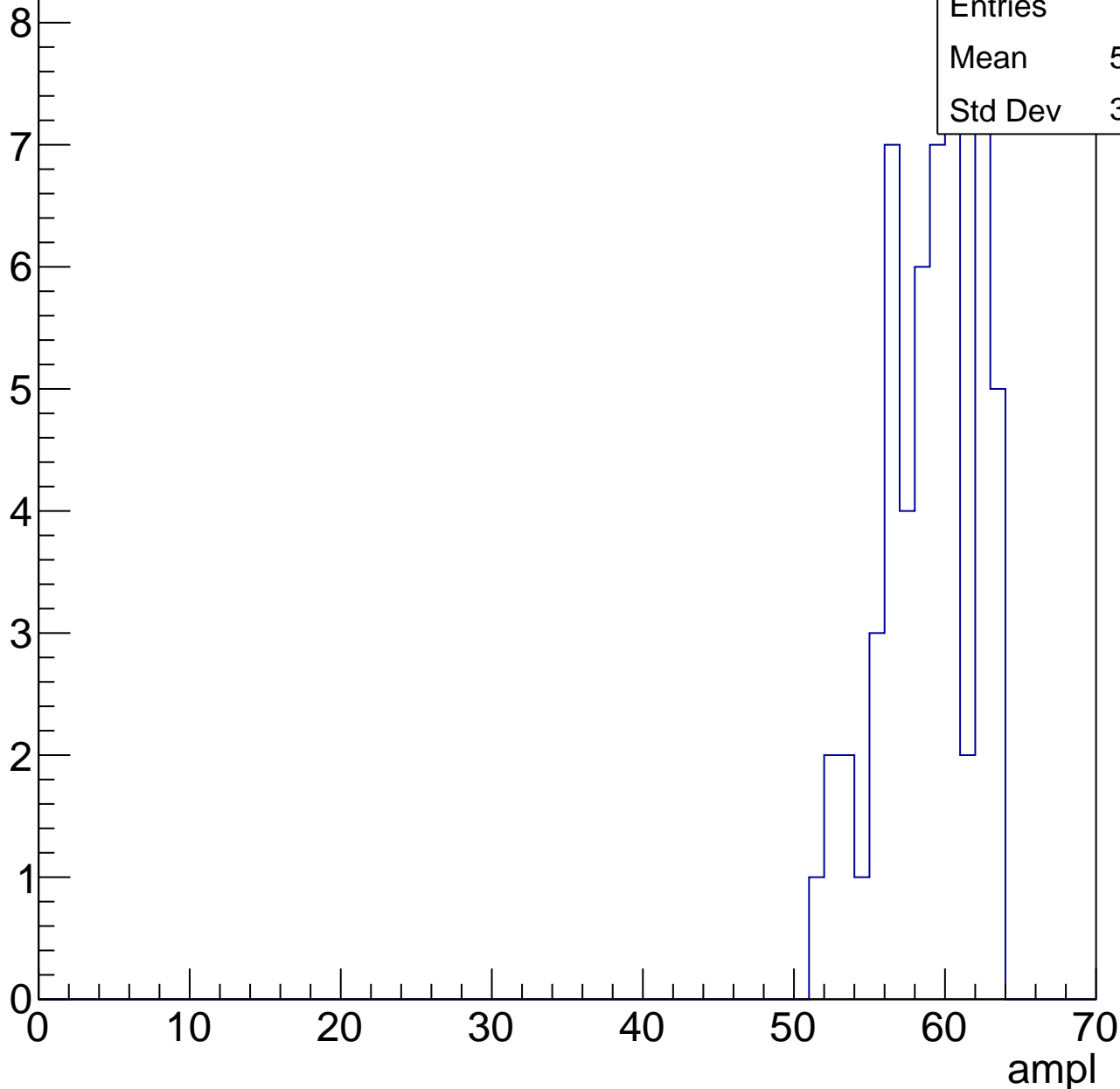


B1L103S, U2-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.46
Std Dev	3.117

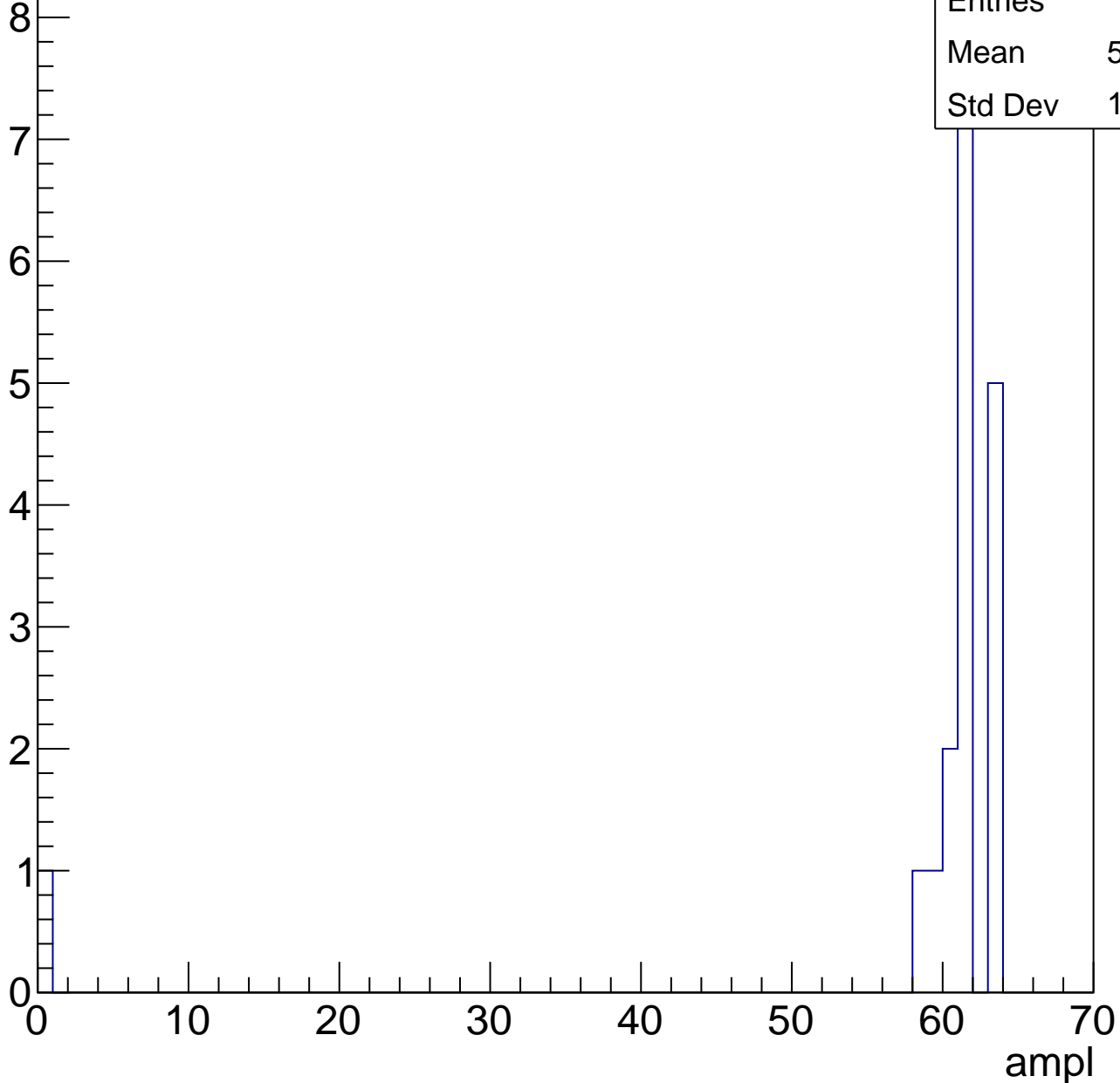


B1L103S, U2-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.78
Std Dev	14.08



B1L103S, U2-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

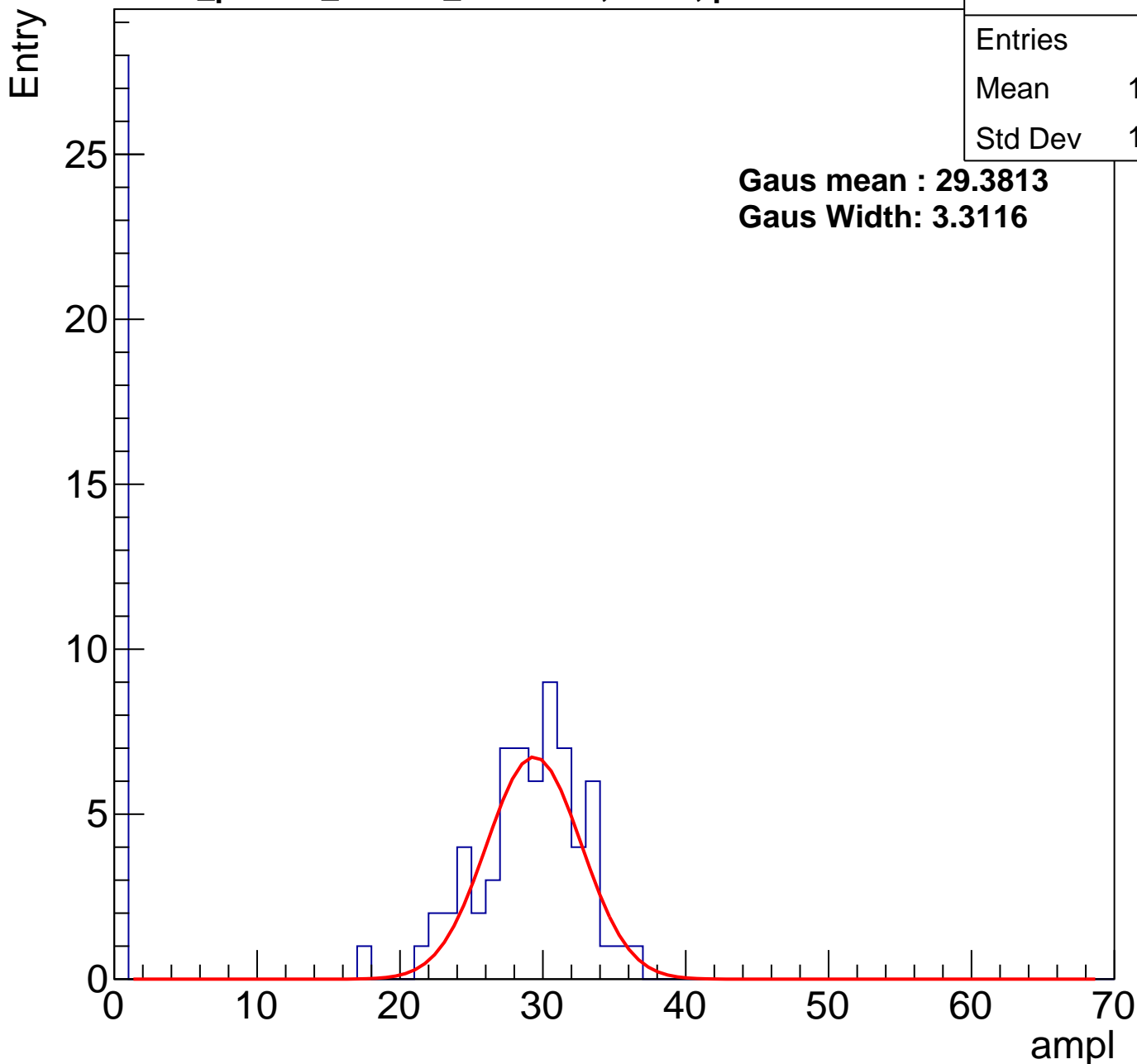
B1L103S, U2-ch44, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	19.88
Std Dev	13.49

Gaus mean : 29.3813

Gaus Width: 3.3116



B1L103S, U2-ch44, adc1

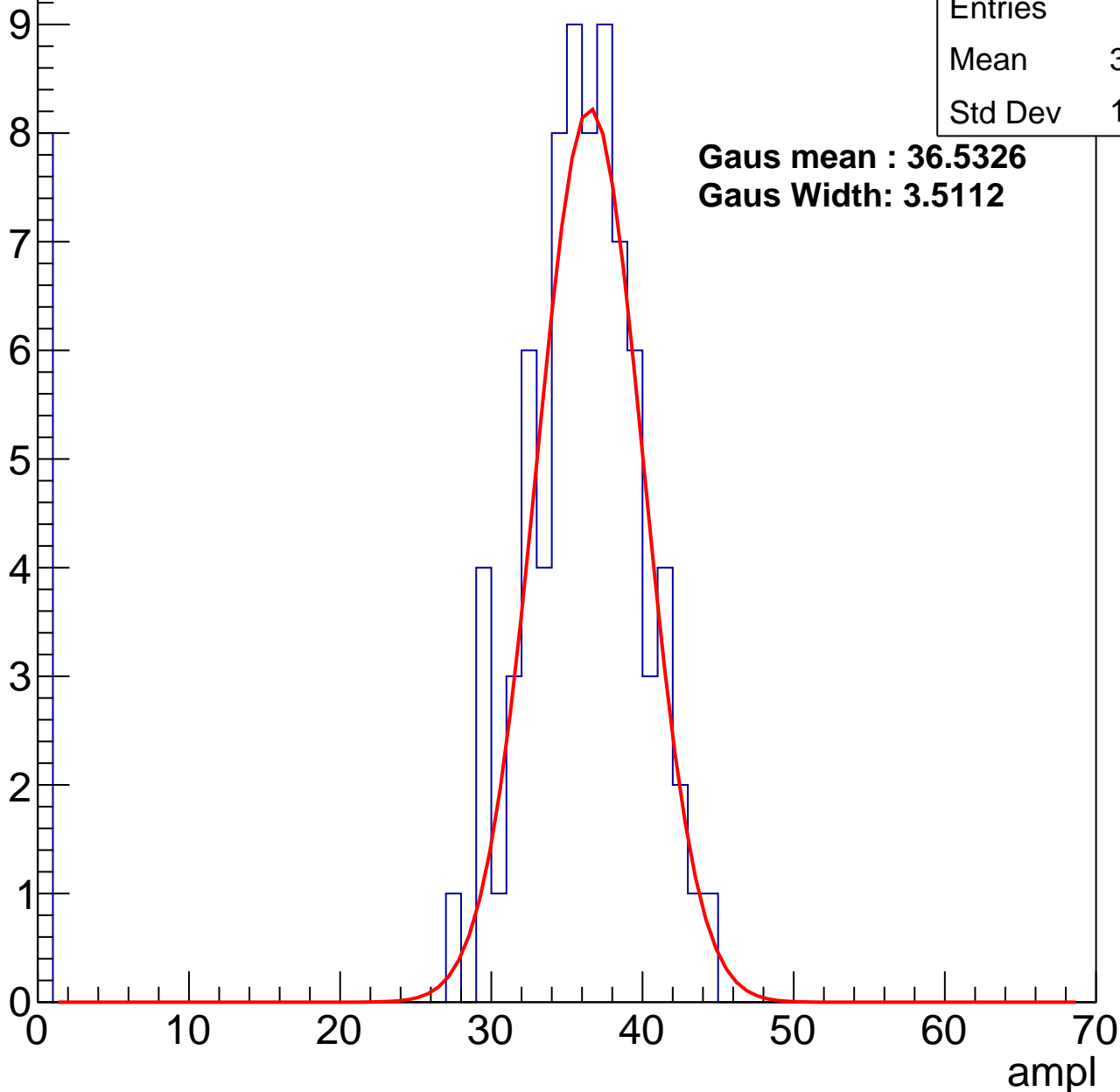
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	32.39
Std Dev	10.98

Gaus mean : 36.5326

Gaus Width: 3.5112



B1L103S, U2-ch44, adc2

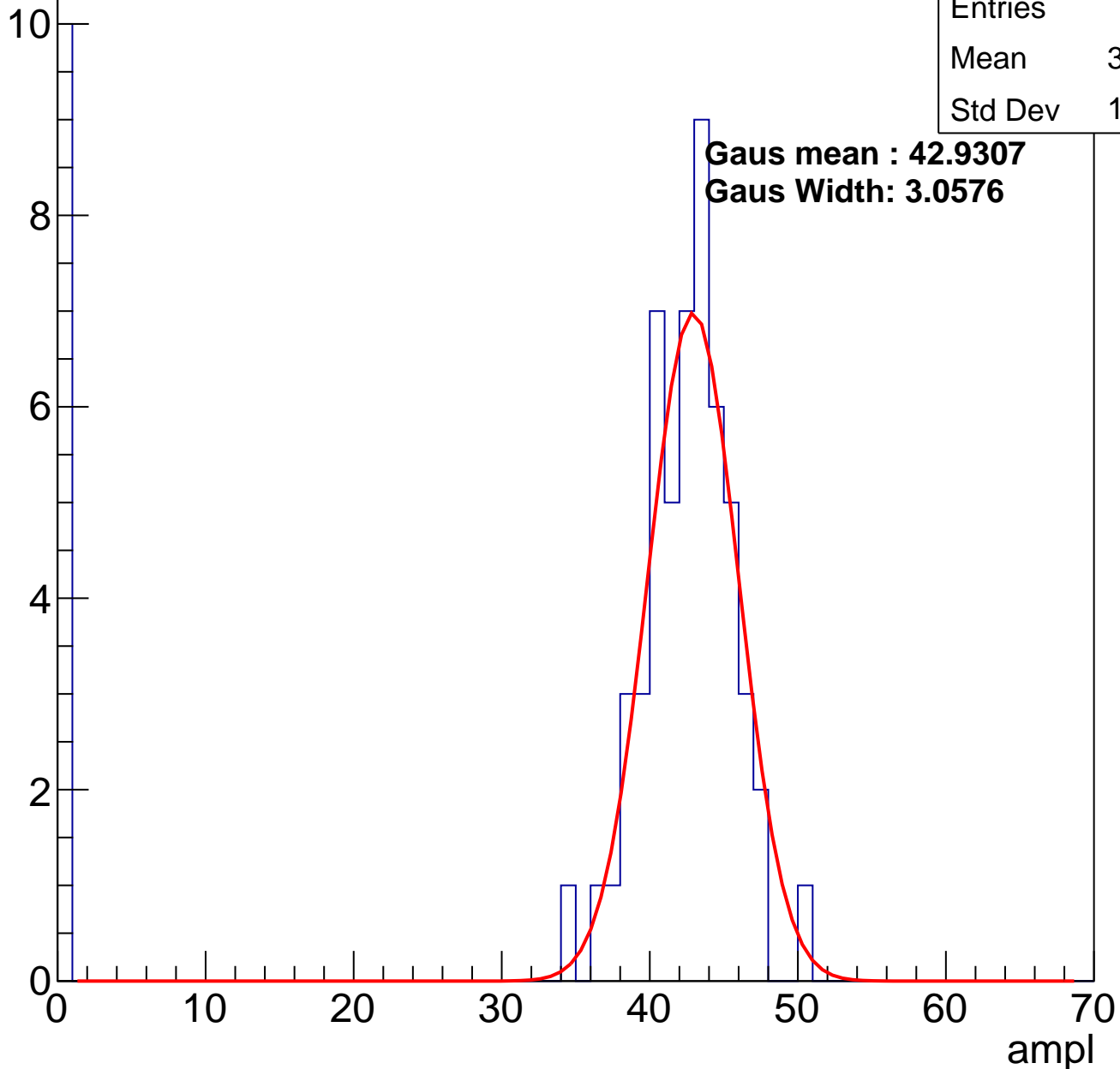
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	35.55
Std Dev	15.54

Gaus mean : 42.9307

Gaus Width: 3.0576

Entry

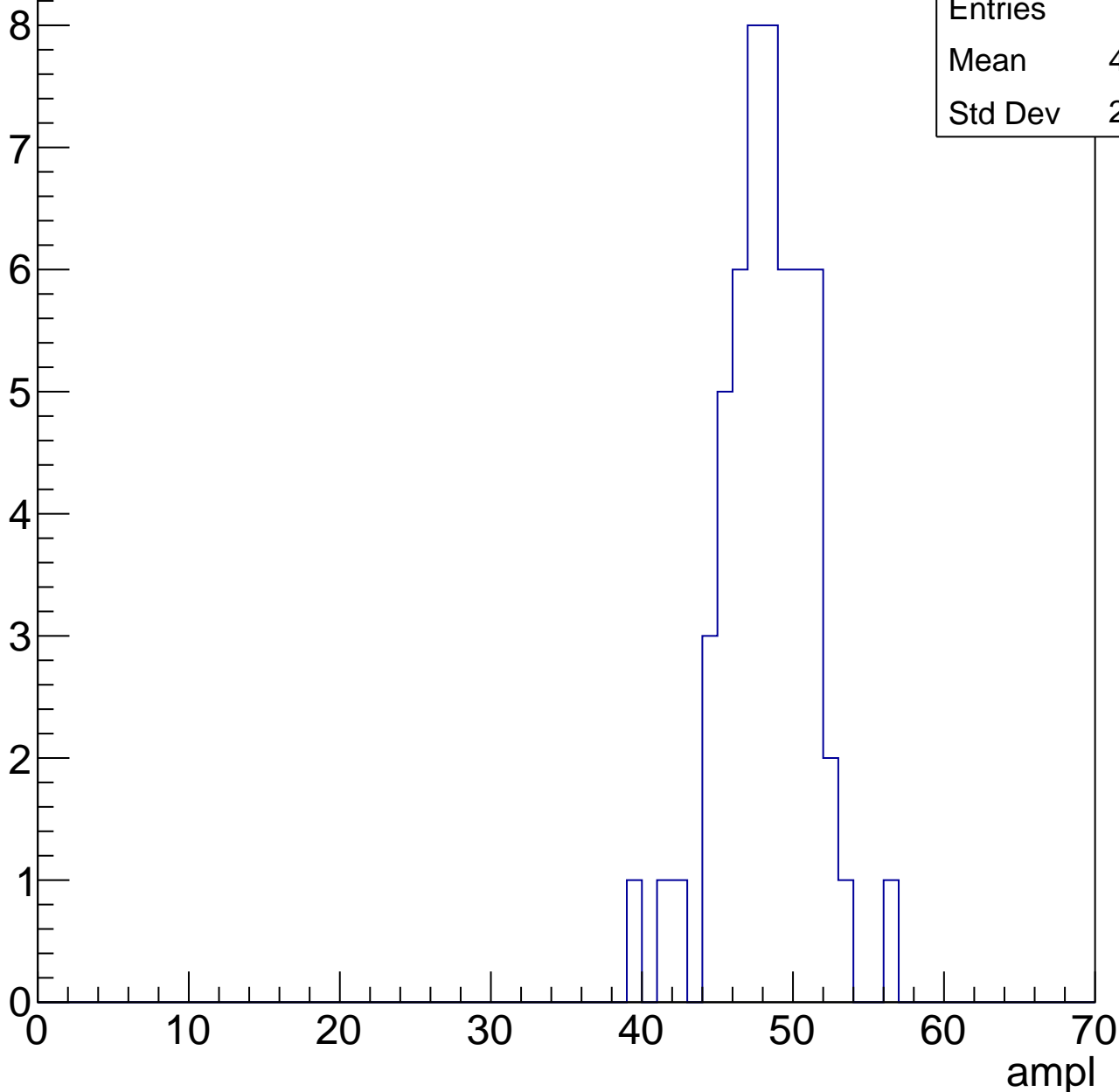


B1L103S, U2-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	47.78
Std Dev	2.995

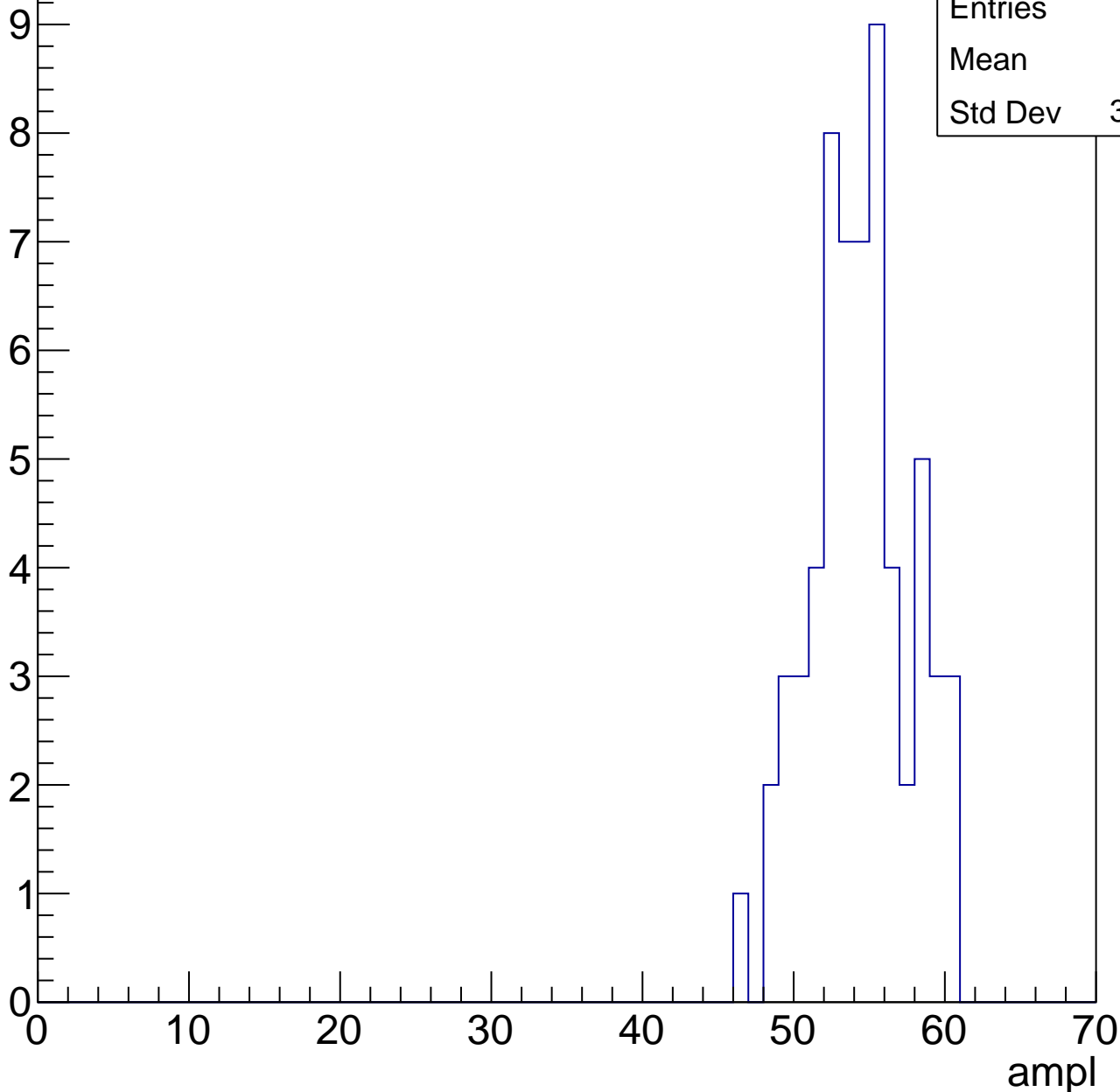


B1L103S, U2-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	53.9
Std Dev	3.238

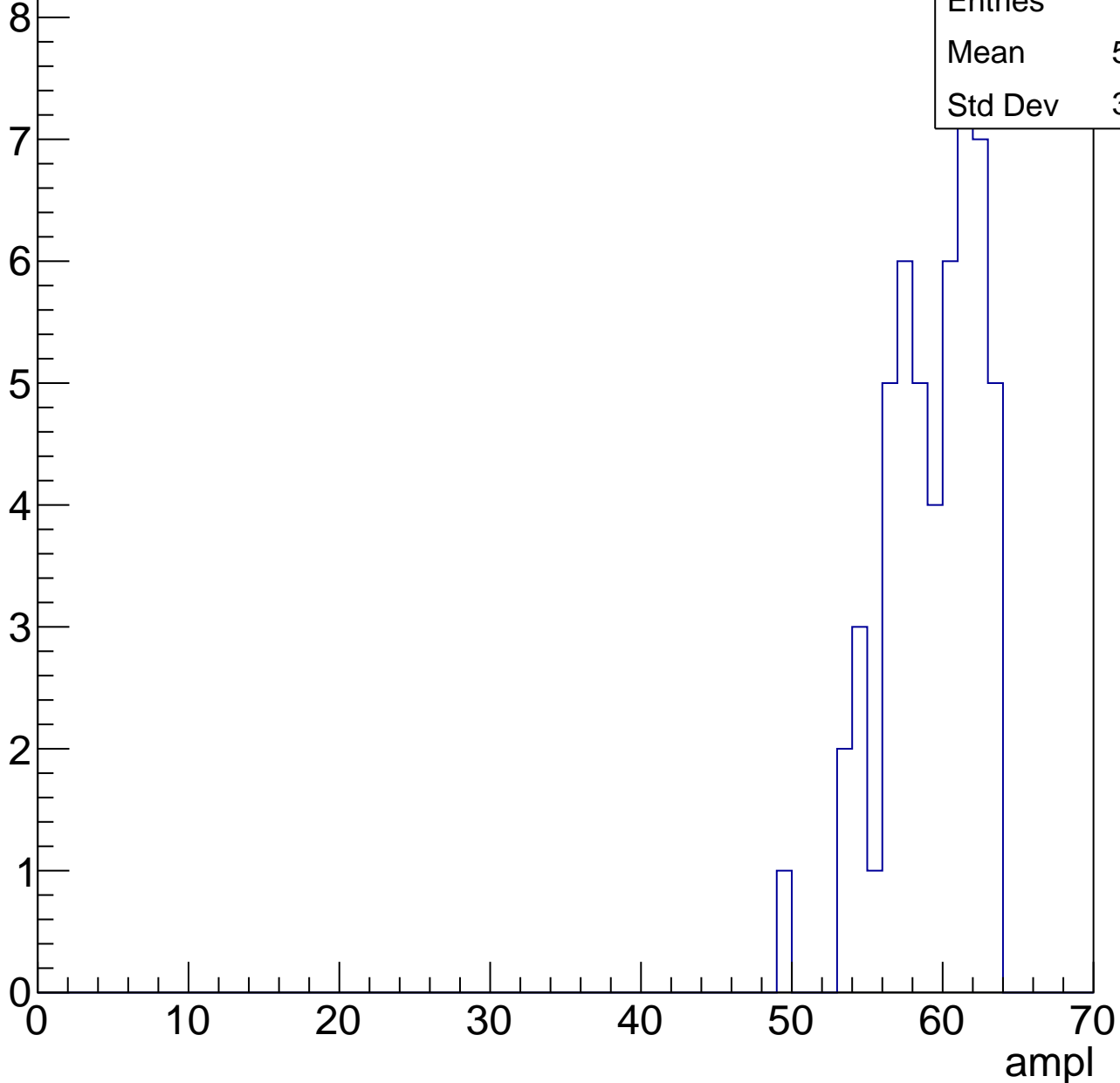


B1L103S, U2-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.81
Std Dev	3.121

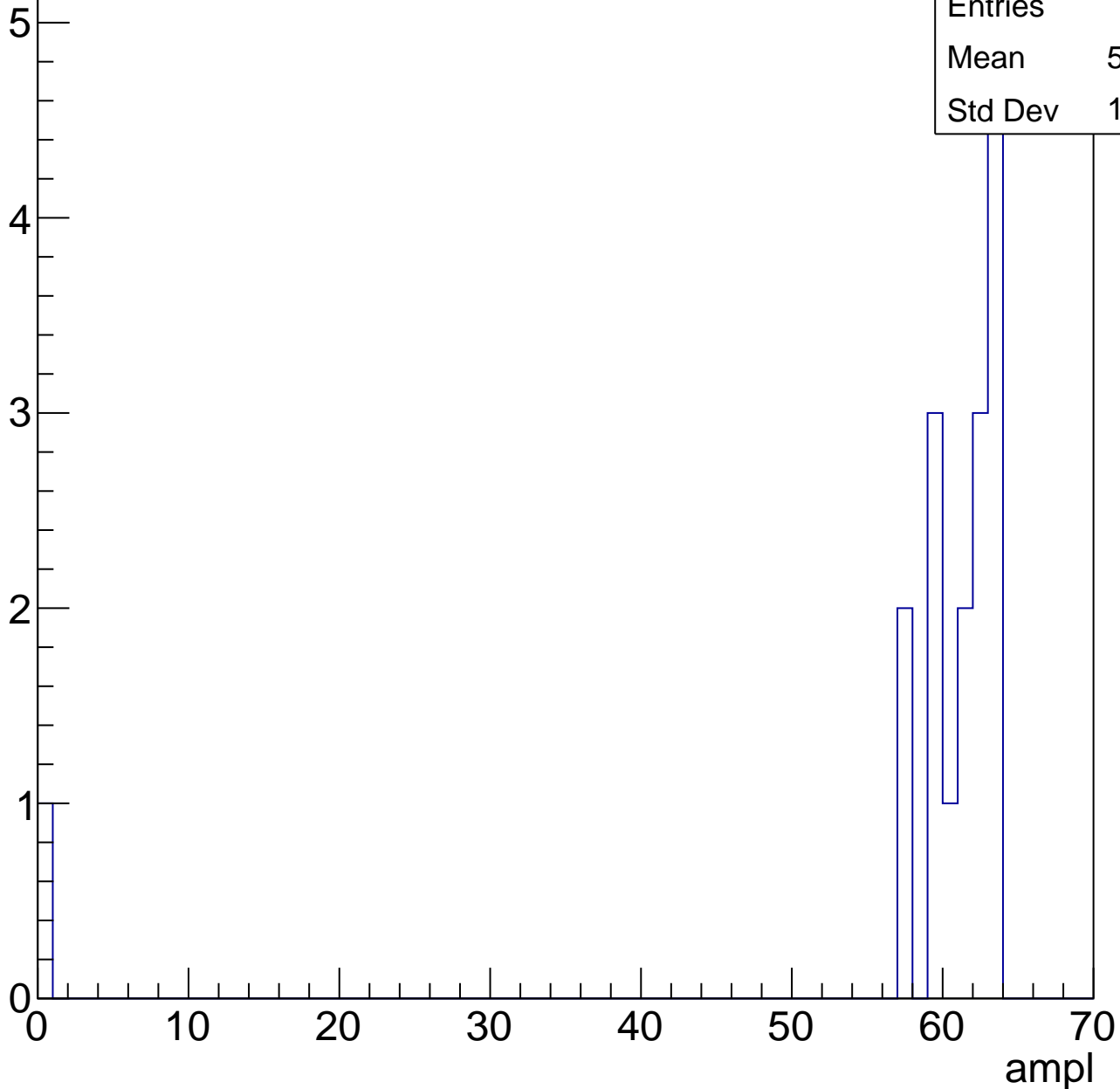


B1L103S, U2-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.29
Std Dev	14.46



B1L103S, U2-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U2-ch45, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	24.97
Std Dev	11.35

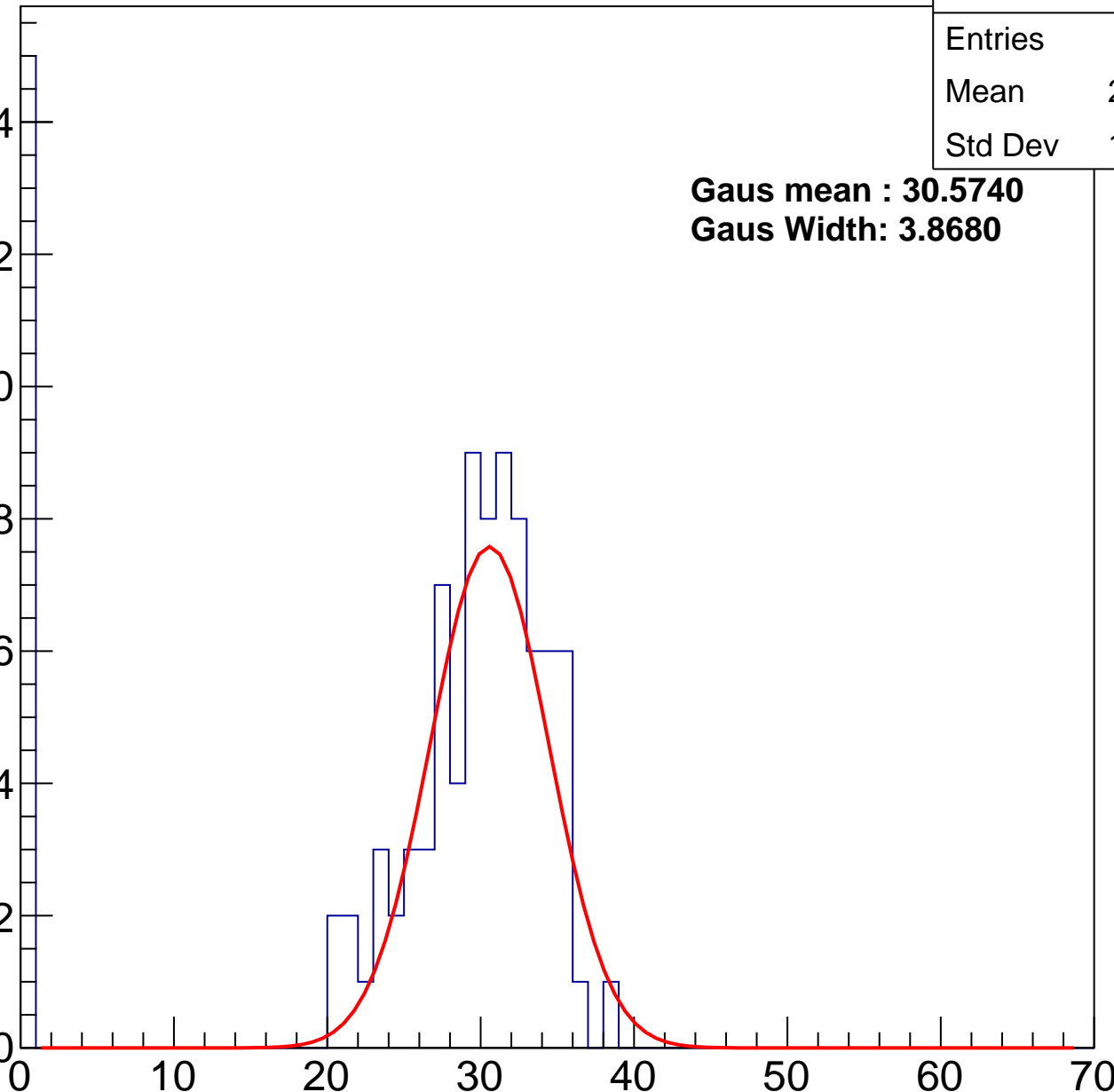
Gaus mean : 30.5740

Gaus Width: 3.8680

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch45, adc1

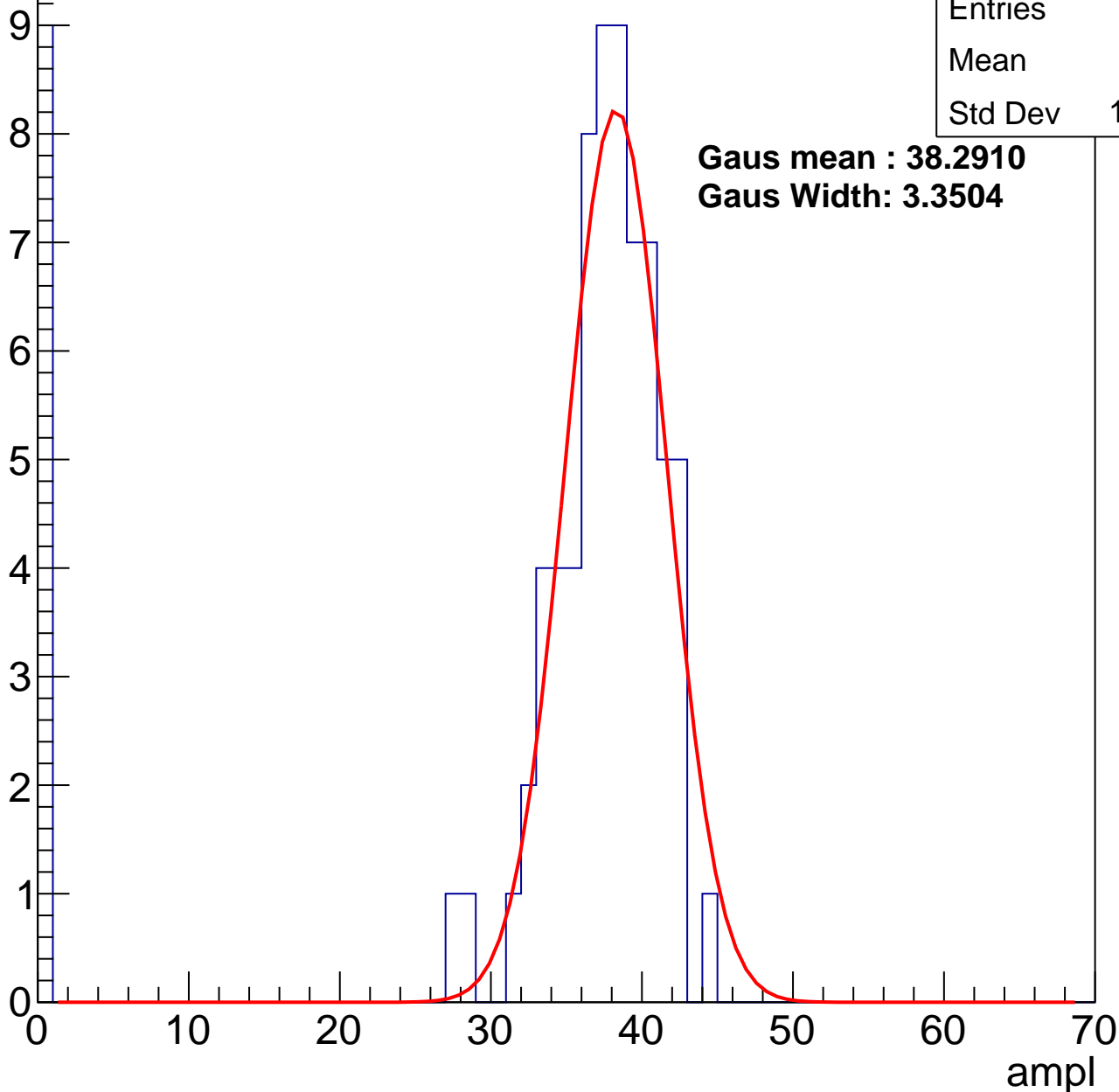
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	32.9
Std Dev	12.36

Gaus mean : 38.2910

Gaus Width: 3.3504



B1L103S, U2-ch45, adc2

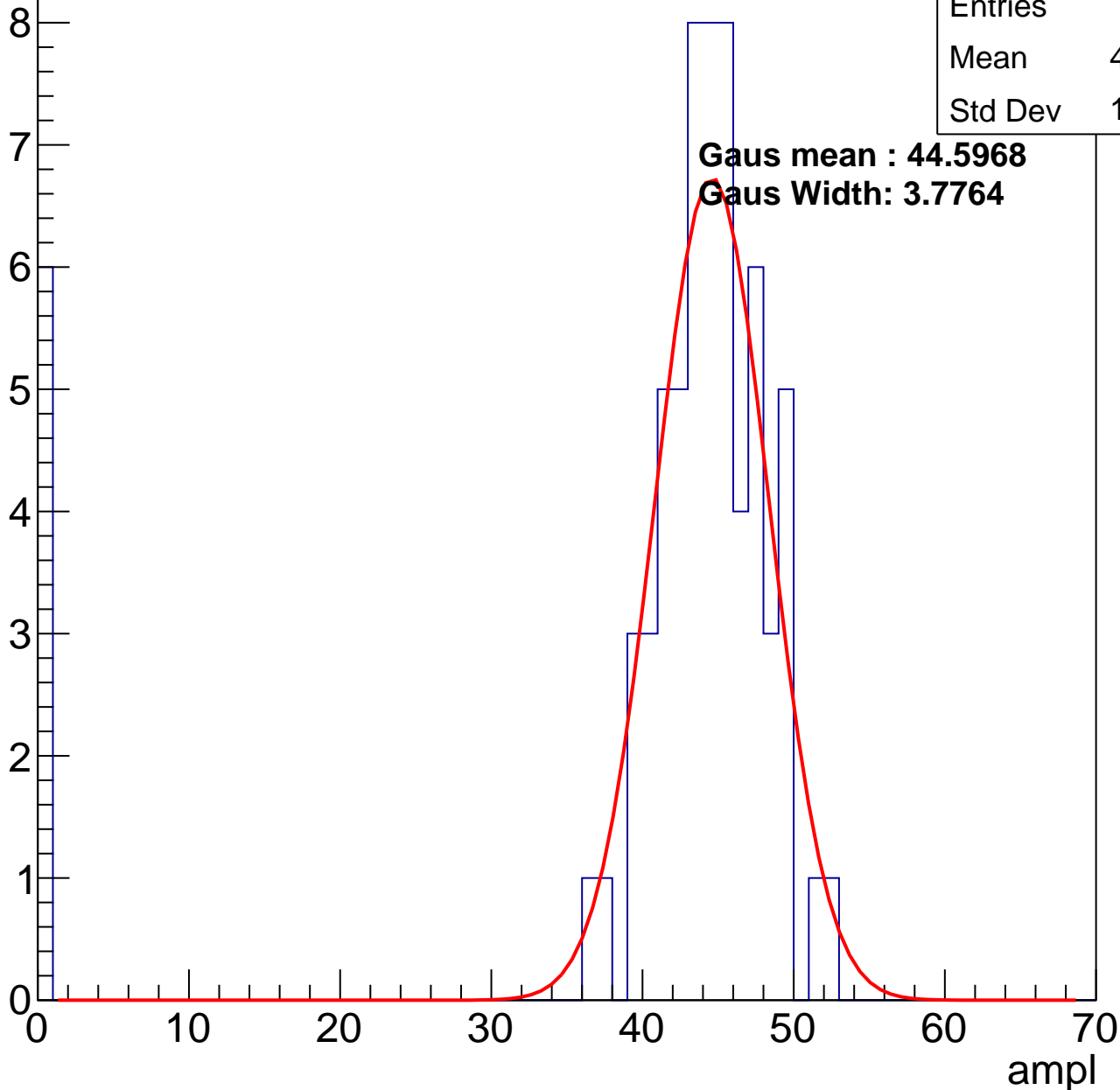
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.28
Std Dev	12.92

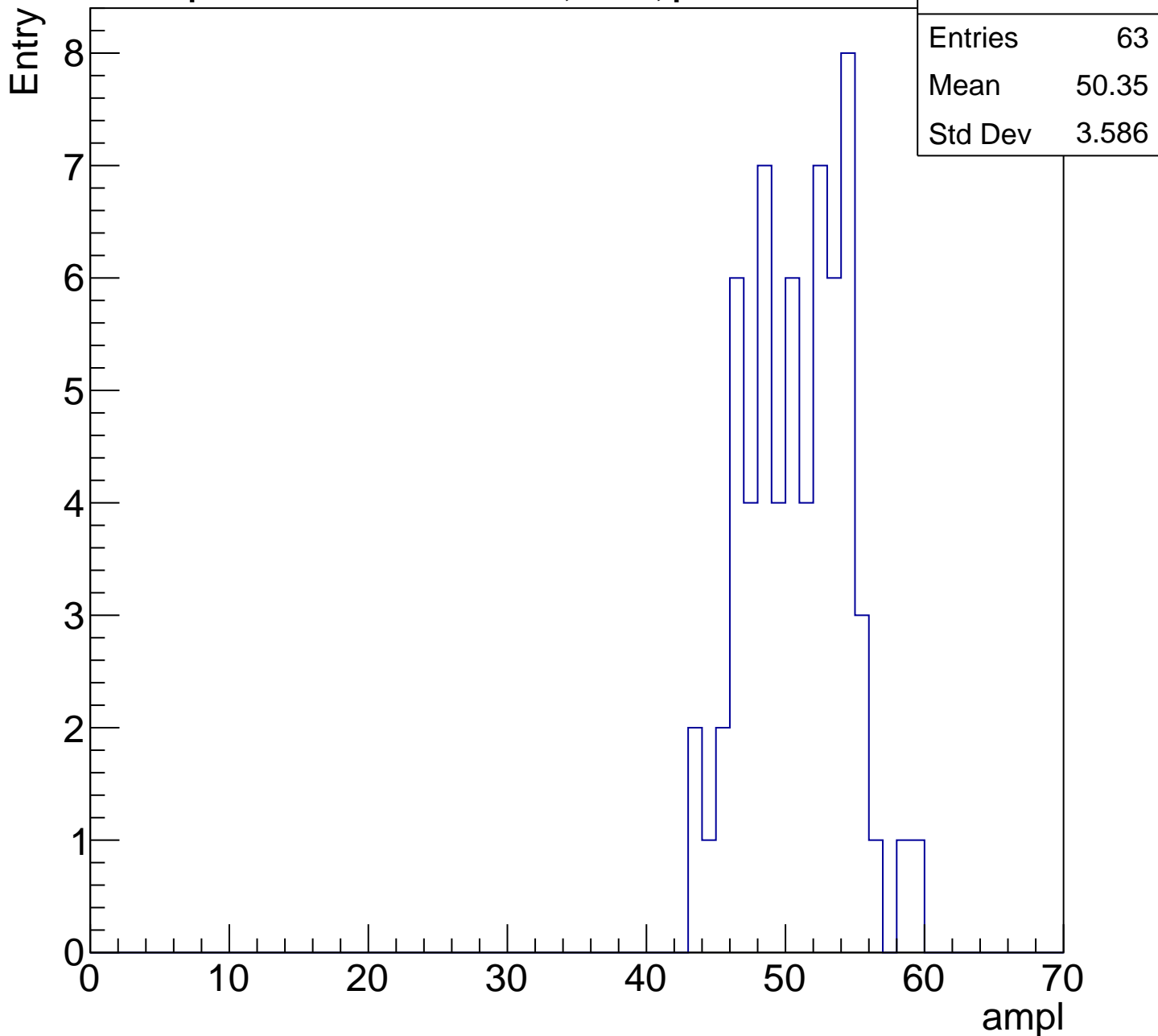
Gaus mean : 44.5968

Gaus Width: 3.7764



B1L103S, U2-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

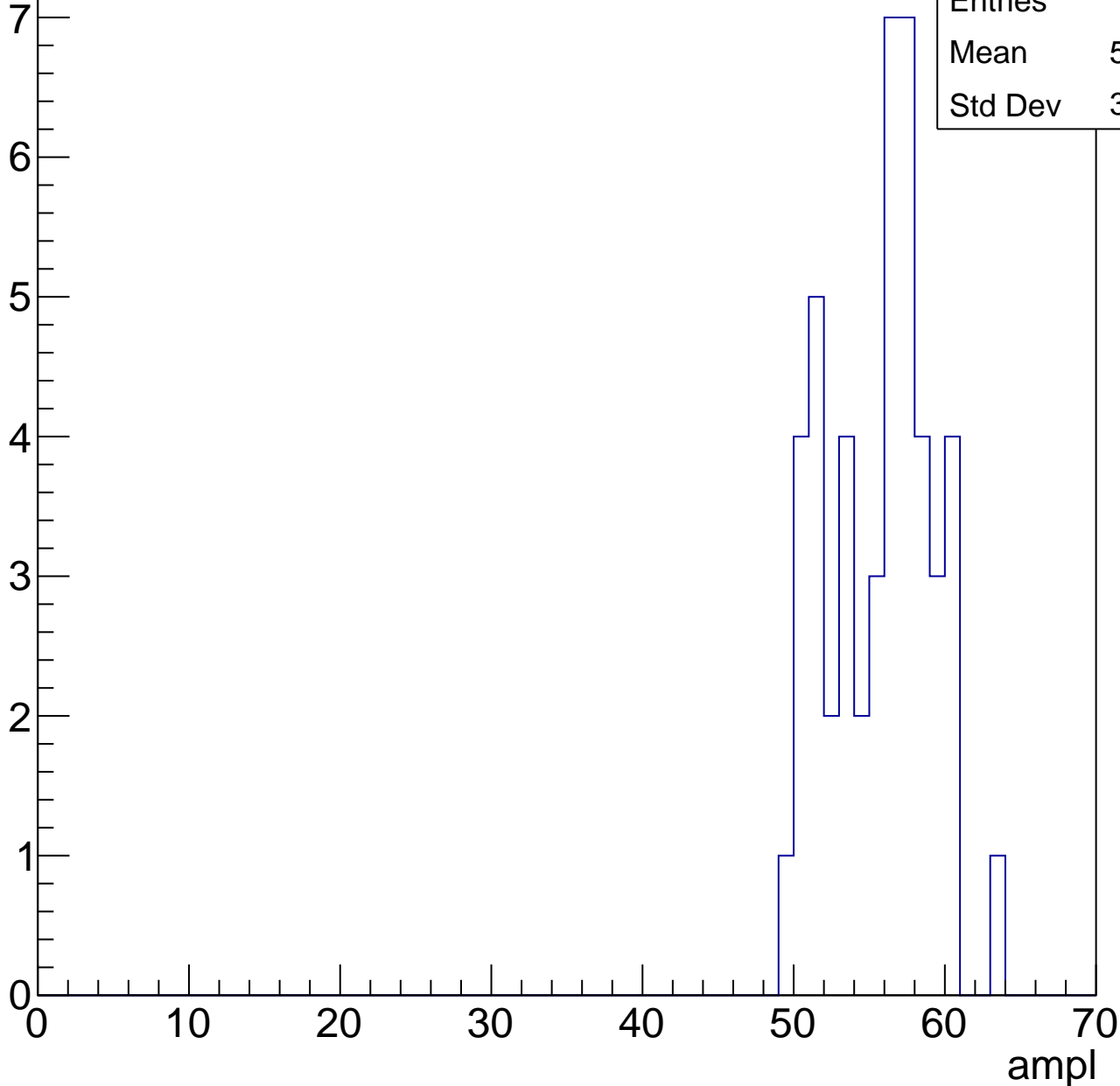


B1L103S, U2-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	55.23
Std Dev	3.366

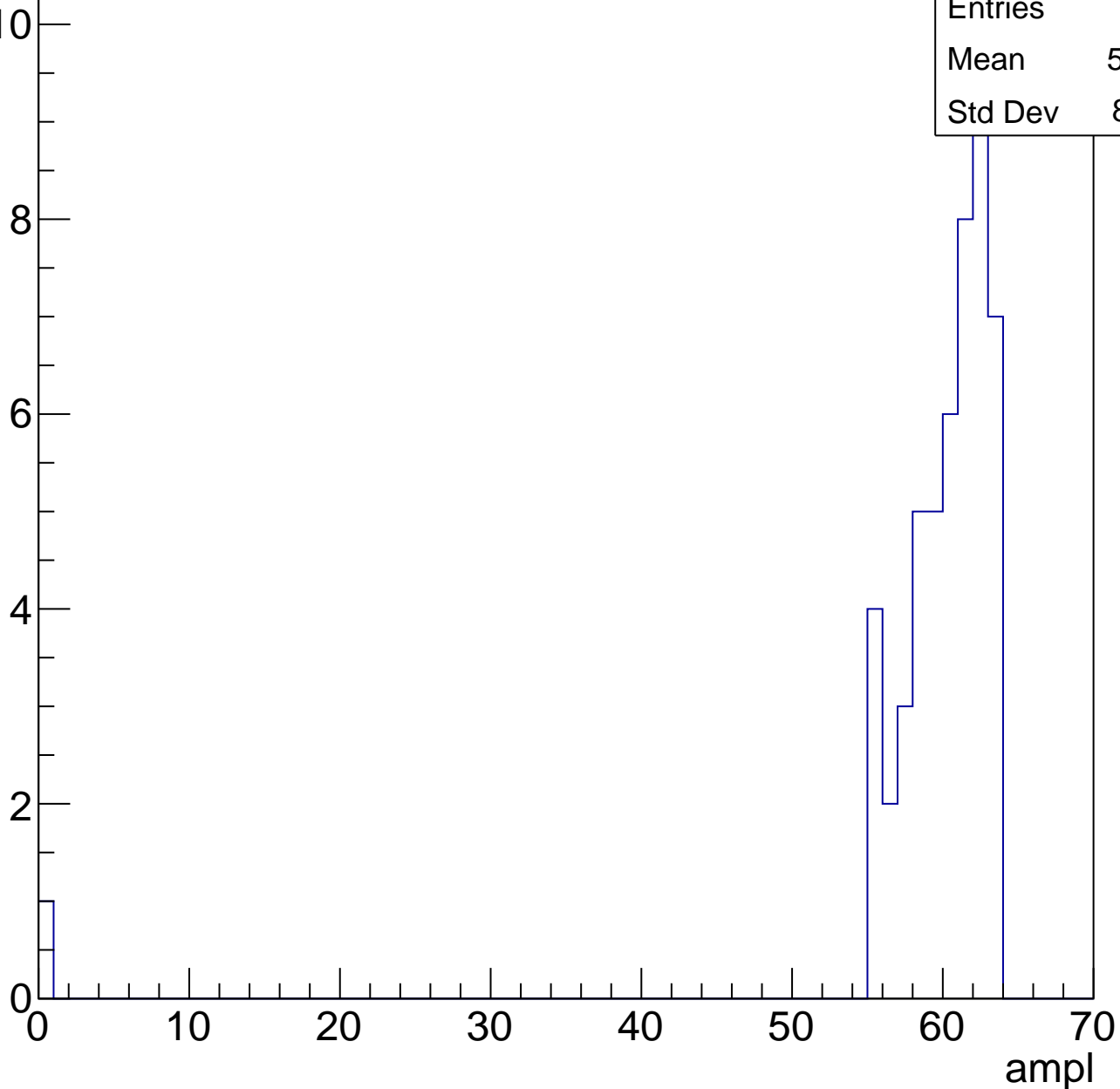


B1L103S, U2-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.76
Std Dev	8.651



B1L103S, U2-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

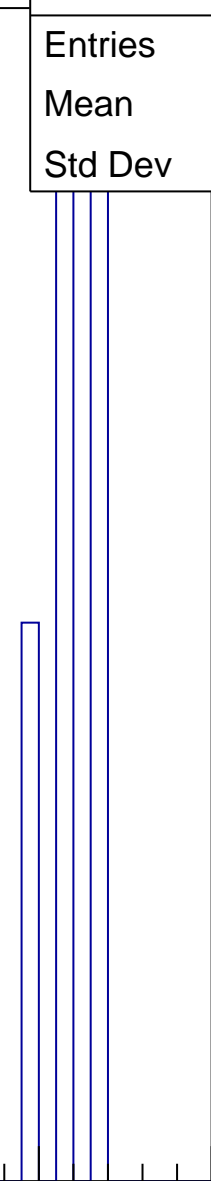
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.497

0 10 20 30 40 50 60 70

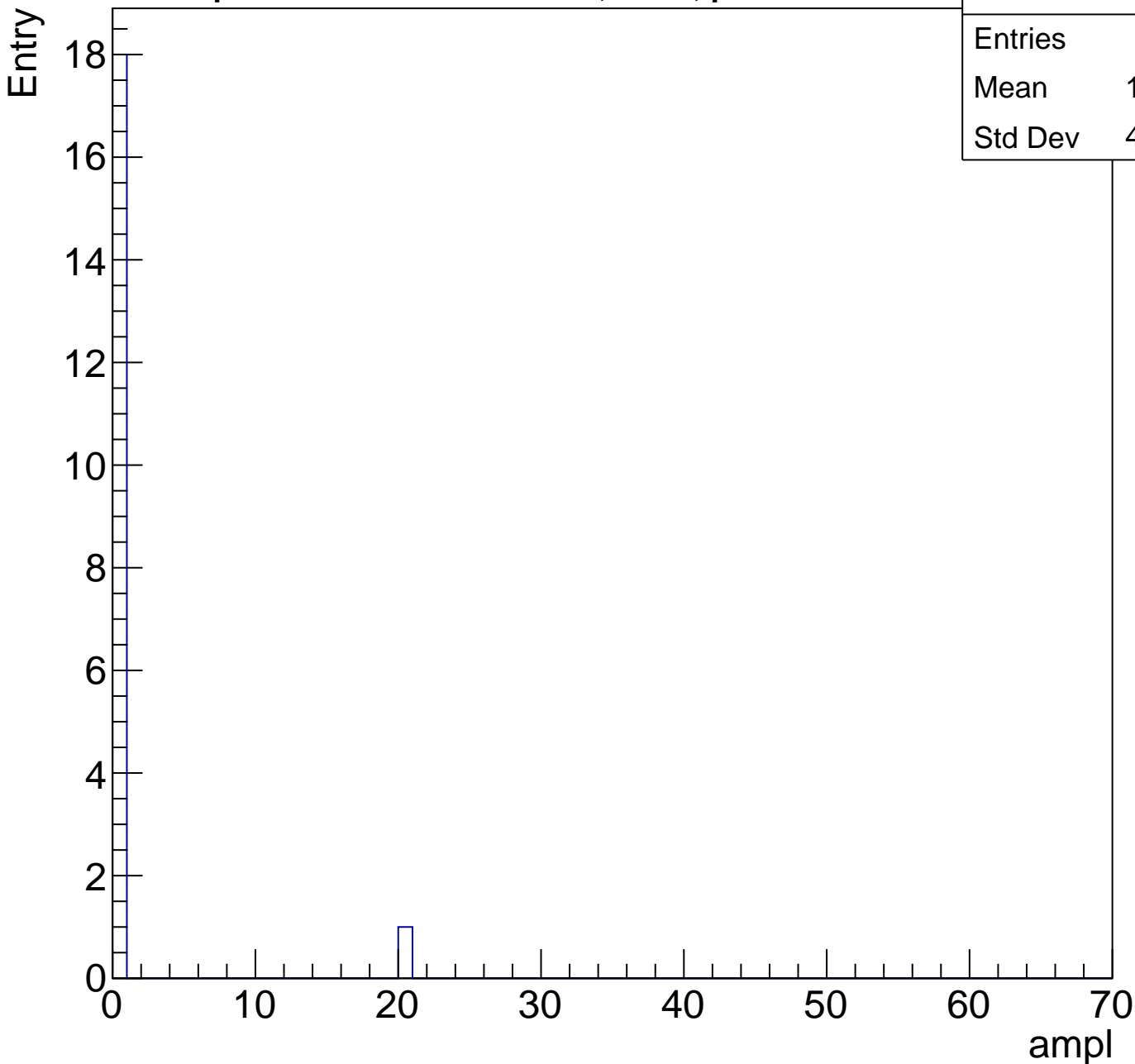
ampl



B1L103S, U2-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466



B1L103S, U2-ch46, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	23.61
Std Dev	11.01

Gaus mean : 28.7058

Gaus Width: 3.6402

Entry

12

10

8

6

4

2

0

0

10

20

30

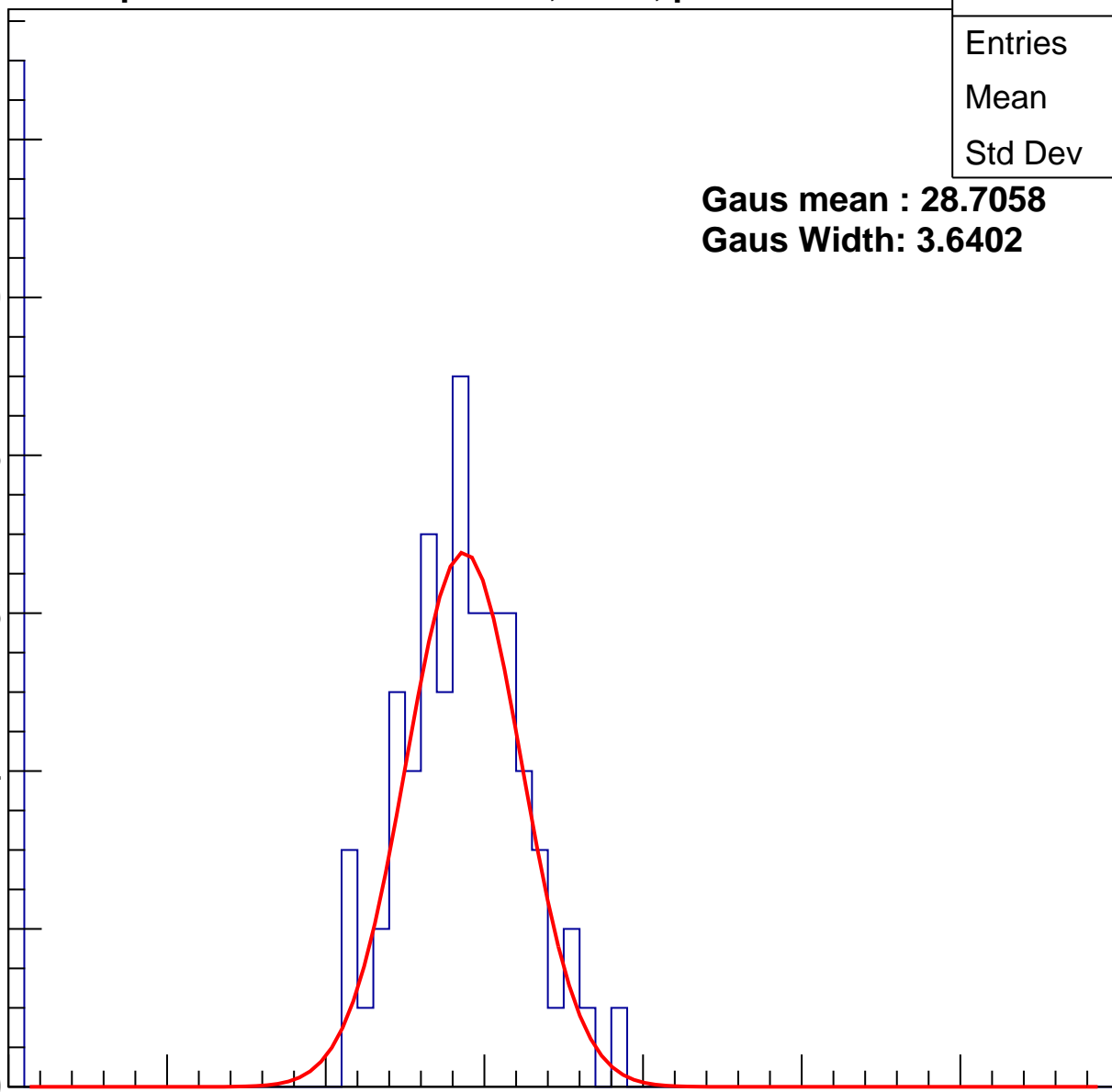
40

50

60

70

ampl



B1L103S, U2-ch46, adc1

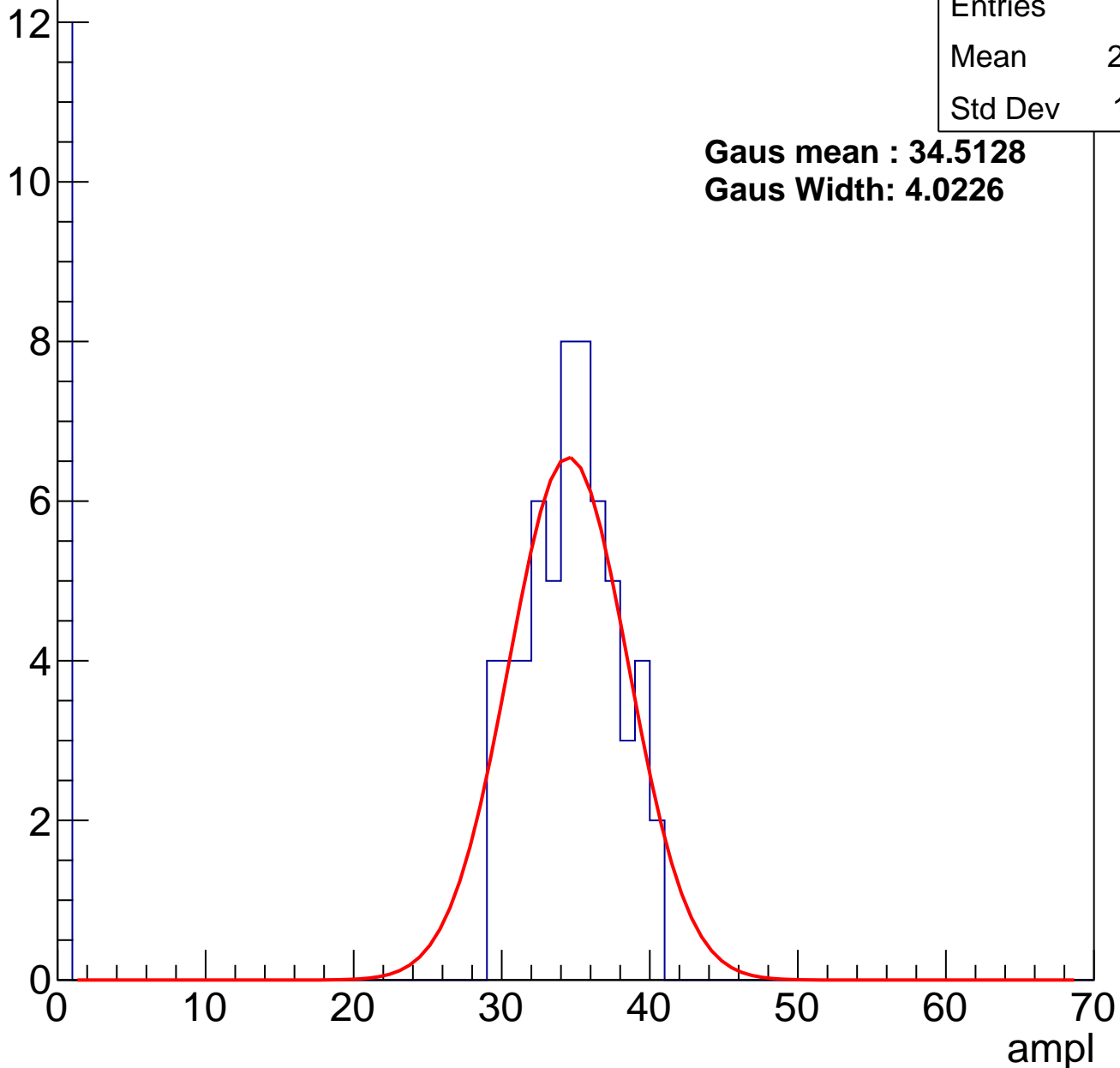
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	28.45
Std Dev	13.11

Gaus mean : 34.5128

Gaus Width: 4.0226

Entry



B1L103S, U2-ch46, adc2

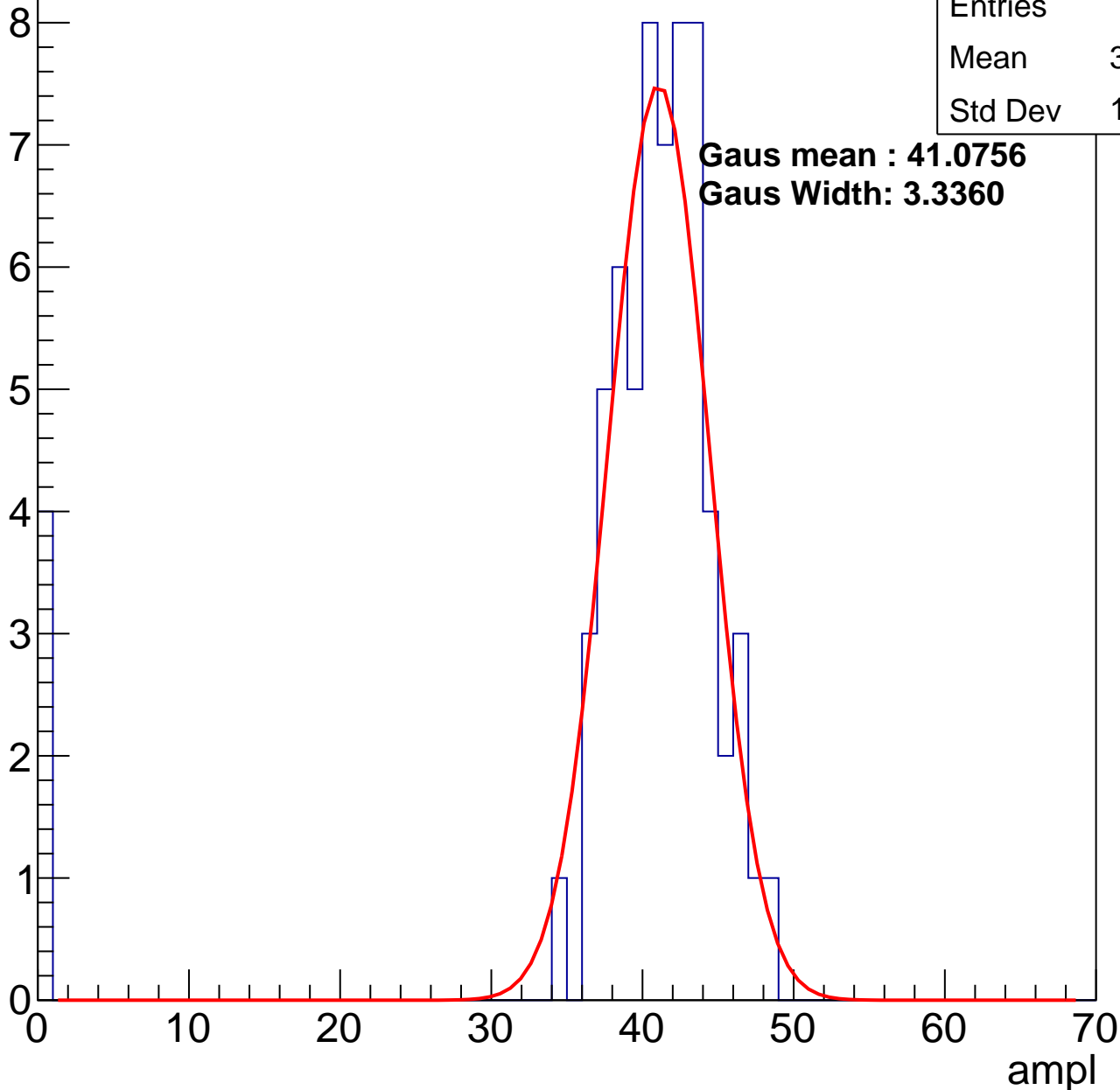
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	38.42
Std Dev	10.18

Gaus mean : 41.0756

Gaus Width: 3.3360

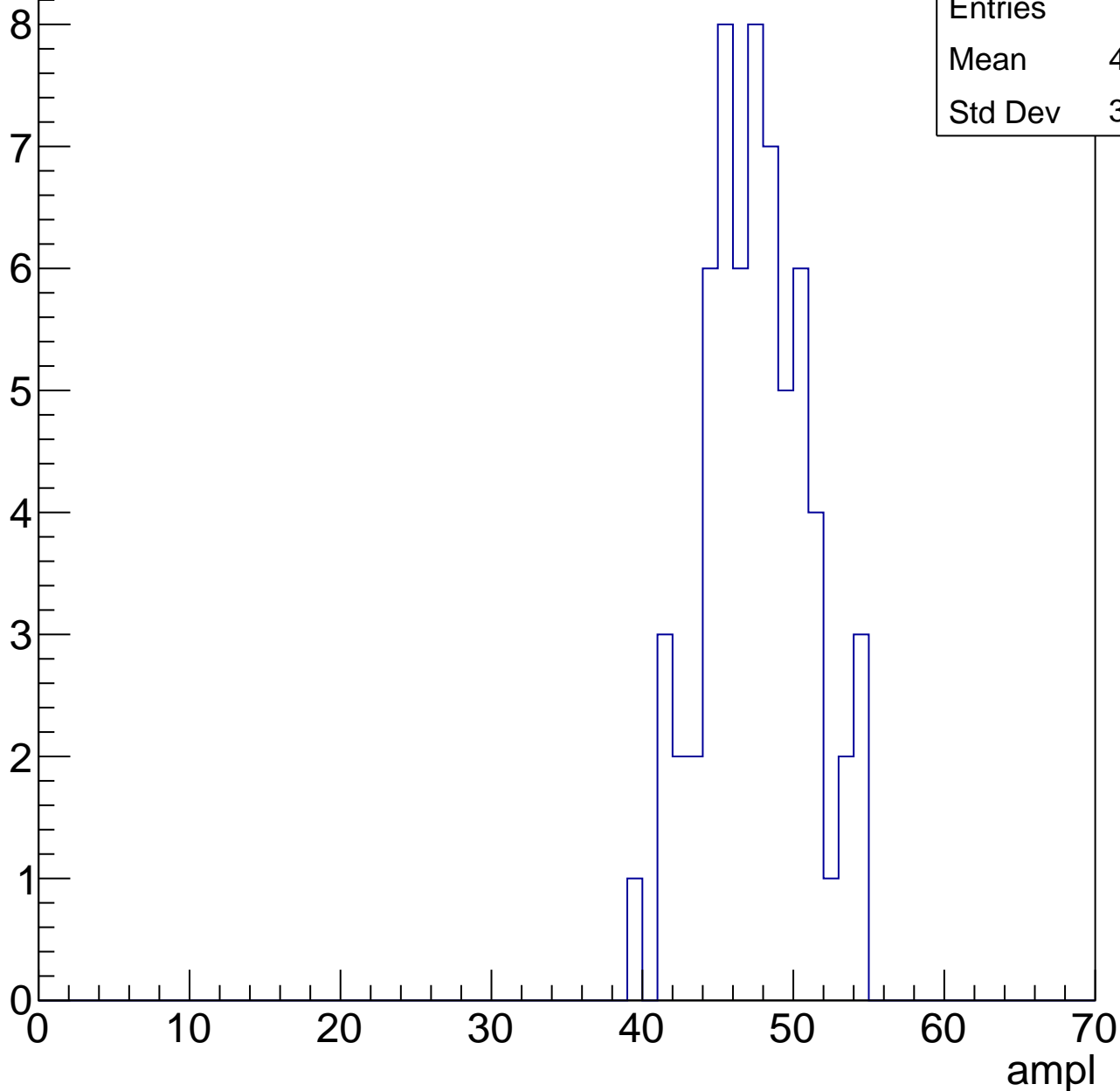


B1L103S, U2-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

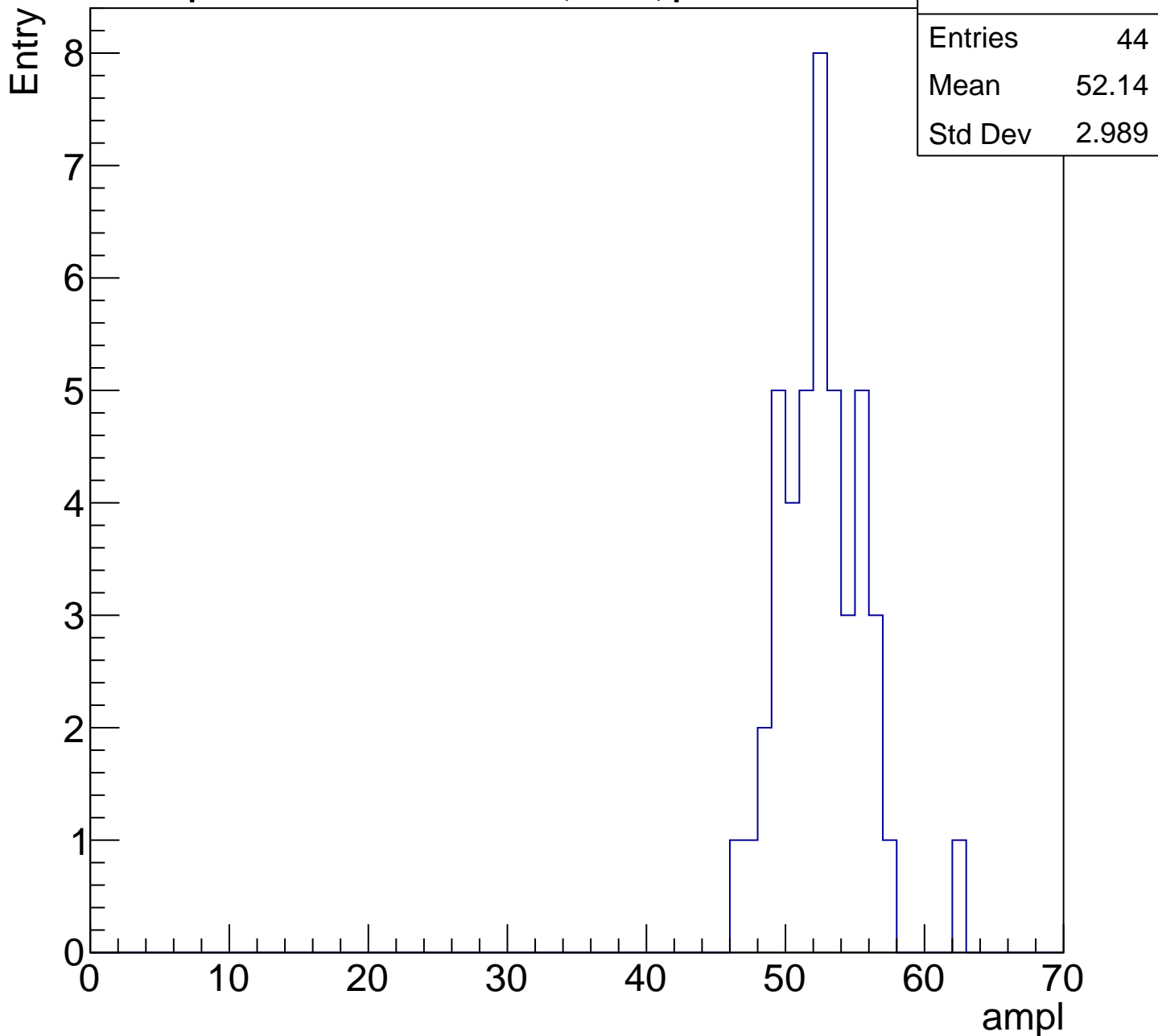
Entry

Entries	64
Mean	47.08
Std Dev	3.388



B1L103S, U2-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

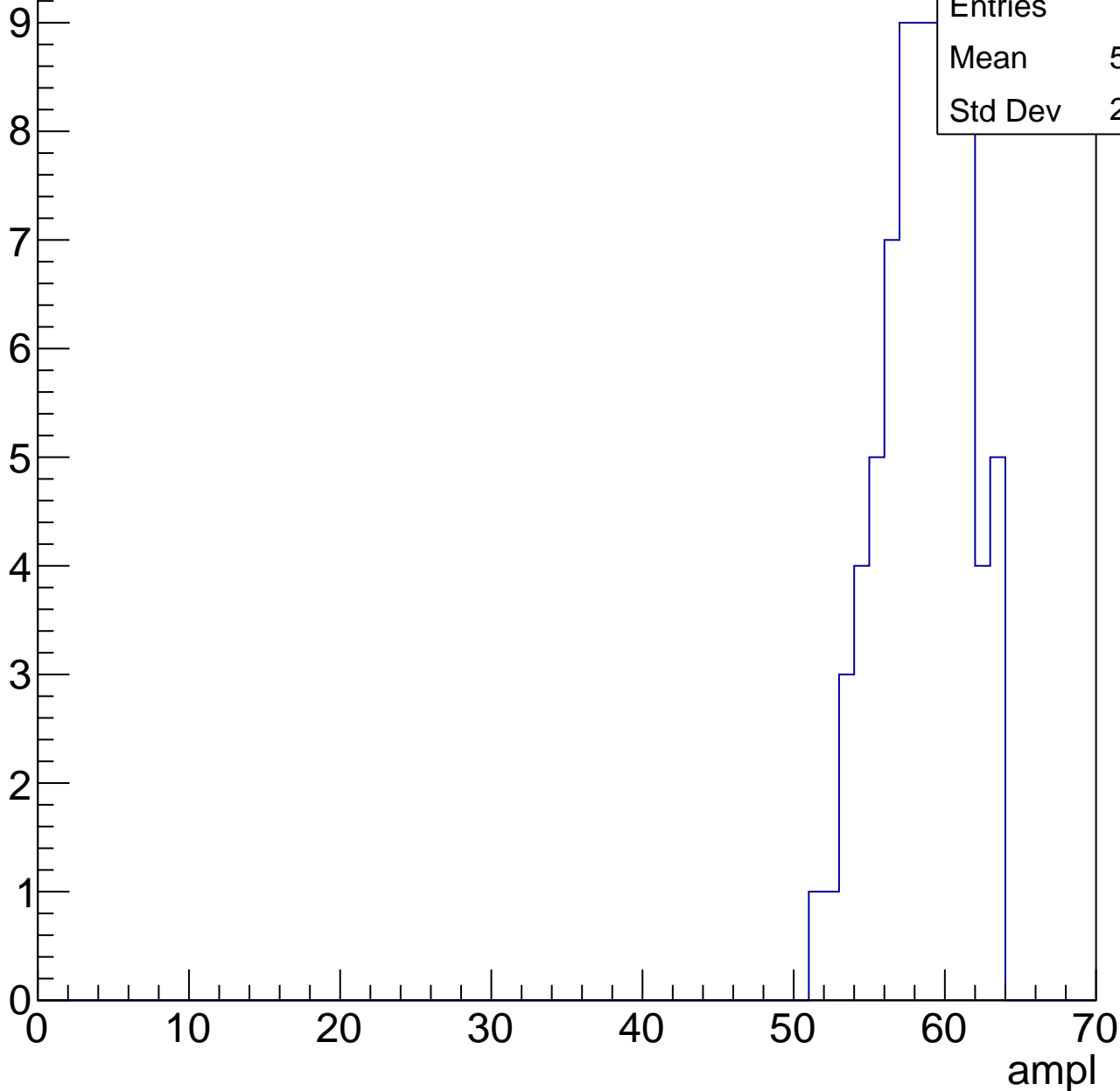


B1L103S, U2-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	58.15
Std Dev	2.884

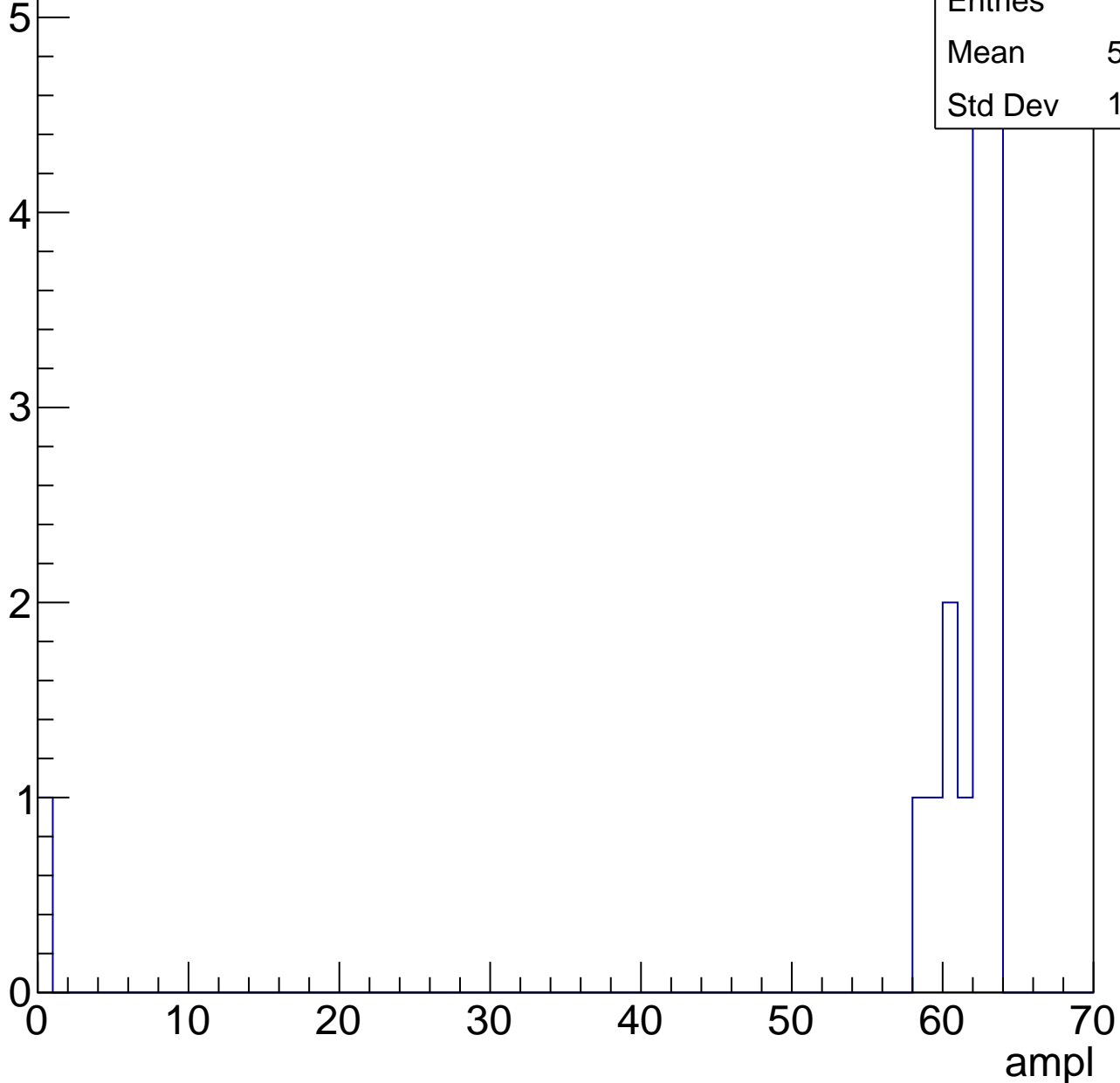


B1L103S, U2-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.69
Std Dev	14.97



B1L103S, U2-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch47, adc0

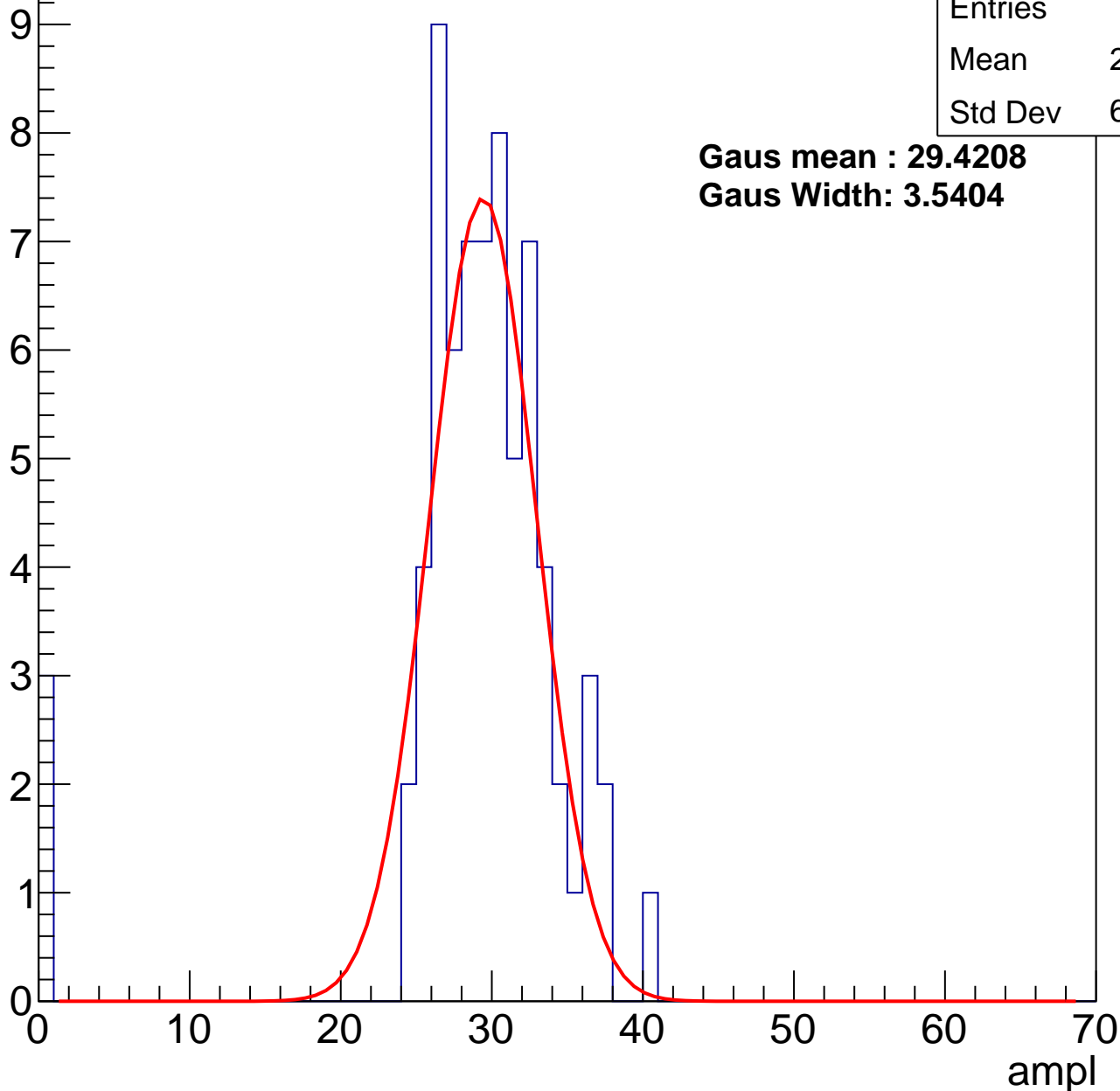
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	28.44
Std Dev	6.883

Gaus mean : 29.4208

Gaus Width: 3.5404



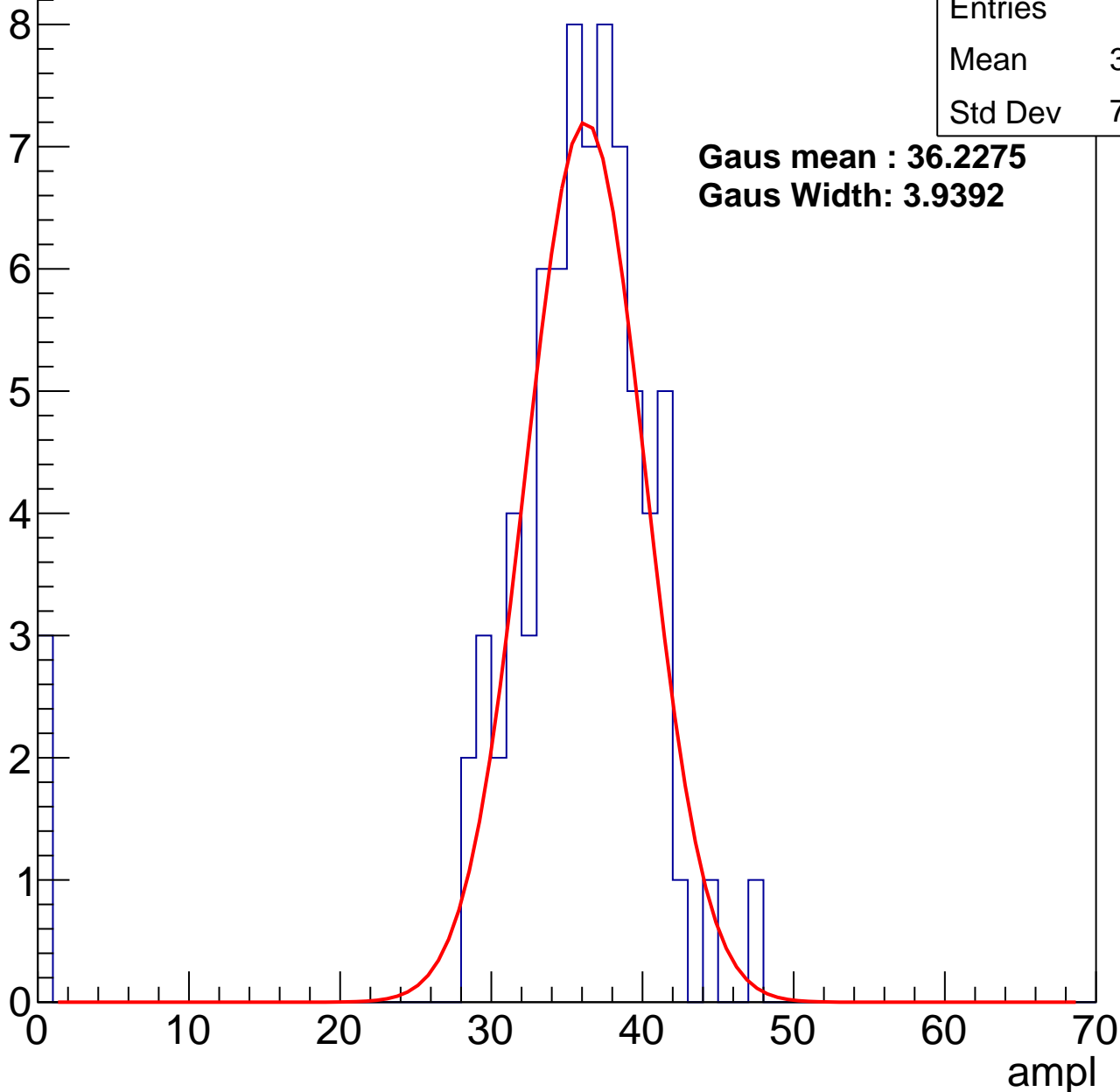
B1L103S, U2-ch47, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.37
Std Dev	7.912

Gaus mean : 36.2275
Gaus Width: 3.9392



B1L103S, U2-ch47, adc2

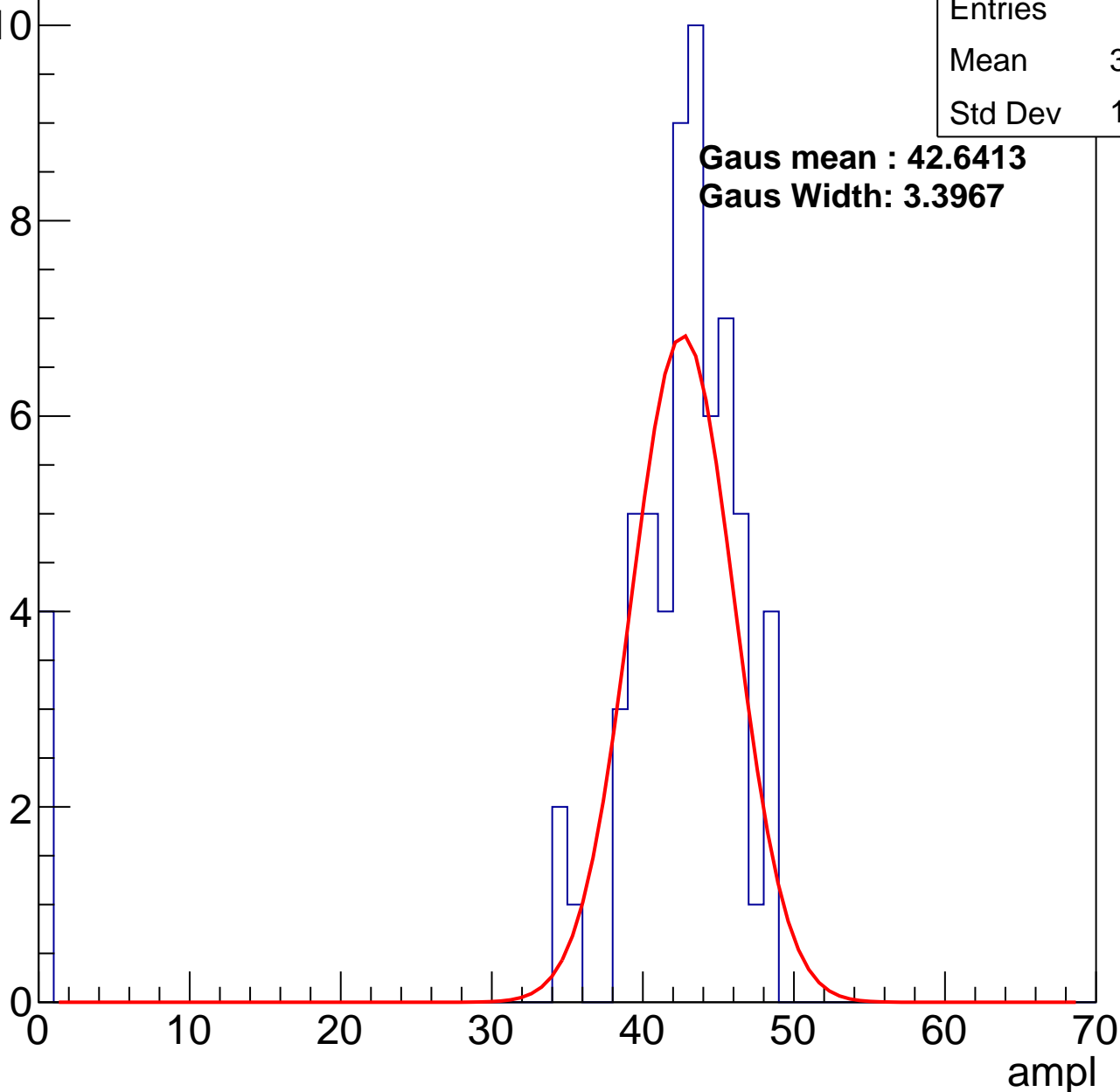
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	39.88
Std Dev	10.59

Gaus mean : 42.6413

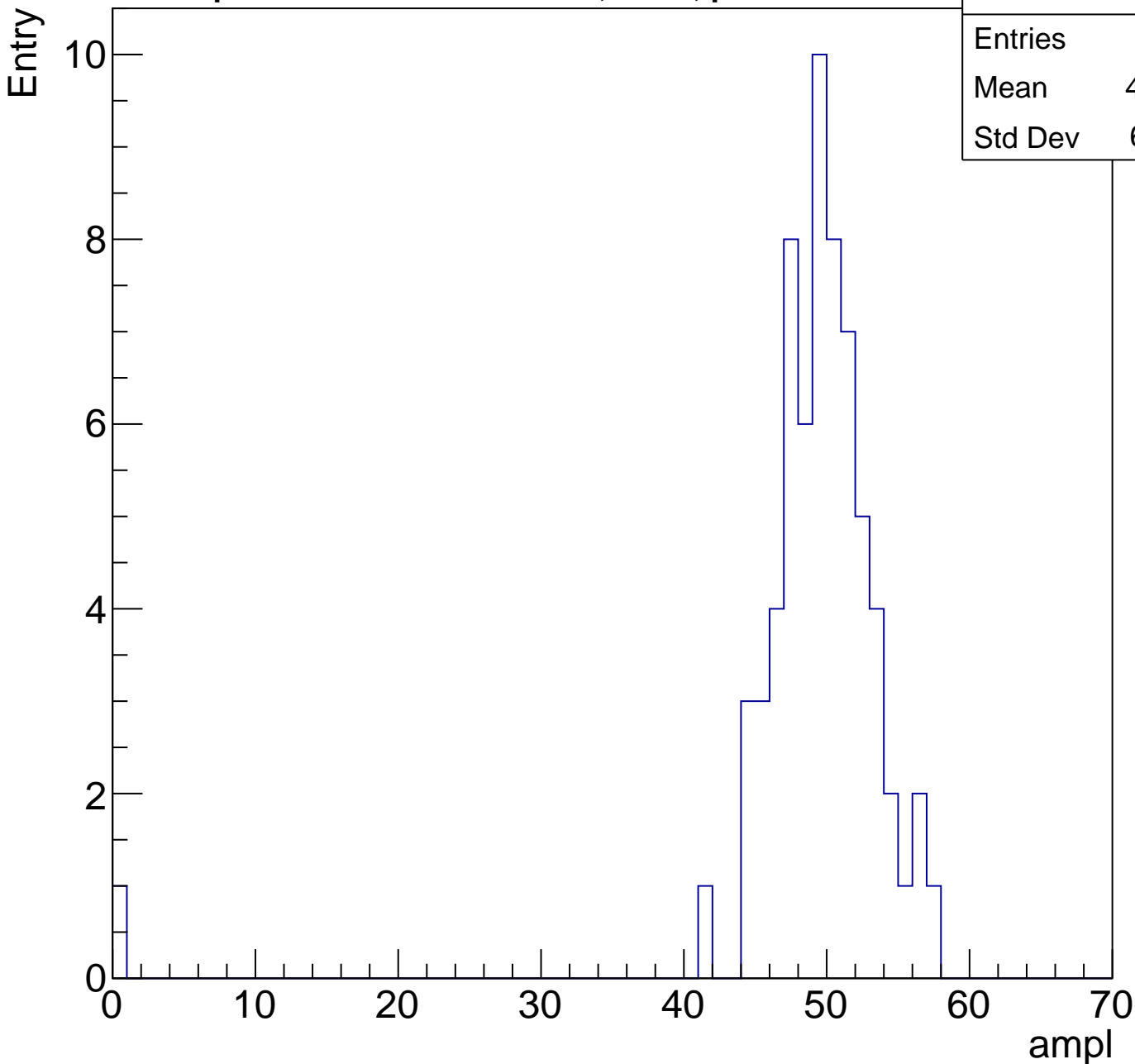
Gaus Width: 3.3967



B1L103S, U2-ch47, adc3

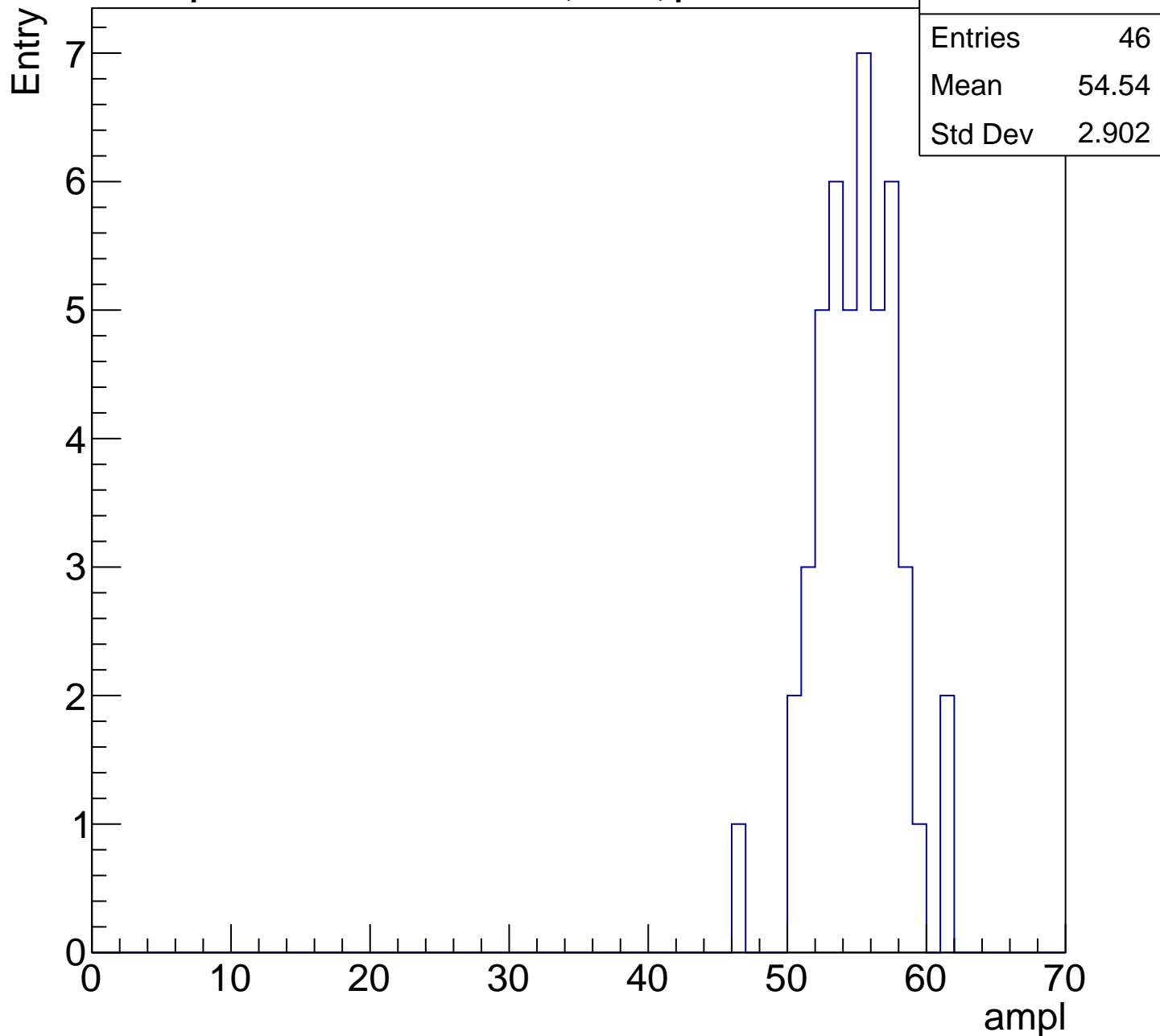
calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	48.59
Std Dev	6.791



B1L103S, U2-ch47, adc4

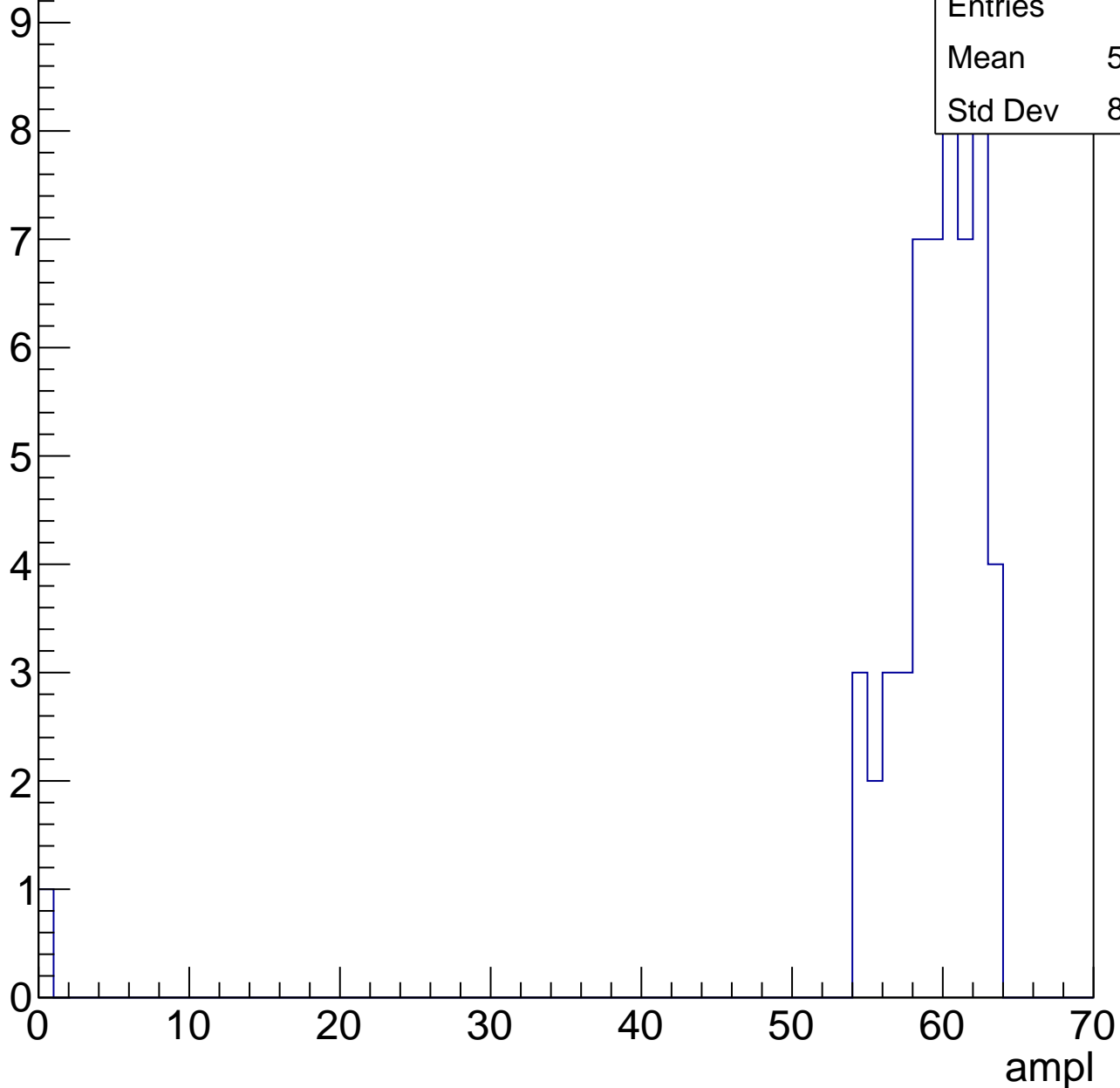
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

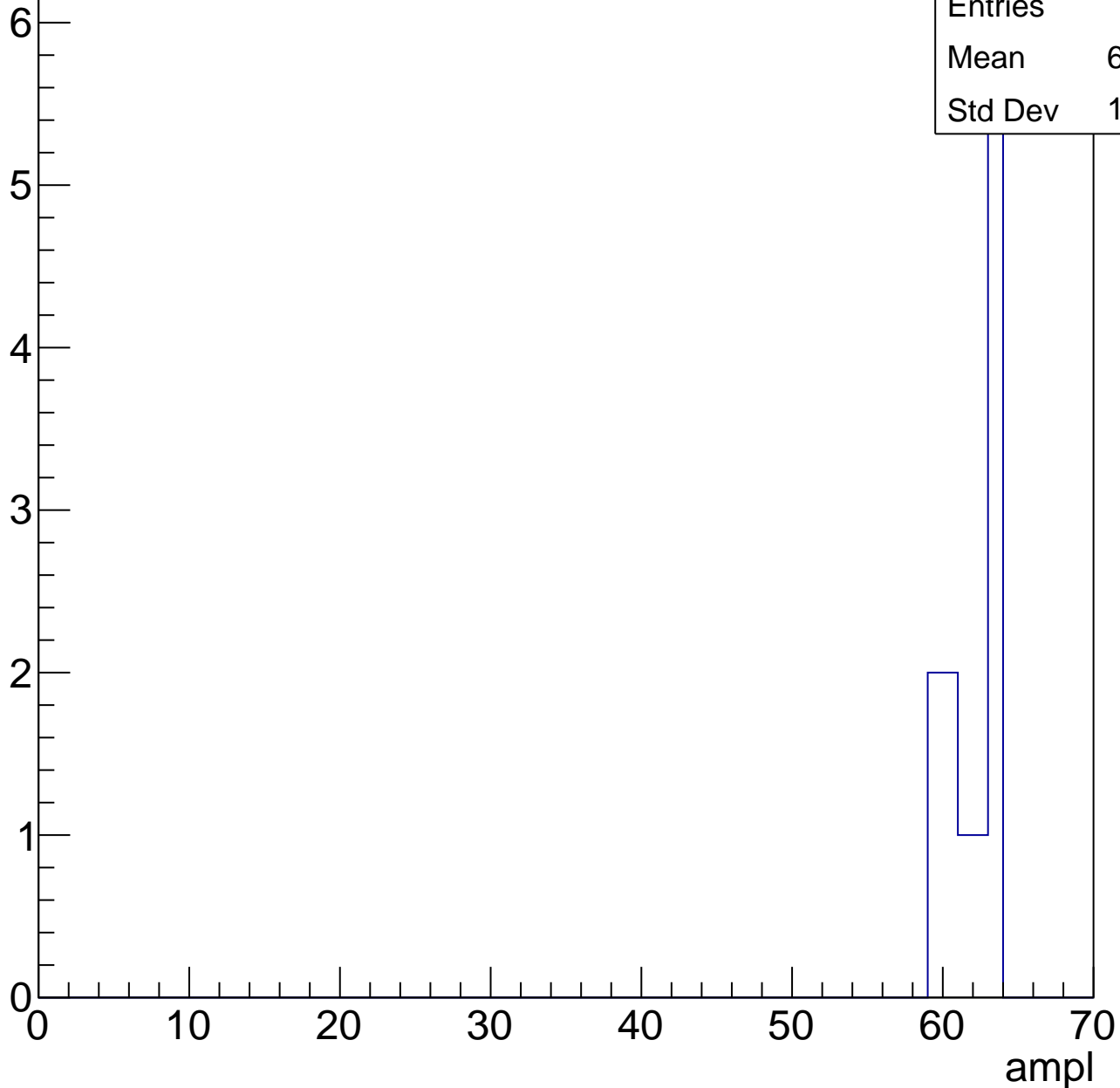
Entry



B1L103S, U2-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch48, adc0

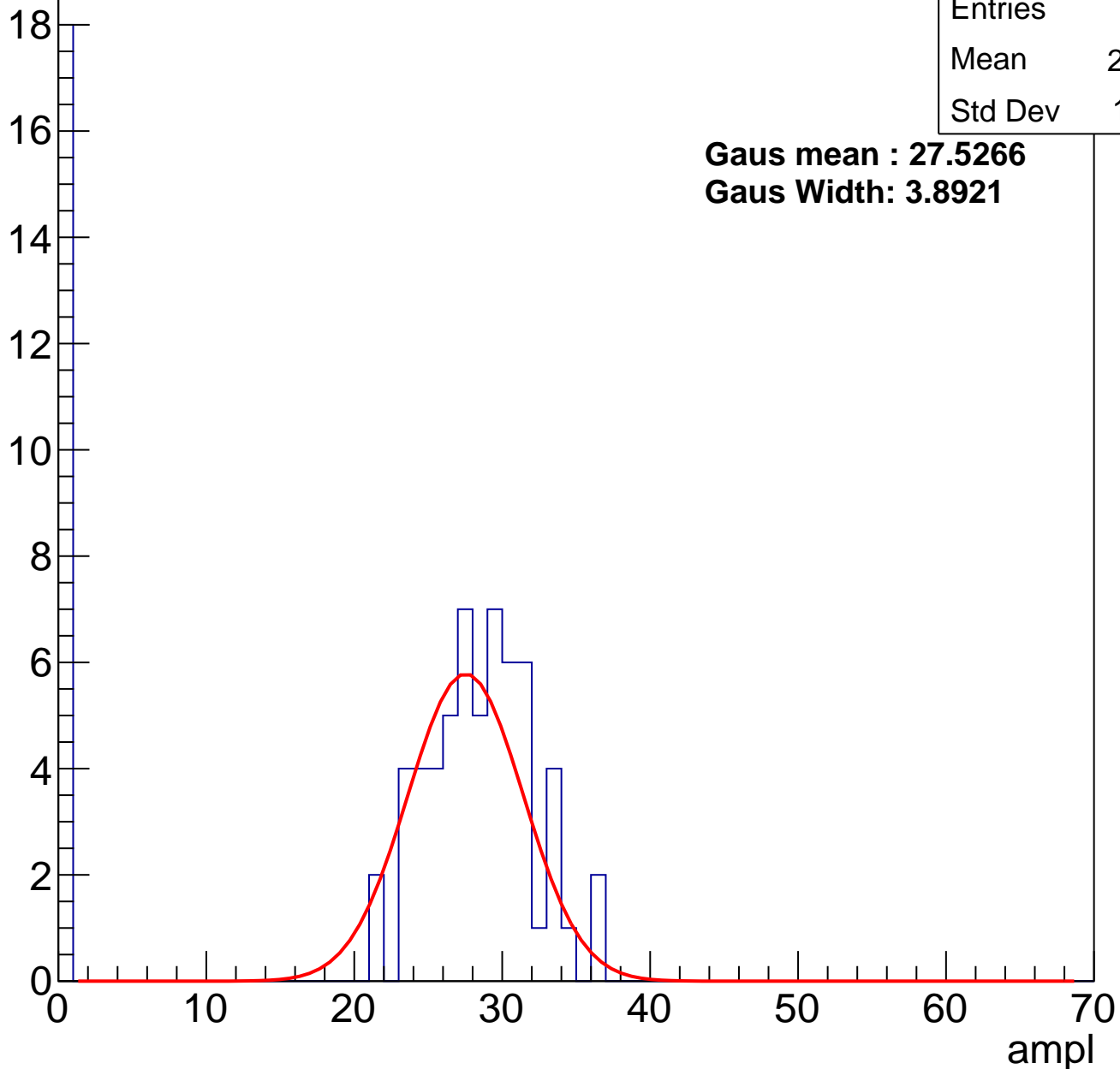
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	21.42
Std Dev	12.31

Gaus mean : 27.5266

Gaus Width: 3.8921

Entry



B1L103S, U2-ch48, adc1

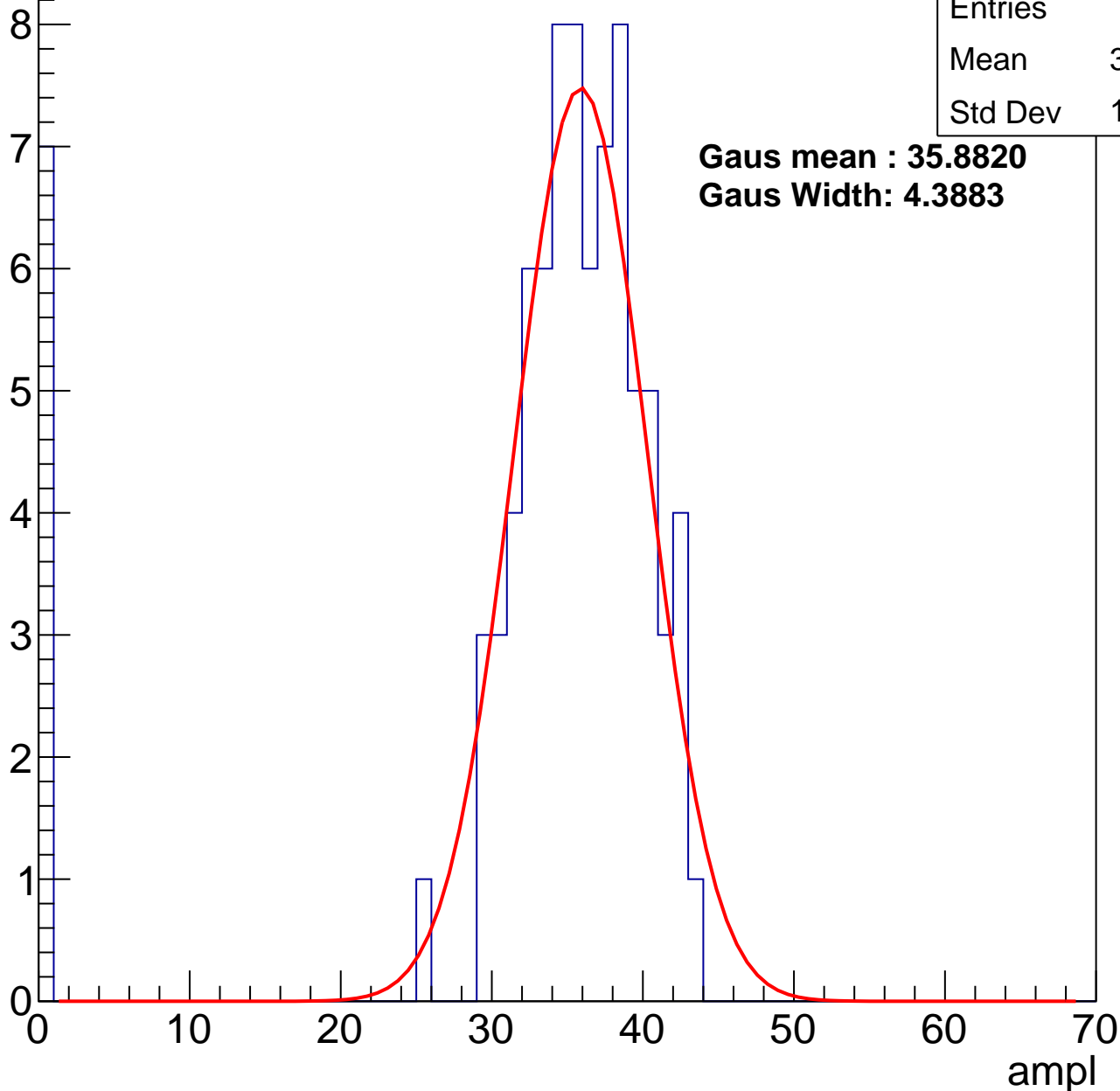
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	32.66
Std Dev	10.42

Gaus mean : 35.8820

Gaus Width: 4.3883



B1L103S, U2-ch48, adc2

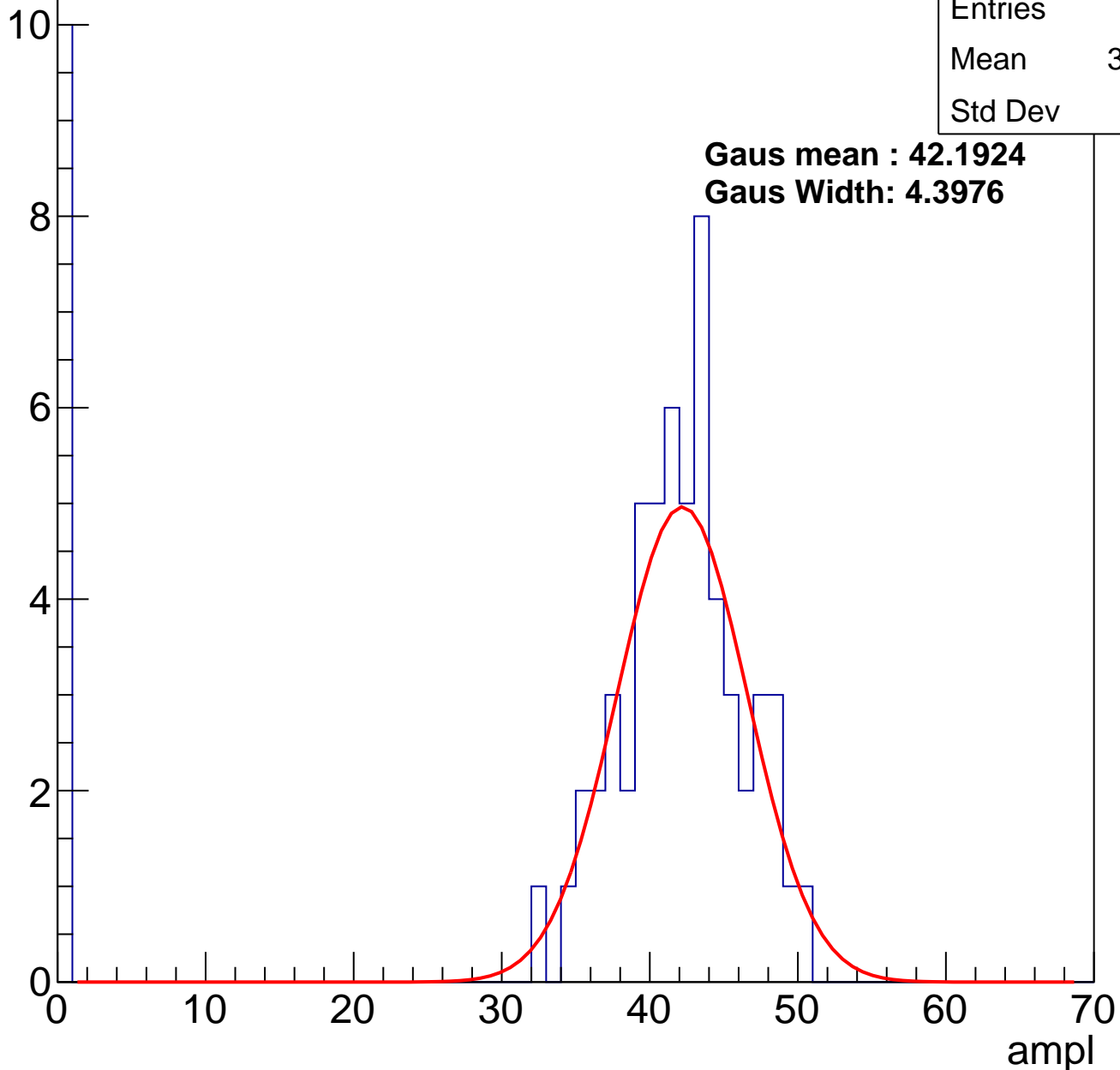
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	35.48
Std Dev	15.3

Gaus mean : 42.1924

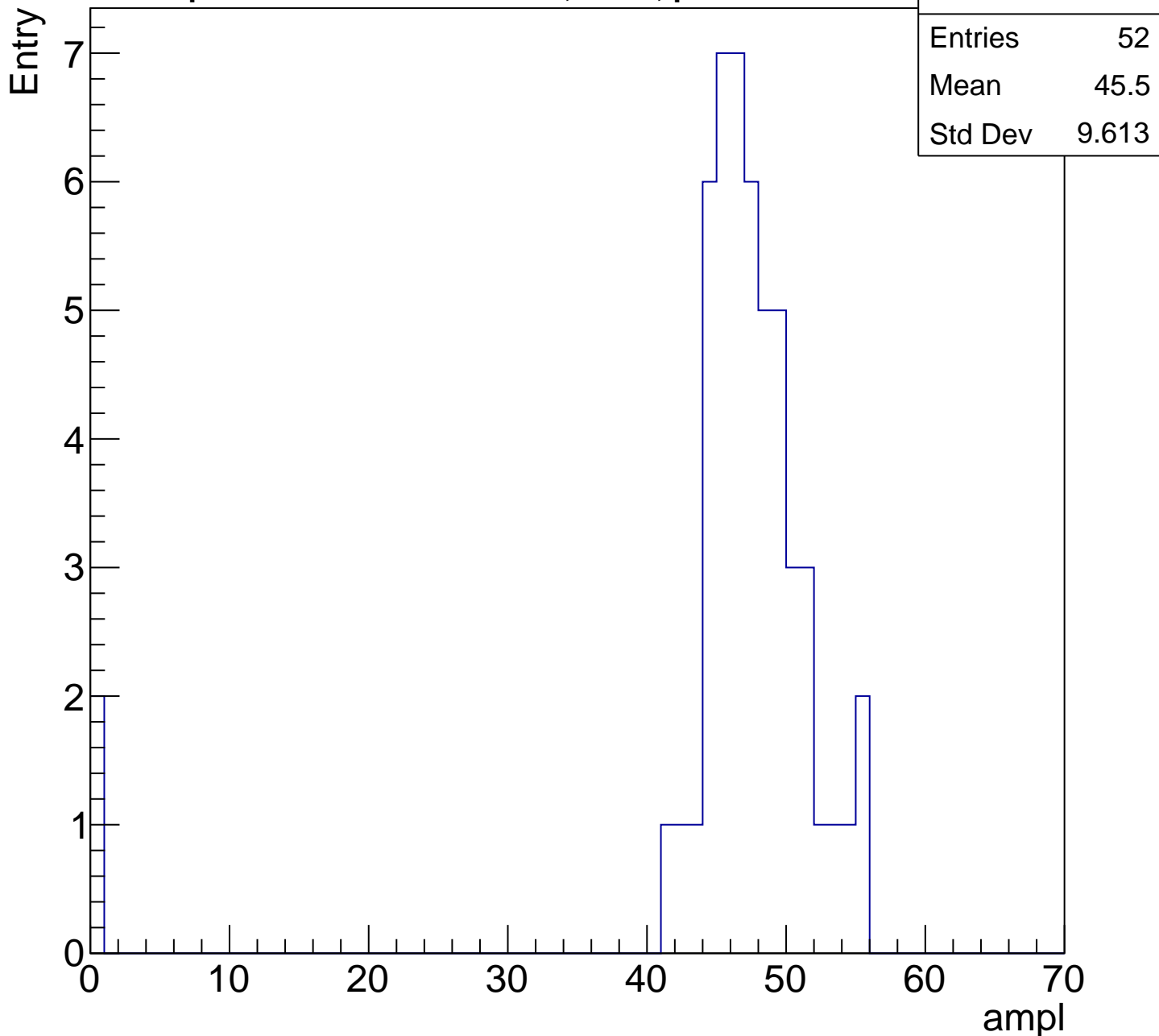
Gaus Width: 4.3976

Entry



B1L103S, U2-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

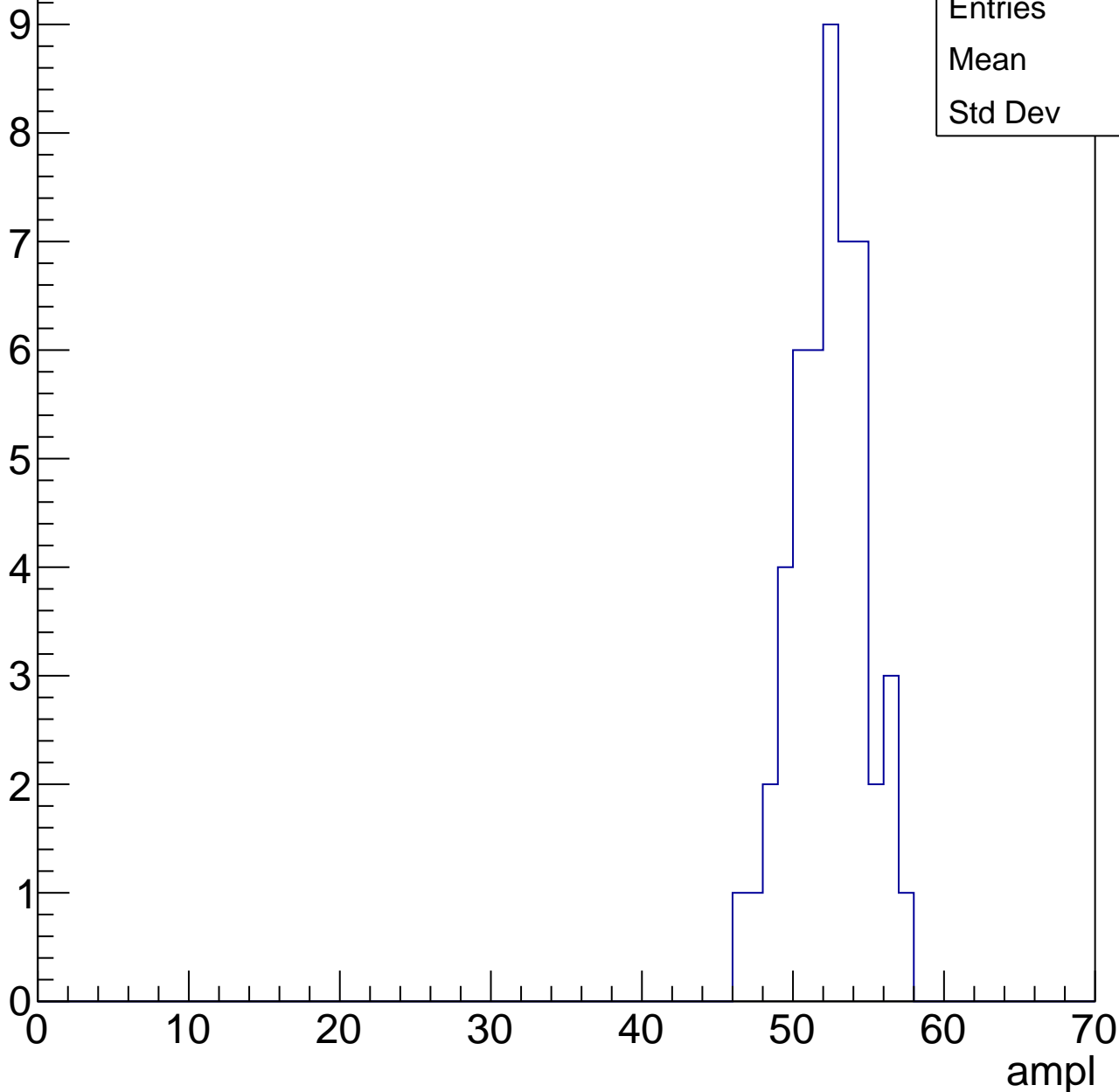


B1L103S, U2-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	51.9
Std Dev	2.41

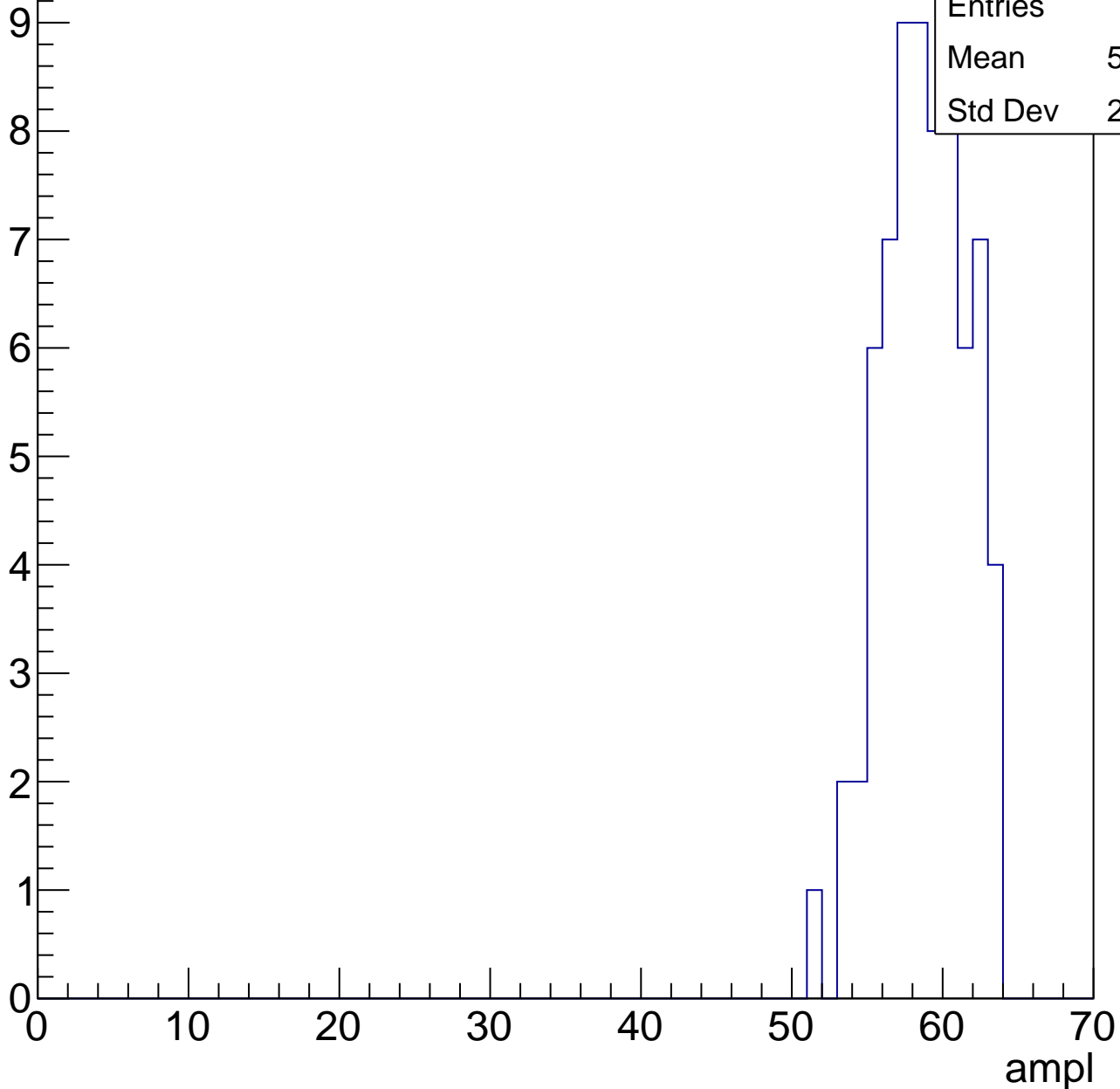


B1L103S, U2-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	58.35
Std Dev	2.744

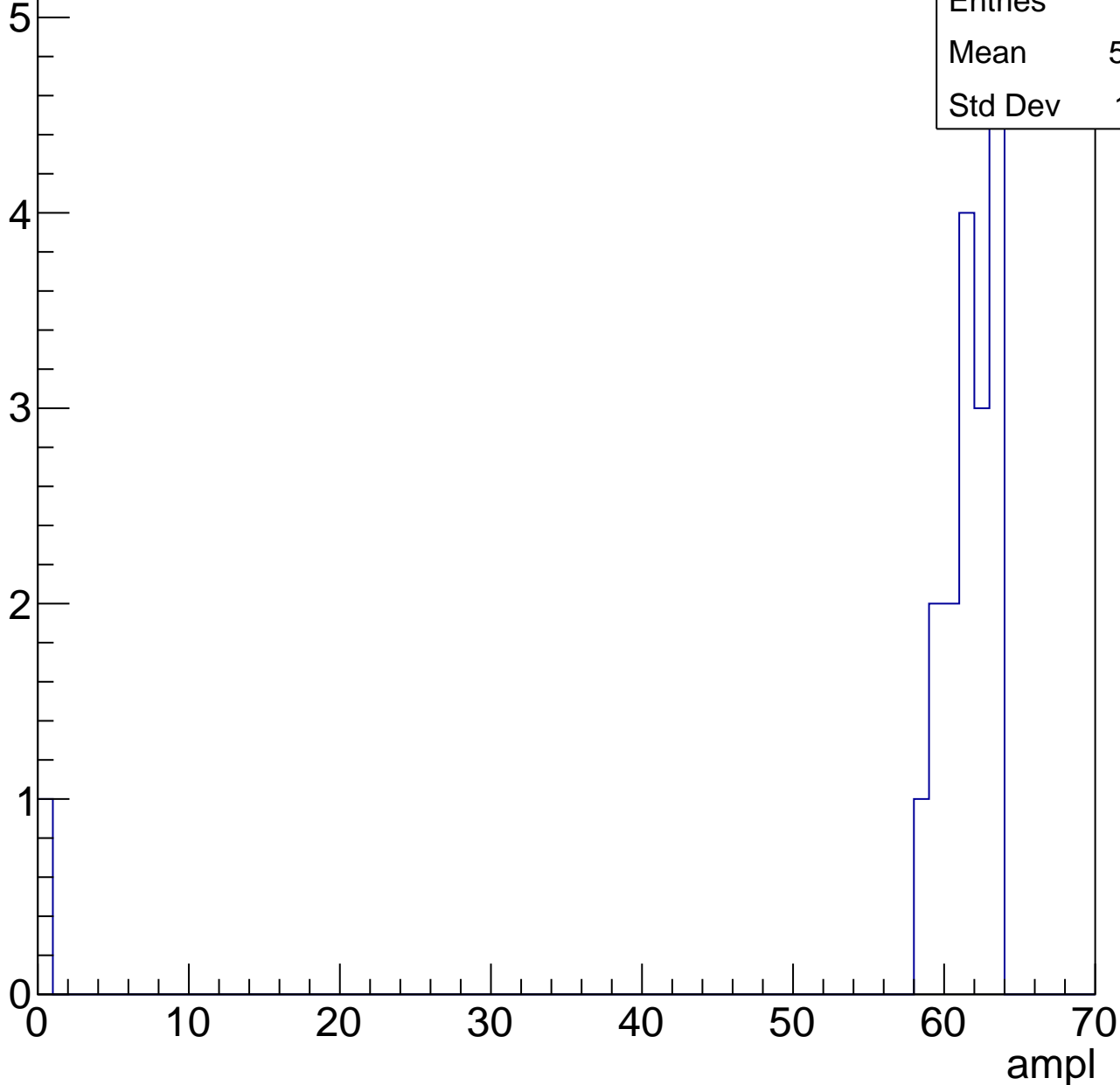


B1L103S, U2-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

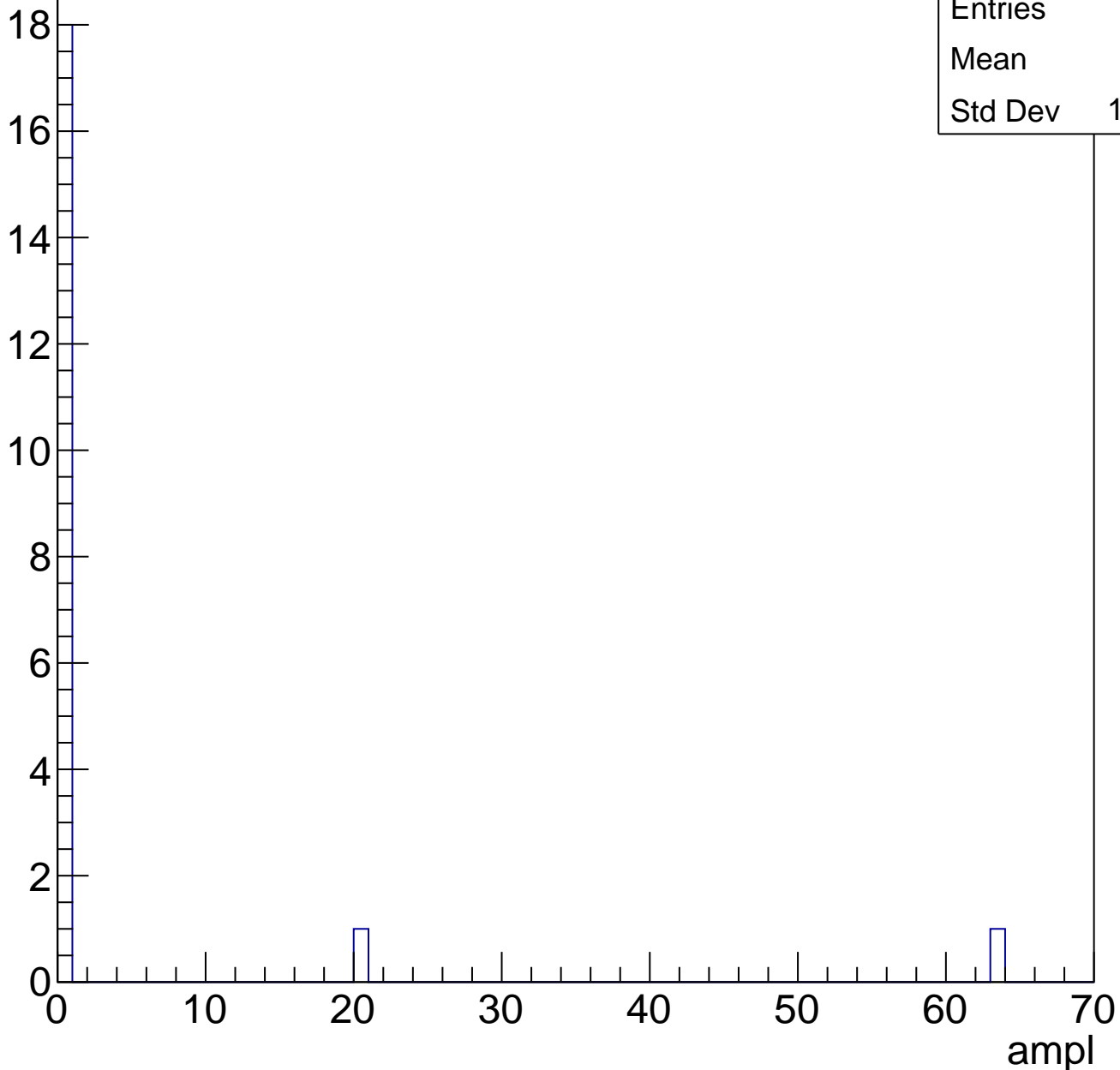
Entries	18
Mean	57.83
Std Dev	14.11



B1L103S, U2-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	4.15
Std Dev	14.19

B1L103S, U2-ch49, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	21.44
Std Dev	13.1

Gaus mean : 30.0323

Gaus Width: 3.9292

Entry

25

20

15

10

5

0

0

10

20

30

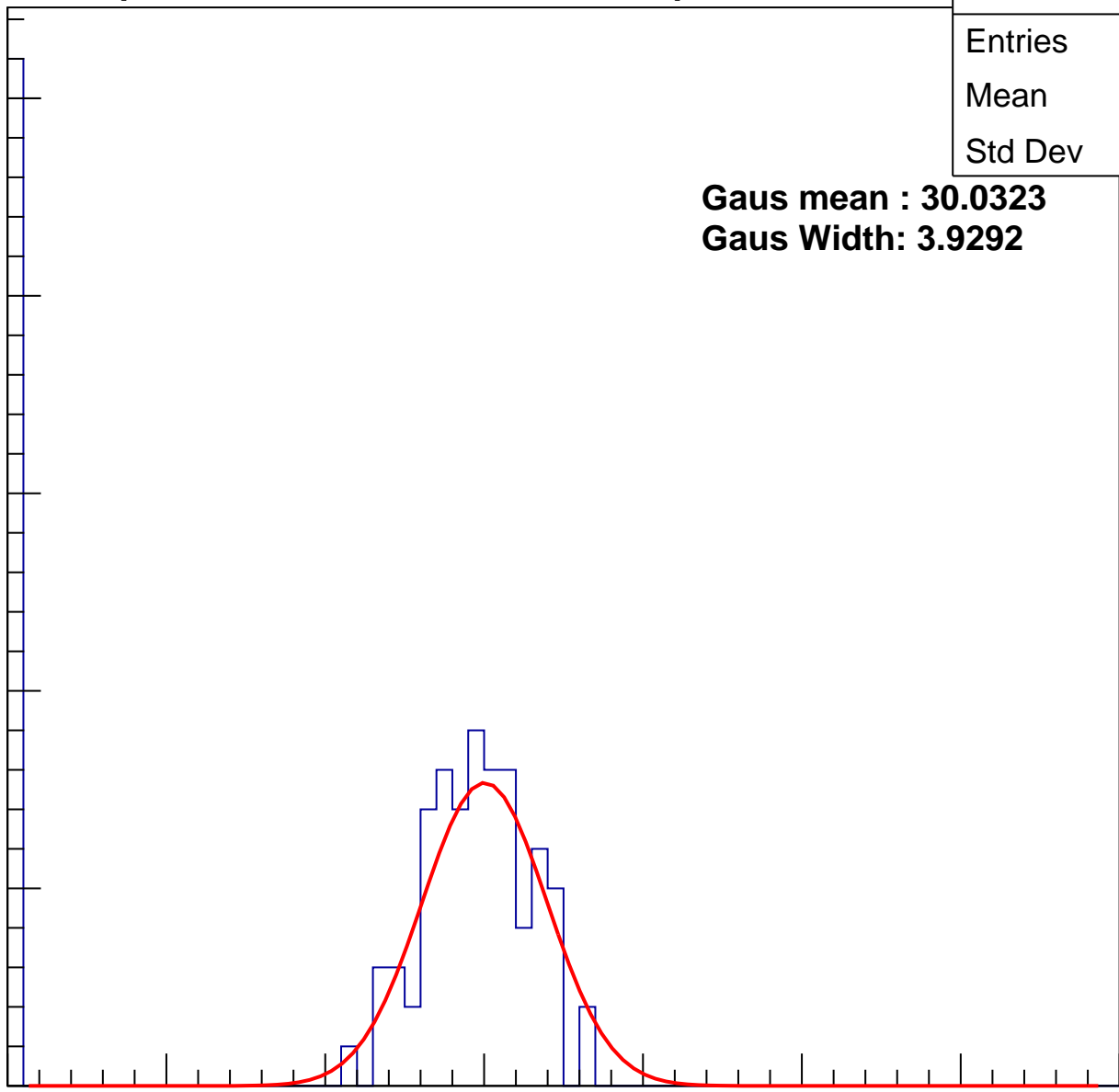
40

50

60

70

ampl



B1L103S, U2-ch49, adc1

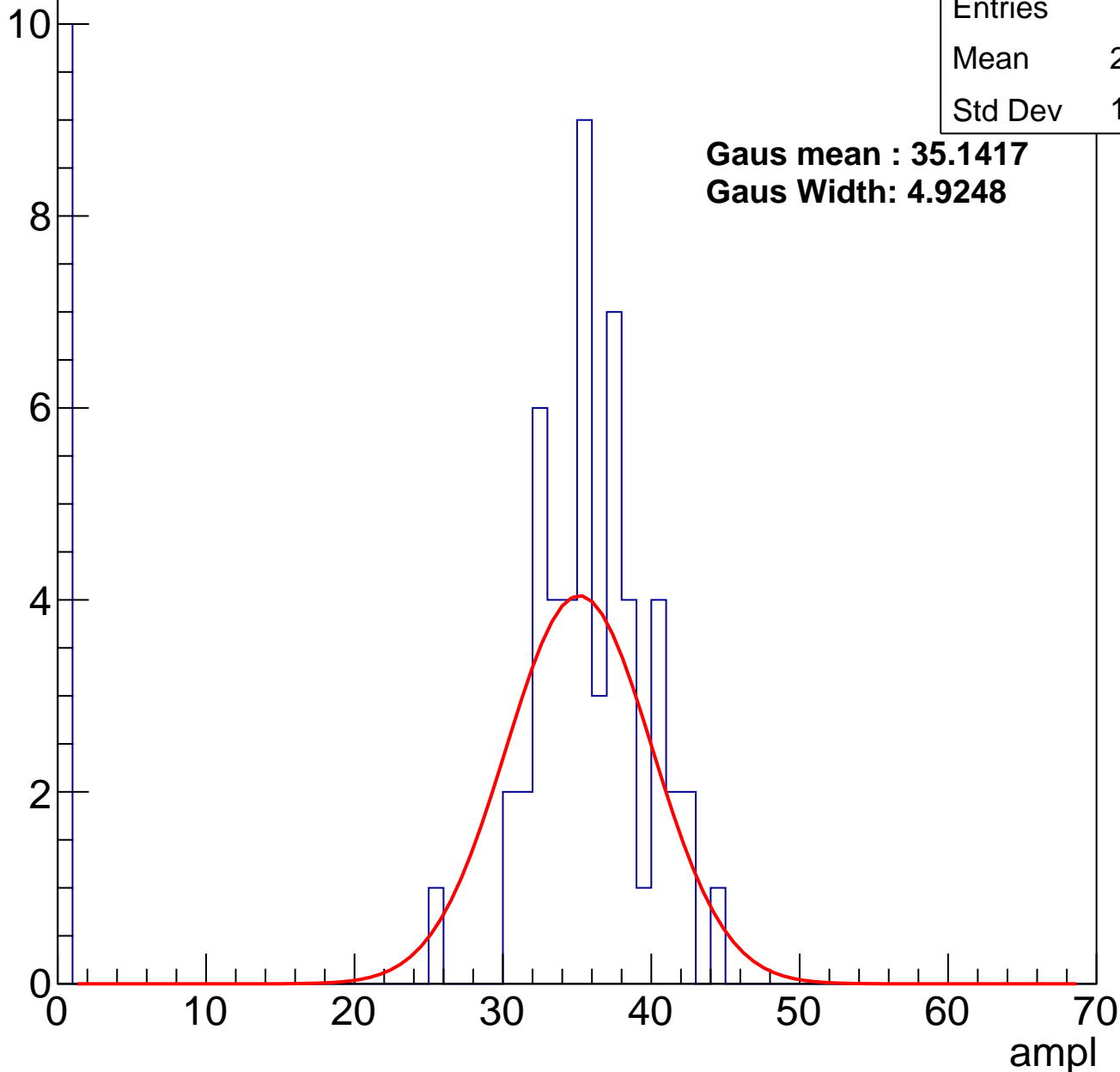
calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	29.84
Std Dev	13.49

Gaus mean : 35.1417

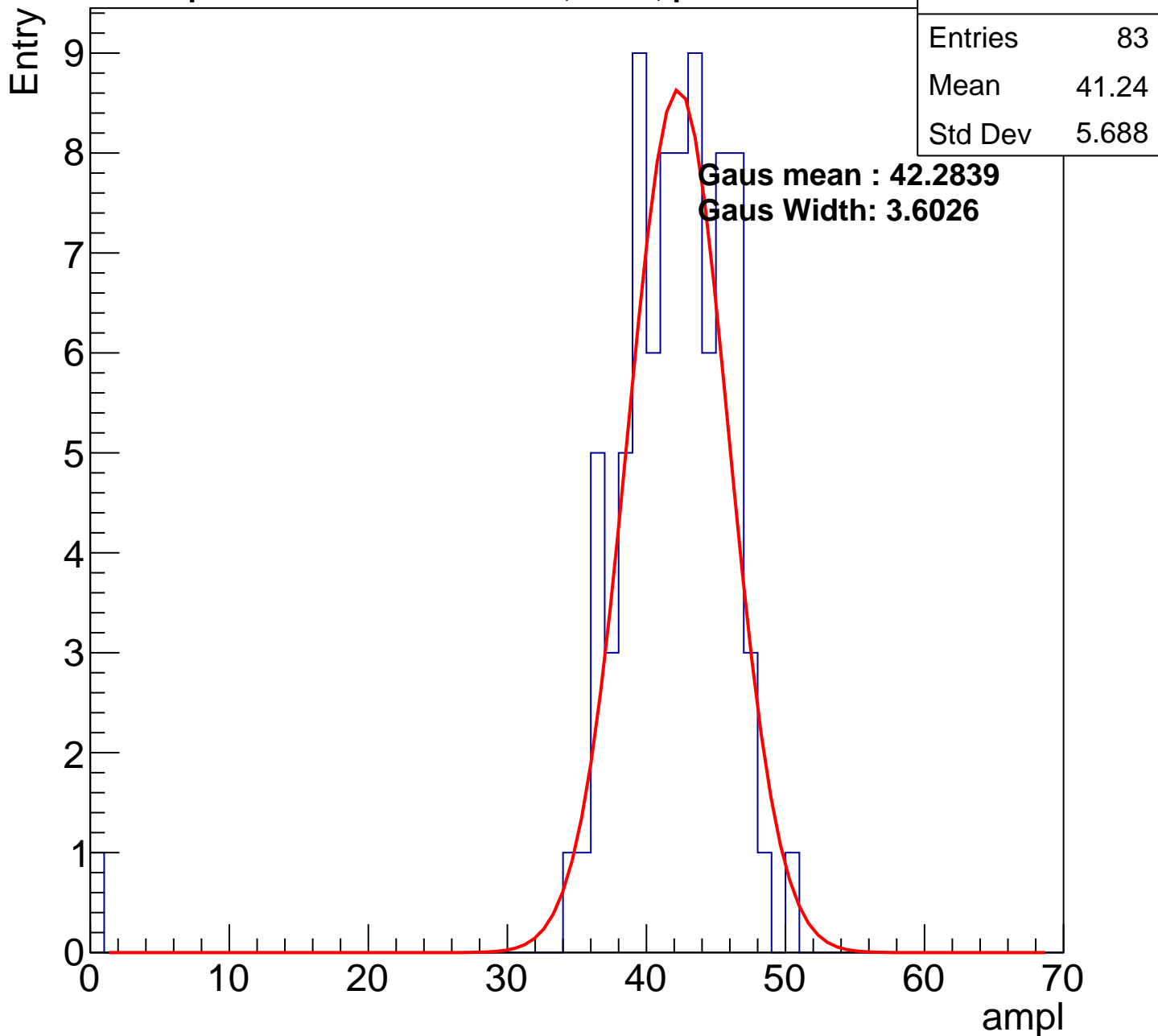
Gaus Width: 4.9248

Entry



B1L103S, U2-ch49, adc2

calib_packv5_041523_1651.root, FC#0, port C2

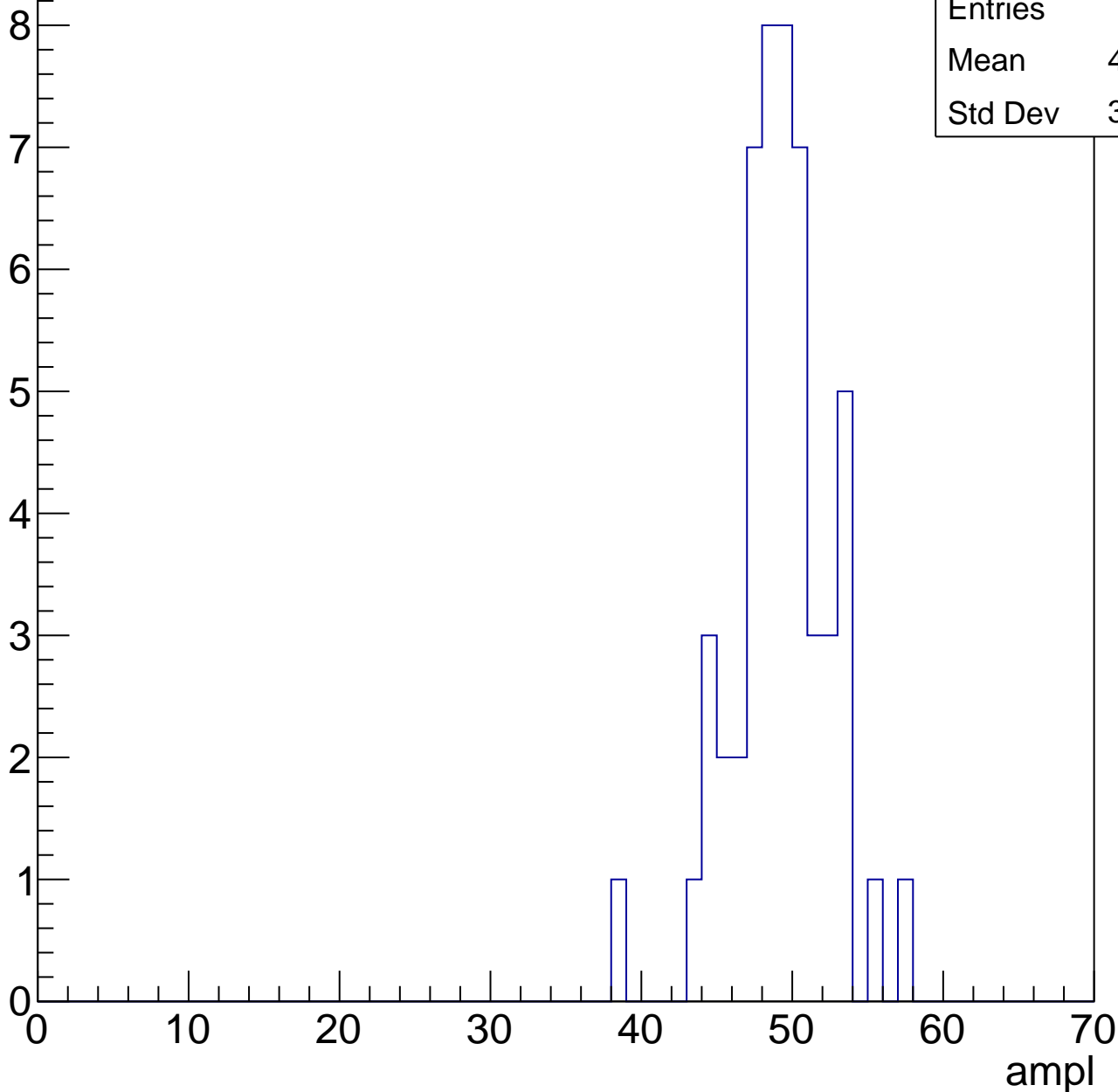


B1L103S, U2-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	48.77
Std Dev	3.226

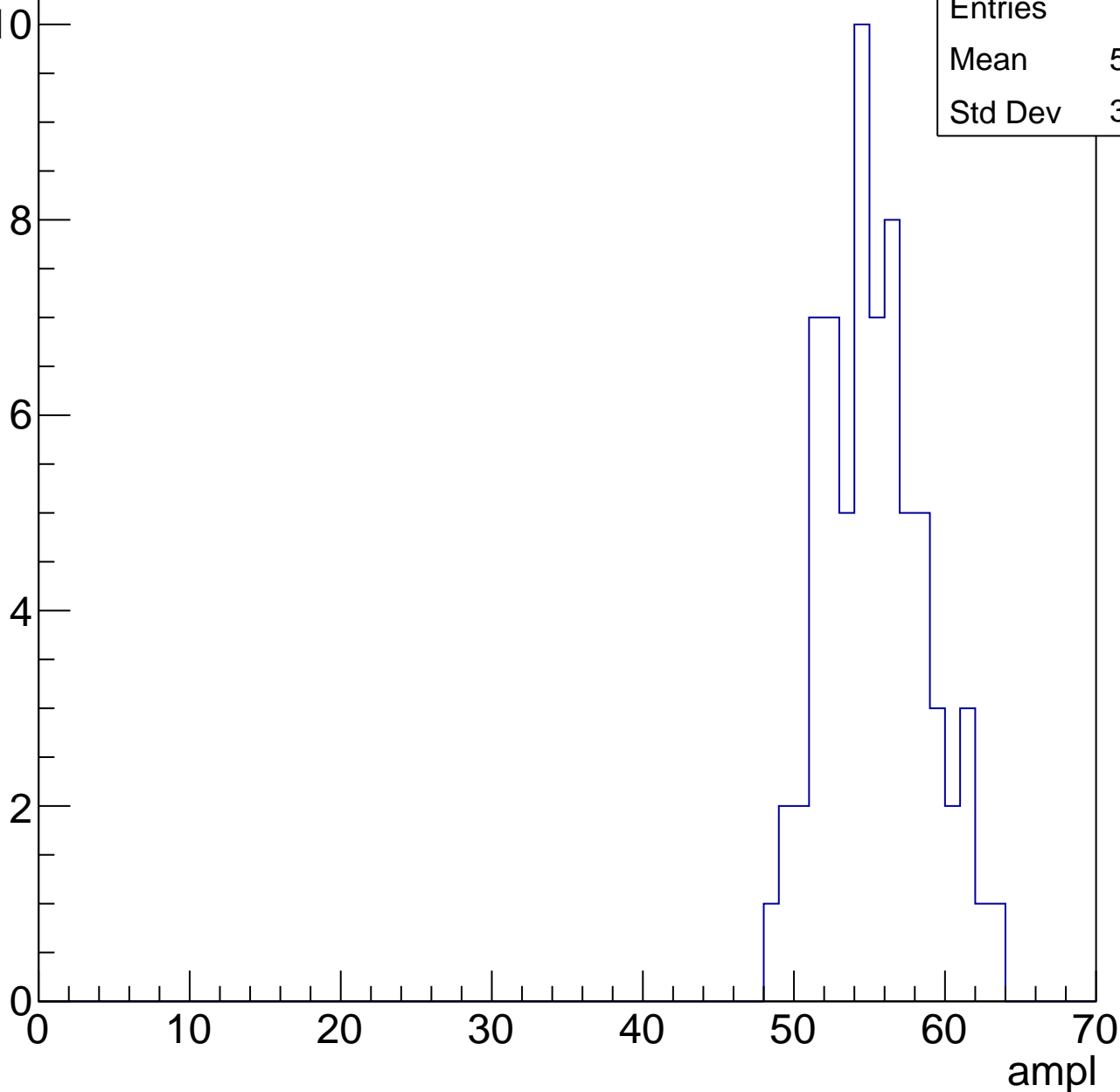


B1L103S, U2-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	54.86
Std Dev	3.333



B1L103S, U2-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

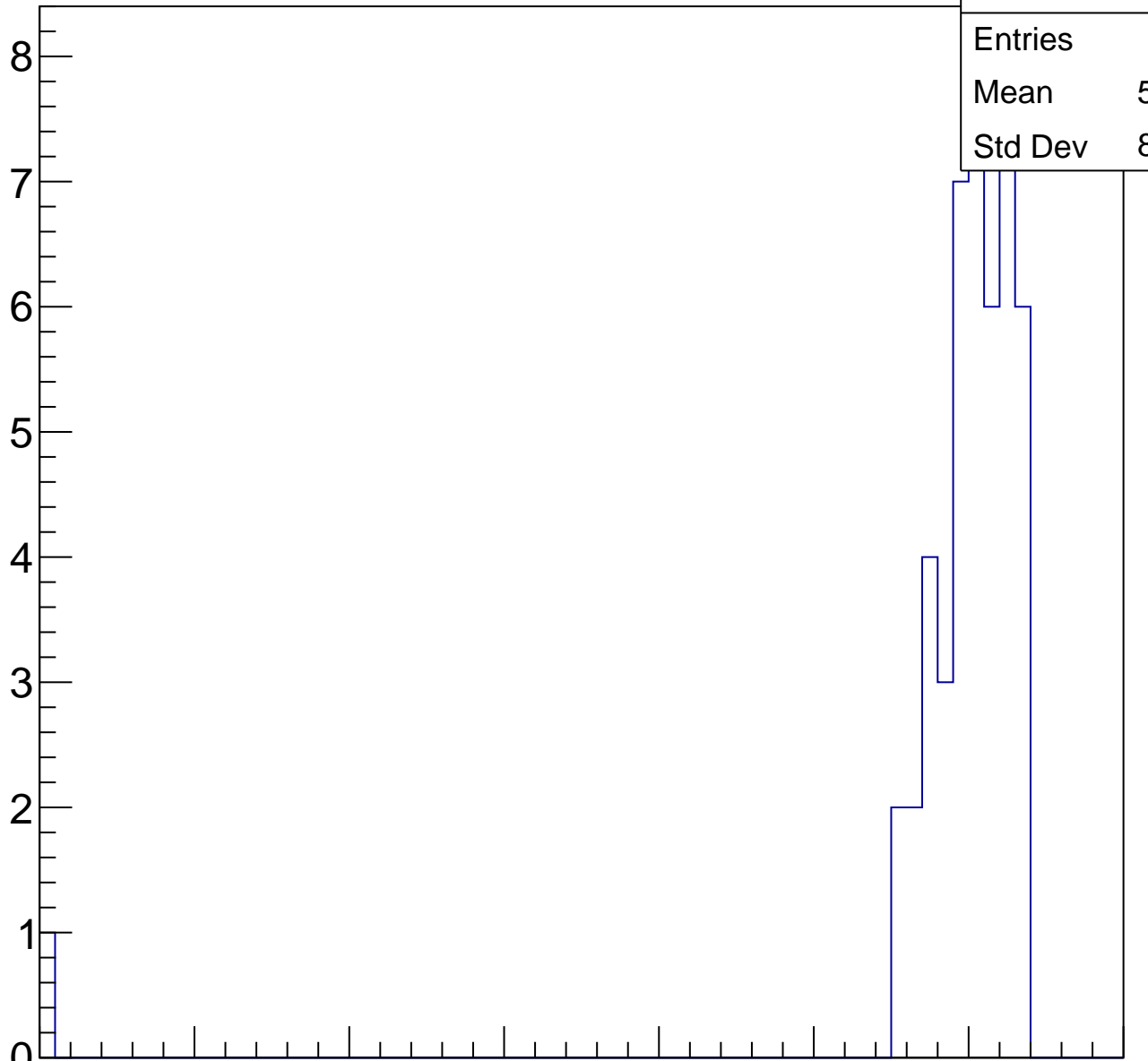
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.66
Std Dev	8.926

ampl

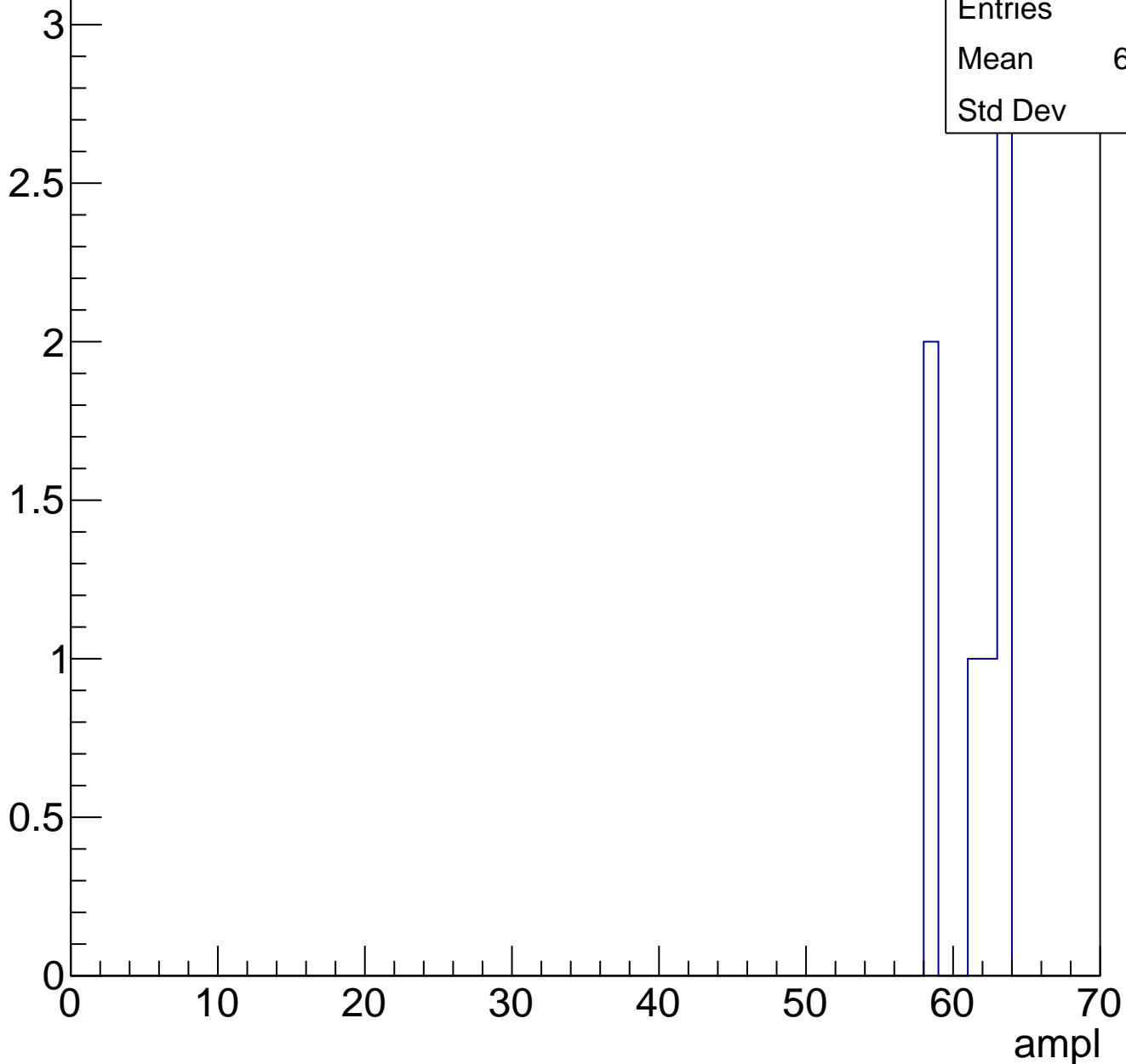
0 10 20 30 40 50 60 70



B1L103S, U2-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

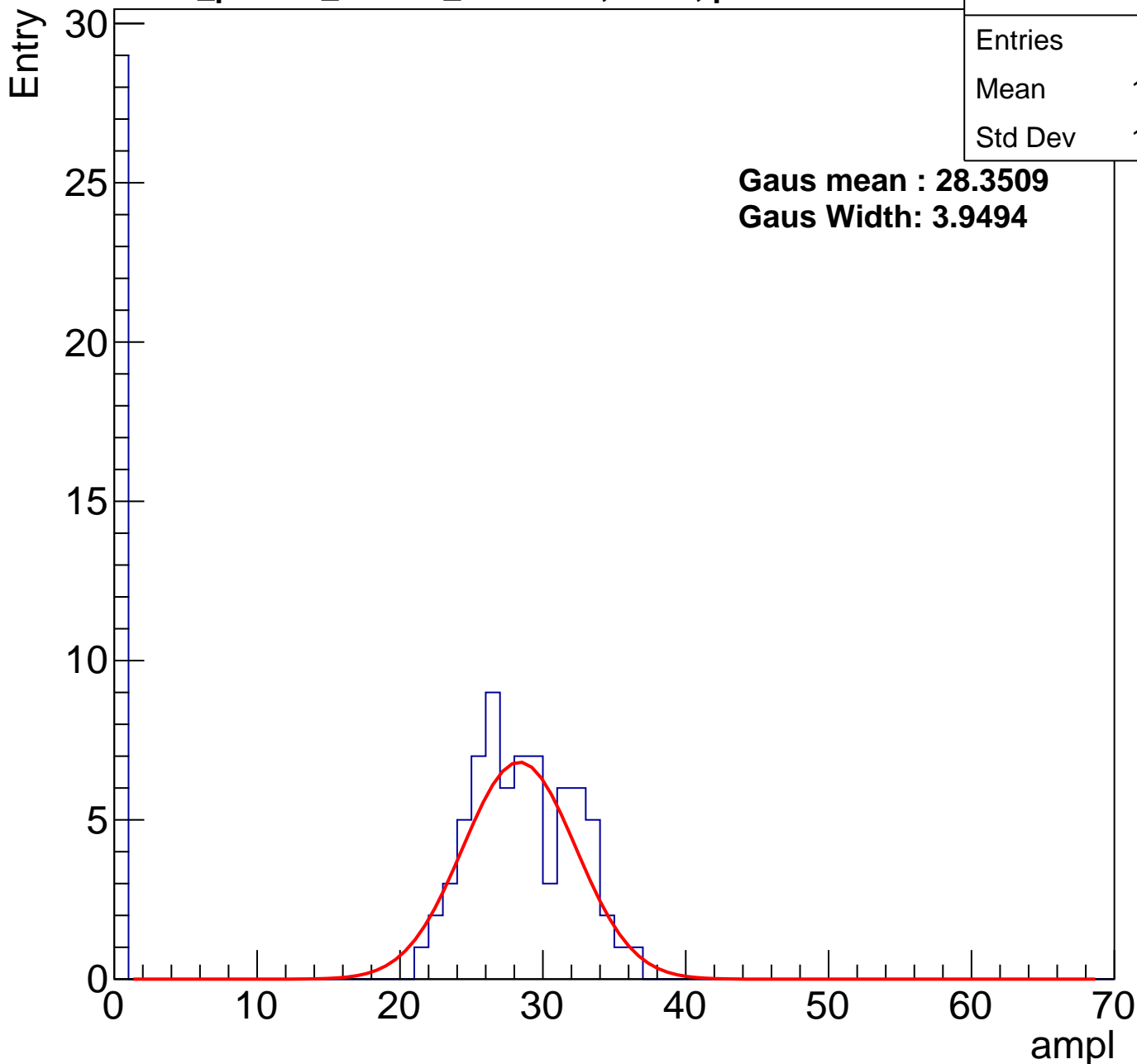
B1L103S, U2-ch50, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	19.96
Std Dev	13.09

Gaus mean : 28.3509

Gaus Width: 3.9494



B1L103S, U2-ch50, adc1

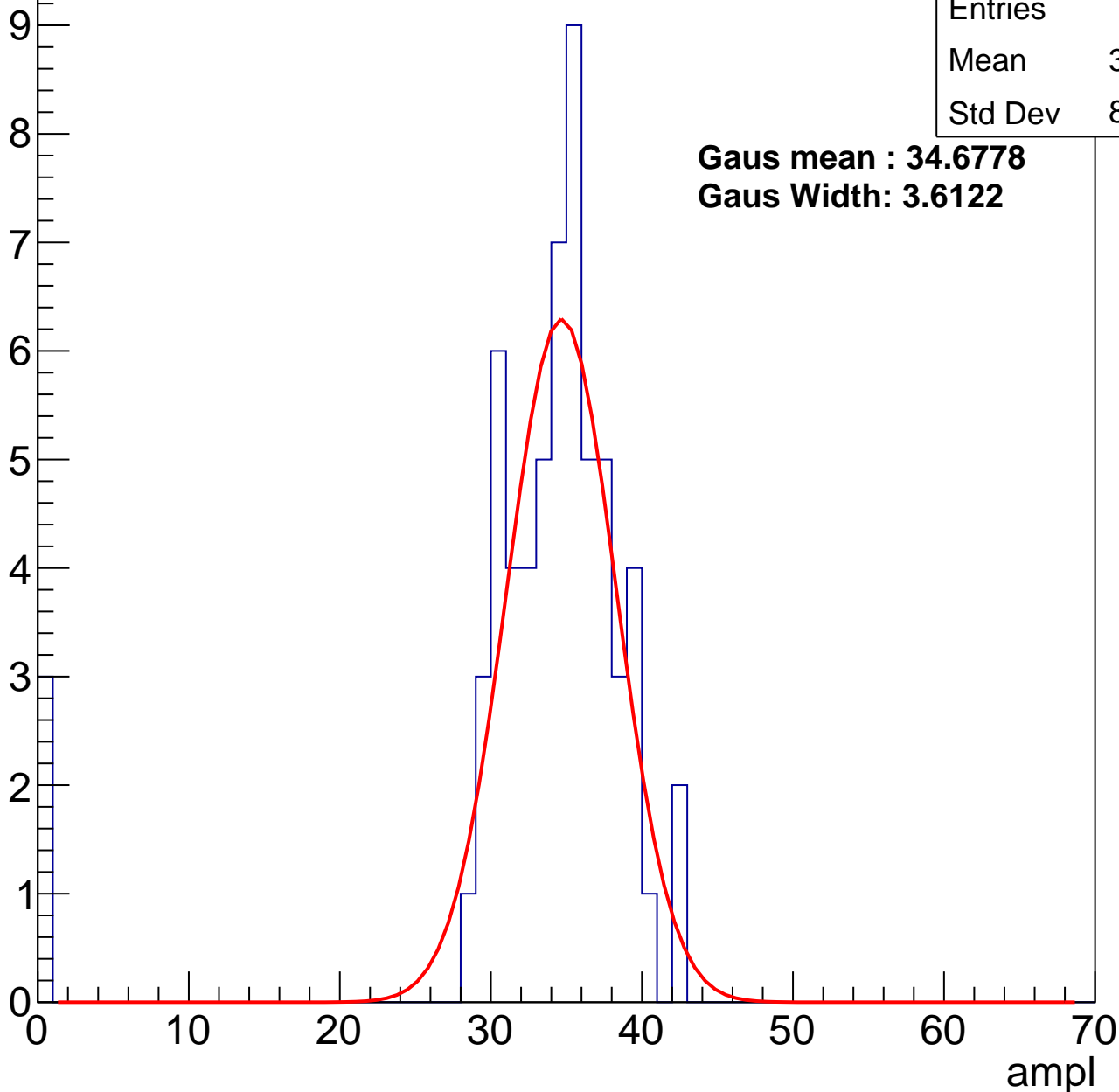
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.65
Std Dev	8.036

Gaus mean : 34.6778

Gaus Width: 3.6122



B1L103S, U2-ch50, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	32.32
Std Dev	16.67

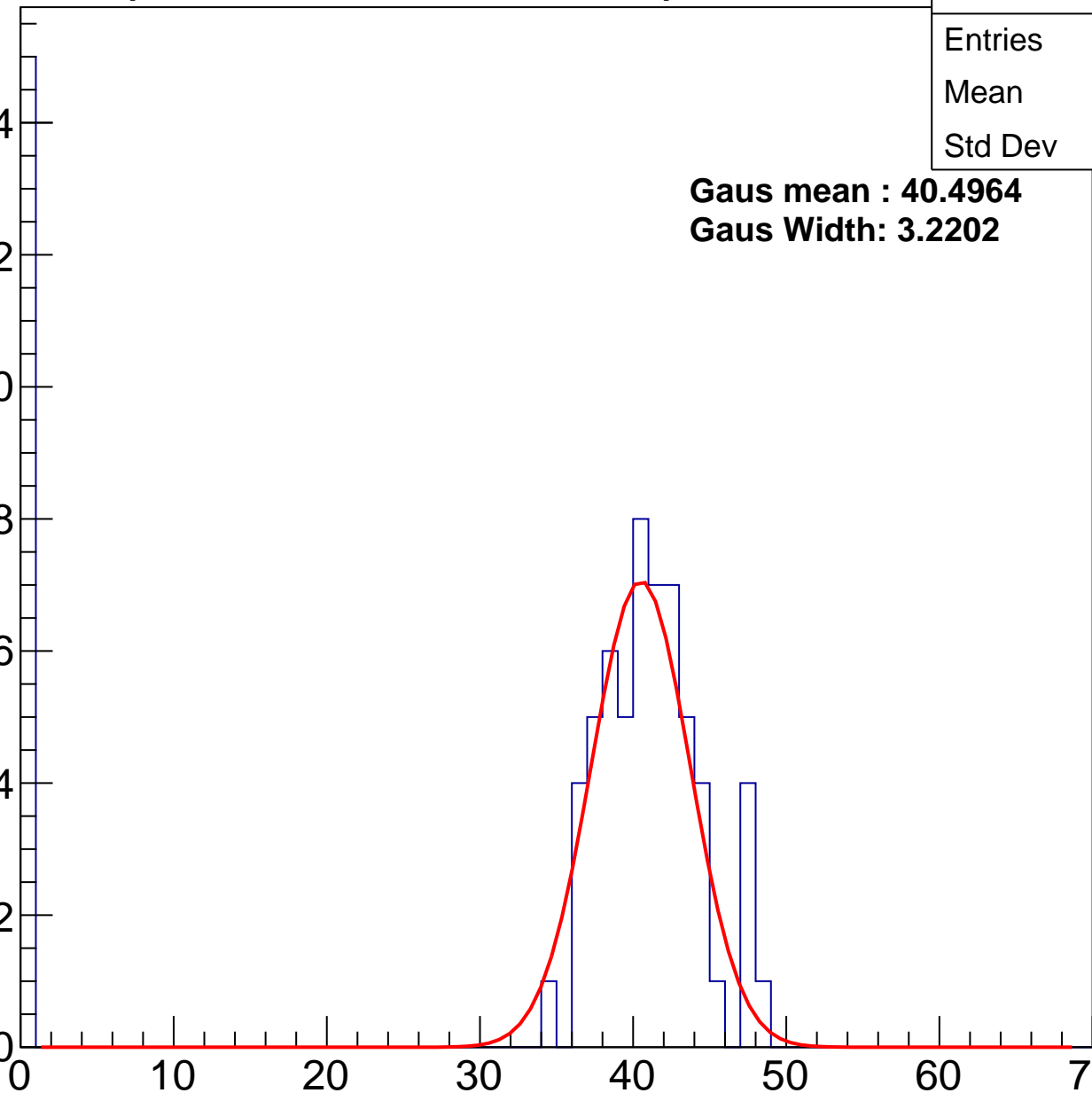
Gaus mean : 40.4964

Gaus Width: 3.2202

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

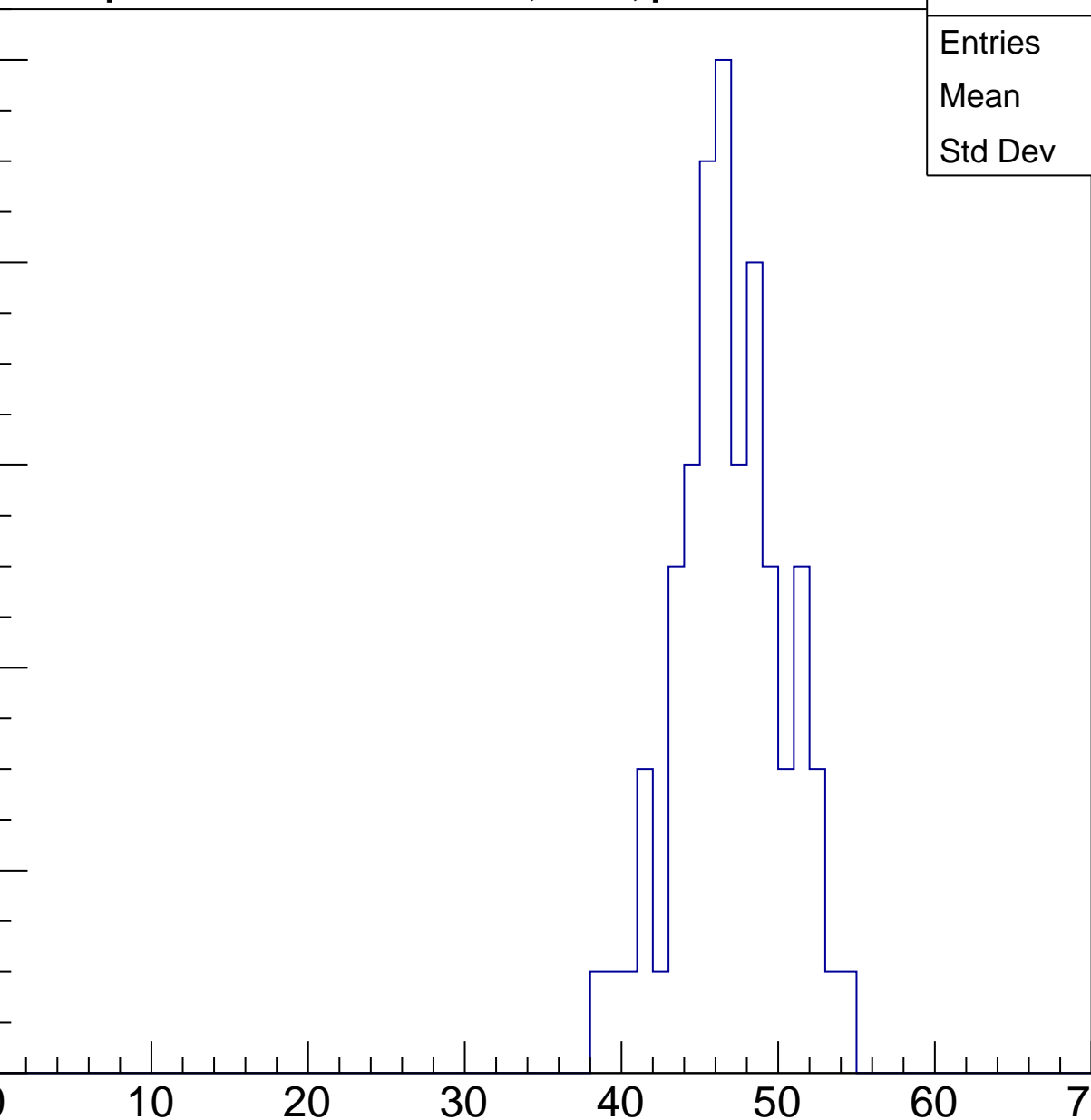
Entries	69
Mean	46.45
Std Dev	3.365

Entry

10
8
6
4
2
0

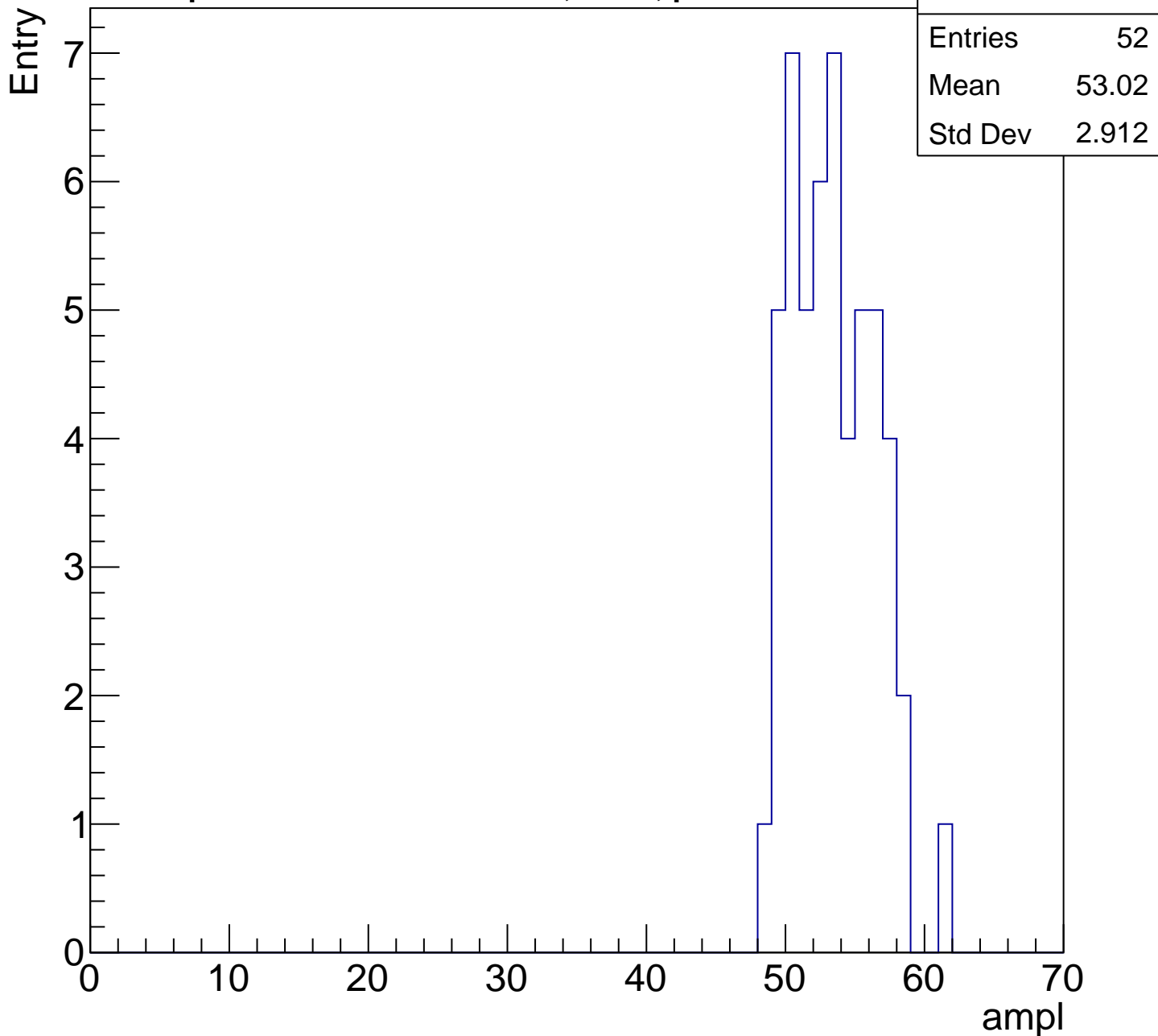
0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

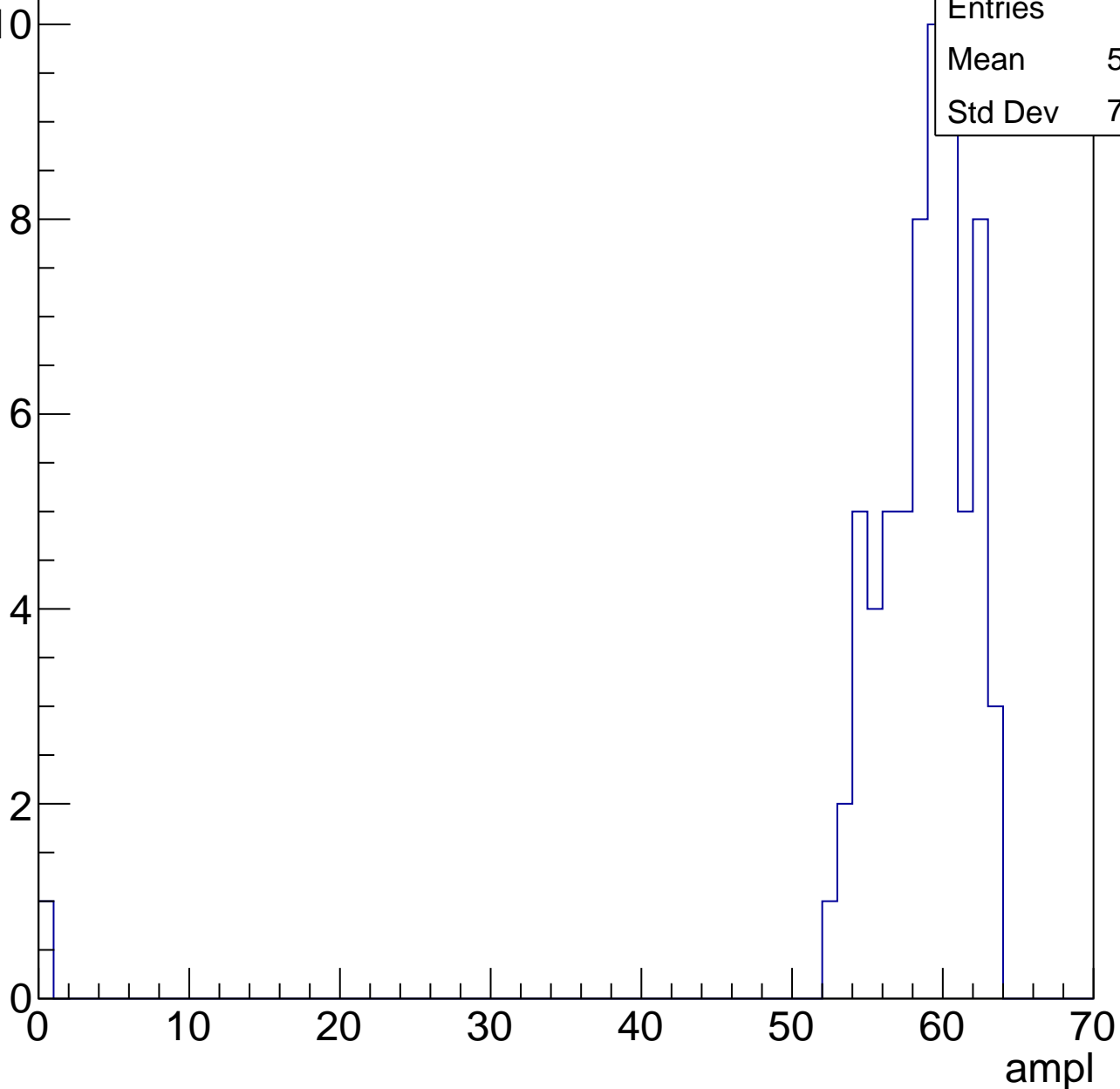


B1L103S, U2-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	57.53
Std Dev	7.658

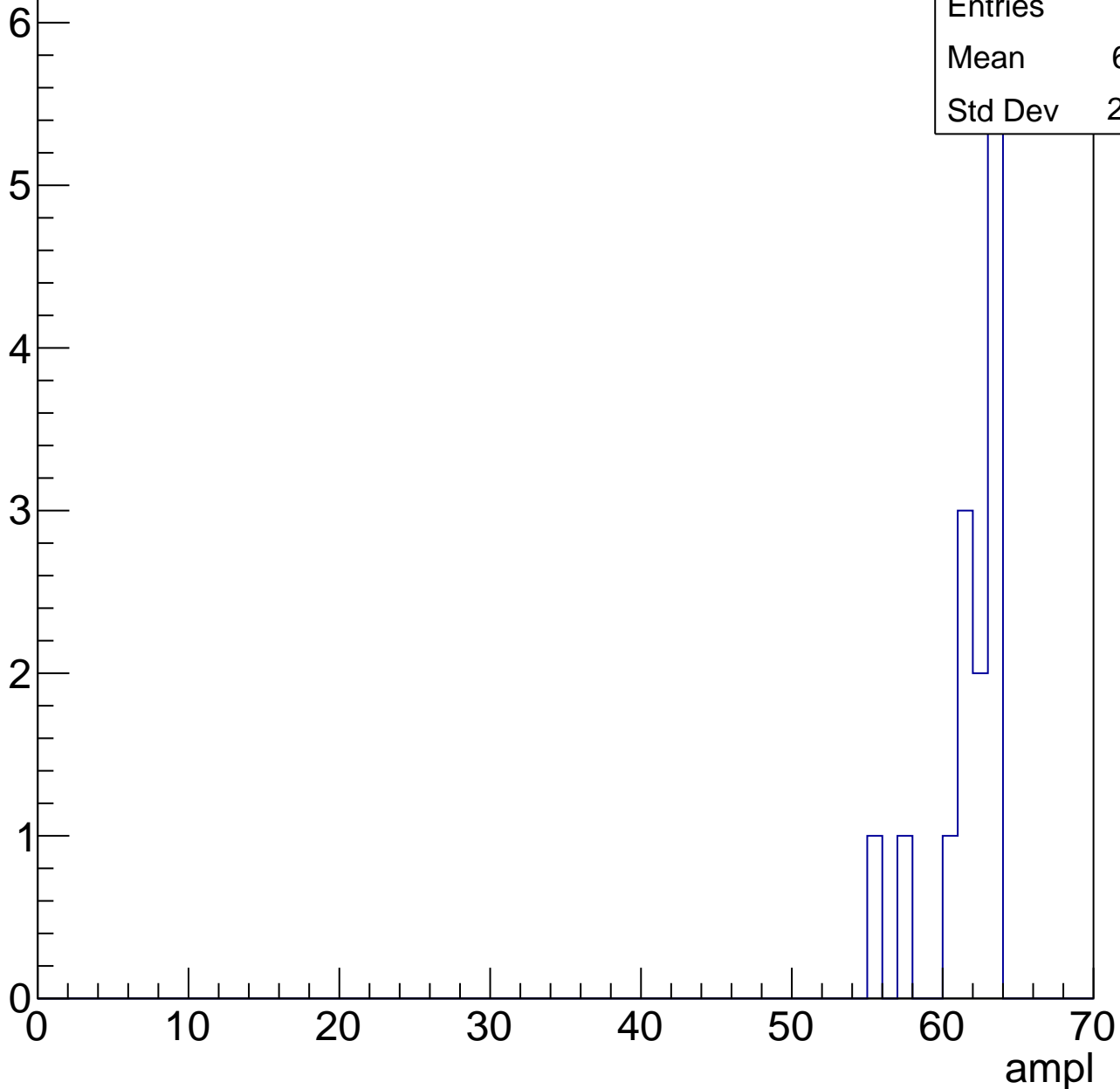


B1L103S, U2-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

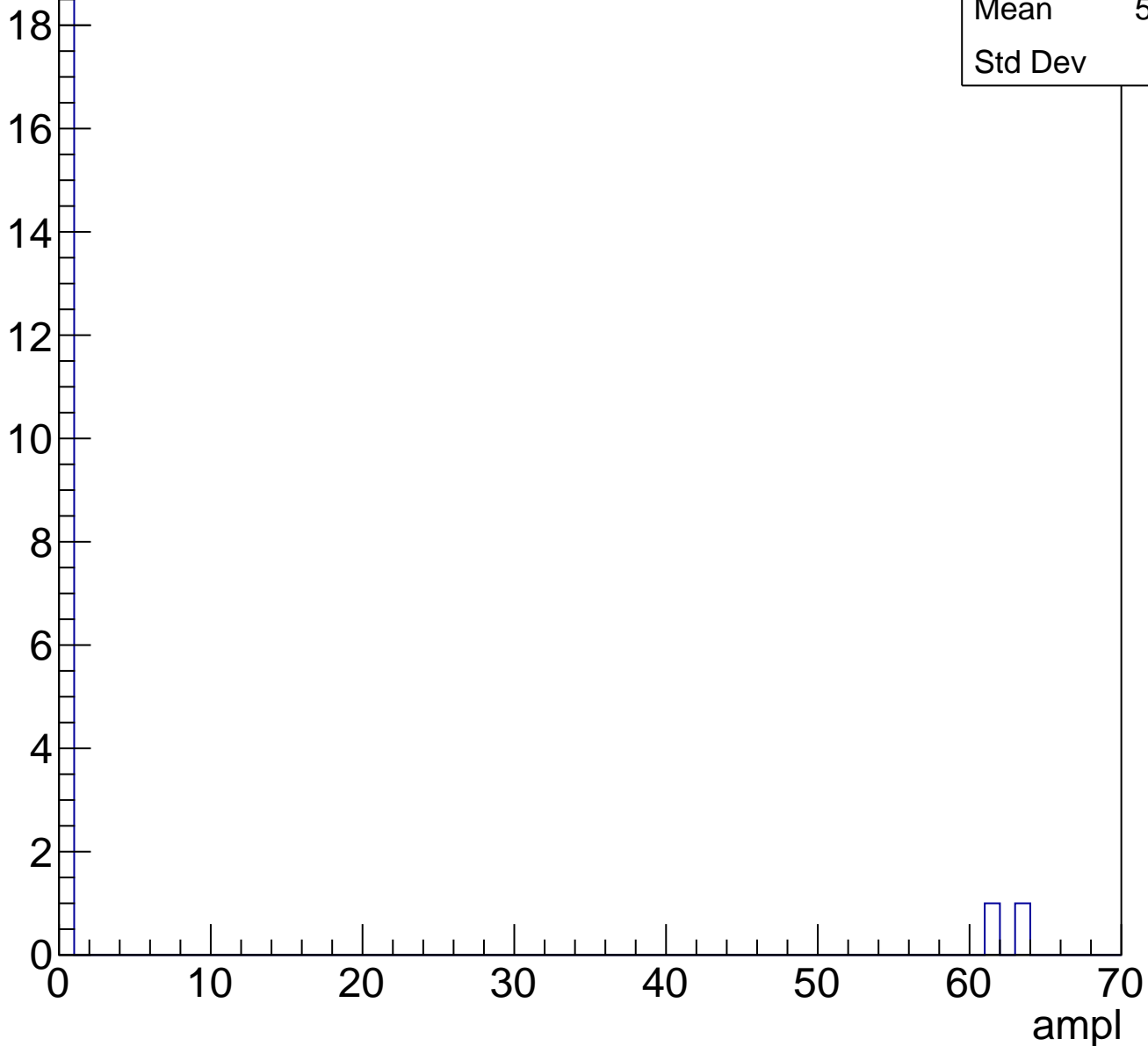
Entries	14
Mean	61.21
Std Dev	2.366



B1L103S, U2-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	5.905
Std Dev	18.2

B1L103S, U2-ch51, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	23.87
Std Dev	11.33

Gaus mean : 29.4092

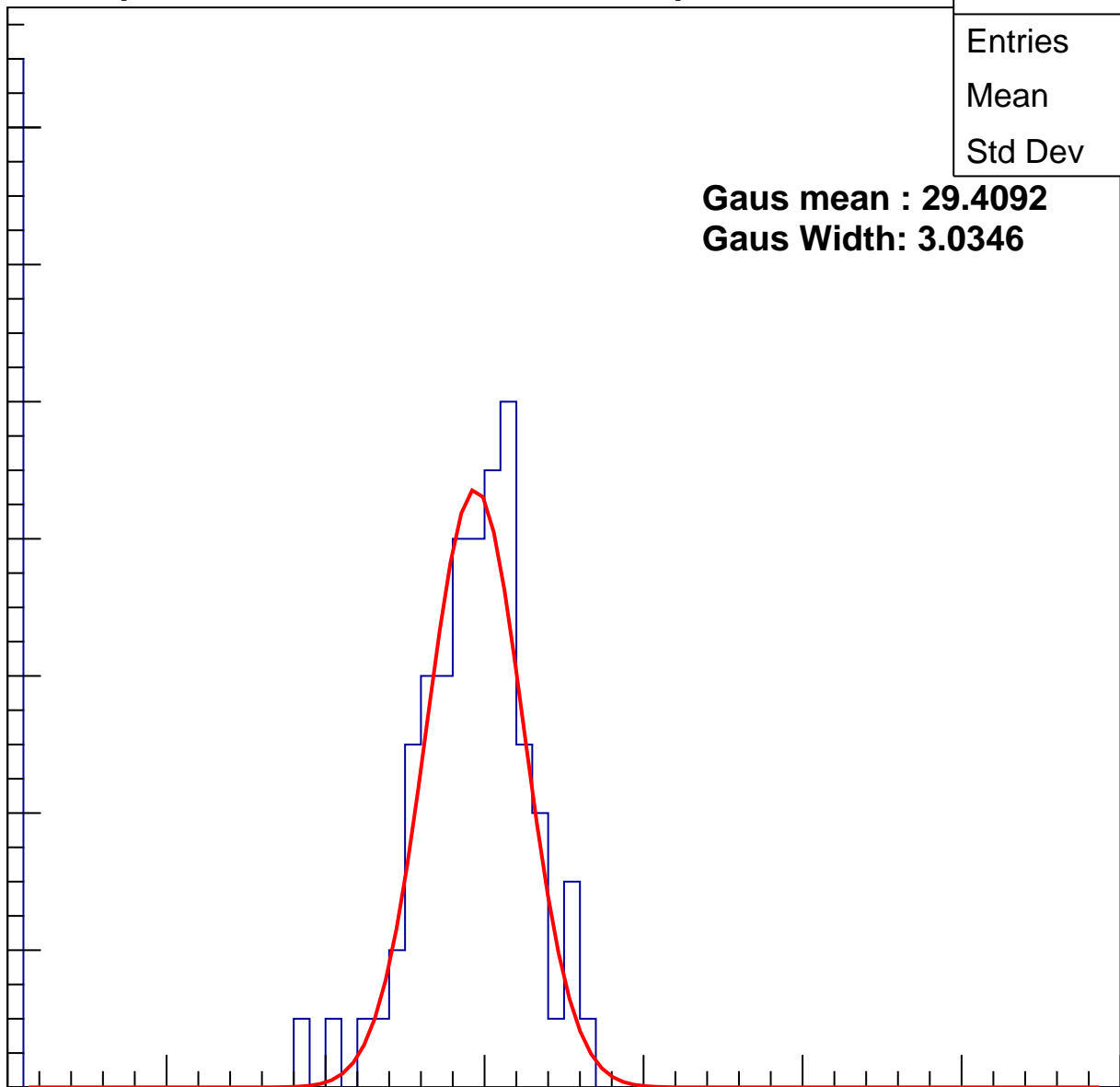
Gaus Width: 3.0346

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch51, adc1

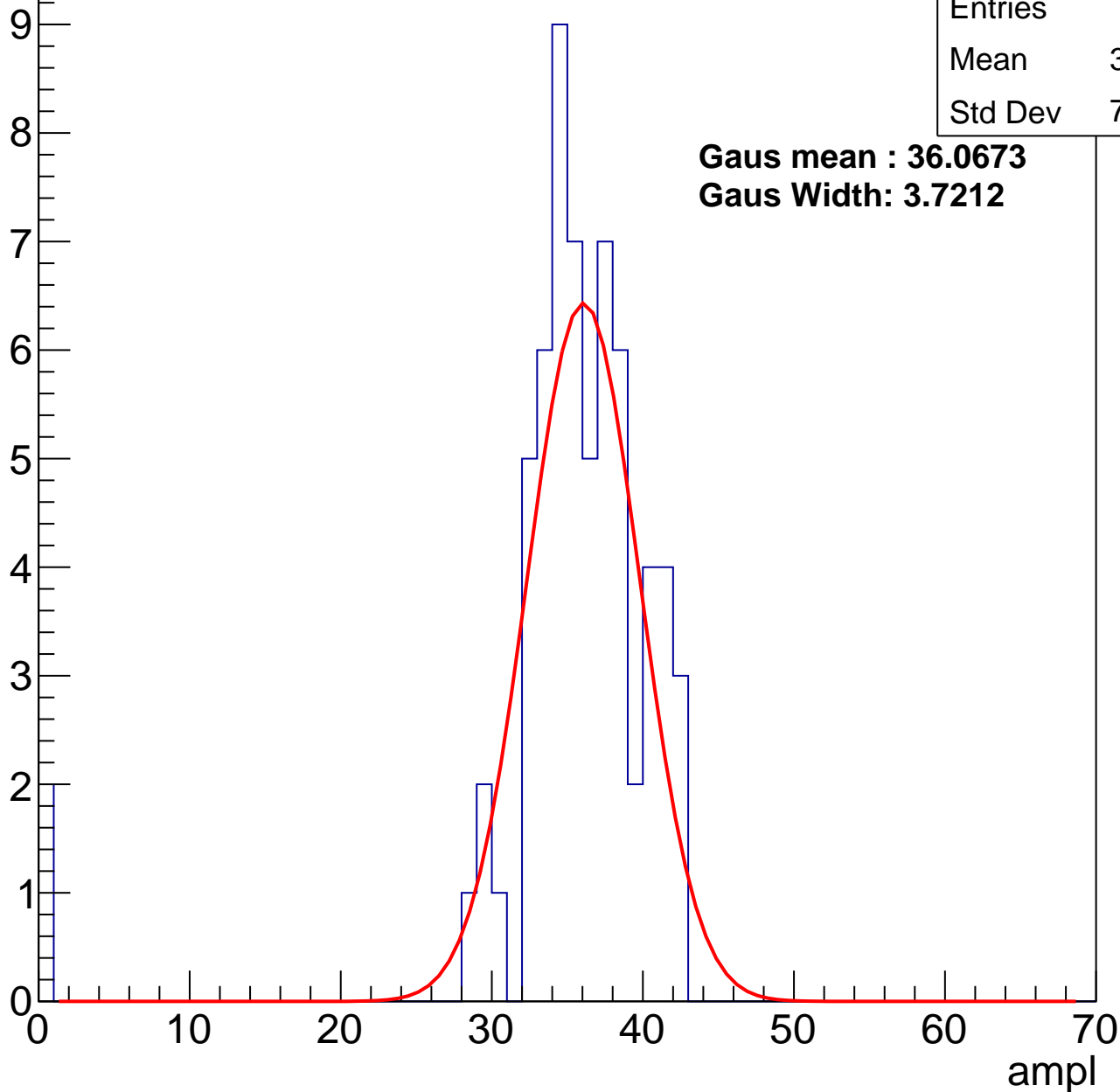
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.69
Std Dev	7.046

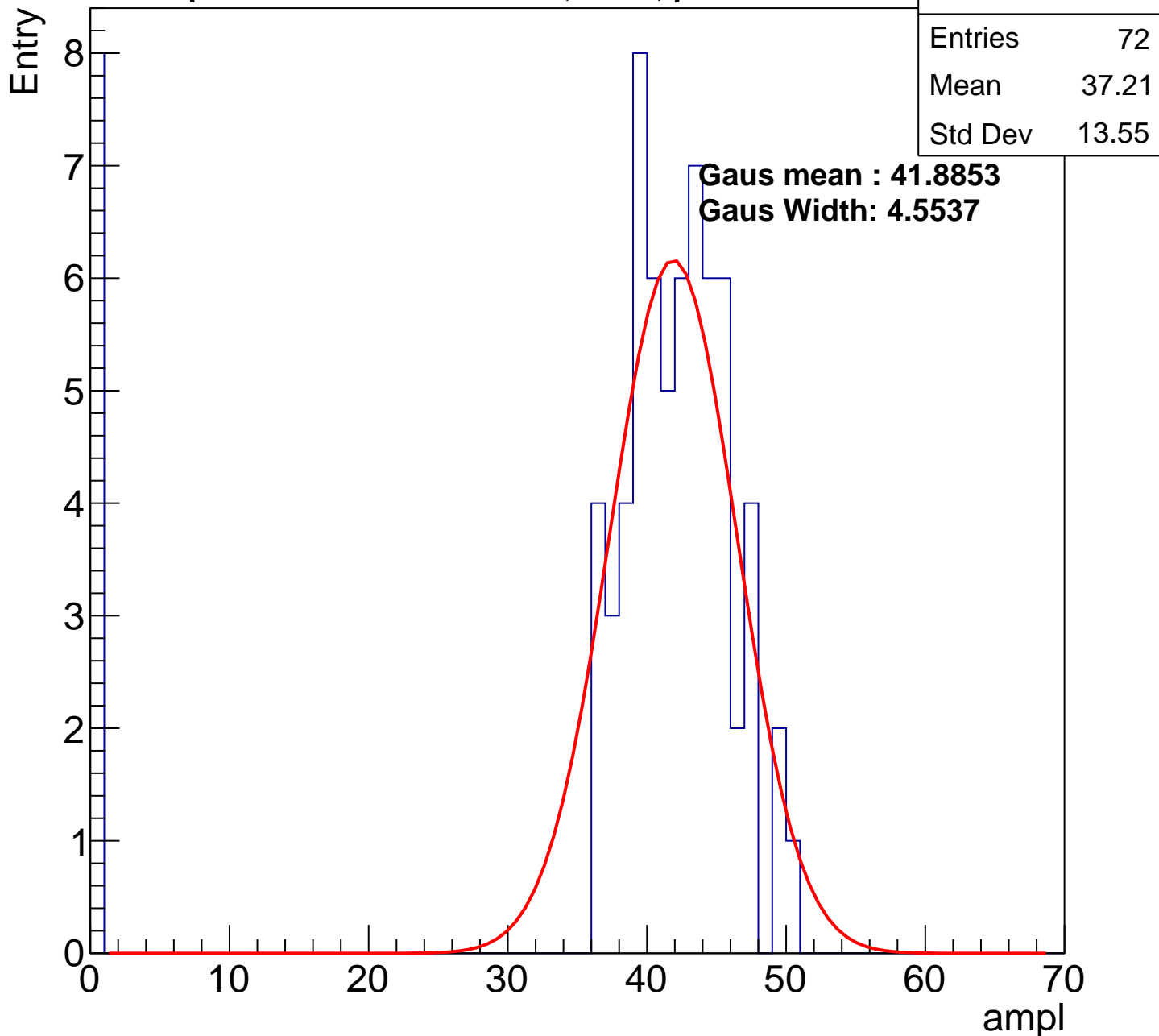
Gaus mean : 36.0673

Gaus Width: 3.7212



B1L103S, U2-ch51, adc2

calib_packv5_041523_1651.root, FC#0, port C2

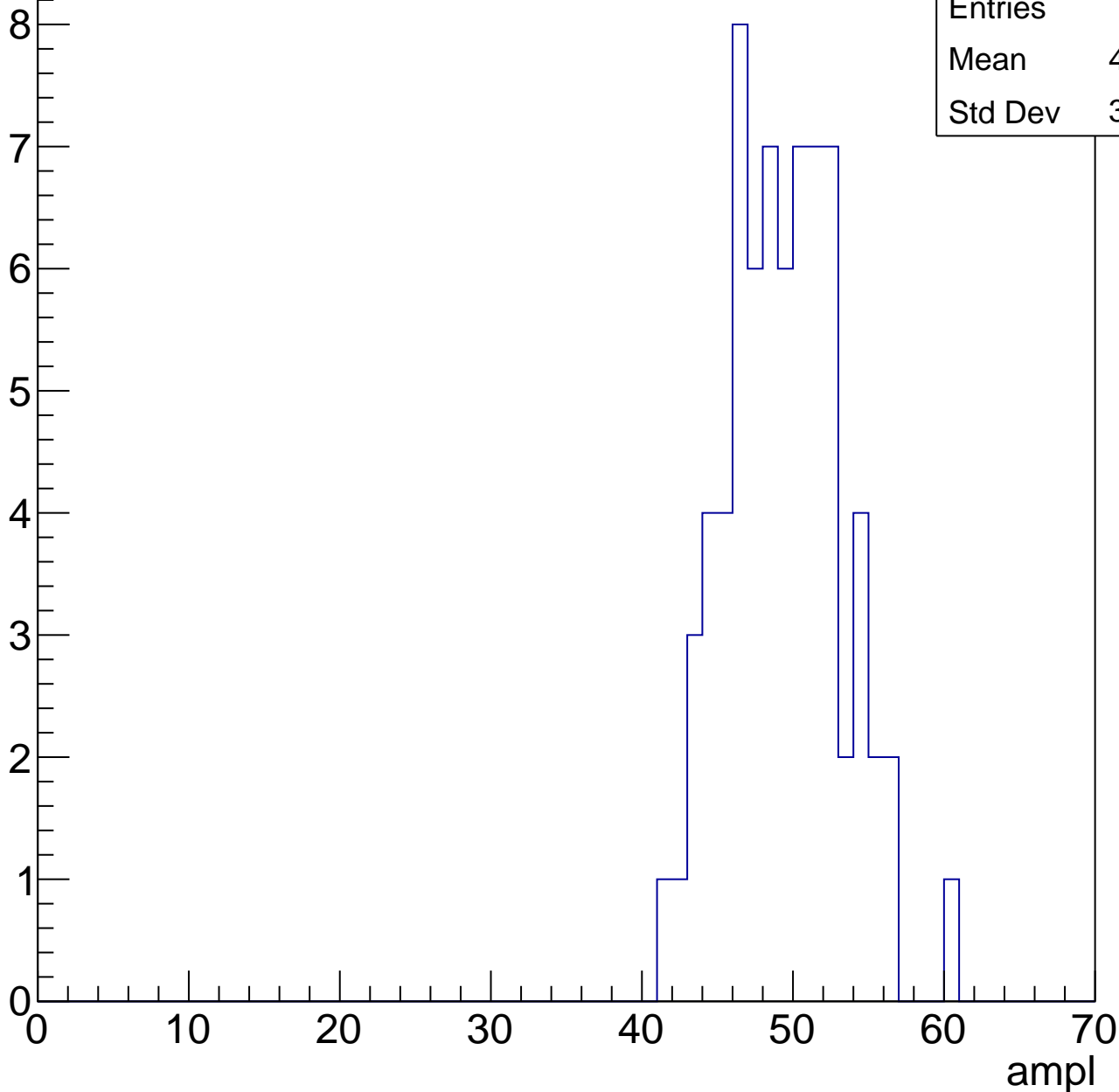


B1L103S, U2-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	48.93
Std Dev	3.743

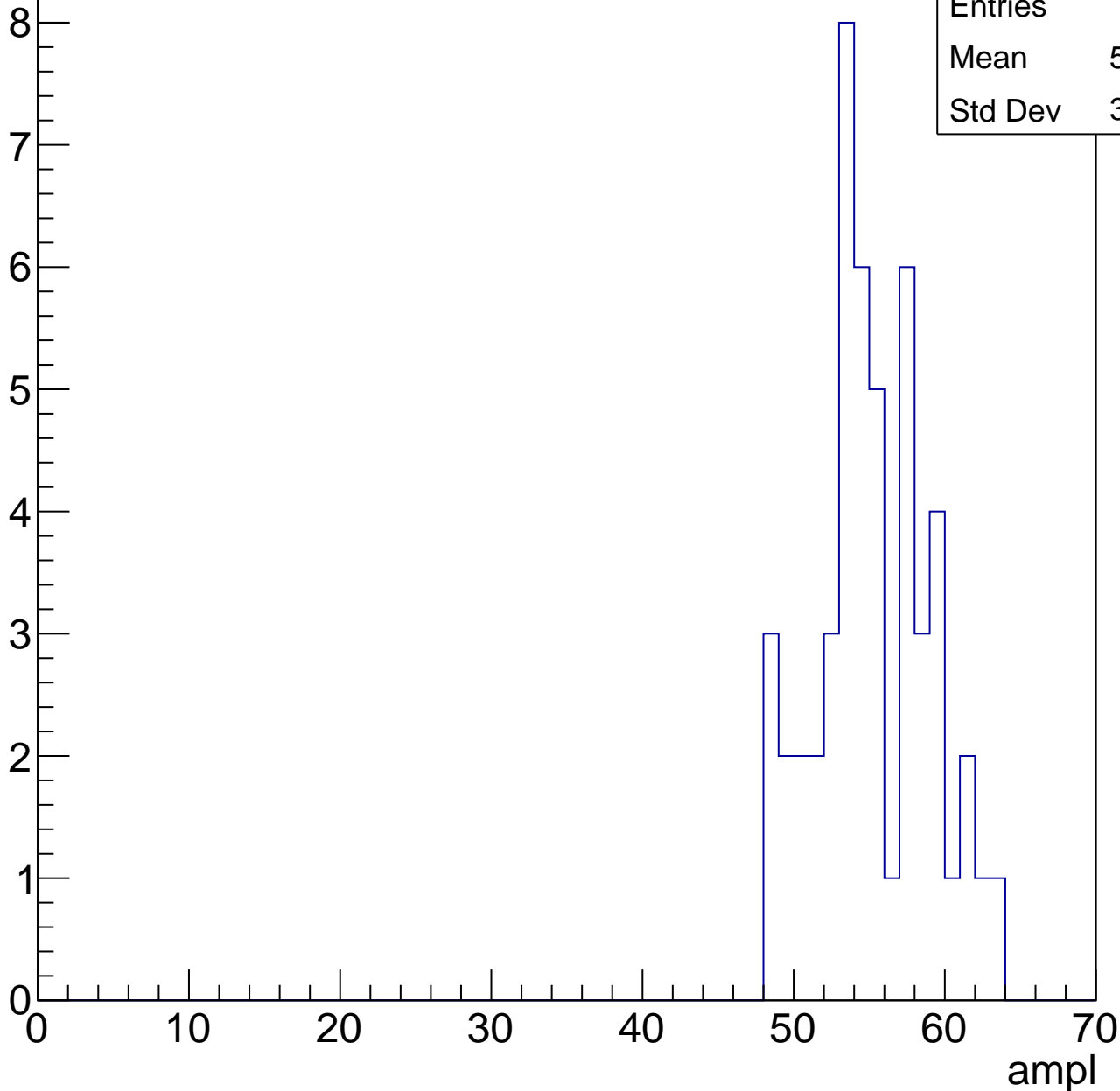


B1L103S, U2-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	54.76
Std Dev	3.712

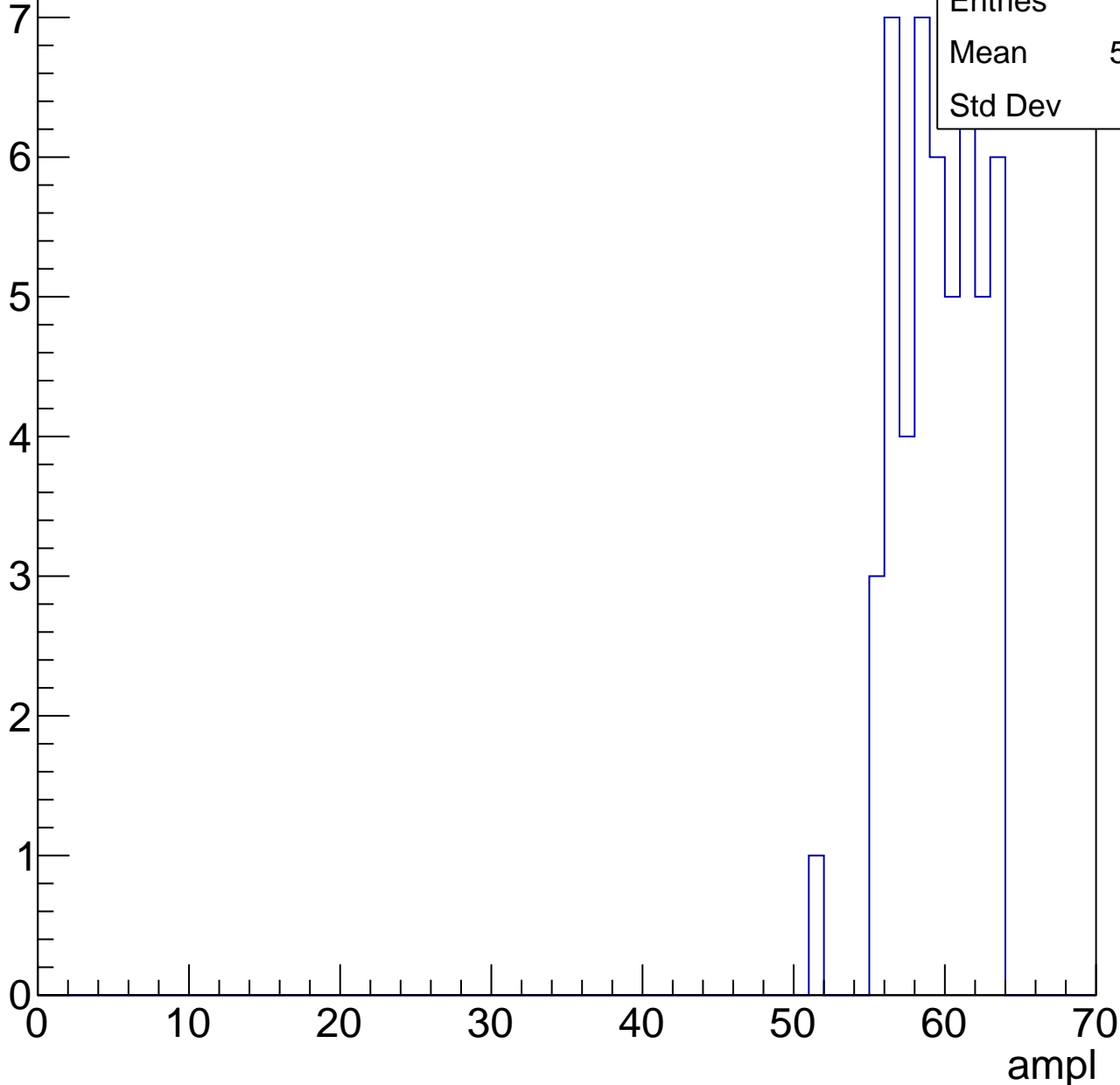


B1L103S, U2-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

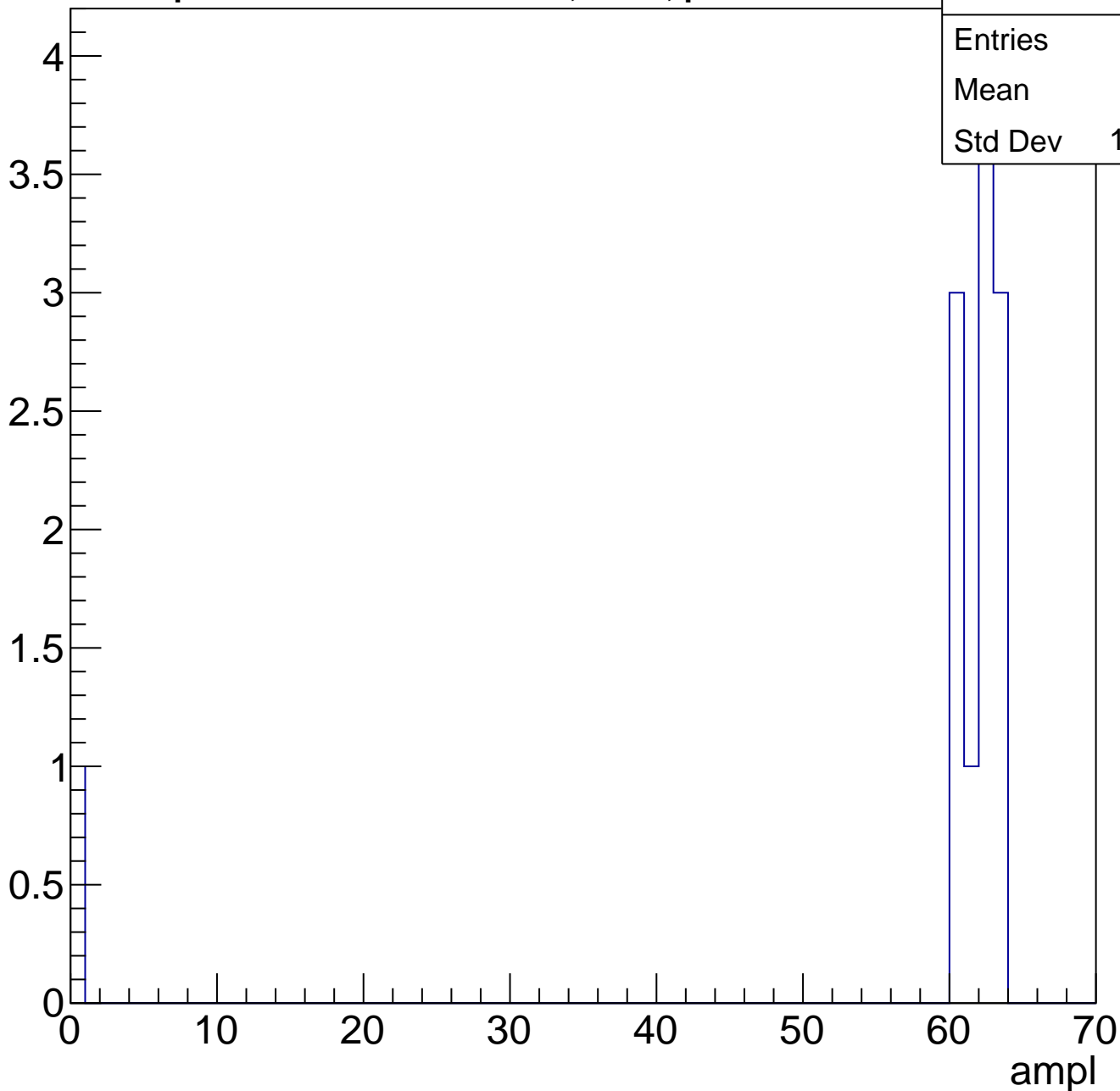
Entries	51
Mean	59.04
Std Dev	2.7



B1L103S, U2-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch52, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	21.55
Std Dev	13.13

Gaus mean : 30.0861

Gaus Width: 3.8923

Entry

25

20

15

10

5

0

0

10

20

30

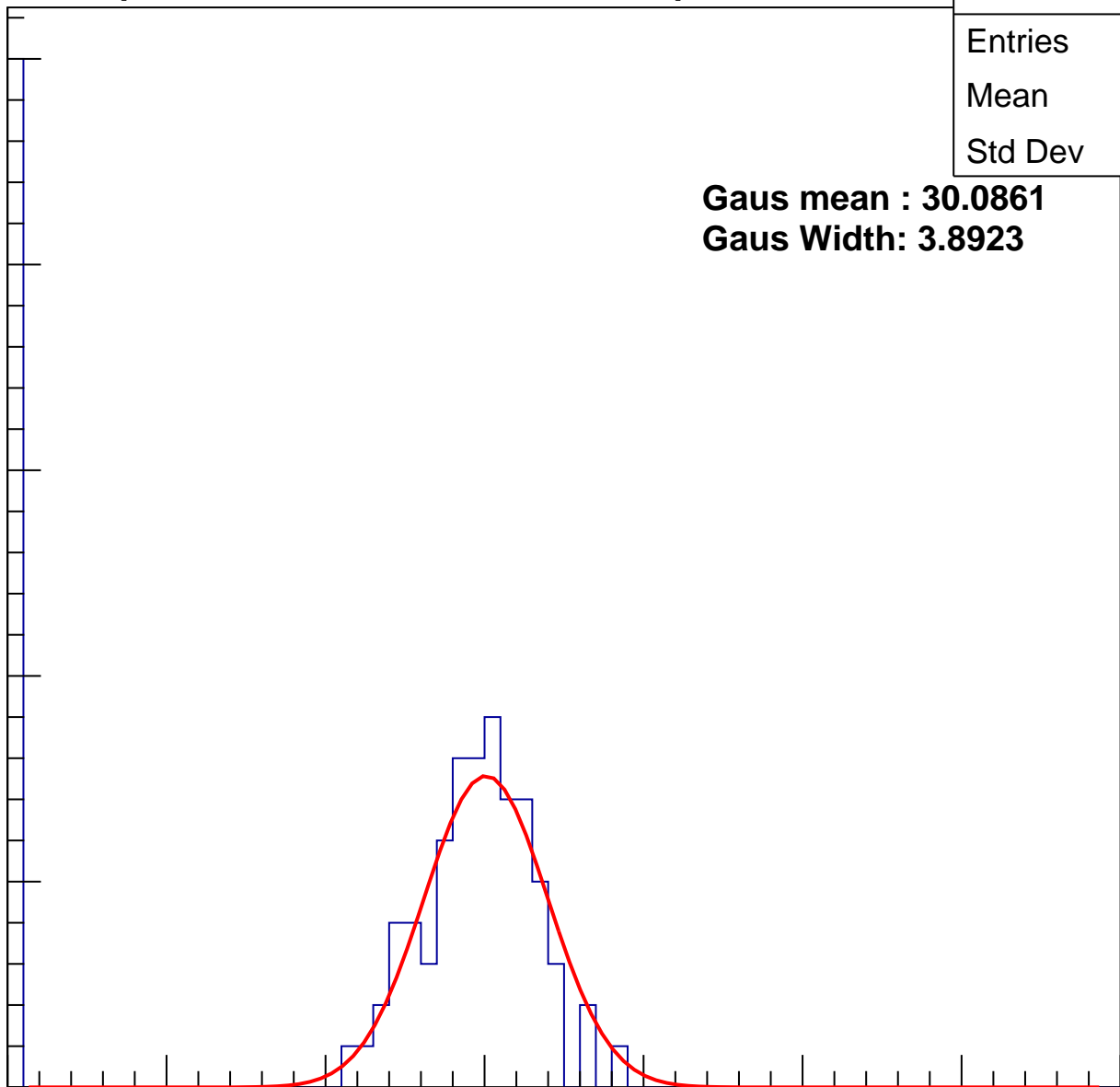
40

50

60

70

ampl



B1L103S, U2-ch52, adc1

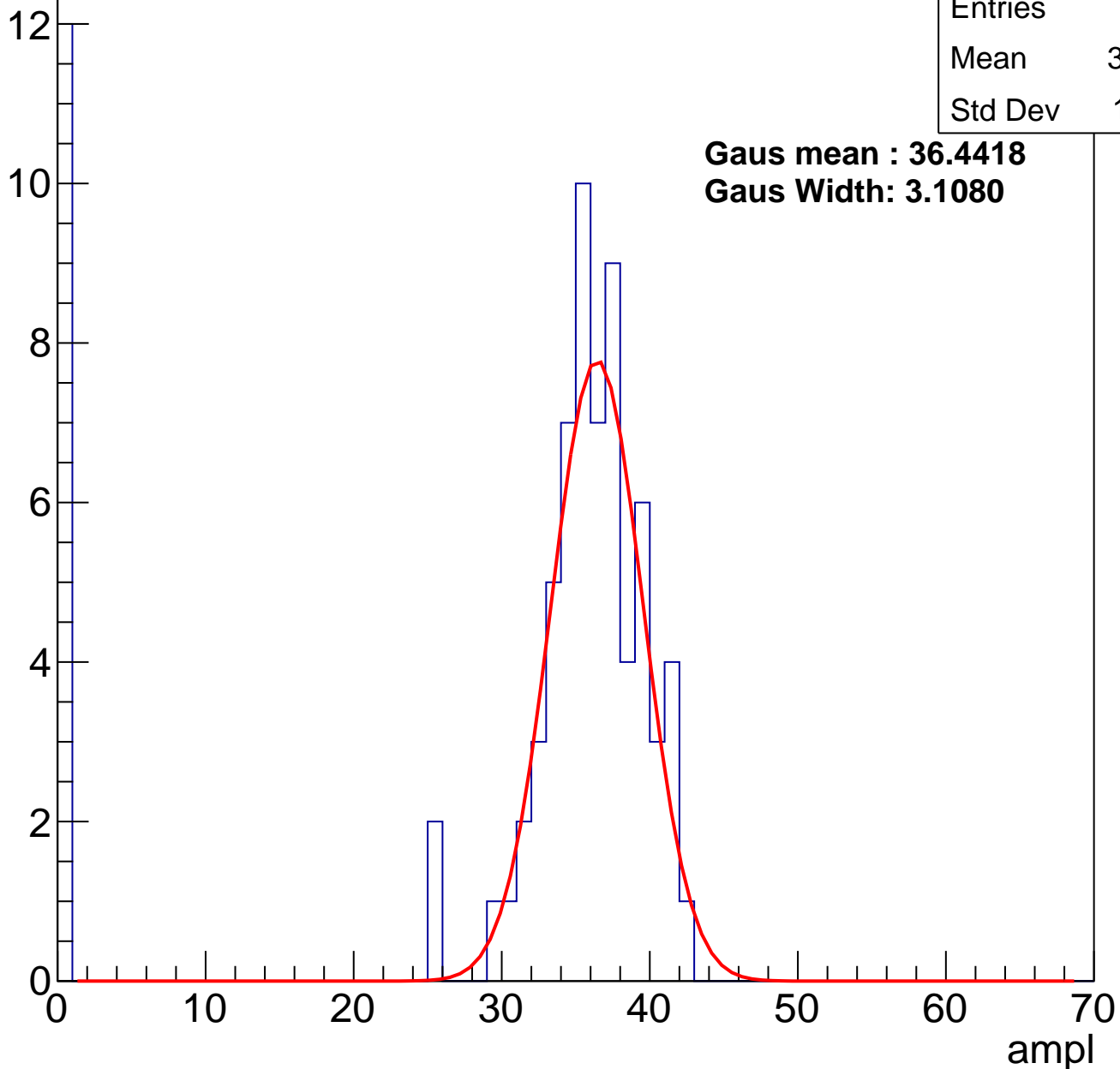
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	30.09
Std Dev	13.31

Gaus mean : 36.4418

Gaus Width: 3.1080

Entry



B1L103S, U2-ch52, adc2

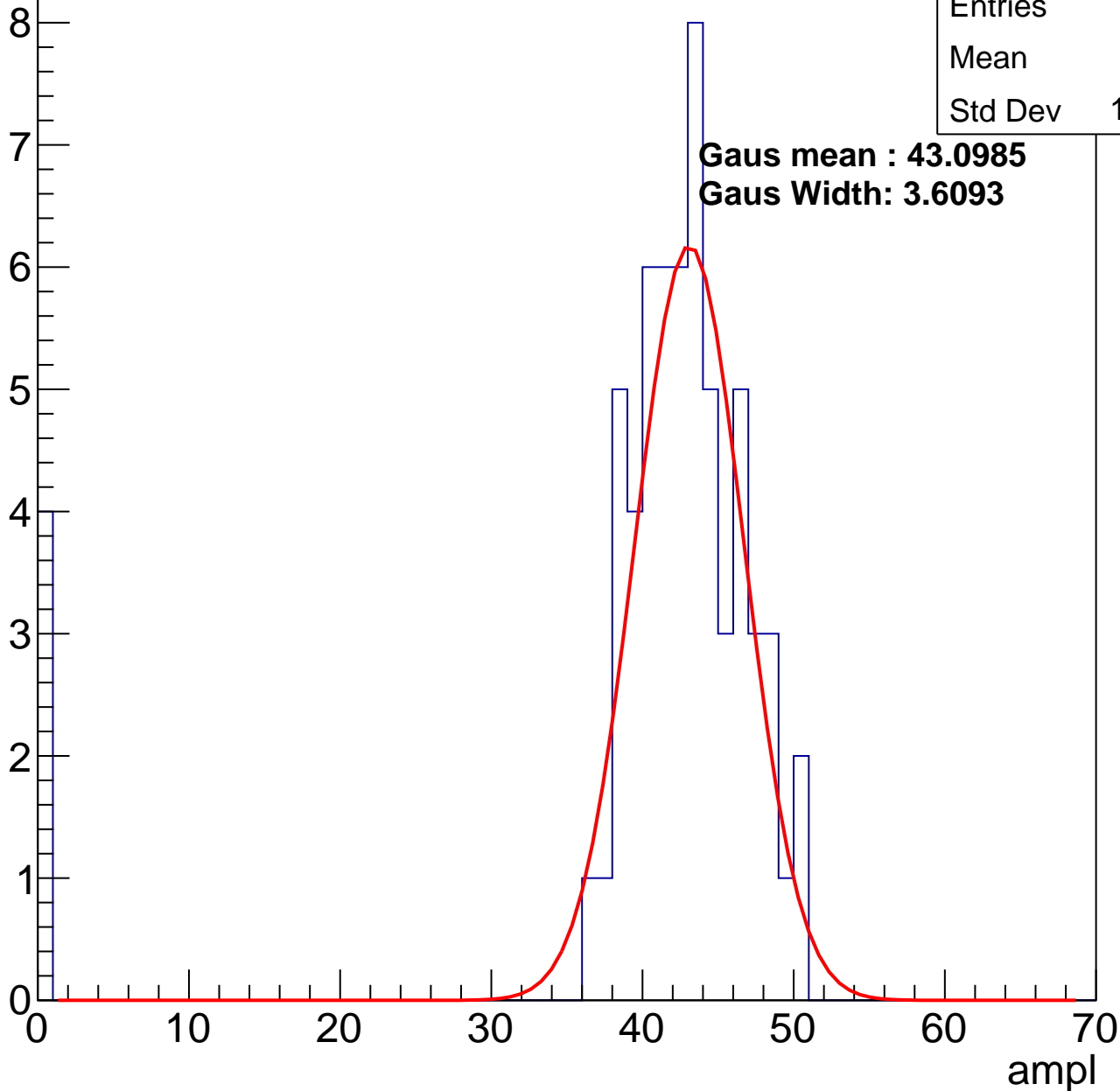
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	40
Std Dev	10.92

Gaus mean : 43.0985

Gaus Width: 3.6093

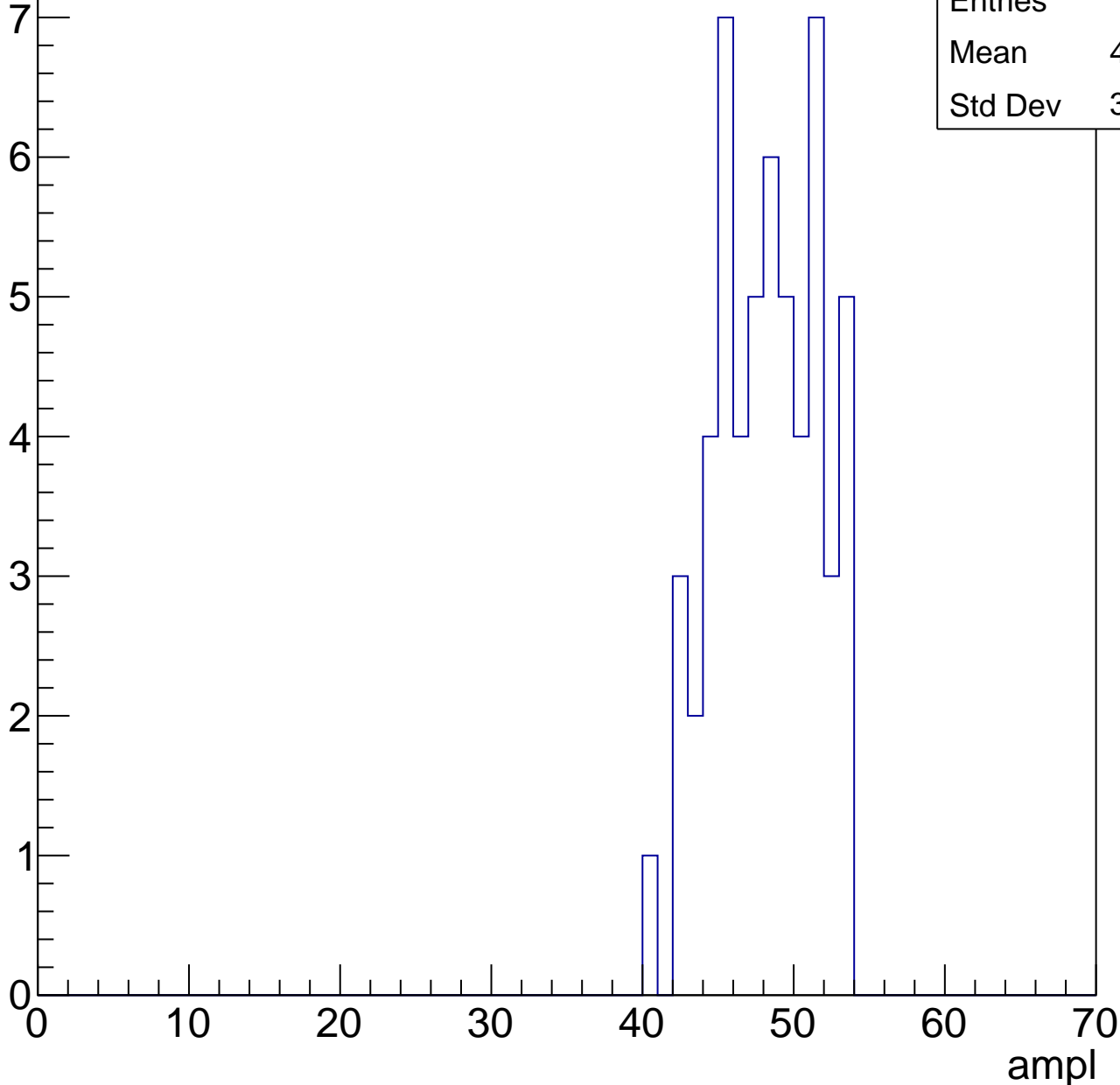


B1L103S, U2-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	47.73
Std Dev	3.335

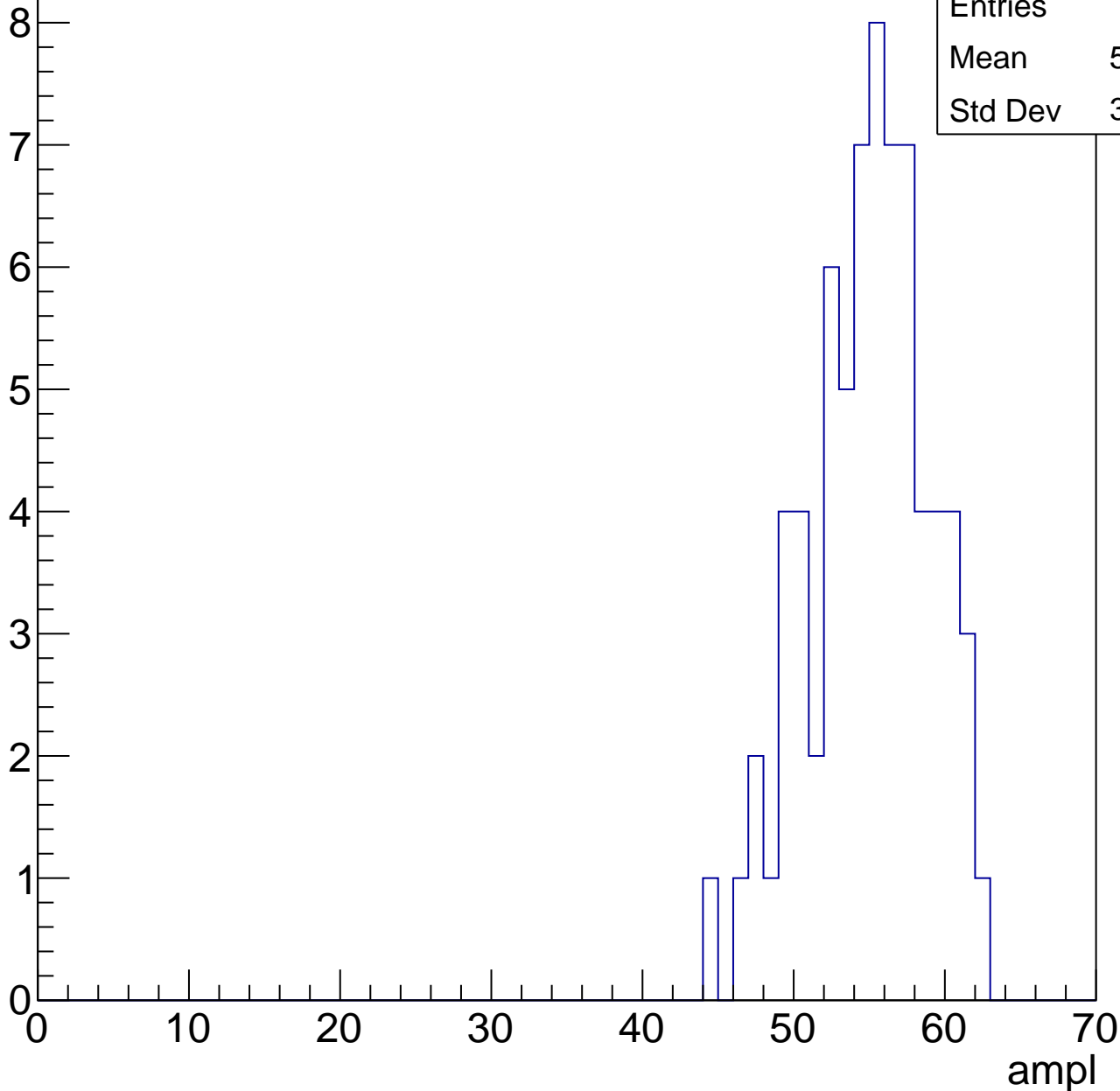


B1L103S, U2-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	54.49
Std Dev	3.954



B1L103S, U2-ch52, adc5

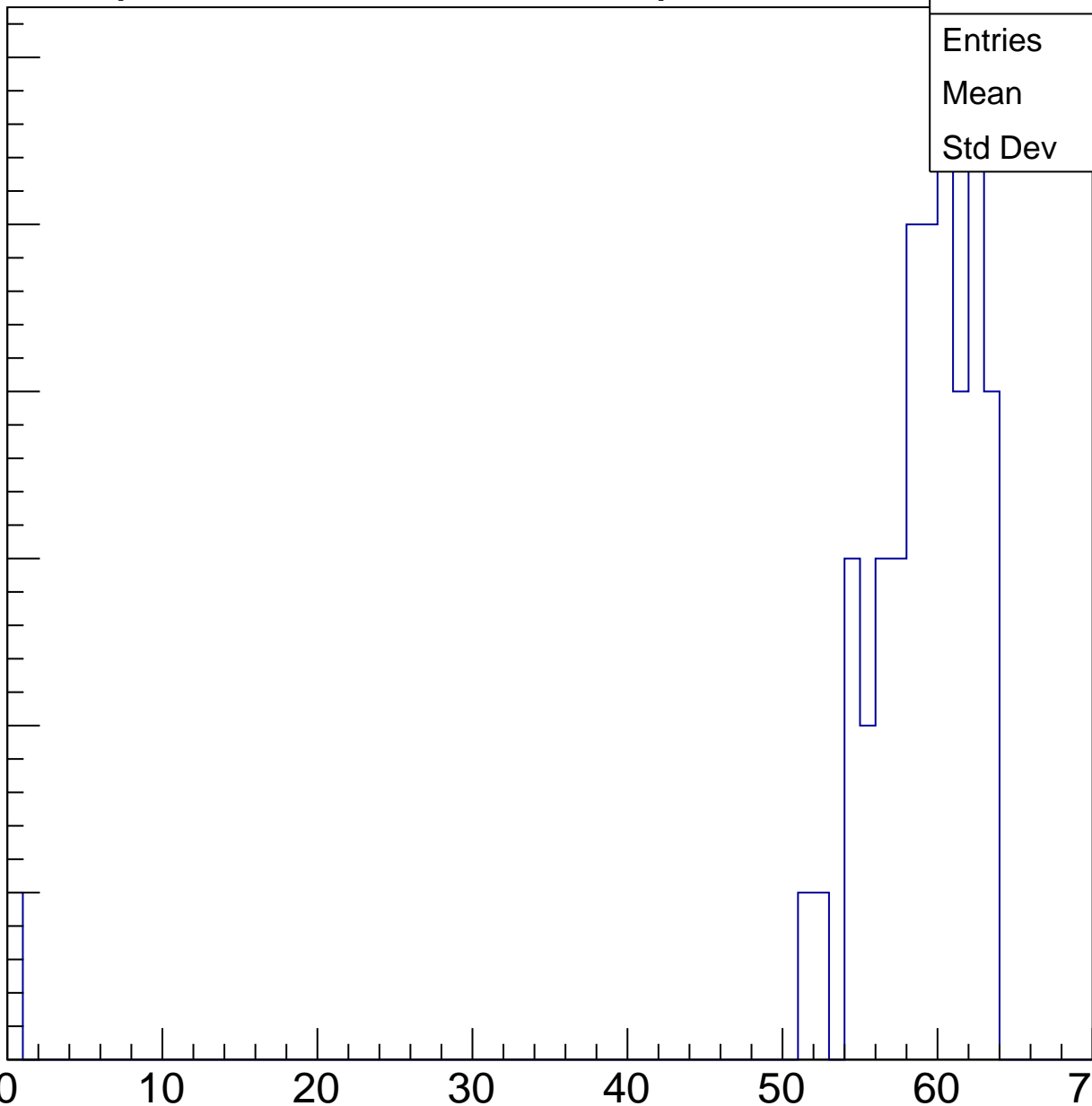
calib_packv5_041523_1651.root, FC#0, port C2

Entry

6
5
4
3
2
1
0

Entries	44
Mean	57.43
Std Dev	9.265

ampl

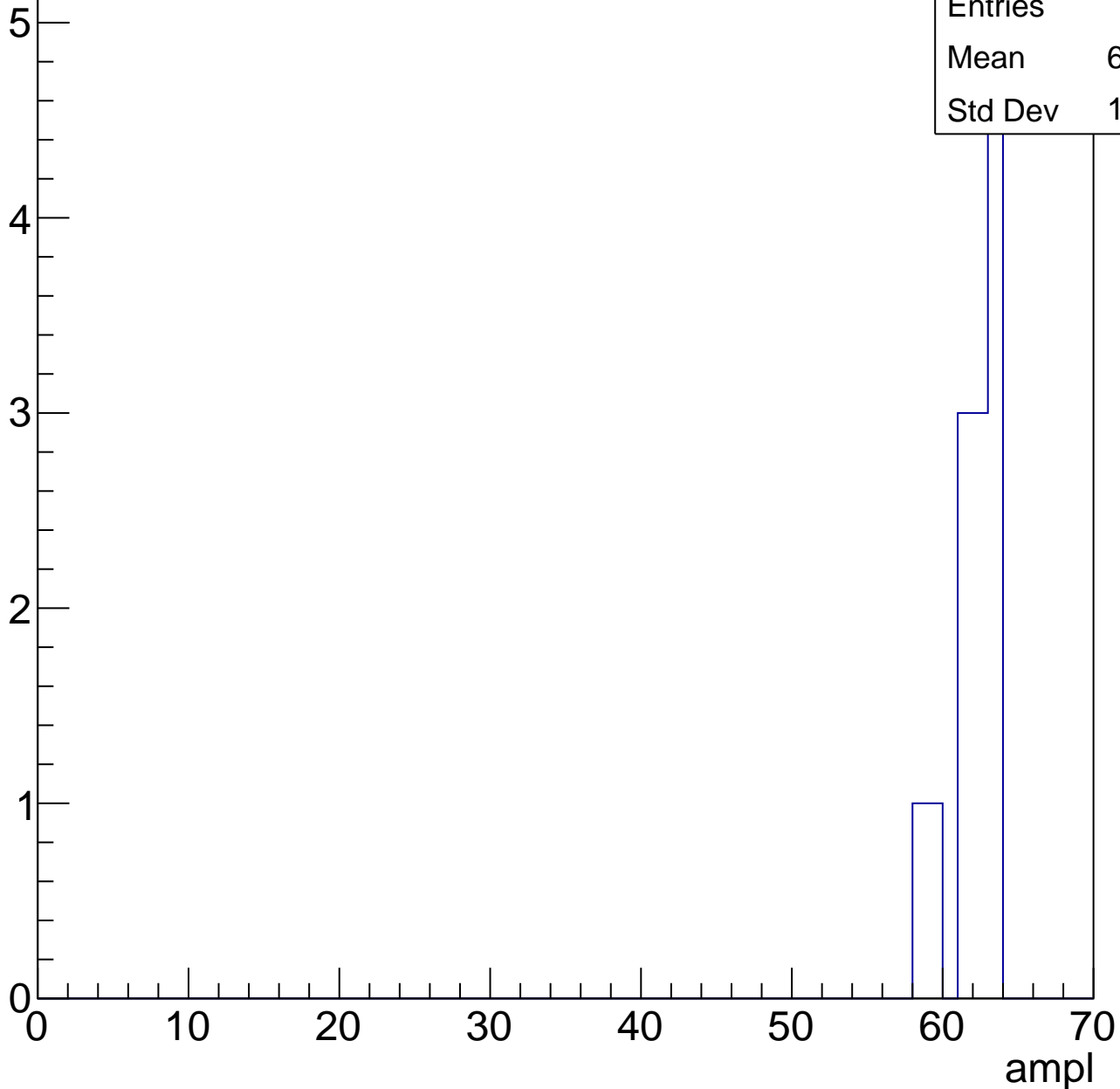


B1L103S, U2-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

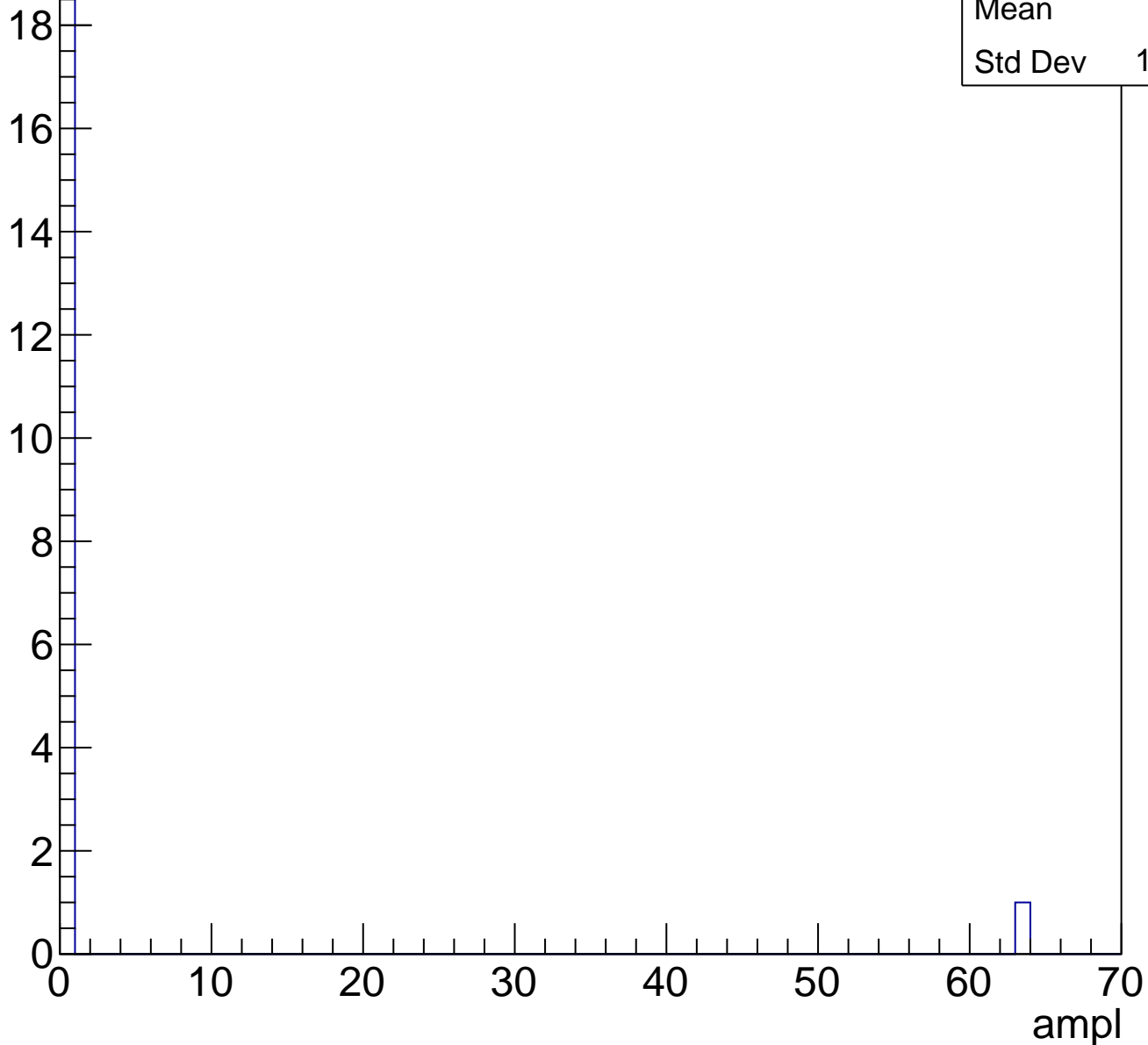
Entries	13
Mean	61.62
Std Dev	1.546



B1L103S, U2-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.15
Std Dev	13.73

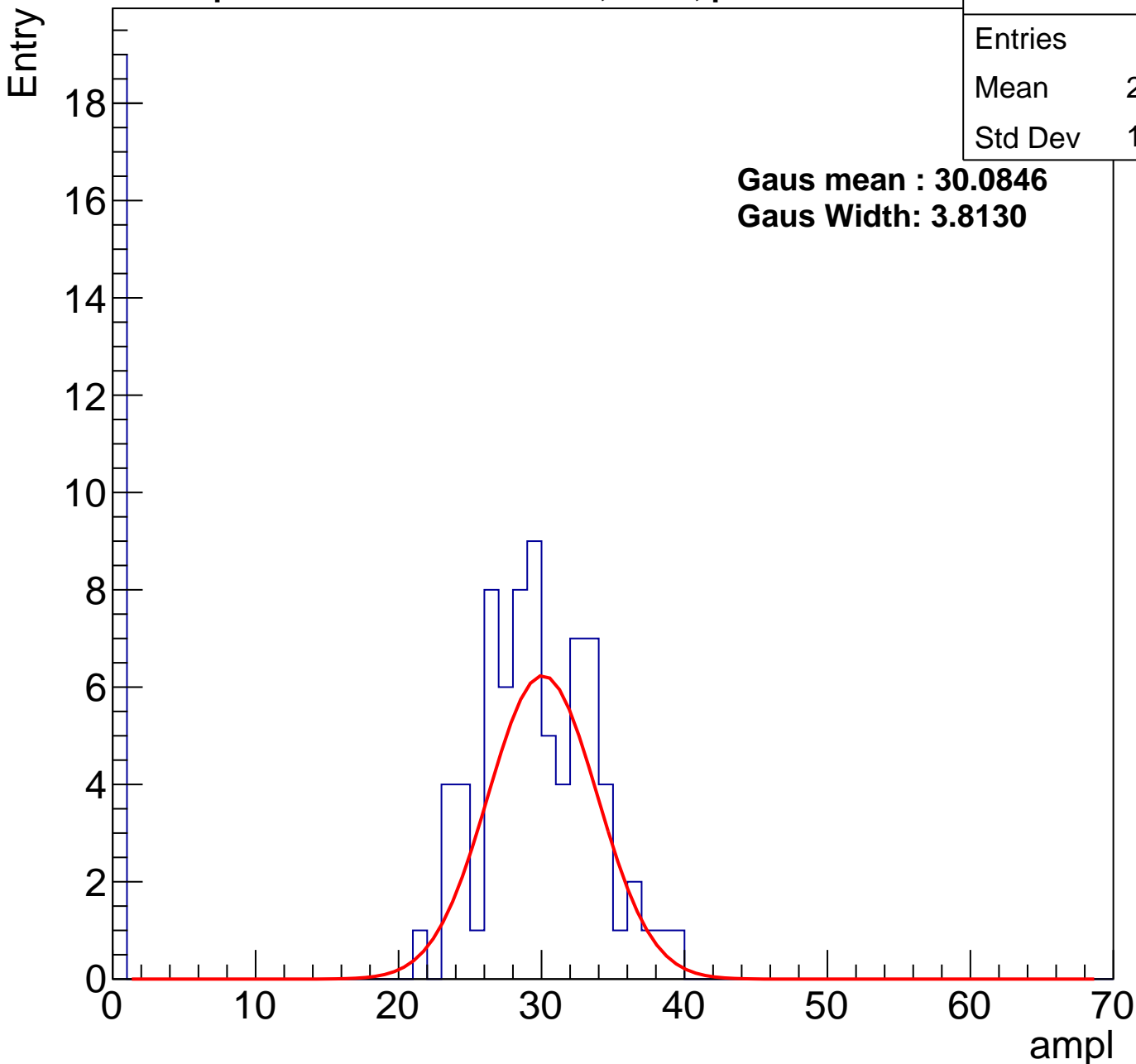
B1L103S, U2-ch53, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	23.39
Std Dev	12.34

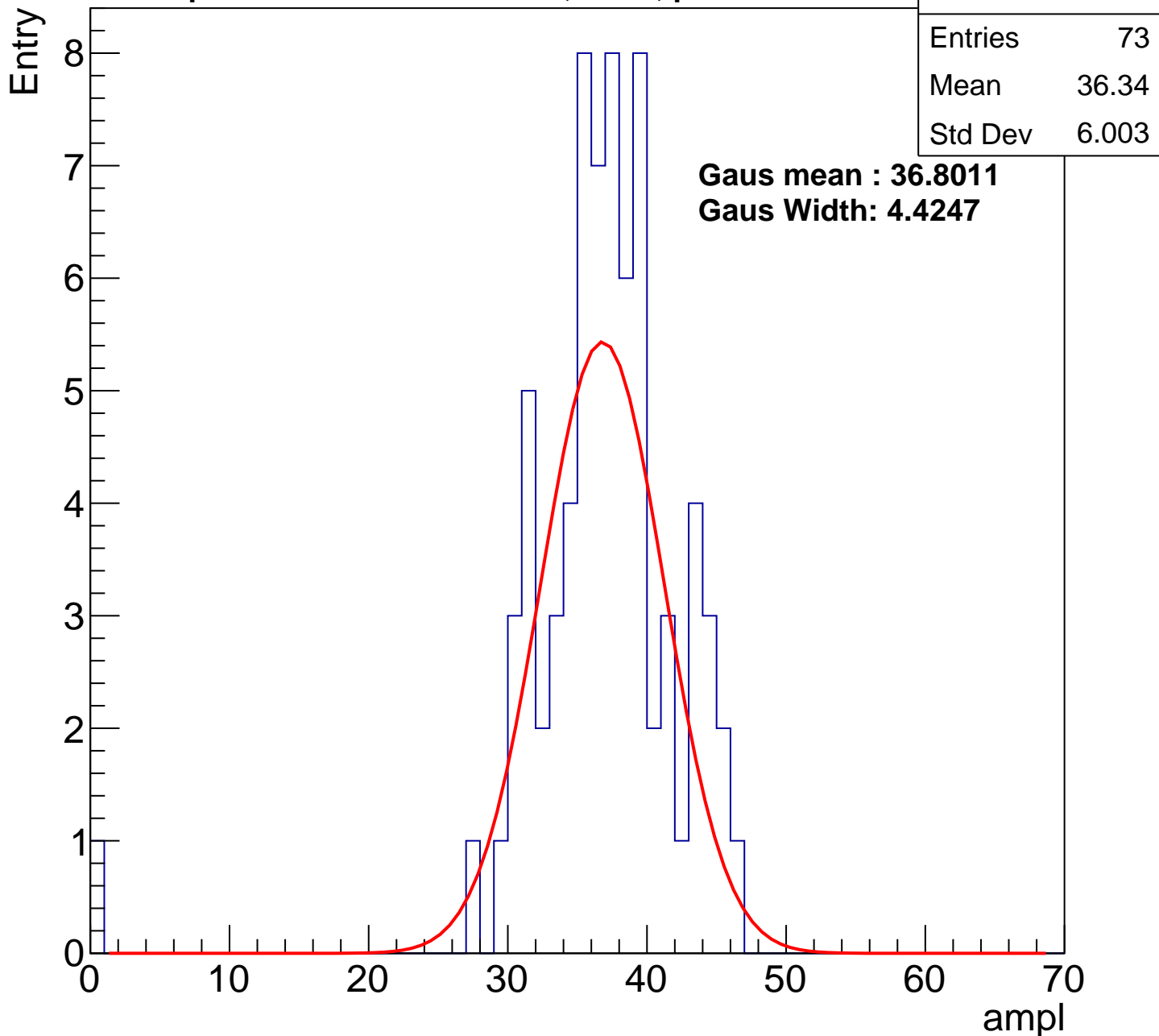
Gaus mean : 30.0846

Gaus Width: 3.8130



B1L103S, U2-ch53, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch53, adc2

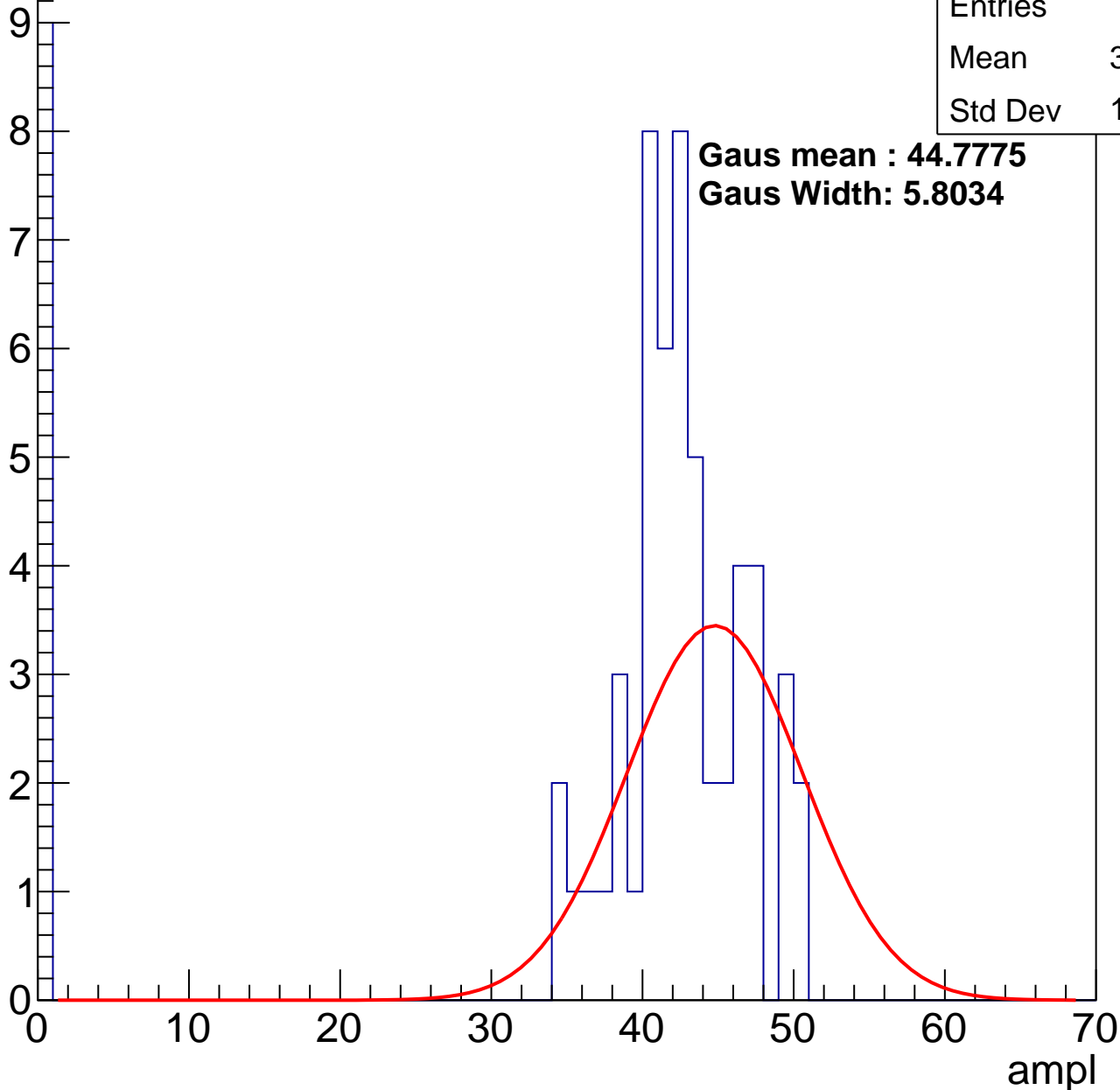
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	36.18
Std Dev	15.33

Gaus mean : 44.7775

Gaus Width: 5.8034

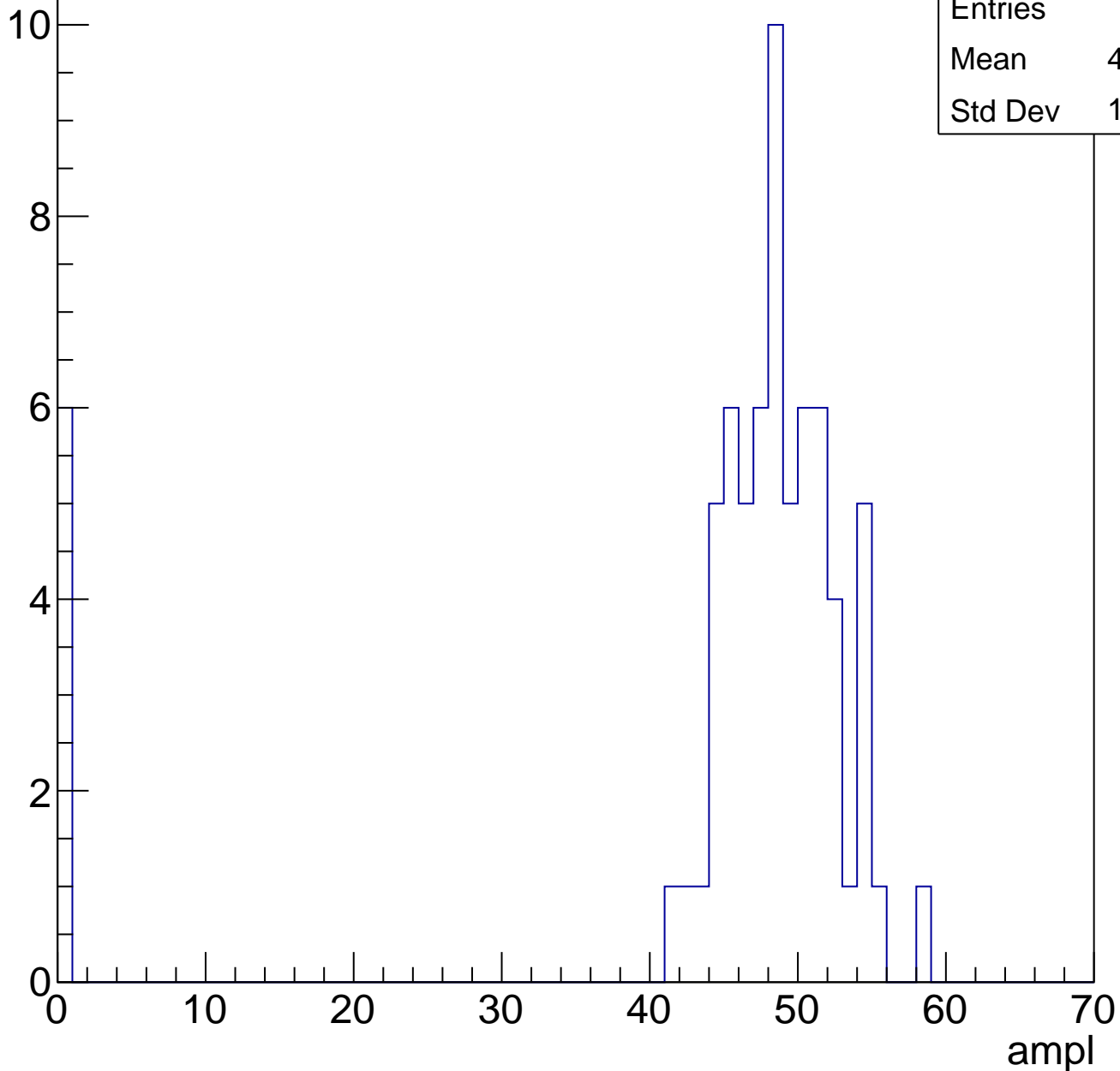


B1L103S, U2-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	44.33
Std Dev	13.96

Entry

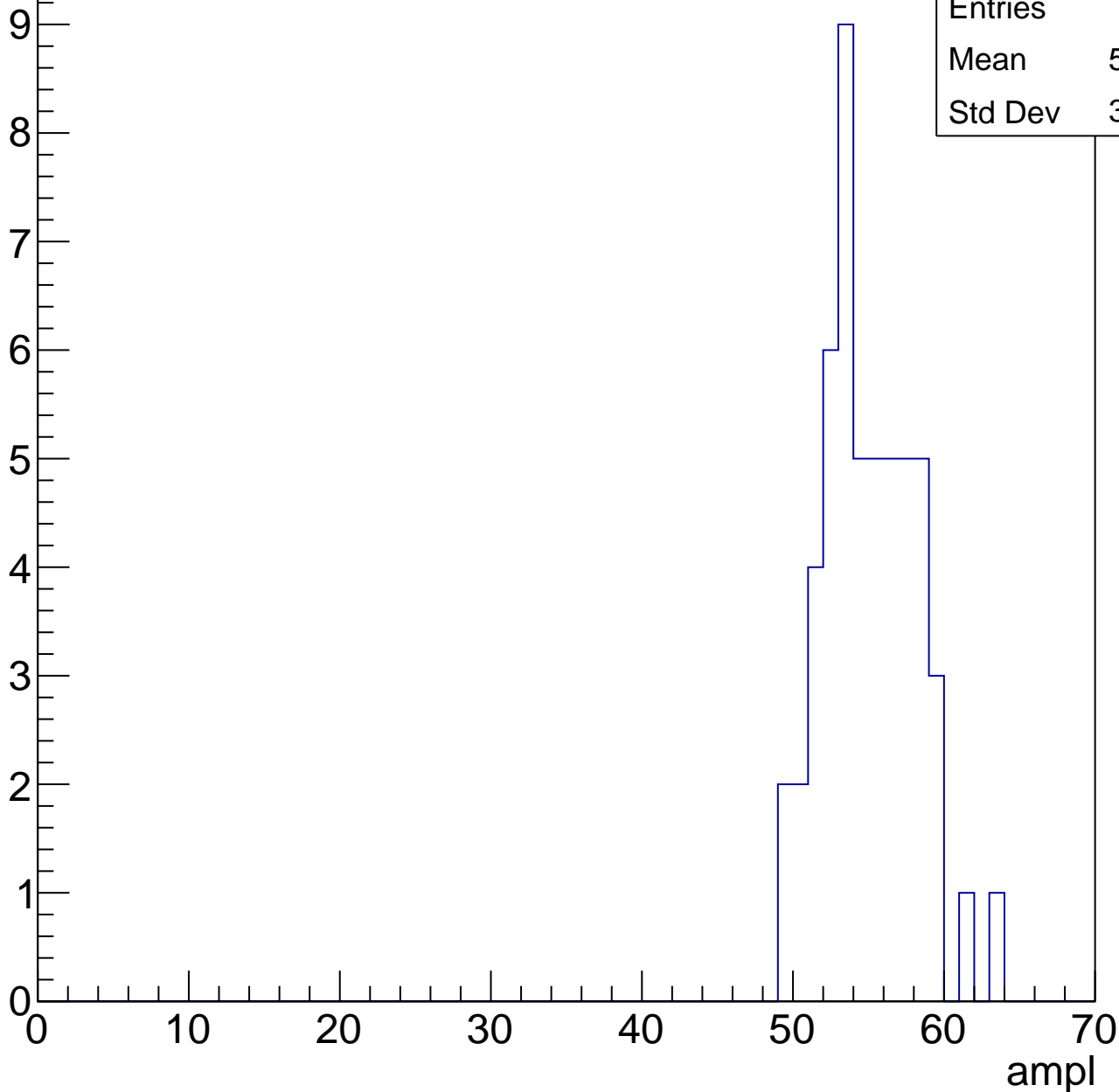


B1L103S, U2-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	54.57
Std Dev	3.038

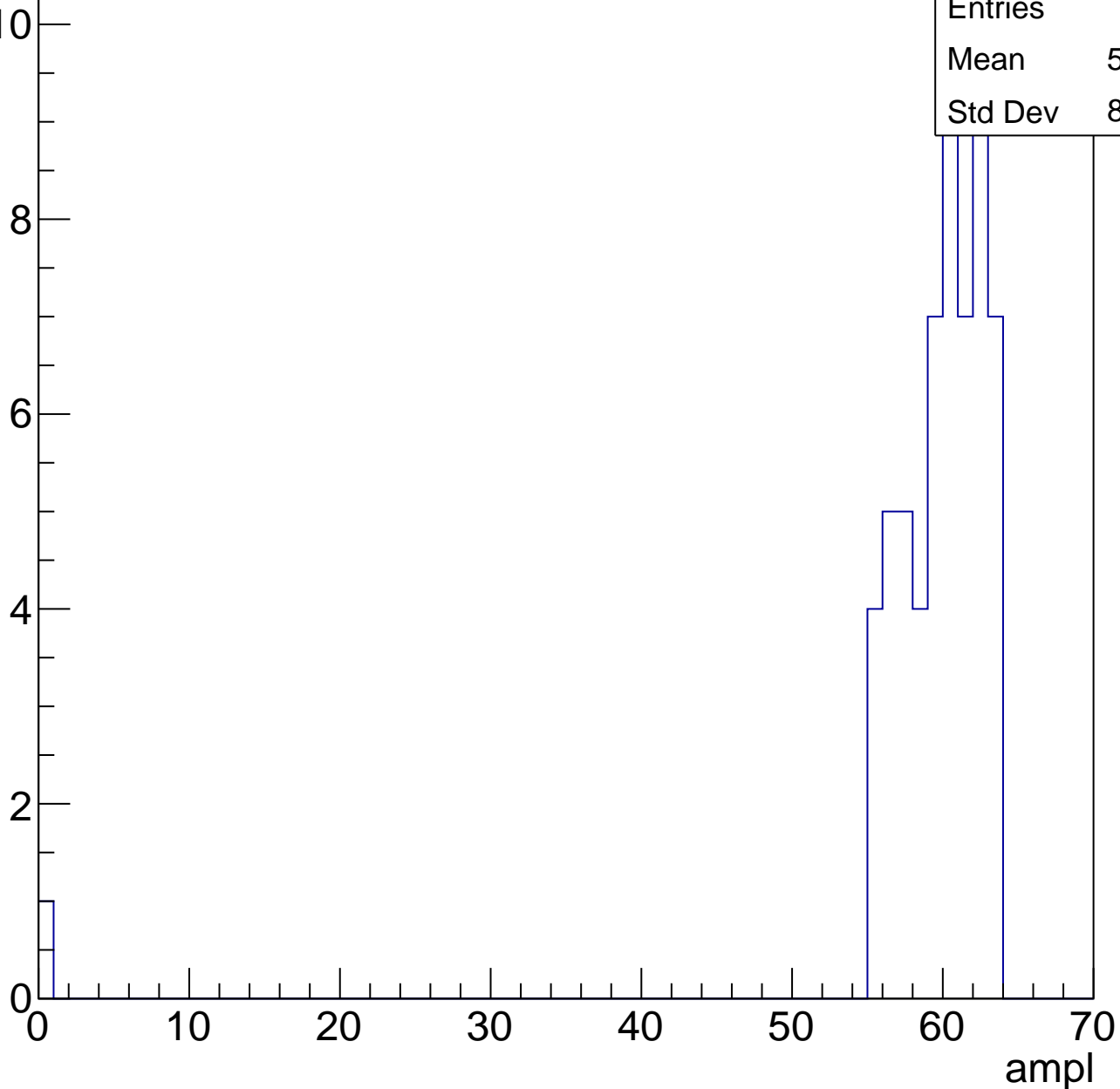


B1L103S, U2-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	58.58
Std Dev	8.062



B1L103S, U2-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

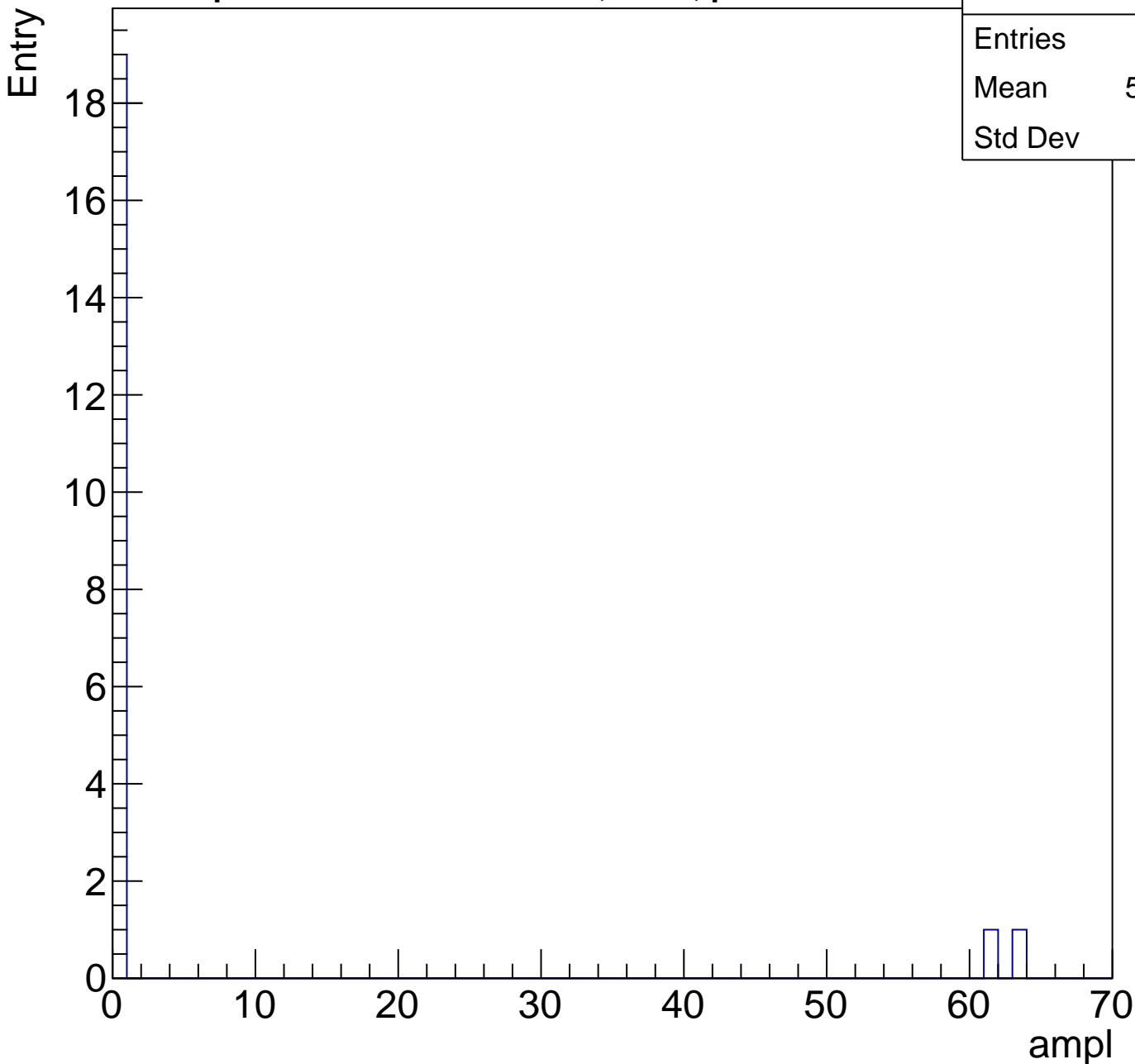
Entry



B1L103S, U2-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2



B1L103S, U2-ch54, adc0

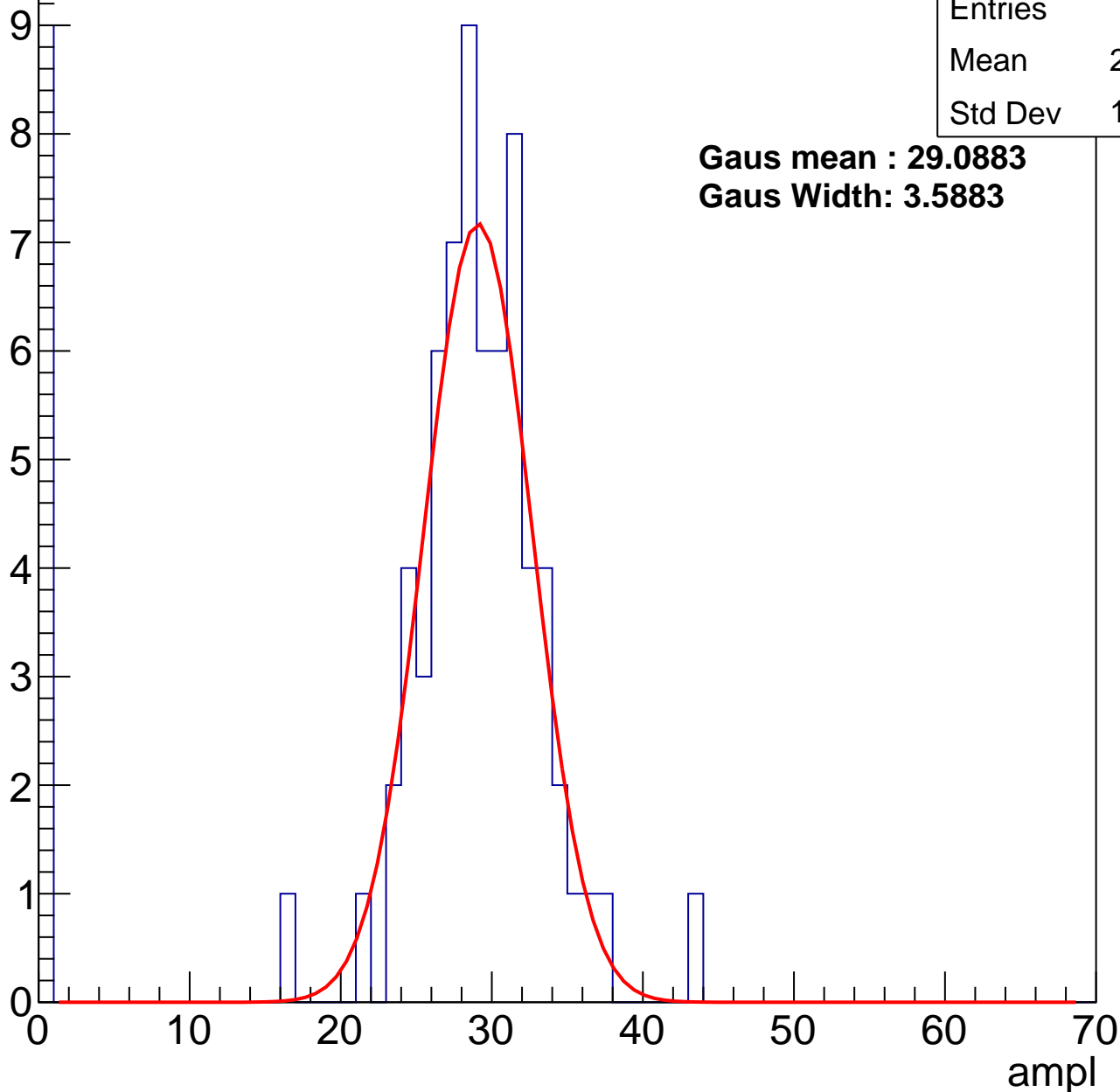
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	25.42
Std Dev	10.05

Gaus mean : 29.0883

Gaus Width: 3.5883



B1L103S, U2-ch54, adc1

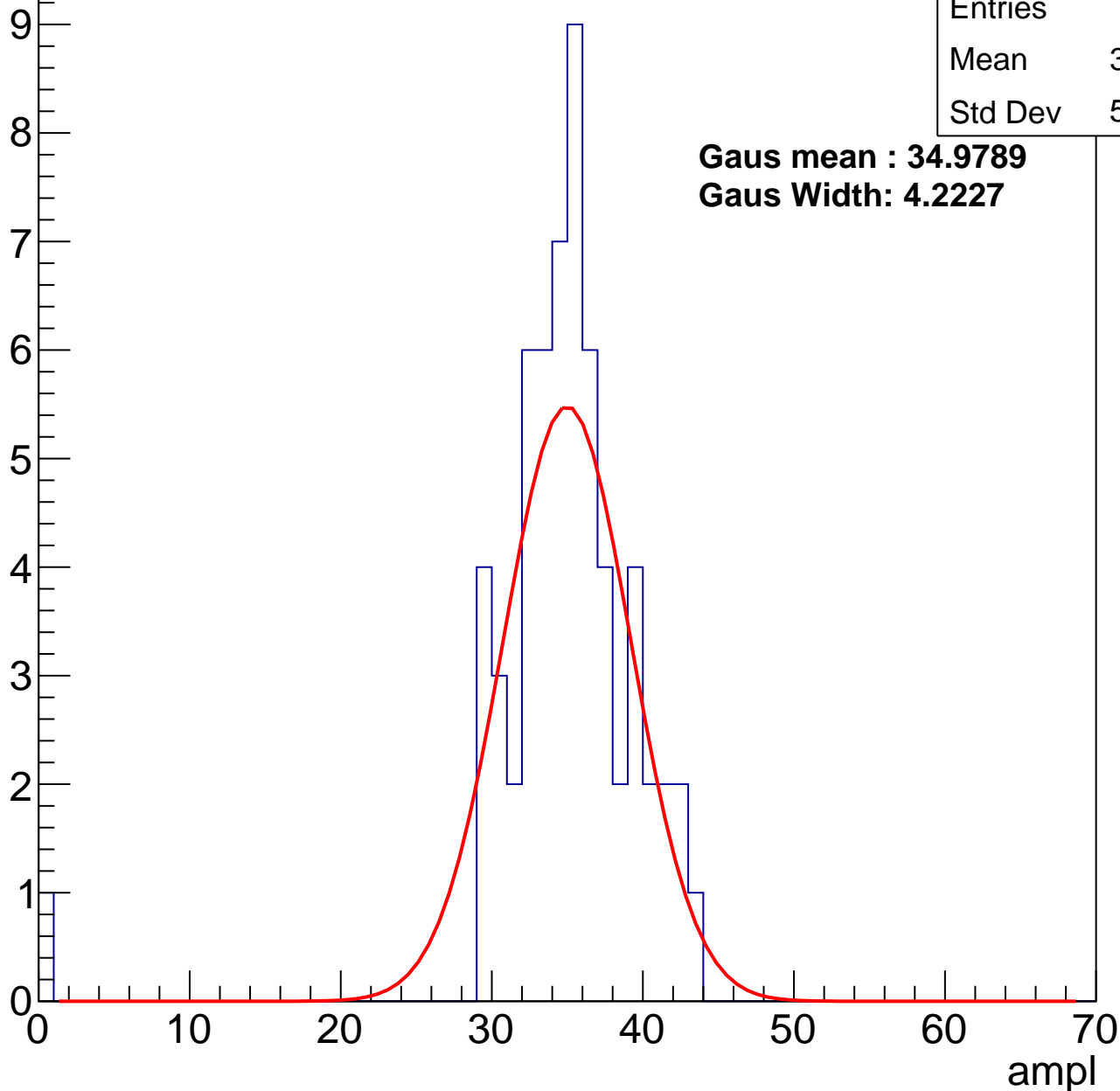
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.36
Std Dev	5.616

Gaus mean : 34.9789

Gaus Width: 4.2227



B1L103S, U2-ch54, adc2

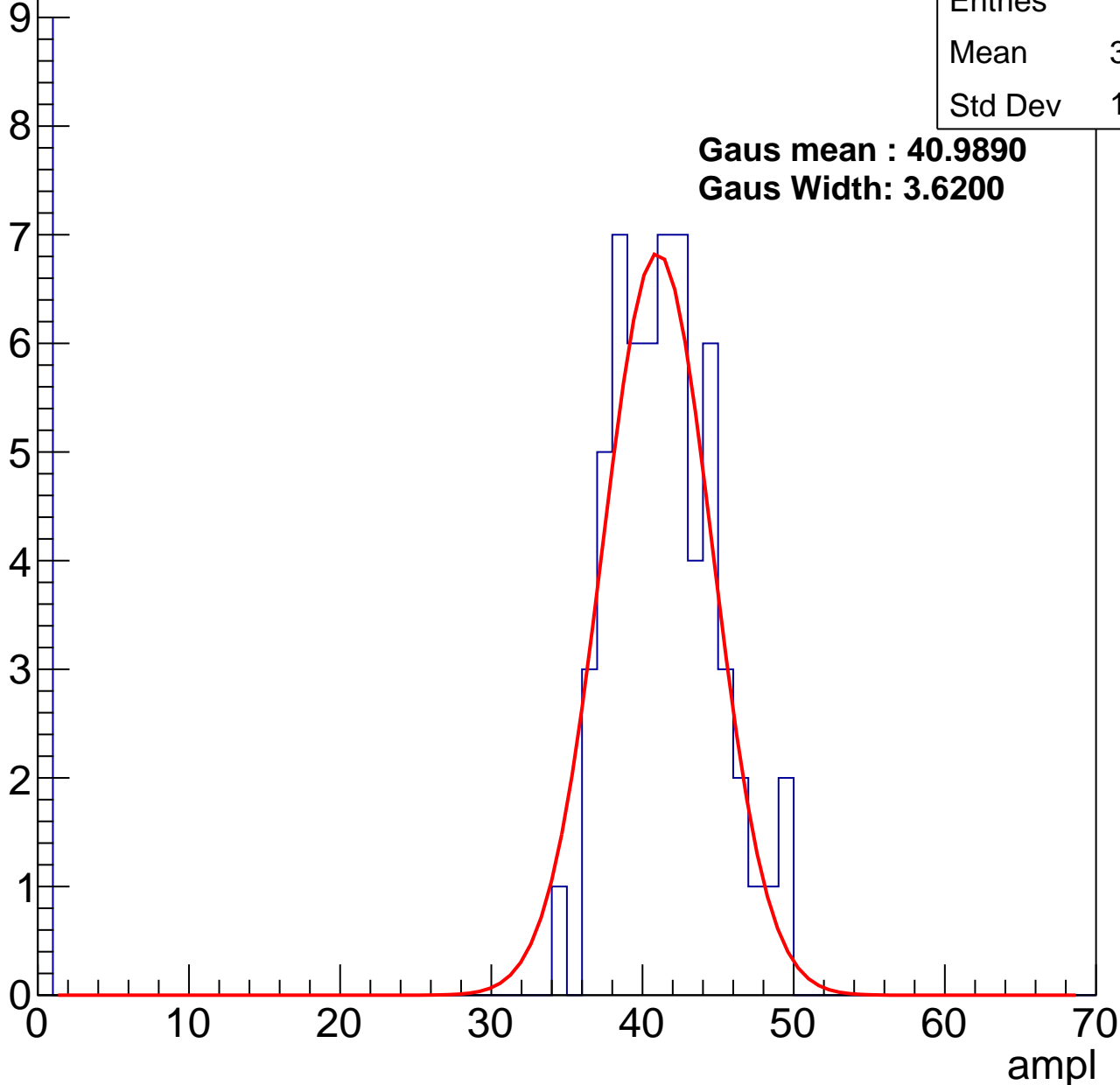
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	35.77
Std Dev	14.09

Gaus mean : 40.9890

Gaus Width: 3.6200

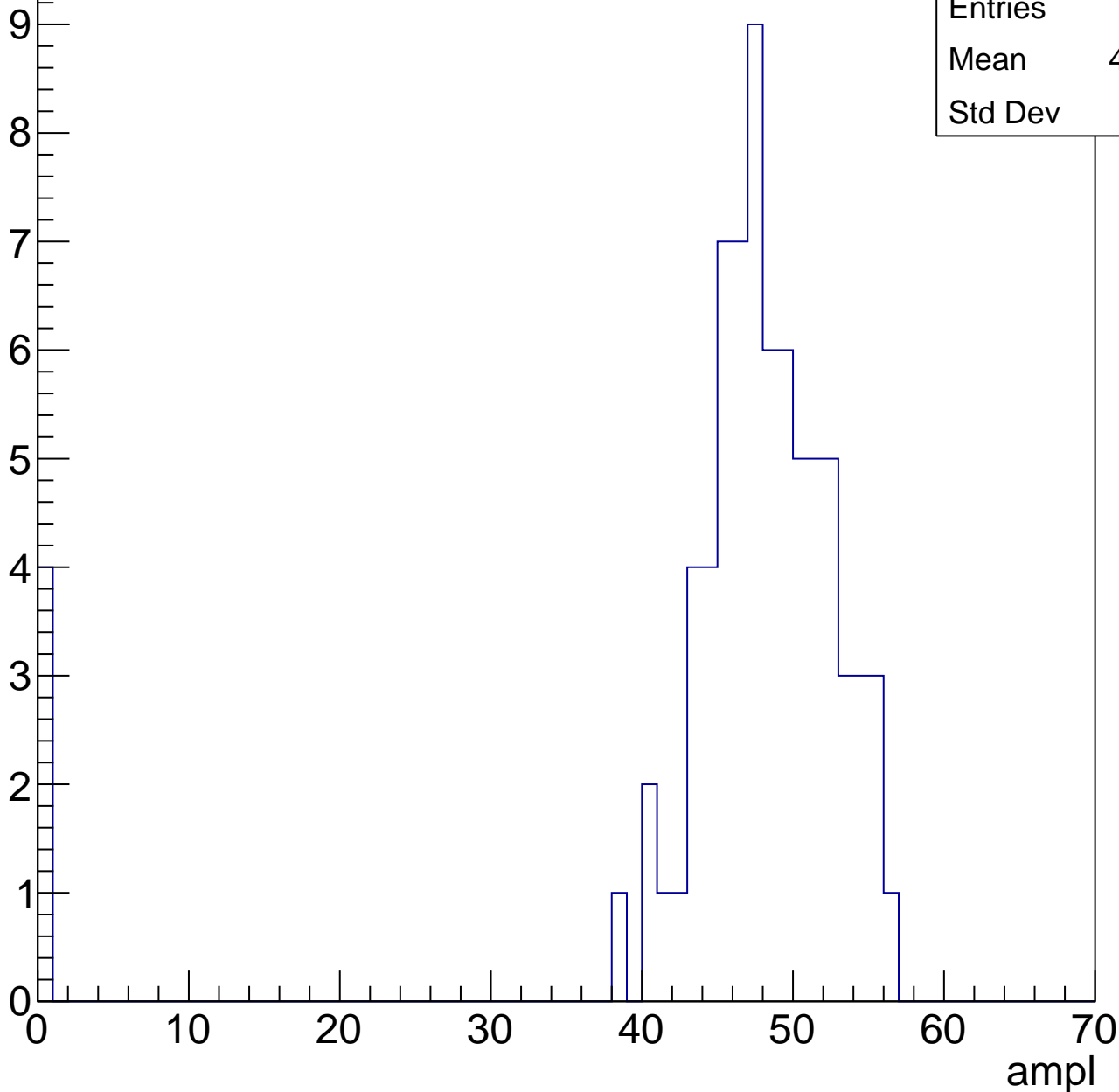


B1L103S, U2-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	45.43
Std Dev	11.3

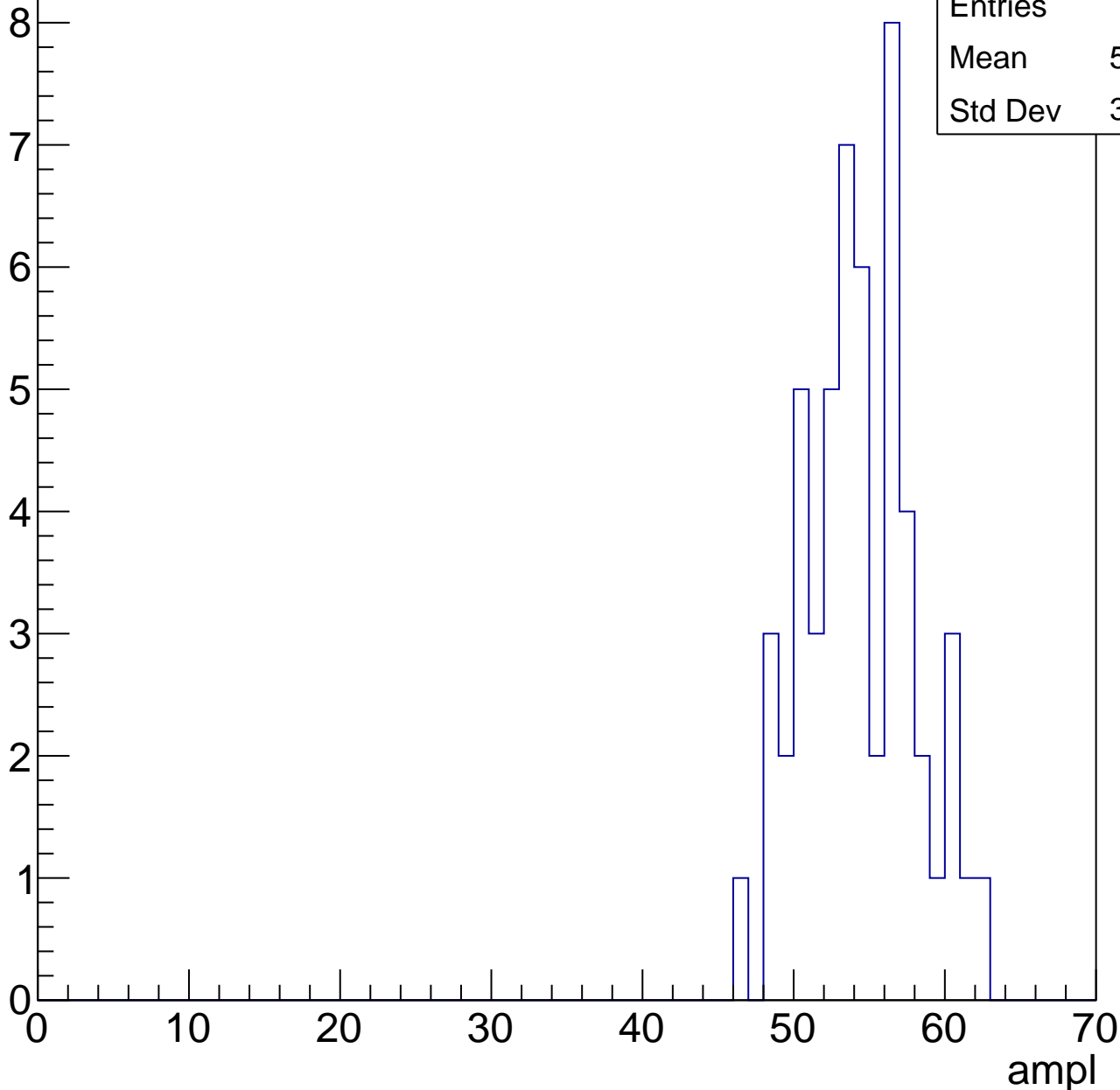


B1L103S, U2-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

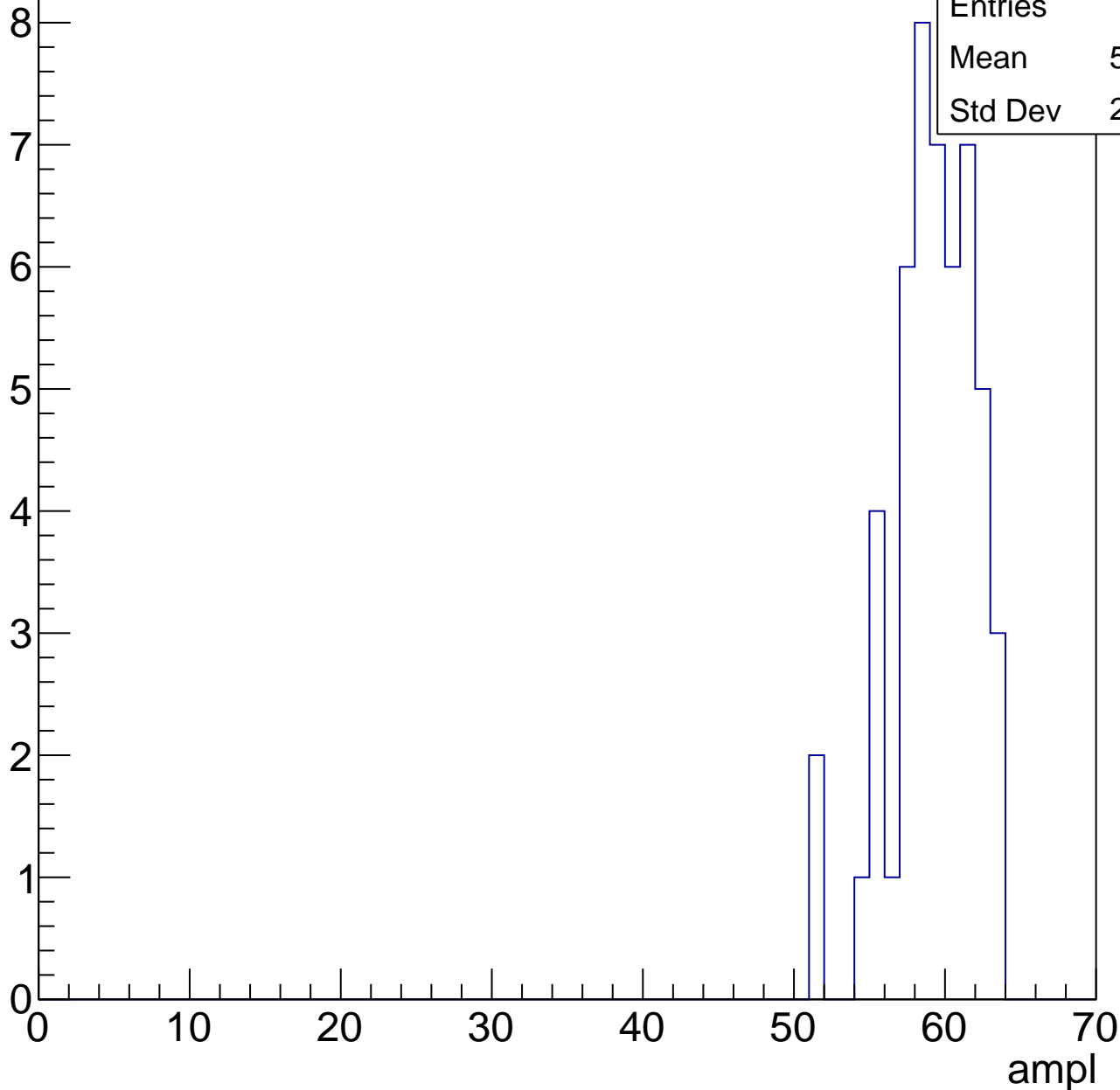
Entries	54
Mean	53.89
Std Dev	3.609



B1L103S, U2-ch54, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

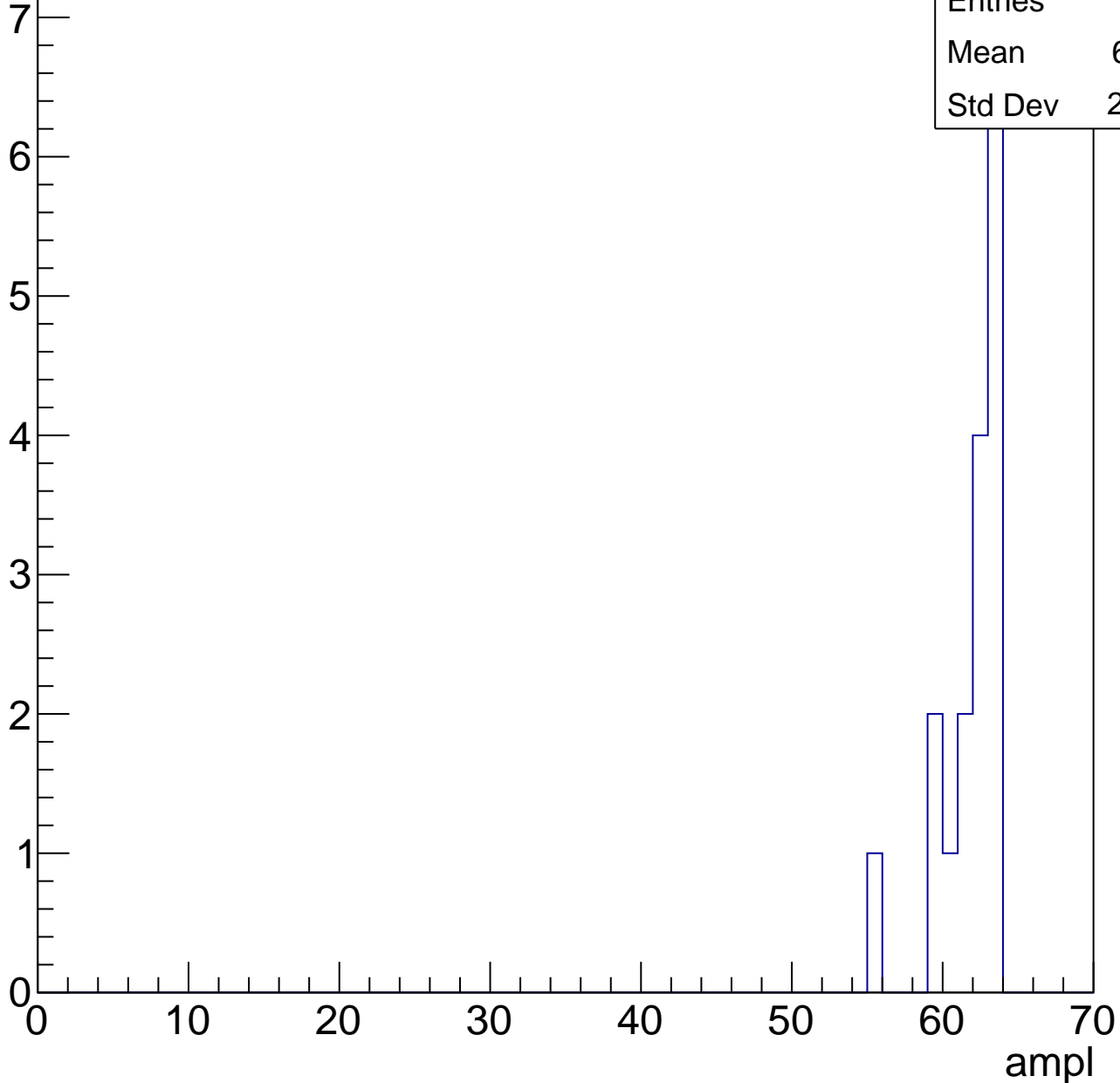


B1L103S, U2-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.41
Std Dev	2.088



B1L103S, U2-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch55, adc0

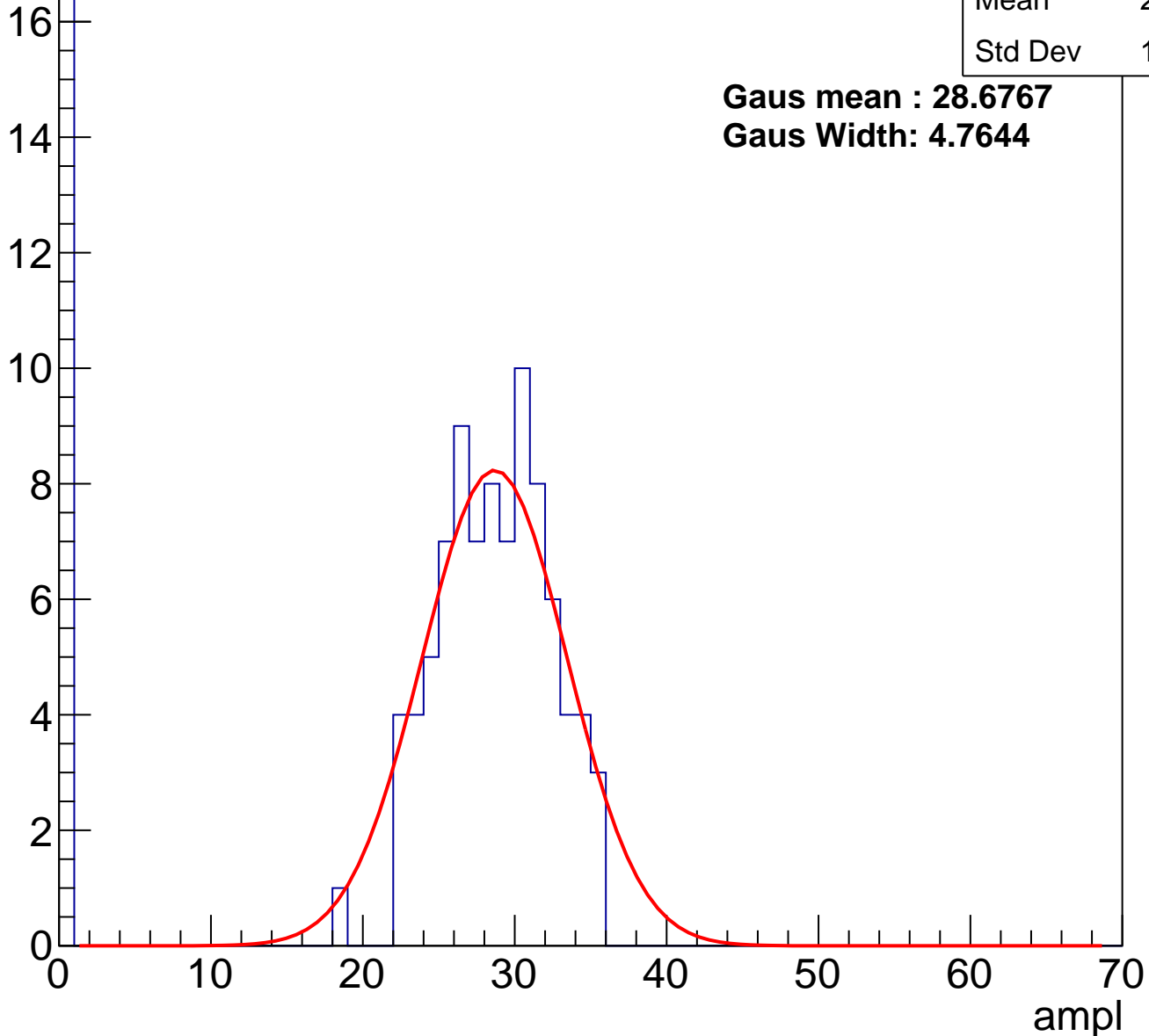
calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	23.62
Std Dev	10.95

Gaus mean : 28.6767

Gaus Width: 4.7644

Entry



B1L103S, U2-ch55, adc1

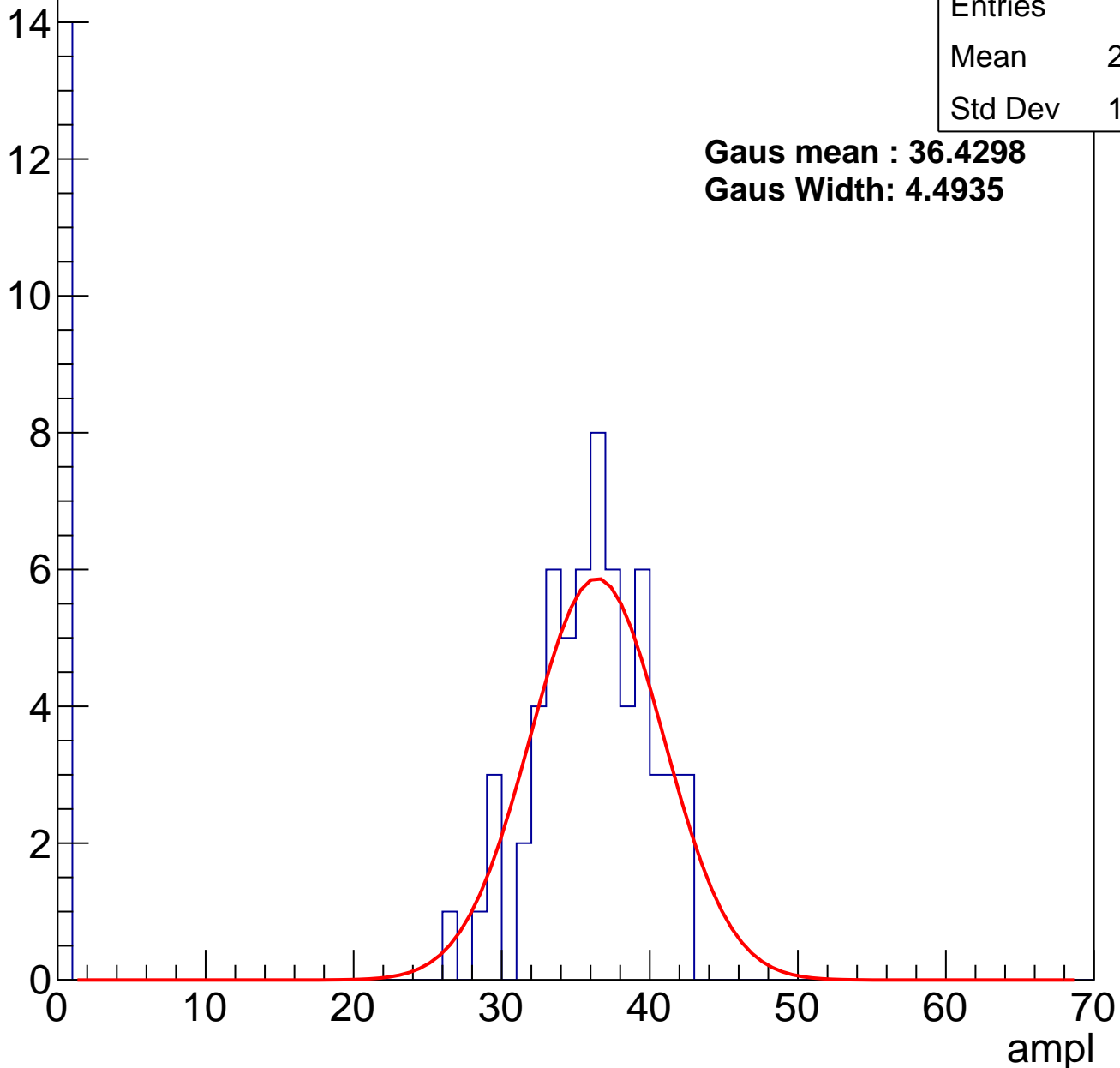
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	28.99
Std Dev	14.27

Gaus mean : 36.4298

Gaus Width: 4.4935

Entry



B1L103S, U2-ch55, adc2

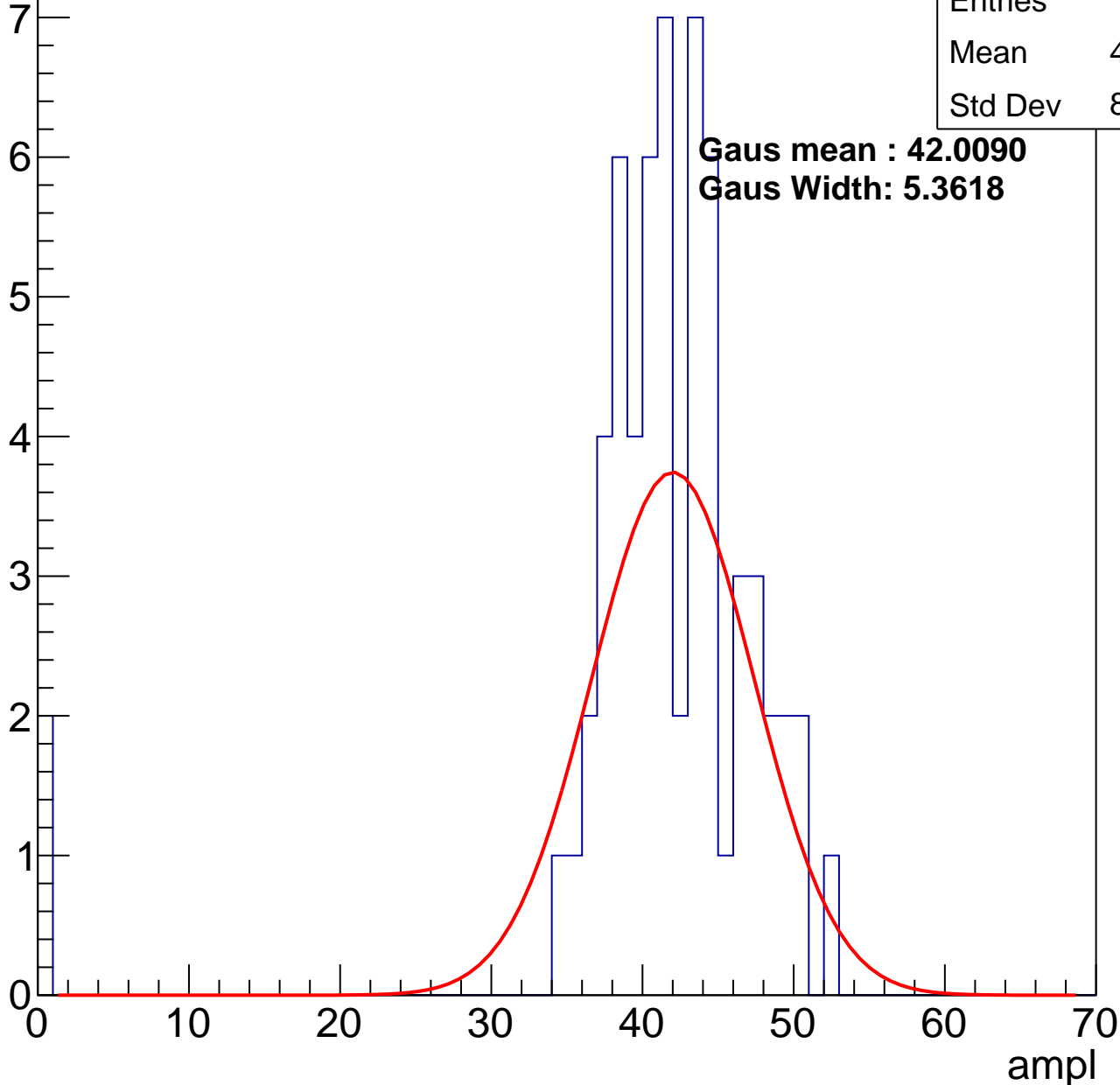
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	40.63
Std Dev	8.444

Gaus mean : 42.0090

Gaus Width: 5.3618

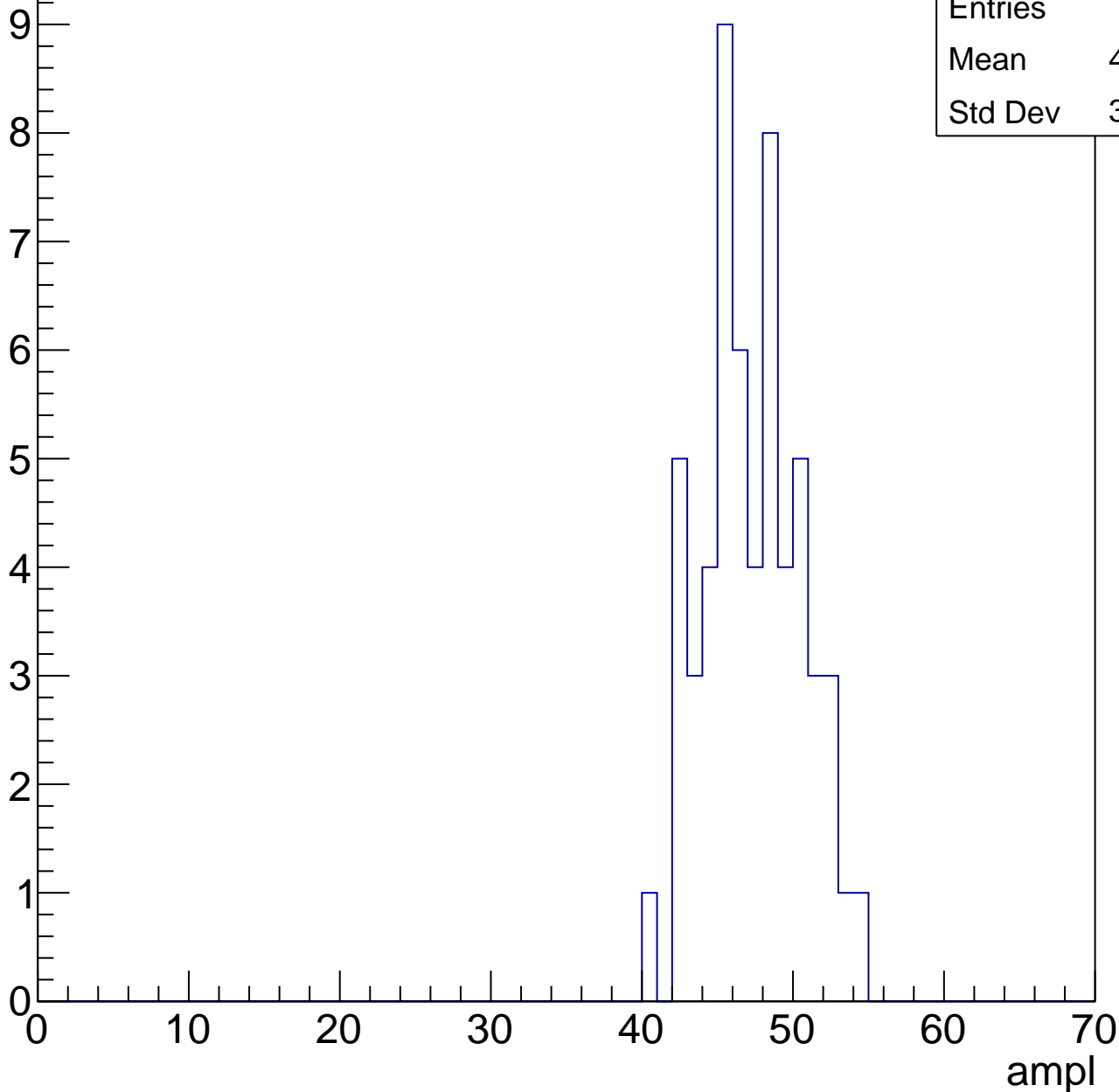


B1L103S, U2-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	46.84
Std Dev	3.178

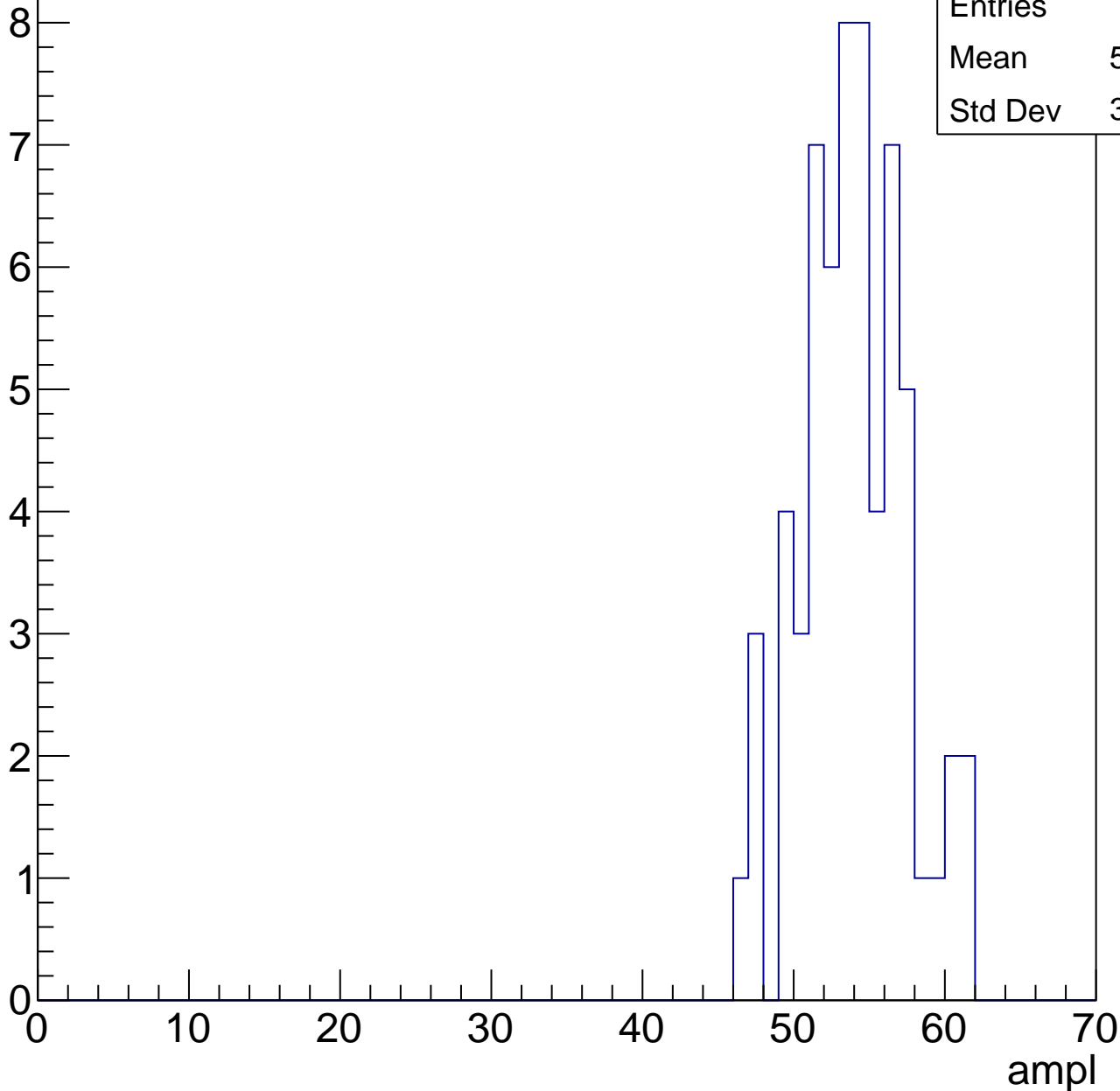


B1L103S, U2-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	53.45
Std Dev	3.416

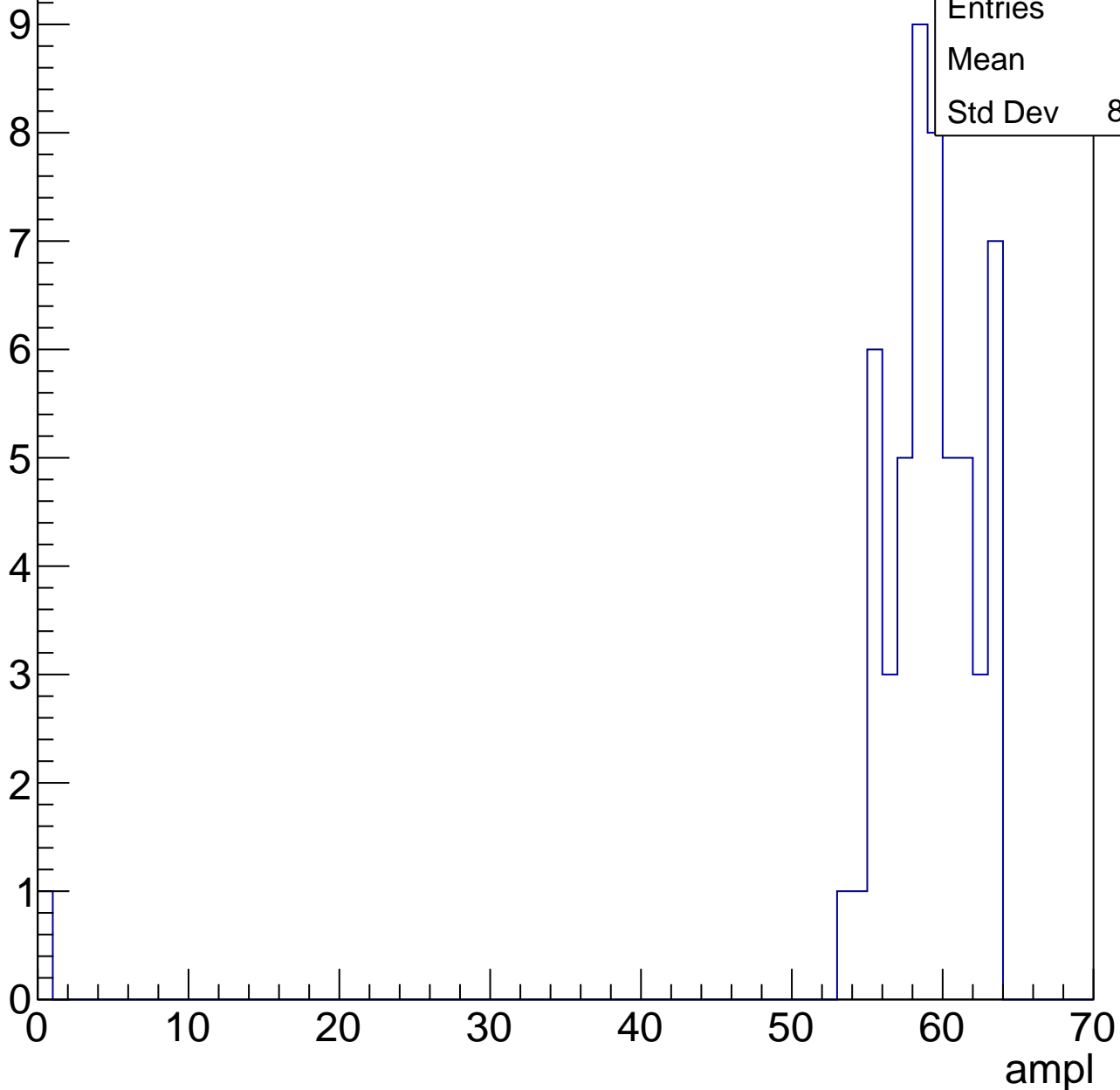


B1L103S, U2-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	57.7
Std Dev	8.352

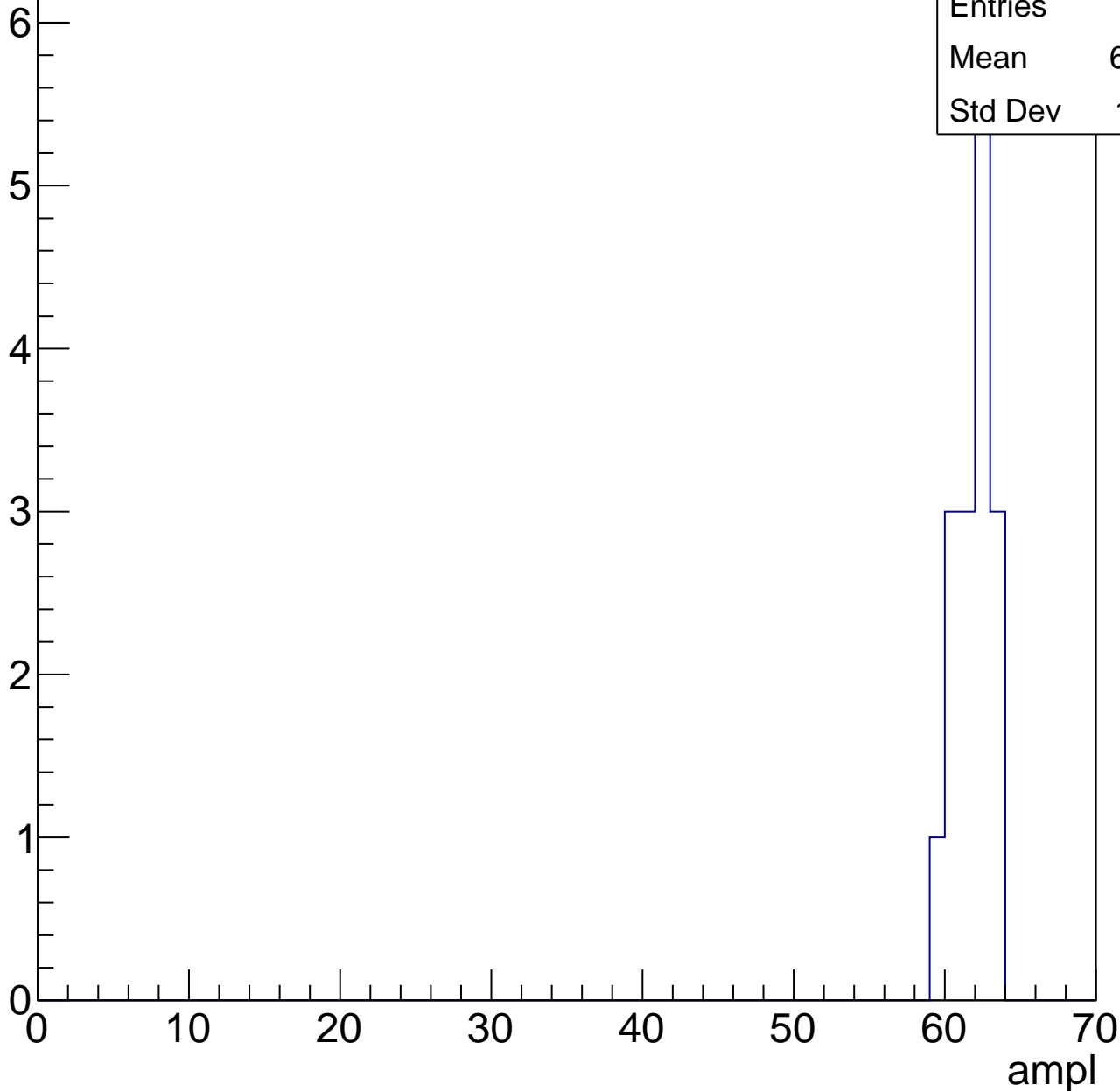


B1L103S, U2-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.44
Std Dev	1.171

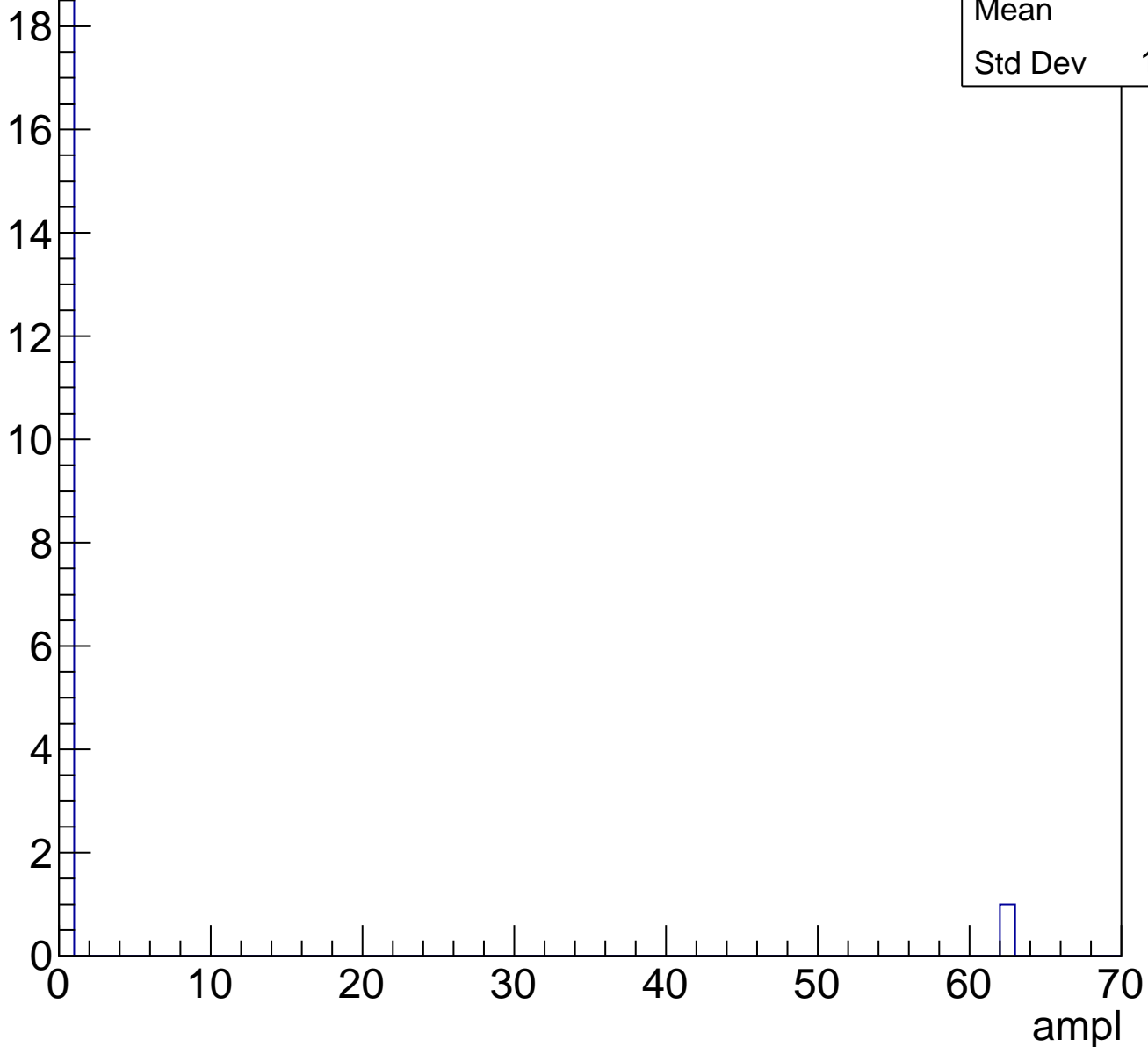


B1L103S, U2-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U2-ch56, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	25.83
Std Dev	10.25

Gaus mean : 29.4227

Gaus Width: 4.1709

Entry

10

8

6

4

2

0

0

10

20

30

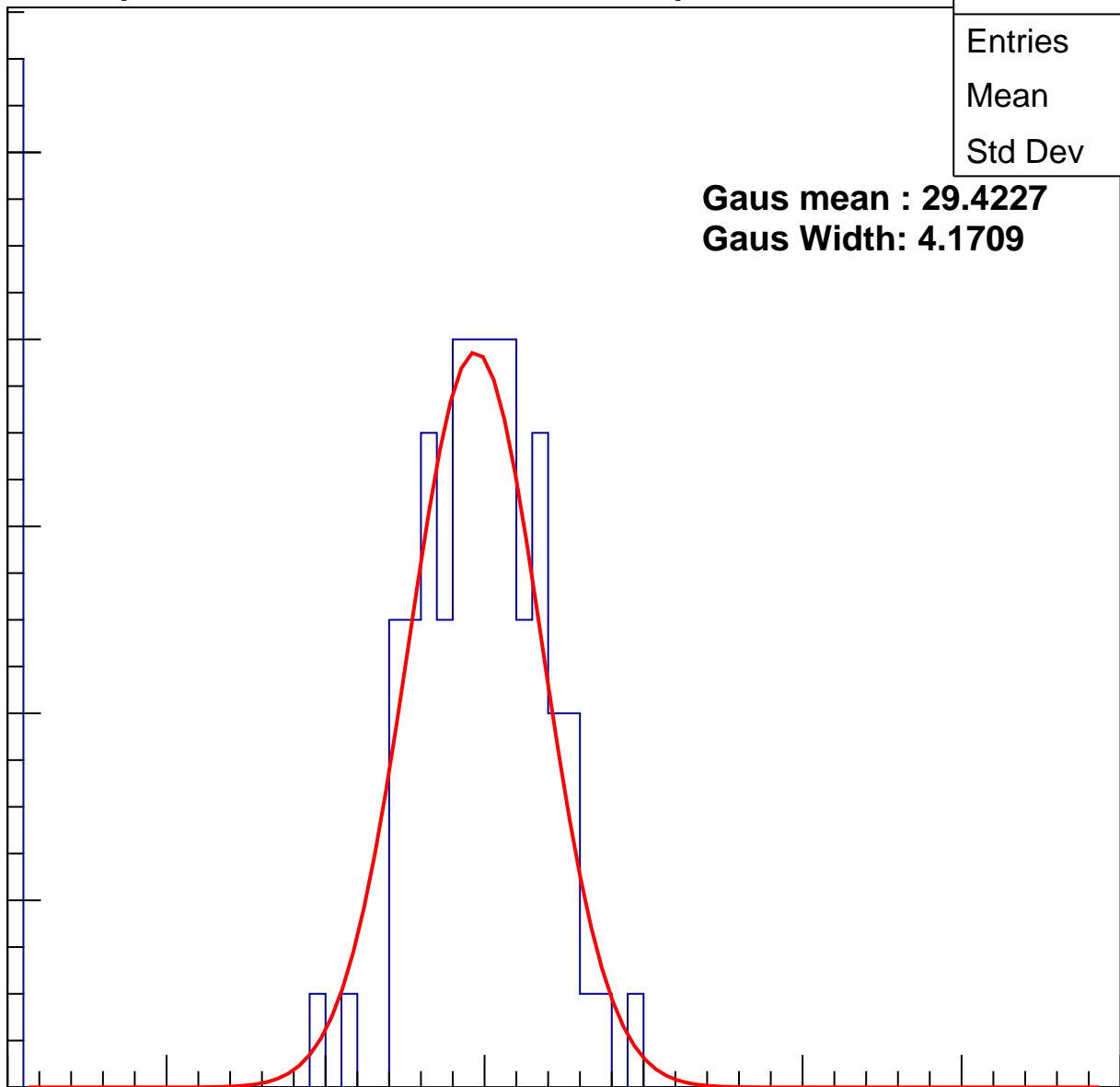
40

50

60

70

ampl



B1L103S, U2-ch56, adc1

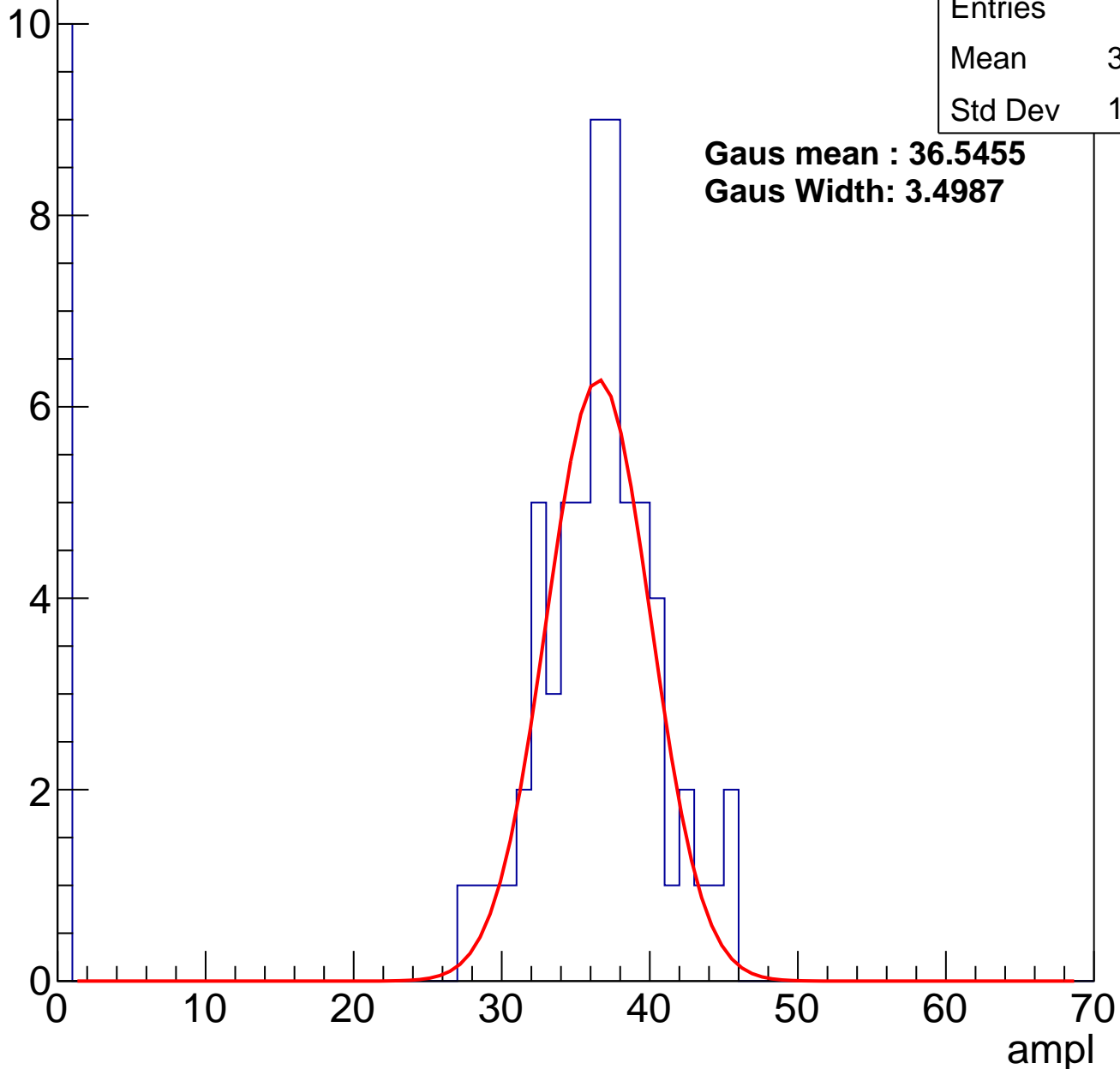
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	31.29
Std Dev	12.96

Gaus mean : 36.5455

Gaus Width: 3.4987

Entry



B1L103S, U2-ch56, adc2

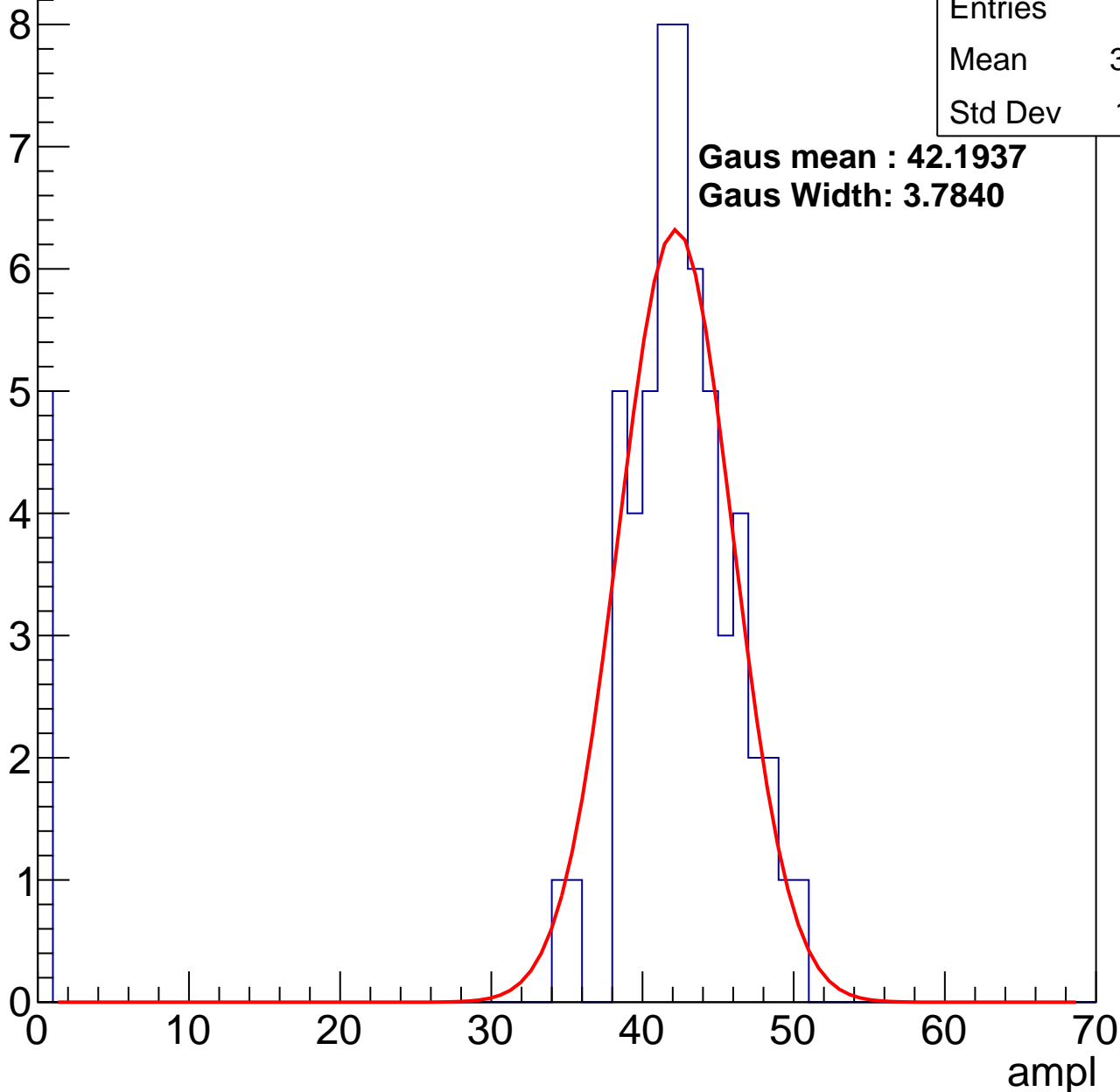
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	38.77
Std Dev	12.01

Gaus mean : 42.1937

Gaus Width: 3.7840

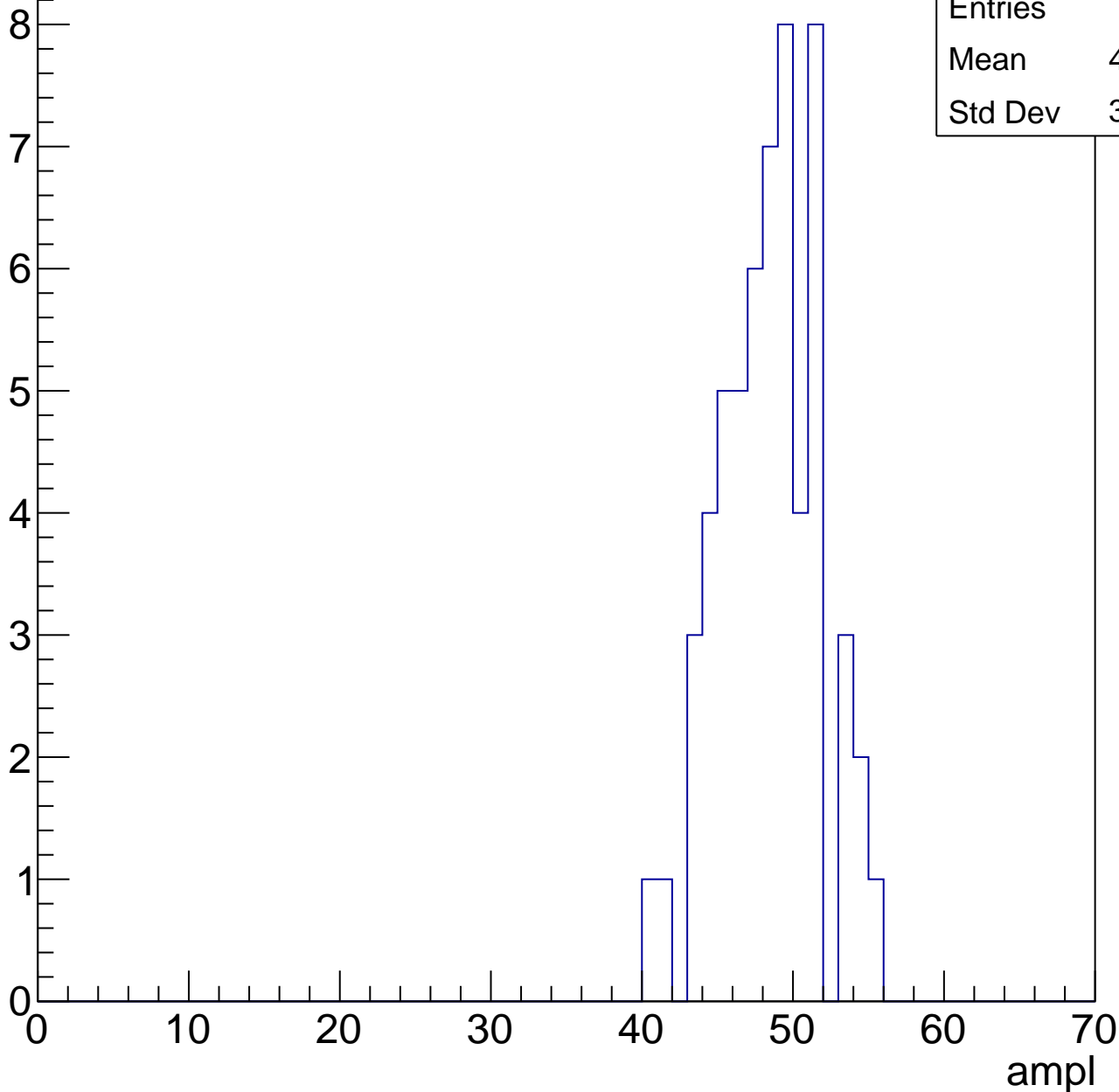


B1L103S, U2-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

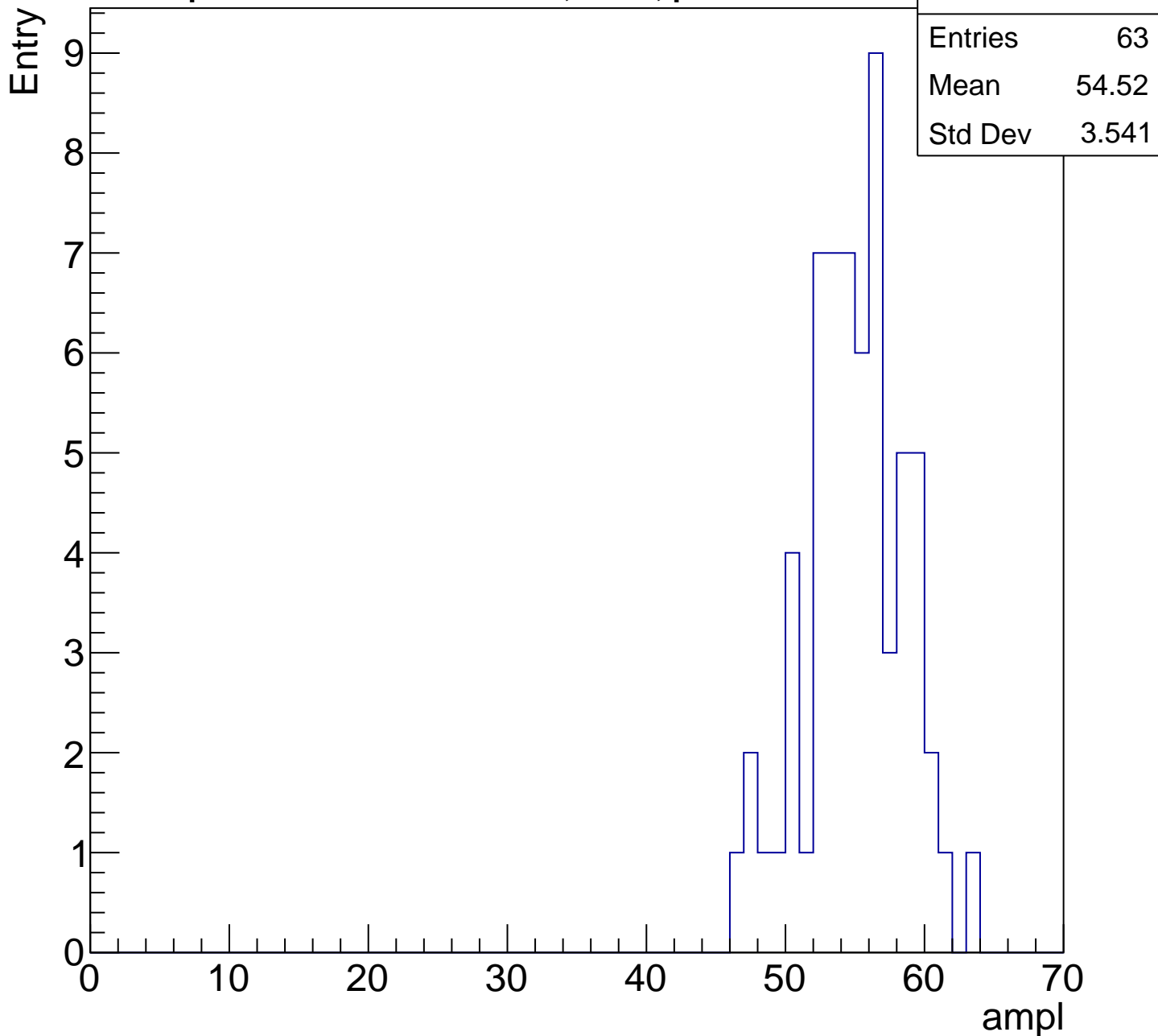
Entry

Entries	58
Mean	47.95
Std Dev	3.256



B1L103S, U2-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

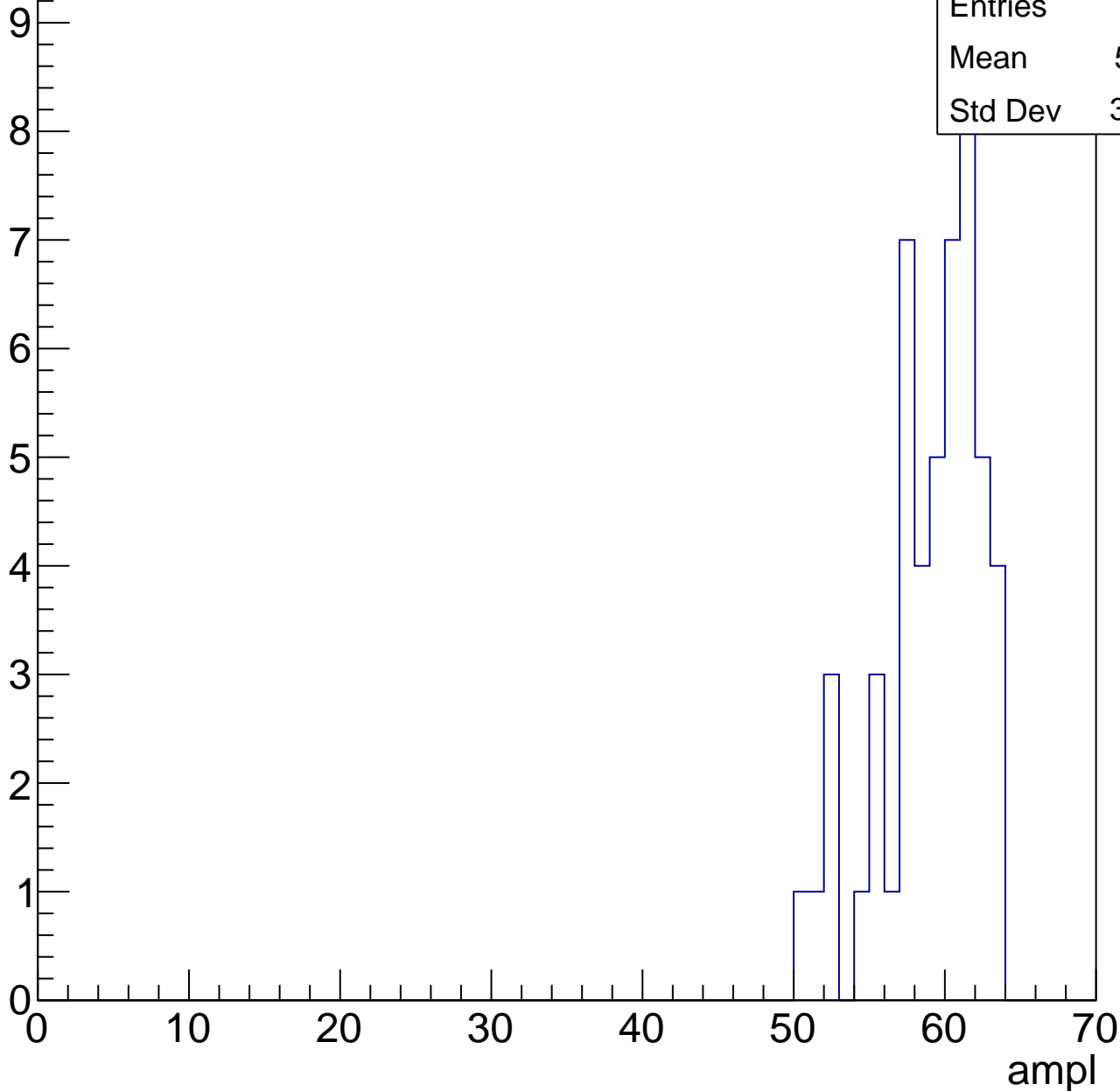


B1L103S, U2-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.61
Std Dev	3.284

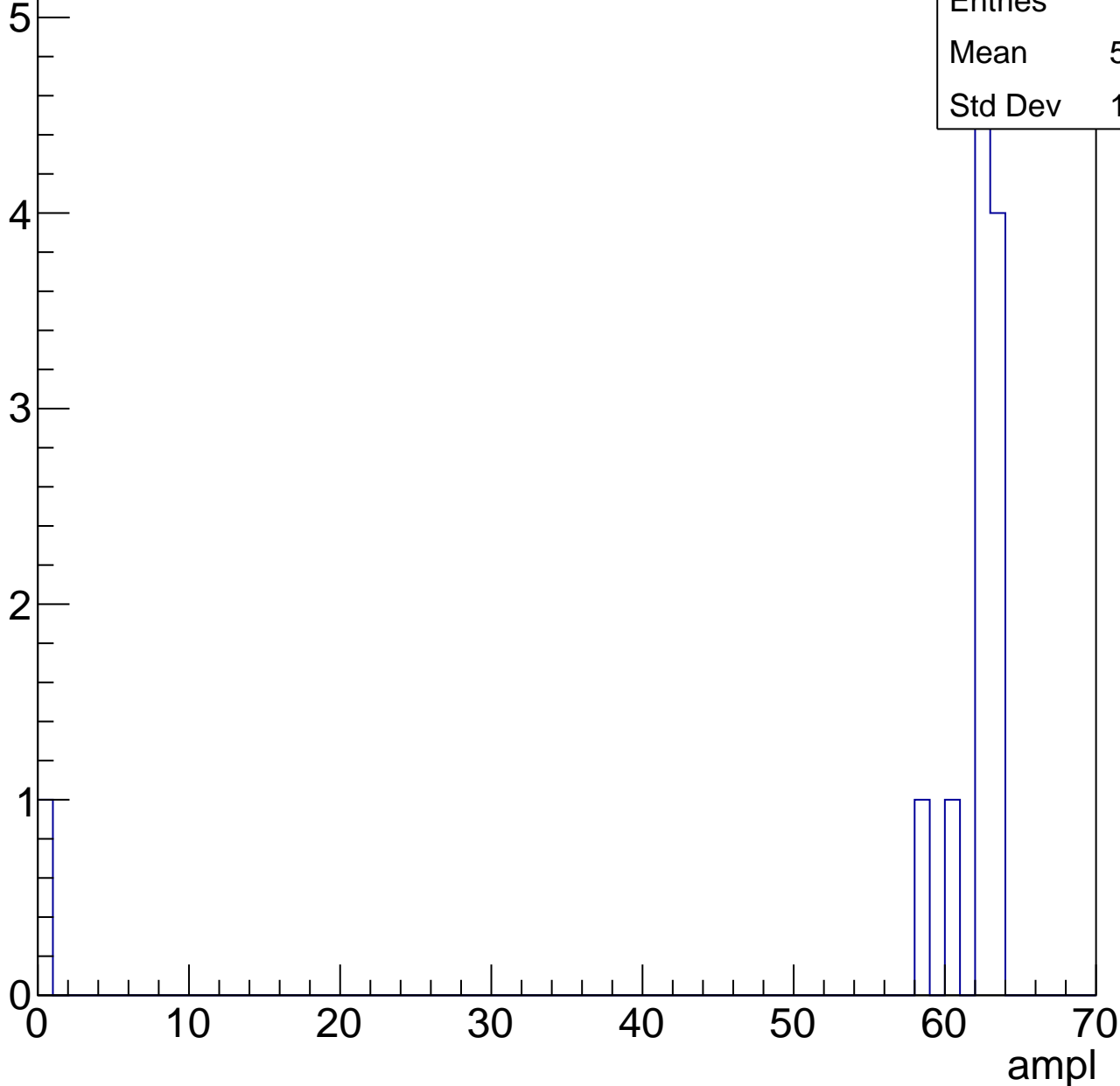


B1L103S, U2-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	56.67
Std Dev	17.14



B1L103S, U2-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U2-ch57, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	24.33
Std Dev	12

Gaus mean : 30.4679

Gaus Width: 3.6047

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

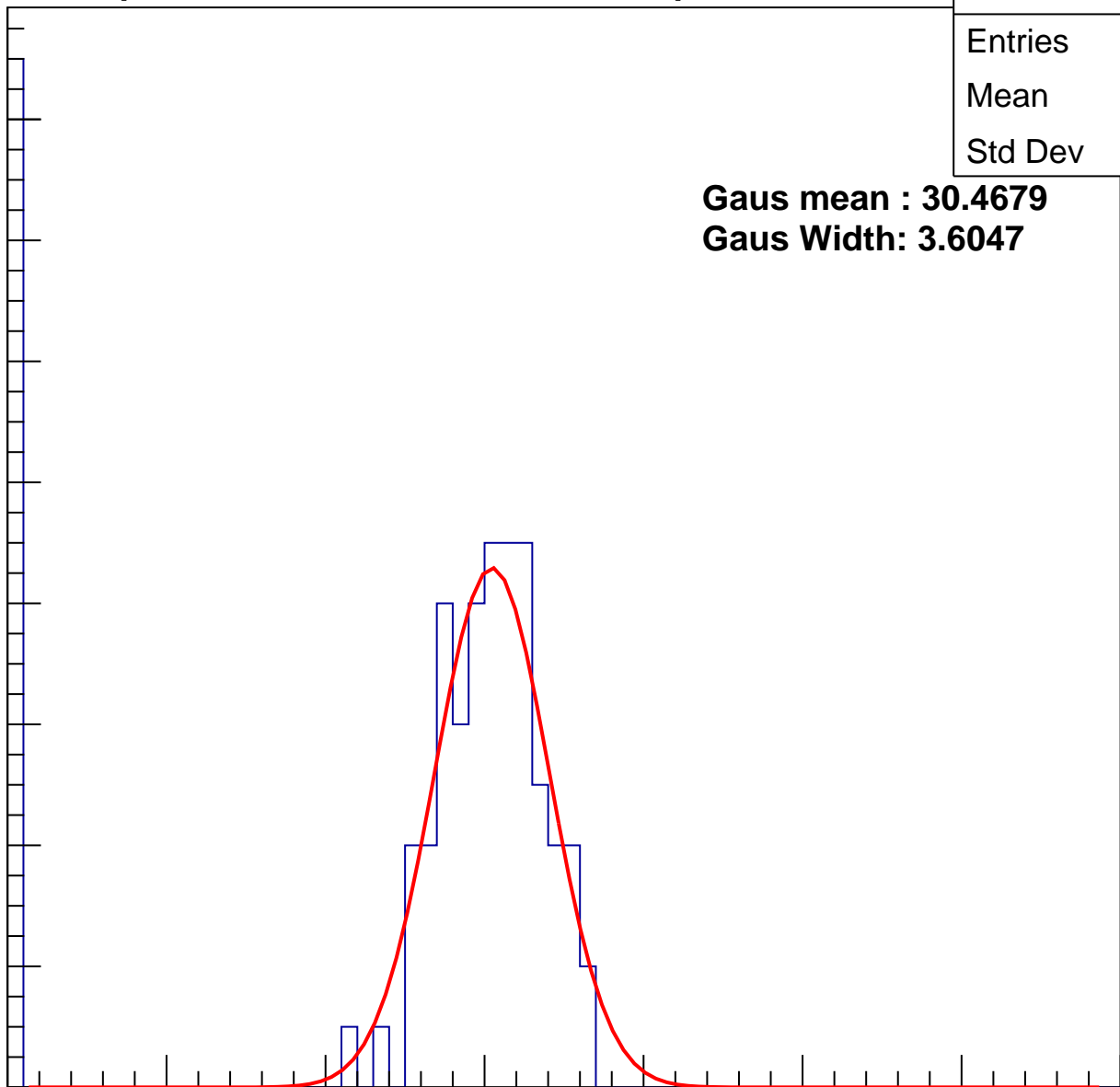
40

50

60

70

ampl

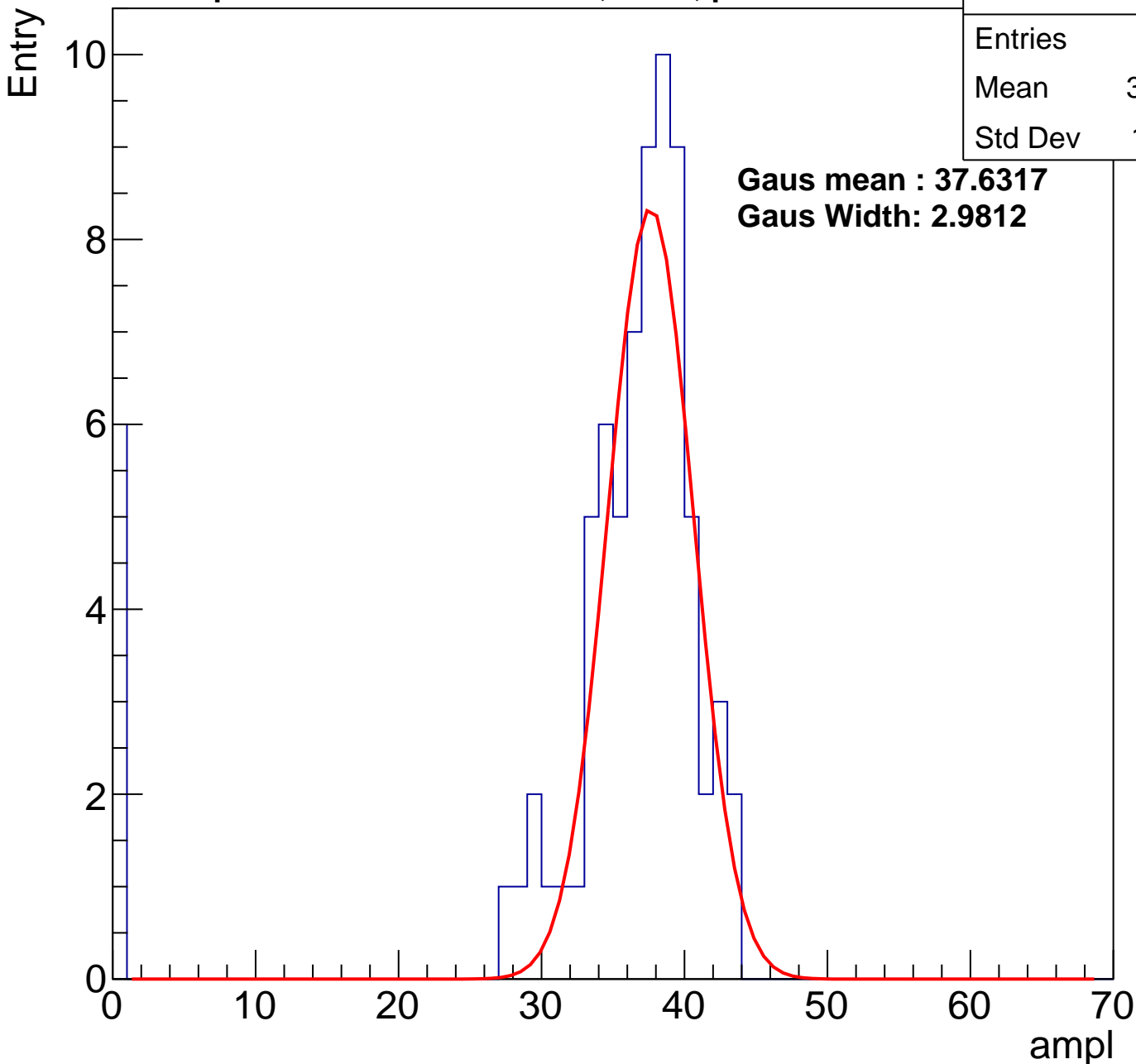


B1L103S, U2-ch57, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	33.68
Std Dev	10.41

Gaus mean : 37.6317
Gaus Width: 2.9812



B1L103S, U2-ch57, adc2

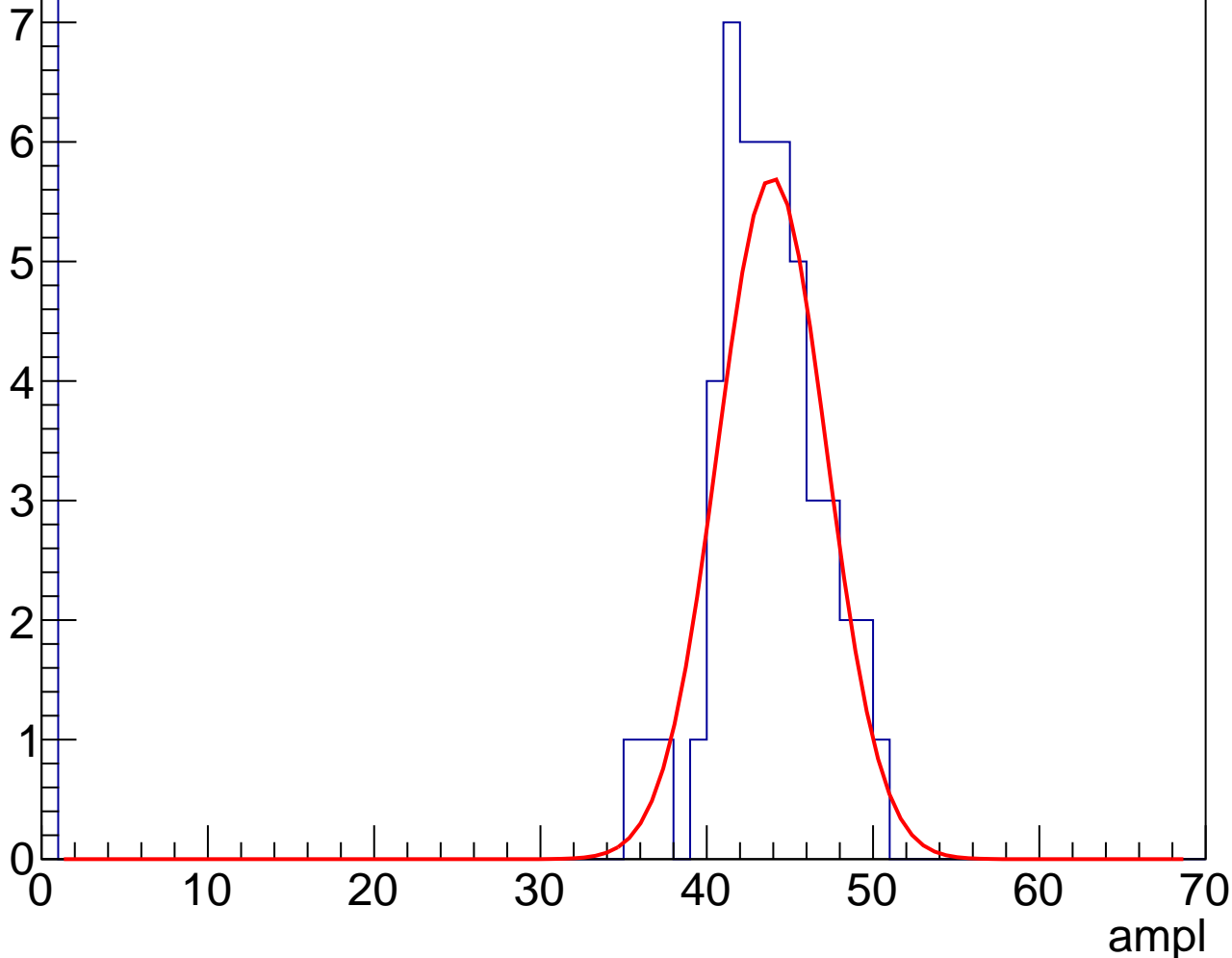
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	36.48
Std Dev	15.91

Gaus mean : 43.9241

Gaus Width: 3.2567

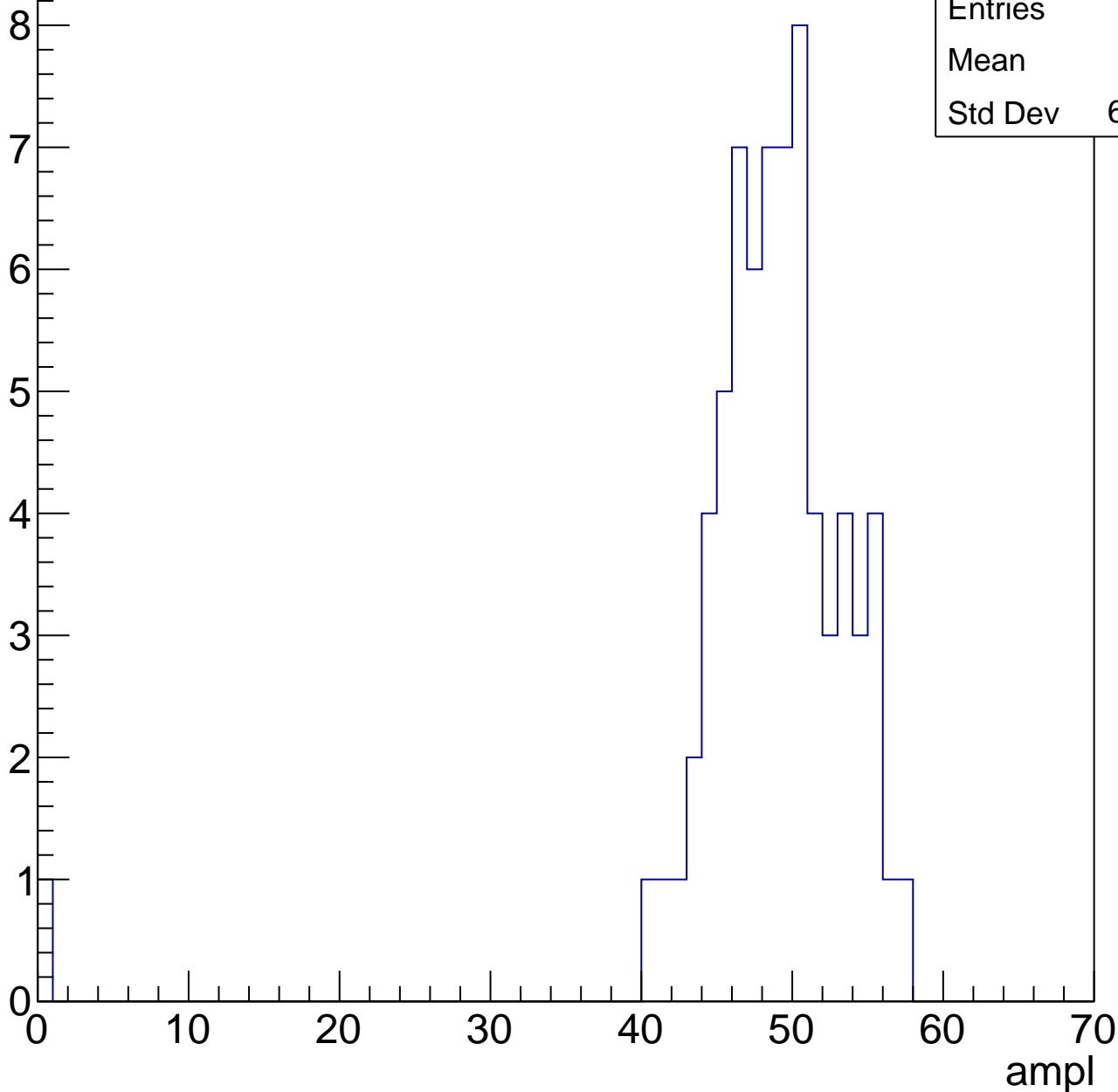


B1L103S, U2-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	48
Std Dev	6.885

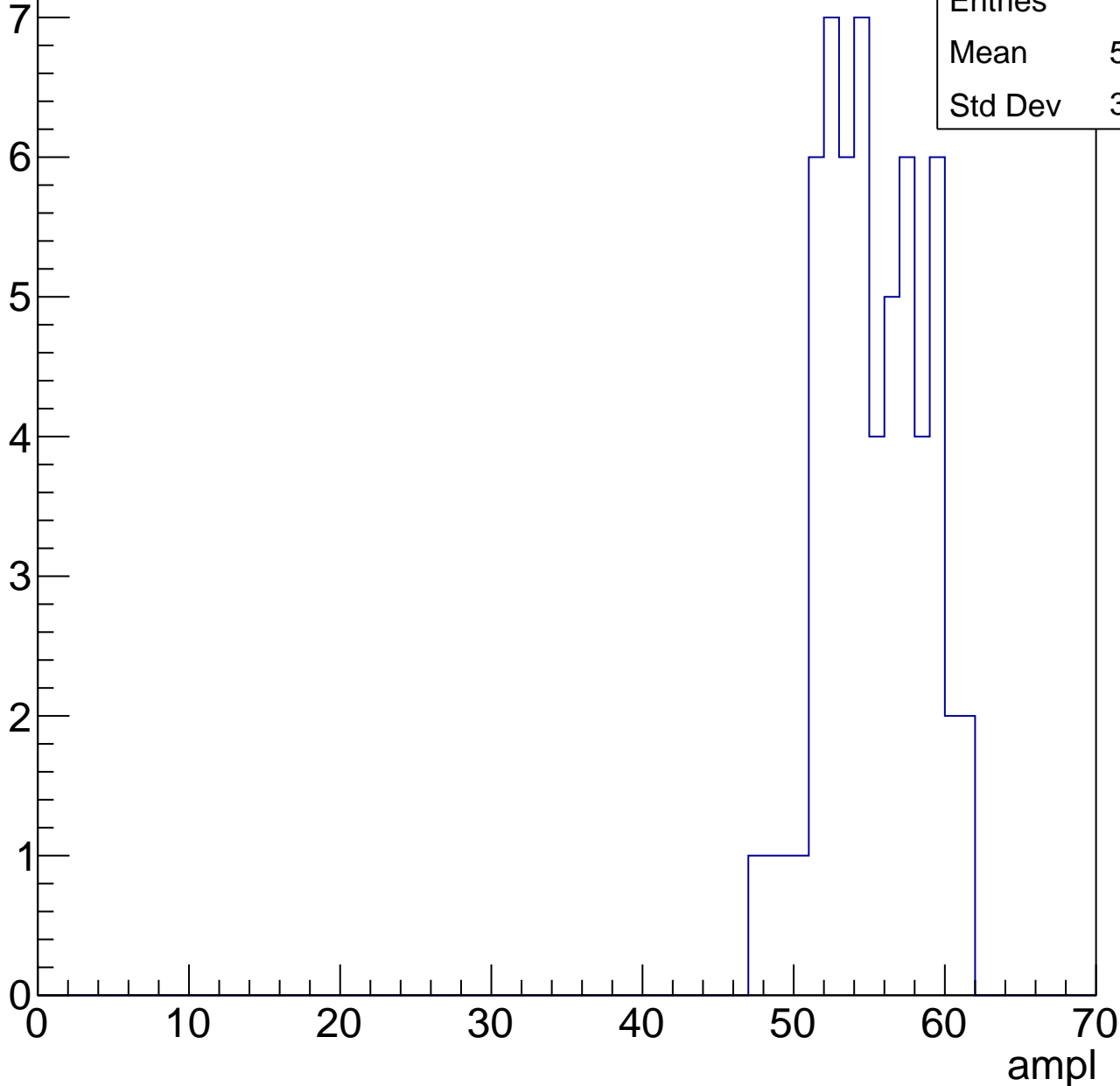


B1L103S, U2-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.75
Std Dev	3.302

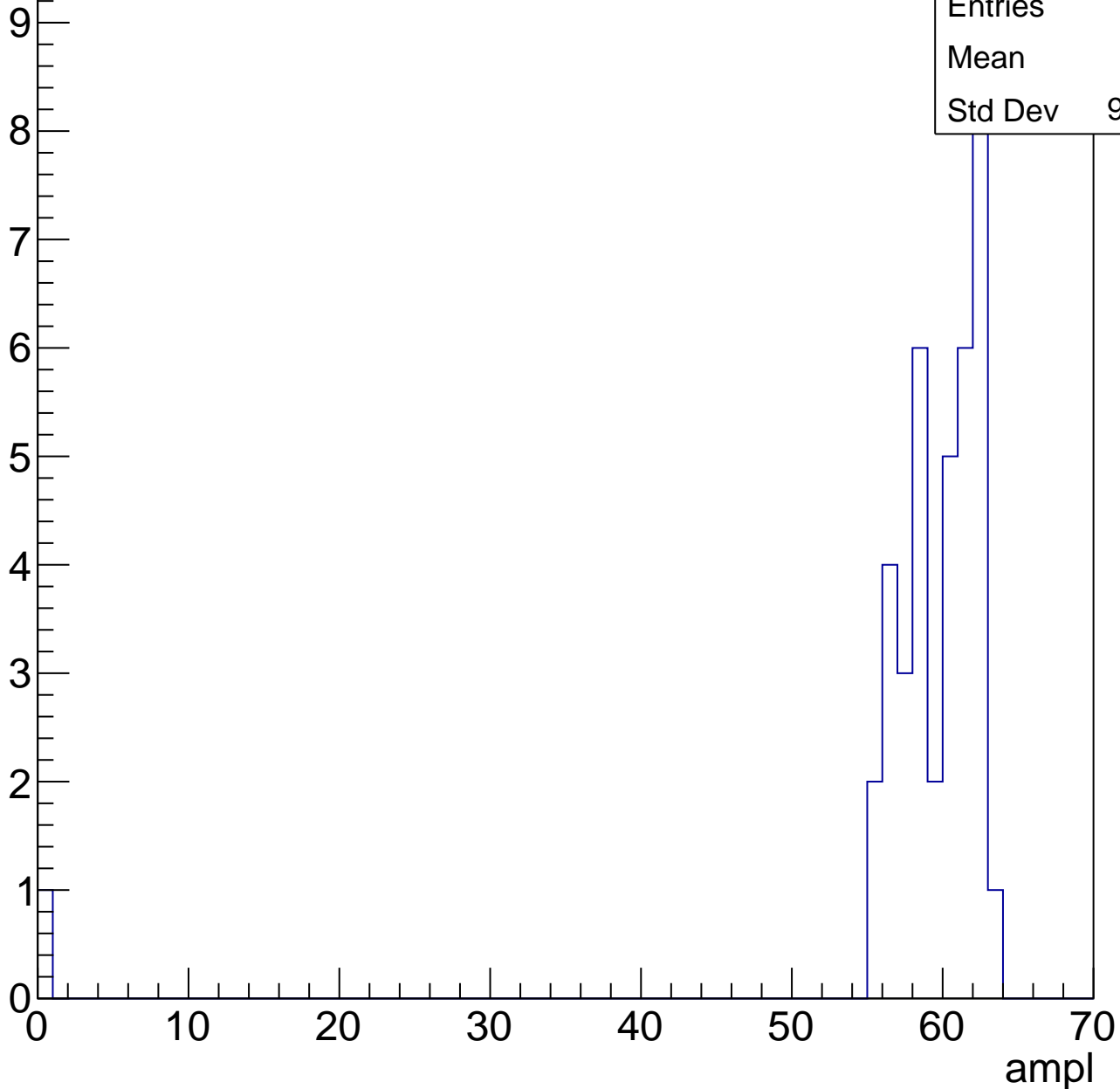


B1L103S, U2-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	57.9
Std Dev	9.668

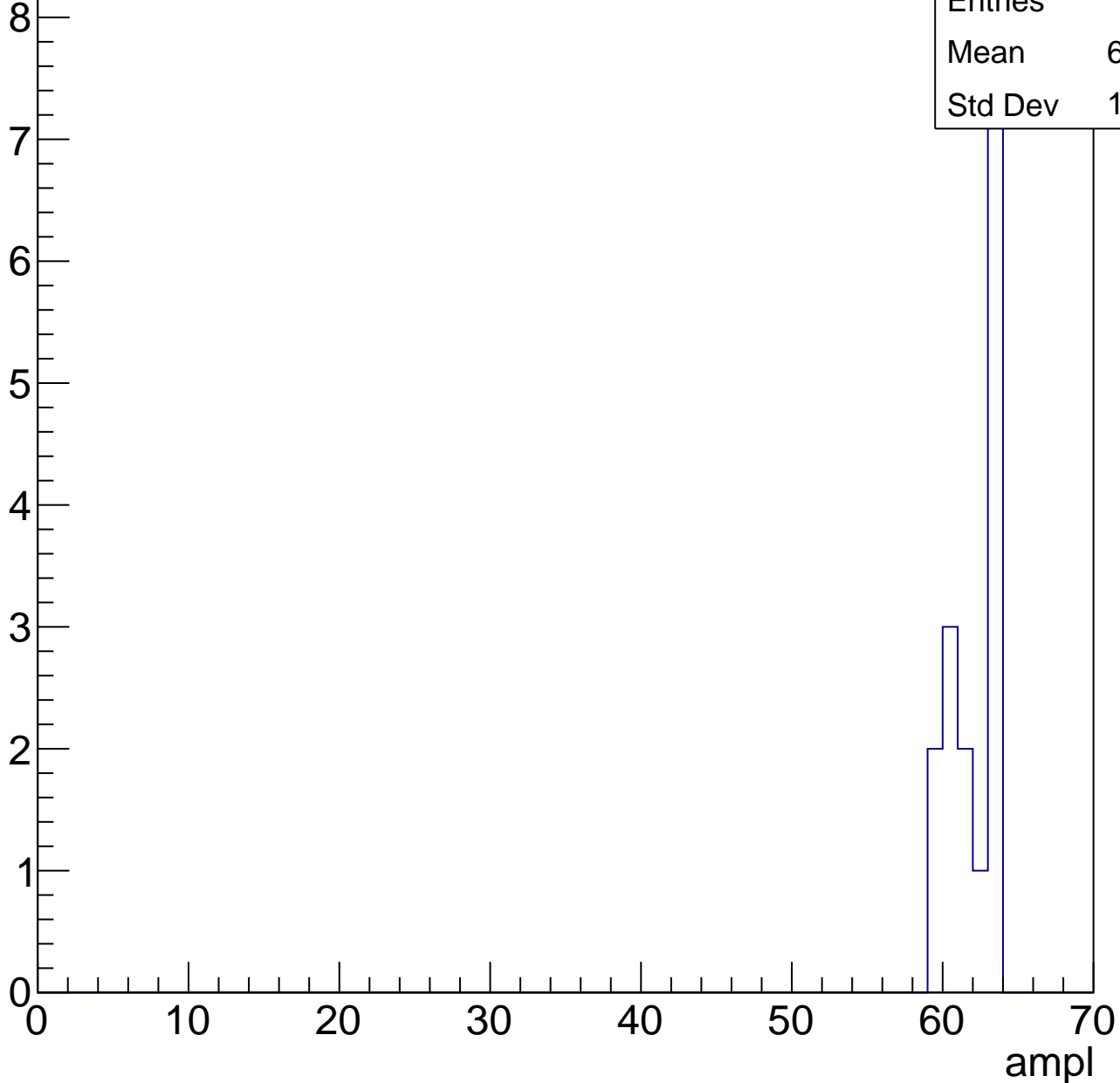


B1L103S, U2-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.62
Std Dev	1.536



B1L103S, U2-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



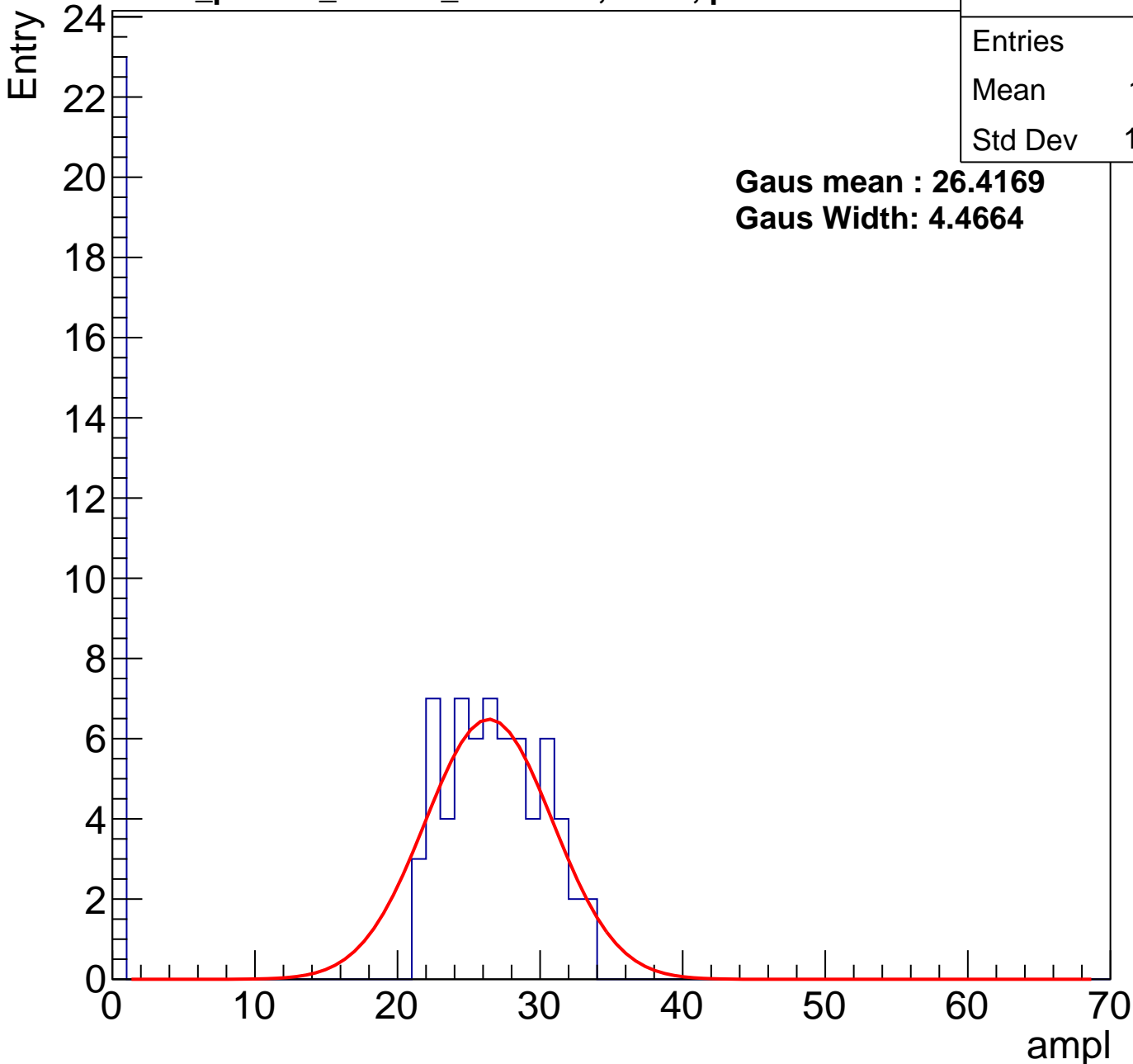
B1L103S, U2-ch58, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	19.41
Std Dev	11.97

Gaus mean : 26.4169

Gaus Width: 4.4664



B1L103S, U2-ch58, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	28.96
Std Dev	11.34

Gaus mean : 33.8437

Gaus Width: 3.2760

Entry

10

8

6

4

2

0

0

10

20

30

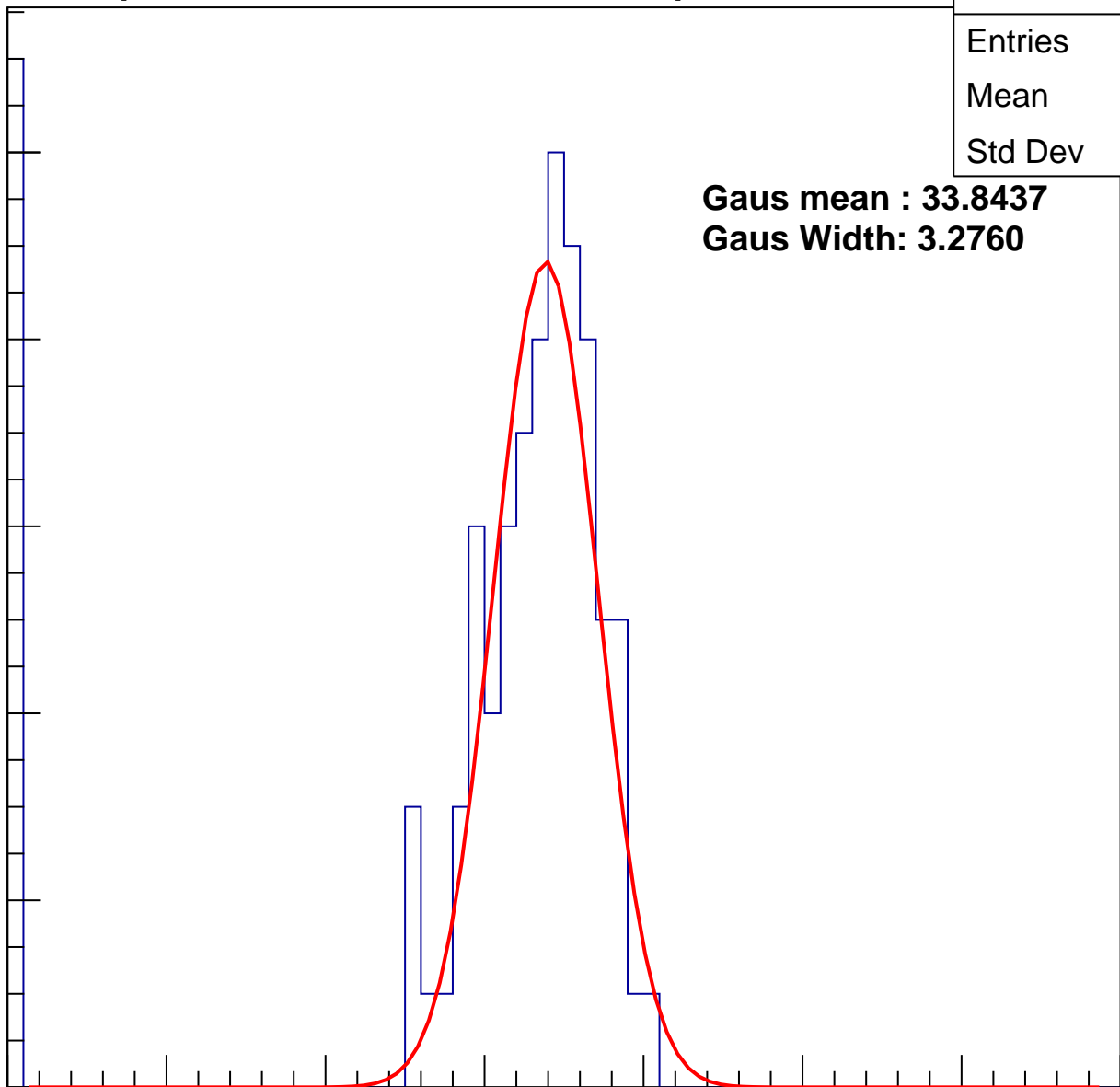
40

50

60

70

ampl



B1L103S, U2-ch58, adc2

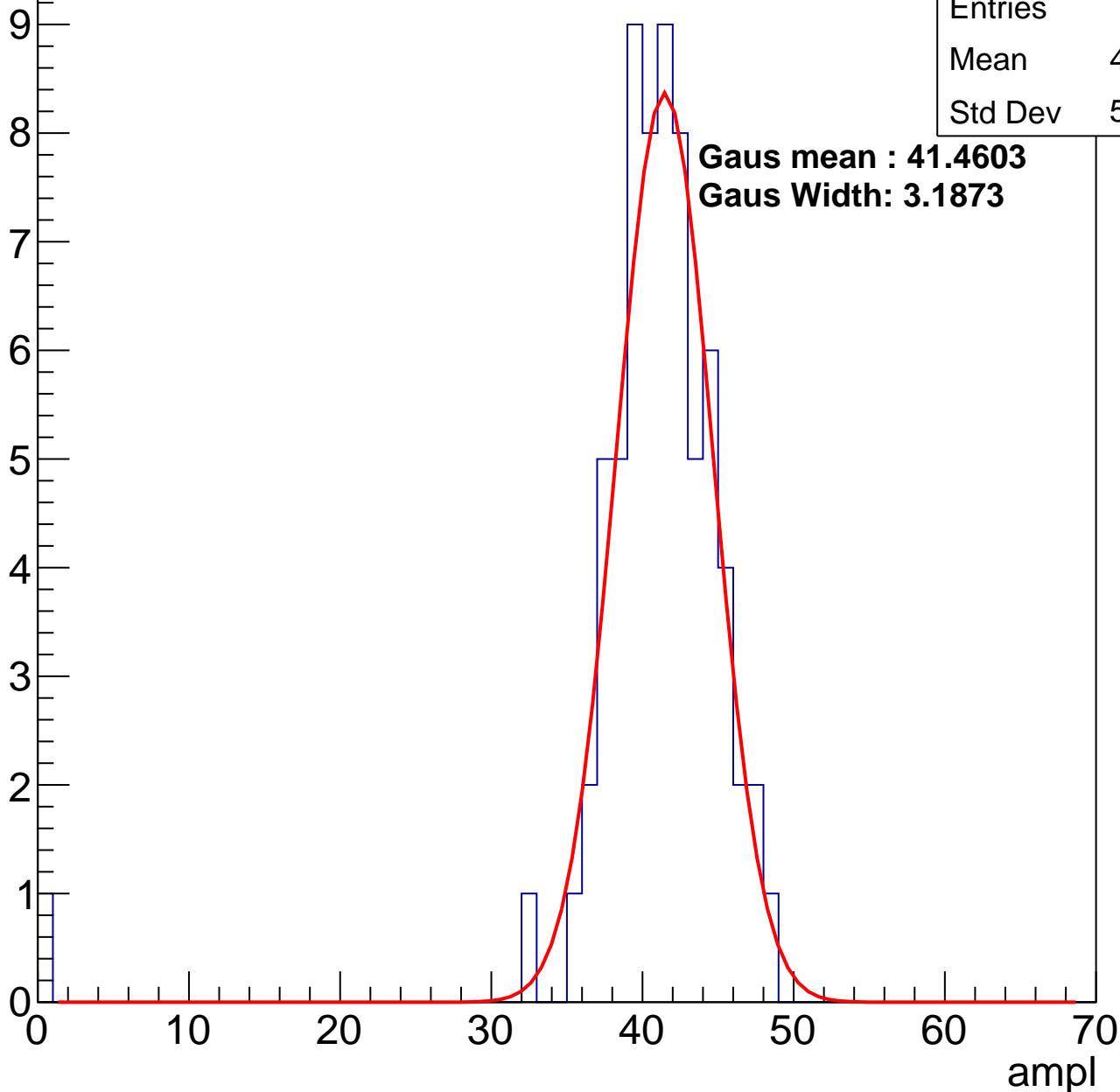
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.33
Std Dev	5.778

Gaus mean : 41.4603

Gaus Width: 3.1873

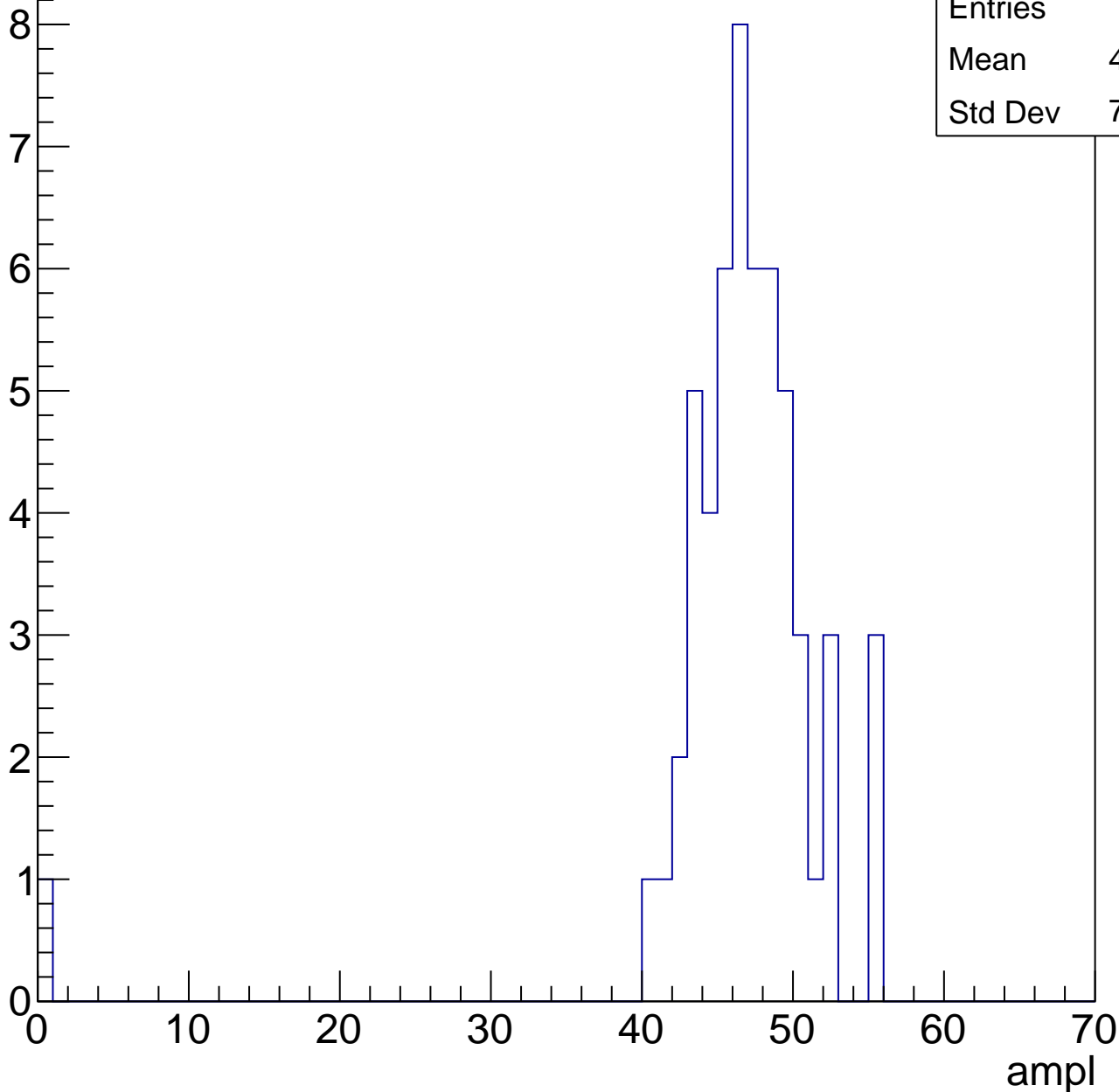


B1L103S, U2-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	46.02
Std Dev	7.103

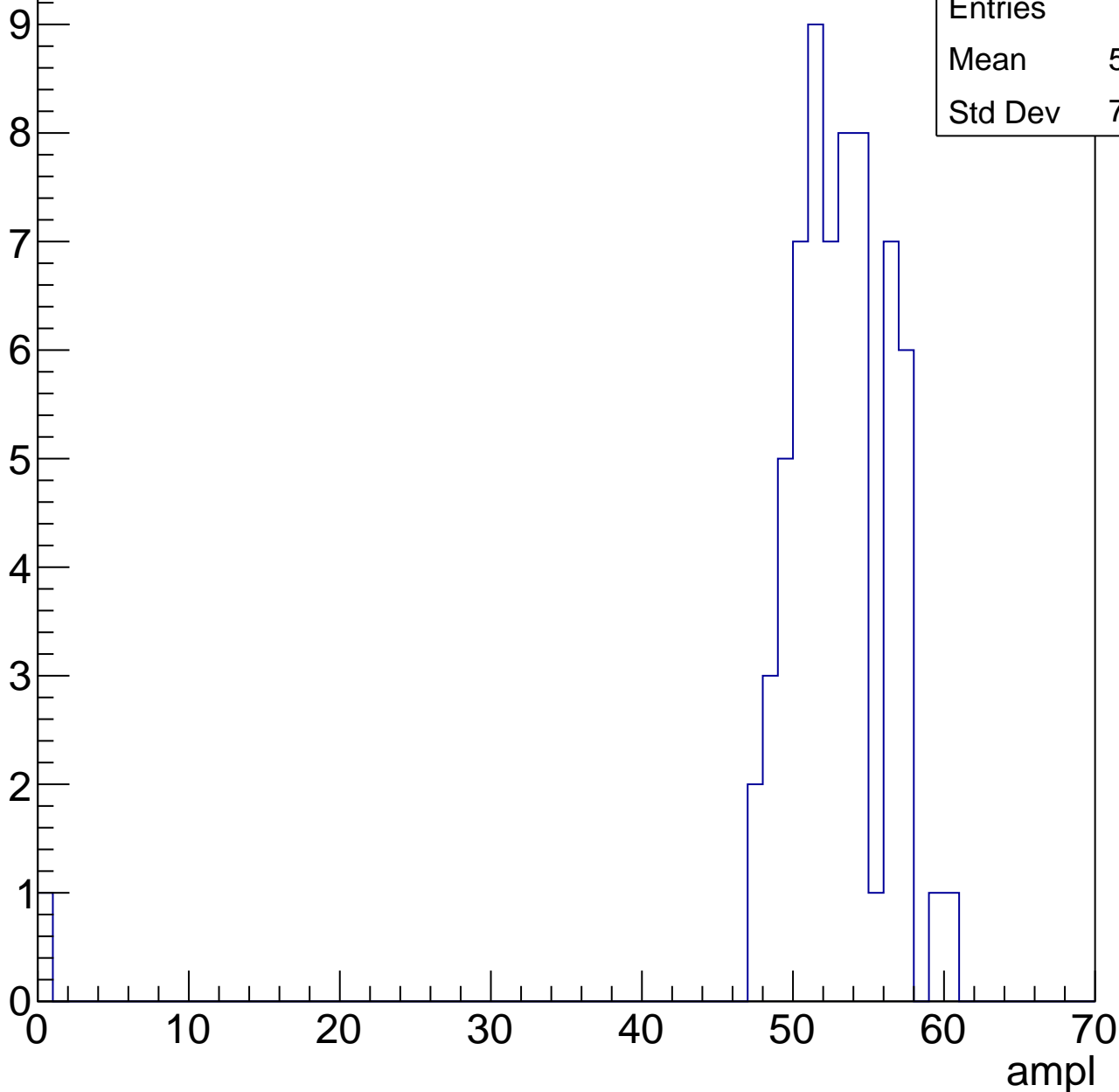


B1L103S, U2-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	51.82
Std Dev	7.077

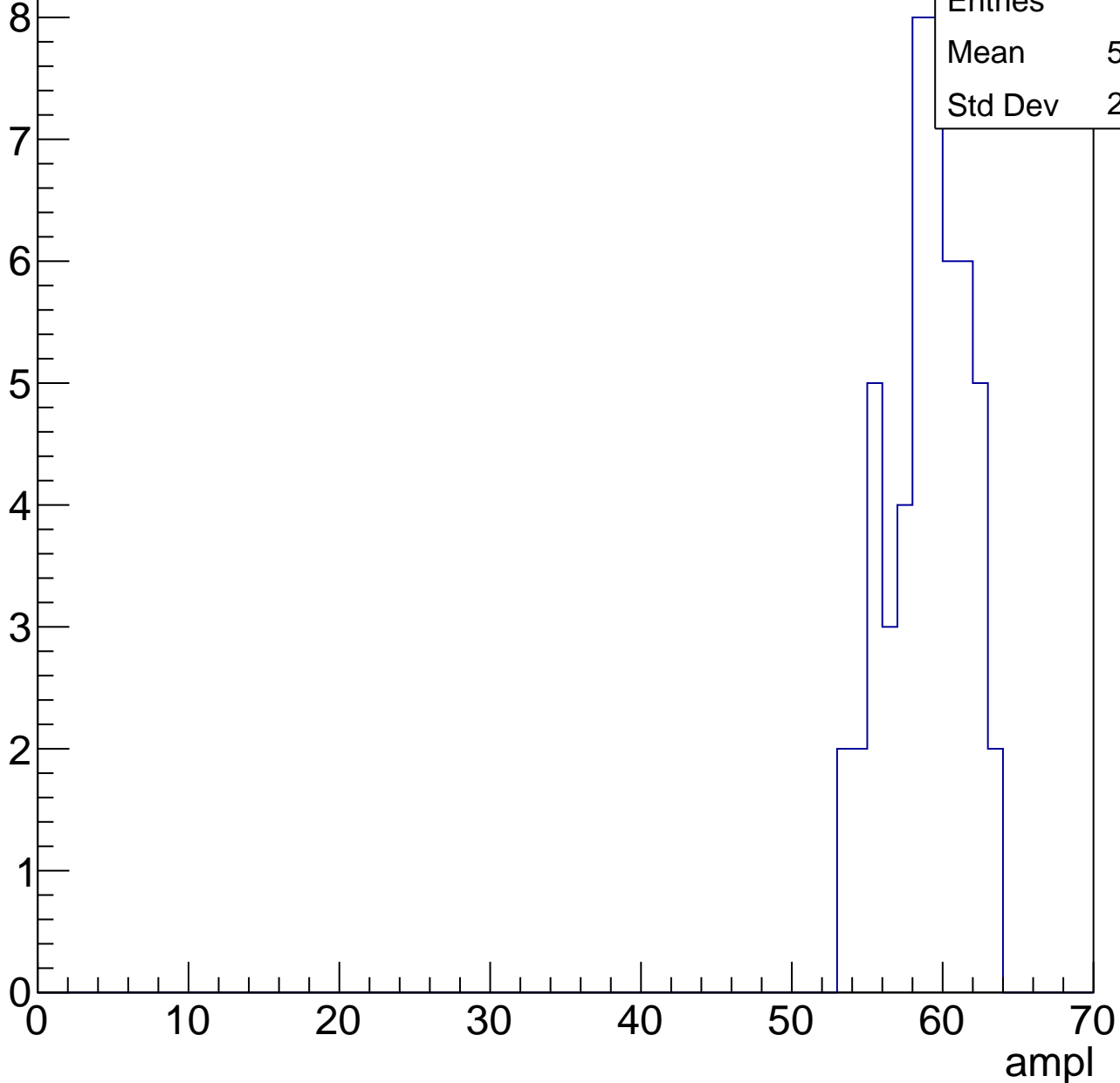


B1L103S, U2-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.49
Std Dev	2.607

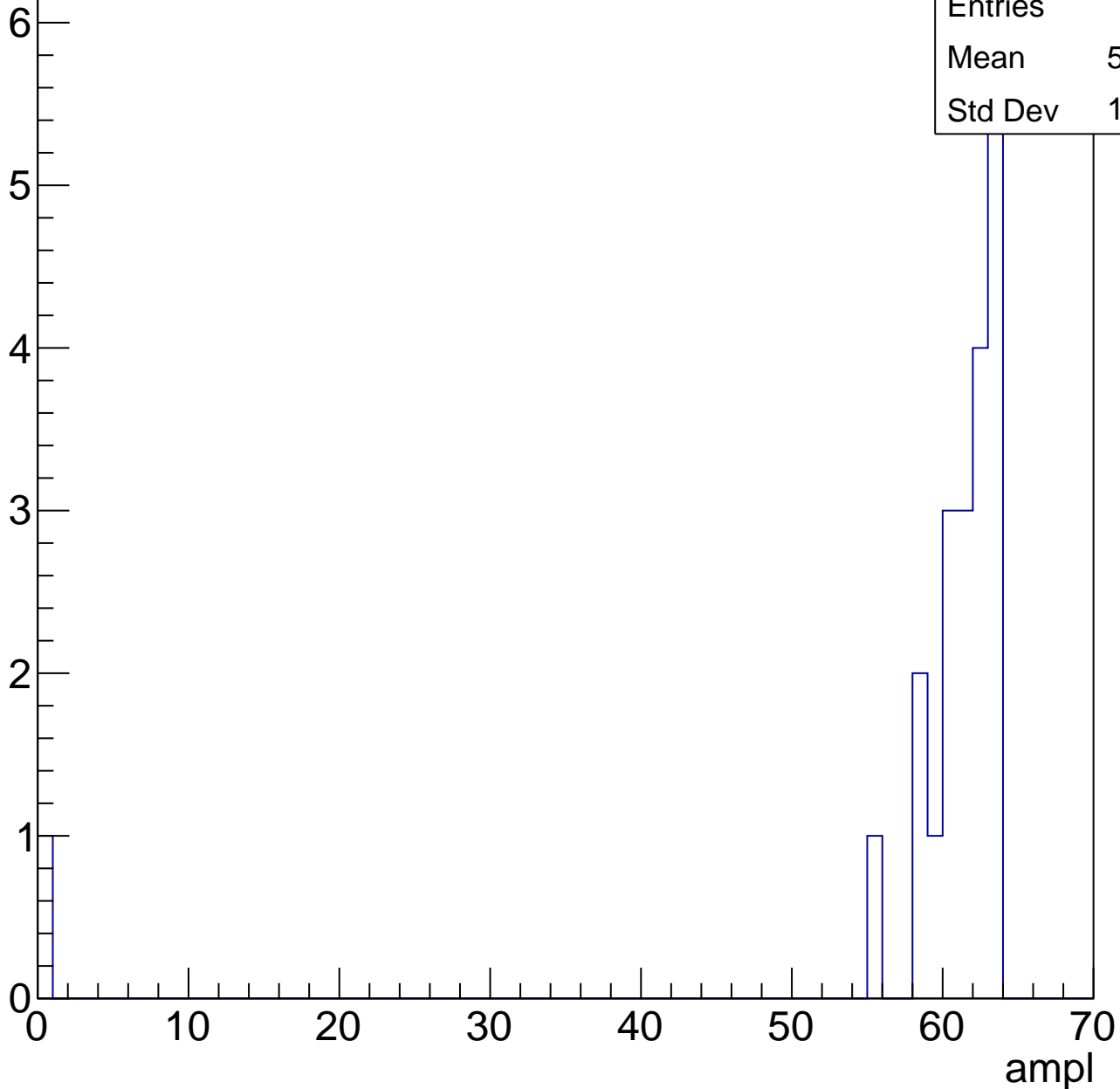


B1L103S, U2-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.05
Std Dev	13.14

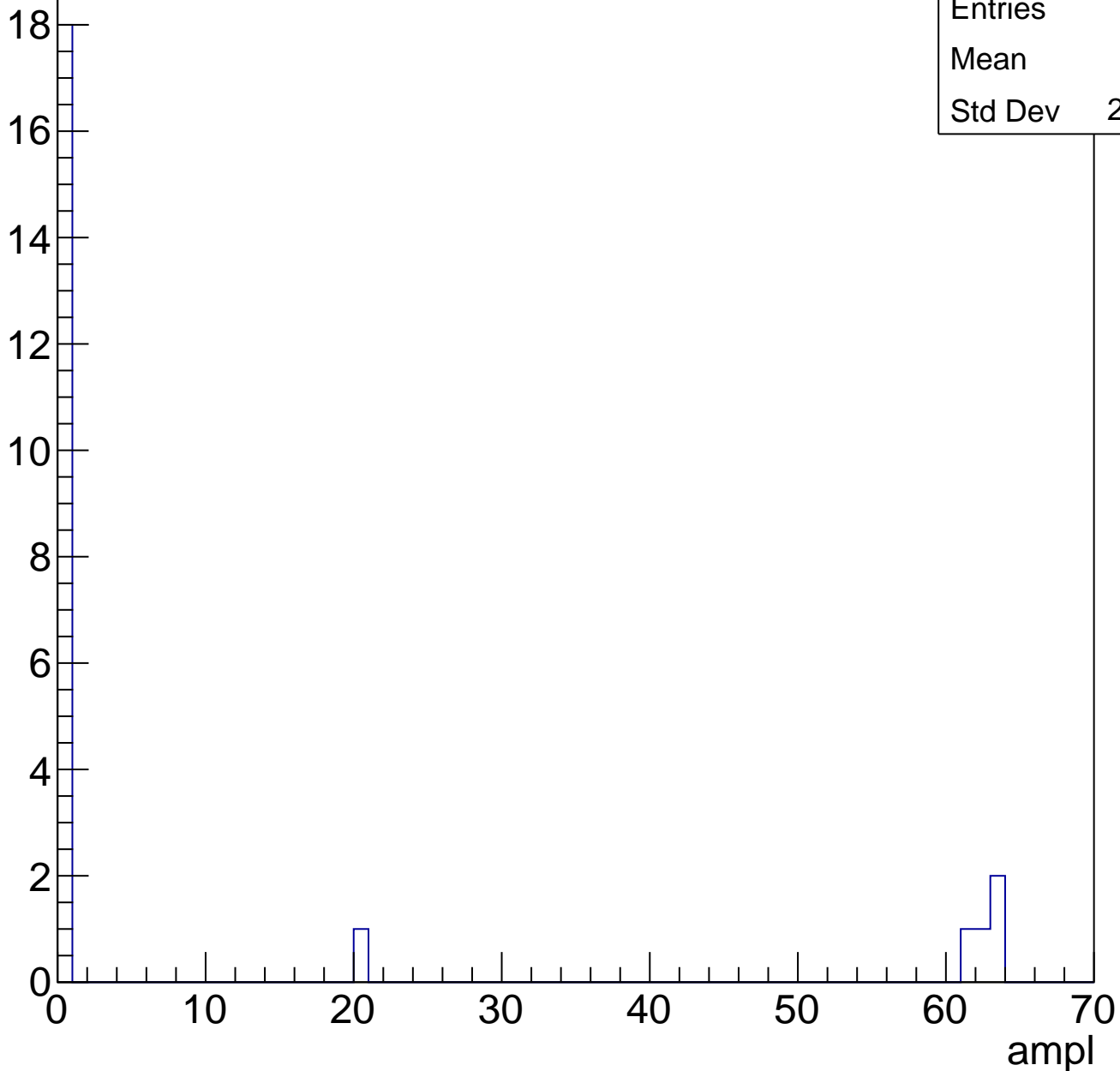


B1L103S, U2-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	11.7
Std Dev	23.55

Entry



B1L103S, U2-ch59, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	25.03
Std Dev	10.82

Gaus mean : 29.6884

Gaus Width: 3.9100

Entry

10

8

6

4

2

0

0

10

20

30

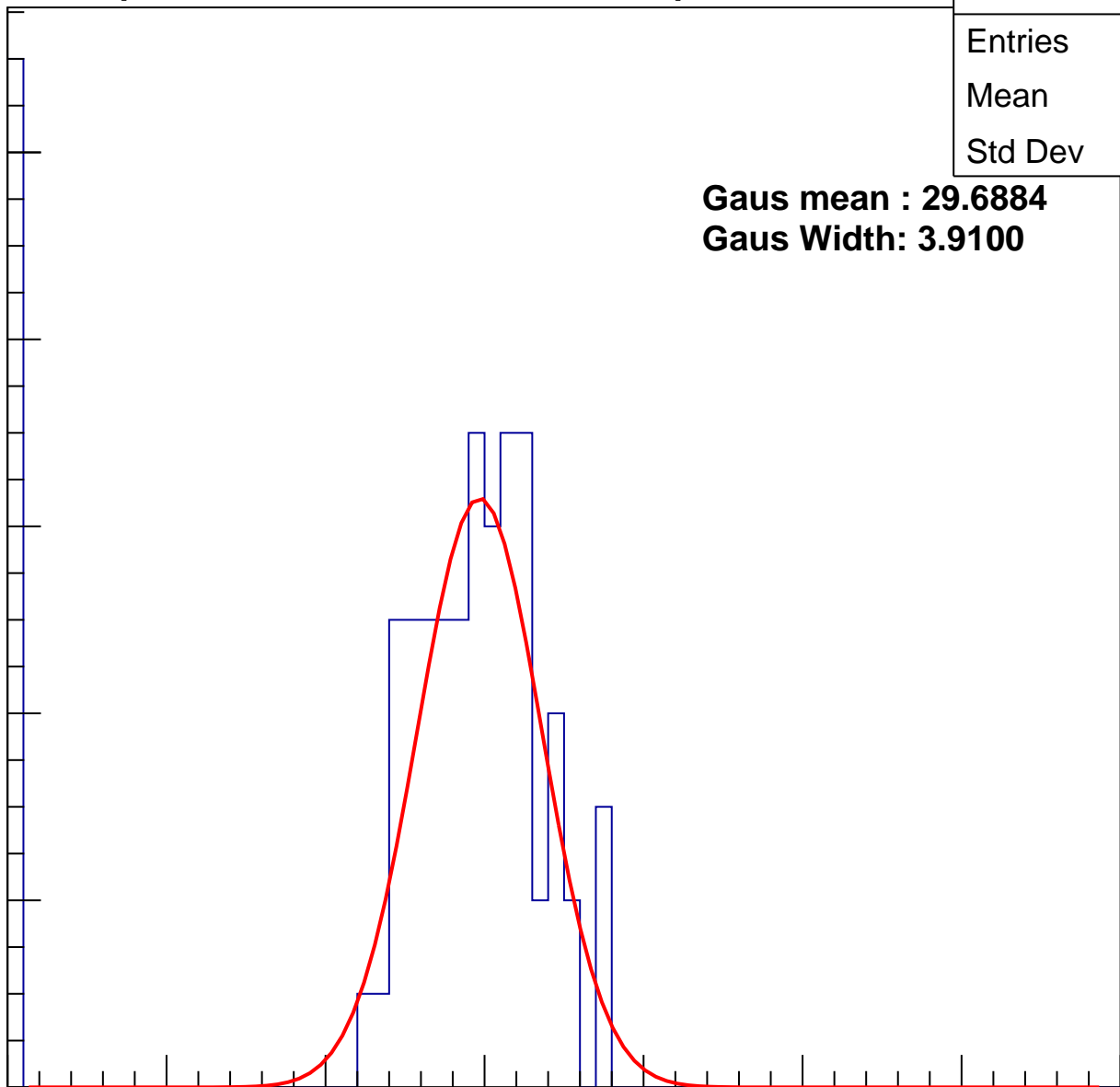
40

50

60

70

ampl



B1L103S, U2-ch59, adc1

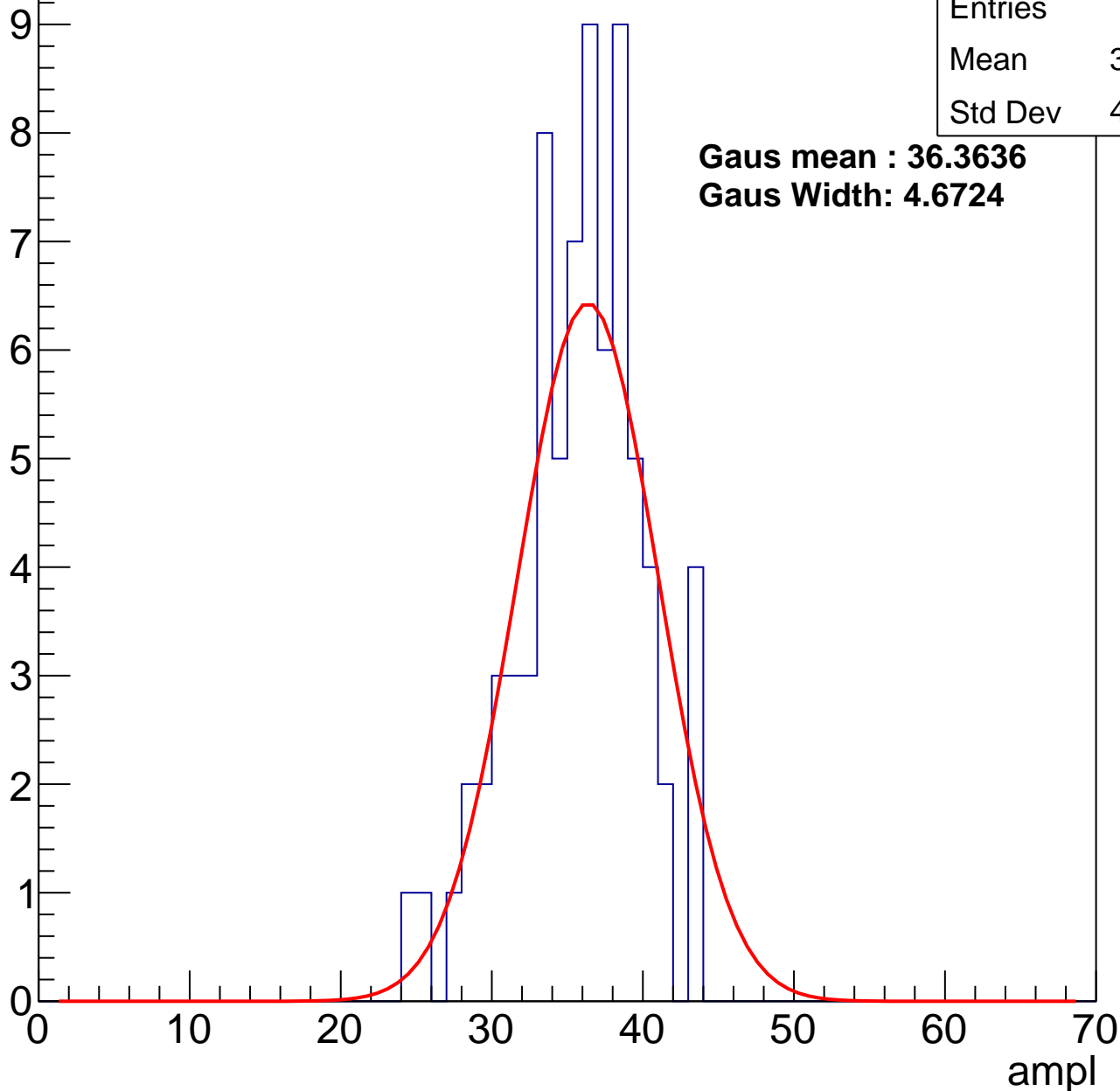
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	35.27
Std Dev	4.097

Gaus mean : 36.3636

Gaus Width: 4.6724

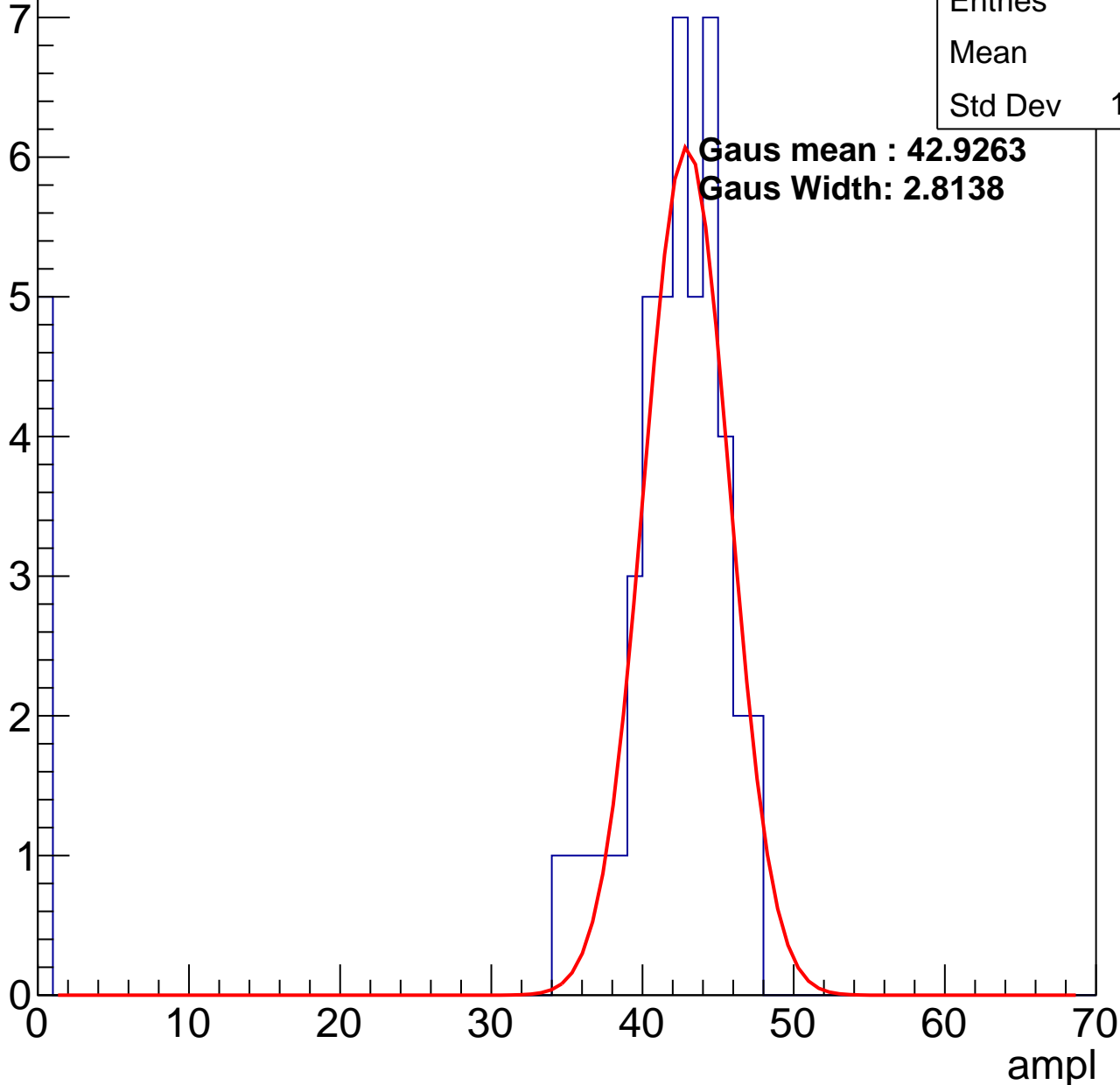


B1L103S, U2-ch59, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

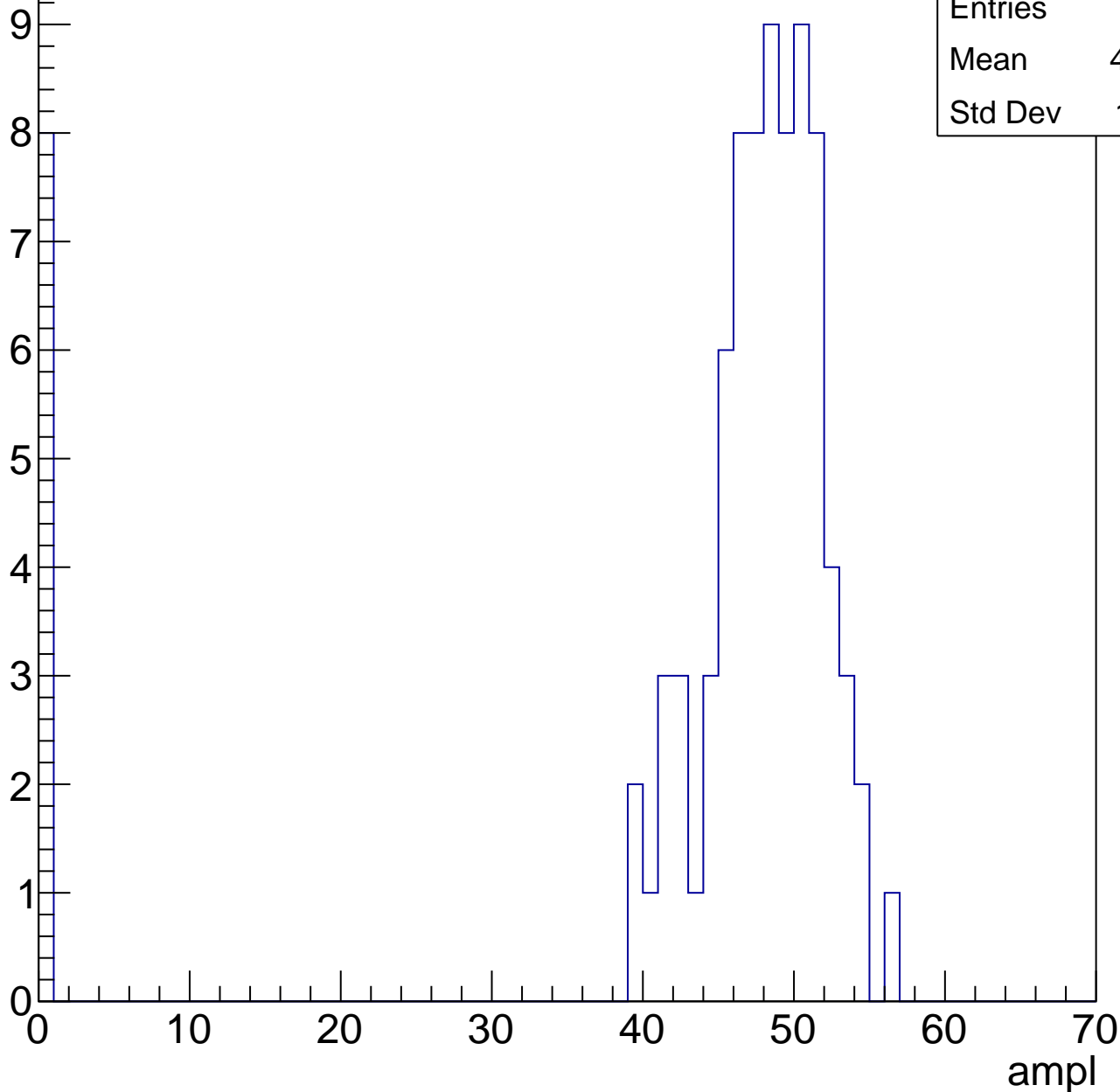
Entries	50
Mean	37.7
Std Dev	12.88



B1L103S, U2-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

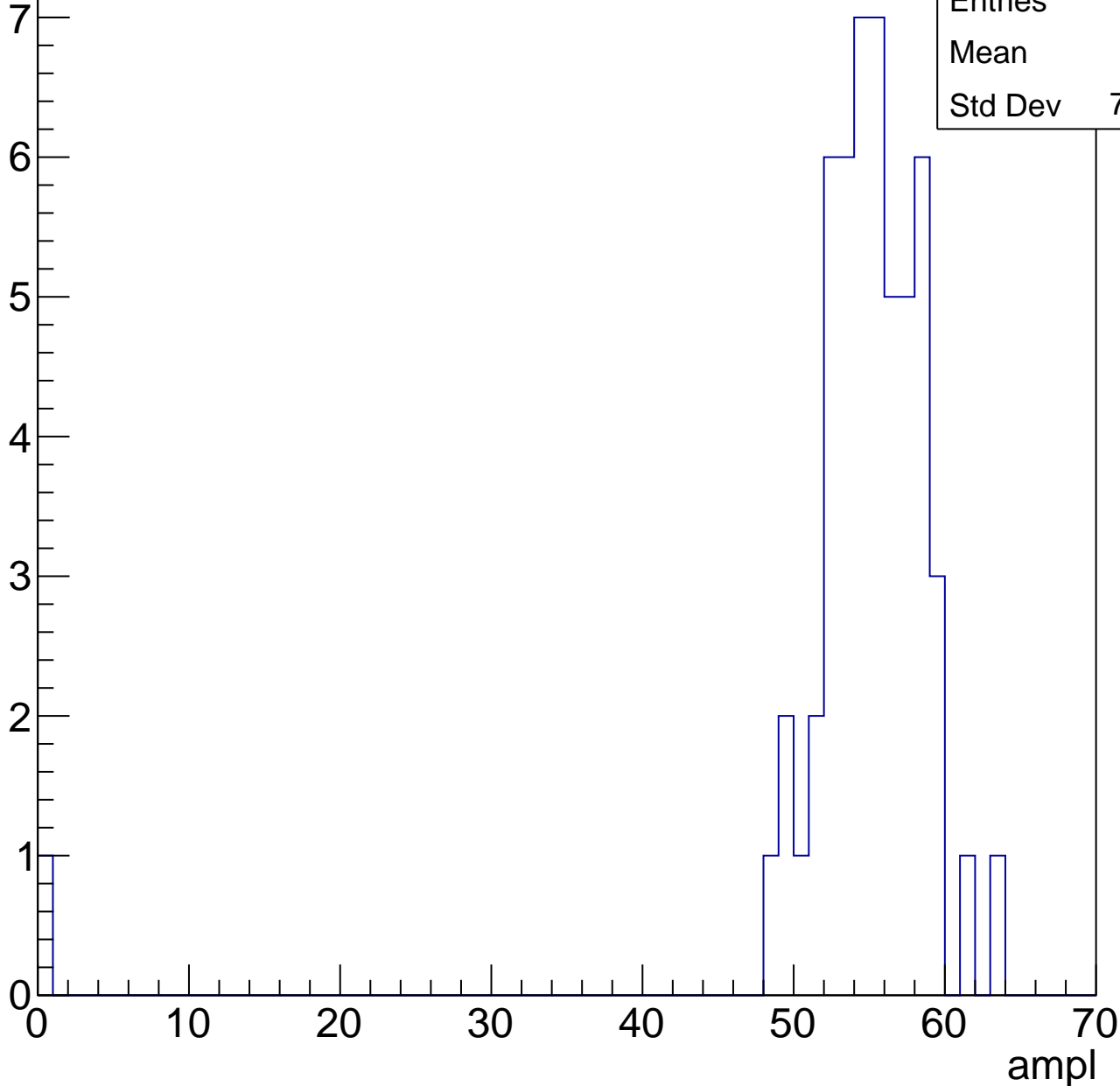


B1L103S, U2-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

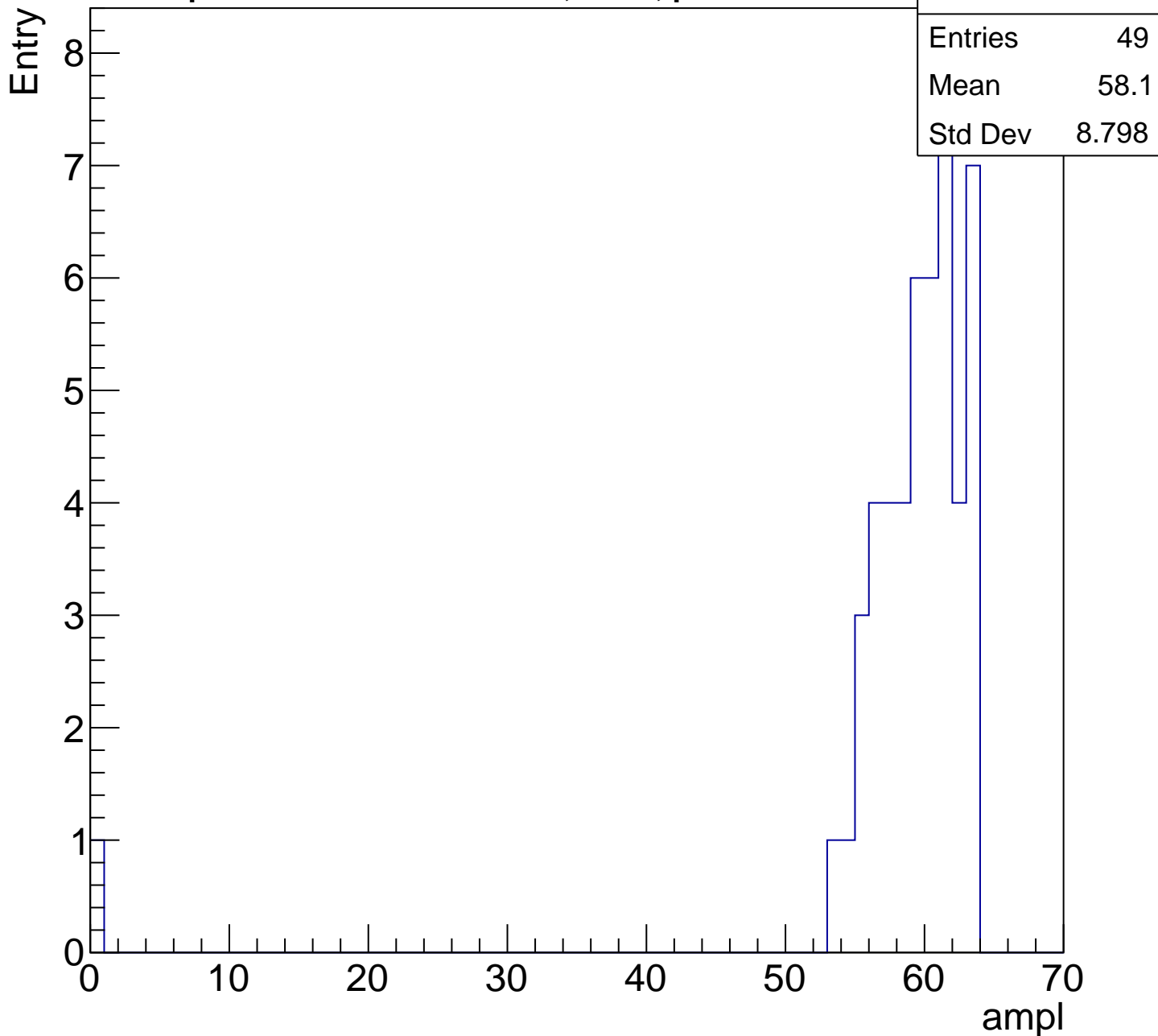
Entry

Entries	54
Mean	53.8
Std Dev	7.978



B1L103S, U2-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

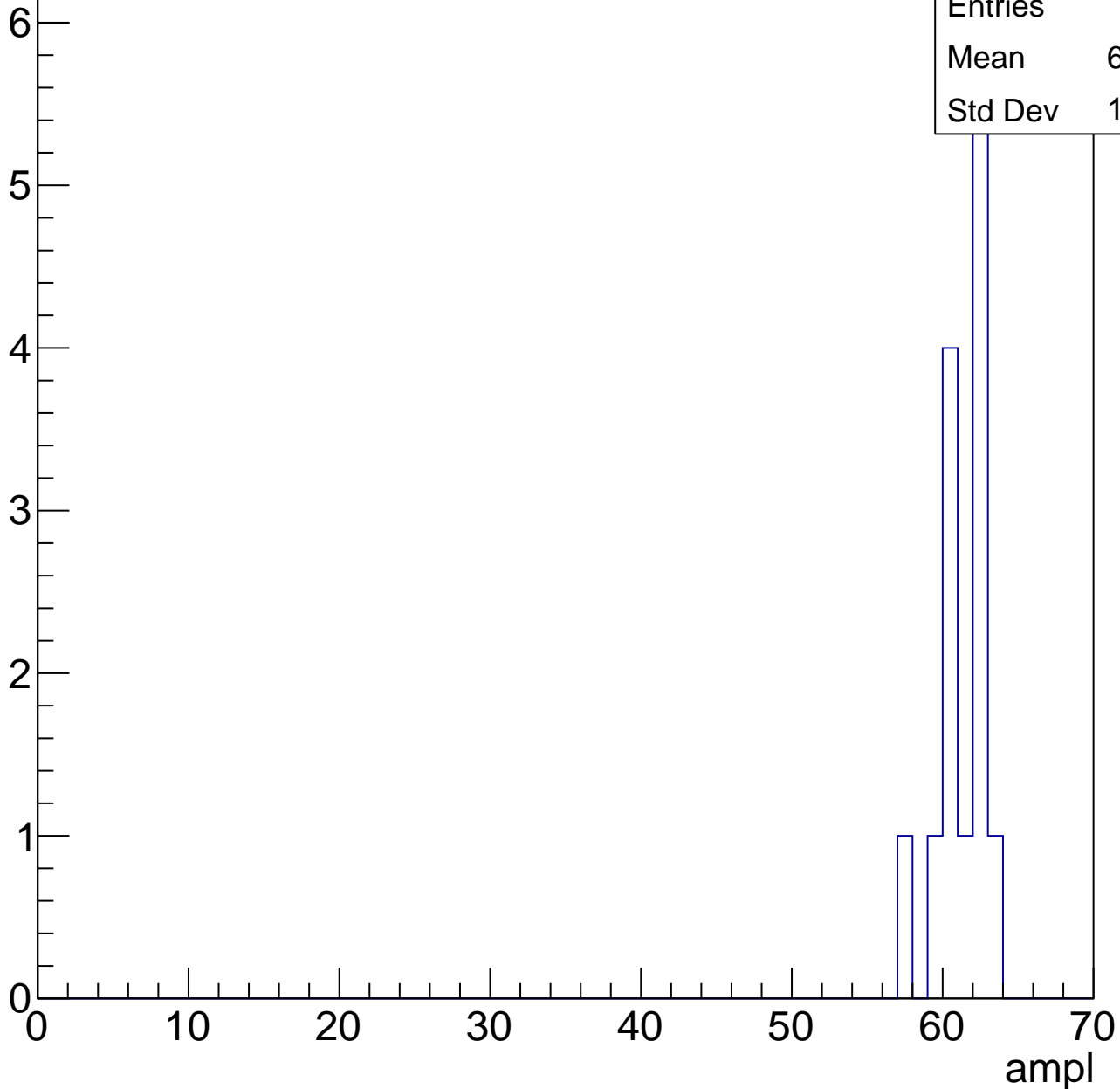


B1L103S, U2-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

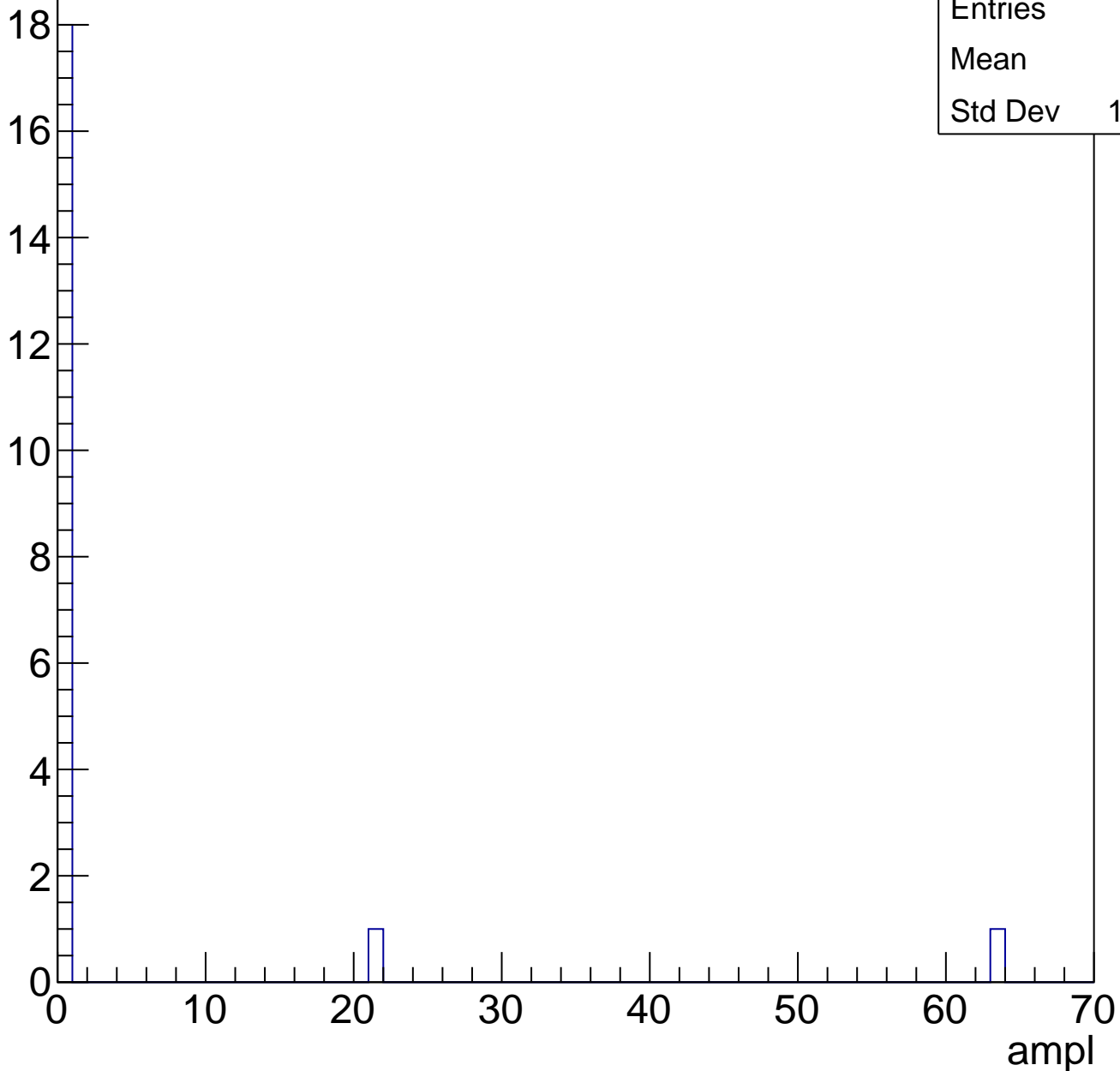
Entries	14
Mean	60.86
Std Dev	1.552



B1L103S, U2-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch60, adc0

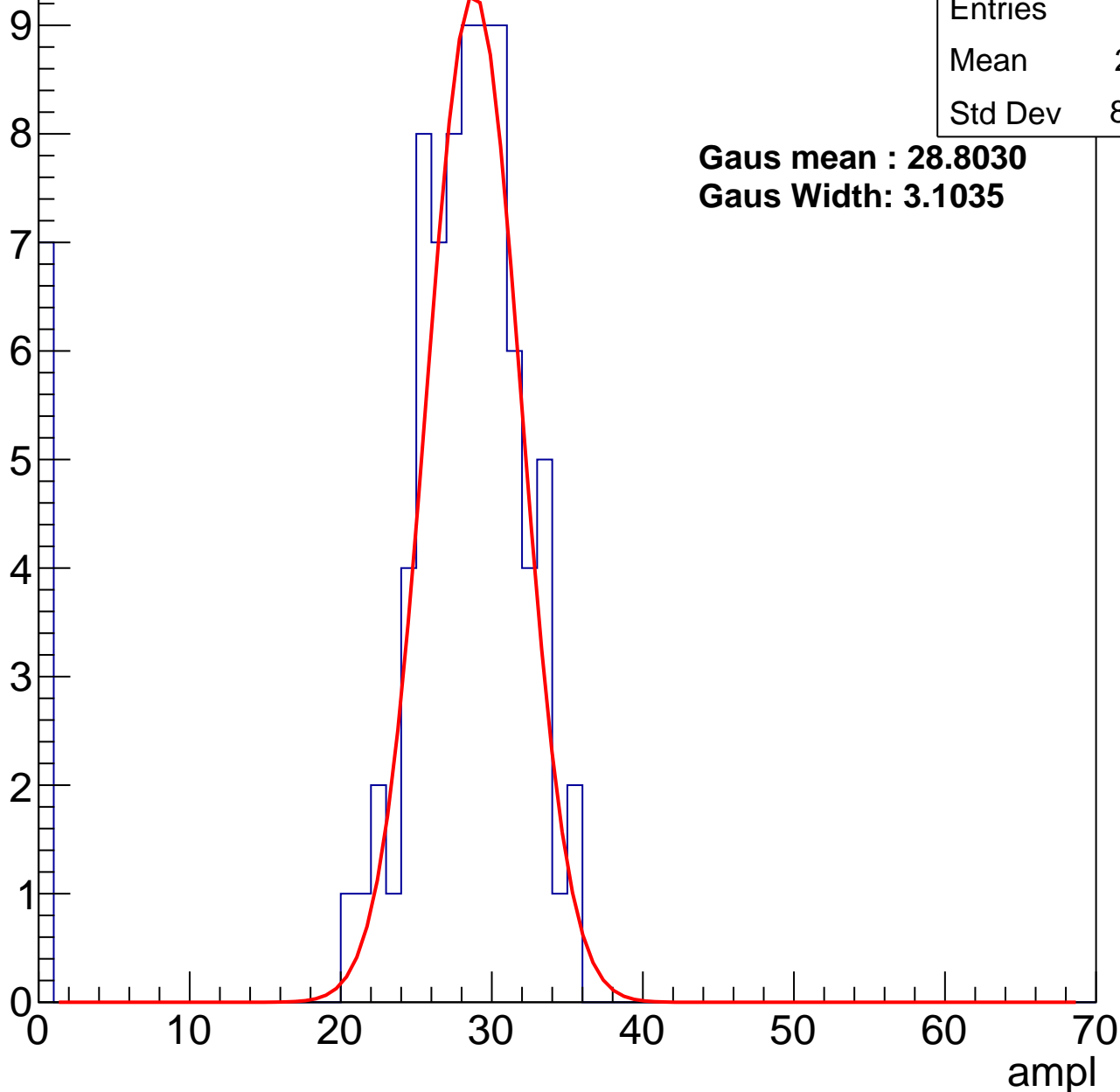
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	25.81
Std Dev	8.373

Gaus mean : 28.8030

Gaus Width: 3.1035



B1L103S, U2-ch60, adc1

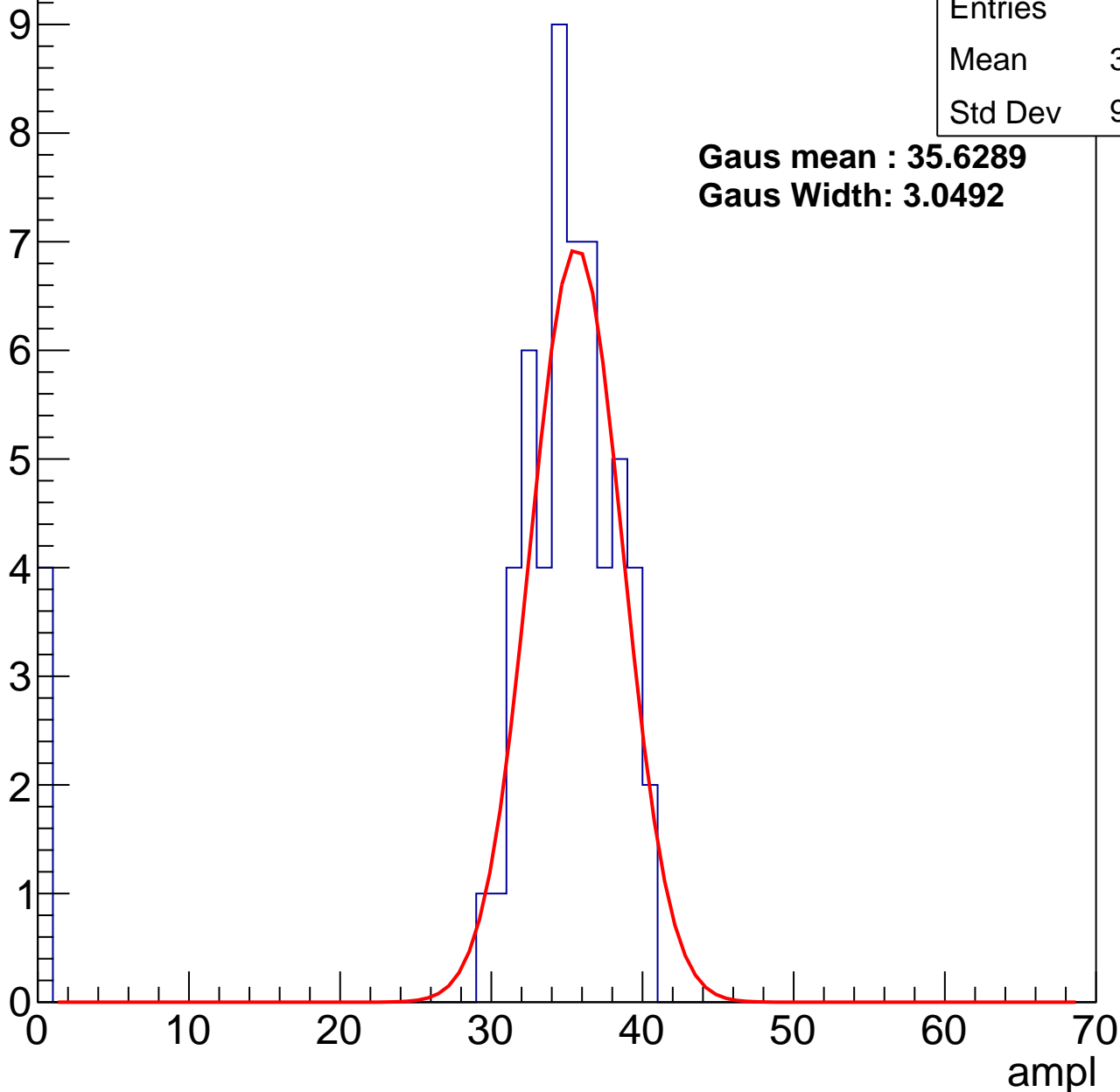
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	32.48
Std Dev	9.209

Gaus mean : 35.6289

Gaus Width: 3.0492



B1L103S, U2-ch60, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	33.76
Std Dev	15.37

Gaus mean : 40.8112

Gaus Width: 3.4122

Entry

10

8

6

4

2

0

0

10

20

30

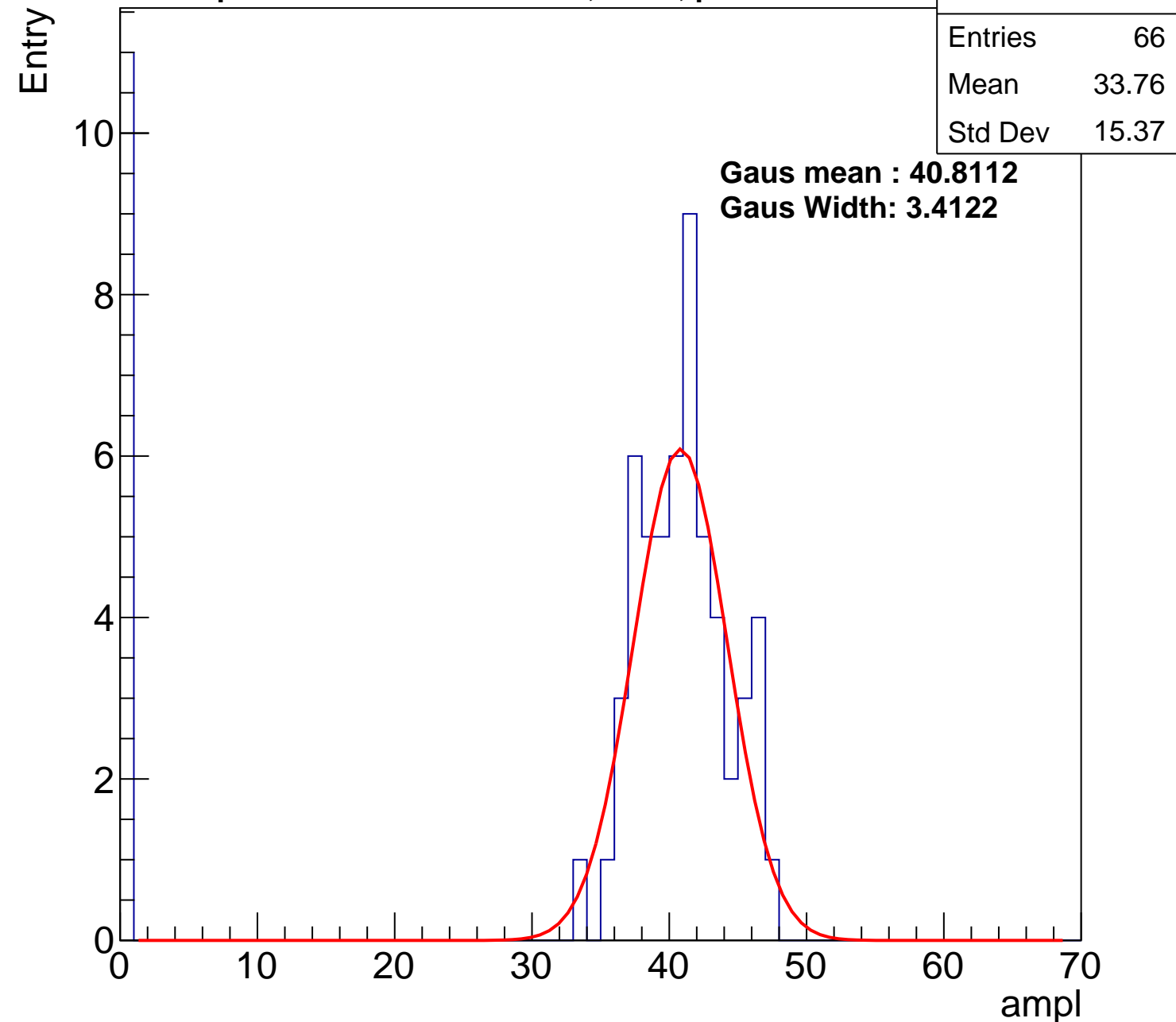
40

50

60

70

ampl

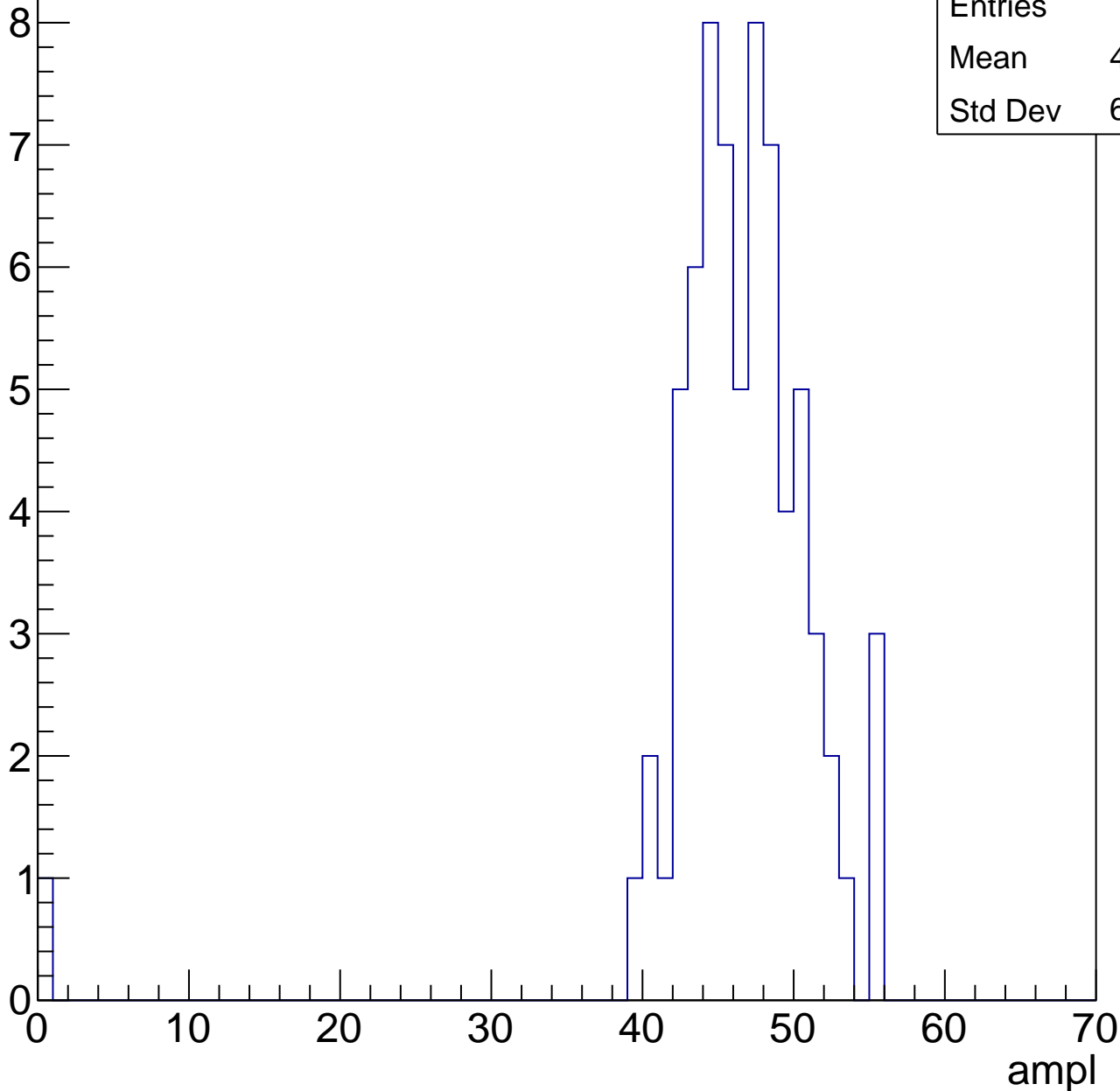


B1L103S, U2-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	45.77
Std Dev	6.618

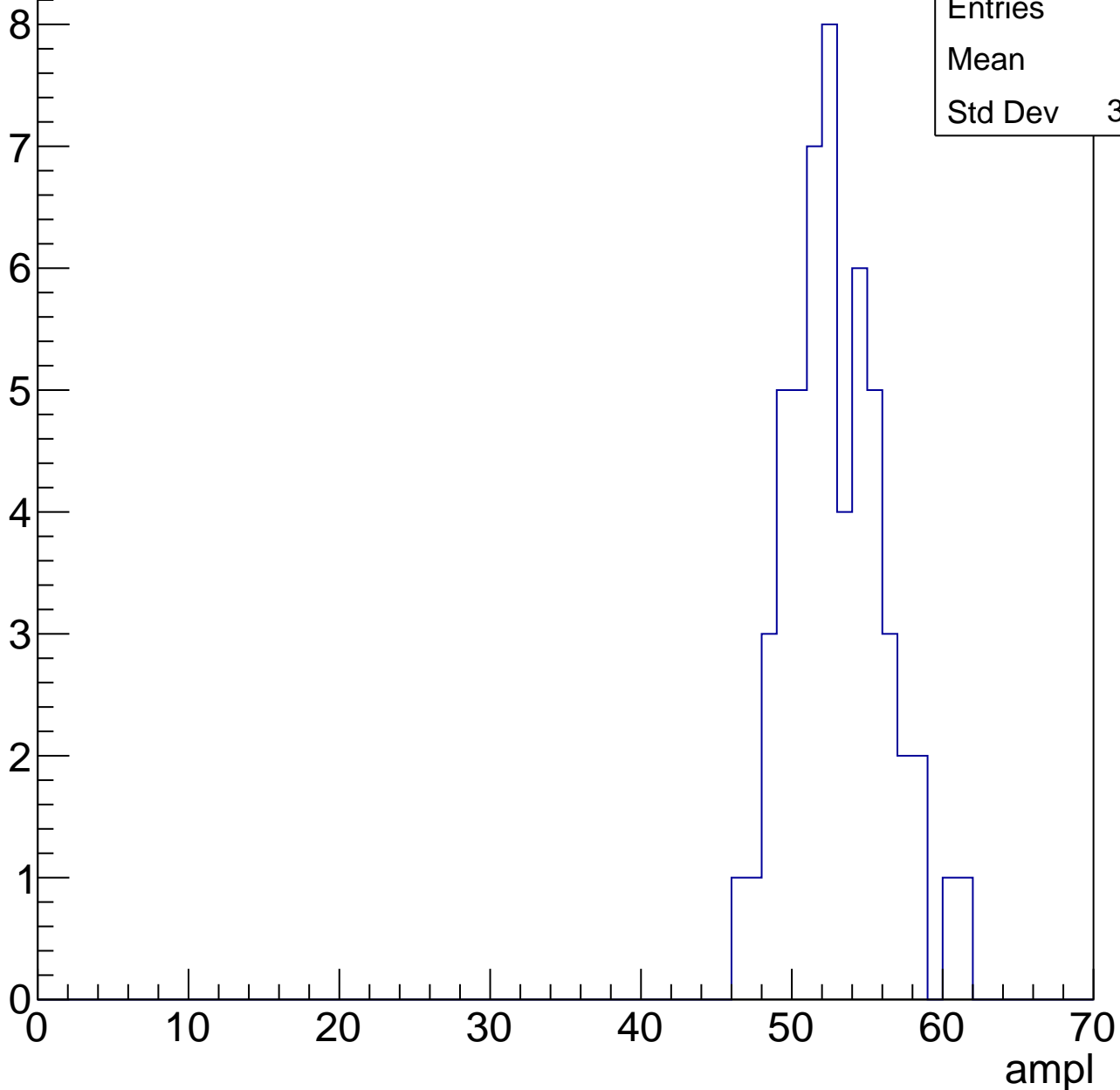


B1L103S, U2-ch60, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

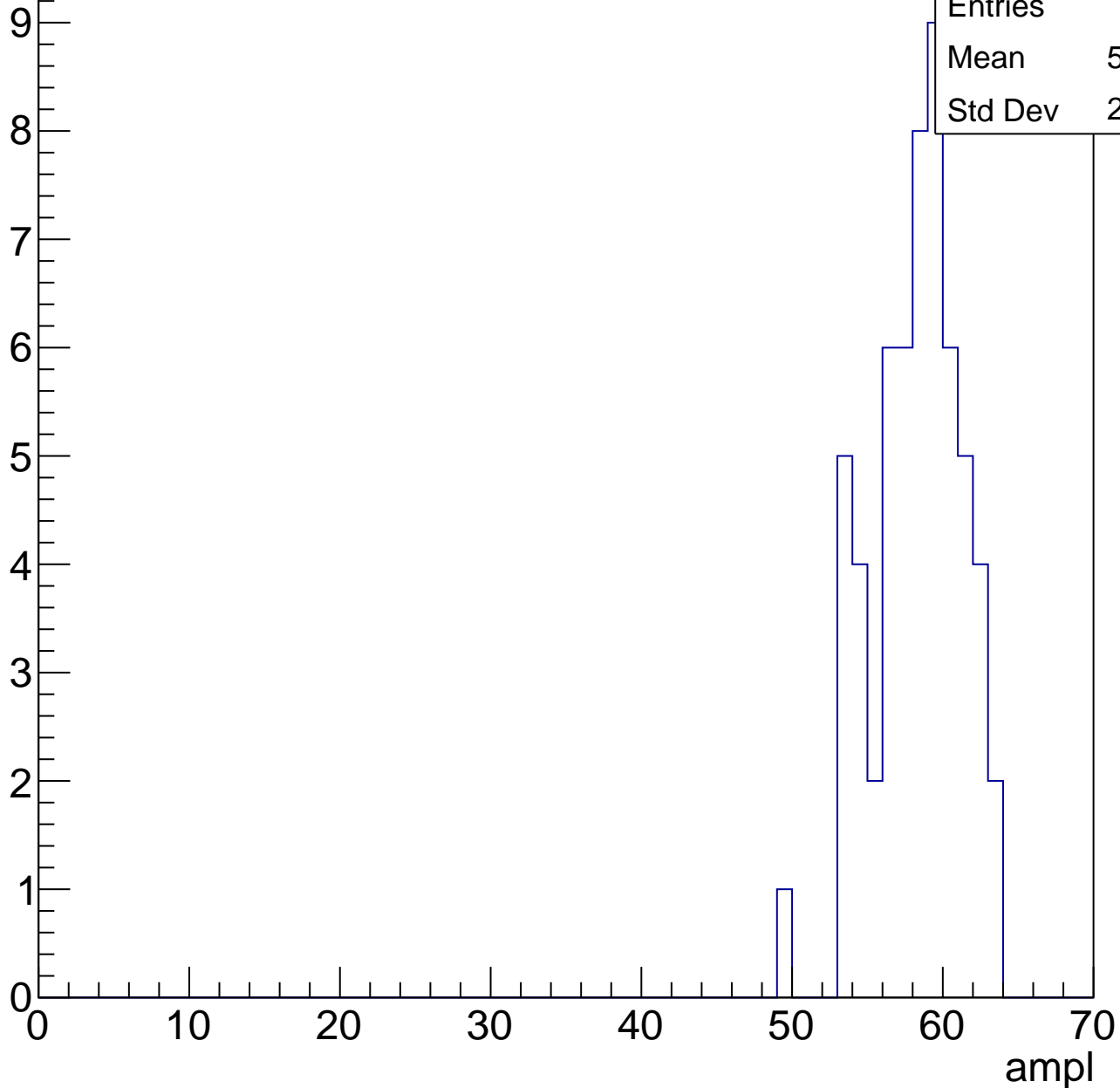
Entries	54
Mean	52.5
Std Dev	3.207



B1L103S, U2-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

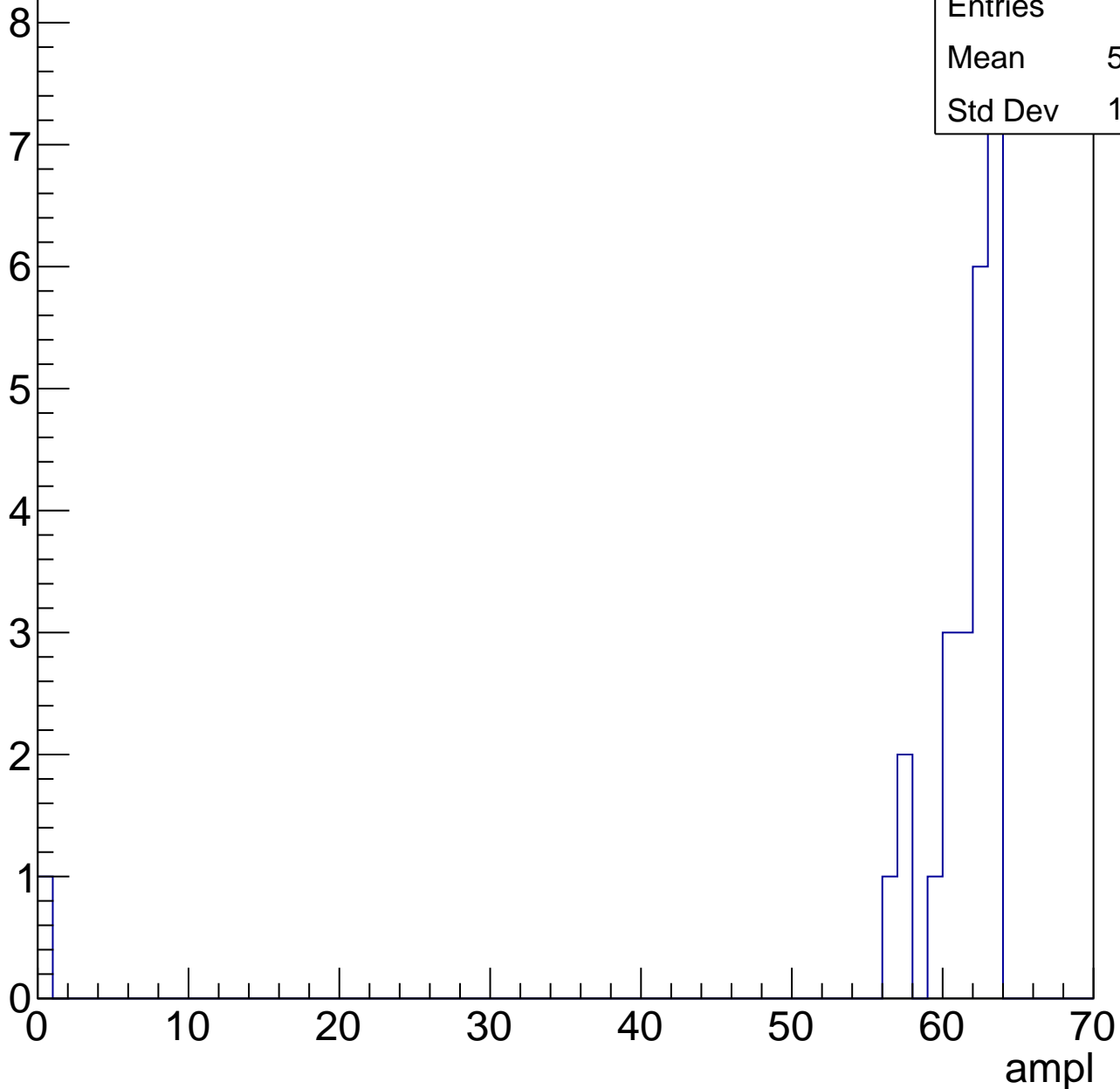


B1L103S, U2-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

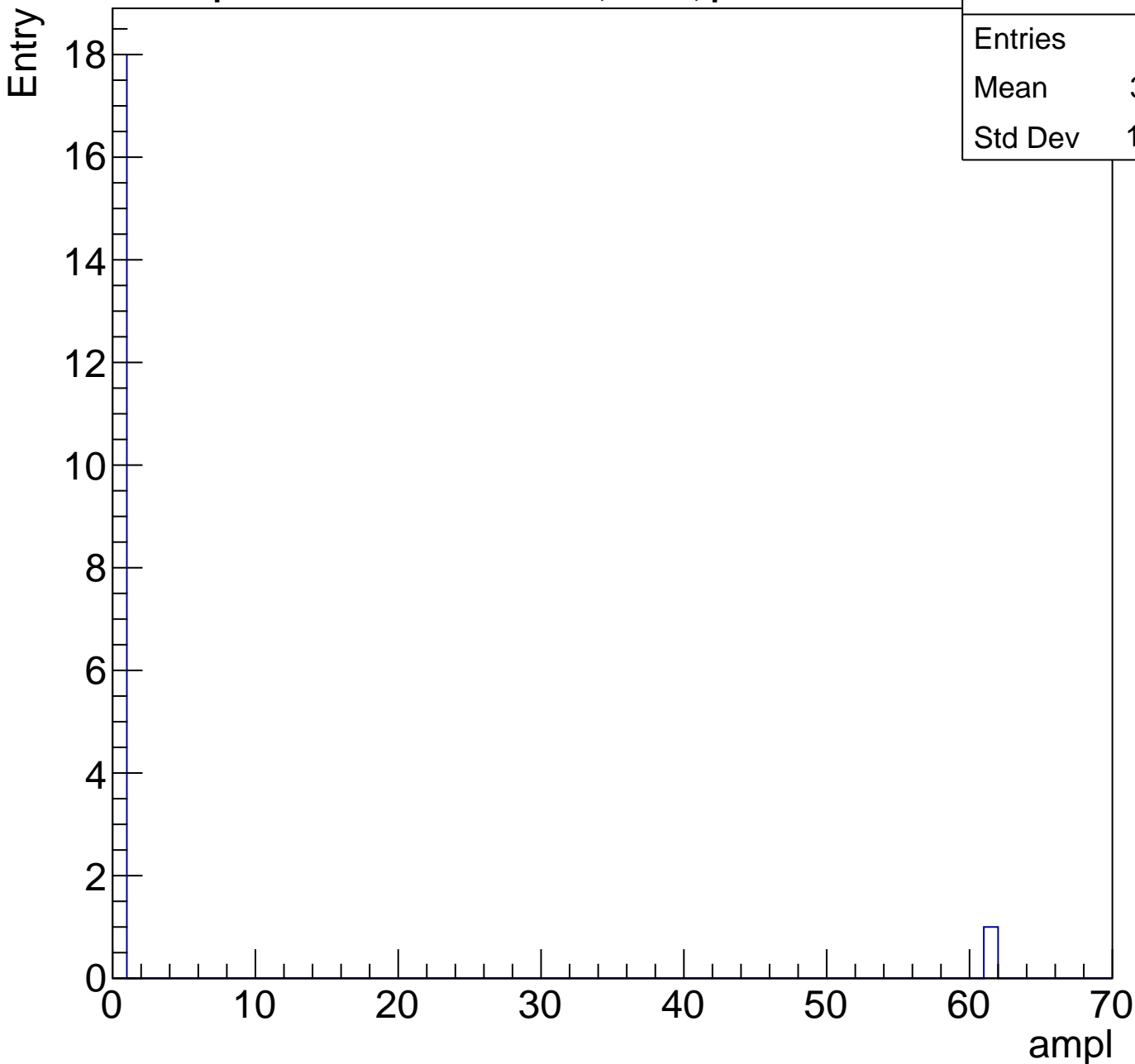
Entries	25
Mean	58.72
Std Dev	12.15



B1L103S, U2-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.211
Std Dev	13.62



B1L103S, U2-ch61, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	24.4
Std Dev	11.42

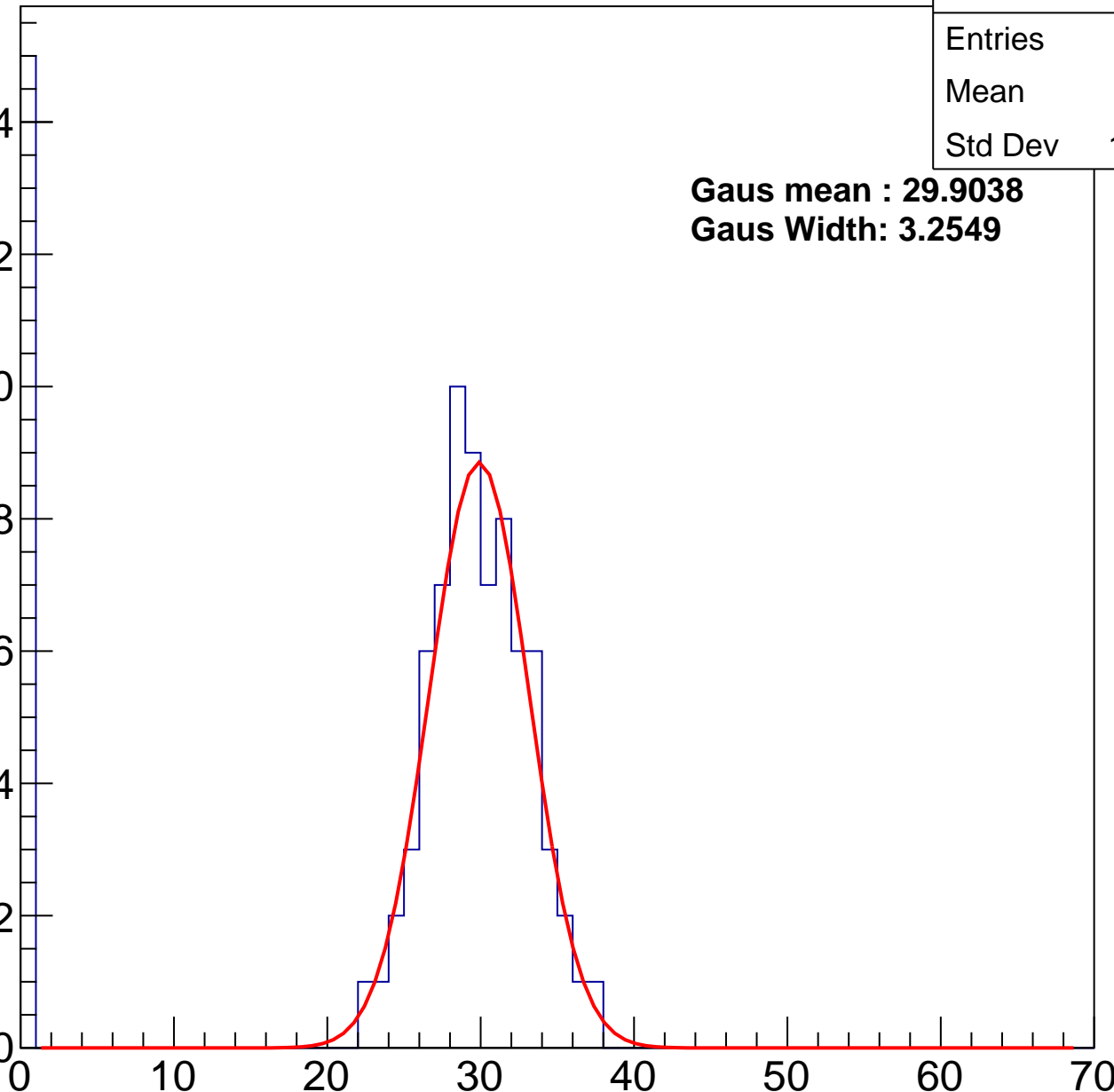
Gaus mean : 29.9038

Gaus Width: 3.2549

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch61, adc1

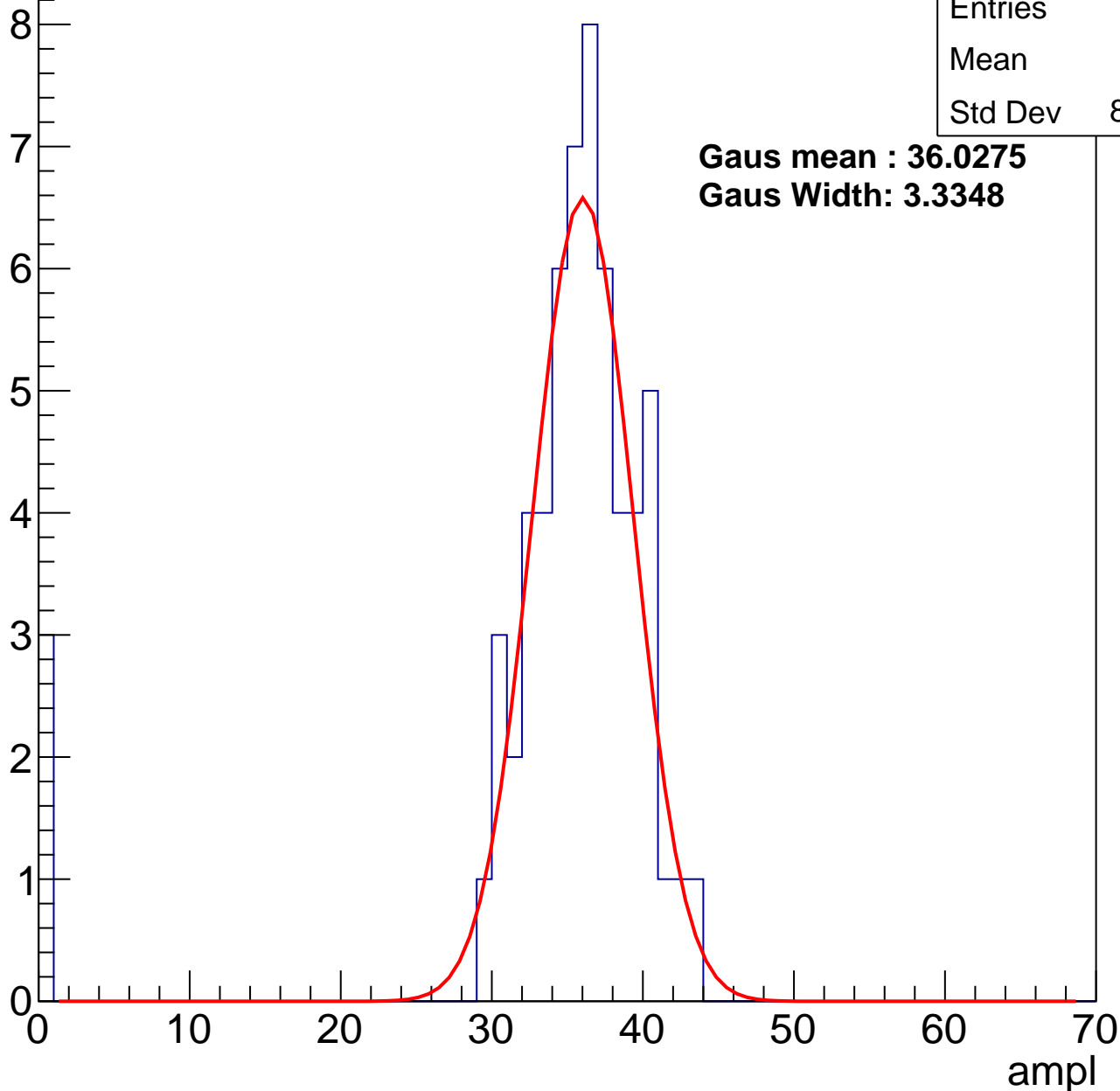
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	33.9
Std Dev	8.376

Gaus mean : 36.0275

Gaus Width: 3.3348



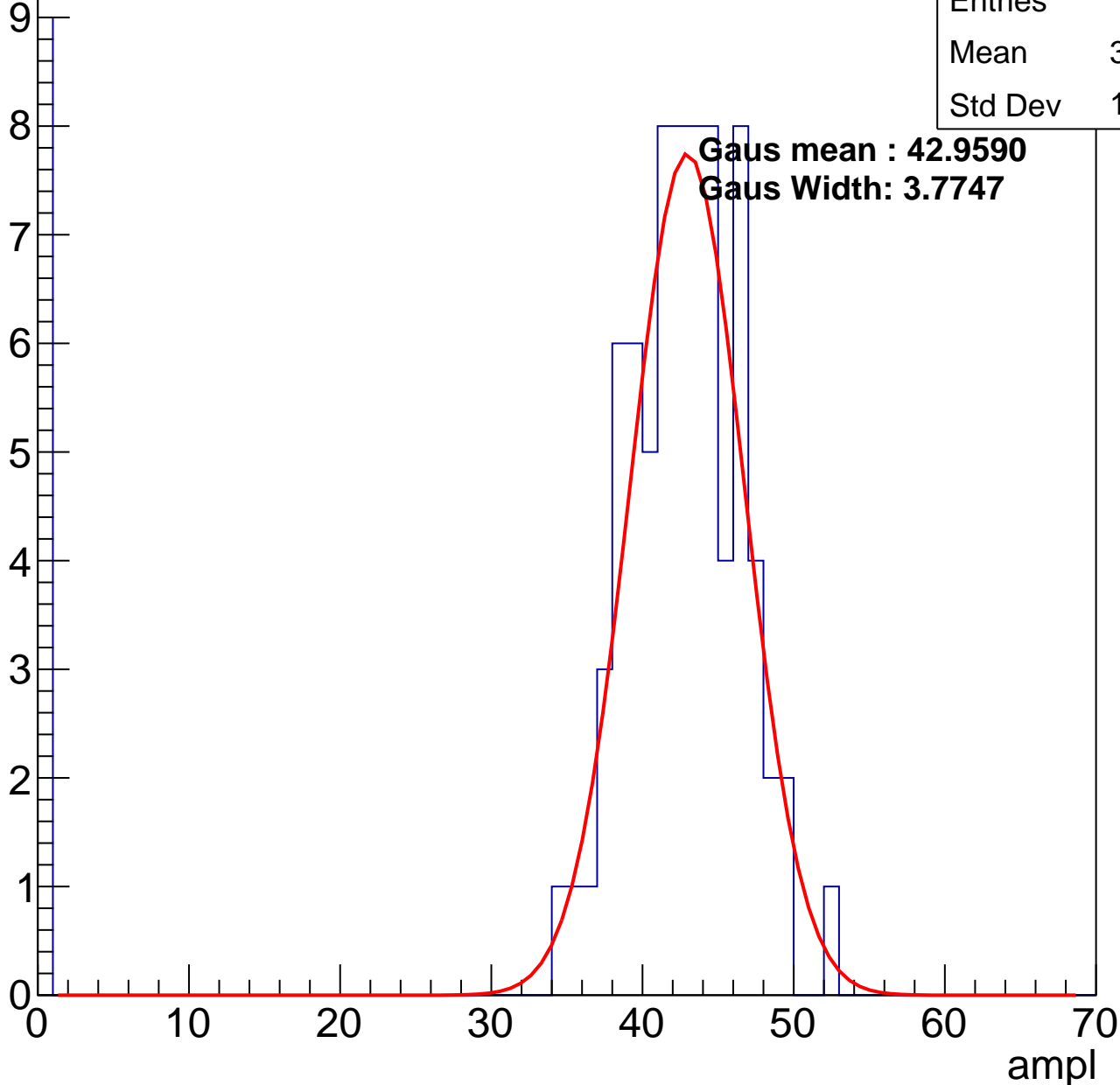
B1L103S, U2-ch61, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	37.88
Std Dev	13.46

Gaus mean : 42.9590
Gaus Width: 3.7747

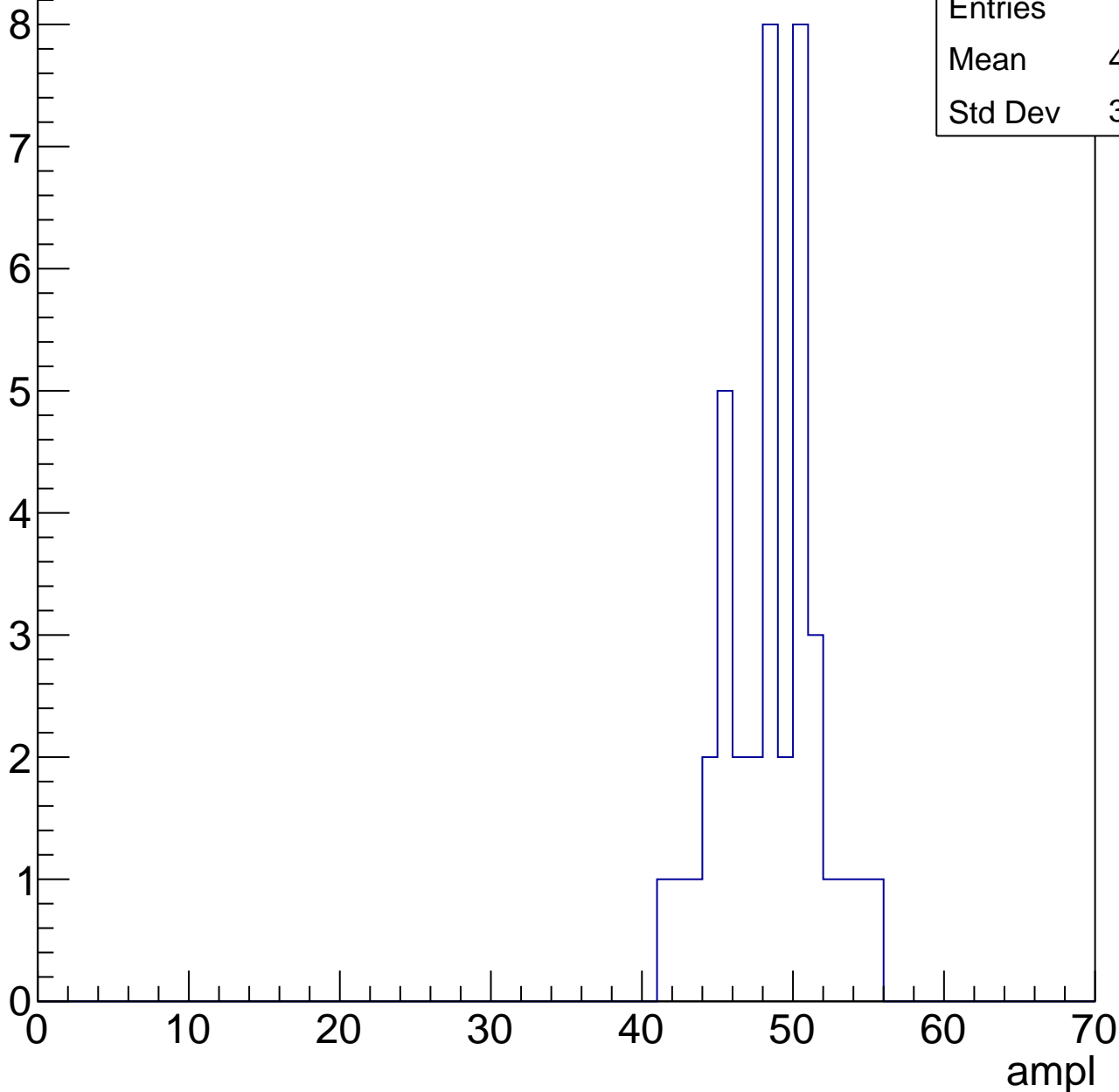


B1L103S, U2-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	48.05
Std Dev	3.137

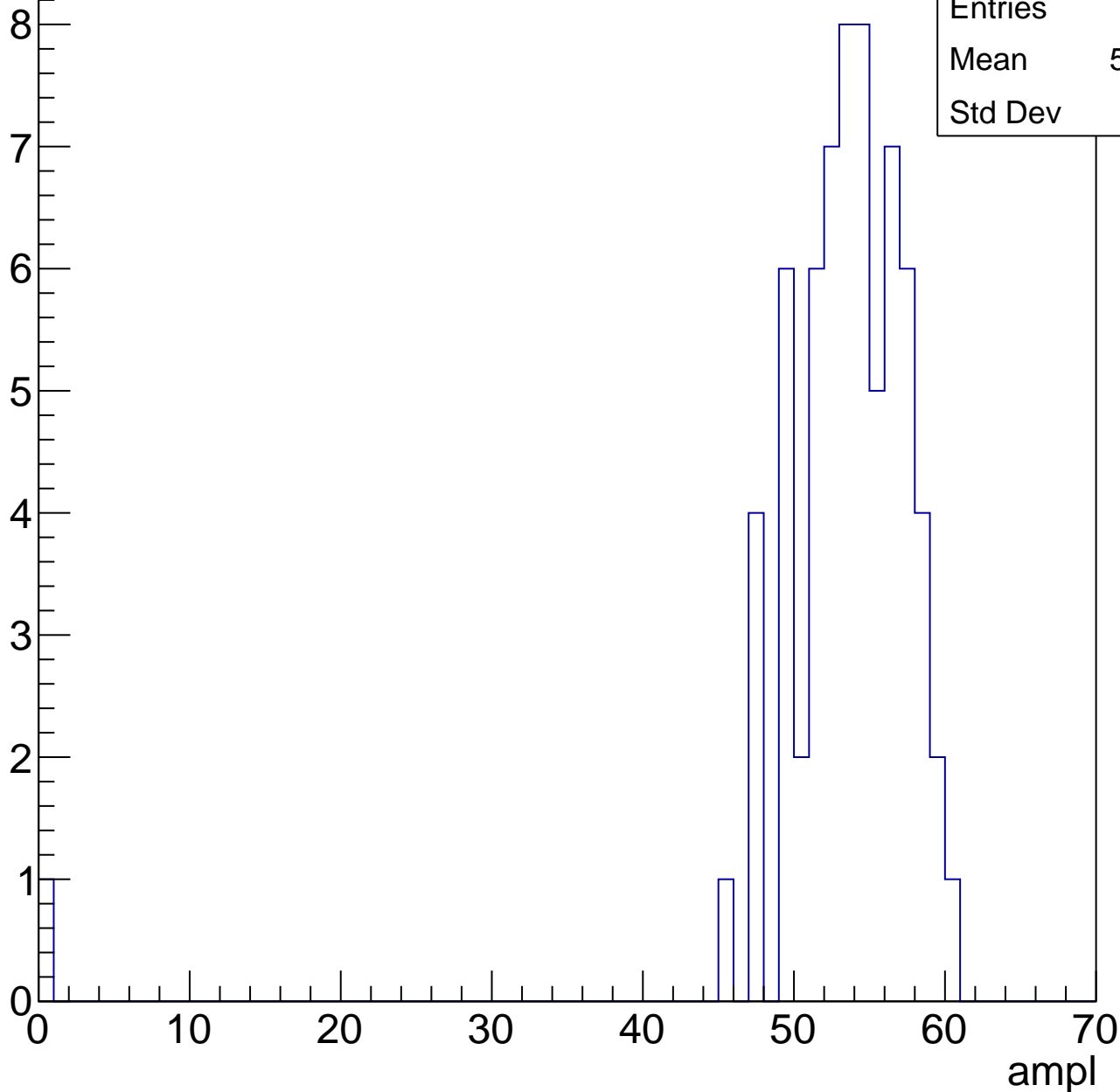


B1L103S, U2-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

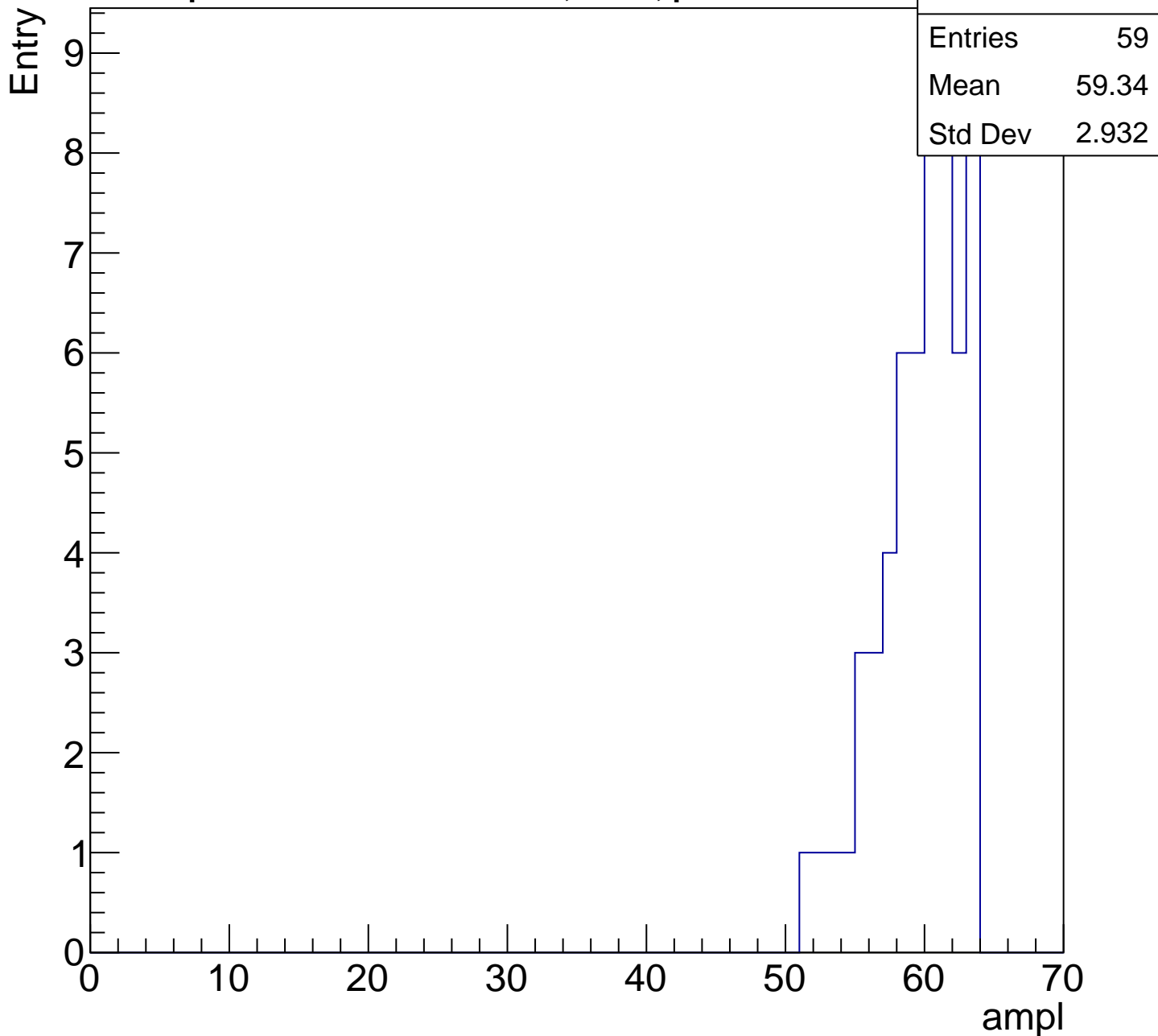
Entry

Entries	68
Mean	52.53
Std Dev	7.23



B1L103S, U2-ch61, adc5

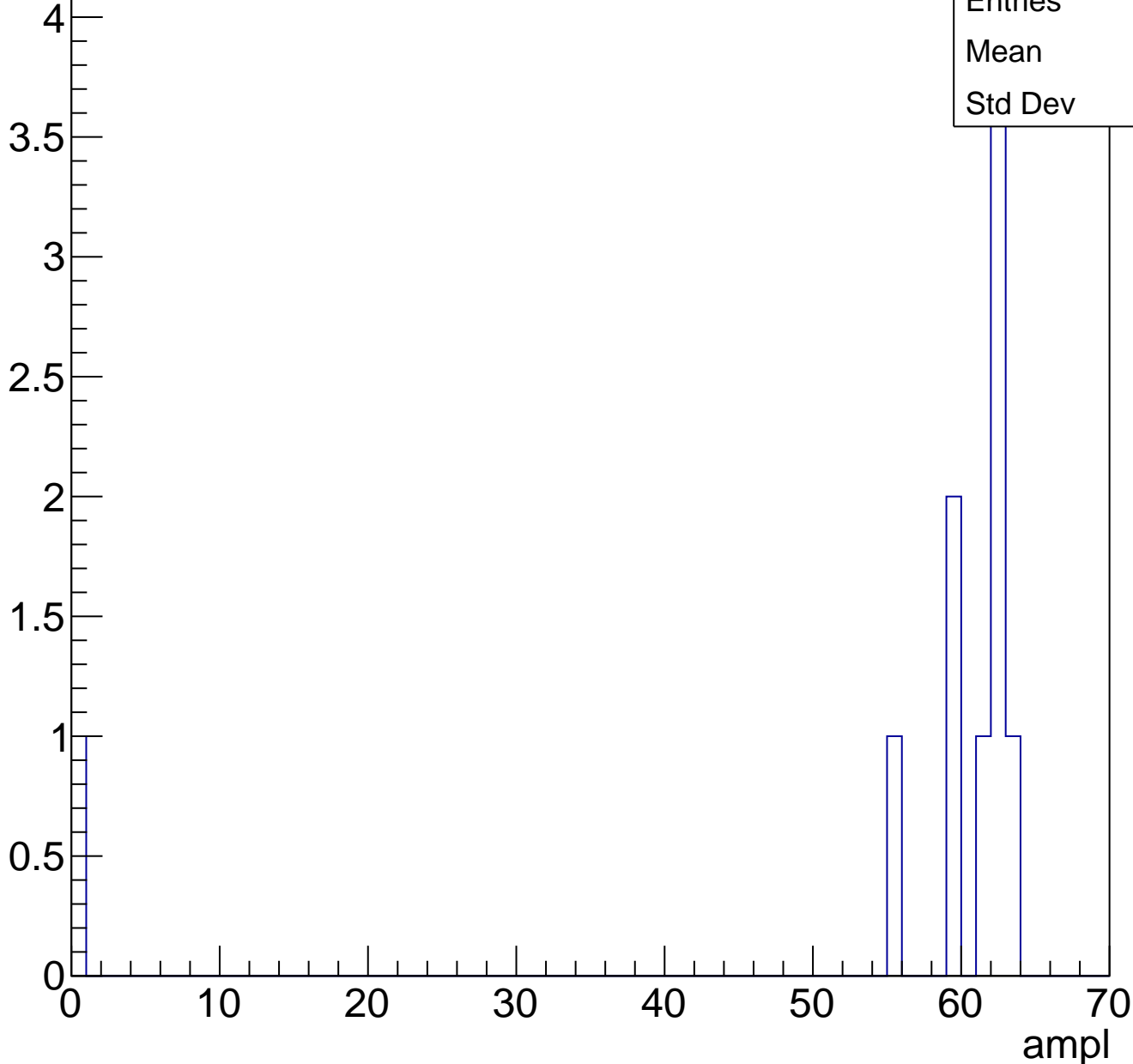
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch62, adc0

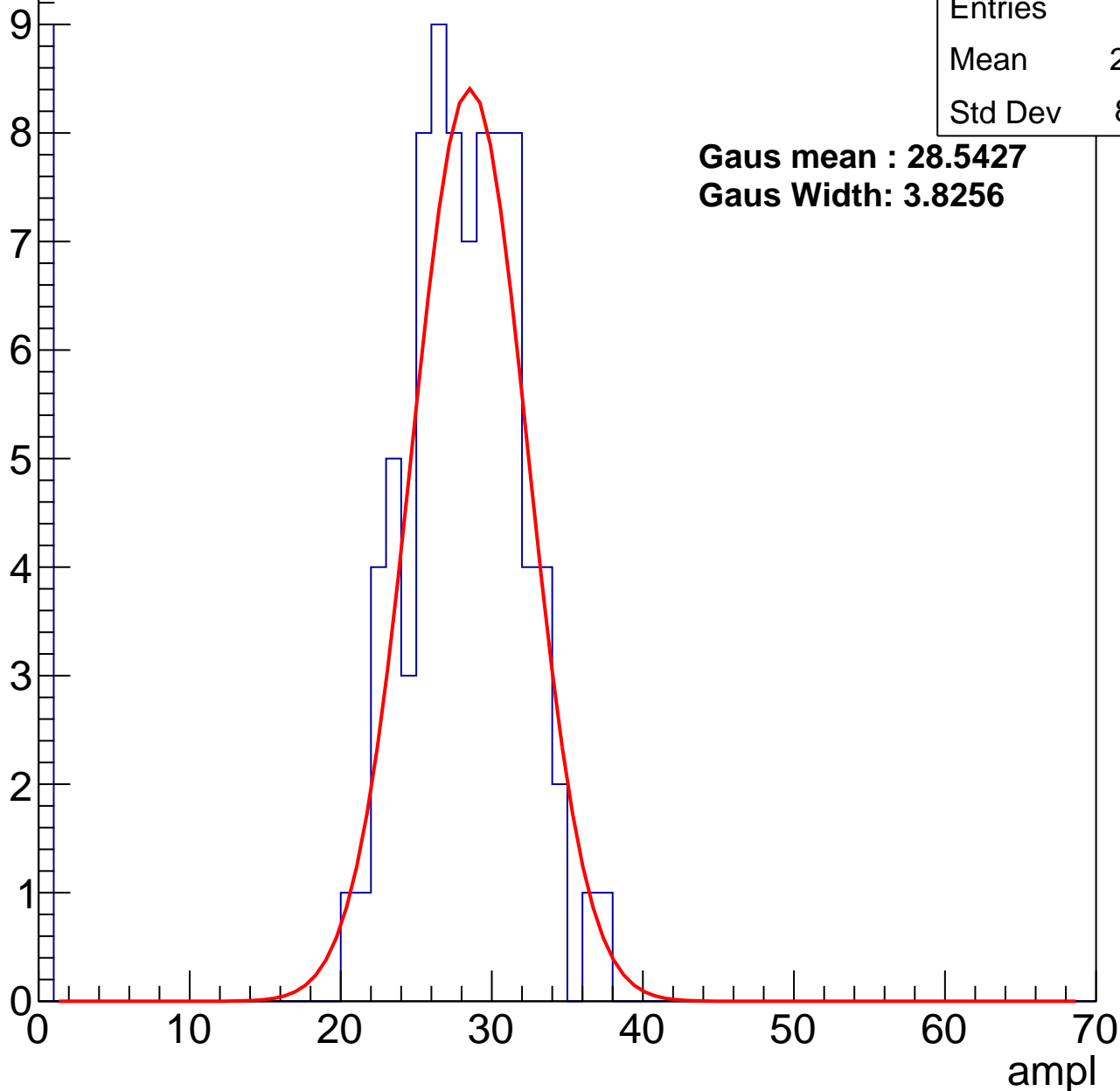
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	91
Mean	25.09
Std Dev	8.971

Gaus mean : 28.5427

Gaus Width: 3.8256



B1L103S, U2-ch62, adc1

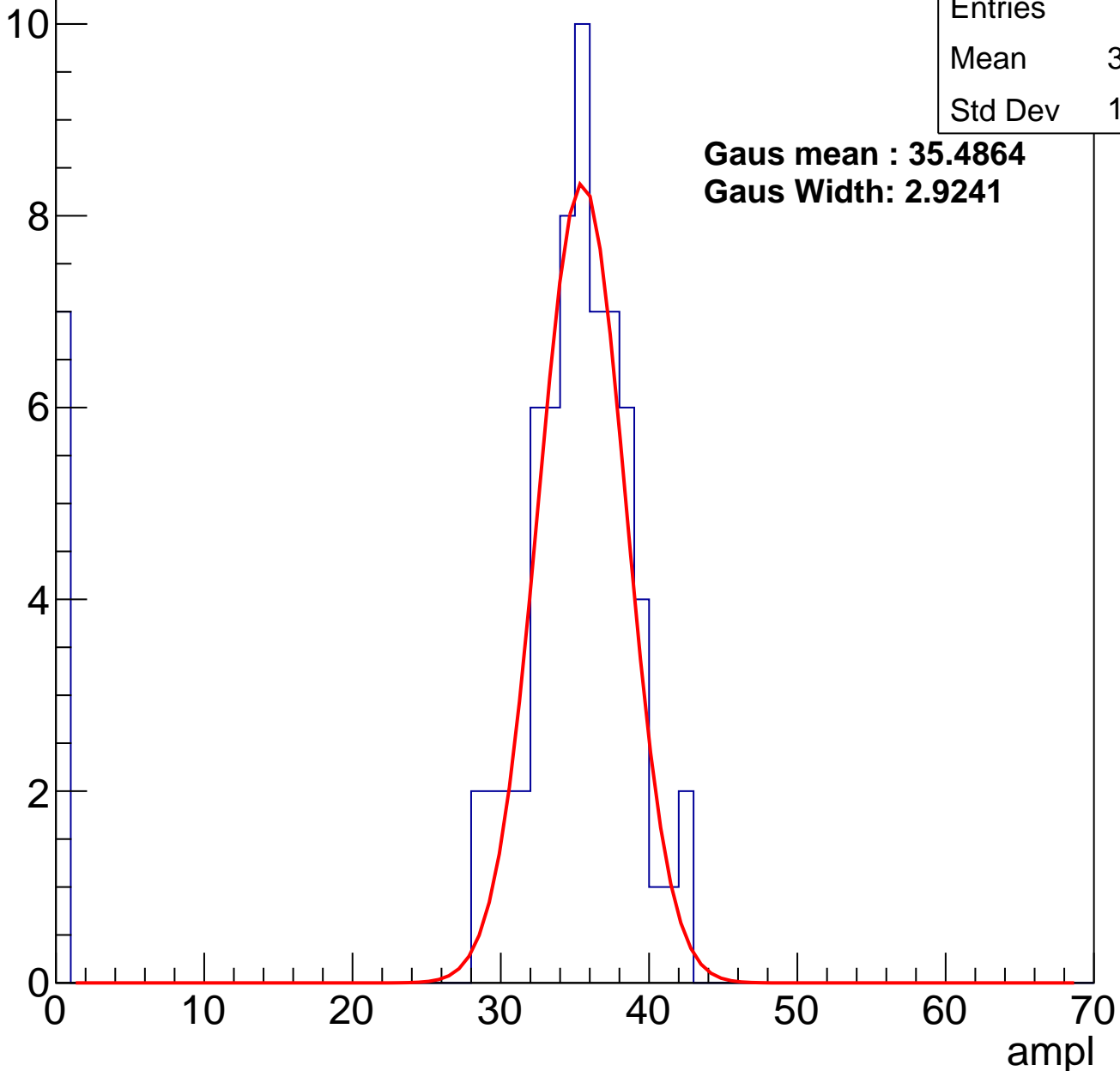
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	31.62
Std Dev	10.72

Gaus mean : 35.4864

Gaus Width: 2.9241

Entry



B1L103S, U2-ch62, adc2

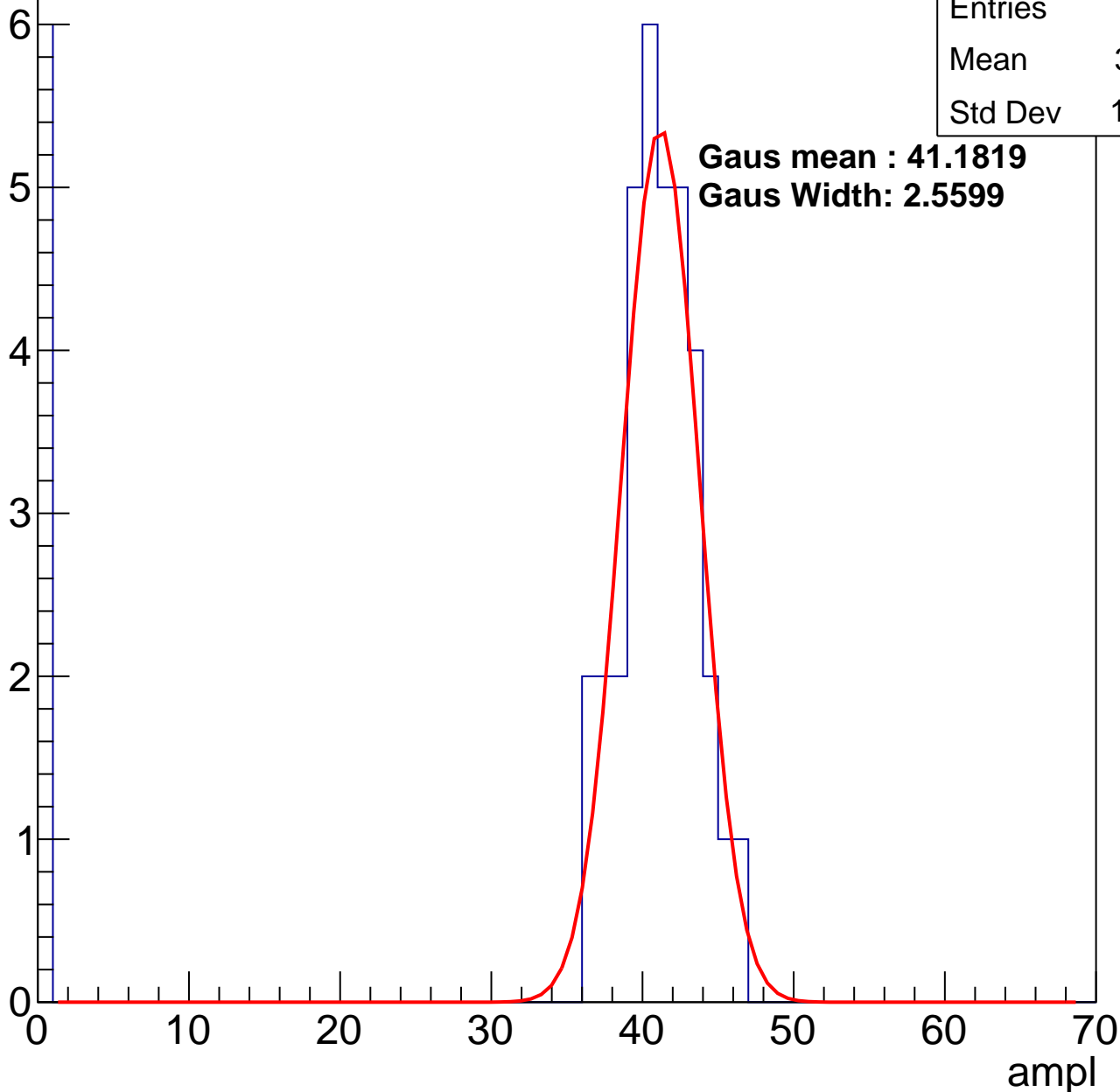
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	34.71
Std Dev	14.54

Gaus mean : 41.1819

Gaus Width: 2.5599



B1L103S, U2-ch62, adc3

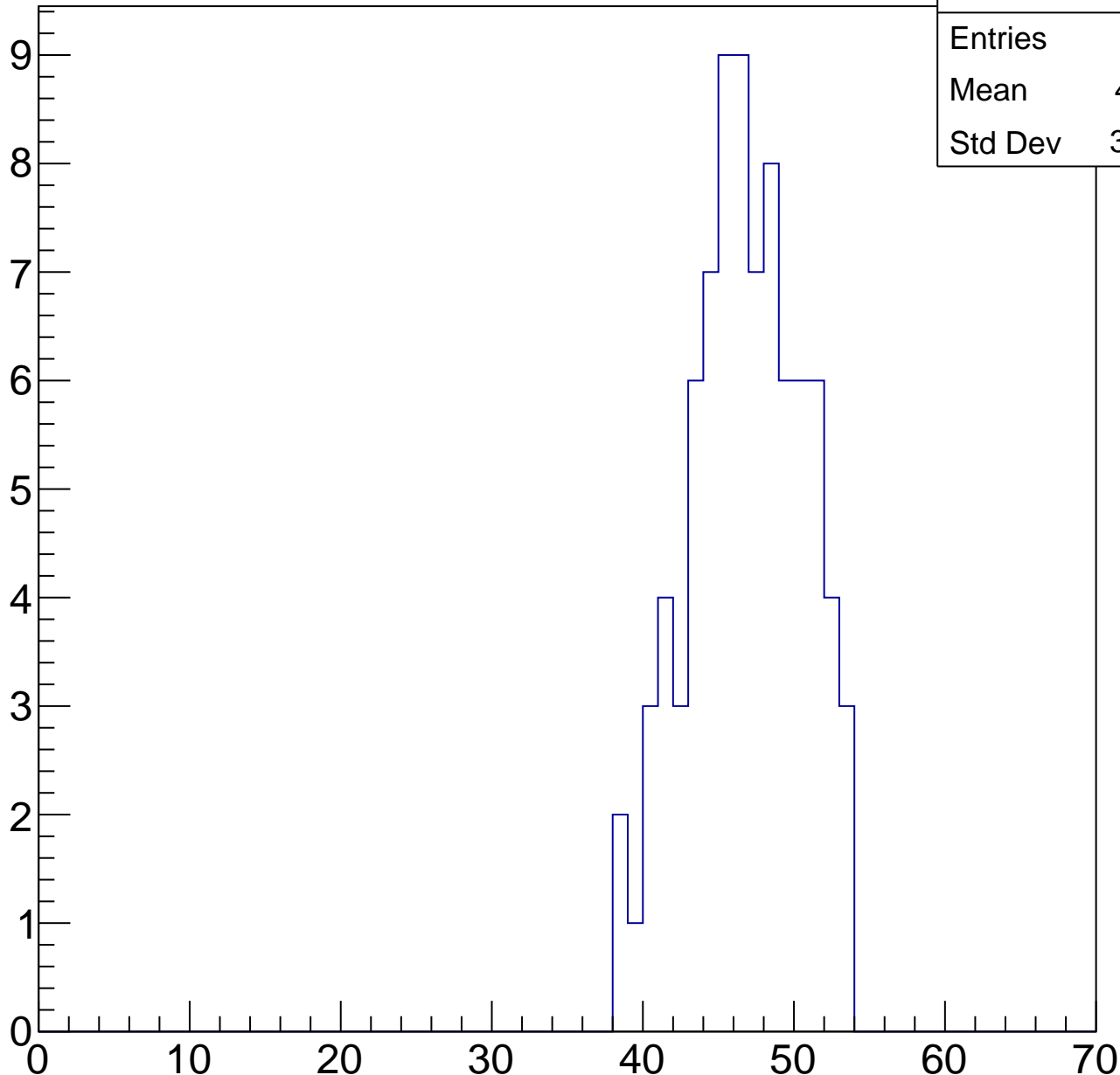
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	84
Mean	46.31
Std Dev	3.694

ampl

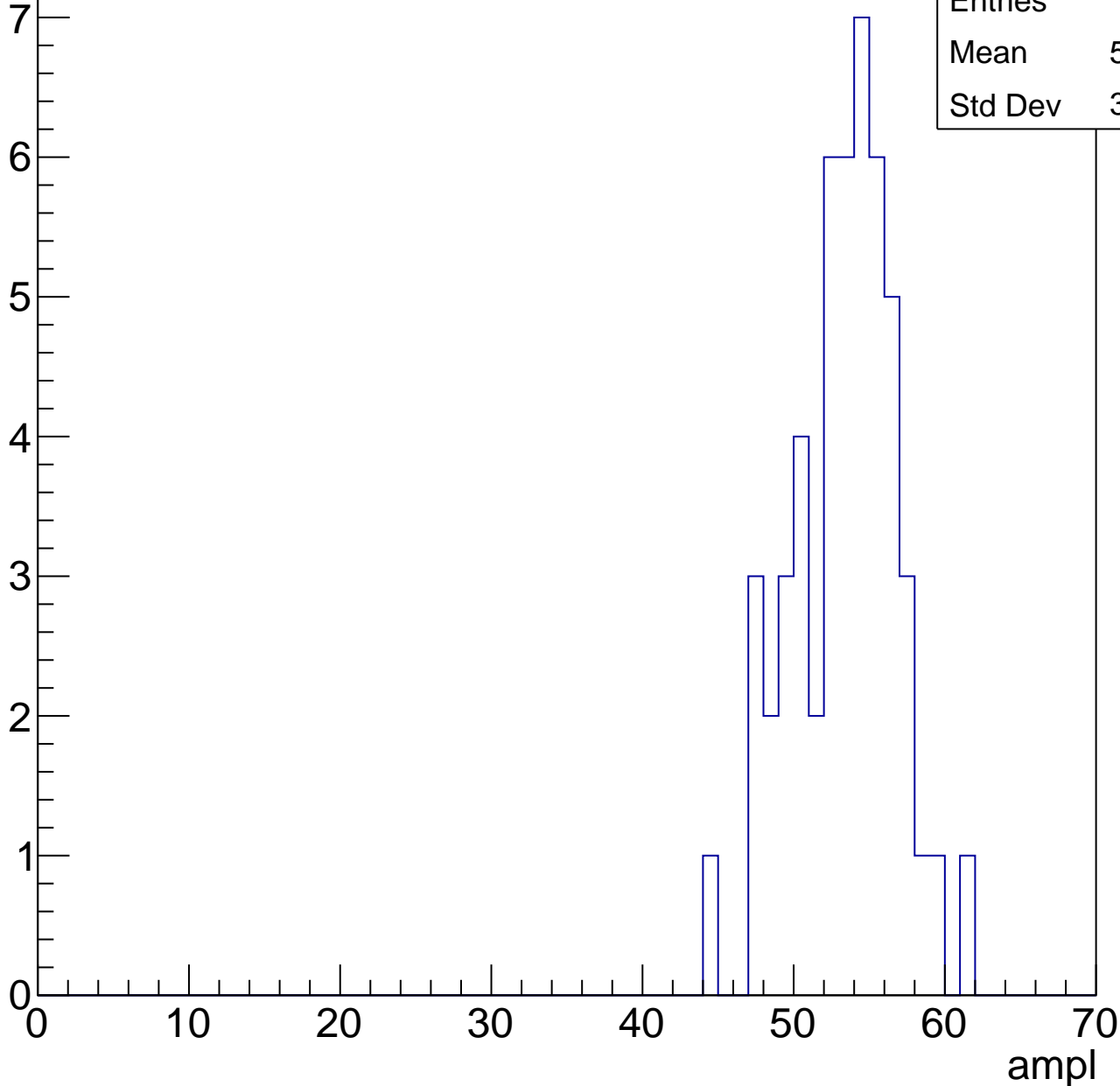


B1L103S, U2-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

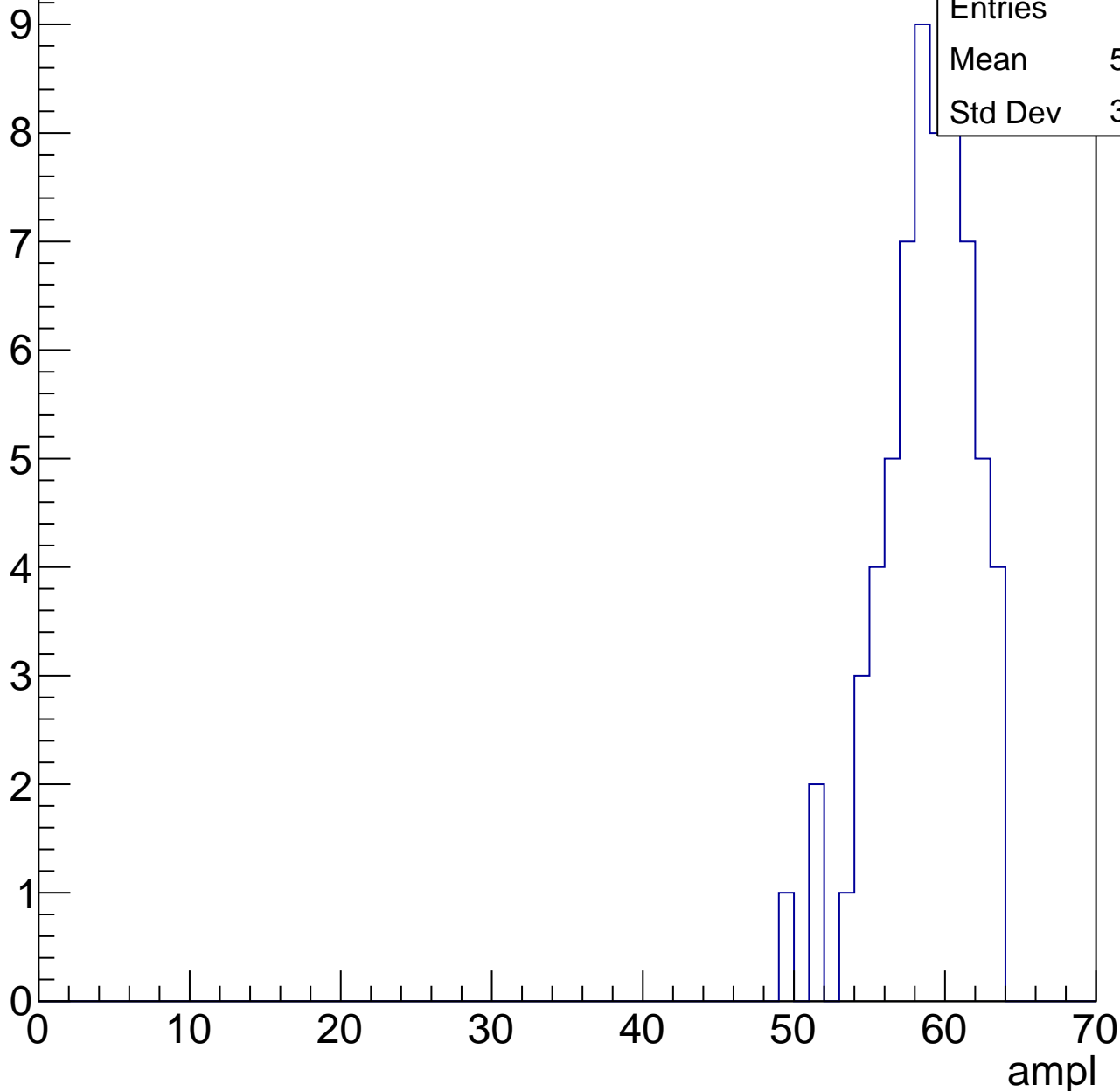
Entries	51
Mean	52.88
Std Dev	3.388



B1L103S, U2-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

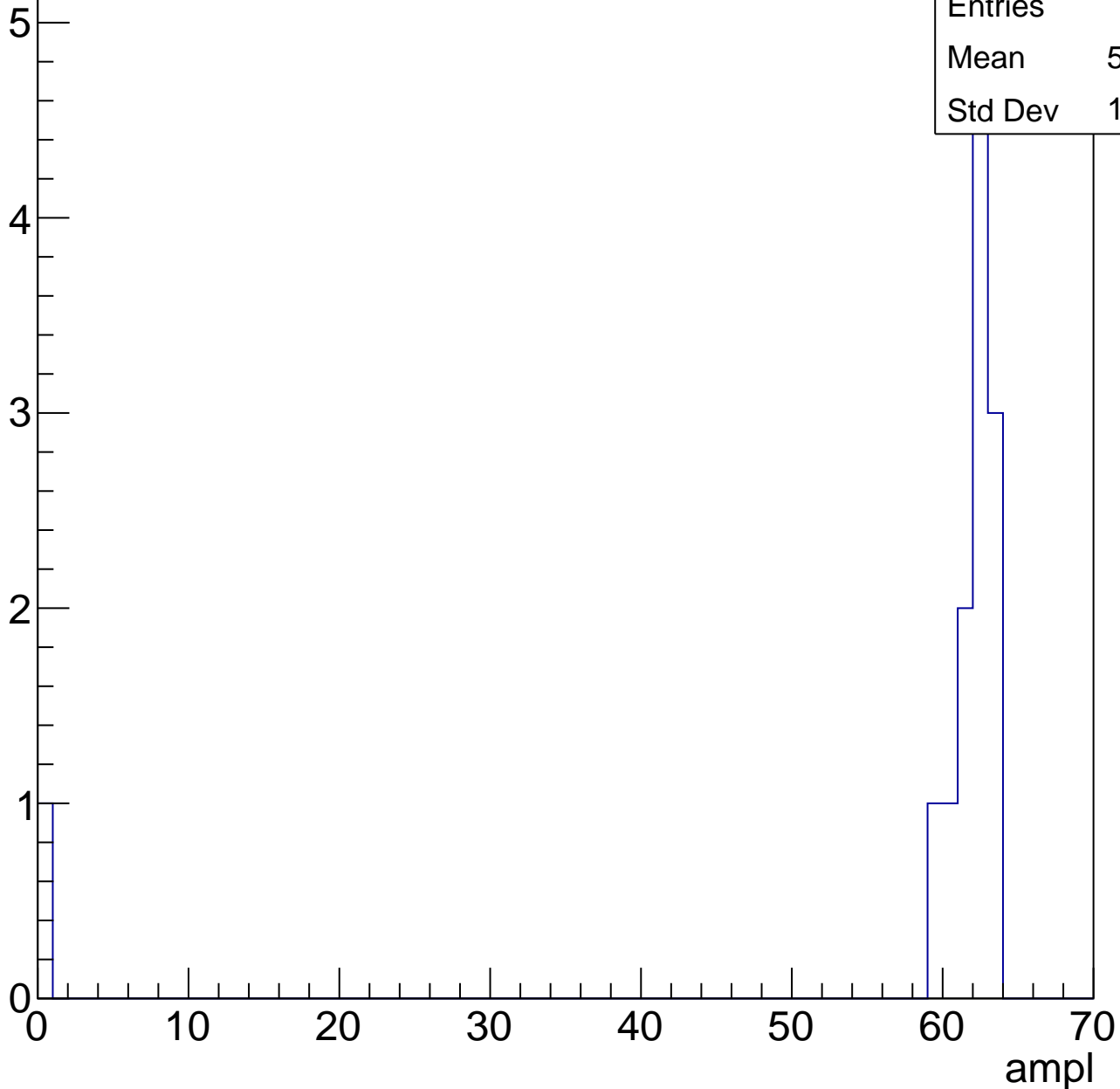


B1L103S, U2-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	56.92
Std Dev	16.47



B1L103S, U2-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

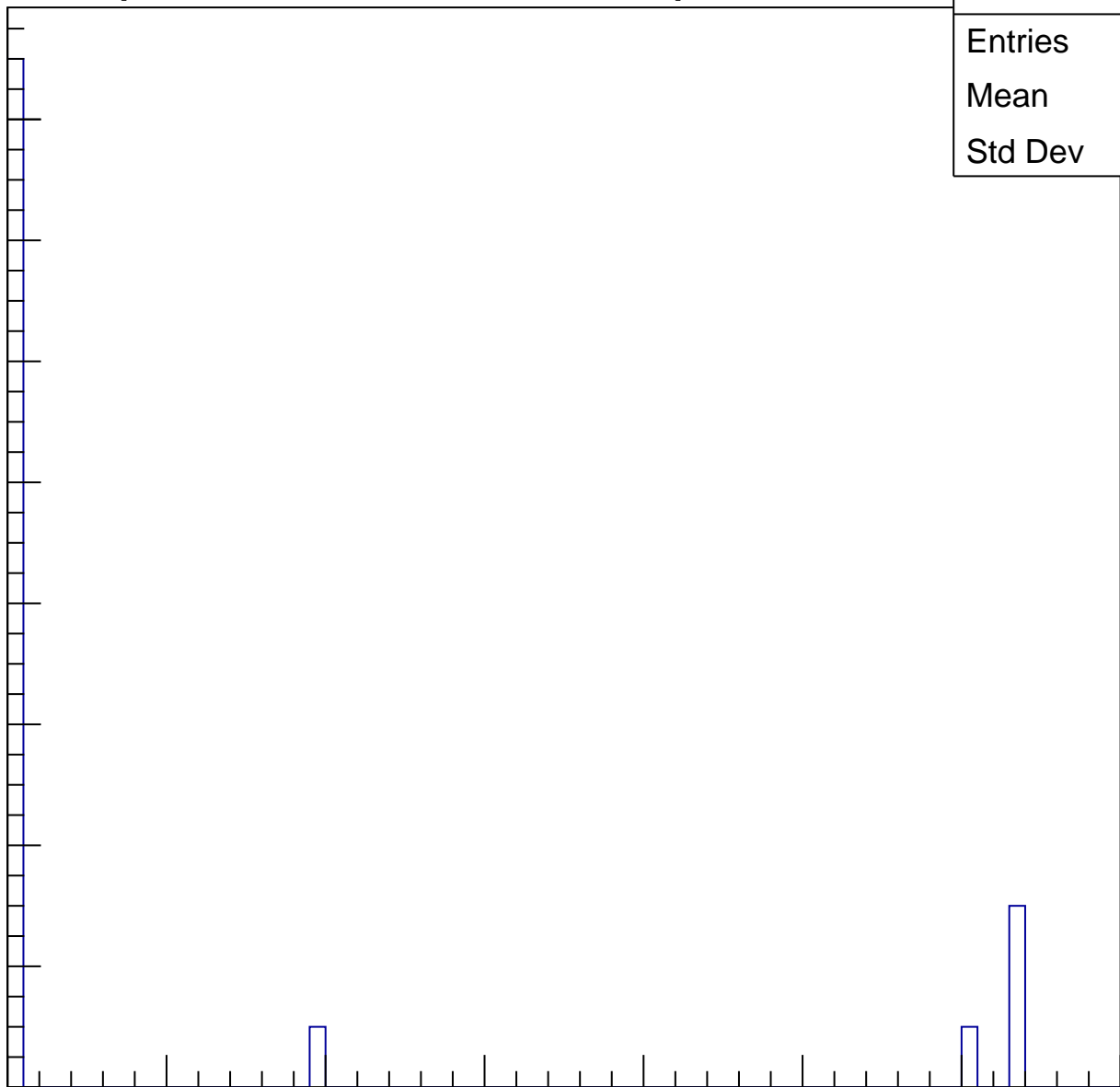
Entries	22
Mean	12.18
Std Dev	23.93

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



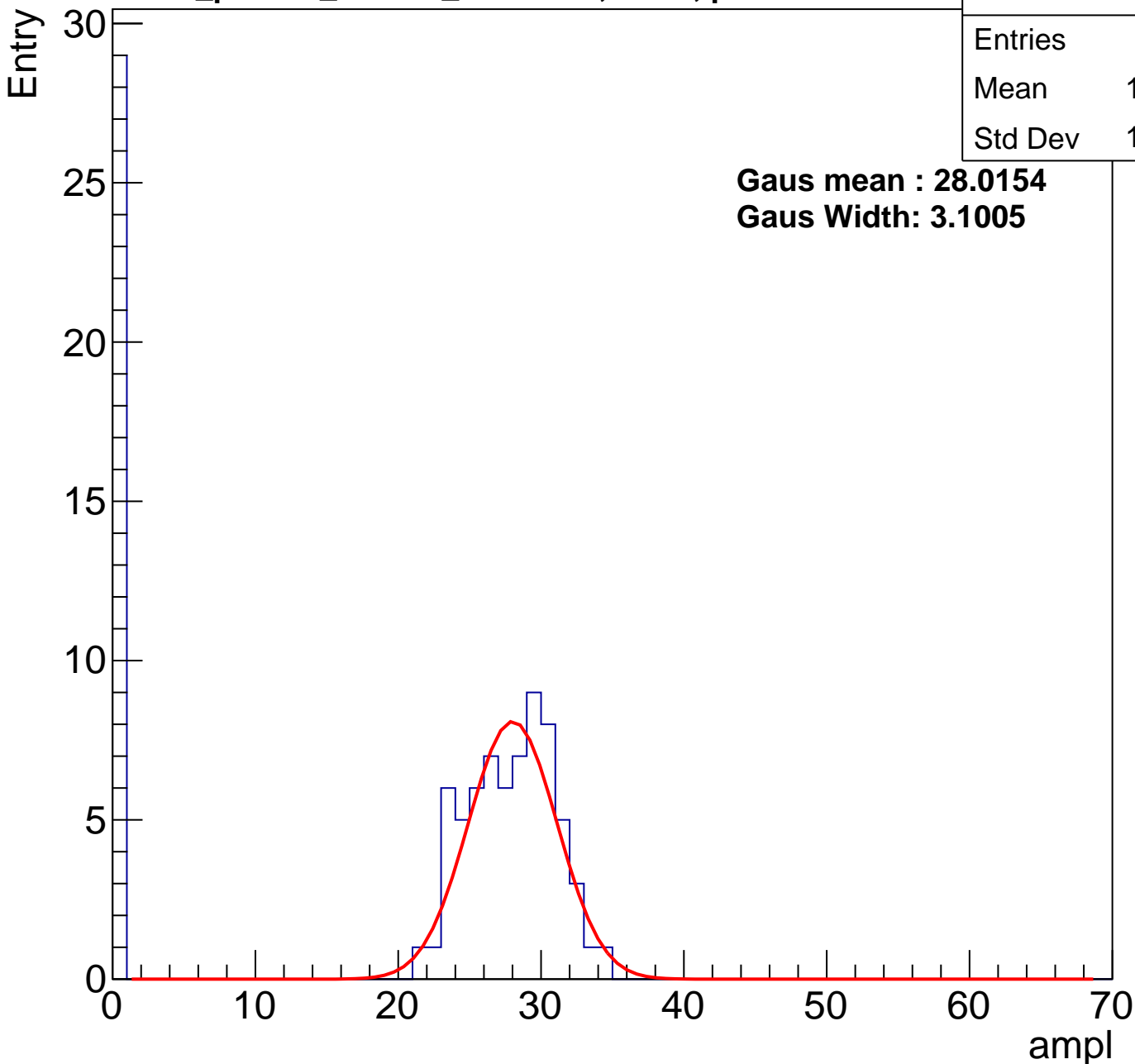
B1L103S, U2-ch63, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	19.05
Std Dev	12.87

Gaus mean : 28.0154

Gaus Width: 3.1005



B1L103S, U2-ch63, adc1

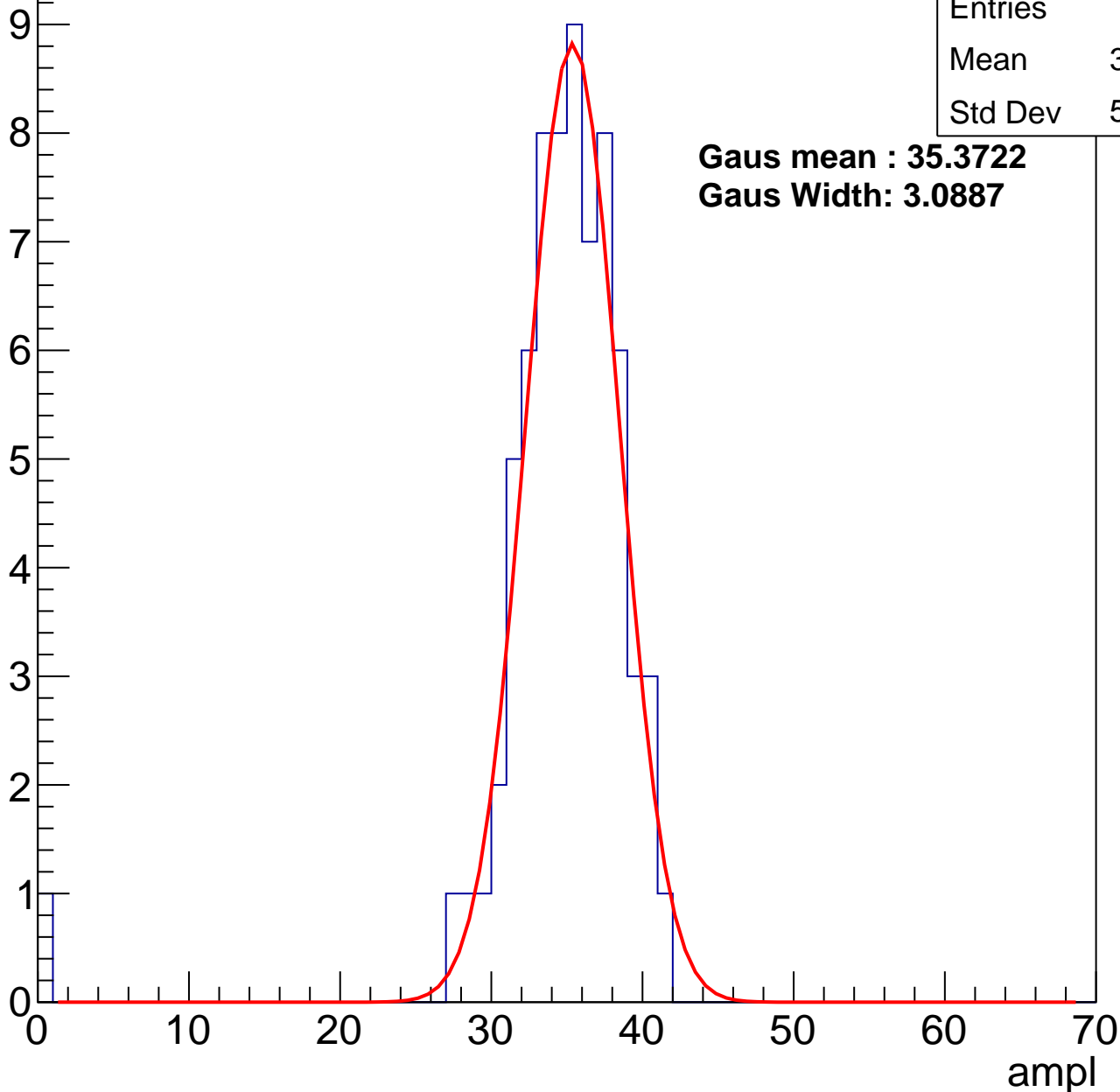
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.23
Std Dev	5.083

Gaus mean : 35.3722

Gaus Width: 3.0887



B1L103S, U2-ch63, adc2

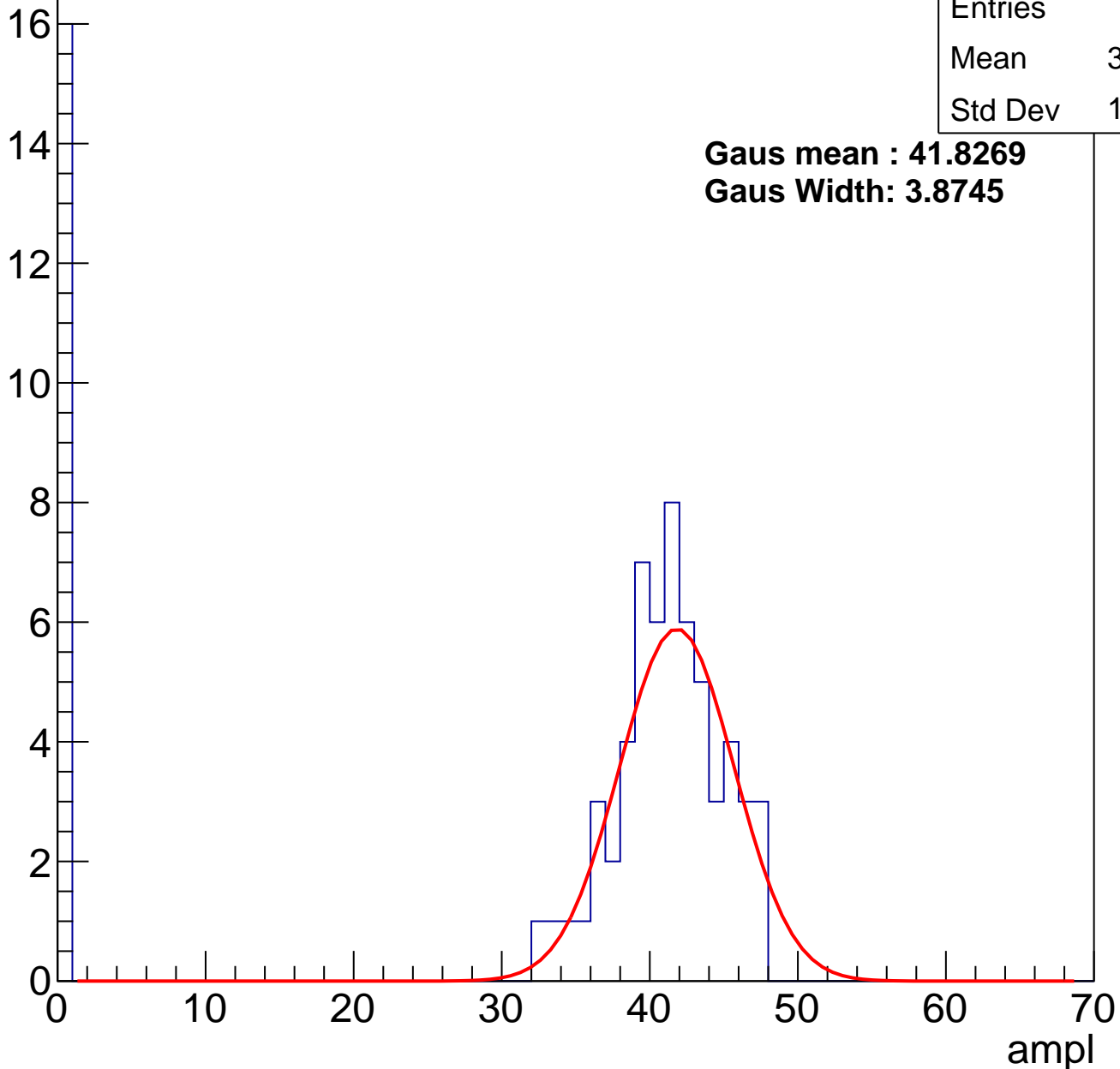
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.99
Std Dev	17.08

Gaus mean : 41.8269

Gaus Width: 3.8745

Entry

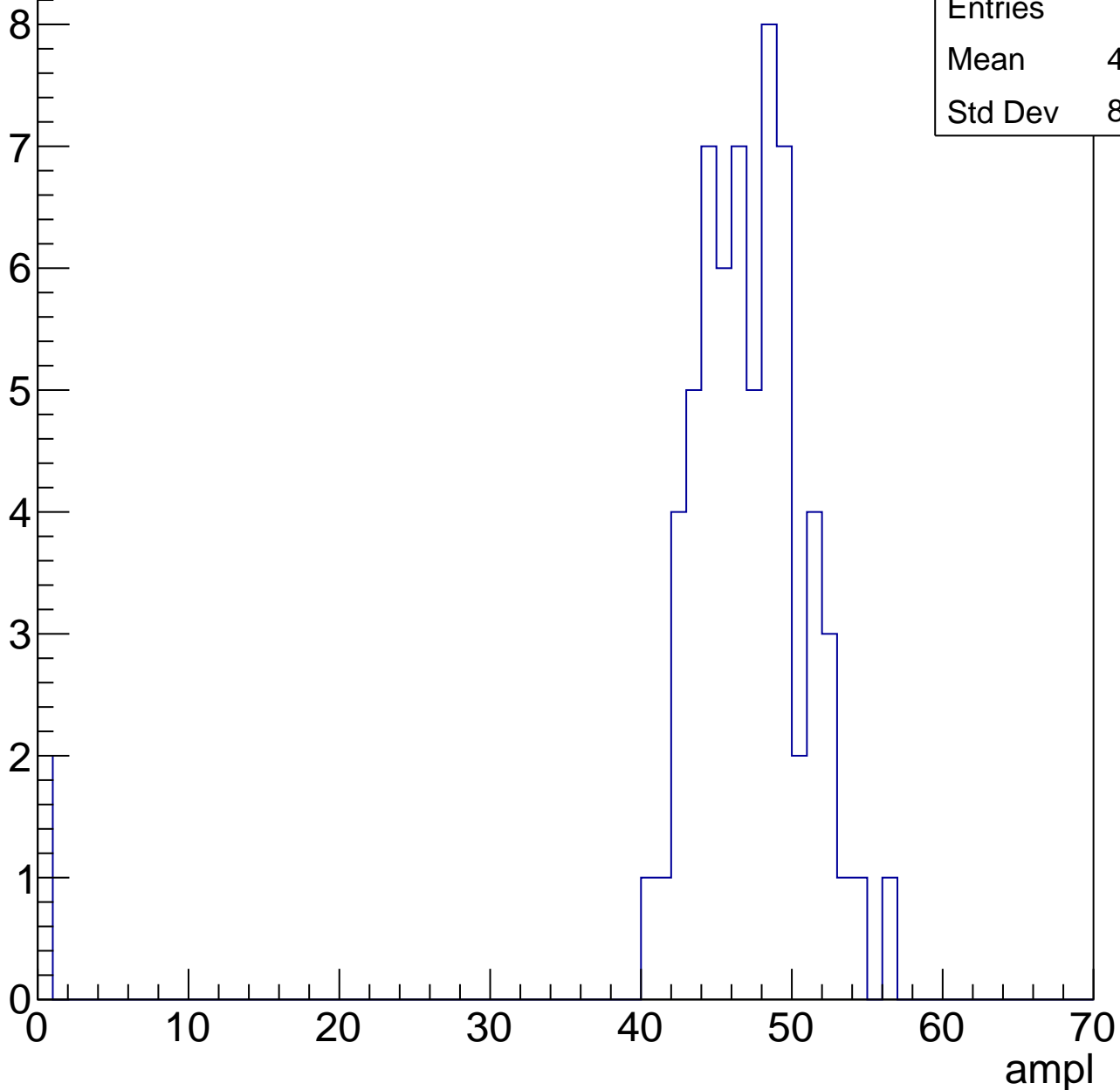


B1L103S, U2-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

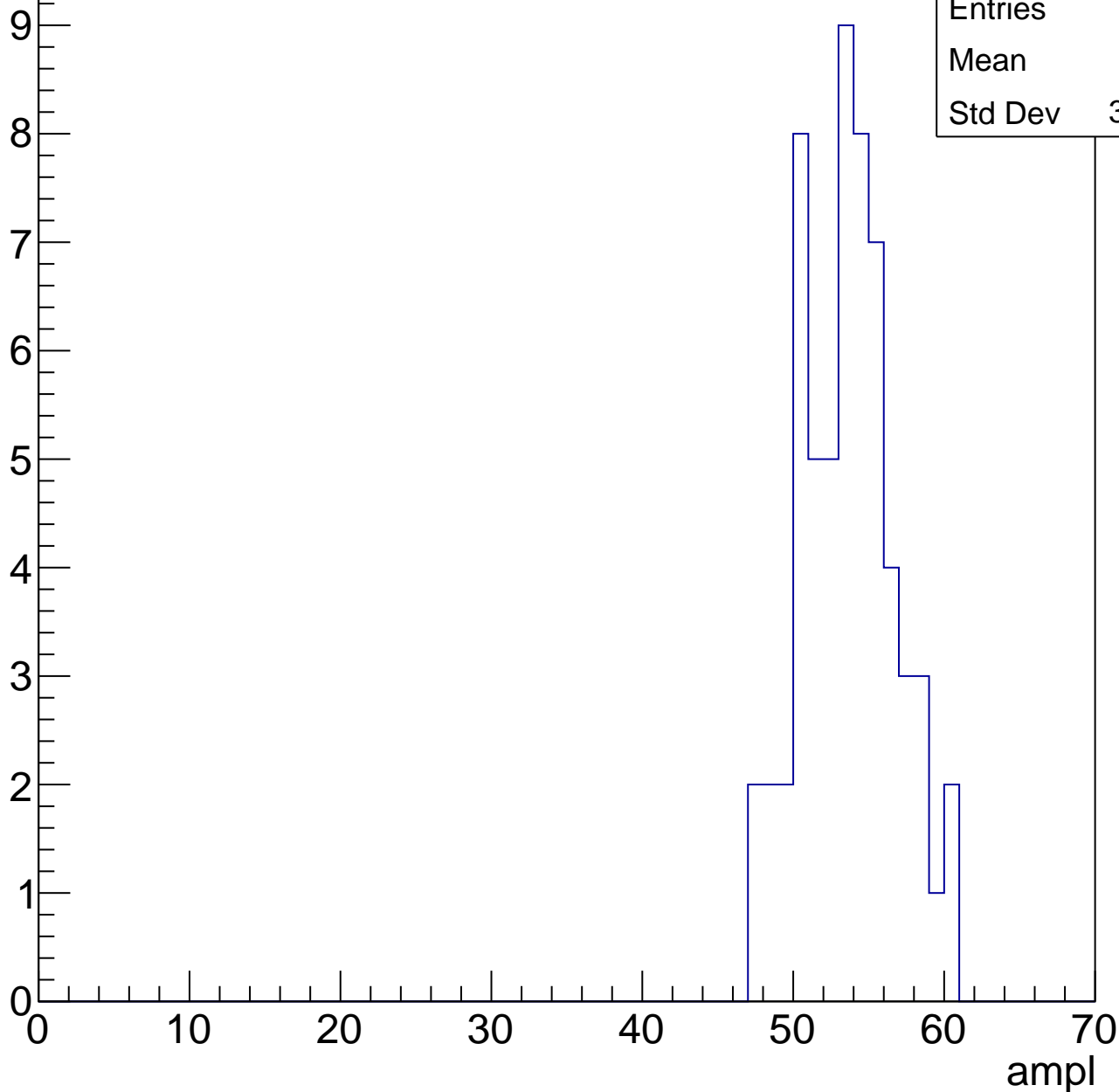
Entries	65
Mean	45.37
Std Dev	8.738



B1L103S, U2-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

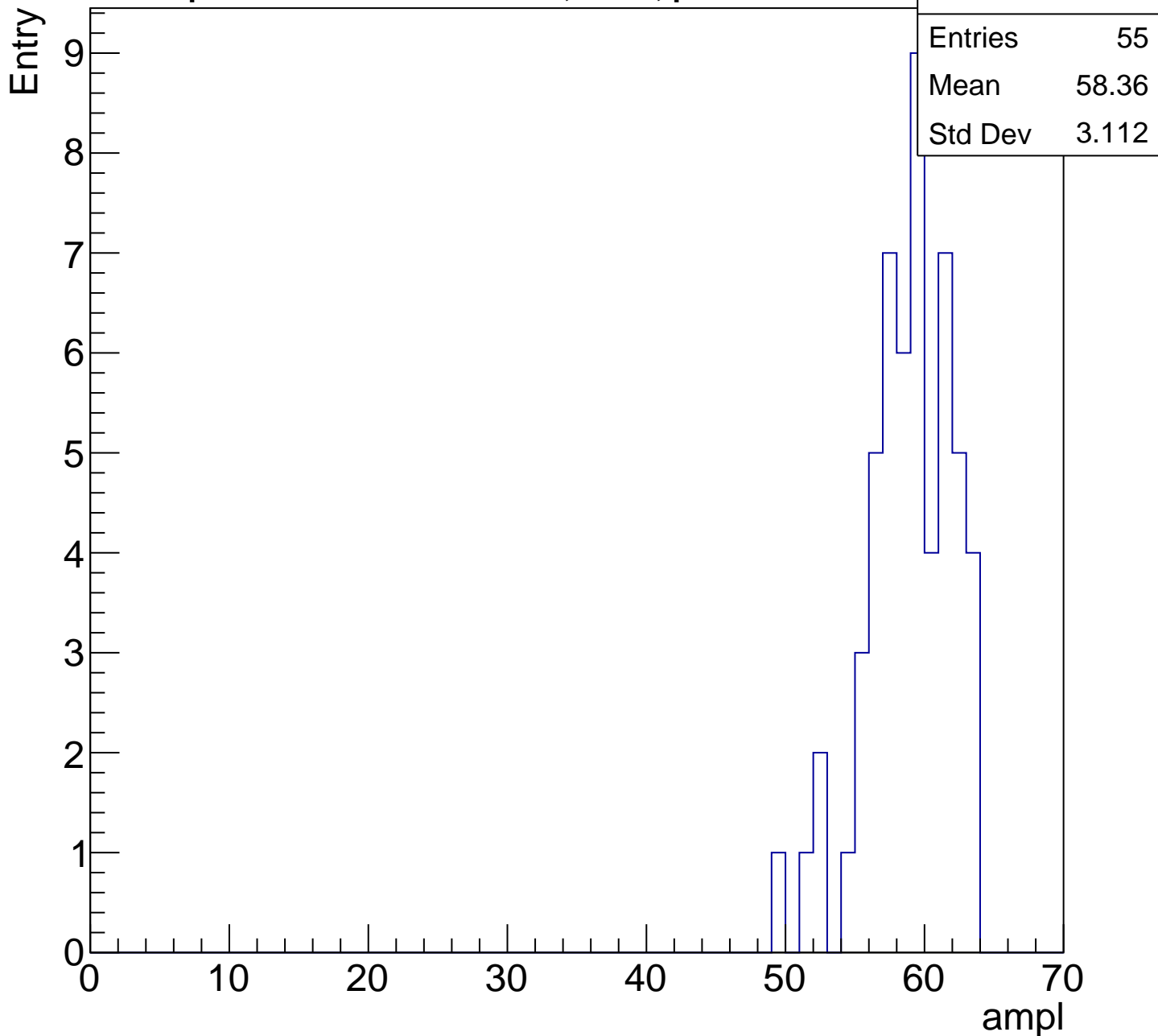
Entry



Entries	61
Mean	53.2
Std Dev	3.077

B1L103S, U2-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

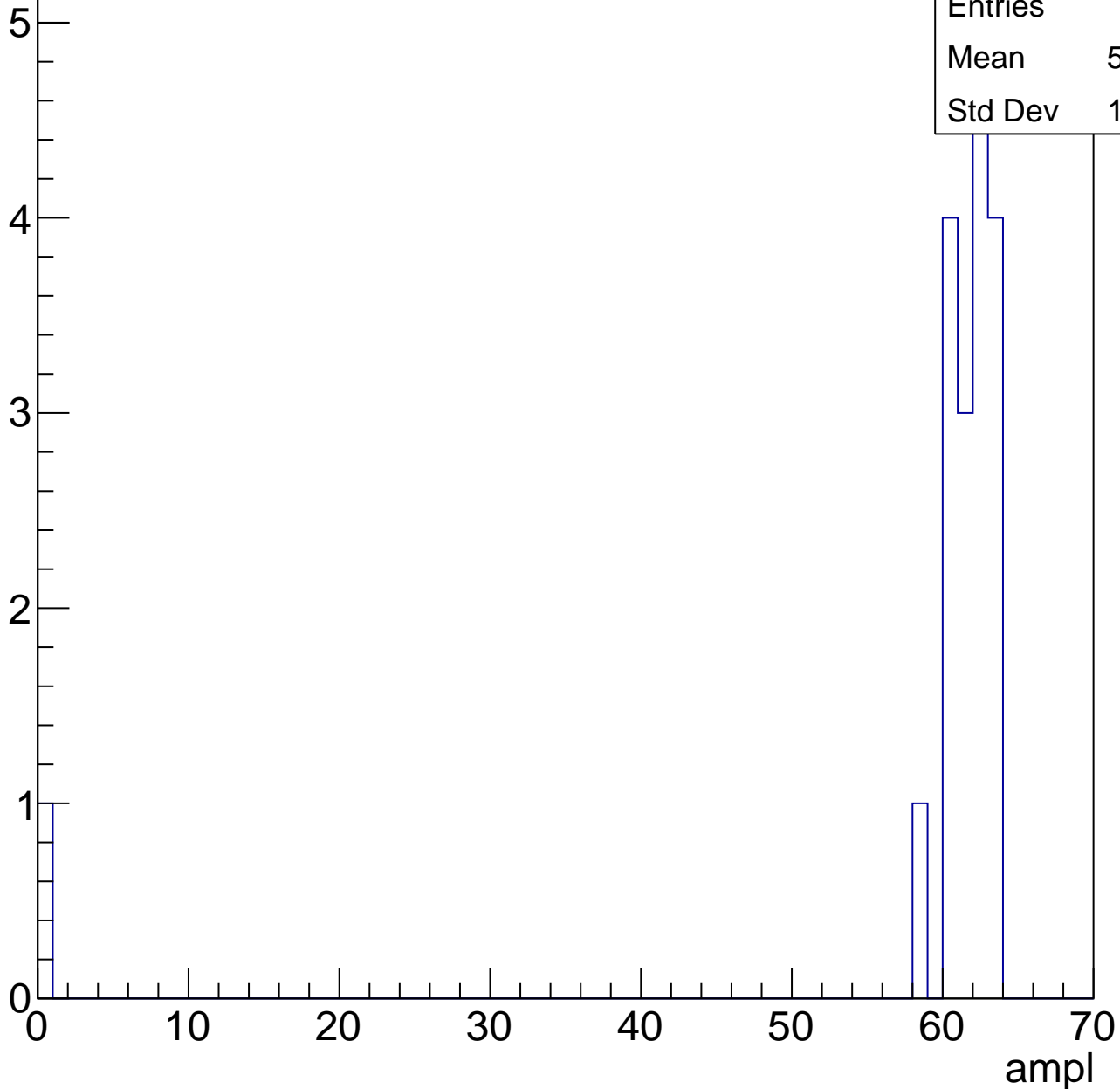


B1L103S, U2-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.94
Std Dev	14.12



B1L103S, U2-ch63, adc7

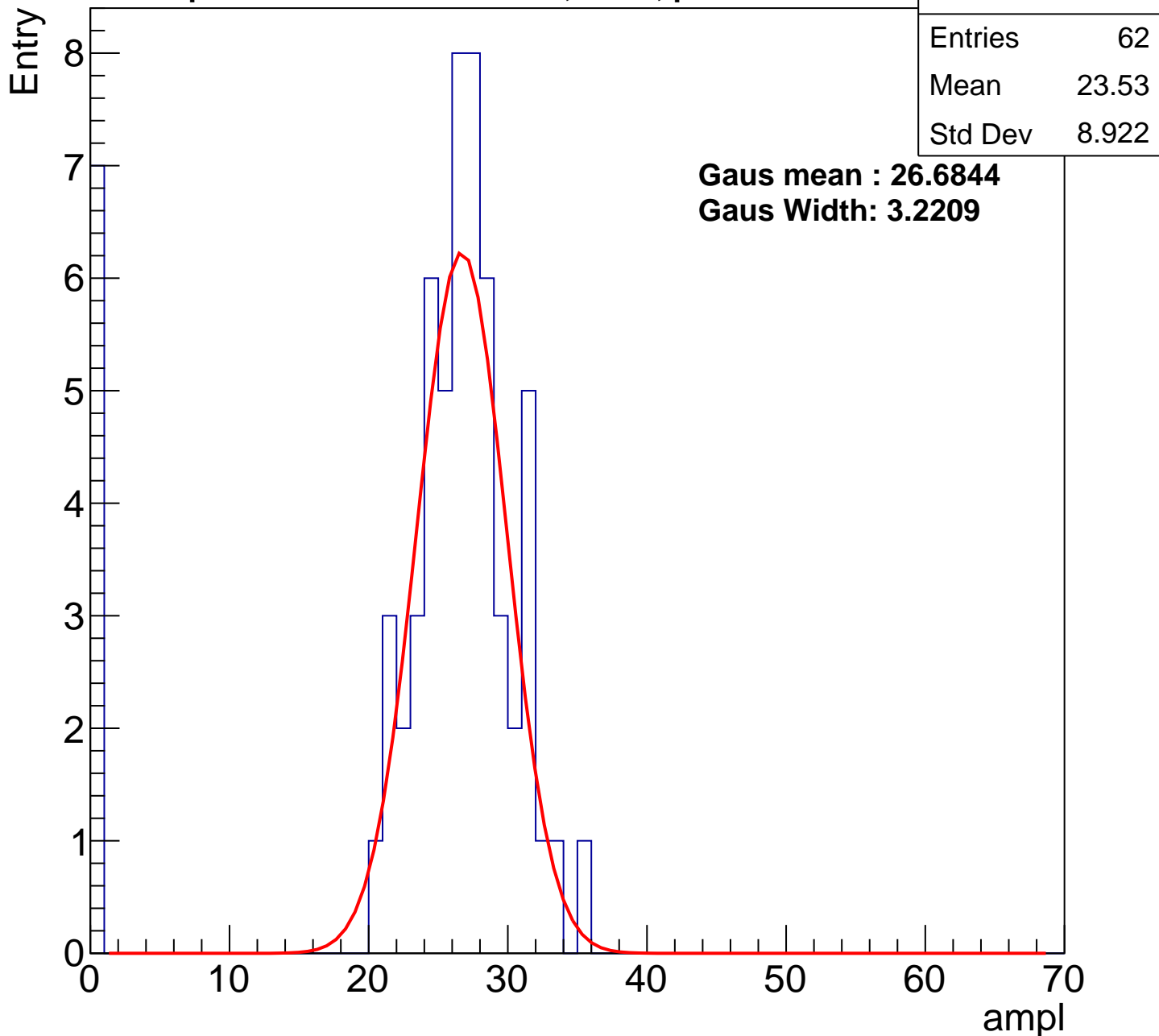
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch64, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch64, adc1

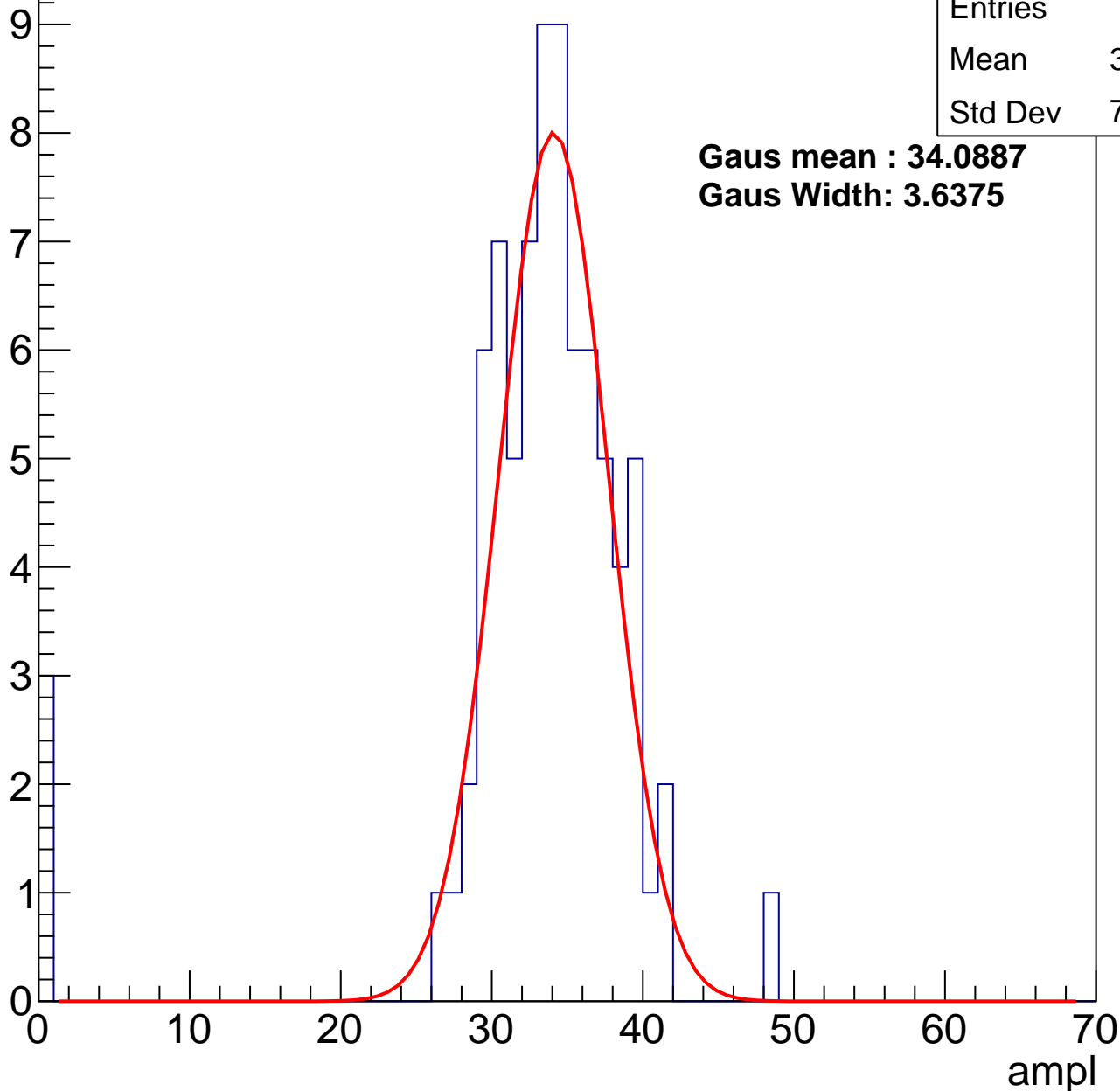
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	32.54
Std Dev	7.433

Gaus mean : 34.0887

Gaus Width: 3.6375



B1L103S, U2-ch64, adc2

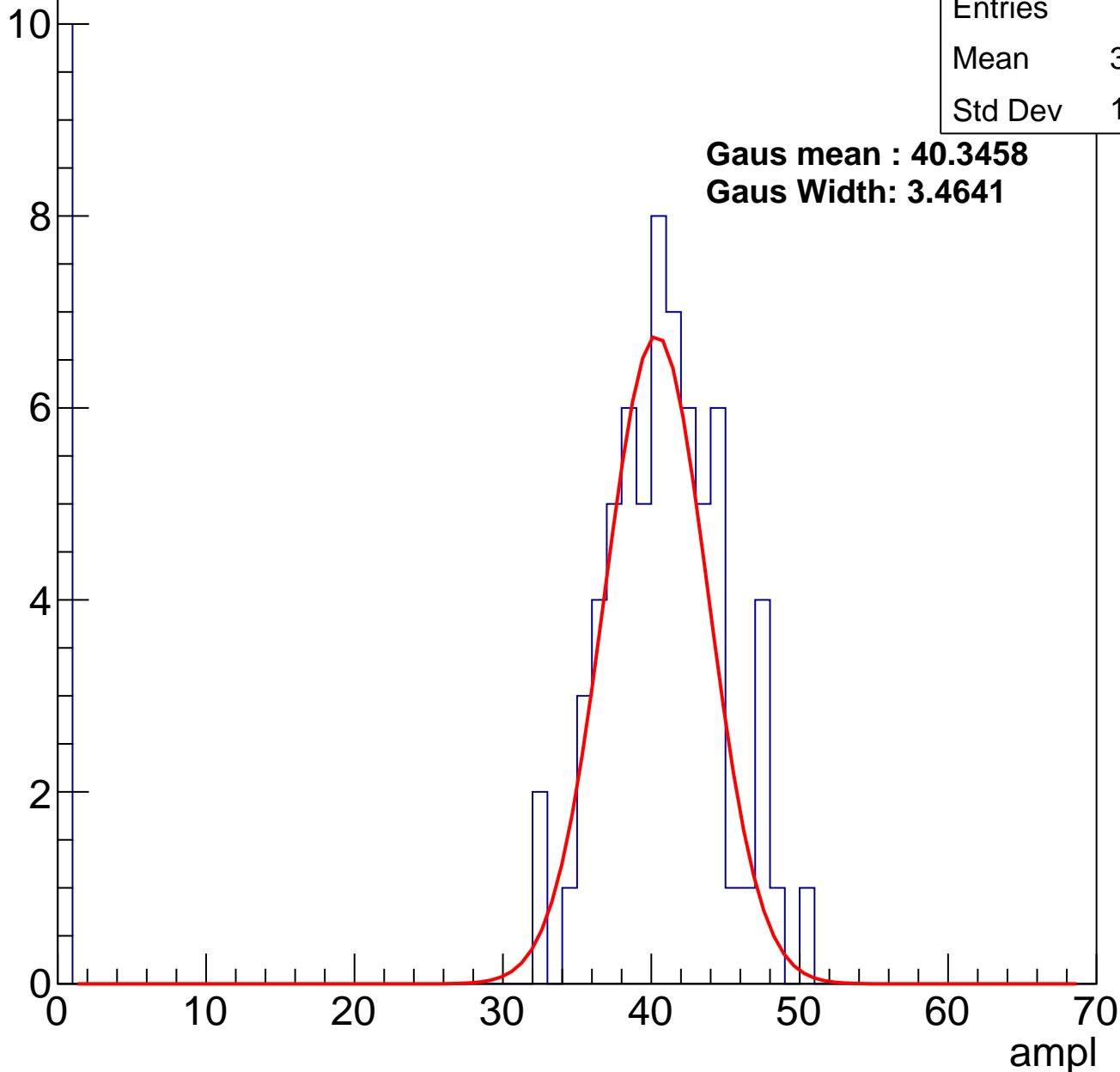
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	35.13
Std Dev	14.13

Gaus mean : 40.3458

Gaus Width: 3.4641

Entry

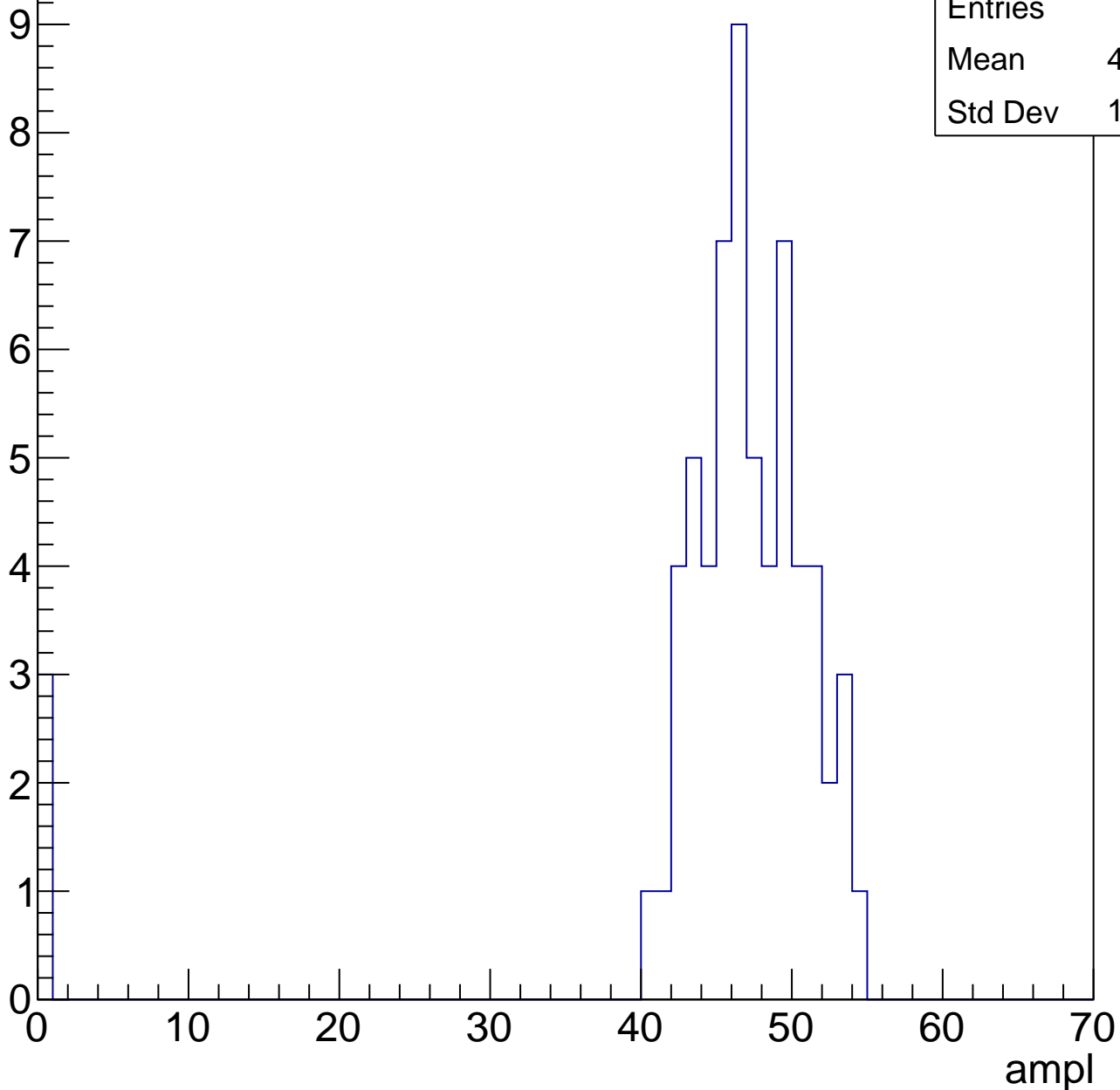


B1L103S, U2-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	44.69
Std Dev	10.43

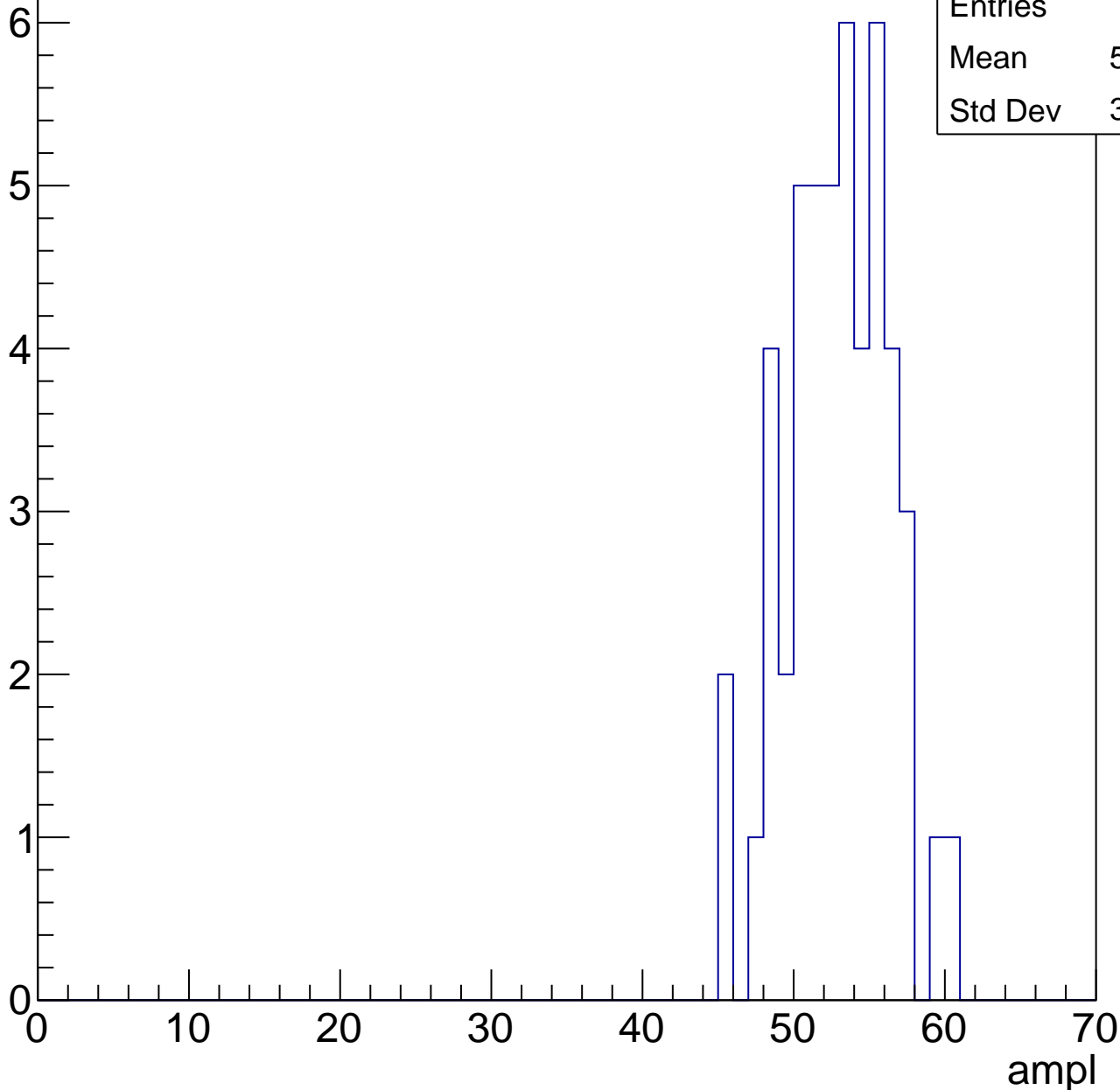


B1L103S, U2-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	52.45
Std Dev	3.345

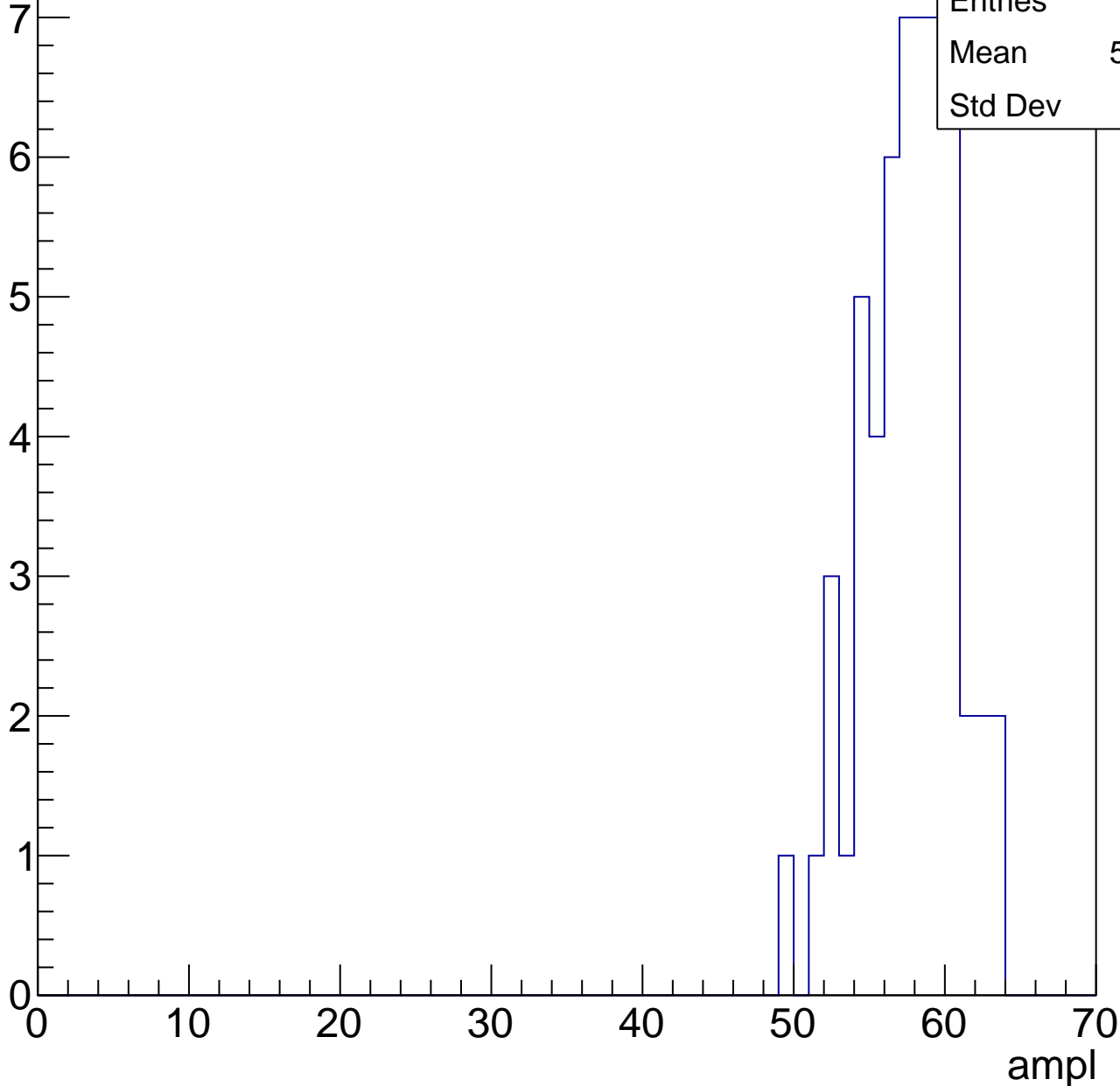


B1L103S, U2-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.18
Std Dev	3.04

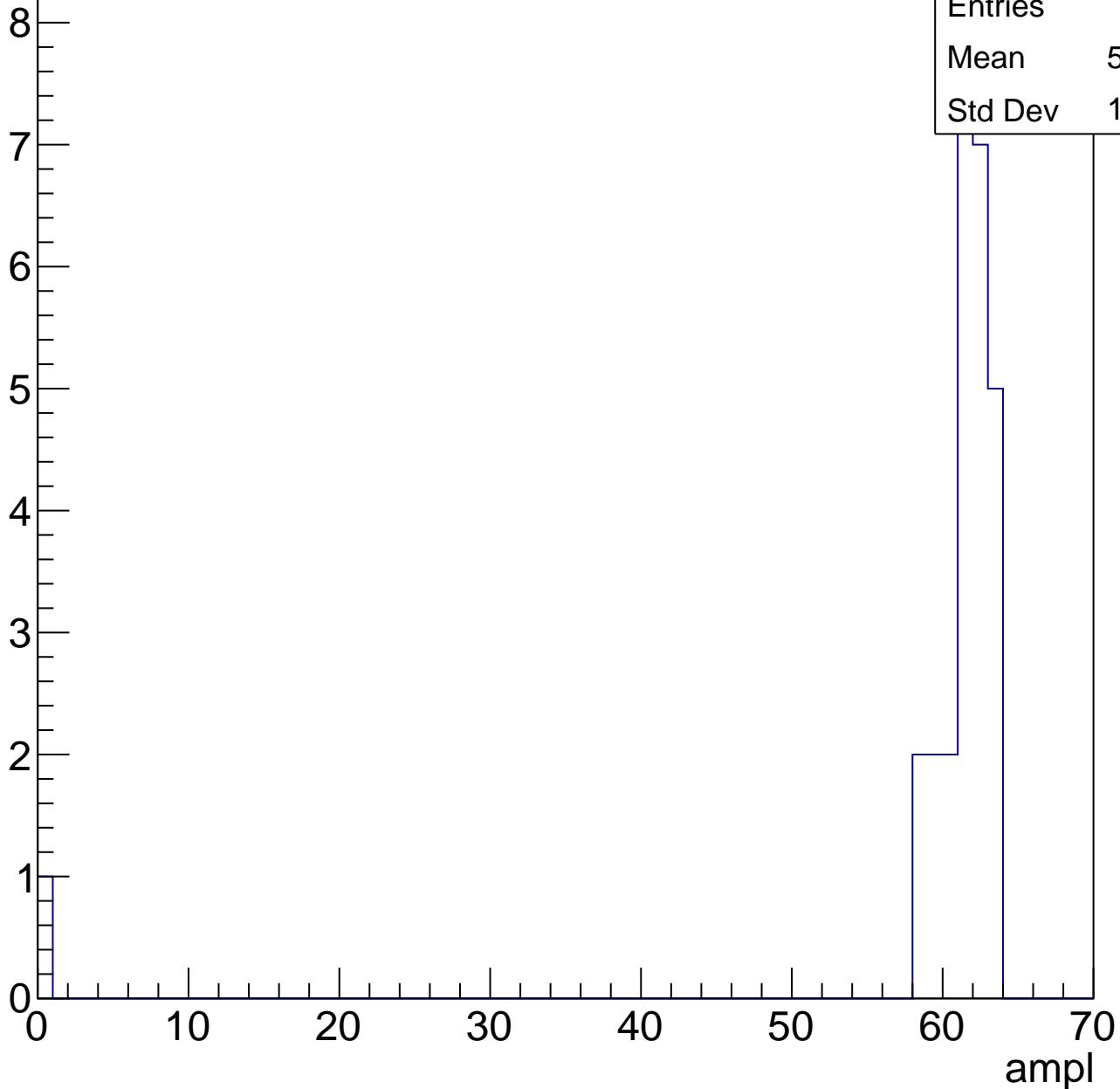


B1L103S, U2-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	58.93
Std Dev	11.64

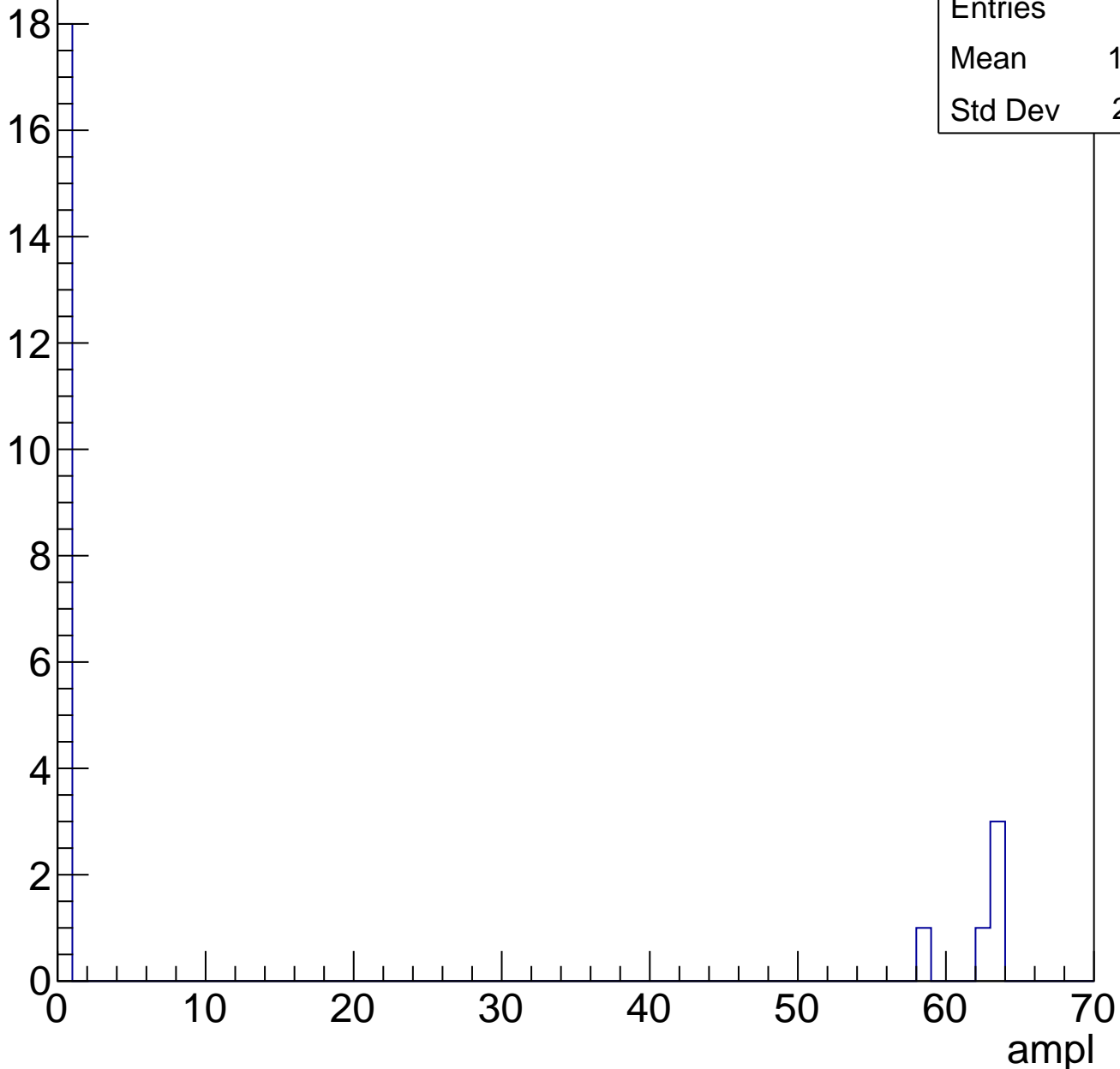


B1L103S, U2-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	13.43
Std Dev	25.51

Entry



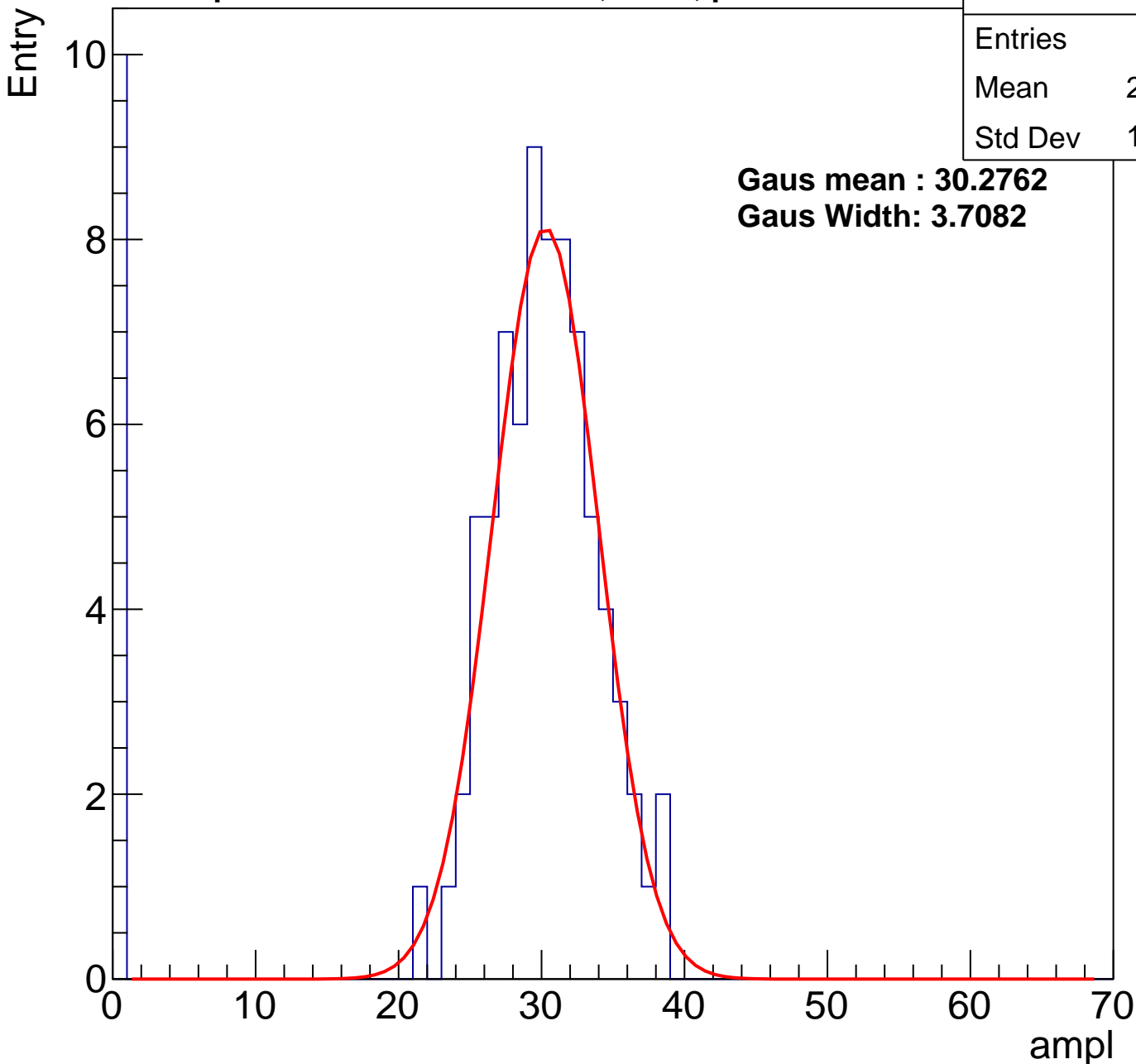
B1L103S, U2-ch65, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	26.37
Std Dev	10.14

Gaus mean : 30.2762

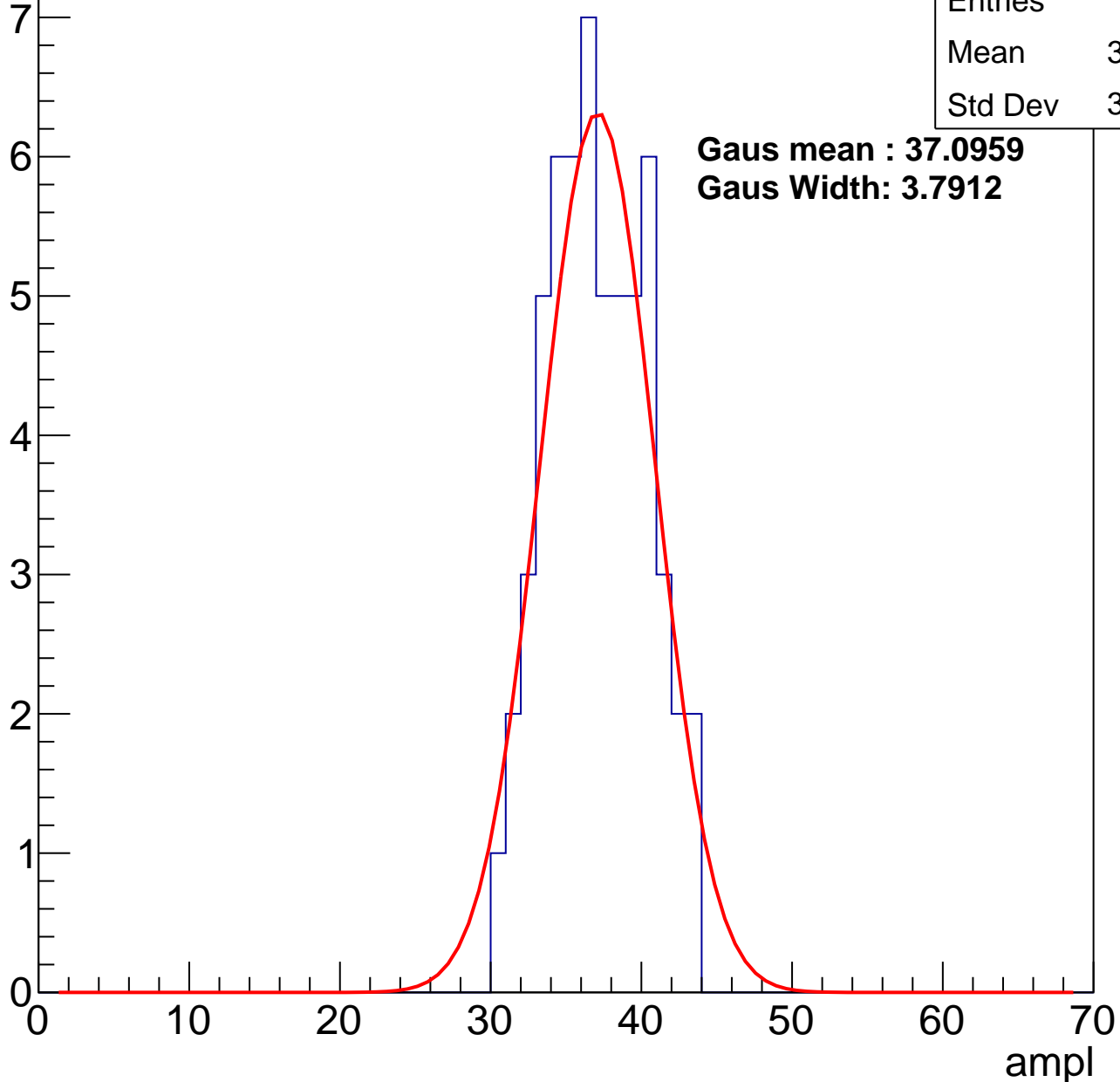
Gaus Width: 3.7082



B1L103S, U2-ch65, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	58
Mean	36.59
Std Dev	3.217

B1L103S, U2-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

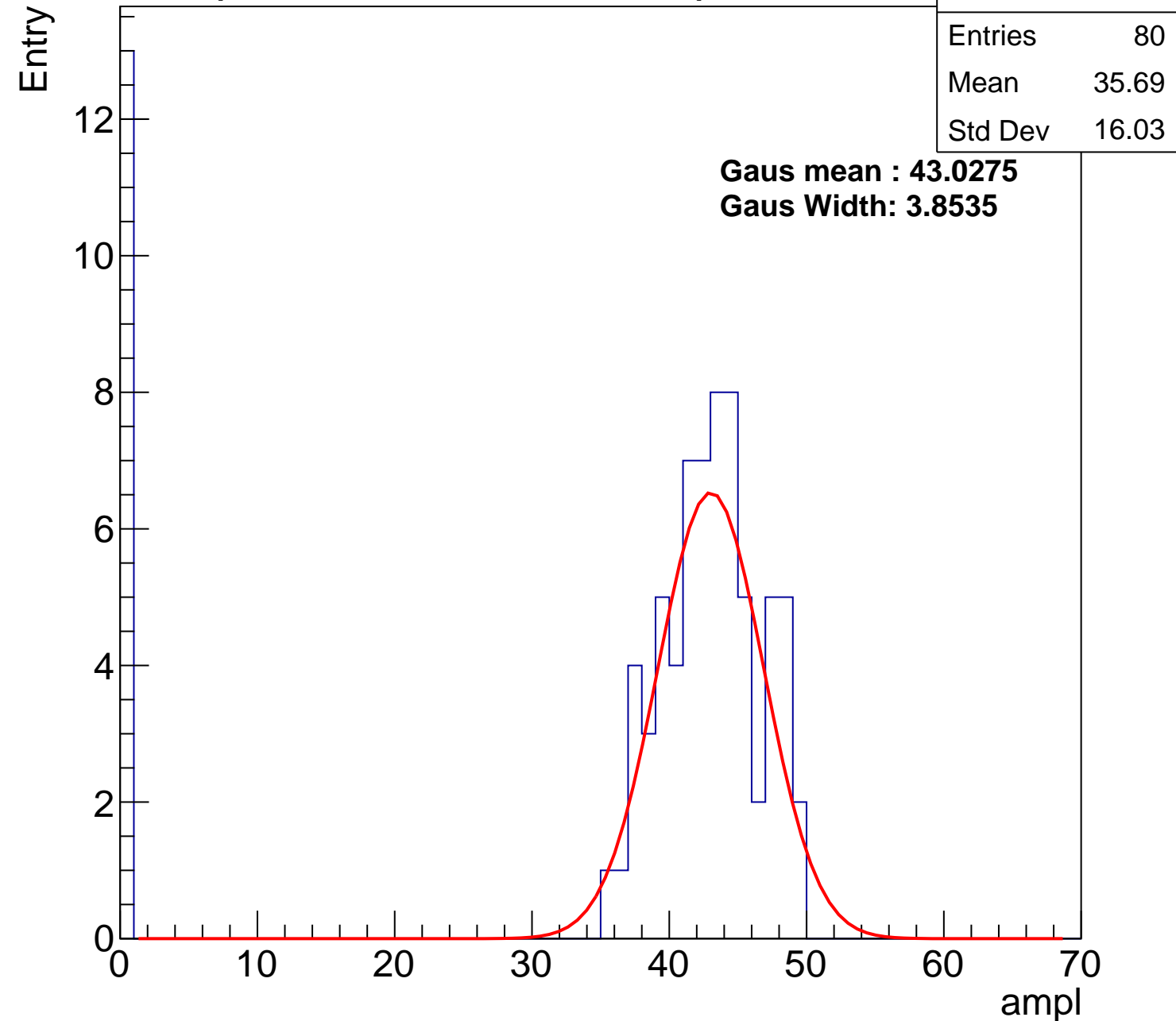
70

ampl

Entries	80
Mean	35.69
Std Dev	16.03

Gaus mean : 43.0275

Gaus Width: 3.8535

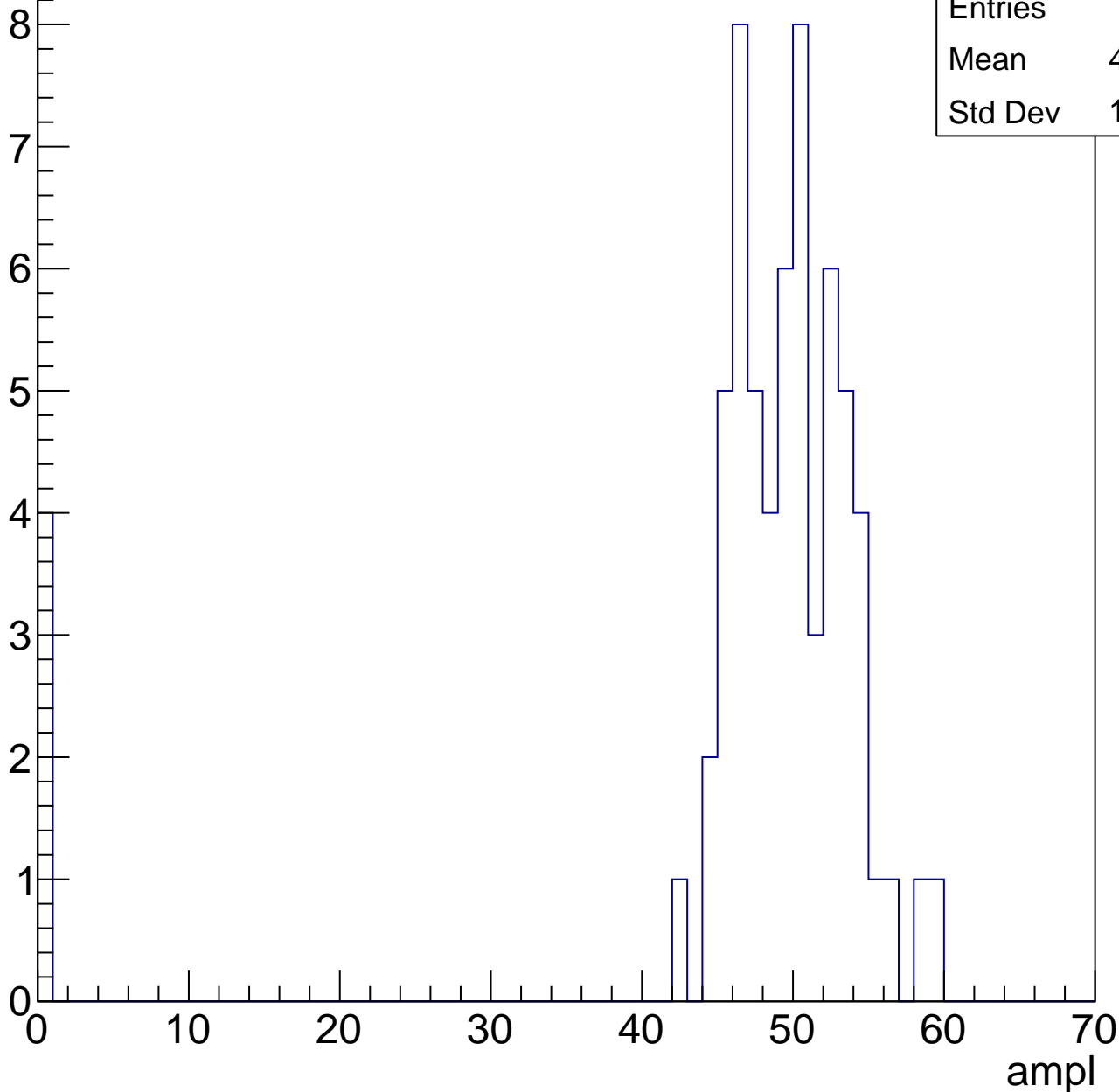


B1L103S, U2-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	46.43
Std Dev	12.38

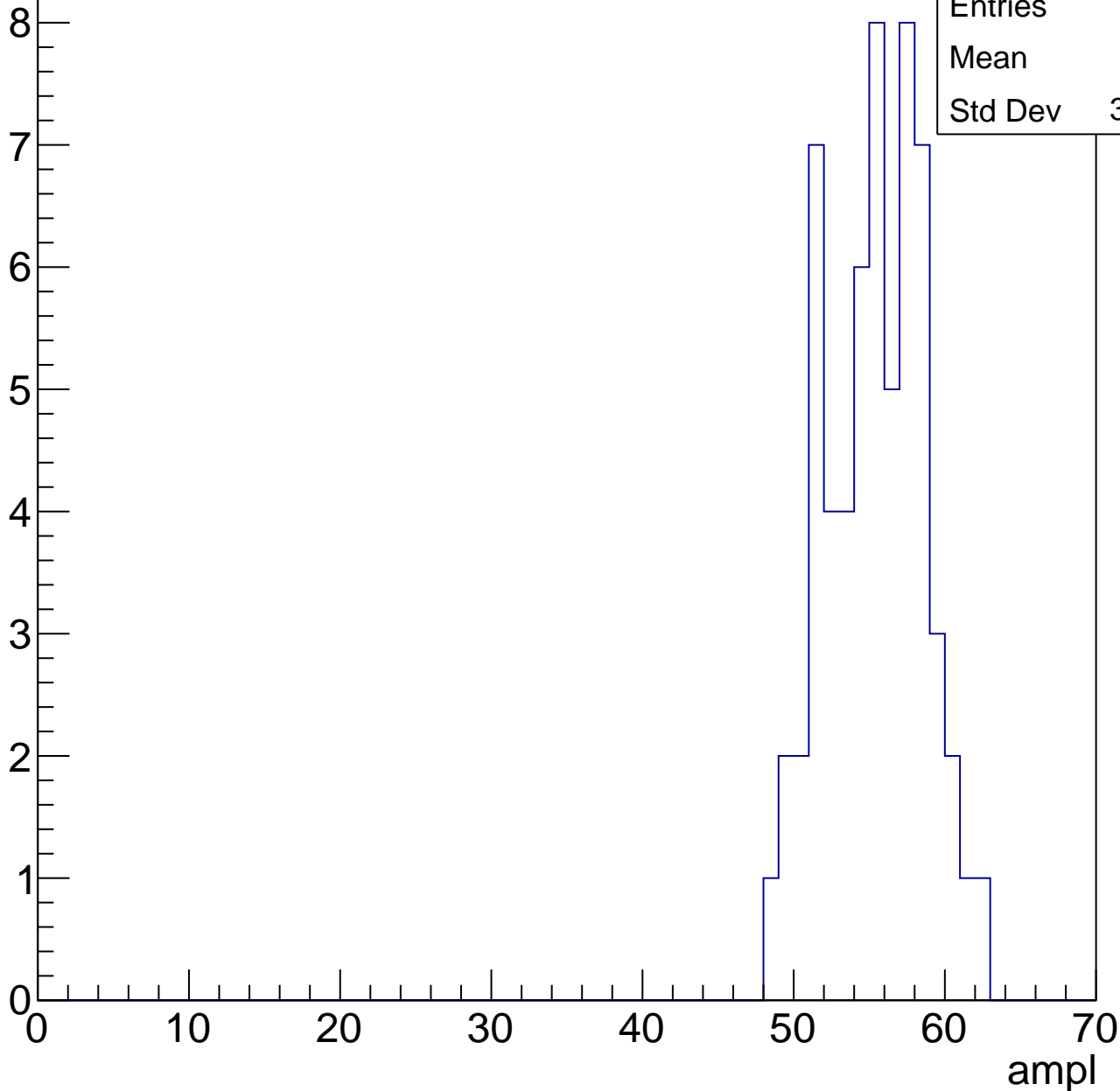


B1L103S, U2-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	54.9
Std Dev	3.197



B1L103S, U2-ch65, adc5

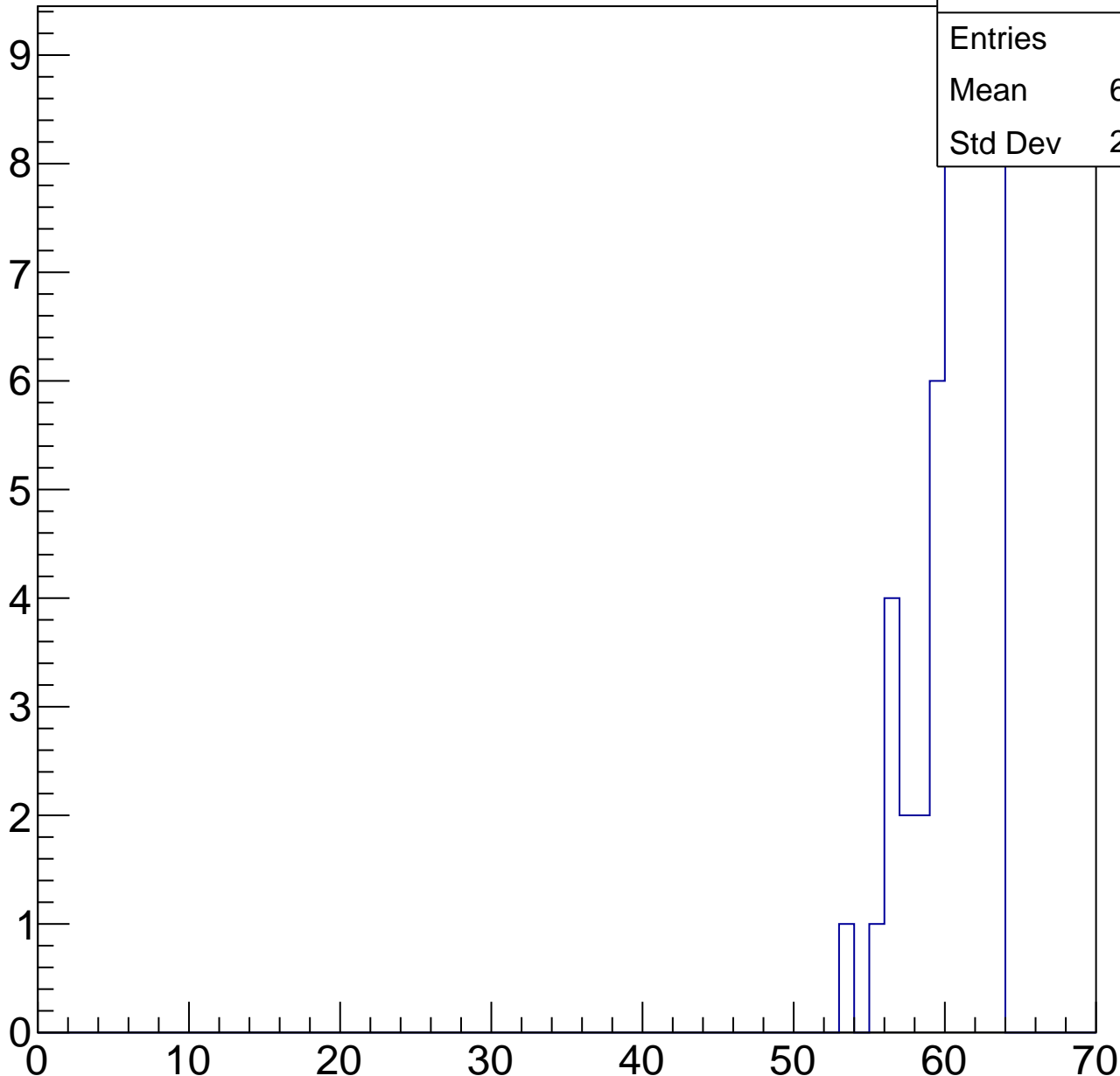
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	60.12
Std Dev	2.413

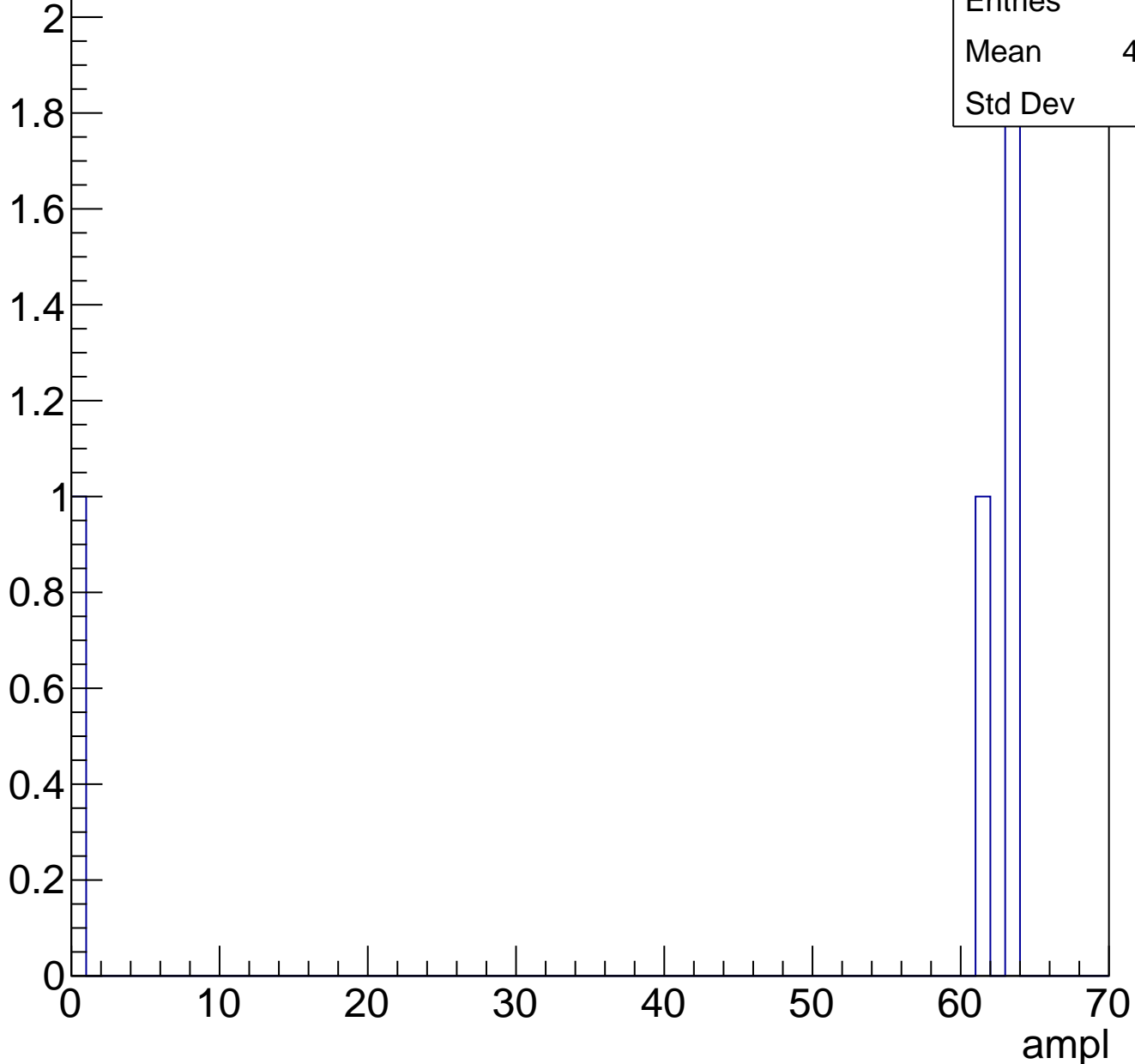
ampl



B1L103S, U2-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



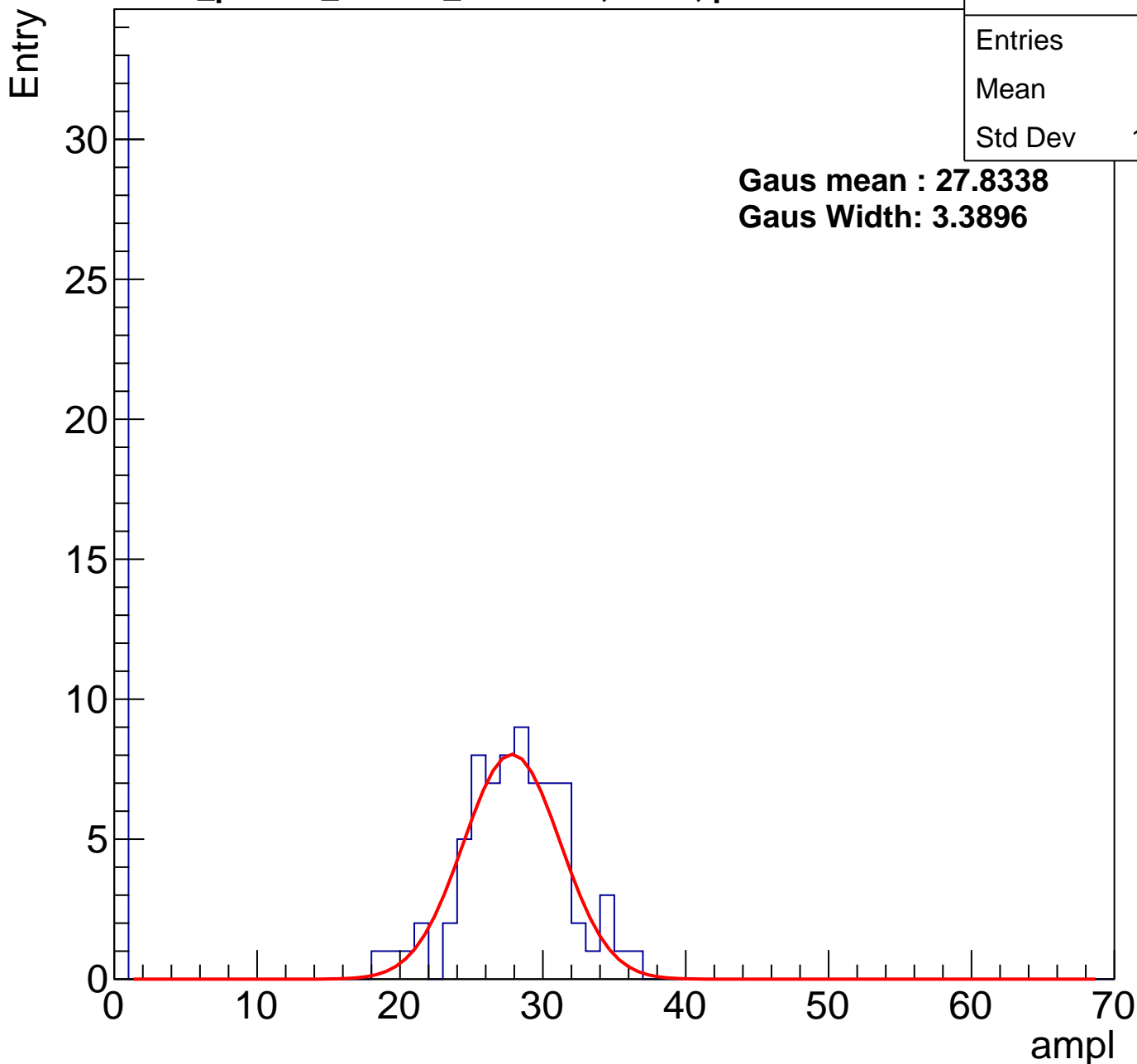
B1L103S, U2-ch66, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	19.01
Std Dev	13.13

Gaus mean : 27.8338

Gaus Width: 3.3896



B1L103S, U2-ch66, adc1

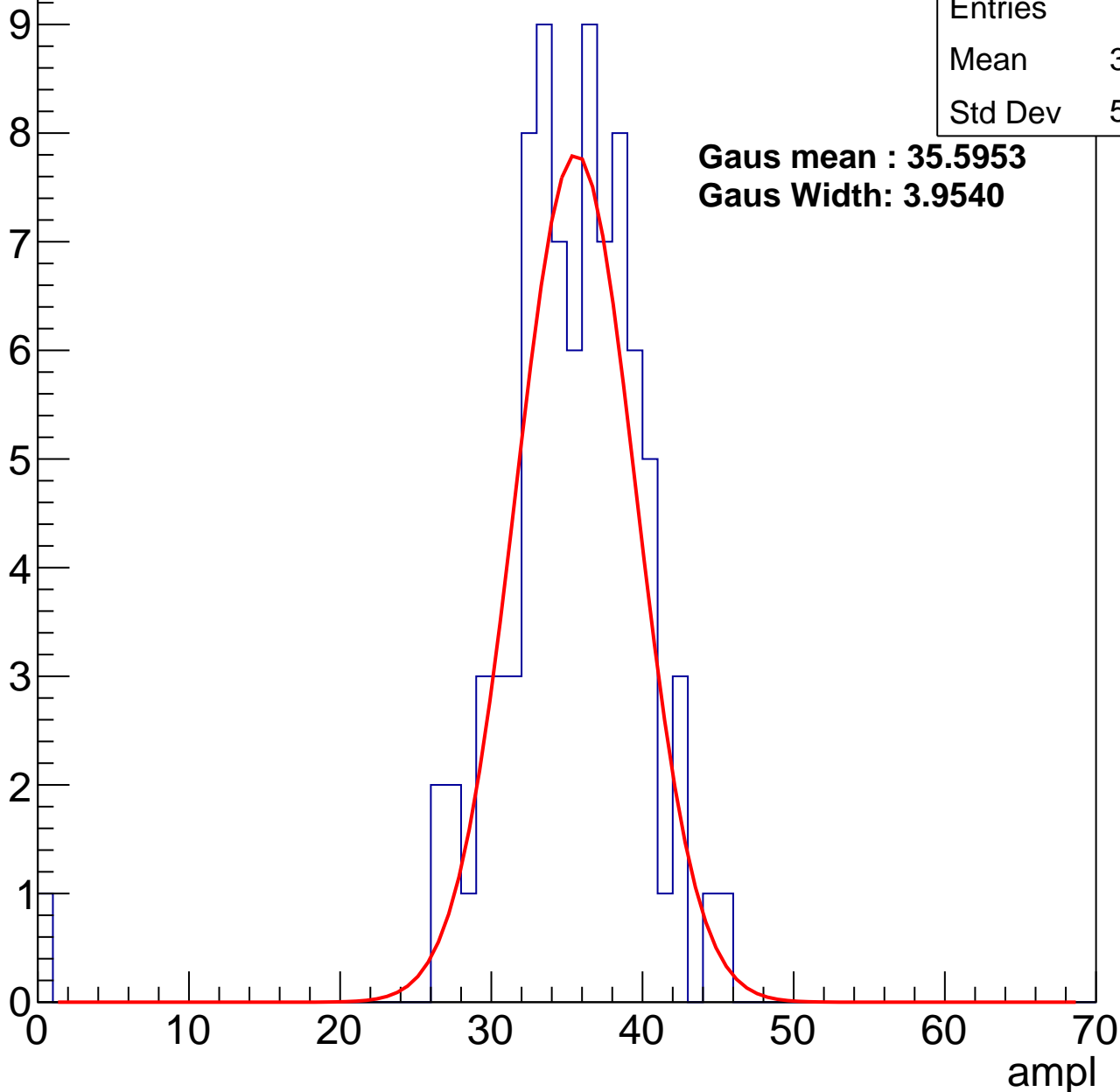
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	34.67
Std Dev	5.493

Gaus mean : 35.5953

Gaus Width: 3.9540



B1L103S, U2-ch66, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	55
Mean	31.45
Std Dev	17.69

Gaus mean : 41.7503

Gaus Width: 3.2834

Entry

12

10

8

6

4

2

0

0

10

20

30

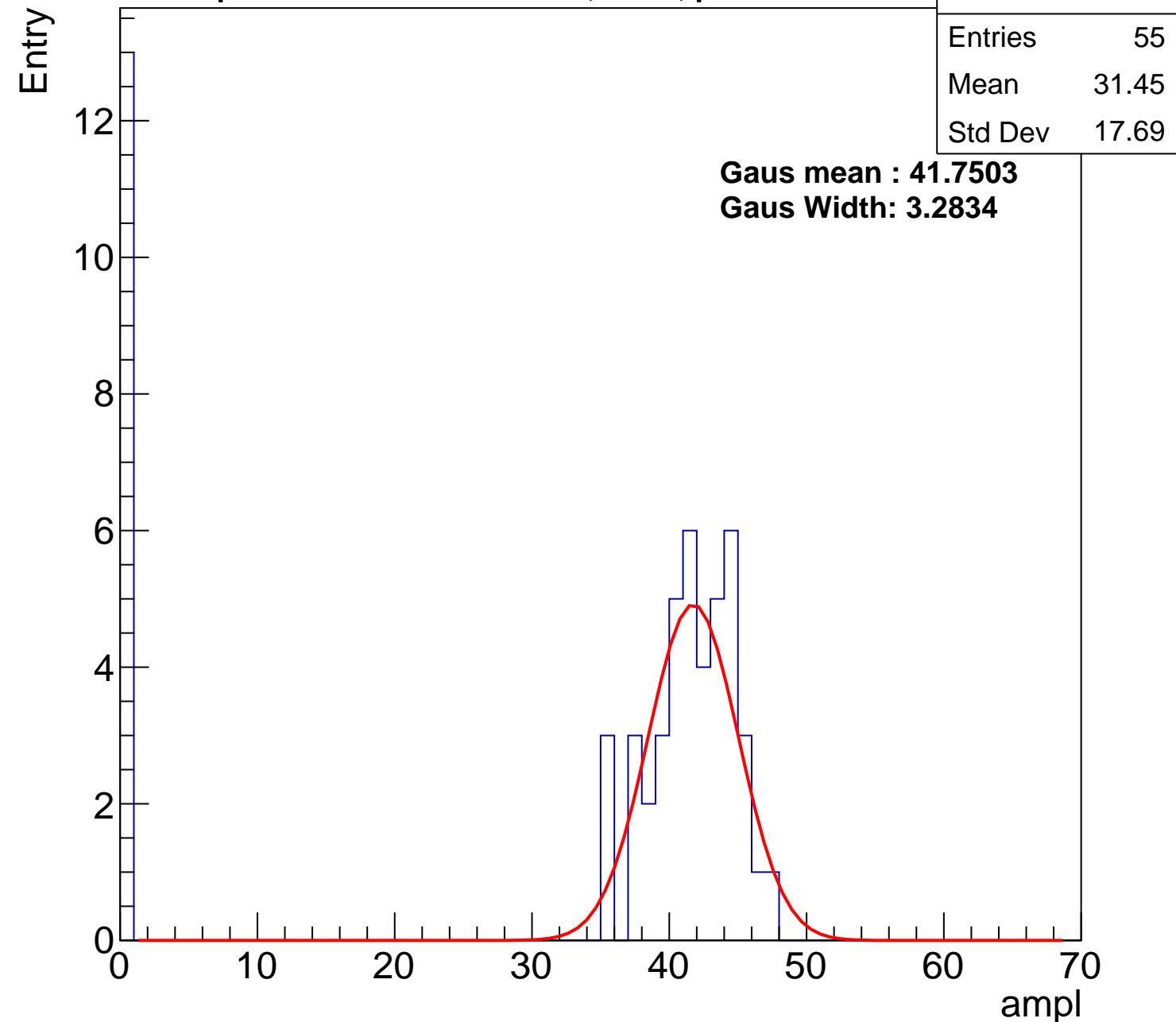
40

50

60

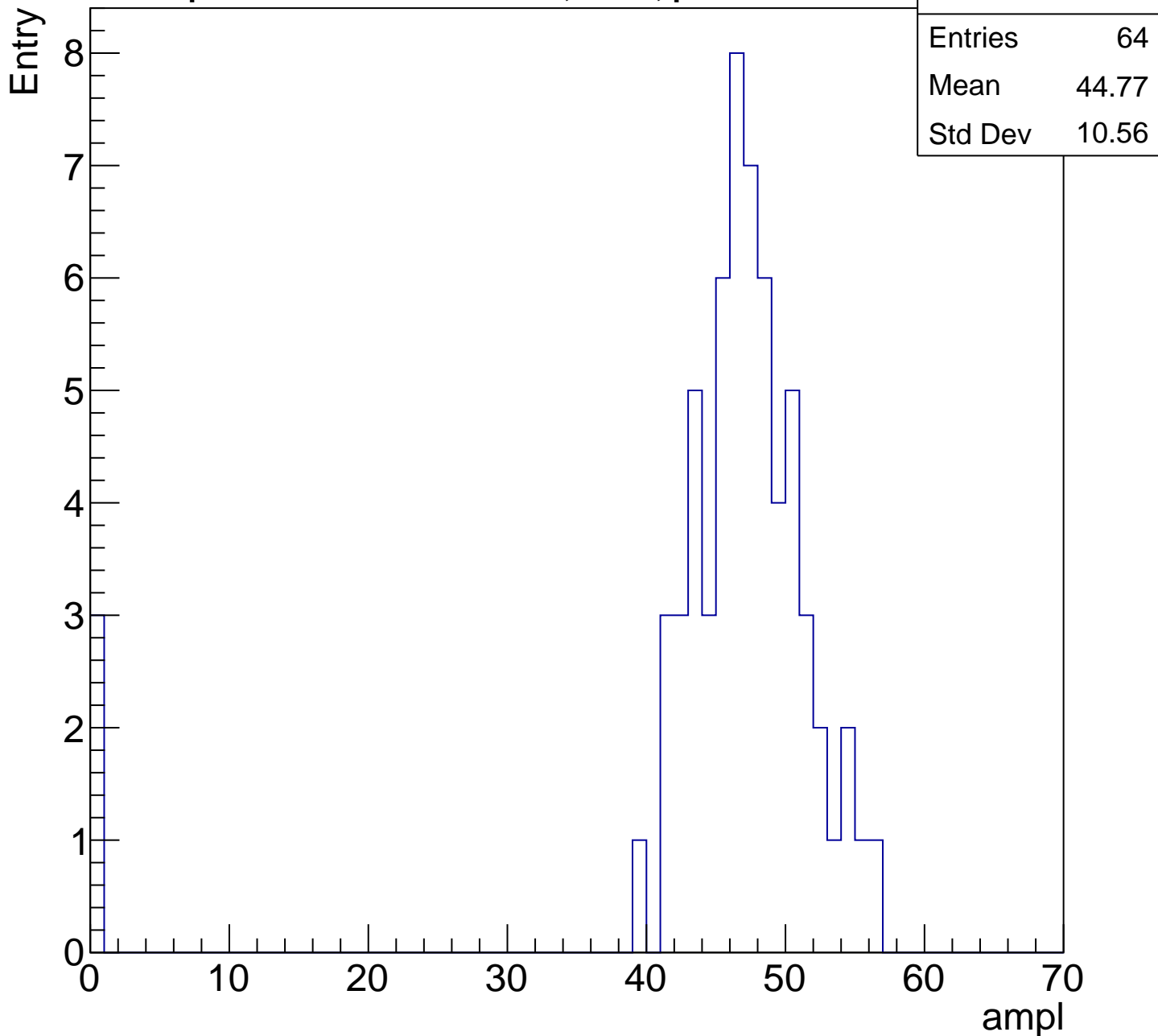
70

ampl



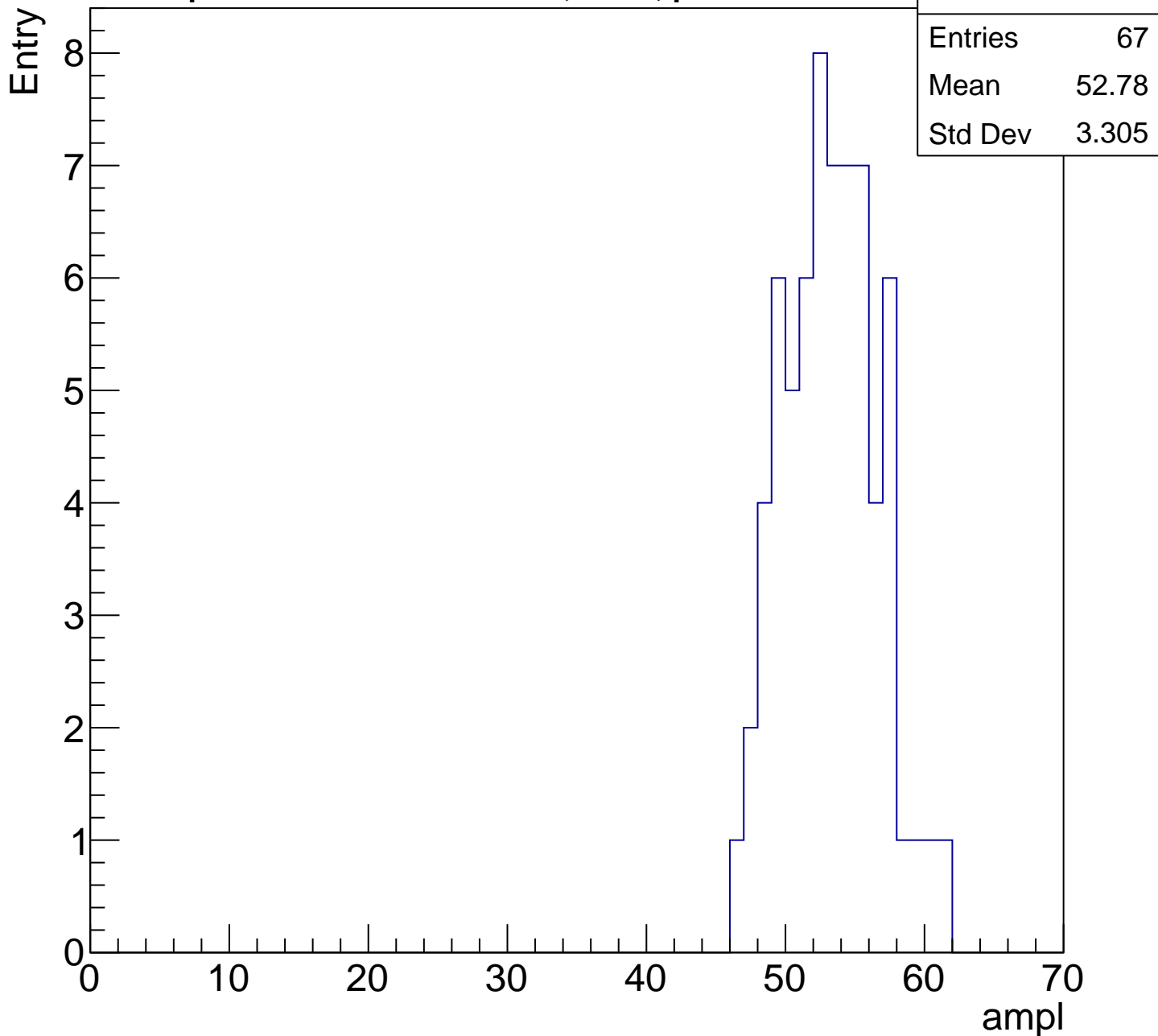
B1L103S, U2-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

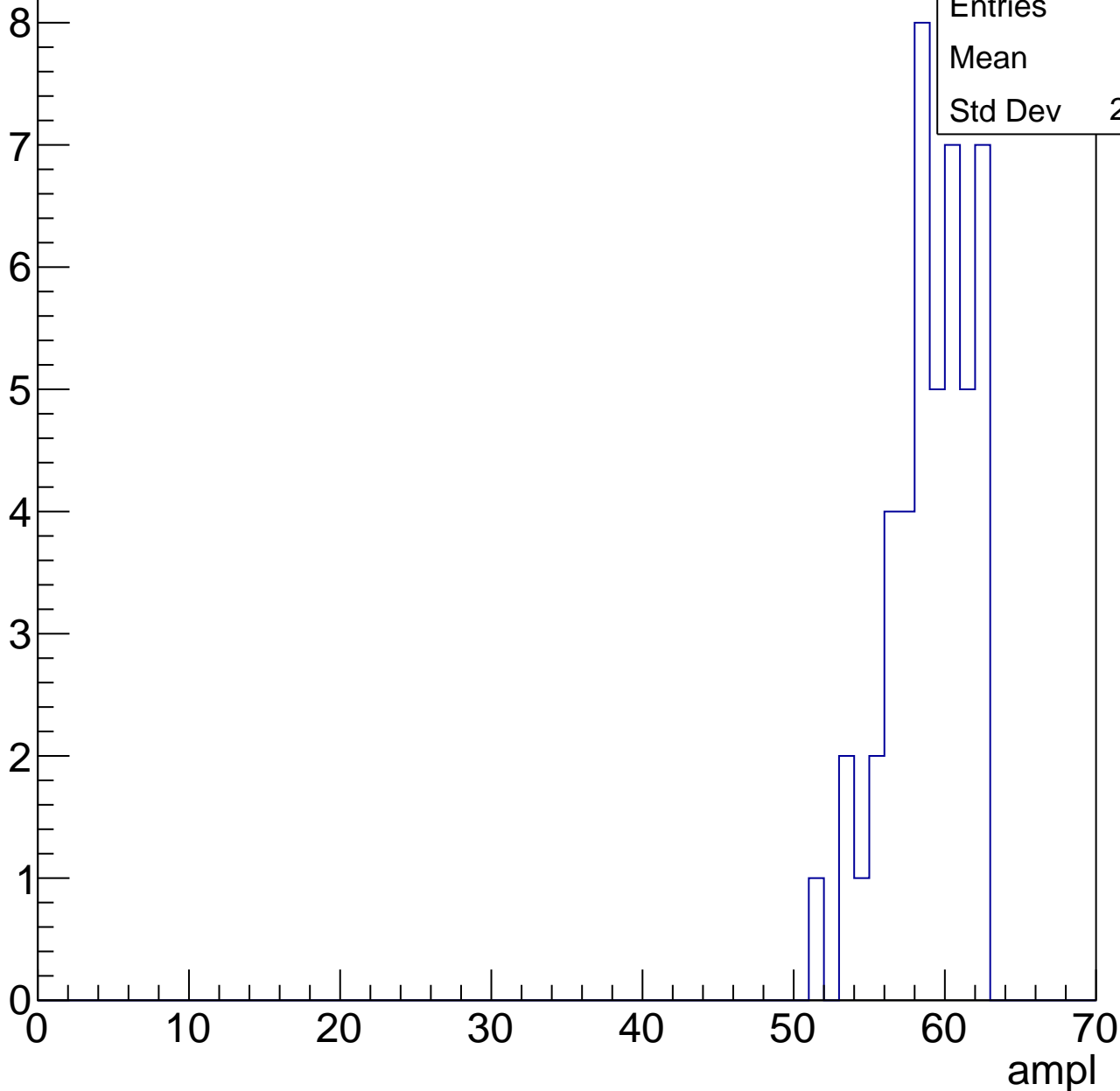


B1L103S, U2-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.5
Std Dev	2.684

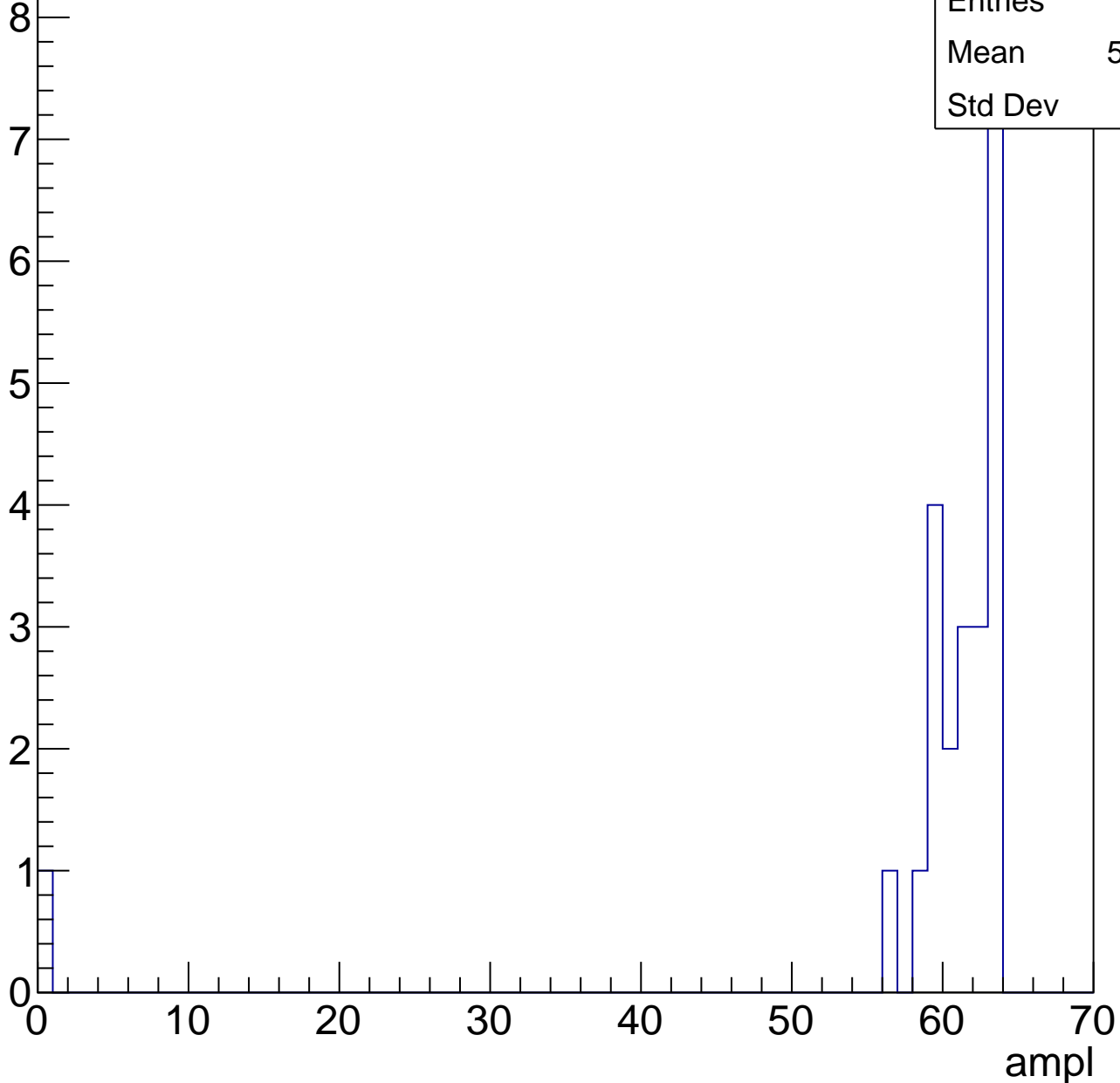


B1L103S, U2-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

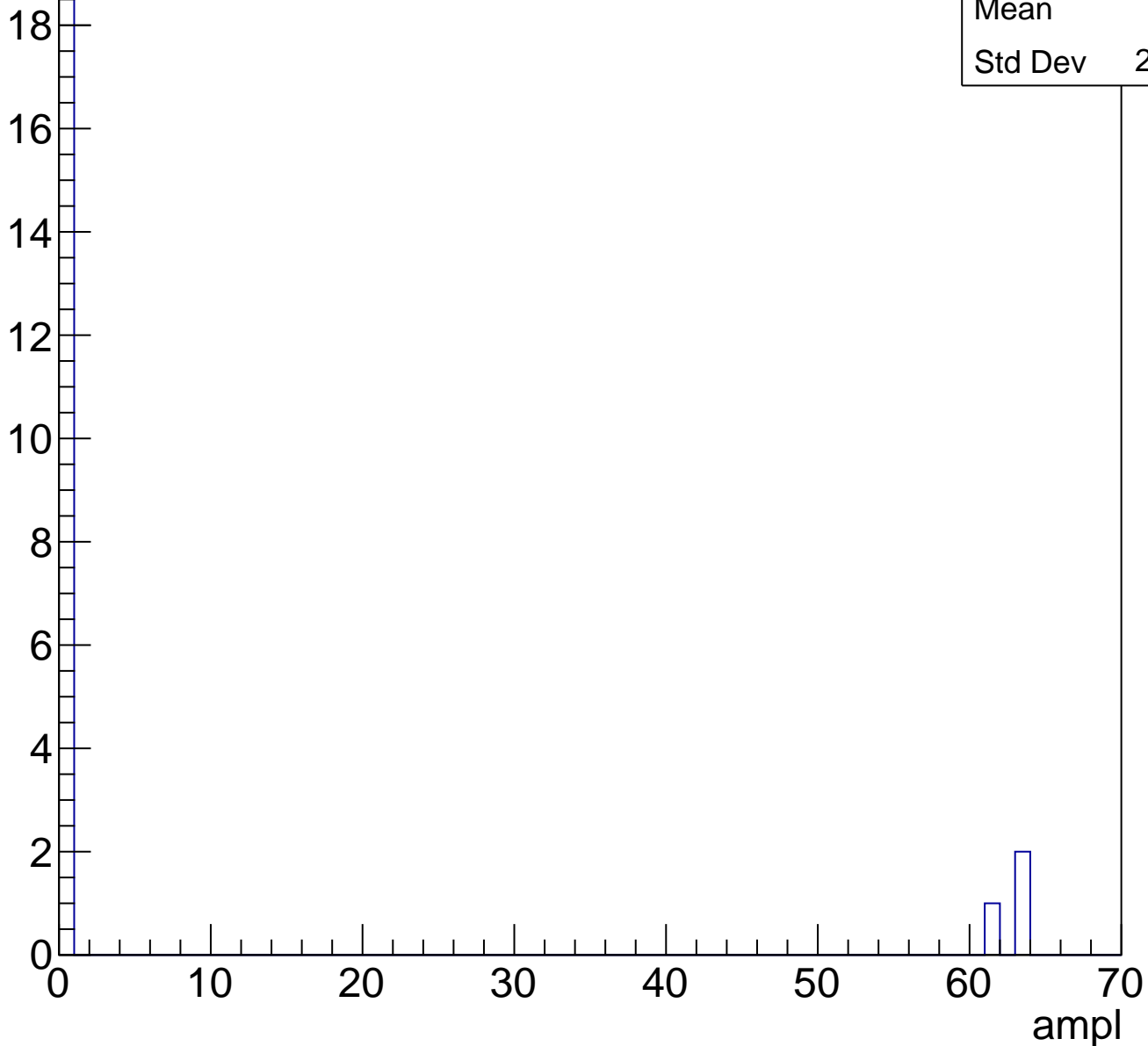
Entries	23
Mean	58.39
Std Dev	12.6



B1L103S, U2-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	22
Mean	8.5
Std Dev	21.39

B1L103S, U2-ch67, adc0

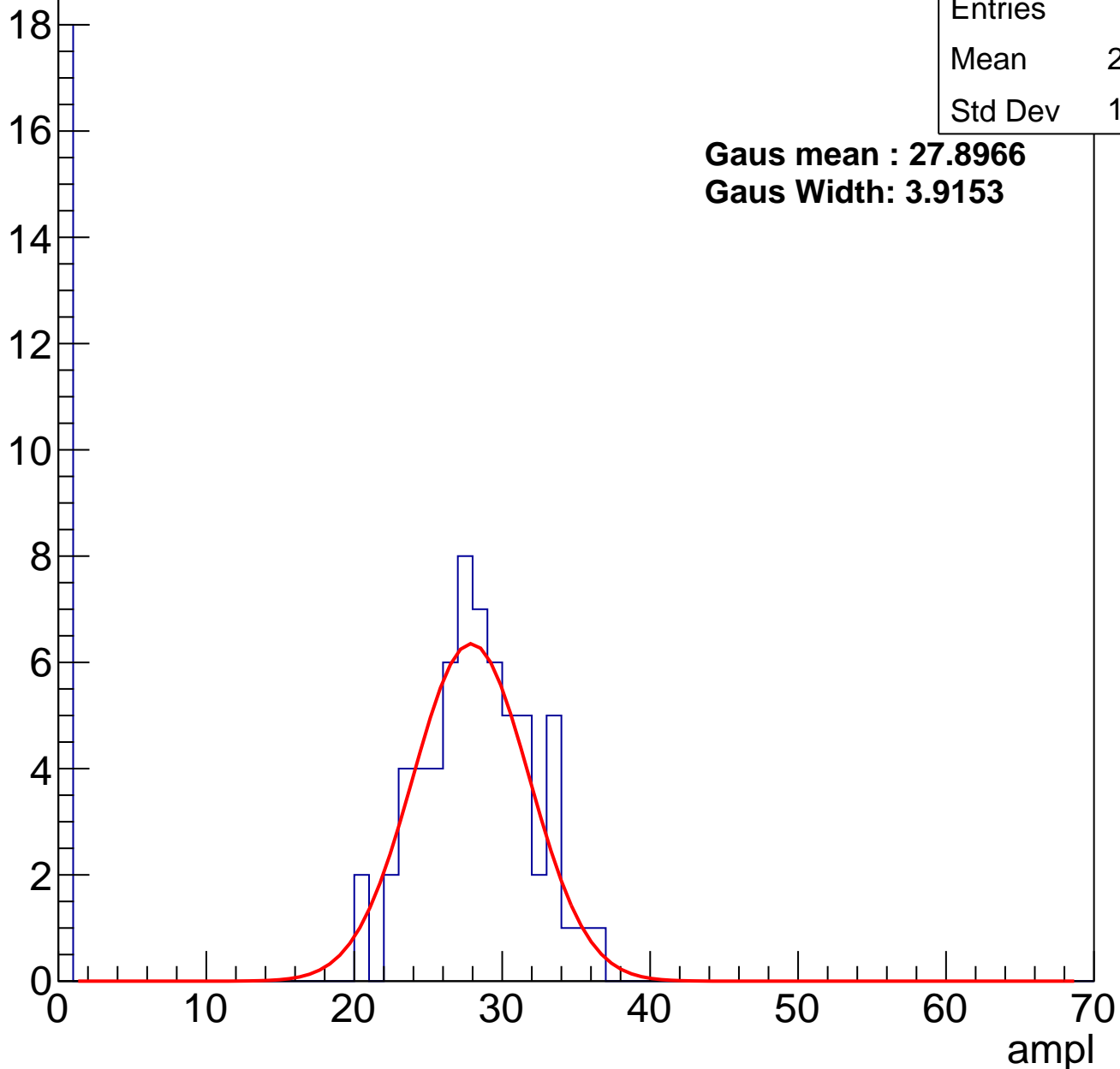
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	21.64
Std Dev	11.99

Gaus mean : 27.8966

Gaus Width: 3.9153

Entry



B1L103S, U2-ch67, adc1

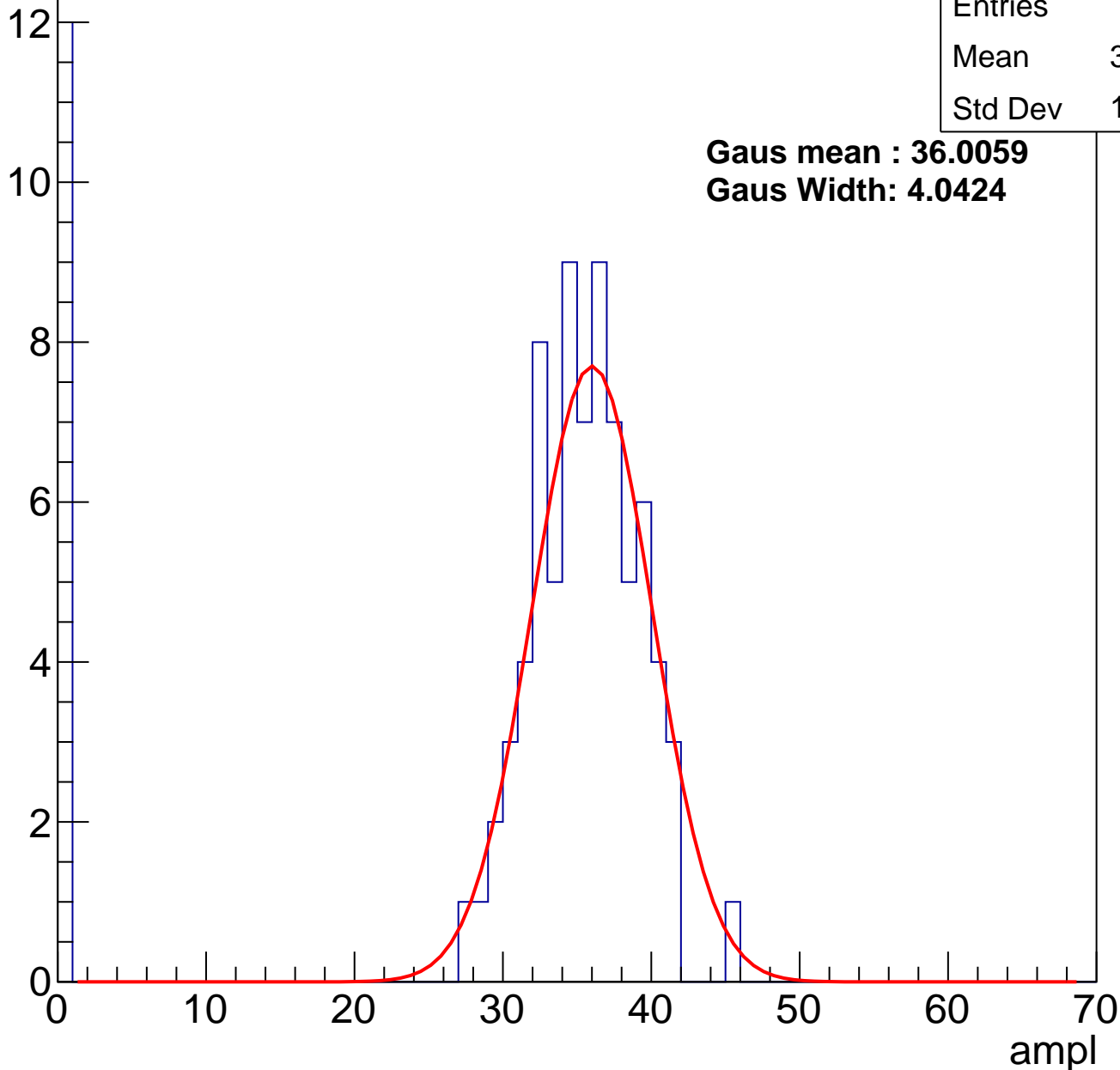
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	30.28
Std Dev	12.53

Gaus mean : 36.0059

Gaus Width: 4.0424

Entry



B1L103S, U2-ch67, adc2

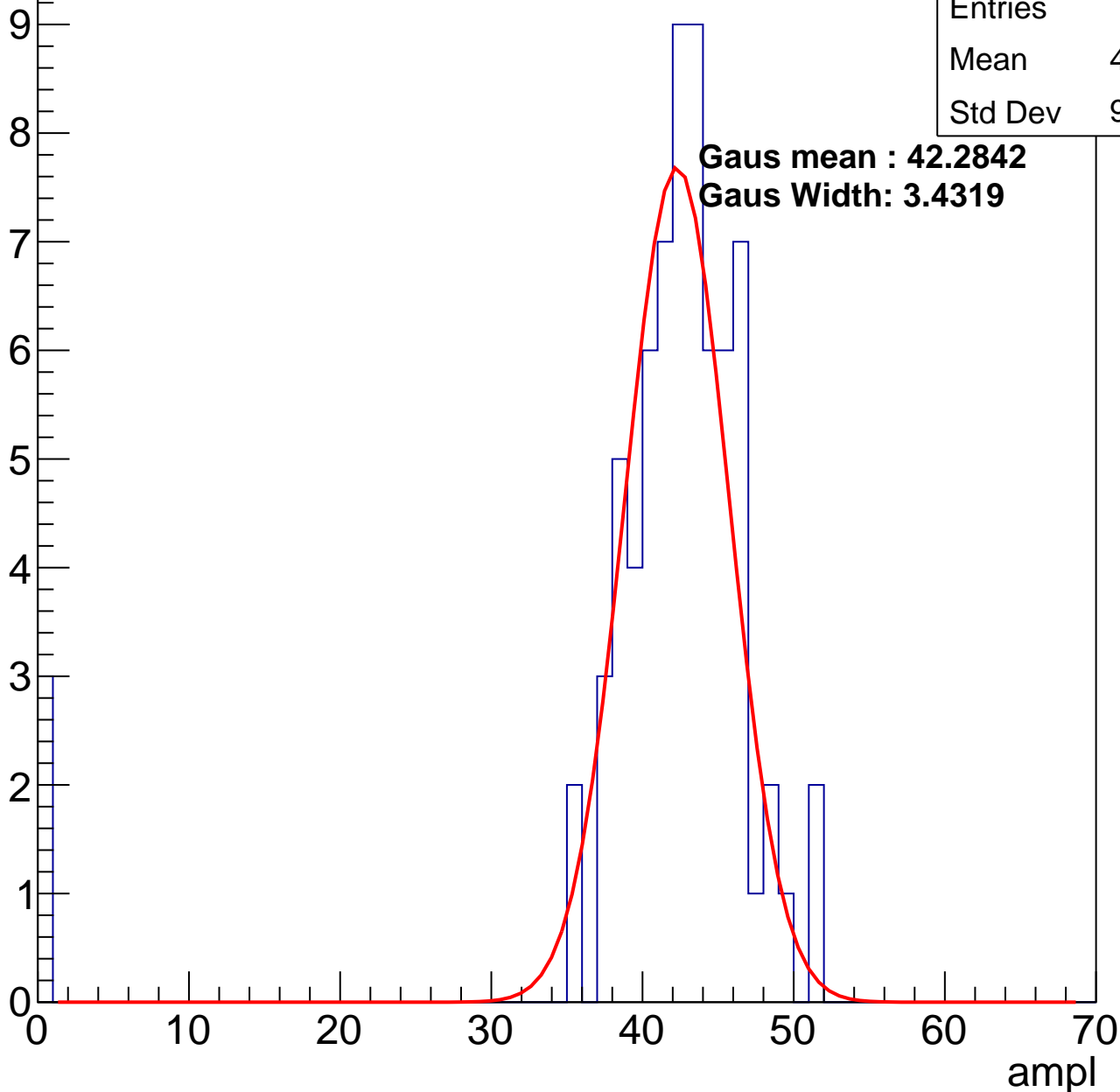
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.67
Std Dev	9.058

Gaus mean : 42.2842

Gaus Width: 3.4319

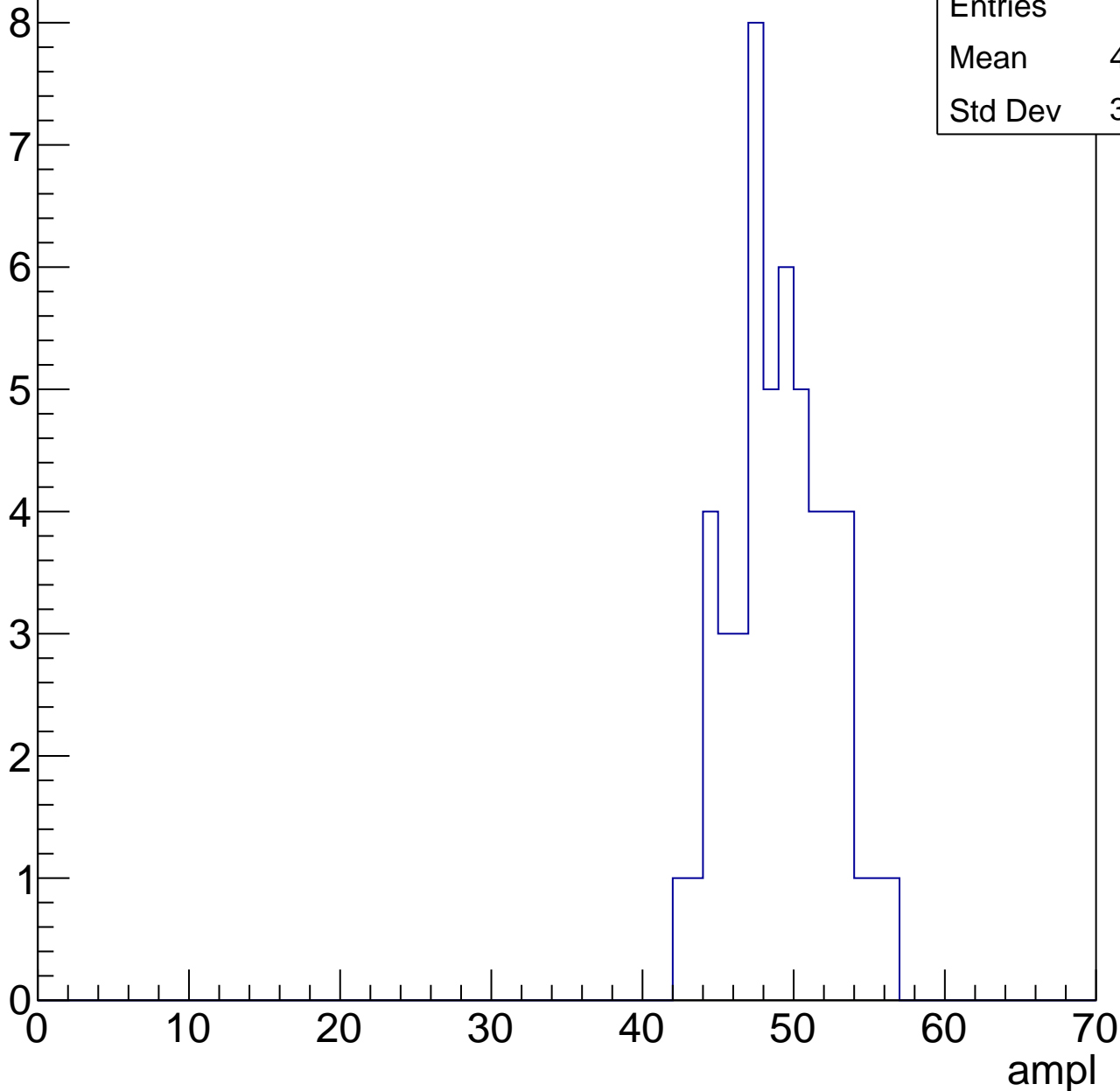


B1L103S, U2-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

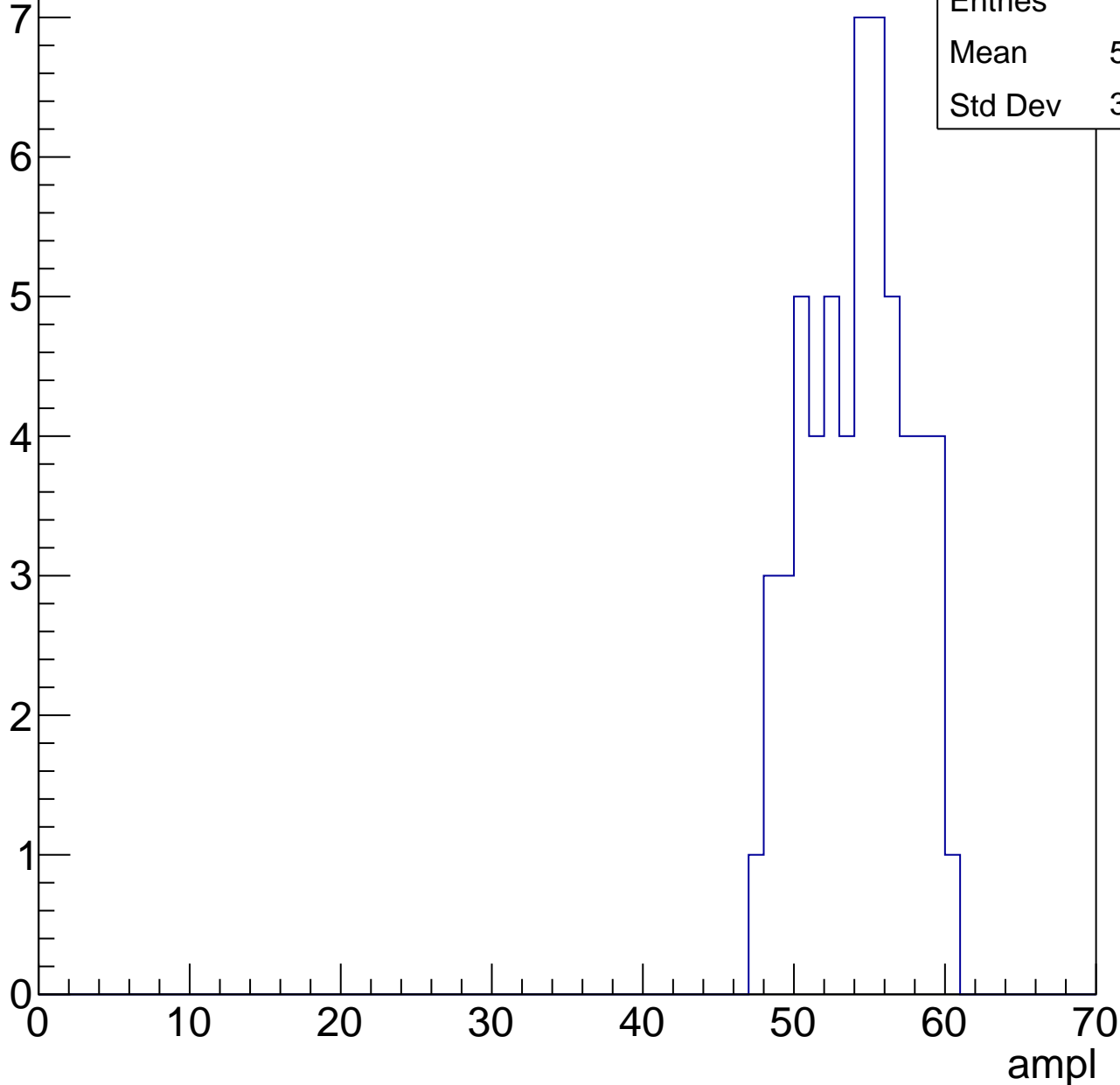
Entries	51
Mean	48.69
Std Dev	3.202



B1L103S, U2-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

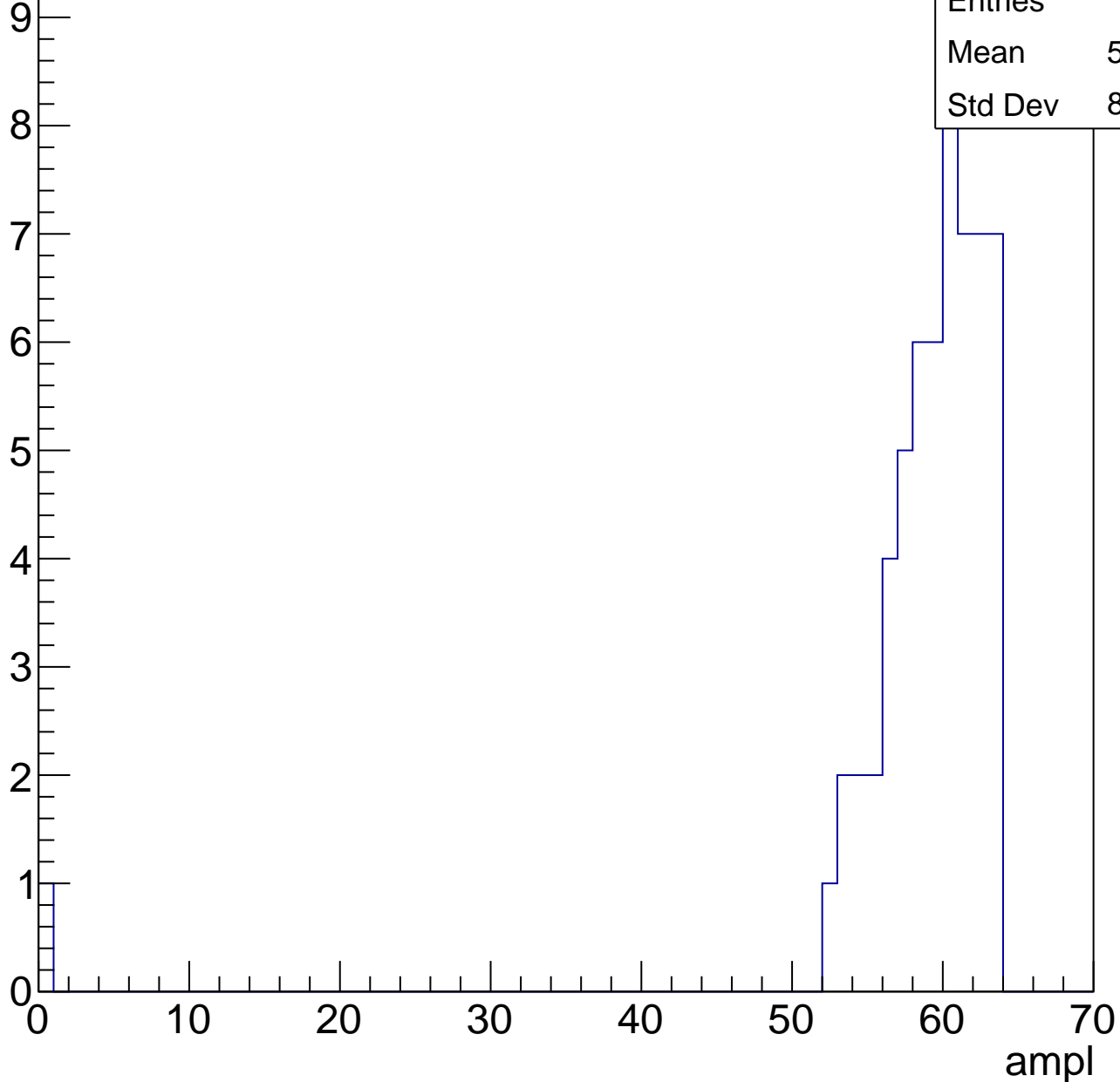


Entries	57
Mean	53.74
Std Dev	3.327

B1L103S, U2-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

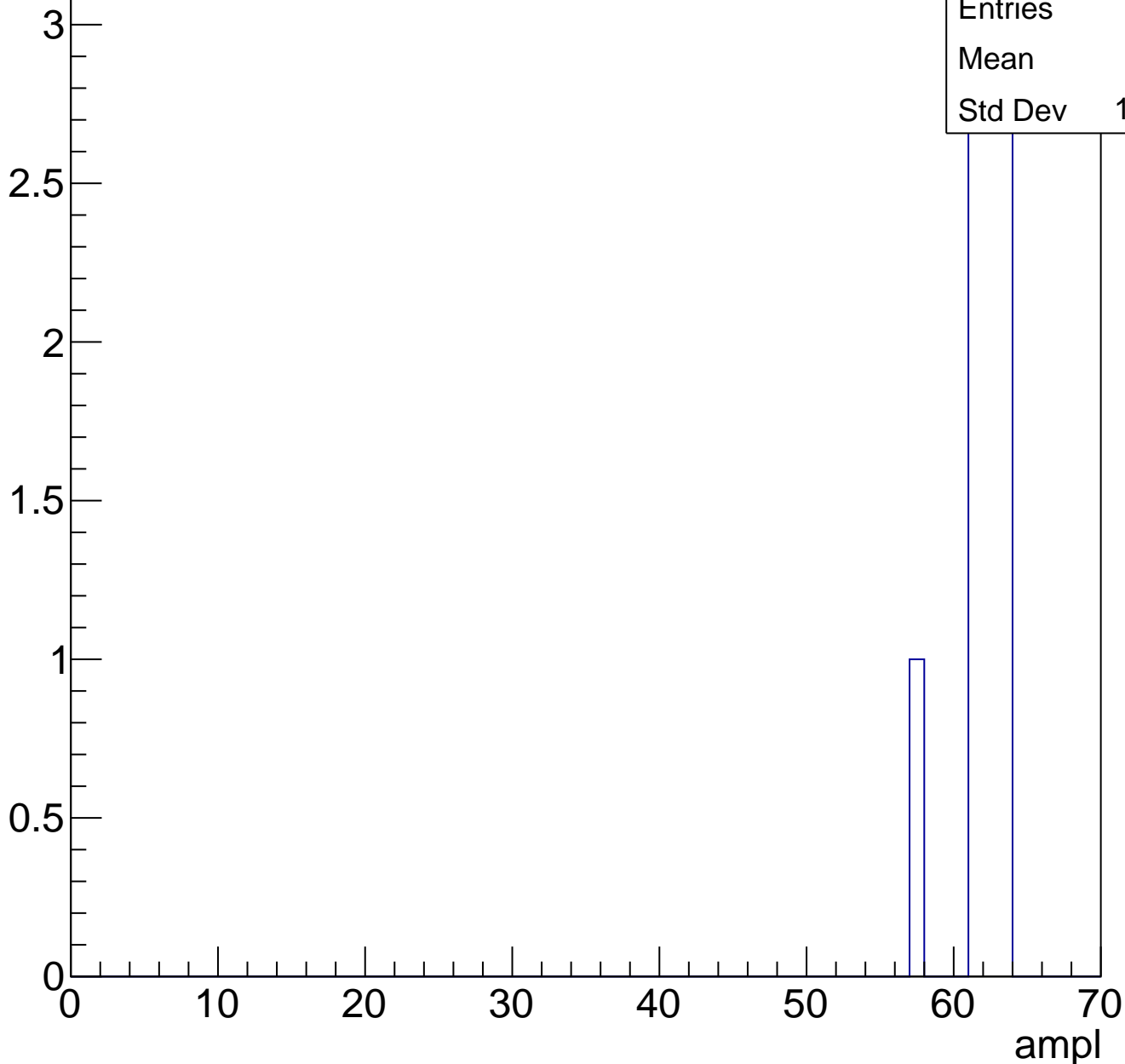
Entry



B1L103S, U2-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



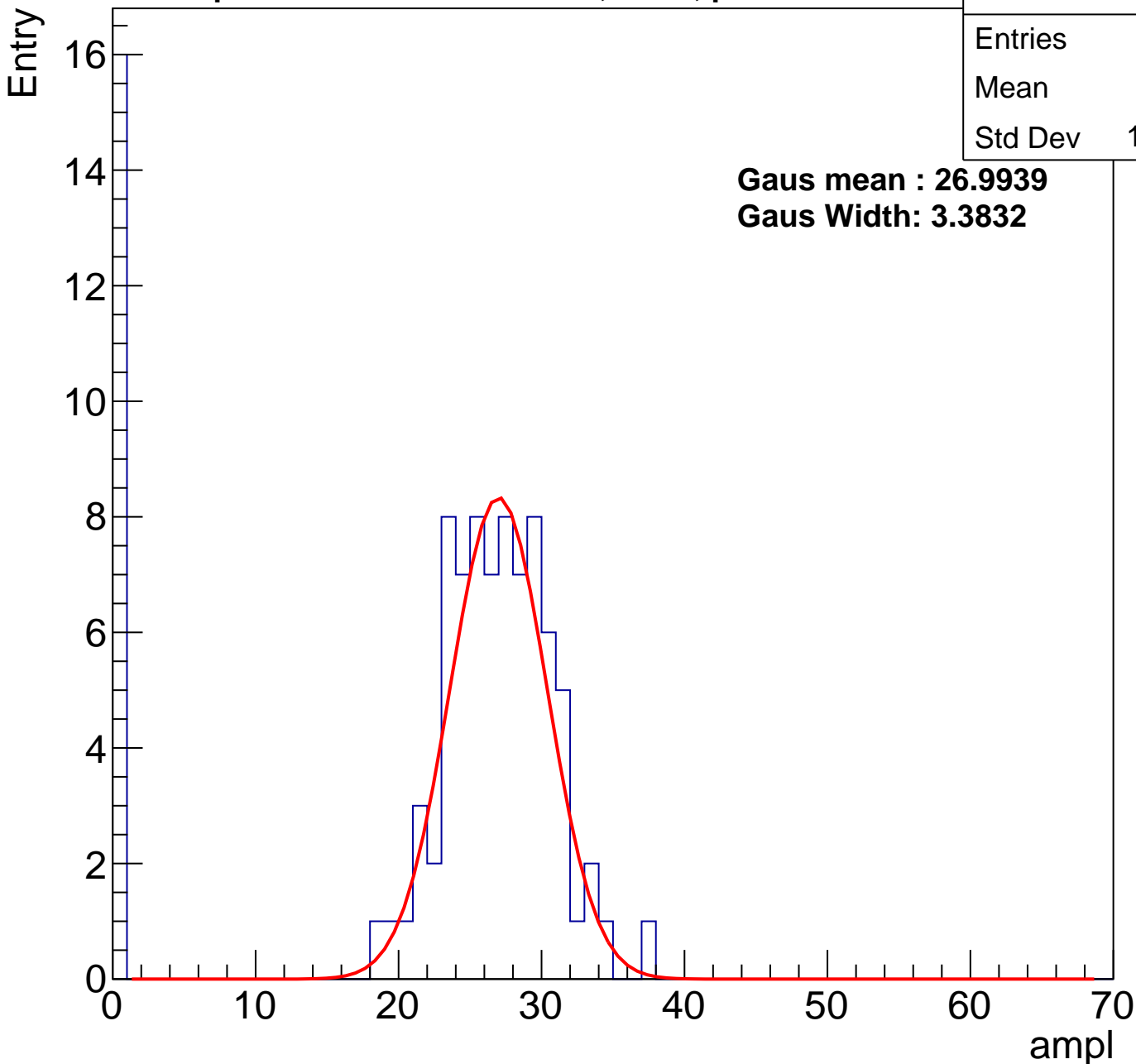
B1L103S, U2-ch68, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	22
Std Dev	10.54

Gaus mean : 26.9939

Gaus Width: 3.3832



B1L103S, U2-ch68, adc1

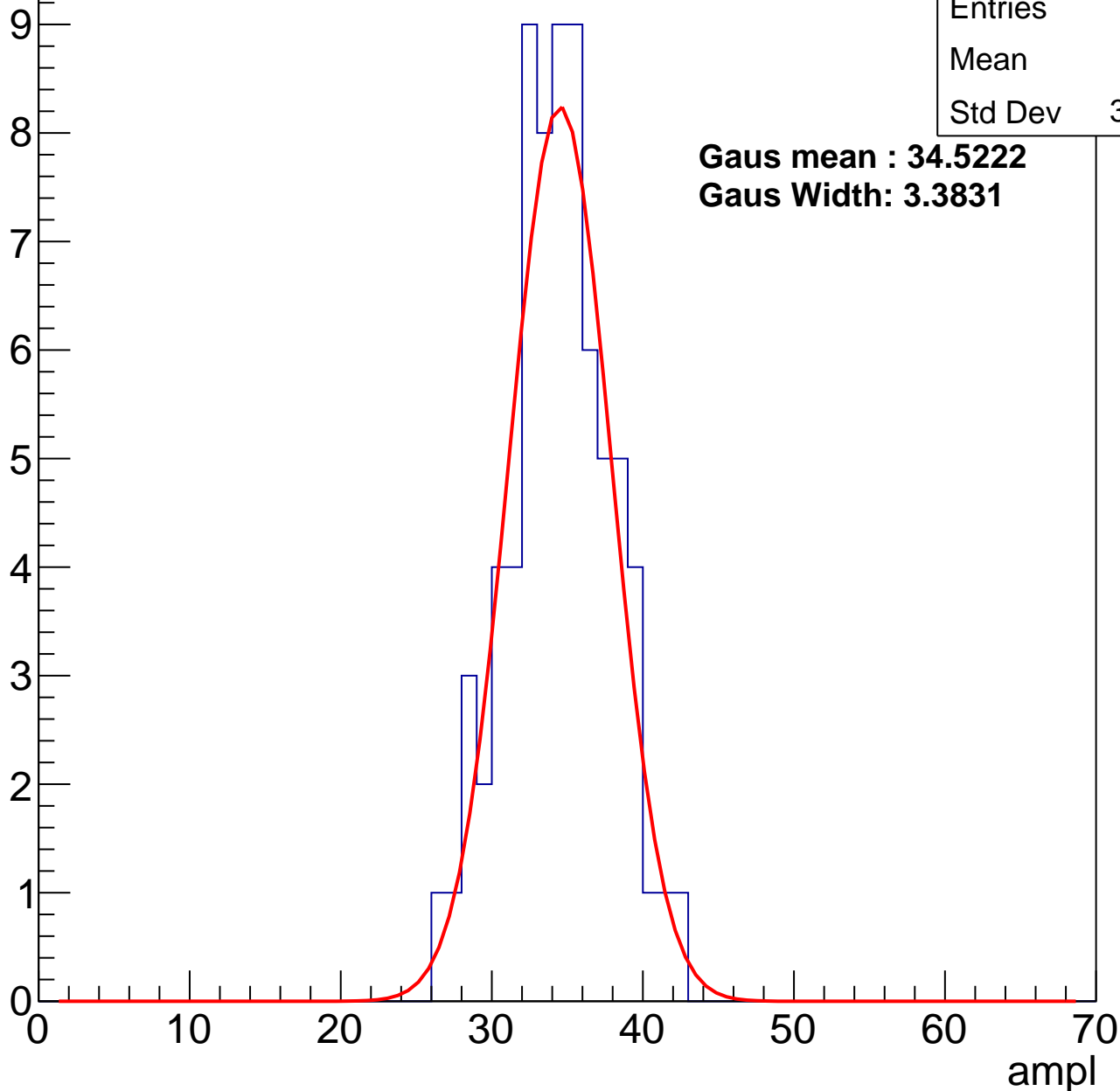
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	34
Std Dev	3.356

Gaus mean : 34.5222

Gaus Width: 3.3831



B1L103S, U2-ch68, adc2

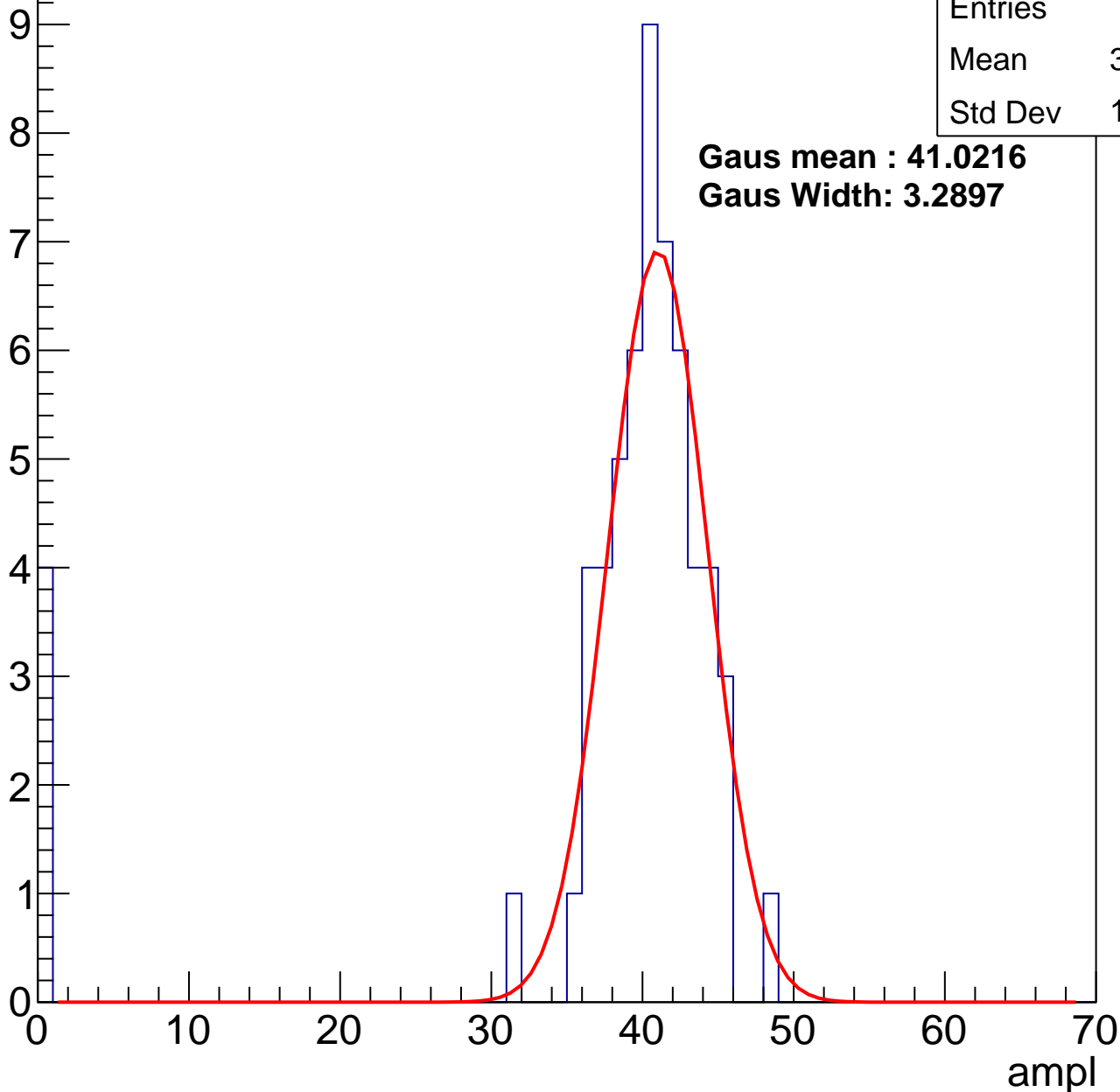
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	37.49
Std Dev	10.52

Gaus mean : 41.0216

Gaus Width: 3.2897



B1L103S, U2-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	39.79
Std Dev	17

Entry

10

8

6

4

2

0

0

10

20

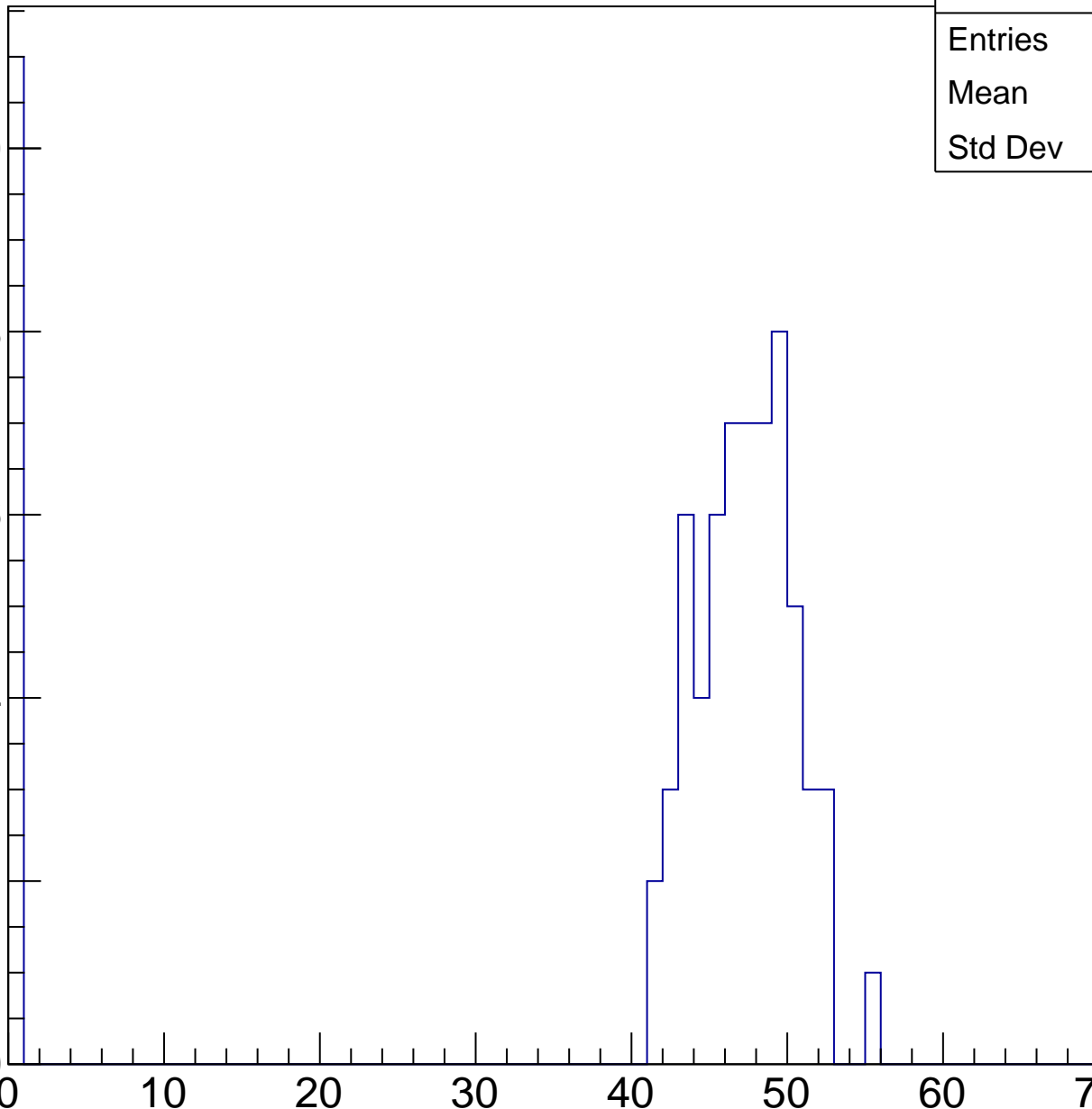
30

40

50

60

ampl

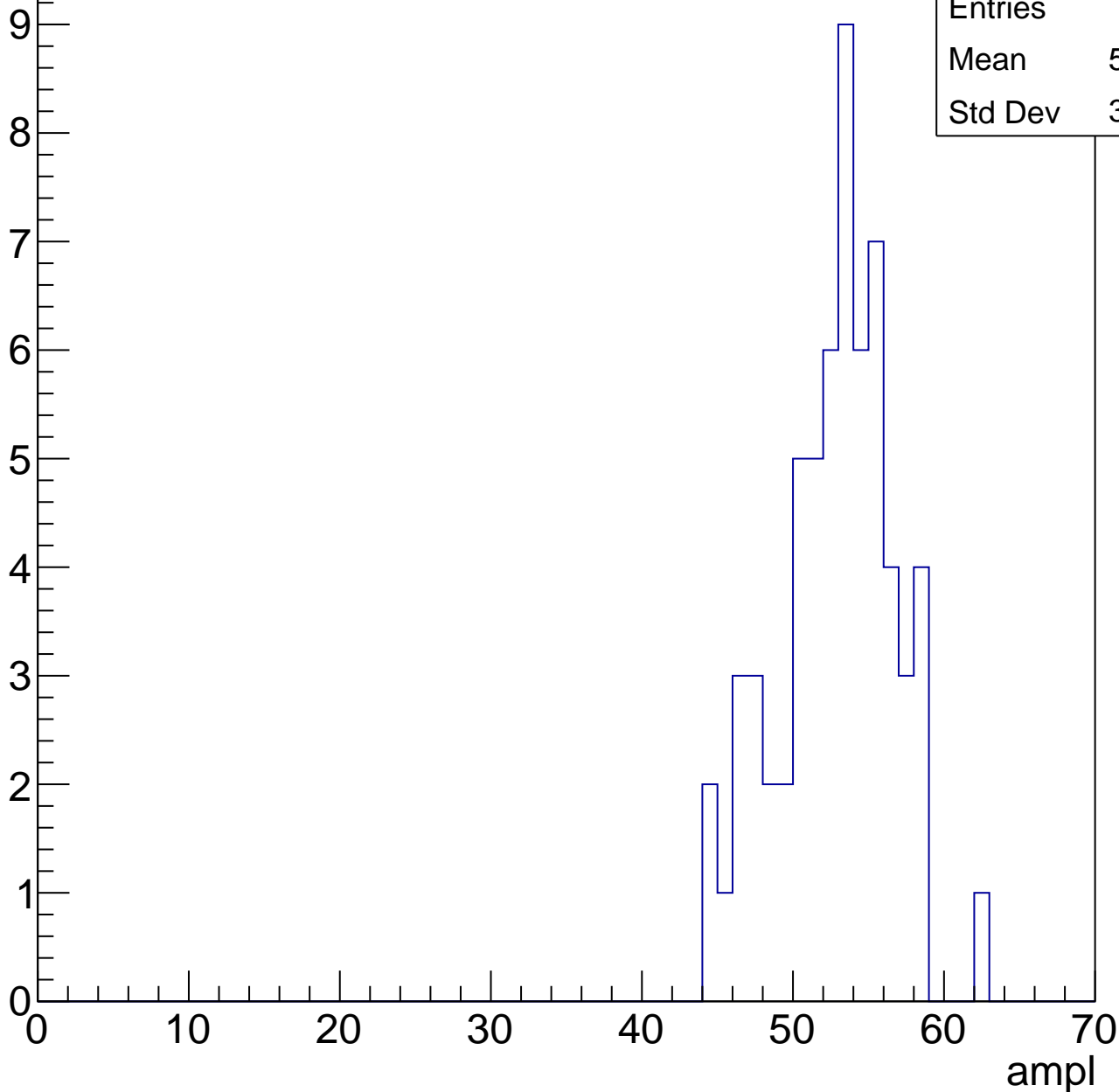


B1L103S, U2-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.35
Std Dev	3.793

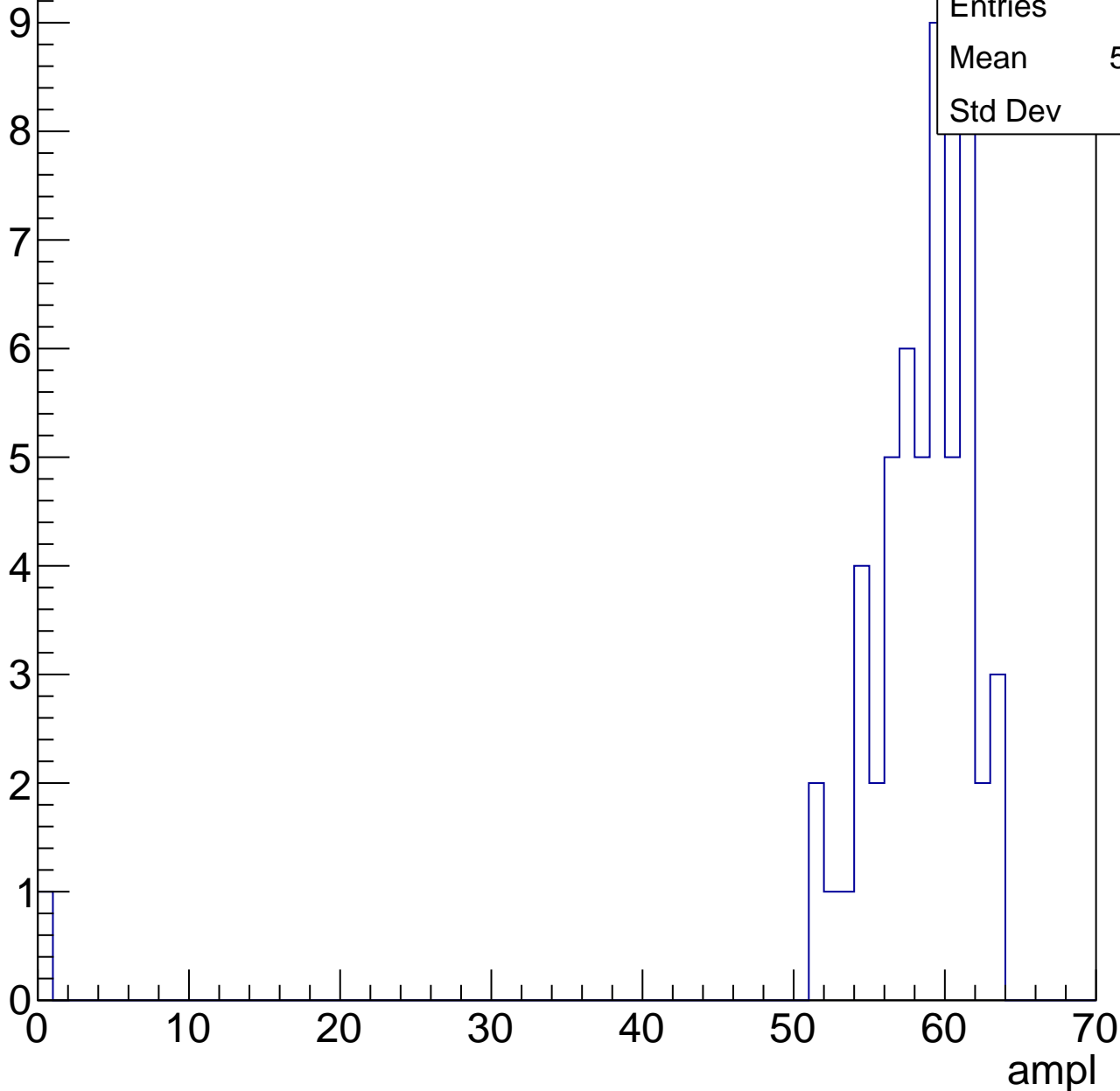


B1L103S, U2-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.98
Std Dev	8.37

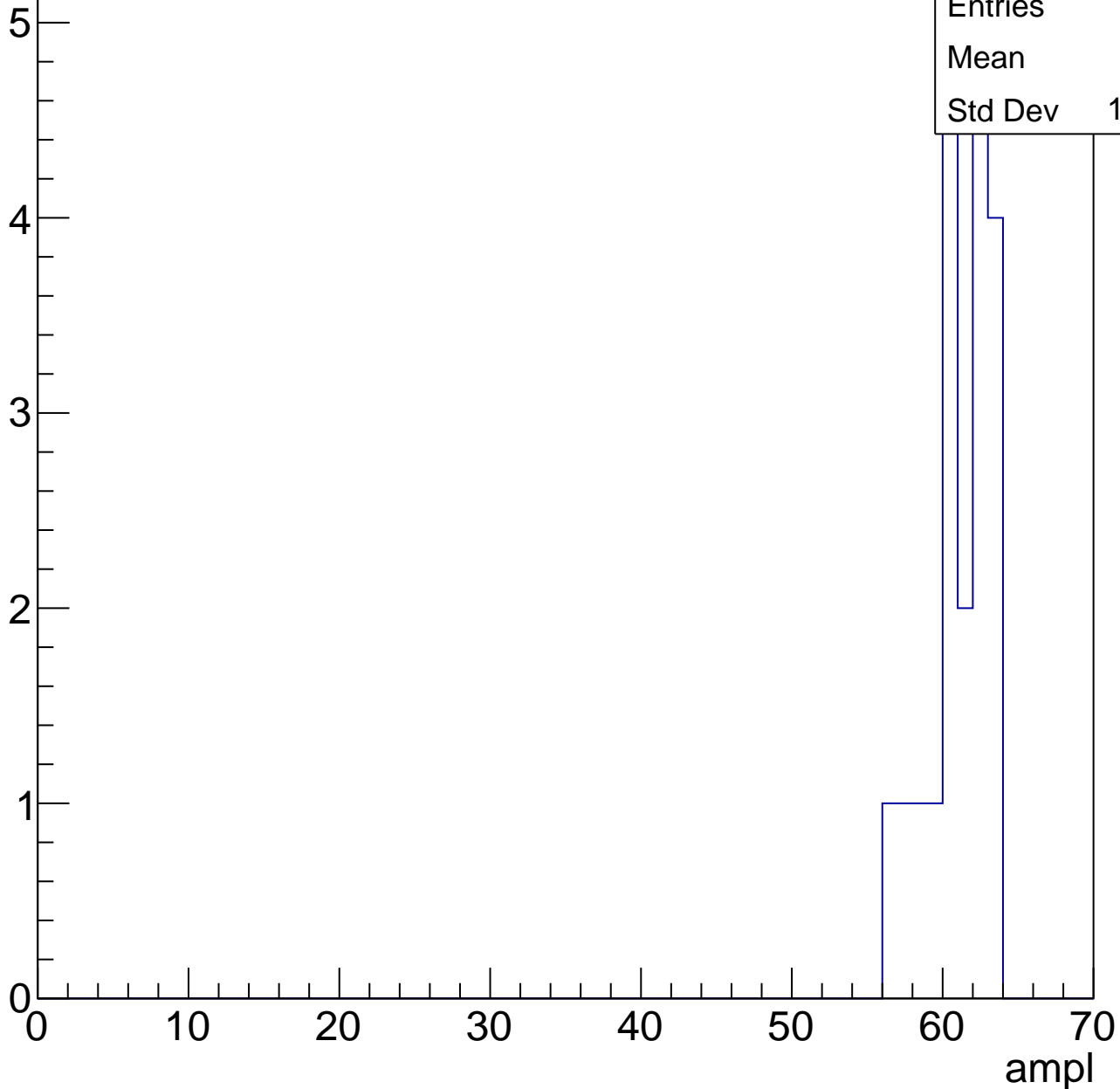


B1L103S, U2-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

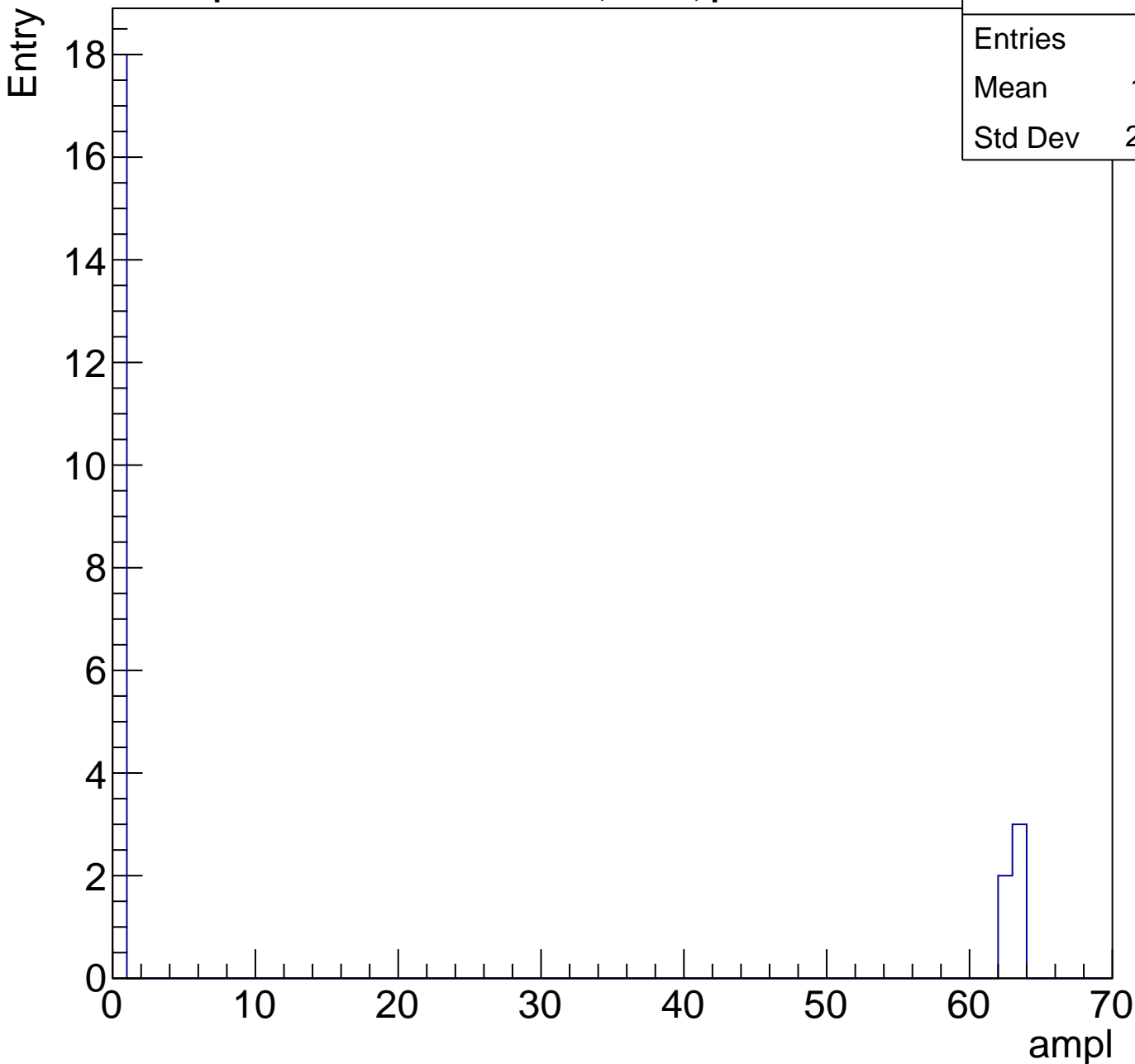
Entries	20
Mean	60.7
Std Dev	1.977



B1L103S, U2-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	13.61
Std Dev	25.82



B1L103S, U2-ch69, adc0

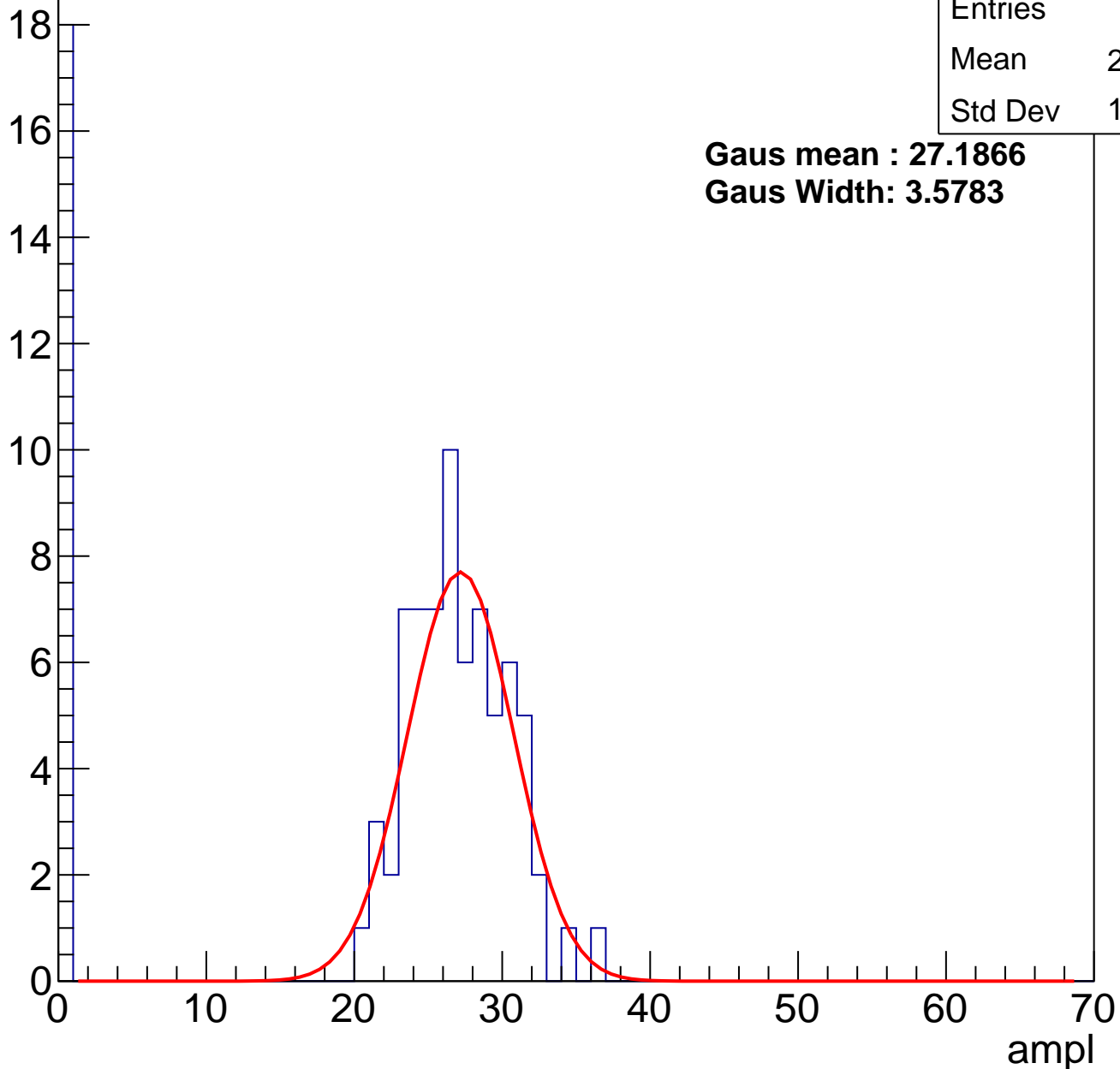
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	21.17
Std Dev	11.13

Gaus mean : 27.1866

Gaus Width: 3.5783

Entry



B1L103S, U2-ch69, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	32.24
Std Dev	8.173

Gaus mean : 34.3970

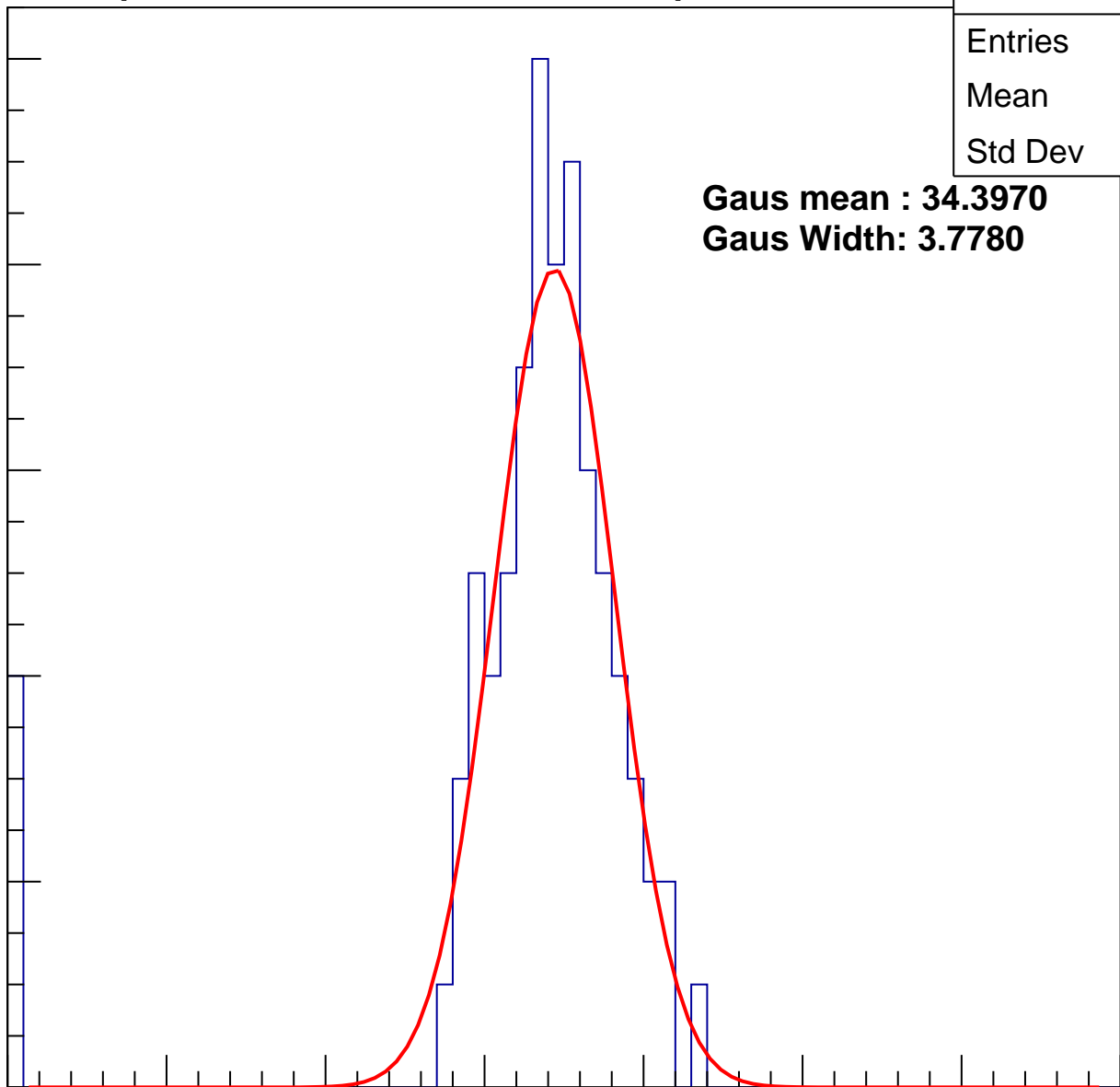
Gaus Width: 3.7780

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch69, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	32.9
Std Dev	15.71

Gaus mean : 40.8632

Gaus Width: 3.5386

Entry

10

8

6

4

2

0

0

10

20

30

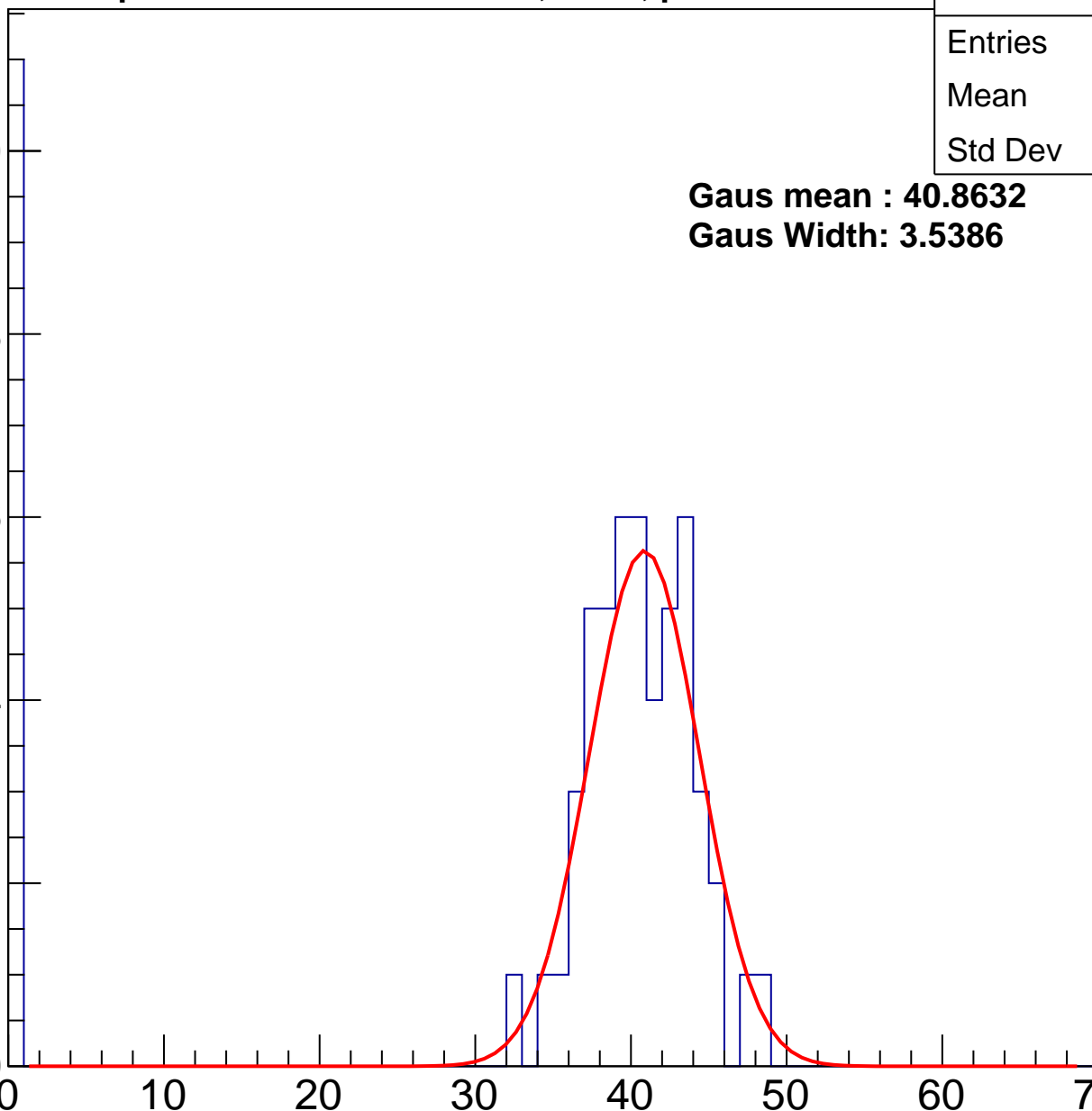
40

50

60

70

ampl

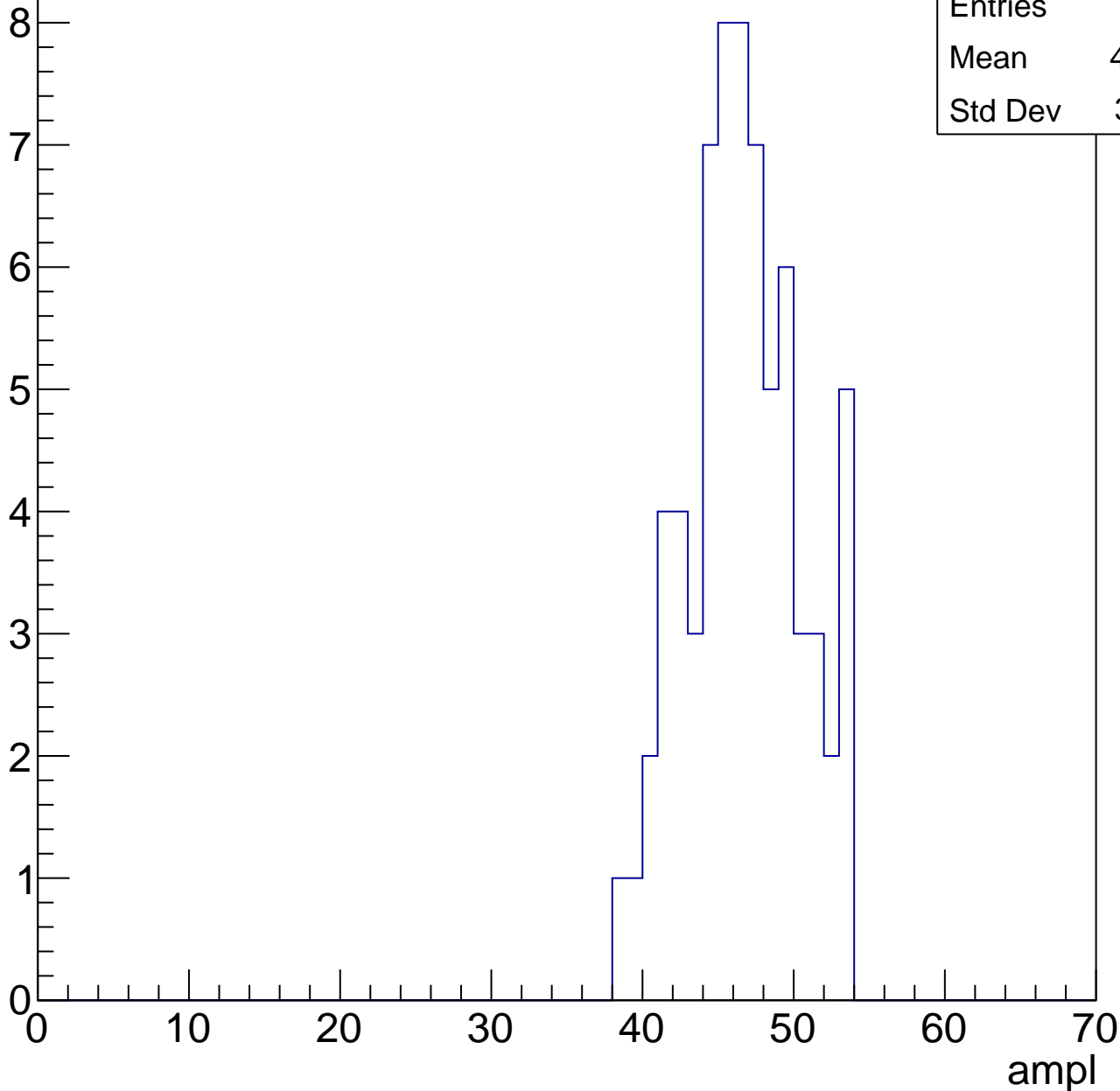


B1L103S, U2-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	46.22
Std Dev	3.671

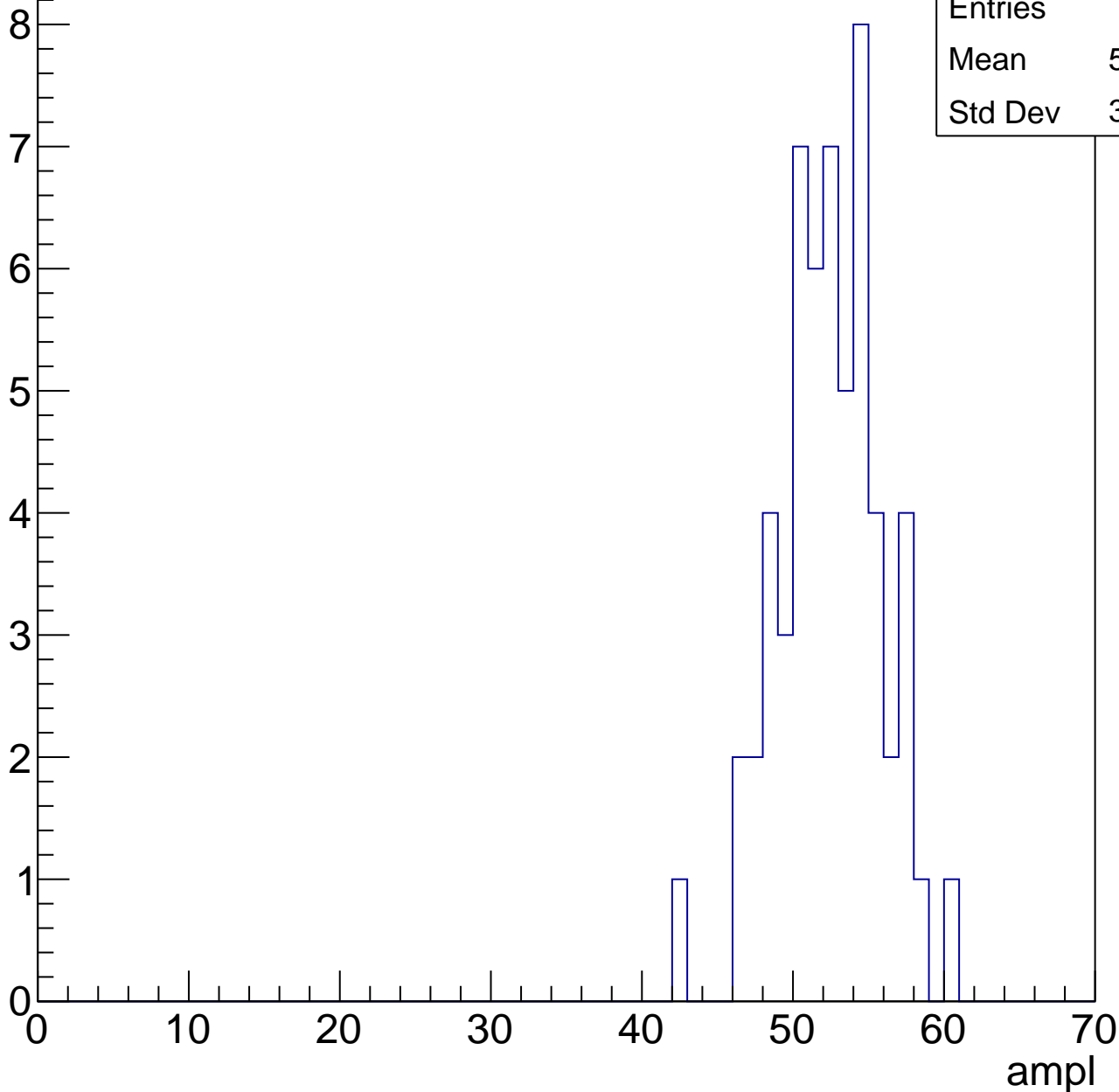


B1L103S, U2-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	51.96
Std Dev	3.382

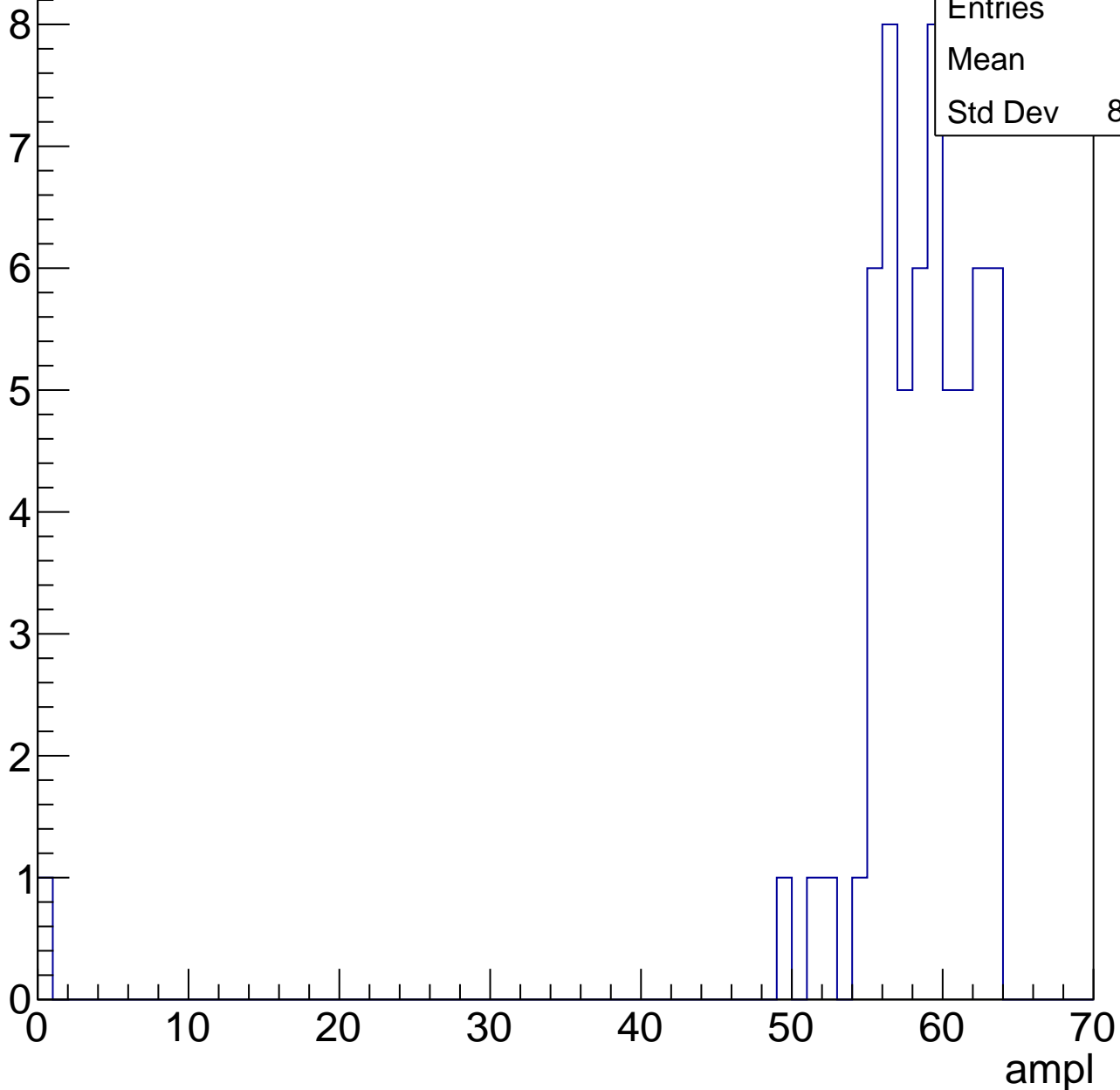


B1L103S, U2-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

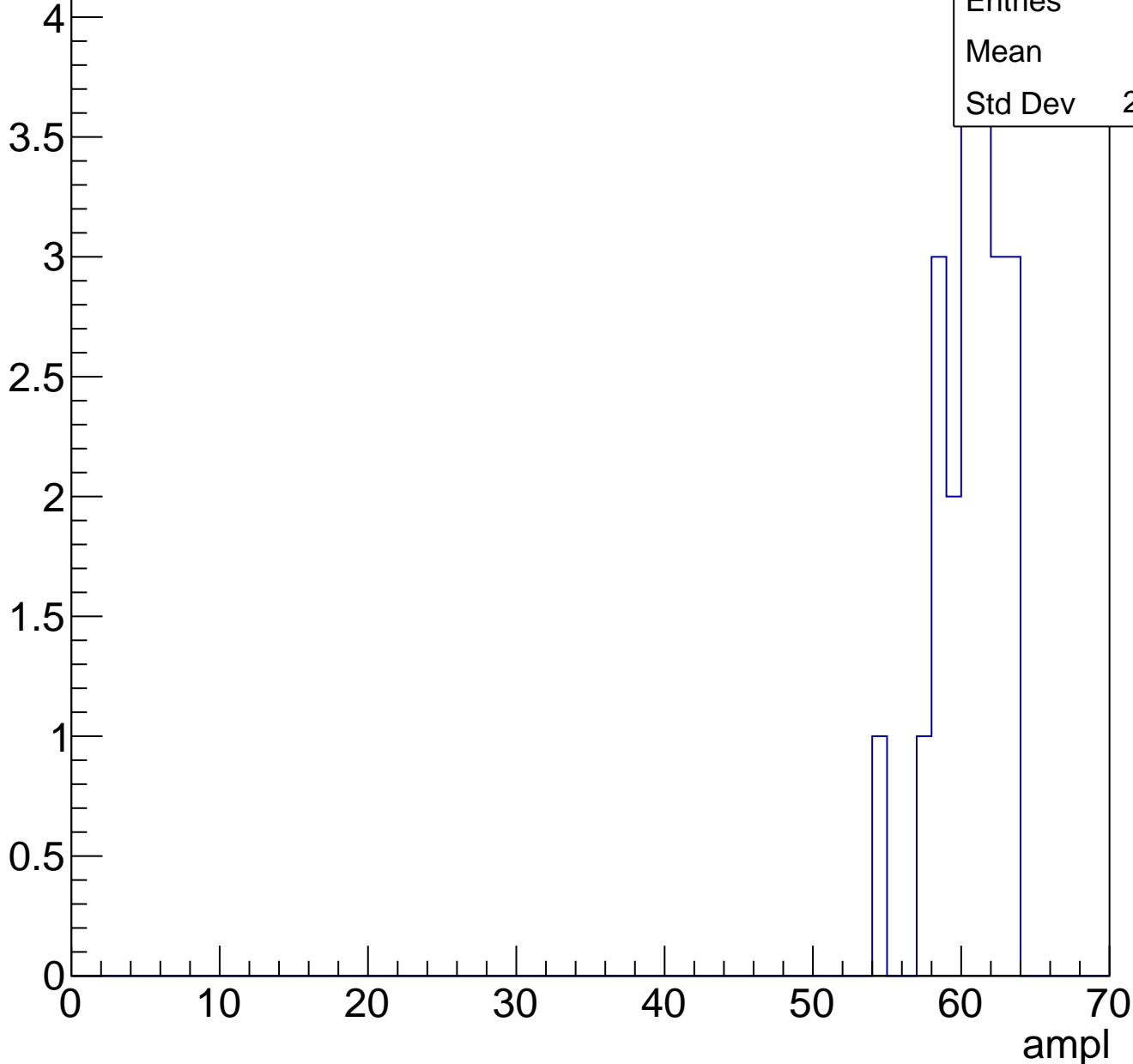
Entries	60
Mean	57.4
Std Dev	8.098



B1L103S, U2-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

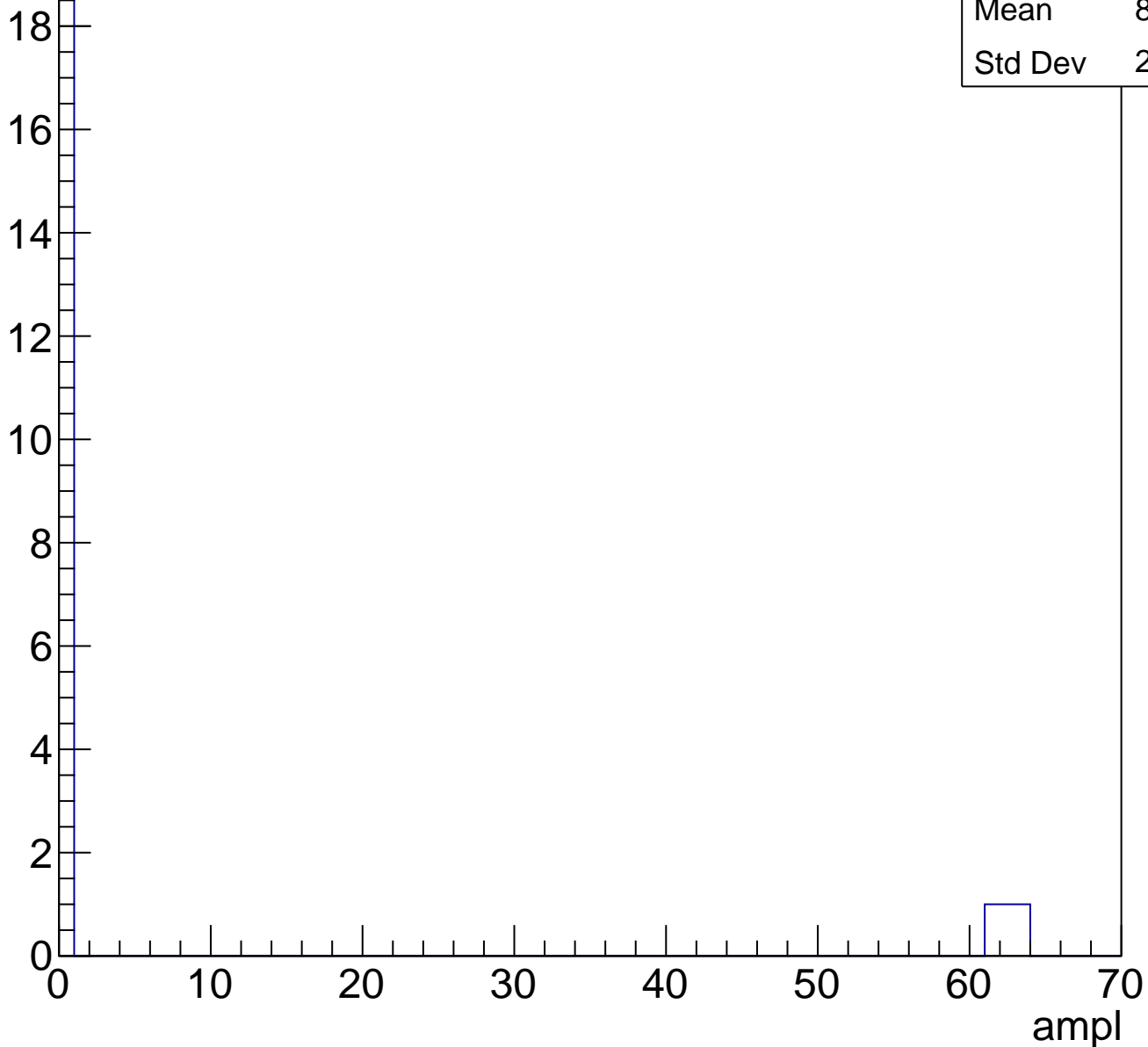
Entry



B1L103S, U2-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	22
Mean	8.455
Std Dev	21.28

B1L103S, U2-ch70, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	23.09
Std Dev	10.84

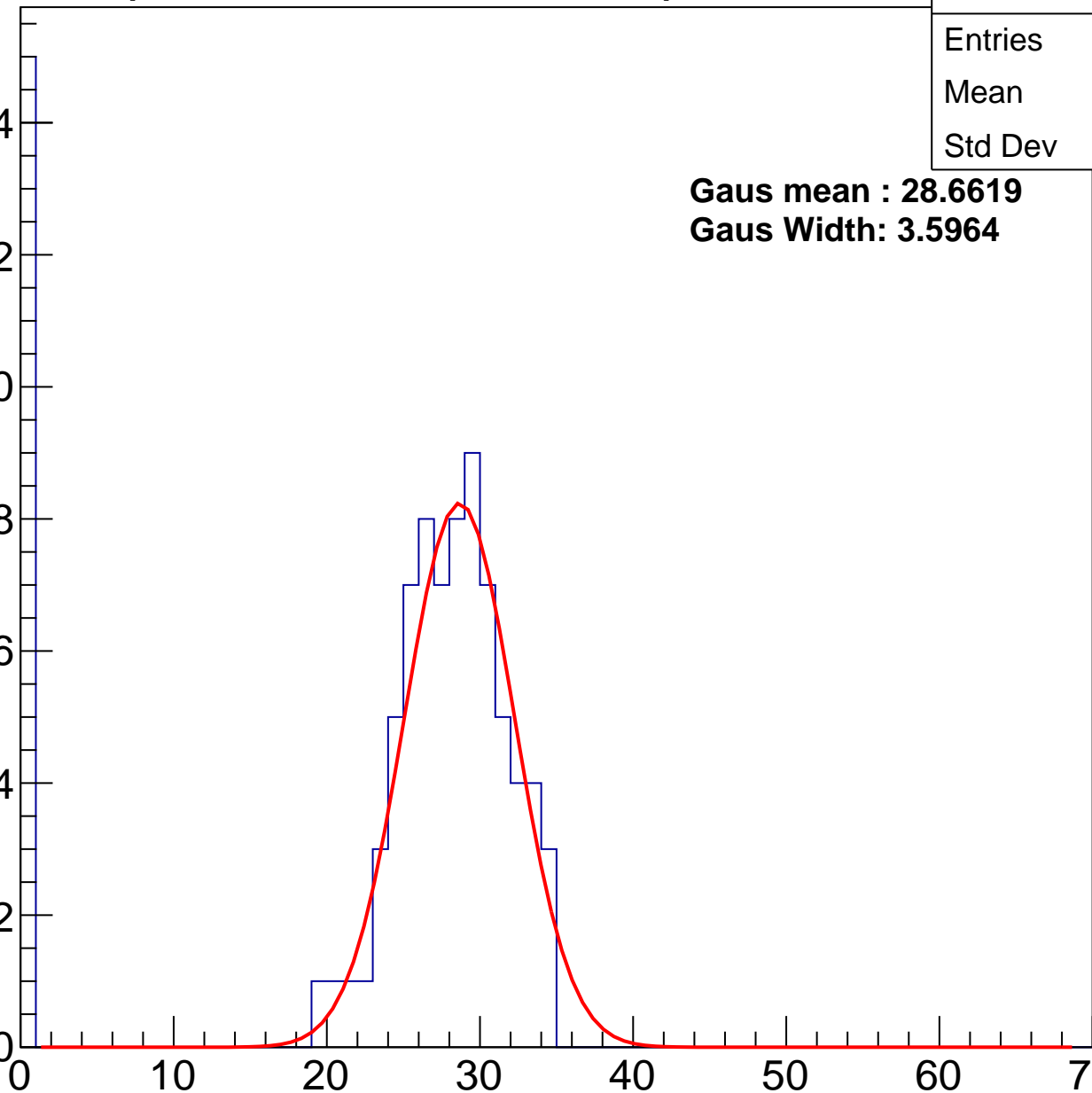
Gaus mean : 28.6619

Gaus Width: 3.5964

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch70, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	32.7
Std Dev	9.381

Gaus mean : 35.3852

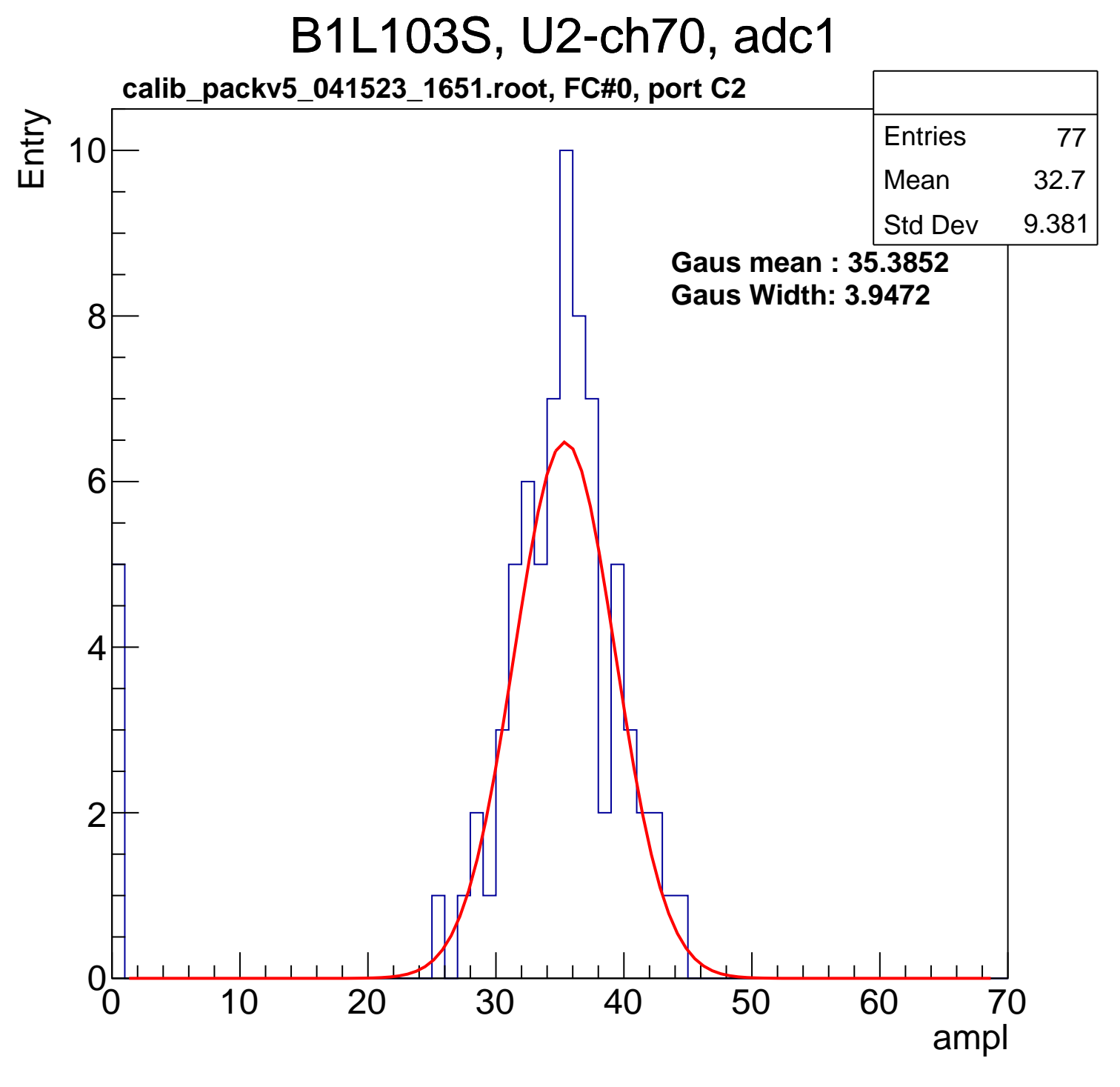
Gaus Width: 3.9472

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch70, adc2

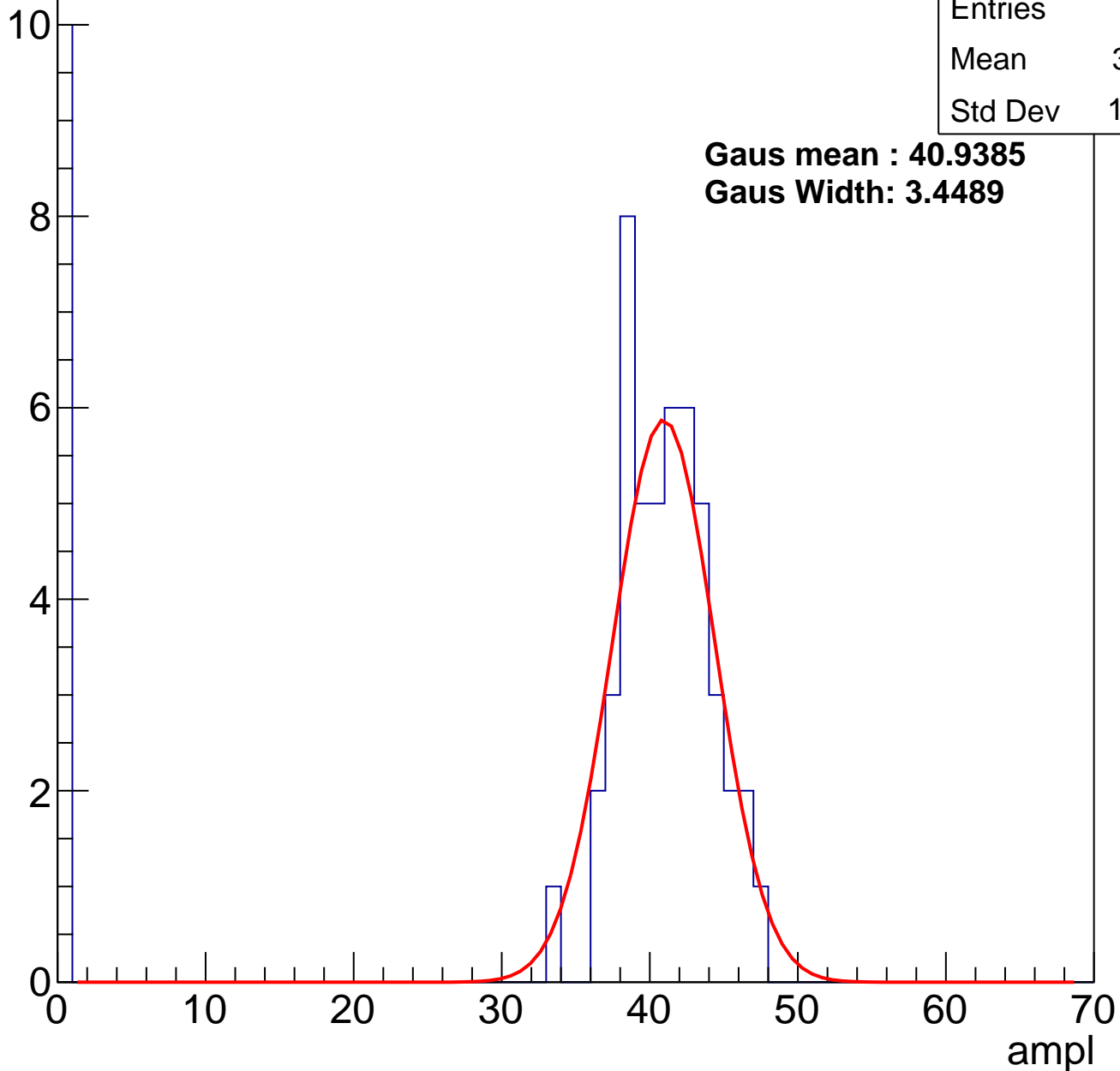
calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	33.71
Std Dev	15.46

Gaus mean : 40.9385

Gaus Width: 3.4489

Entry



B1L103S, U2-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	47.67
Std Dev	3.531

Entry

10

8

6

4

2

0

0

10

20

30

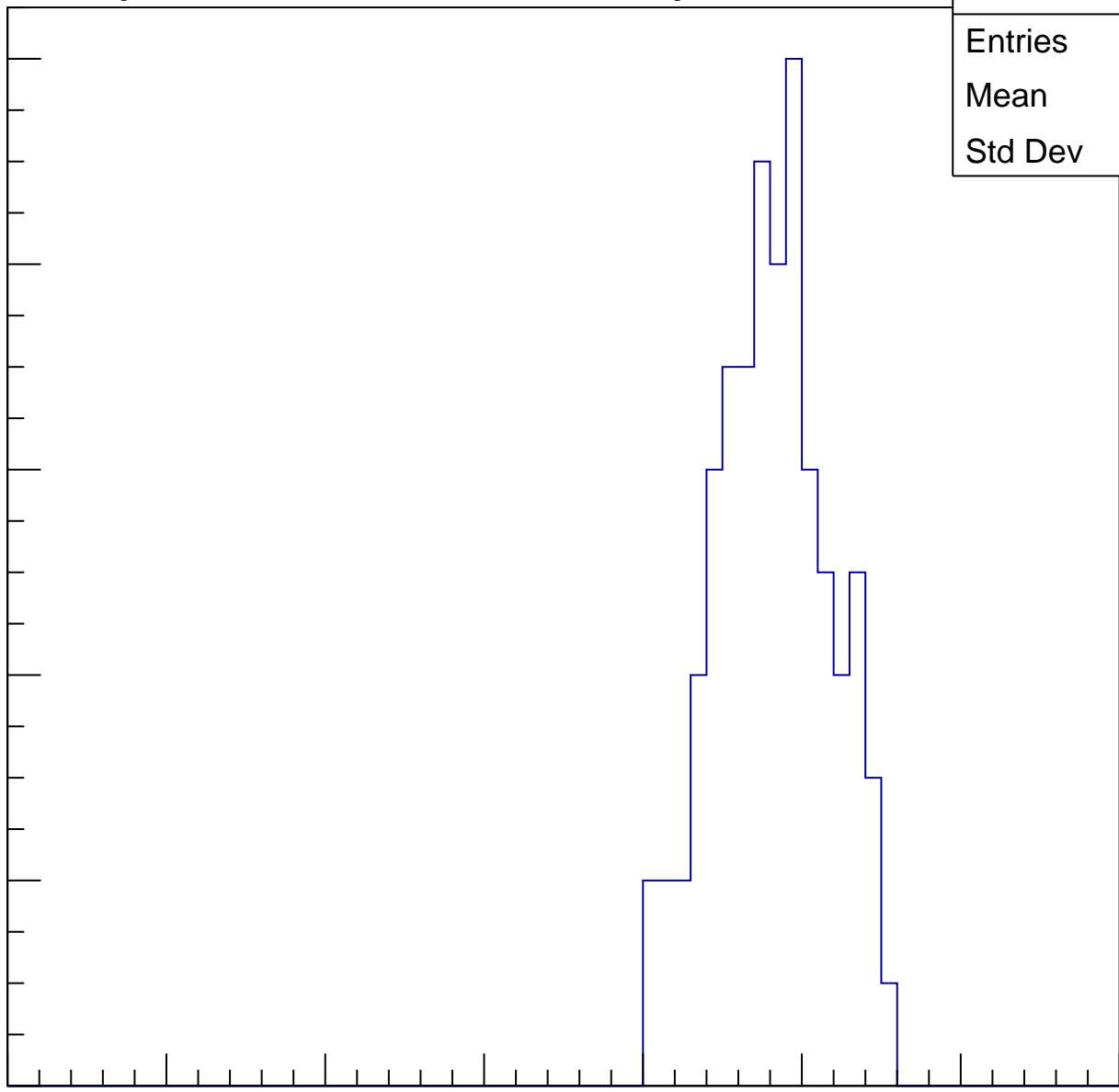
40

50

60

70

ampl

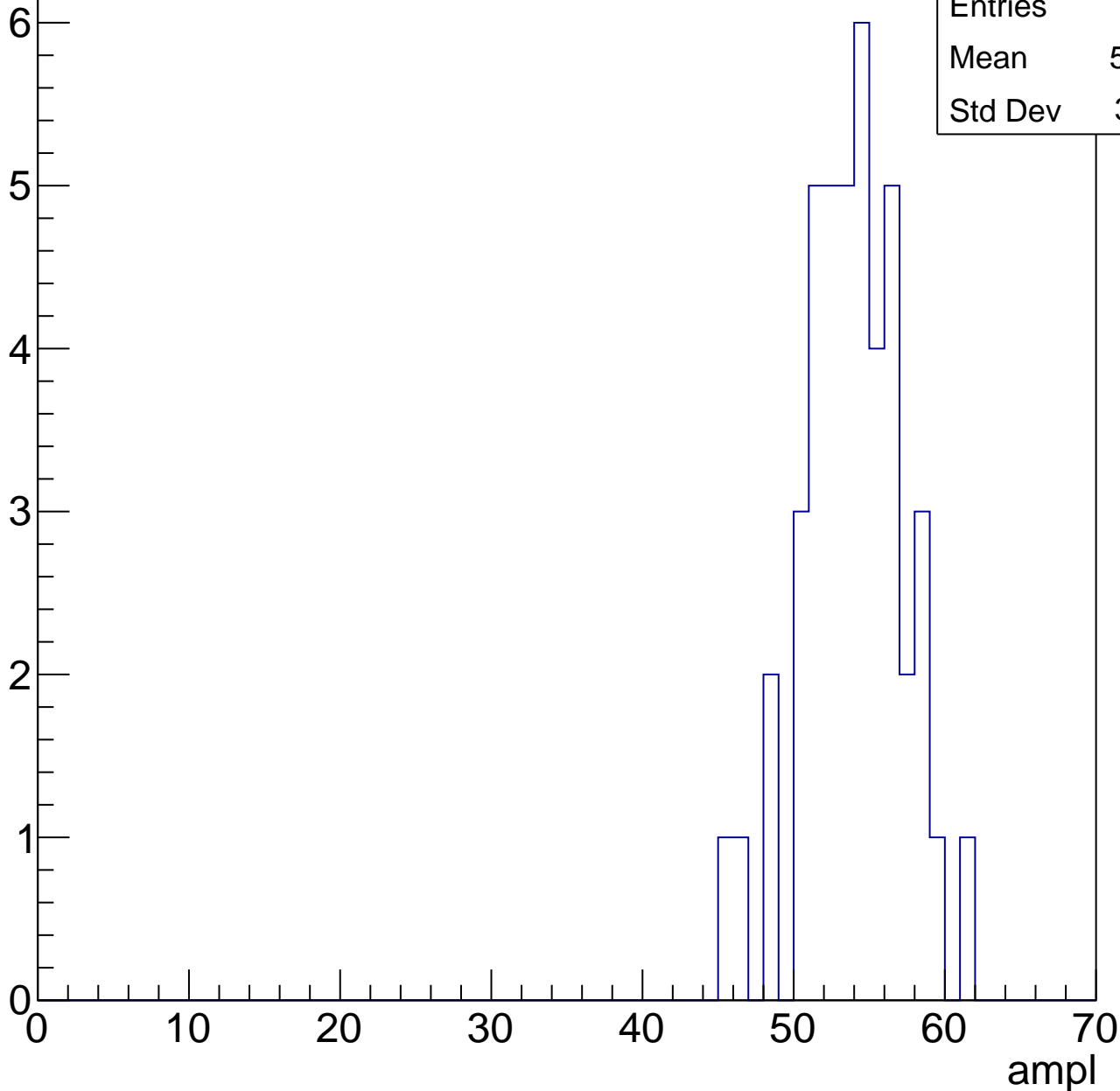


B1L103S, U2-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	53.39
Std Dev	3.311

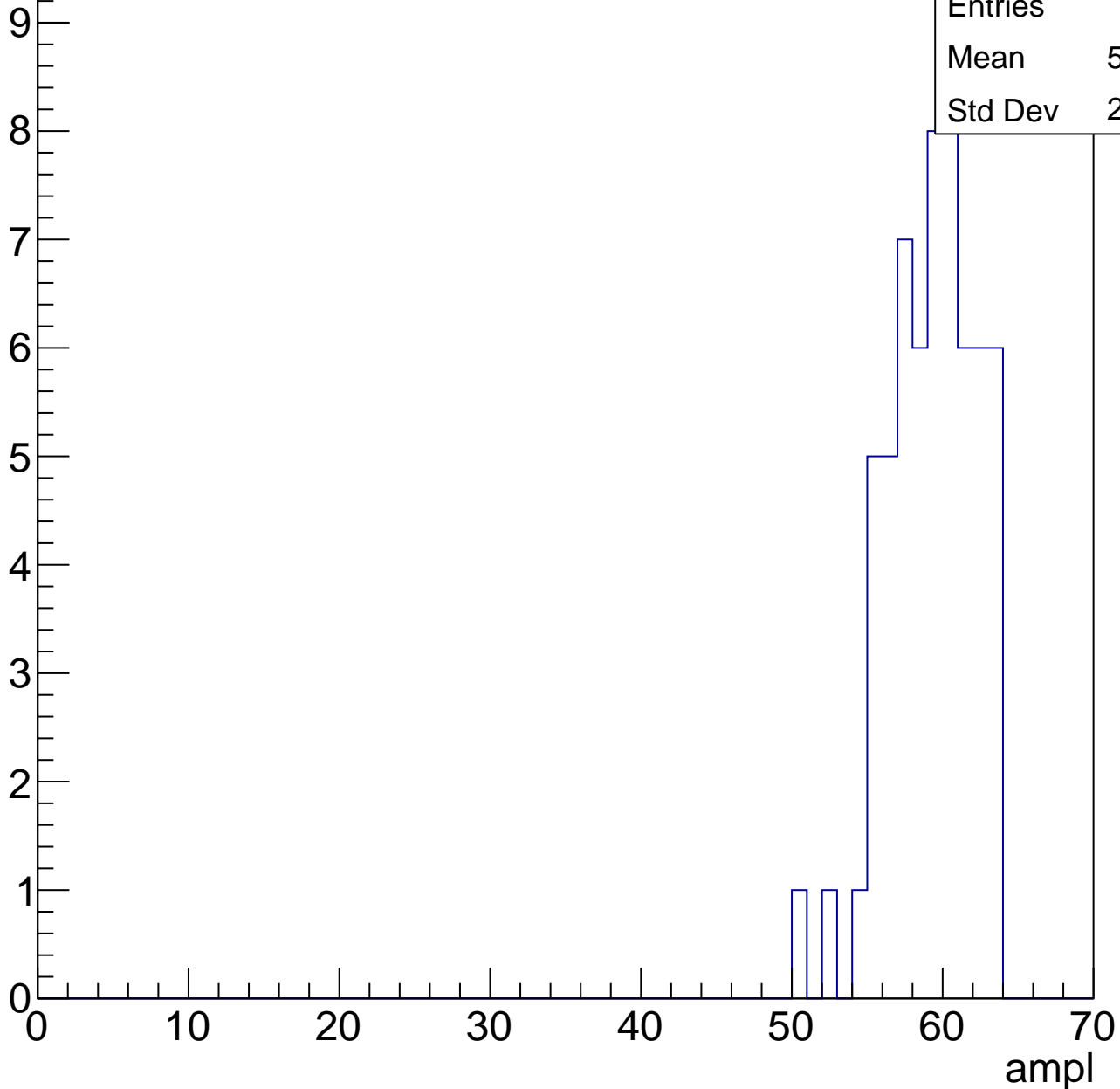


B1L103S, U2-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

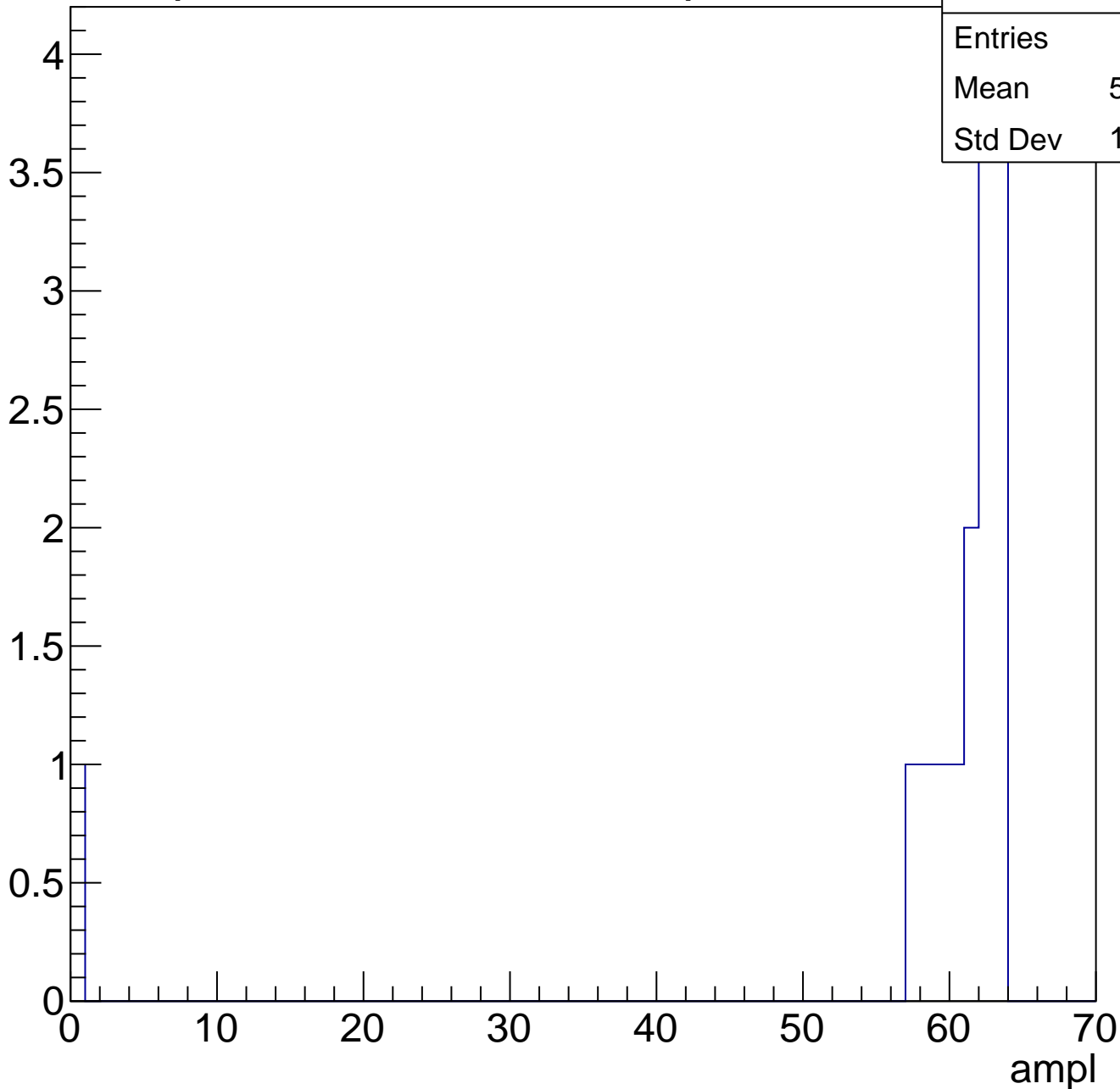
Entries	61
Mean	58.79
Std Dev	2.846



B1L103S, U2-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



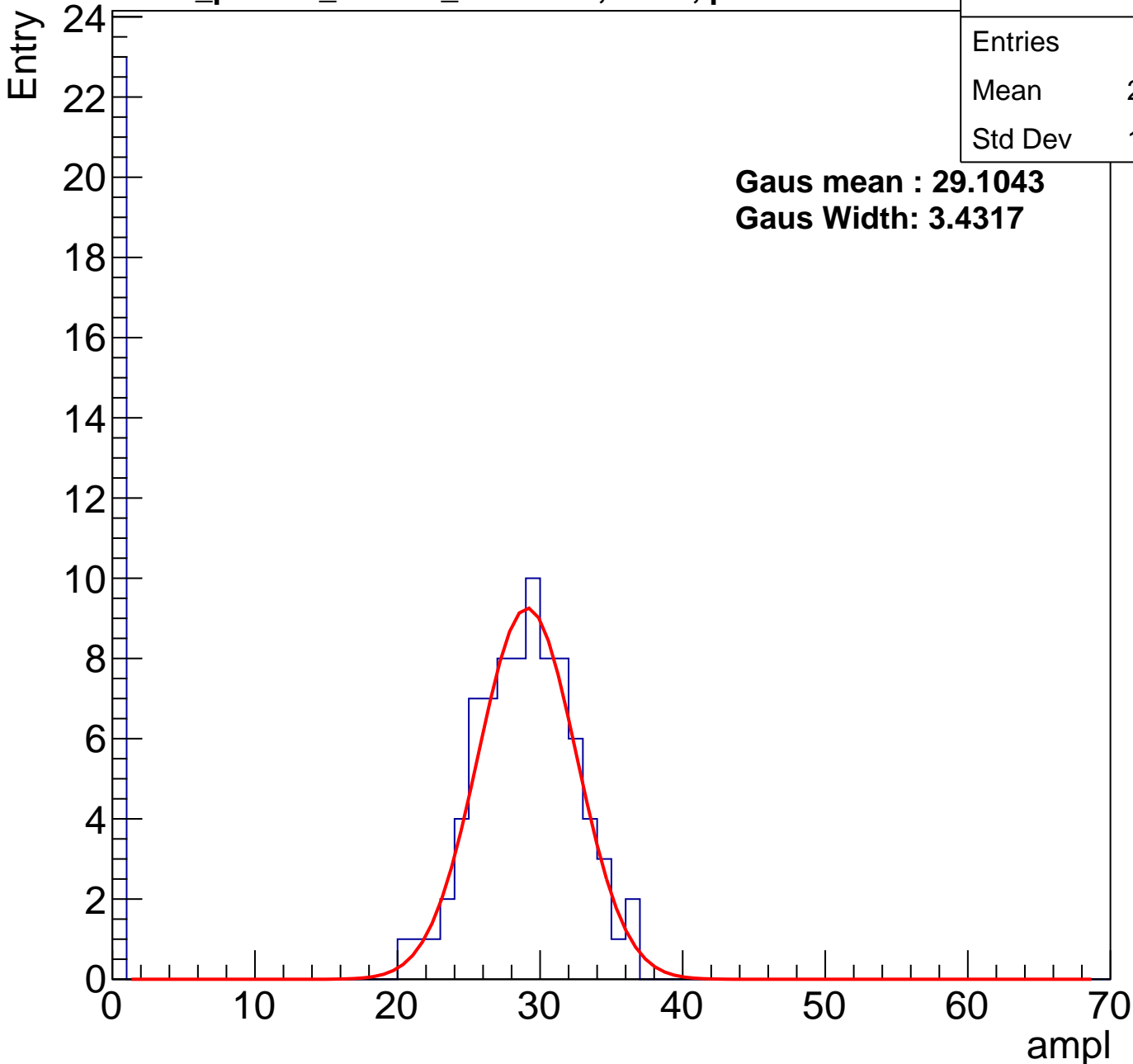
B1L103S, U2-ch71, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	22.24
Std Dev	12.22

Gaus mean : 29.1043

Gaus Width: 3.4317



B1L103S, U2-ch71, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	30.99
Std Dev	12.85

Gaus mean : 36.3056

Gaus Width: 3.0867

Entry

10

8

6

4

2

0

0

10

20

30

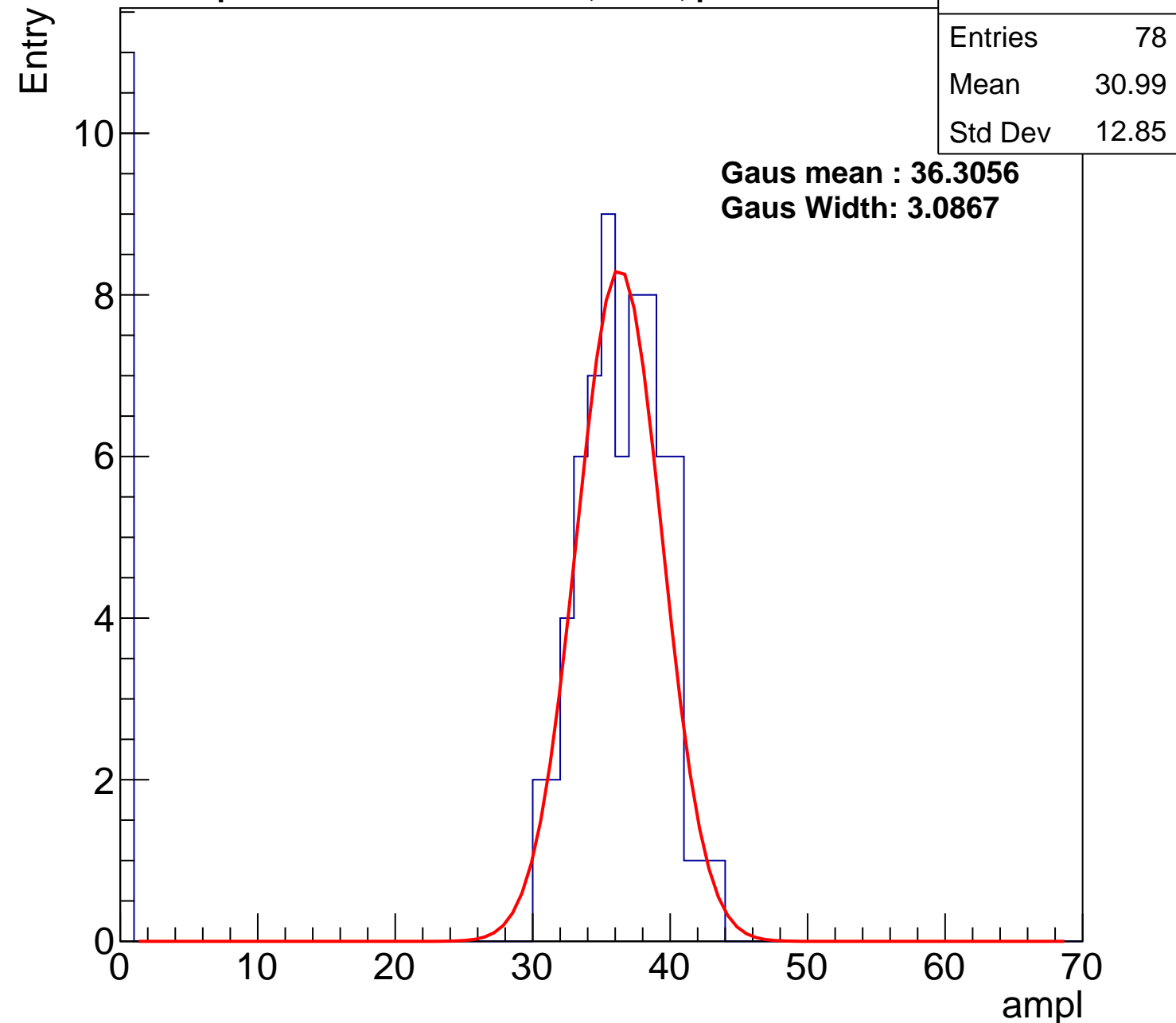
40

50

60

70

ampl



B1L103S, U2-ch71, adc2

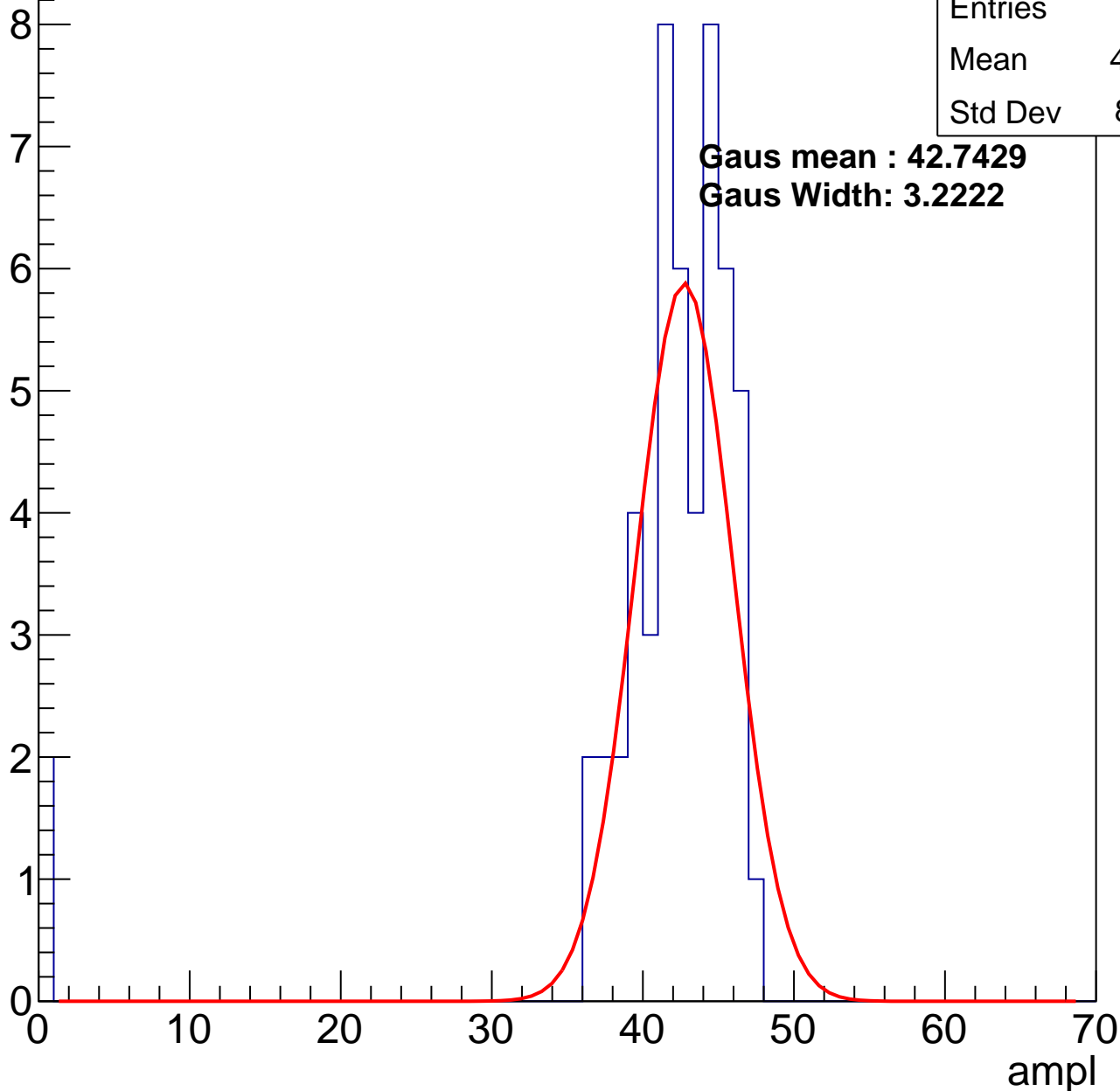
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	40.55
Std Dev	8.491

Gaus mean : 42.7429

Gaus Width: 3.2222



B1L103S, U2-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

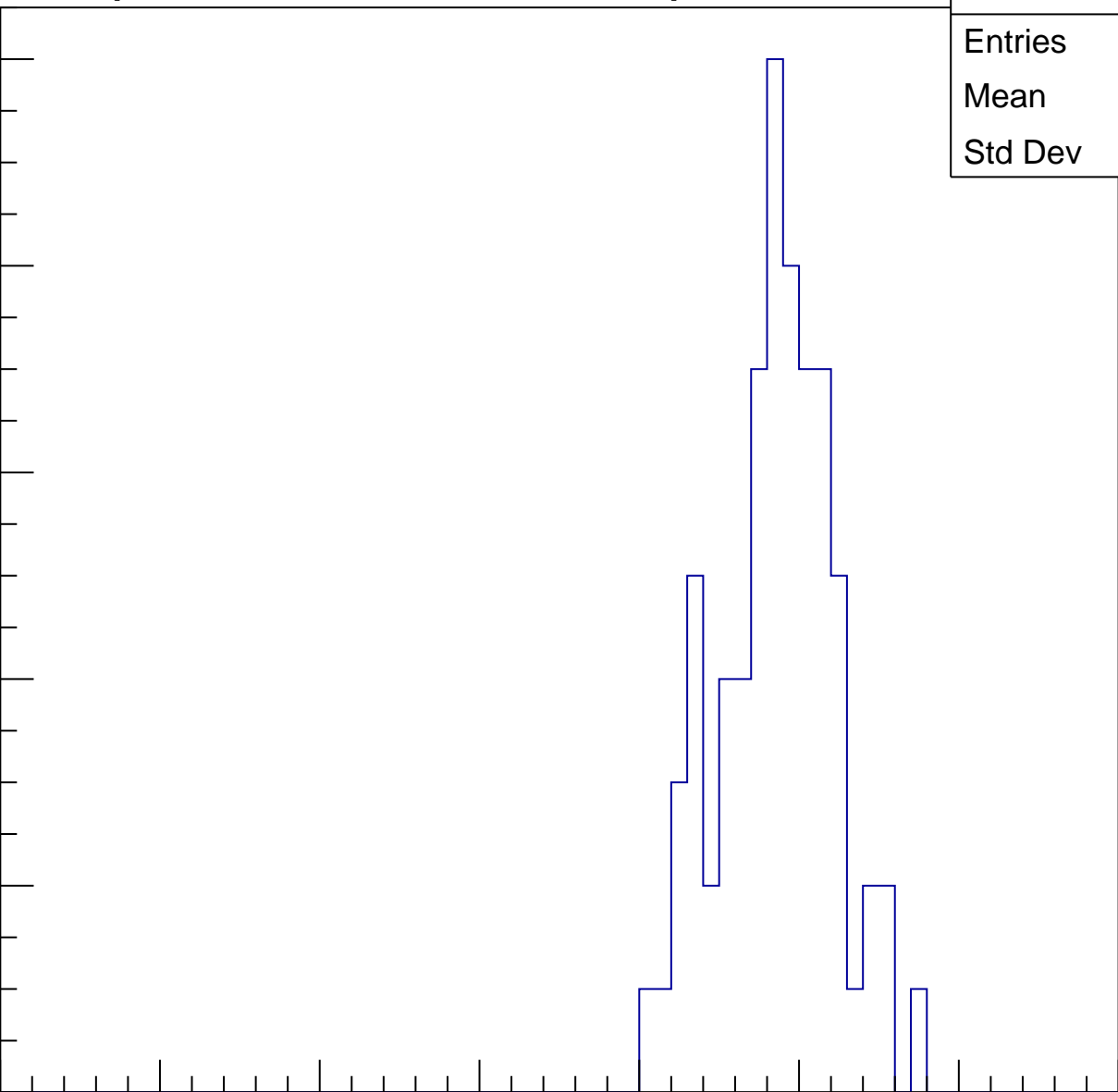
Entries	70
Mean	48.14
Std Dev	3.567

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

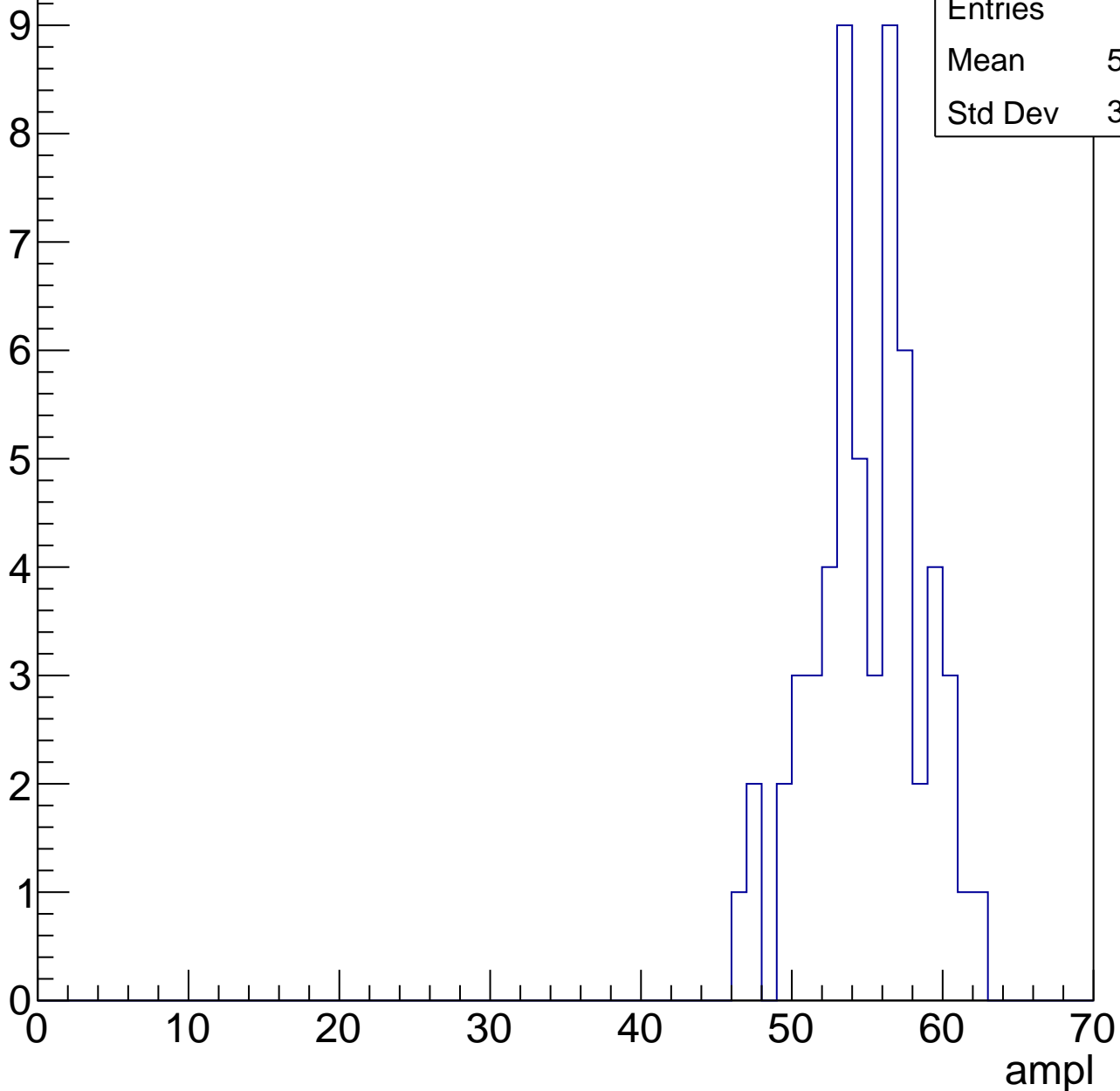


B1L103S, U2-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	54.52
Std Dev	3.578

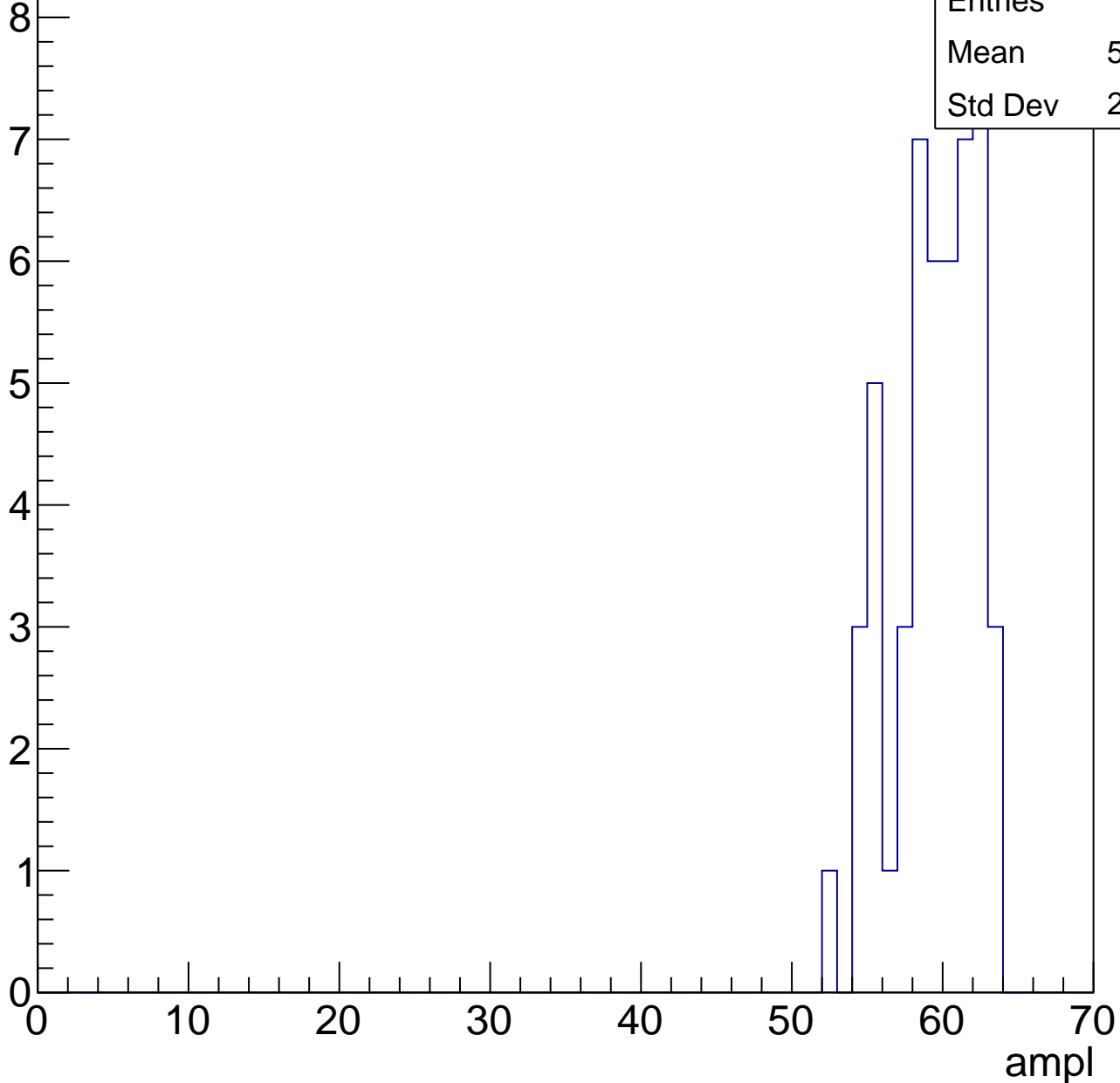


B1L103S, U2-ch71, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.96
Std Dev	2.778

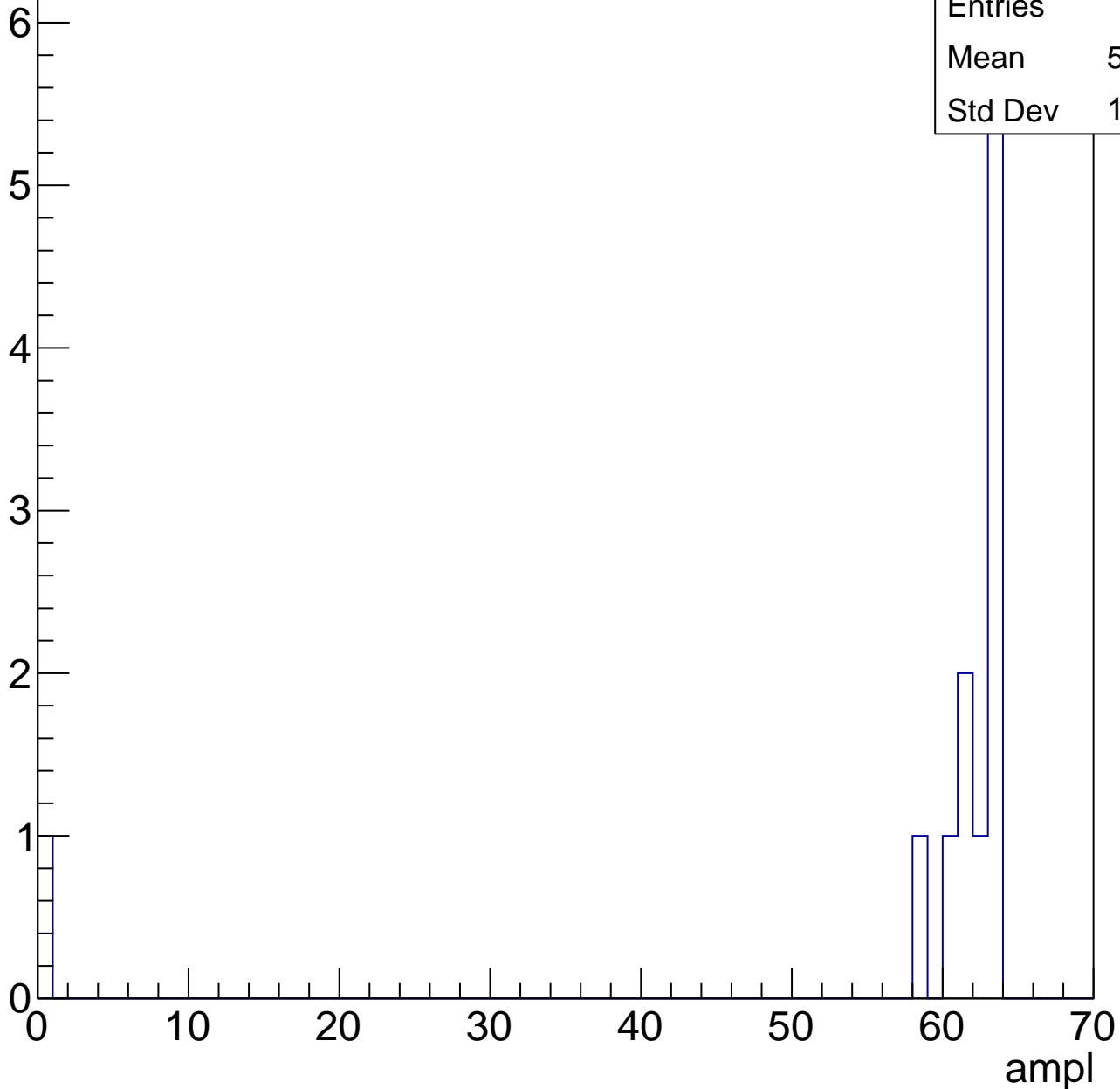


B1L103S, U2-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	56.67
Std Dev	17.15



B1L103S, U2-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U2-ch72, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	18.23
Std Dev	13.28

Gaus mean : 27.6873

Gaus Width: 3.5355

Entry

25

20

15

10

5

0

0

10

20

30

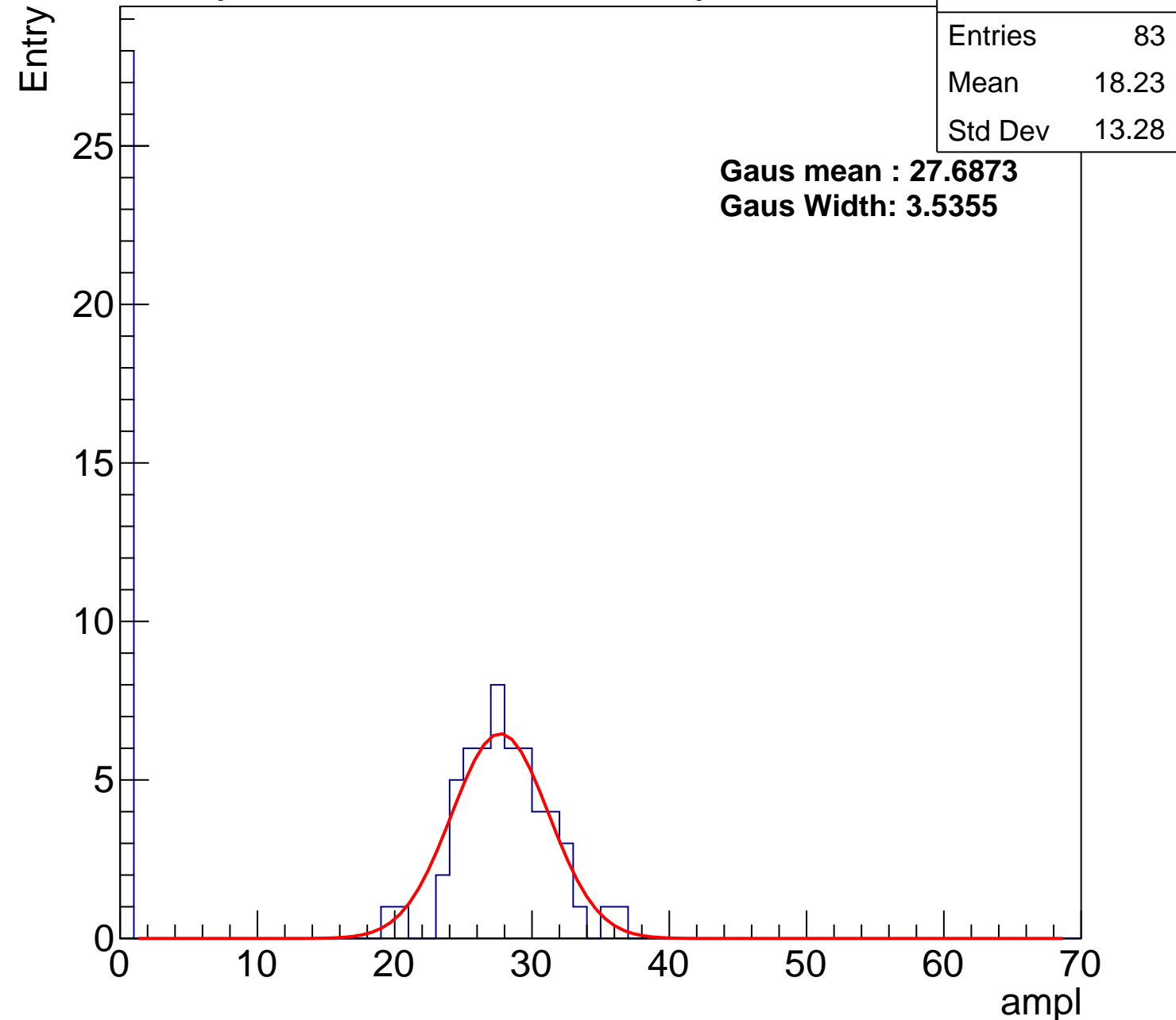
40

50

60

70

ampl



B1L103S, U2-ch72, adc1

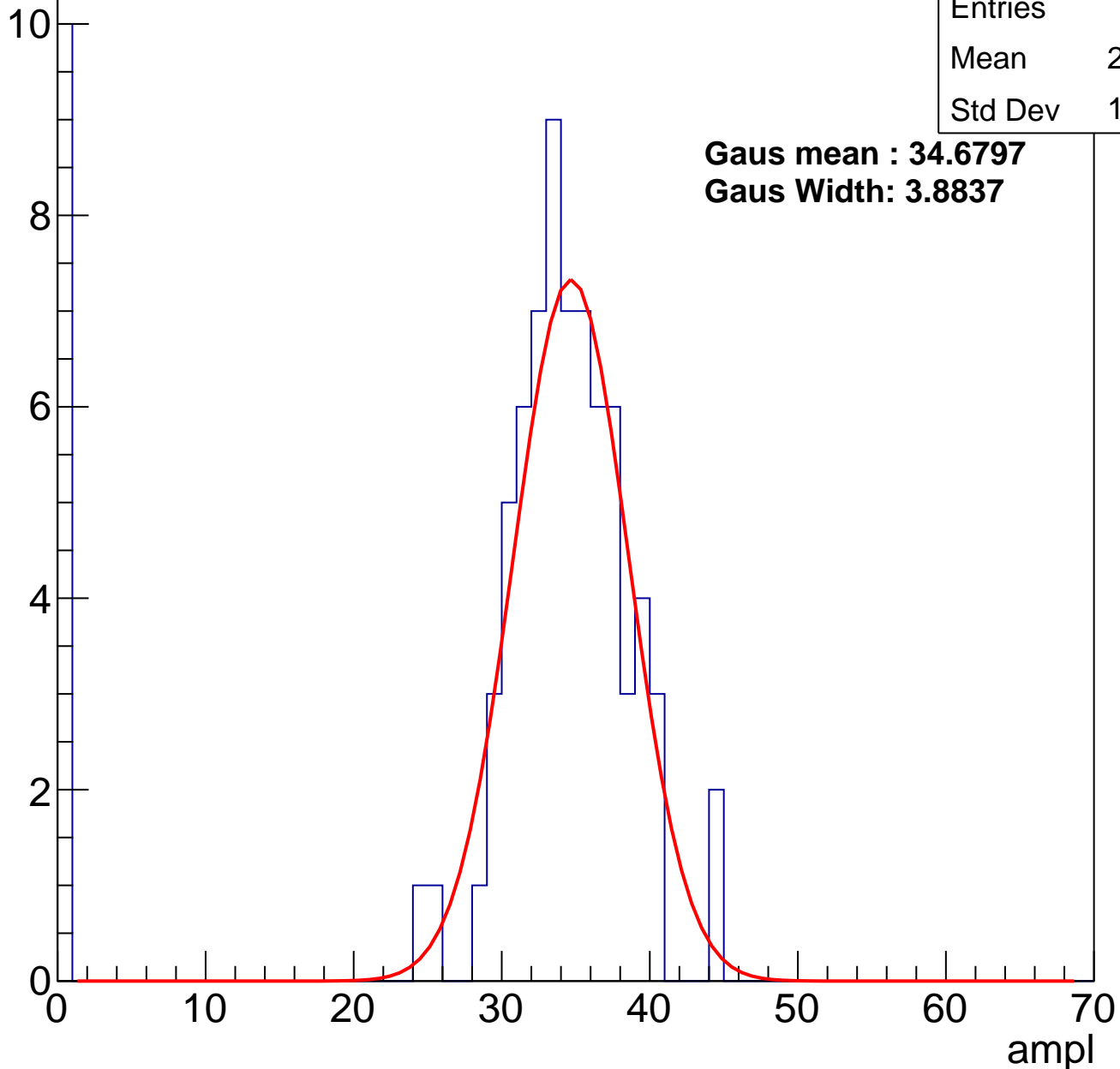
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	29.88
Std Dev	11.75

Gaus mean : 34.6797

Gaus Width: 3.8837

Entry



B1L103S, U2-ch72, adc2

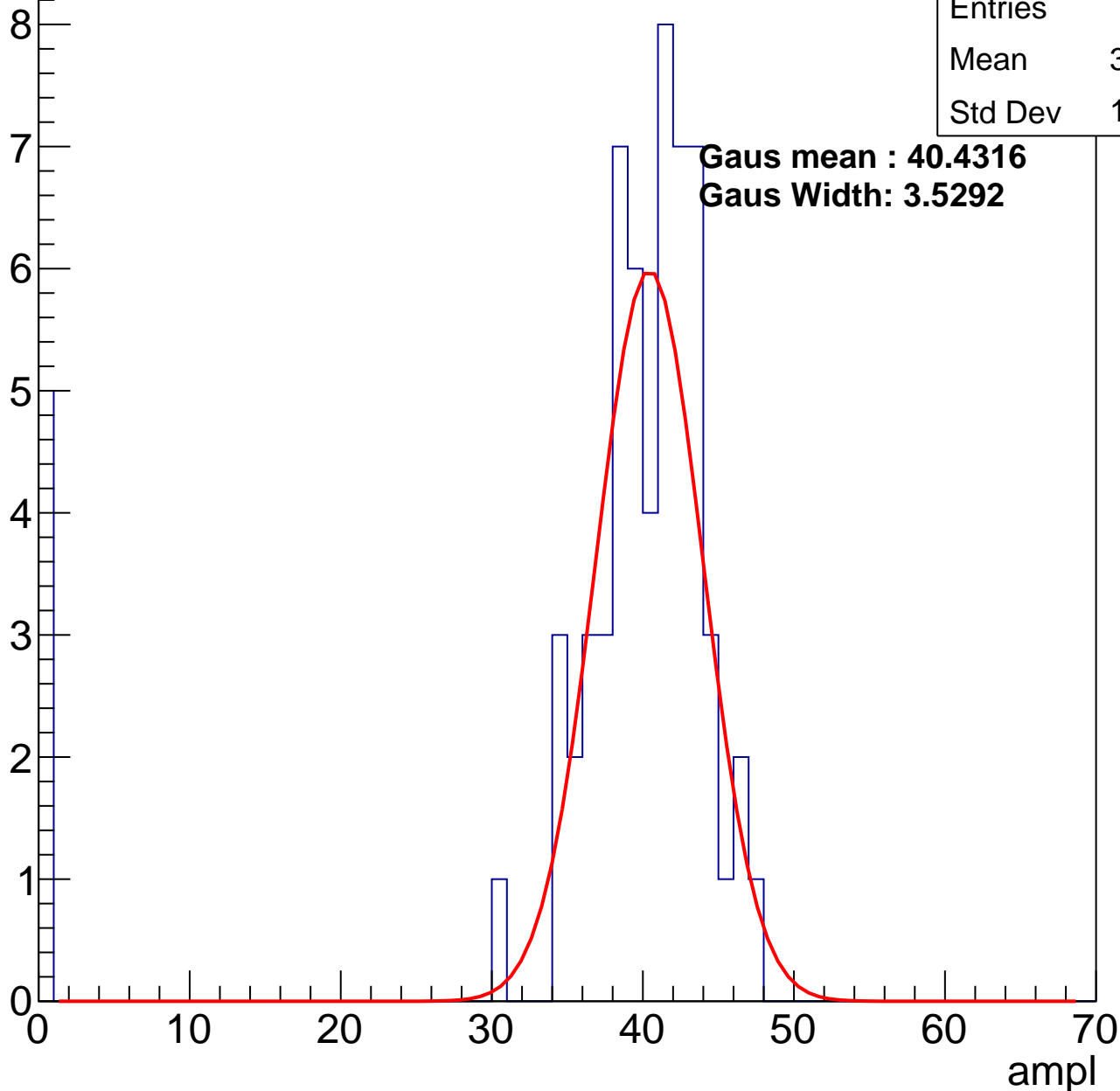
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.83
Std Dev	11.29

Gaus mean : 40.4316

Gaus Width: 3.5292

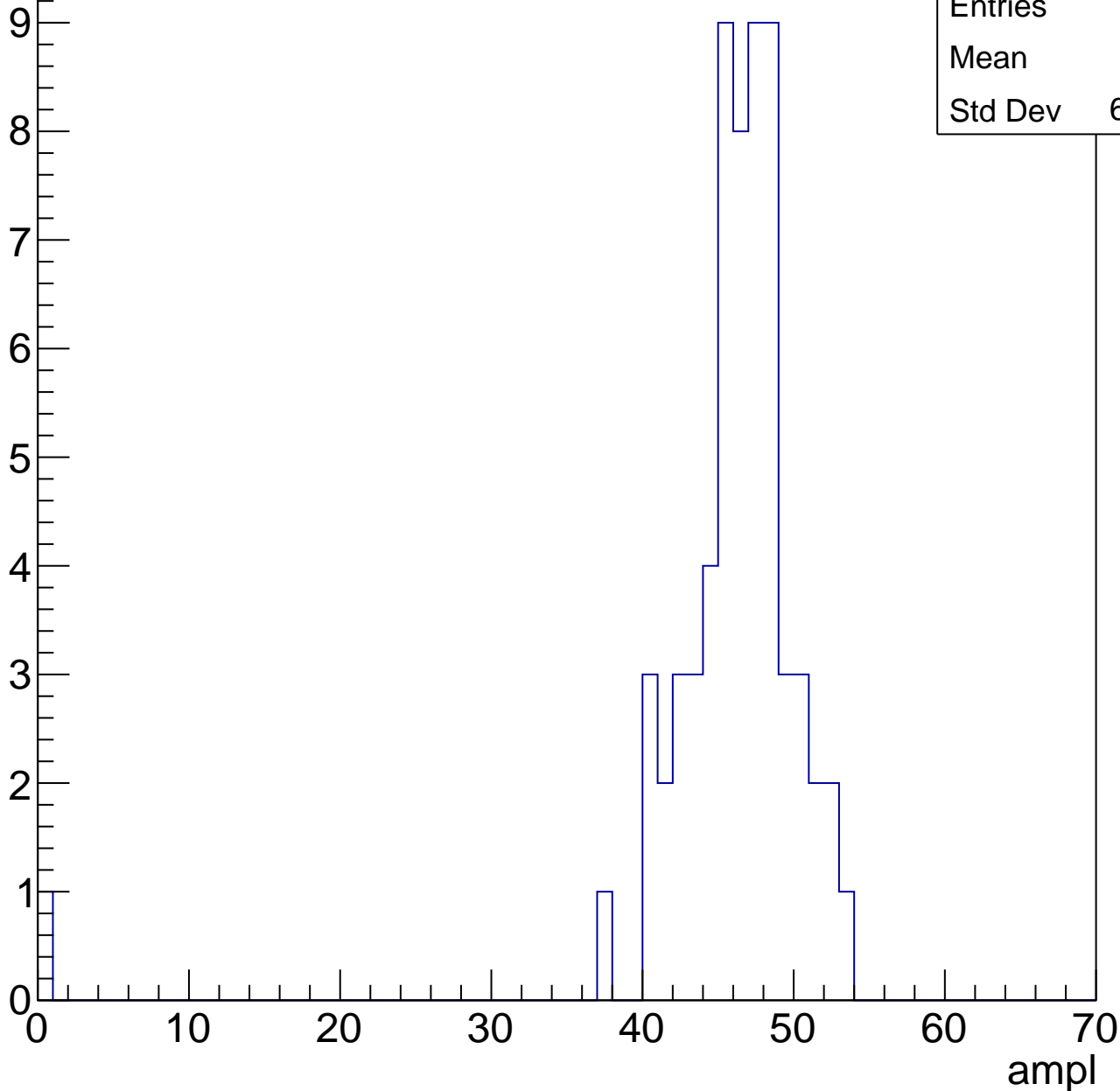


B1L103S, U2-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

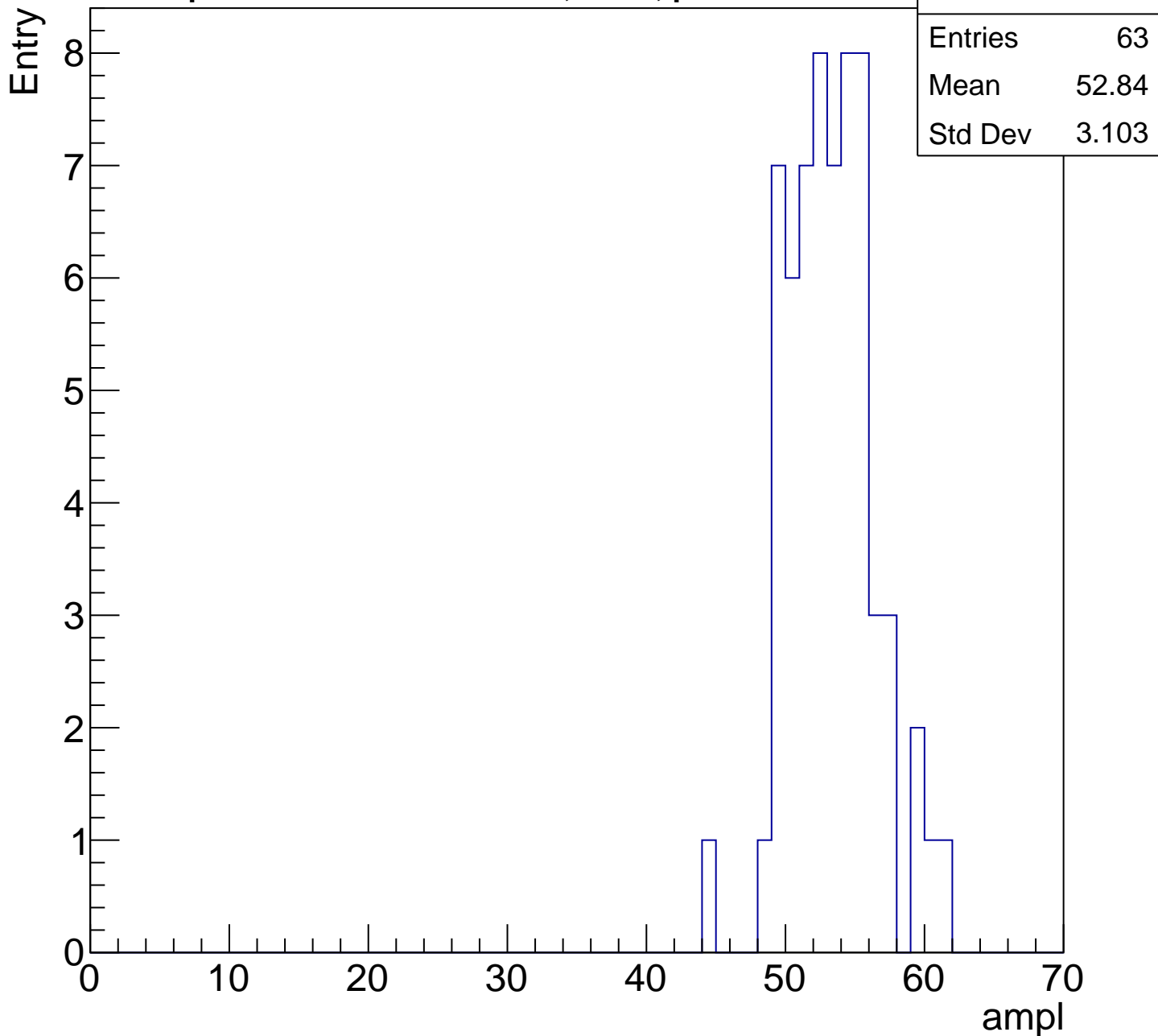
Entry

Entries	63
Mean	45.3
Std Dev	6.565



B1L103S, U2-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

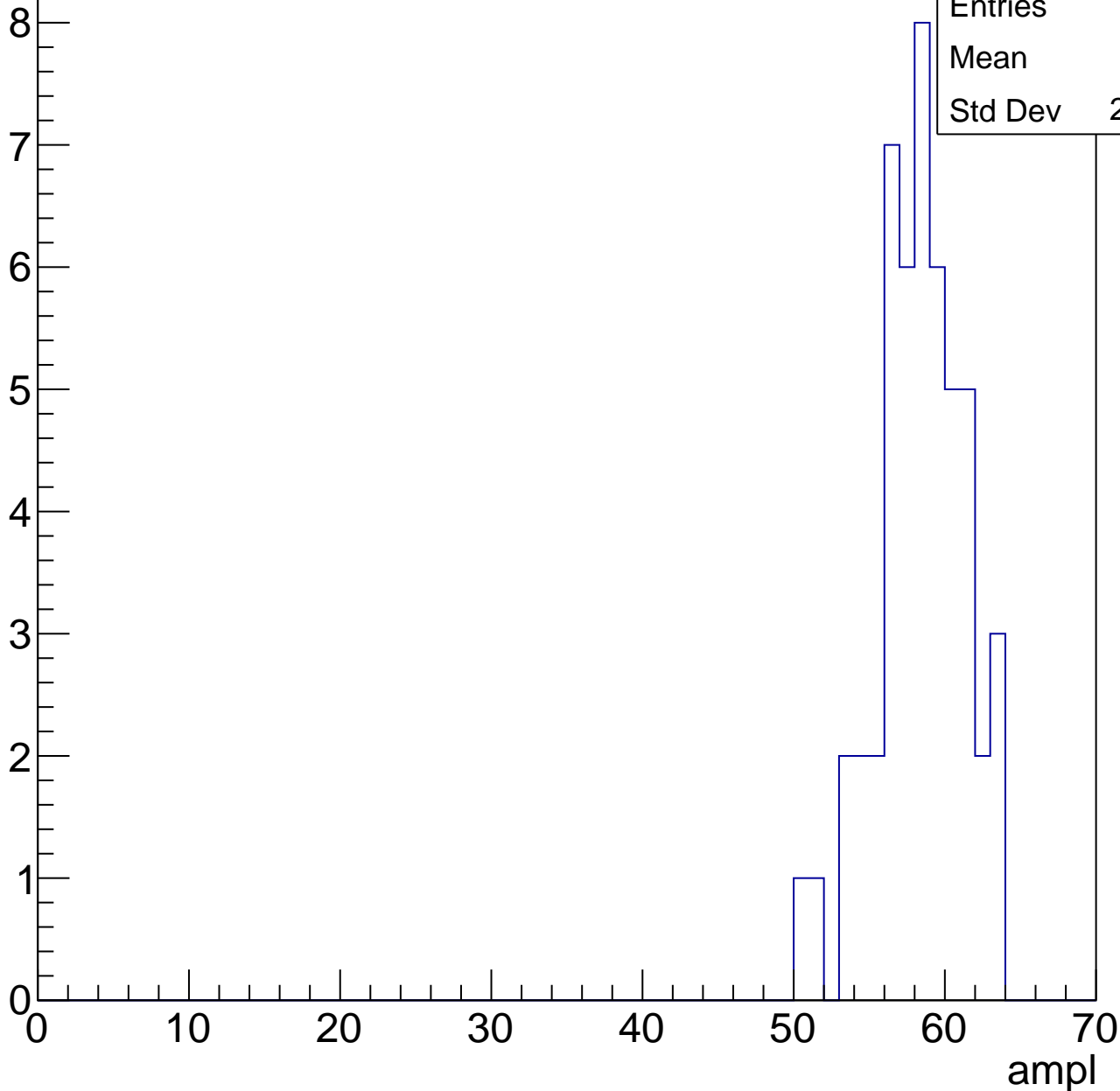


B1L103S, U2-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.9
Std Dev	2.914

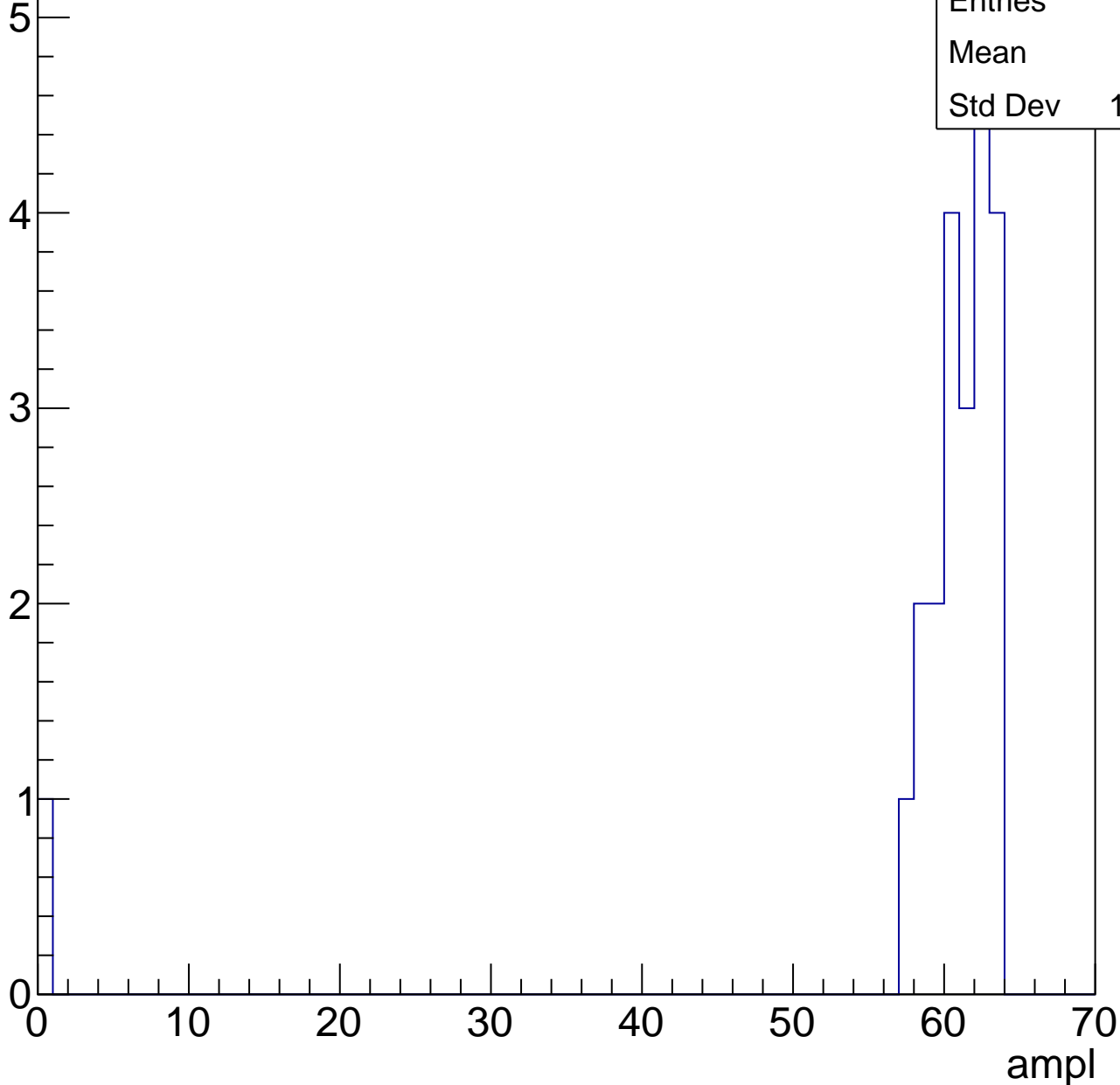


B1L103S, U2-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58
Std Dev	12.77

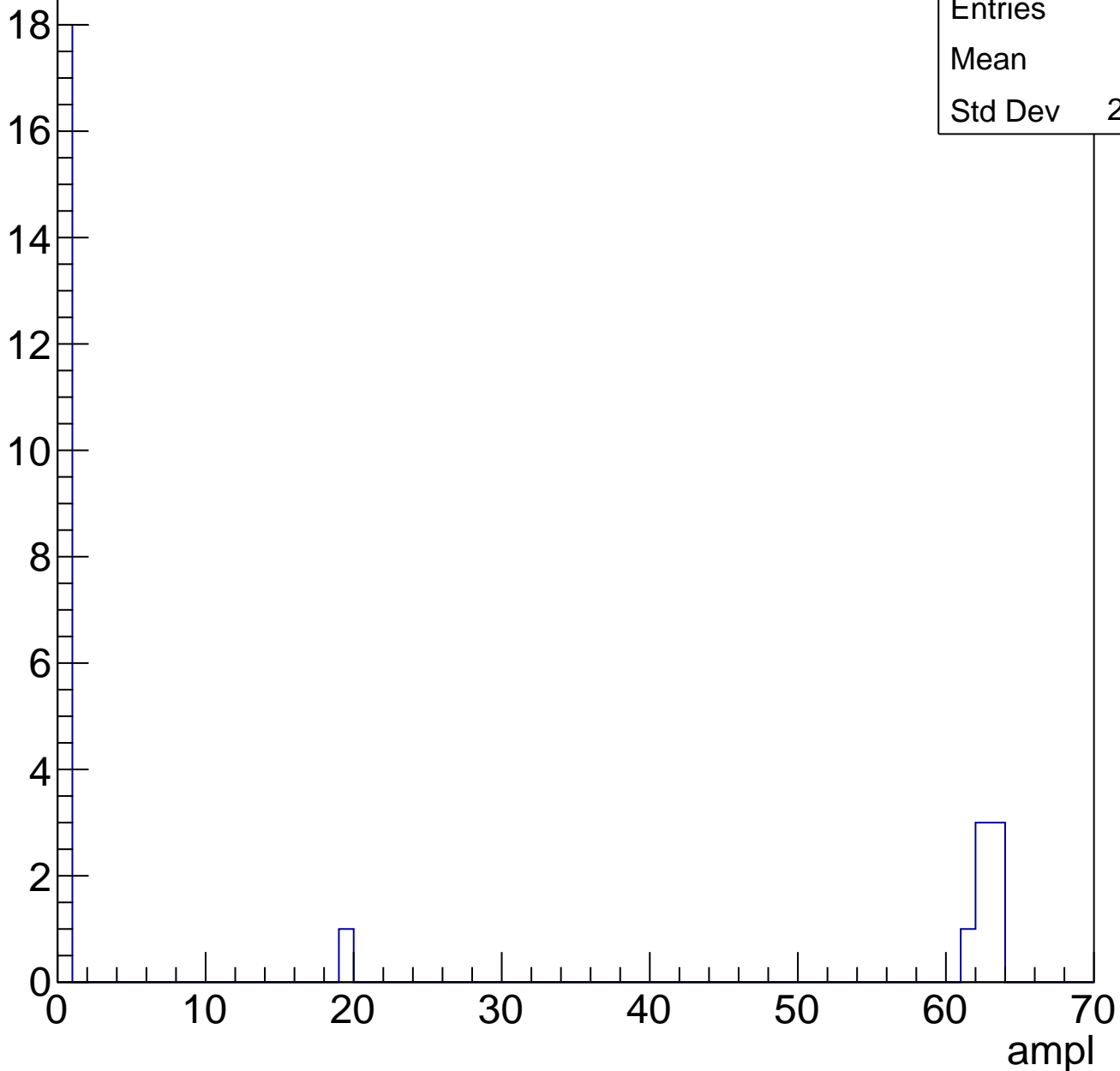


B1L103S, U2-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	17.5
Std Dev	27.43

Entry



B1L103S, U2-ch73, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	23.95
Std Dev	10.55

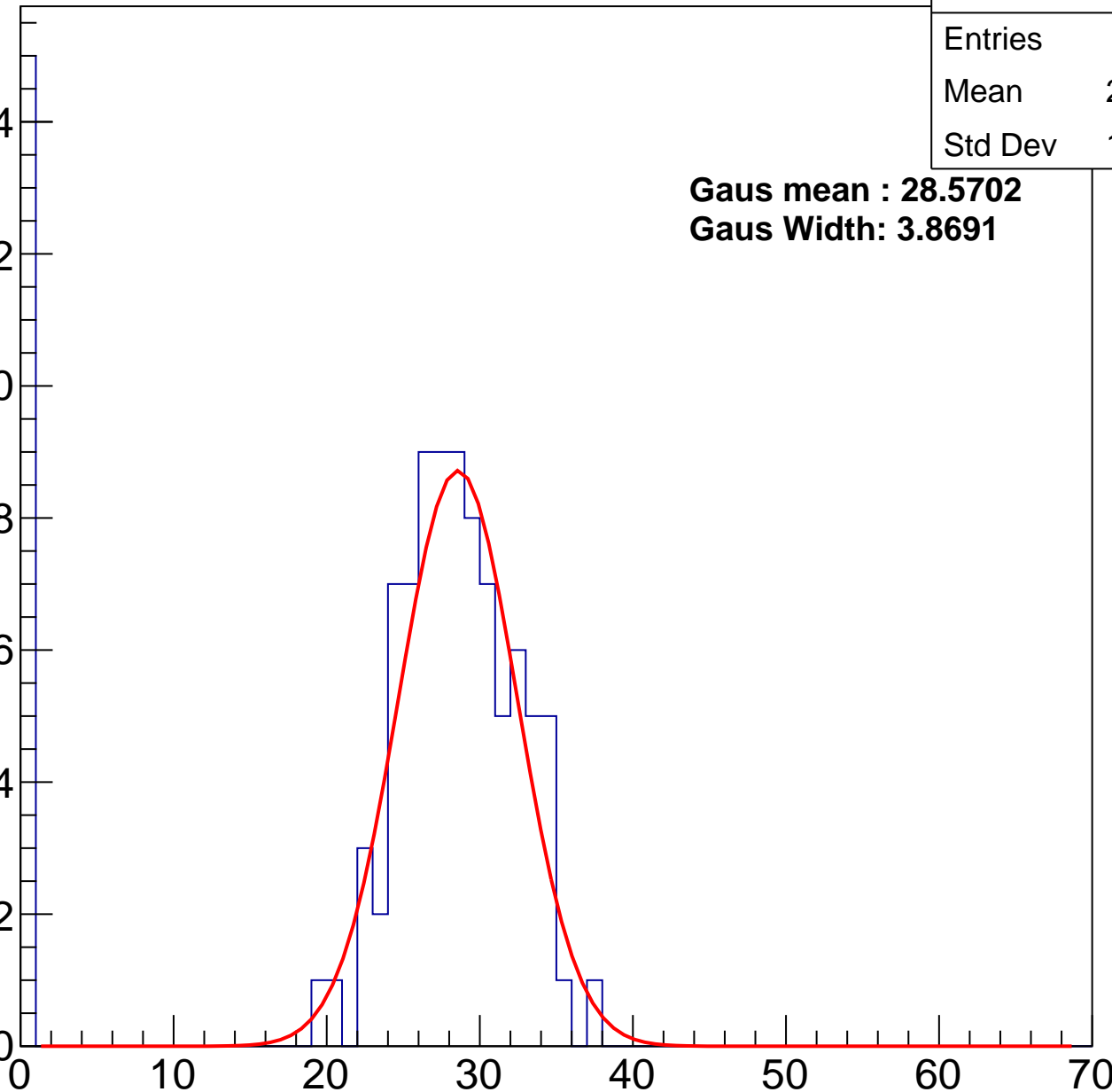
Gaus mean : 28.5702

Gaus Width: 3.8691

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch73, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	35.19
Std Dev	3.486

Gaus mean : 35.2167

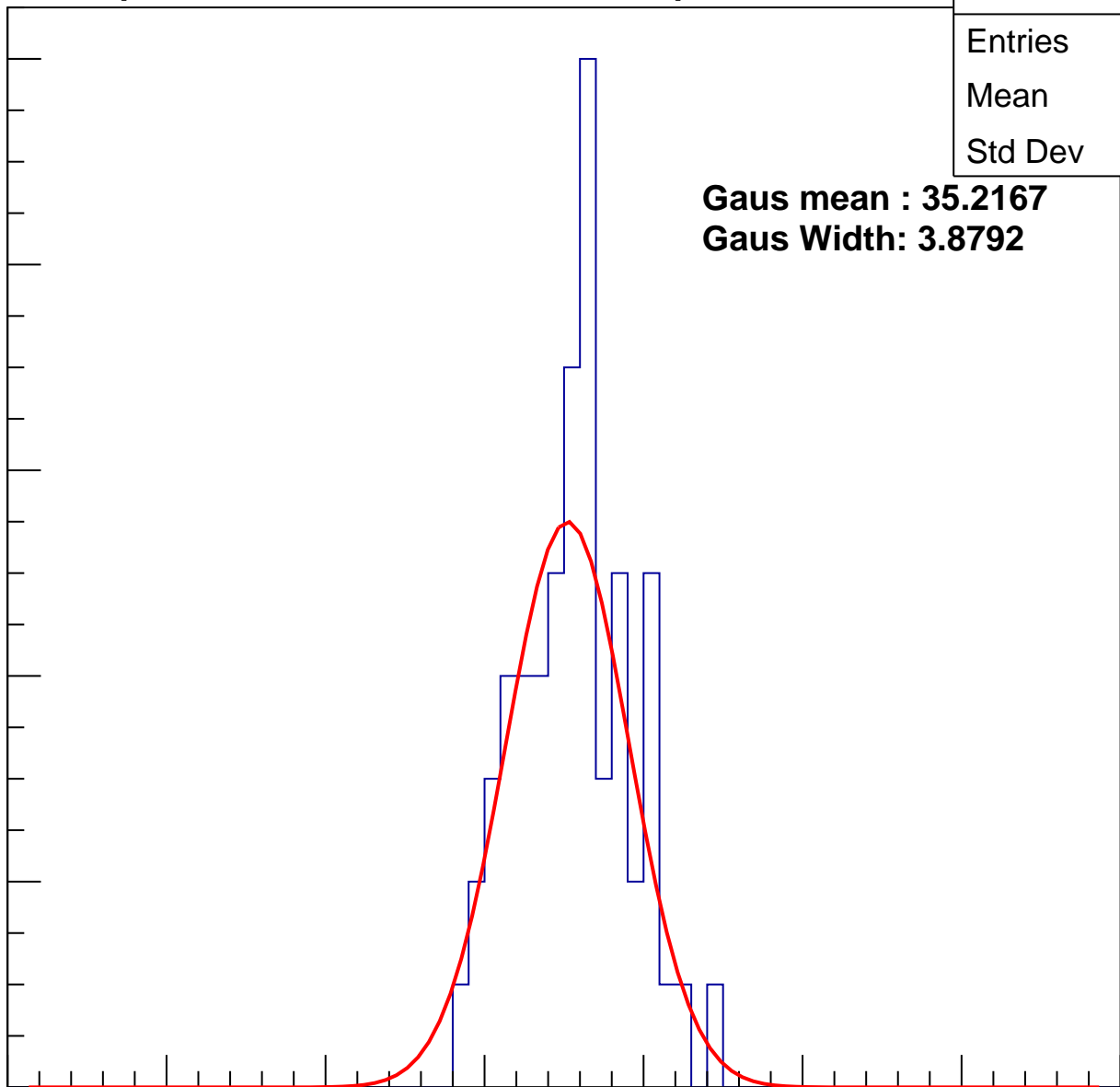
Gaus Width: 3.8792

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch73, adc2

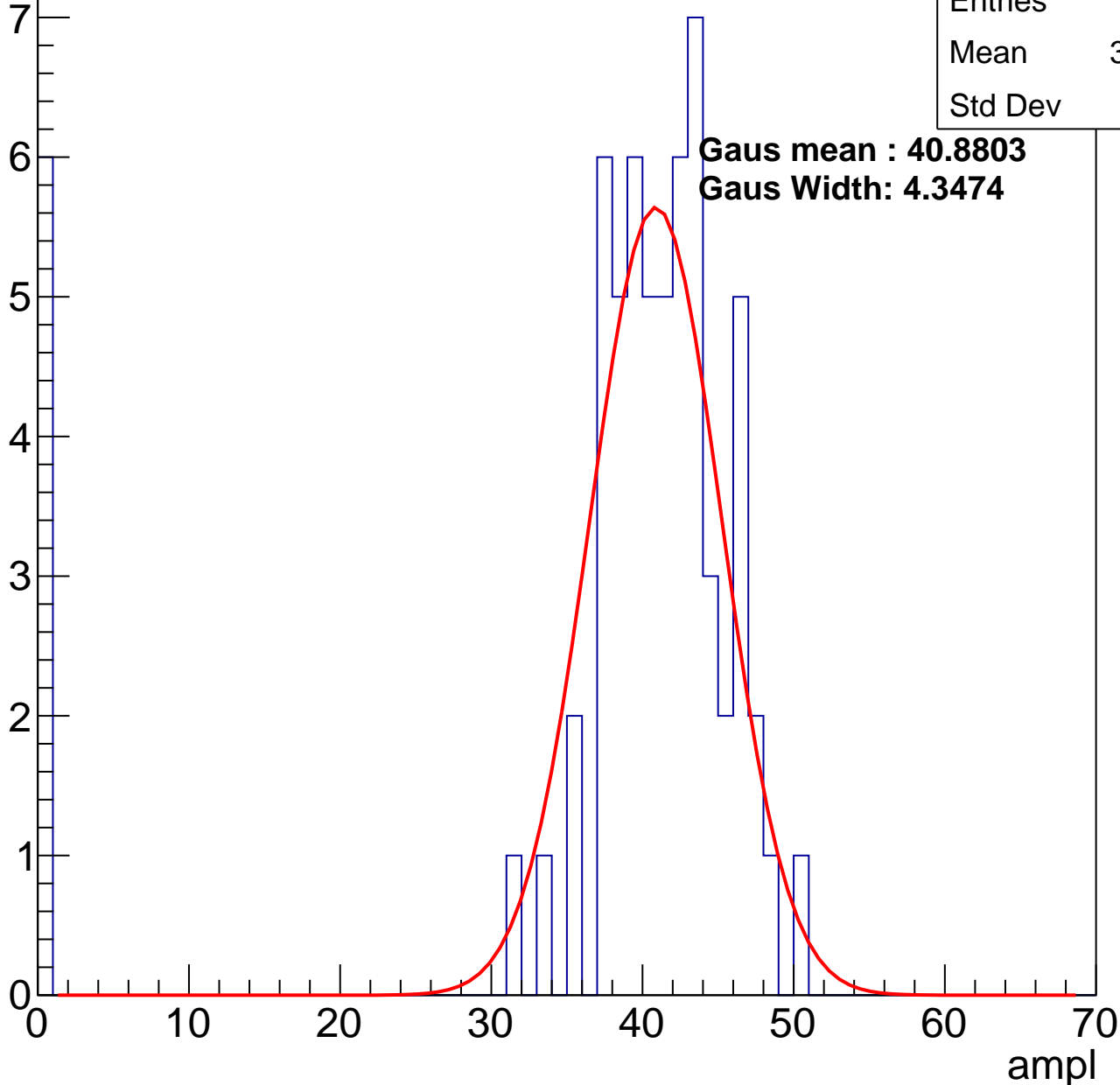
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.22
Std Dev	12.5

Gaus mean : 40.8803

Gaus Width: 4.3474

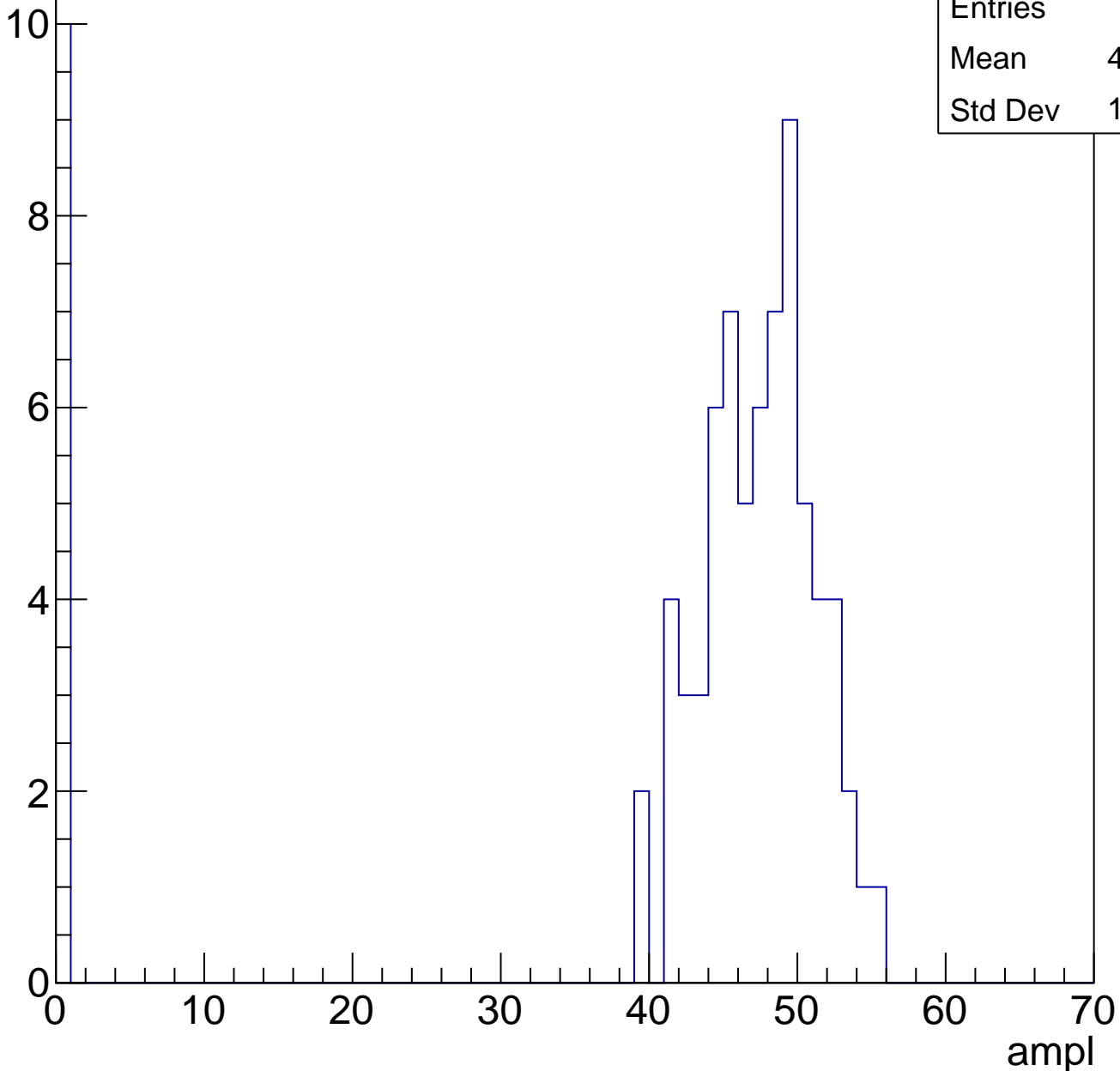


B1L103S, U2-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	41.04
Std Dev	15.99

Entry

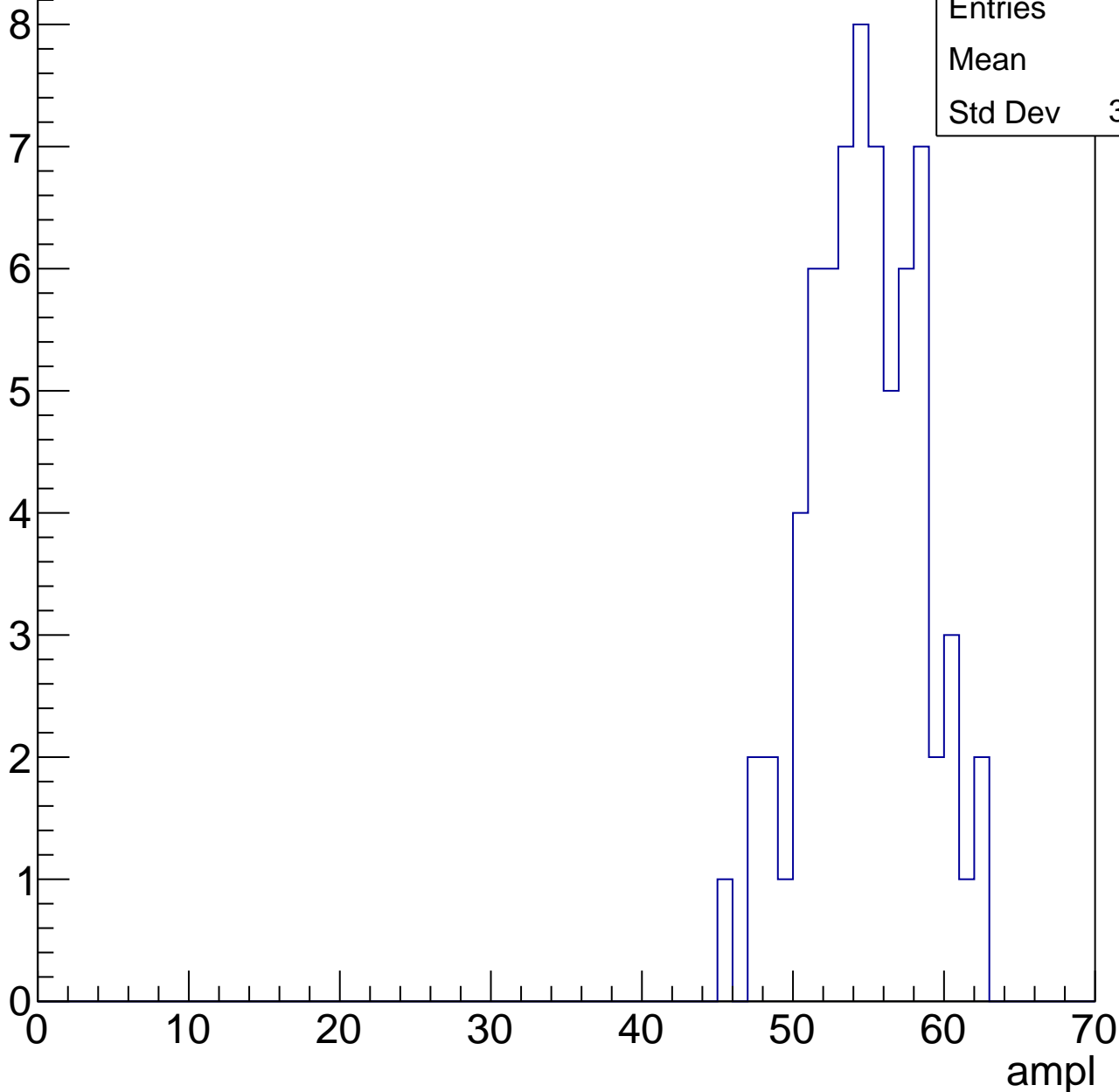


B1L103S, U2-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	54.3
Std Dev	3.678

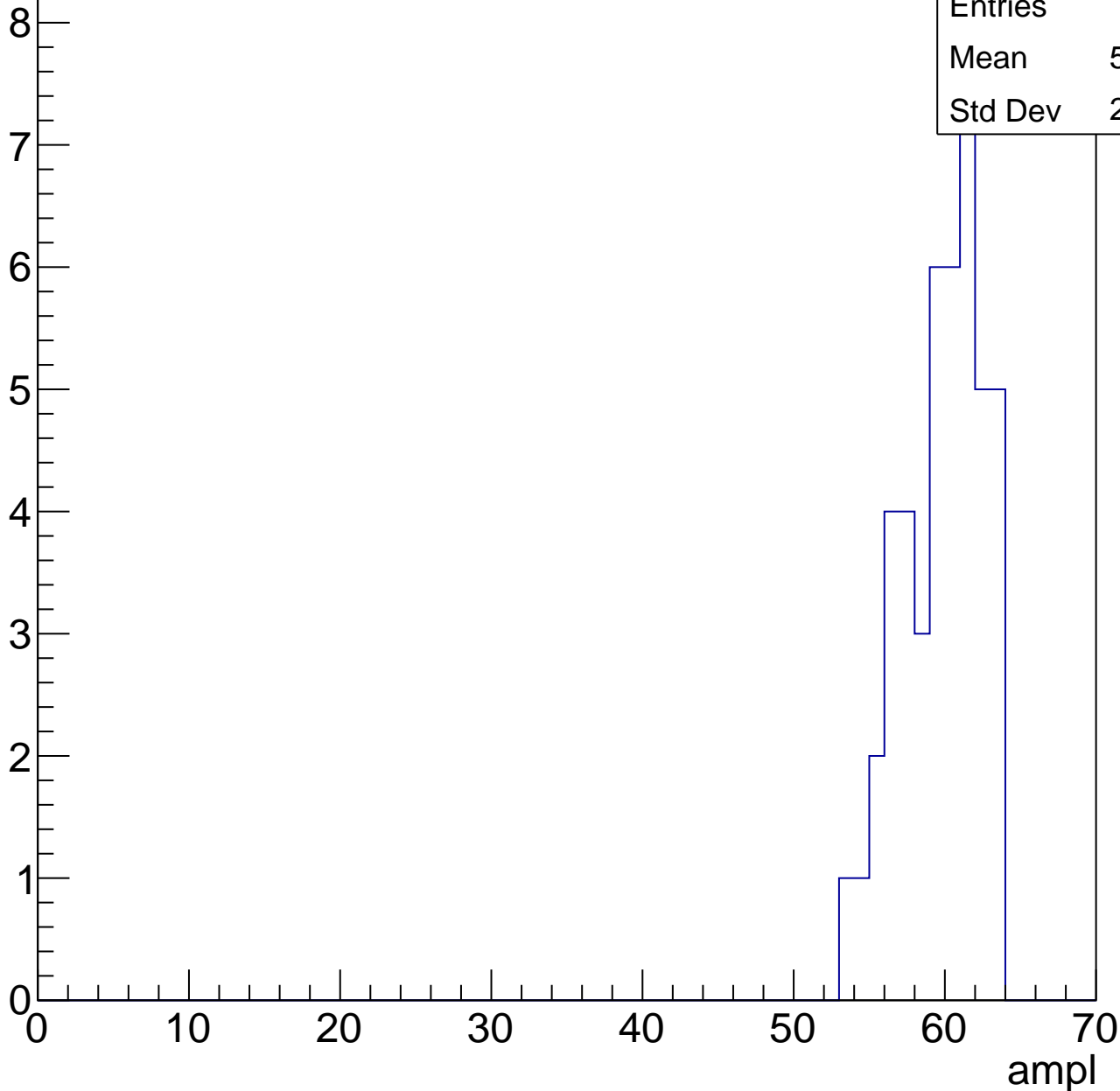


B1L103S, U2-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

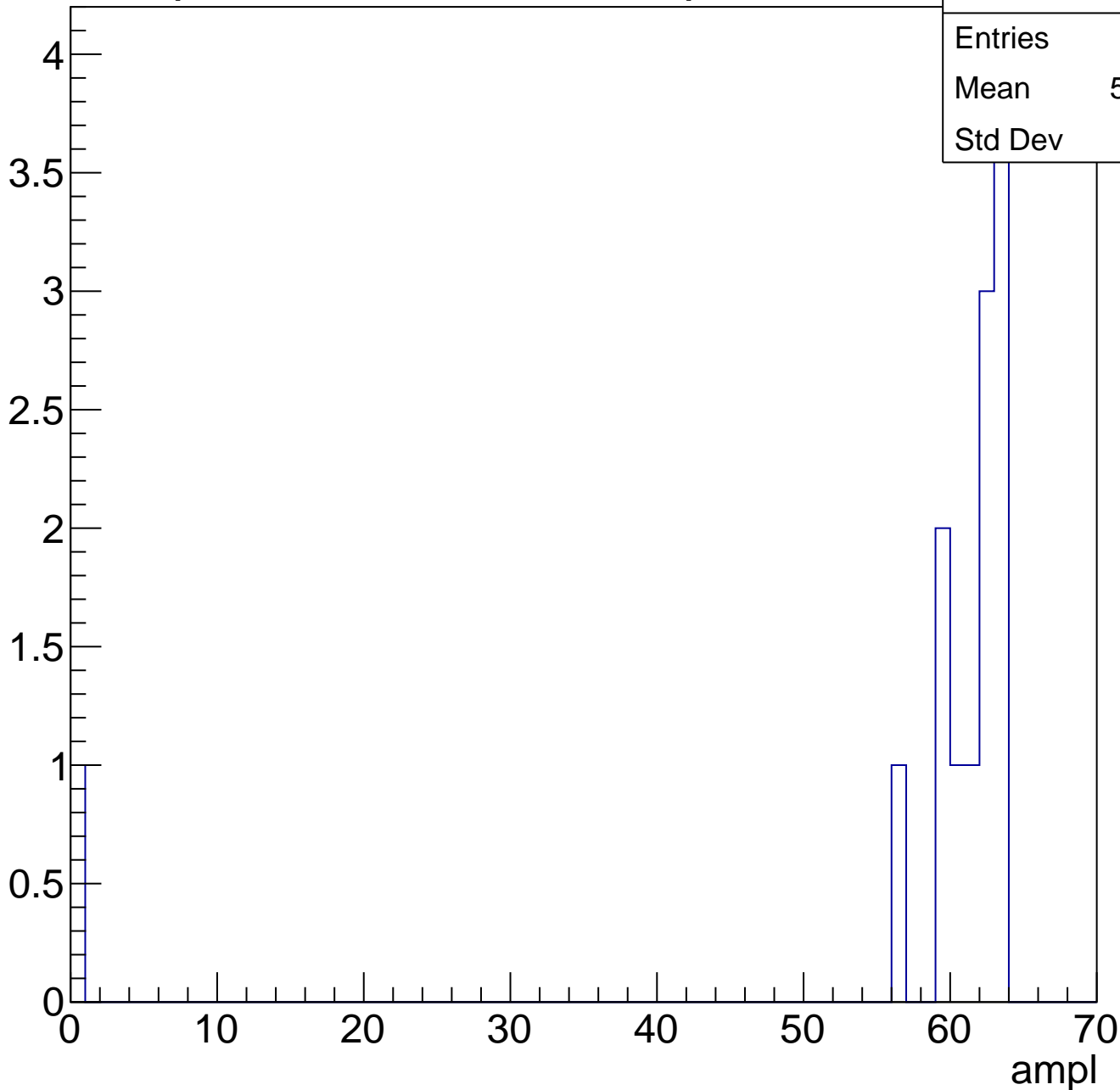
Entries	45
Mean	59.33
Std Dev	2.608



B1L103S, U2-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

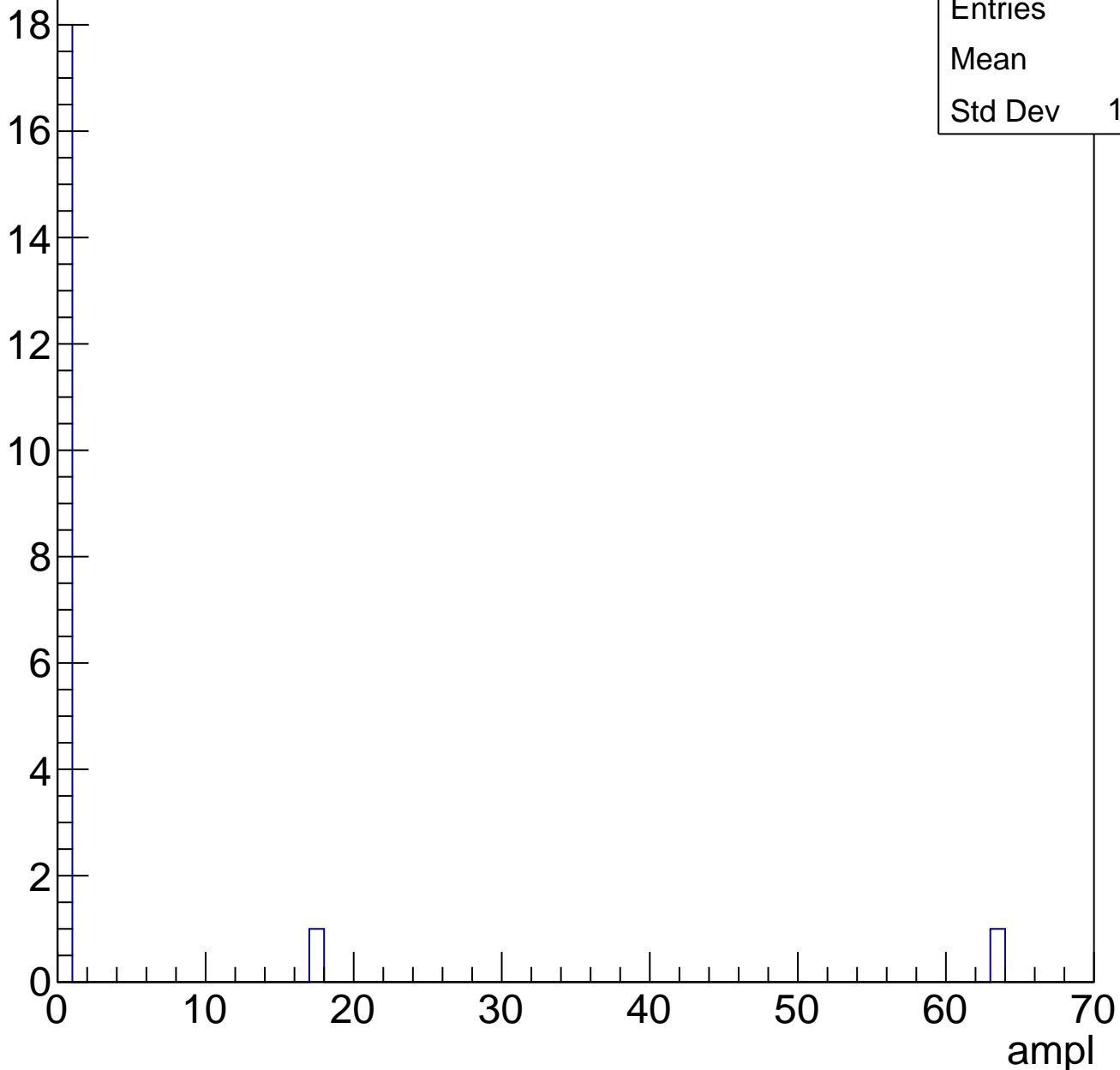
Entry



B1L103S, U2-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	4
Std Dev	14.03

B1L103S, U2-ch74, adc0

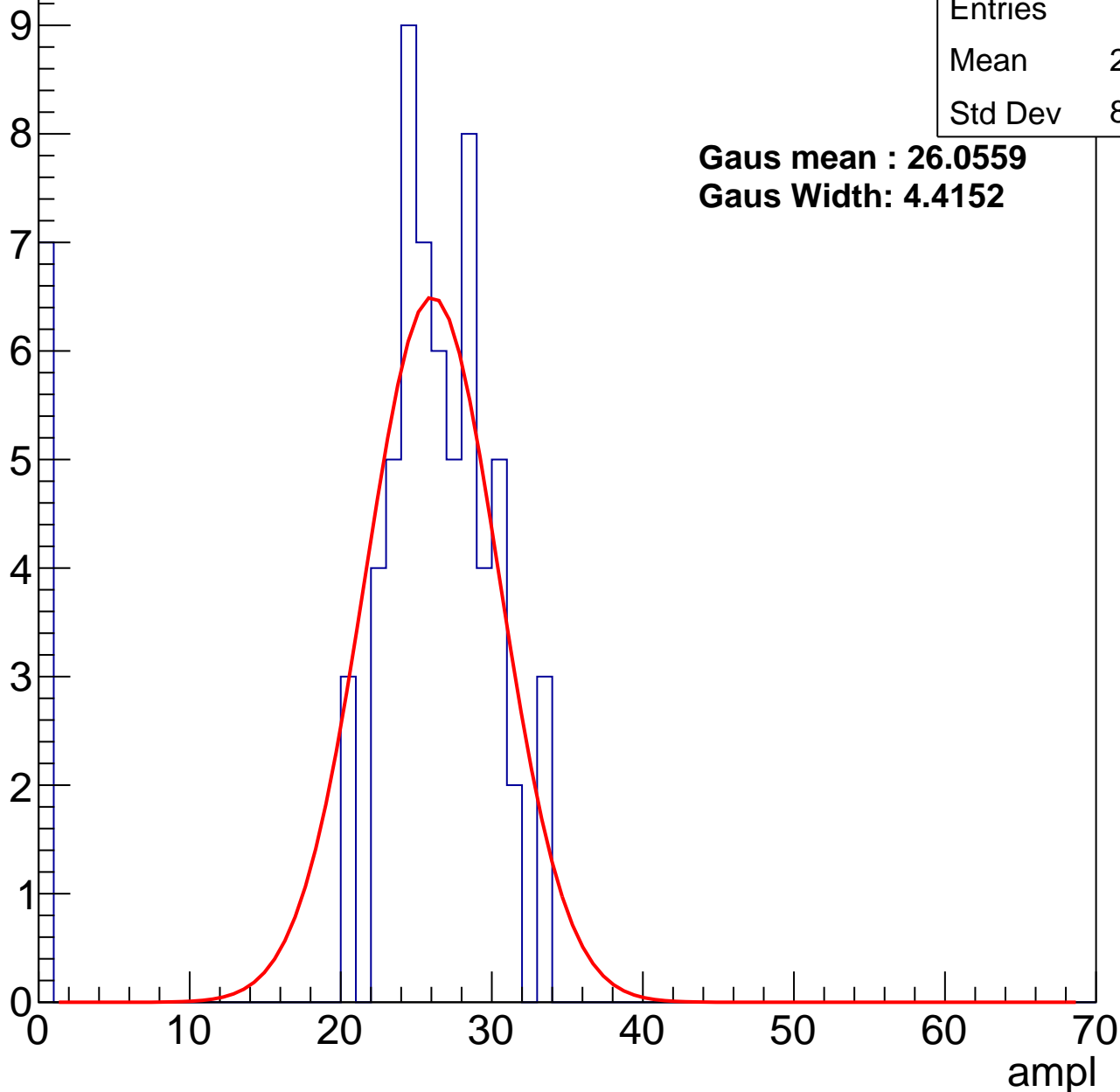
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	23.47
Std Dev	8.495

Gaus mean : 26.0559

Gaus Width: 4.4152



B1L103S, U2-ch74, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	31.77
Std Dev	7.894

Gaus mean : 33.8586

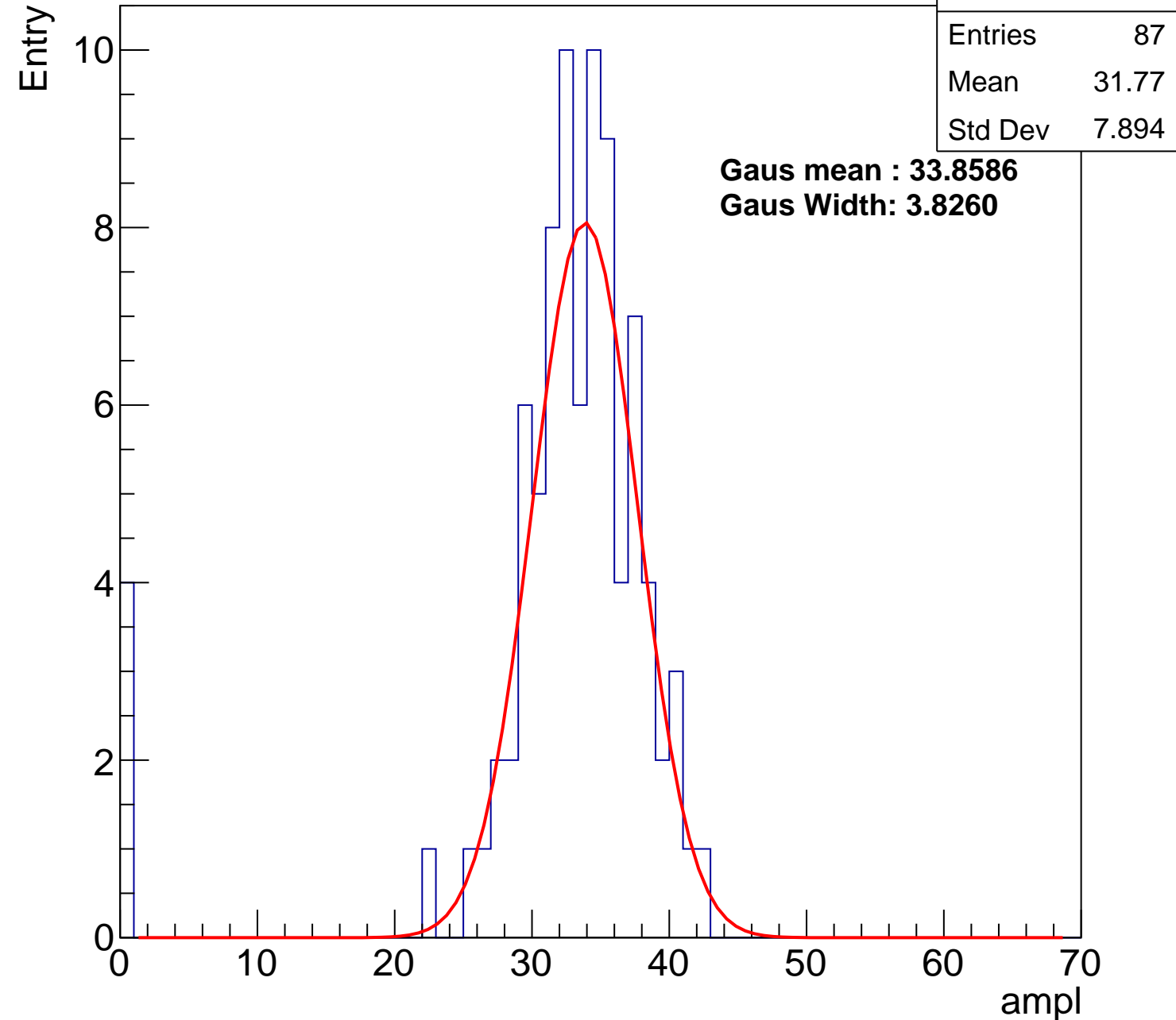
Gaus Width: 3.8260

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch74, adc2

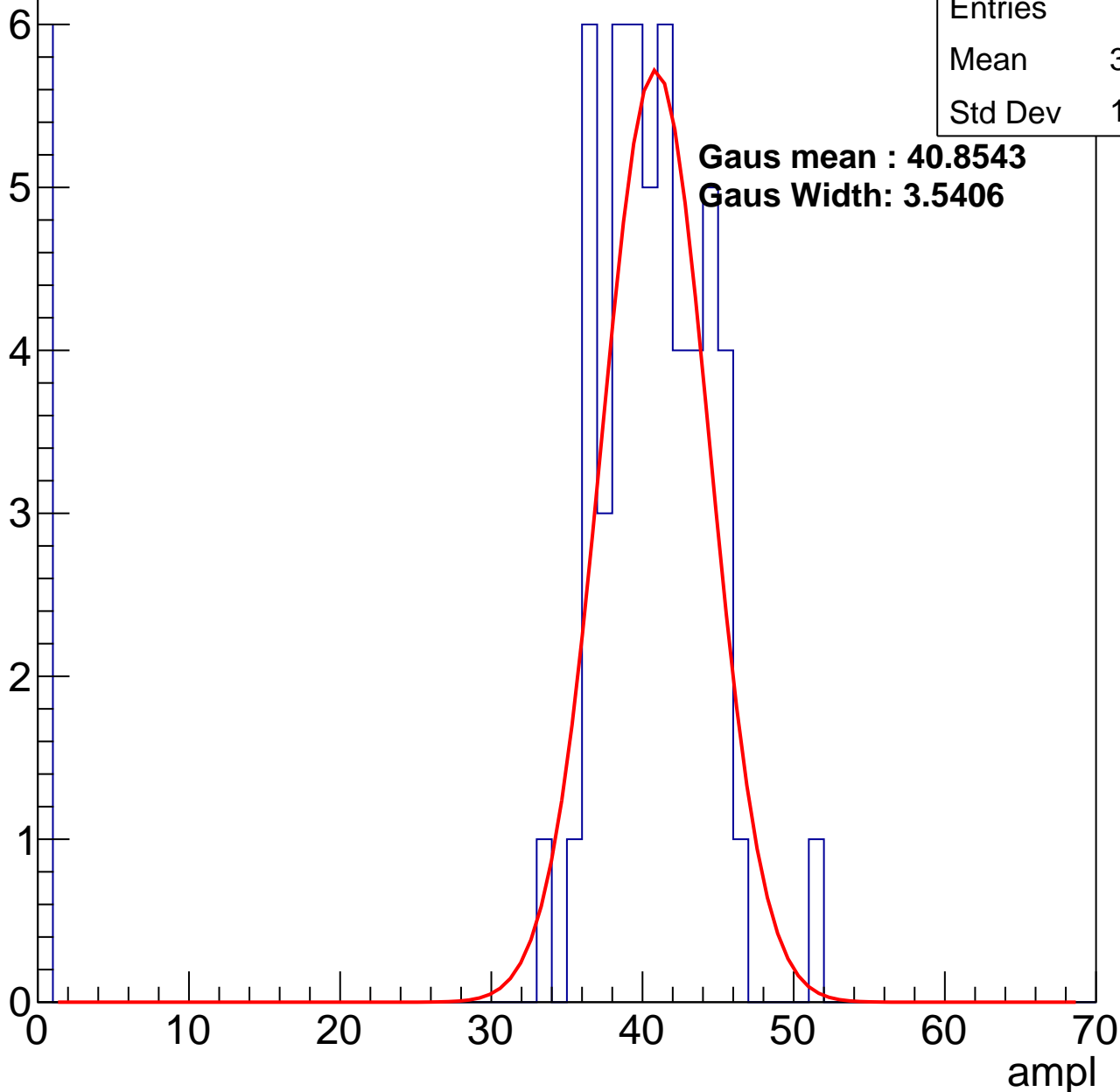
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	36.27
Std Dev	12.62

Gaus mean : 40.8543

Gaus Width: 3.5406

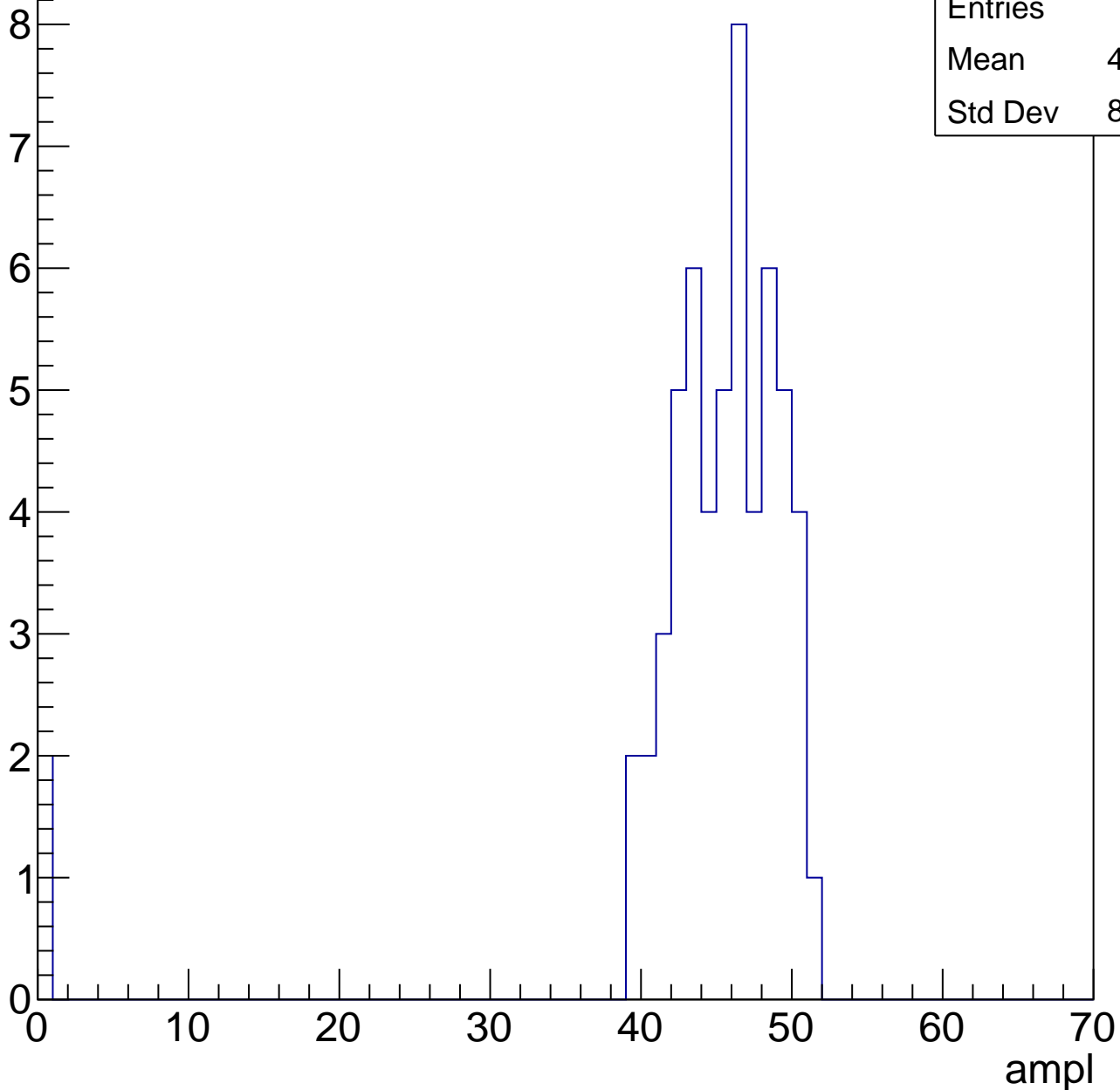


B1L103S, U2-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

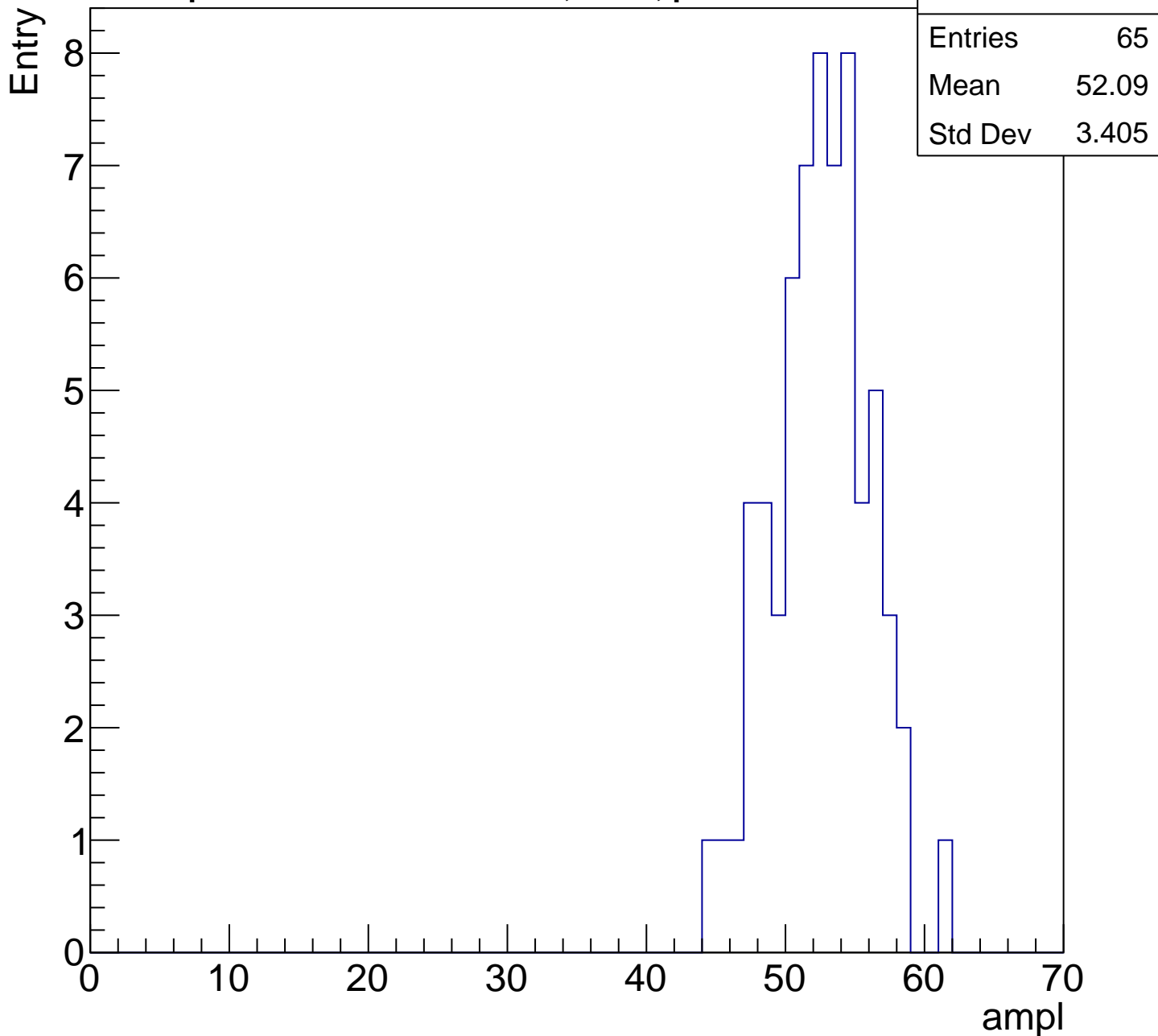
Entry

Entries	57
Mean	43.68
Std Dev	8.874



B1L103S, U2-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

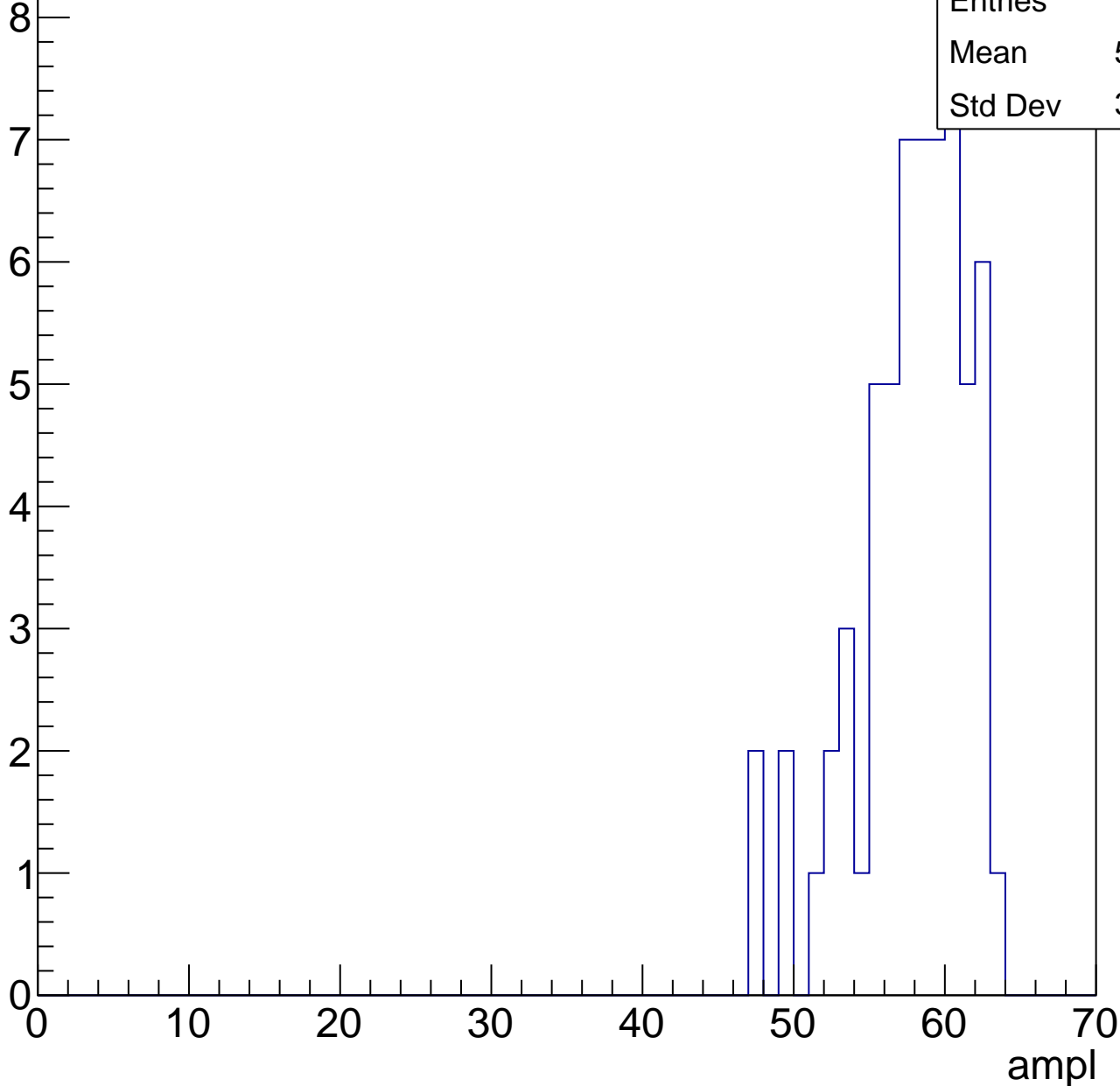


B1L103S, U2-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

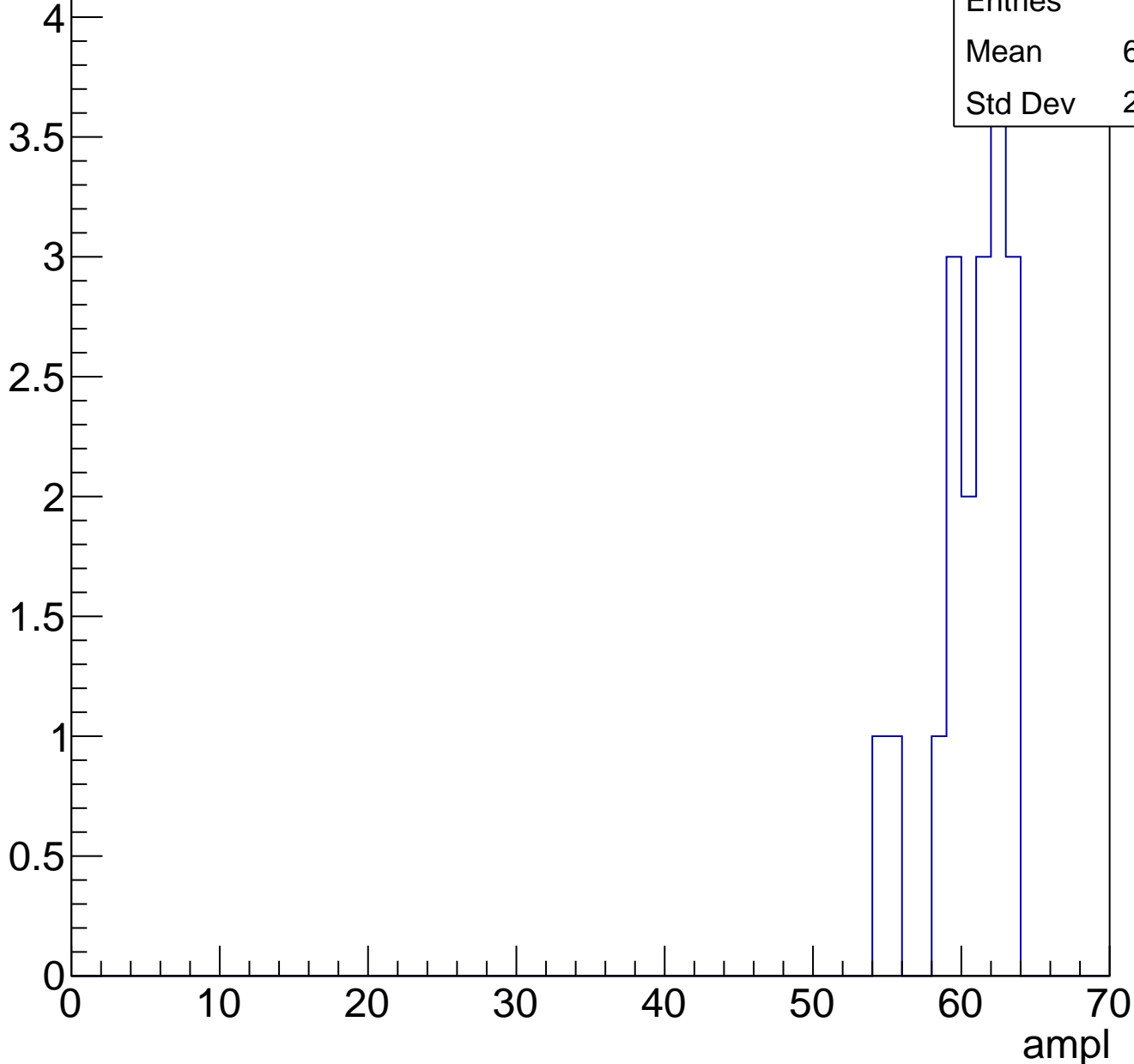
Entries	62
Mean	57.31
Std Dev	3.731



B1L103S, U2-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

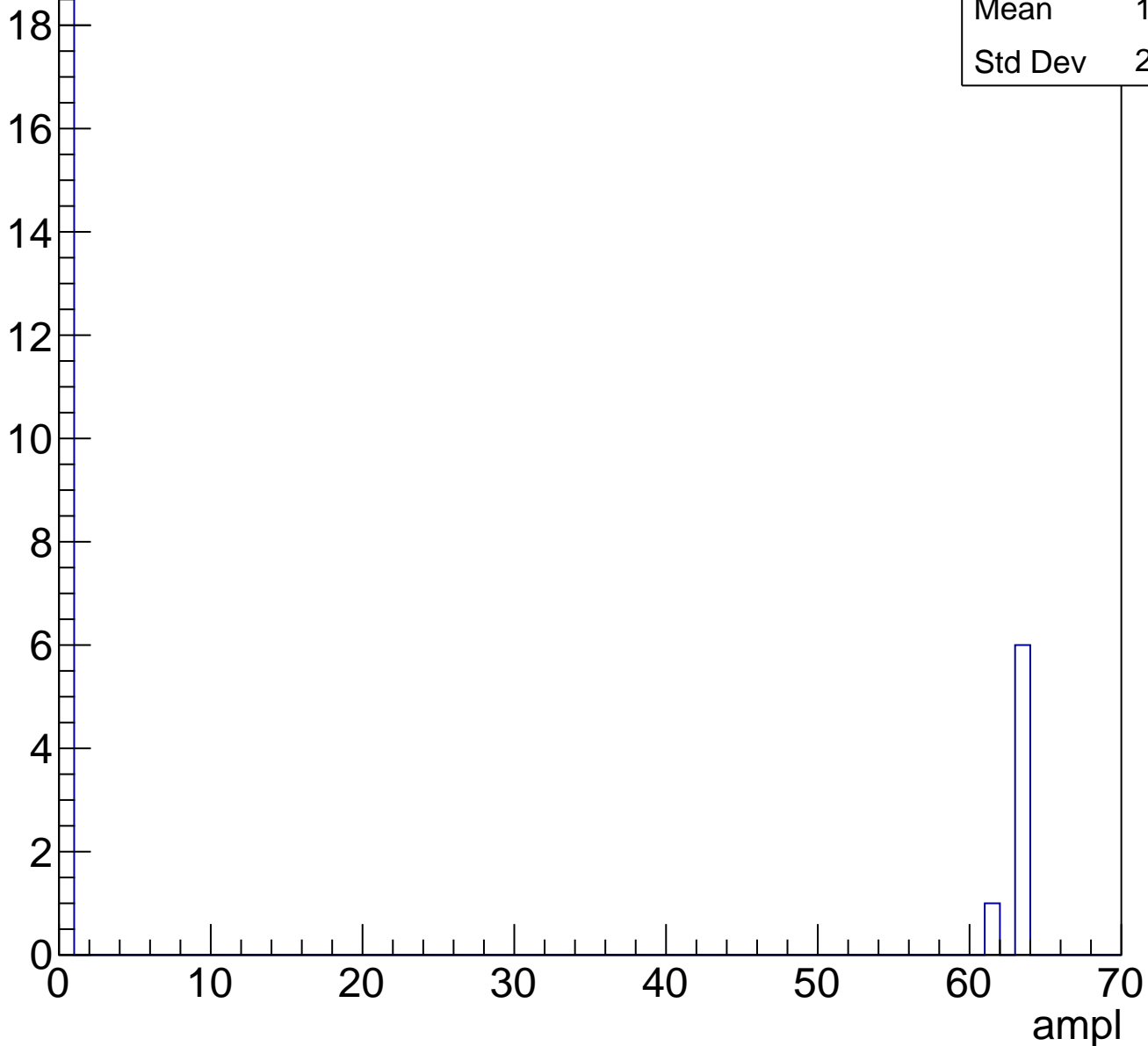


B1L103S, U2-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	16.88
Std Dev	27.82

Entry



B1L103S, U2-ch75, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	25.38
Std Dev	10.05

Gaus mean : 30.3965

Gaus Width: 3.4319

Entry

10

8

6

4

2

0

0

10

20

30

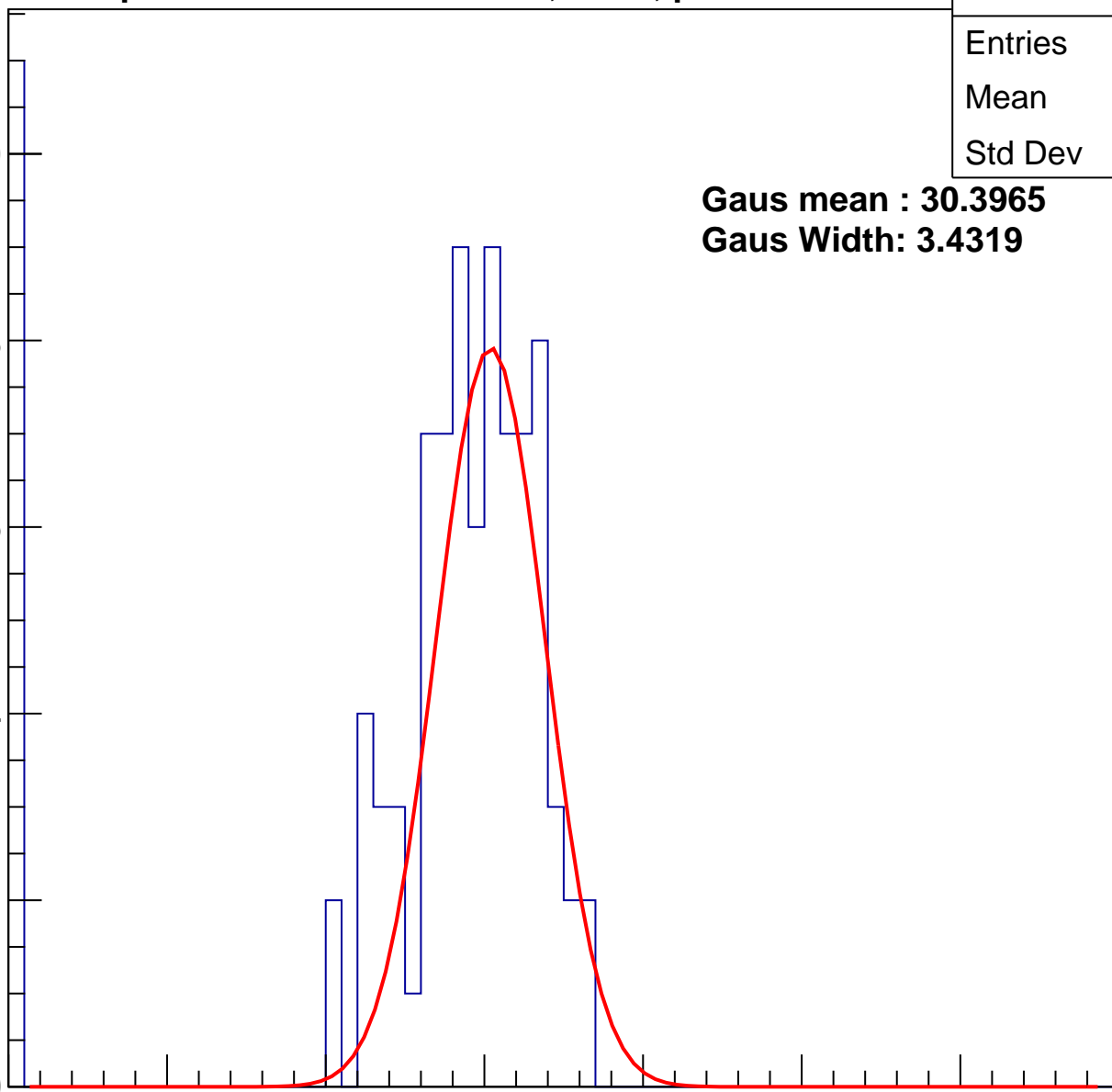
40

50

60

70

ampl



B1L103S, U2-ch75, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	29.2
Std Dev	14.41

Gaus mean : 36.6988

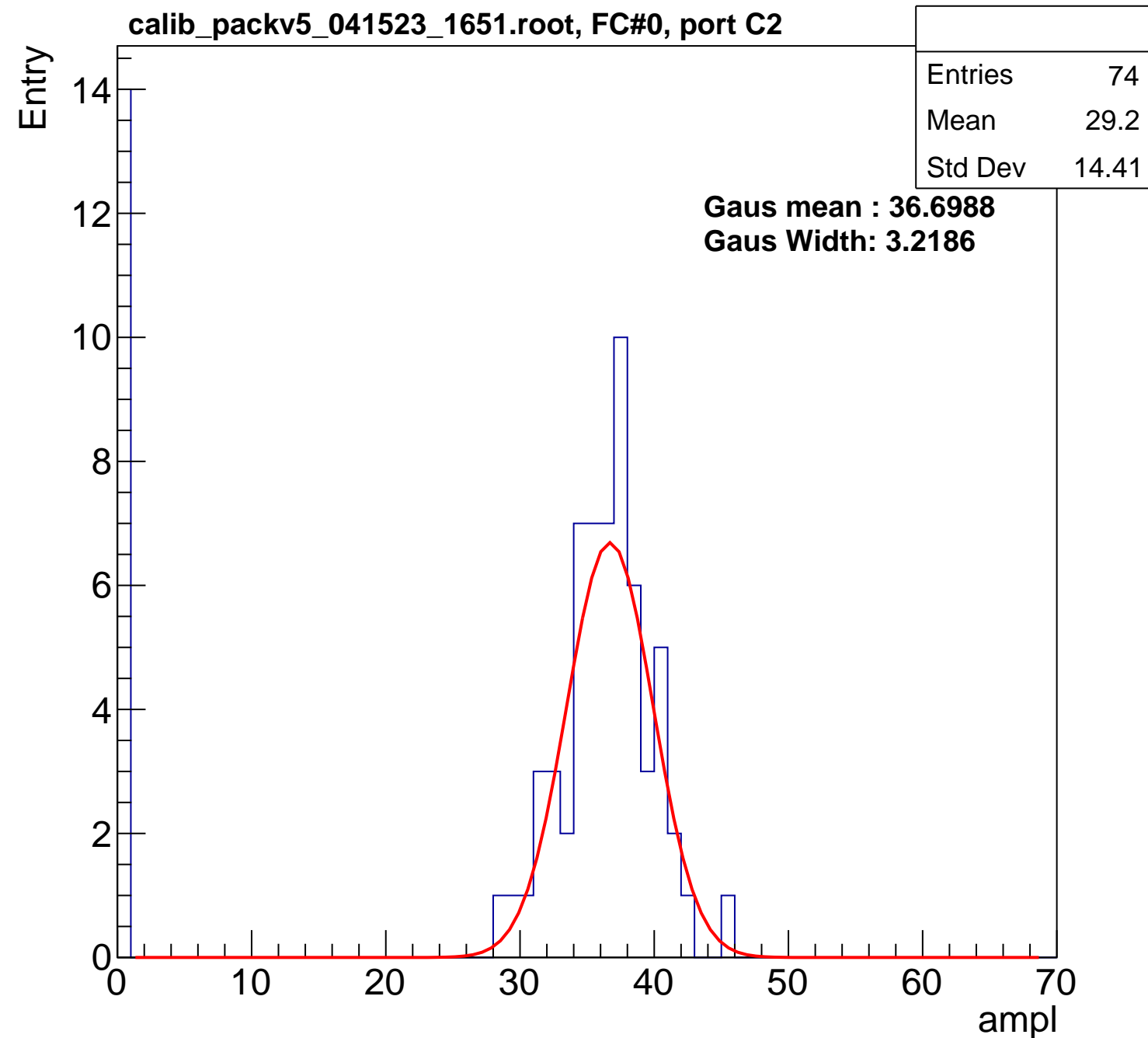
Gaus Width: 3.2186

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch75, adc2

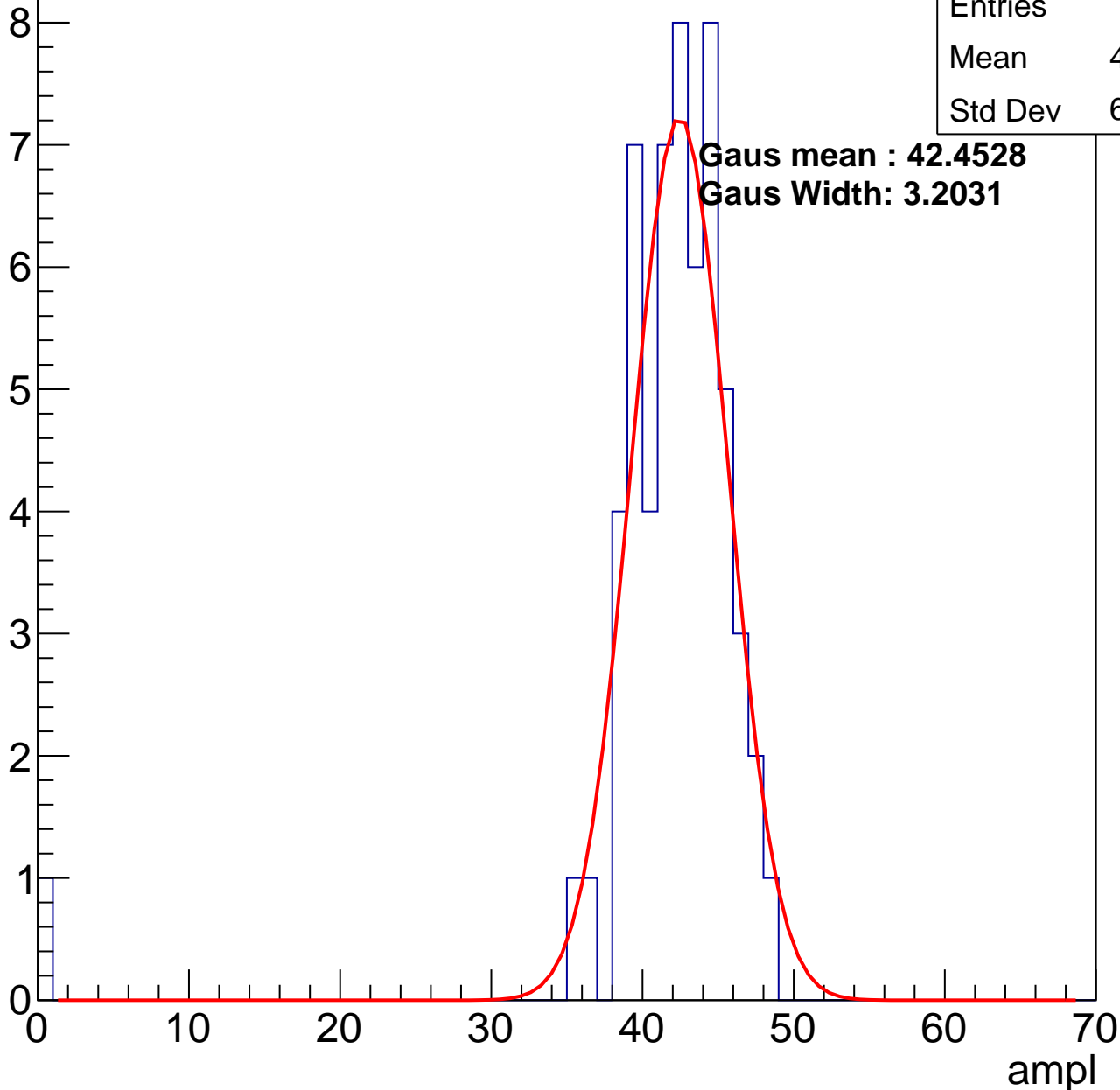
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	41.28
Std Dev	6.139

Gaus mean : 42.4528

Gaus Width: 3.2031

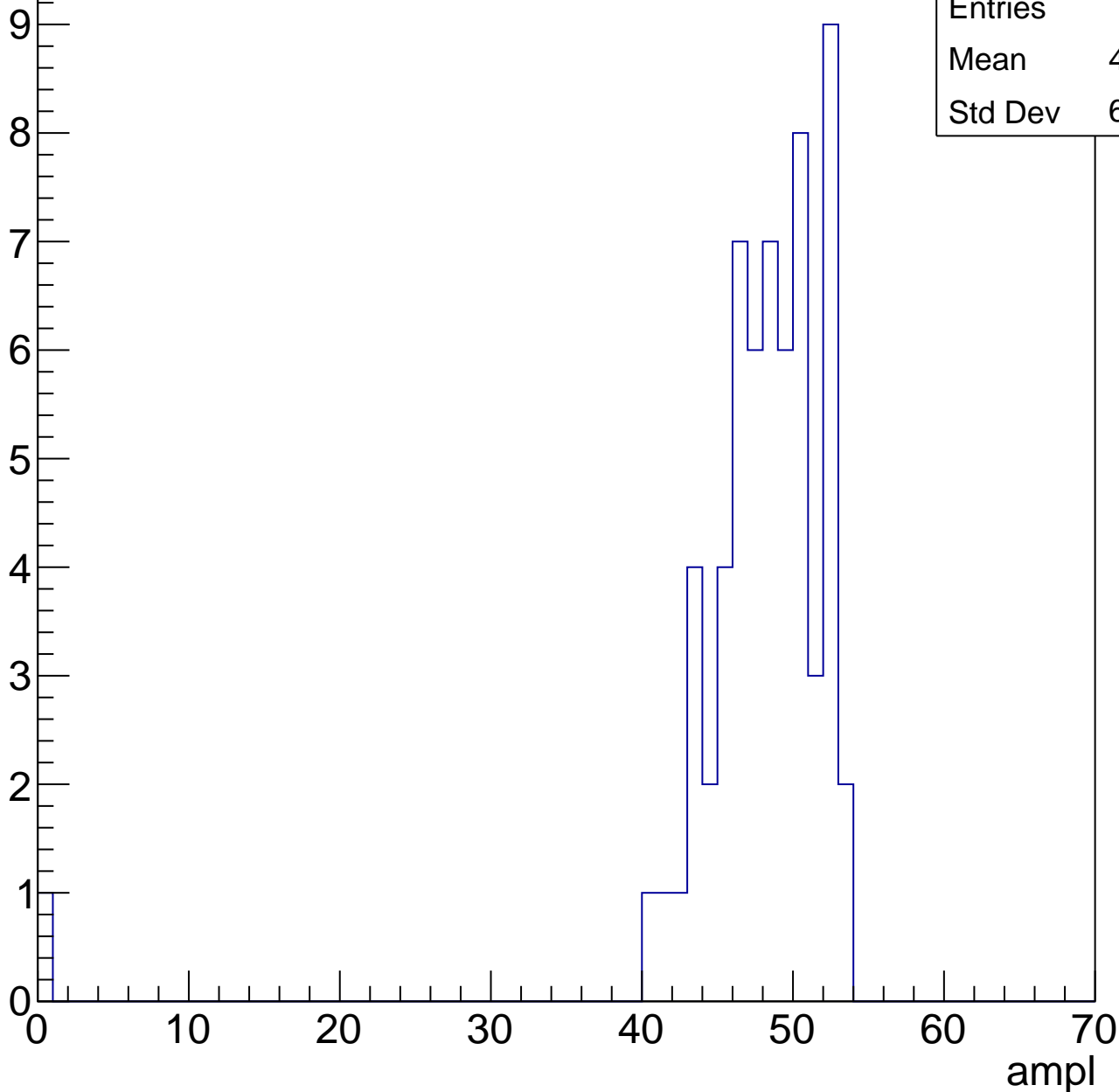


B1L103S, U2-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	47.16
Std Dev	6.804

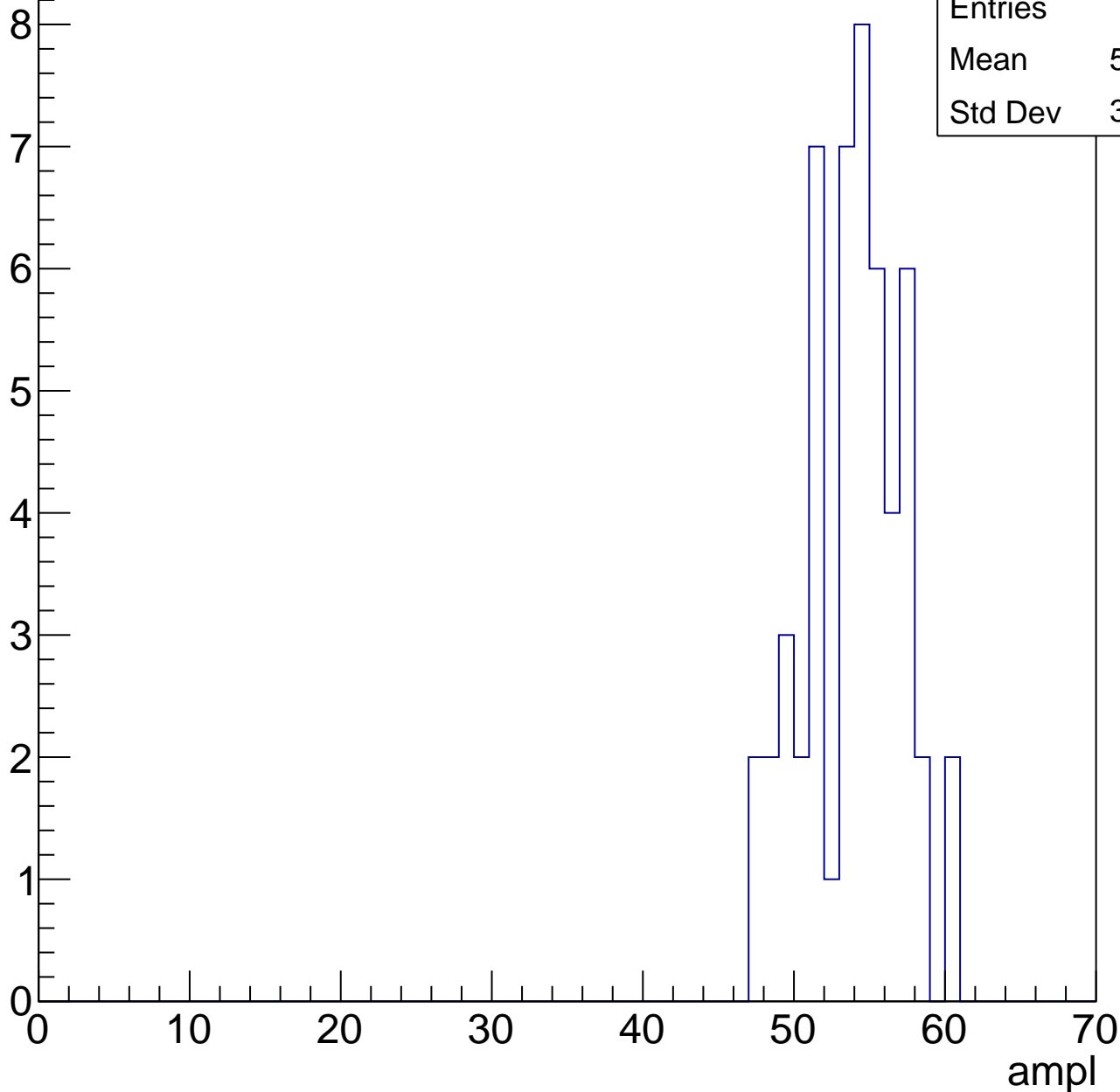


B1L103S, U2-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.48
Std Dev	3.153

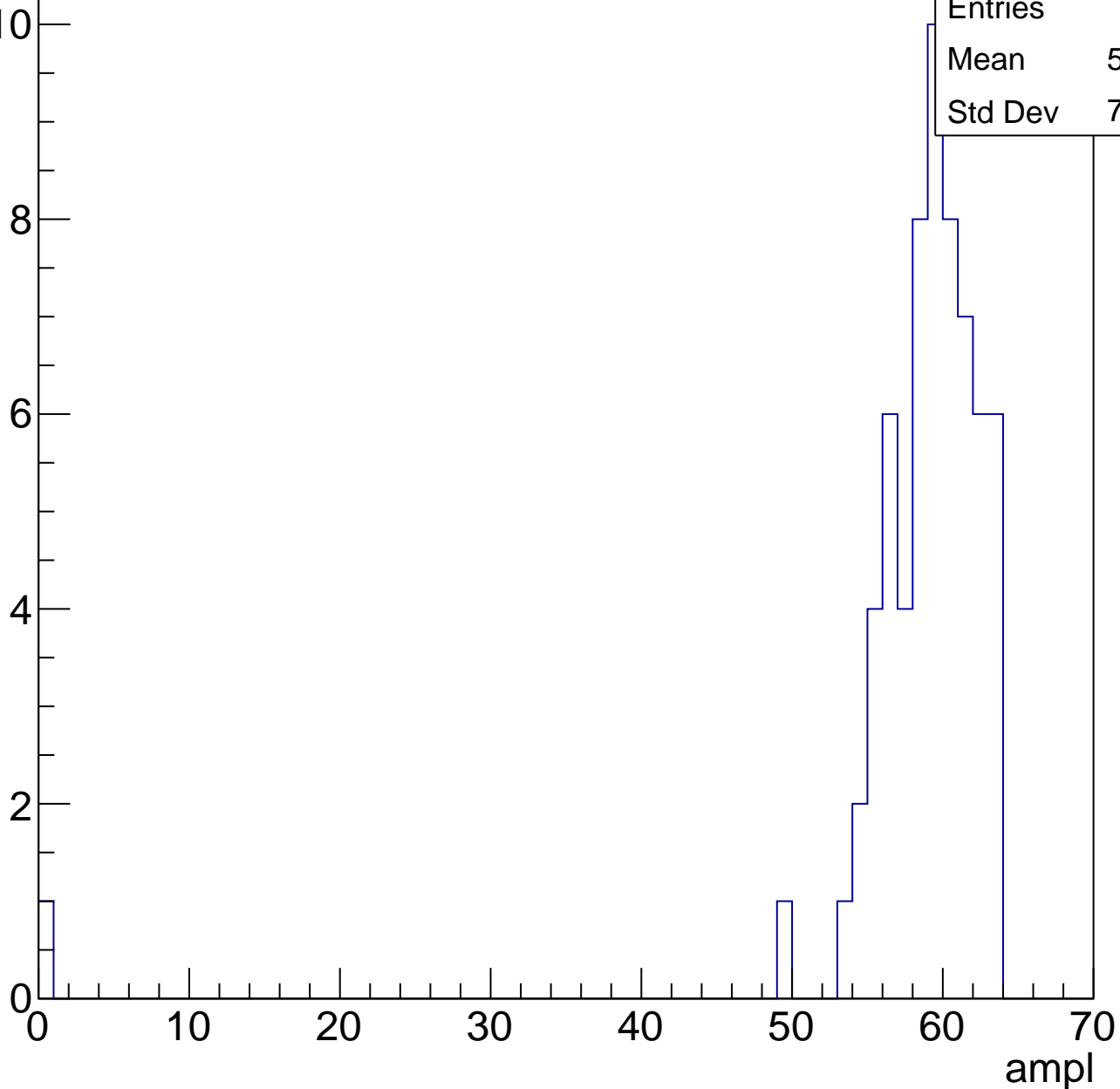


B1L103S, U2-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

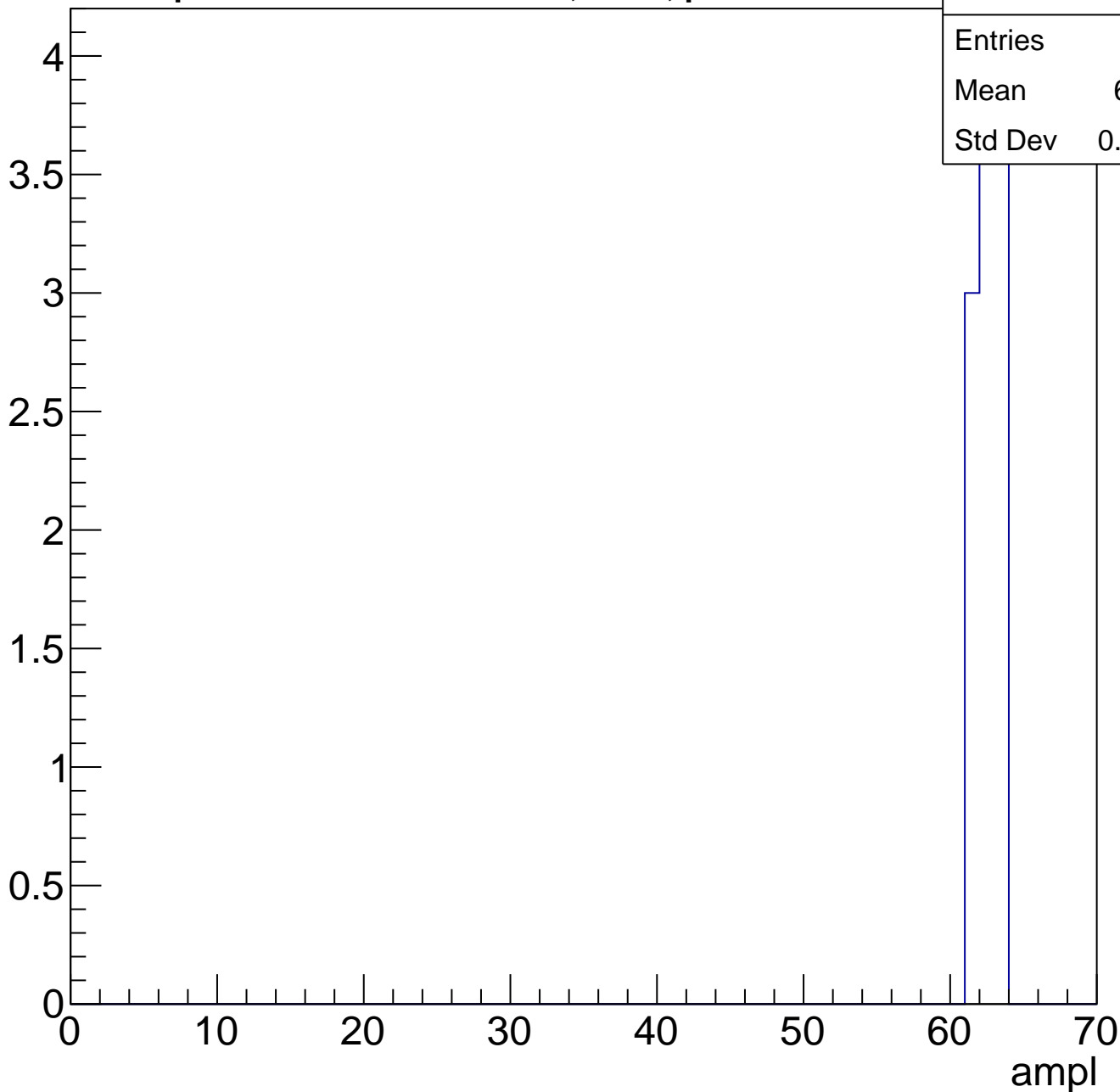
Entries	64
Mean	57.89
Std Dev	7.822



B1L103S, U2-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch76, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	21.09
Std Dev	11.85

Gaus mean : 28.2976

Gaus Width: 3.8029

Entry

25

20

15

10

5

0

0

10

20

30

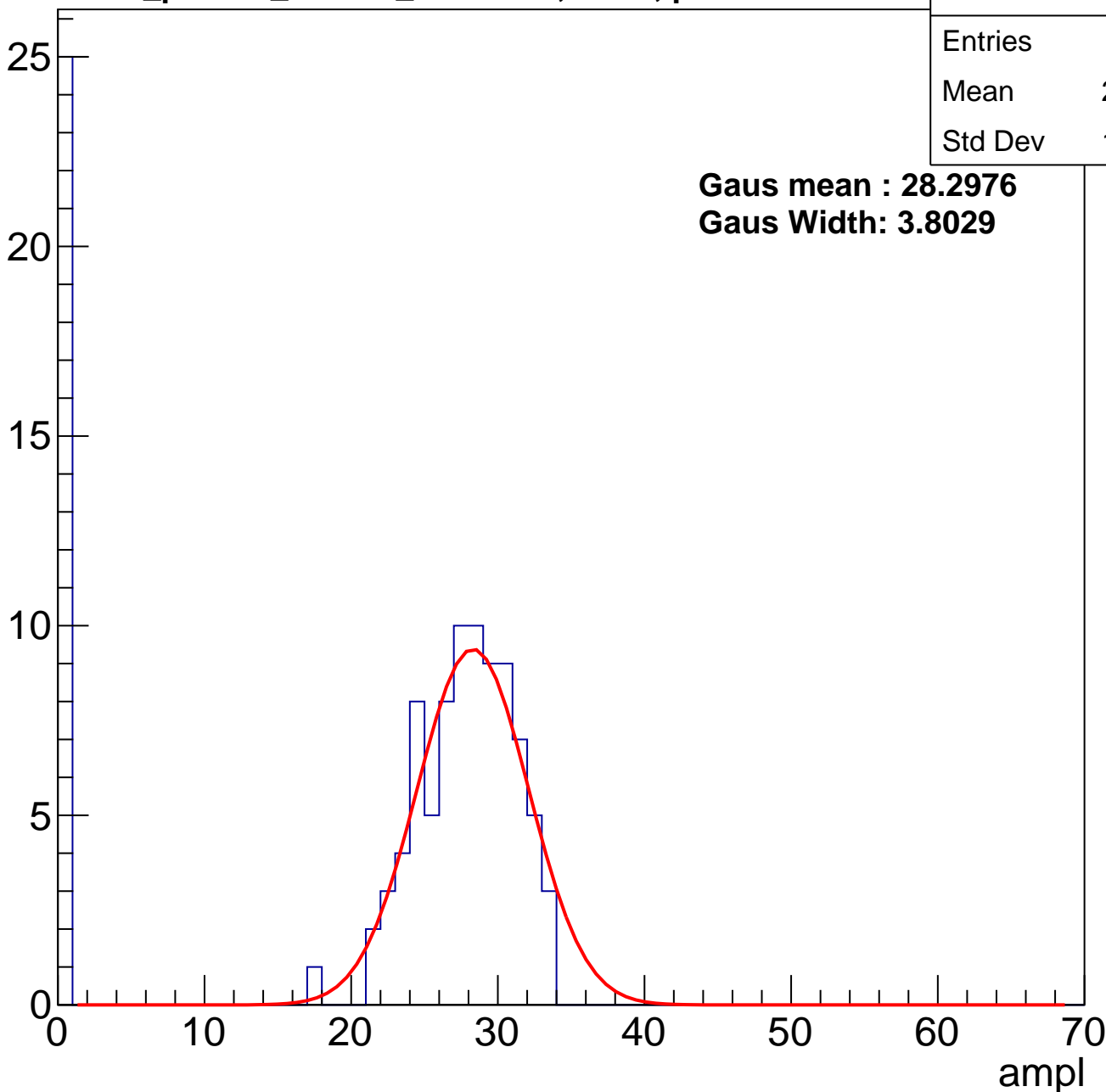
40

50

60

70

ampl



B1L103S, U2-ch76, adc1

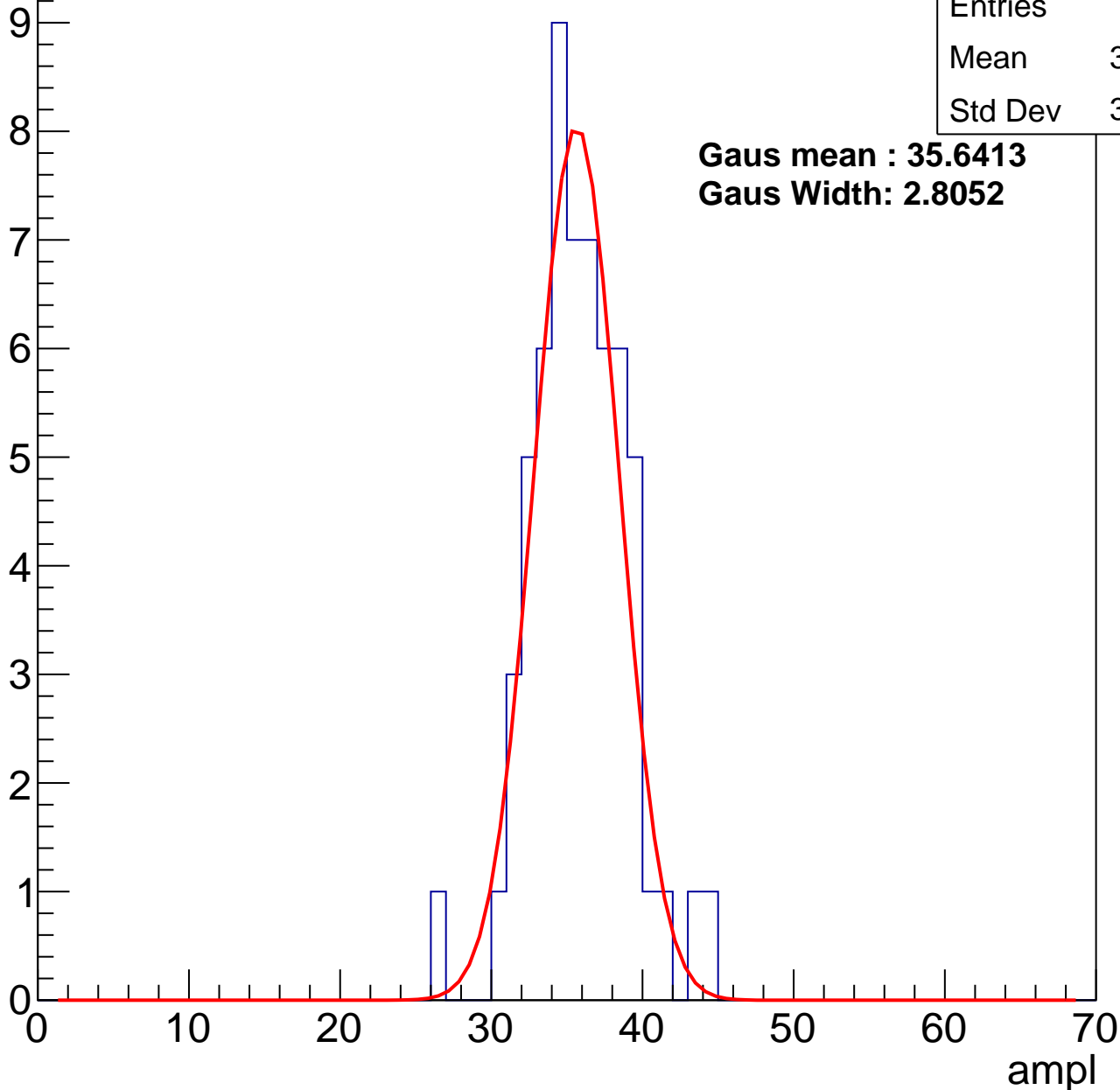
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	35.38
Std Dev	3.147

Gaus mean : 35.6413

Gaus Width: 2.8052



B1L103S, U2-ch76, adc2

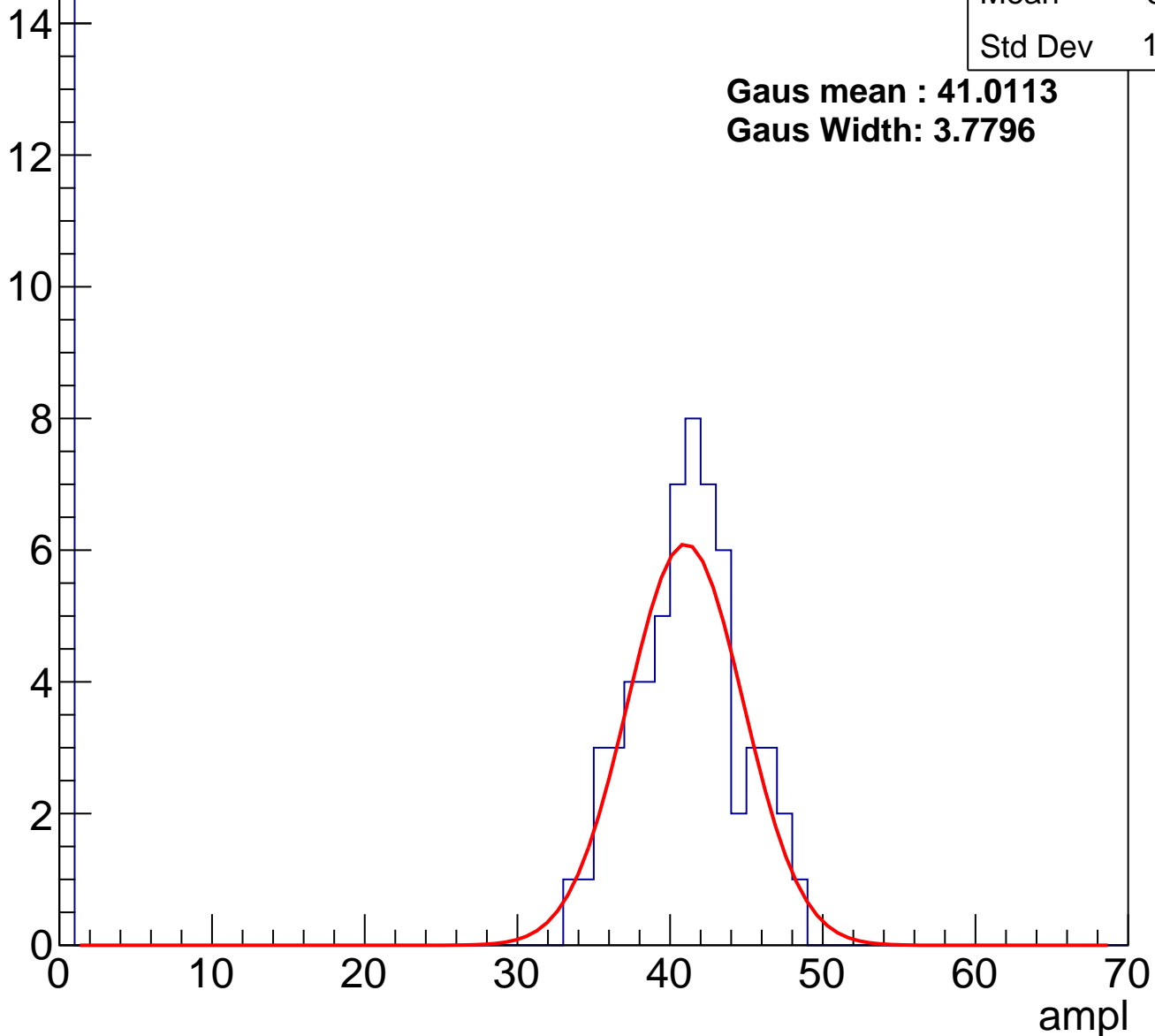
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	32.51
Std Dev	16.54

Gaus mean : 41.0113

Gaus Width: 3.7796

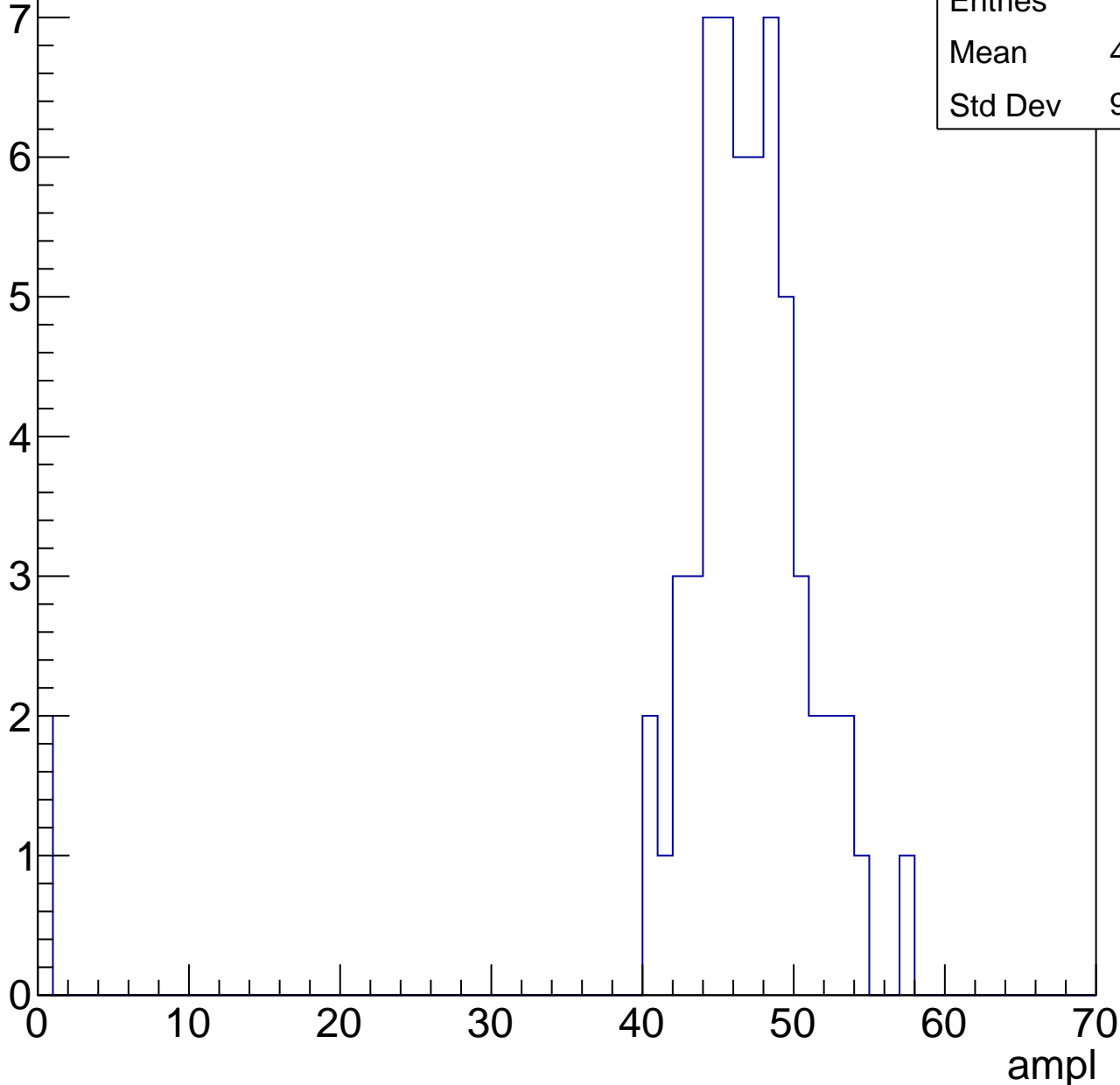


B1L103S, U2-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	45.18
Std Dev	9.065

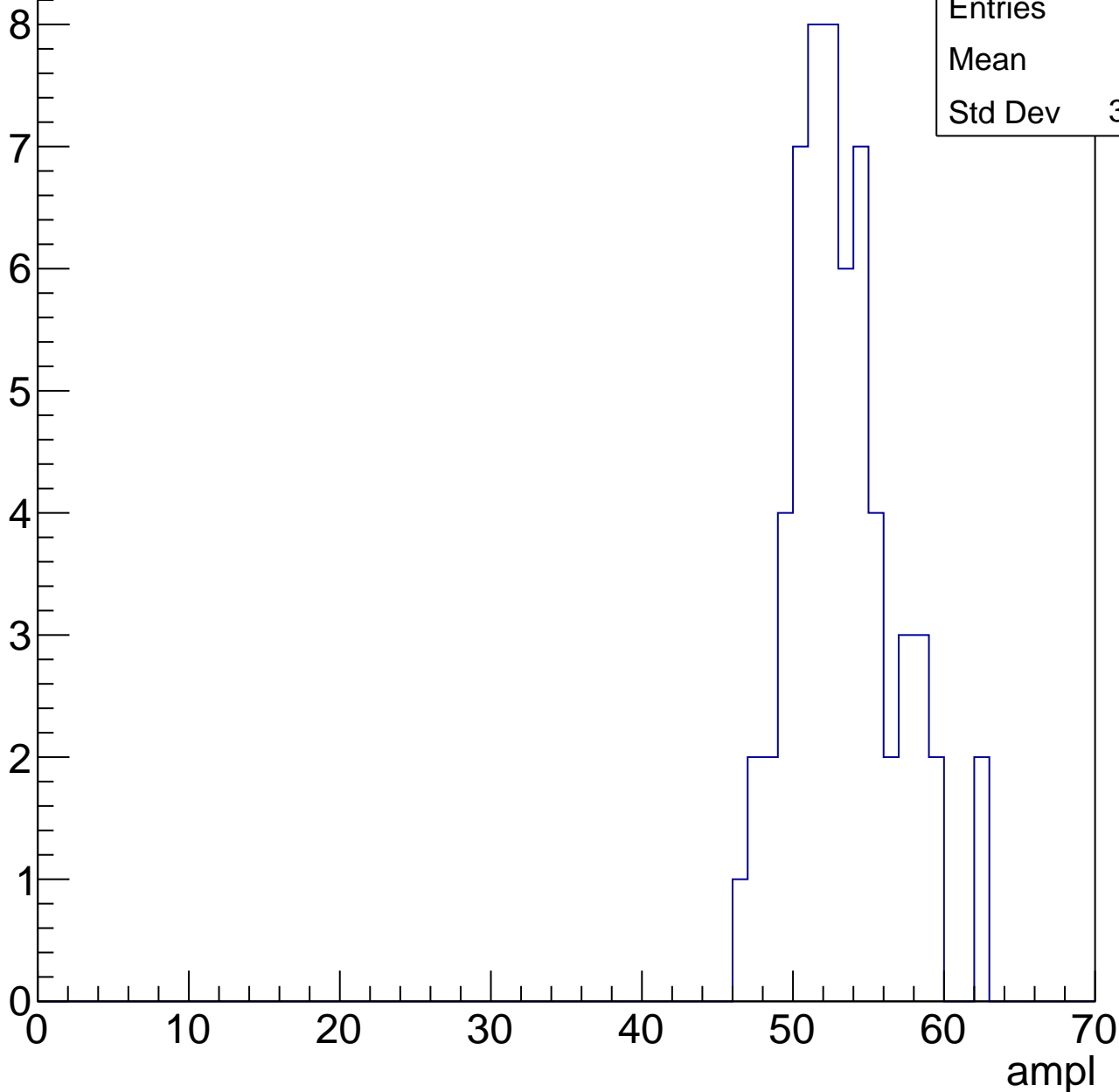


B1L103S, U2-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	52.8
Std Dev	3.468



B1L103S, U2-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

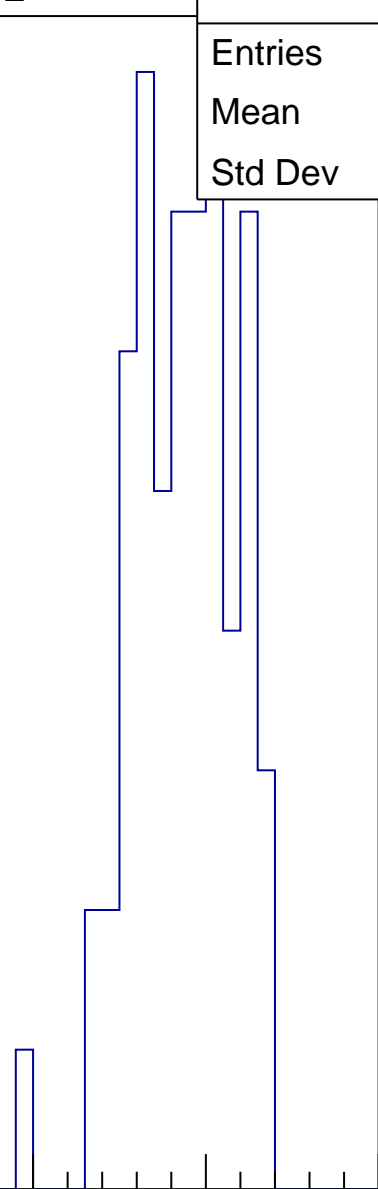
Entry

8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.2
Std Dev	2.926

ampl

0 10 20 30 40 50 60 70

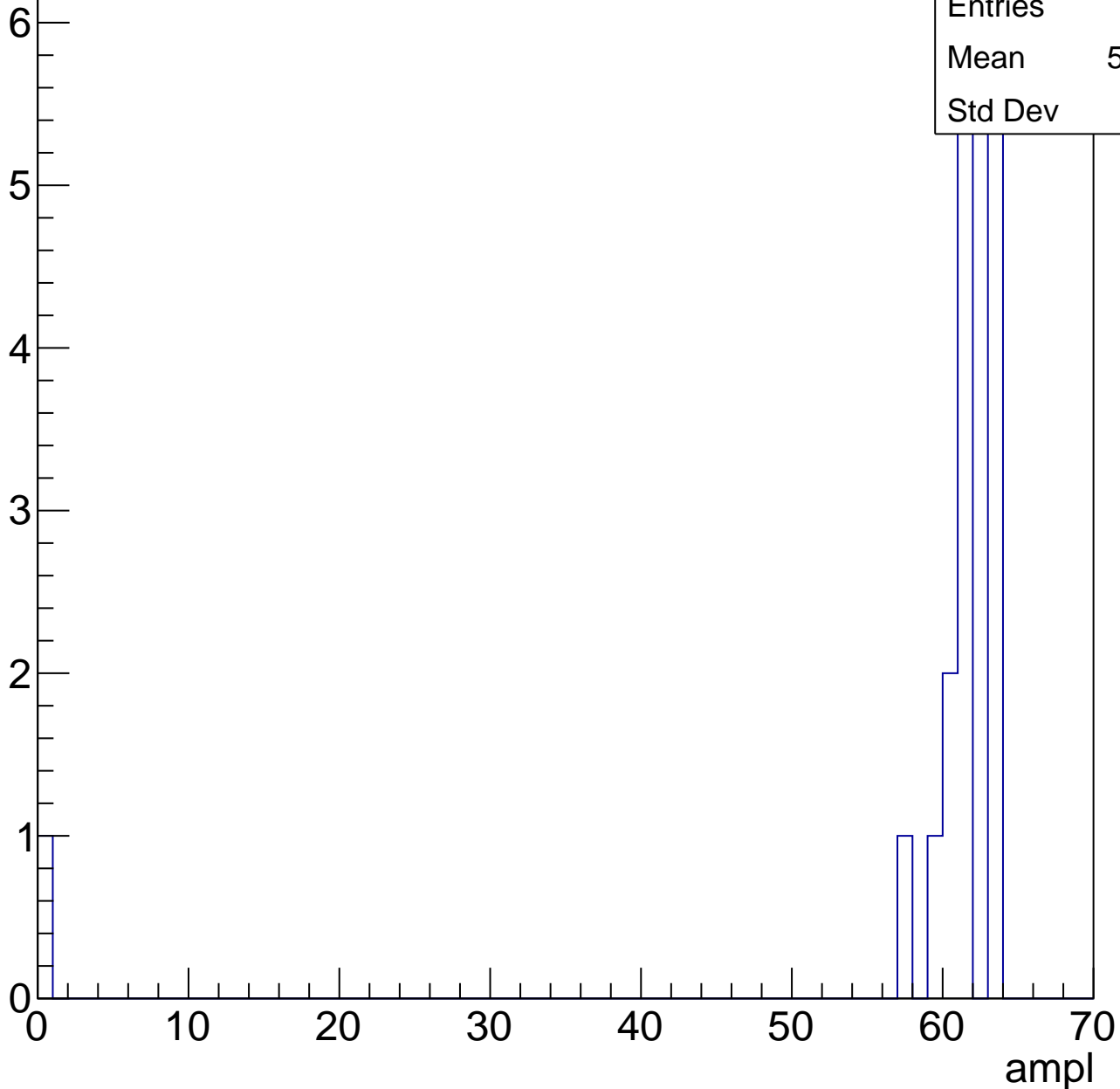


B1L103S, U2-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.65
Std Dev	14.5



B1L103S, U2-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	21
Mean	5.952
Std Dev	18.35

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch77, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	20.6
Std Dev	13.49

Gaus mean : 29.9498

Gaus Width: 4.1907

Entry

25

20

15

10

5

0

0

10

20

30

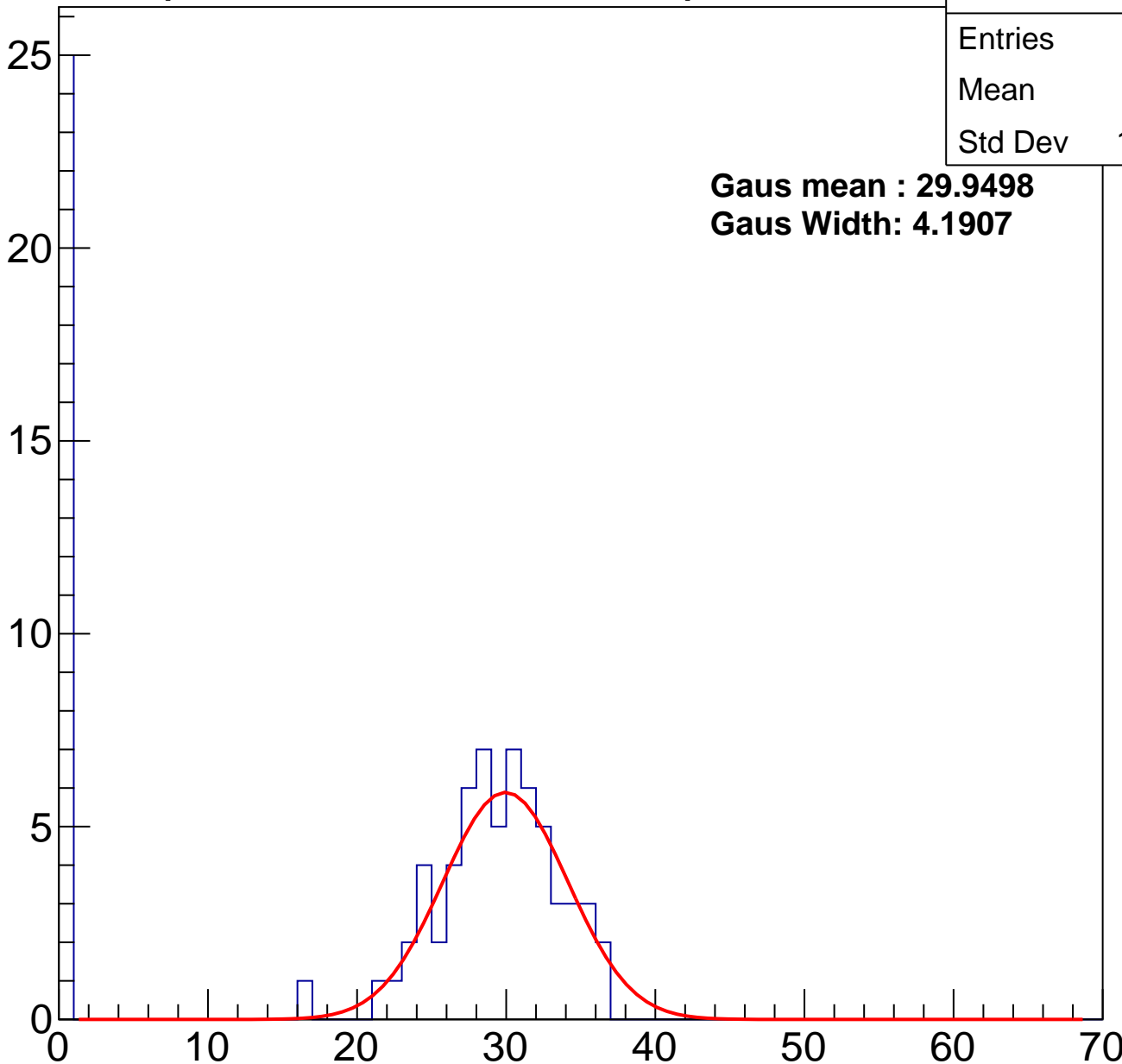
40

50

60

70

ampl



B1L103S, U2-ch77, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	30.08
Std Dev	13.09

Gaus mean : 36.0636

Gaus Width: 3.6671

Entry

12

10

8

6

4

2

0

0

10

20

30

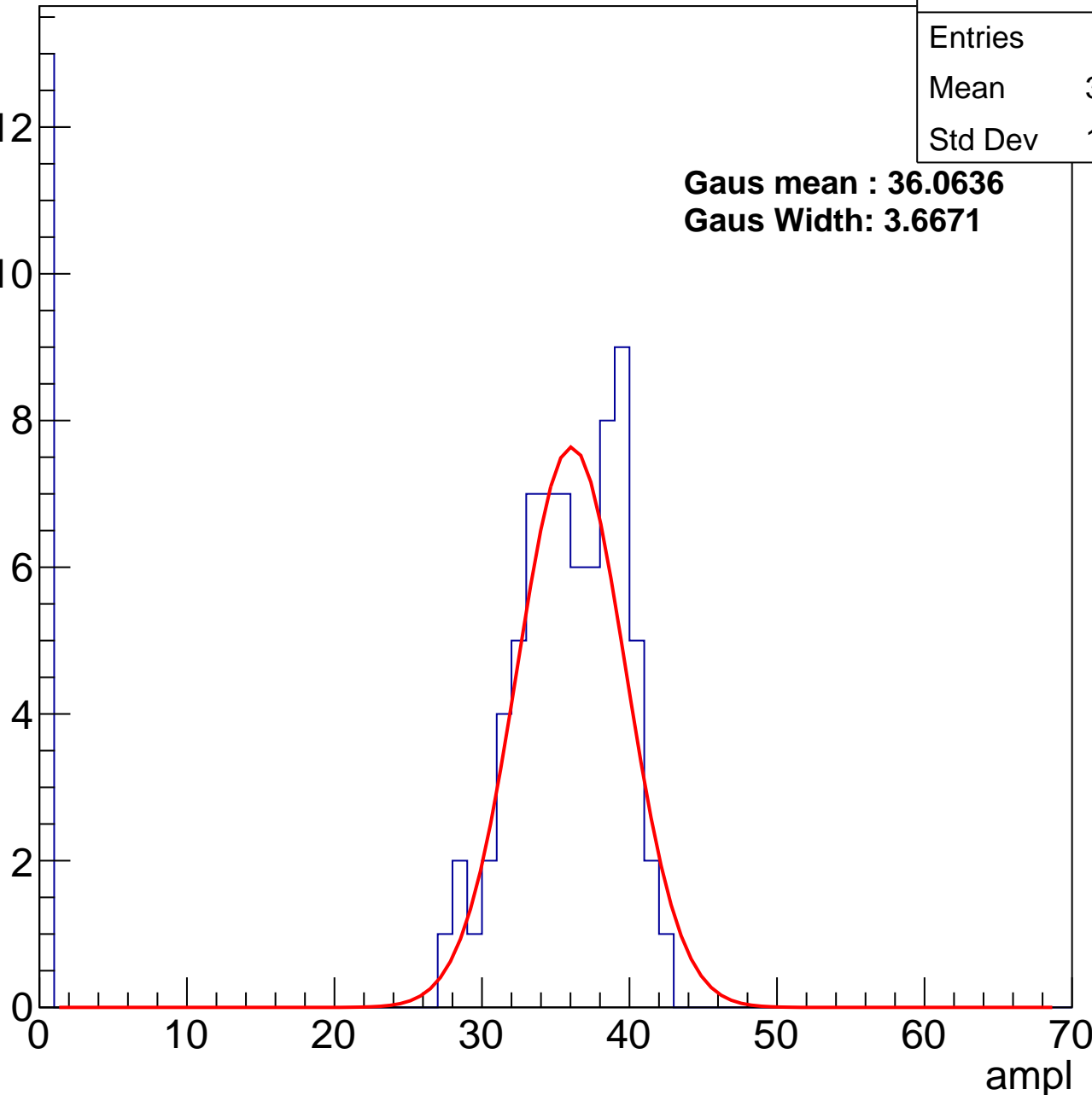
40

50

60

70

ampl



B1L103S, U2-ch77, adc2

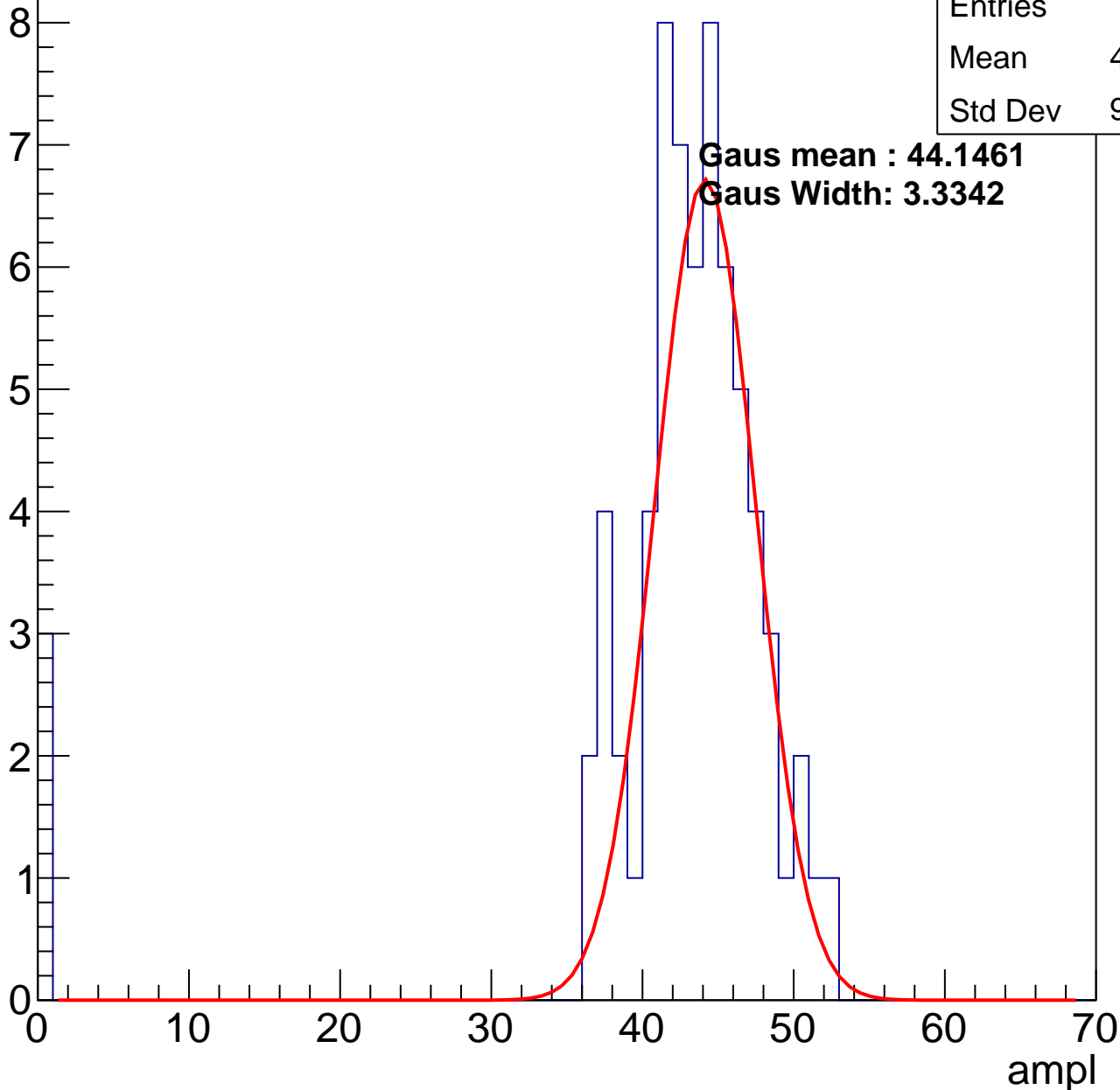
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.34
Std Dev	9.577

Gaus mean : 44.1461

Gaus Width: 3.3342

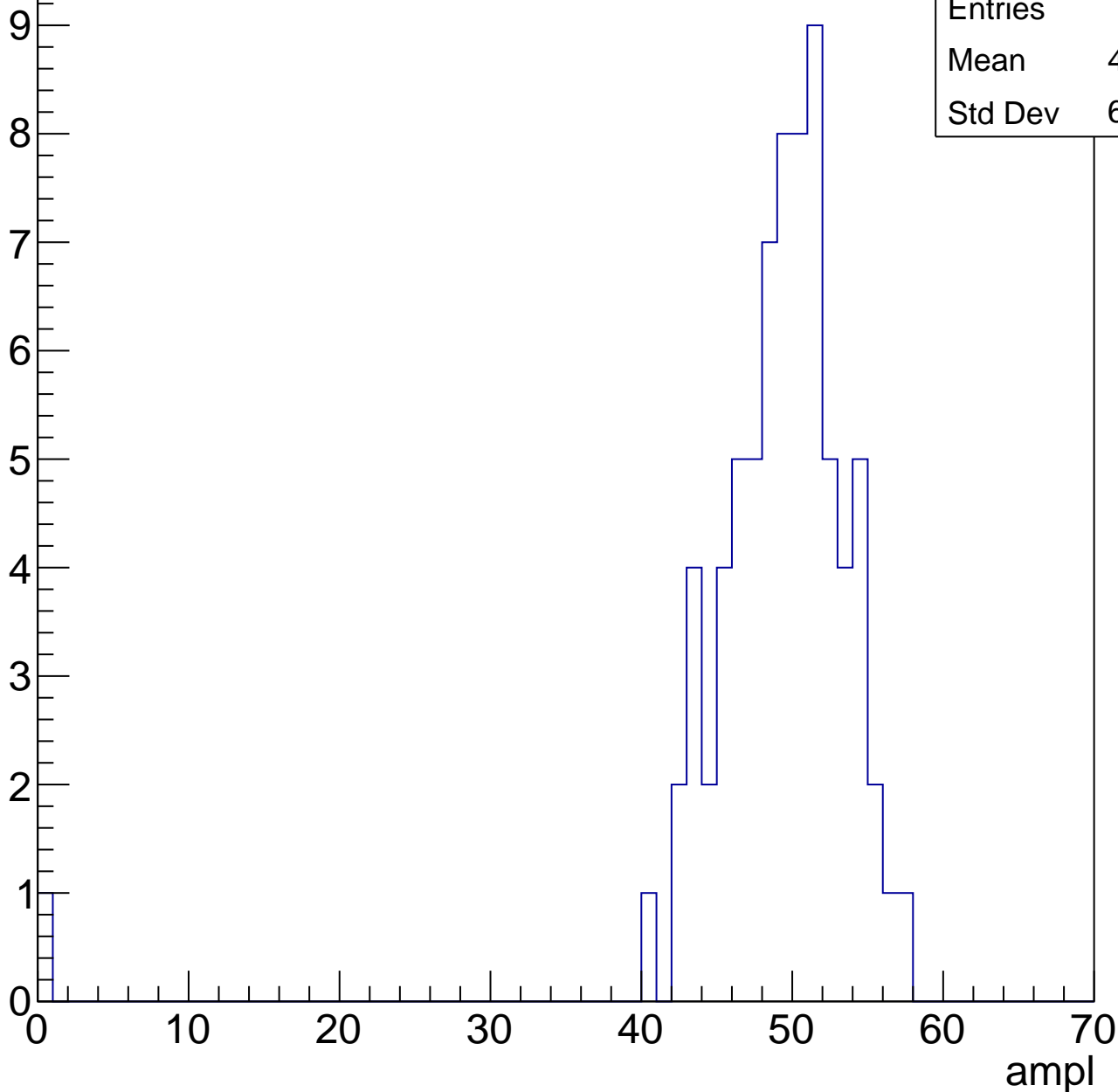


B1L103S, U2-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	48.39
Std Dev	6.724

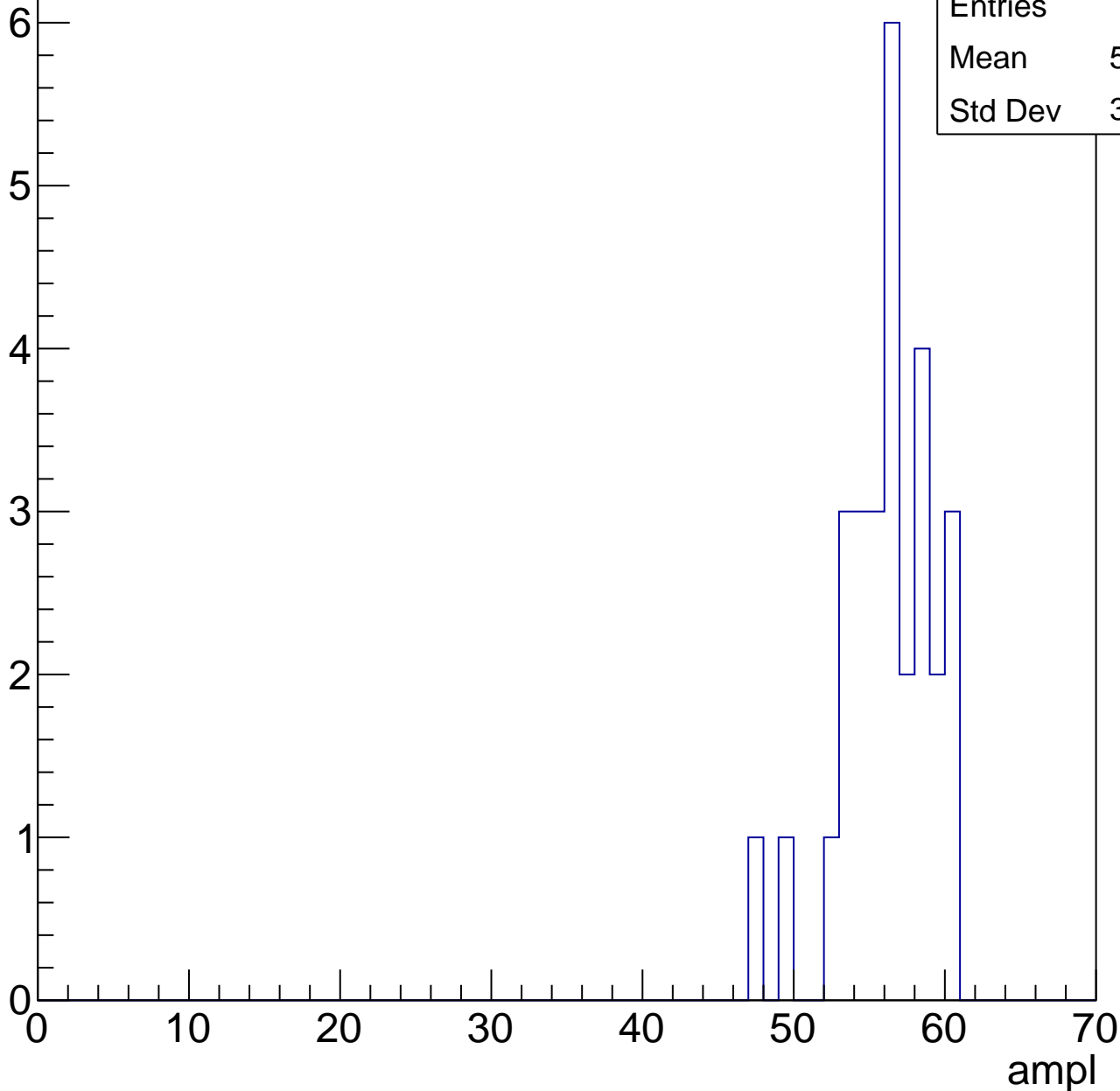


B1L103S, U2-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	55.66
Std Dev	3.043

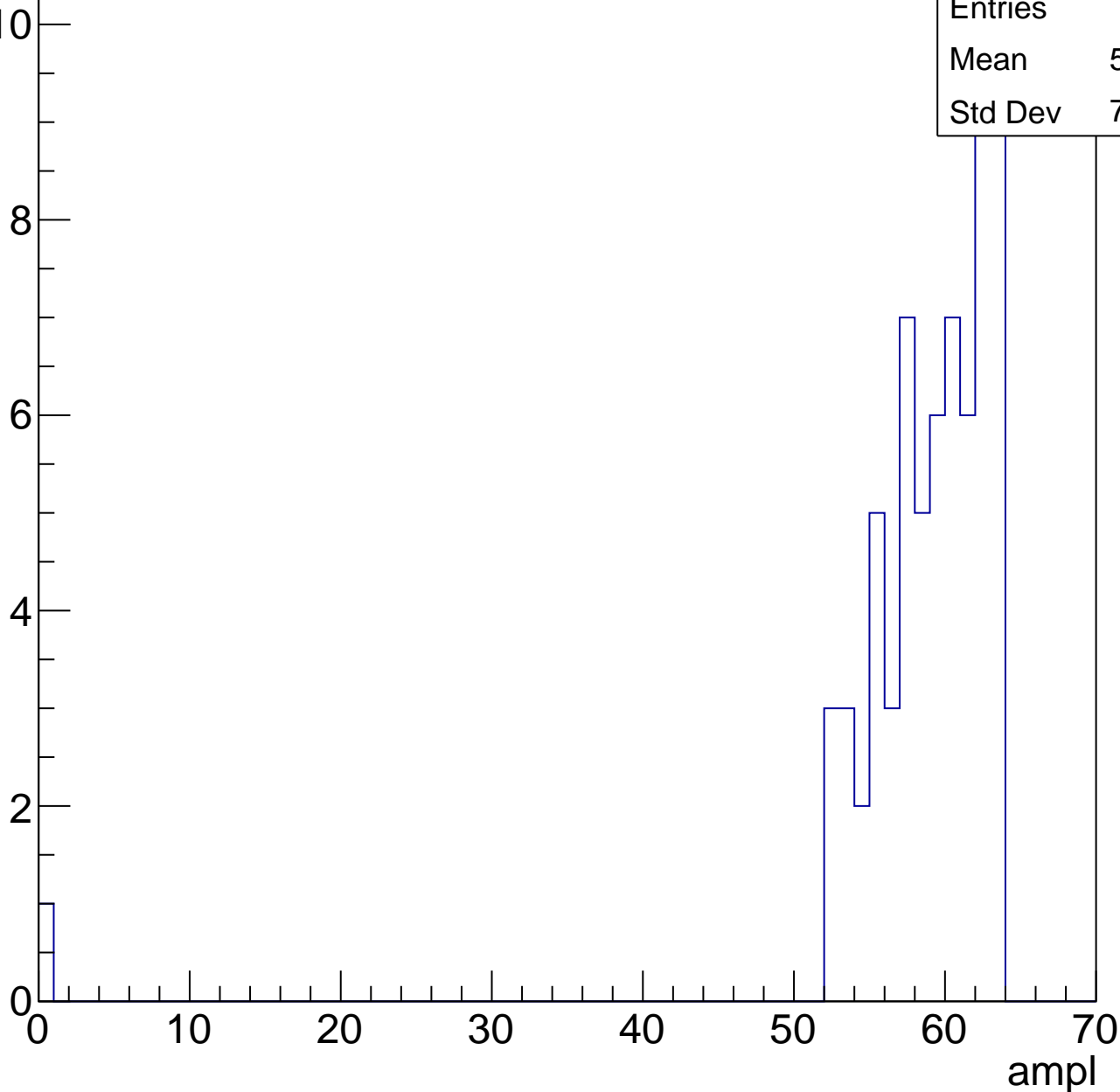


B1L103S, U2-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

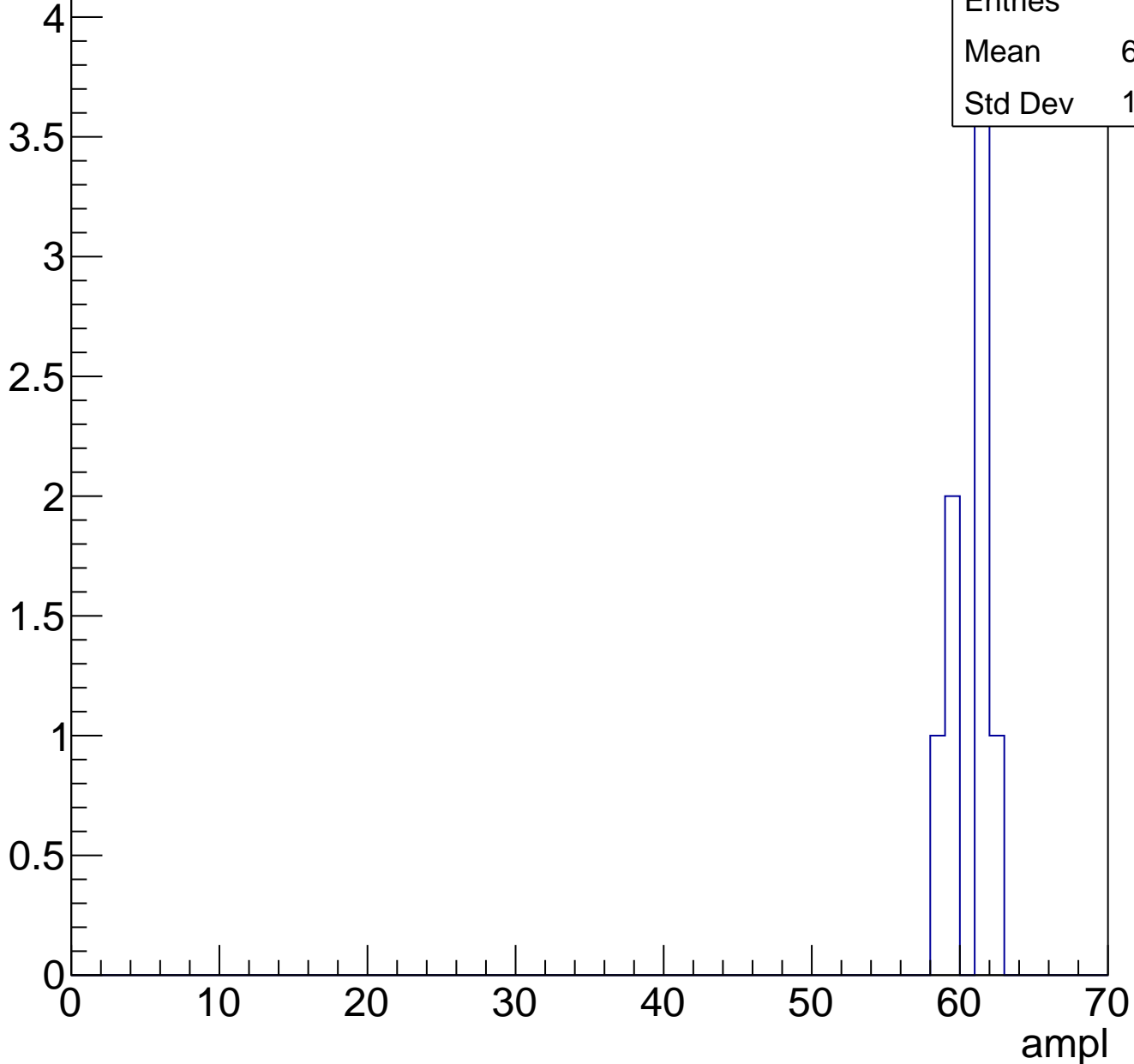
Entries	67
Mean	57.96
Std Dev	7.846



B1L103S, U2-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U2-ch78, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	17
Std Dev	12.52

Gaus mean : 25.9936

Gaus Width: 4.1947

Entry

30

25

20

15

10

5

0

0

10

20

30

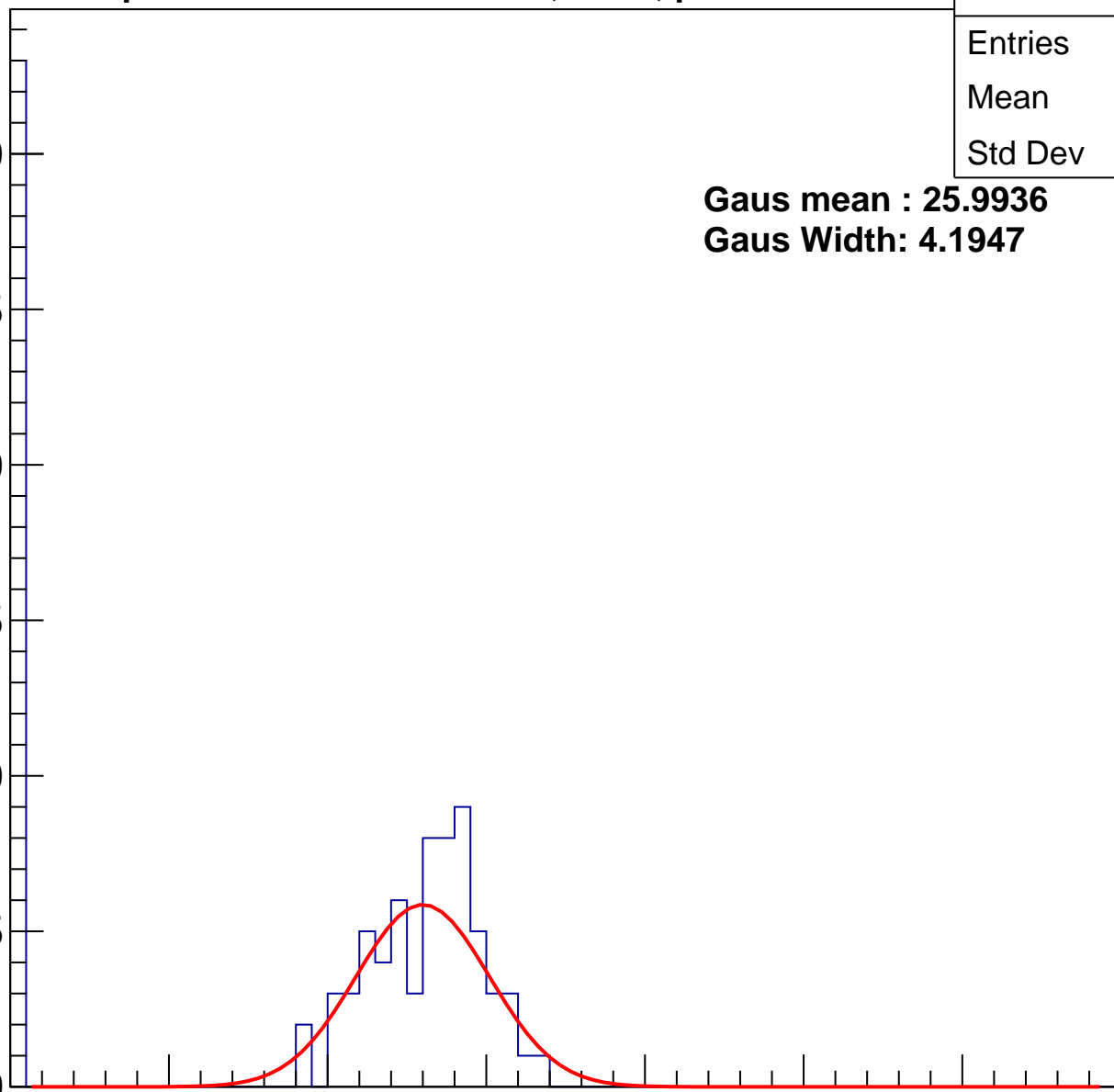
40

50

60

70

ampl



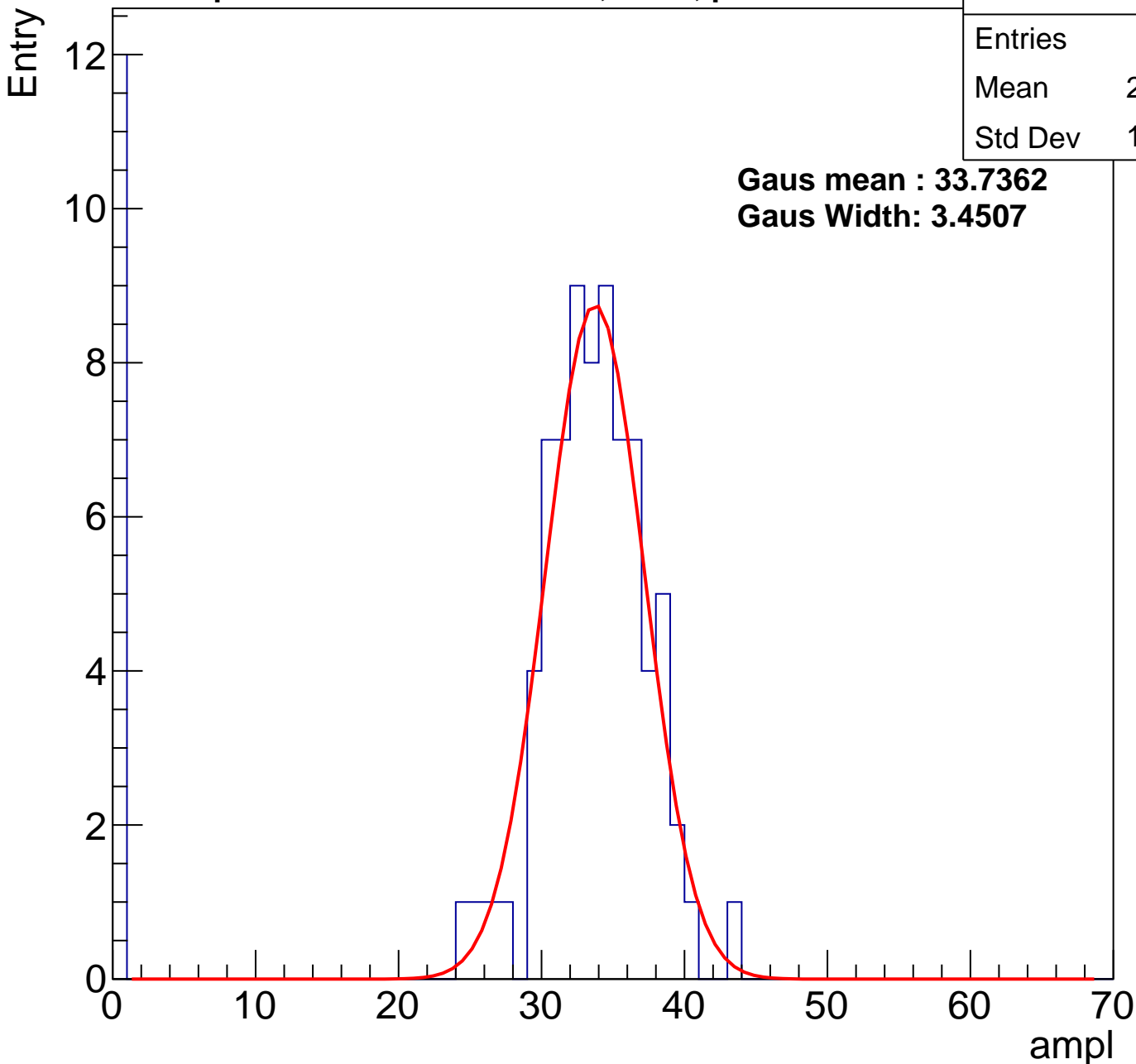
B1L103S, U2-ch78, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	28.72
Std Dev	11.93

Gaus mean : 33.7362

Gaus Width: 3.4507



B1L103S, U2-ch78, adc2

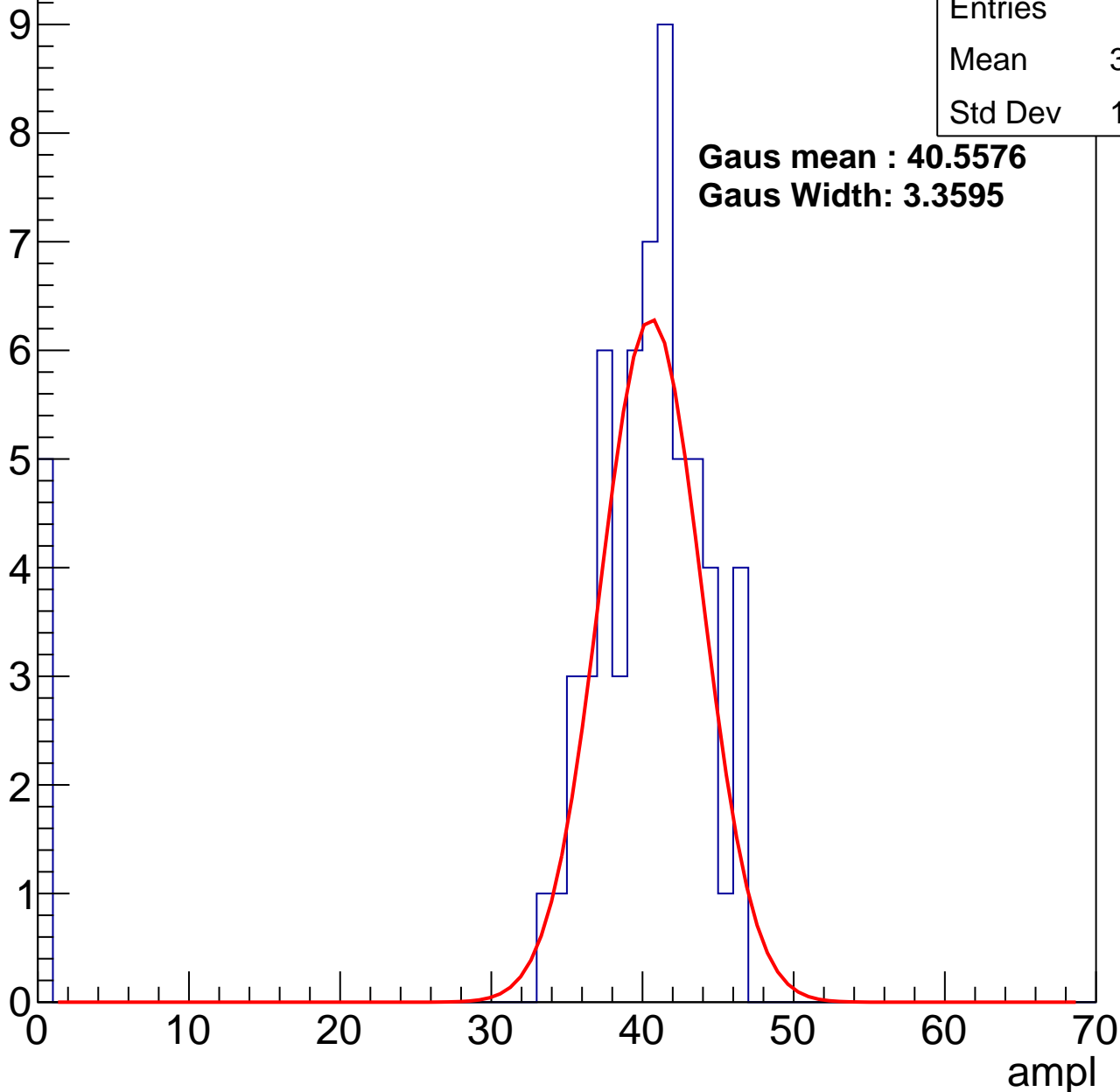
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.97
Std Dev	11.27

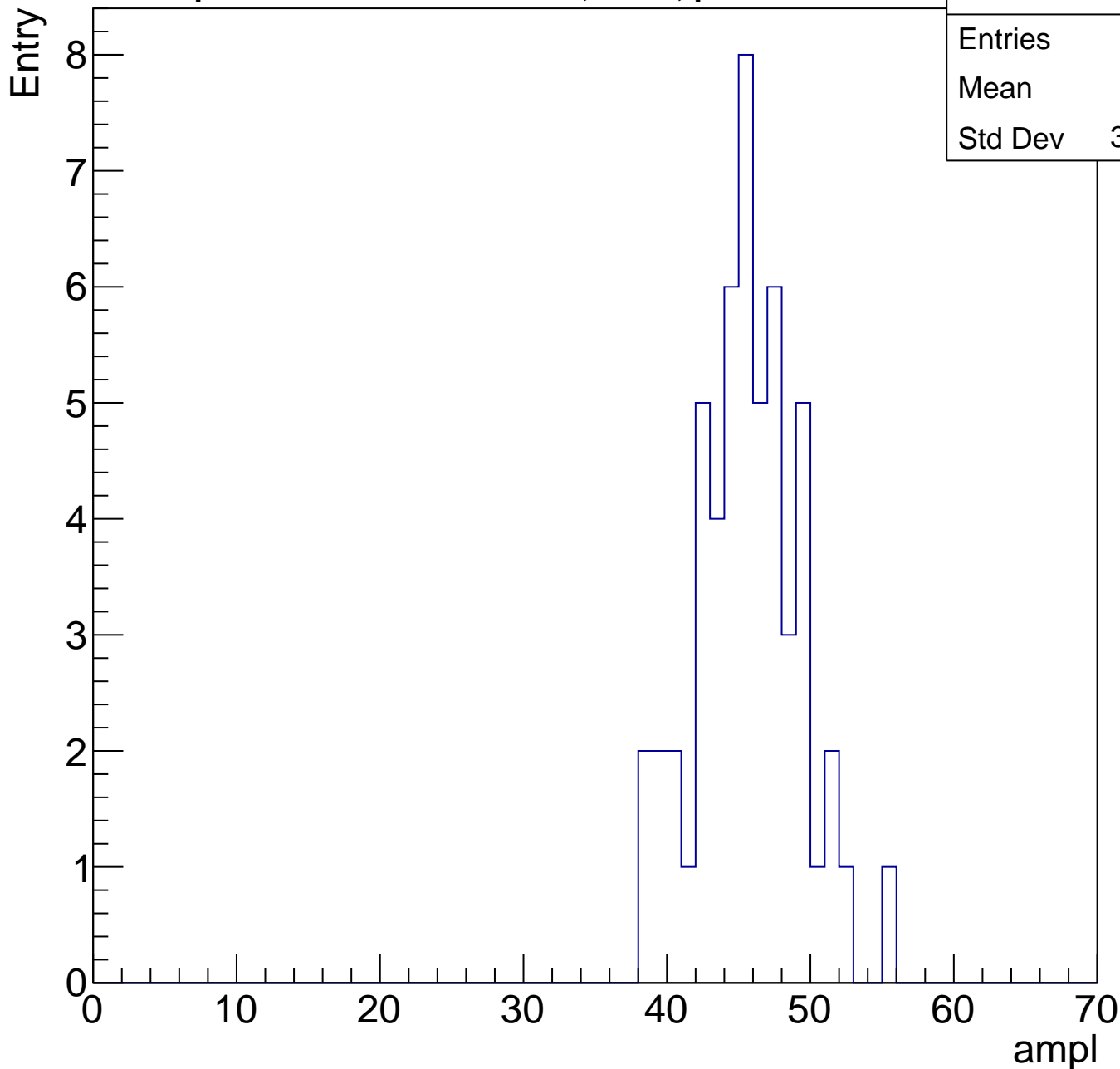
Gaus mean : 40.5576

Gaus Width: 3.3595



B1L103S, U2-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

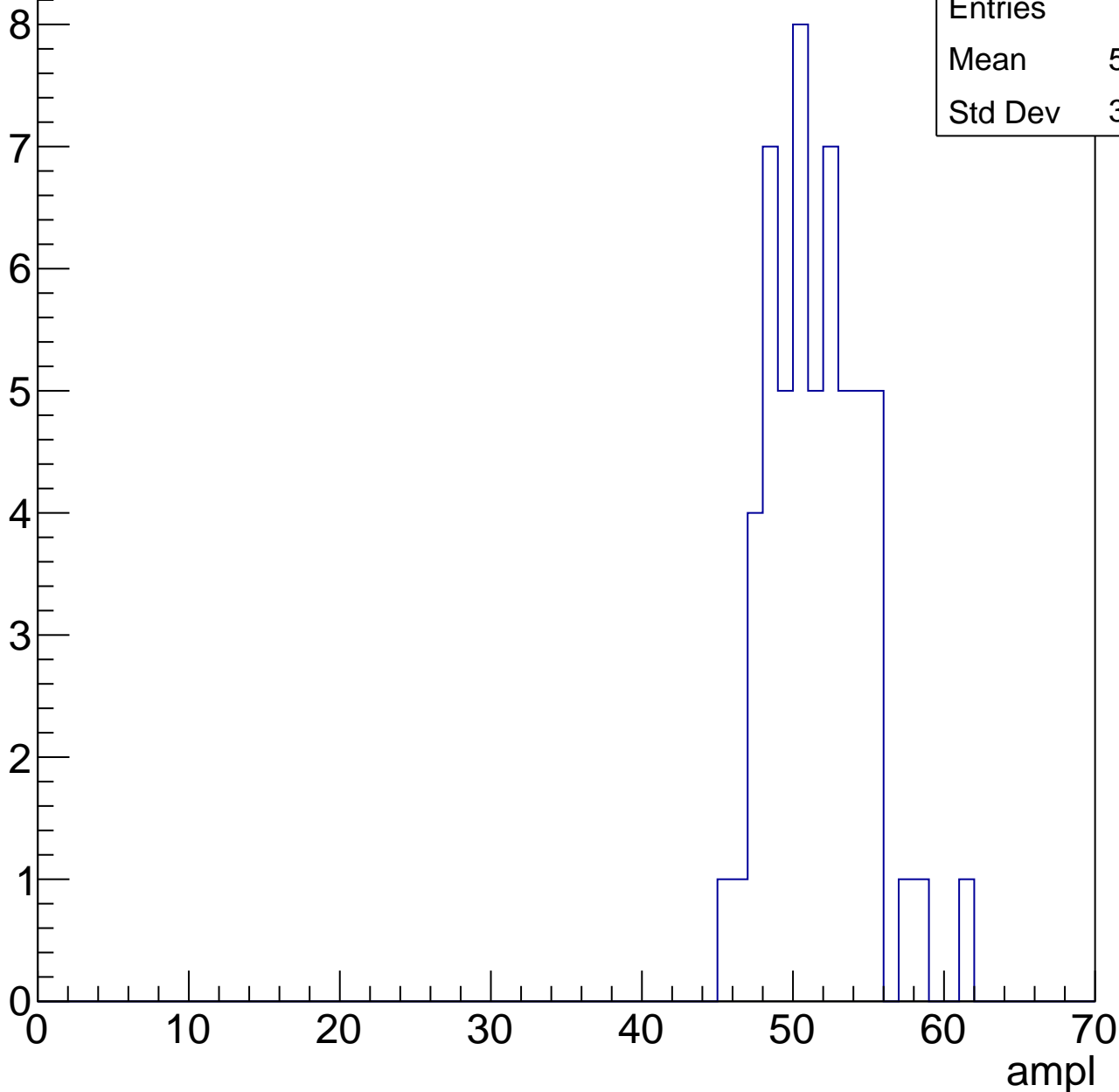


B1L103S, U2-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	51.16
Std Dev	3.138

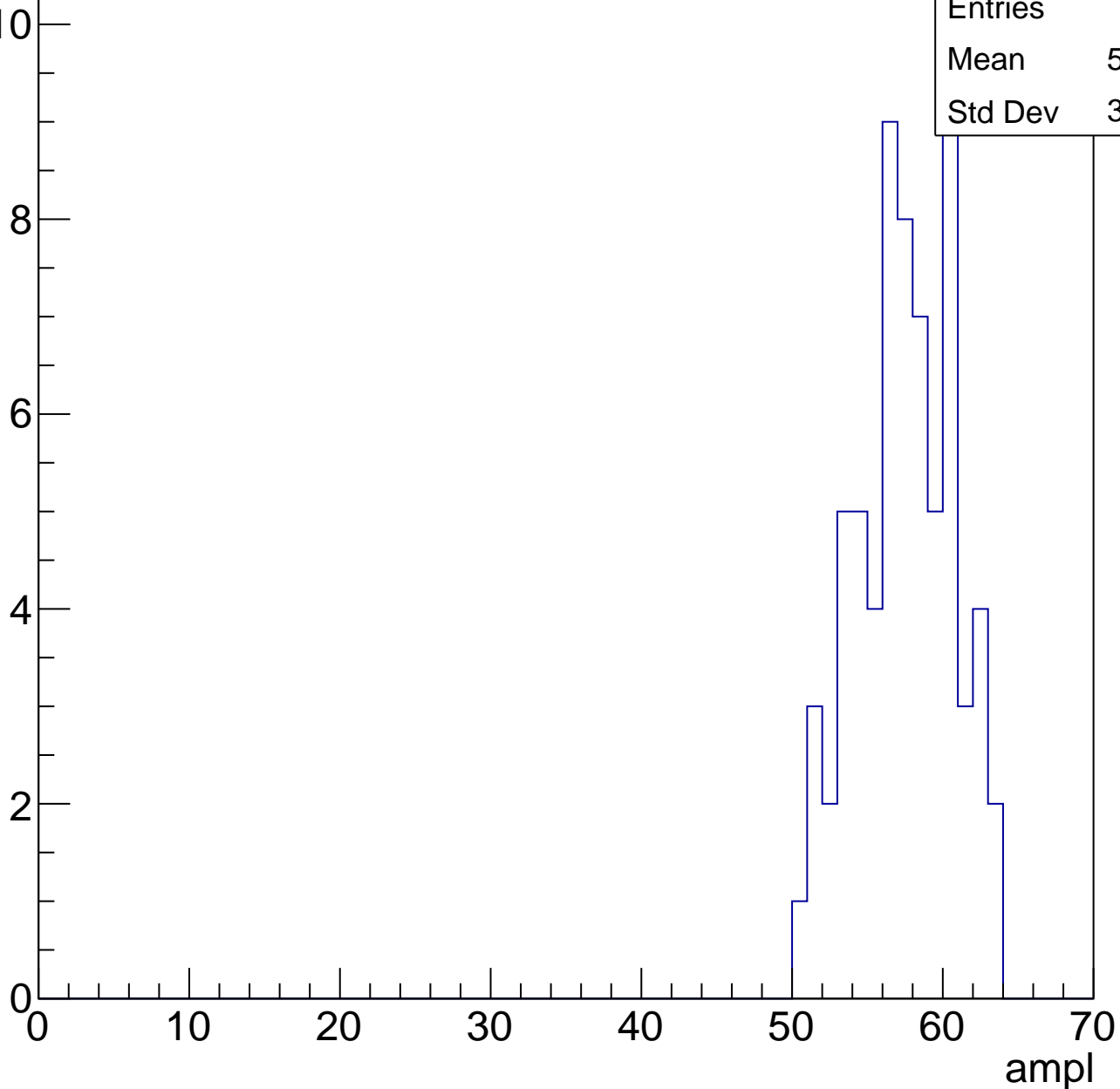


B1L103S, U2-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	57.06
Std Dev	3.194

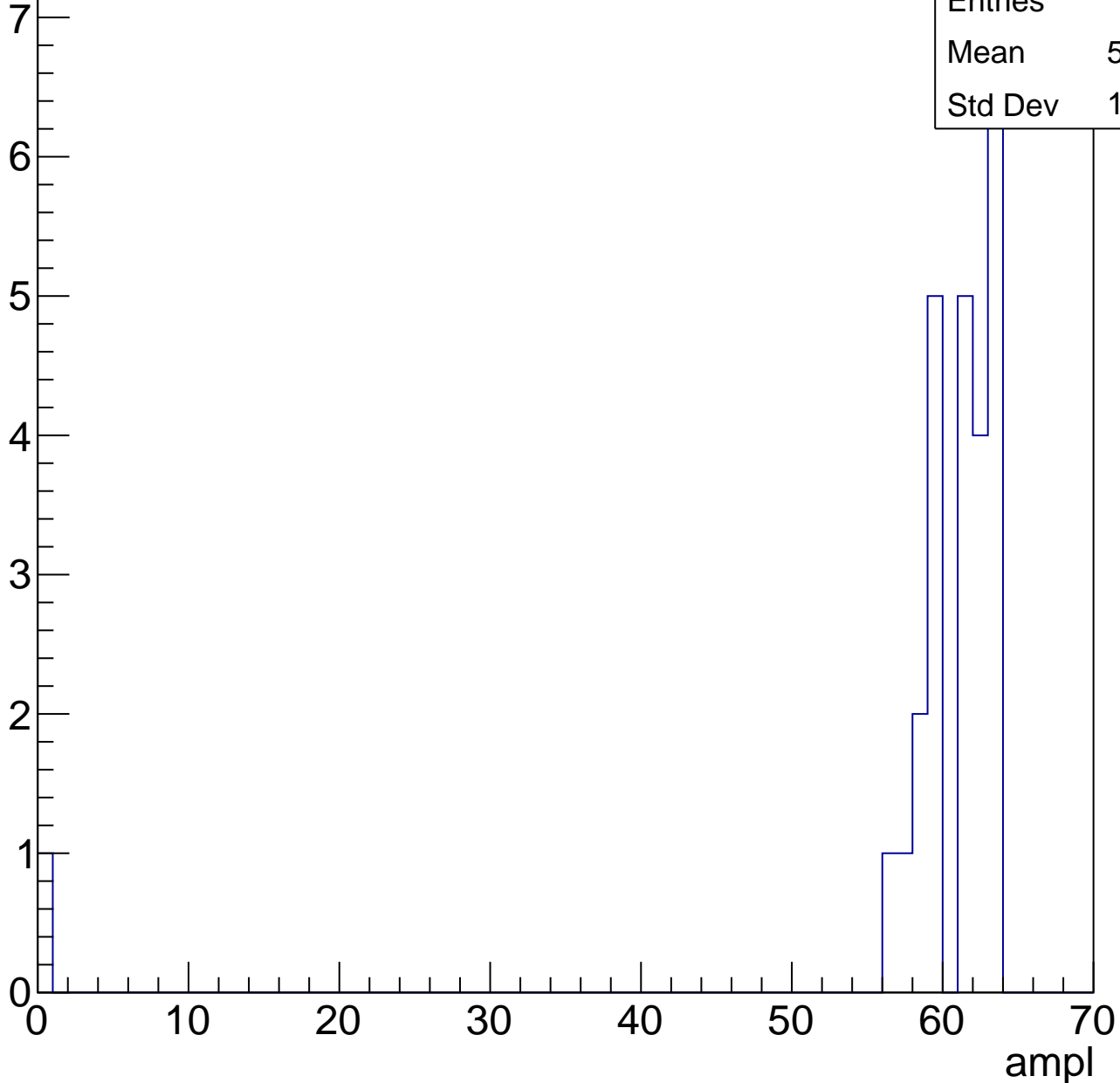


B1L103S, U2-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.38
Std Dev	11.86

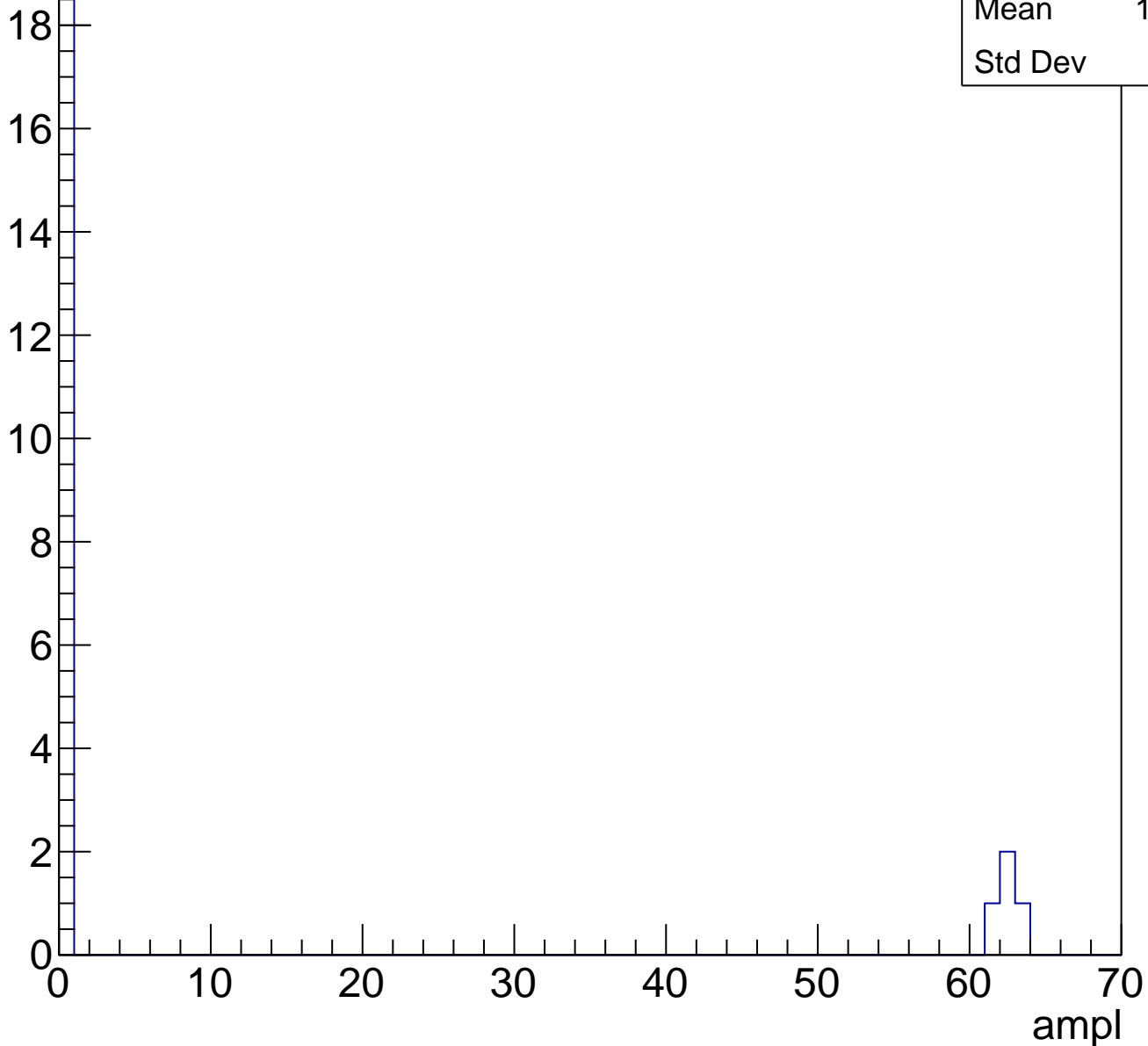


B1L103S, U2-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.78
Std Dev	23.5

Entry



B1L103S, U2-ch79, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	22.45
Std Dev	12.47

Gaus mean : 28.7532

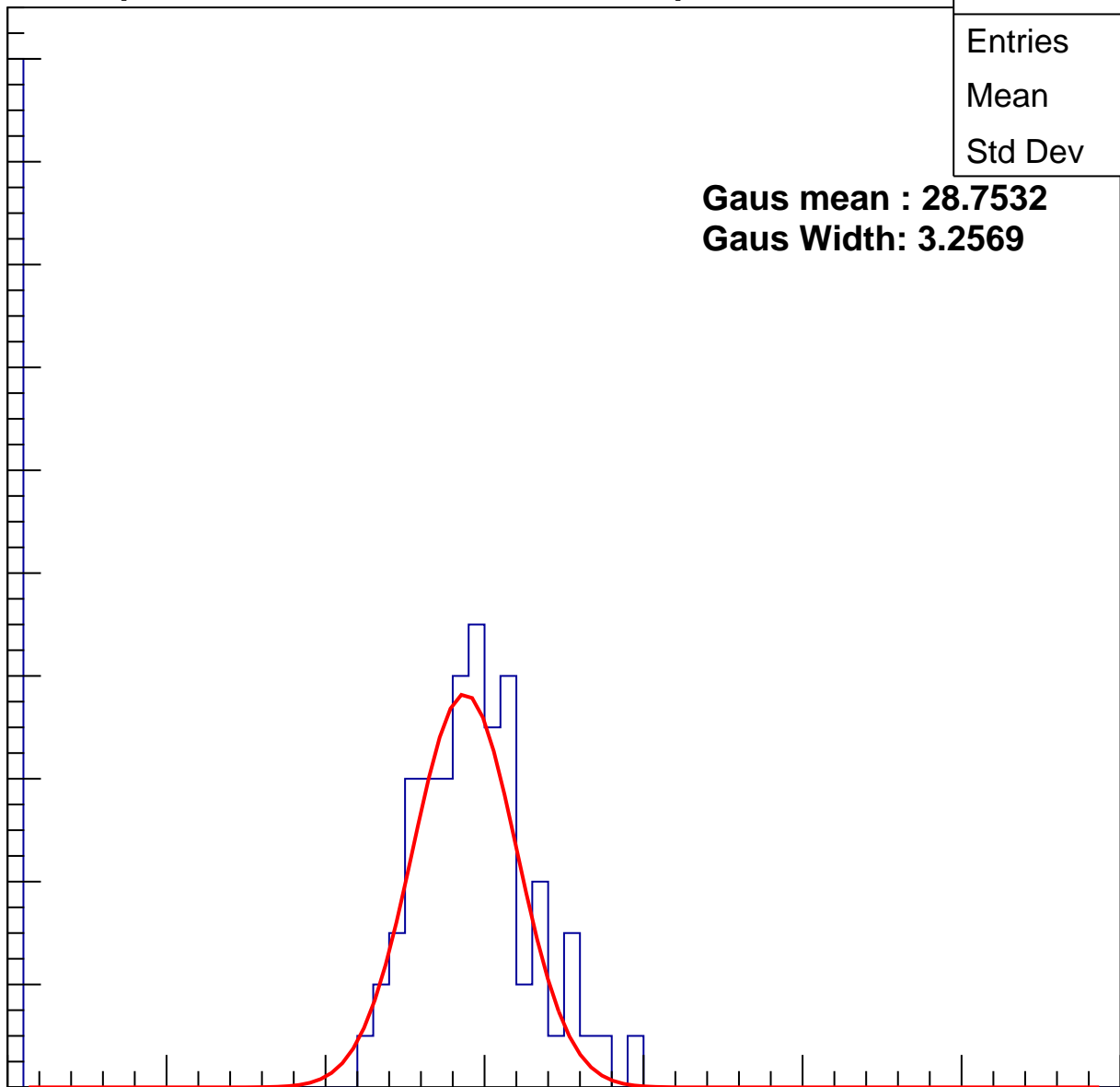
Gaus Width: 3.2569

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch79, adc1

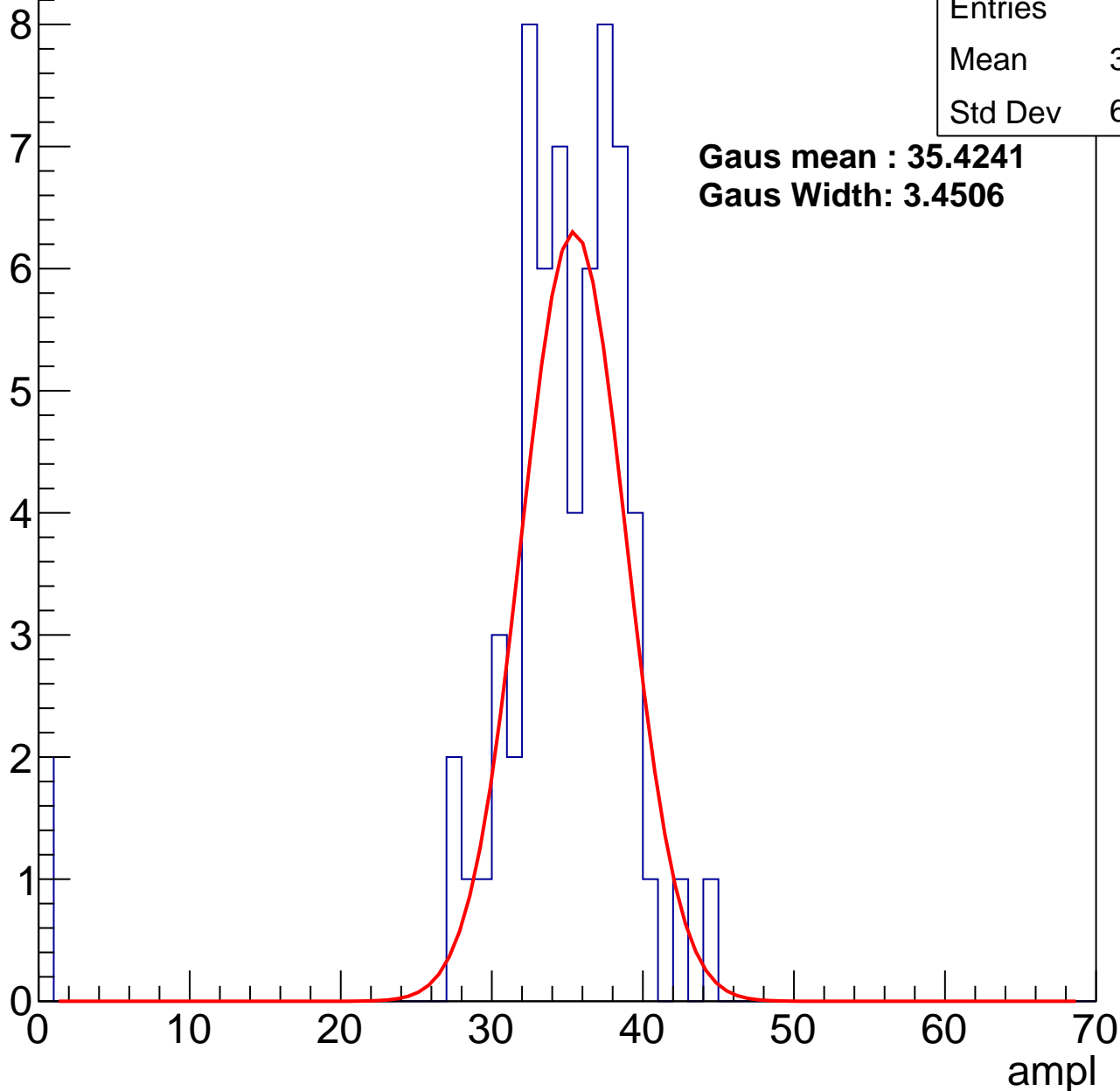
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	33.67
Std Dev	6.937

Gaus mean : 35.4241

Gaus Width: 3.4506



B1L103S, U2-ch79, adc2

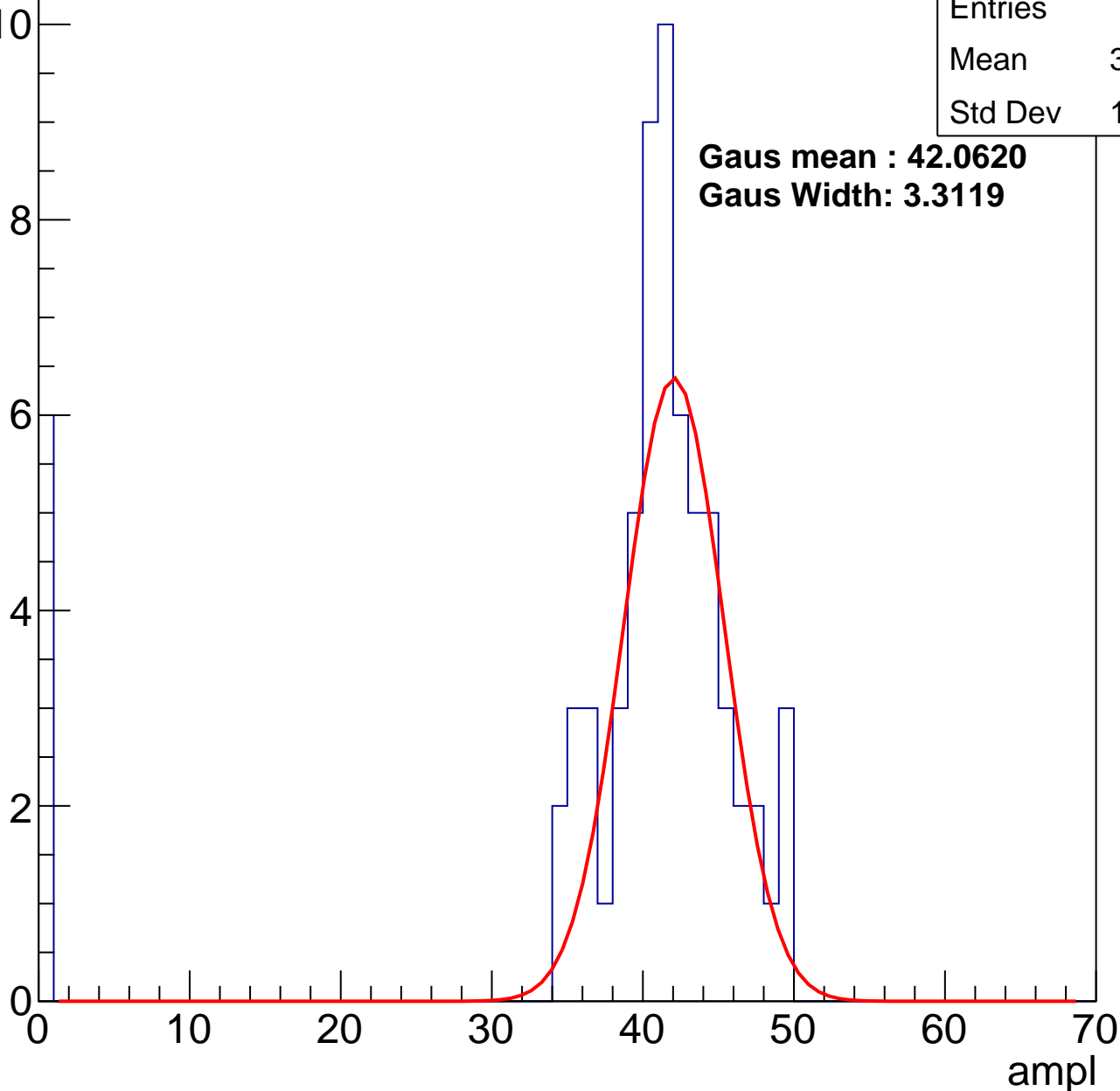
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	37.68
Std Dev	12.14

Gaus mean : 42.0620

Gaus Width: 3.3119

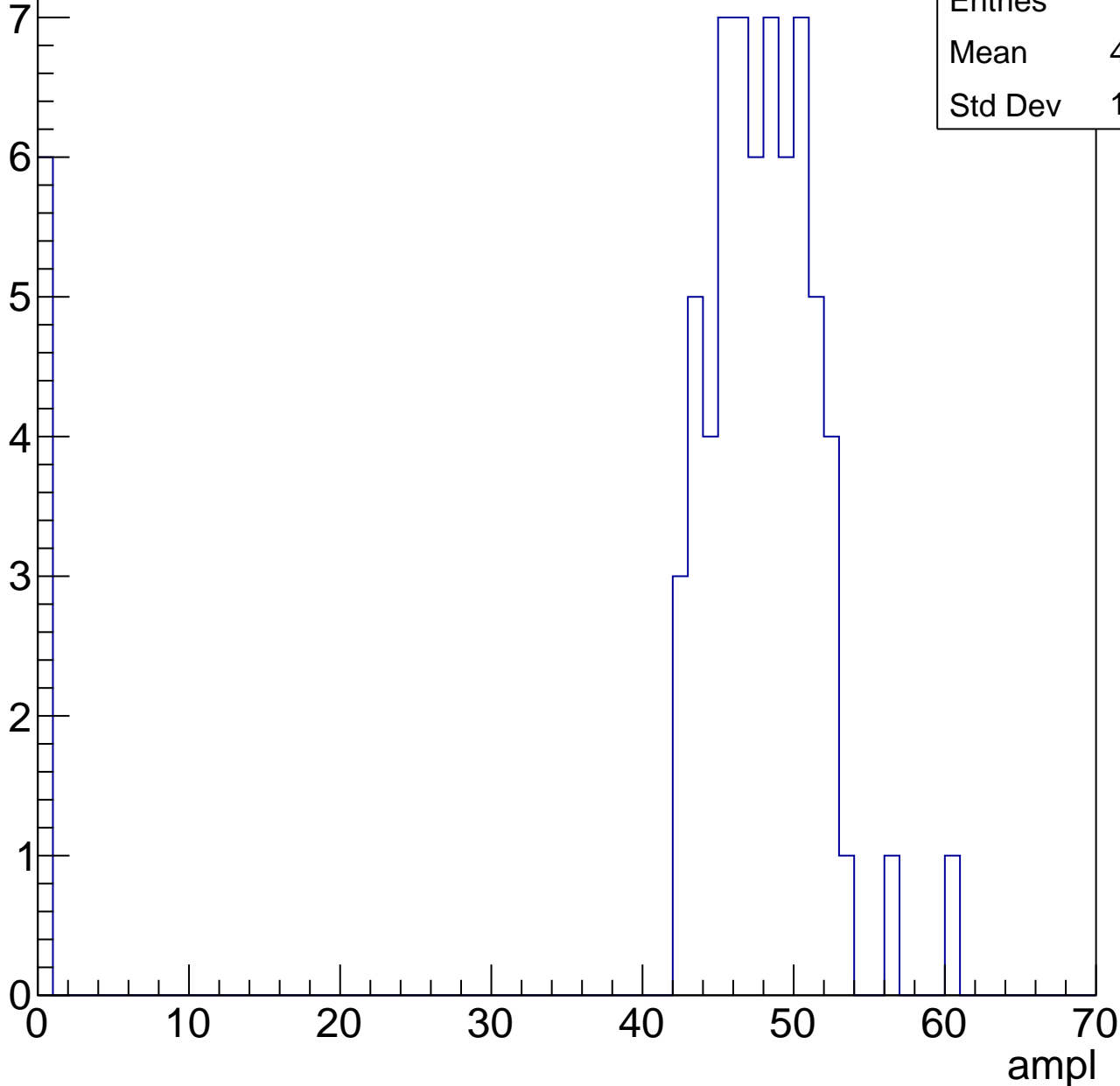


B1L103S, U2-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	43.54
Std Dev	13.73

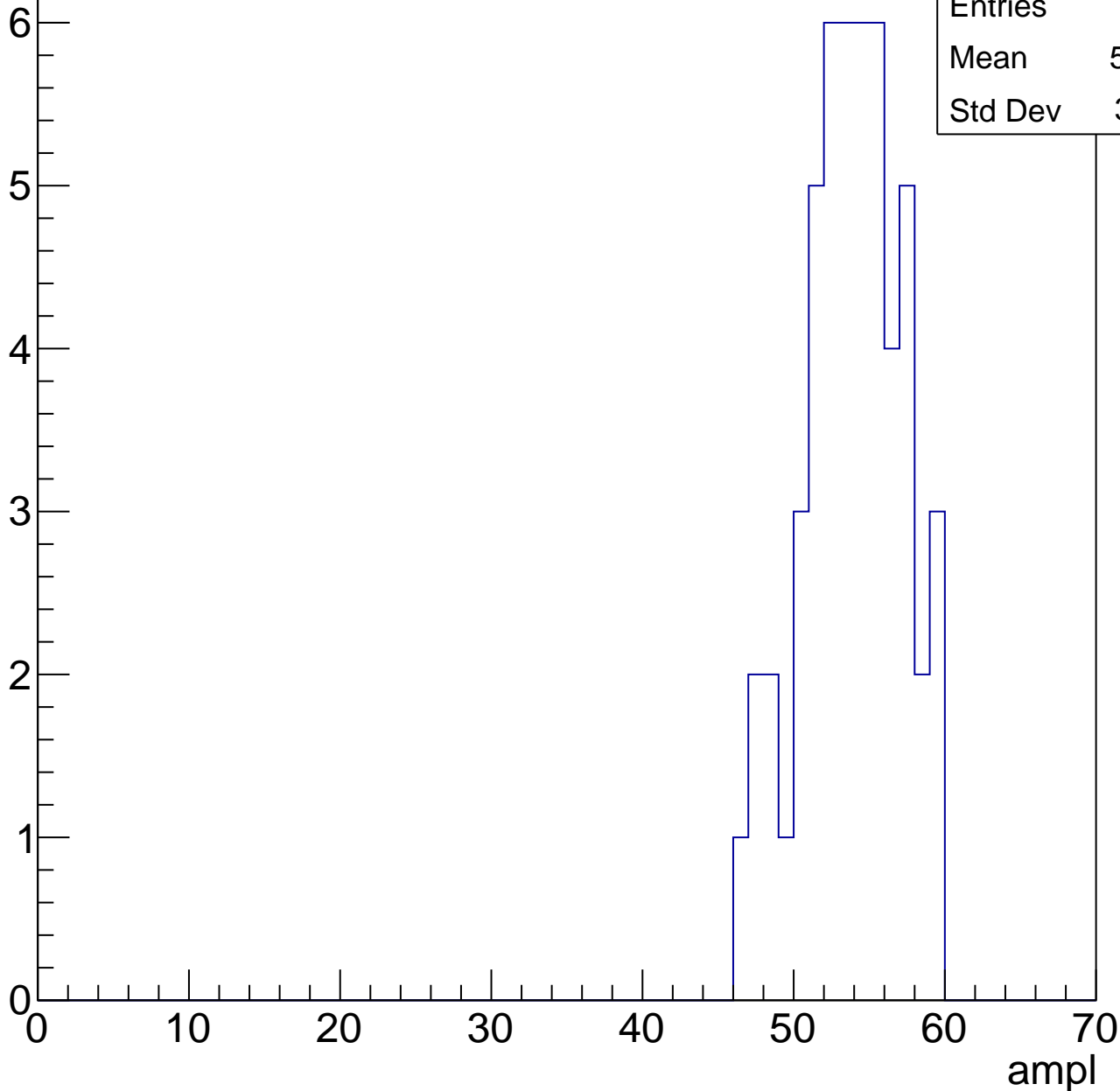


B1L103S, U2-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.38
Std Dev	3.211



B1L103S, U2-ch79, adc5

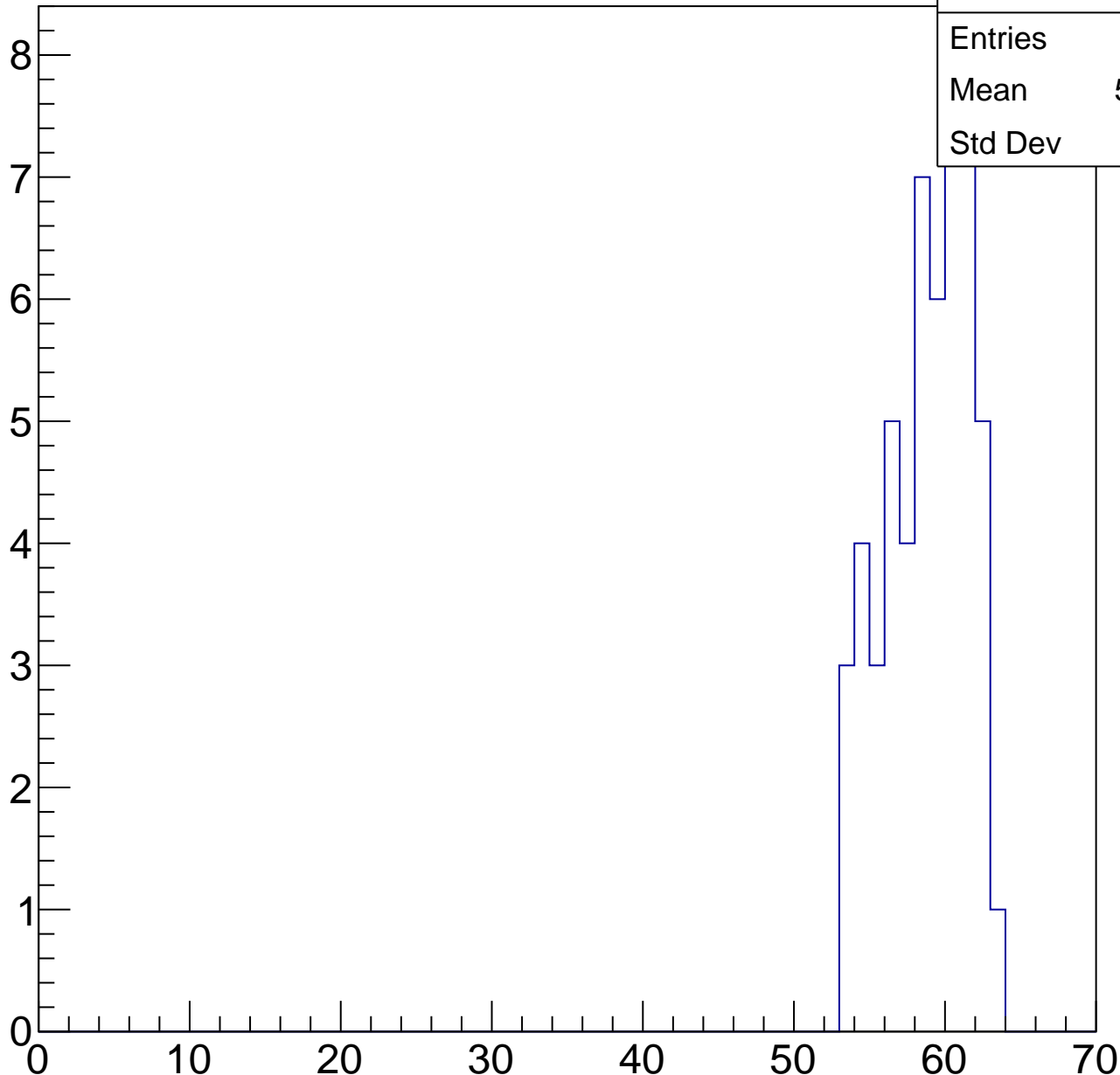
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.31
Std Dev	2.72

ampl

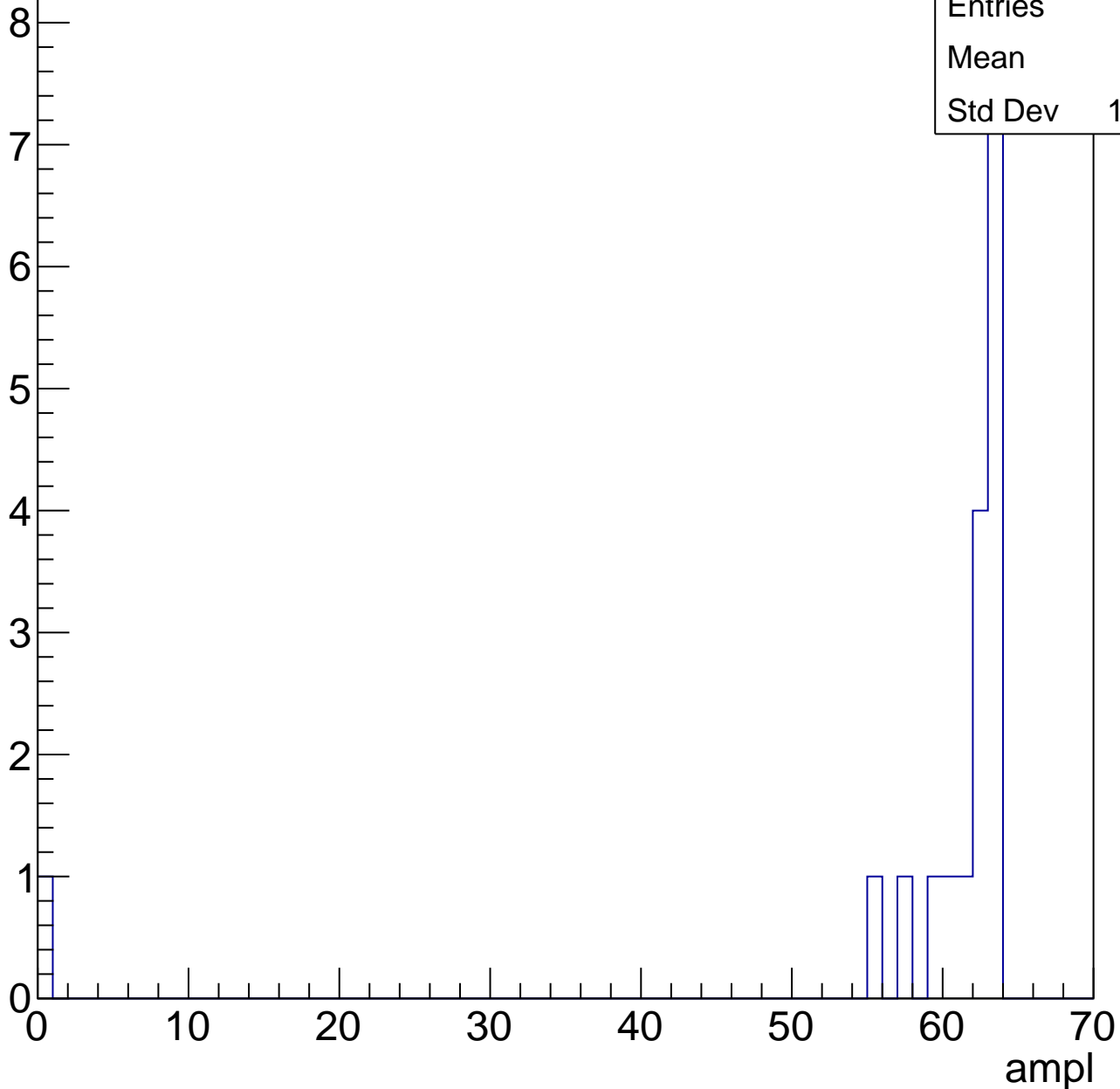


B1L103S, U2-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	58
Std Dev	14.24

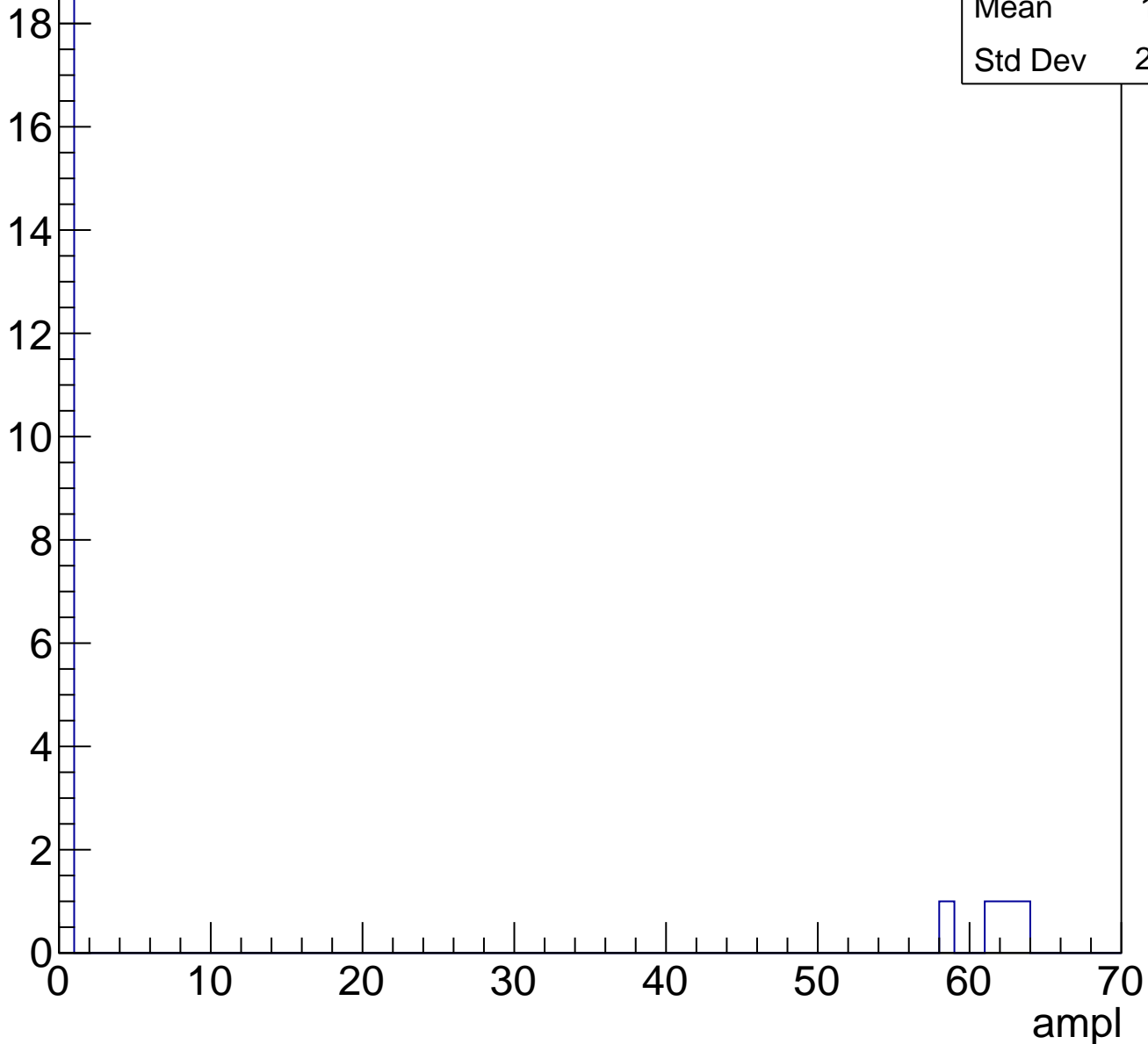


B1L103S, U2-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.61
Std Dev	23.13

Entry



B1L103S, U2-ch80, adc0

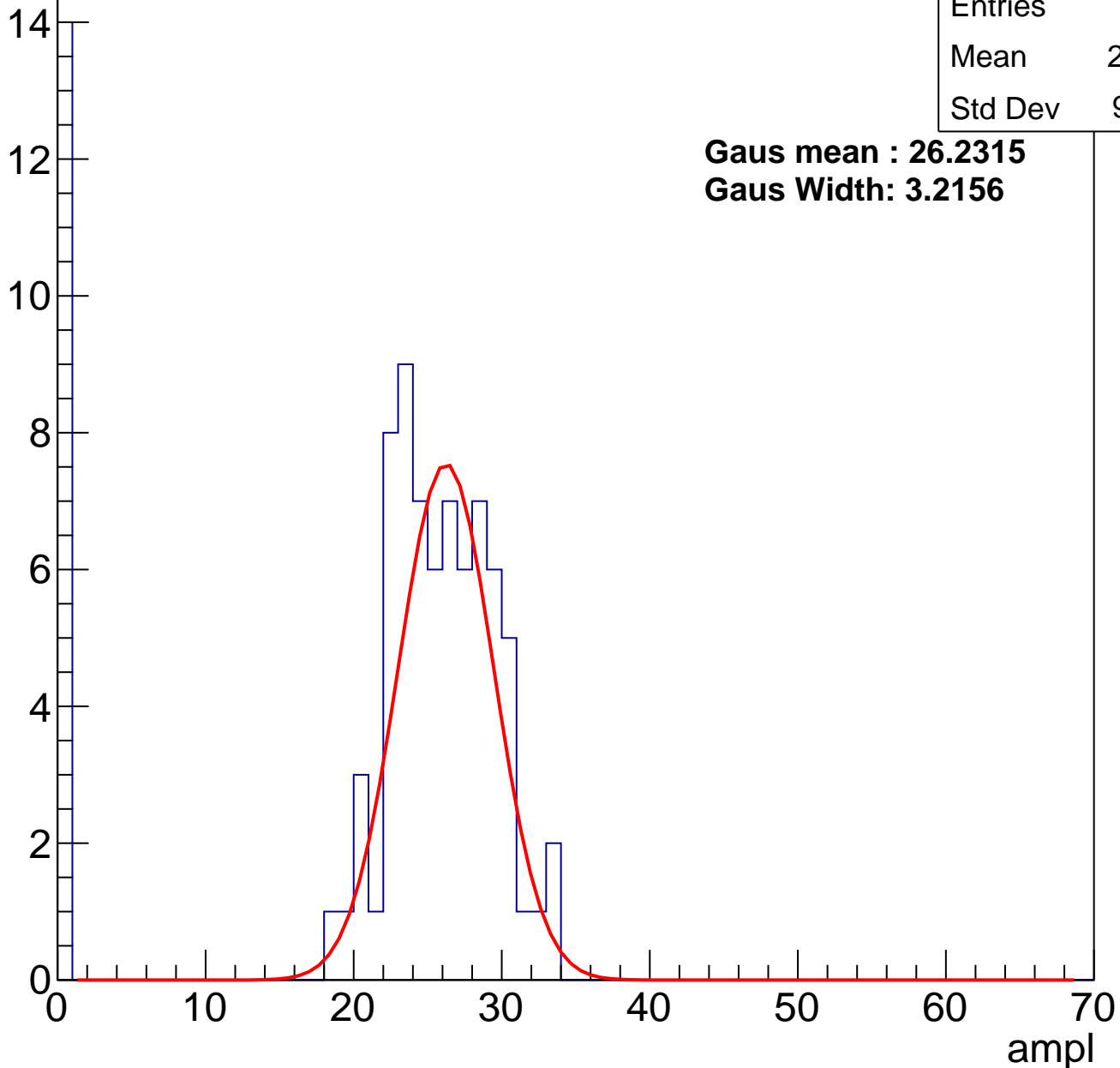
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	21.32
Std Dev	9.951

Gaus mean : 26.2315

Gaus Width: 3.2156

Entry



B1L103S, U2-ch80, adc1

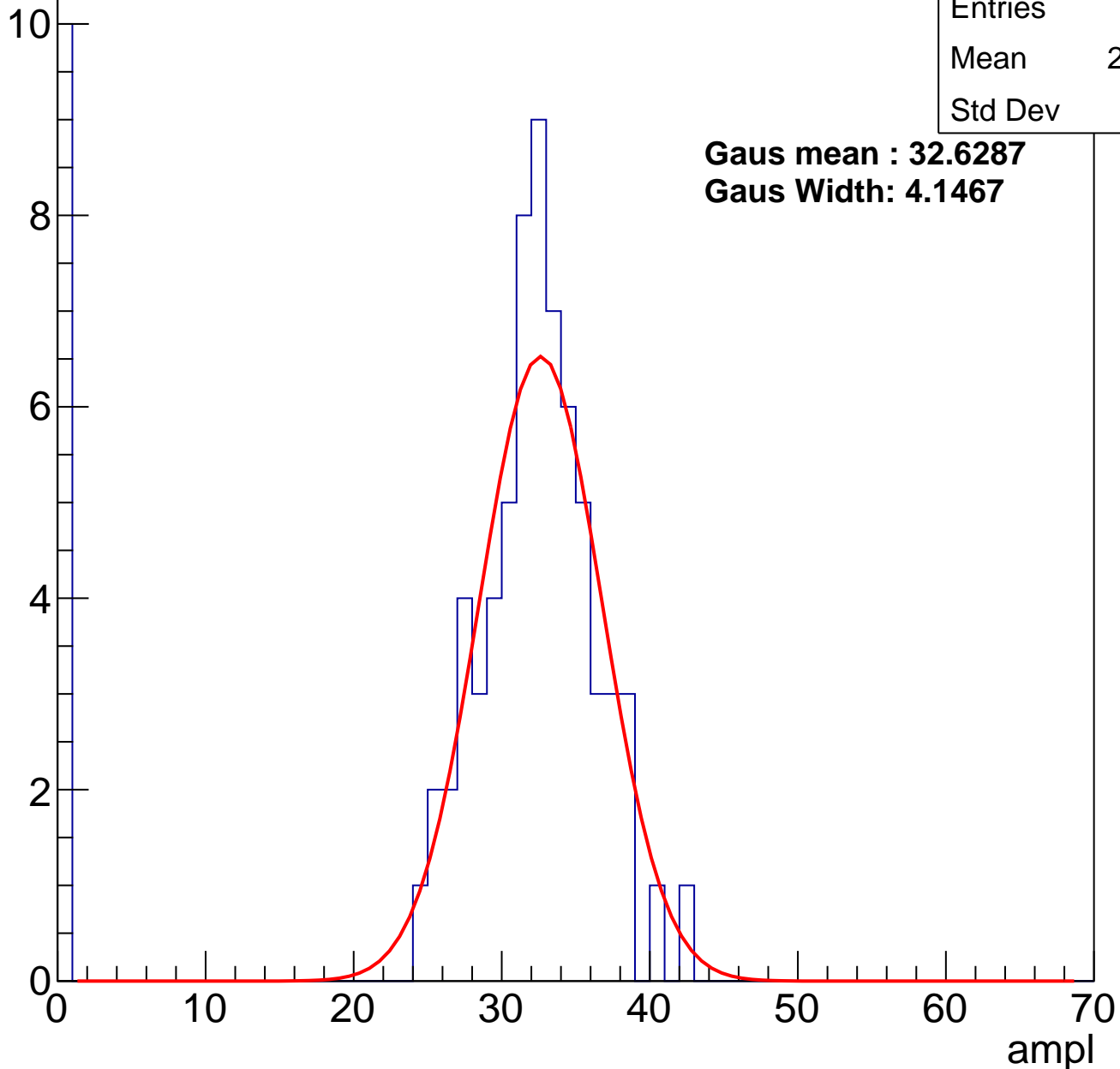
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	27.86
Std Dev	11.3

Gaus mean : 32.6287

Gaus Width: 4.1467

Entry



B1L103S, U2-ch80, adc2

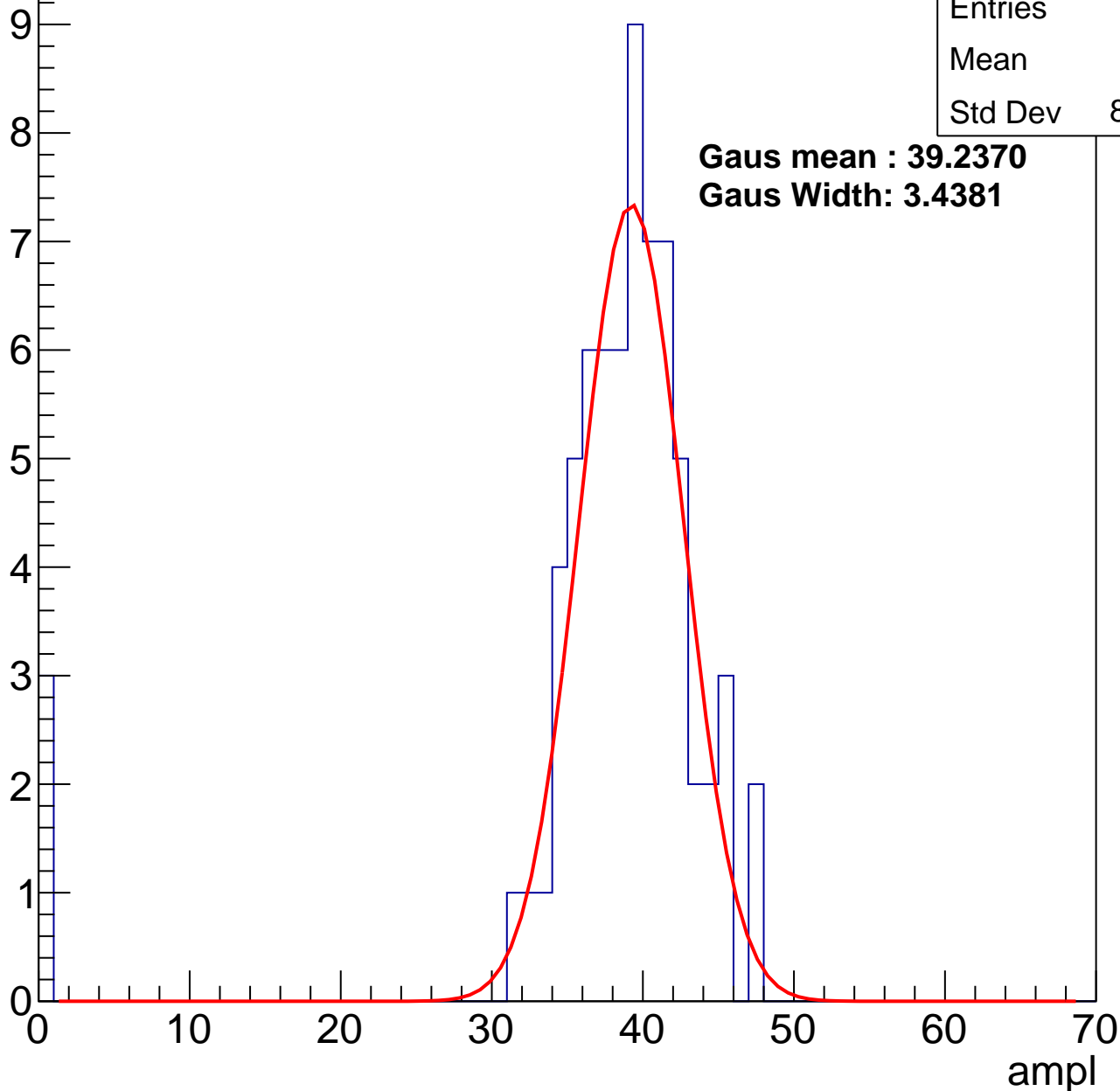
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	37.2
Std Dev	8.575

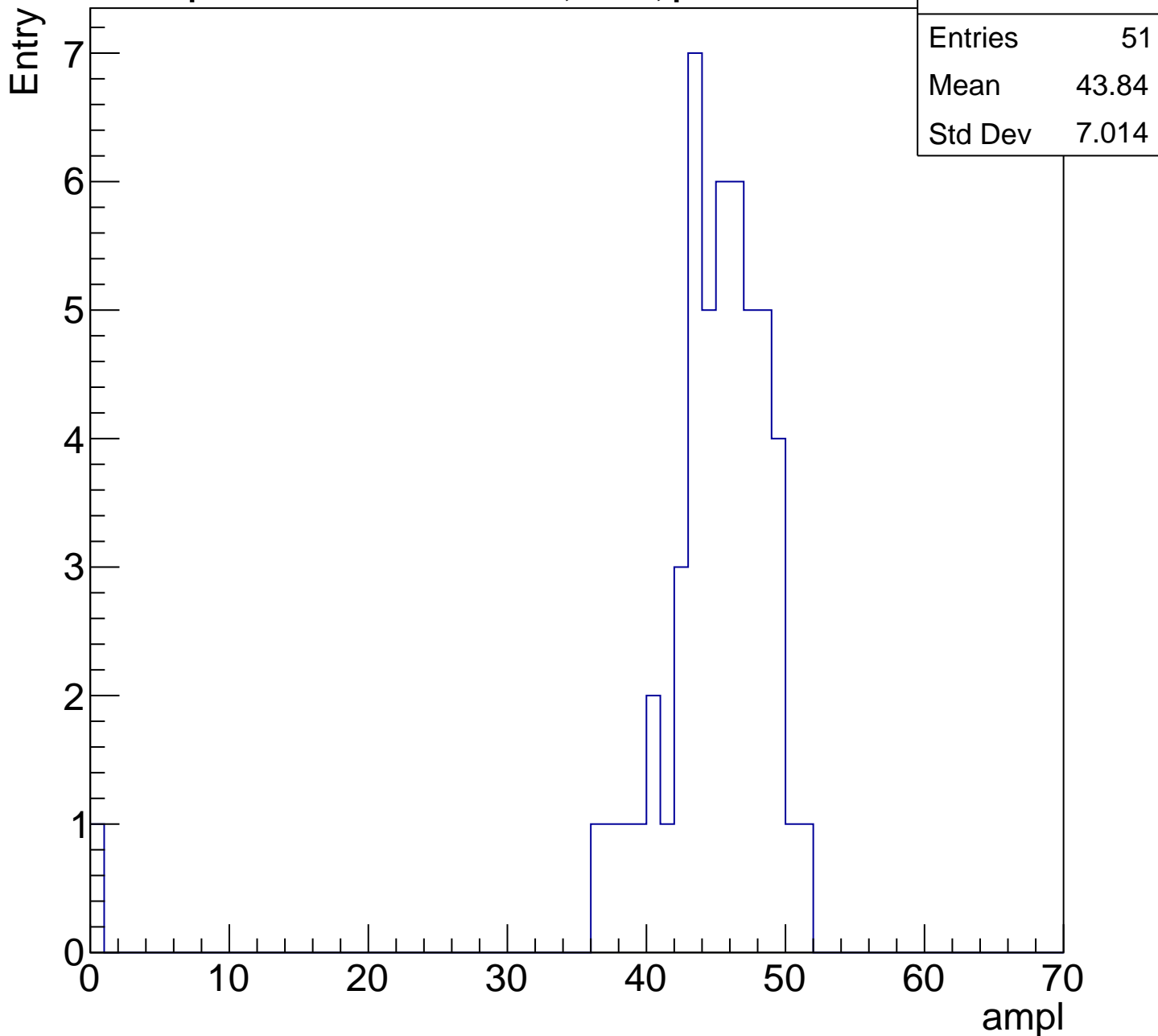
Gaus mean : 39.2370

Gaus Width: 3.4381



B1L103S, U2-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

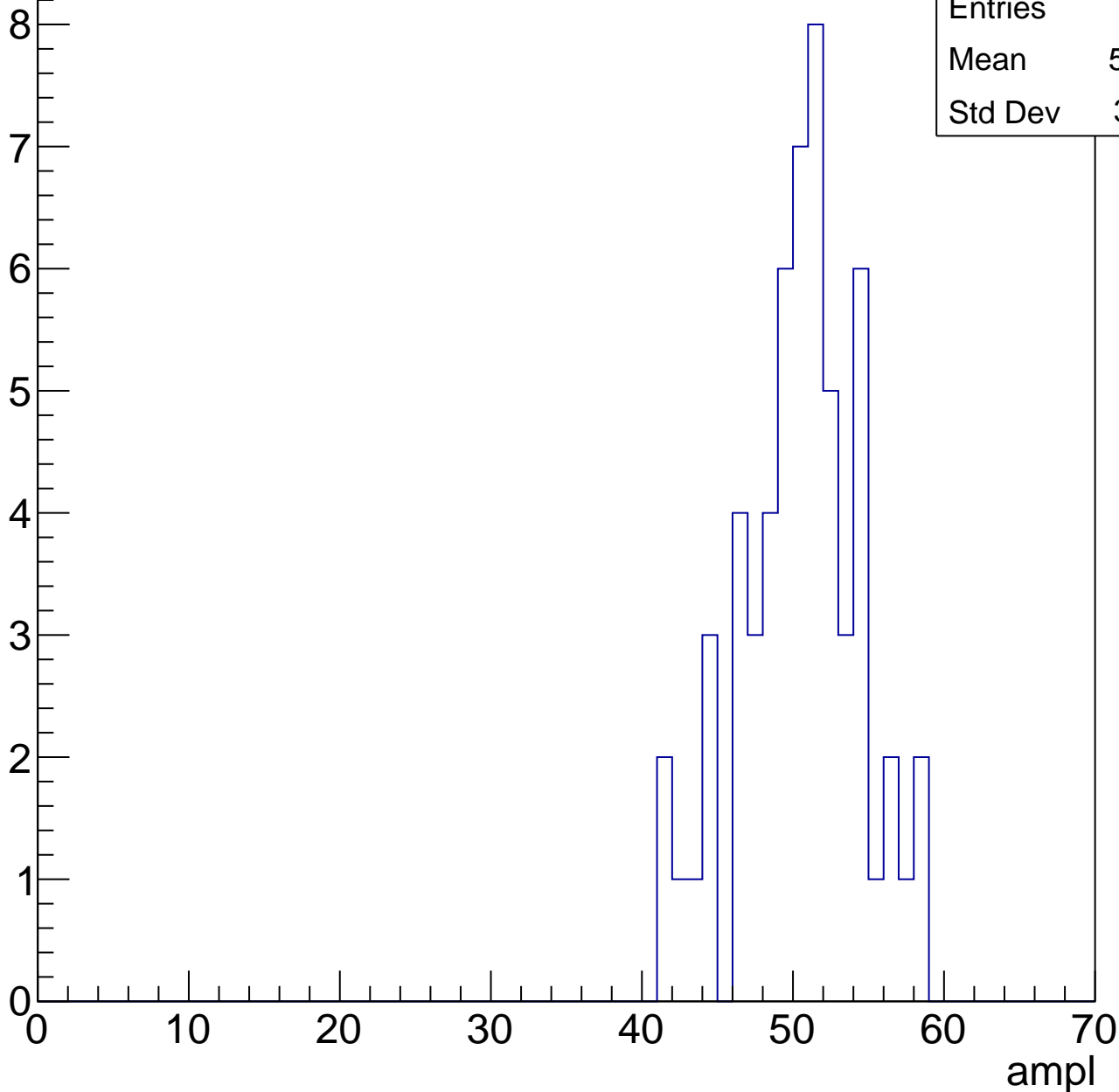


B1L103S, U2-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	50.02
Std Dev	3.951

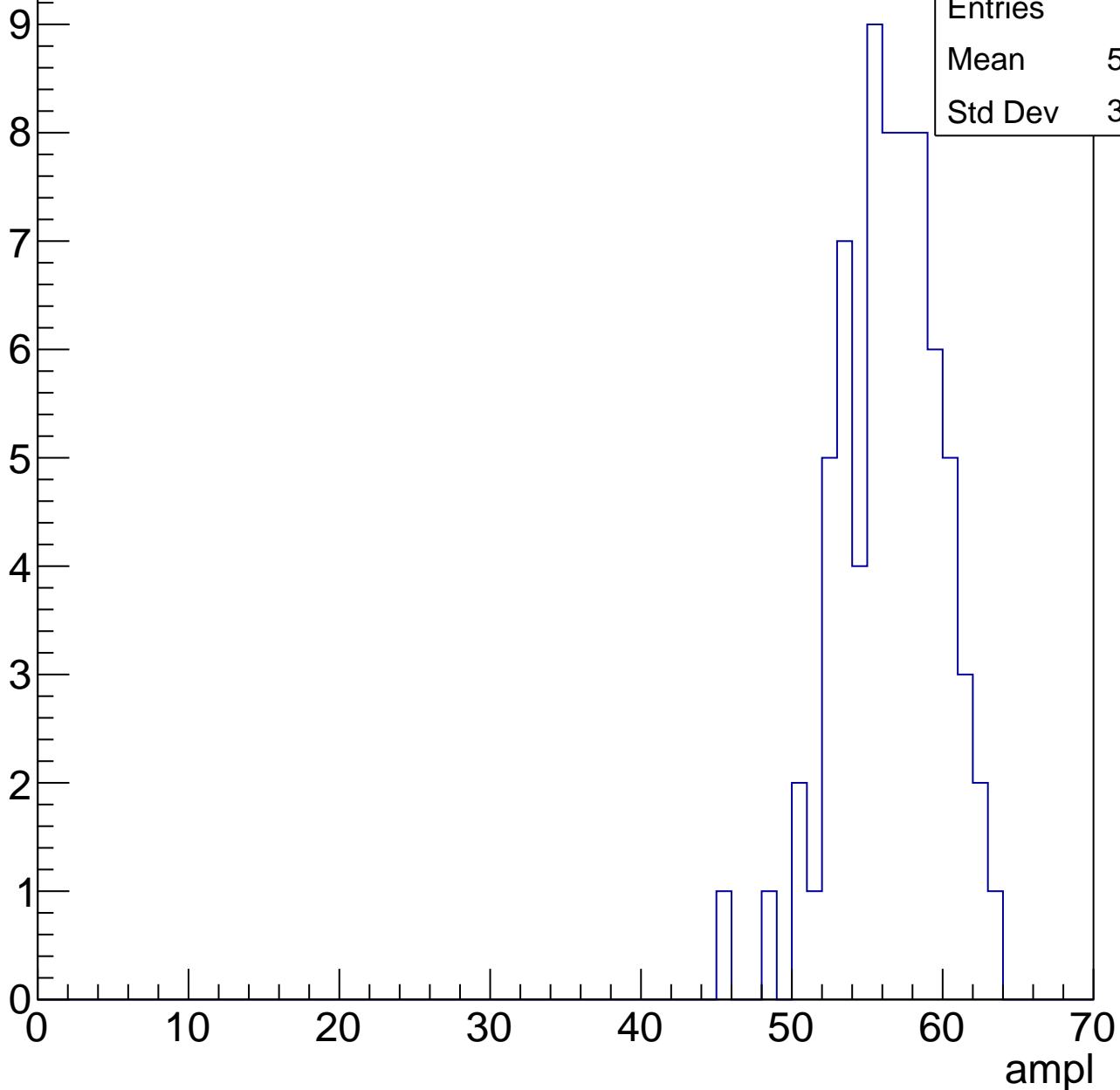


B1L103S, U2-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	56.03
Std Dev	3.398

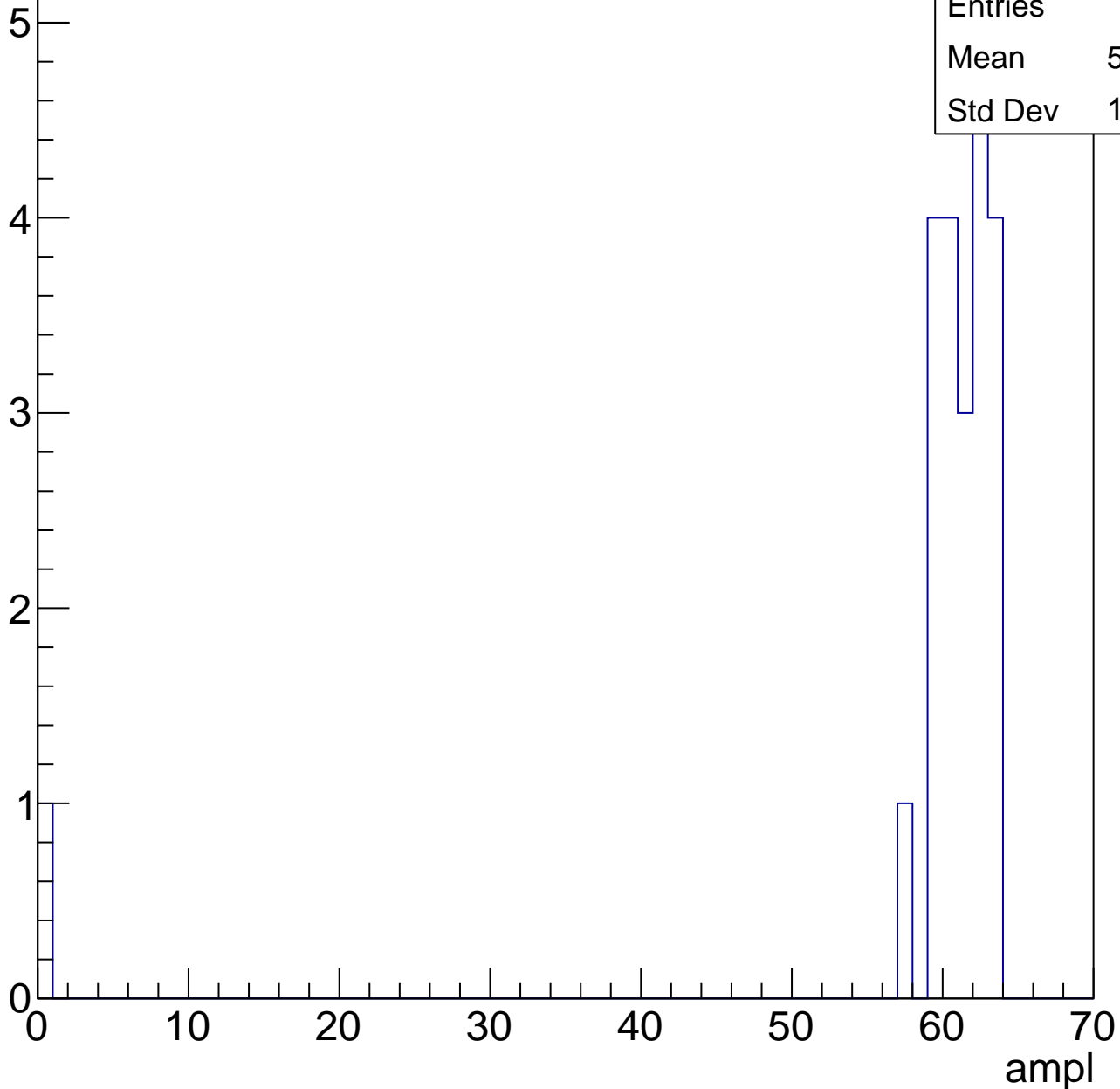


B1L103S, U2-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.09
Std Dev	12.78

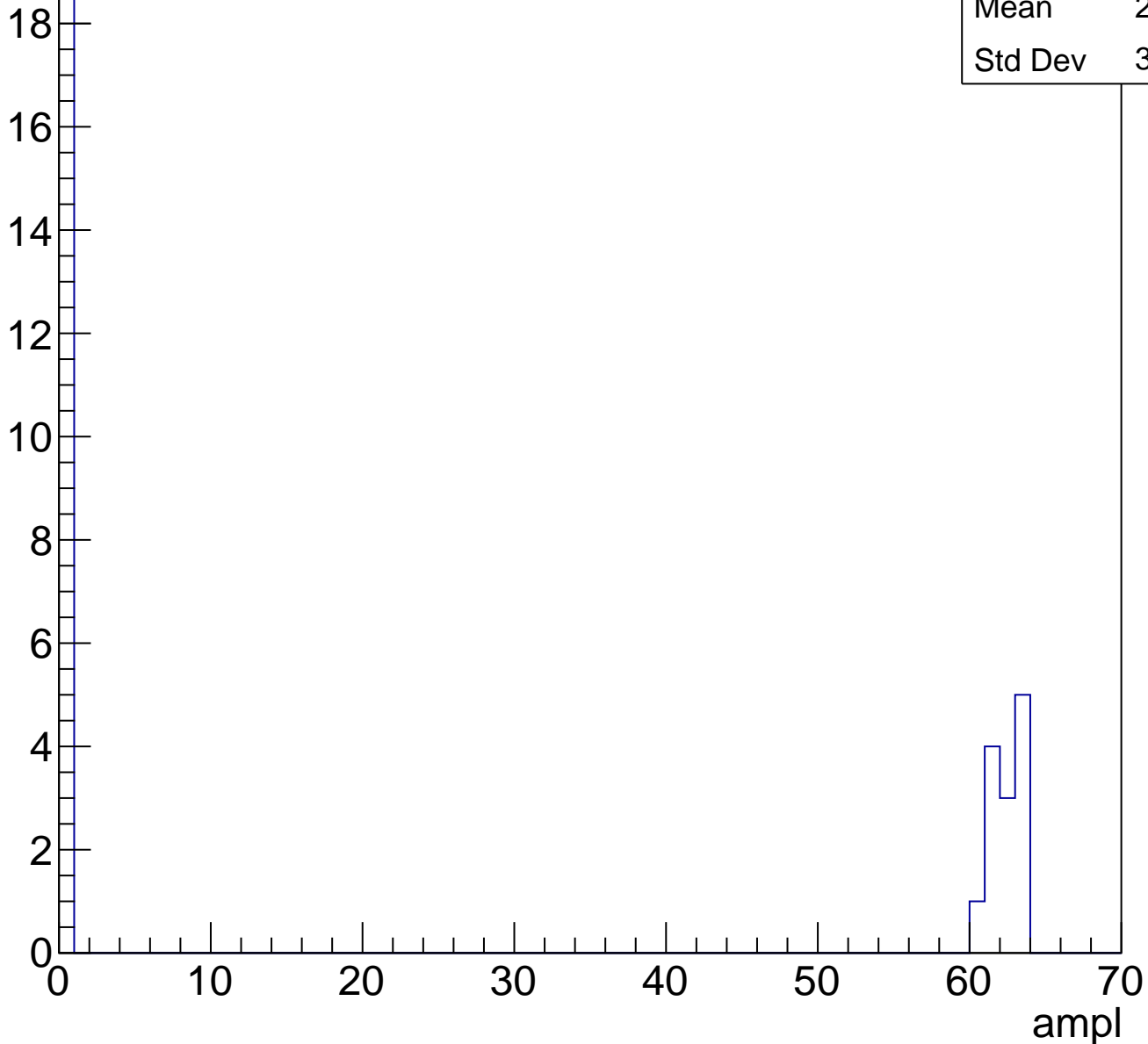


B1L103S, U2-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	32
Mean	25.16
Std Dev	30.42

Entry



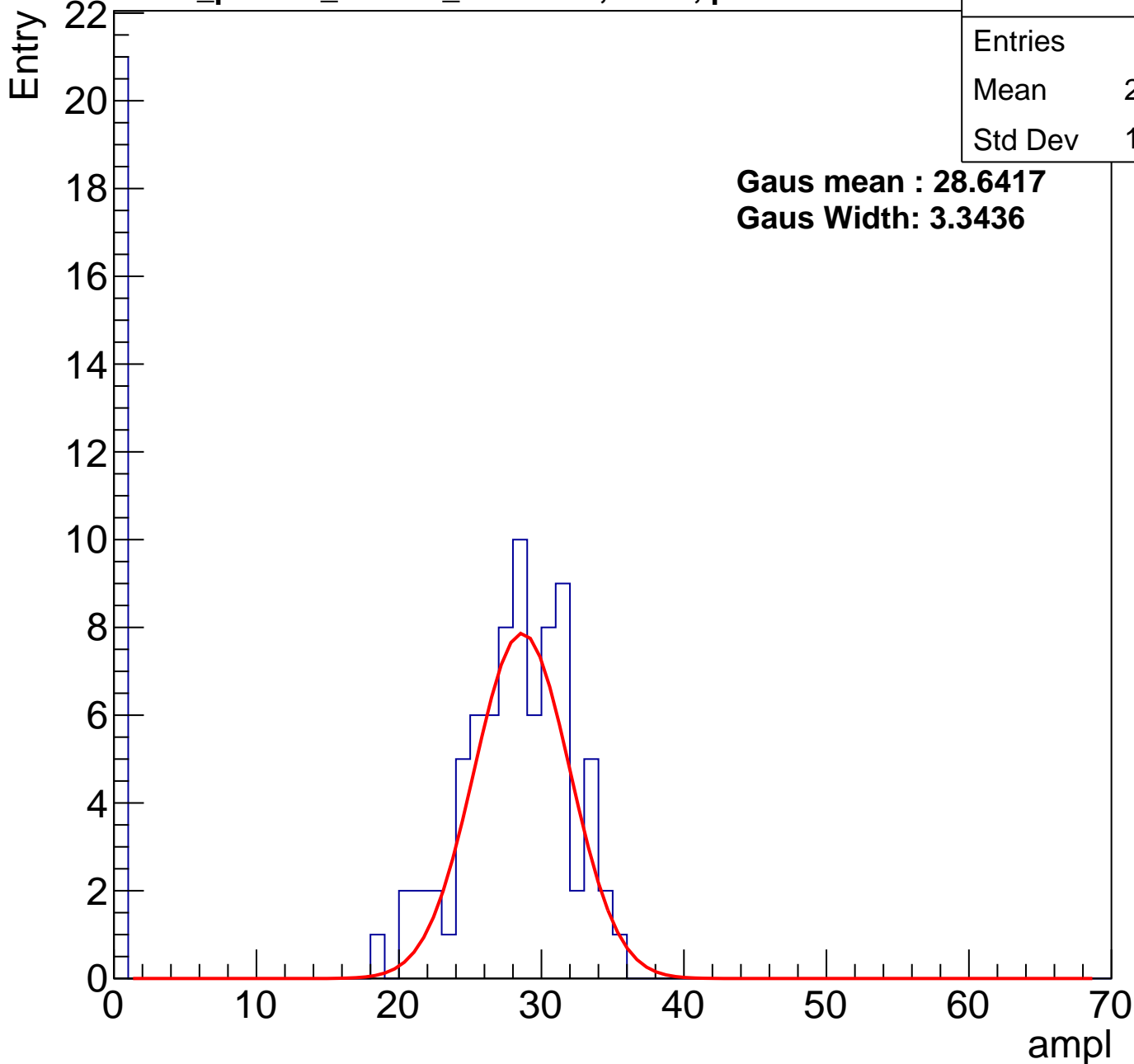
B1L103S, U2-ch81, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	21.79
Std Dev	11.89

Gaus mean : 28.6417

Gaus Width: 3.3436



B1L103S, U2-ch81, adc1

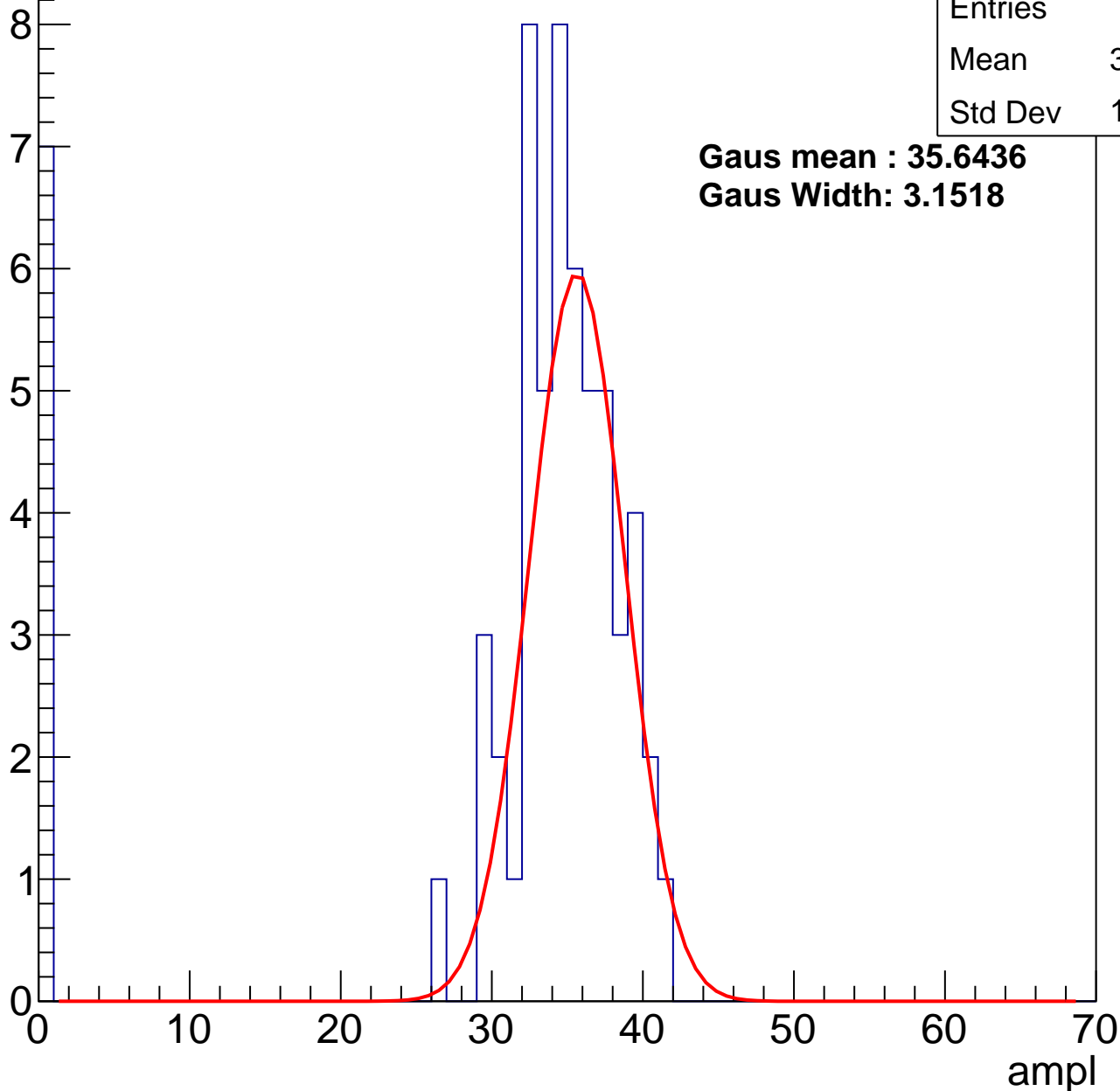
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	30.54
Std Dev	11.39

Gaus mean : 35.6436

Gaus Width: 3.1518



B1L103S, U2-ch81, adc2

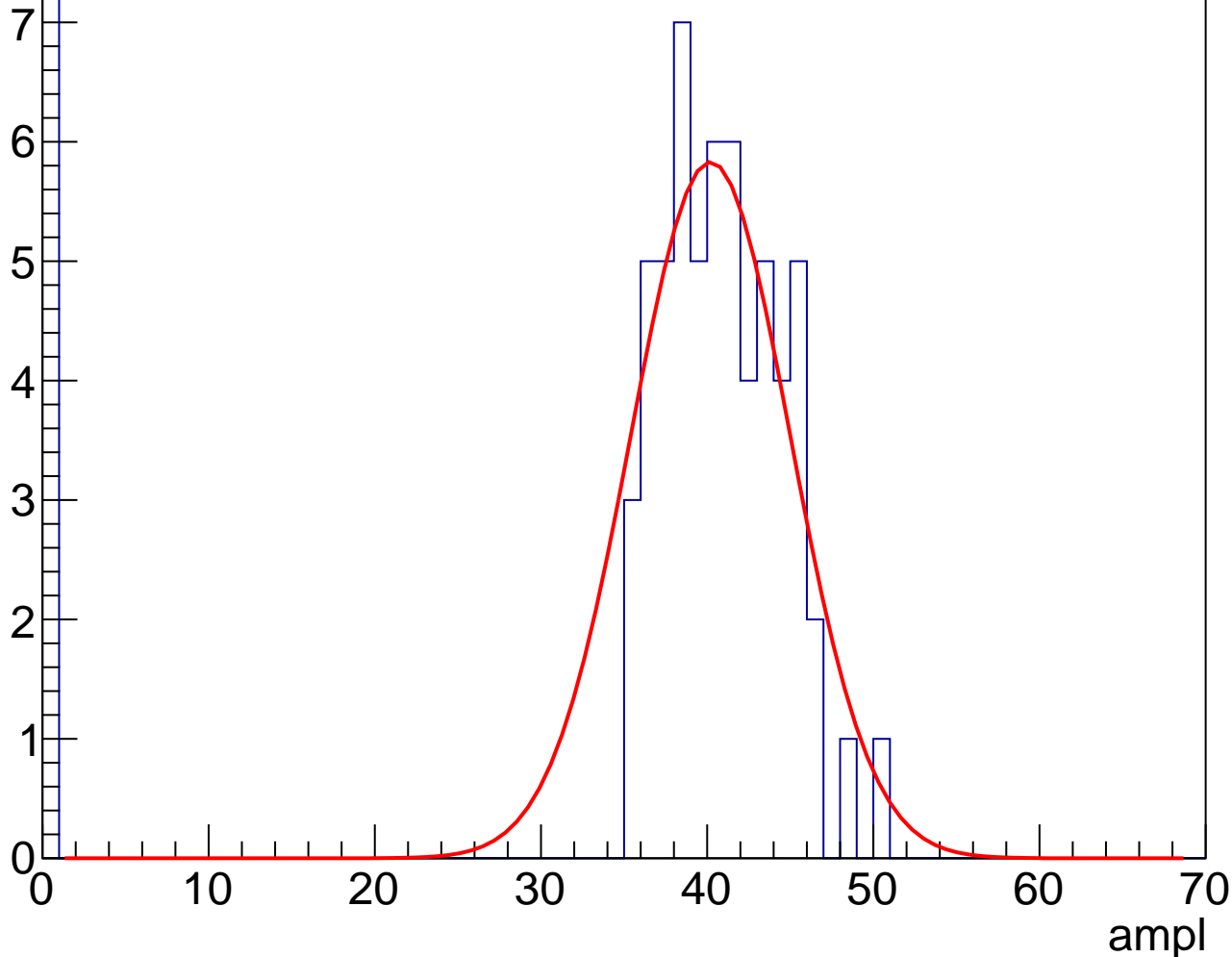
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	35.16
Std Dev	14.11

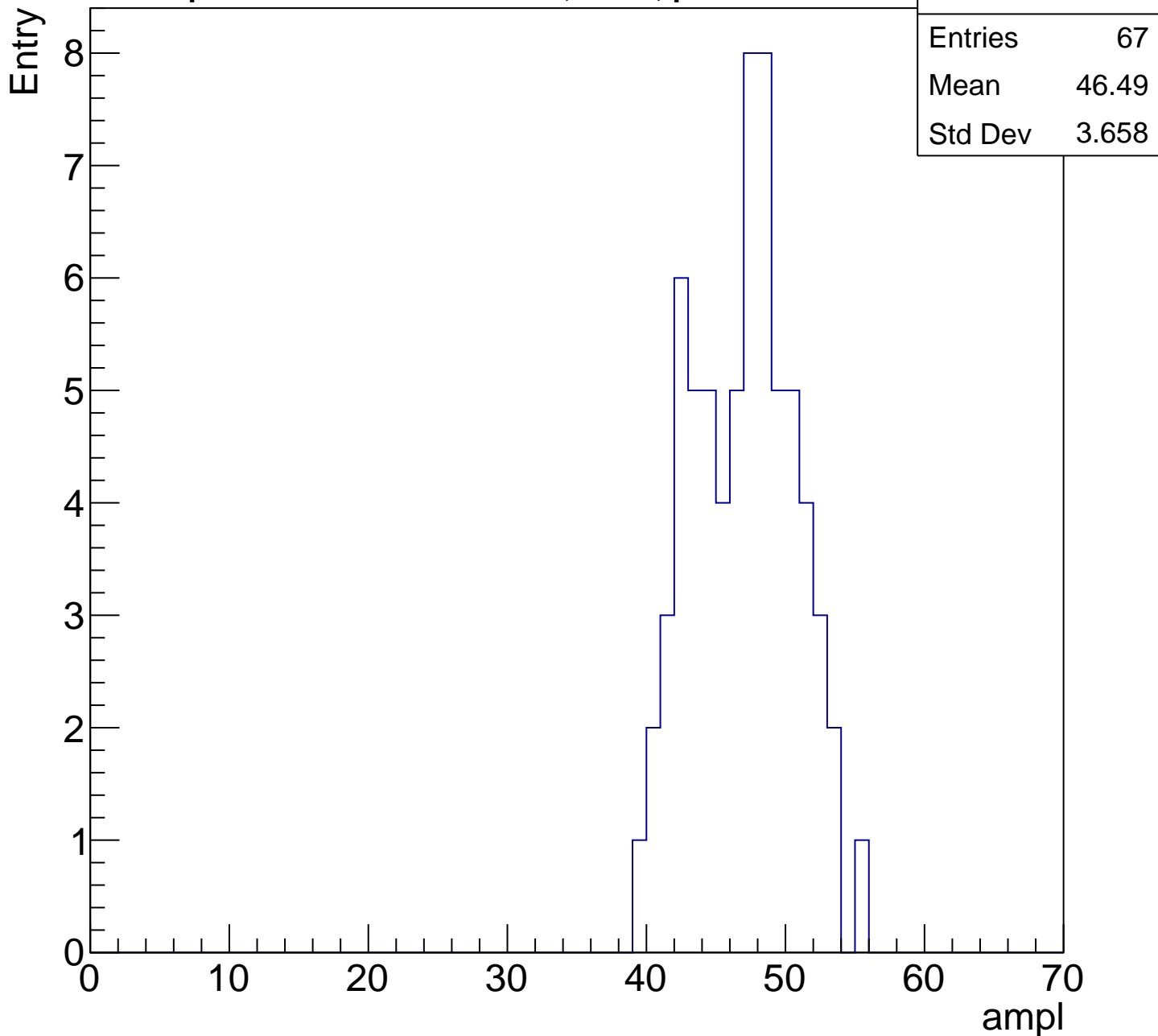
Gaus mean : 40.2011

Gaus Width: 4.8038



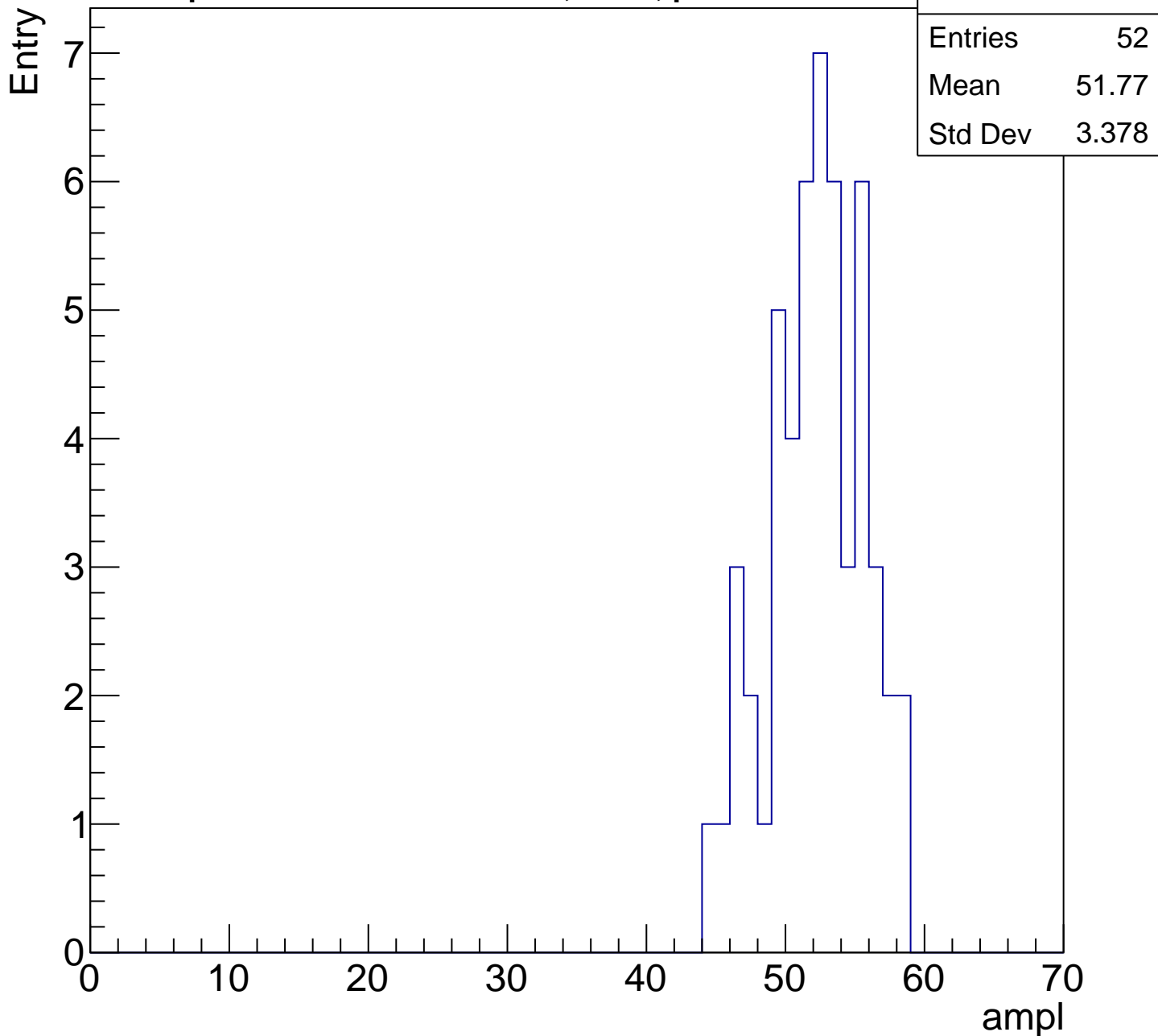
B1L103S, U2-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 61

Mean 58.05

Std Dev 2.743

8

6

4

2

0

0

10

20

30

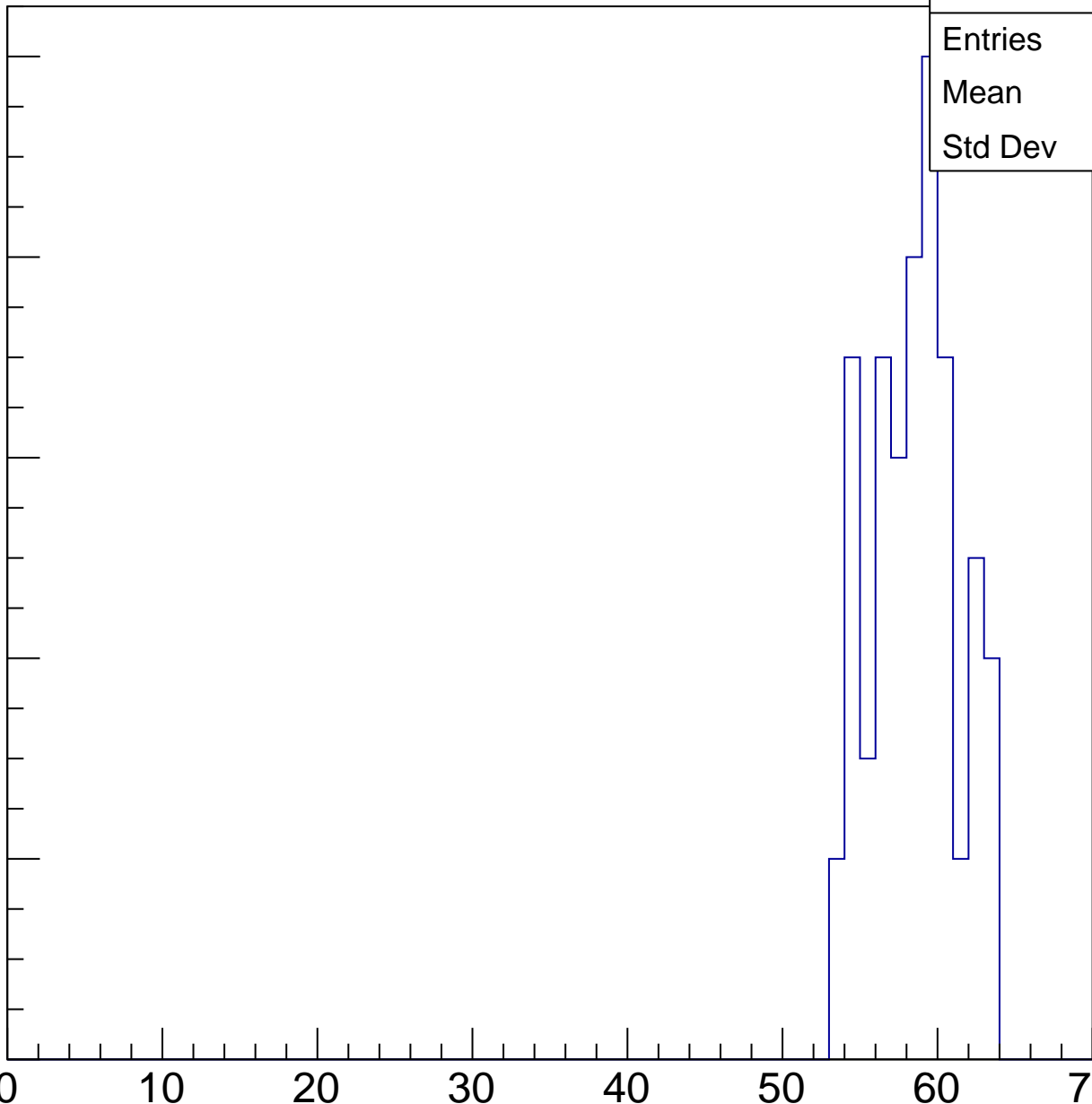
40

50

ampl

60

70

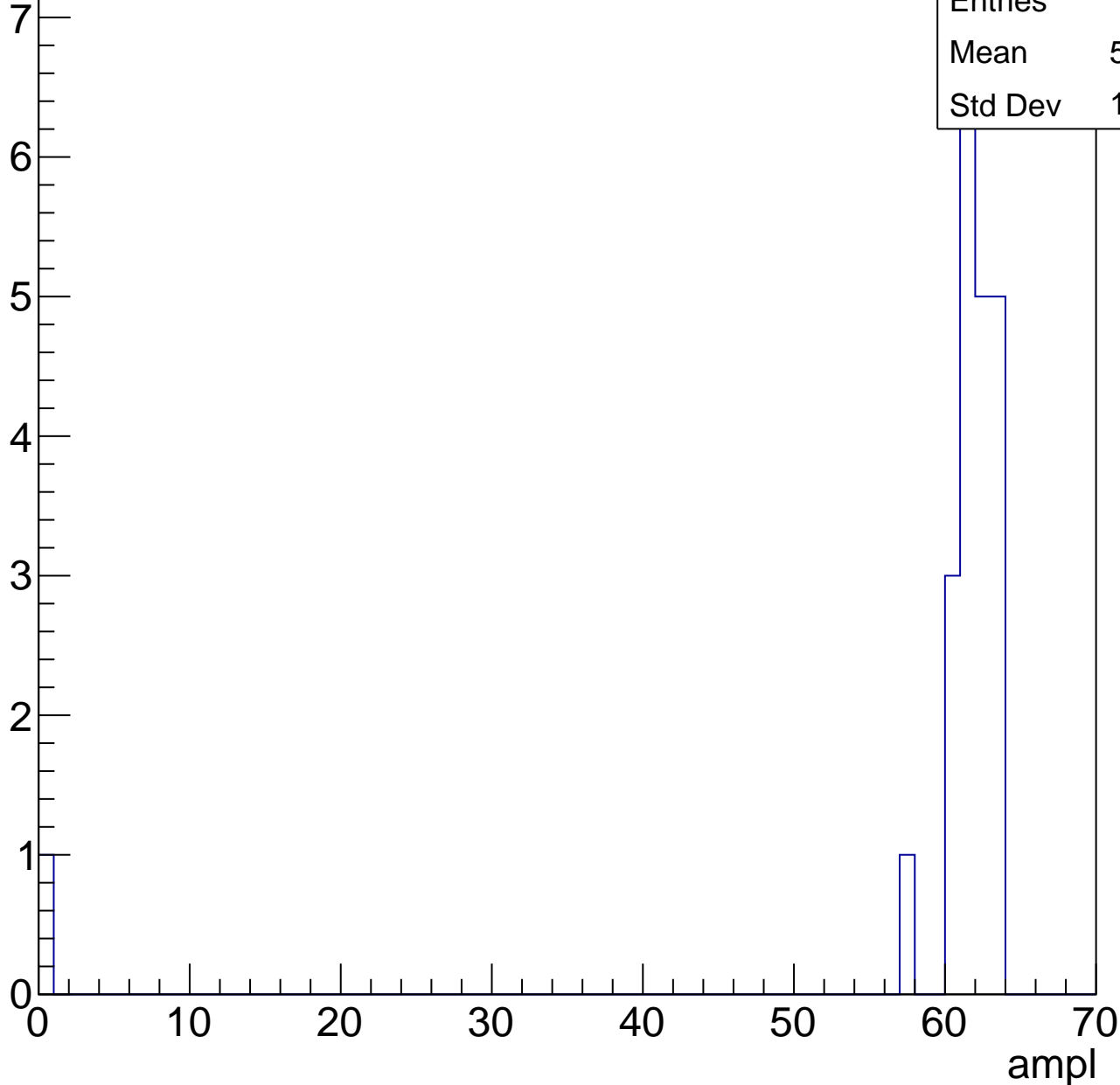


B1L103S, U2-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

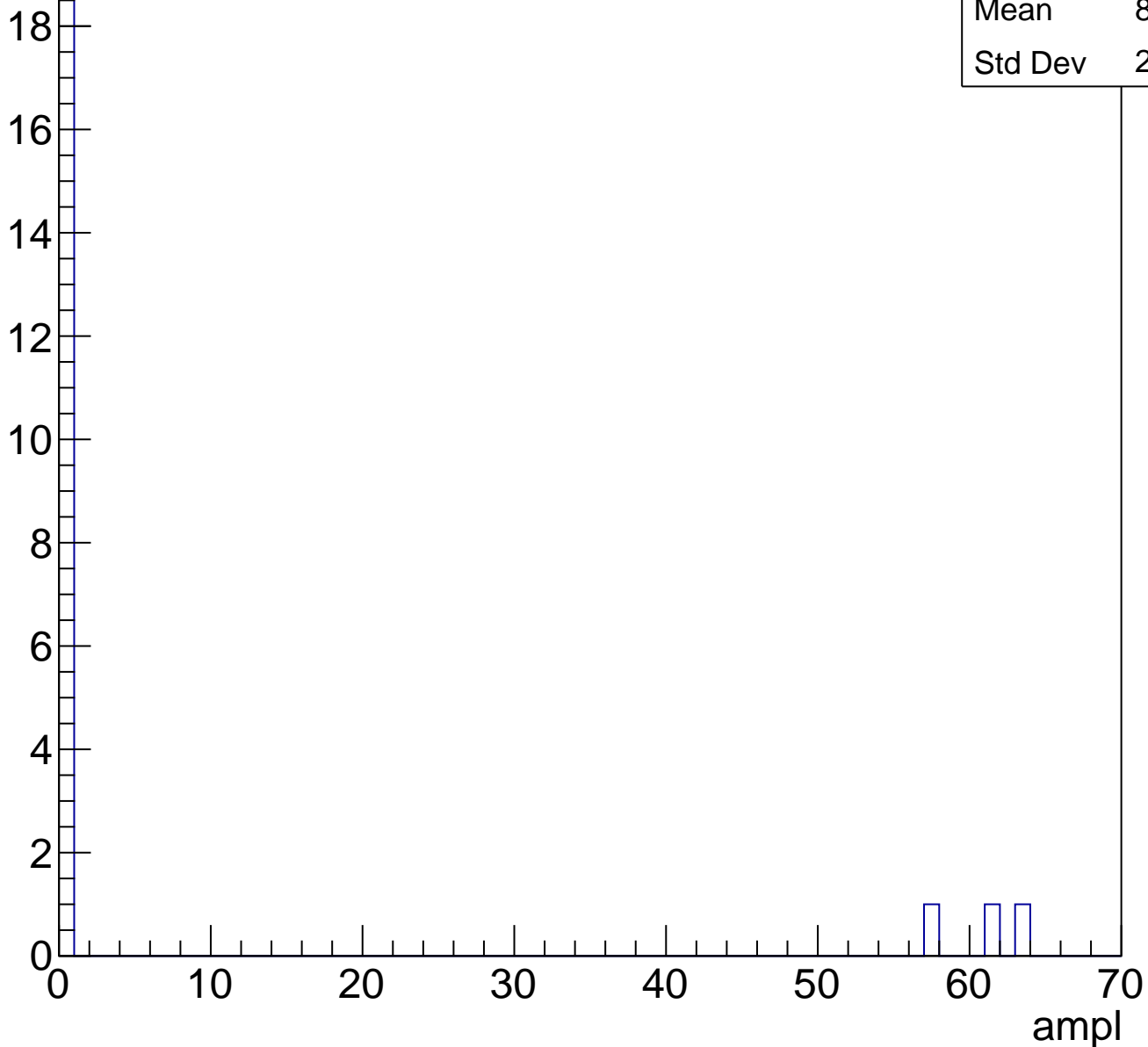
Entries	22
Mean	58.59
Std Dev	12.86



B1L103S, U2-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	22
Mean	8.227
Std Dev	20.73

B1L103S, U2-ch82, adc0

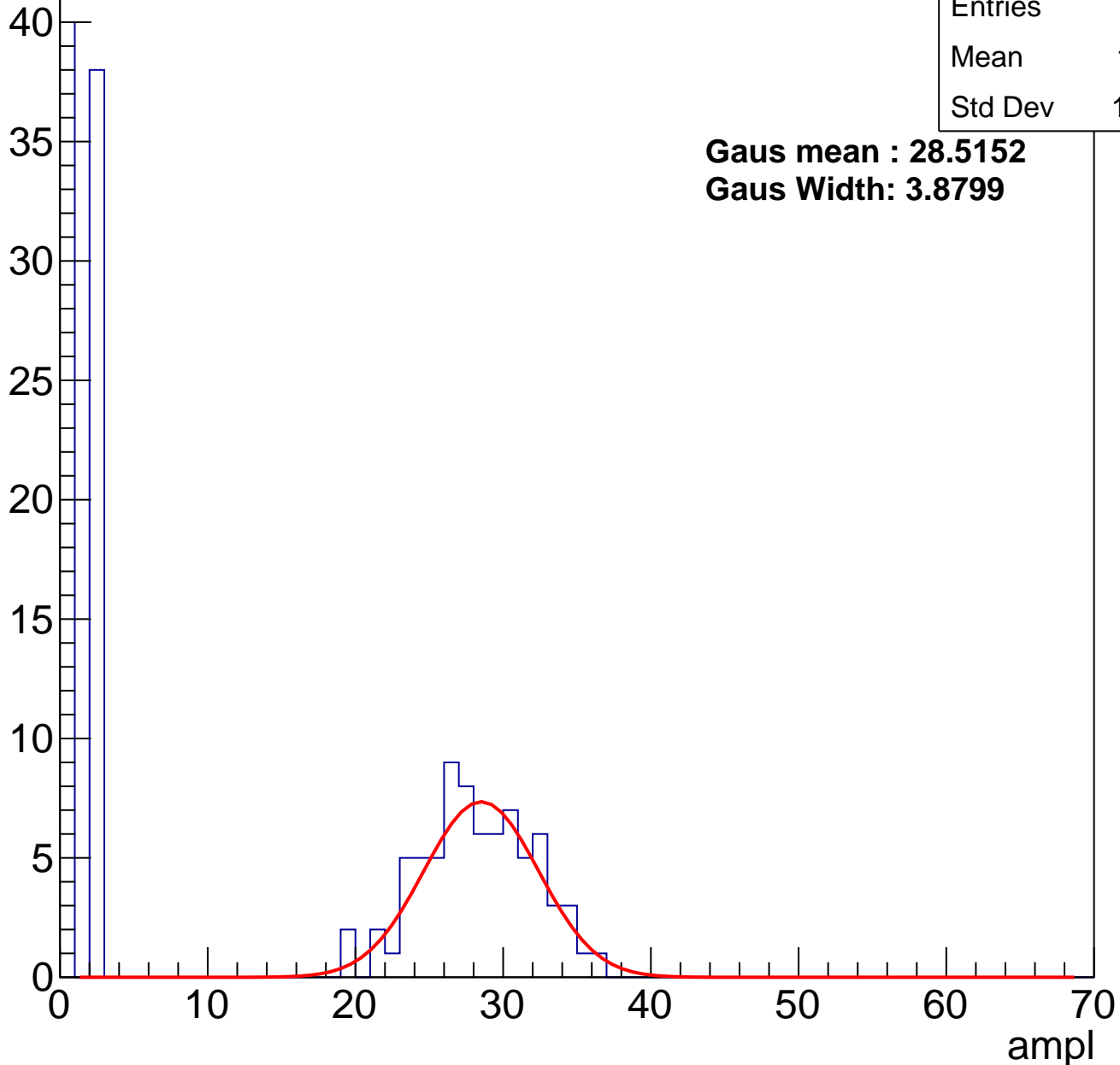
calib_packv5_041523_1651.root, FC#0, port C2

Entries	153
Mean	14.11
Std Dev	13.67

Gaus mean : 28.5152

Gaus Width: 3.8799

Entry



B1L103S, U2-ch82, adc1

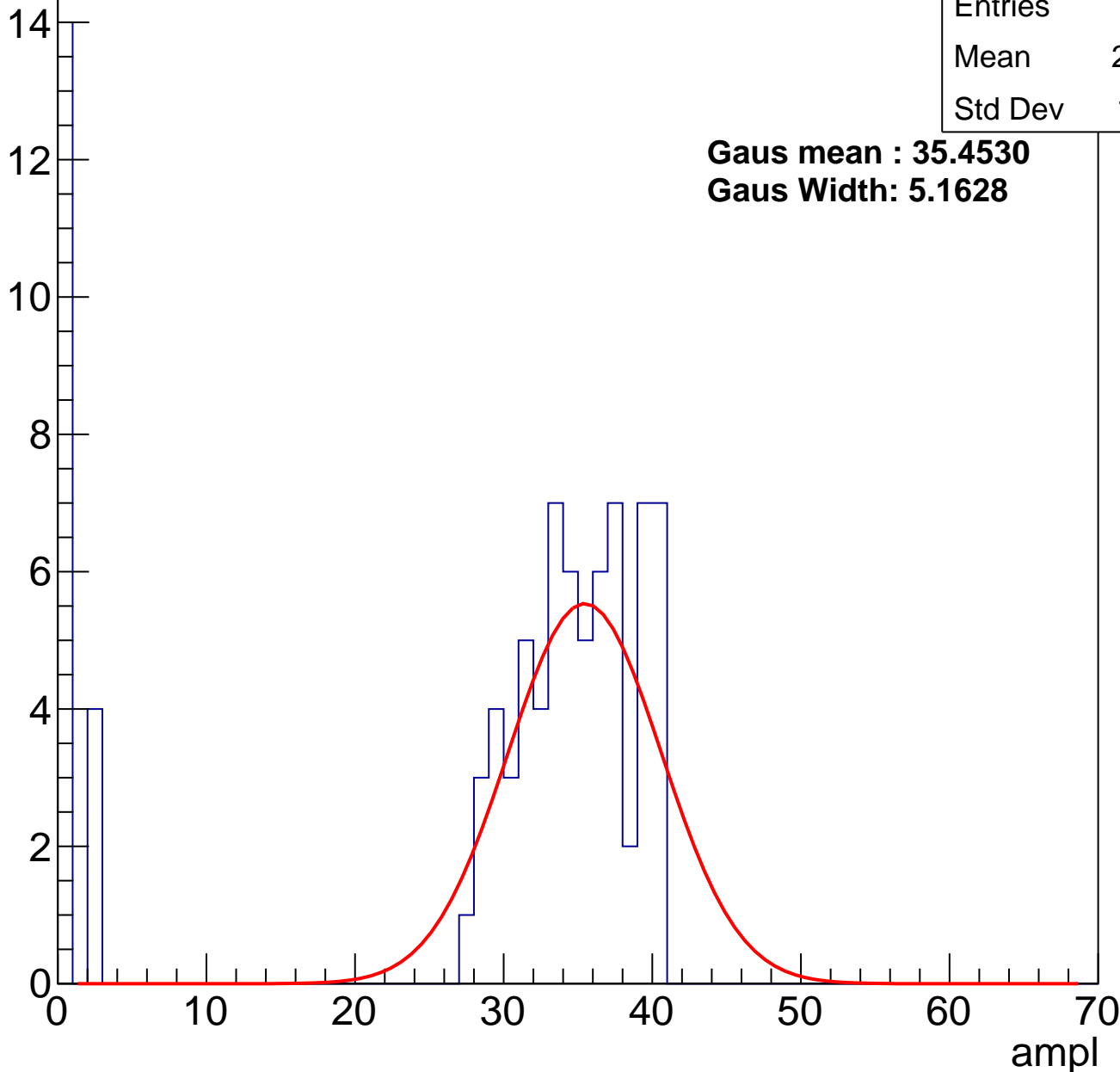
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	27.32
Std Dev	14.31

Gaus mean : 35.4530

Gaus Width: 5.1628

Entry



B1L103S, U2-ch82, adc2

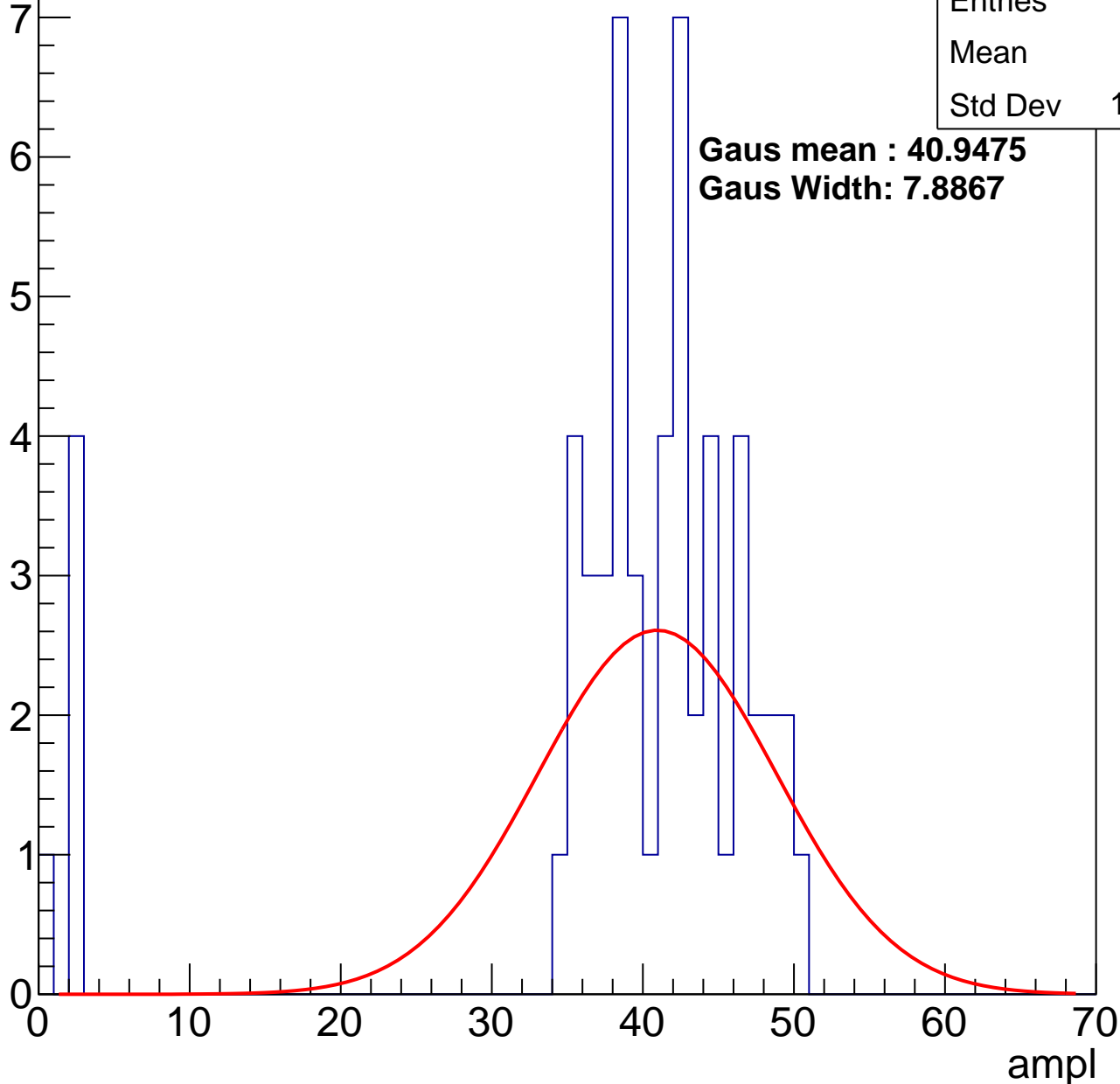
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	37.7
Std Dev	12.02

Gaus mean : 40.9475

Gaus Width: 7.8867

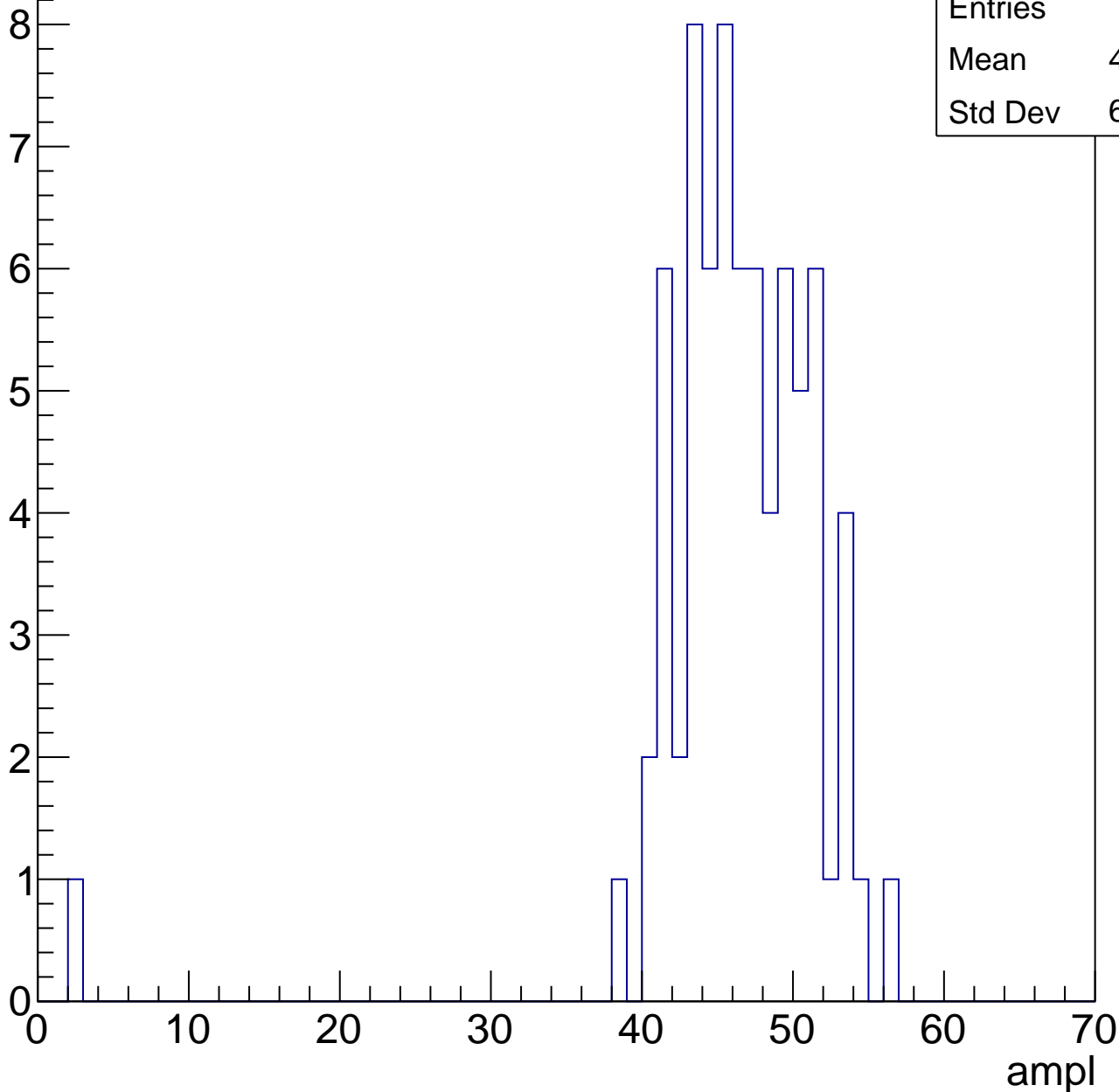


B1L103S, U2-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

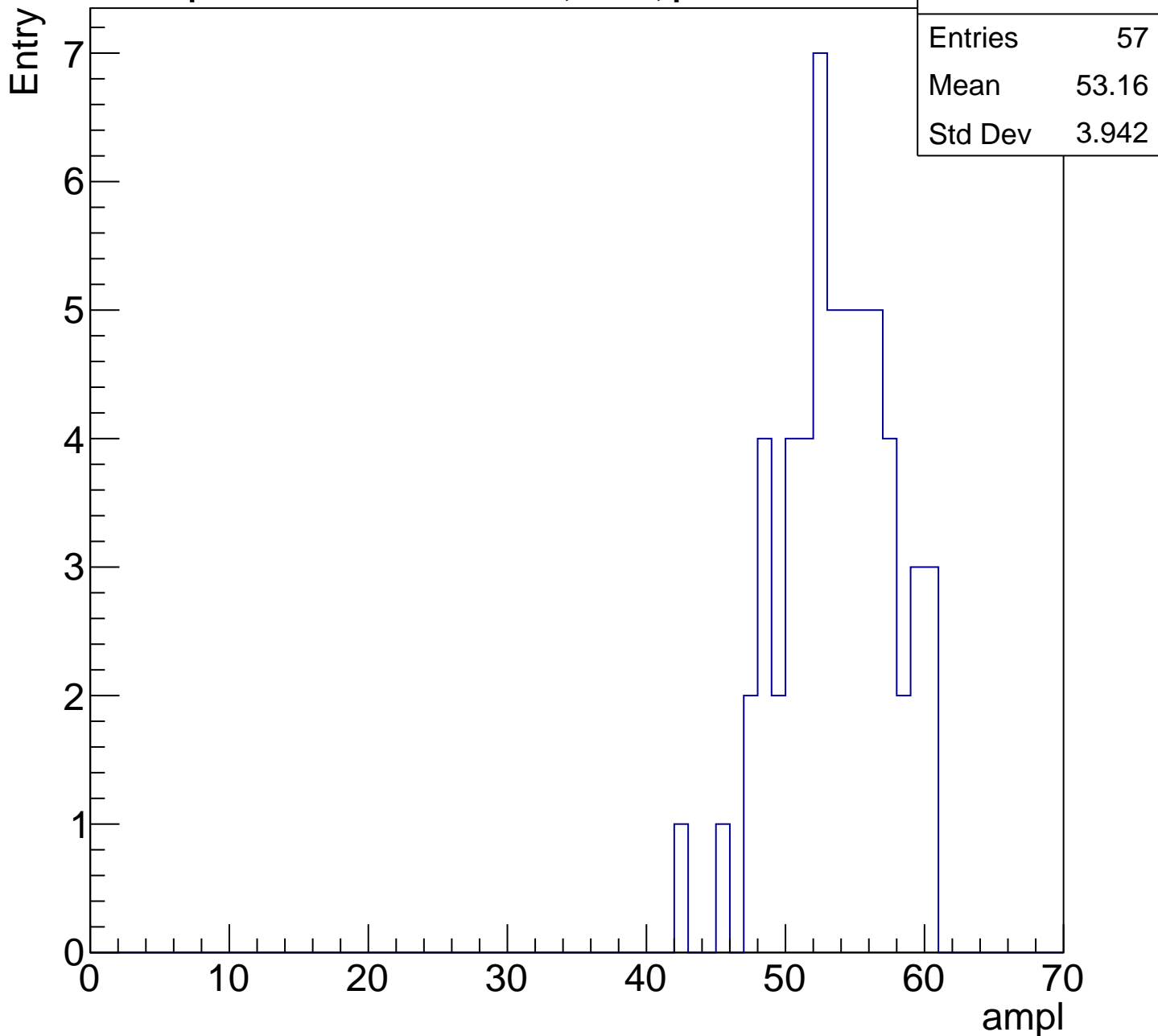
Entry

Entries	74
Mean	45.84
Std Dev	6.428



B1L103S, U2-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 49

Mean 57.94

Std Dev 2.867

2

1

0

1

2

3

4

5

6

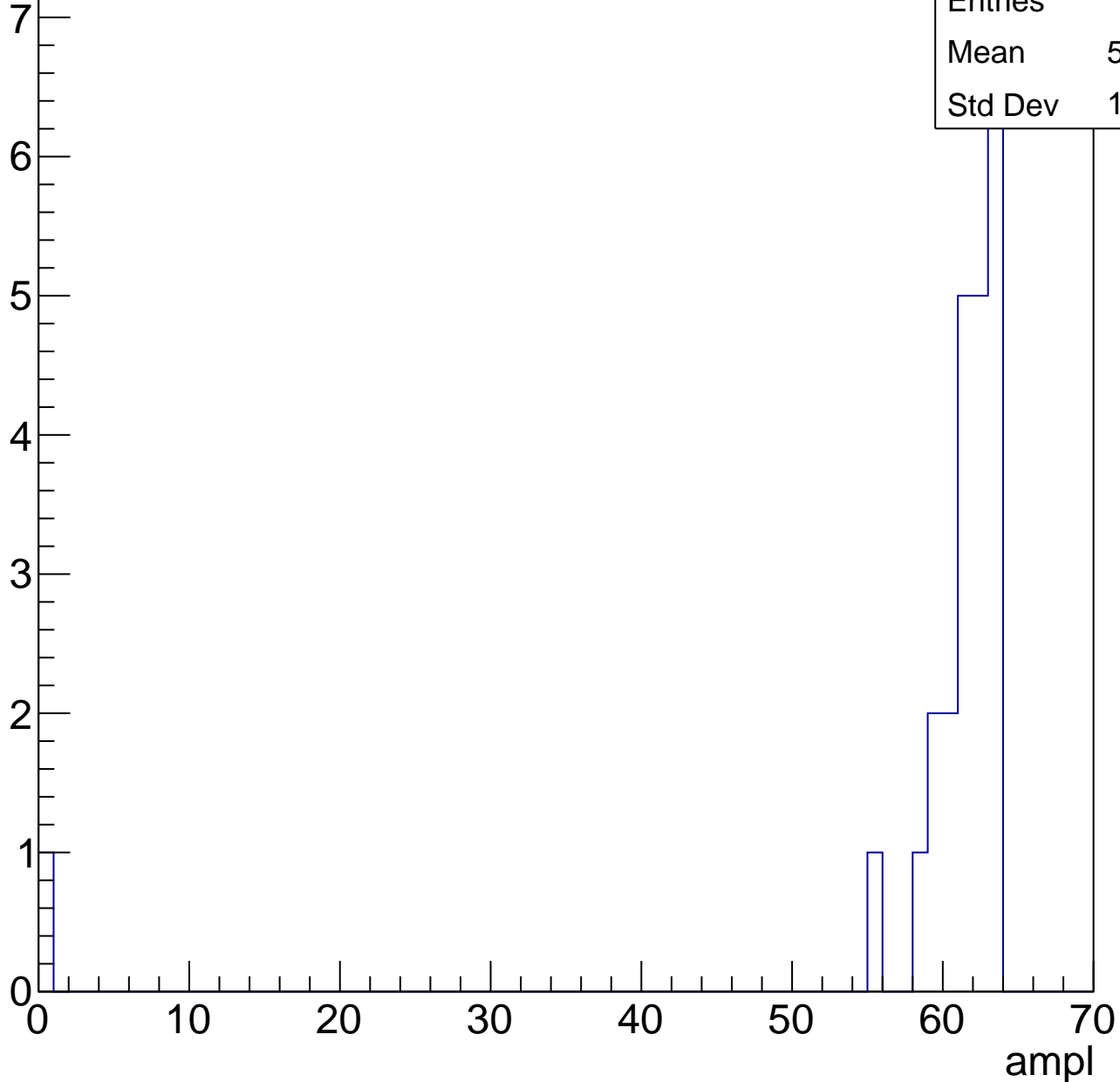
7

B1L103S, U2-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.62
Std Dev	12.37

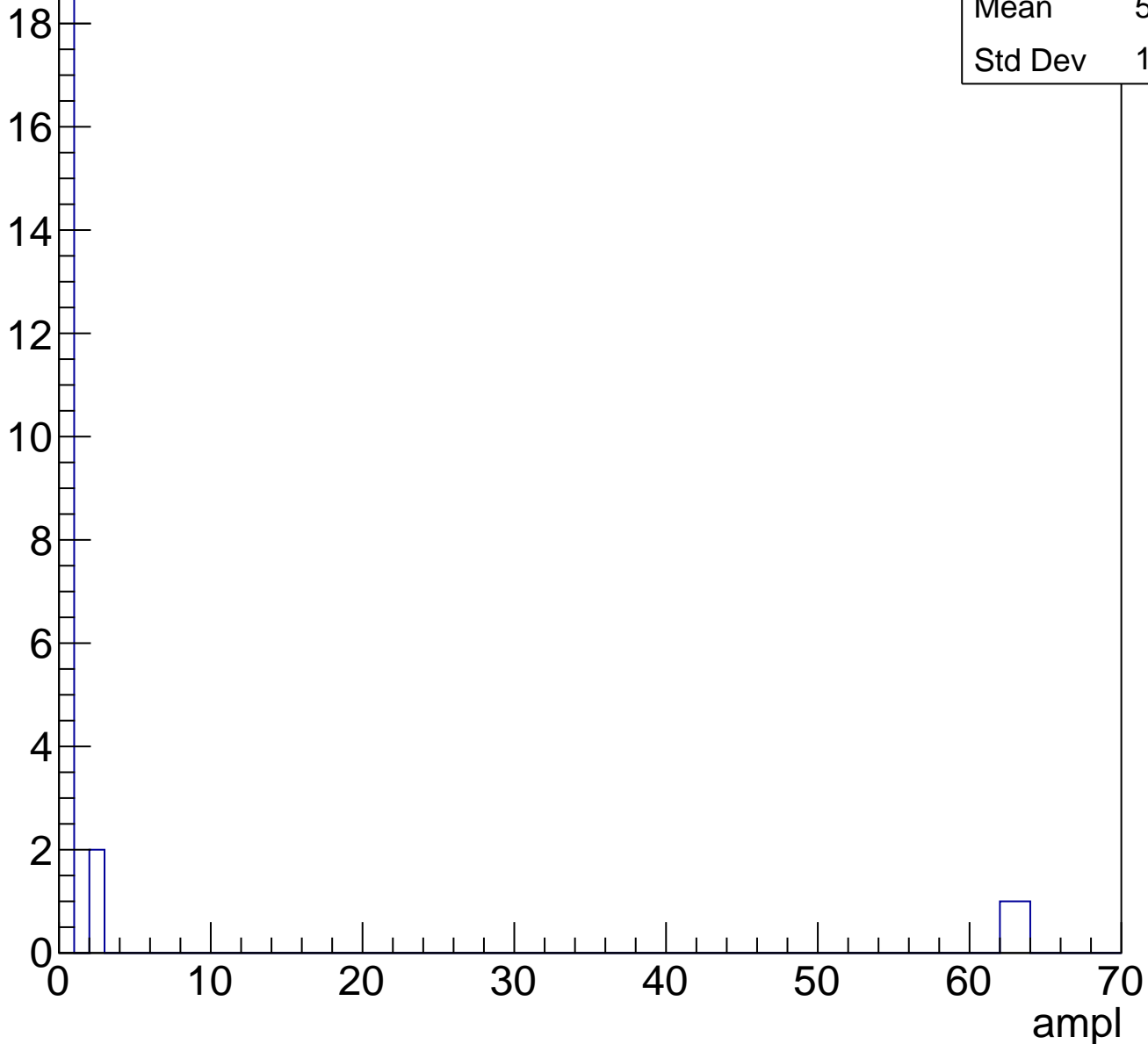


B1L103S, U2-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	5.609
Std Dev	17.57

Entry



B1L103S, U2-ch83, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	19.32
Std Dev	13.79

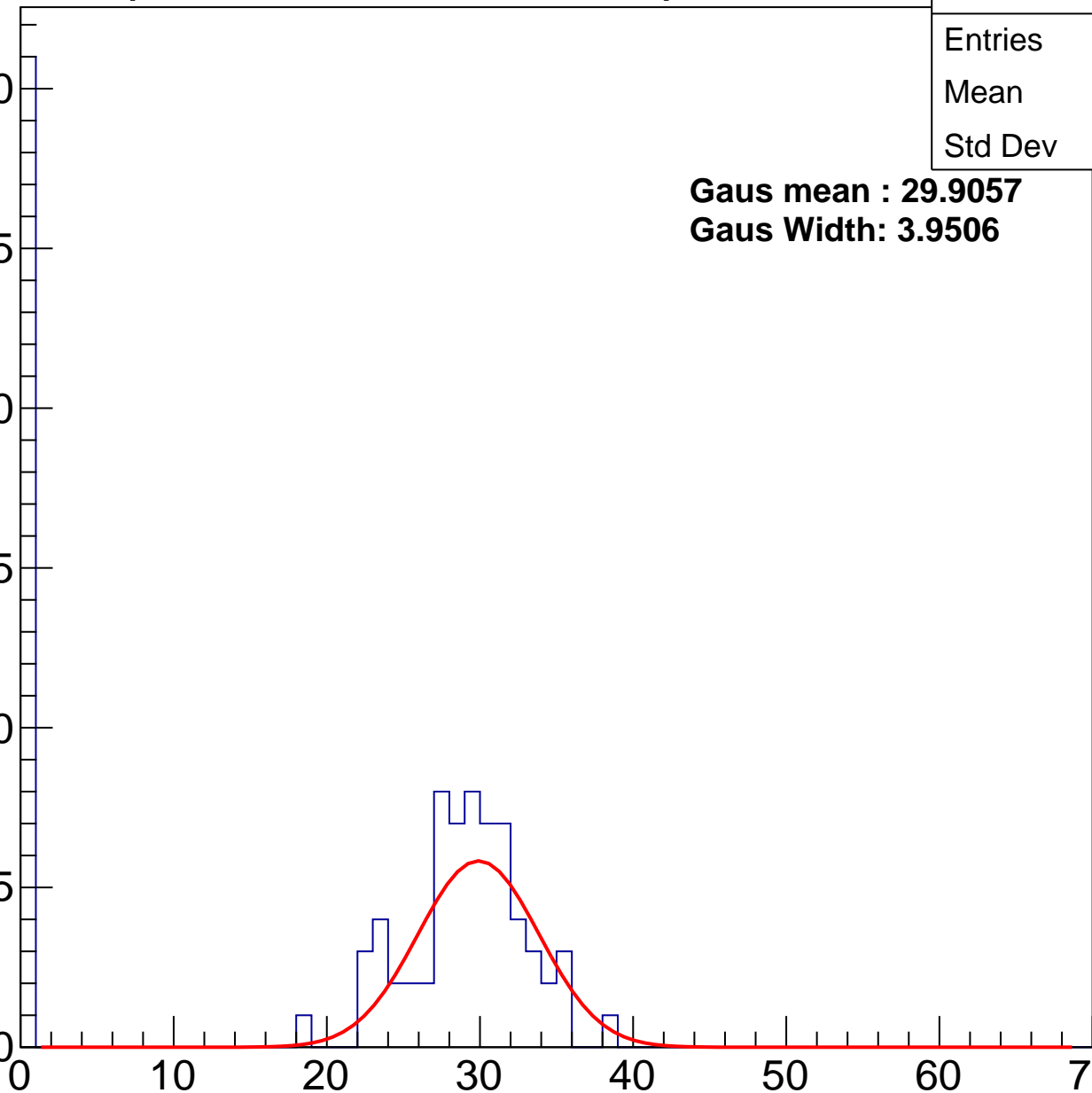
Gaus mean : 29.9057

Gaus Width: 3.9506

Entry

30
25
20
15
10
5
0

ampl



B1L103S, U2-ch83, adc1

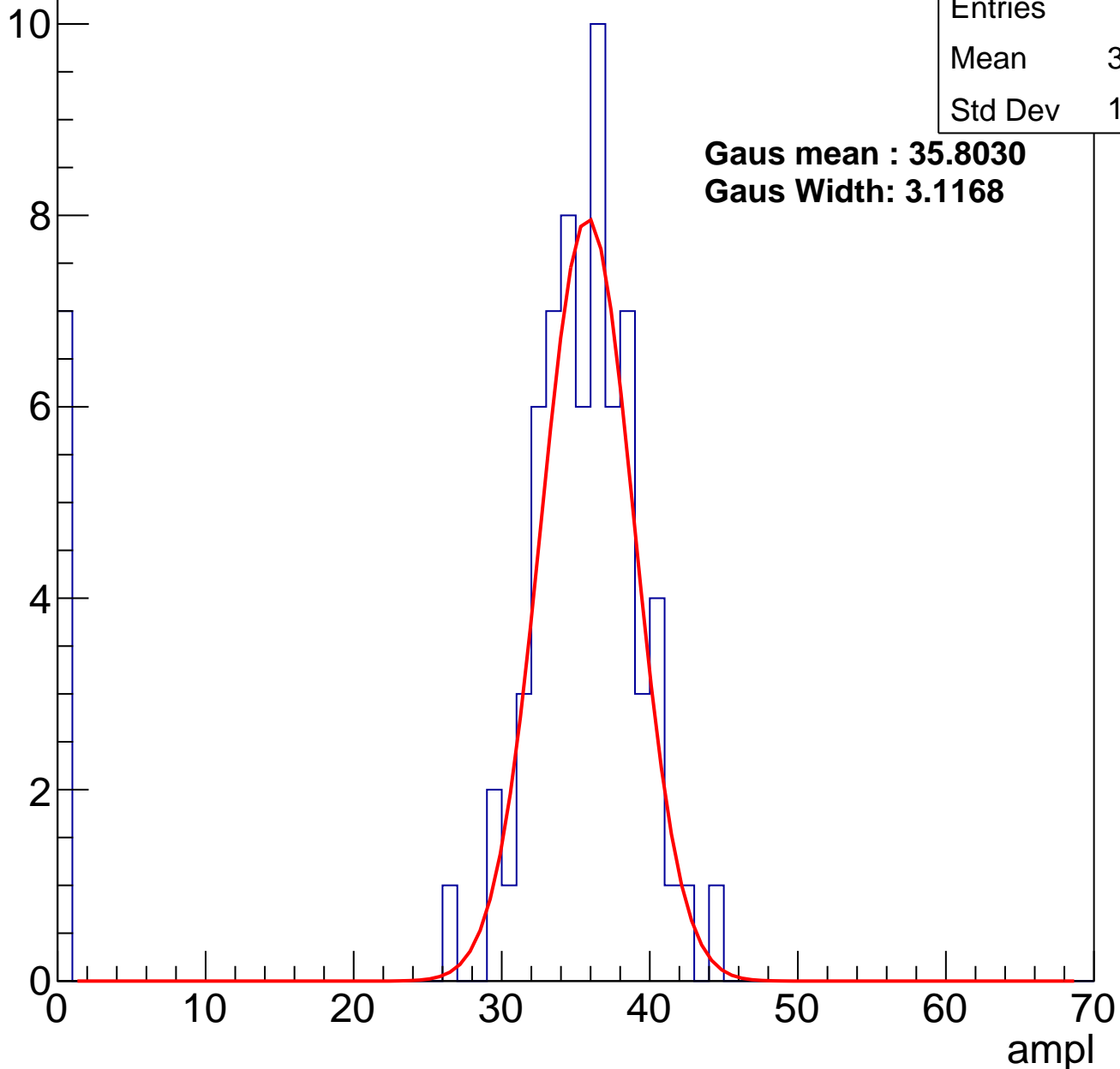
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.95
Std Dev	10.79

Gaus mean : 35.8030

Gaus Width: 3.1168

Entry



B1L103S, U2-ch83, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	64
Mean	34.33
Std Dev	15.83

Gaus mean : 41.8898

Gaus Width: 2.8720

0

2

4

6

8

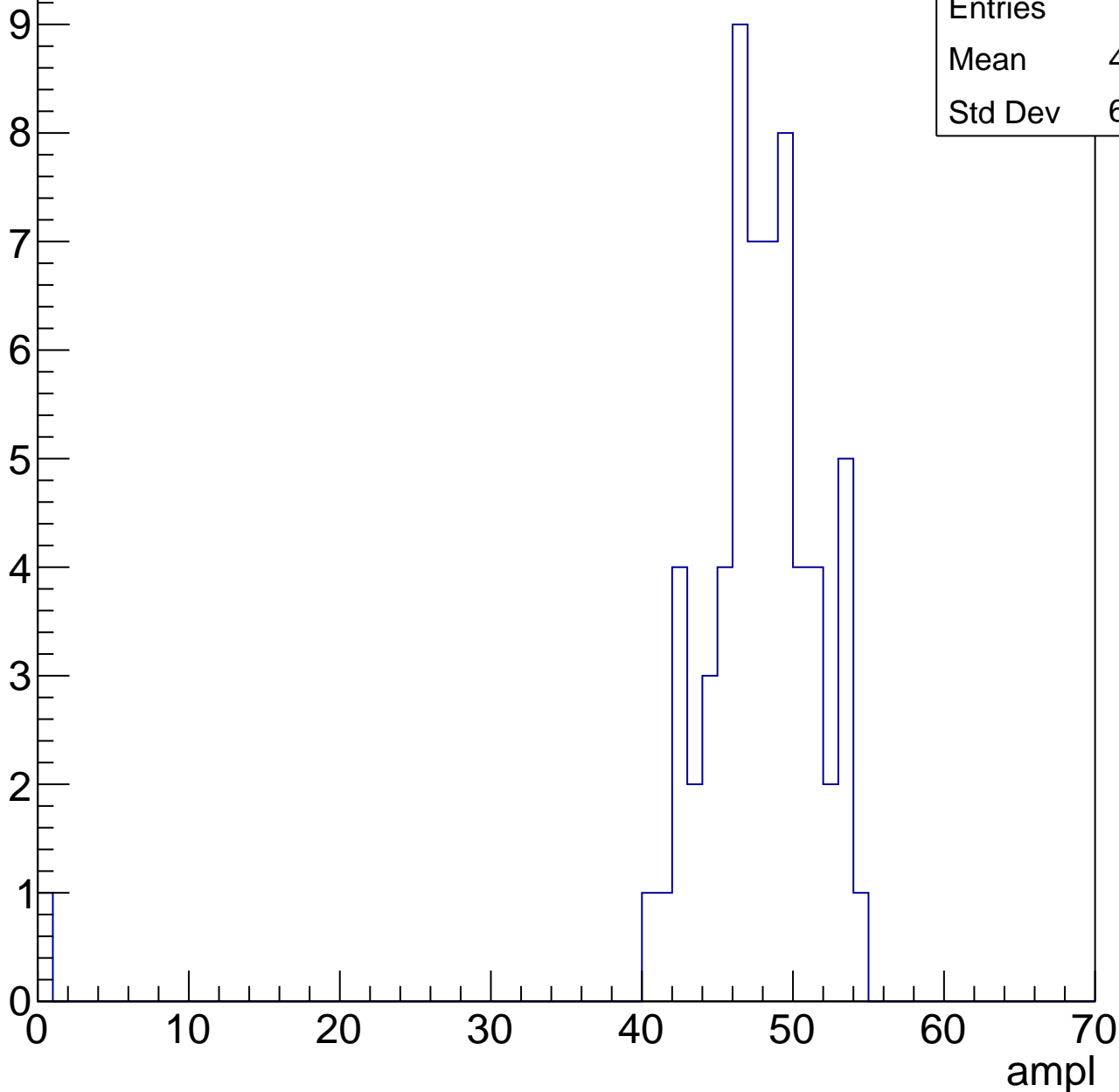
10

B1L103S, U2-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

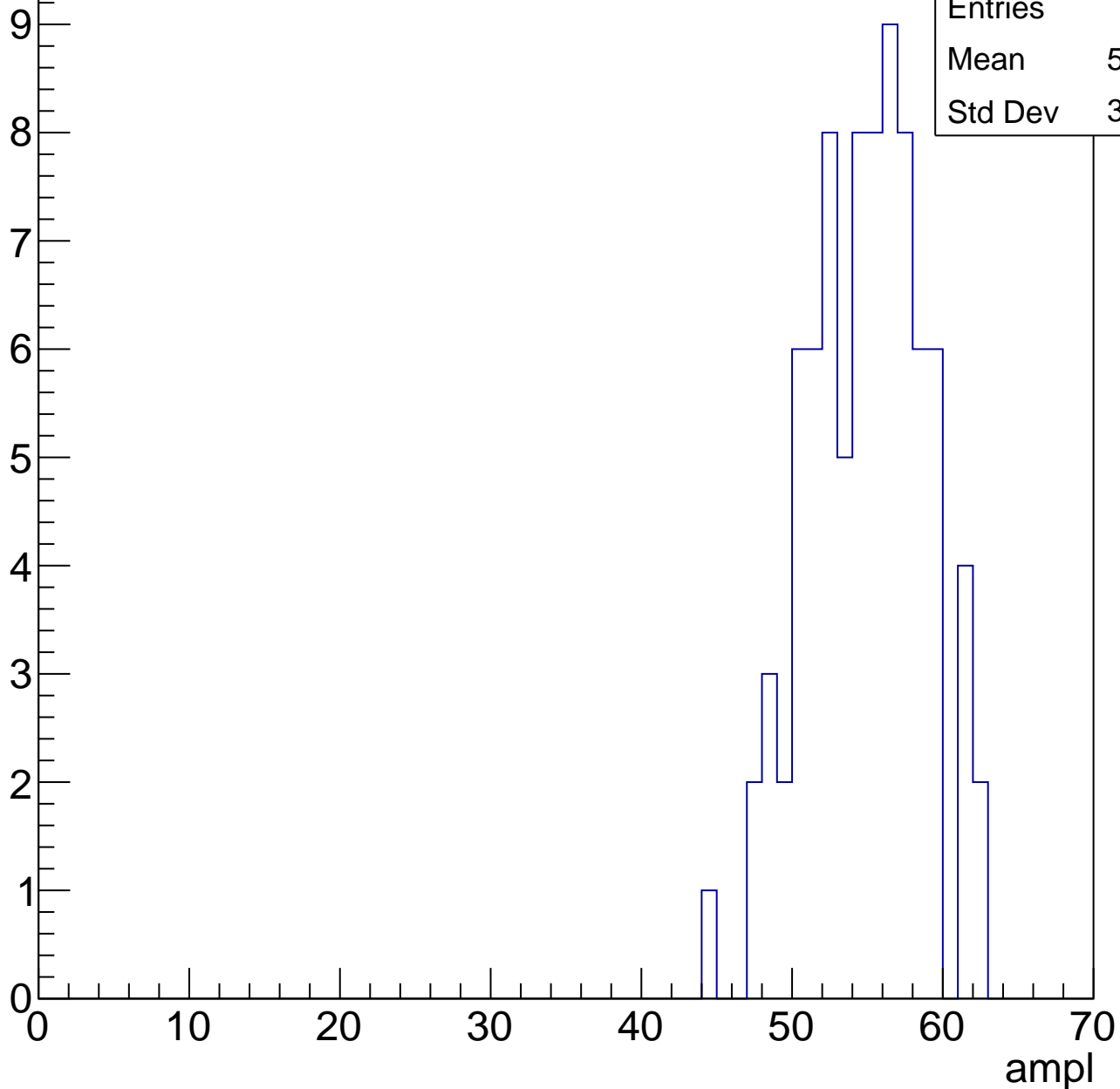
Entries	63
Mean	46.75
Std Dev	6.775



B1L103S, U2-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

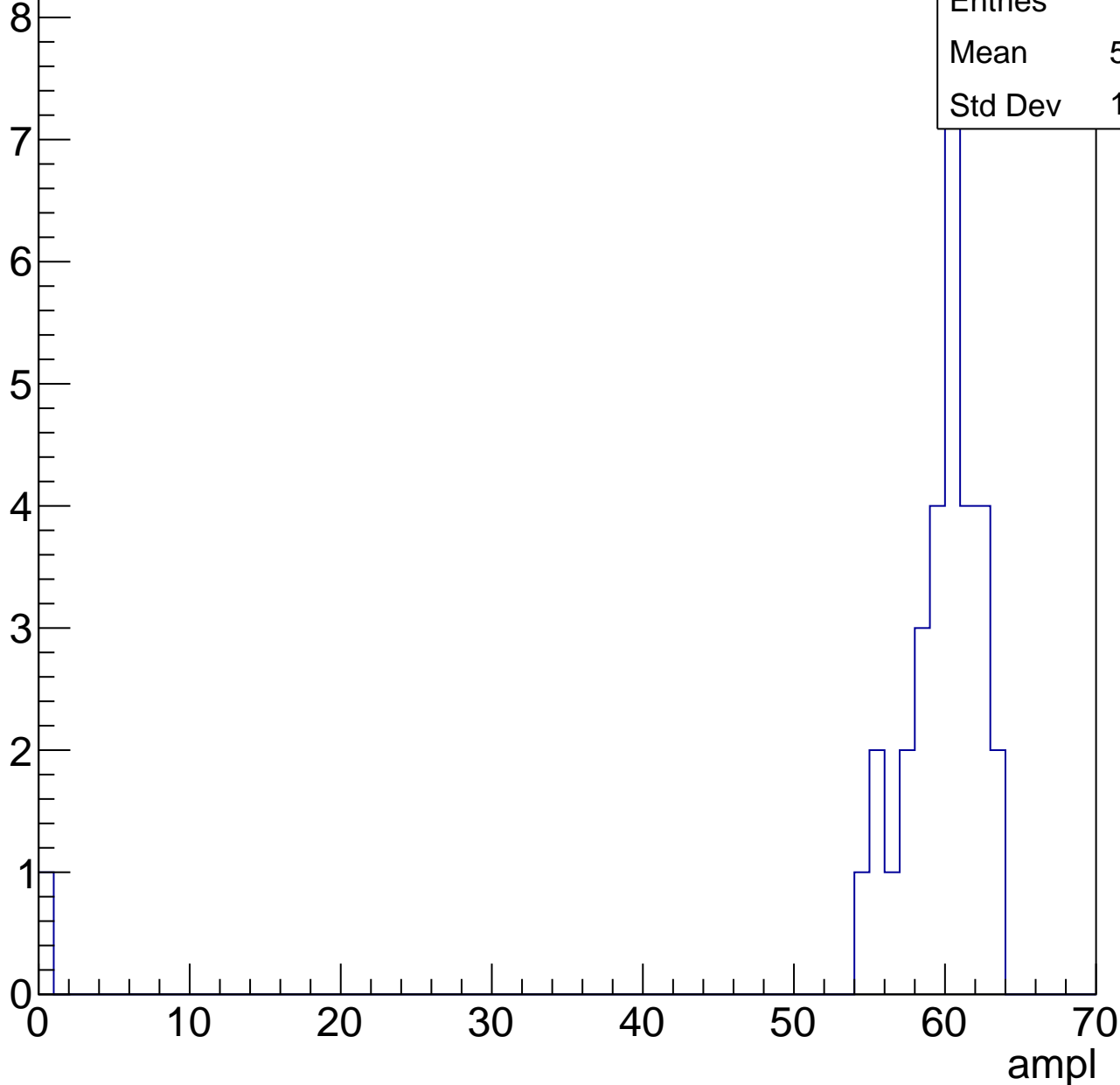


B1L103S, U2-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	57.56
Std Dev	10.58

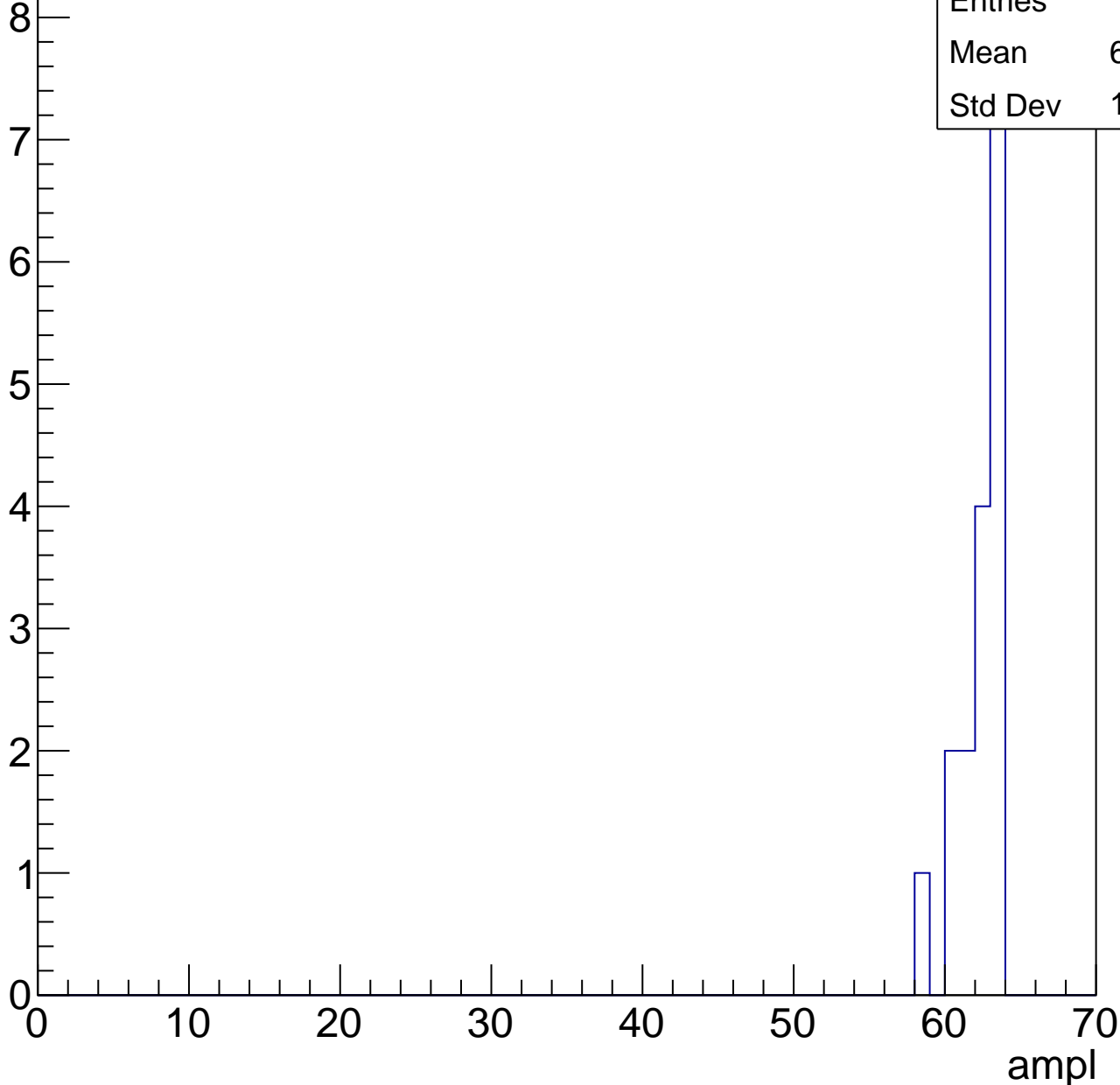


B1L103S, U2-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.88
Std Dev	1.409



B1L103S, U2-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

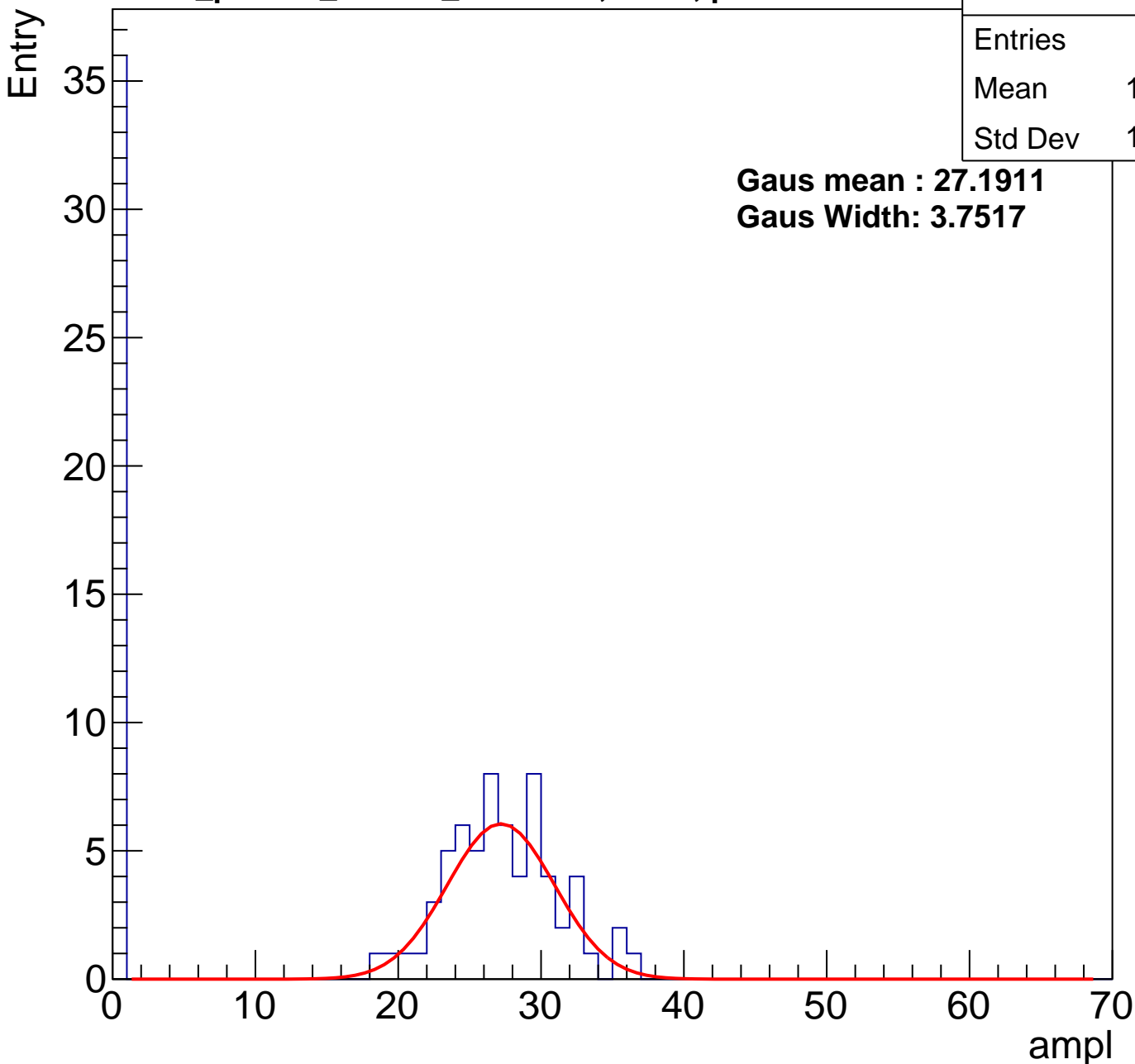
B1L103S, U2-ch84, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	17.08
Std Dev	13.26

Gaus mean : 27.1911

Gaus Width: 3.7517



B1L103S, U2-ch84, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	26.93
Std Dev	12.7

Gaus mean : 33.2508

Gaus Width: 3.8097

Entry

12

10

8

6

4

2

0

0

10

20

30

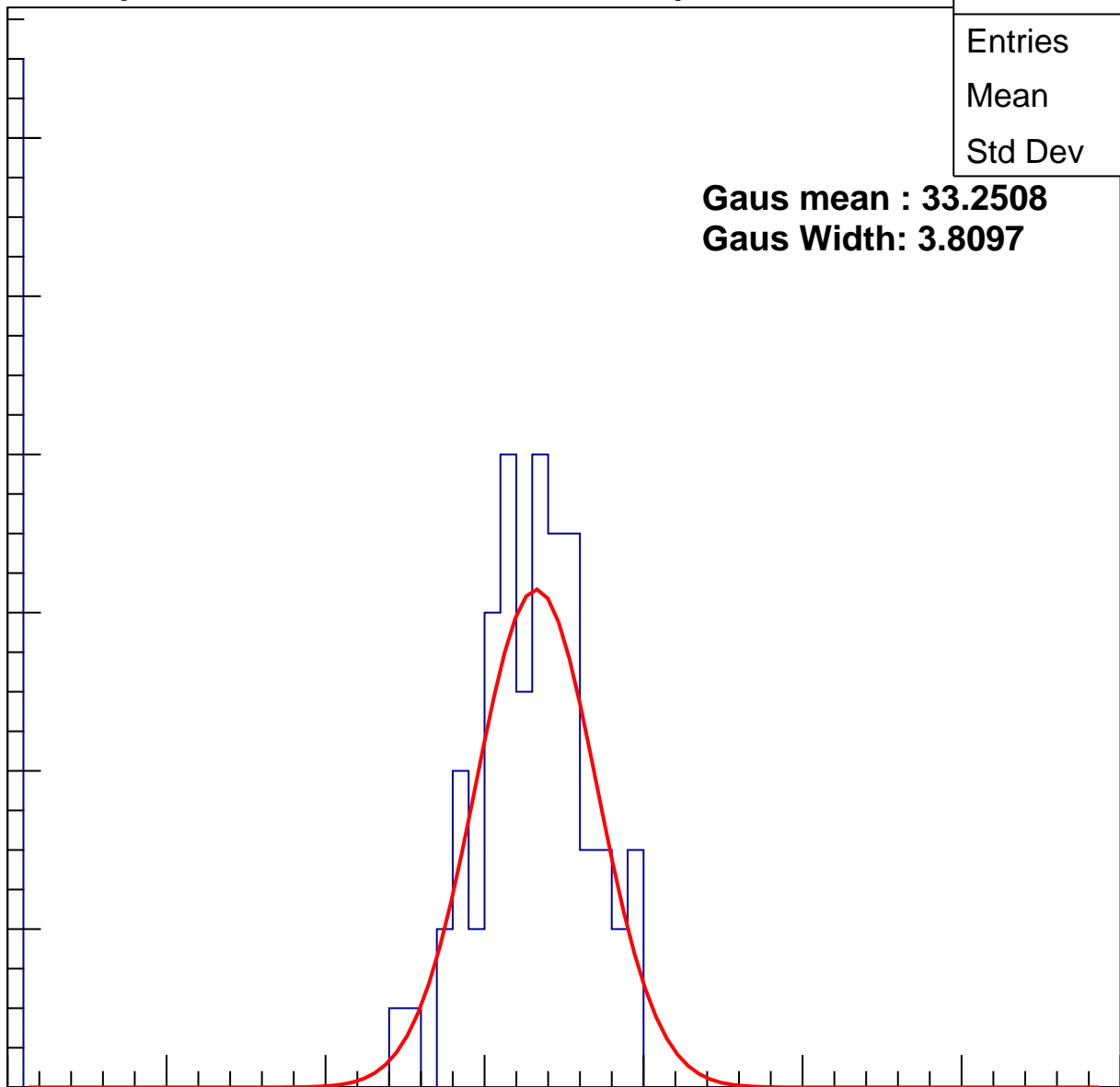
40

50

60

70

ampl



B1L103S, U2-ch84, adc2

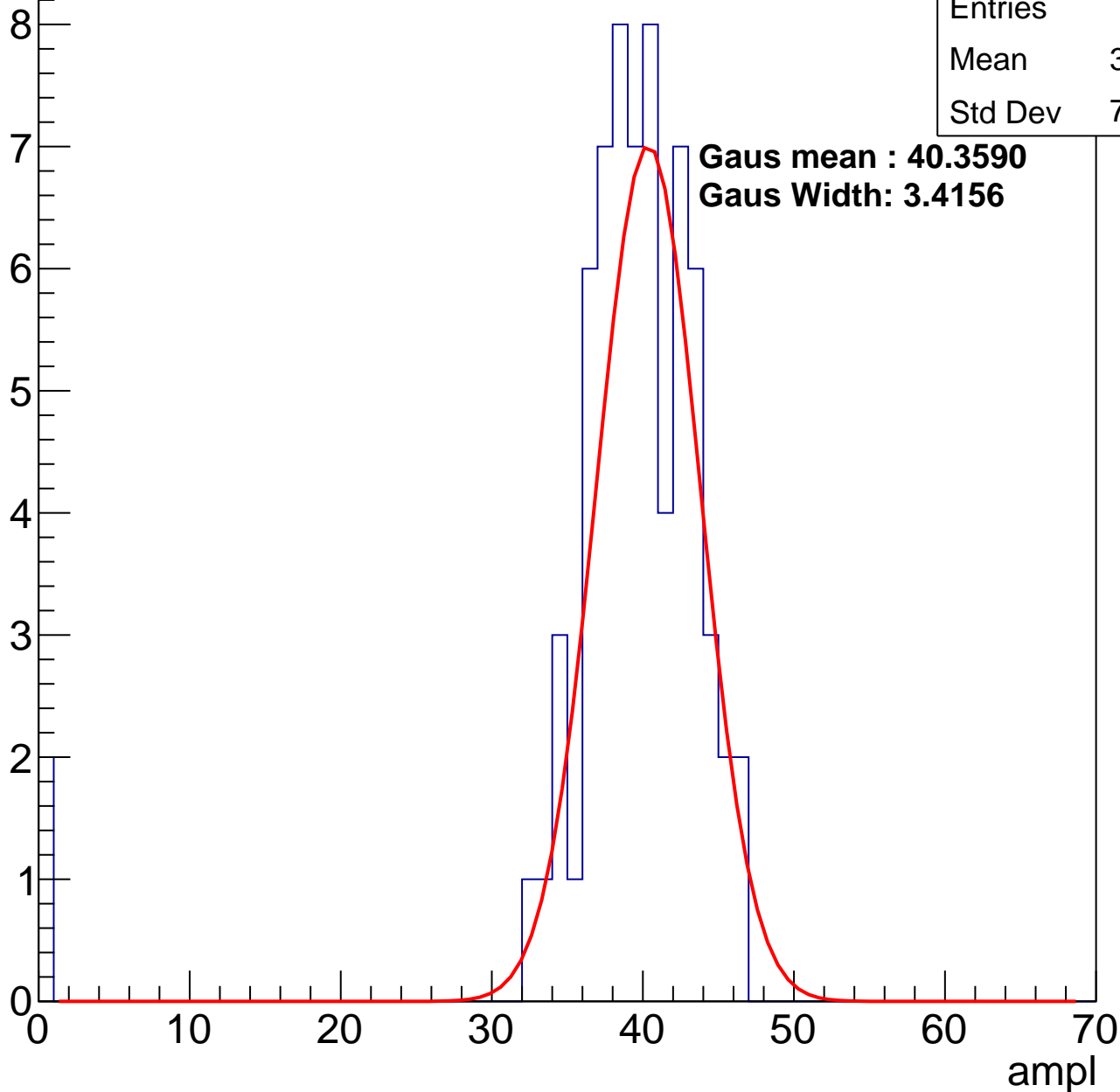
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.29
Std Dev	7.379

Gaus mean : 40.3590

Gaus Width: 3.4156

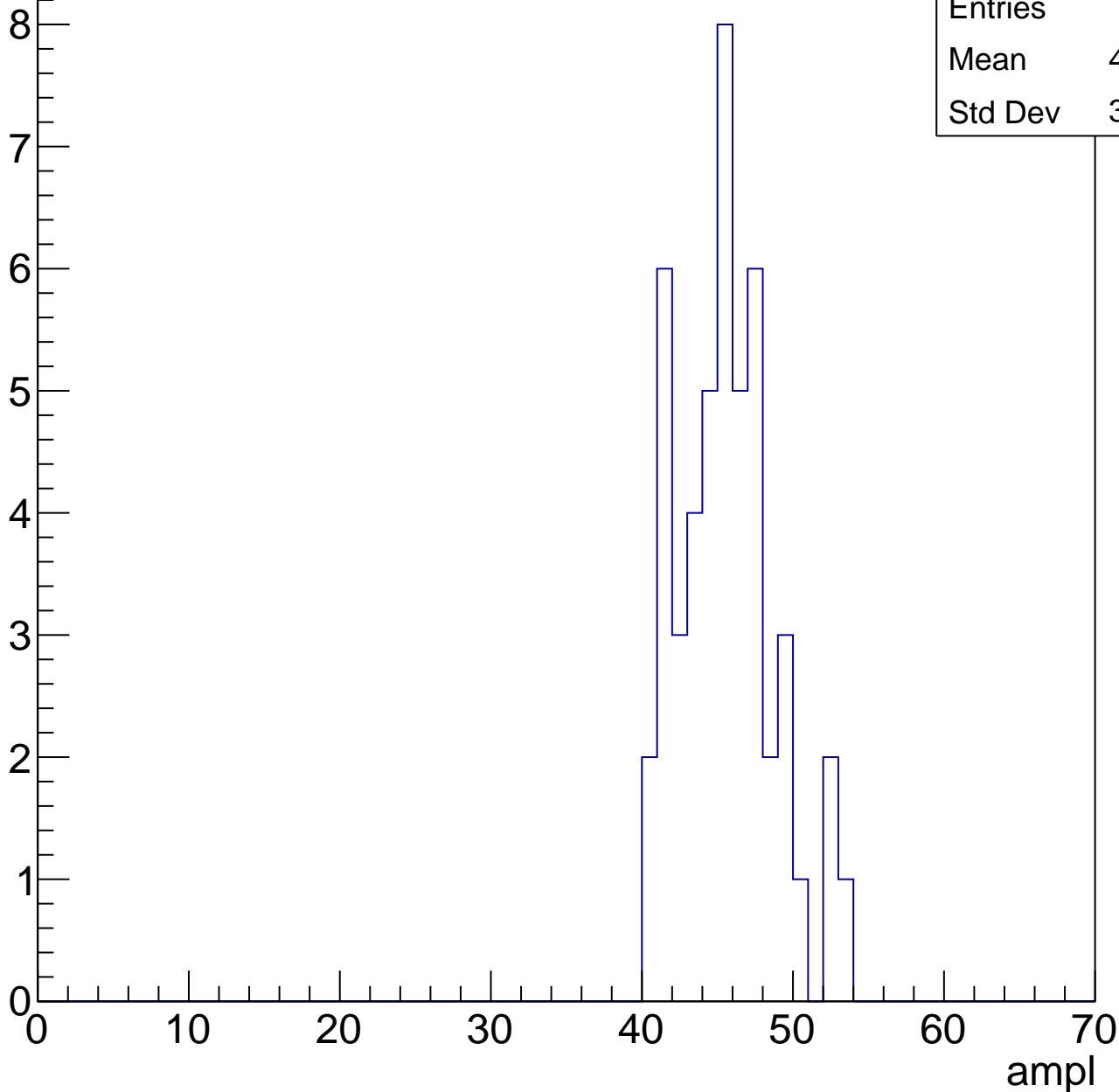


B1L103S, U2-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	45.12
Std Dev	3.147



B1L103S, U2-ch84, adc4

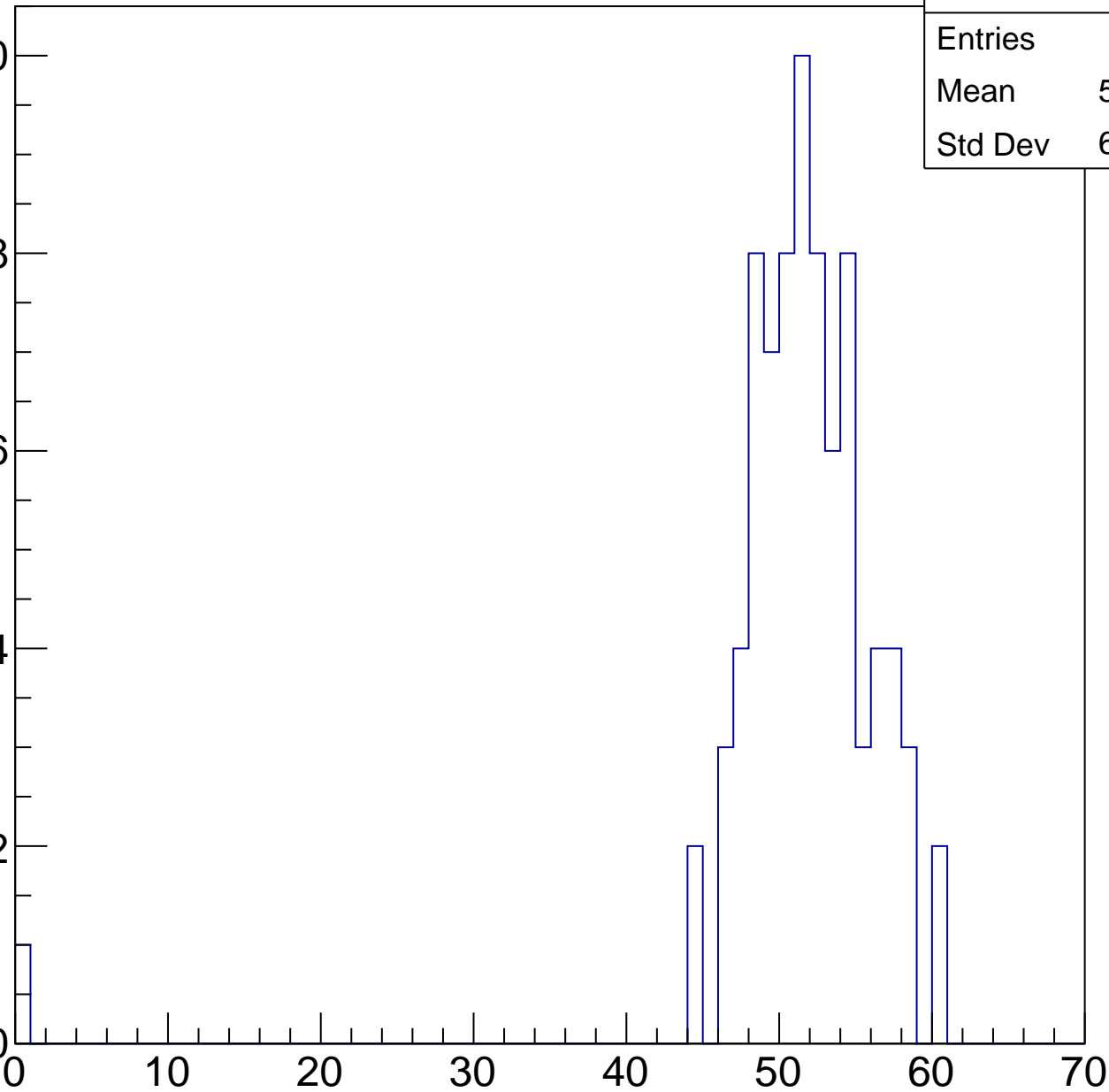
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	50.96
Std Dev	6.714

Entry

10
8
6
4
2
0

ampl

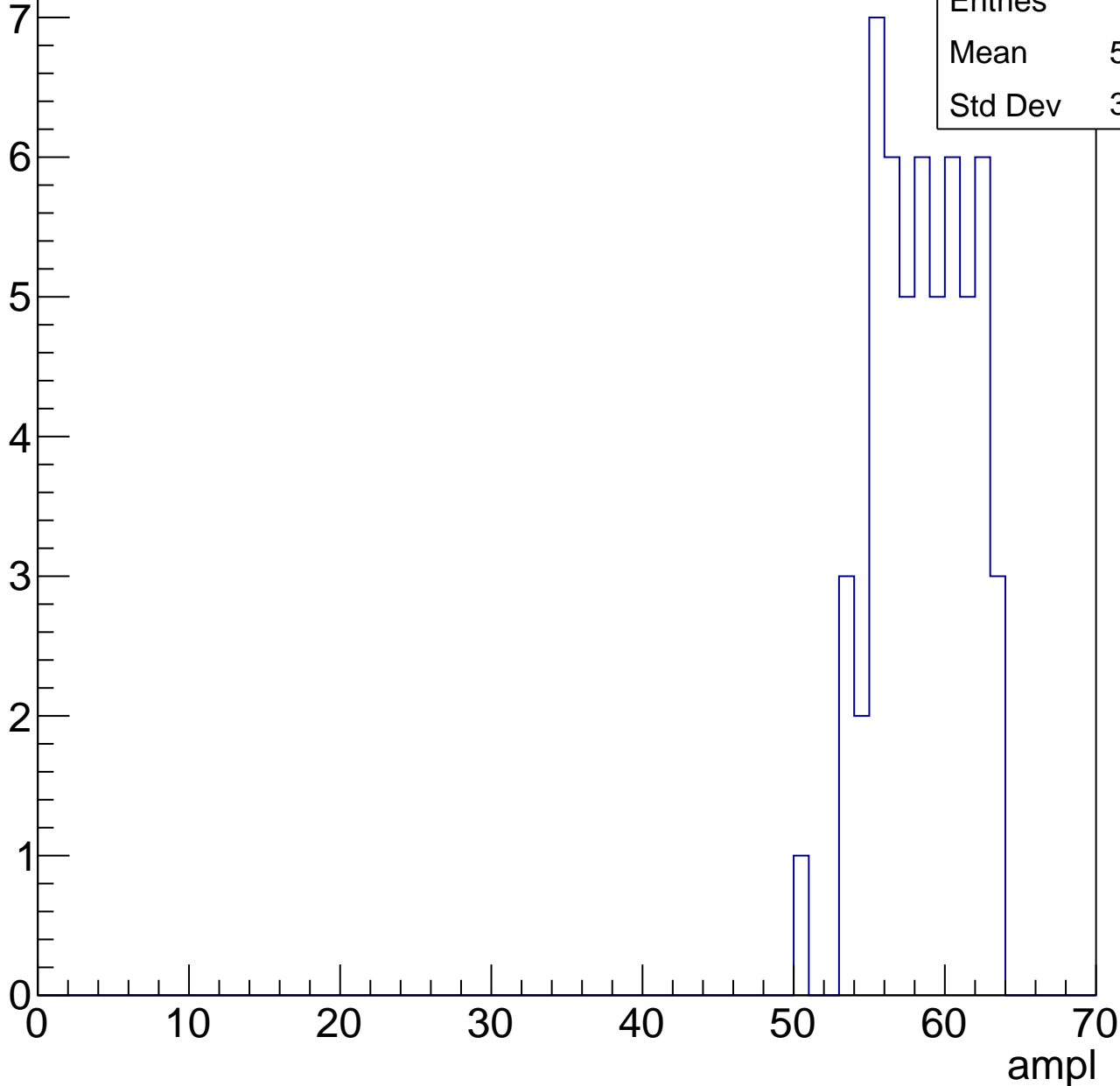


B1L103S, U2-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	58.04
Std Dev	3.039

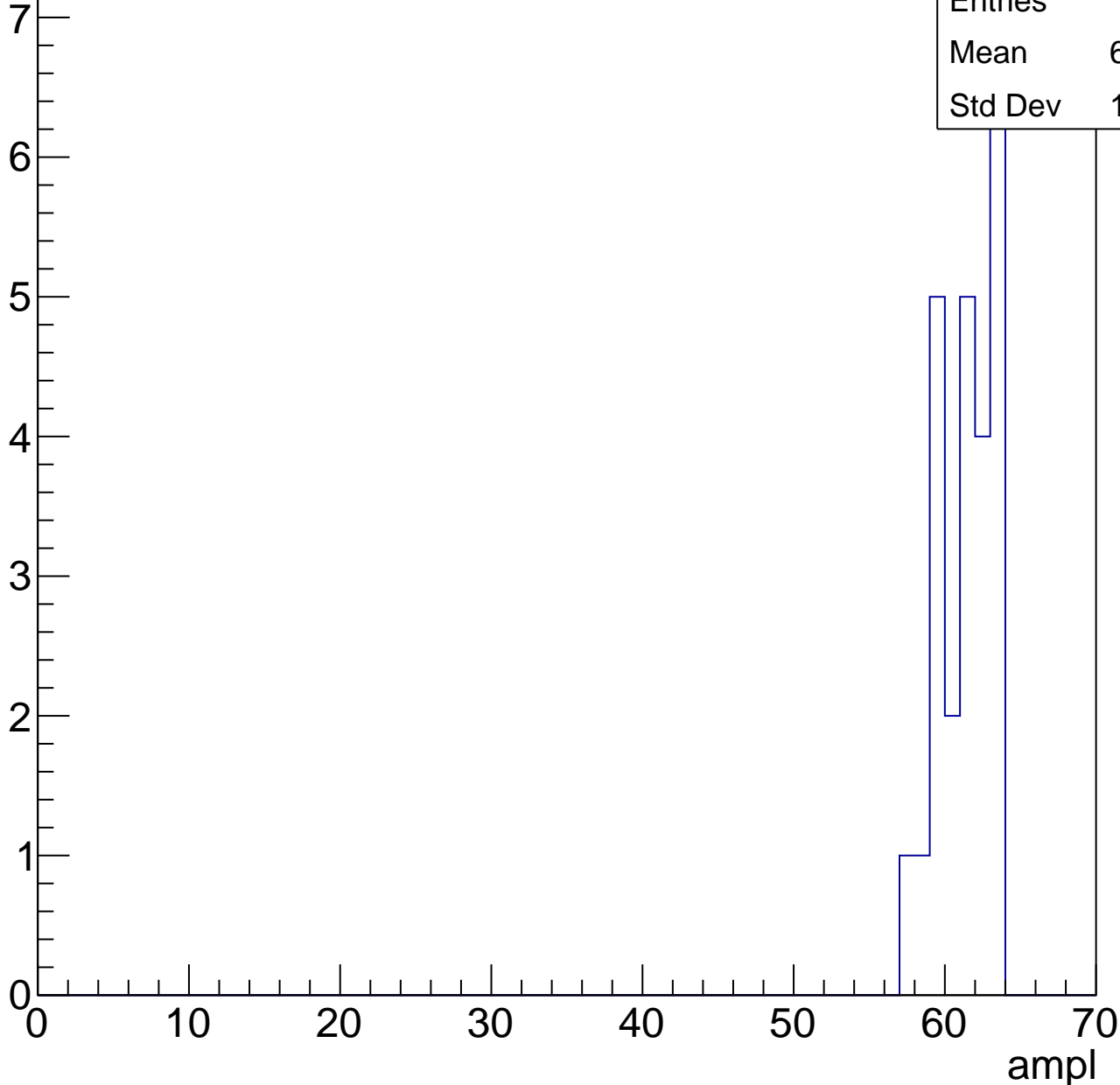


B1L103S, U2-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	60.96
Std Dev	1.777



B1L103S, U2-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch85, adc0

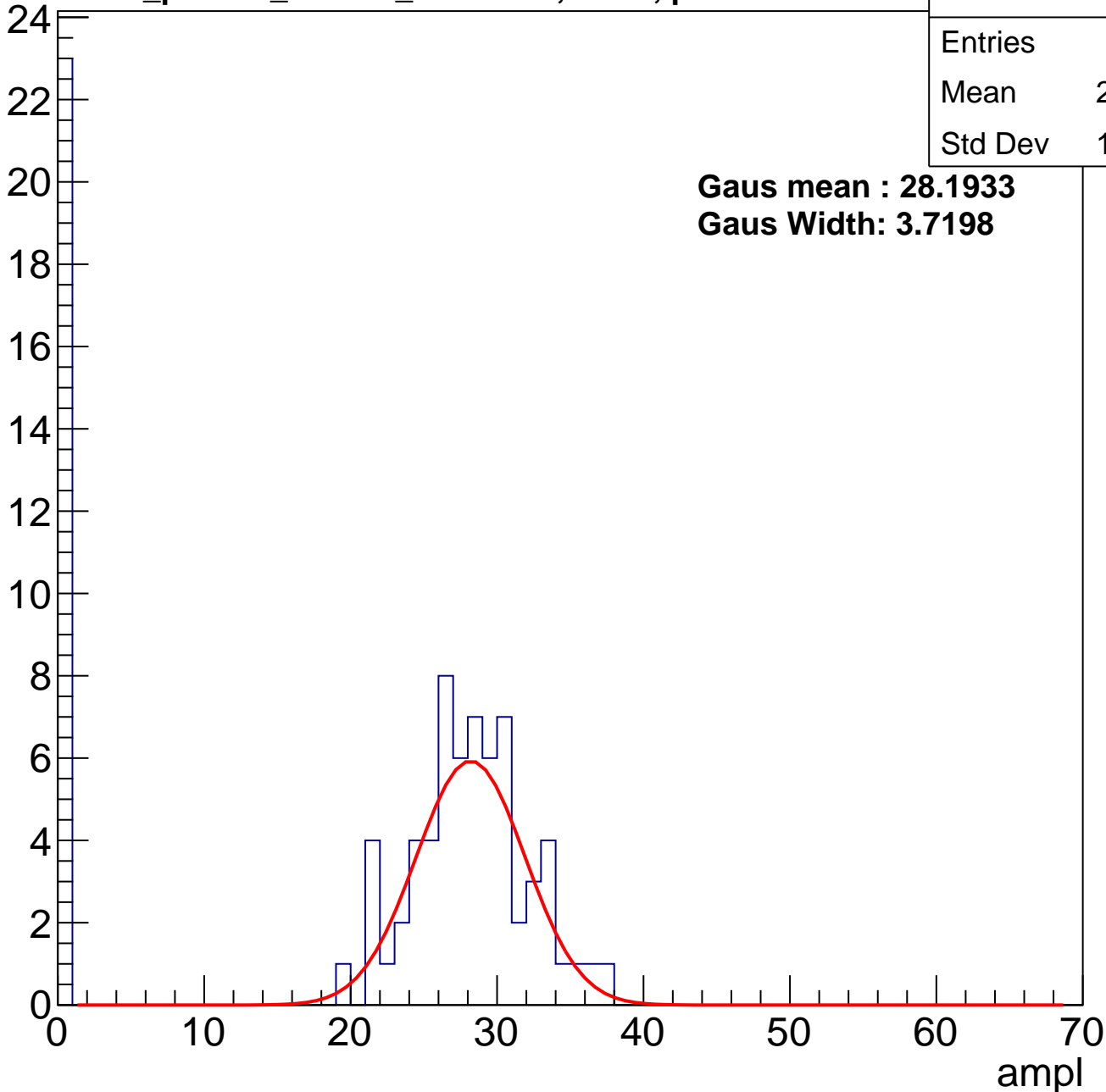
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	20.34
Std Dev	12.72

Gaus mean : 28.1933

Gaus Width: 3.7198

Entry



B1L103S, U2-ch85, adc1

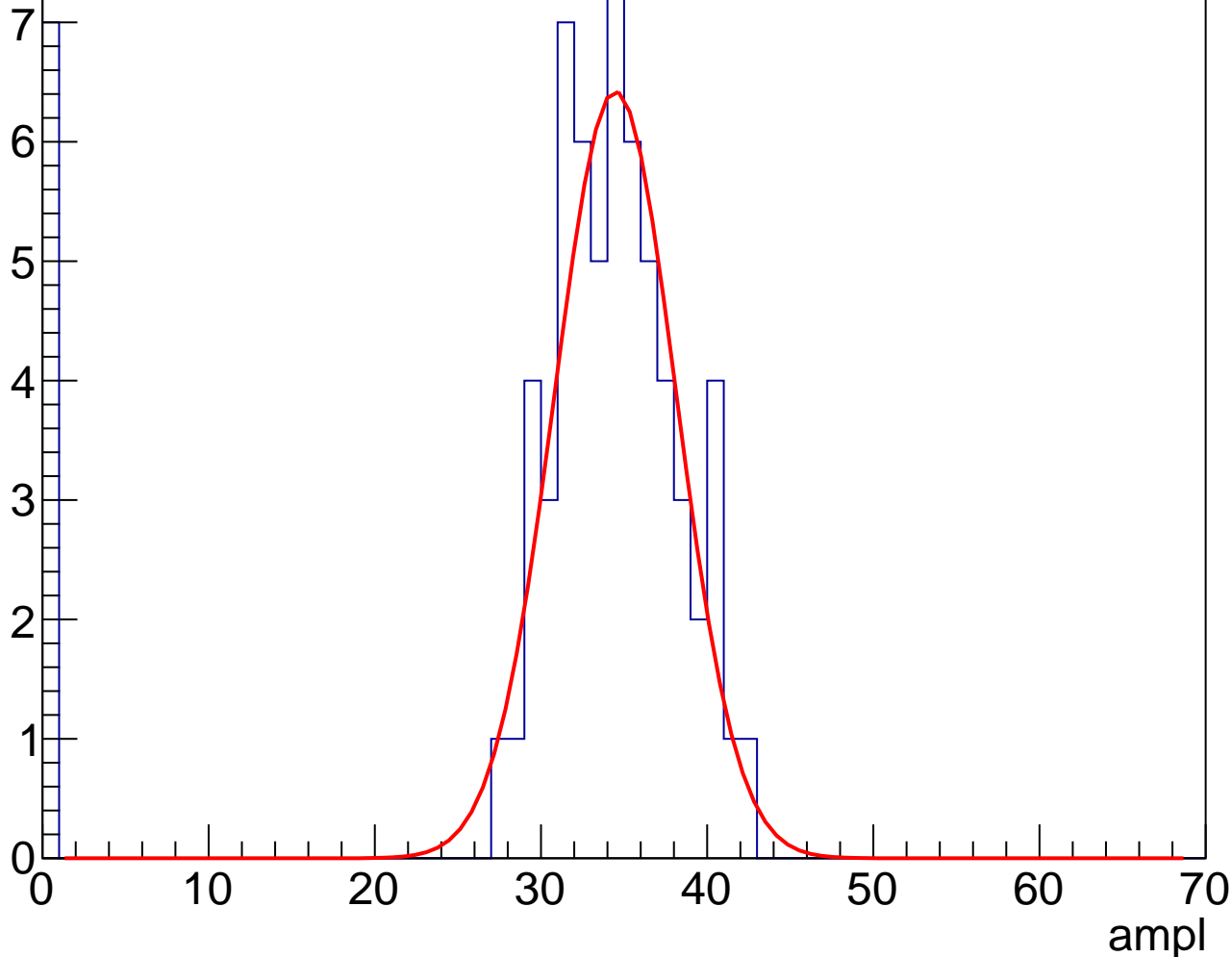
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	30.64
Std Dev	10.8

Gaus mean : 34.4793

Gaus Width: 3.6542



B1L103S, U2-ch85, adc2

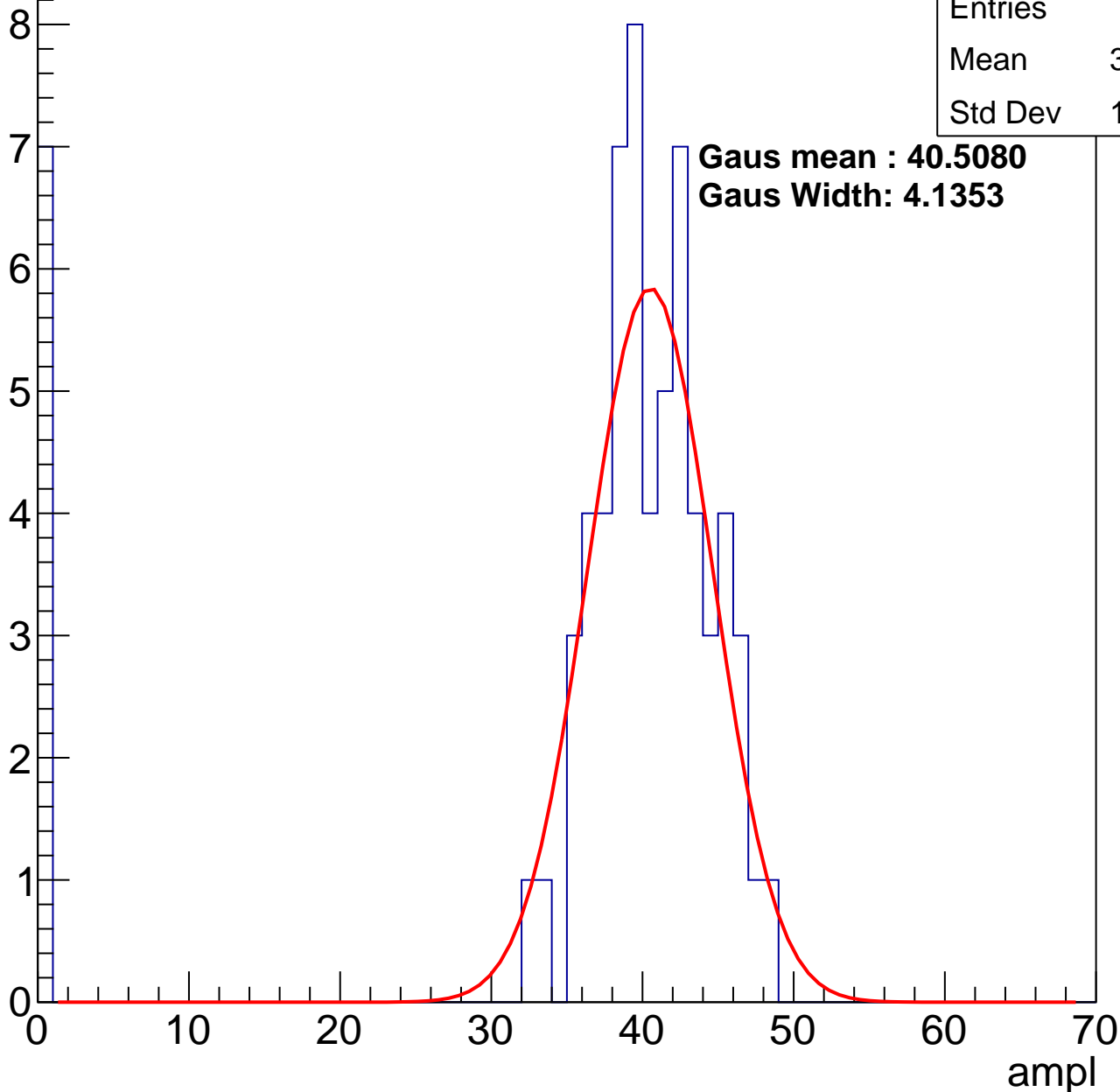
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	36.06
Std Dev	12.77

Gaus mean : 40.5080

Gaus Width: 4.1353

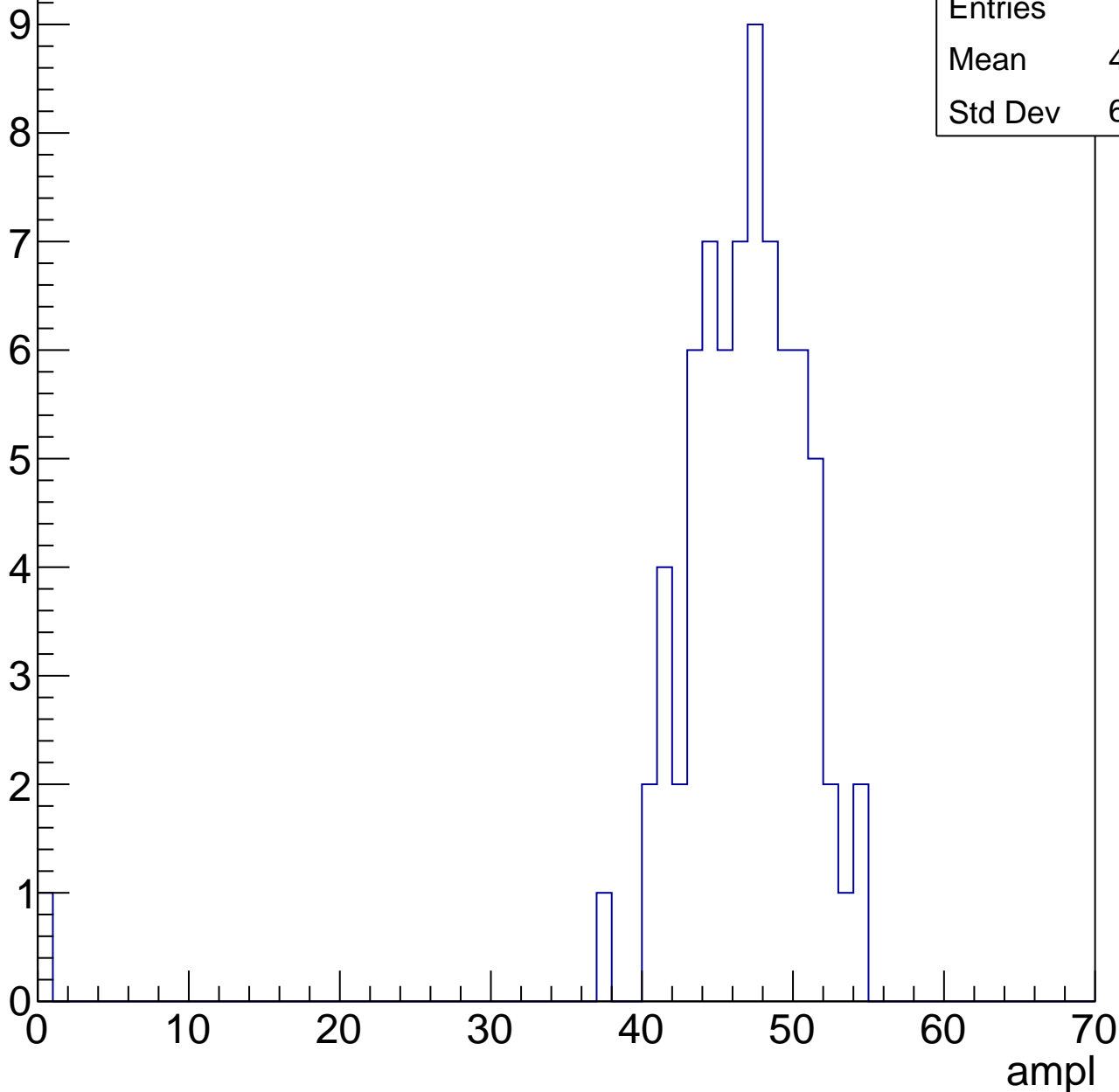


B1L103S, U2-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	45.89
Std Dev	6.419

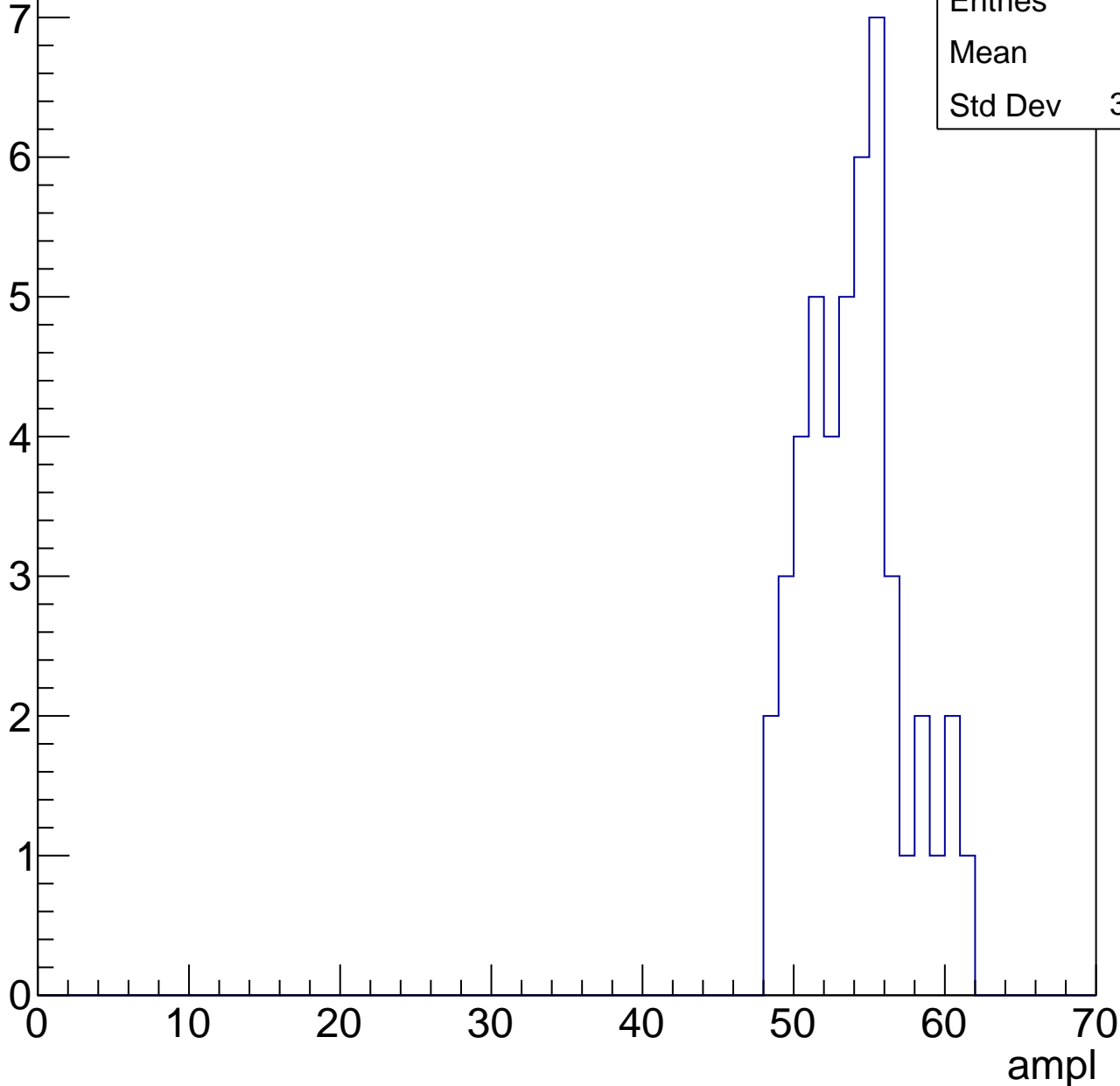


B1L103S, U2-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	53.5
Std Dev	3.202

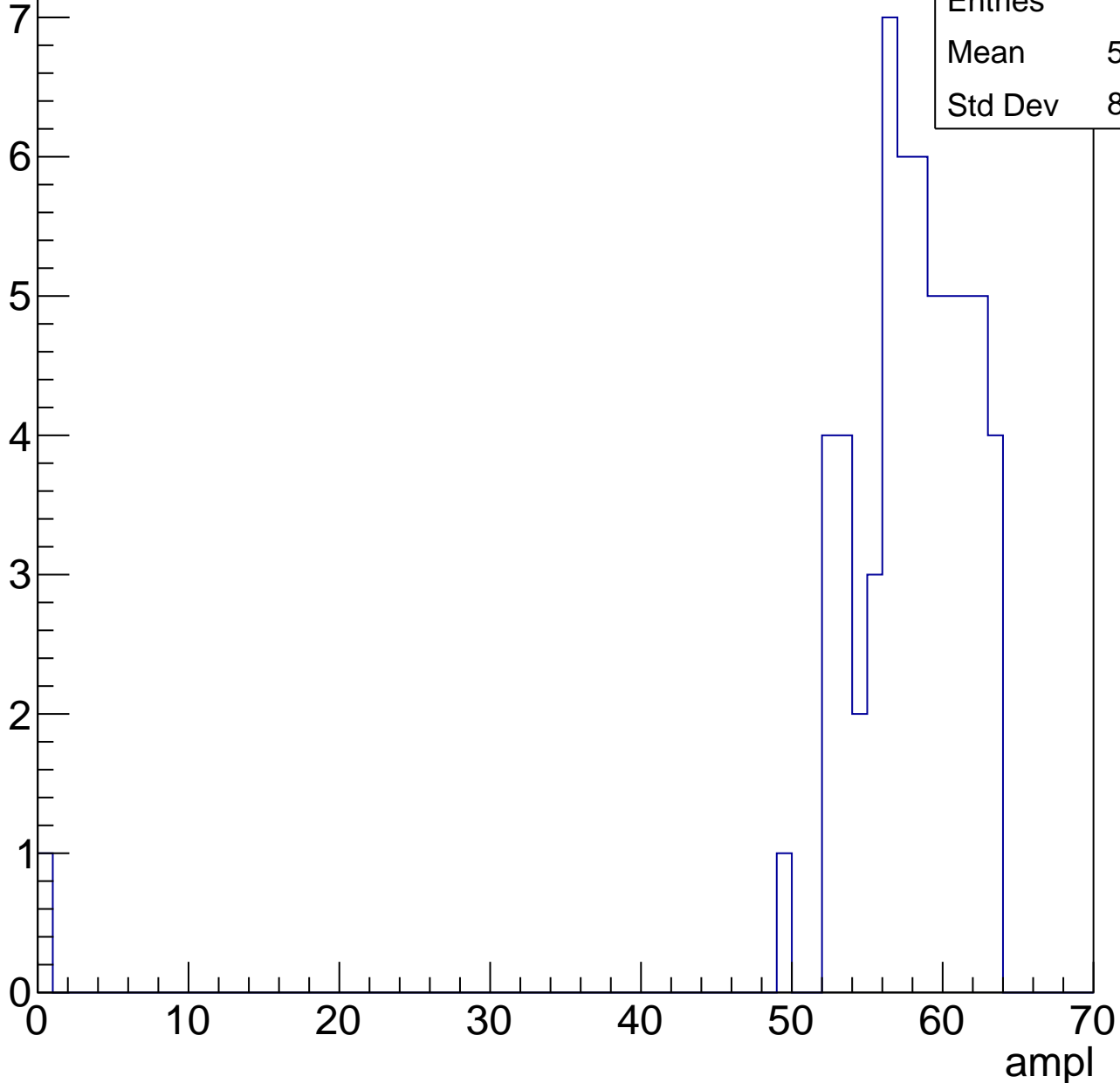


B1L103S, U2-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.66
Std Dev	8.229

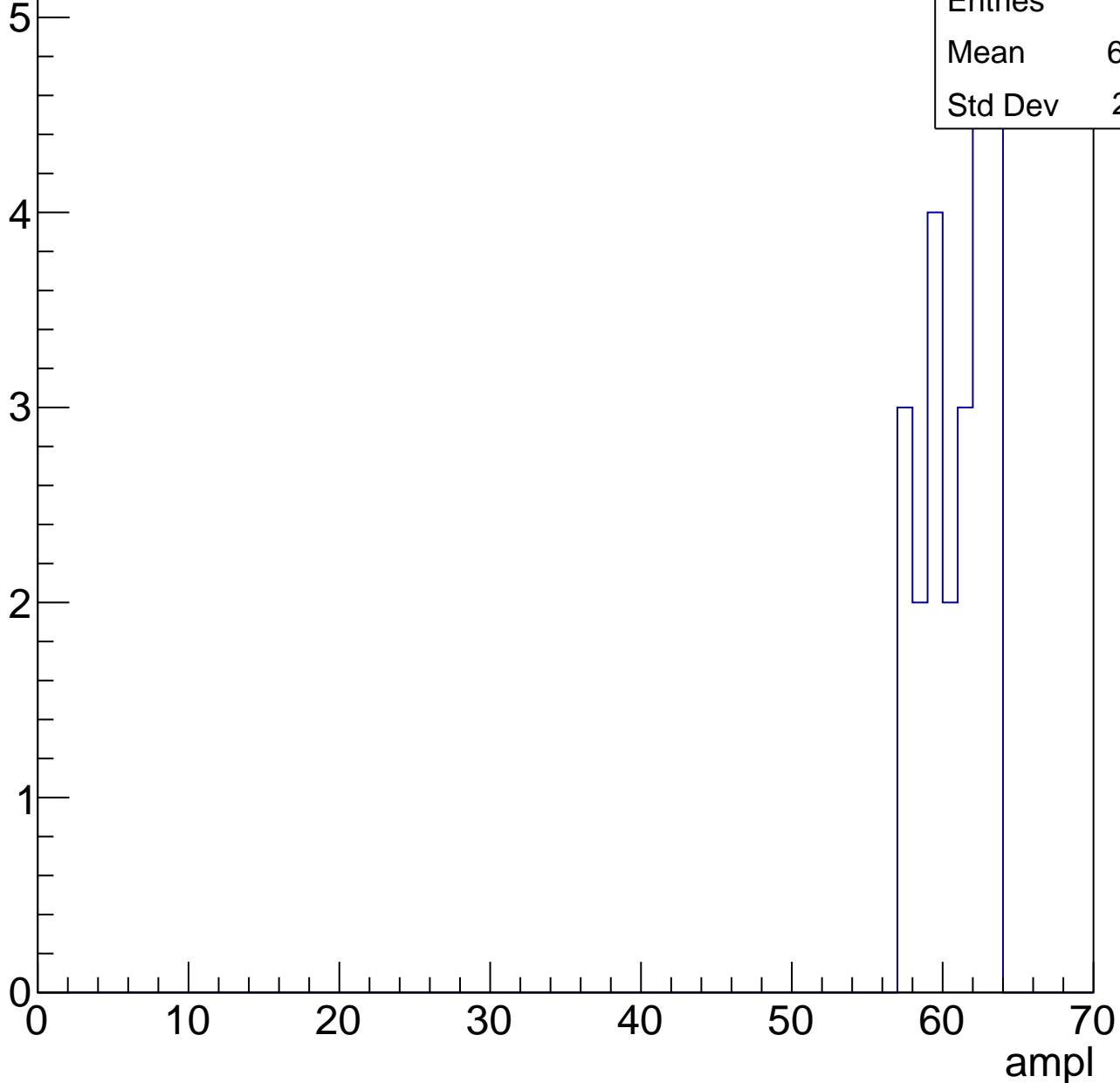


B1L103S, U2-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	60.46
Std Dev	2.061

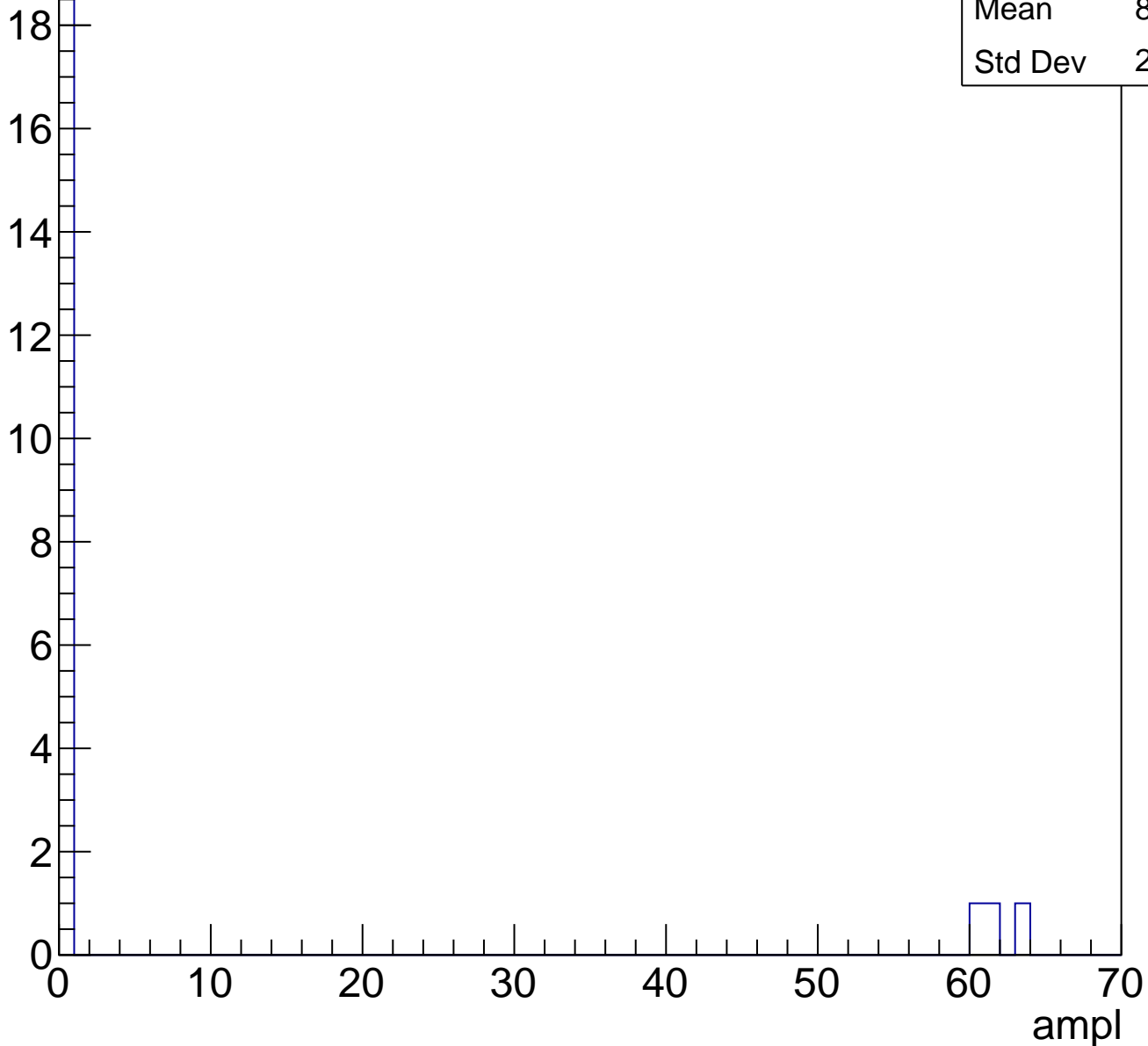


B1L103S, U2-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.364
Std Dev	21.05

Entry



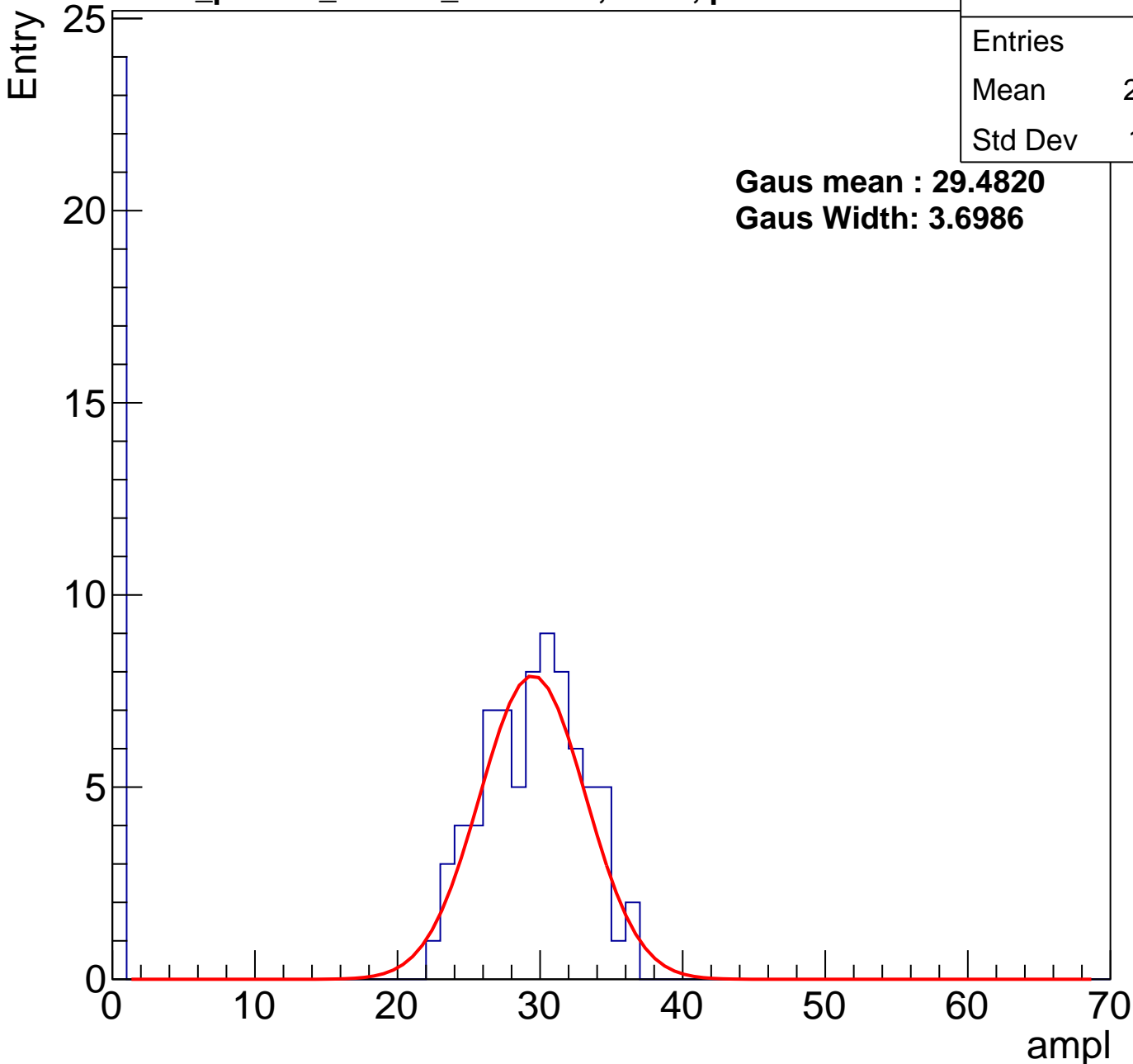
B1L103S, U2-ch86, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	22.04
Std Dev	12.81

Gaus mean : 29.4820

Gaus Width: 3.6986



B1L103S, U2-ch86, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.81
Std Dev	12.24

Gaus mean : 36.8398

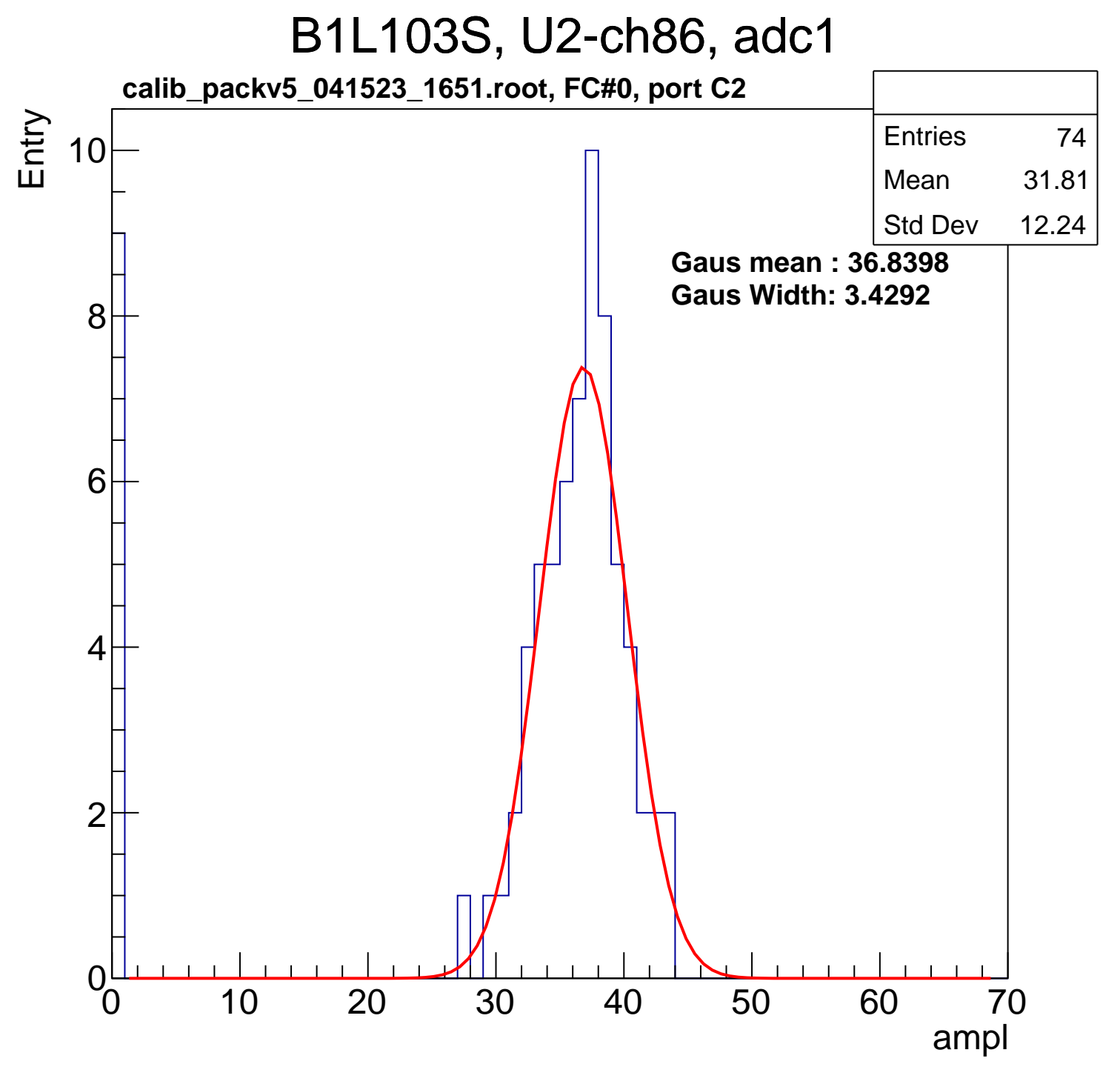
Gaus Width: 3.4292

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch86, adc2

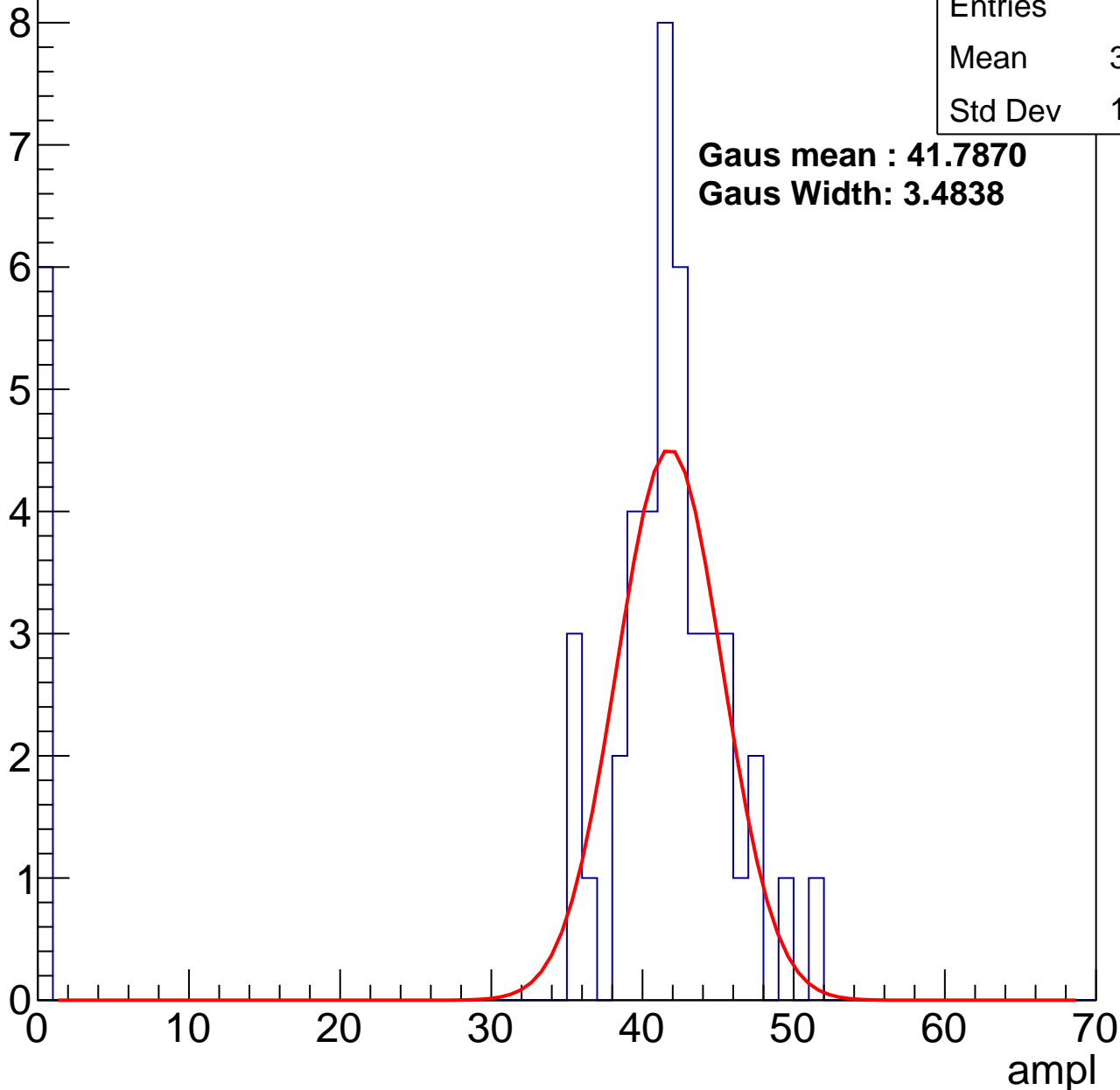
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	36.44
Std Dev	14.15

Gaus mean : 41.7870

Gaus Width: 3.4838

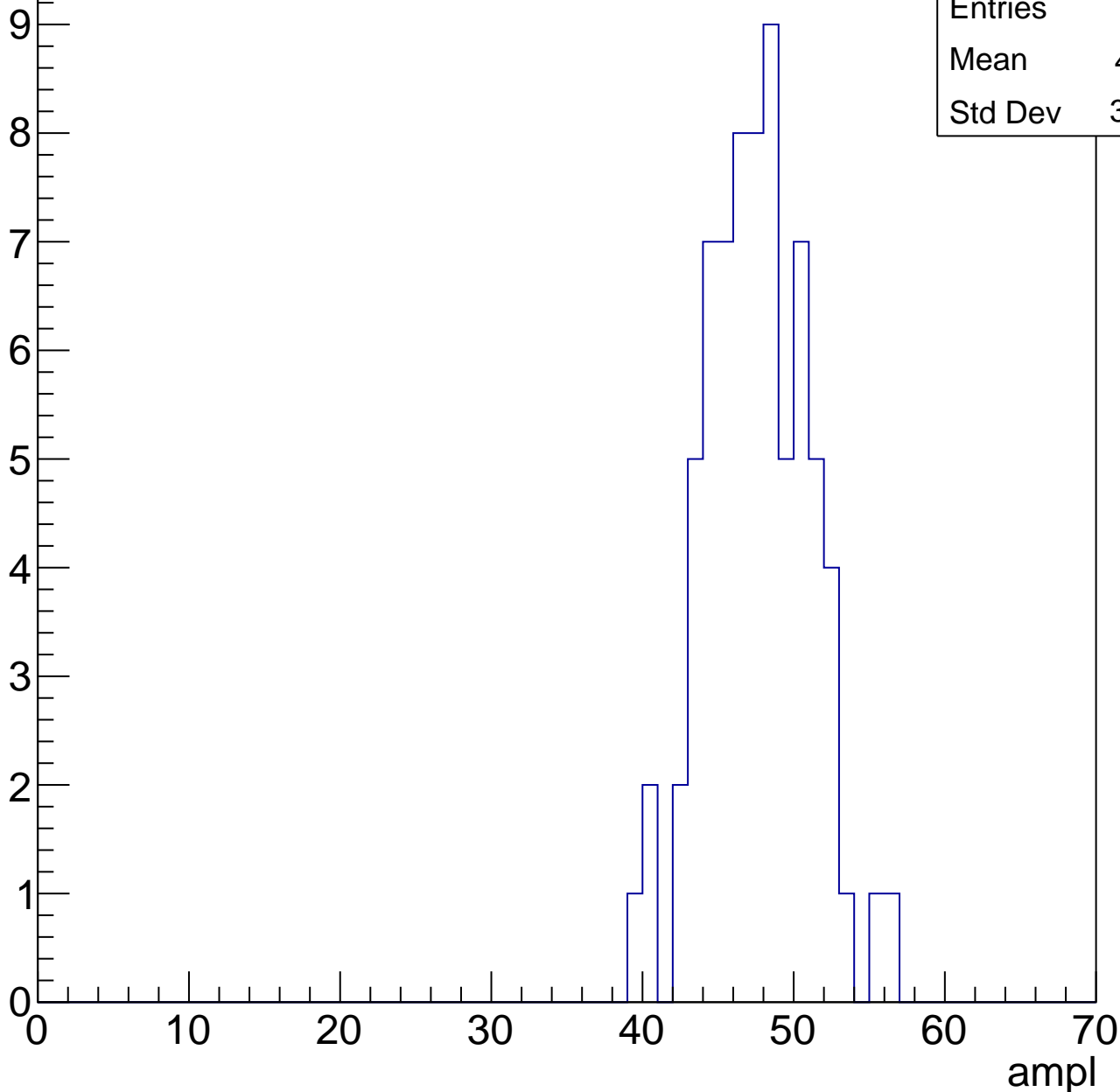


B1L103S, U2-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	47.11
Std Dev	3.398

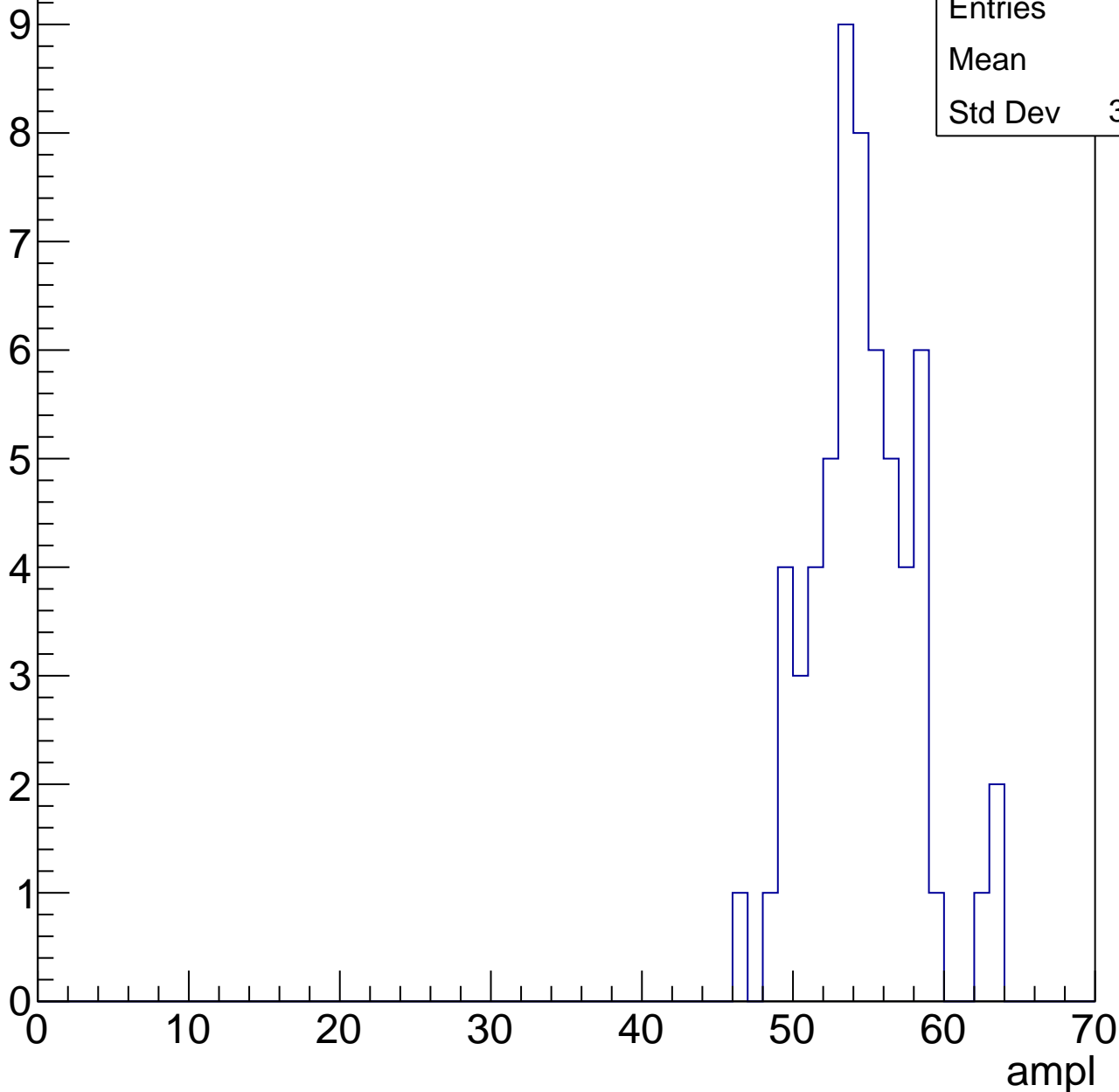


B1L103S, U2-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.1
Std Dev	3.467

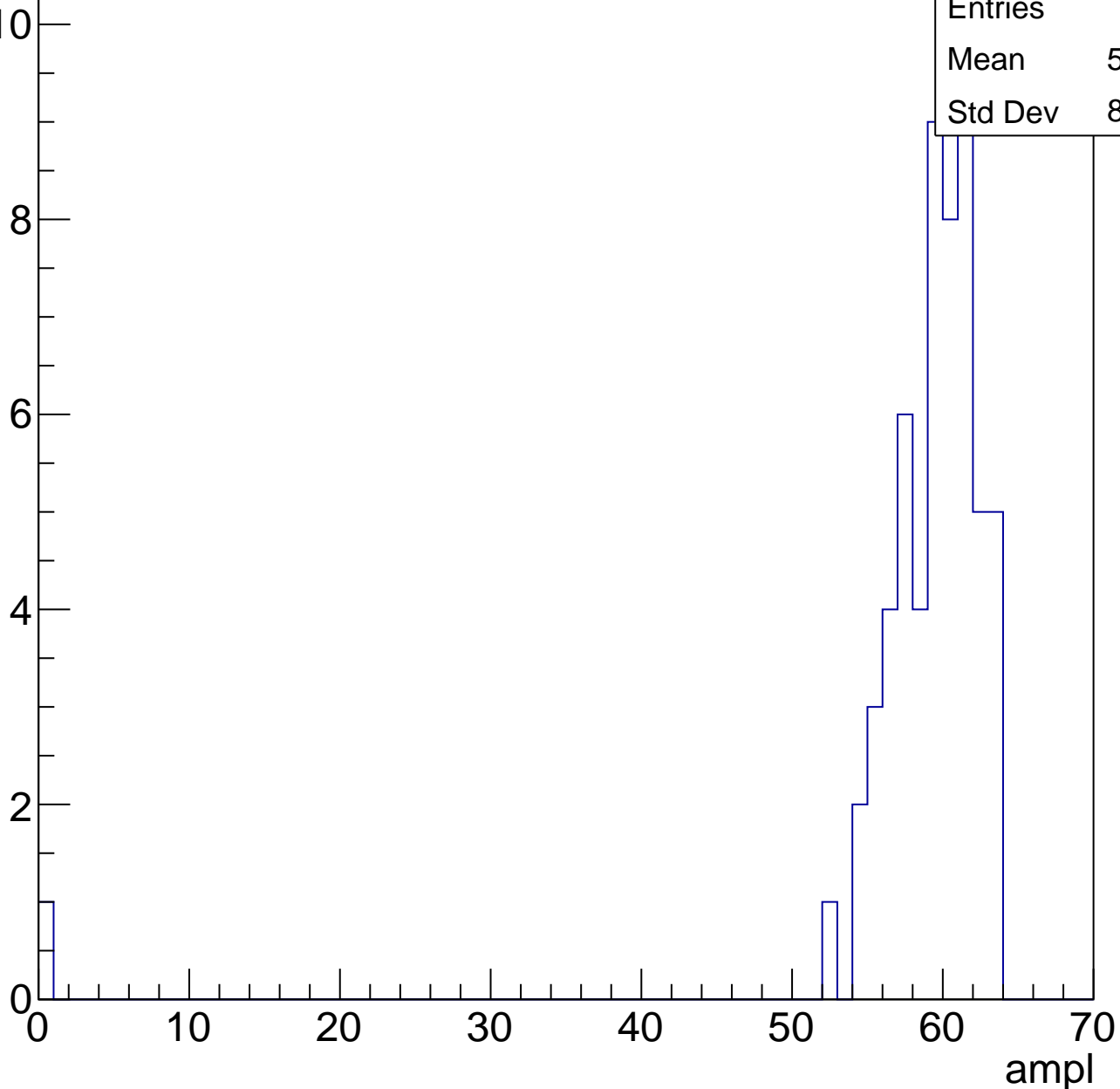


B1L103S, U2-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

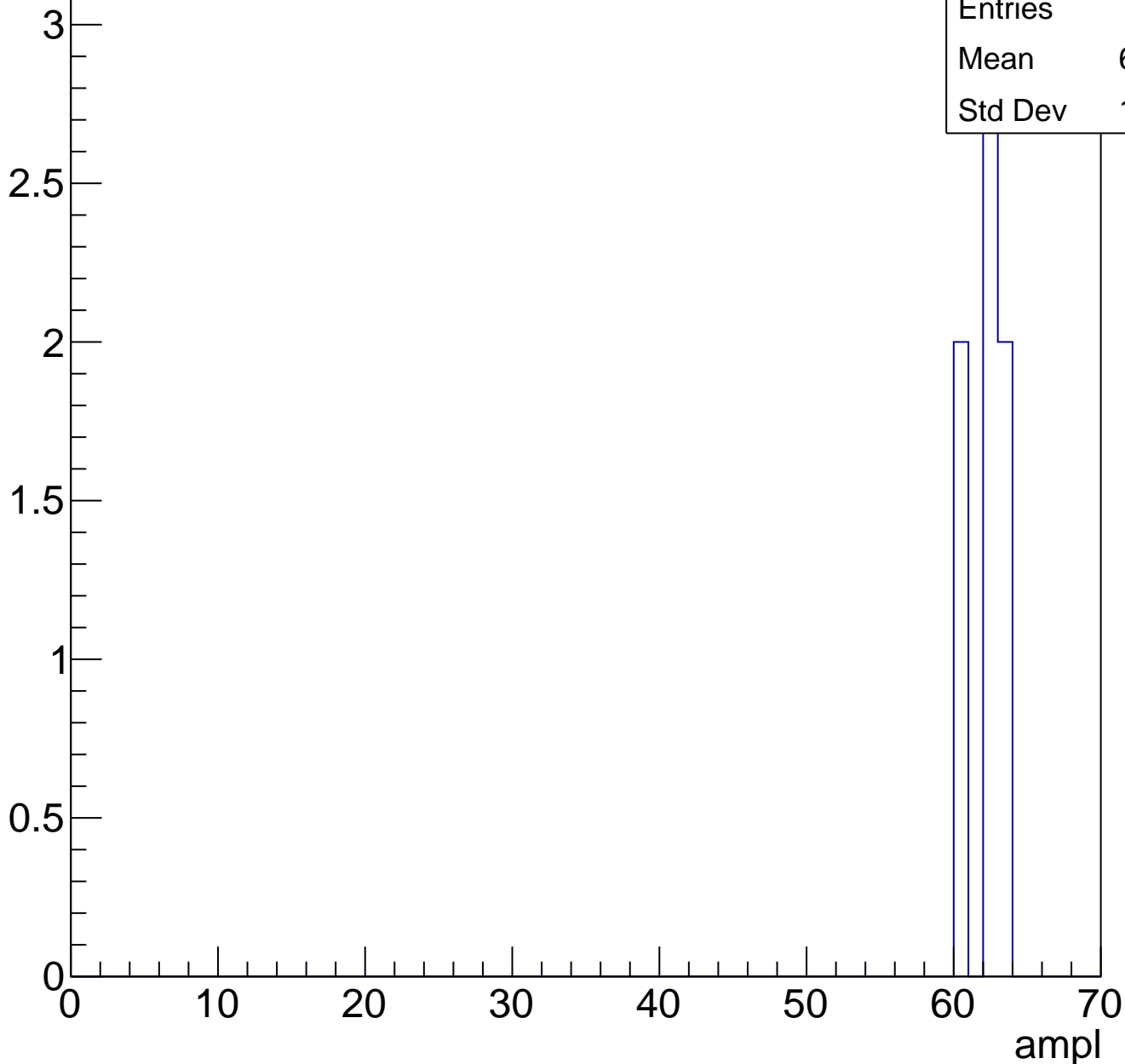
Entries	58
Mean	58.09
Std Dev	8.112



B1L103S, U2-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

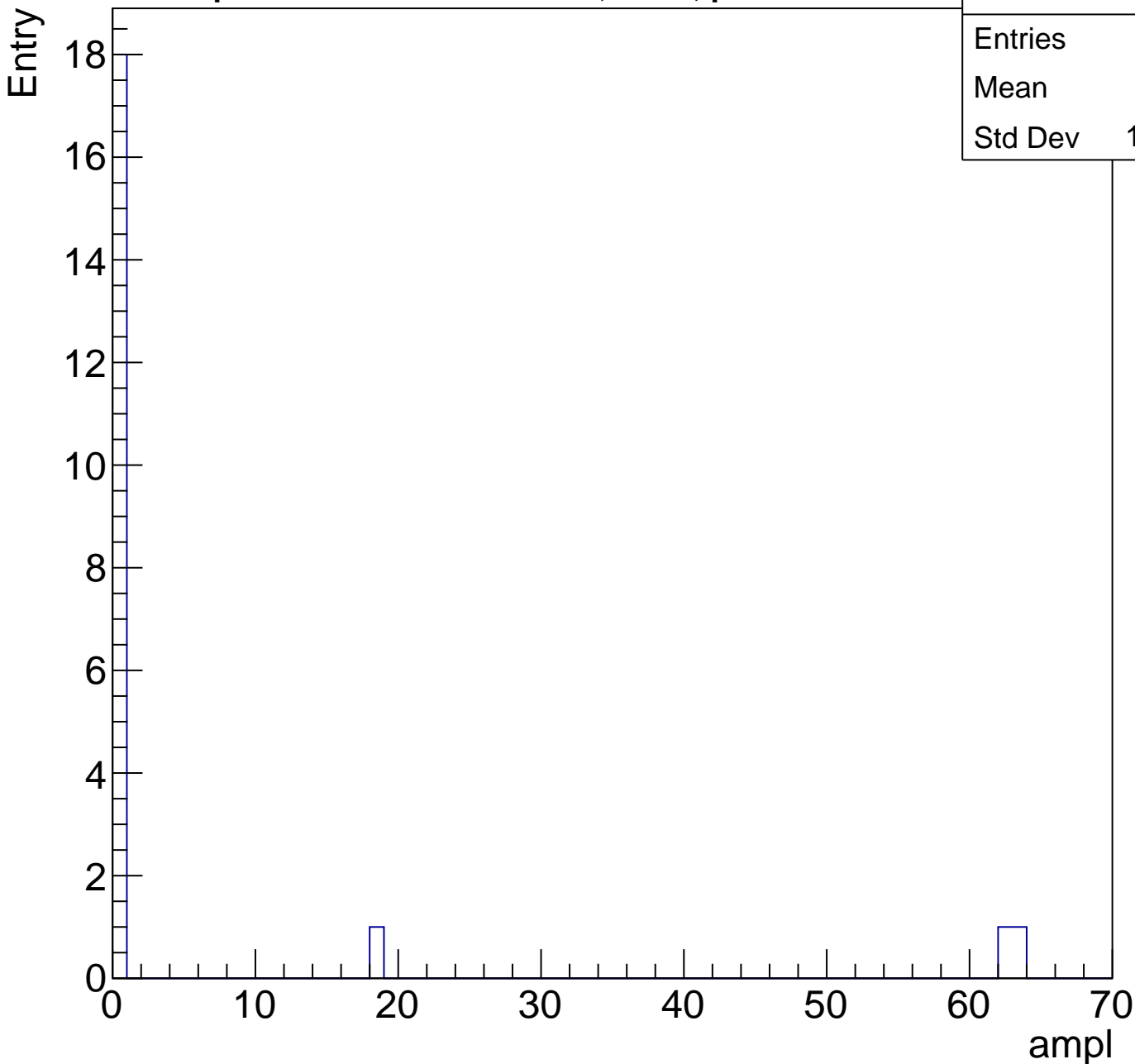
Entry



B1L103S, U2-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.81
Std Dev	18.47



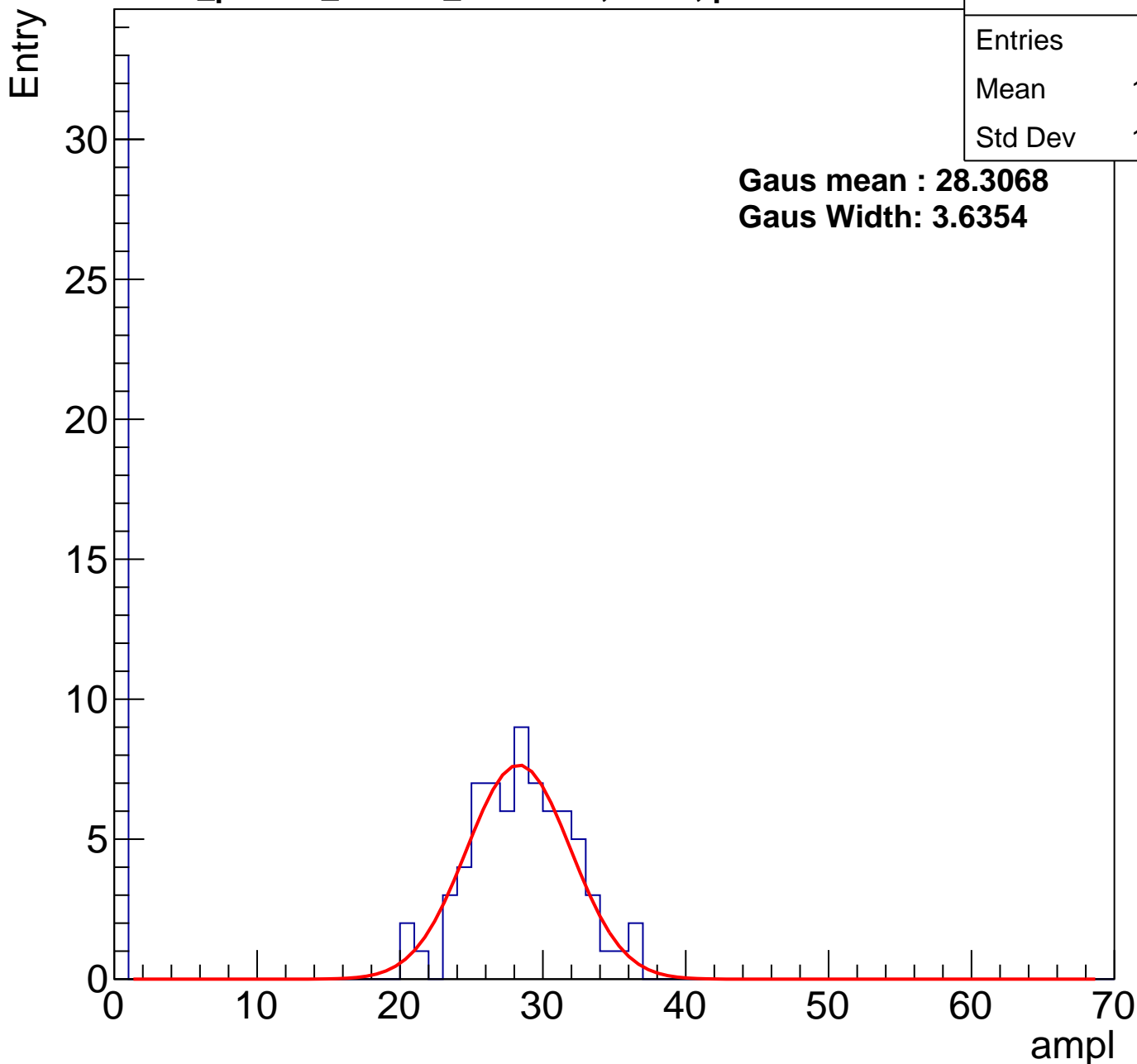
B1L103S, U2-ch87, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	19.09
Std Dev	13.42

Gaus mean : 28.3068

Gaus Width: 3.6354



B1L103S, U2-ch87, adc1

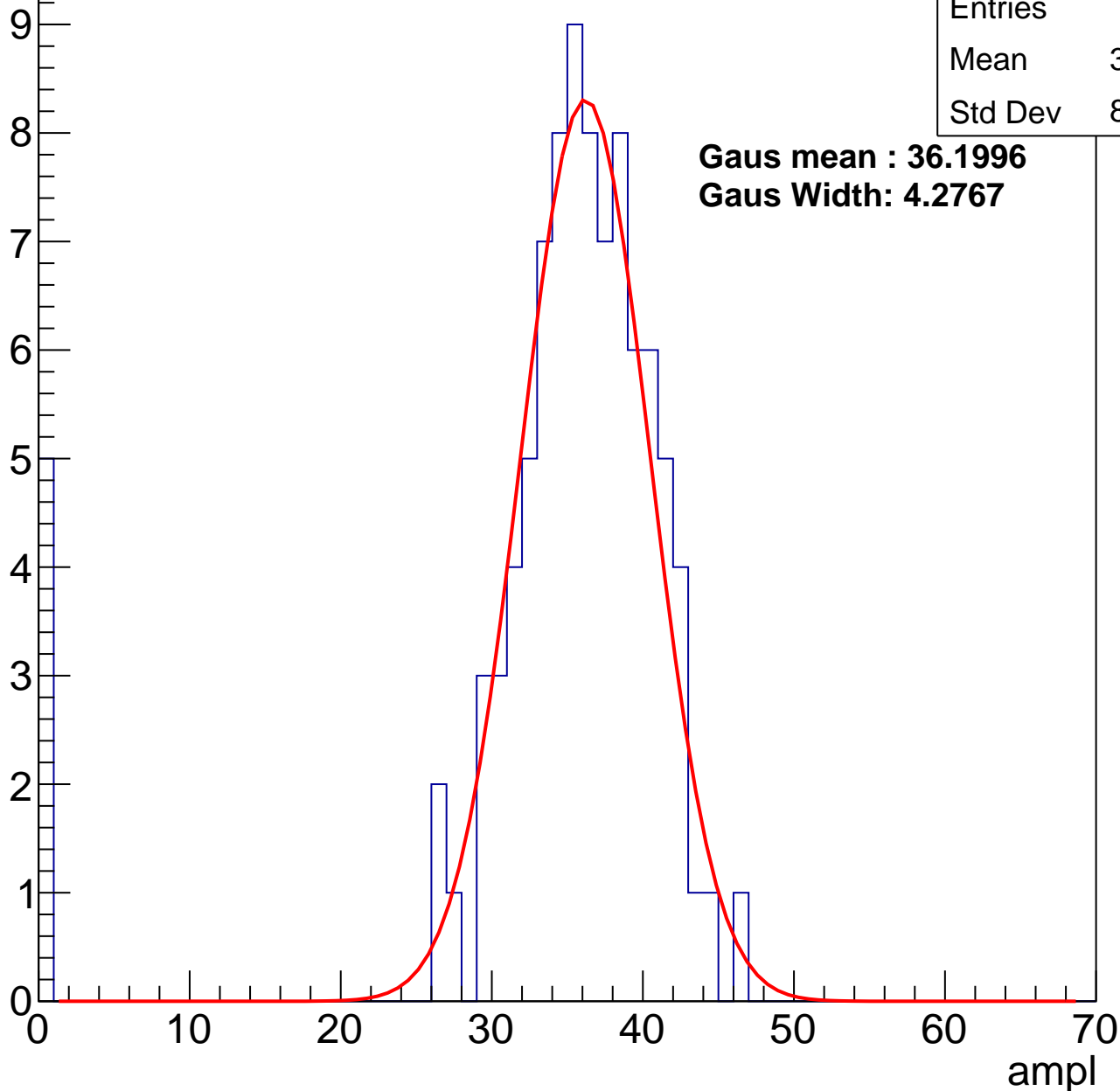
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	94
Mean	33.93
Std Dev	8.972

Gaus mean : 36.1996

Gaus Width: 4.2767



B1L103S, U2-ch87, adc2

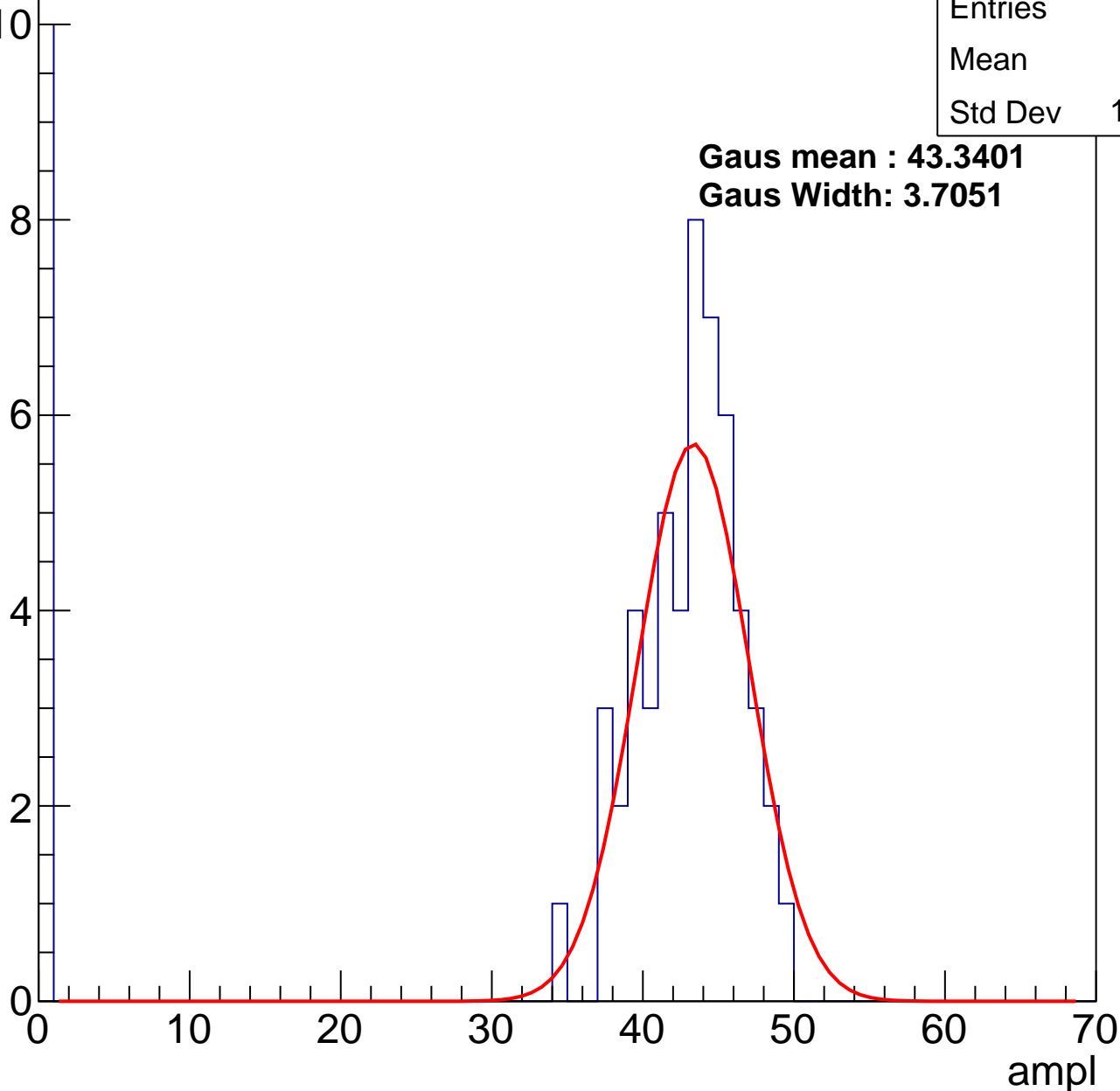
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	35.9
Std Dev	15.87

Gaus mean : 43.3401

Gaus Width: 3.7051

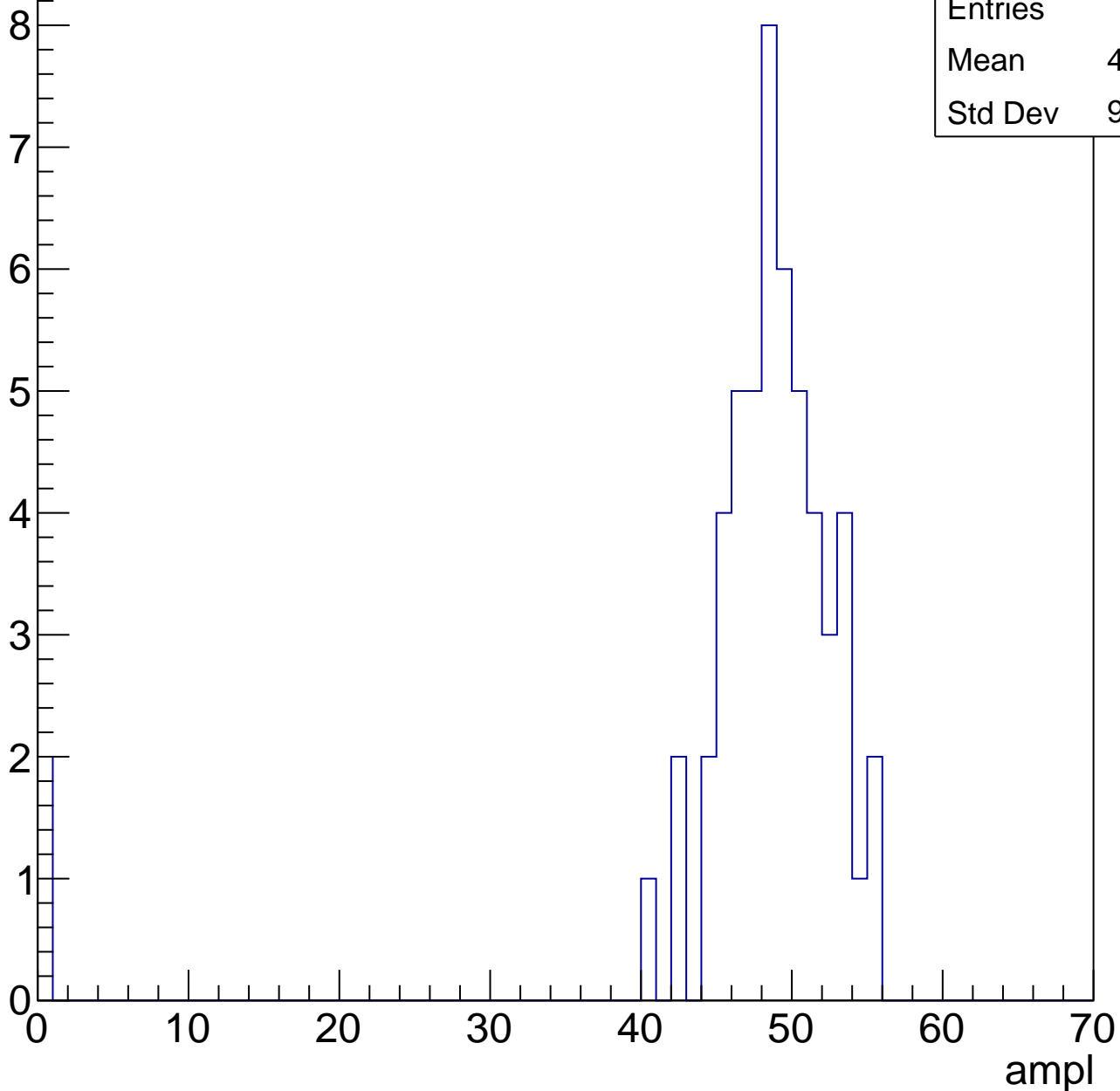


B1L103S, U2-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	46.69
Std Dev	9.706

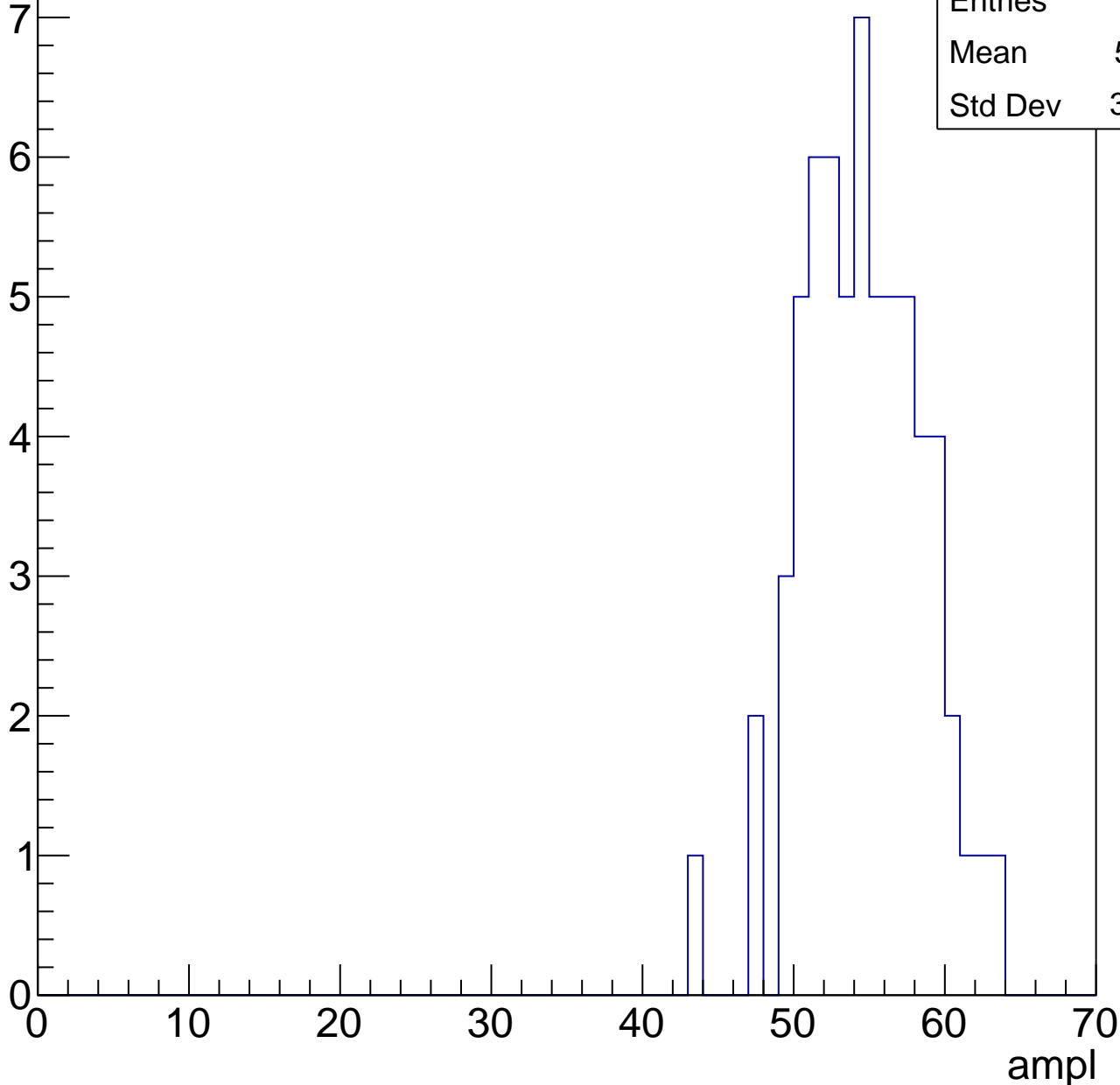


B1L103S, U2-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	54.11
Std Dev	3.896

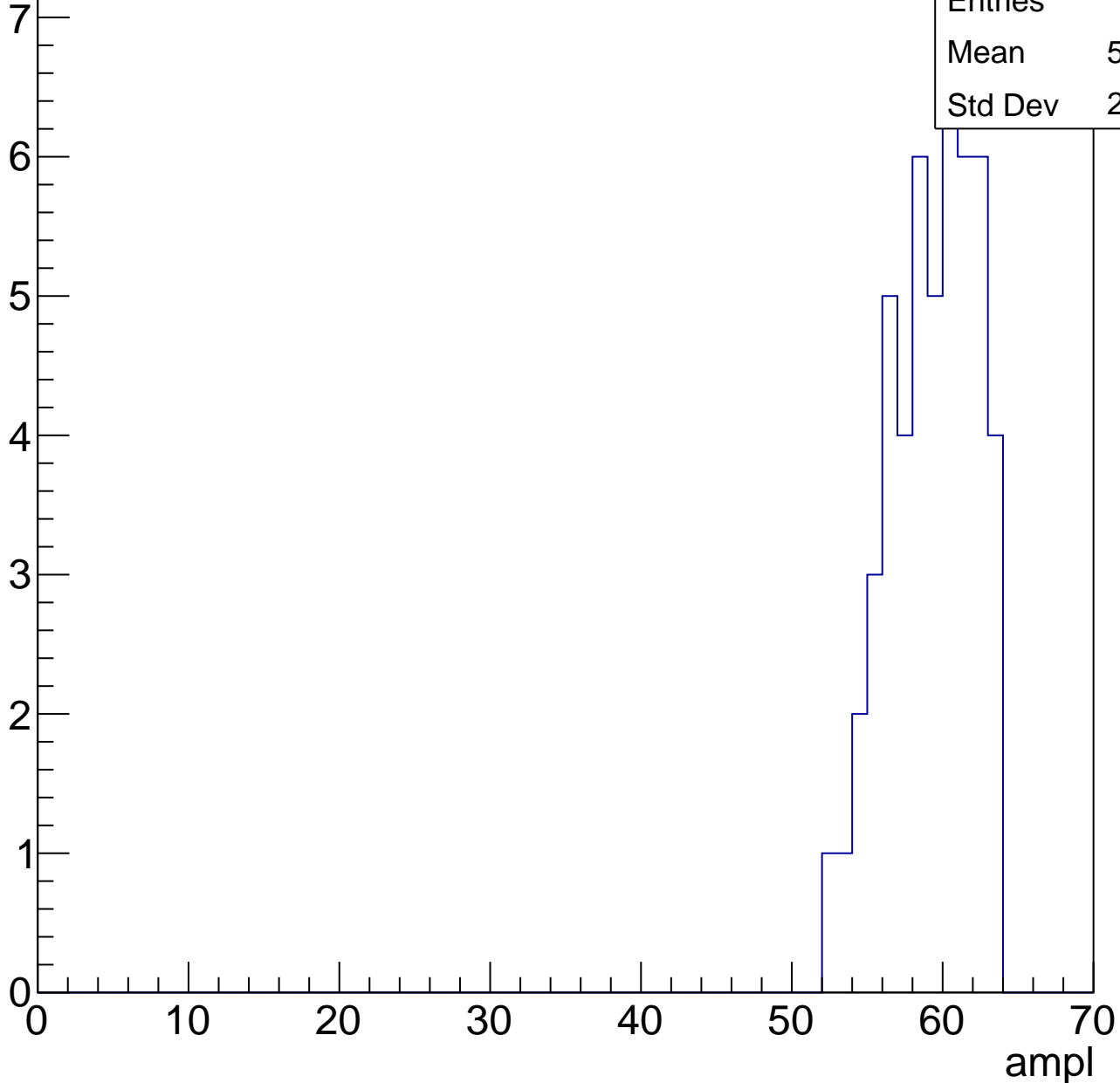


B1L103S, U2-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.78
Std Dev	2.816

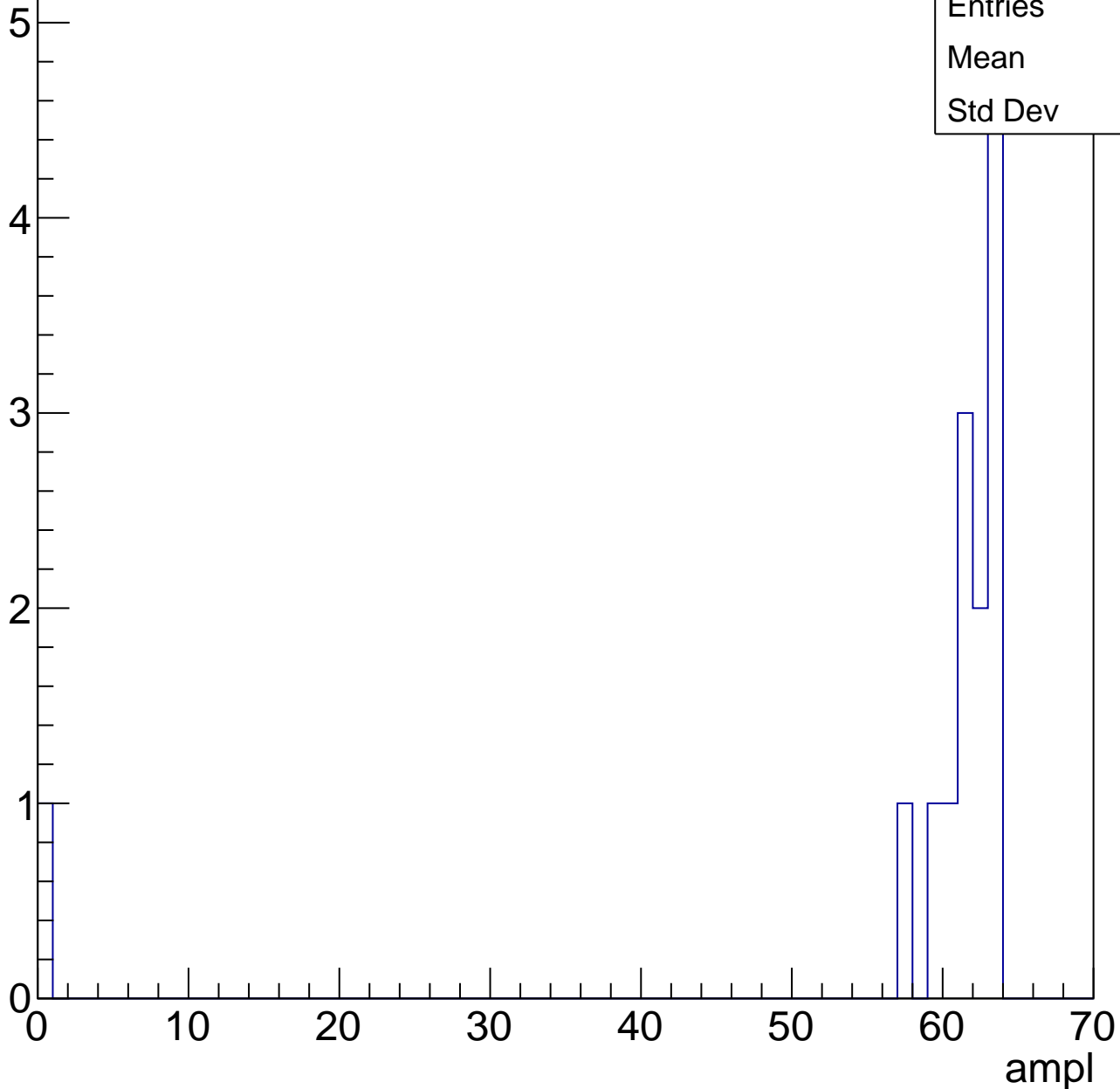


B1L103S, U2-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57
Std Dev	15.9



B1L103S, U2-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U2-ch88, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	22.74
Std Dev	10.87

Gaus mean : 27.7100

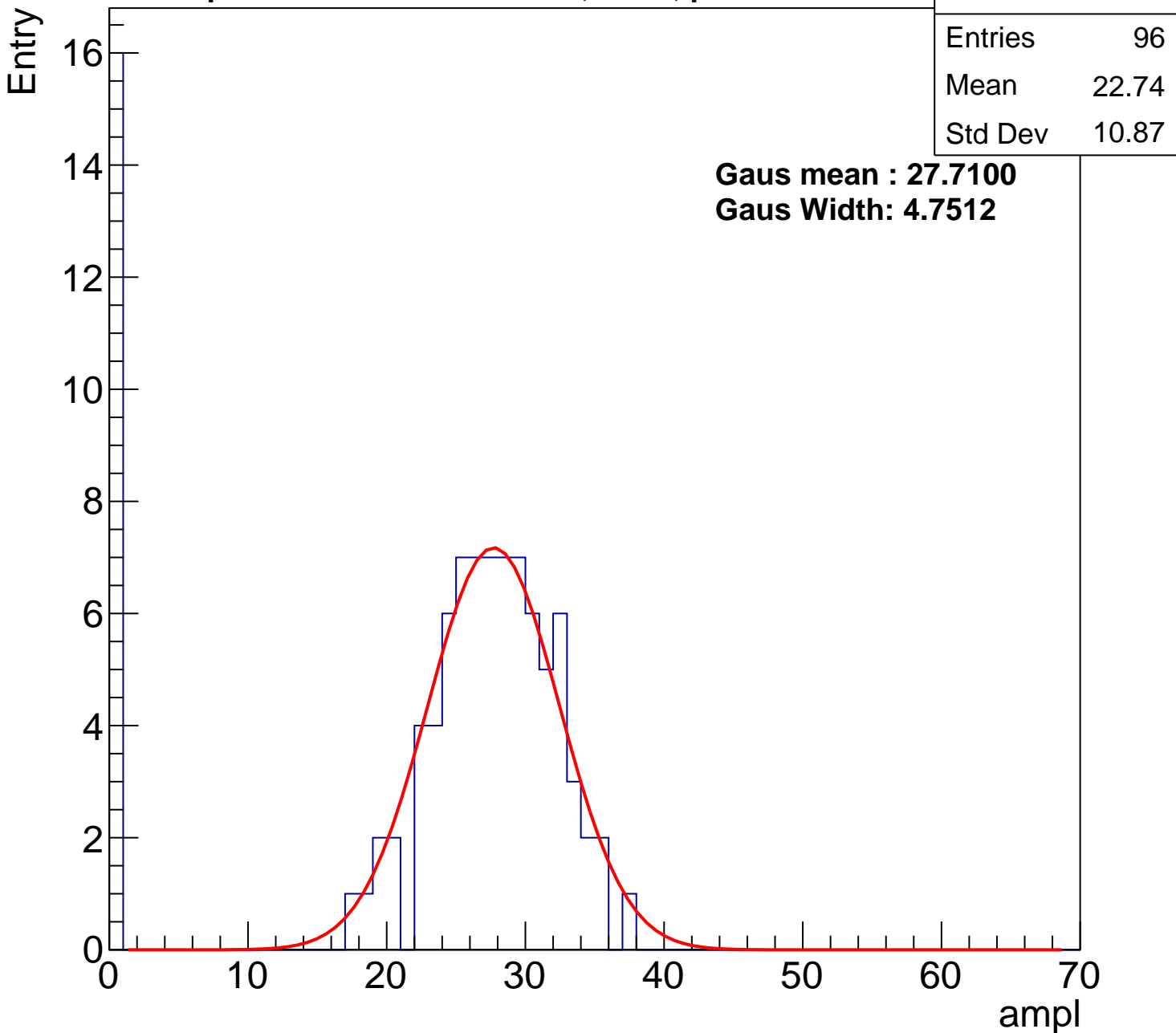
Gaus Width: 4.7512

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch88, adc1

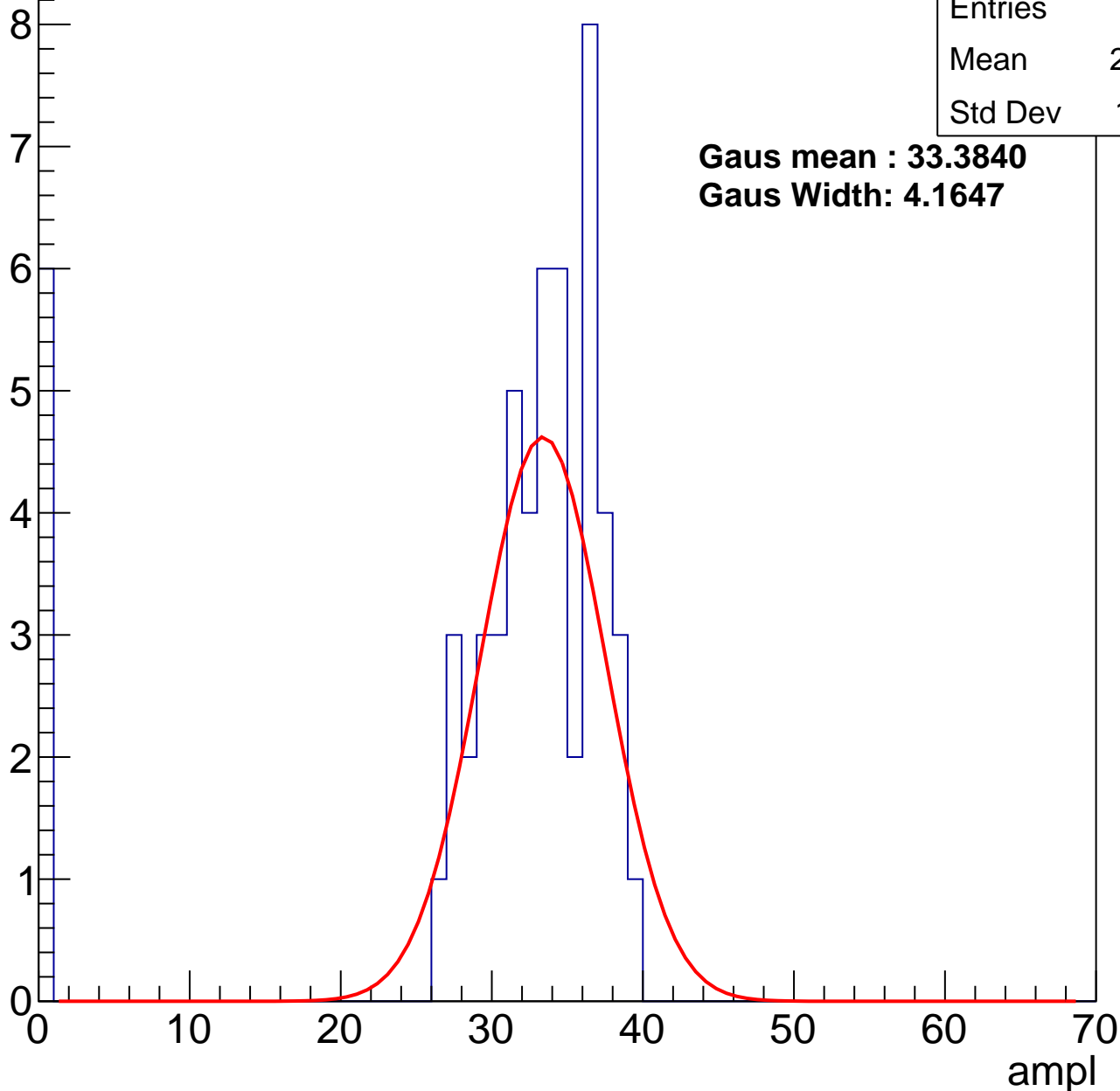
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	29.54
Std Dev	10.61

Gaus mean : 33.3840

Gaus Width: 4.1647



B1L103S, U2-ch88, adc2

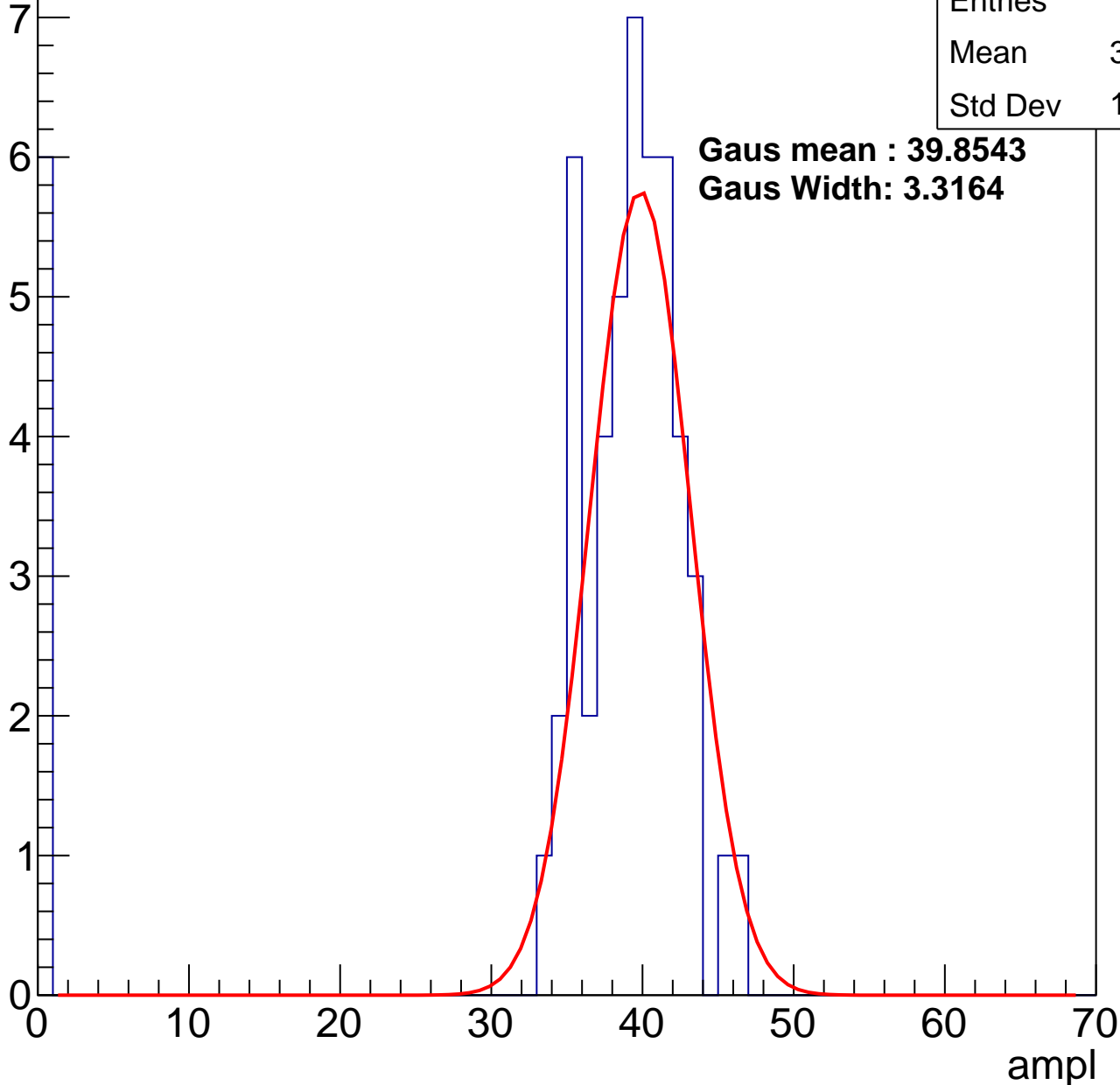
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	34.59
Std Dev	12.54

Gaus mean : 39.8543

Gaus Width: 3.3164

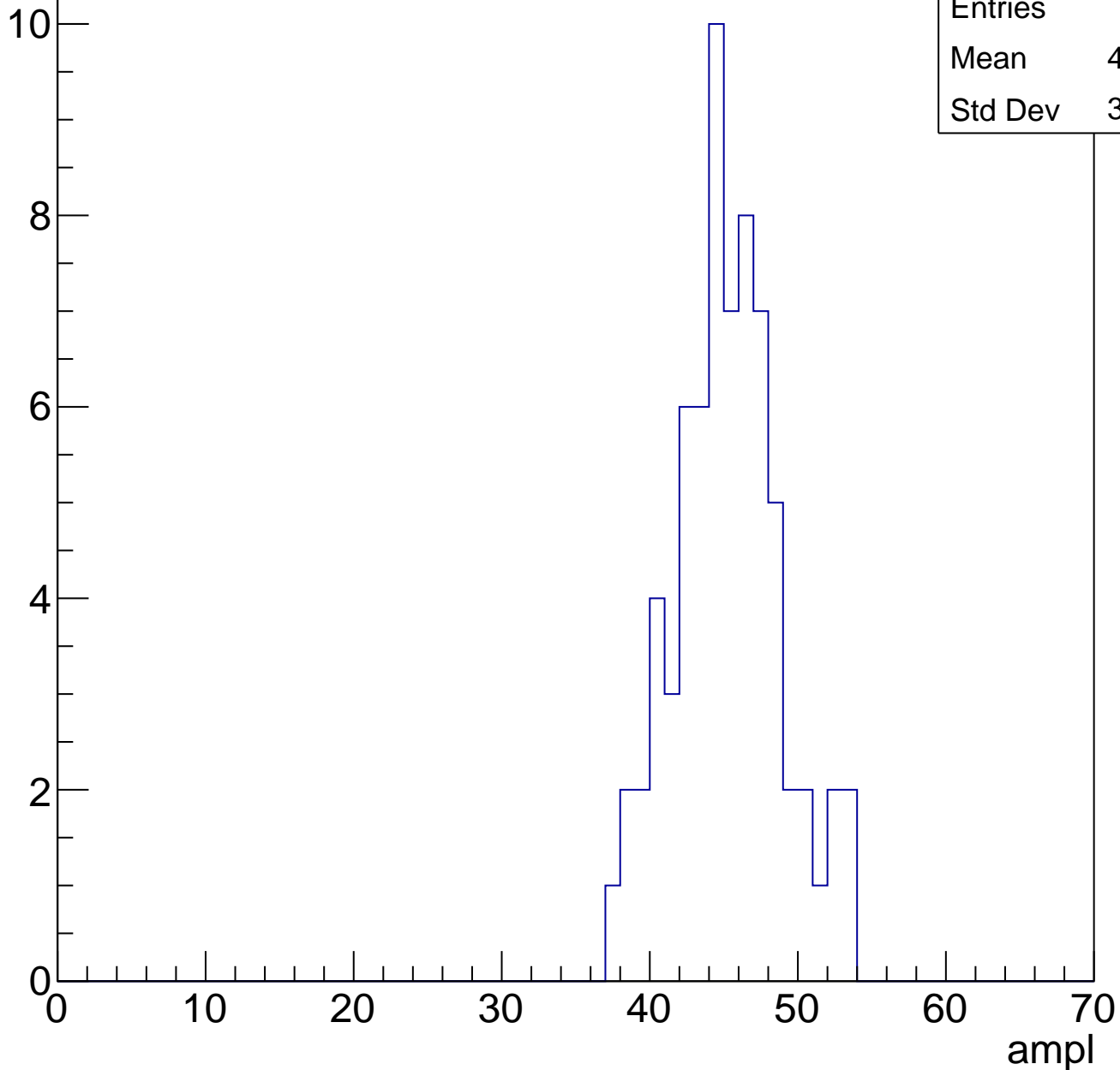


B1L103S, U2-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	44.79
Std Dev	3.569

Entry

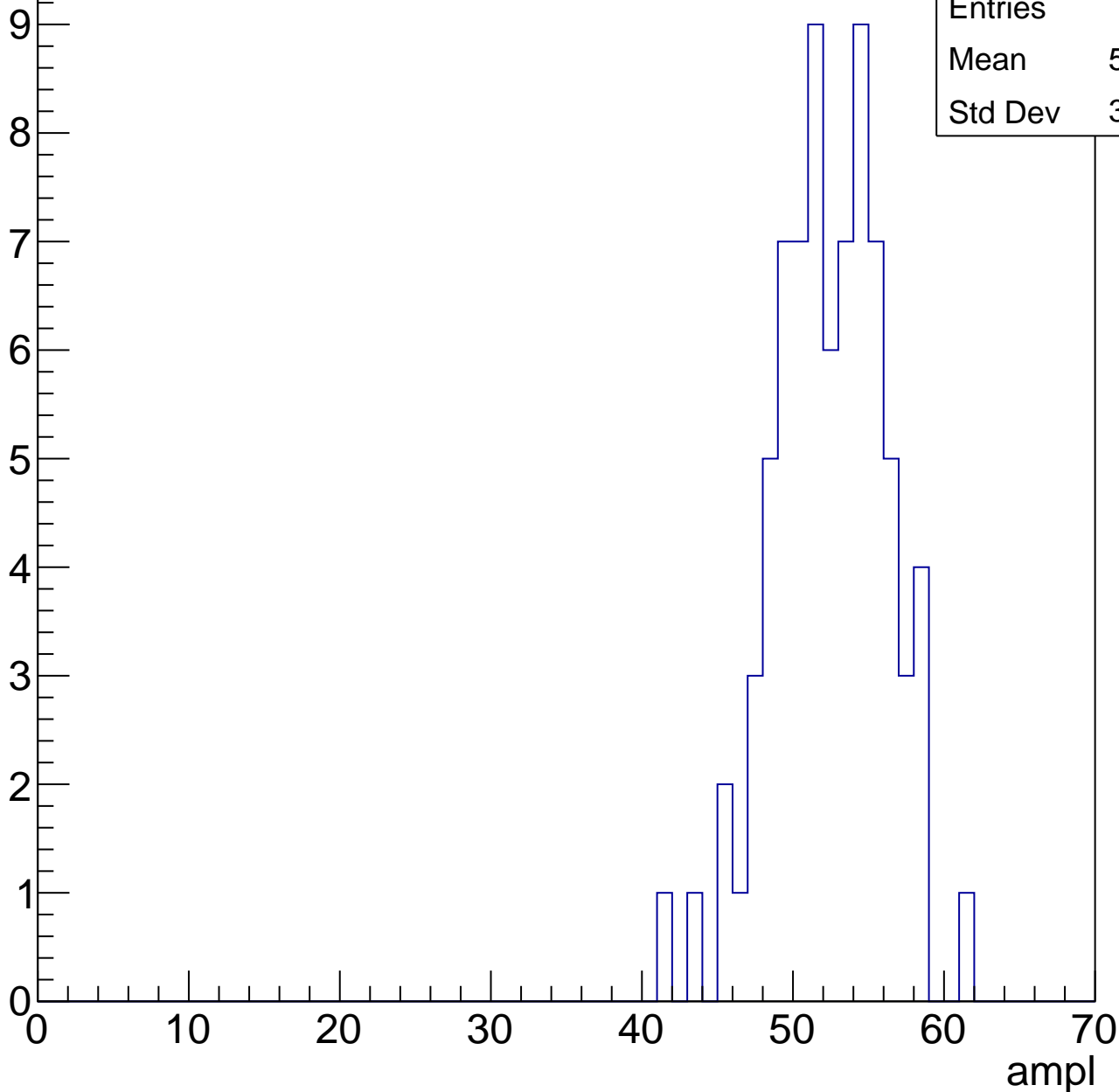


B1L103S, U2-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	51.94
Std Dev	3.715



B1L103S, U2-ch88, adc5

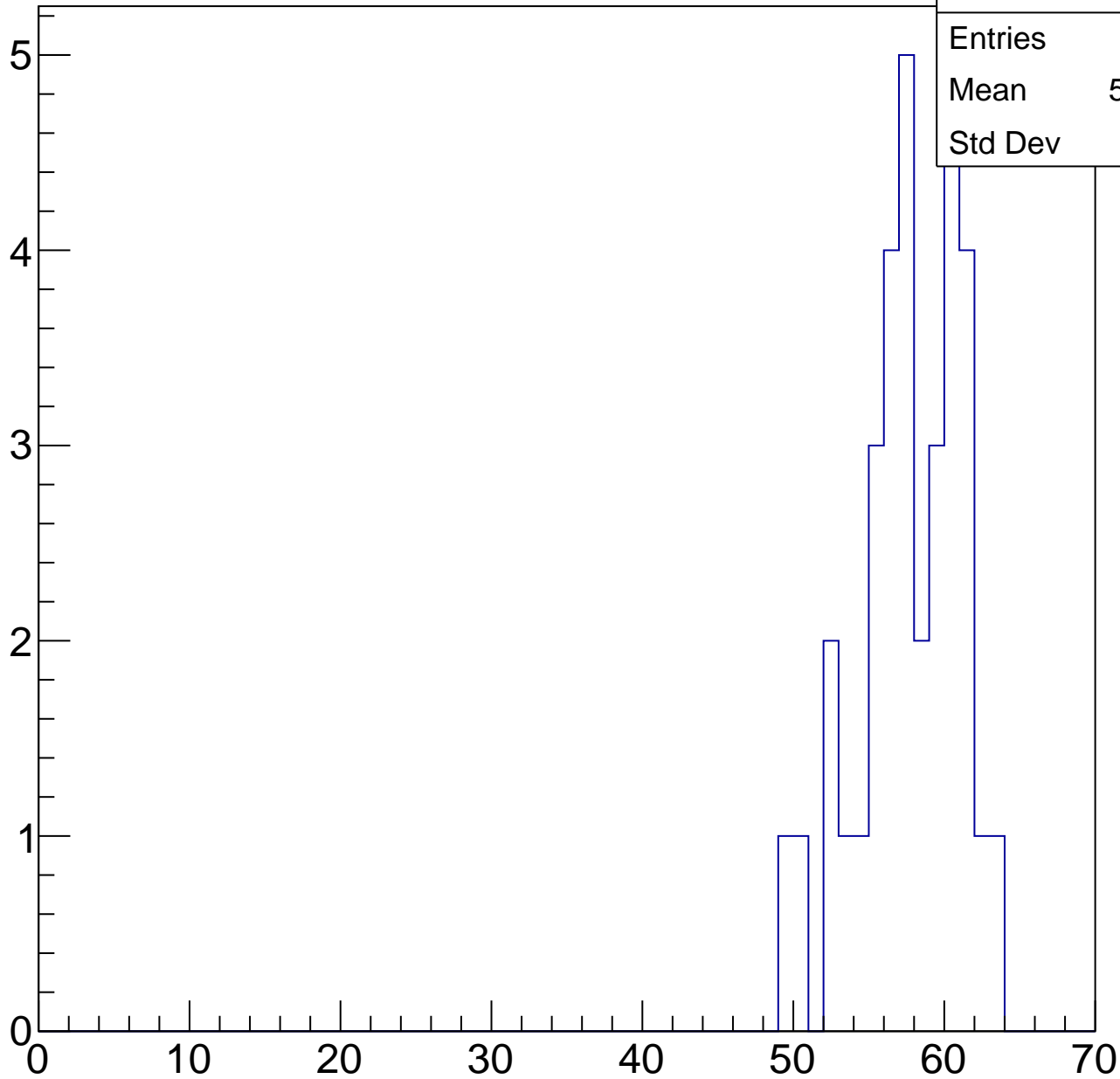
calib_packv5_041523_1651.root, FC#0, port C2

Entry

5
4
3
2
1
0

Entries	34
Mean	57.24
Std Dev	3.37

ampl

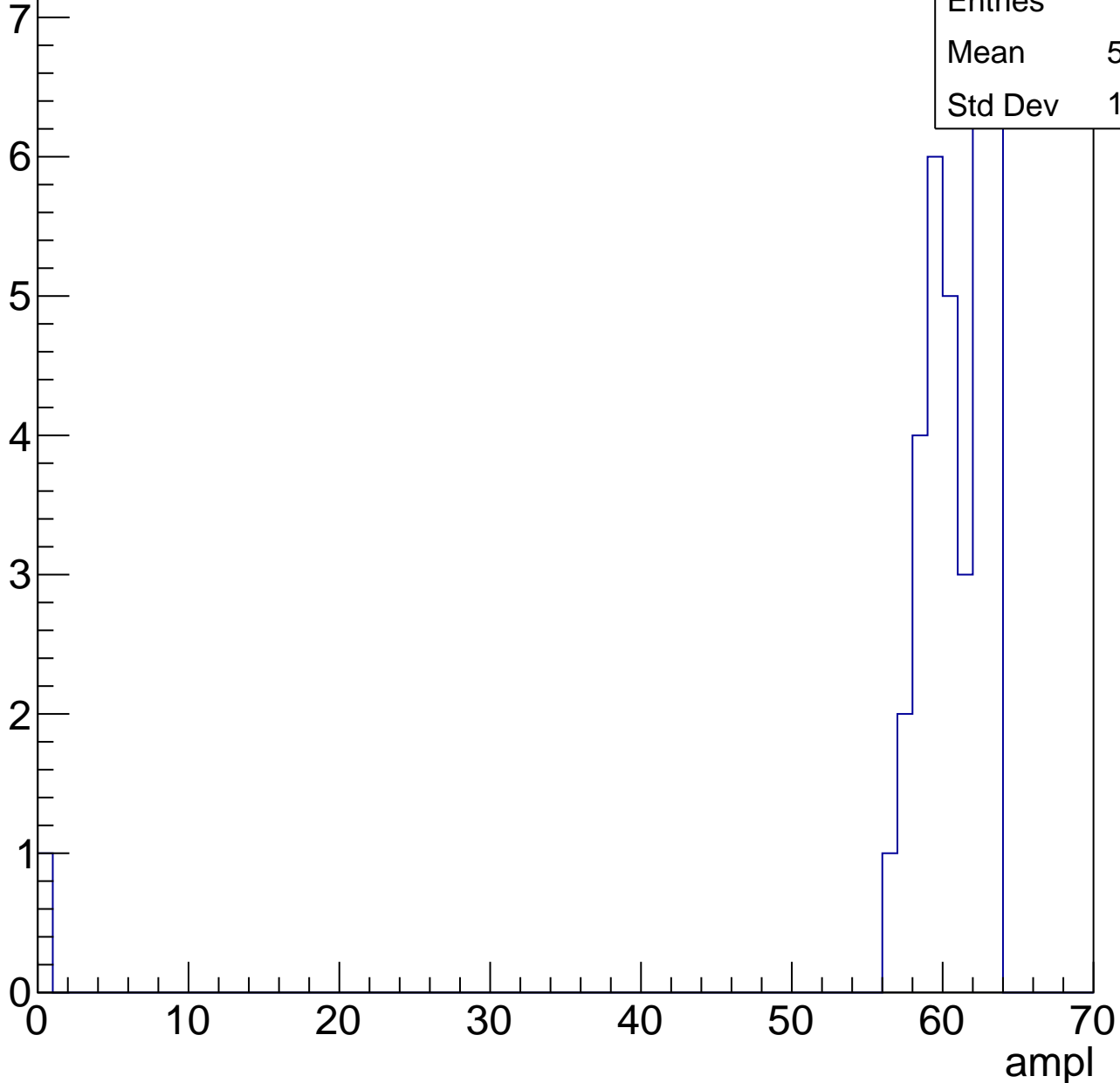


B1L103S, U2-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

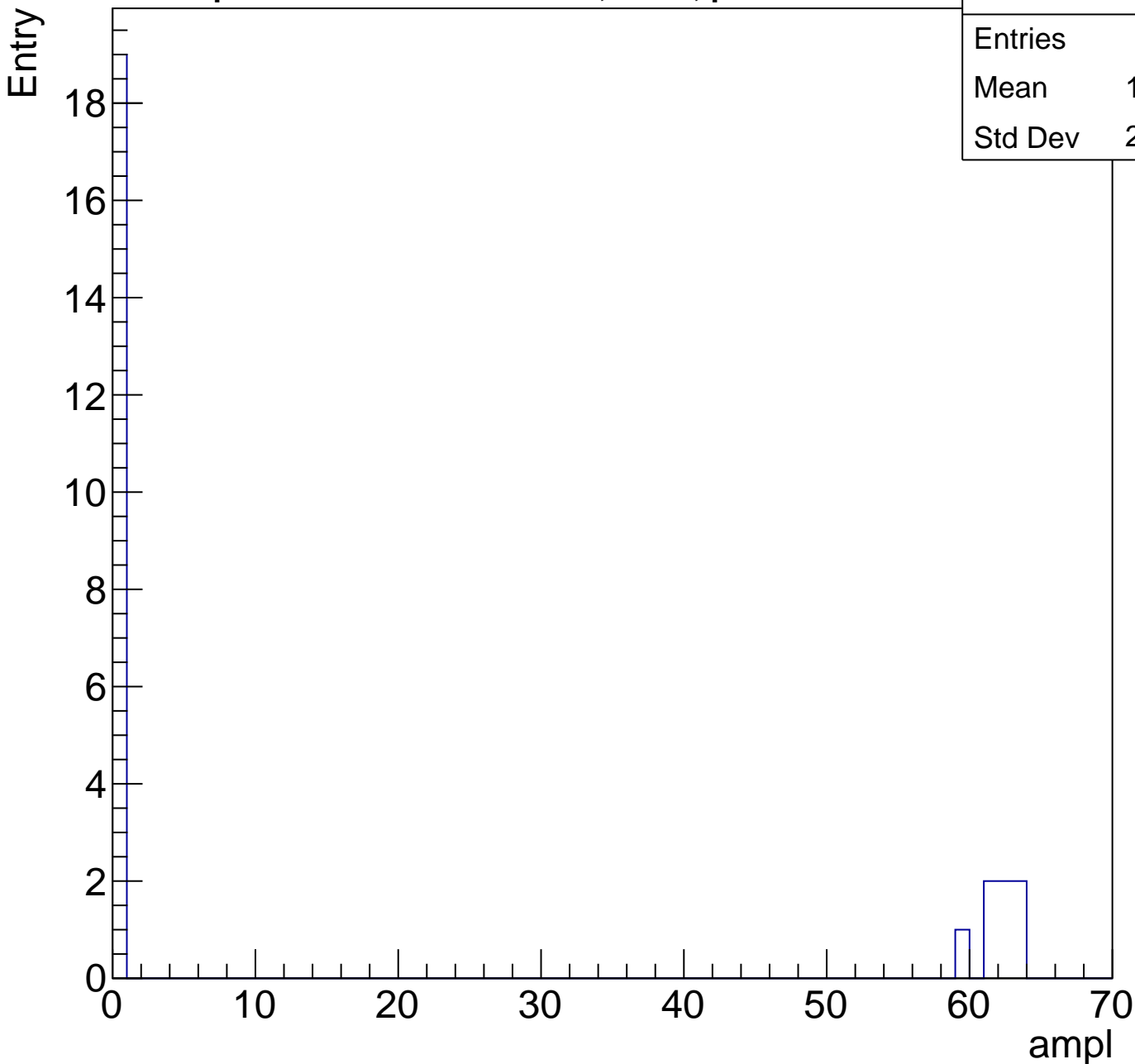
Entries	36
Mean	58.72
Std Dev	10.13



B1L103S, U2-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	16.58
Std Dev	27.32



B1L103S, U2-ch89, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	21.7
Std Dev	12.95

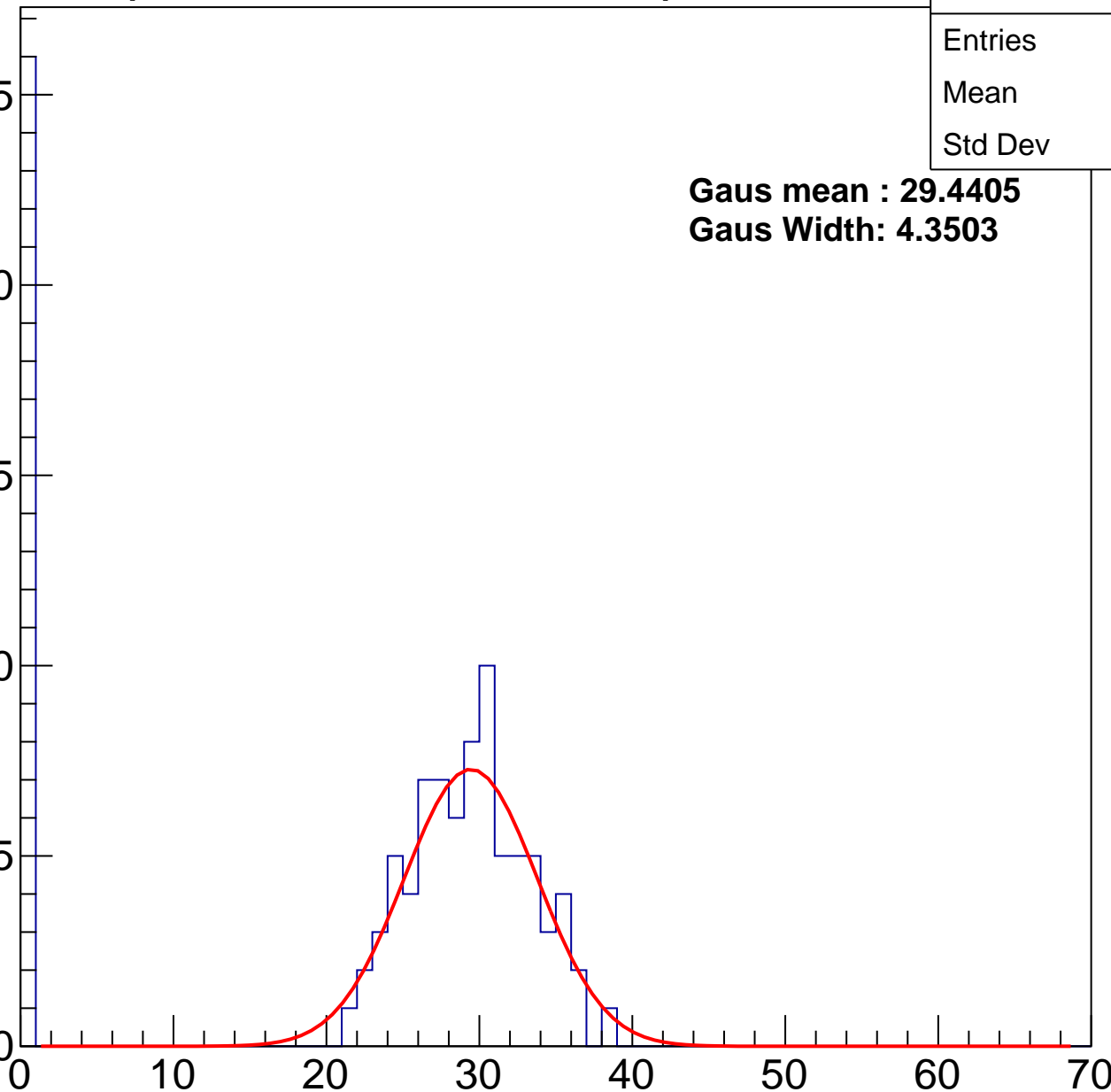
Gaus mean : 29.4405

Gaus Width: 4.3503

Entry

25
20
15
10
5
0

ampl



B1L103S, U2-ch89, adc1

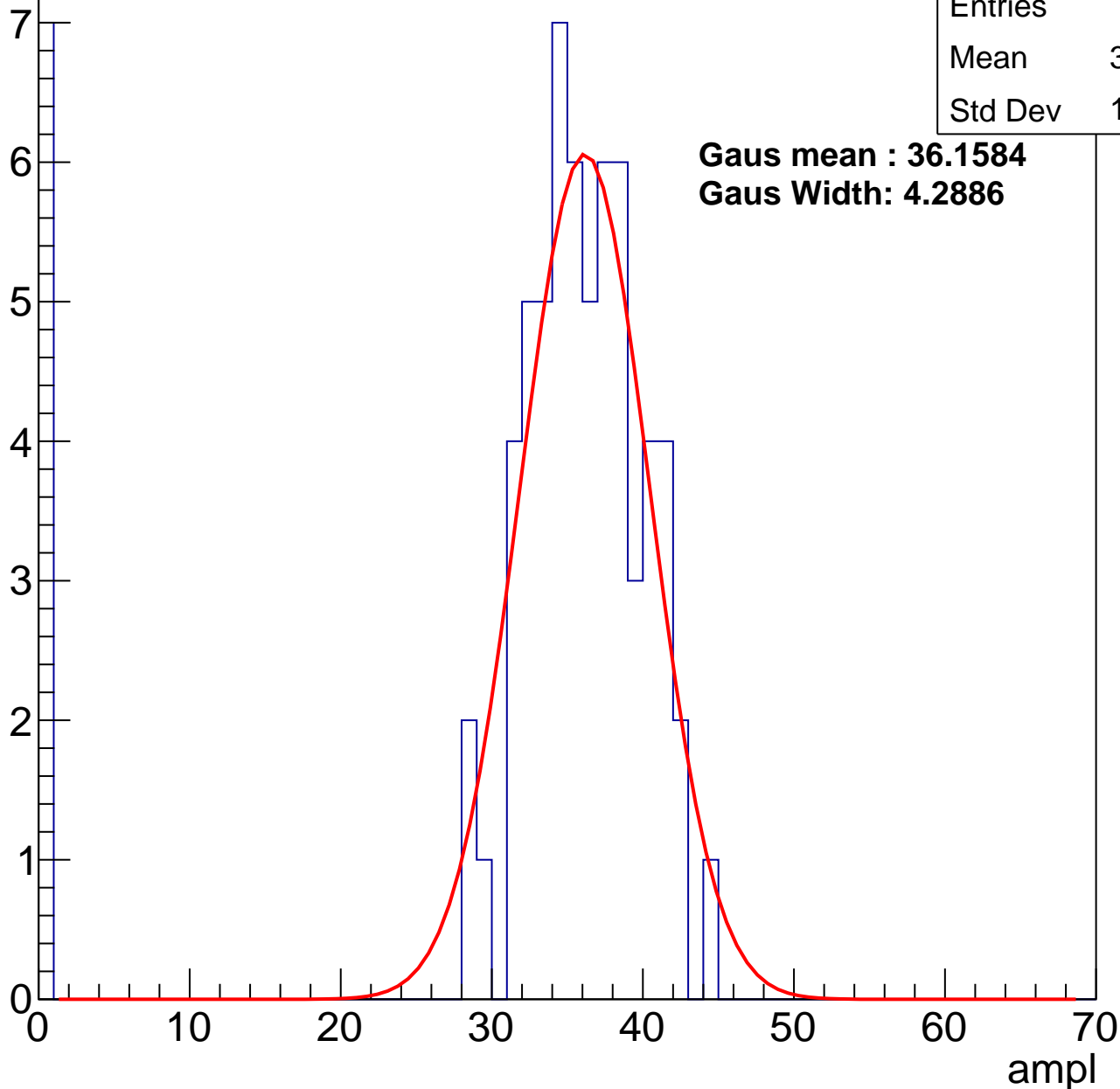
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	32.07
Std Dev	11.39

Gaus mean : 36.1584

Gaus Width: 4.2886



B1L103S, U2-ch89, adc2

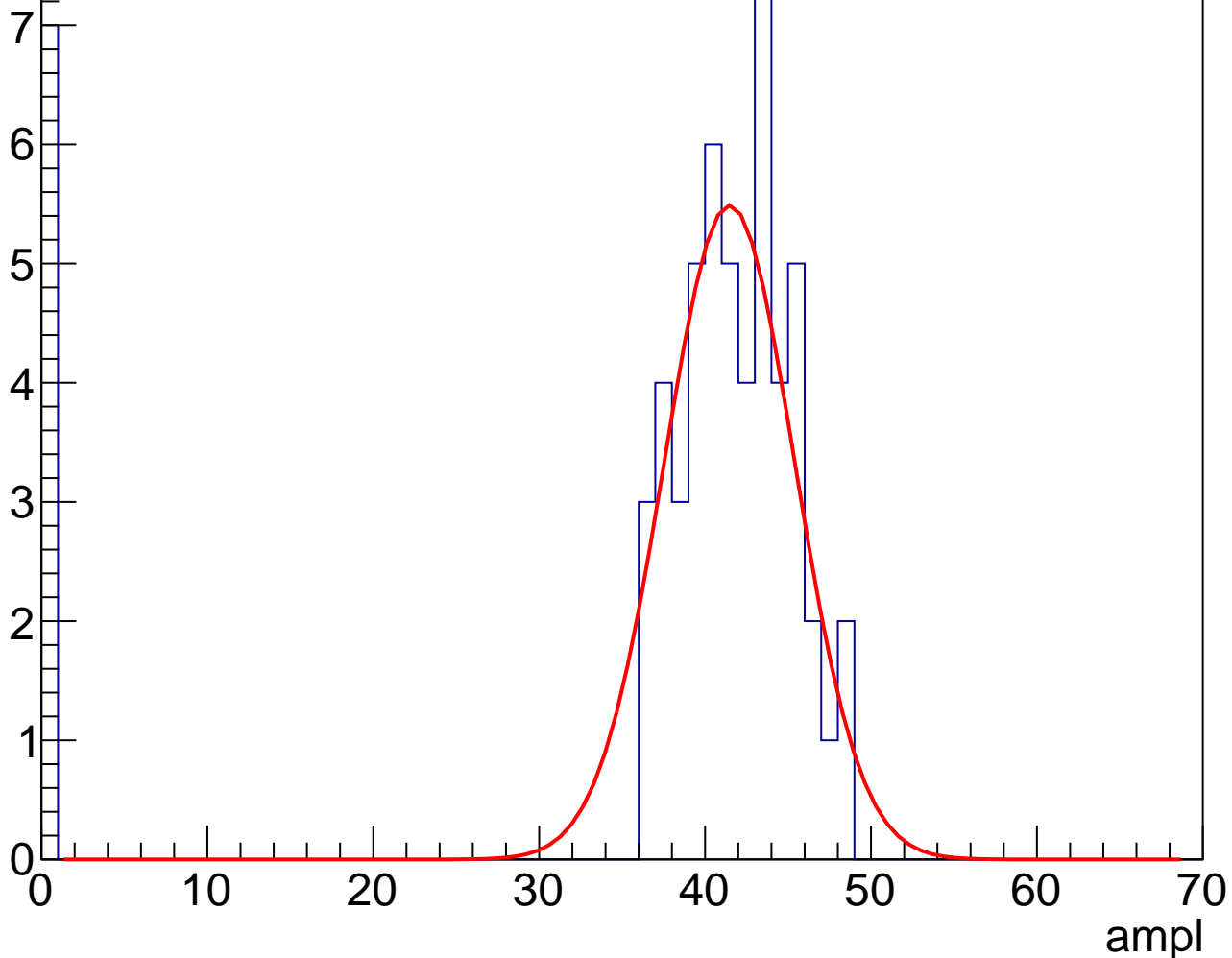
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	36.68
Std Dev	13.65

Gaus mean : 41.4717

Gaus Width: 3.9386

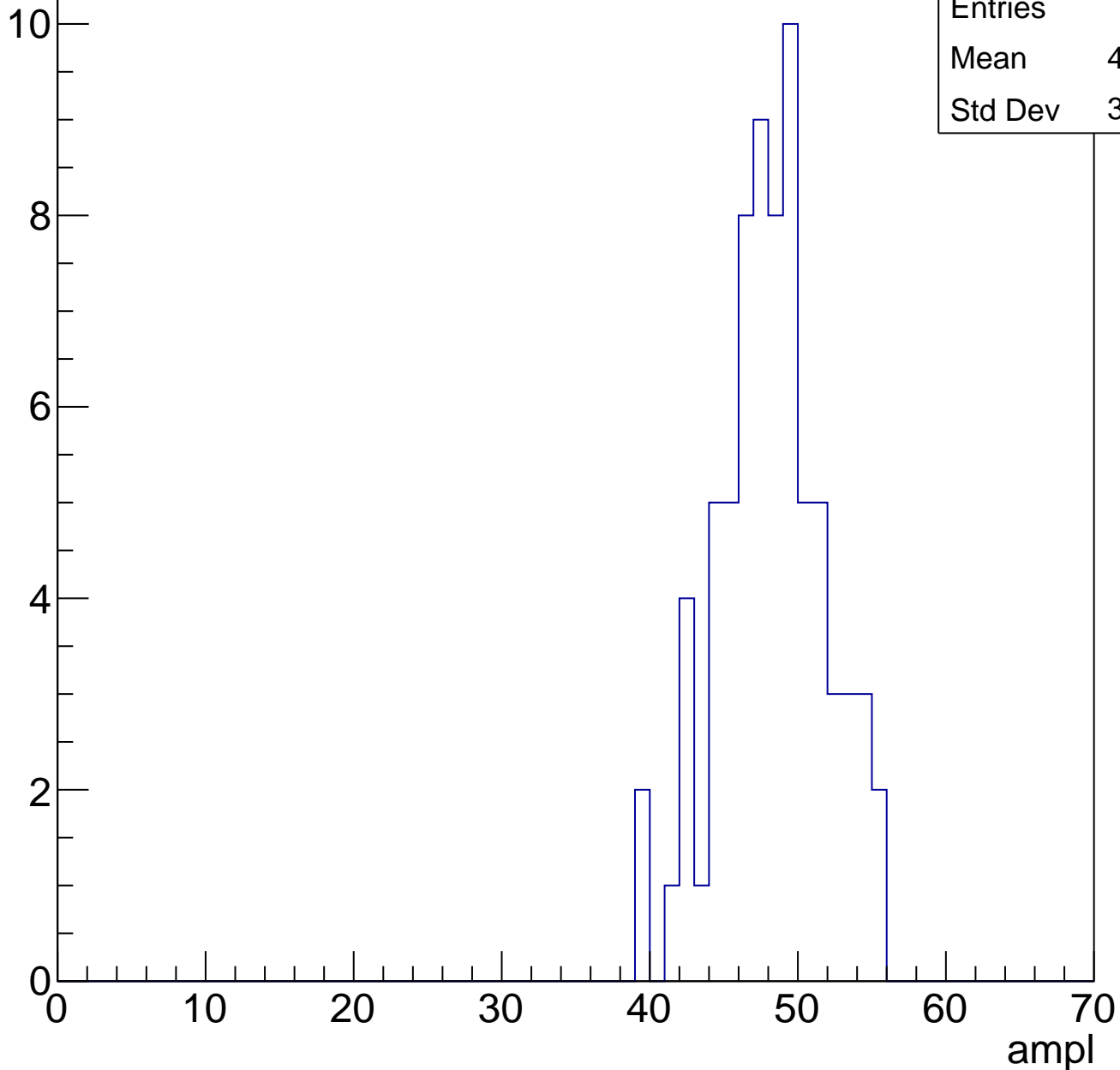


B1L103S, U2-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	47.73
Std Dev	3.573

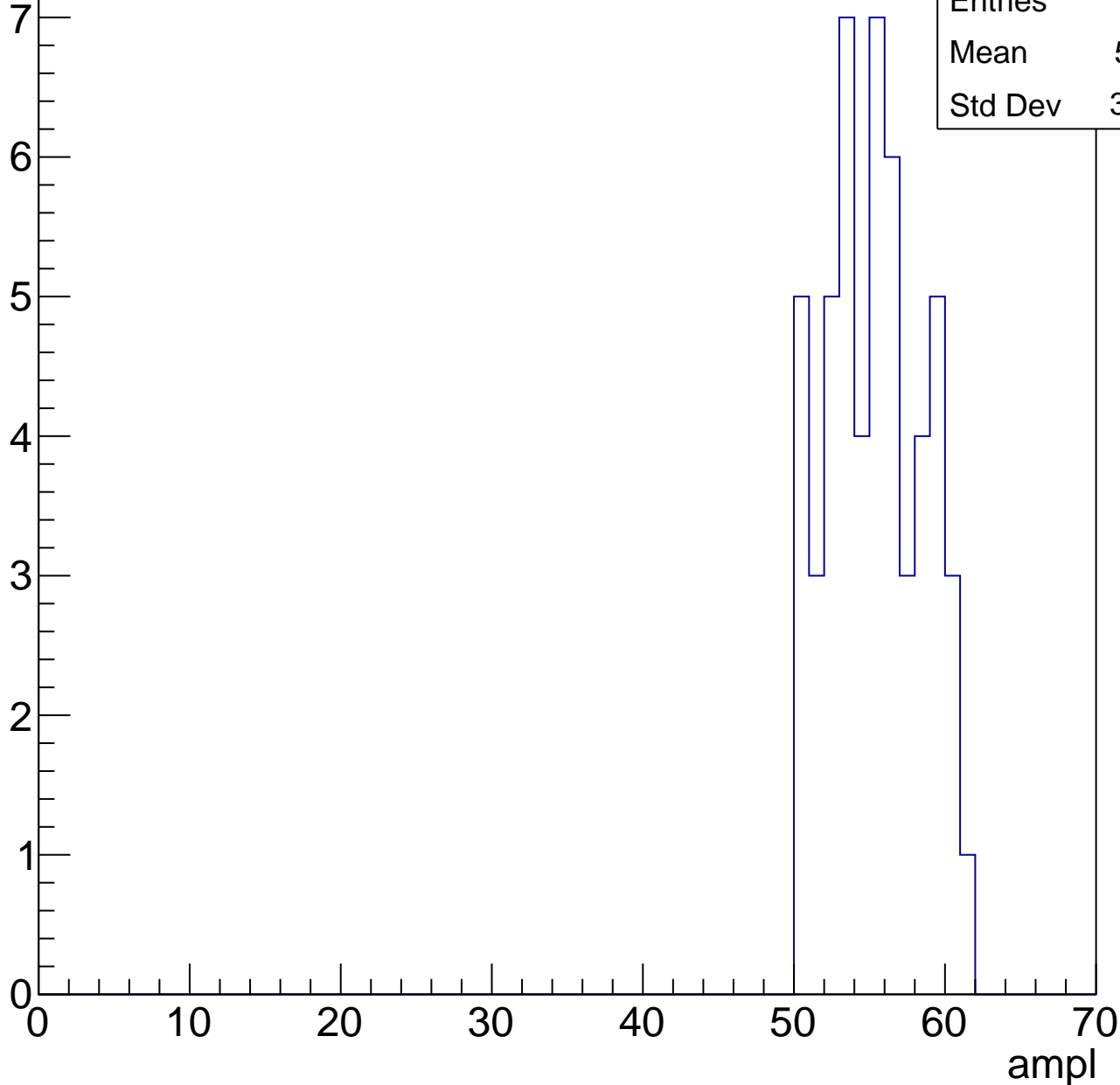
Entry



B1L103S, U2-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

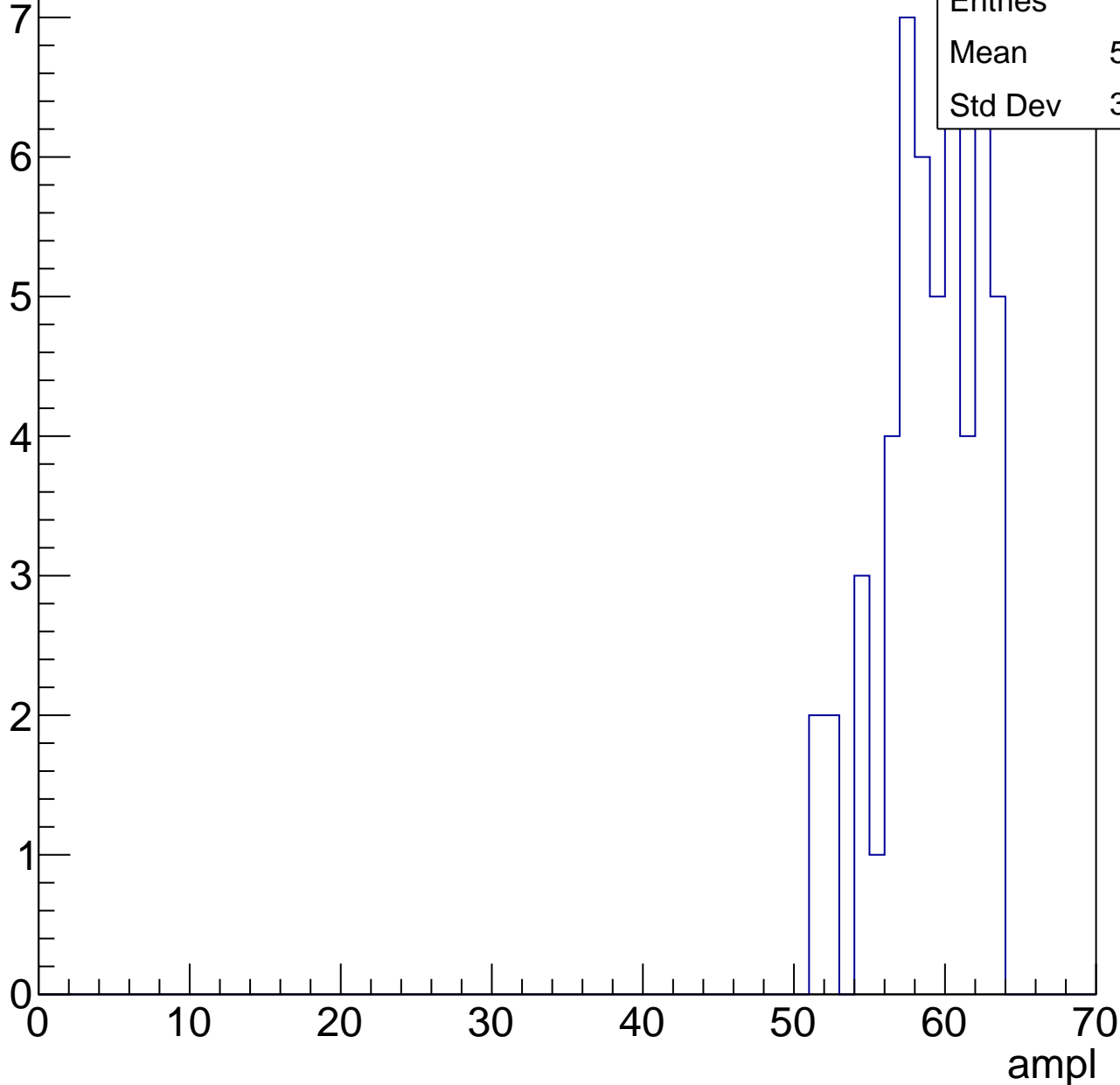
Entry



B1L103S, U2-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

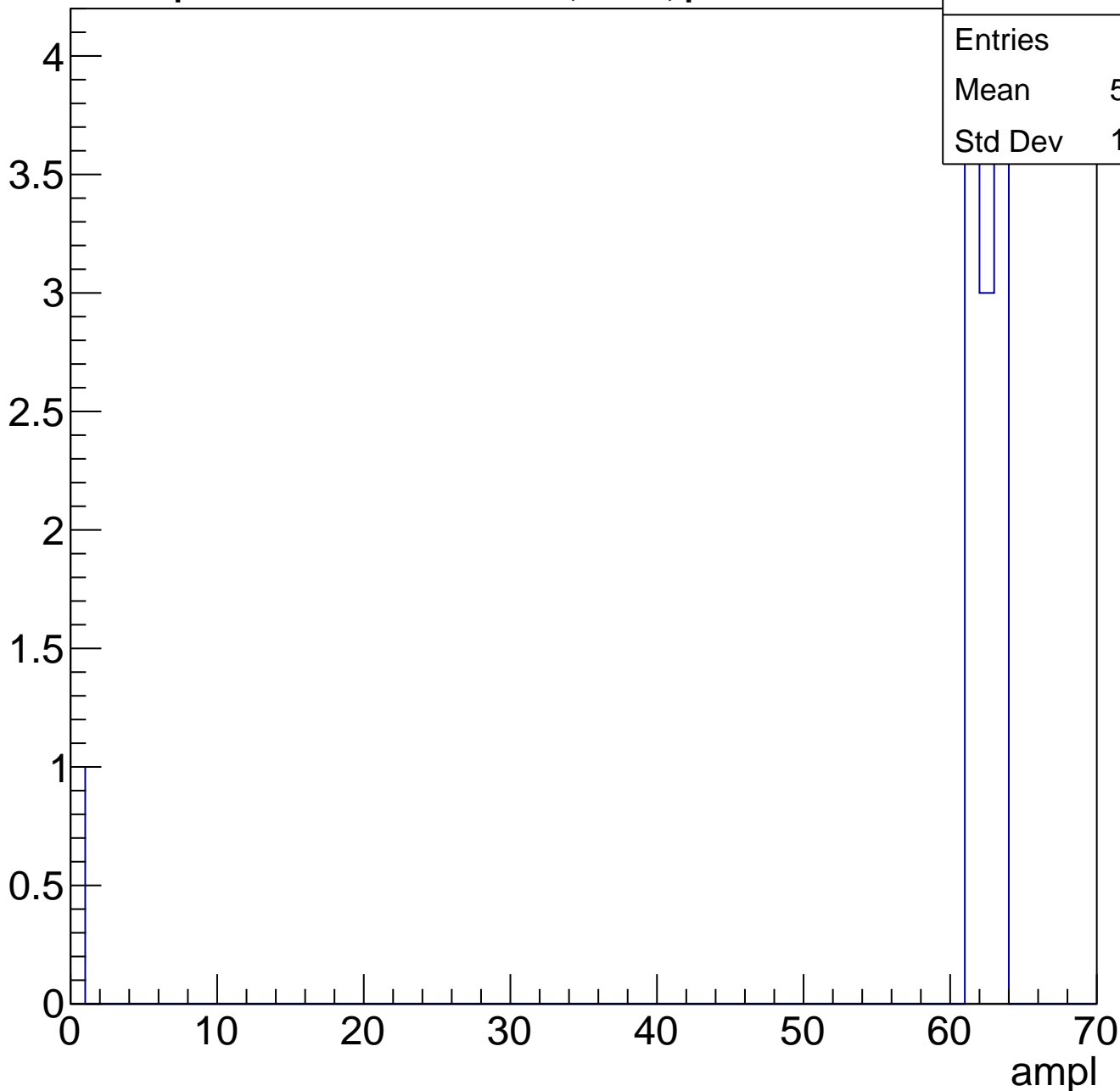
Entry



B1L103S, U2-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U2-ch90, adc0

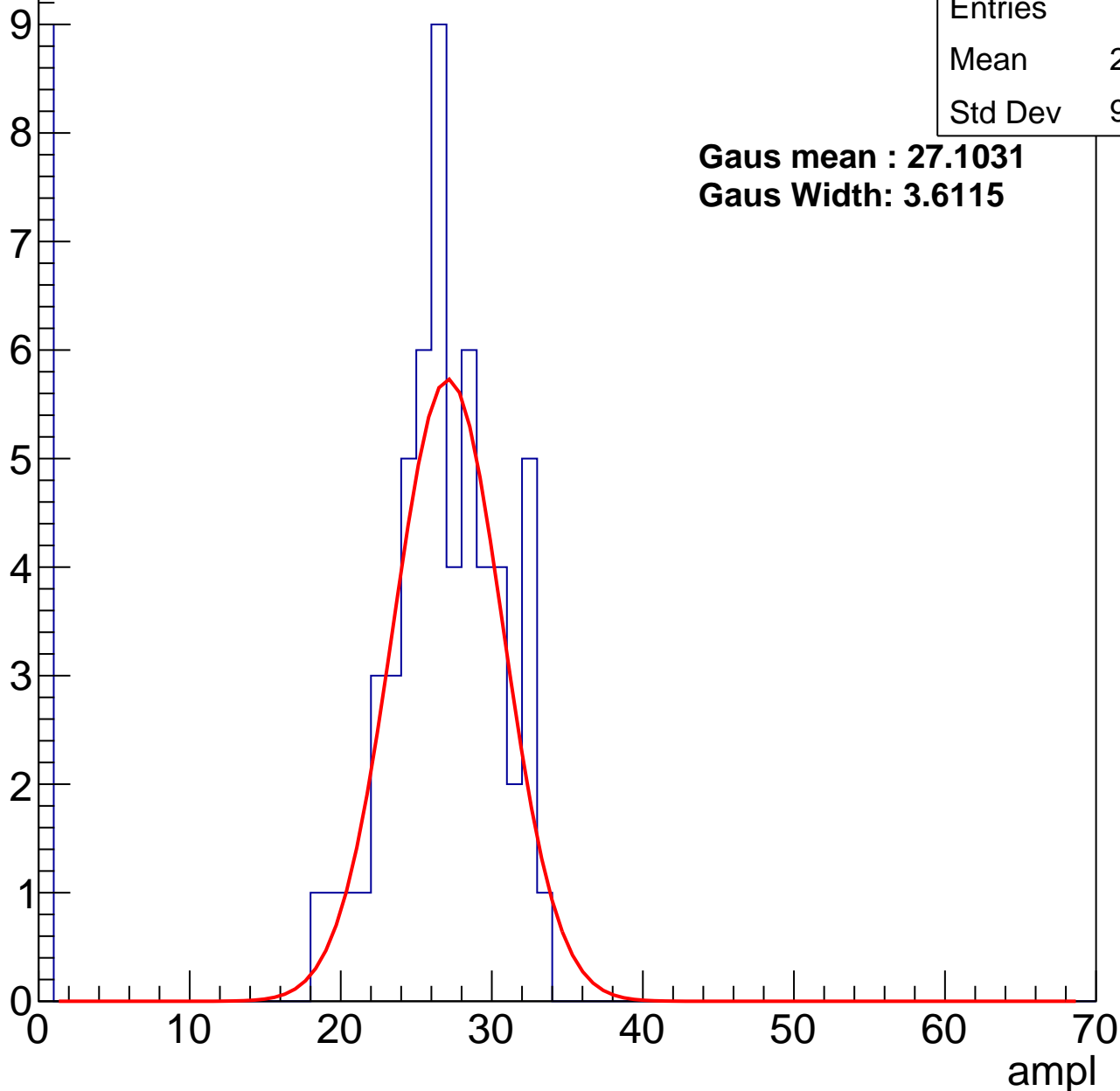
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	22.83
Std Dev	9.698

Gaus mean : 27.1031

Gaus Width: 3.6115



B1L103S, U2-ch90, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	32.52
Std Dev	5.17

Gaus mean : 33.3309

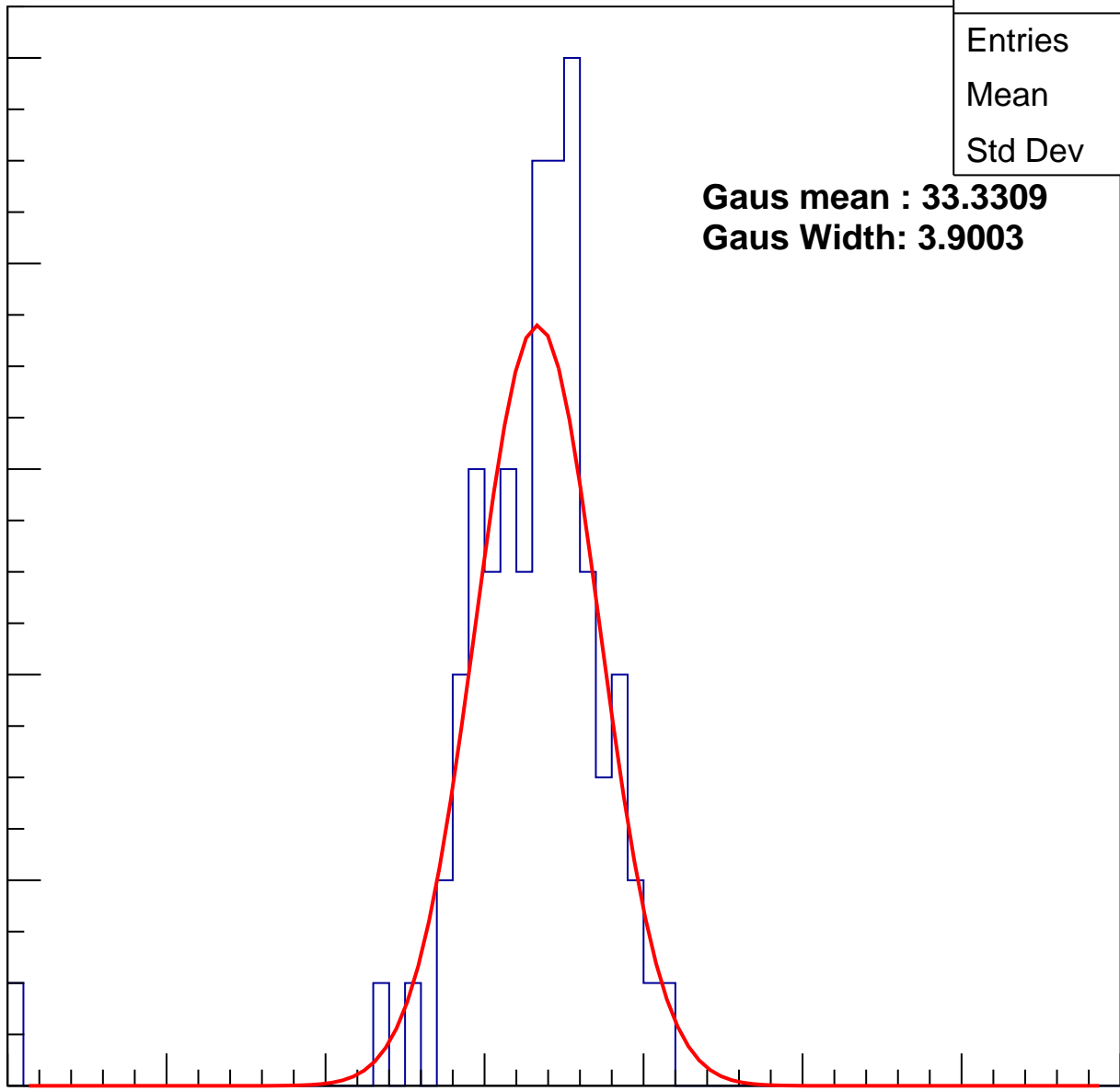
Gaus Width: 3.9003

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch90, adc2

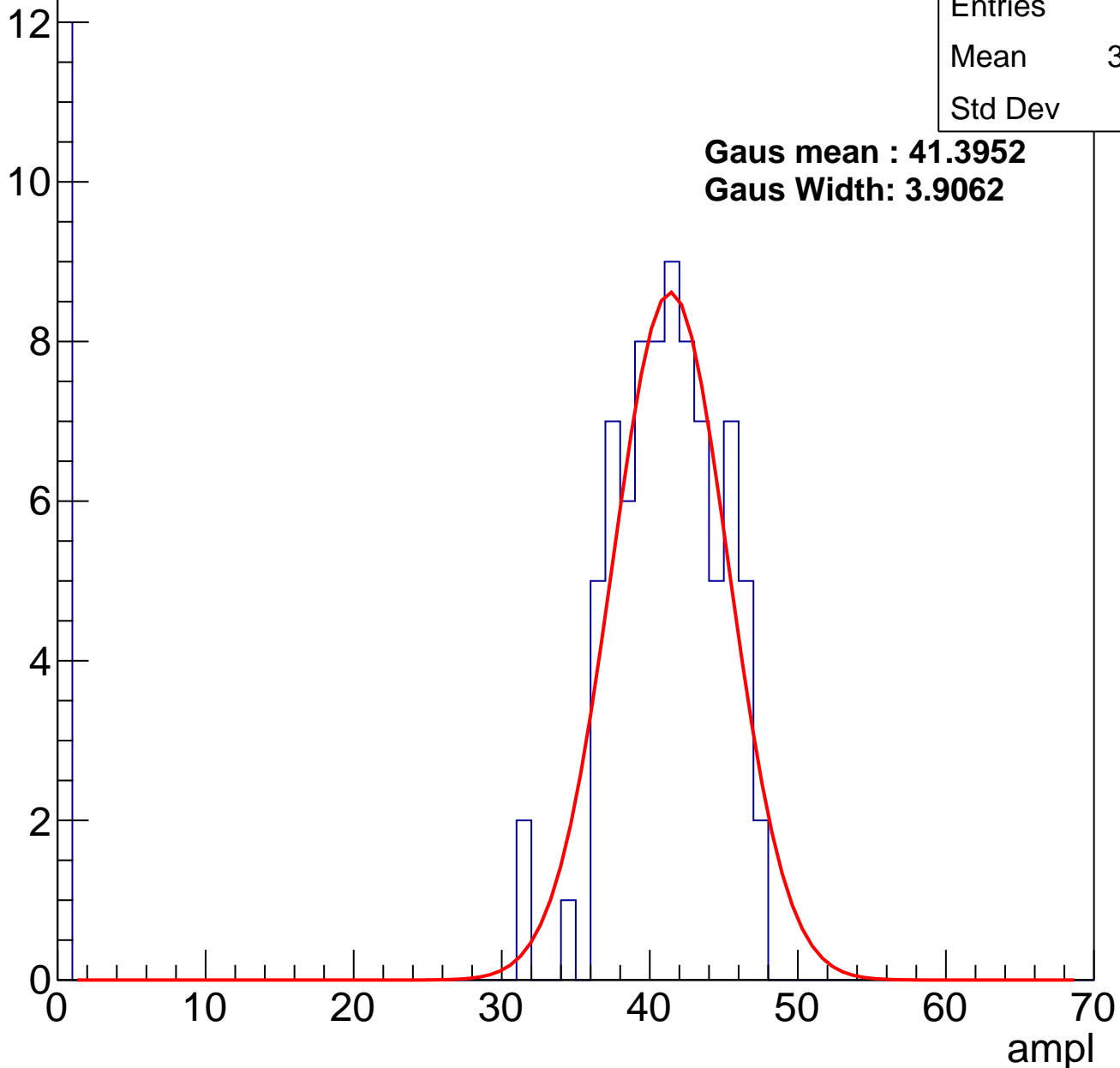
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	35.43
Std Dev	14.1

Gaus mean : 41.3952

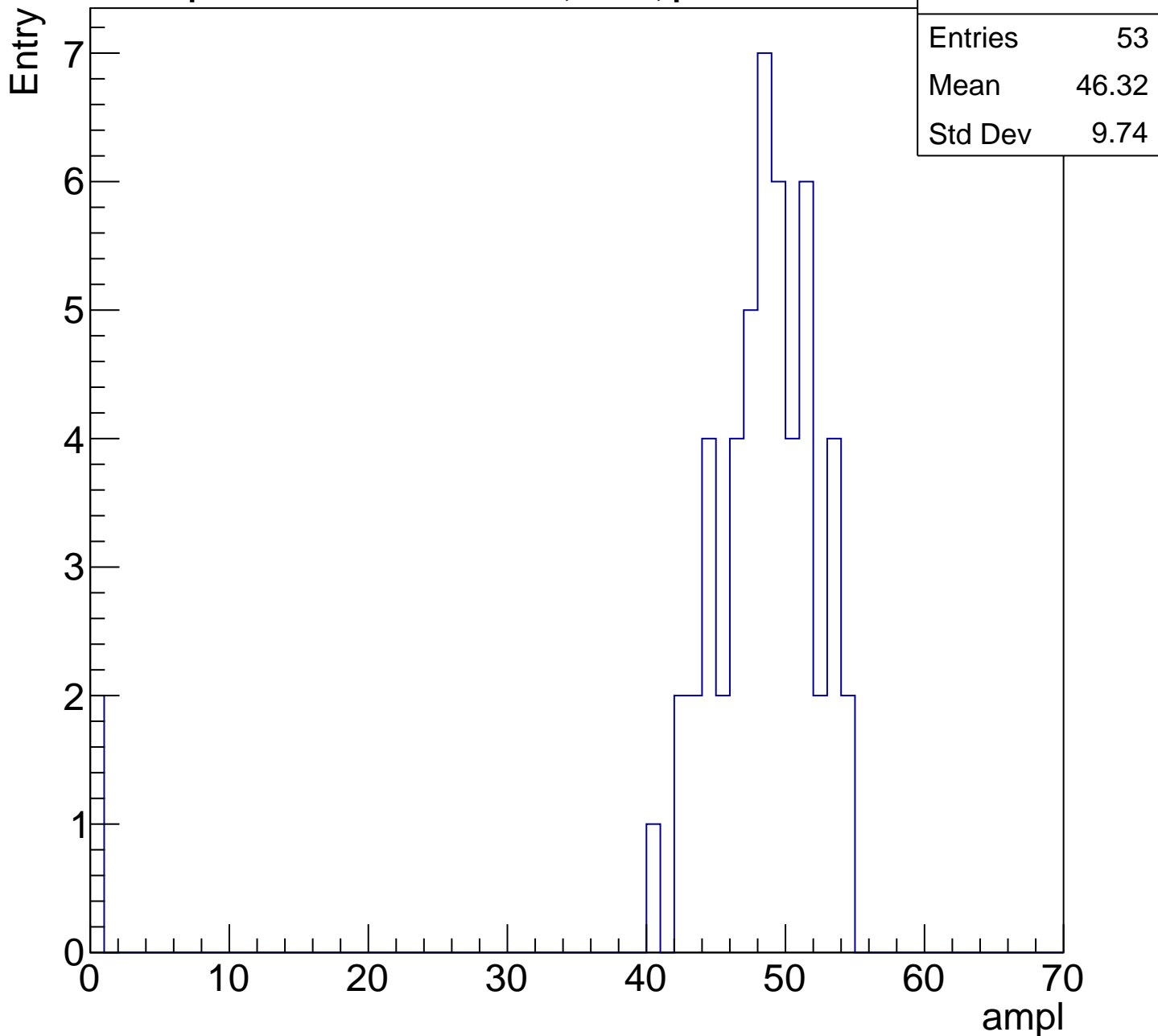
Gaus Width: 3.9062

Entry



B1L103S, U2-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

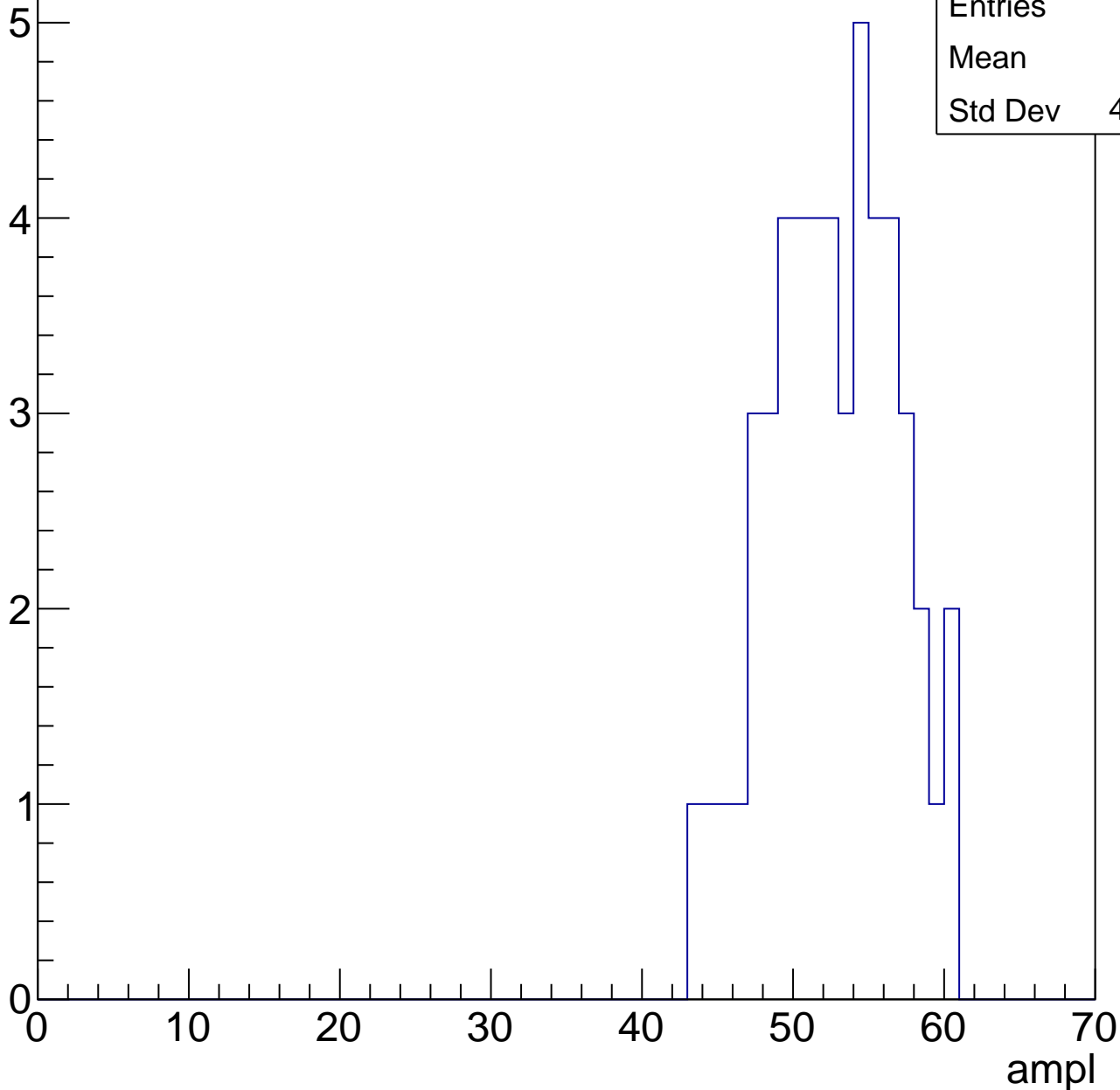


B1L103S, U2-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	52.2
Std Dev	4.133

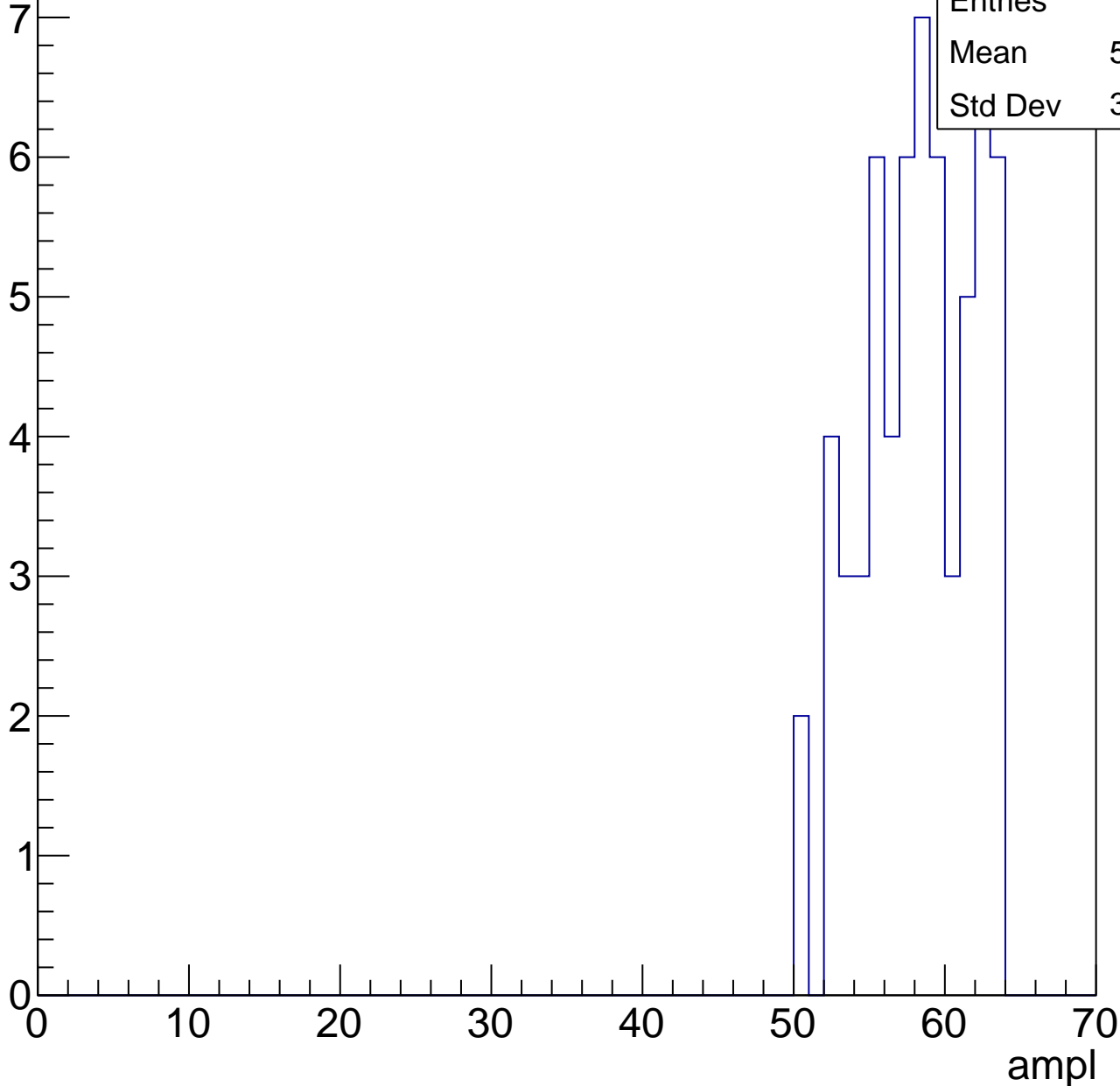


B1L103S, U2-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	57.77
Std Dev	3.576

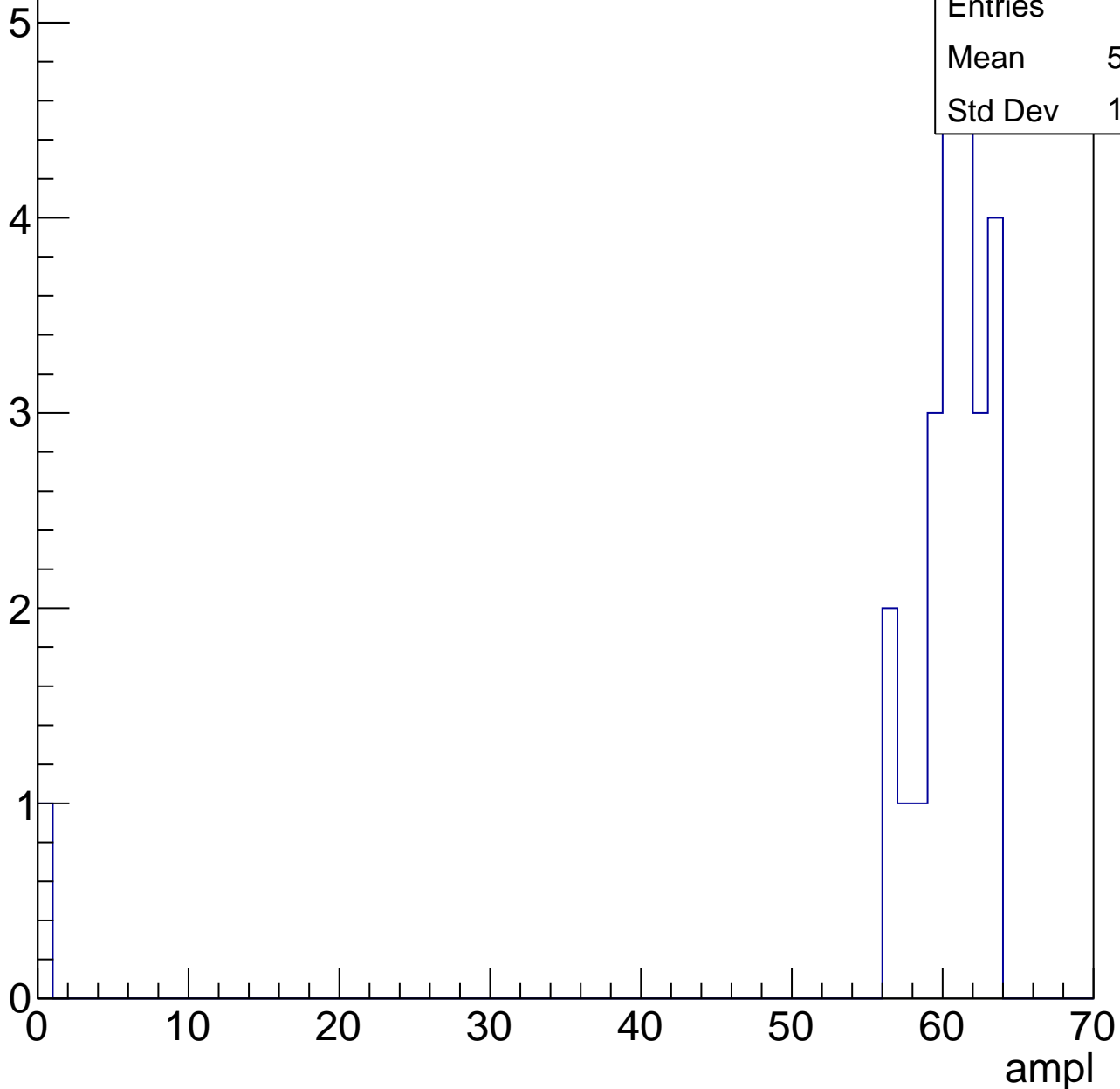


B1L103S, U2-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	57.88
Std Dev	11.98



B1L103S, U2-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch91, adc0

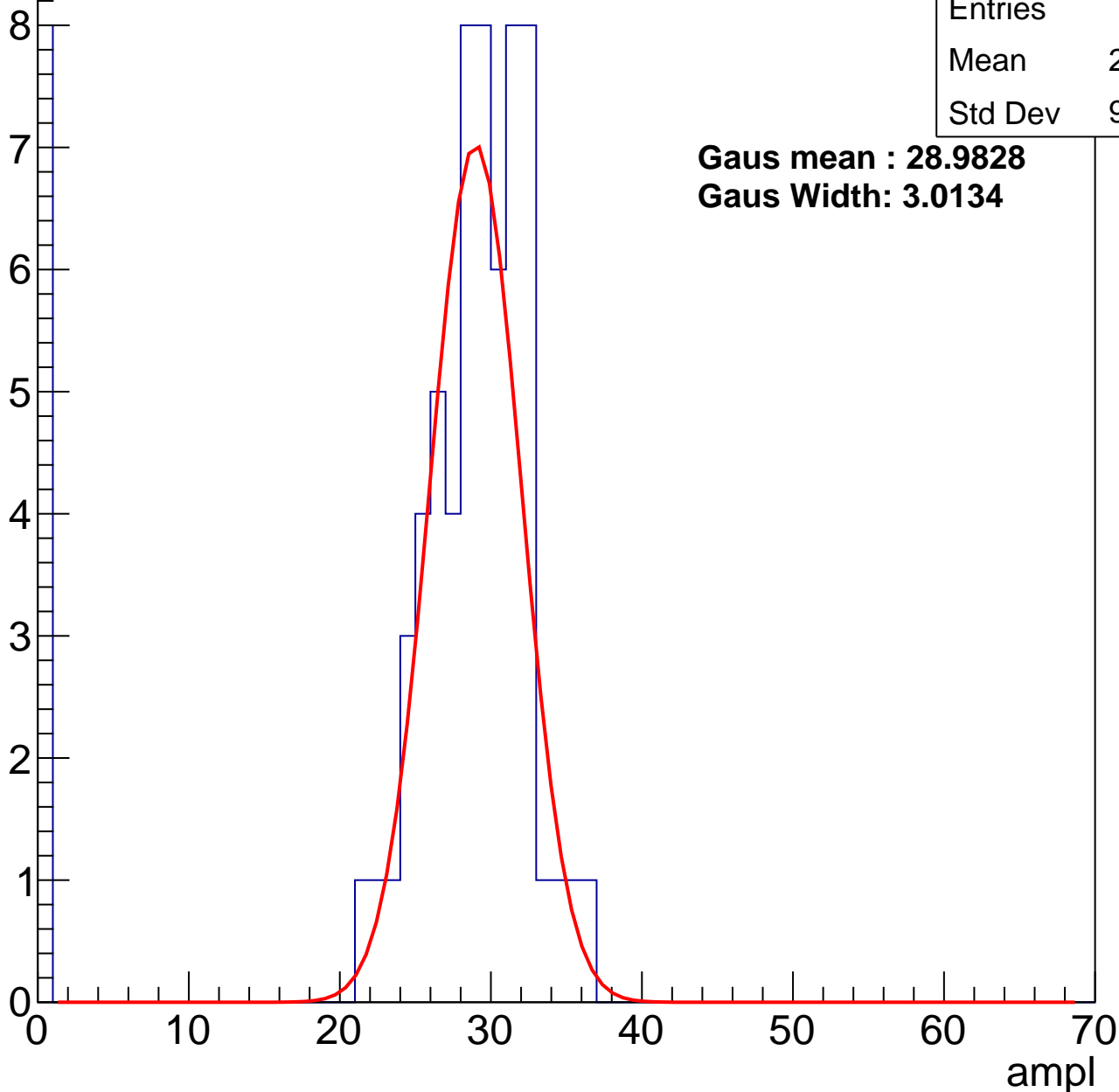
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	25.42
Std Dev	9.659

Gaus mean : 28.9828

Gaus Width: 3.0134



B1L103S, U2-ch91, adc1

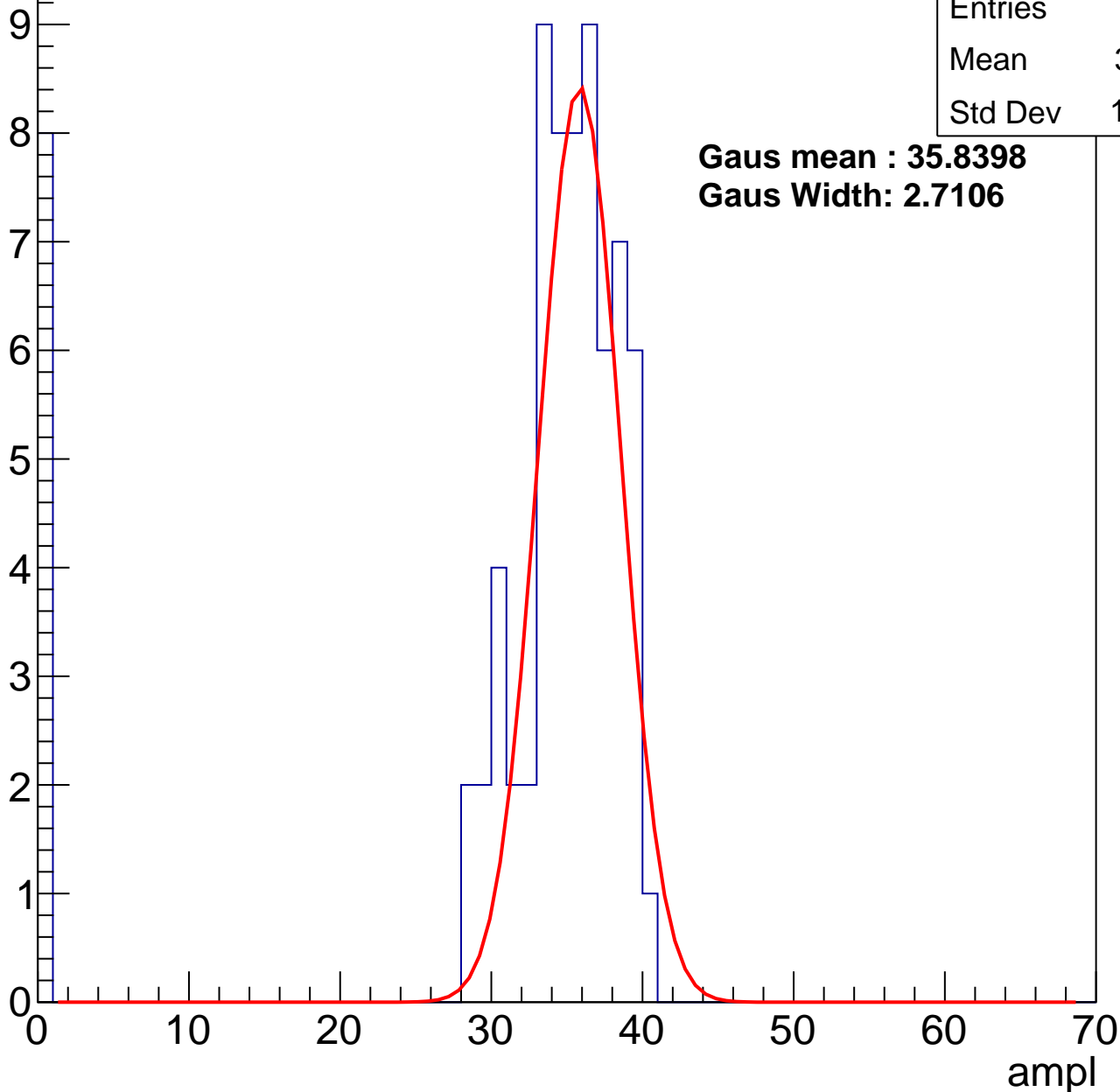
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	31.01
Std Dev	11.15

Gaus mean : 35.8398

Gaus Width: 2.7106



B1L103S, U2-ch91, adc2

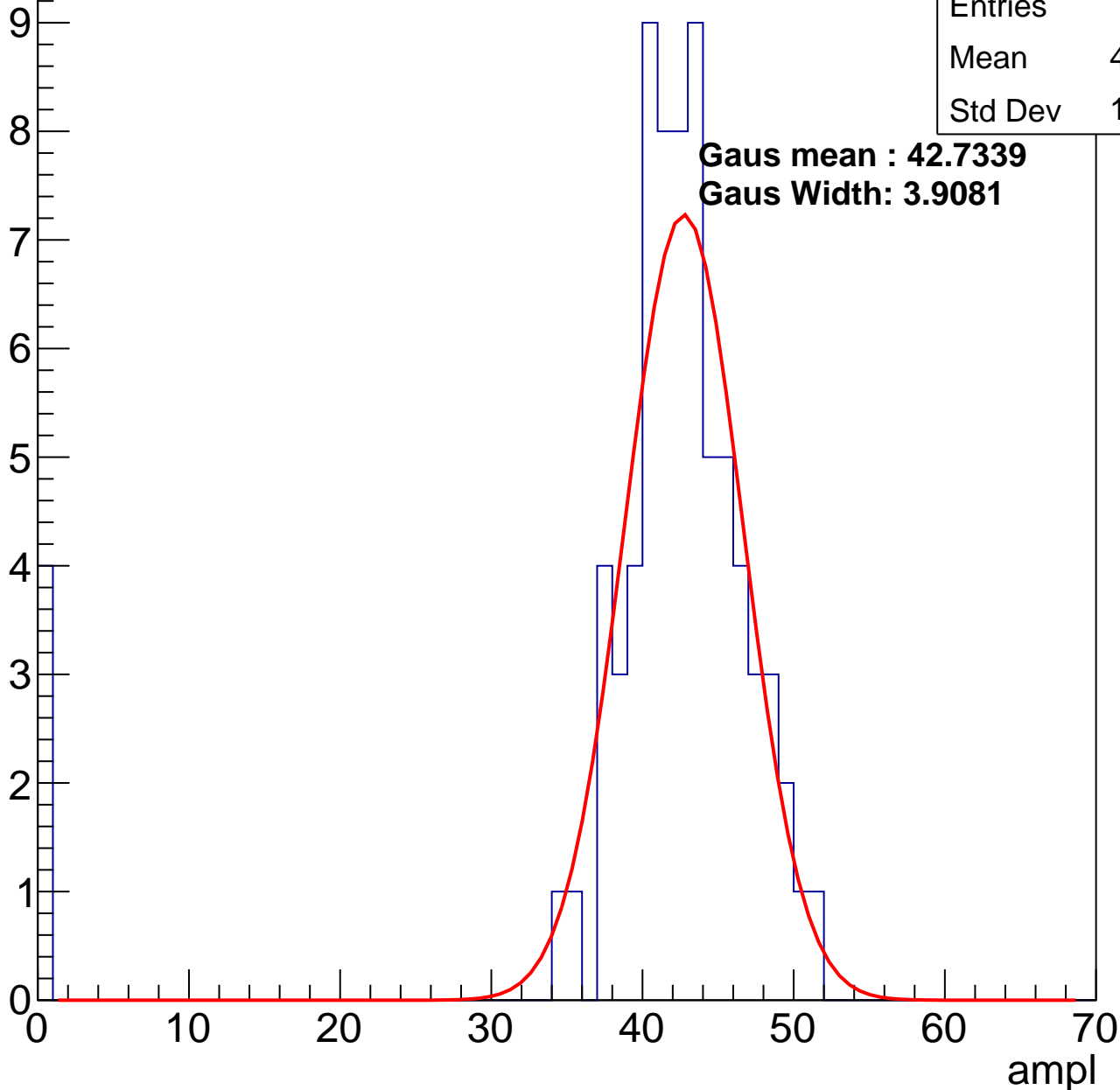
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	40.15
Std Dev	10.13

Gaus mean : 42.7339

Gaus Width: 3.9081



B1L103S, U2-ch91, adc3

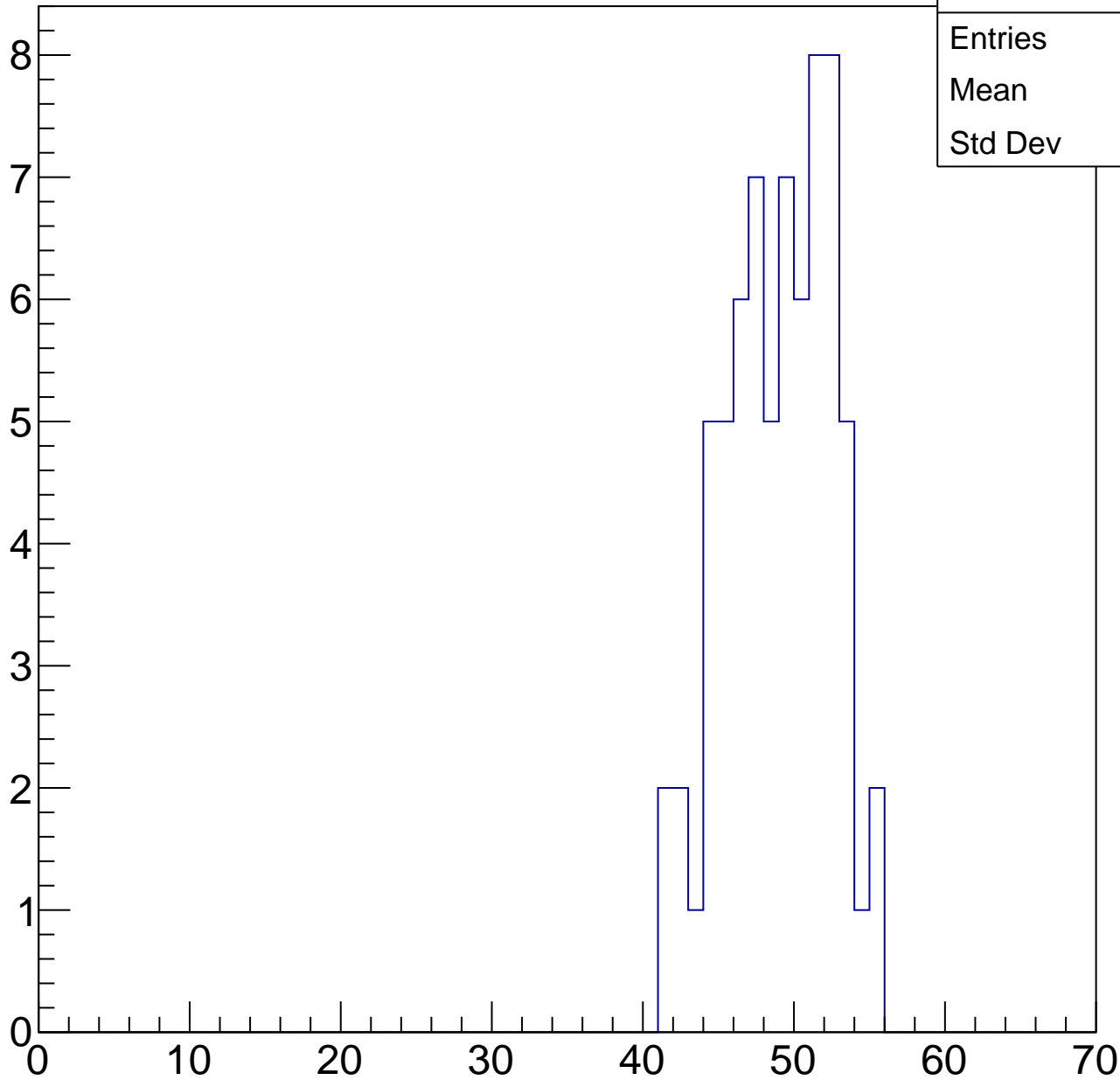
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

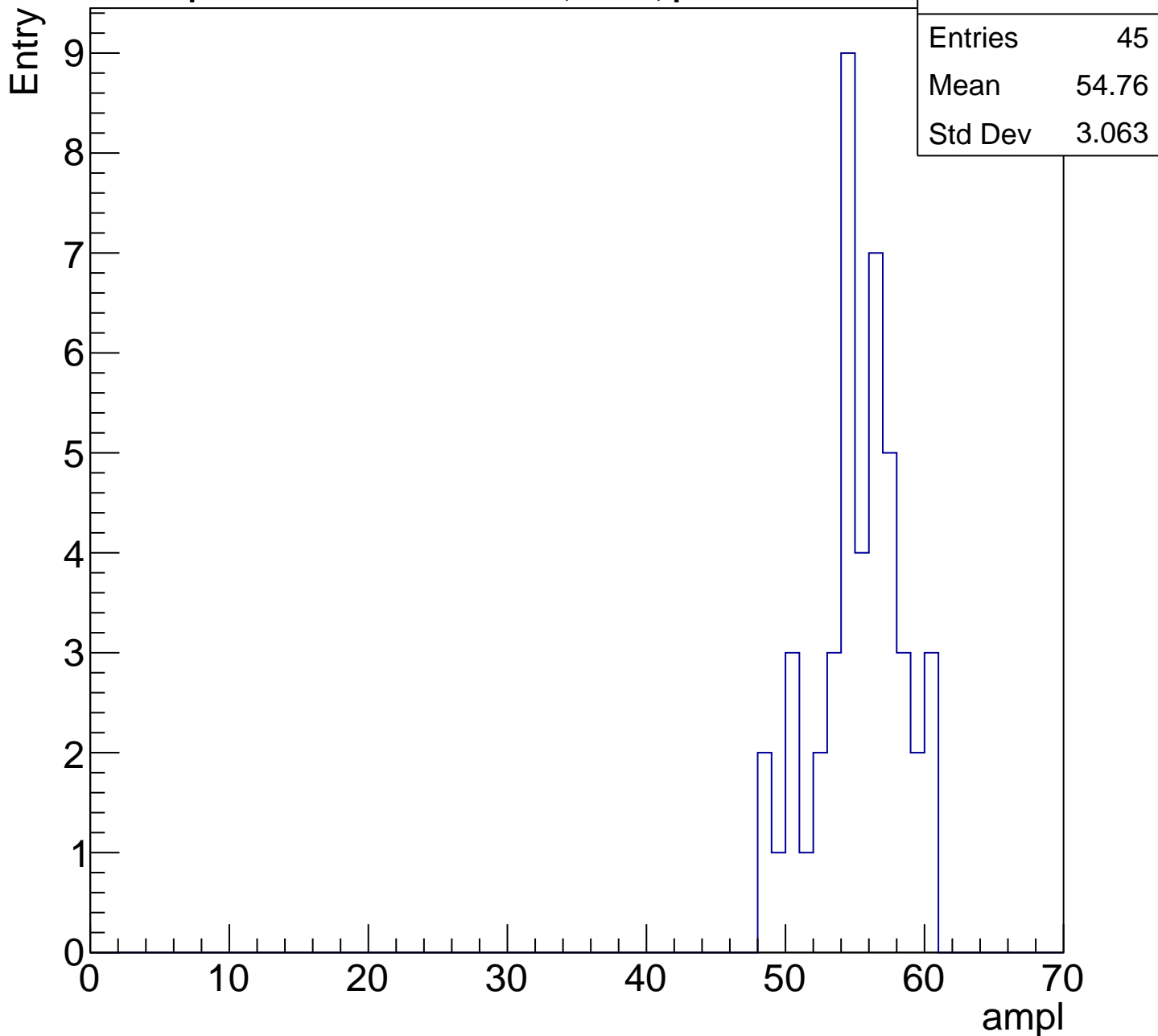
Entries	70
Mean	48.5
Std Dev	3.43

ampl



B1L103S, U2-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2

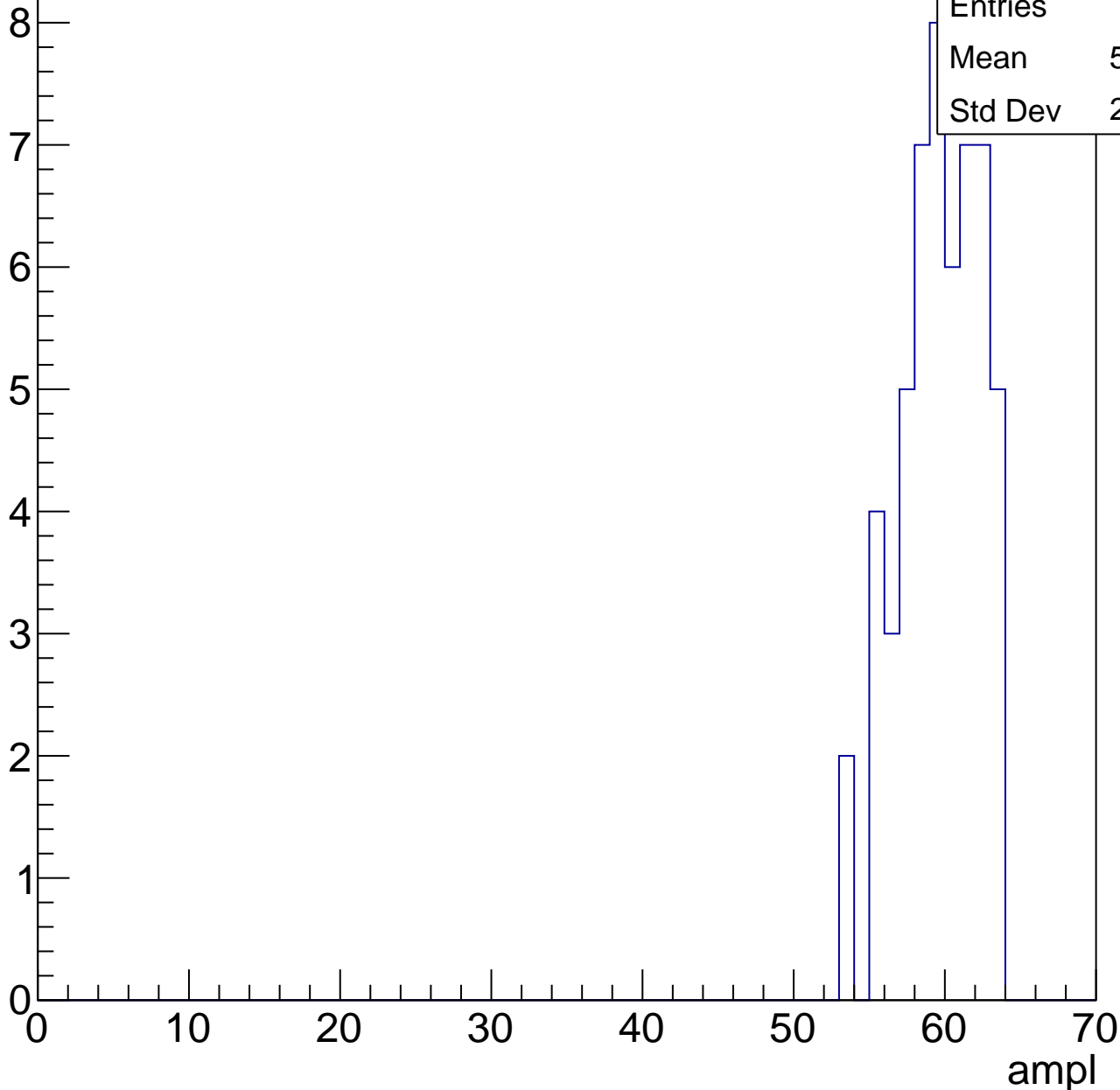


B1L103S, U2-ch91, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

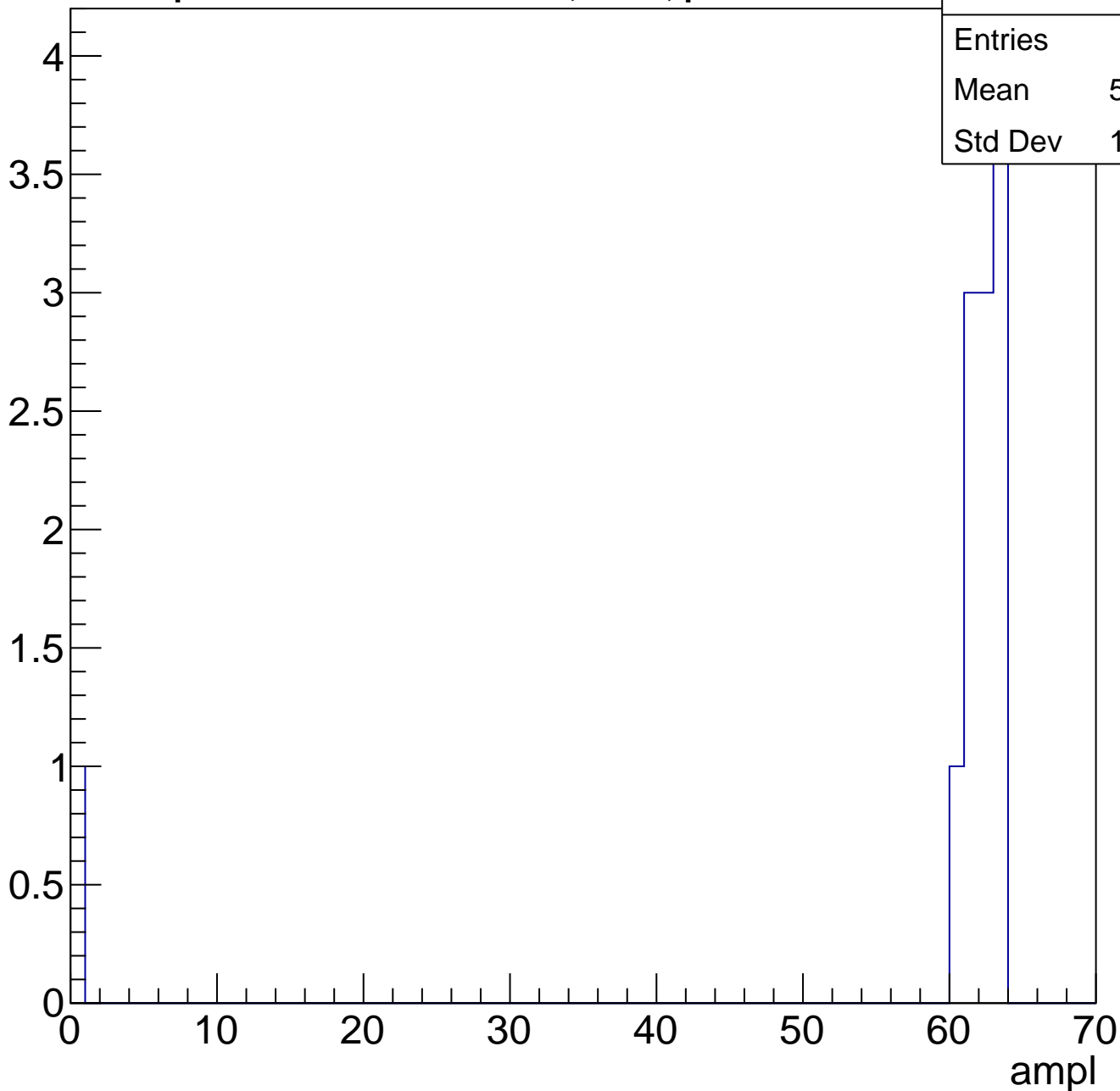
Entries	54
Mean	59.13
Std Dev	2.604



B1L103S, U2-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

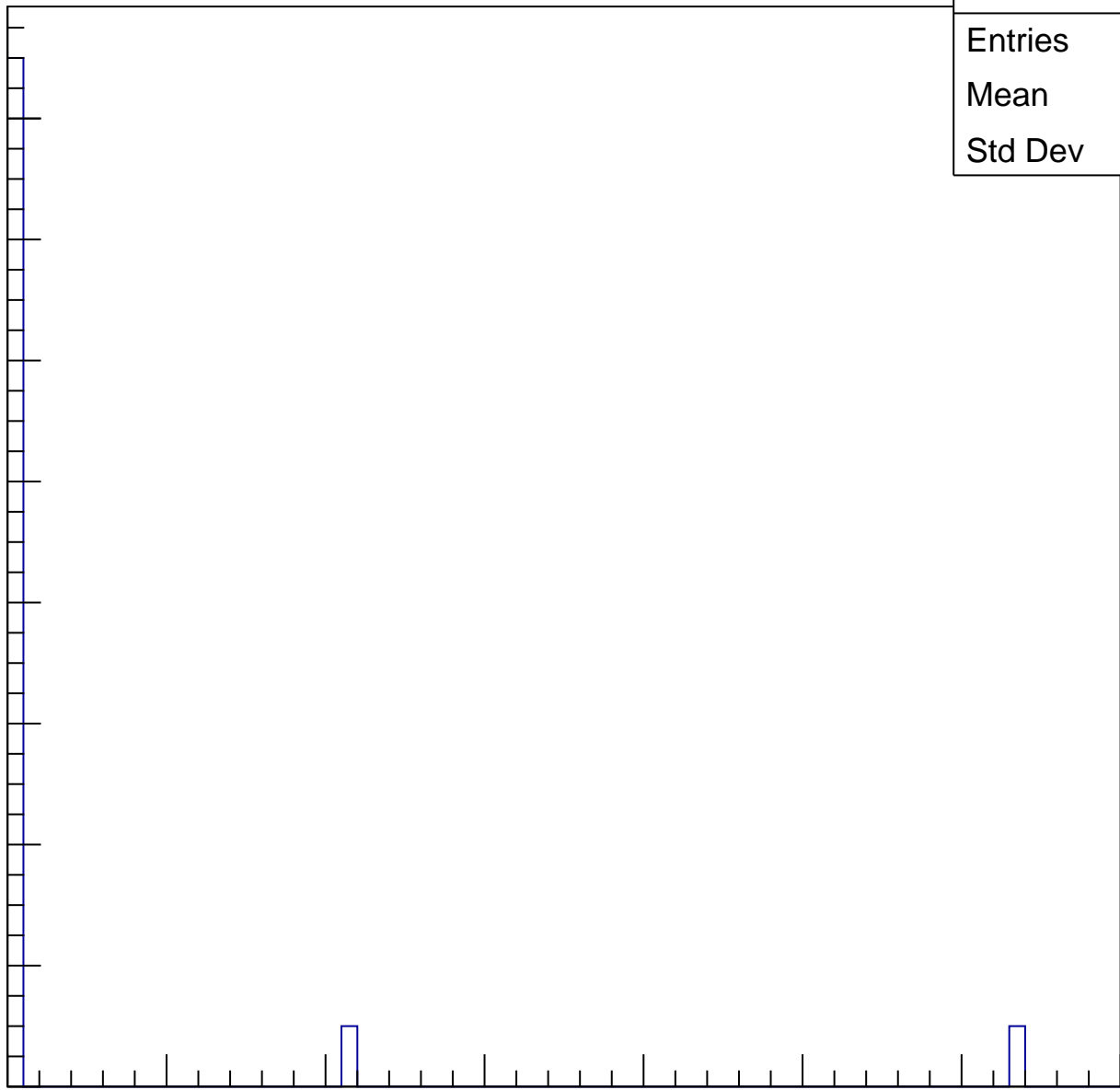
Entries	19
Mean	4.421
Std Dev	14.58

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch92, adc0

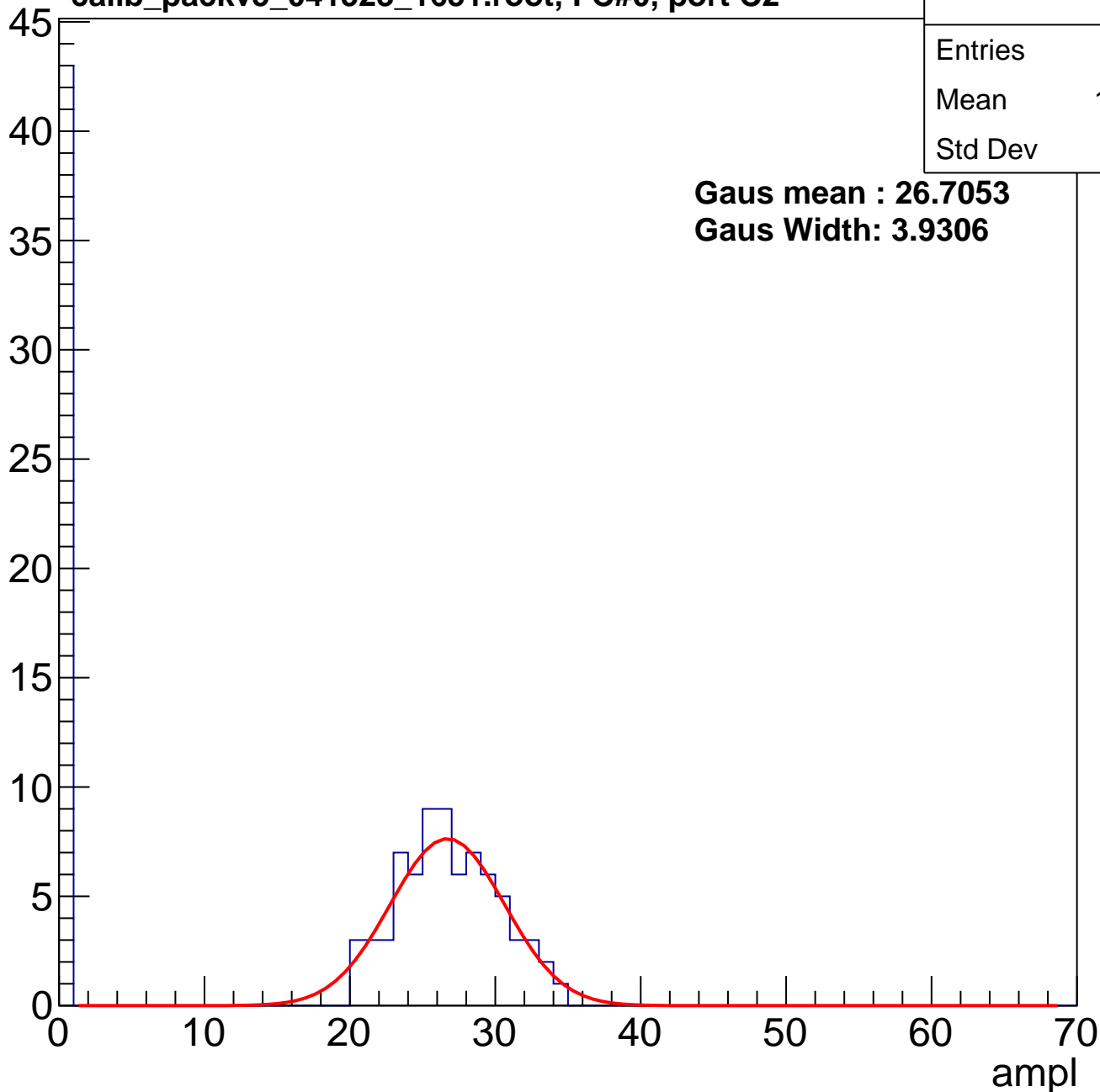
calib_packv5_041523_1651.root, FC#0, port C2

Entries	116
Mean	16.59
Std Dev	13.01

Gaus mean : 26.7053

Gaus Width: 3.9306

Entry



B1L103S, U2-ch92, adc1

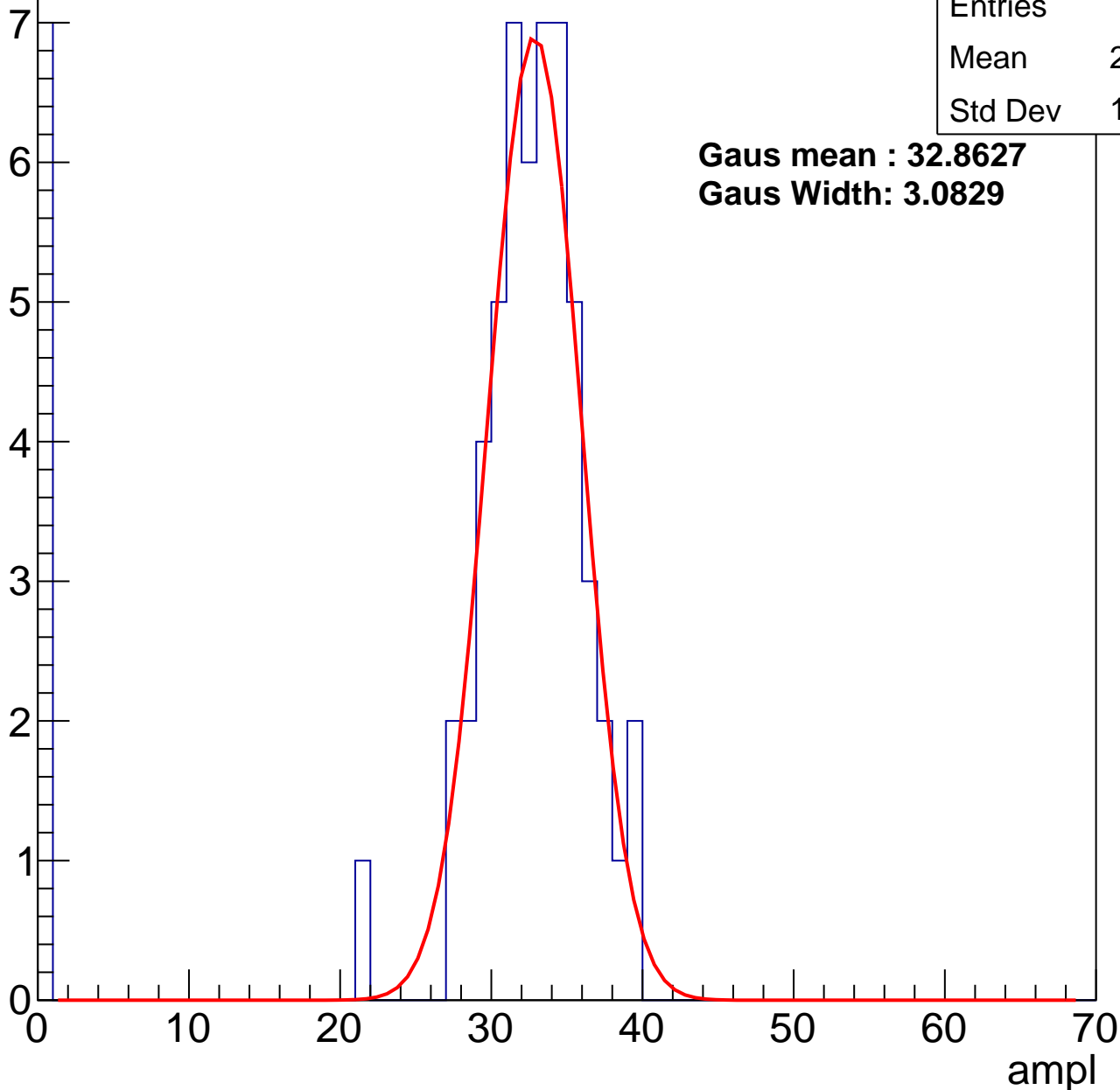
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	28.66
Std Dev	10.76

Gaus mean : 32.8627

Gaus Width: 3.0829



B1L103S, U2-ch92, adc2

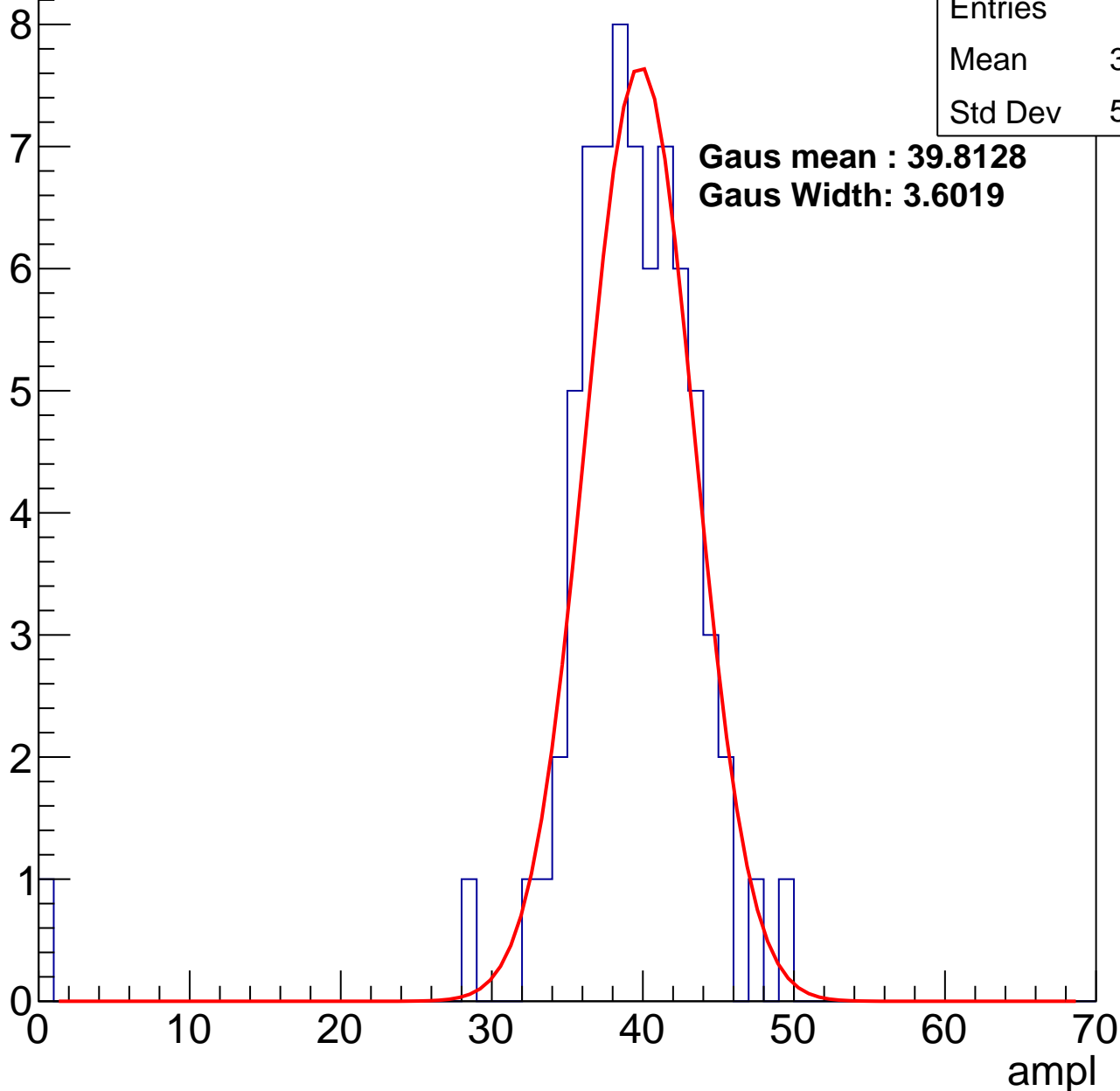
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	38.54
Std Dev	5.838

Gaus mean : 39.8128

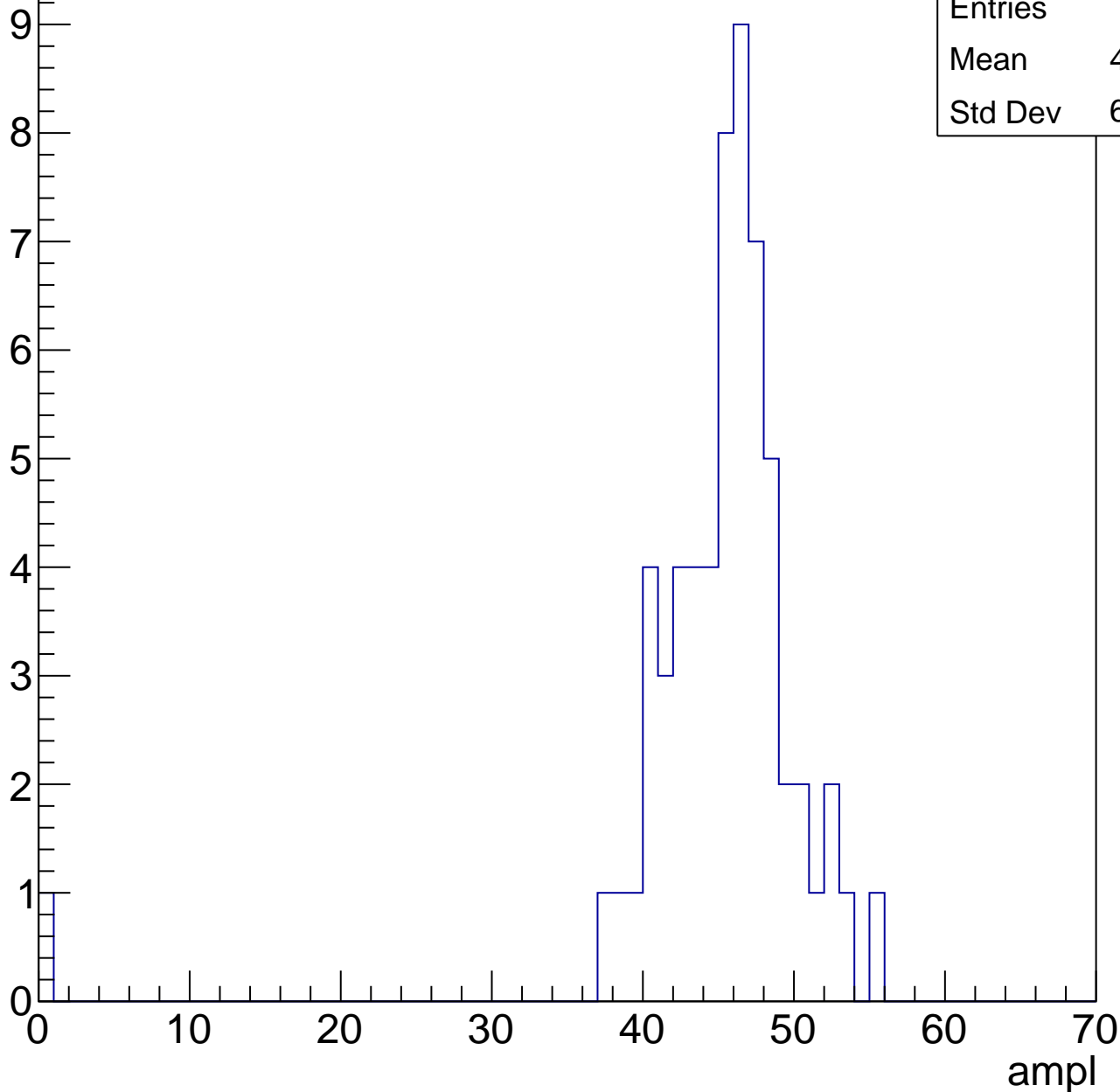
Gaus Width: 3.6019



B1L103S, U2-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

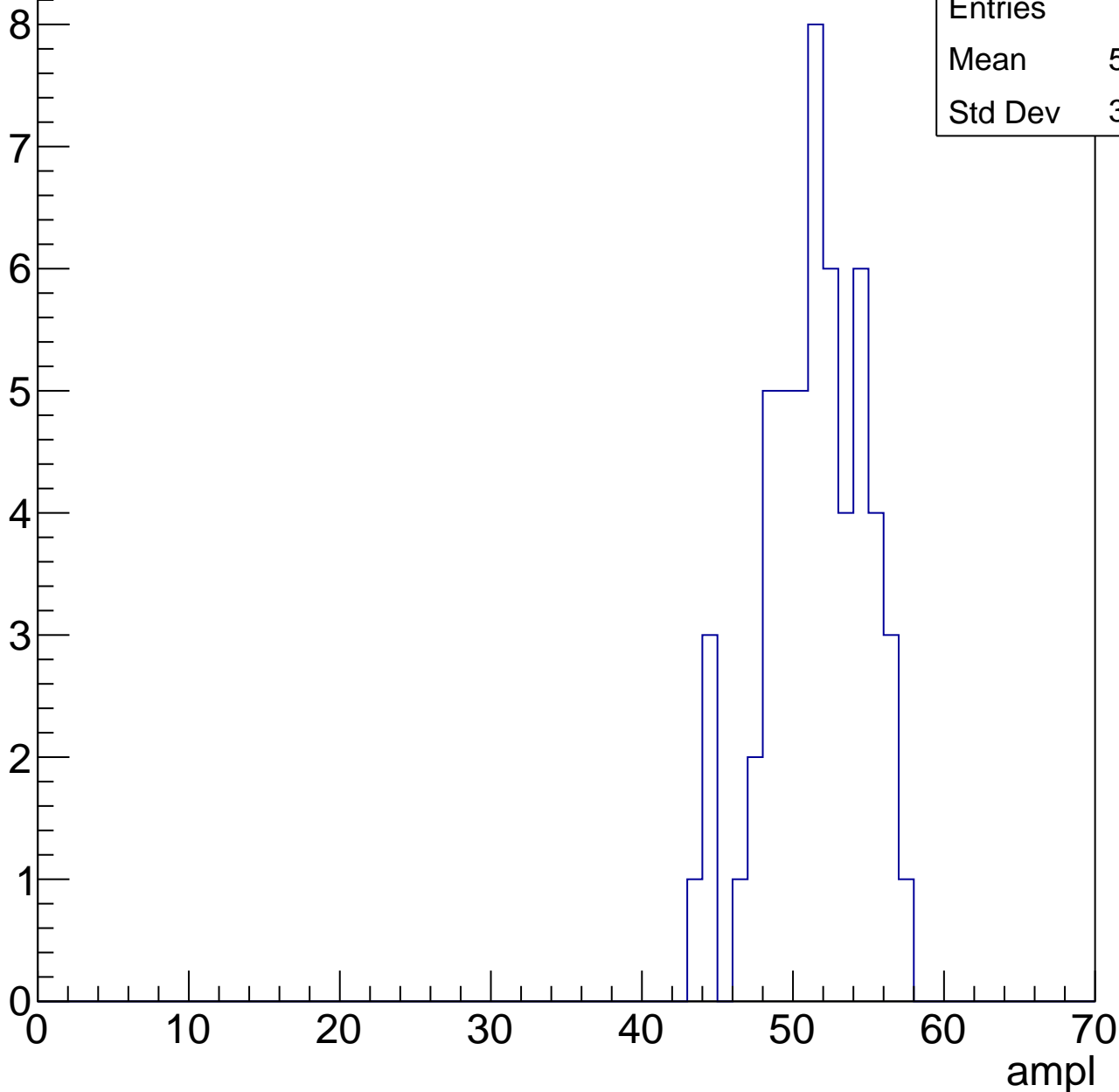


B1L103S, U2-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

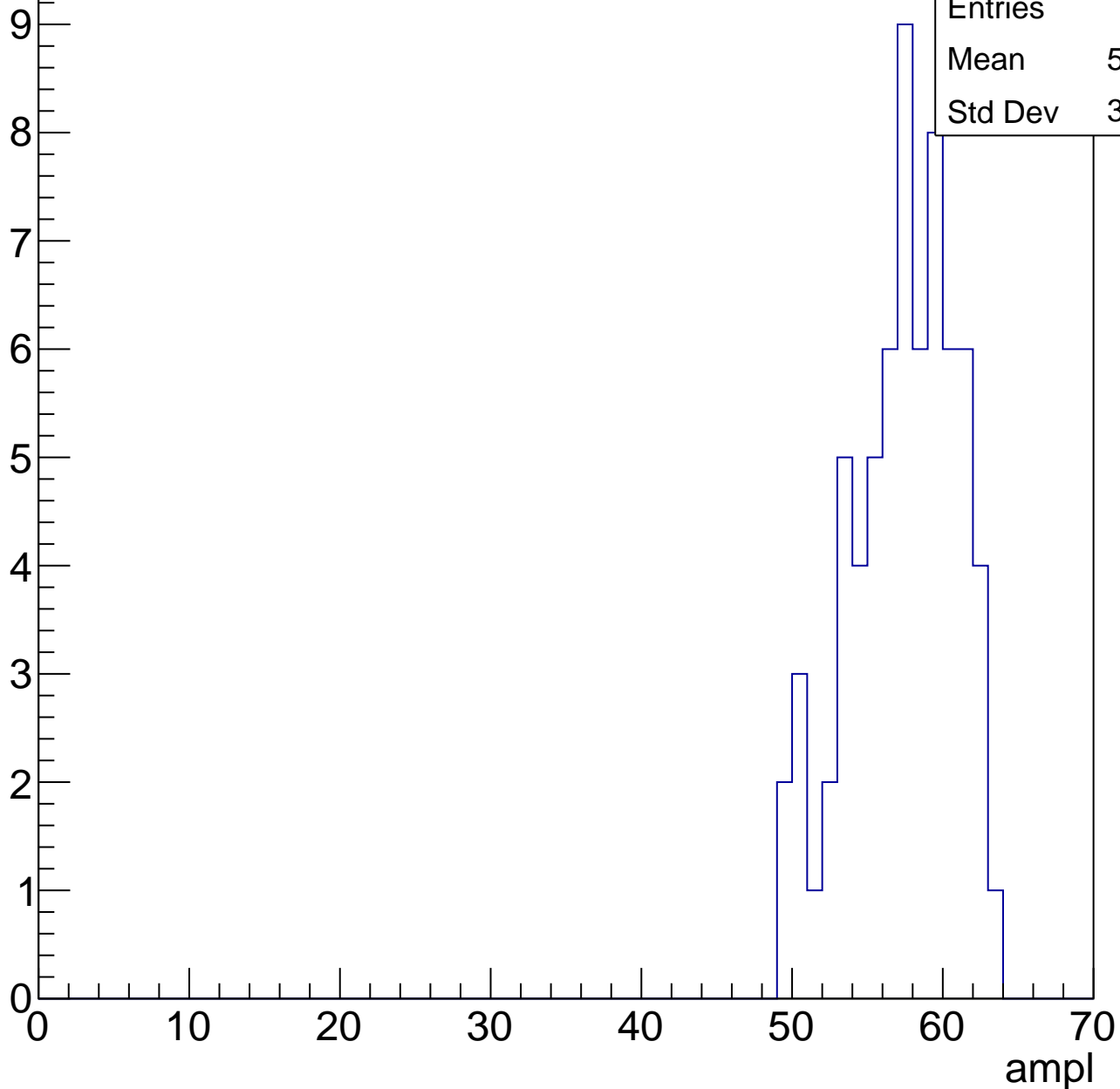
Entries	54
Mean	50.94
Std Dev	3.302



B1L103S, U2-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

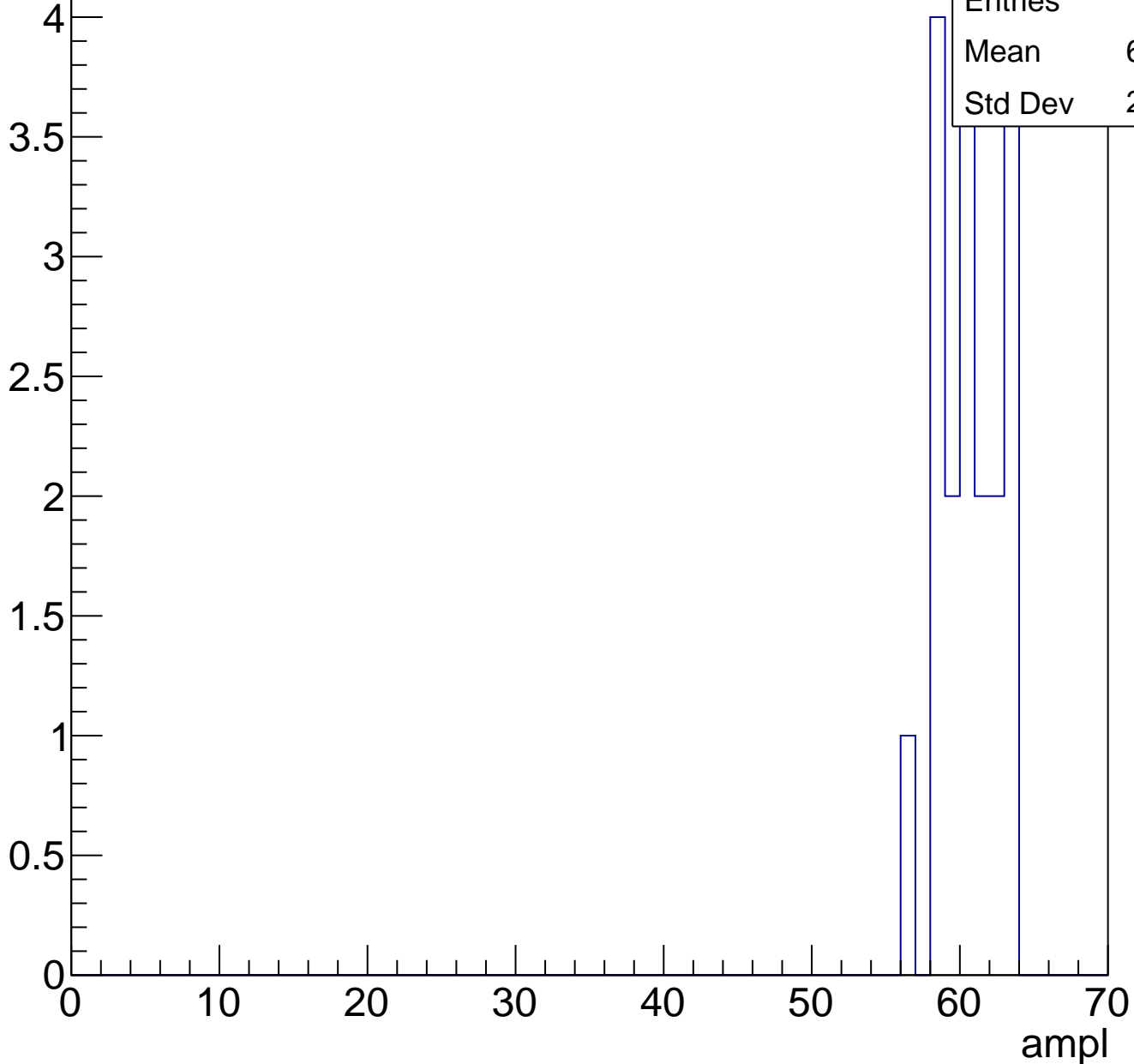
Entry



B1L103S, U2-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	31
Mean	22.1
Std Dev	29.8

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch93, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	17.84
Std Dev	13.84

Gaus mean : 28.8558

Gaus Width: 3.3869

Entry

30

25

20

15

10

5

0

0

10

20

30

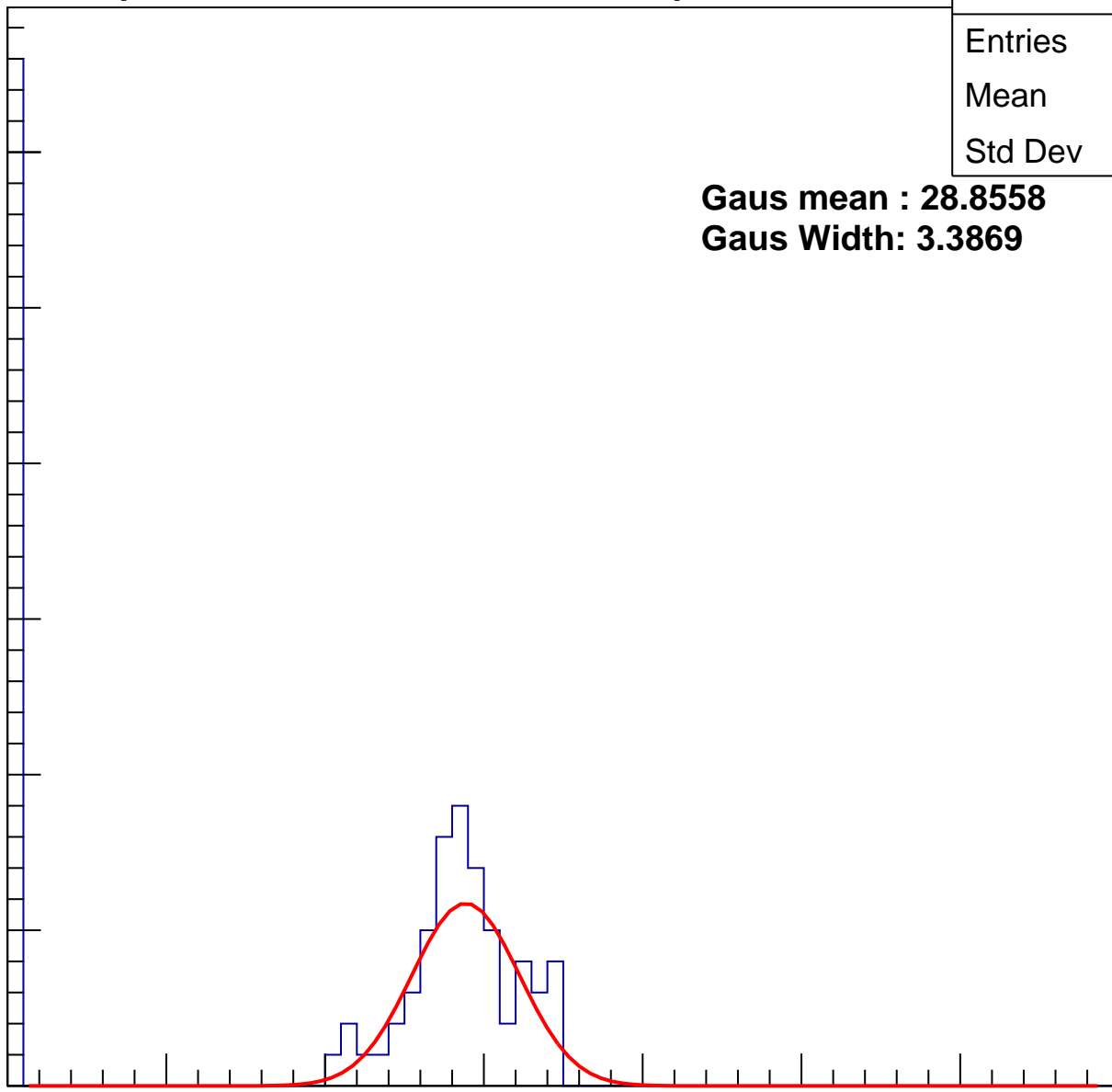
40

50

60

70

ampl



B1L103S, U2-ch93, adc1

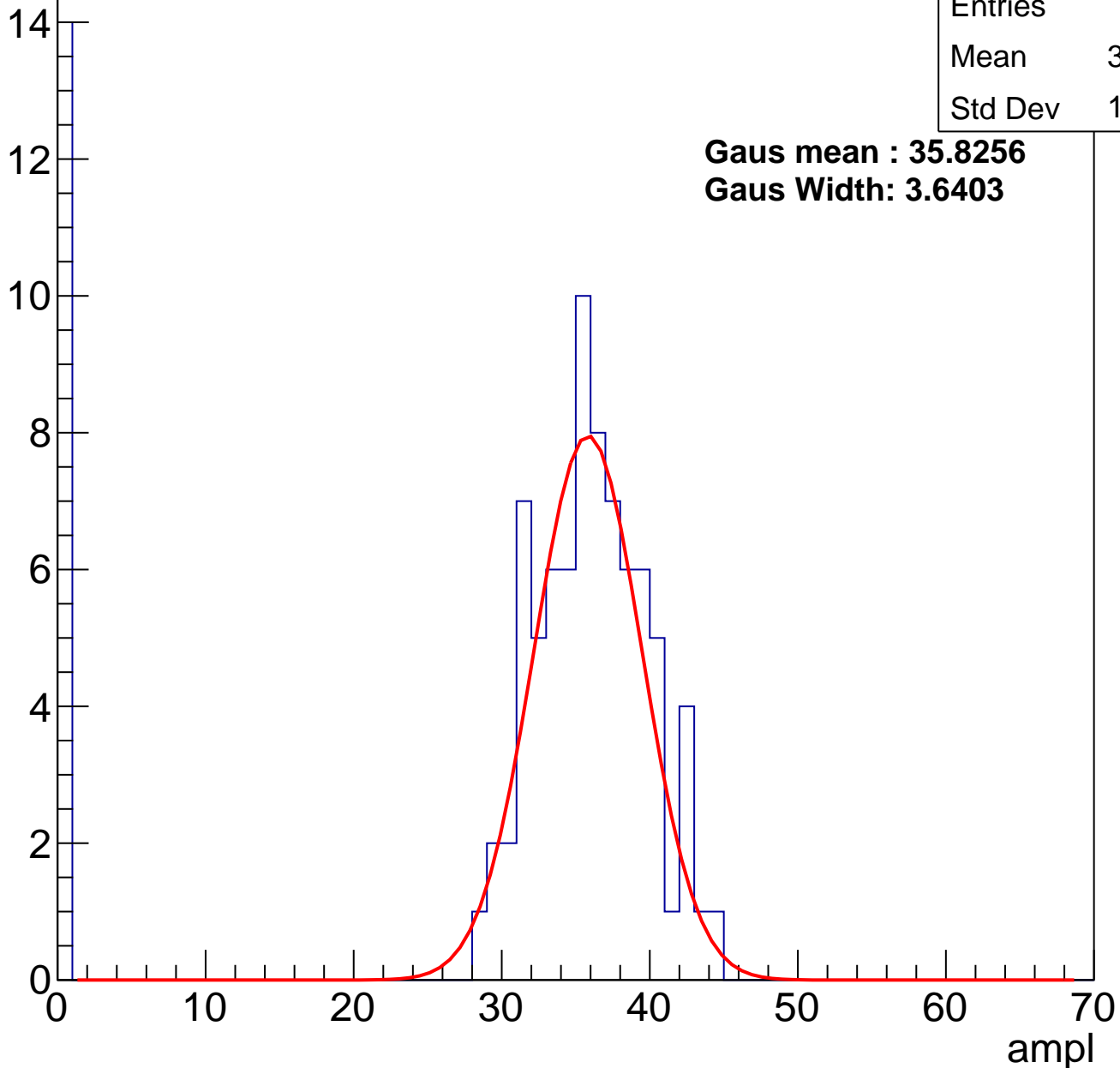
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	30.22
Std Dev	13.23

Gaus mean : 35.8256

Gaus Width: 3.6403

Entry



B1L103S, U2-ch93, adc2

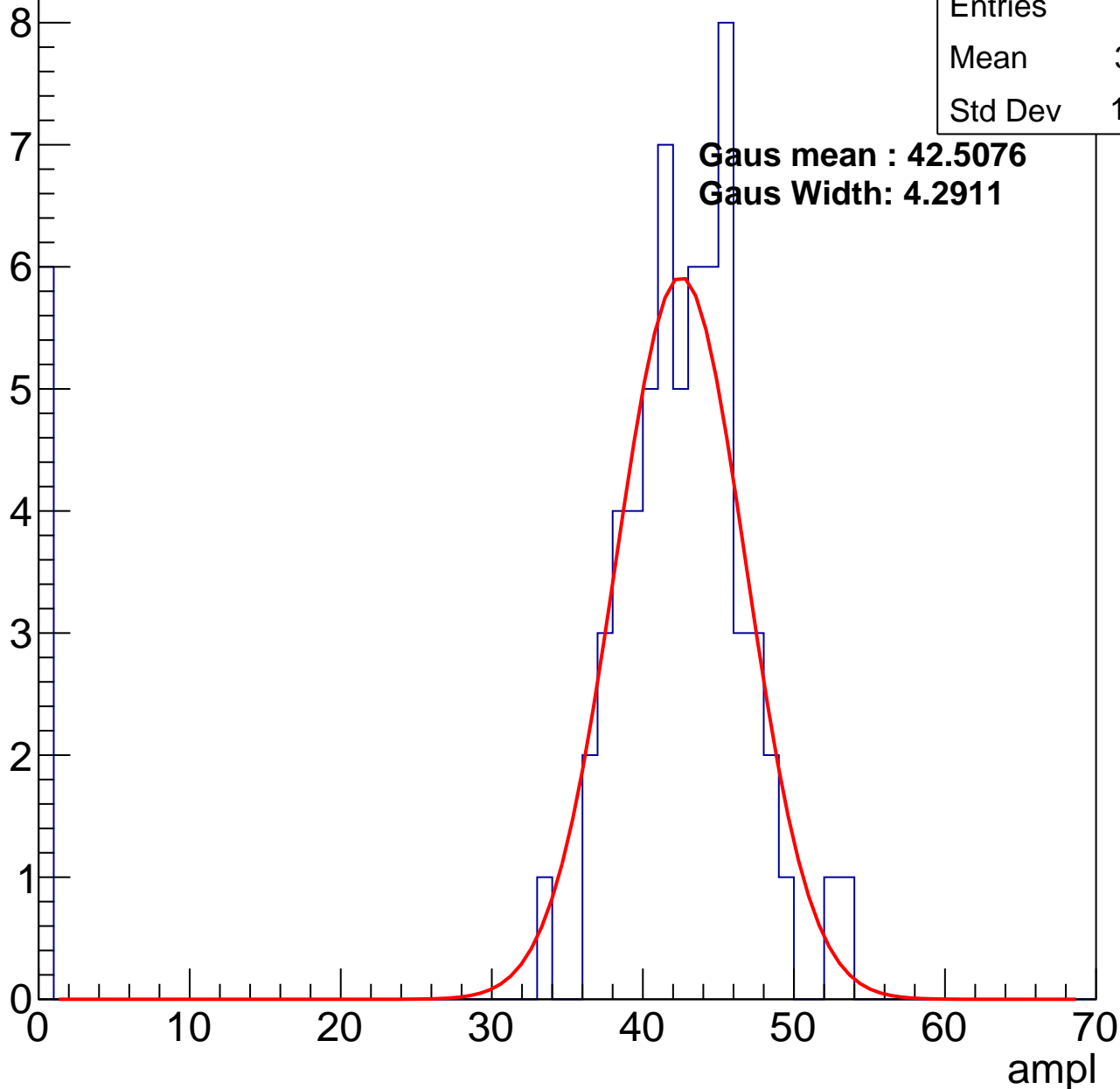
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.71
Std Dev	12.58

Gaus mean : 42.5076

Gaus Width: 4.2911



B1L103S, U2-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	48.64
Std Dev	3.366

Entry

10

8

6

4

2

0

0

10

20

30

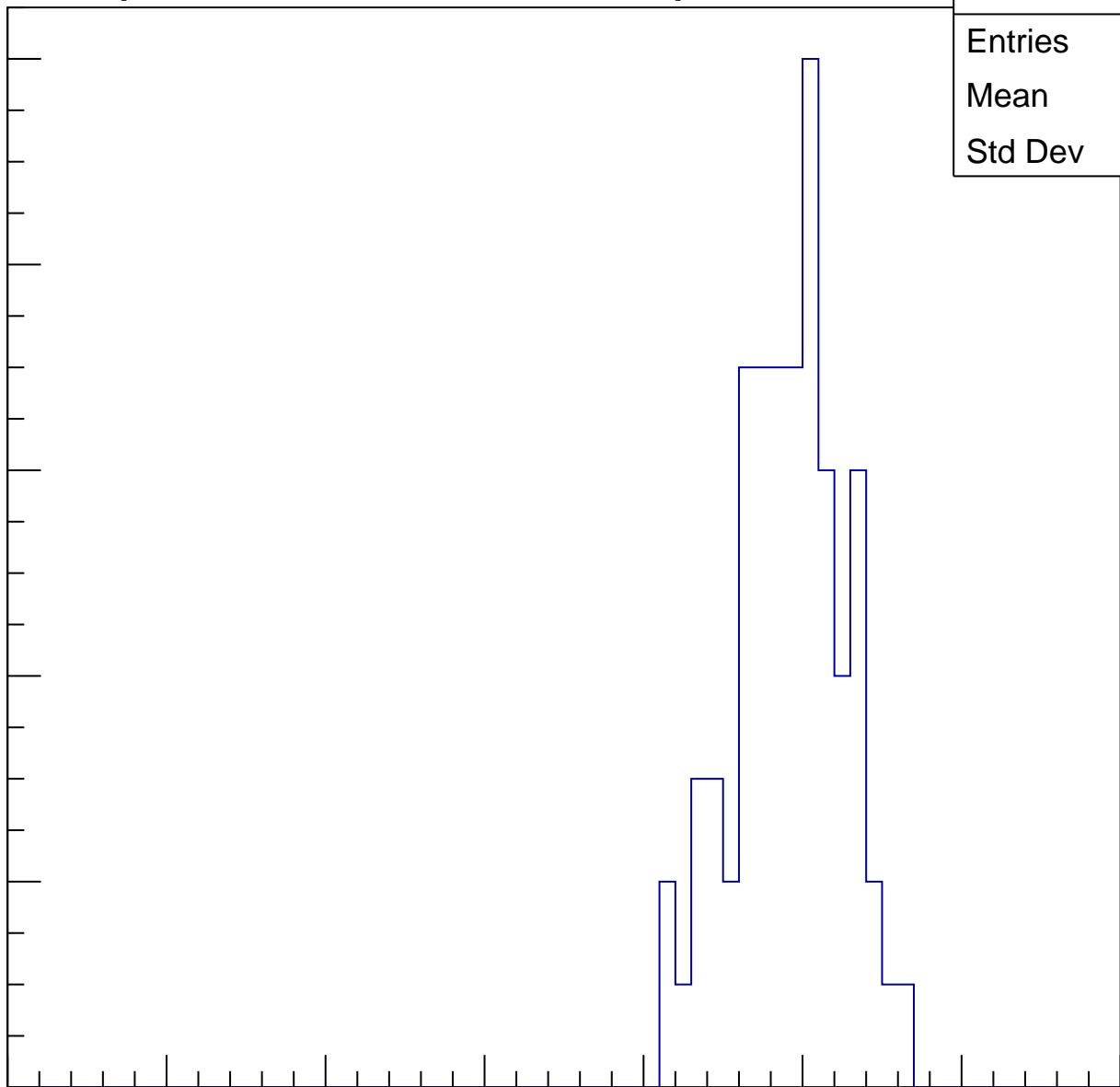
40

50

60

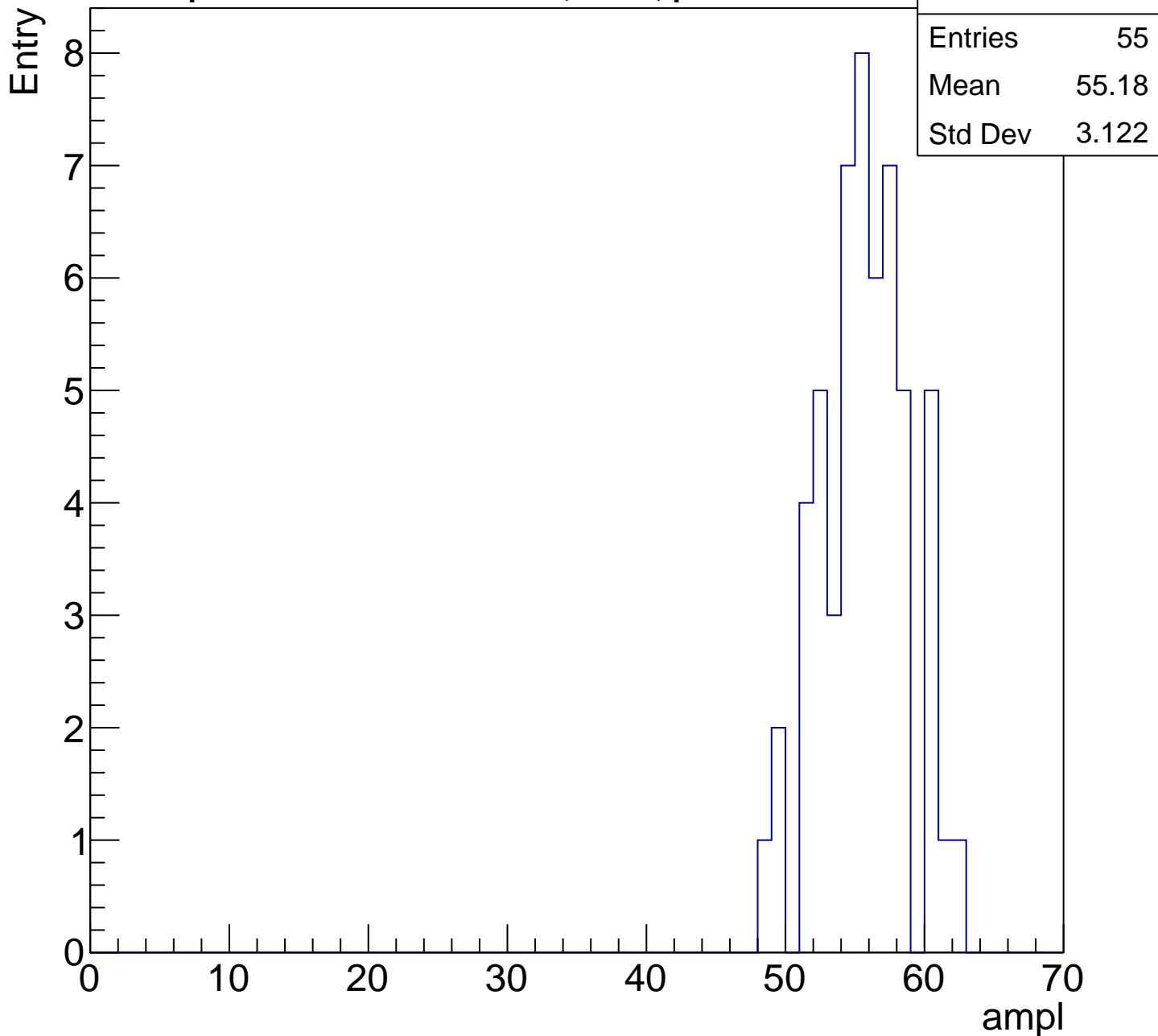
70

ampl



B1L103S, U2-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

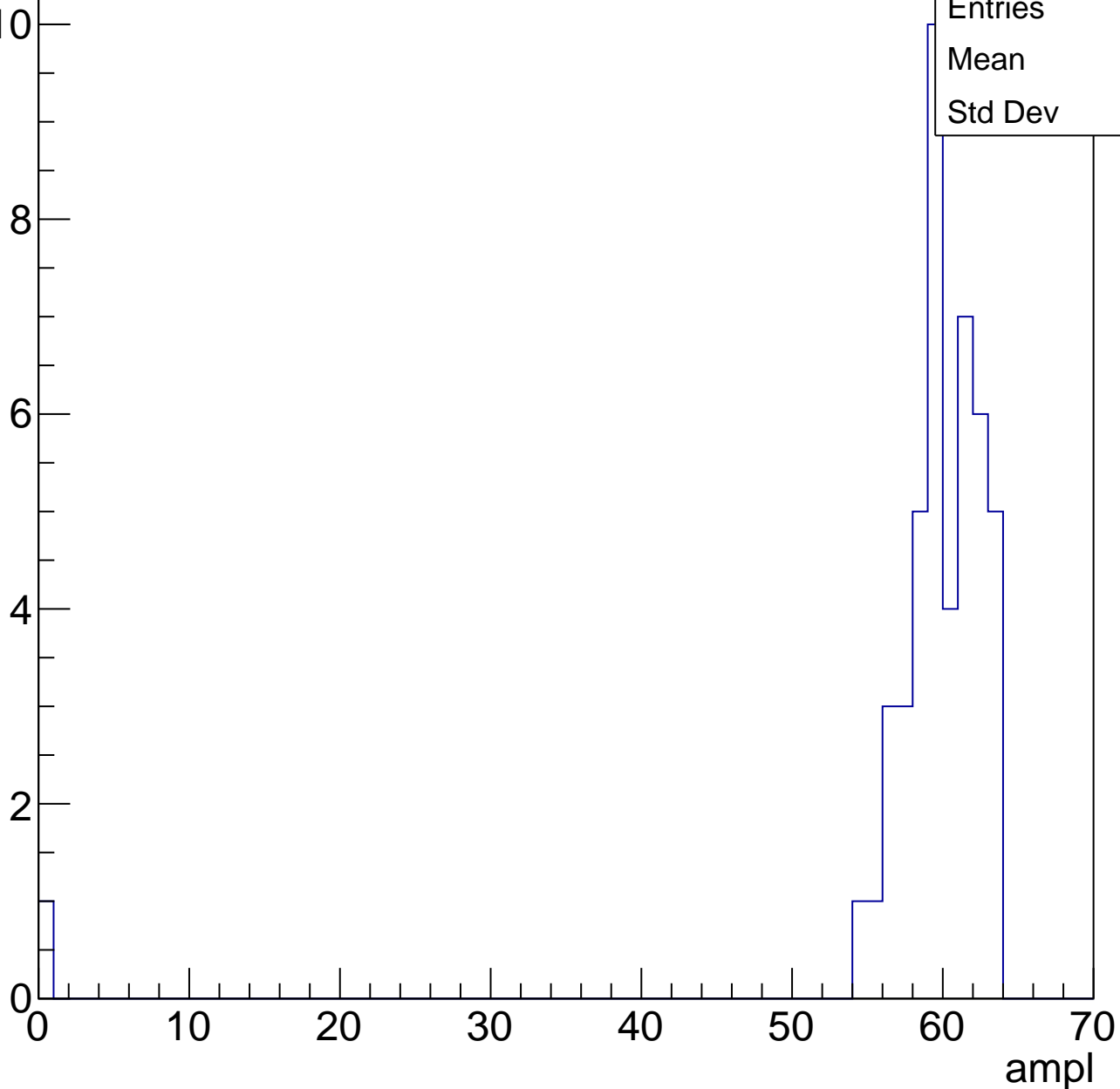


B1L103S, U2-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.3
Std Dev	8.98

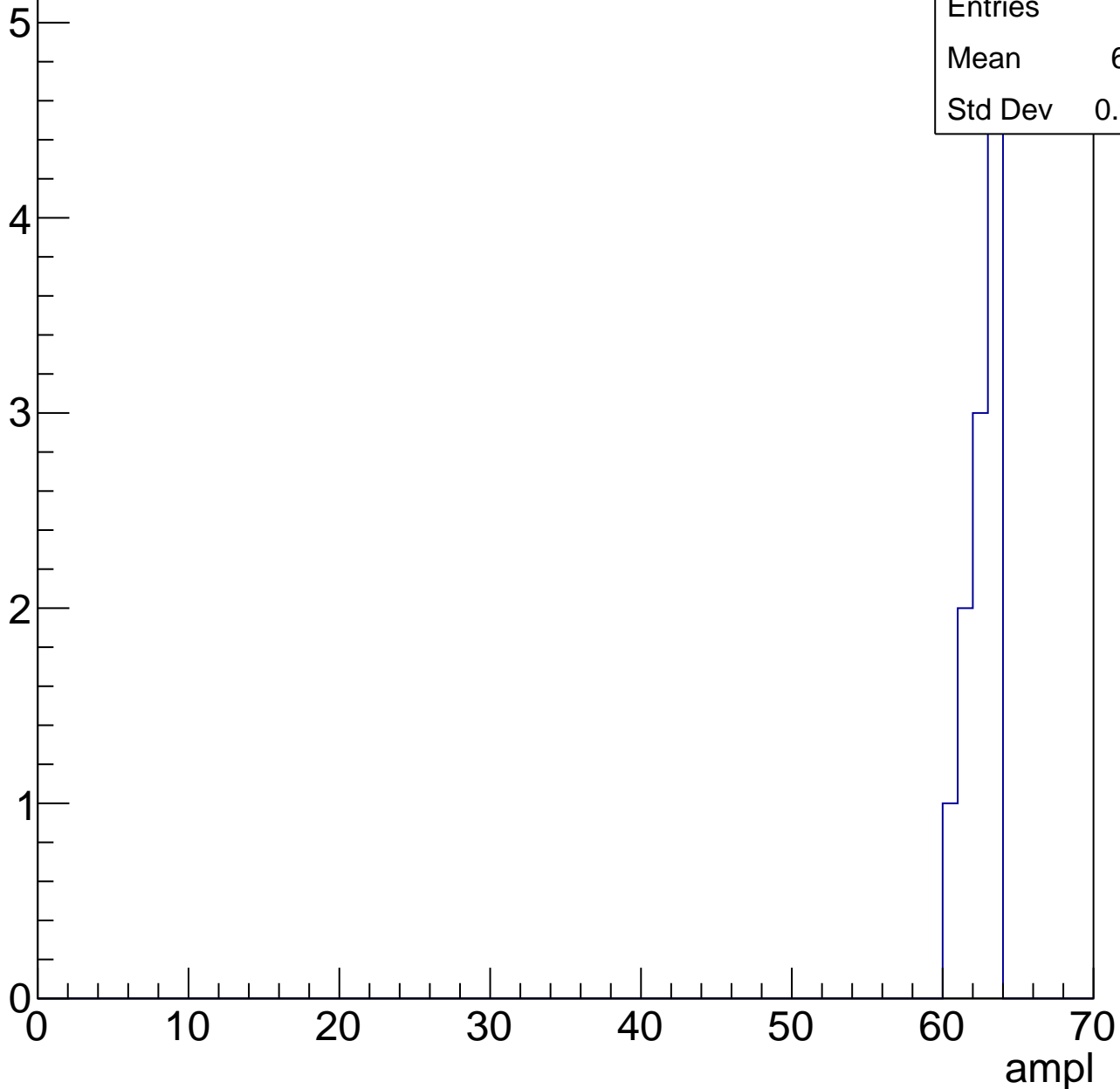


B1L103S, U2-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	62.09
Std Dev	0.9959



B1L103S, U2-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch94, adc0

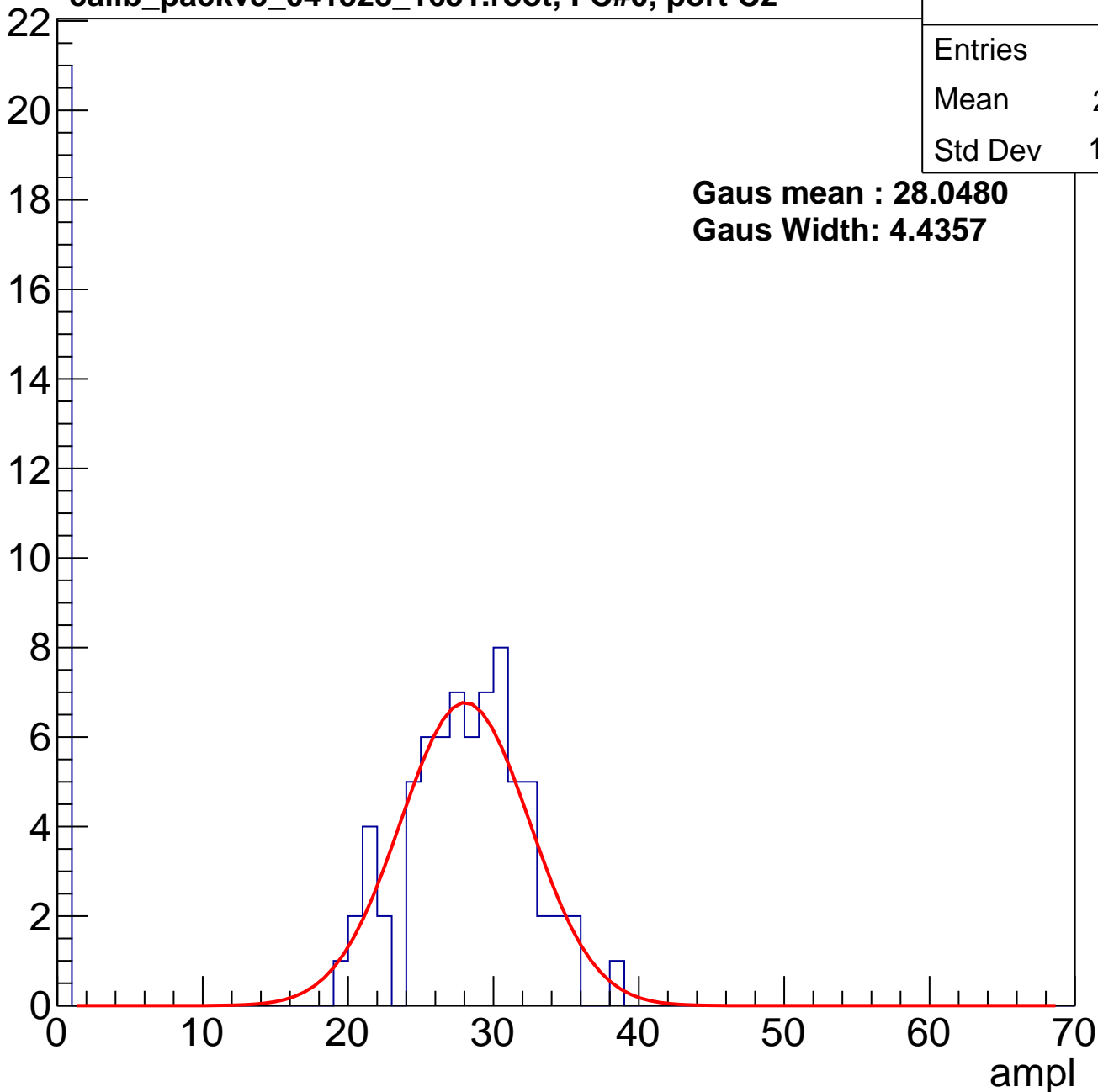
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	21.41
Std Dev	12.16

Gaus mean : 28.0480

Gaus Width: 4.4357

Entry



B1L103S, U2-ch94, adc1

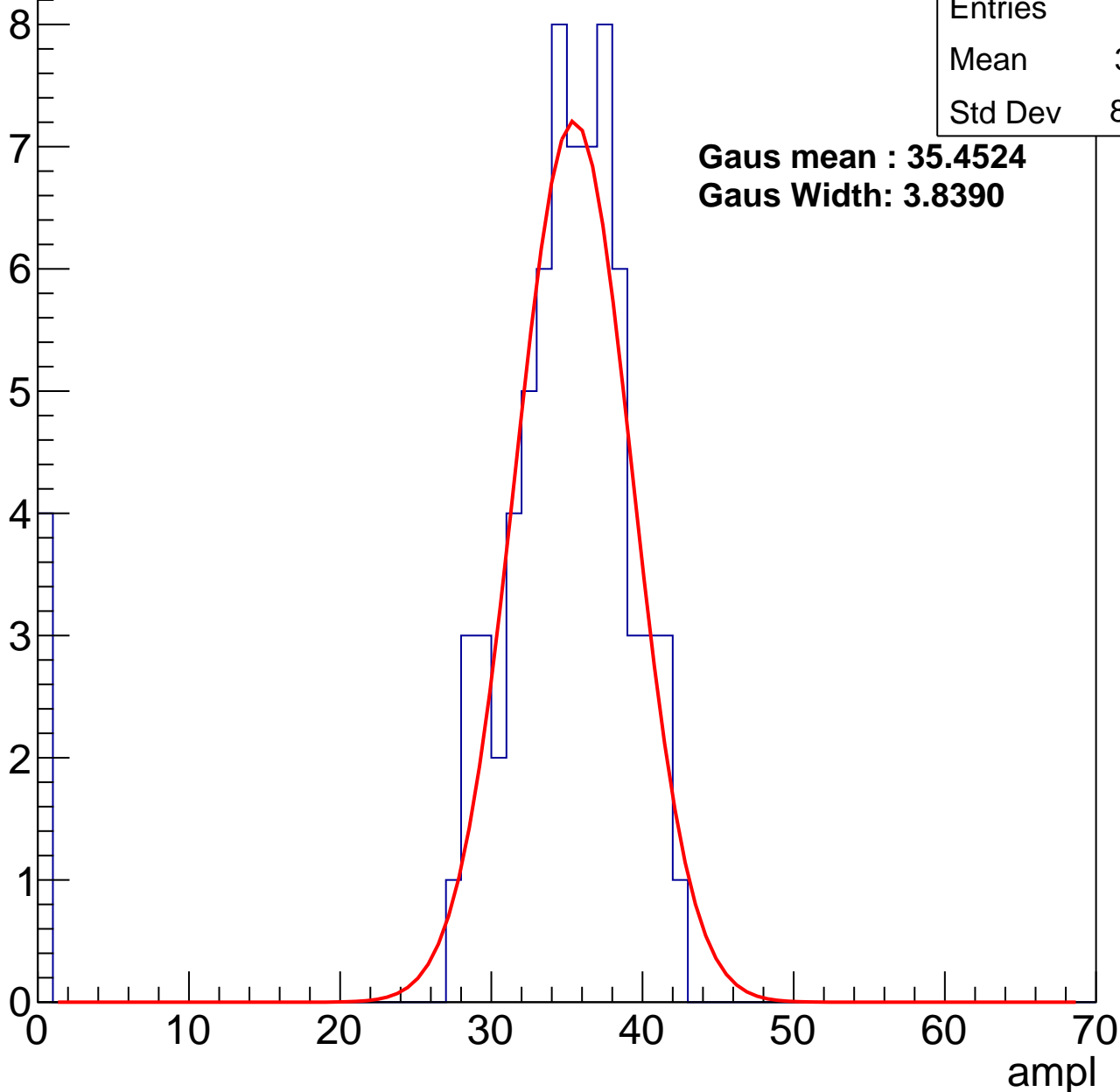
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	32.91
Std Dev	8.588

Gaus mean : 35.4524

Gaus Width: 3.8390



B1L103S, U2-ch94, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	35.4
Std Dev	15.02

Gaus mean : 42.0300

Gaus Width: 3.4023

Entry

10

8

6

4

2

0

0

10

20

30

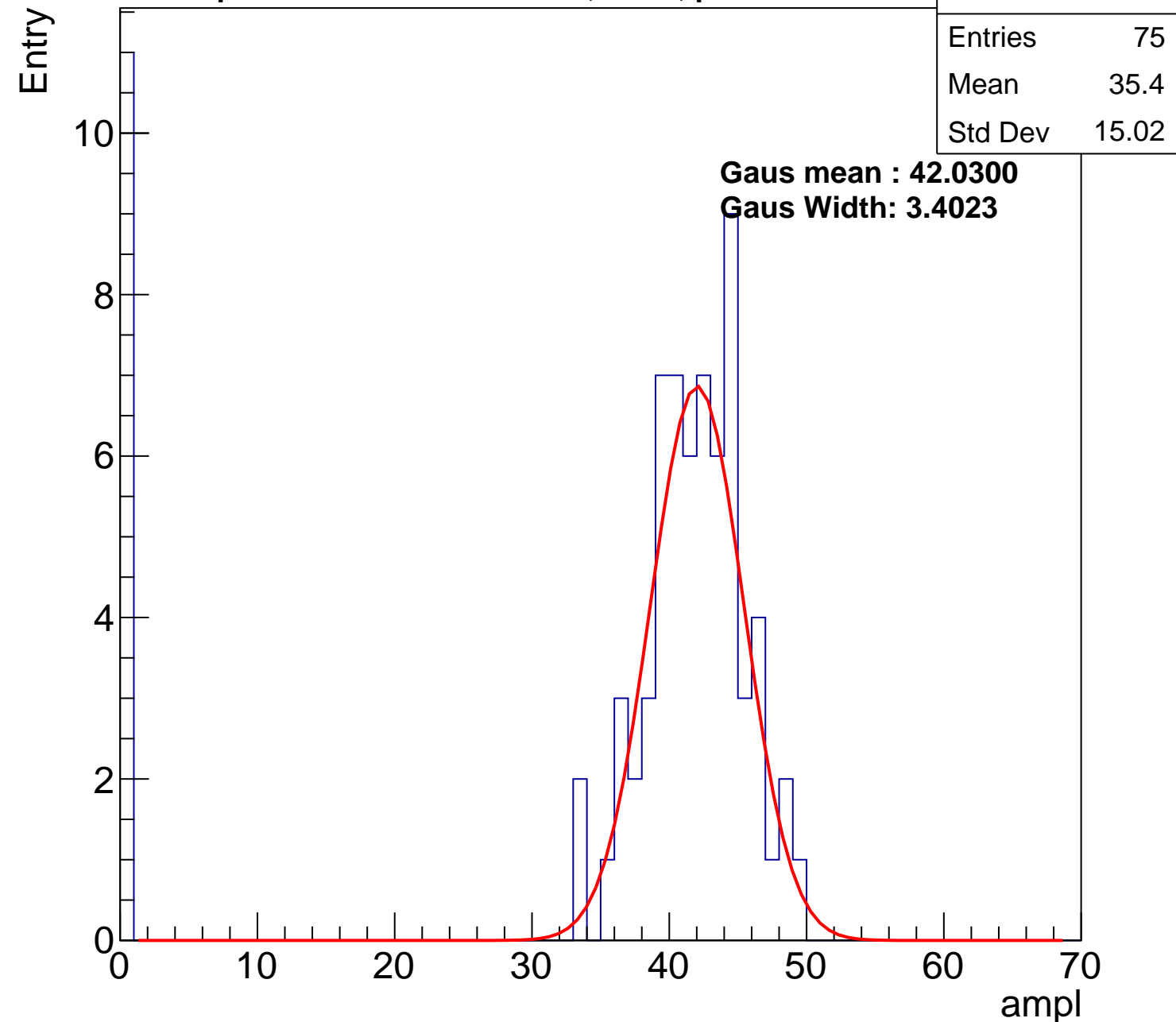
40

50

60

70

ampl

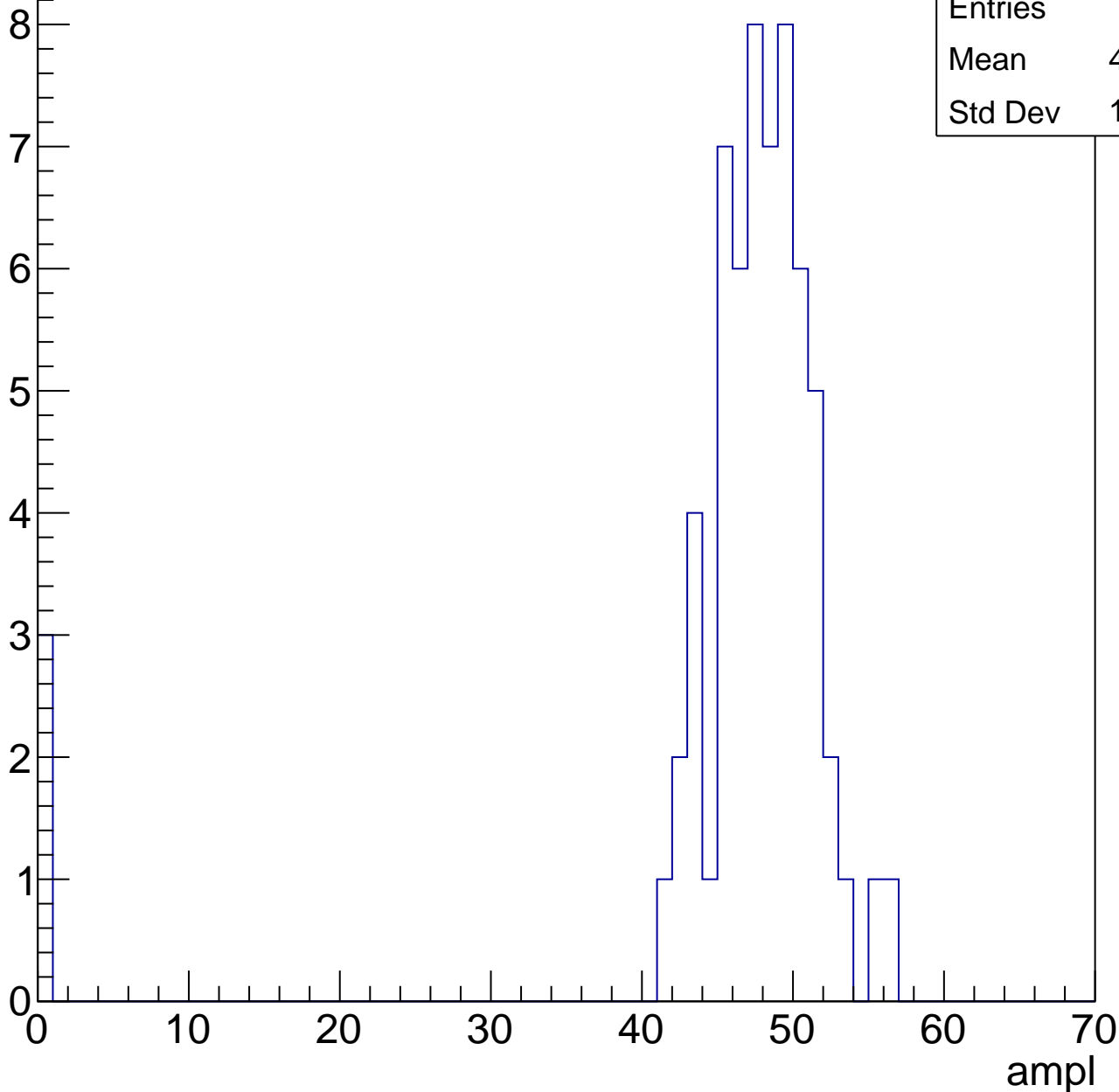


B1L103S, U2-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	45.38
Std Dev	10.59

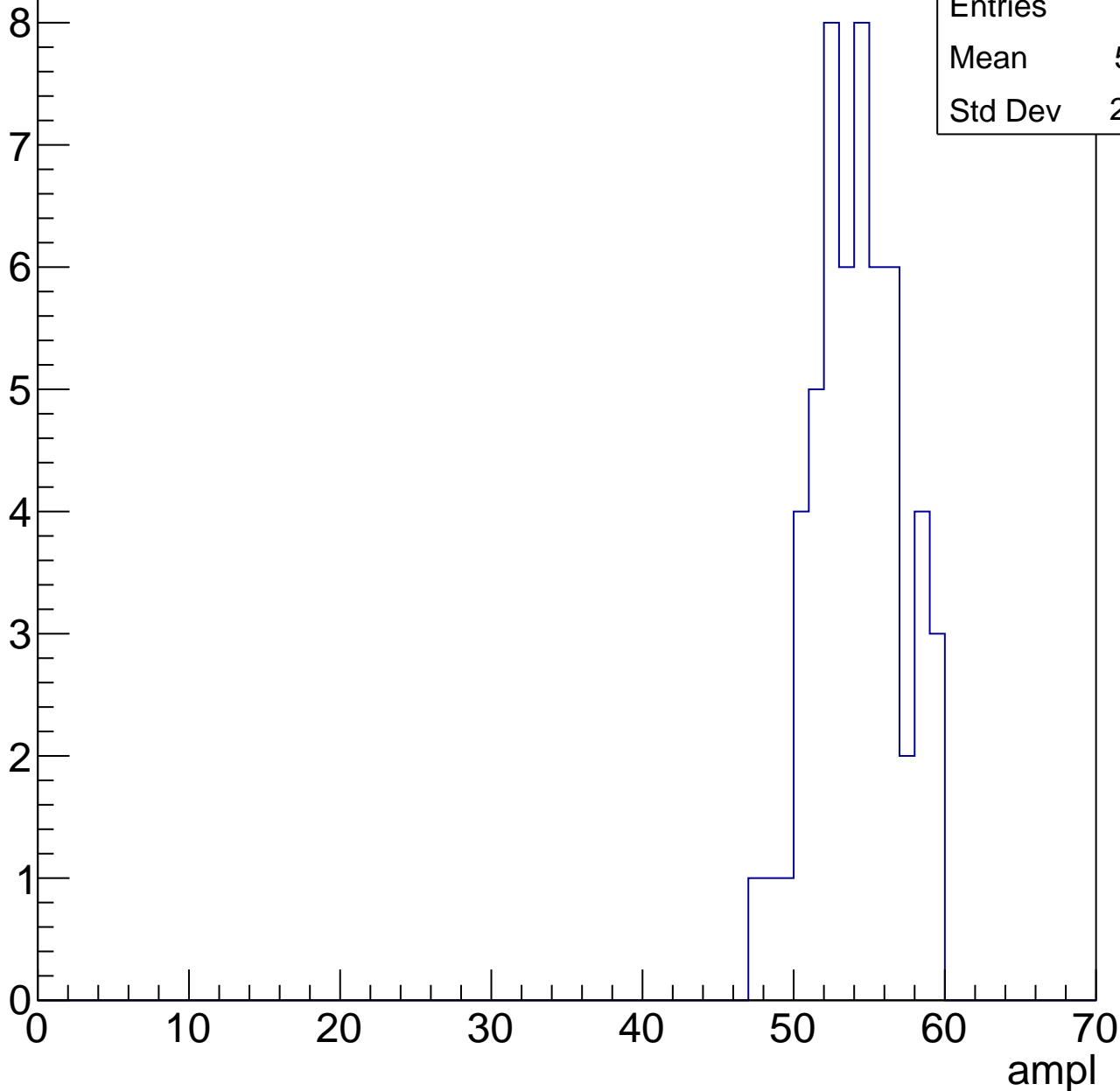


B1L103S, U2-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.71
Std Dev	2.826

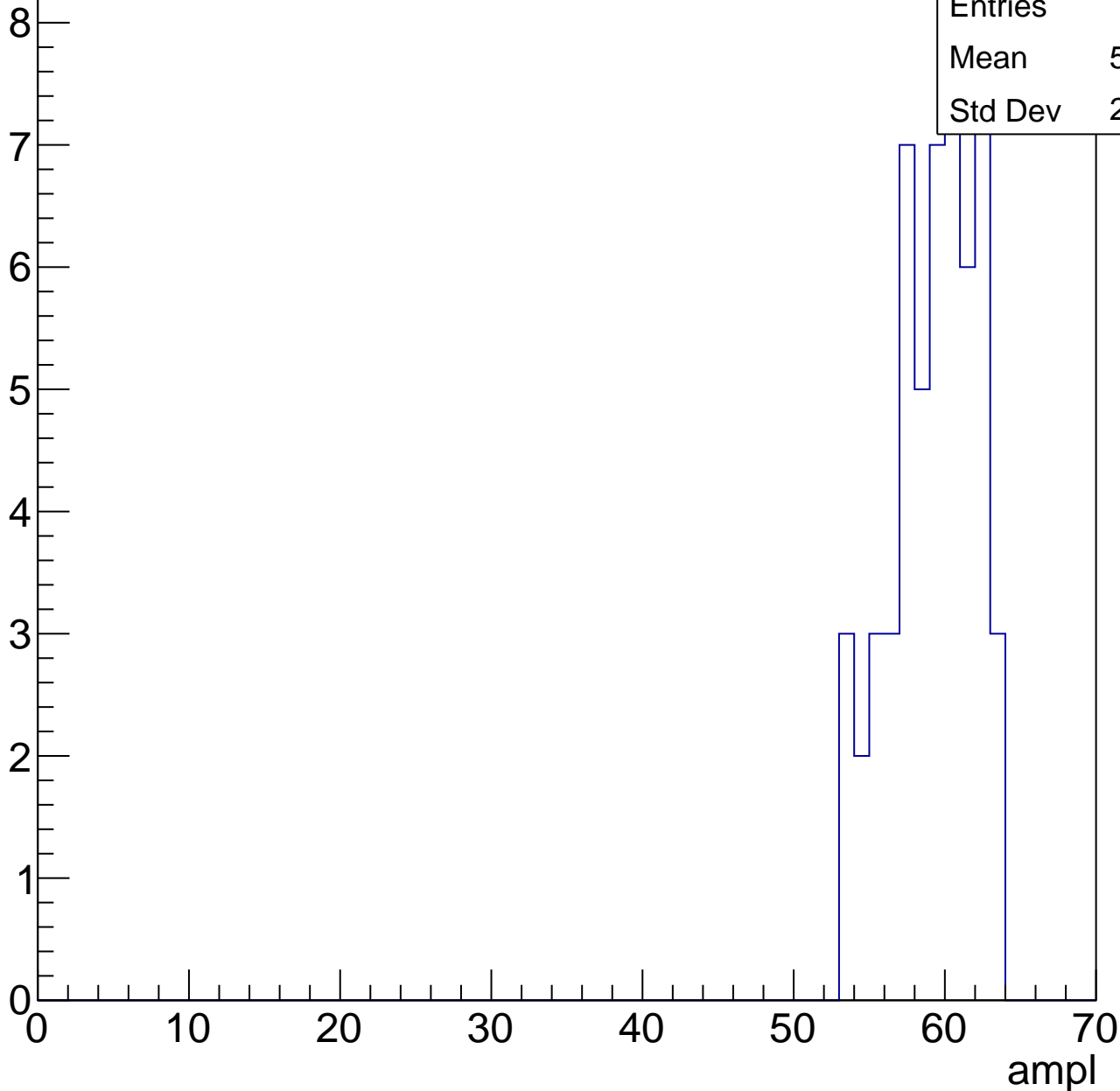


B1L103S, U2-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	58.78
Std Dev	2.748

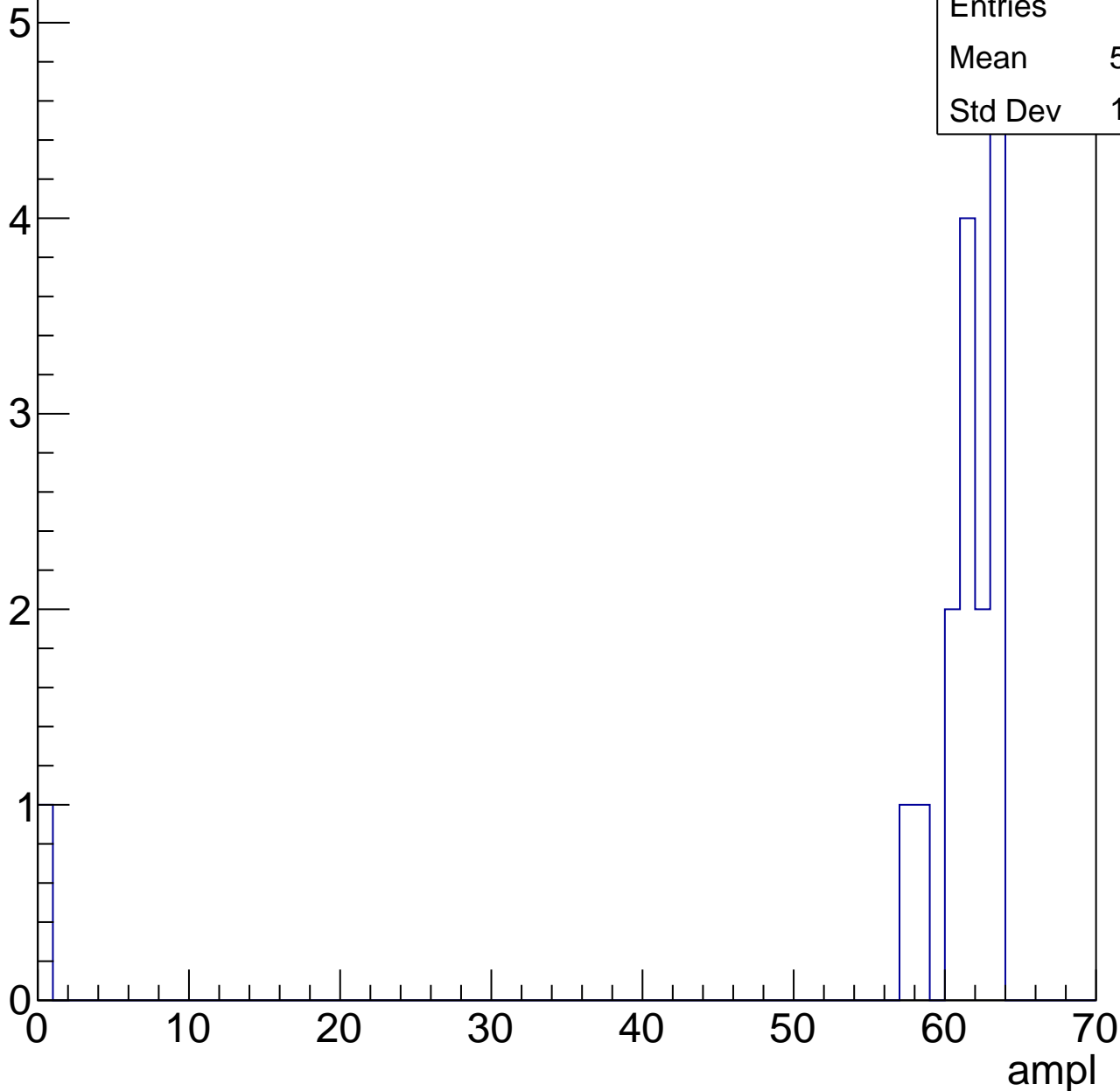


B1L103S, U2-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.38
Std Dev	14.92



B1L103S, U2-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



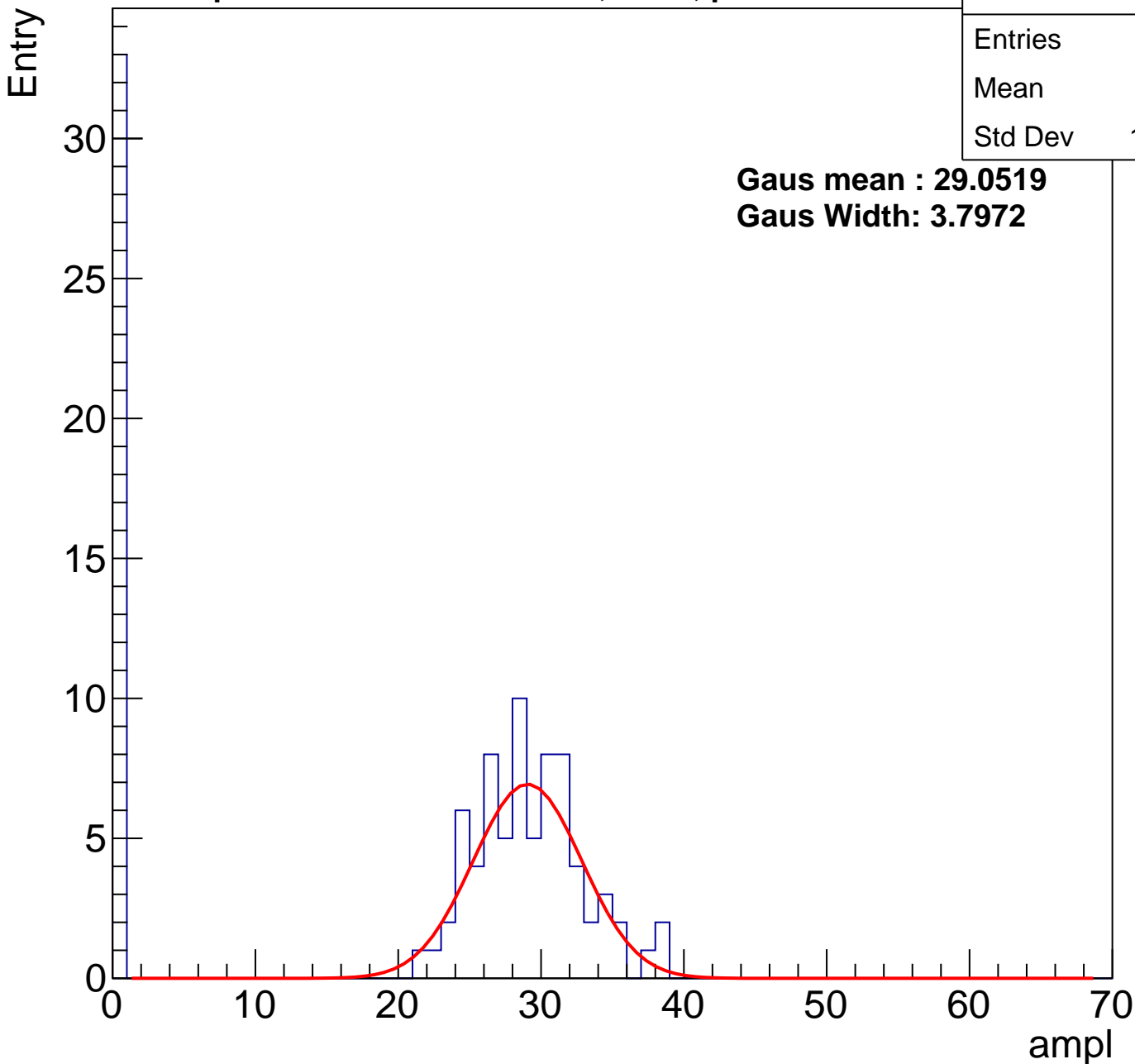
B1L103S, U2-ch95, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	19.7
Std Dev	13.68

Gaus mean : 29.0519

Gaus Width: 3.7972



B1L103S, U2-ch95, adc1

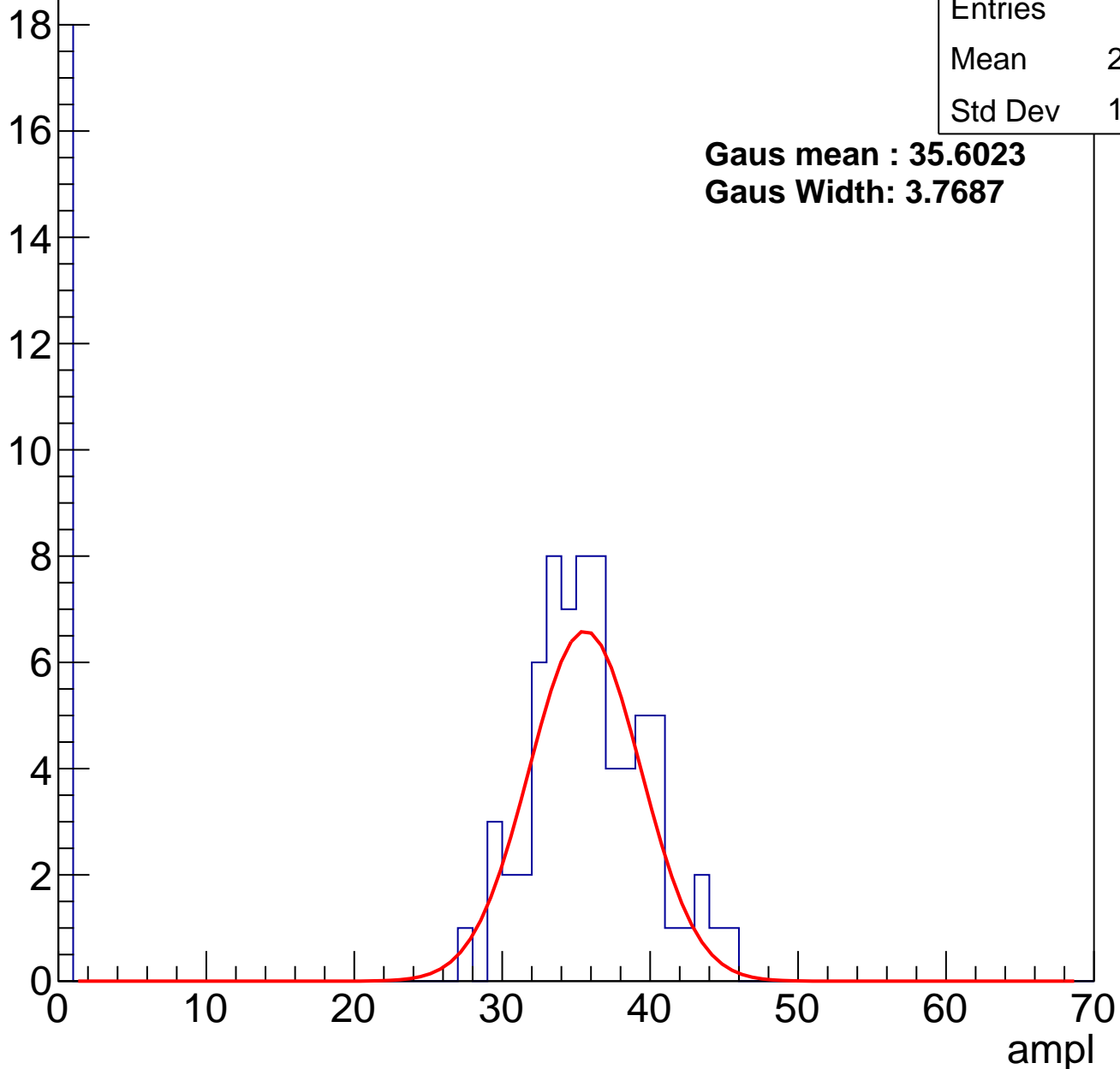
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	28.17
Std Dev	14.78

Gaus mean : 35.6023

Gaus Width: 3.7687

Entry



B1L103S, U2-ch95, adc2

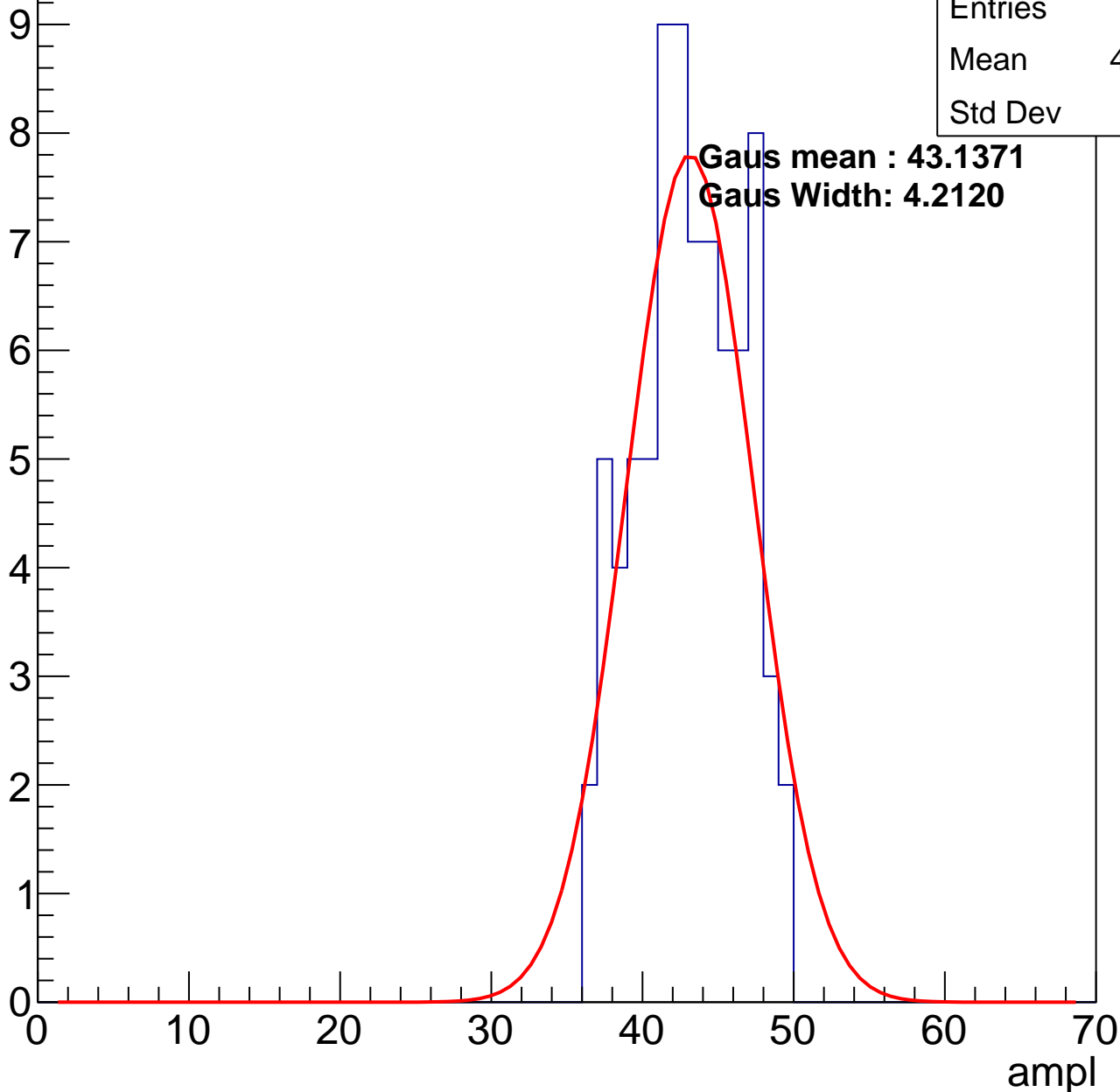
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	42.62
Std Dev	3.39

Gaus mean : 43.1371

Gaus Width: 4.2120

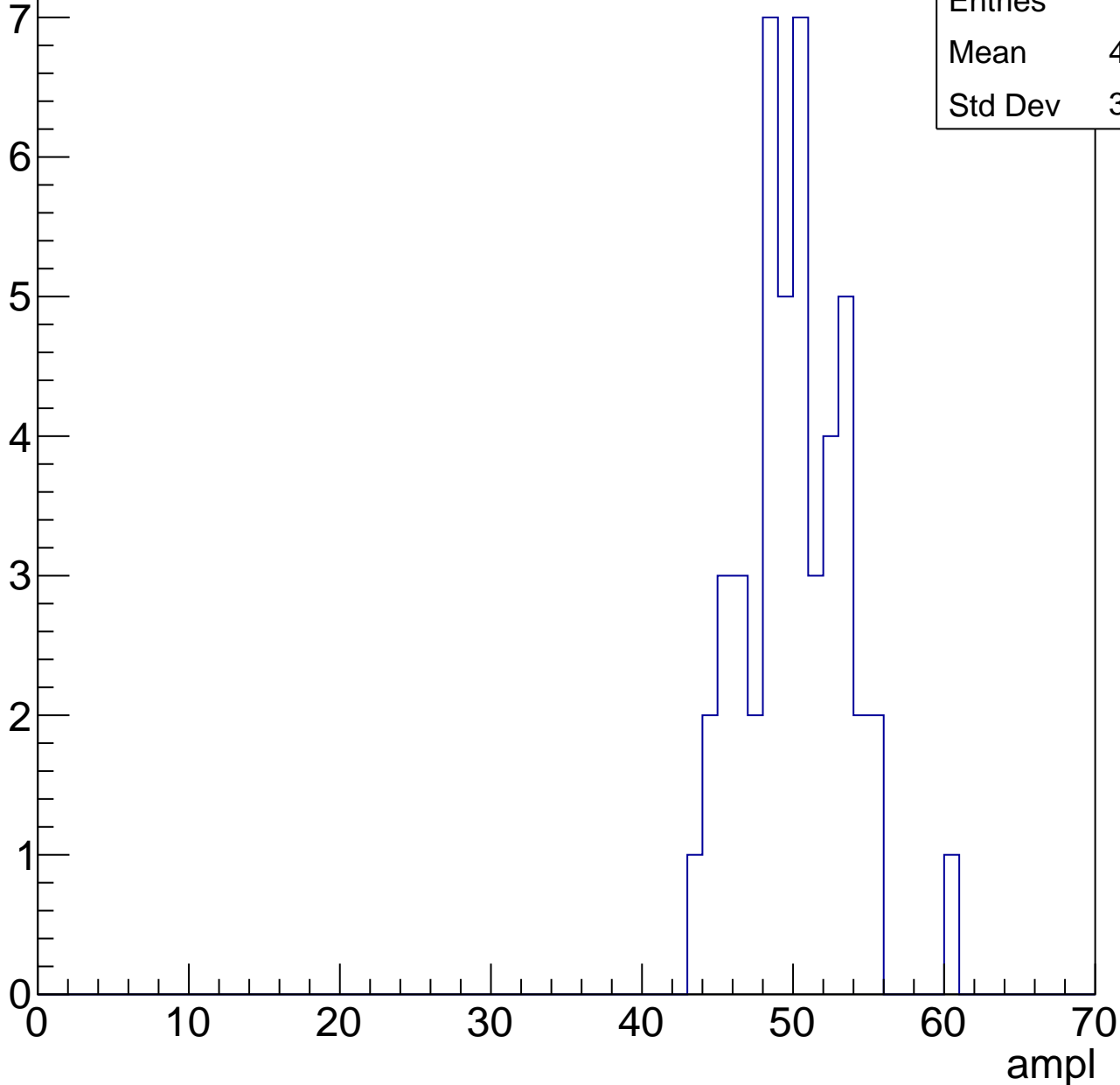


B1L103S, U2-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	49.64
Std Dev	3.373

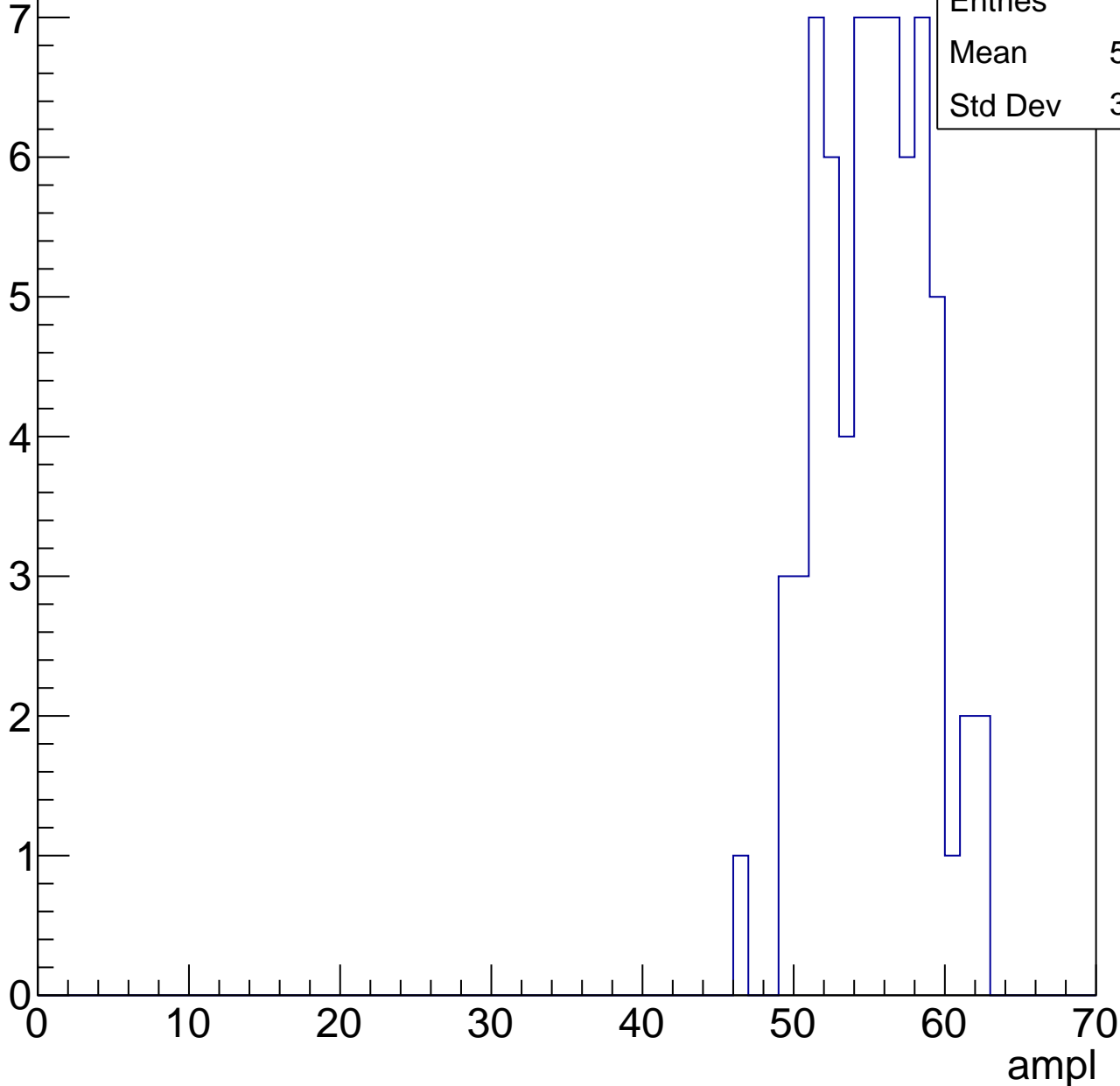


B1L103S, U2-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	54.82
Std Dev	3.472

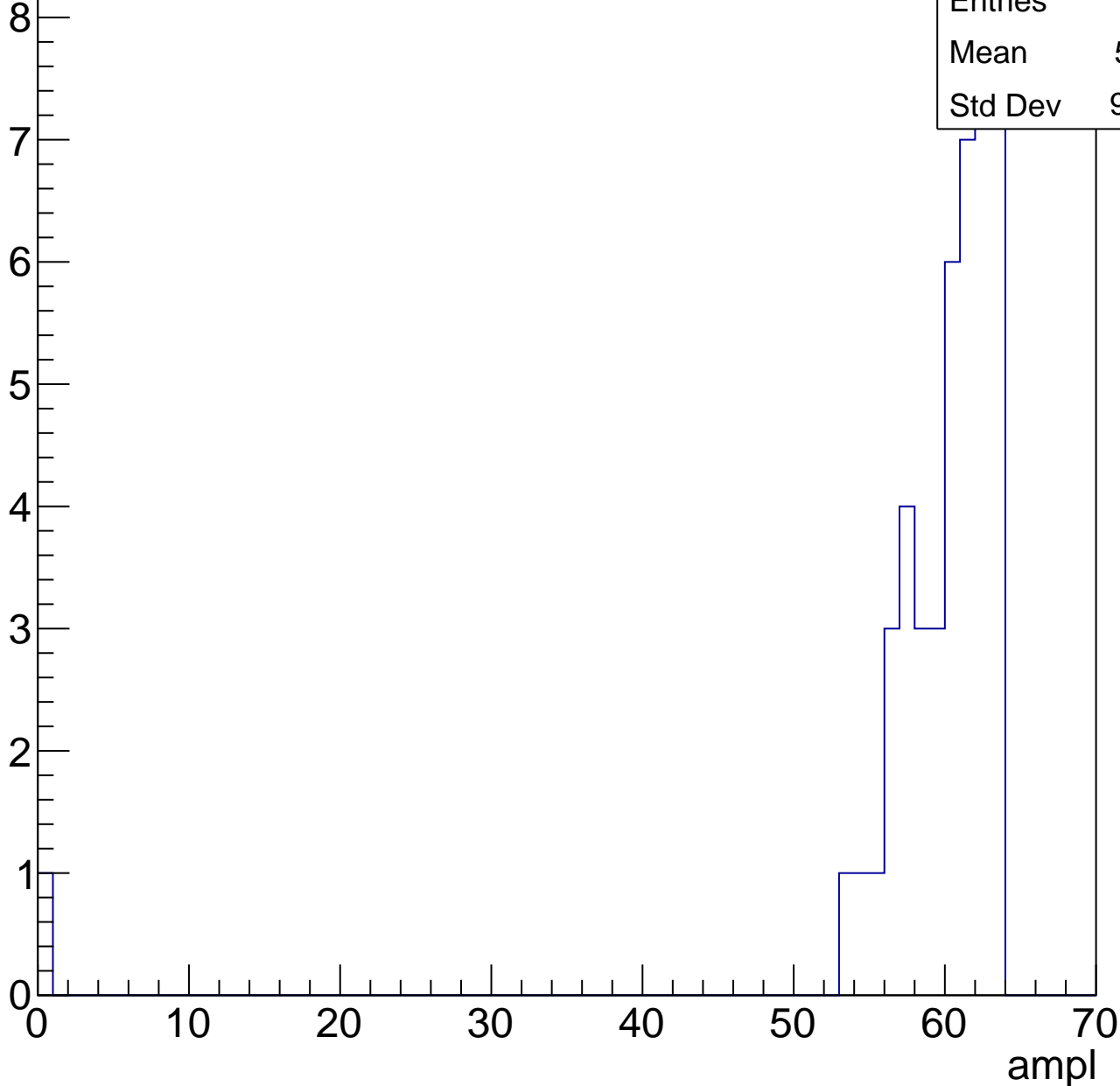


B1L103S, U2-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.61
Std Dev	9.126



B1L103S, U2-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

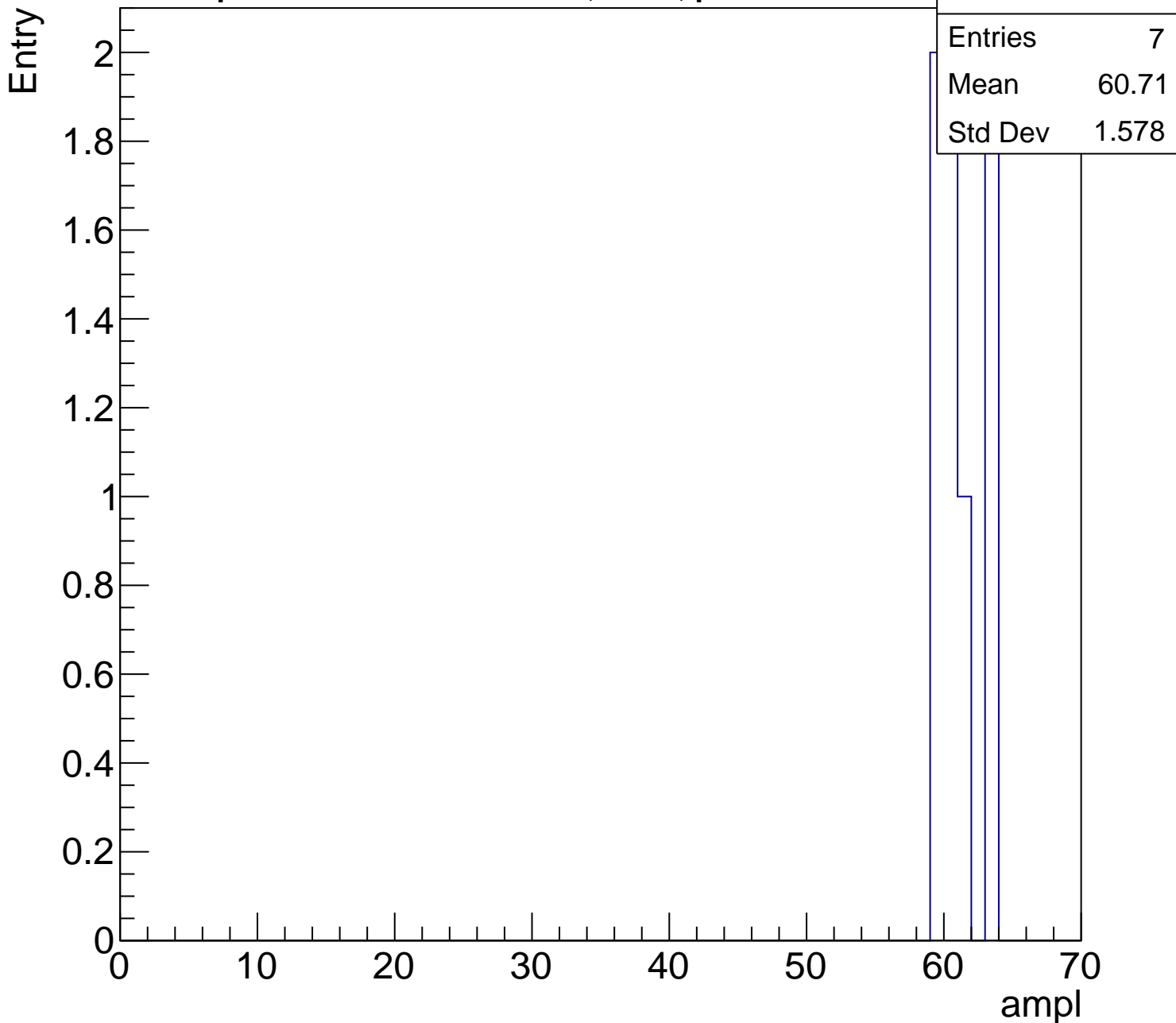
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.71
Std Dev	1.578

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

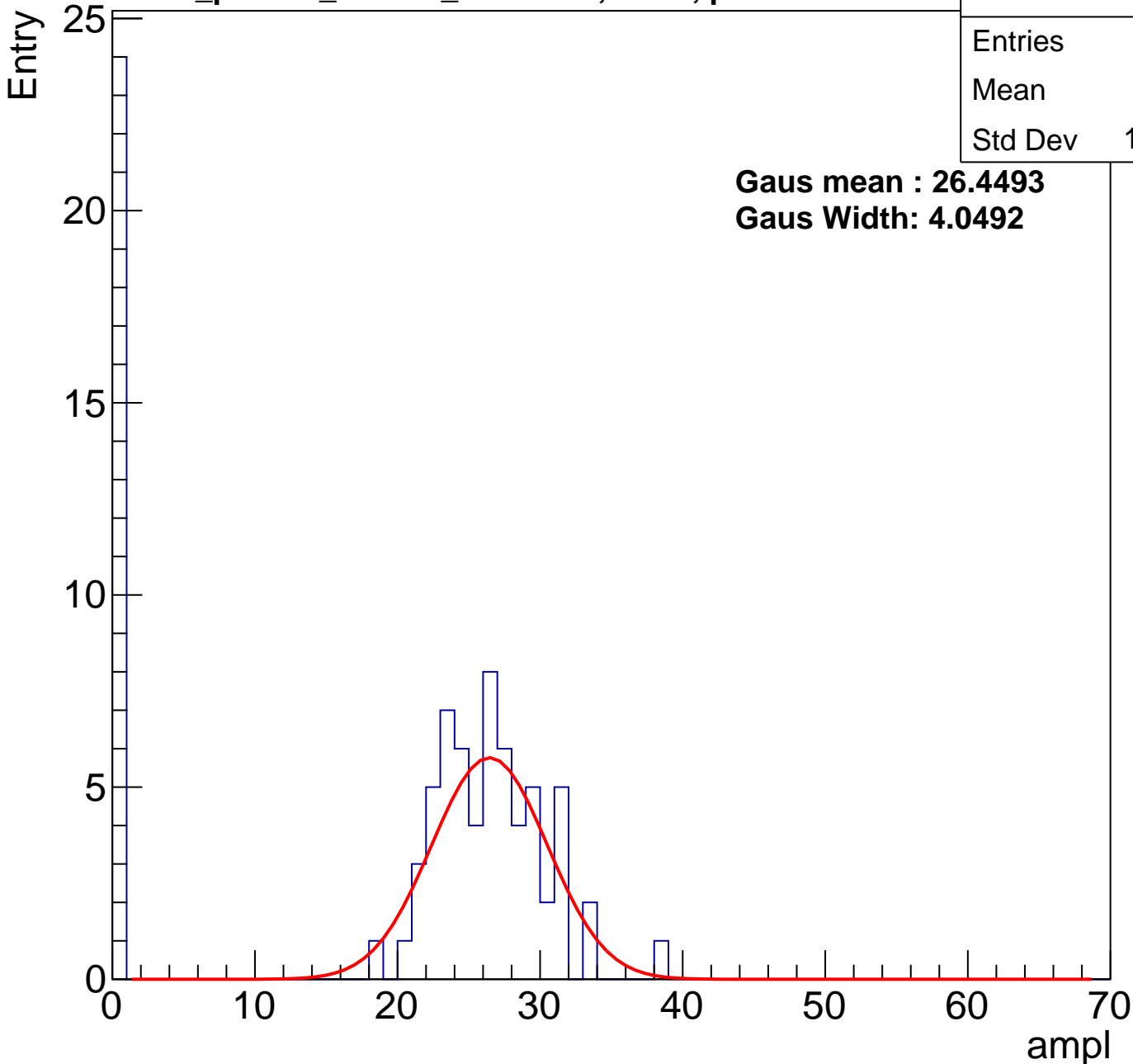
B1L103S, U2-ch96, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	18.6
Std Dev	12.16

Gaus mean : 26.4493

Gaus Width: 4.0492



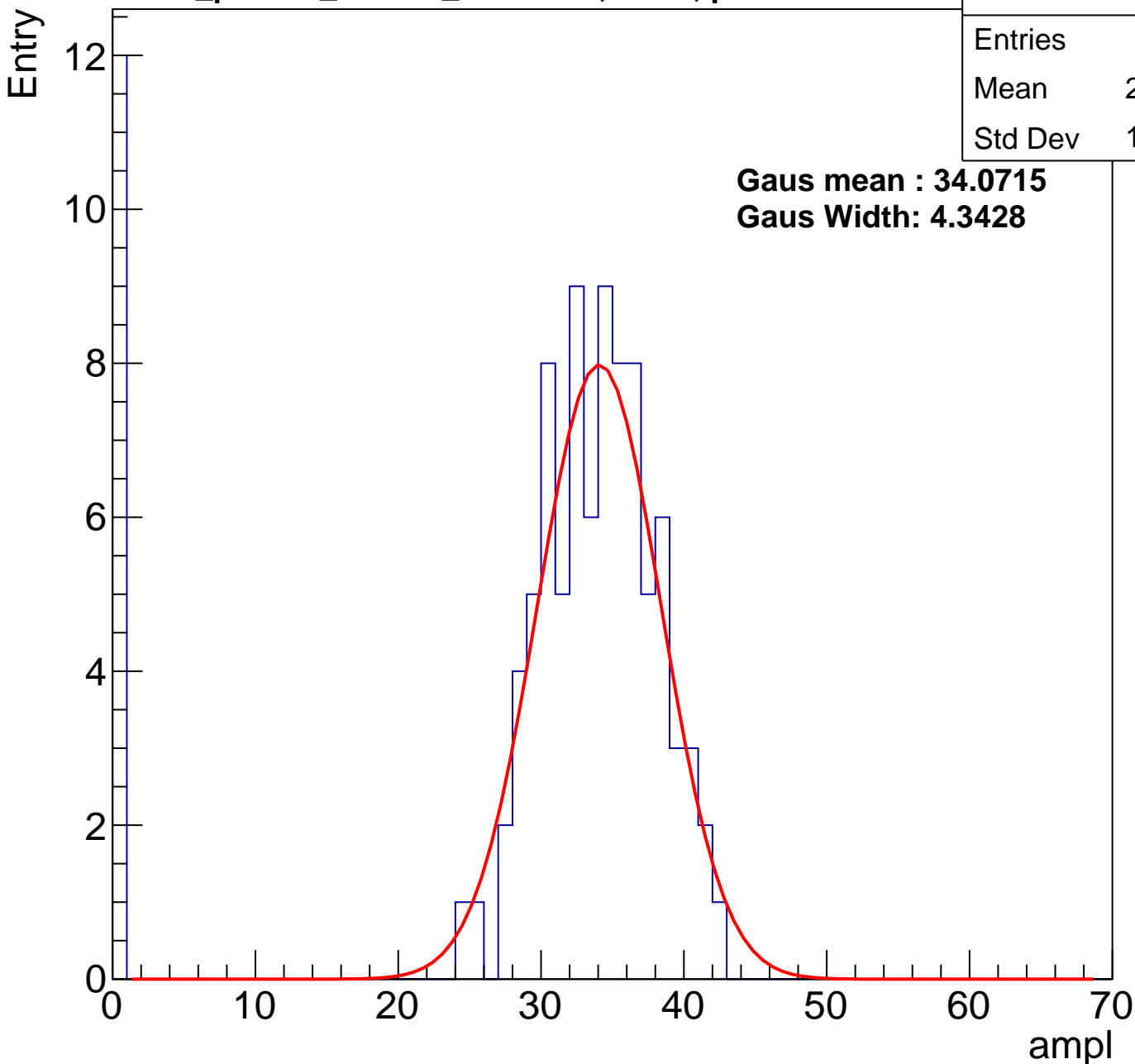
B1L103S, U2-ch96, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	29.48
Std Dev	11.59

Gaus mean : 34.0715

Gaus Width: 4.3428



B1L103S, U2-ch96, adc2

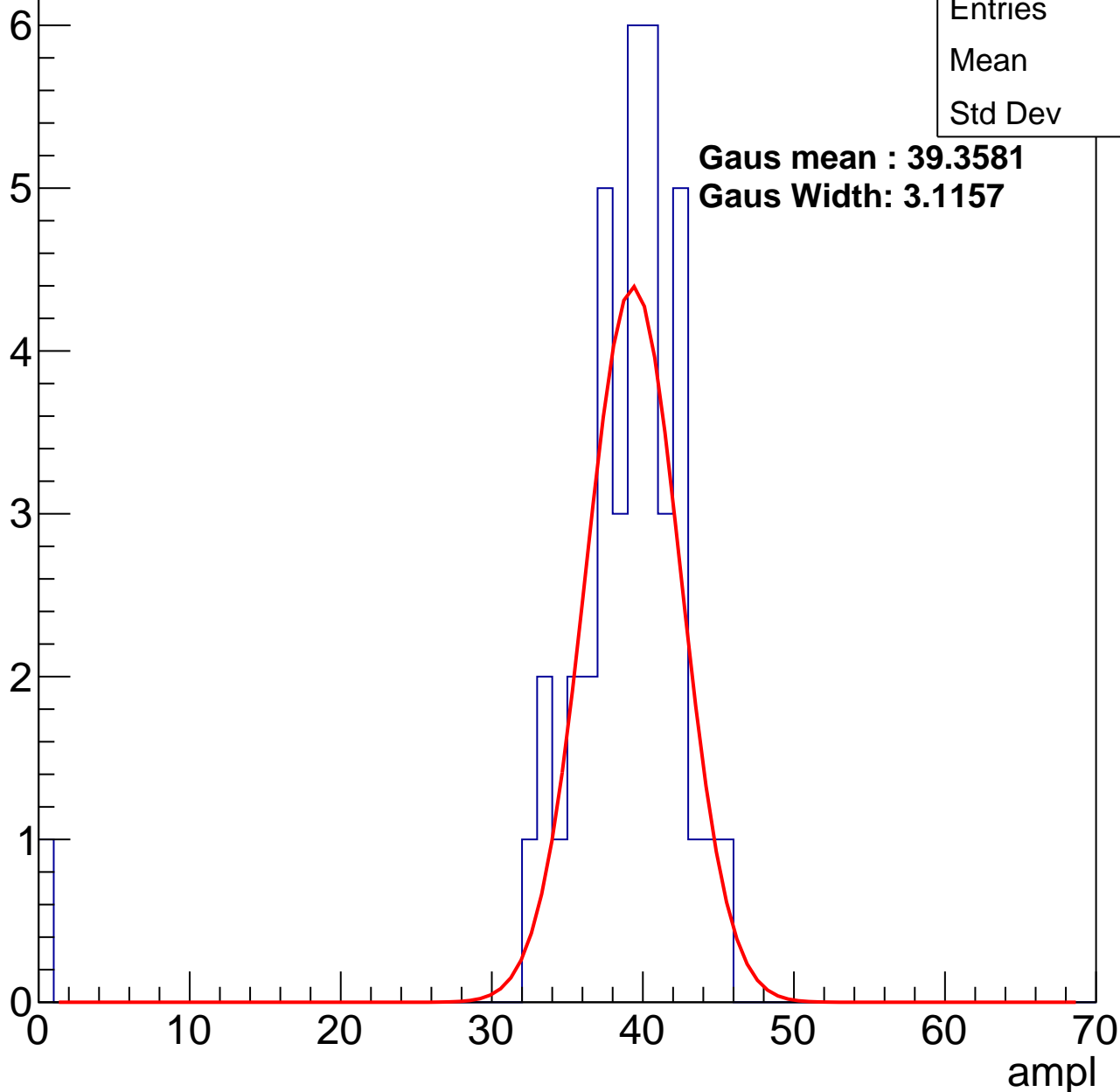
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	37.8
Std Dev	6.75

Gaus mean : 39.3581

Gaus Width: 3.1157

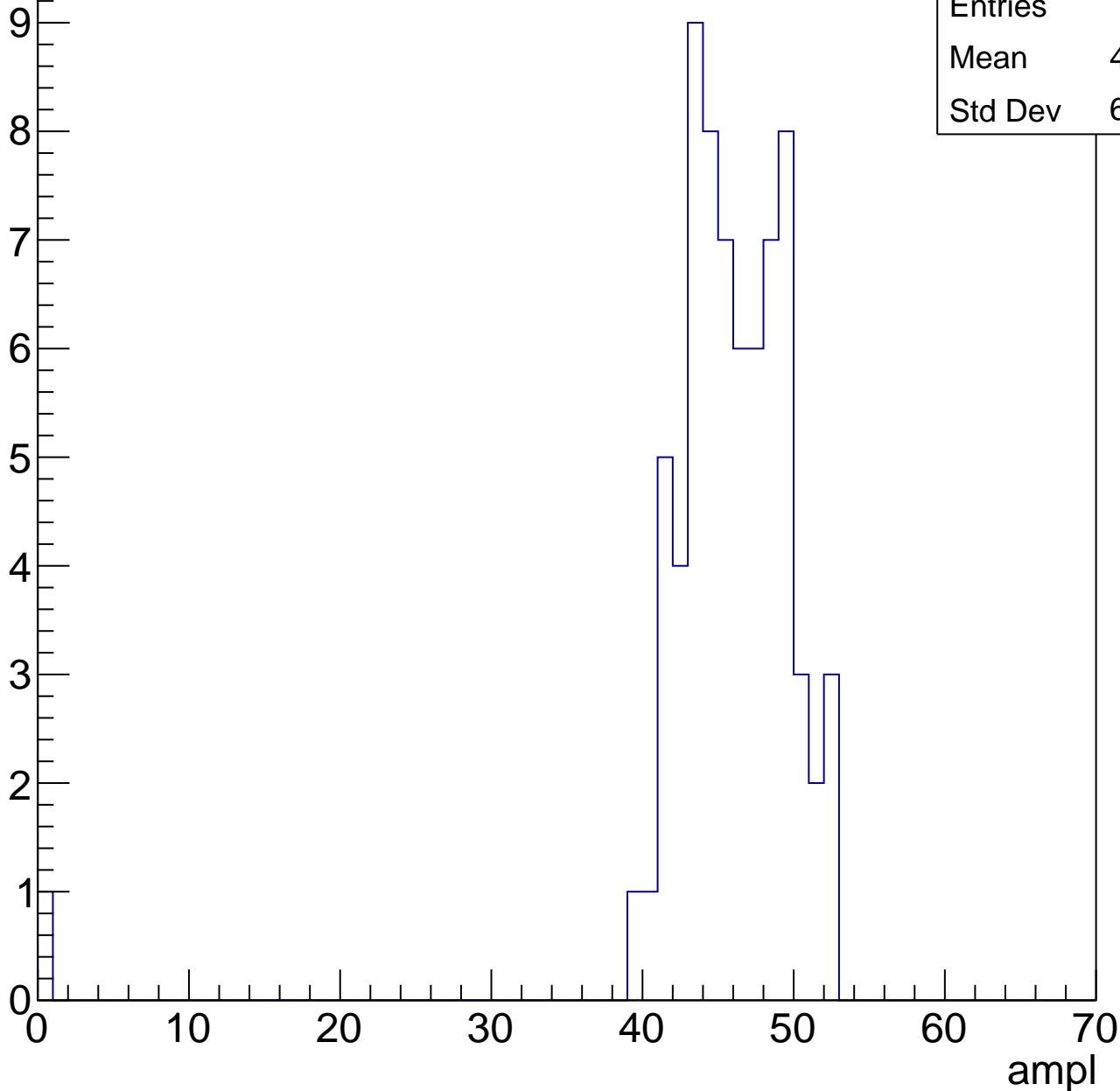


B1L103S, U2-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	45.07
Std Dev	6.236

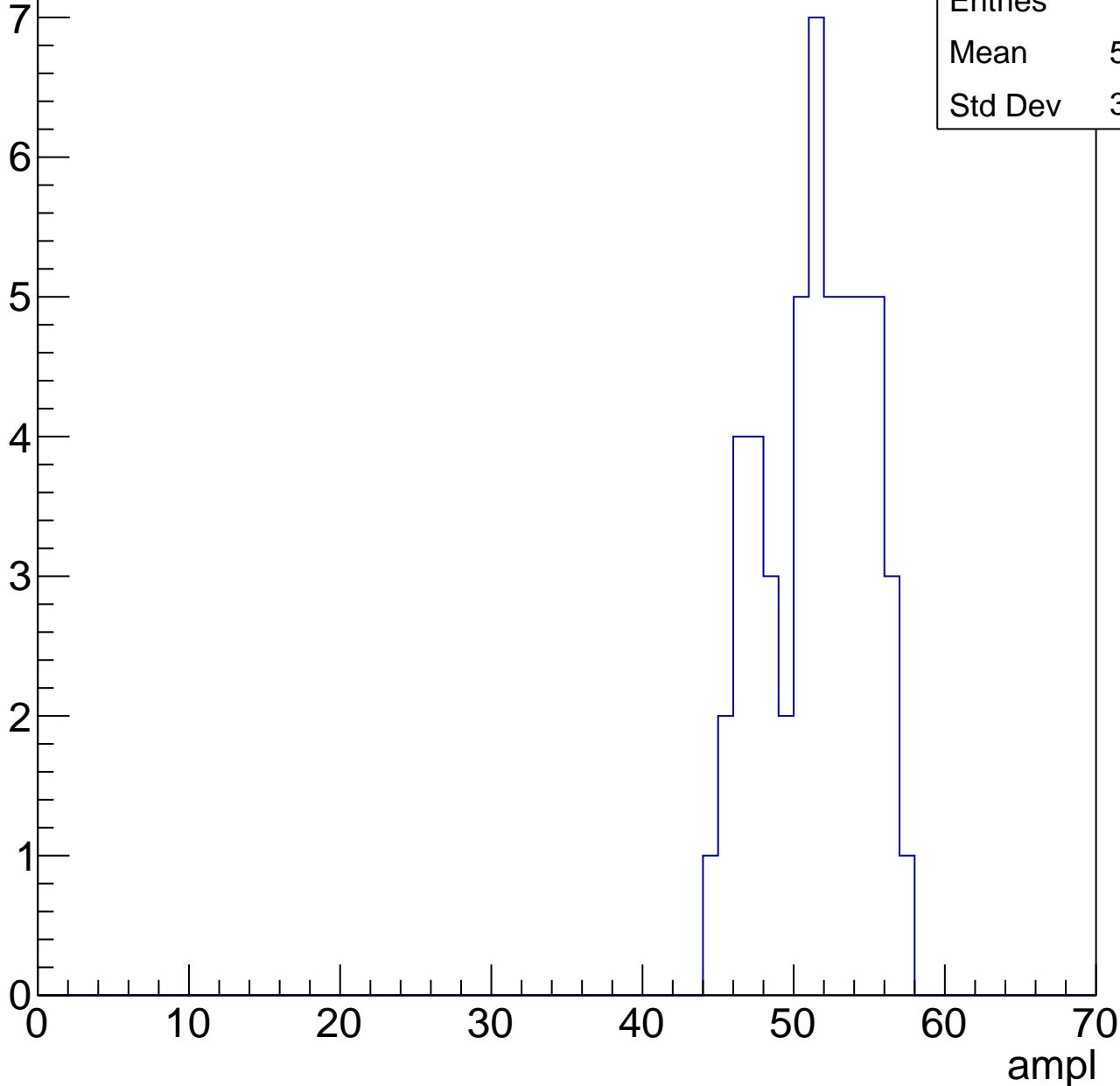


B1L103S, U2-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	50.96
Std Dev	3.357

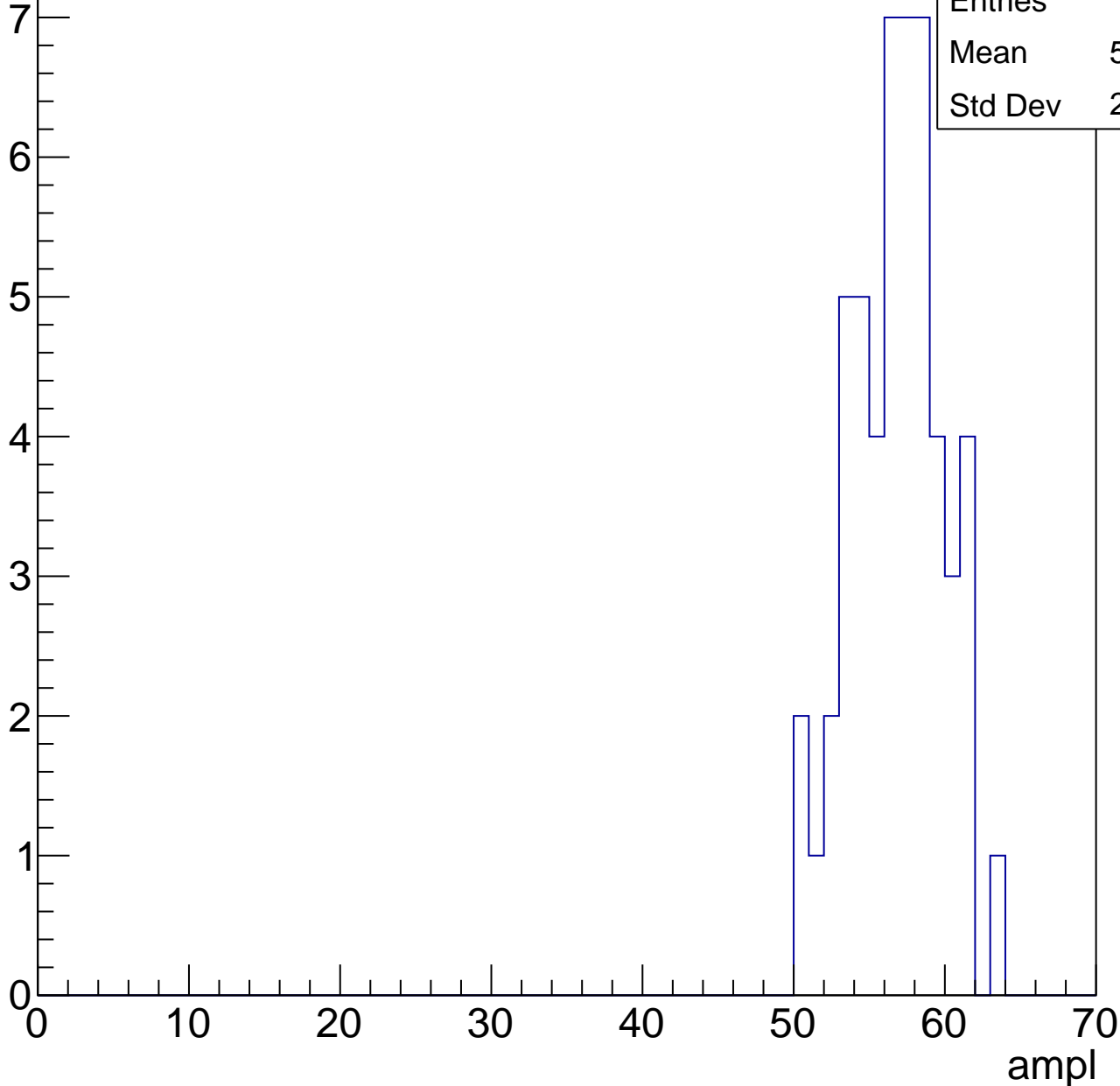


B1L103S, U2-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	56.35
Std Dev	2.974

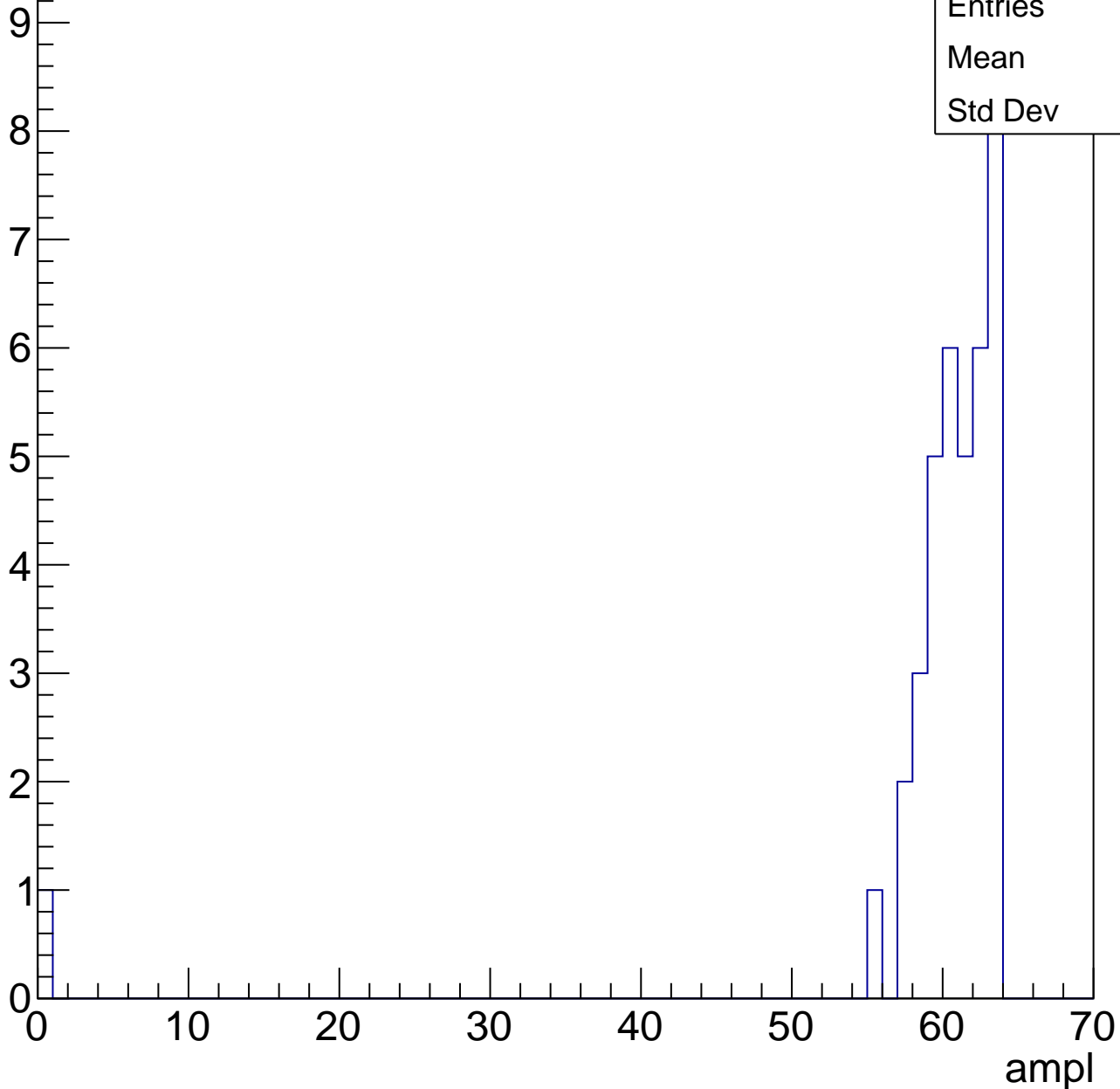


B1L103S, U2-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	59
Std Dev	9.91

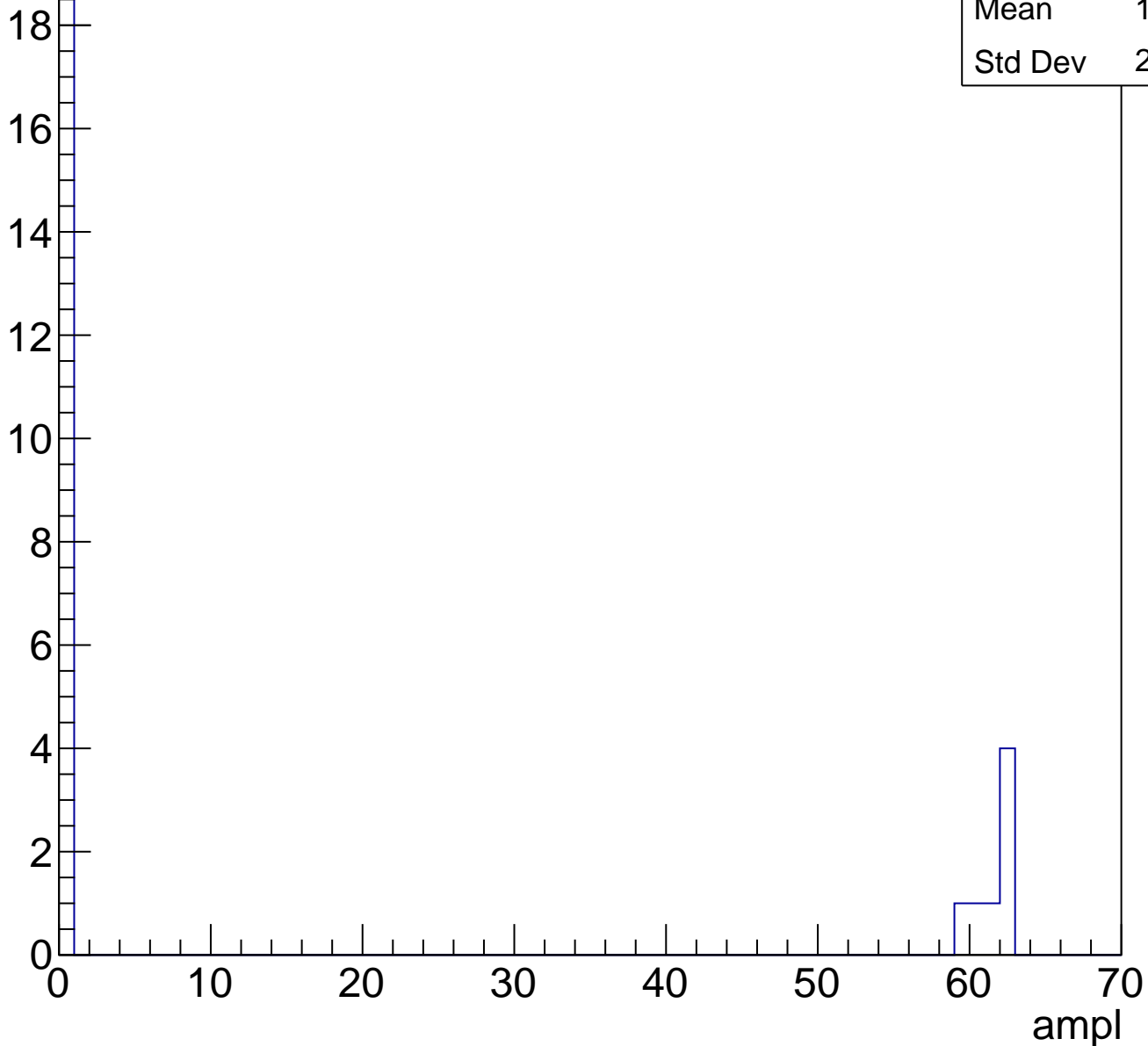


B1L103S, U2-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	16.46
Std Dev	27.13

Entry



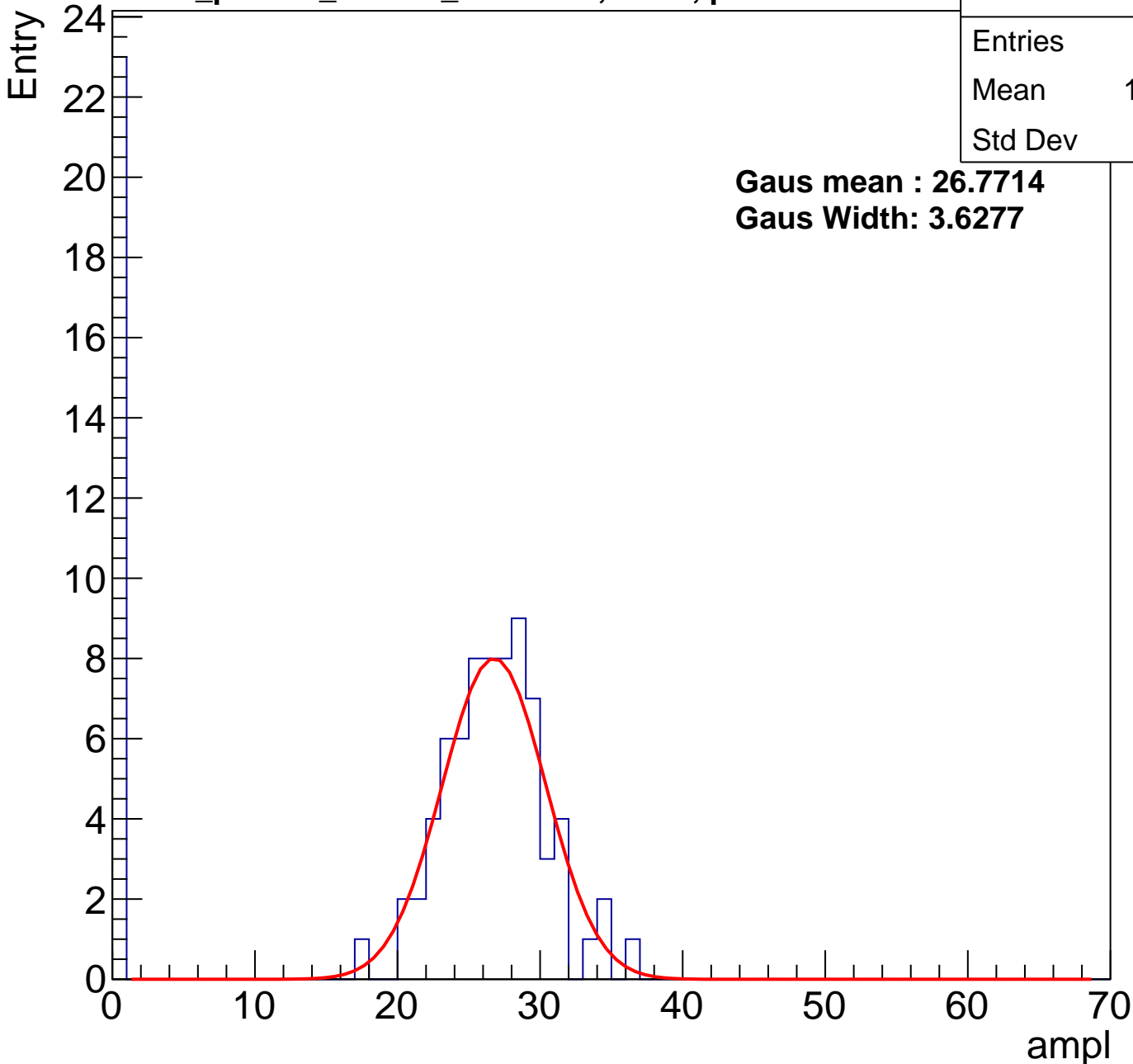
B1L103S, U2-ch97, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	19.99
Std Dev	11.7

Gaus mean : 26.7714

Gaus Width: 3.6277



B1L103S, U2-ch97, adc1

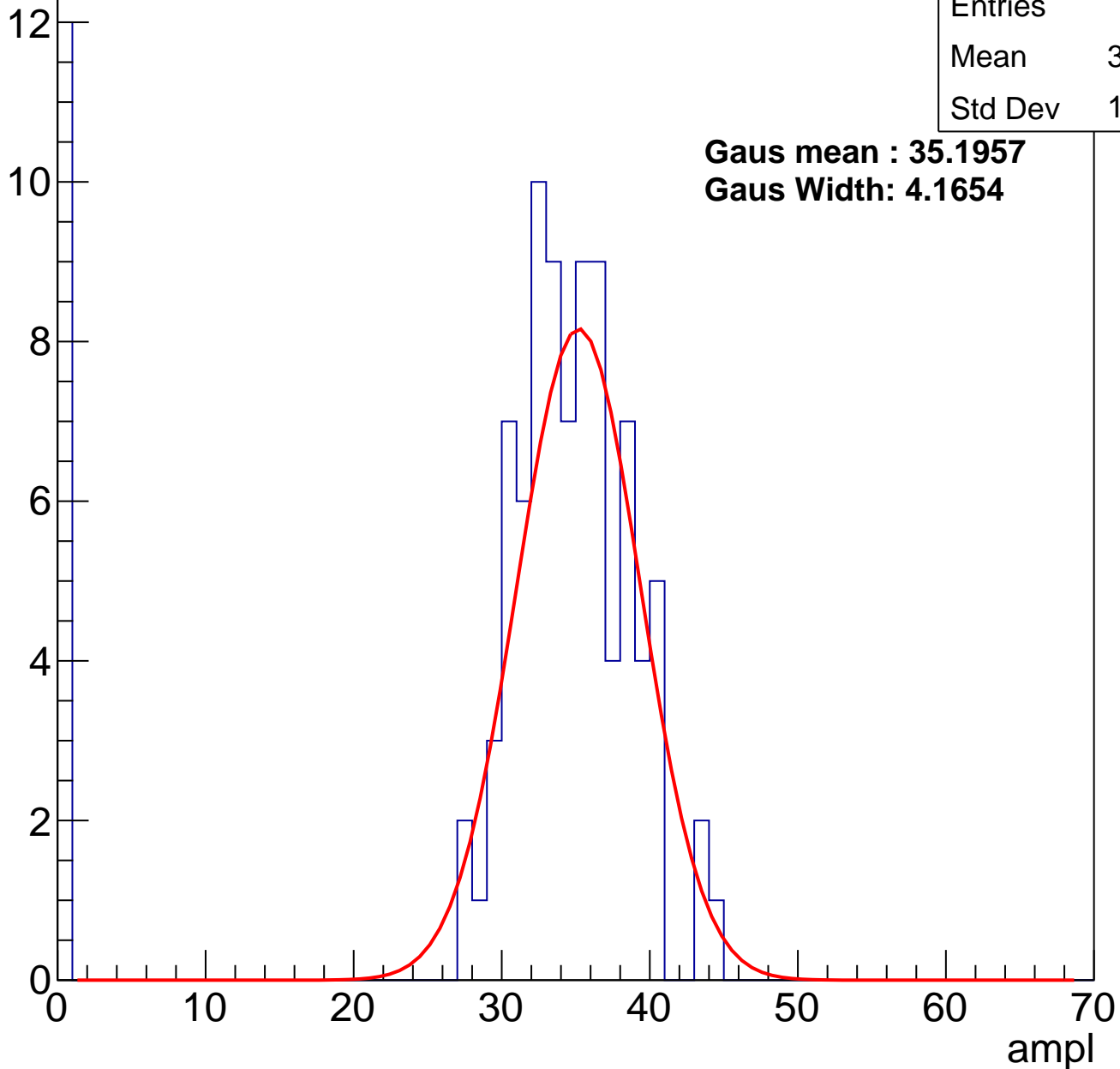
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	30.19
Std Dev	11.79

Gaus mean : 35.1957

Gaus Width: 4.1654

Entry



B1L103S, U2-ch97, adc2

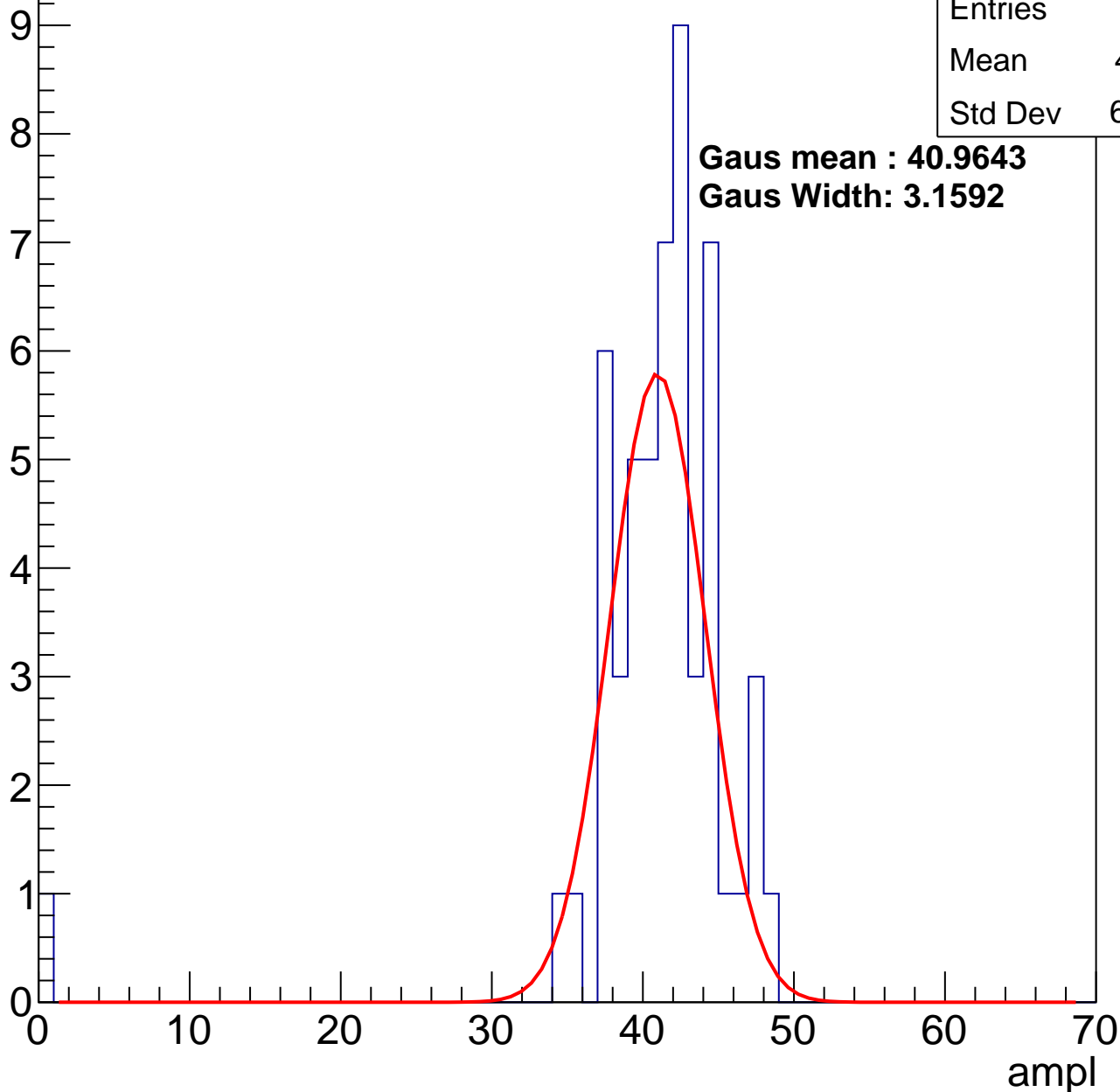
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	40.41
Std Dev	6.349

Gaus mean : 40.9643

Gaus Width: 3.1592

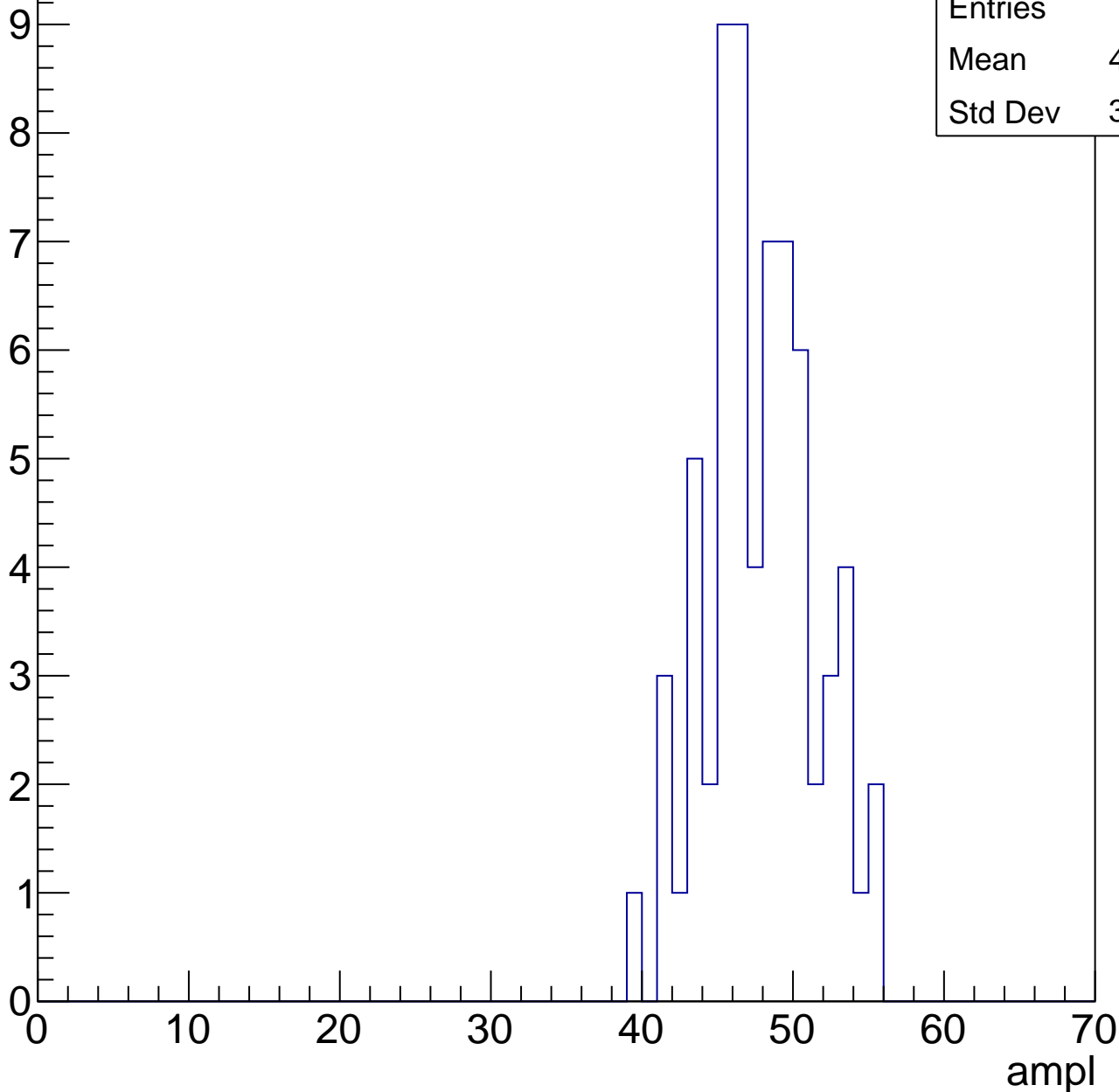


B1L103S, U2-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	47.38
Std Dev	3.583

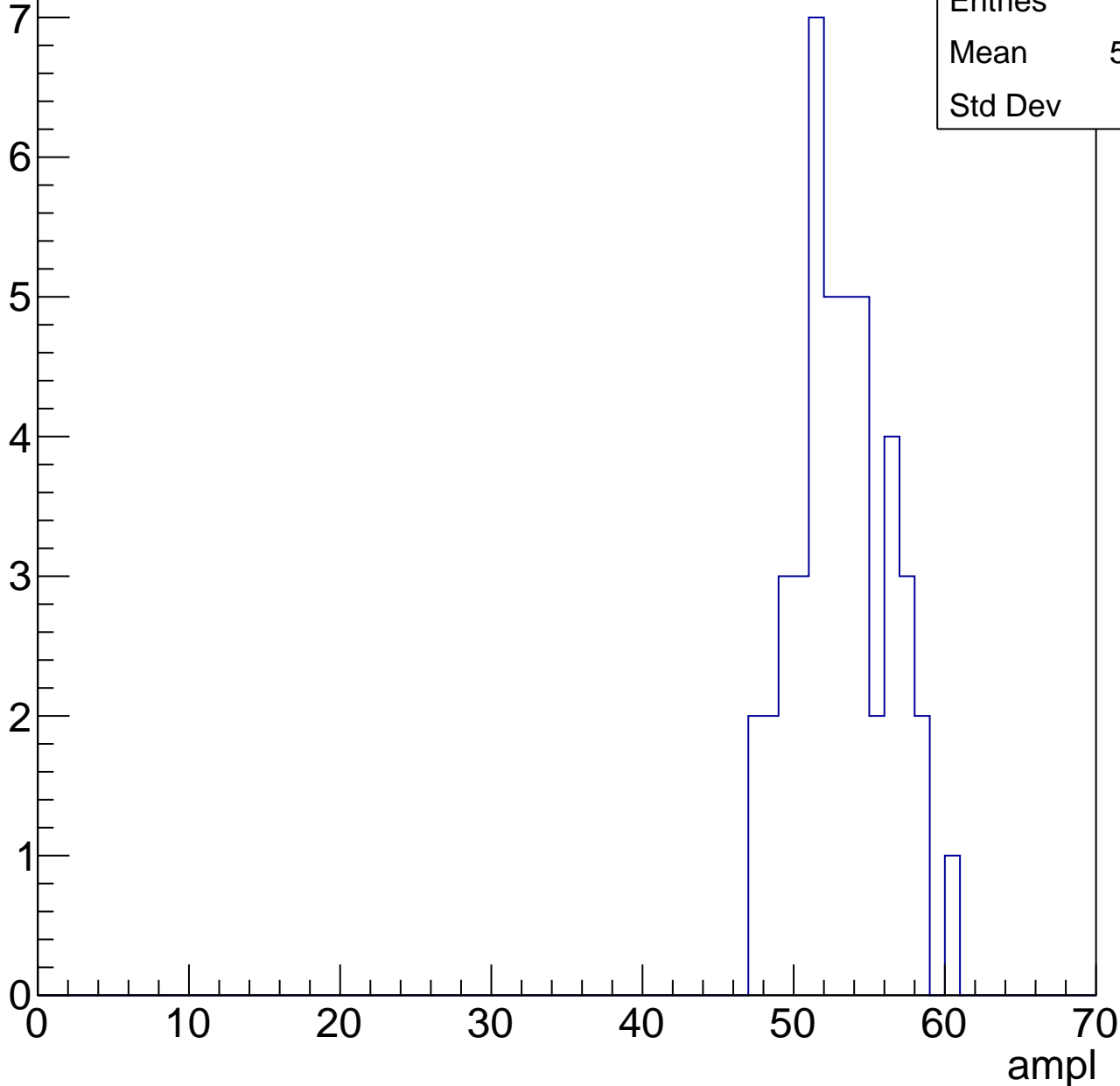


B1L103S, U2-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	52.73
Std Dev	3.1

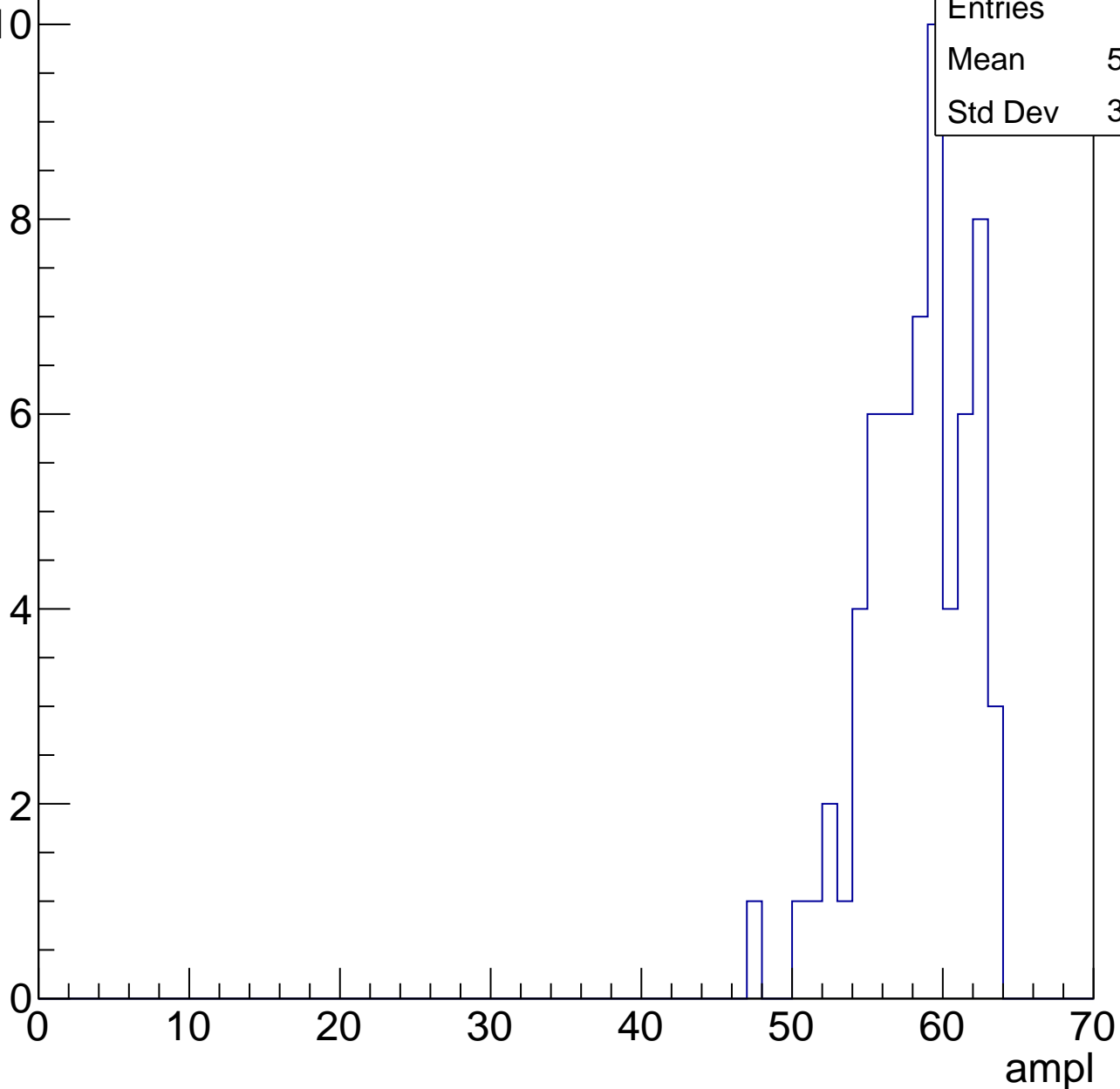


B1L103S, U2-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	57.82
Std Dev	3.393

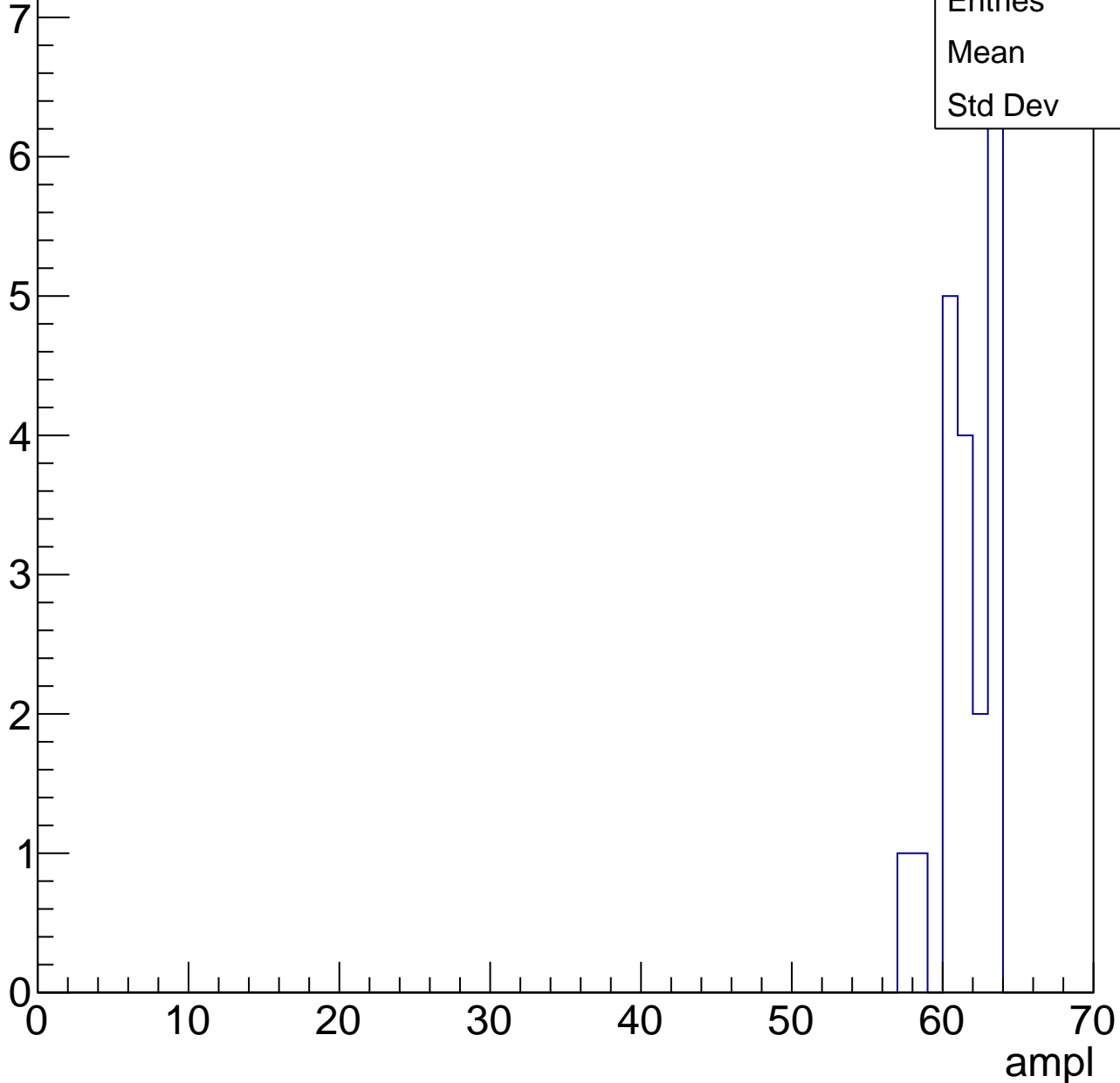


B1L103S, U2-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	61.2
Std Dev	1.72



B1L103S, U2-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch98, adc0

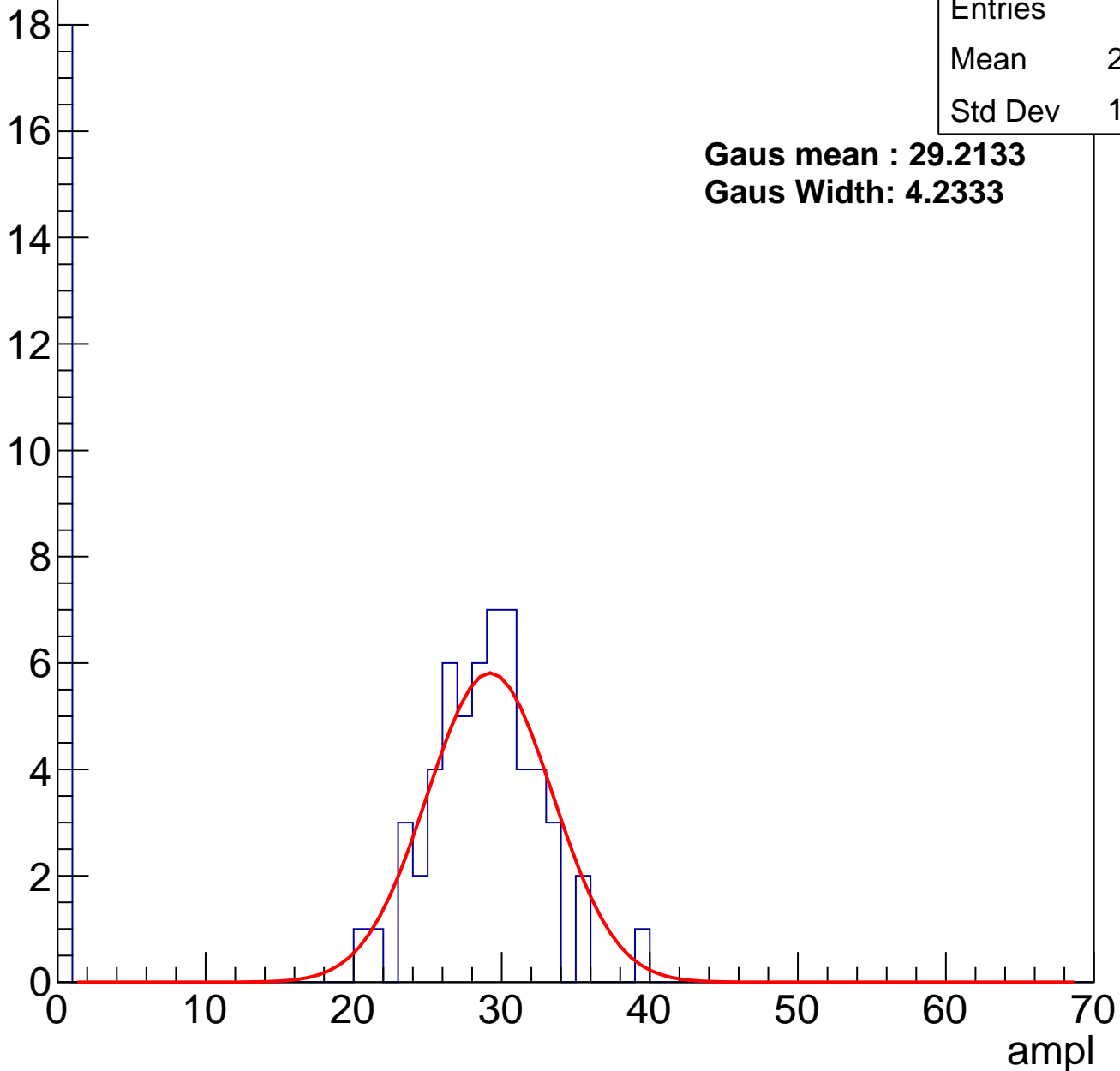
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	21.49
Std Dev	12.57

Gaus mean : 29.2133

Gaus Width: 4.2333

Entry



B1L103S, U2-ch98, adc1

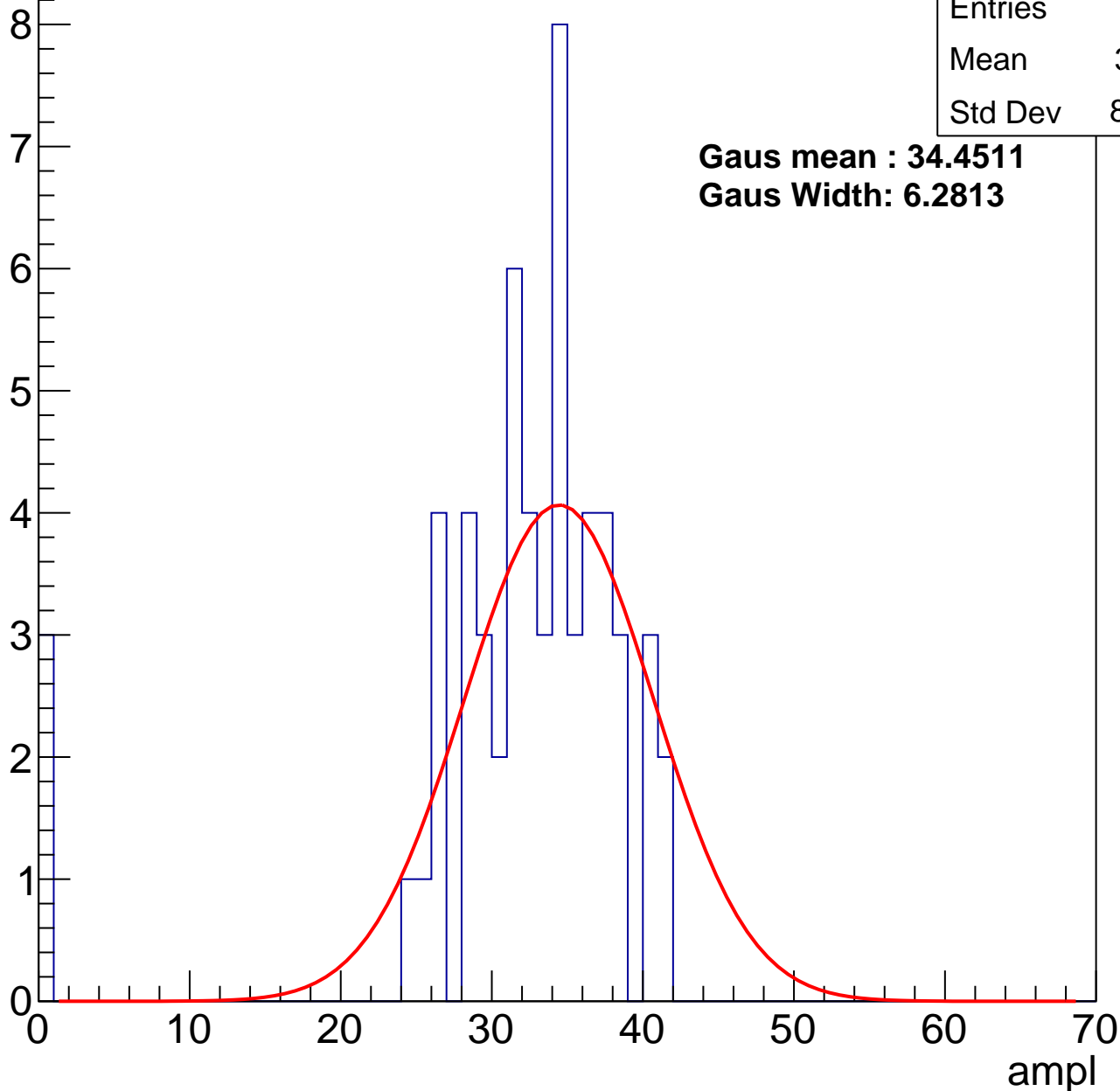
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	31.21
Std Dev	8.393

Gaus mean : 34.4511

Gaus Width: 6.2813



B1L103S, U2-ch98, adc2

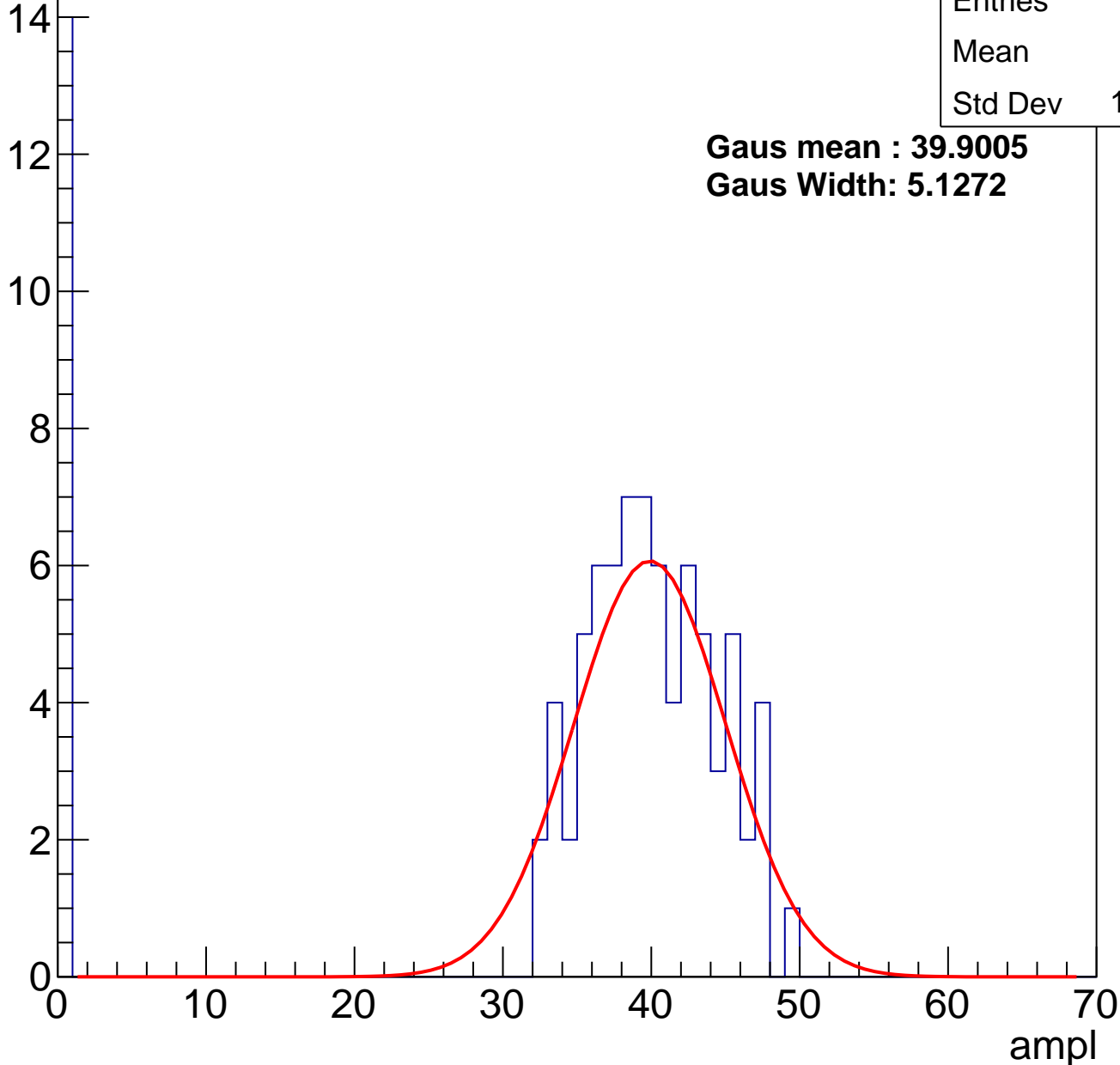
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	33.4
Std Dev	14.93

Gaus mean : 39.9005

Gaus Width: 5.1272

Entry

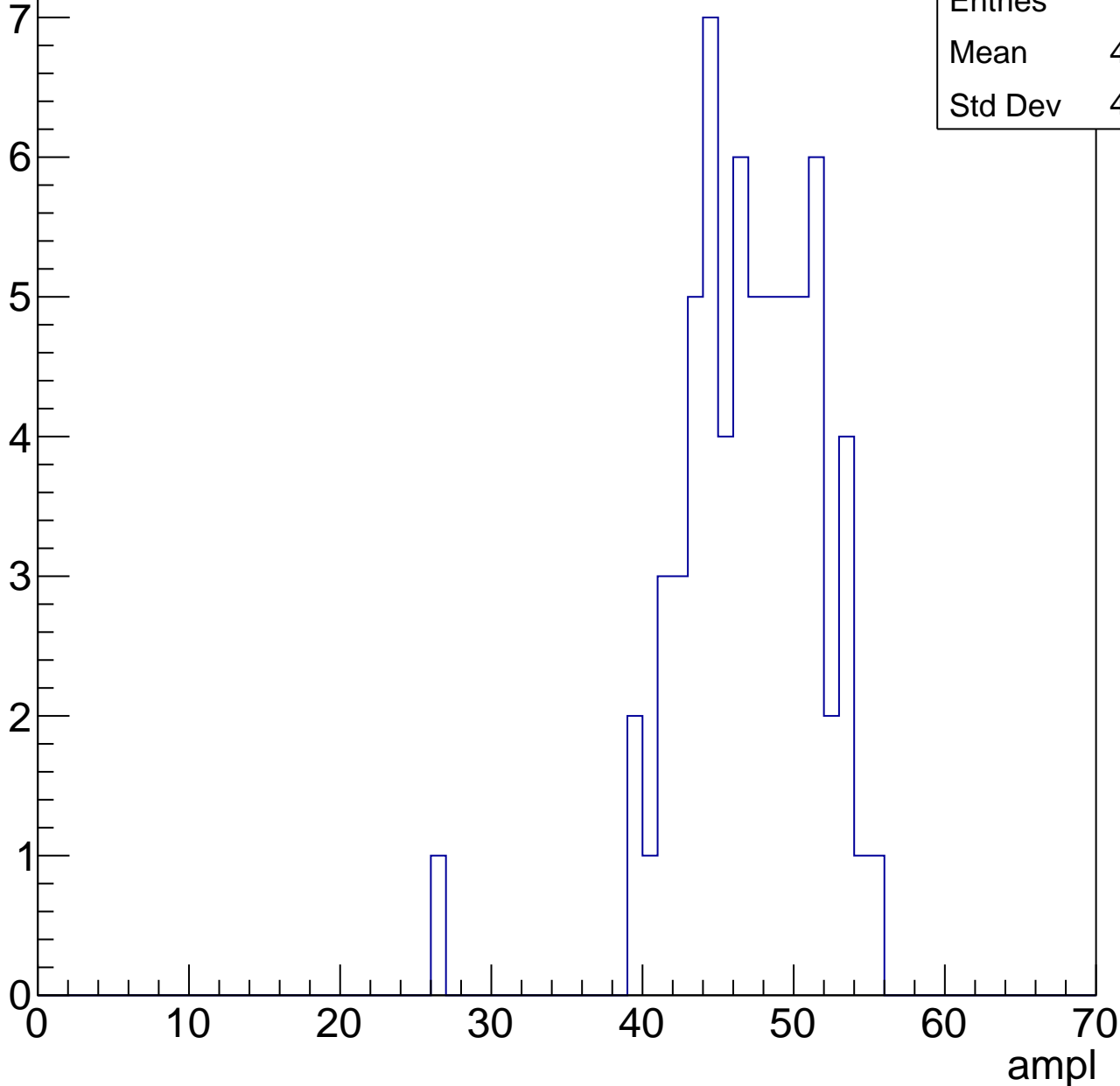


B1L103S, U2-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	46.56
Std Dev	4.649

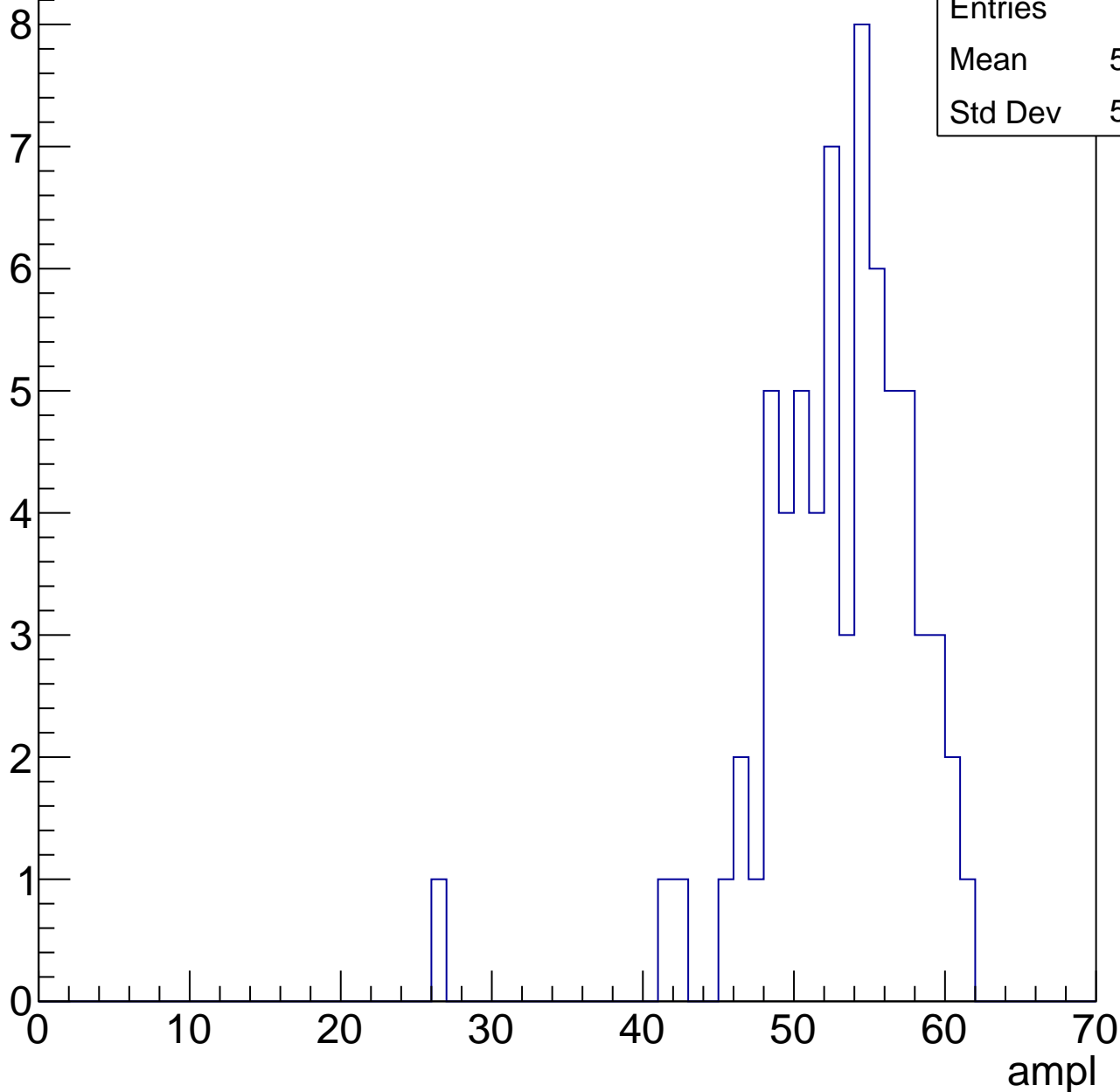


B1L103S, U2-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	52.43
Std Dev	5.326

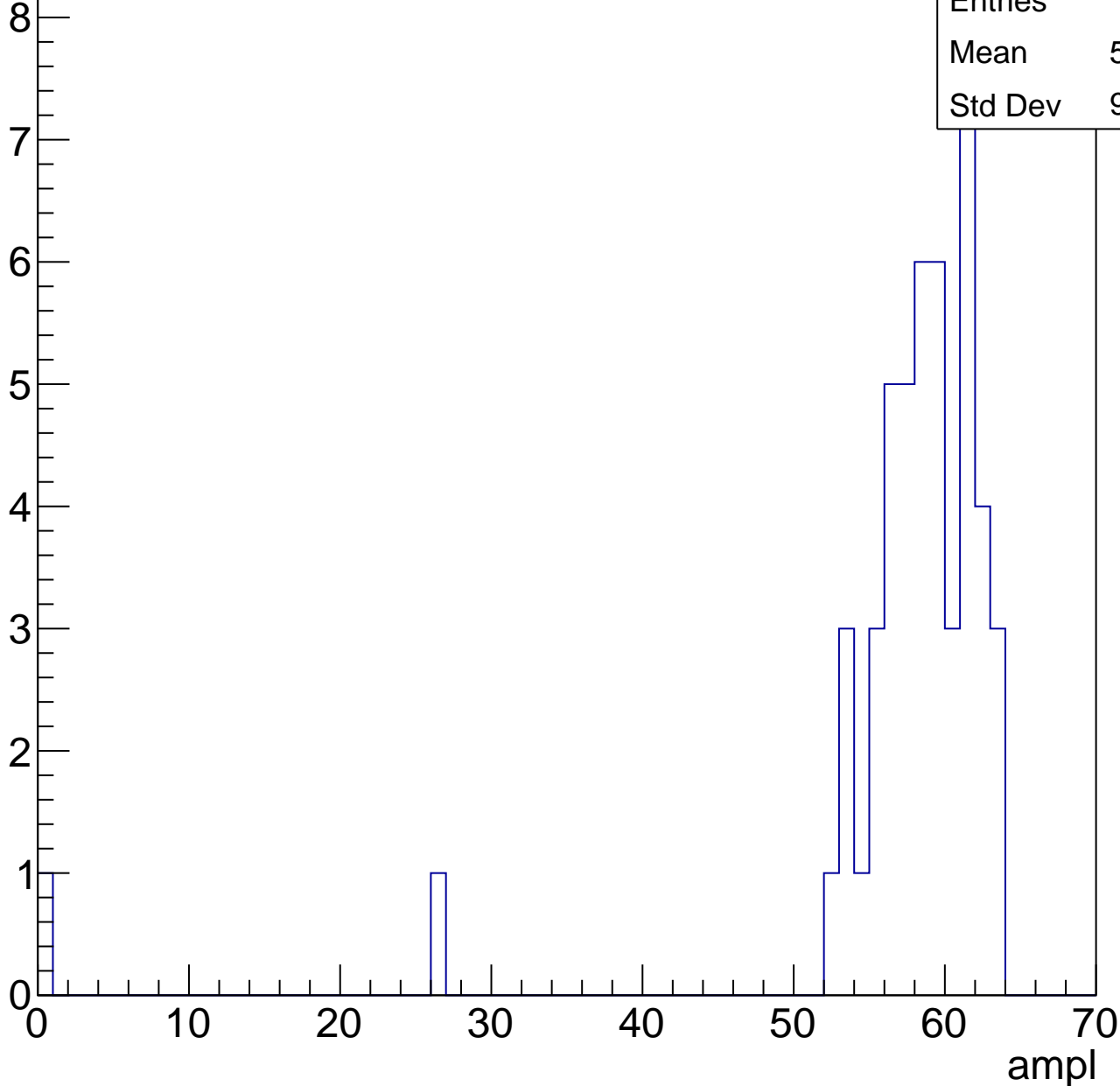


B1L103S, U2-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	56.56
Std Dev	9.687

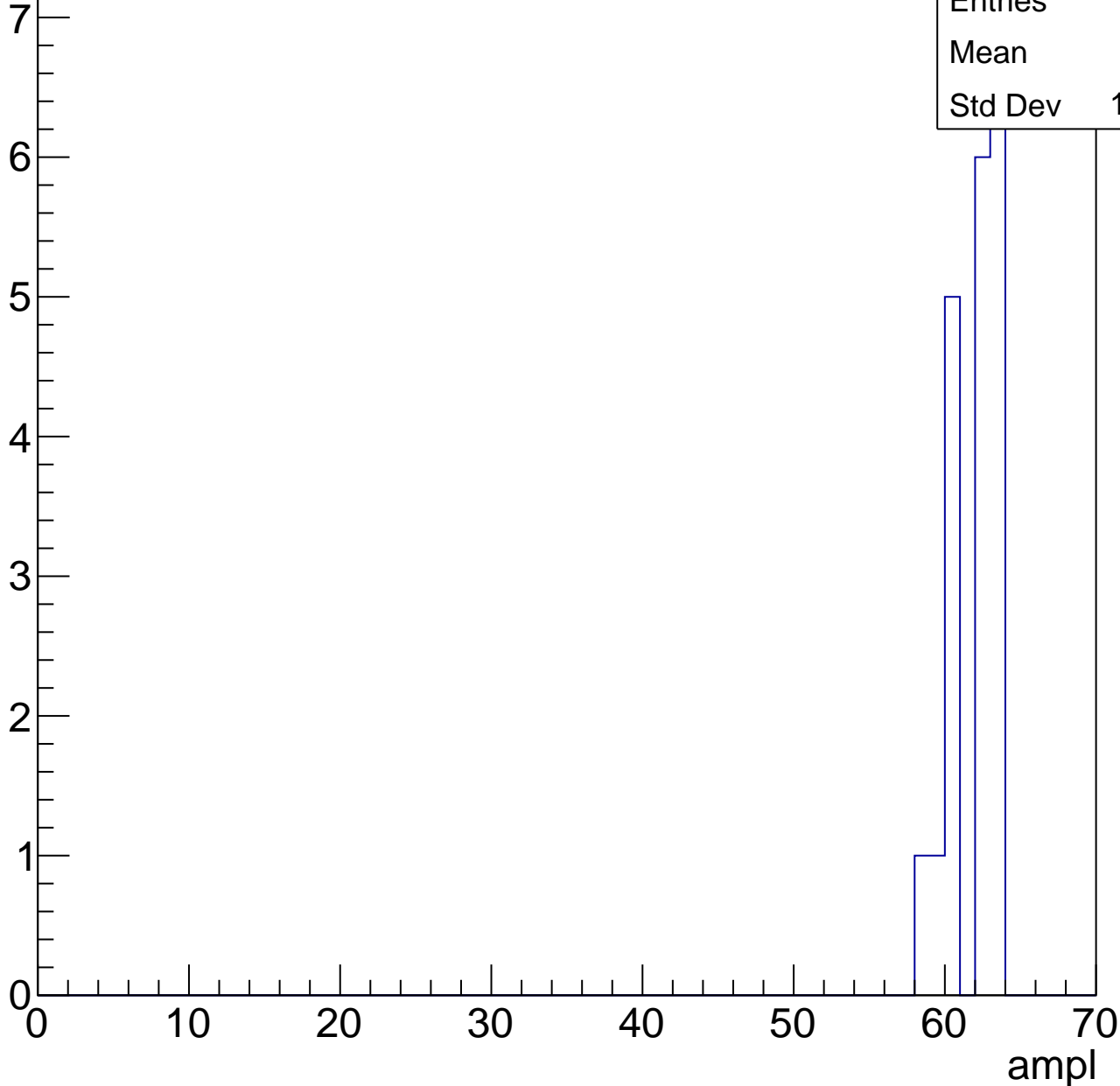


B1L103S, U2-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	61.5
Std Dev	1.533



B1L103S, U2-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U2-ch99, adc0

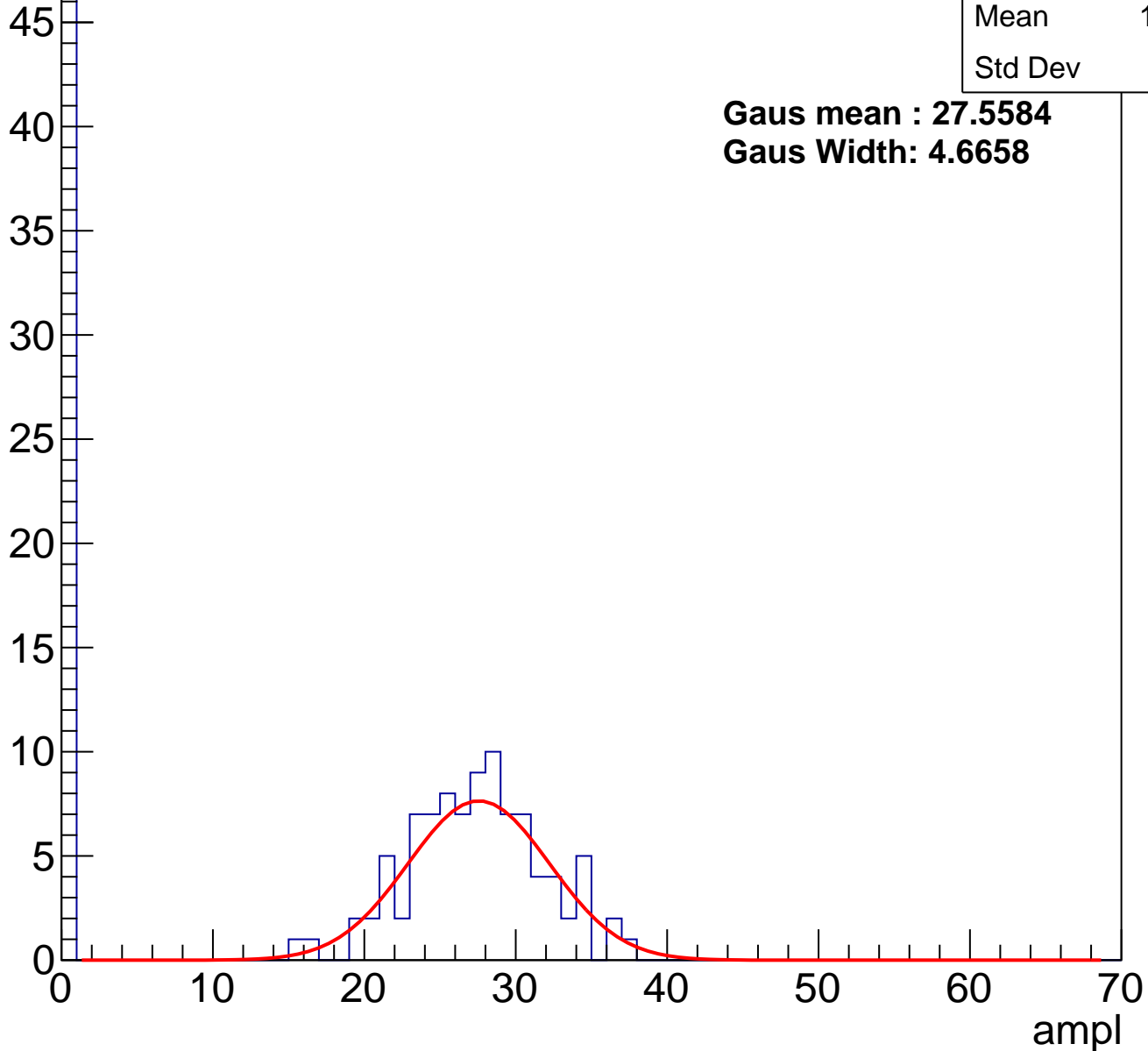
calib_packv5_041523_1651.root, FC#0, port C2

Entries	140
Mean	17.87
Std Dev	13.2

Gaus mean : 27.5584

Gaus Width: 4.6658

Entry



B1L103S, U2-ch99, adc1

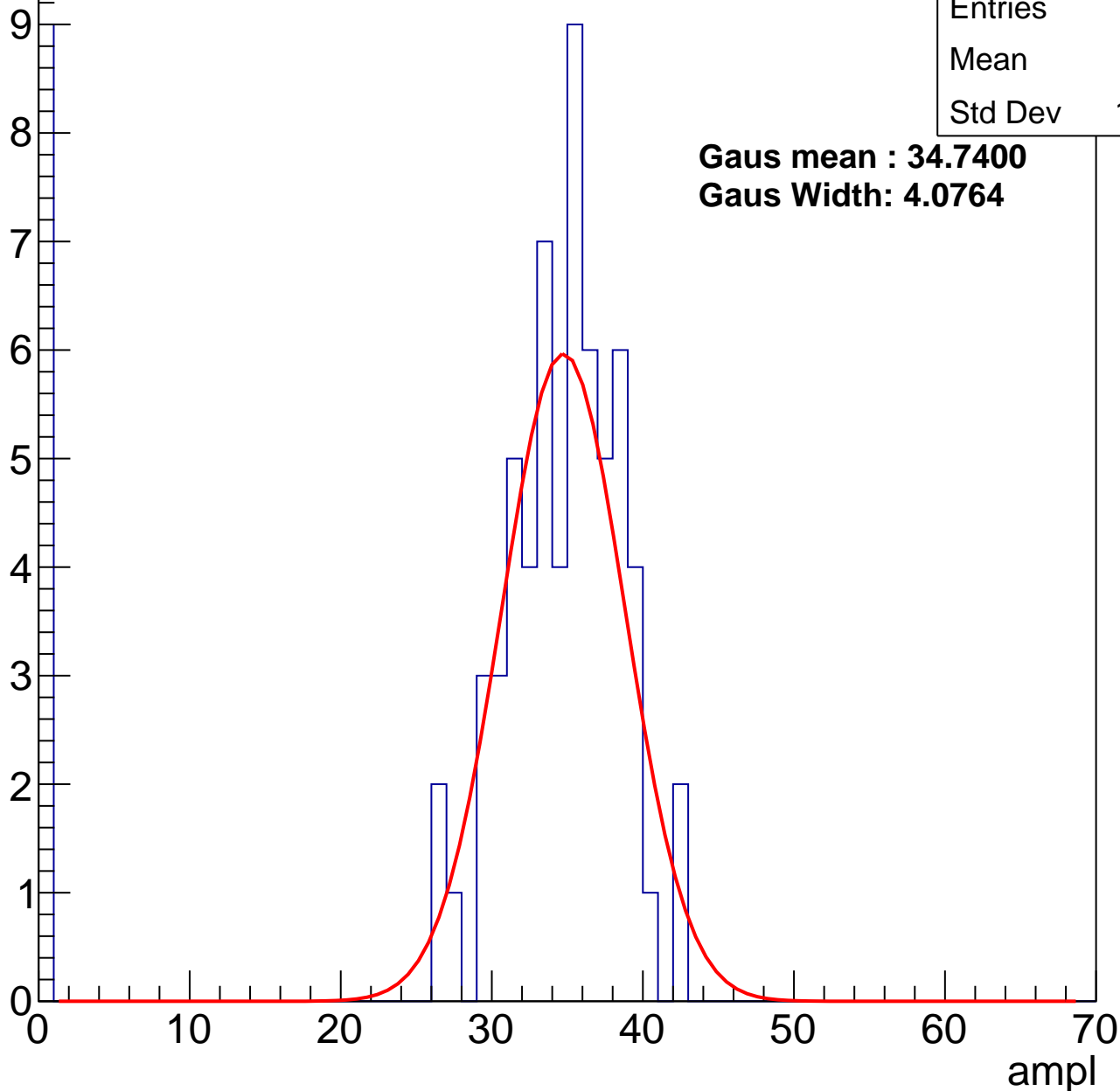
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30
Std Dev	11.91

Gaus mean : 34.7400

Gaus Width: 4.0764



B1L103S, U2-ch99, adc2

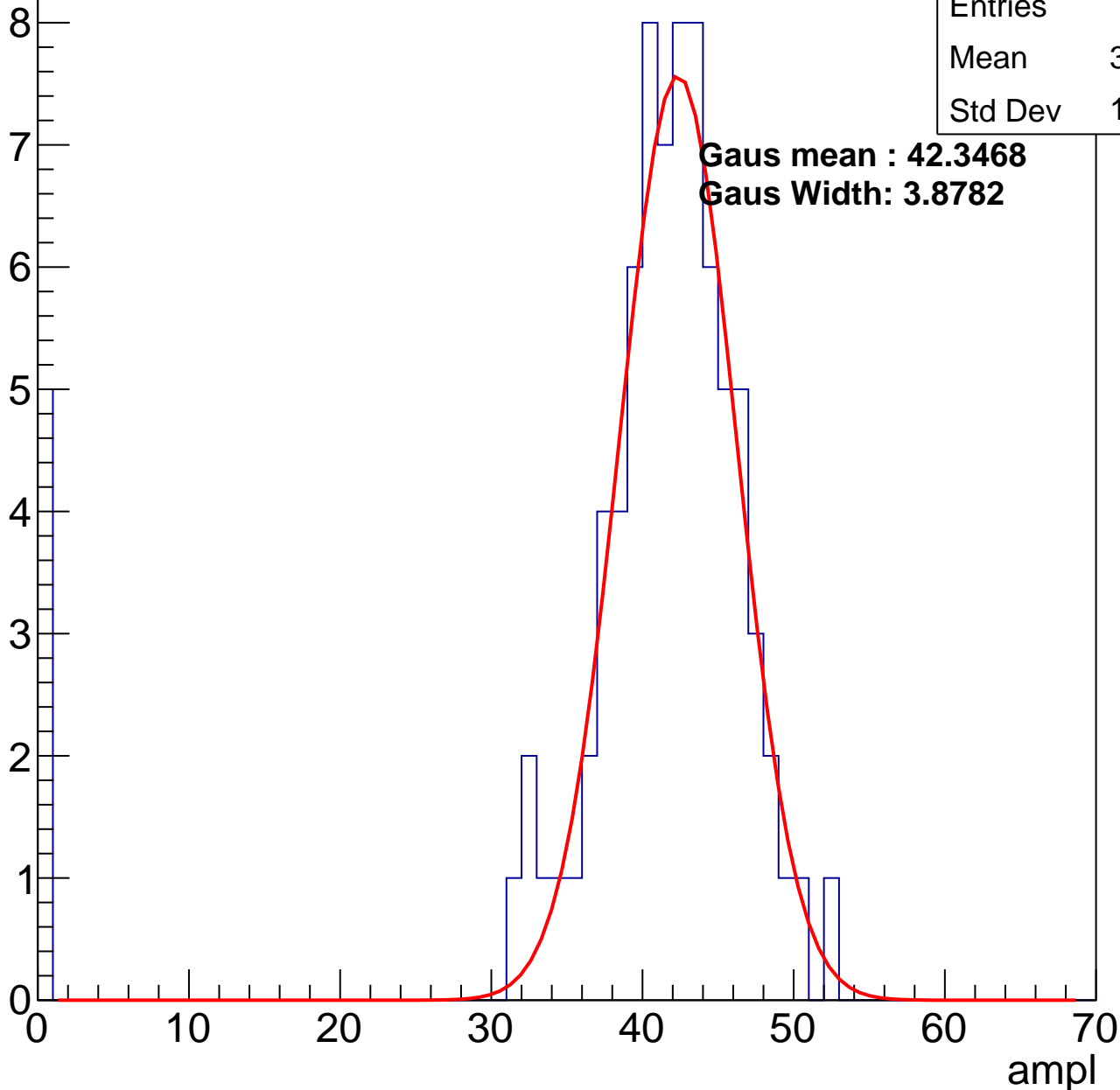
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	38.99
Std Dev	10.73

Gaus mean : 42.3468

Gaus Width: 3.8782



B1L103S, U2-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 56

Mean 47.75

Std Dev 3.496

ampl

0

10

20

30

40

50

60

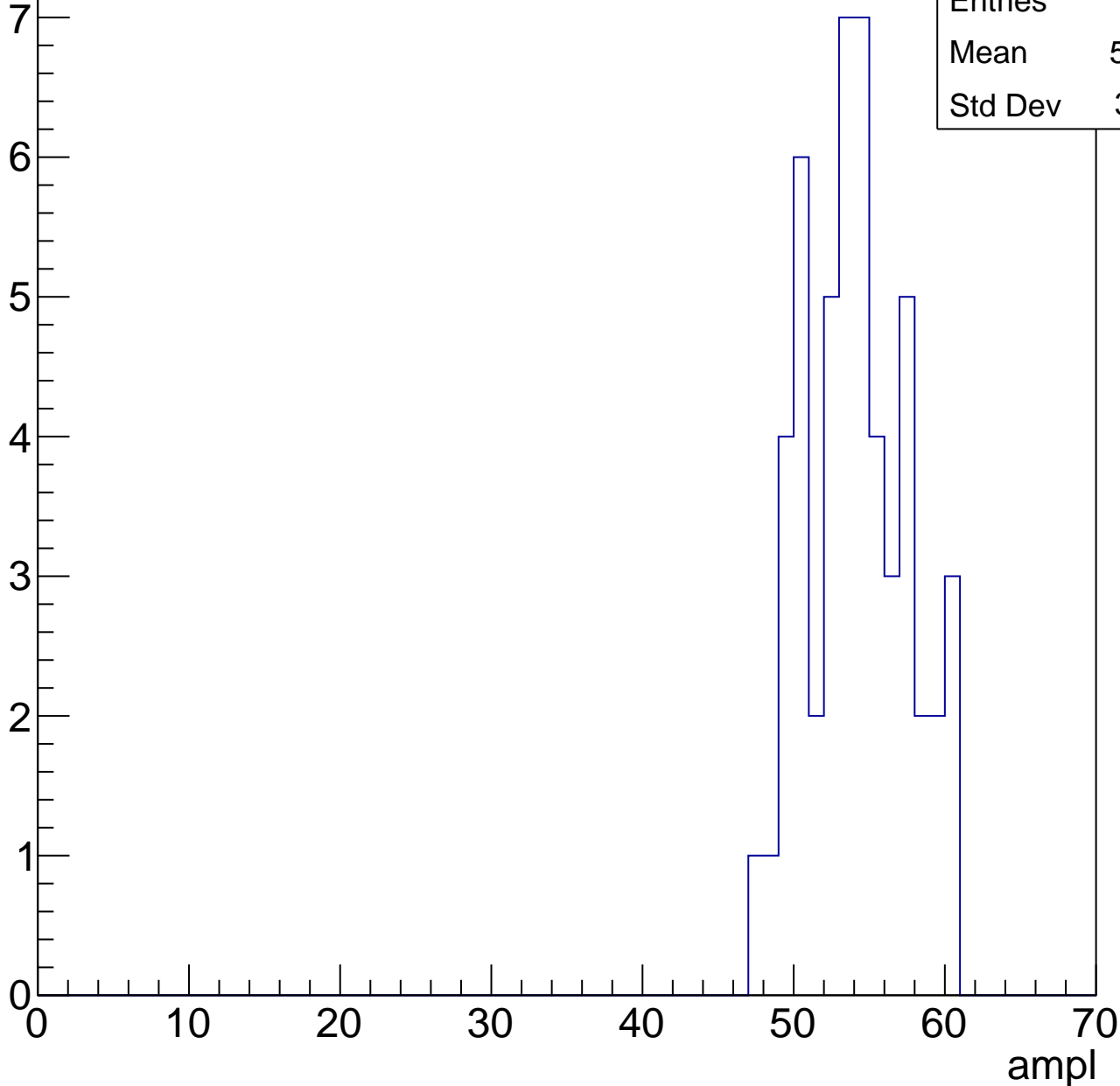
70

B1L103S, U2-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.63
Std Dev	3.311

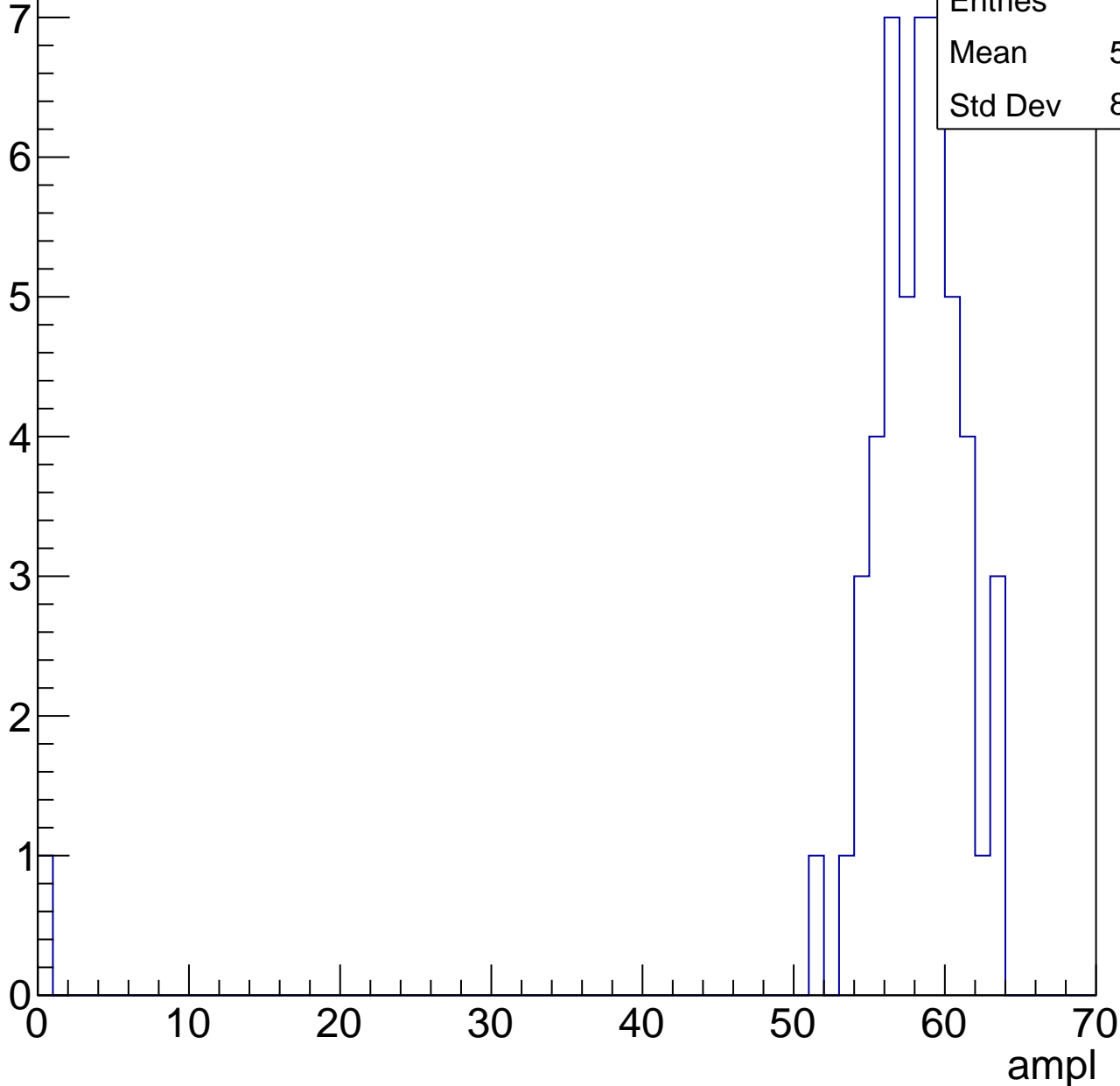


B1L103S, U2-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	56.67
Std Dev	8.598

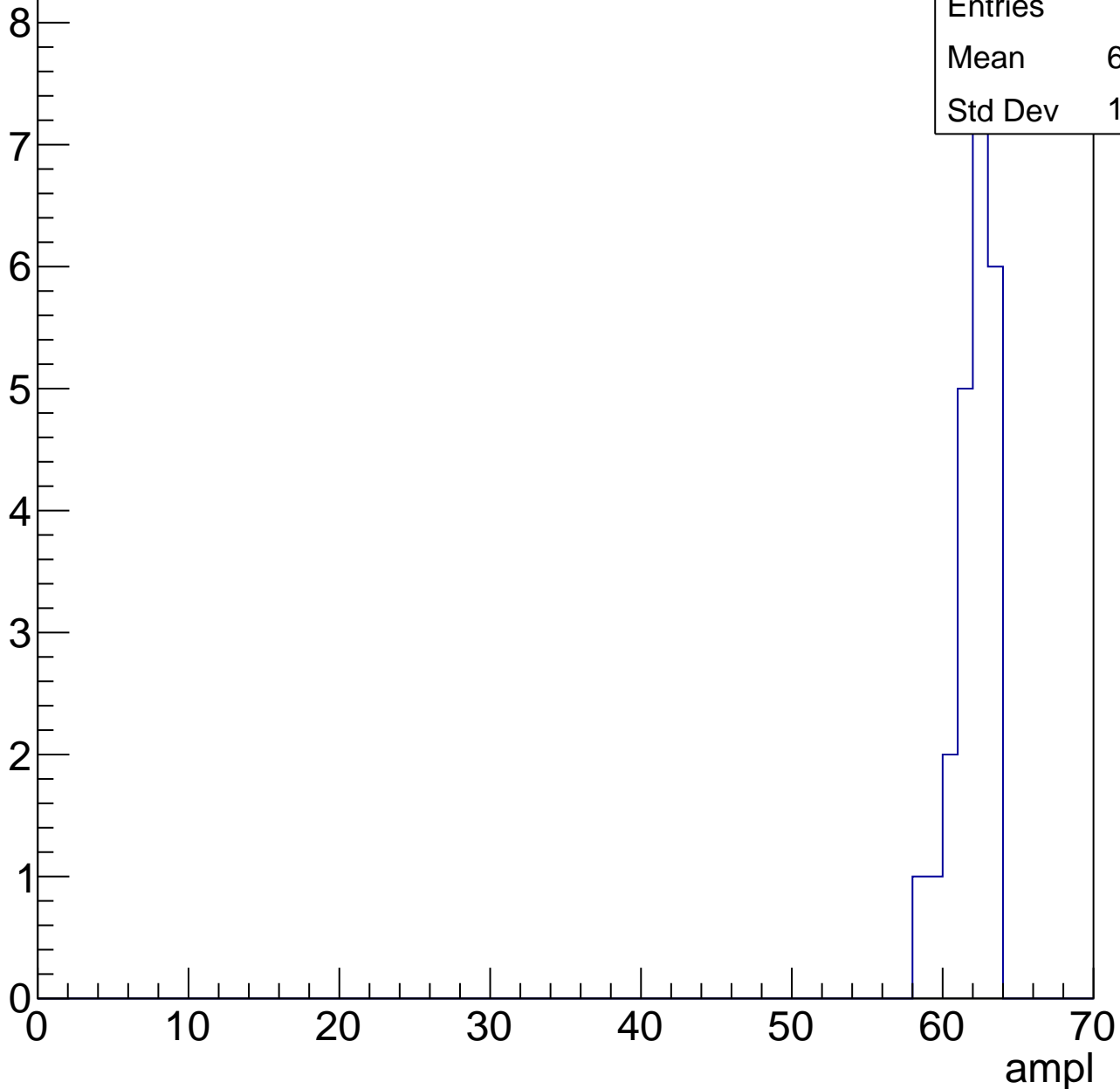


B1L103S, U2-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	61.57
Std Dev	1.313

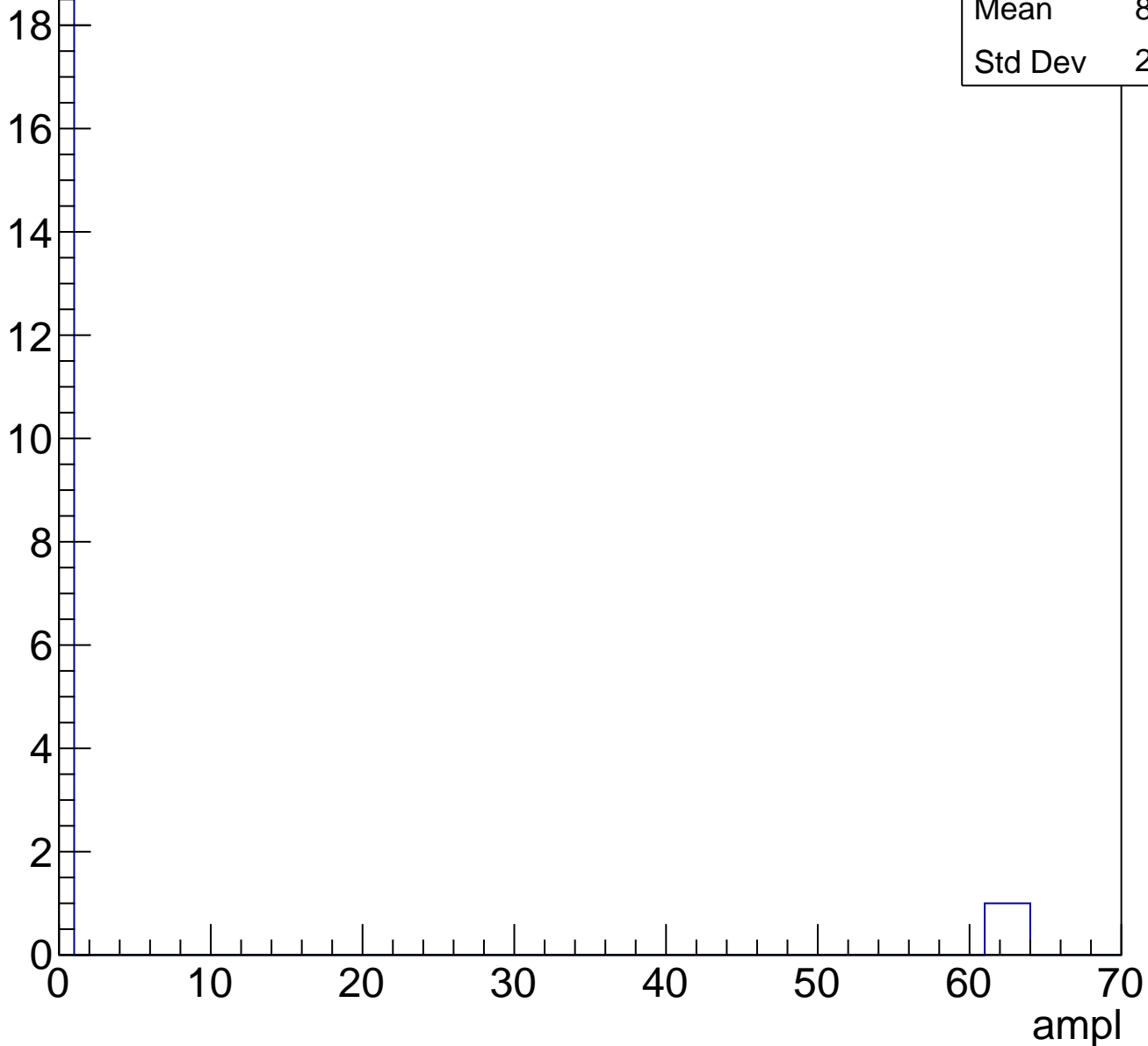


B1L103S, U2-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U2-ch100, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	18.34
Std Dev	12.6

Gaus mean : 26.8941

Gaus Width: 3.6535

Entry

25

20

15

10

5

0

0

10

20

30

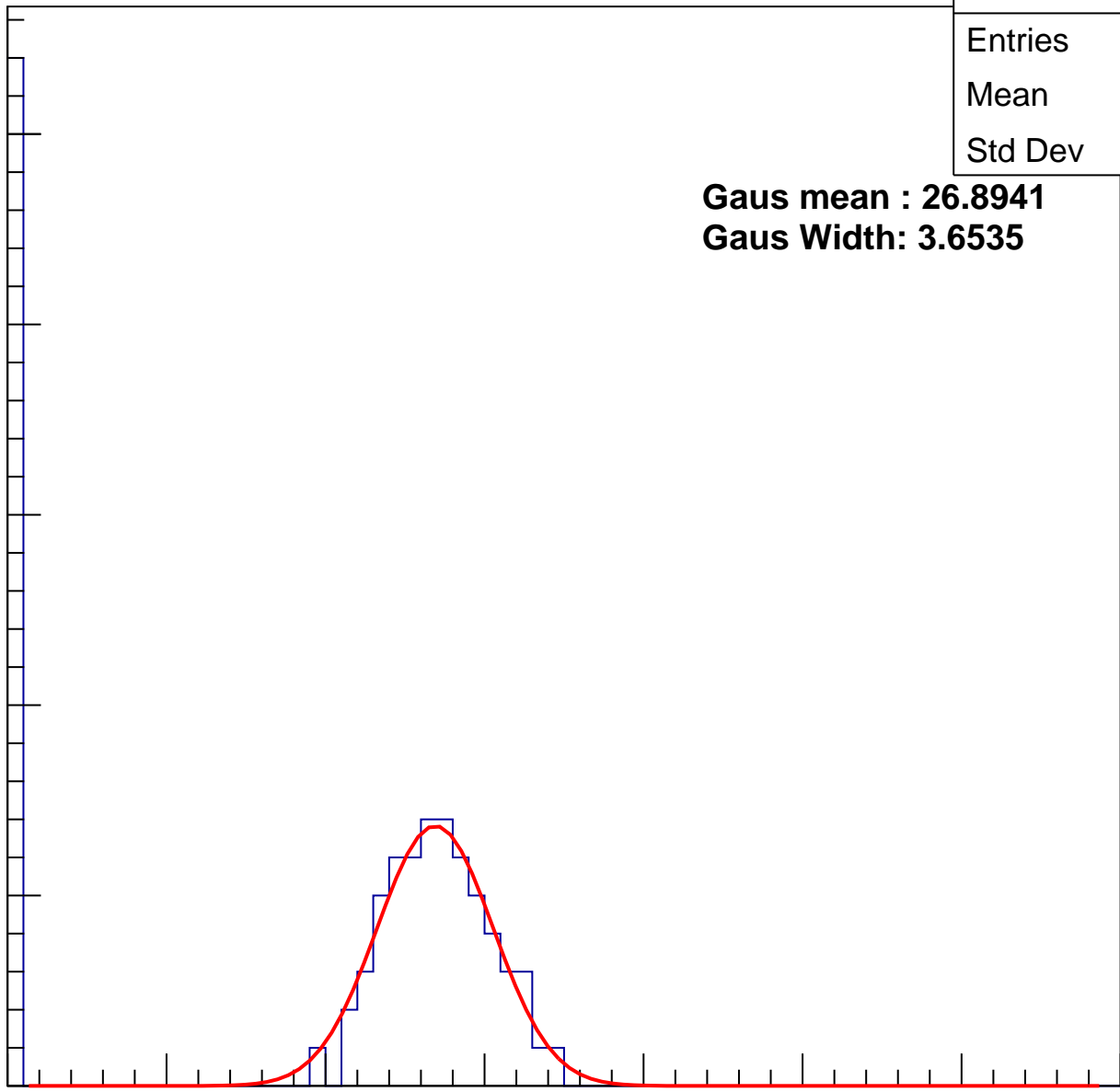
40

50

60

70

ampl



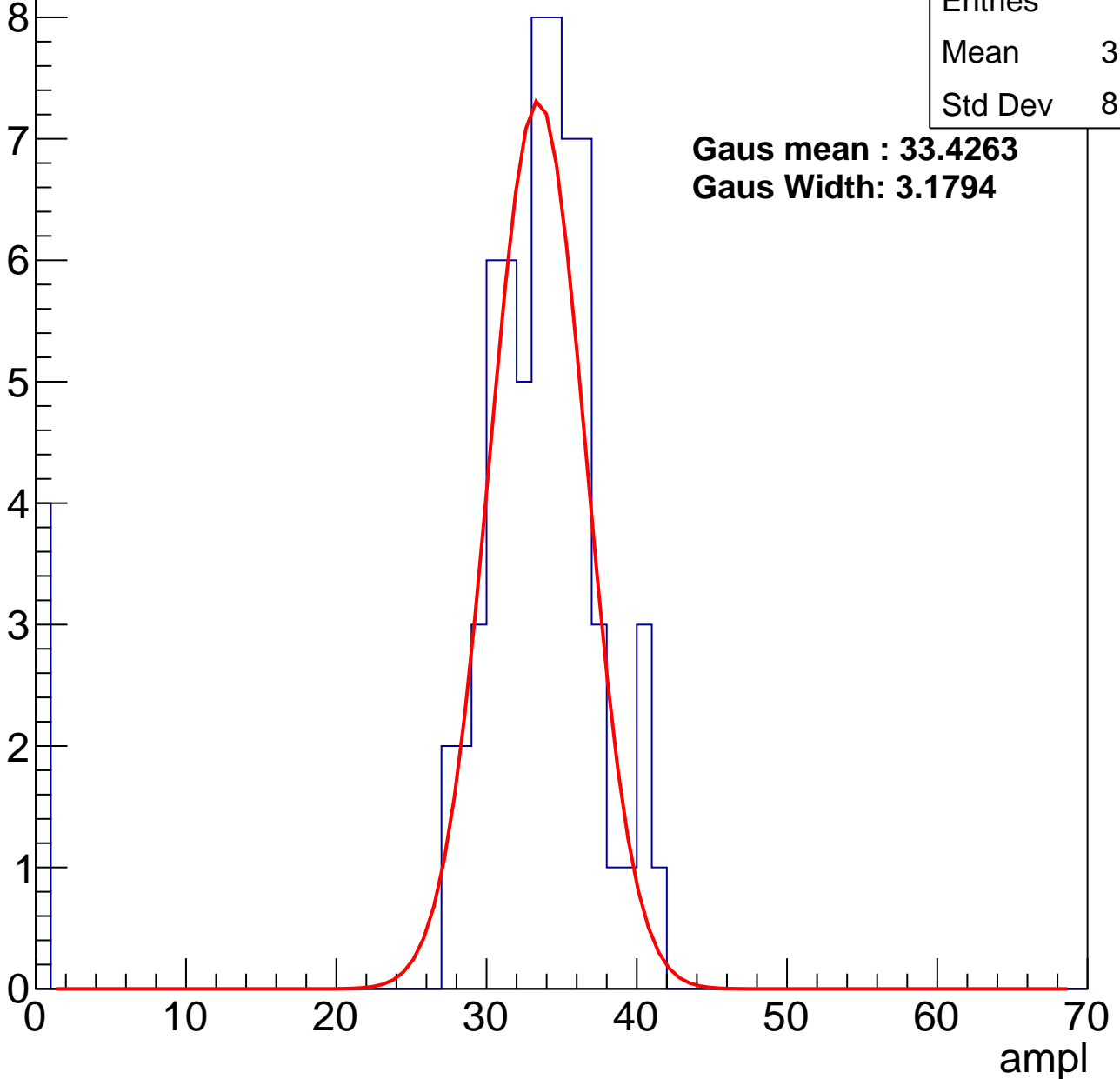
B1L103S, U2-ch100, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	31.42
Std Dev	8.517

Gaus mean : 33.4263
Gaus Width: 3.1794



B1L103S, U2-ch100, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	33.47
Std Dev	14.78

Gaus mean : 41.0082

Gaus Width: 3.9342

Entry

12

10

8

6

4

2

0

0

10

20

30

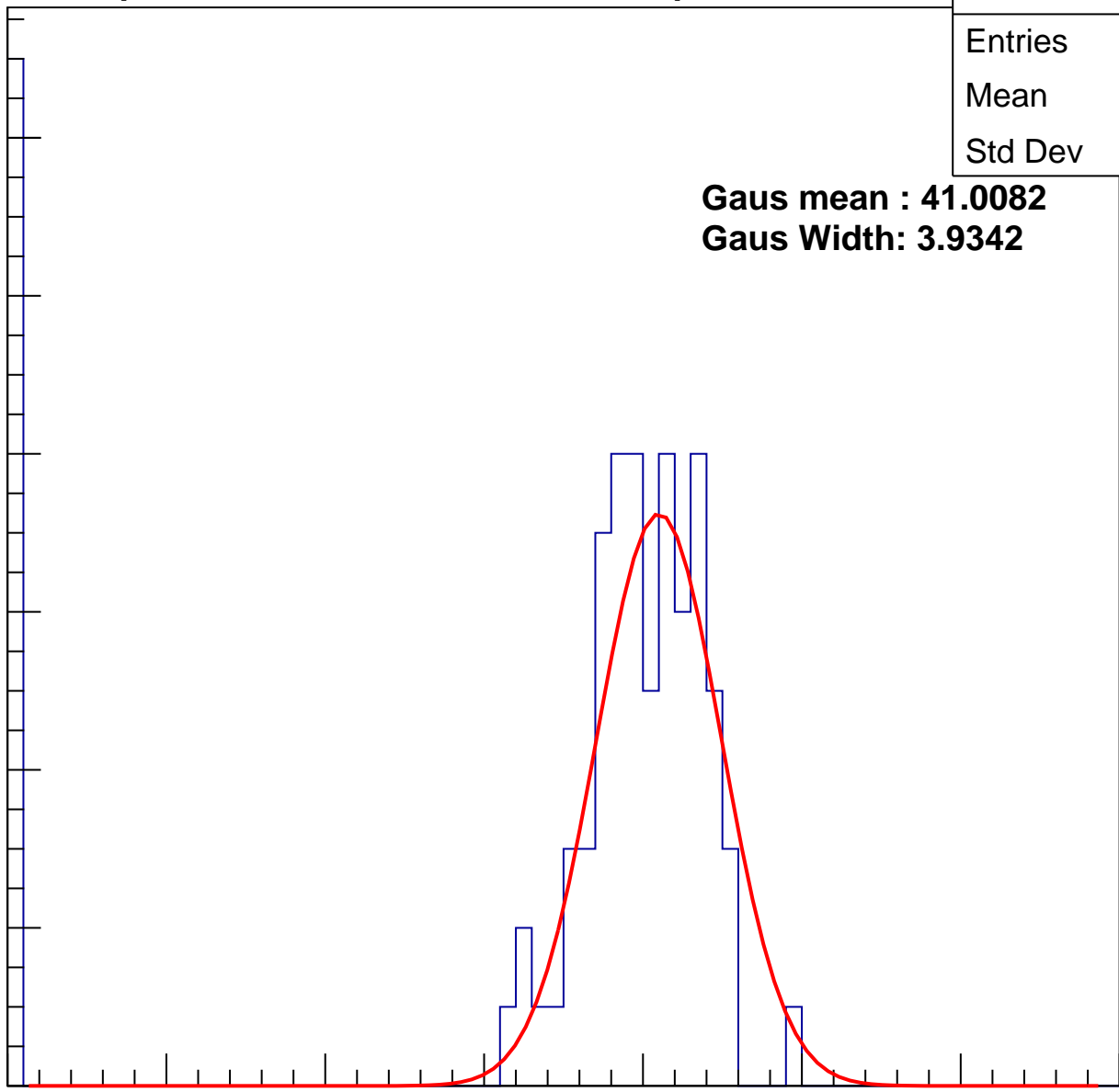
40

50

60

70

ampl



B1L103S, U2-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	46.51
Std Dev	3.565

Entry

10

8

6

4

2

0

0

10

20

30

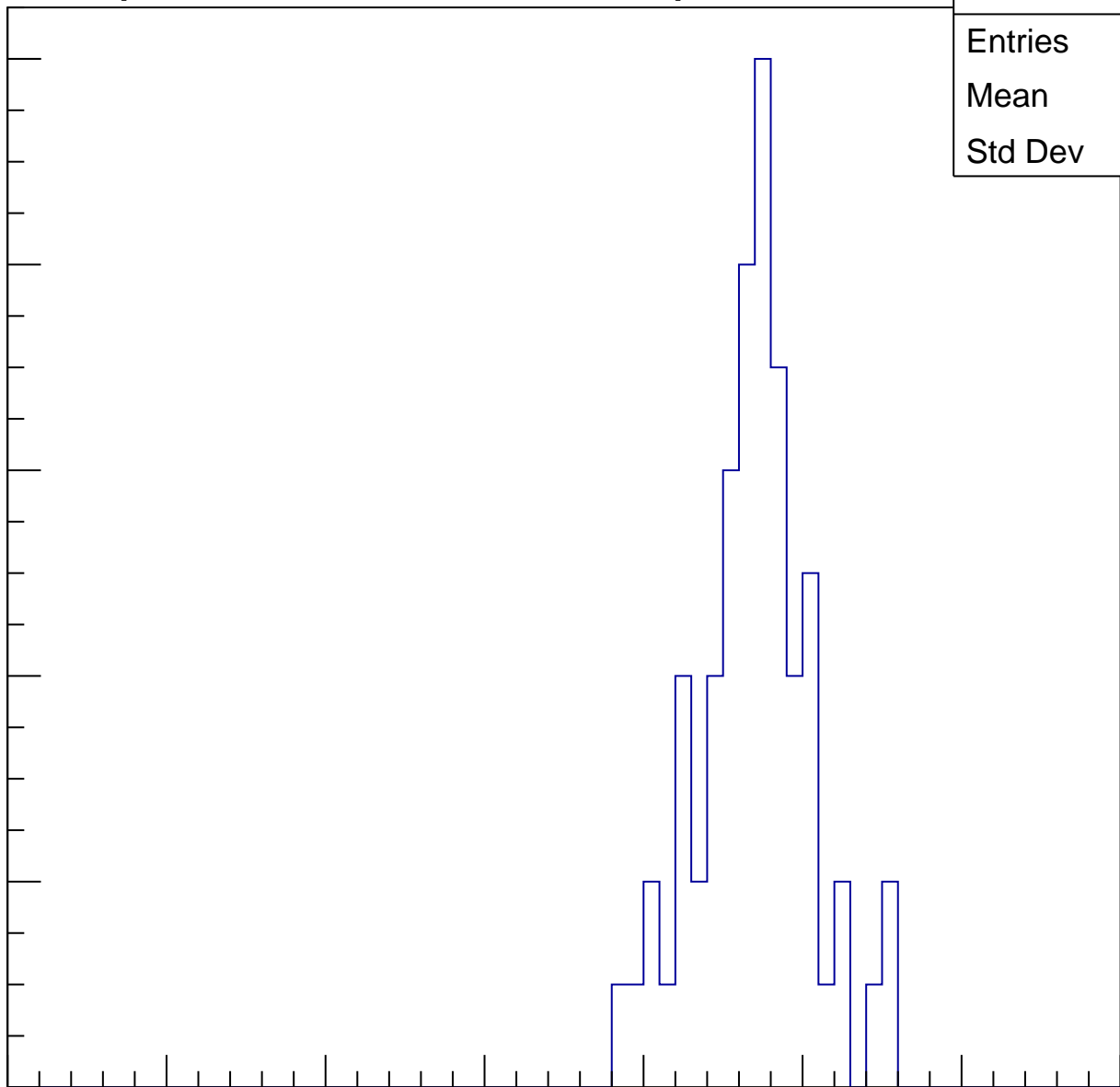
40

50

60

70

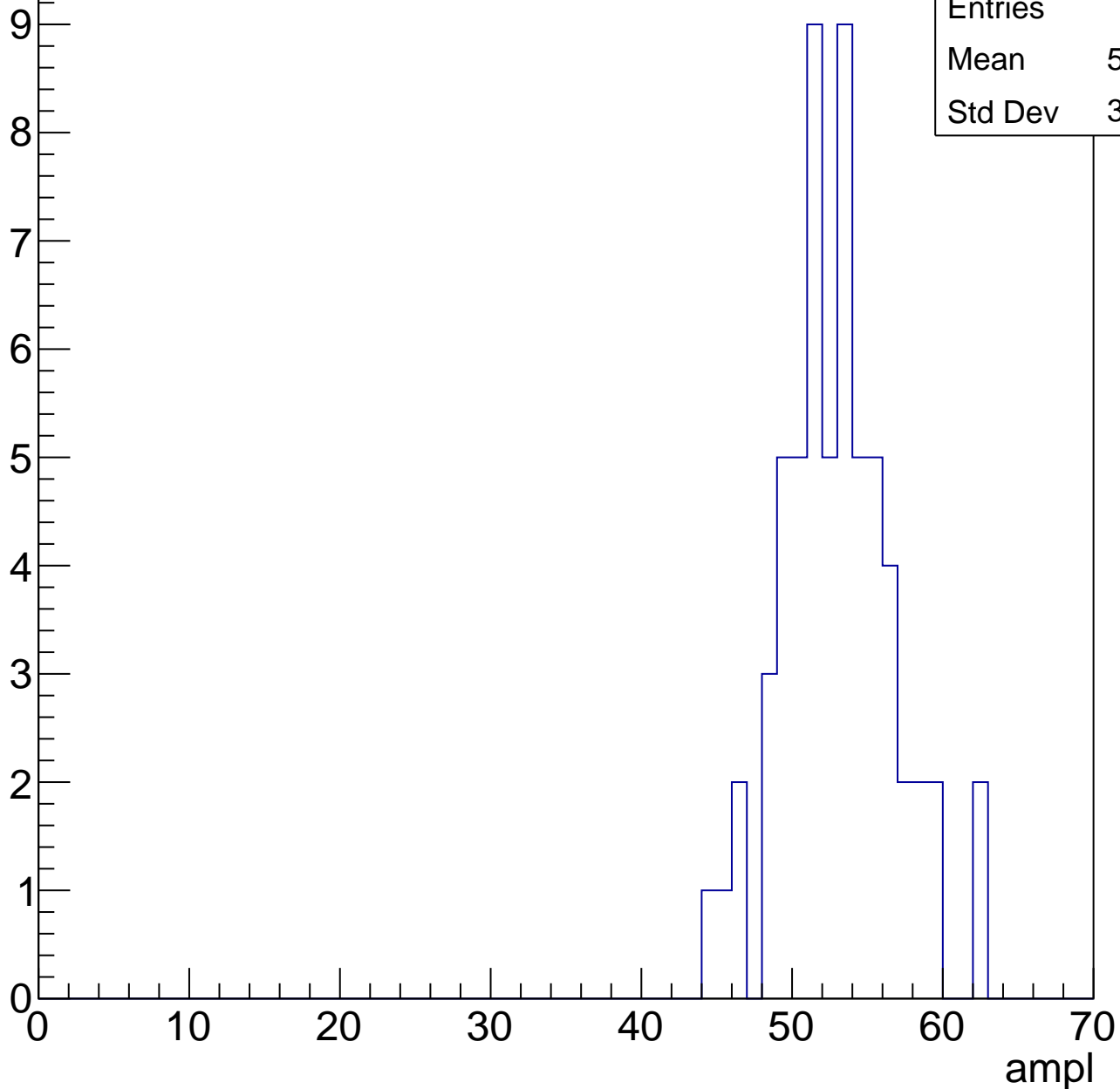
ampl



B1L103S, U2-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



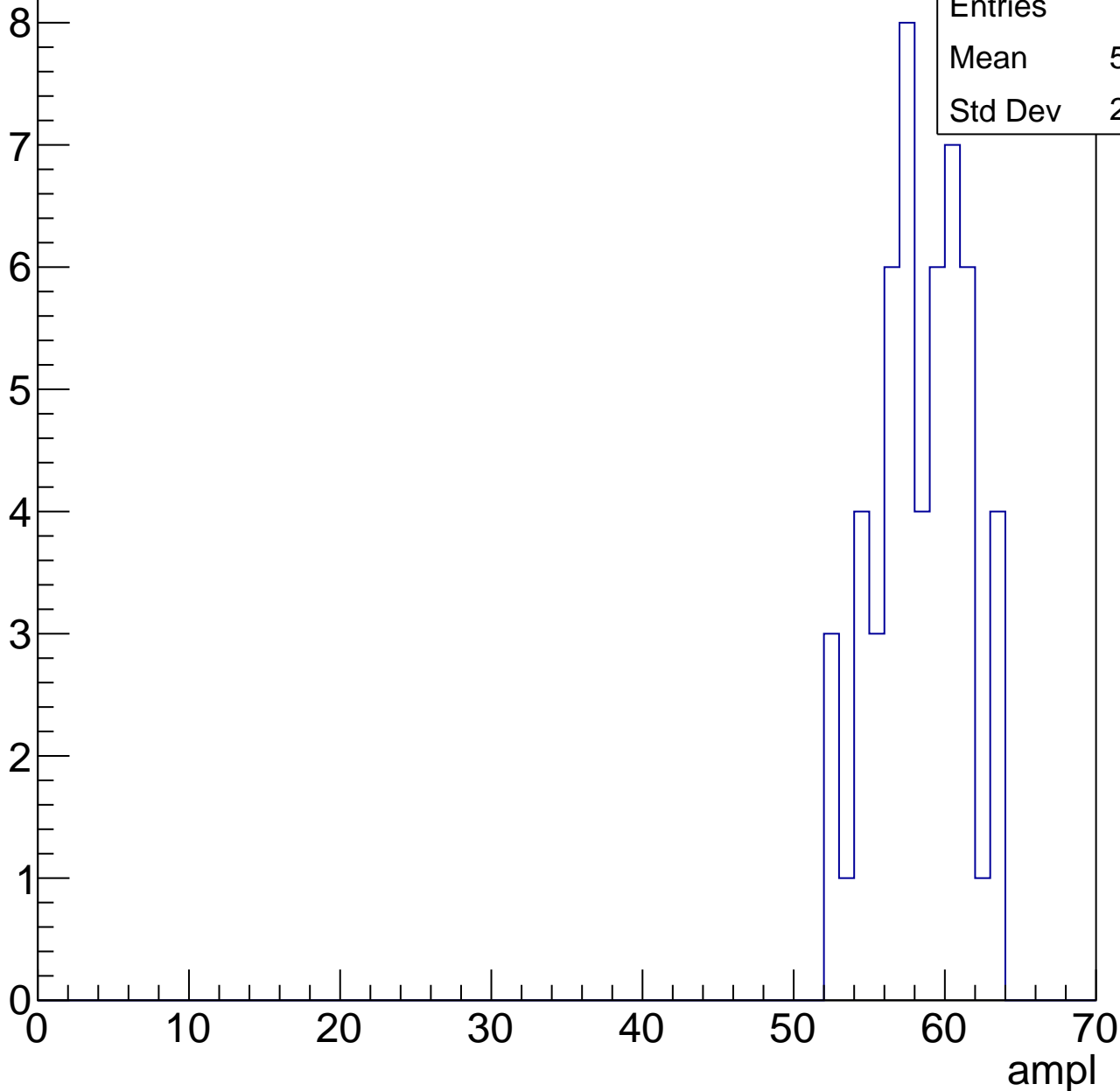
Entries	62
Mean	52.53
Std Dev	3.697

B1L103S, U2-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.89
Std Dev	2.944

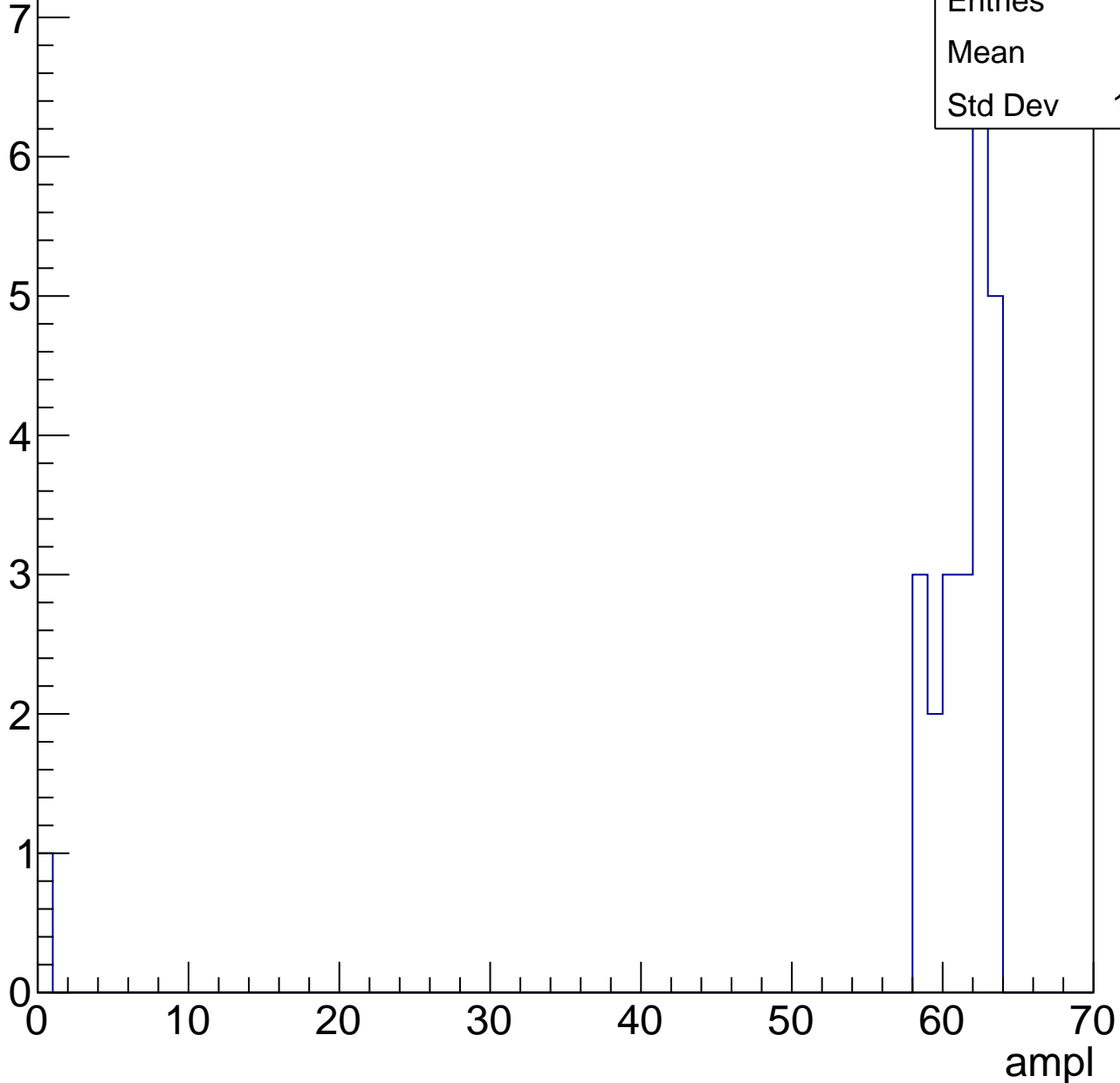


B1L103S, U2-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.5
Std Dev	12.31

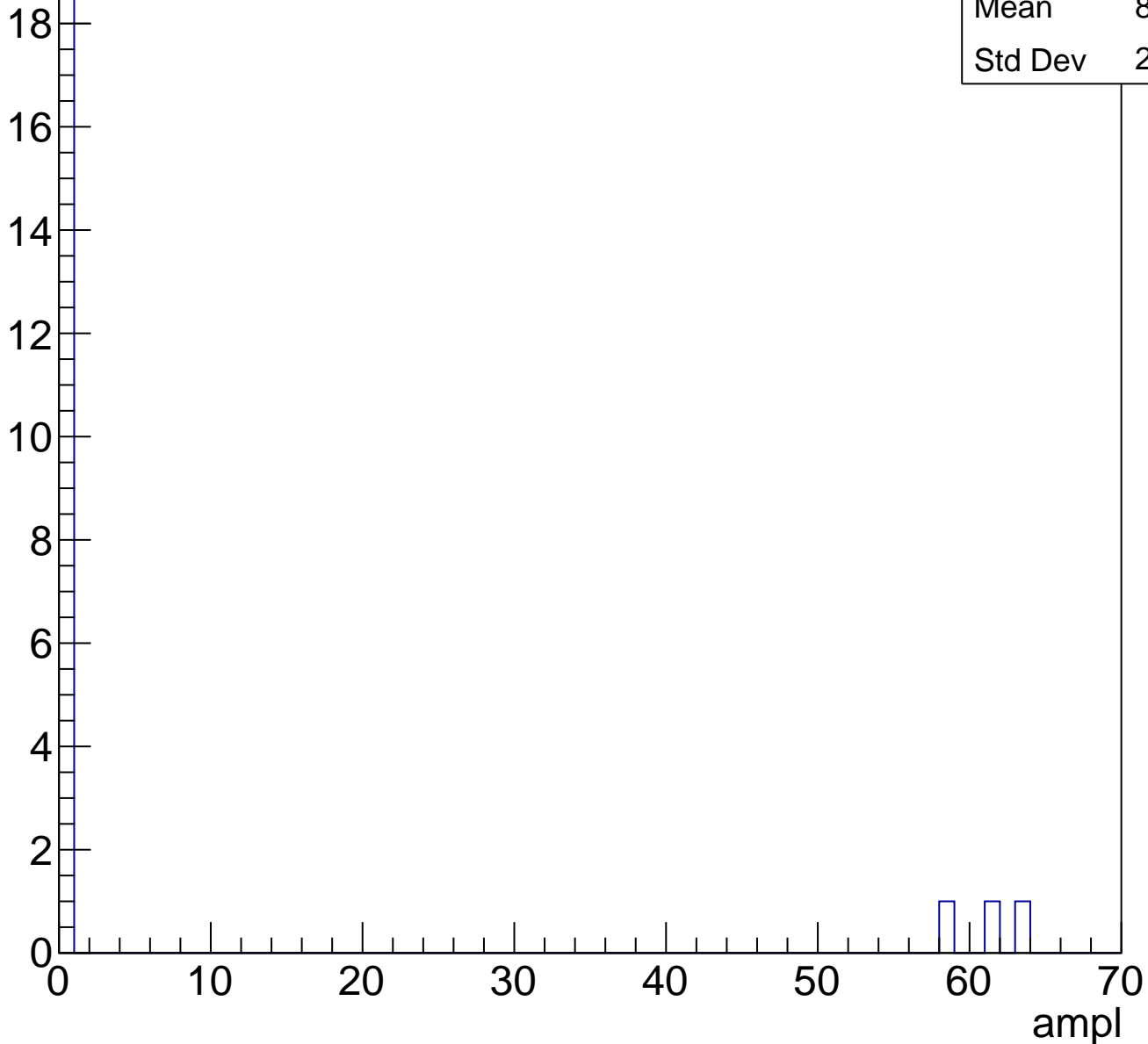


B1L103S, U2-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.273
Std Dev	20.83

Entry



B1L103S, U2-ch101, adc0

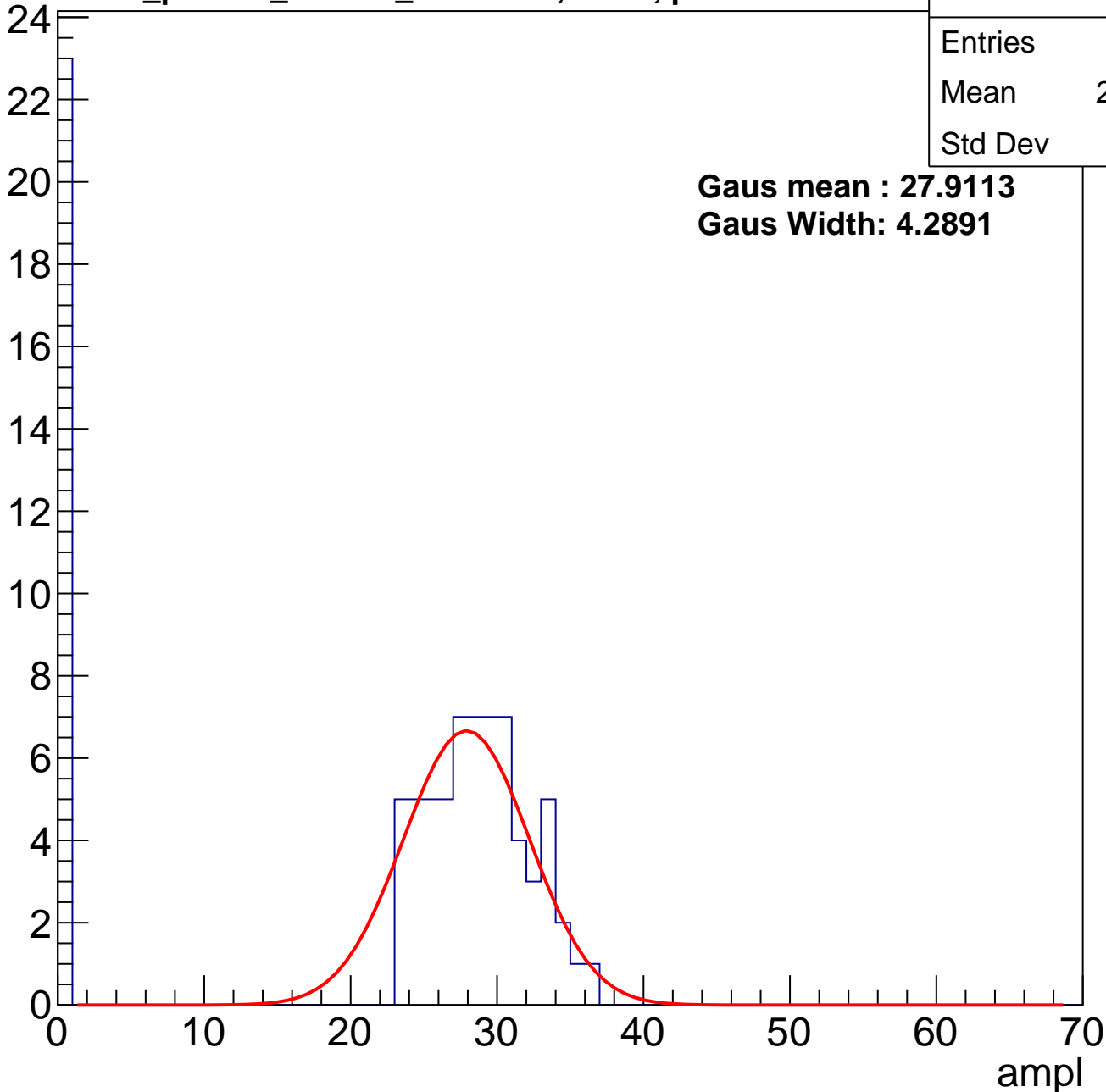
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	20.83
Std Dev	12.8

Gaus mean : 27.9113

Gaus Width: 4.2891

Entry



B1L103S, U2-ch101, adc1

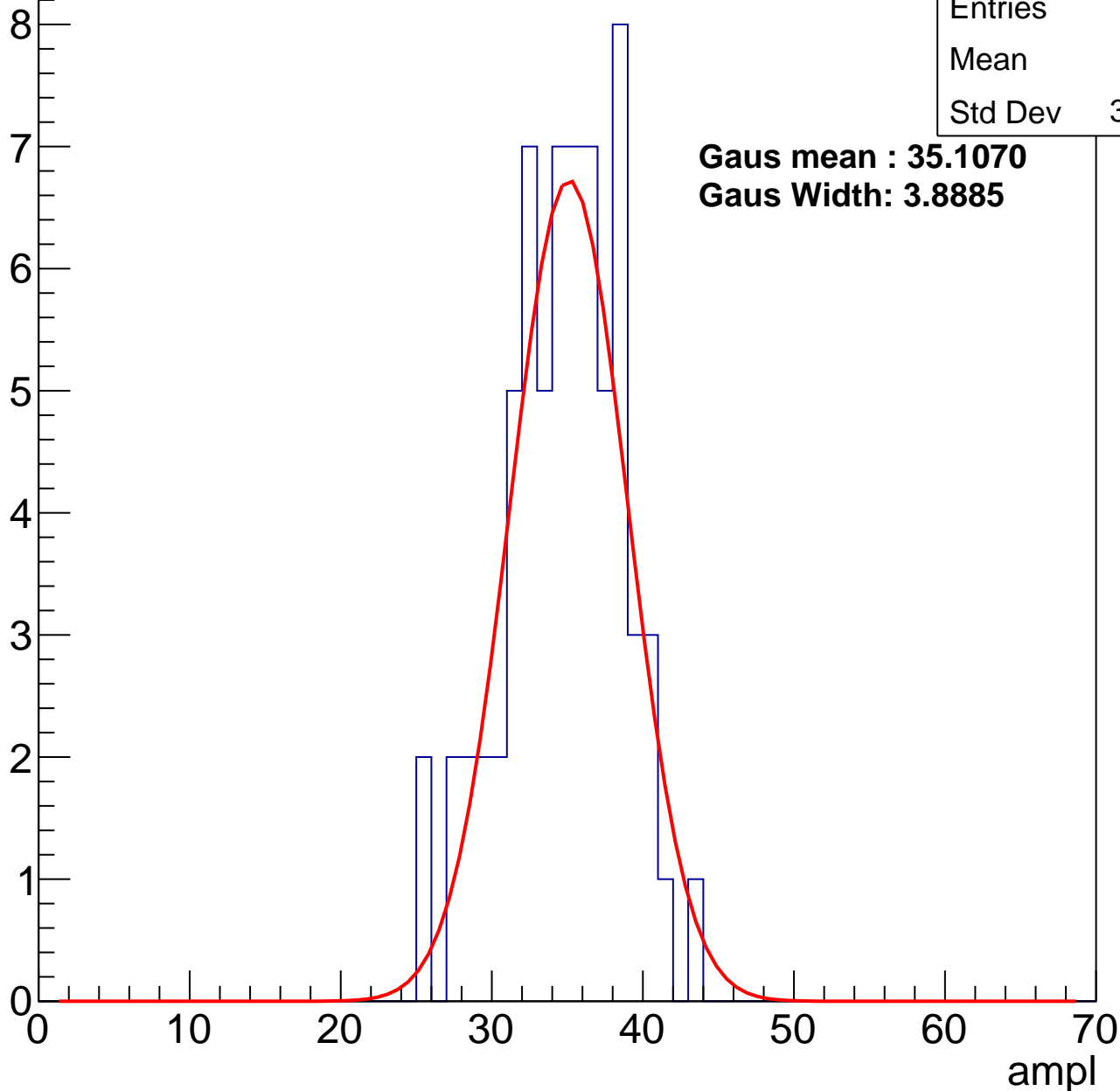
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	34.3
Std Dev	3.819

Gaus mean : 35.1070

Gaus Width: 3.8885



B1L103S, U2-ch101, adc2

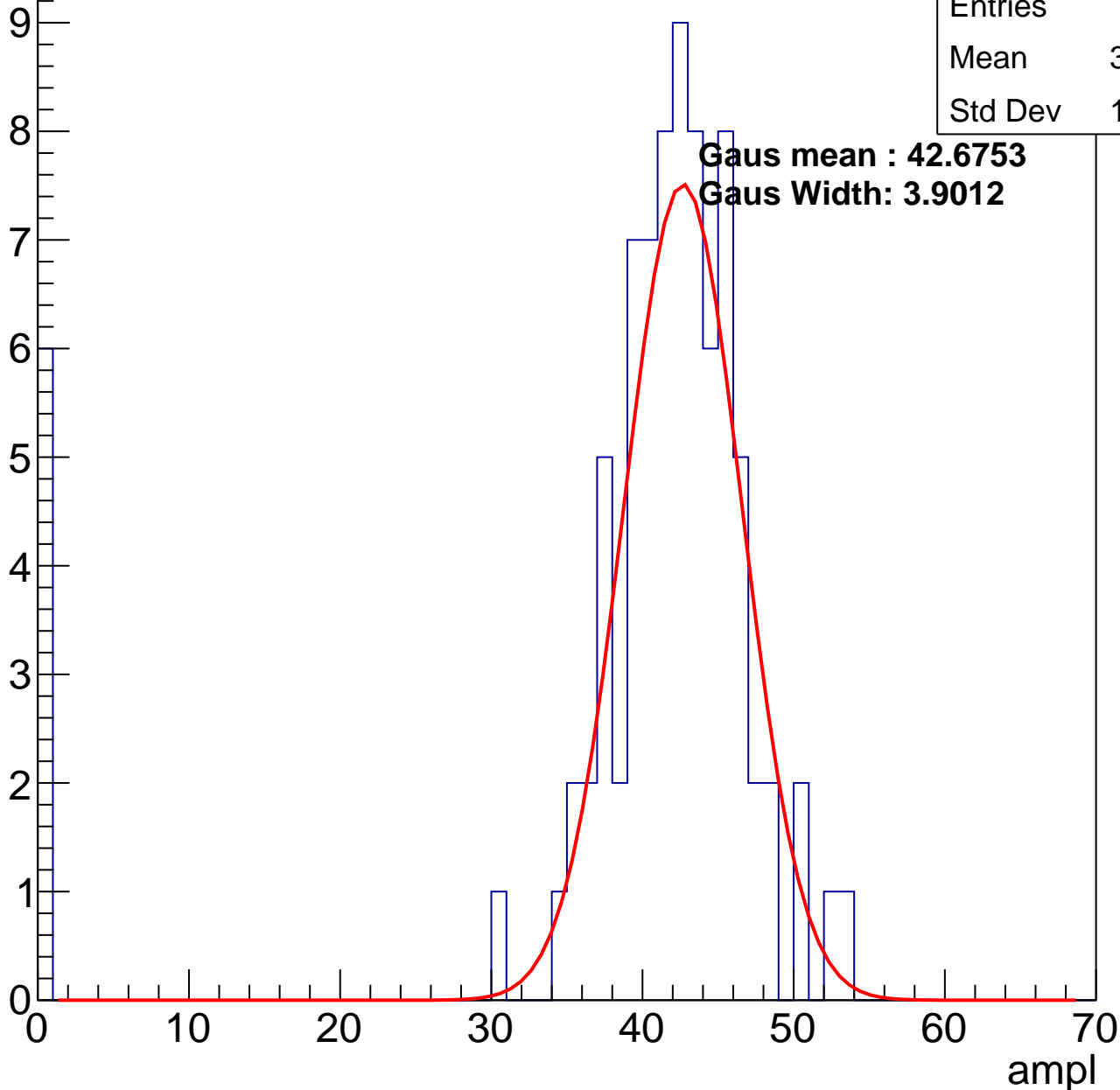
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	39.05
Std Dev	11.44

Gaus mean : 42.6753

Gaus Width: 3.9012



B1L103S, U2-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	40.23
Std Dev	18.58

Entry

10

8

6

4

2

0

0

10

20

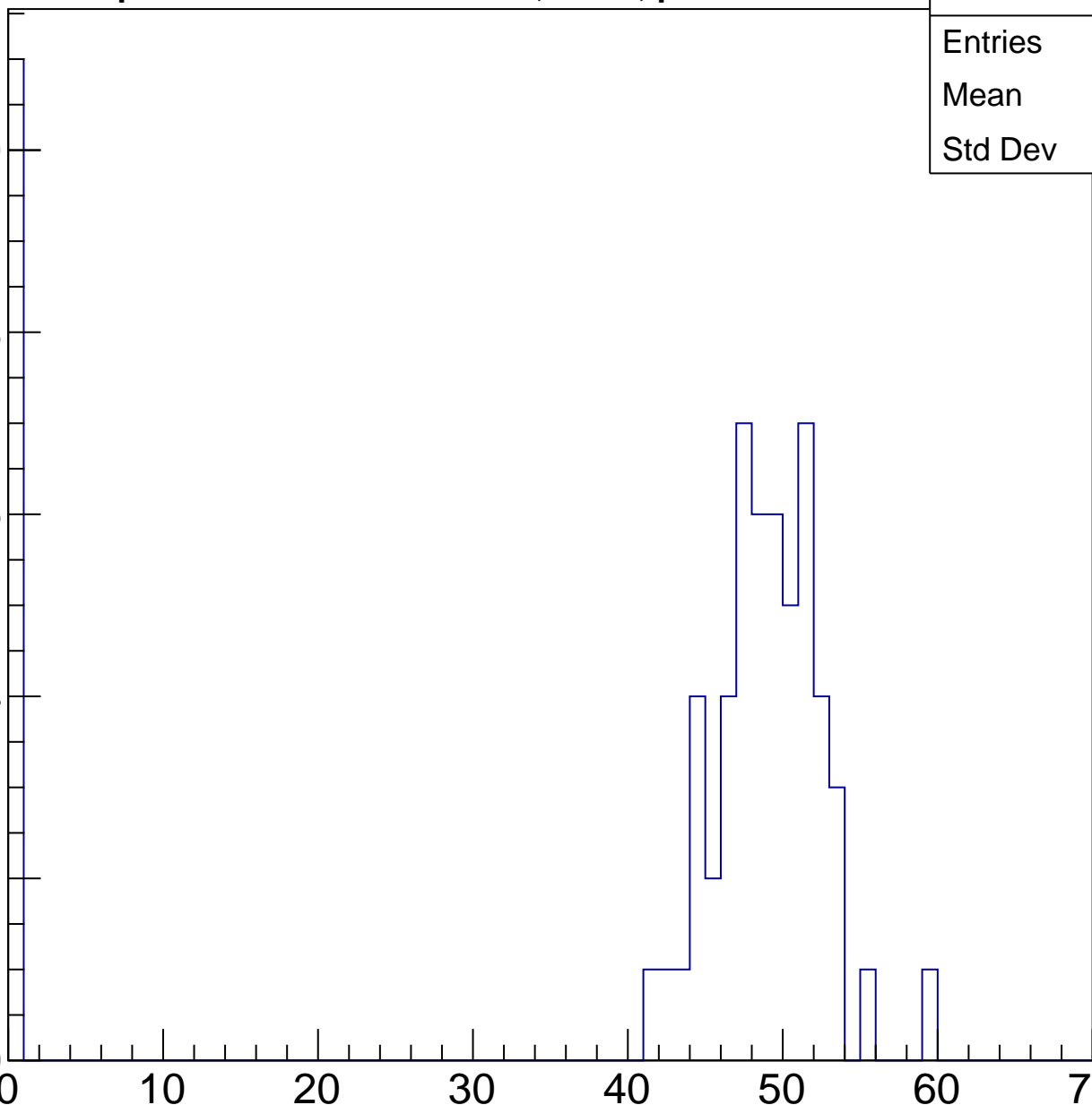
30

40

50

60

ampl

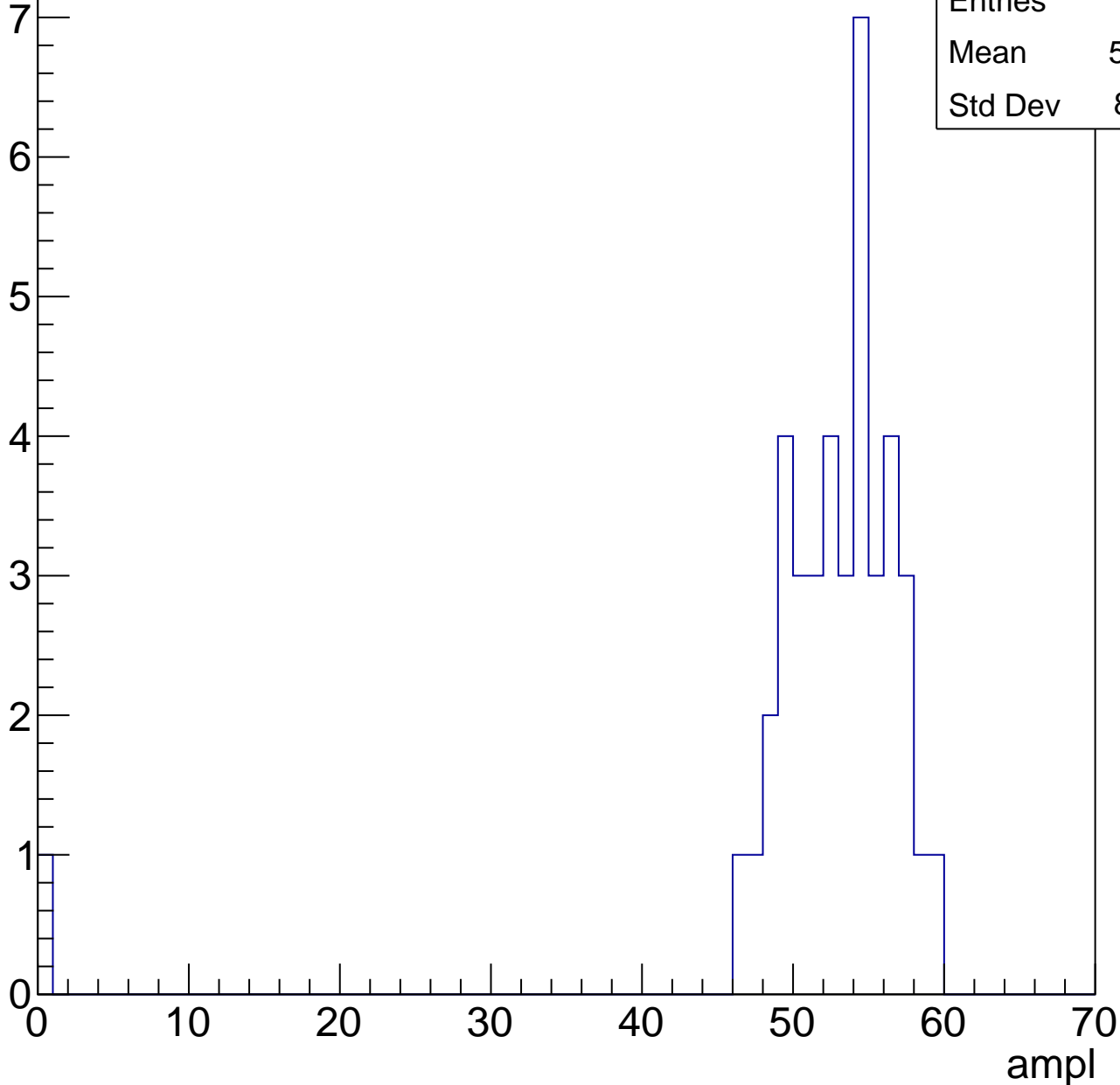


B1L103S, U2-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

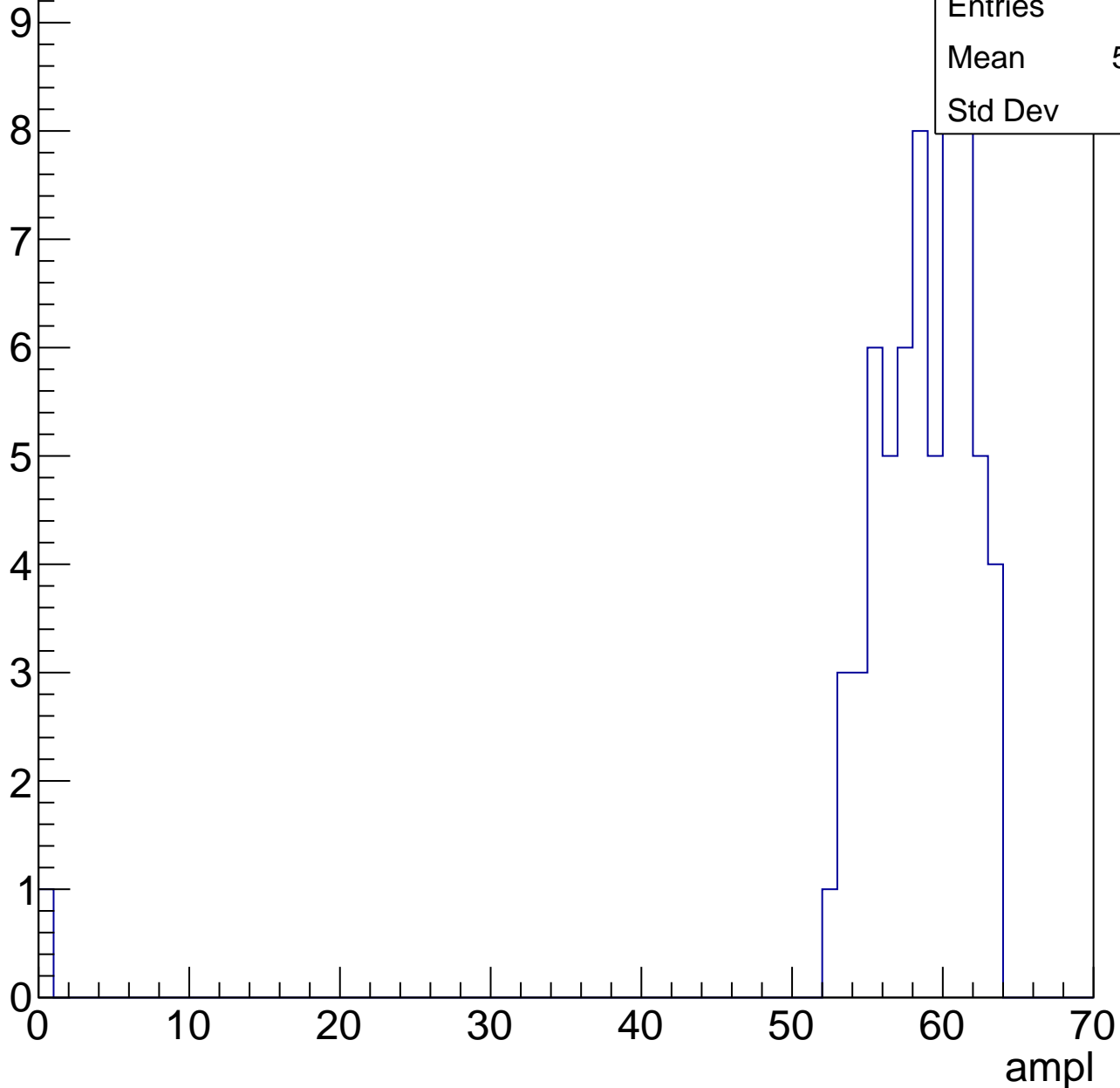
Entries	41
Mean	51.46
Std Dev	8.721



B1L103S, U2-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

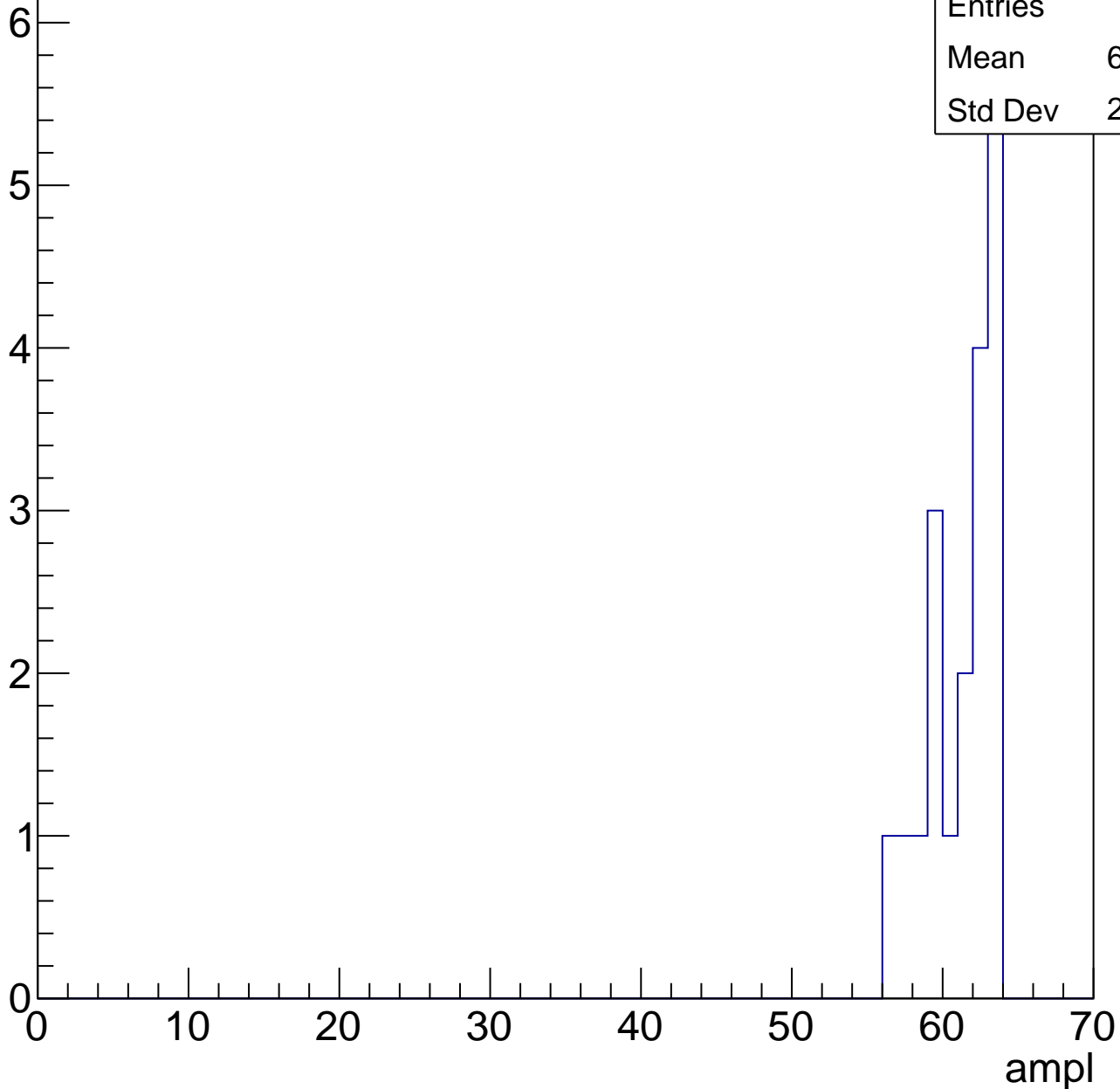


B1L103S, U2-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	60.84
Std Dev	2.183



B1L103S, U2-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U2-ch102, adc0

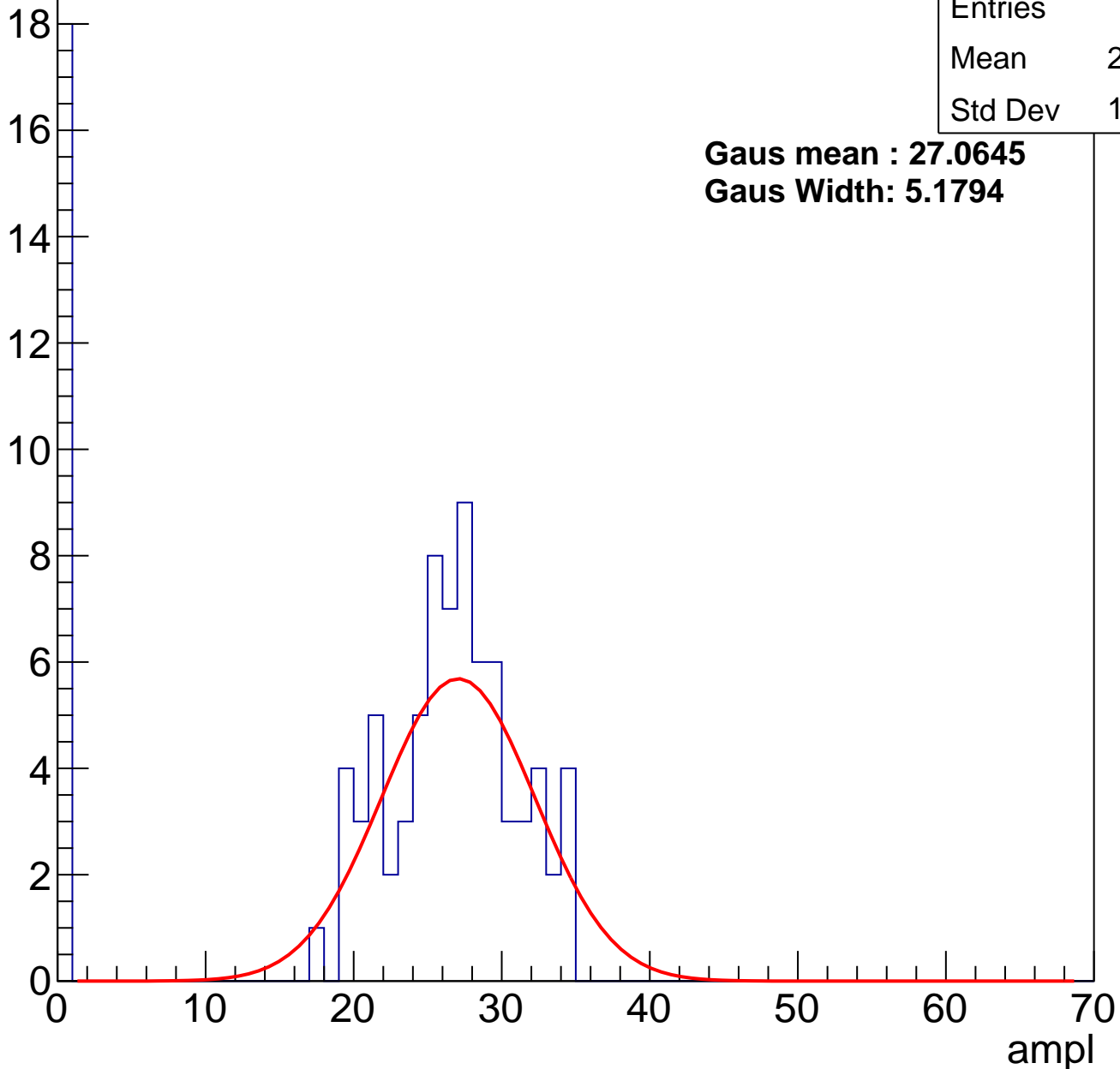
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	21.19
Std Dev	11.03

Gaus mean : 27.0645

Gaus Width: 5.1794

Entry



B1L103S, U2-ch102, adc1

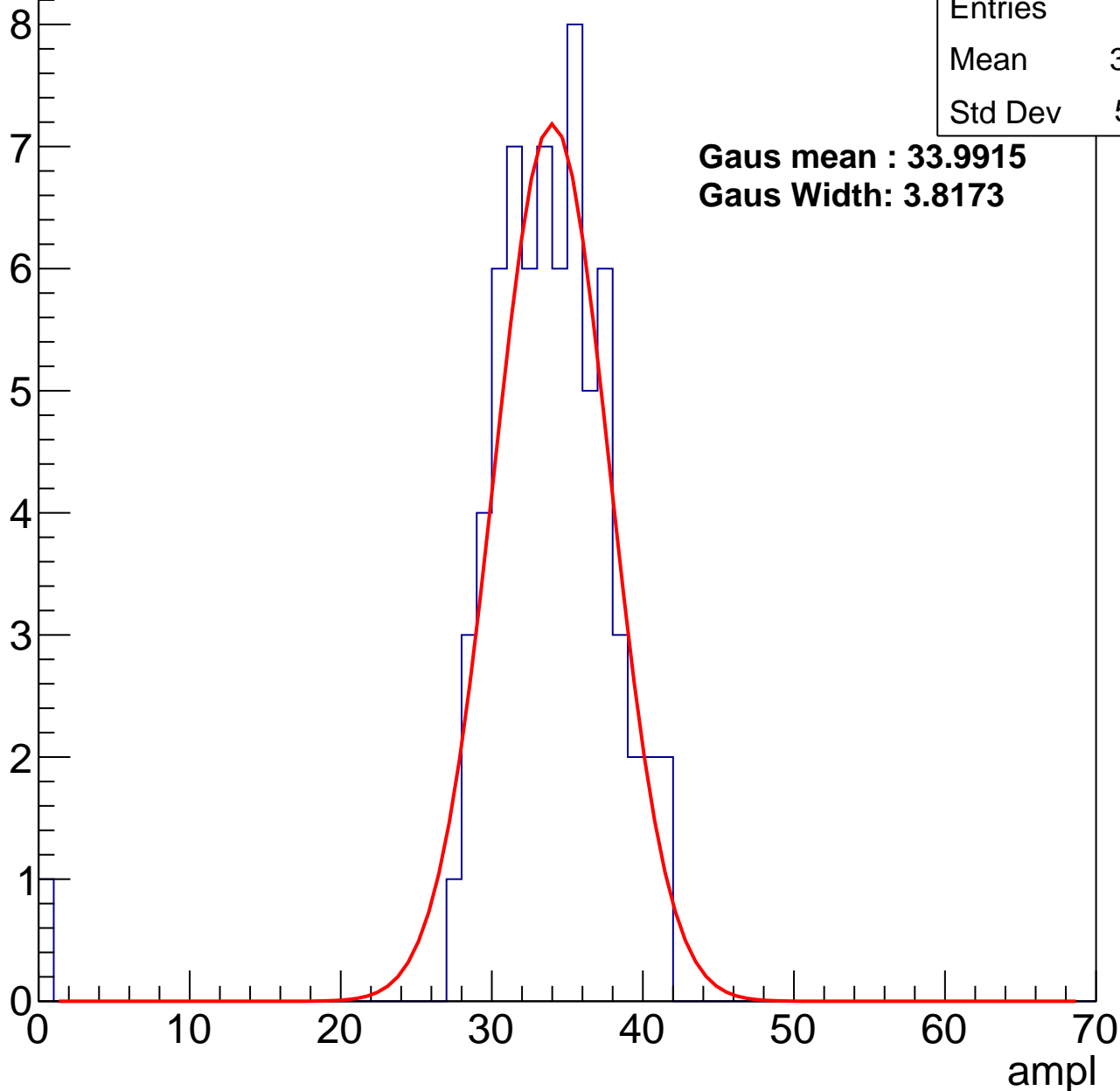
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	33.14
Std Dev	5.251

Gaus mean : 33.9915

Gaus Width: 3.8173



B1L103S, U2-ch102, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	30.87
Std Dev	16.83

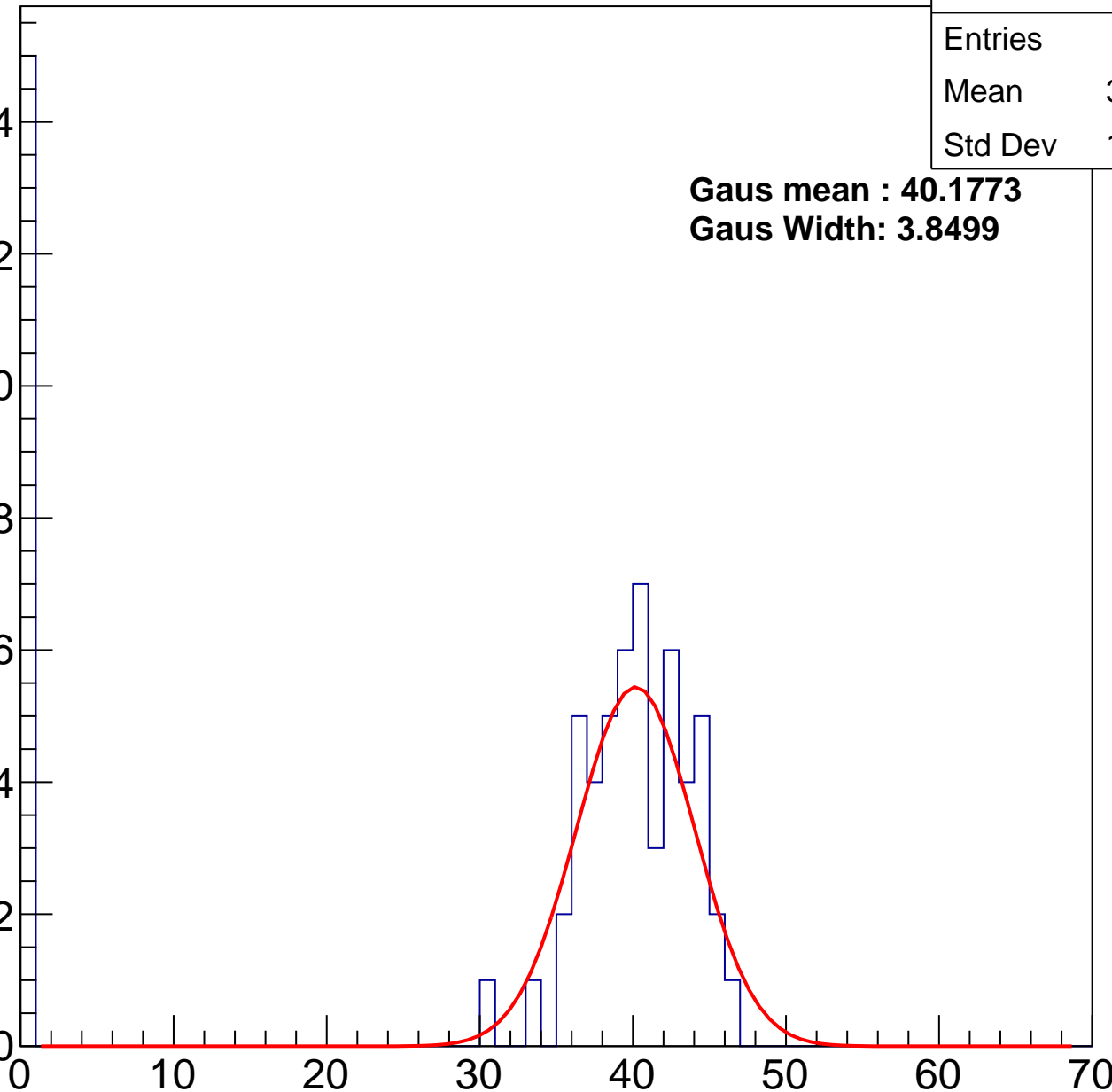
Gaus mean : 40.1773

Gaus Width: 3.8499

Entry

14
12
10
8
6
4
2
0

ampl

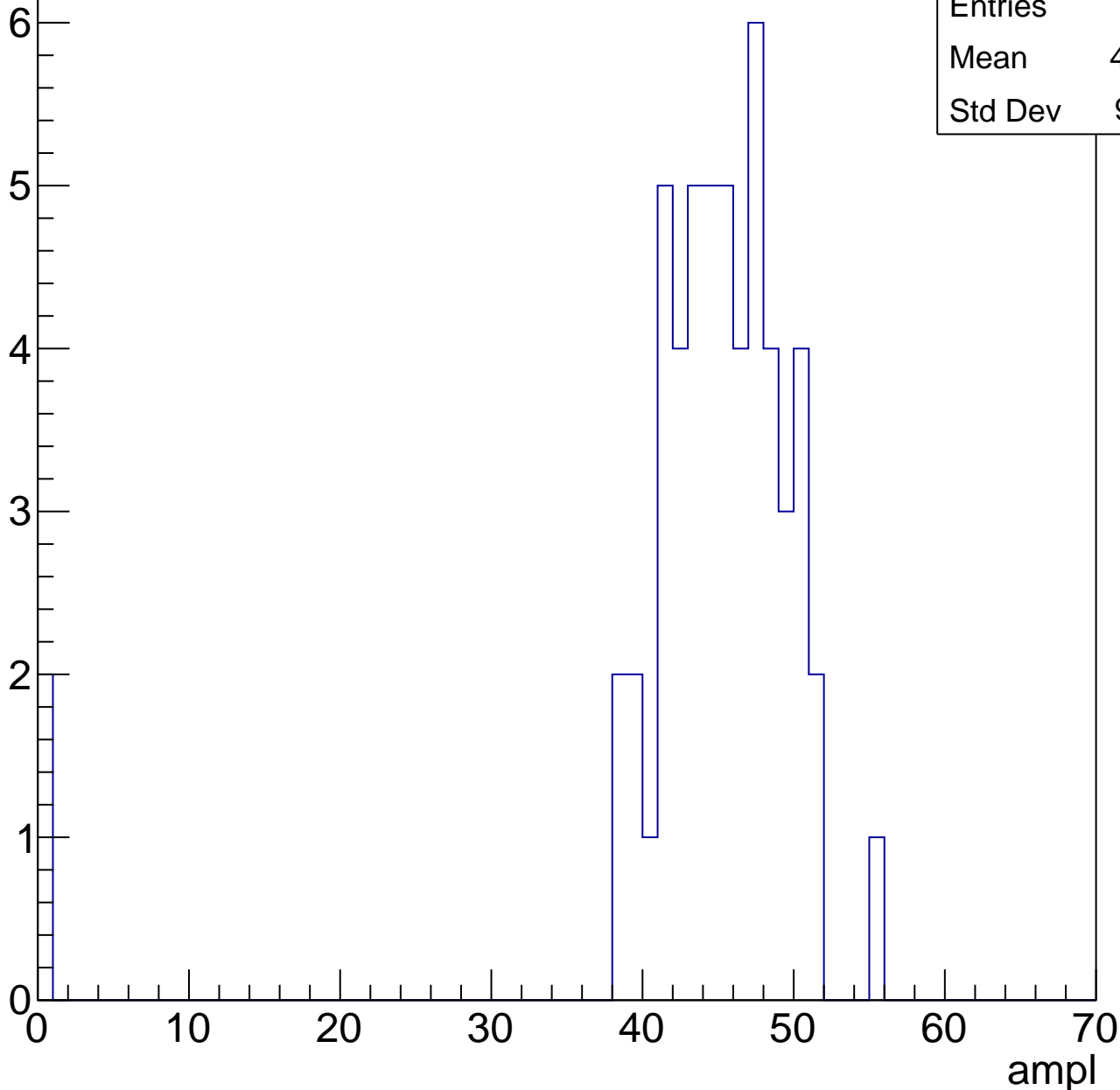


B1L103S, U2-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	43.44
Std Dev	9.181

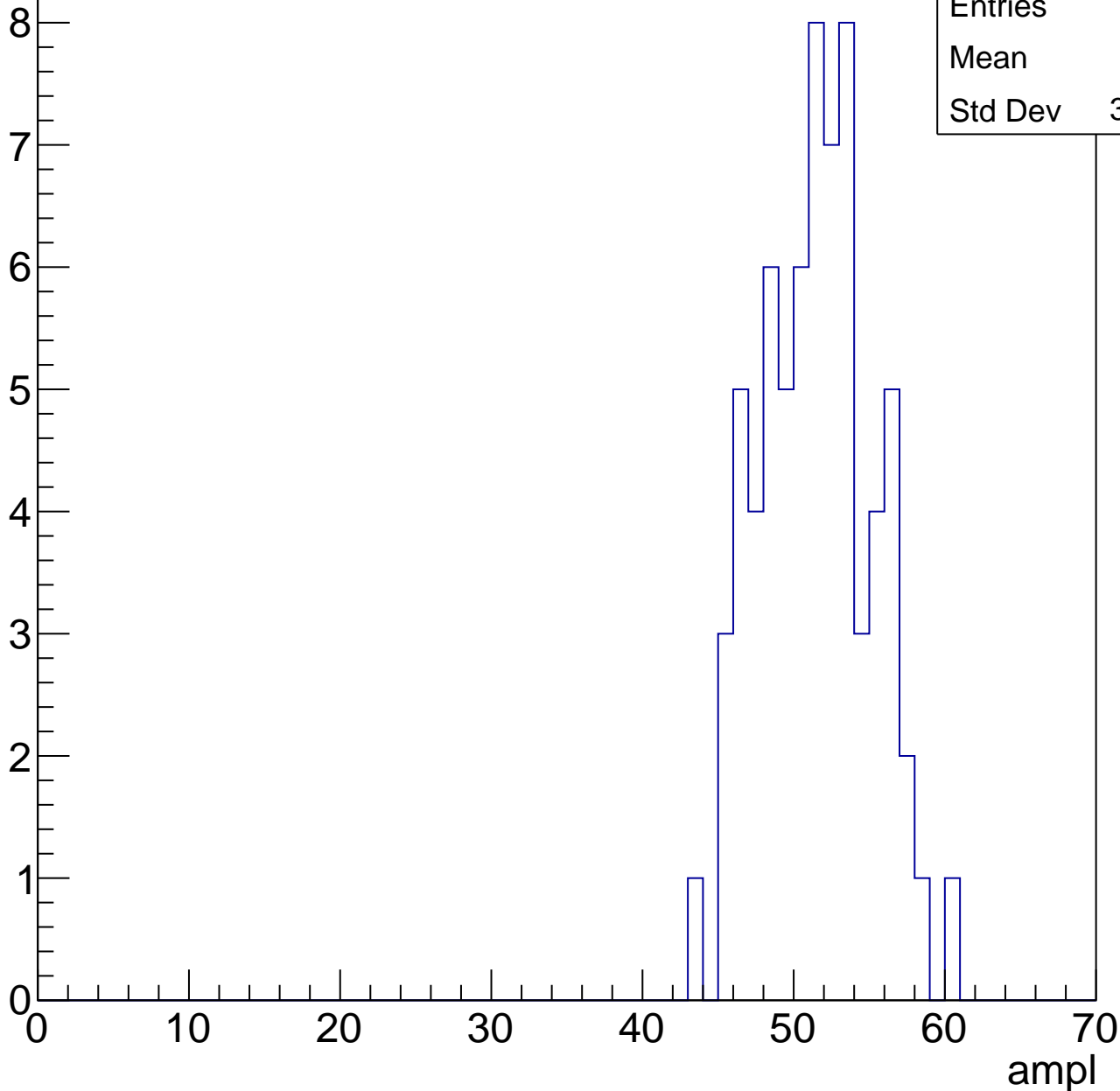


B1L103S, U2-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

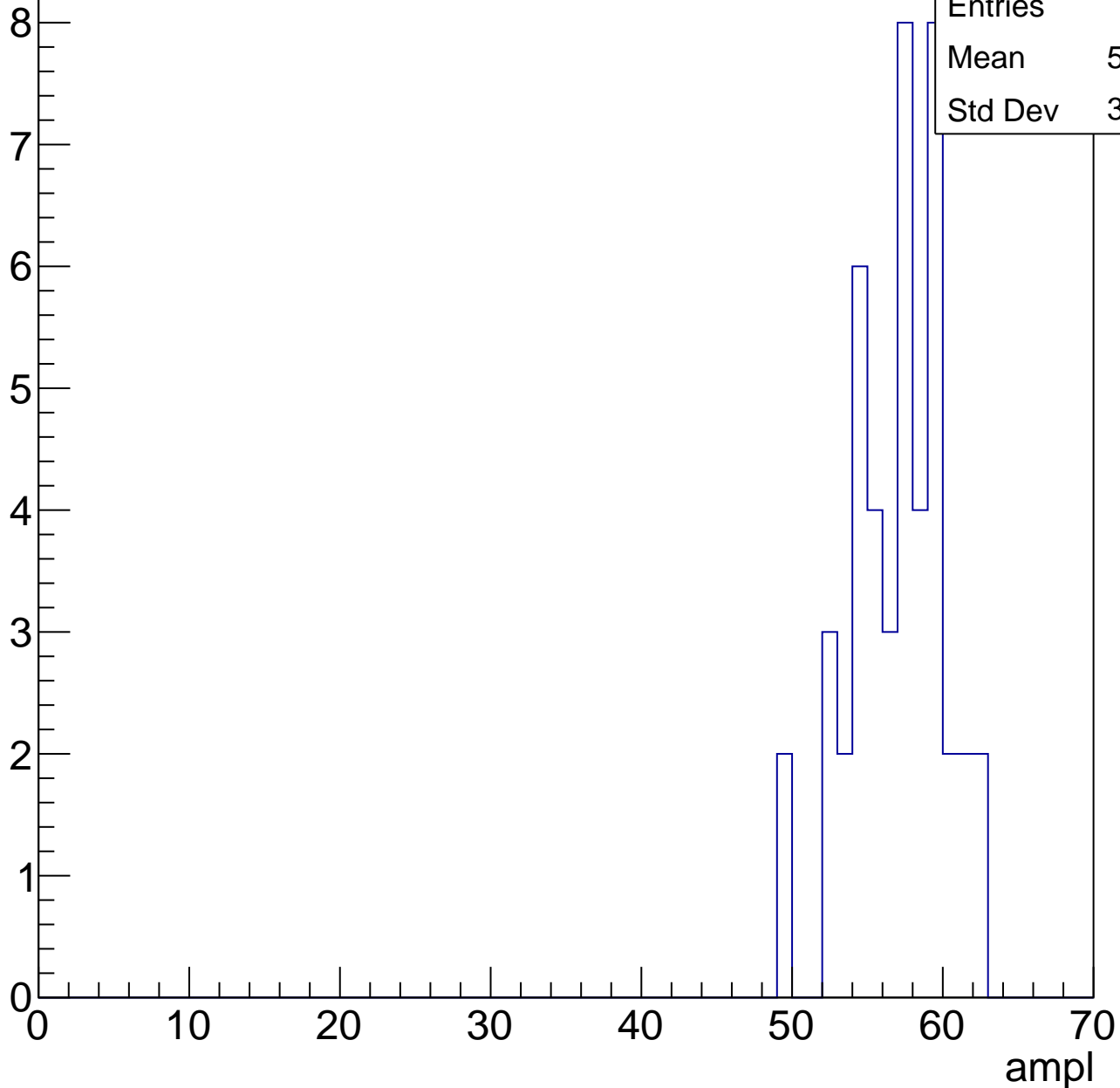
Entries	69
Mean	51
Std Dev	3.608



B1L103S, U2-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



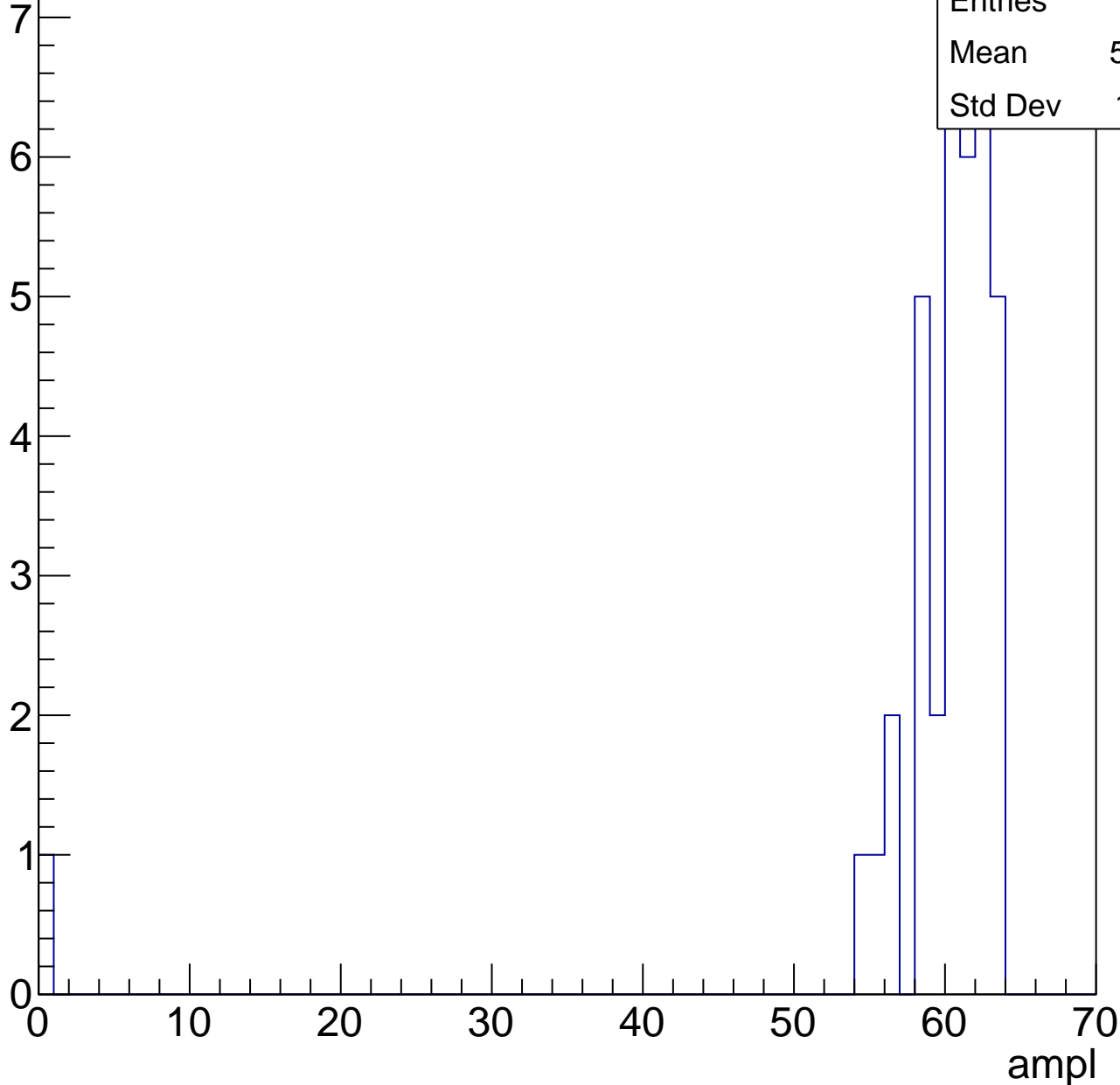
Entries	46
Mean	56.48
Std Dev	3.063

B1L103S, U2-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	58.49
Std Dev	10.01

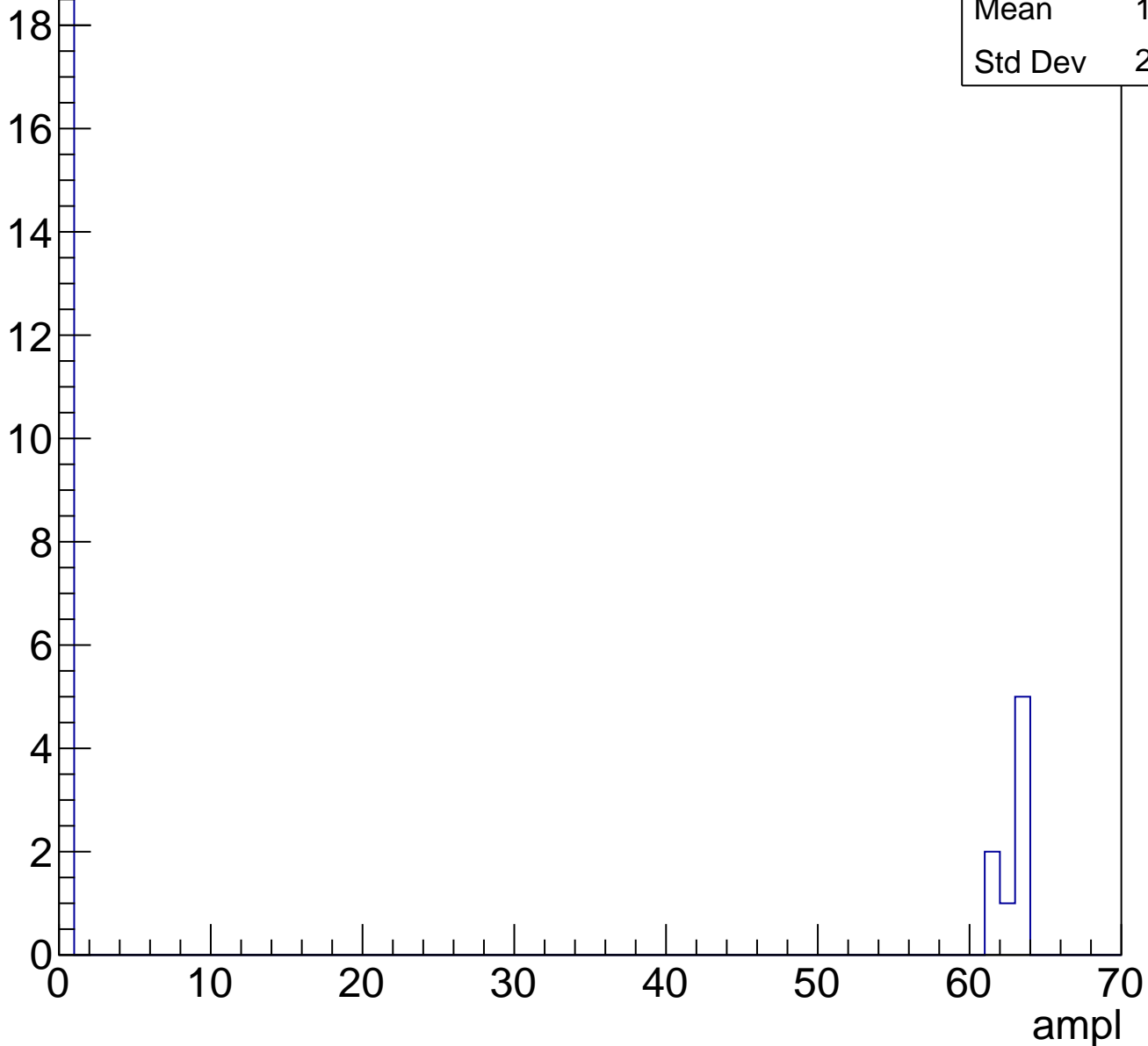


B1L103S, U2-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	27
Mean	18.48
Std Dev	28.49

Entry



B1L103S, U2-ch103, adc0

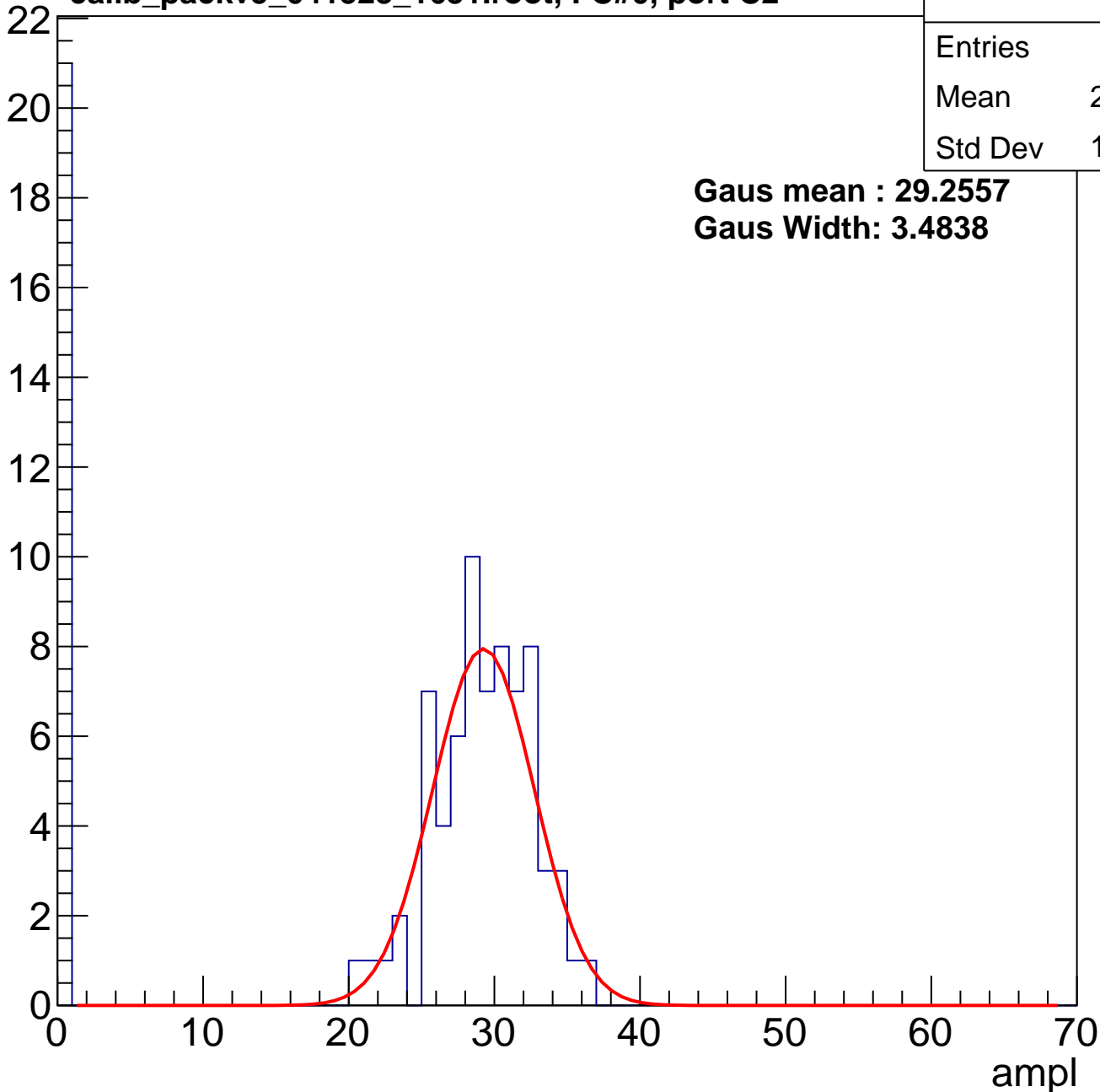
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	22.18
Std Dev	12.49

Gaus mean : 29.2557

Gaus Width: 3.4838

Entry



B1L103S, U2-ch103, adc1

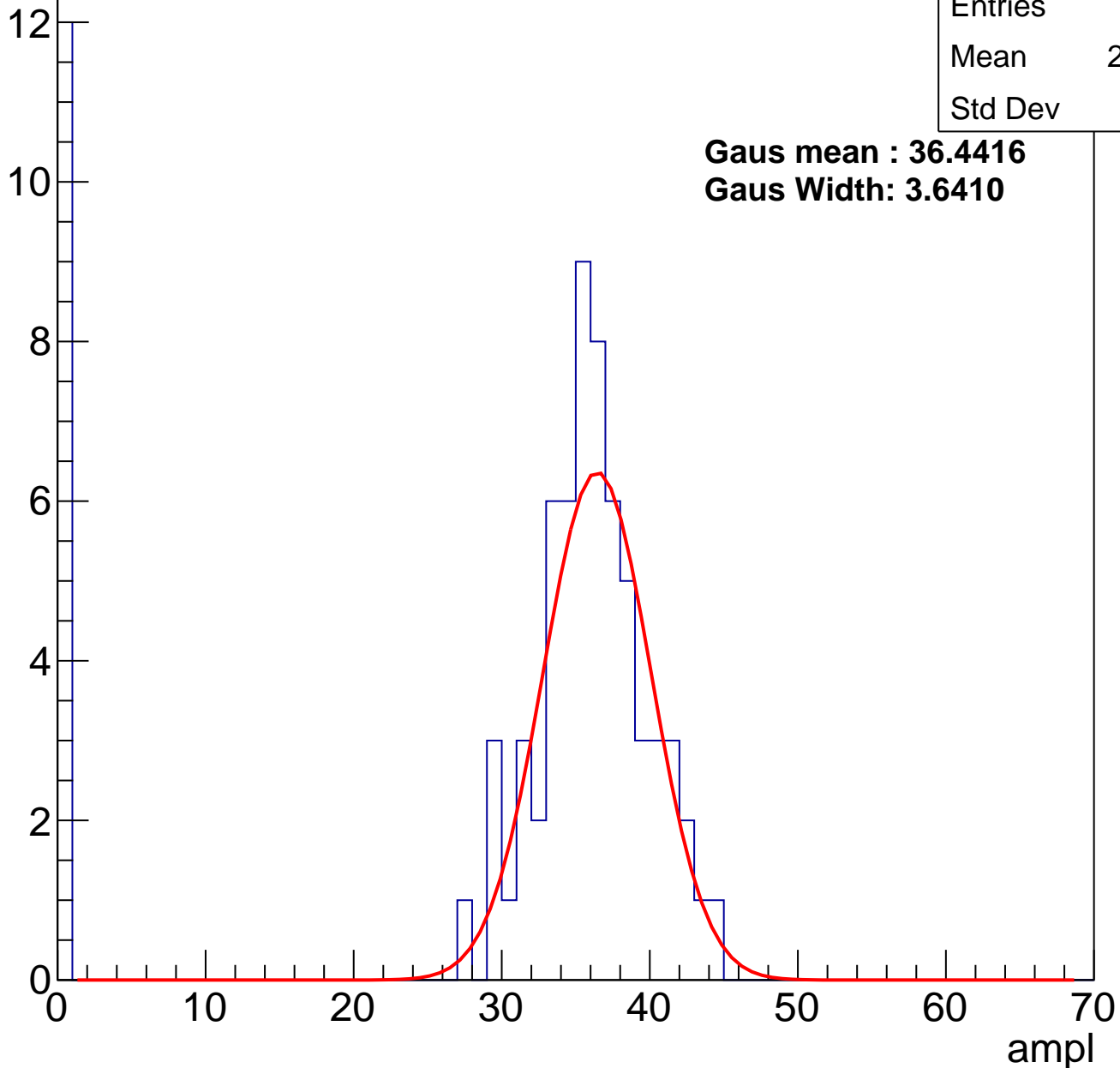
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	29.99
Std Dev	13.5

Gaus mean : 36.4416

Gaus Width: 3.6410

Entry



B1L103S, U2-ch103, adc2

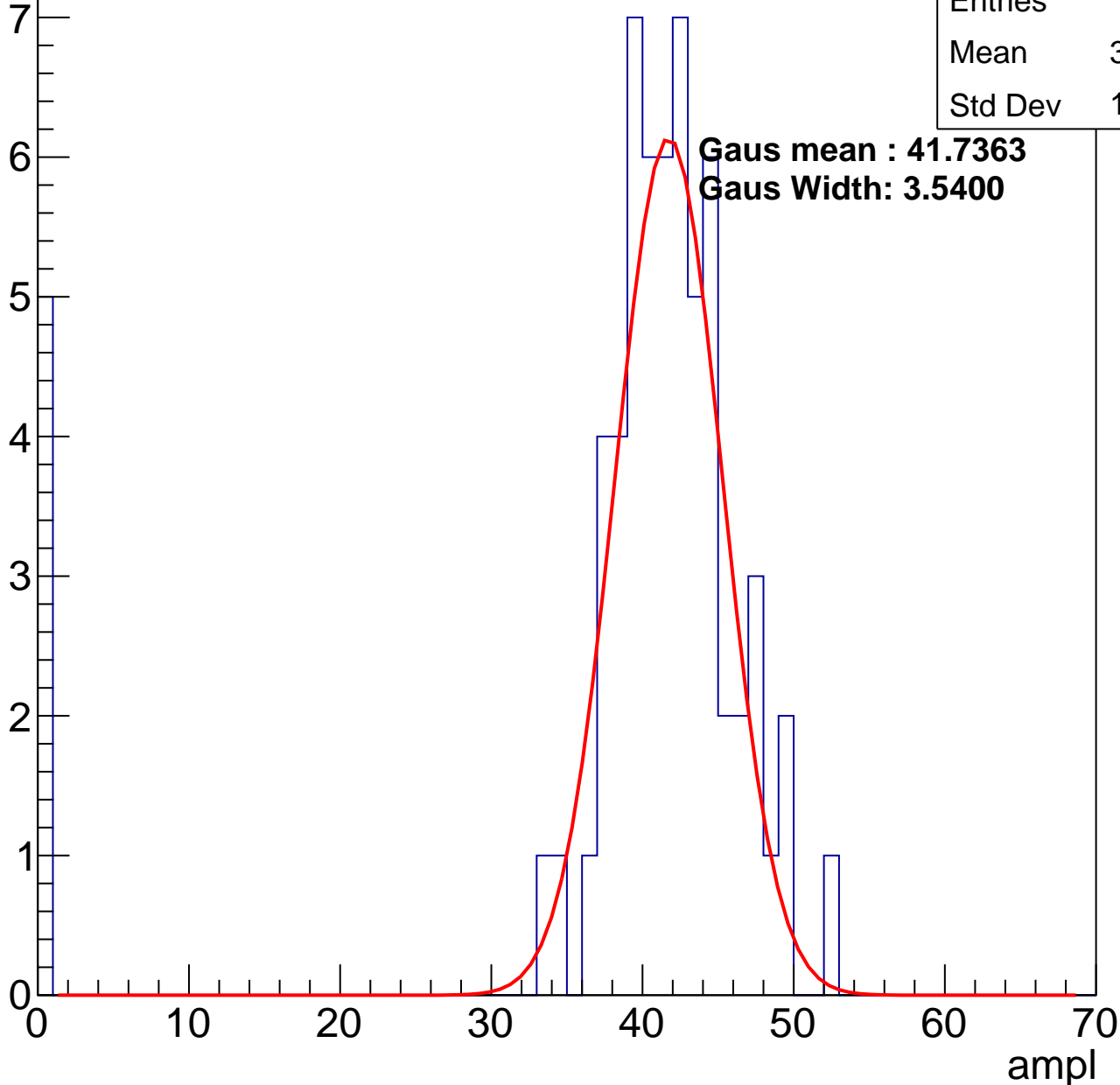
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	38.38
Std Dev	11.73

Gaus mean : 41.7363

Gaus Width: 3.5400

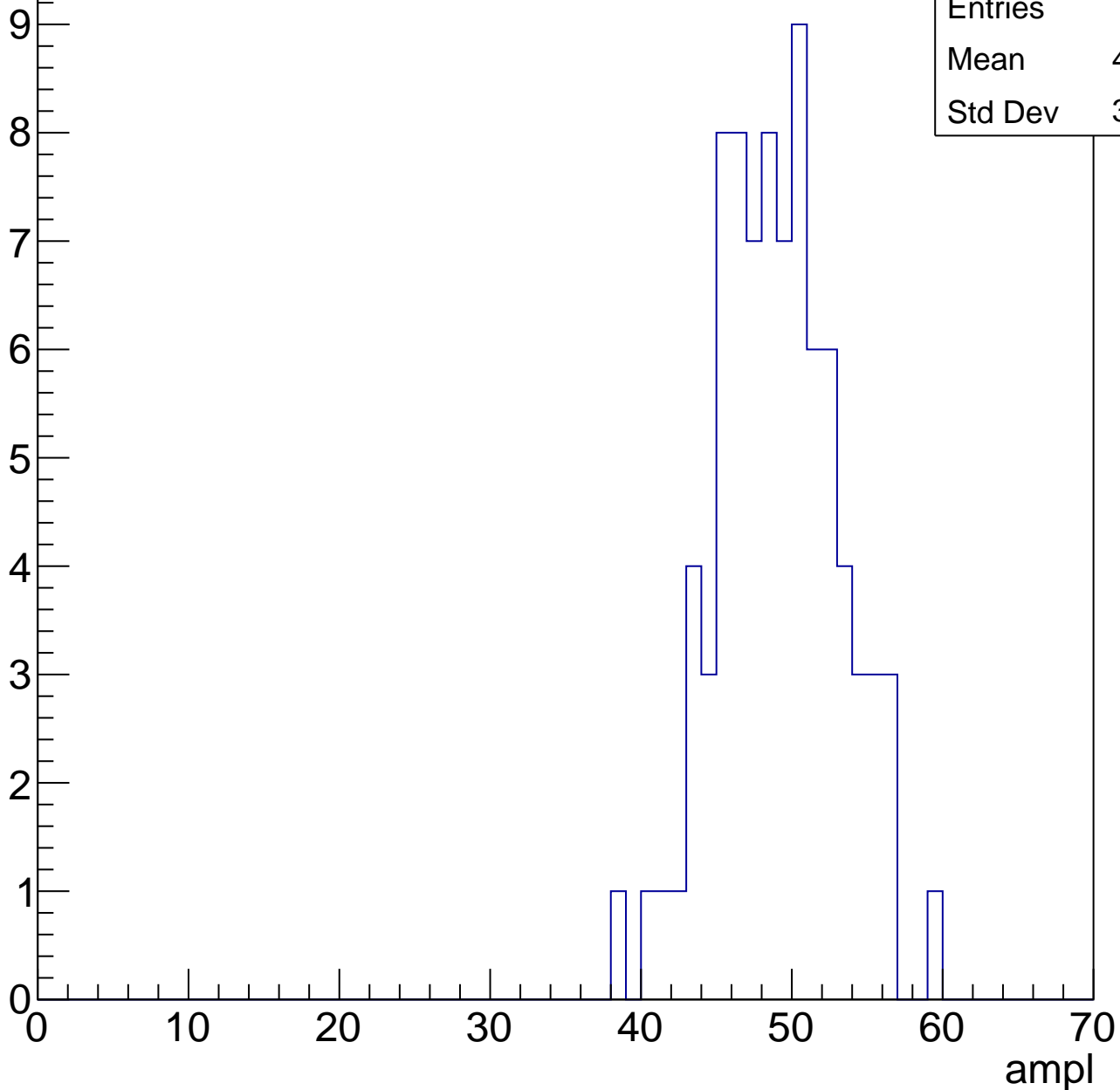


B1L103S, U2-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	48.61
Std Dev	3.991

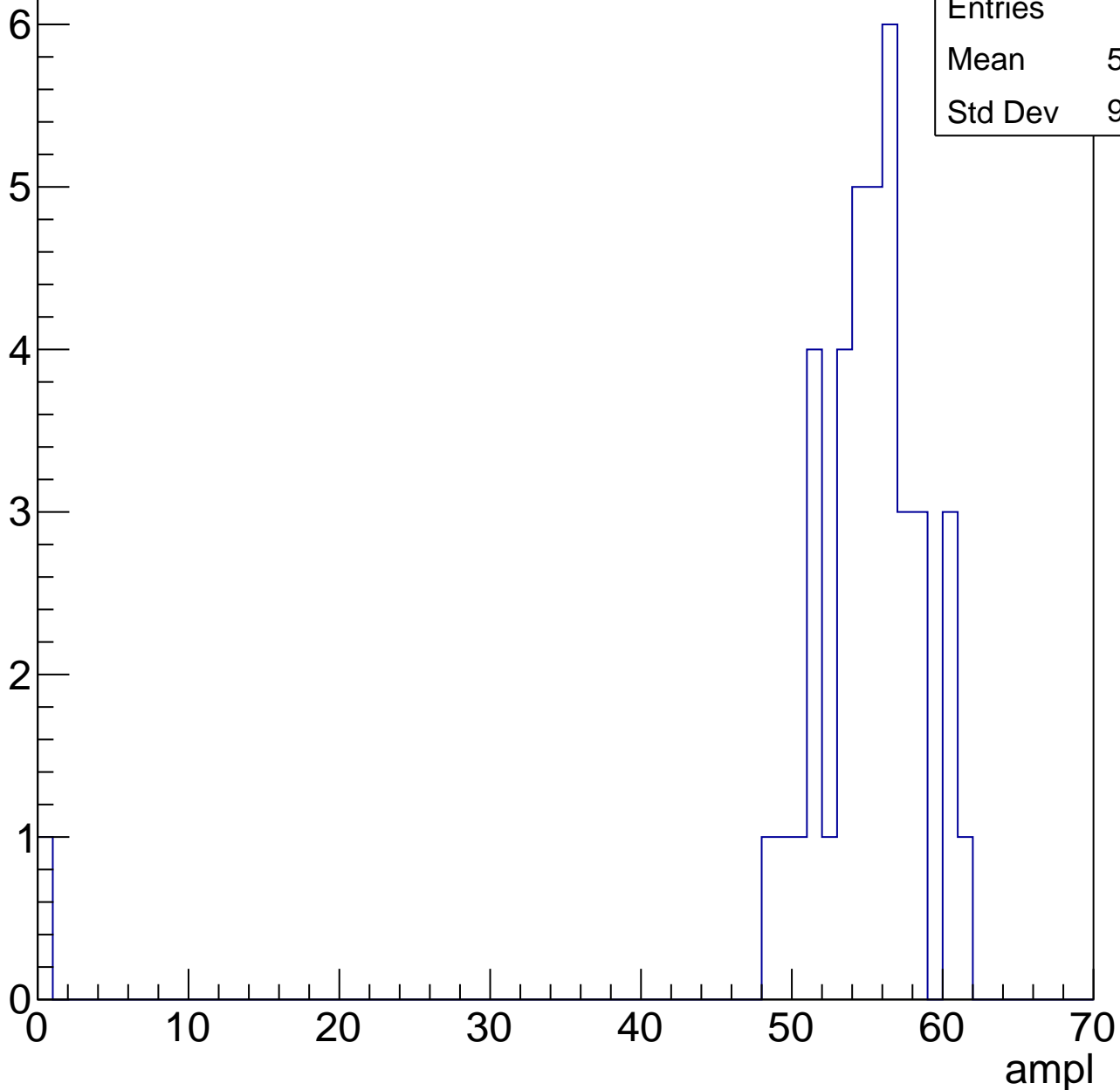


B1L103S, U2-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

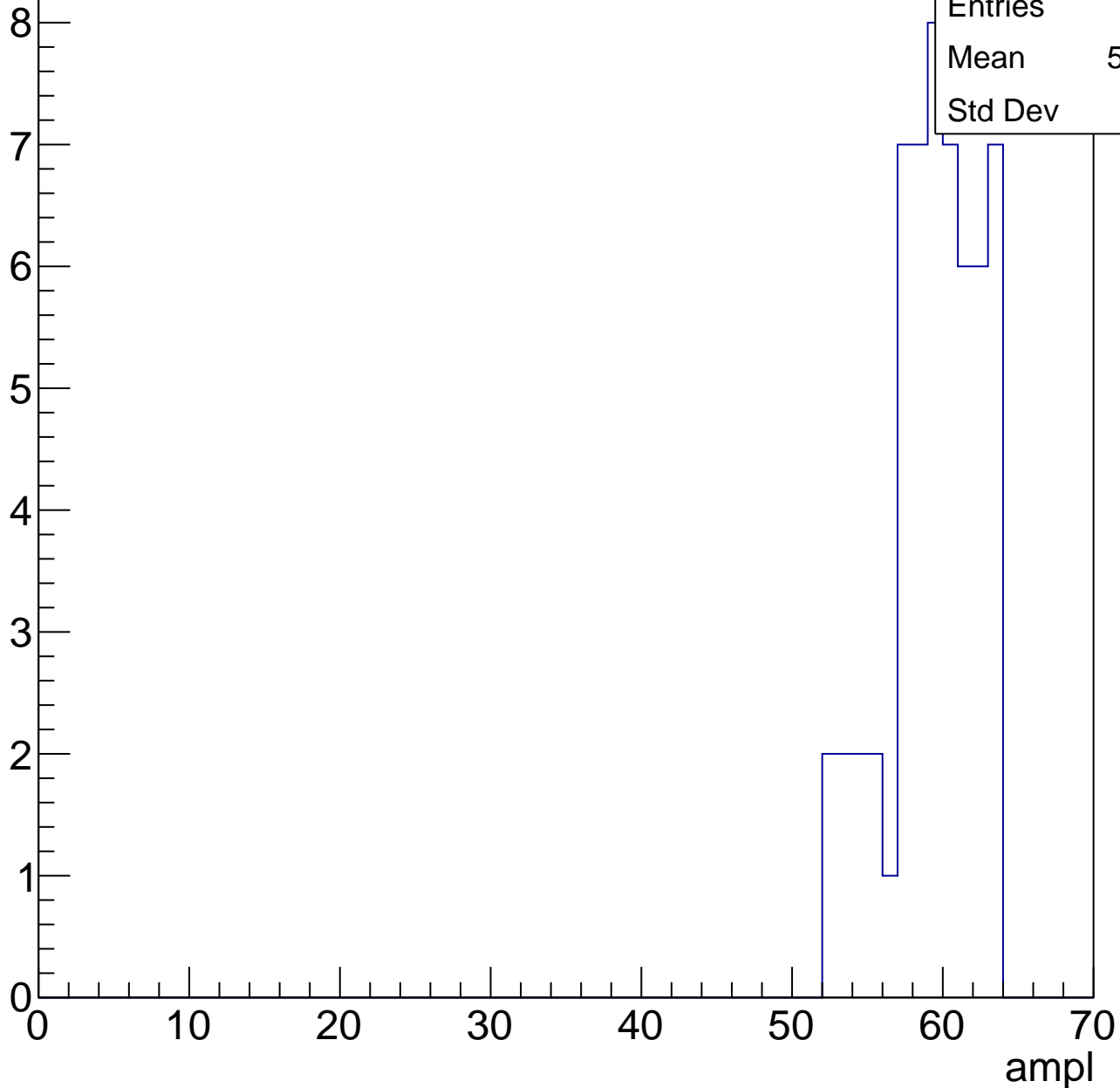
Entries	39
Mean	53.38
Std Dev	9.175



B1L103S, U2-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

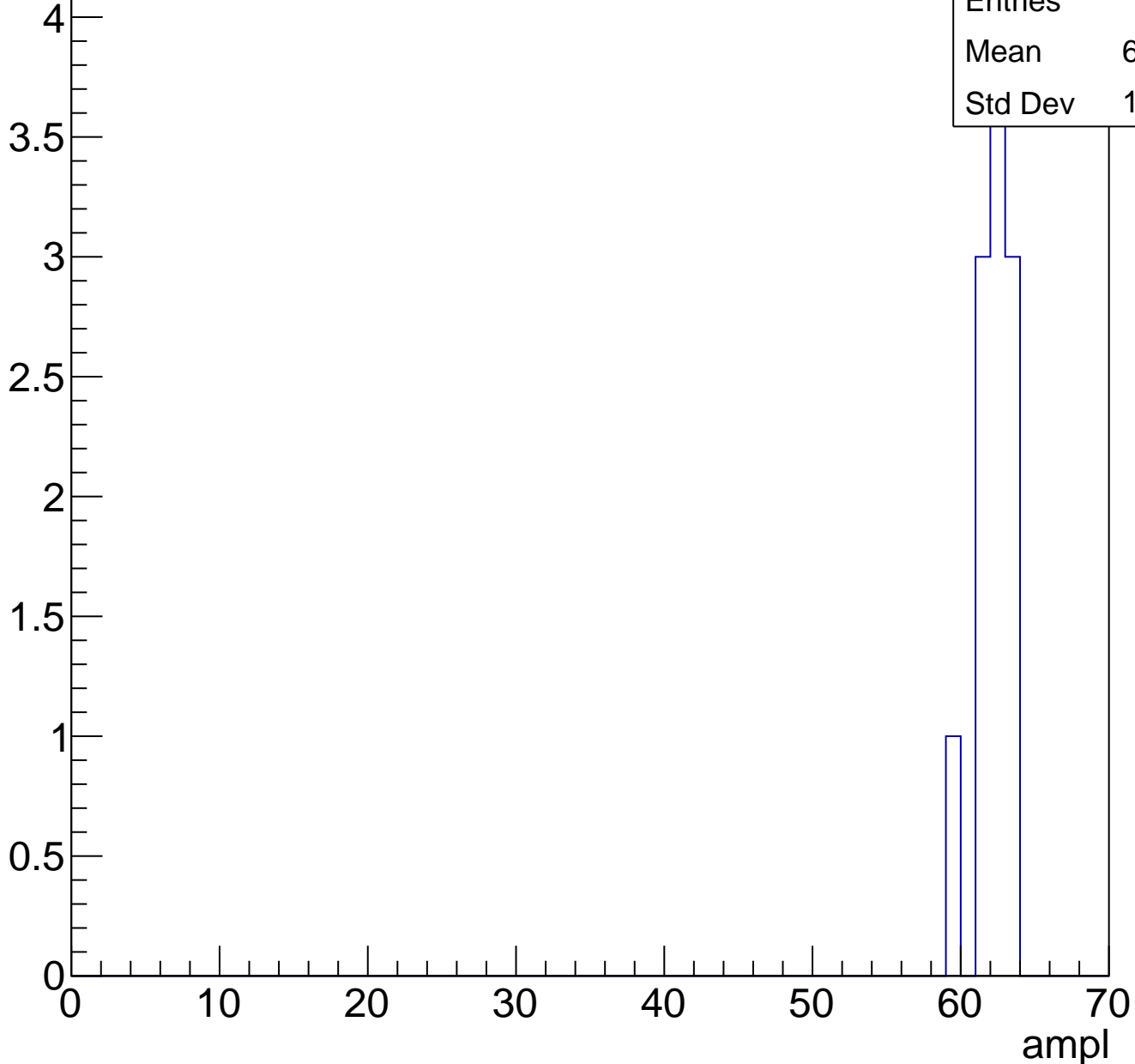
Entry



B1L103S, U2-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L103S, U2-ch104, adc0

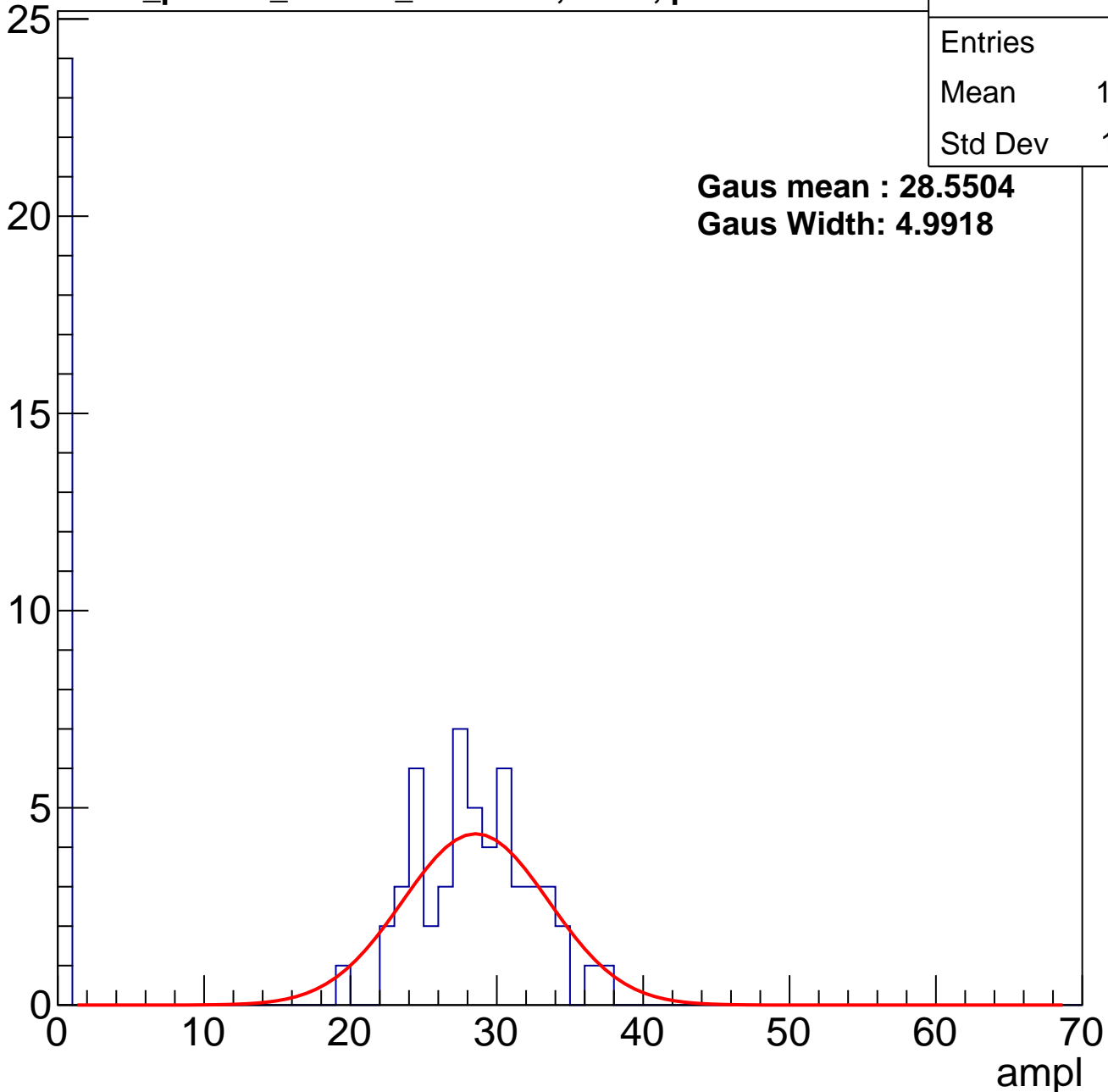
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	19.18
Std Dev	13.41

Gaus mean : 28.5504

Gaus Width: 4.9918

Entry



B1L103S, U2-ch104, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	29.64
Std Dev	12.6

Gaus mean : 35.1416

Gaus Width: 4.4525

Entry

12

10

8

6

4

2

0

0

10

20

30

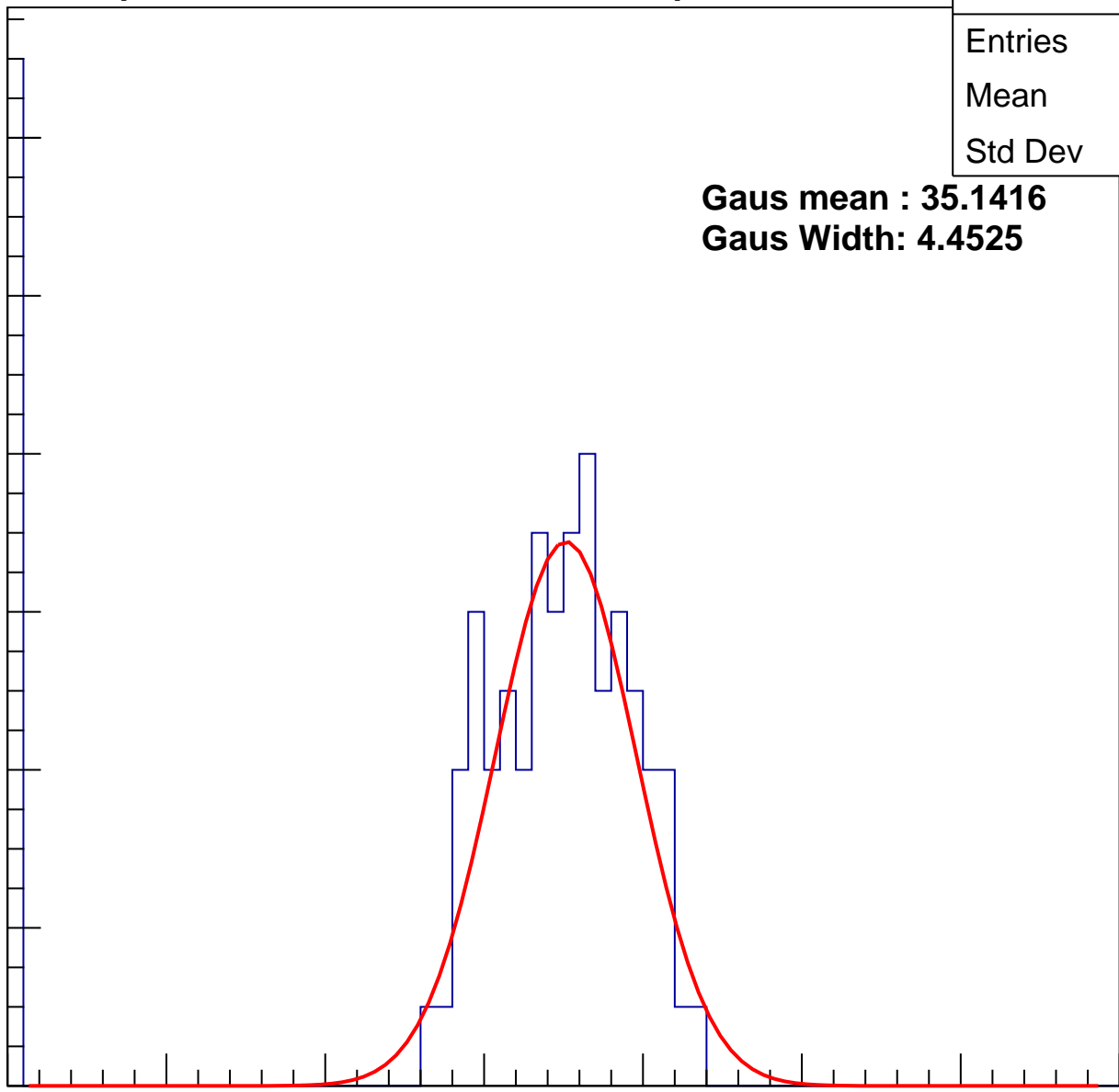
40

50

60

70

ampl



B1L103S, U2-ch104, adc2

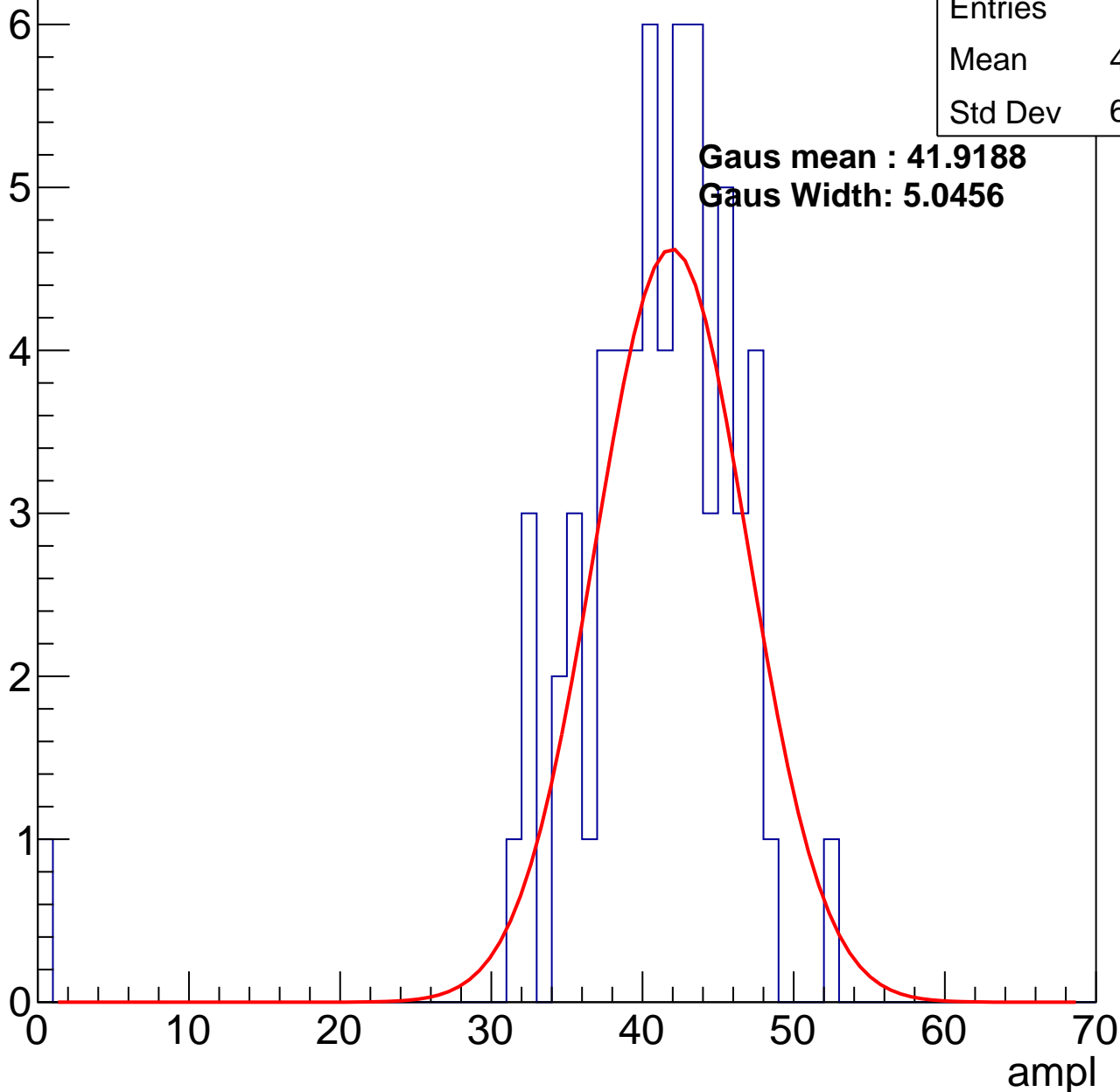
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	40.15
Std Dev	6.789

Gaus mean : 41.9188

Gaus Width: 5.0456

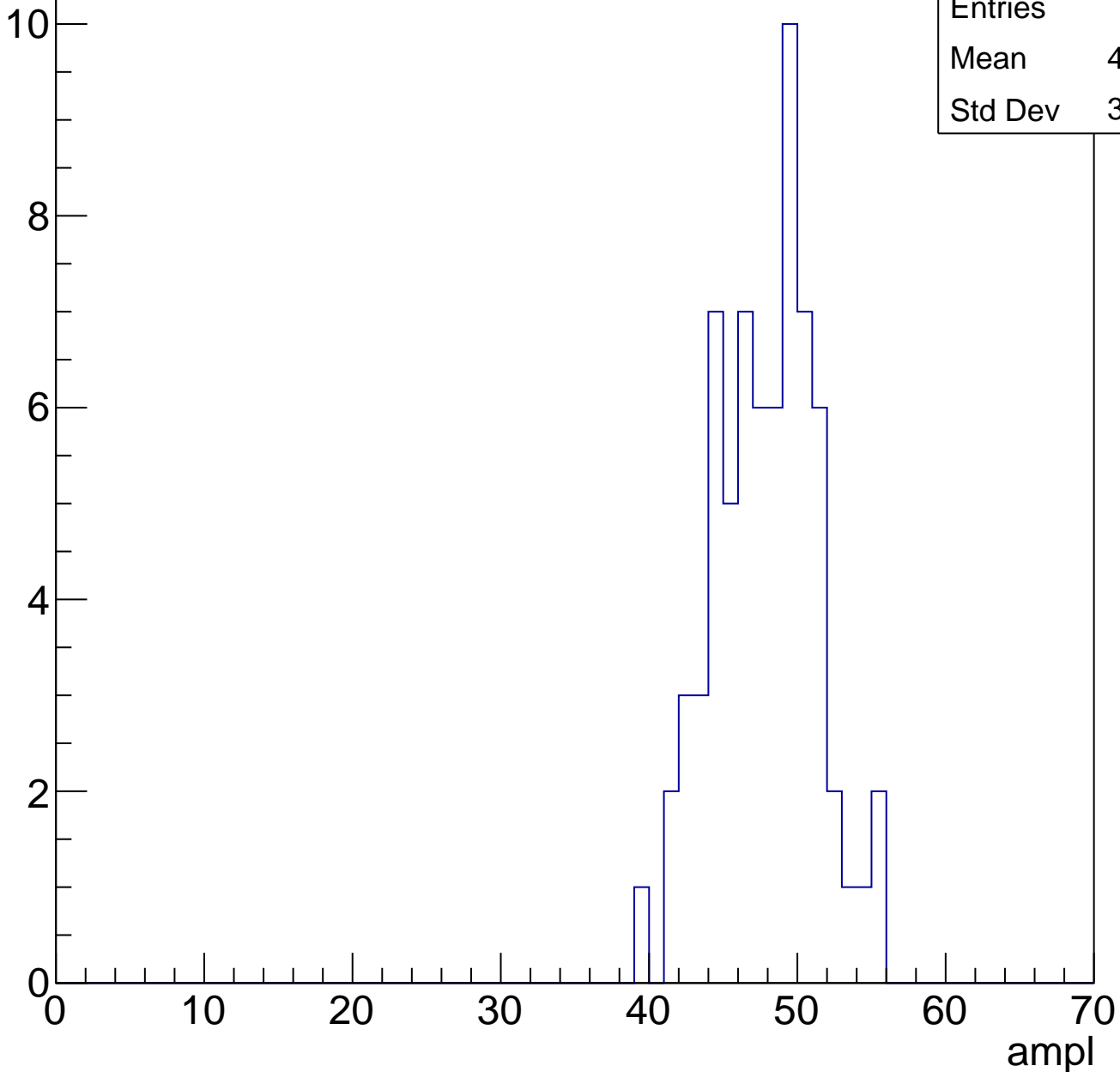


B1L103S, U2-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	47.36
Std Dev	3.422

Entry

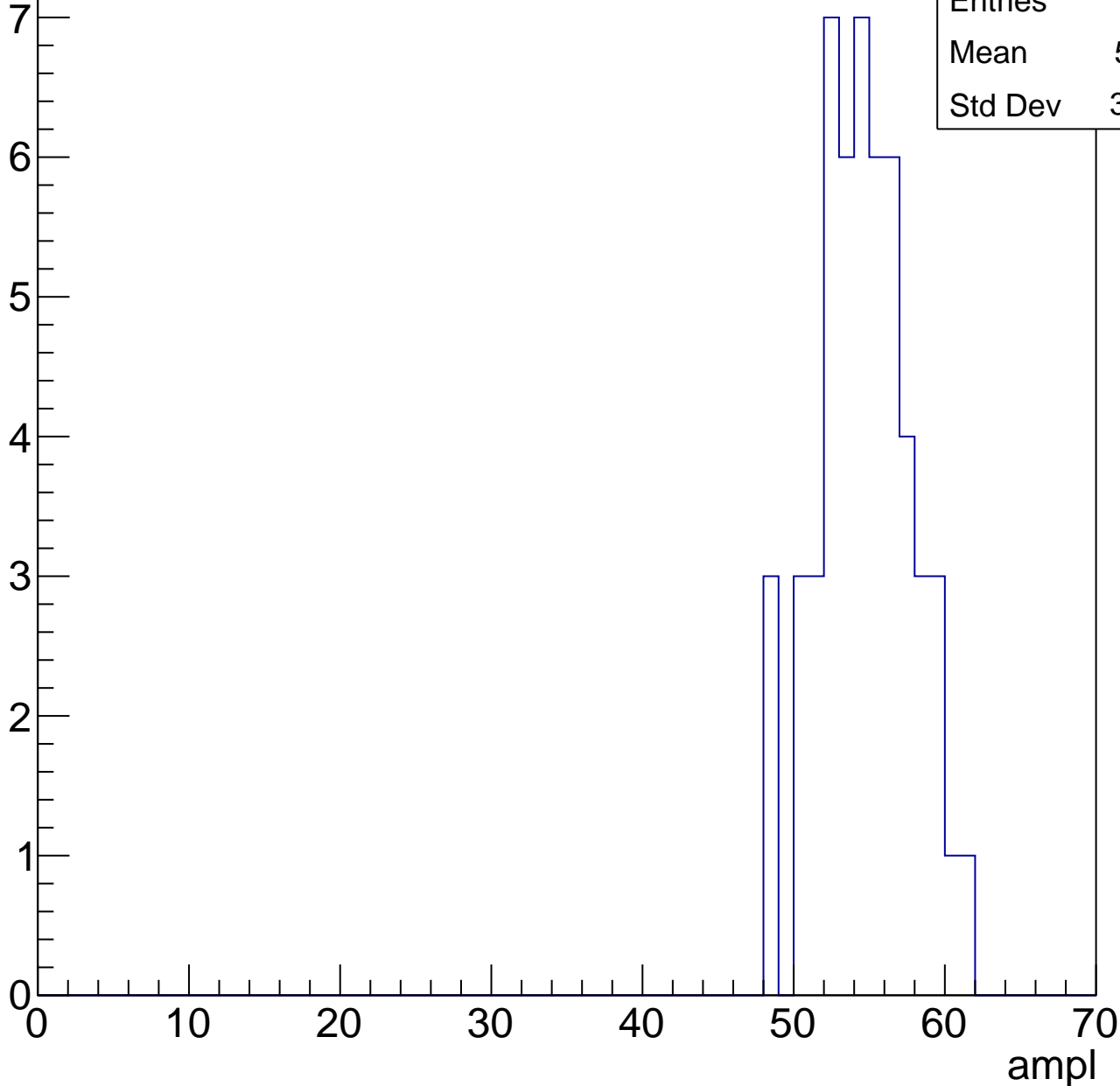


B1L103S, U2-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	54.21
Std Dev	3.037

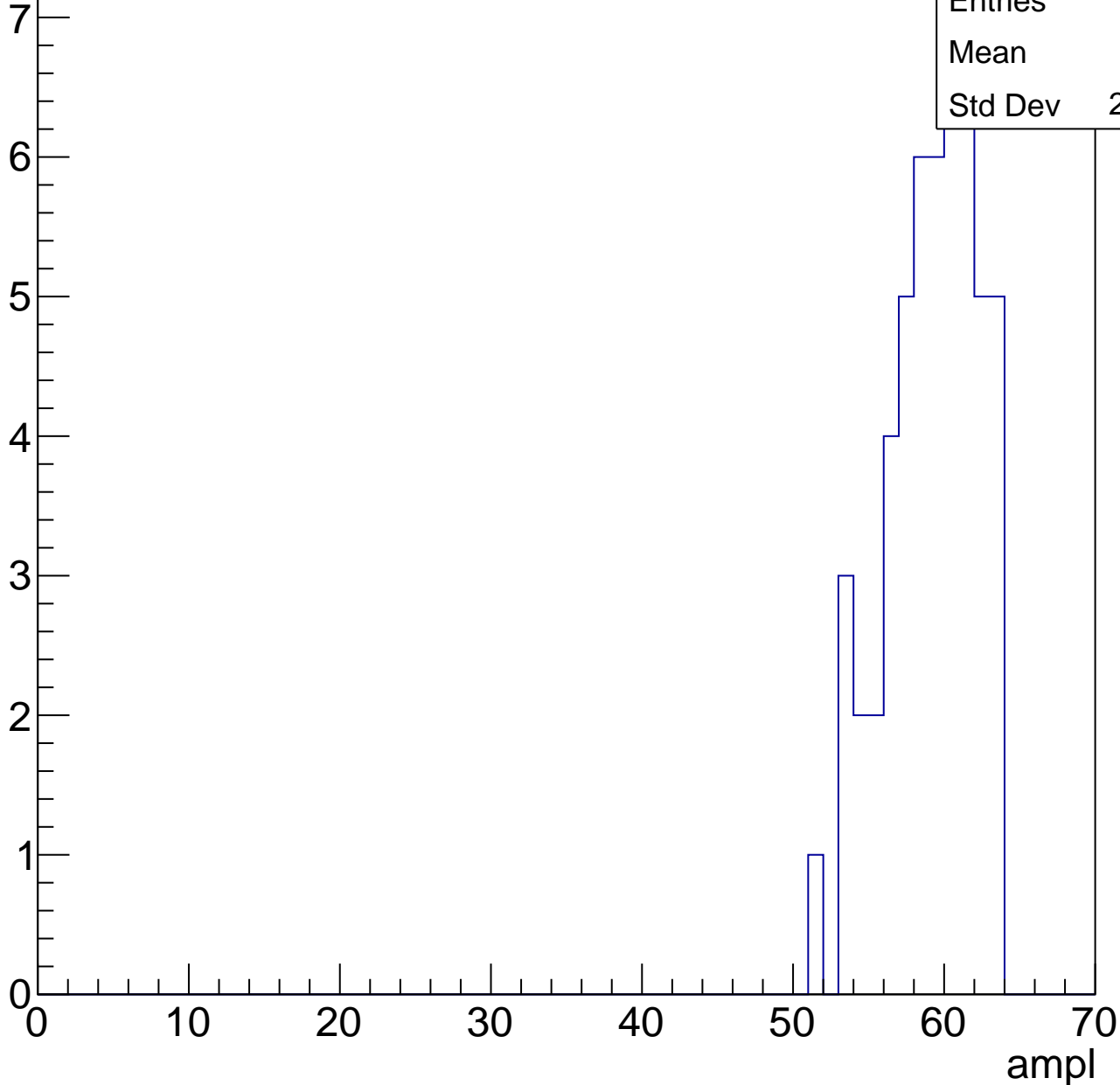


B1L103S, U2-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.7
Std Dev	2.982

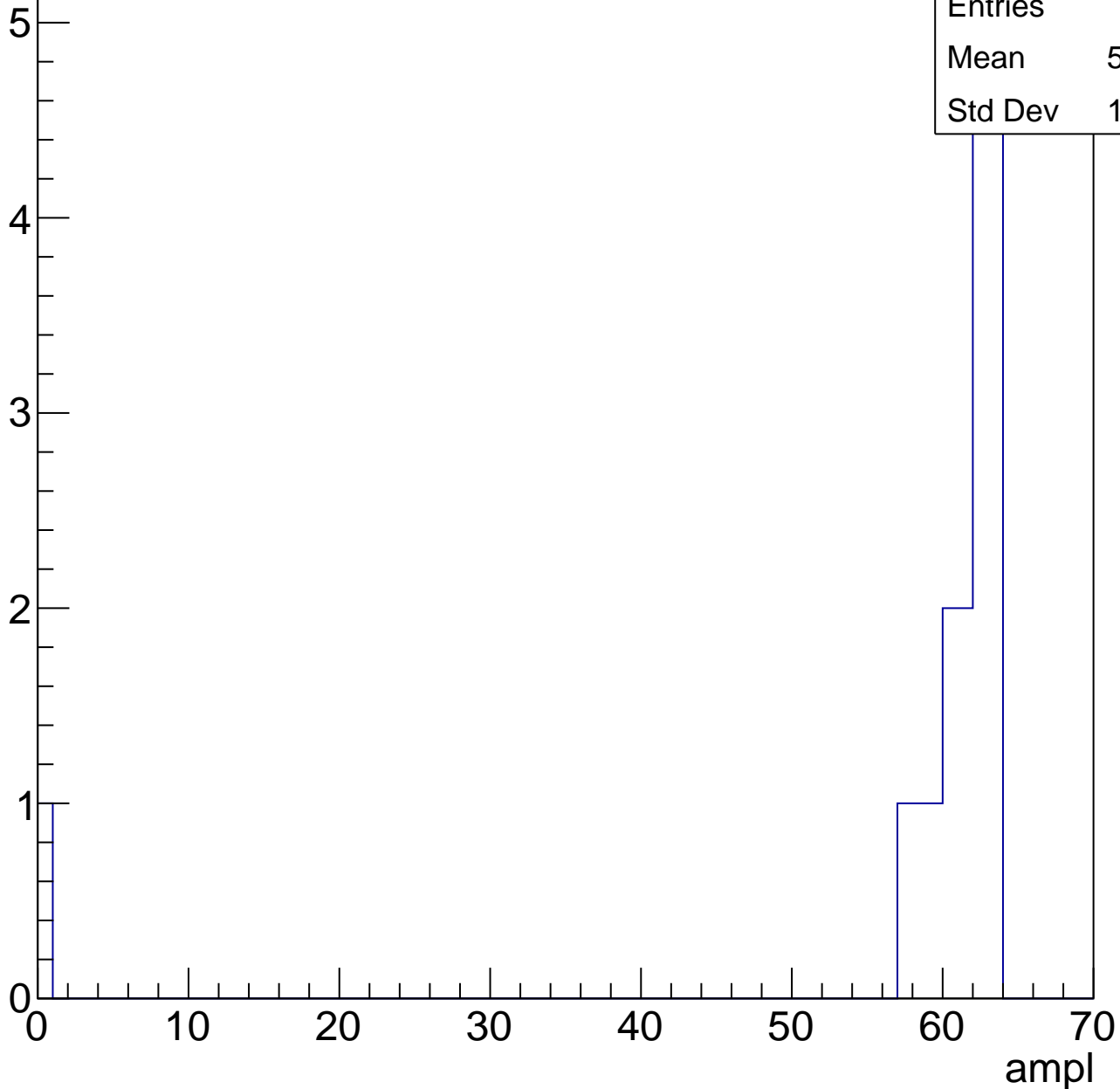


B1L103S, U2-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.83
Std Dev	14.14



B1L103S, U2-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



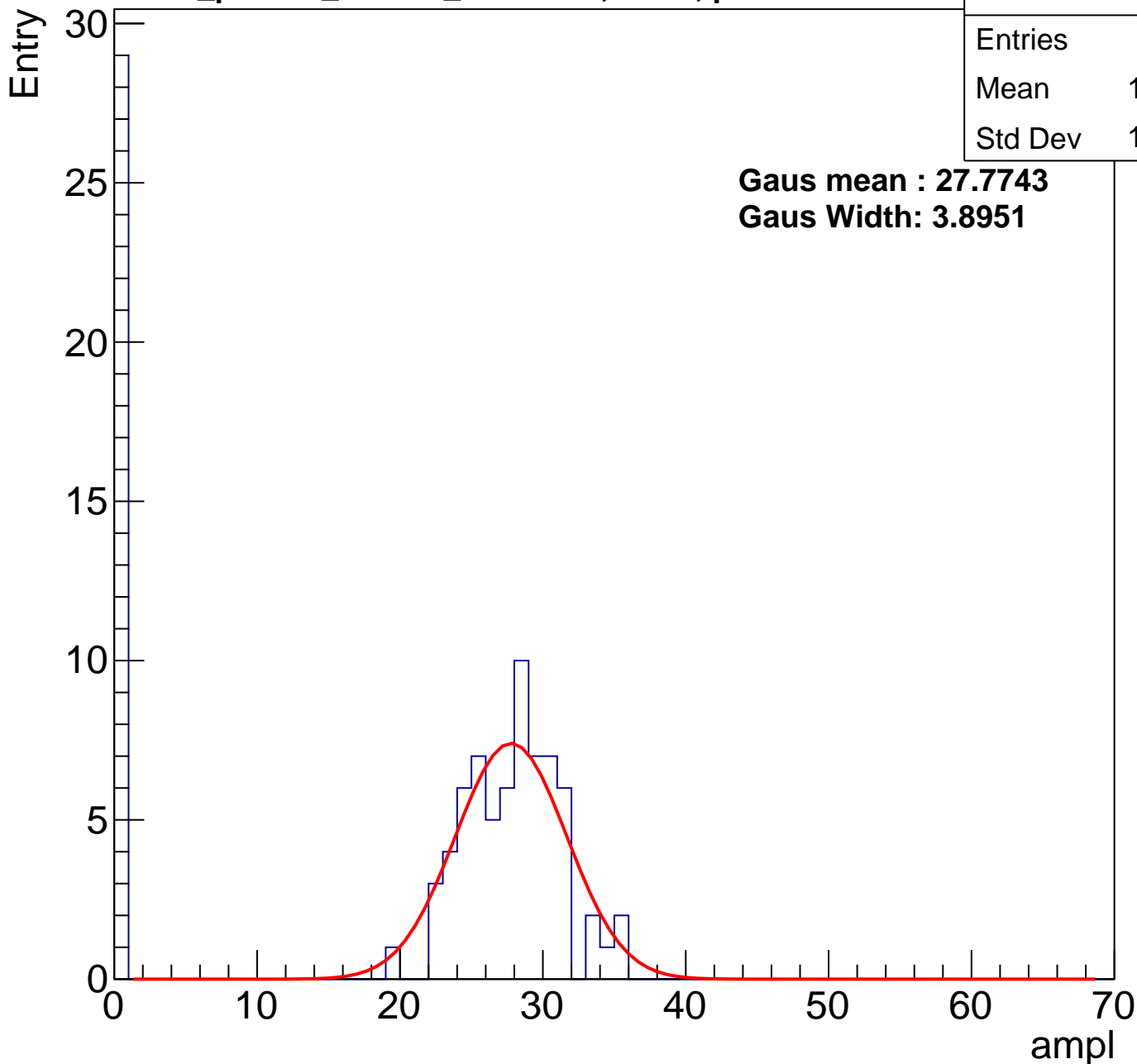
B1L103S, U2-ch105, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	19.14
Std Dev	12.89

Gaus mean : 27.7743

Gaus Width: 3.8951



B1L103S, U2-ch105, adc1

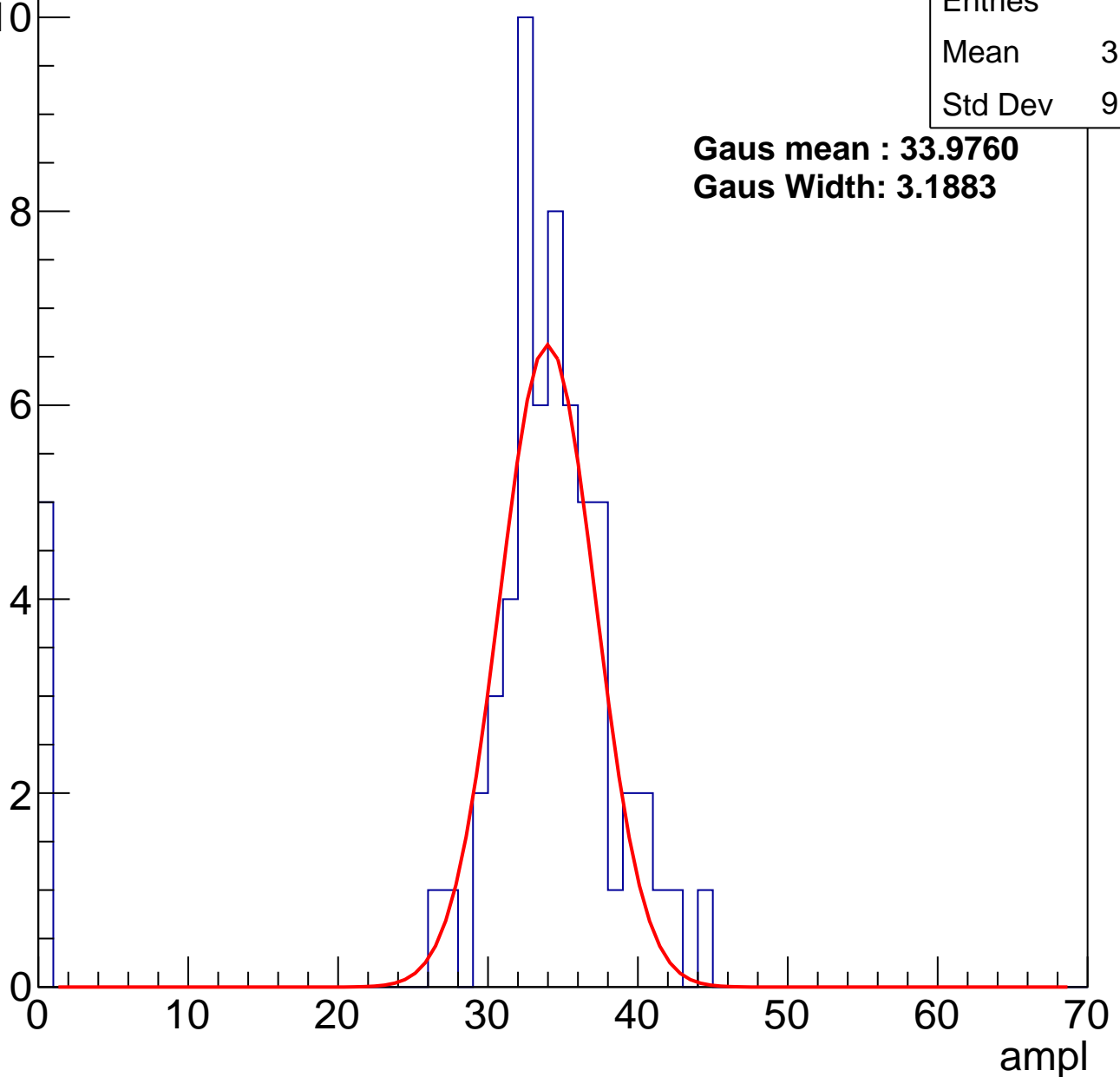
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	31.45
Std Dev	9.747

Gaus mean : 33.9760

Gaus Width: 3.1883



B1L103S, U2-ch105, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	34.58
Std Dev	14.13

Gaus mean : 40.5817

Gaus Width: 3.4002

Entry

10

8

6

4

2

0

0

10

20

30

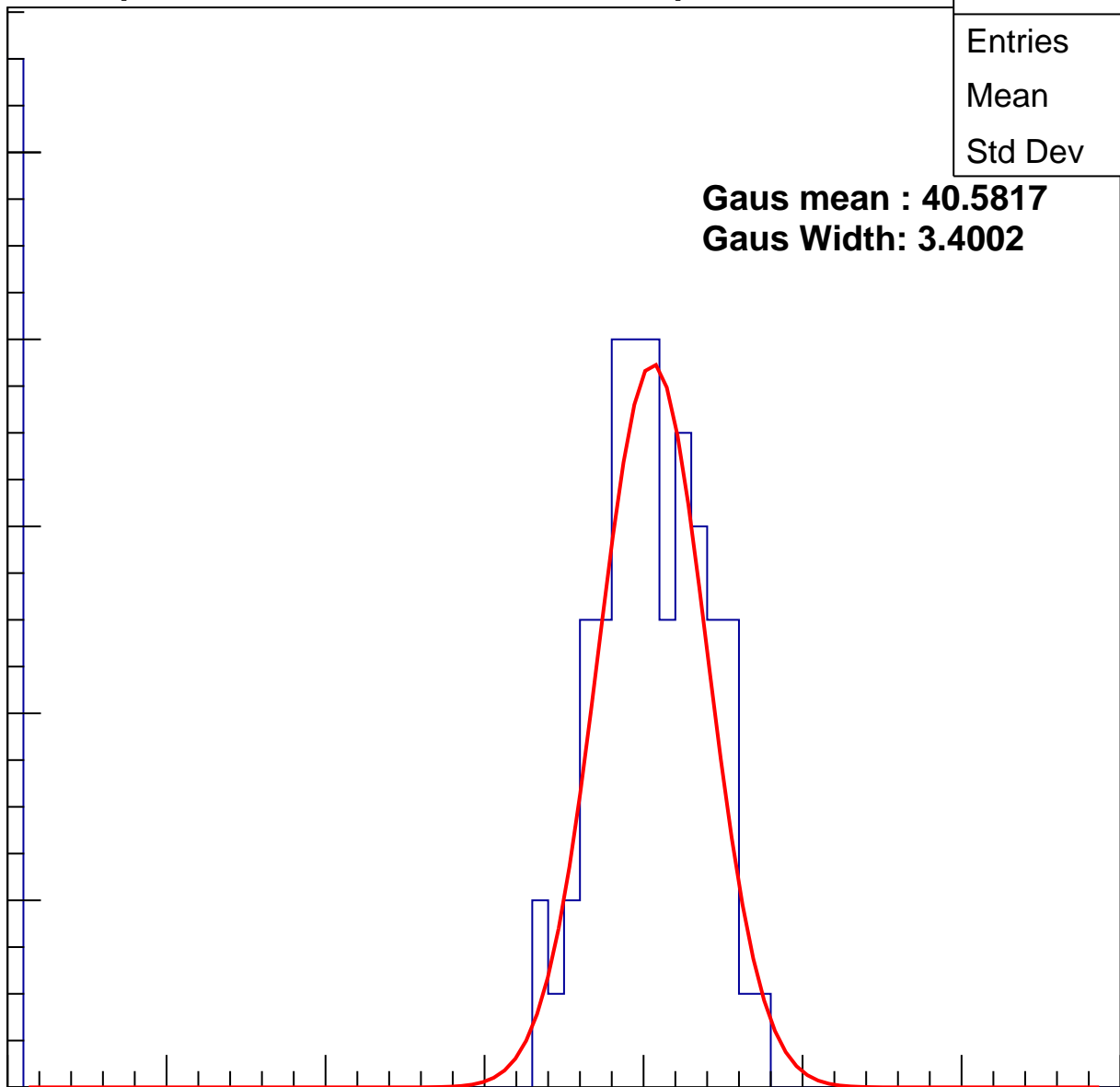
40

50

60

70

ampl

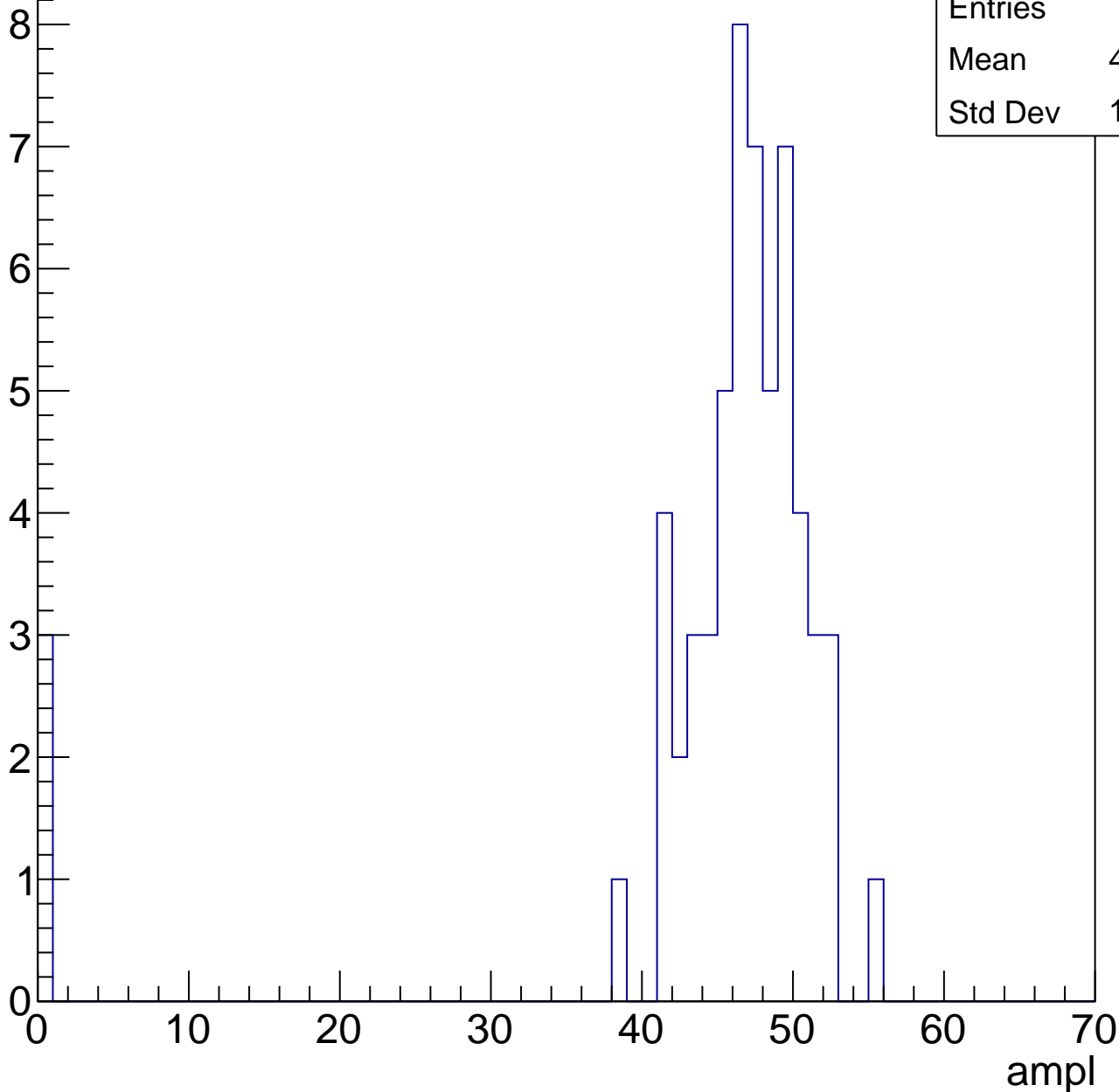


B1L103S, U2-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	44.34
Std Dev	10.77

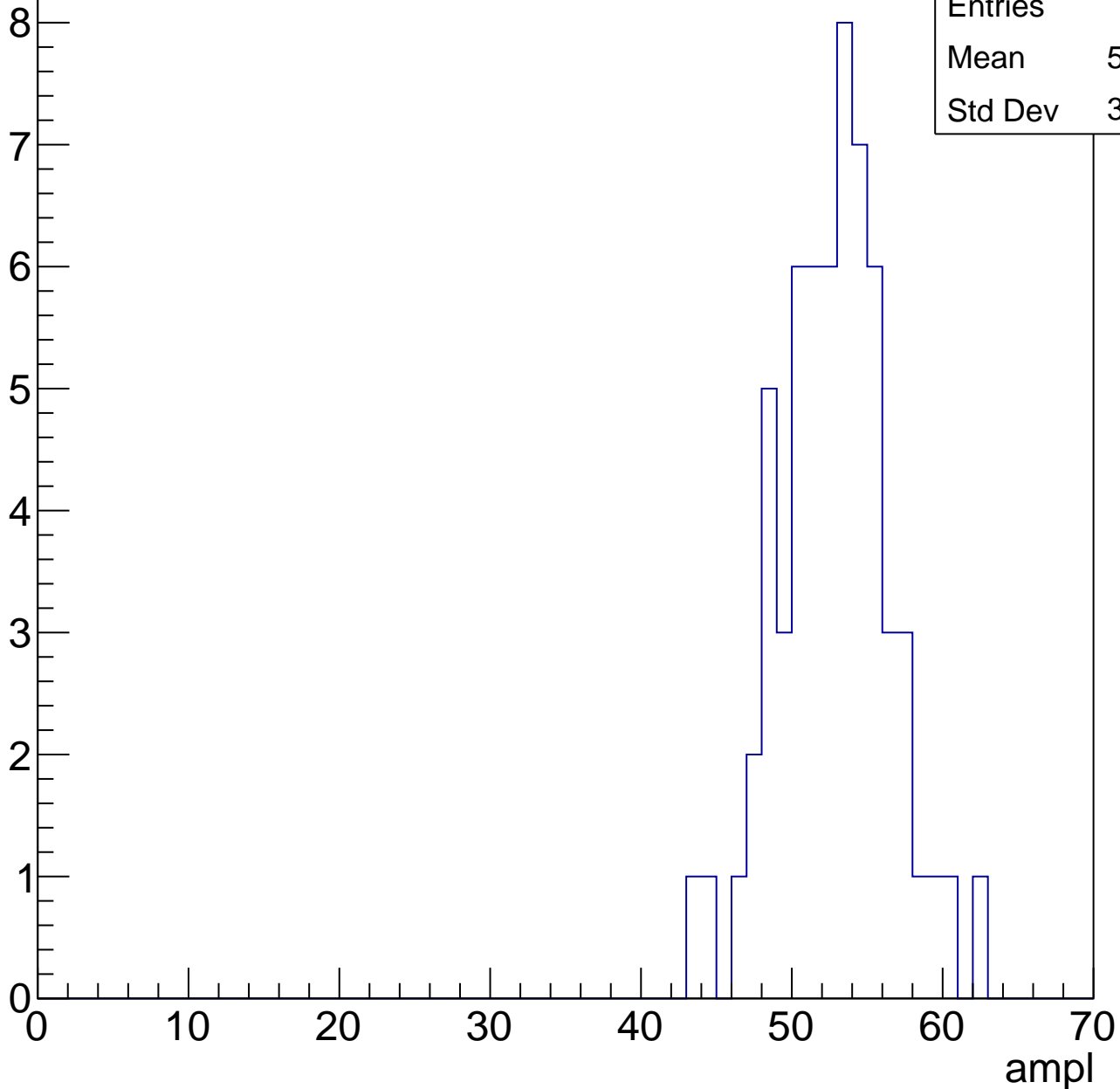


B1L103S, U2-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

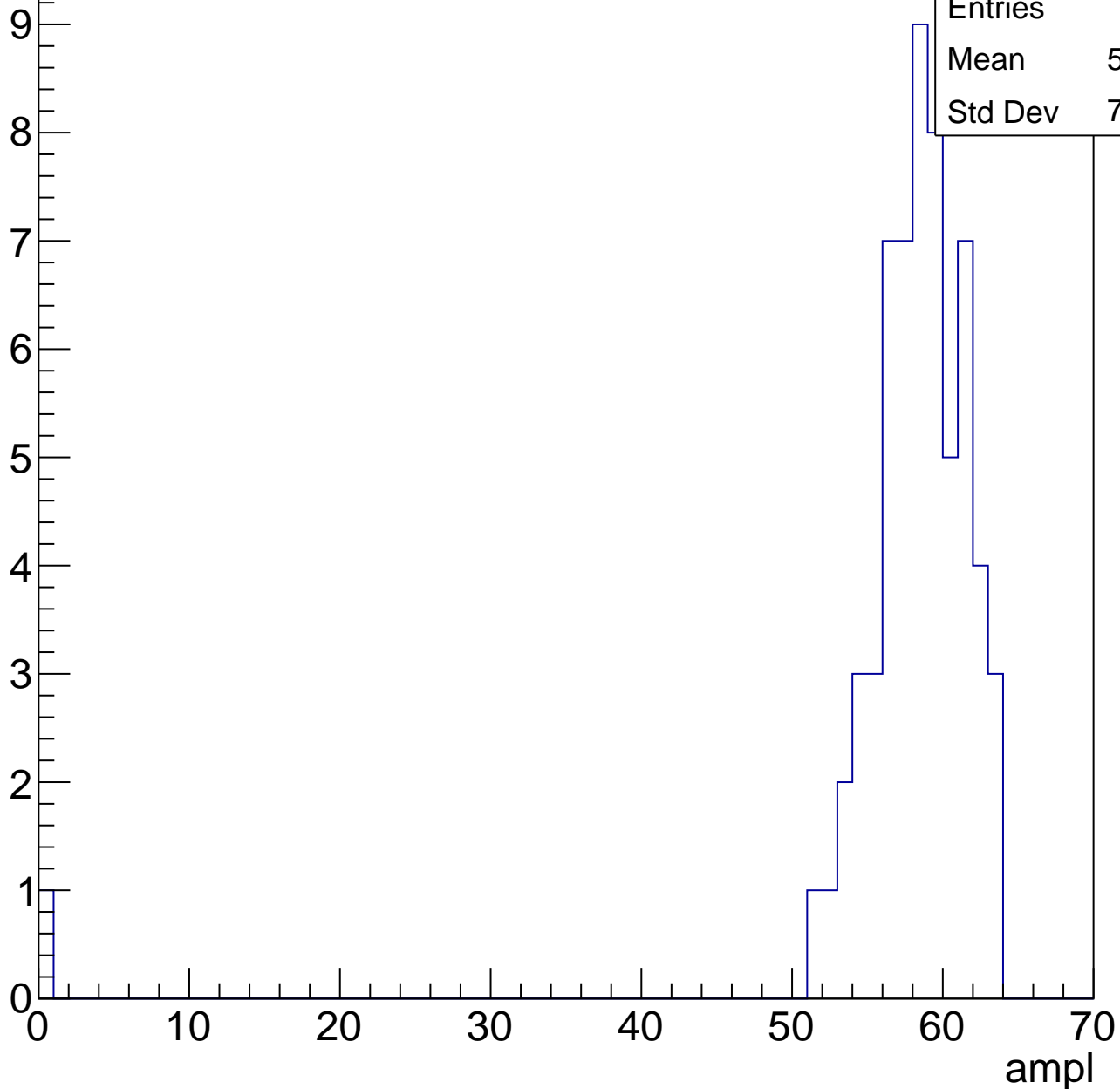
Entries	62
Mean	52.29
Std Dev	3.647



B1L103S, U2-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

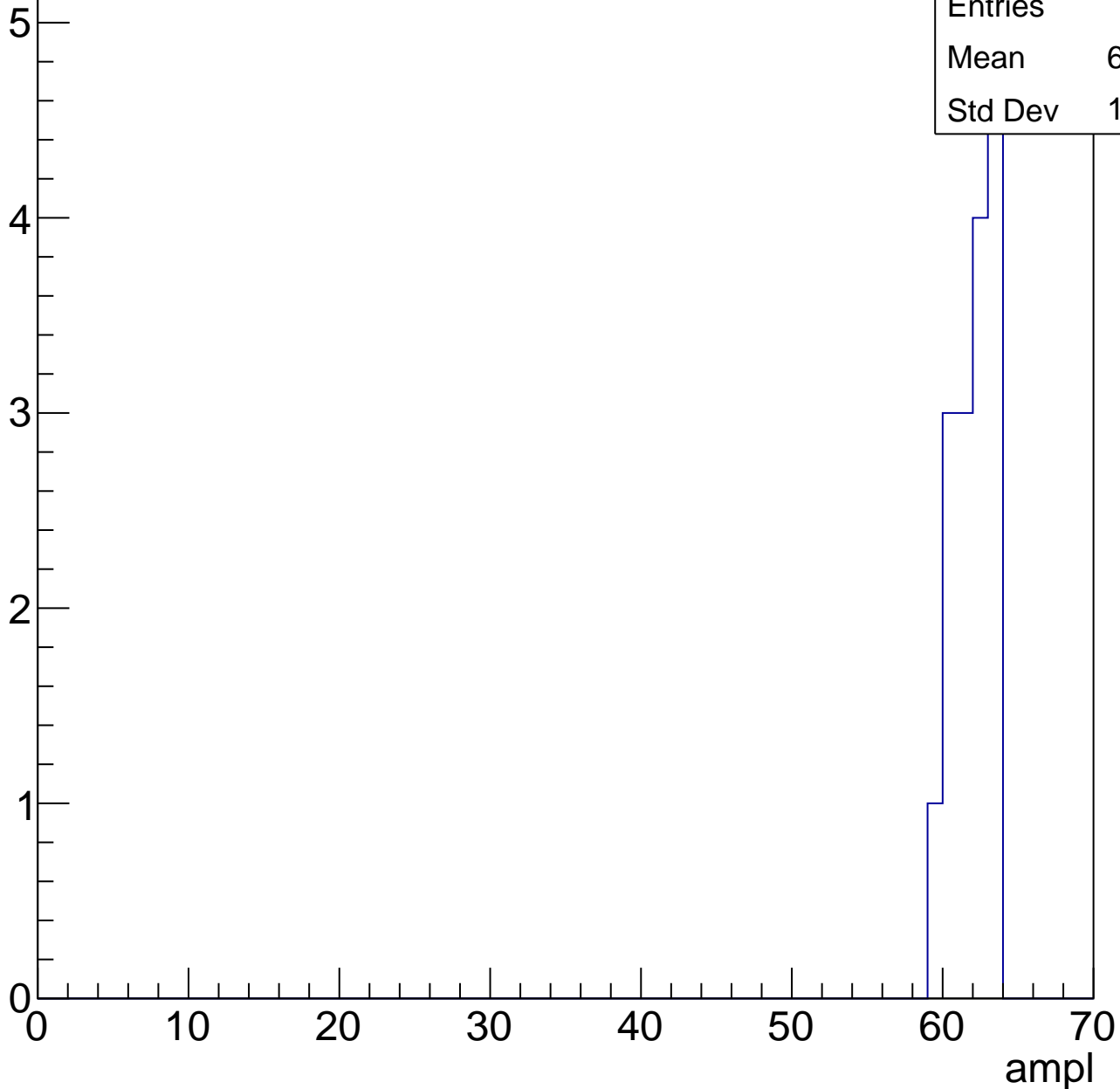


B1L103S, U2-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.56
Std Dev	1.273

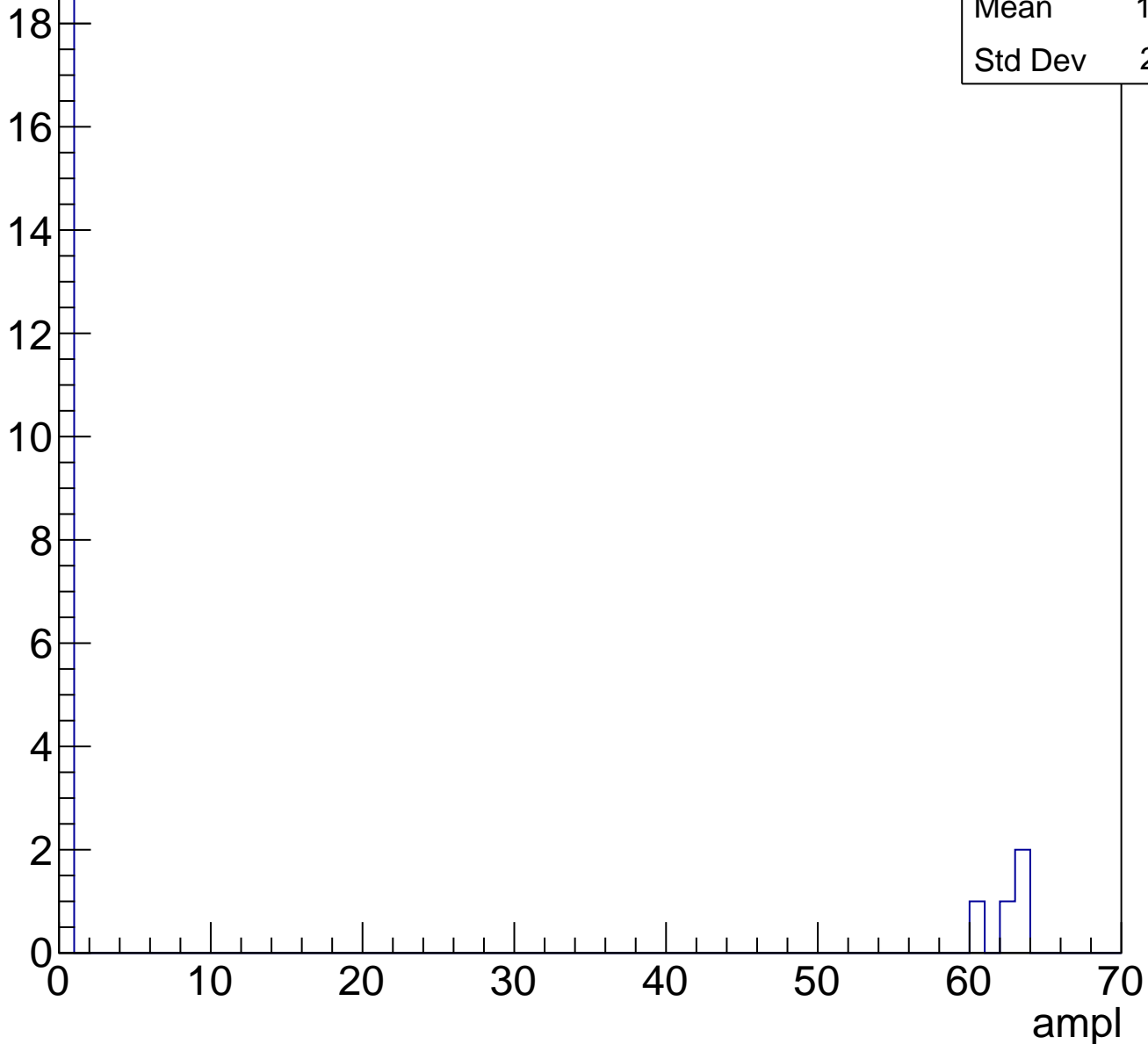


B1L103S, U2-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.78
Std Dev	23.51

Entry



B1L103S, U2-ch106, adc0

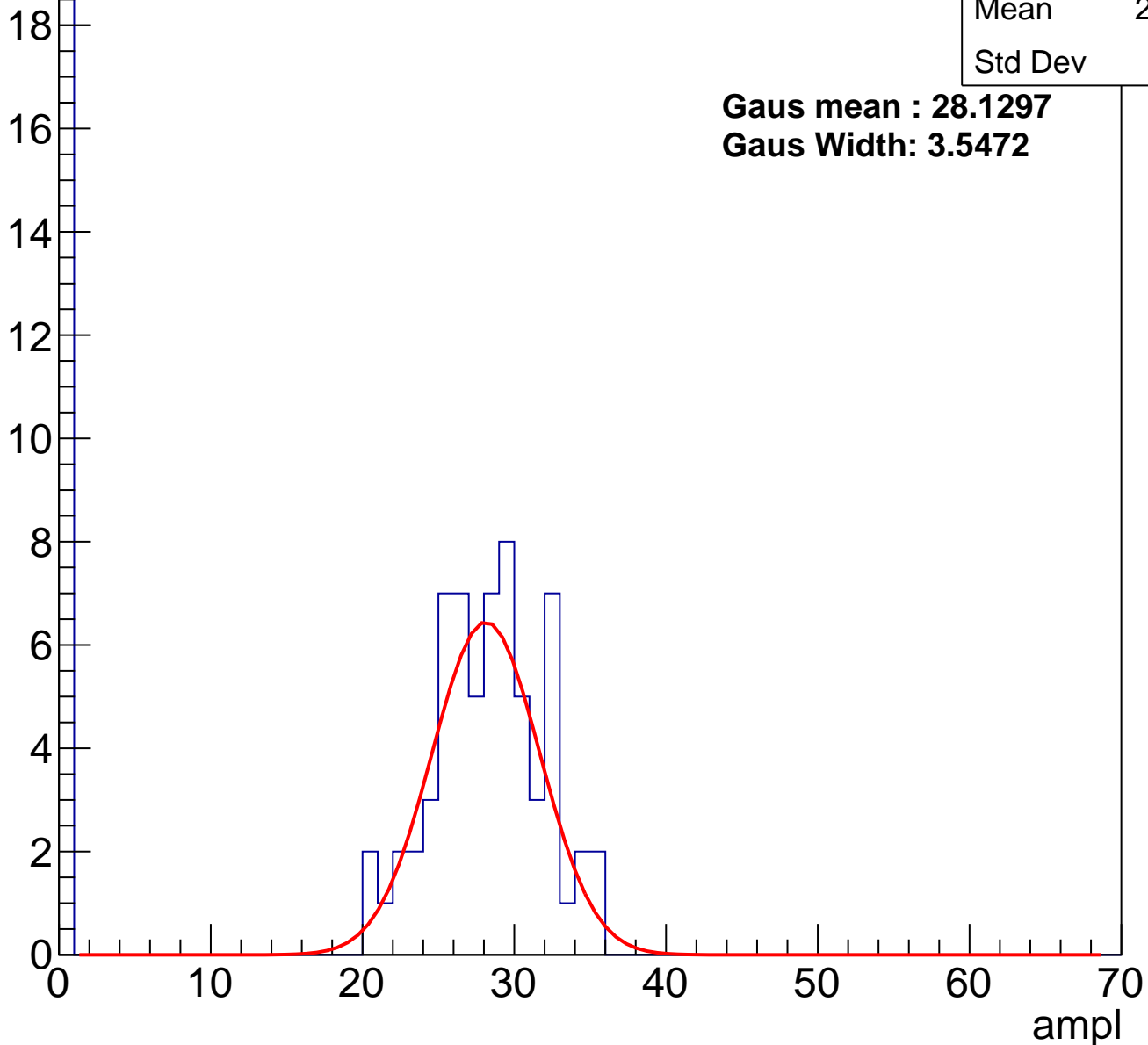
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	21.46
Std Dev	12.1

Gaus mean : 28.1297

Gaus Width: 3.5472

Entry



B1L103S, U2-ch106, adc1

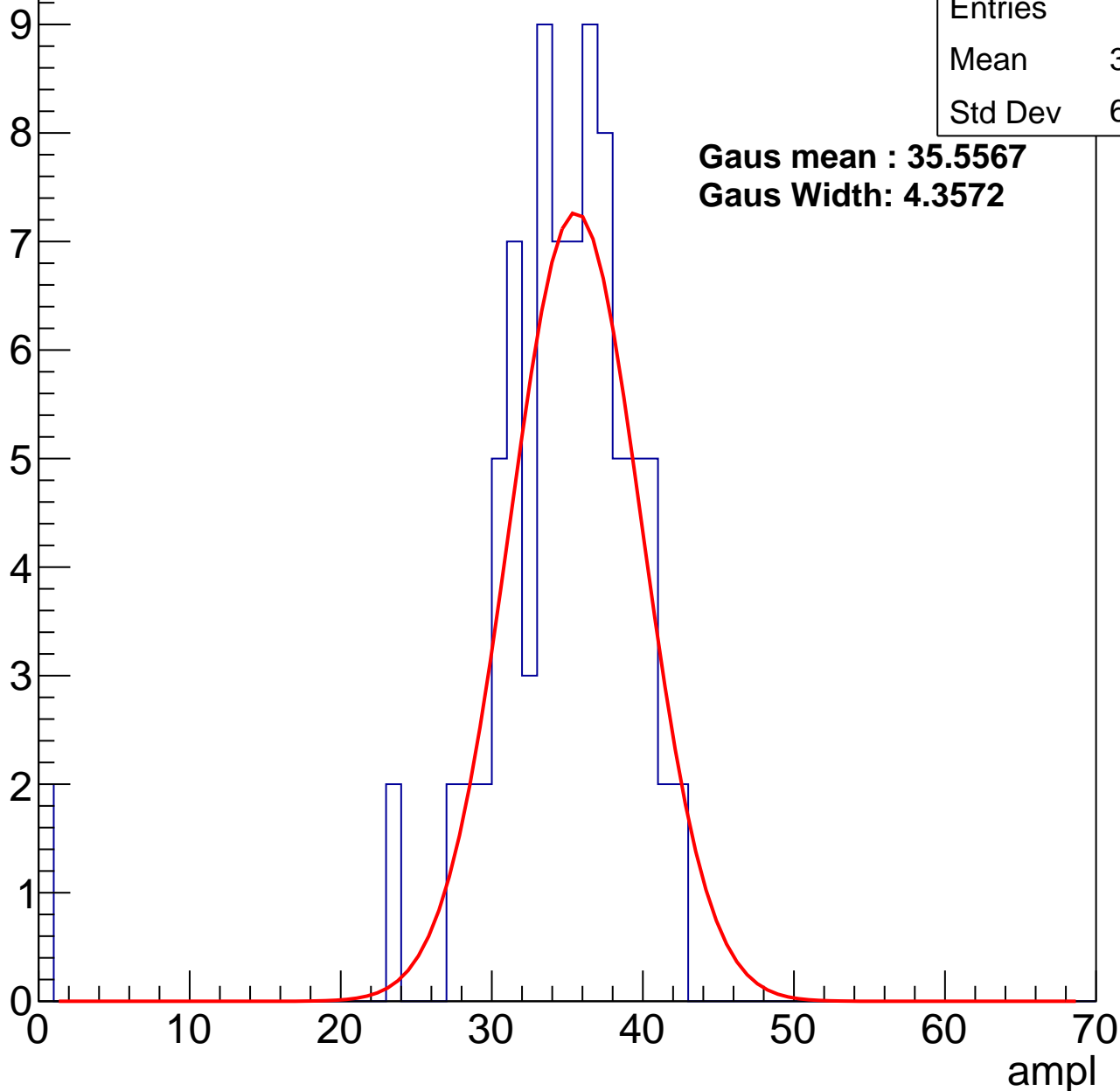
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	33.67
Std Dev	6.596

Gaus mean : 35.5567

Gaus Width: 4.3572



B1L103S, U2-ch106, adc2

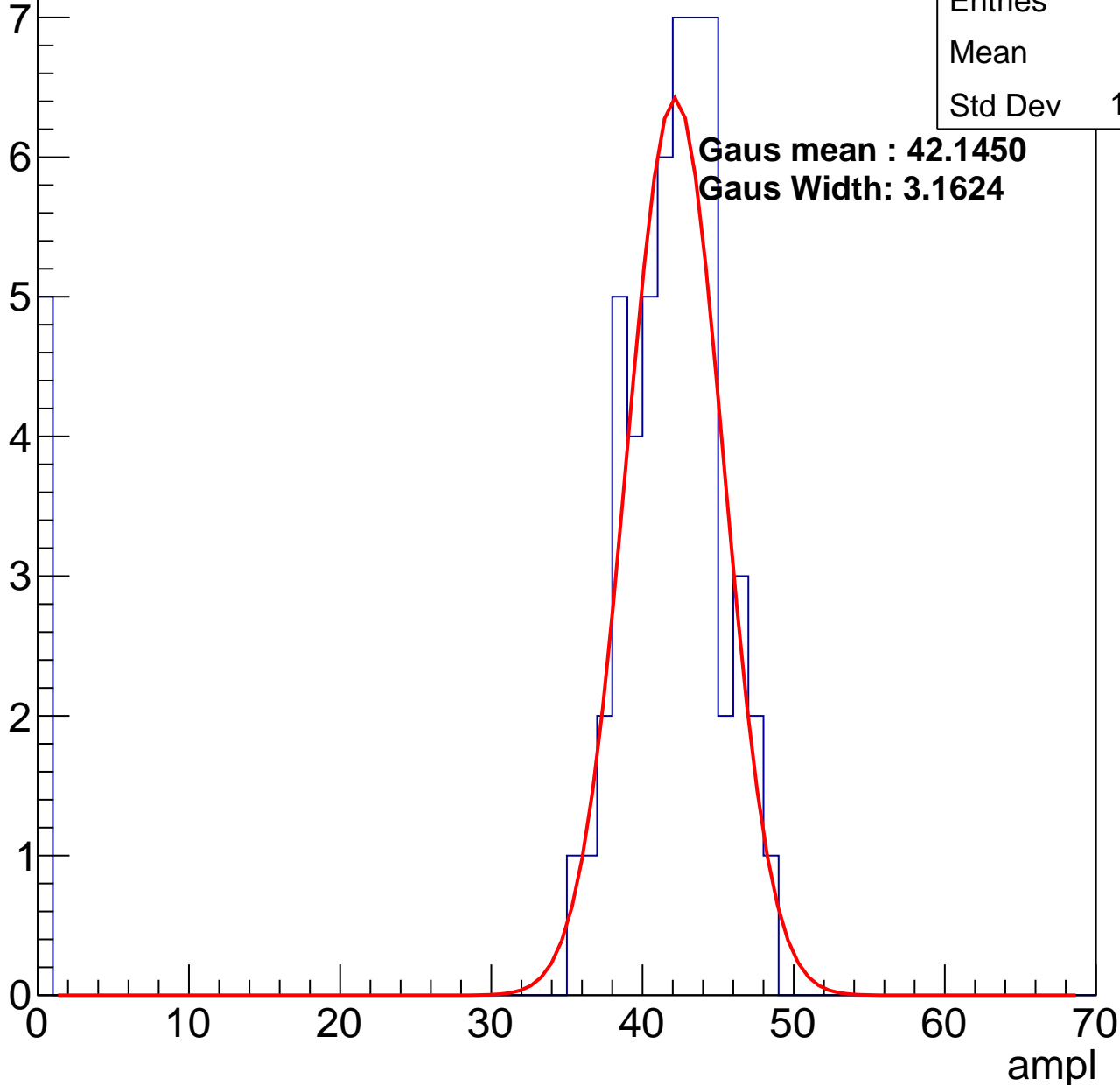
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	38.1
Std Dev	12.04

Gaus mean : 42.1450

Gaus Width: 3.1624

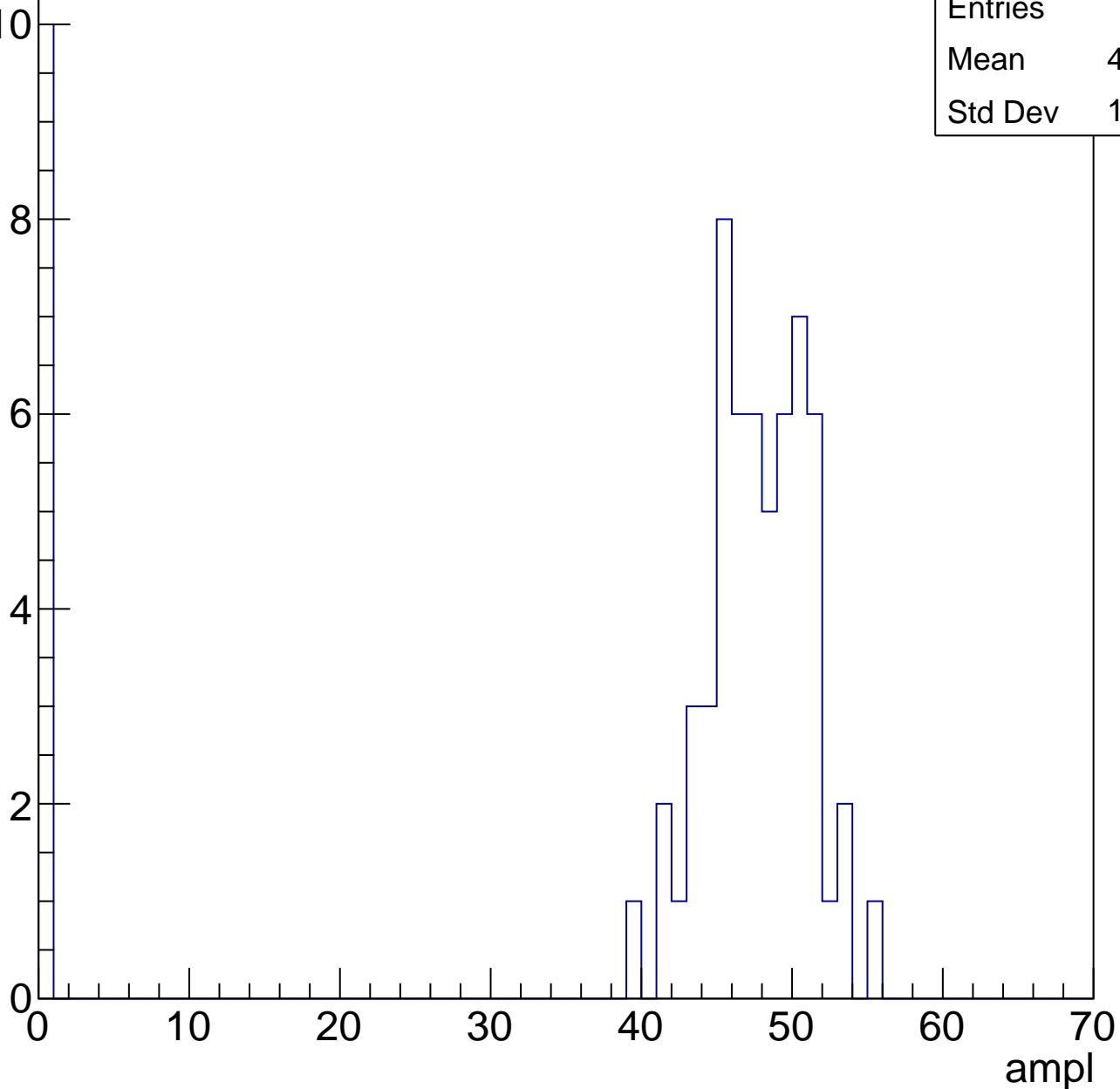


B1L103S, U2-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.37
Std Dev	17.03

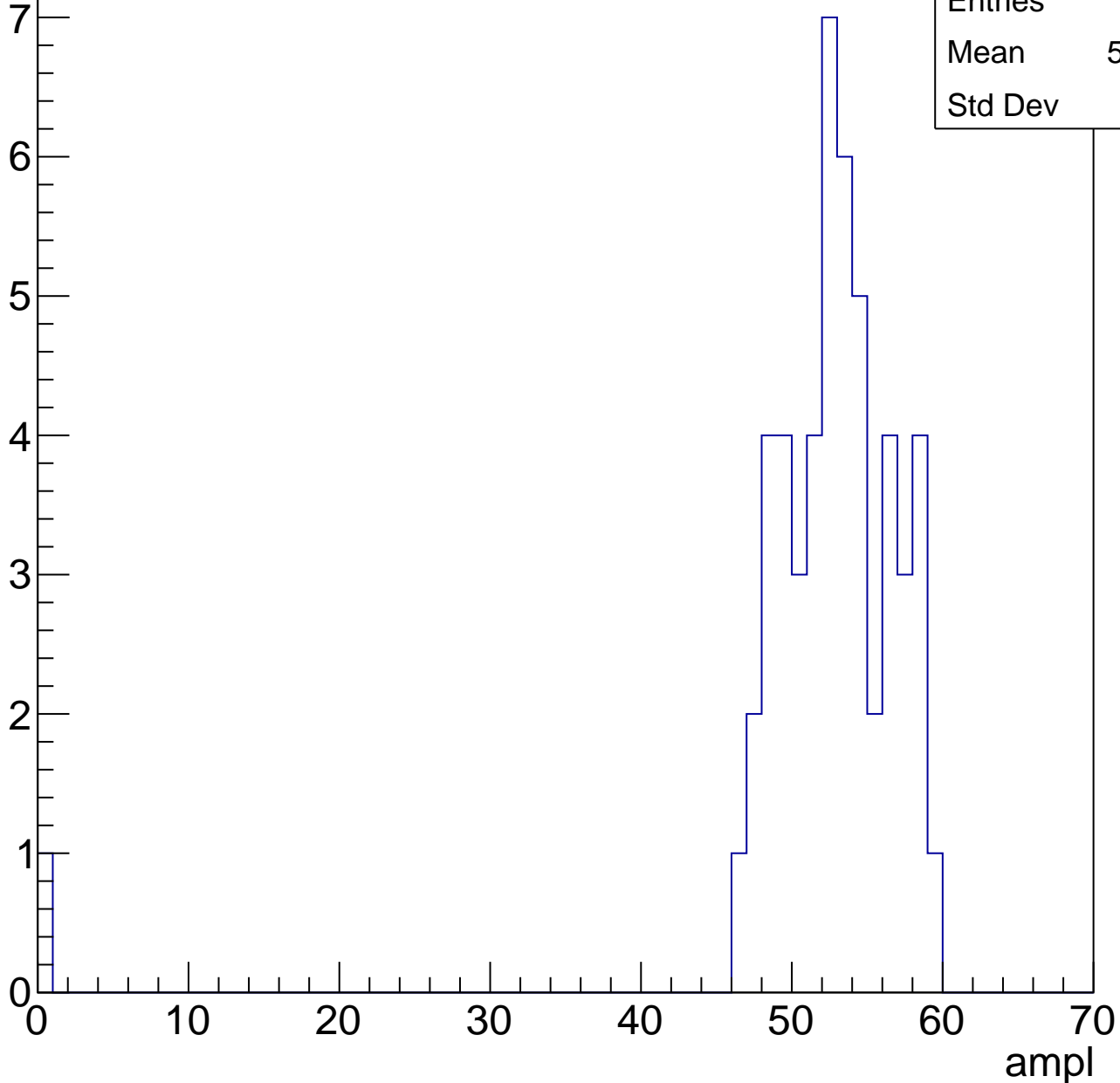


B1L103S, U2-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	51.57
Std Dev	8.01

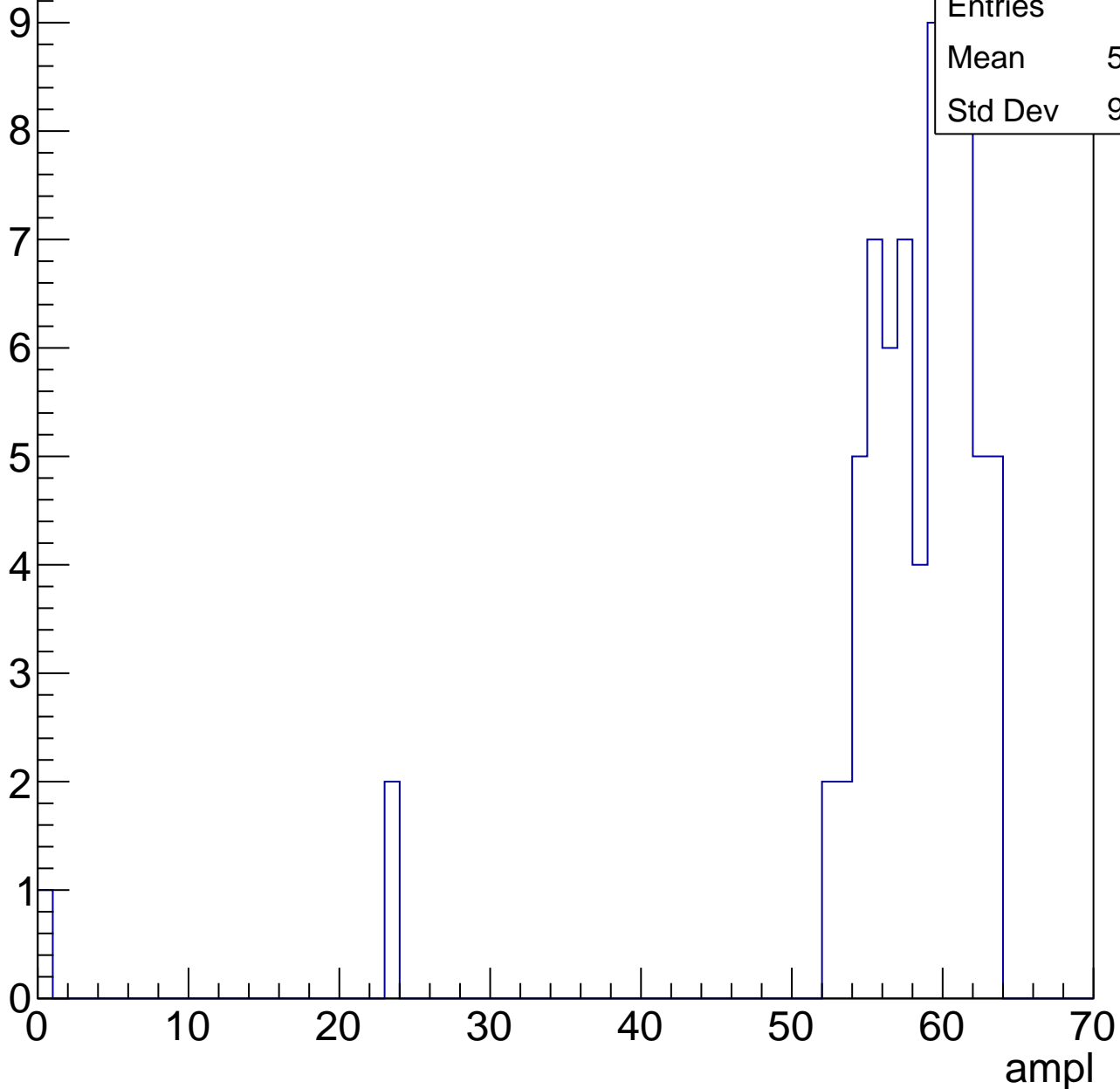


B1L103S, U2-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

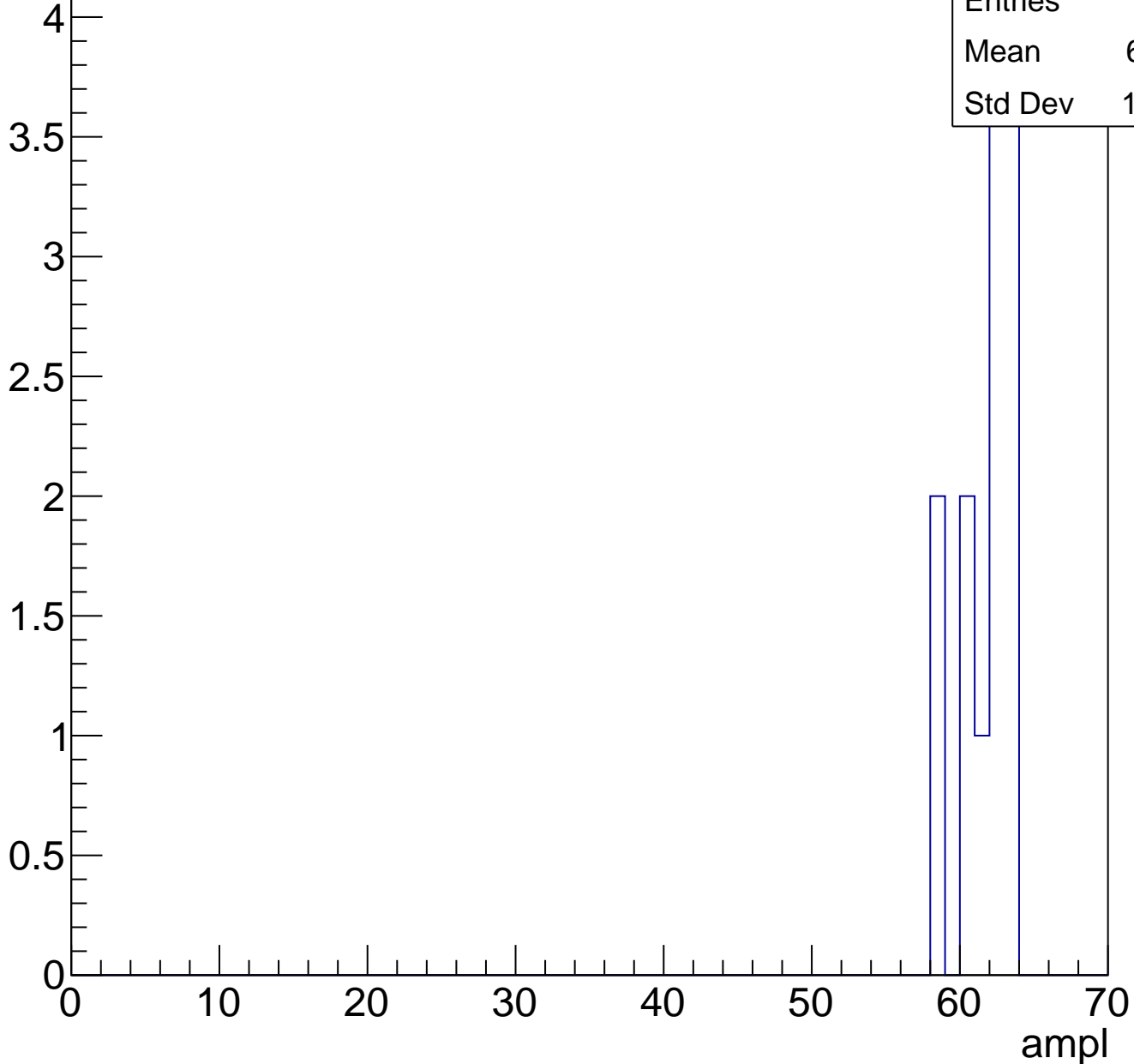
Entries	71
Mean	56.37
Std Dev	9.374



B1L103S, U2-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



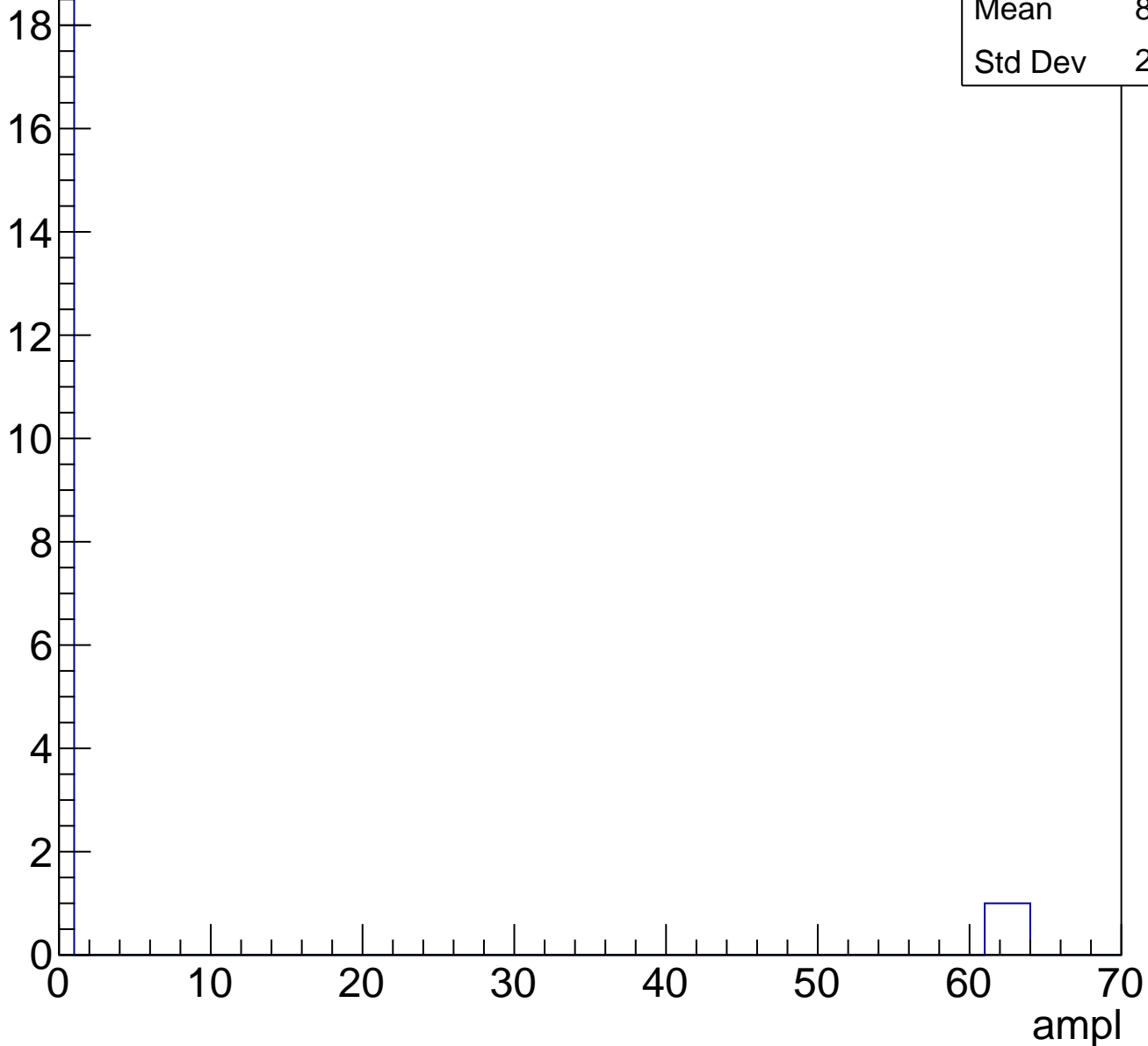
Entries	13
Mean	61.31
Std Dev	1.727

B1L103S, U2-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U2-ch107, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	20.38
Std Dev	12.65

Gaus mean : 27.9145

Gaus Width: 3.3682

Entry

25

20

15

10

5

0

0

10

20

30

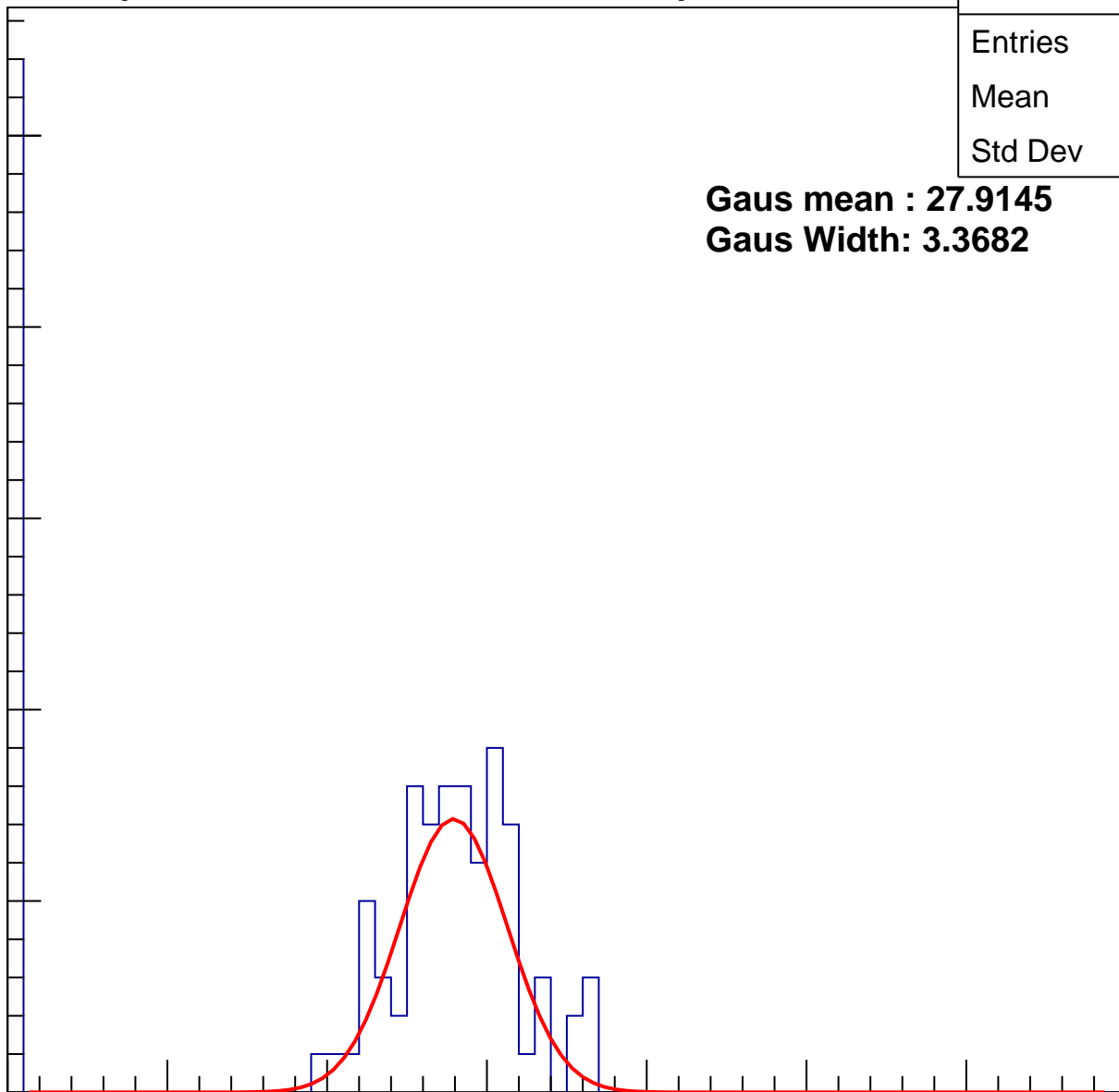
40

50

60

70

ampl



B1L103S, U2-ch107, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	30.98
Std Dev	12.39

Gaus mean : 36.0569

Gaus Width: 3.3547

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U2-ch107, adc2

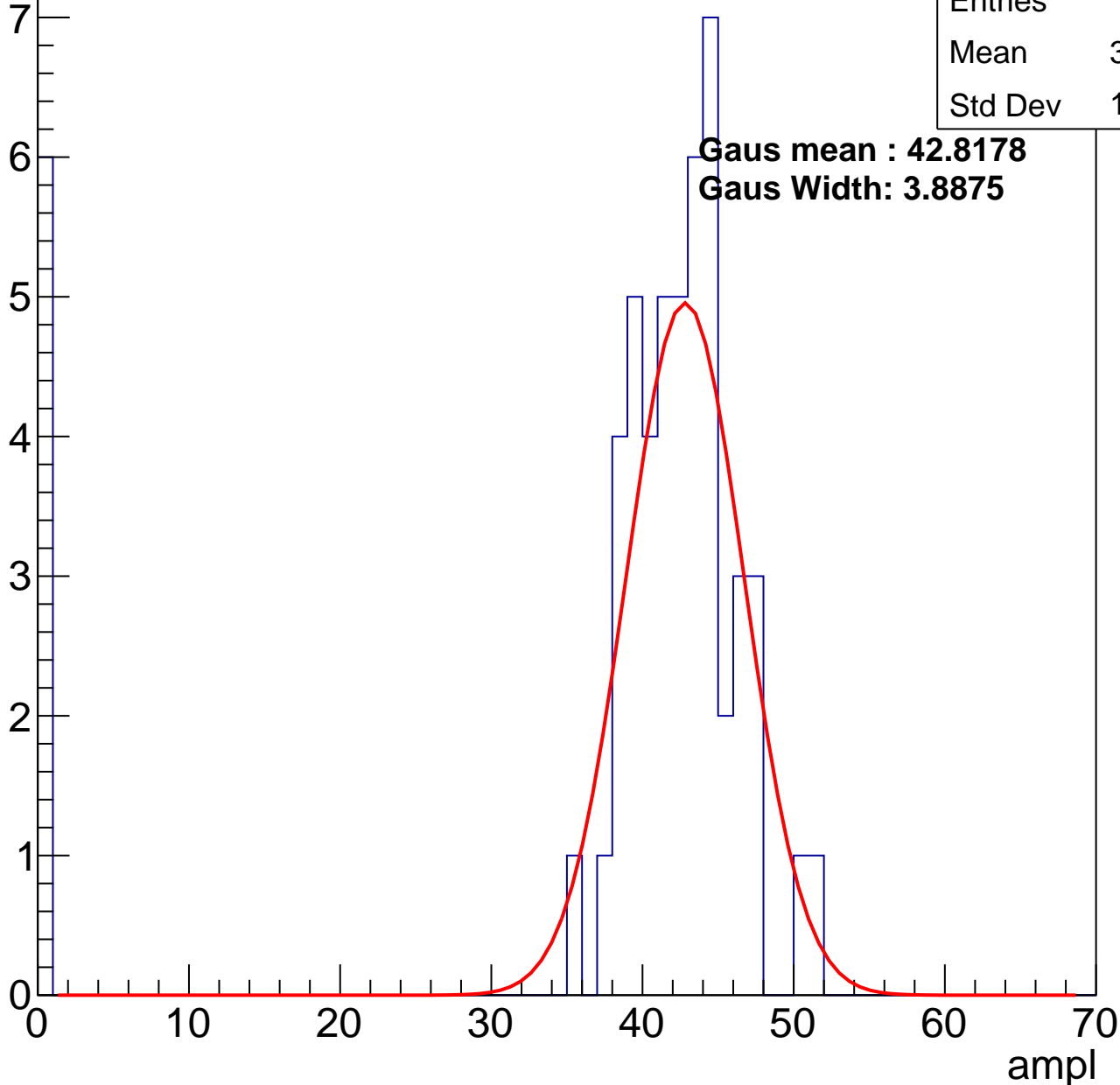
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	37.59
Std Dev	13.65

Gaus mean : 42.8178

Gaus Width: 3.8875

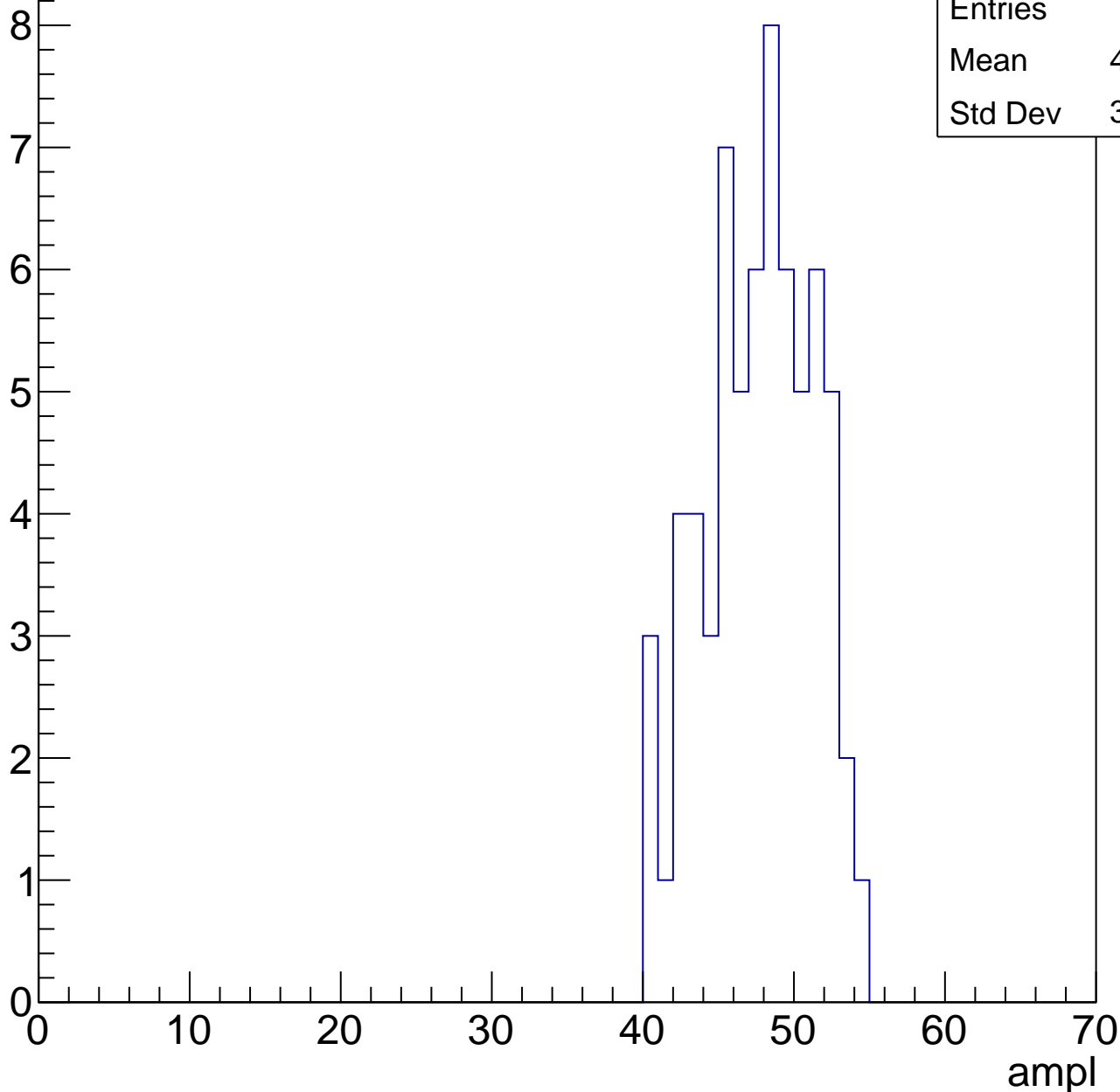


B1L103S, U2-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

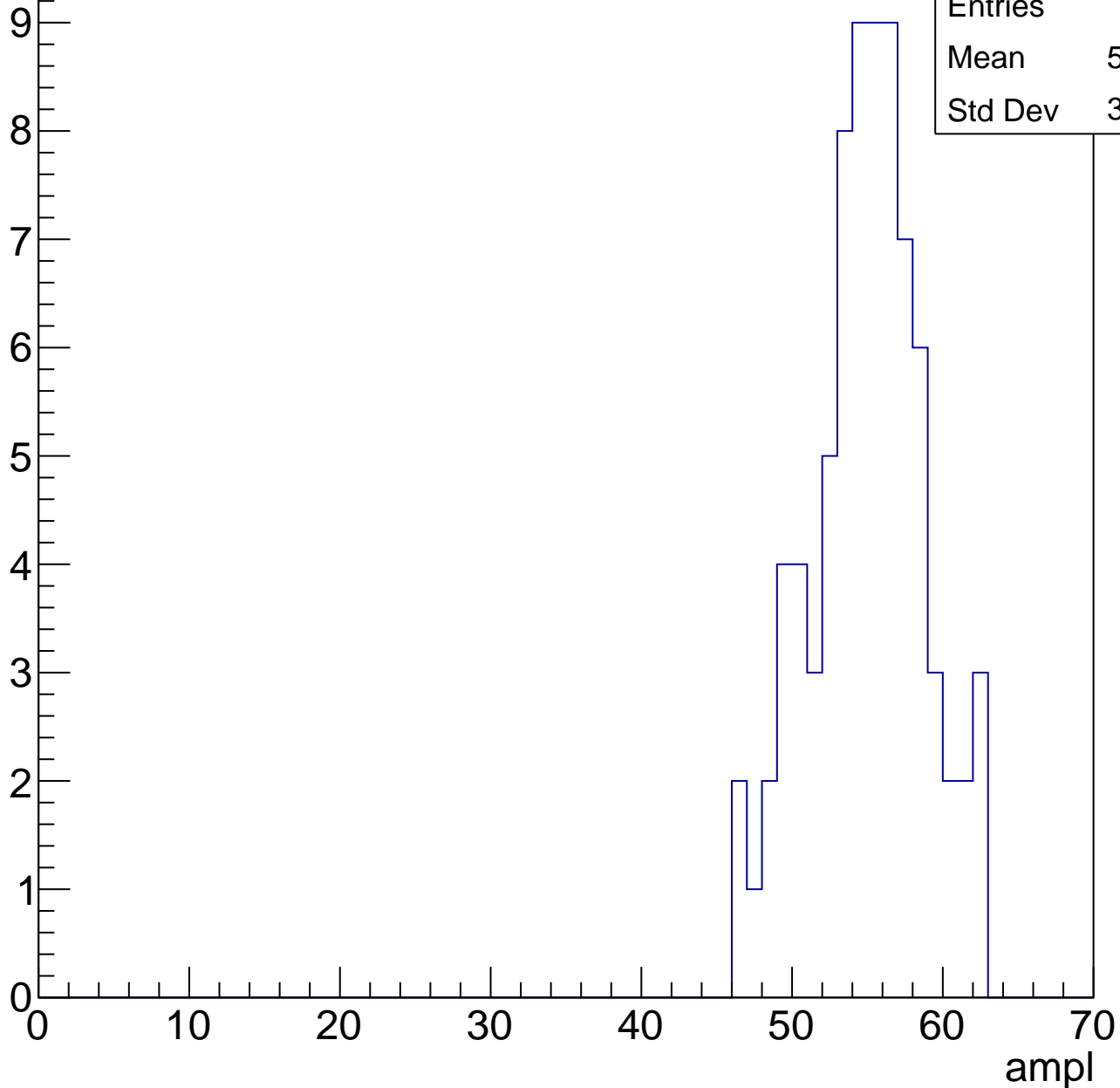
Entries	66
Mean	47.18
Std Dev	3.533



B1L103S, U2-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch107, adc5

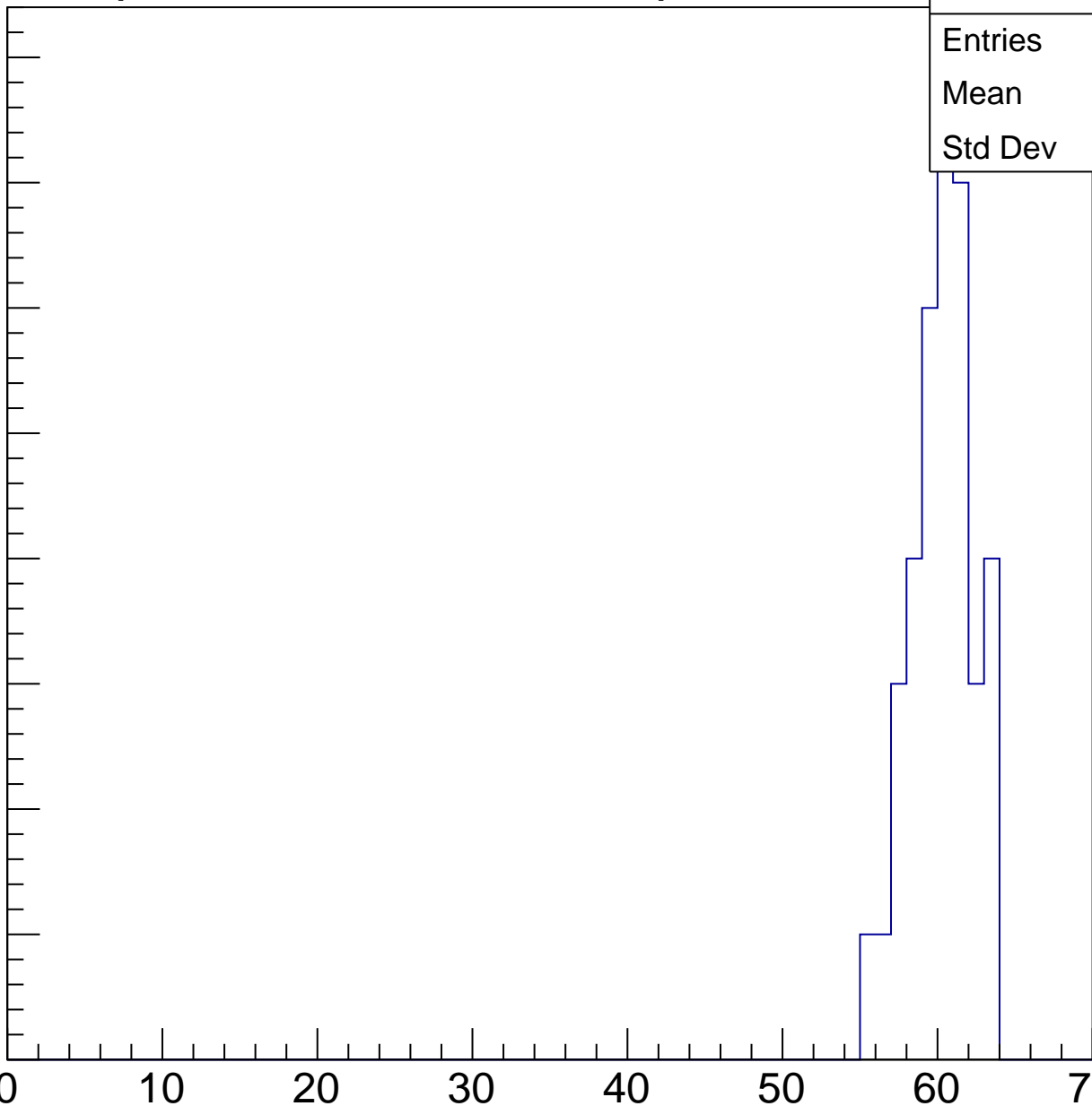
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	59.81
Std Dev	1.971

ampl

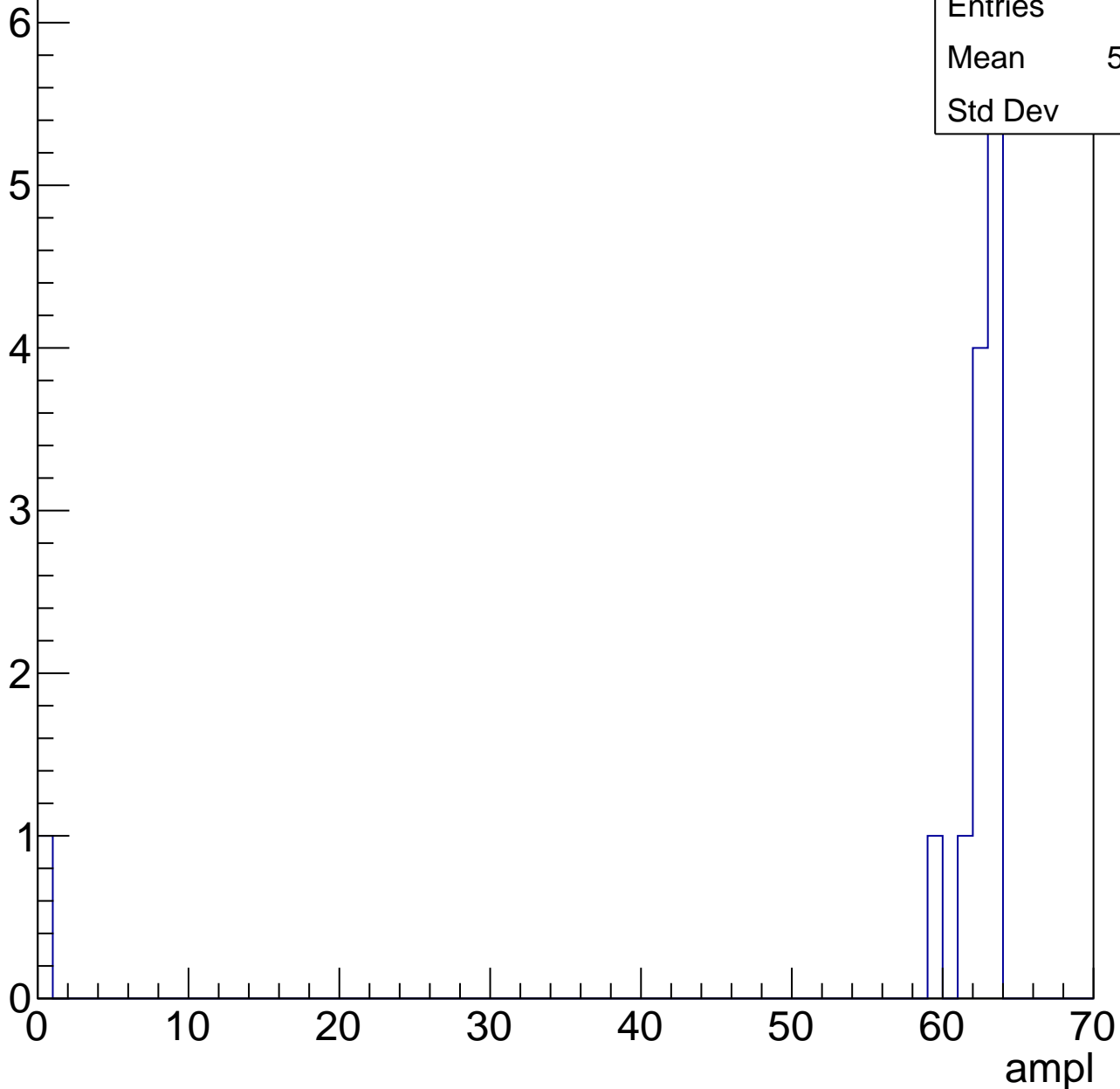


B1L103S, U2-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.38
Std Dev	16.6



B1L103S, U2-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U2-ch108, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	21.14
Std Dev	14.19

Gaus mean : 31.2079

Gaus Width: 4.2050

Entry

25

20

15

10

5

0

0

10

20

30

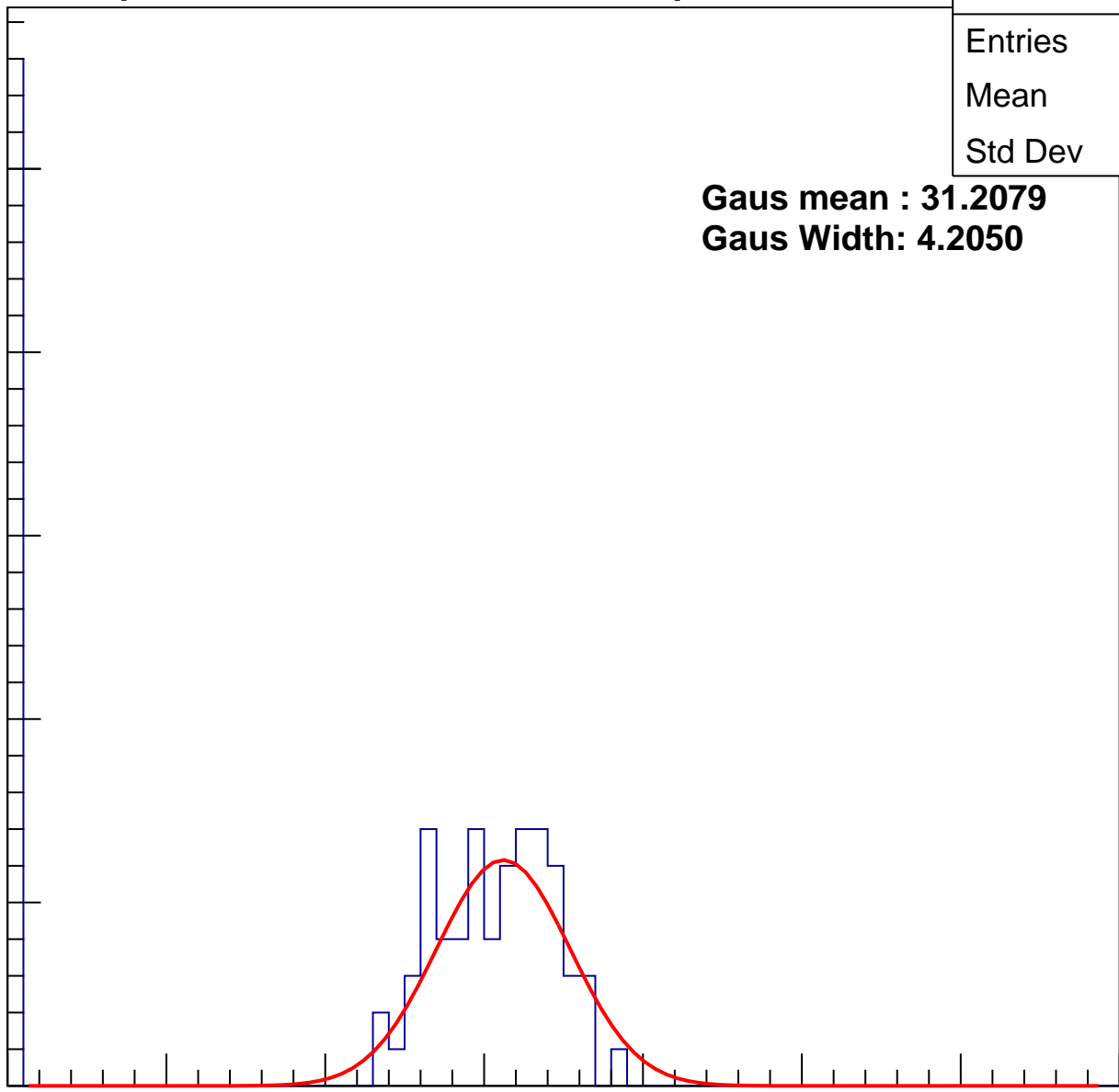
40

50

60

70

ampl



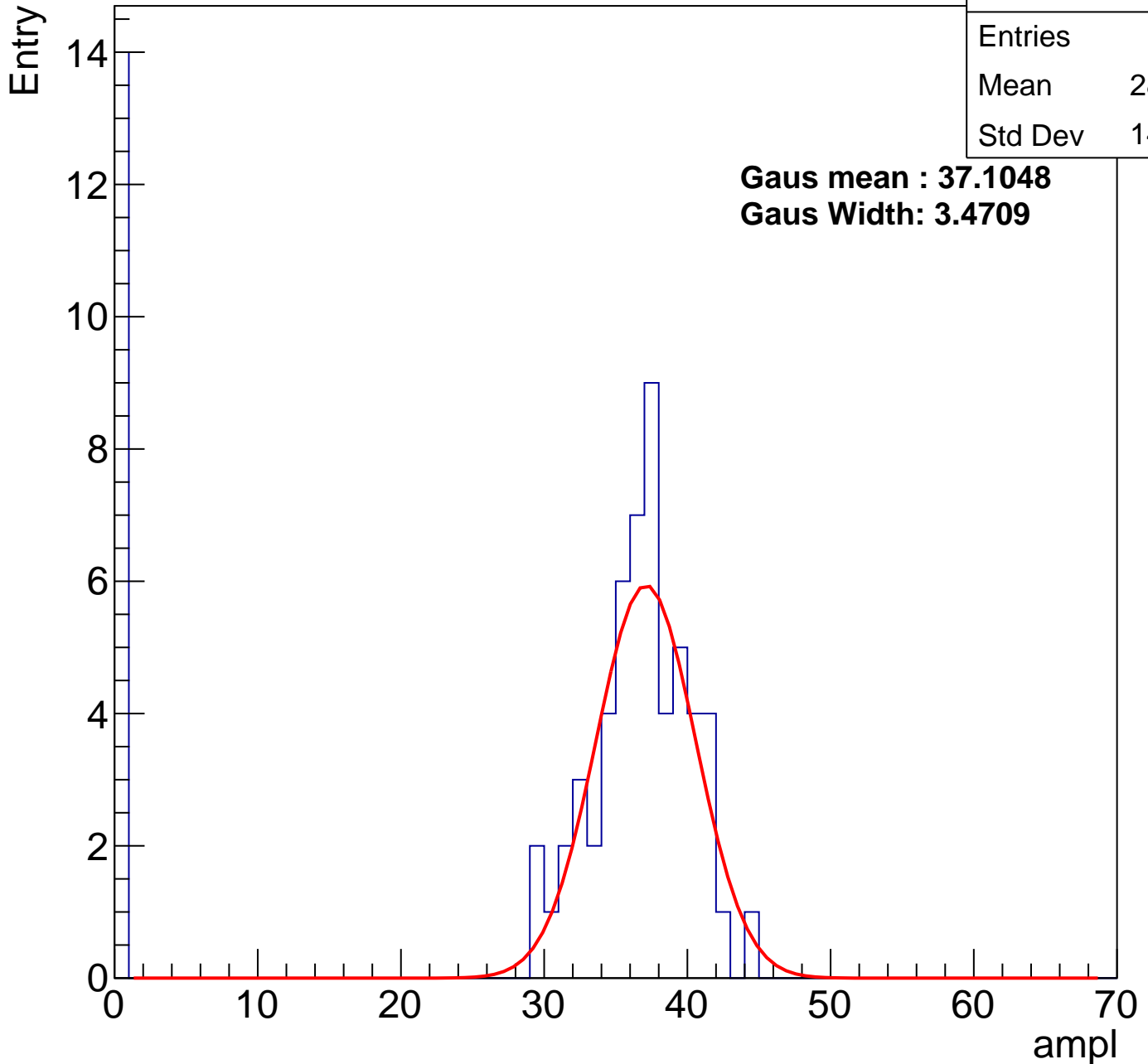
B1L103S, U2-ch108, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	28.99
Std Dev	14.92

Gaus mean : 37.1048

Gaus Width: 3.4709



B1L103S, U2-ch108, adc2

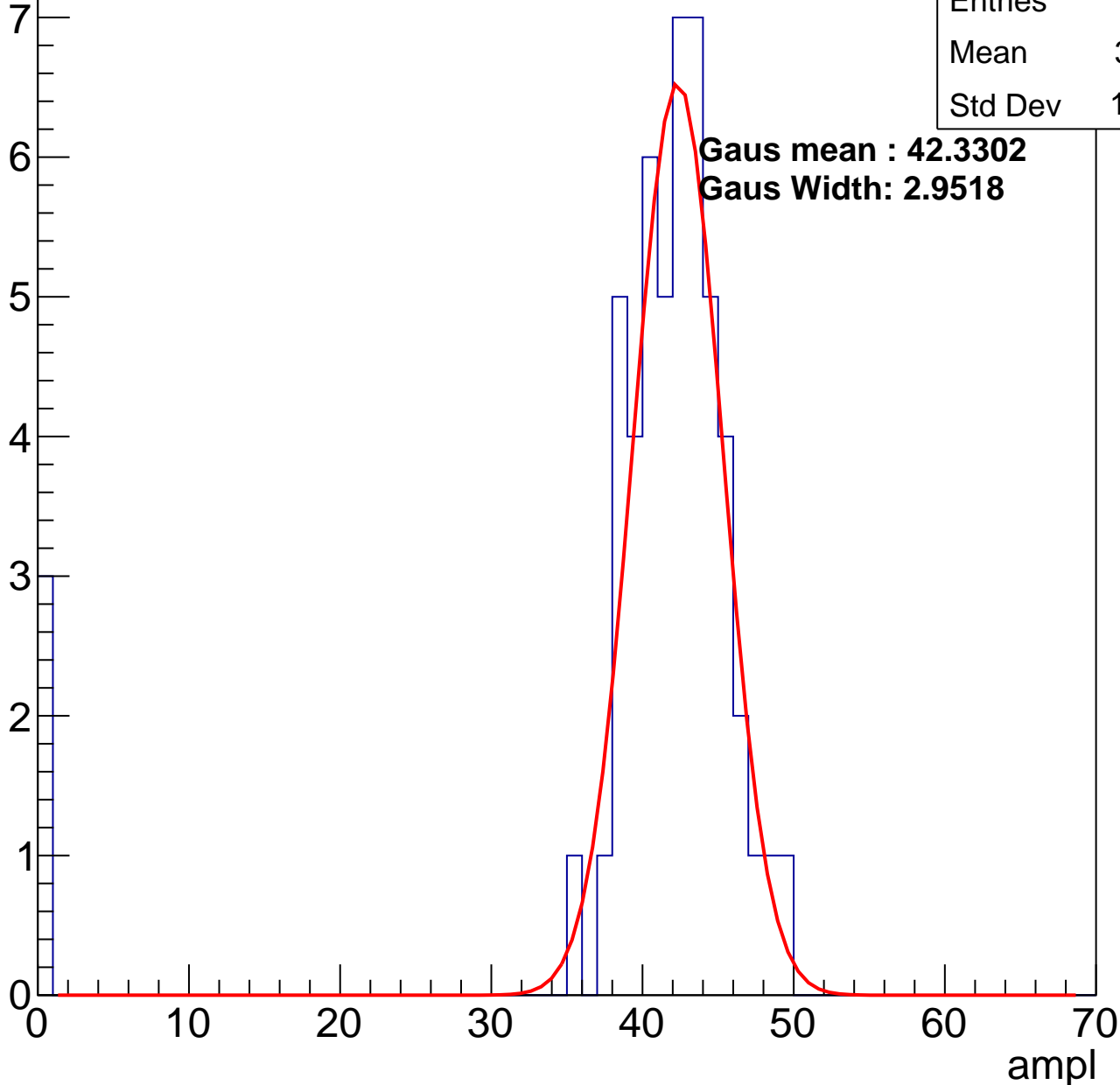
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	39.51
Std Dev	10.08

Gaus mean : 42.3302

Gaus Width: 2.9518

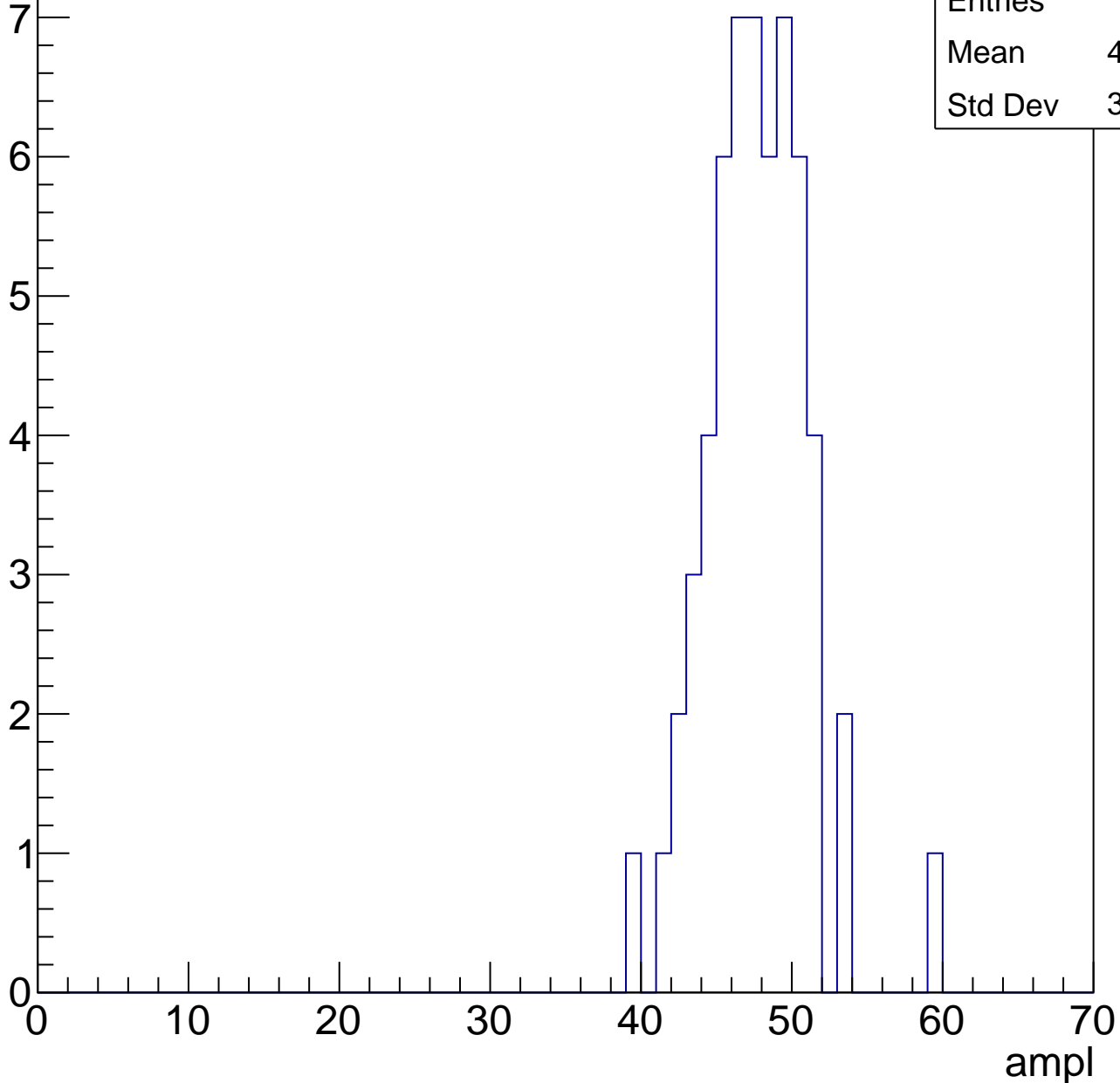


B1L103S, U2-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	47.19
Std Dev	3.327

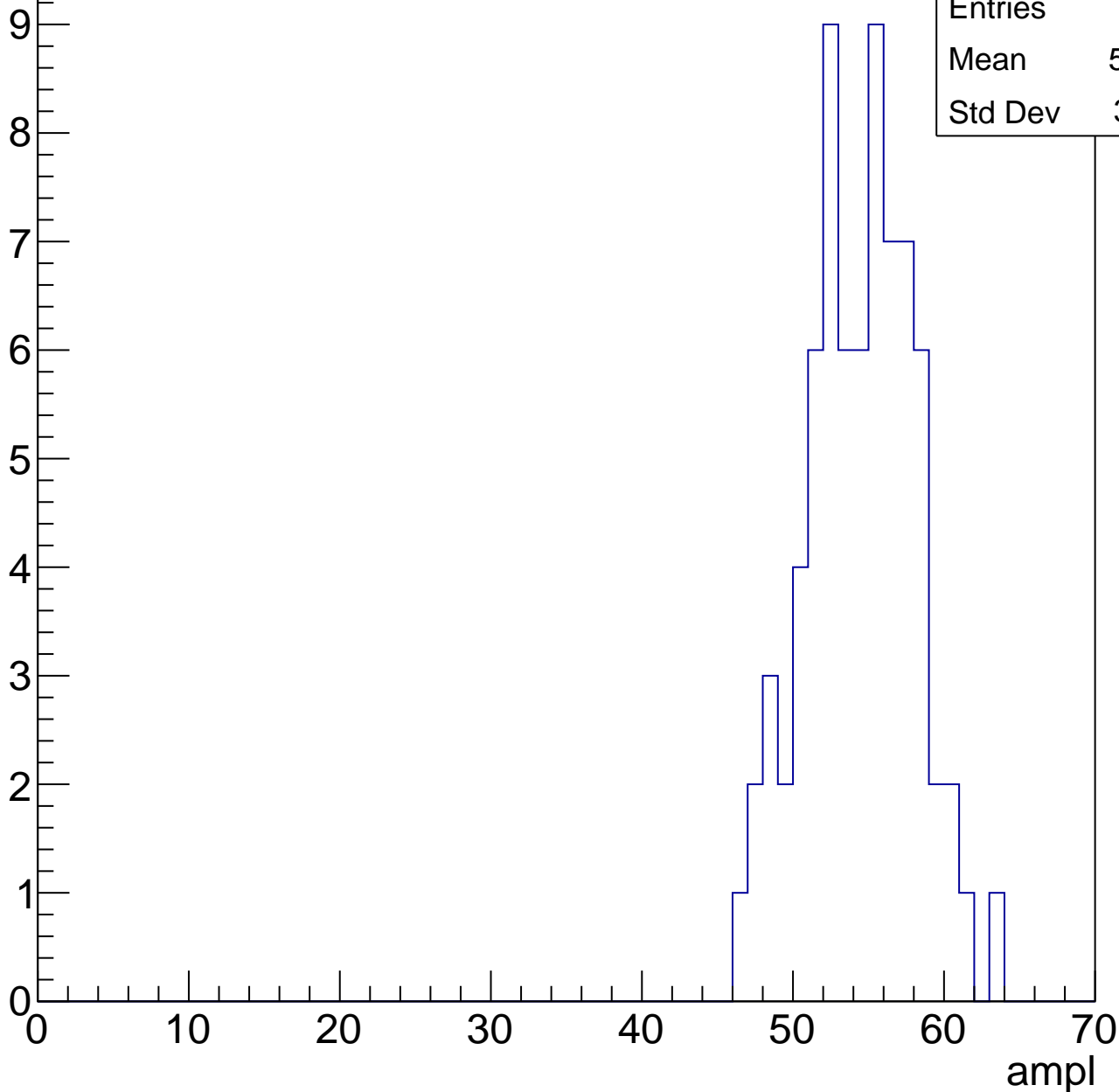


B1L103S, U2-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	53.97
Std Dev	3.541

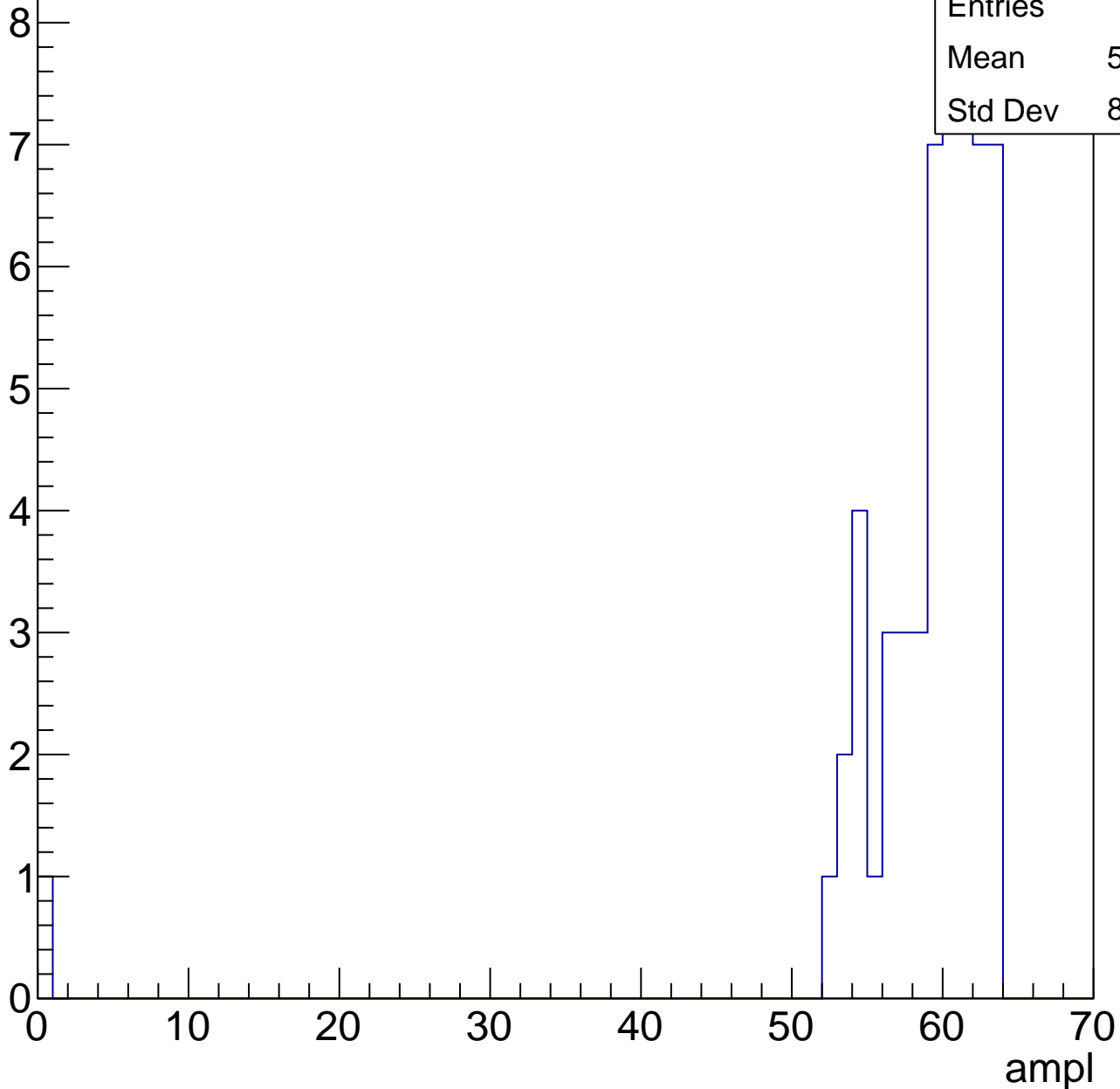


B1L103S, U2-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

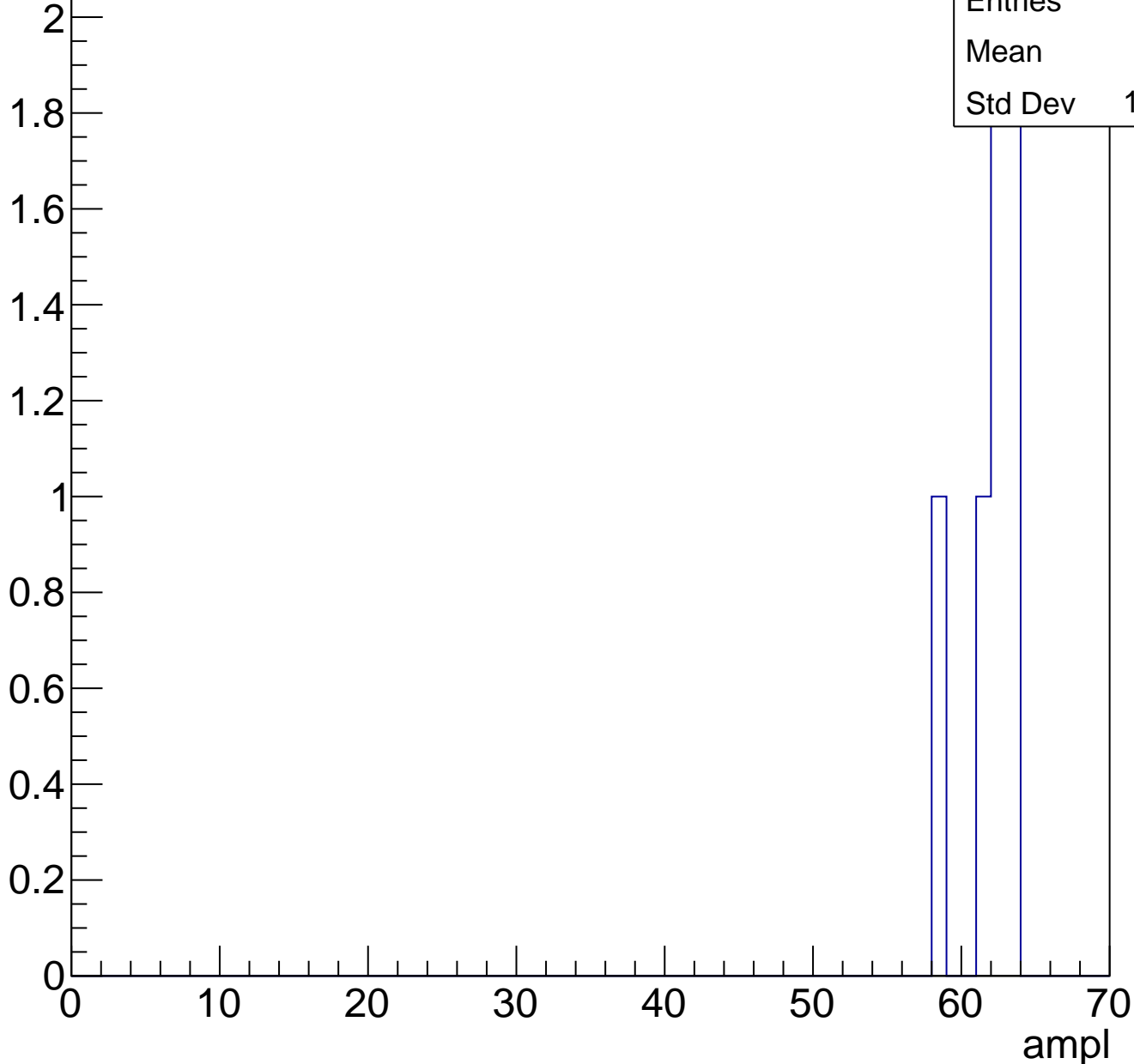
Entries	55
Mean	58.15
Std Dev	8.458



B1L103S, U2-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



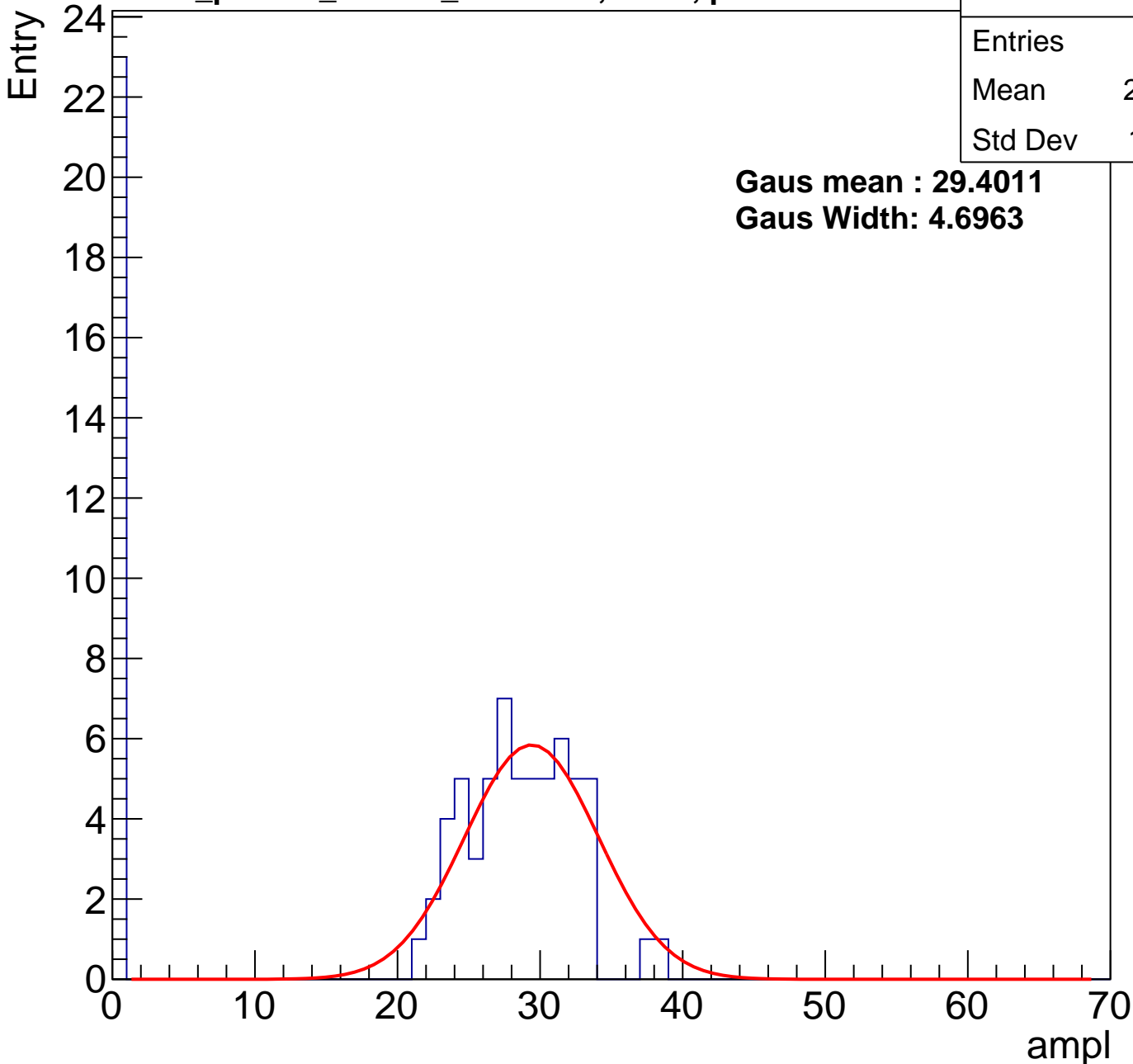
B1L103S, U2-ch109, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	20.39
Std Dev	13.01

Gaus mean : 29.4011

Gaus Width: 4.6963



B1L103S, U2-ch109, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	31.52
Std Dev	10.79

Gaus mean : 35.2920

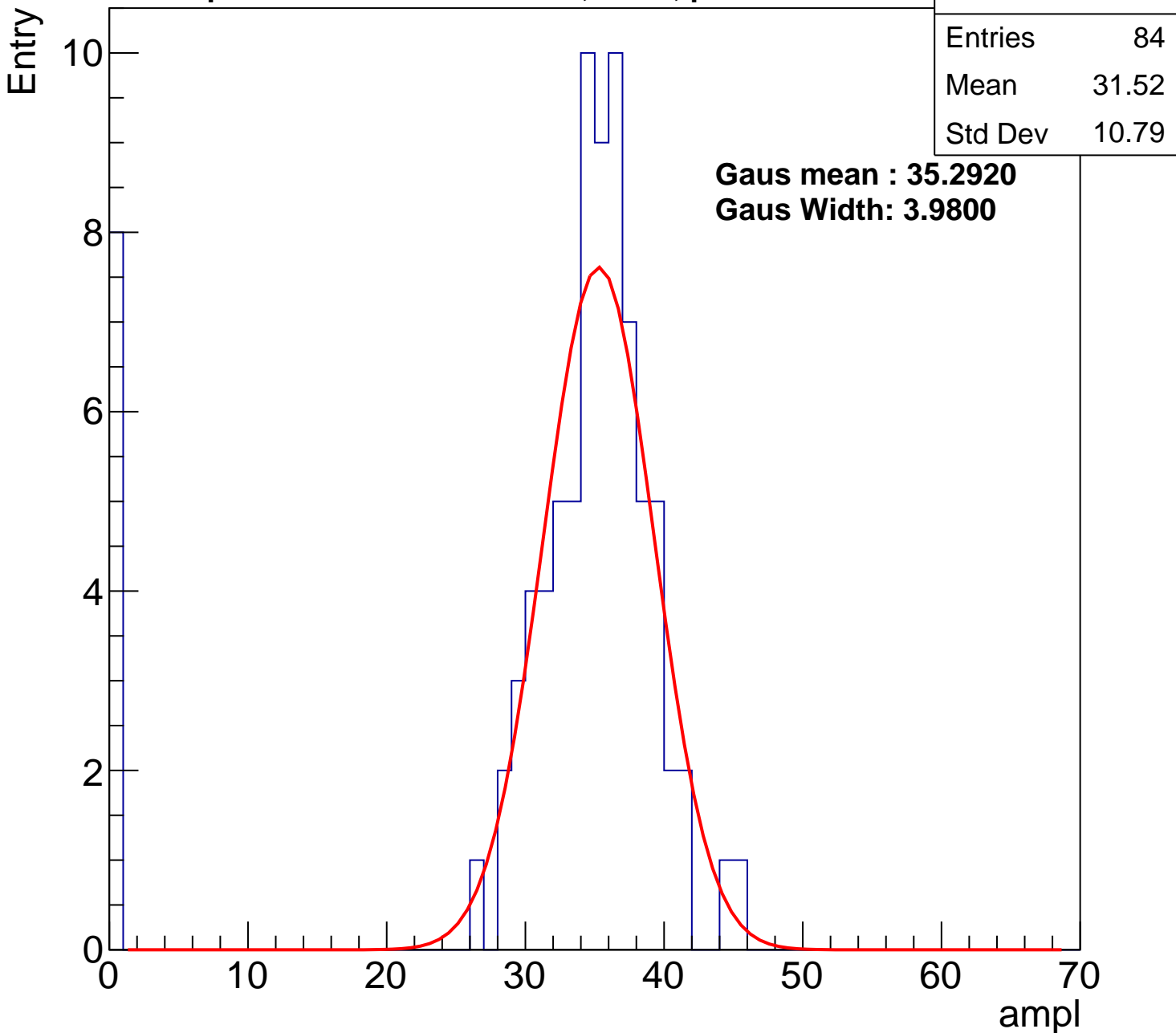
Gaus Width: 3.9800

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch109, adc2

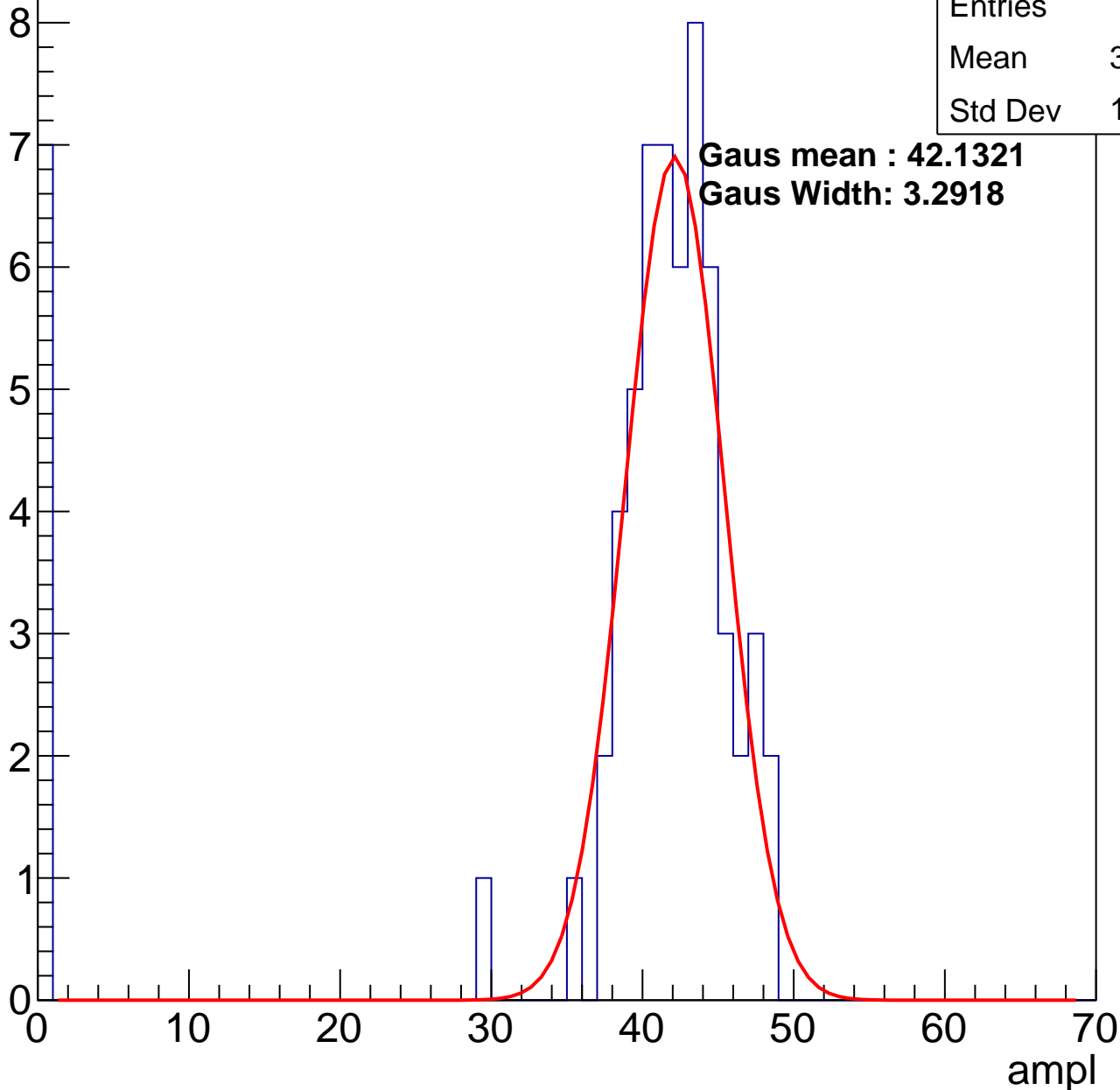
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.12
Std Dev	13.39

Gaus mean : 42.1321

Gaus Width: 3.2918

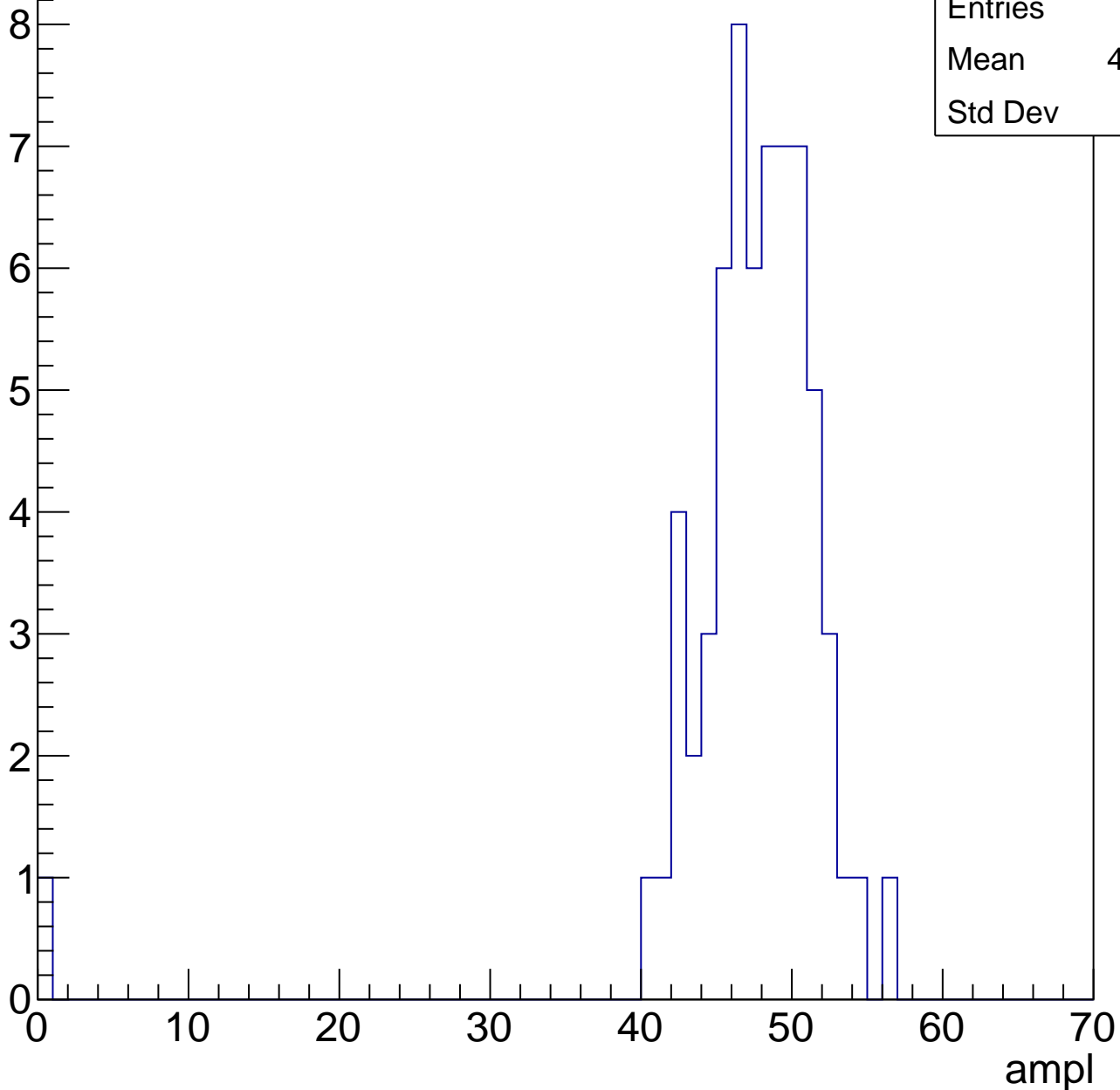


B1L103S, U2-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	46.72
Std Dev	6.73

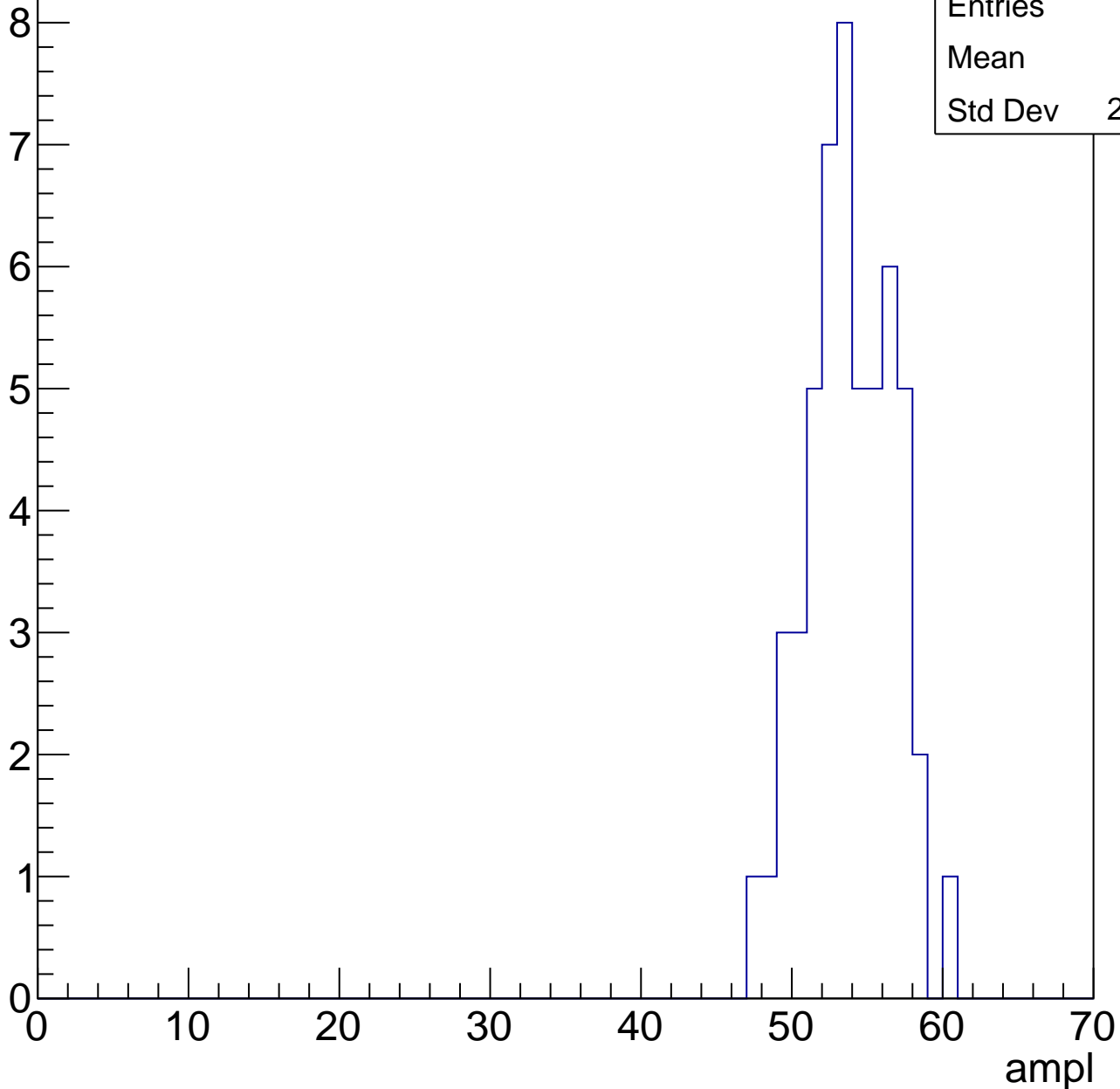


B1L103S, U2-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.4
Std Dev	2.817

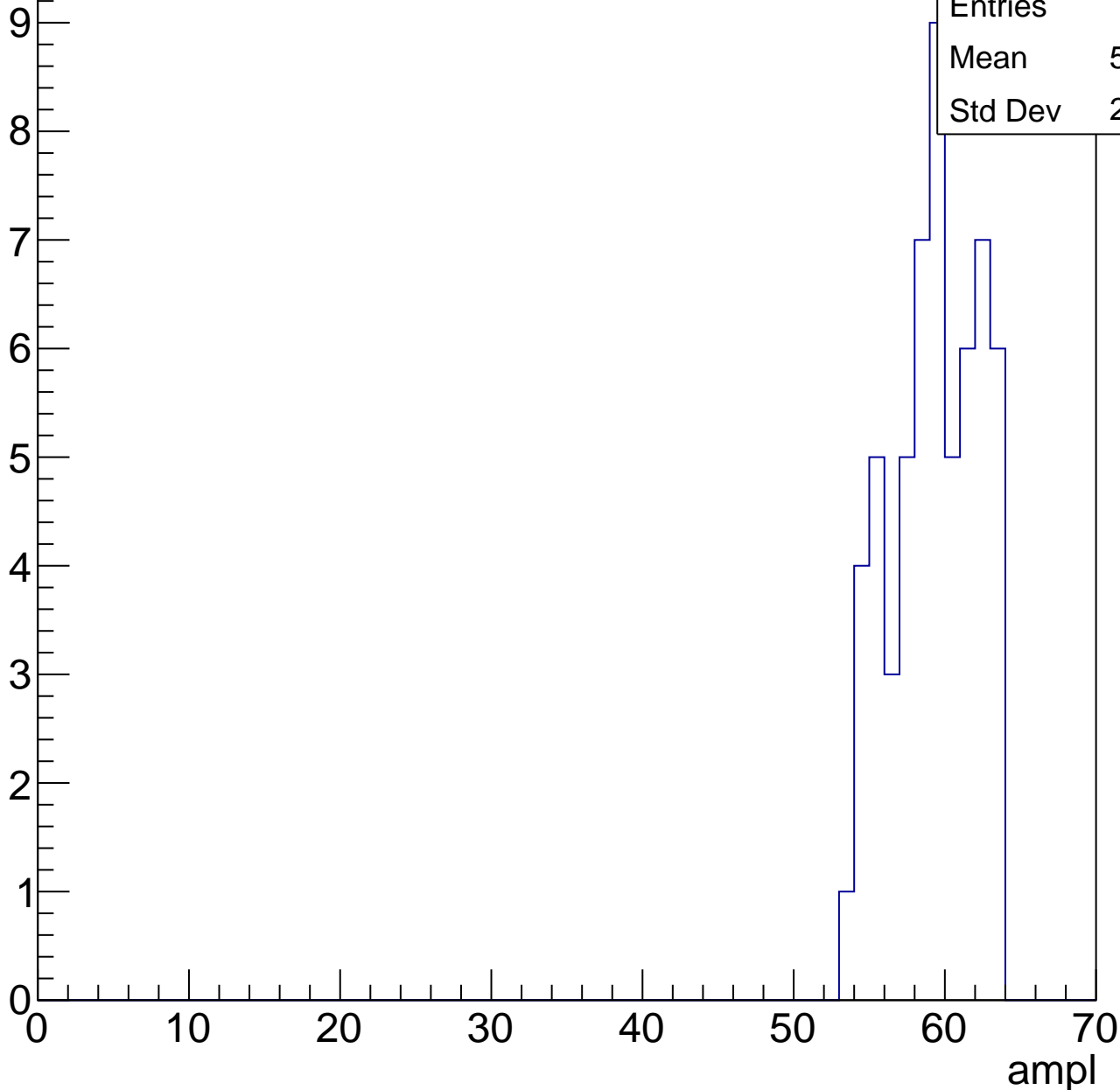


B1L103S, U2-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

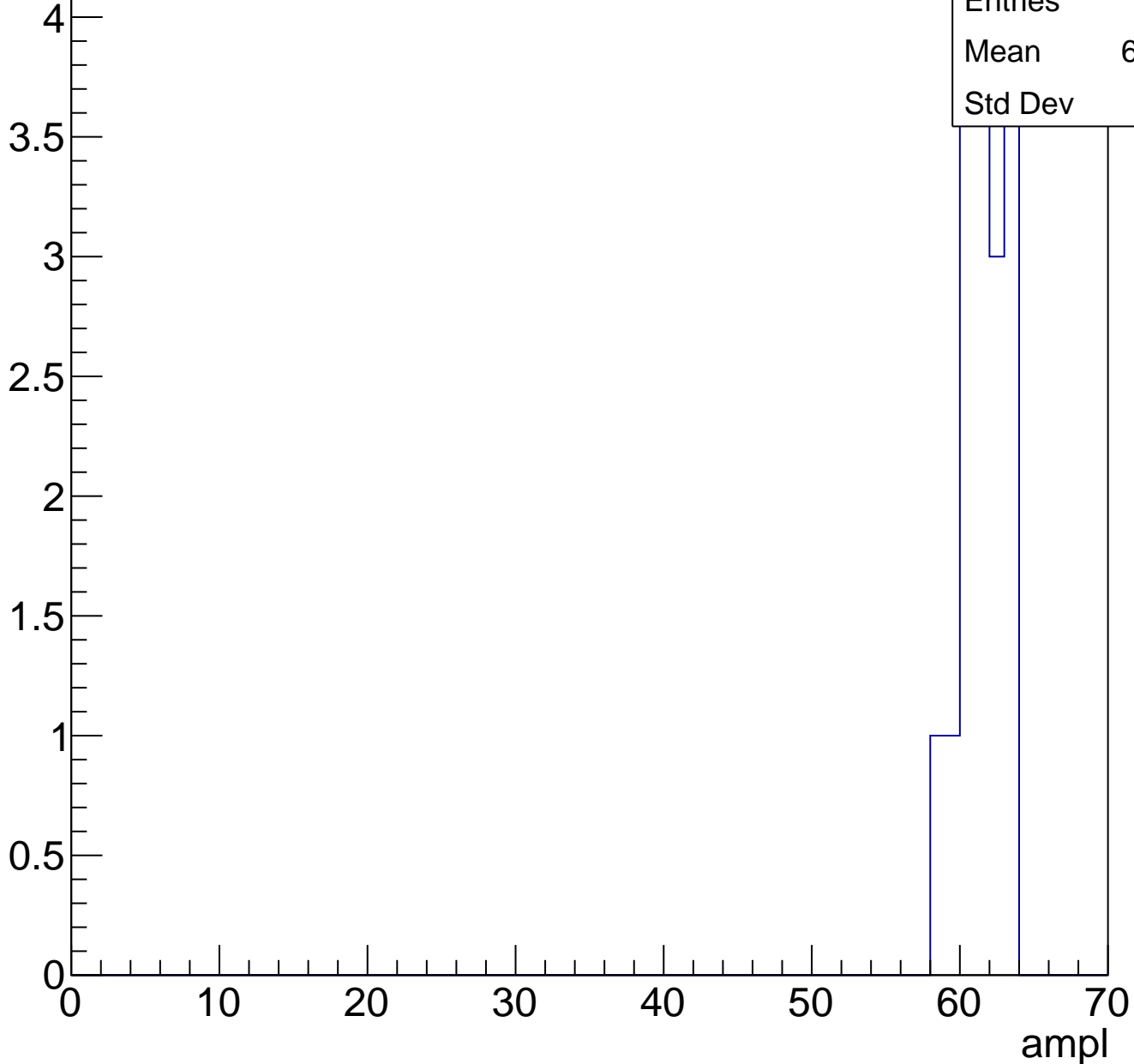
Entries	58
Mean	58.83
Std Dev	2.805



B1L103S, U2-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U2-ch110, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	18.36
Std Dev	12.37

Gaus mean : 26.5711

Gaus Width: 3.8528

Entry

30

25

20

15

10

5

0

0

10

20

30

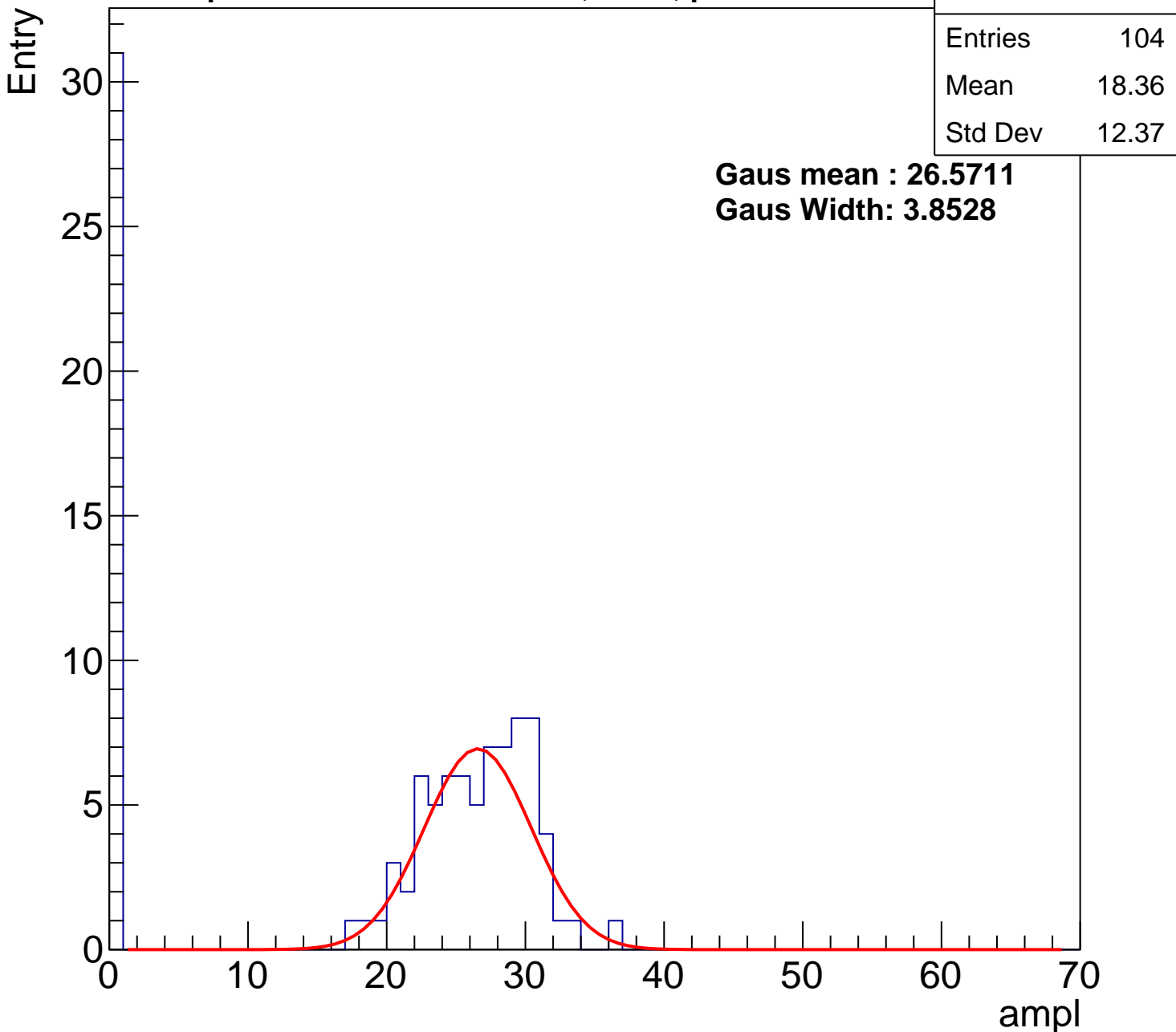
40

50

60

70

ampl



B1L103S, U2-ch110, adc1

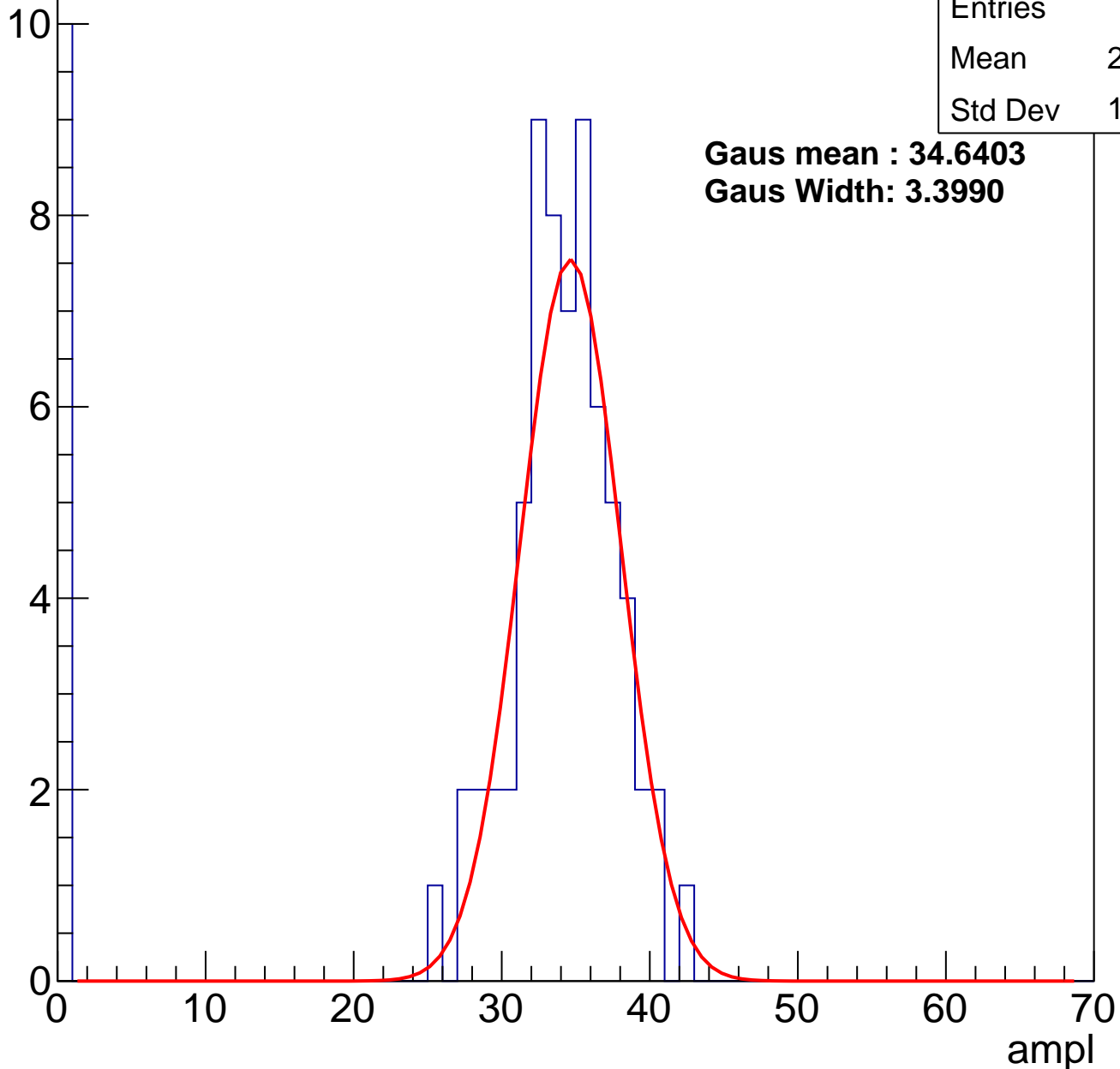
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	29.43
Std Dev	11.79

Gaus mean : 34.6403

Gaus Width: 3.3990

Entry



B1L103S, U2-ch110, adc2

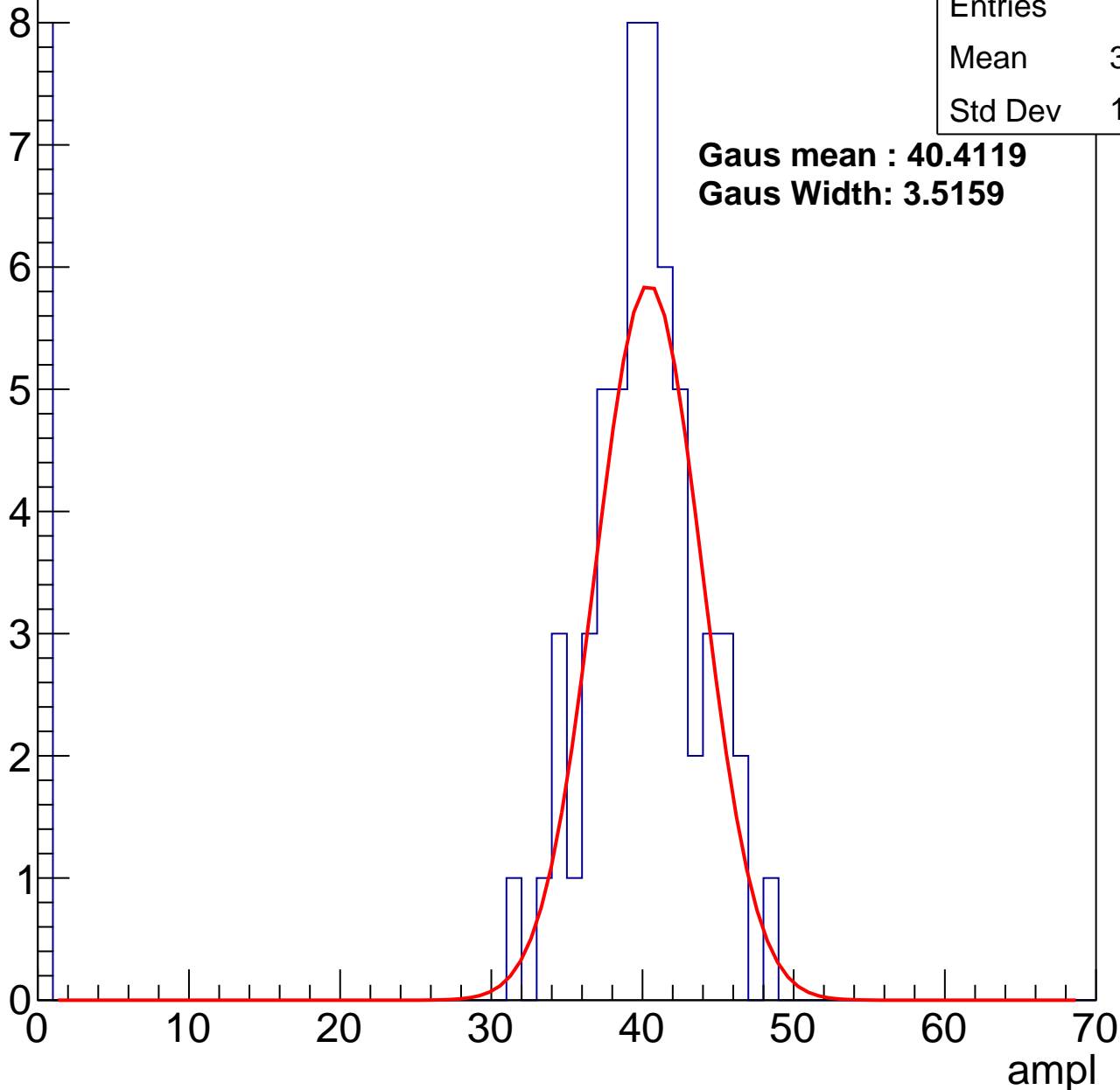
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	34.85
Std Dev	13.45

Gaus mean : 40.4119

Gaus Width: 3.5159

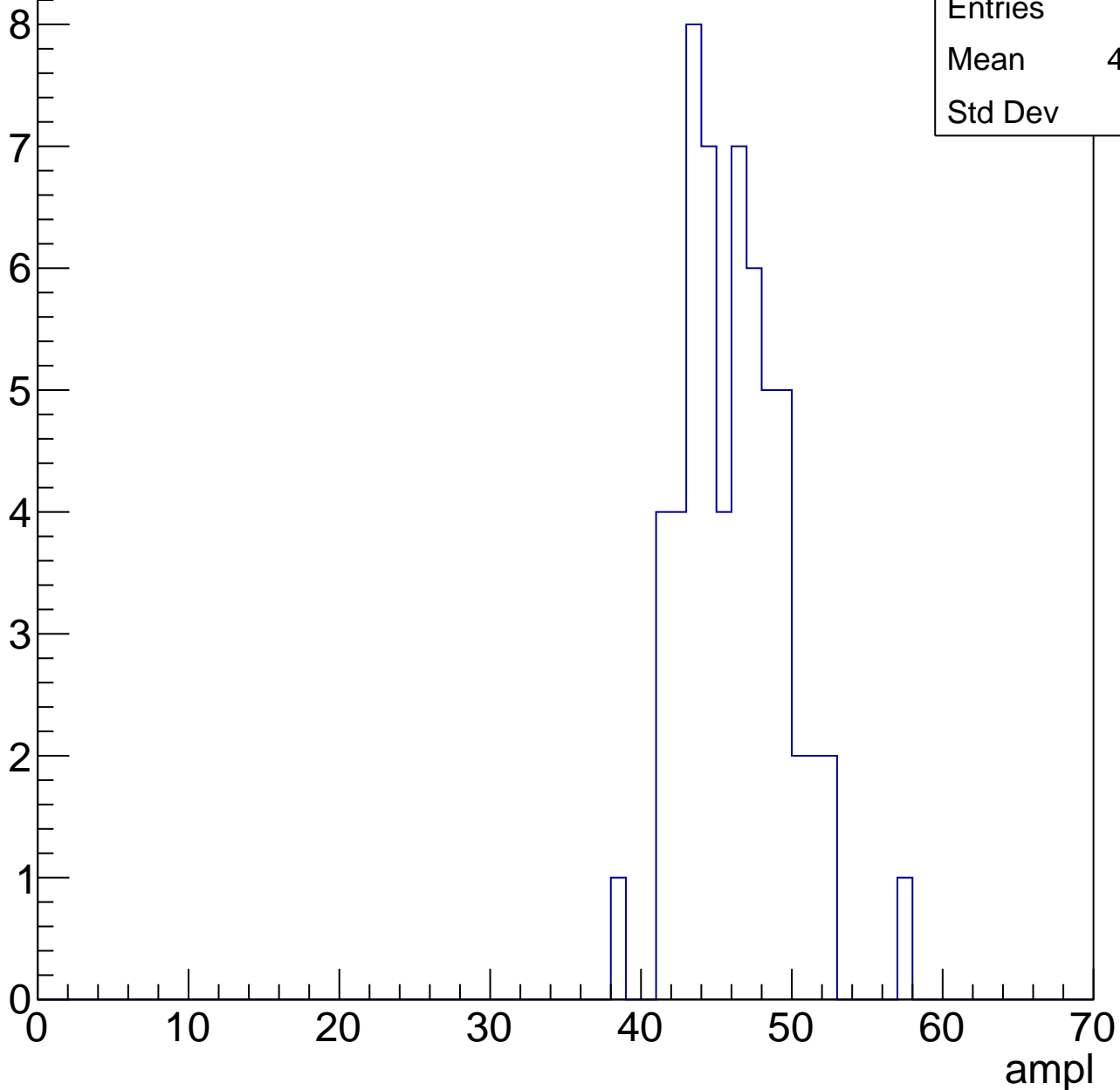


B1L103S, U2-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	45.76
Std Dev	3.41

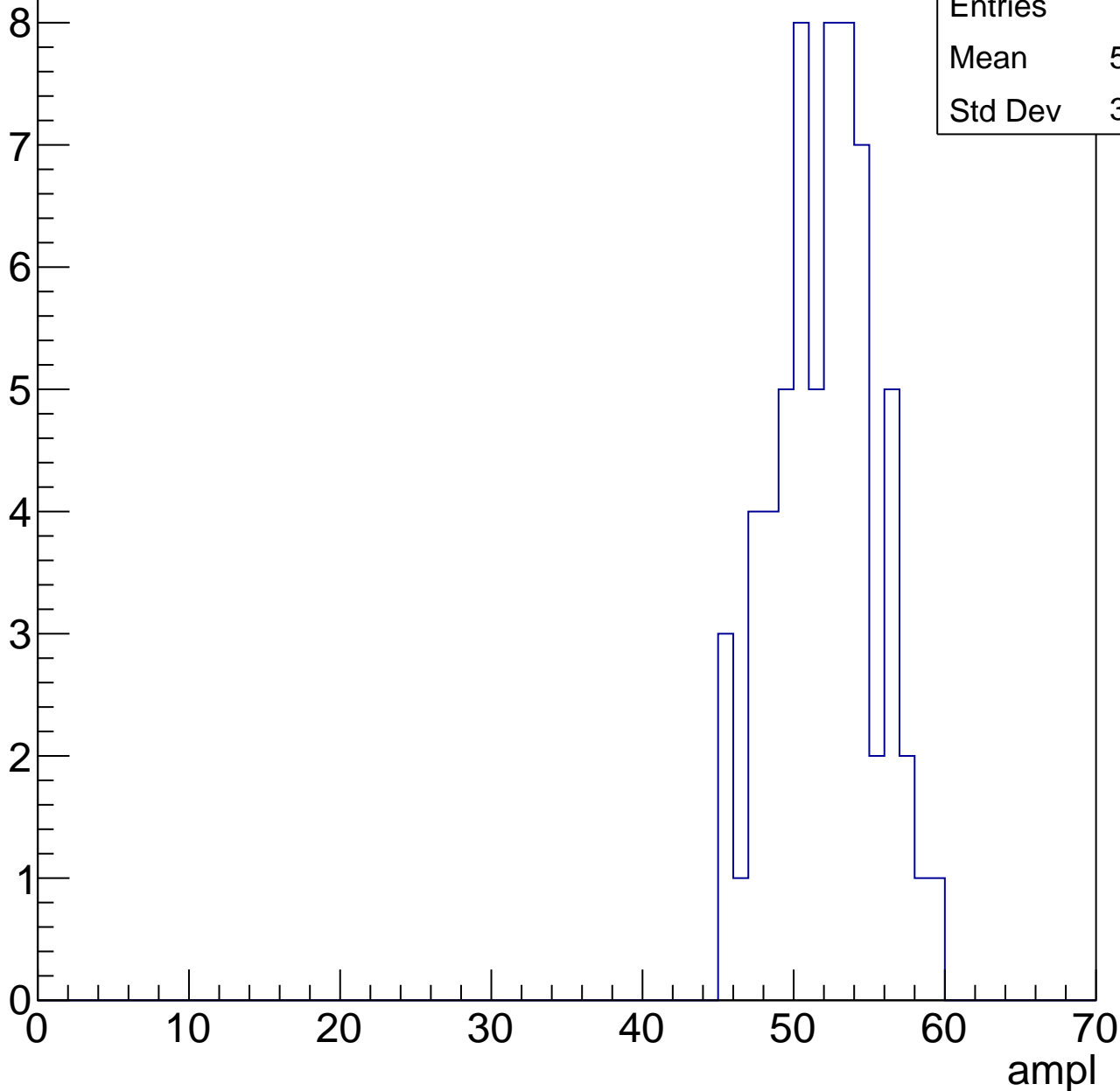


B1L103S, U2-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	51.56
Std Dev	3.273

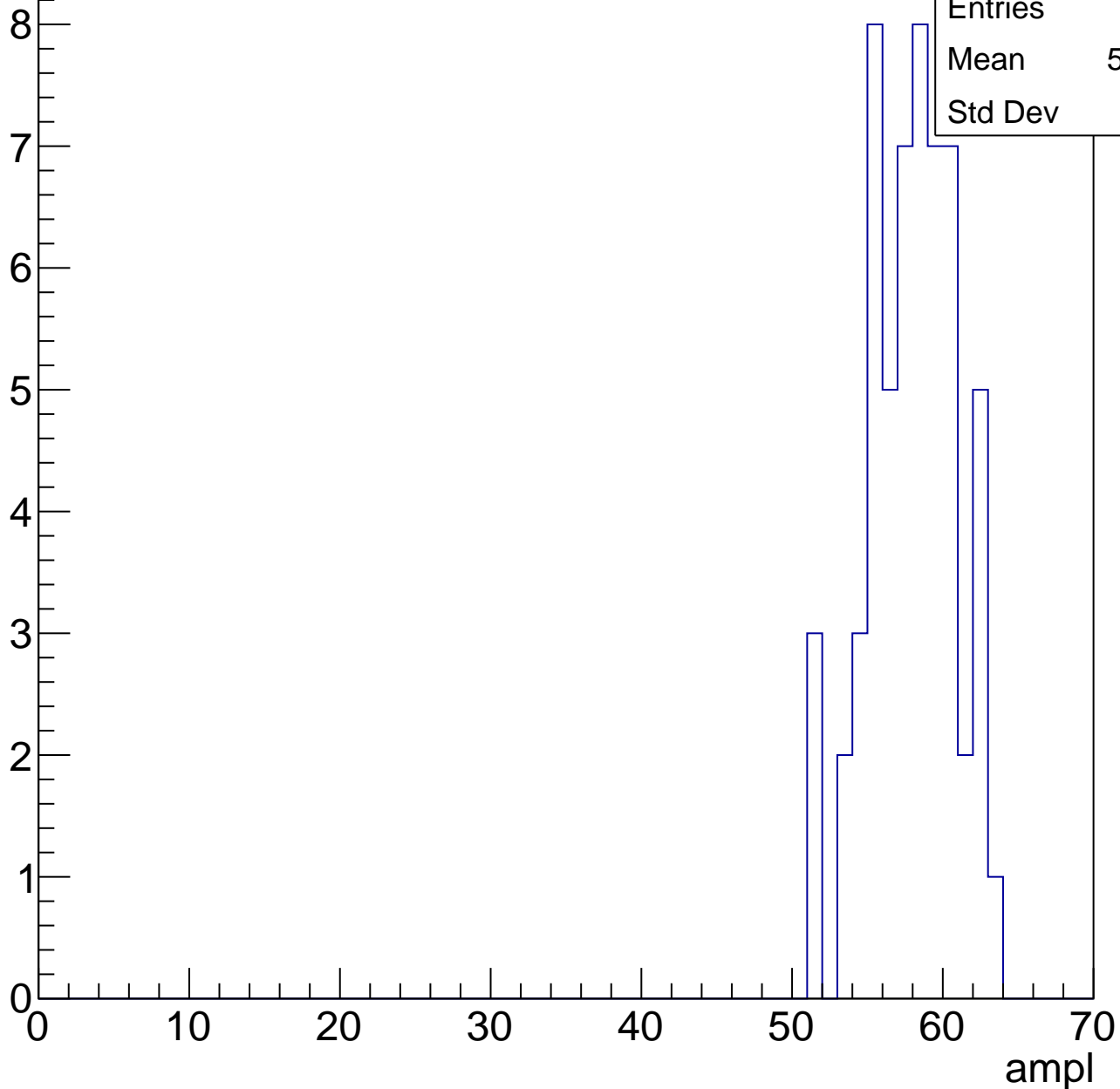


B1L103S, U2-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.45
Std Dev	2.89

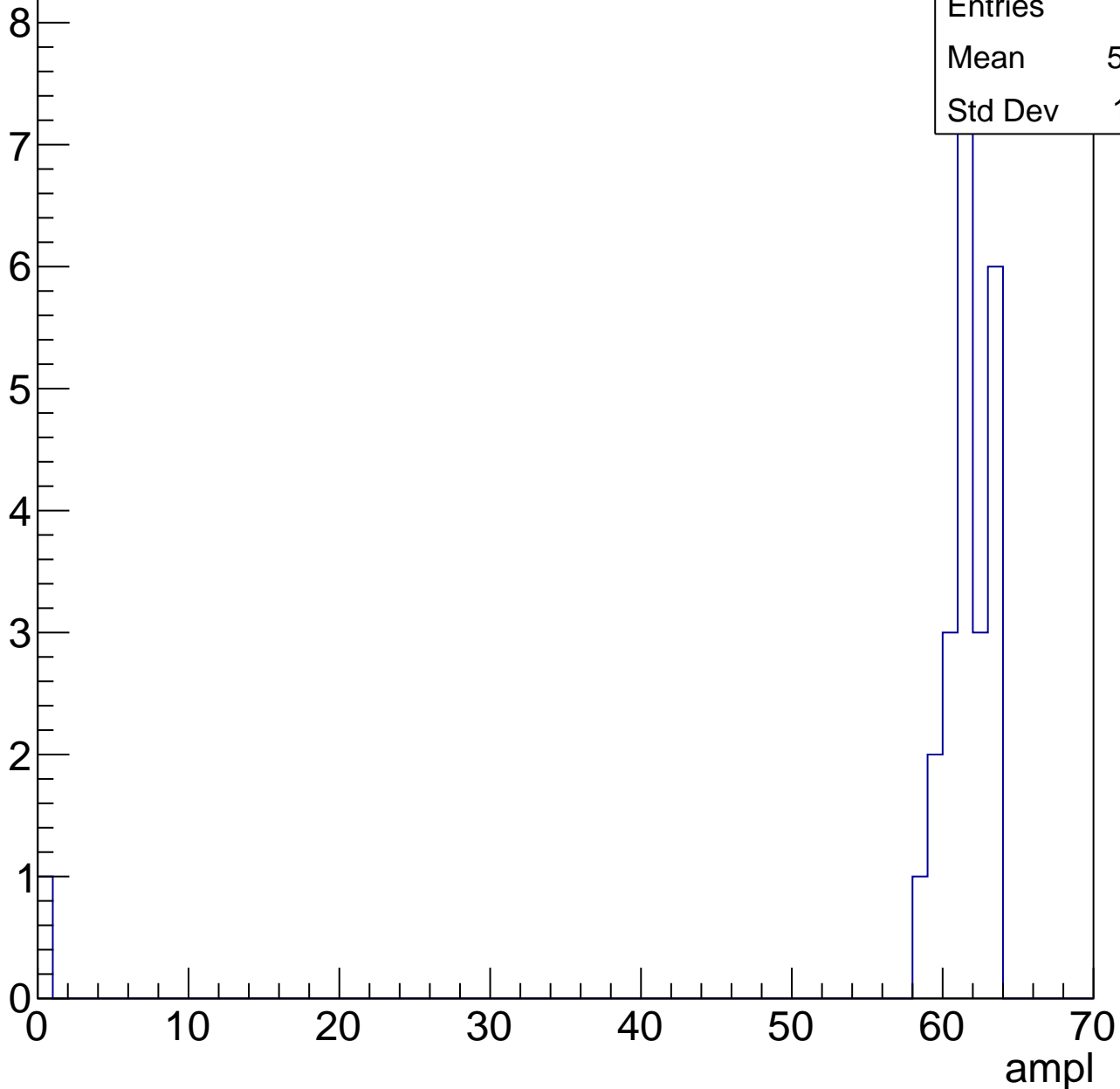


B1L103S, U2-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.67
Std Dev	12.31

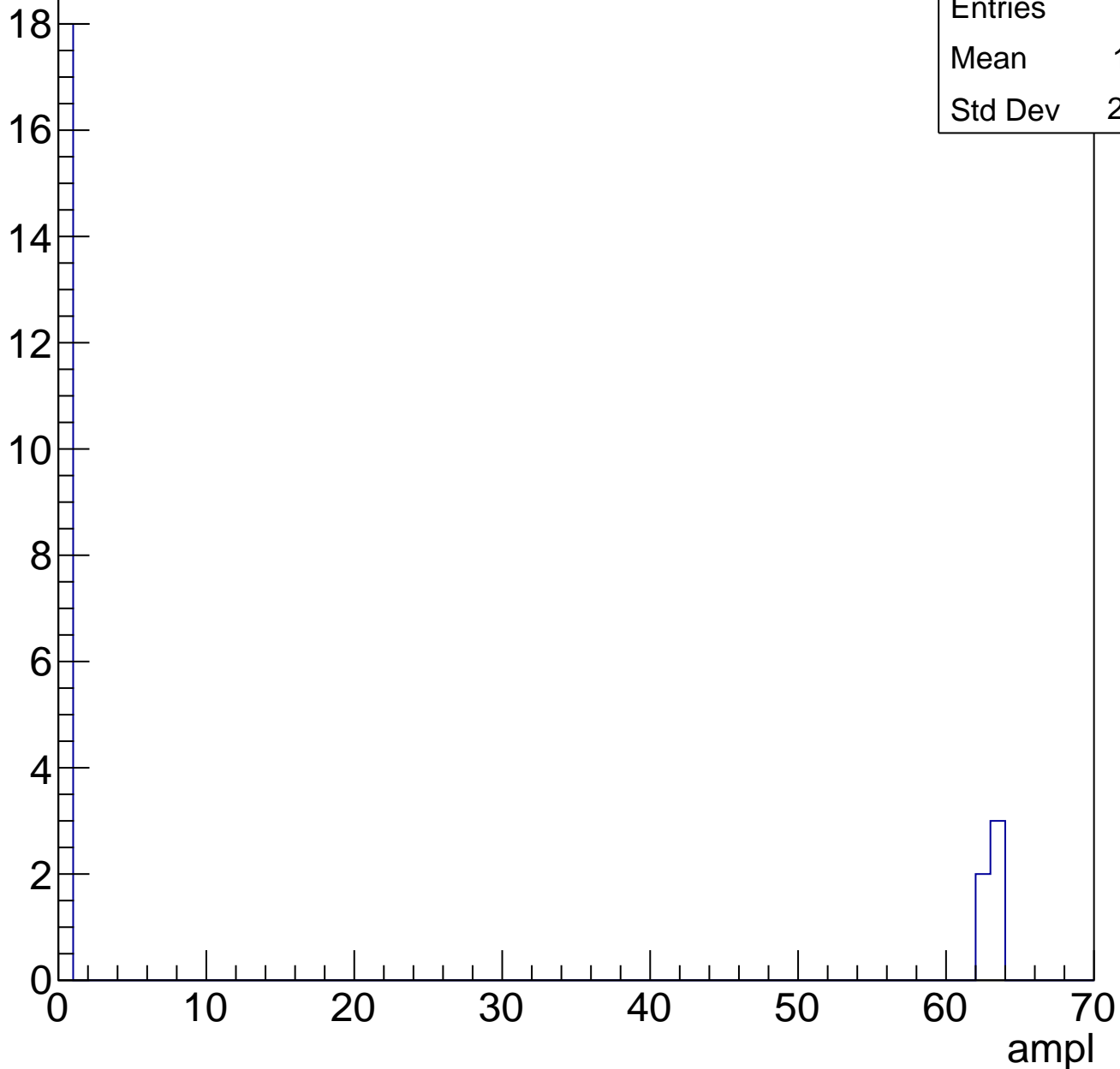


B1L103S, U2-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	13.61
Std Dev	25.82

Entry



B1L103S, U2-ch111, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	21.38
Std Dev	12.63

Gaus mean : 29.1592

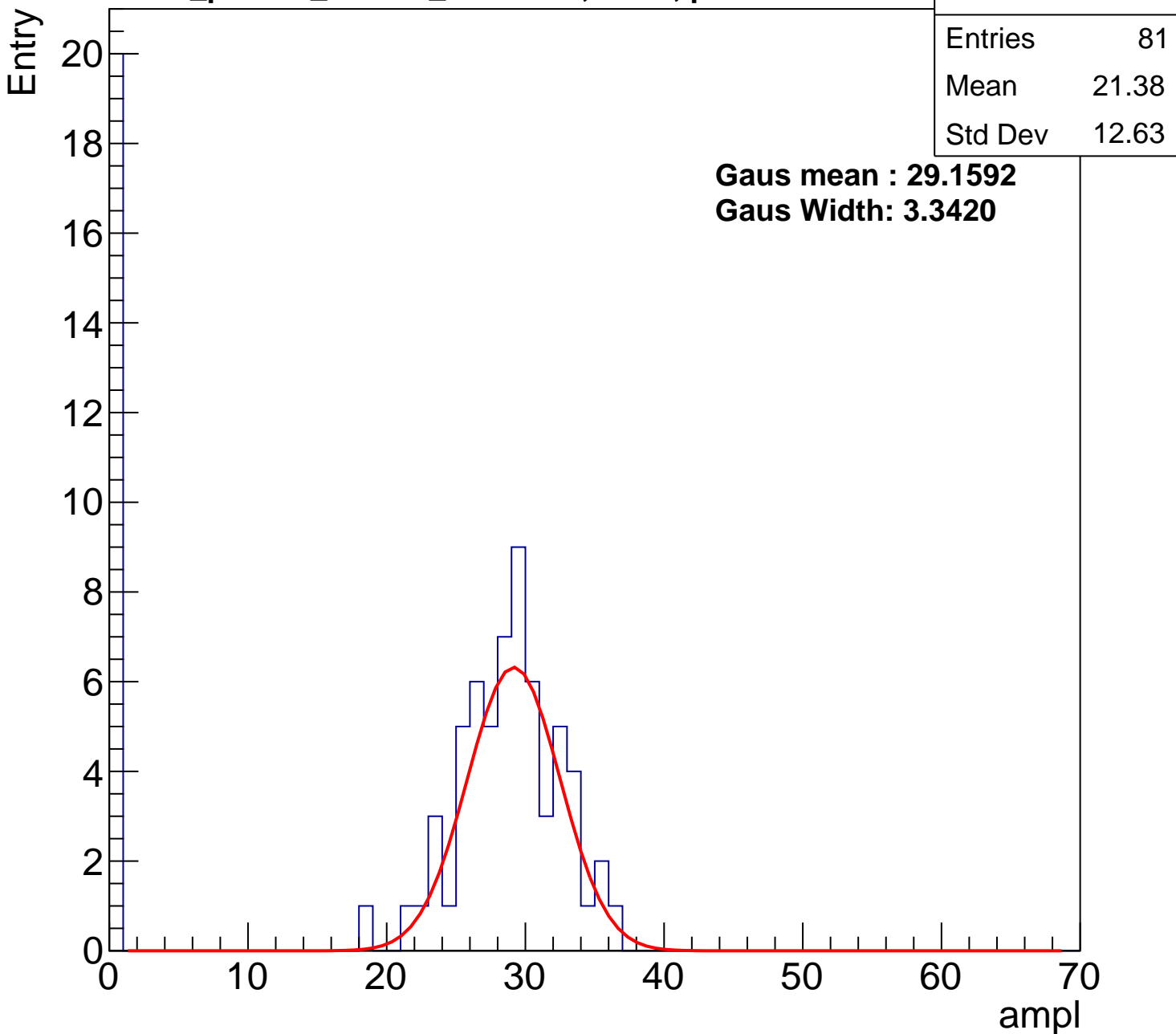
Gaus Width: 3.3420

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch111, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	28.39
Std Dev	13.67

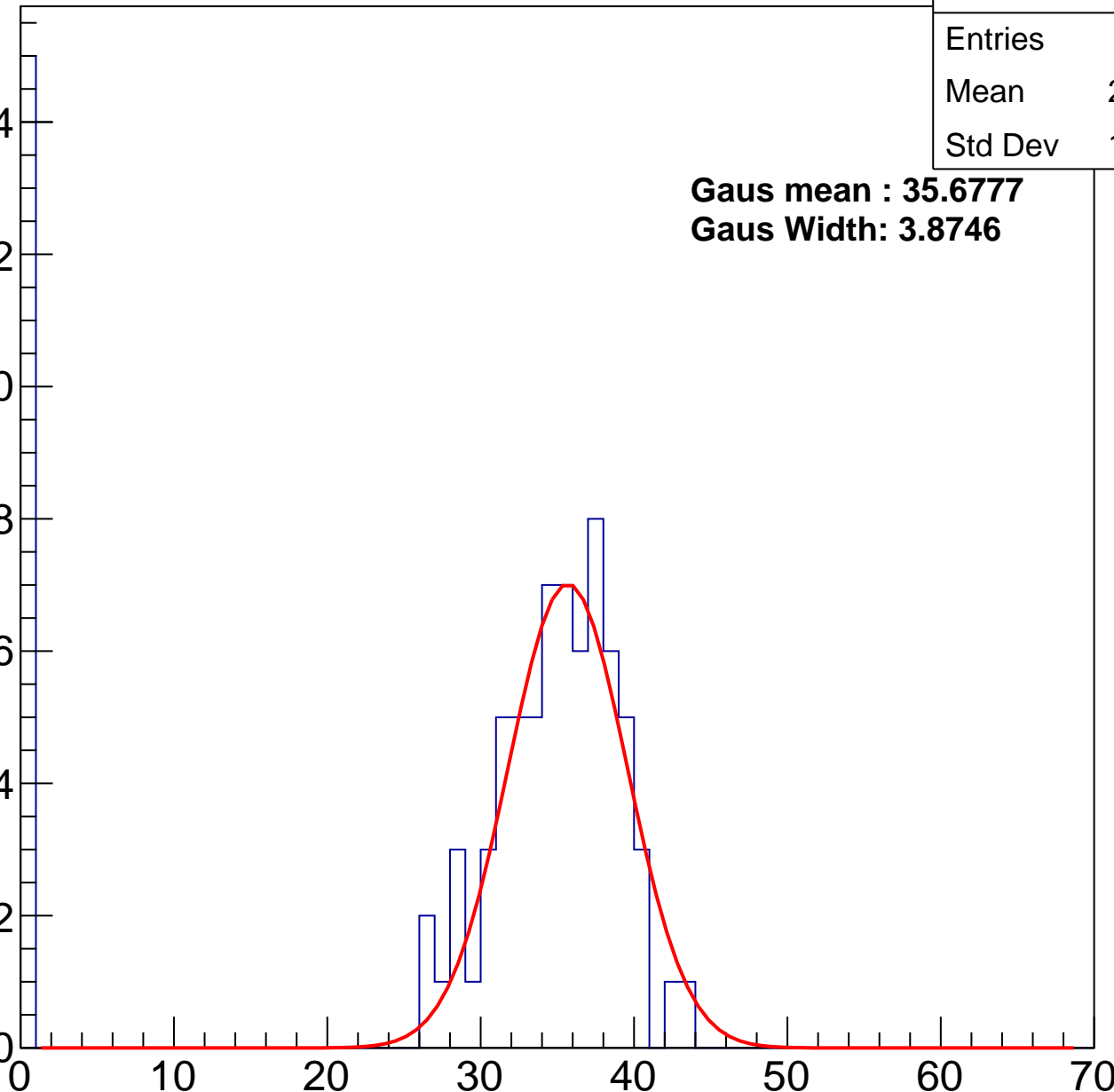
Gaus mean : 35.6777

Gaus Width: 3.8746

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch111, adc2

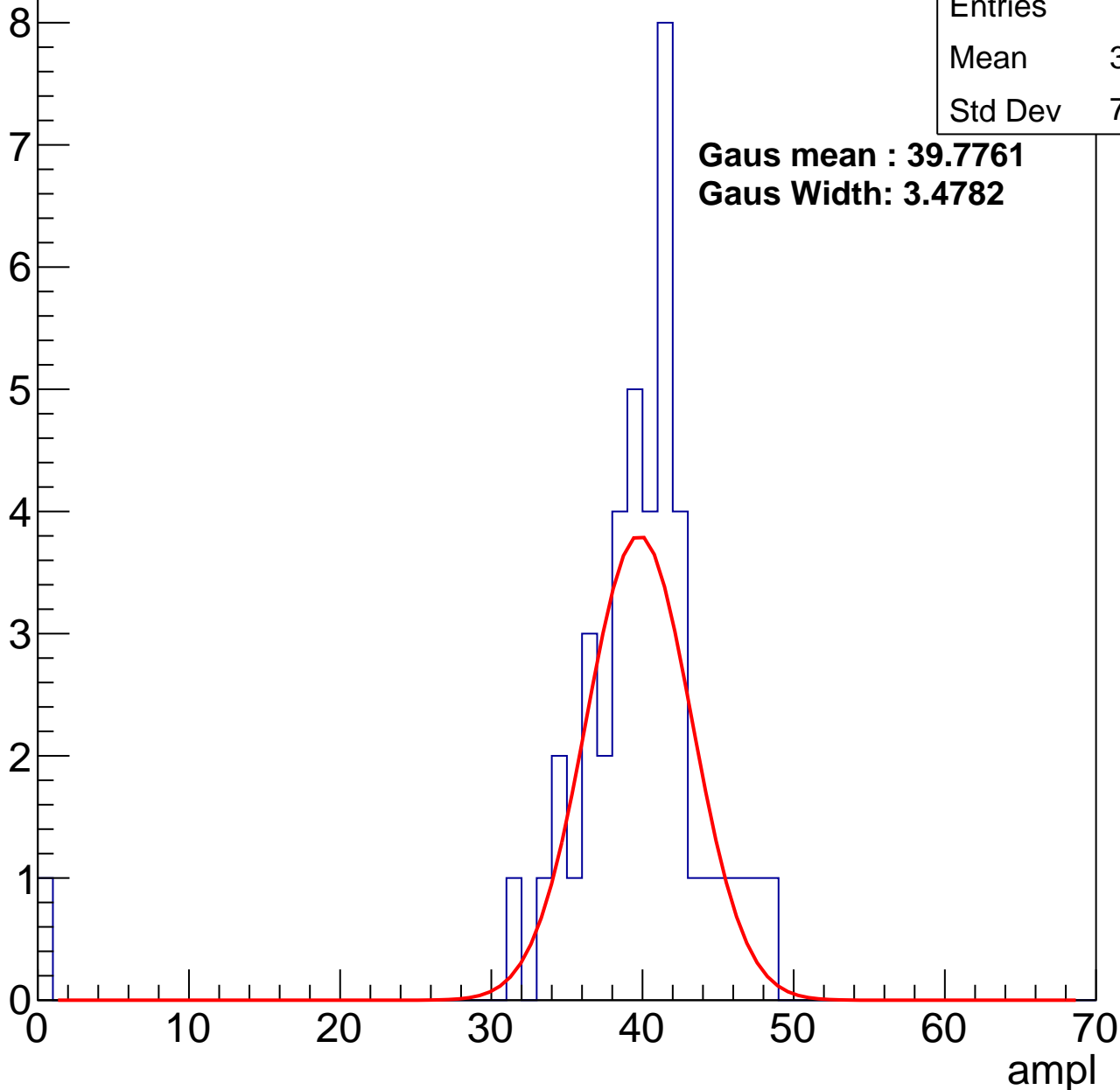
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	38.69
Std Dev	7.012

Gaus mean : 39.7761

Gaus Width: 3.4782

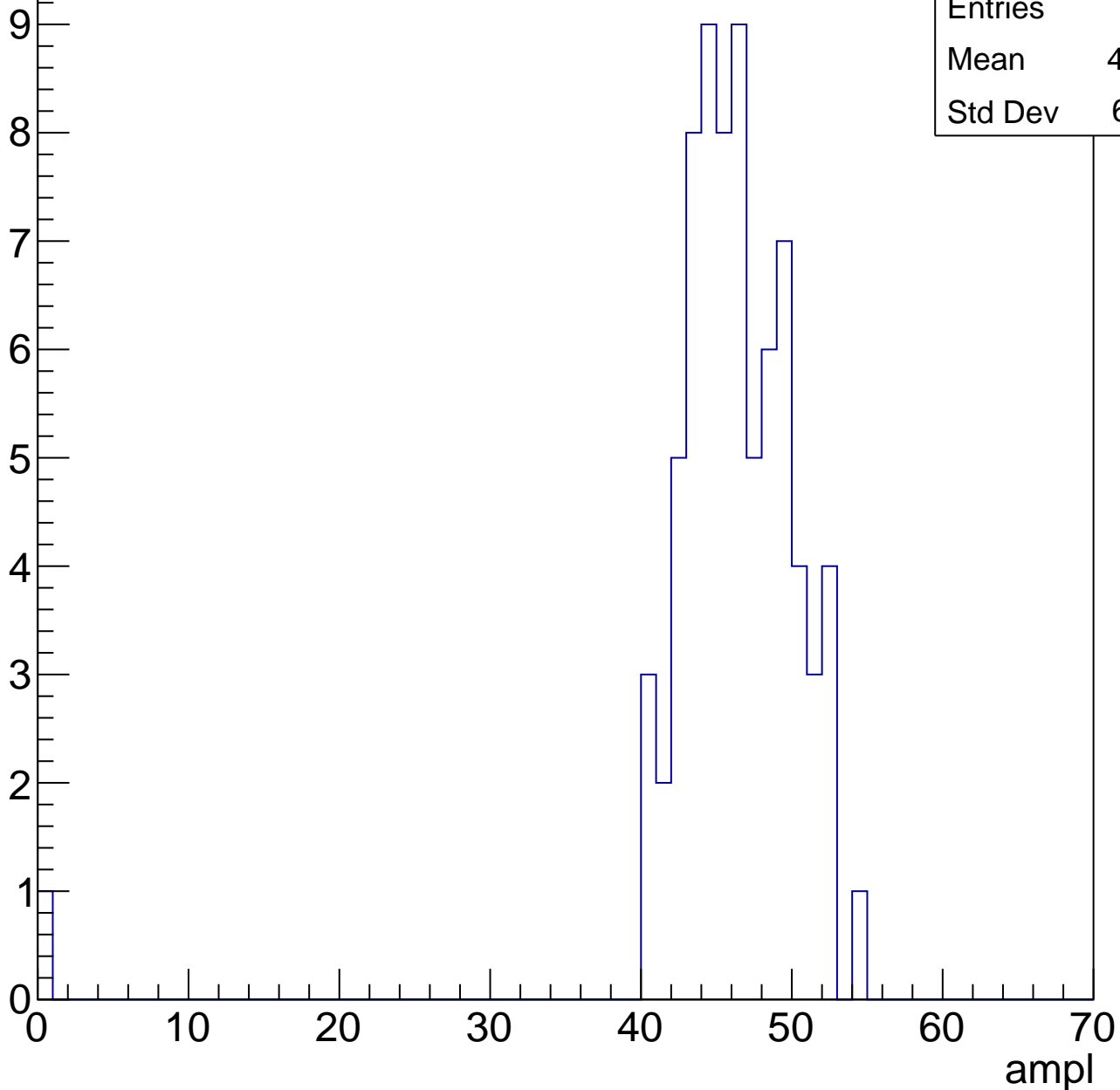


B1L103S, U2-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	45.43
Std Dev	6.201

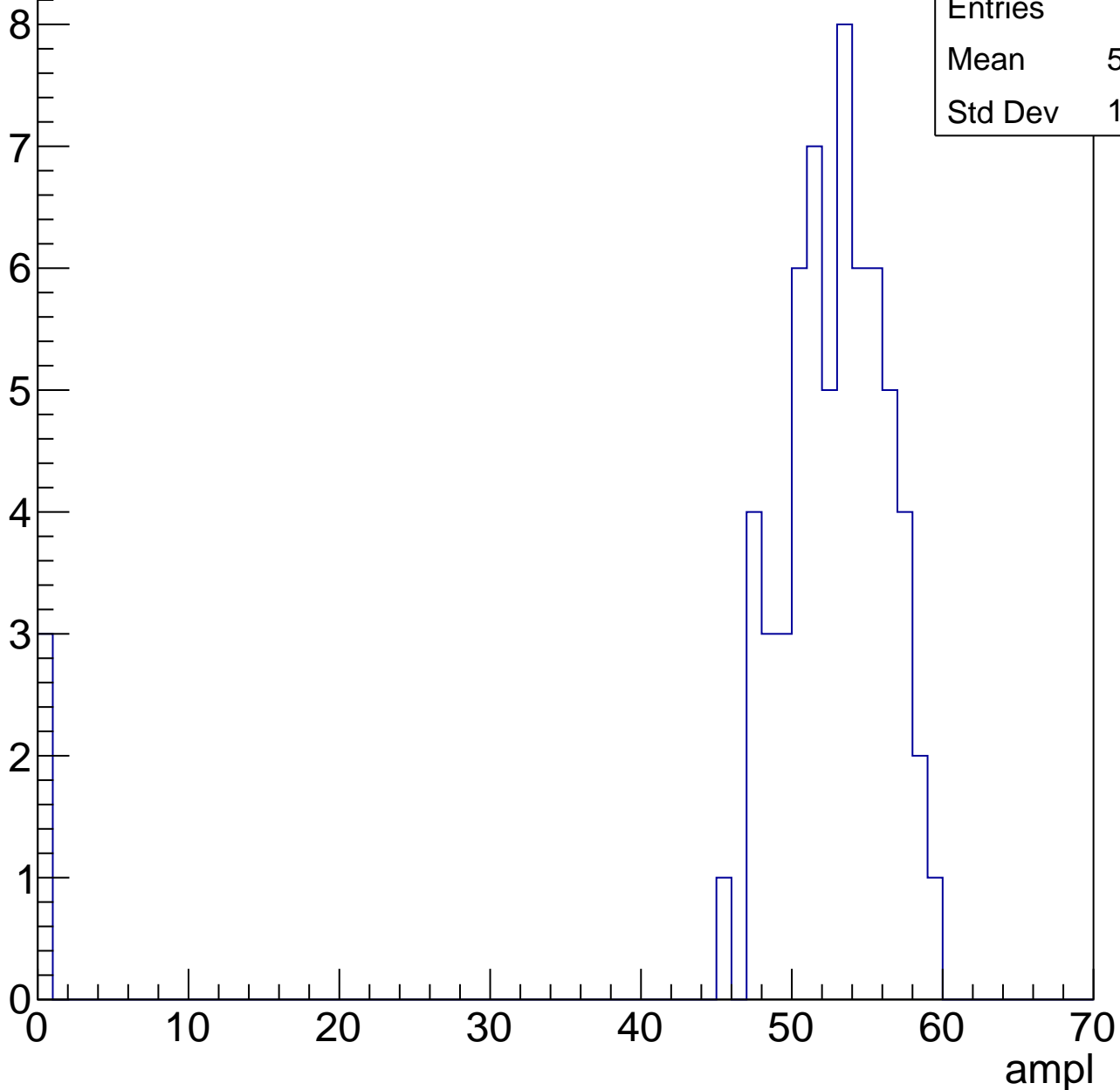


B1L103S, U2-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	50.03
Std Dev	11.53

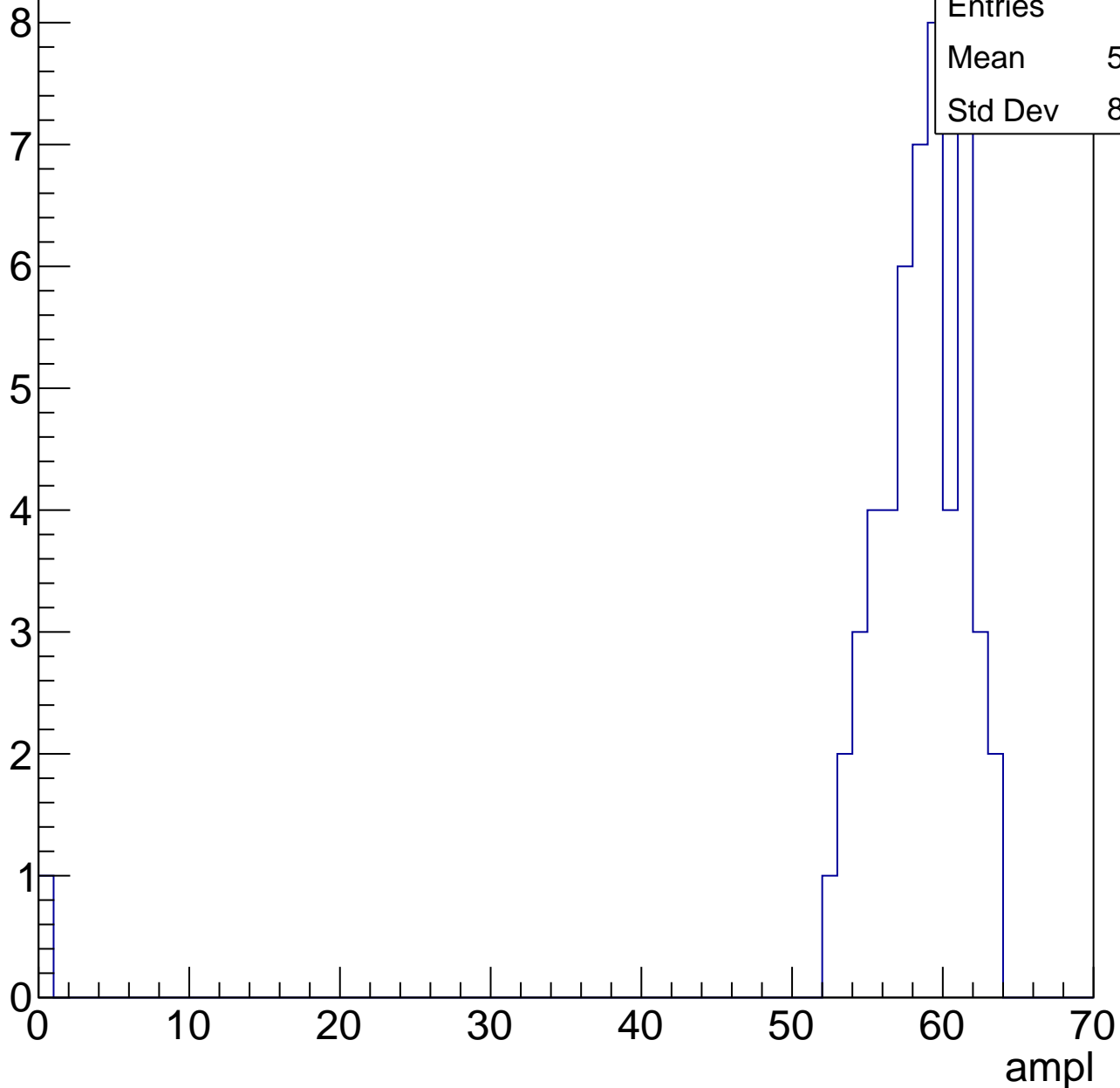


B1L103S, U2-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.06
Std Dev	8.359

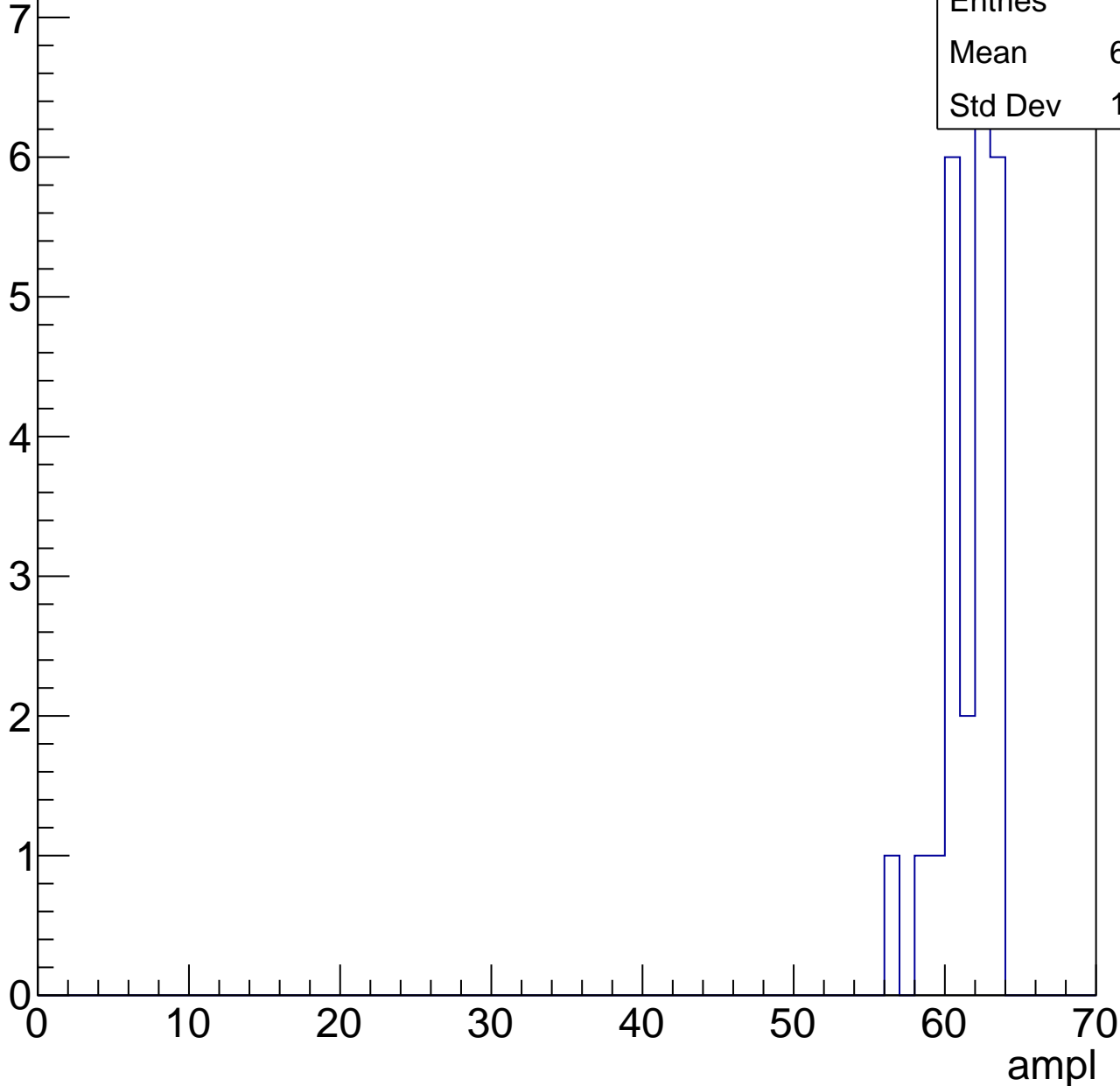


B1L103S, U2-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	61.12
Std Dev	1.763

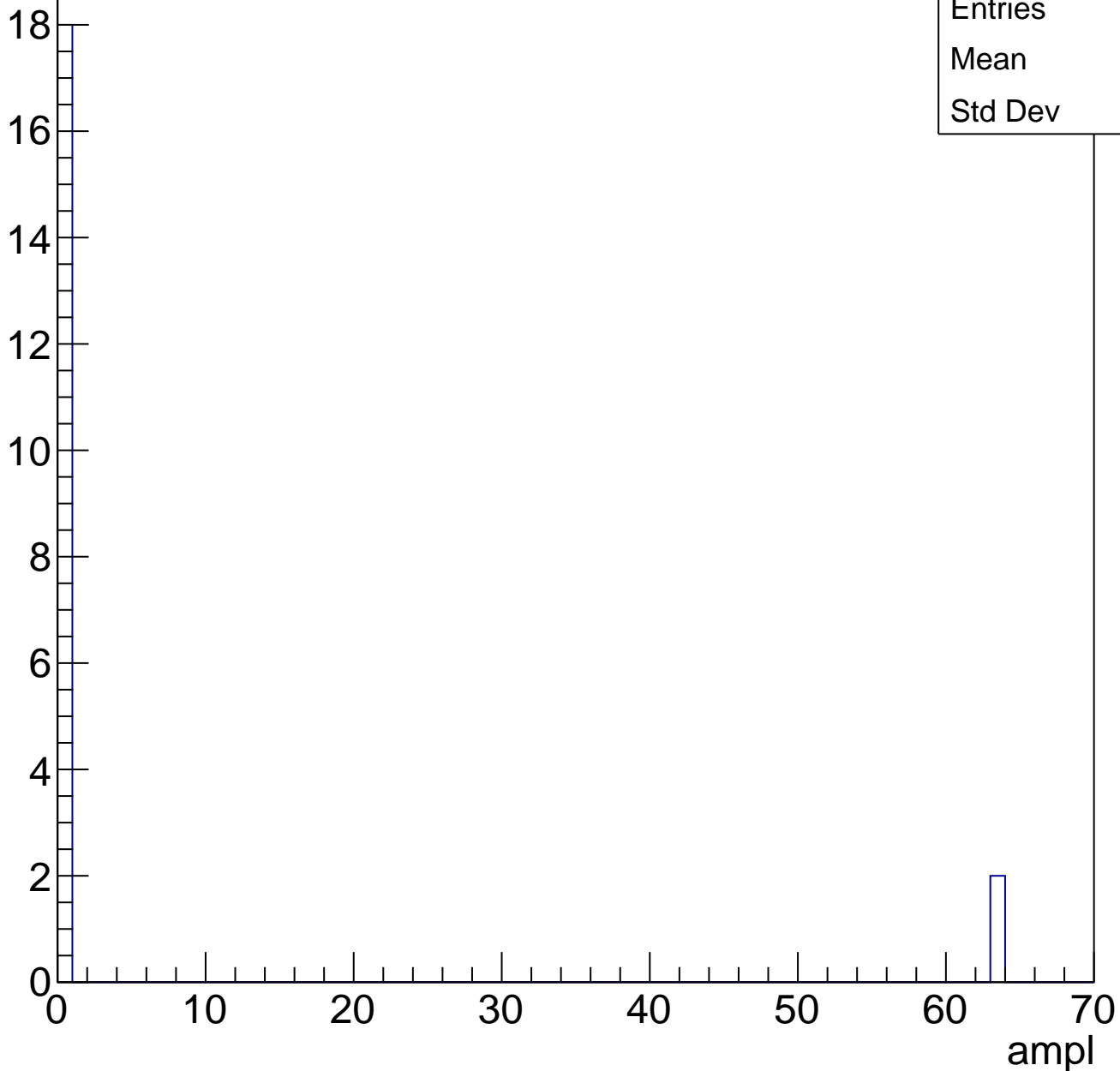


B1L103S, U2-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.3
Std Dev	18.9

Entry



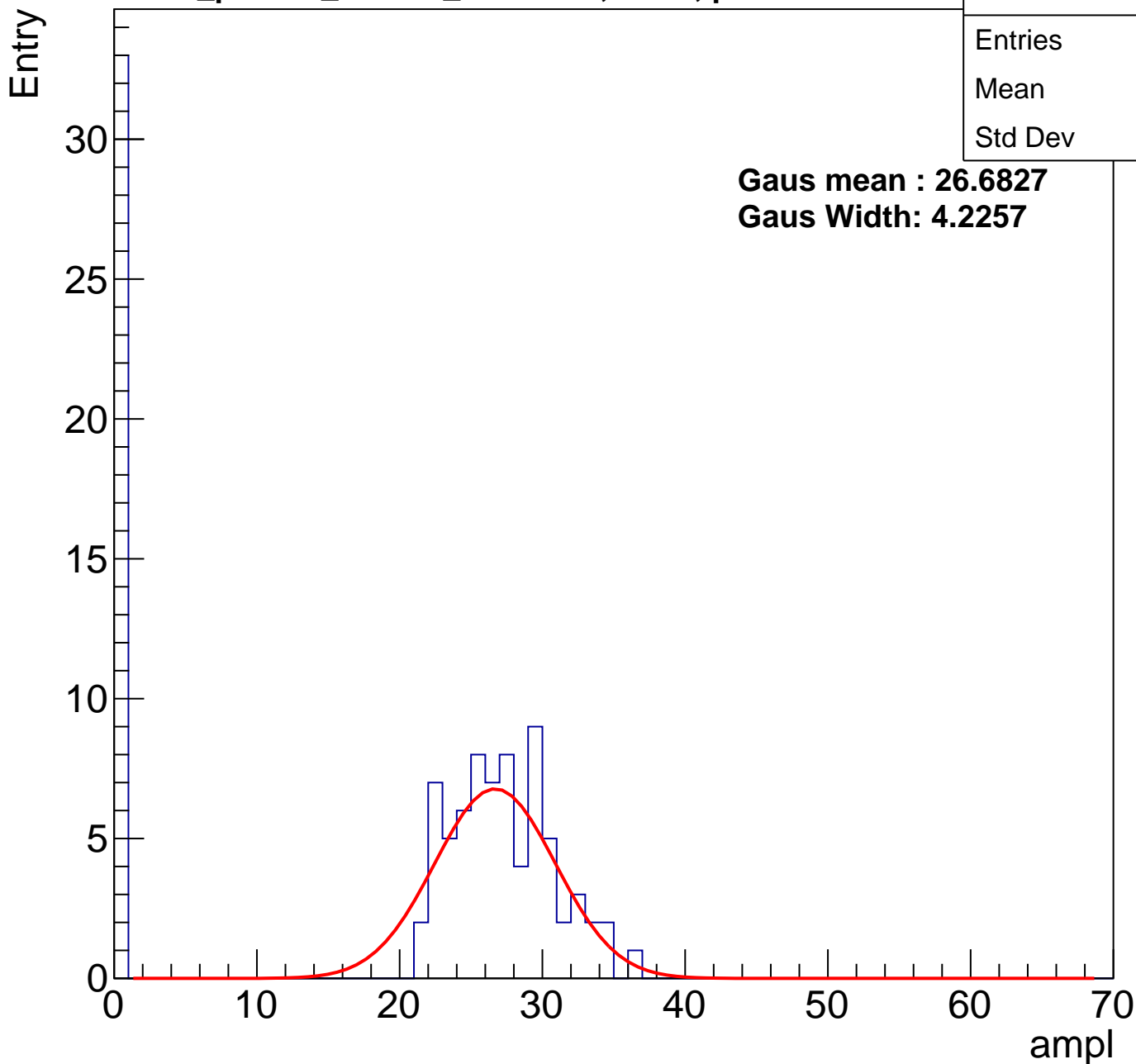
B1L103S, U2-ch112, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	18.31
Std Dev	12.81

Gaus mean : 26.6827

Gaus Width: 4.2257



B1L103S, U2-ch112, adc1

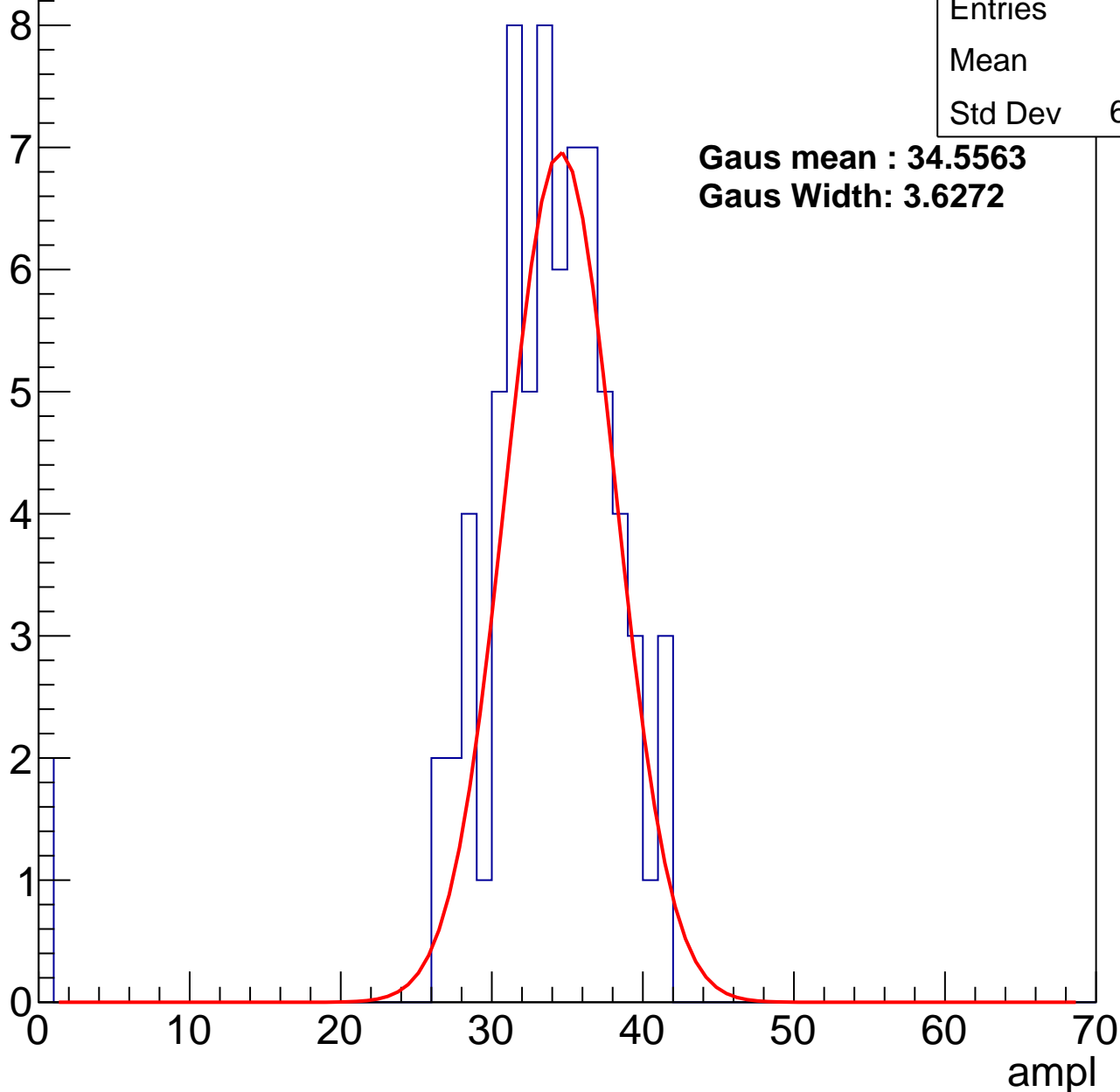
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.7
Std Dev	6.585

Gaus mean : 34.5563

Gaus Width: 3.6272



B1L103S, U2-ch112, adc2

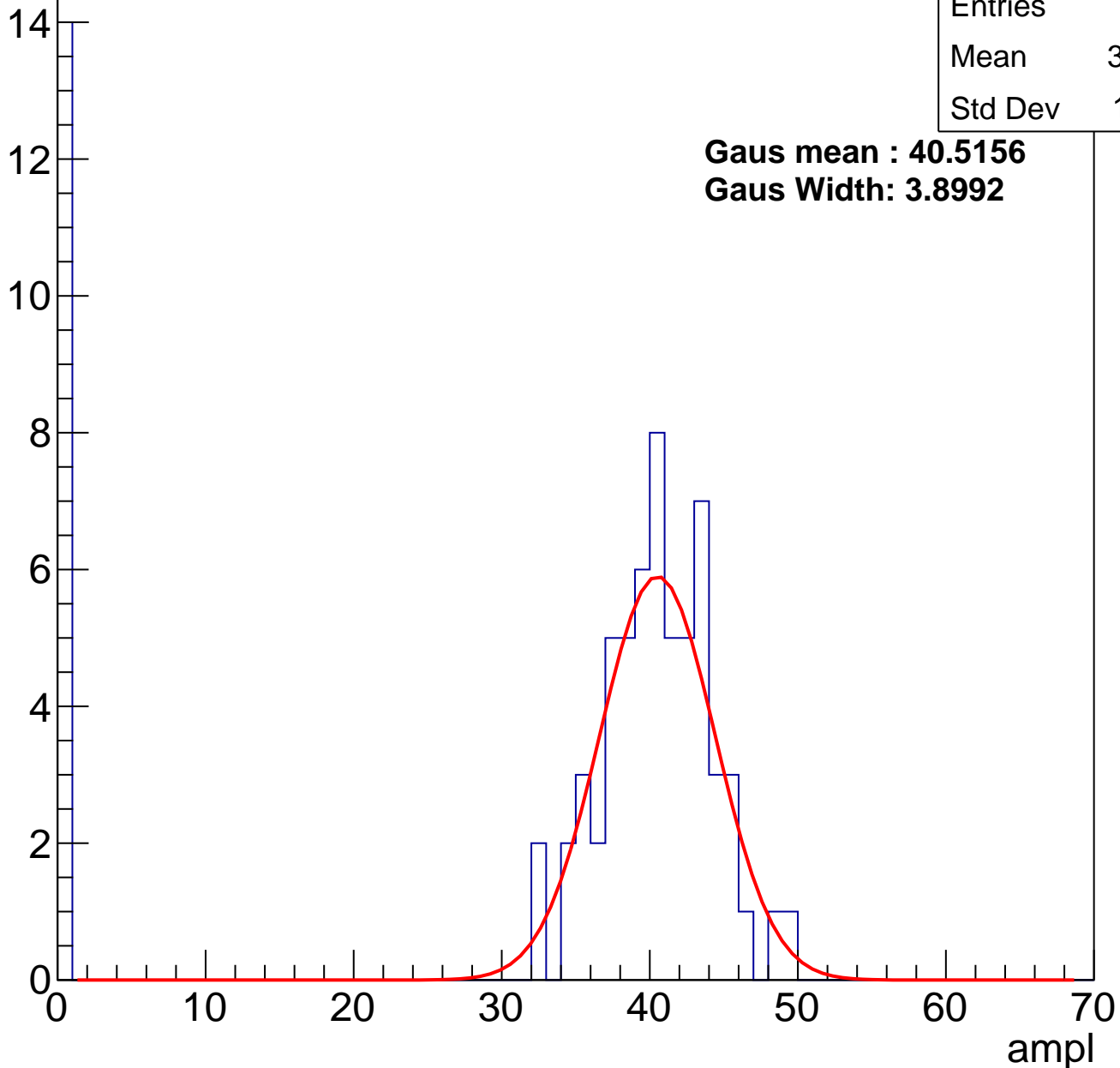
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	32.38
Std Dev	16.11

Gaus mean : 40.5156

Gaus Width: 3.8992

Entry

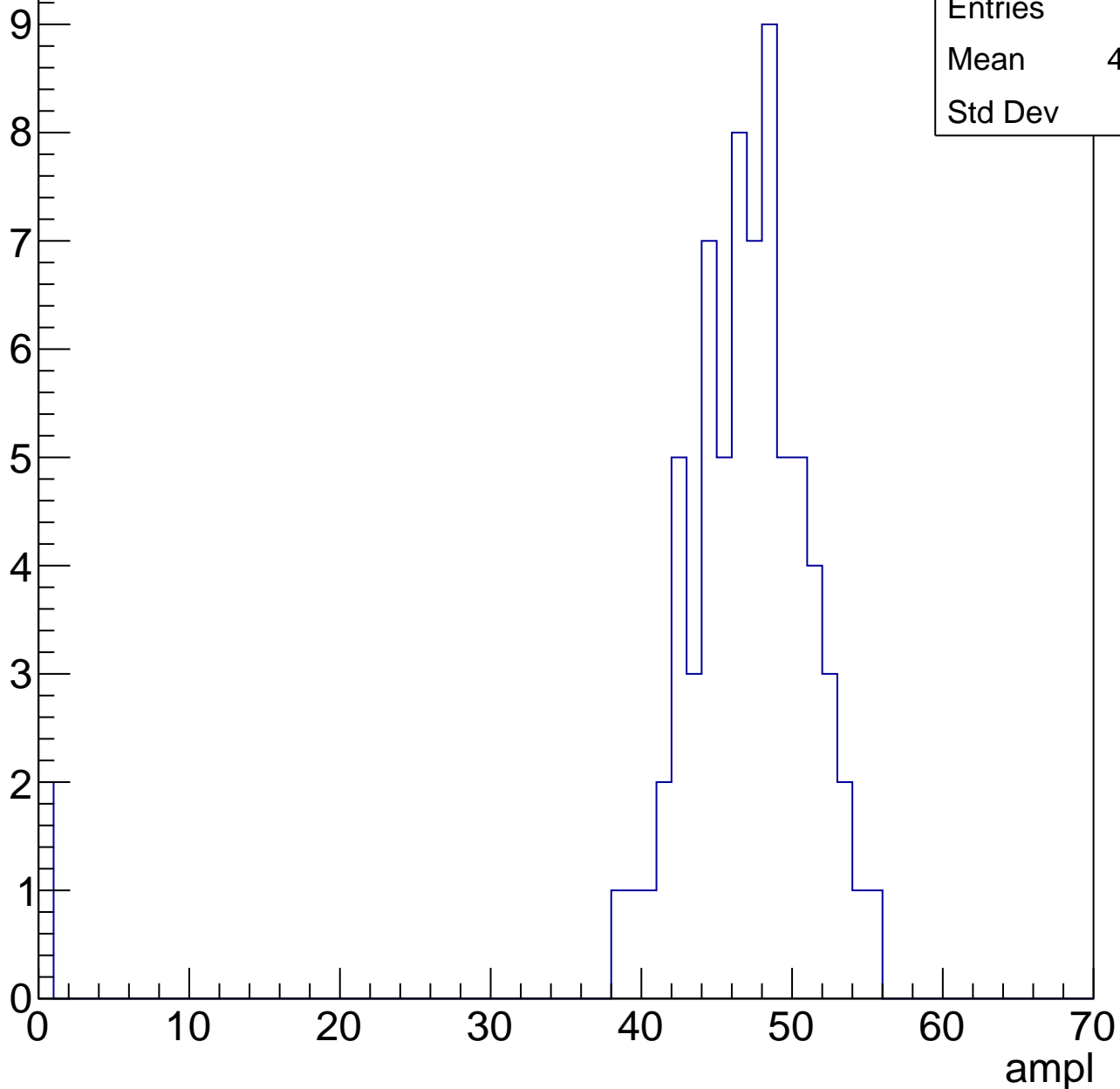


B1L103S, U2-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	45.42
Std Dev	8.47

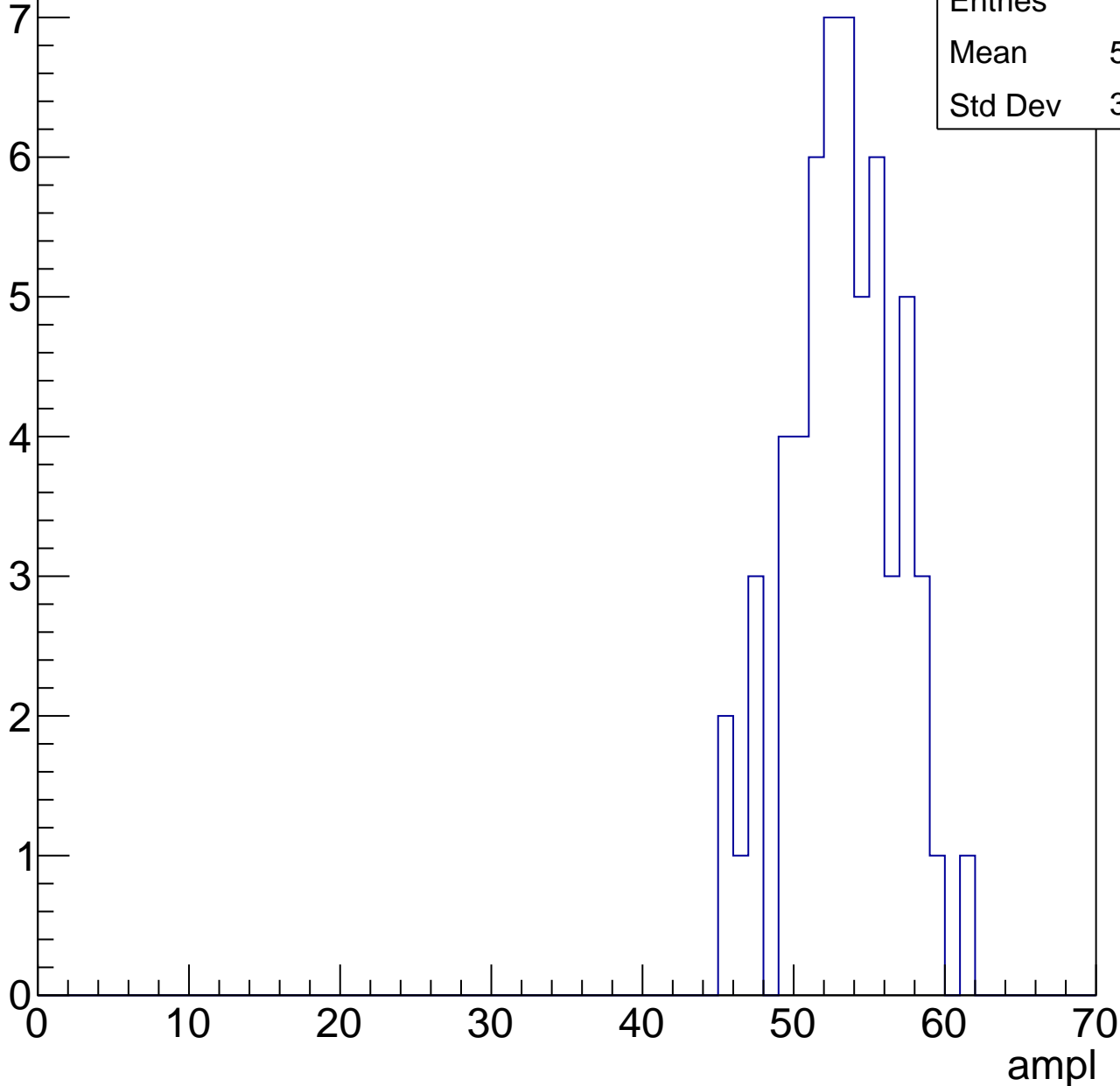


B1L103S, U2-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

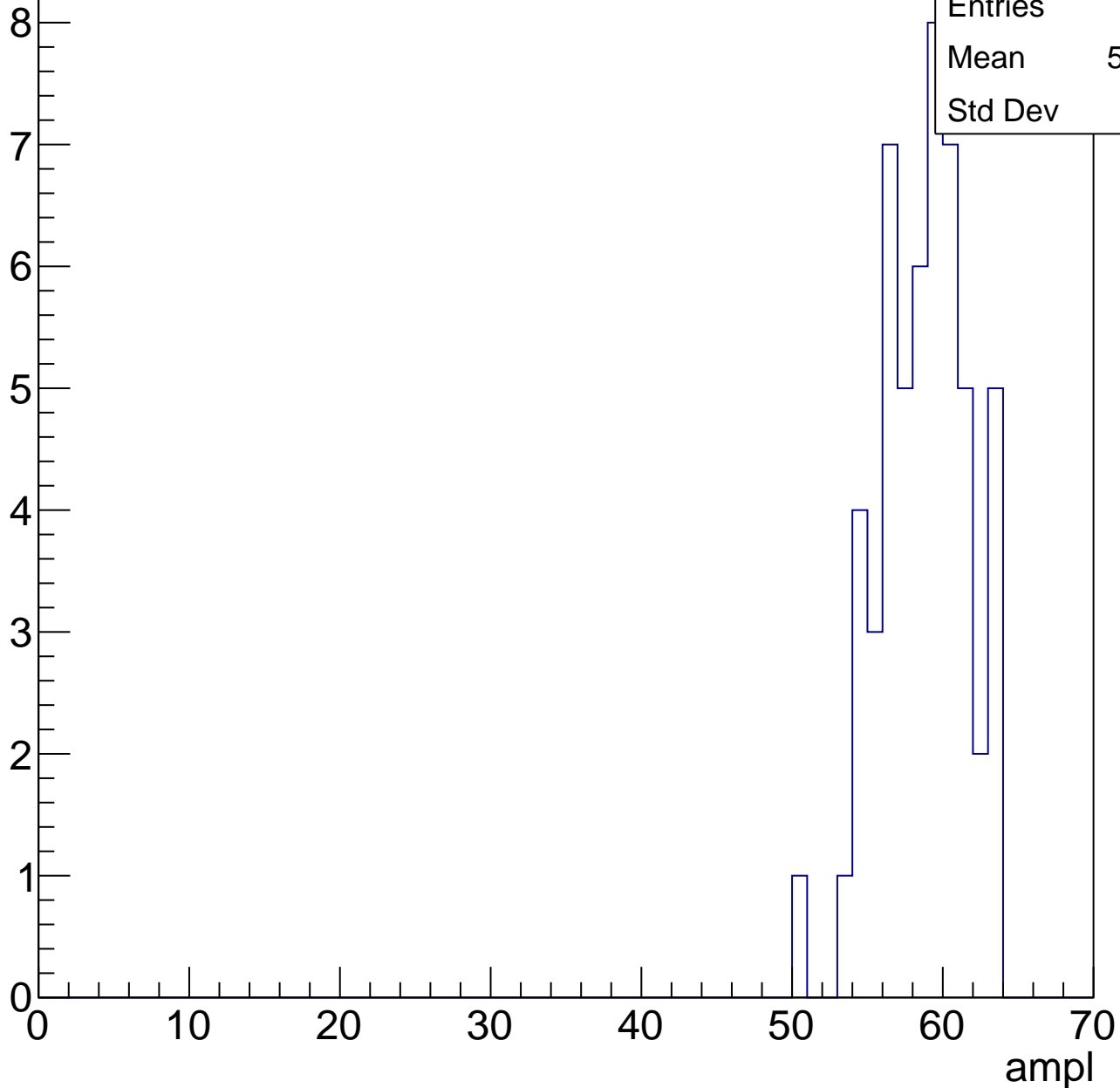
Entries	58
Mean	52.78
Std Dev	3.533



B1L103S, U2-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

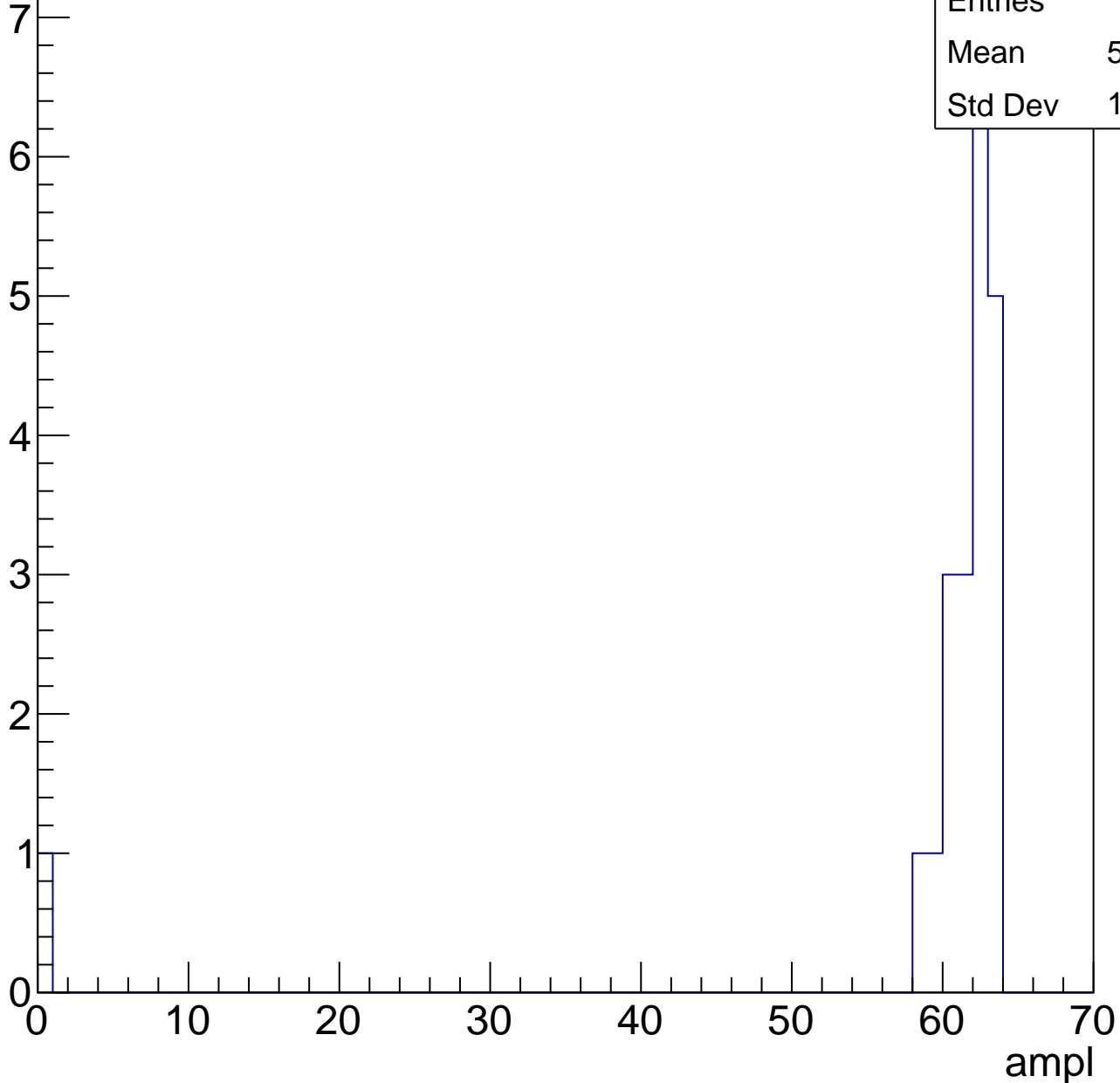


B1L103S, U2-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.52
Std Dev	13.16

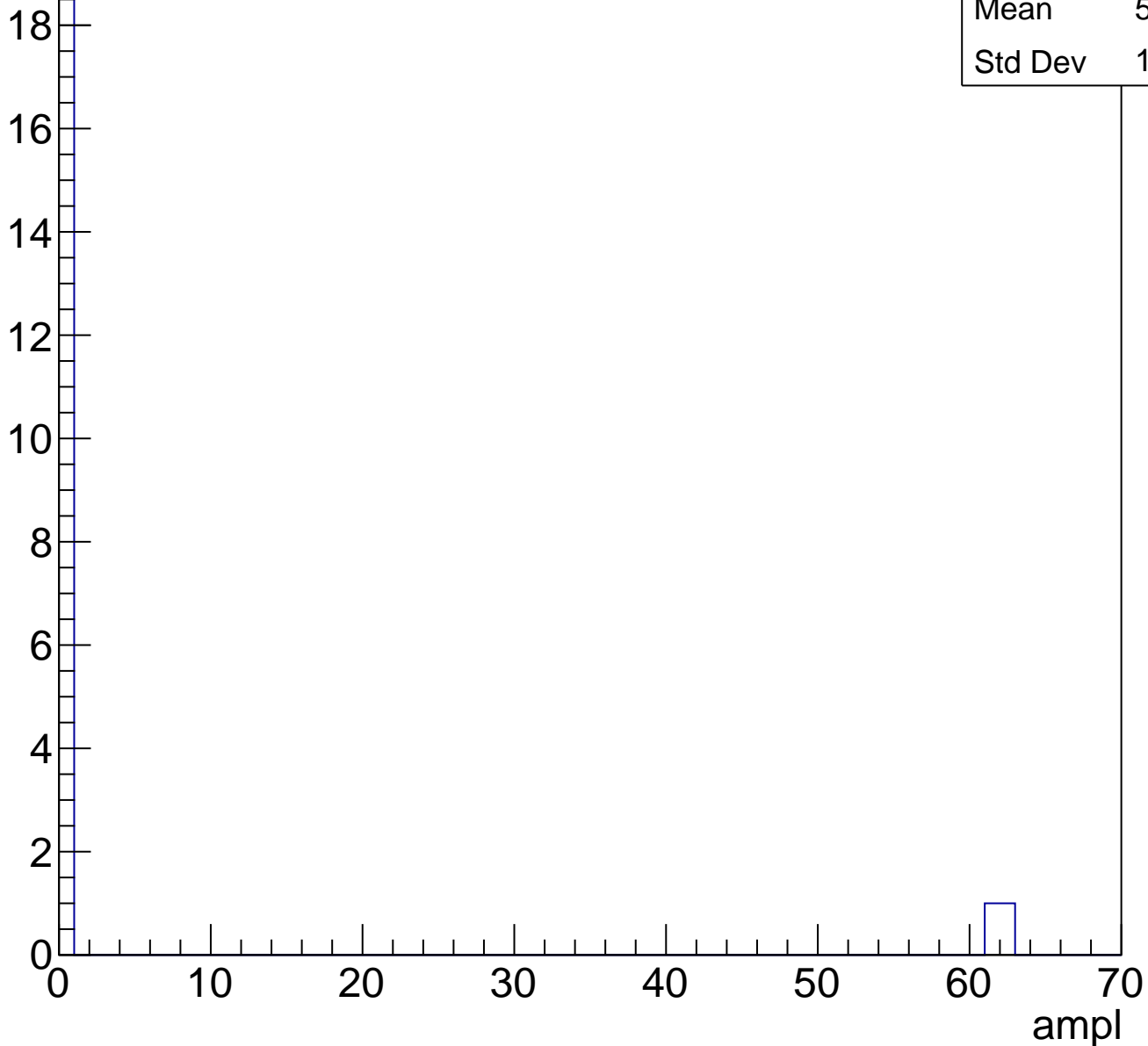


B1L103S, U2-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.857
Std Dev	18.05

Entry



B1L103S, U2-ch113, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	18.5
Std Dev	14.97

Gaus mean : 30.1024

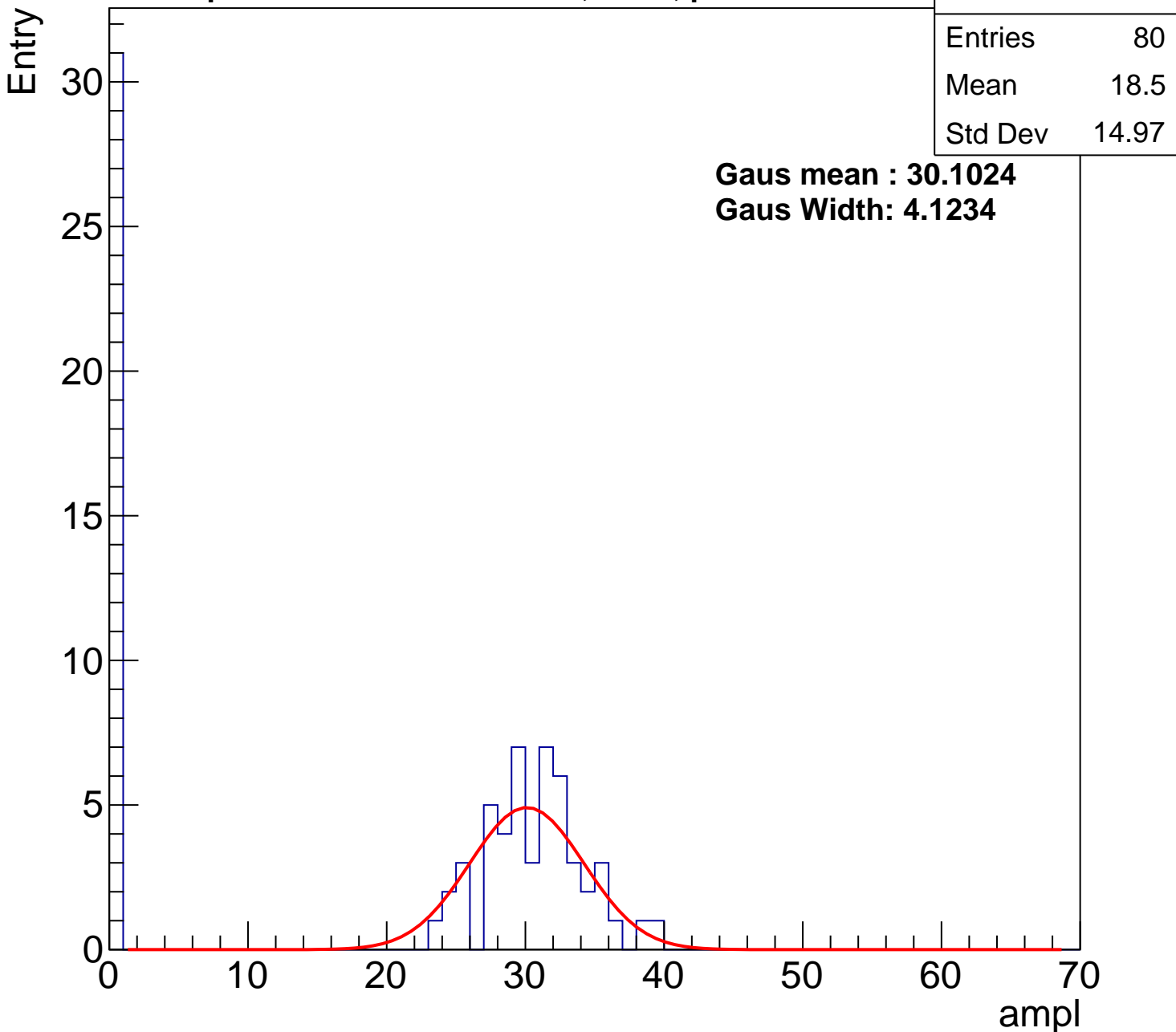
Gaus Width: 4.1234

Entry

30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch113, adc1

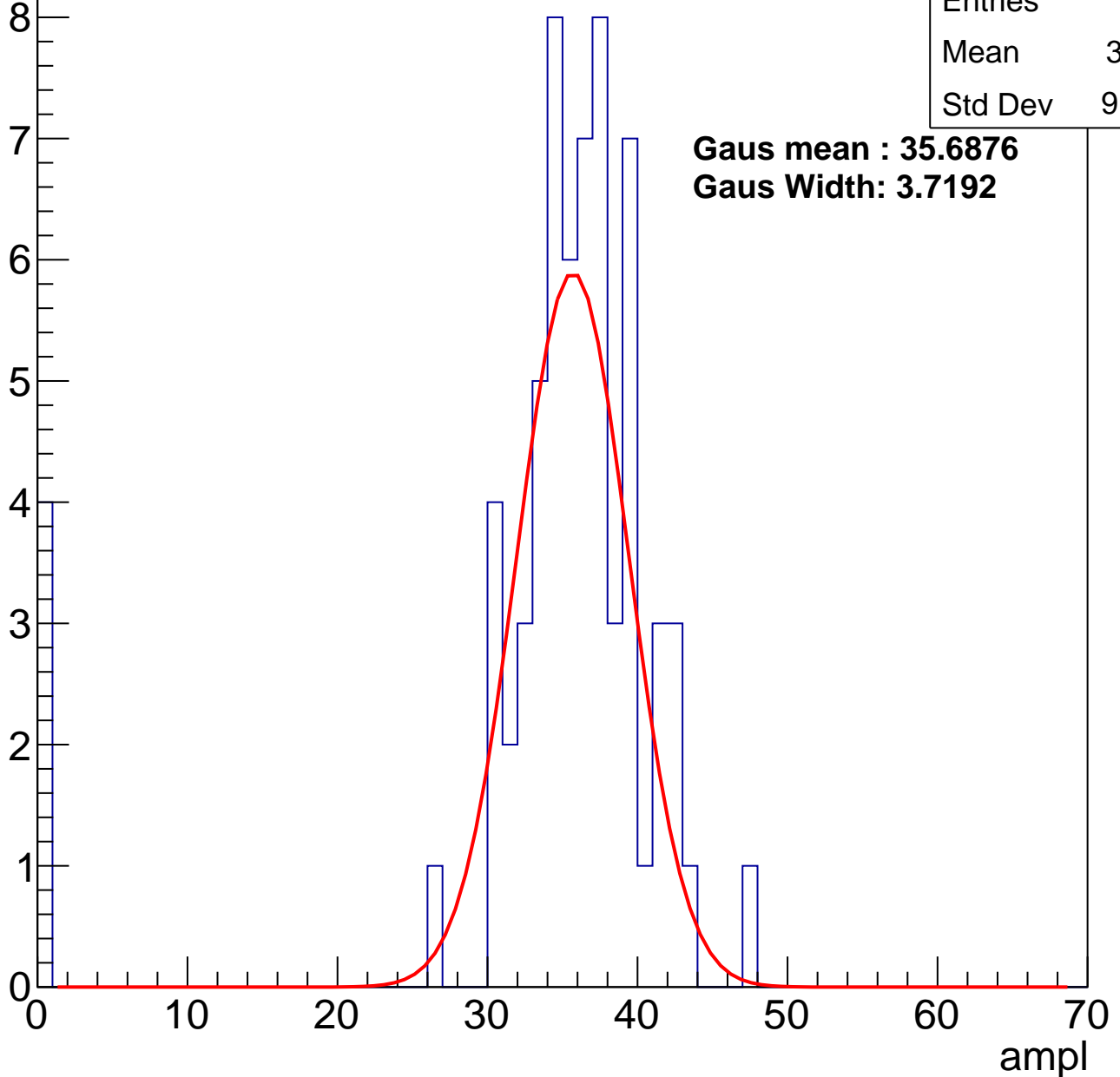
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.81
Std Dev	9.255

Gaus mean : 35.6876

Gaus Width: 3.7192



B1L103S, U2-ch113, adc2

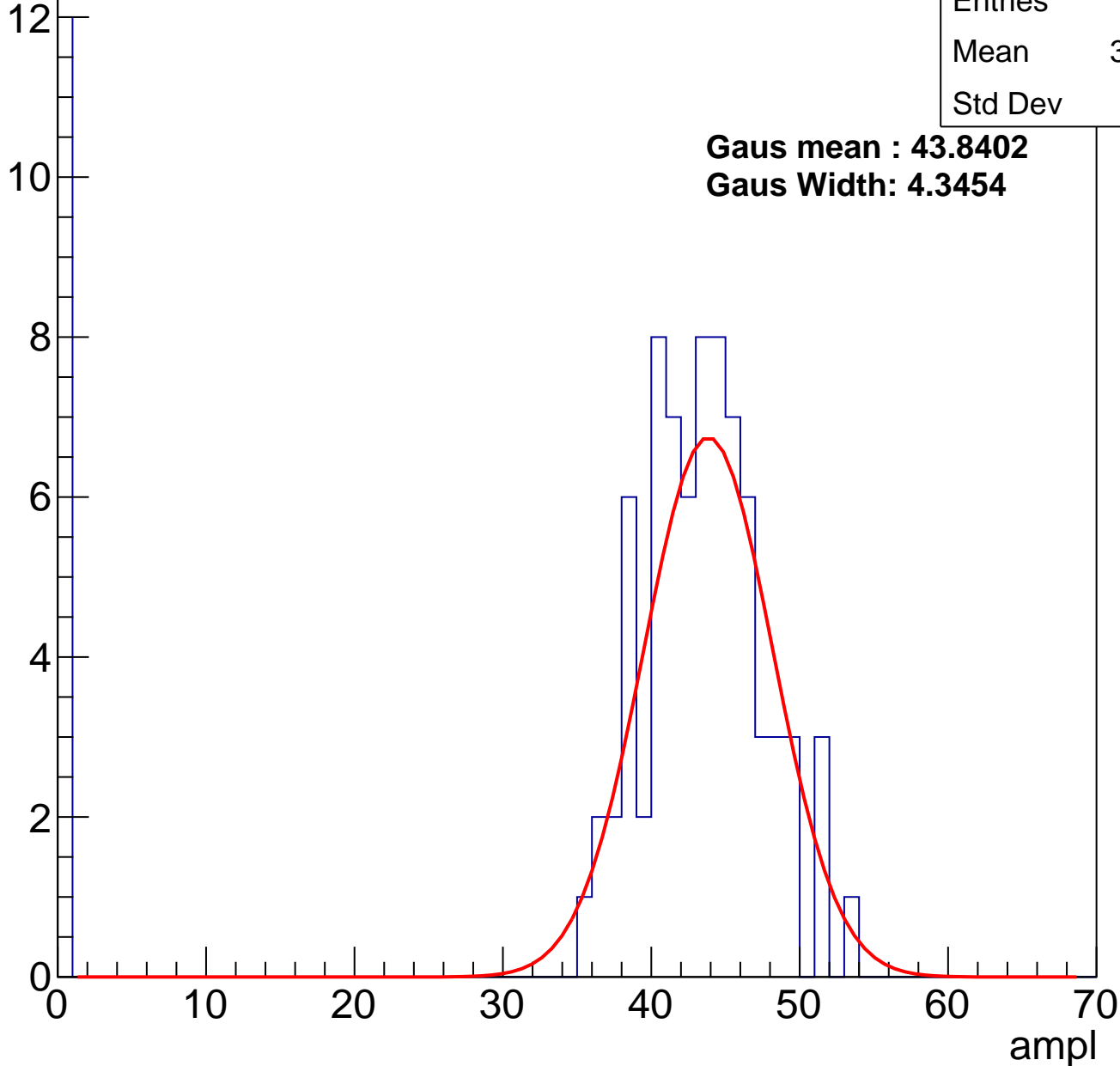
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	37.17
Std Dev	15.2

Gaus mean : 43.8402

Gaus Width: 4.3454

Entry



B1L103S, U2-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

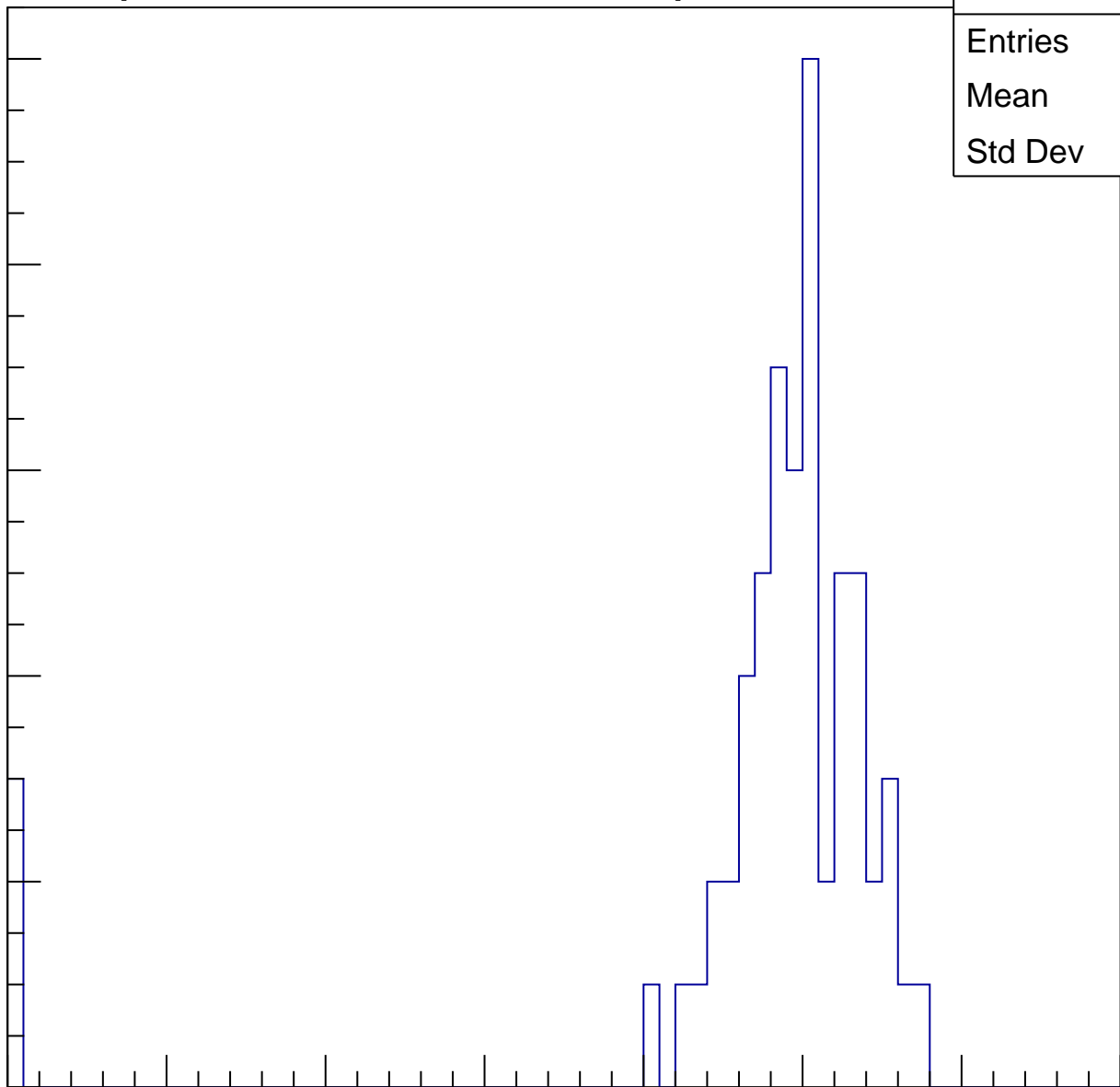
Entries	61
Mean	46.97
Std Dev	11.22

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

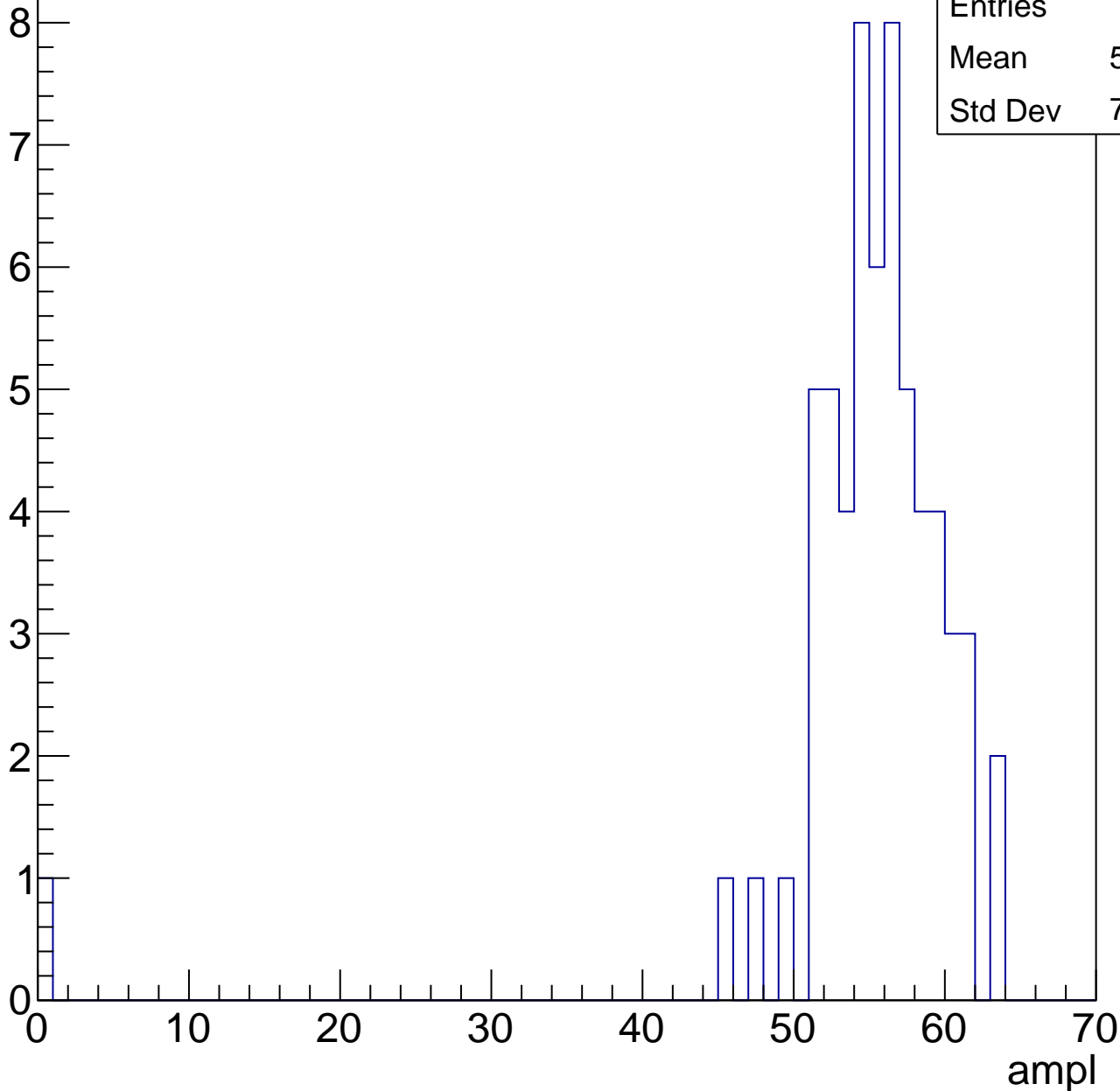


B1L103S, U2-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	54.43
Std Dev	7.885

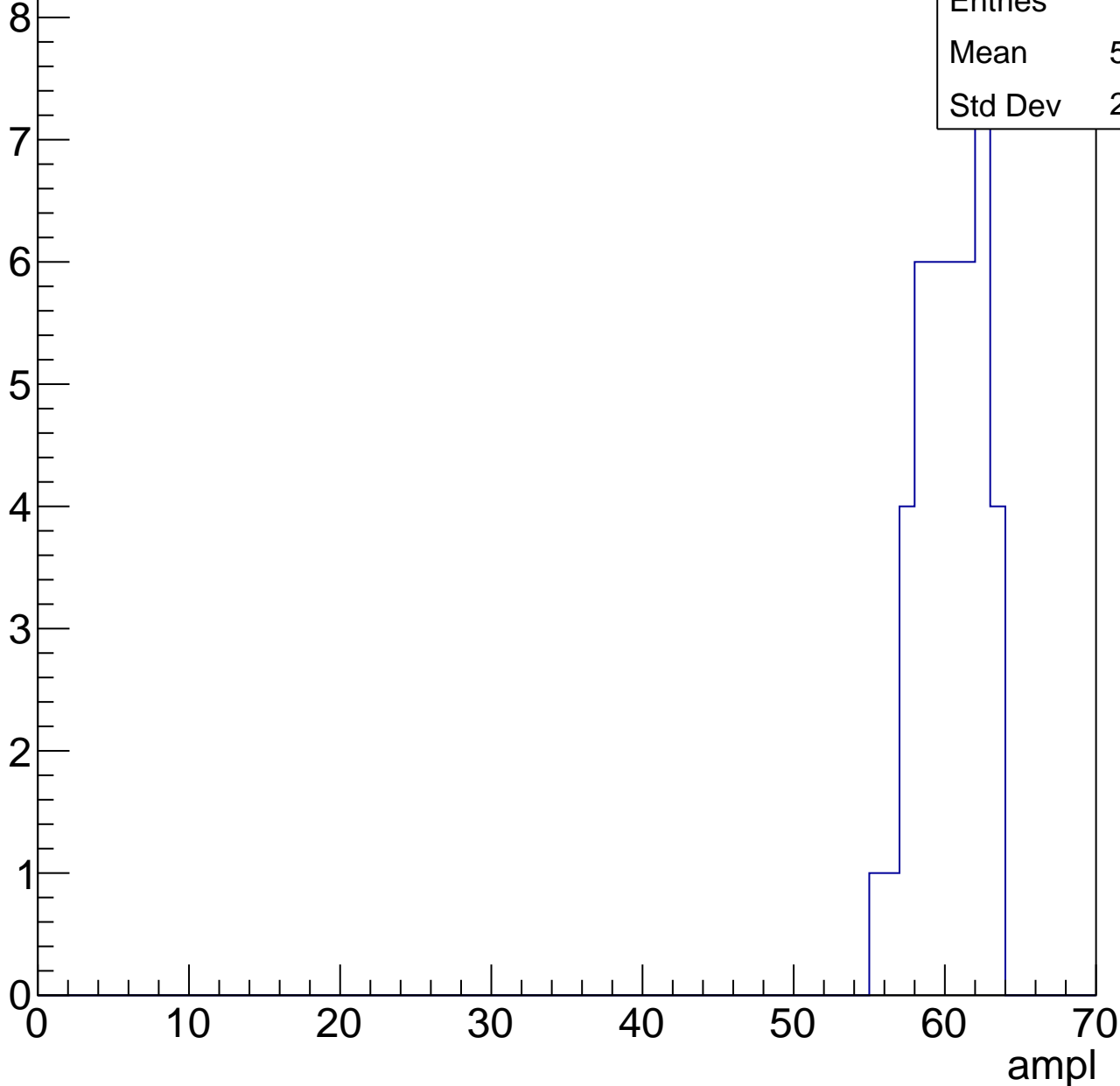


B1L103S, U2-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	59.88
Std Dev	2.073



B1L103S, U2-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U2-ch114, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	17.73
Std Dev	11.82

Gaus mean : 25.4006

Gaus Width: 3.9741

Entry

30

25

20

15

10

5

0

0

10

20

30

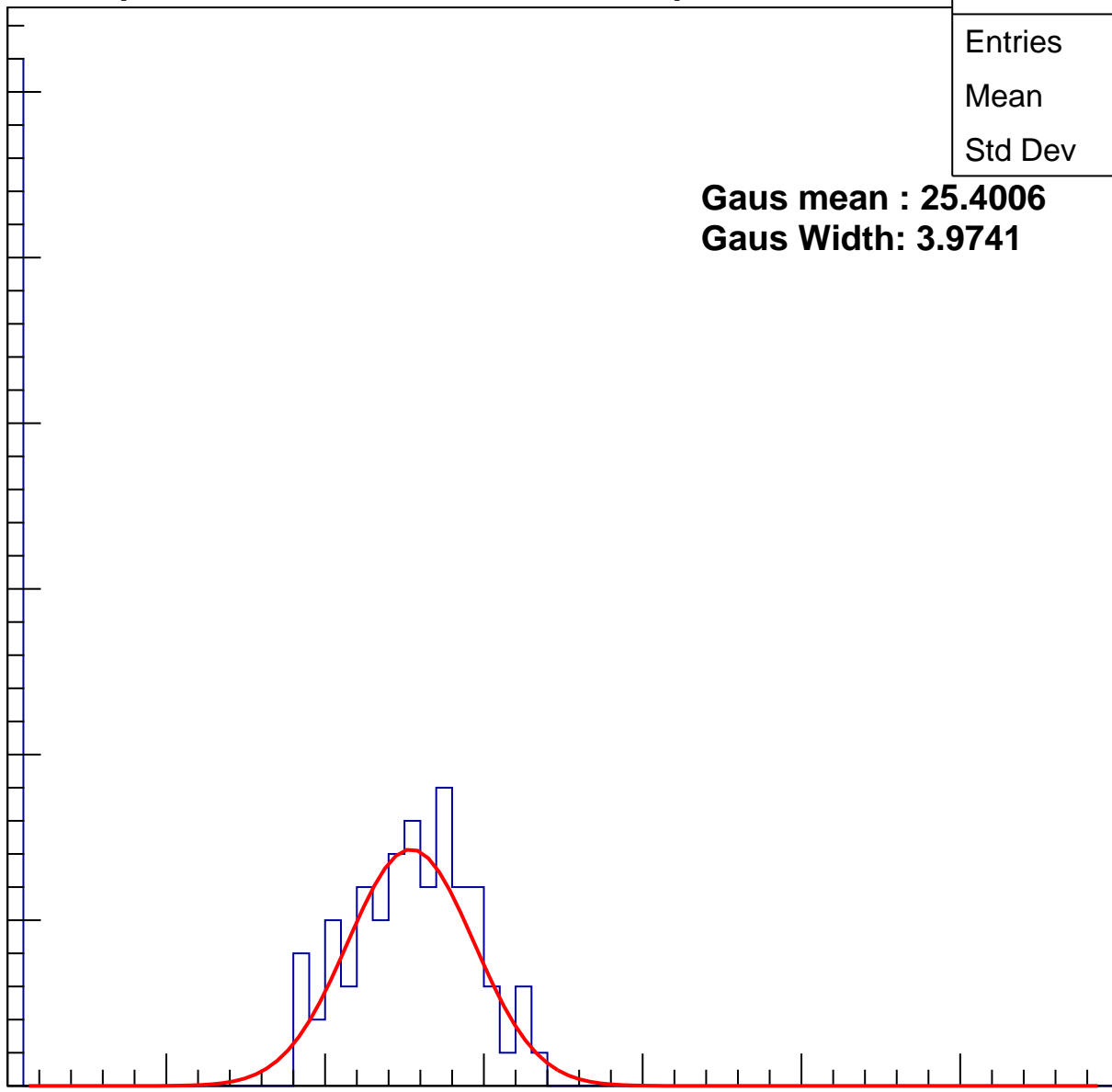
40

50

60

70

ampl



B1L103S, U2-ch114, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	27.9
Std Dev	11.59

Gaus mean : 33.1518

Gaus Width: 3.5328

Entry

10

8

6

4

2

0

ampl

0

10

20

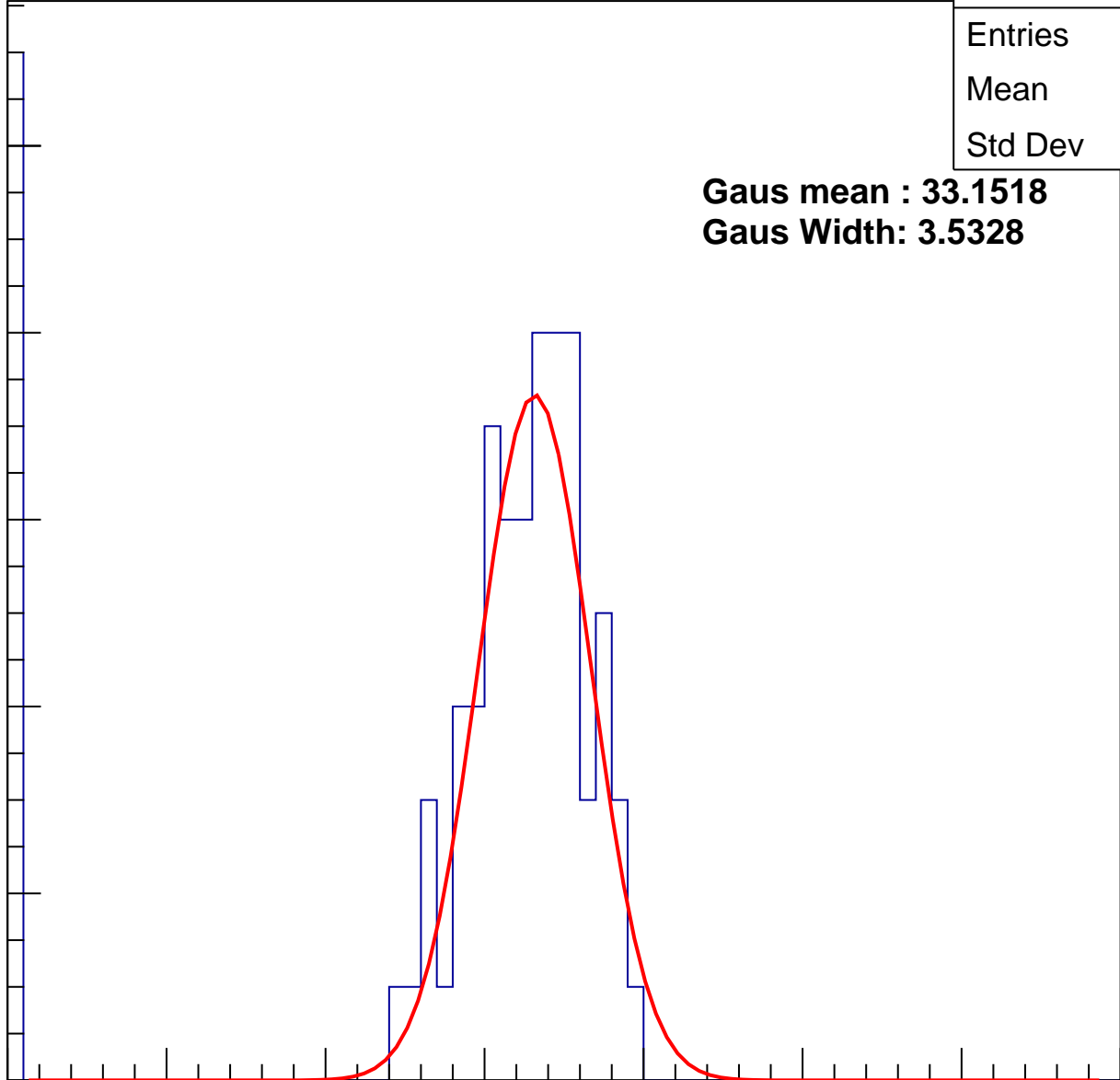
30

40

50

60

70



B1L103S, U2-ch114, adc2

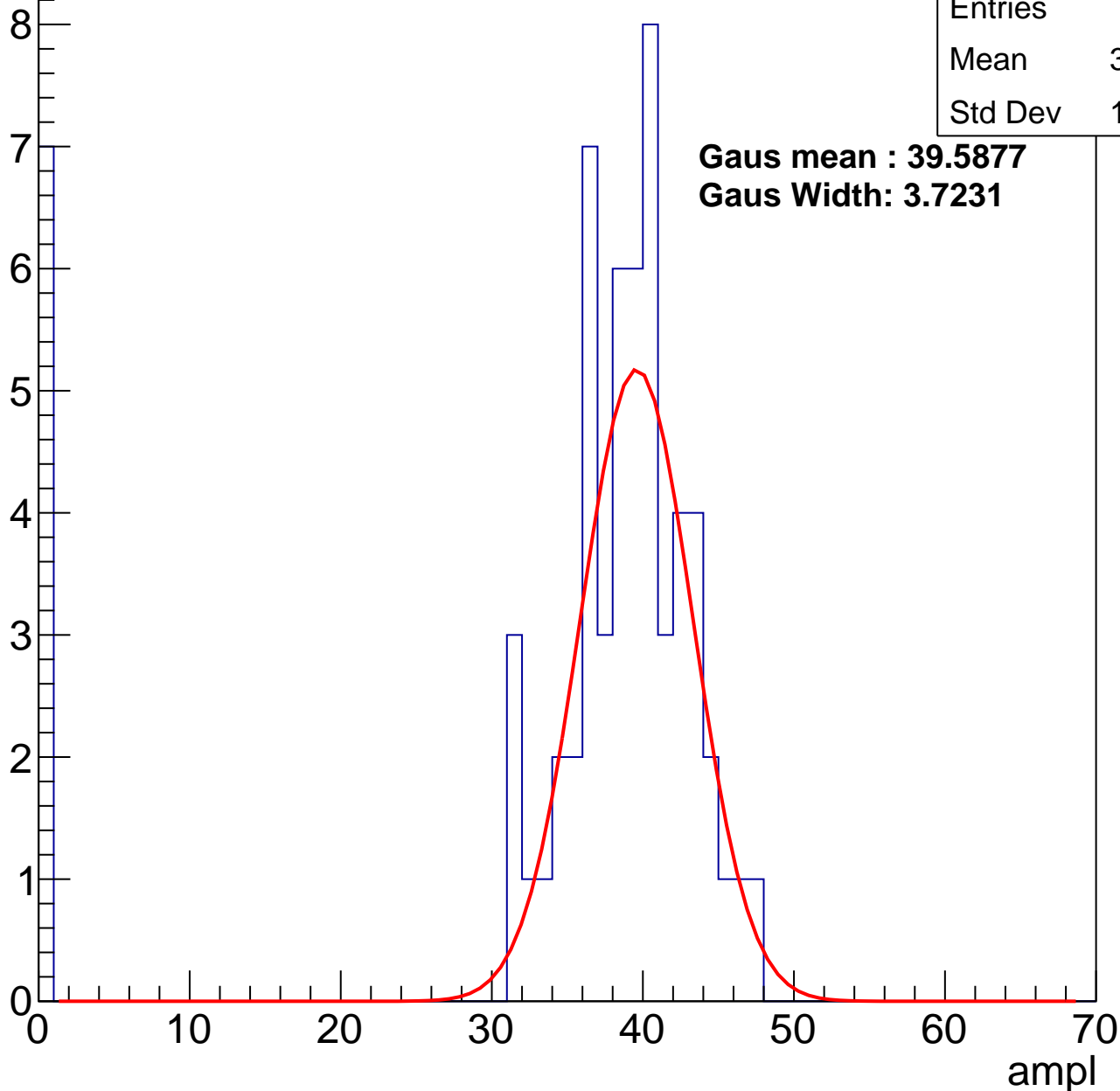
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.35
Std Dev	12.74

Gaus mean : 39.5877

Gaus Width: 3.7231

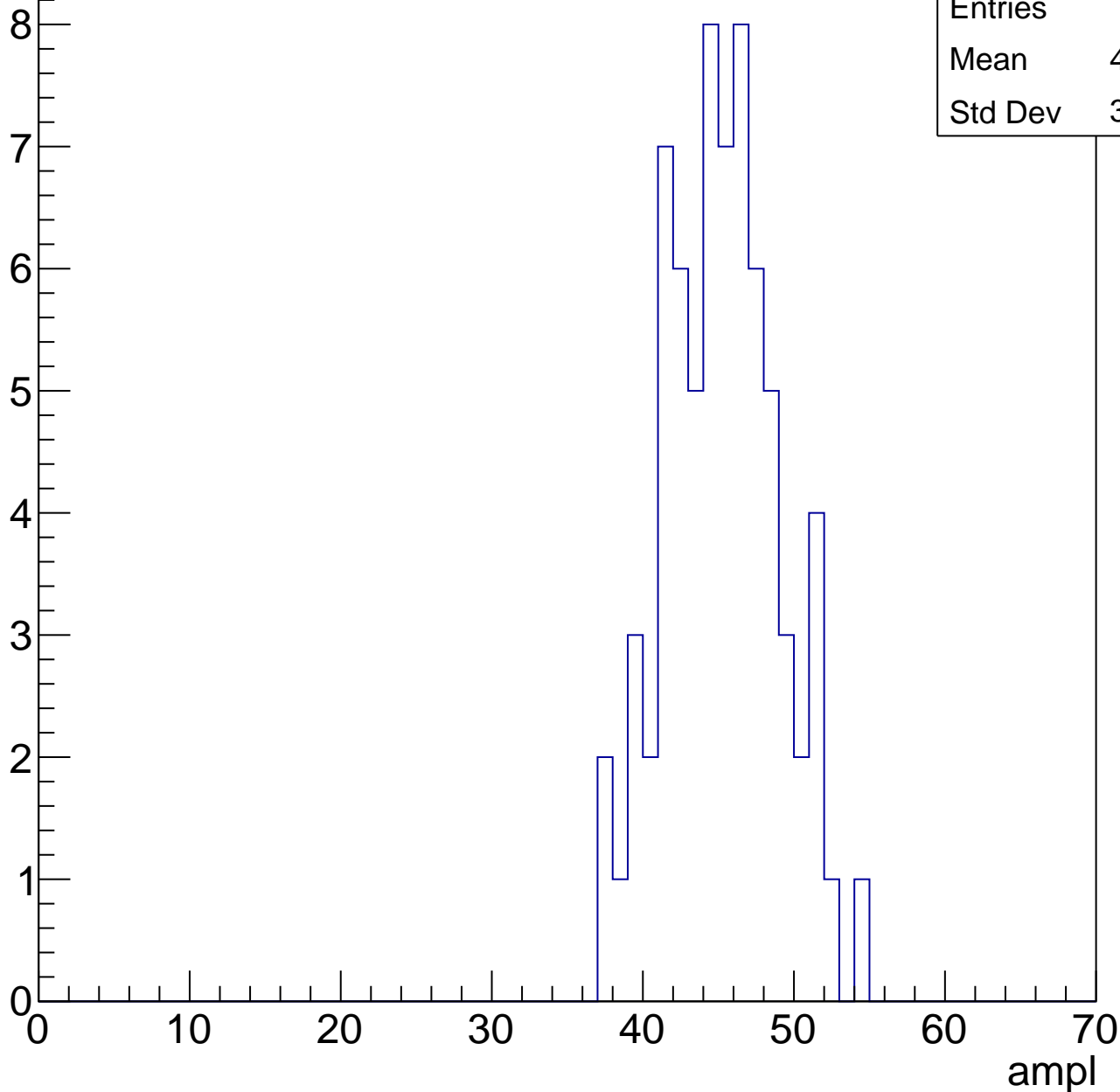


B1L103S, U2-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	44.75
Std Dev	3.703

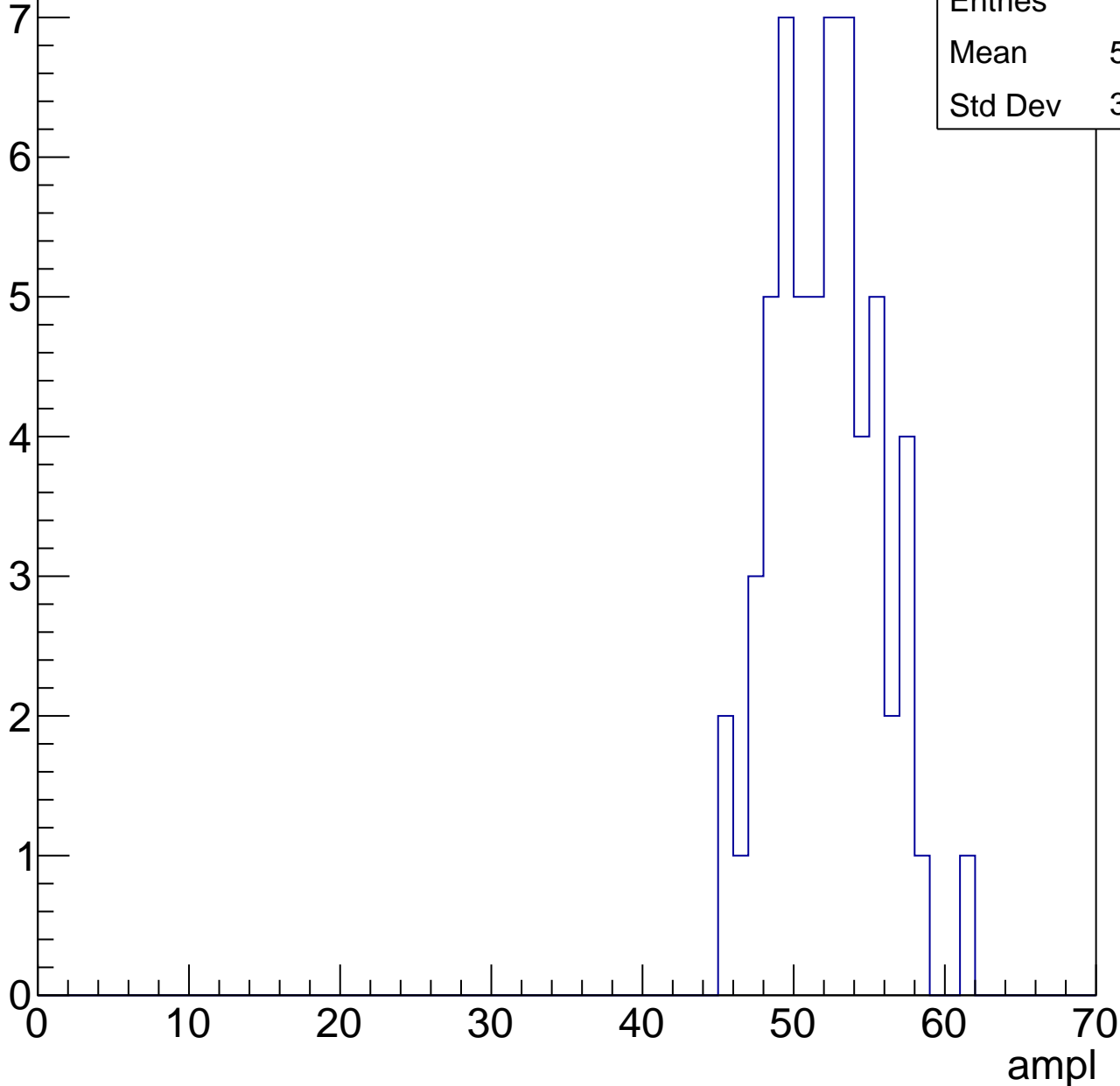


B1L103S, U2-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	51.69
Std Dev	3.416

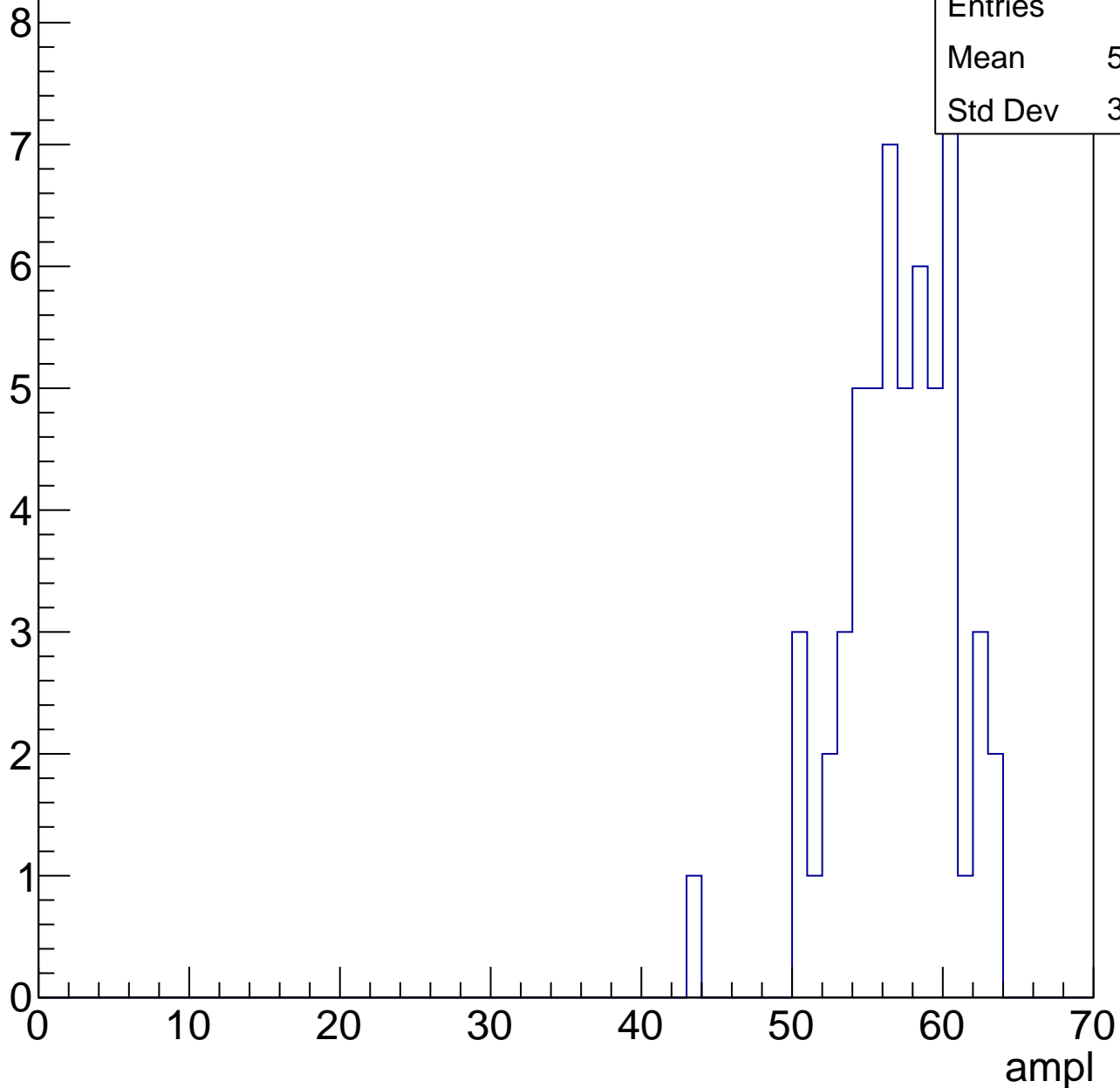


B1L103S, U2-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	56.58
Std Dev	3.746

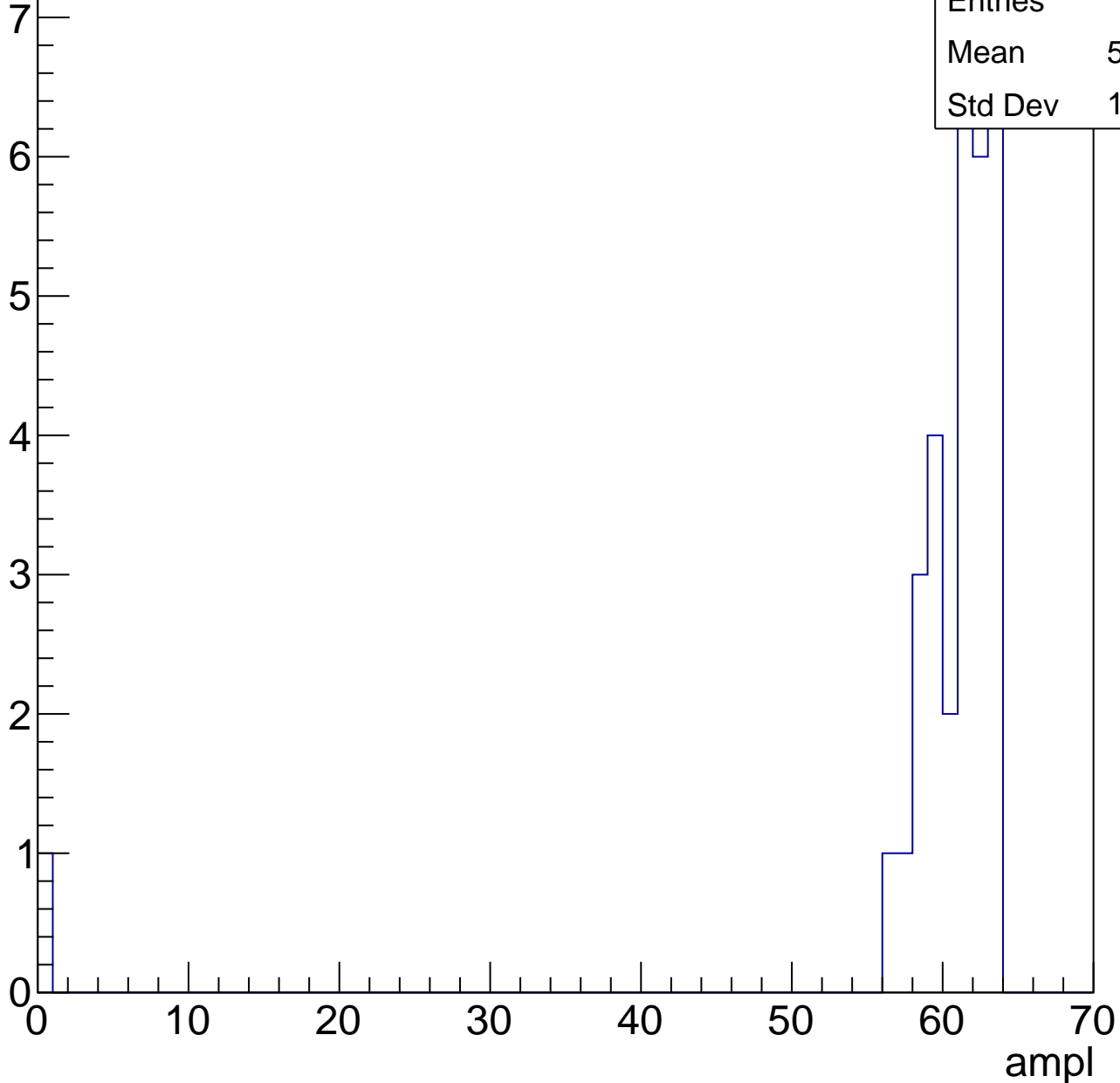


B1L103S, U2-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.84
Std Dev	10.74

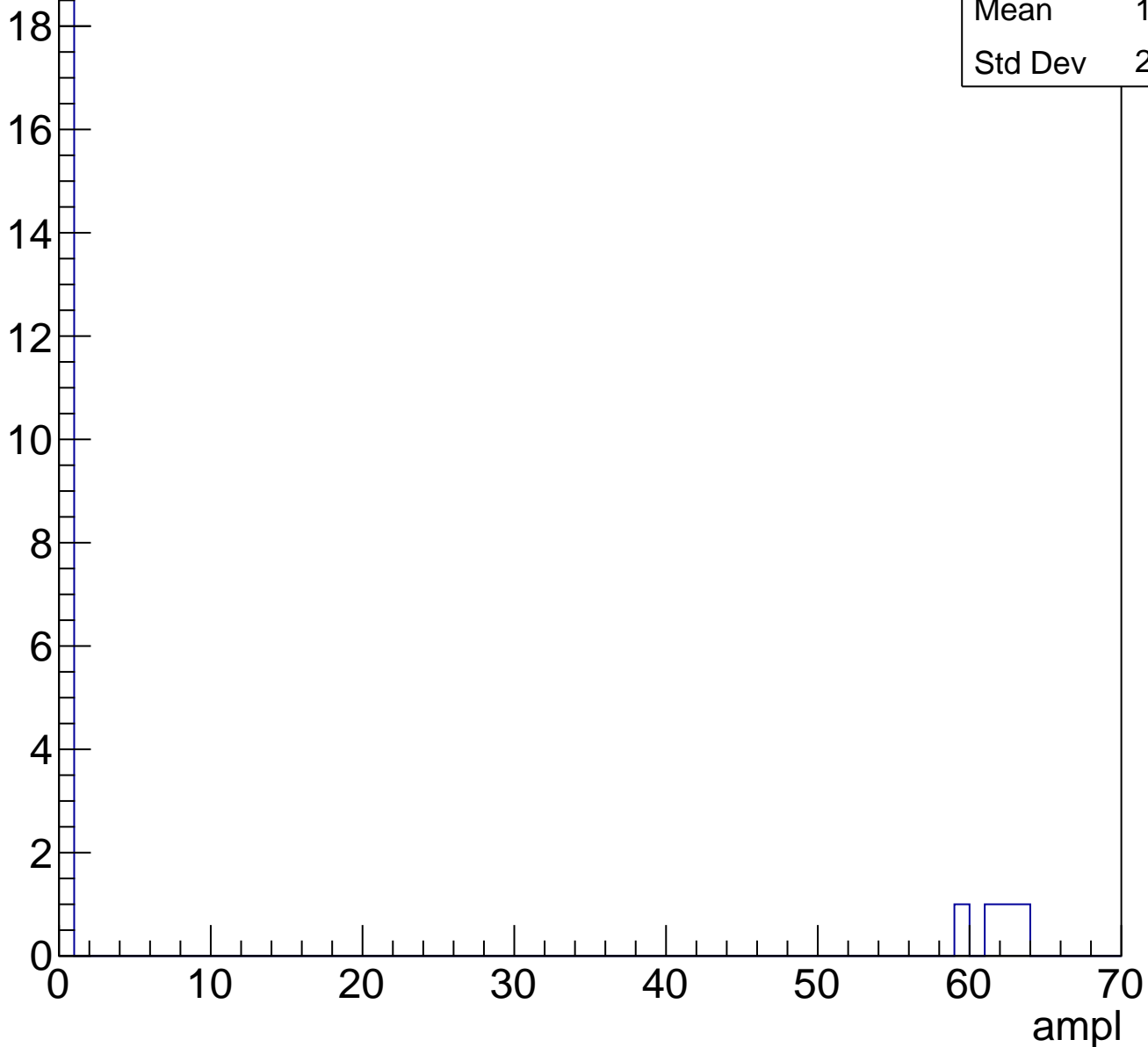


B1L103S, U2-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.65
Std Dev	23.22

Entry



B1L103S, U2-ch115, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	21.54
Std Dev	12.84

Gaus mean : 29.7878

Gaus Width: 3.1785

Entry

25

20

15

10

5

0

0

10

20

30

40

50

60

70

ampl



B1L103S, U2-ch115, adc1

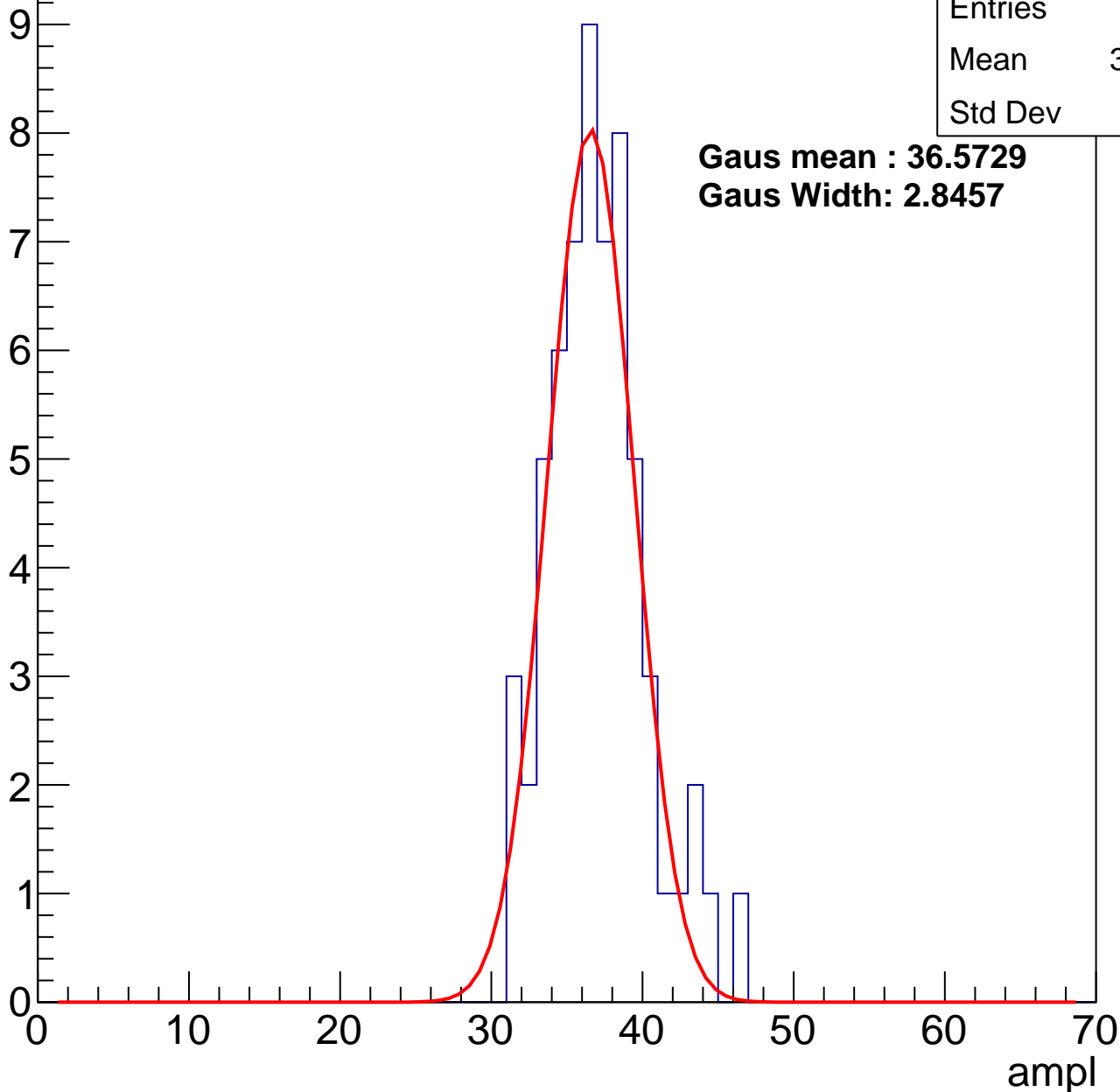
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	36.59
Std Dev	3.18

Gaus mean : 36.5729

Gaus Width: 2.8457



B1L103S, U2-ch115, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	36.52
Std Dev	14.9

Gaus mean : 42.8721

Gaus Width: 3.2959

Entry

10

8

6

4

2

0

0

10

20

30

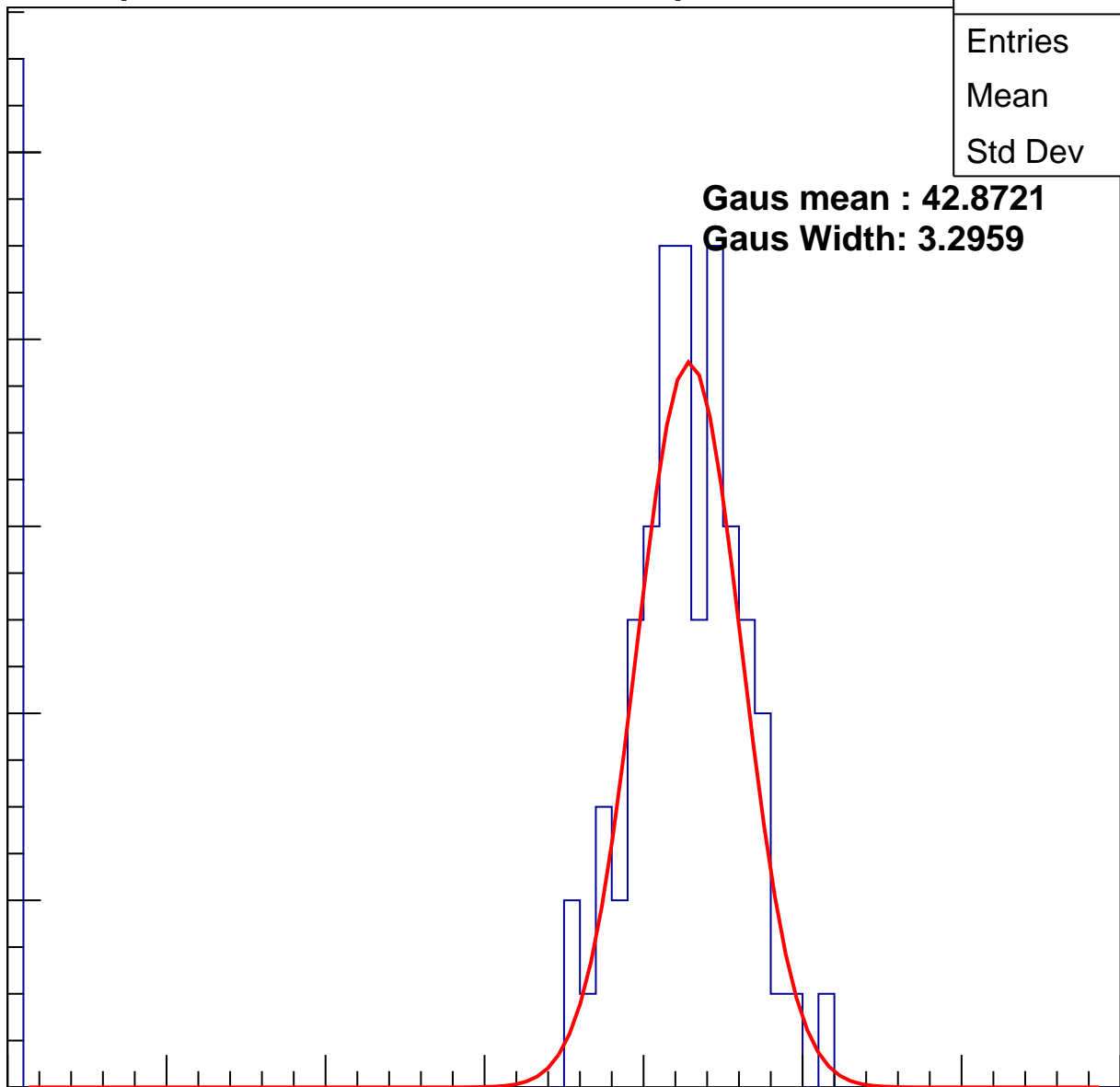
40

50

60

70

ampl

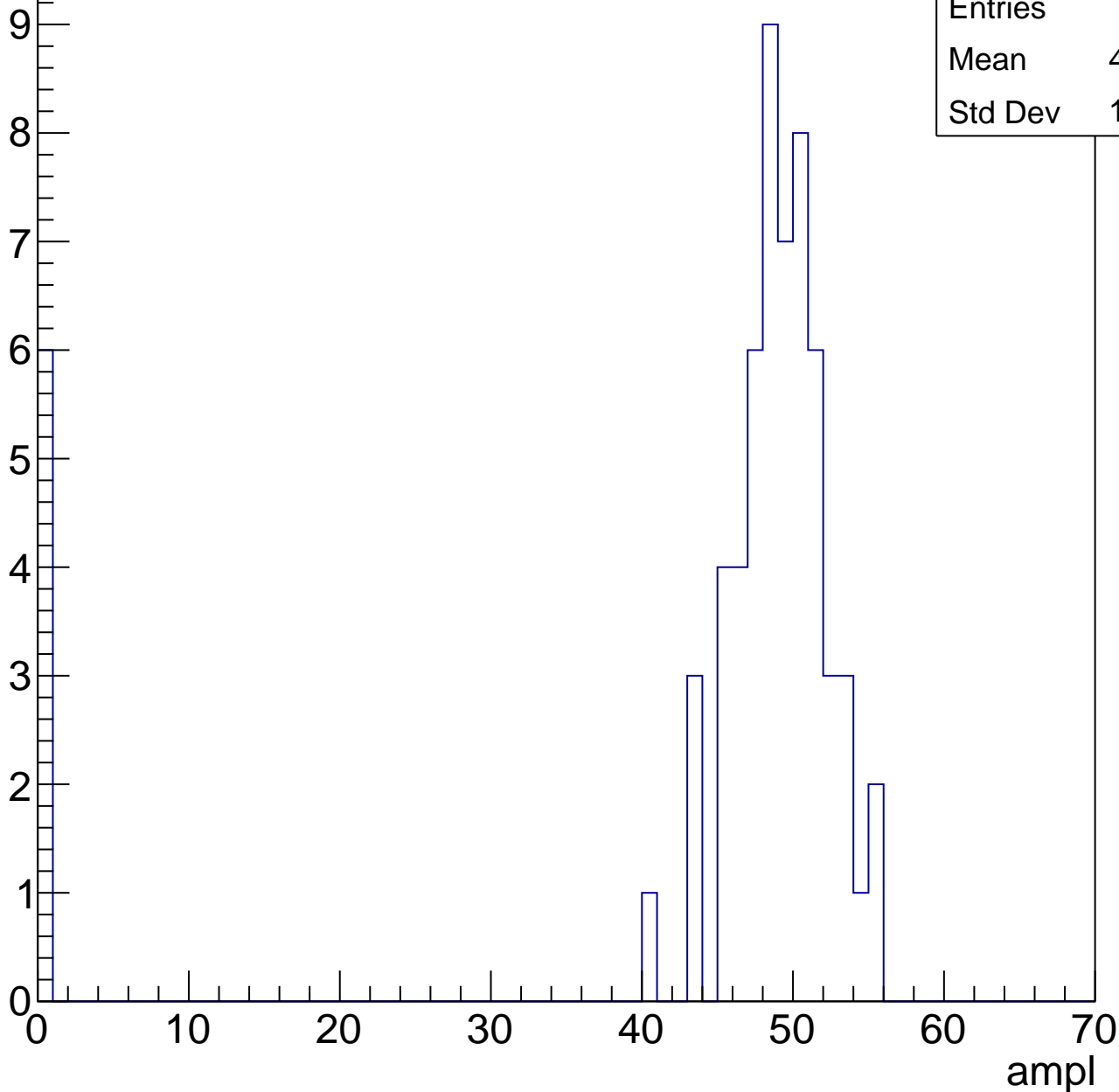


B1L103S, U2-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

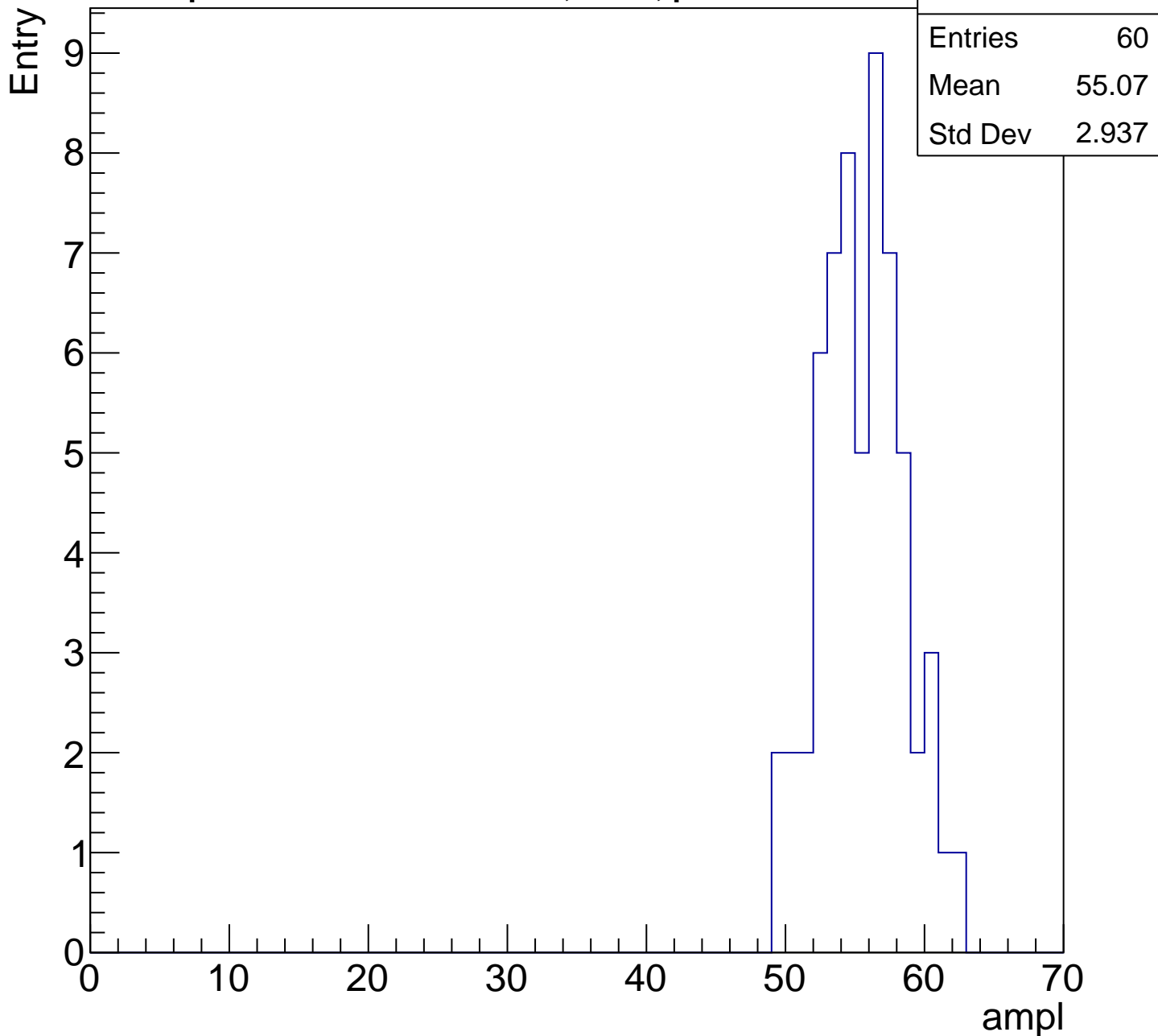
Entry

Entries	63
Mean	44.05
Std Dev	14.58



B1L103S, U2-ch115, adc4

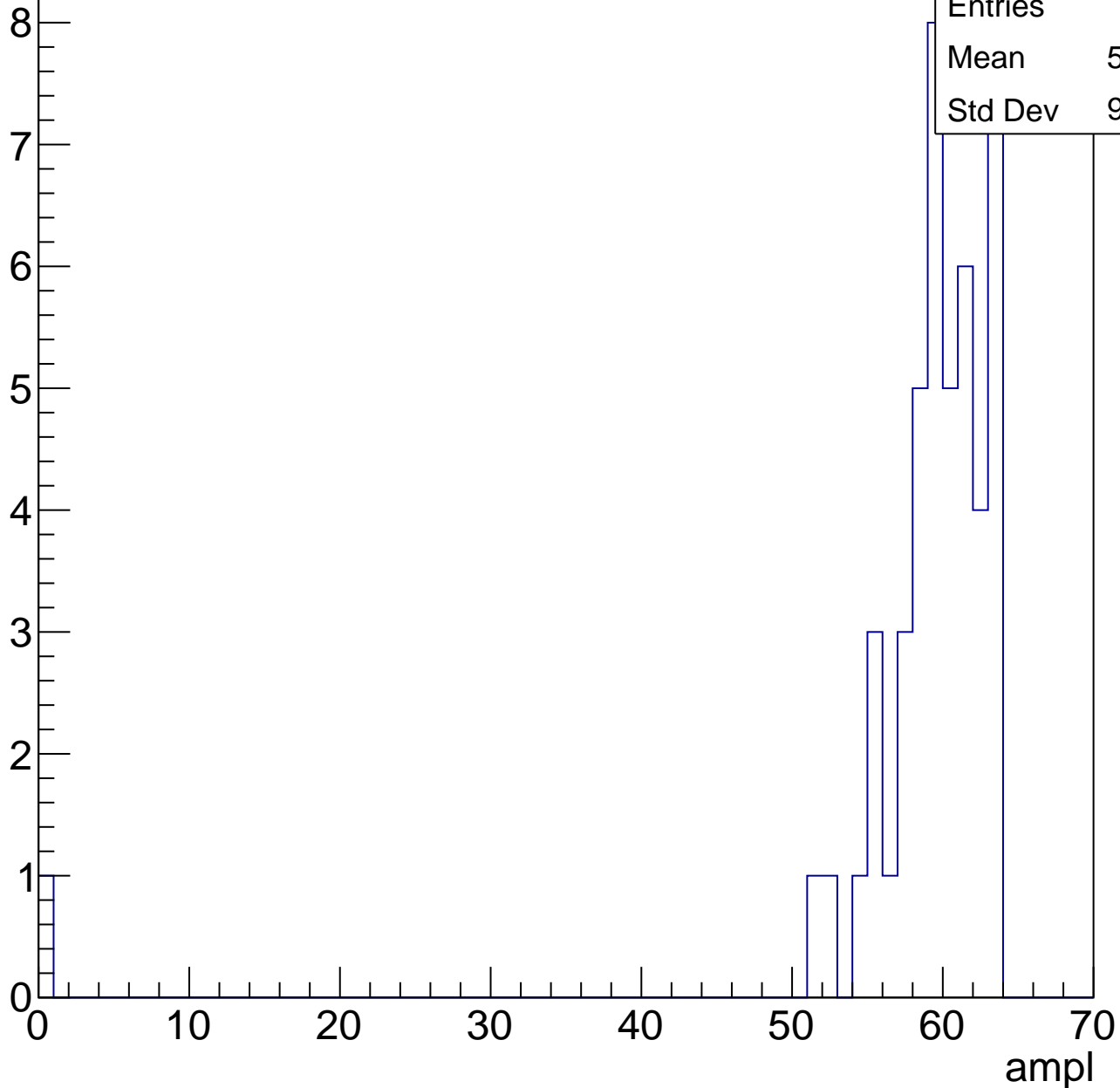
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U2-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

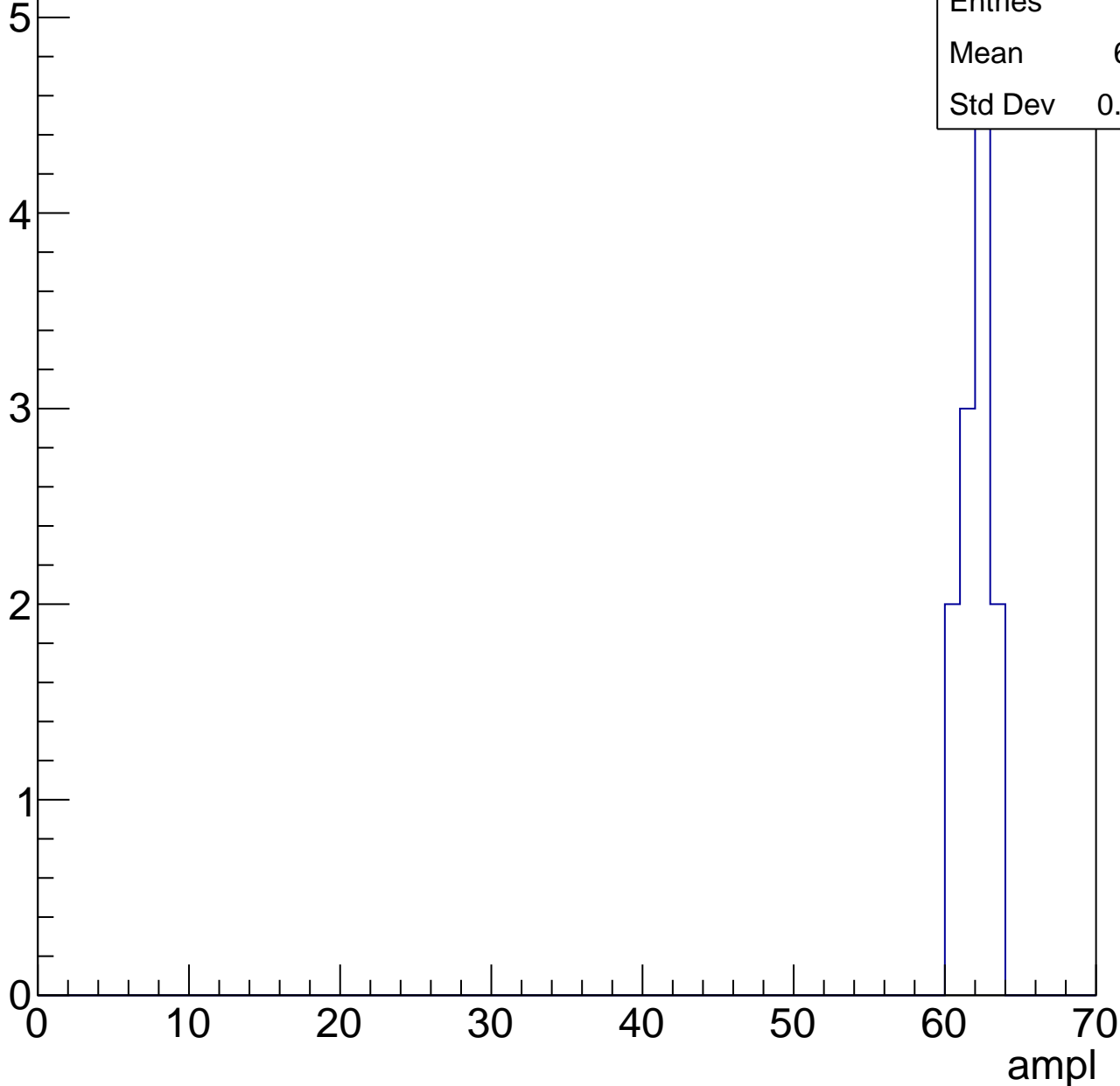


B1L103S, U2-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.58
Std Dev	0.9538



B1L103S, U2-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U2-ch116, adc0

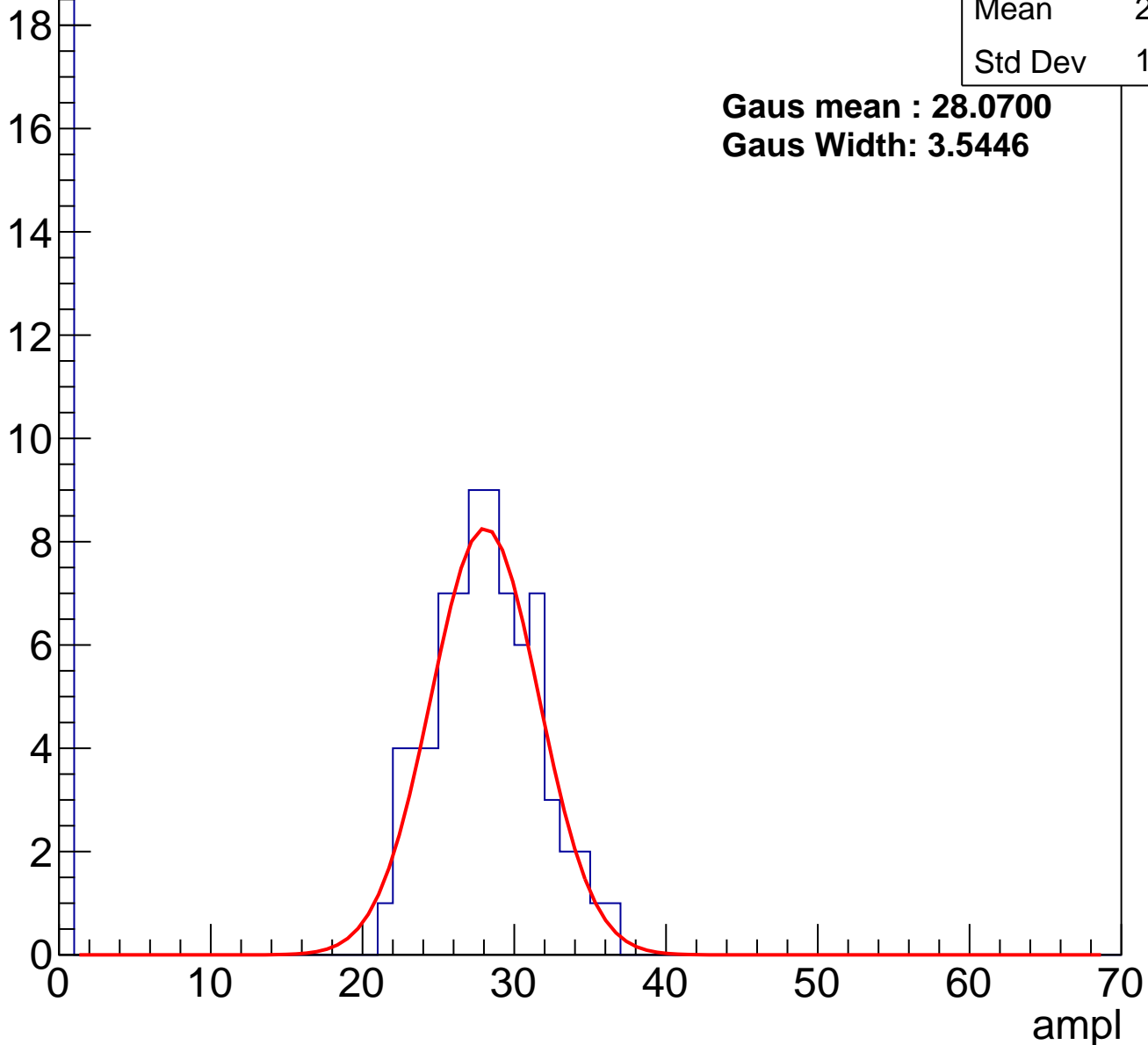
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	22.04
Std Dev	11.56

Gaus mean : 28.0700

Gaus Width: 3.5446

Entry



B1L103S, U2-ch116, adc1

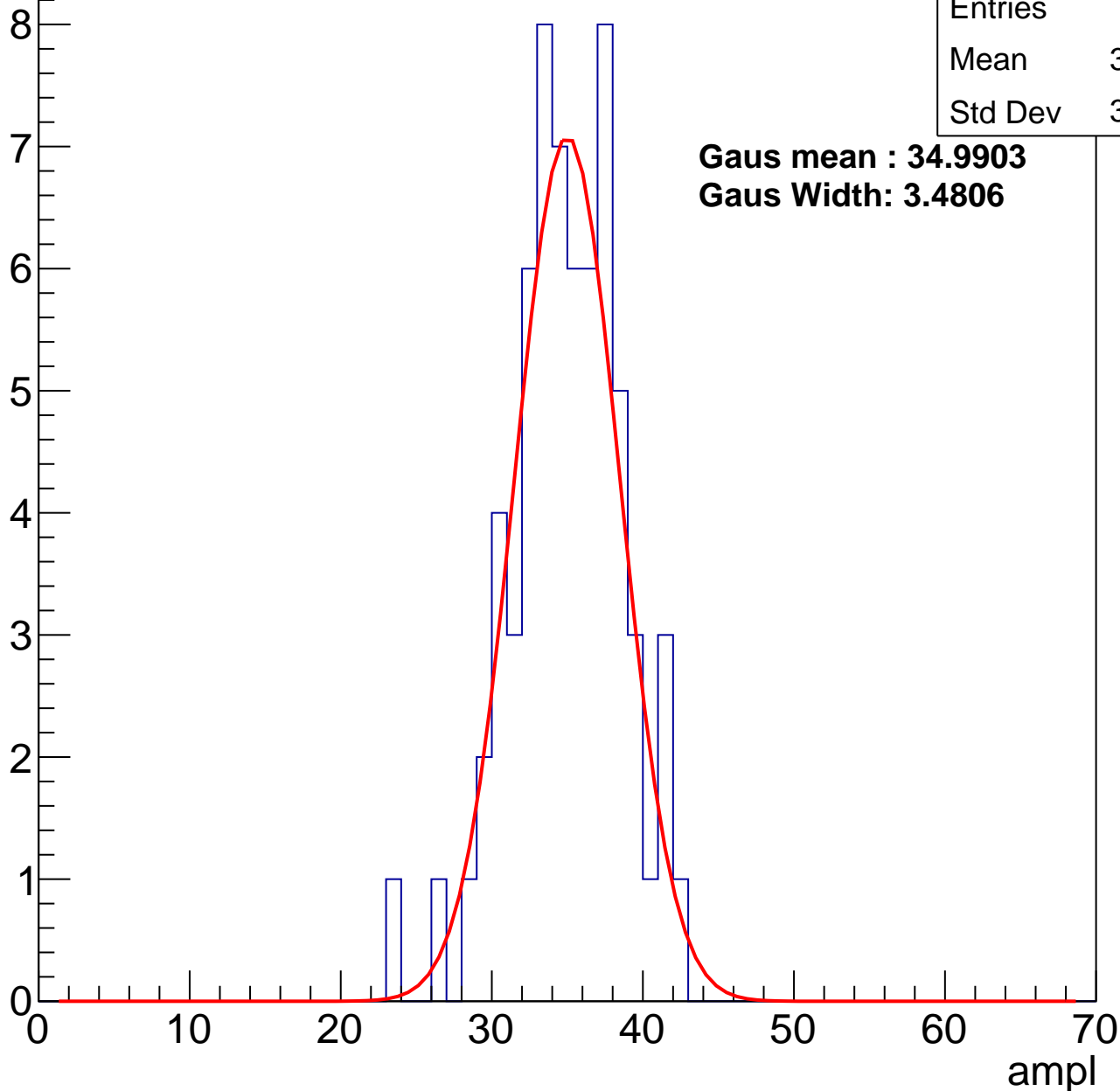
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.48
Std Dev	3.657

Gaus mean : 34.9903

Gaus Width: 3.4806



B1L103S, U2-ch116, adc2

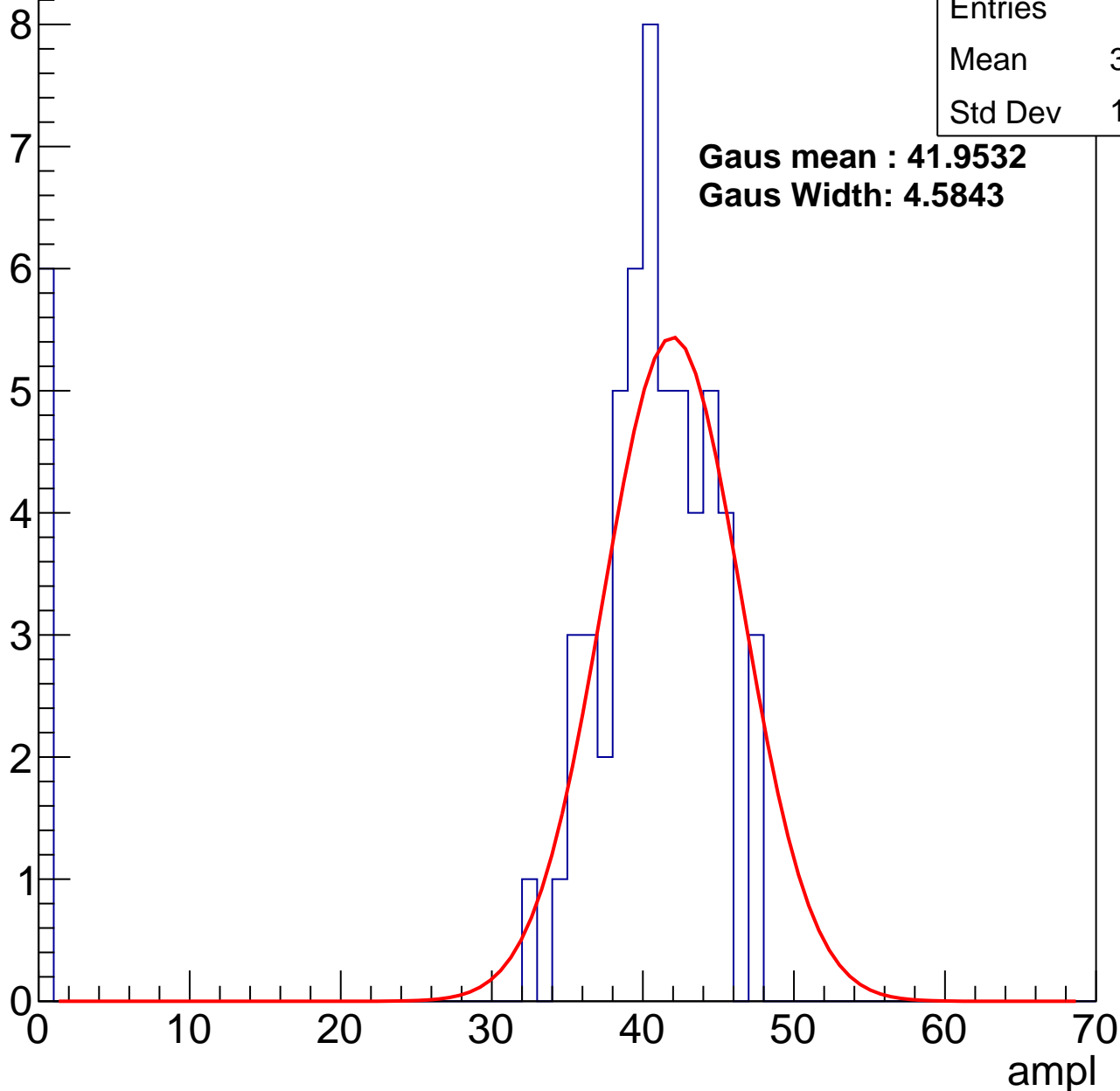
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	36.48
Std Dev	12.48

Gaus mean : 41.9532

Gaus Width: 4.5843

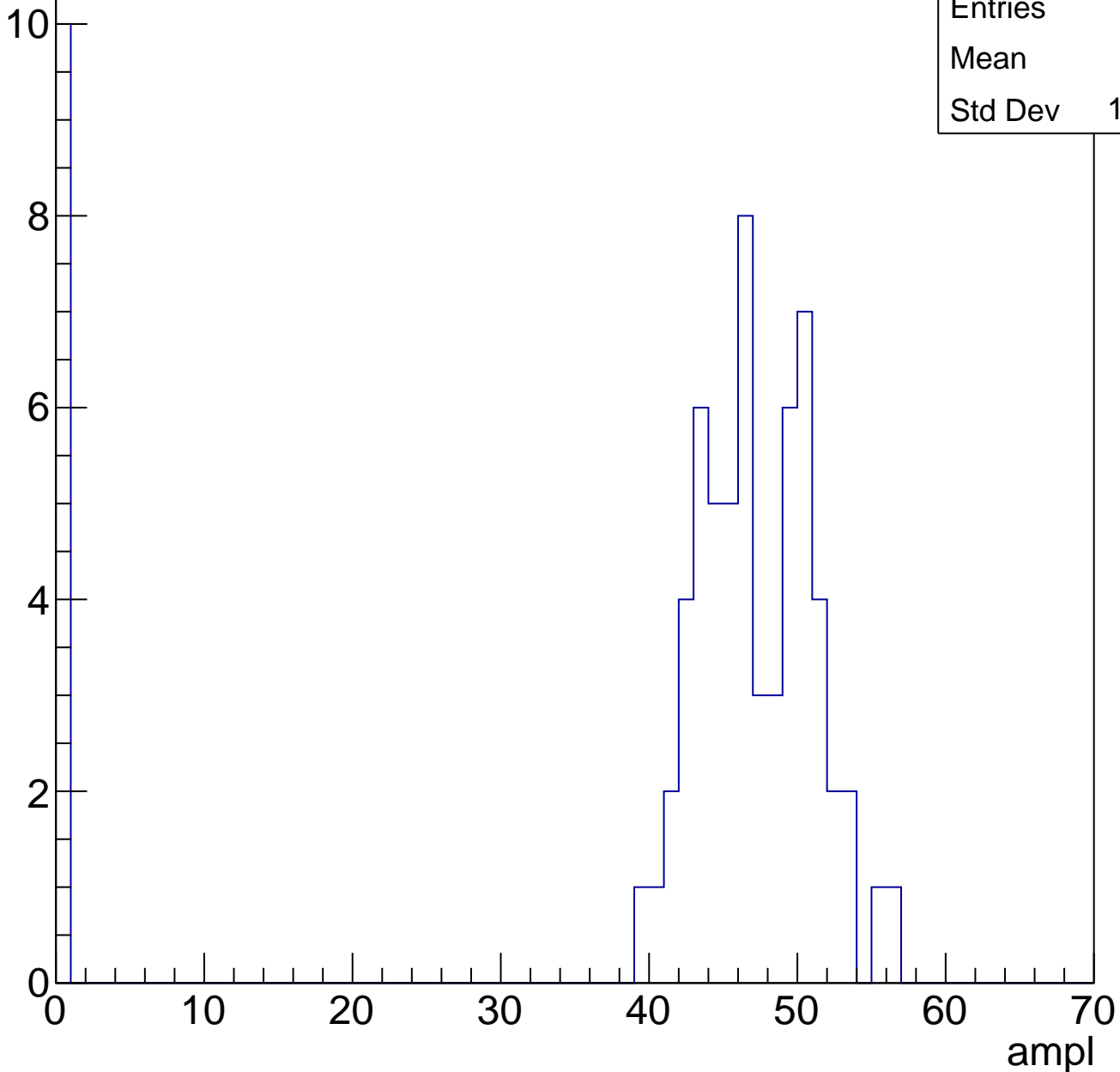


B1L103S, U2-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	40.2
Std Dev	16.65

Entry

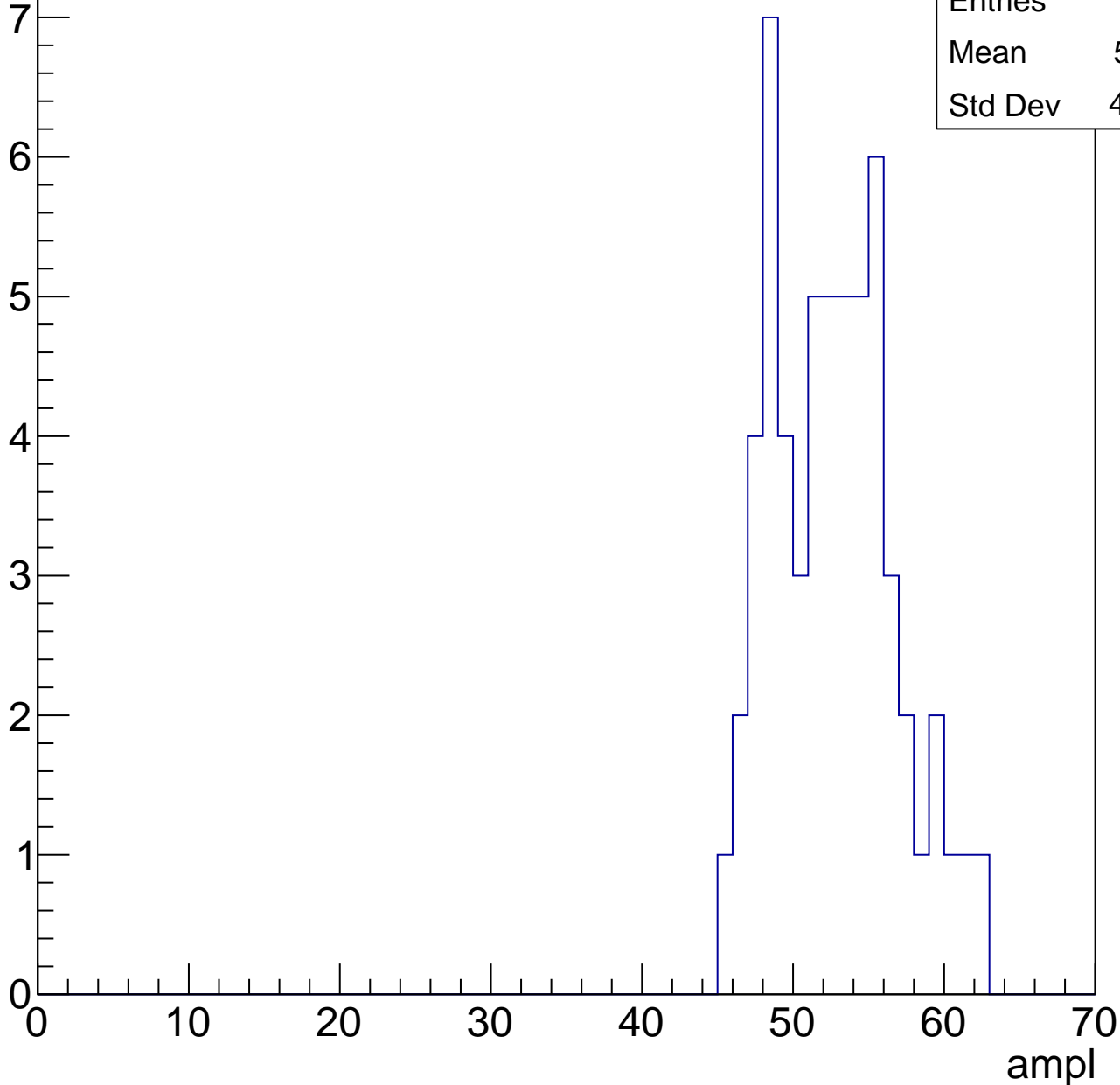


B1L103S, U2-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.21
Std Dev	4.025



B1L103S, U2-ch116, adc5

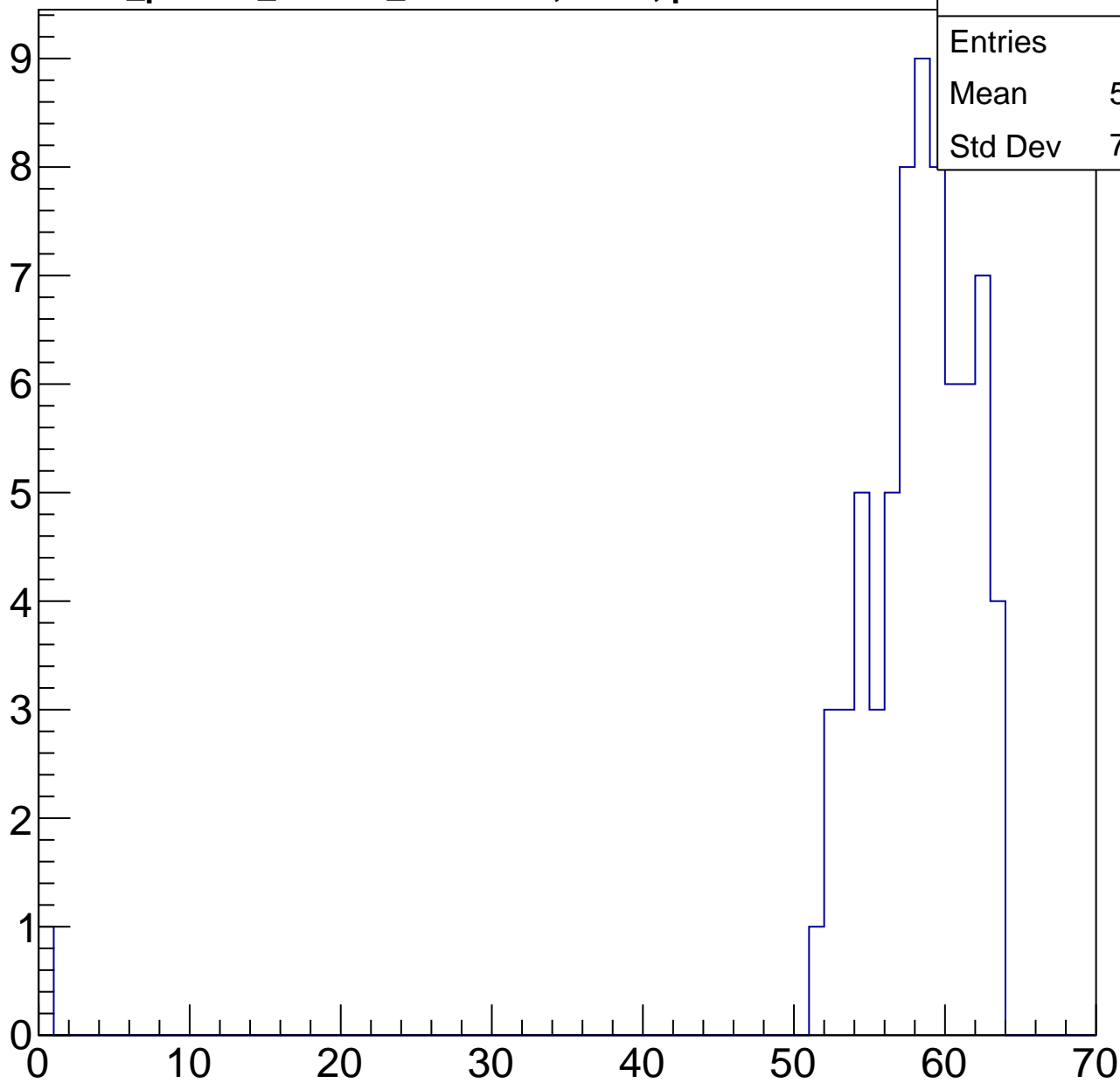
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	57.14
Std Dev	7.593

ampl

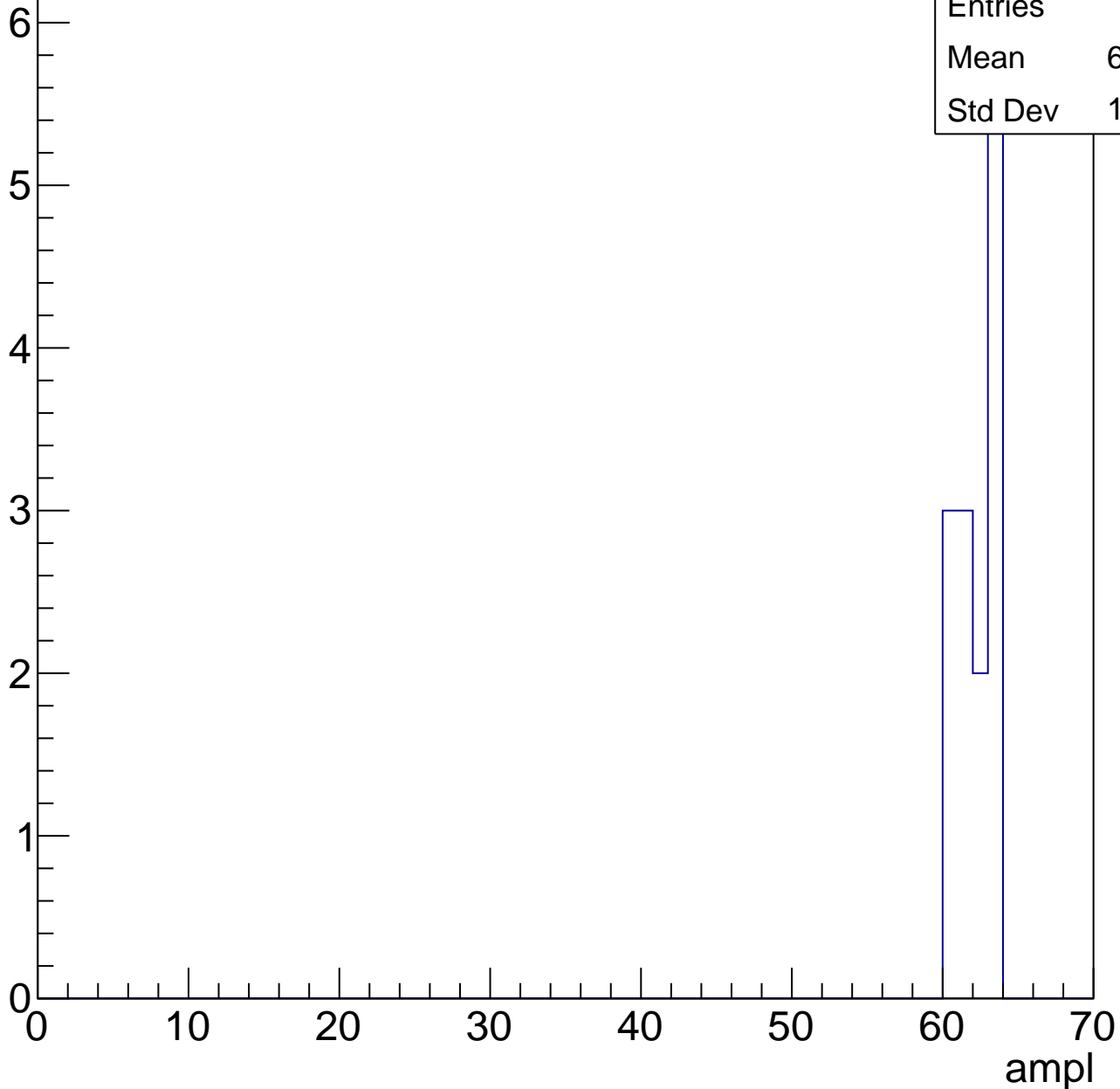


B1L103S, U2-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.79
Std Dev	1.206



B1L103S, U2-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.947
Std Dev	12.98

Entry

16

14

12

10

8

6

4

2

0

0

10

20

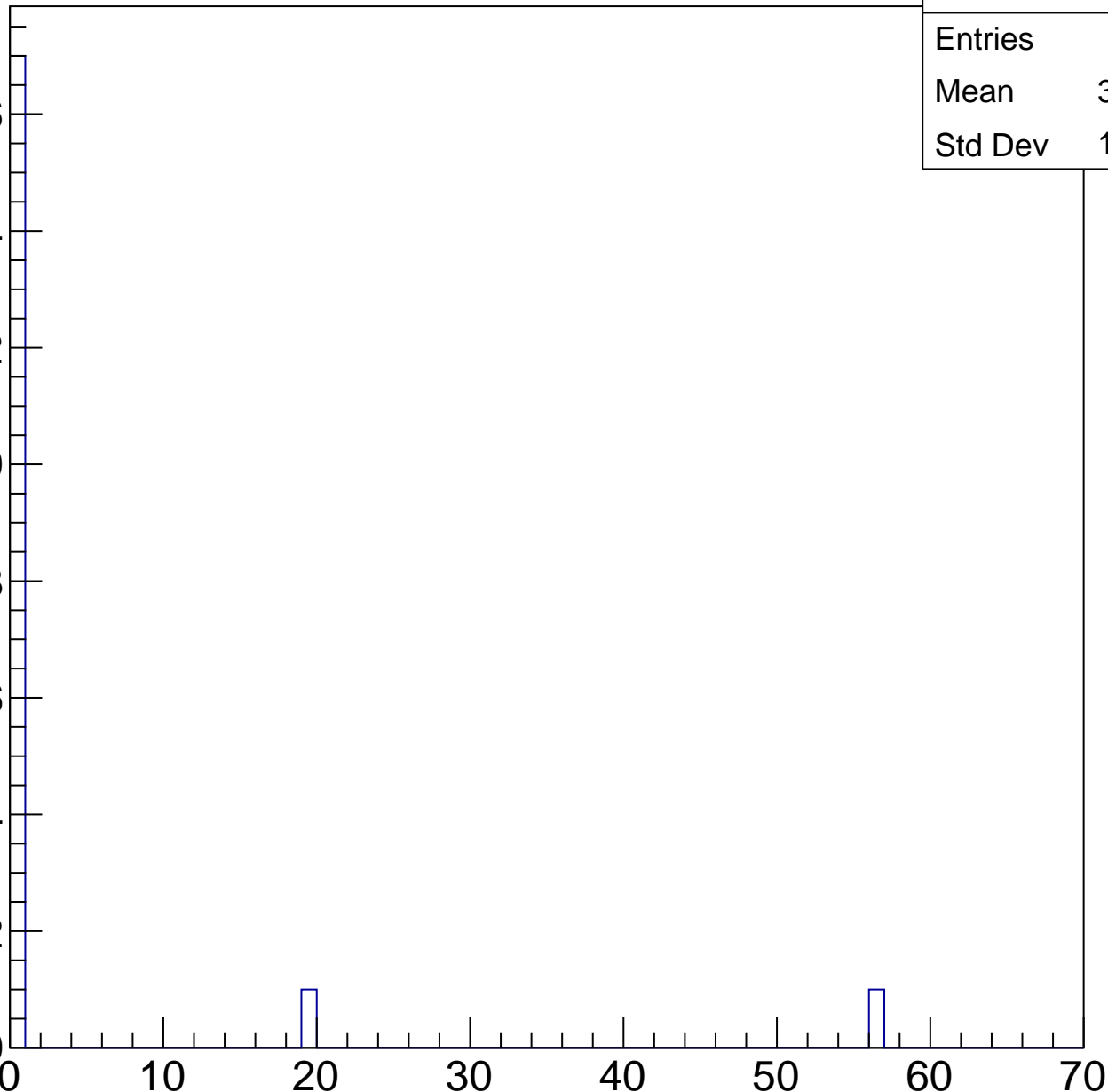
30

40

50

60

ampl



B1L103S, U2-ch117, adc0

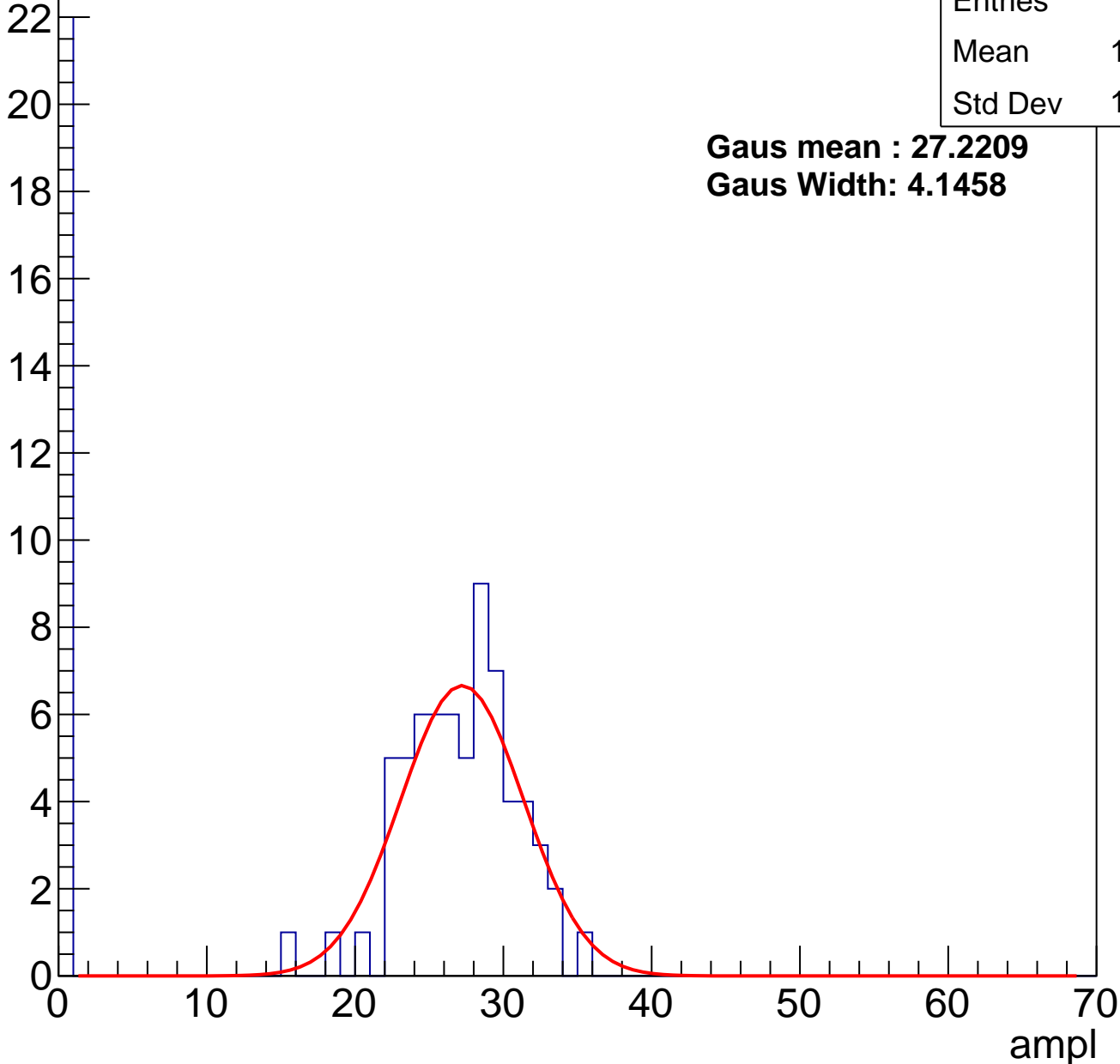
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	19.99
Std Dev	11.98

Gaus mean : 27.2209

Gaus Width: 4.1458

Entry



B1L103S, U2-ch117, adc1

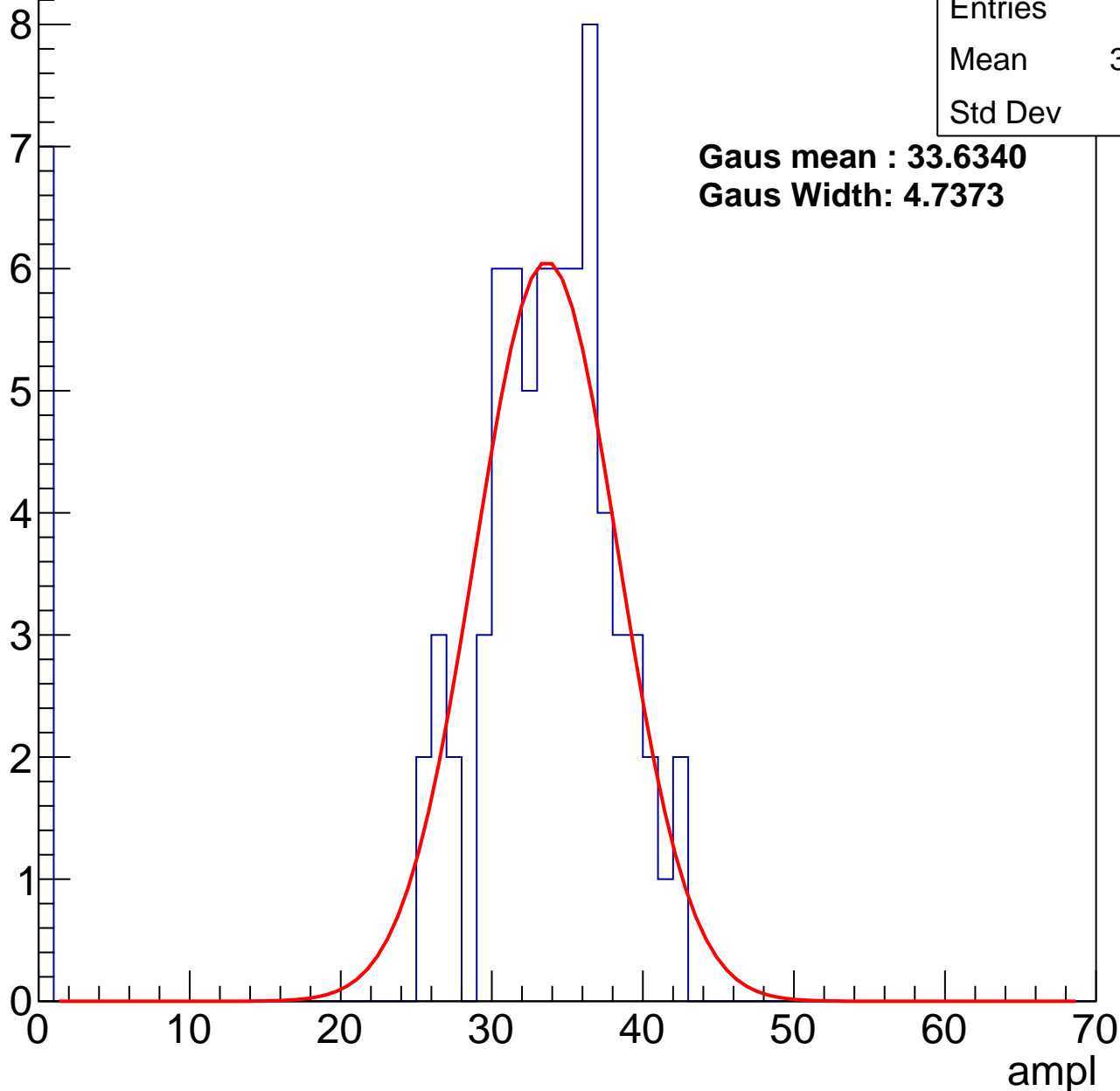
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	30.39
Std Dev	10.5

Gaus mean : 33.6340

Gaus Width: 4.7373



B1L103S, U2-ch117, adc2

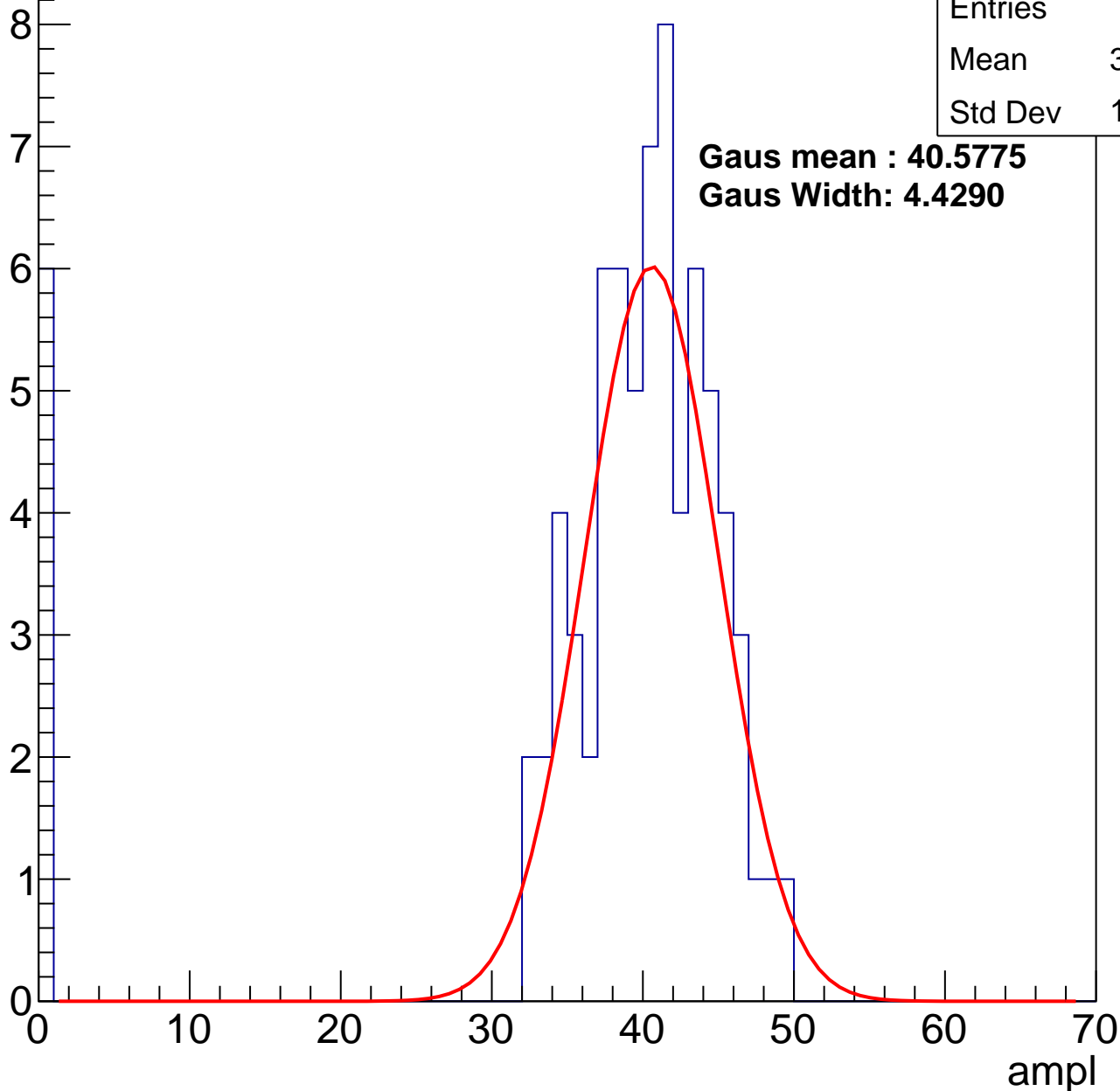
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	36.89
Std Dev	11.46

Gaus mean : 40.5775

Gaus Width: 4.4290

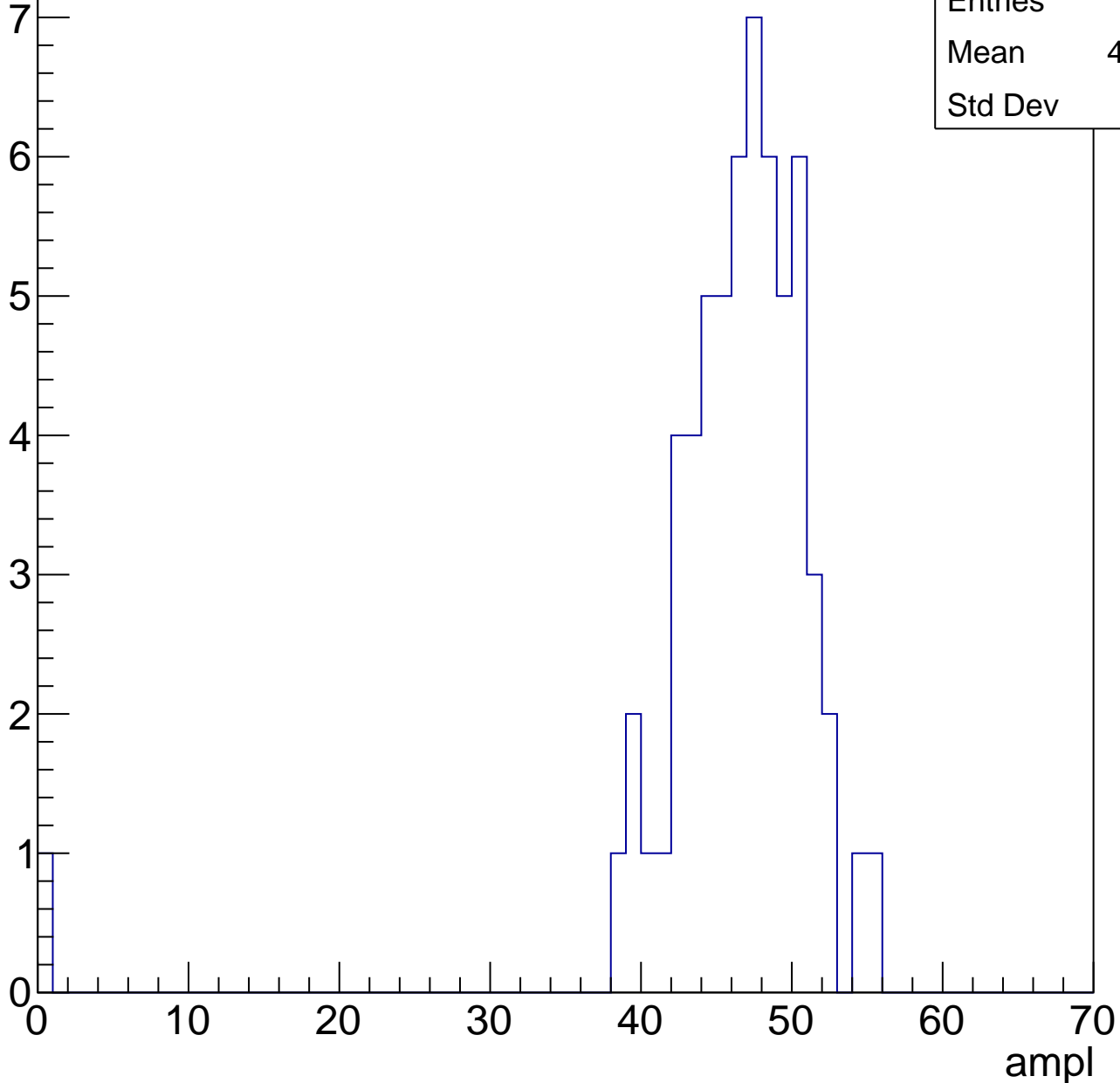


B1L103S, U2-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	45.67
Std Dev	6.92

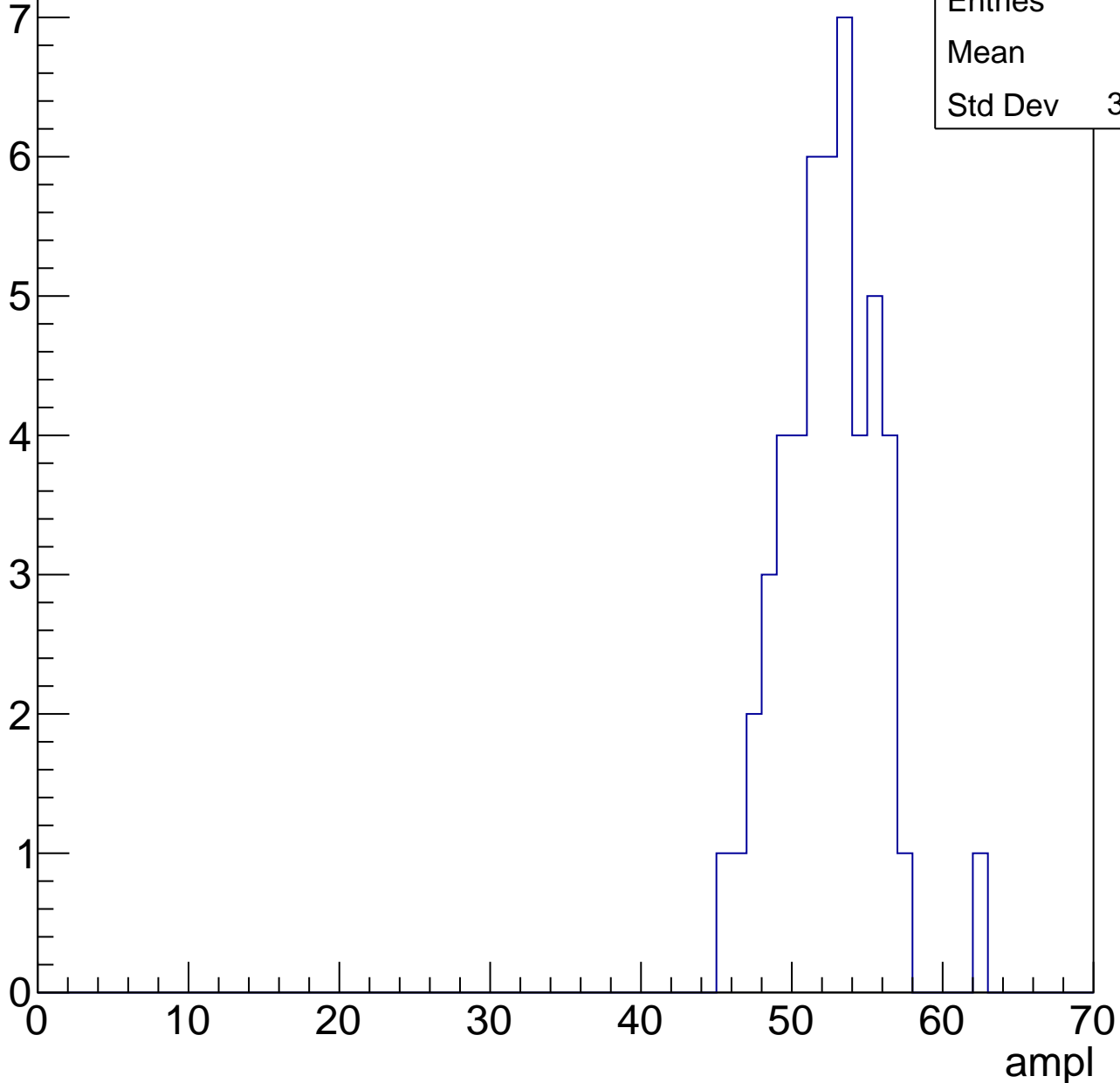


B1L103S, U2-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	52
Std Dev	3.188



B1L103S, U2-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	57.96
Std Dev	2.948

Entry

10

8

6

4

2

0

0

10

20

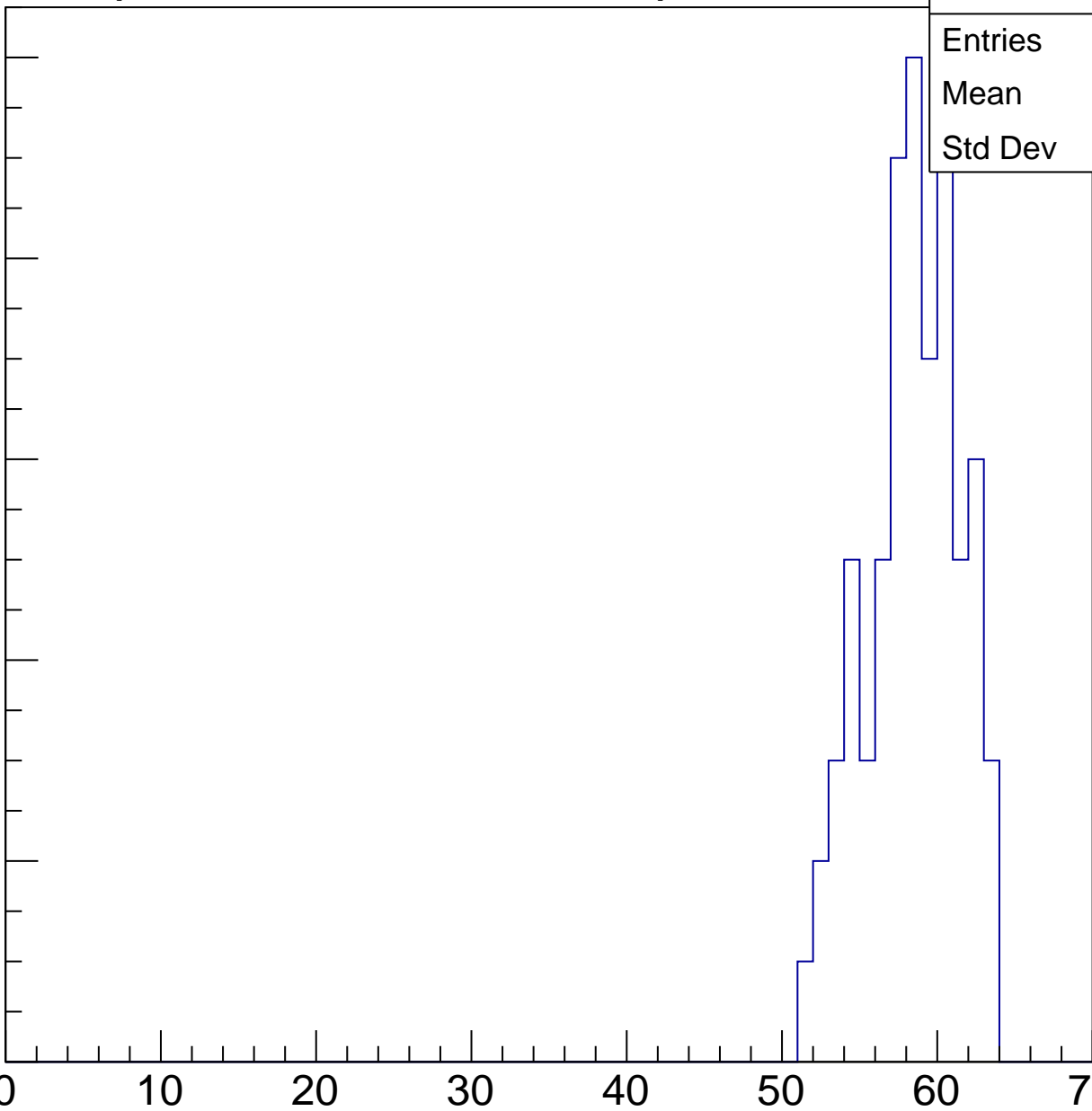
30

40

50

60

ampl

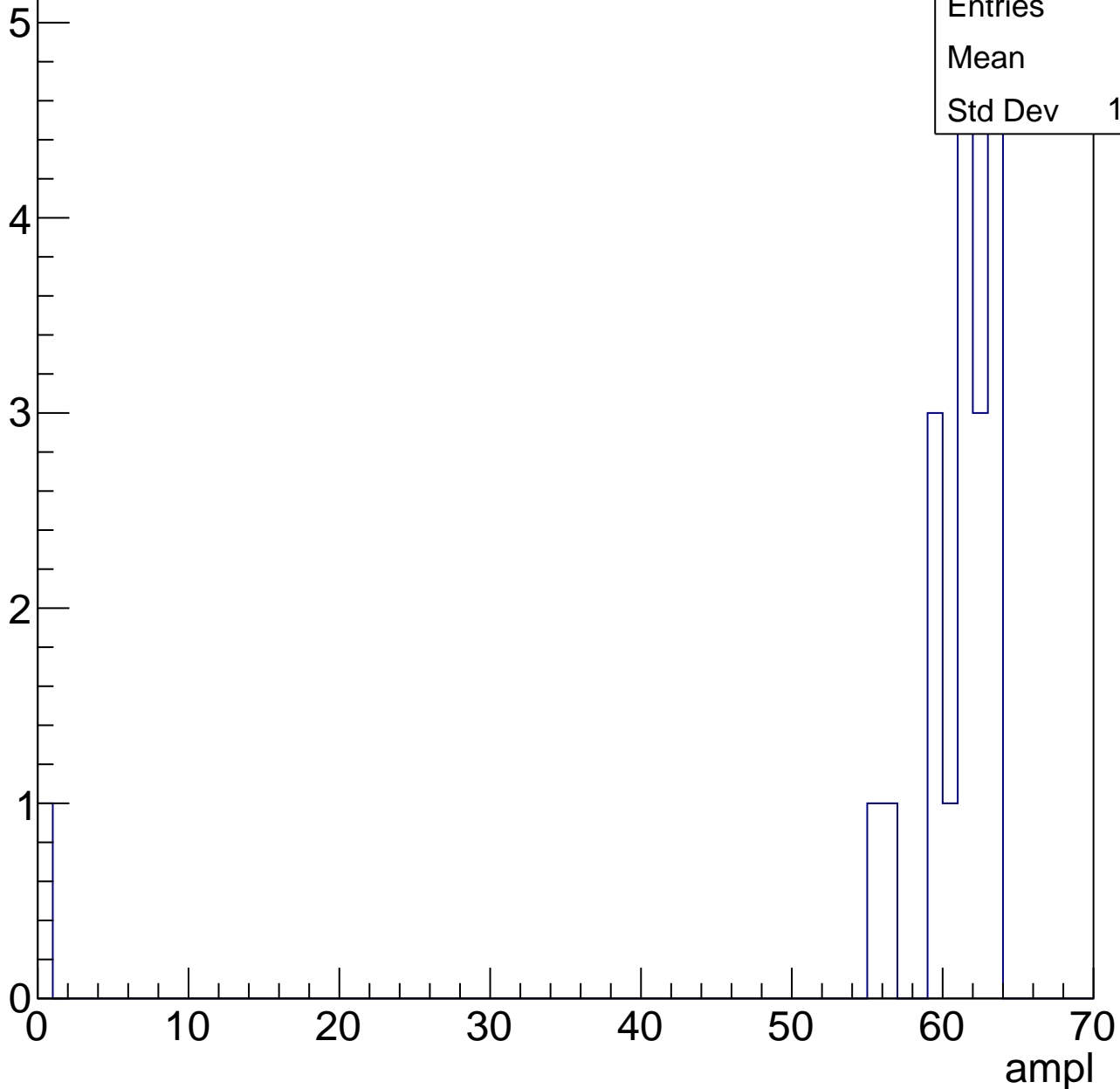


B1L103S, U2-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	57.7
Std Dev	13.42



B1L103S, U2-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U2-ch118, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	19.79
Std Dev	12.65

Gaus mean : 28.3085

Gaus Width: 3.8923

Entry

25

20

15

10

5

0

0

10

20

30

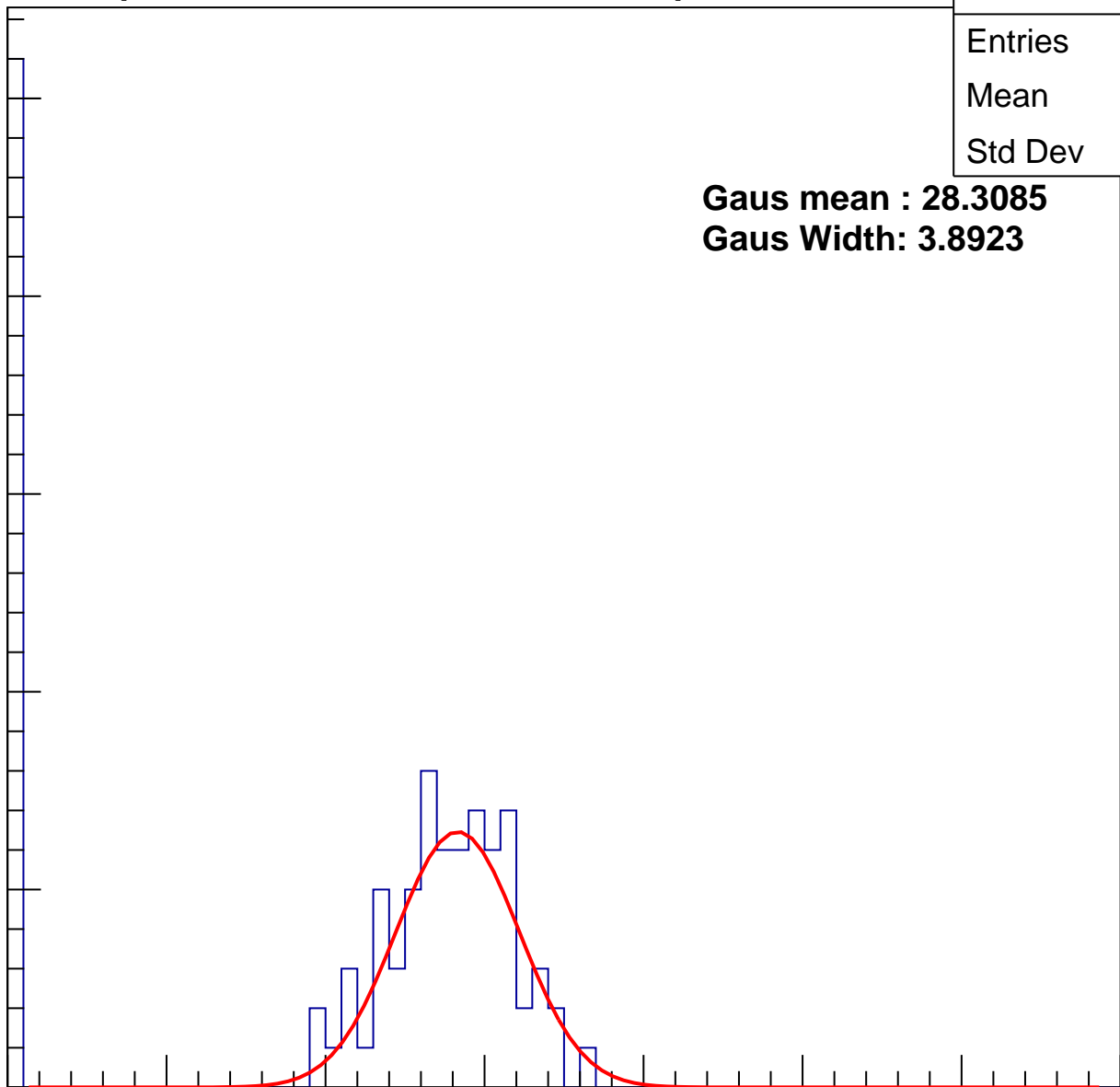
40

50

60

70

ampl



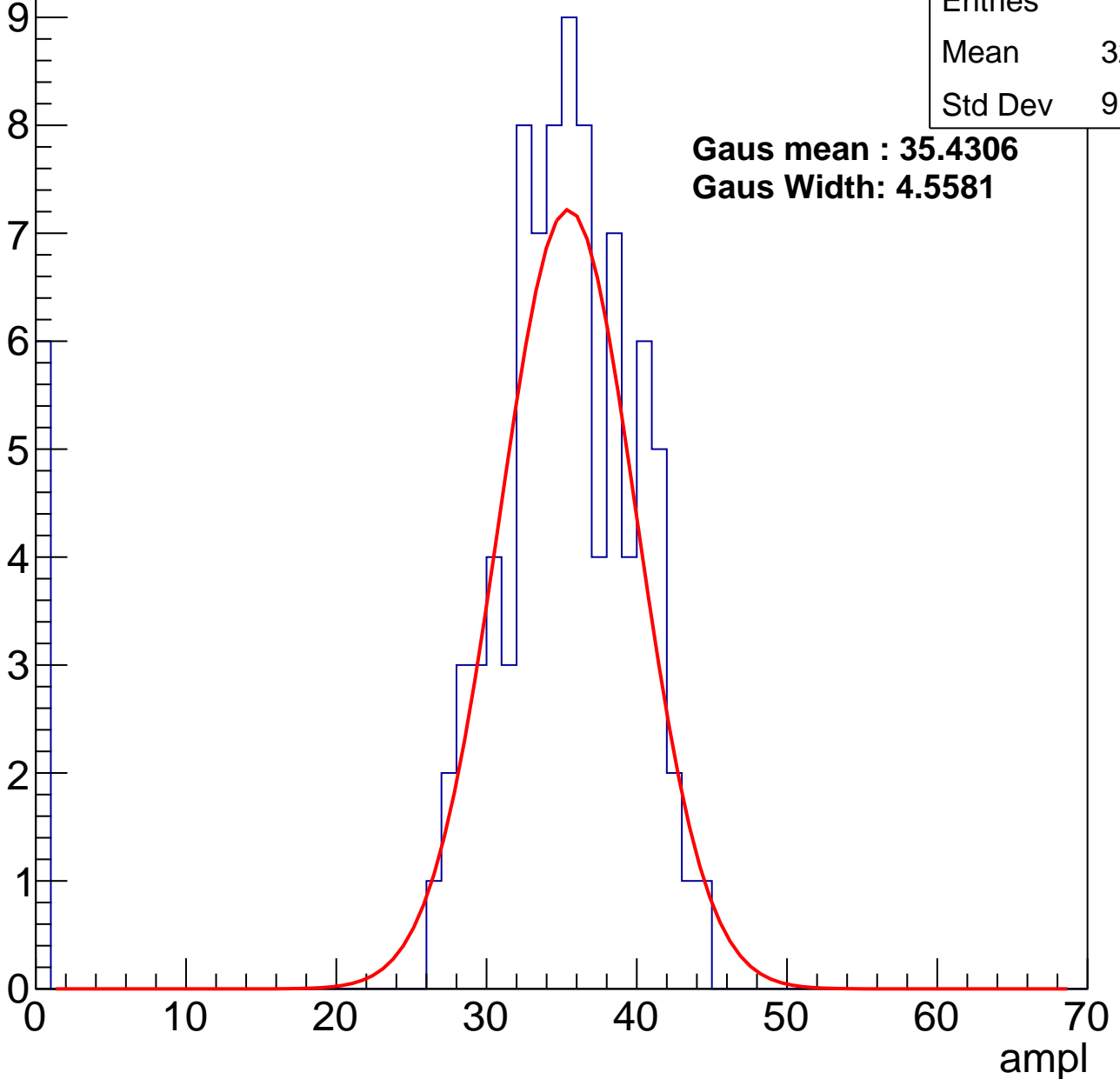
B1L103S, U2-ch118, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	92
Mean	32.74
Std Dev	9.512

Gaus mean : 35.4306
Gaus Width: 4.5581



B1L103S, U2-ch118, adc2

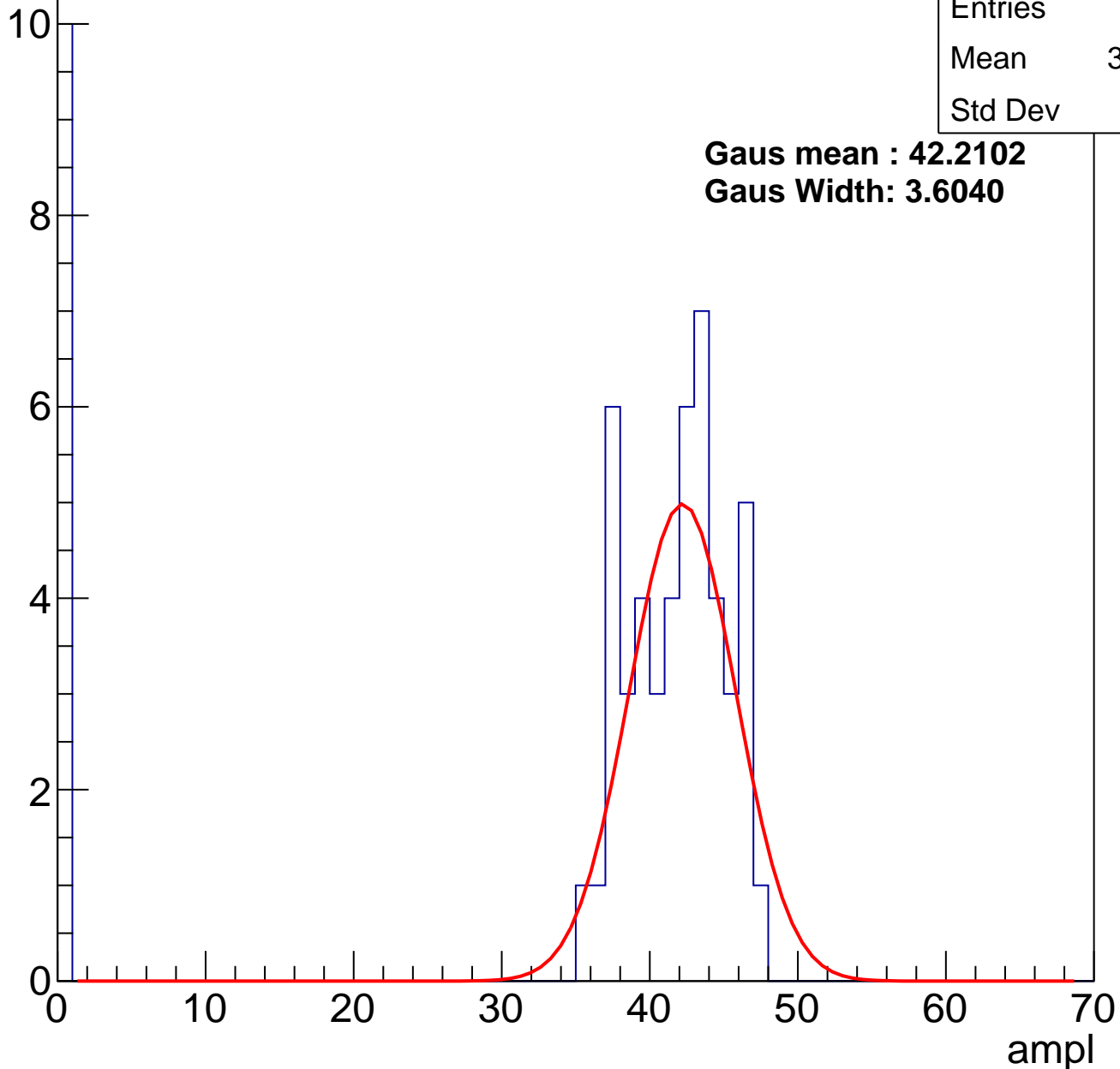
calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	34.28
Std Dev	15.9

Gaus mean : 42.2102

Gaus Width: 3.6040

Entry

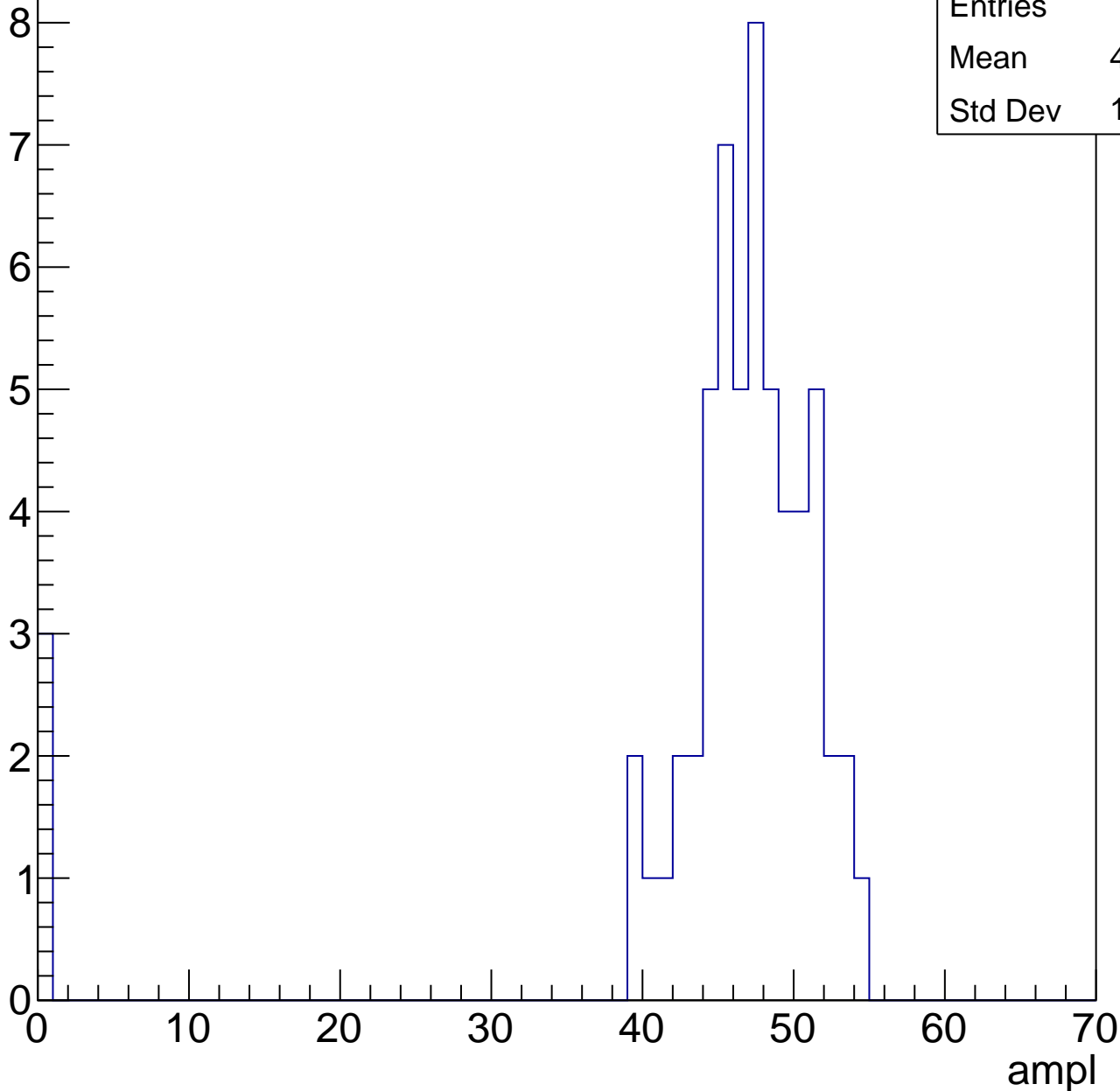


B1L103S, U2-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	44.49
Std Dev	10.84

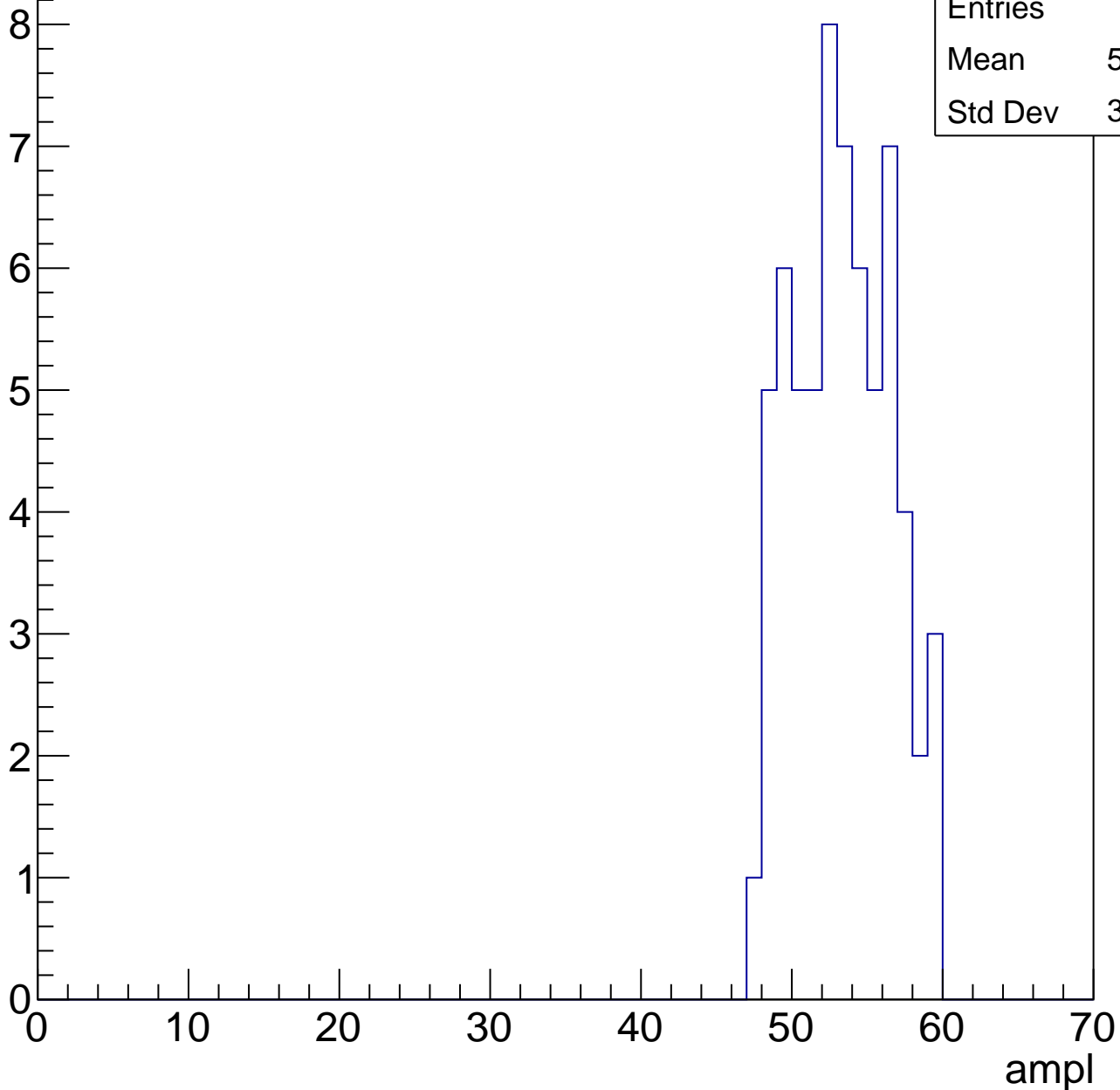


B1L103S, U2-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	52.89
Std Dev	3.163

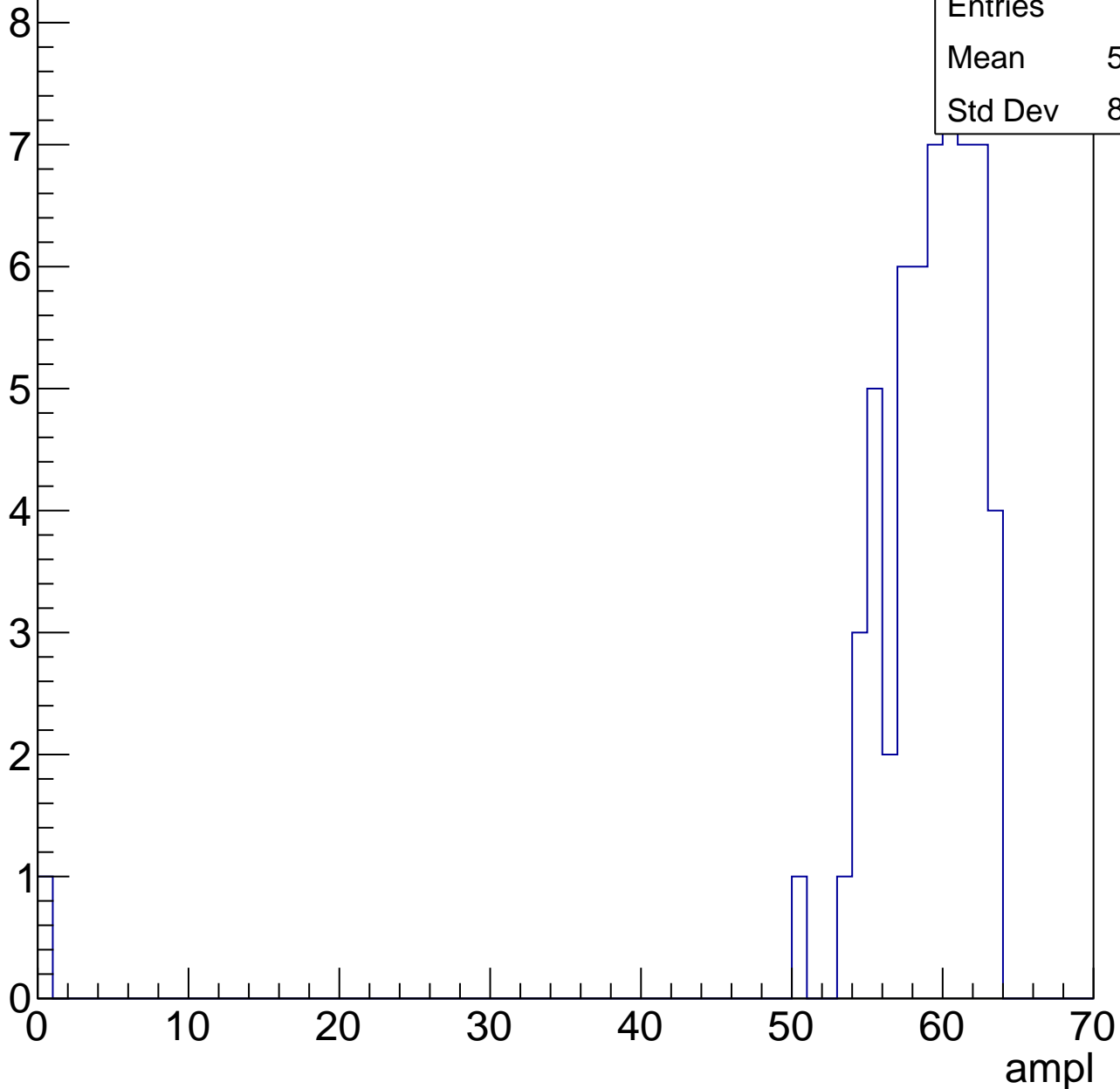


B1L103S, U2-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.72
Std Dev	8.168

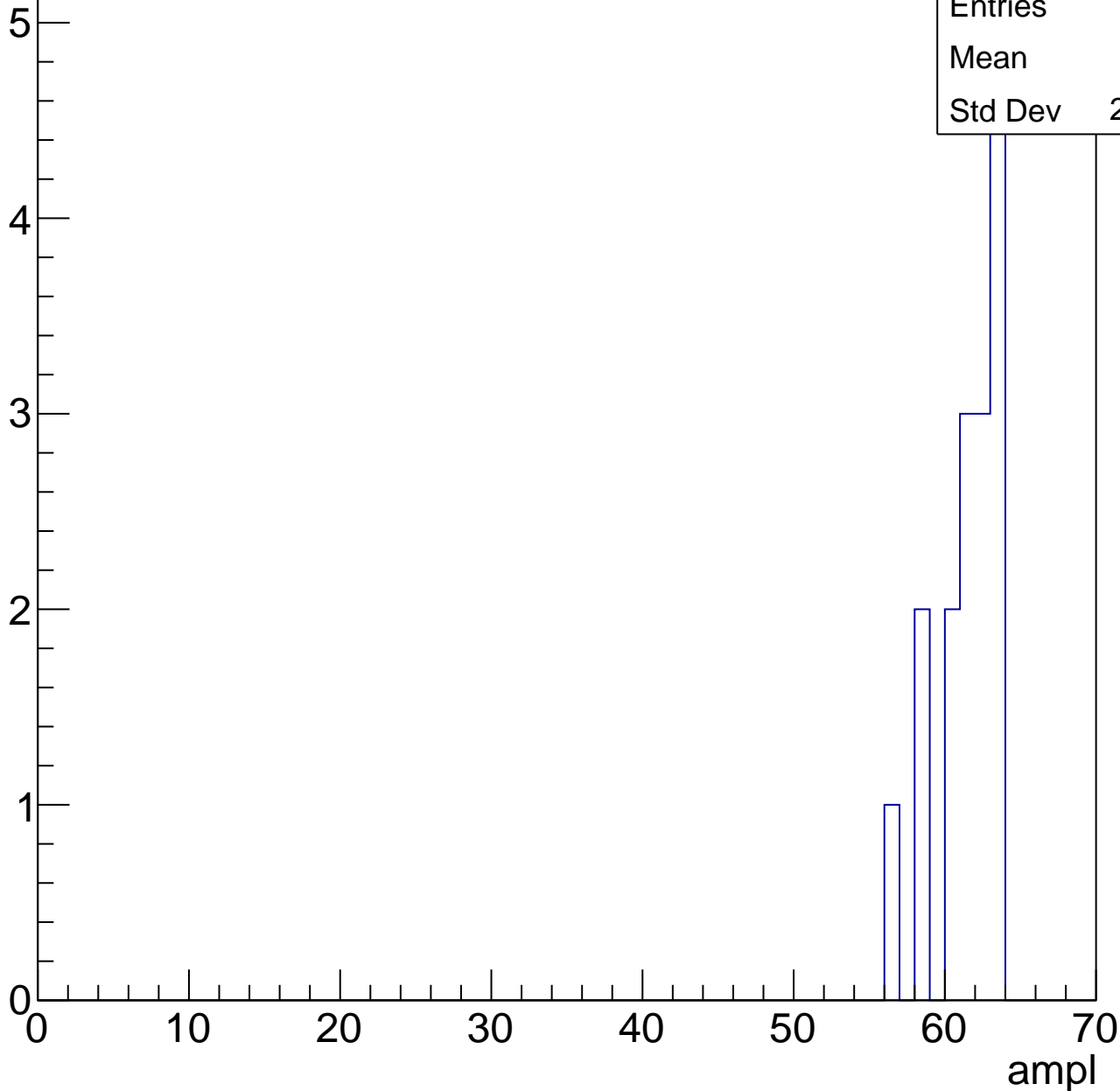


B1L103S, U2-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61
Std Dev	2.062



B1L103S, U2-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

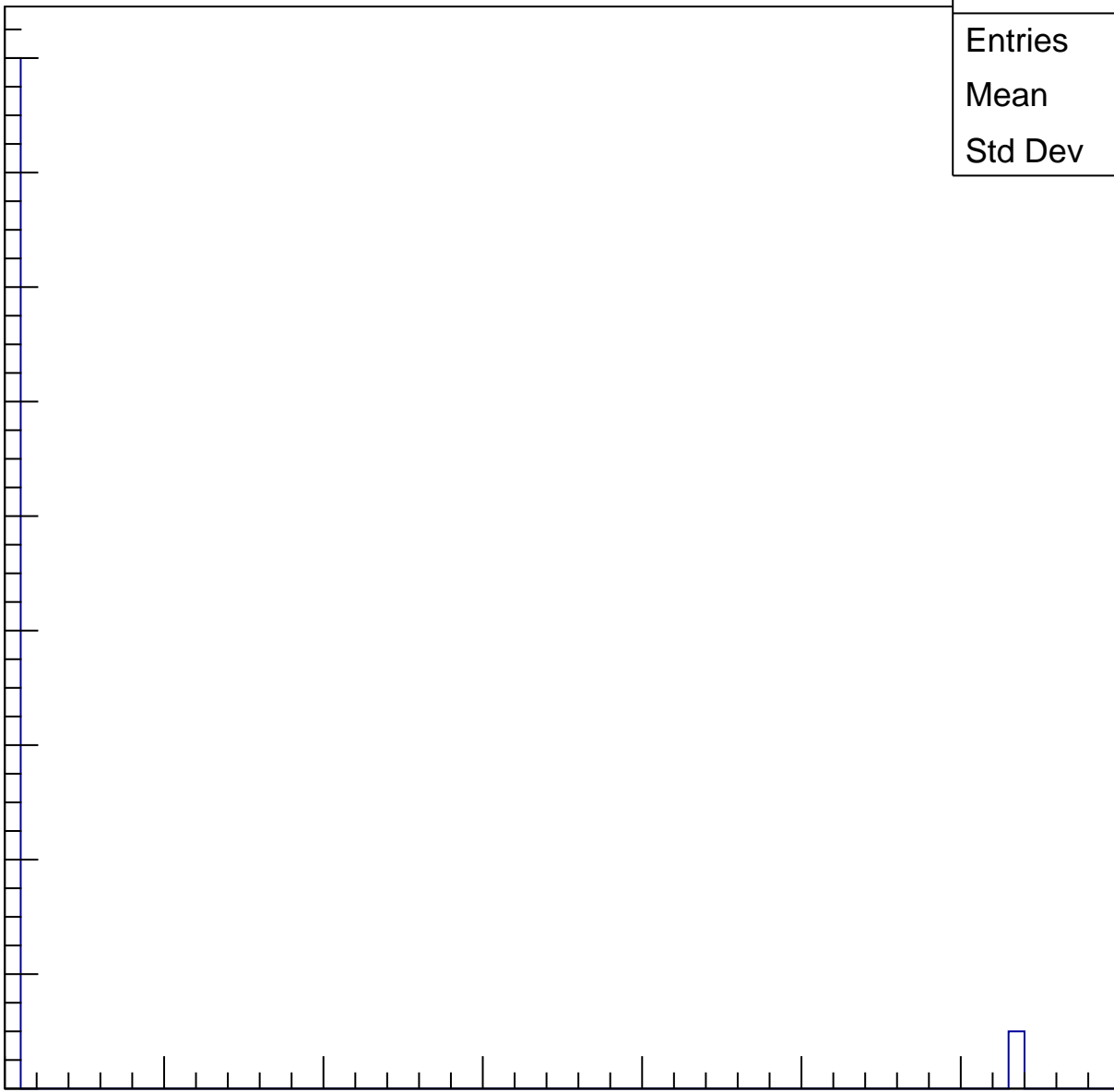
Entries	19
Mean	3.316
Std Dev	14.07

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



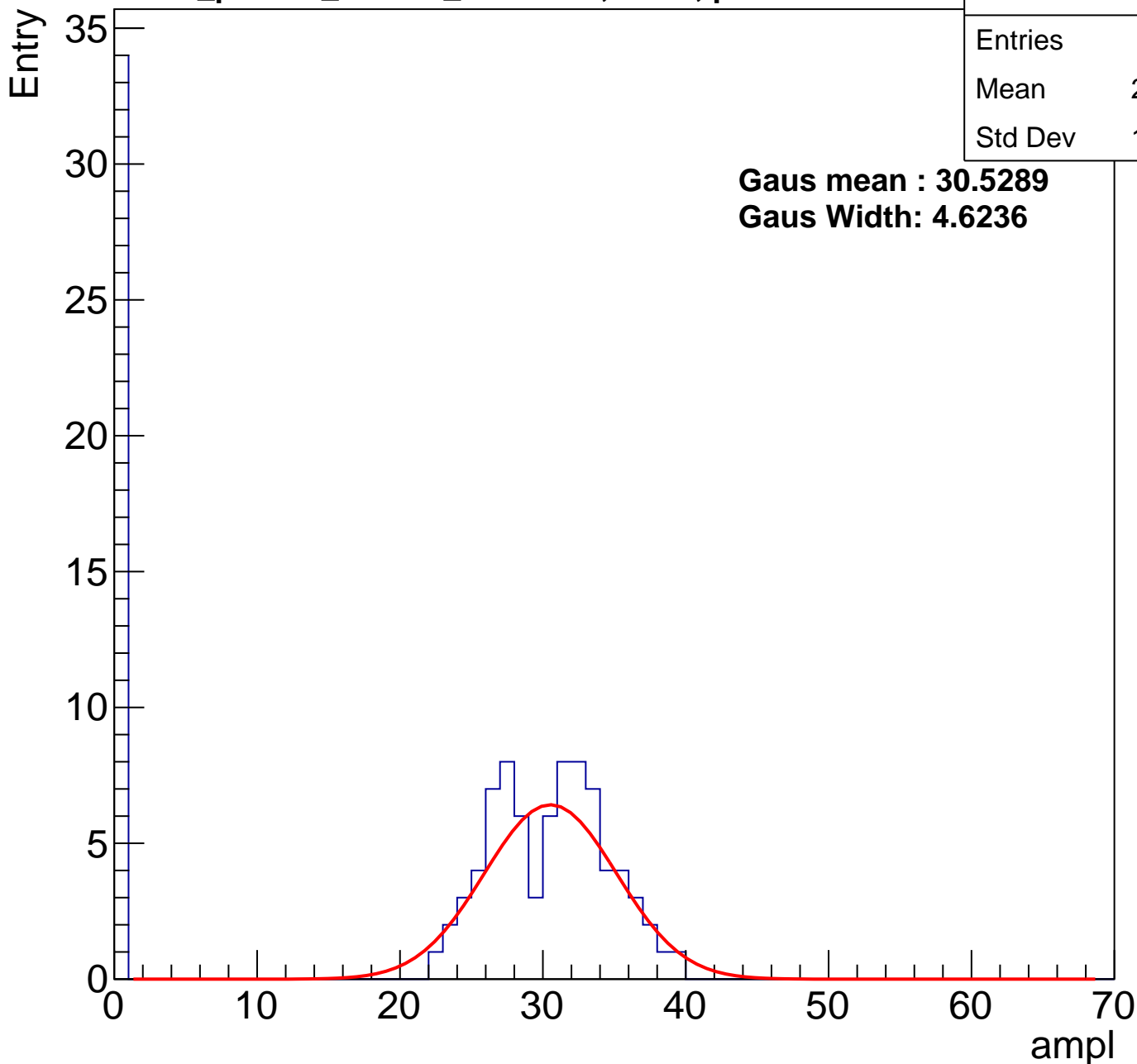
B1L103S, U2-ch119, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	112
Mean	20.92
Std Dev	14.19

Gaus mean : 30.5289

Gaus Width: 4.6236



B1L103S, U2-ch119, adc1

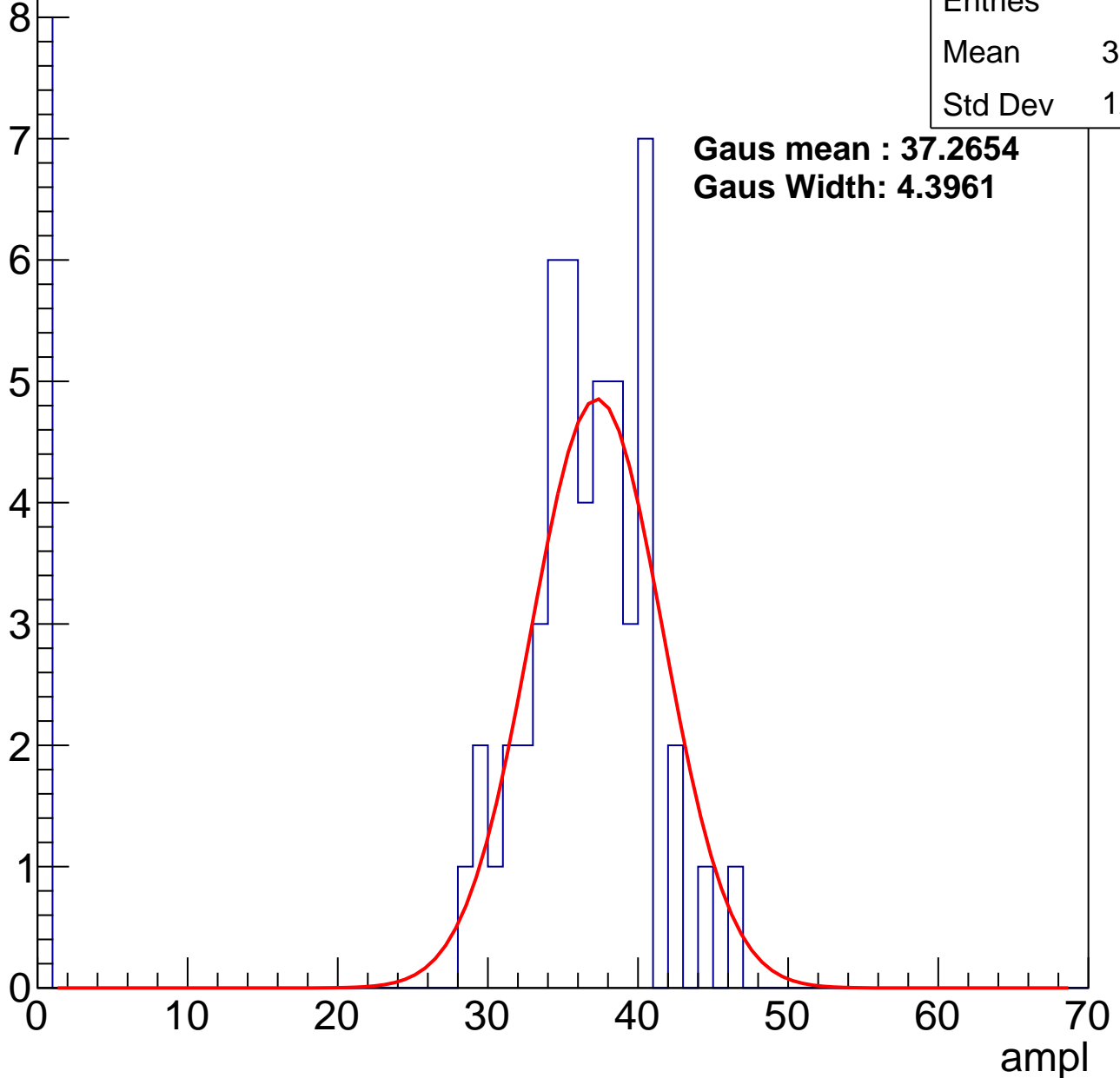
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	31.27
Std Dev	12.88

Gaus mean : 37.2654

Gaus Width: 4.3961



B1L103S, U2-ch119, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	39.01
Std Dev	12.16

Gaus mean : 43.2307

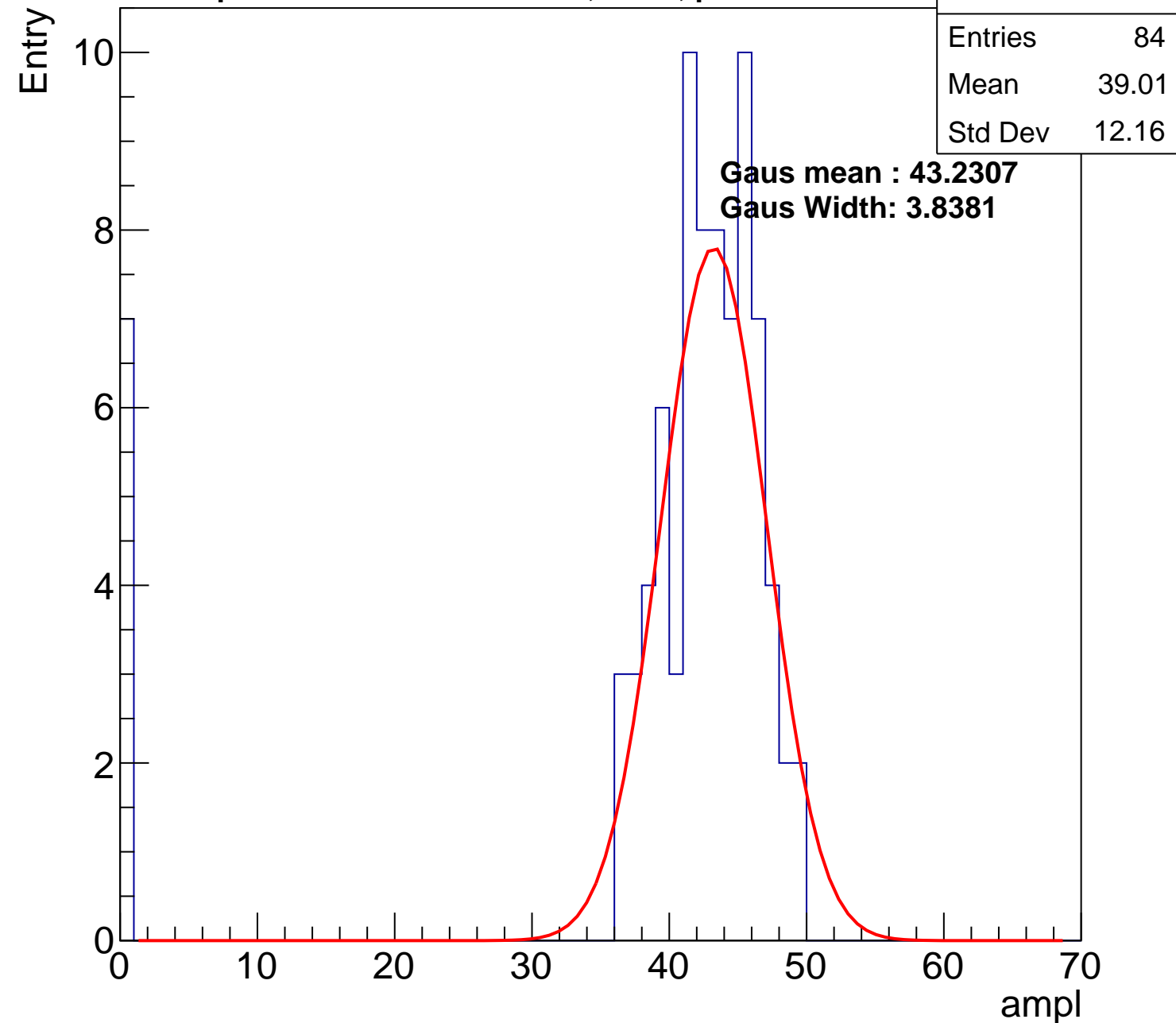
Gaus Width: 3.8381

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

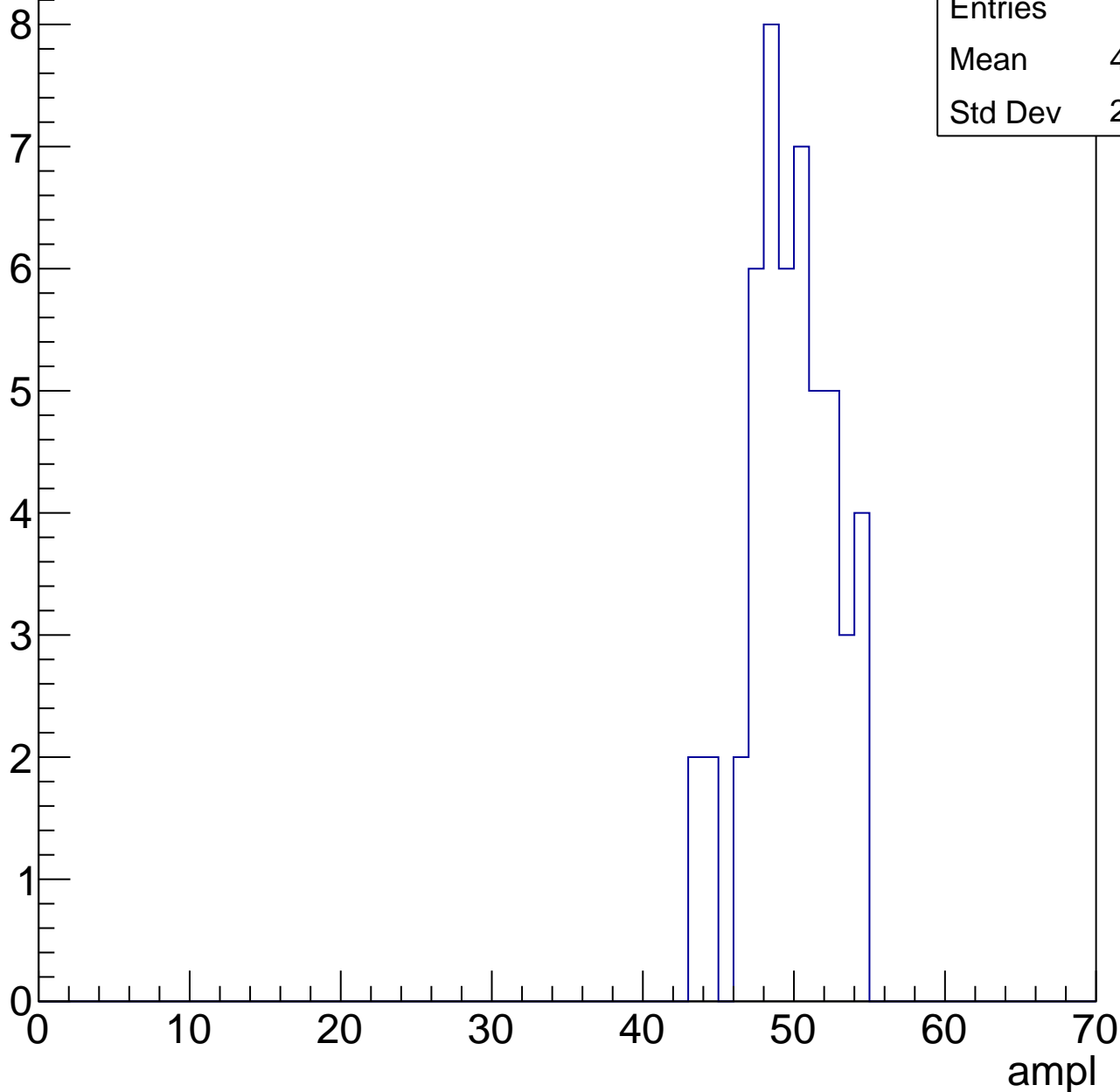


B1L103S, U2-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

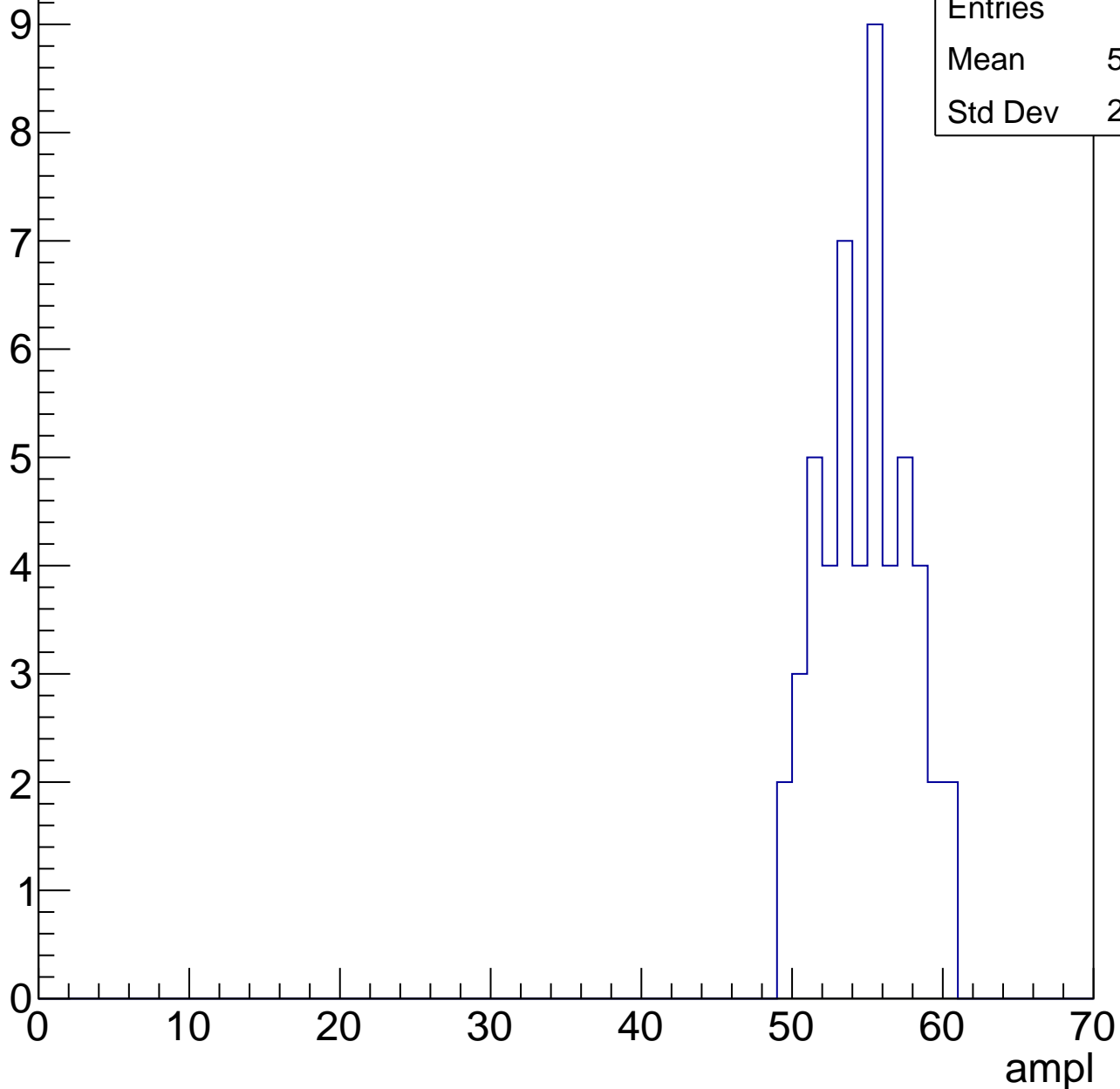
Entries	50
Mean	49.32
Std Dev	2.782



B1L103S, U2-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

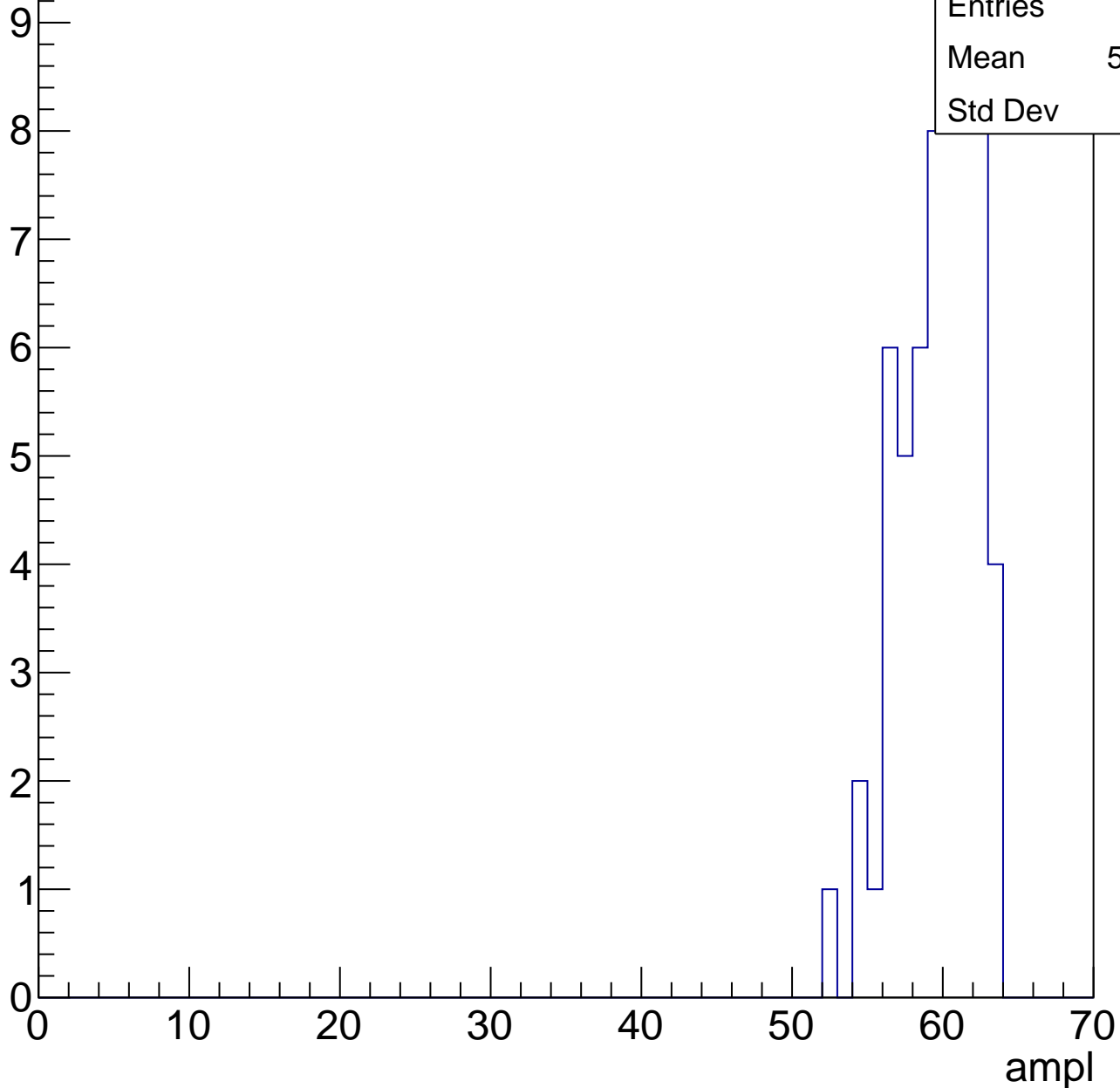


Entries	51
Mean	54.35
Std Dev	2.855

B1L103S, U2-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

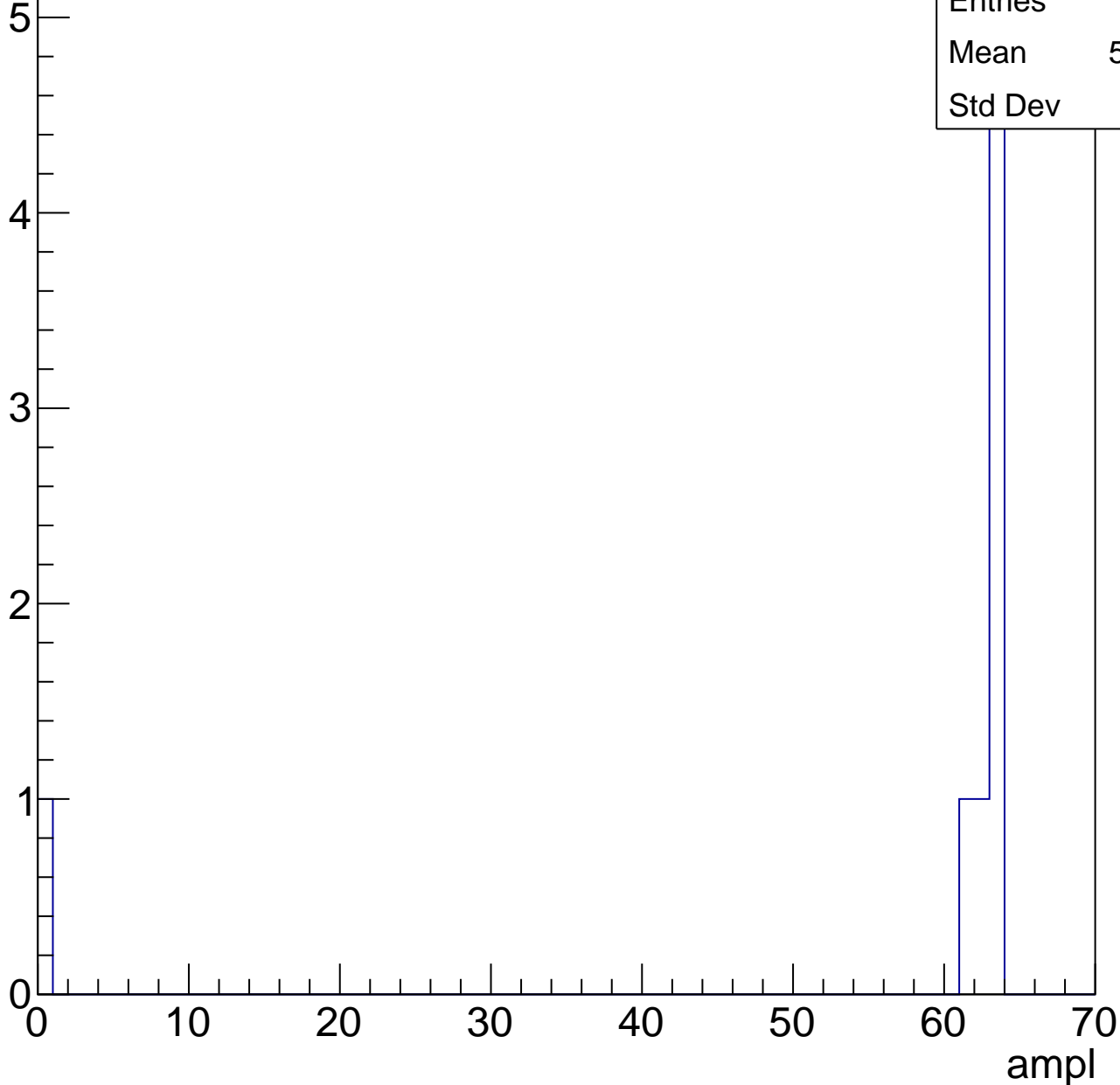


B1L103S, U2-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	8
Mean	54.75
Std Dev	20.7



B1L103S, U2-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U2-ch120, adc0

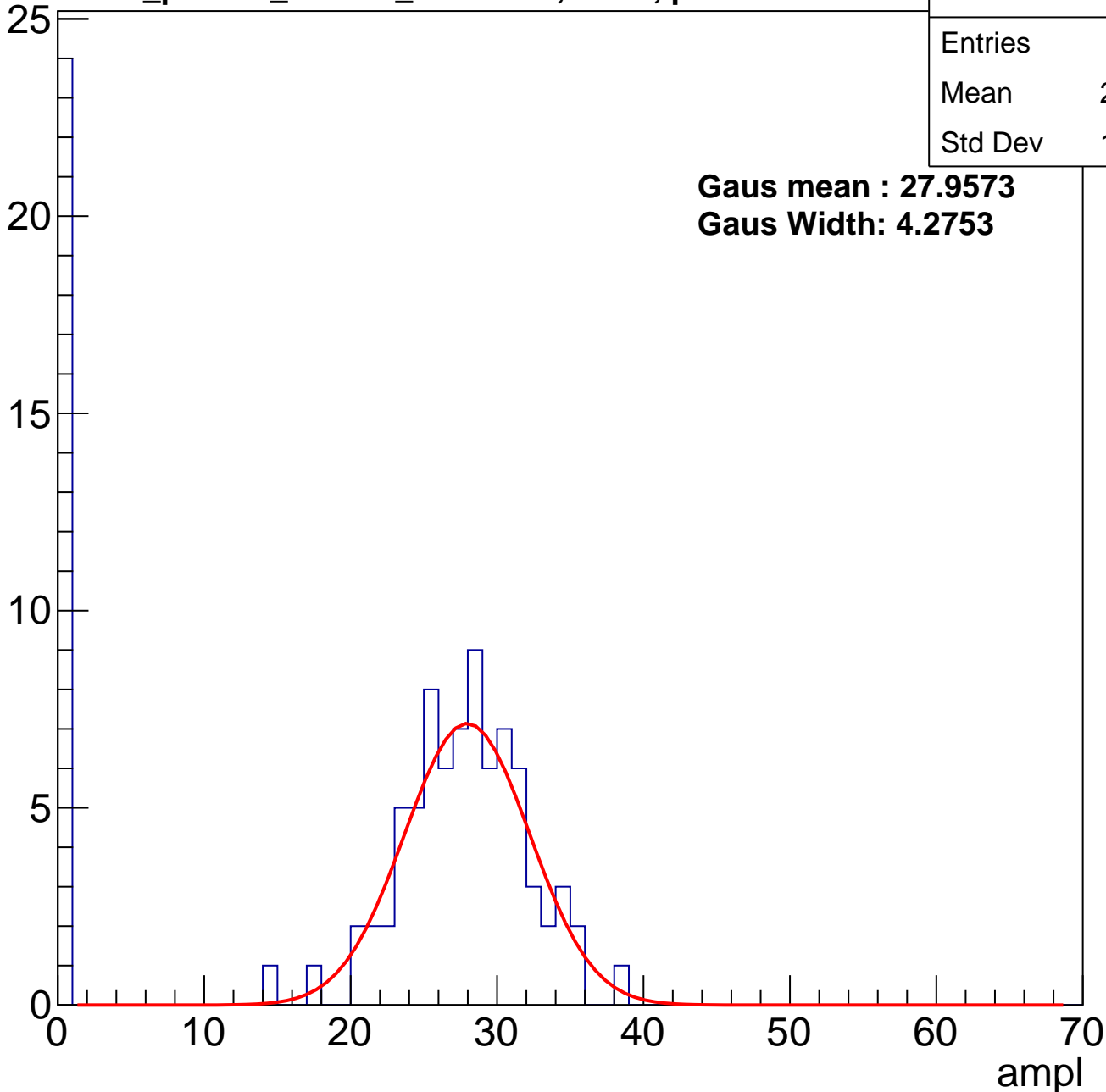
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	20.89
Std Dev	12.16

Gaus mean : 27.9573

Gaus Width: 4.2753

Entry



B1L103S, U2-ch120, adc1

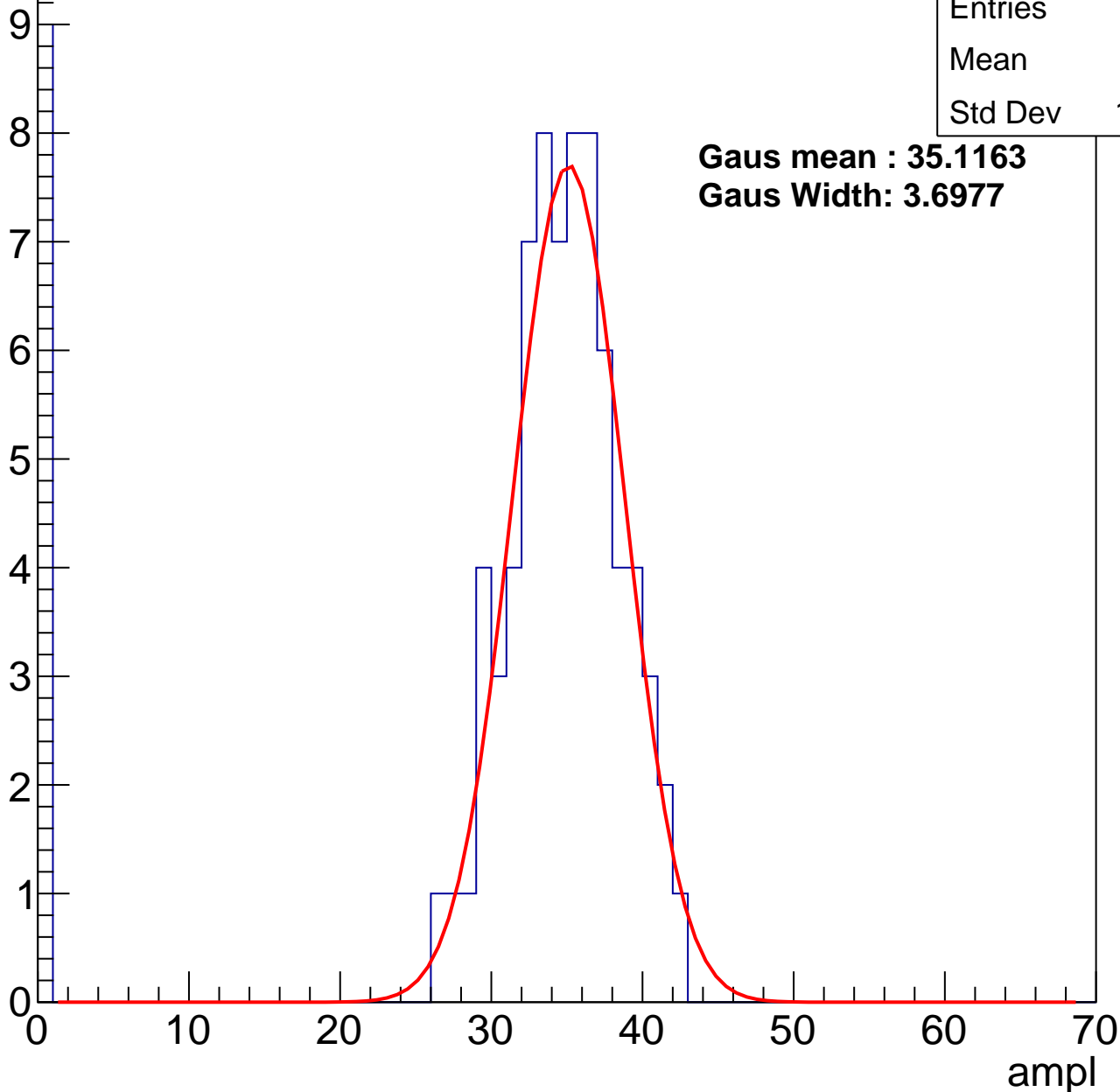
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	30.6
Std Dev	11.31

Gaus mean : 35.1163

Gaus Width: 3.6977



B1L103S, U2-ch120, adc2

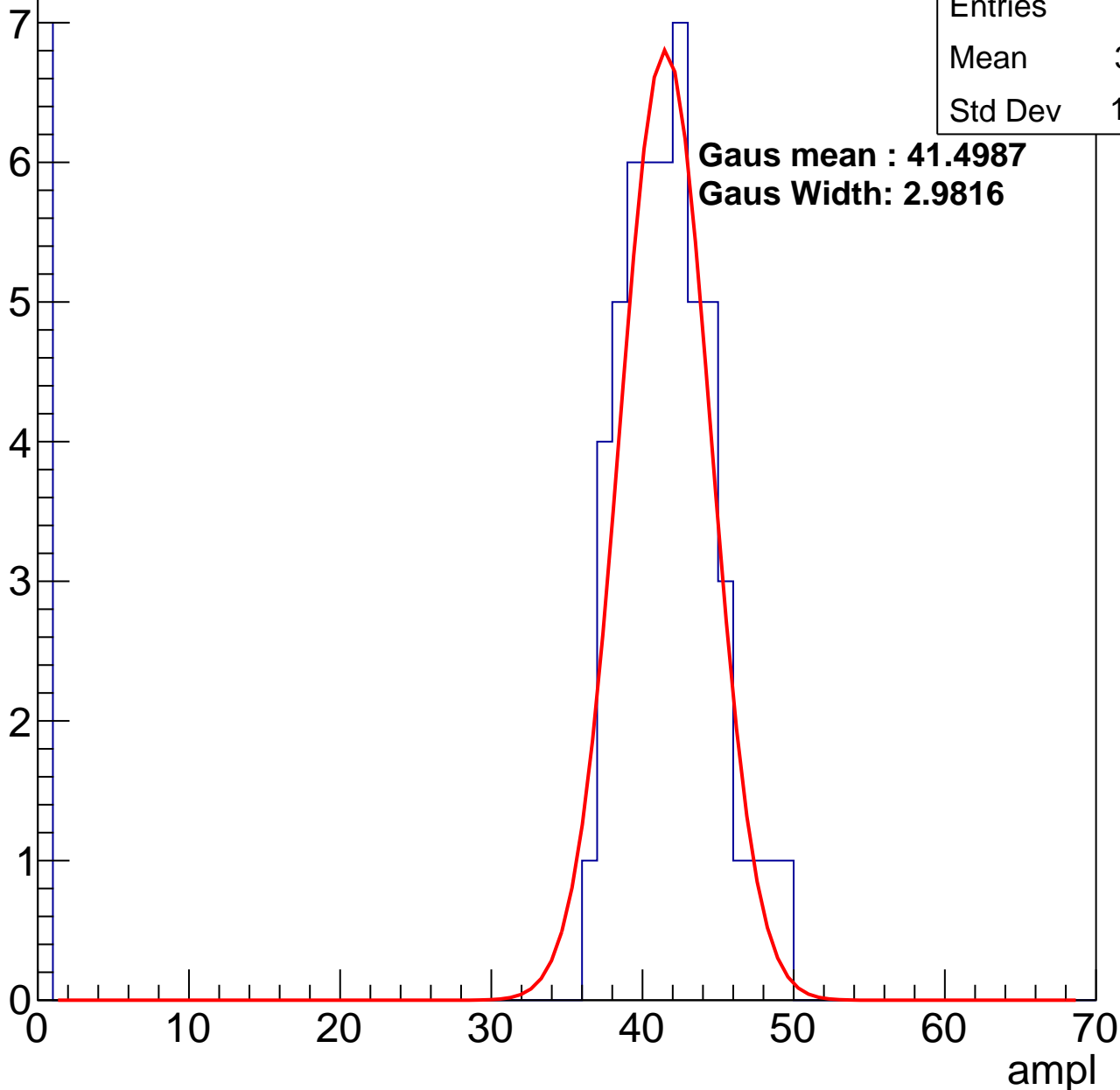
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	36.41
Std Dev	13.64

Gaus mean : 41.4987

Gaus Width: 2.9816

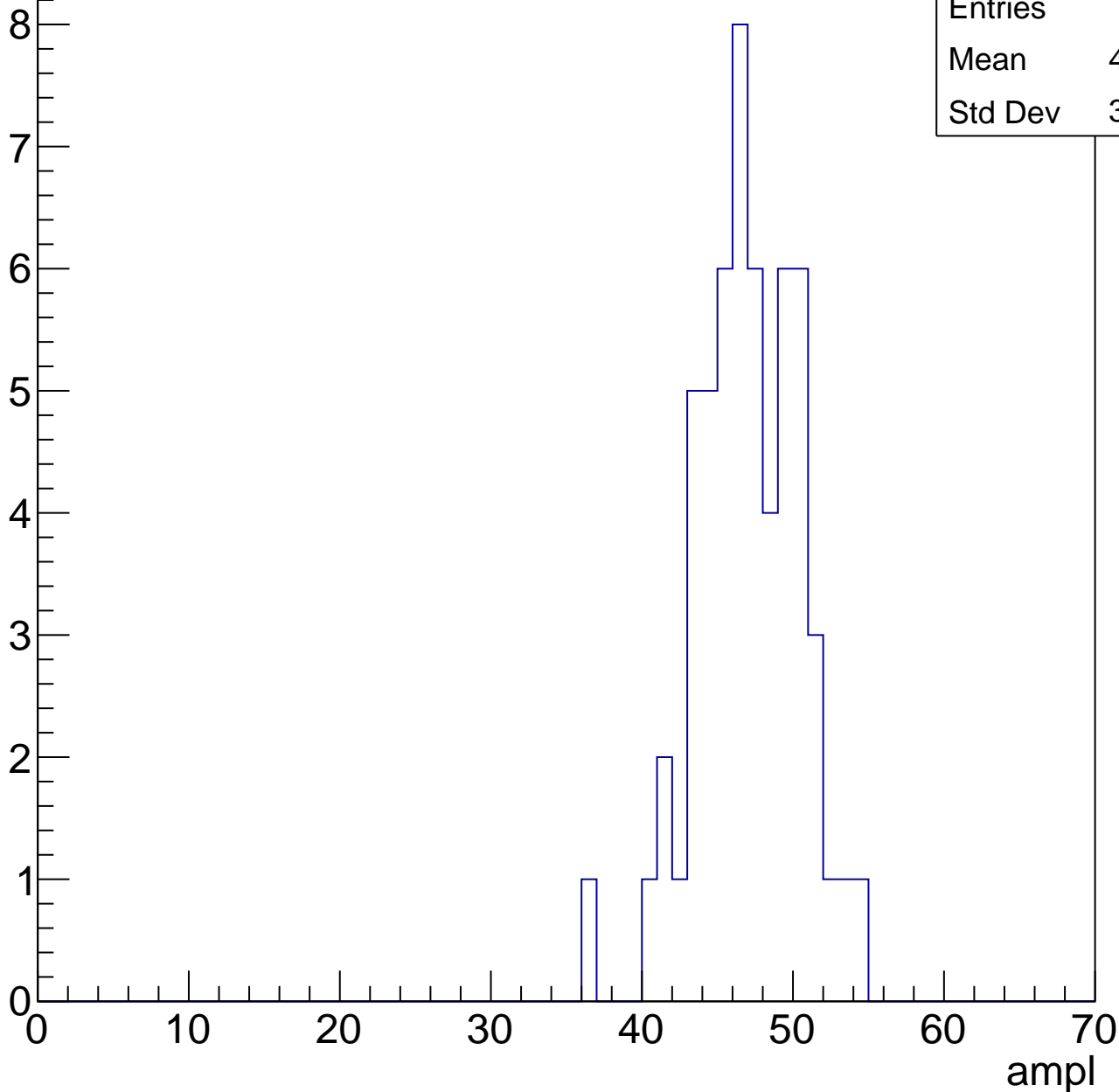


B1L103S, U2-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	46.54
Std Dev	3.382

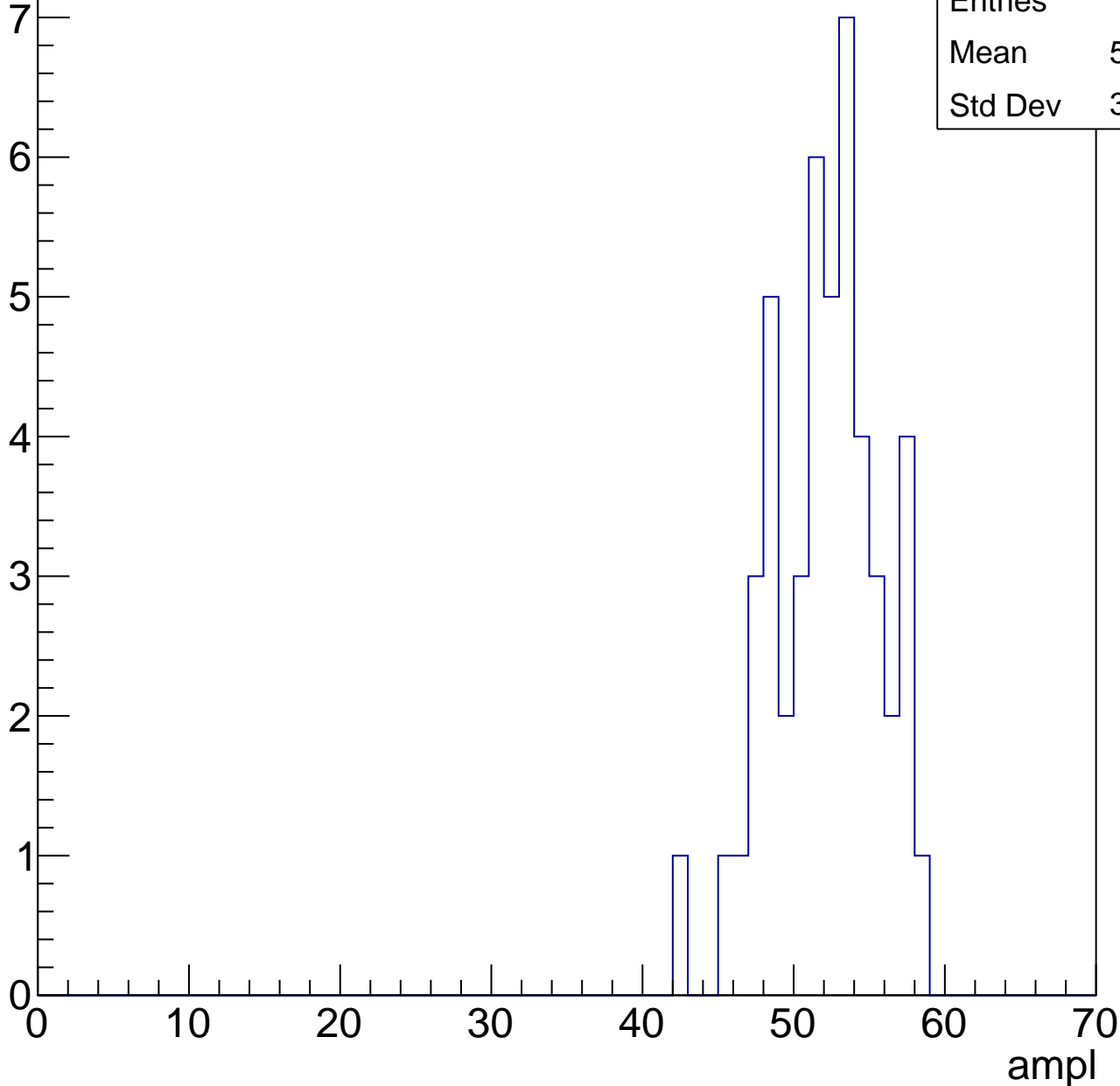


B1L103S, U2-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	51.62
Std Dev	3.498

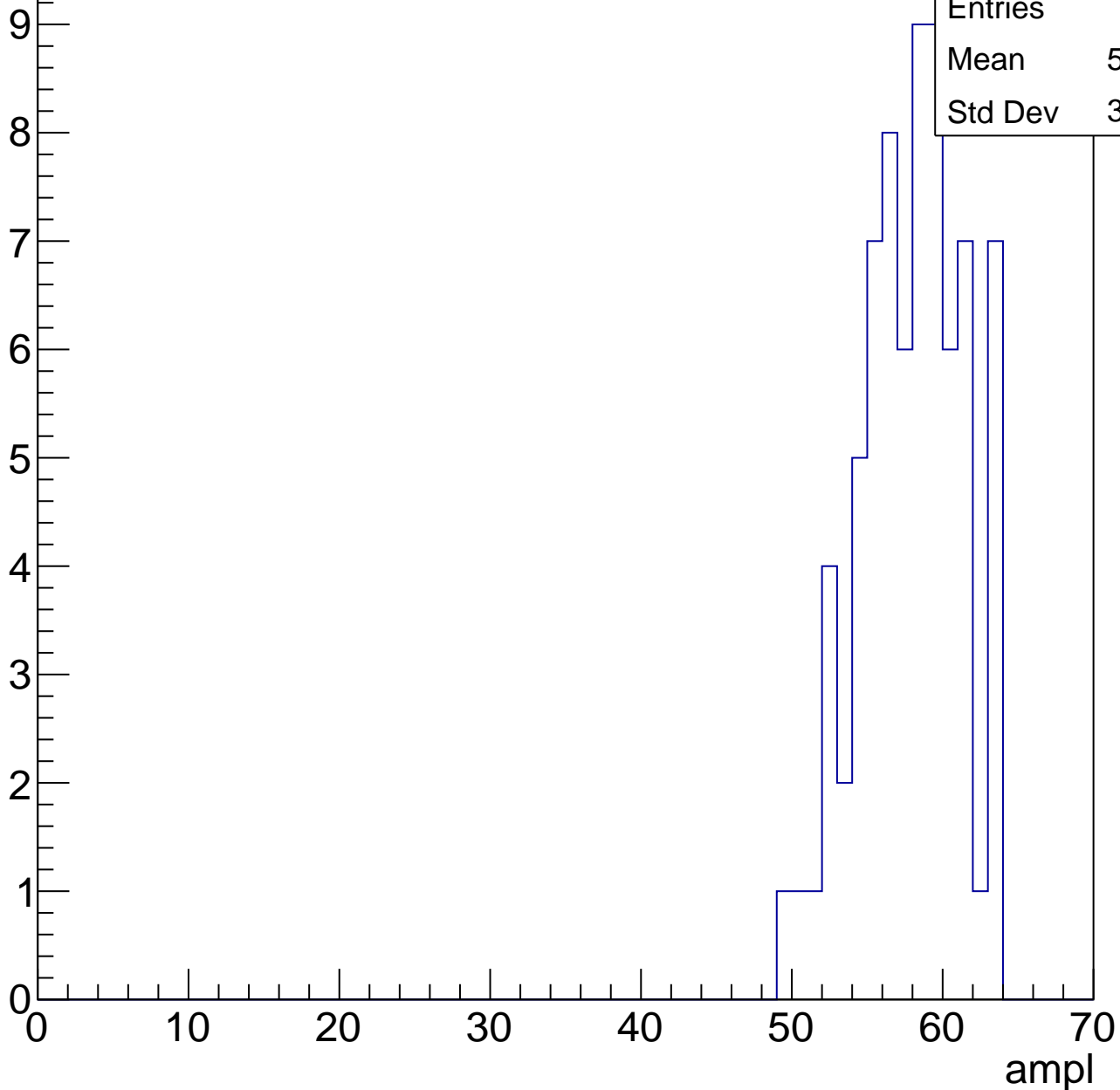


B1L103S, U2-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	57.46
Std Dev	3.358

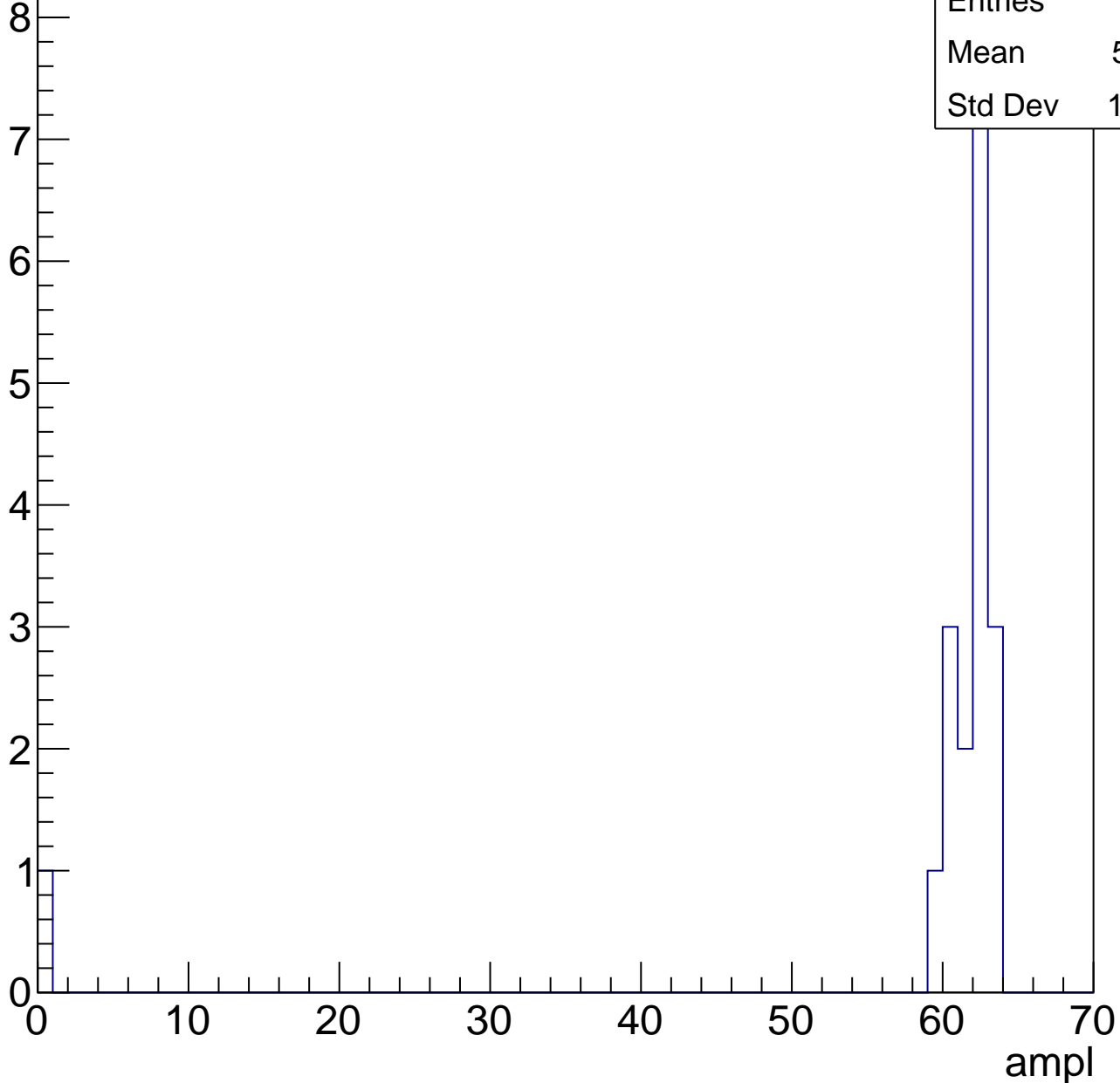


B1L103S, U2-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	58.11
Std Dev	14.14

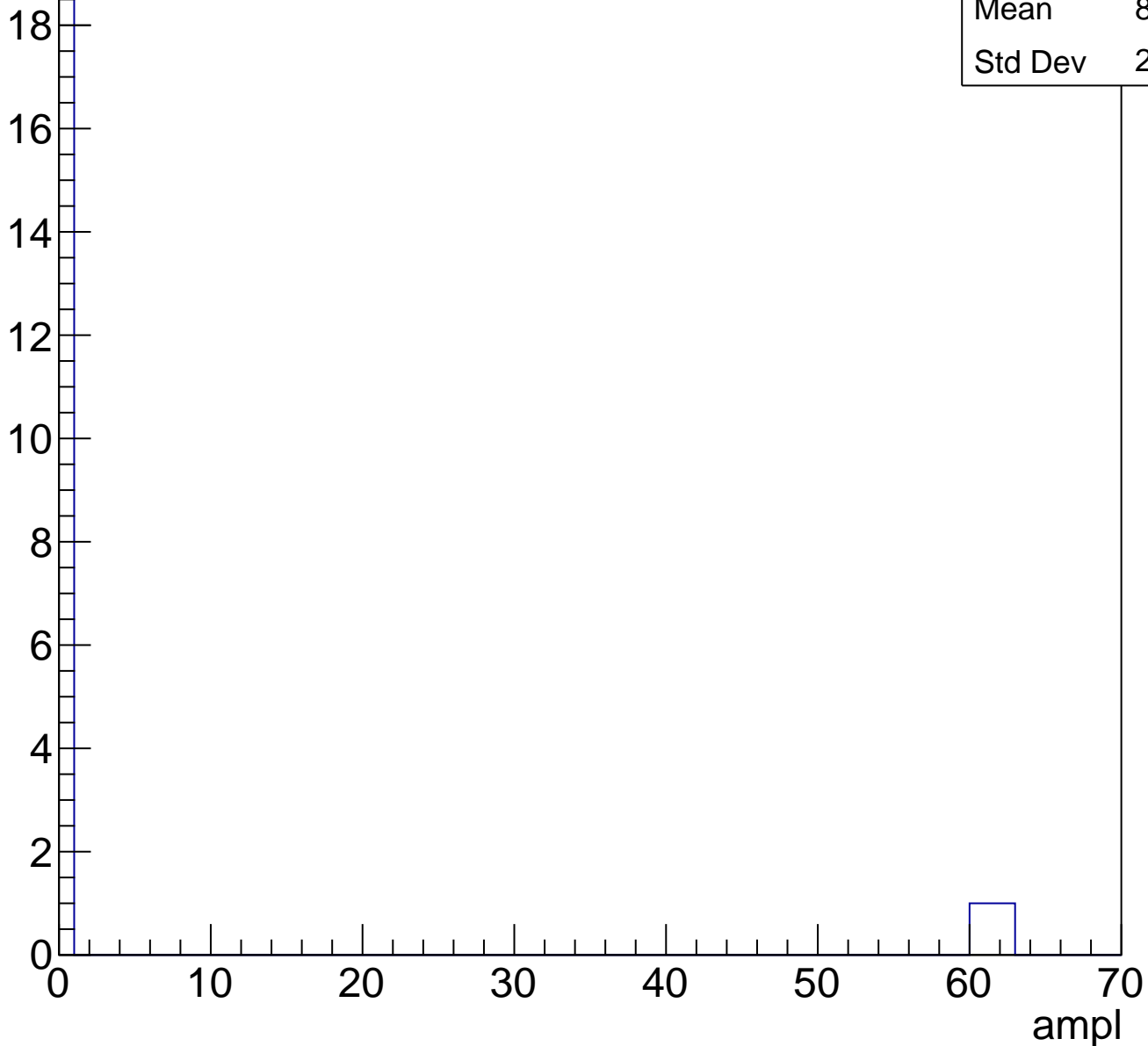


B1L103S, U2-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.318
Std Dev	20.94

Entry



B1L103S, U2-ch121, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	23.02
Std Dev	11.62

Gaus mean : 28.8125

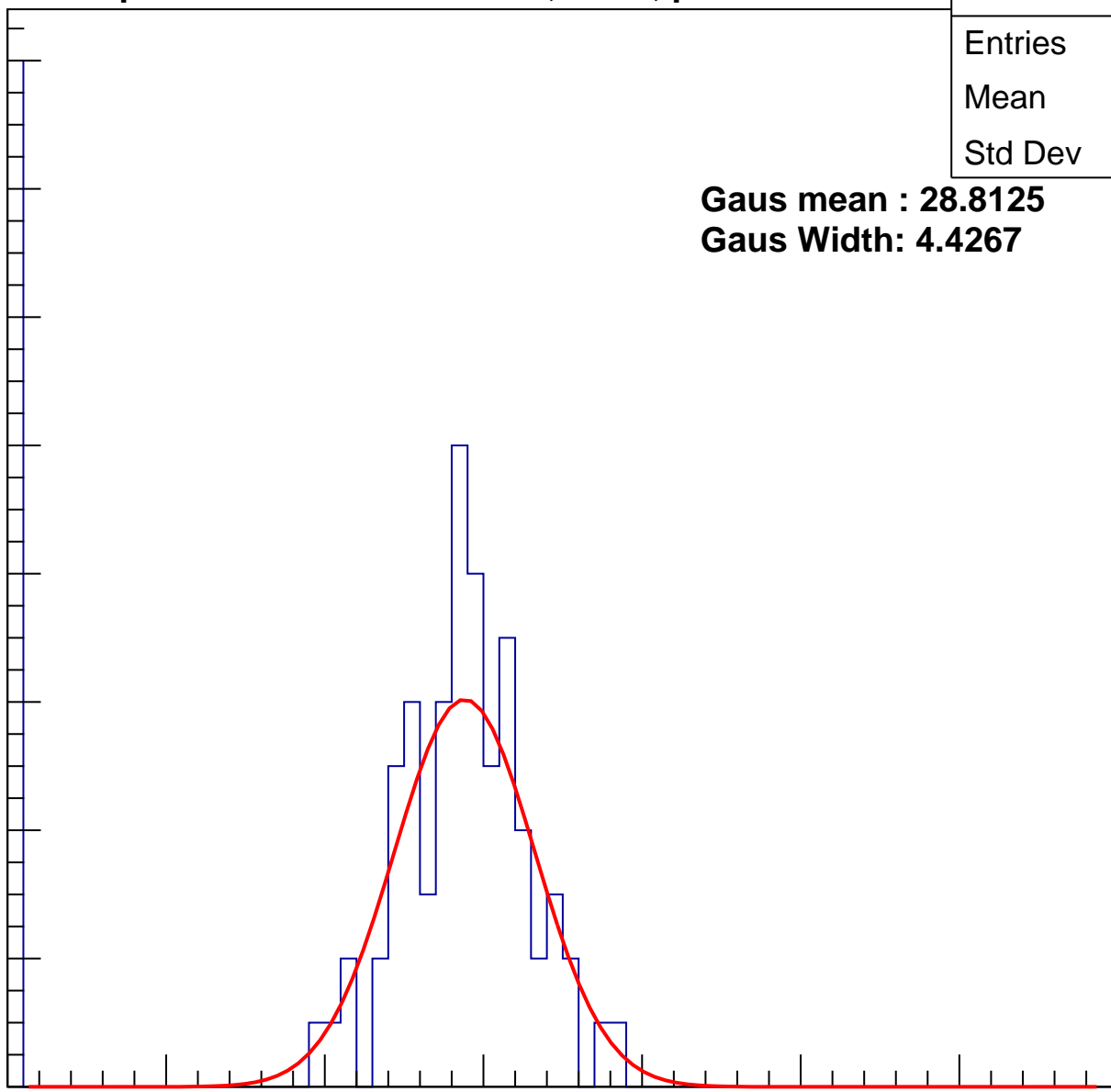
Gaus Width: 4.4267

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch121, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	30.41
Std Dev	13.06

Gaus mean : 35.3991

Gaus Width: 3.6121

Entry

12

10

8

6

4

2

0

0

10

20

30

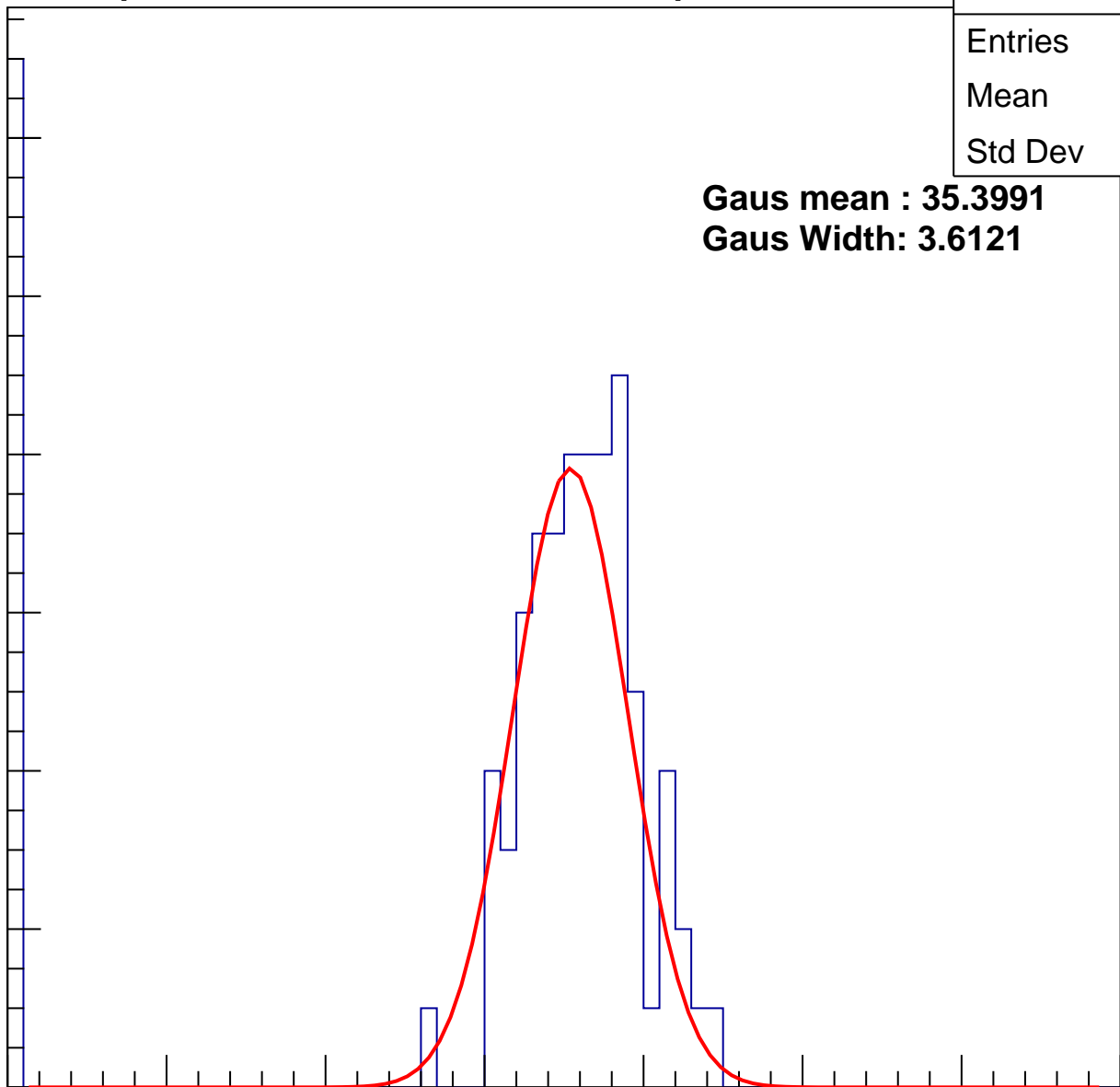
40

50

60

70

ampl



B1L103S, U2-ch121, adc2

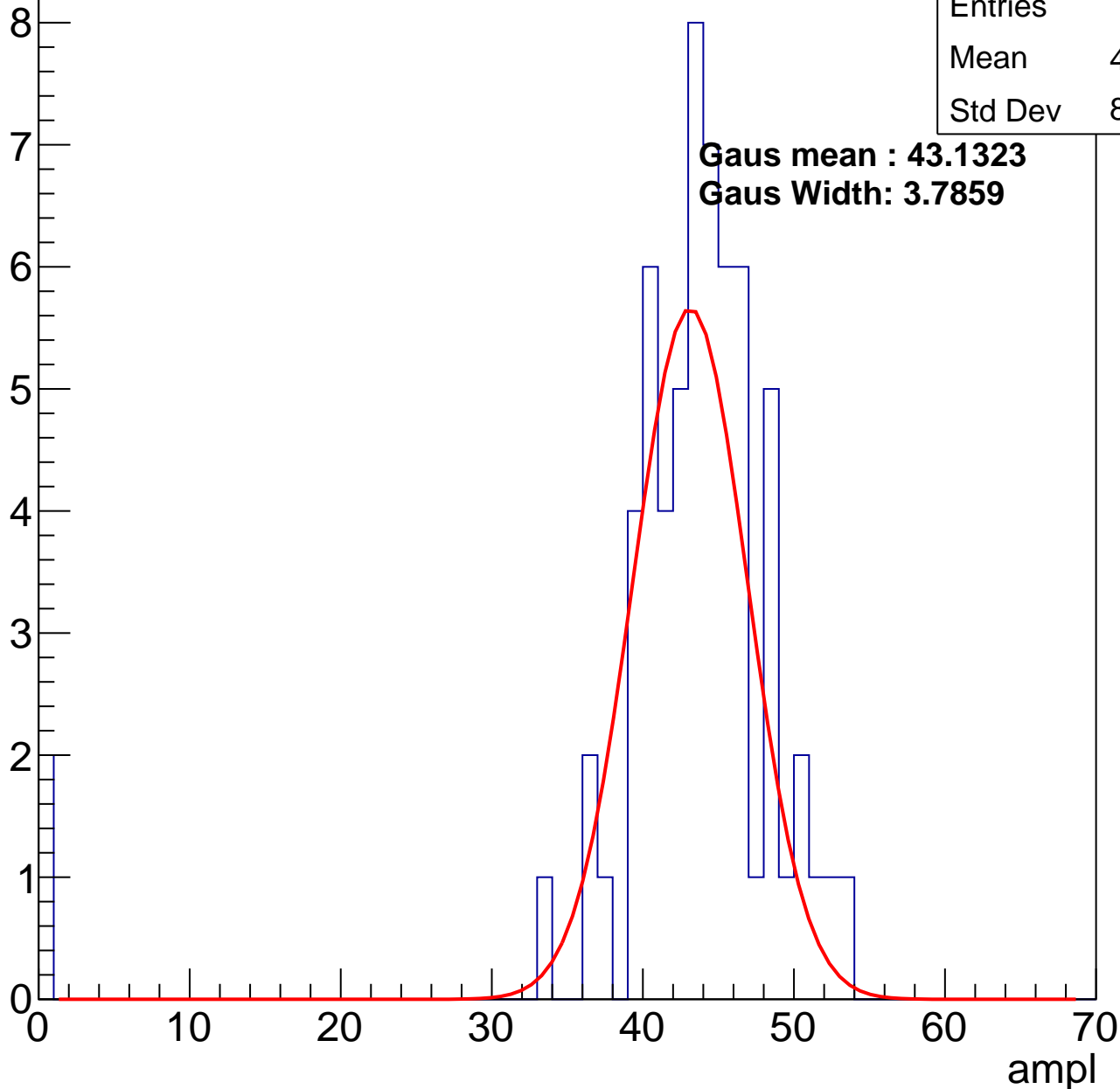
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	42.22
Std Dev	8.512

Gaus mean : 43.1323

Gaus Width: 3.7859

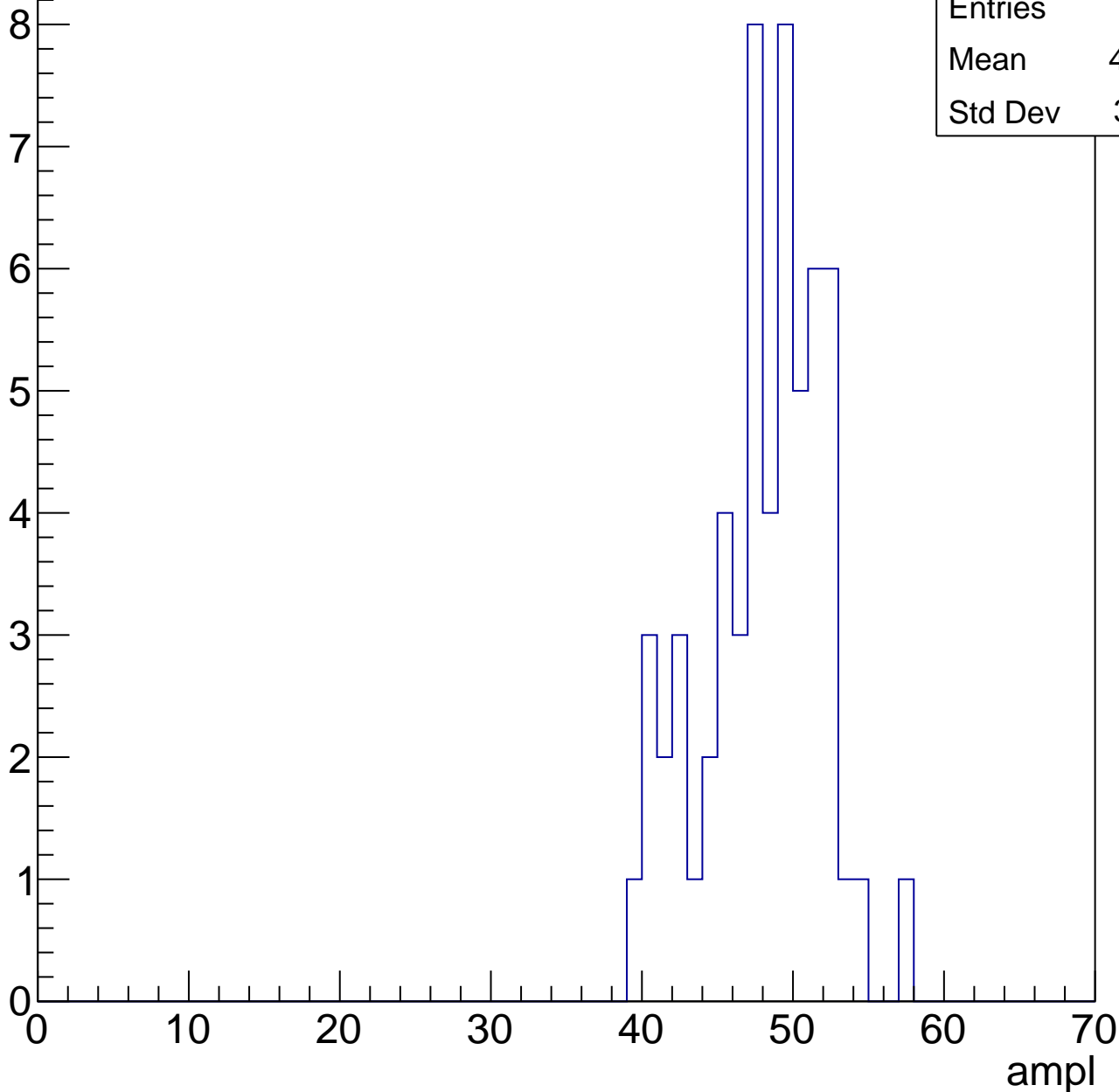


B1L103S, U2-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	47.59
Std Dev	3.911

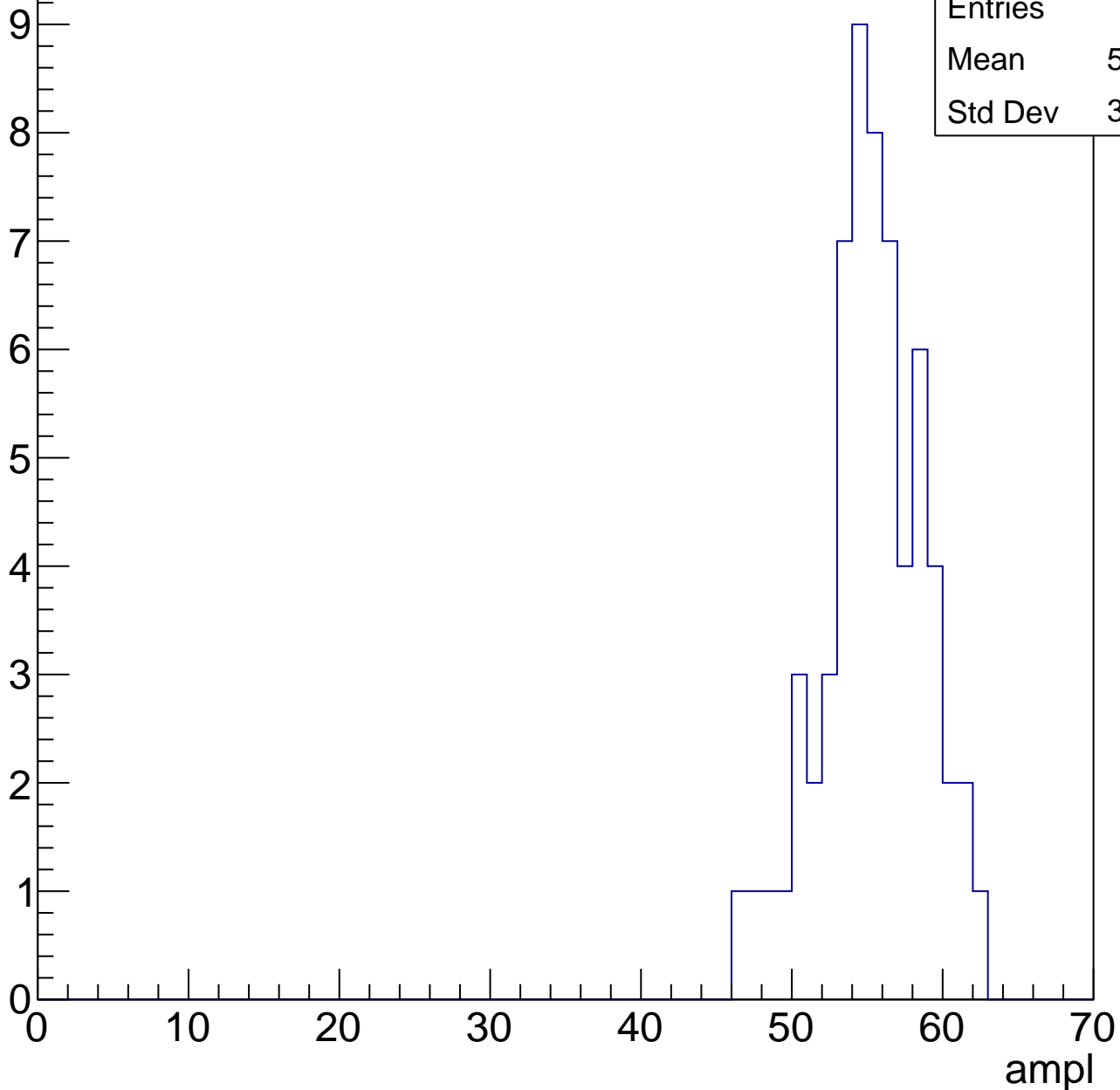


B1L103S, U2-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	54.89
Std Dev	3.389



B1L103S, U2-ch121, adc5

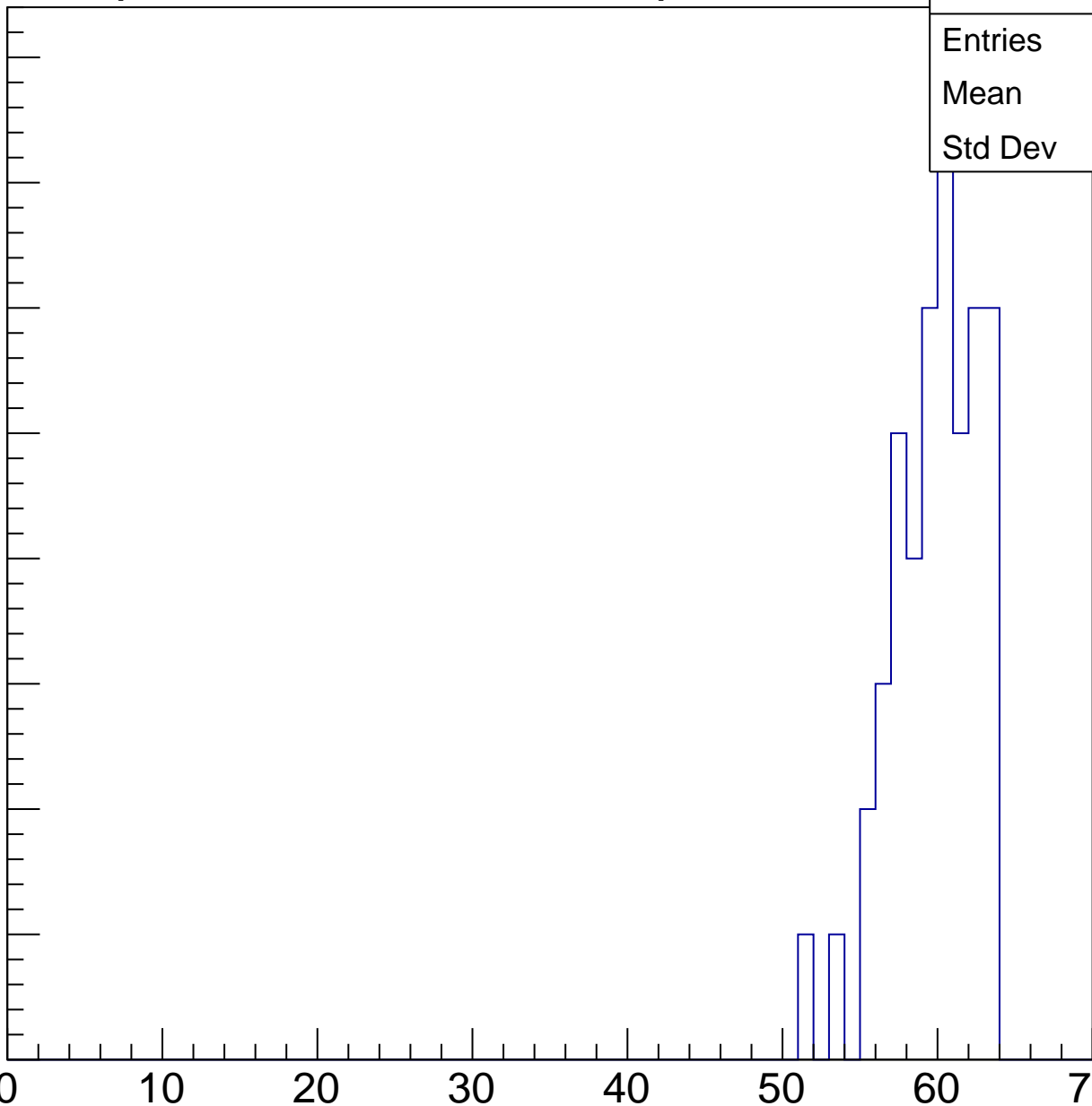
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.32
Std Dev	2.753

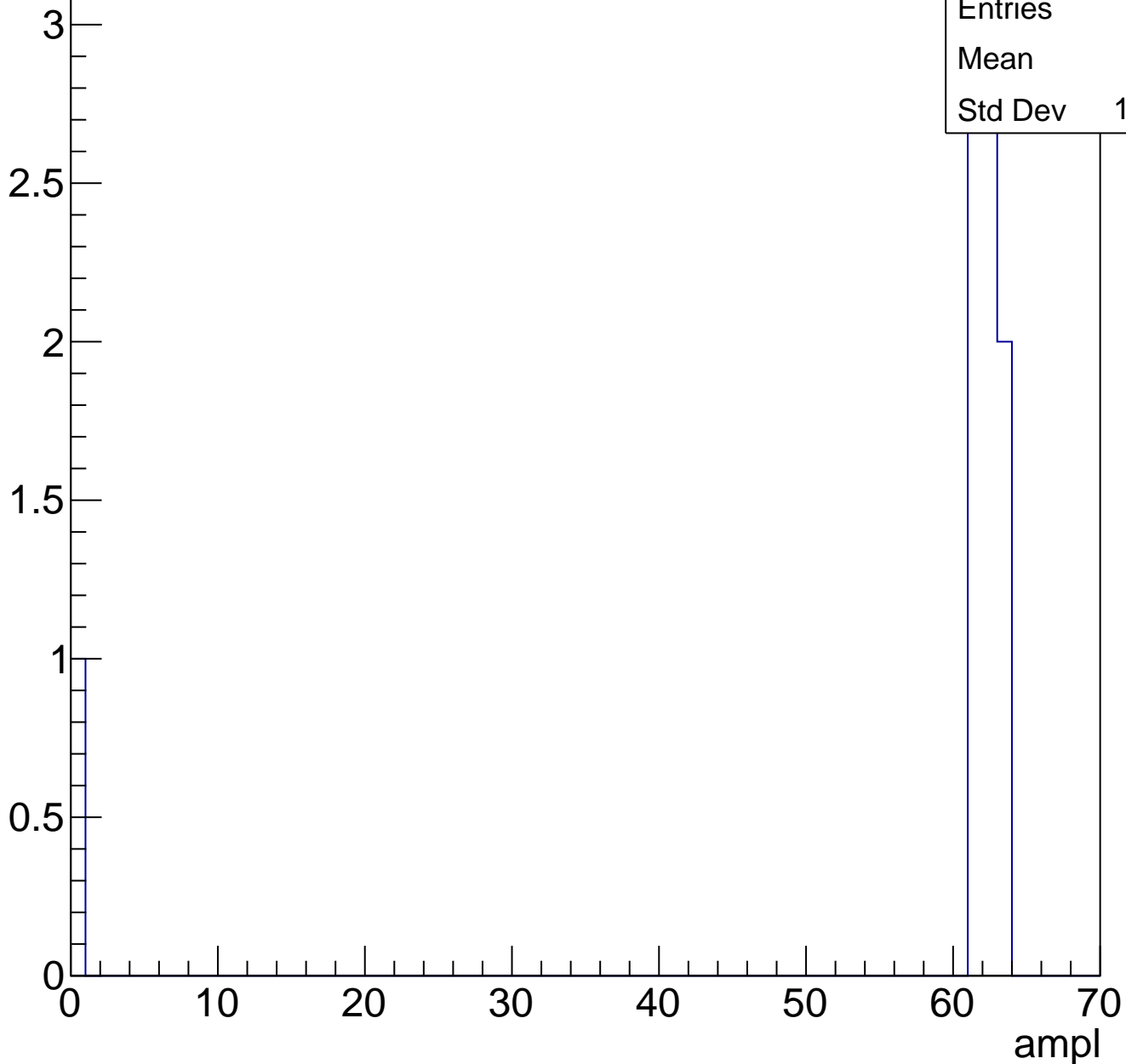
ampl



B1L103S, U2-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

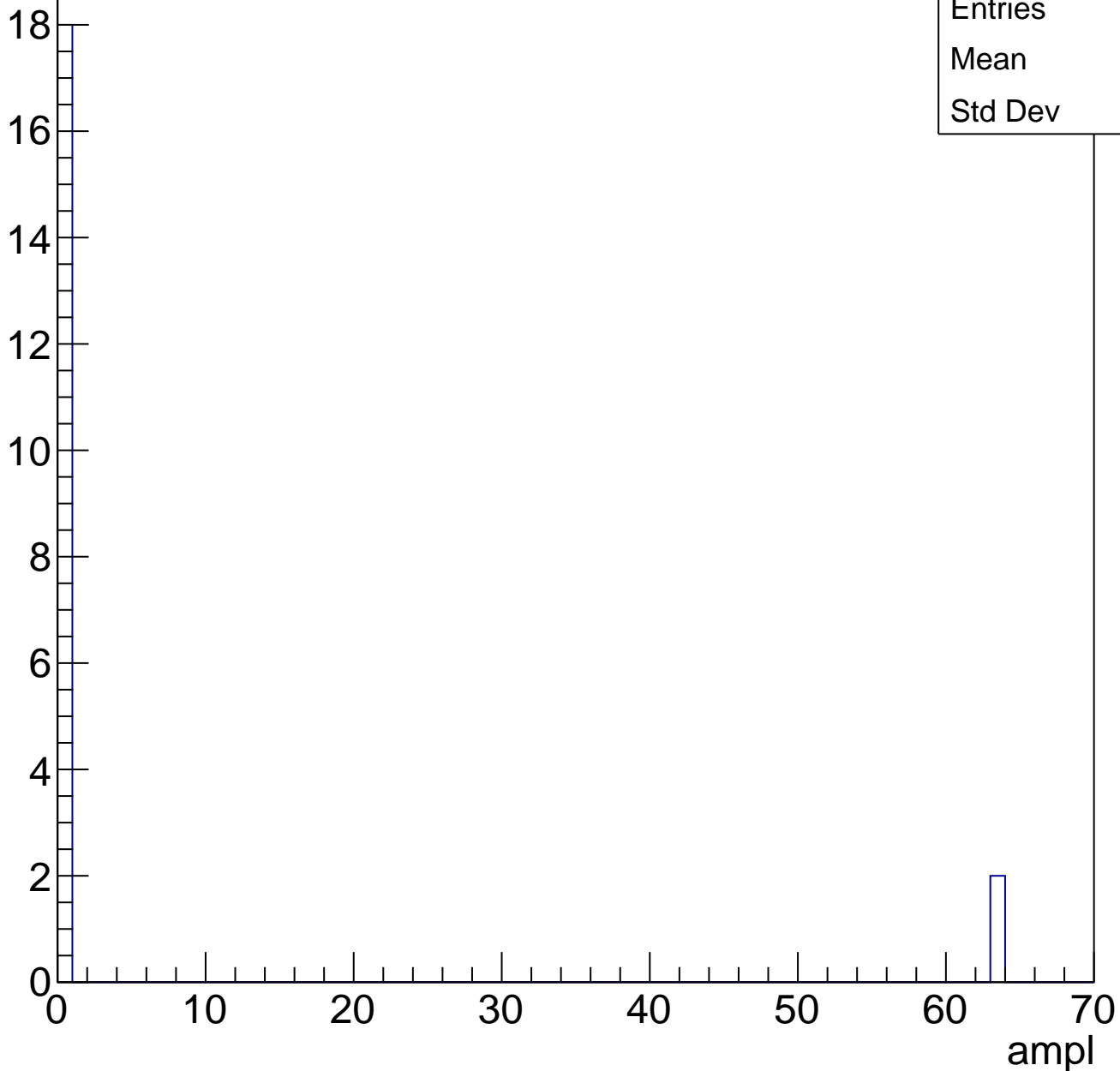


B1L103S, U2-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.3
Std Dev	18.9

Entry



B1L103S, U2-ch122, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	23.57
Std Dev	11.62

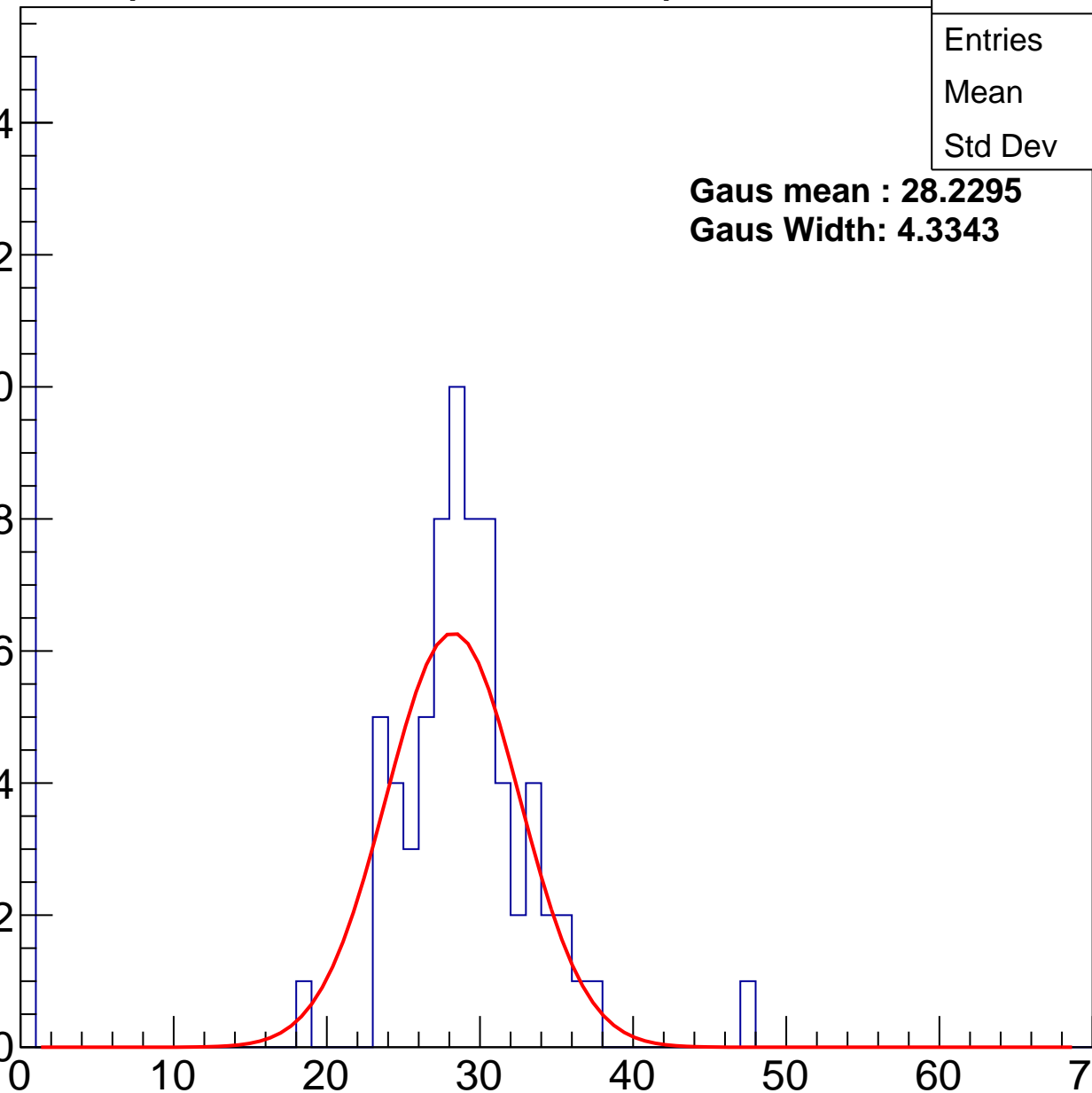
Gaus mean : 28.2295

Gaus Width: 4.3343

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U2-ch122, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	30.99
Std Dev	12.58

Gaus mean : 35.7222

Gaus Width: 3.5552

Entry

10

8

6

4

2

0

0

10

20

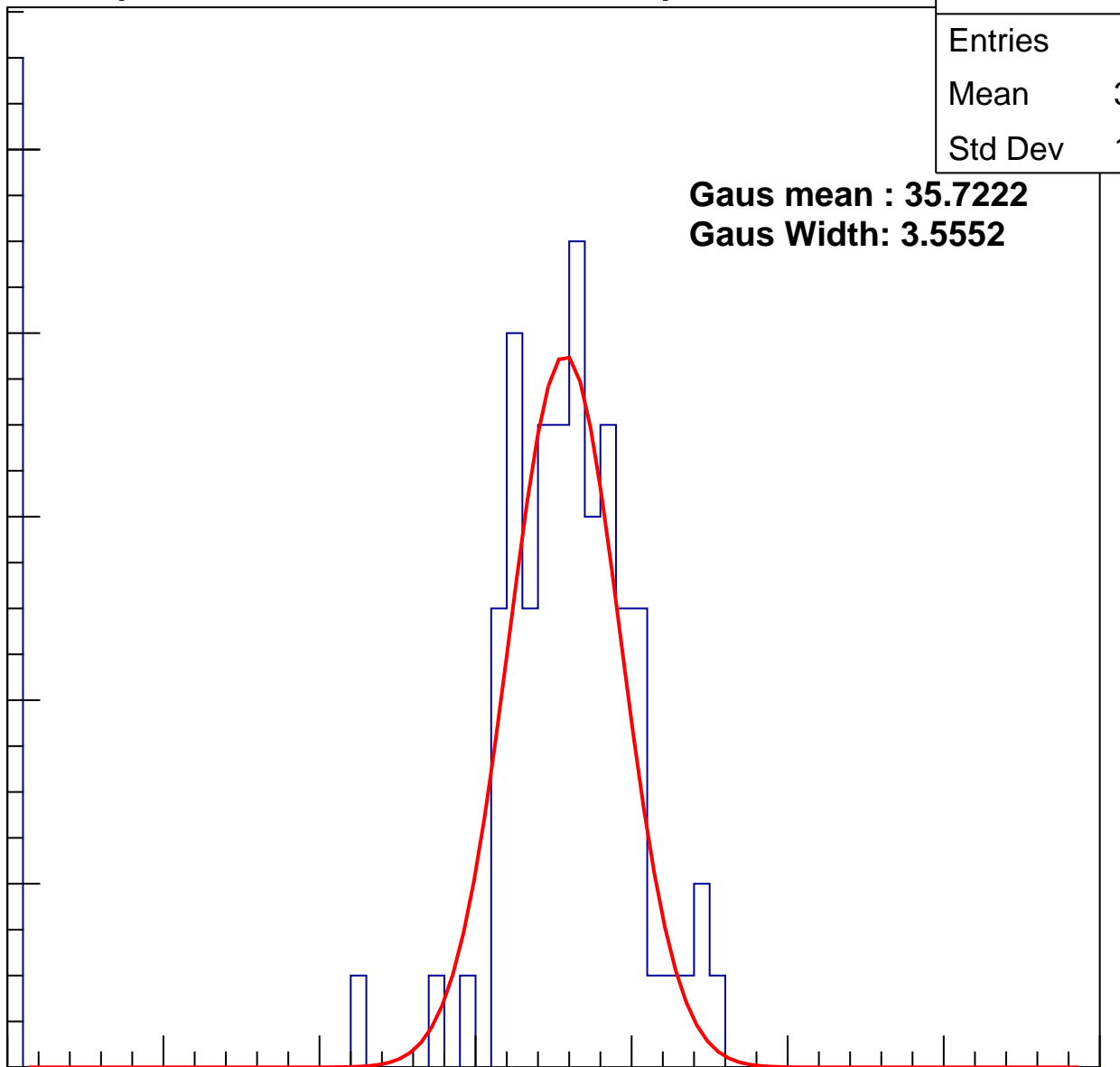
30

40

50

60

ampl



B1L103S, U2-ch122, adc2

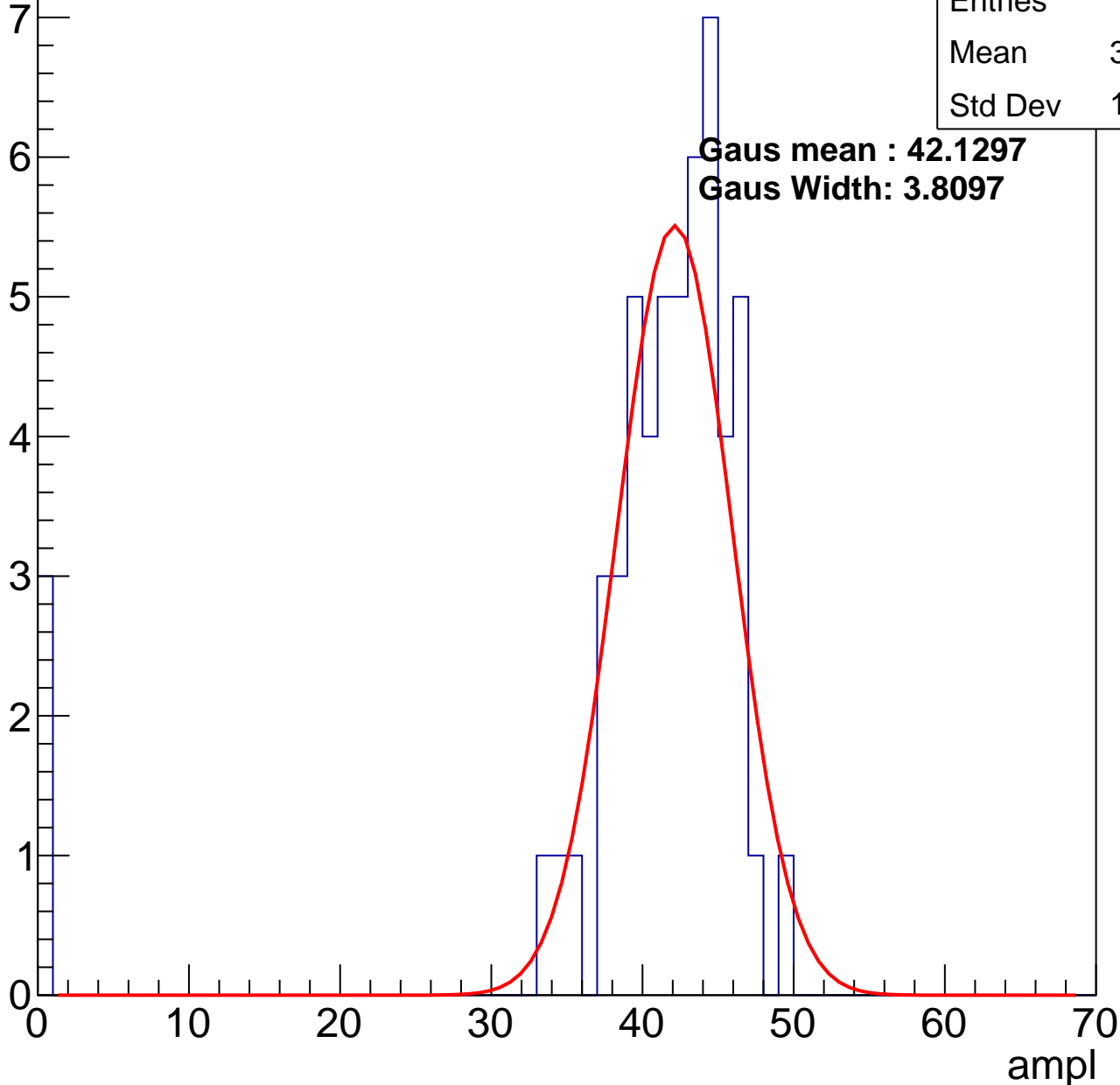
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	39.44
Std Dev	10.03

Gaus mean : 42.1297

Gaus Width: 3.8097

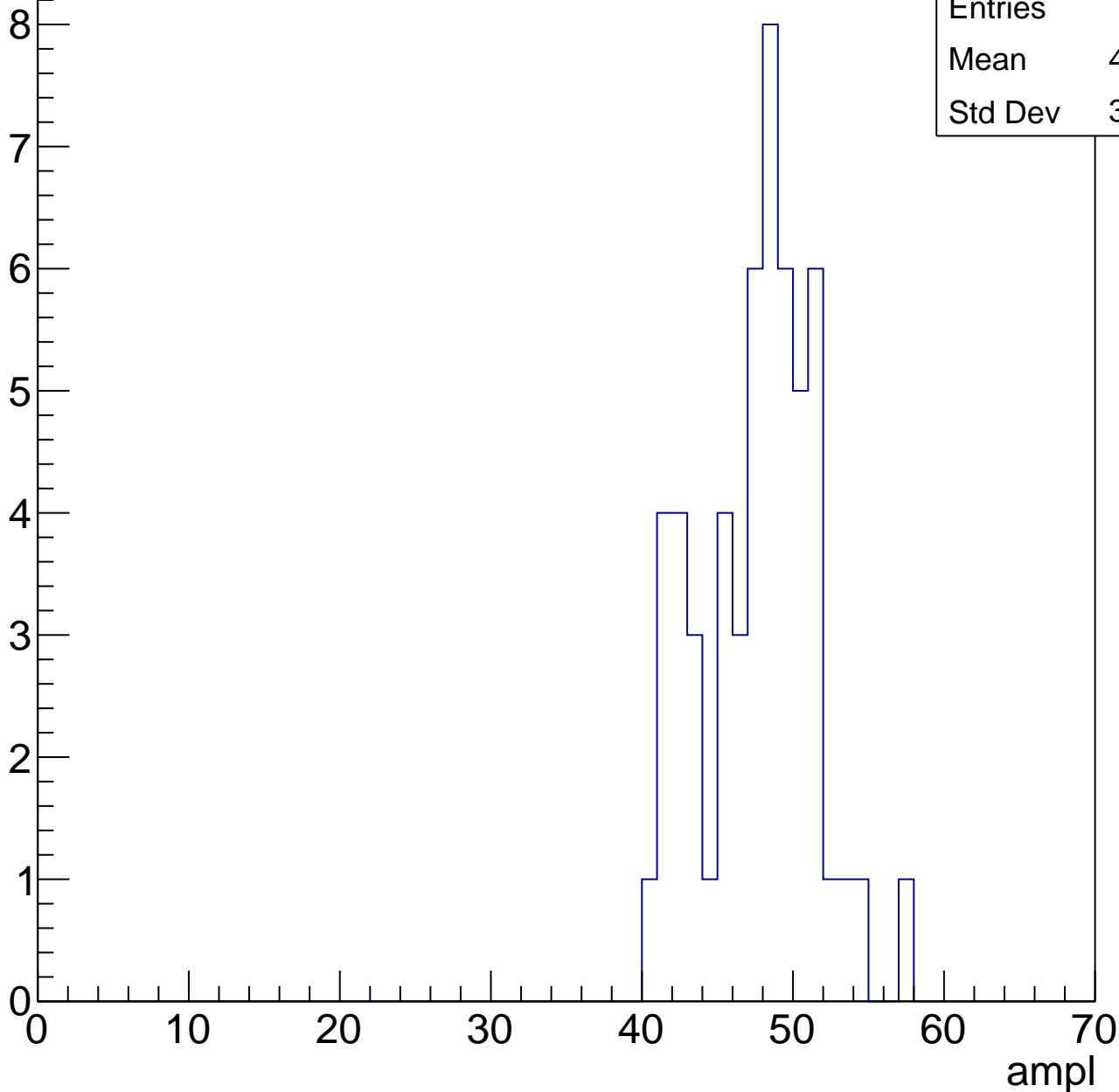


B1L103S, U2-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	47.18
Std Dev	3.693



B1L103S, U2-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	53.67
Std Dev	3.615

Entry

10

8

6

4

2

0

0

10

20

30

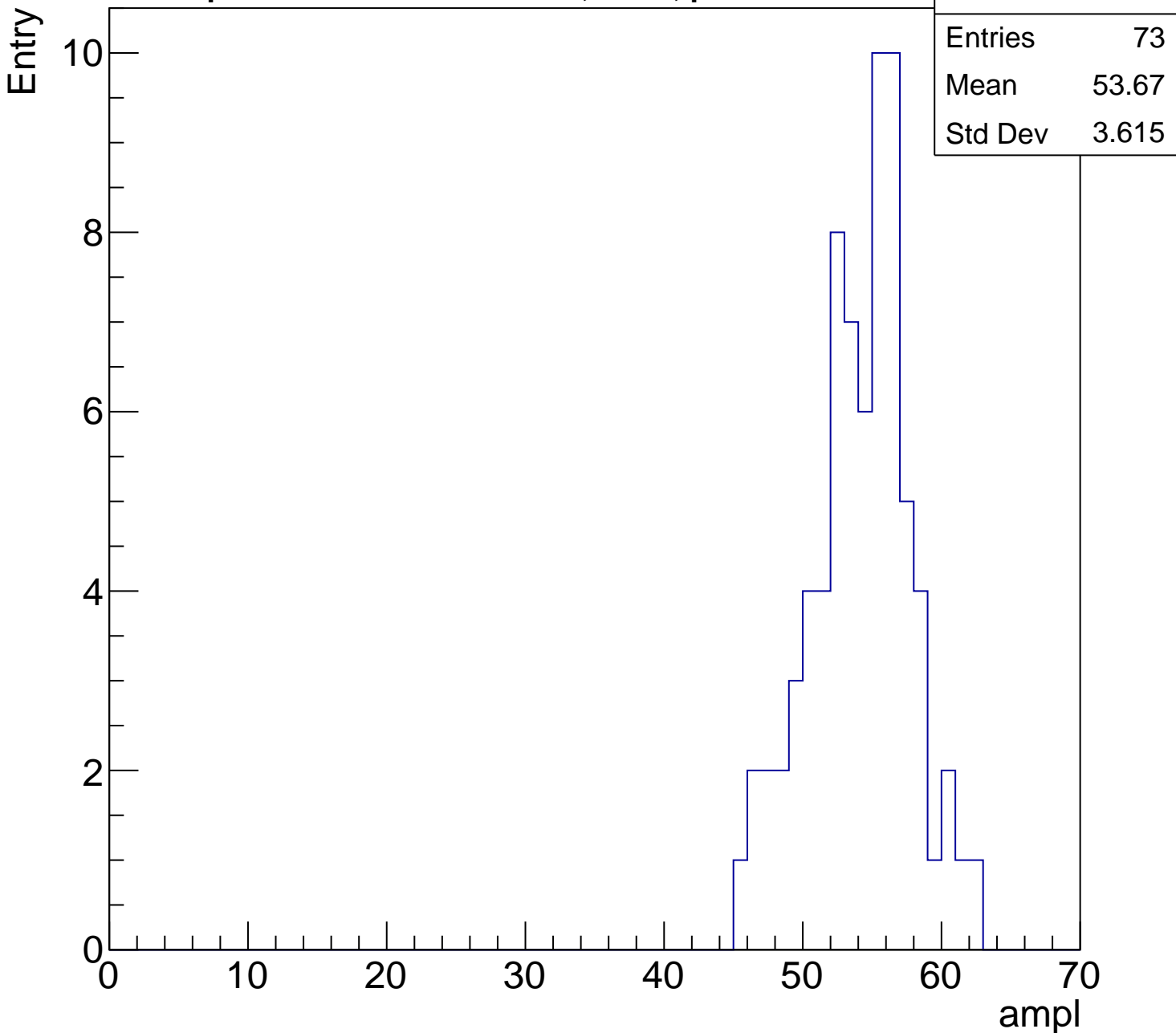
40

50

60

ampl

70

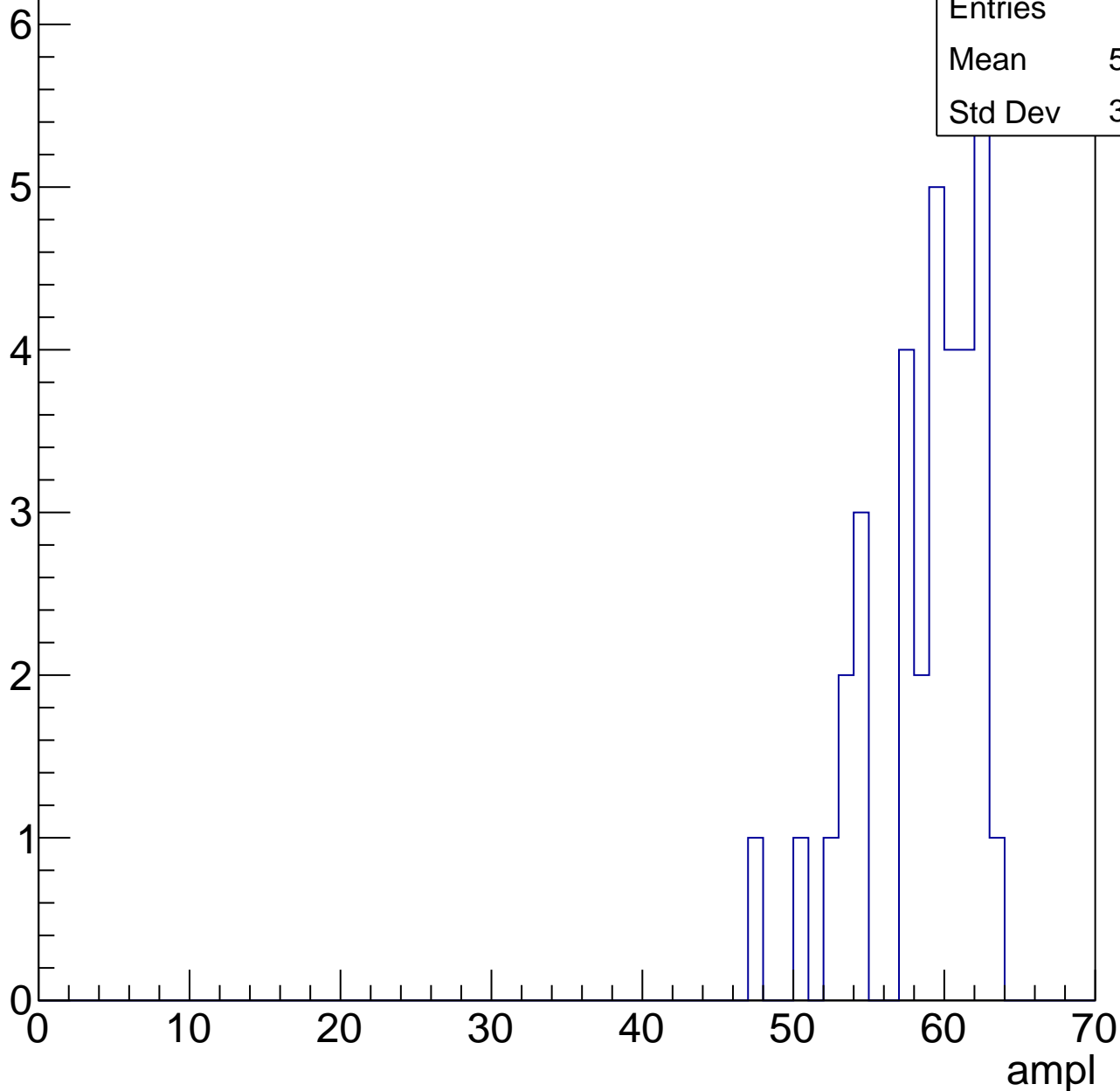


B1L103S, U2-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	58.09
Std Dev	3.838

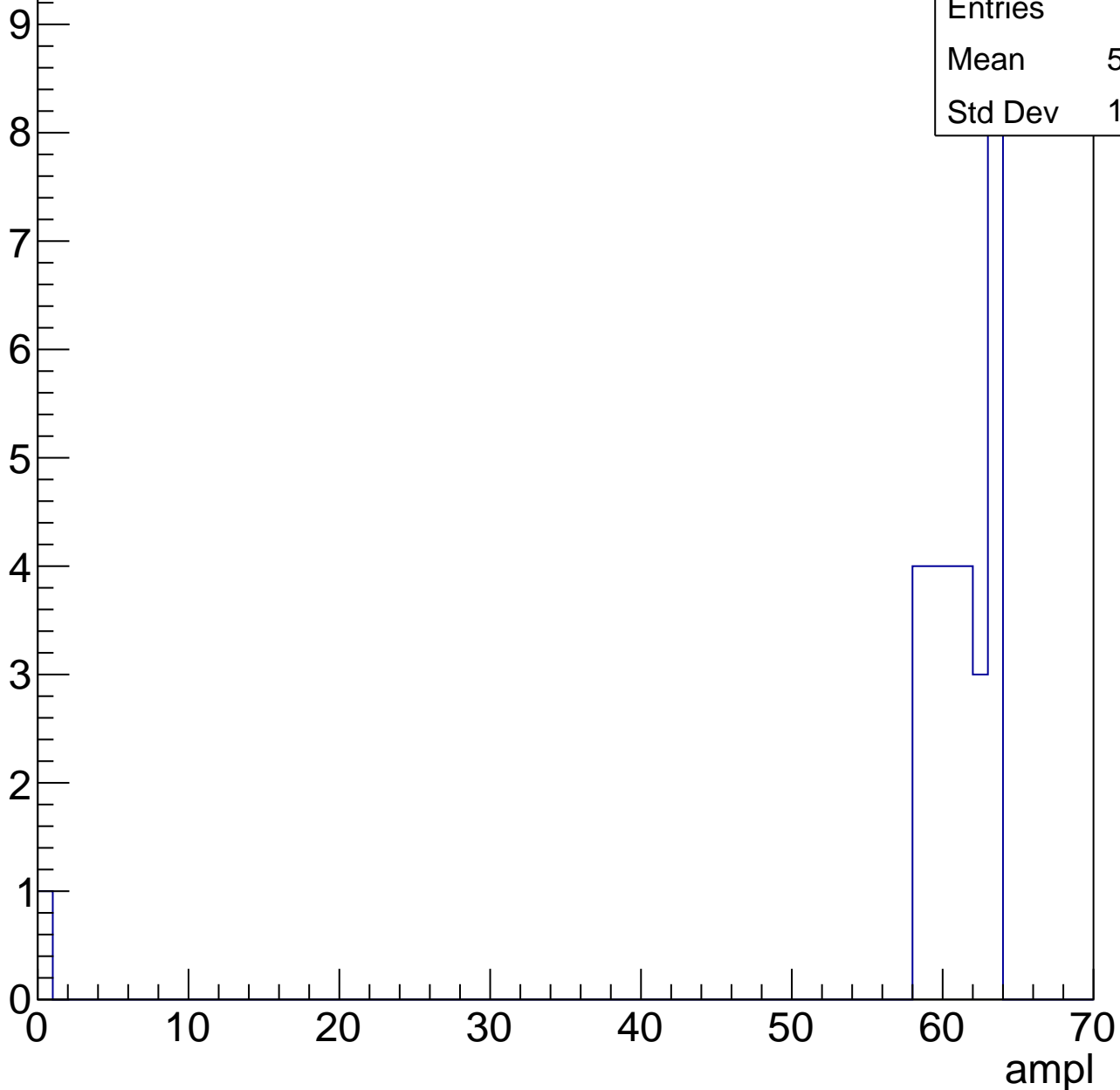


B1L103S, U2-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	58.79
Std Dev	11.26



B1L103S, U2-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U2-ch123, adc0

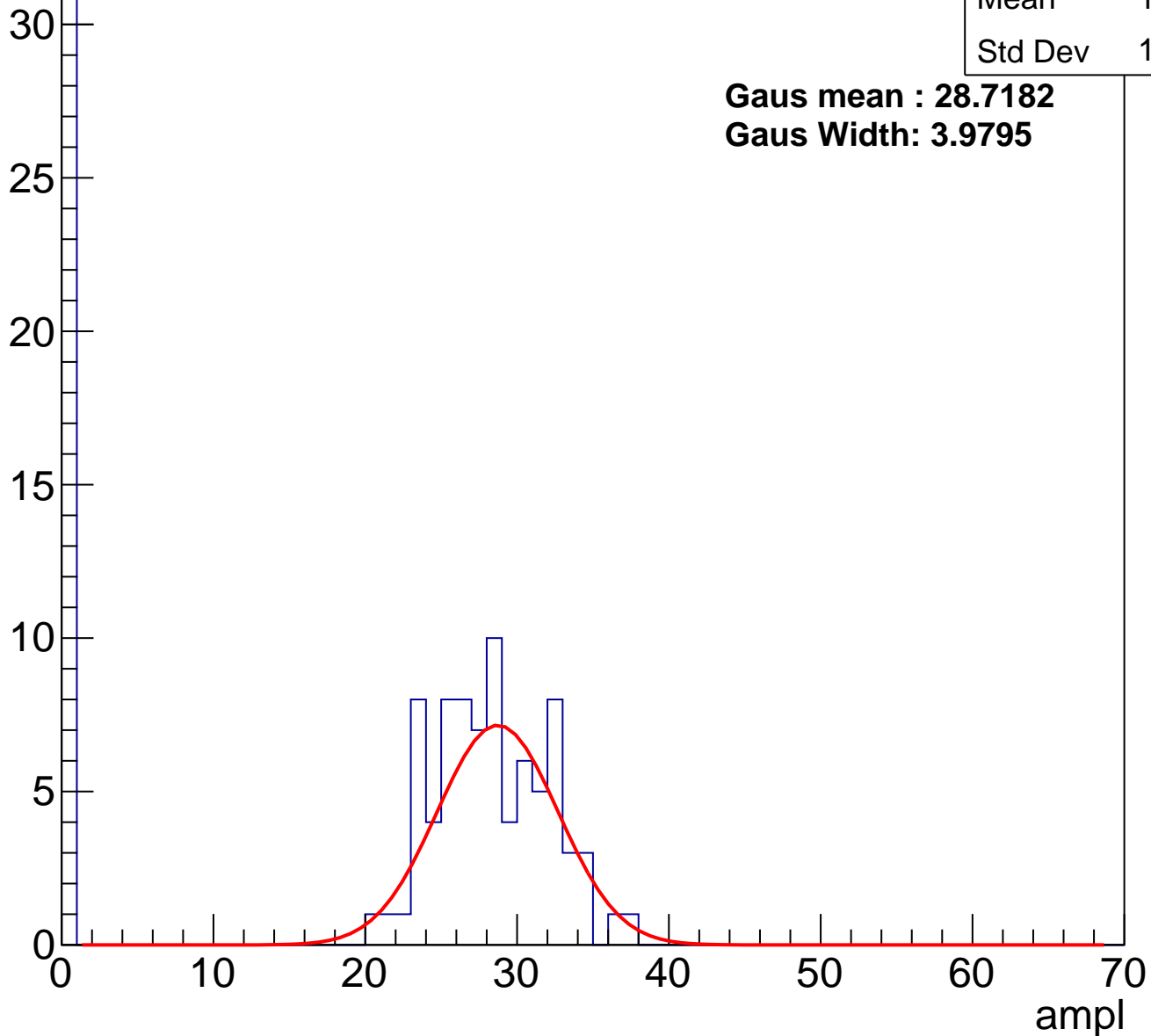
calib_packv5_041523_1651.root, FC#0, port C2

Entries	111
Mean	19.83
Std Dev	12.99

Gaus mean : 28.7182

Gaus Width: 3.9795

Entry



B1L103S, U2-ch123, adc1

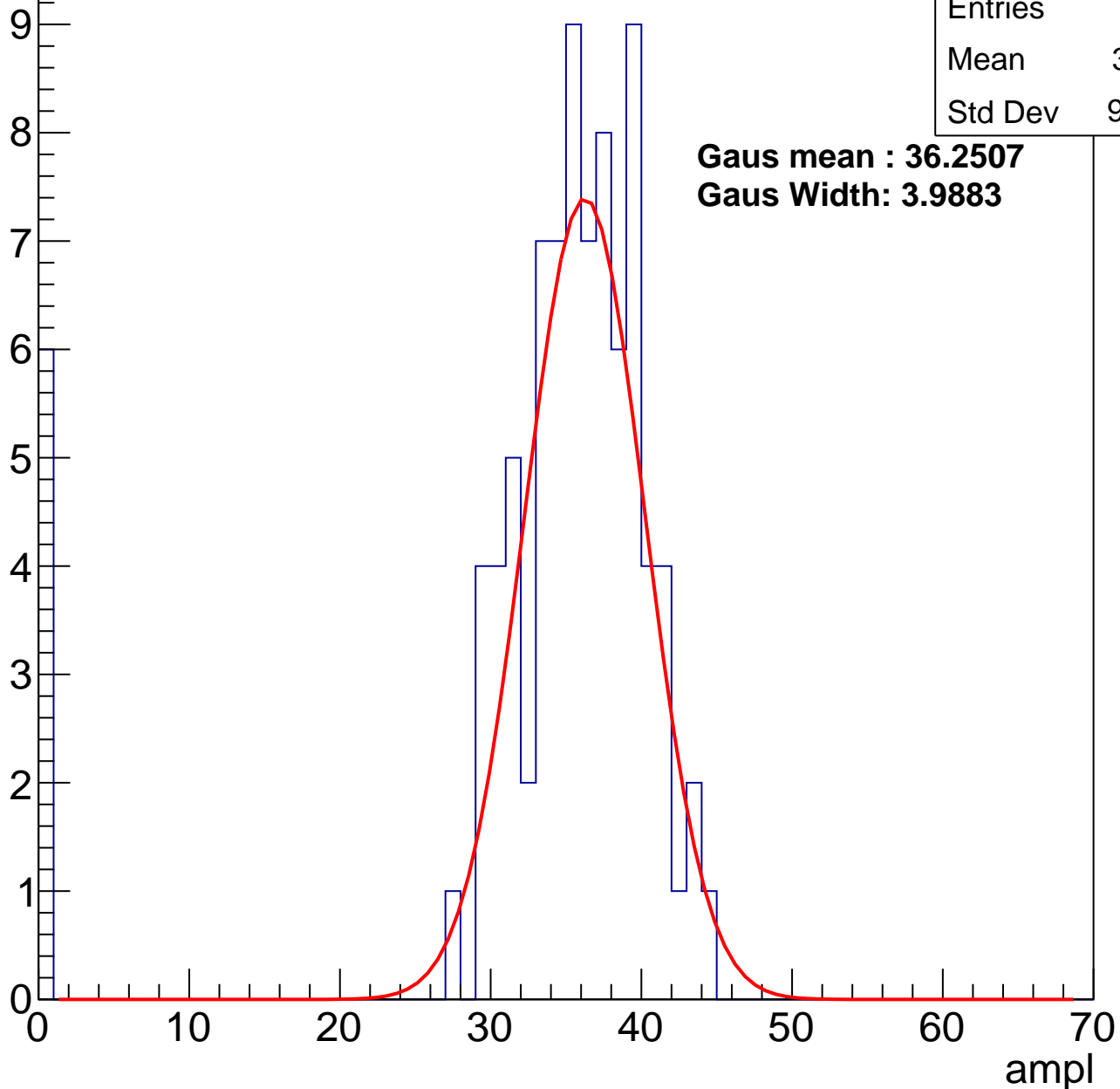
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	33.21
Std Dev	9.738

Gaus mean : 36.2507

Gaus Width: 3.9883



B1L103S, U2-ch123, adc2

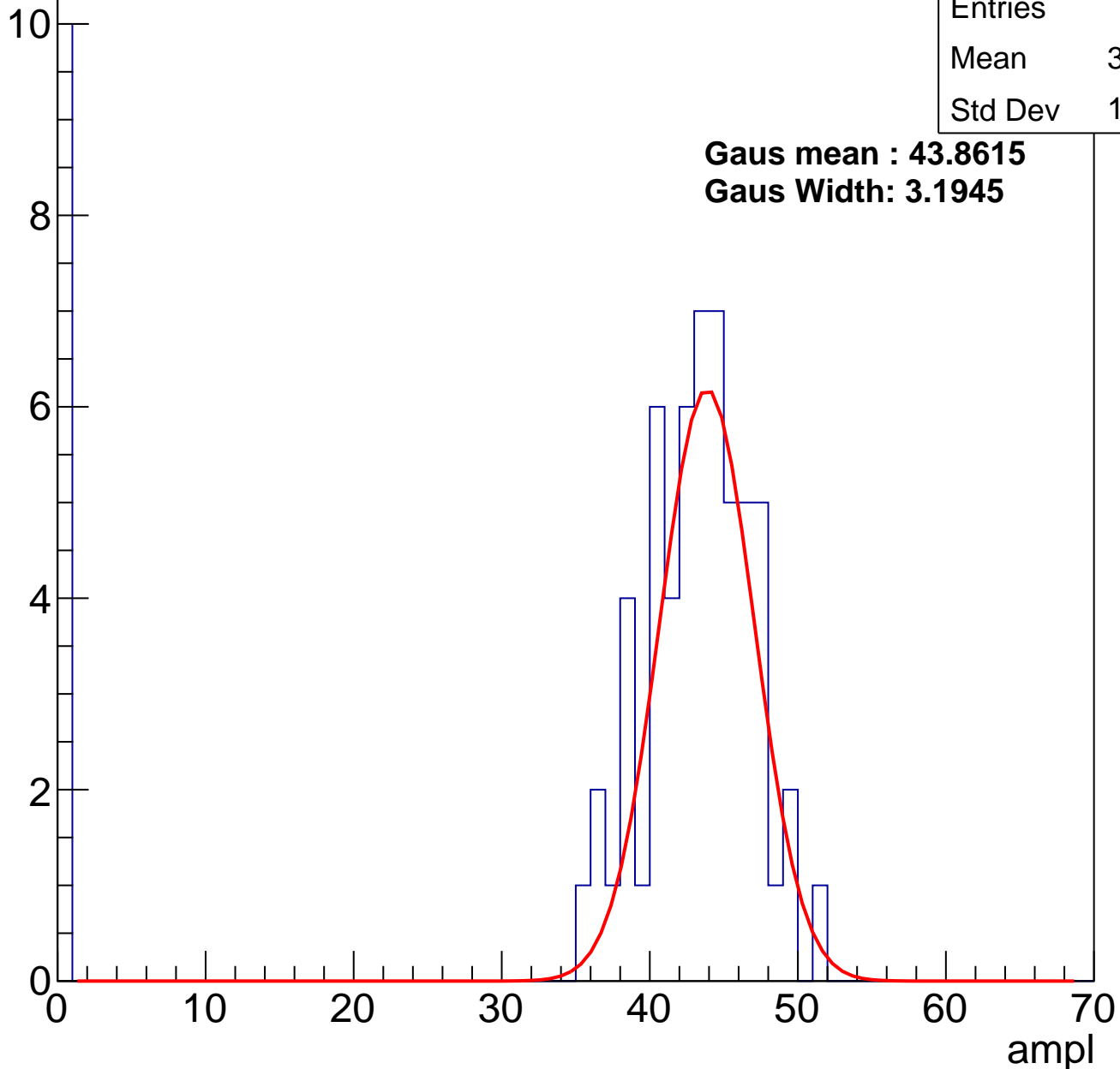
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	36.57
Std Dev	15.52

Gaus mean : 43.8615

Gaus Width: 3.1945

Entry

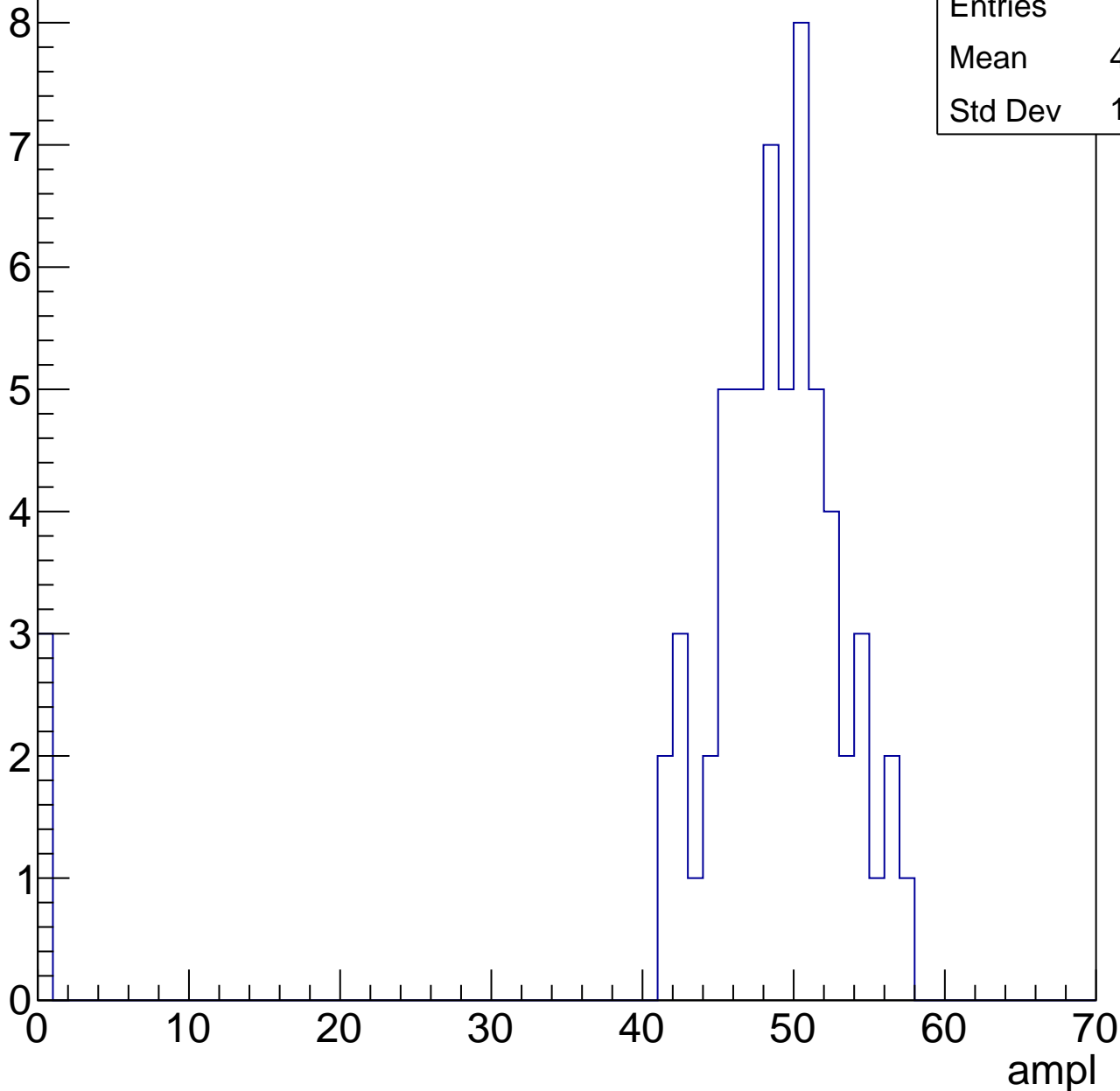


B1L103S, U2-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	46.33
Std Dev	10.92

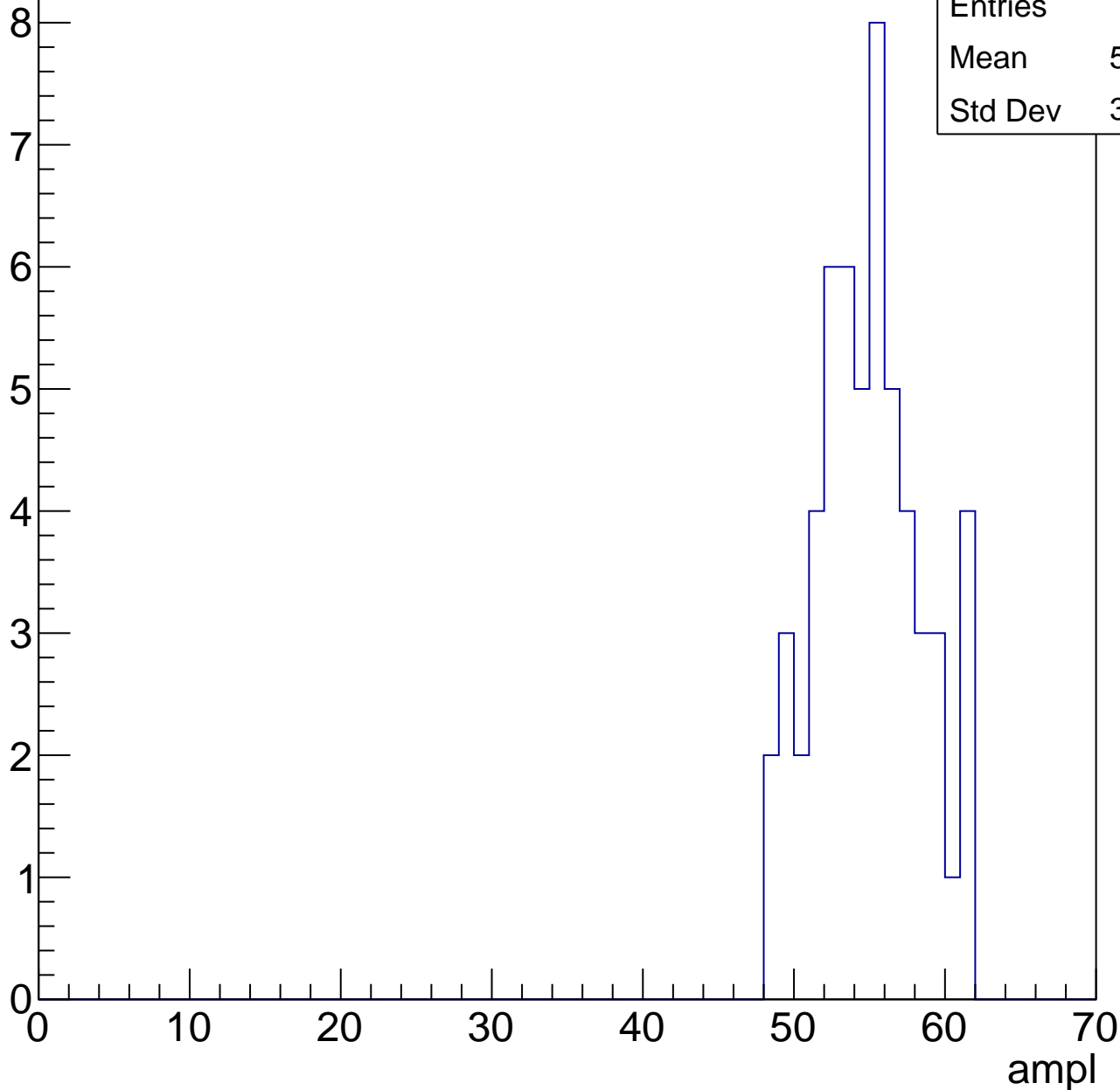


B1L103S, U2-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

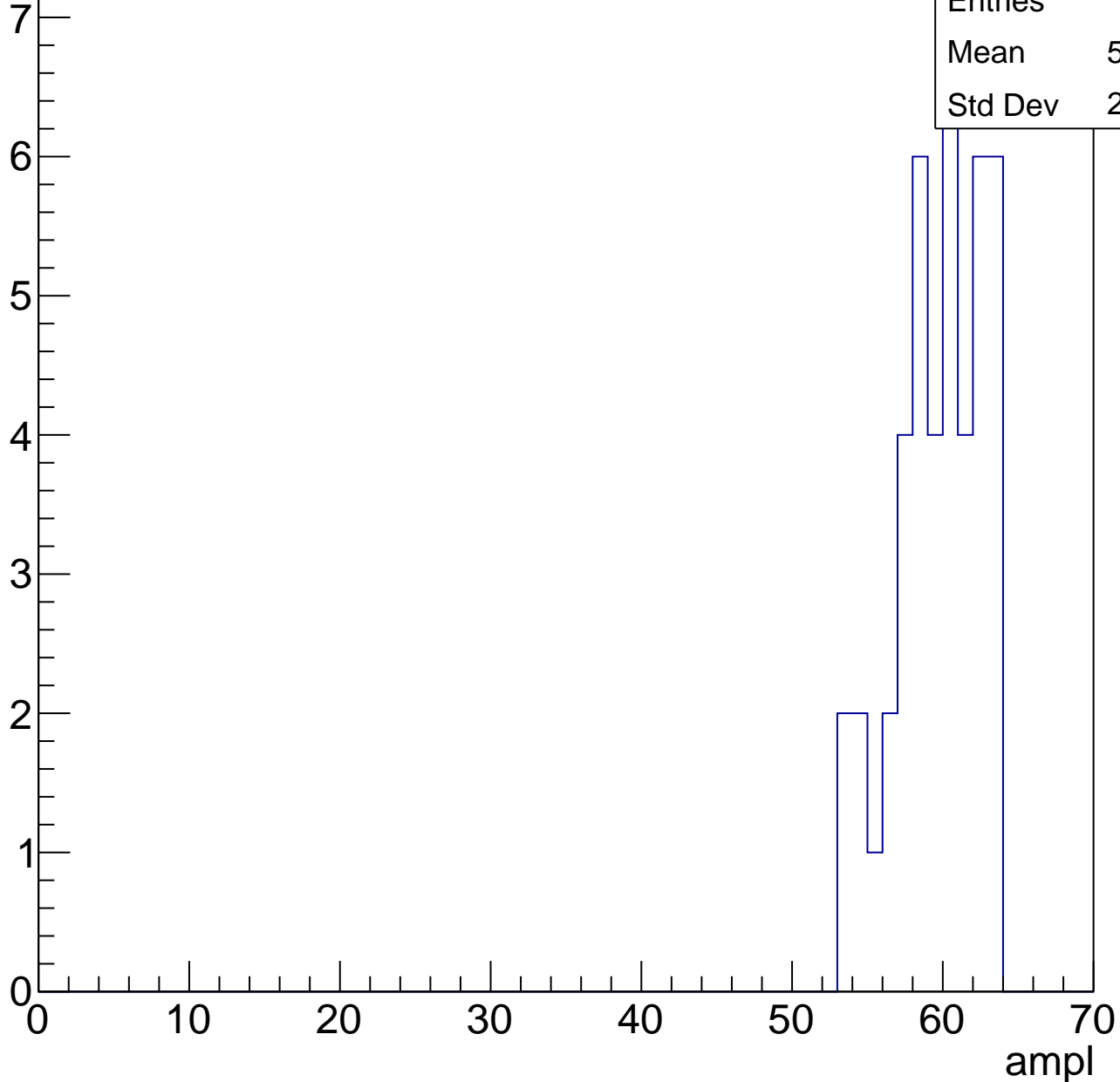
Entries	56
Mean	54.46
Std Dev	3.412



B1L103S, U2-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

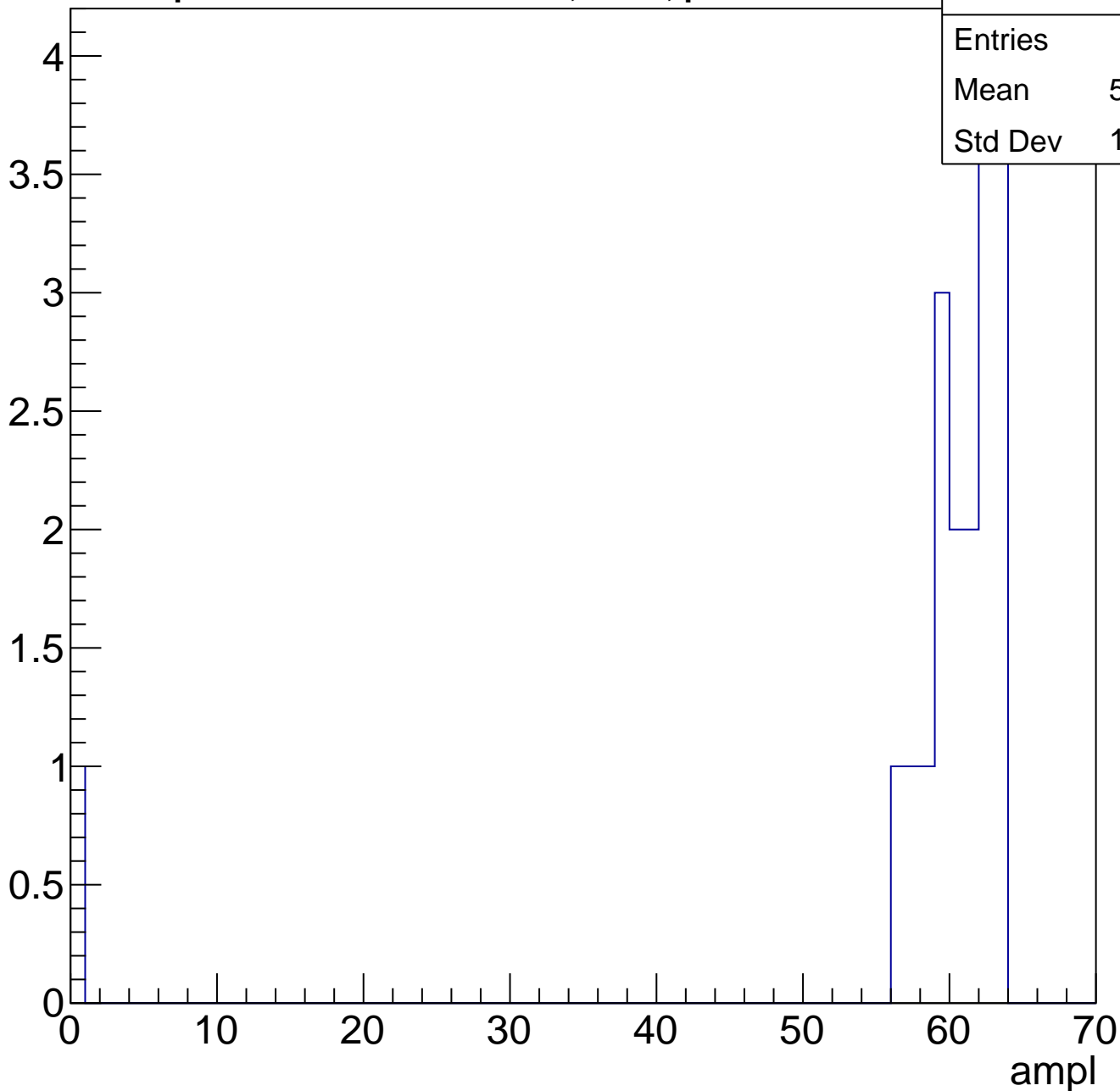
Entry



B1L103S, U2-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U2-ch124, adc0

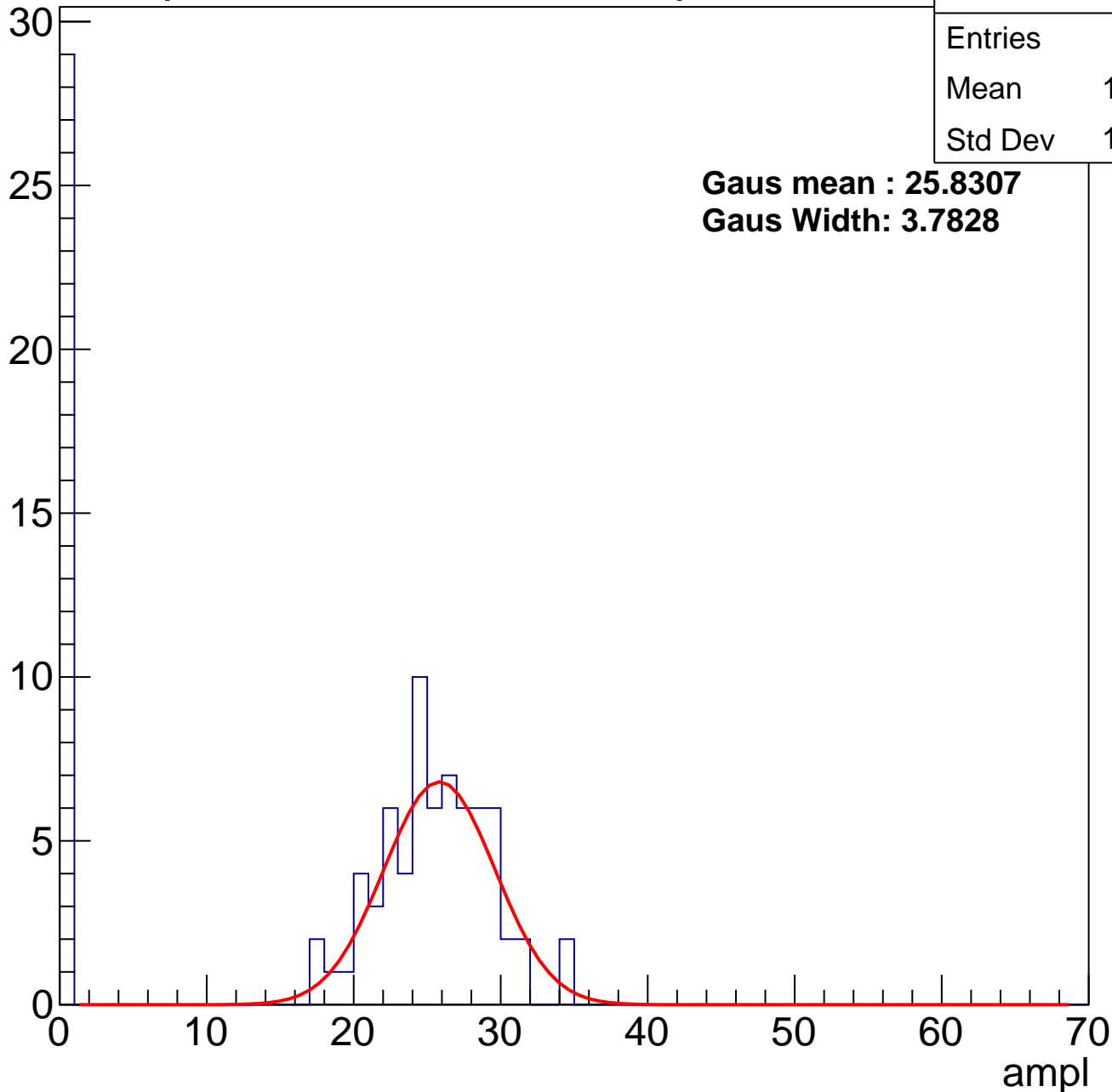
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	17.57
Std Dev	11.87

Gaus mean : 25.8307

Gaus Width: 3.7828

Entry



B1L103S, U2-ch124, adc1

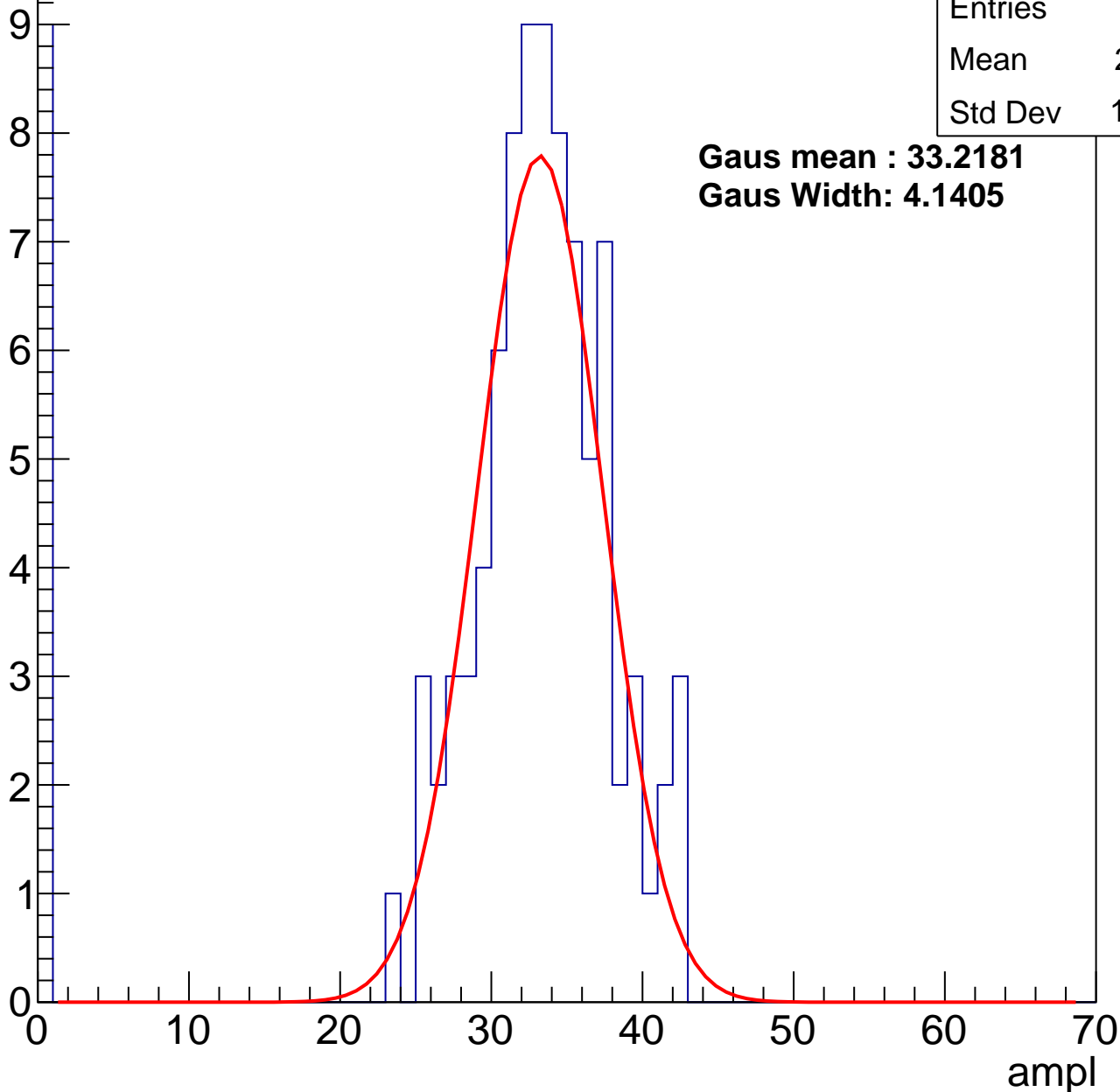
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	95
Mean	29.91
Std Dev	10.46

Gaus mean : 33.2181

Gaus Width: 4.1405



B1L103S, U2-ch124, adc2

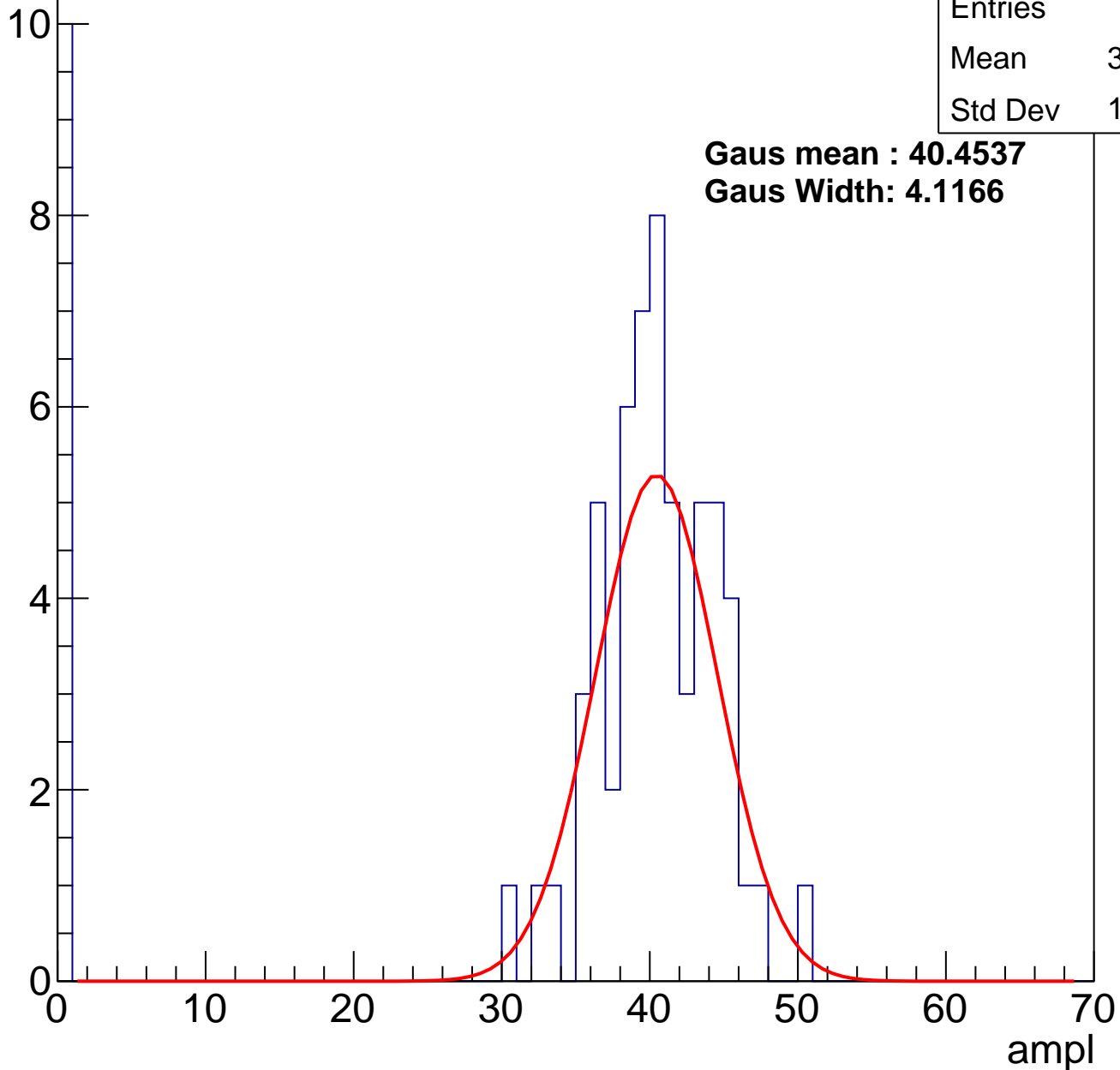
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	34.26
Std Dev	14.53

Gaus mean : 40.4537

Gaus Width: 4.1166

Entry

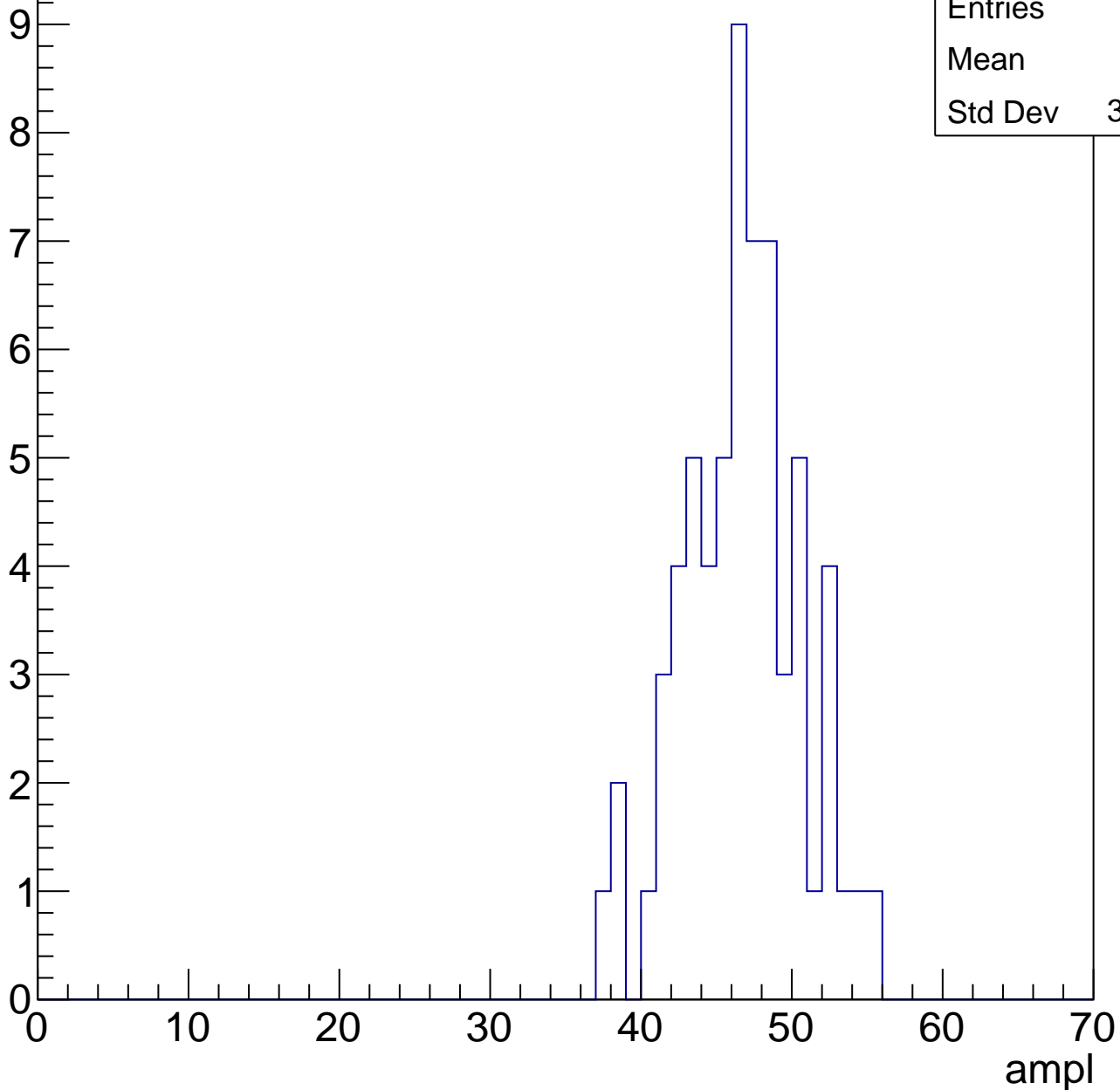


B1L103S, U2-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	46.2
Std Dev	3.858

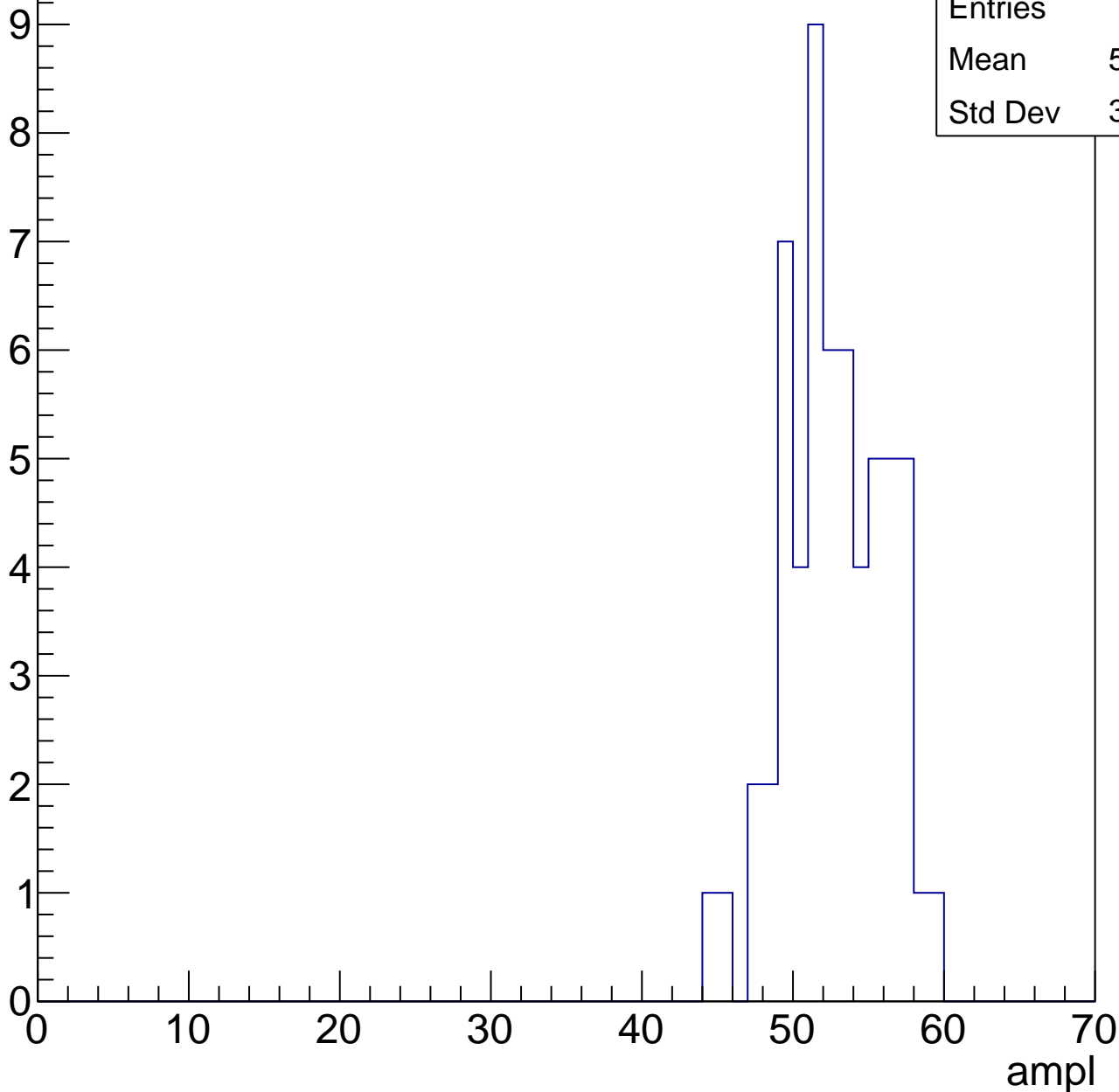


B1L103S, U2-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	52.27
Std Dev	3.282

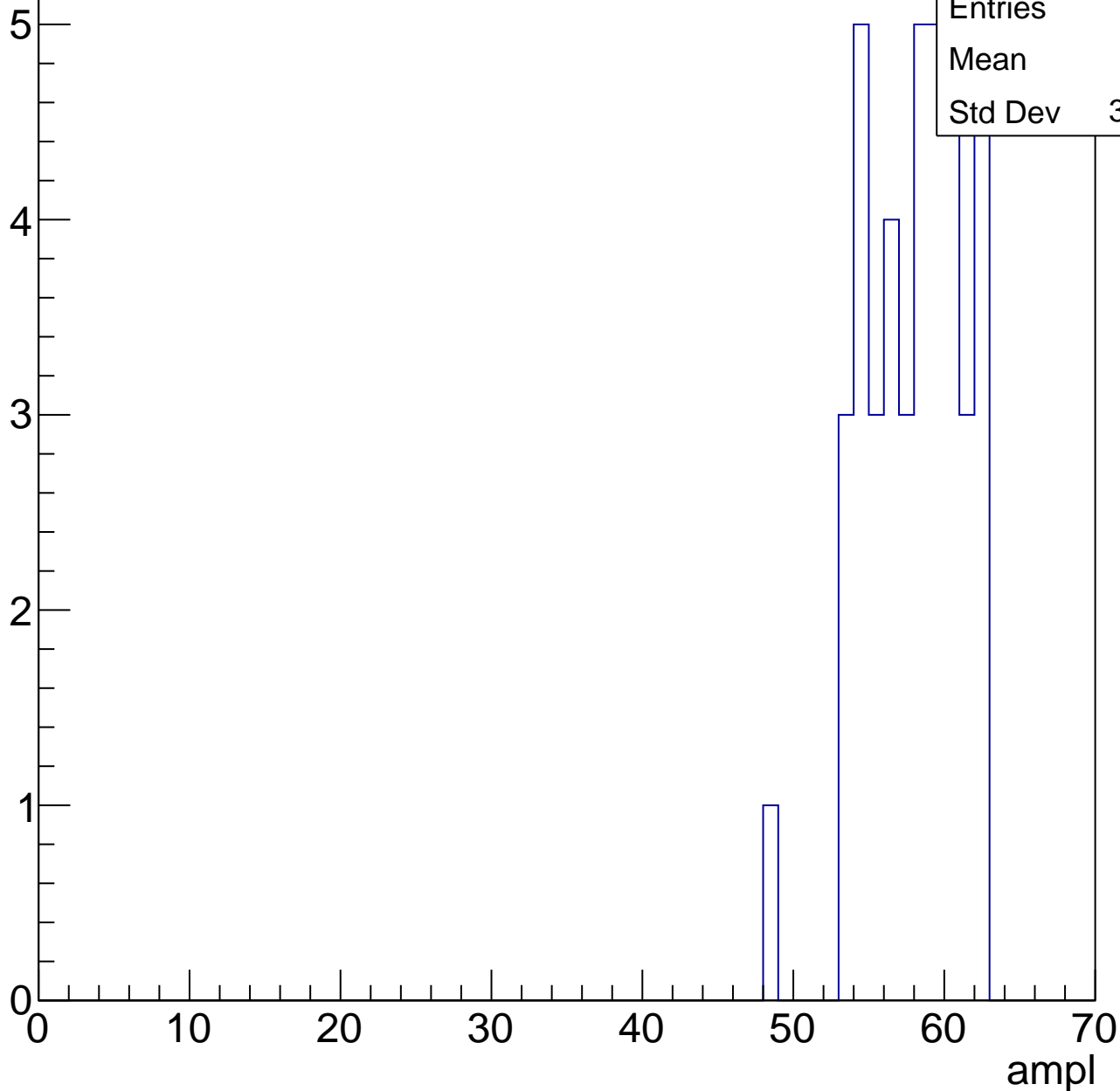


B1L103S, U2-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	57.5
Std Dev	3.172

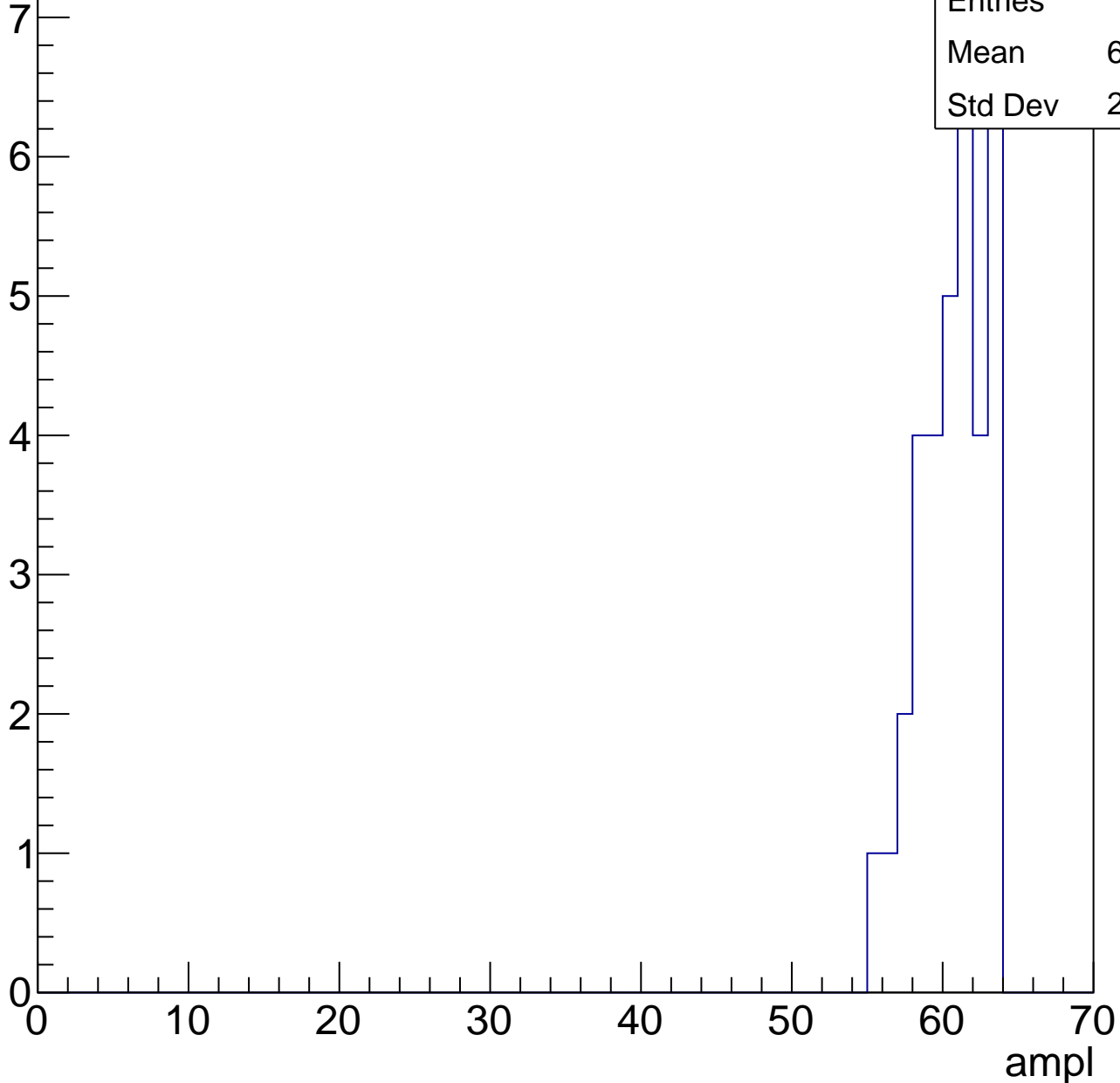


B1L103S, U2-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	60.26
Std Dev	2.156



B1L103S, U2-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

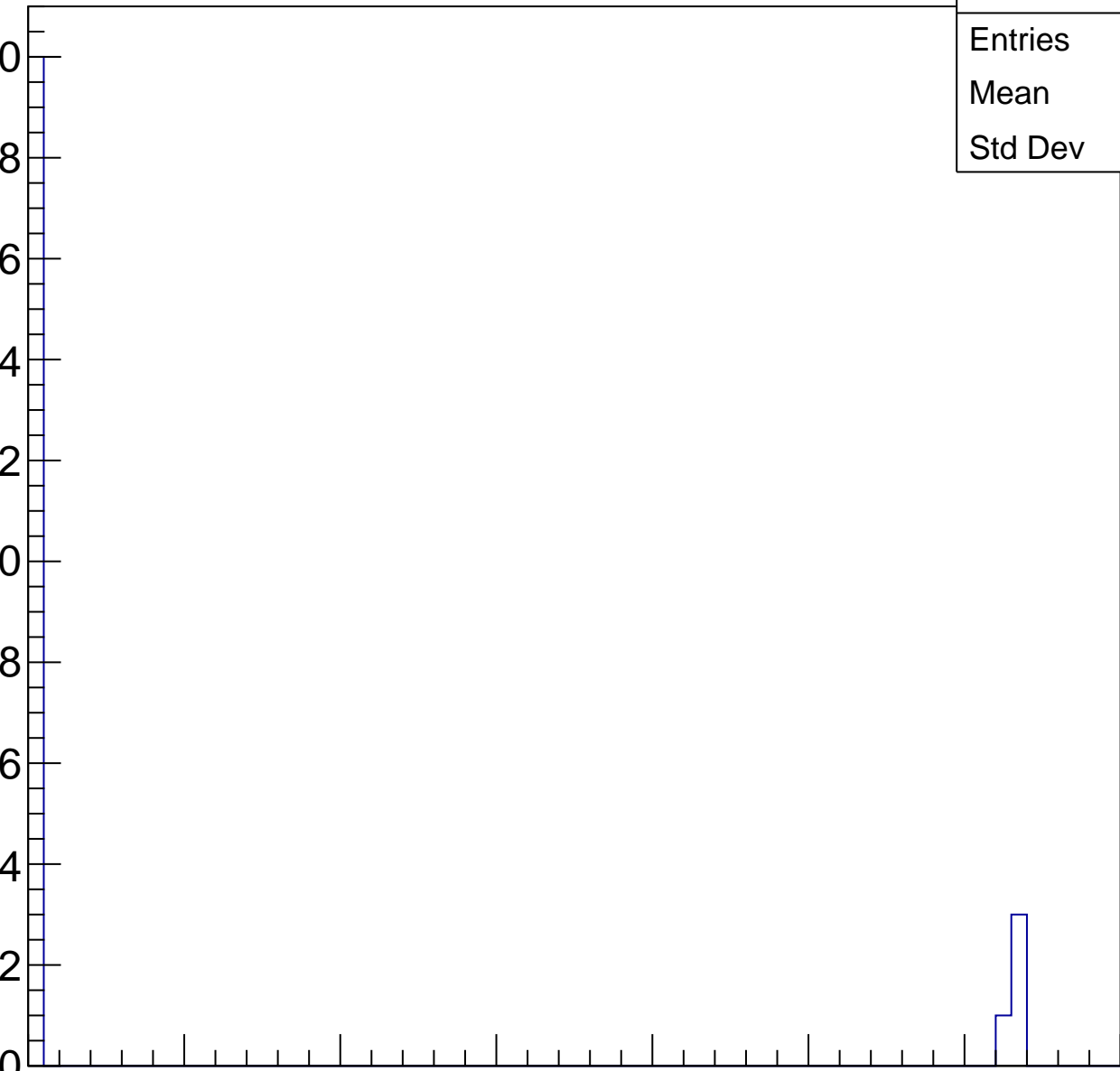
Entries	24
Mean	10.46
Std Dev	23.39

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U2-ch125, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	20.3
Std Dev	12.68

Gaus mean : 28.6682

Gaus Width: 4.2168

Entry

25

20

15

10

5

0

0

10

20

30

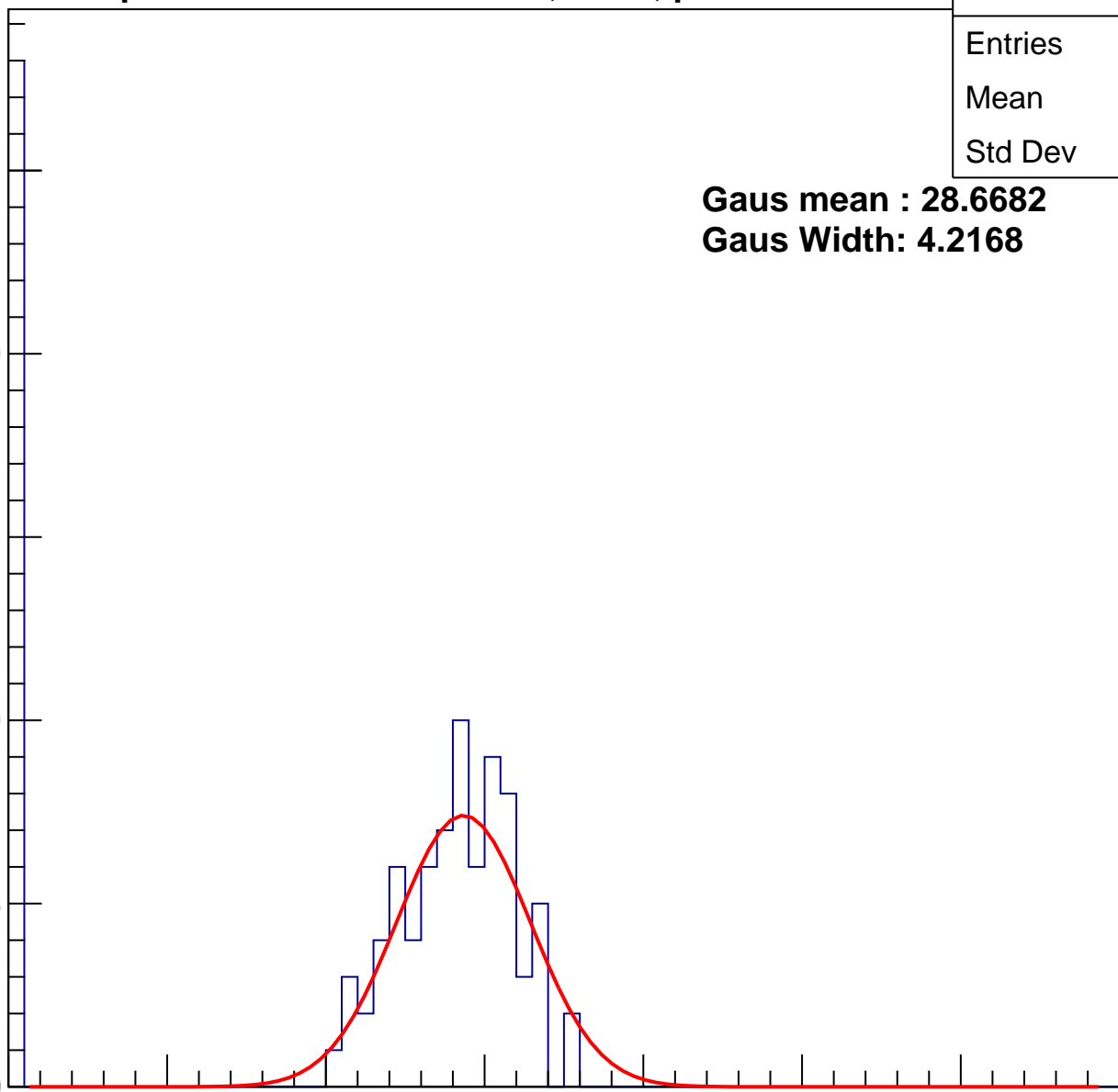
40

50

60

70

ampl



B1L103S, U2-ch125, adc1

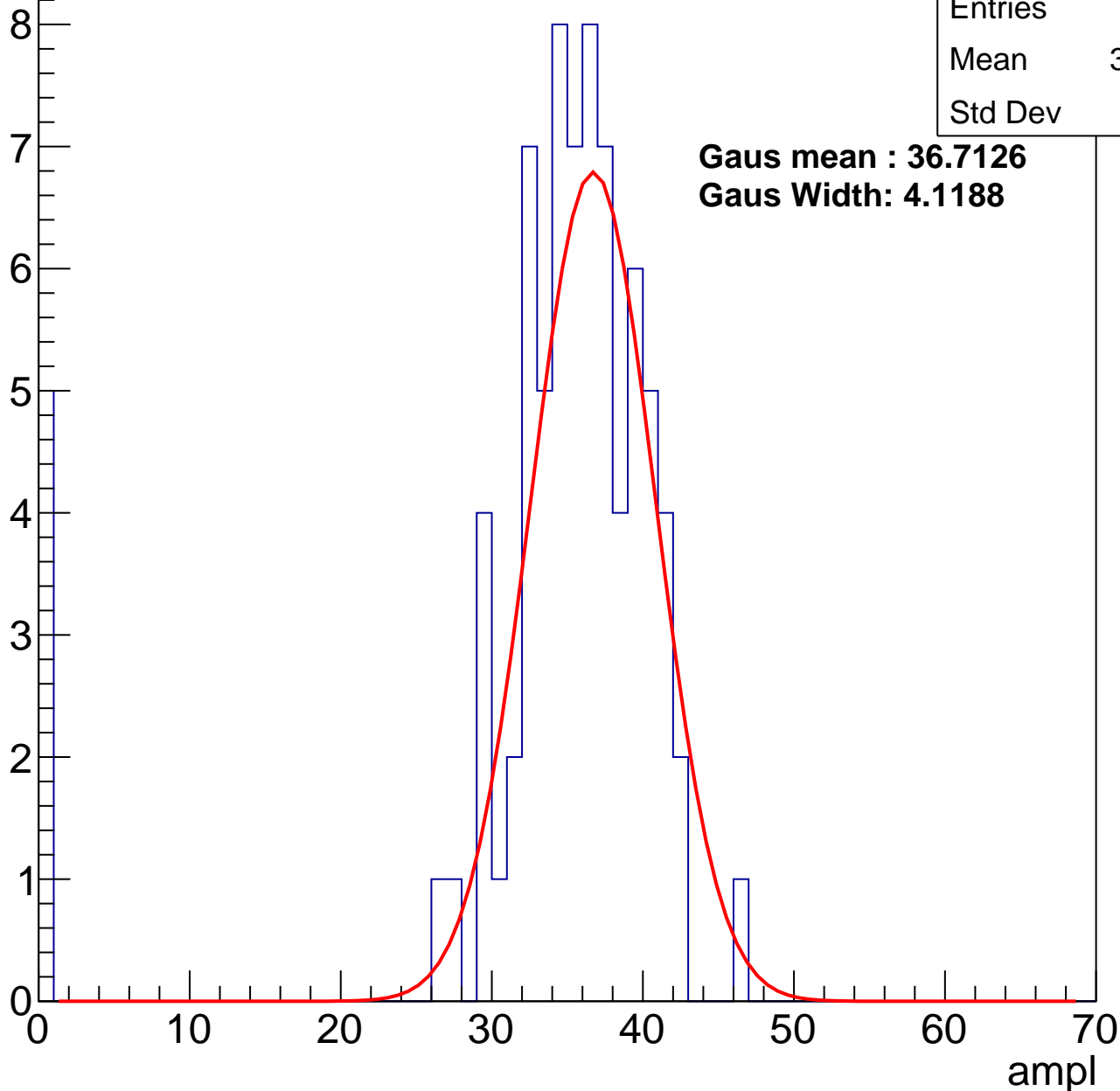
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	33.26
Std Dev	9.46

Gaus mean : 36.7126

Gaus Width: 4.1188



B1L103S, U2-ch125, adc2

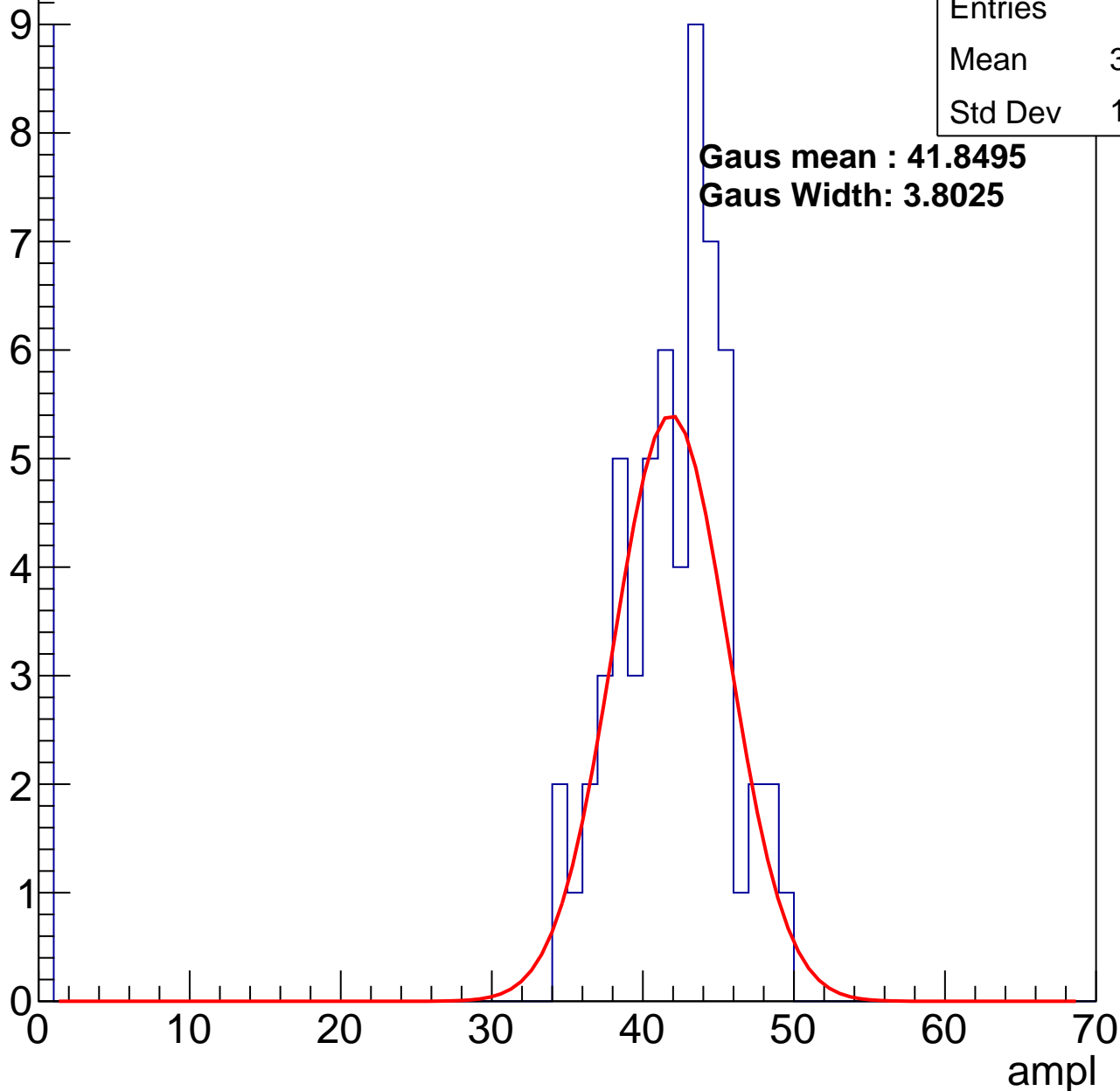
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	36.13
Std Dev	14.49

Gaus mean : 41.8495

Gaus Width: 3.8025

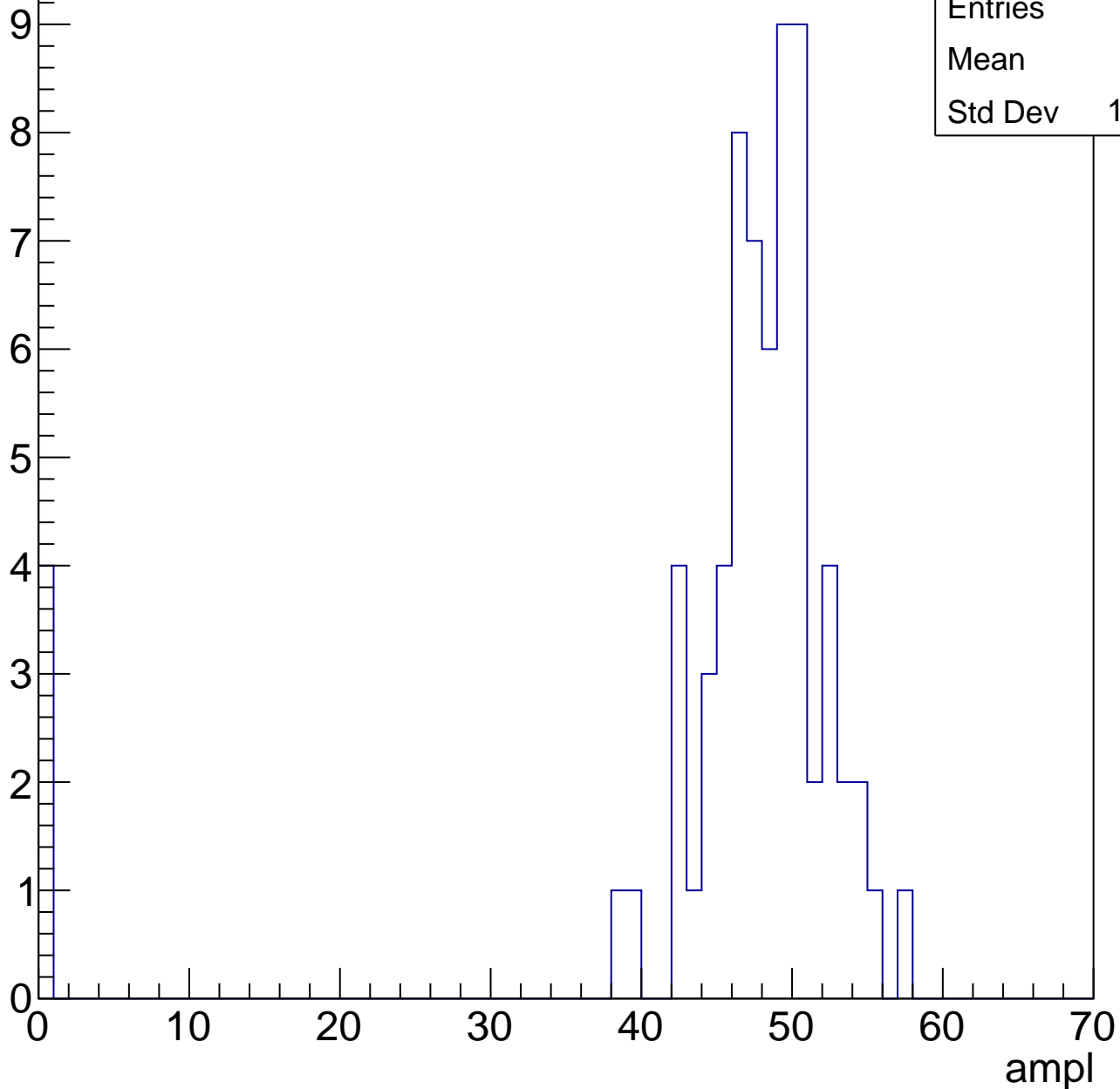


B1L103S, U2-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	45.1
Std Dev	11.73

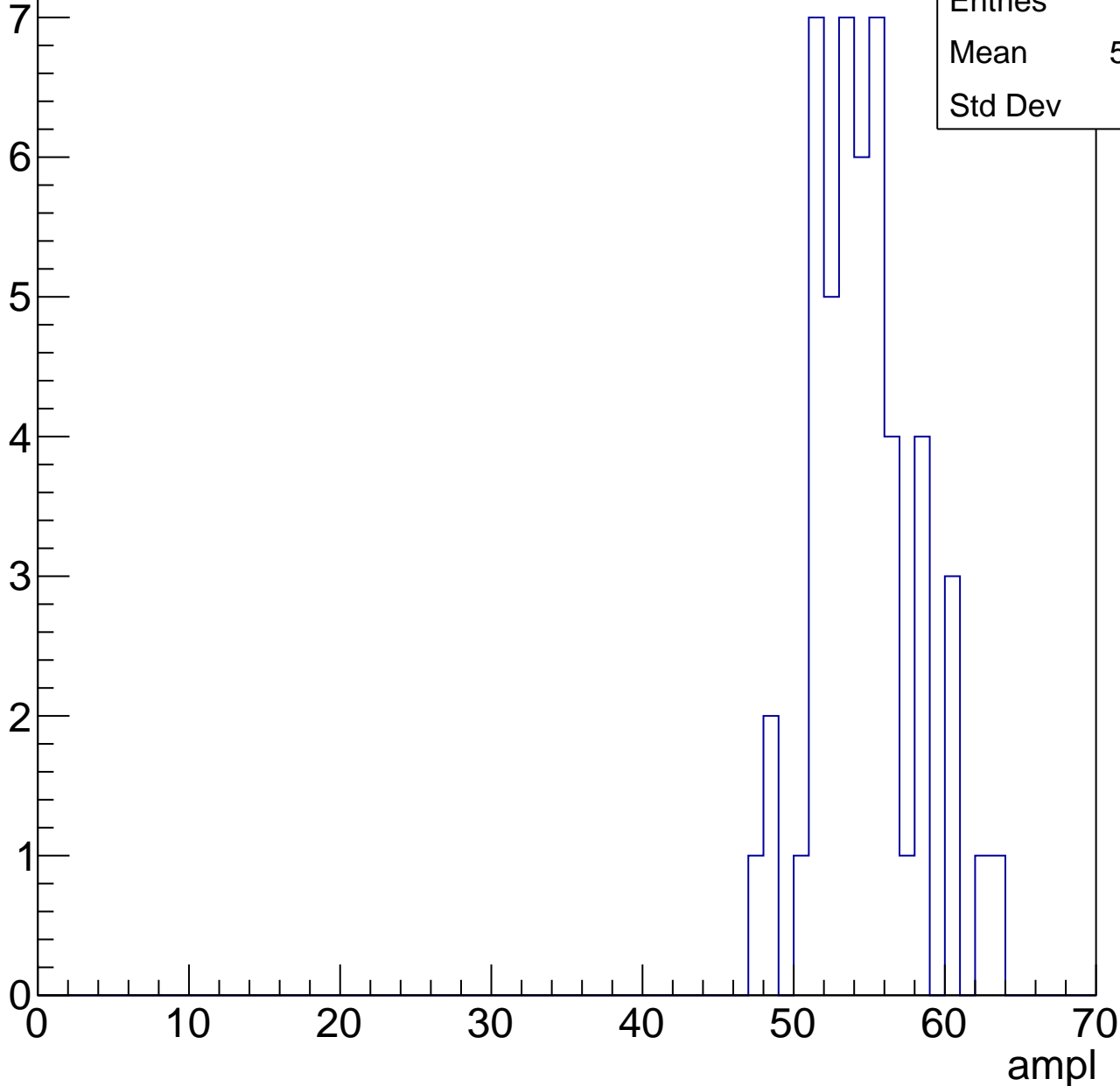


B1L103S, U2-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

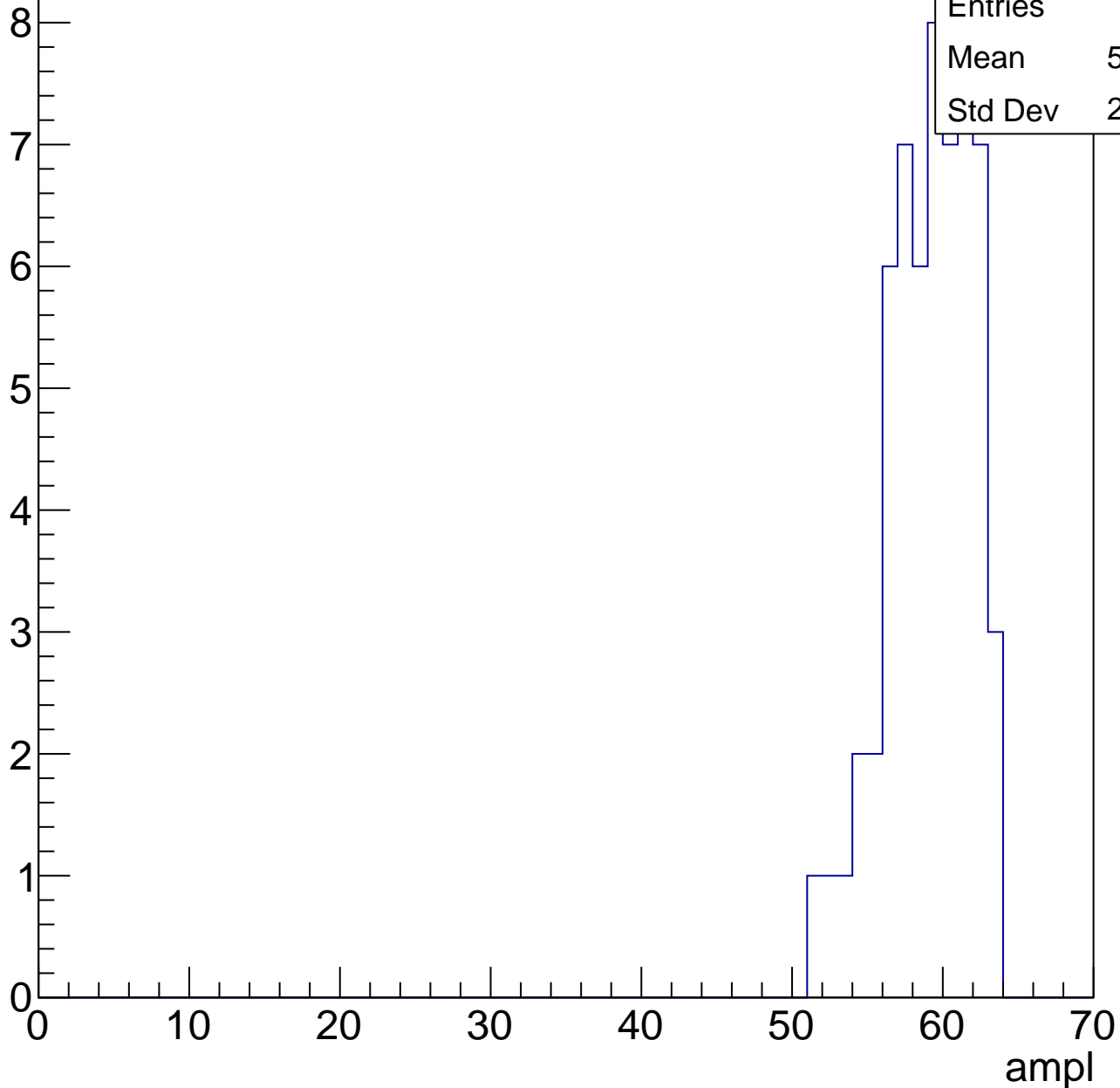
Entries	50
Mean	54.16
Std Dev	3.39



B1L103S, U2-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

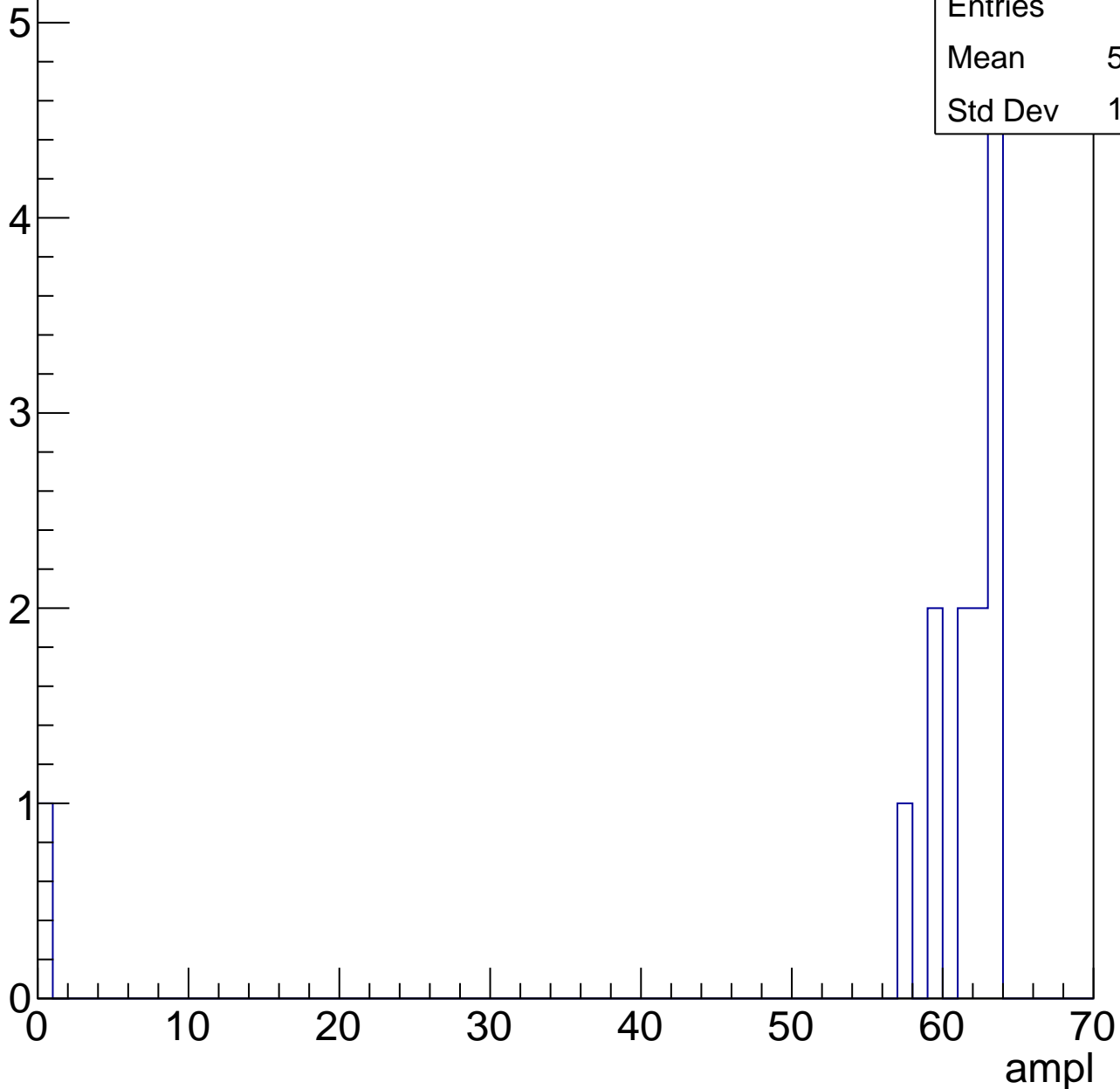


B1L103S, U2-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	56.62
Std Dev	16.45

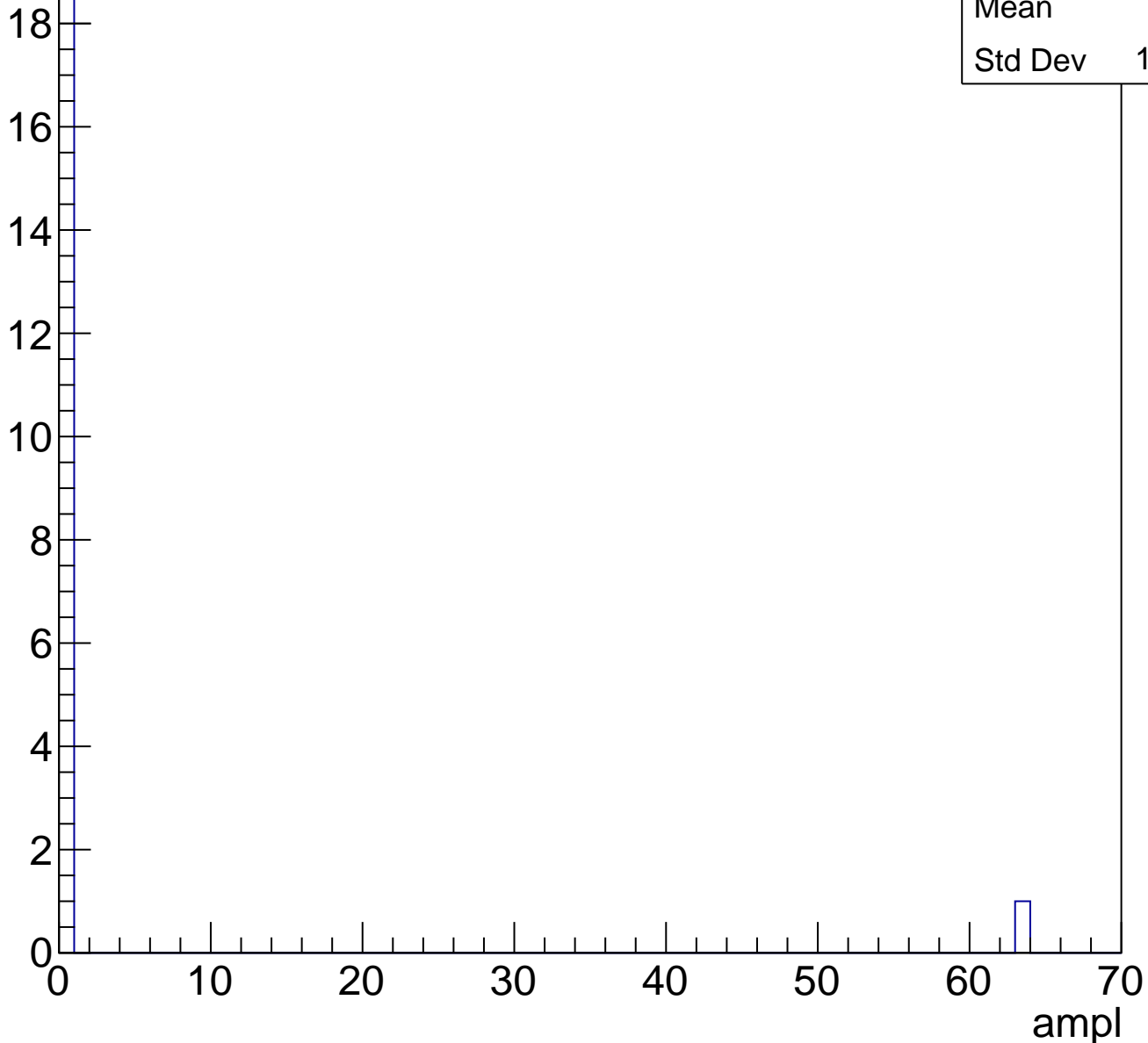


B1L103S, U2-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U2-ch126, adc0

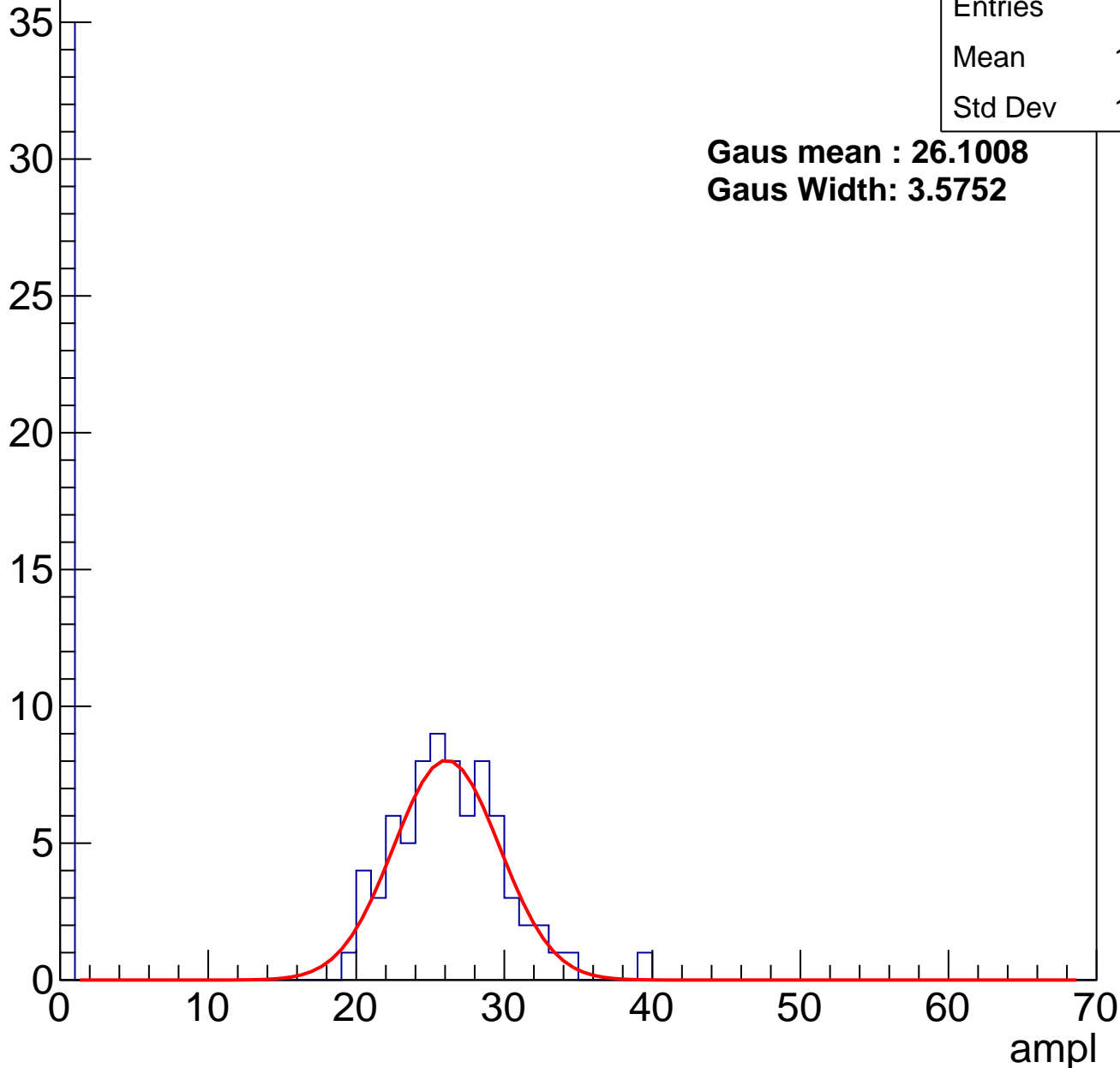
calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	17.58
Std Dev	12.46

Gaus mean : 26.1008

Gaus Width: 3.5752

Entry



B1L103S, U2-ch126, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	26.66
Std Dev	13.01

Gaus mean : 33.5639

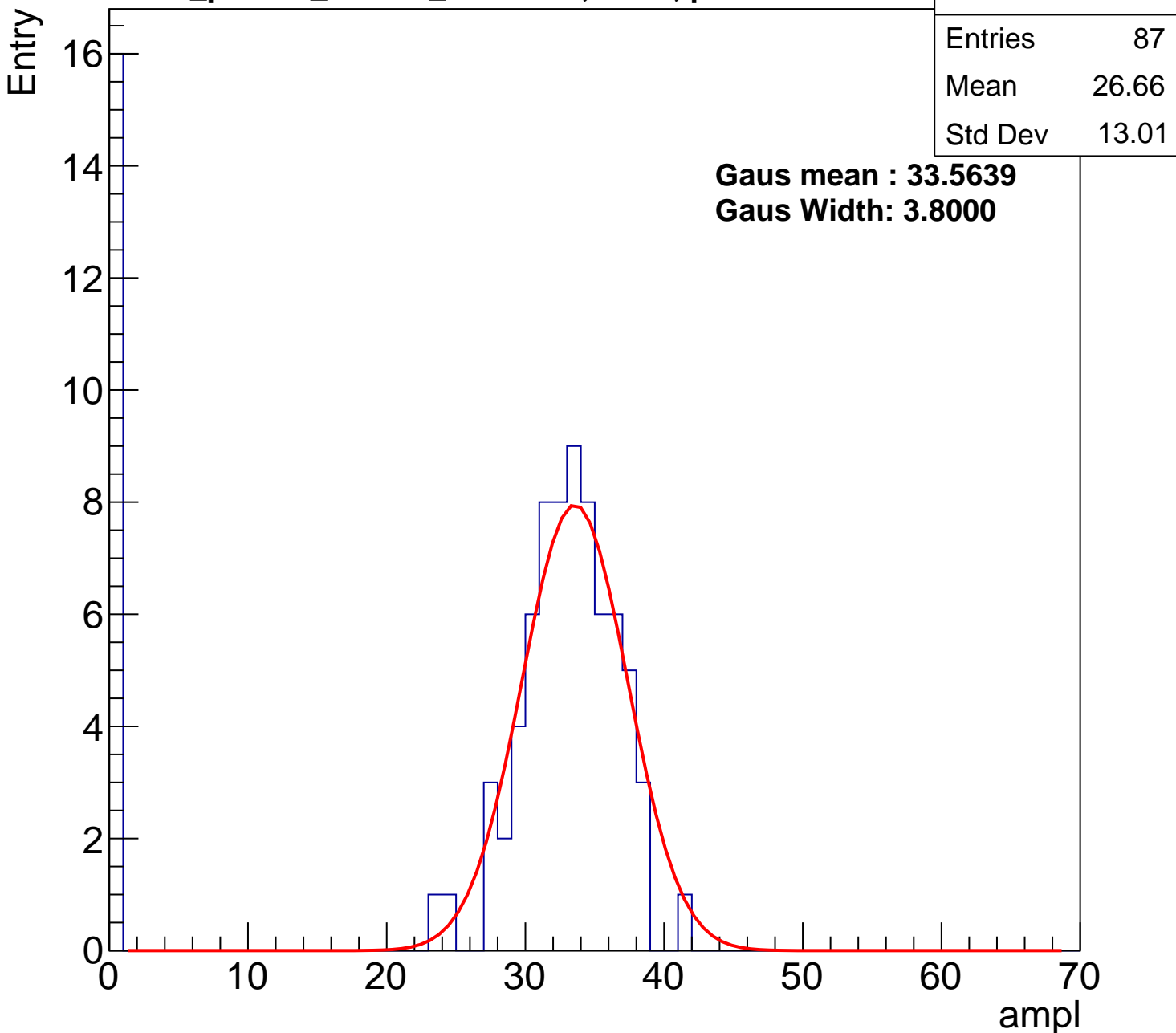
Gaus Width: 3.8000

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U2-ch126, adc2

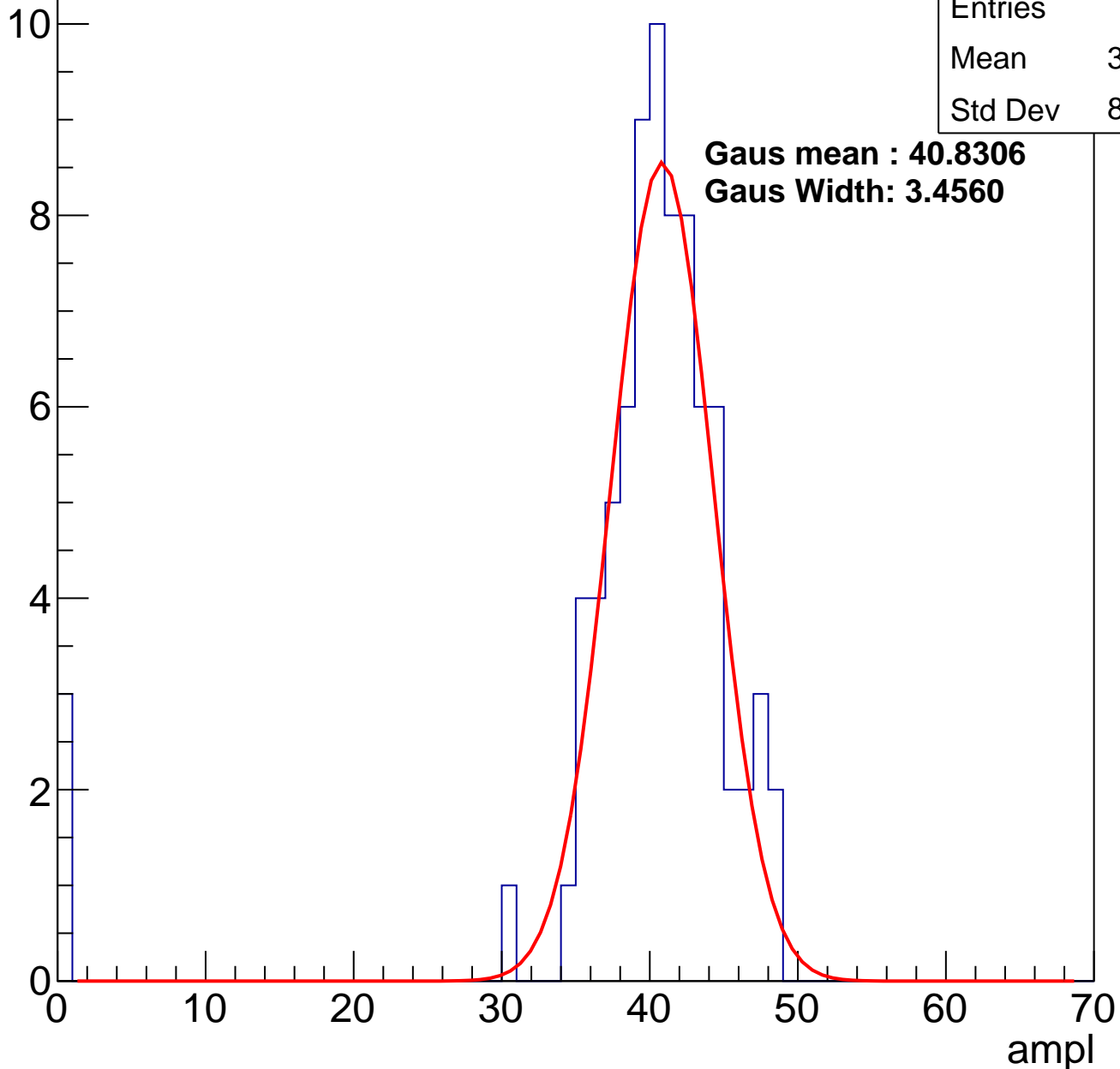
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	38.96
Std Dev	8.425

Gaus mean : 40.8306

Gaus Width: 3.4560

Entry

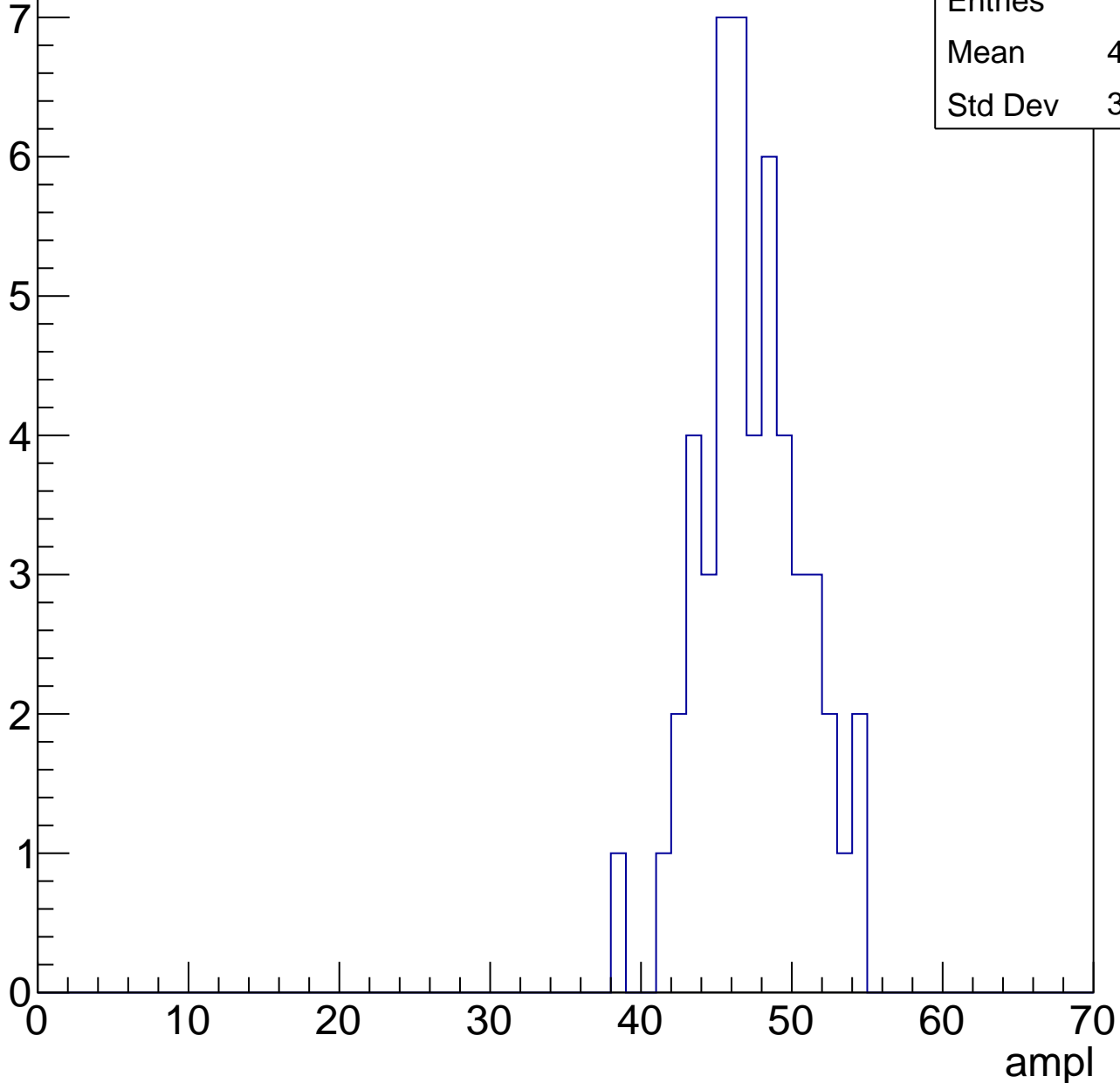


B1L103S, U2-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	46.88
Std Dev	3.386

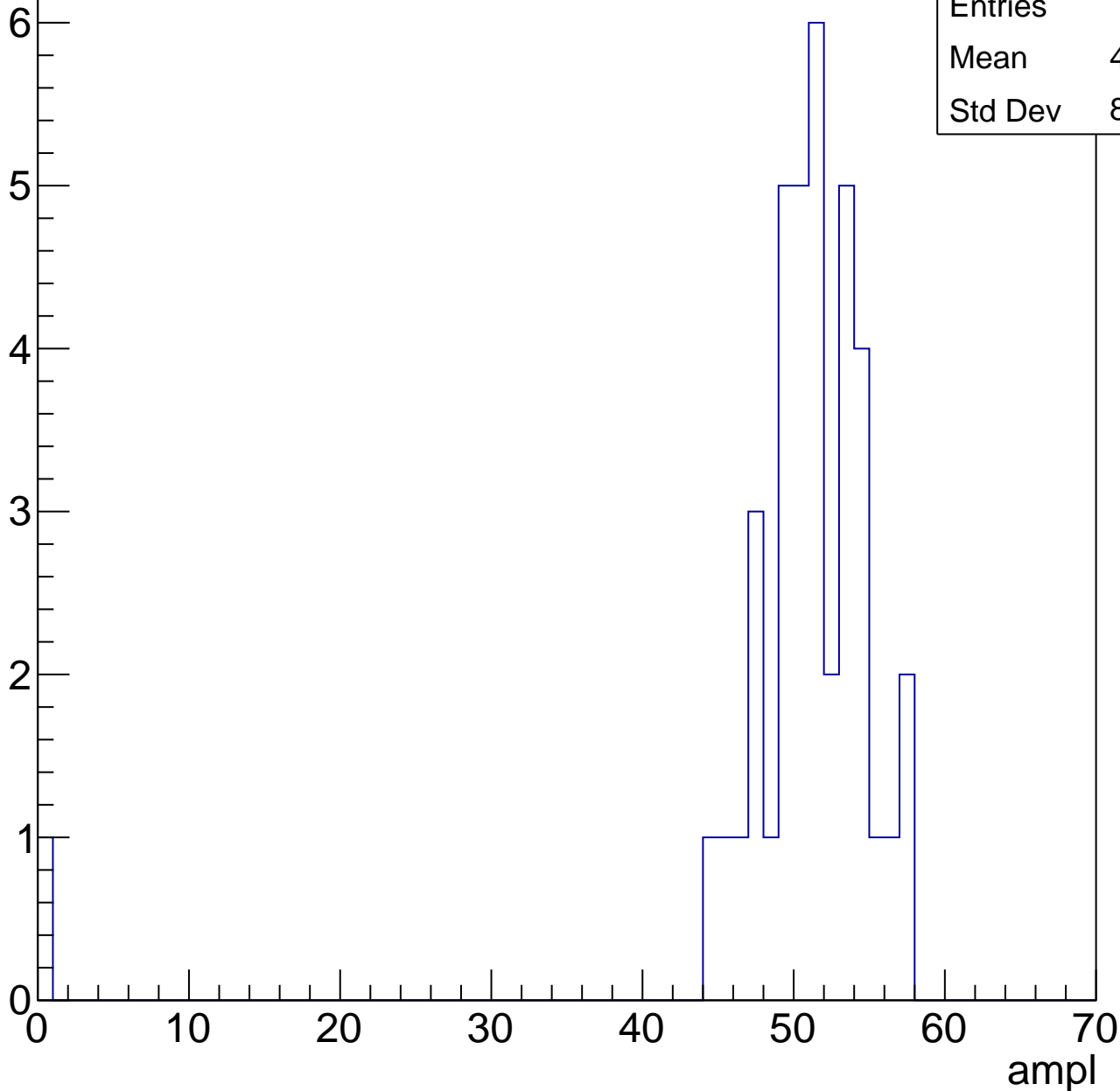


B1L103S, U2-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

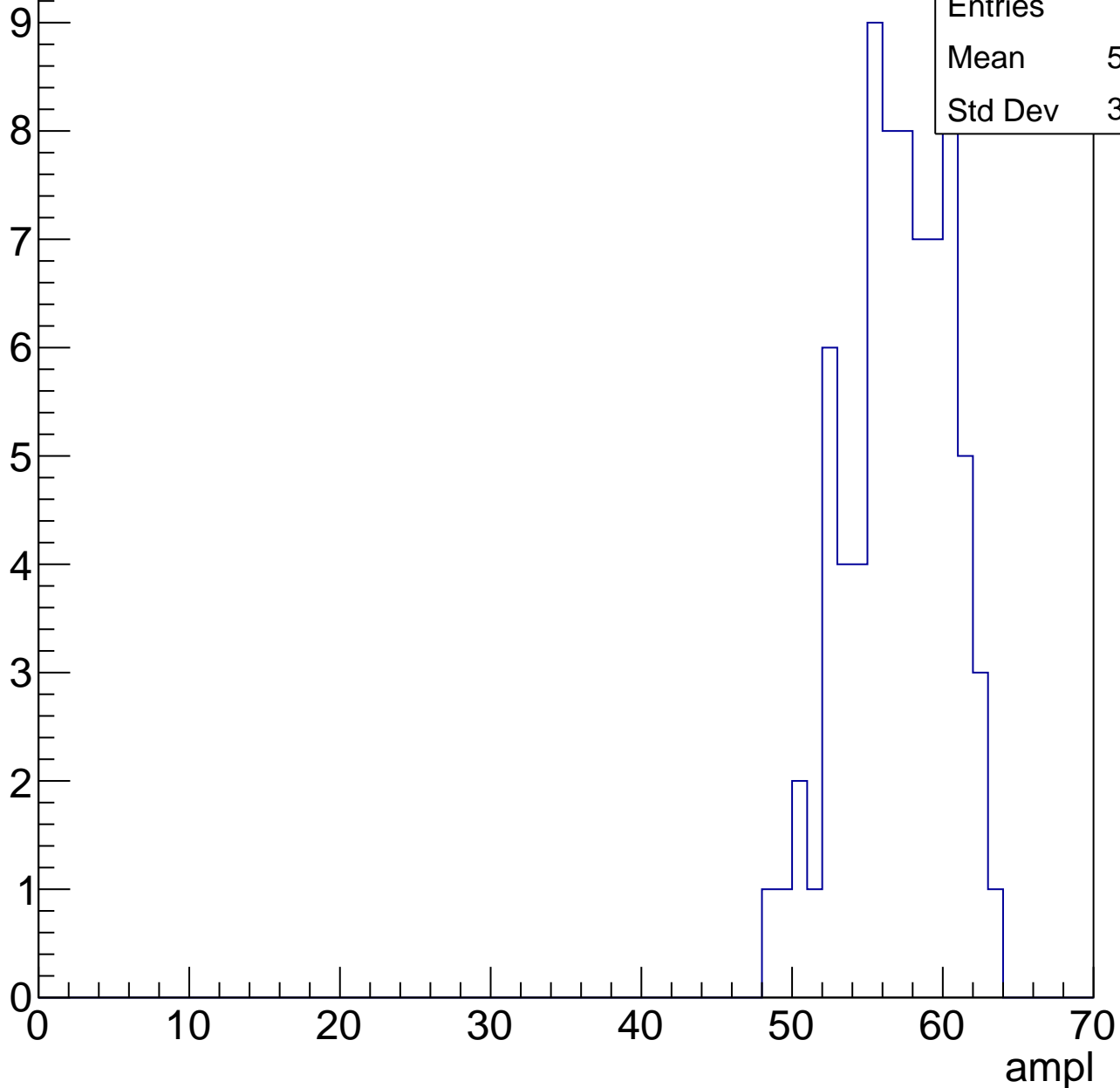
Entries	39
Mean	49.62
Std Dev	8.607



B1L103S, U2-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

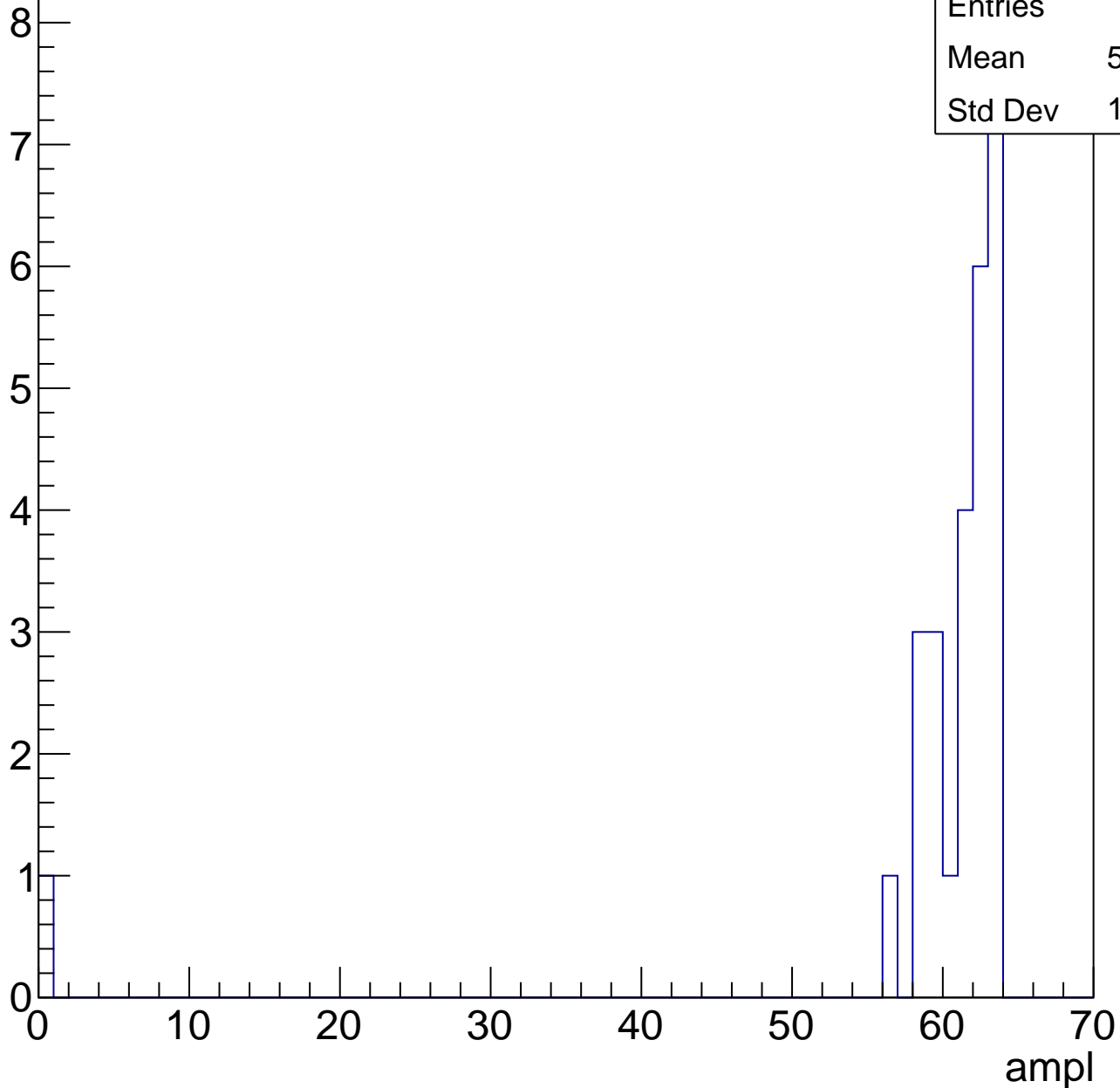


B1L103S, U2-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	58.78
Std Dev	11.69

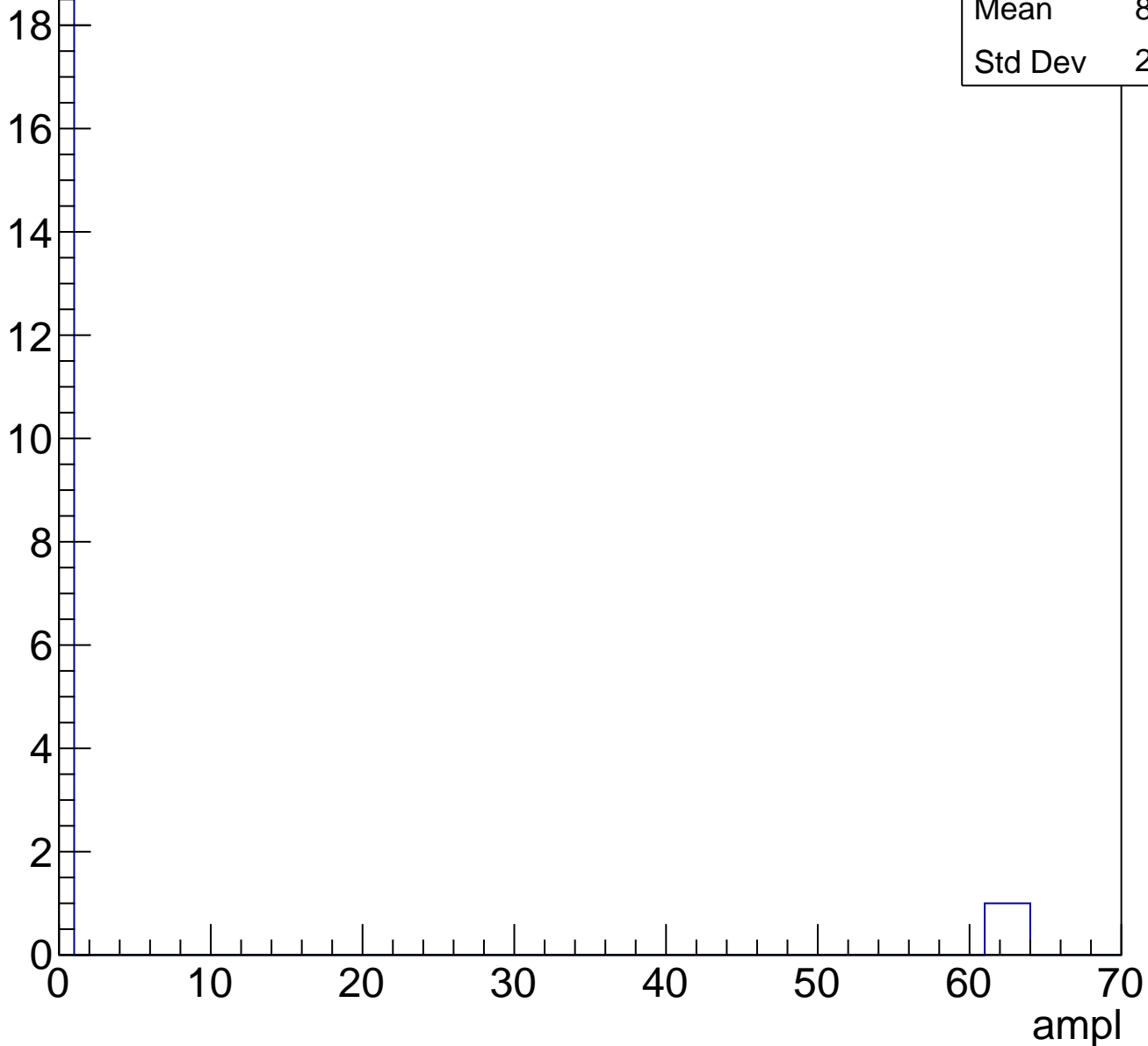


B1L103S, U2-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U2-ch127, adc0

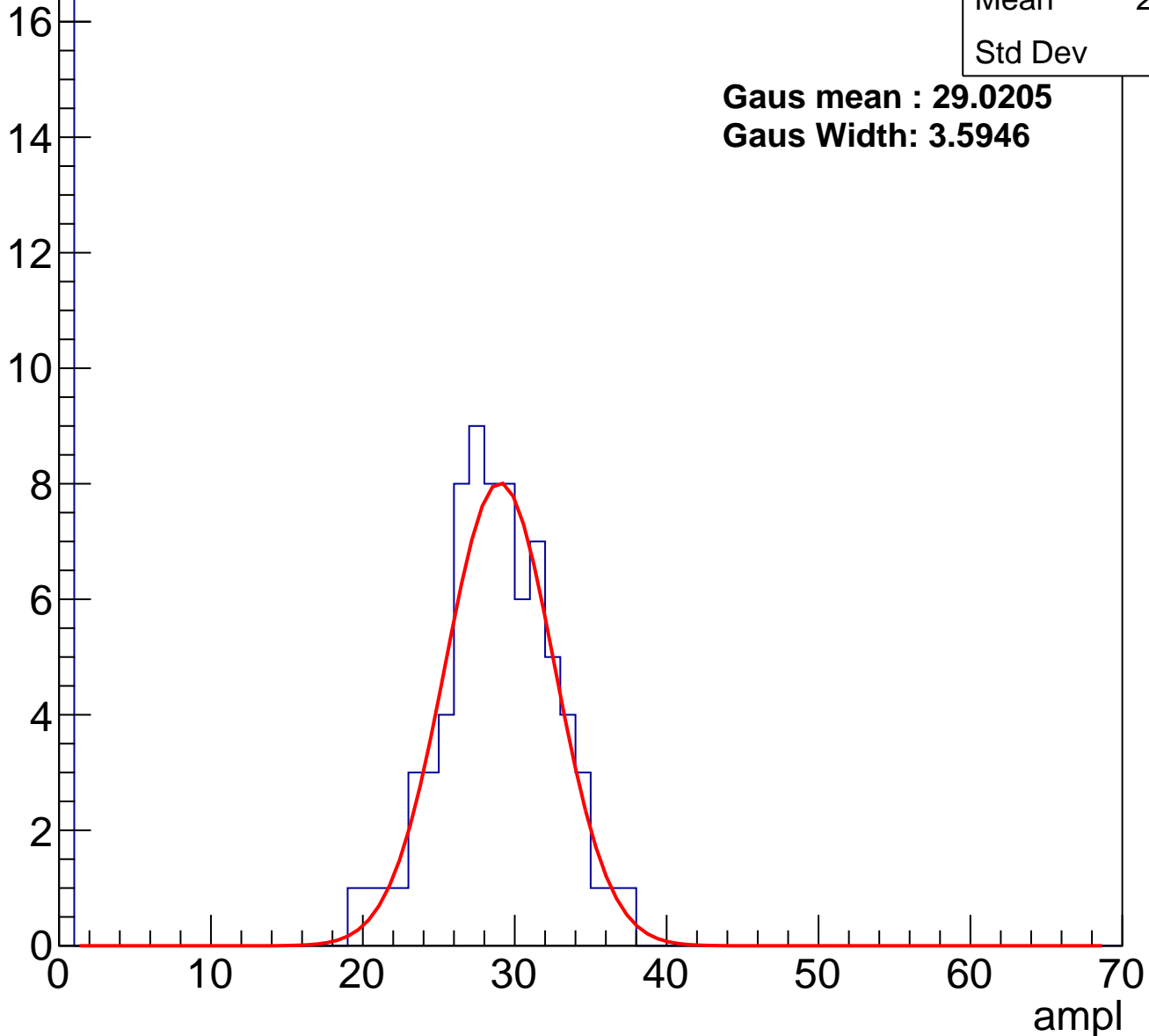
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	23.14
Std Dev	11.5

Gaus mean : 29.0205

Gaus Width: 3.5946

Entry



B1L103S, U2-ch127, adc1

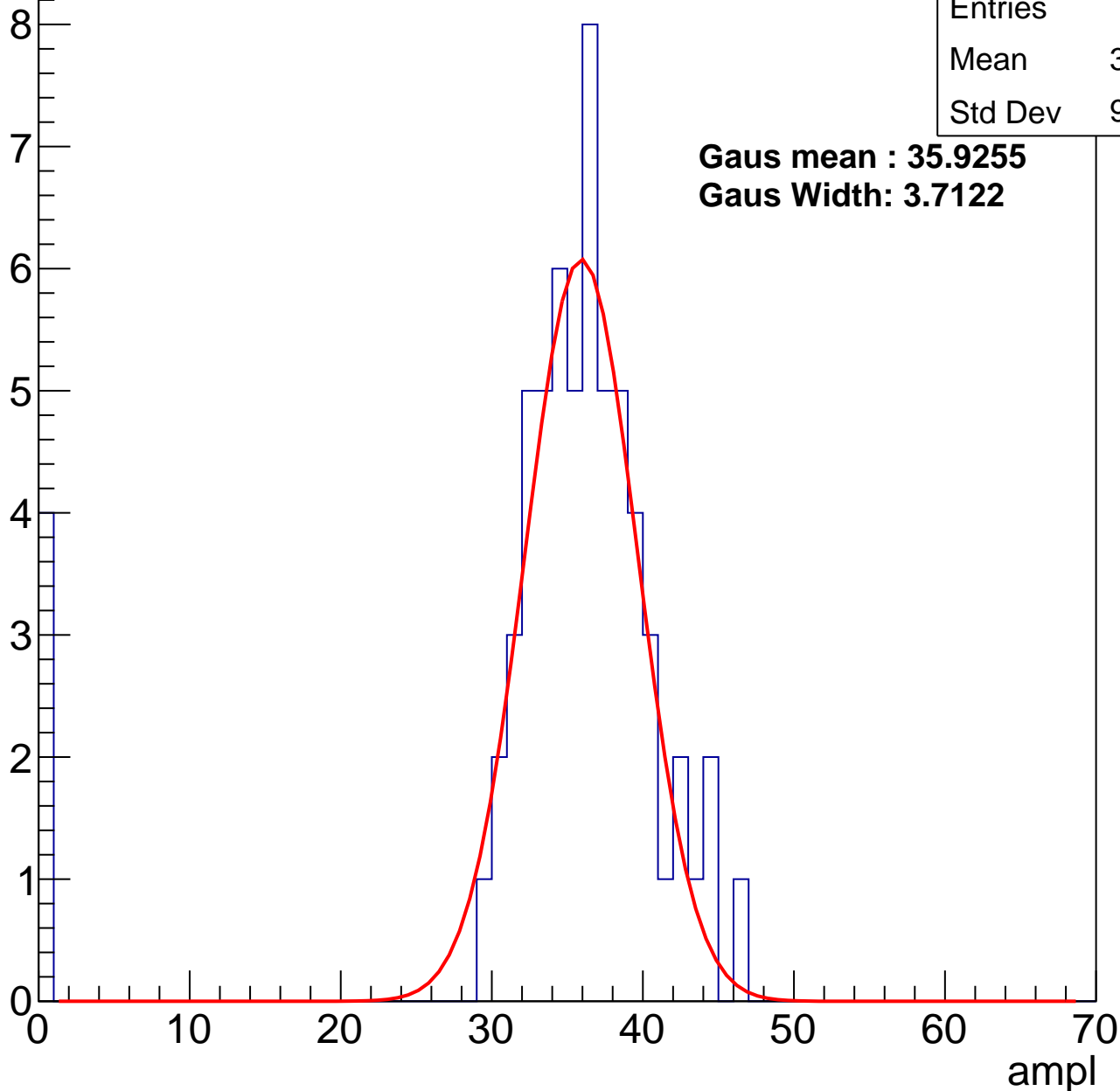
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	33.76
Std Dev	9.513

Gaus mean : 35.9255

Gaus Width: 3.7122



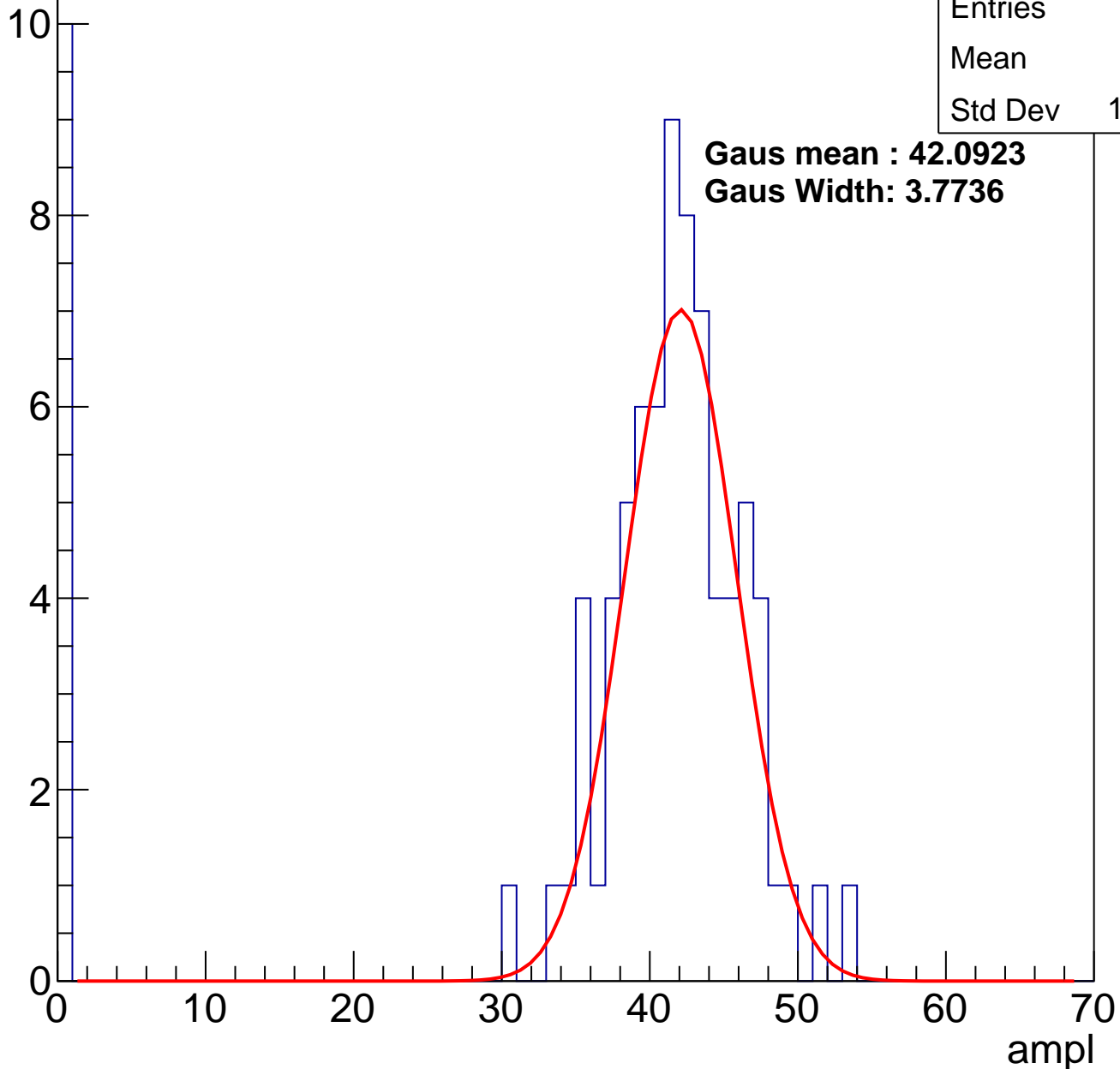
B1L103S, U2-ch127, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	36.5
Std Dev	13.98

Gaus mean : 42.0923
Gaus Width: 3.7736

Entry

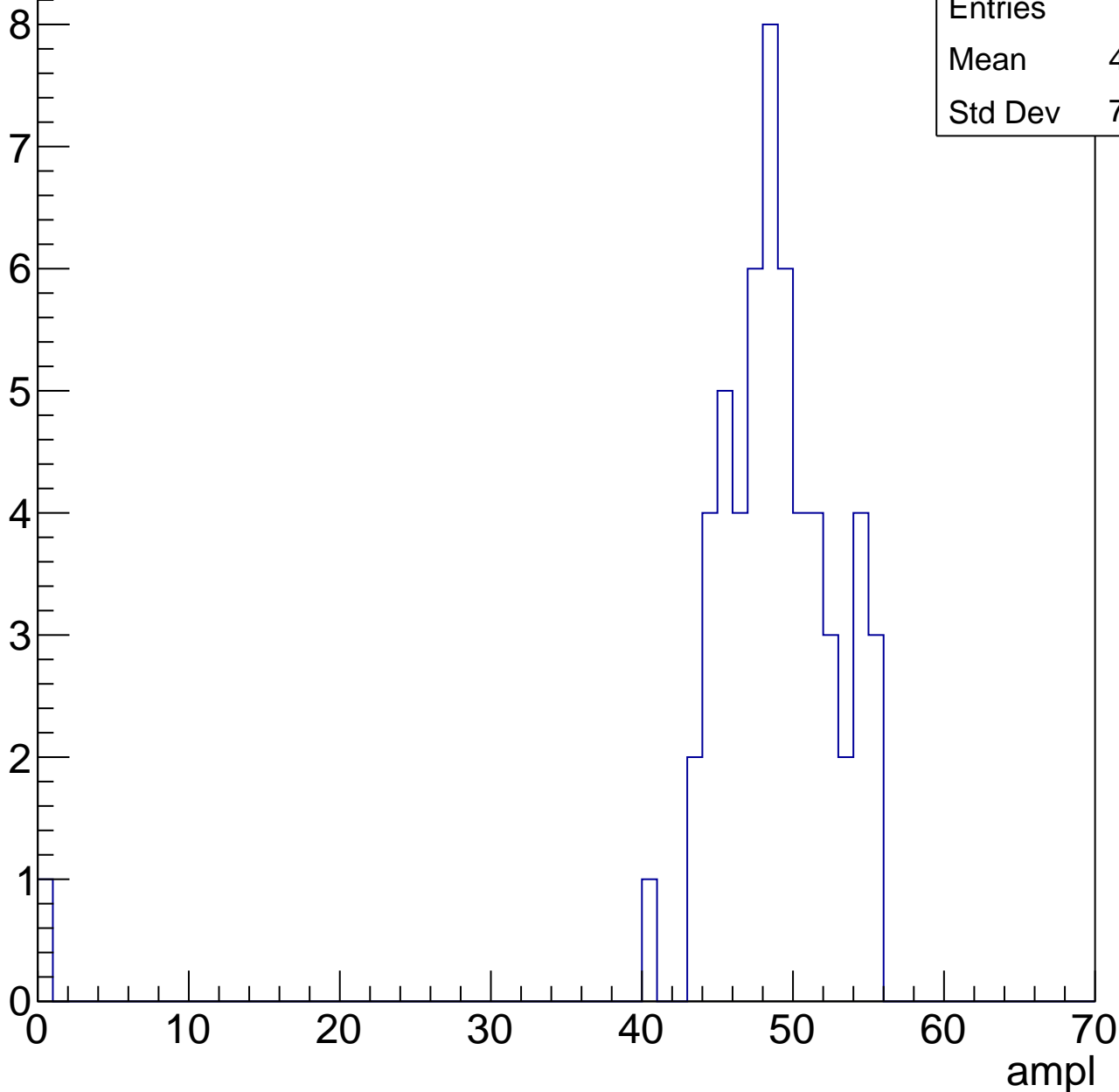


B1L103S, U2-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	47.68
Std Dev	7.243

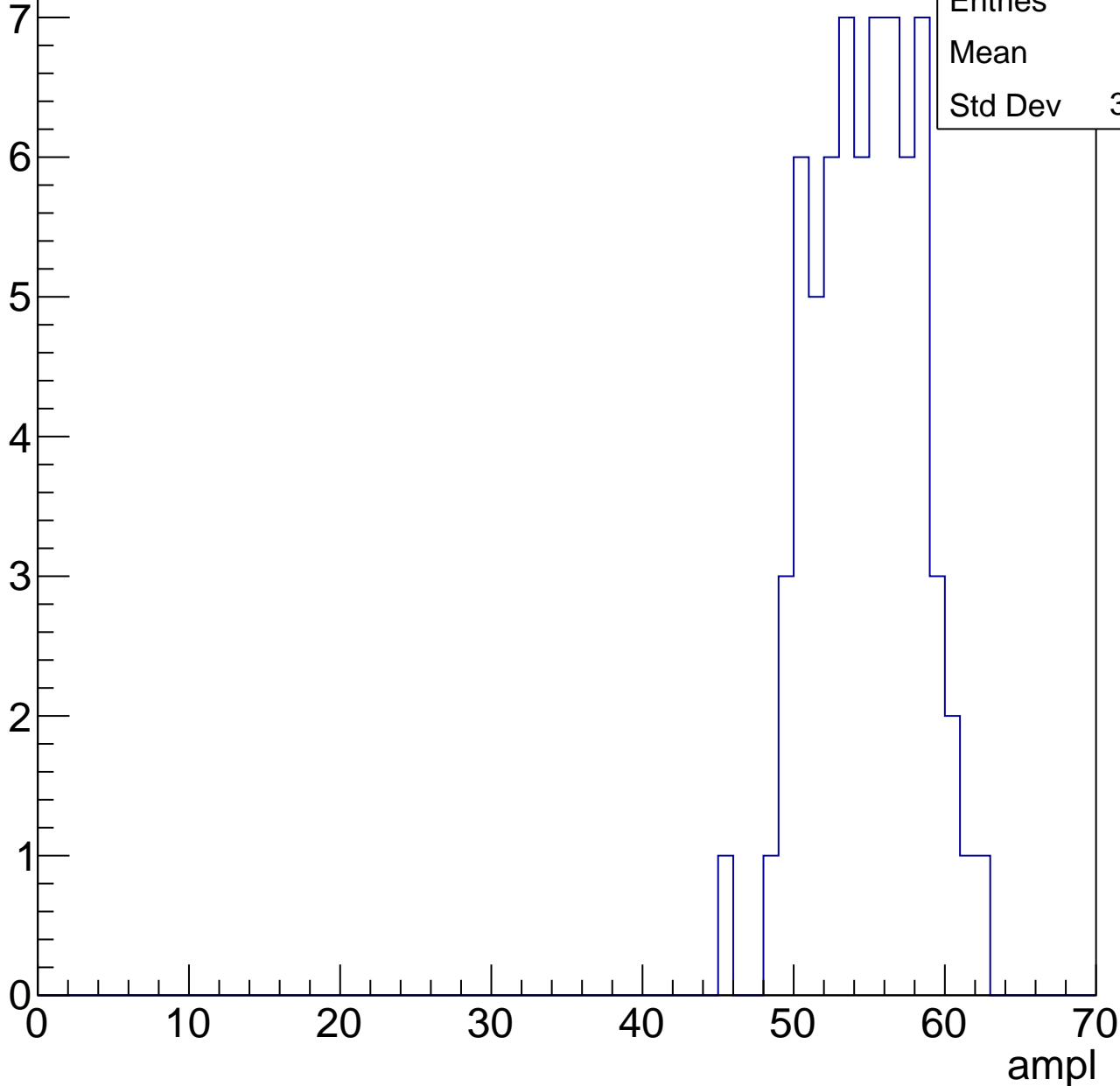


B1L103S, U2-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	54.3
Std Dev	3.444

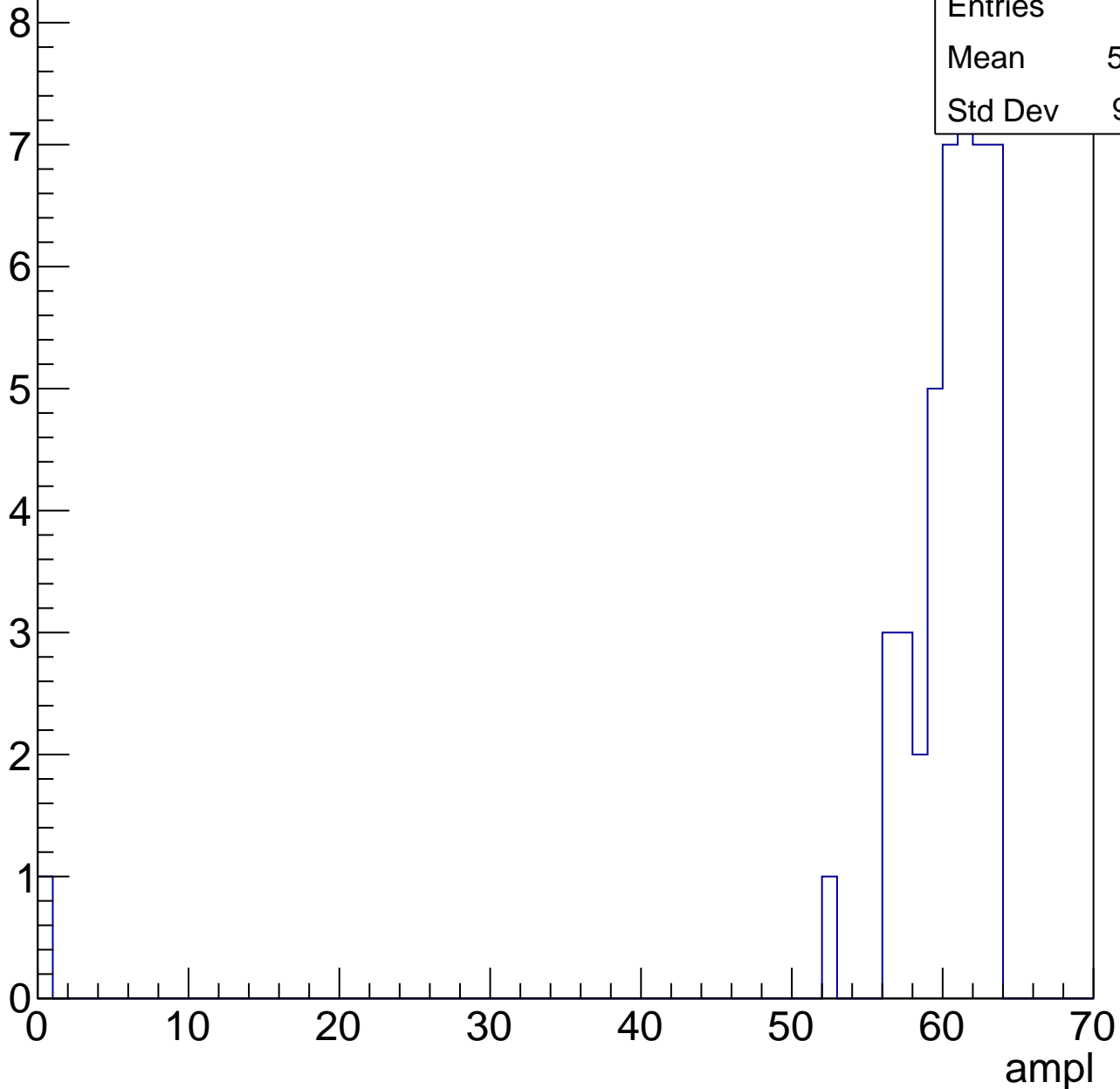


B1L103S, U2-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	58.75
Std Dev	9.271



B1L103S, U2-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

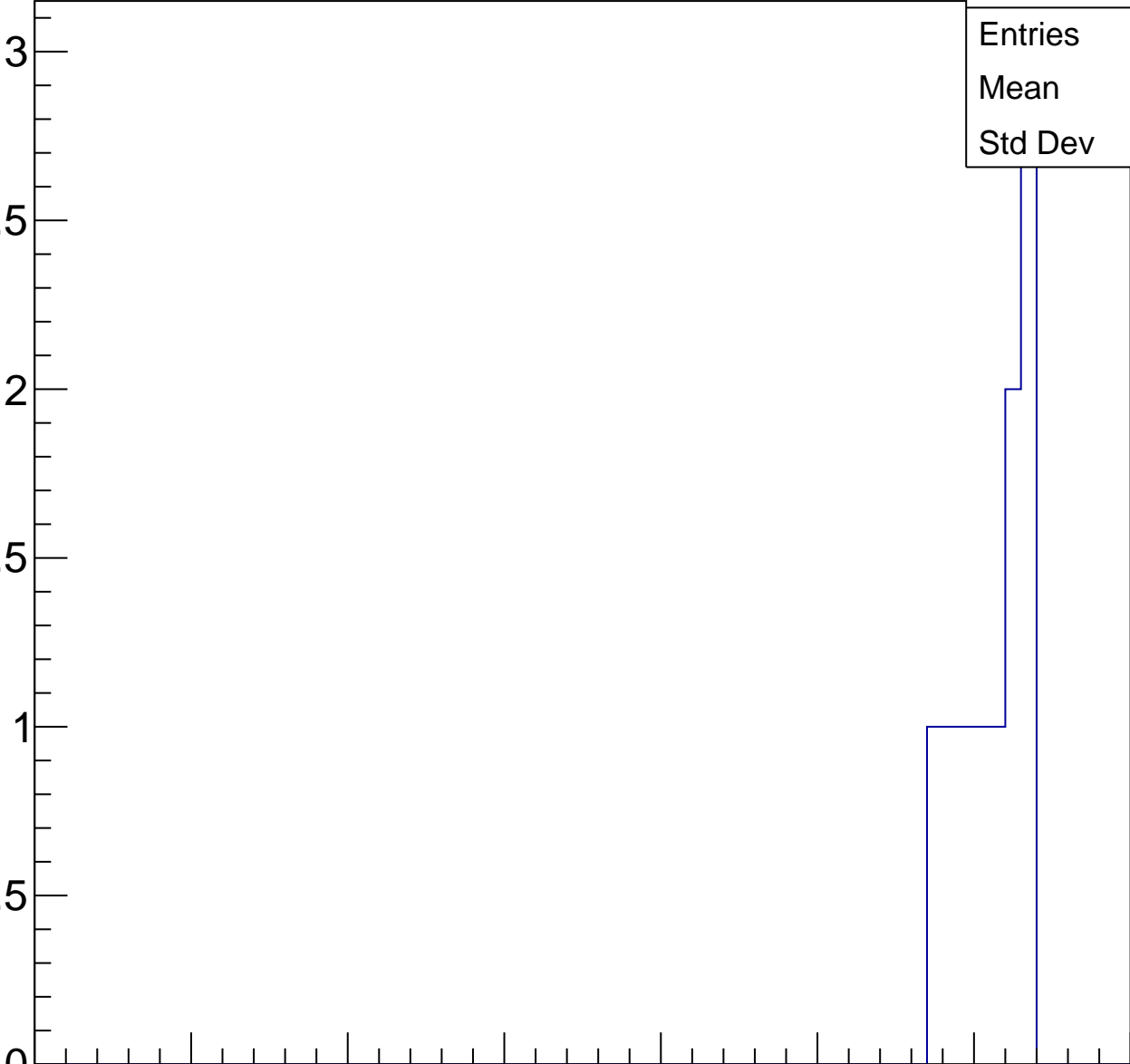
Entry

3
2.5
2
1.5
1
0.5
0

Entries	10
Mean	60.8
Std Dev	2.088

ampl

0 10 20 30 40 50 60 70

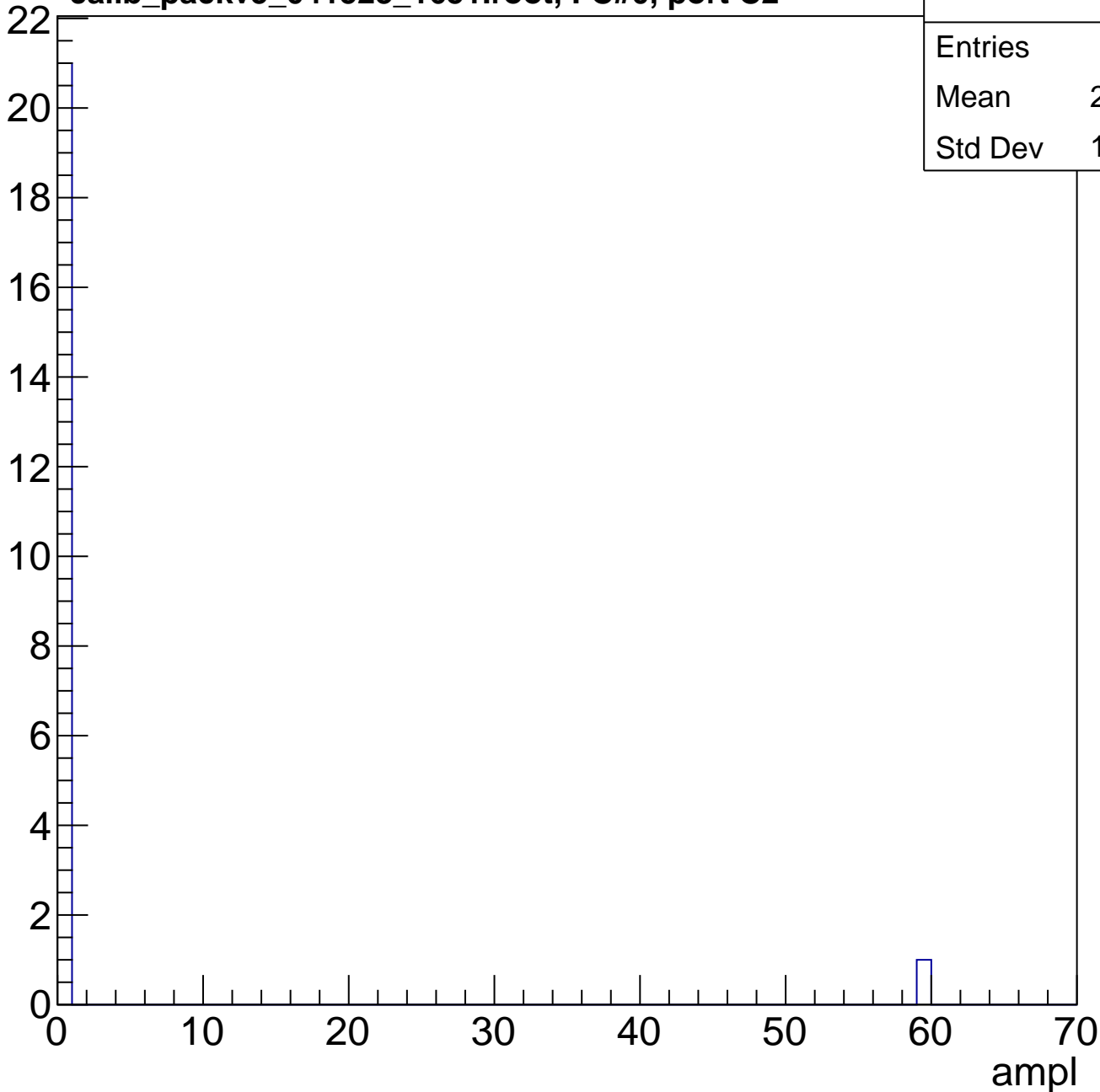


B1L103S, U2-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	2.682
Std Dev	12.29

Entry



B1L103S, U2-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	2.682
Std Dev	12.29

Entry

