

B1L003S, U19-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.33
Std Dev	10.97

Turn on : 28.5075

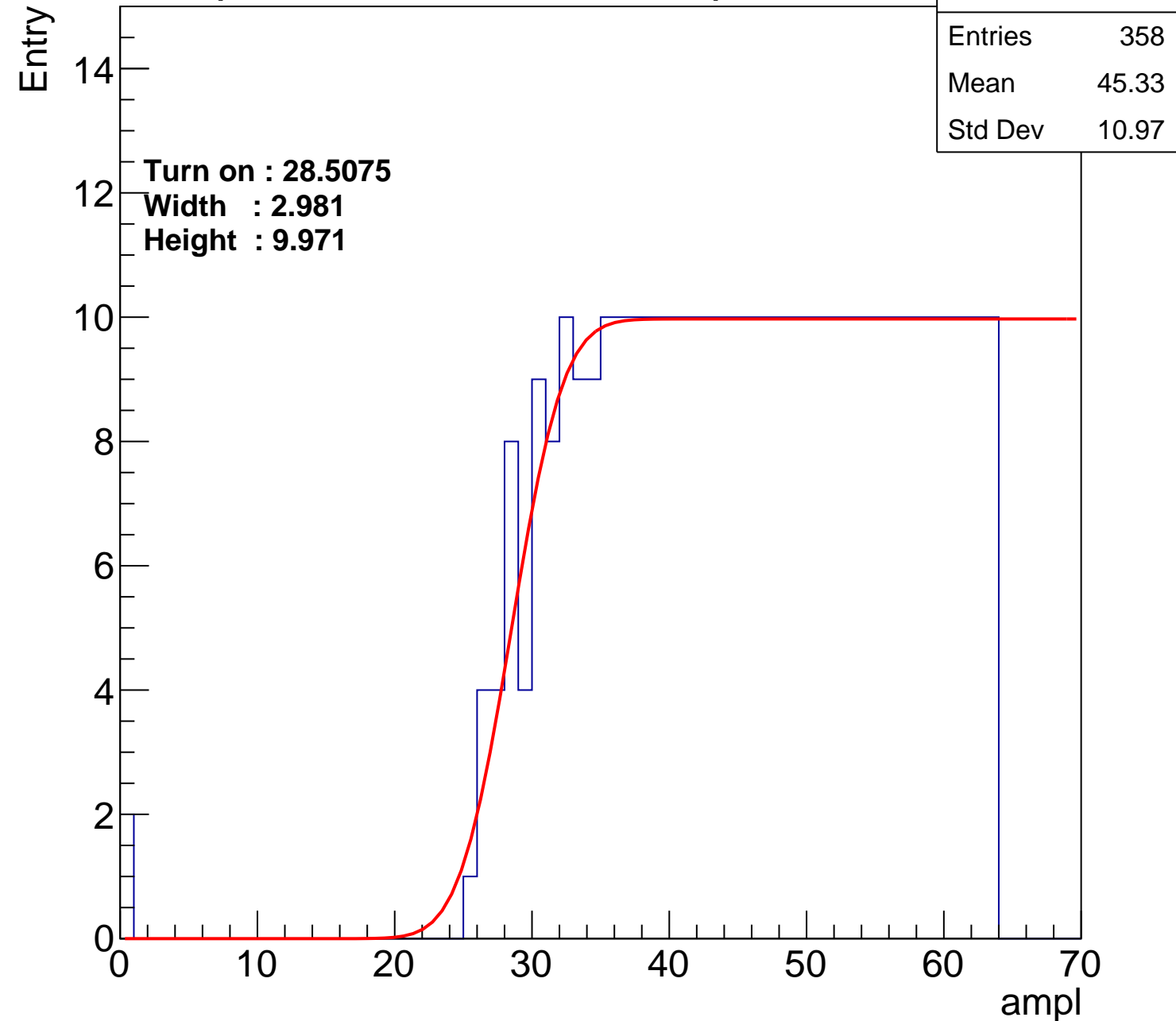
Width : 2.981

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.66
Std Dev	11.5

Turn on : 28.4935

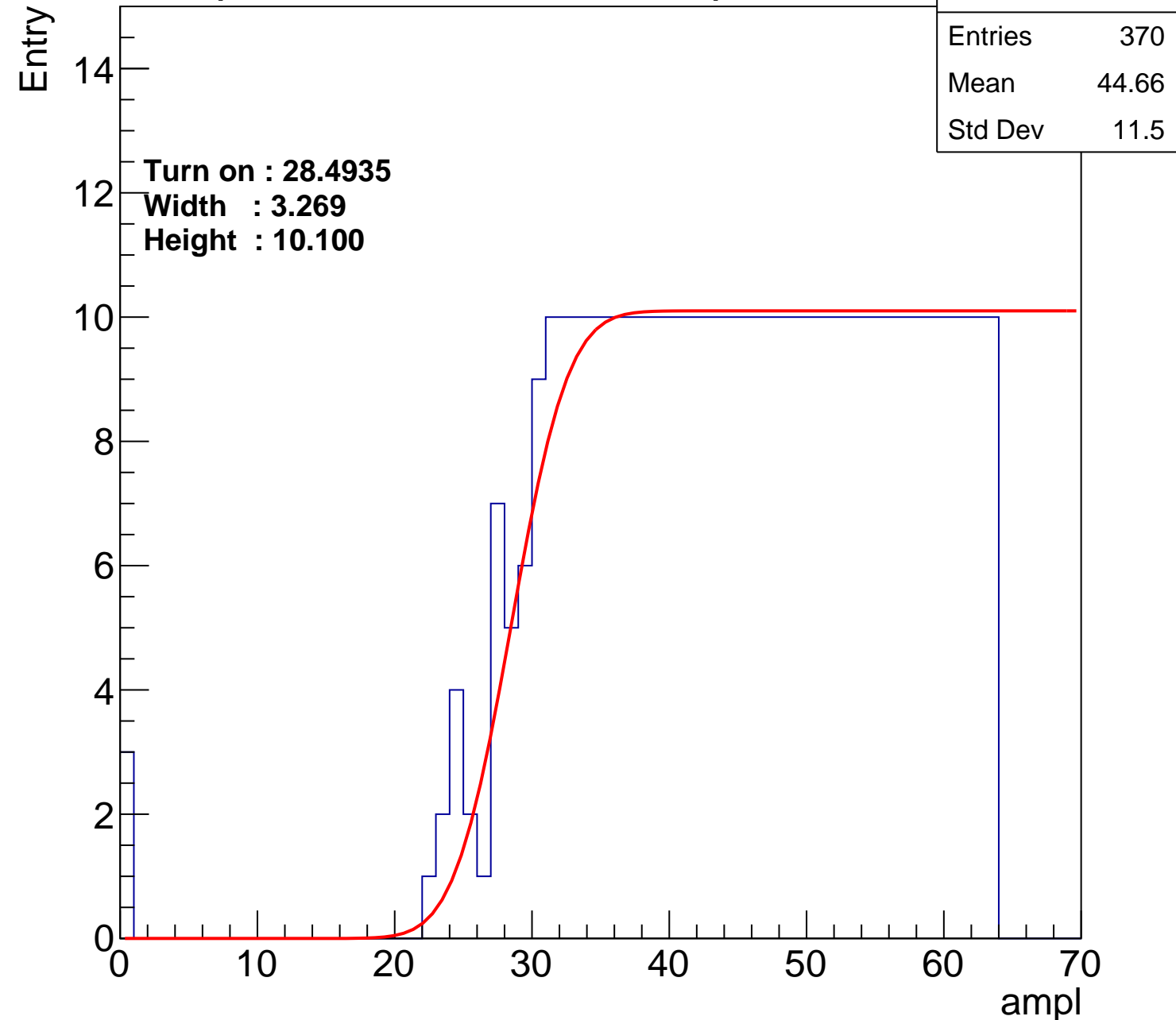
Width : 3.269

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
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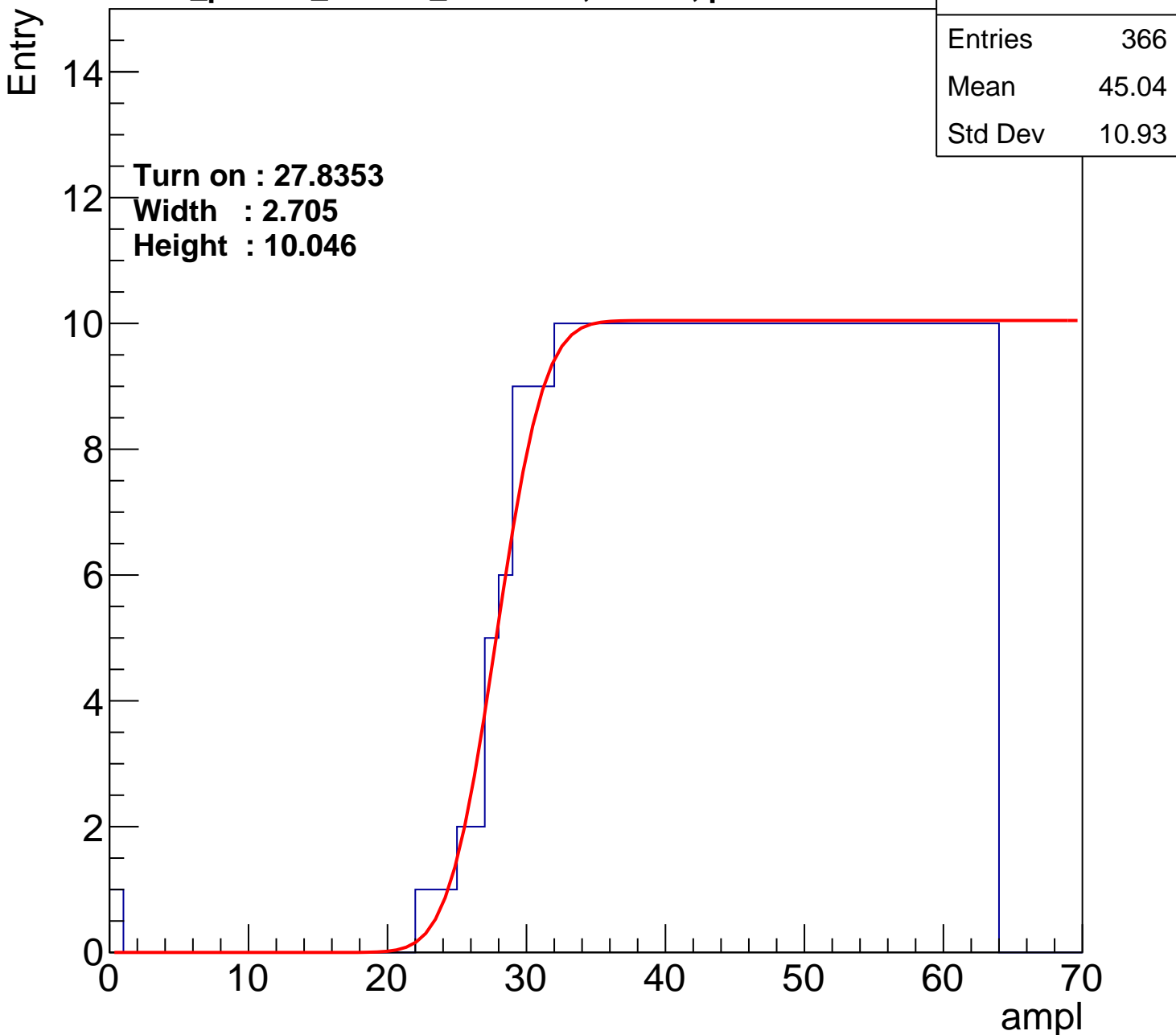
Mean	45.04
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Std Dev	10.93
---------	-------

Turn on : 27.8353

Width : 2.705

Height : 10.046



B1L003S, U19-ch3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 27.9497

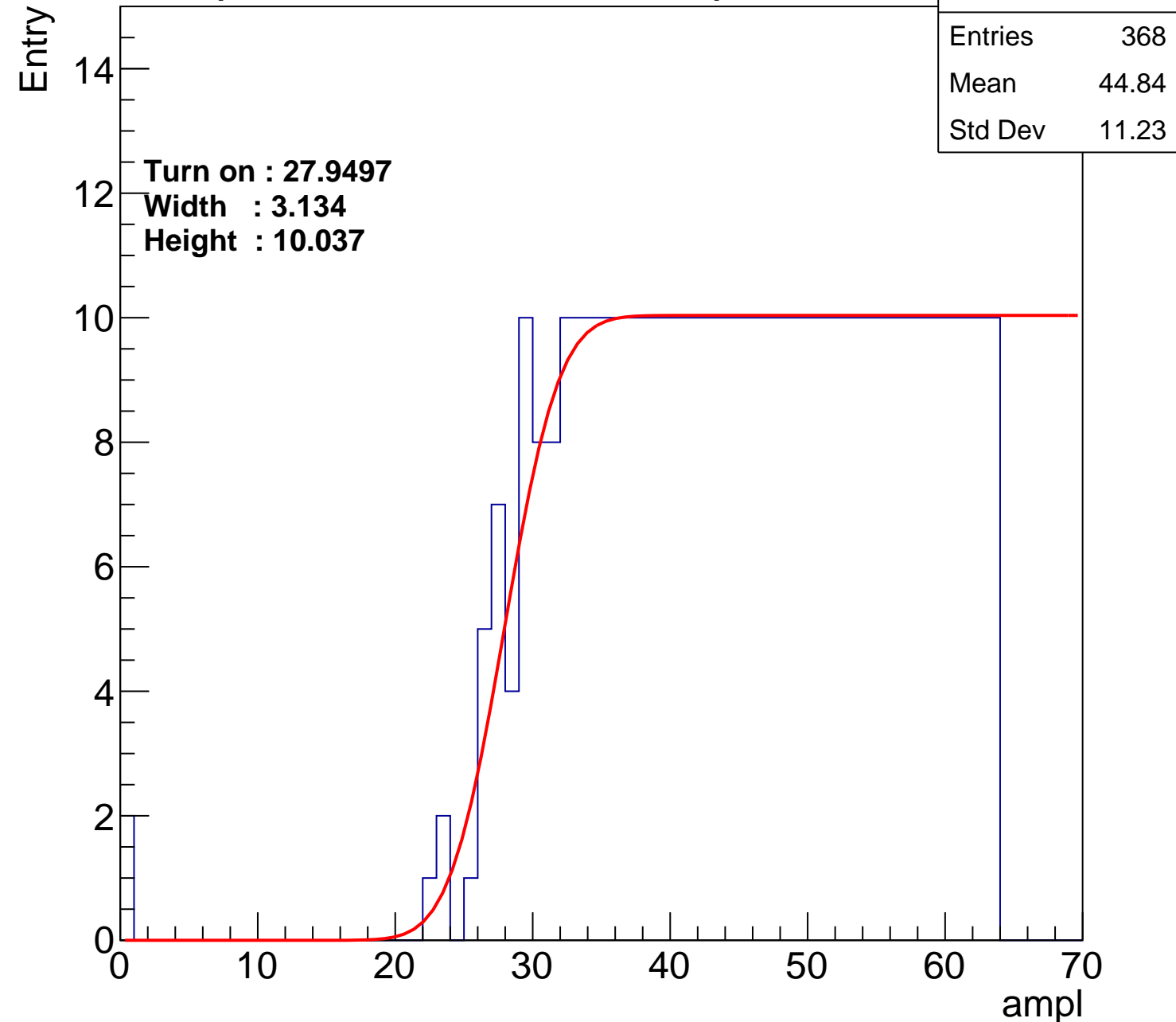
Width : 3.134

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.54
Std Dev	12.12

Turn on : 25.2005

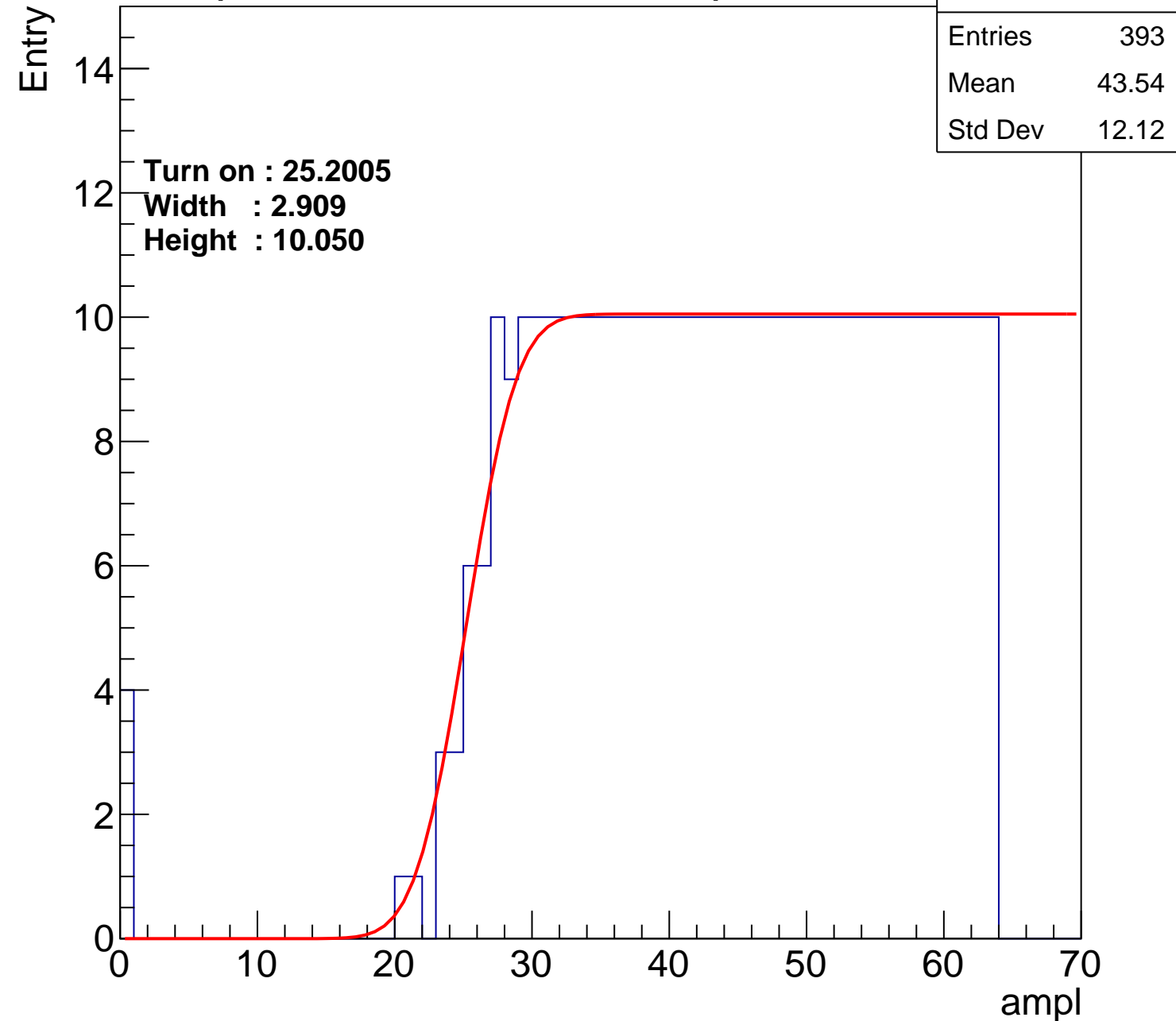
Width : 2.909

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.12
Std Dev	11.7

Turn on : 25.9316

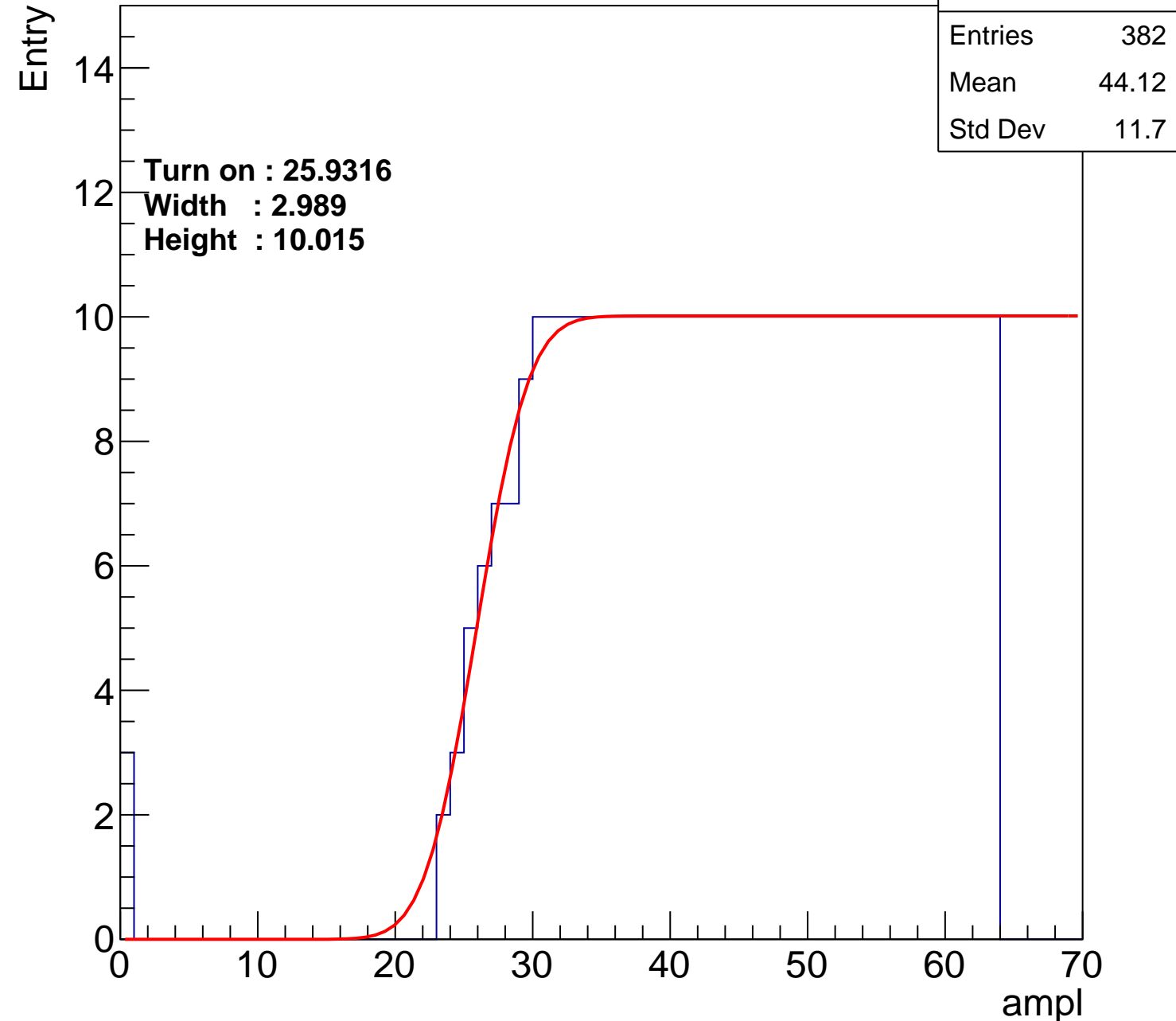
Width : 2.989

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.8800

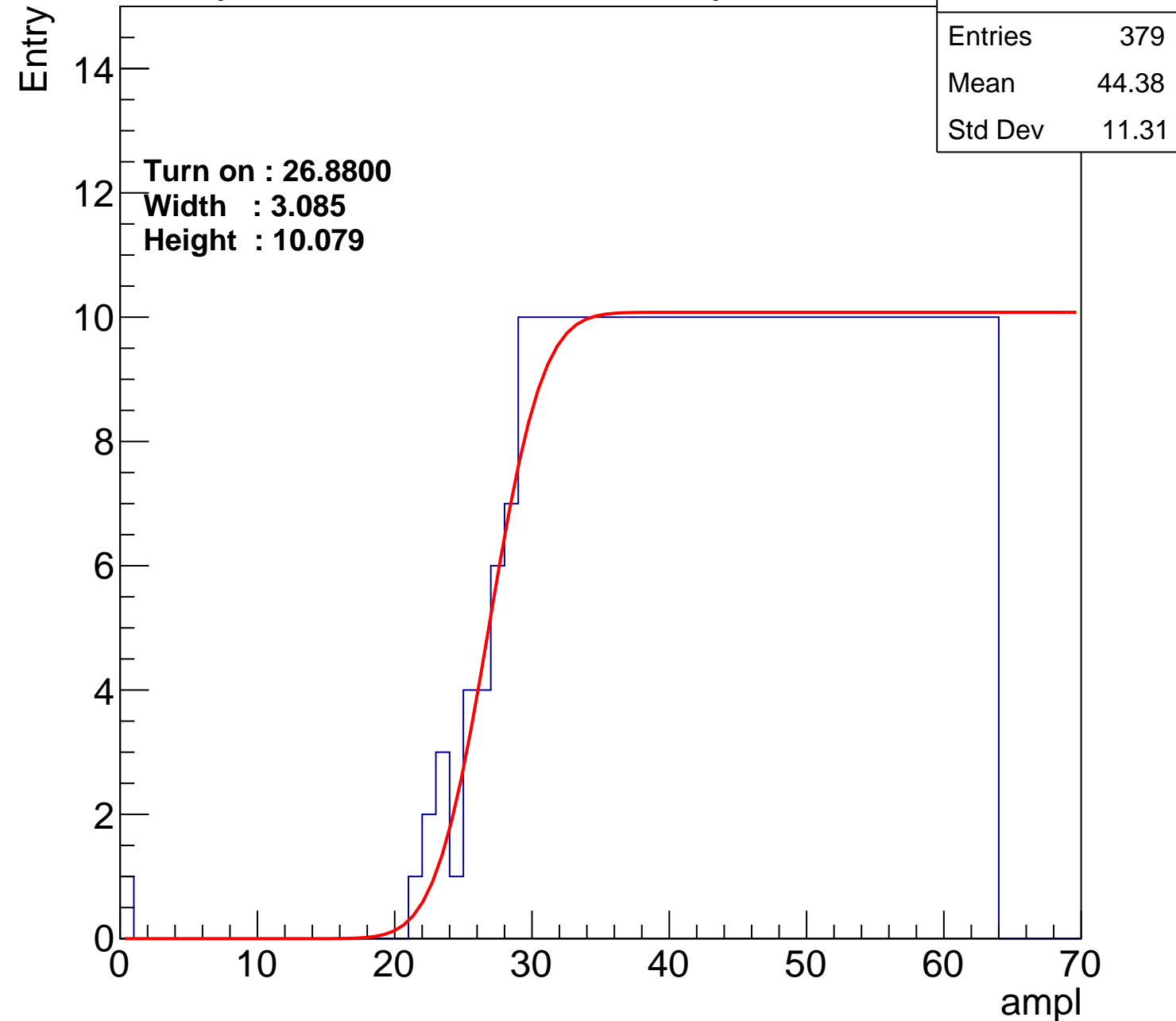
Width : 3.085

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 27.4087

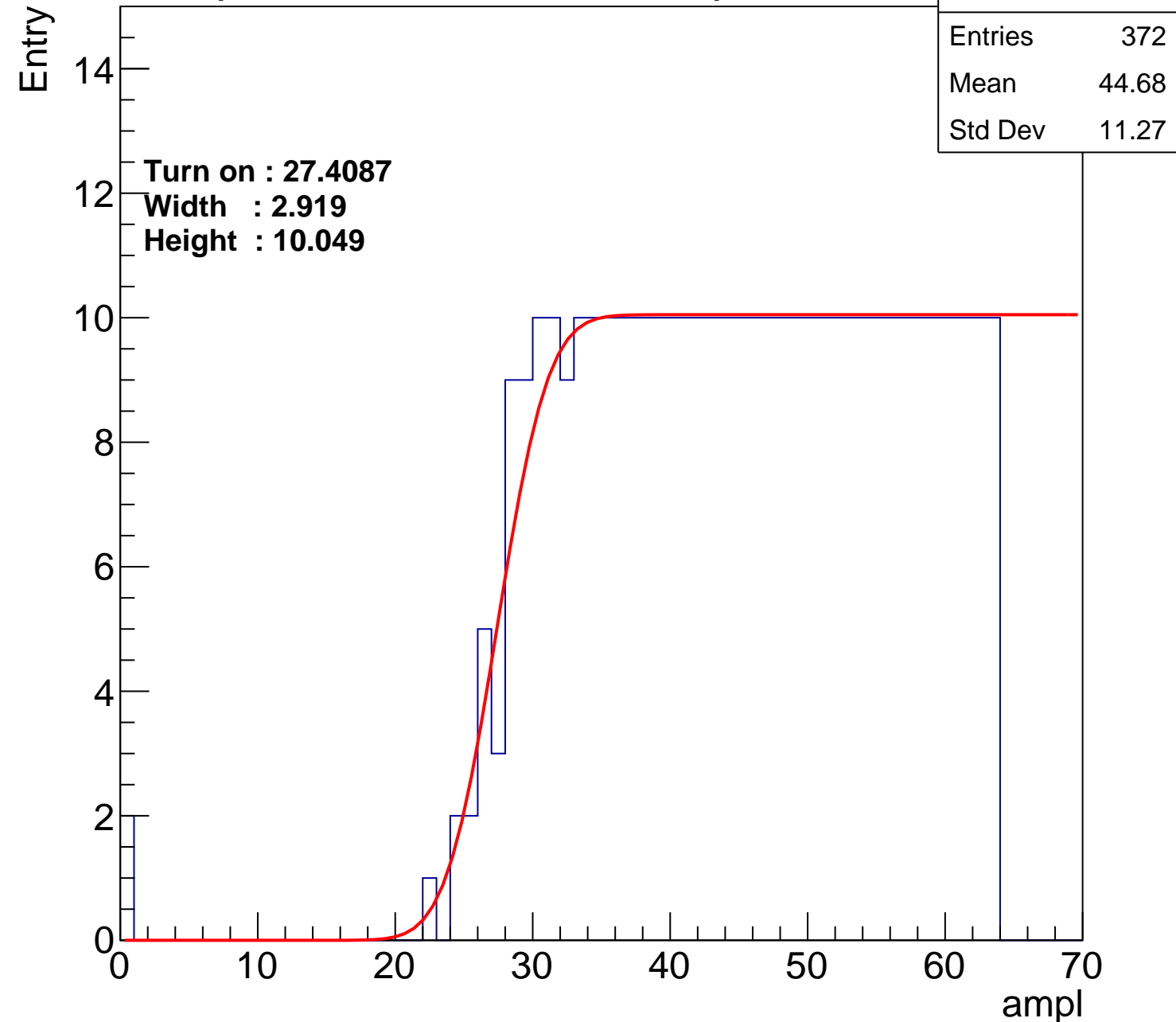
Width : 2.919

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch8

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.89
Std Dev	11.14

Turn on : 27.4129

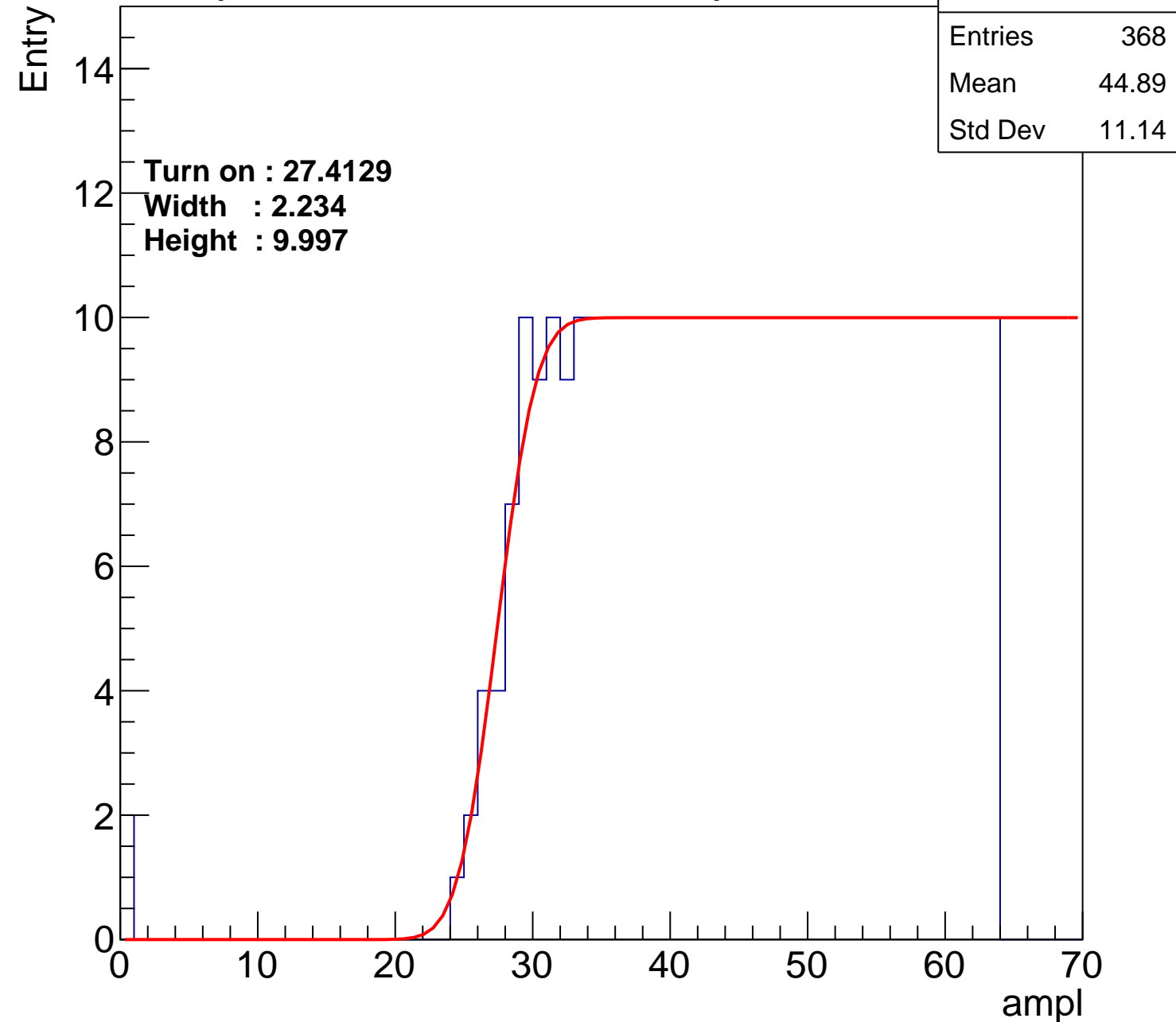
Width : 2.234

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.96
Std Dev	11.33

Turn on : 27.8706

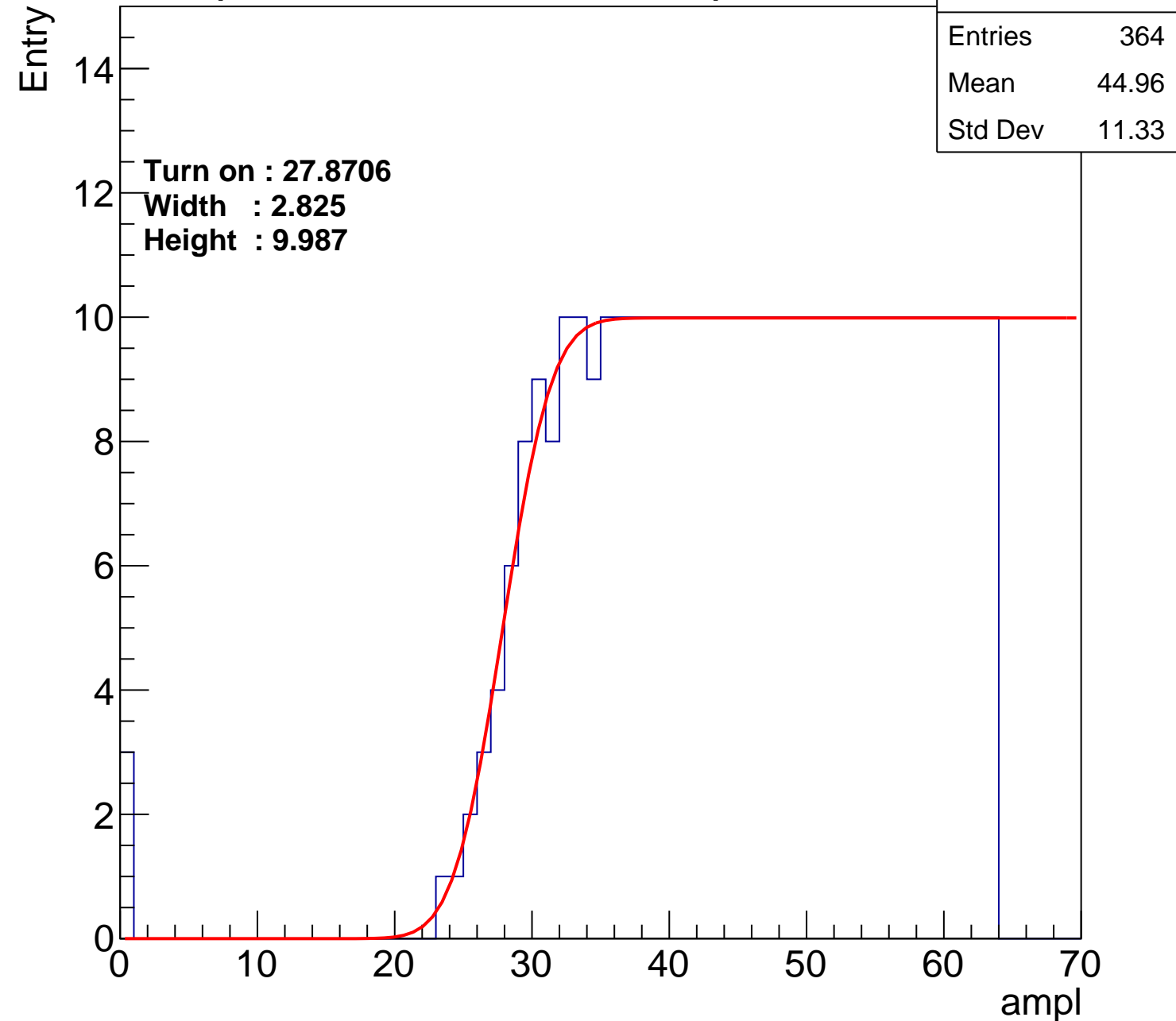
Width : 2.825

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.71
Std Dev	11.22

Turn on : 26.9026

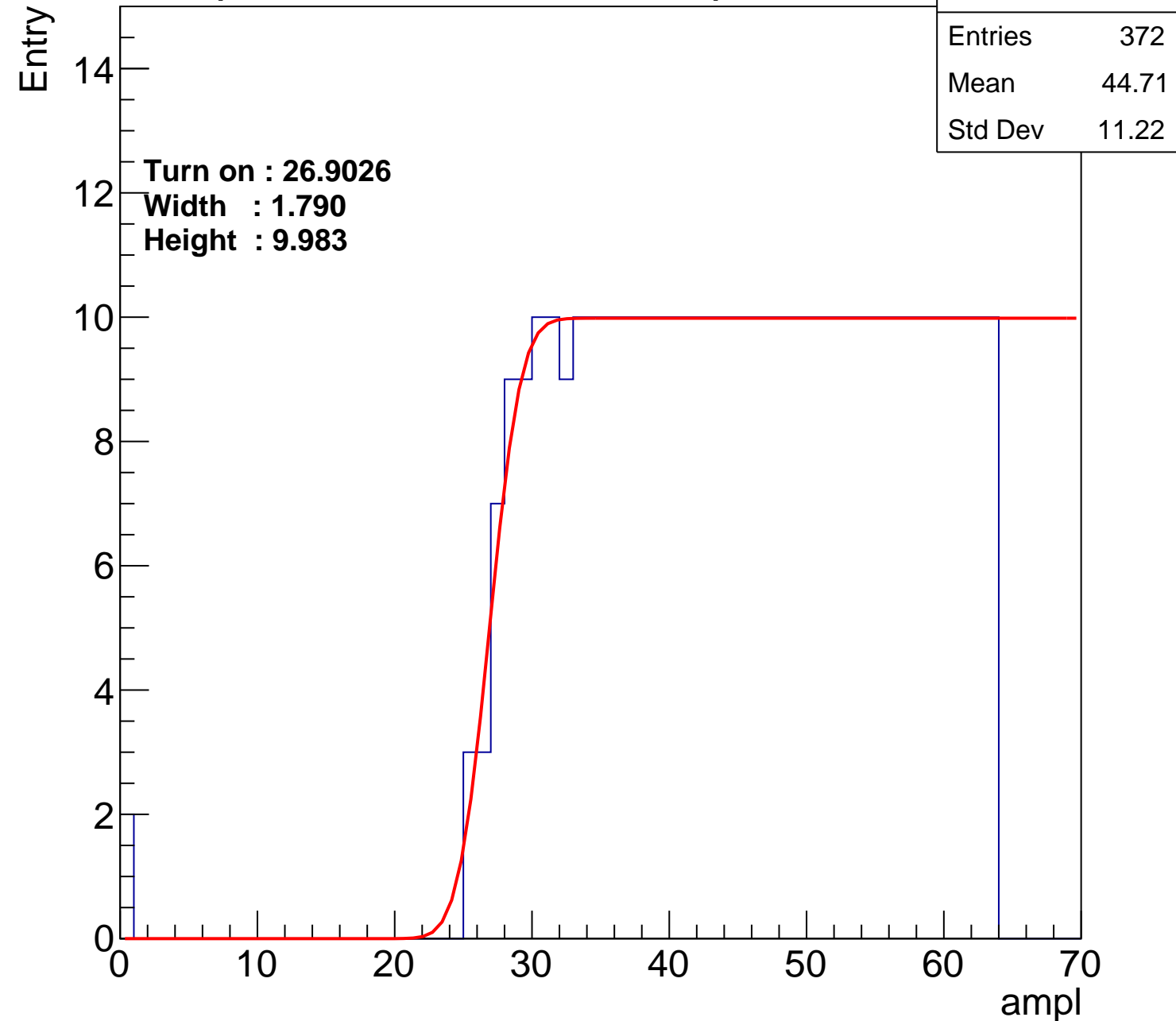
Width : 1.790

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch11

calib_packv5_042523_0143.root, FC#13, port D2

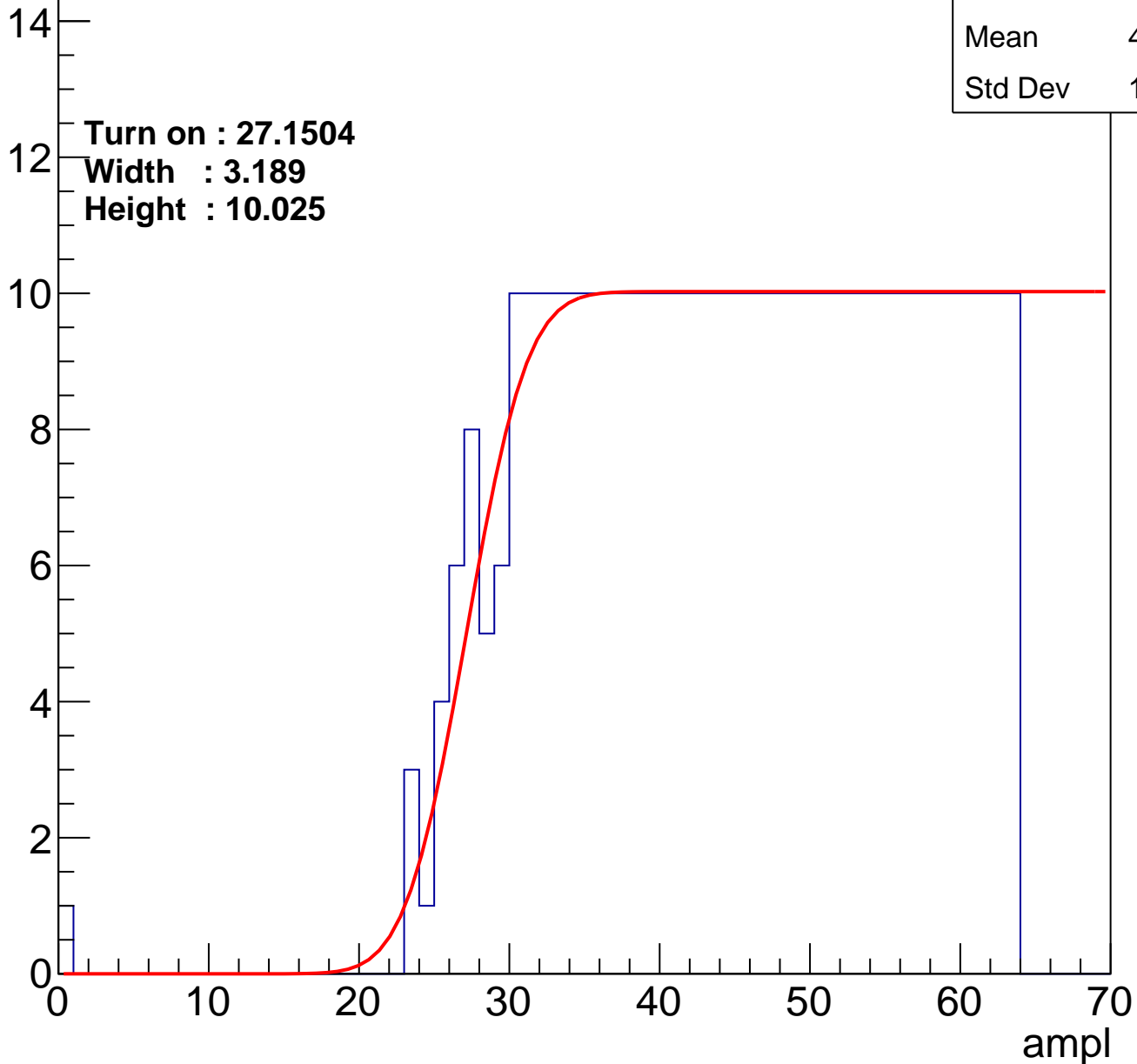
Entries	374
Mean	44.62
Std Dev	11.17

Turn on : 27.1504

Width : 3.189

Height : 10.025

Entry



B1L003S, U19-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.06
Std Dev	11.25

Turn on : 28.3227

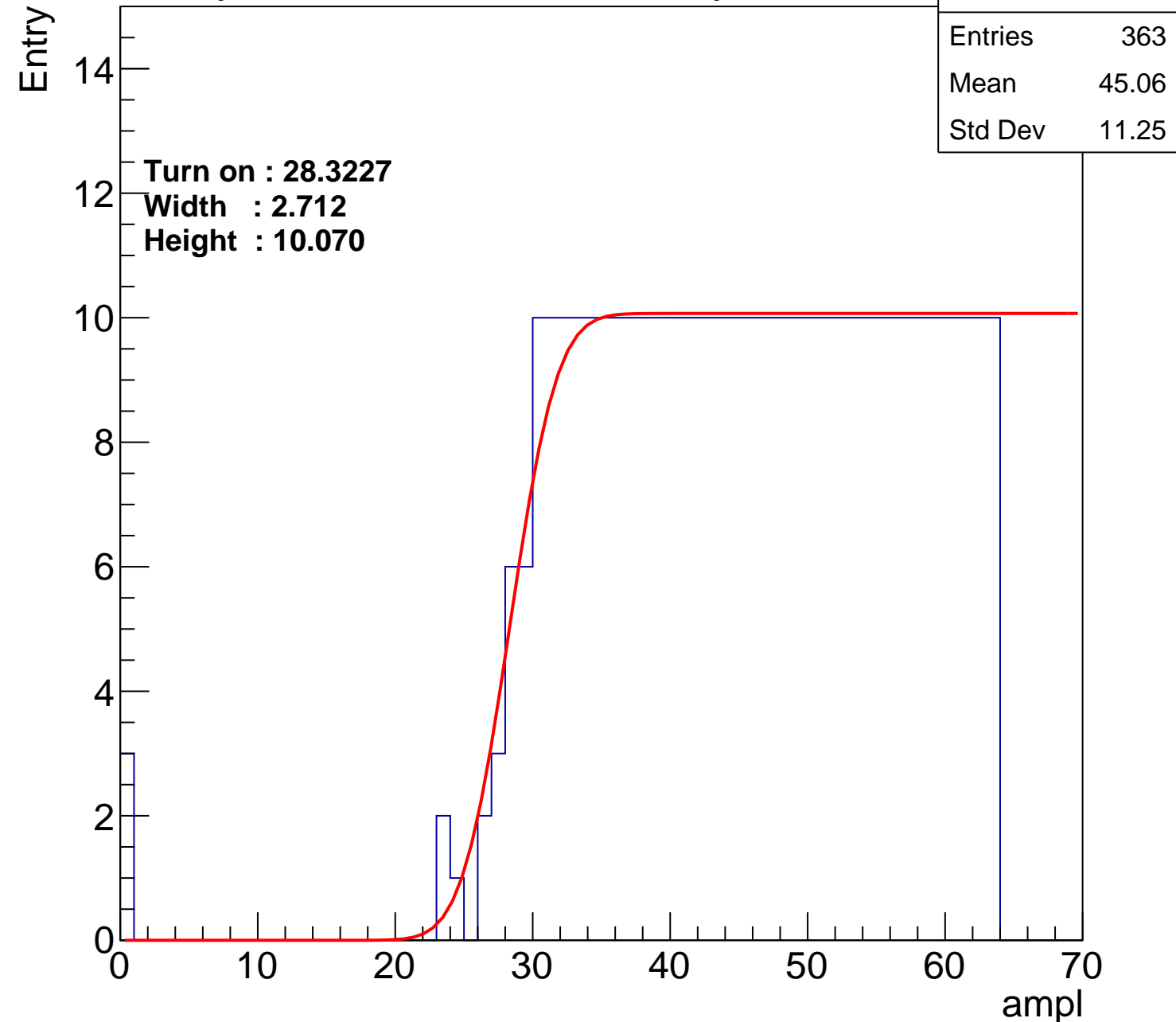
Width : 2.712

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch13

calib_packv5_042523_0143.root, FC#13, port D2

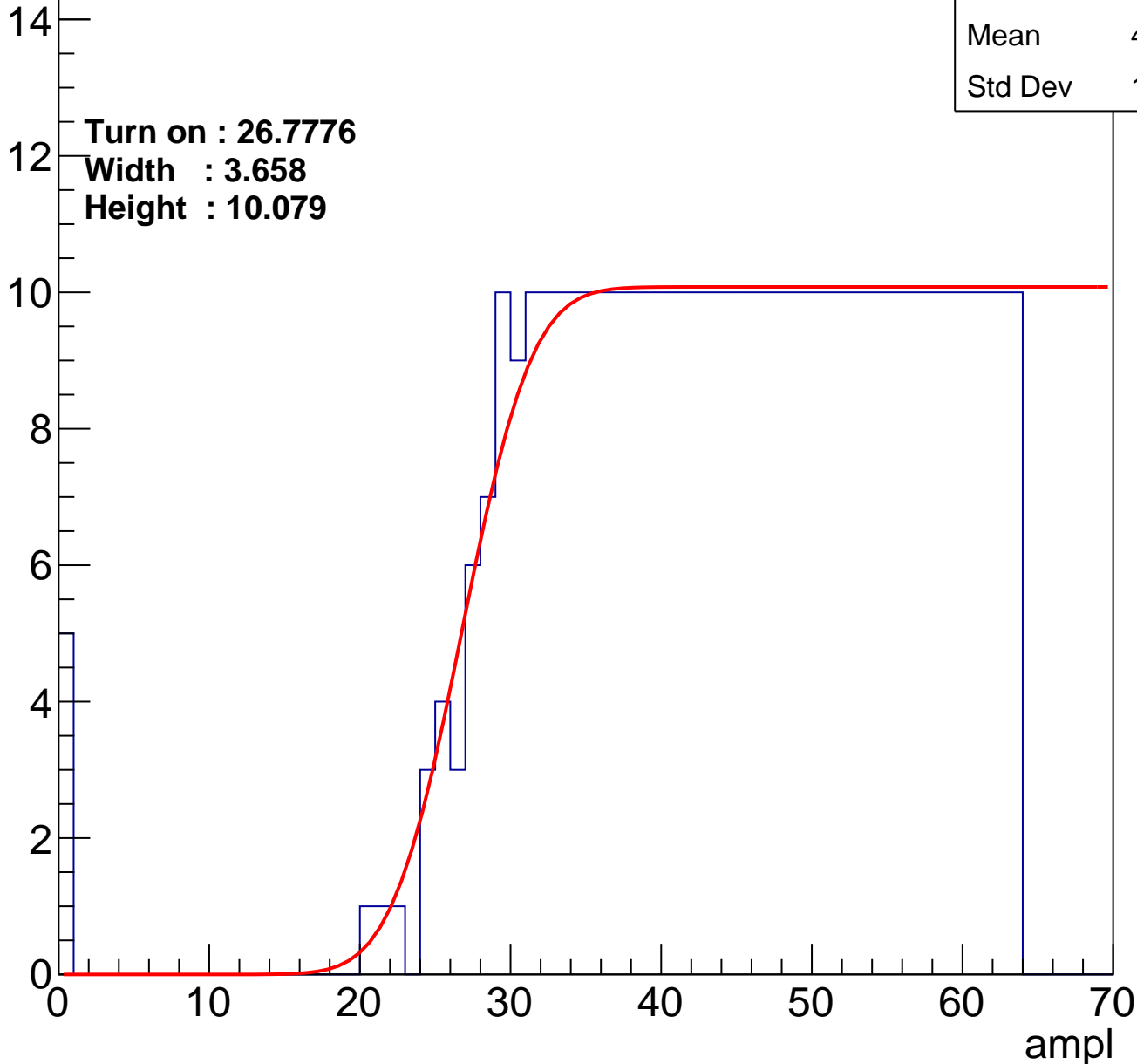
Entries	380
Mean	44.06
Std Dev	12.06

Turn on : 26.7776

Width : 3.658

Height : 10.079

Entry



B1L003S, U19-ch14

calib_packv5_042523_0143.root, FC#13, port D2

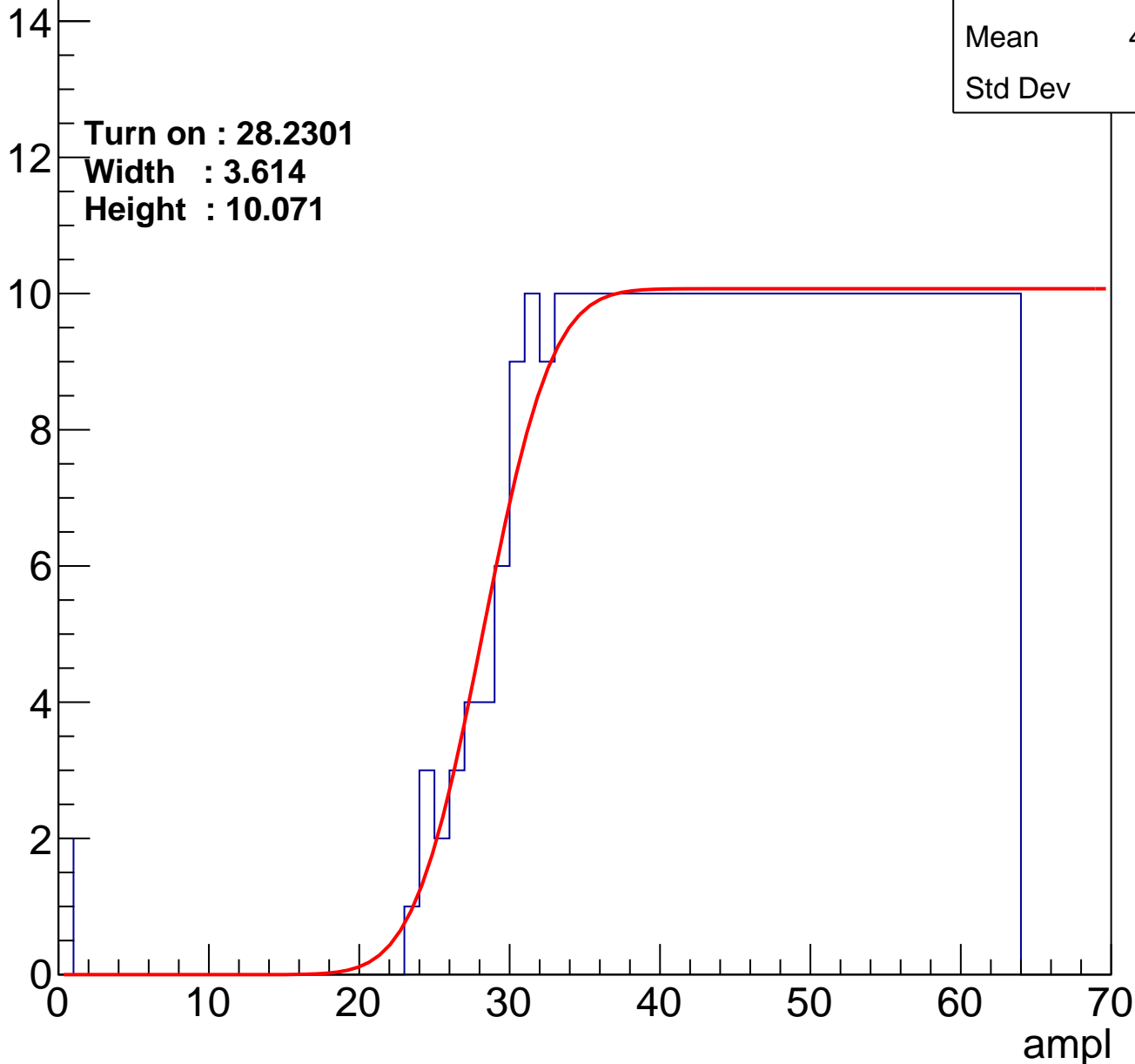
Entries	363
Mean	45.08
Std Dev	11.11

Turn on : 28.2301

Width : 3.614

Height : 10.071

Entry



B1L003S, U19-ch15

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.2845

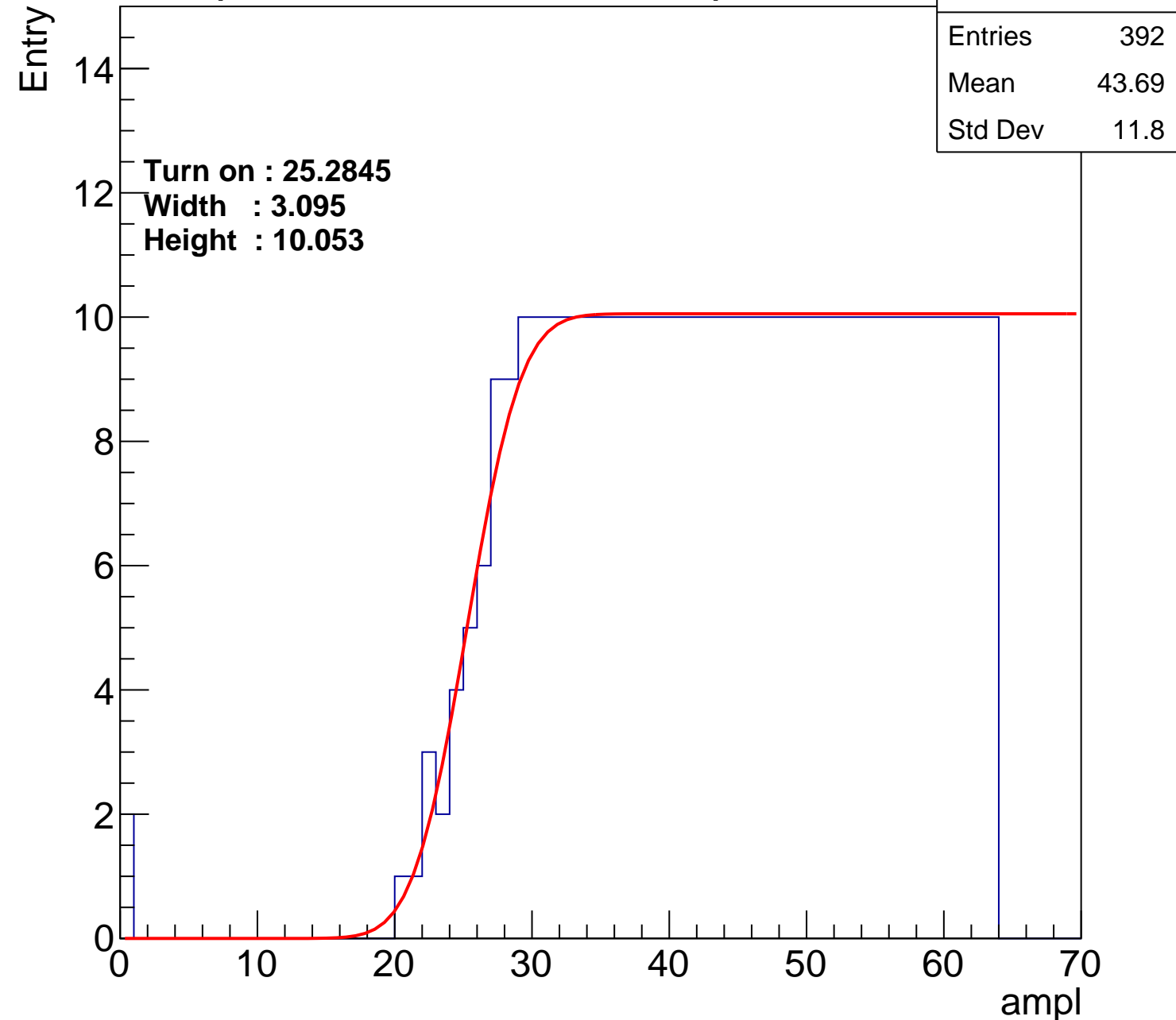
Width : 3.095

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.74
Std Dev	11.27

Turn on : 27.7074

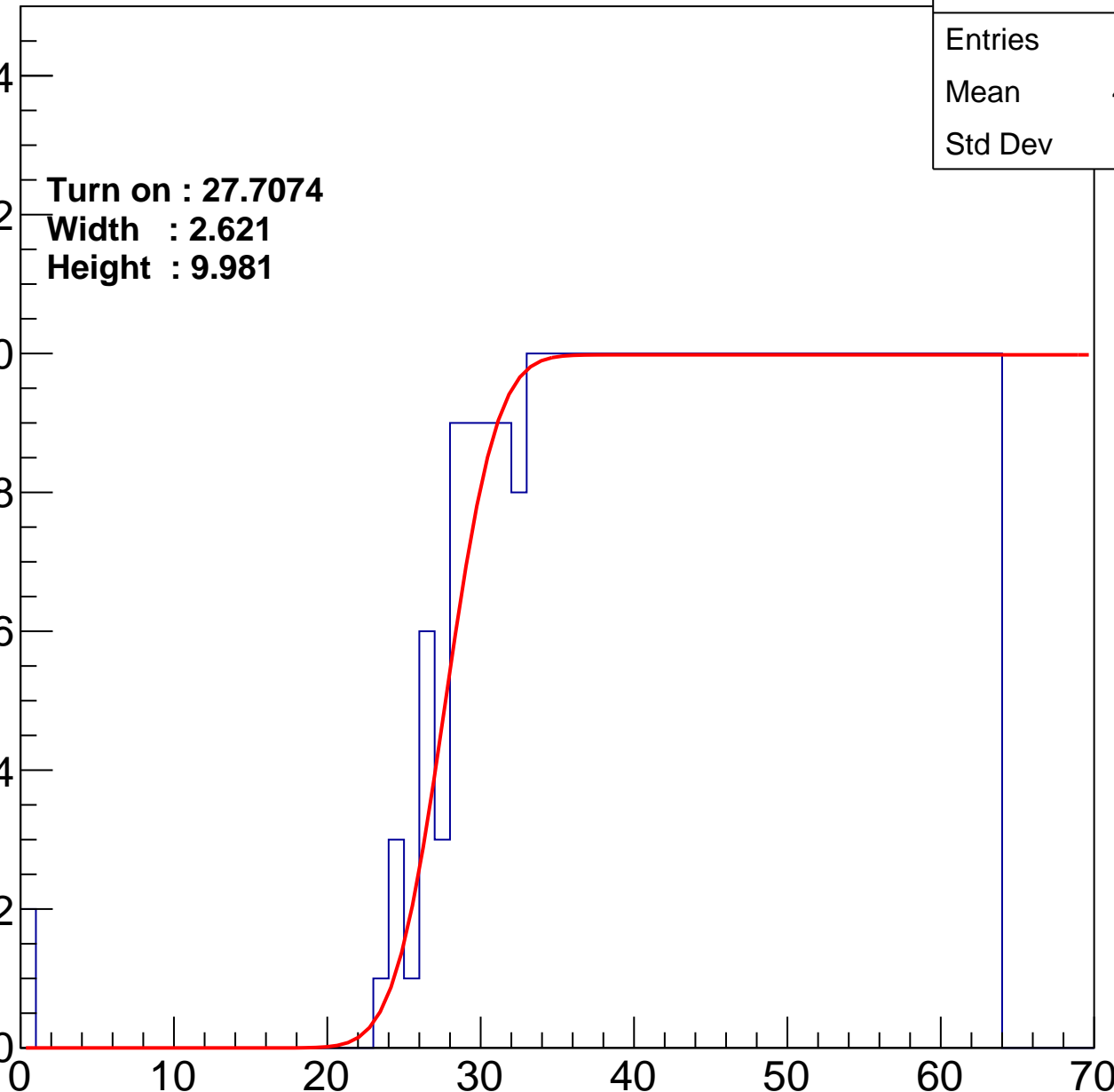
Width : 2.621

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch17

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.5551

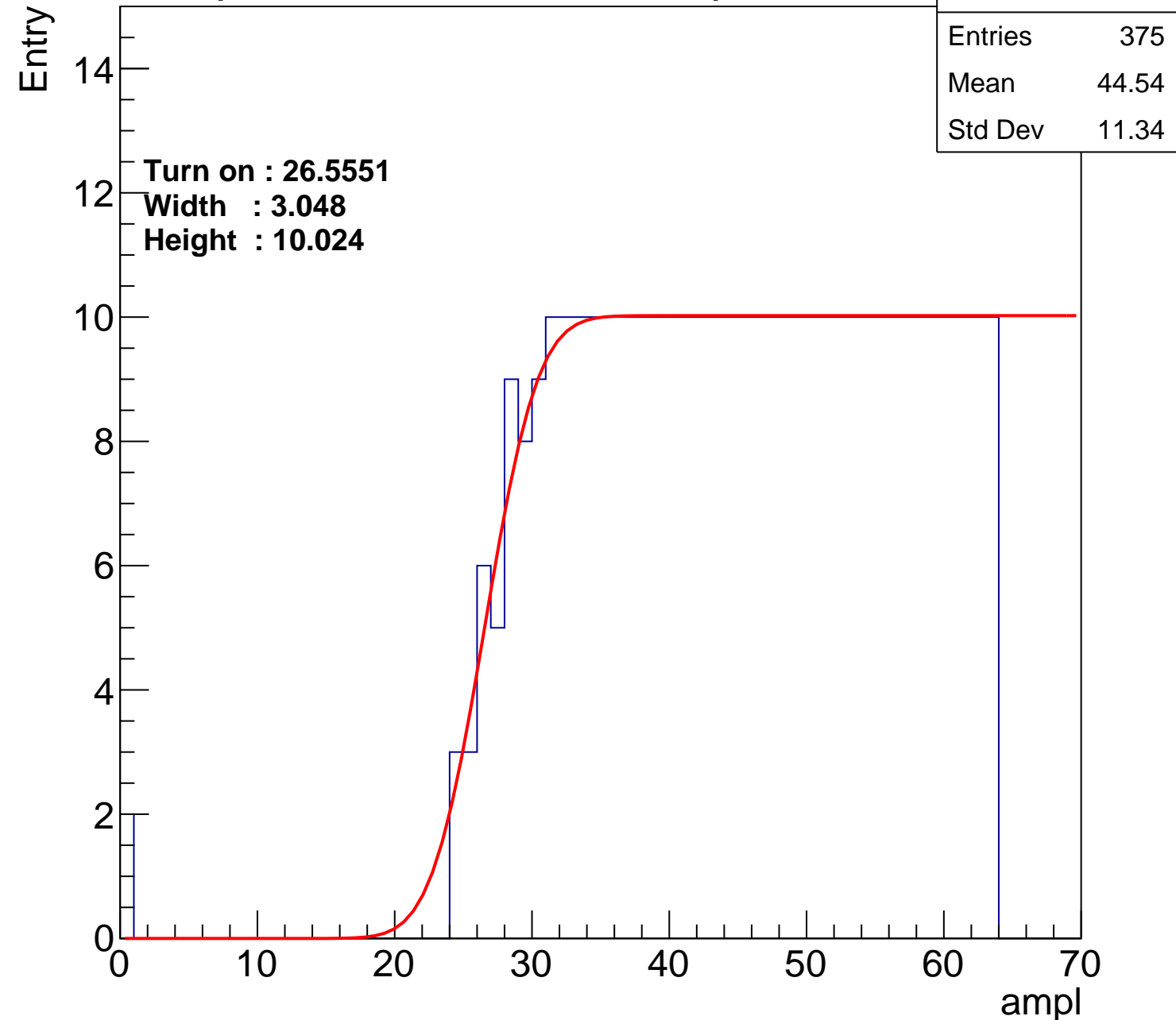
Width : 3.048

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch18

calib_packv5_042523_0143.root, FC#13, port D2

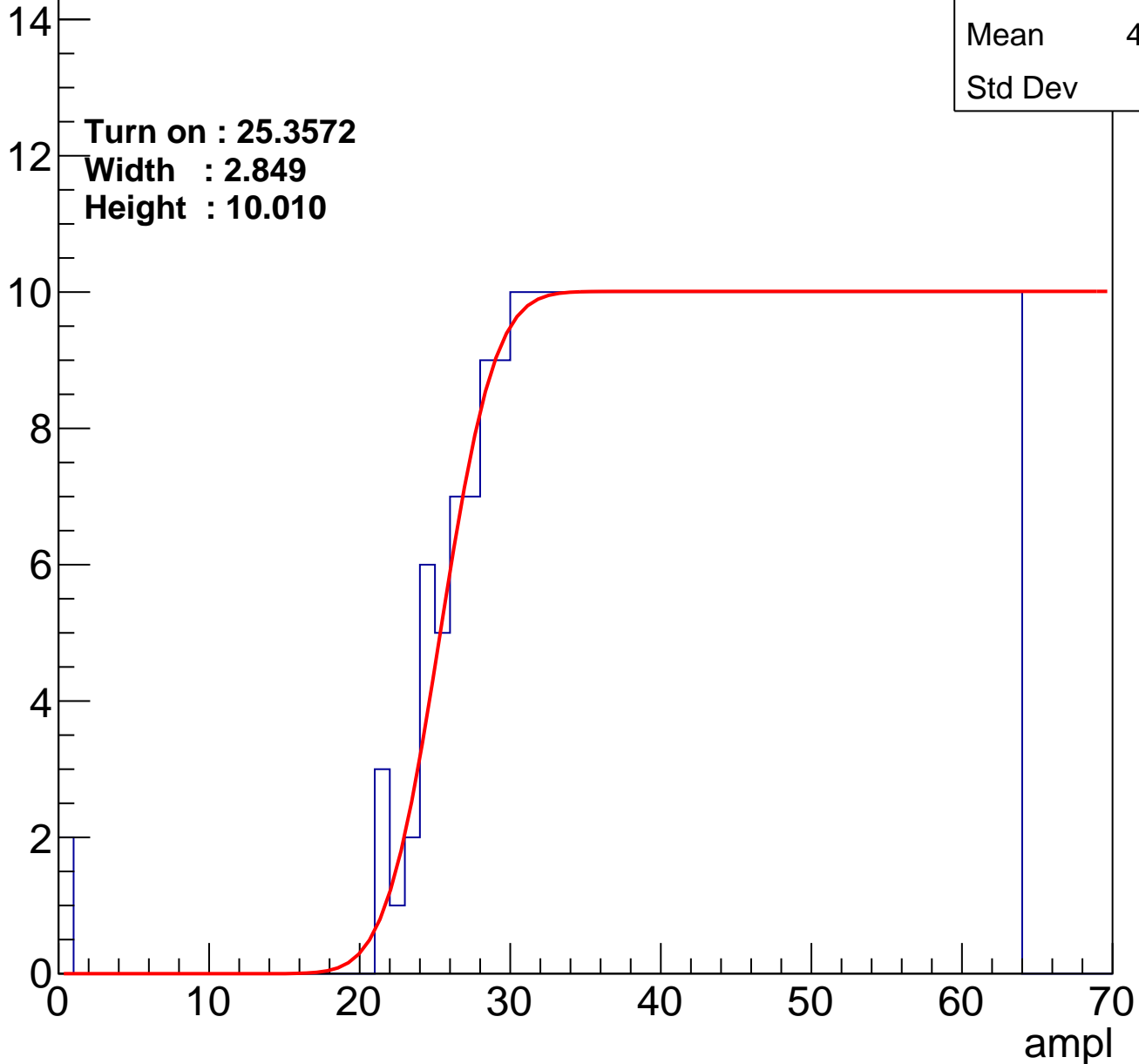
Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 25.3572

Width : 2.849

Height : 10.010

Entry



B1L003S, U19-ch19

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.32
Std Dev	10.78

Turn on : 27.8444

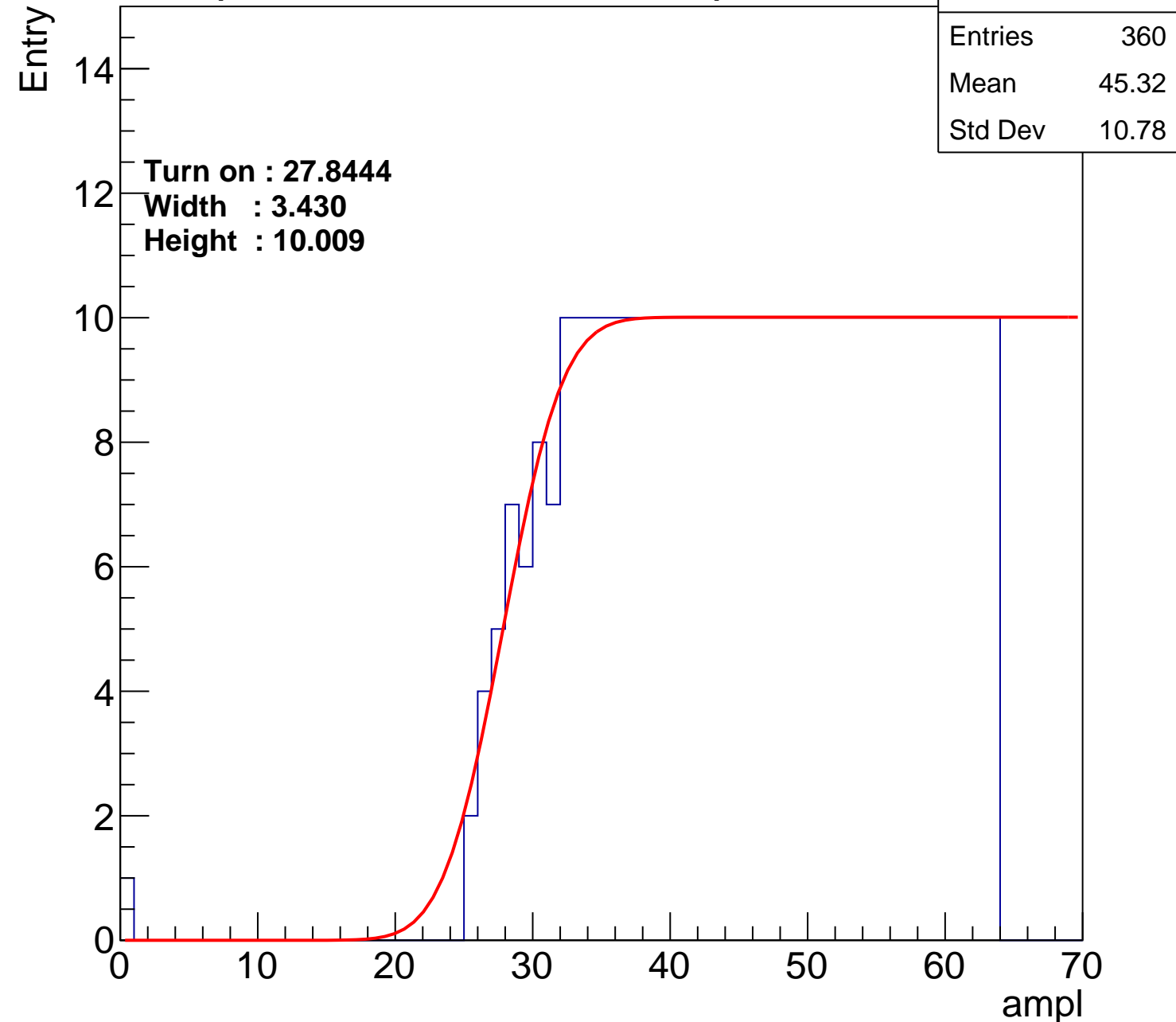
Width : 3.430

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.74
Std Dev	11.65

Turn on : 25.7999

Width : 2.901

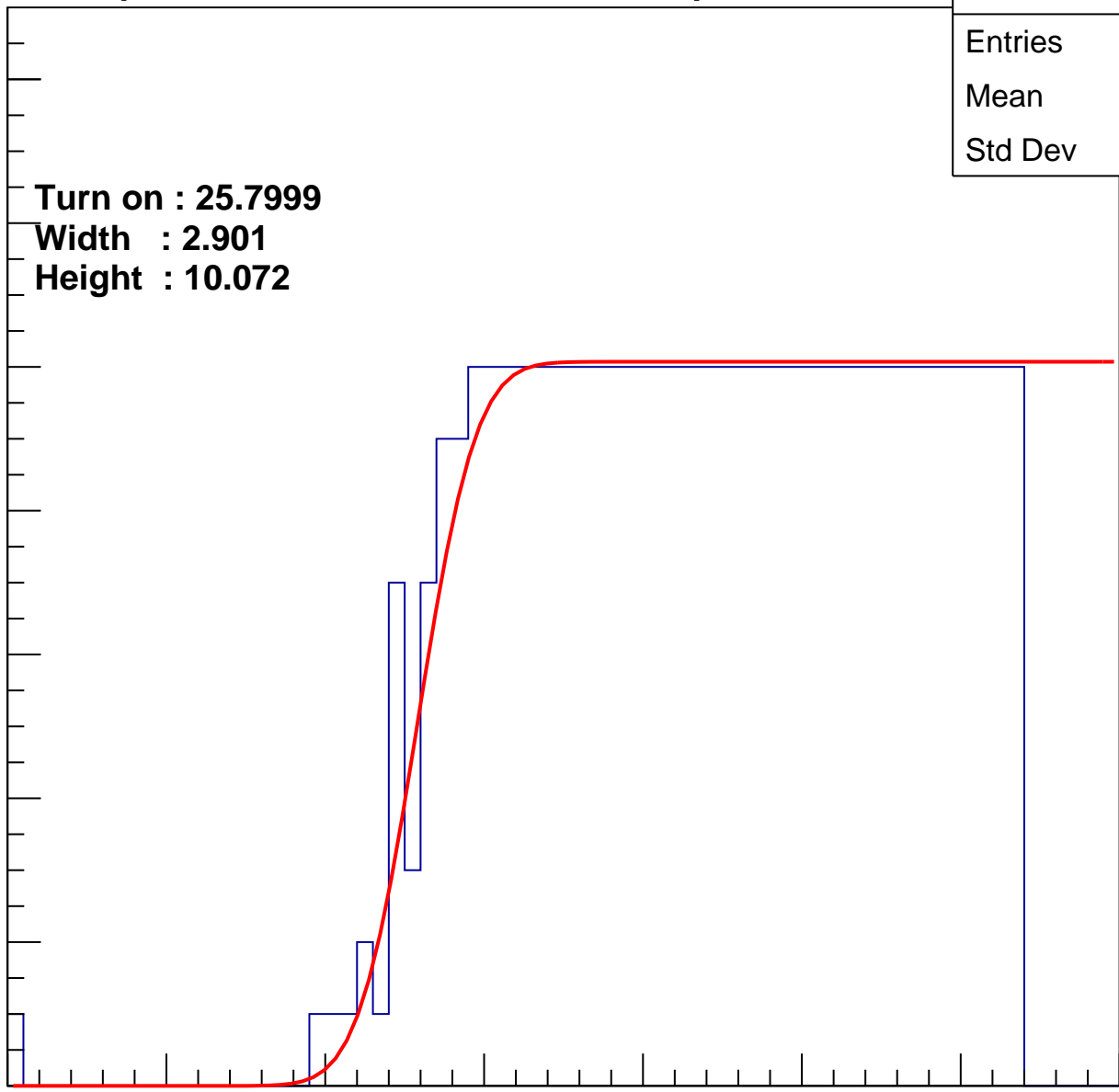
Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U19-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.24
Std Dev	11.36

Turn on : 26.0955

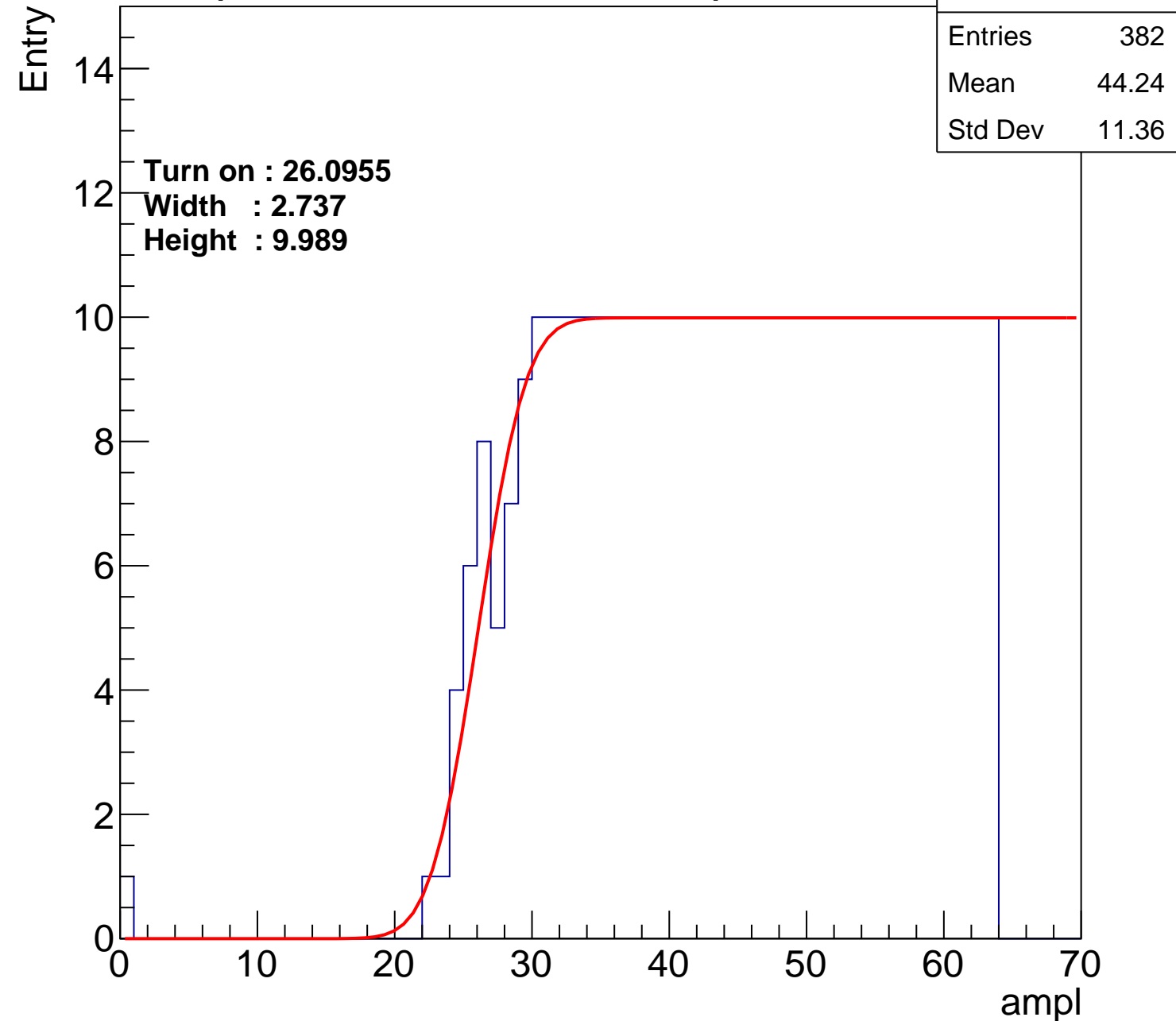
Width : 2.737

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch22

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.98
Std Dev	11.3

Turn on : 27.9920

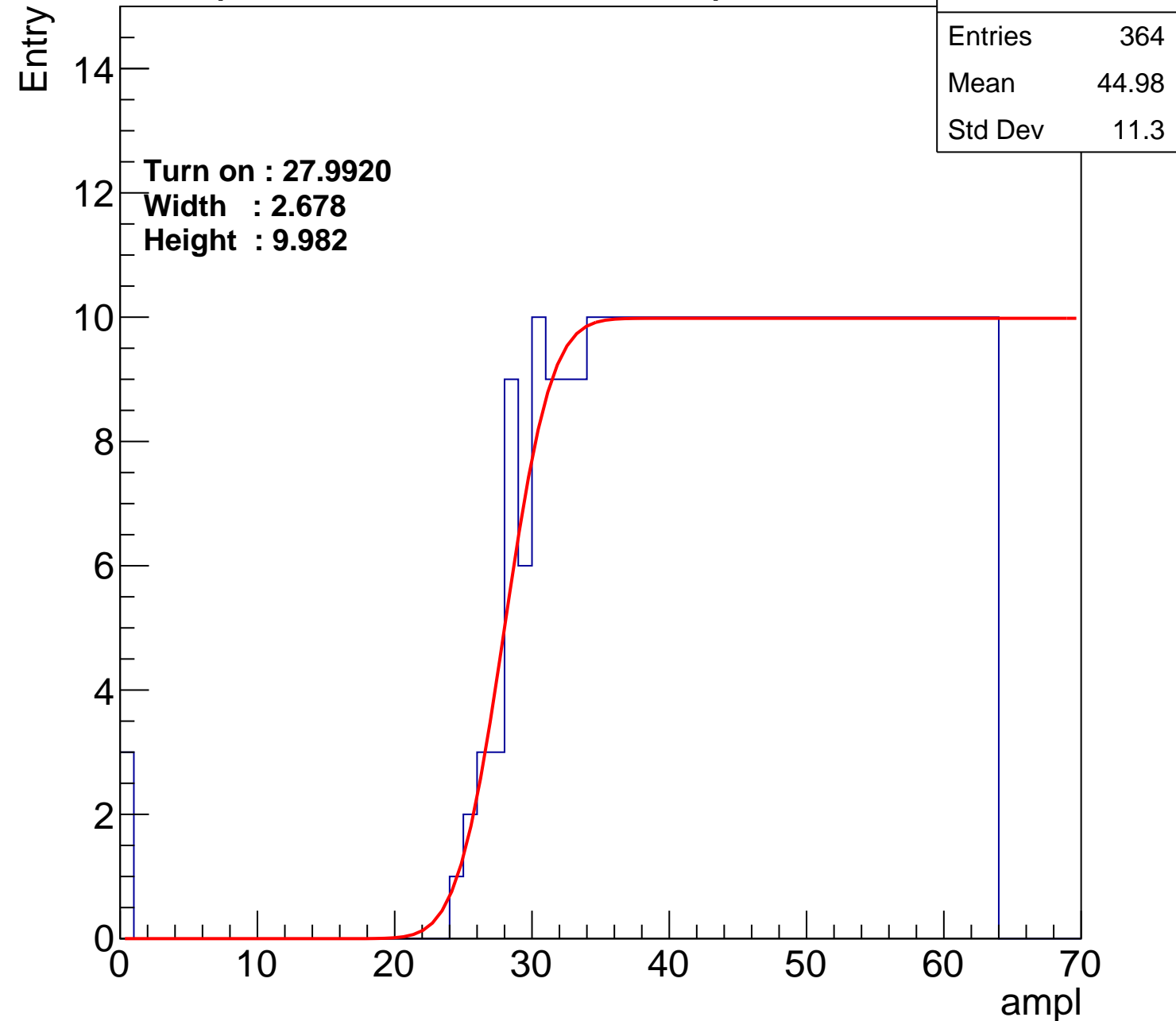
Width : 2.678

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch23

calib_packv5_042523_0143.root, FC#13, port D2

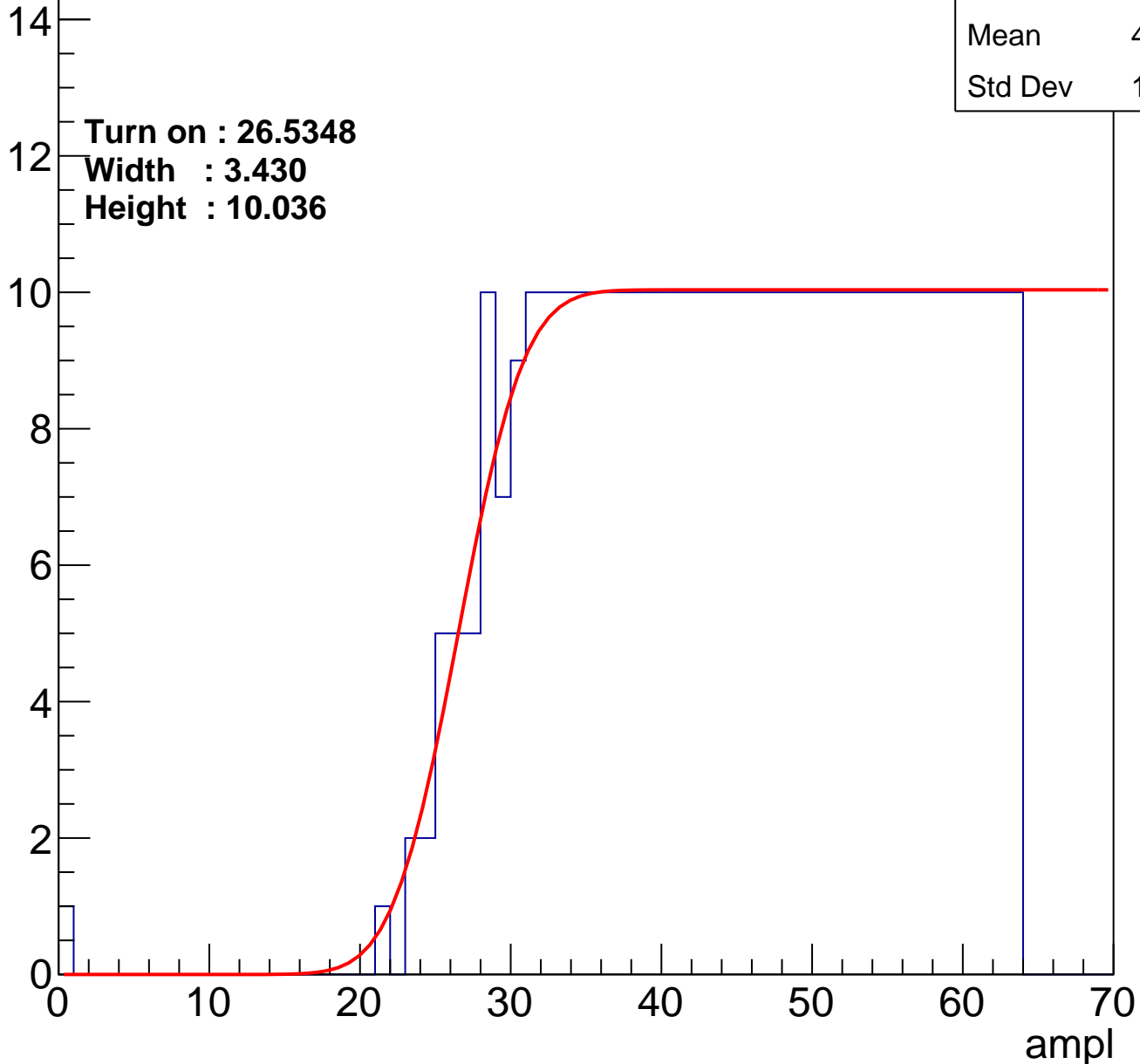
Entries	377
Mean	44.48
Std Dev	11.25

Turn on : 26.5348

Width : 3.430

Height : 10.036

Entry



B1L003S, U19-ch24

calib_packv5_042523_0143.root, FC#13, port D2

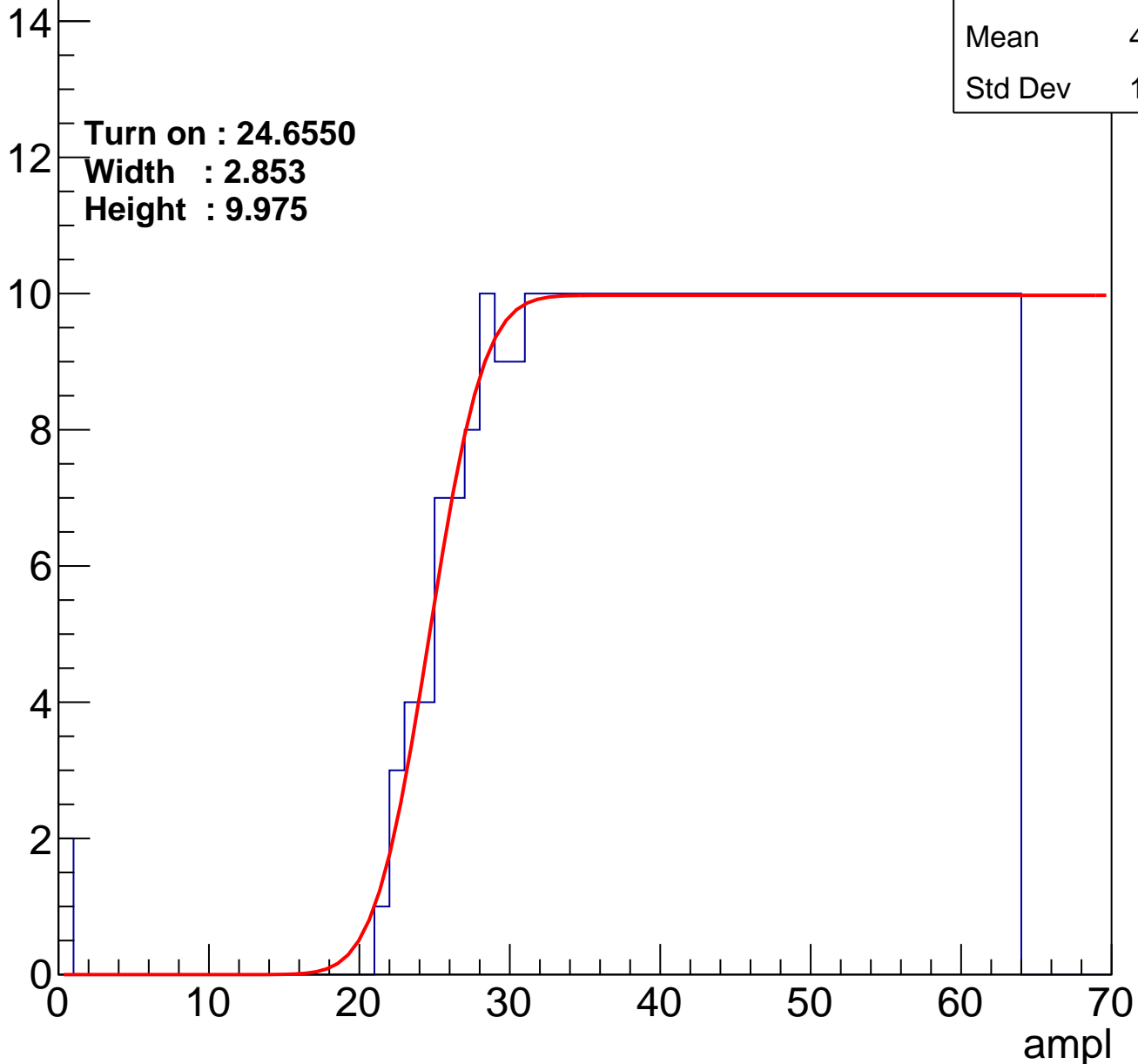
Entries	394
Mean	43.58
Std Dev	11.87

Turn on : 24.6550

Width : 2.853

Height : 9.975

Entry



B1L003S, U19-ch25

calib_packv5_042523_0143.root, FC#13, port D2

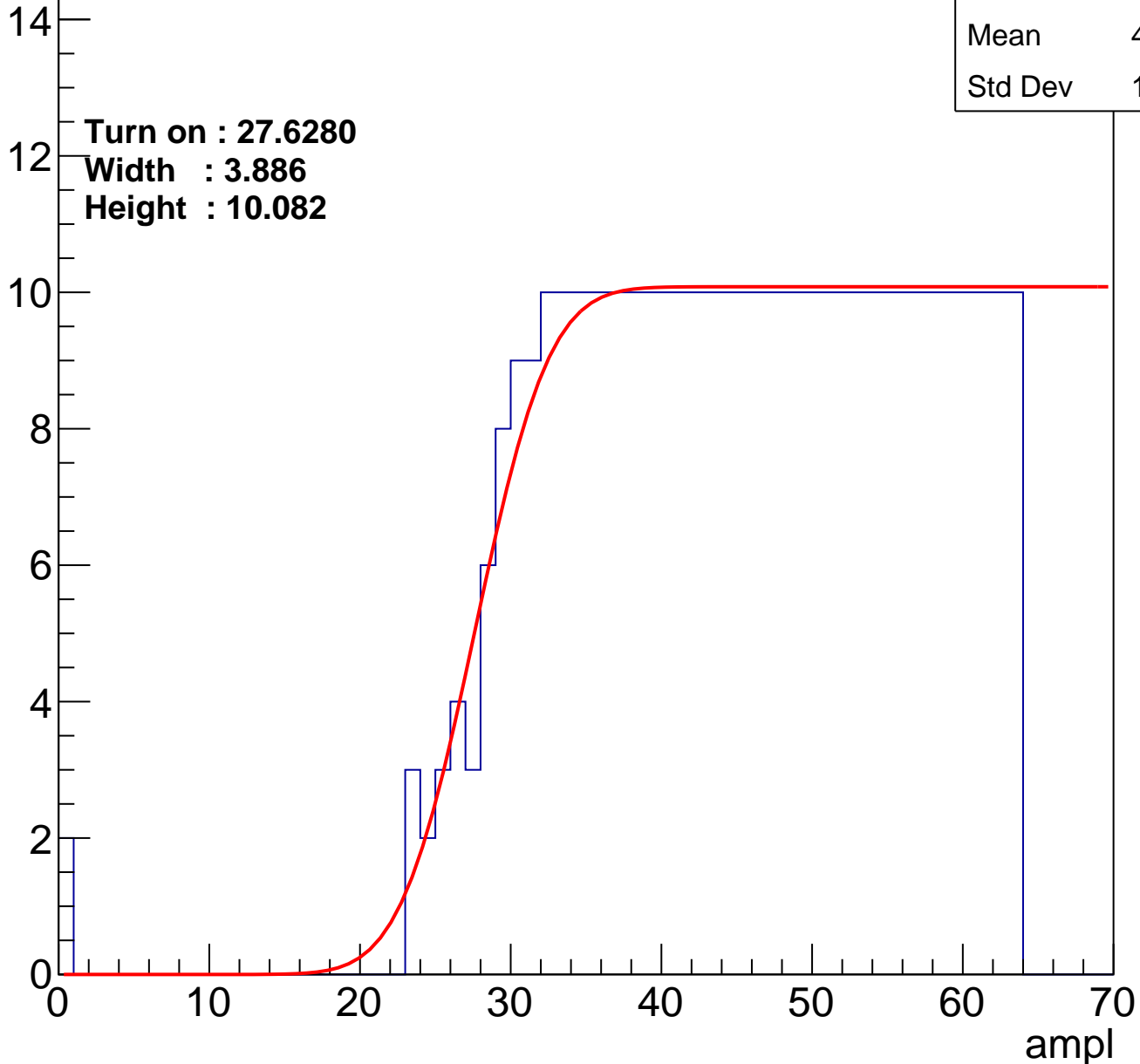
Entries	369
Mean	44.79
Std Dev	11.27

Turn on : 27.6280

Width : 3.886

Height : 10.082

Entry



B1L003S, U19-ch26

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	43.97
Std Dev	12.08

Turn on : 26.4631

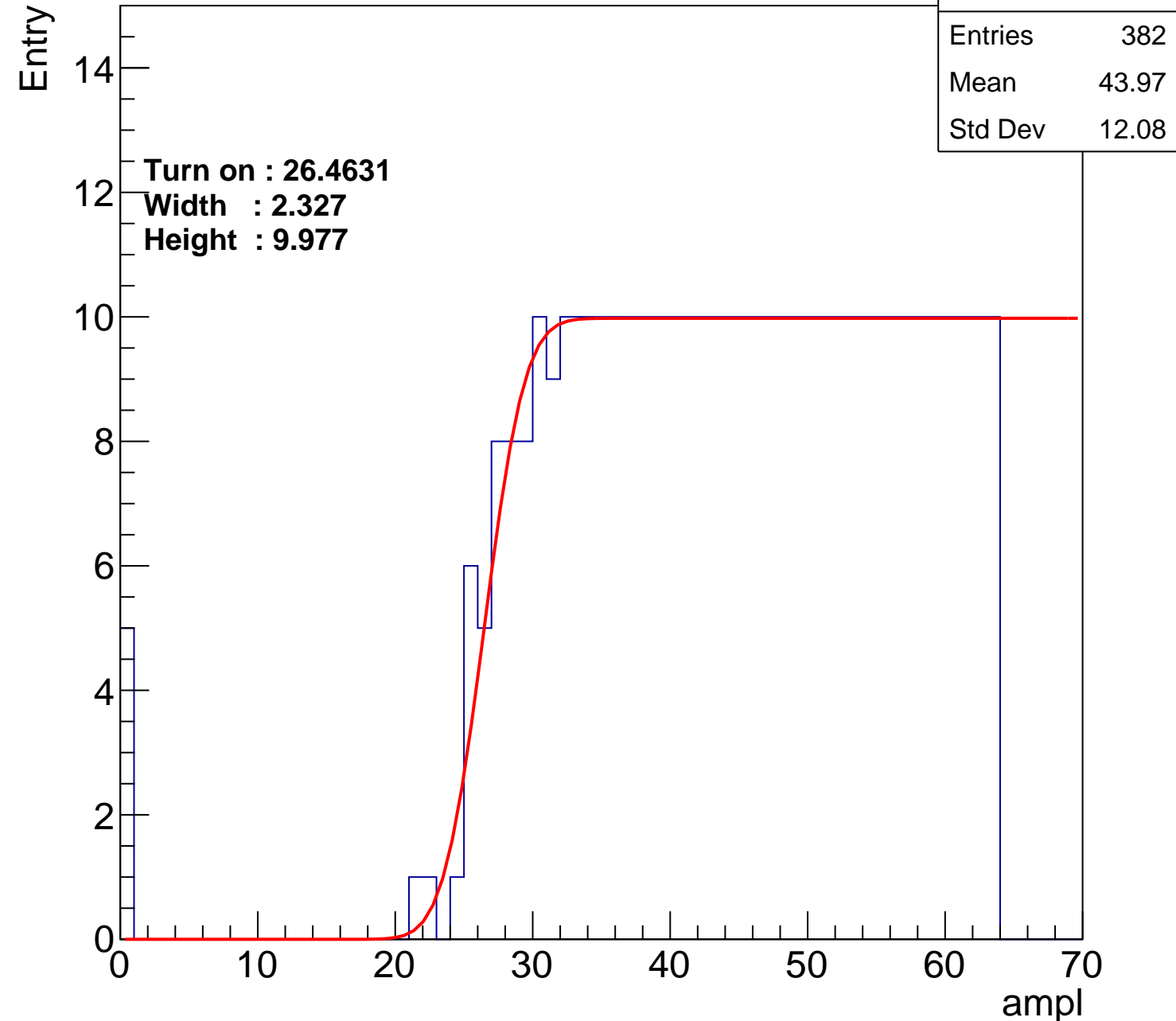
Width : 2.327

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch27

calib_packv5_042523_0143.root, FC#13, port D2

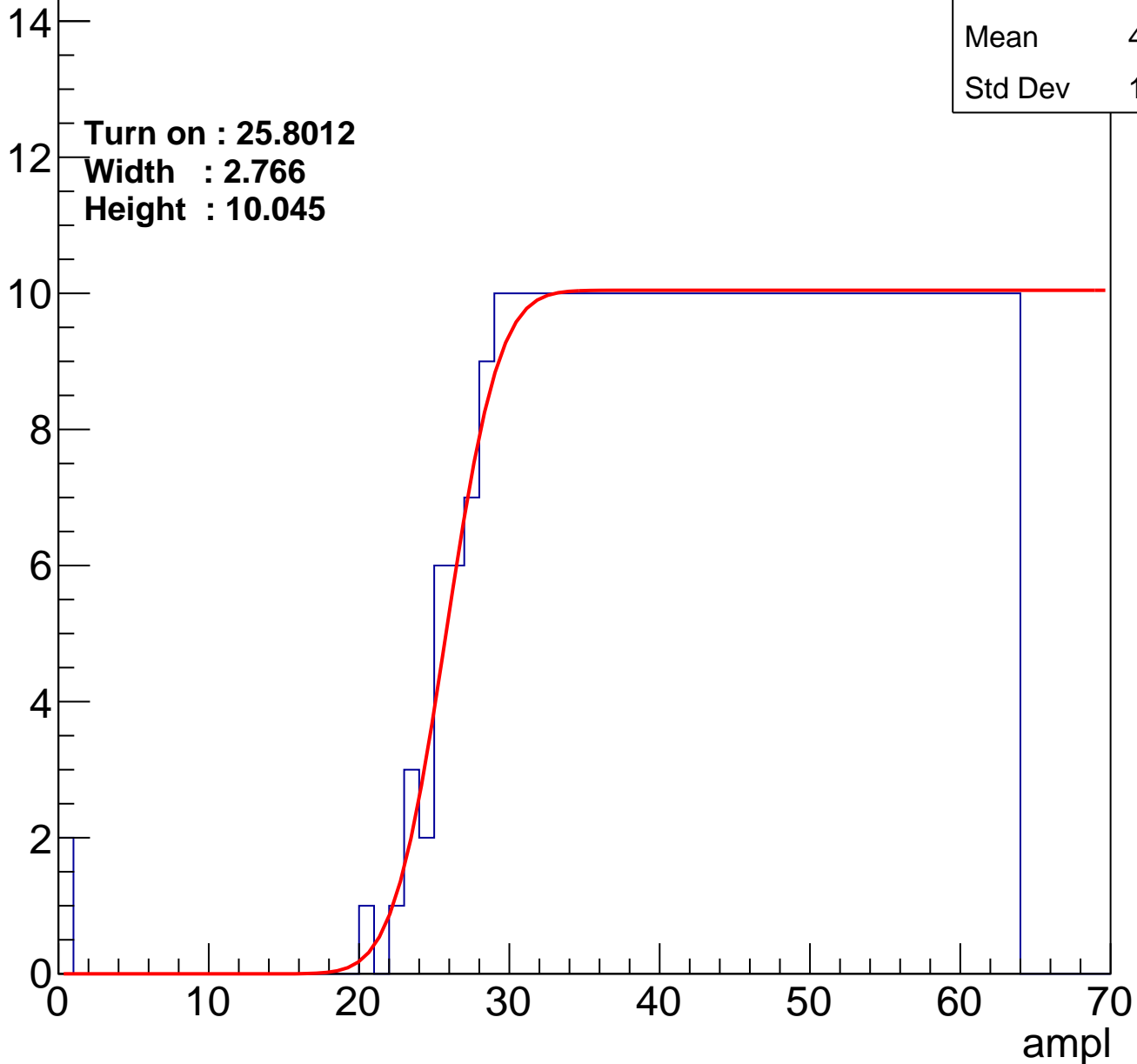
Entries	387
Mean	43.94
Std Dev	11.66

Turn on : 25.8012

Width : 2.766

Height : 10.045

Entry



B1L003S, U19-ch28

calib_packv5_042523_0143.root, FC#13, port D2

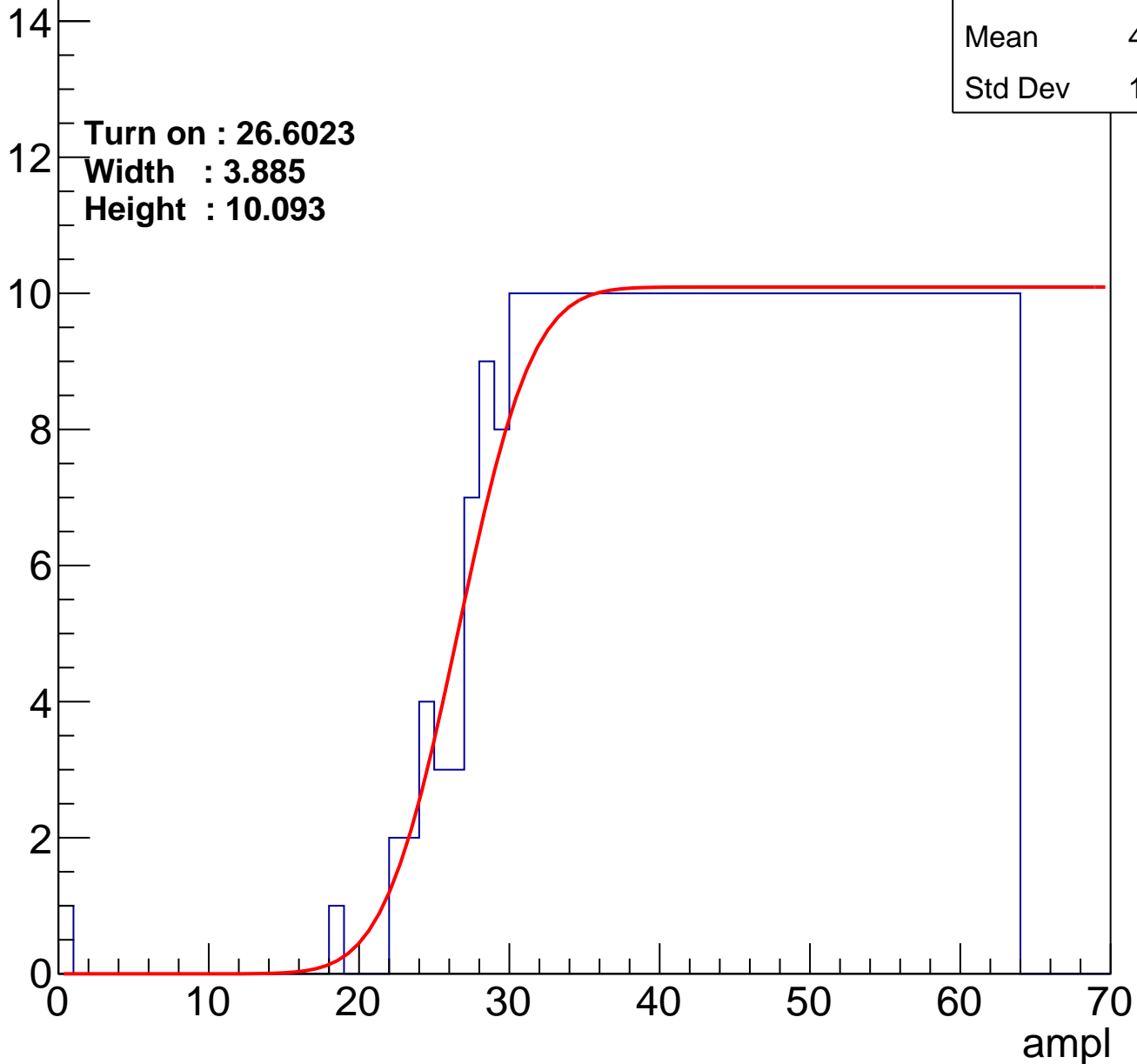
Entries	380
Mean	44.32
Std Dev	11.36

Turn on : 26.6023

Width : 3.885

Height : 10.093

Entry



B1L003S, U19-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.63
Std Dev	11.49

Turn on : 27.1919

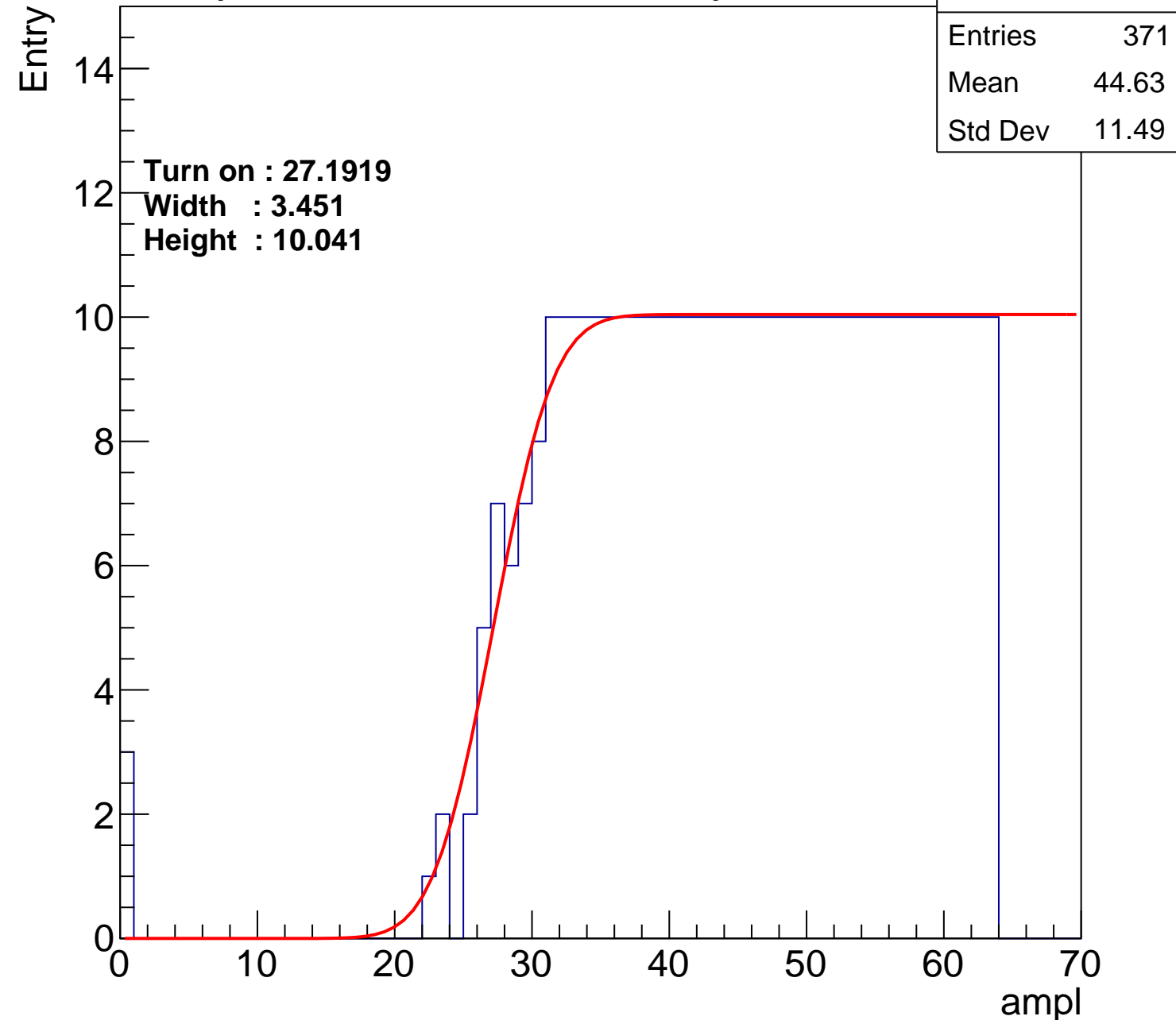
Width : 3.451

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch30

calib_packv5_042523_0143.root, FC#13, port D2

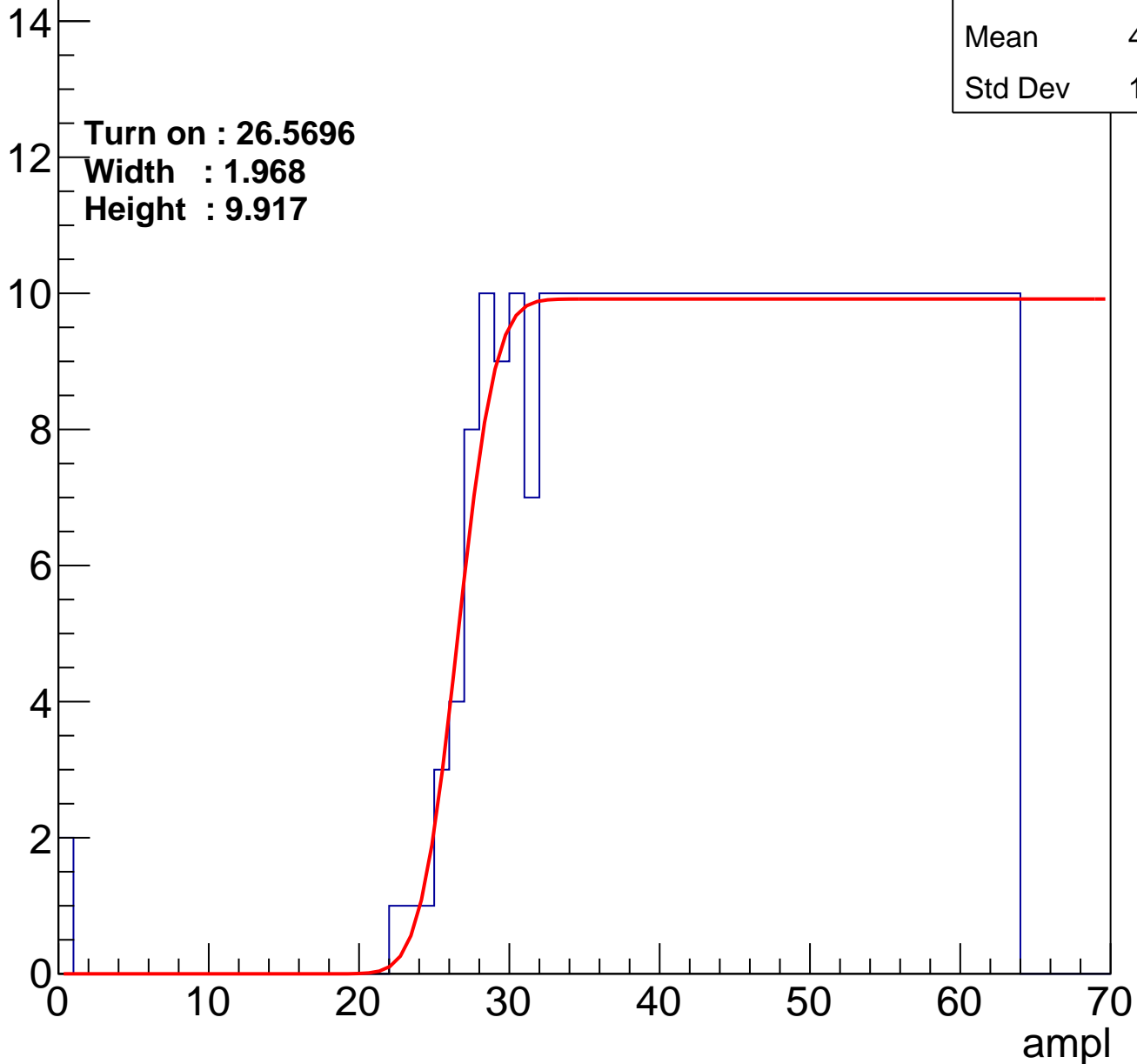
Entries	376
Mean	44.47
Std Dev	11.38

Turn on : 26.5696

Width : 1.968

Height : 9.917

Entry



B1L003S, U19-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.61
Std Dev	11.65

Turn on : 27.6826

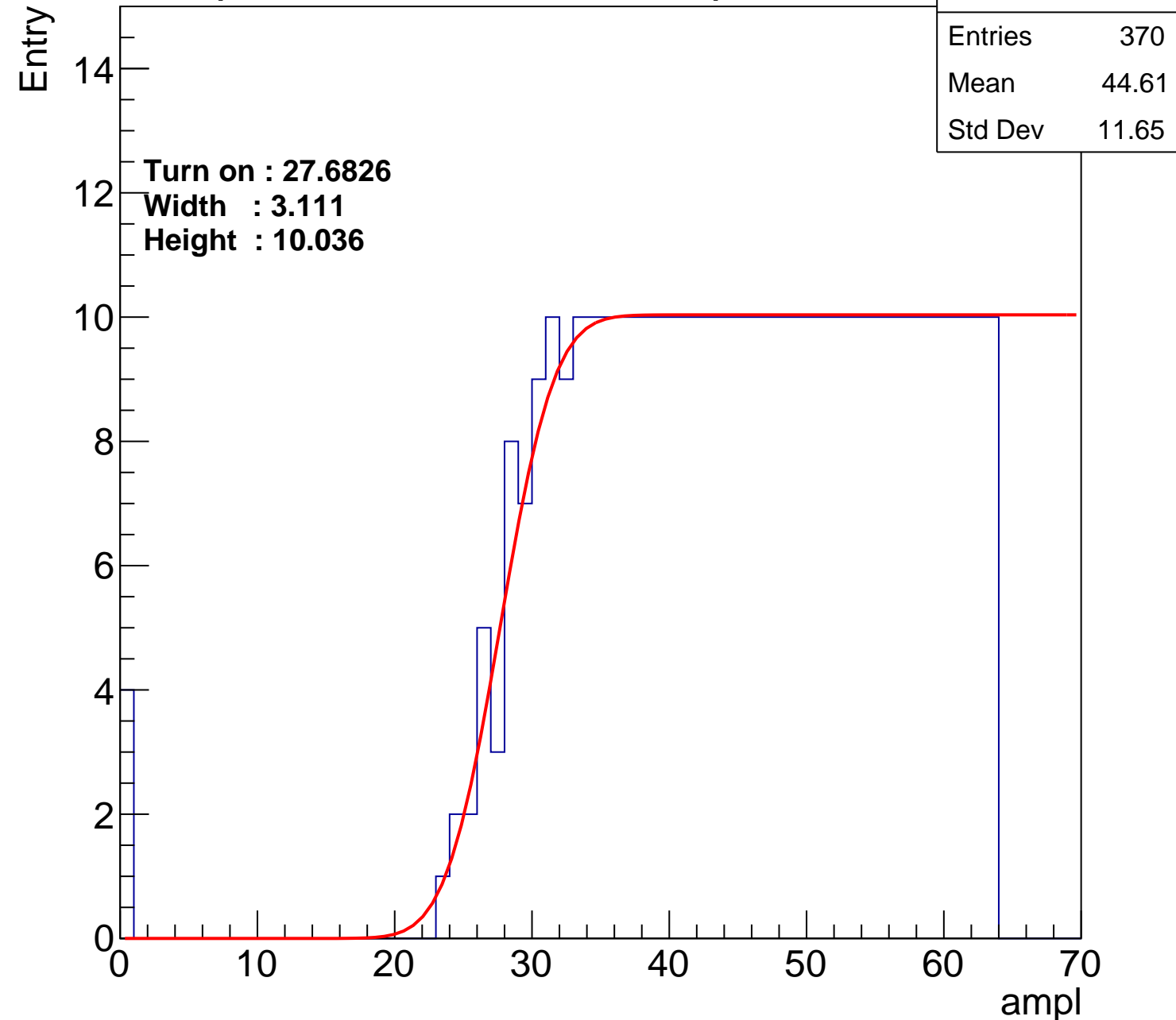
Width : 3.111

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch32

calib_packv5_042523_0143.root, FC#13, port D2

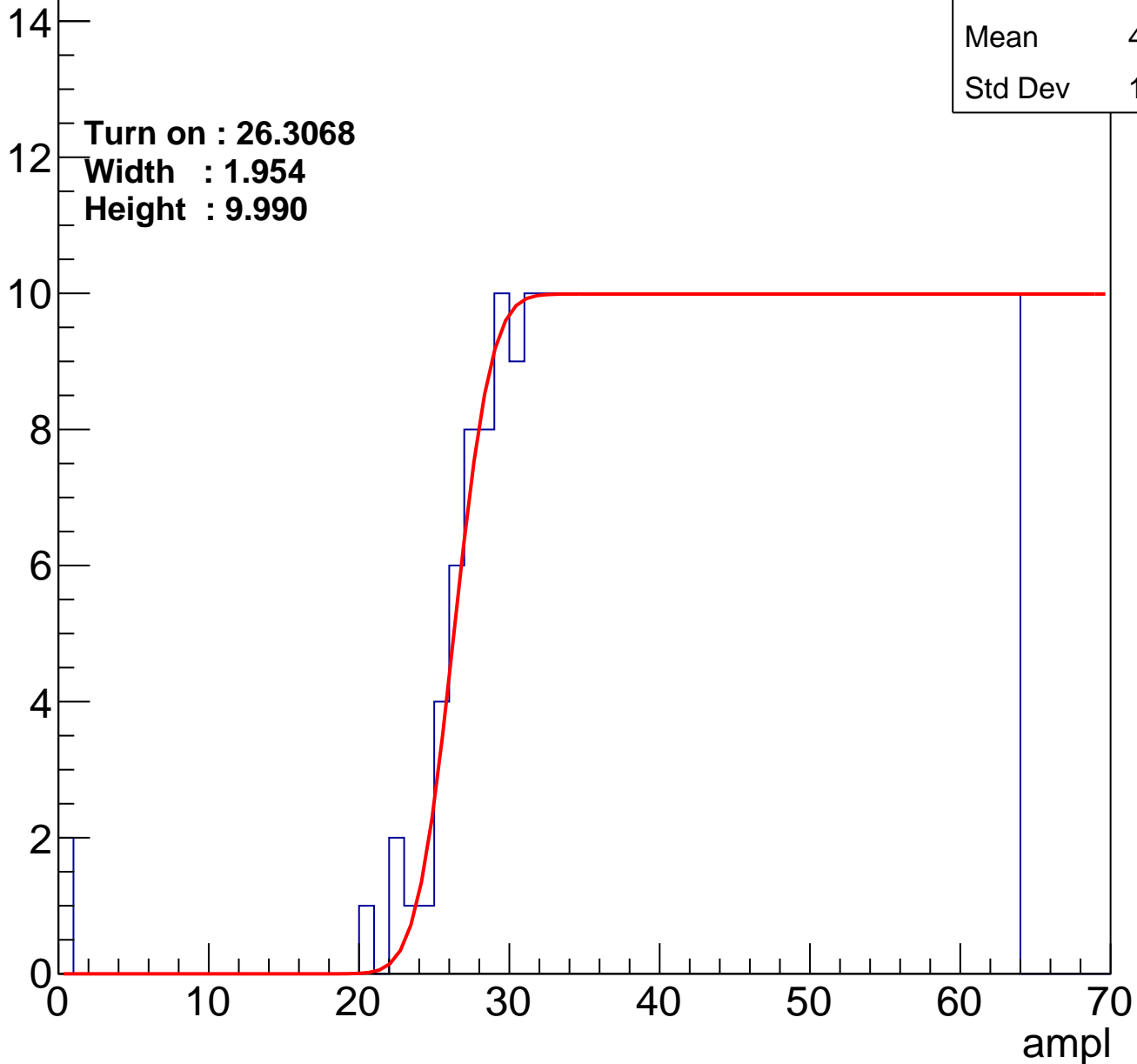
Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.3068

Width : 1.954

Height : 9.990

Entry



B1L003S, U19-ch33

calib_packv5_042523_0143.root, FC#13, port D2

Entries	394
Mean	43.49
Std Dev	12.14

Turn on : 25.6743

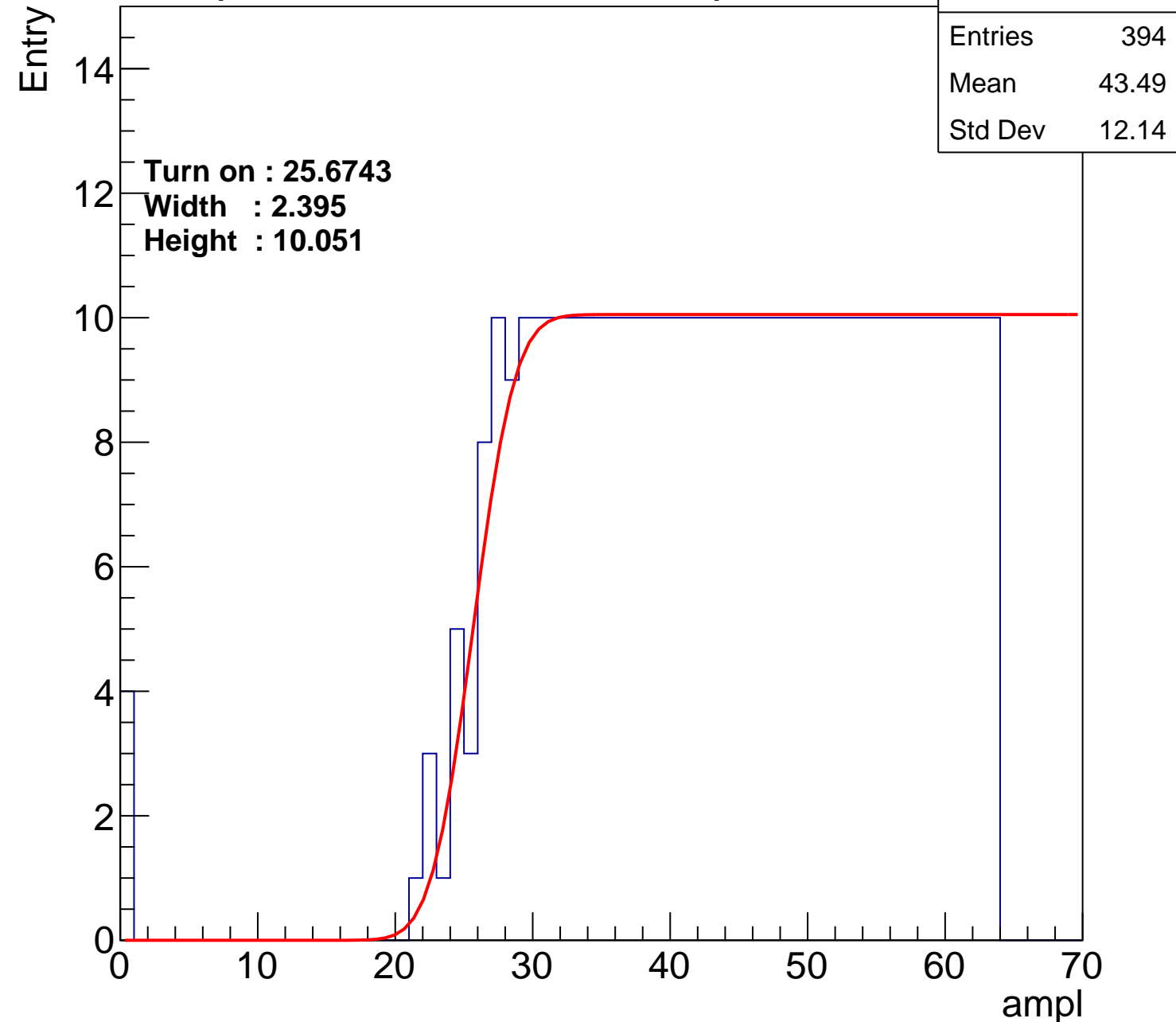
Width : 2.395

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch34

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.84
Std Dev	11.77

Turn on : 25.6713

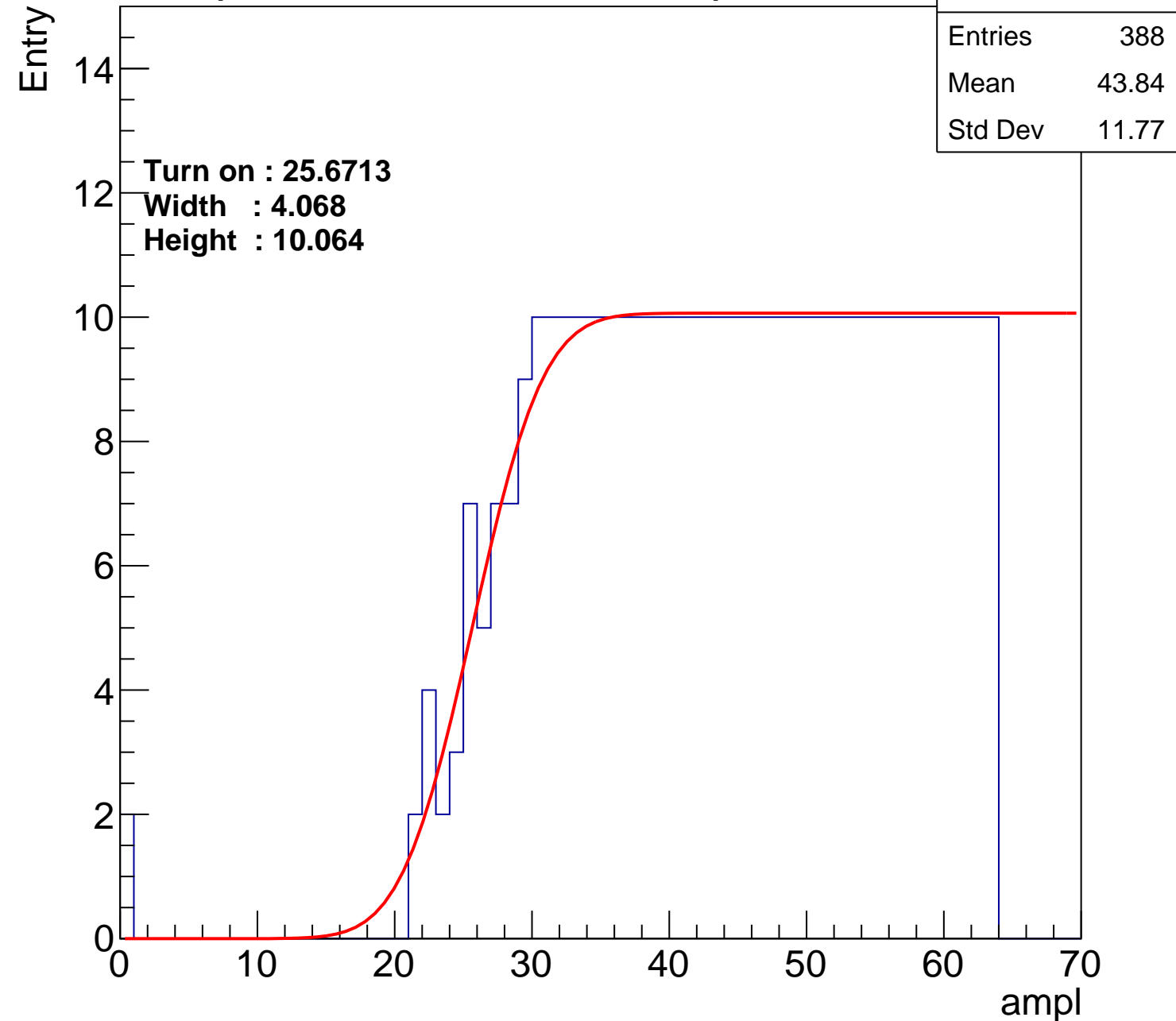
Width : 4.068

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch35

calib_packv5_042523_0143.root, FC#13, port D2

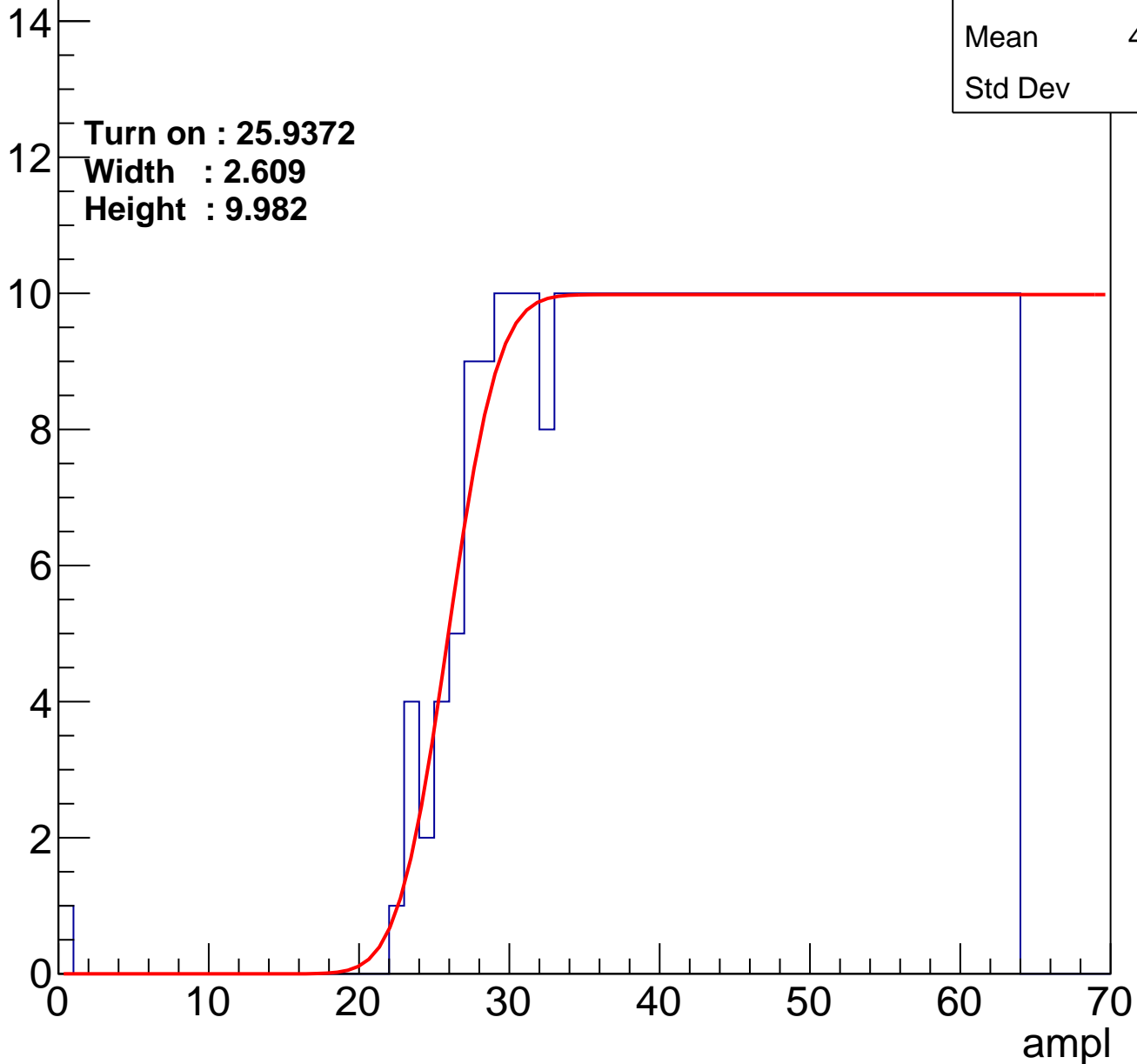
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.9372

Width : 2.609

Height : 9.982

Entry



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 28.0929
Width : 2.682
Height : 10.015



B1L003S, U19-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.36
Std Dev	11.65

Turn on : 27.0069

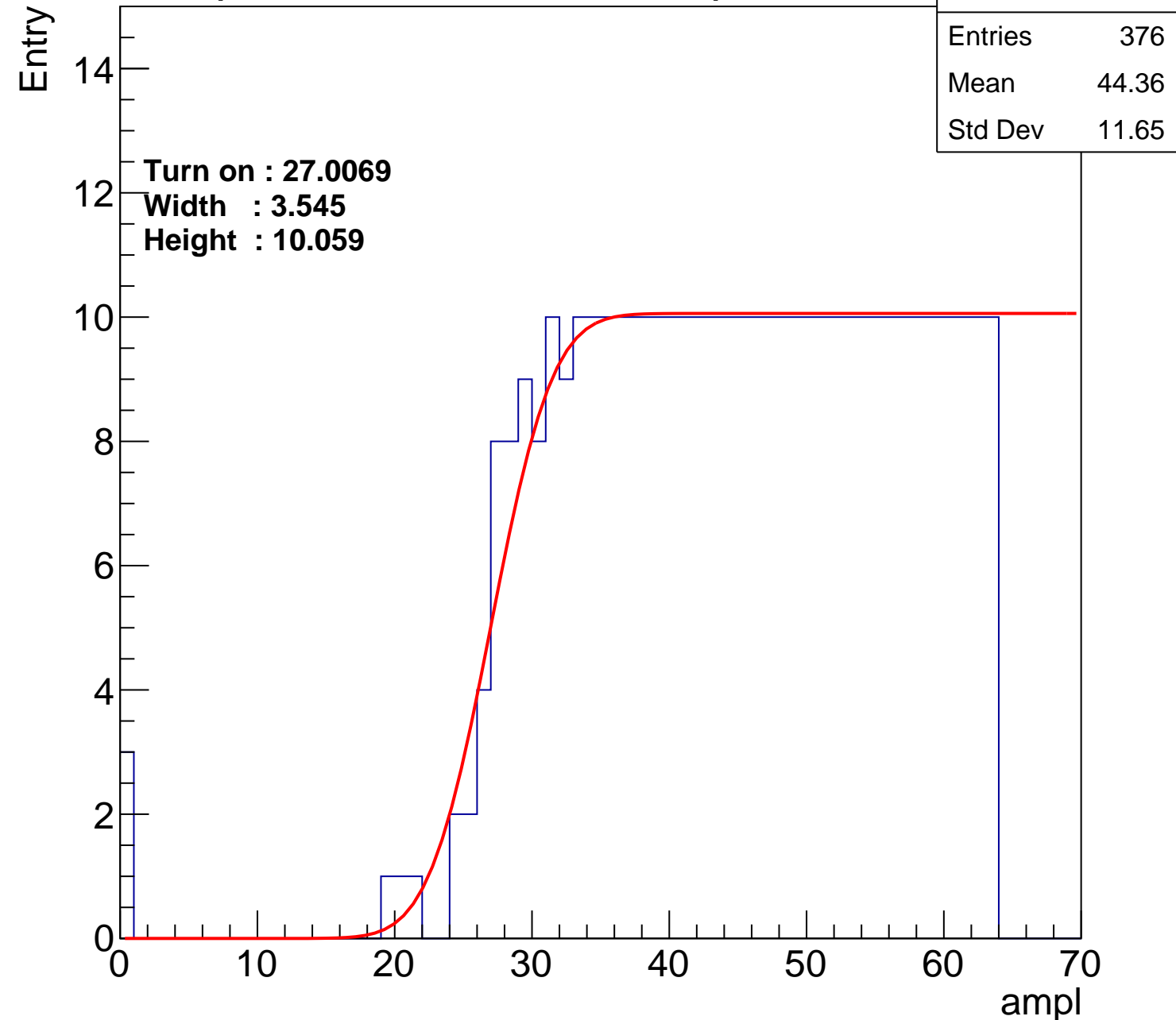
Width : 3.545

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch38

calib_packv5_042523_0143.root, FC#13, port D2

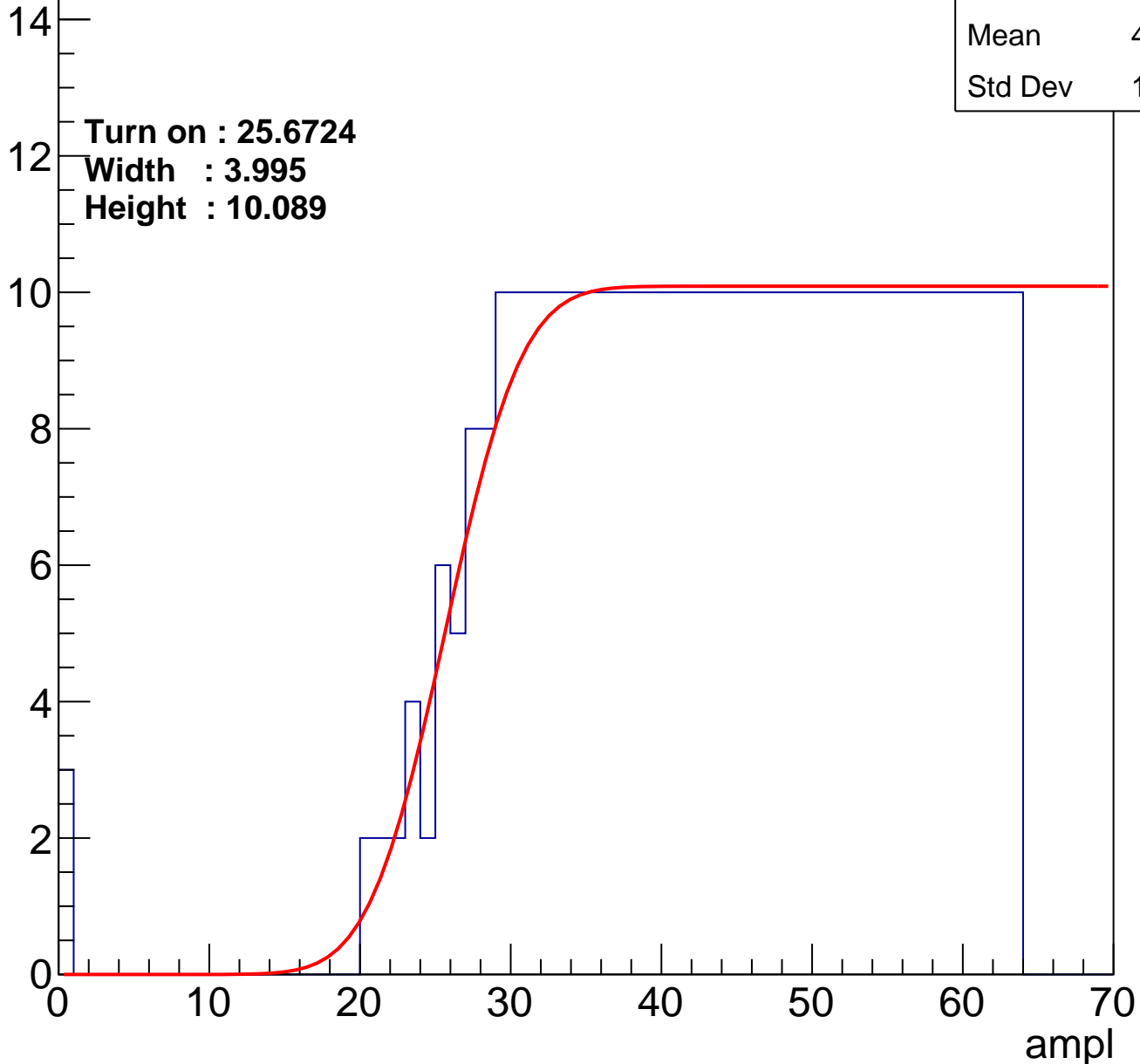
Entries	392
Mean	43.59
Std Dev	12.03

Turn on : 25.6724

Width : 3.995

Height : 10.089

Entry



B1L003S, U19-ch39

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	44.05
Std Dev	11.63

Turn on : 26.1094

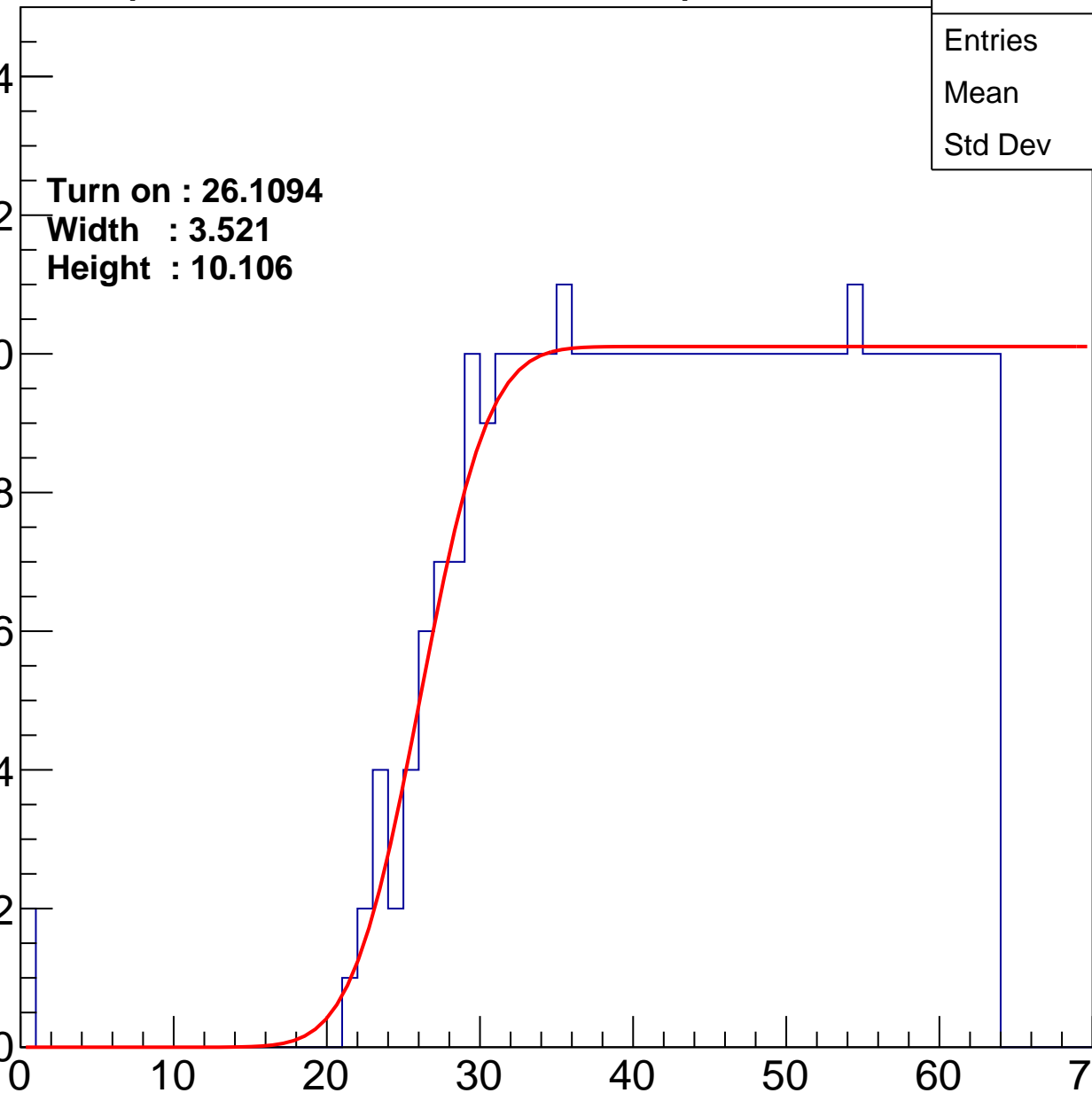
Width : 3.521

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch40

calib_packv5_042523_0143.root, FC#13, port D2

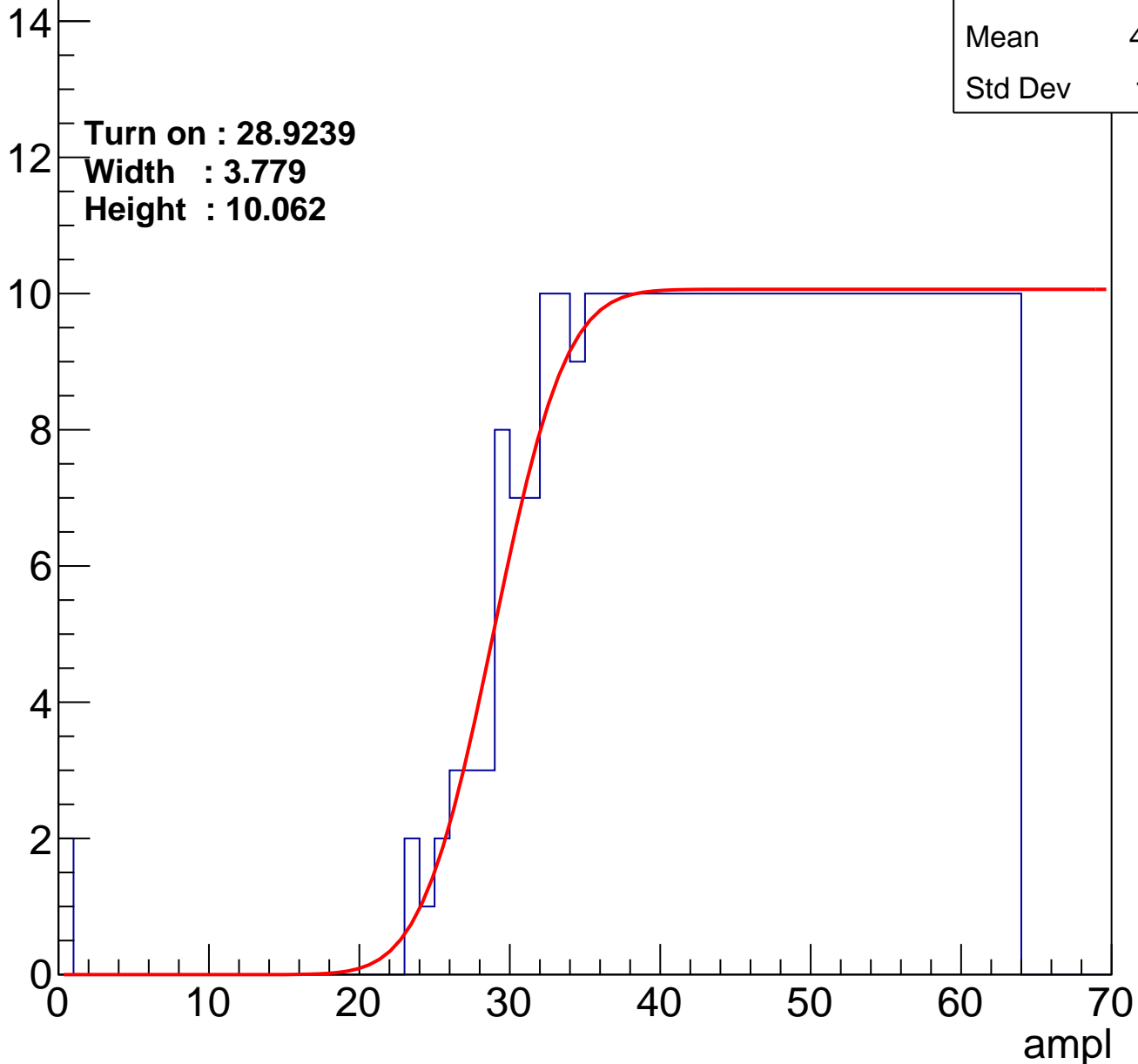
Entries	357
Mean	45.34
Std Dev	11.01

Turn on : 28.9239

Width : 3.779

Height : 10.062

Entry



B1L003S, U19-ch41

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.72
Std Dev	11.65

Turn on : 28.3044

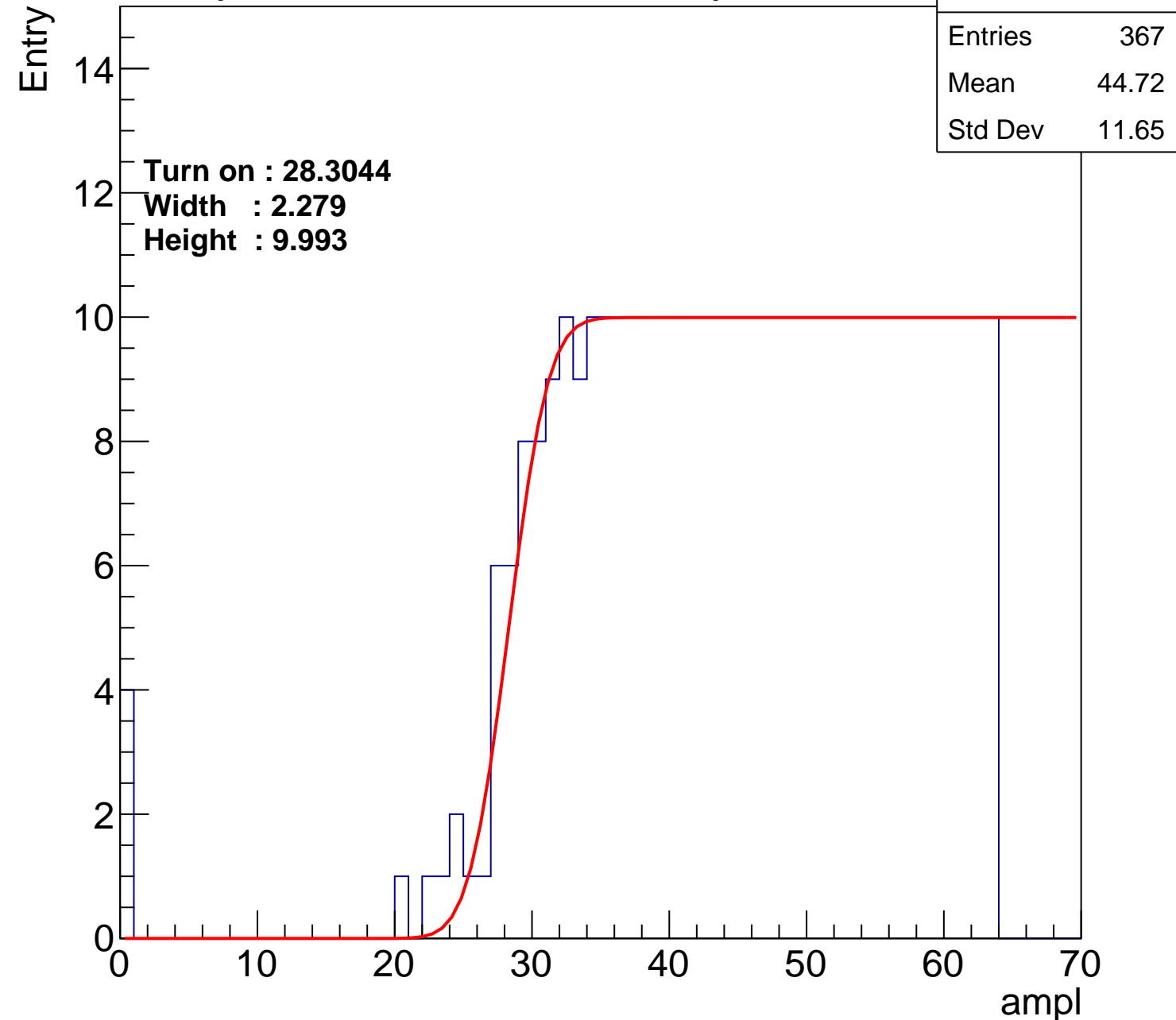
Width : 2.279

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch42

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.6
Std Dev	11.36

Turn on : 27.2746

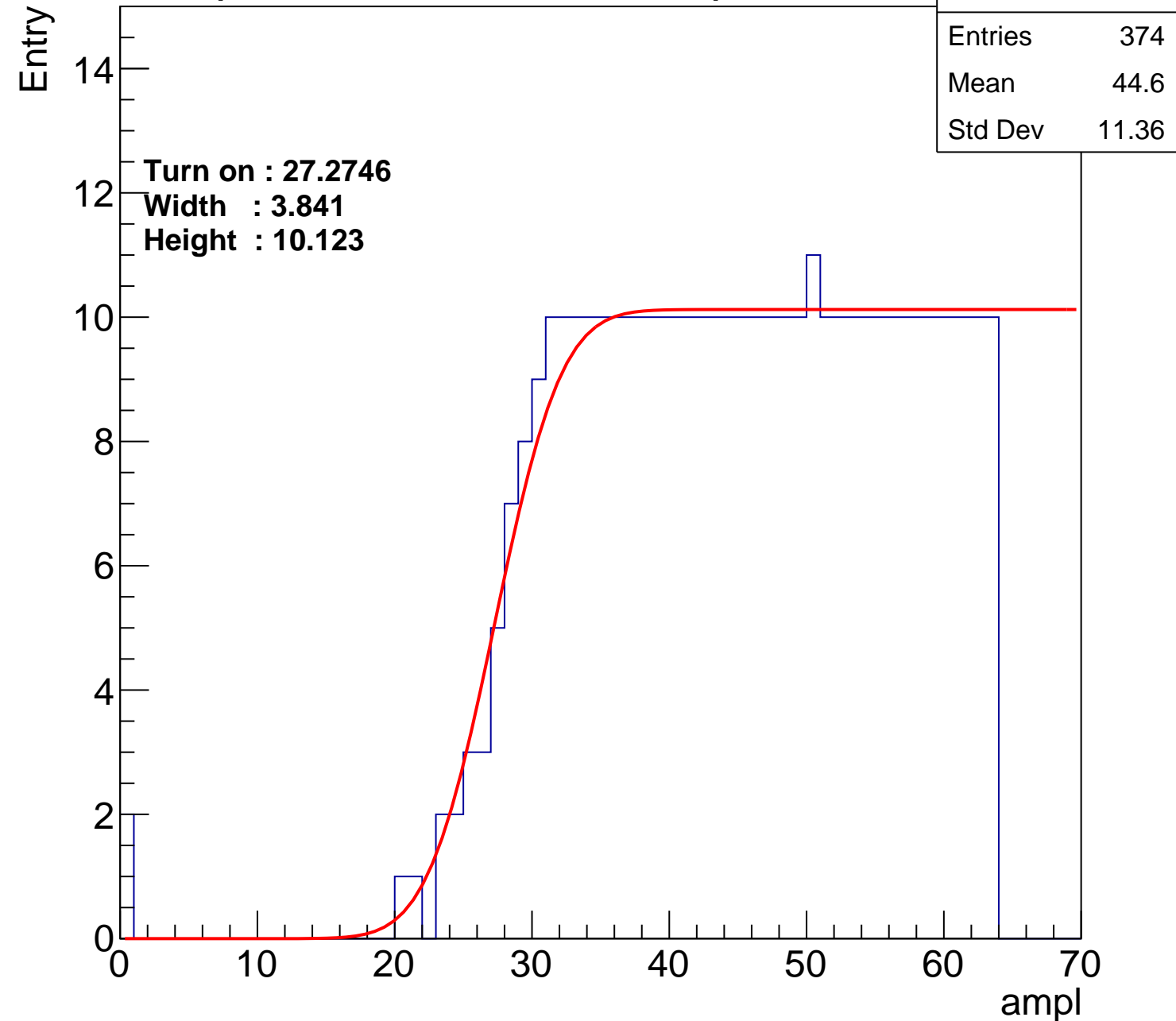
Width : 3.841

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch43

calib_packv5_042523_0143.root, FC#13, port D2

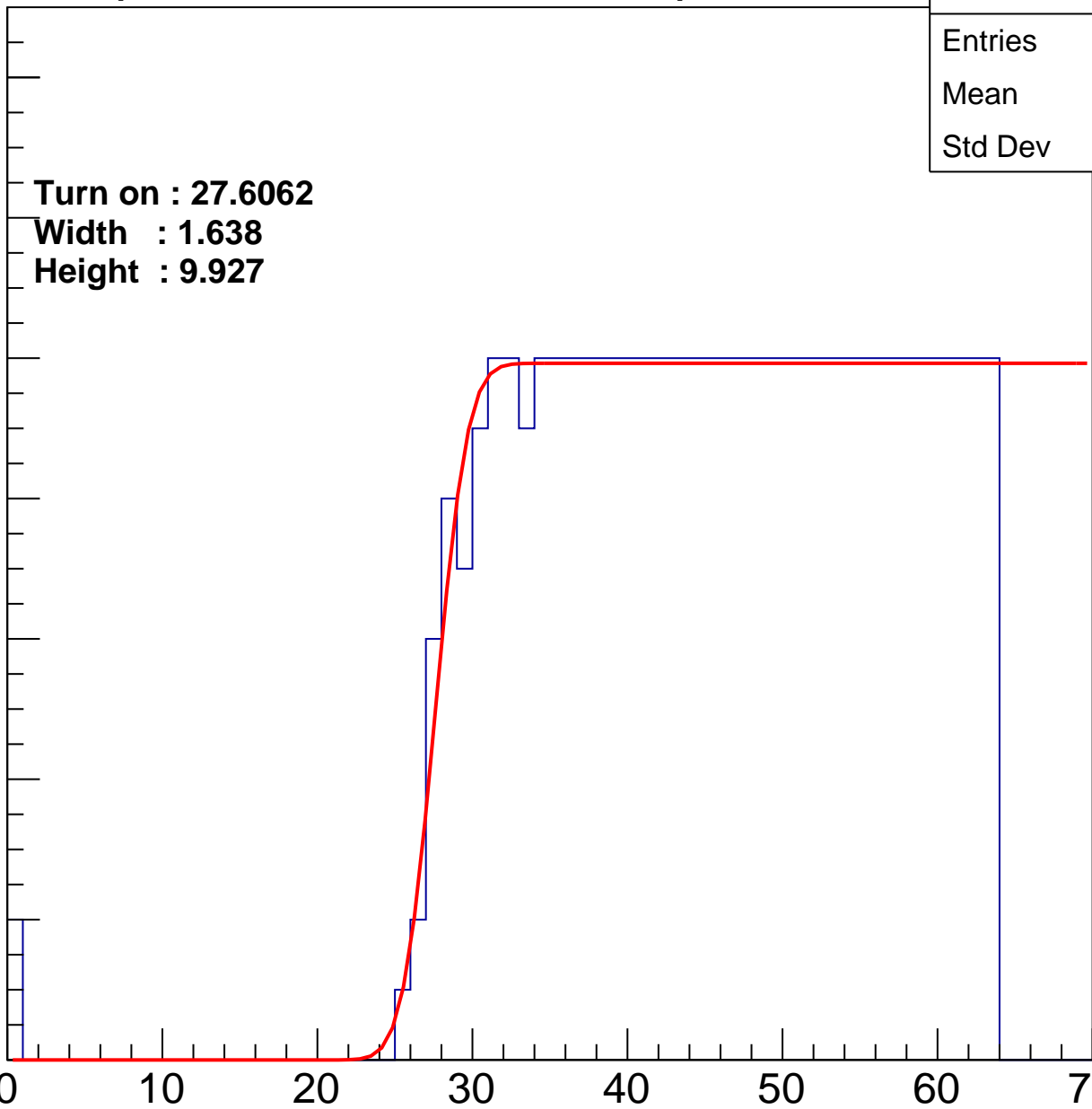
Entry

14
12
10
8
6
4
2
0

Turn on : 27.6062
Width : 1.638
Height : 9.927

Entries	364
Mean	45.09
Std Dev	11.04

ampl



B1L003S, U19-ch44

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.37
Std Dev	11.31

Turn on : 26.4987

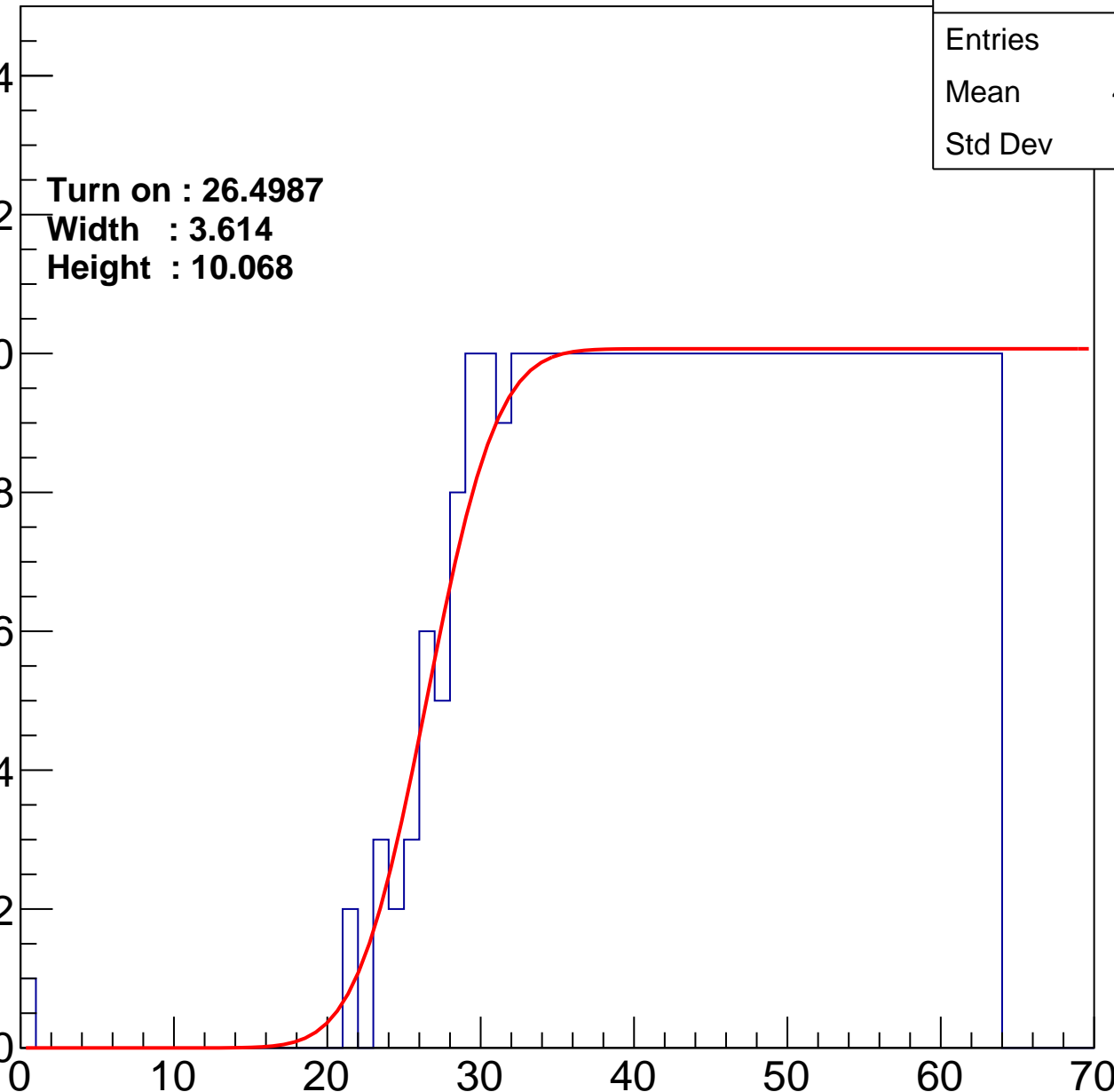
Width : 3.614

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch45

calib_packv5_042523_0143.root, FC#13, port D2

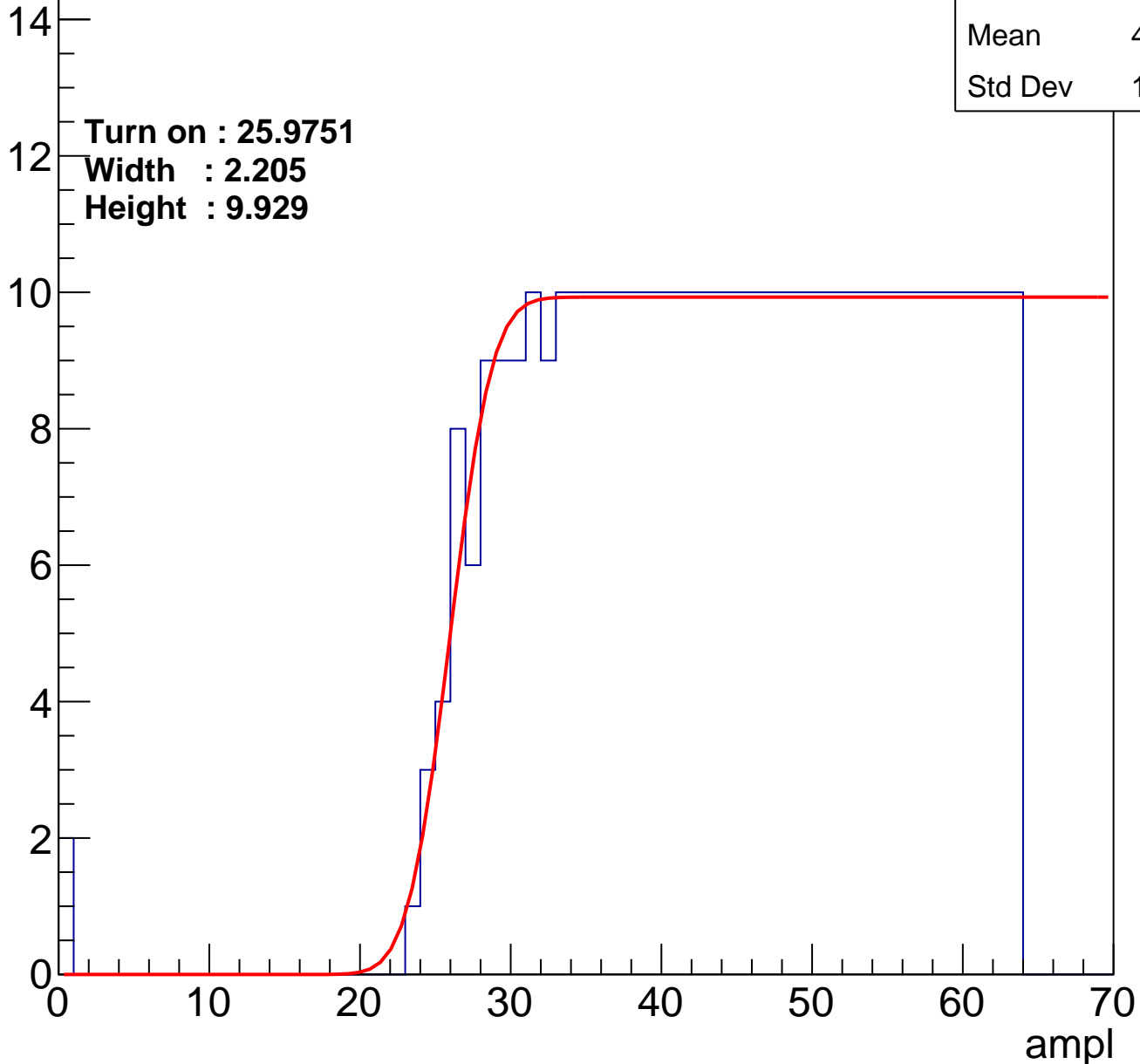
Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 25.9751

Width : 2.205

Height : 9.929

Entry



B1L003S, U19-ch46

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.86
Std Dev	11.53

Turn on : 25.3689

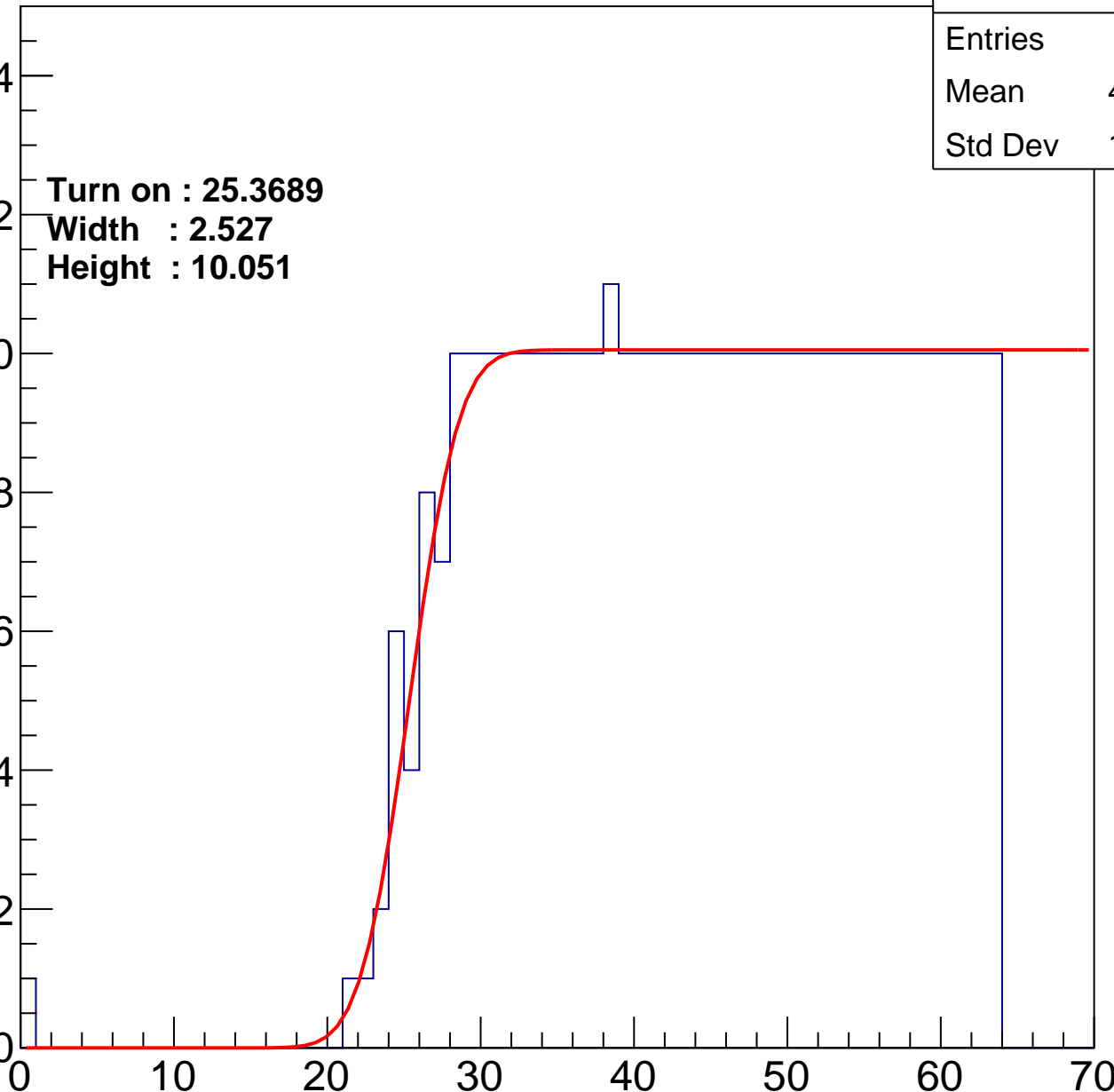
Width : 2.527

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch47

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.13
Std Dev	11.86

Turn on : 26.2015

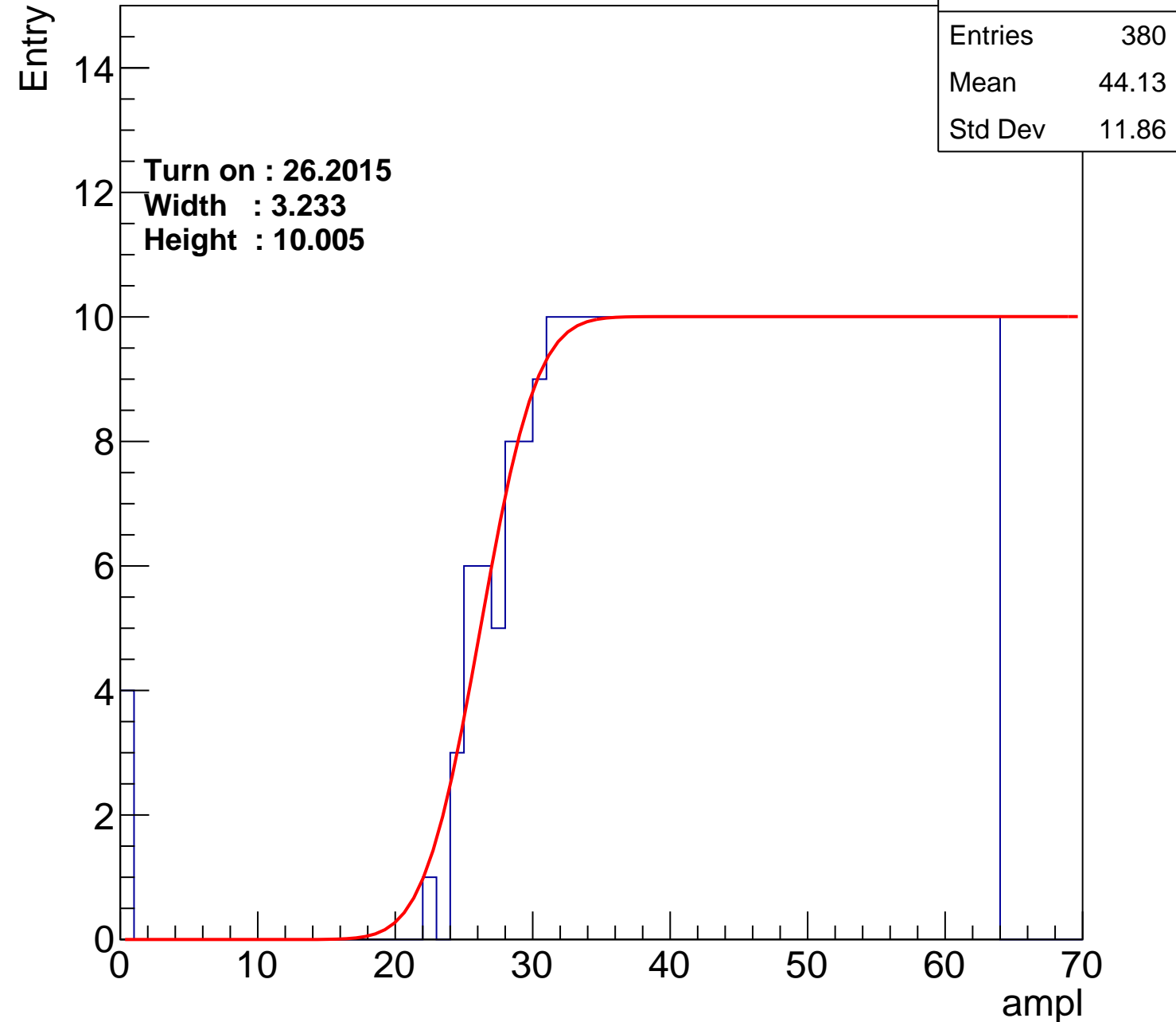
Width : 3.233

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch48

calib_packv5_042523_0143.root, FC#13, port D2

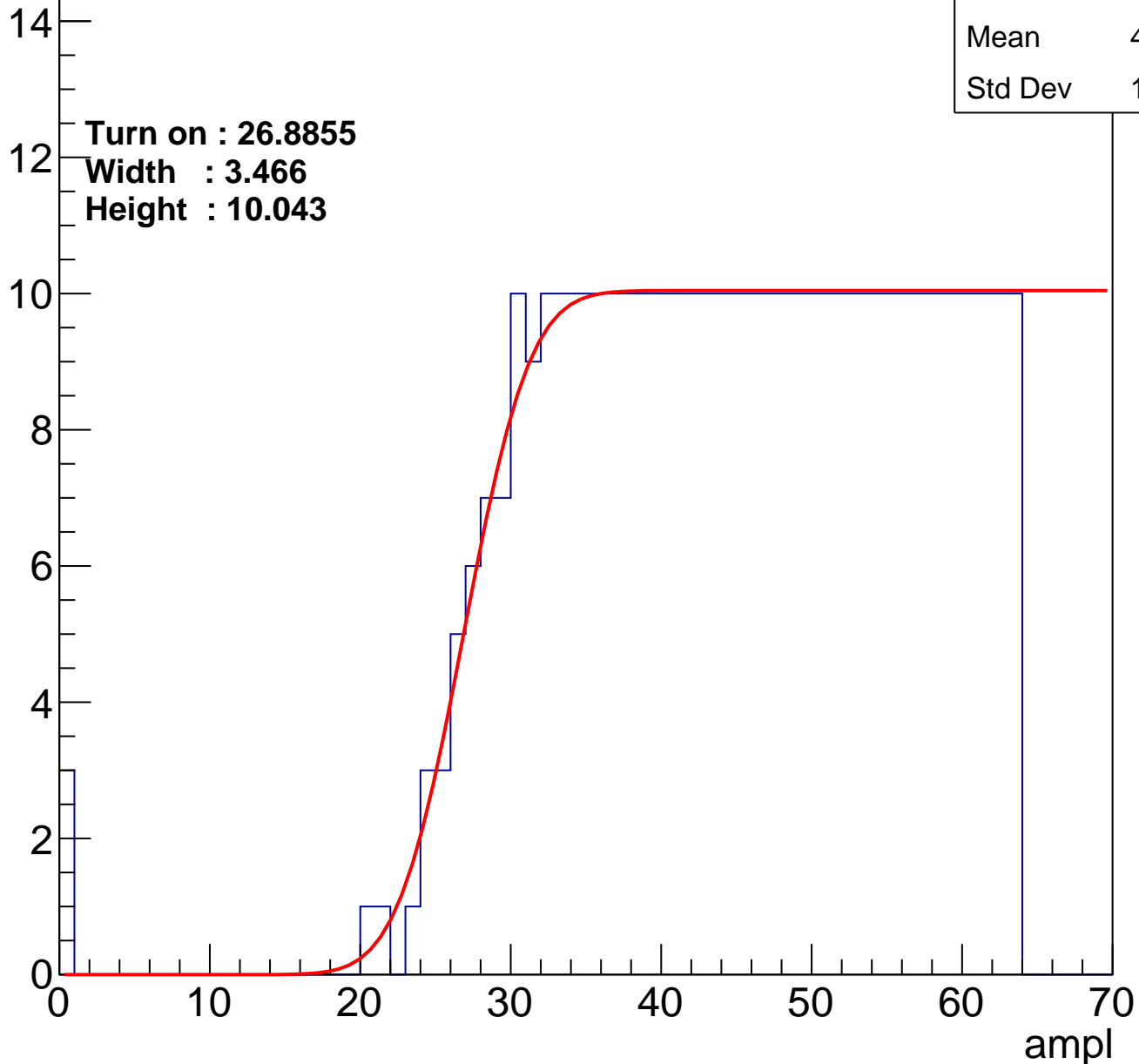
Entries	376
Mean	44.36
Std Dev	11.65

Turn on : 26.8855

Width : 3.466

Height : 10.043

Entry



B1L003S, U19-ch49

calib_packv5_042523_0143.root, FC#13, port D2

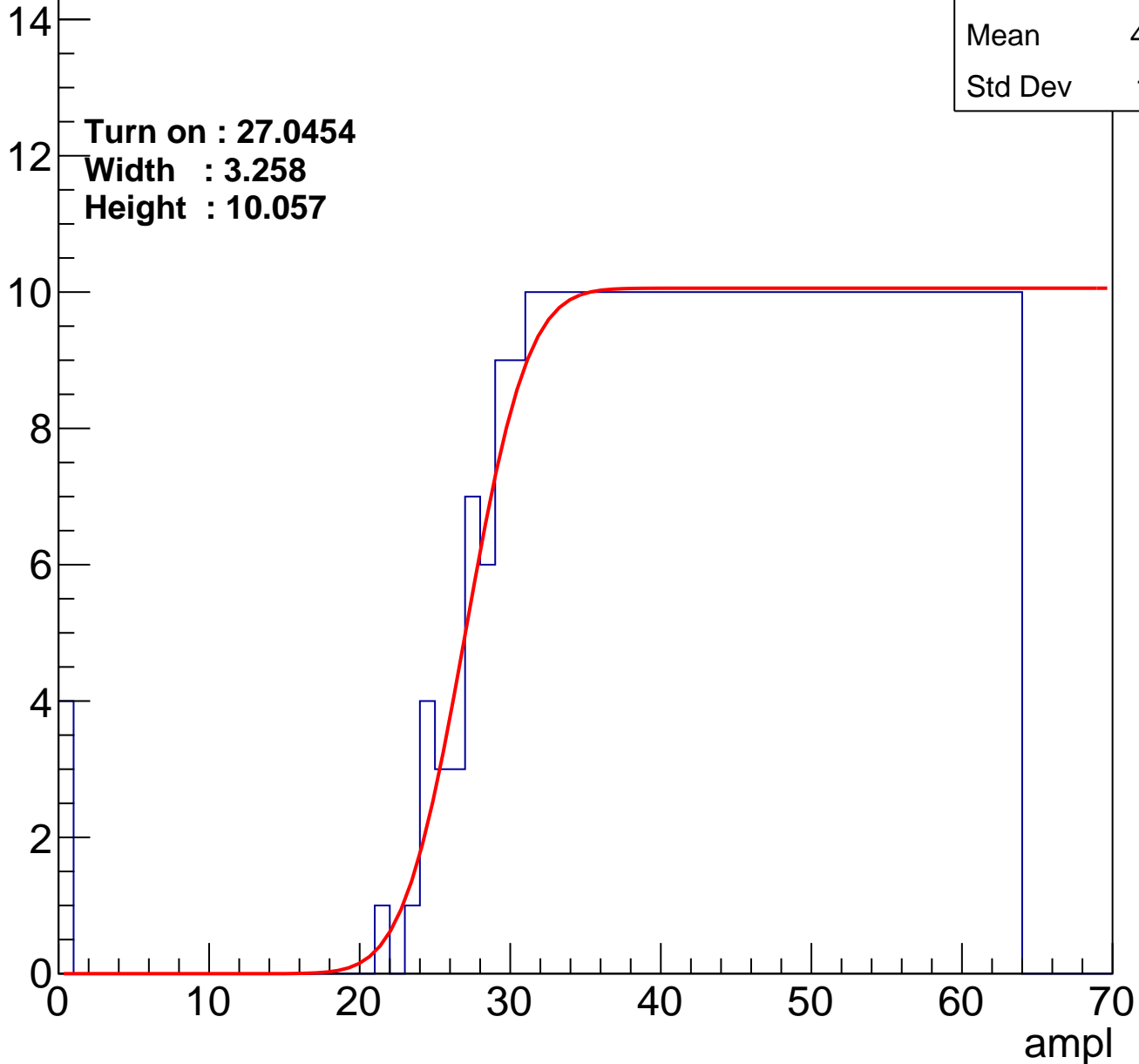
Entries	377
Mean	44.27
Std Dev	11.81

Turn on : 27.0454

Width : 3.258

Height : 10.057

Entry



B1L003S, U19-ch50

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.28
Std Dev	10.96

Turn on : 28.8164

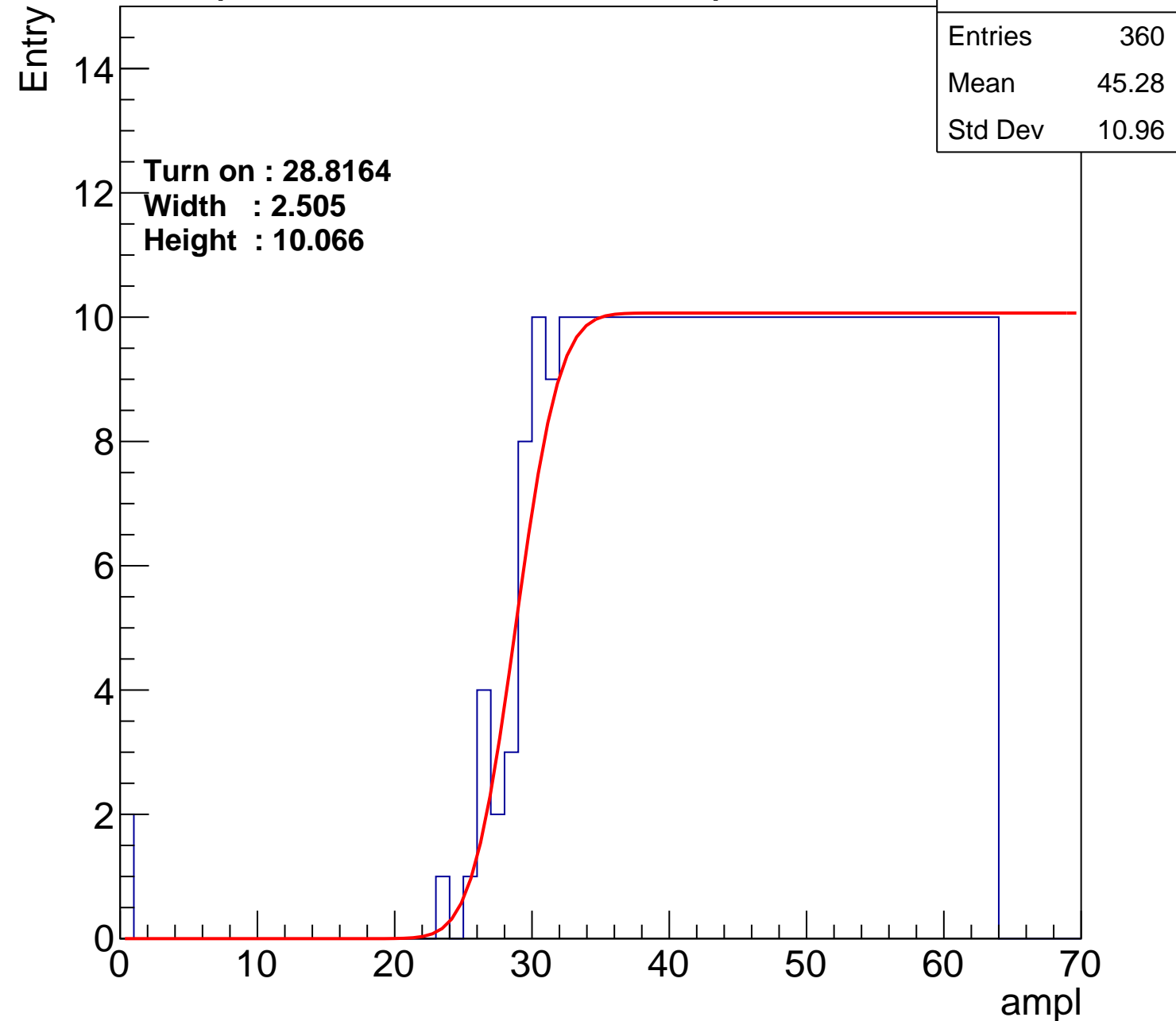
Width : 2.505

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch51

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	44.07
Std Dev	11.44

Turn on : 25.9728

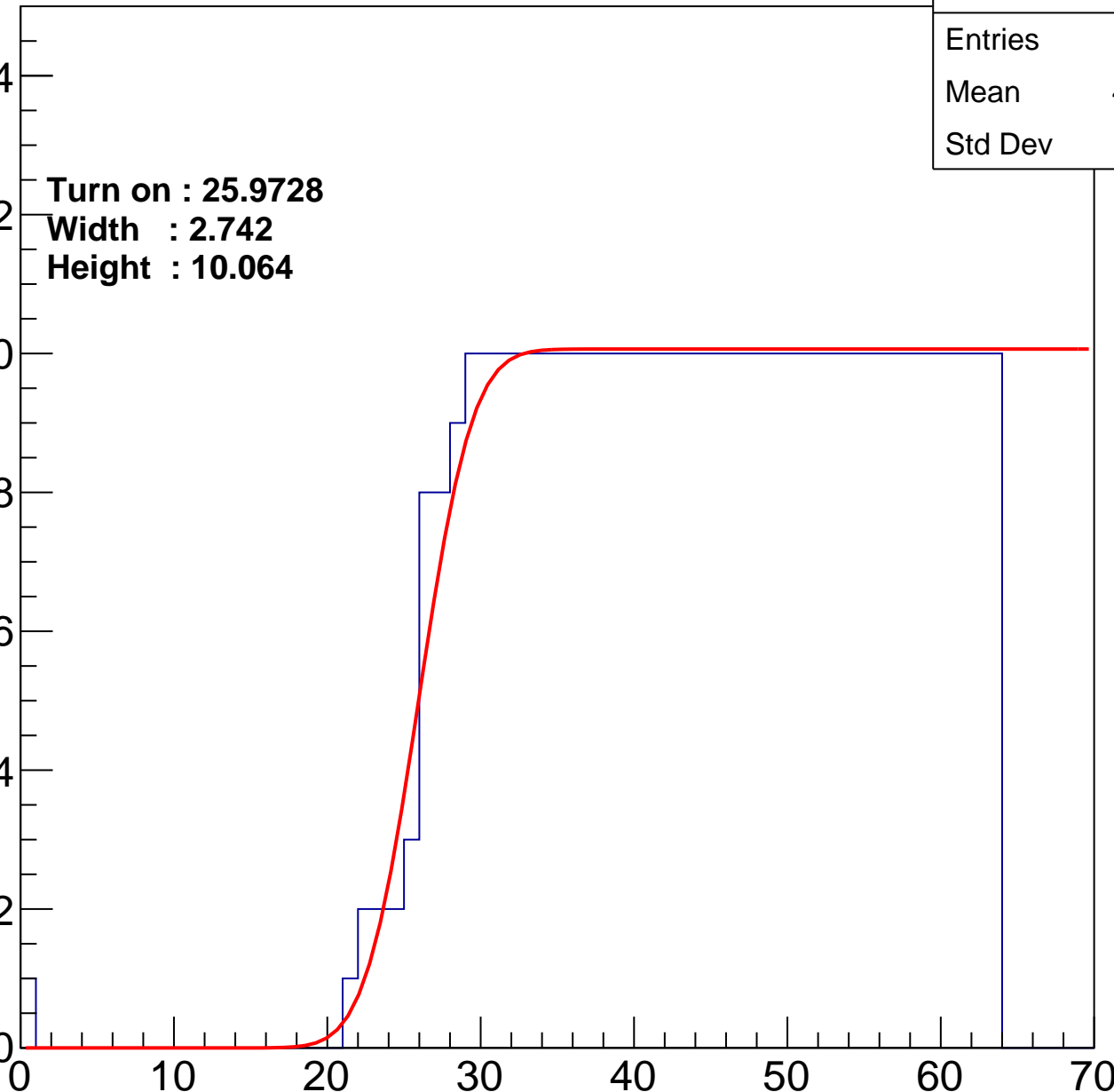
Width : 2.742

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch52

calib_packv5_042523_0143.root, FC#13, port D2

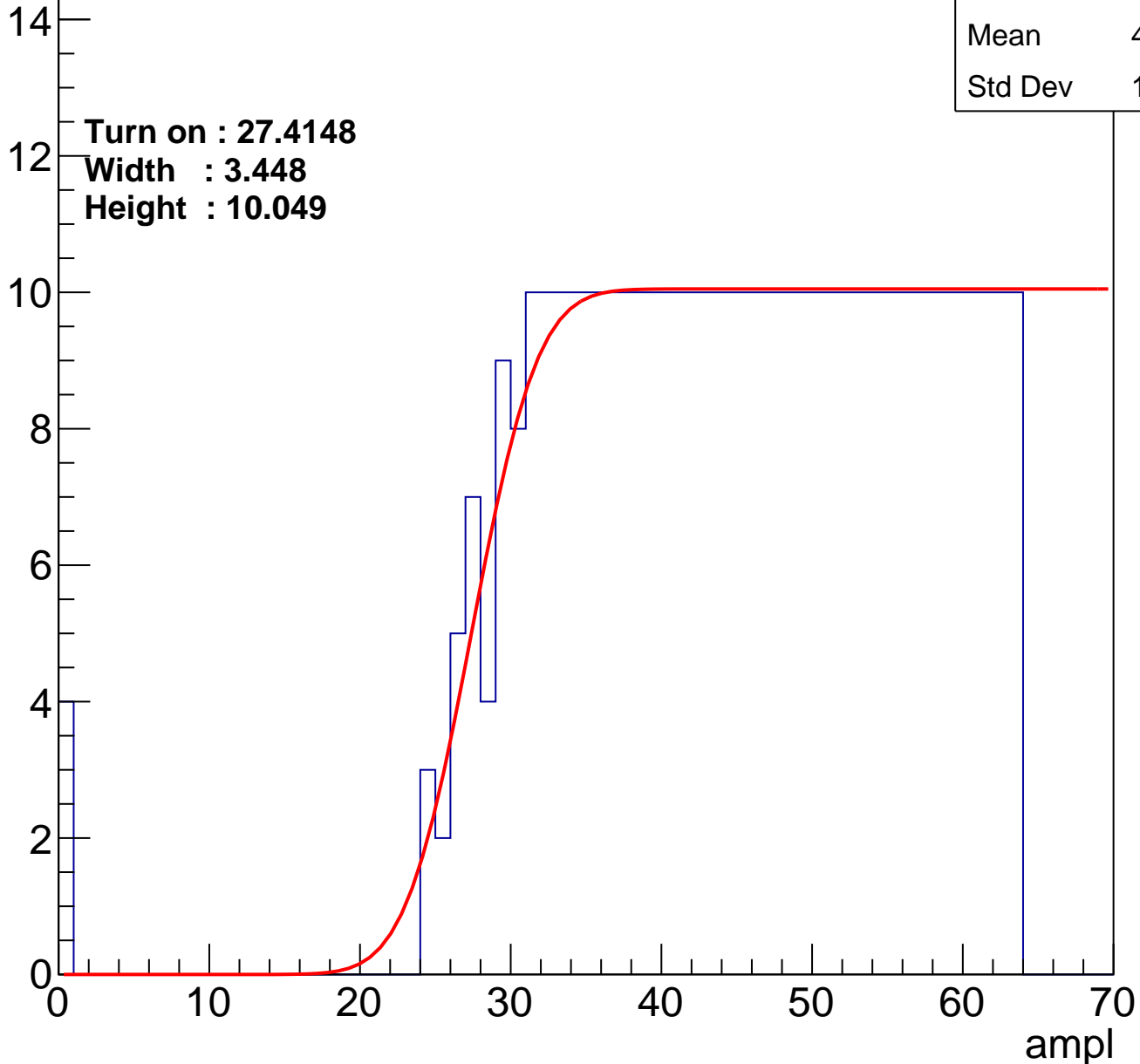
Entries	372
Mean	44.53
Std Dev	11.68

Turn on : 27.4148

Width : 3.448

Height : 10.049

Entry



B1L003S, U19-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.37
Std Dev	11.5

Turn on : 26.9502

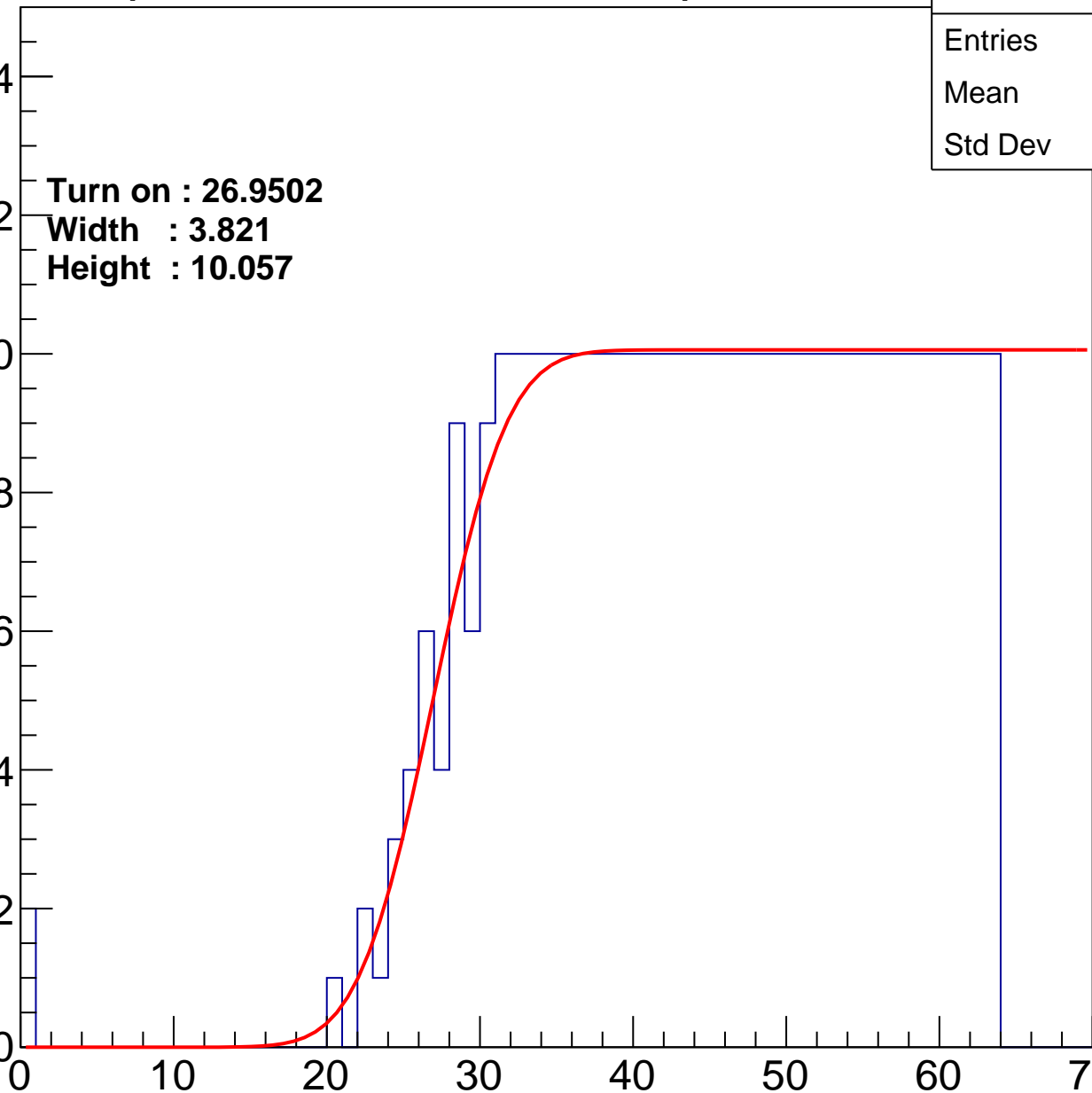
Width : 3.821

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	404
Mean	43.03
Std Dev	12.27

Turn on : 24.1367

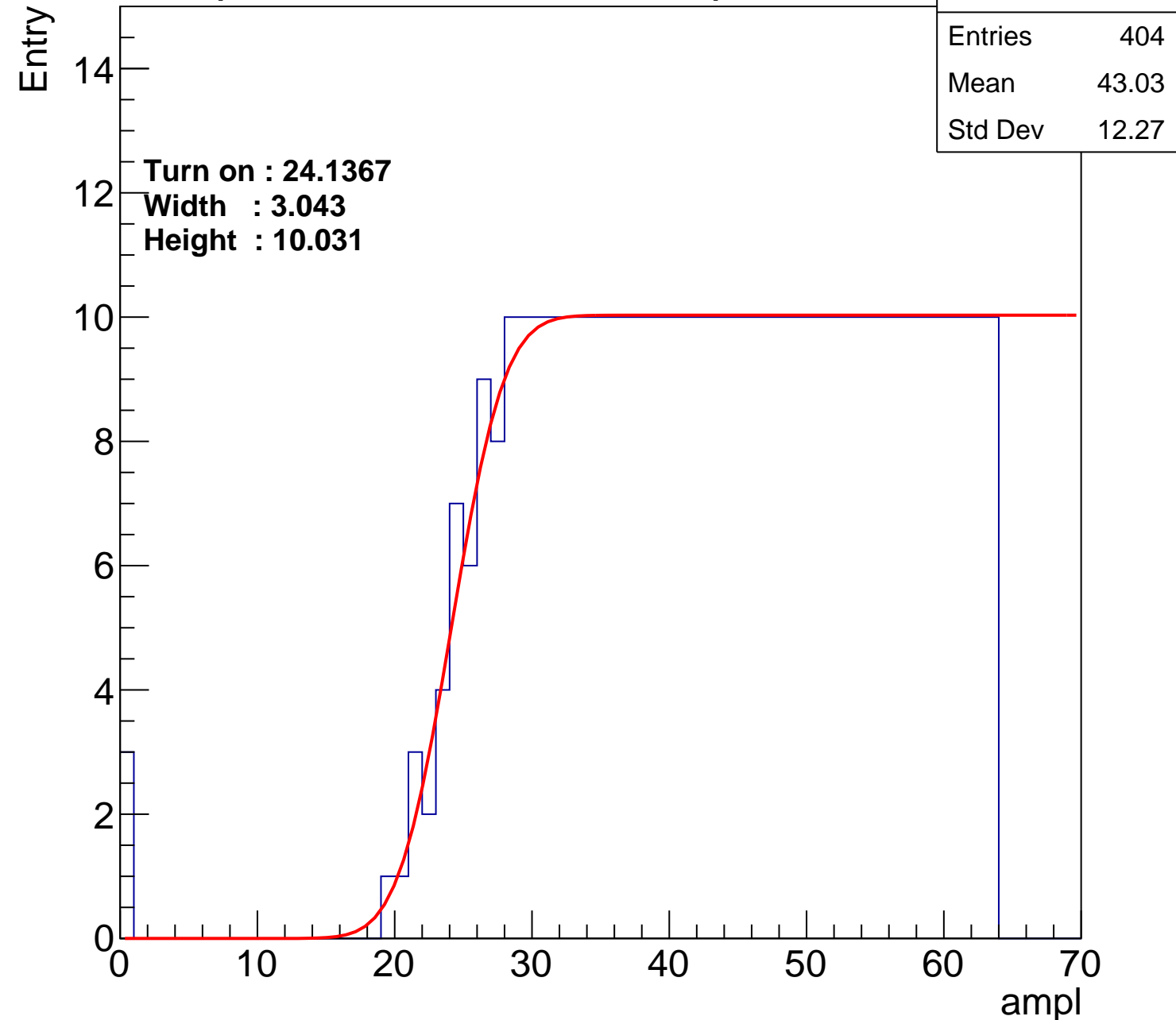
Width : 3.043

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch55

calib_packv5_042523_0143.root, FC#13, port D2

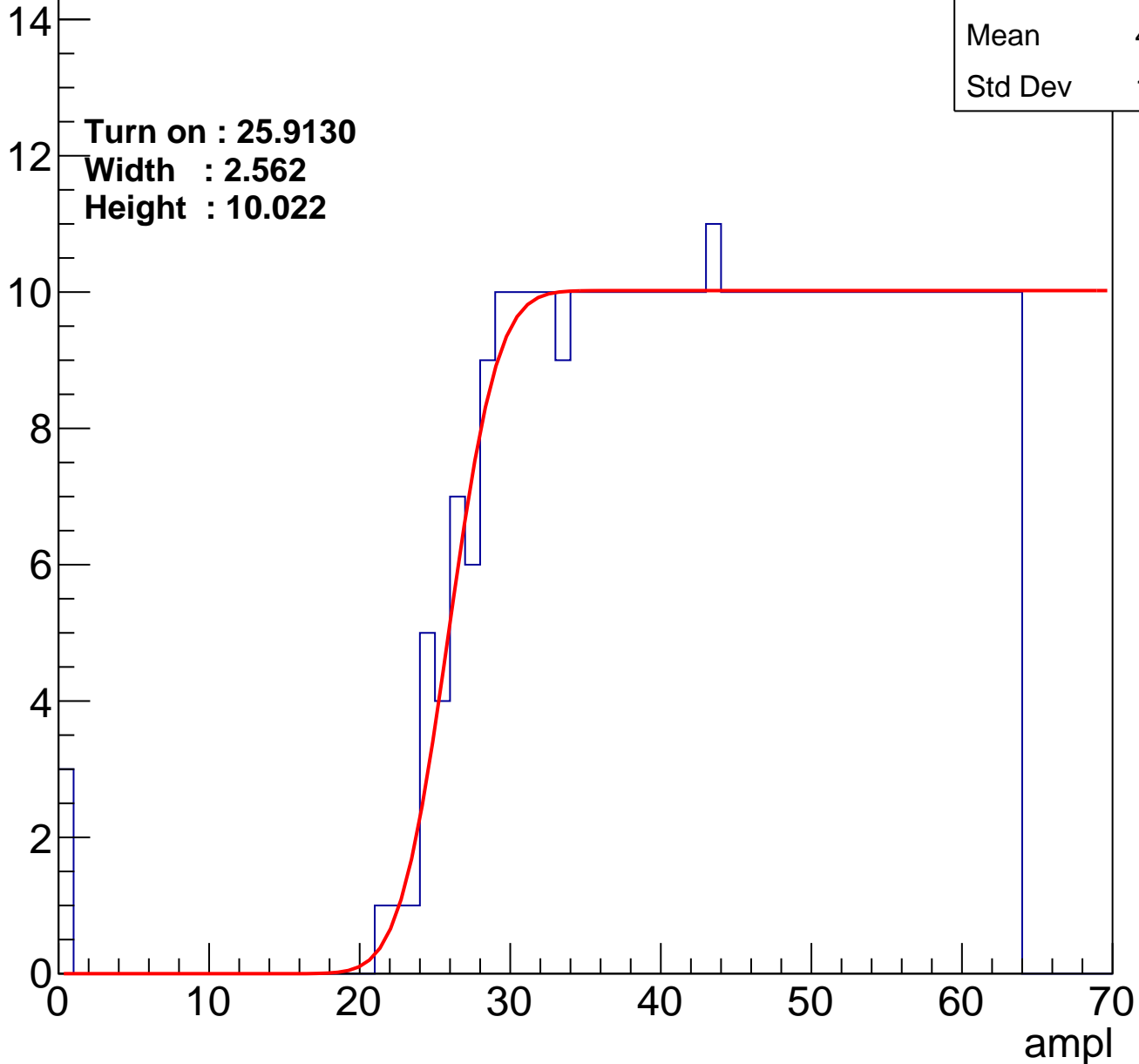
Entries	387
Mean	43.91
Std Dev	11.81

Turn on : 25.9130

Width : 2.562

Height : 10.022

Entry



B1L003S, U19-ch56

calib_packv5_042523_0143.root, FC#13, port D2

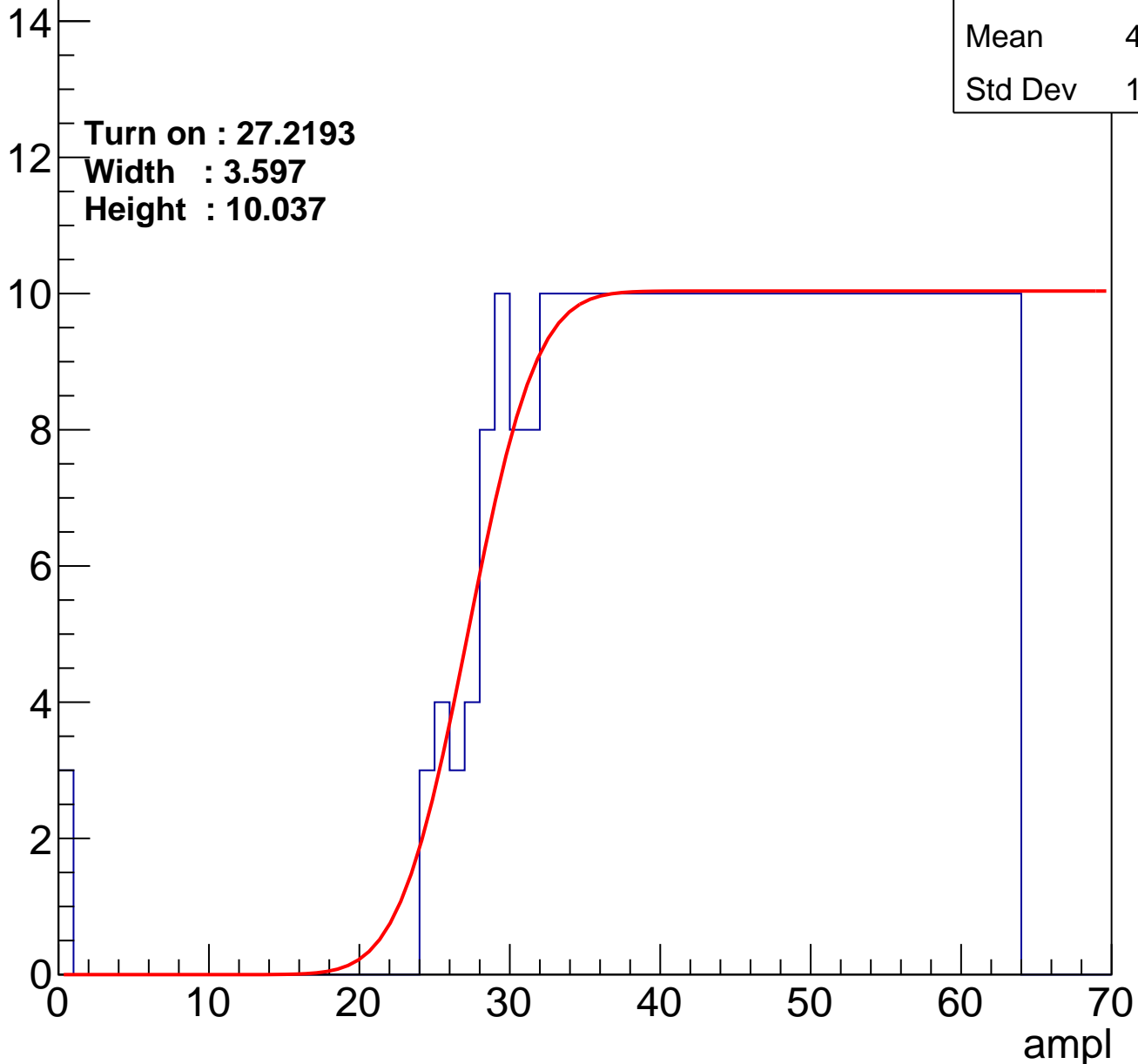
Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 27.2193

Width : 3.597

Height : 10.037

Entry



B1L003S, U19-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.3974

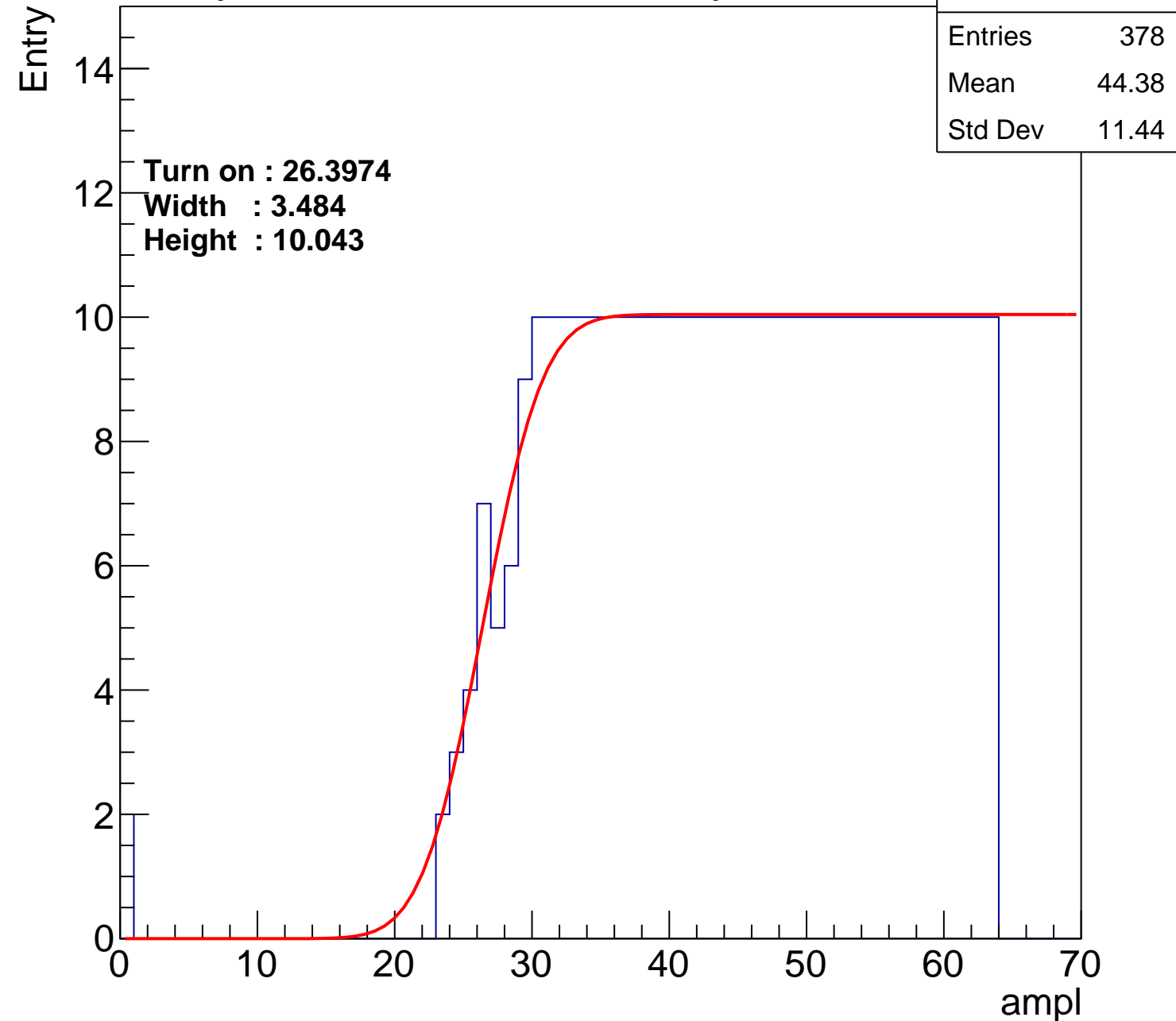
Width : 3.484

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch58

calib_packv5_042523_0143.root, FC#13, port D2

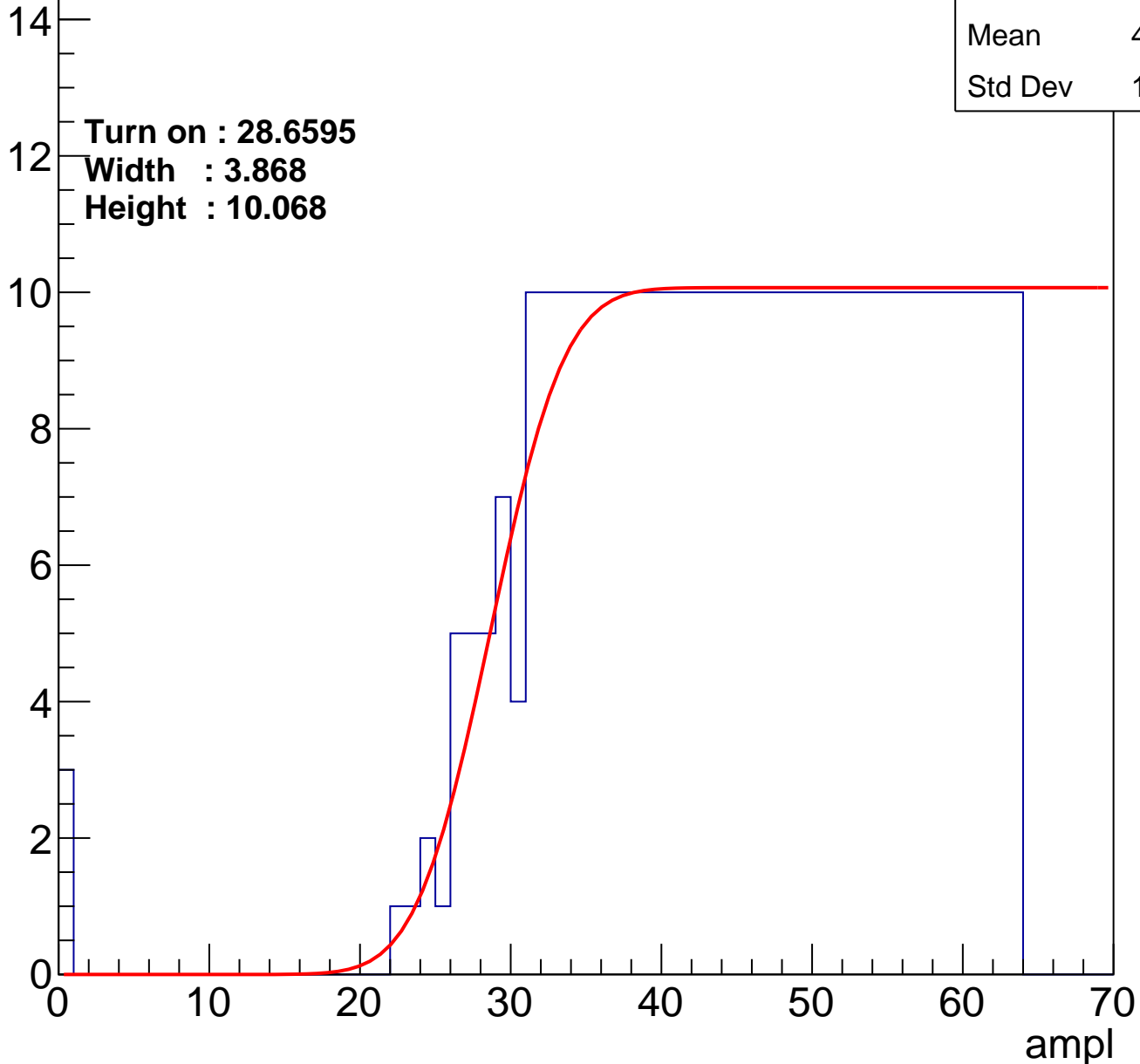
Entries	364
Mean	44.93
Std Dev	11.38

Turn on : 28.6595

Width : 3.868

Height : 10.068

Entry



B1L003S, U19-ch59

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.99
Std Dev	11.74

Turn on : 25.2177

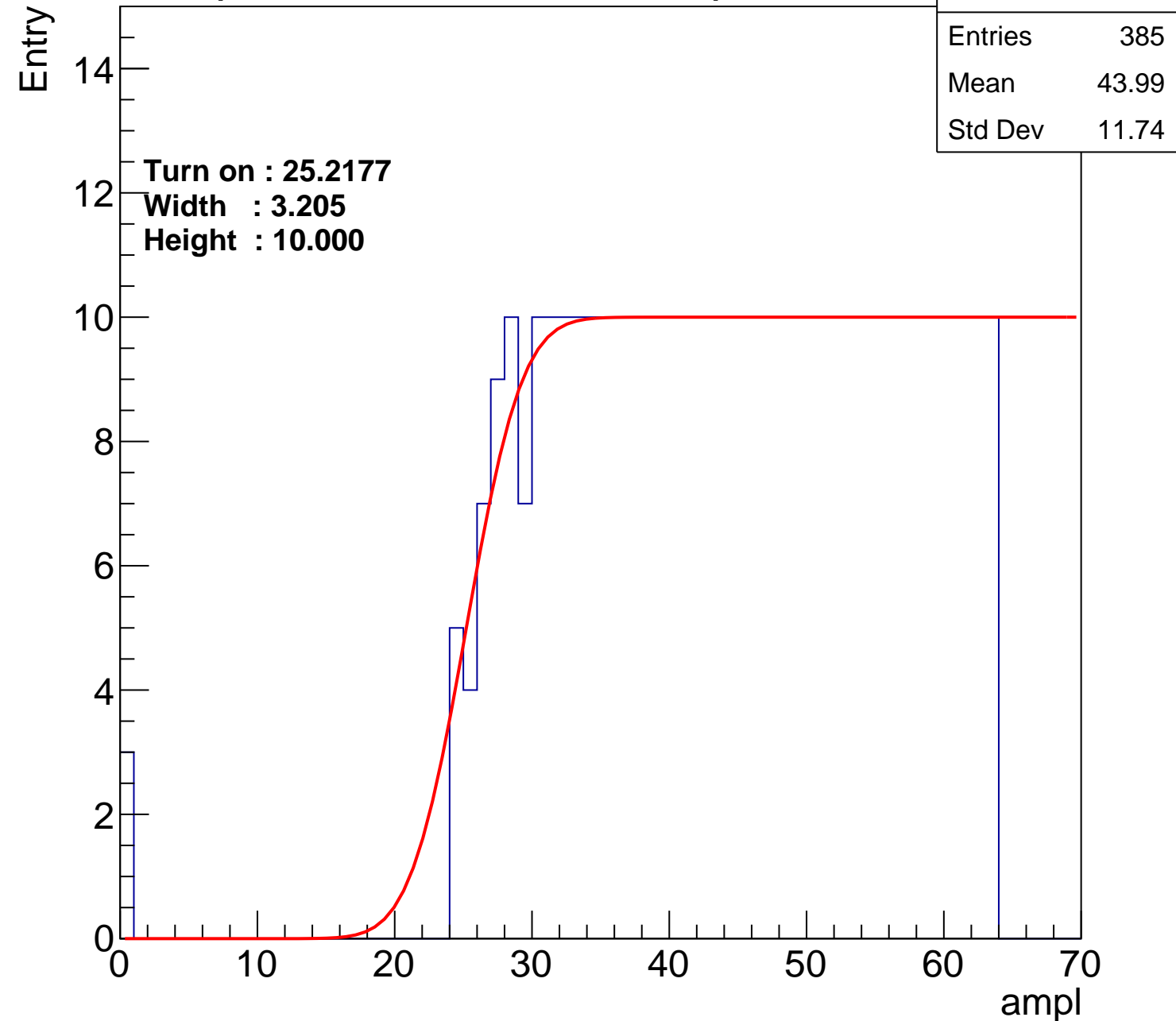
Width : 3.205

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.31
Std Dev	11.3

Turn on : 26.1130

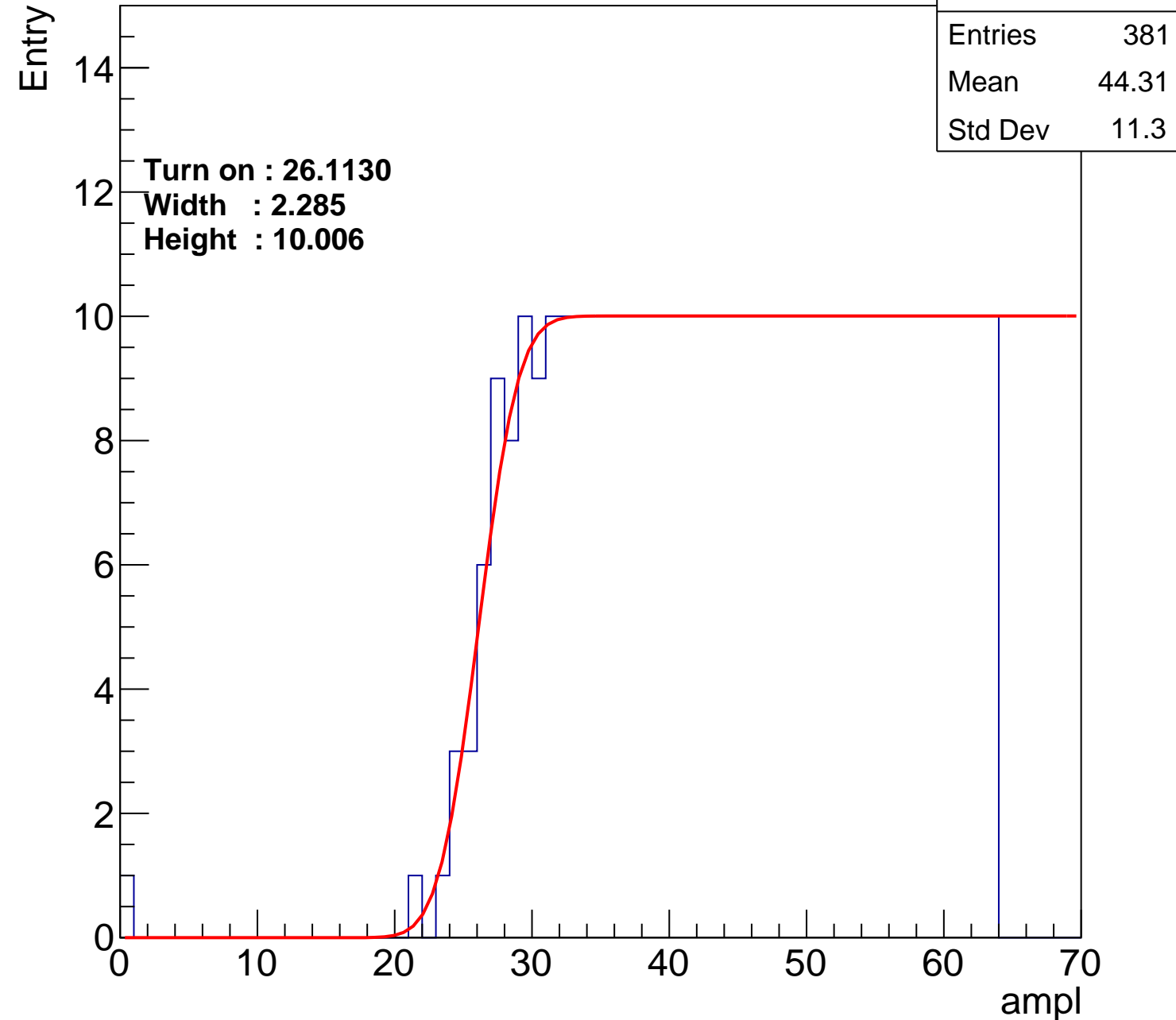
Width : 2.285

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch61

calib_packv5_042523_0143.root, FC#13, port D2

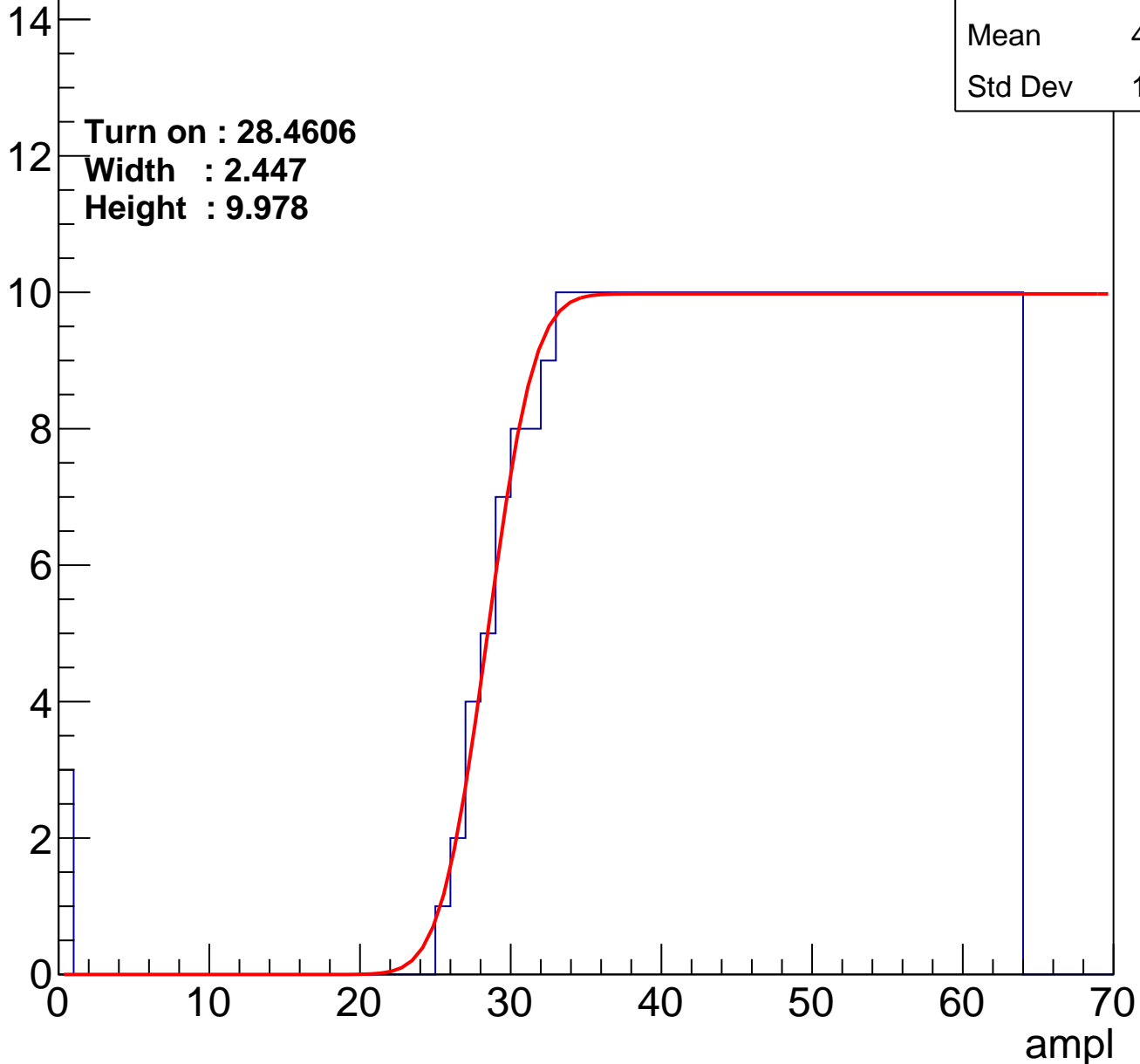
Entries	357
Mean	45.33
Std Dev	11.12

Turn on : 28.4606

Width : 2.447

Height : 9.978

Entry



B1L003S, U19-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.56
Std Dev	11.54

Turn on : 26.9719

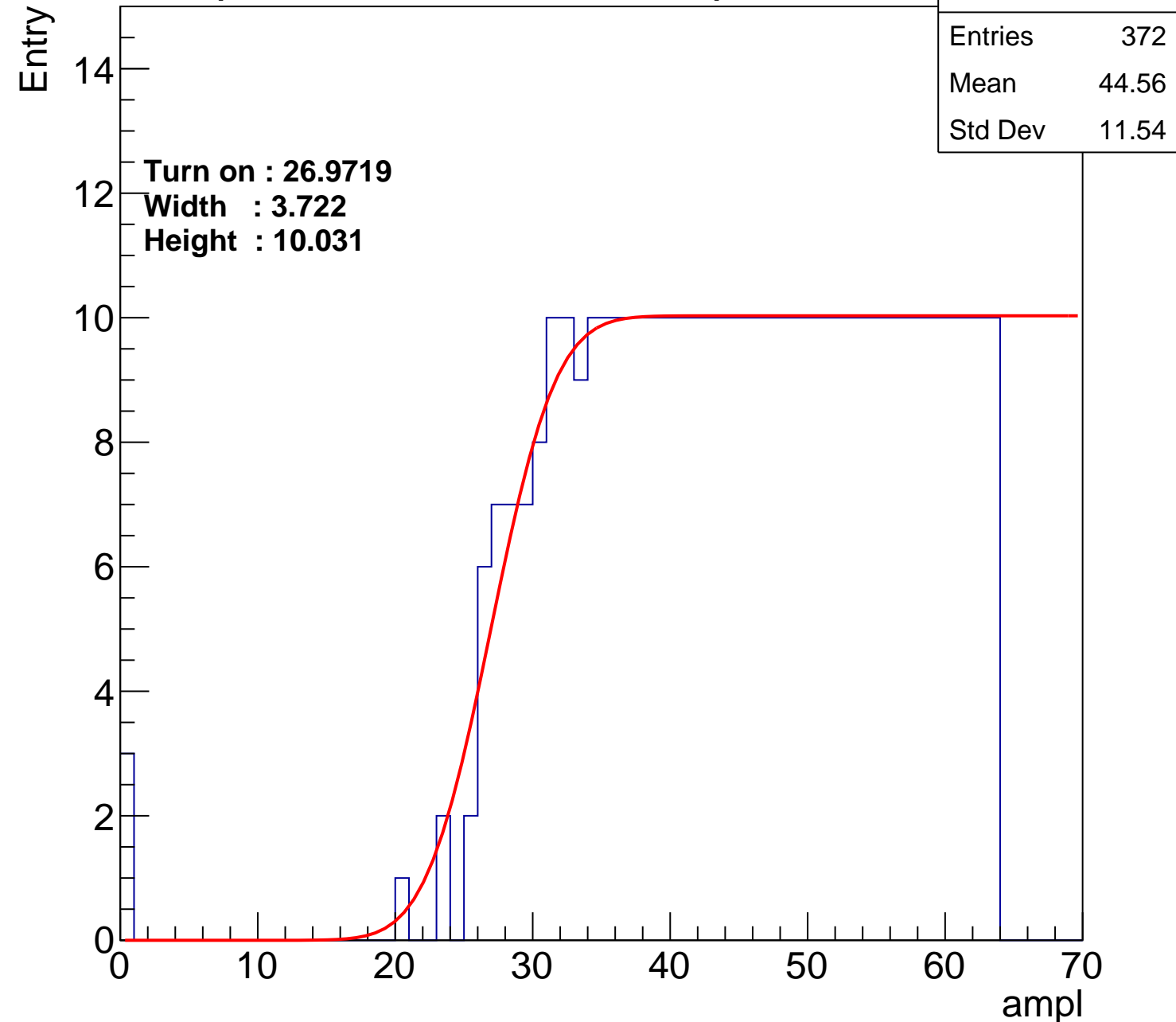
Width : 3.722

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch63

calib_packv5_042523_0143.root, FC#13, port D2

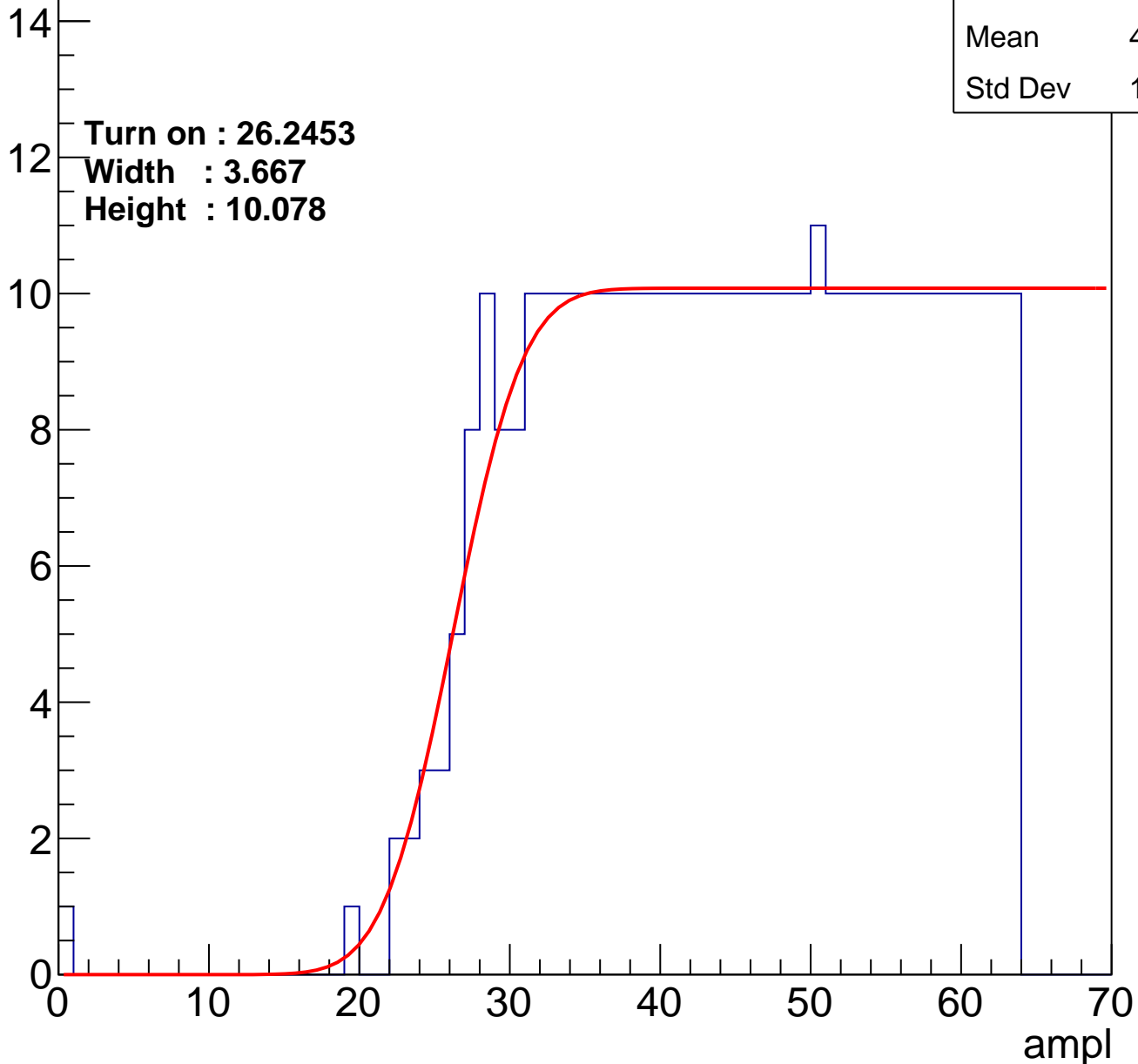
Entries	382
Mean	44.28
Std Dev	11.38

Turn on : 26.2453

Width : 3.667

Height : 10.078

Entry



B1L003S, U19-ch64

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.62
Std Dev	11.51

Turn on : 27.0605

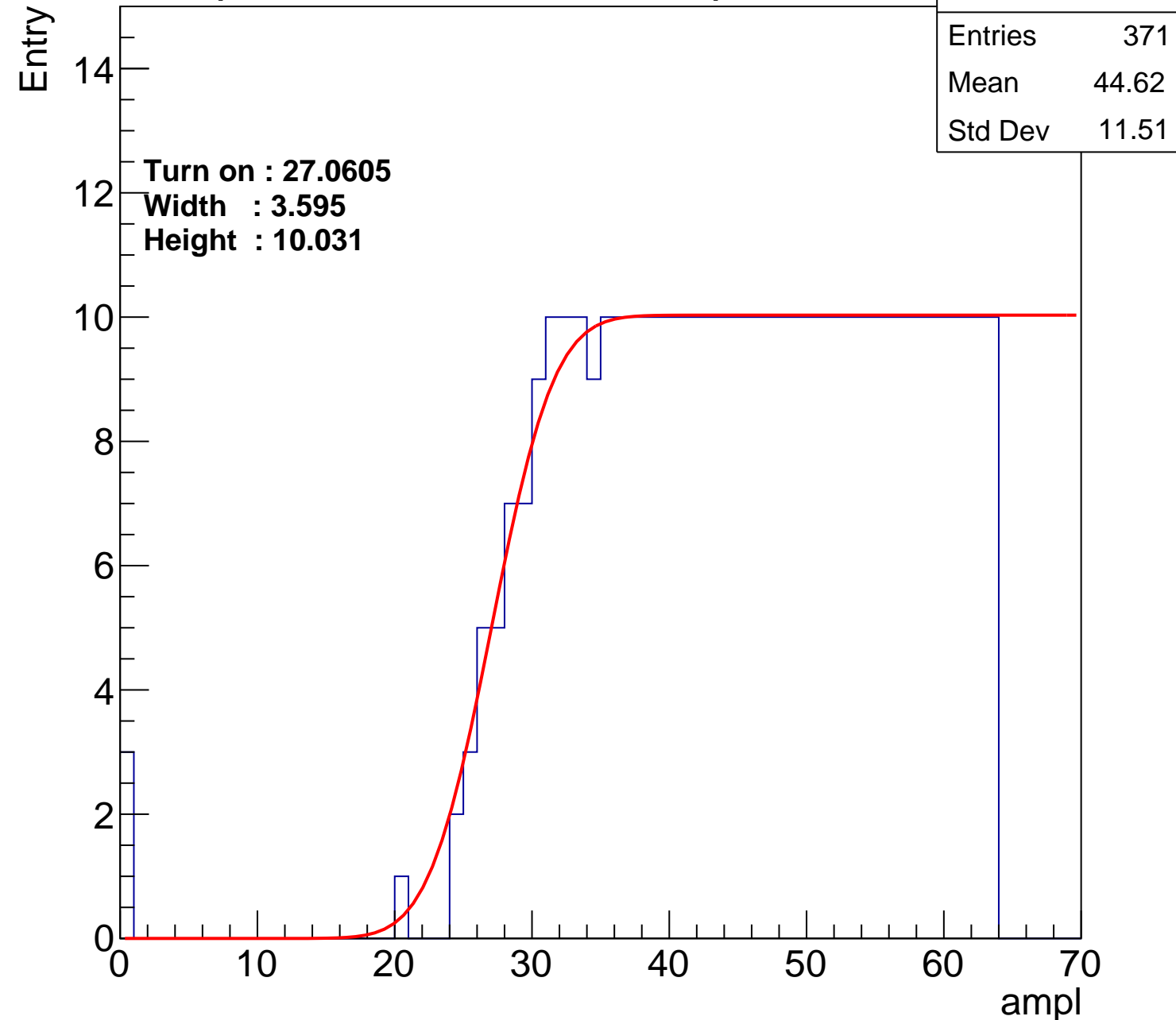
Width : 3.595

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.4
Std Dev	11.59

Turn on : 26.9381

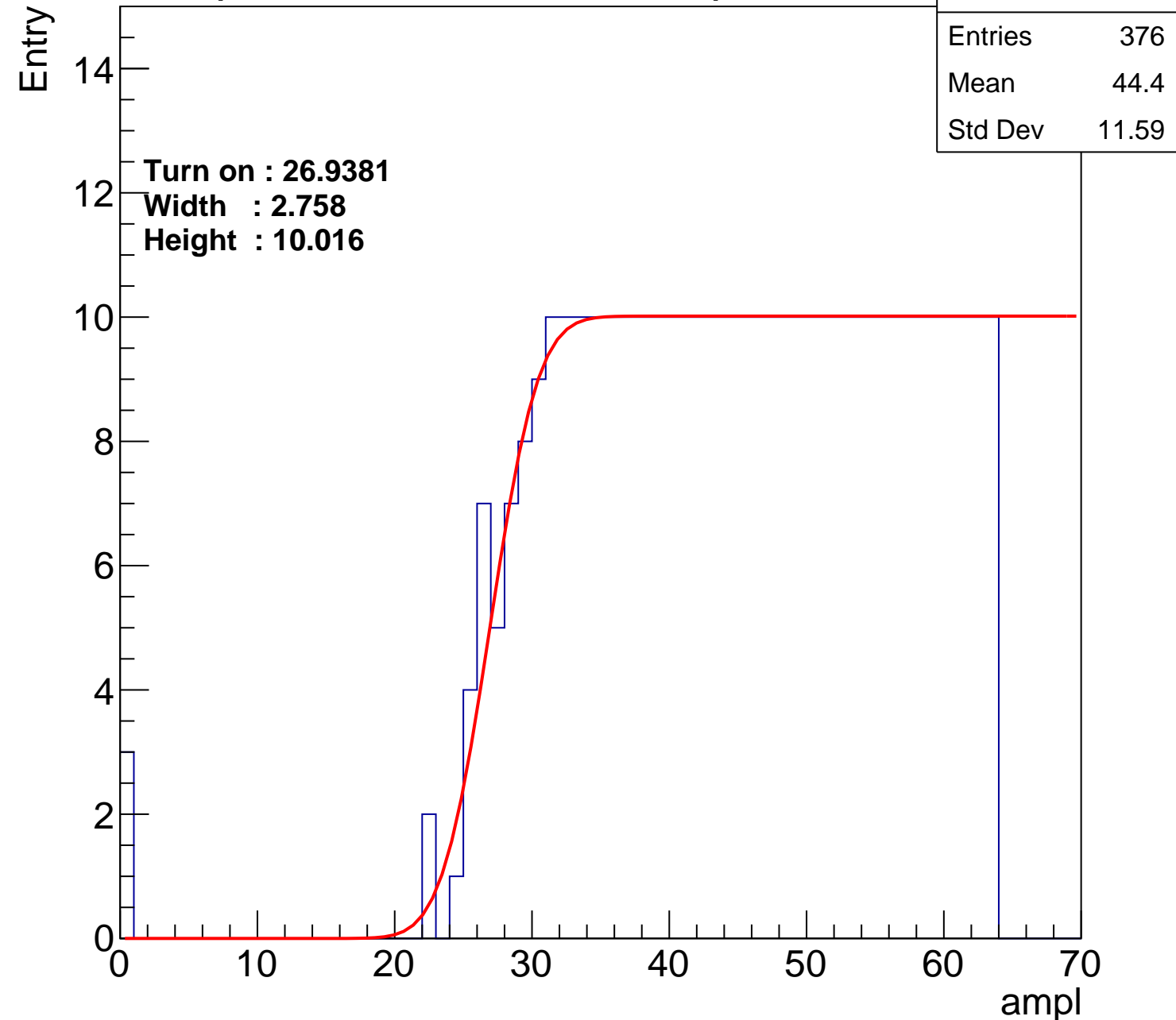
Width : 2.758

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch66

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.7014

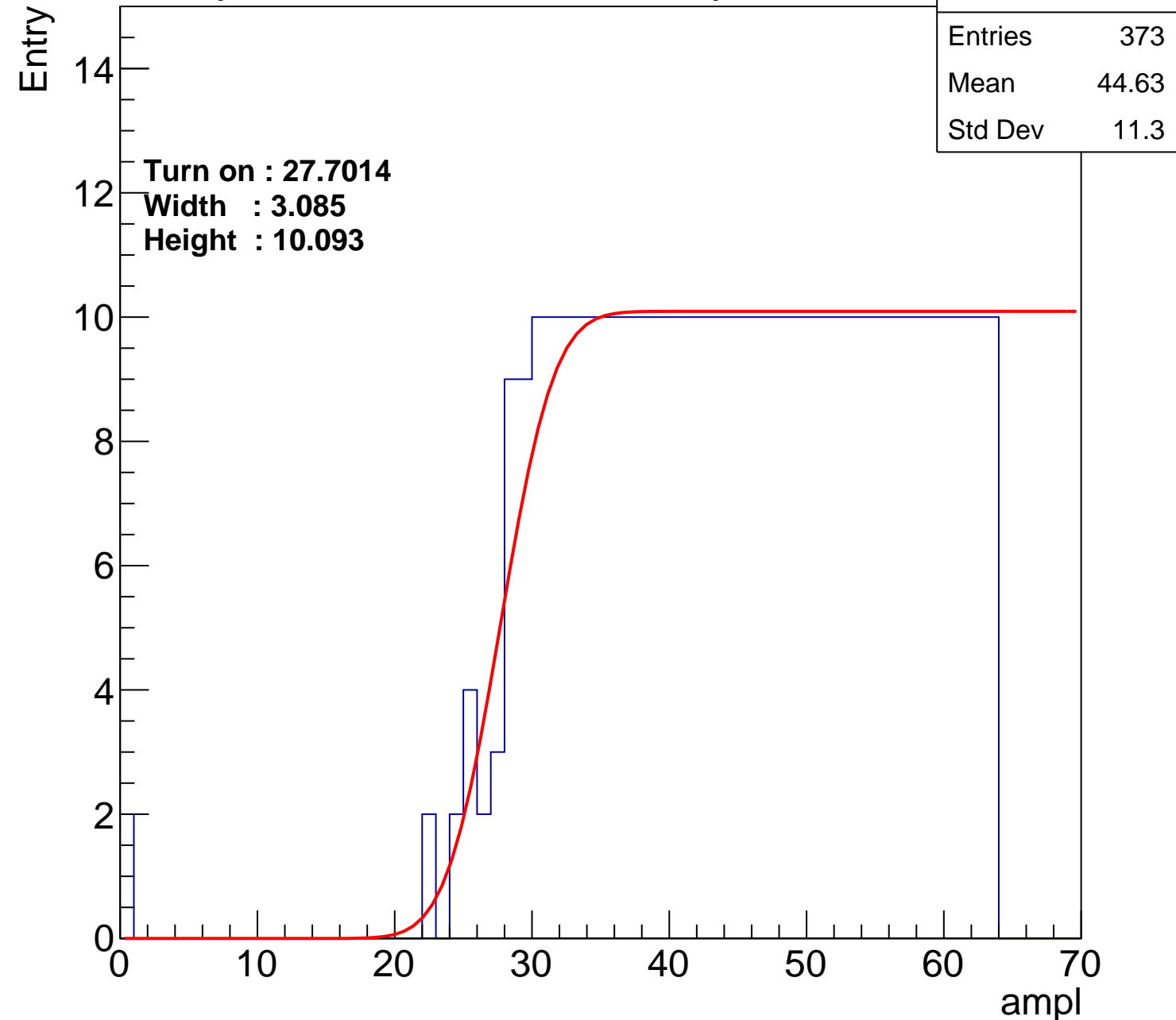
Width : 3.085

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch67

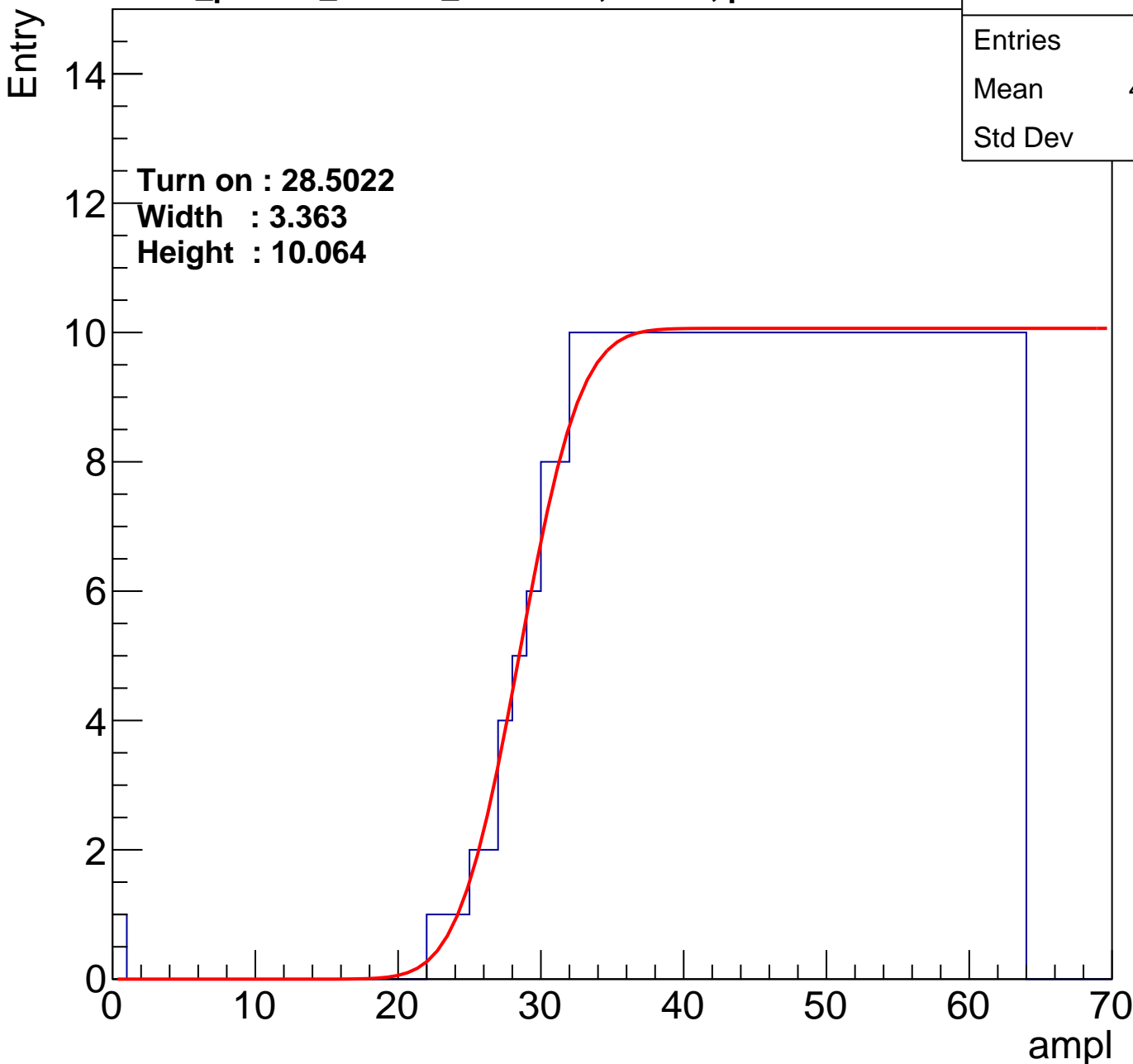
calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.35
Std Dev	10.8

Turn on : 28.5022

Width : 3.363

Height : 10.064



B1L003S, U19-ch68

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.24
Std Dev	11.55

Turn on : 26.4947

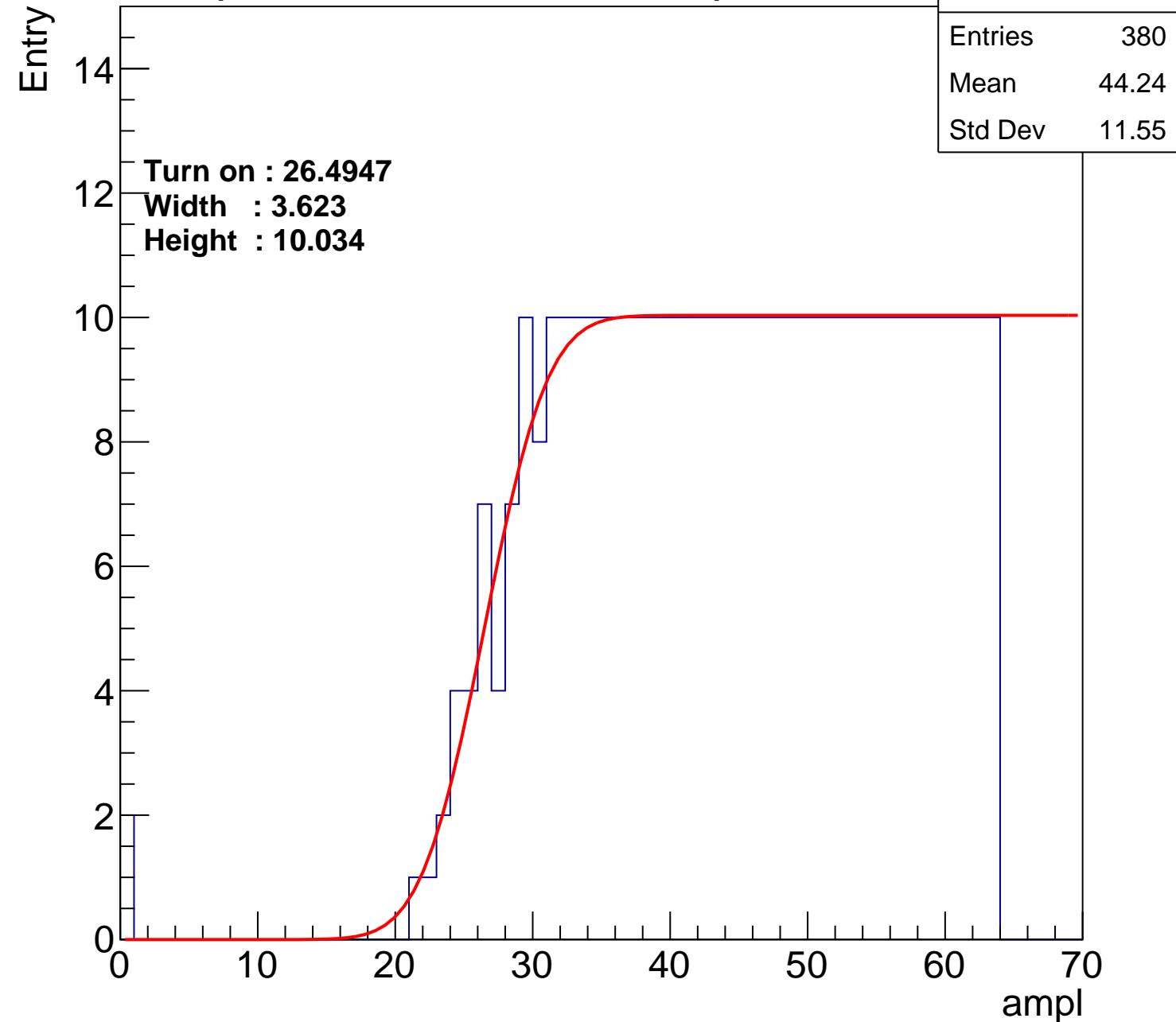
Width : 3.623

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch69

calib_packv5_042523_0143.root, FC#13, port D2

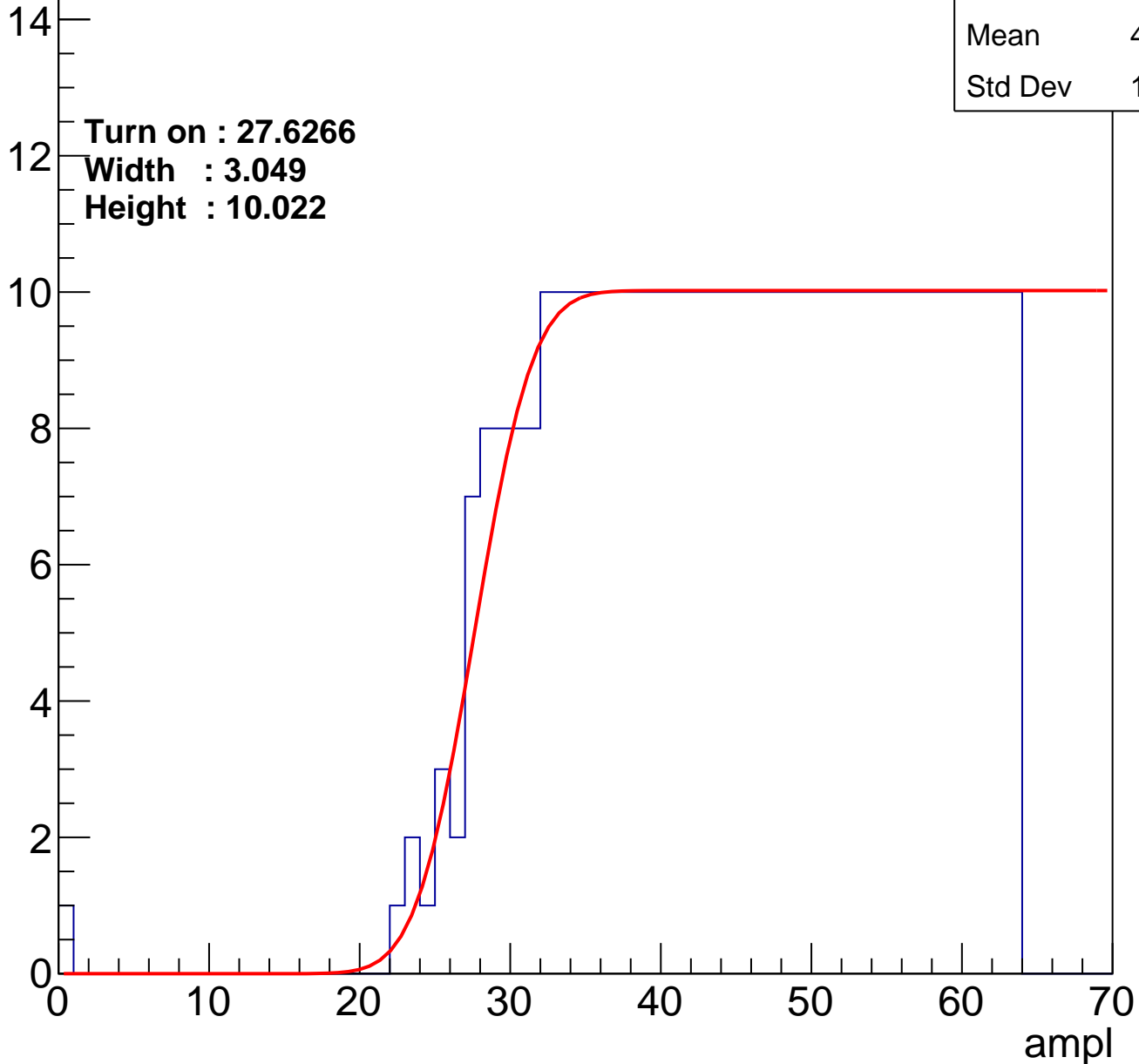
Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.6266

Width : 3.049

Height : 10.022

Entry



B1L003S, U19-ch70

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 27.2534

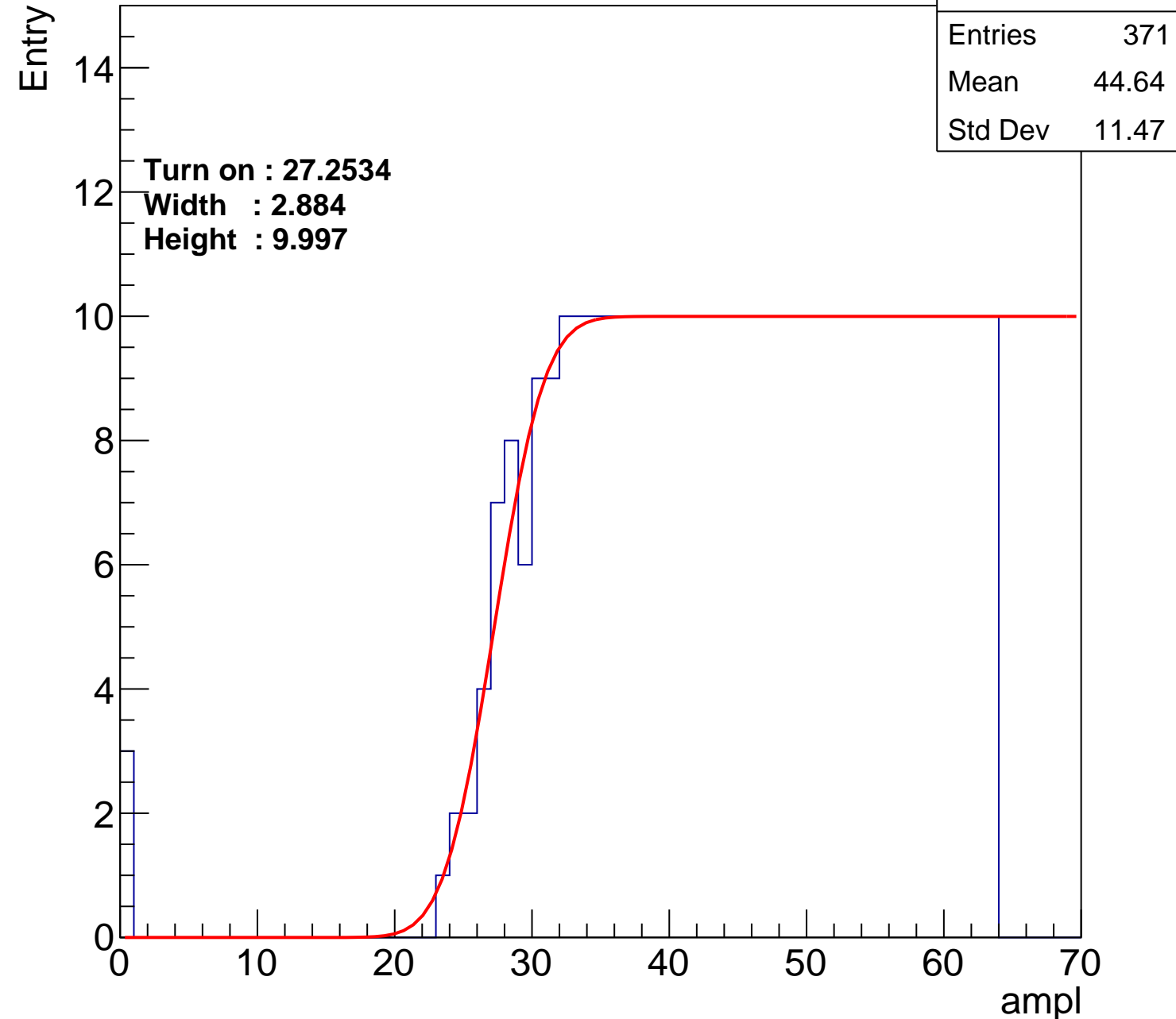
Width : 2.884

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch71

calib_packv5_042523_0143.root, FC#13, port D2

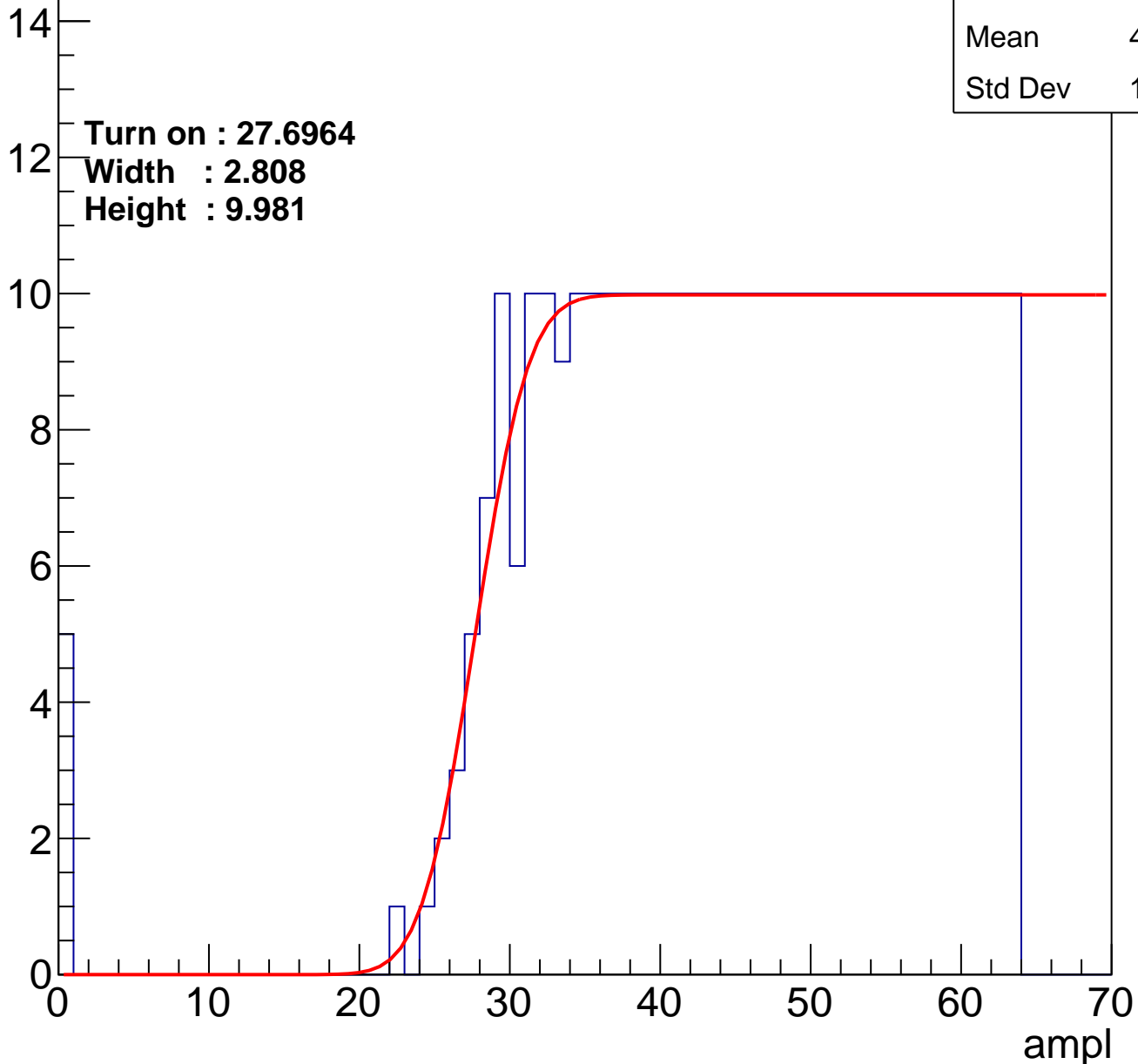
Entry

Entries	369
Mean	44.59
Std Dev	11.82

Turn on : 27.6964

Width : 2.808

Height : 9.981



B1L003S, U19-ch72

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.9140

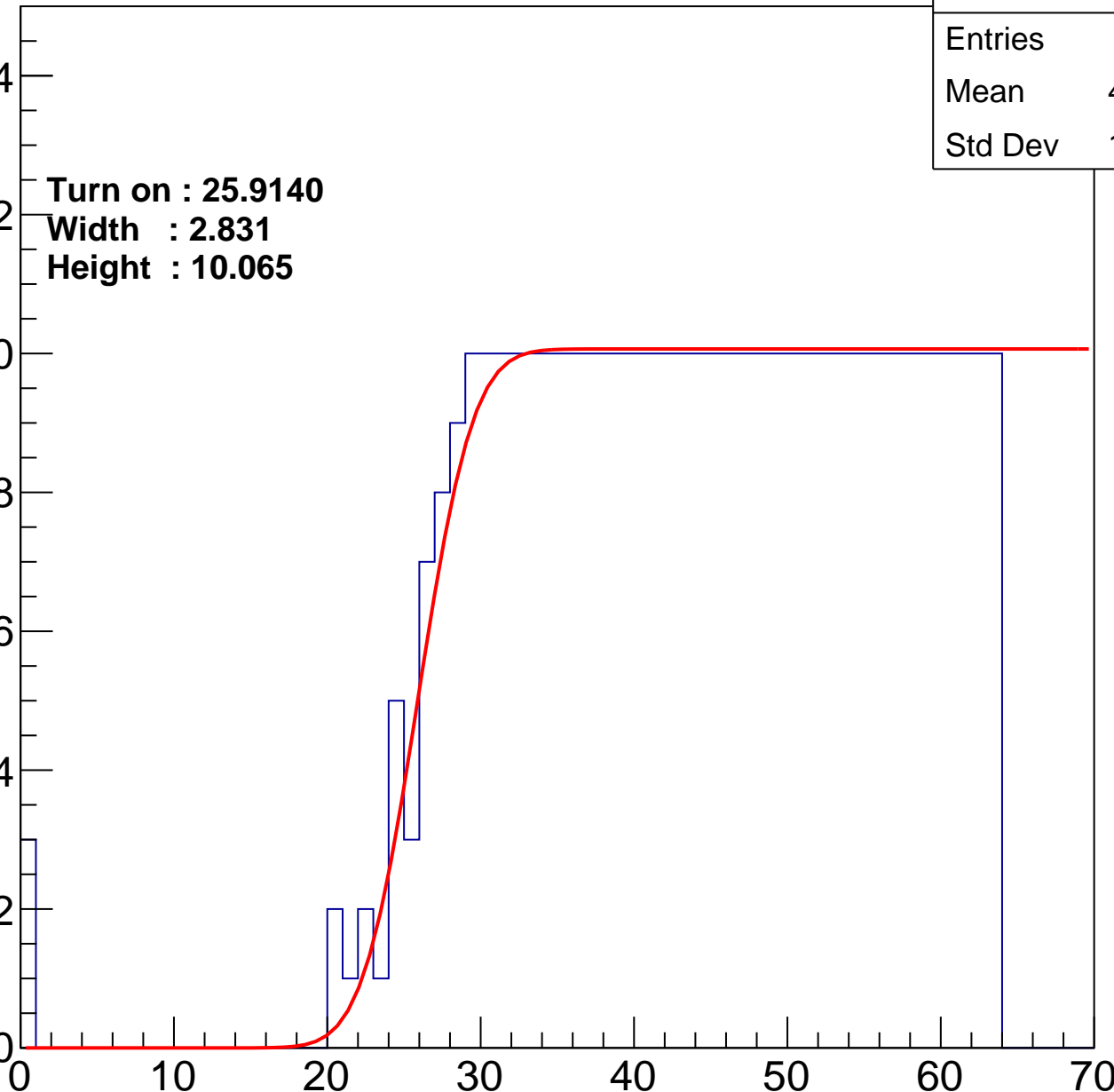
Width : 2.831

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch73

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.1
Std Dev	11.28

Turn on : 28.8369

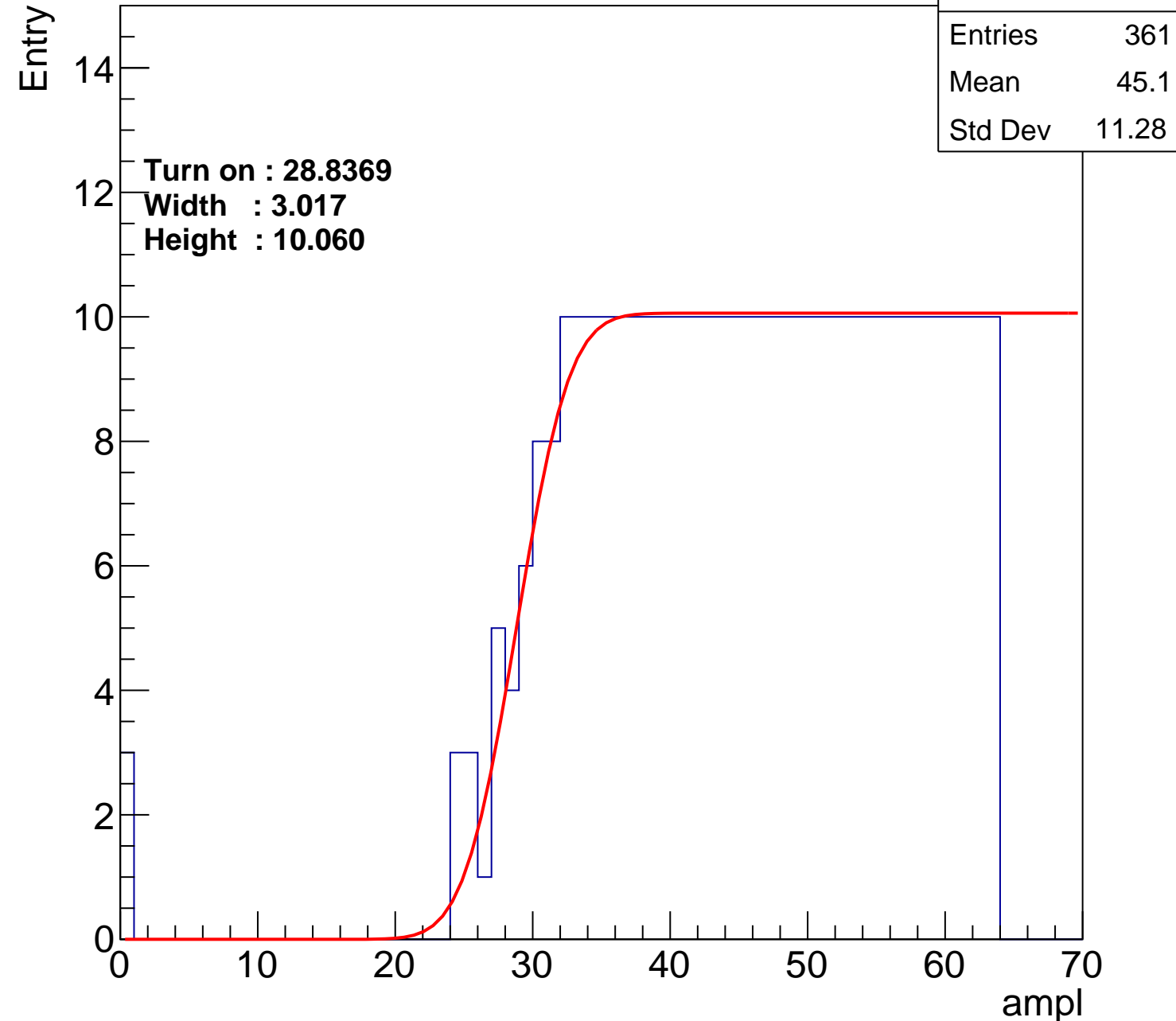
Width : 3.017

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch74

calib_packv5_042523_0143.root, FC#13, port D2

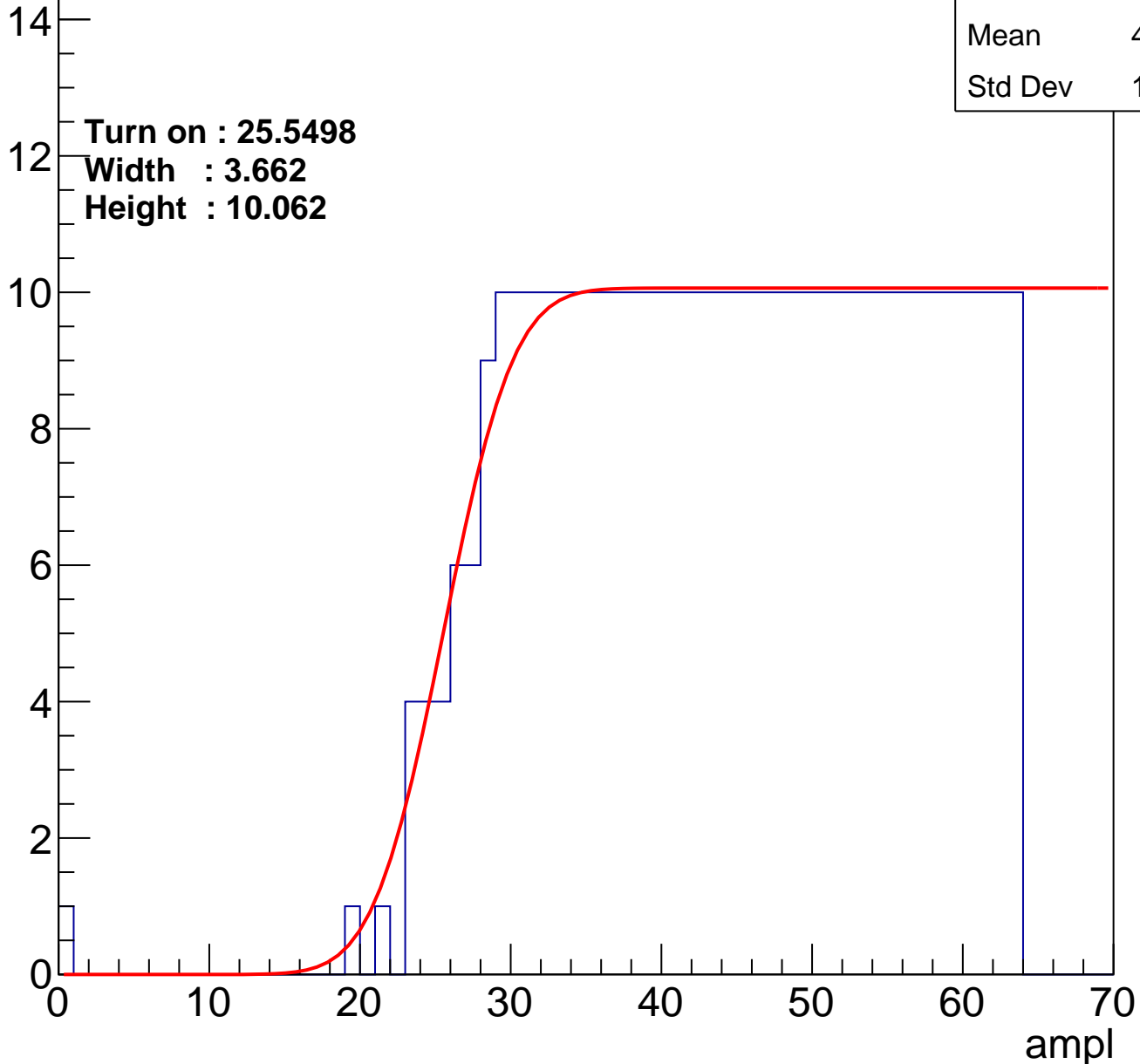
Entries	386
Mean	44.04
Std Dev	11.49

Turn on : 25.5498

Width : 3.662

Height : 10.062

Entry



B1L003S, U19-ch75

calib_packv5_042523_0143.root, FC#13, port D2

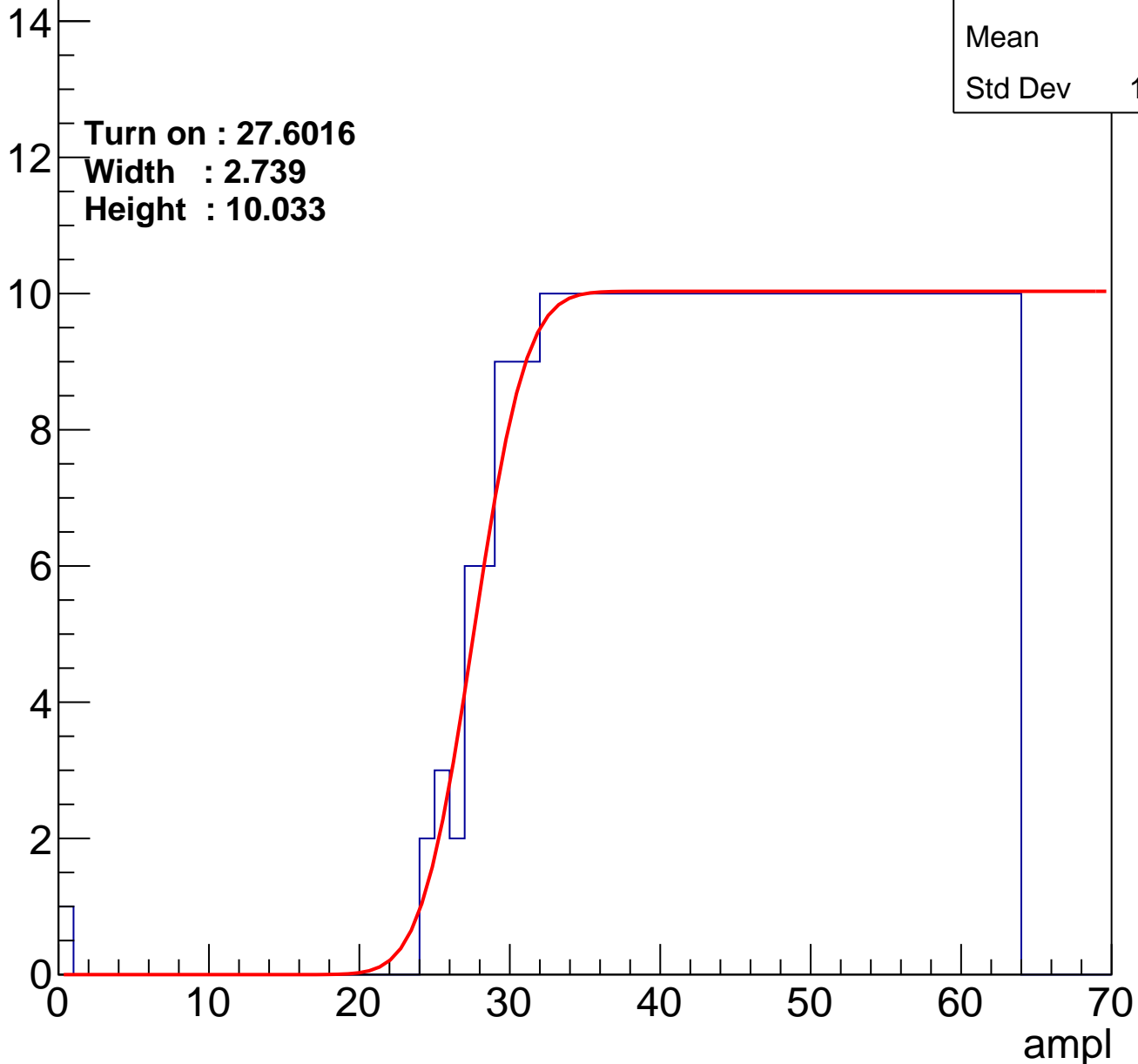
Entries	367
Mean	45
Std Dev	10.93

Turn on : 27.6016

Width : 2.739

Height : 10.033

Entry



B1L003S, U19-ch76

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.63
Std Dev	11.34

Turn on : 26.7729

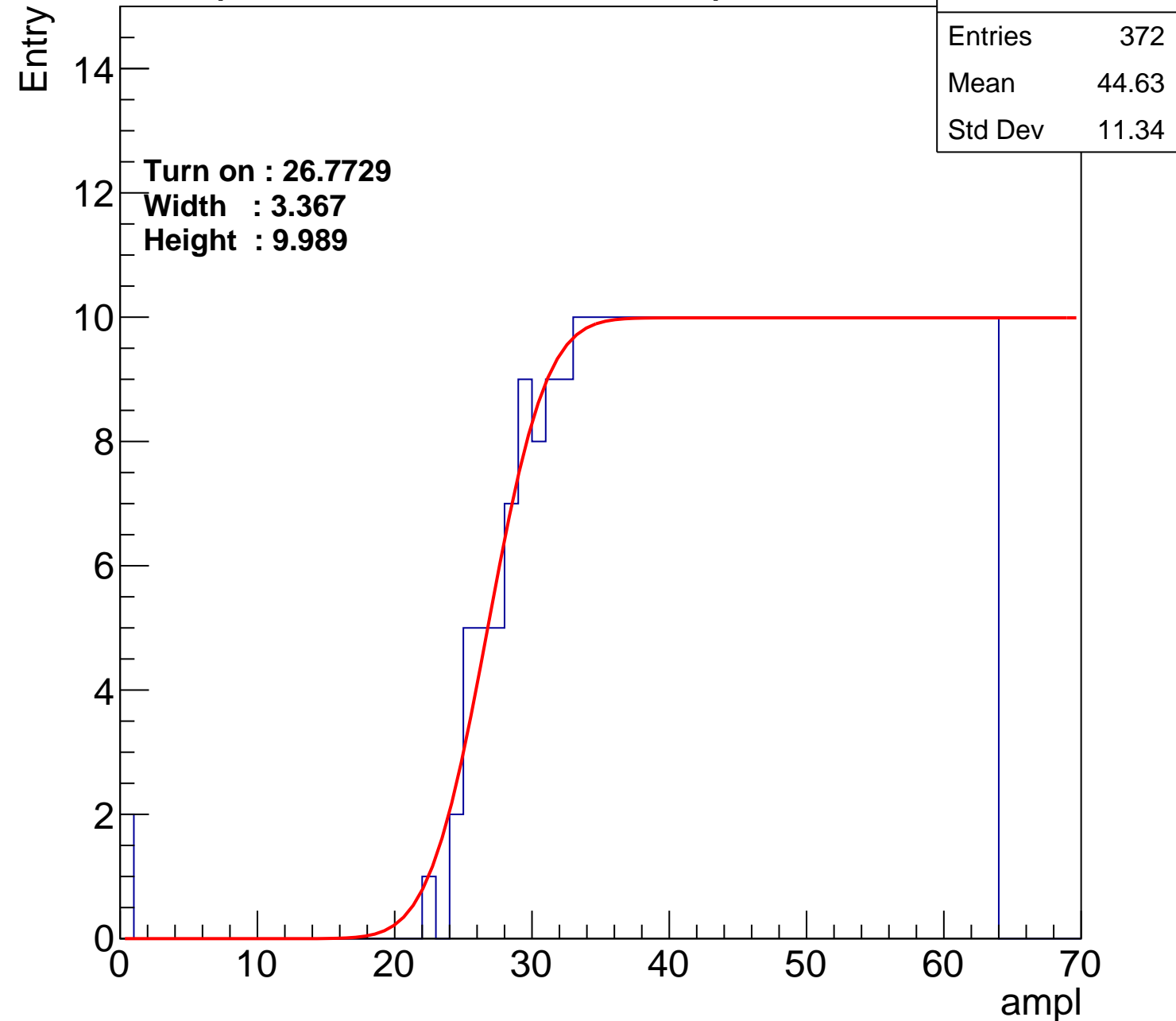
Width : 3.367

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch77

calib_packv5_042523_0143.root, FC#13, port D2

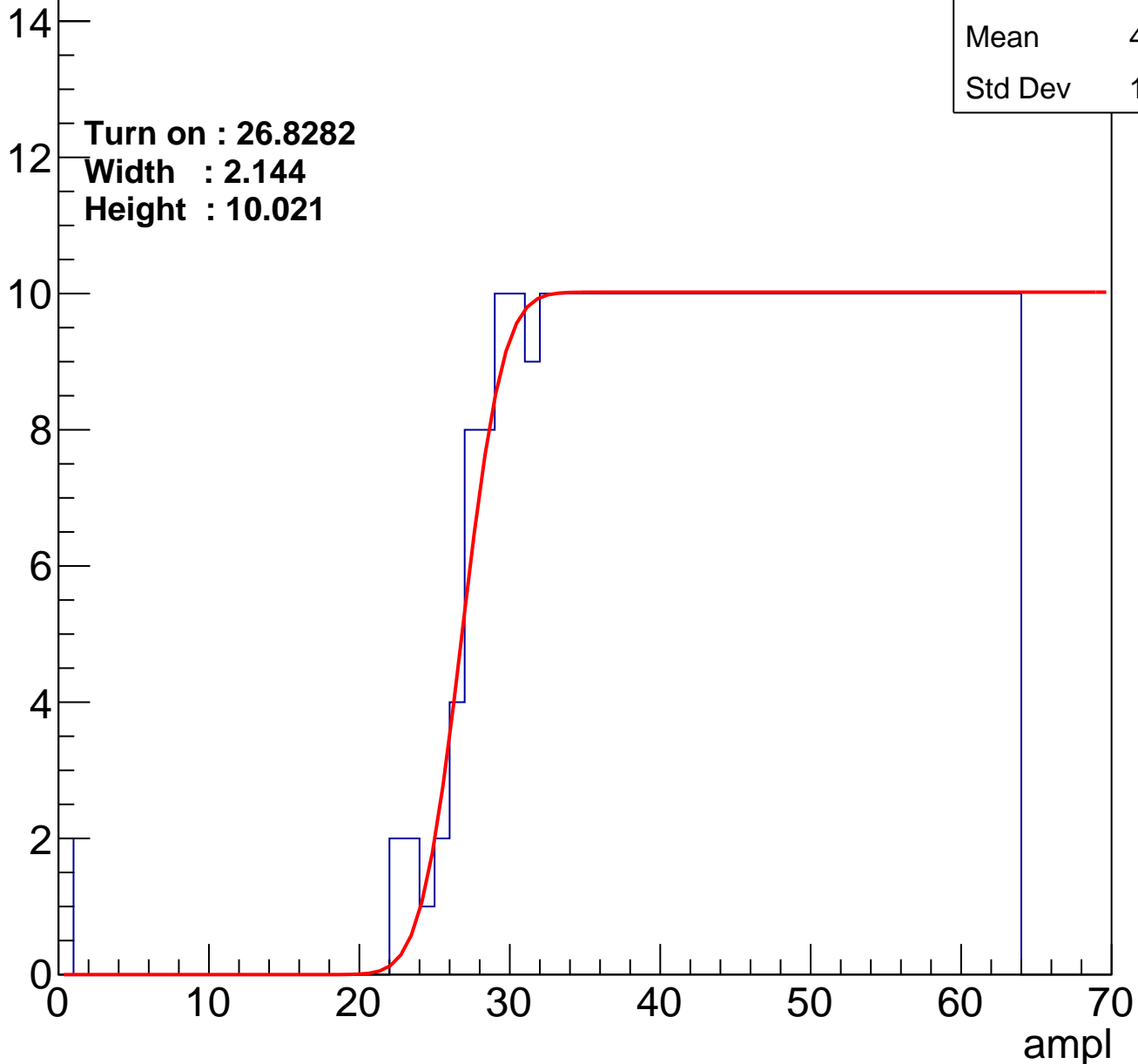
Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.8282

Width : 2.144

Height : 10.021

Entry



B1L003S, U19-ch78

calib_packv5_042523_0143.root, FC#13, port D2

Entries	395
Mean	43.53
Std Dev	11.89

Turn on : 25.4304

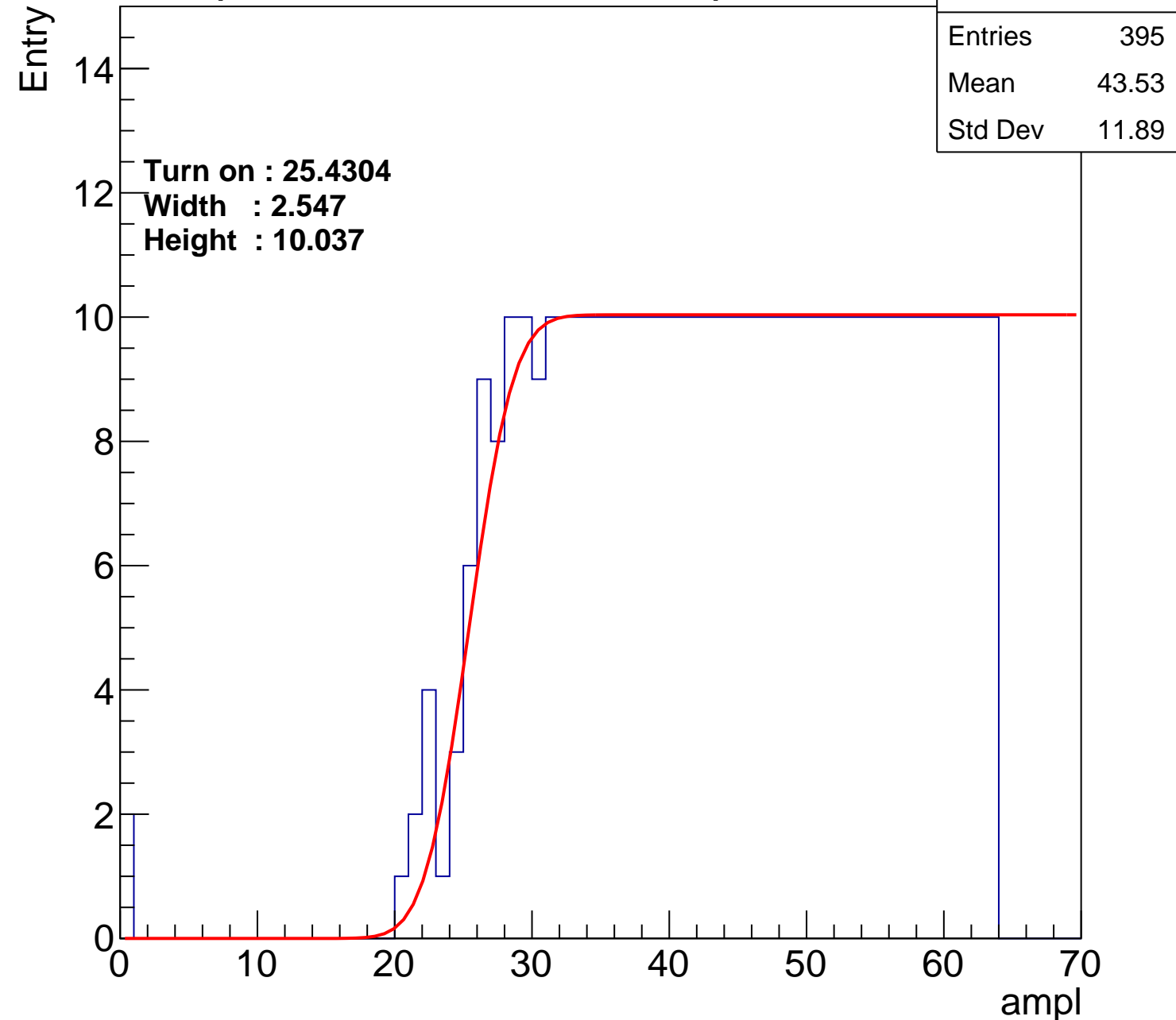
Width : 2.547

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 26.9946

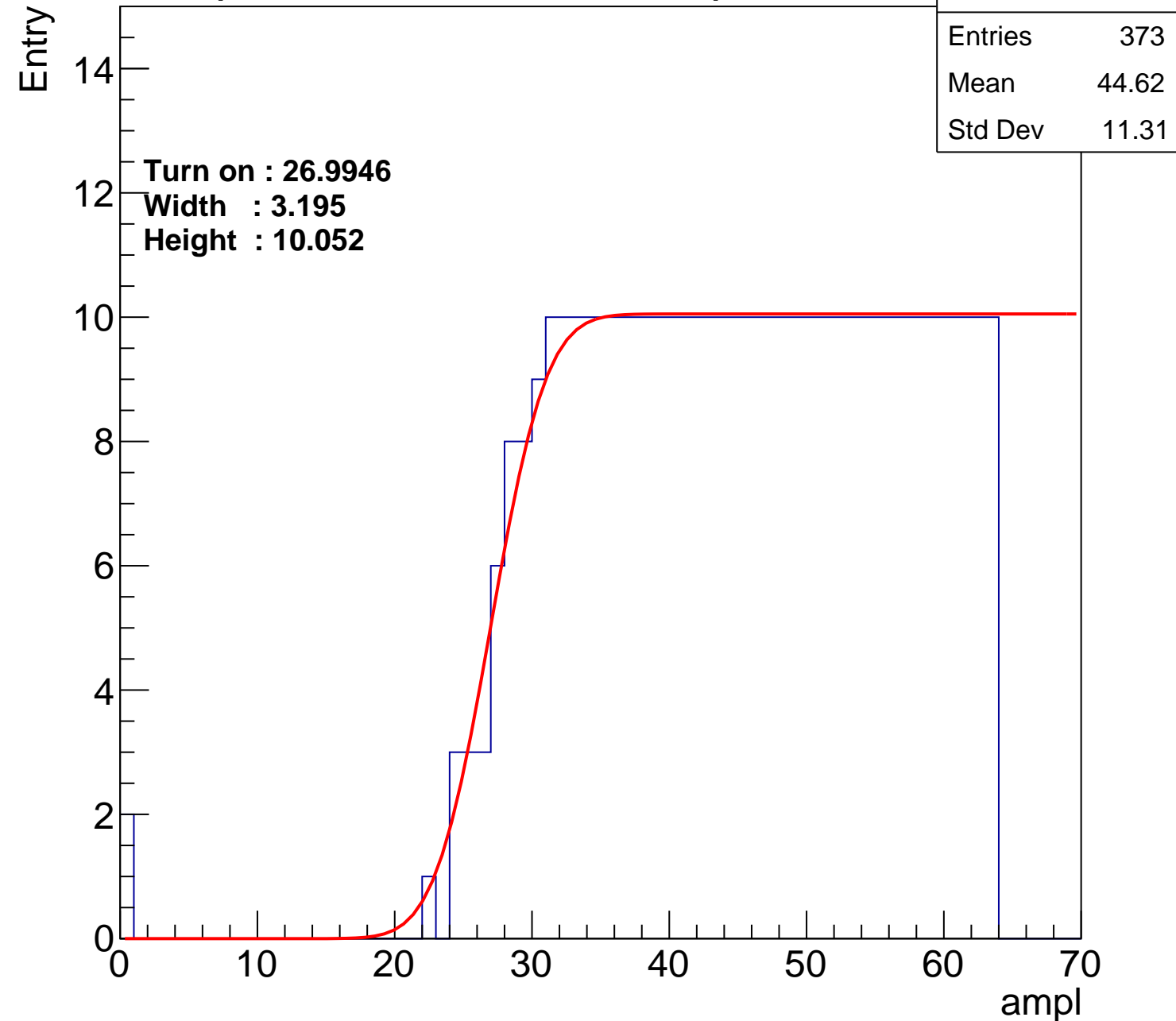
Width : 3.195

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch80

calib_packv5_042523_0143.root, FC#13, port D2

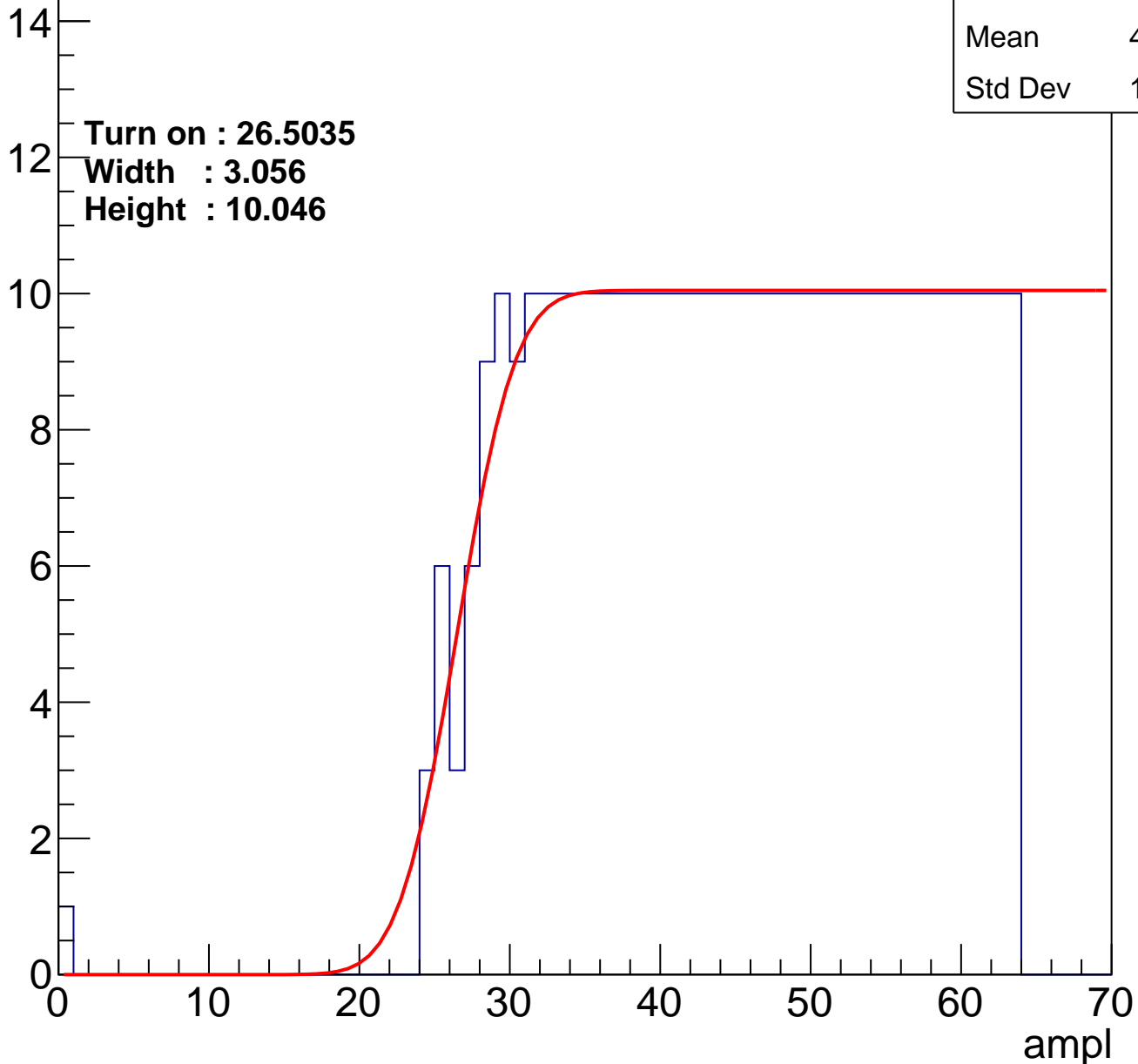
Entries	377
Mean	44.52
Std Dev	11.18

Turn on : 26.5035

Width : 3.056

Height : 10.046

Entry



B1L003S, U19-ch81

calib_packv5_042523_0143.root, FC#13, port D2

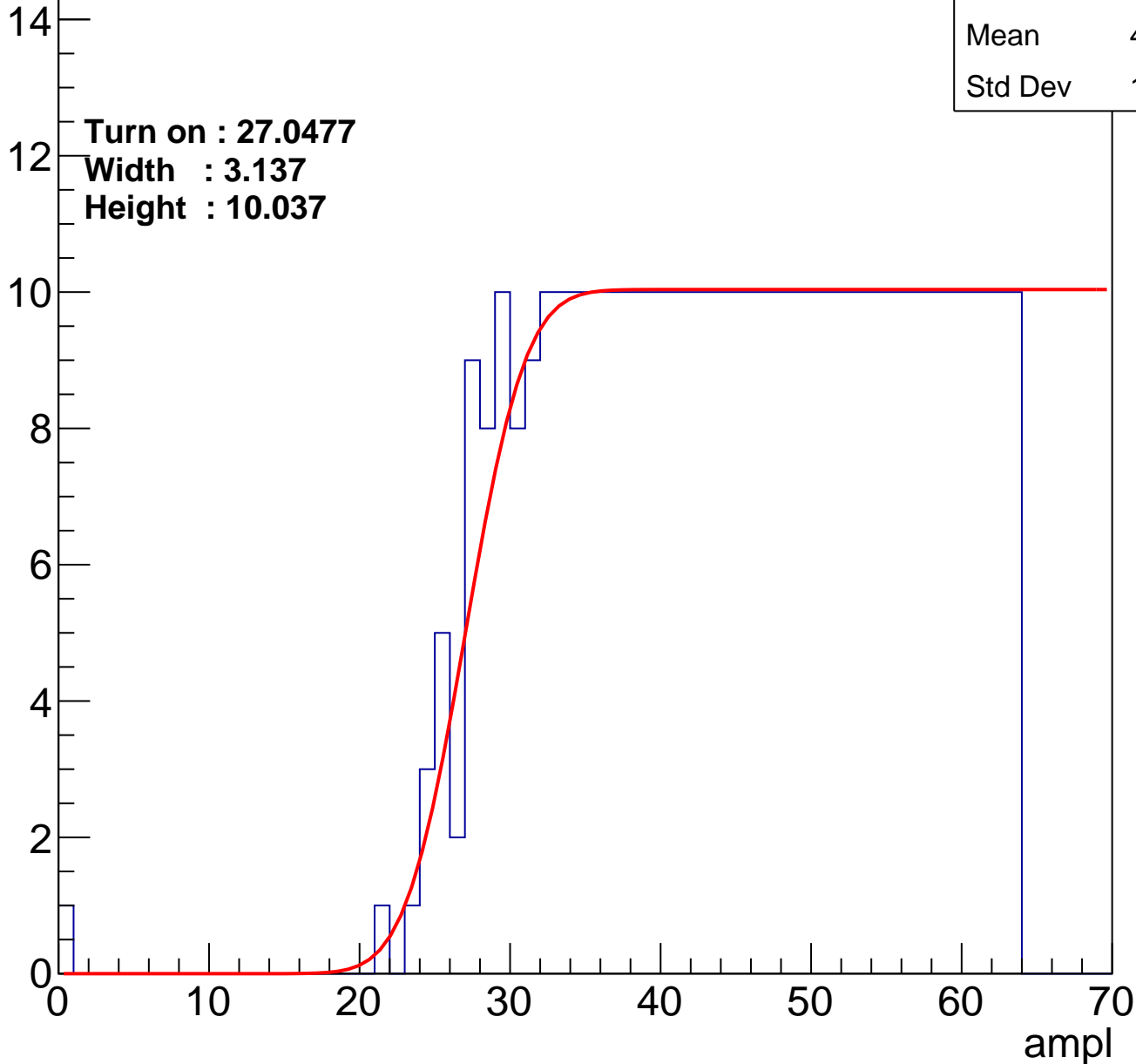
Entries	377
Mean	44.48
Std Dev	11.24

Turn on : 27.0477

Width : 3.137

Height : 10.037

Entry



B1L003S, U19-ch82

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.64
Std Dev	11.82

Turn on : 25.1248

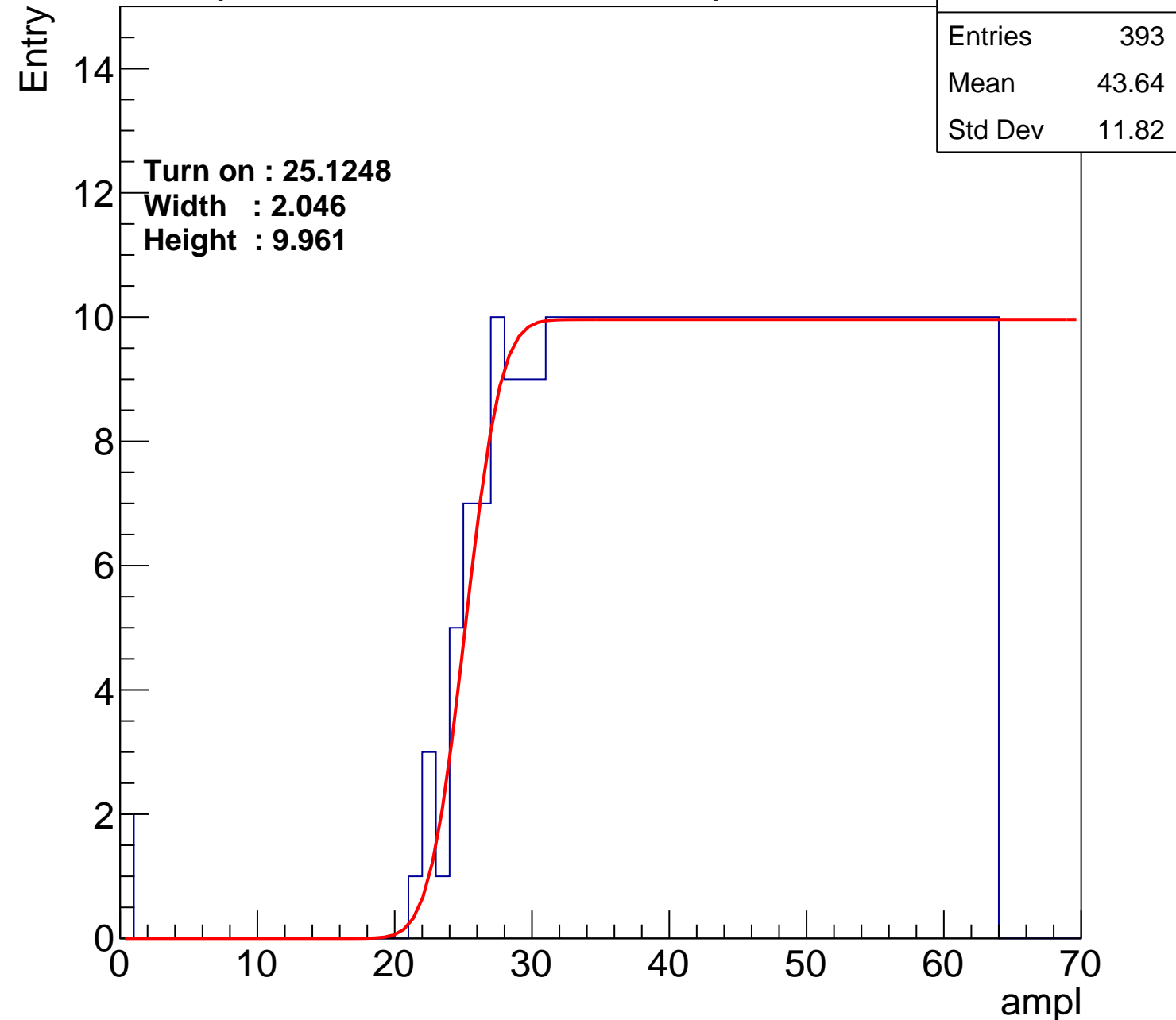
Width : 2.046

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.07
Std Dev	12.04

Turn on : 26.1942

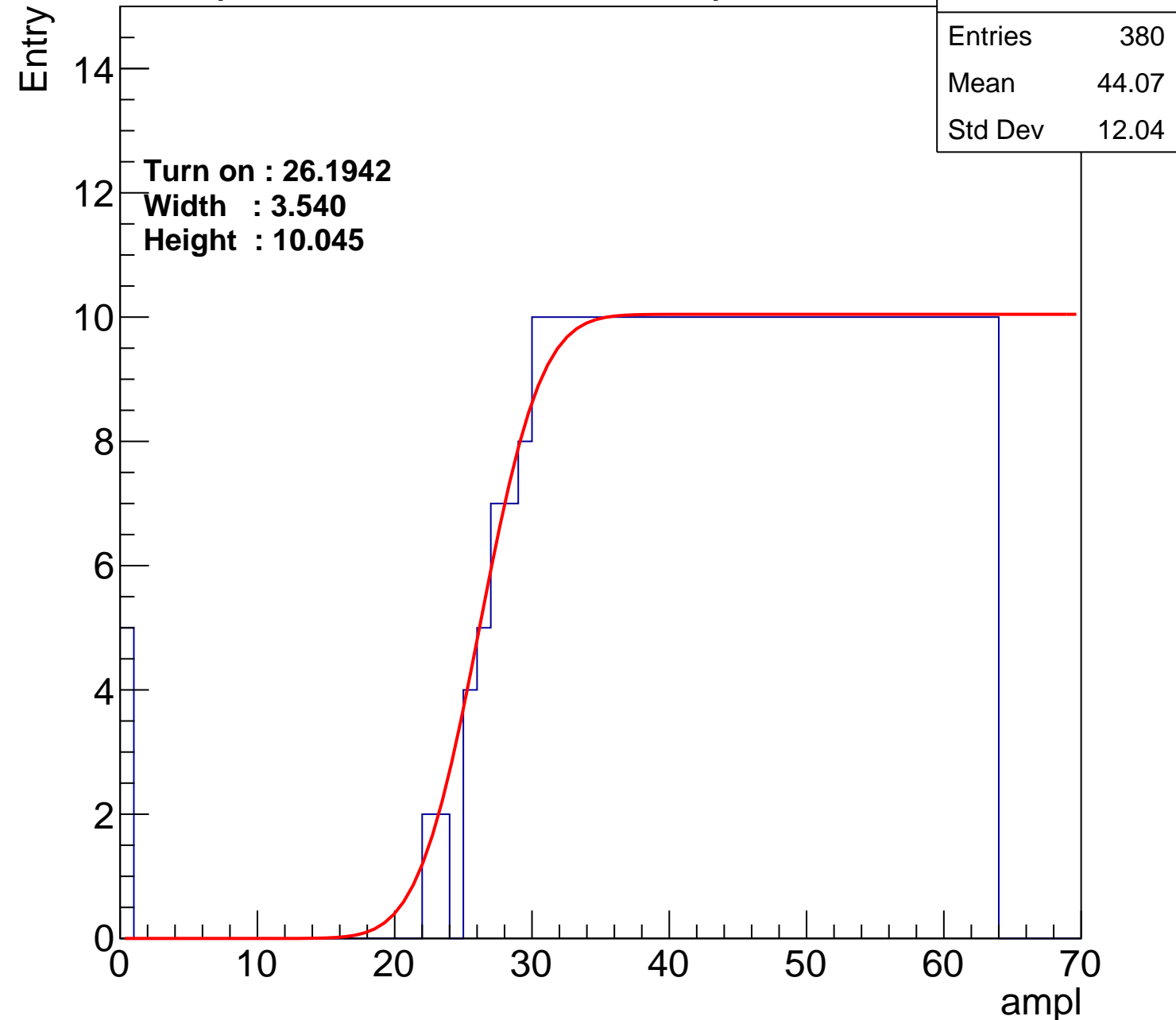
Width : 3.540

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 26.7311

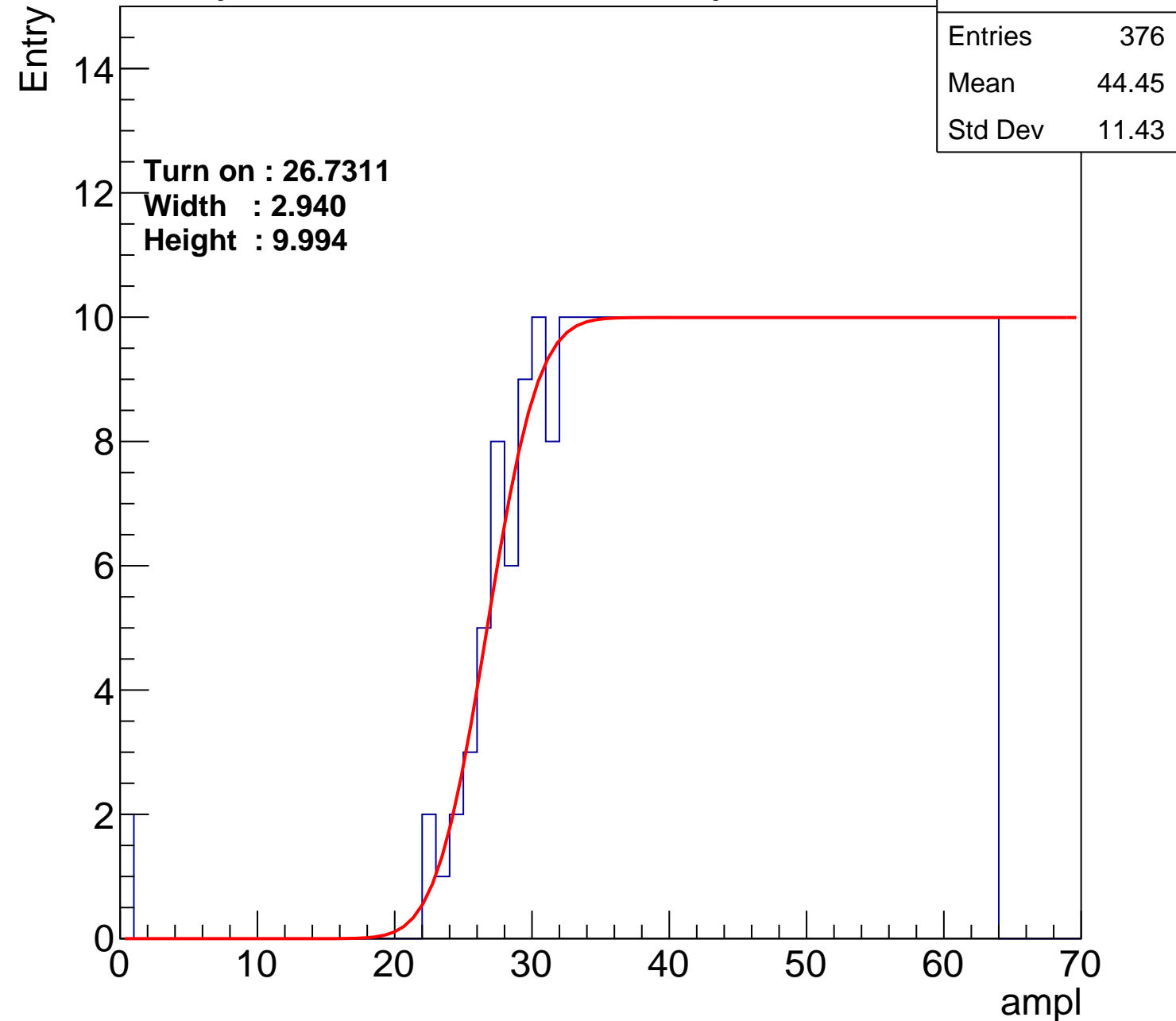
Width : 2.940

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch85

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.62
Std Dev	11.45

Turn on : 27.0951

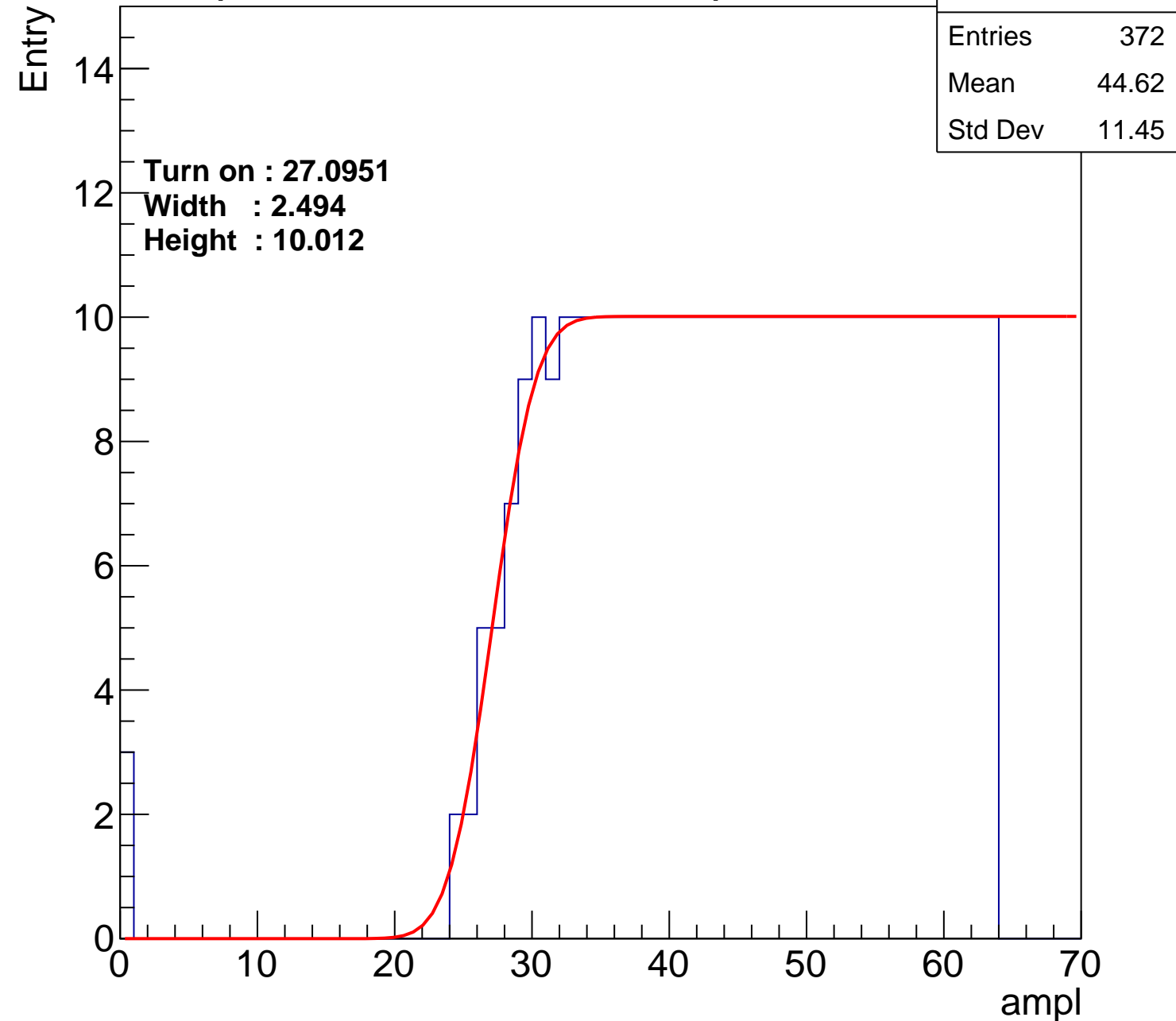
Width : 2.494

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch86

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.37
Std Dev	11.27

Turn on : 26.4967

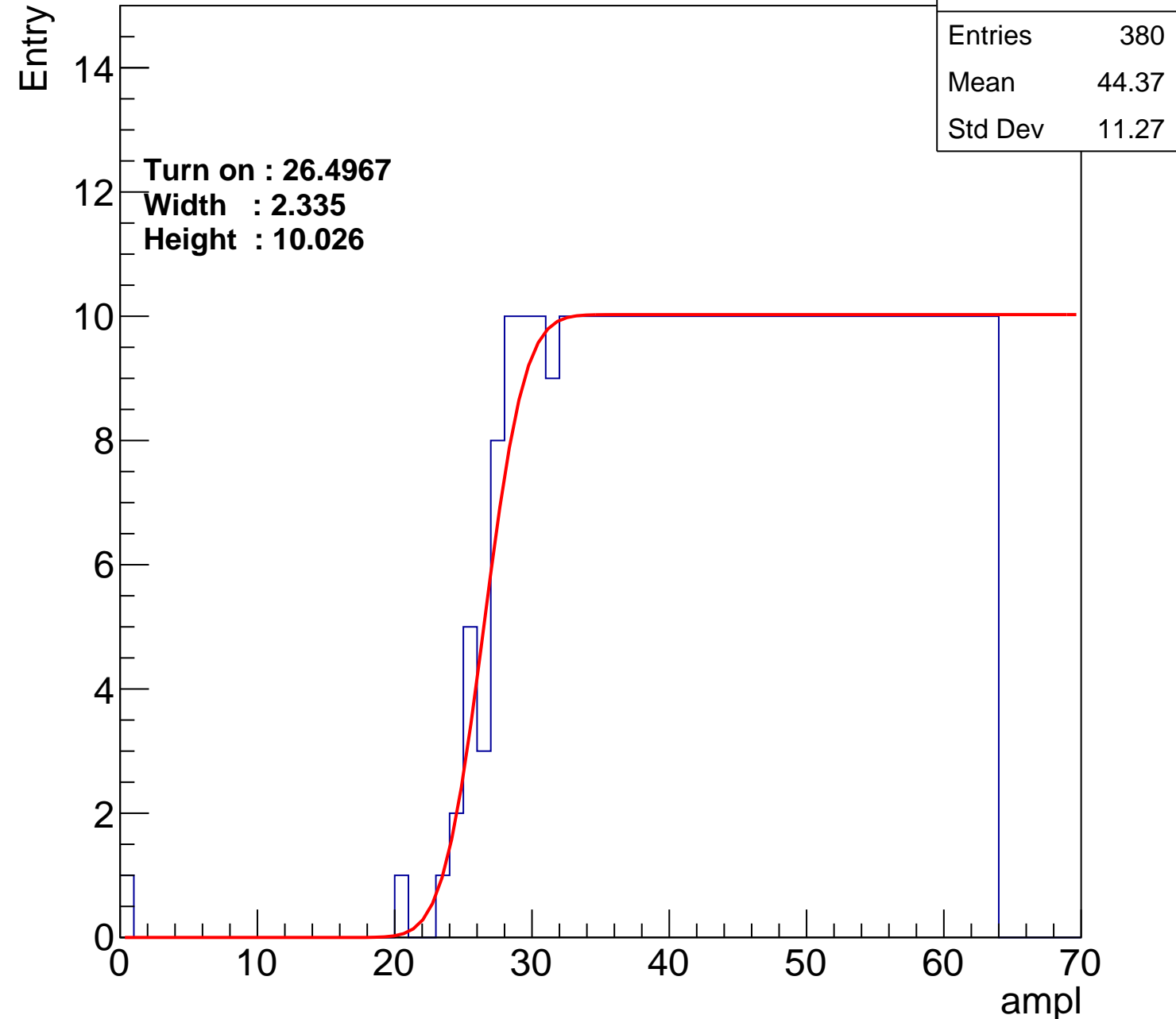
Width : 2.335

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch87

calib_packv5_042523_0143.root, FC#13, port D2

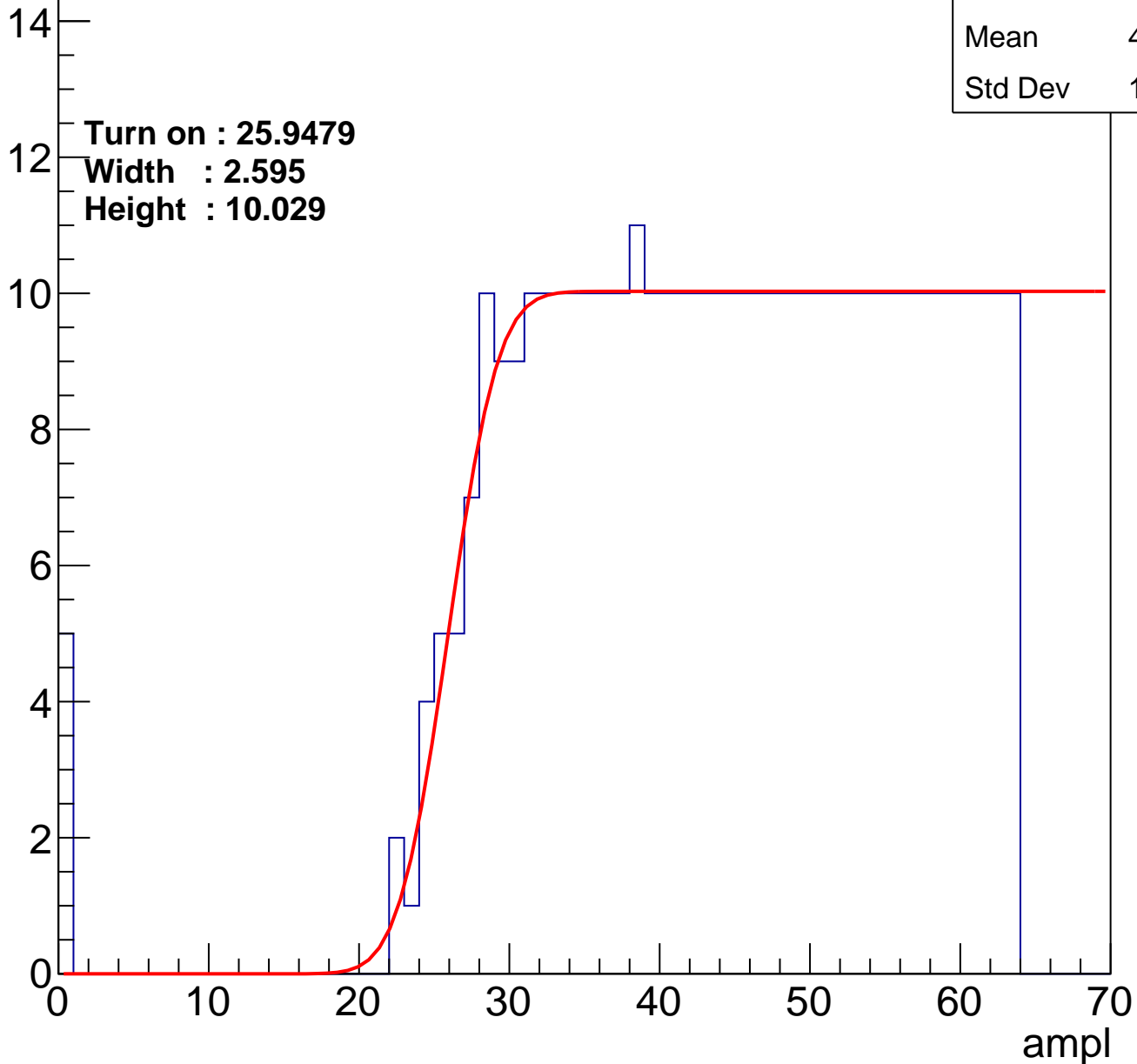
Entries	388
Mean	43.73
Std Dev	12.16

Turn on : 25.9479

Width : 2.595

Height : 10.029

Entry



B1L003S, U19-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.52
Std Dev	11.5

Turn on : 26.9146

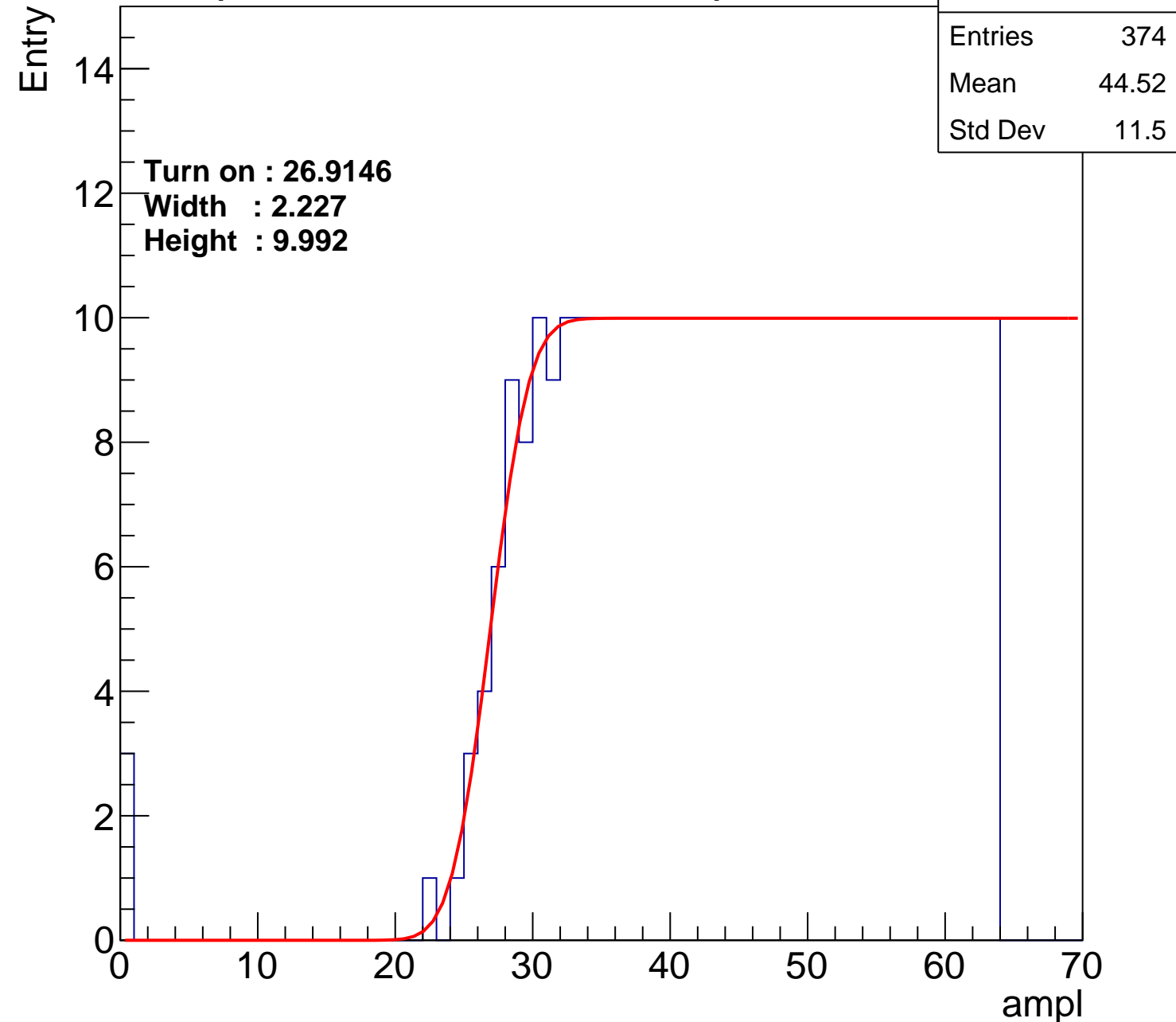
Width : 2.227

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch89

calib_packv5_042523_0143.root, FC#13, port D2

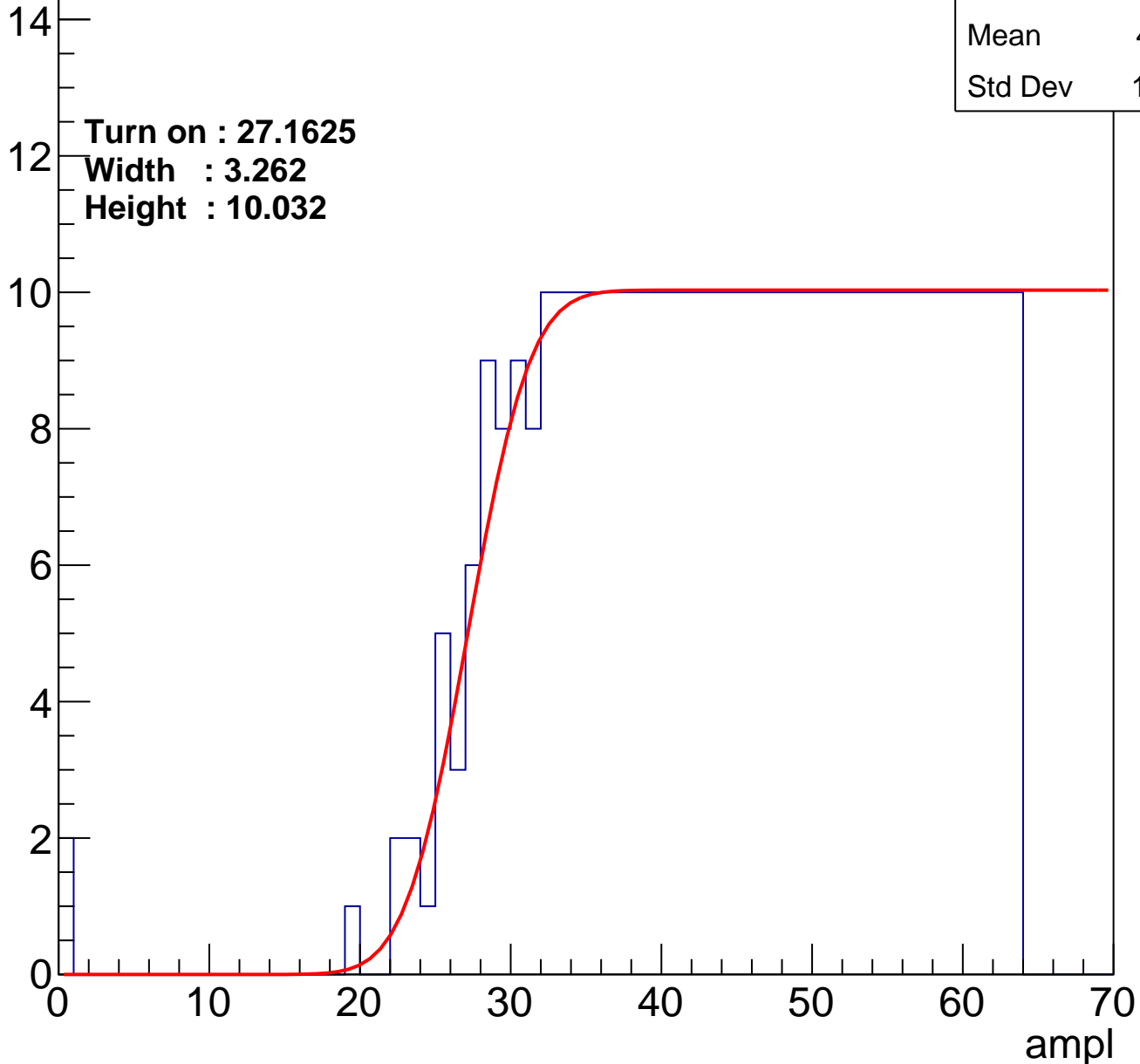
Entries	376
Mean	44.41
Std Dev	11.49

Turn on : 27.1625

Width : 3.262

Height : 10.032

Entry



B1L003S, U19-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.88
Std Dev	11.55

Turn on : 28.4322

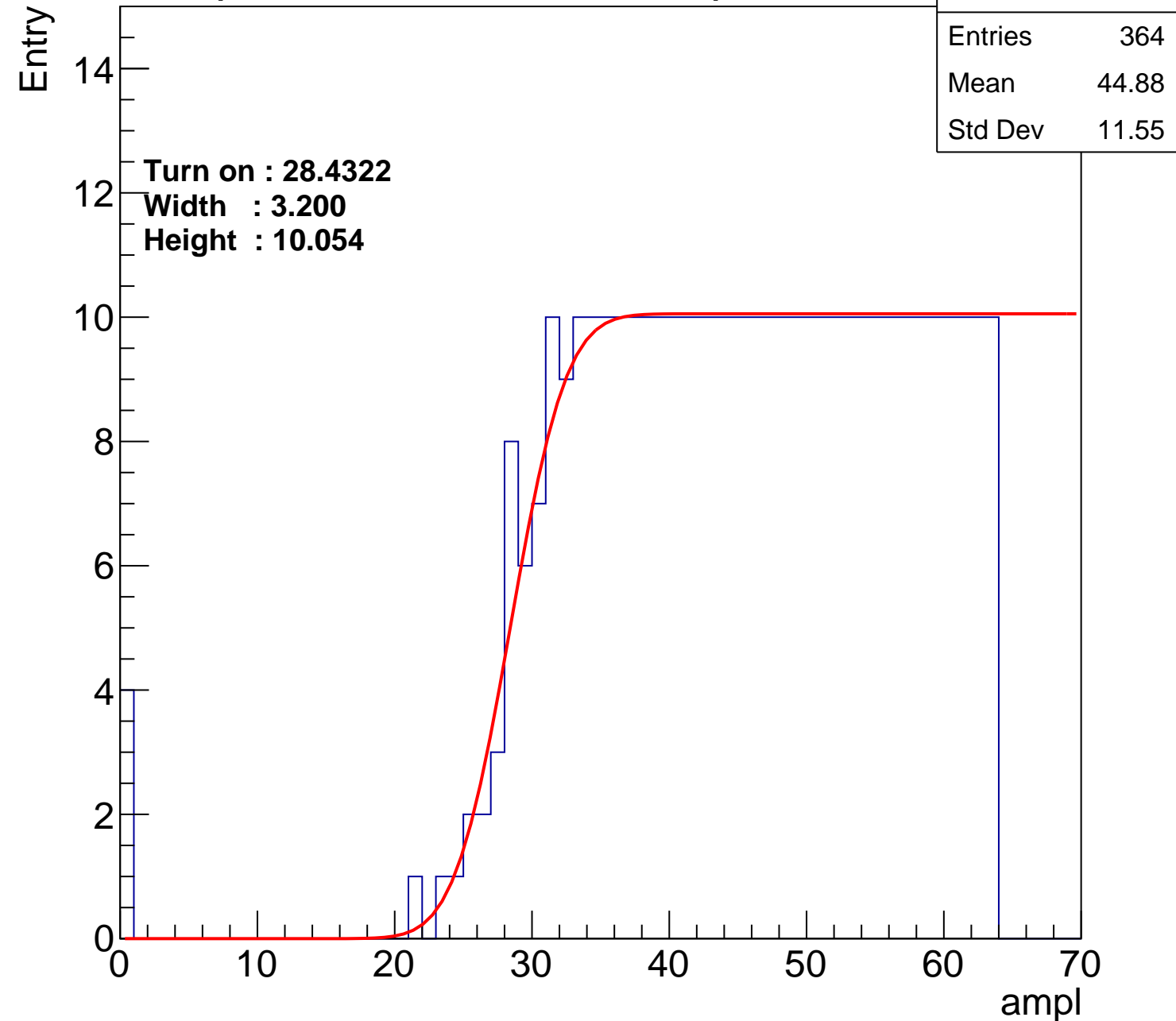
Width : 3.200

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch91

calib_packv5_042523_0143.root, FC#13, port D2

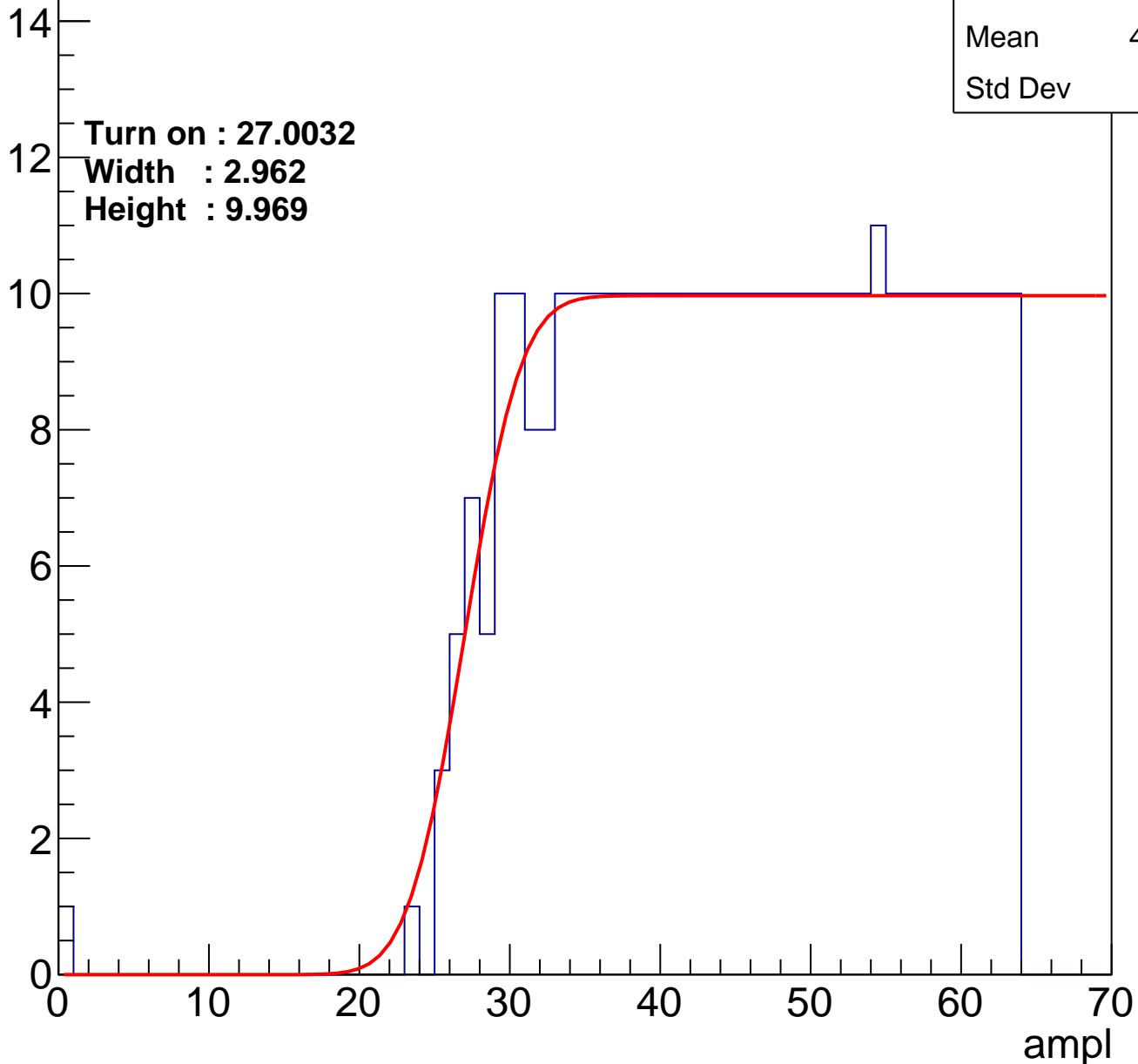
Entries	369
Mean	44.95
Std Dev	11

Turn on : 27.0032

Width : 2.962

Height : 9.969

Entry



B1L003S, U19-ch92

calib_packv5_042523_0143.root, FC#13, port D2

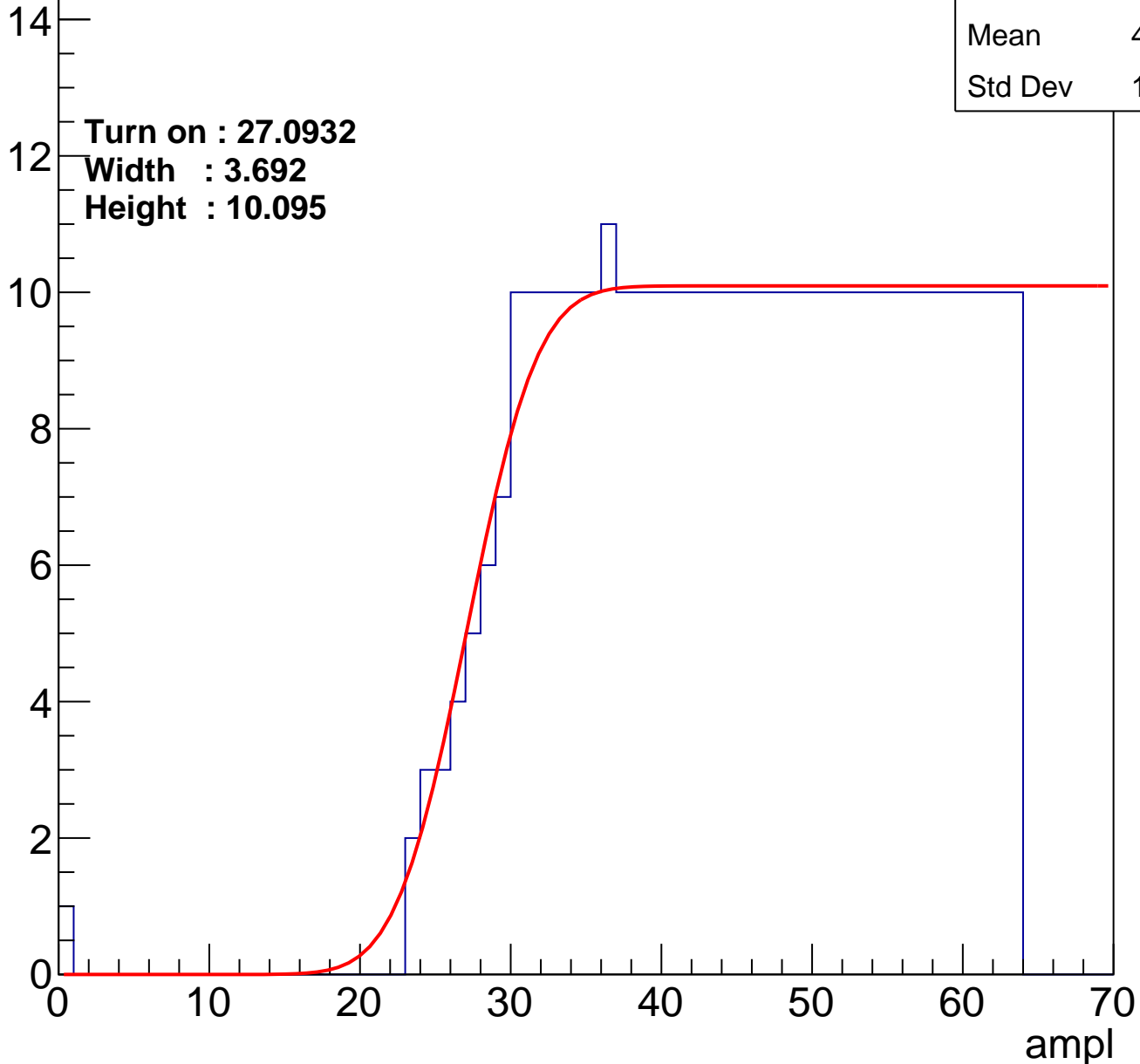
Entries	372
Mean	44.76
Std Dev	11.08

Turn on : 27.0932

Width : 3.692

Height : 10.095

Entry



B1L003S, U19-ch93

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.35
Std Dev	11.51

Turn on : 26.3146

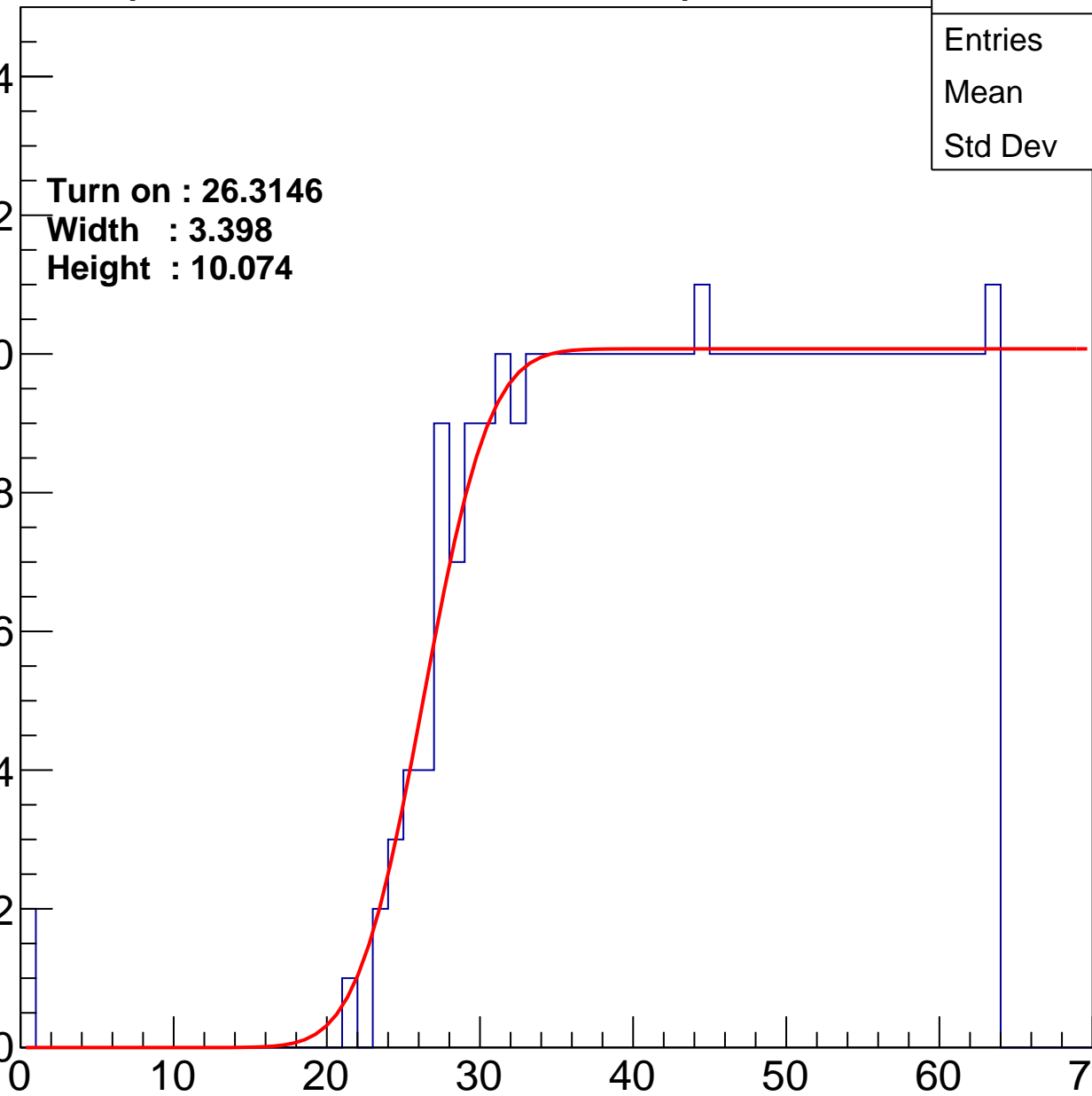
Width : 3.398

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch94

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.98
Std Dev	10.96

Turn on : 27.5170

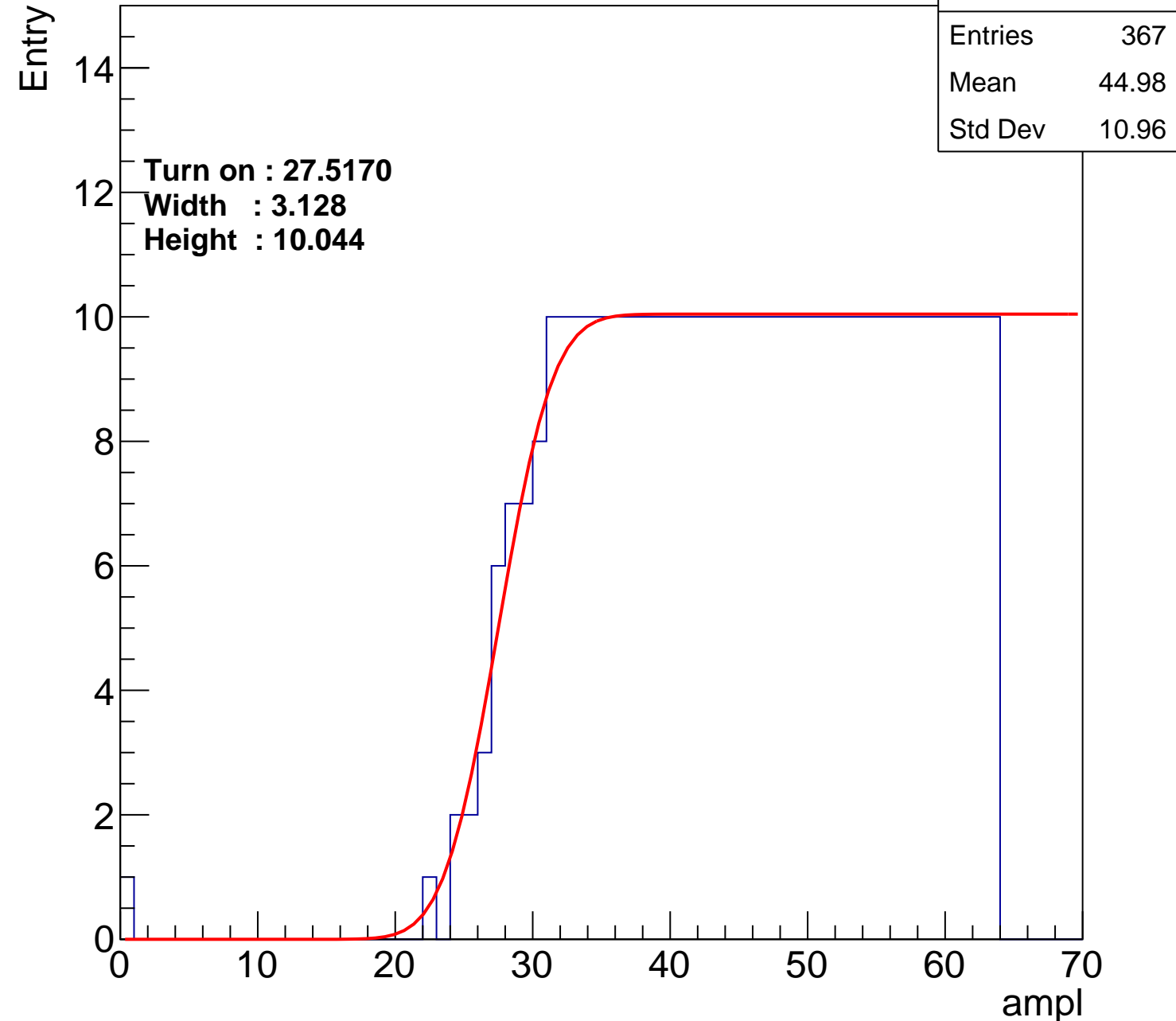
Width : 3.128

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch95

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.6399

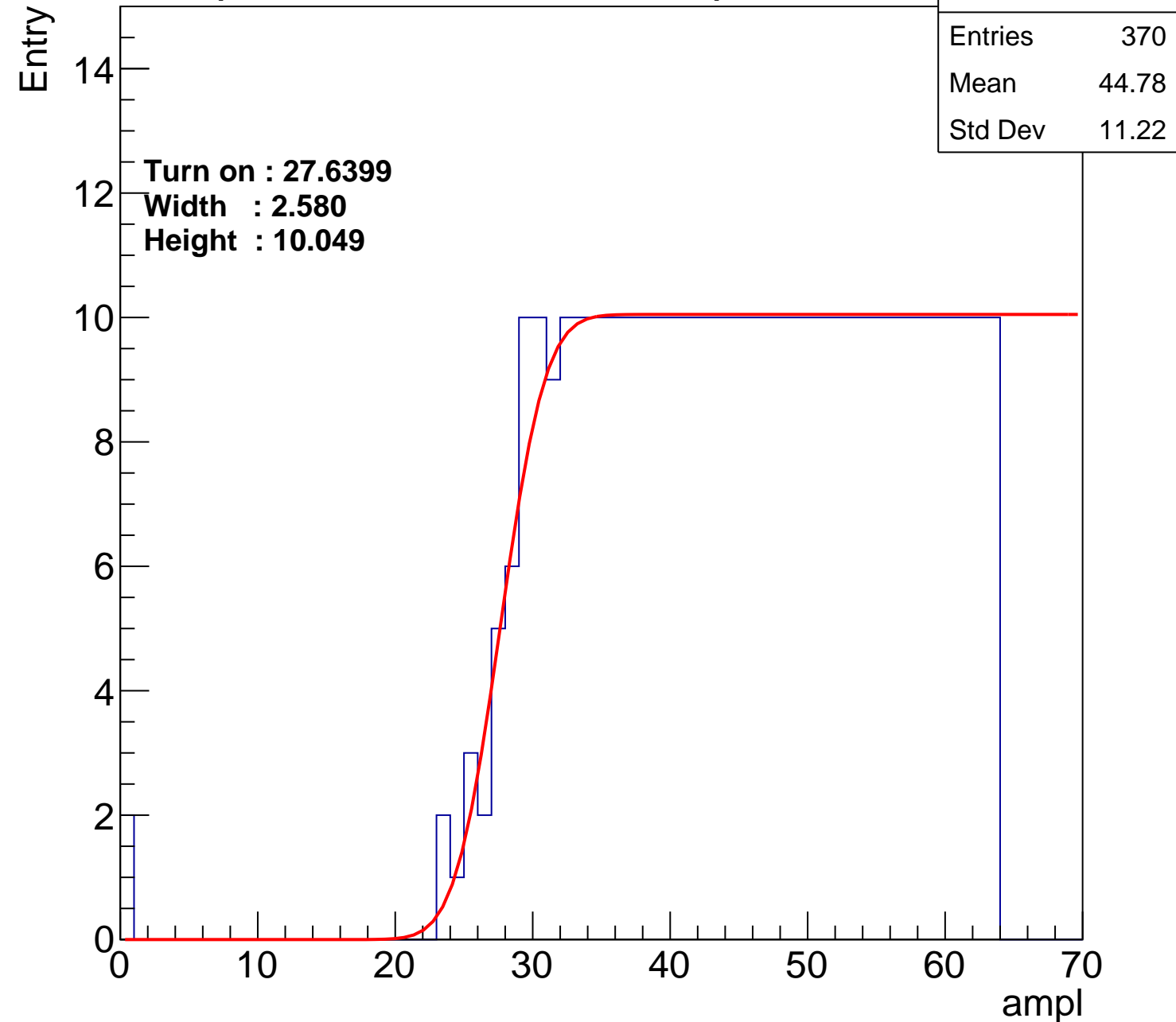
Width : 2.580

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.4
Std Dev	10.71

Turn on : 28.2388

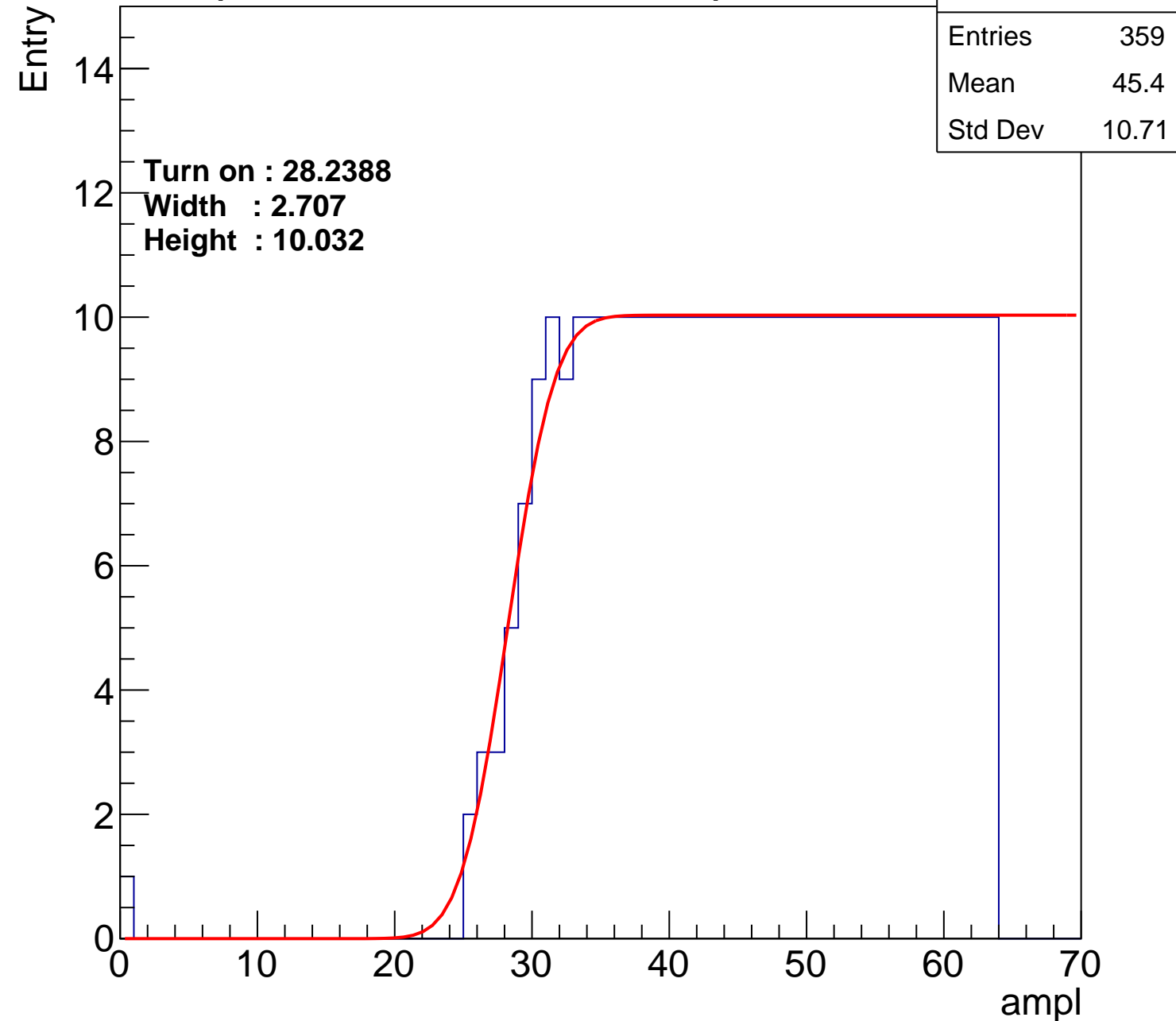
Width : 2.707

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch97

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.3
Std Dev	11.32

Turn on : 26.5735

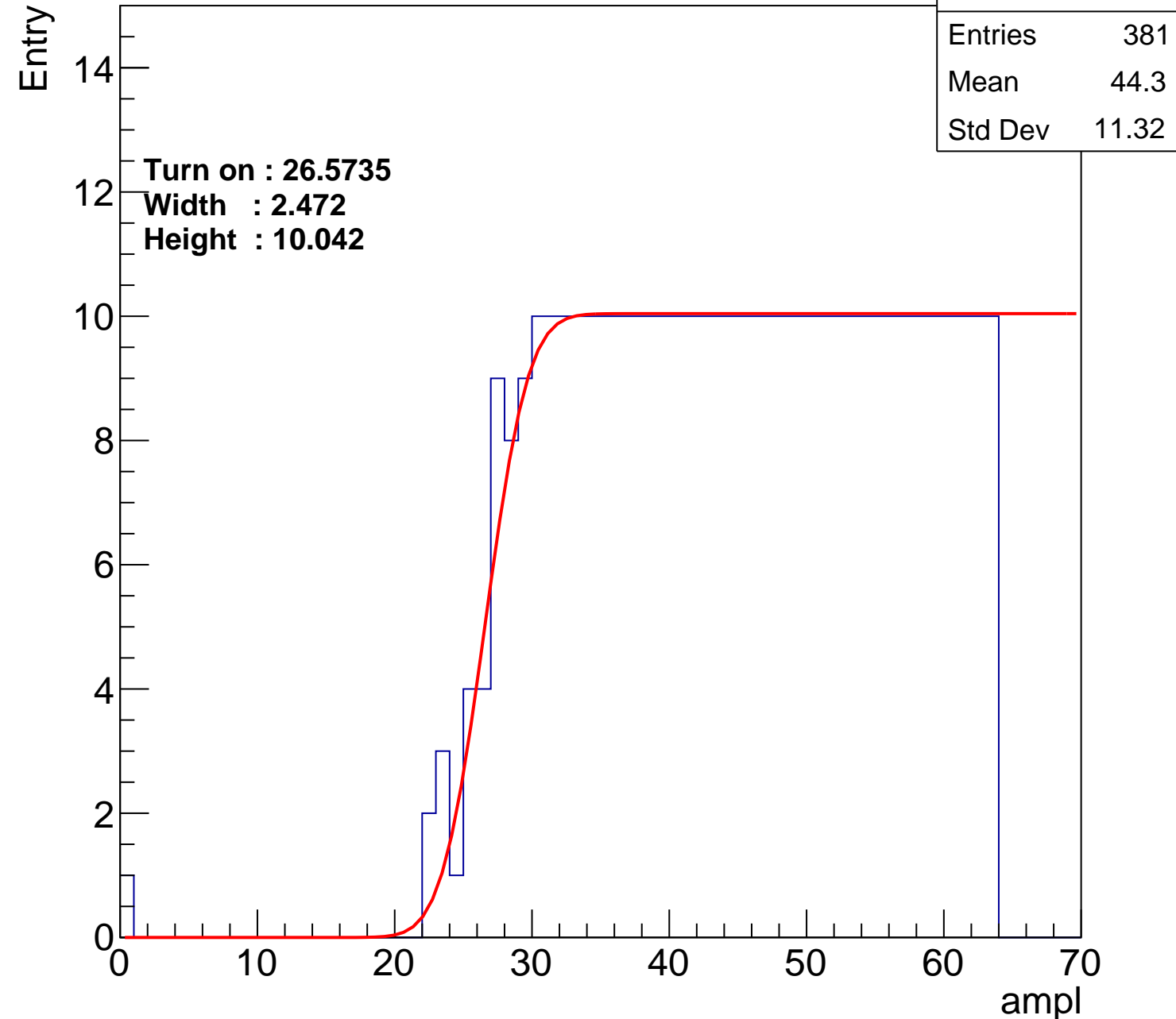
Width : 2.472

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.03
Std Dev	11.27

Turn on : 26.9451

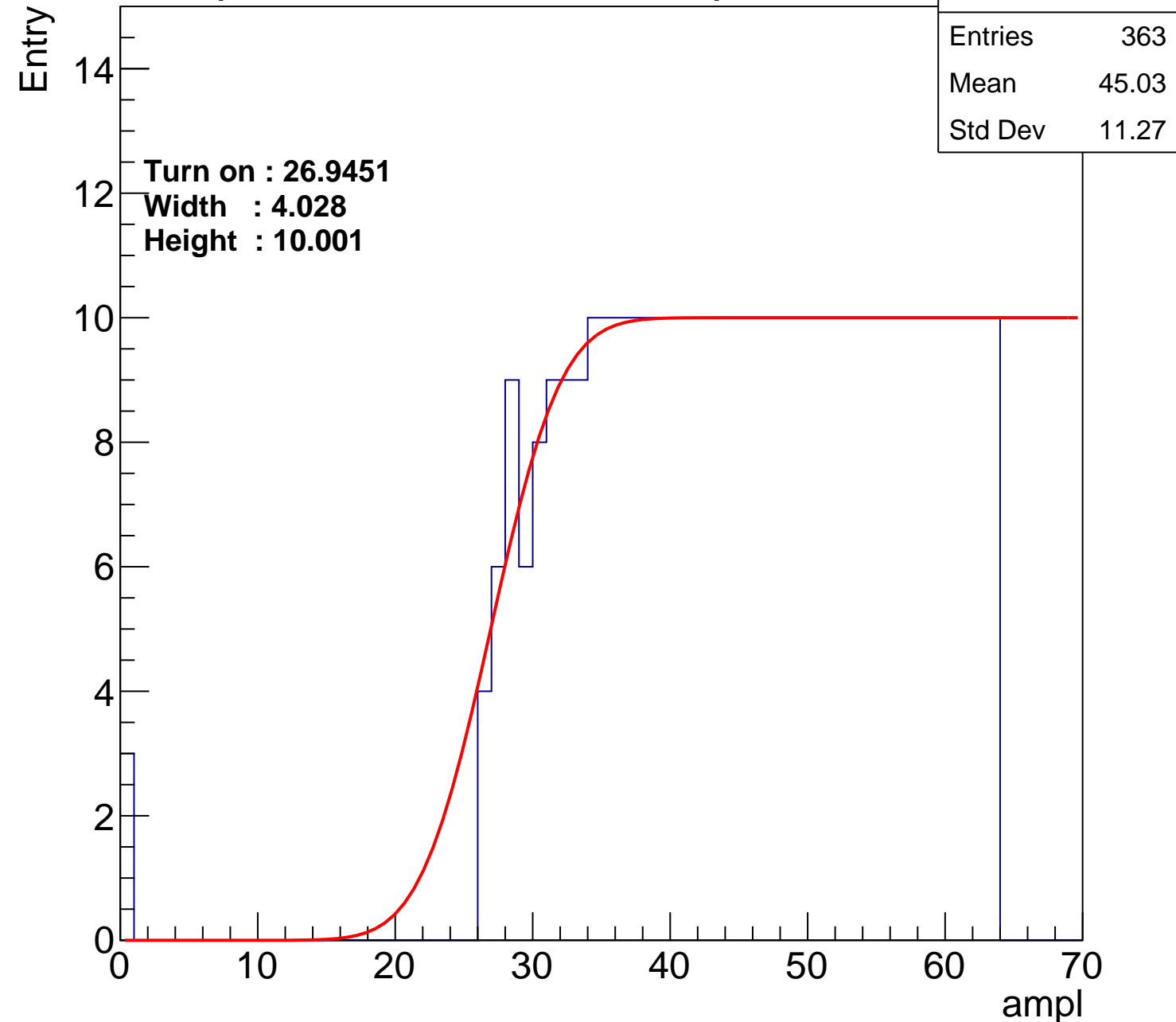
Width : 4.028

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch99

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.86
Std Dev	11.09

Turn on : 27.3072

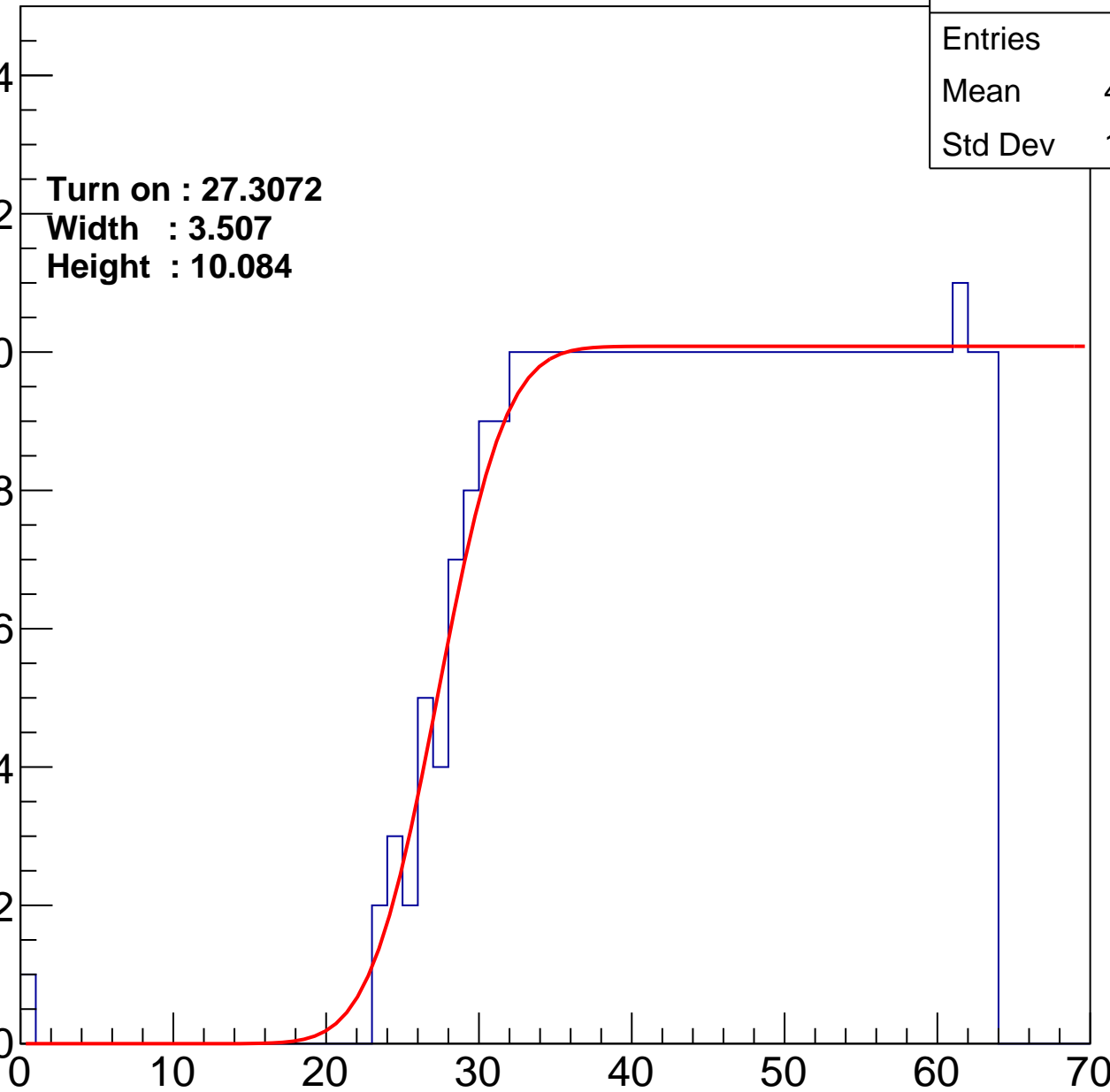
Width : 3.507

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch100

calib_packv5_042523_0143.root, FC#13, port D2

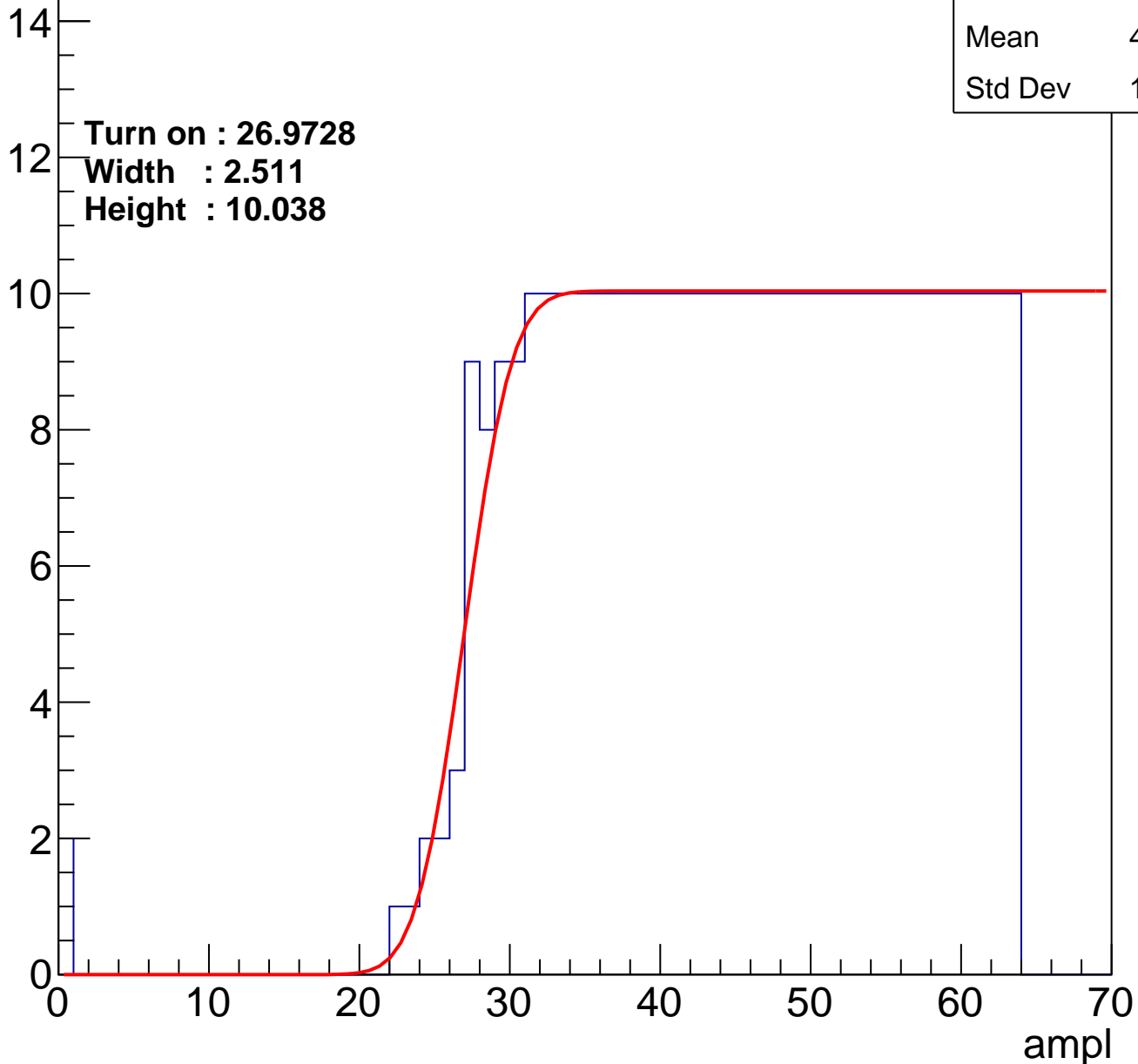
Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.9728

Width : 2.511

Height : 10.038

Entry



B1L003S, U19-ch101

calib_packv5_042523_0143.root, FC#13, port D2

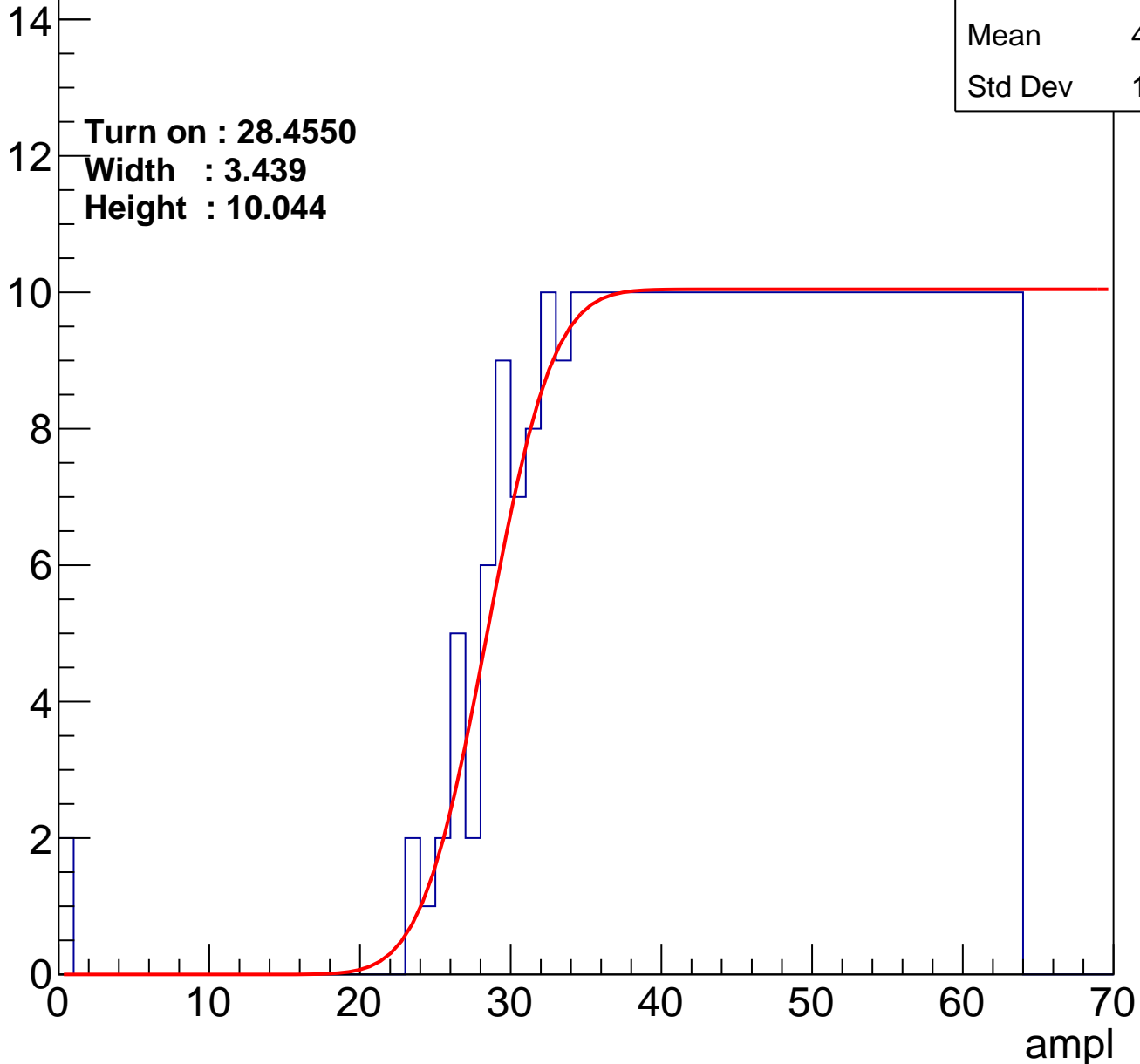
Entries	363
Mean	45.06
Std Dev	11.14

Turn on : 28.4550

Width : 3.439

Height : 10.044

Entry



B1L003S, U19-ch102

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.86
Std Dev	11.72

Turn on : 25.0619

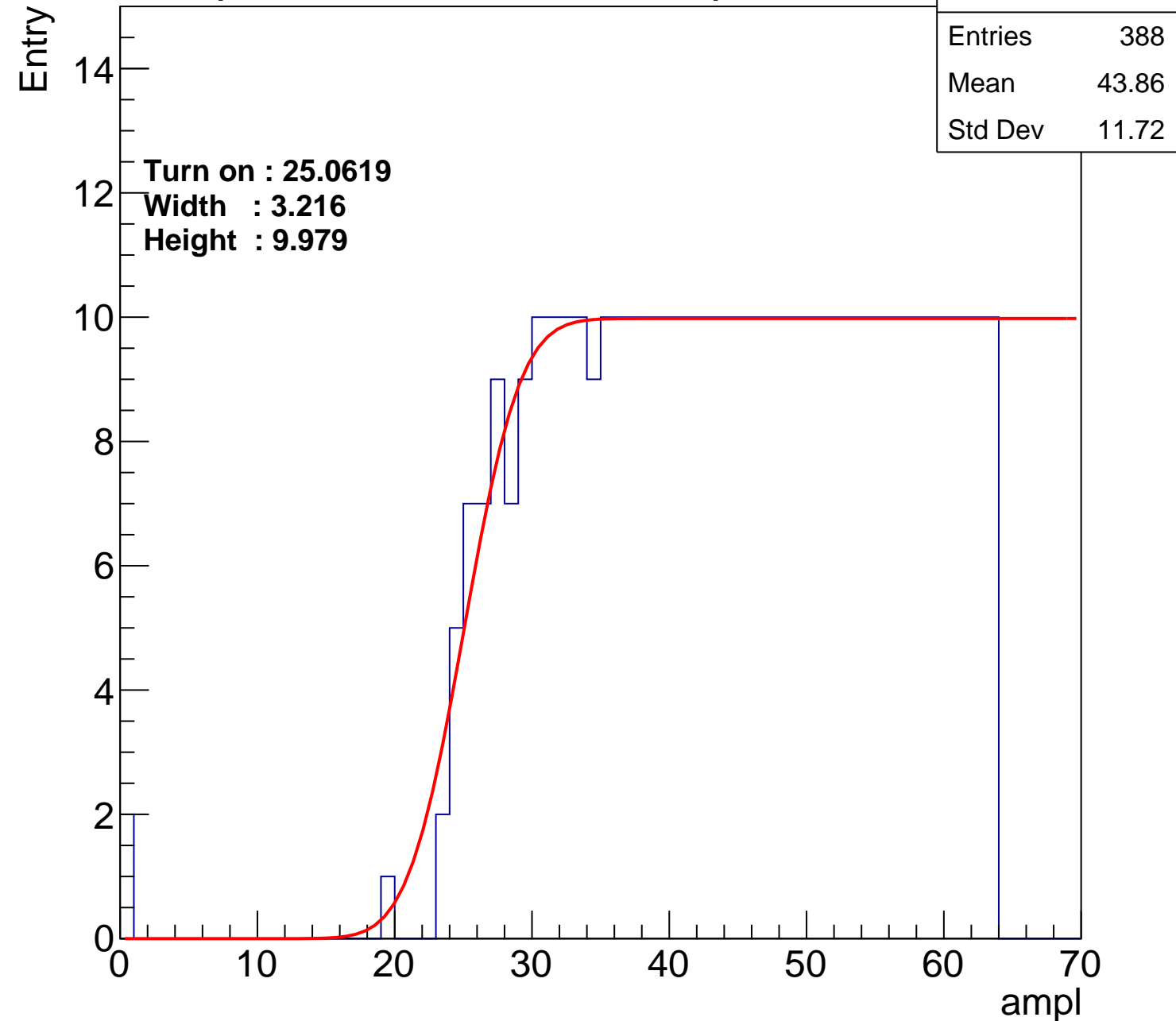
Width : 3.216

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch103

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.75
Std Dev	11.13

Turn on : 27.4012

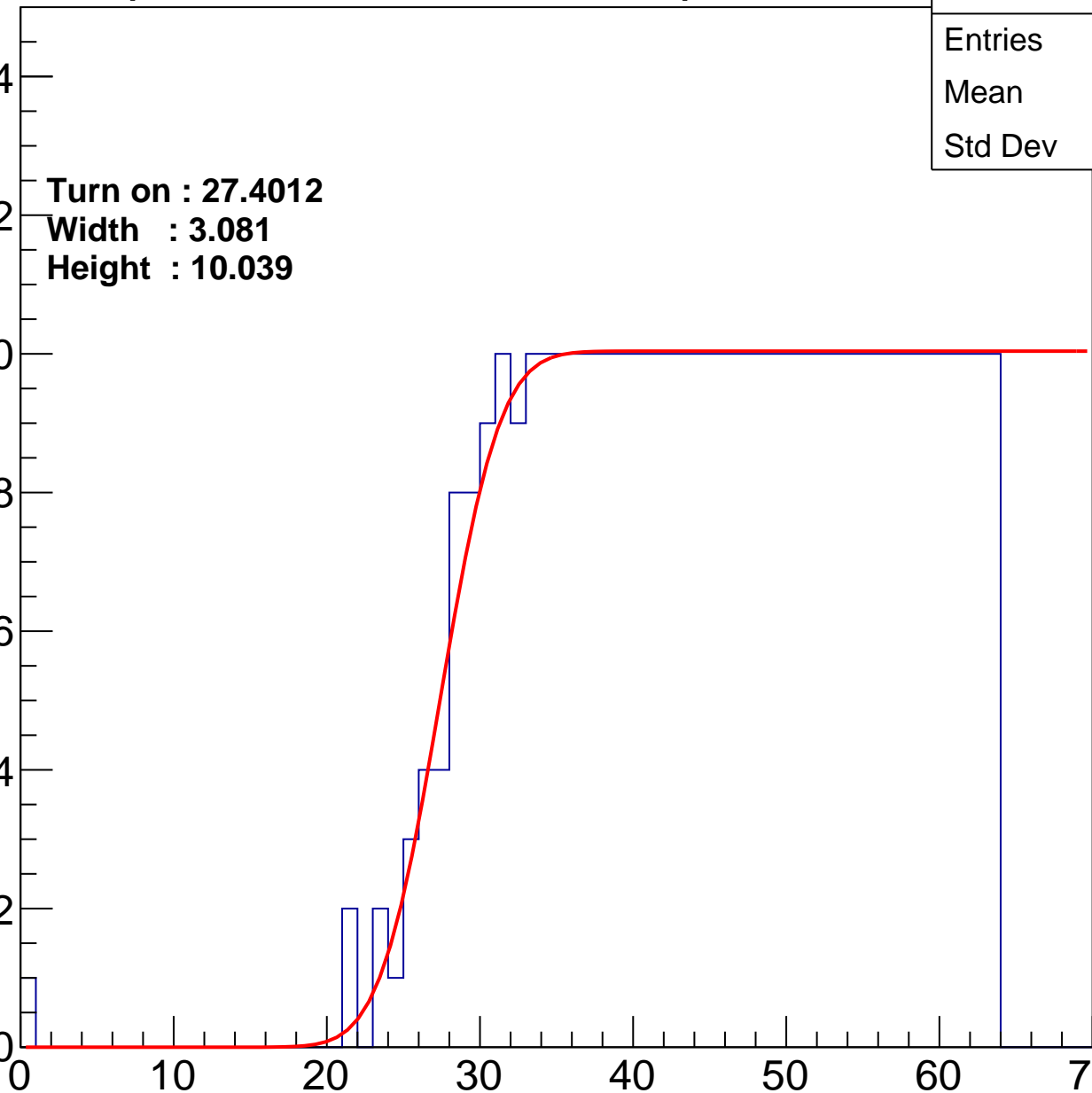
Width : 3.081

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch104

calib_packv5_042523_0143.root, FC#13, port D2

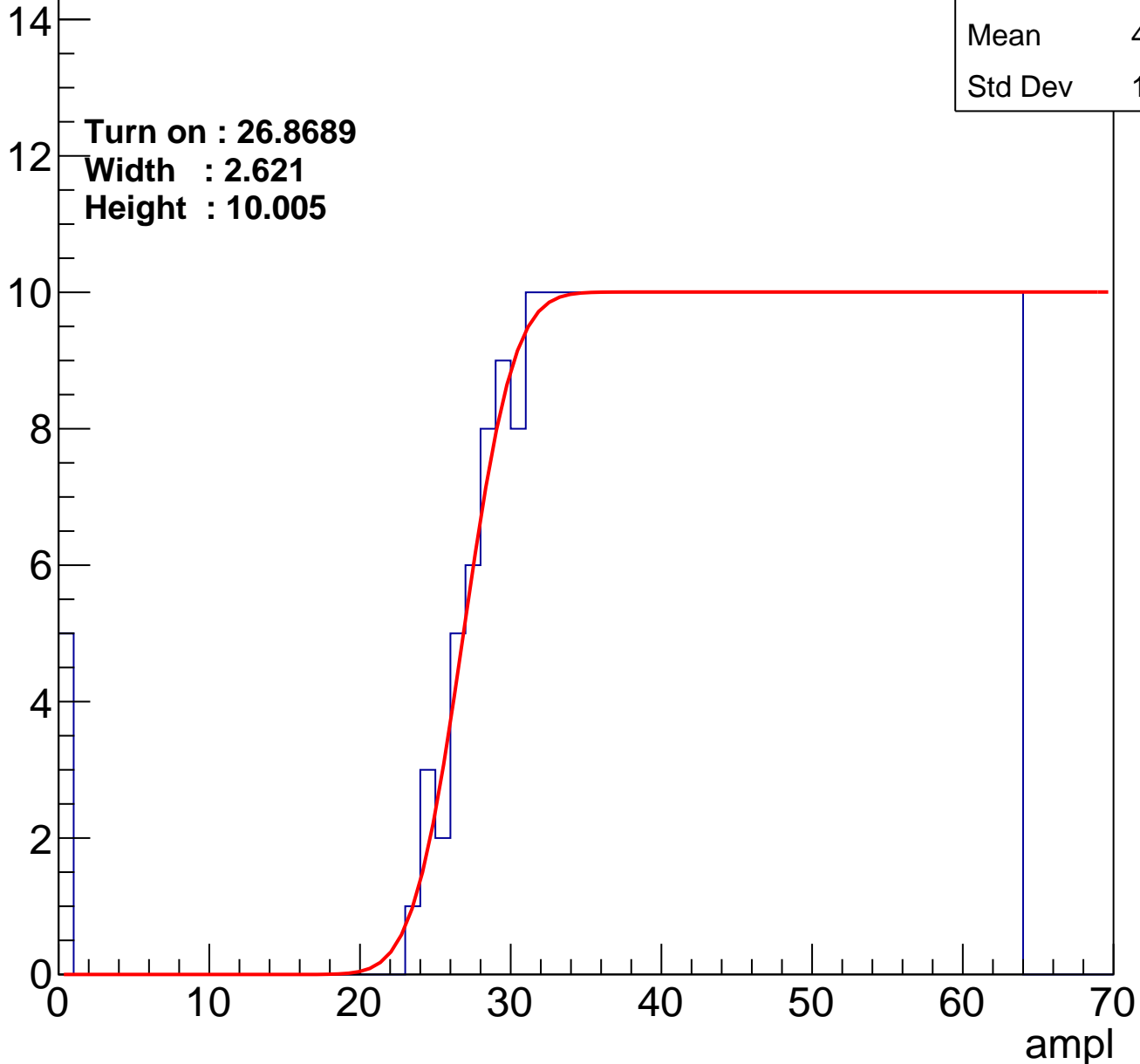
Entries	377
Mean	44.22
Std Dev	11.96

Turn on : 26.8689

Width : 2.621

Height : 10.005

Entry



B1L003S, U19-ch105

calib_packv5_042523_0143.root, FC#13, port D2

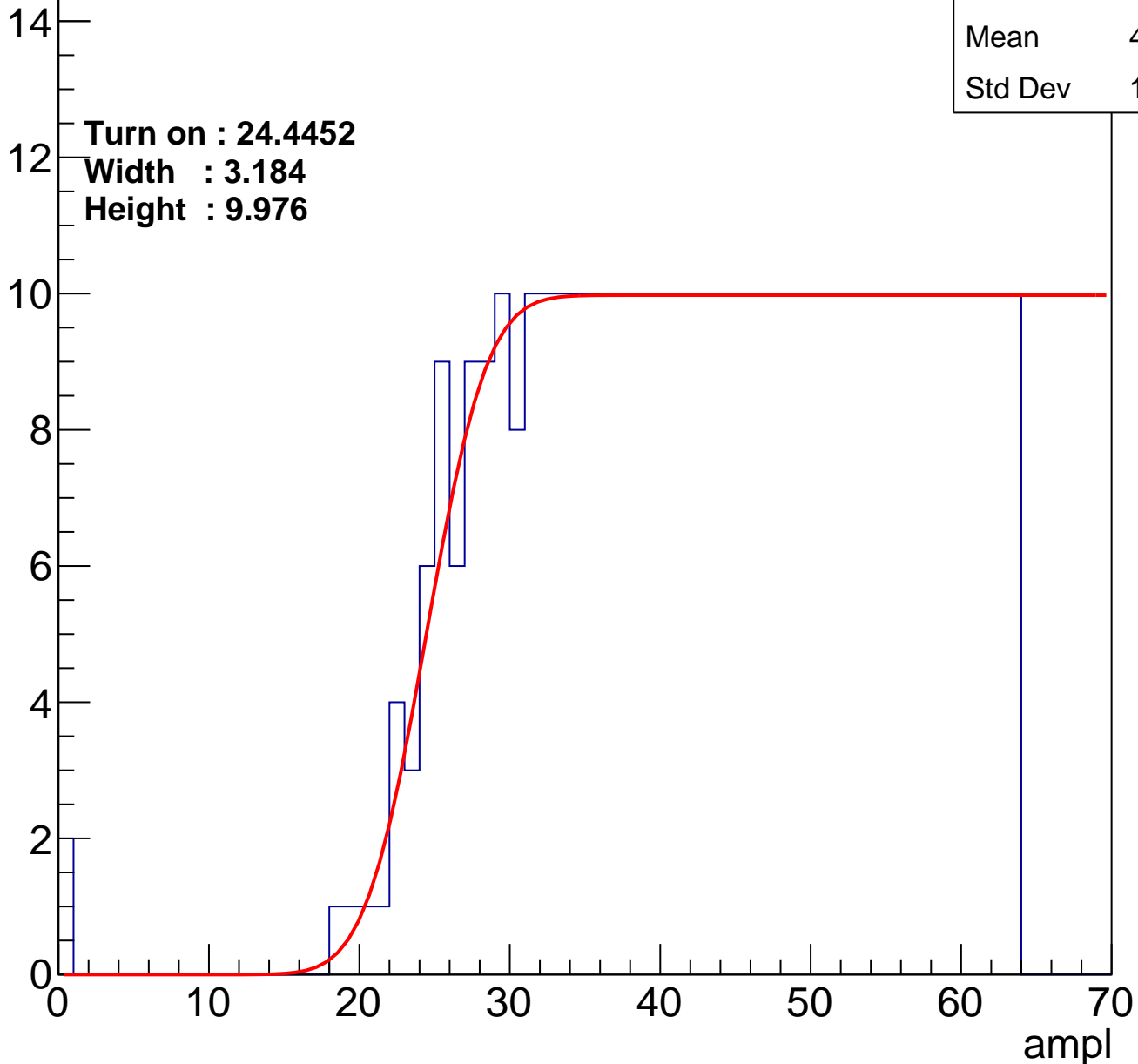
Entries	400
Mean	43.24
Std Dev	12.09

Turn on : 24.4452

Width : 3.184

Height : 9.976

Entry



B1L003S, U19-ch106

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.54
Std Dev	12.12

Turn on : 25.5685

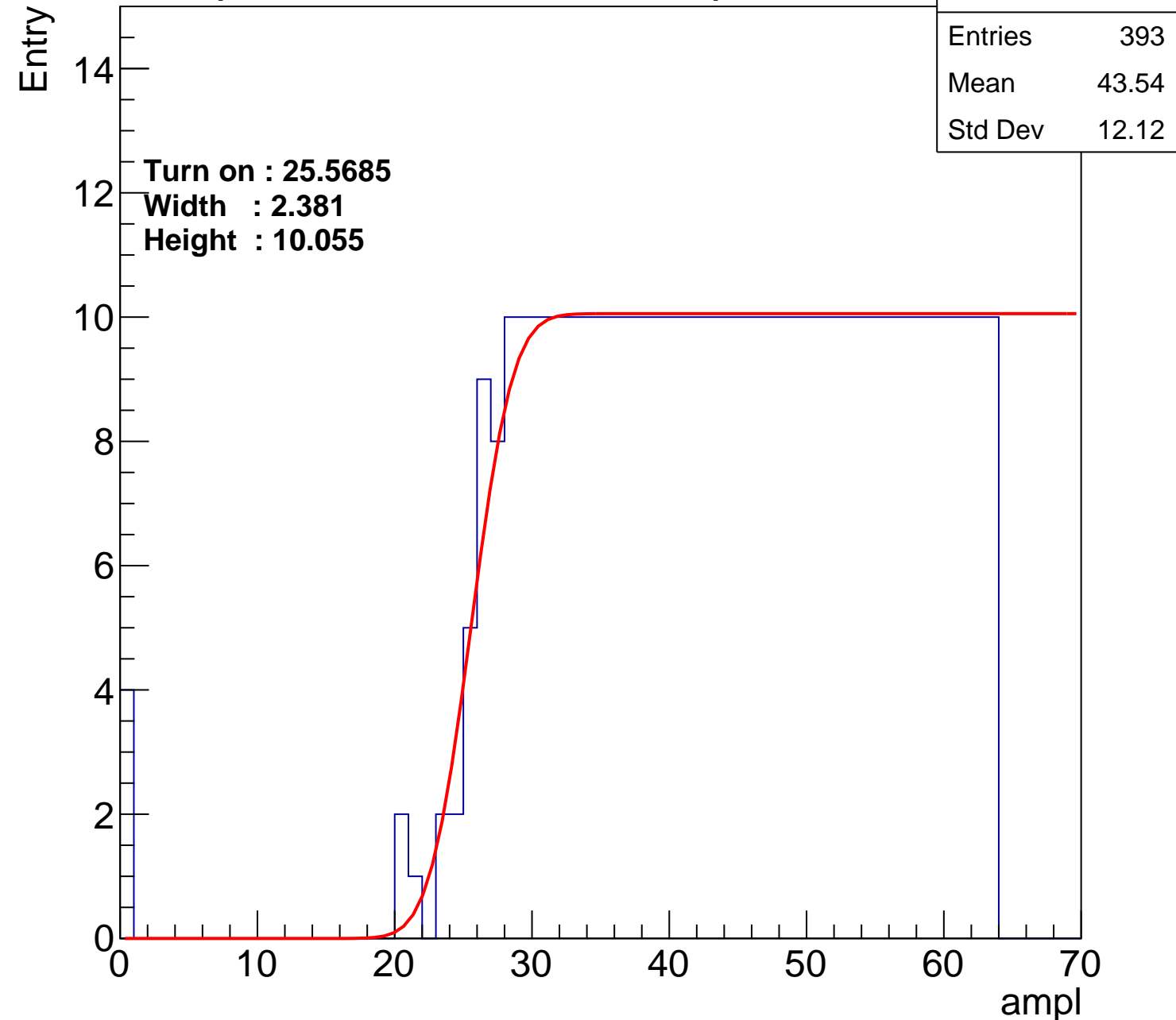
Width : 2.381

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.41
Std Dev	10.96

Turn on : 28.8413

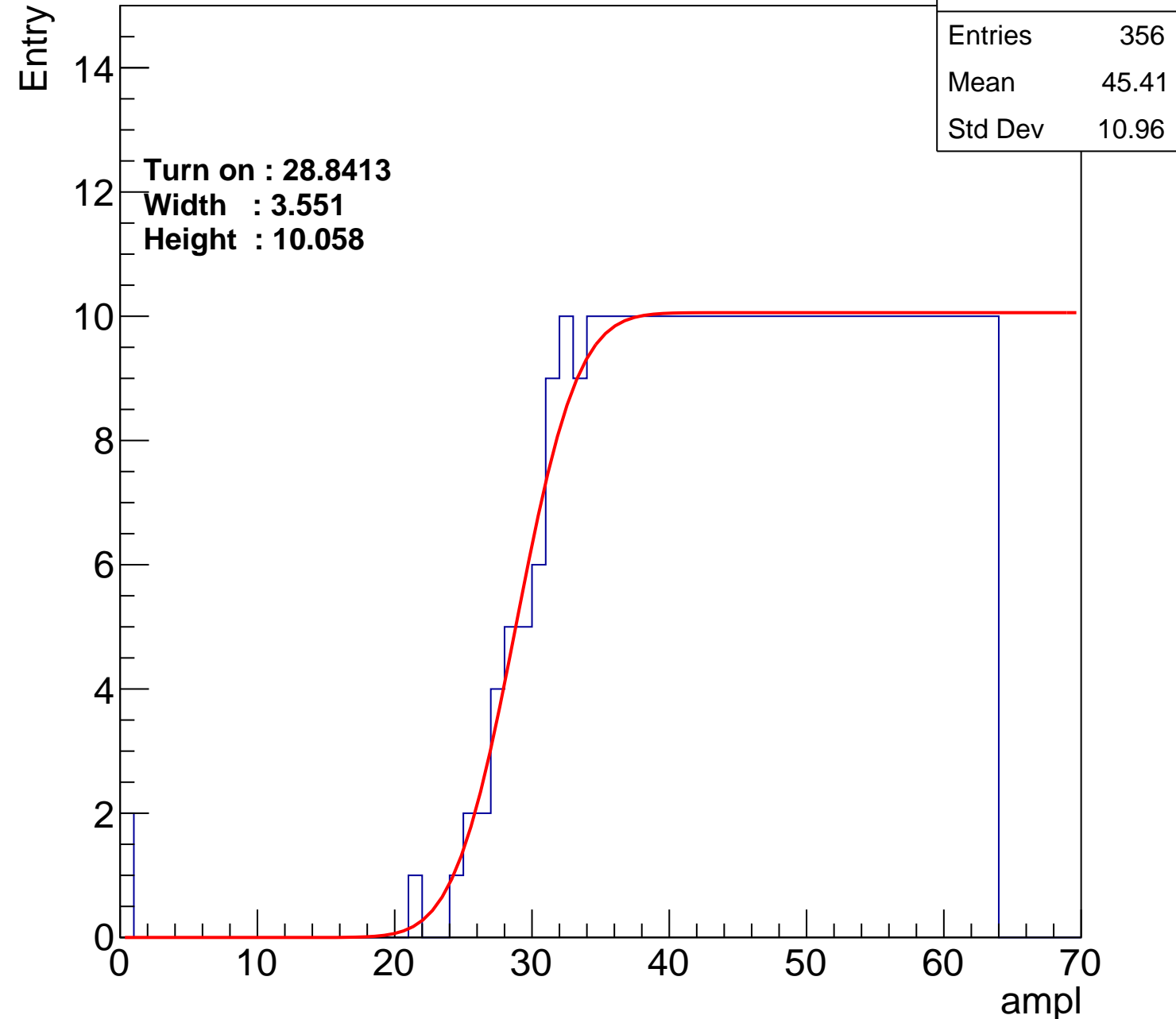
Width : 3.551

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.72
Std Dev	11.85

Turn on : 25.3519

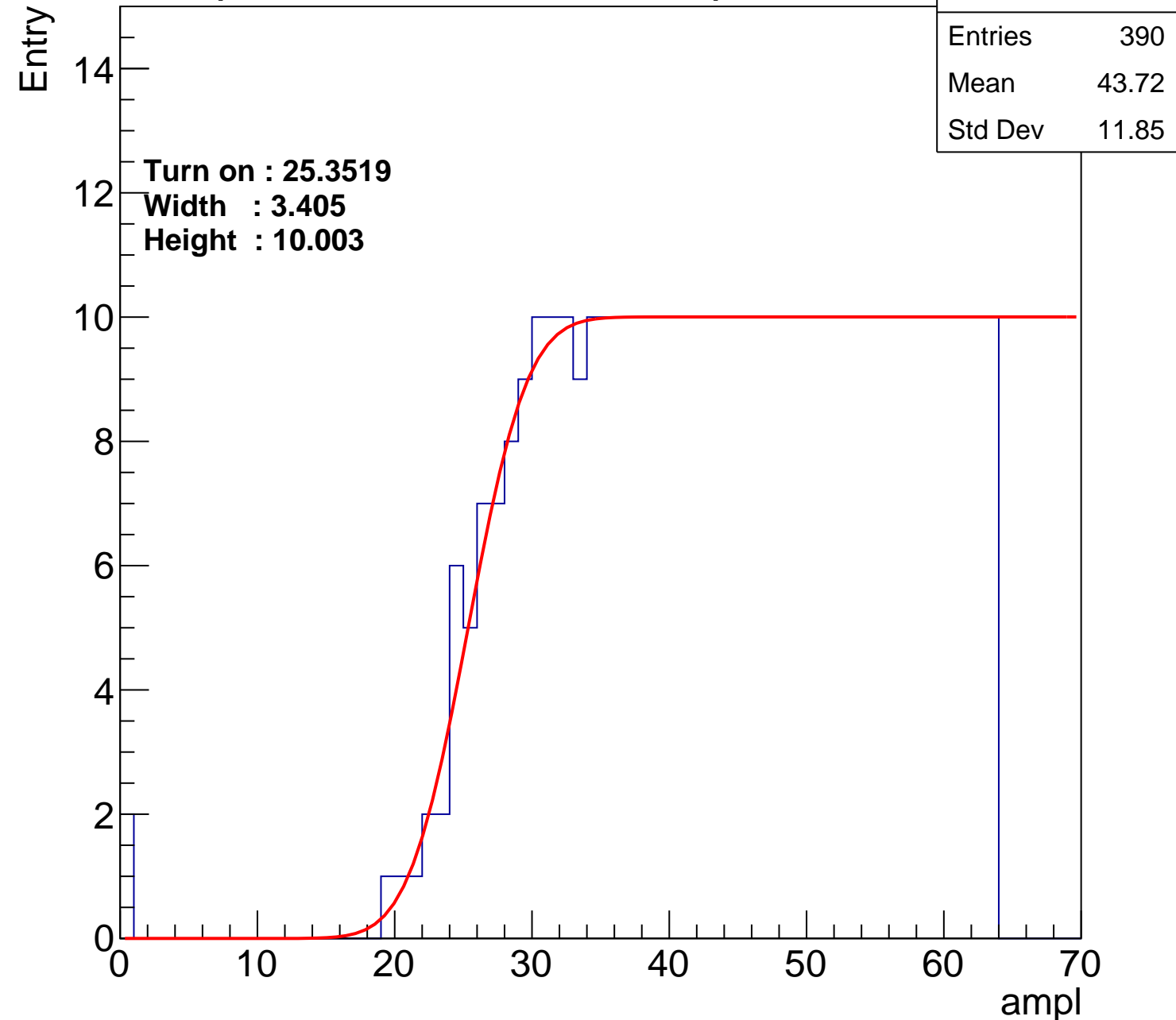
Width : 3.405

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.65
Std Dev	11.64

Turn on : 27.3741

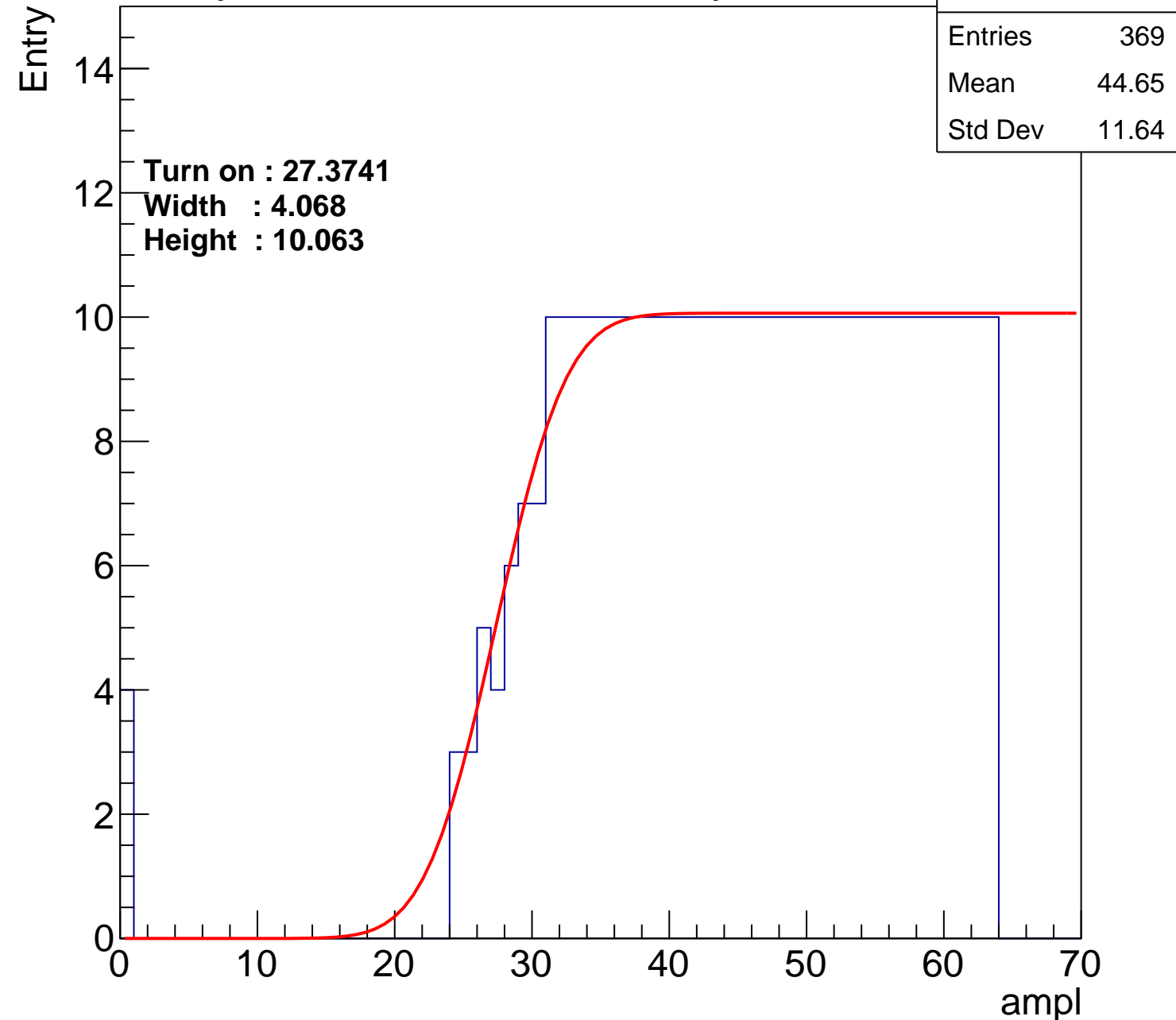
Width : 4.068

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	407
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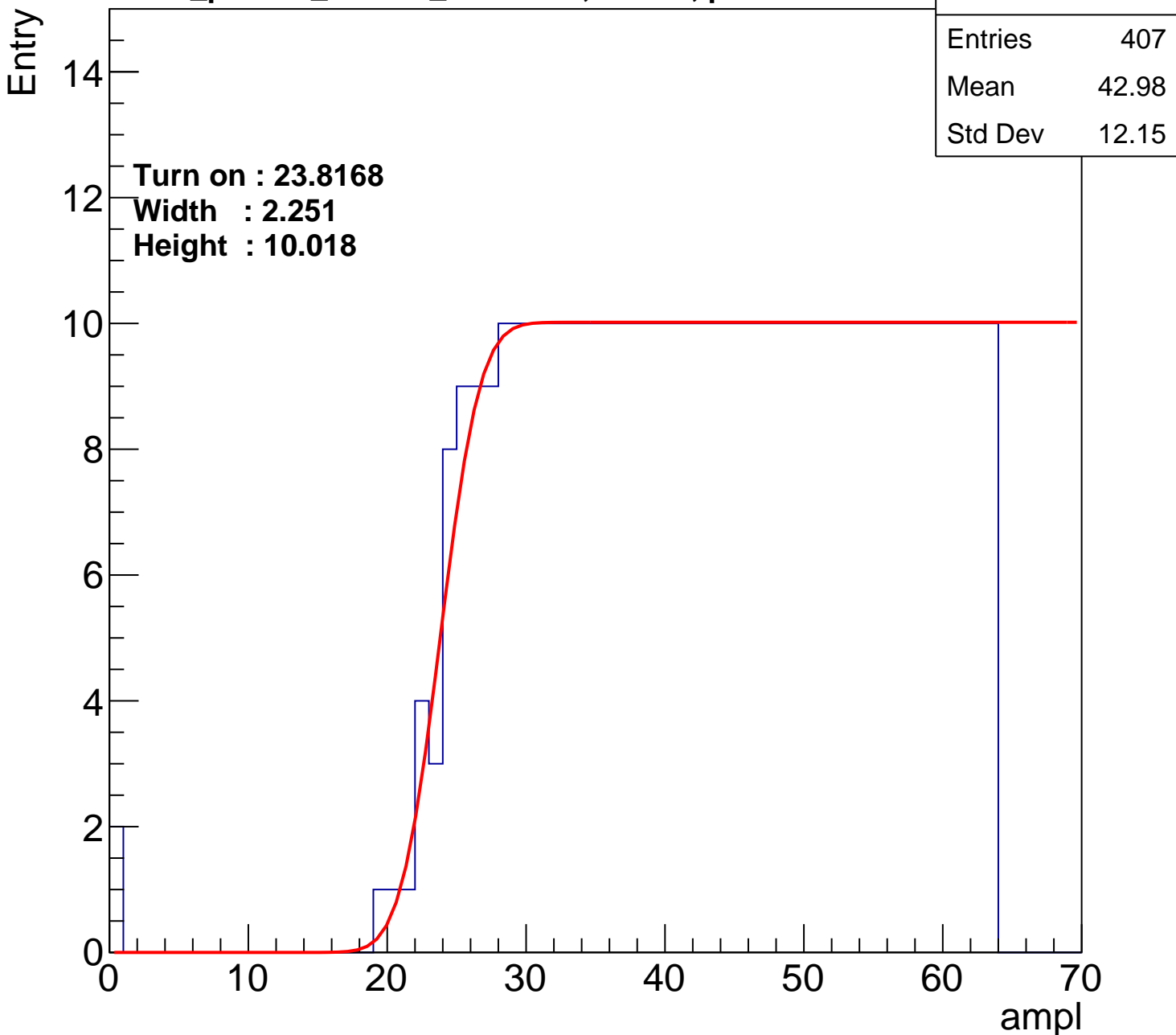
Mean	42.98
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Std Dev	12.15
---------	-------

Turn on : 23.8168

Width : 2.251

Height : 10.018



B1L003S, U19-ch111

calib_packv5_042523_0143.root, FC#13, port D2

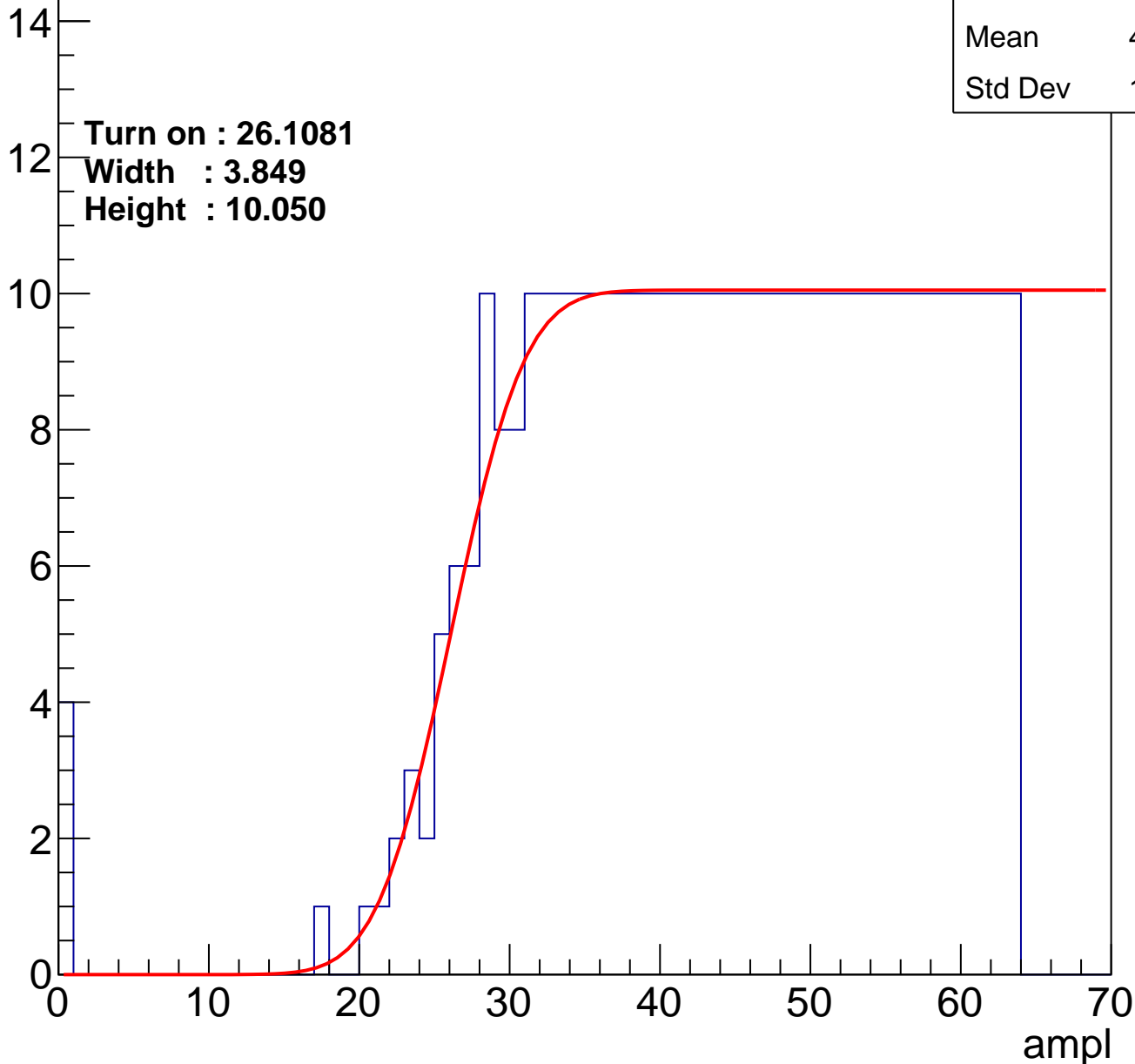
Entries	387
Mean	43.73
Std Dev	12.13

Turn on : 26.1081

Width : 3.849

Height : 10.050

Entry



B1L003S, U19-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.54
Std Dev	11.38

Turn on : 26.7420

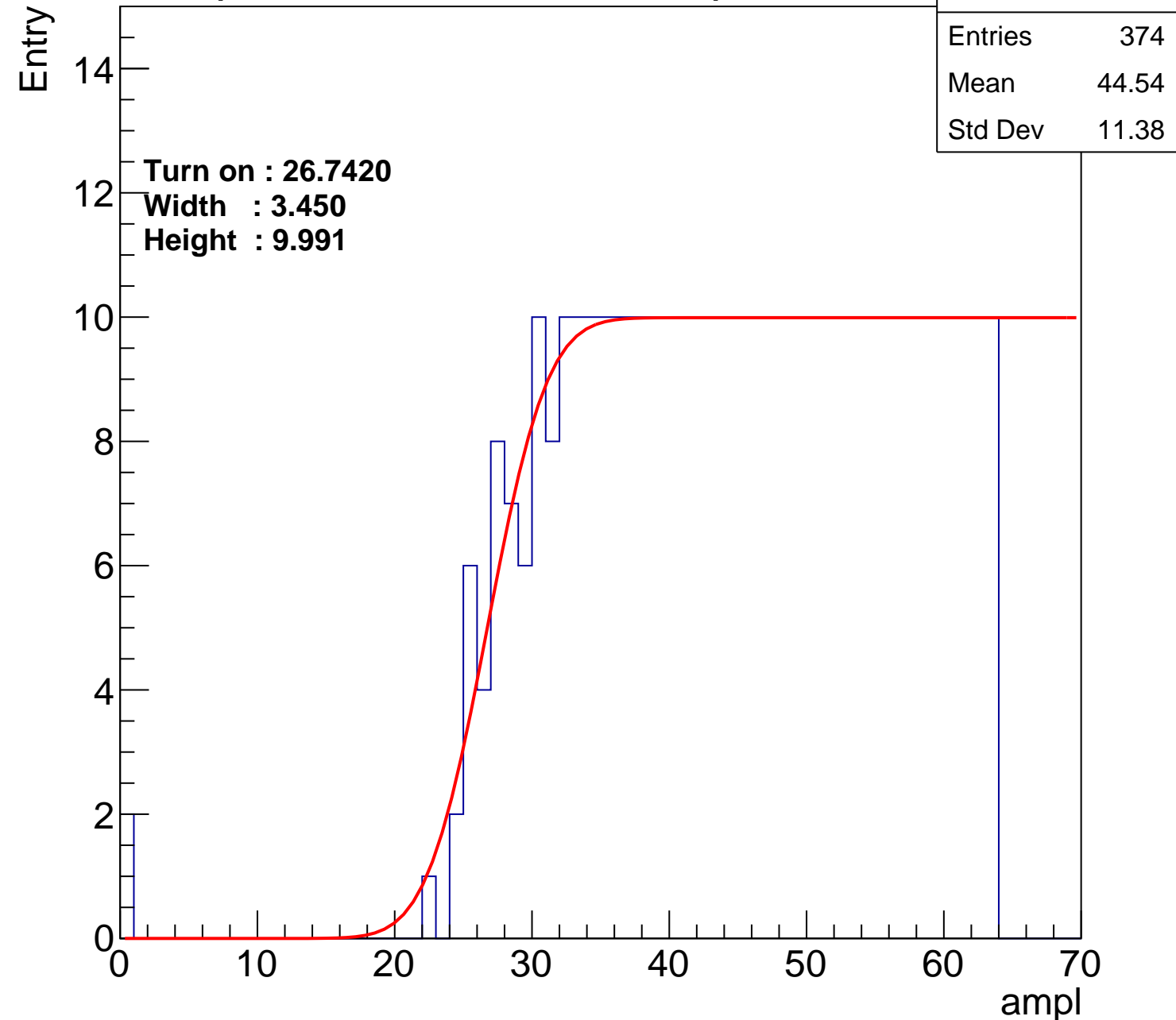
Width : 3.450

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch113

calib_packv5_042523_0143.root, FC#13, port D2

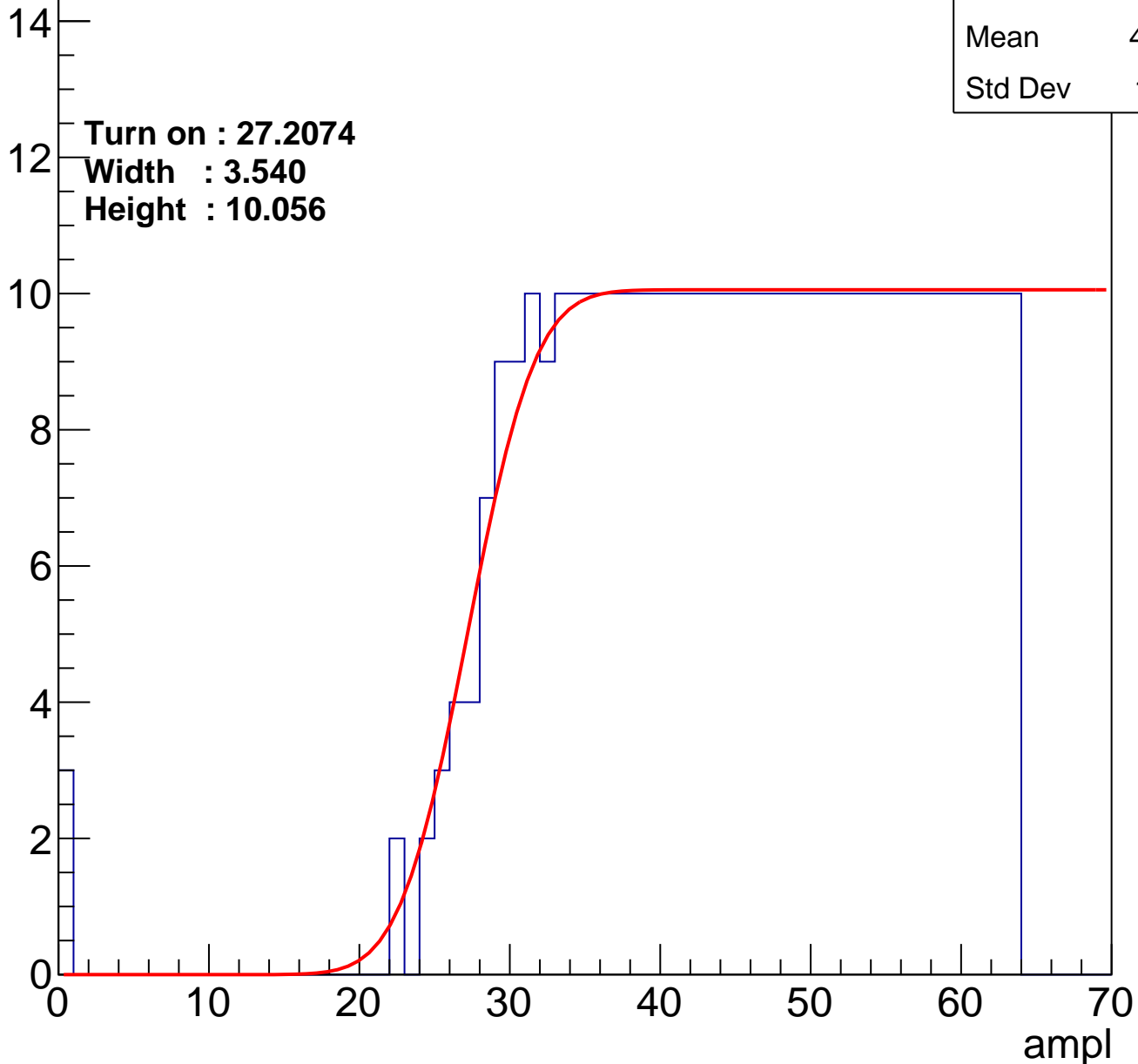
Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.2074

Width : 3.540

Height : 10.056

Entry



B1L003S, U19-ch114

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.42
Std Dev	11.43

Turn on : 26.1620

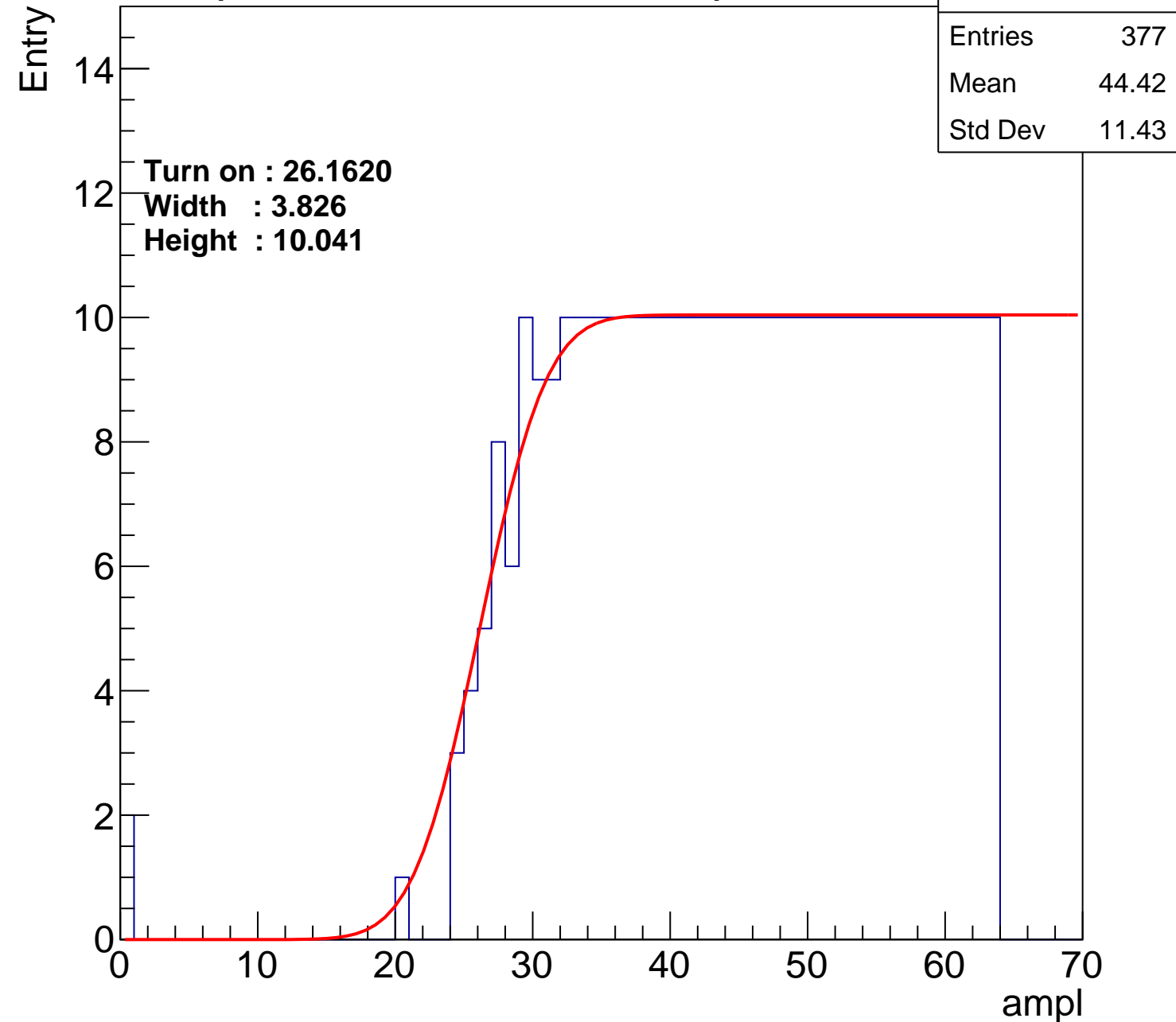
Width : 3.826

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch115

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 27.1749

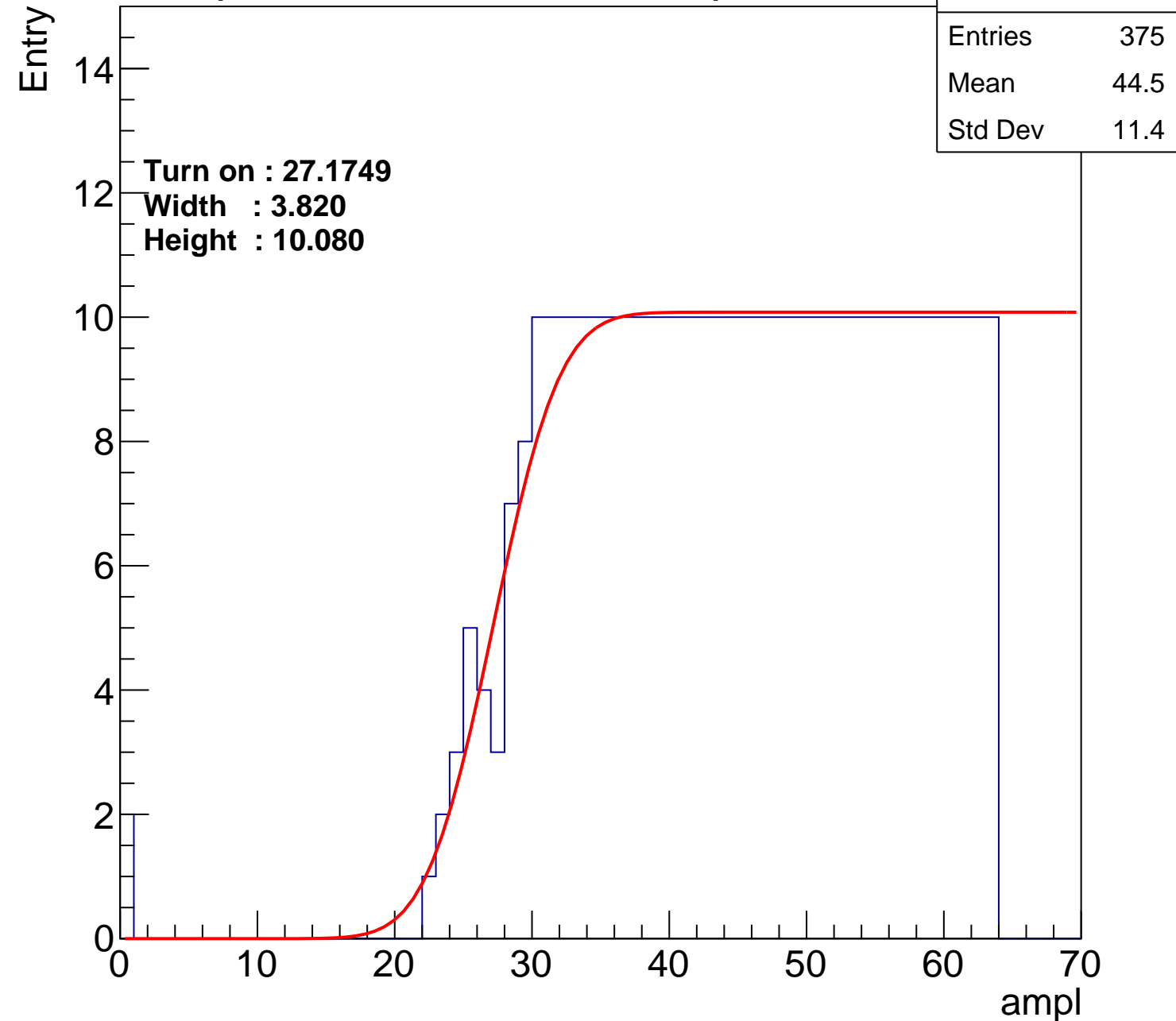
Width : 3.820

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.74
Std Dev	11.65

Turn on : 25.5180

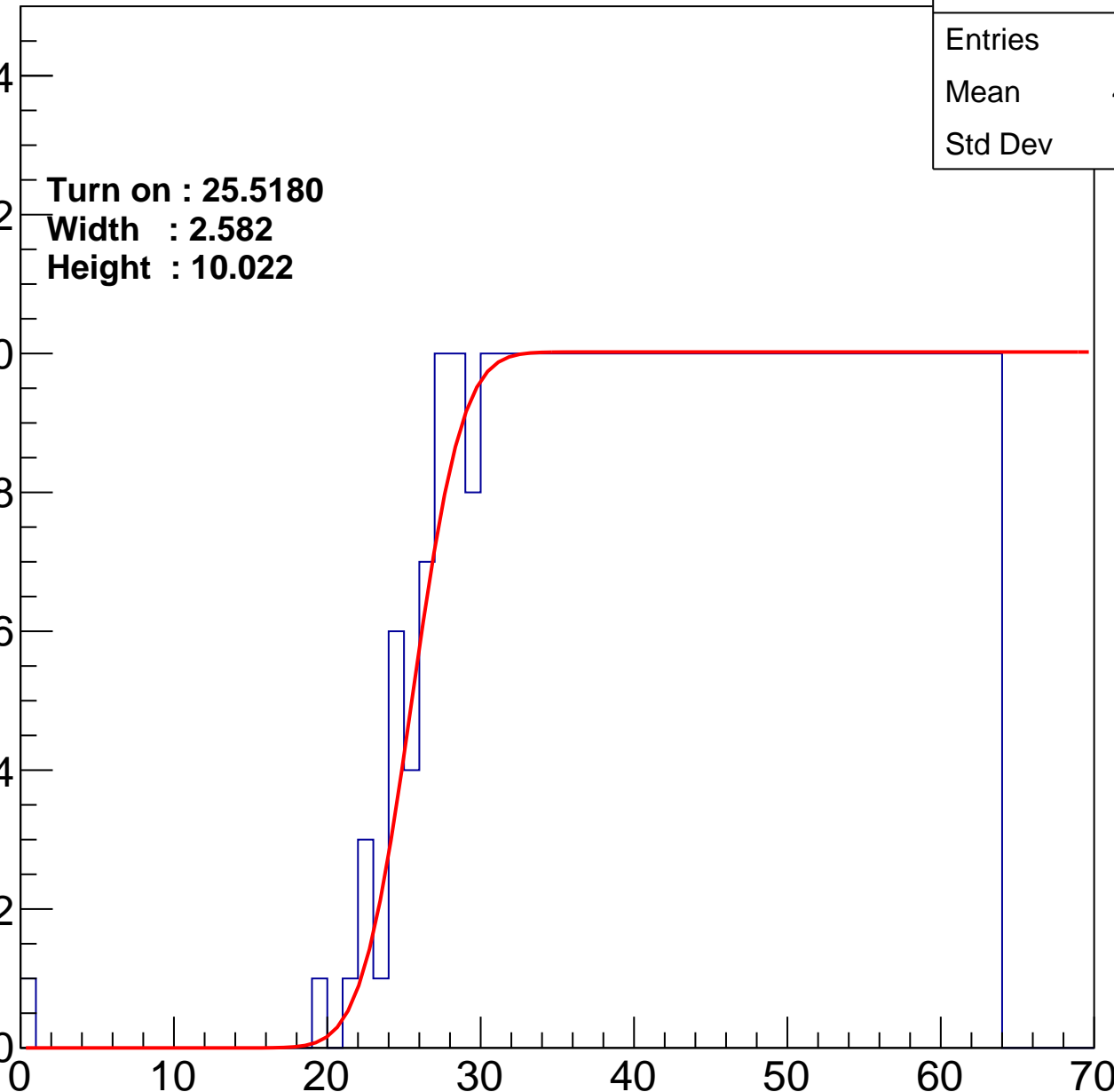
Width : 2.582

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch117

calib_packv5_042523_0143.root, FC#13, port D2

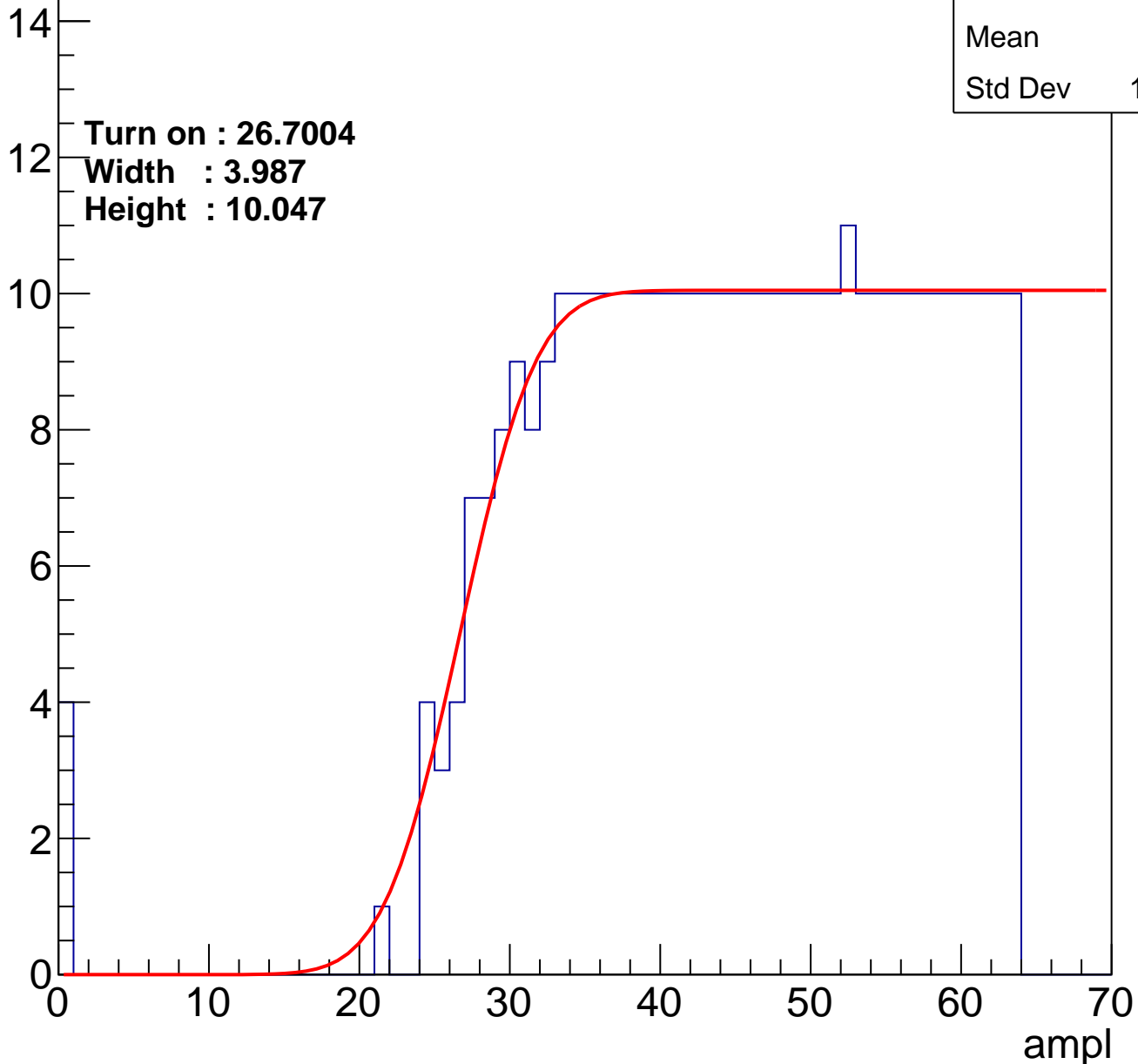
Entries	375
Mean	44.4
Std Dev	11.78

Turn on : 26.7004

Width : 3.987

Height : 10.047

Entry



B1L003S, U19-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.13
Std Dev	11.09

Turn on : 28.2998

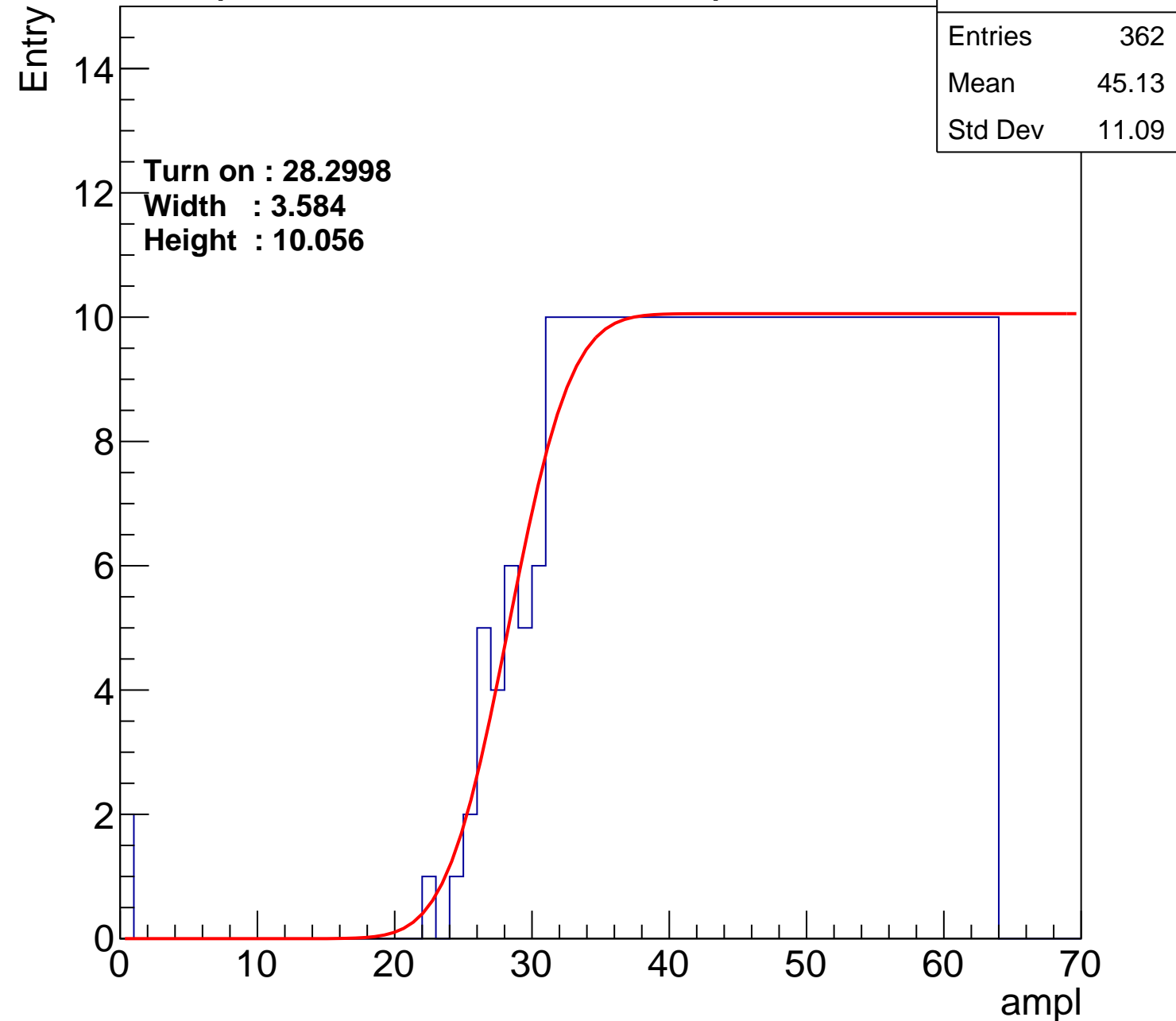
Width : 3.584

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch119

calib_packv5_042523_0143.root, FC#13, port D2

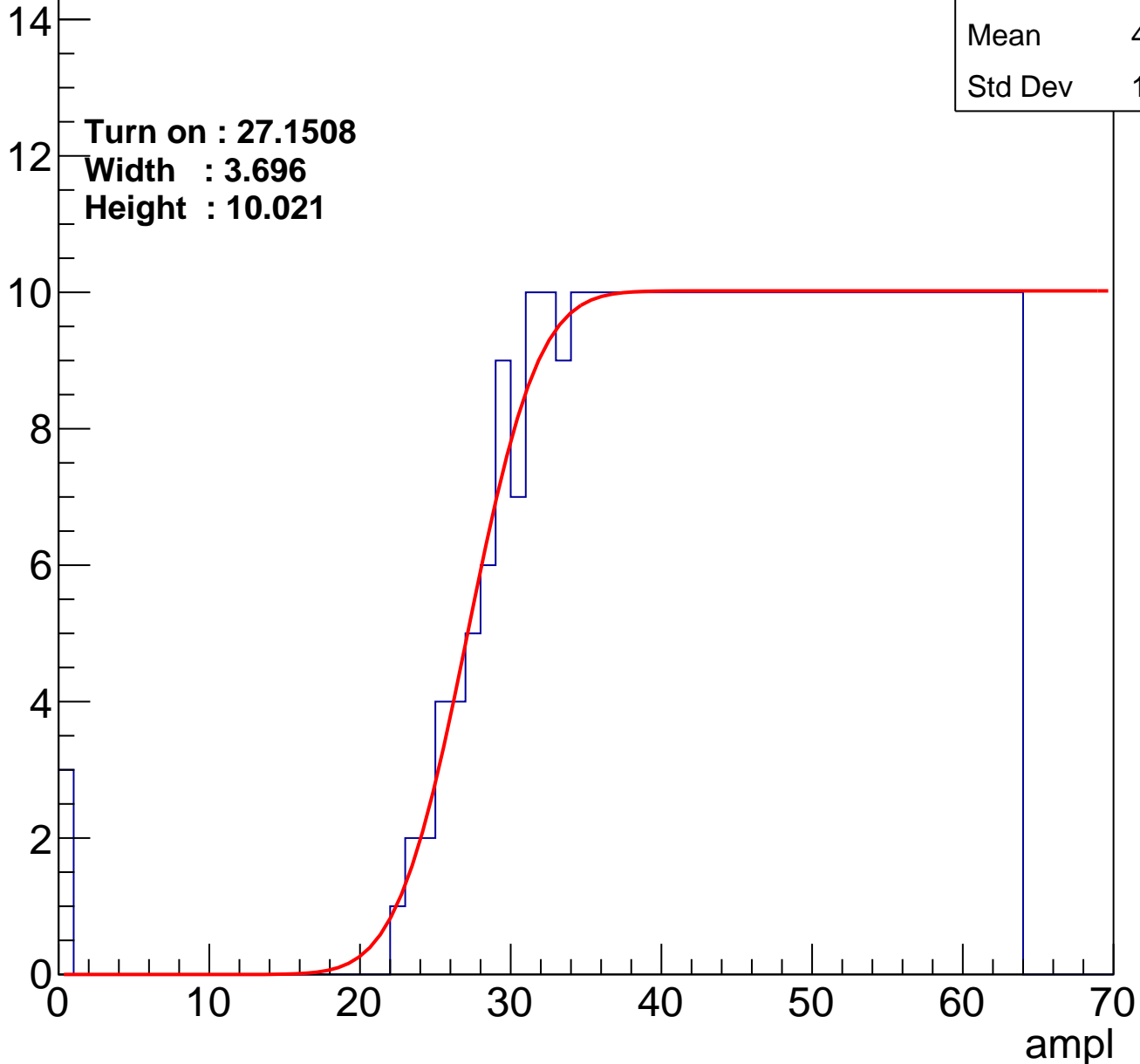
Entries	372
Mean	44.55
Std Dev	11.56

Turn on : 27.1508

Width : 3.696

Height : 10.021

Entry



B1L003S, U19-ch120

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.13
Std Dev	11.87

Turn on : 26.6896

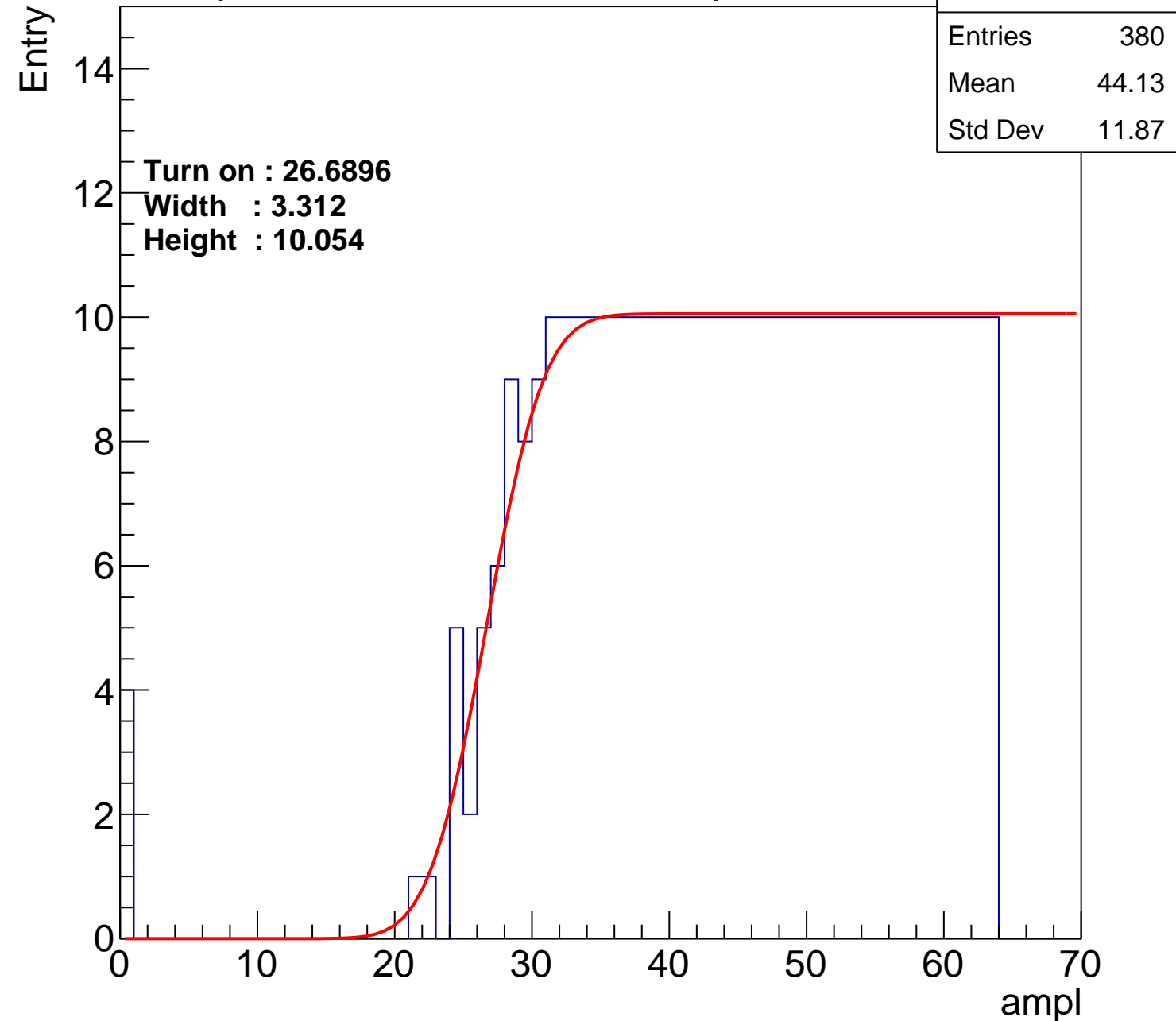
Width : 3.312

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch121

calib_packv5_042523_0143.root, FC#13, port D2

Entries	395
Mean	43.42
Std Dev	12.19

Turn on : 24.8614

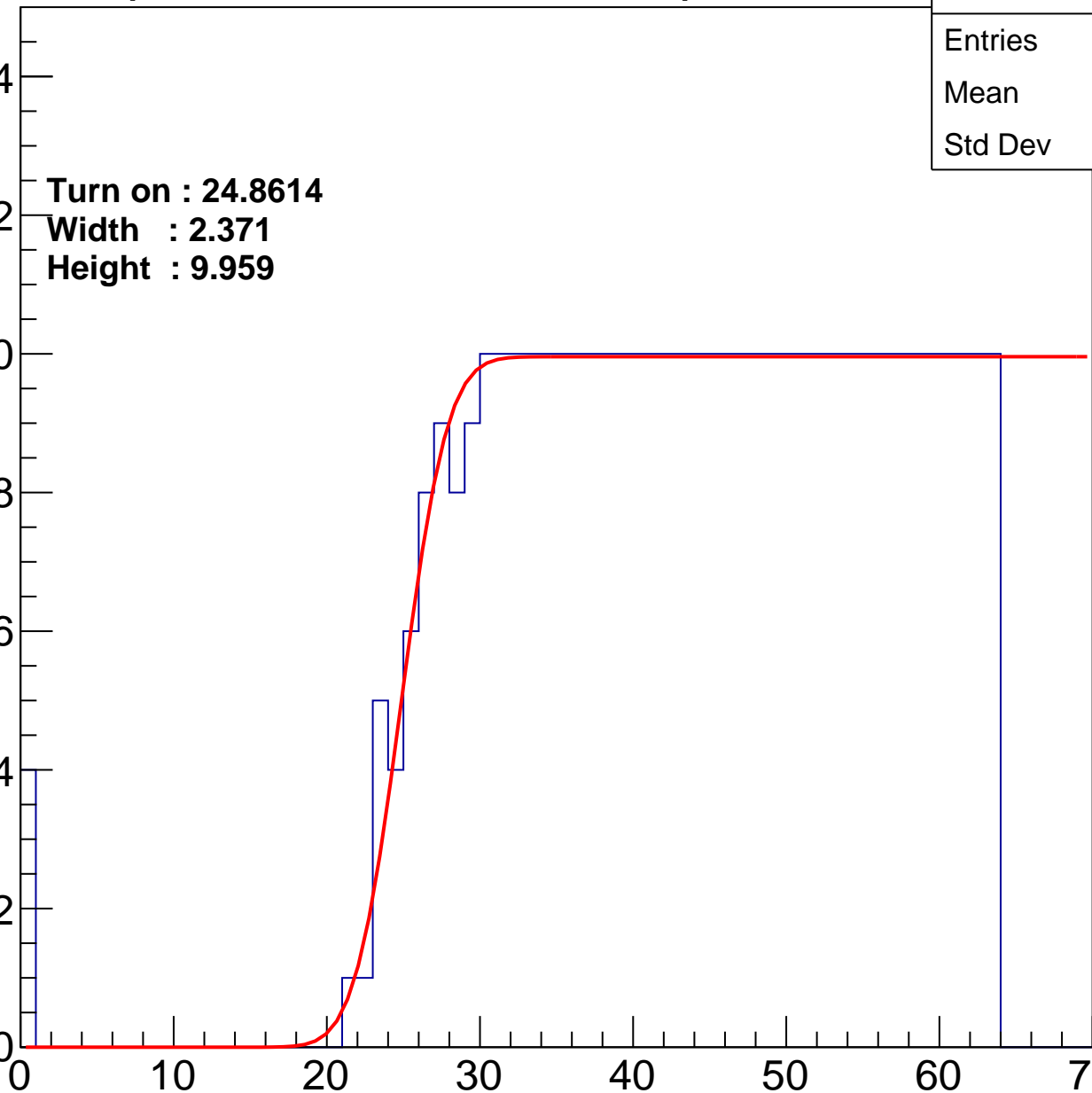
Width : 2.371

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch122

calib_packv5_042523_0143.root, FC#13, port D2

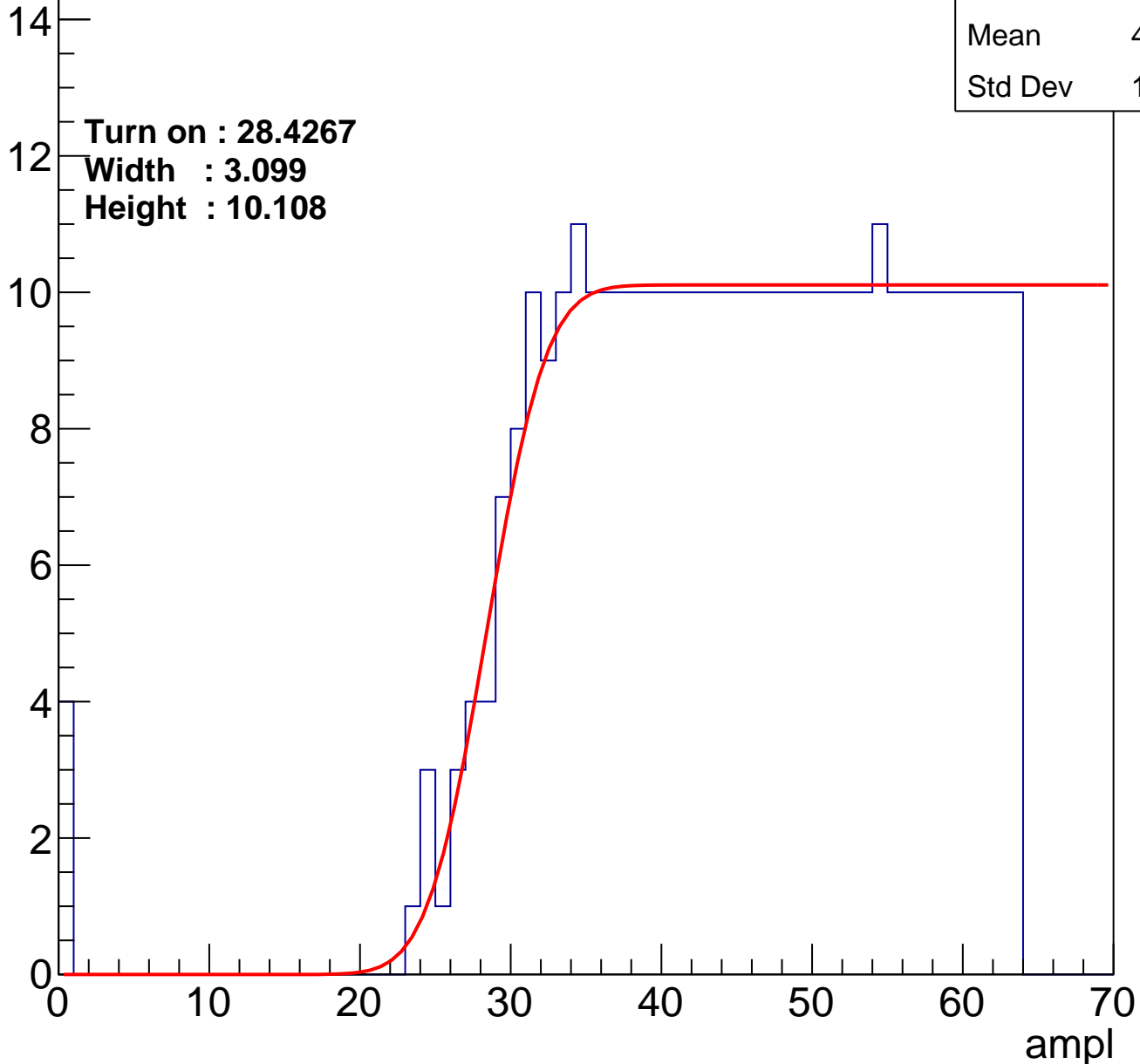
Entries	366
Mean	44.88
Std Dev	11.54

Turn on : 28.4267

Width : 3.099

Height : 10.108

Entry



B1L003S, U19-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.86
Std Dev	10.99

Turn on : 26.9415

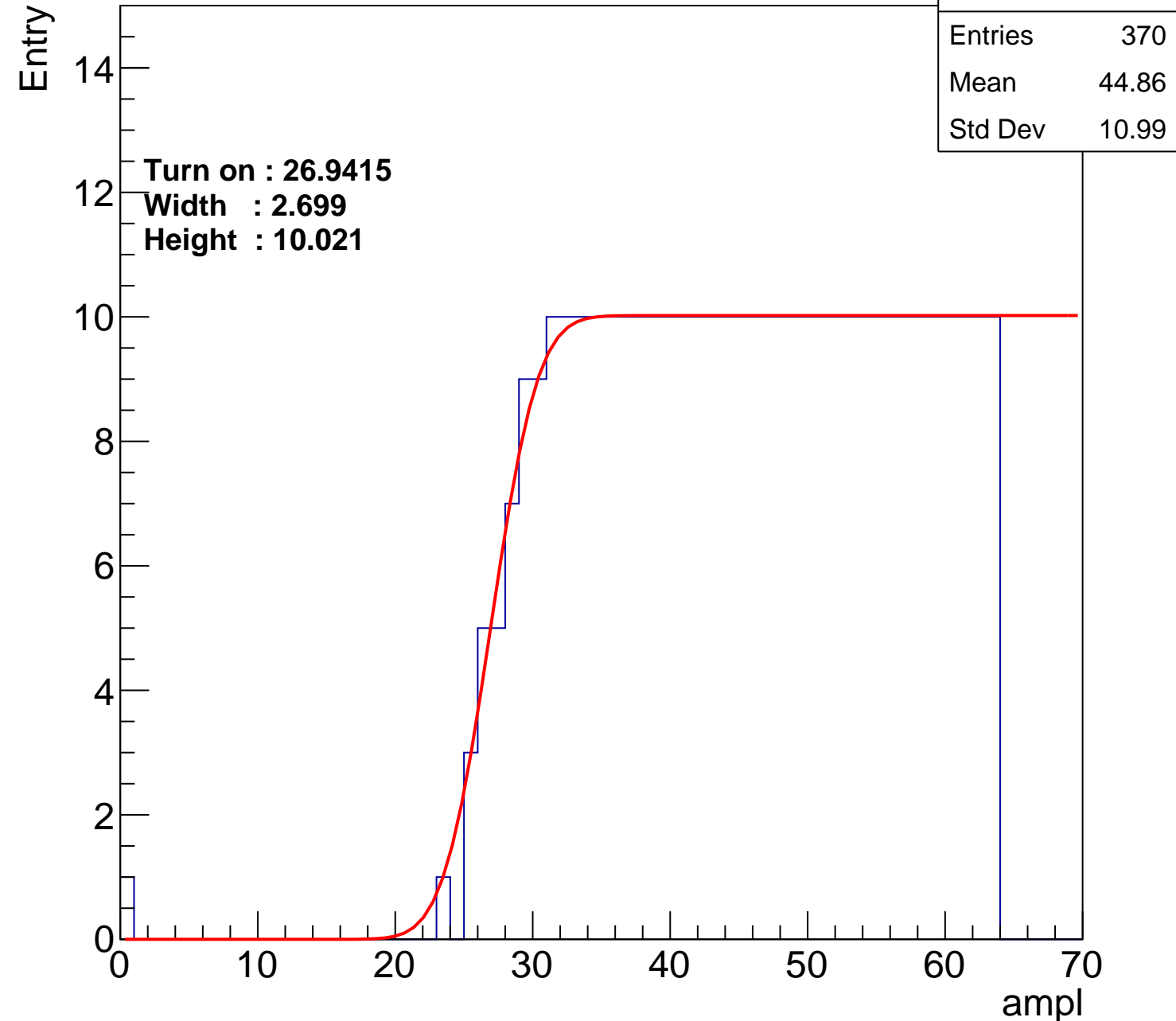
Width : 2.699

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 28.0857

Width : 3.236

Height : 10.097

Entries	369
Mean	44.89
Std Dev	11.01



B1L003S, U19-ch125

calib_packv5_042523_0143.root, FC#13, port D2

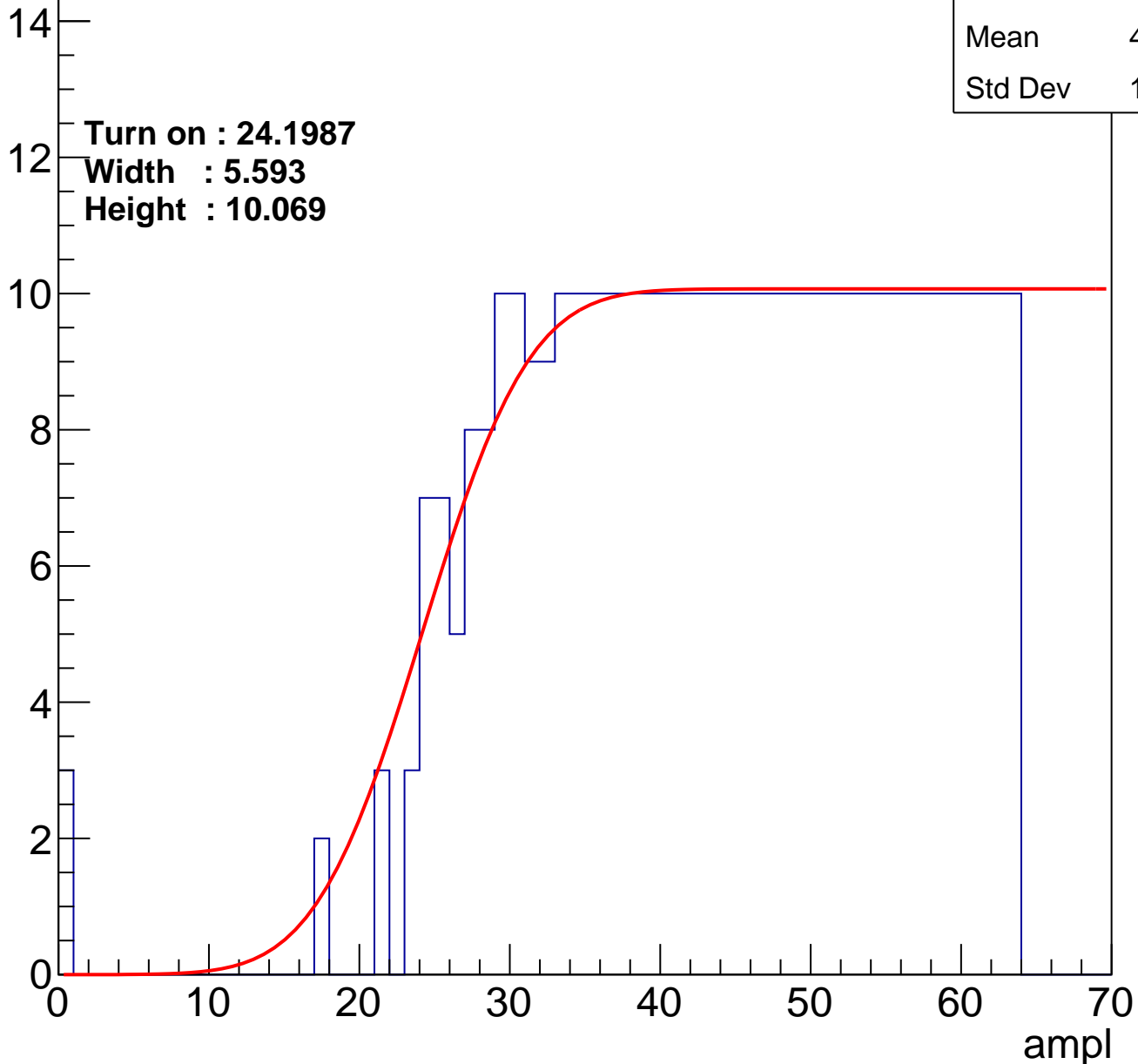
Entries	394
Mean	43.44
Std Dev	12.15

Turn on : 24.1987

Width : 5.593

Height : 10.069

Entry



B1L003S, U19-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.83
Std Dev	11.26

Turn on : 28.7068

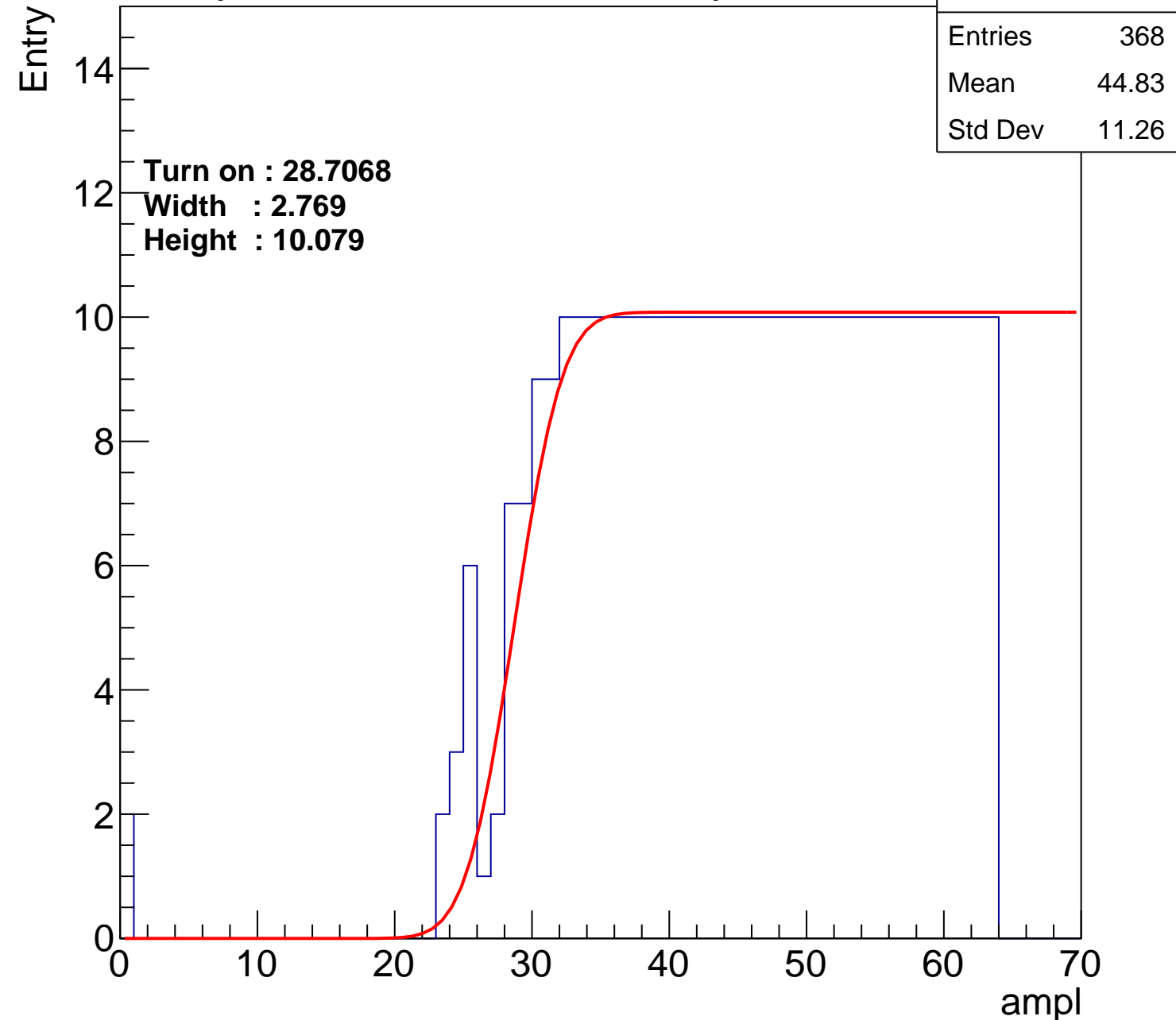
Width : 2.769

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.19
Std Dev	11.24

Turn on : 28.4063

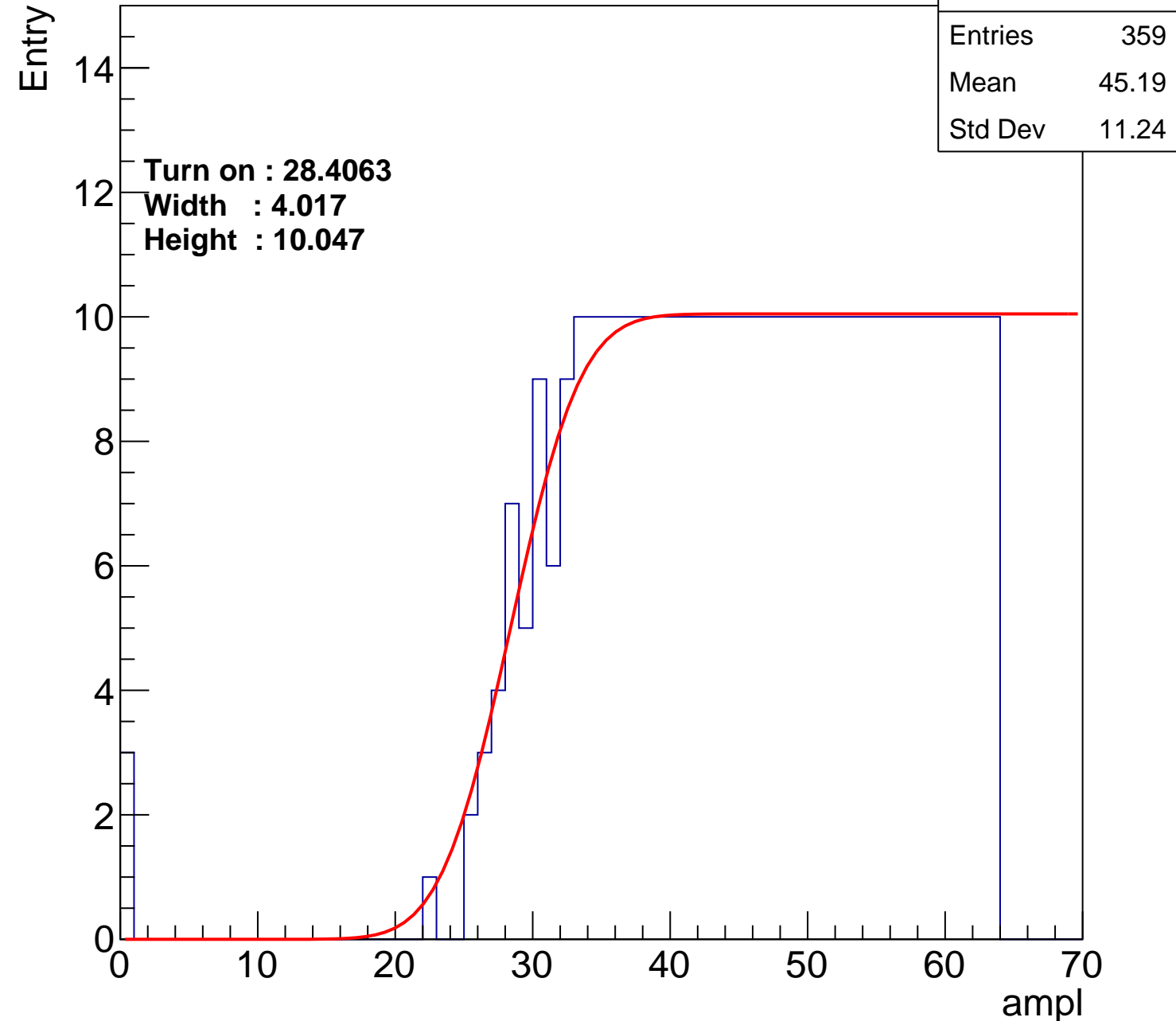
Width : 4.017

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U19-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.19
Std Dev	11.24

Turn on : 28.4063

Width : 4.017

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl

