



# B0L102S, U6-ch0, adc0

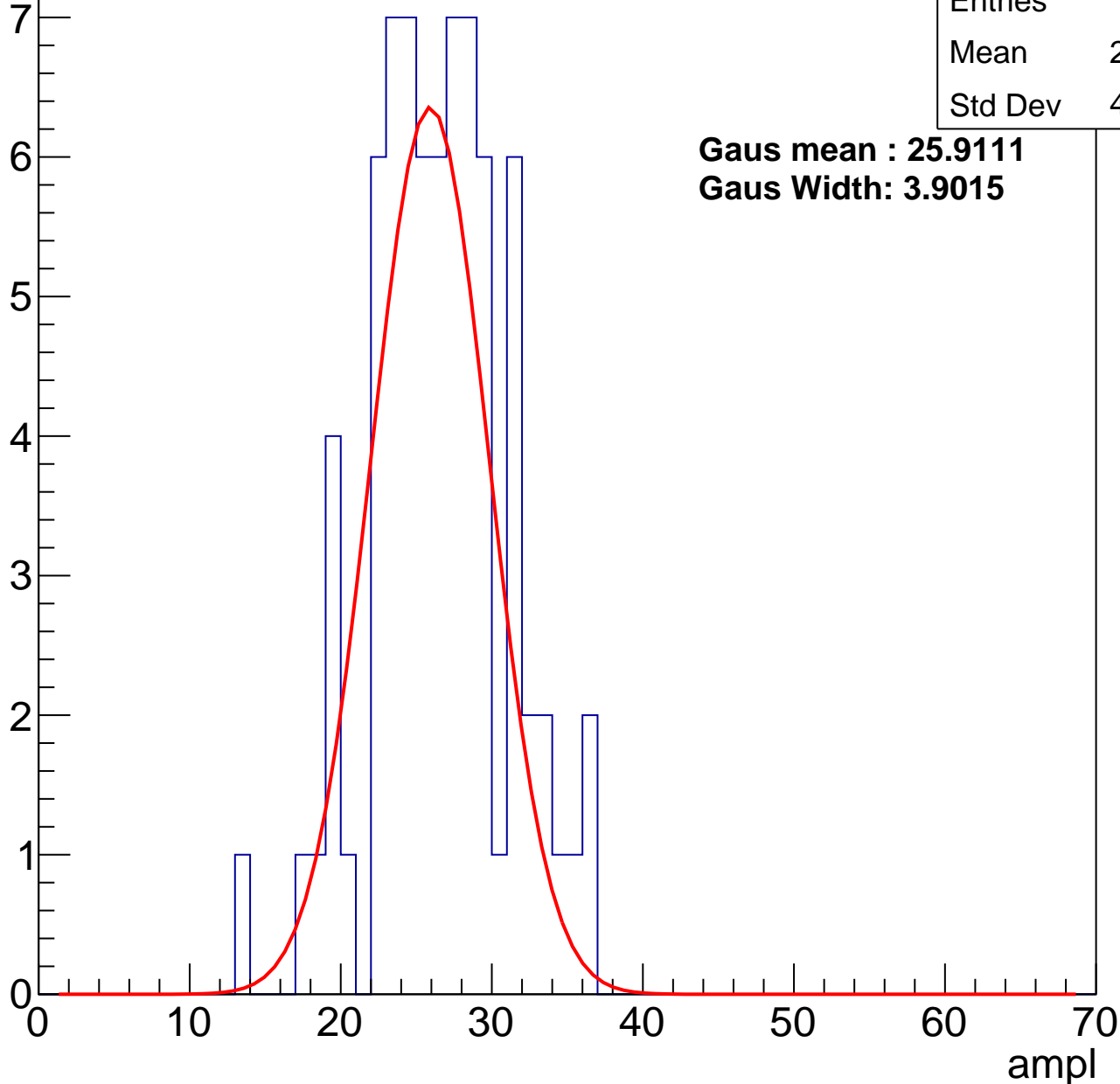
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	26.09
Std Dev	4.485

**Gaus mean : 25.9111**

**Gaus Width: 3.9015**



# B0L102S, U6-ch0, adc1

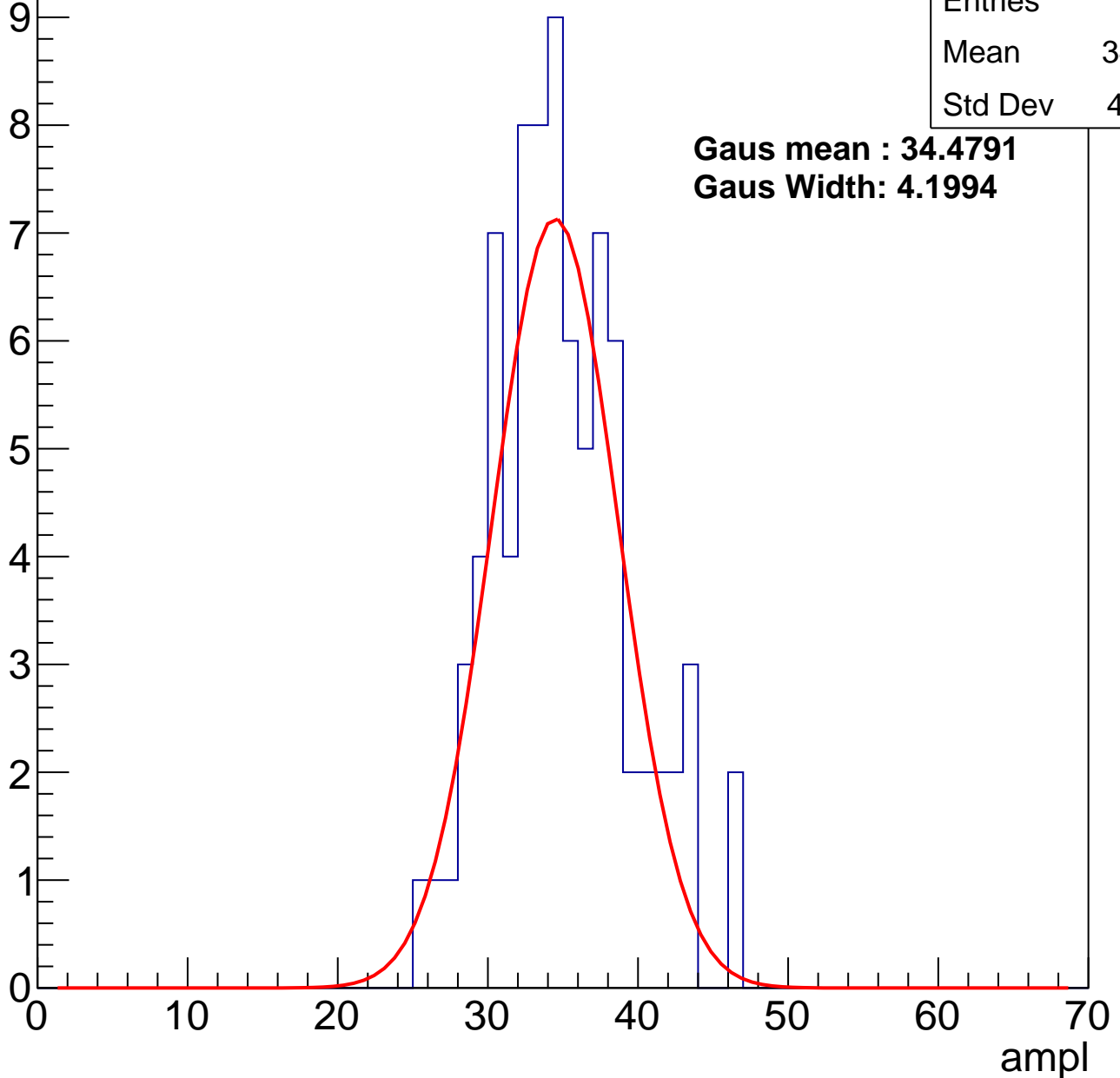
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	83
Mean	34.46
Std Dev	4.441

**Gaus mean : 34.4791**

**Gaus Width: 4.1994**



# B0L102S, U6-ch0, adc2

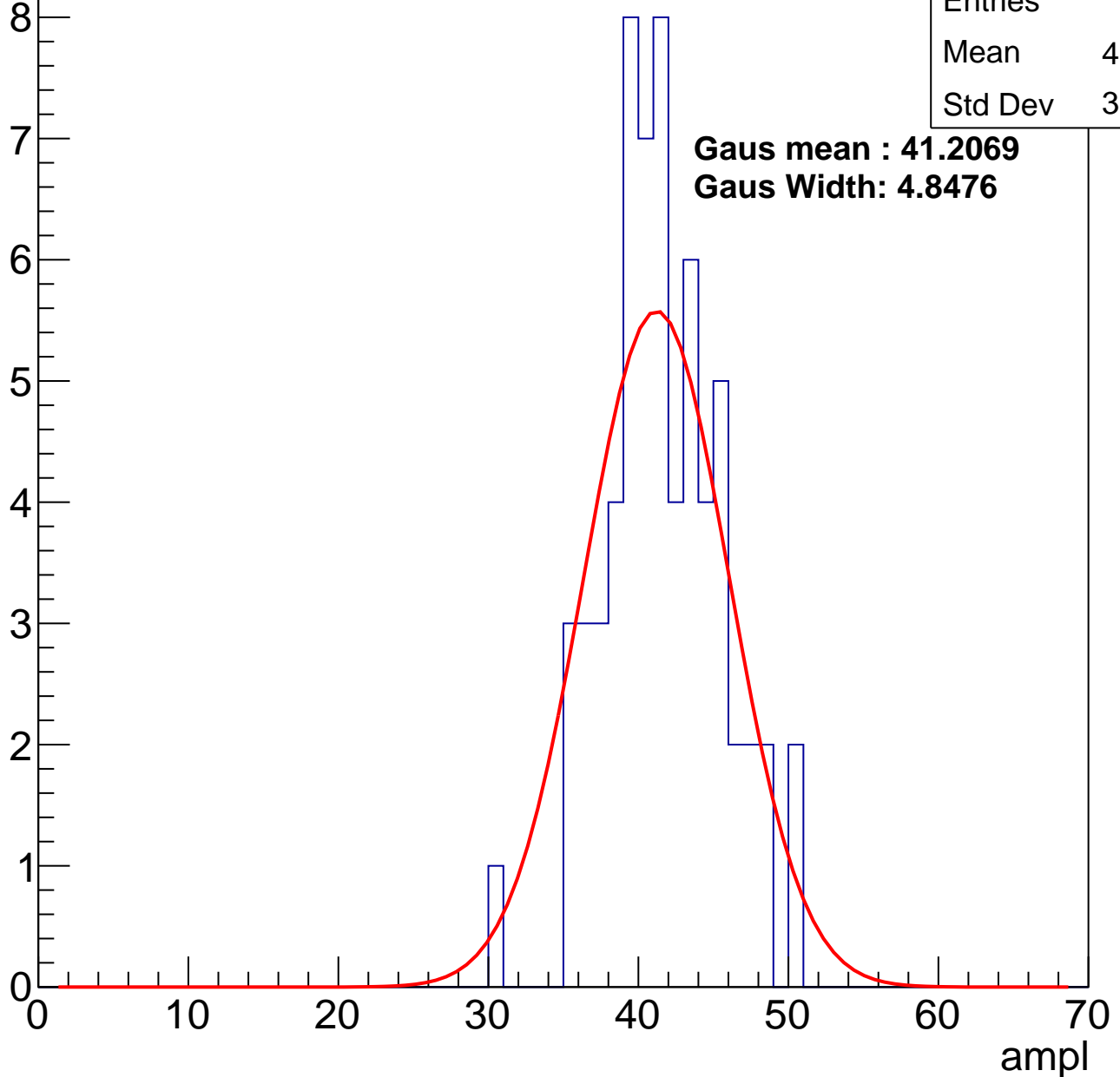
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	41.17
Std Dev	3.855

**Gaus mean : 41.2069**

**Gaus Width: 4.8476**

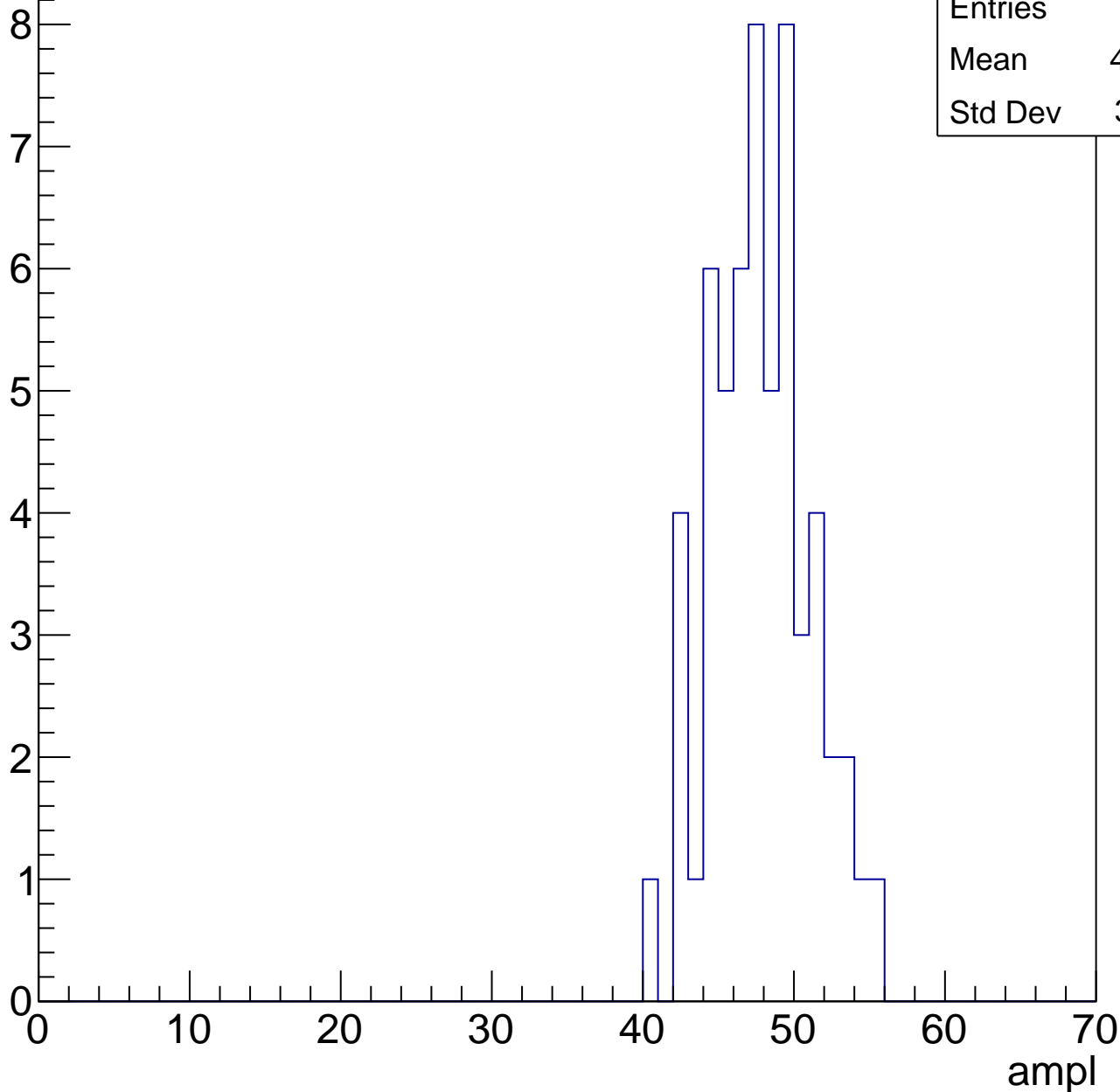


# B0L102S, U6-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

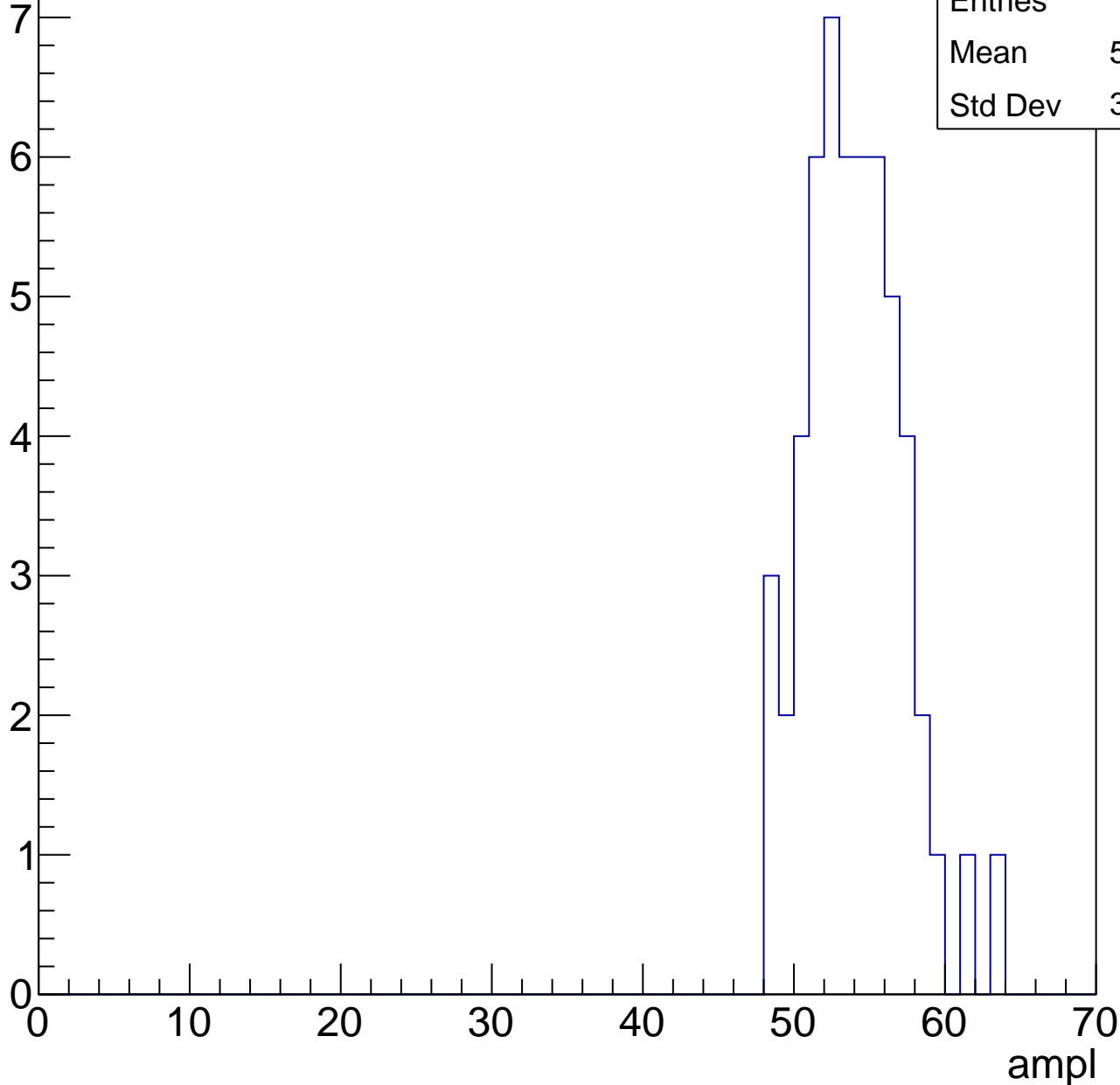
Entries	57
Mean	47.32
Std Dev	3.251



# B0L102S, U6-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



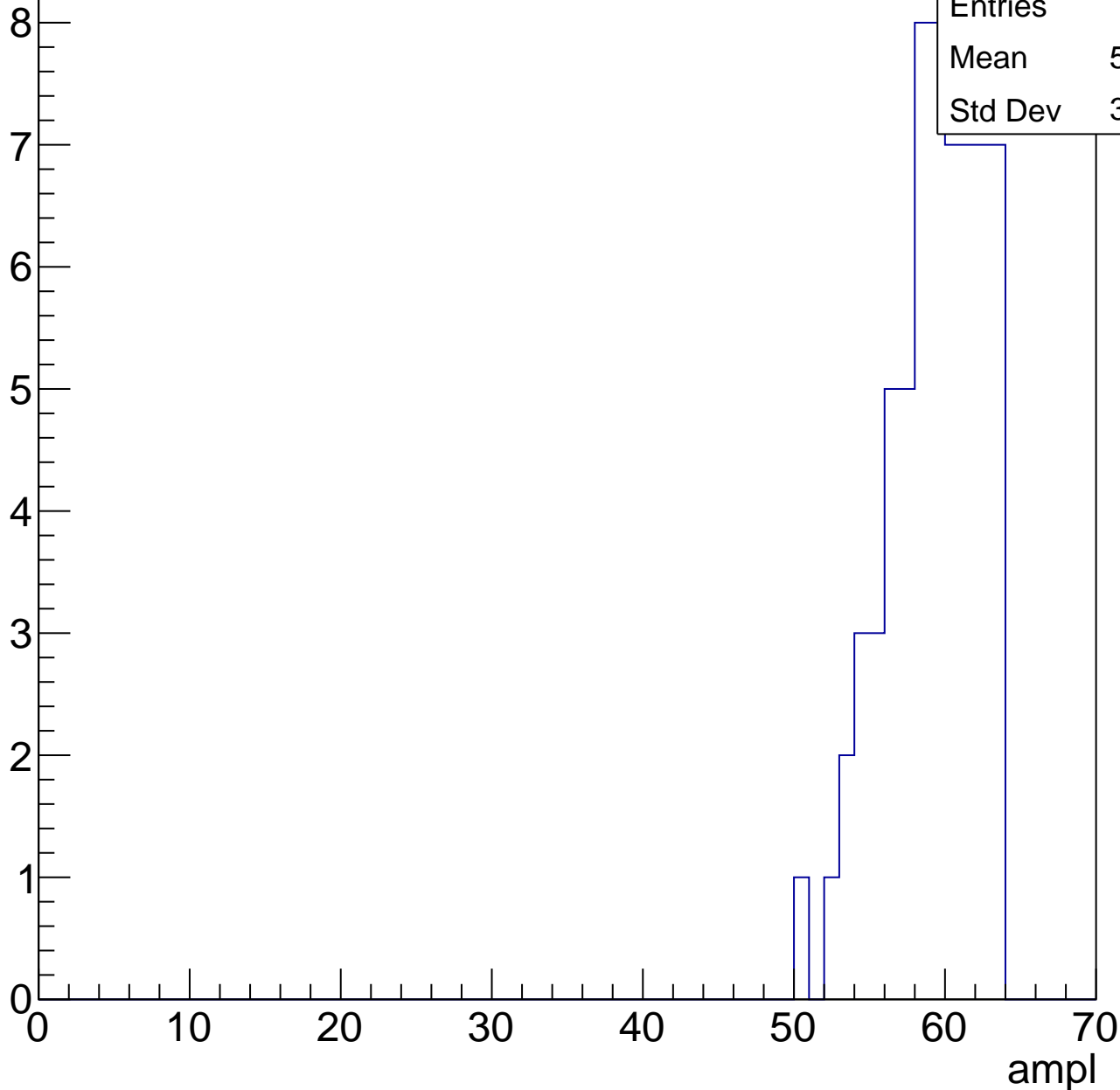
Entries	54
Mean	53.54
Std Dev	3.184

# B0L102S, U6-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

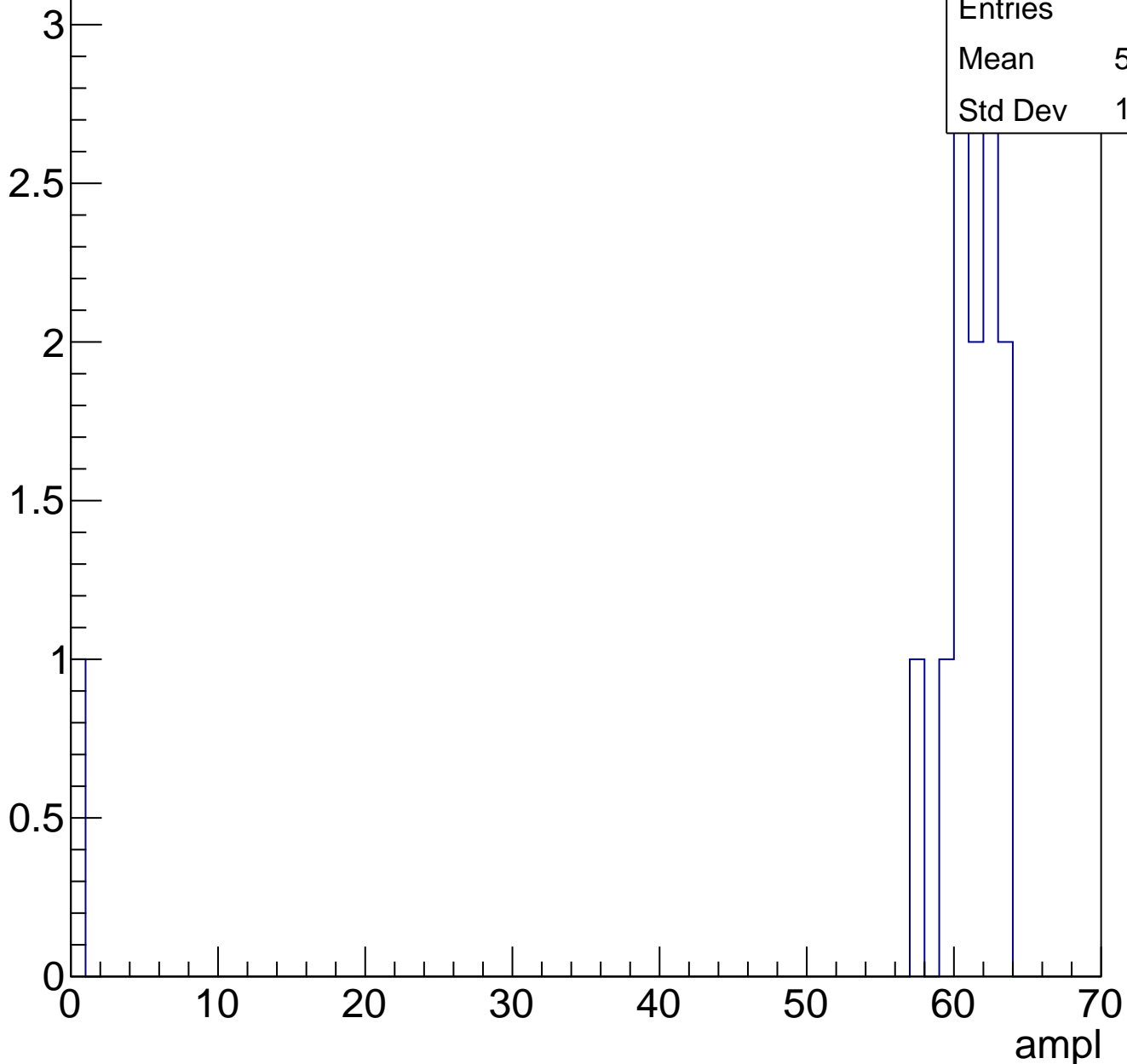
Entries	64
Mean	58.72
Std Dev	3.069



# B0L102S, U6-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

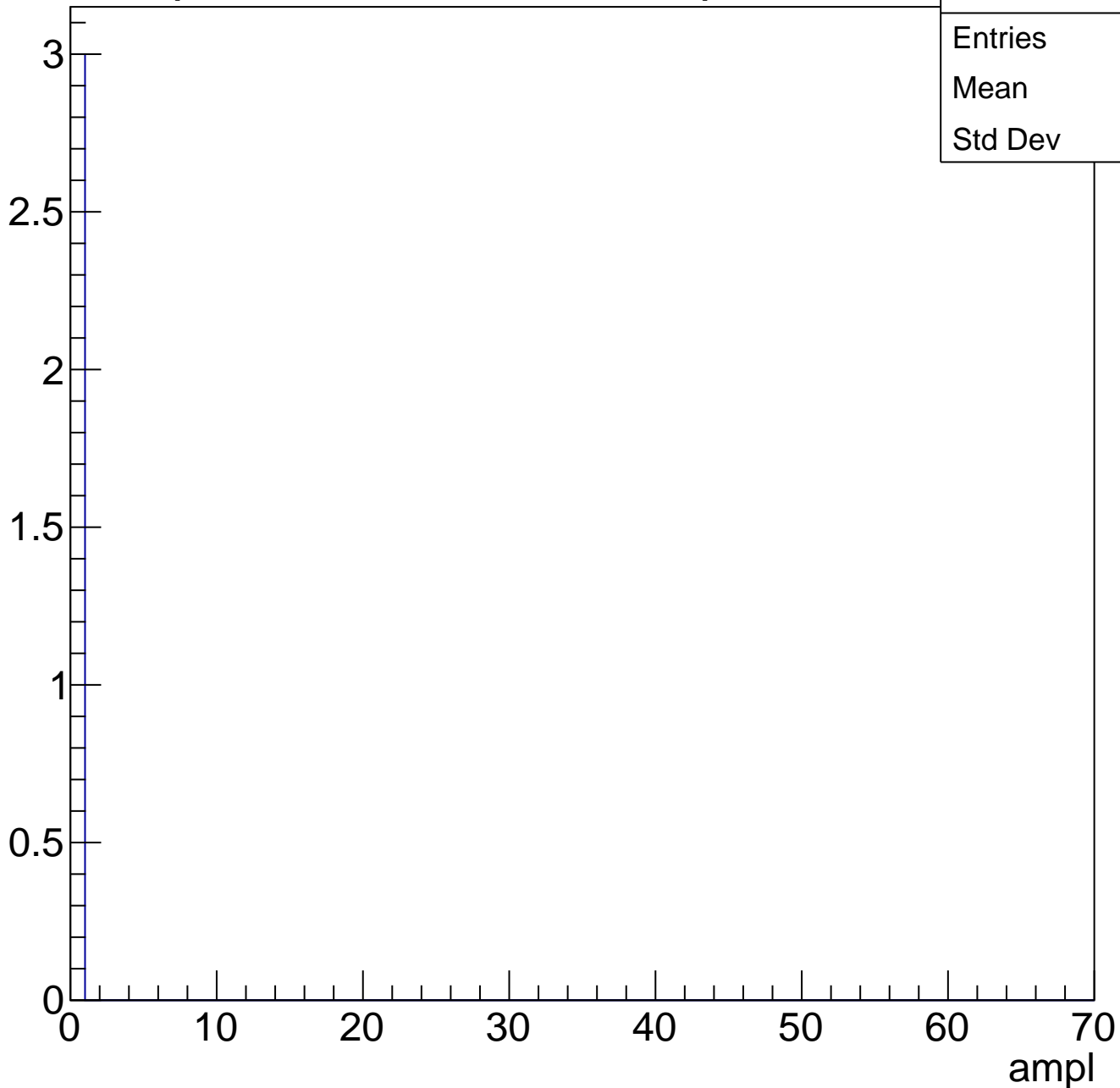




# B0L102S, U6-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L102S, U6-ch1, adc0

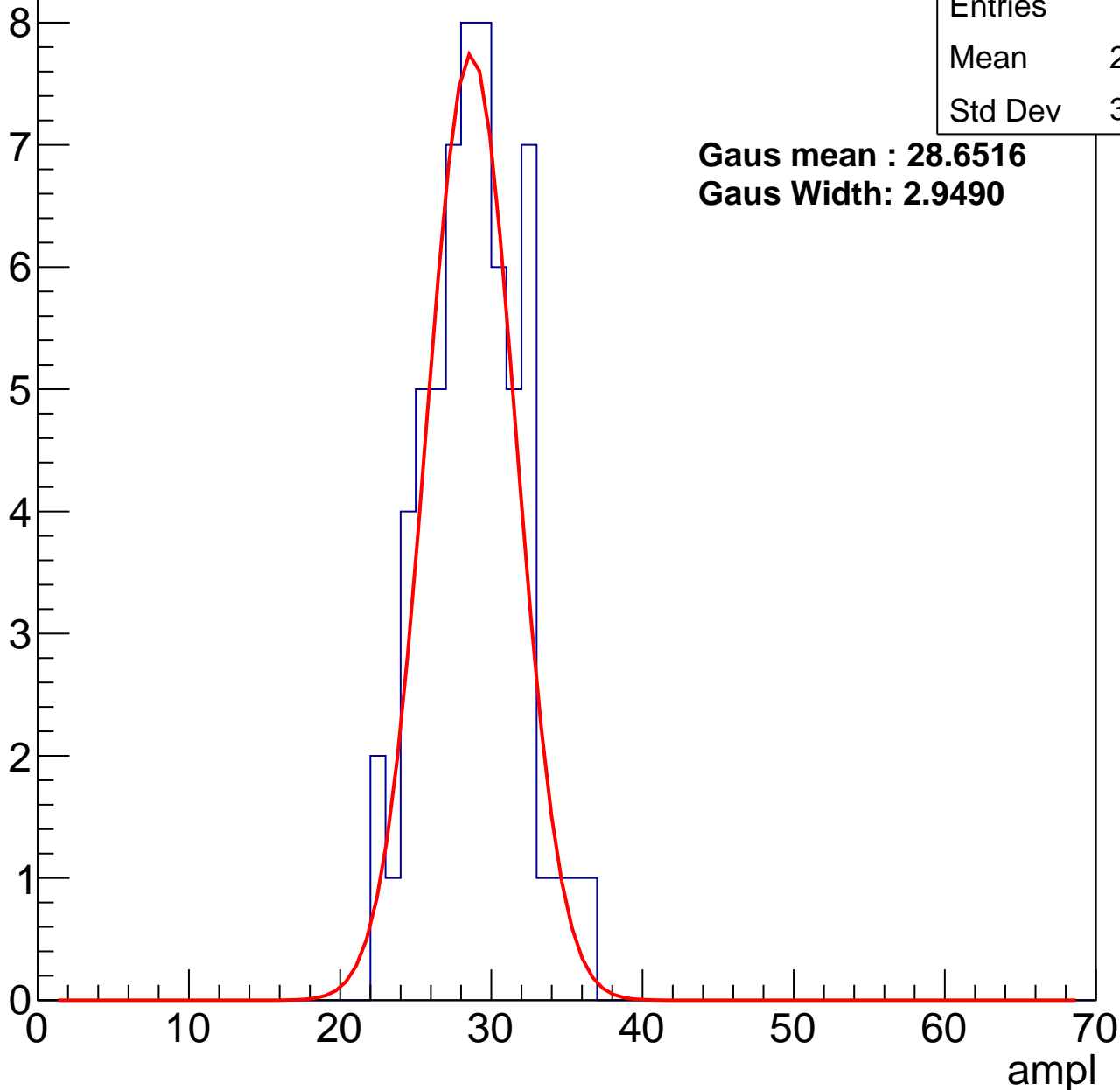
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	28.39
Std Dev	3.076

**Gaus mean : 28.6516**

**Gaus Width: 2.9490**



# B0L102S, U6-ch1, adc1

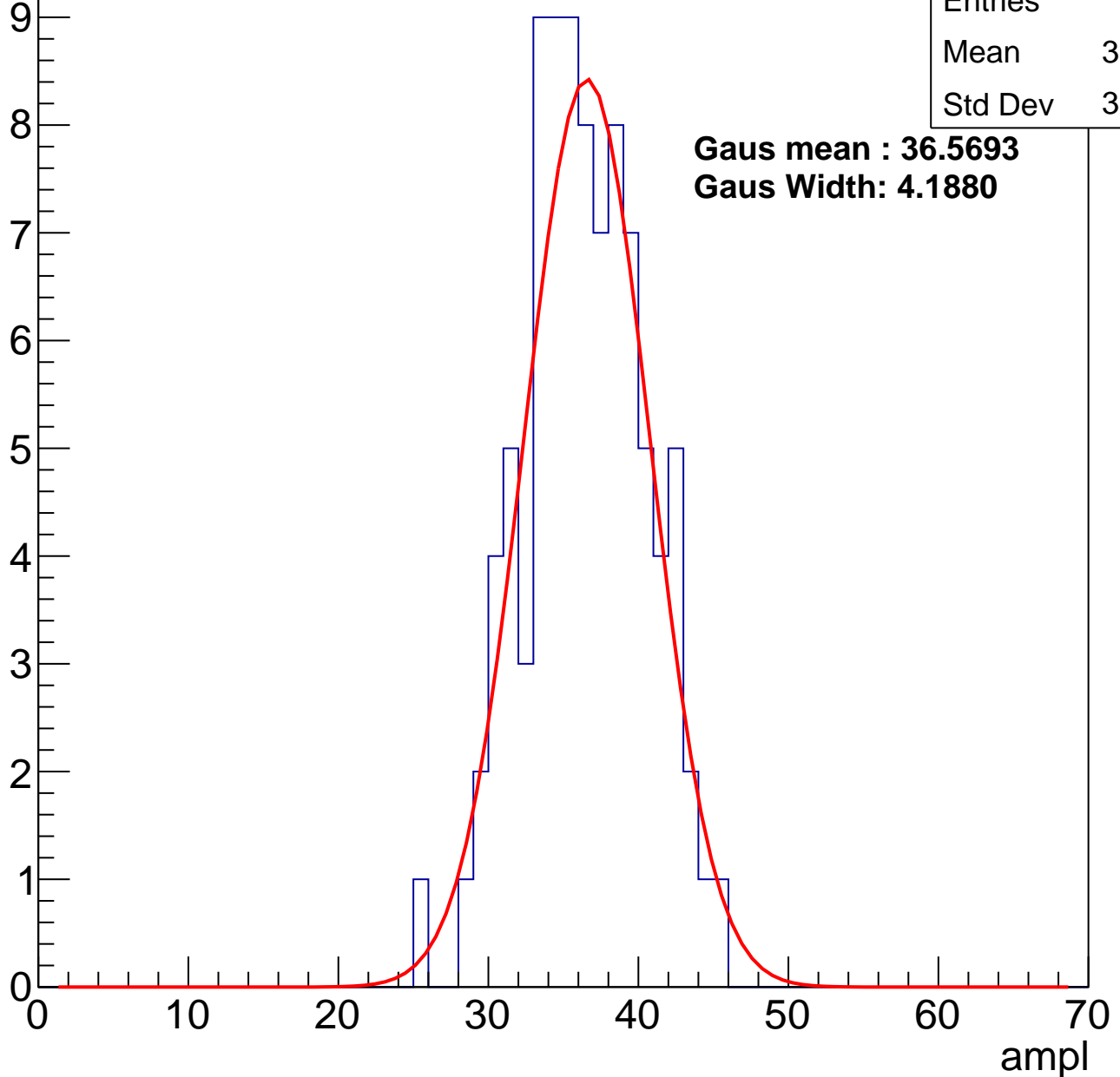
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	91
Mean	35.97
Std Dev	3.965

**Gaus mean : 36.5693**

**Gaus Width: 4.1880**



# B0L102S, U6-ch1, adc2

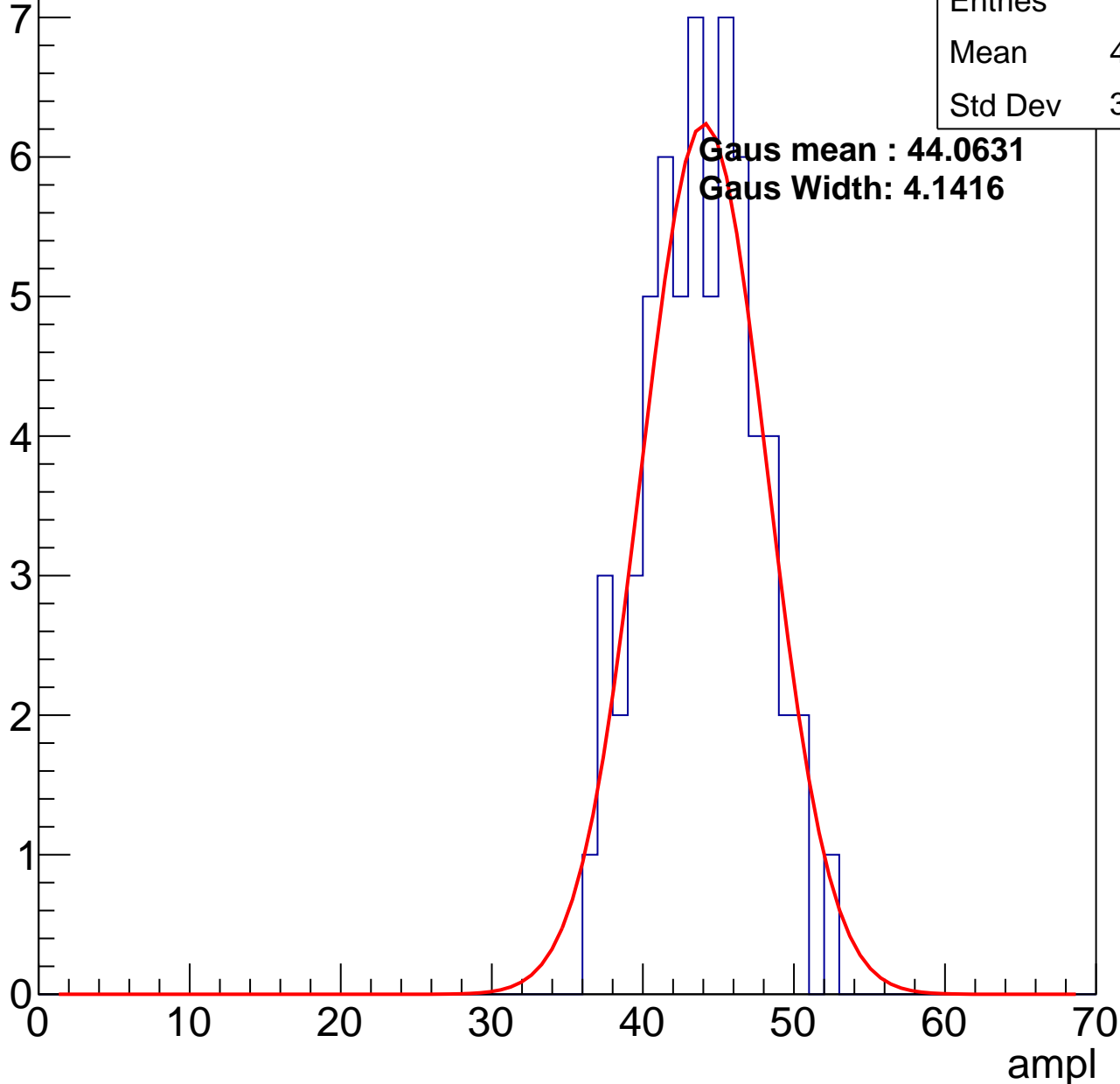
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.46
Std Dev	3.603

**Gaus mean : 44.0631**

**Gaus Width: 4.1416**

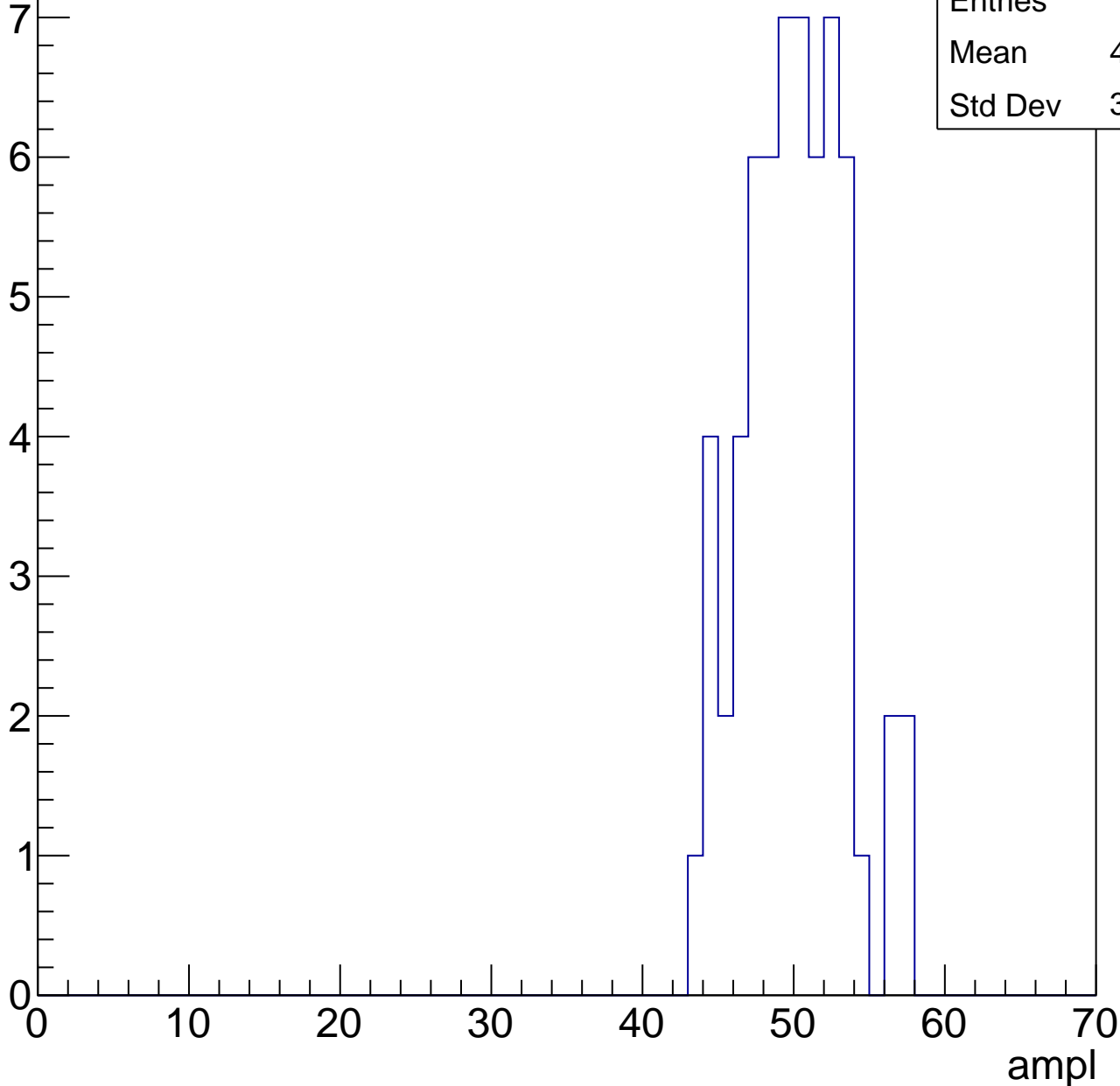


# B0L102S, U6-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

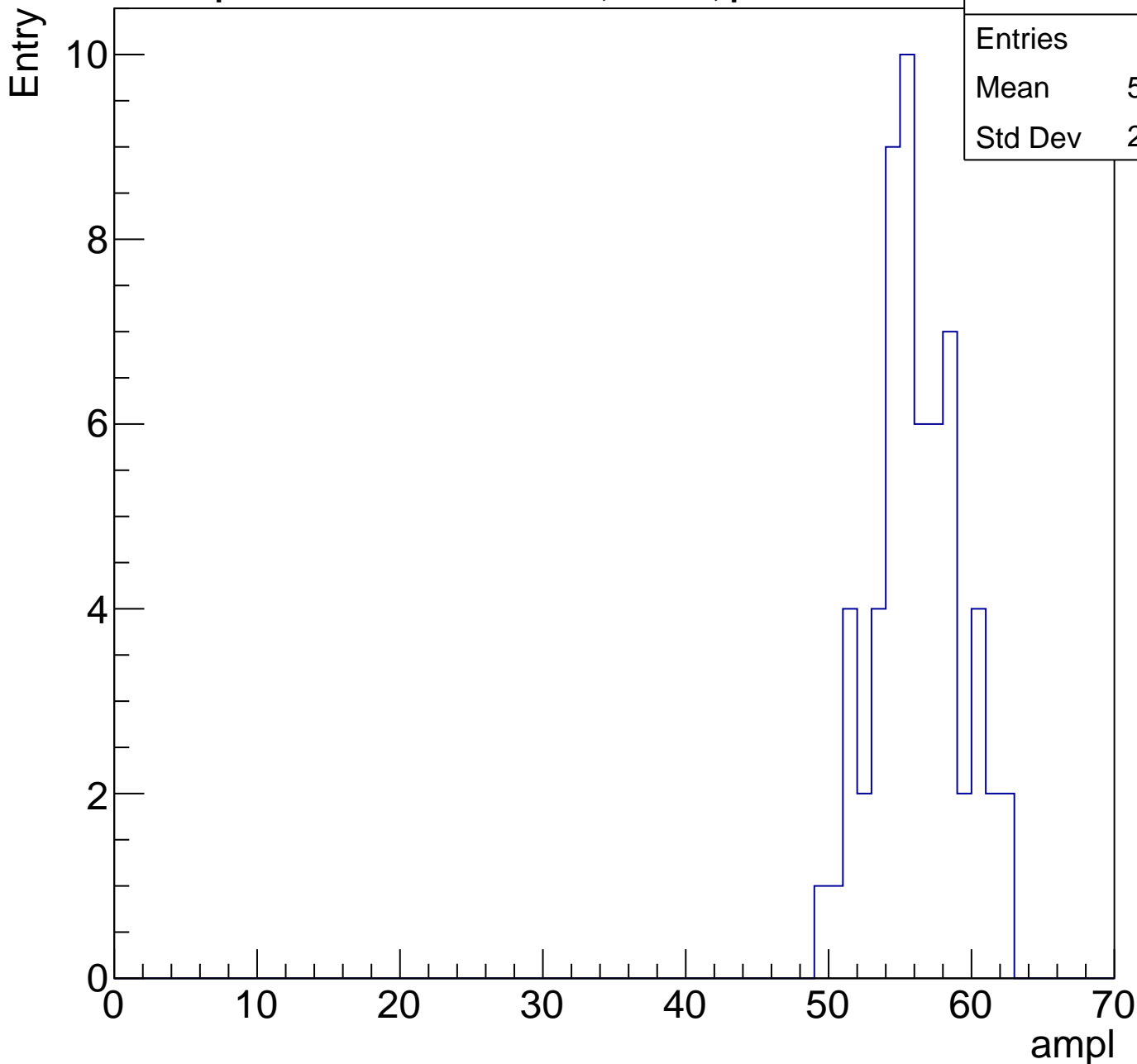
Entries	61
Mean	49.57
Std Dev	3.277



# B0L102S, U6-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

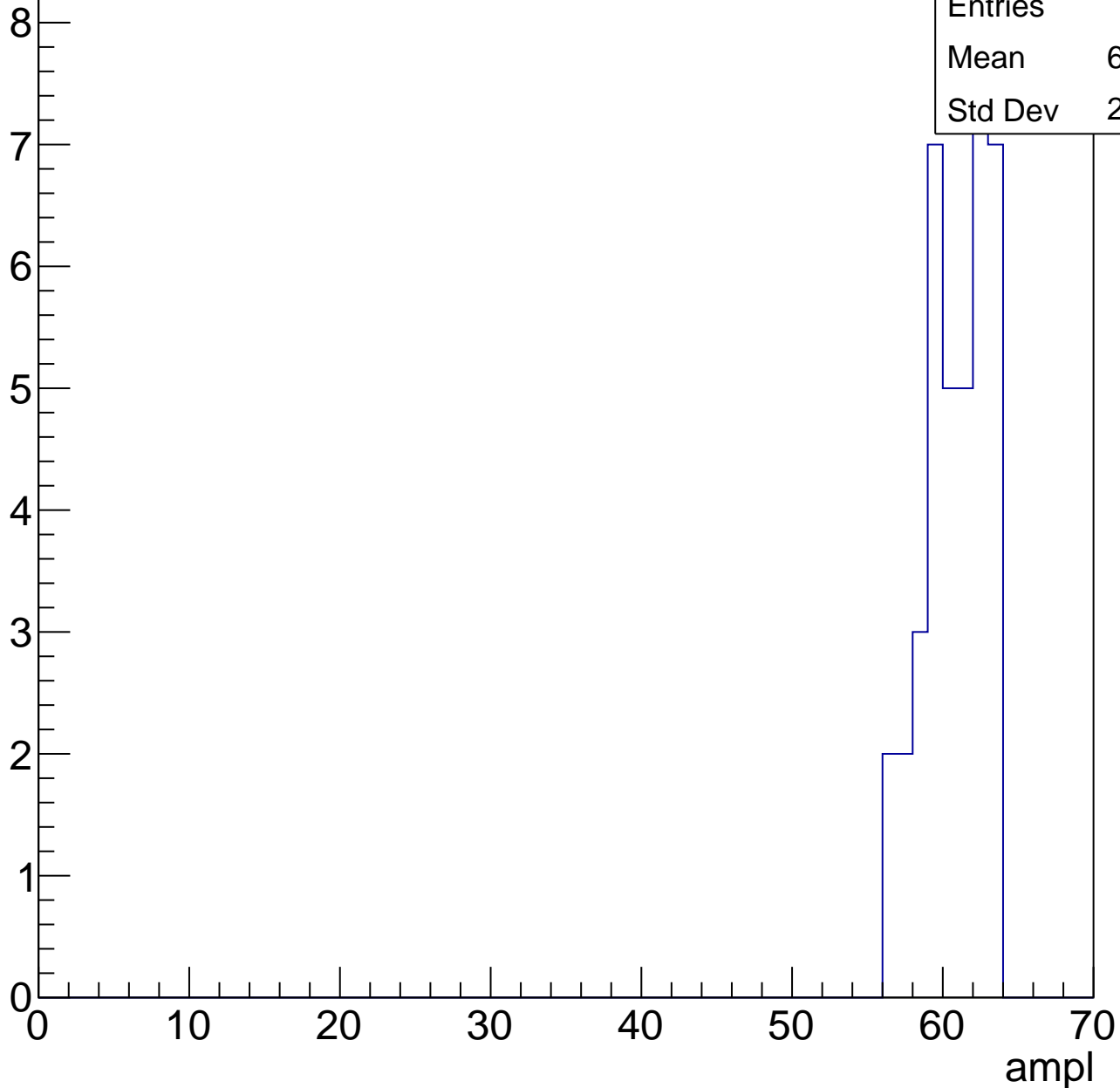
Entries	60
Mean	55.72
Std Dev	2.978



# B0L102S, U6-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

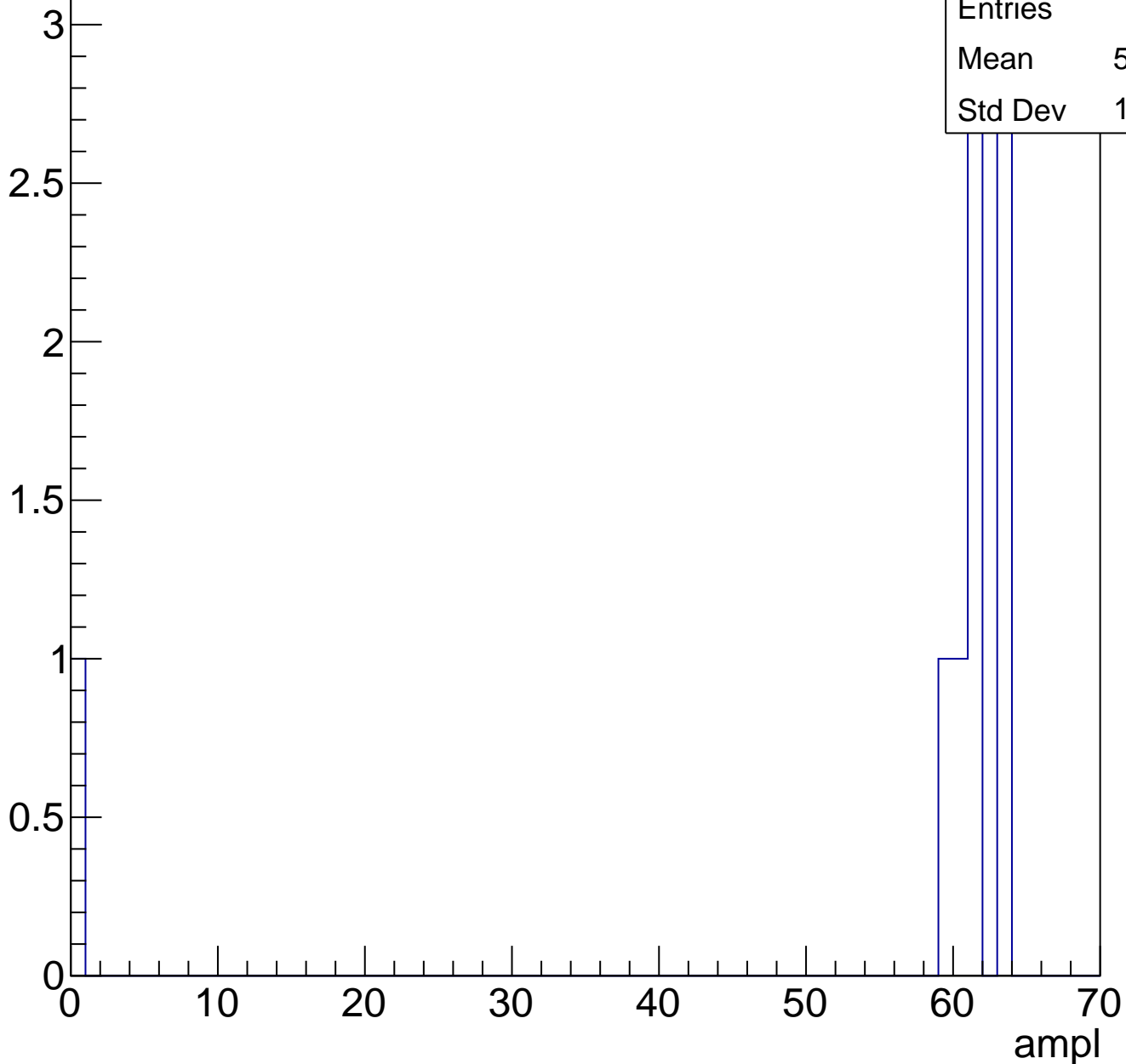
Entry



# B0L102S, U6-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch2, adc0

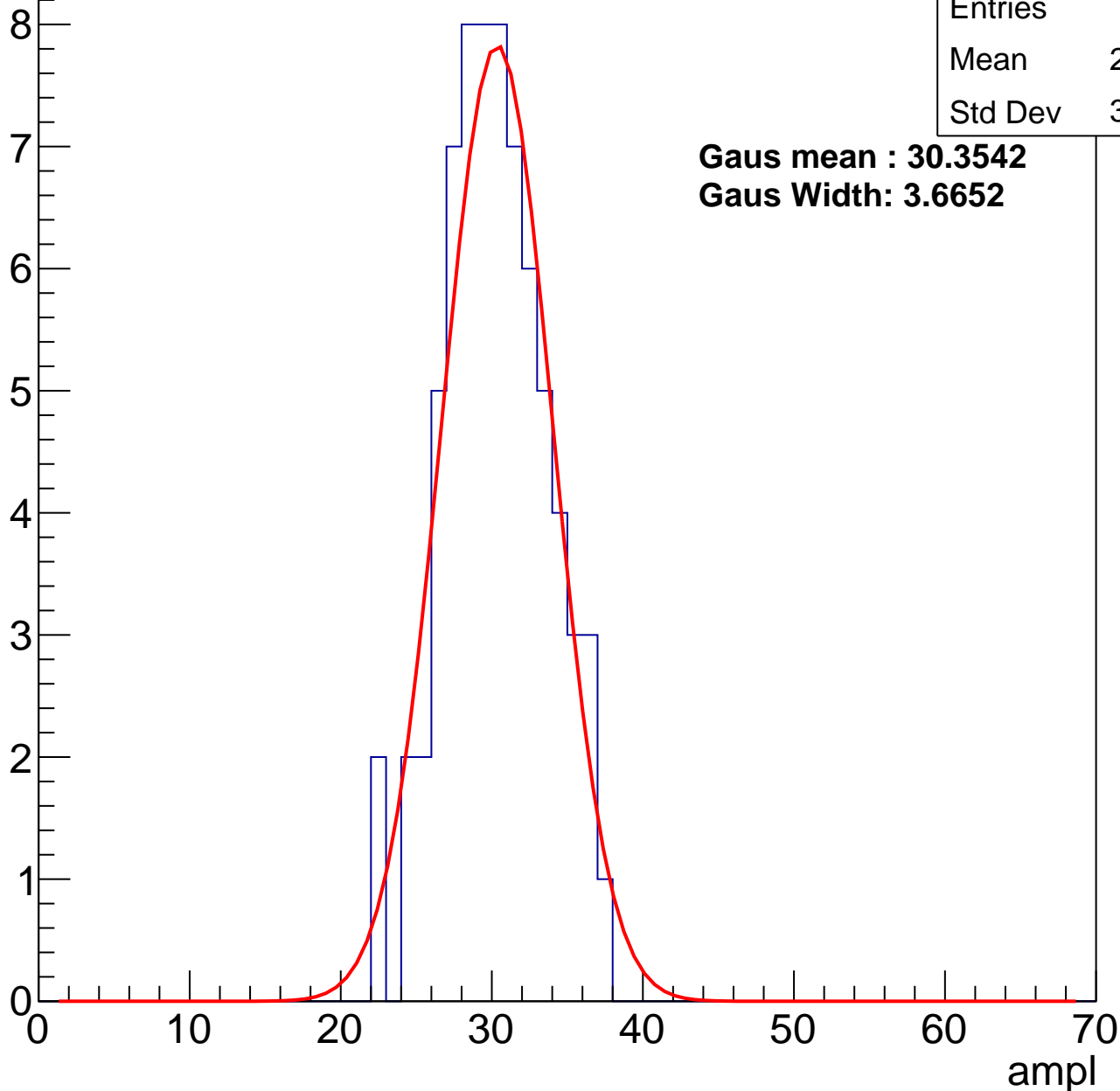
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	29.82
Std Dev	3.362

**Gaus mean : 30.3542**

**Gaus Width: 3.6652**



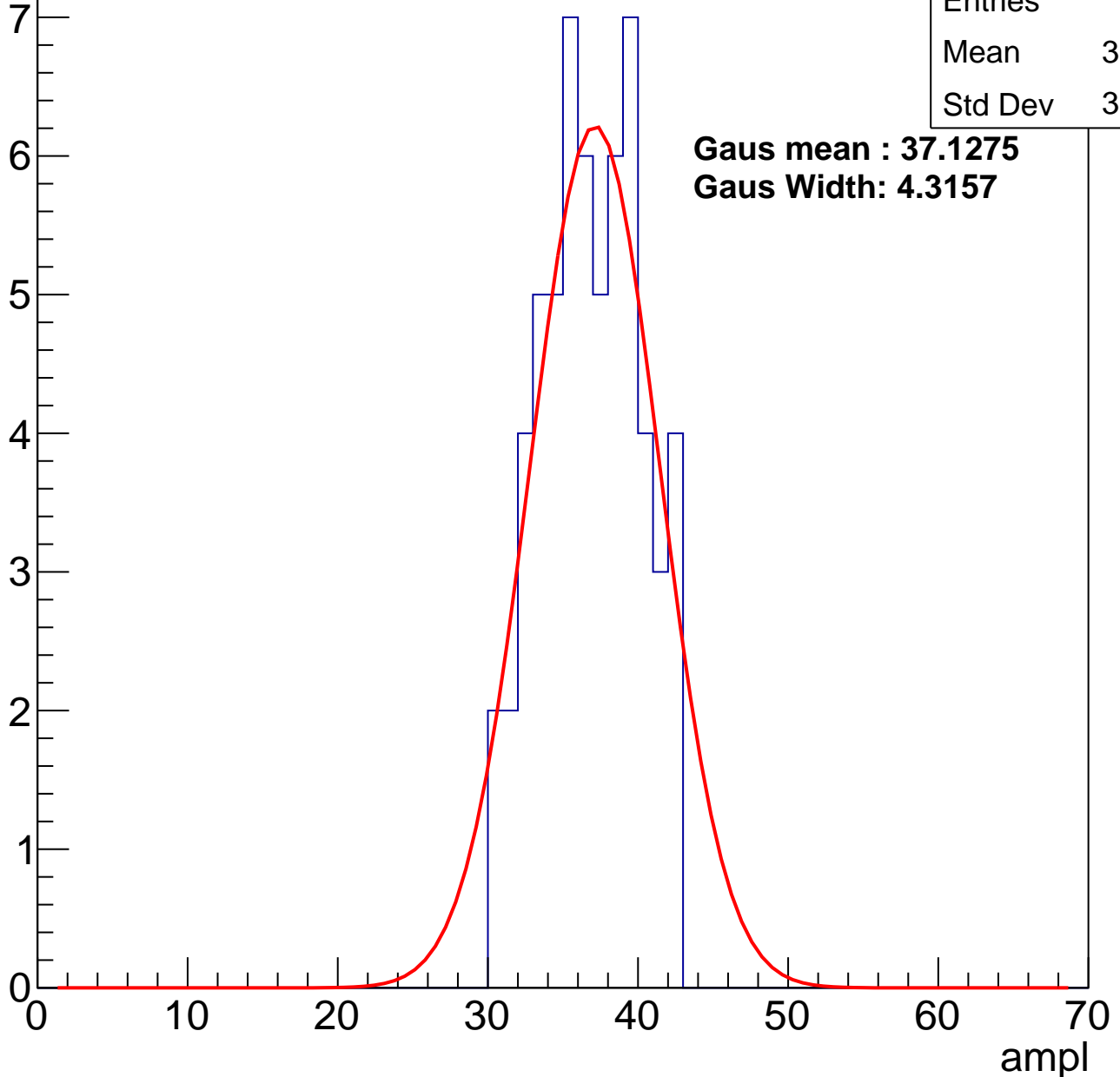
# B0L102S, U6-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	36.38
Std Dev	3.225

**Gaus mean : 37.1275**  
**Gaus Width: 4.3157**



# B0L102S, U6-ch2, adc2

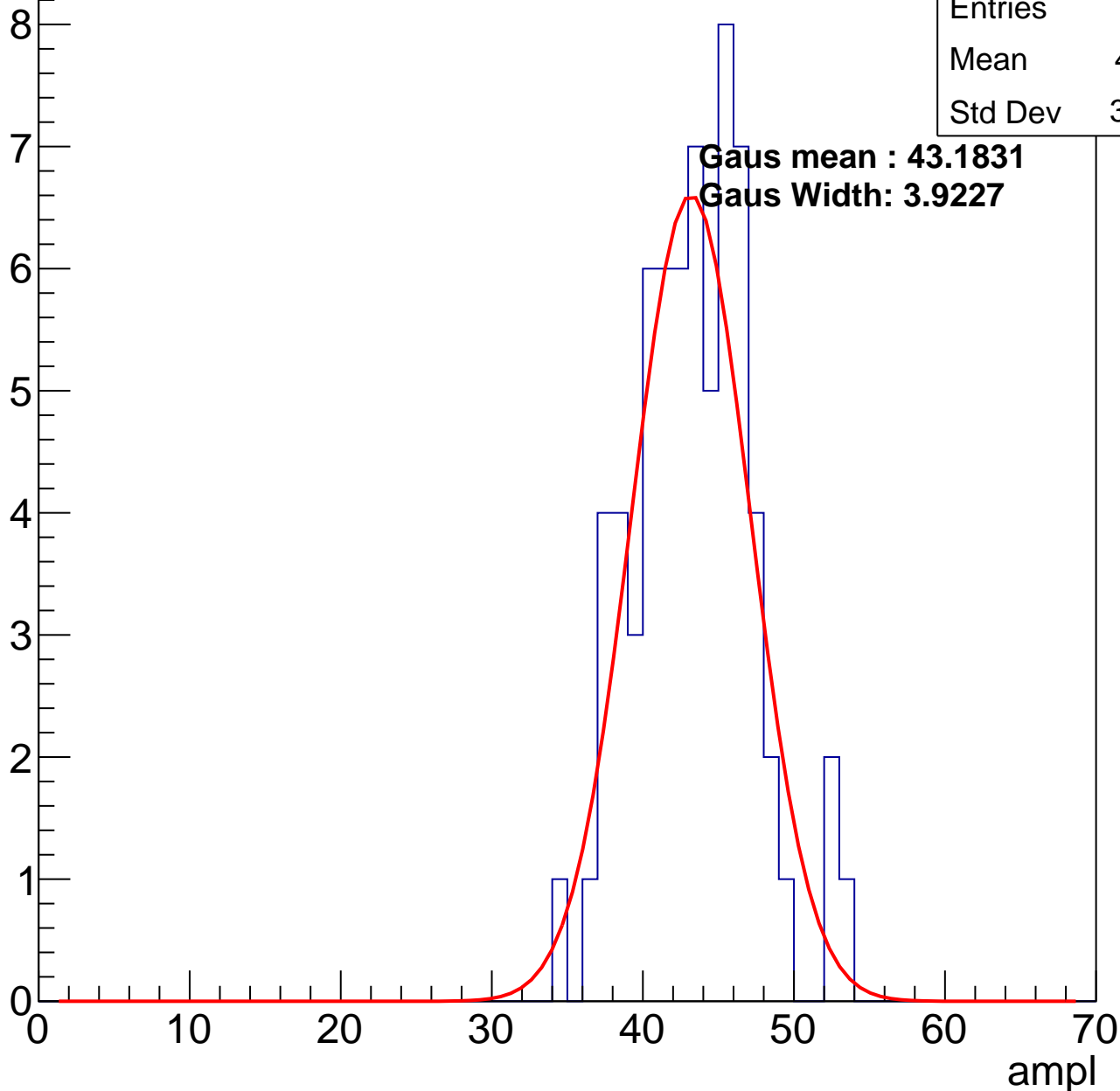
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	42.91
Std Dev	3.868

**Gaus mean : 43.1831**

**Gaus Width: 3.9227**

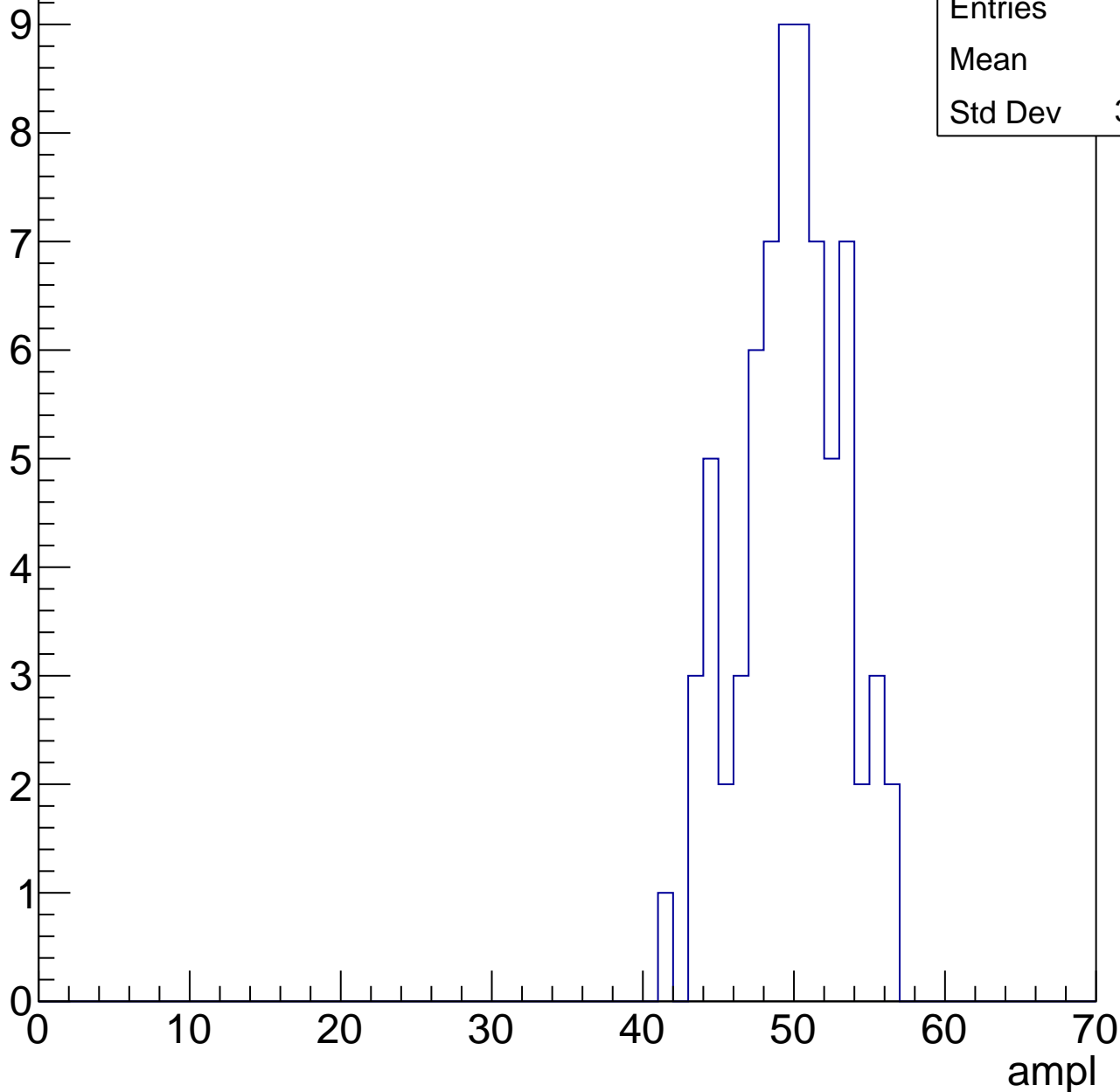


# B0L102S, U6-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

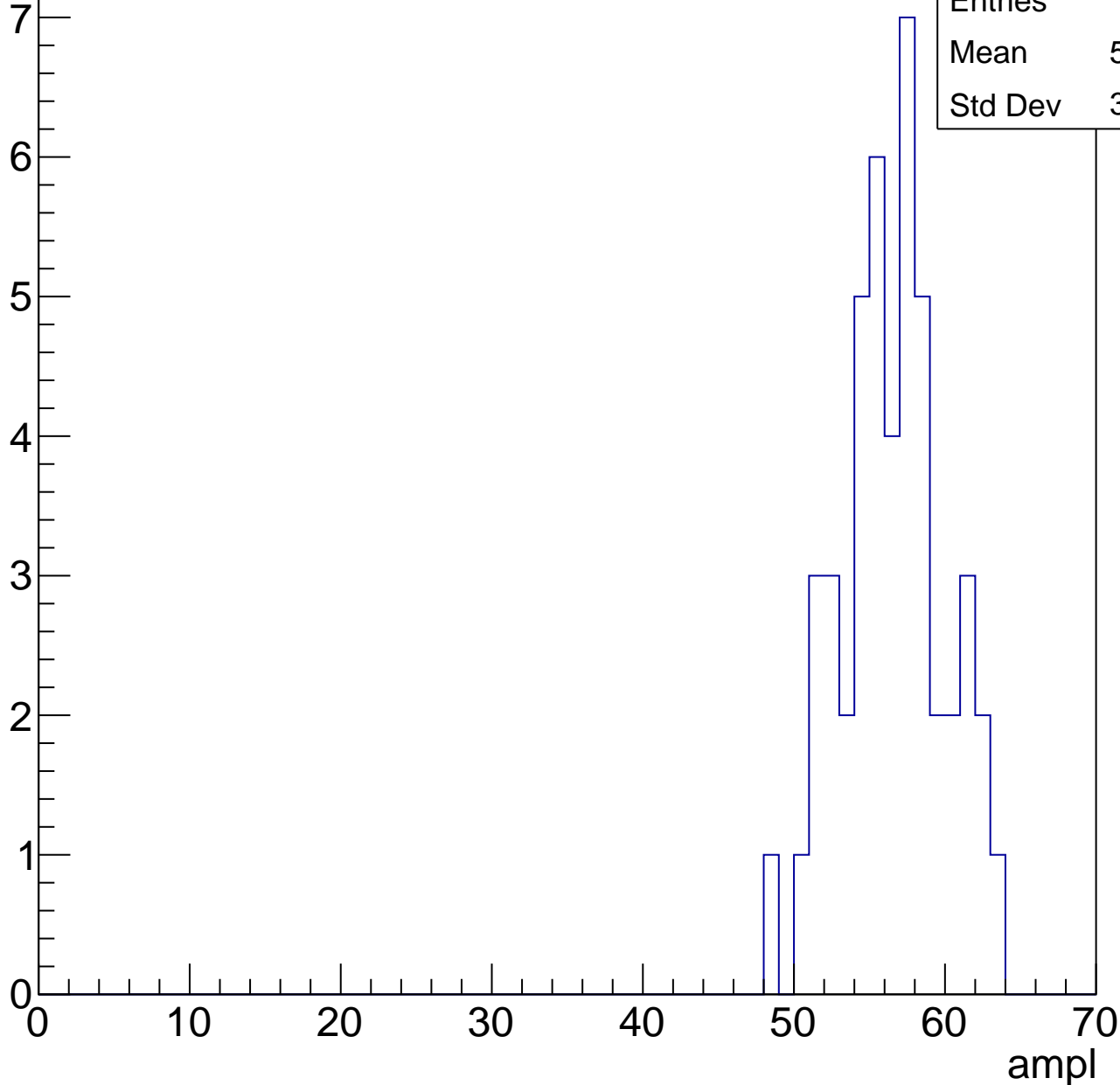
Entries	71
Mean	49.3
Std Dev	3.421



# B0L102S, U6-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	47
Mean	56.04
Std Dev	3.389

# B0L102S, U6-ch2, adc5

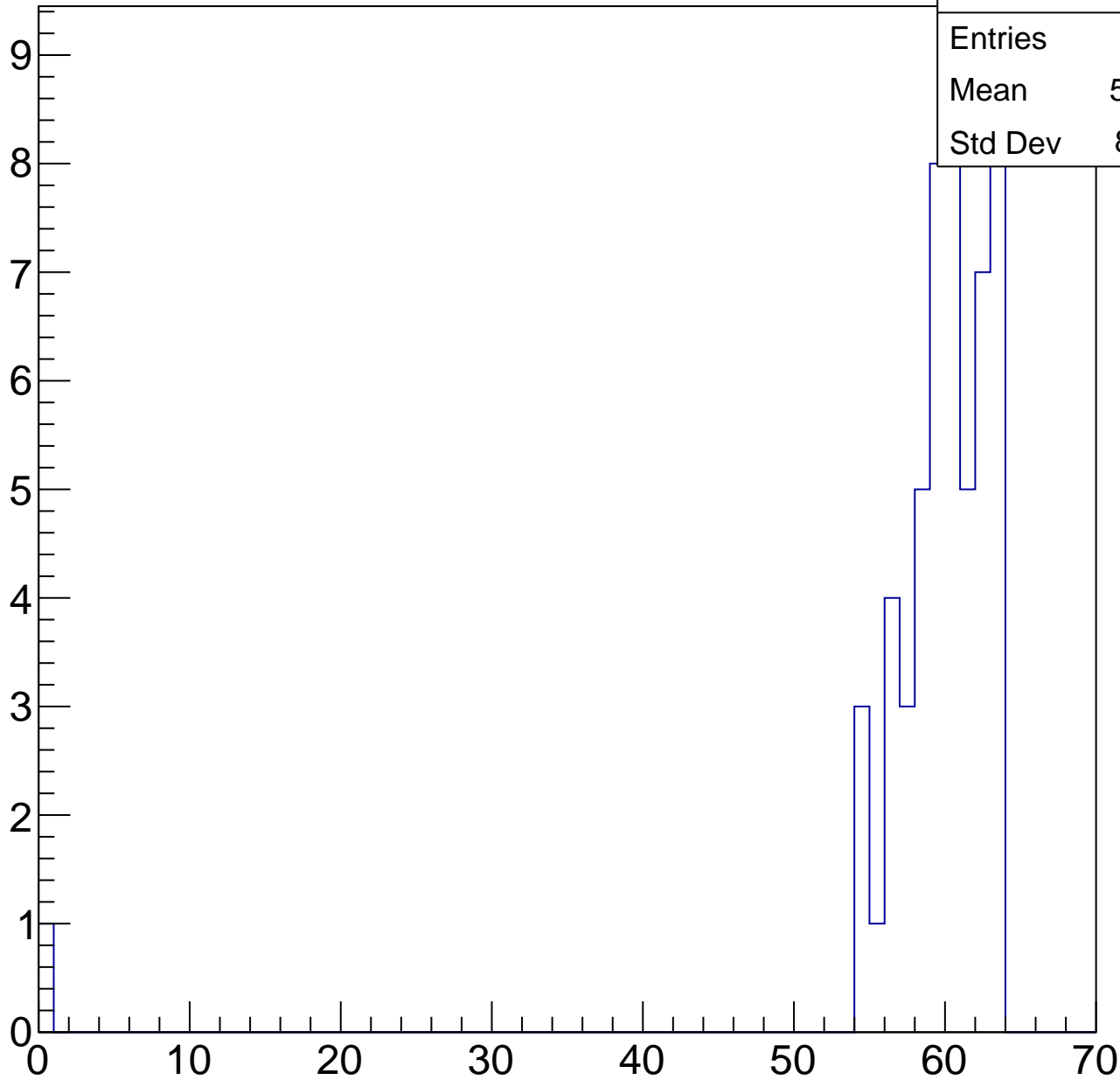
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.52
Std Dev	8.441

ampl



# B0L102S, U6-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch3, adc0

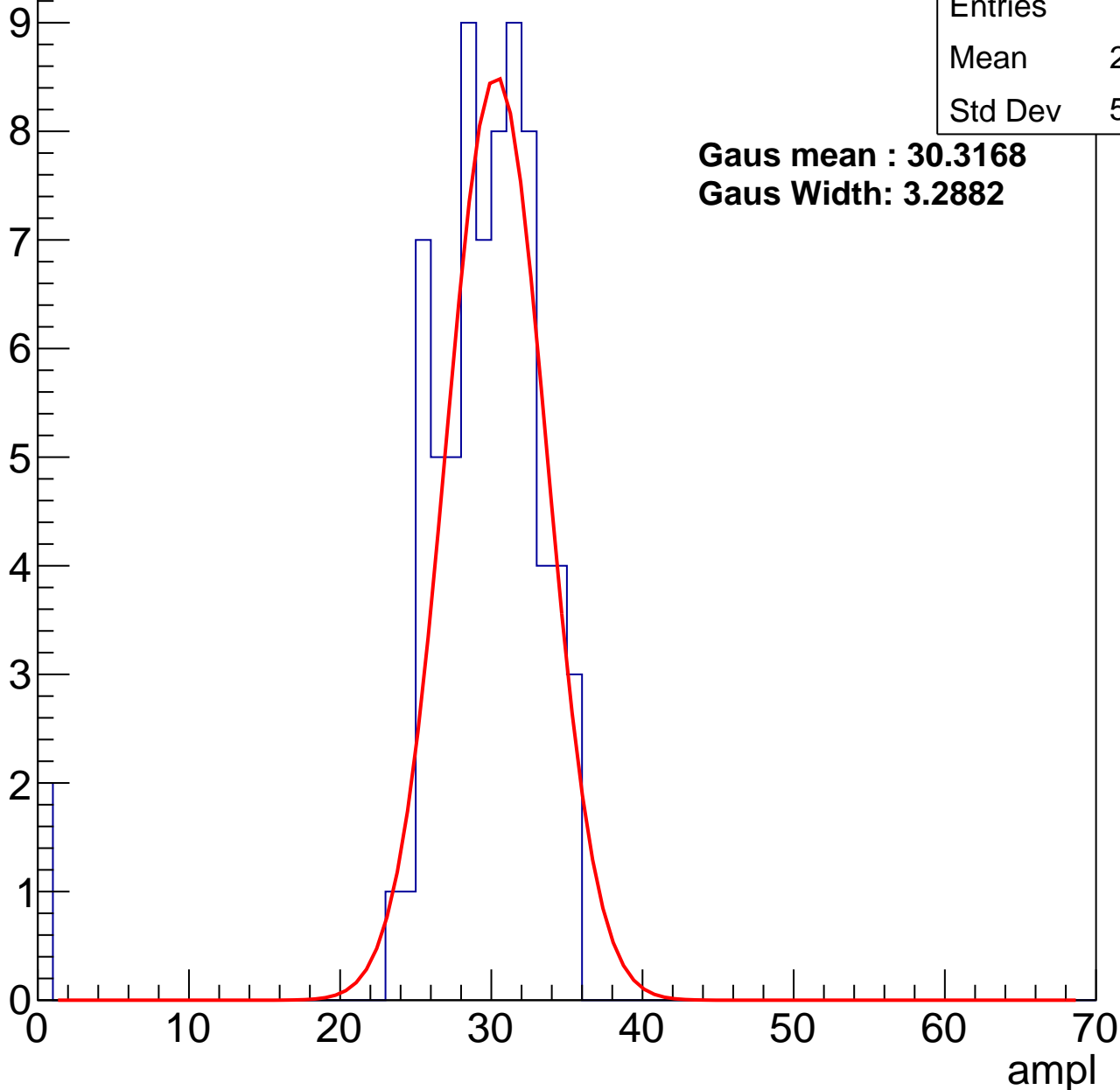
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	28.63
Std Dev	5.622

**Gaus mean : 30.3168**

**Gaus Width: 3.2882**



# B0L102S, U6-ch3, adc1

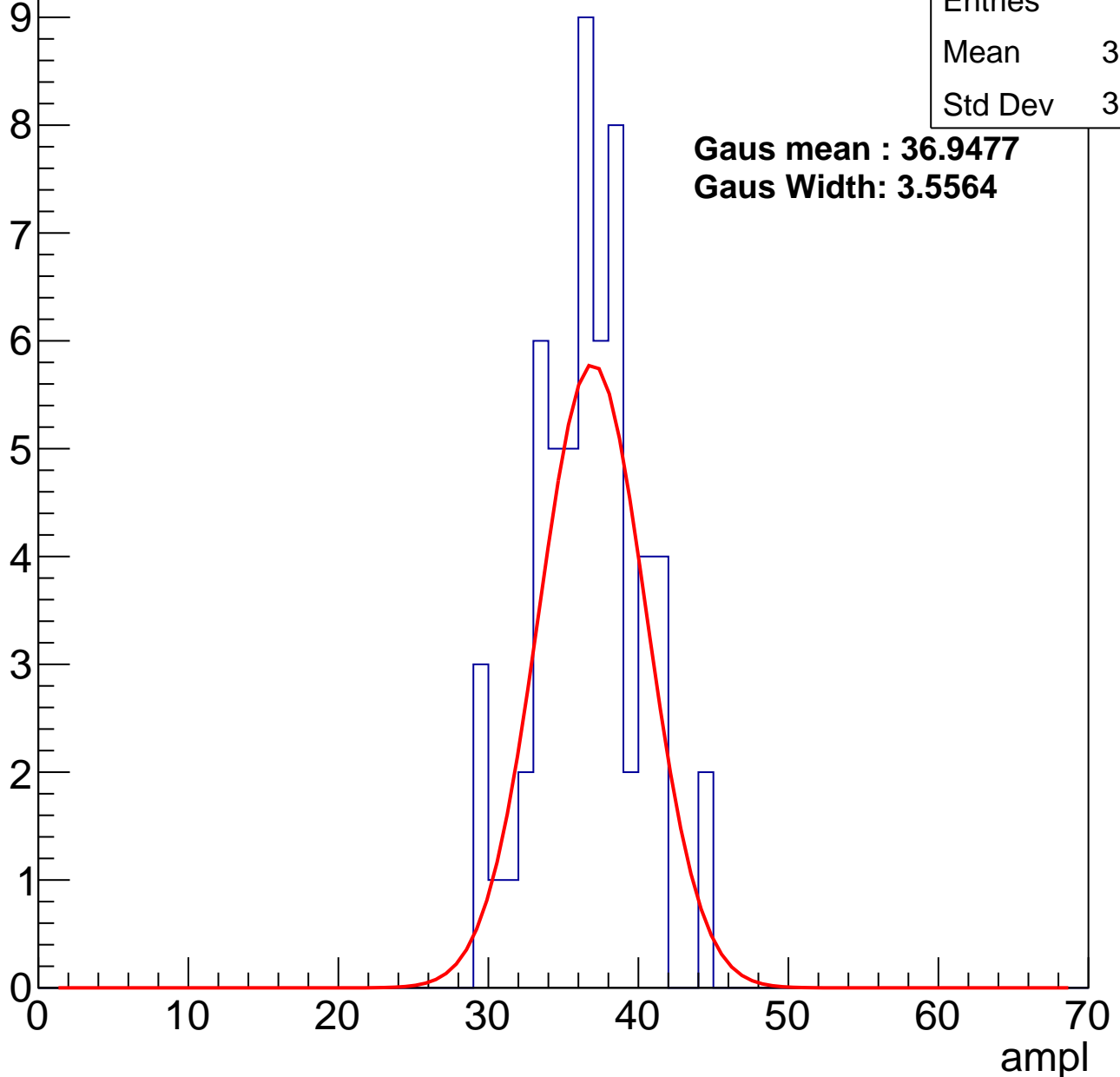
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	36.12
Std Dev	3.389

**Gaus mean : 36.9477**

**Gaus Width: 3.5564**



# B0L102S, U6-ch3, adc2

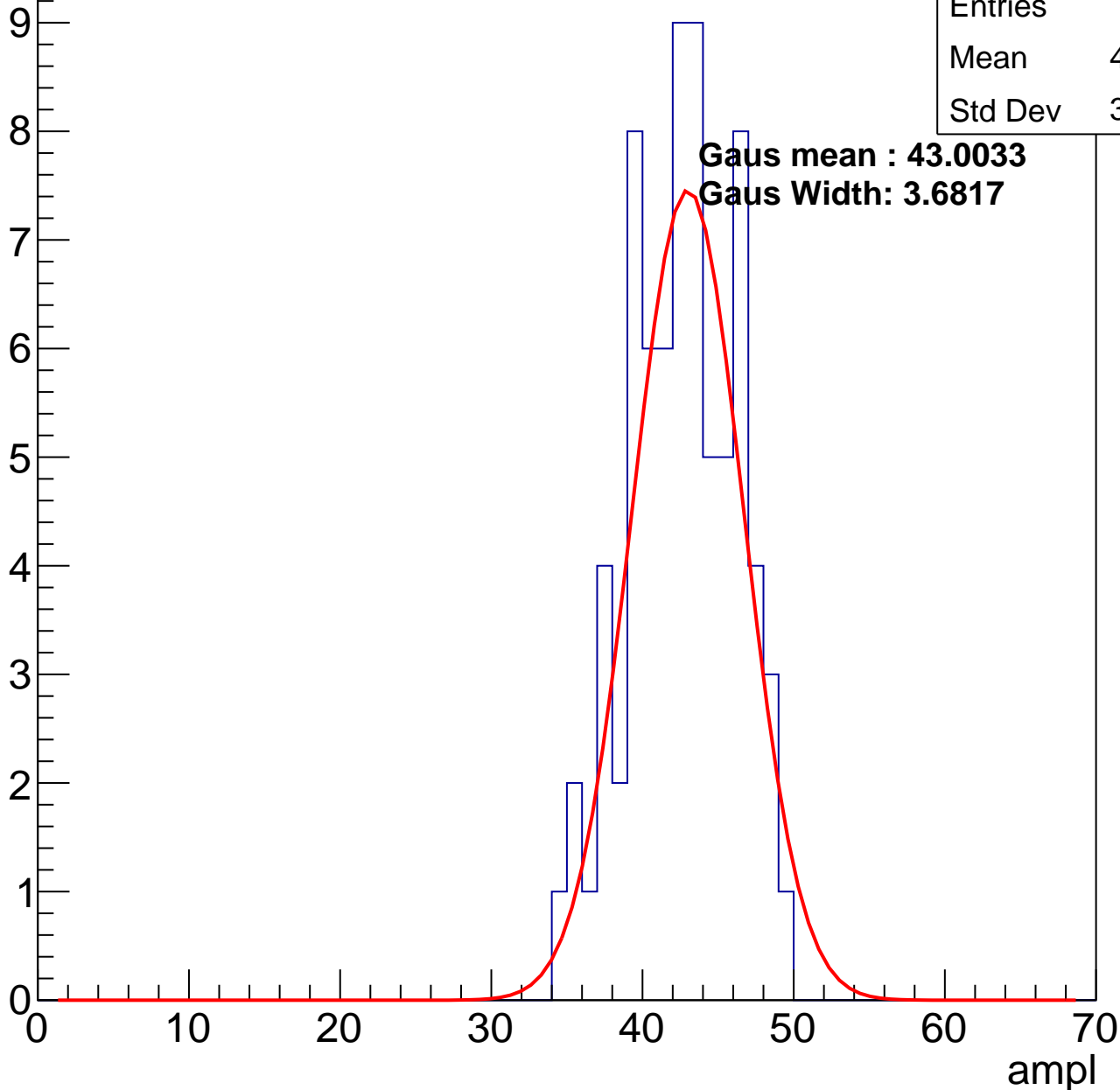
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	42.18
Std Dev	3.465

**Gaus mean : 43.0033**

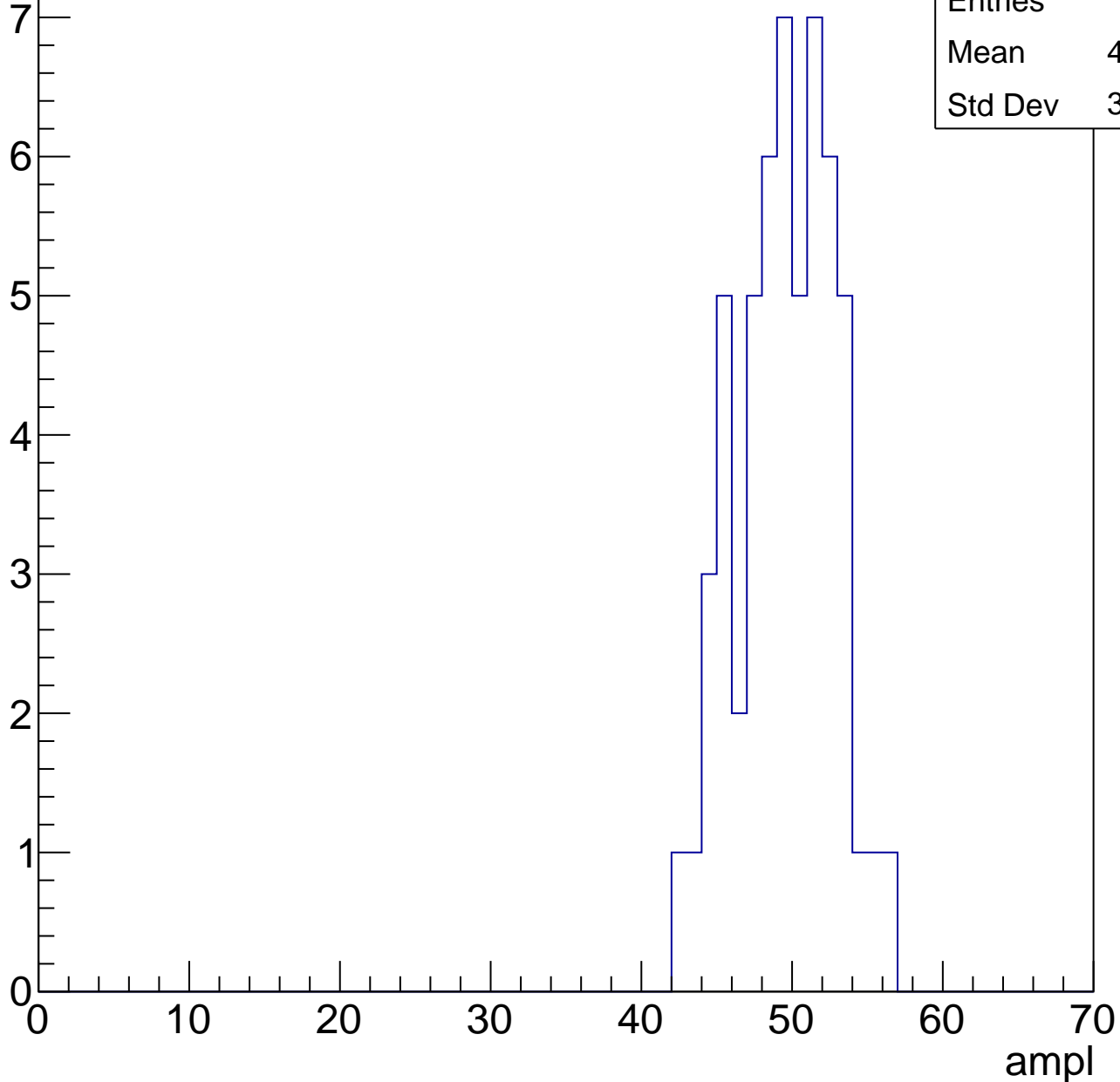
**Gaus Width: 3.6817**



# B0L102S, U6-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

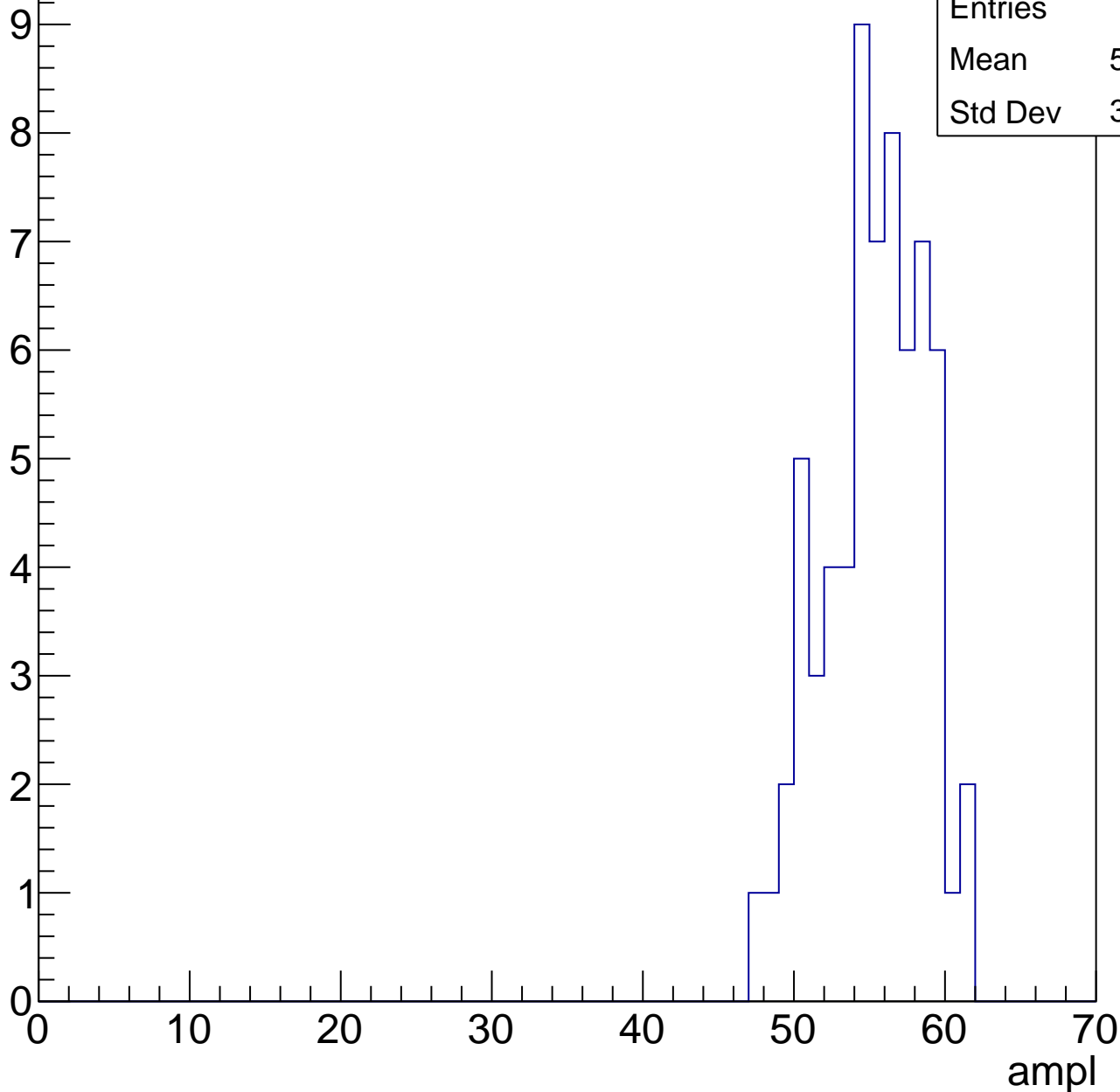


Entries	56
Mean	49.09
Std Dev	3.164

# B0L102S, U6-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch3, adc5

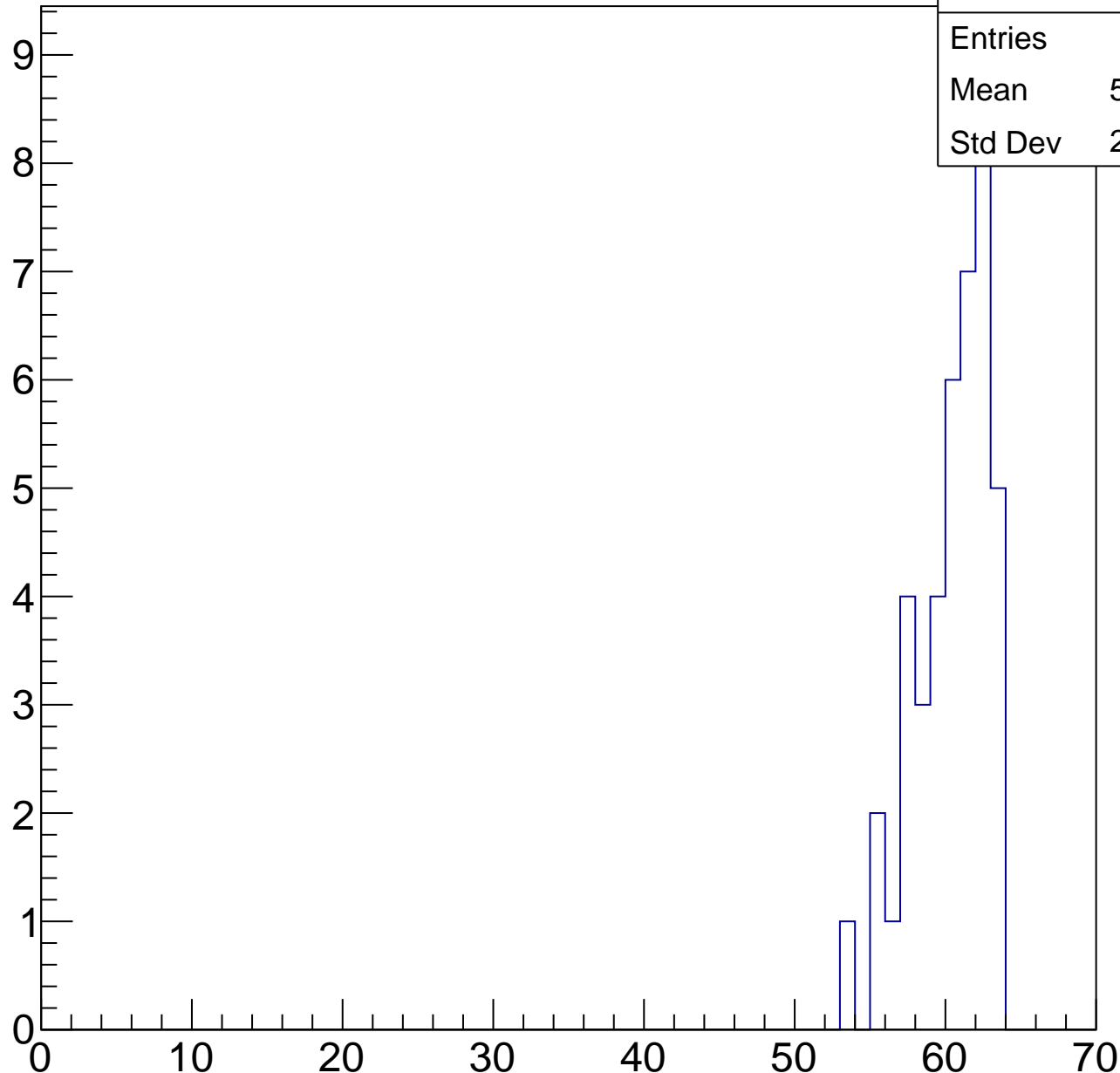
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	42
Mean	59.93
Std Dev	2.463

ampl

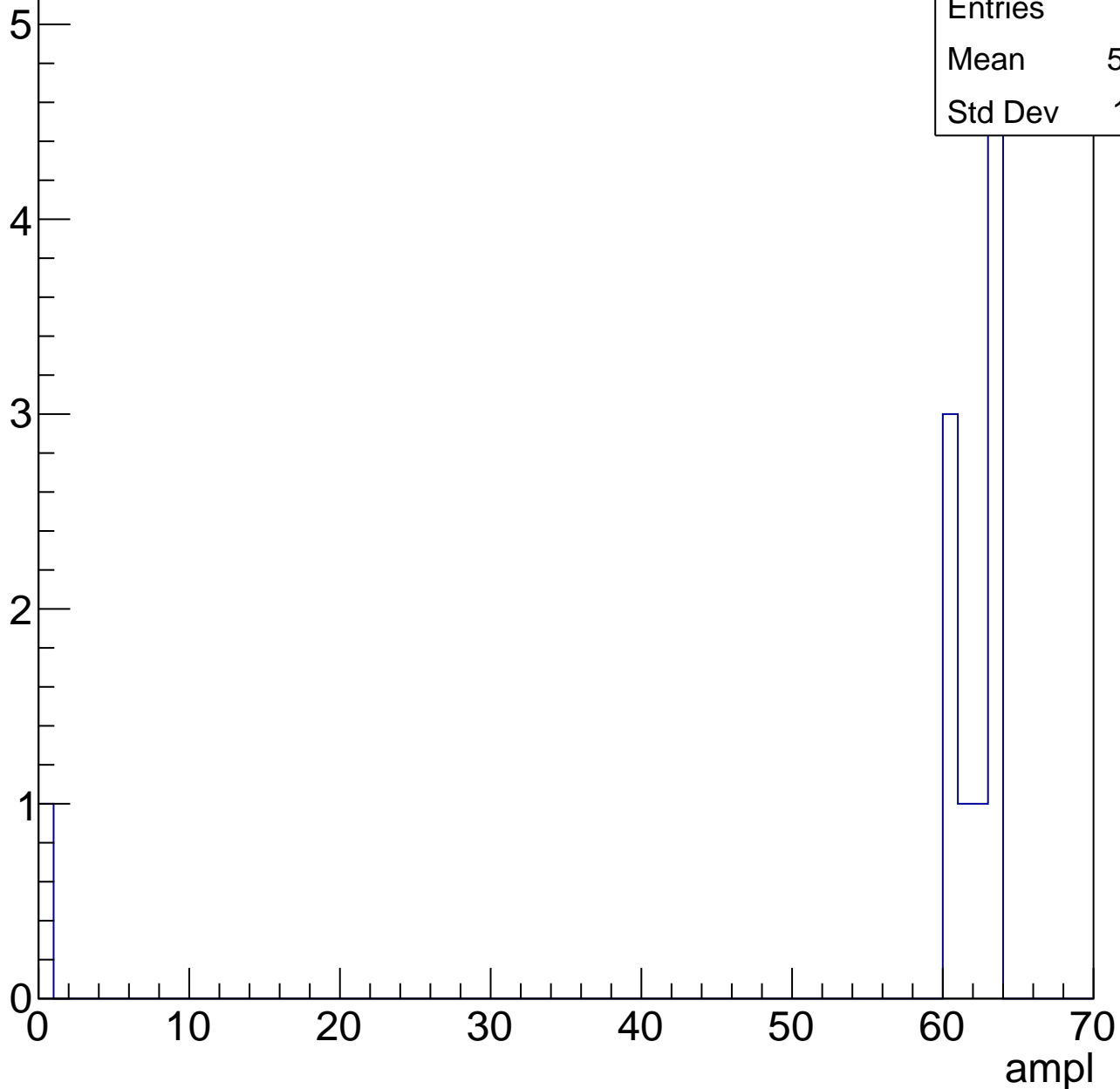


# B0L102S, U6-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	11
Mean	56.18
Std Dev	17.81





# B0L102S, U6-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch4, adc0

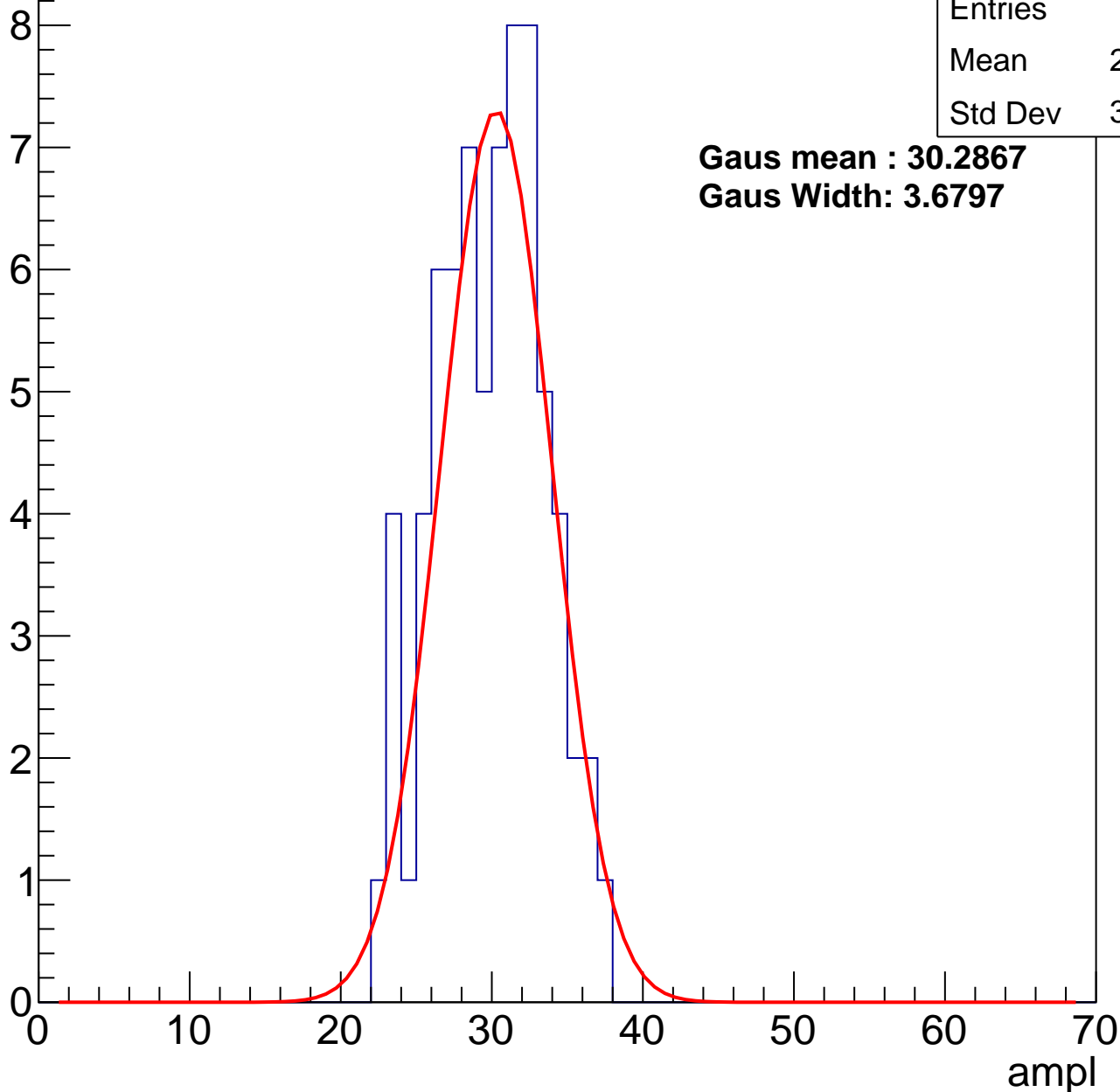
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	29.45
Std Dev	3.516

**Gaus mean : 30.2867**

**Gaus Width: 3.6797**



# B0L102S, U6-ch4, adc1

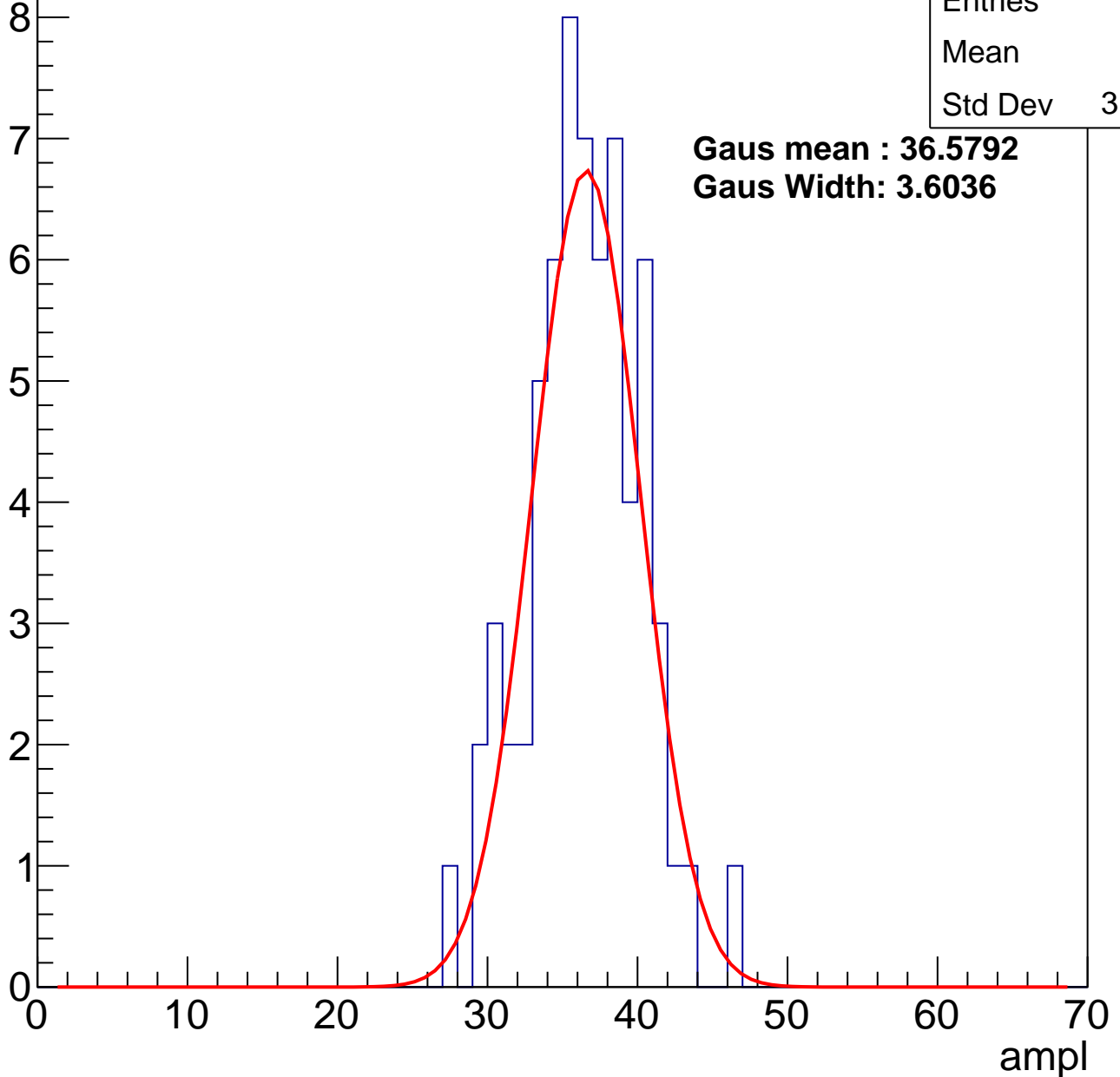
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36
Std Dev	3.663

**Gaus mean : 36.5792**

**Gaus Width: 3.6036**



# B0L102S, U6-ch4, adc2

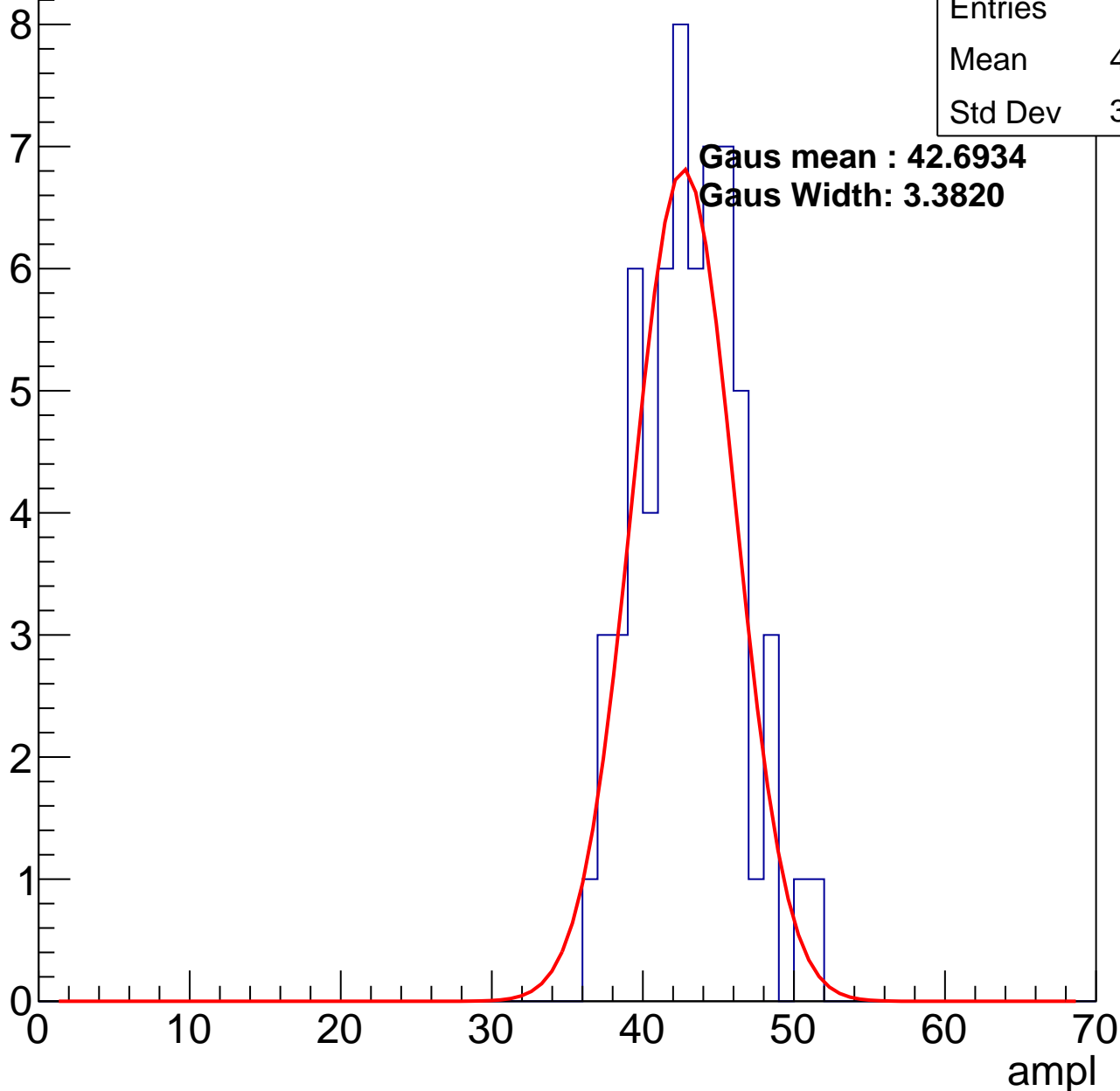
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.58
Std Dev	3.285

**Gaus mean : 42.6934**

**Gaus Width: 3.3820**

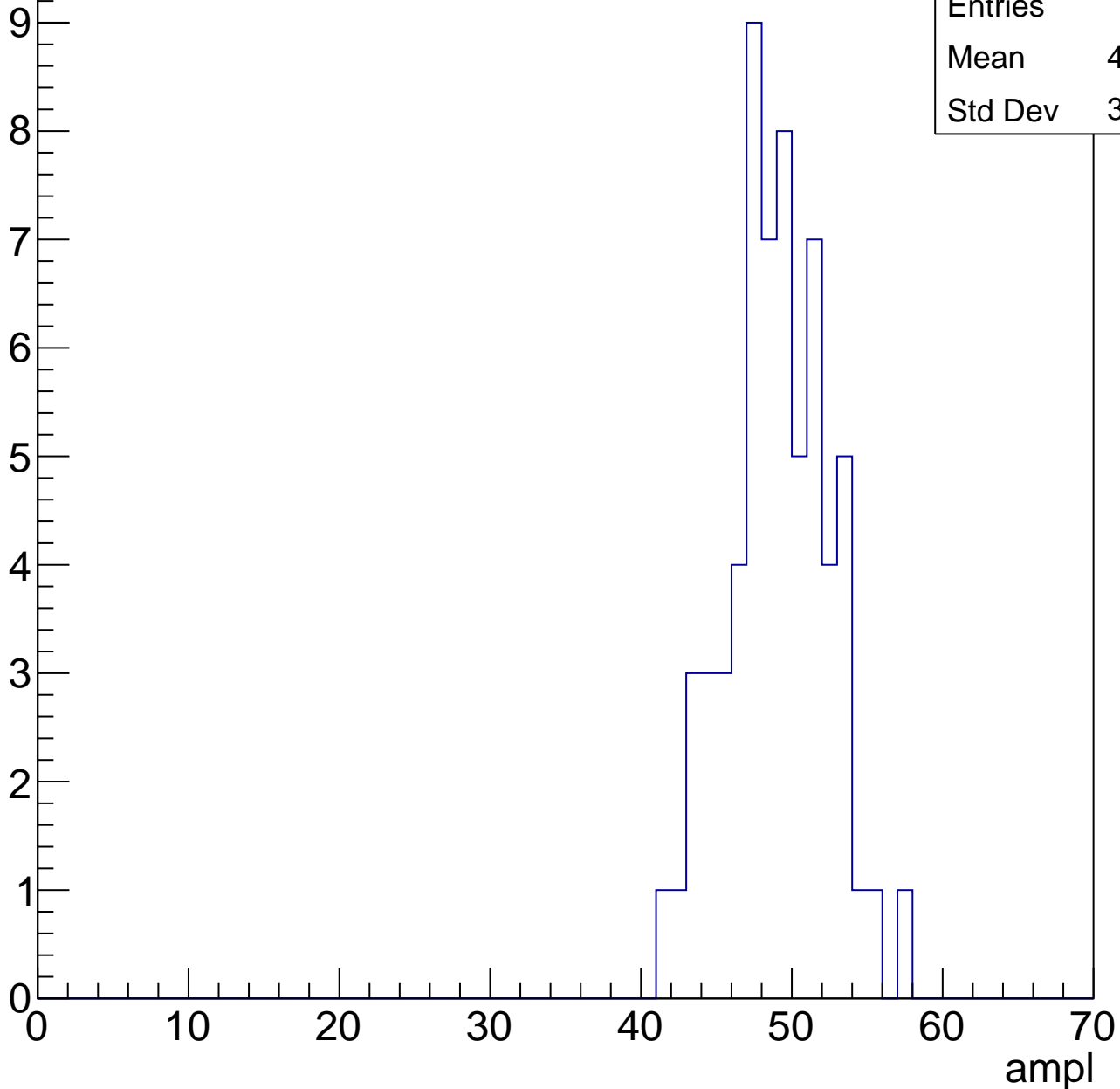


# B0L102S, U6-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	48.57
Std Dev	3.298

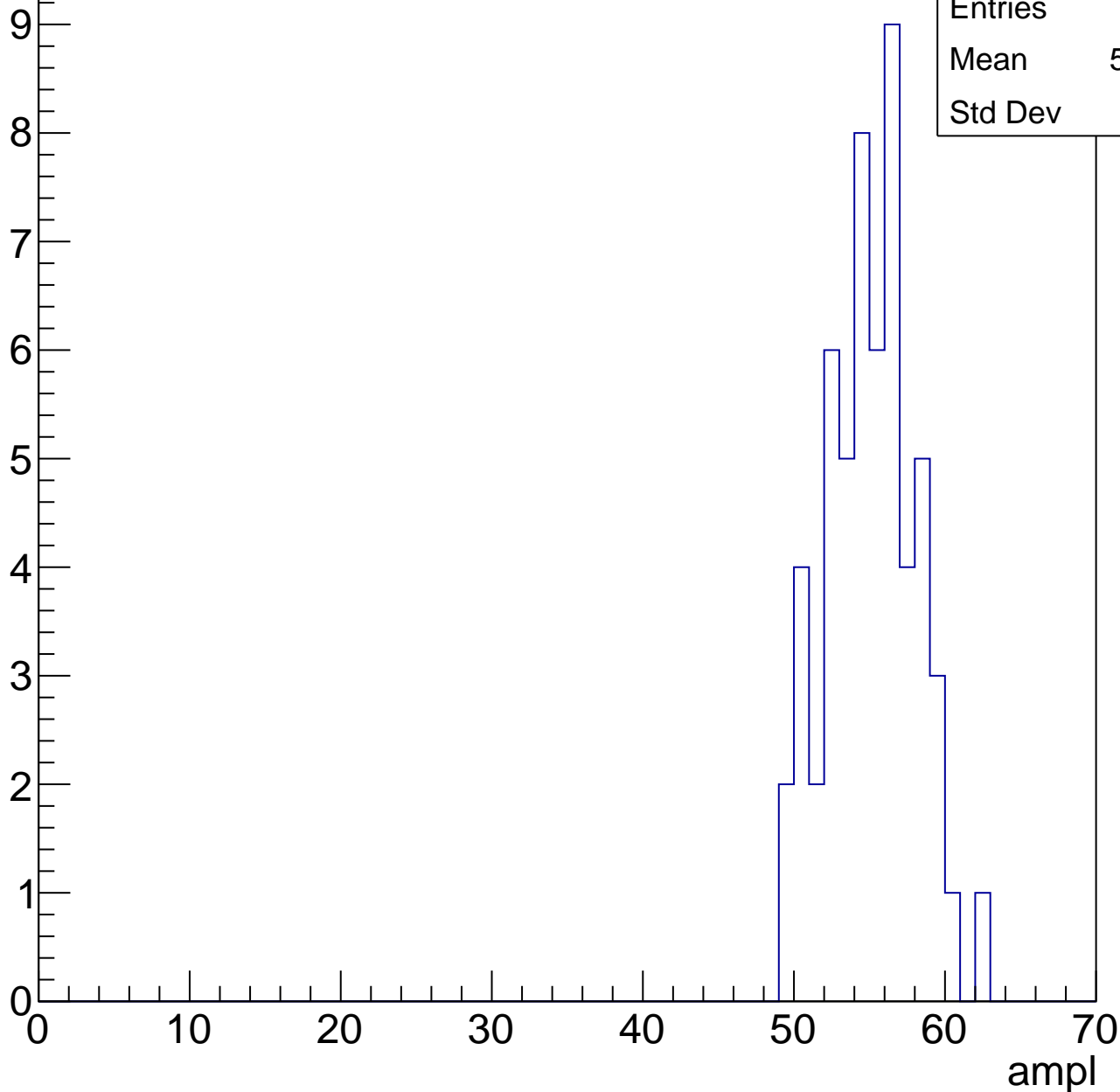


# B0L102S, U6-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	54.64
Std Dev	2.9



# B0L102S, U6-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	50
Mean	58.76
Std Dev	8.701

Entry

10

8

6

4

2

0

0

10

20

30

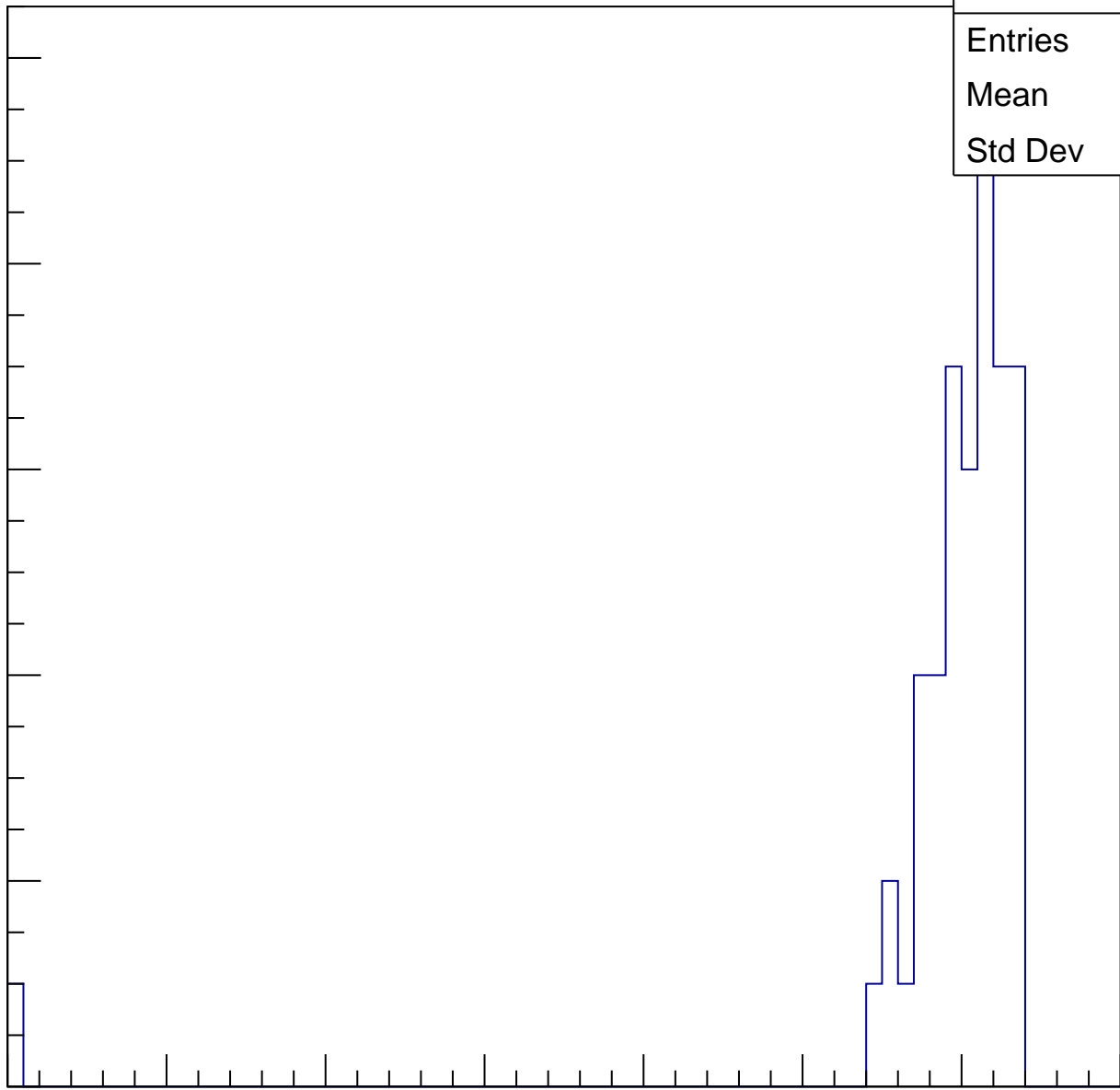
40

50

60

70

ampl



# B0L102S, U6-ch4, adc6

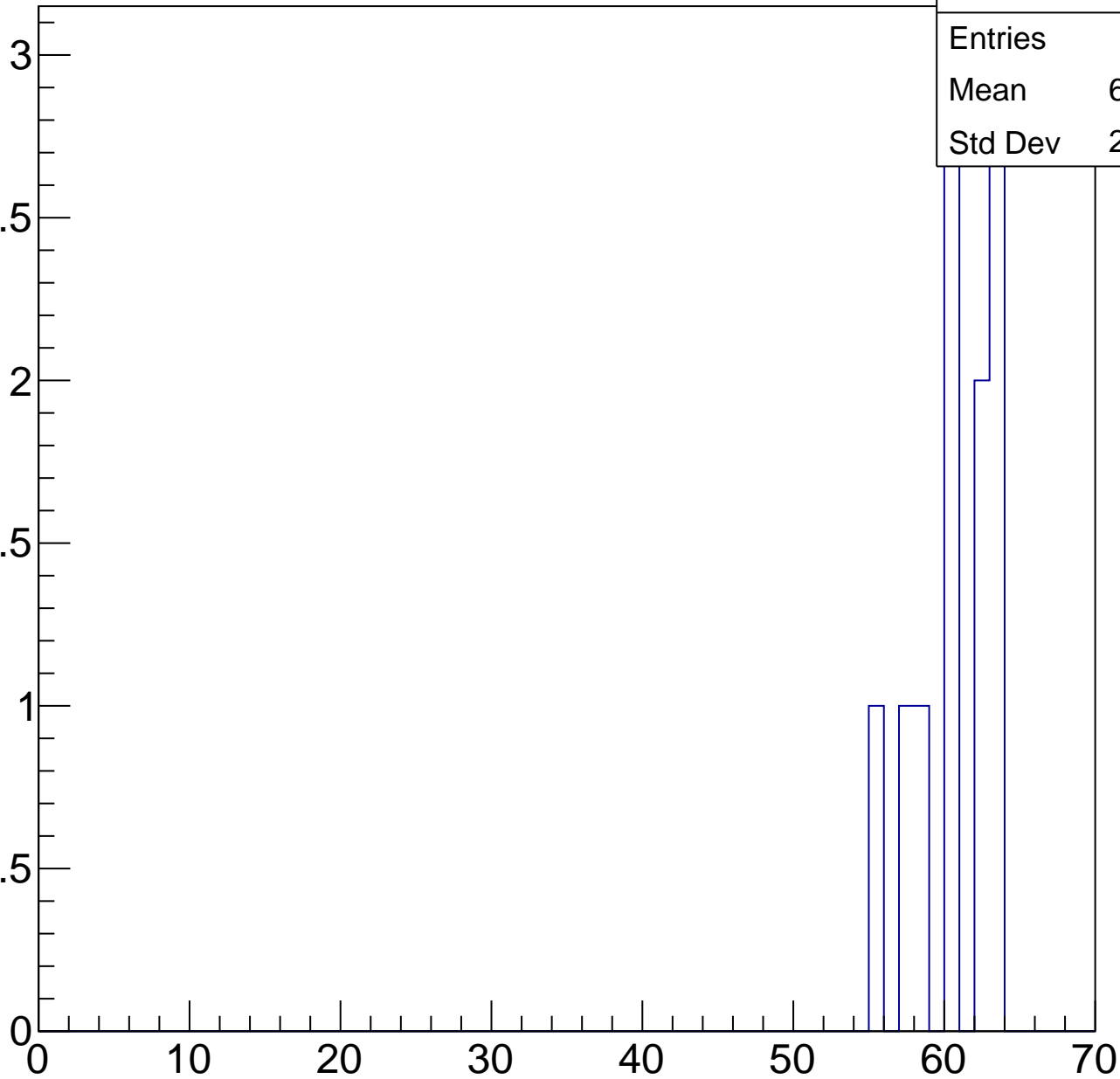
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	11
Mean	60.27
Std Dev	2.562

ampl





# B0L102S, U6-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L102S, U6-ch5, adc0

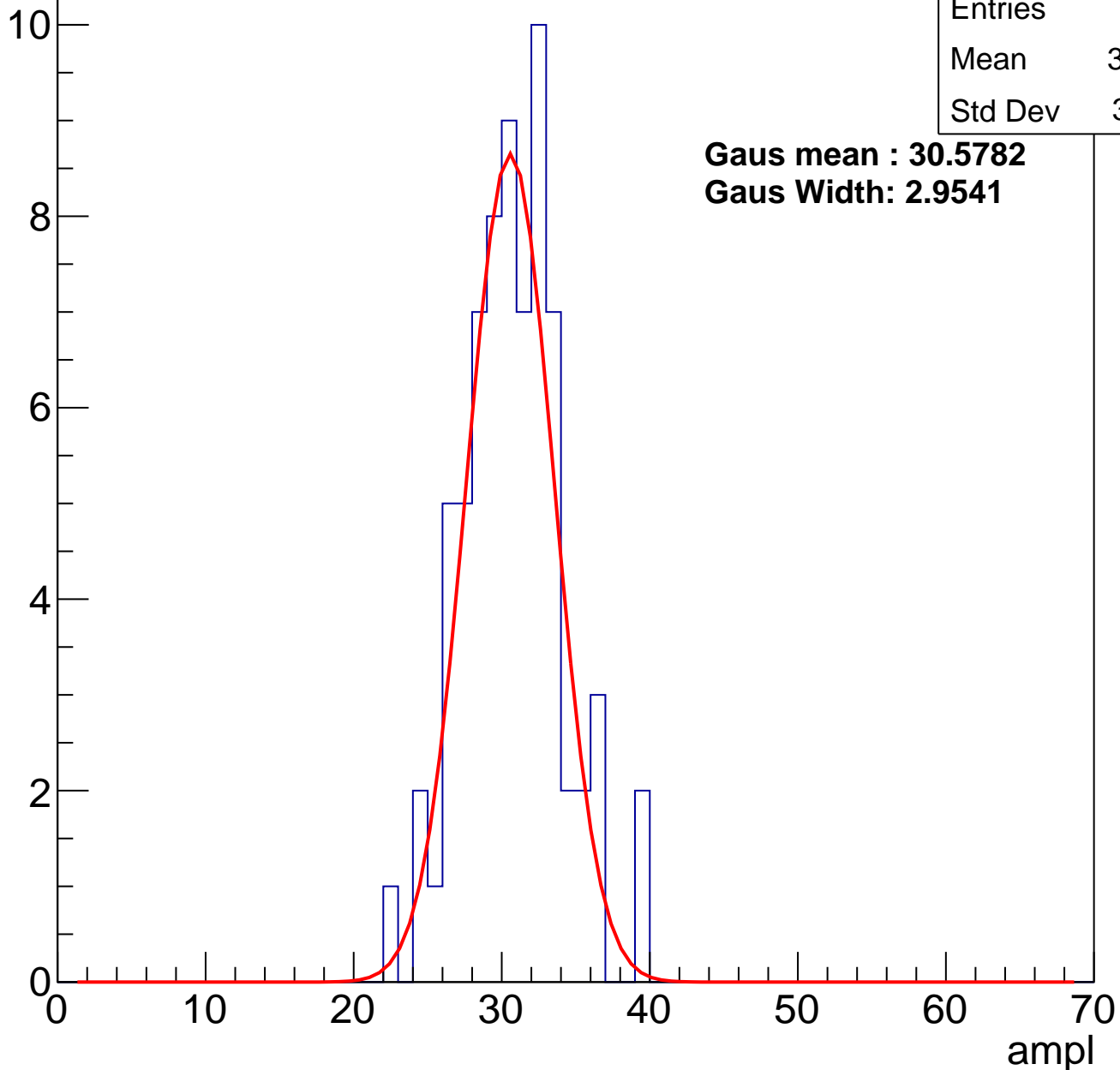
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	71
Mean	30.28
Std Dev	3.311

**Gaus mean : 30.5782**

**Gaus Width: 2.9541**

Entry



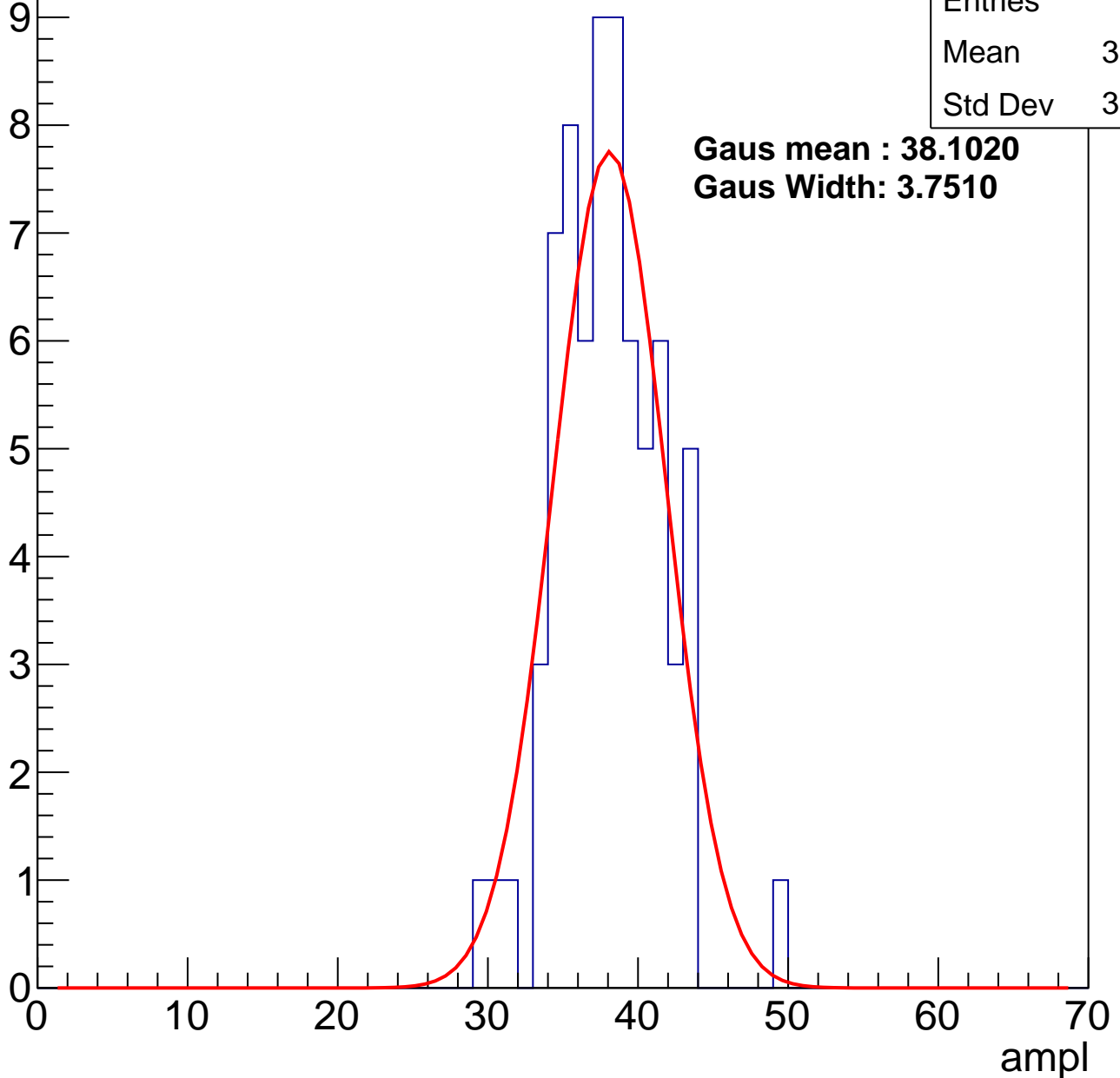
# B0L102S, U6-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	37.58
Std Dev	3.455

**Gaus mean : 38.1020**  
**Gaus Width: 3.7510**



# B0L102S, U6-ch5, adc2

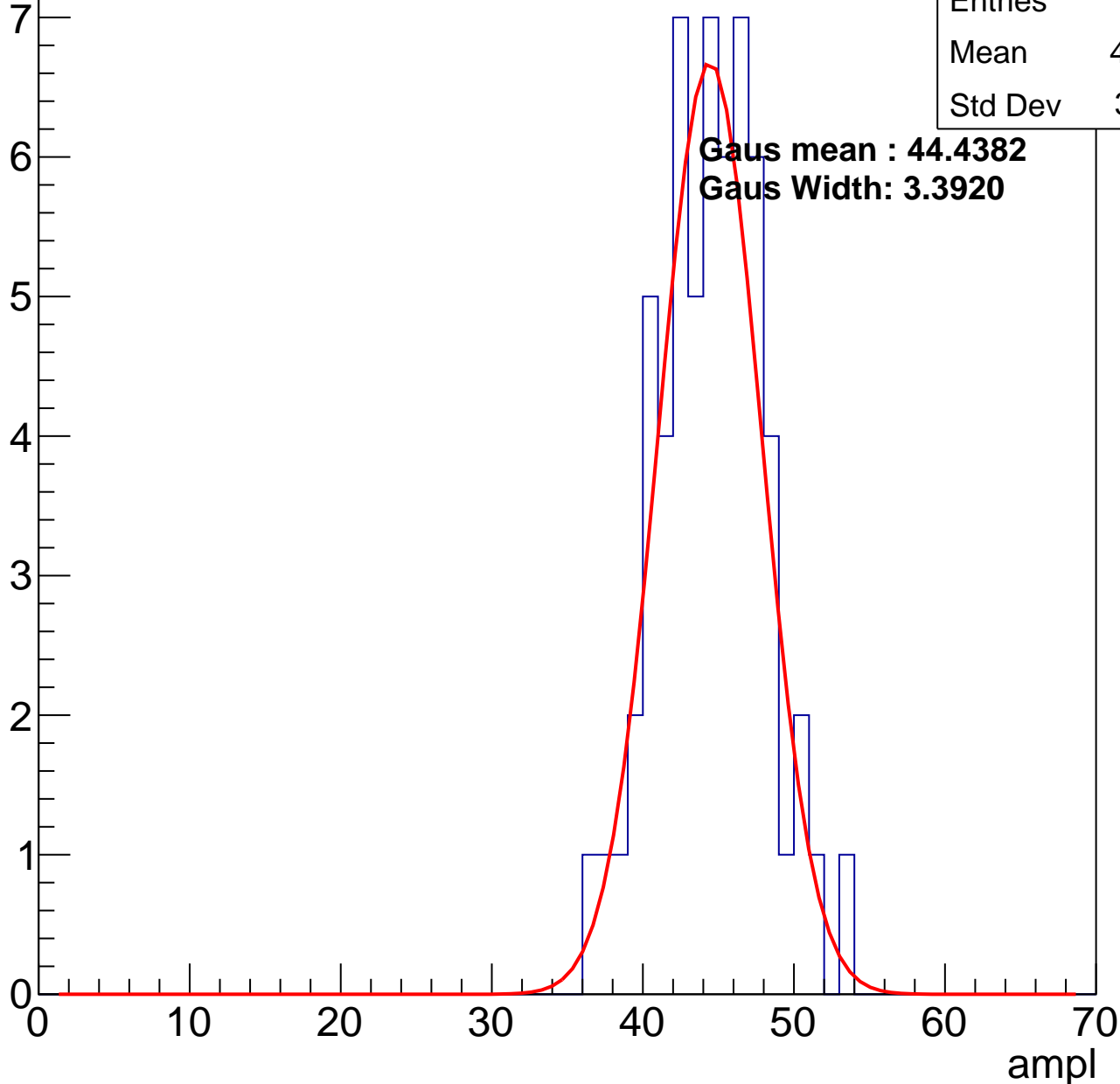
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	44.08
Std Dev	3.451

**Gaus mean : 44.4382**

**Gaus Width: 3.3920**

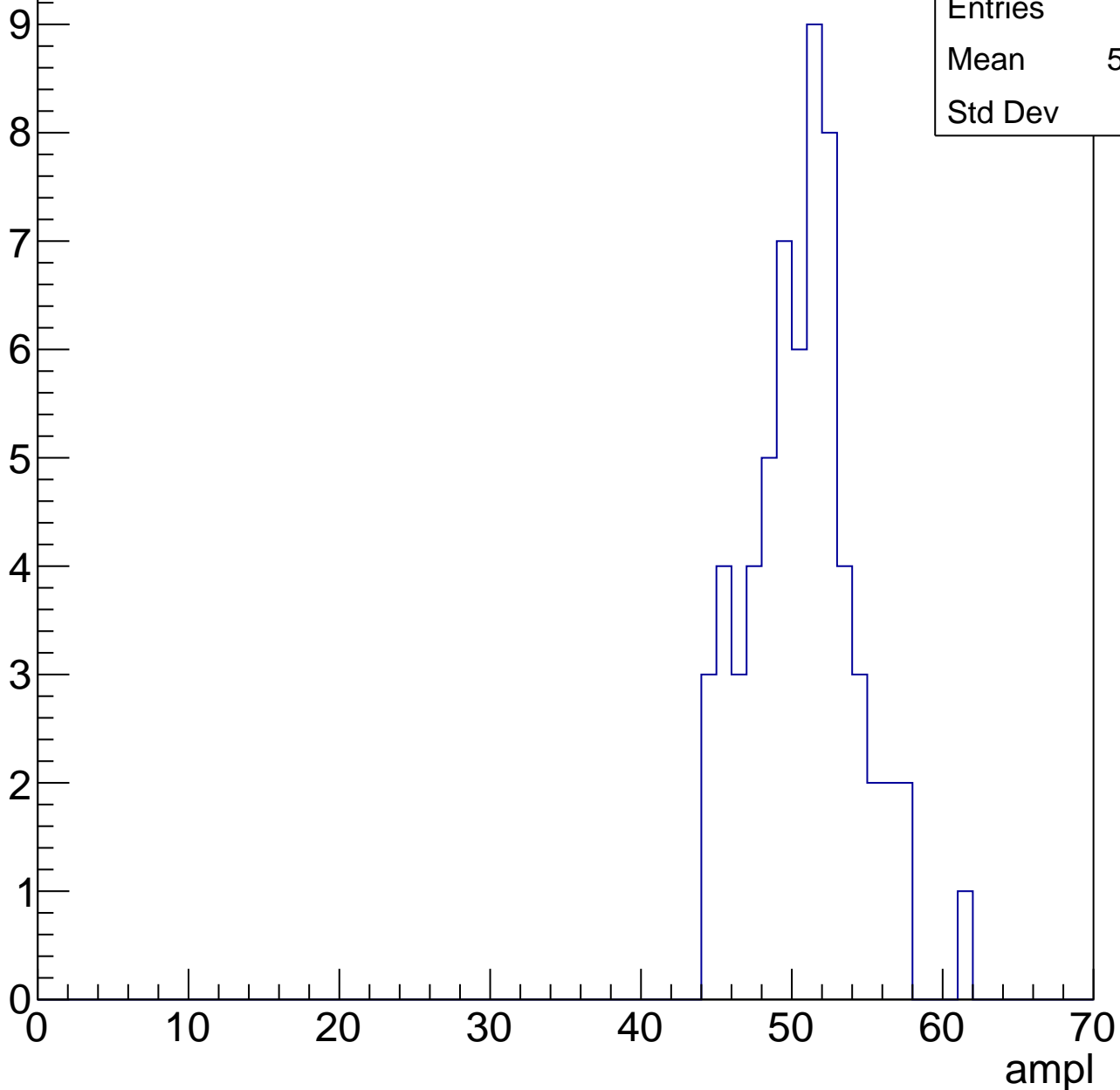


# B0L102S, U6-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	50.27
Std Dev	3.52



# B0L102S, U6-ch5, adc4

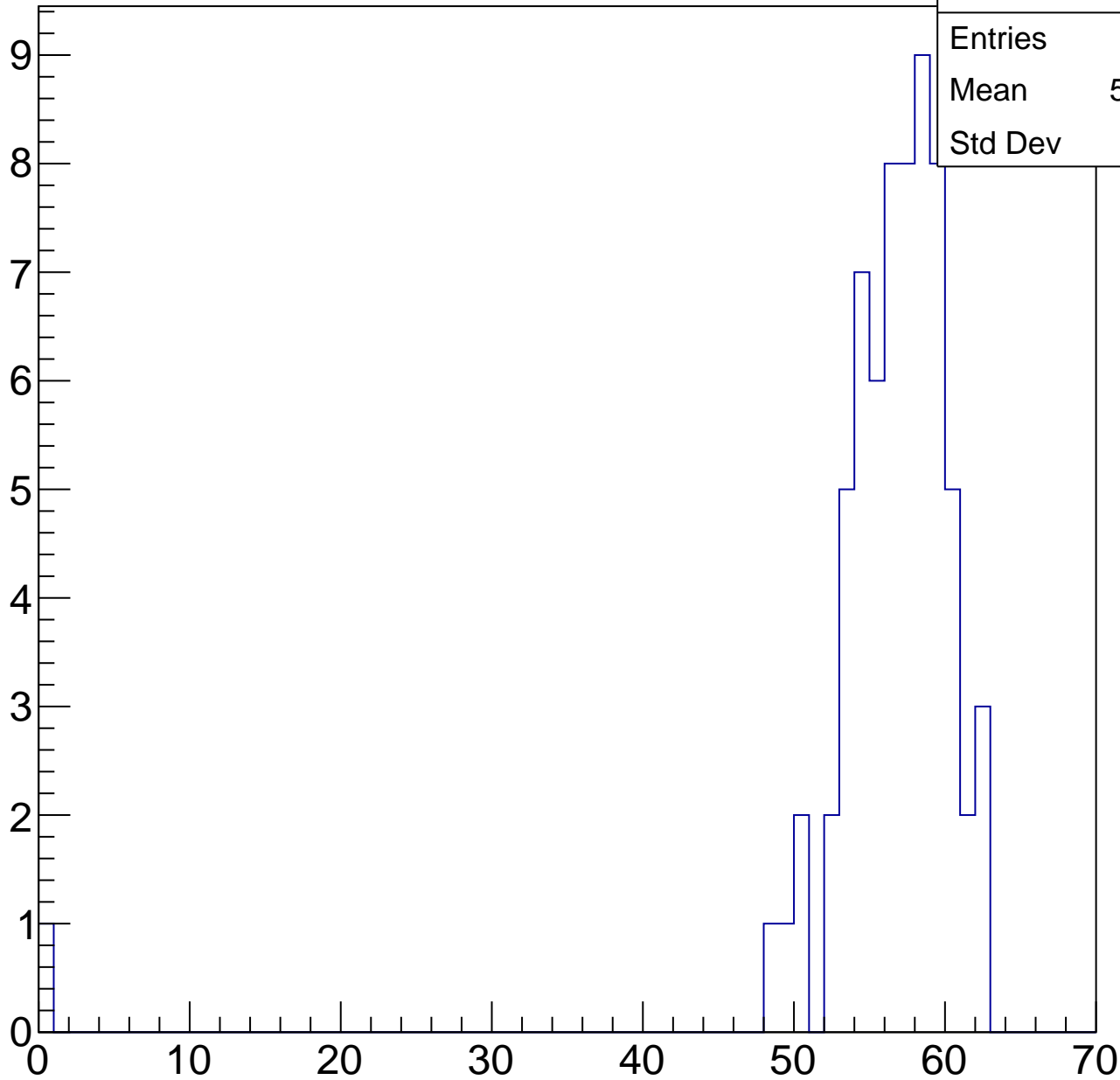
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	68
Mean	55.59
Std Dev	7.45

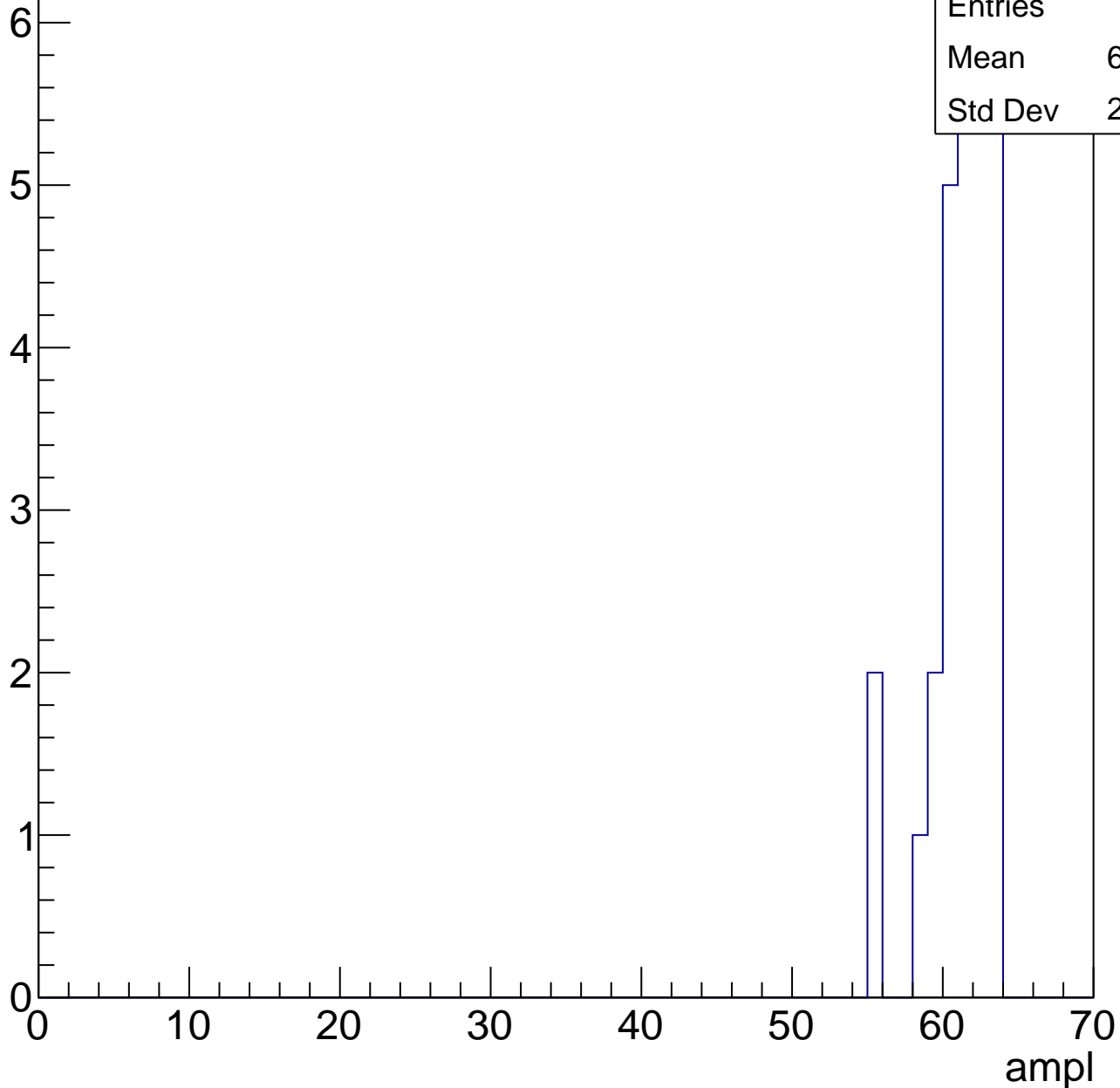
ampl



# B0L102S, U6-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch6, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	30.79
Std Dev	4.981

**Gaus mean : 31.9141**

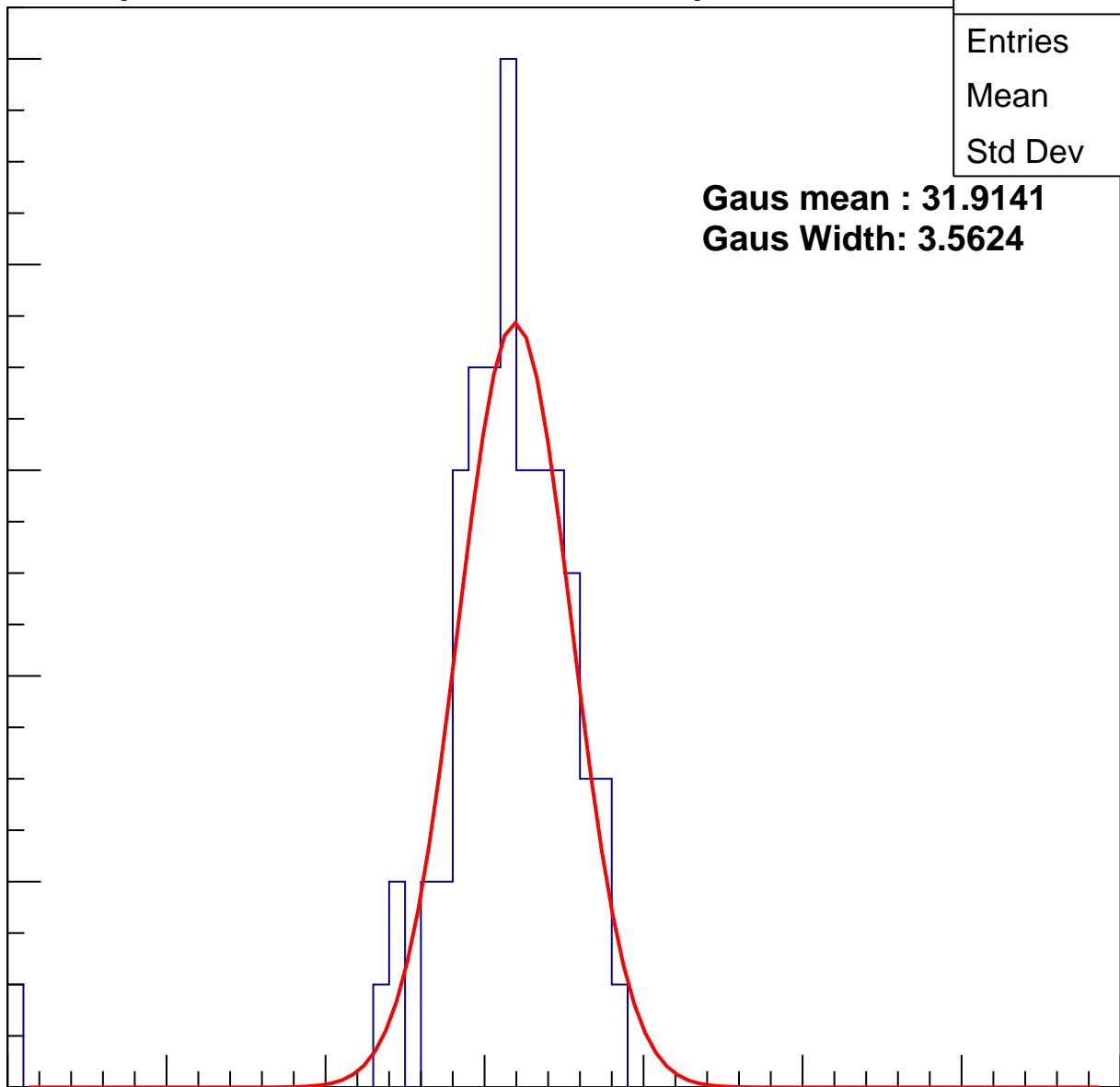
**Gaus Width: 3.5624**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch6, adc1

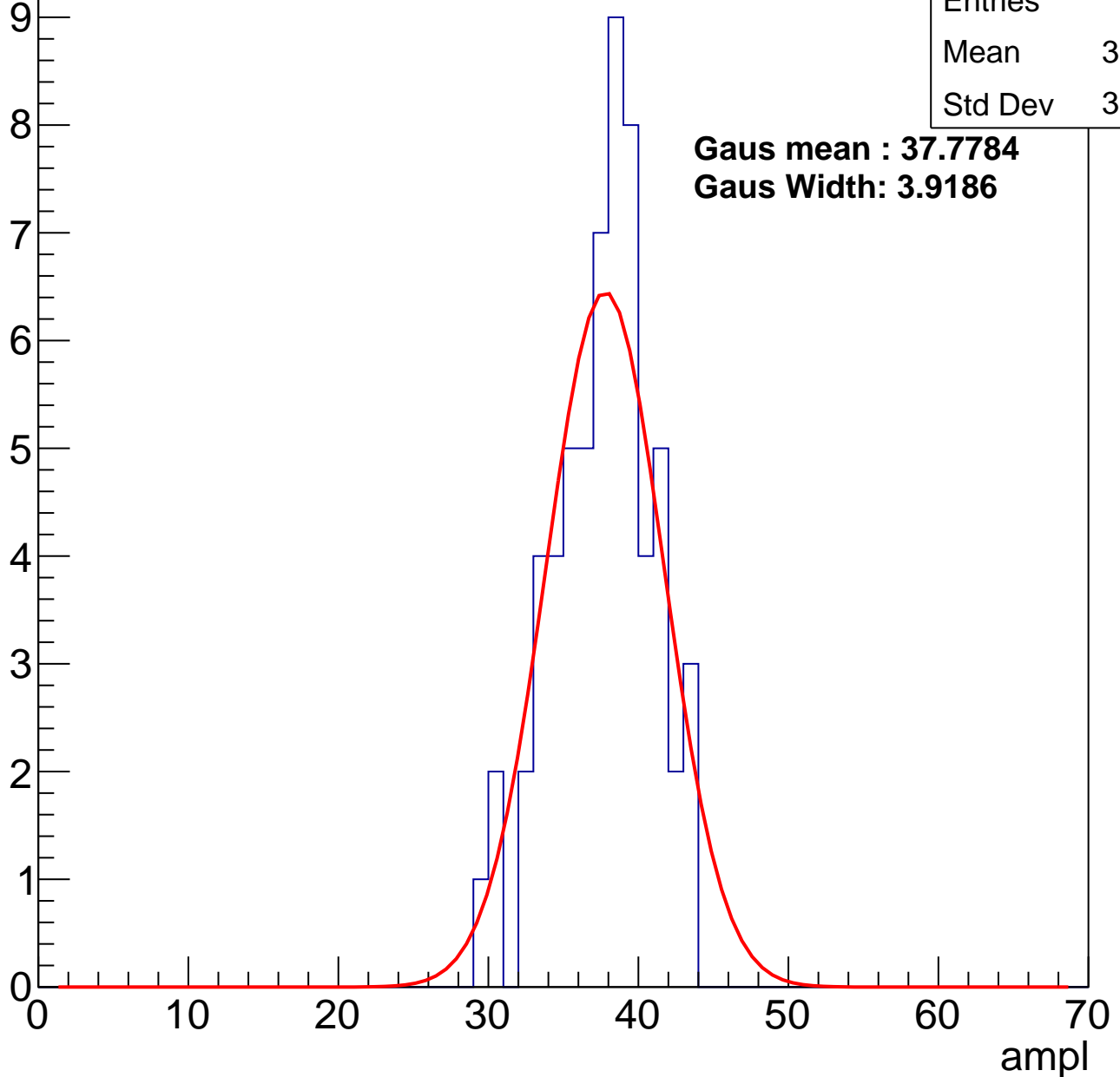
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	37.16
Std Dev	3.265

**Gaus mean : 37.7784**

**Gaus Width: 3.9186**



# B0L102S, U6-ch6, adc2

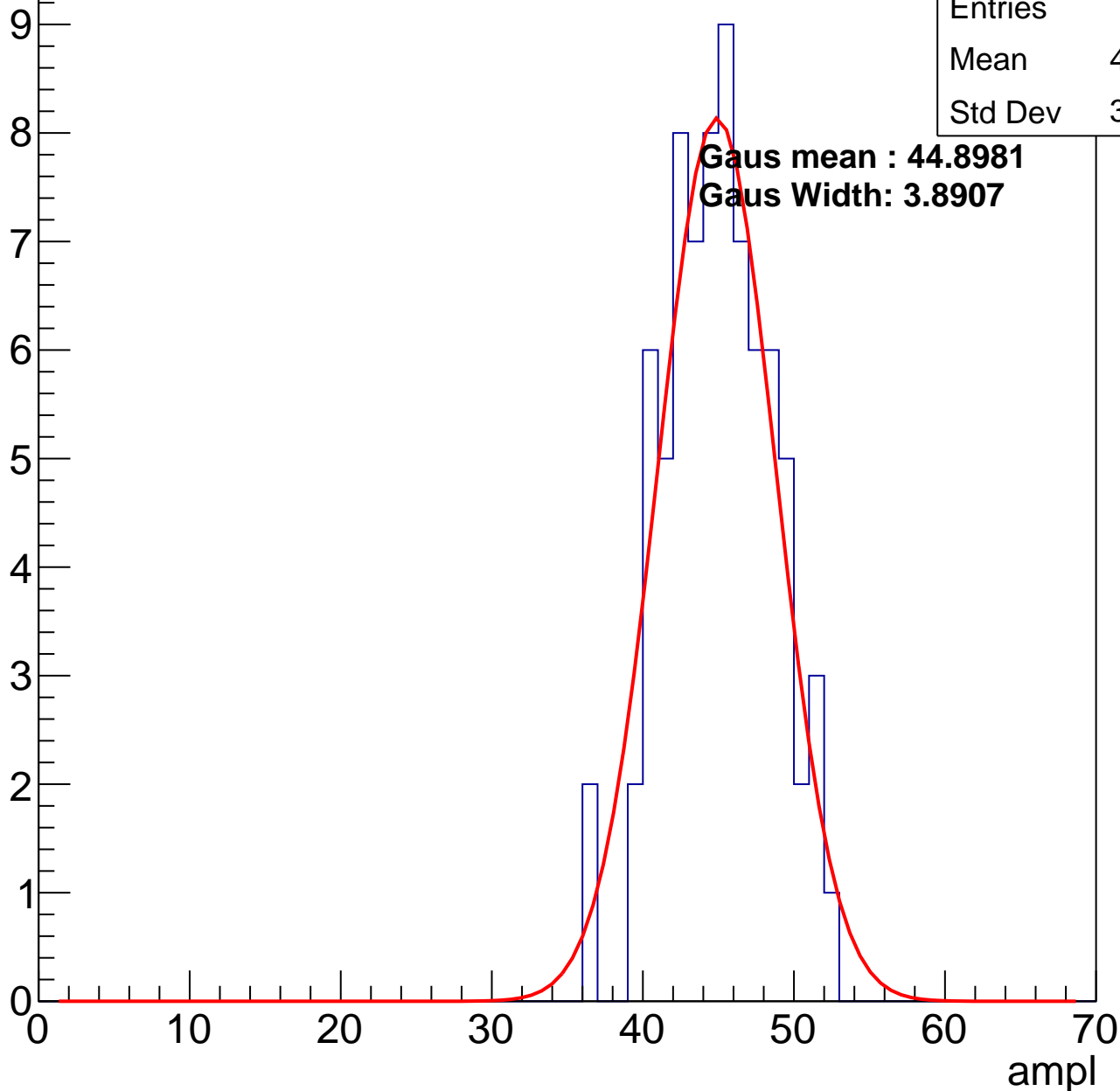
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	44.56
Std Dev	3.473

**Gaus mean : 44.8981**

**Gaus Width: 3.8907**

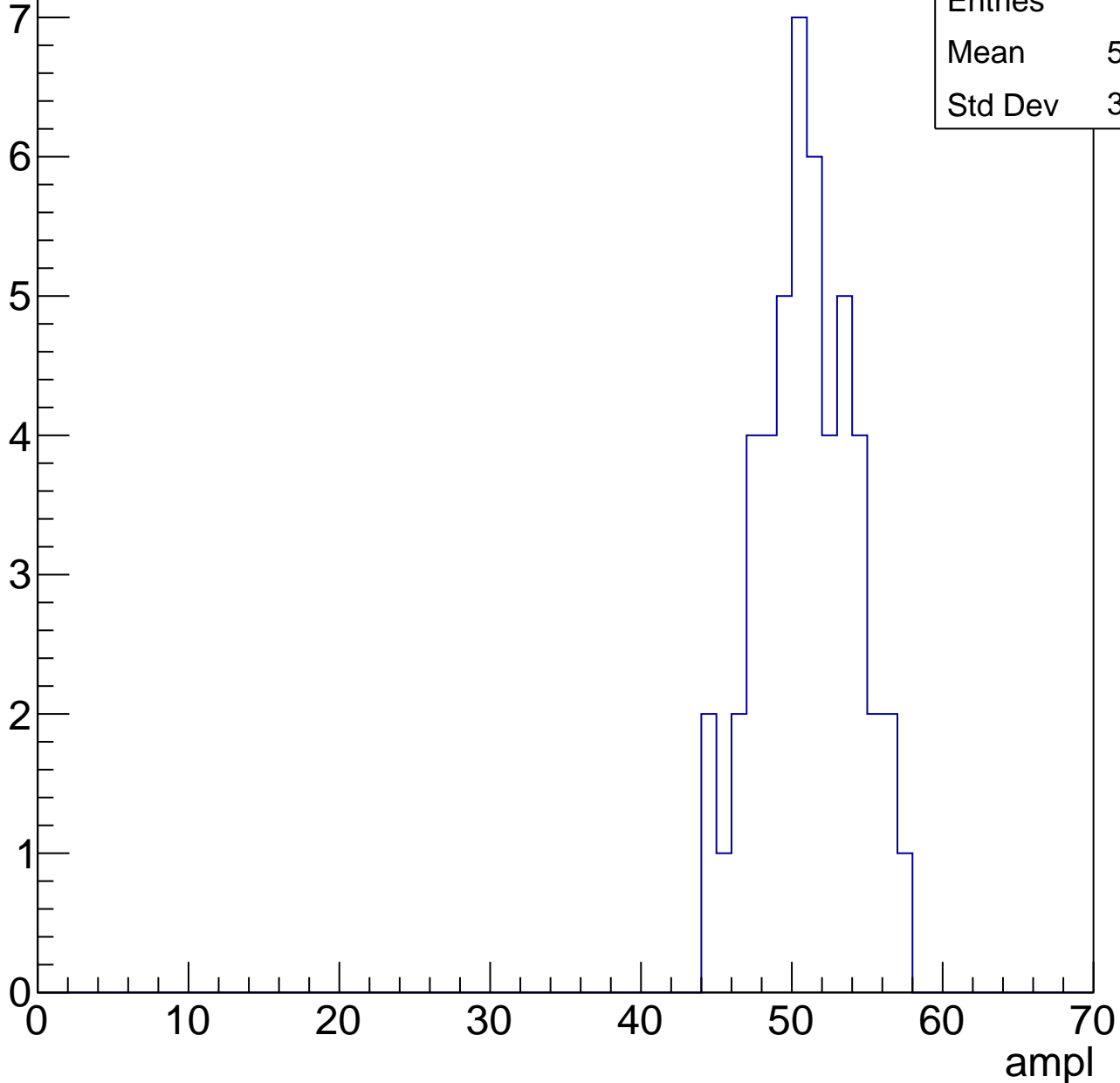


# B0L102S, U6-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	50.49
Std Dev	3.118

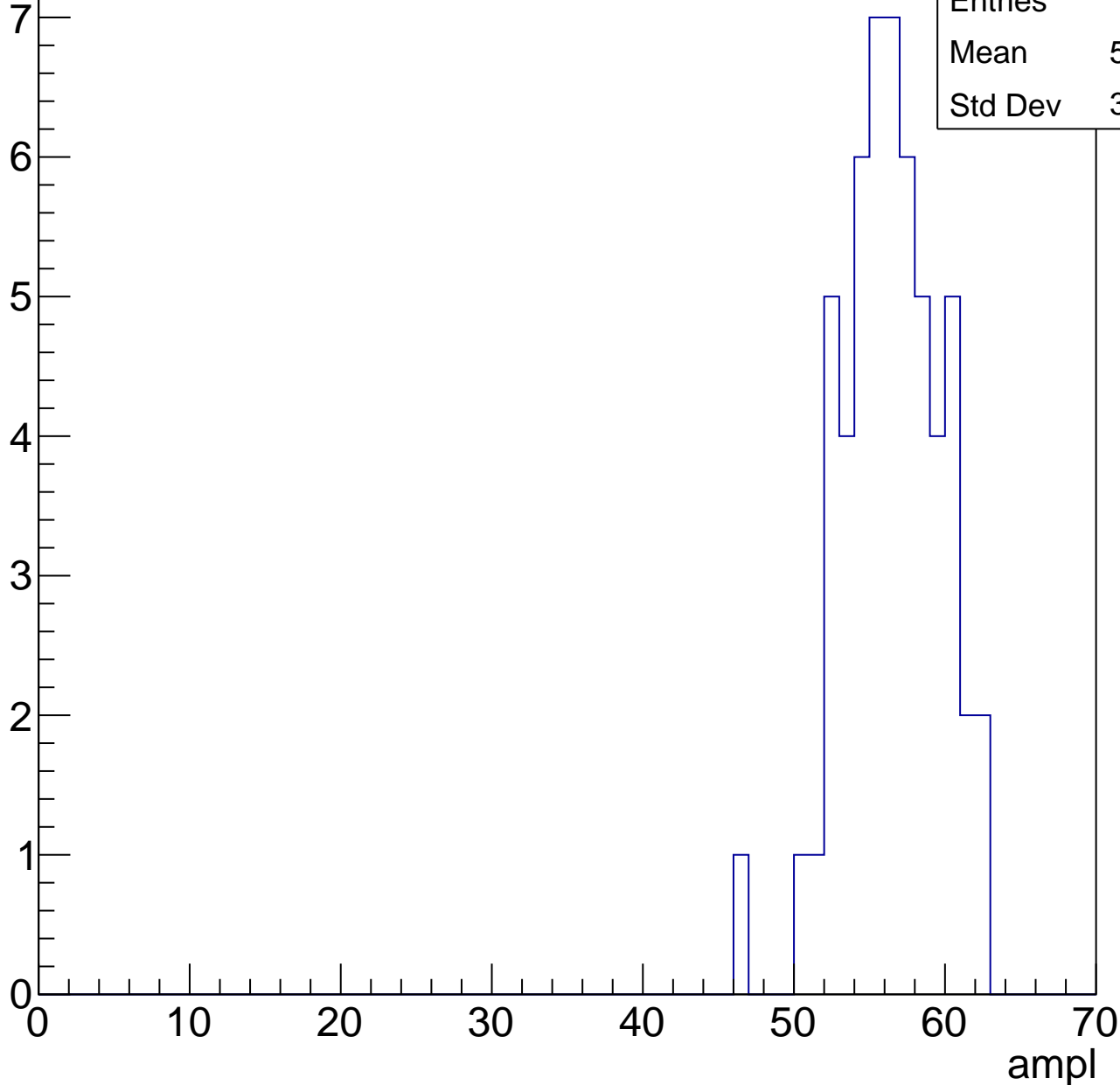


# B0L102S, U6-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

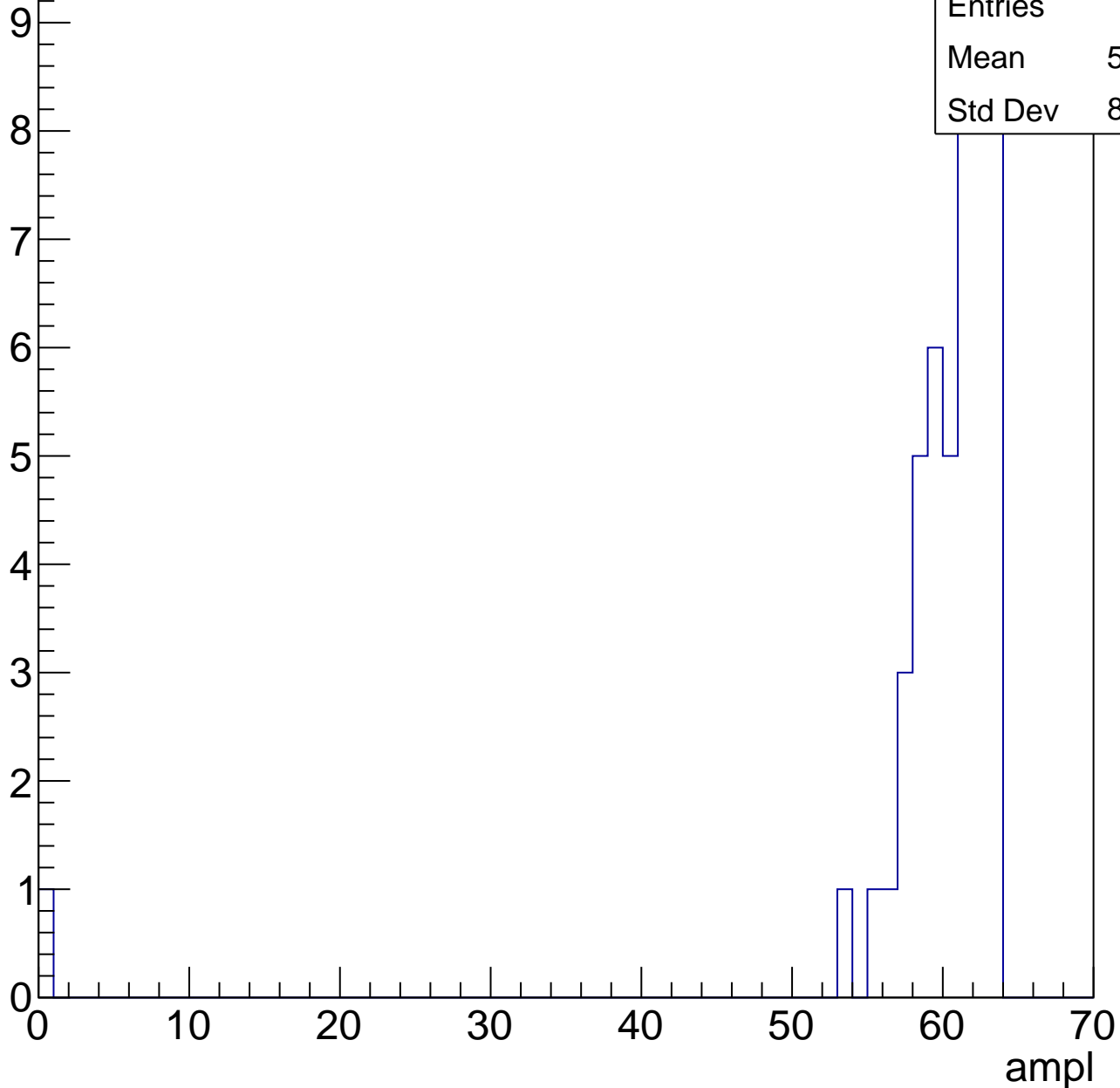
Entries	56
Mean	55.96
Std Dev	3.196



# B0L102S, U6-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	63
Std Dev	0

ampl



# B0L102S, U6-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch7, adc0

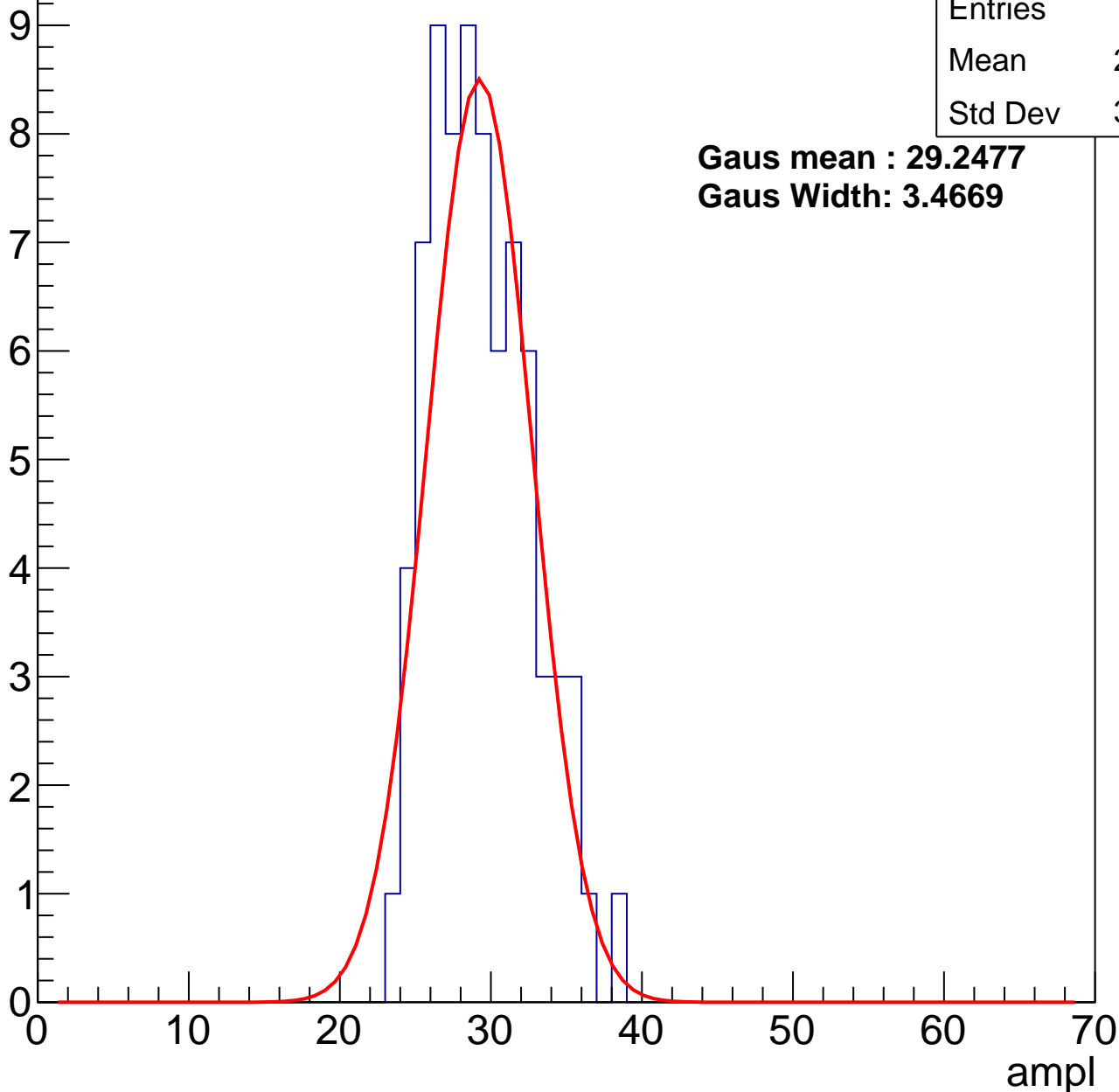
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	28.91
Std Dev	3.281

**Gaus mean : 29.2477**

**Gaus Width: 3.4669**



# B0L102S, U6-ch7, adc1

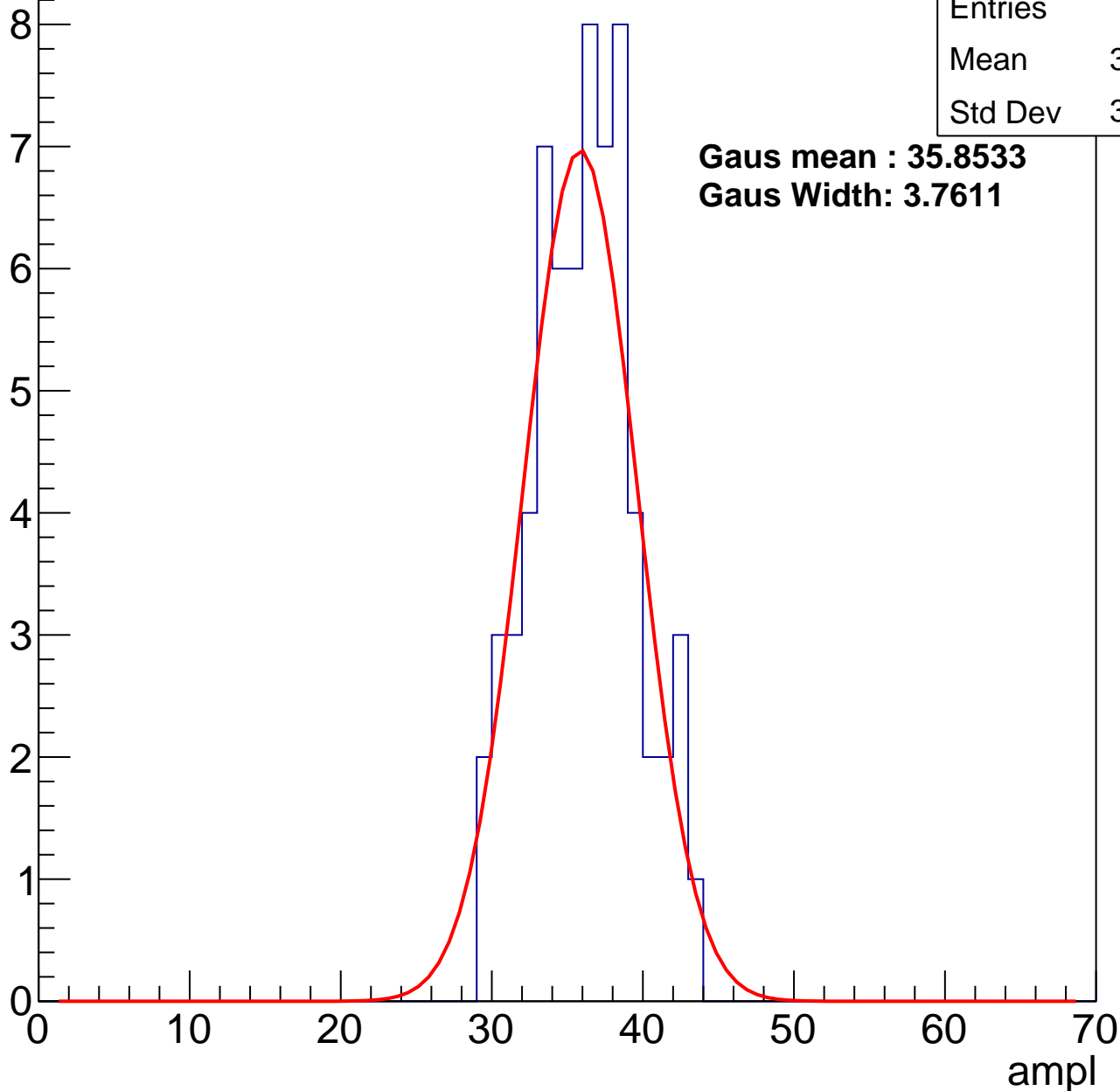
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	35.64
Std Dev	3.356

**Gaus mean : 35.8533**

**Gaus Width: 3.7611**



# B0L102S, U6-ch7, adc2

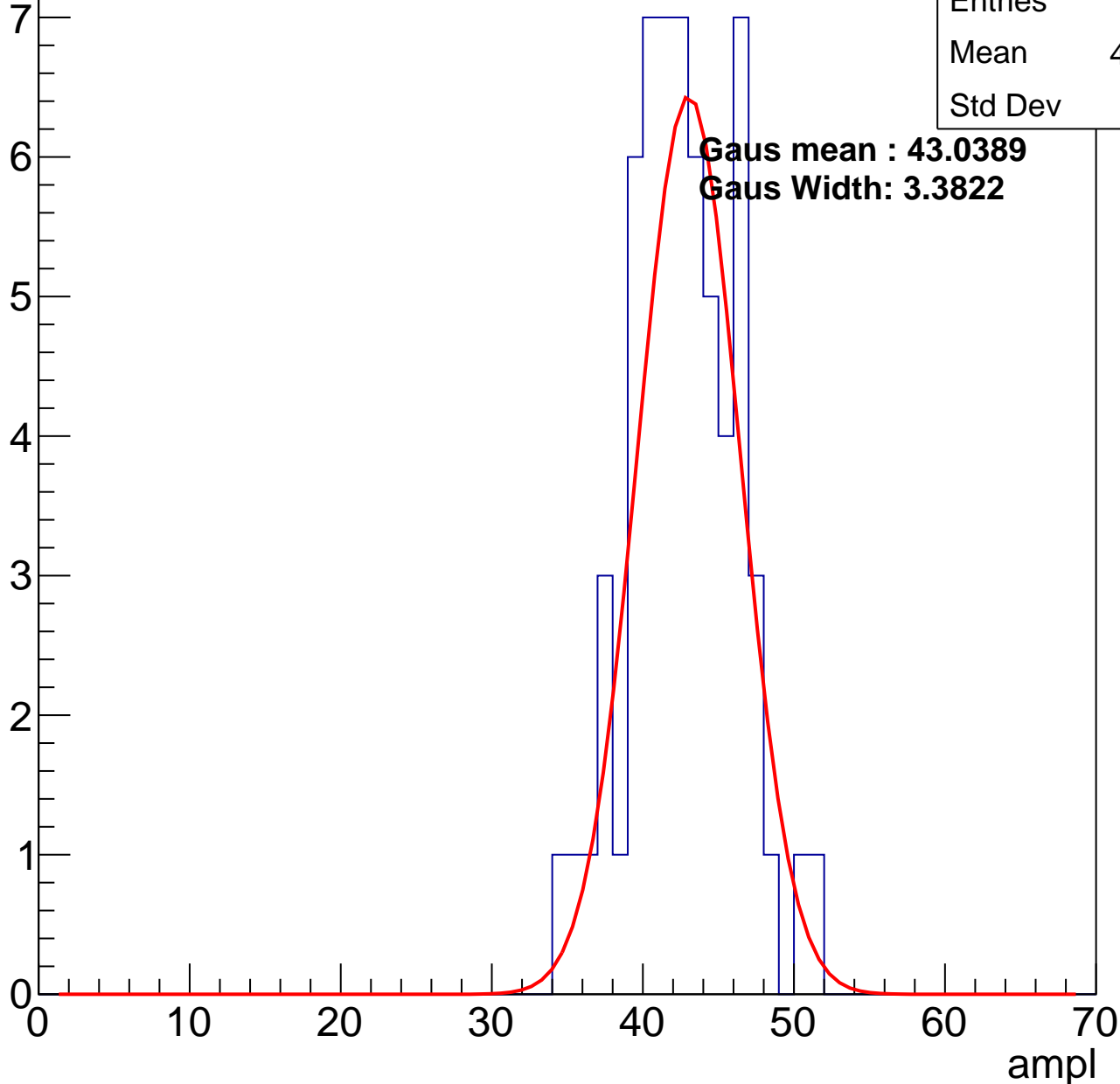
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.24
Std Dev	3.49

**Gaus mean : 43.0389**

**Gaus Width: 3.3822**

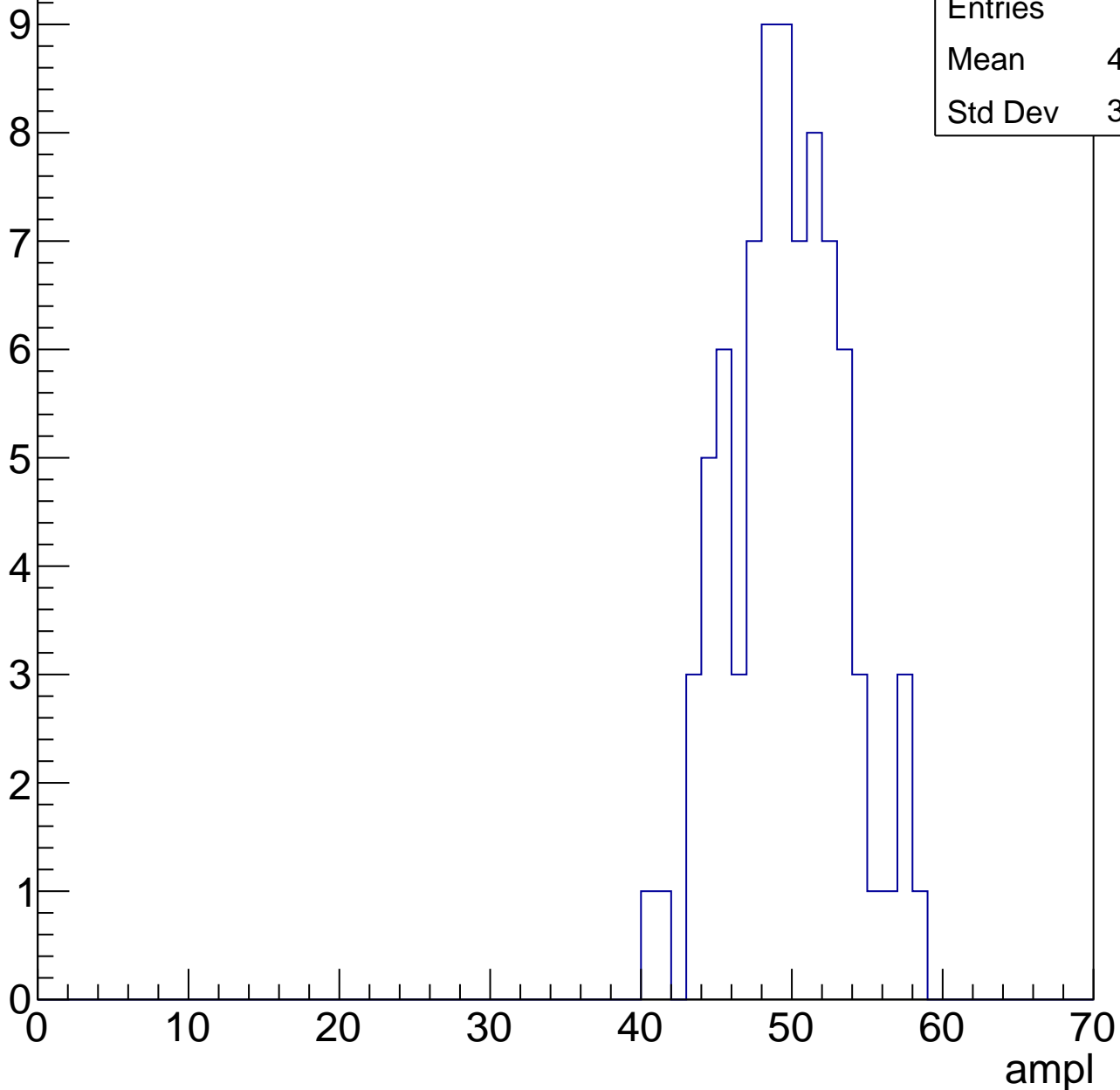


# B0L102S, U6-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	49.16
Std Dev	3.796

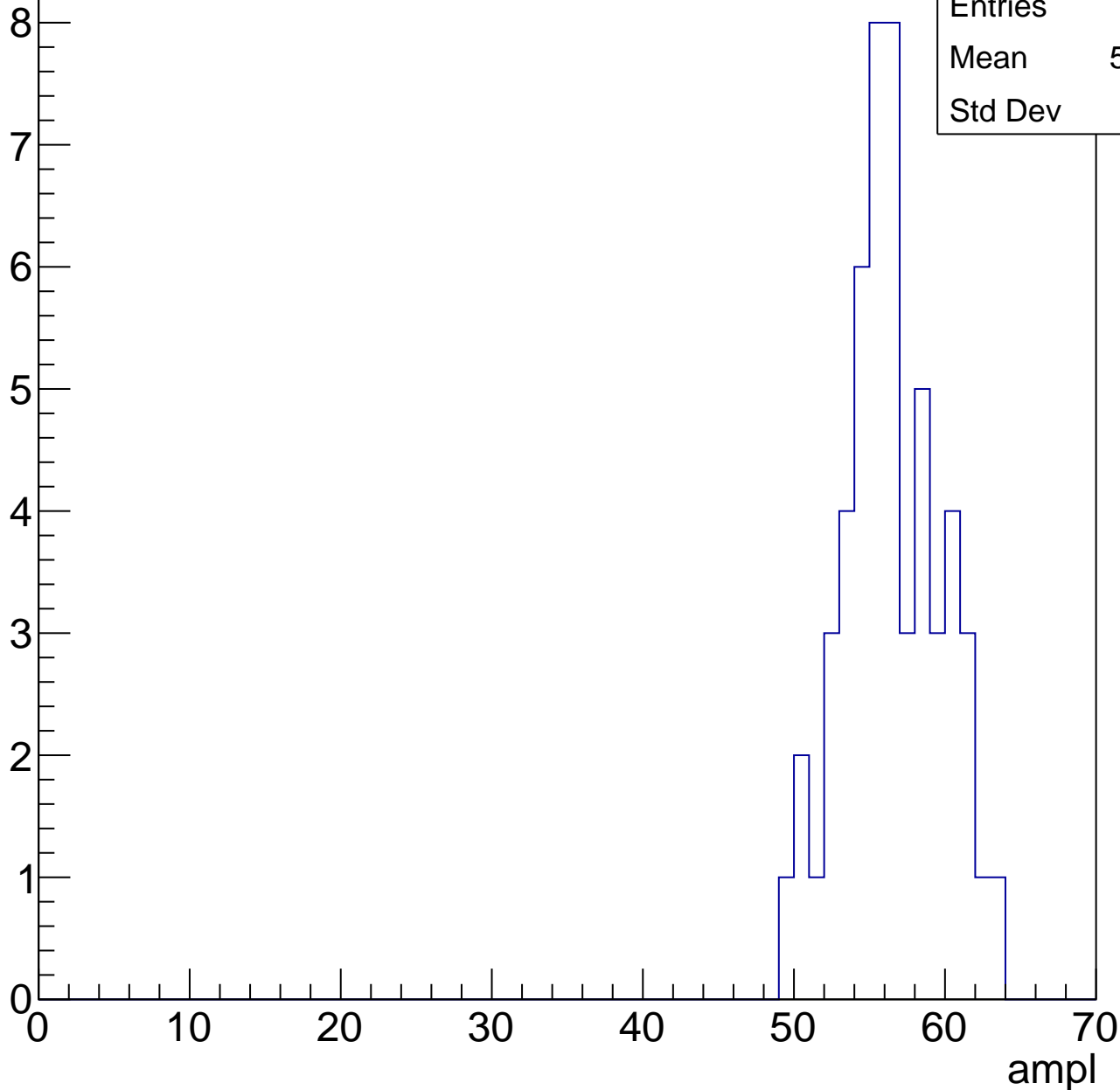


# B0L102S, U6-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	55.96
Std Dev	3.18



# B0L102S, U6-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	59.9
Std Dev	2.287

0

10

20

30

40

50

60

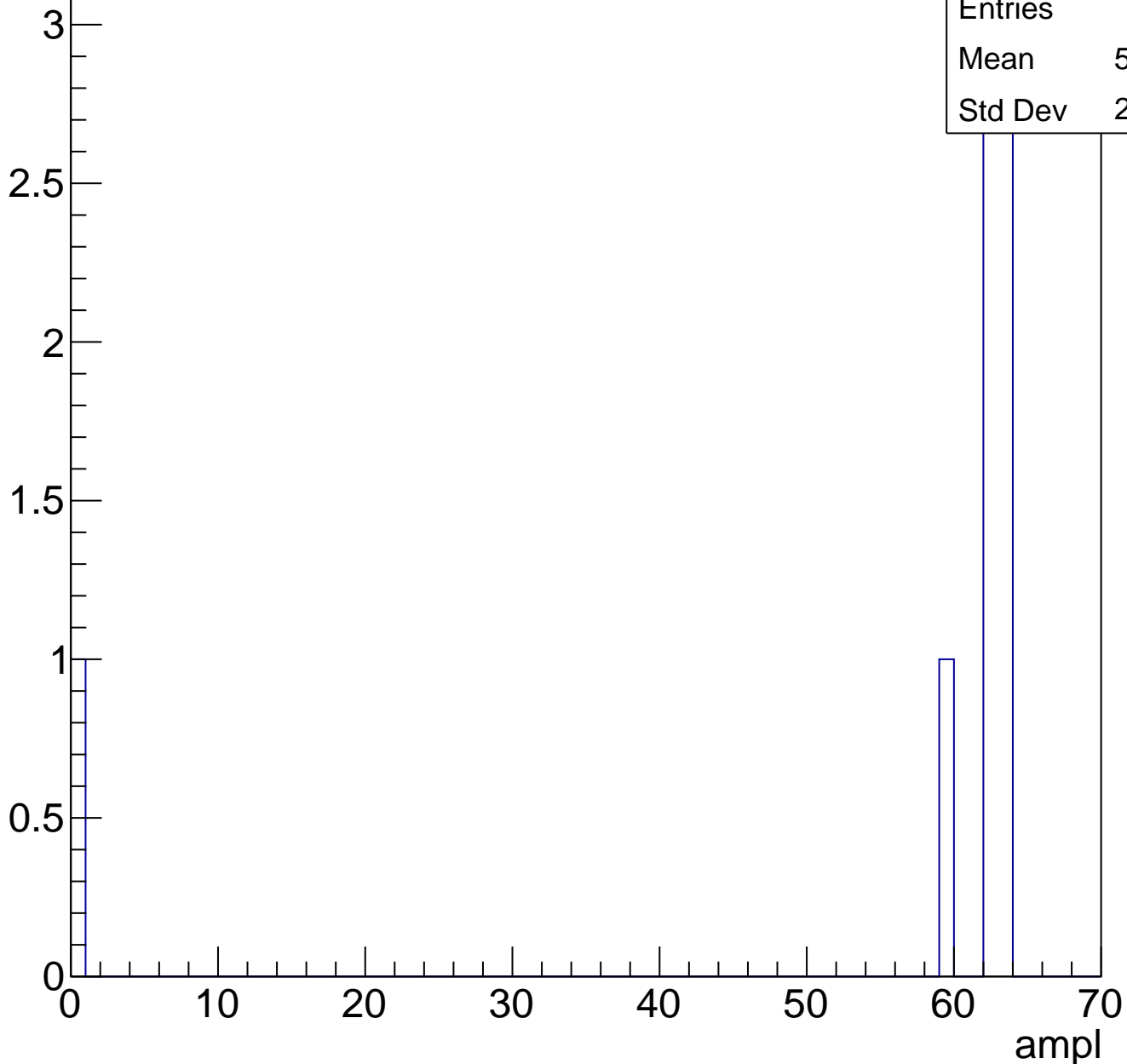
70

ampl

# B0L102S, U6-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

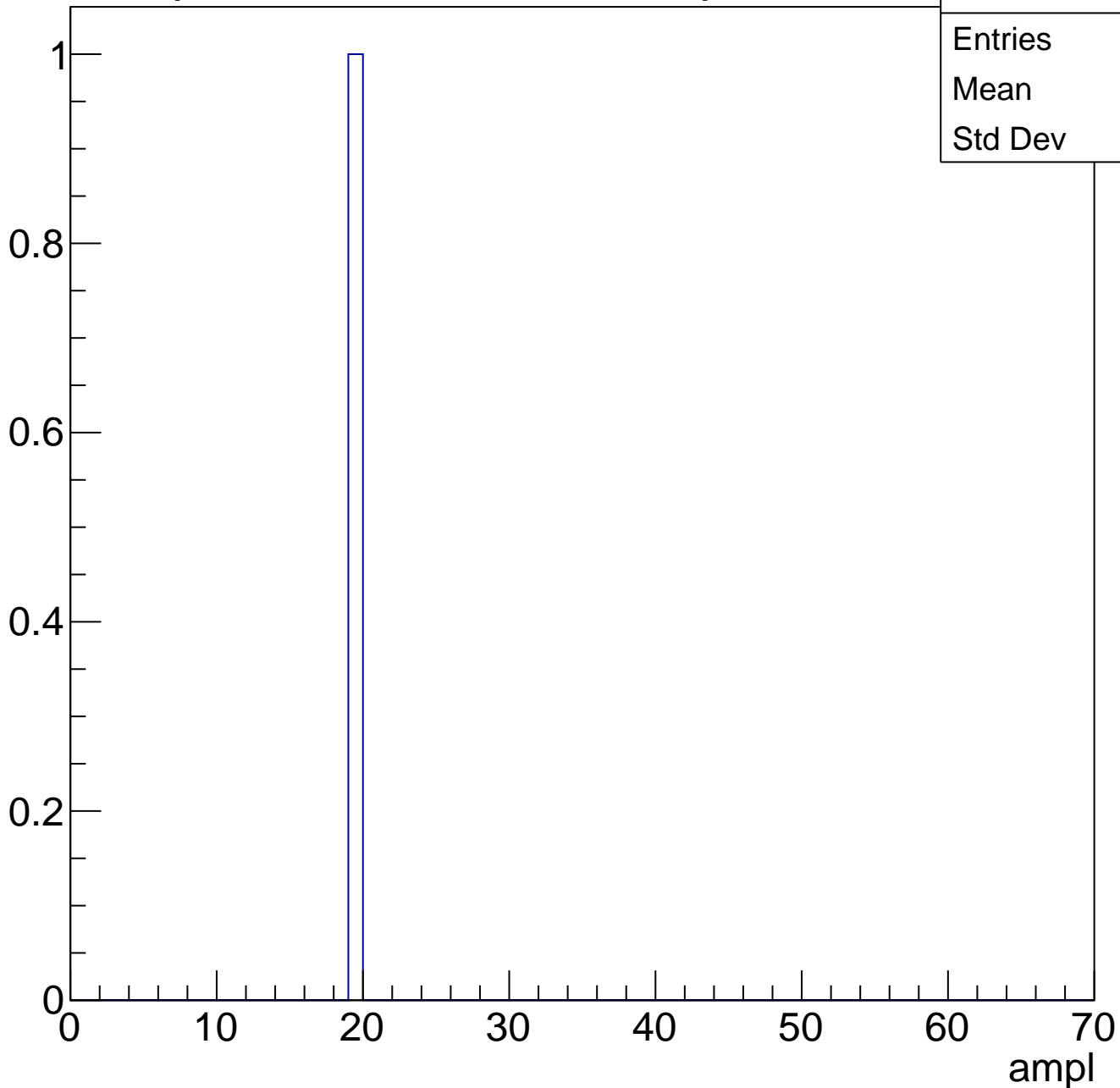




# B0L102S, U6-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	19
Std Dev	0

# B0L102S, U6-ch8, adc0

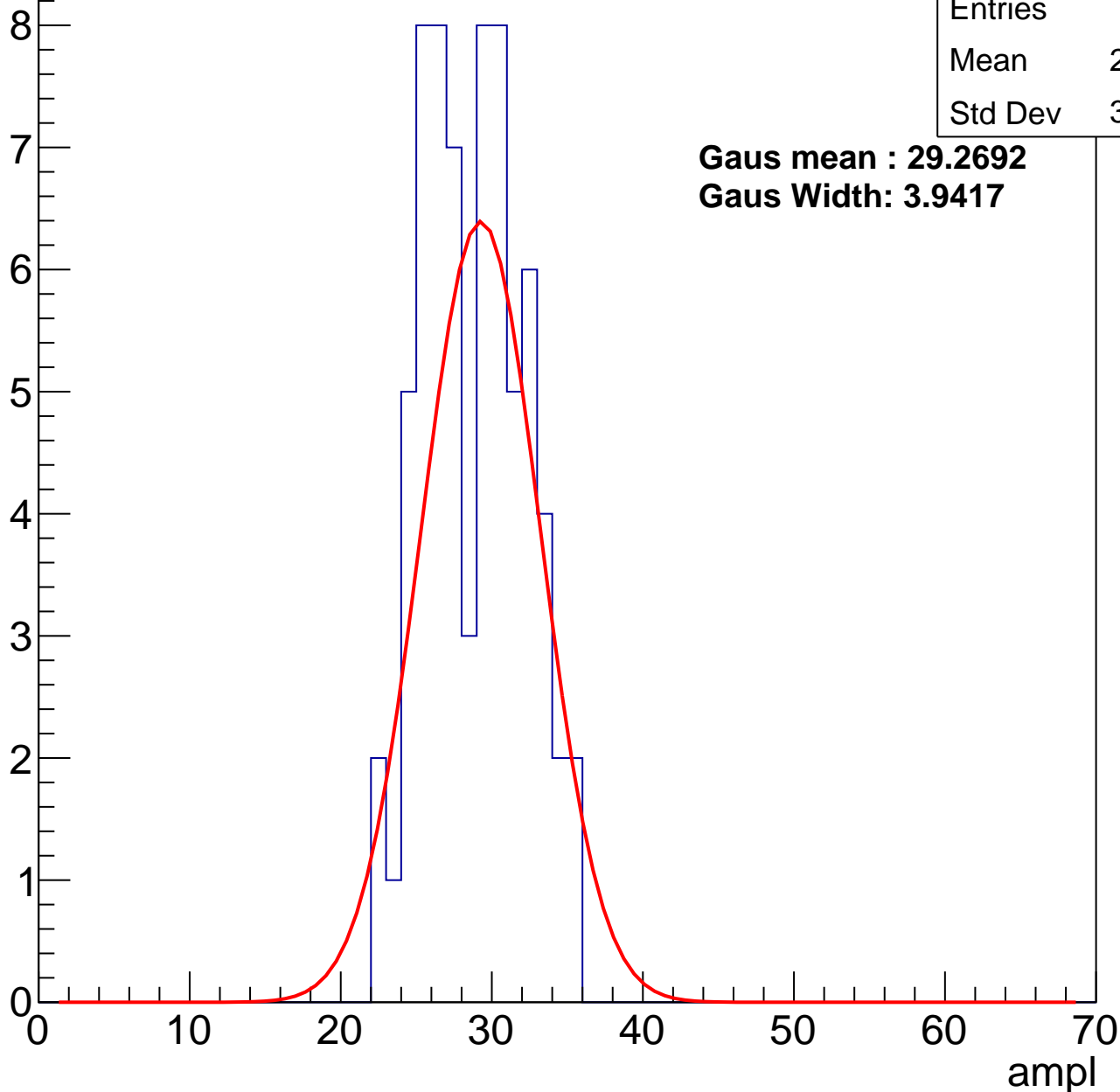
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	28.36
Std Dev	3.253

**Gaus mean : 29.2692**

**Gaus Width: 3.9417**



# B0L102S, U6-ch8, adc1

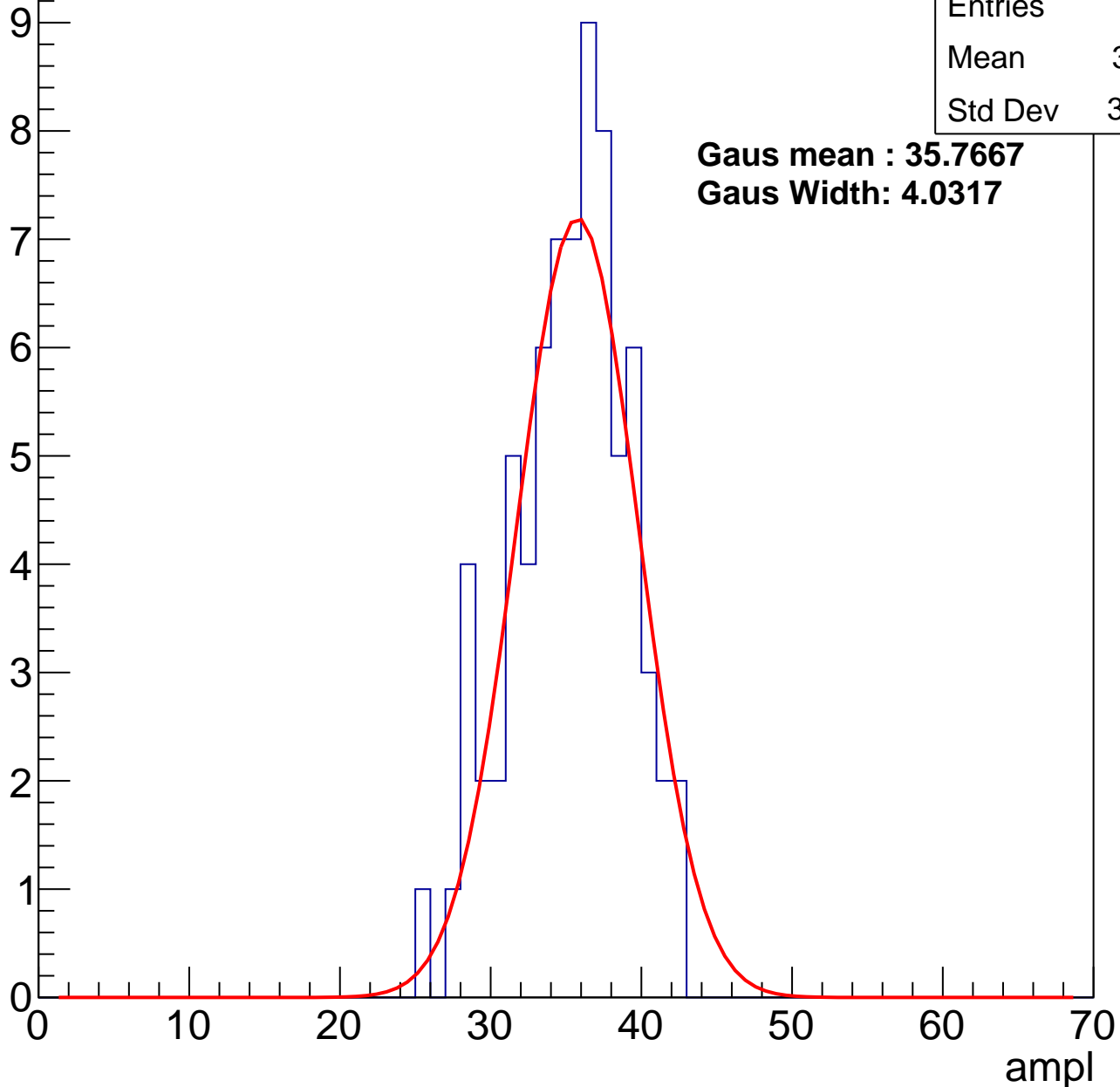
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	34.81
Std Dev	3.773

**Gaus mean : 35.7667**

**Gaus Width: 4.0317**



# B0L102S, U6-ch8, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	60
Mean	41.6
Std Dev	2.922

**Gaus mean : 42.1303**

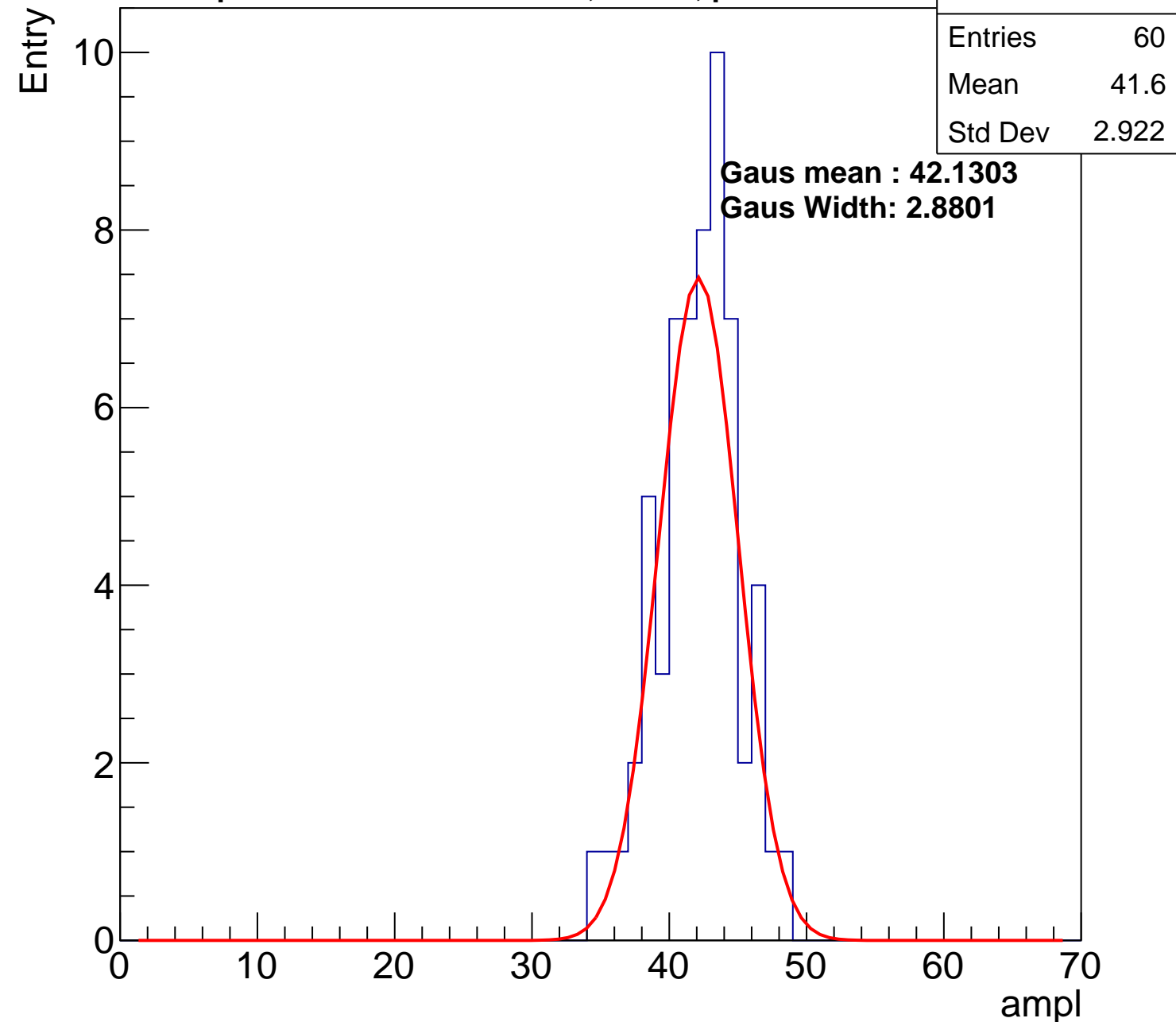
**Gaus Width: 2.8801**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

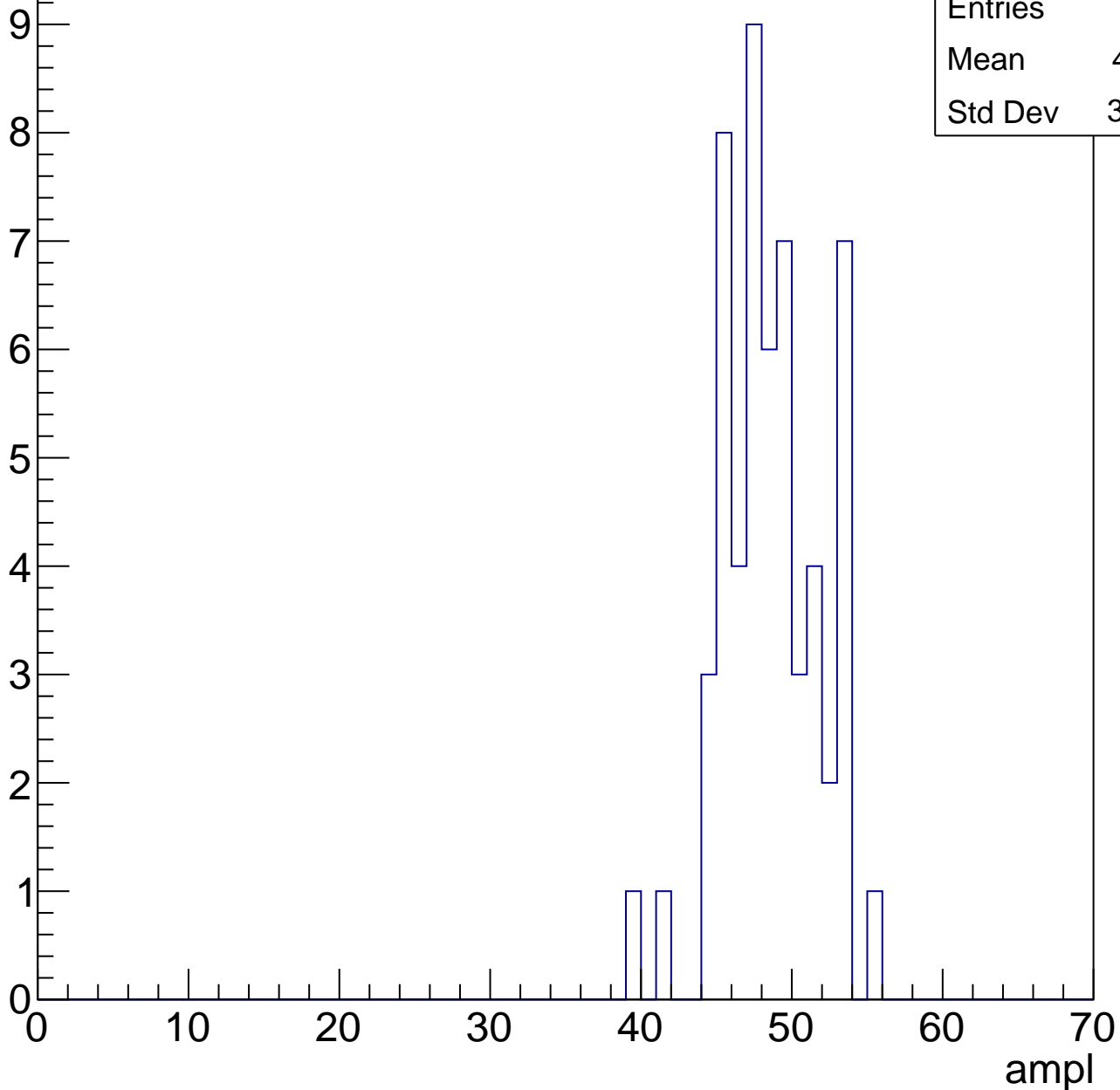


# B0L102S, U6-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	48.11
Std Dev	3.233

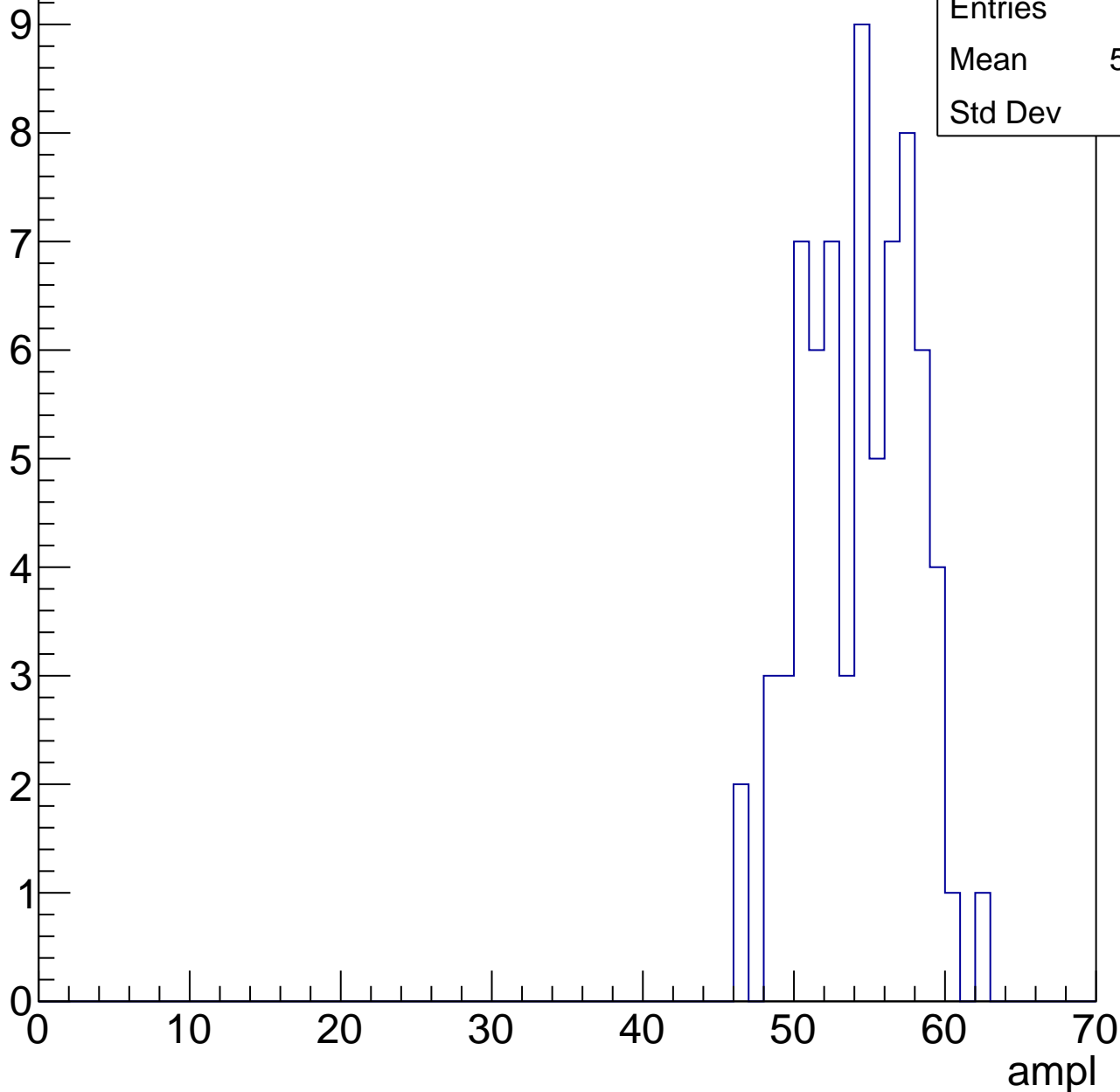


# B0L102S, U6-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	53.85
Std Dev	3.55

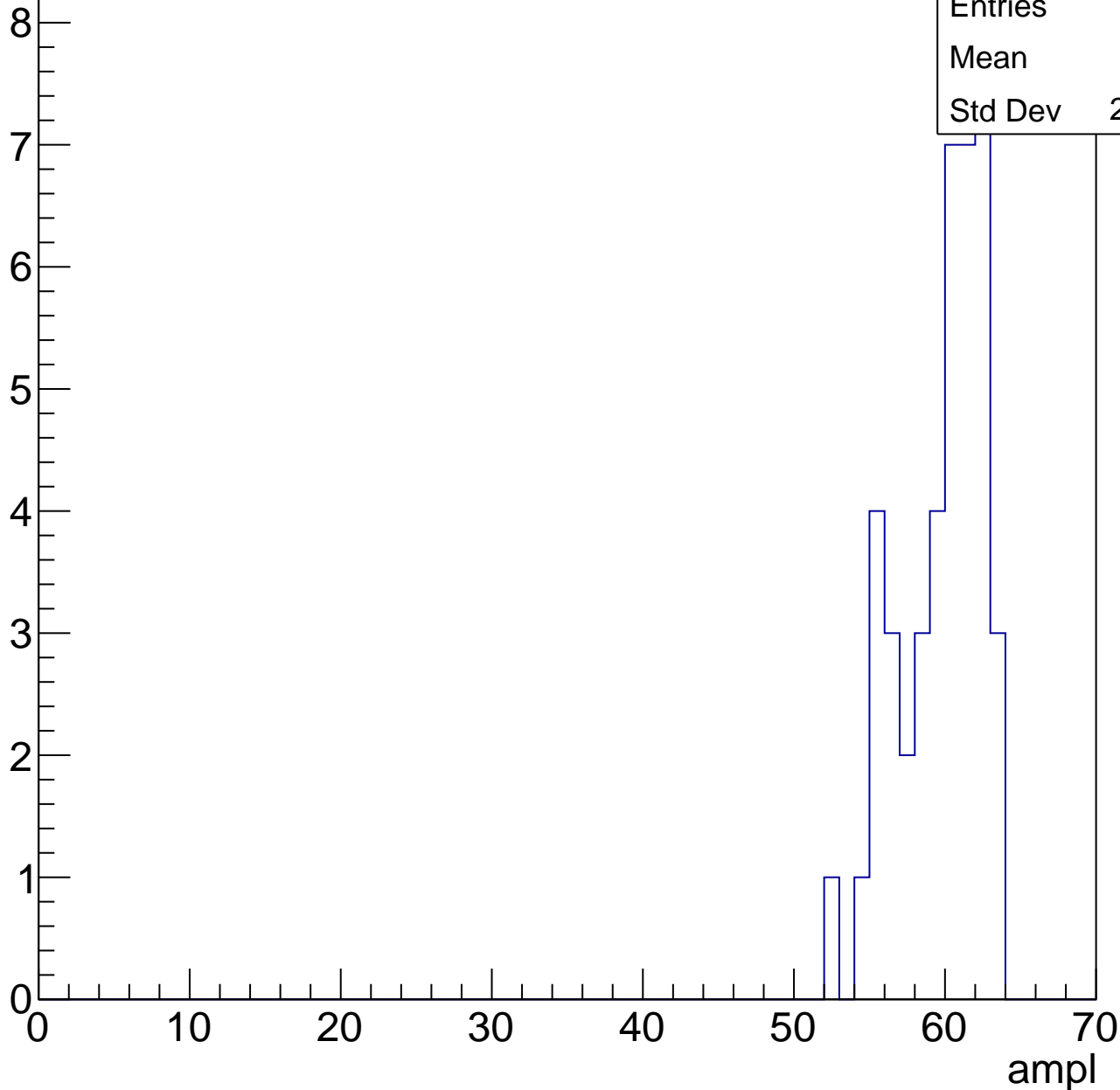


# B0L102S, U6-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	43
Mean	59.3
Std Dev	2.758

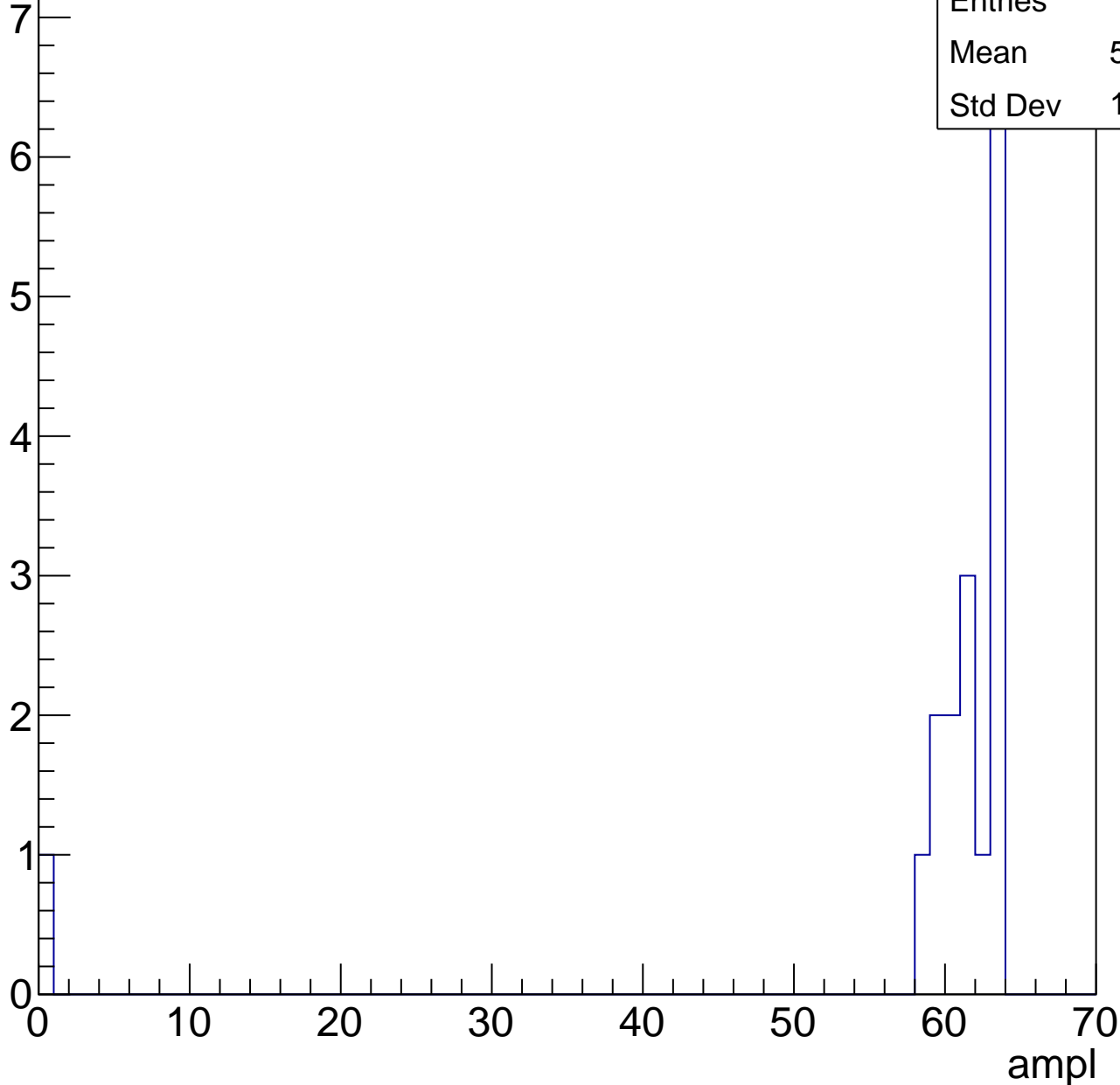


# B0L102S, U6-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	17
Mean	57.76
Std Dev	14.53





# B0L102S, U6-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	10
Std Dev	10

ampl

0 10 20 30 40 50 60 70

# B0L102S, U6-ch9, adc0

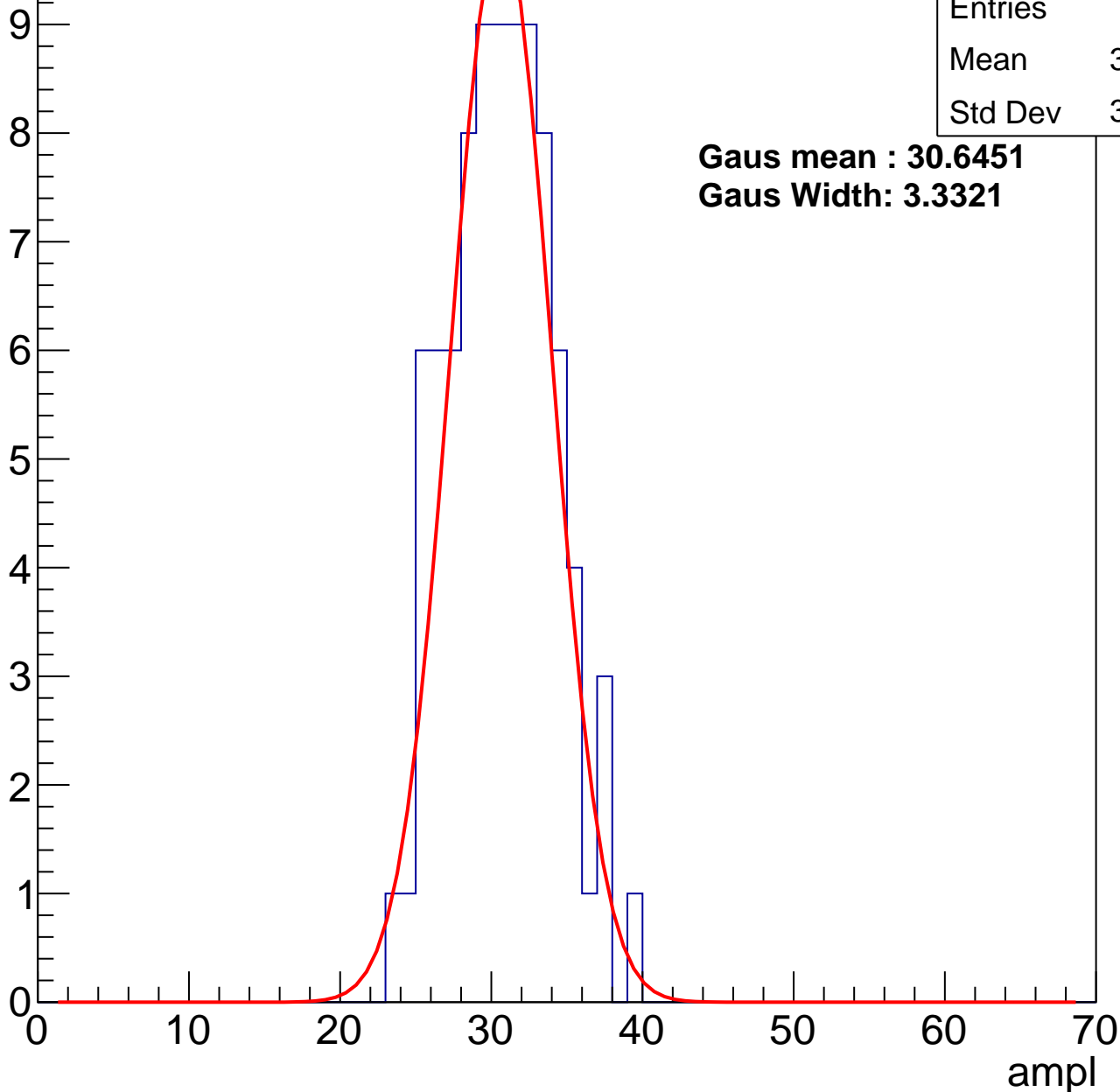
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	87
Mean	30.24
Std Dev	3.387

**Gaus mean : 30.6451**

**Gaus Width: 3.3321**



# B0L102S, U6-ch9, adc1

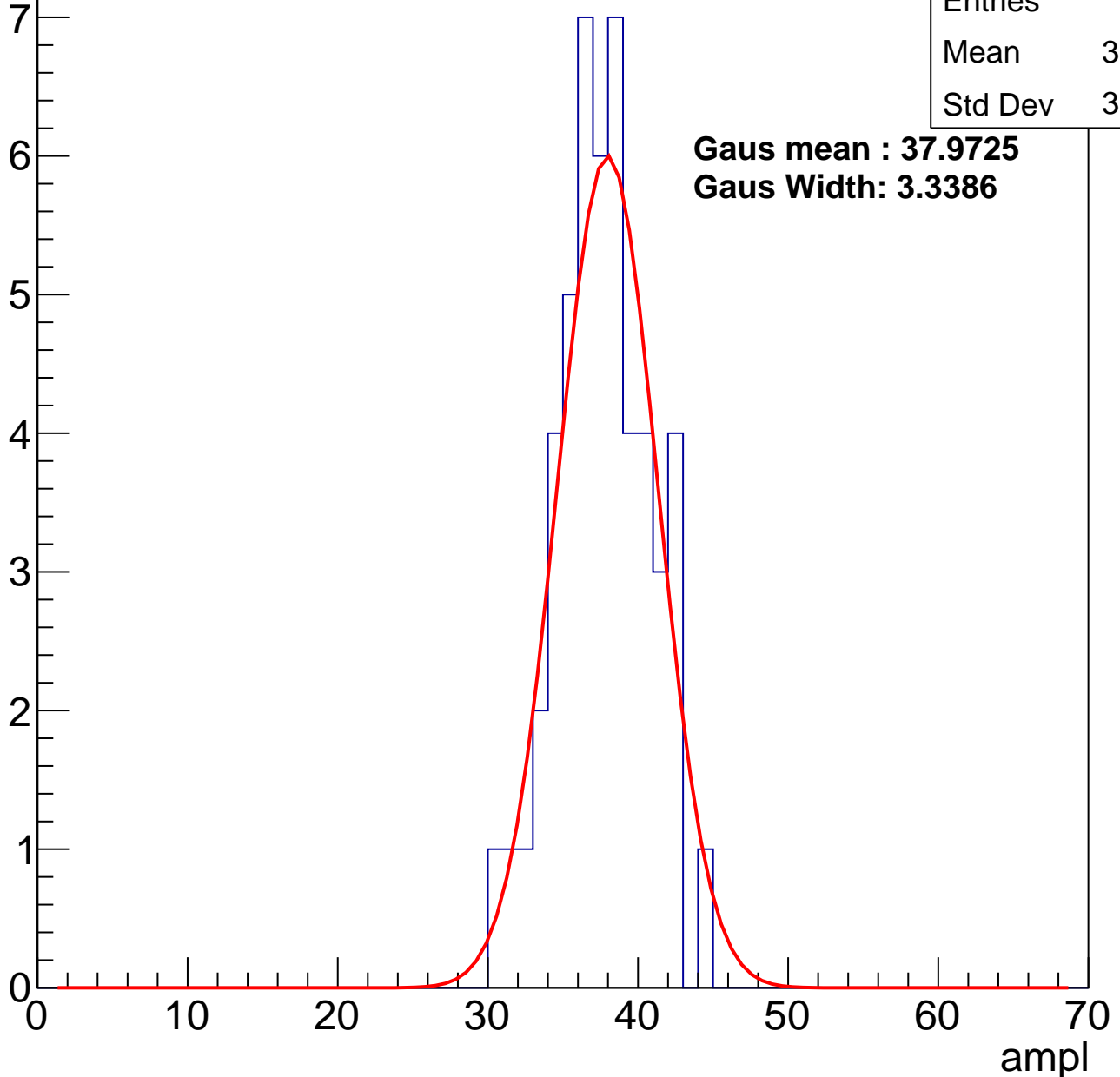
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	37.22
Std Dev	3.028

**Gaus mean : 37.9725**

**Gaus Width: 3.3386**



# B0L102S, U6-ch9, adc2

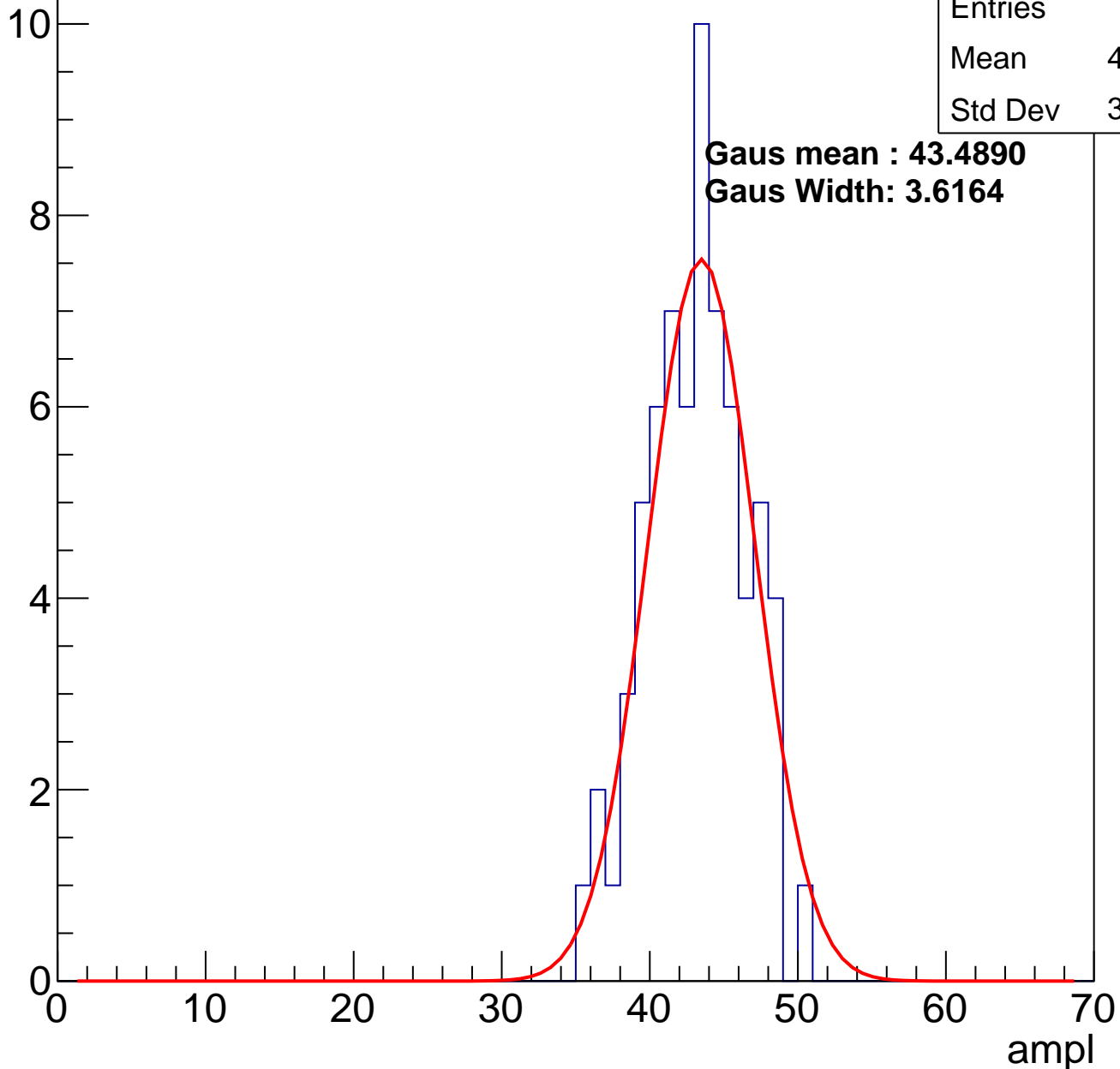
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	42.66
Std Dev	3.275

**Gaus mean : 43.4890**

**Gaus Width: 3.6164**

Entry



# B0L102S, U6-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

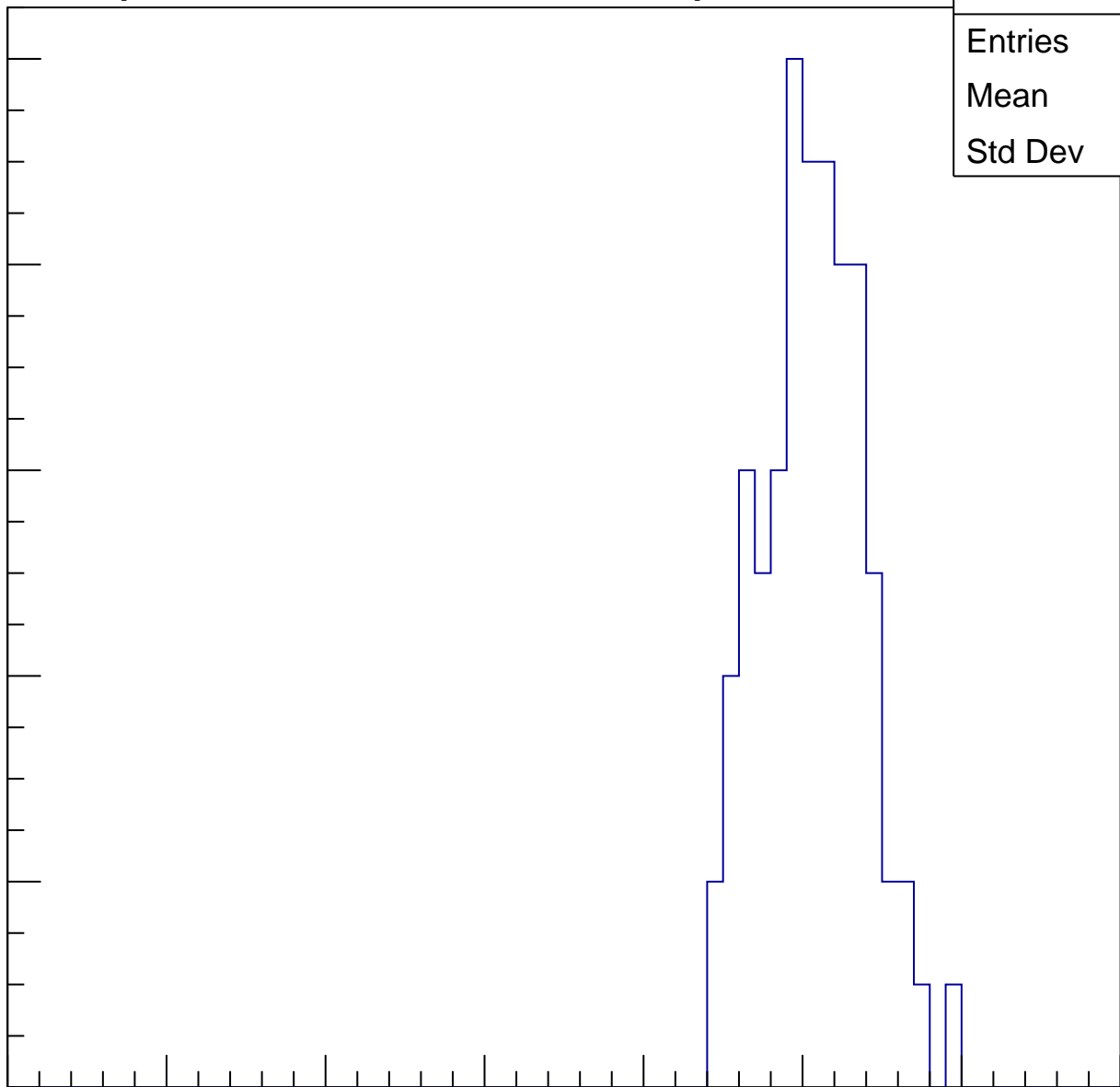
Entries	78
Mean	50.18
Std Dev	3.181

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

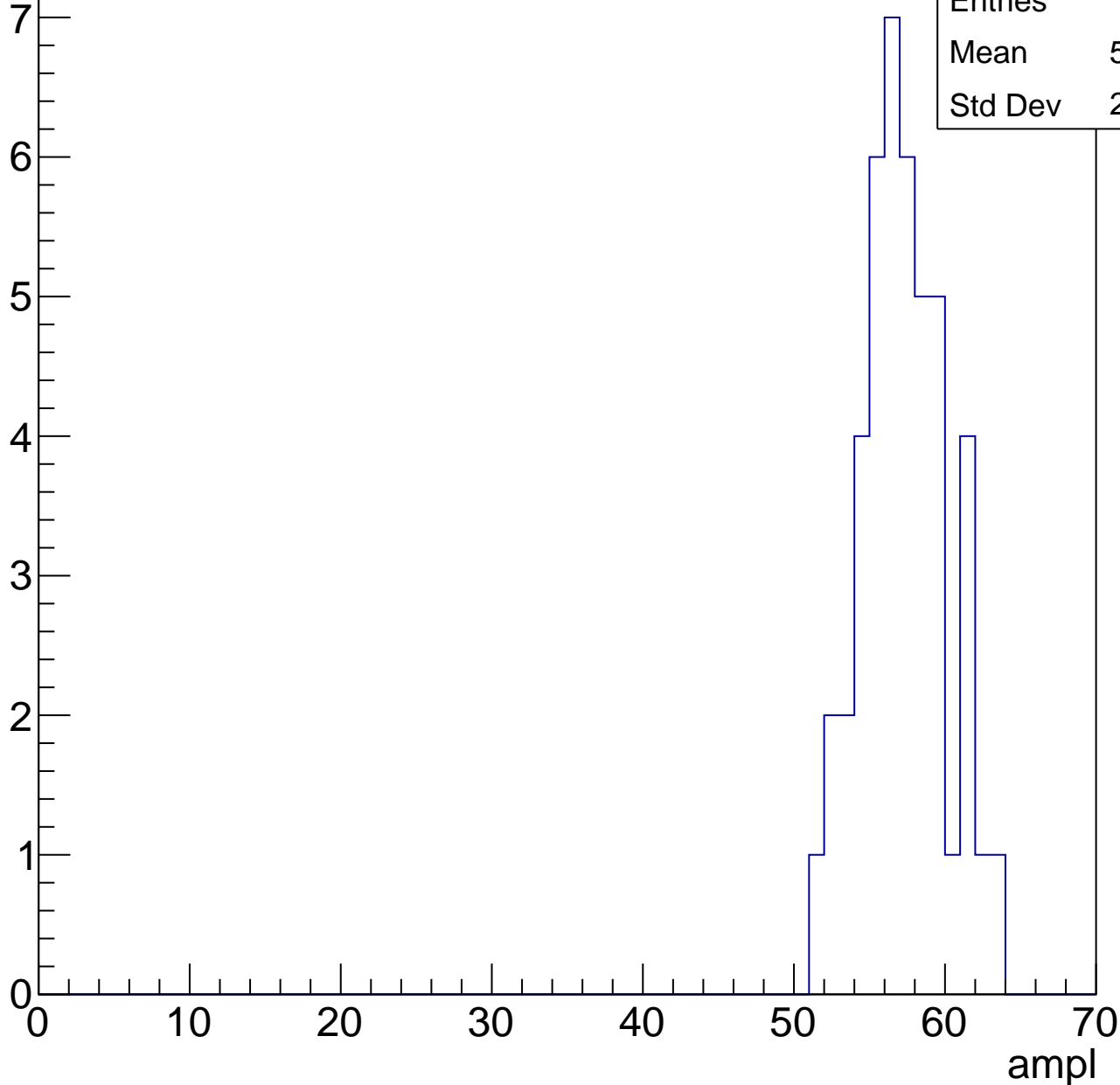


# B0L102S, U6-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

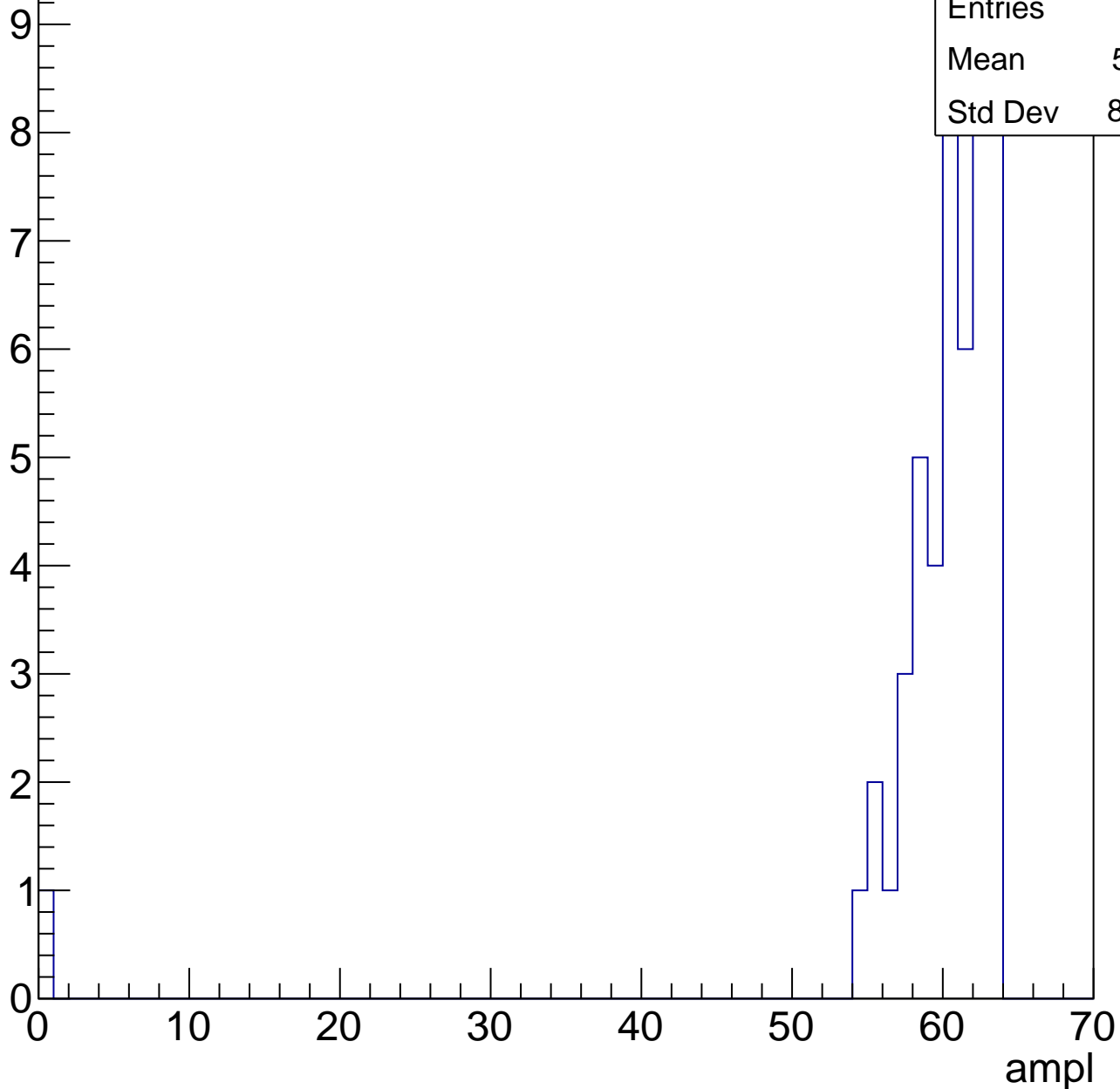
Entries	45
Mean	56.78
Std Dev	2.756



# B0L102S, U6-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

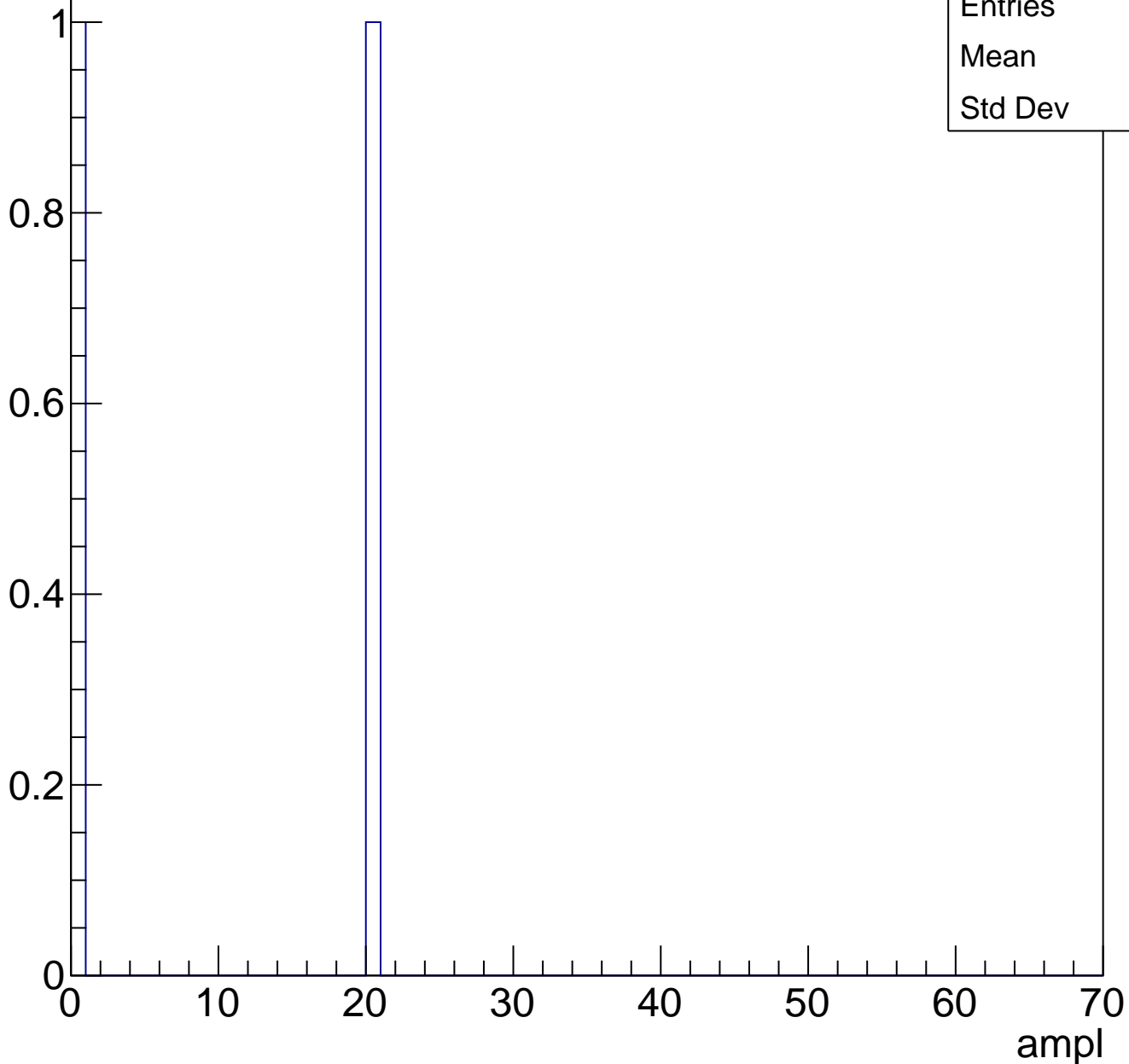




# B0L102S, U6-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L102S, U6-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	93
Mean	29.1
Std Dev	3.471

**Gaus mean : 29.4014**

**Gaus Width: 3.9508**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0 10 20 30 40 50 60 70

# B0L102S, U6-ch10, adc1

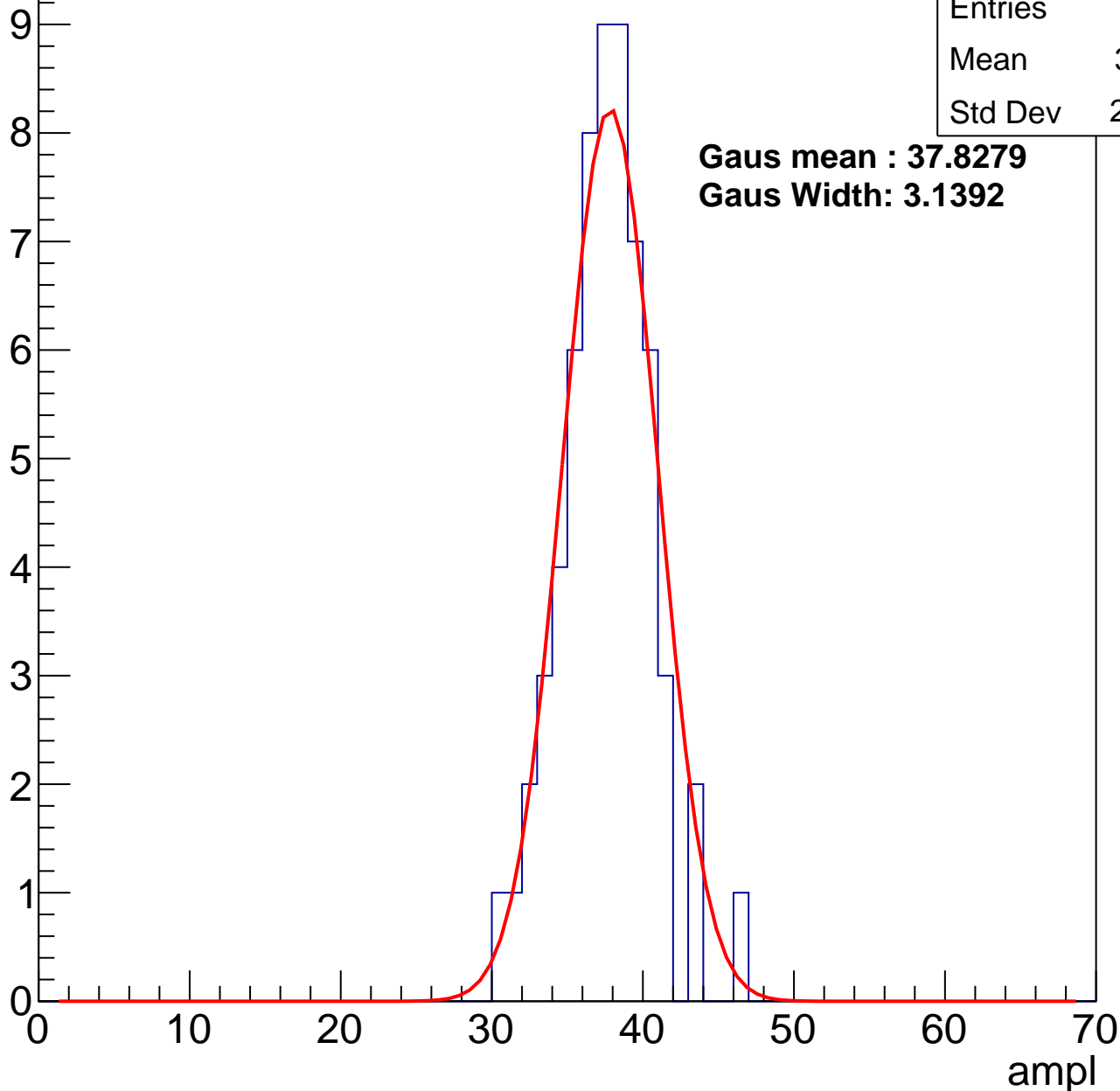
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	37.11
Std Dev	2.957

**Gaus mean : 37.8279**

**Gaus Width: 3.1392**



# B0L102S, U6-ch10, adc2

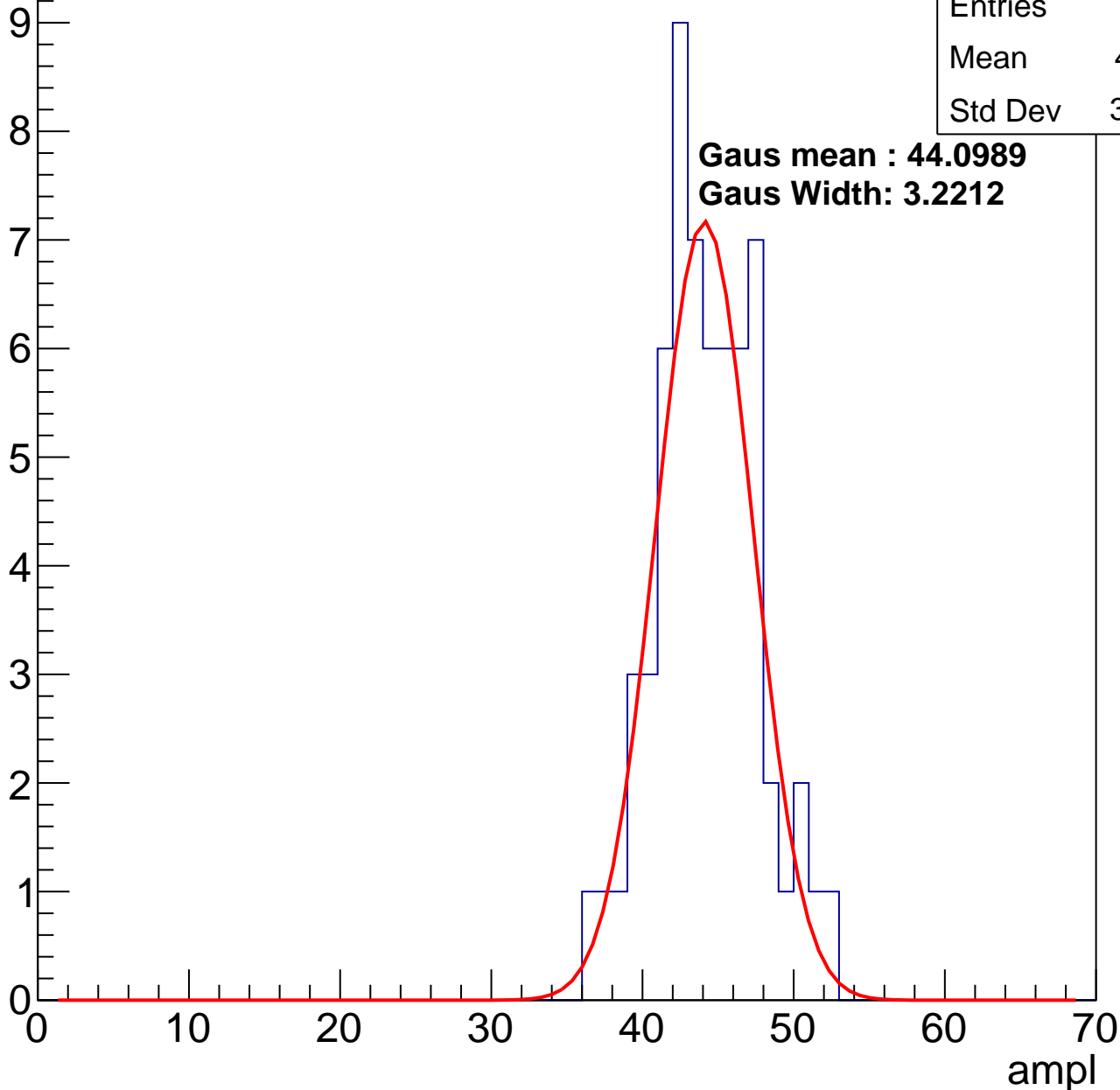
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.81
Std Dev	3.337

**Gaus mean : 44.0989**

**Gaus Width: 3.2212**

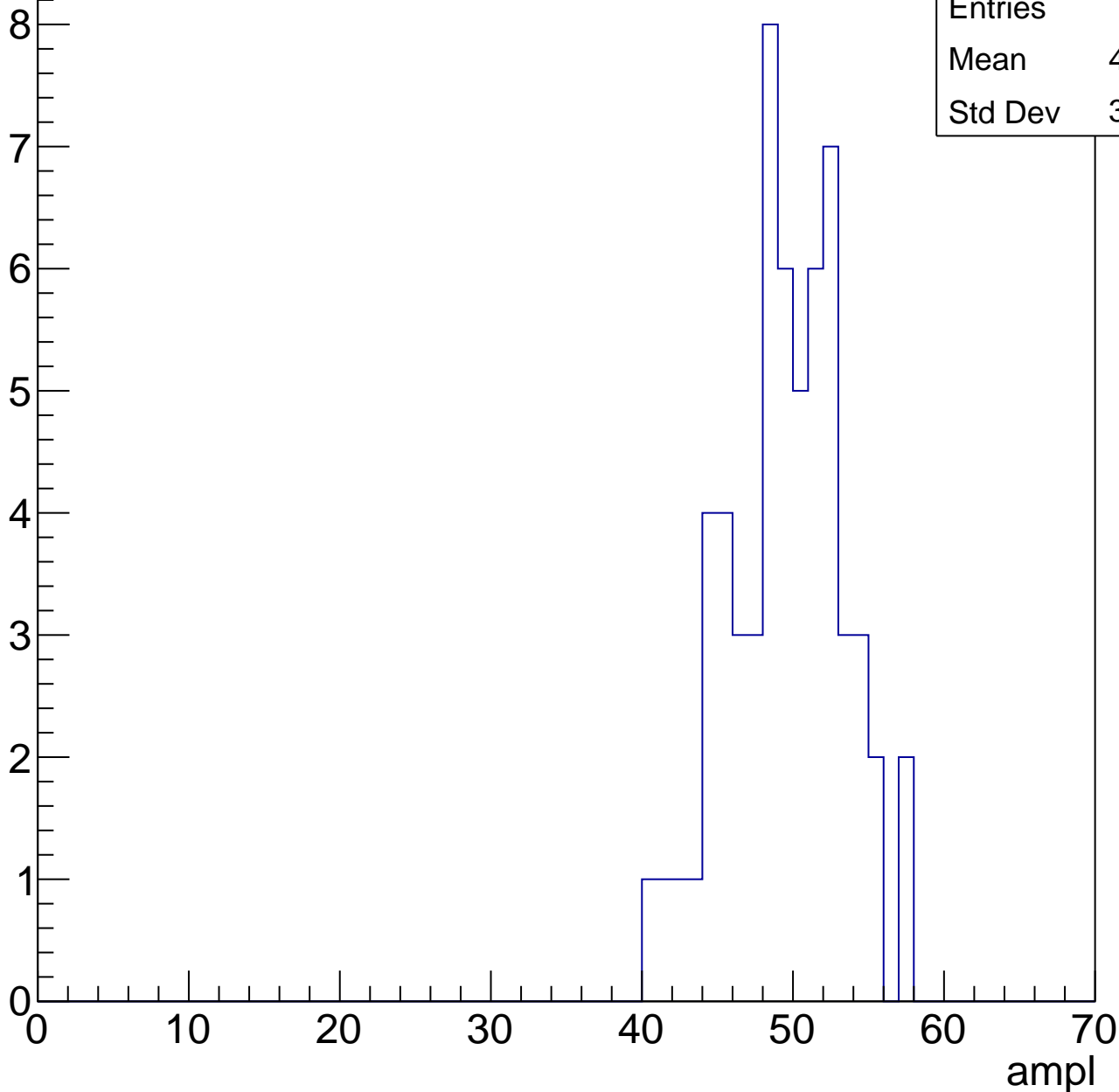


# B0L102S, U6-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	49.07
Std Dev	3.777

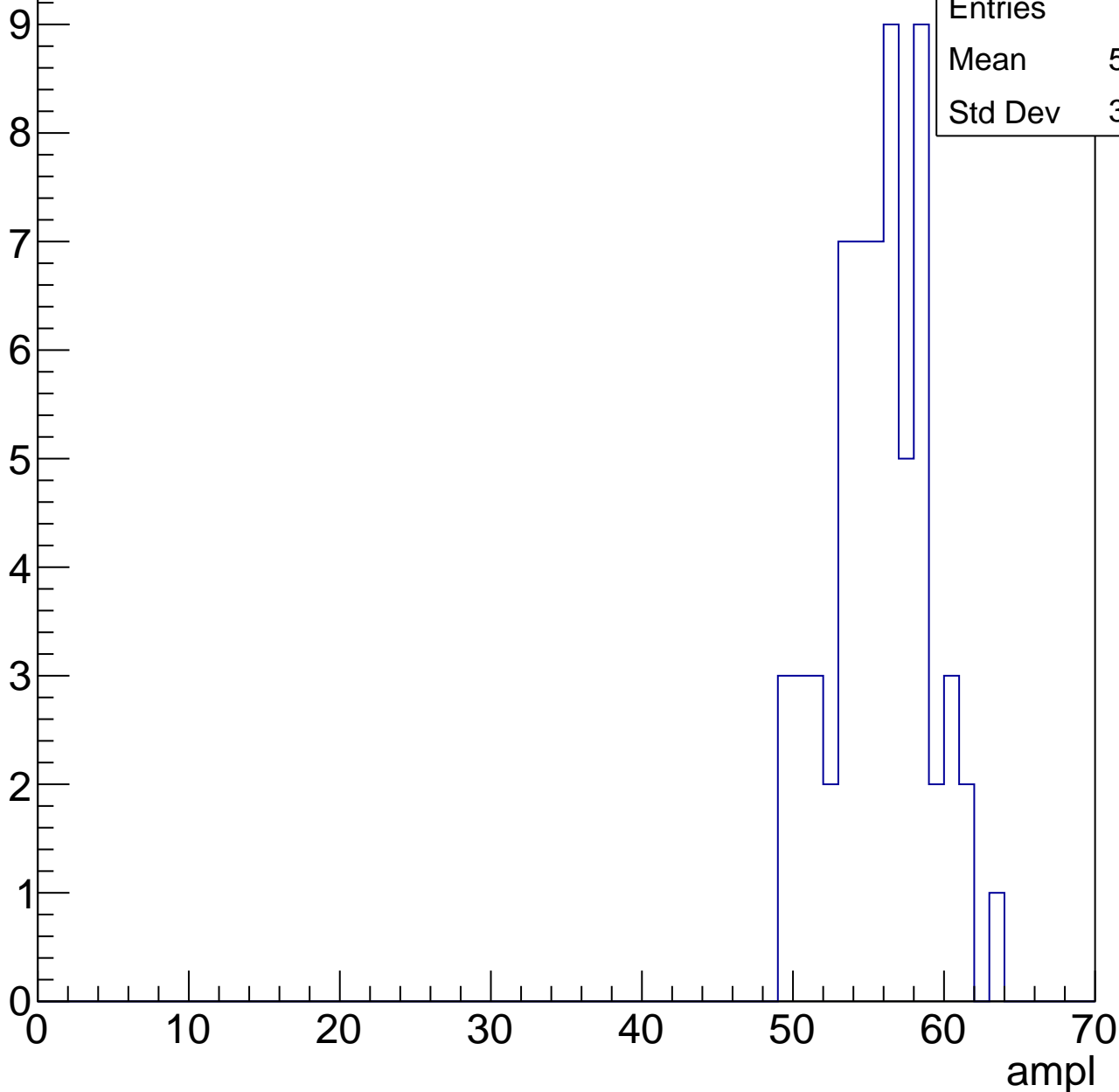


# B0L102S, U6-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.27
Std Dev	3.168



# B0L102S, U6-ch10, adc5

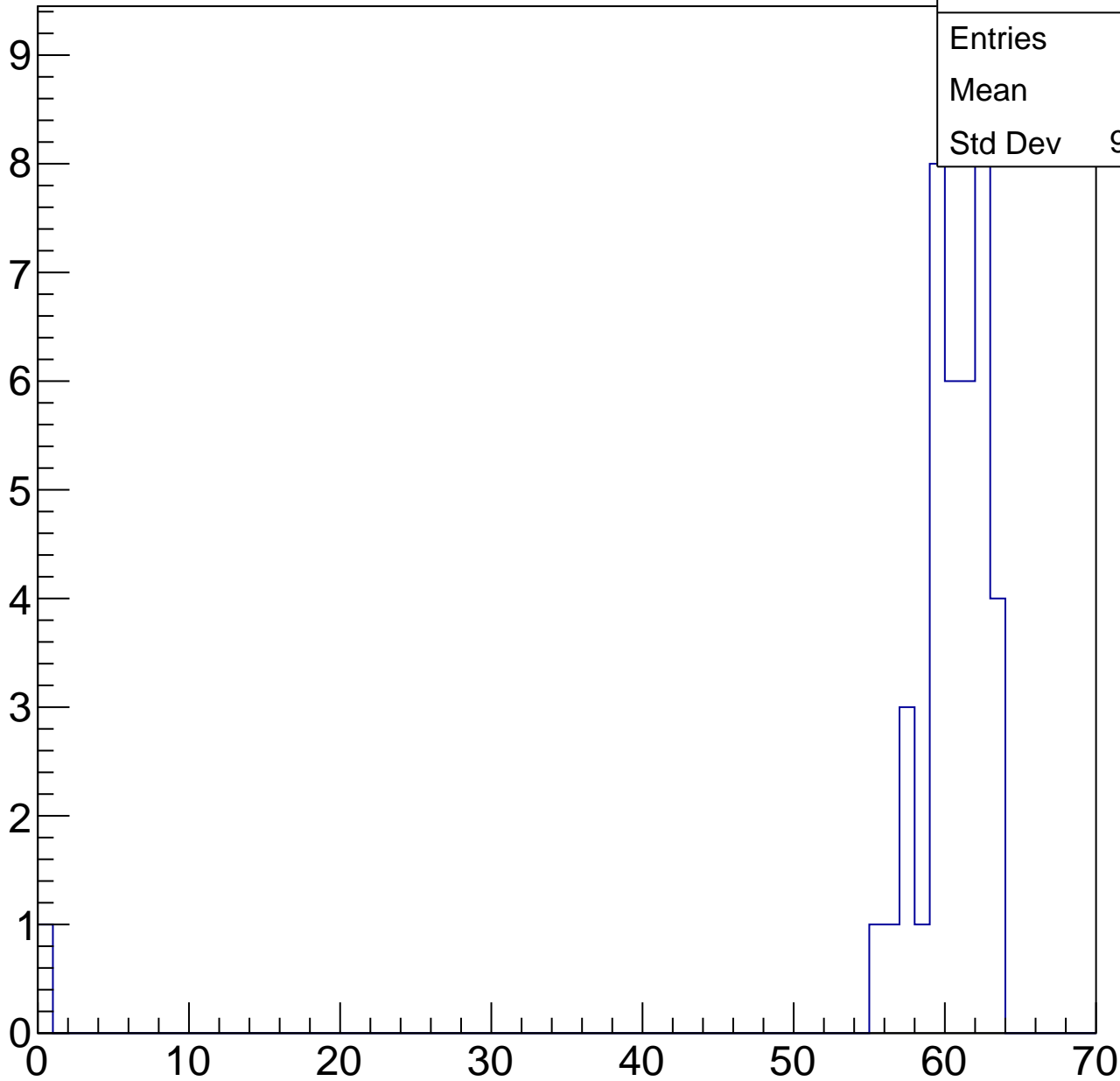
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	58.7
Std Dev	9.605

ampl

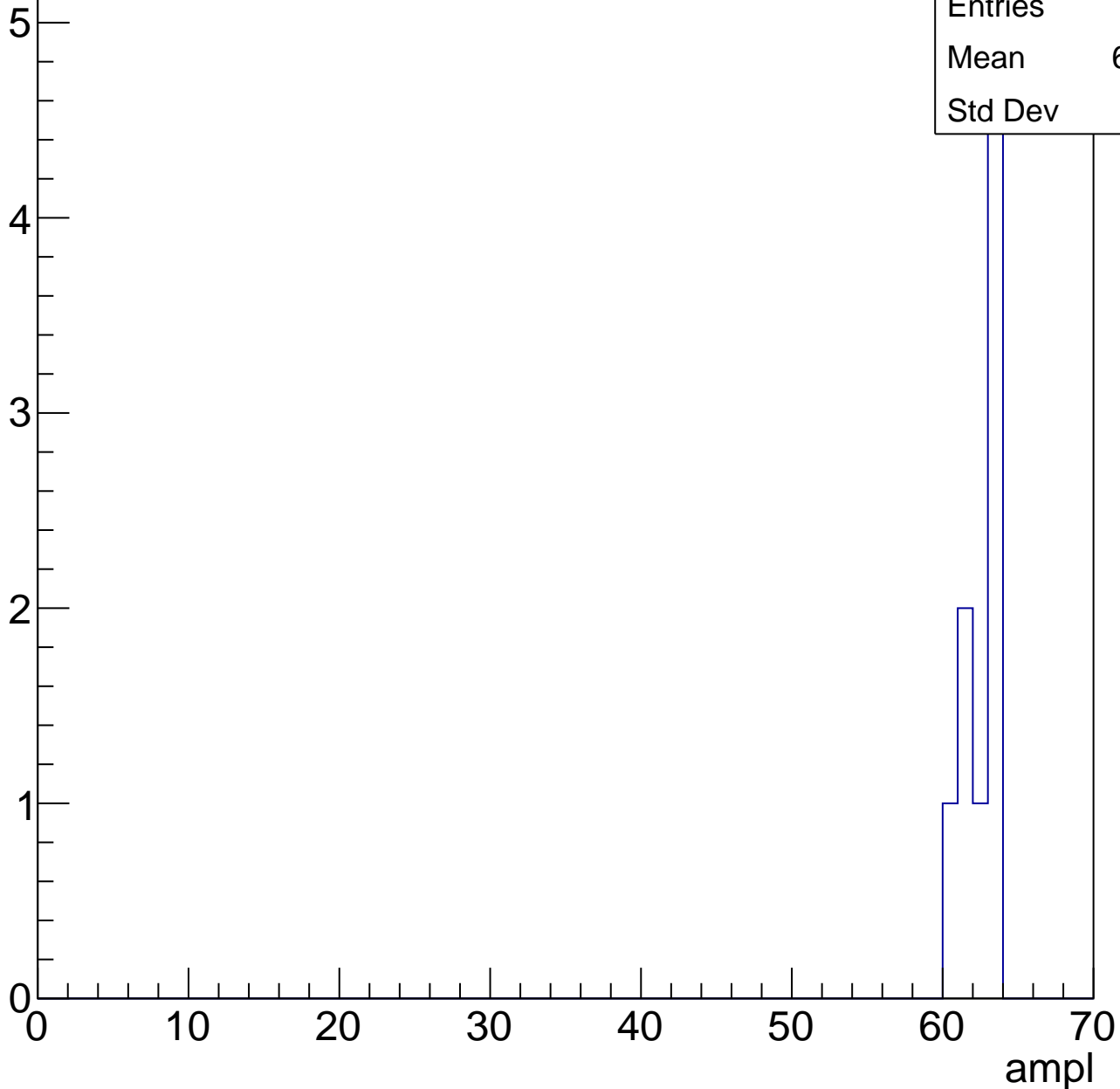


# B0L102S, U6-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	9
Mean	62.11
Std Dev	1.1





# B0L102S, U6-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch11, adc0

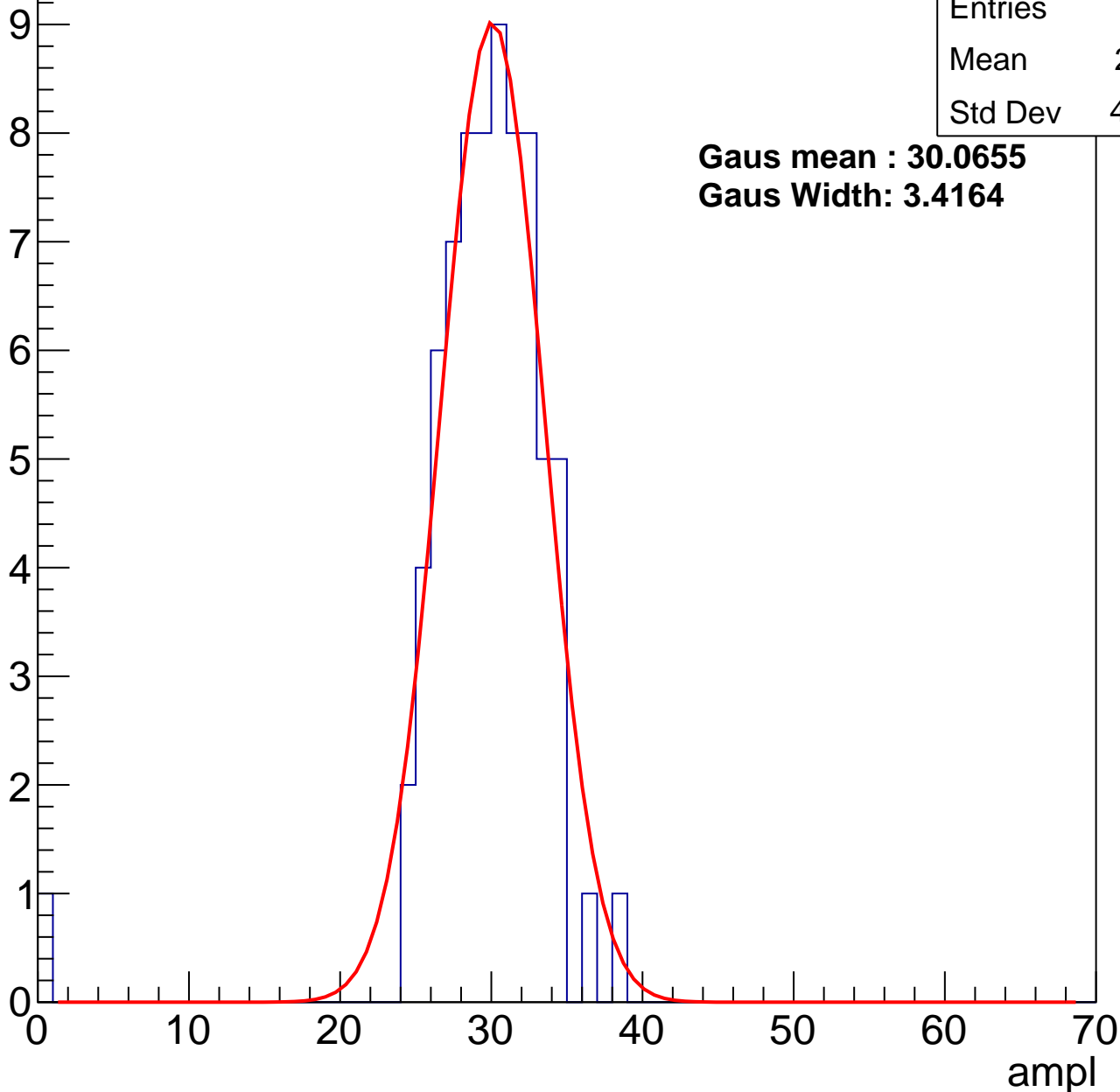
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	29.21
Std Dev	4.518

**Gaus mean : 30.0655**

**Gaus Width: 3.4164**



# B0L102S, U6-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	72
Mean	36.61
Std Dev	3.195

**Gaus mean : 37.0831**

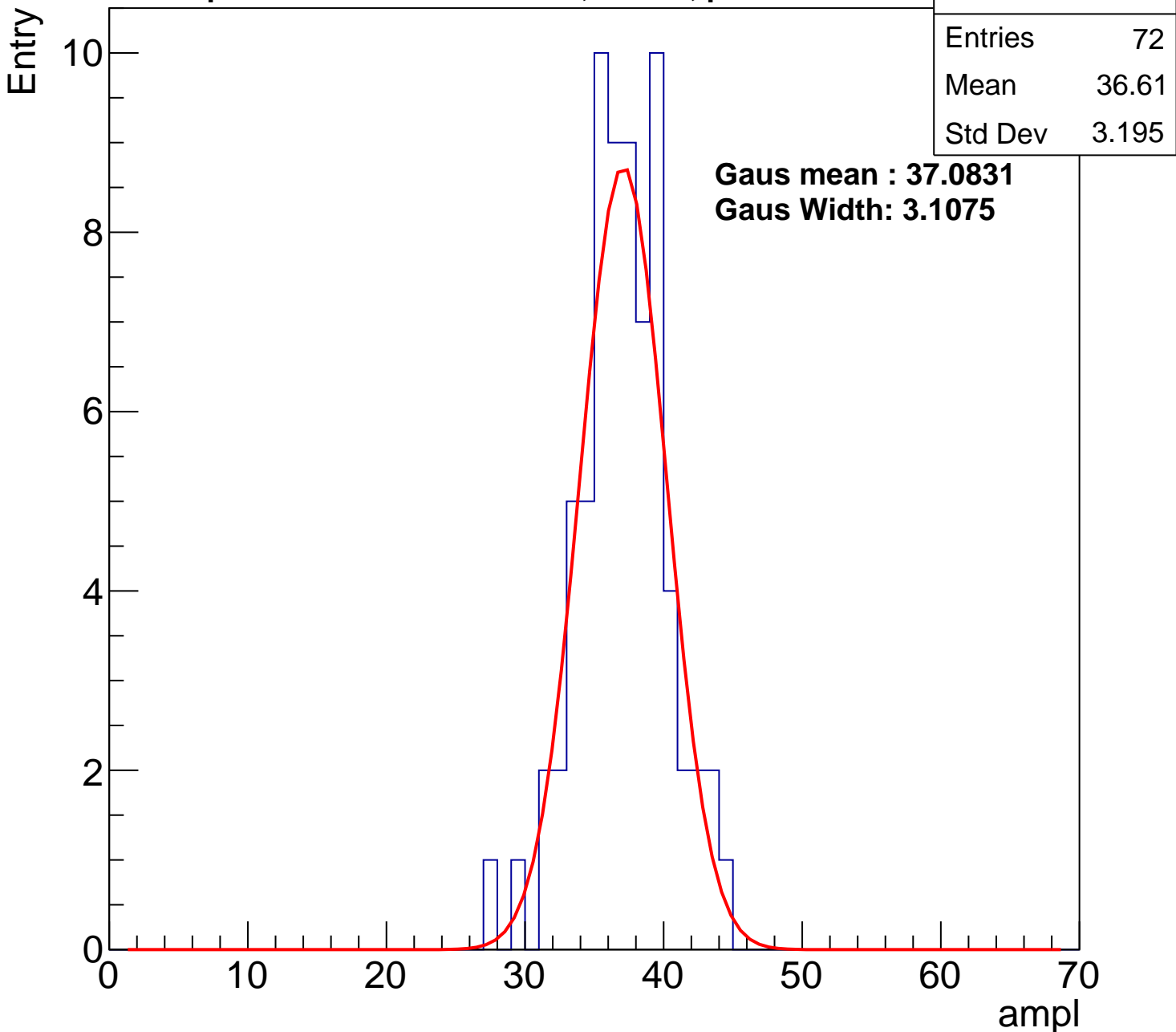
**Gaus Width: 3.1075**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch11, adc2

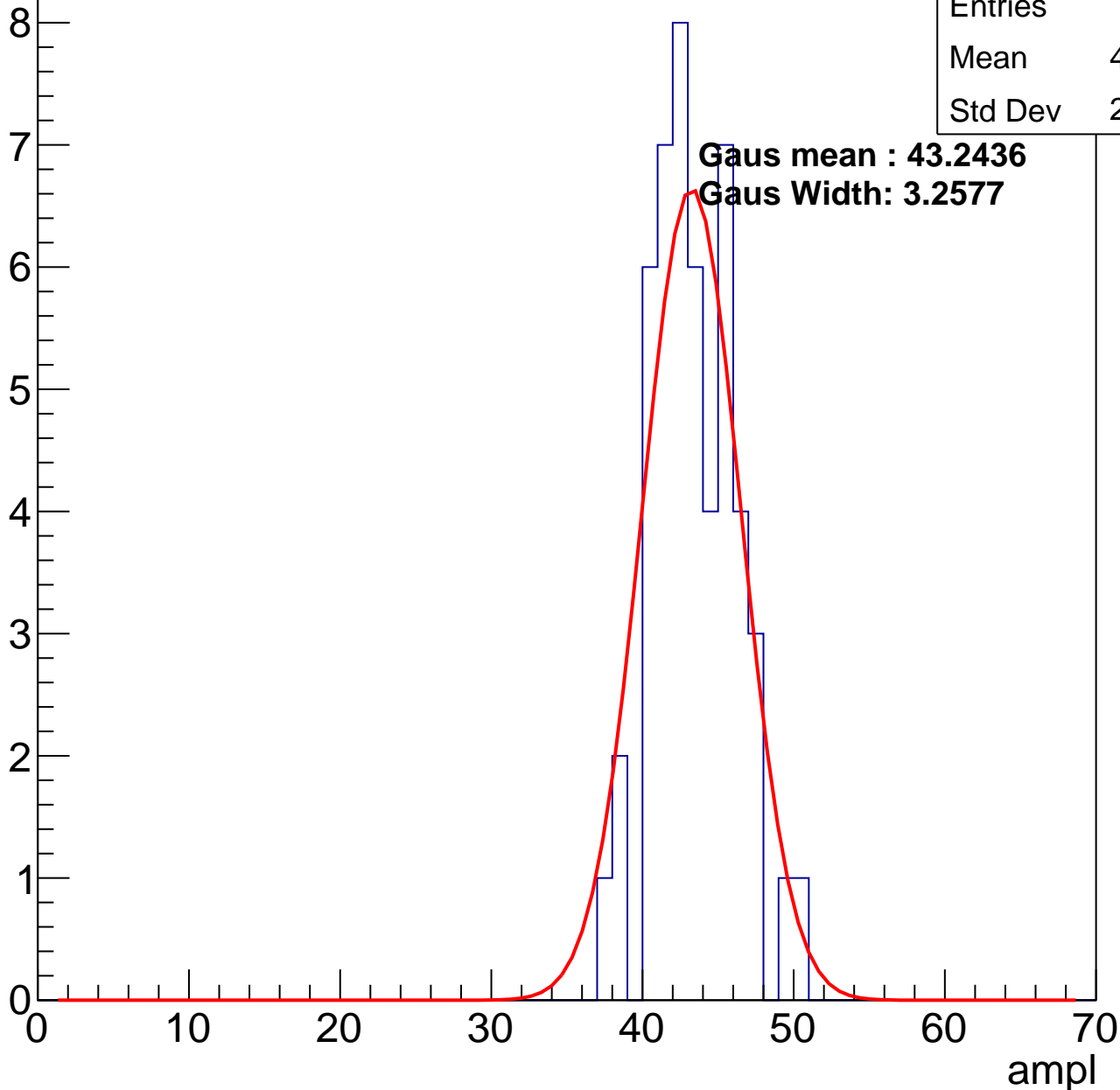
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	42.98
Std Dev	2.746

**Gaus mean : 43.2436**

**Gaus Width: 3.2577**

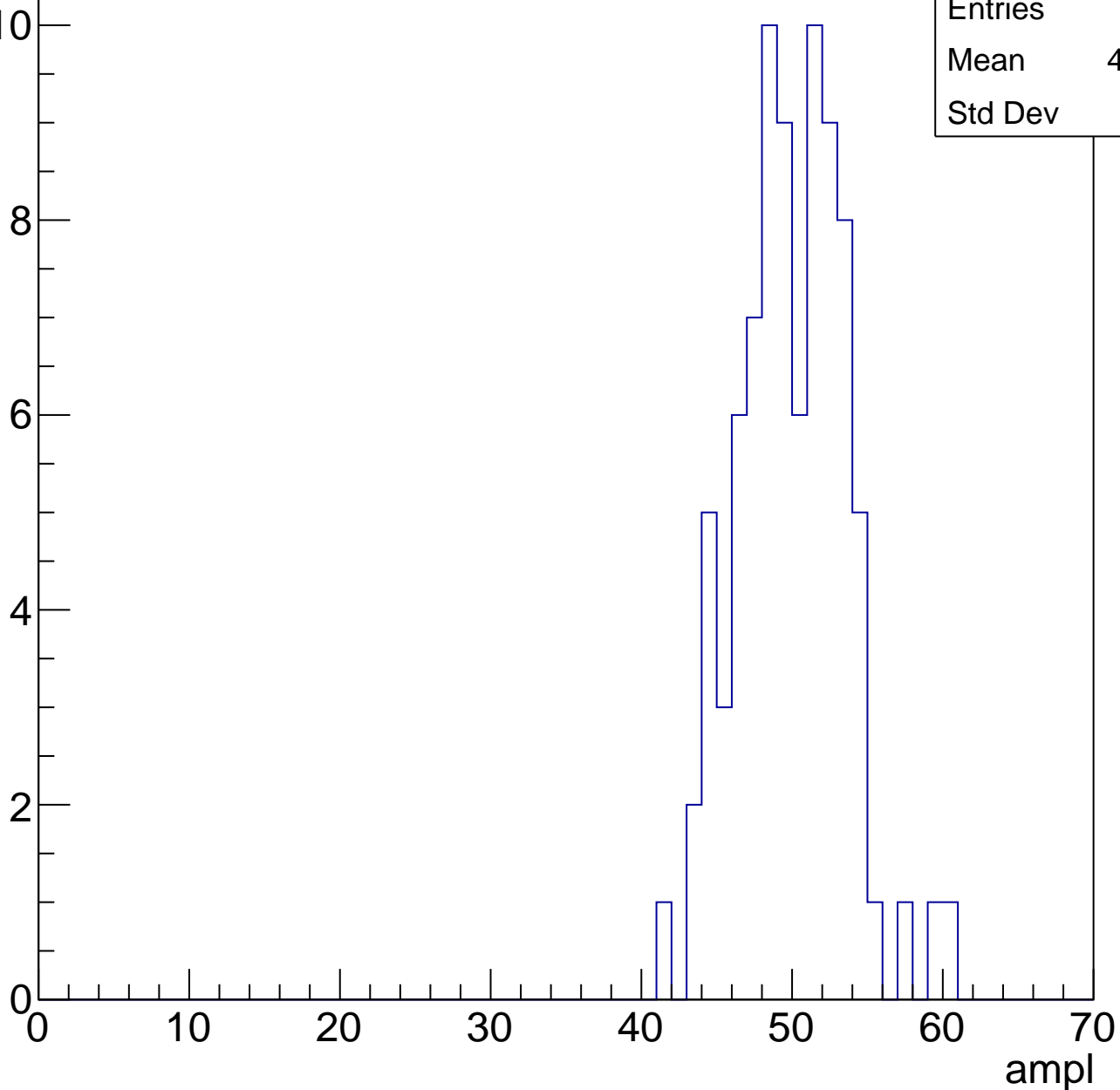


# B0L102S, U6-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	49.54
Std Dev	3.56

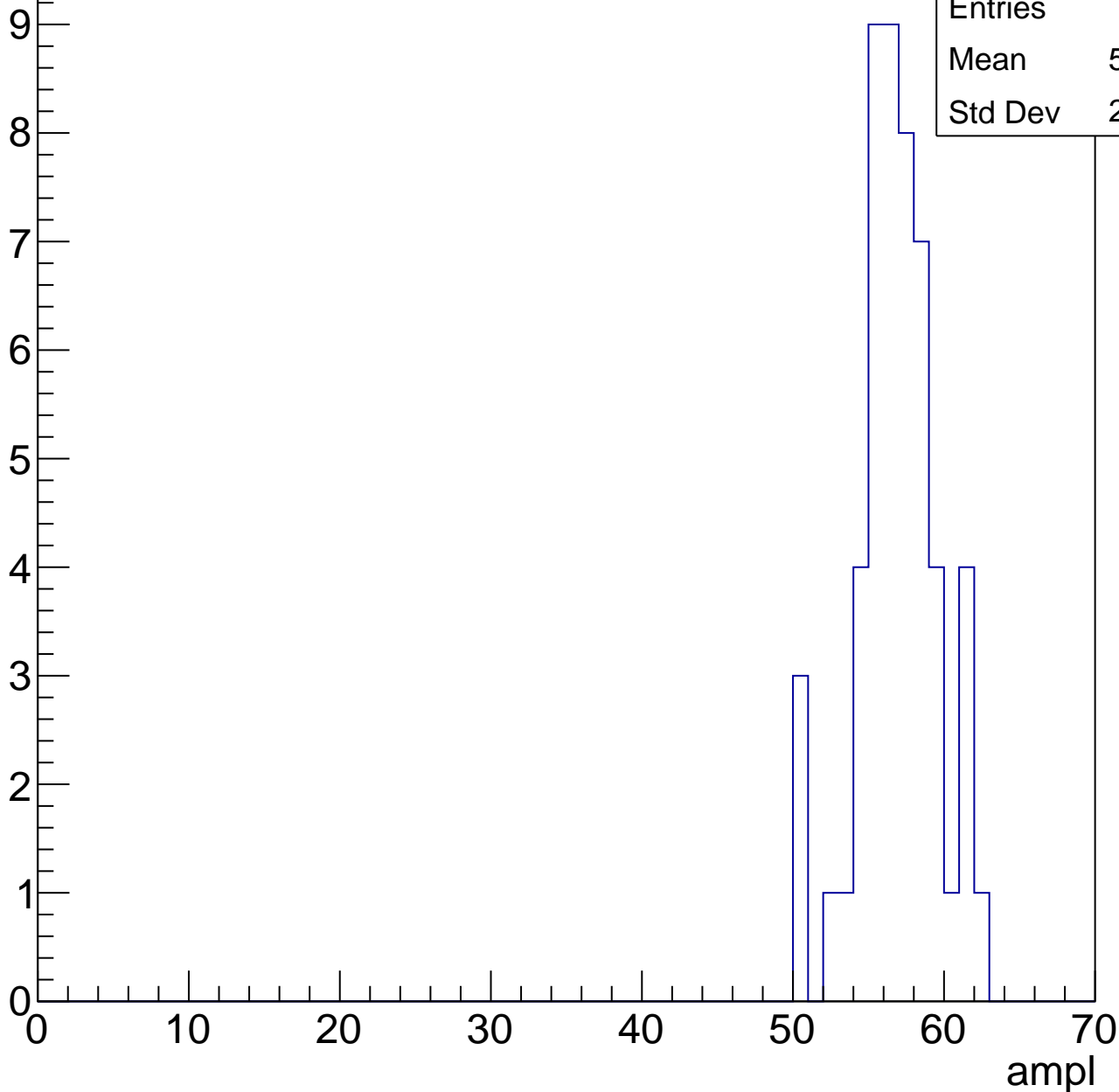


# B0L102S, U6-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	56.42
Std Dev	2.677

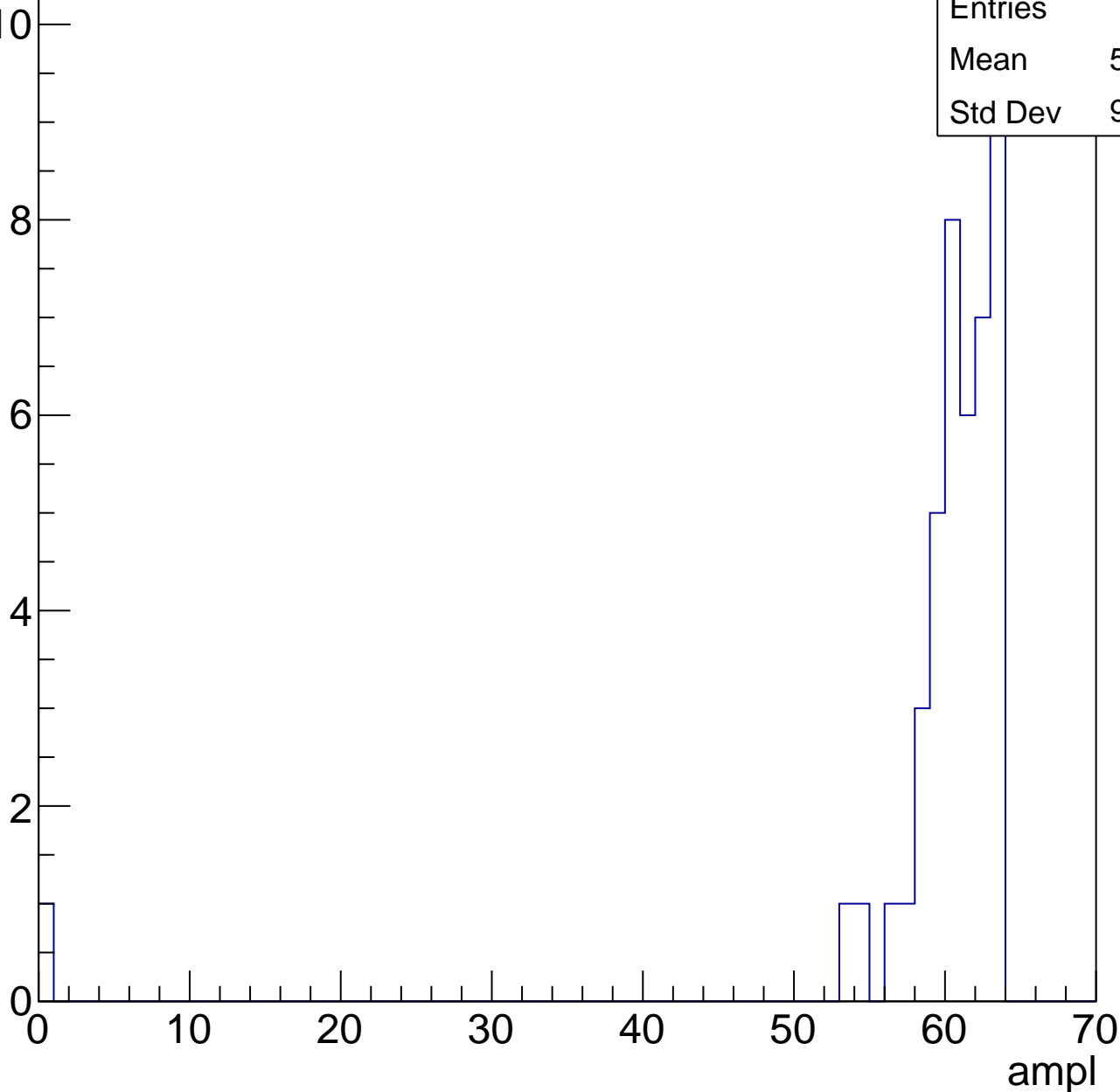


# B0L102S, U6-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	59.07
Std Dev	9.309



# B0L102S, U6-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B0L102S, U6-ch12, adc0

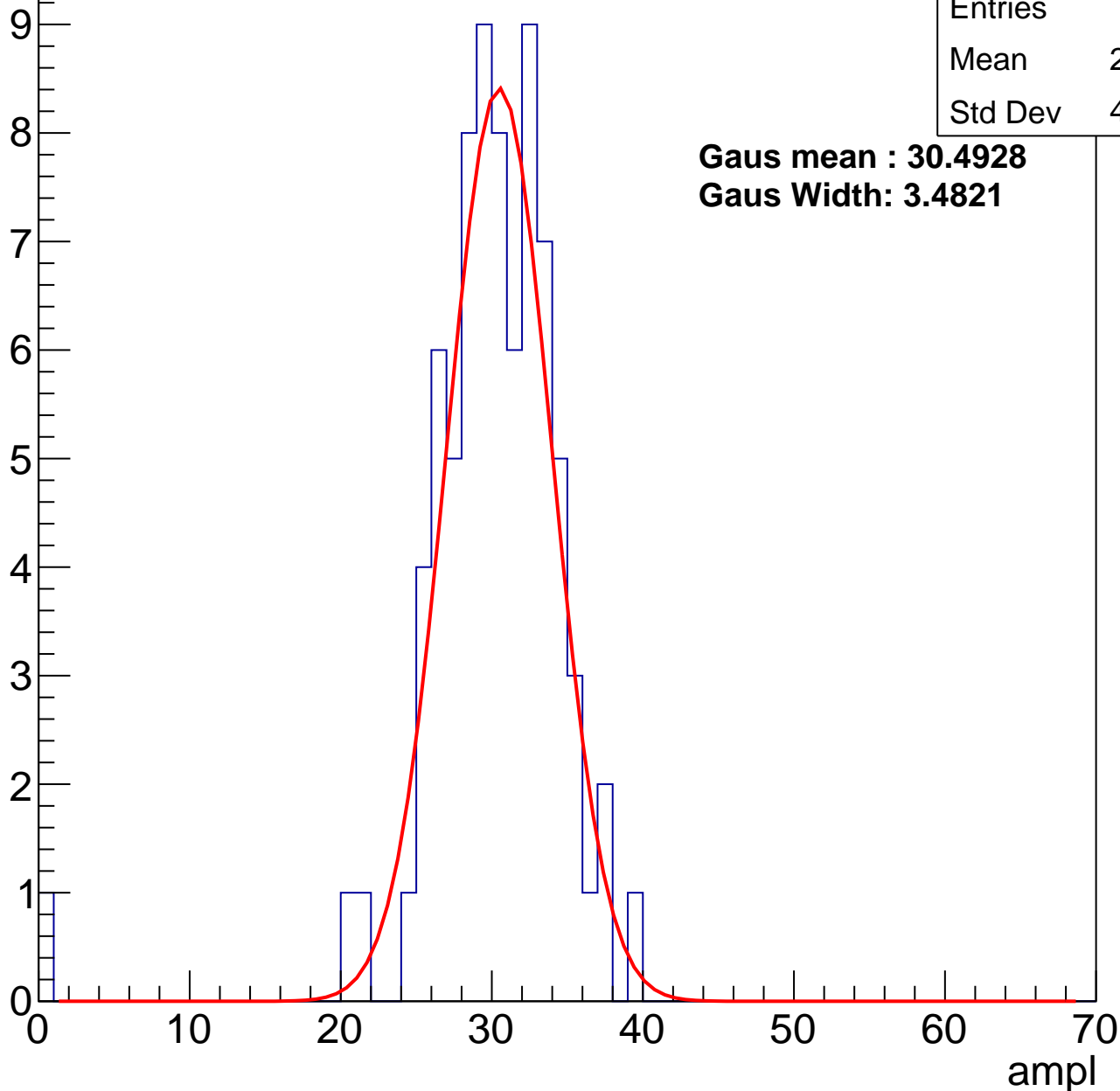
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	29.62
Std Dev	4.886

**Gaus mean : 30.4928**

**Gaus Width: 3.4821**



# B0L102S, U6-ch12, adc1

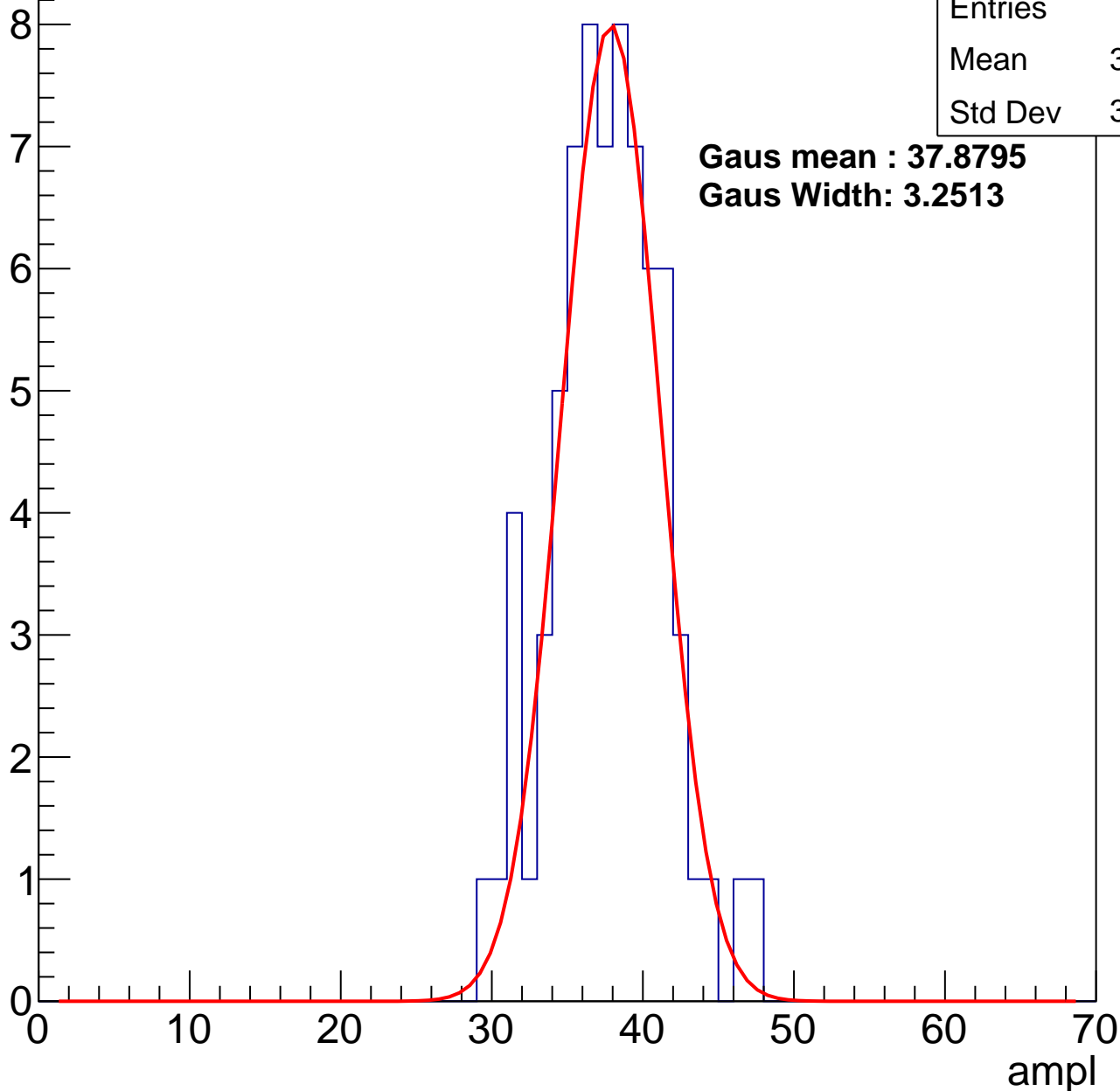
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	37.25
Std Dev	3.626

**Gaus mean : 37.8795**

**Gaus Width: 3.2513**



# B0L102S, U6-ch12, adc2

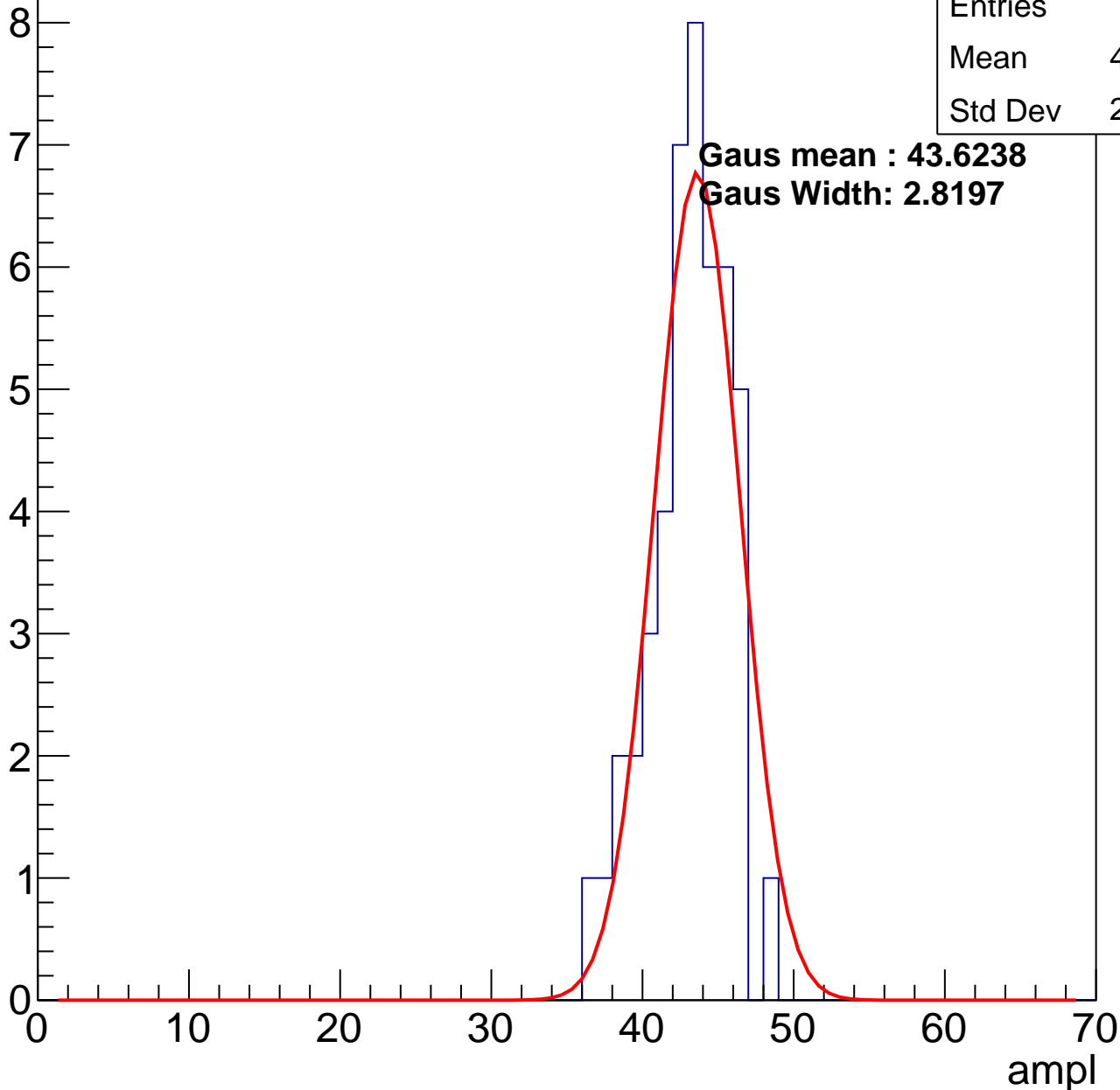
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	42.63
Std Dev	2.599

**Gaus mean : 43.6238**

**Gaus Width: 2.8197**



# B0L102S, U6-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

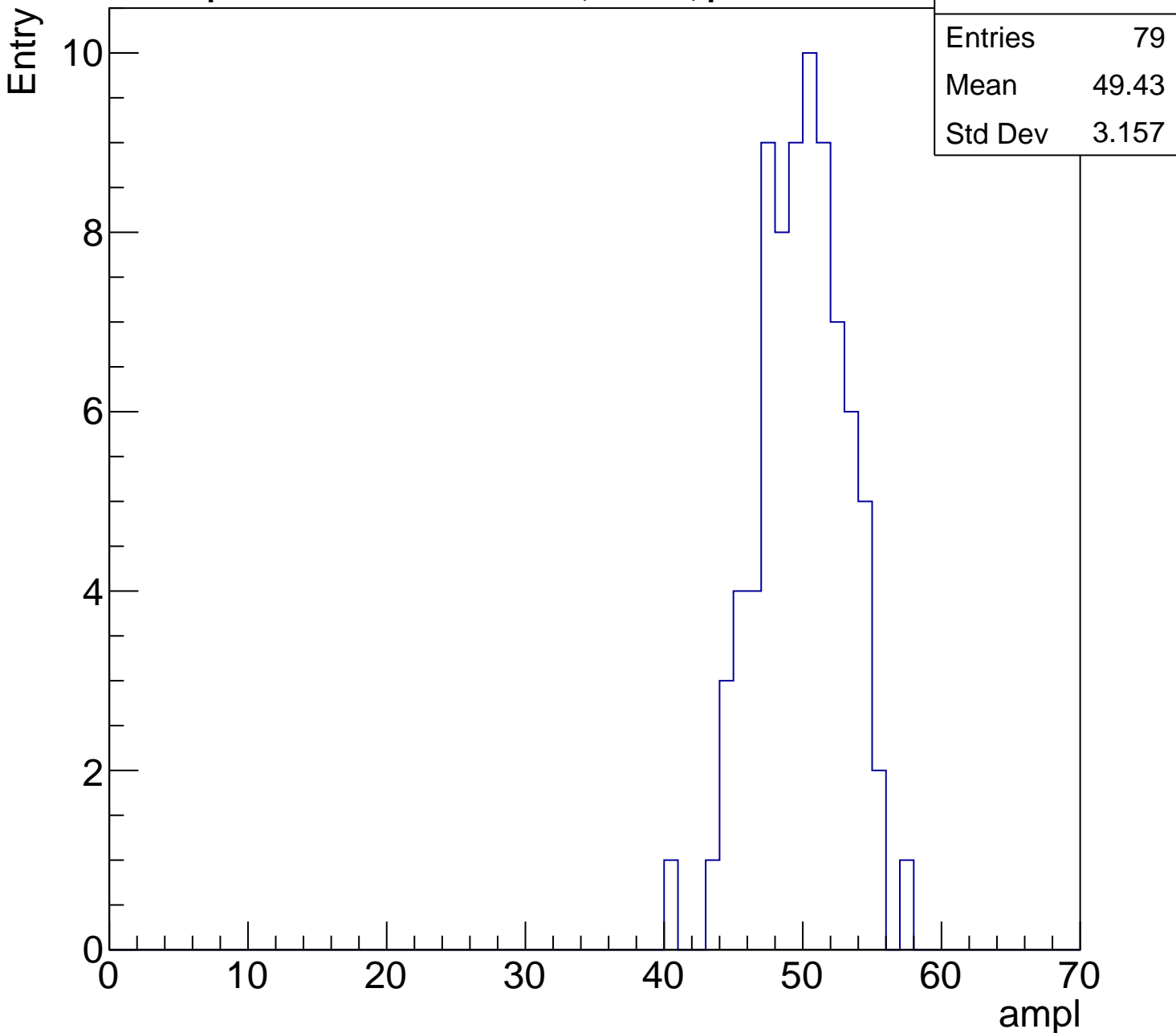
Entries	79
Mean	49.43
Std Dev	3.157

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

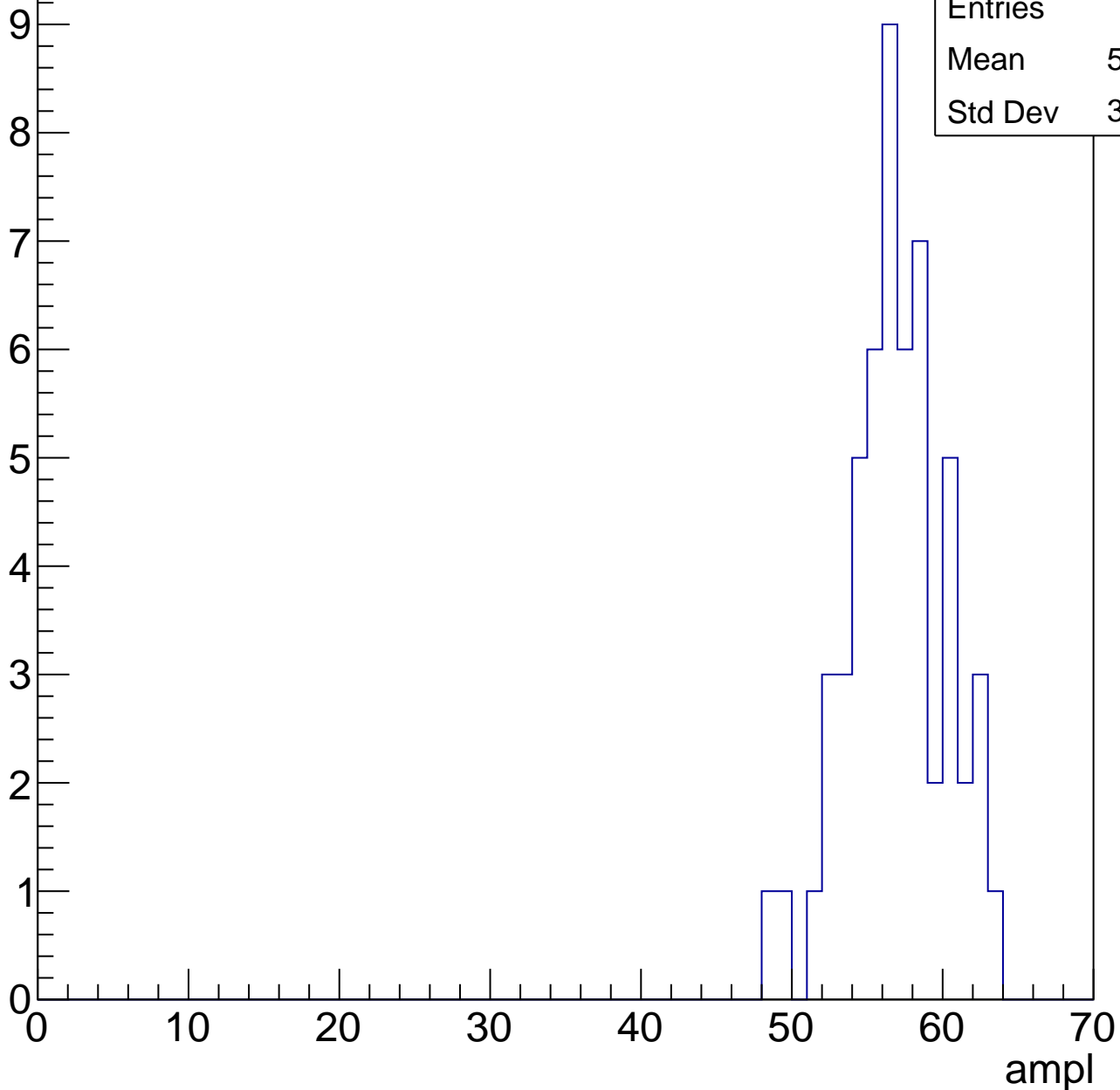


# B0L102S, U6-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

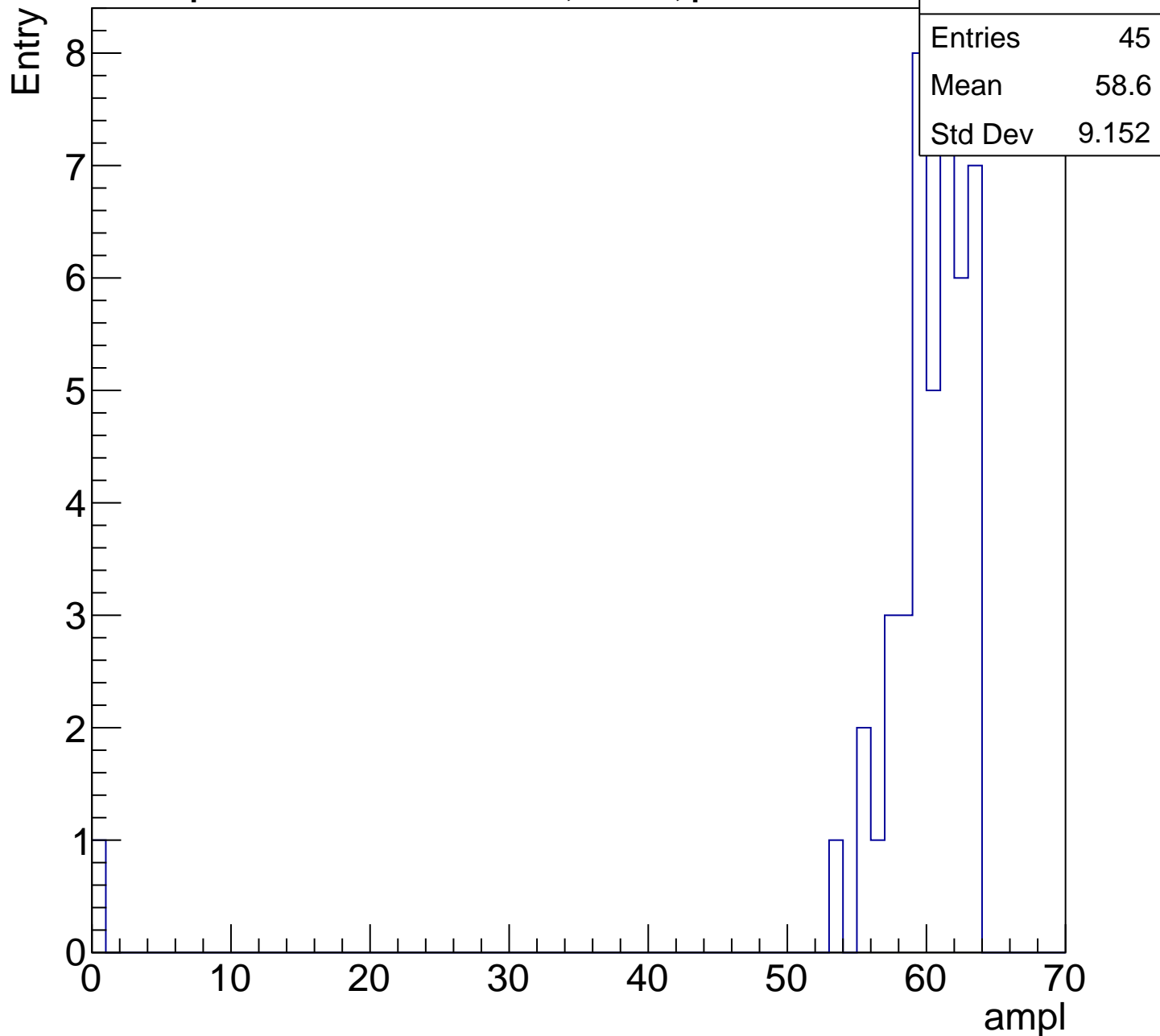
Entry

Entries	55
Mean	56.44
Std Dev	3.212



# B0L102S, U6-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch13, adc0

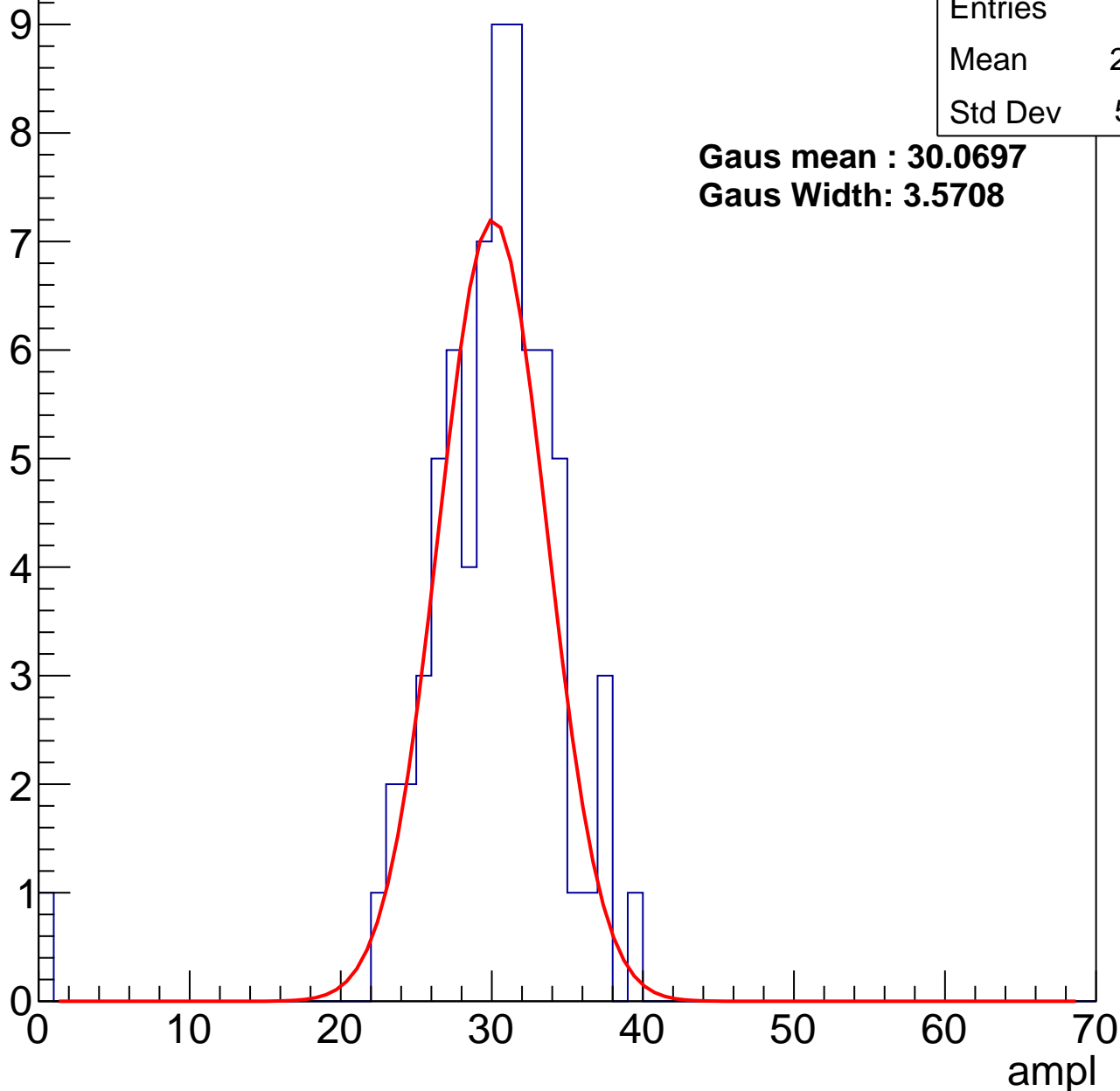
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	29.56
Std Dev	5.011

**Gaus mean : 30.0697**

**Gaus Width: 3.5708**



# B0L102S, U6-ch13, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	74
Mean	37.45
Std Dev	3.301

**Gaus mean : 37.7758**

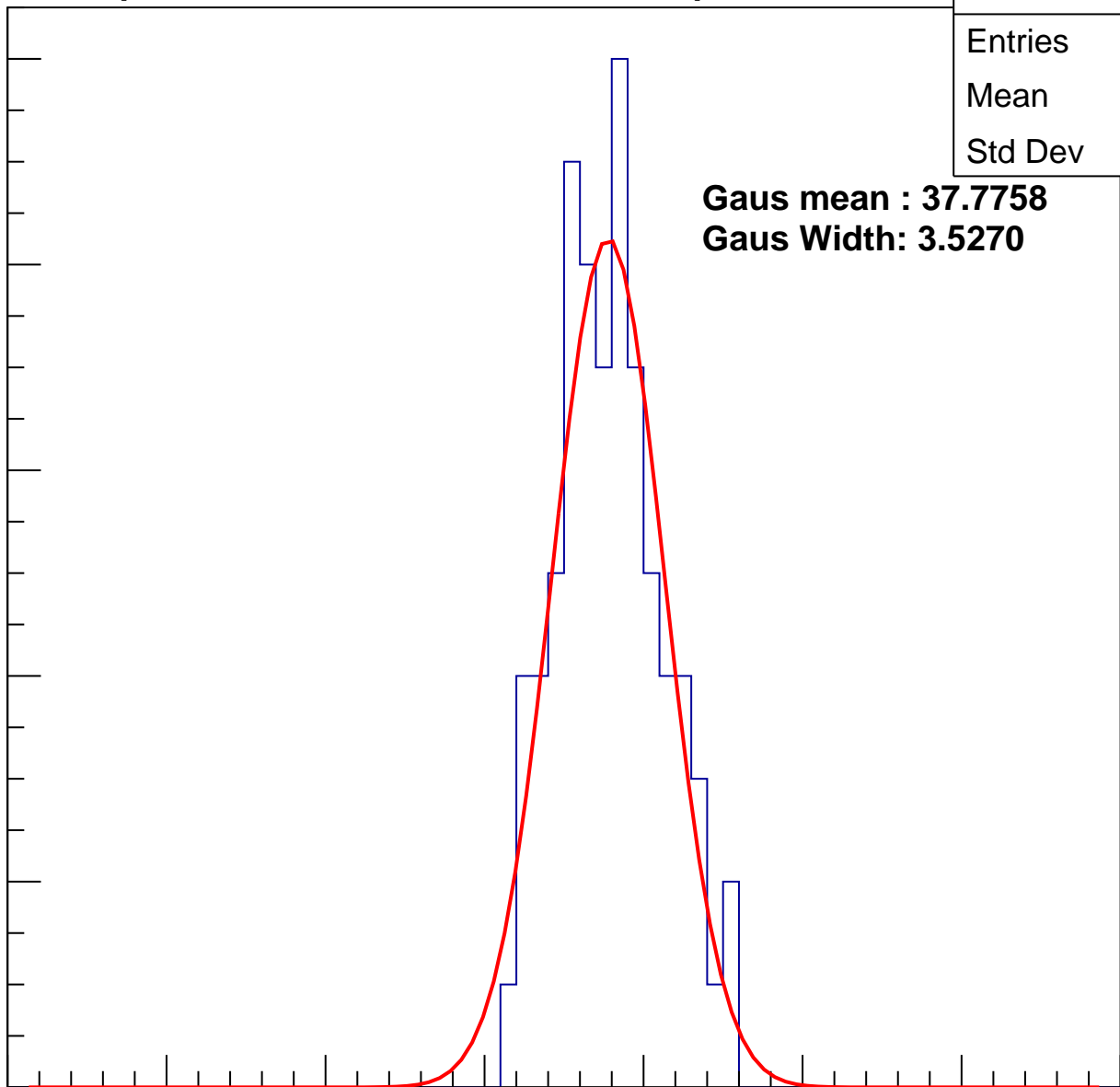
**Gaus Width: 3.5270**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U6-ch13, adc2

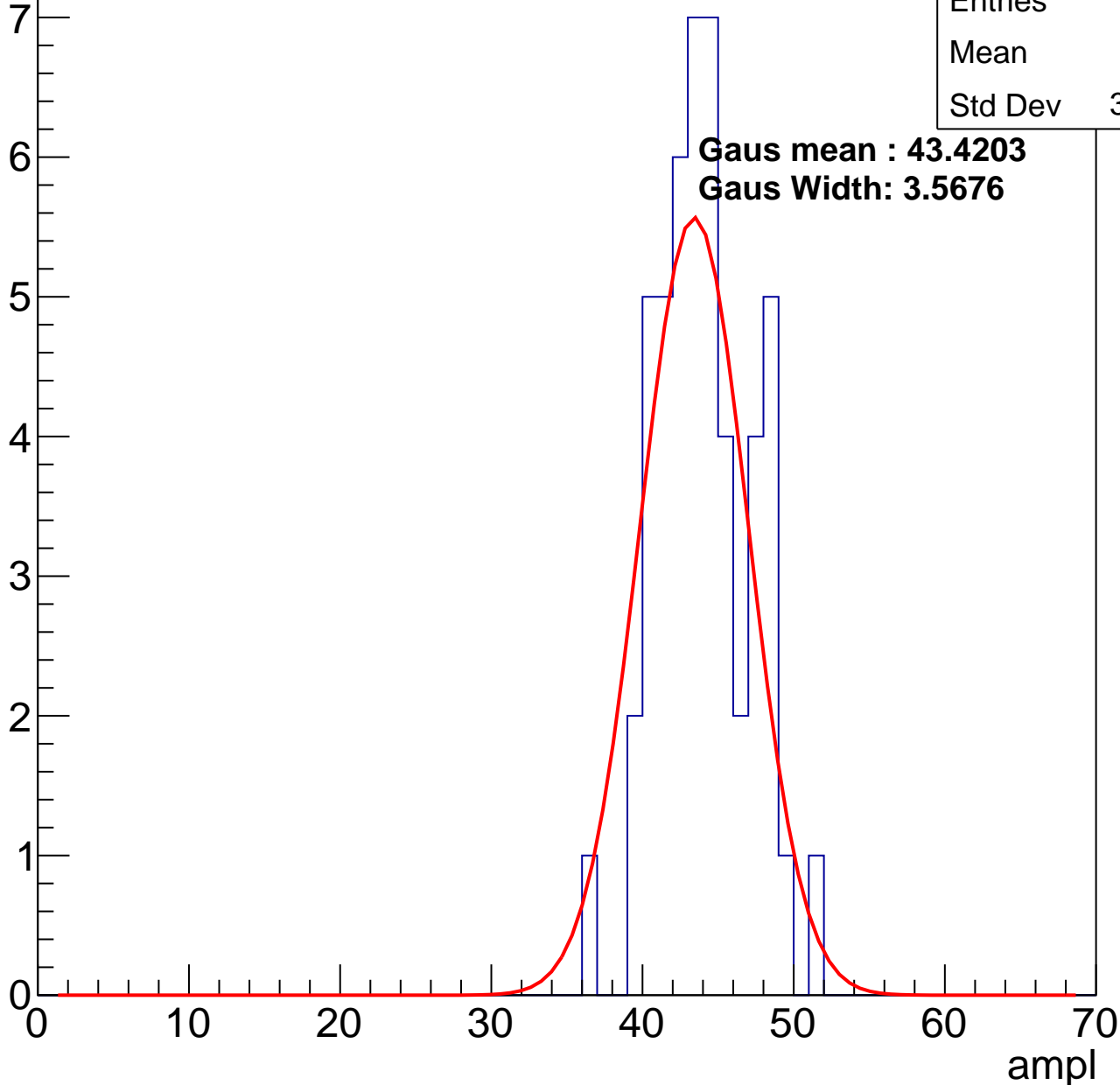
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	43.6
Std Dev	3.046

**Gaus mean : 43.4203**

**Gaus Width: 3.5676**

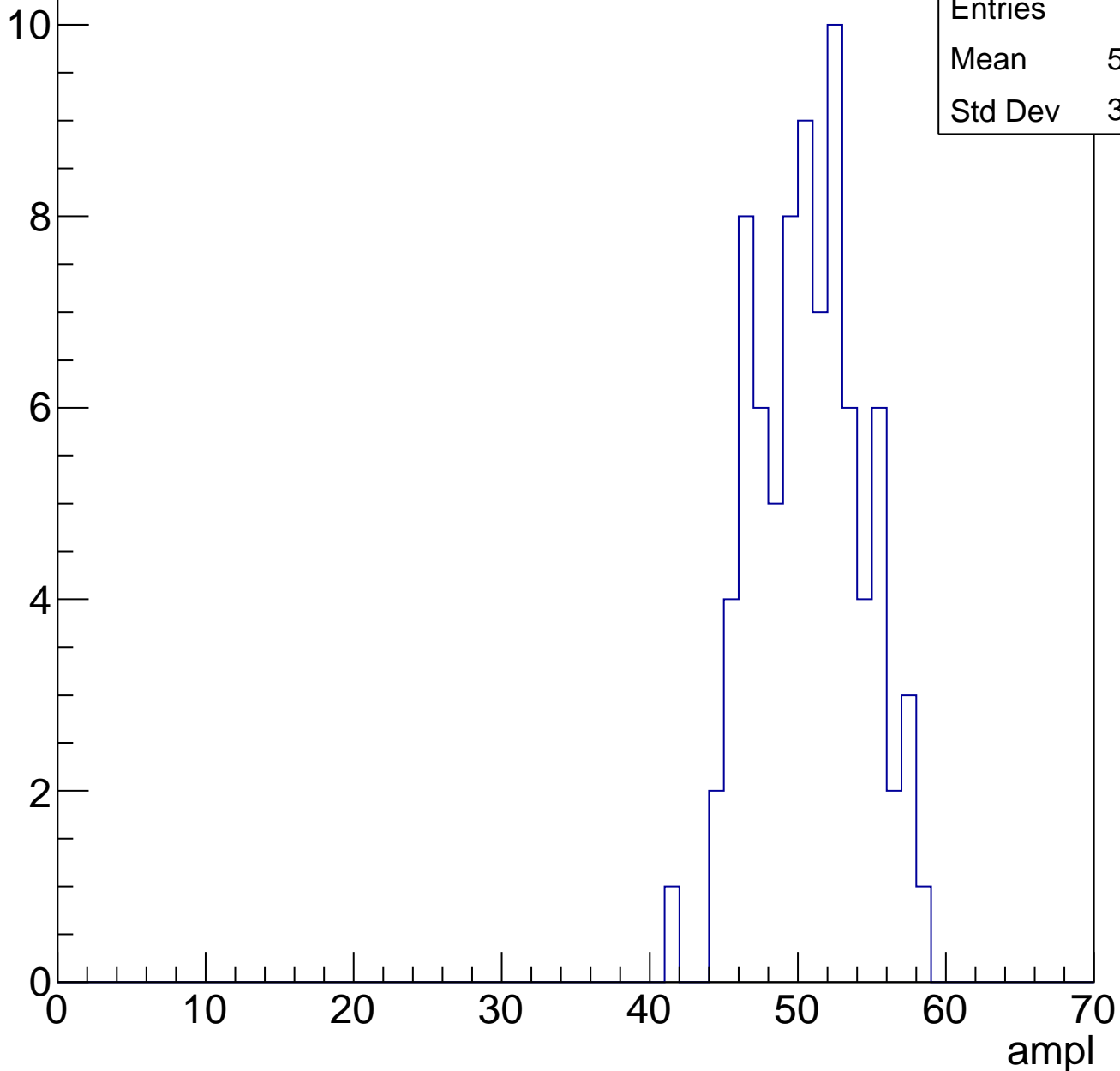


# B0L102S, U6-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	82
Mean	50.28
Std Dev	3.586

Entry

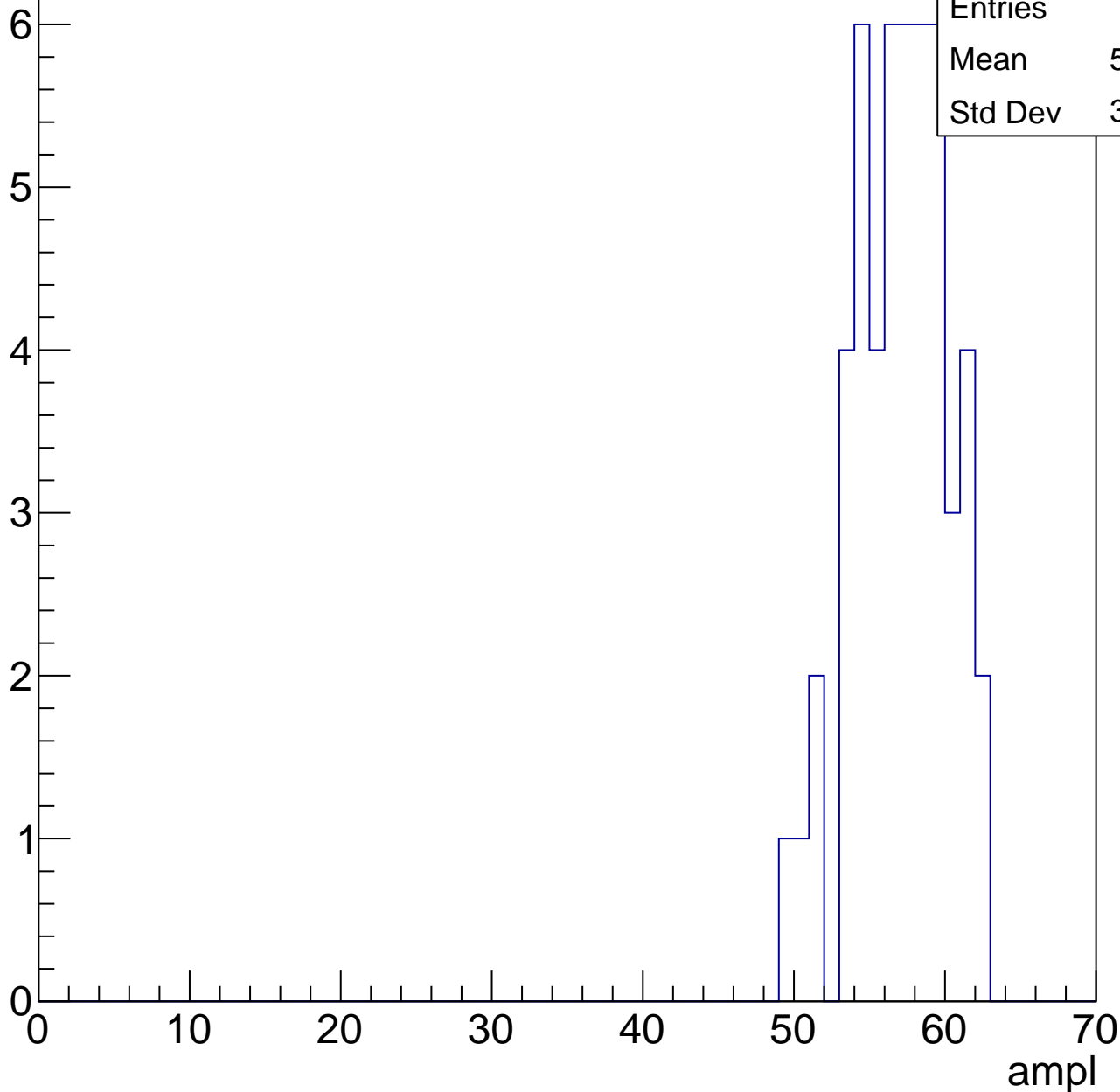


# B0L102S, U6-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

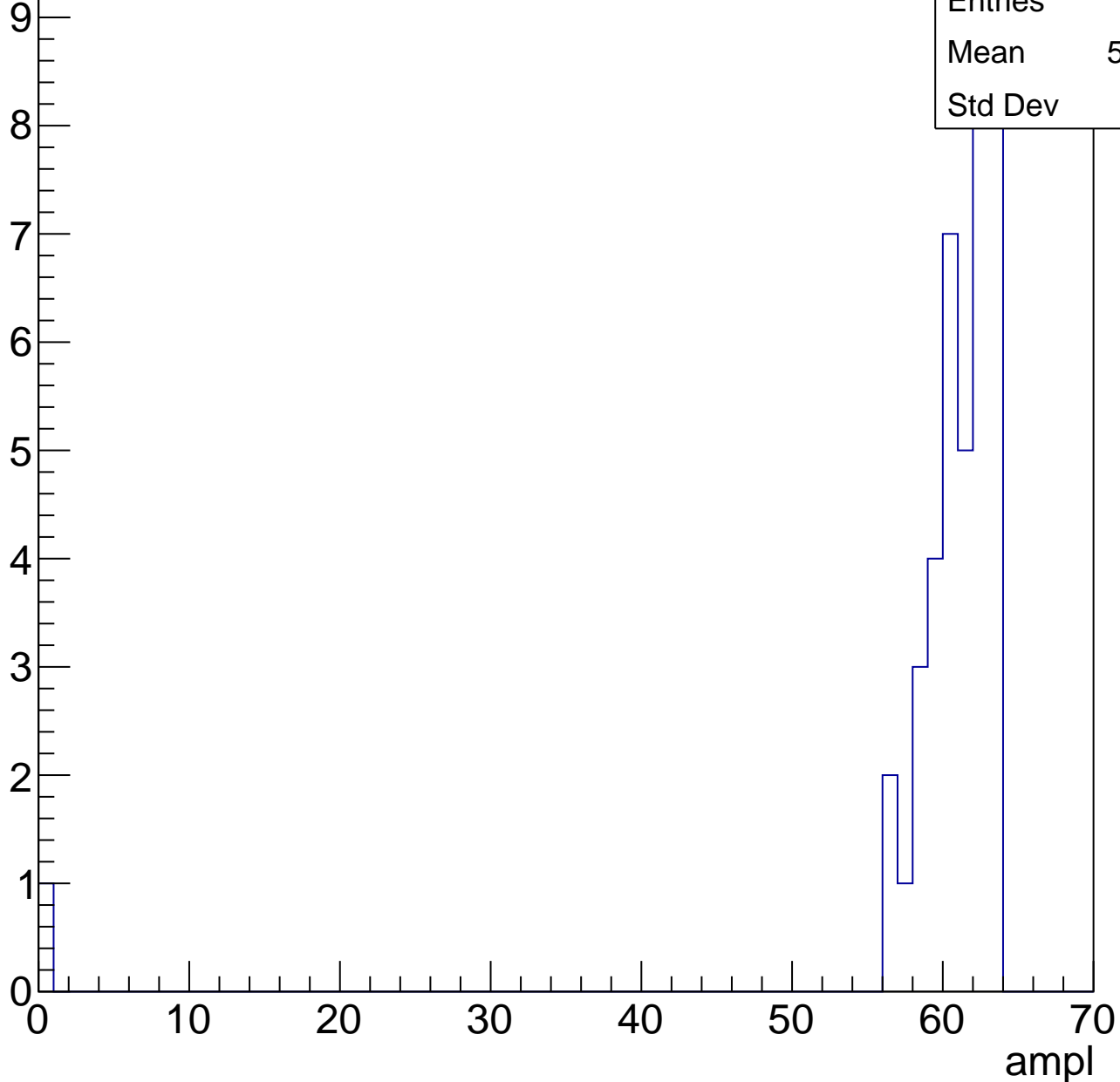
Entries	51
Mean	56.57
Std Dev	3.089



# B0L102S, U6-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch14, adc0

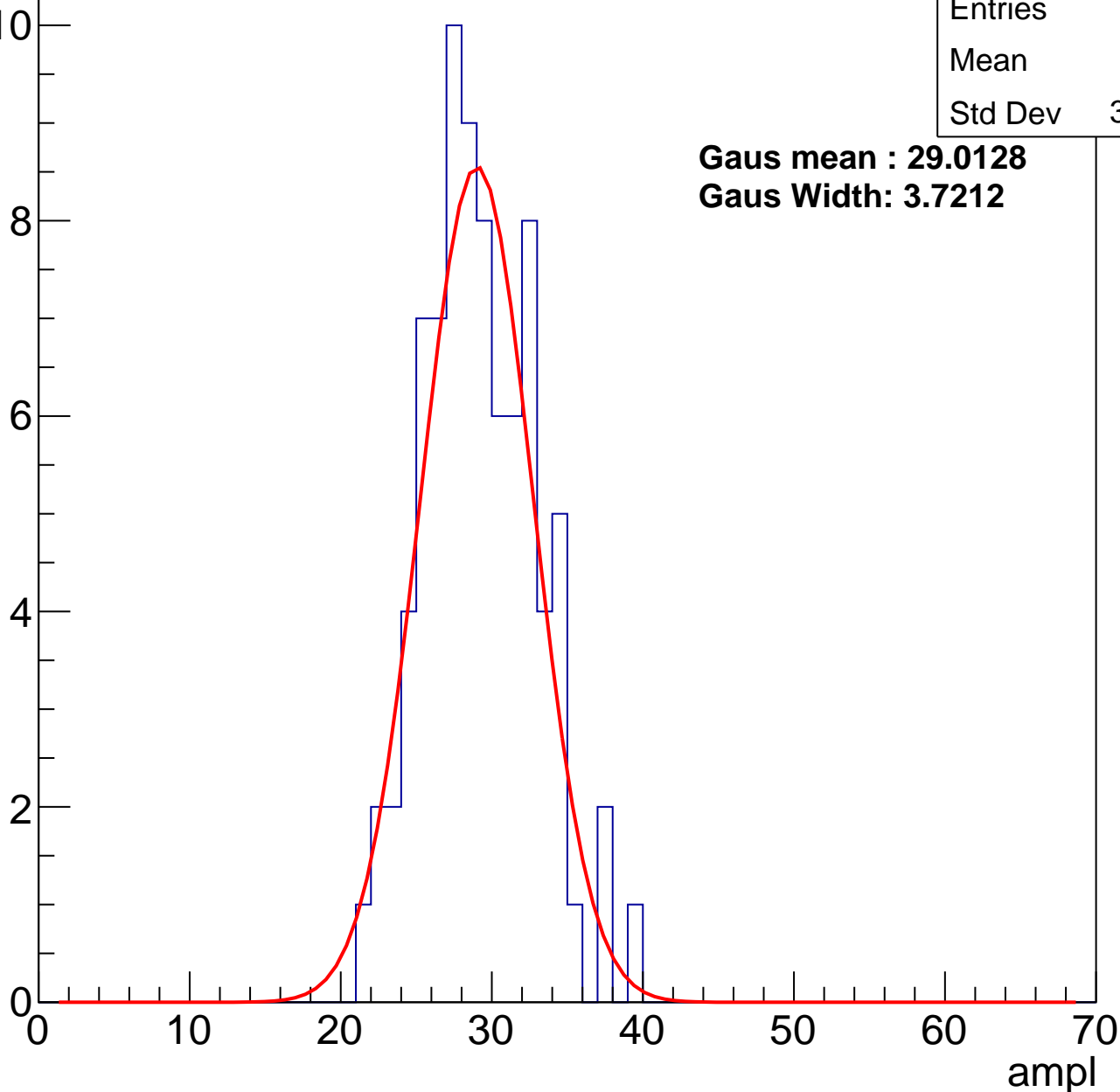
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	83
Mean	28.8
Std Dev	3.656

**Gaus mean : 29.0128**

**Gaus Width: 3.7212**



# B0L102S, U6-ch14, adc1

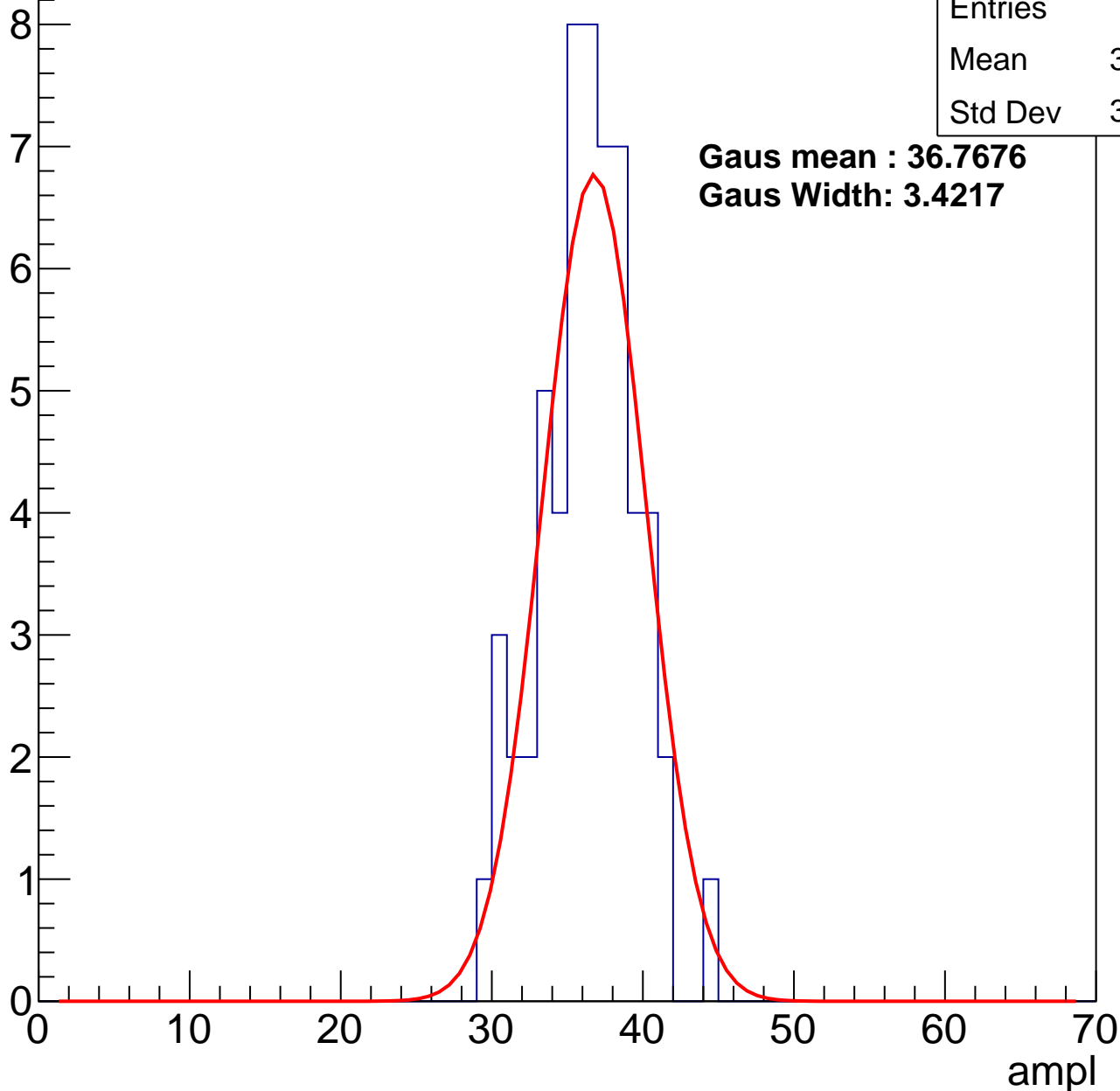
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	35.88
Std Dev	3.097

**Gaus mean : 36.7676**

**Gaus Width: 3.4217**



# B0L102S, U6-ch14, adc2

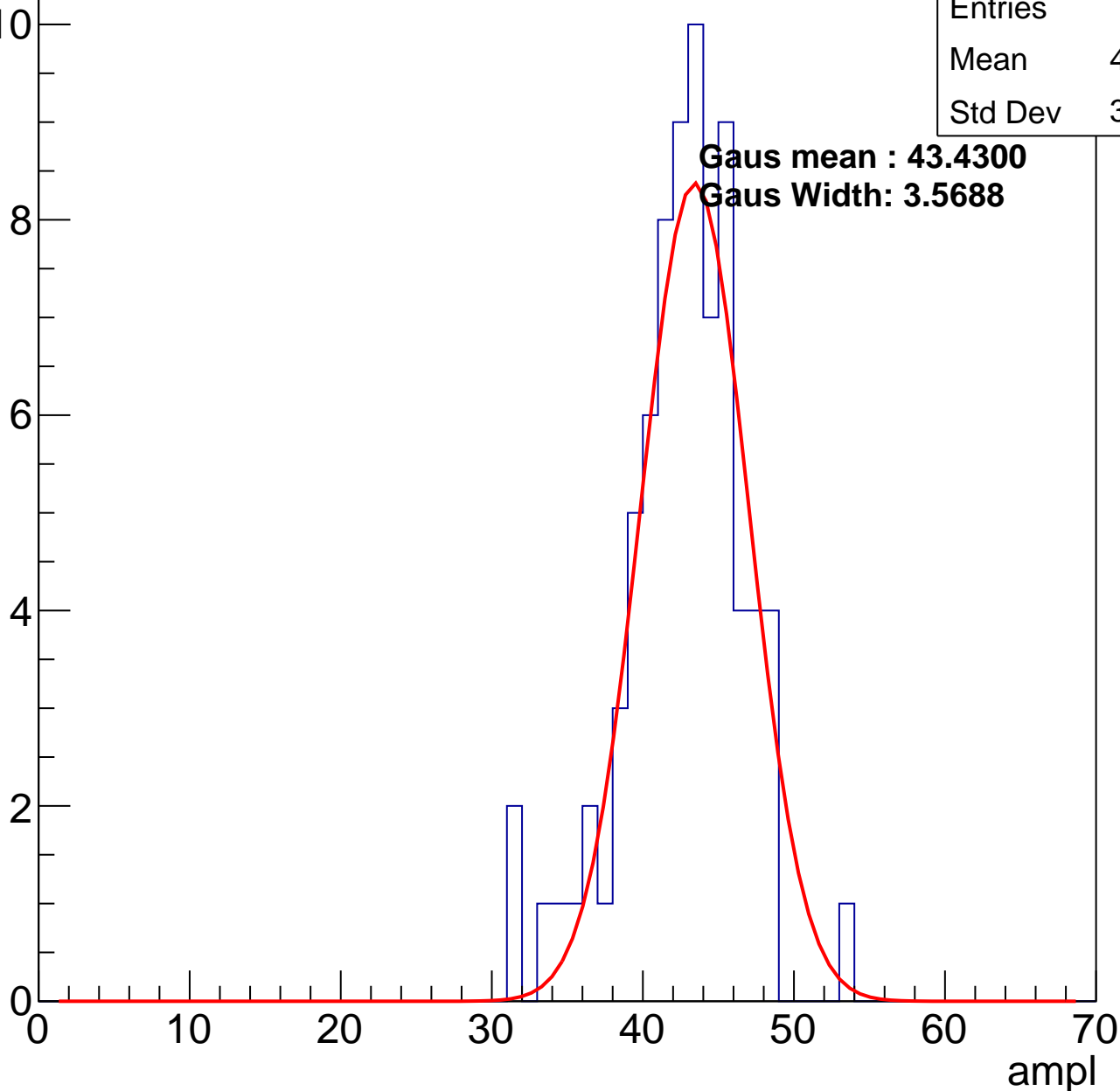
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	42.15
Std Dev	3.929

**Gaus mean : 43.4300**

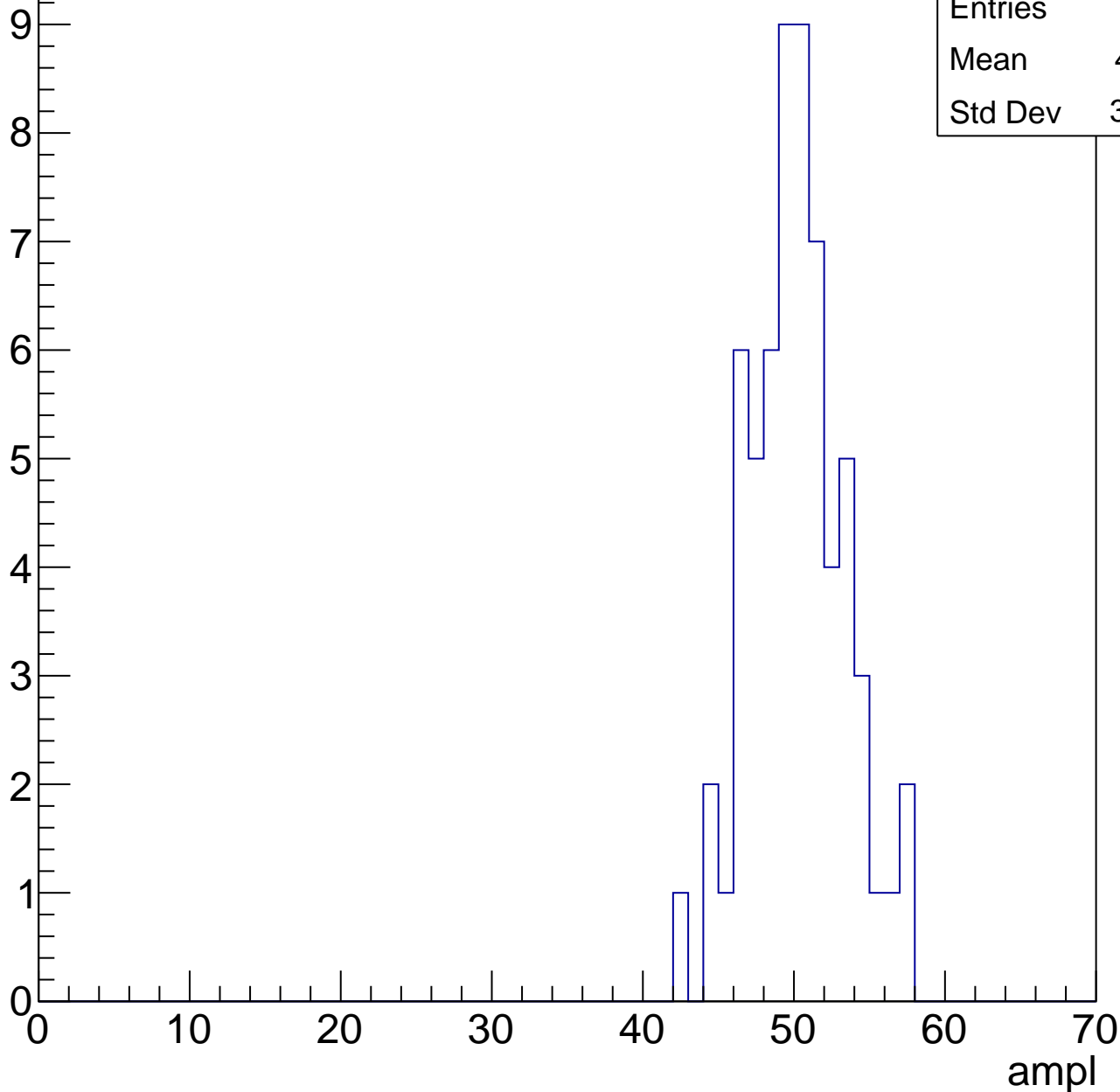
**Gaus Width: 3.5688**



# B0L102S, U6-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

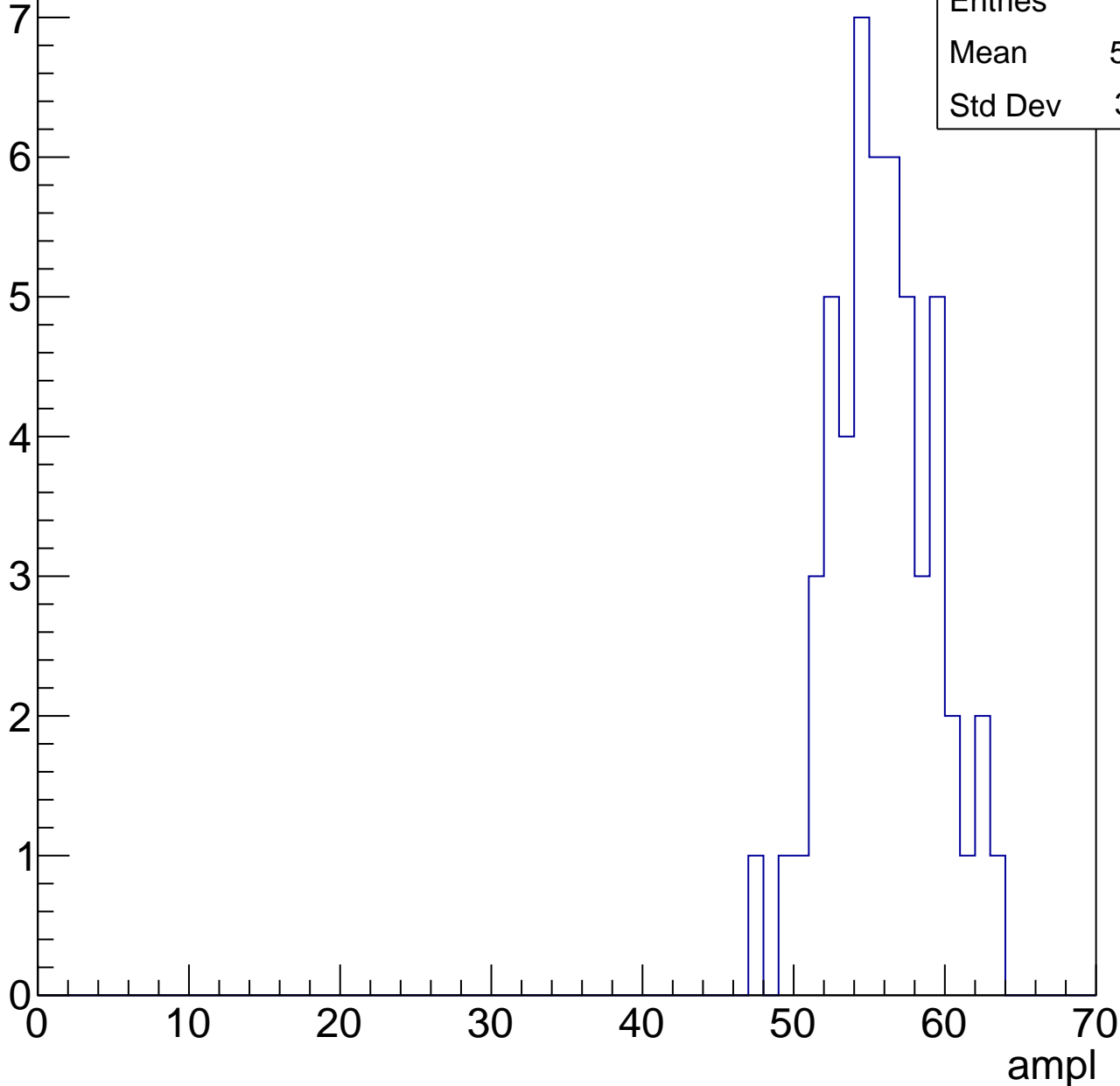


# B0L102S, U6-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	55.42
Std Dev	3.401



# B0L102S, U6-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	58.44
Std Dev	8.57

ampl

0 10 20 30 40 50 60 70

1

3

5

7

8

# B0L102S, U6-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

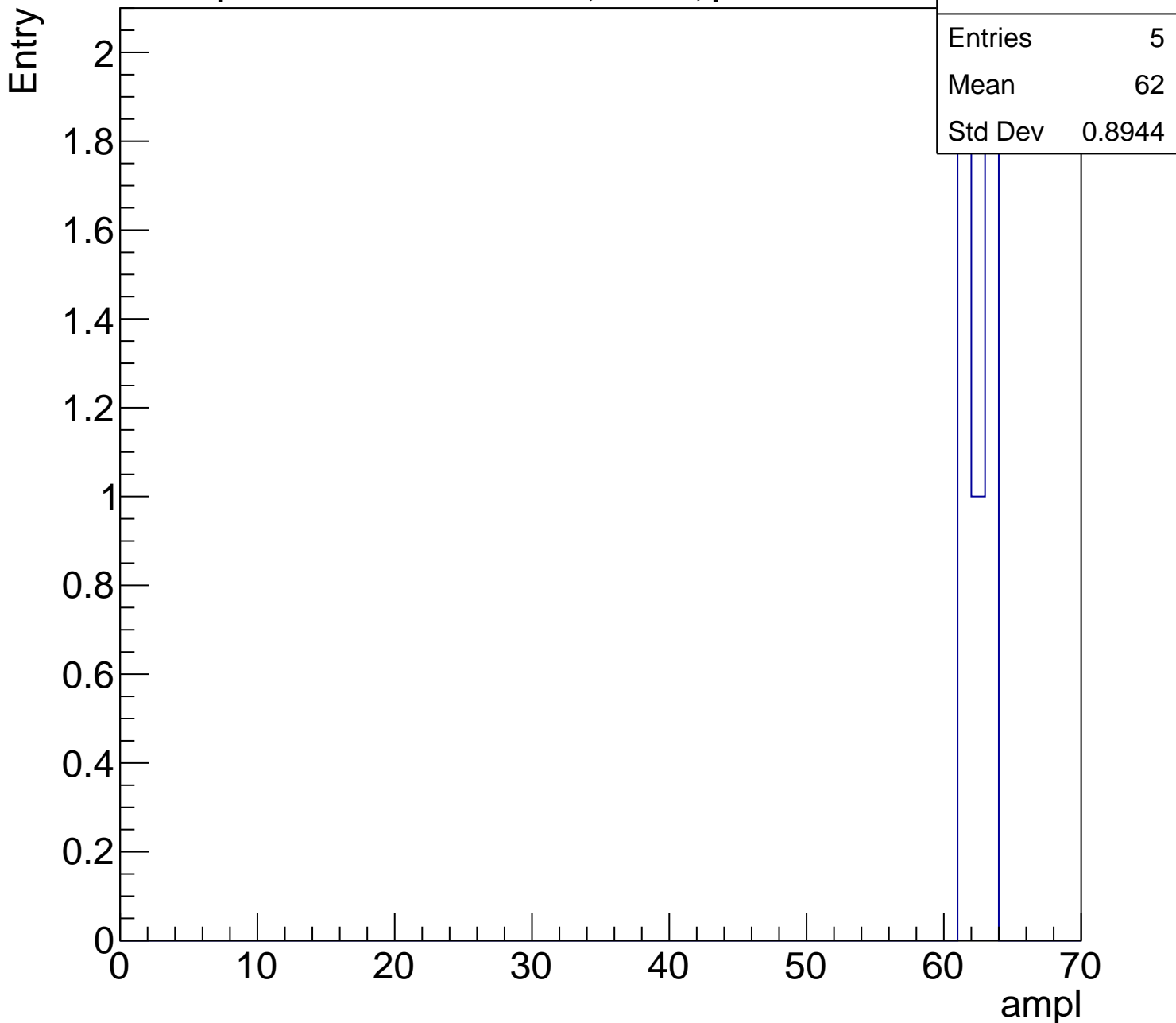
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62
Std Dev	0.8944

0 10 20 30 40 50 60 70

ampl





# B0L102S, U6-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch15, adc0

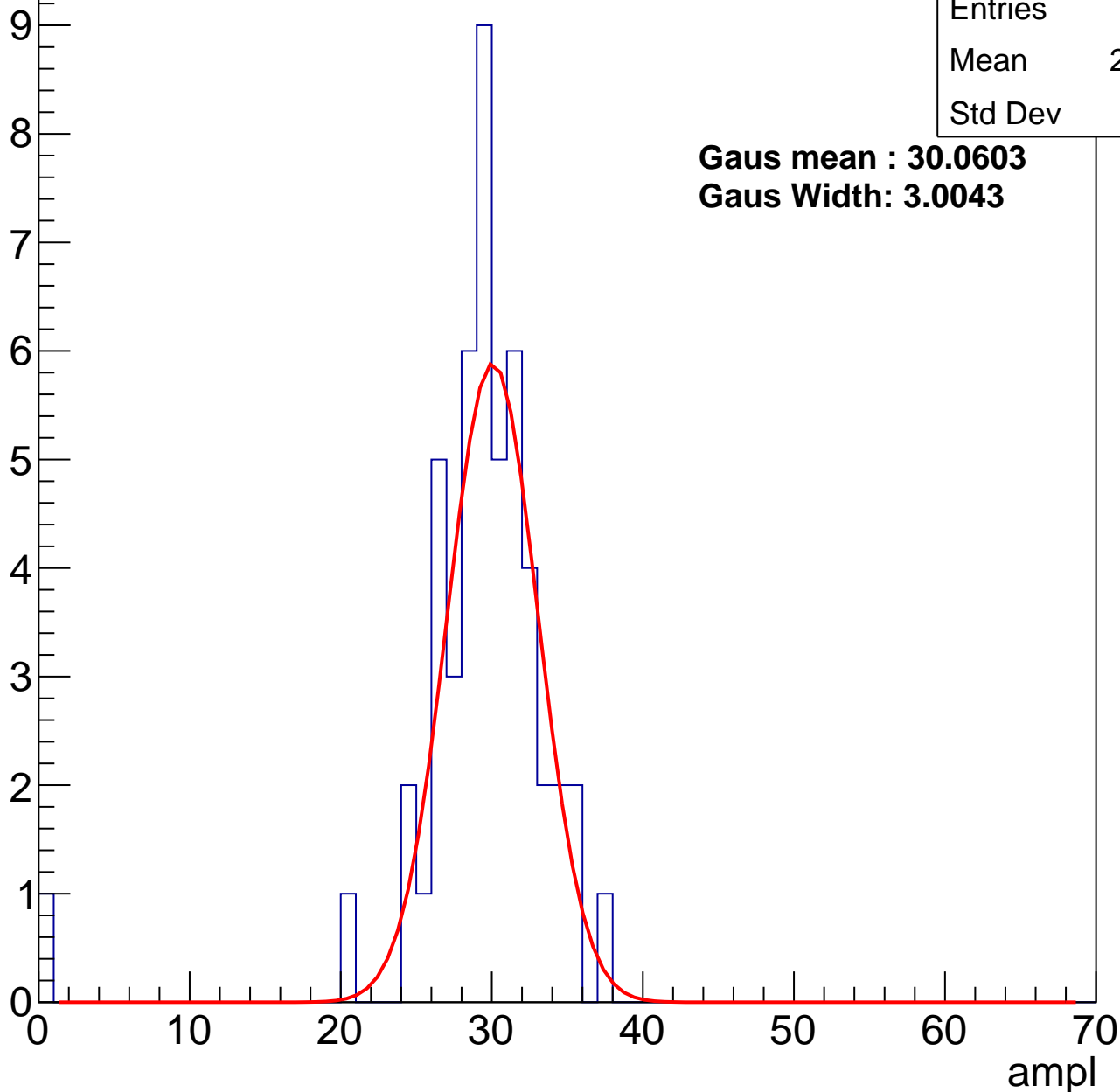
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	28.76
Std Dev	5.16

**Gaus mean : 30.0603**

**Gaus Width: 3.0043**



# B0L102S, U6-ch15, adc1

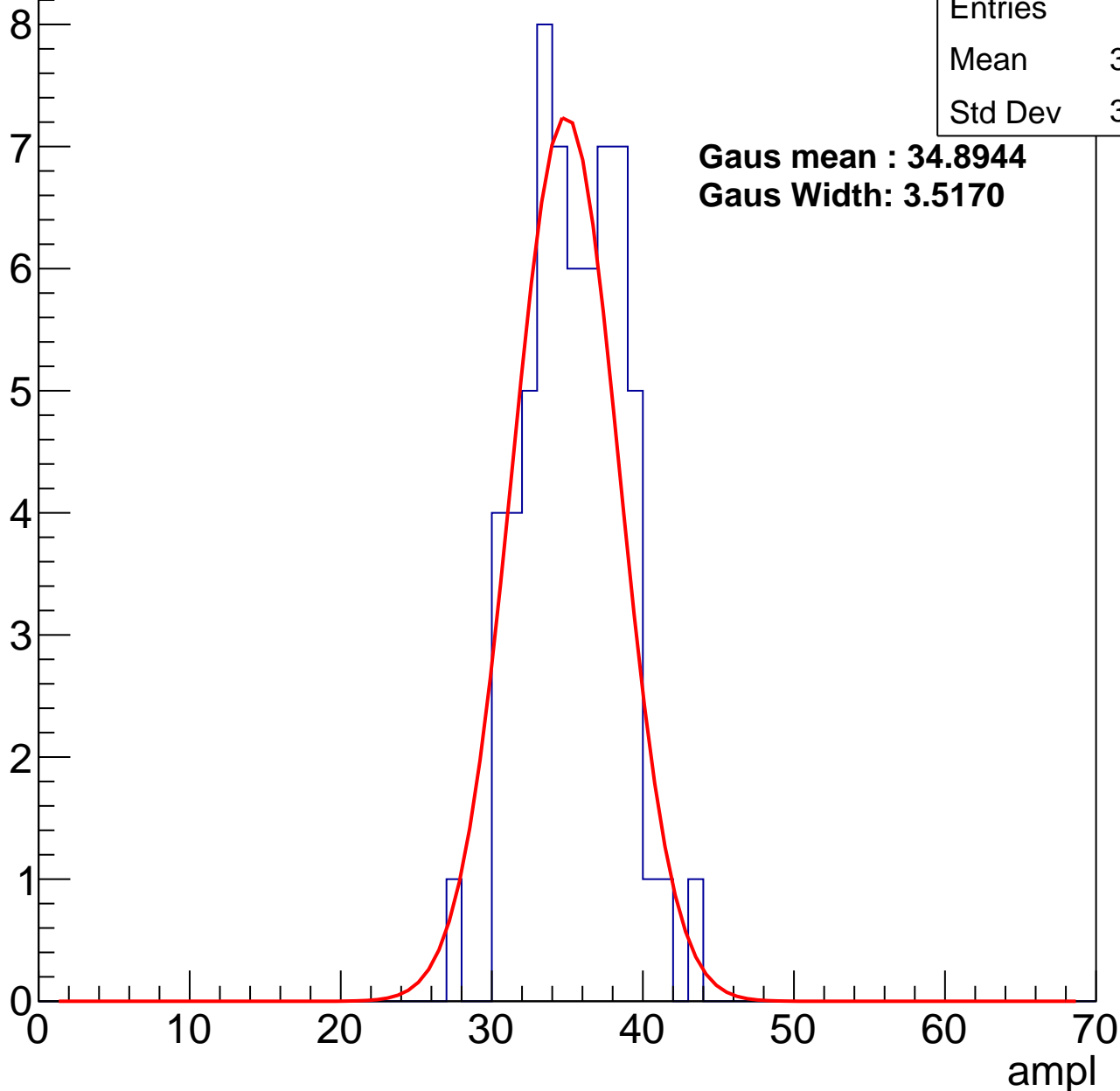
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	34.97
Std Dev	3.122

**Gaus mean : 34.8944**

**Gaus Width: 3.5170**



# B0L102S, U6-ch15, adc2

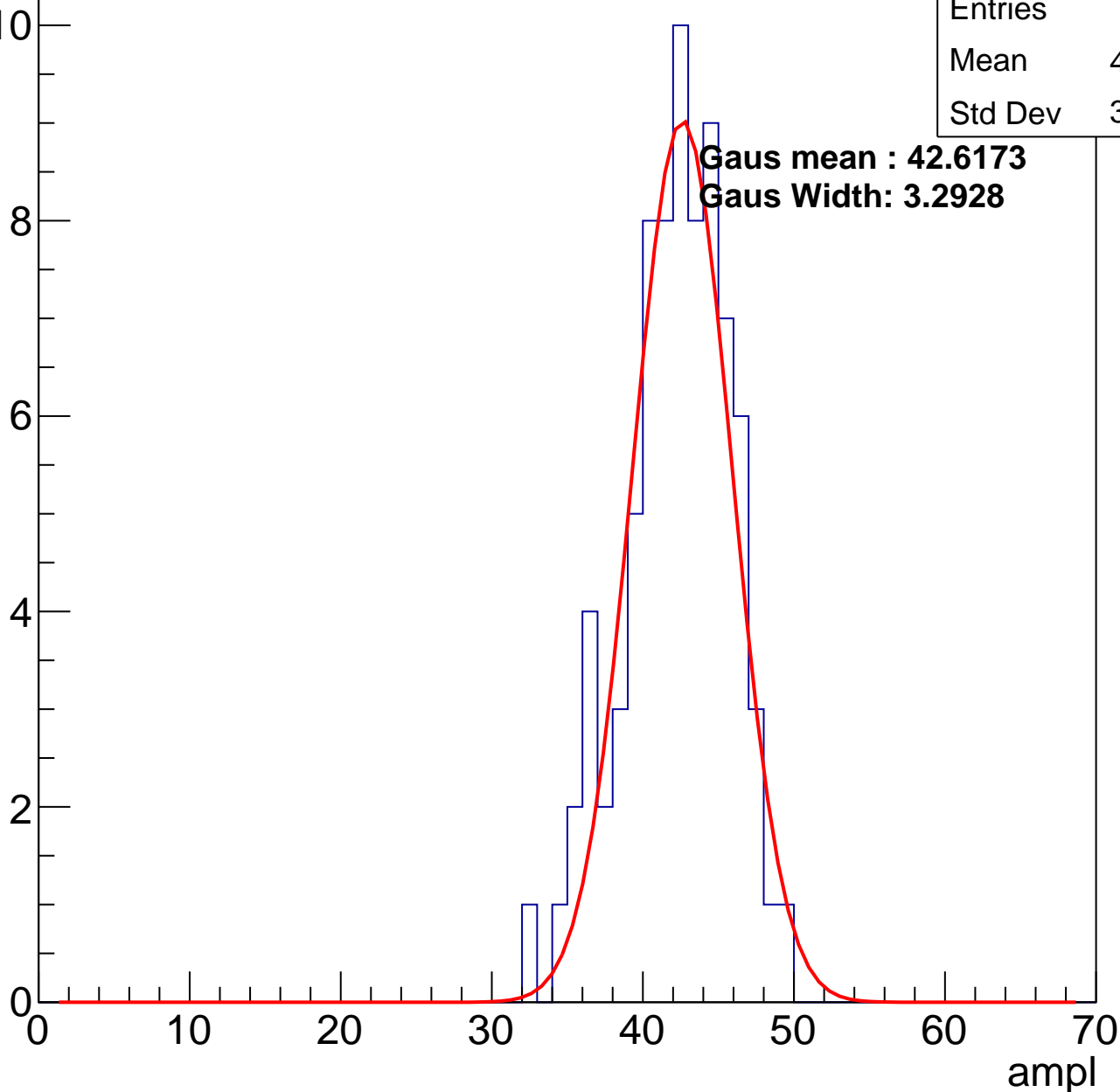
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	41.77
Std Dev	3.464

**Gaus mean : 42.6173**

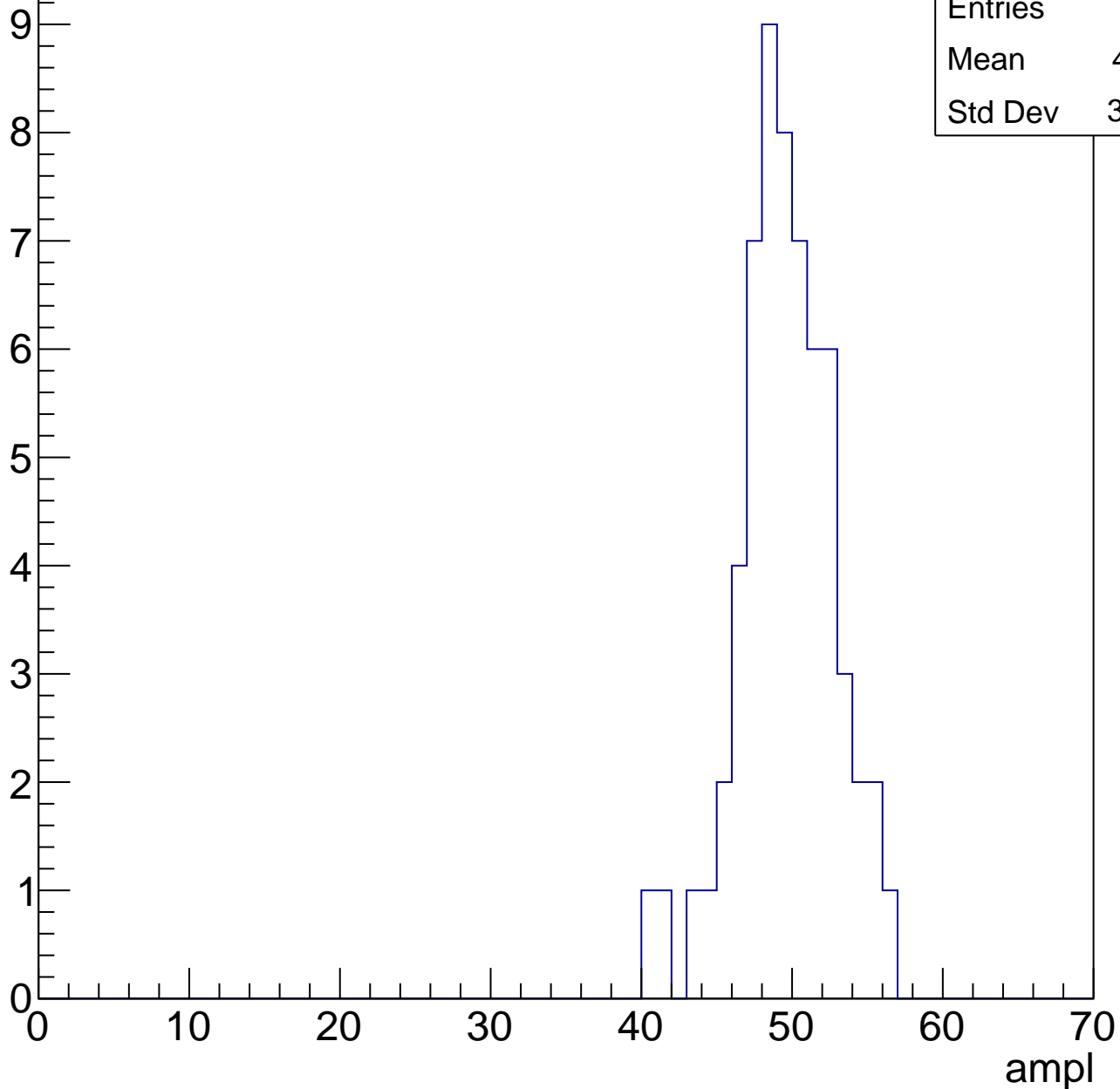
**Gaus Width: 3.2928**



# B0L102S, U6-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



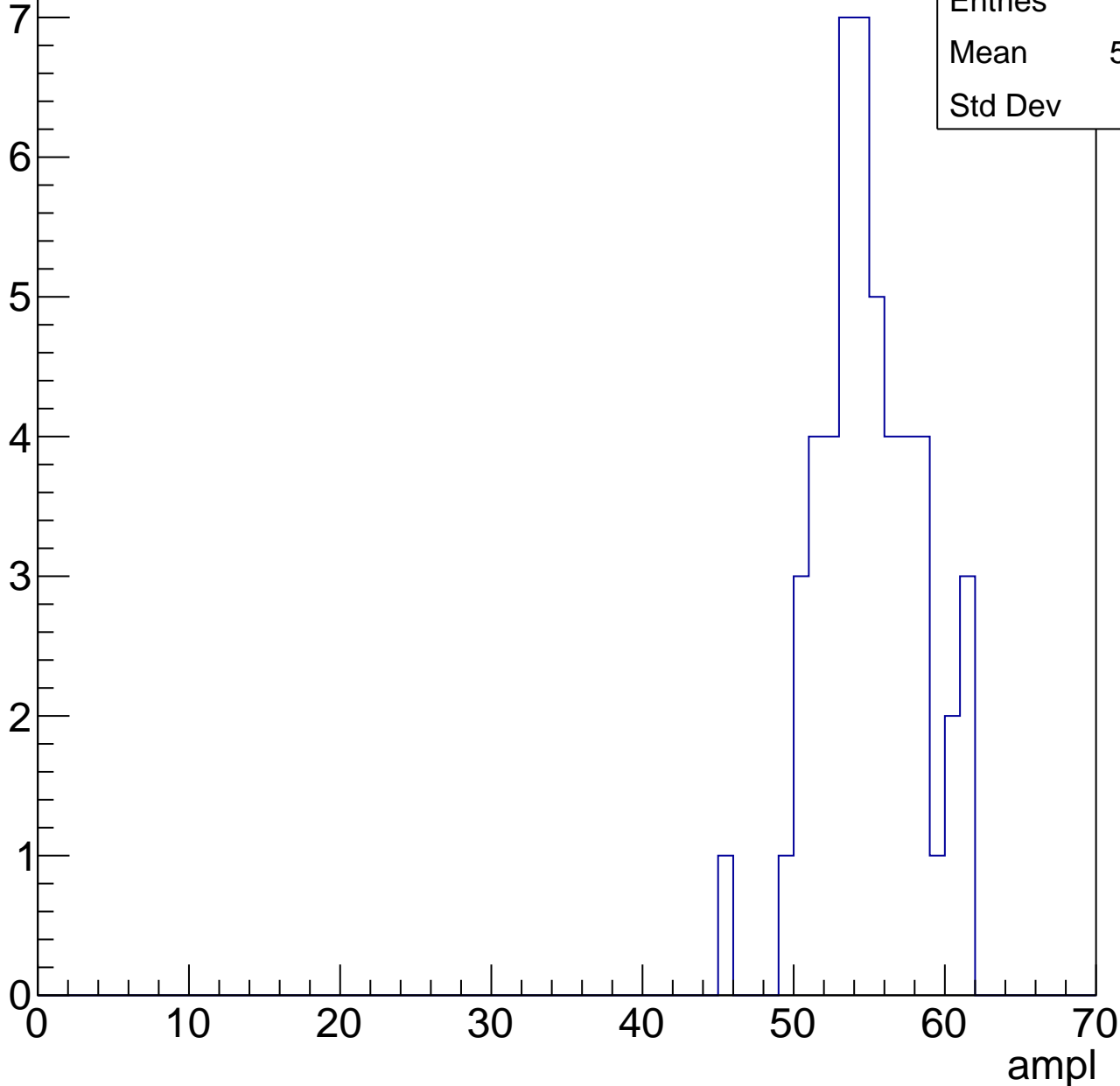
Entries	61
Mean	49.11
Std Dev	3.173

# B0L102S, U6-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

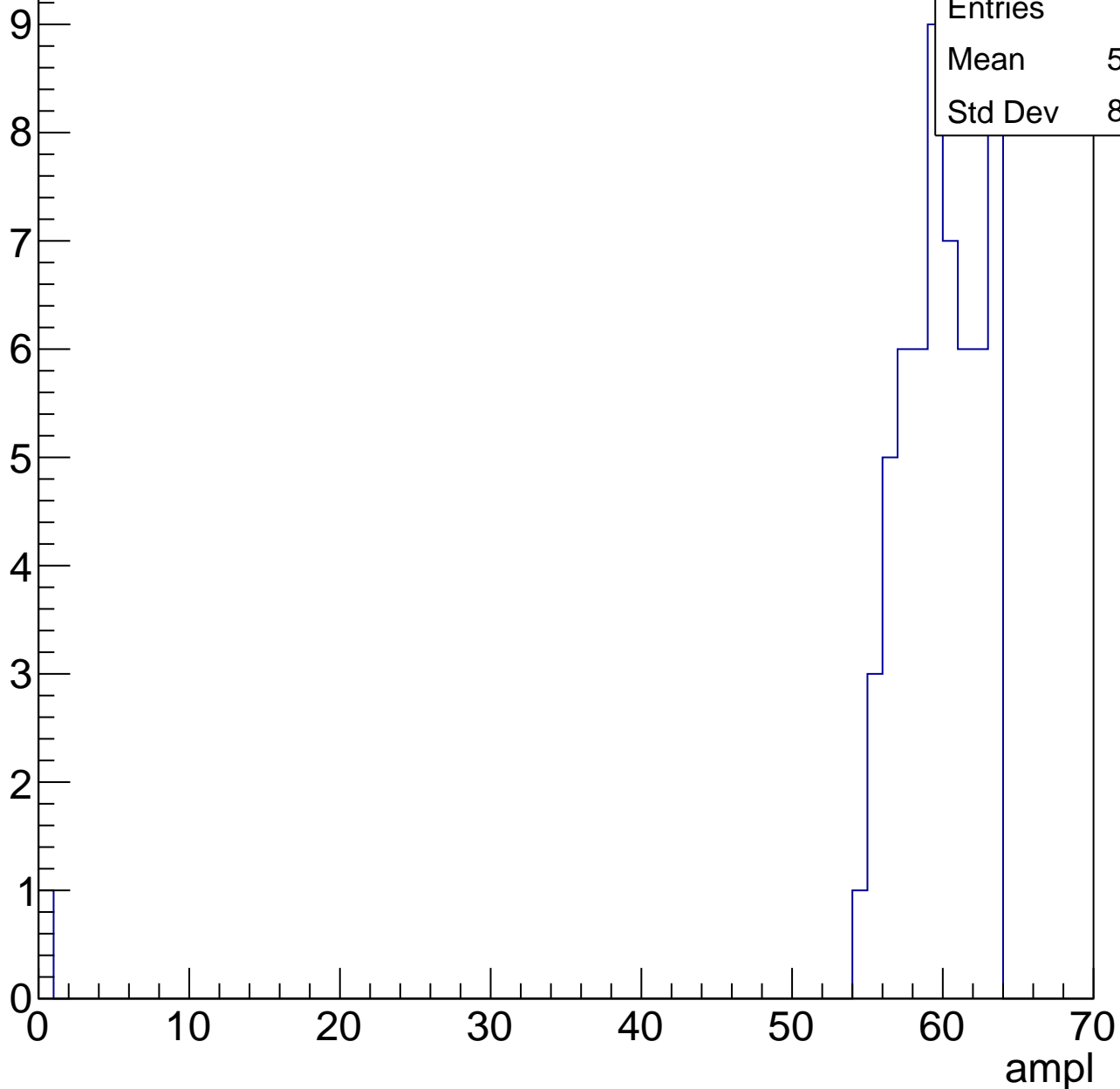
Entries	50
Mean	54.52
Std Dev	3.36



# B0L102S, U6-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

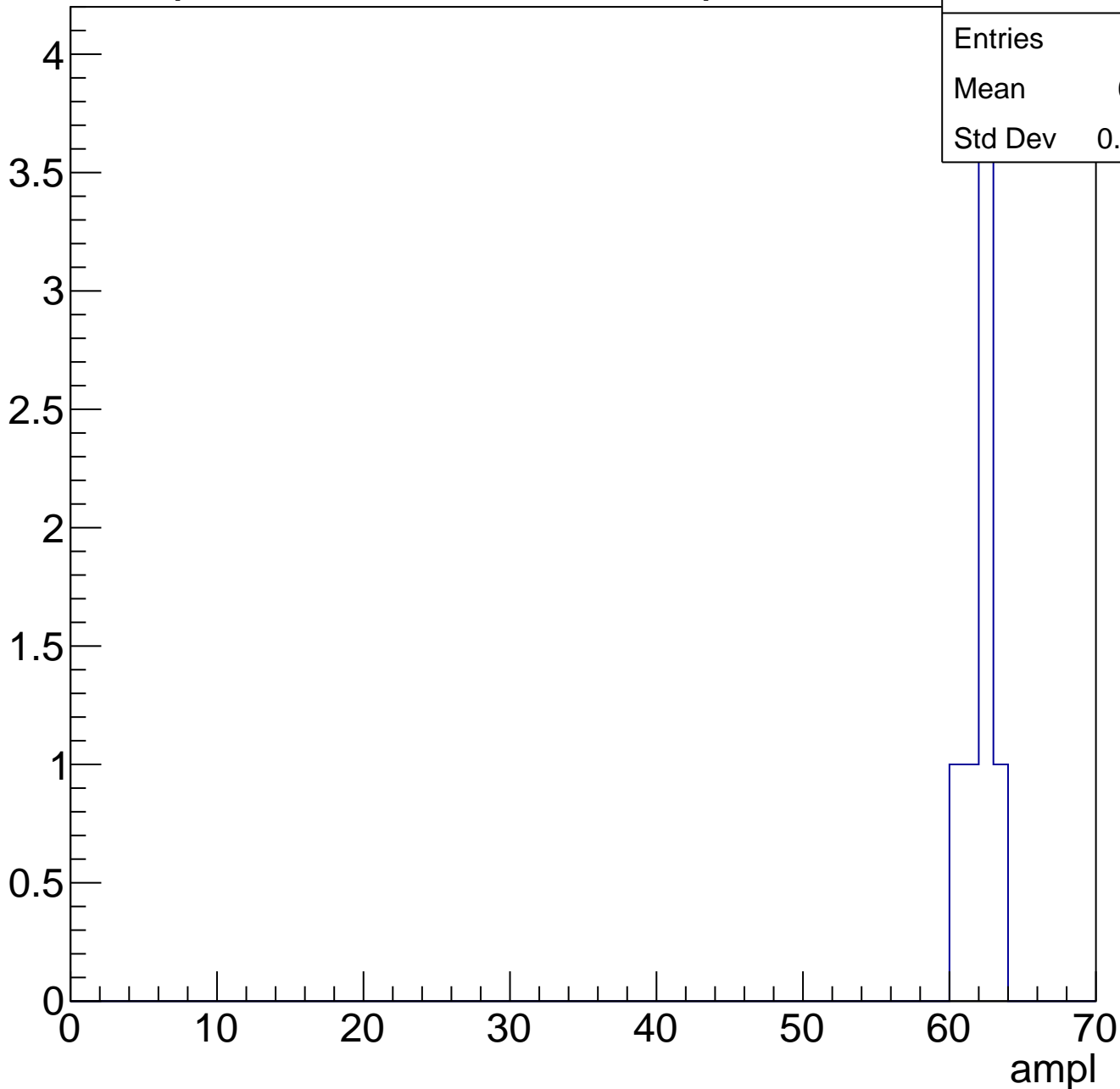
Entry



# B0L102S, U6-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch16, adc0

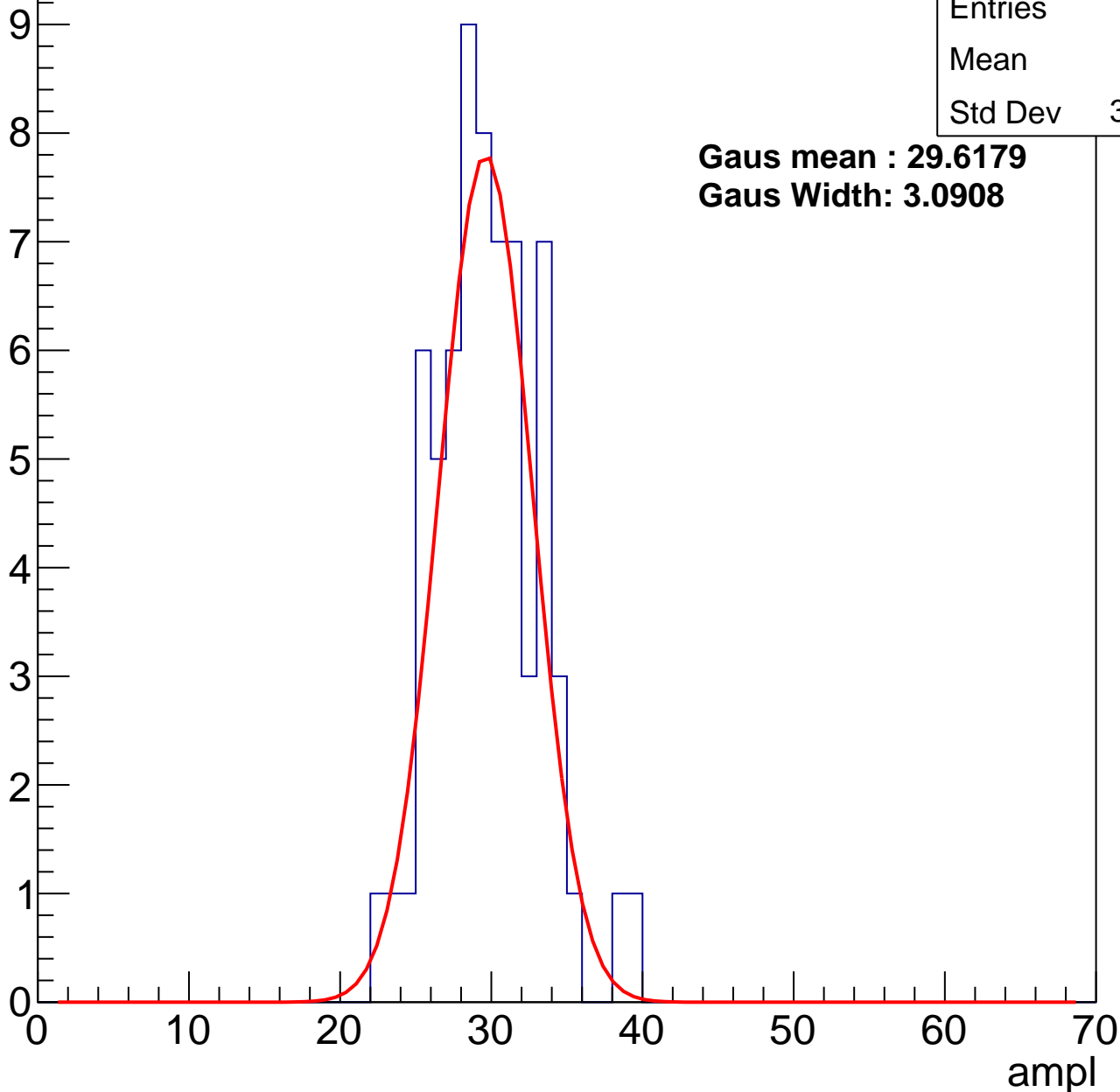
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	29.3
Std Dev	3.332

**Gaus mean : 29.6179**

**Gaus Width: 3.0908**



# B0L102S, U6-ch16, adc1

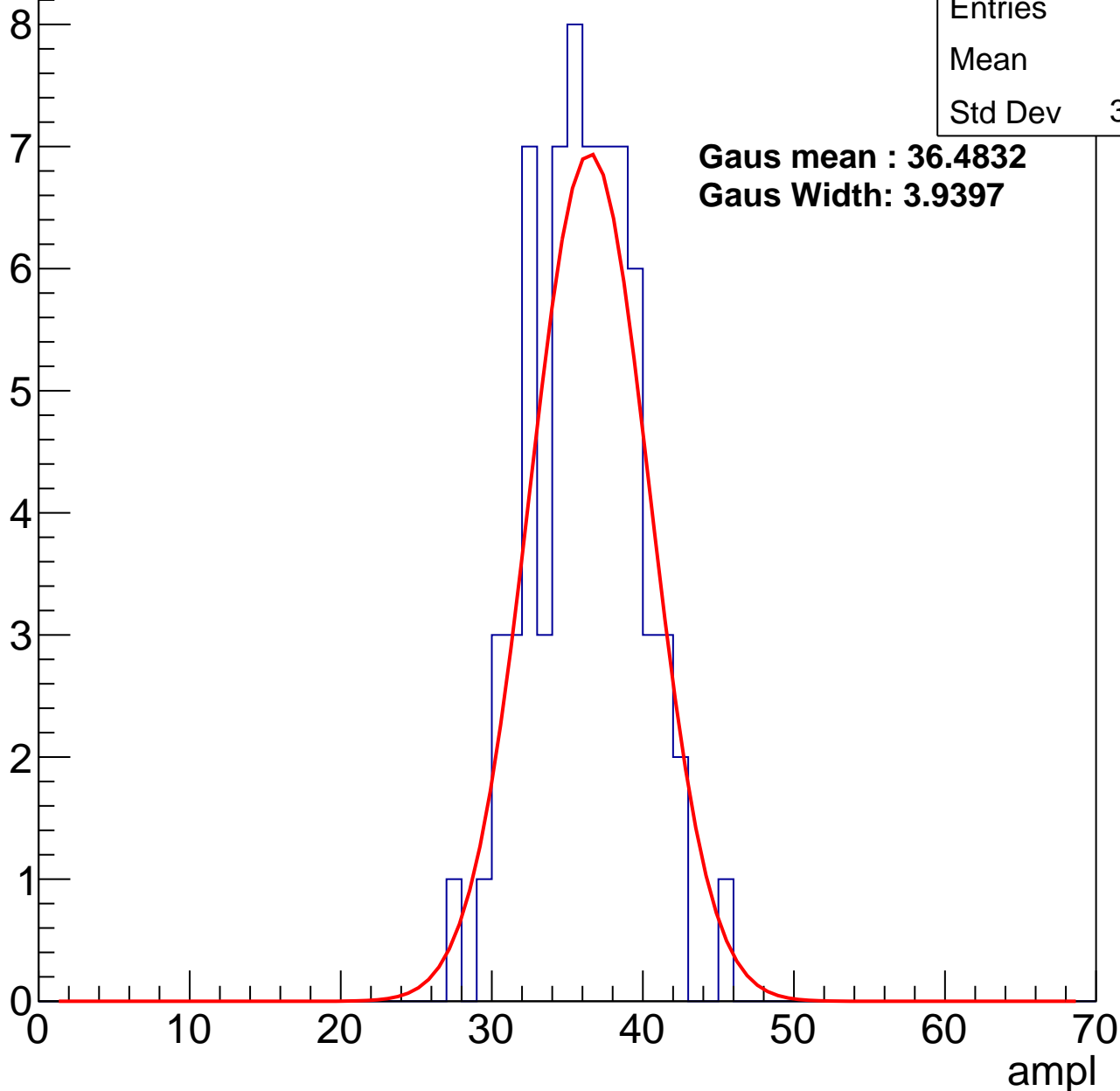
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	35.7
Std Dev	3.503

**Gaus mean : 36.4832**

**Gaus Width: 3.9397**



# B0L102S, U6-ch16, adc2

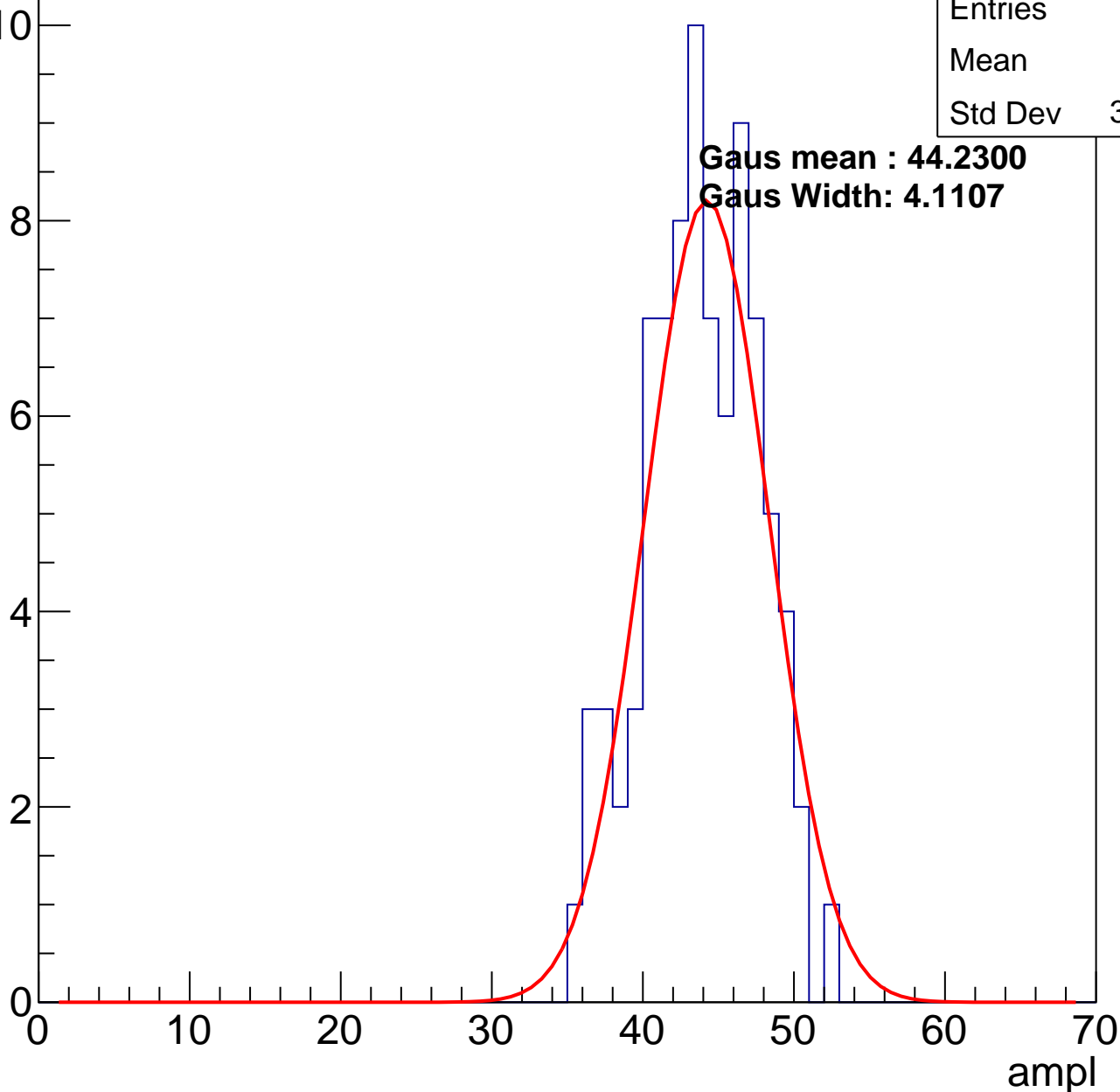
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	43.4
Std Dev	3.717

**Gaus mean : 44.2300**

**Gaus Width: 4.1107**

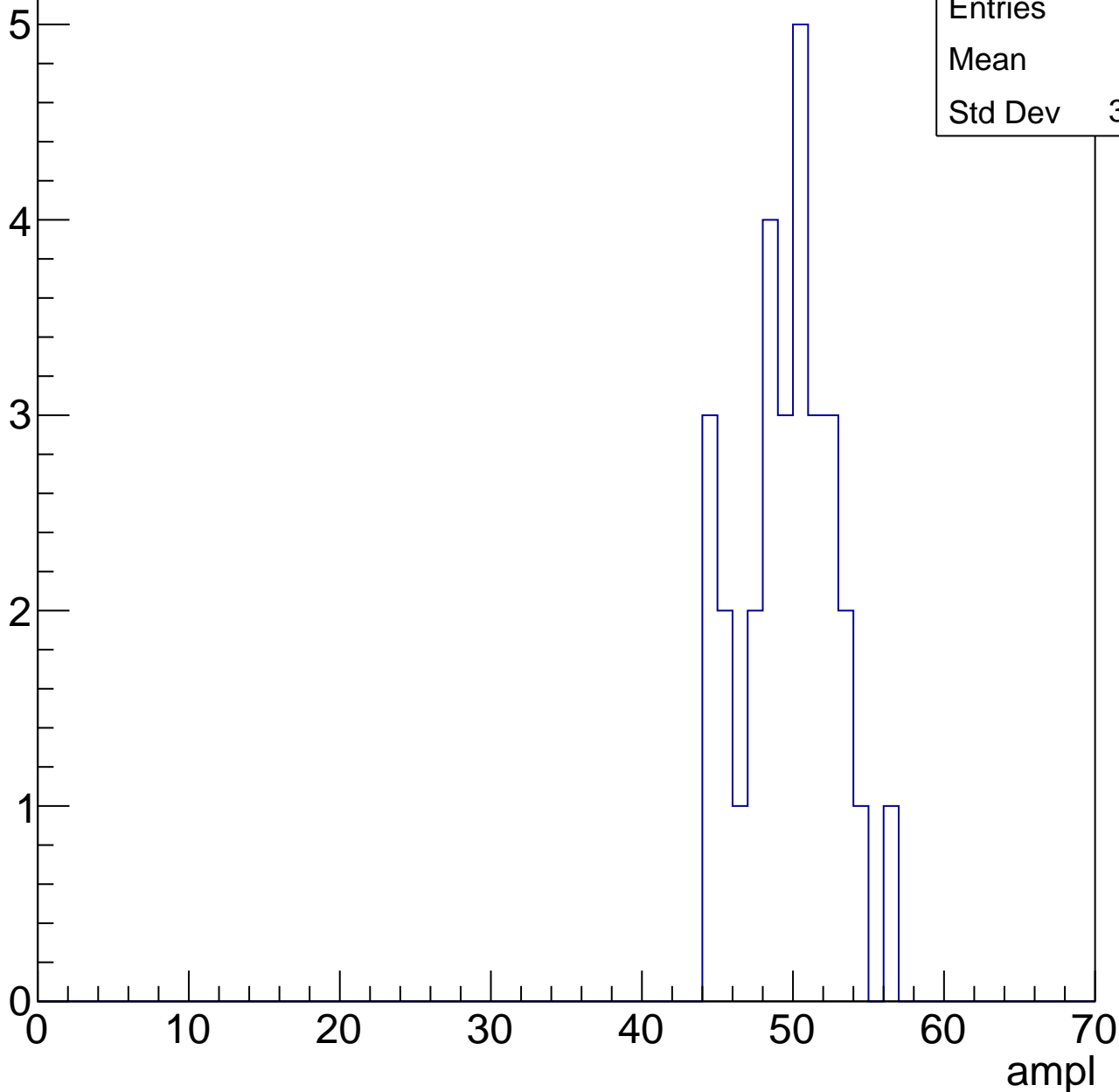


# B0L102S, U6-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

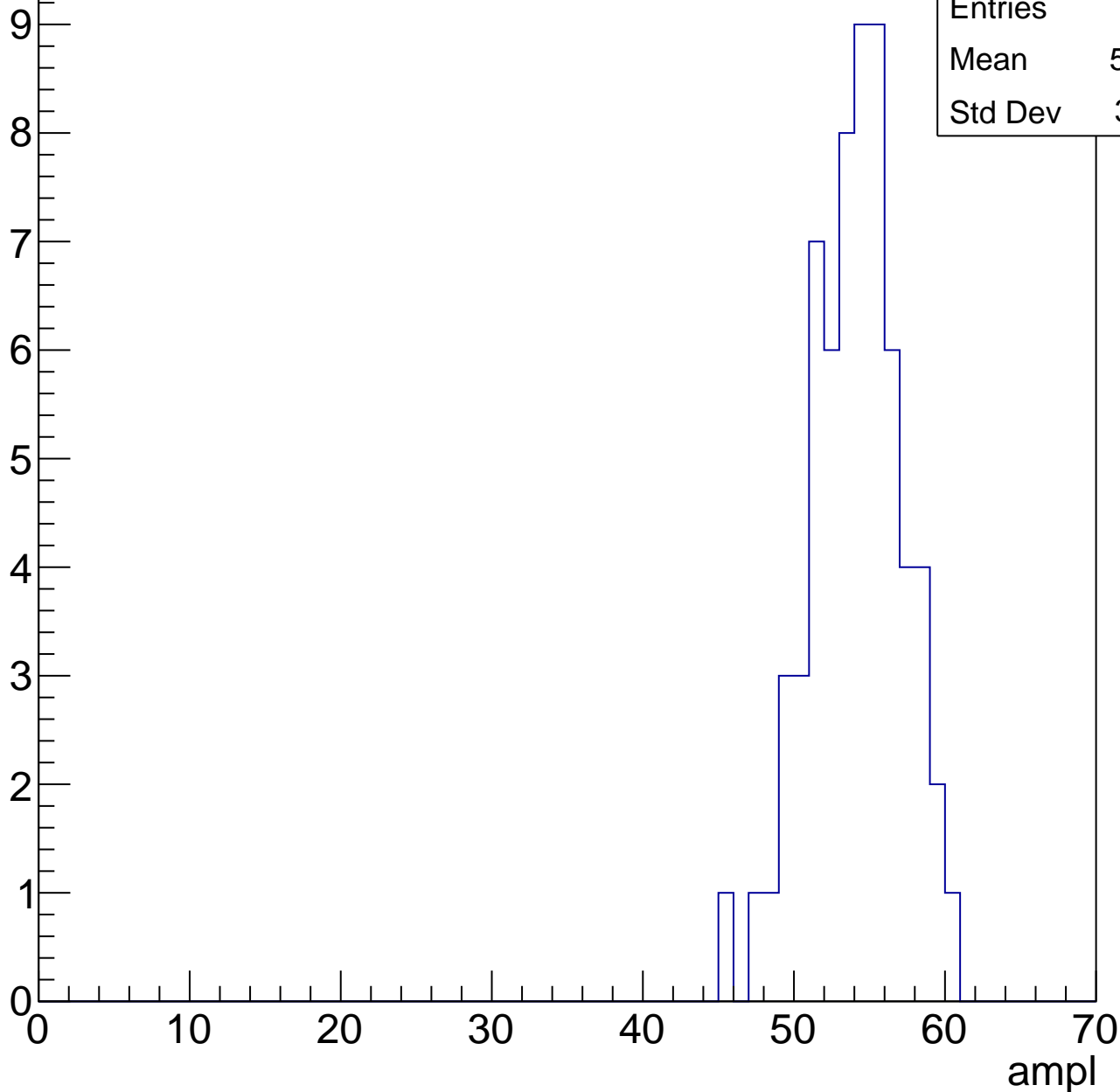
Entries	30
Mean	49.2
Std Dev	3.038



# B0L102S, U6-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

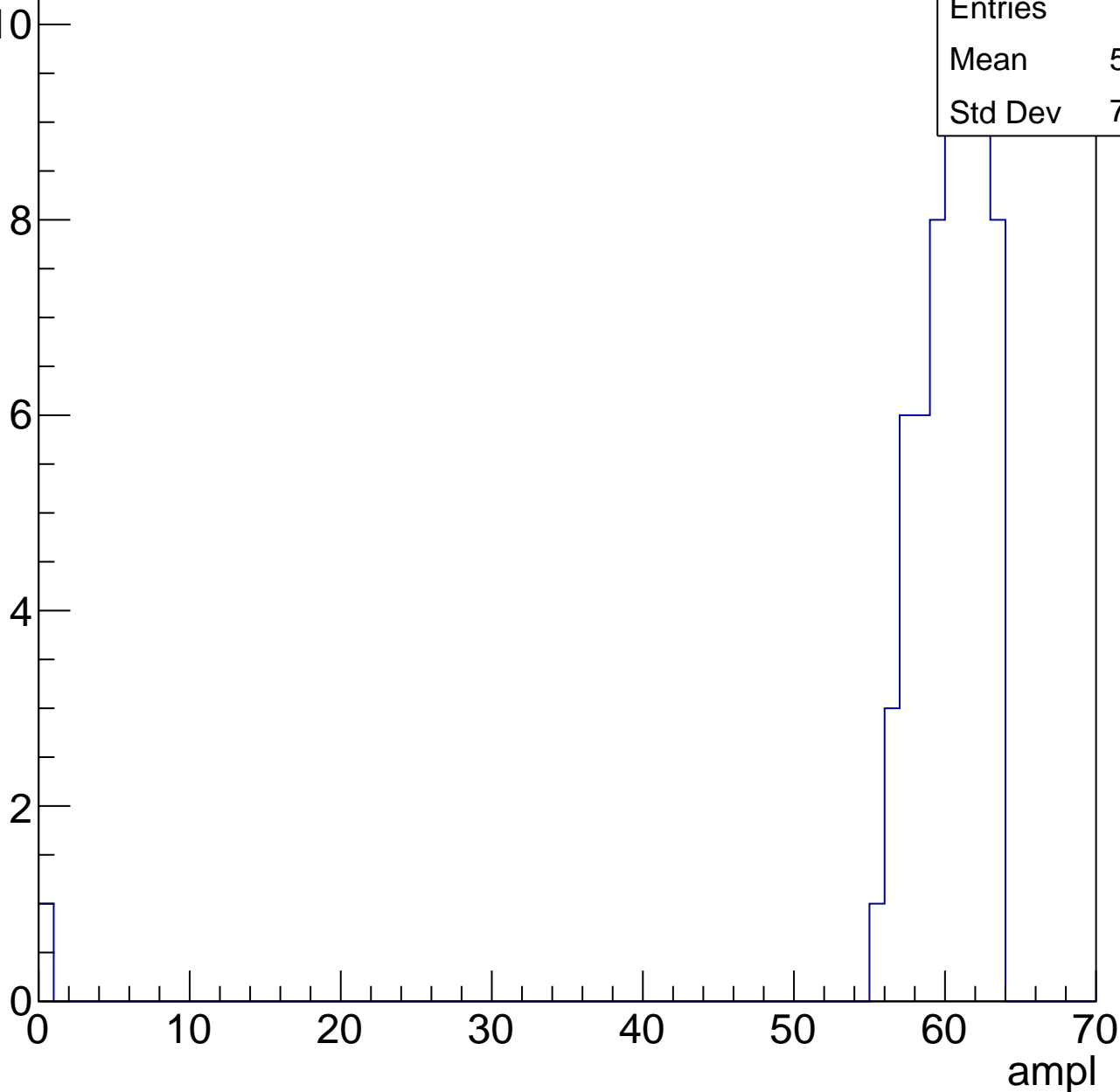


# B0L102S, U6-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	58.97
Std Dev	7.905



# B0L102S, U6-ch16, adc6

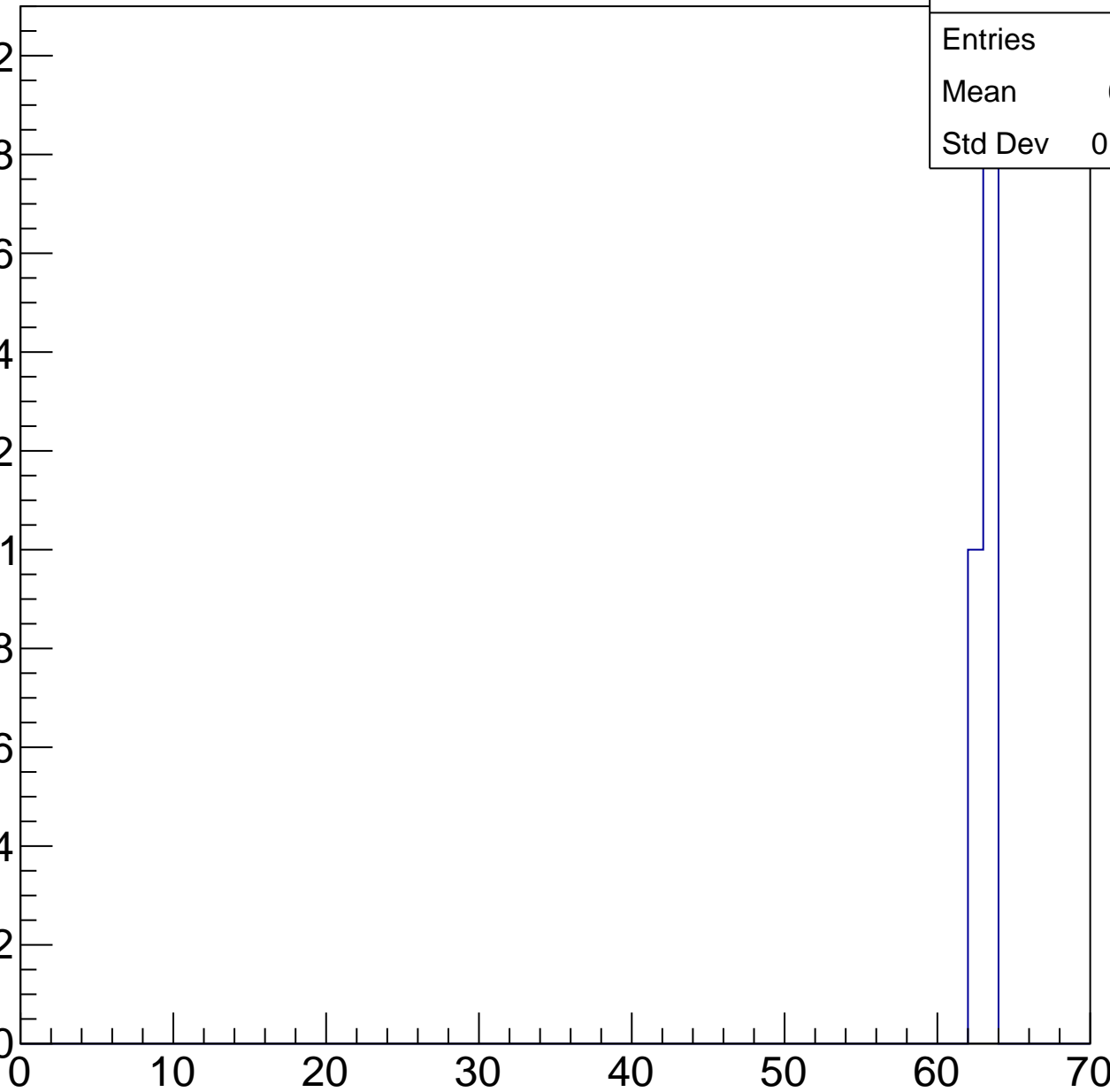
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl





# B0L102S, U6-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch17, adc0

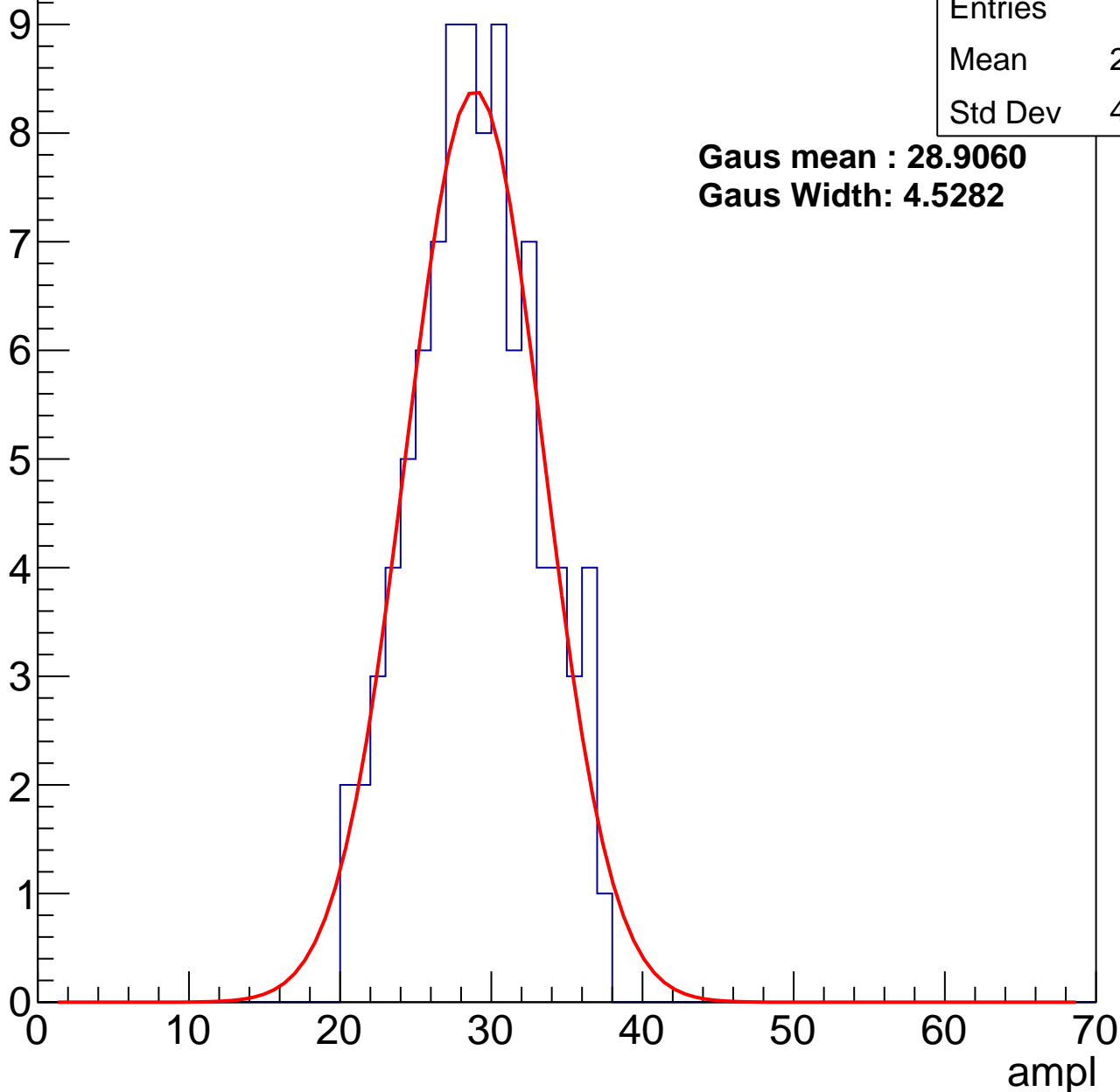
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	93
Mean	28.53
Std Dev	4.039

**Gaus mean : 28.9060**

**Gaus Width: 4.5282**



# B0L102S, U6-ch17, adc1

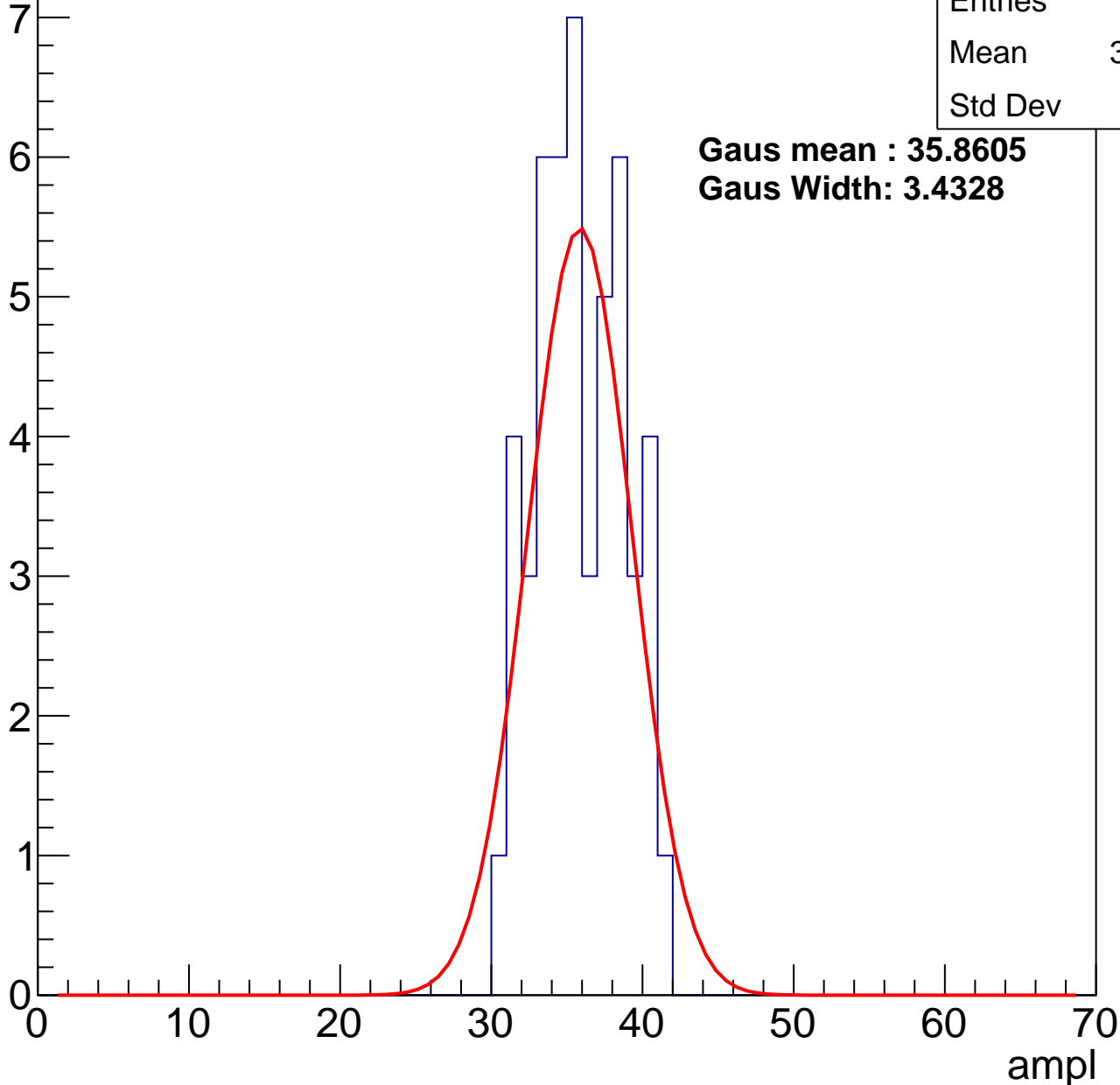
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	35.43
Std Dev	2.85

**Gaus mean : 35.8605**

**Gaus Width: 3.4328**



# B0L102S, U6-ch17, adc2

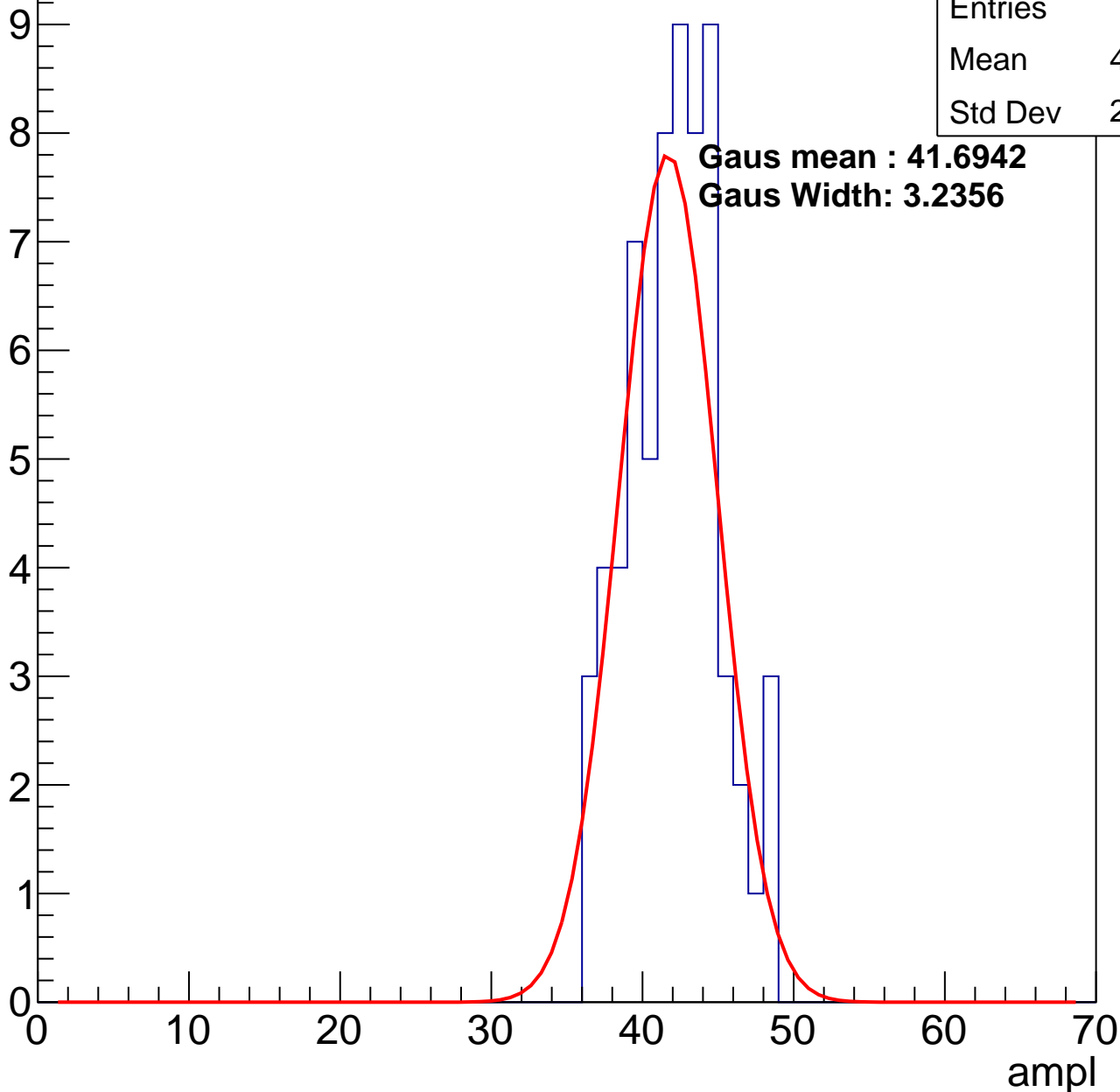
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	41.59
Std Dev	2.985

**Gaus mean : 41.6942**

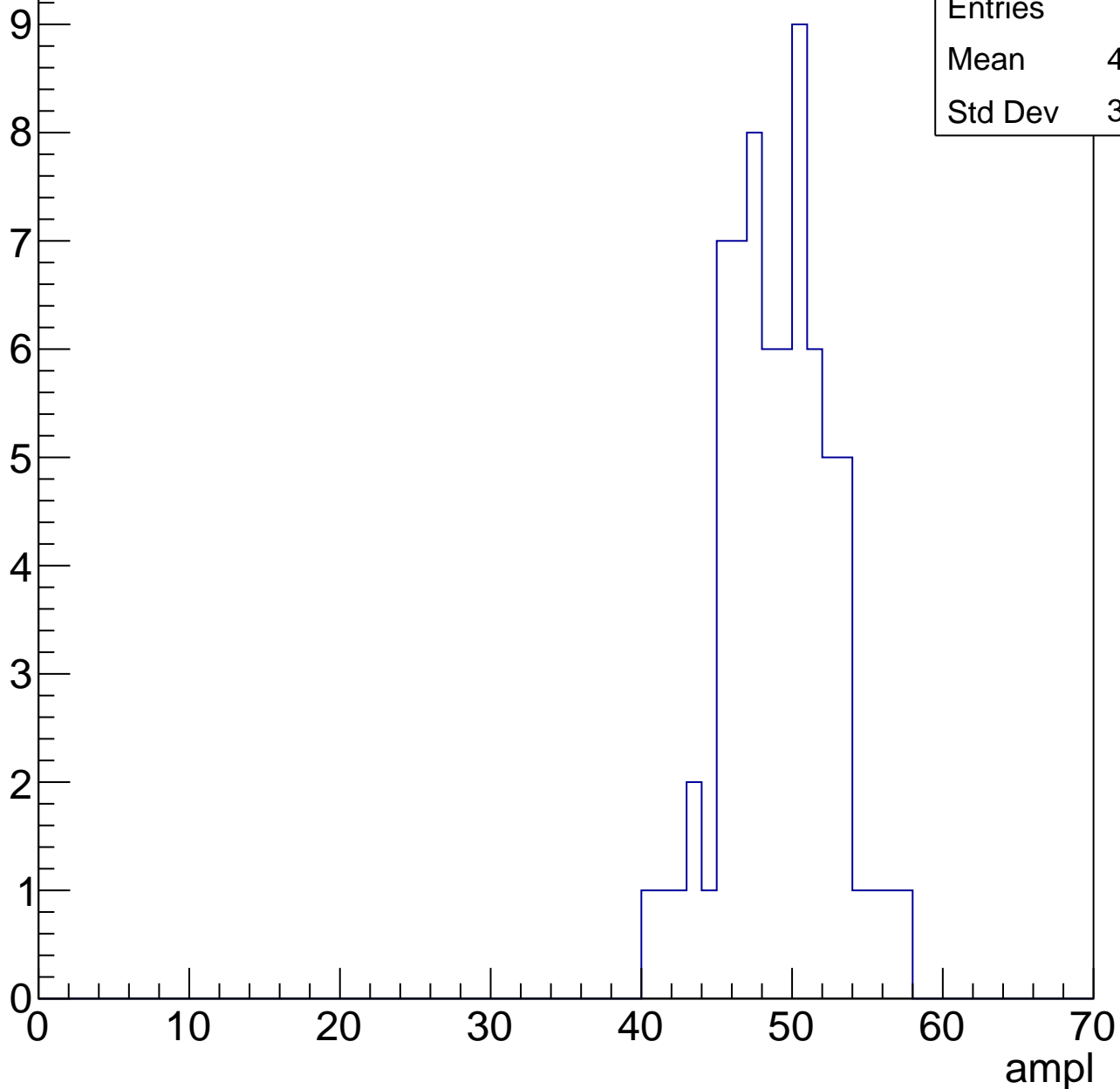
**Gaus Width: 3.2356**



# B0L102S, U6-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

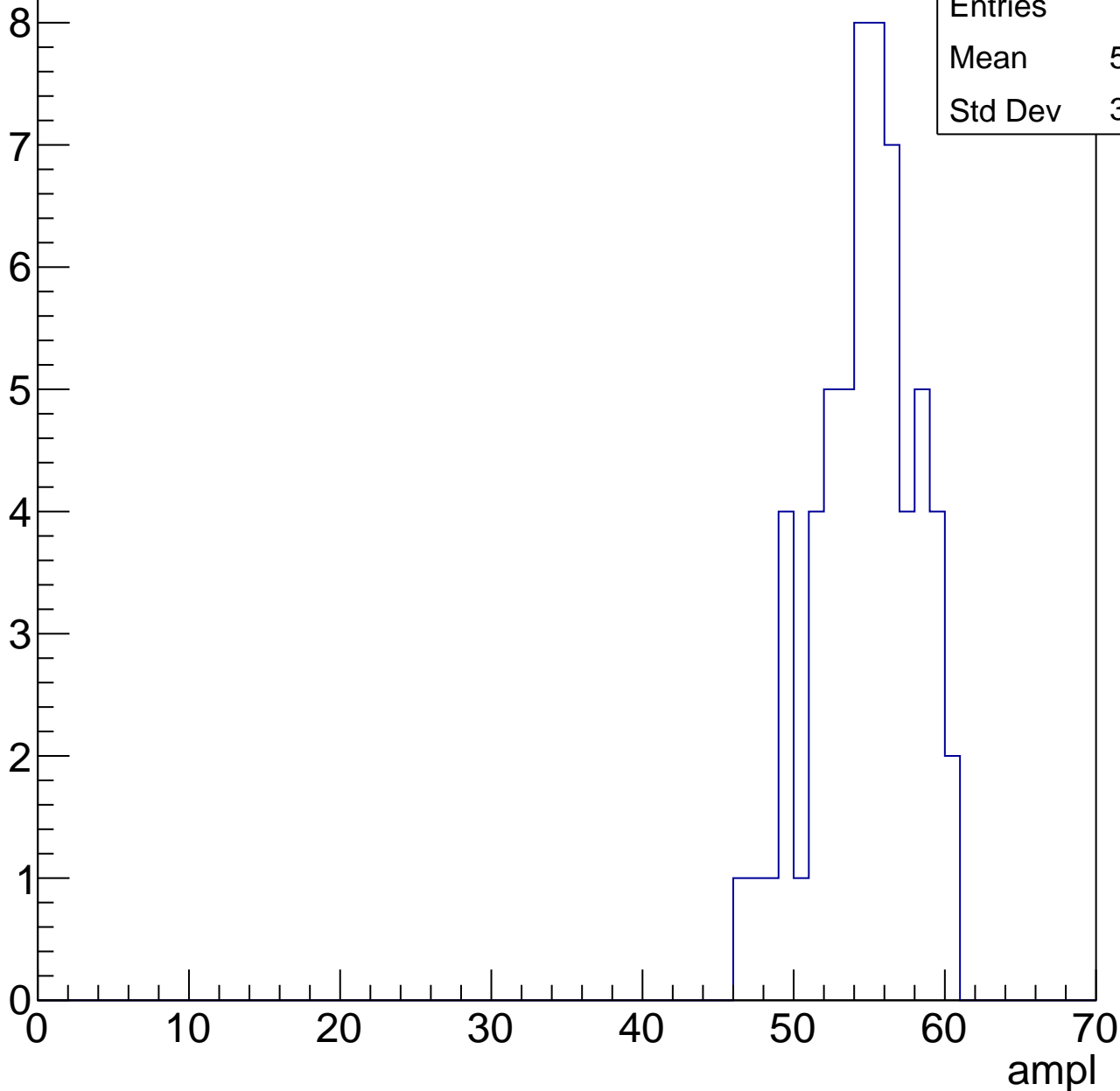


# B0L102S, U6-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	54.23
Std Dev	3.283



# B0L102S, U6-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

Entries	57
Mean	59.04
Std Dev	8.206

ampl

0

10

20

30

40

50

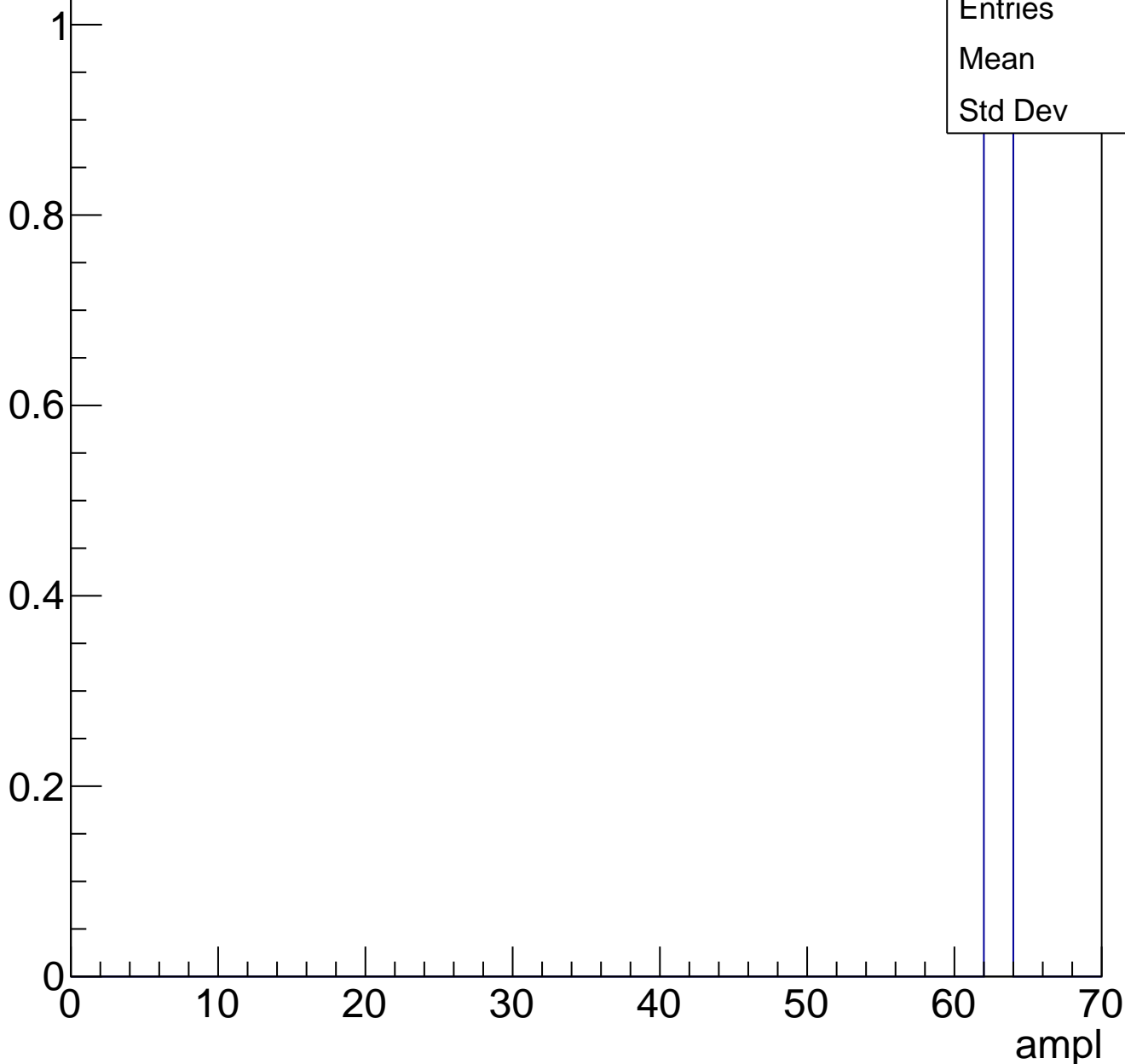
60

70

# B0L102S, U6-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch18, adc0

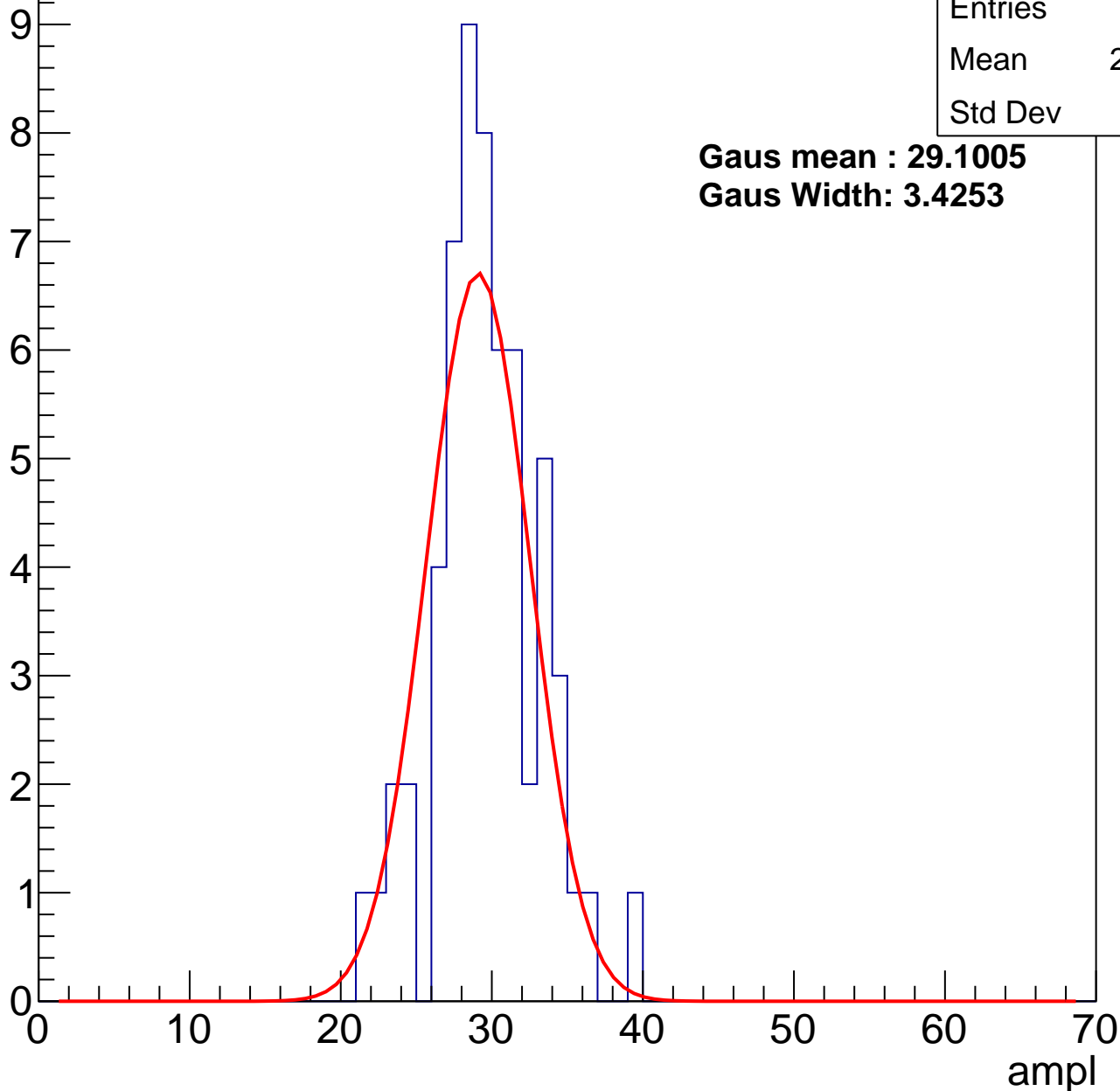
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	29.17
Std Dev	3.43

**Gaus mean : 29.1005**

**Gaus Width: 3.4253**



# B0L102S, U6-ch18, adc1

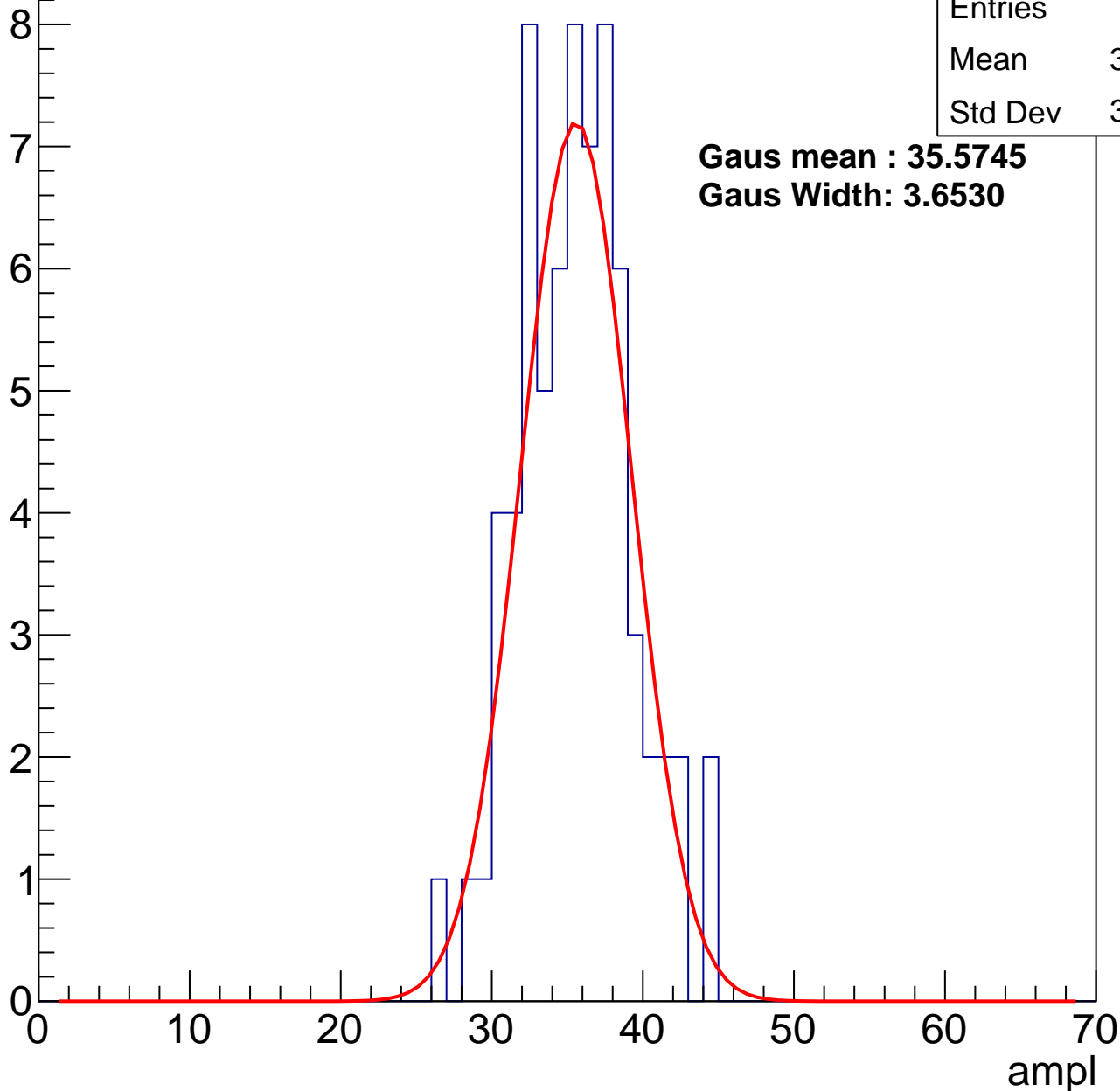
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	35.13
Std Dev	3.684

**Gaus mean : 35.5745**

**Gaus Width: 3.6530**



# B0L102S, U6-ch18, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	75
Mean	42.24
Std Dev	3.409

**Gaus mean : 42.9218**

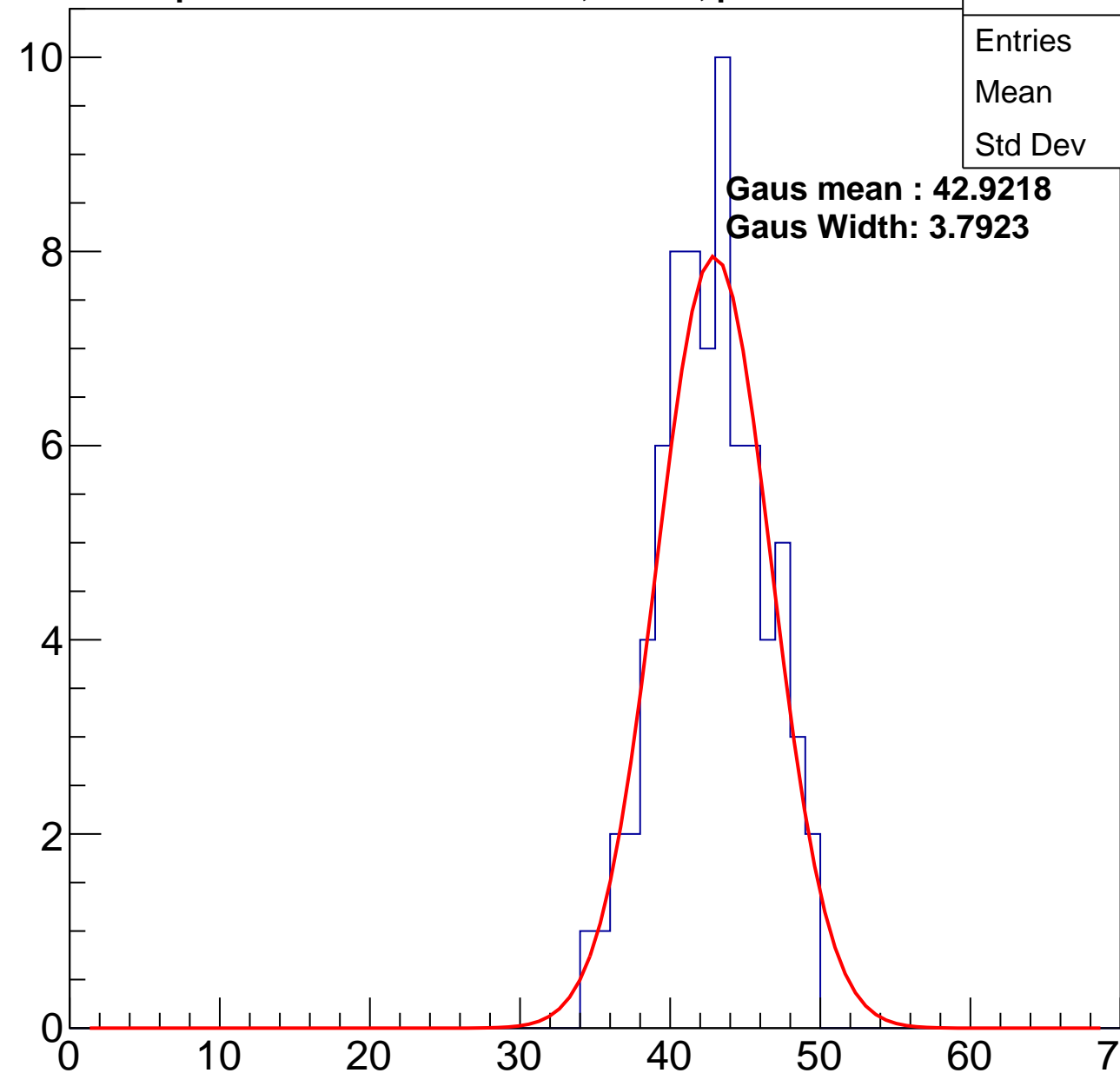
**Gaus Width: 3.7923**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

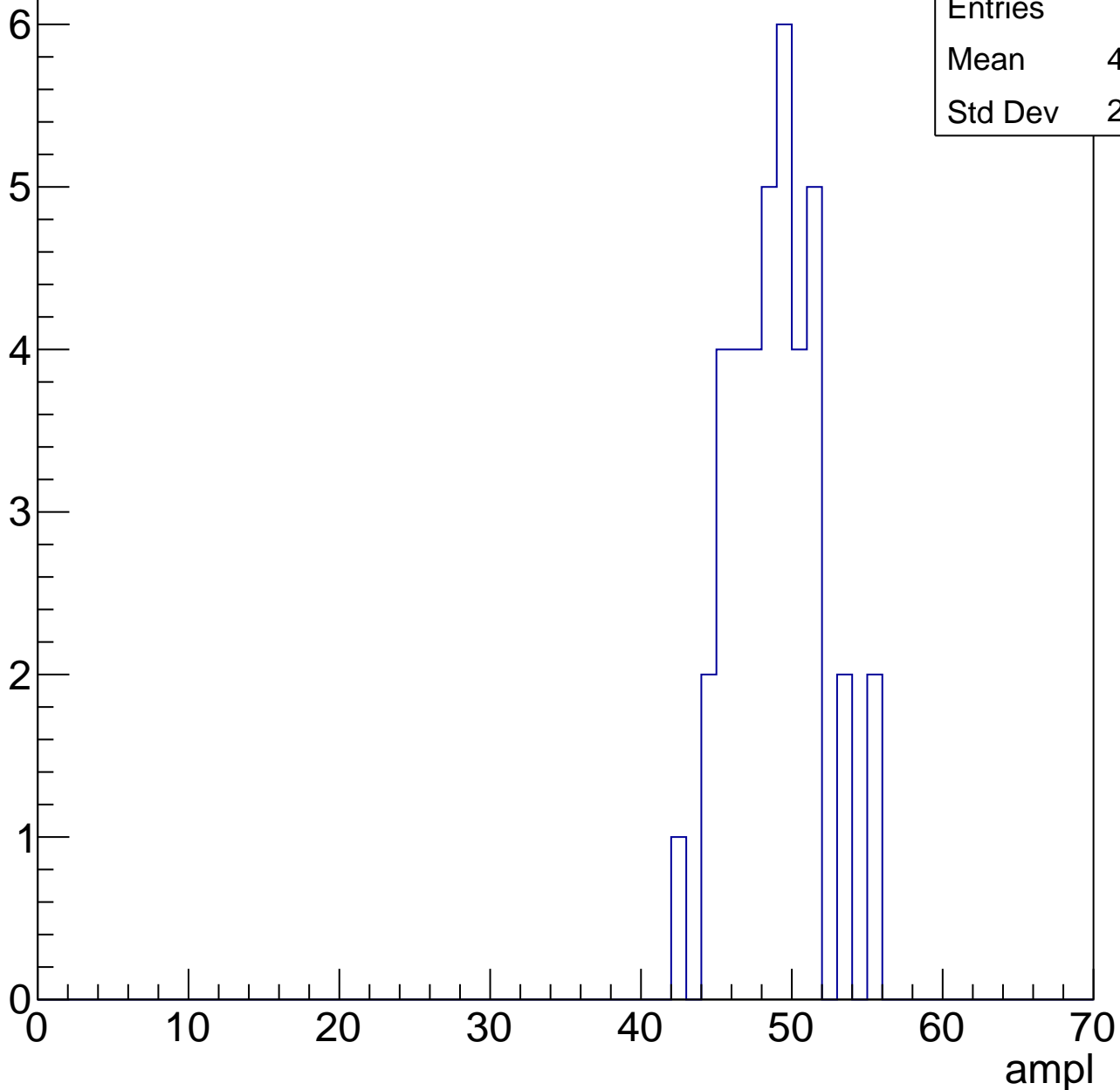


# B0L102S, U6-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	39
Mean	48.38
Std Dev	2.923

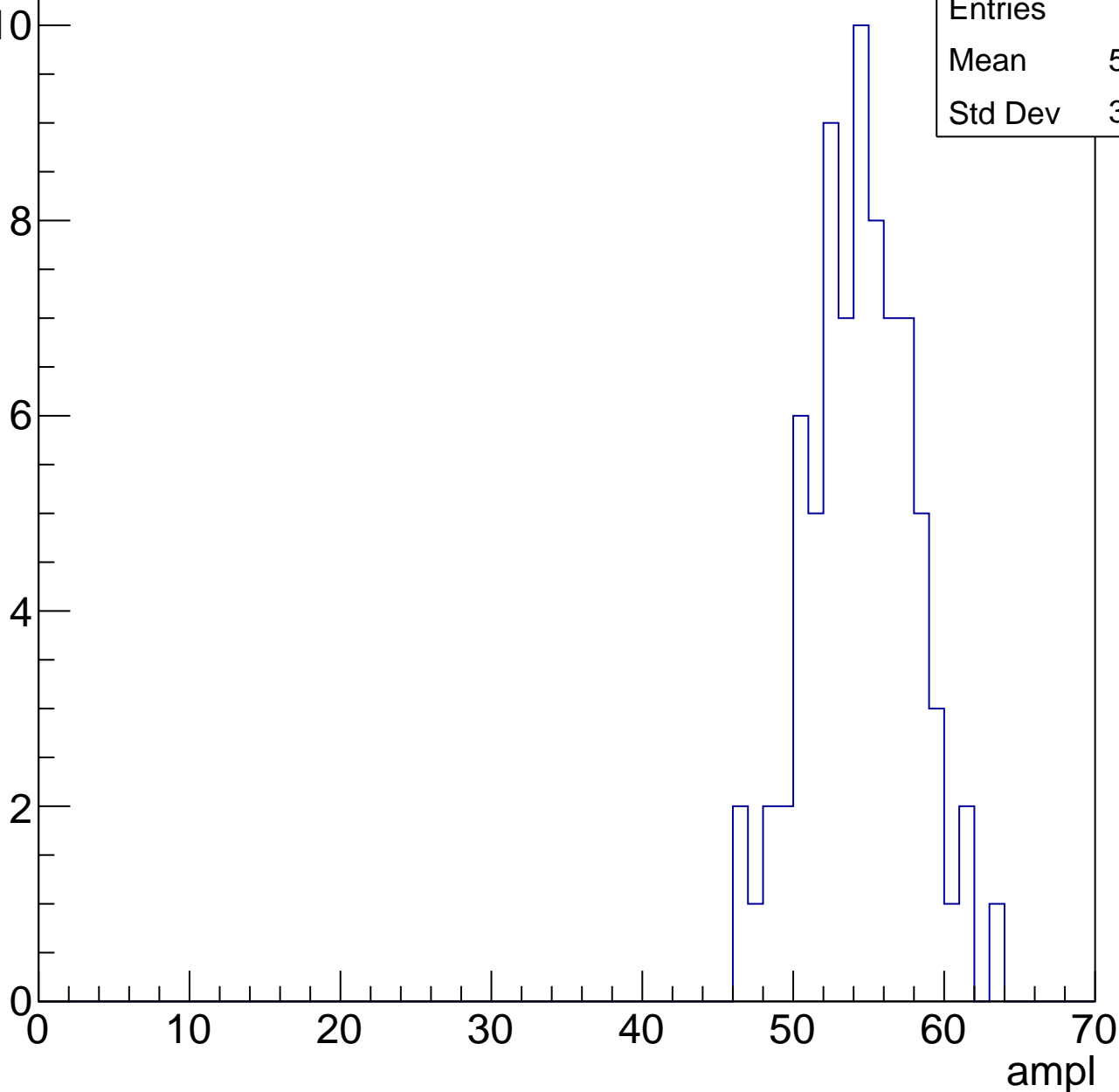


# B0L102S, U6-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

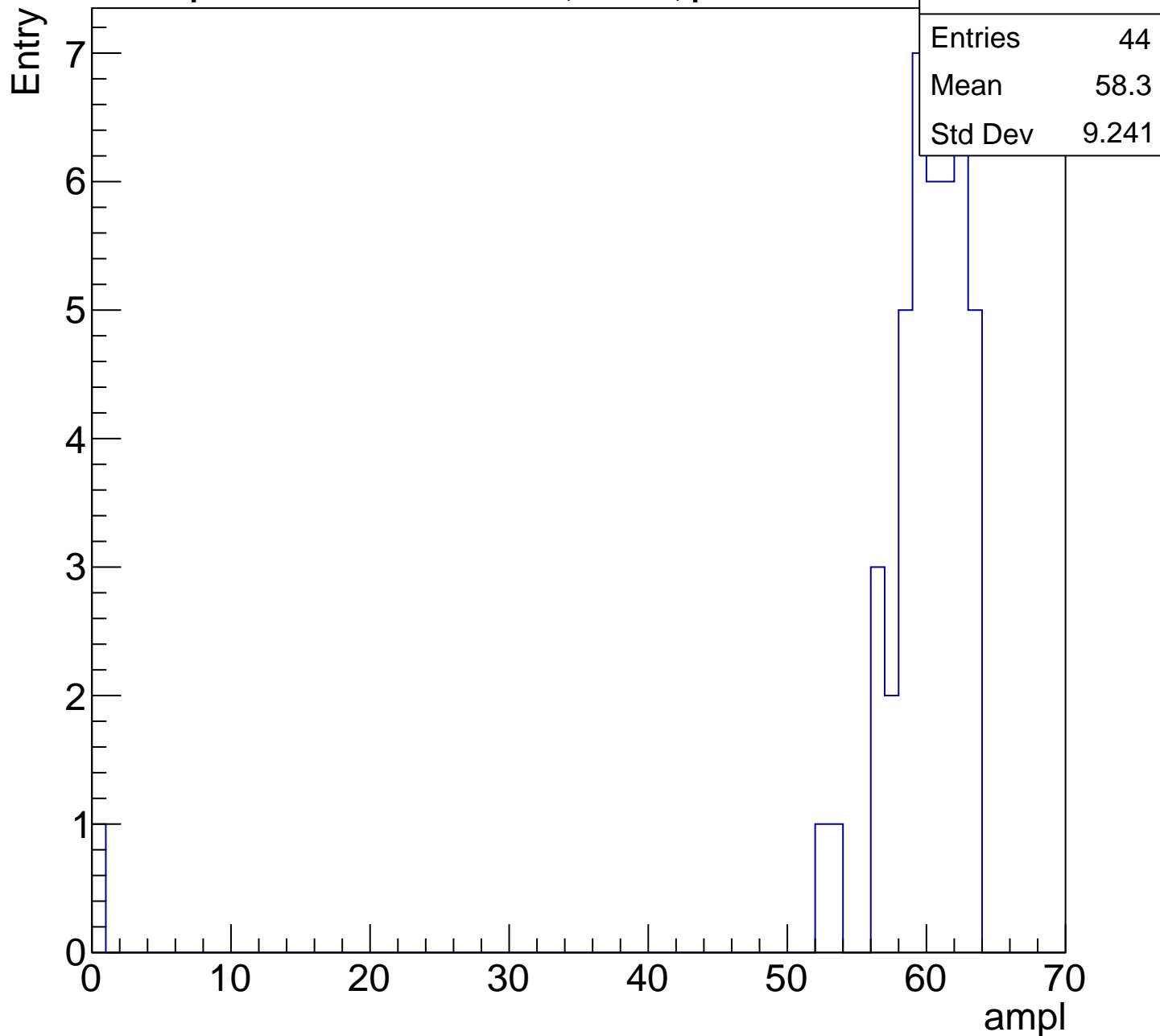
Entry

Entries	78
Mean	53.97
Std Dev	3.493



# B0L102S, U6-ch18, adc5

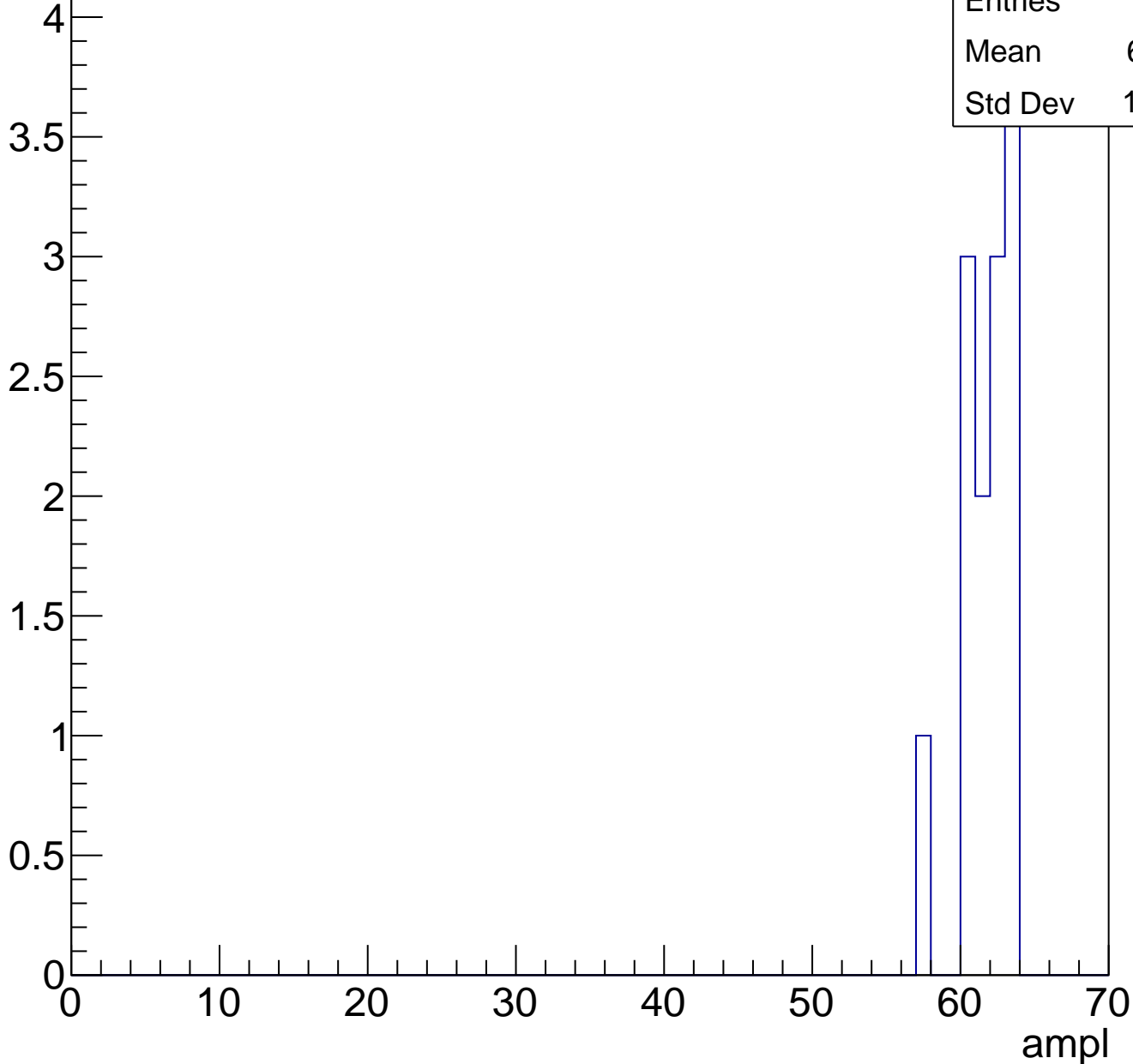
calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch19, adc0

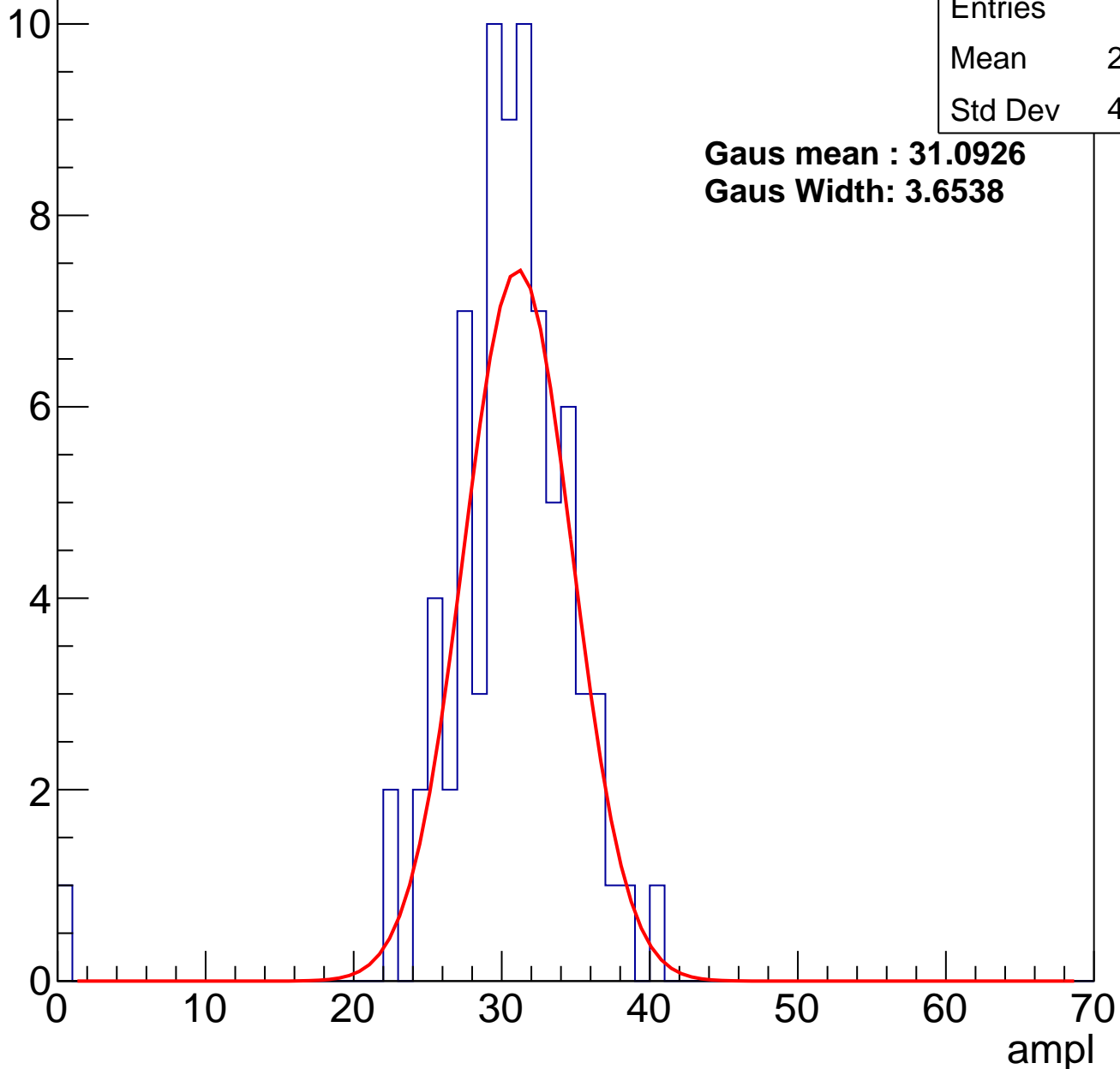
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	77
Mean	29.97
Std Dev	4.957

**Gaus mean : 31.0926**

**Gaus Width: 3.6538**

Entry



# B0L102S, U6-ch19, adc1

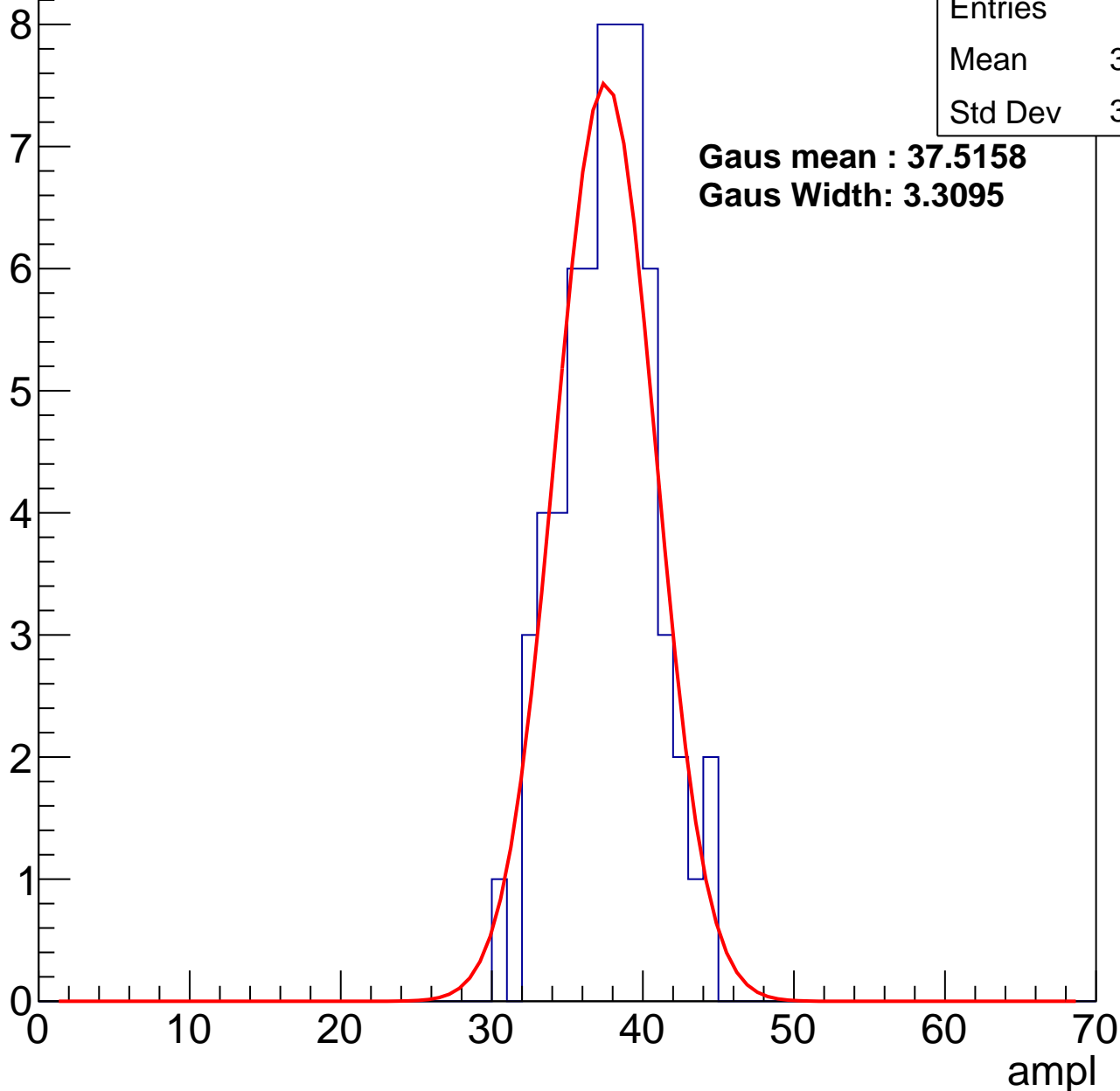
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	37.26
Std Dev	3.048

**Gaus mean : 37.5158**

**Gaus Width: 3.3095**



# B0L102S, U6-ch19, adc2

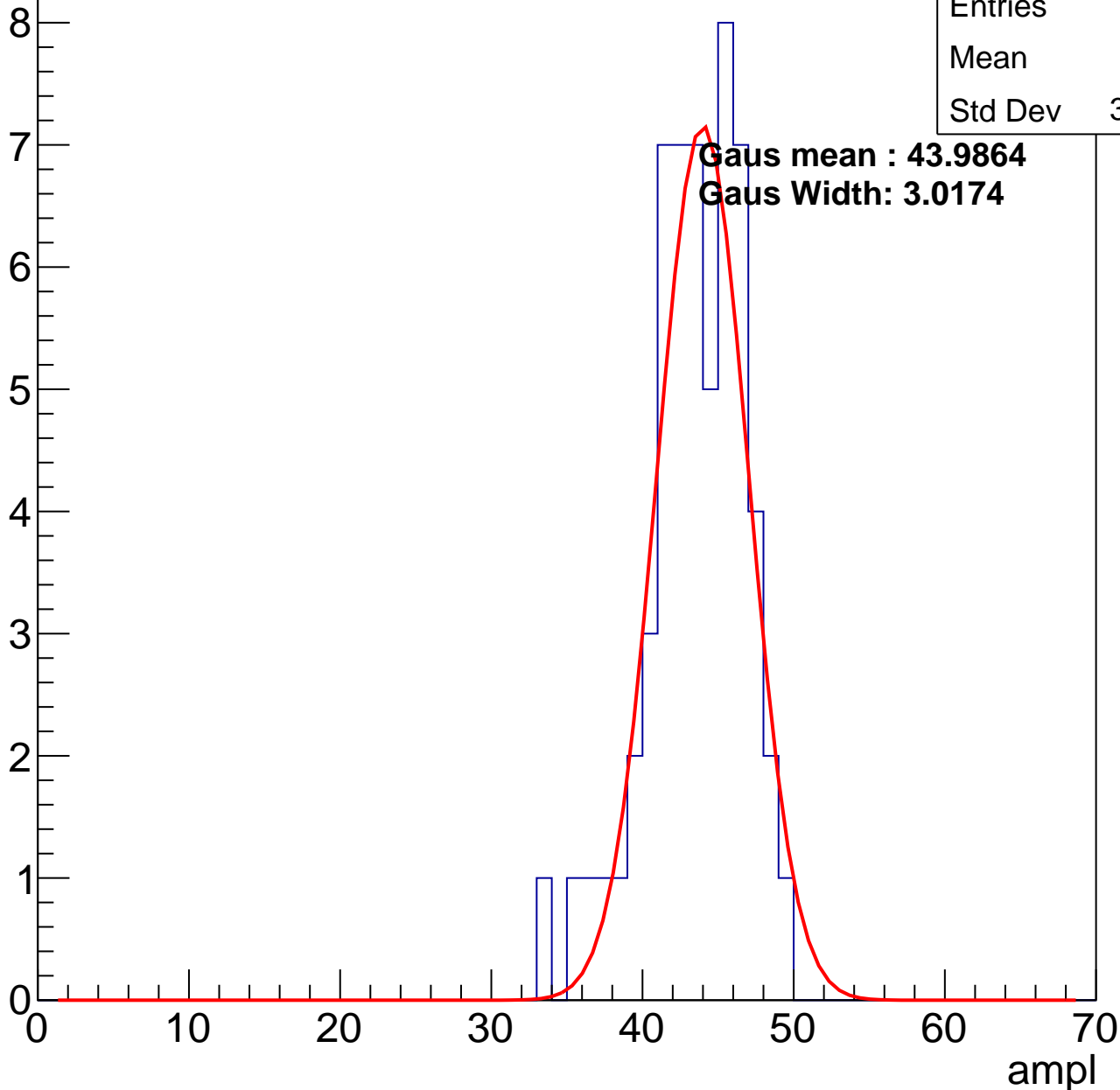
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	43
Std Dev	3.264

**Gaus mean : 43.9864**

**Gaus Width: 3.0174**

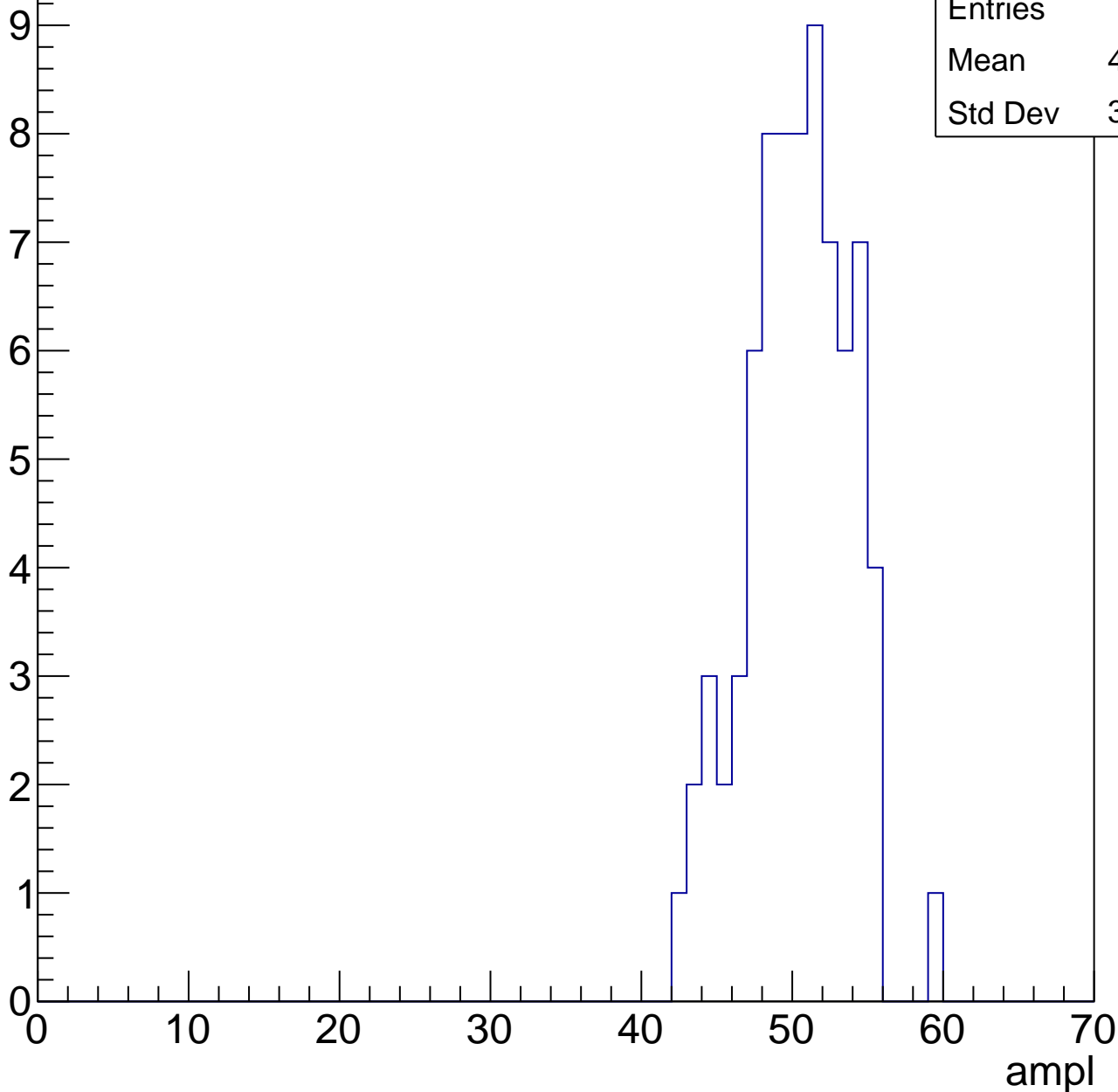


# B0L102S, U6-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	49.92
Std Dev	3.358

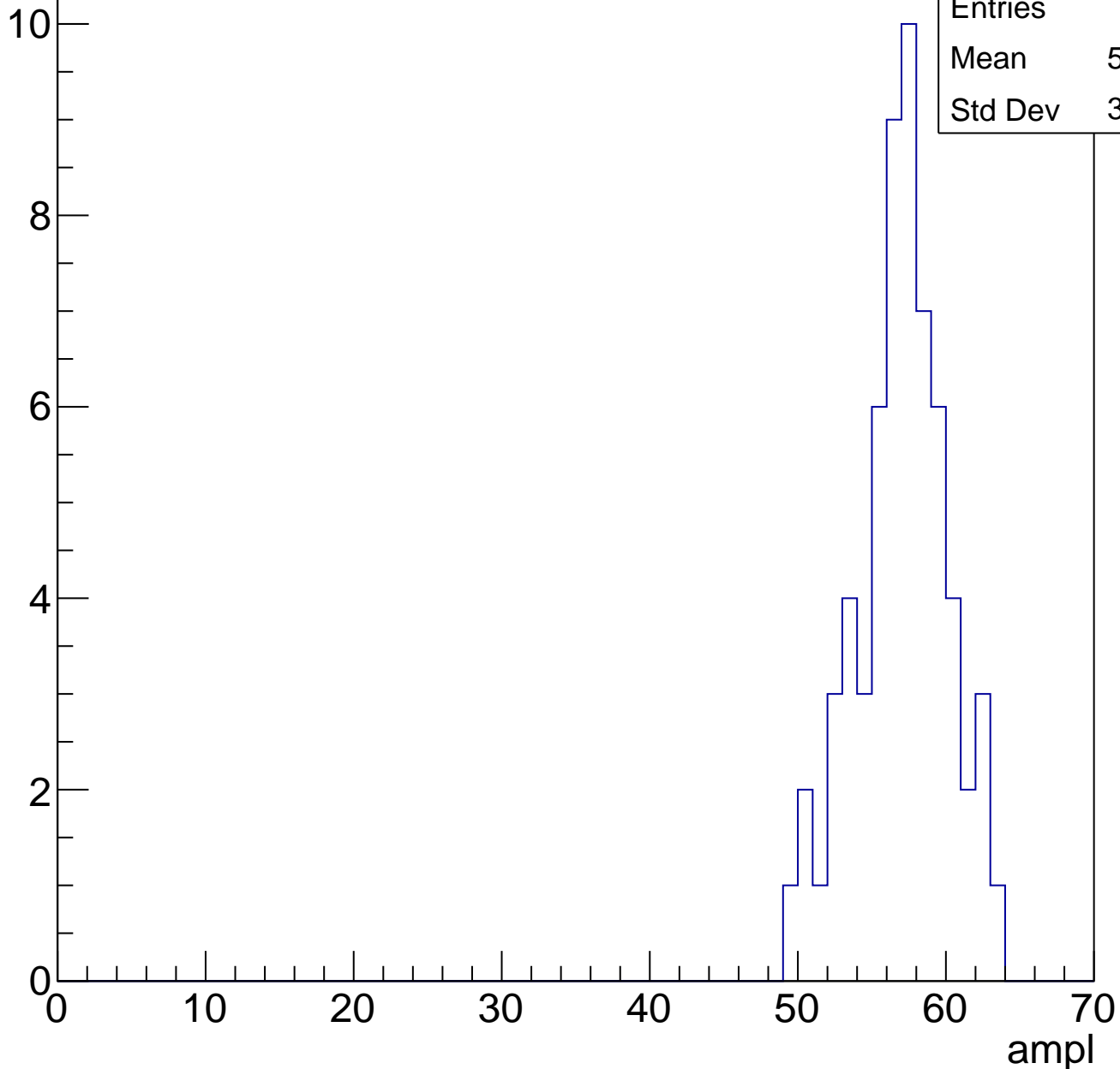


# B0L102S, U6-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	62
Mean	56.53
Std Dev	3.094

Entry



# B0L102S, U6-ch19, adc5

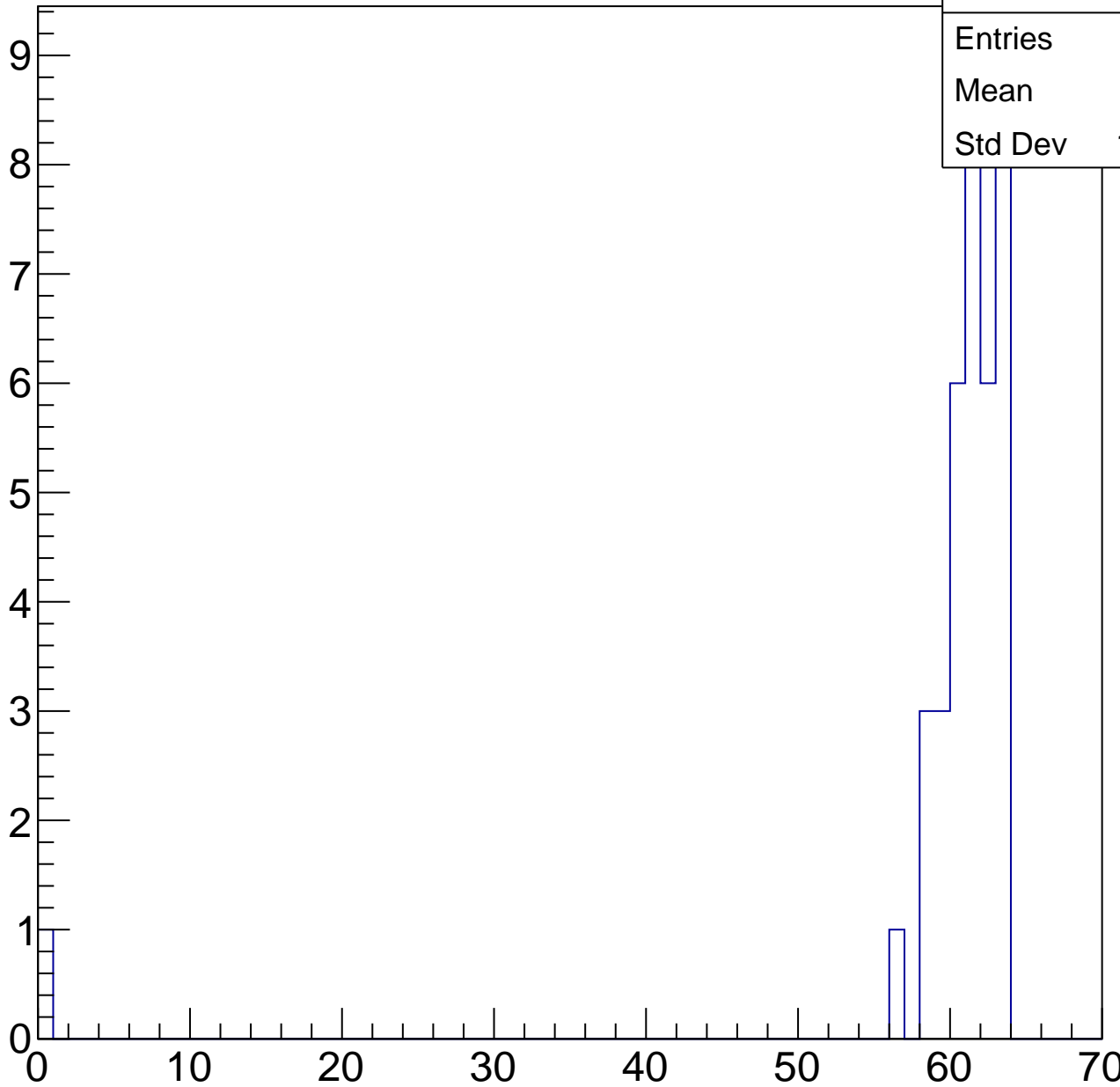
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	37
Mean	59.3
Std Dev	10.03

ampl



# B0L102S, U6-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

62

Std Dev

0



# B0L102S, U6-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch20, adc0

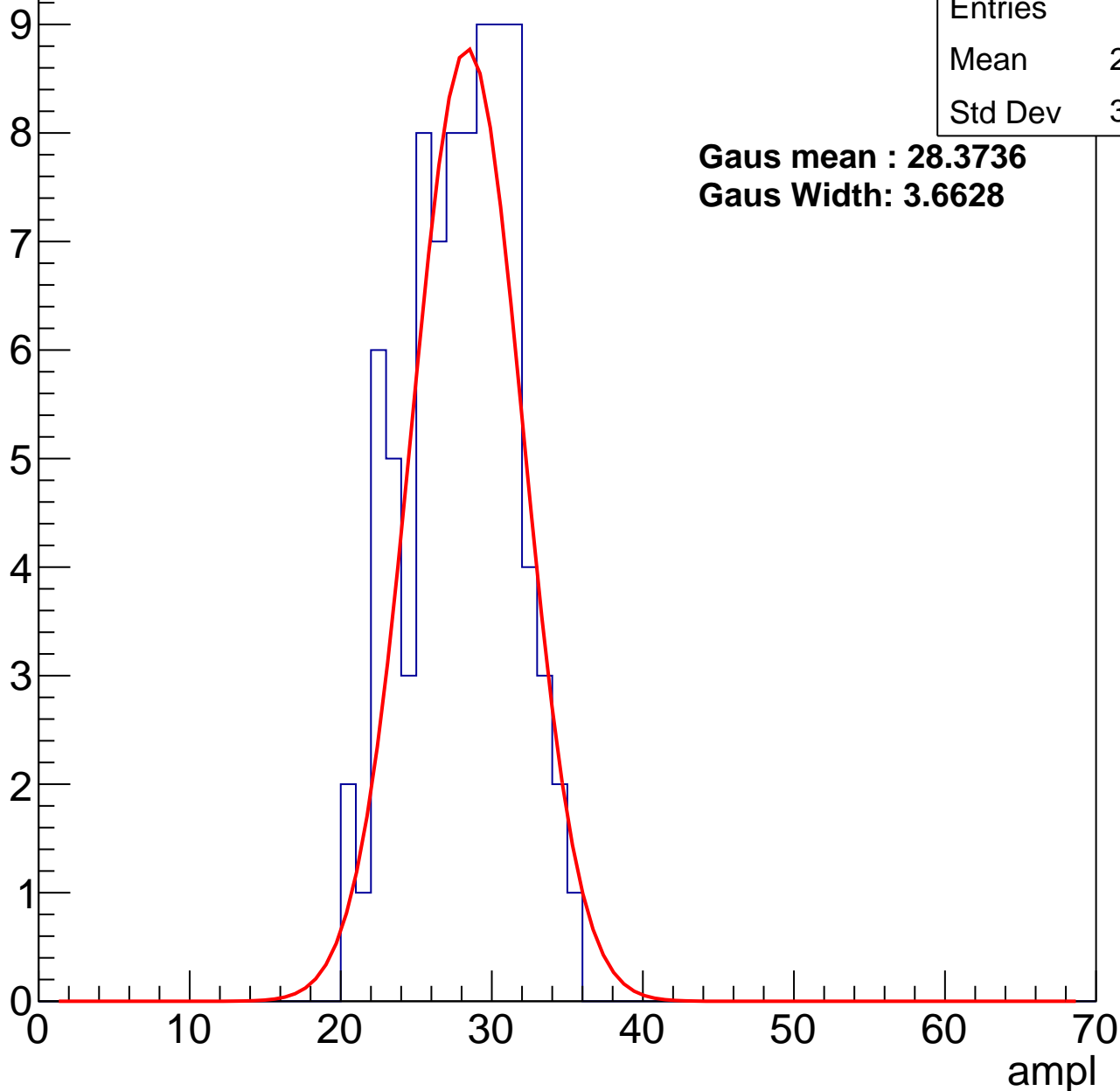
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	27.55
Std Dev	3.503

**Gaus mean : 28.3736**

**Gaus Width: 3.6628**



# B0L102S, U6-ch20, adc1

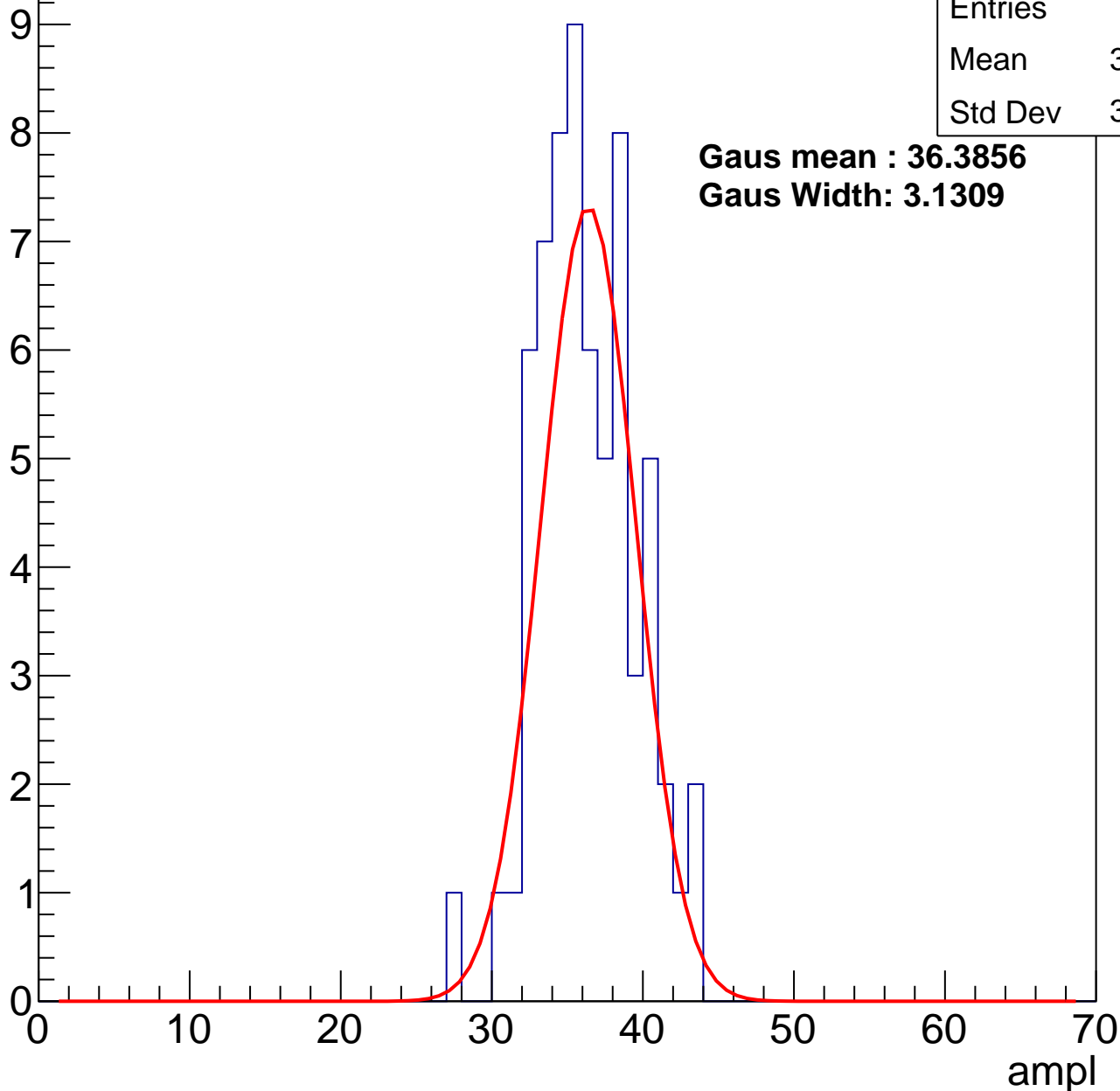
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	35.85
Std Dev	3.207

**Gaus mean : 36.3856**

**Gaus Width: 3.1309**



# B0L102S, U6-ch20, adc2

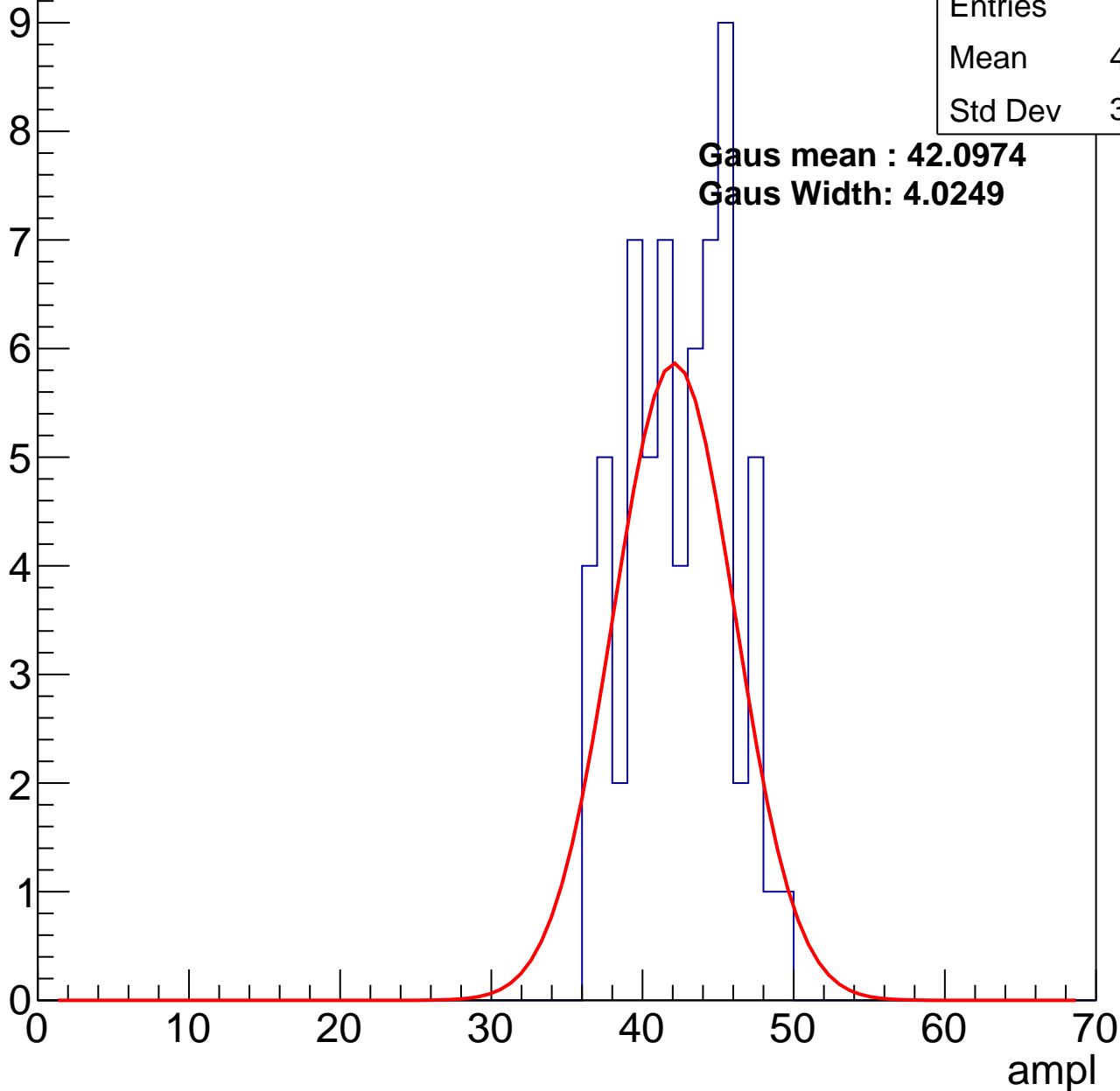
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.97
Std Dev	3.392

**Gaus mean : 42.0974**

**Gaus Width: 4.0249**

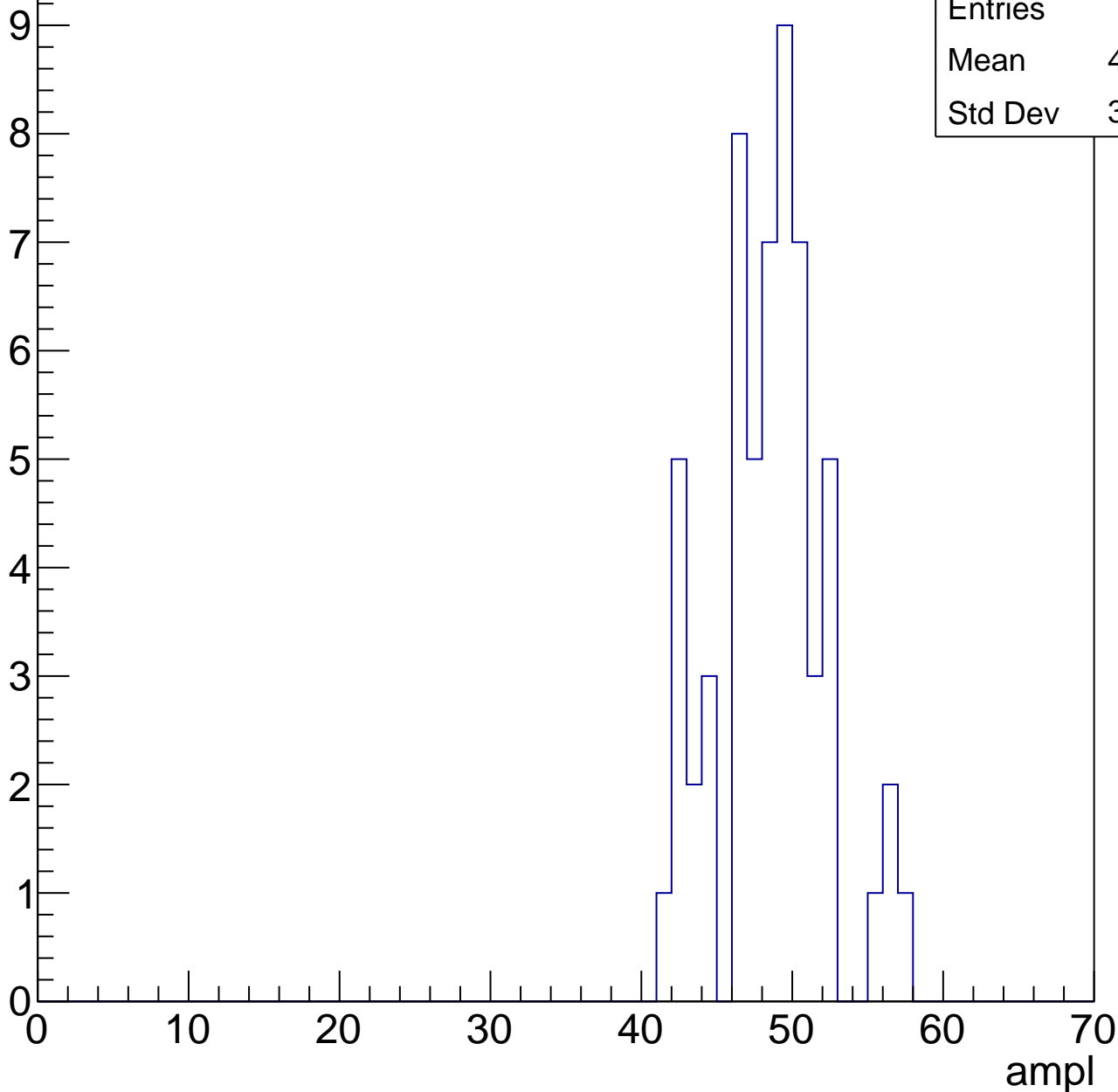


# B0L102S, U6-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

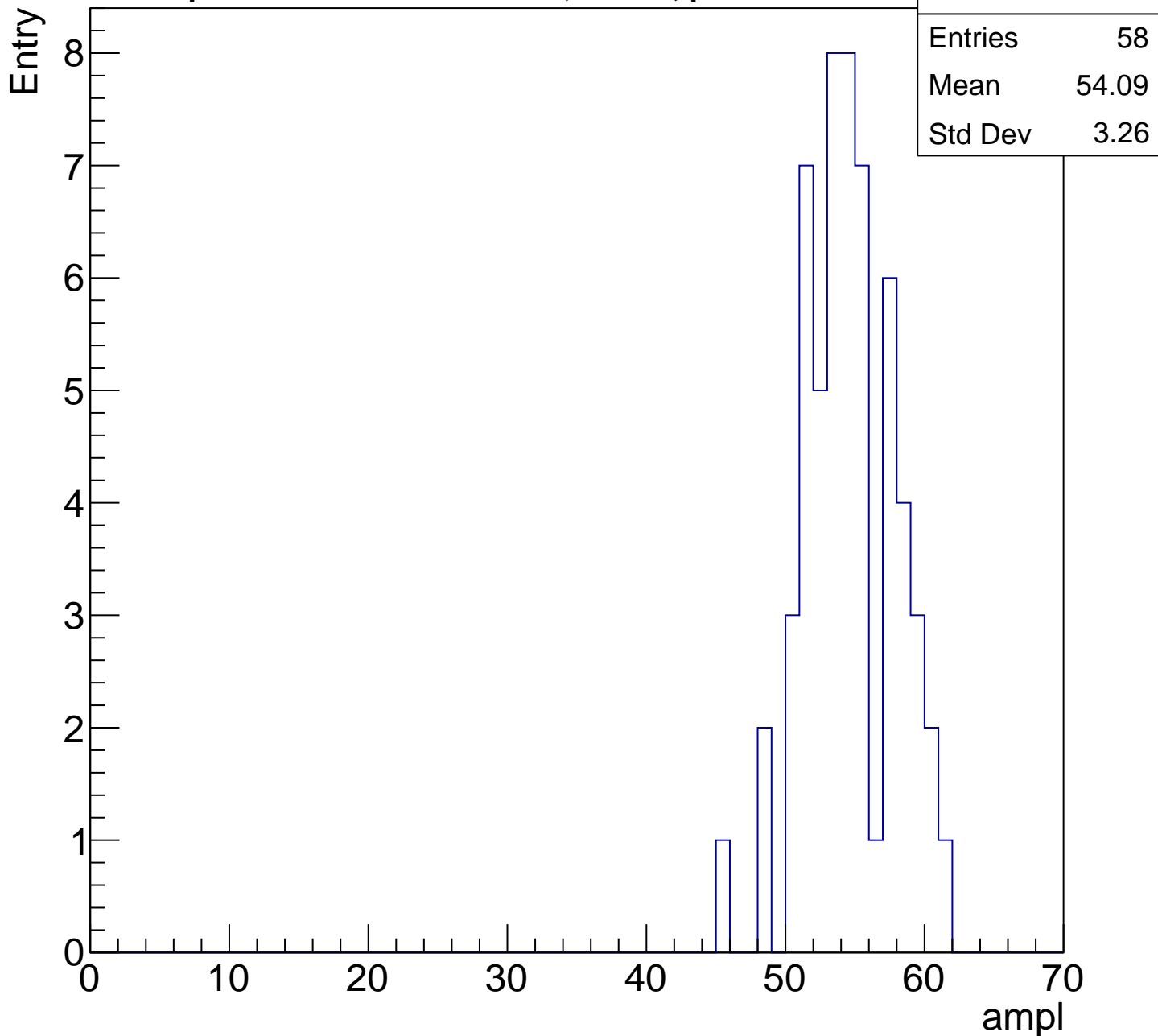
Entry

Entries	59
Mean	48.07
Std Dev	3.603



# B0L102S, U6-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

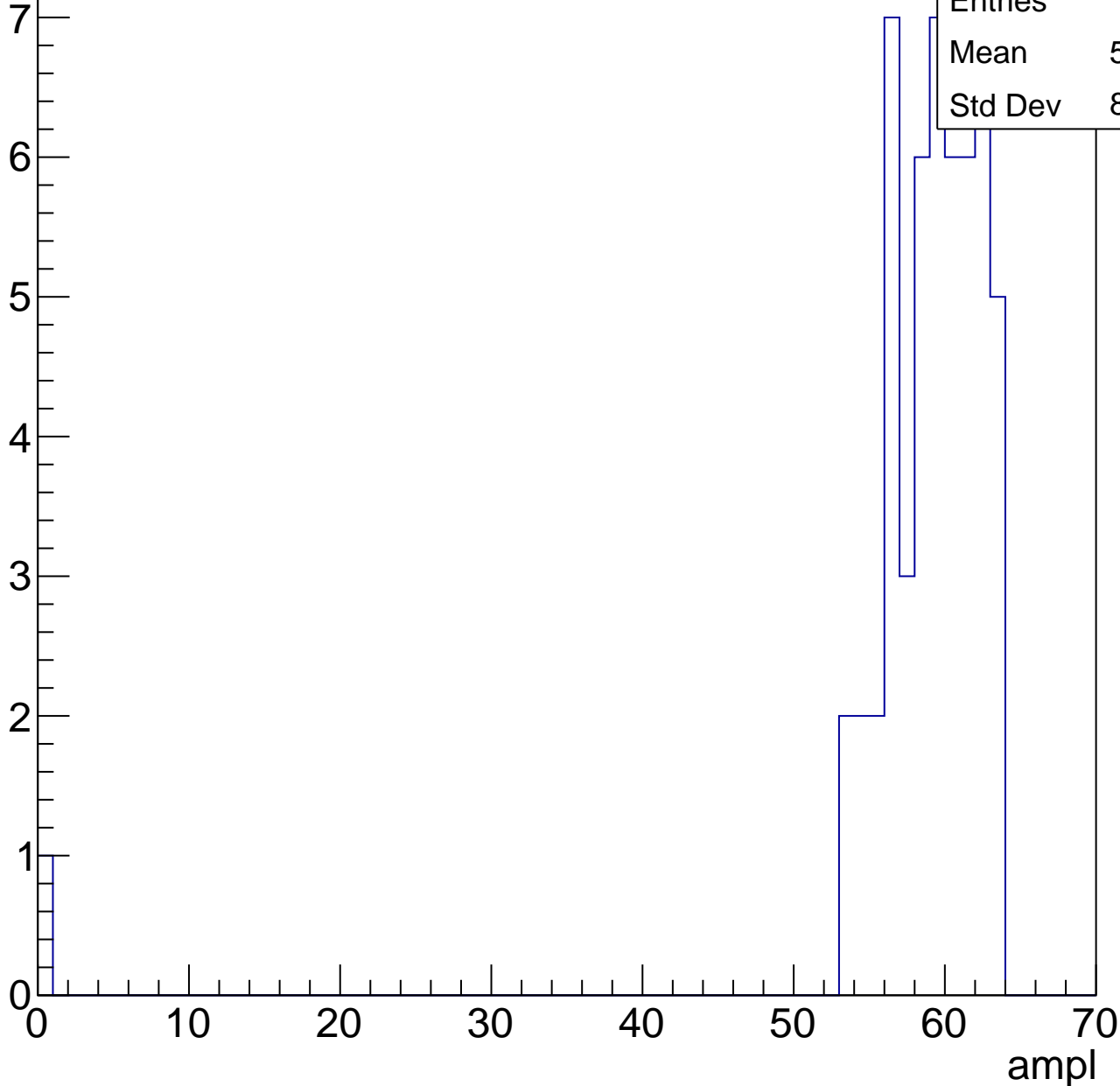


# B0L102S, U6-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	57.83
Std Dev	8.406

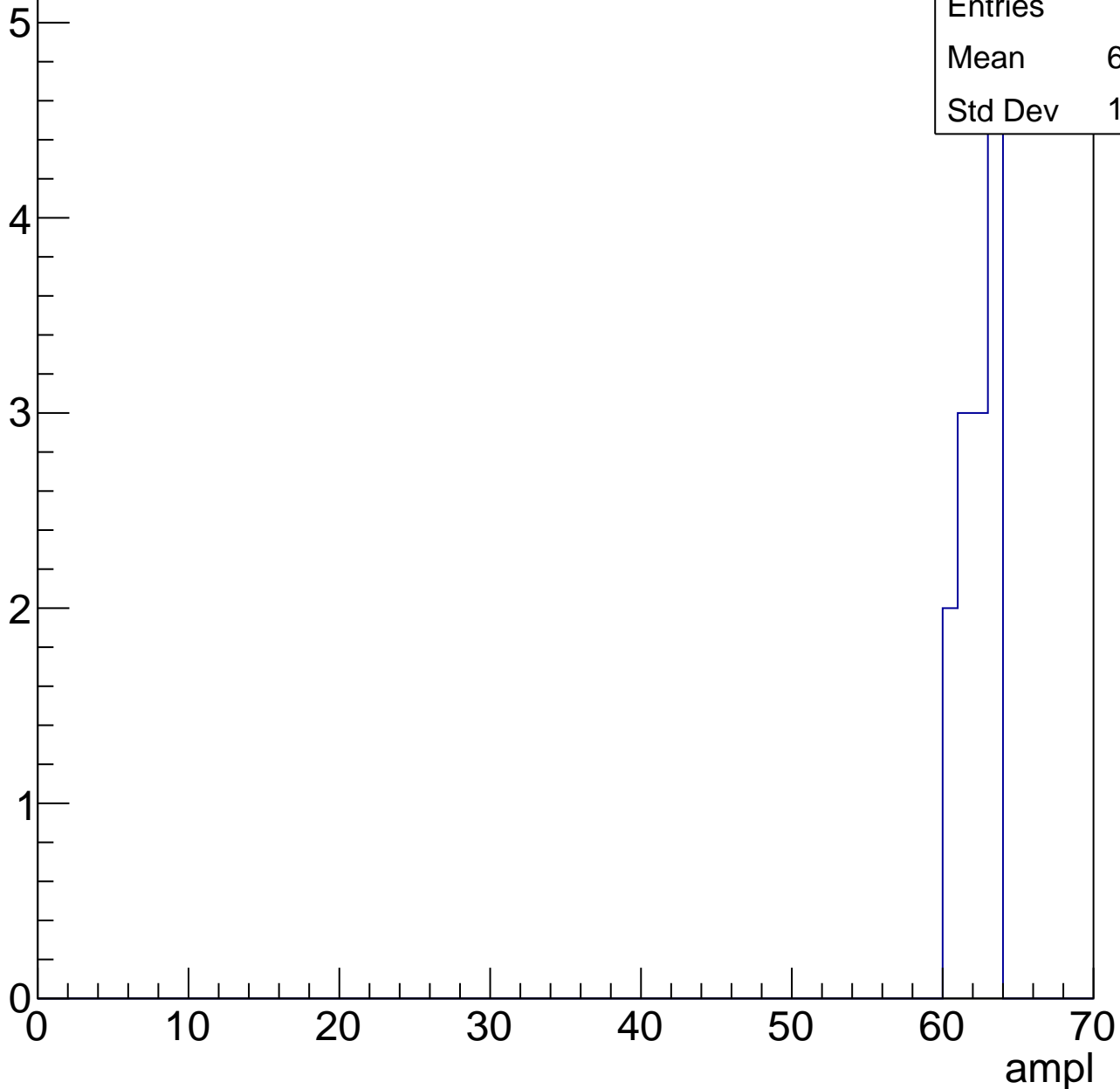


# B0L102S, U6-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	61.85
Std Dev	1.099





# B0L102S, U6-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch21, adc0

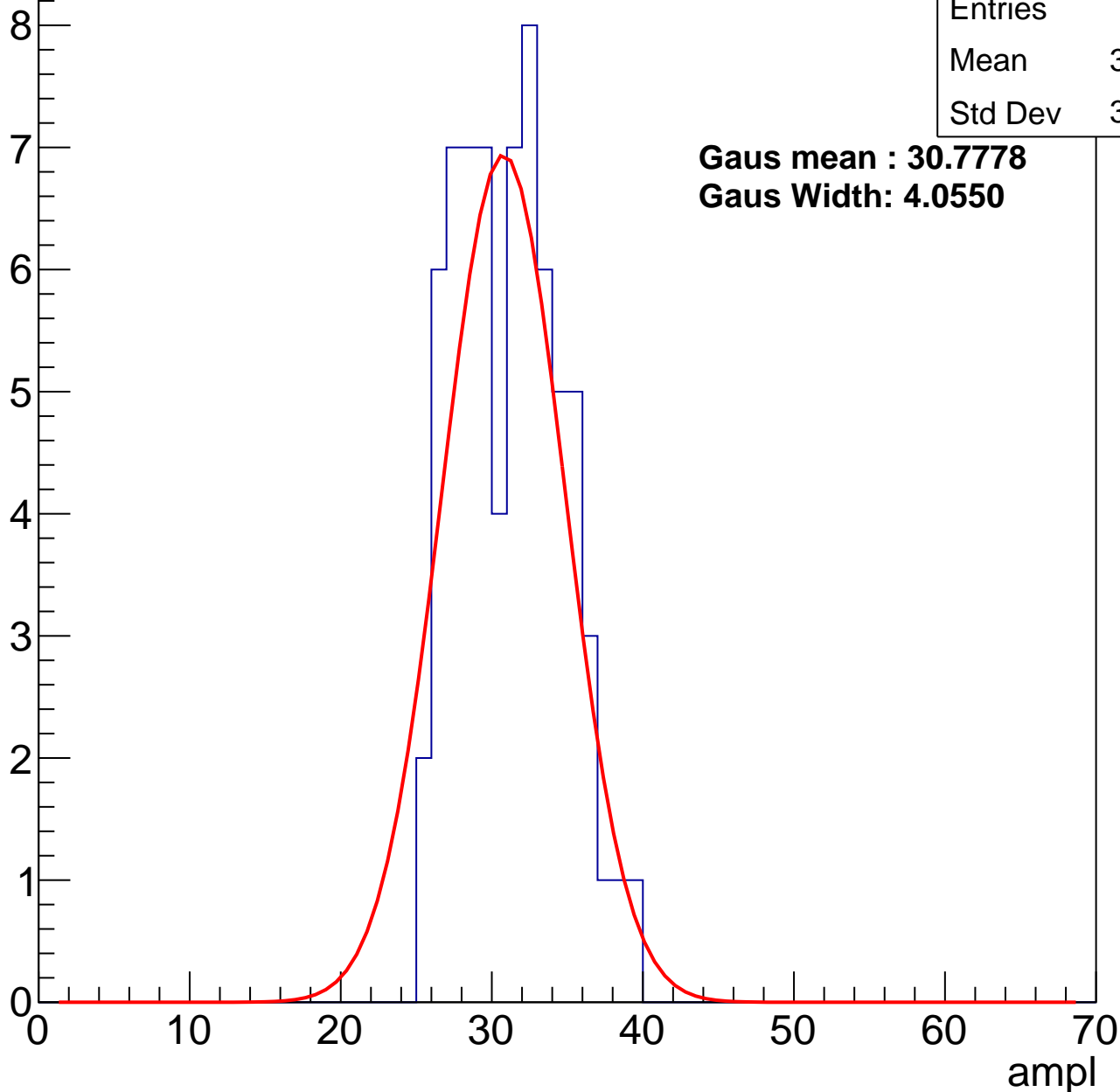
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	30.74
Std Dev	3.392

**Gaus mean : 30.7778**

**Gaus Width: 4.0550**



# B0L102S, U6-ch21, adc1

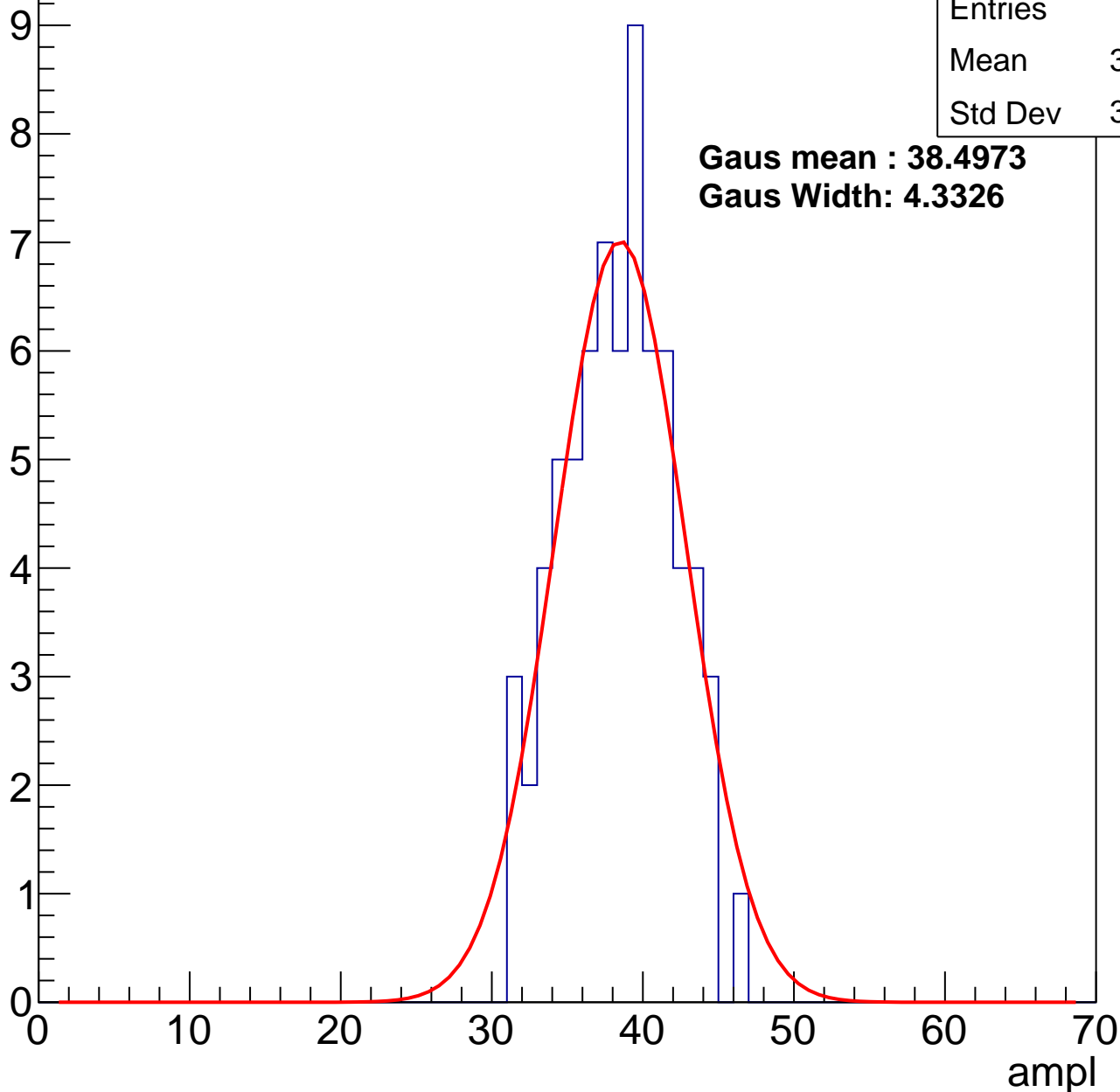
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	37.92
Std Dev	3.555

**Gaus mean : 38.4973**

**Gaus Width: 4.3326**



# B0L102S, U6-ch21, adc2

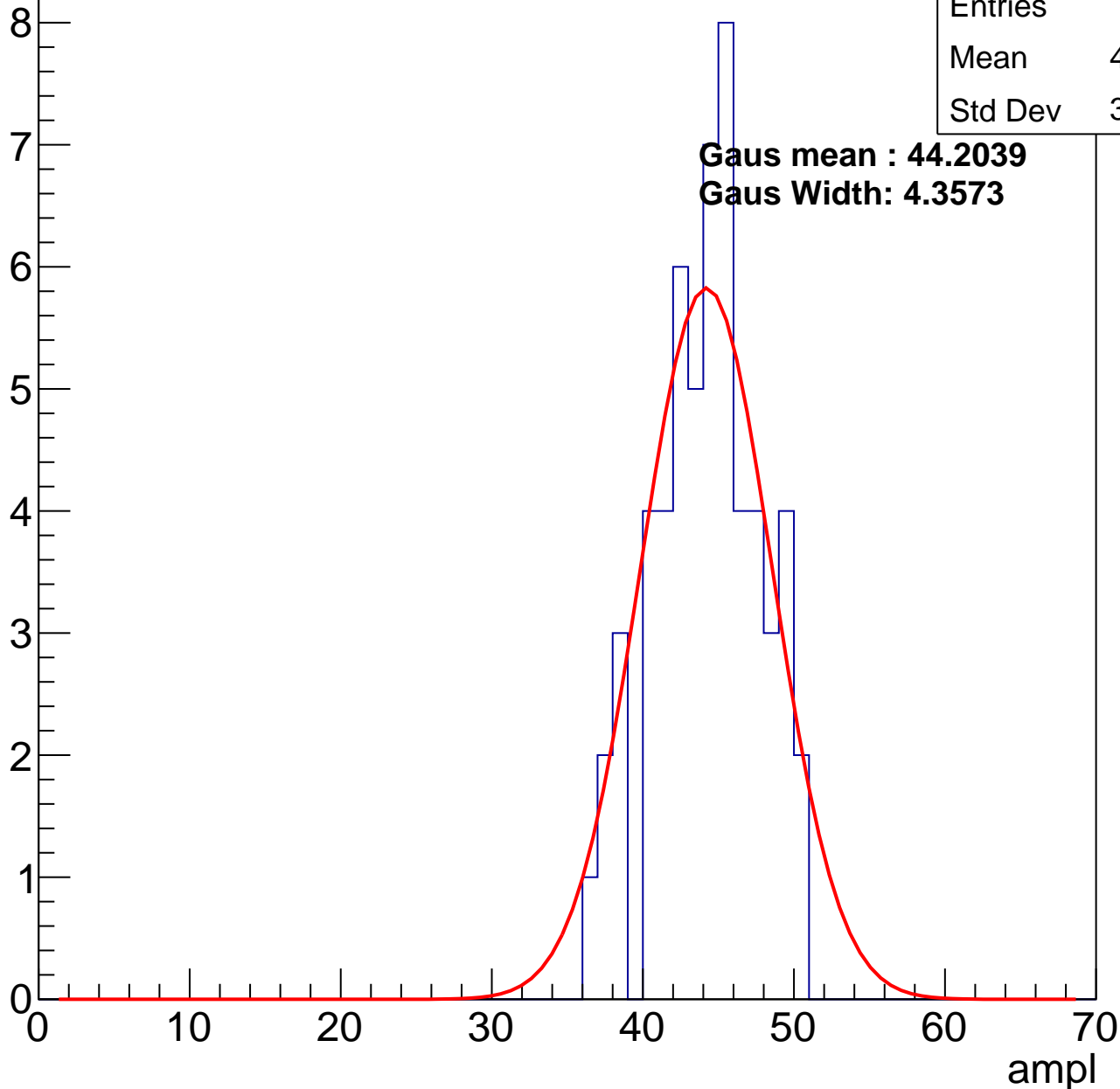
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	43.77
Std Dev	3.444

**Gaus mean : 44.2039**

**Gaus Width: 4.3573**

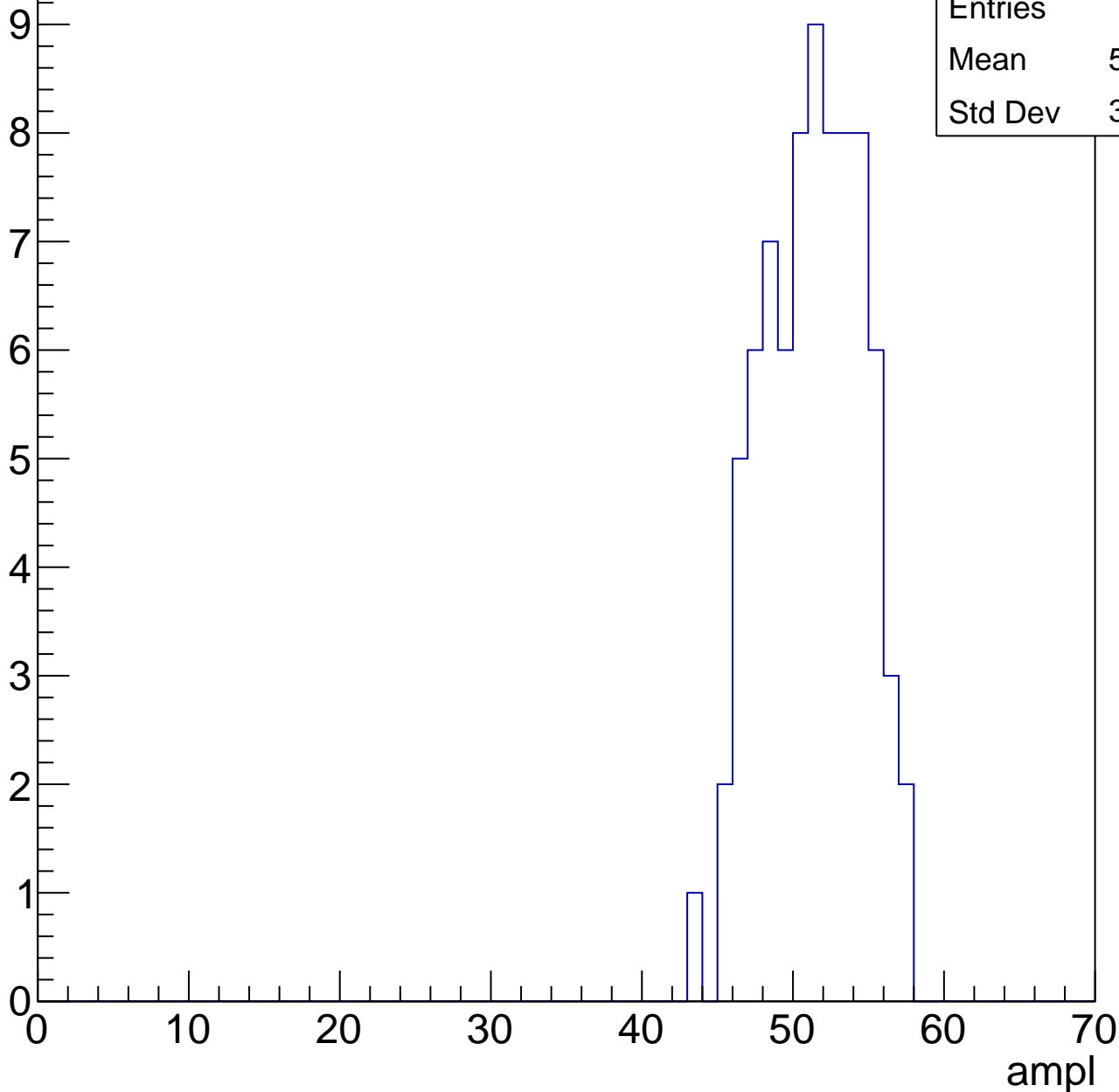


# B0L102S, U6-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	50.86
Std Dev	3.193



# B0L102S, U6-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 44

Mean 57.3

Std Dev 2.676

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

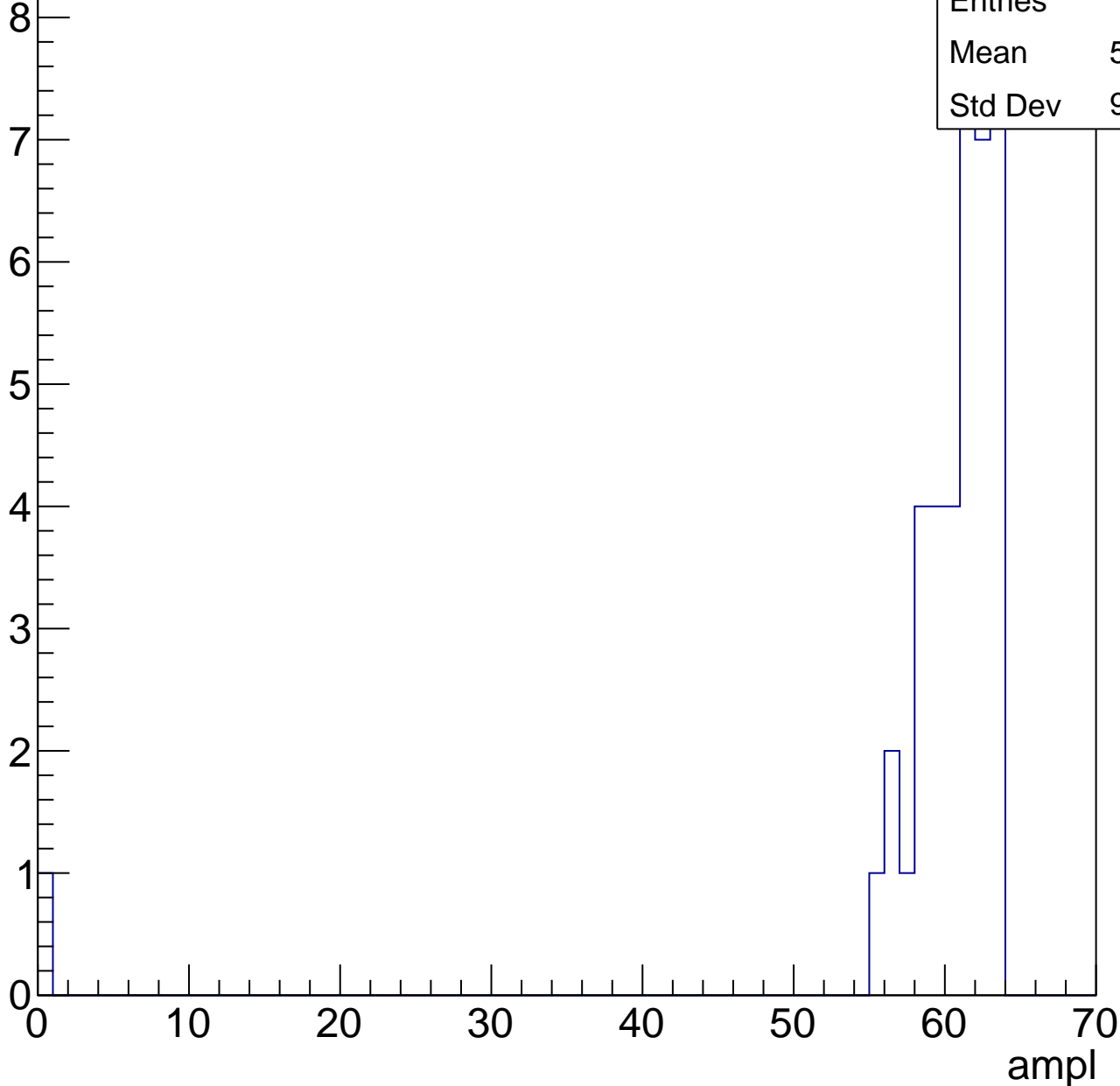
ampl

# B0L102S, U6-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	58.95
Std Dev	9.682



# B0L102S, U6-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch22, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	27.74
Std Dev	5.671

**Gaus mean : 29.0289**

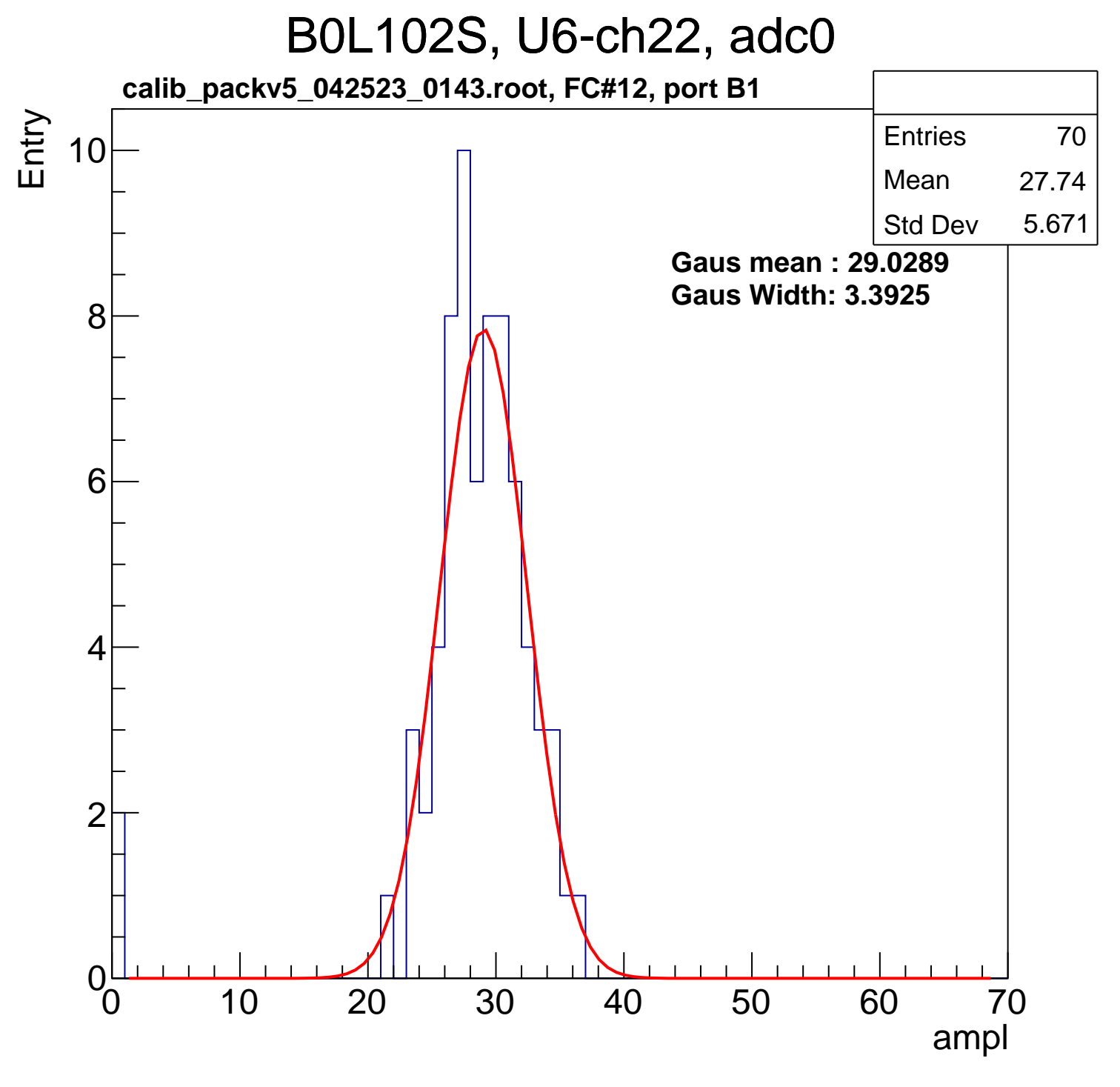
**Gaus Width: 3.3925**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch22, adc1

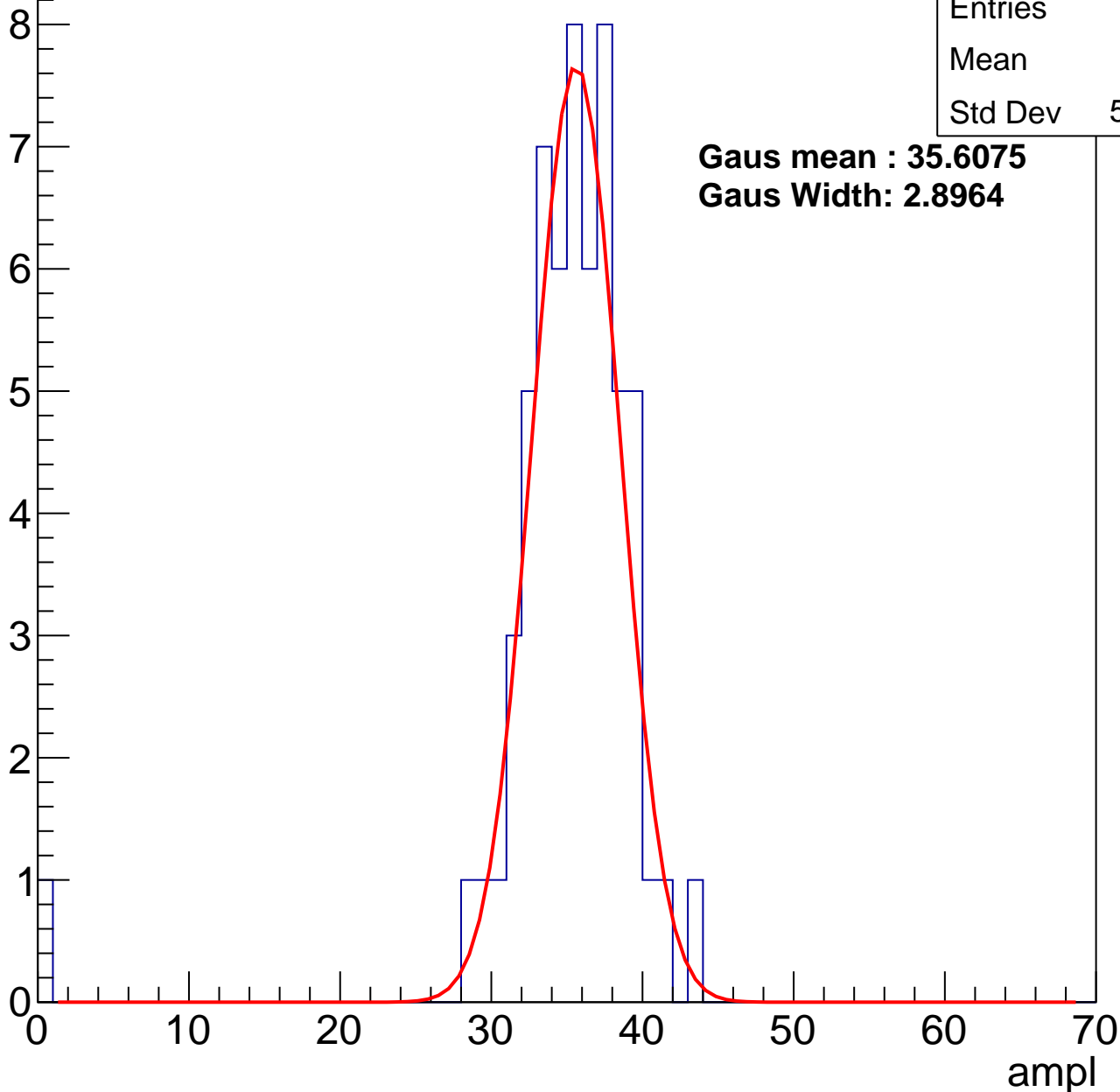
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	34.6
Std Dev	5.386

**Gaus mean : 35.6075**

**Gaus Width: 2.8964**



# B0L102S, U6-ch22, adc2

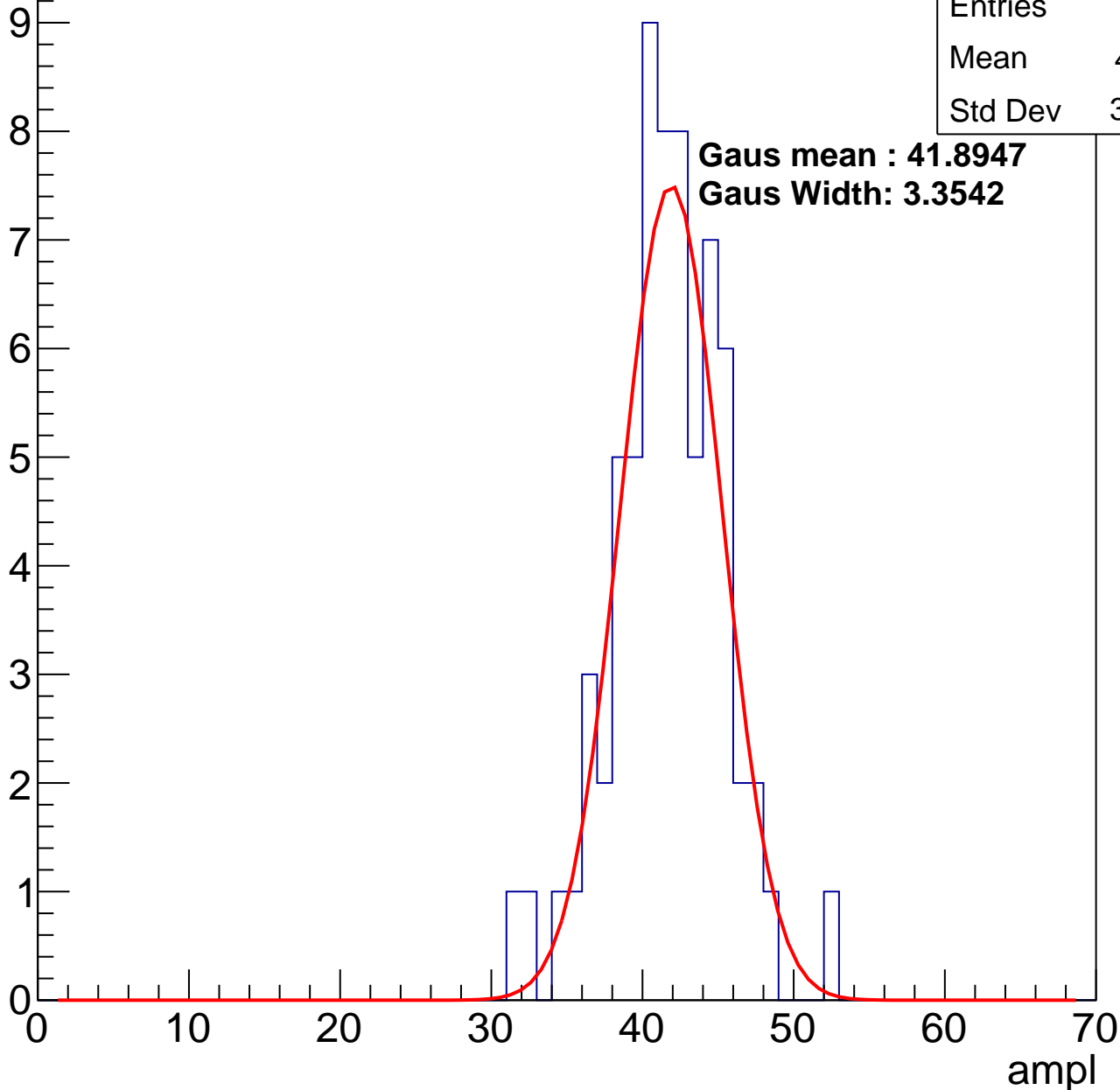
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	41.21
Std Dev	3.684

**Gaus mean : 41.8947**

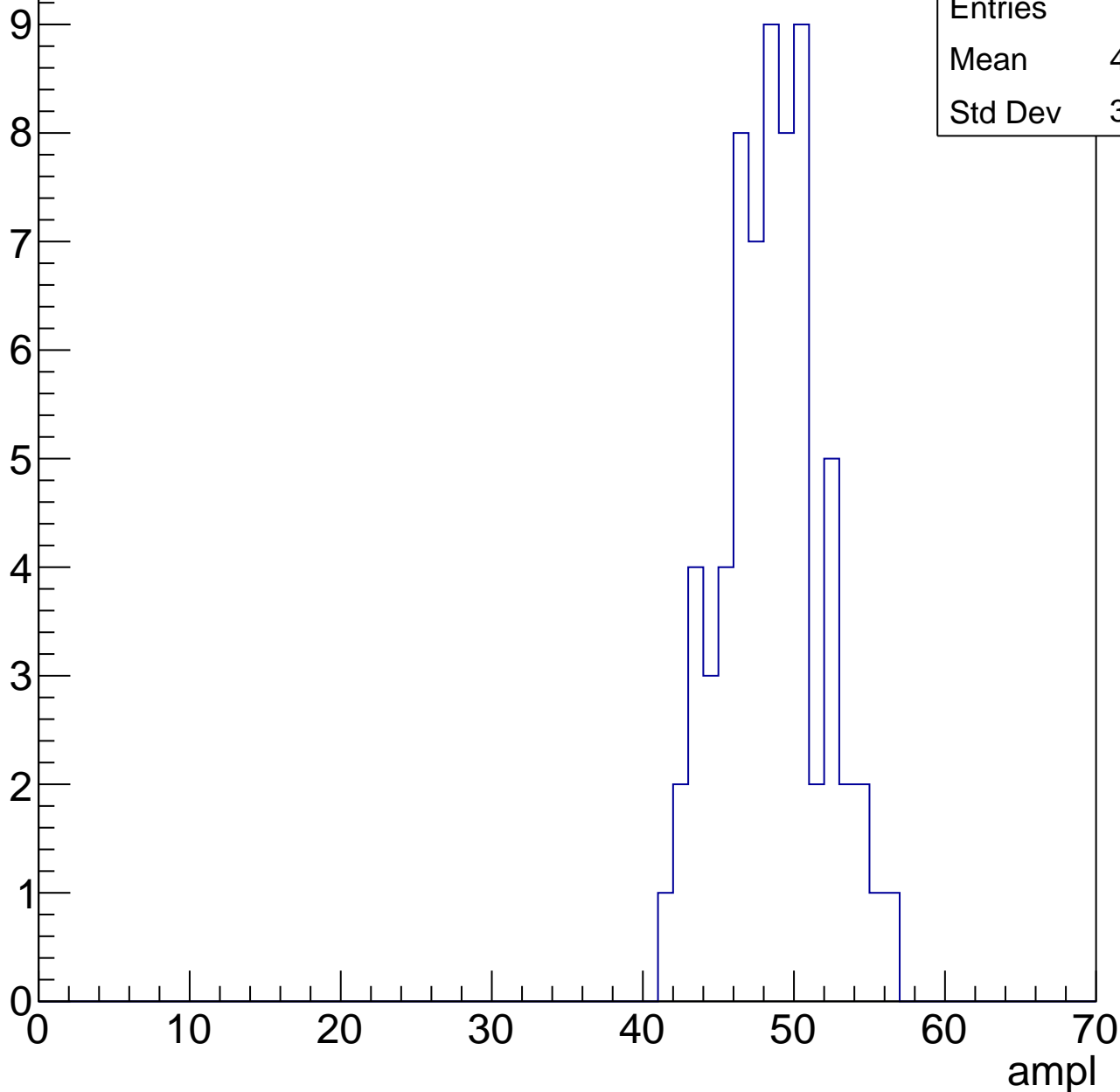
**Gaus Width: 3.3542**



# B0L102S, U6-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



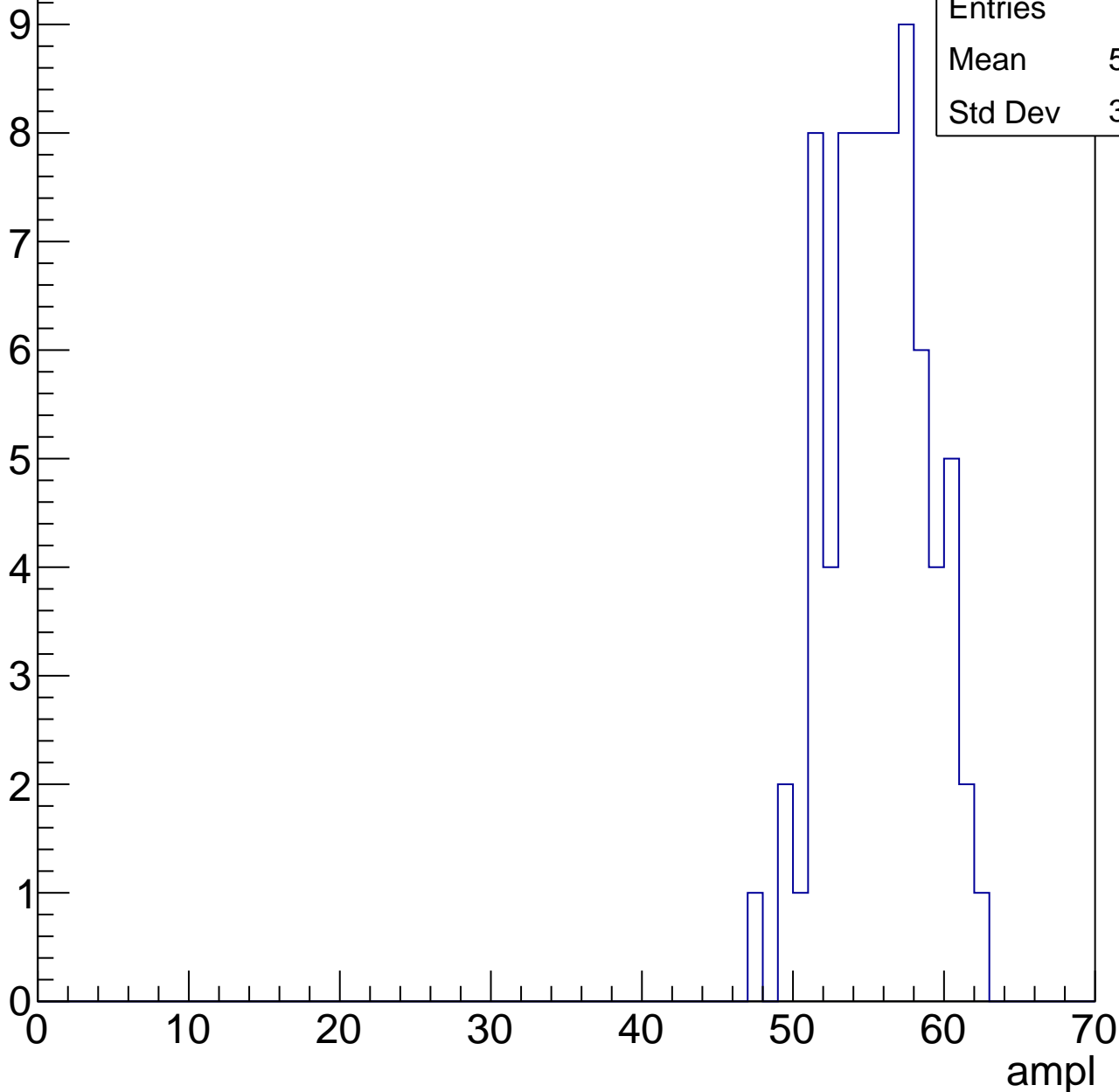
Entries	68
Mean	48.04
Std Dev	3.256

# B0L102S, U6-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	55.15
Std Dev	3.207

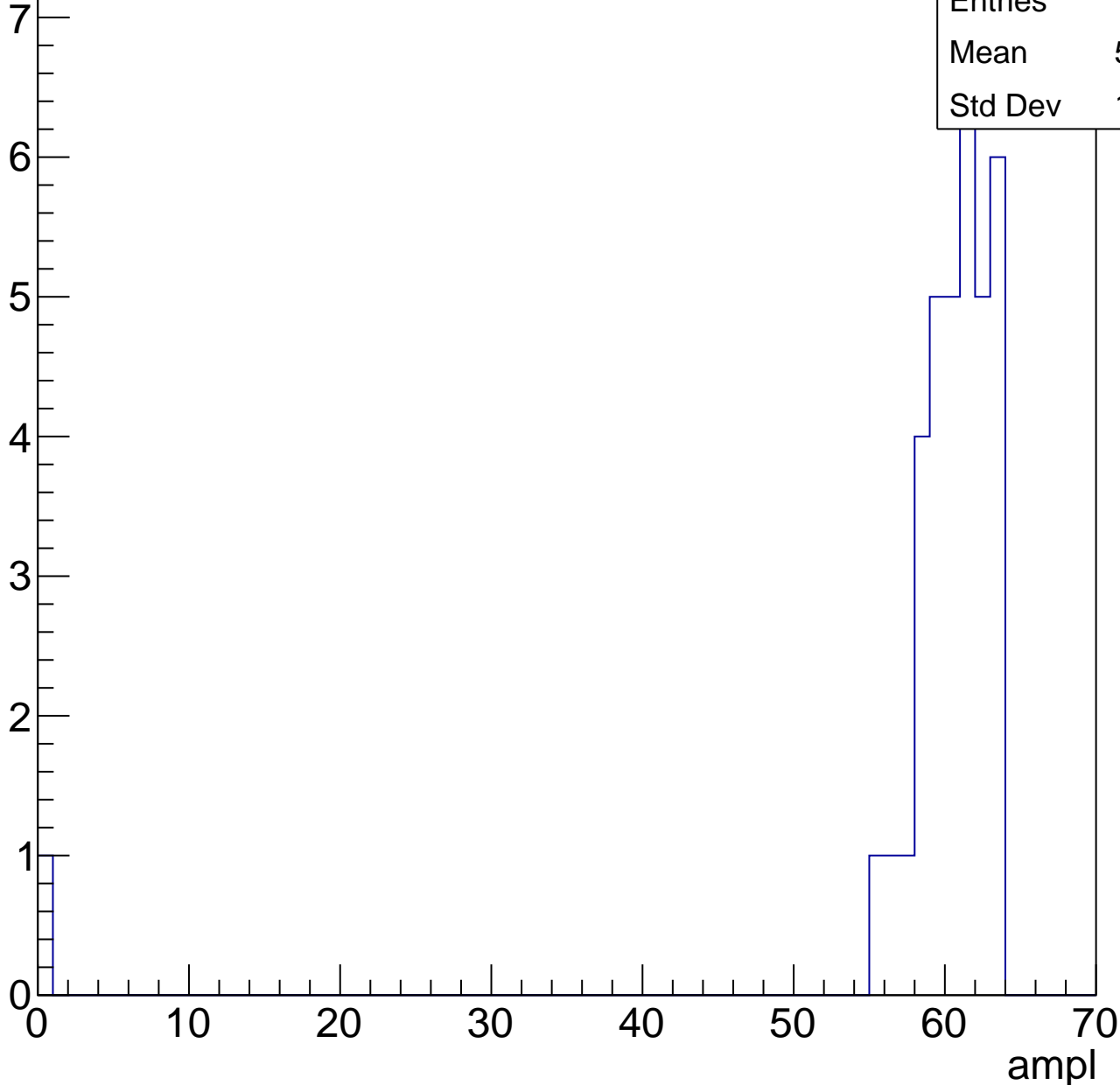


# B0L102S, U6-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

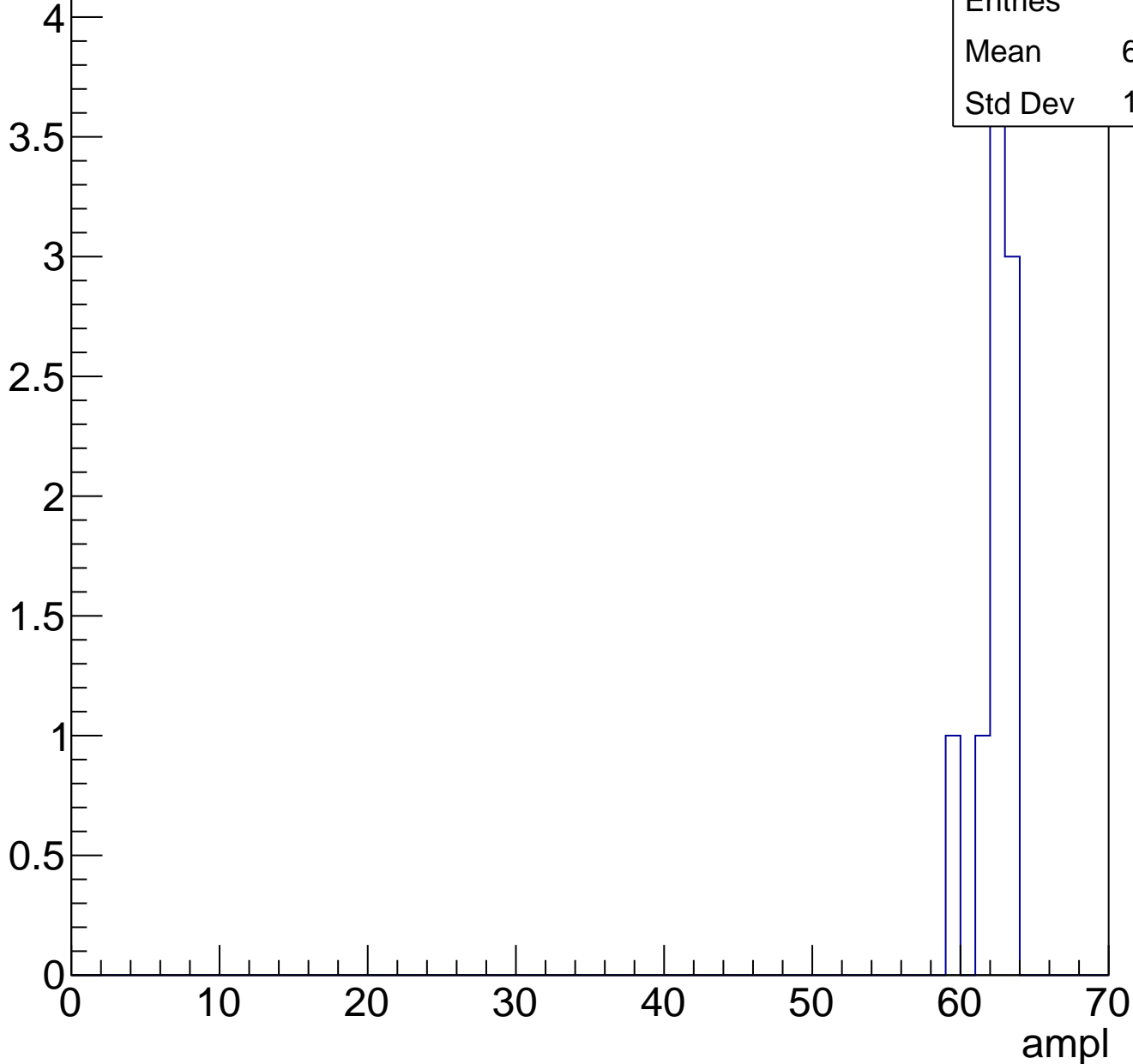
Entries	36
Mean	58.61
Std Dev	10.11



# B0L102S, U6-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch23, adc0

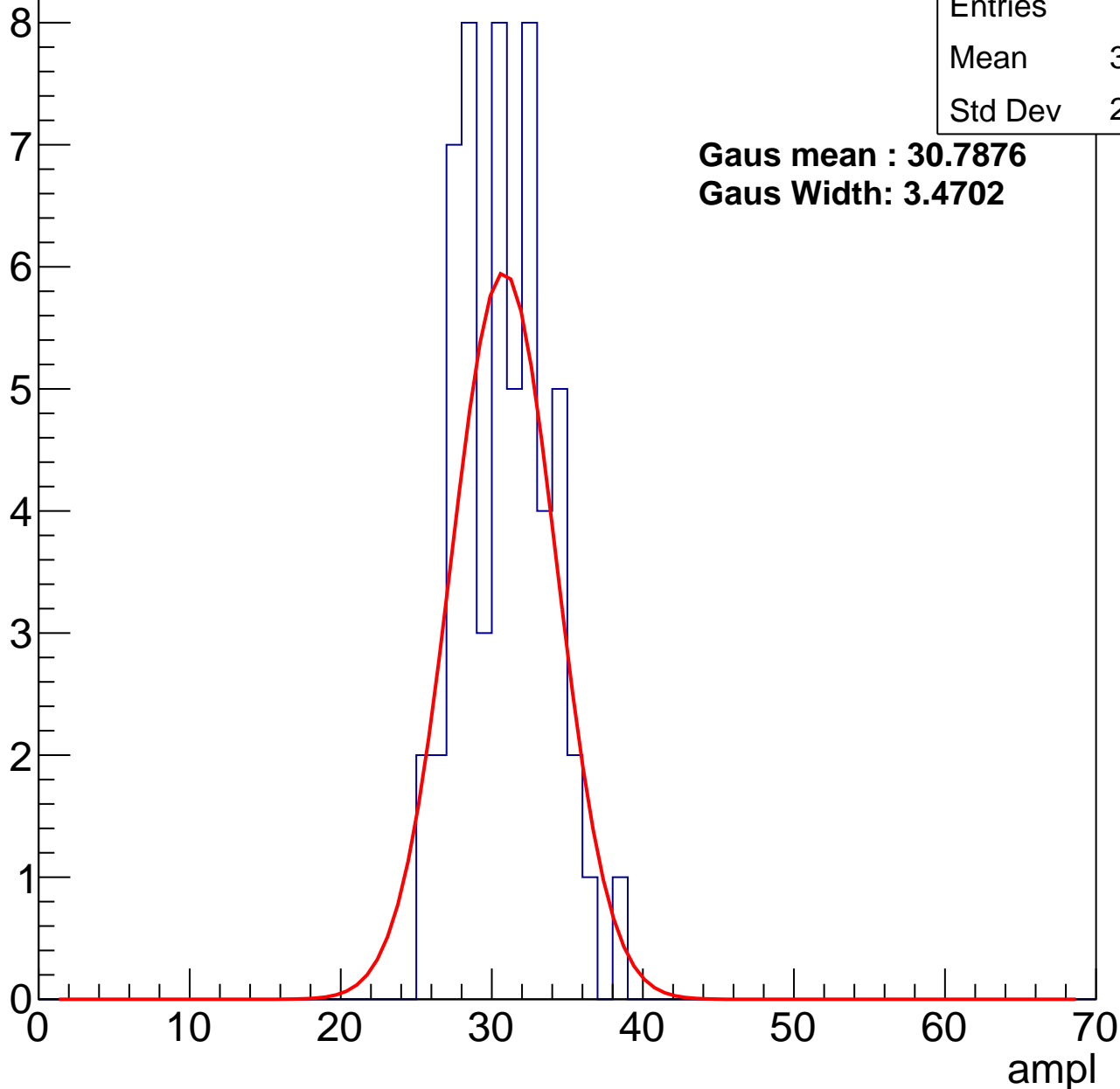
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	30.34
Std Dev	2.917

**Gaus mean : 30.7876**

**Gaus Width: 3.4702**



# B0L102S, U6-ch23, adc1

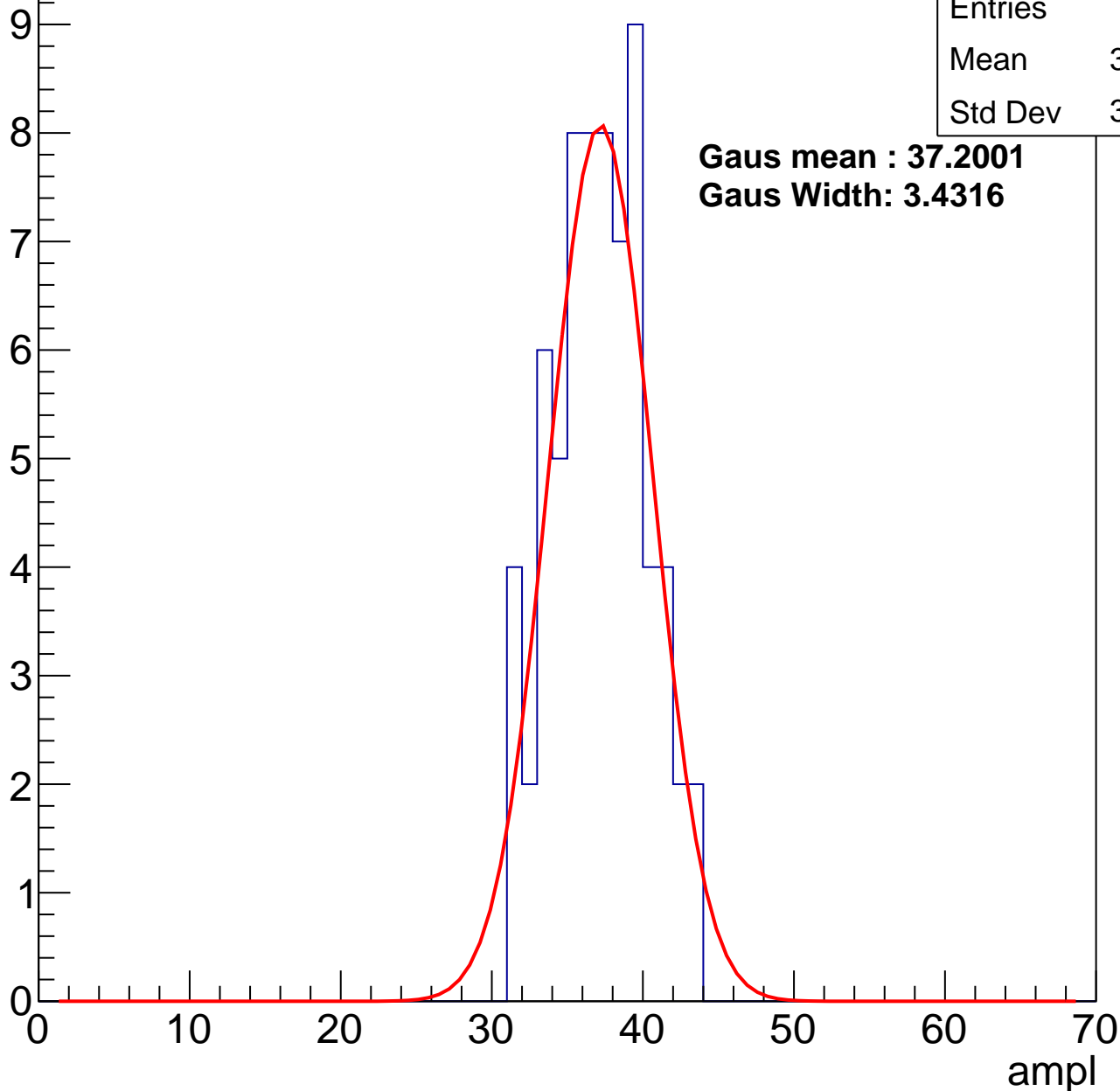
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	36.68
Std Dev	3.029

**Gaus mean : 37.2001**

**Gaus Width: 3.4316**



# B0L102S, U6-ch23, adc2

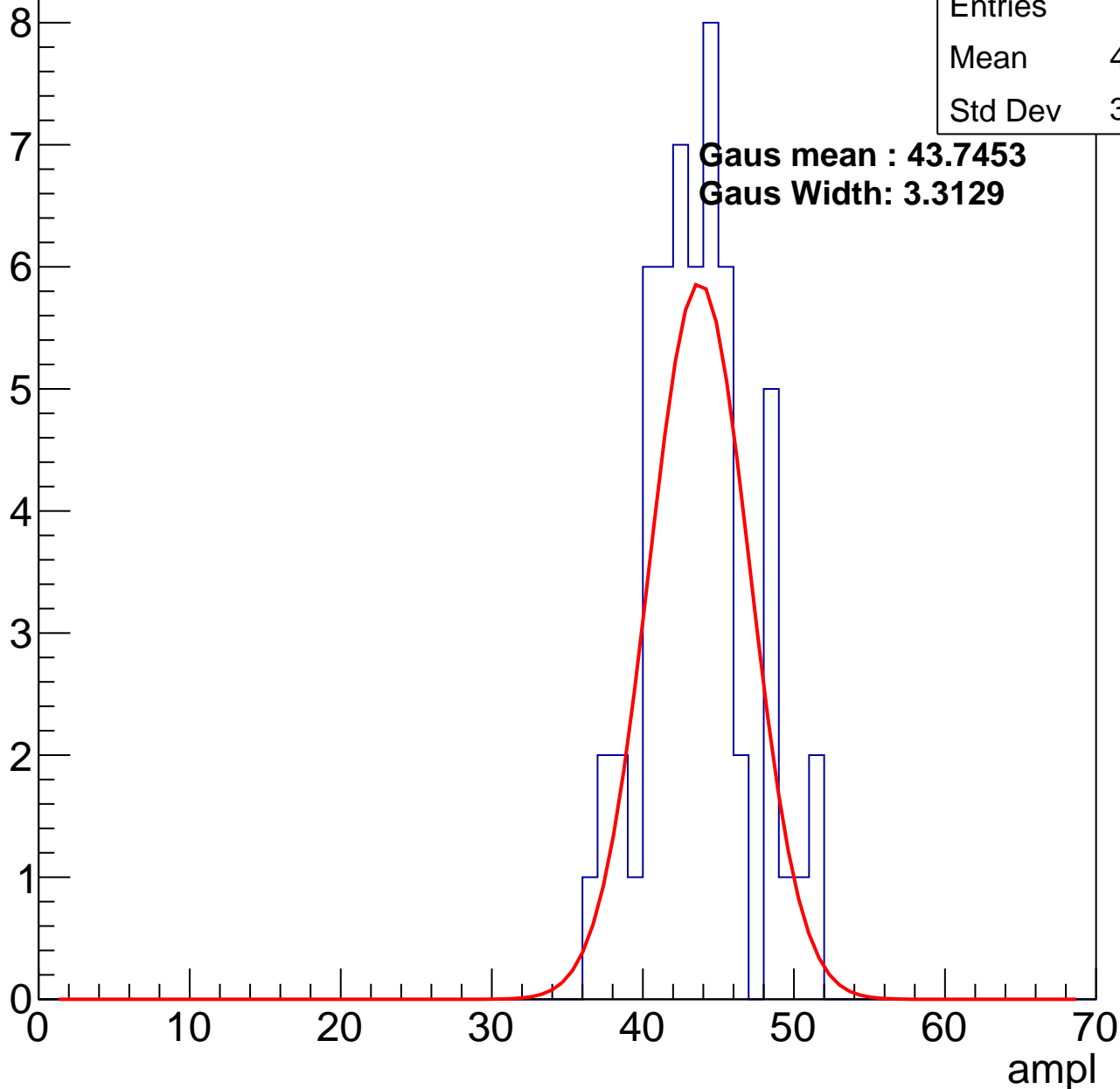
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	43.18
Std Dev	3.428

**Gaus mean : 43.7453**

**Gaus Width: 3.3129**



# B0L102S, U6-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

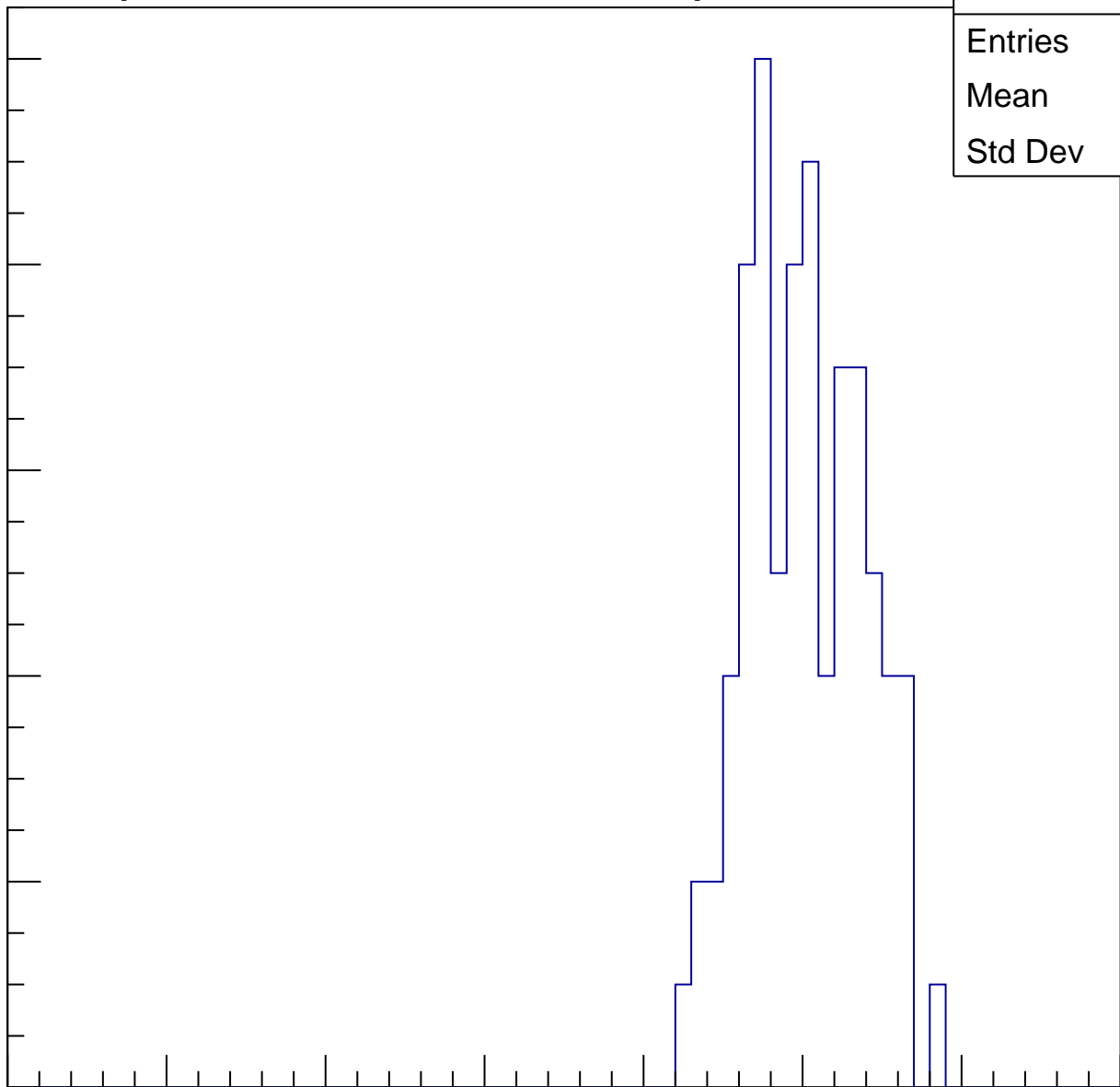
Entries	81
Mean	49.72
Std Dev	3.611

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

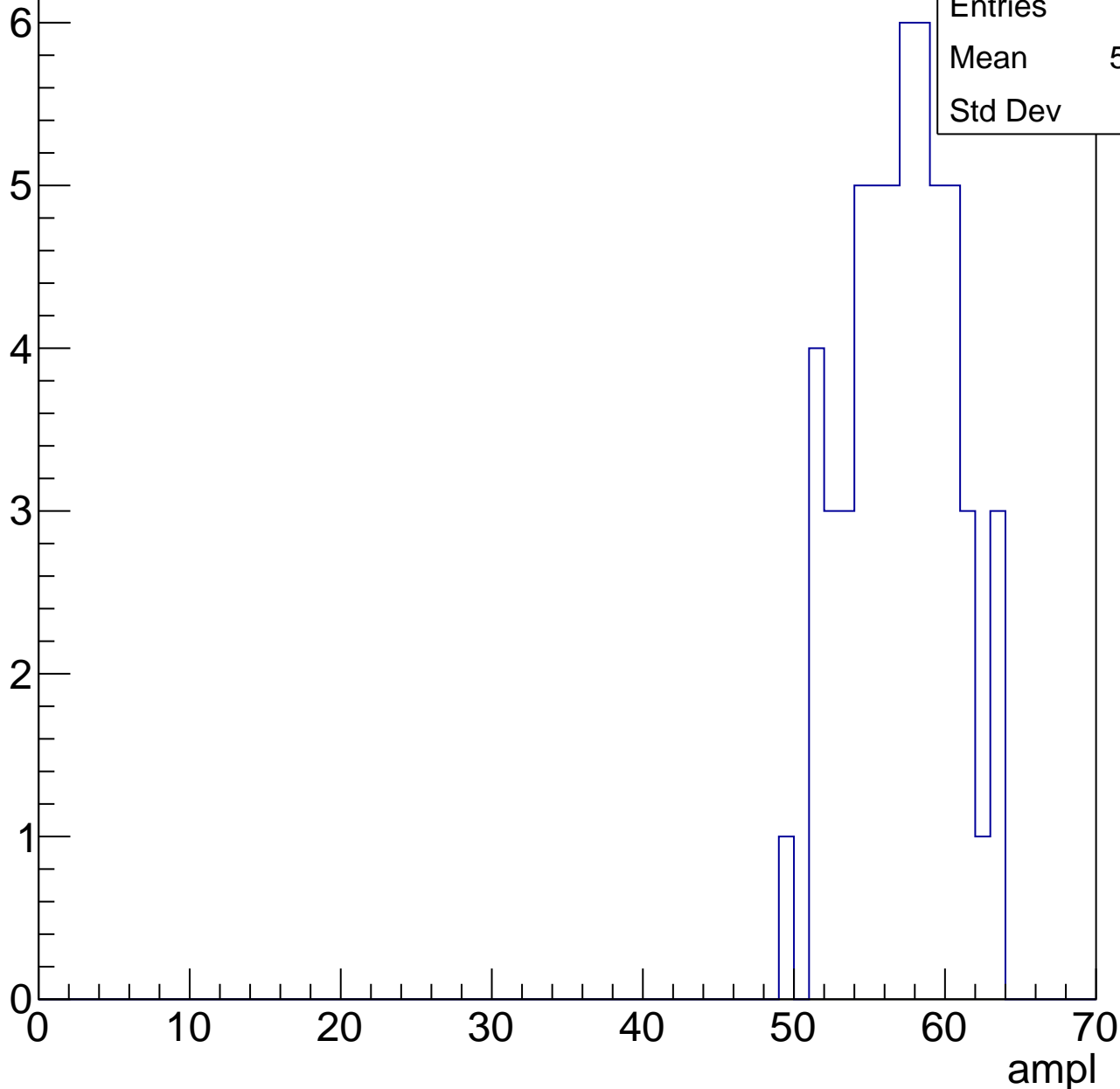


# B0L102S, U6-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	56.58
Std Dev	3.42

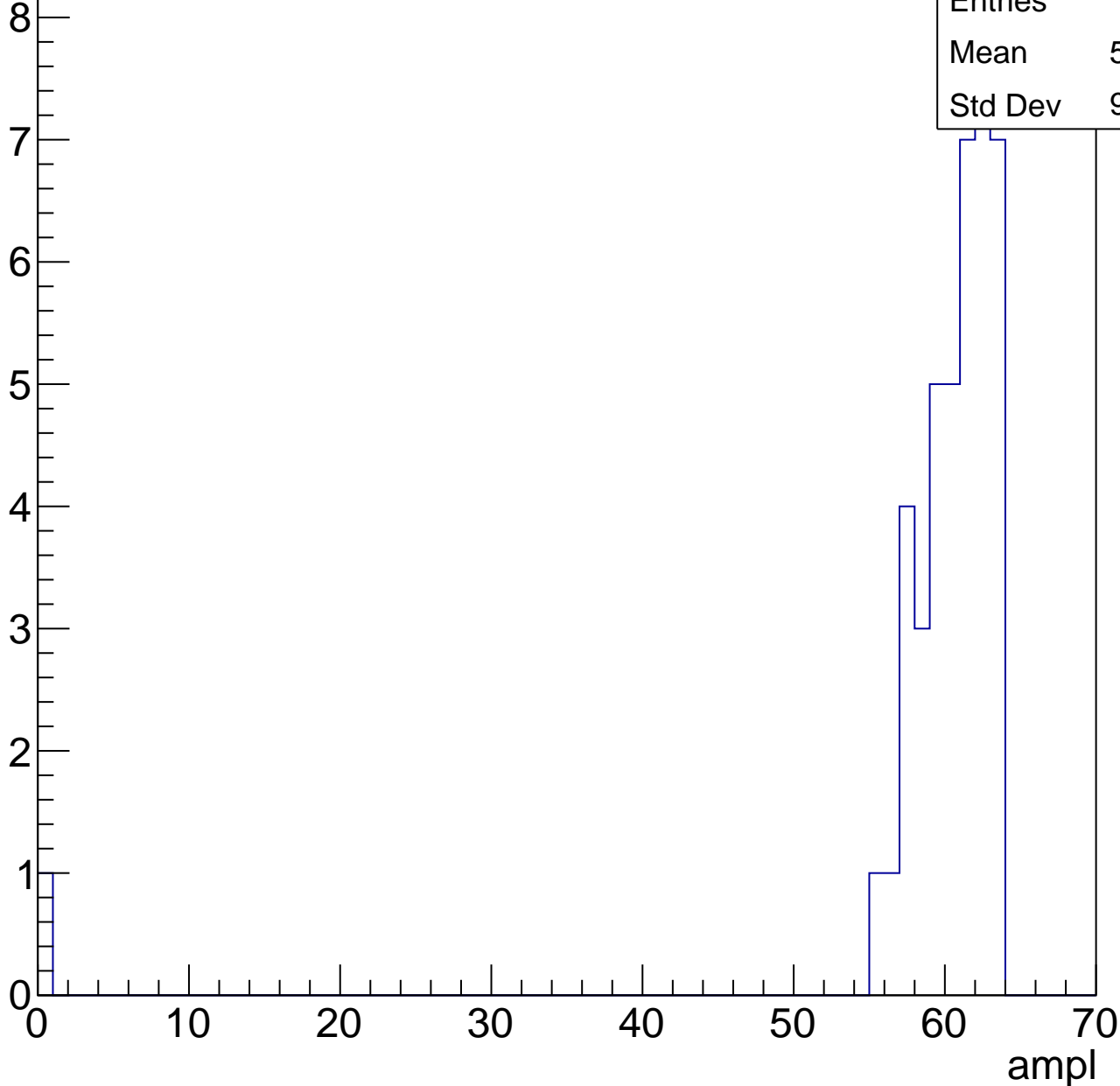


# B0L102S, U6-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

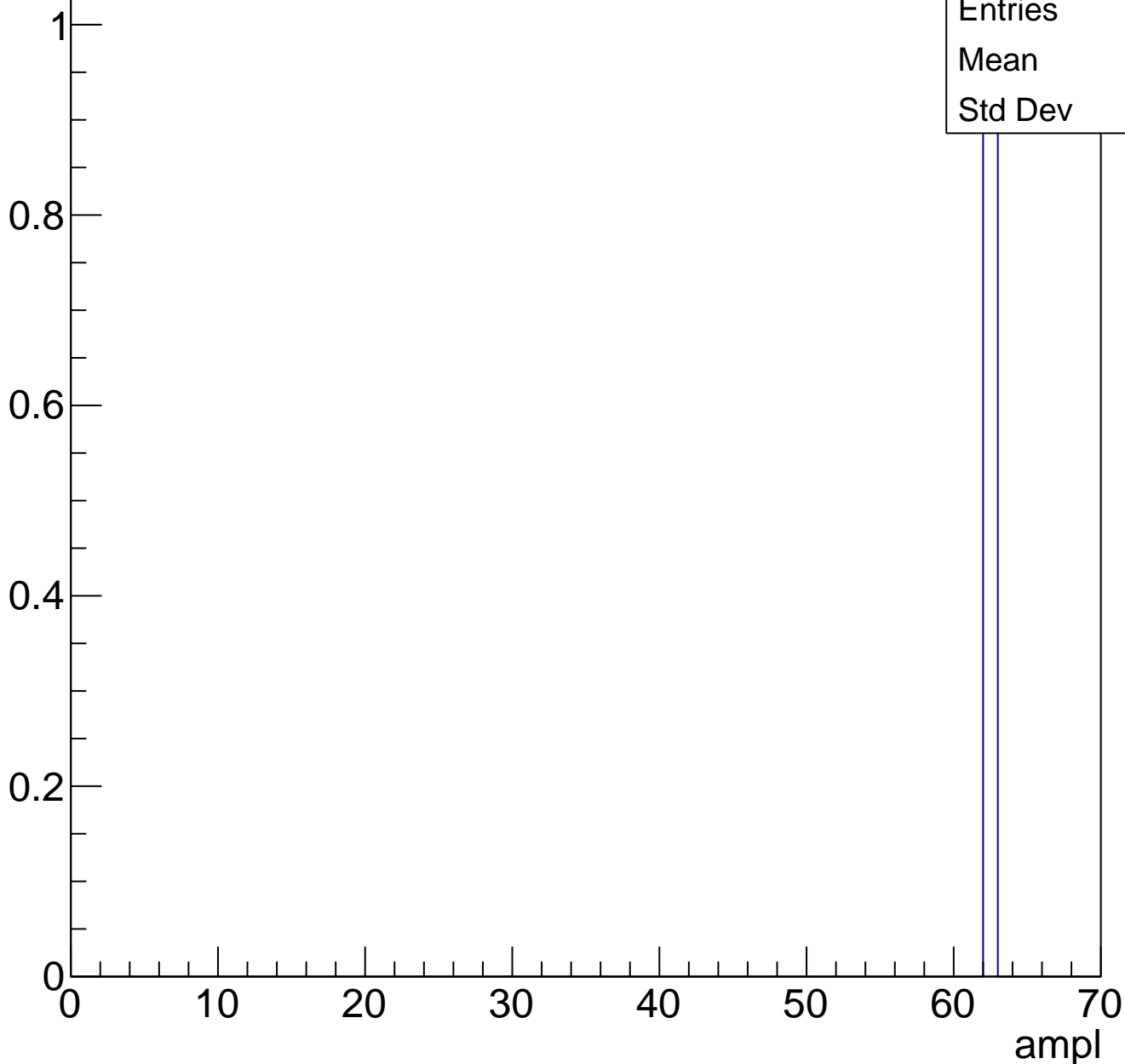
Entries	42
Mean	58.86
Std Dev	9.438



# B0L102S, U6-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch24, adc0

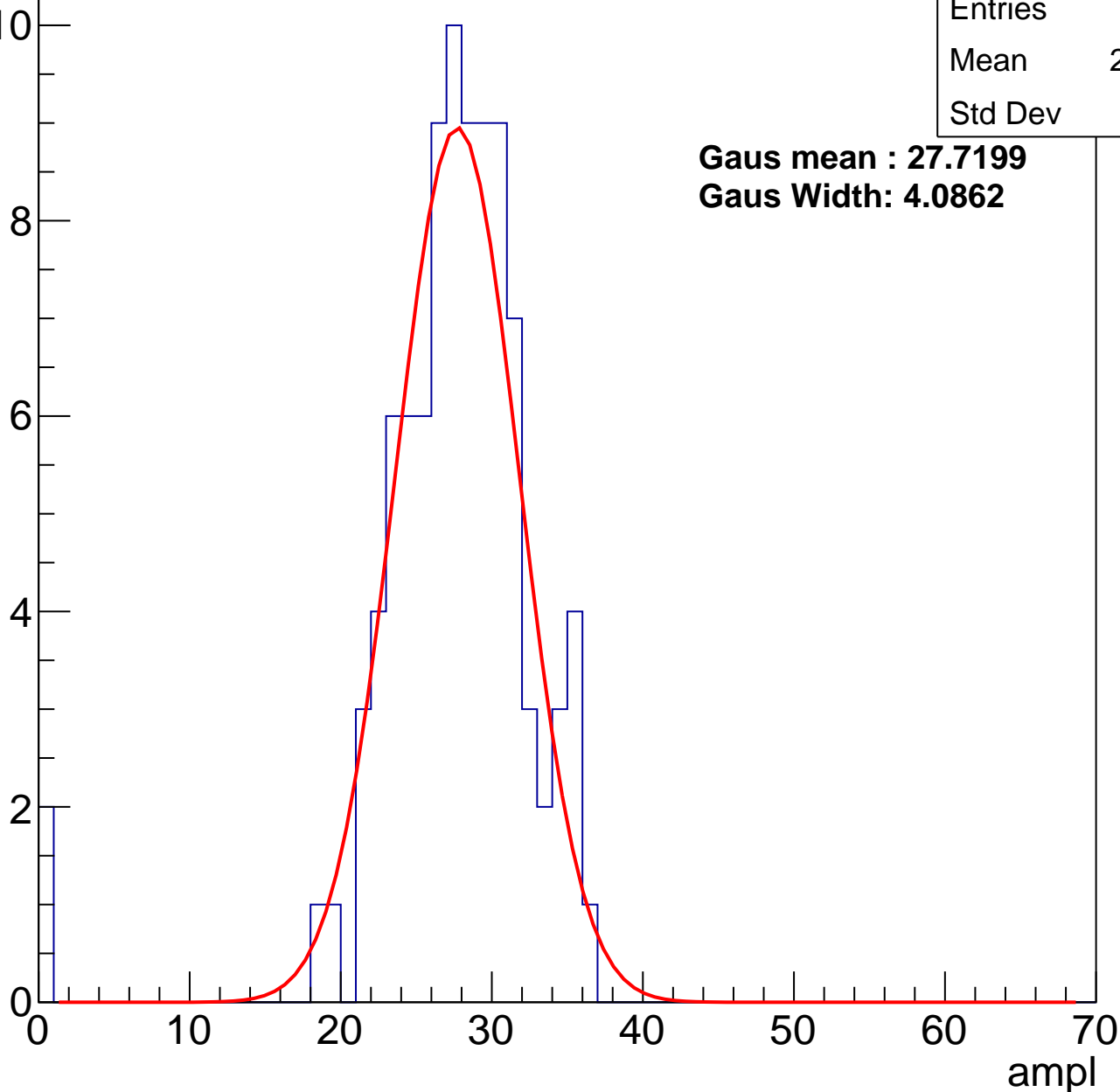
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	95
Mean	26.99
Std Dev	5.49

**Gaus mean : 27.7199**

**Gaus Width: 4.0862**



# B0L102S, U6-ch24, adc1

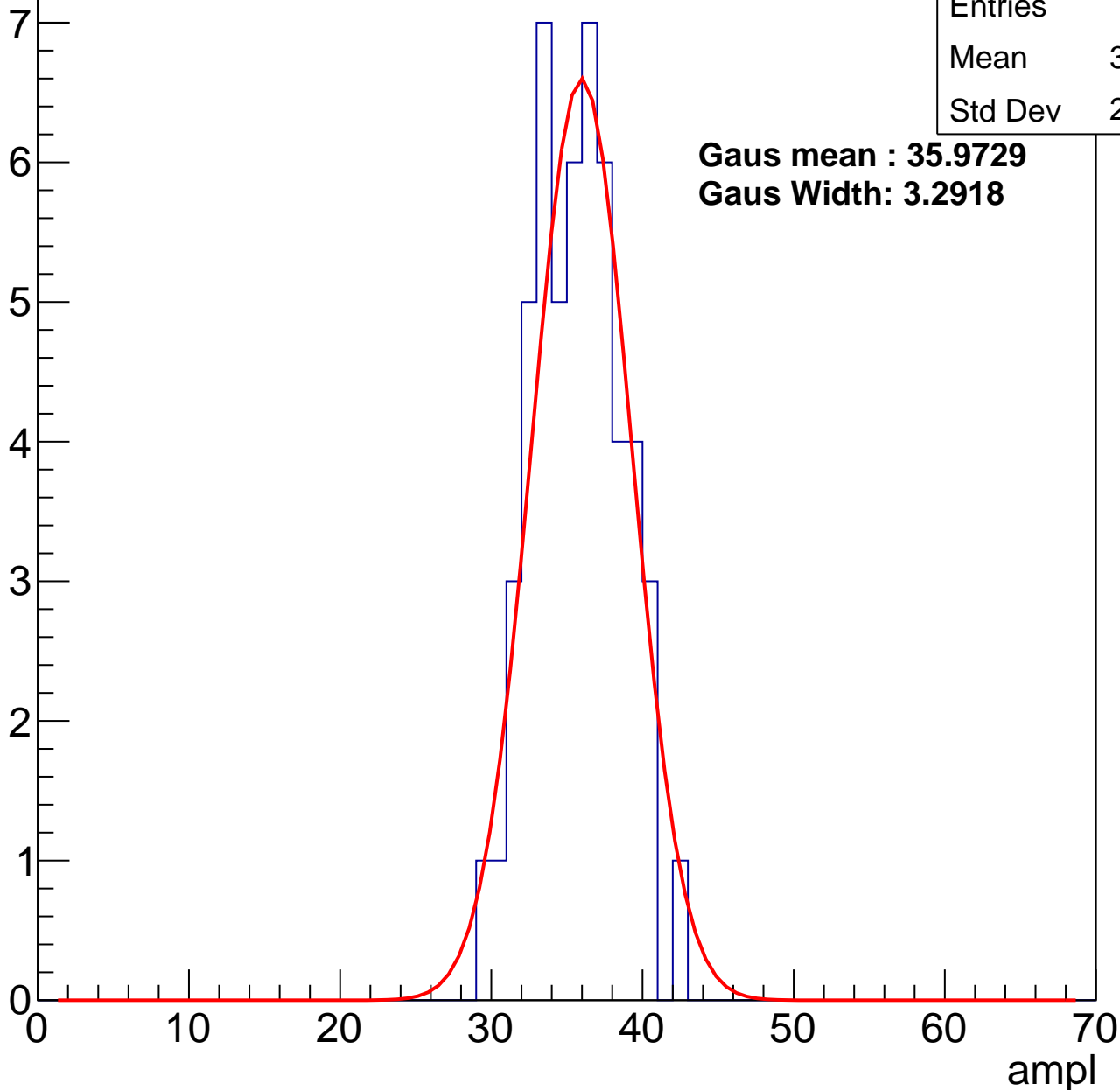
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	35.23
Std Dev	2.879

**Gaus mean : 35.9729**

**Gaus Width: 3.2918**



# B0L102S, U6-ch24, adc2

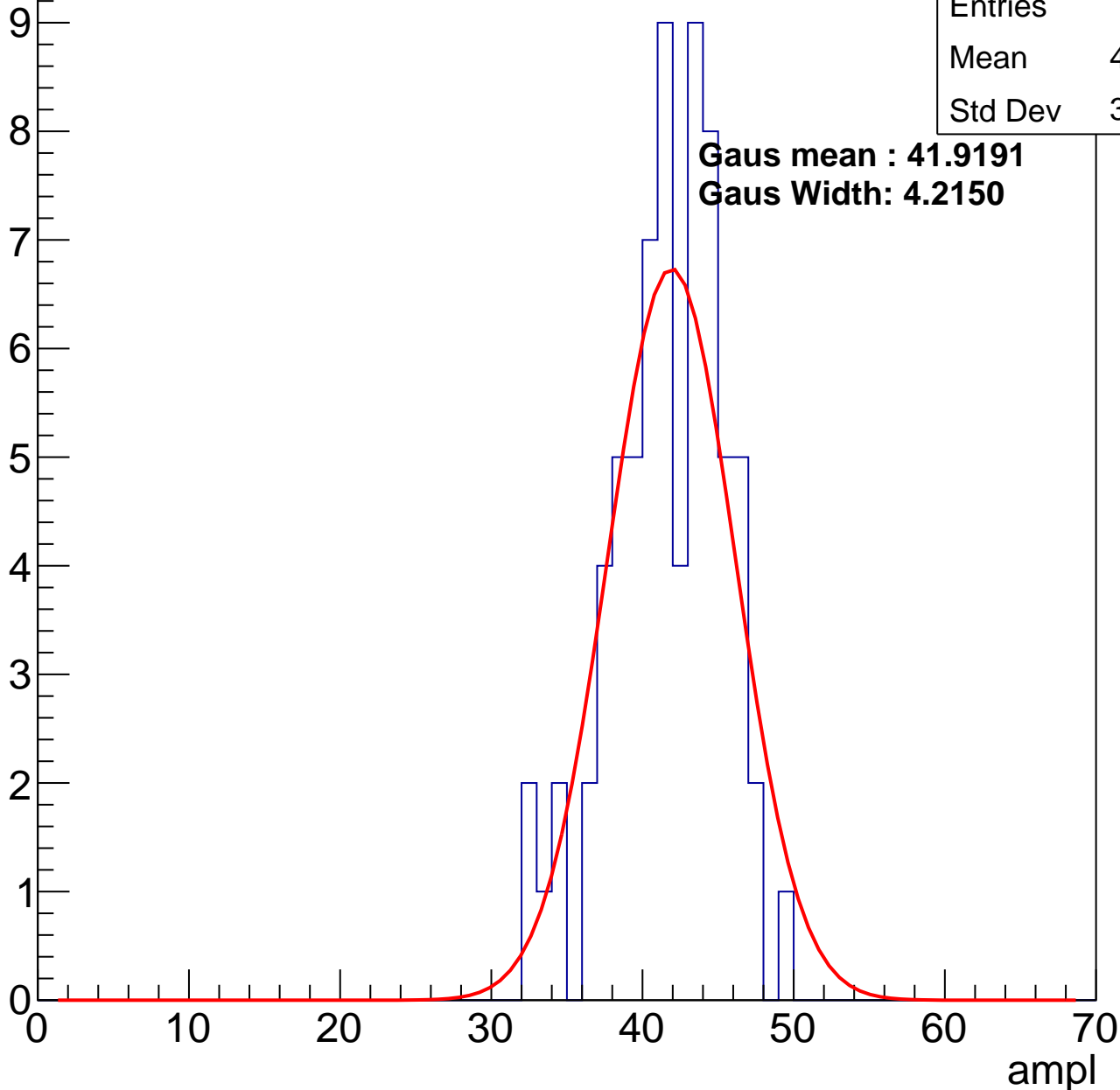
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	41.18
Std Dev	3.678

**Gaus mean : 41.9191**

**Gaus Width: 4.2150**

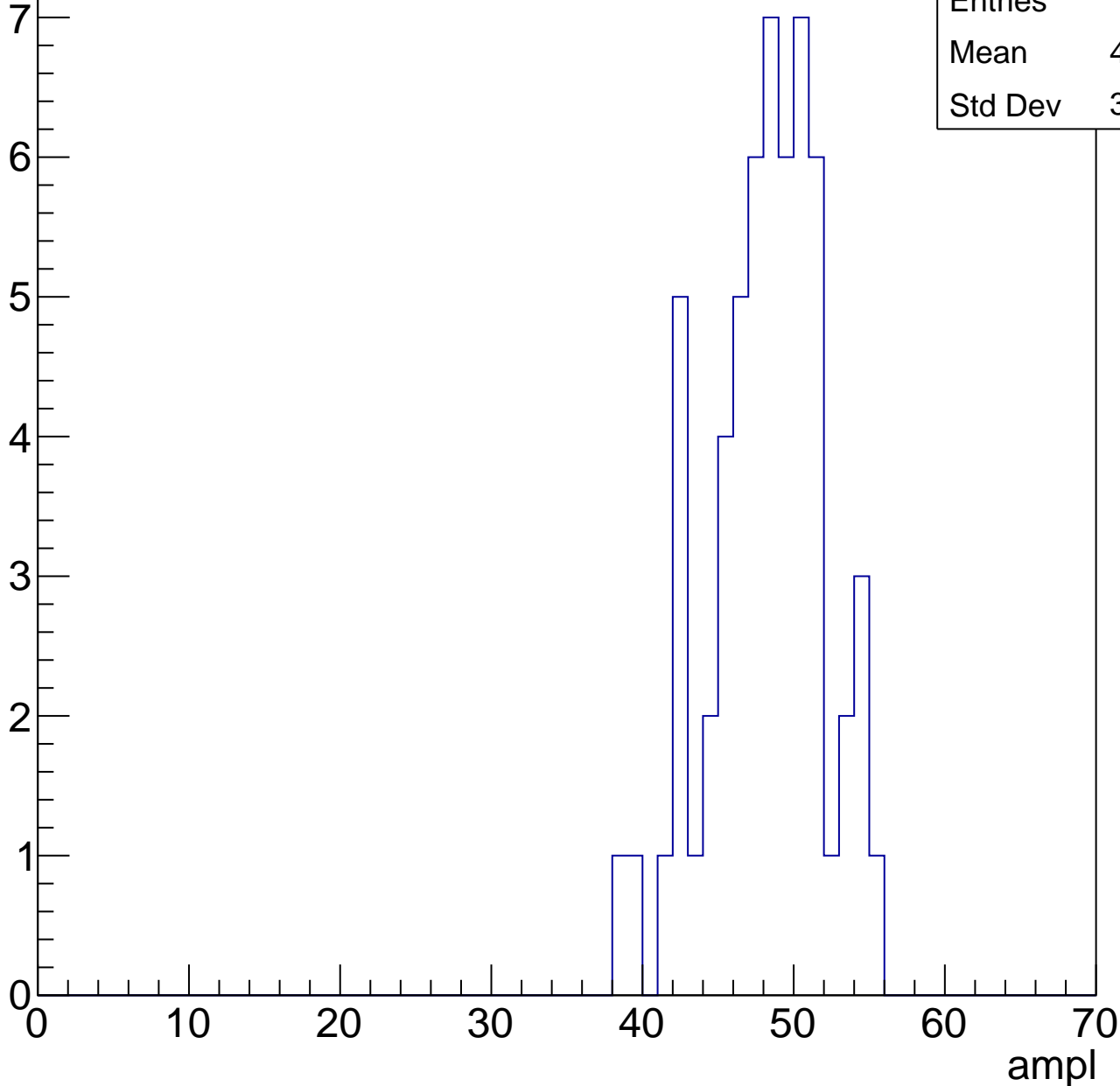


# B0L102S, U6-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	47.66
Std Dev	3.758

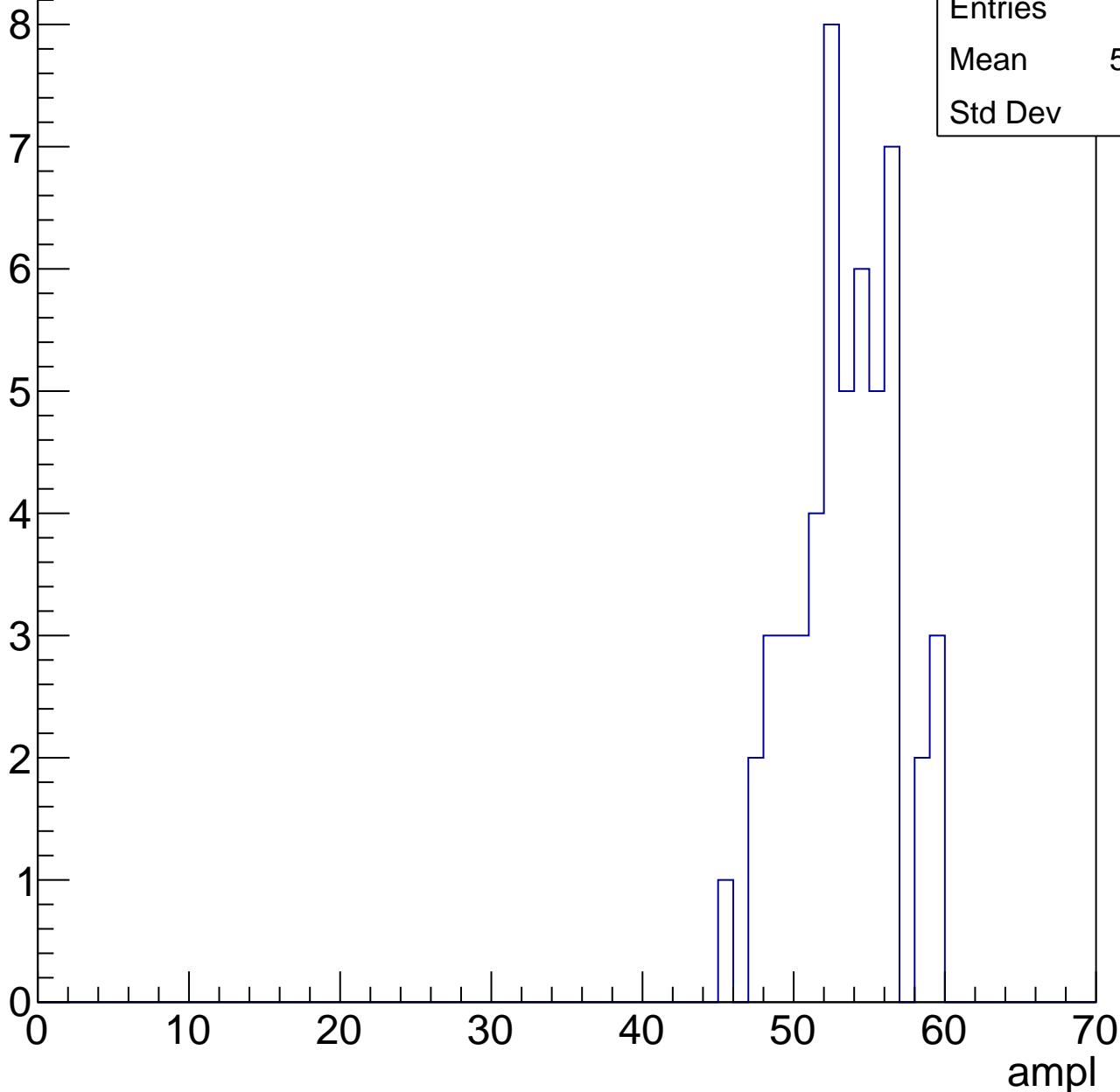


# B0L102S, U6-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	52.87
Std Dev	3.27



# B0L102S, U6-ch24, adc5

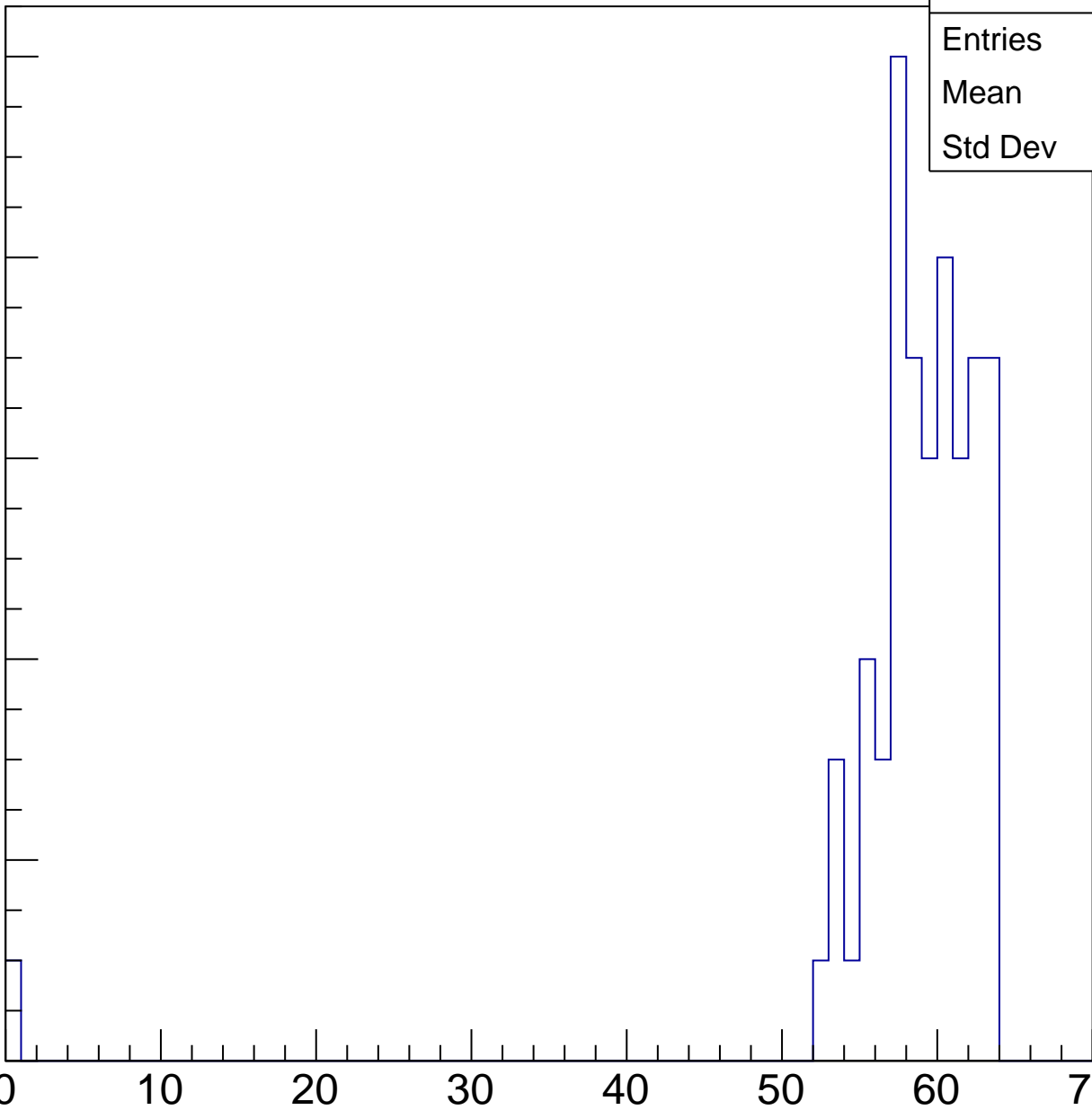
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10  
8  
6  
4  
2  
0

Entries	64
Mean	57.88
Std Dev	7.833

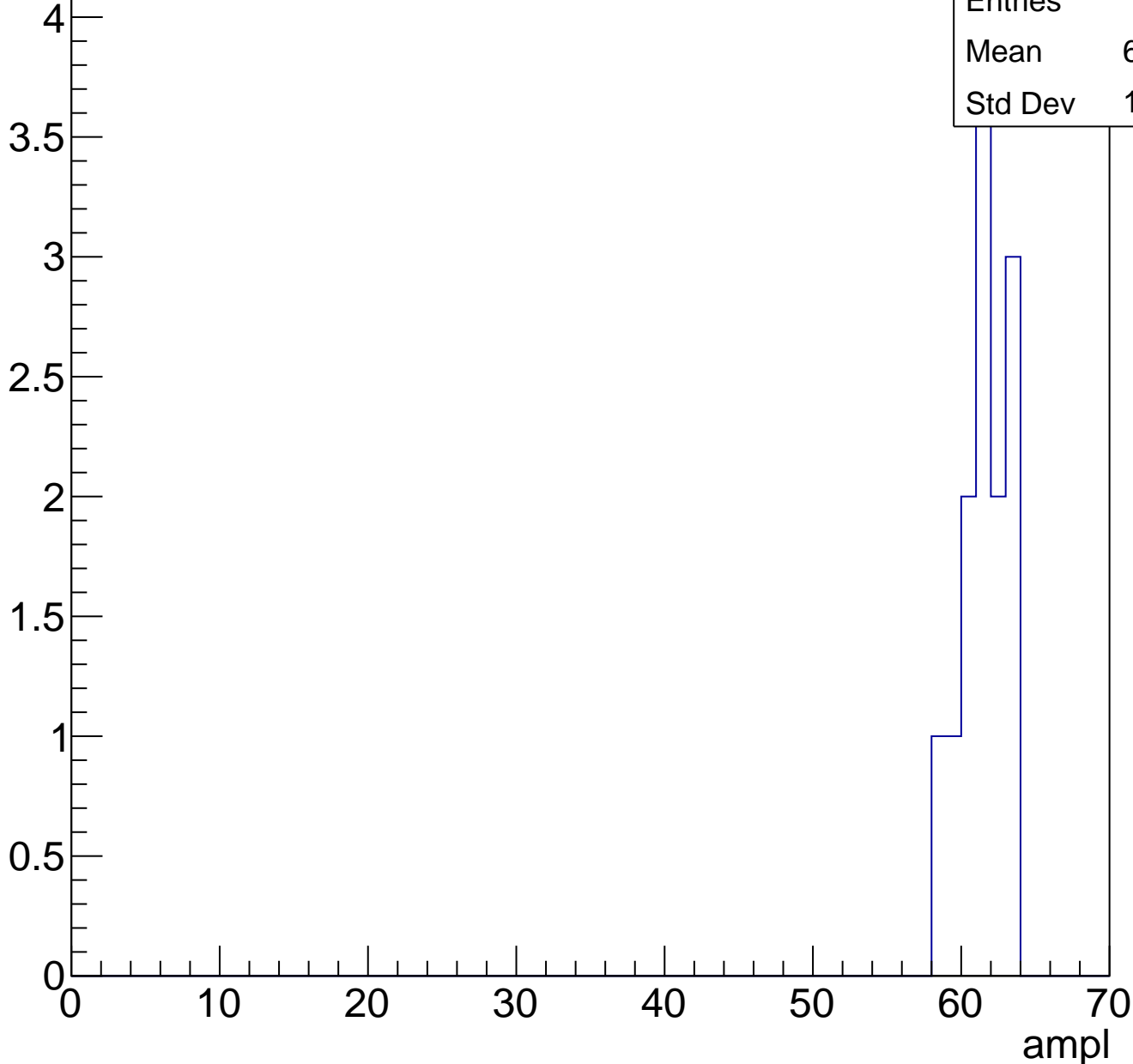
ampl



# B0L102S, U6-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch25, adc0

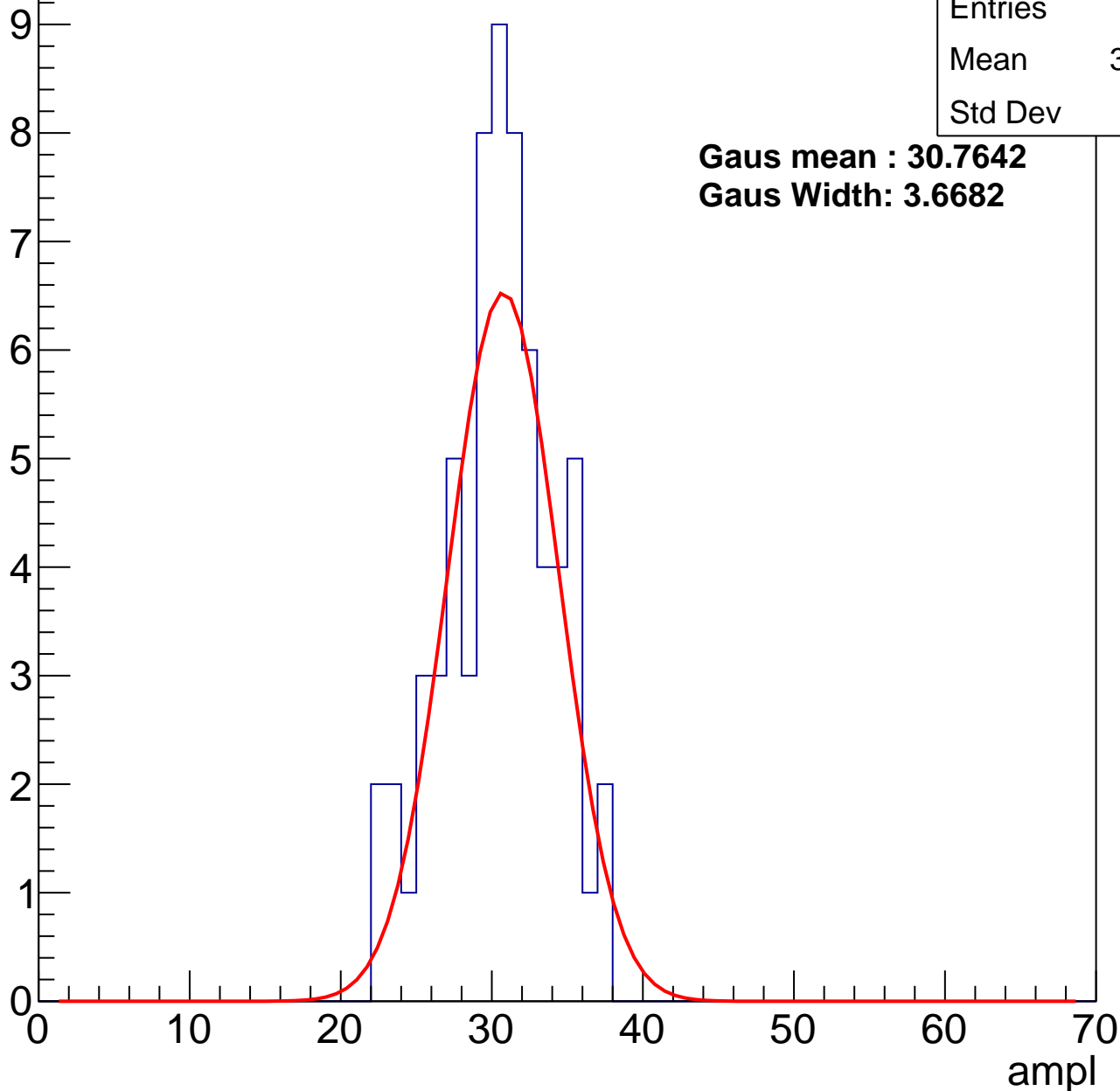
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	30.02
Std Dev	3.57

**Gaus mean : 30.7642**

**Gaus Width: 3.6682**



# B0L102S, U6-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	87
Mean	37.9
Std Dev	3.591

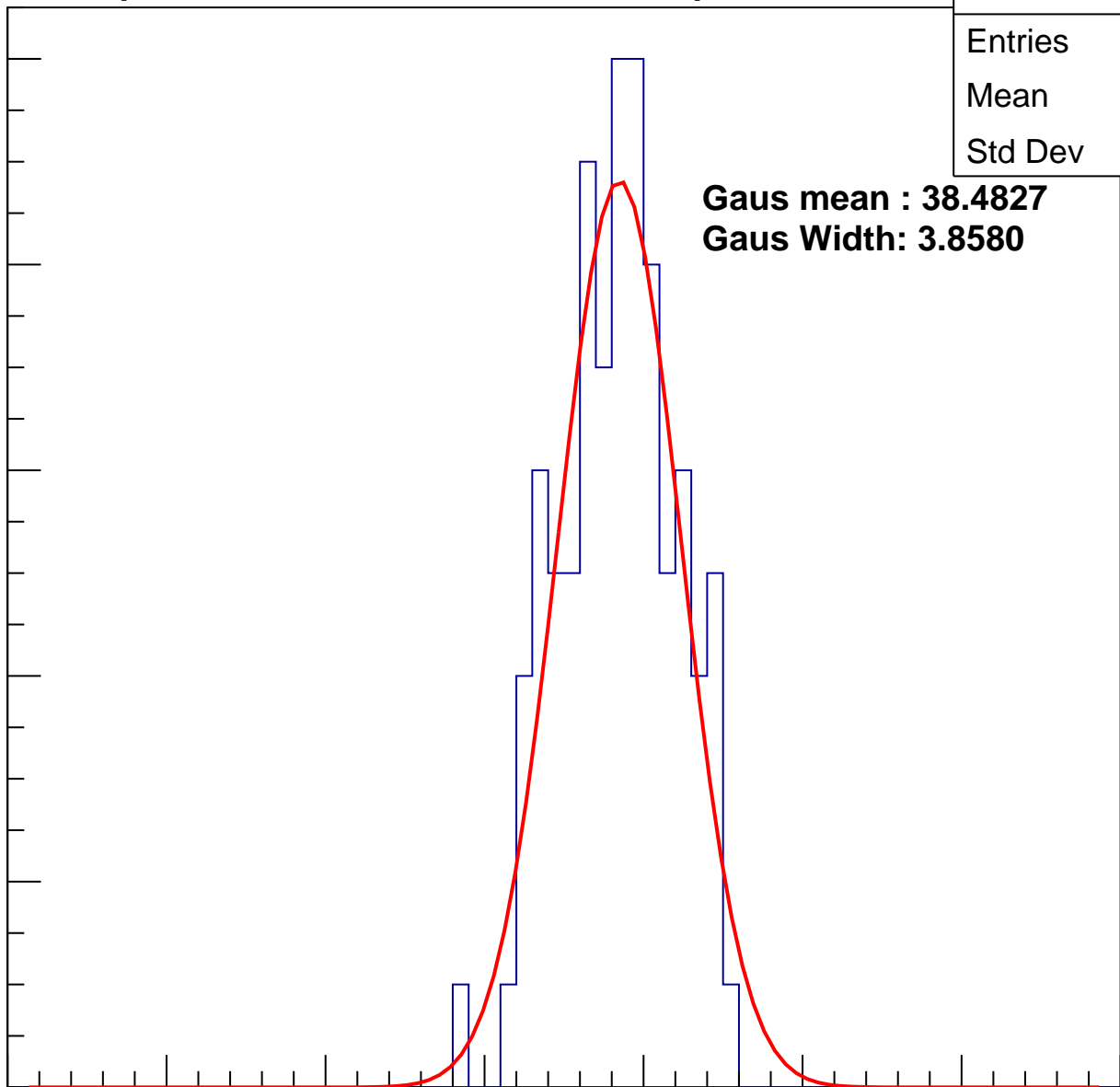
**Gaus mean : 38.4827**  
**Gaus Width: 3.8580**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U6-ch25, adc2

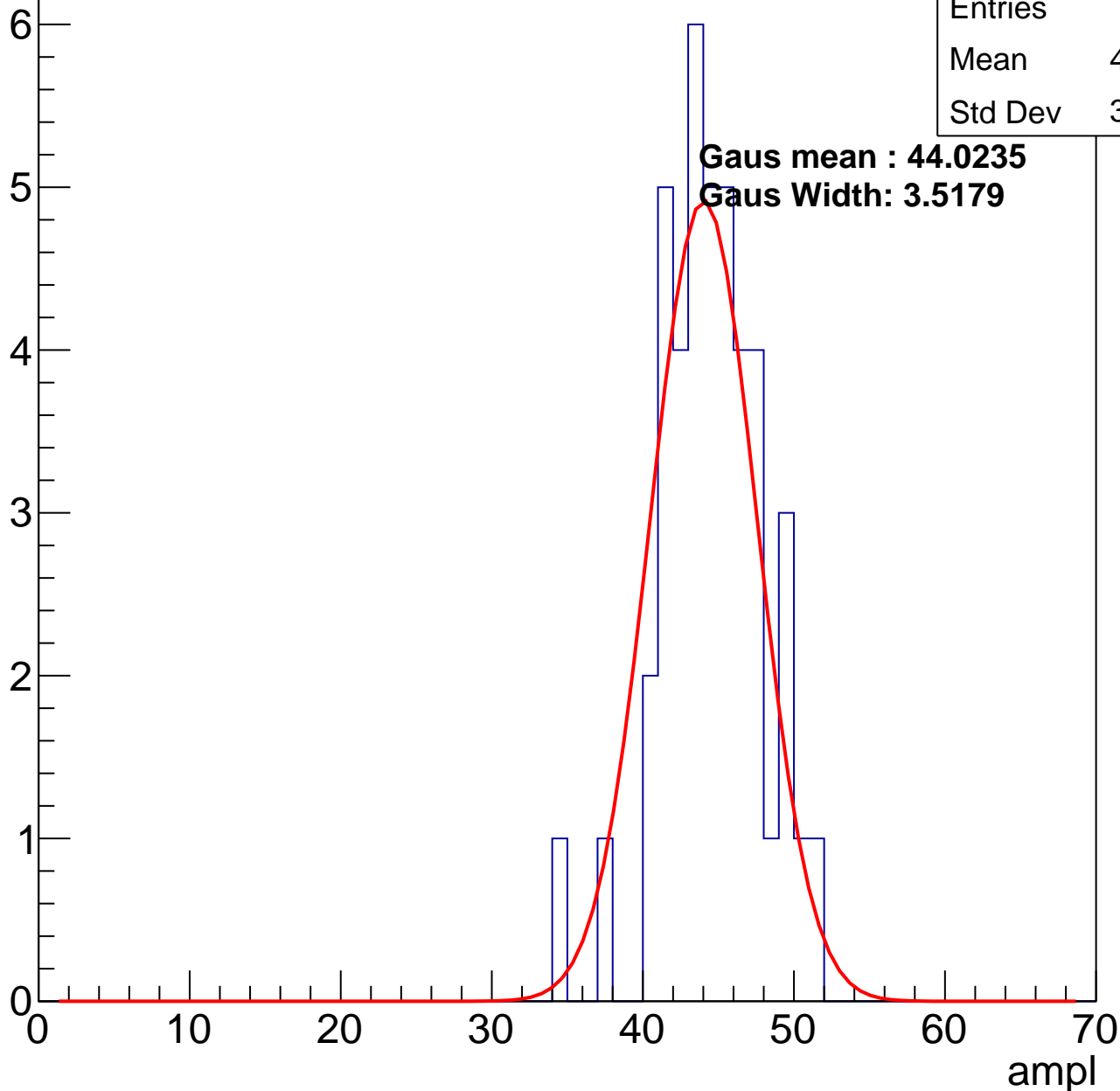
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	43
Mean	44.07
Std Dev	3.344

**Gaus mean : 44.0235**

**Gaus Width: 3.5179**

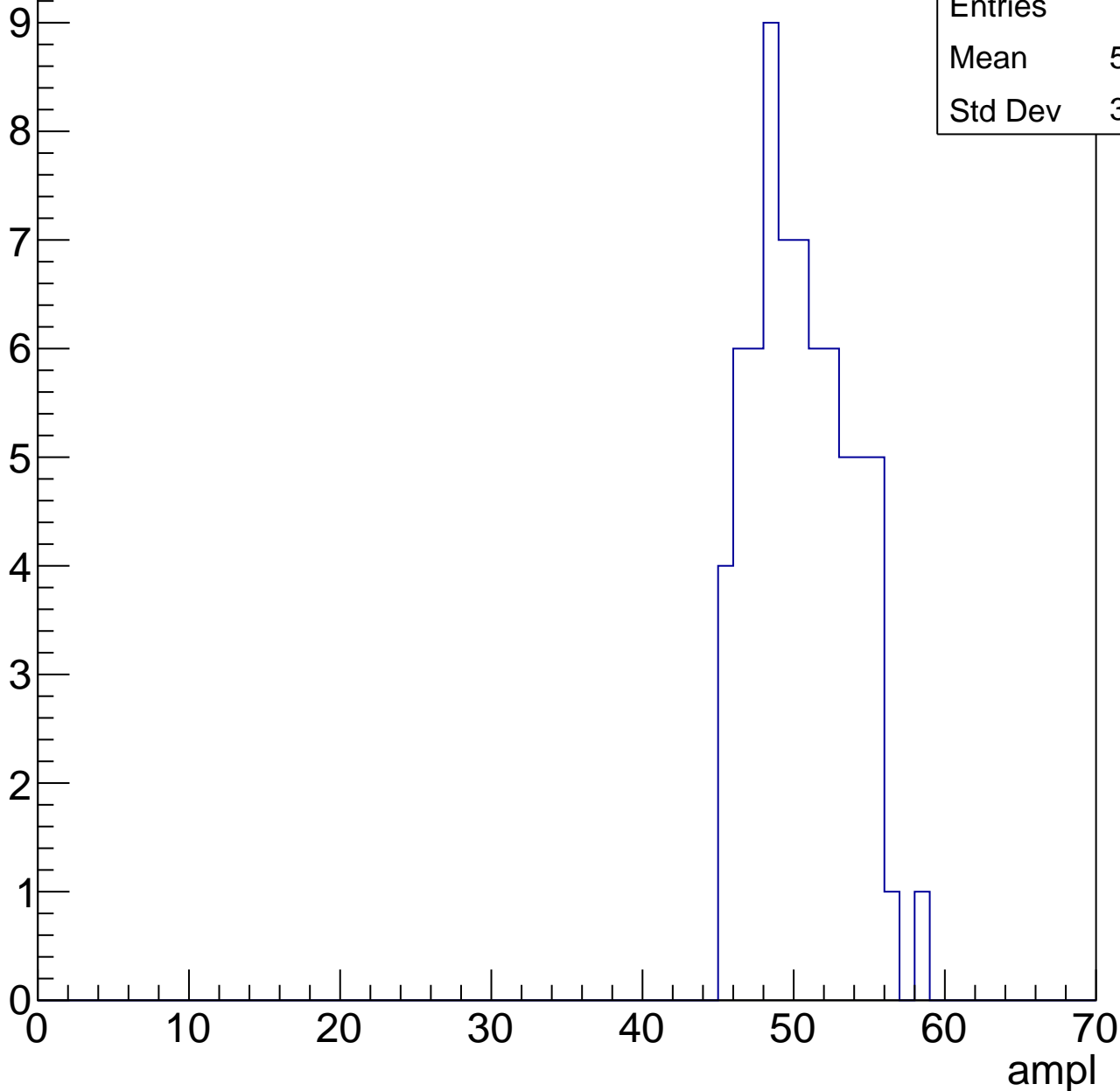


# B0L102S, U6-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	50.07
Std Dev	3.145

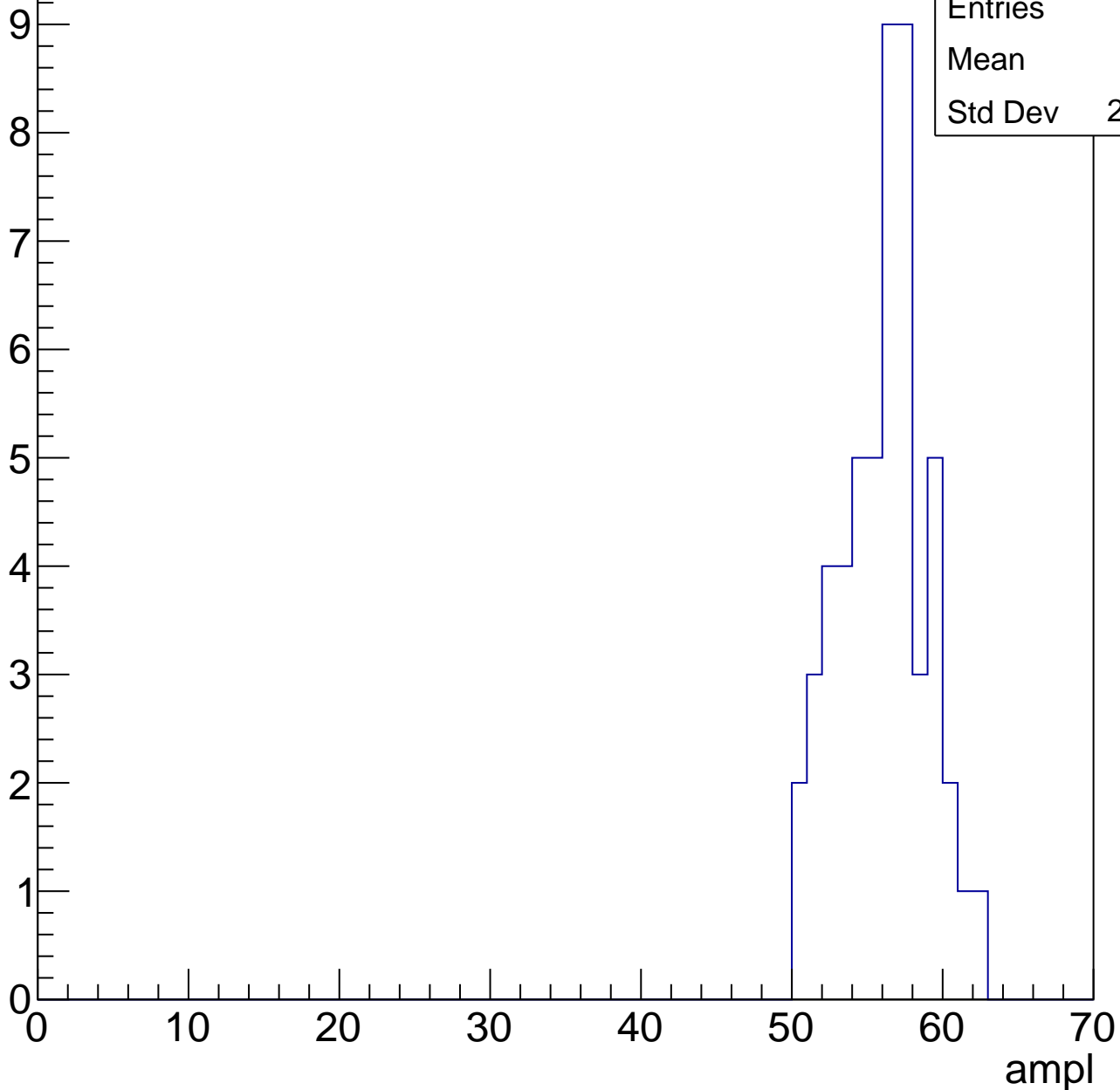


# B0L102S, U6-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

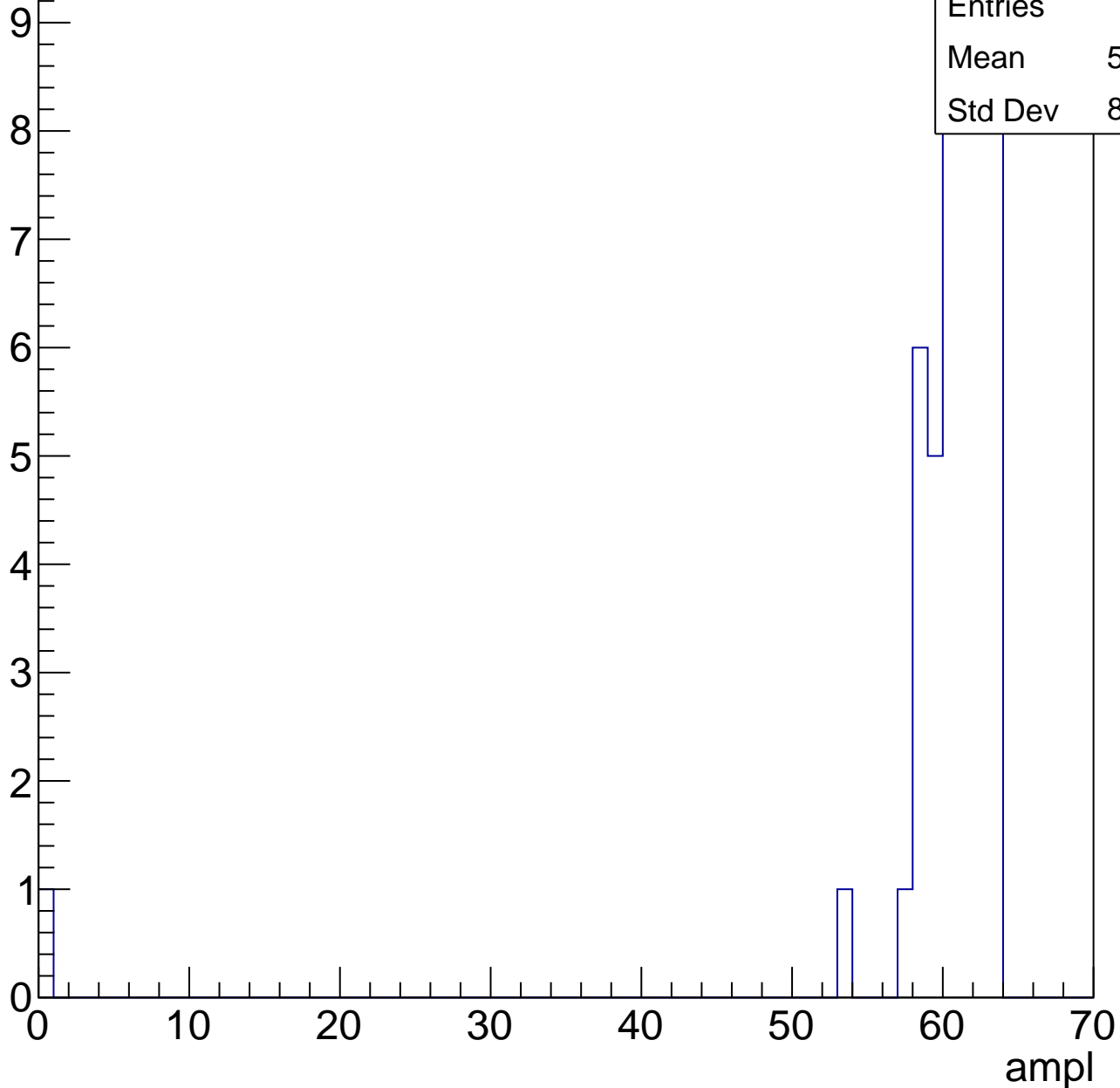
Entries	53
Mean	55.6
Std Dev	2.824



# B0L102S, U6-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch26, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	90
Mean	28.66
Std Dev	5.63

**Gaus mean : 29.6056**

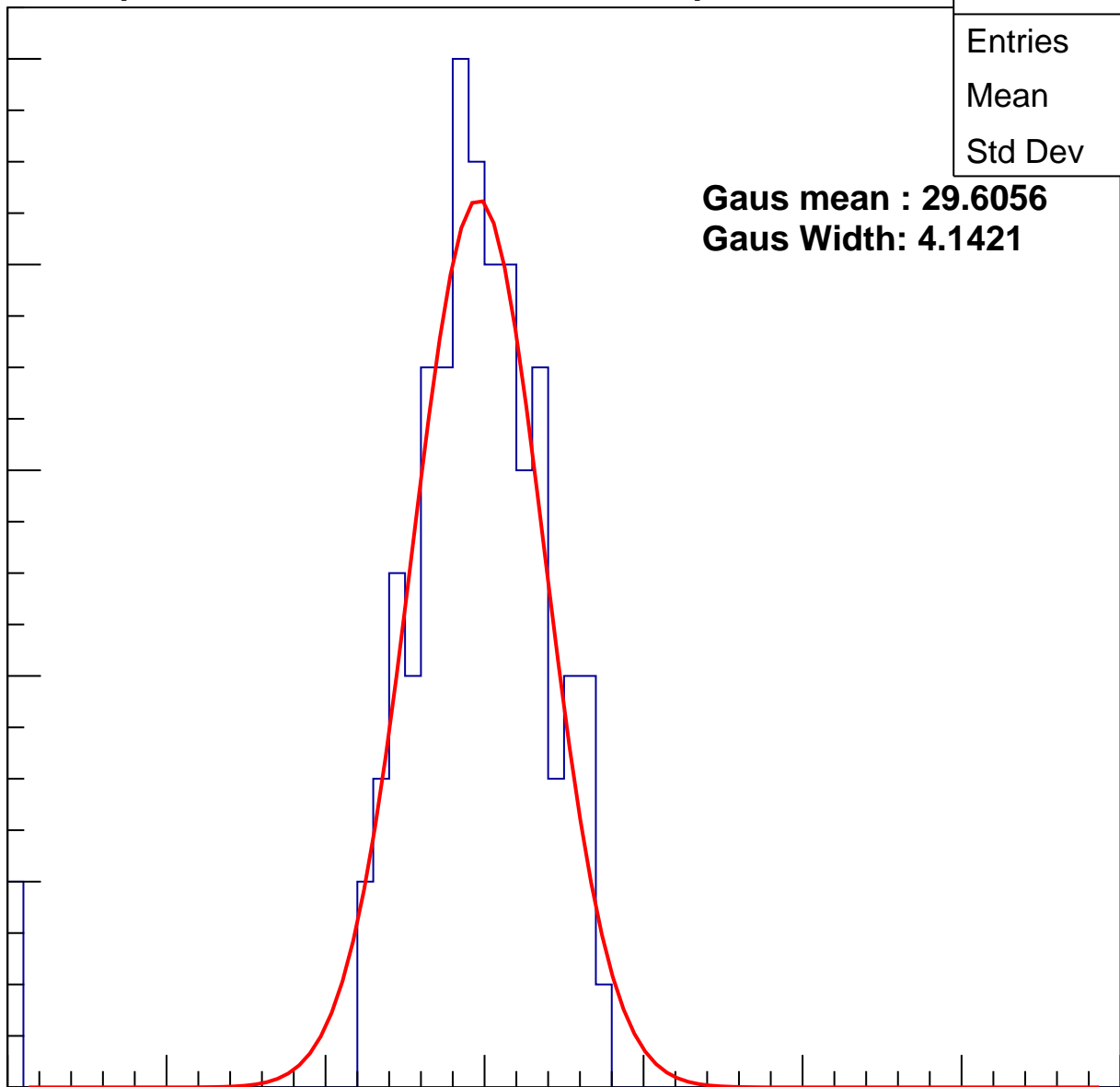
**Gaus Width: 4.1421**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch26, adc1

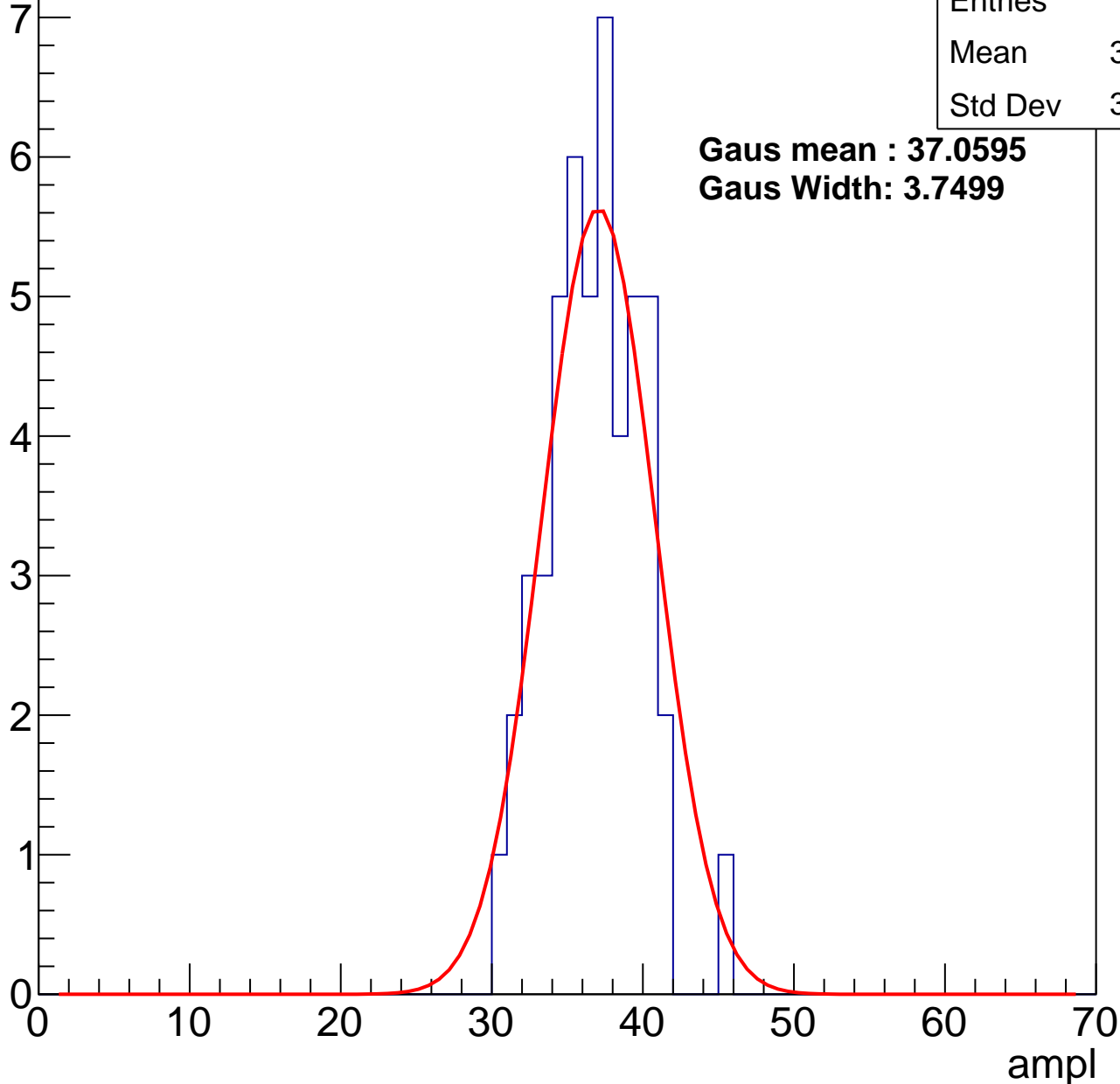
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	36.33
Std Dev	3.067

**Gaus mean : 37.0595**

**Gaus Width: 3.7499**



# B0L102S, U6-ch26, adc2

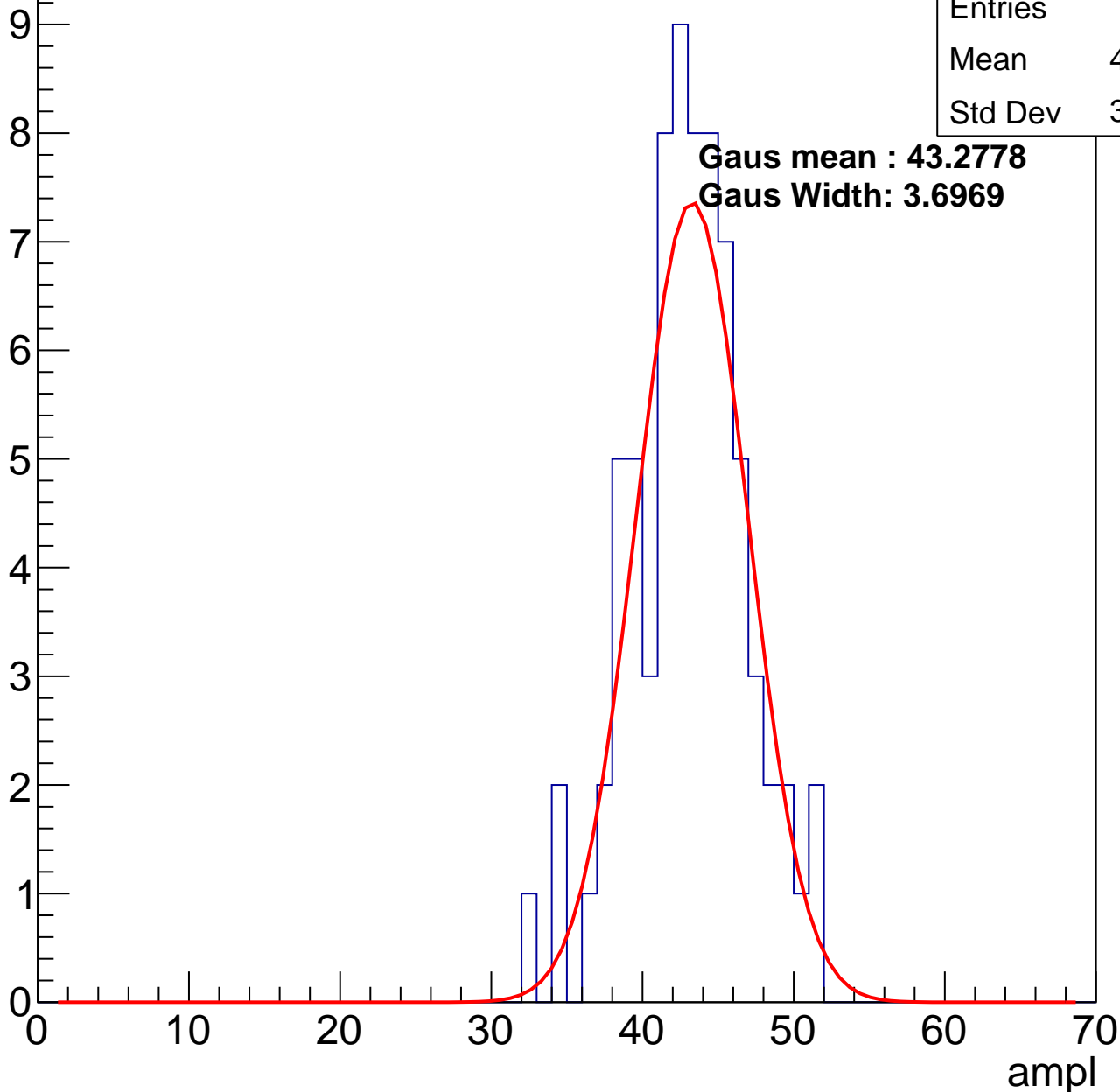
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	42.55
Std Dev	3.838

**Gaus mean : 43.2778**

**Gaus Width: 3.6969**

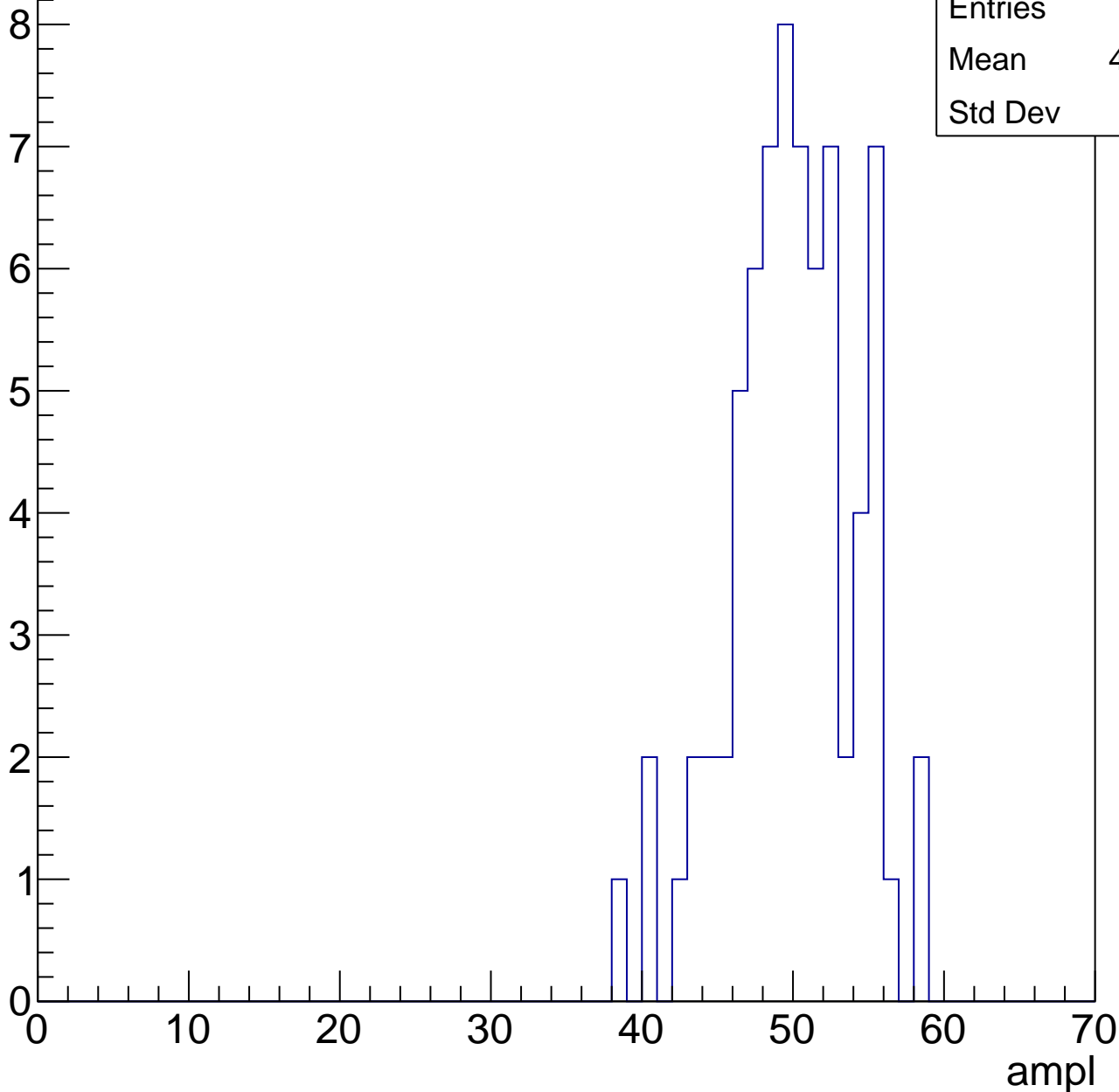


# B0L102S, U6-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

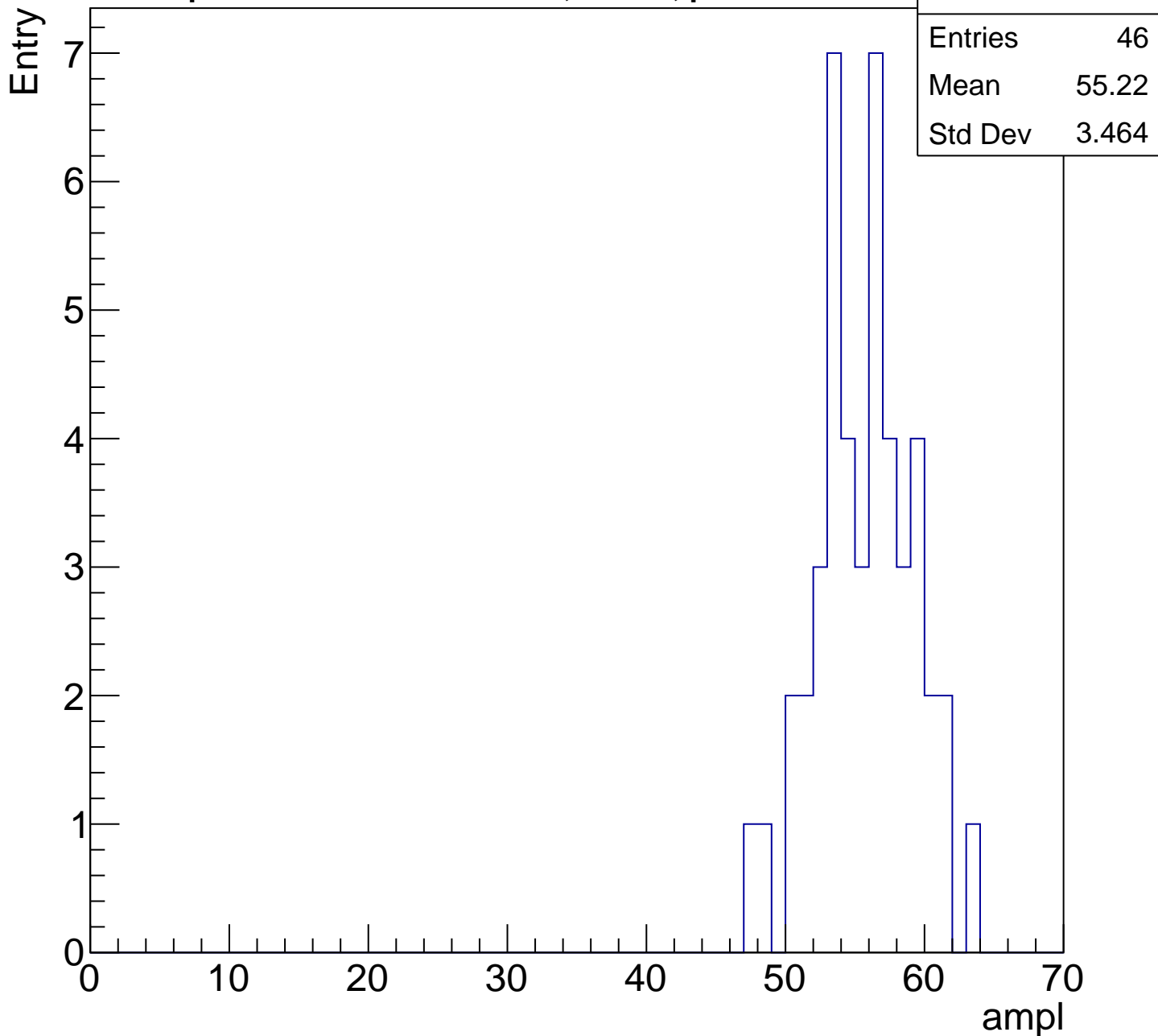
Entry

Entries	72
Mean	49.49
Std Dev	4.14



# B0L102S, U6-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch26, adc5

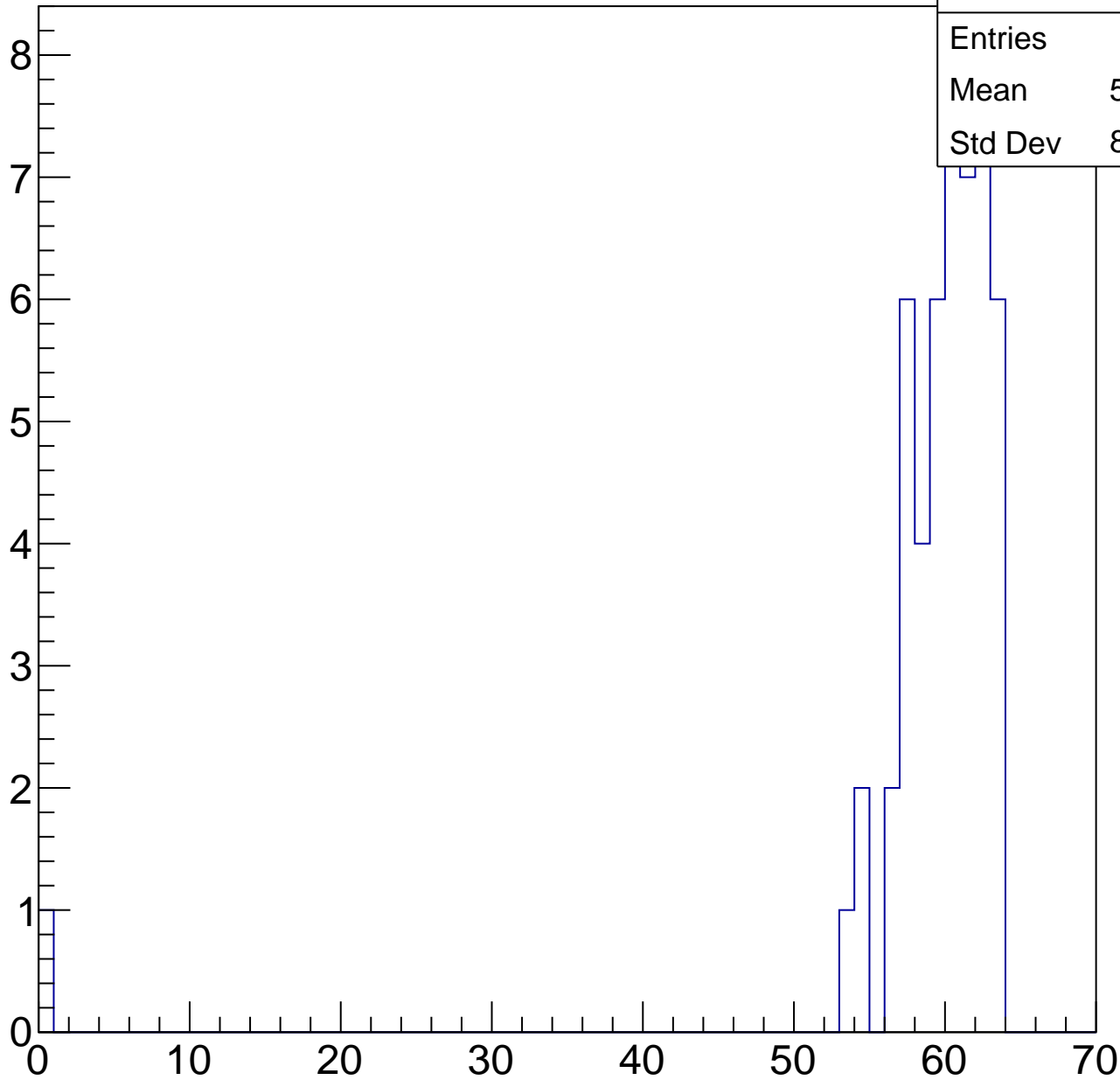
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	58.47
Std Dev	8.635

ampl



# B0L102S, U6-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

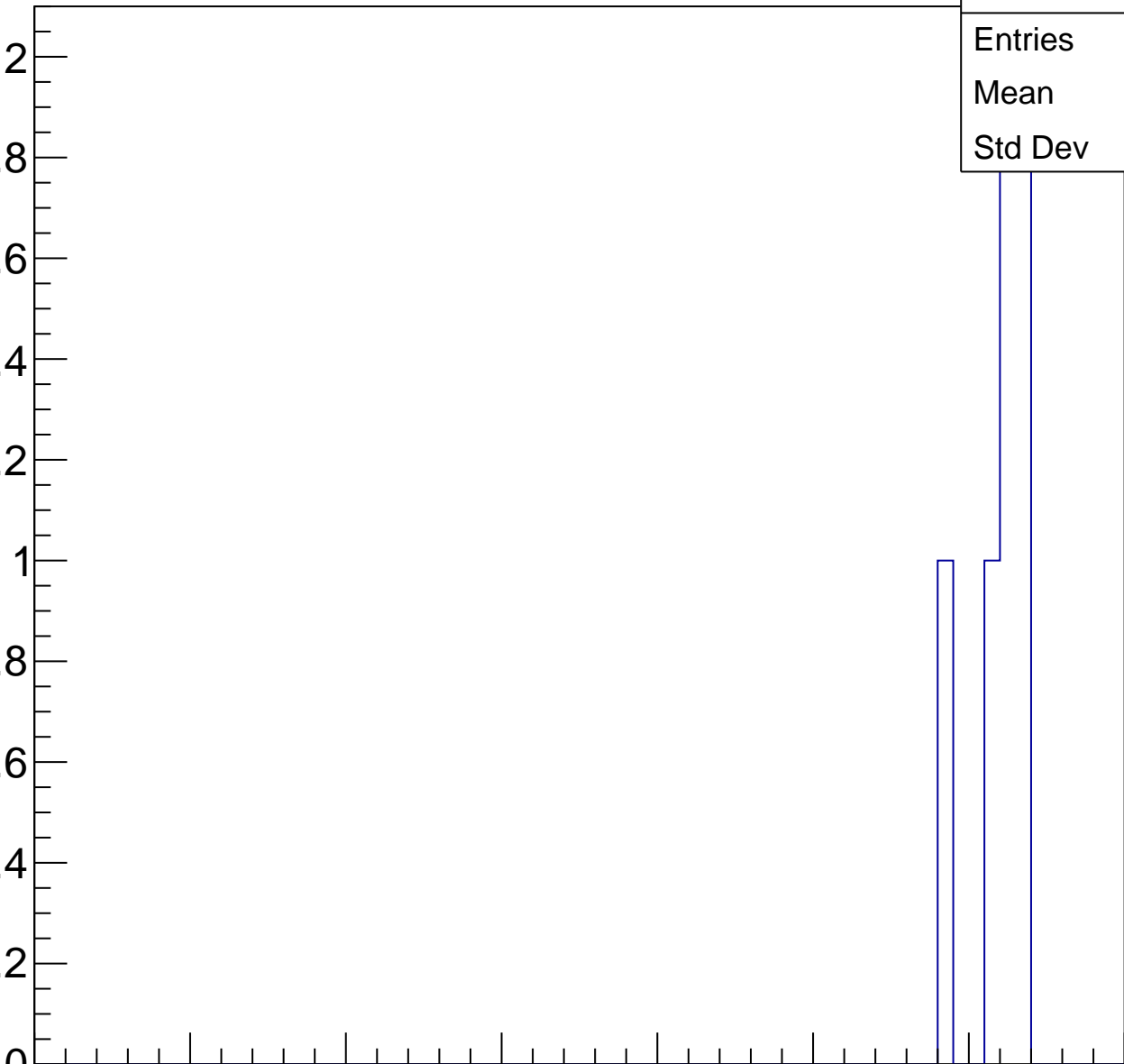
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	1.708

0 10 20 30 40 50 60 70

ampl





# B0L102S, U6-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch27, adc0

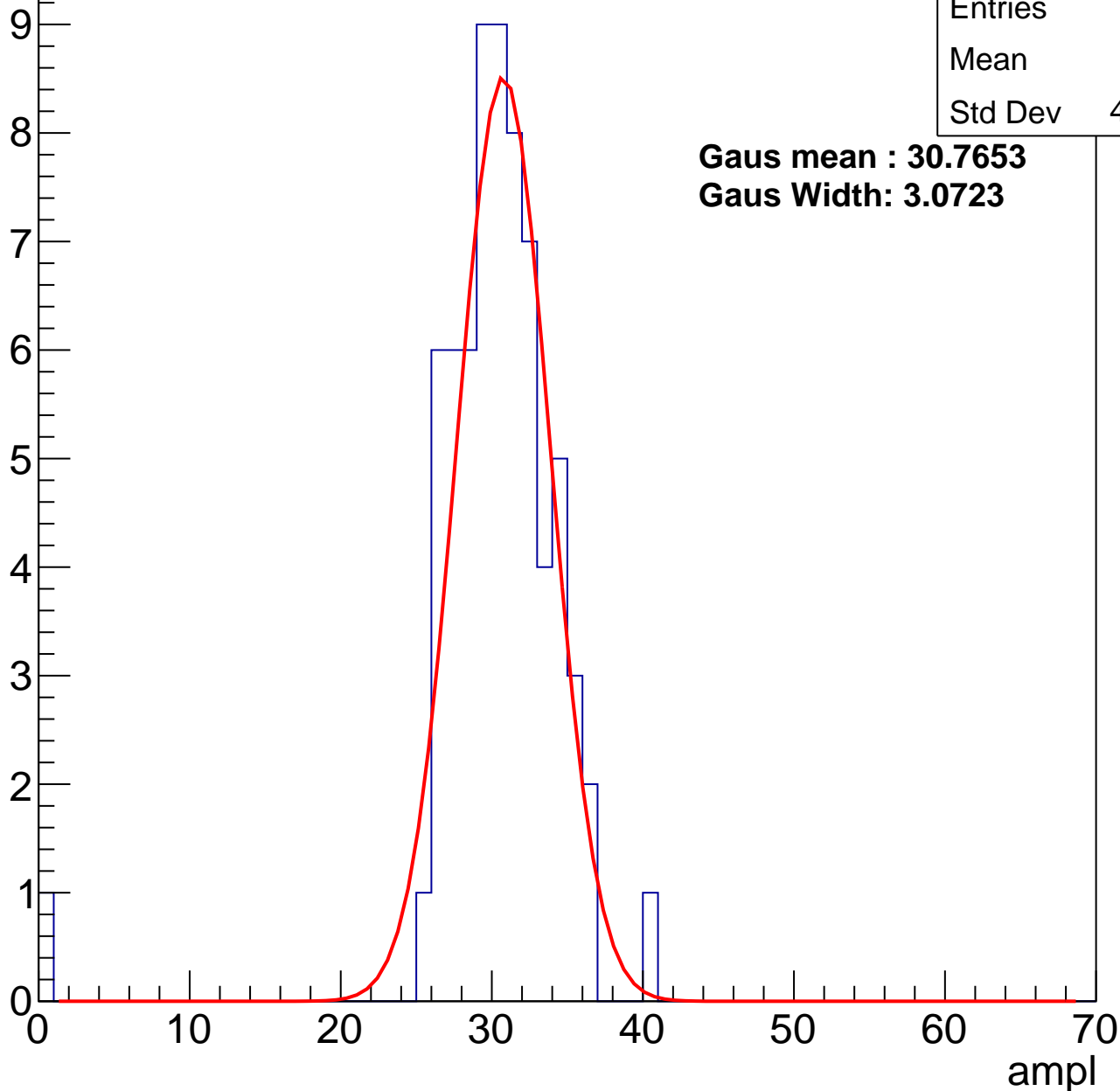
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	29.9
Std Dev	4.707

**Gaus mean : 30.7653**

**Gaus Width: 3.0723**



# B0L102S, U6-ch27, adc1

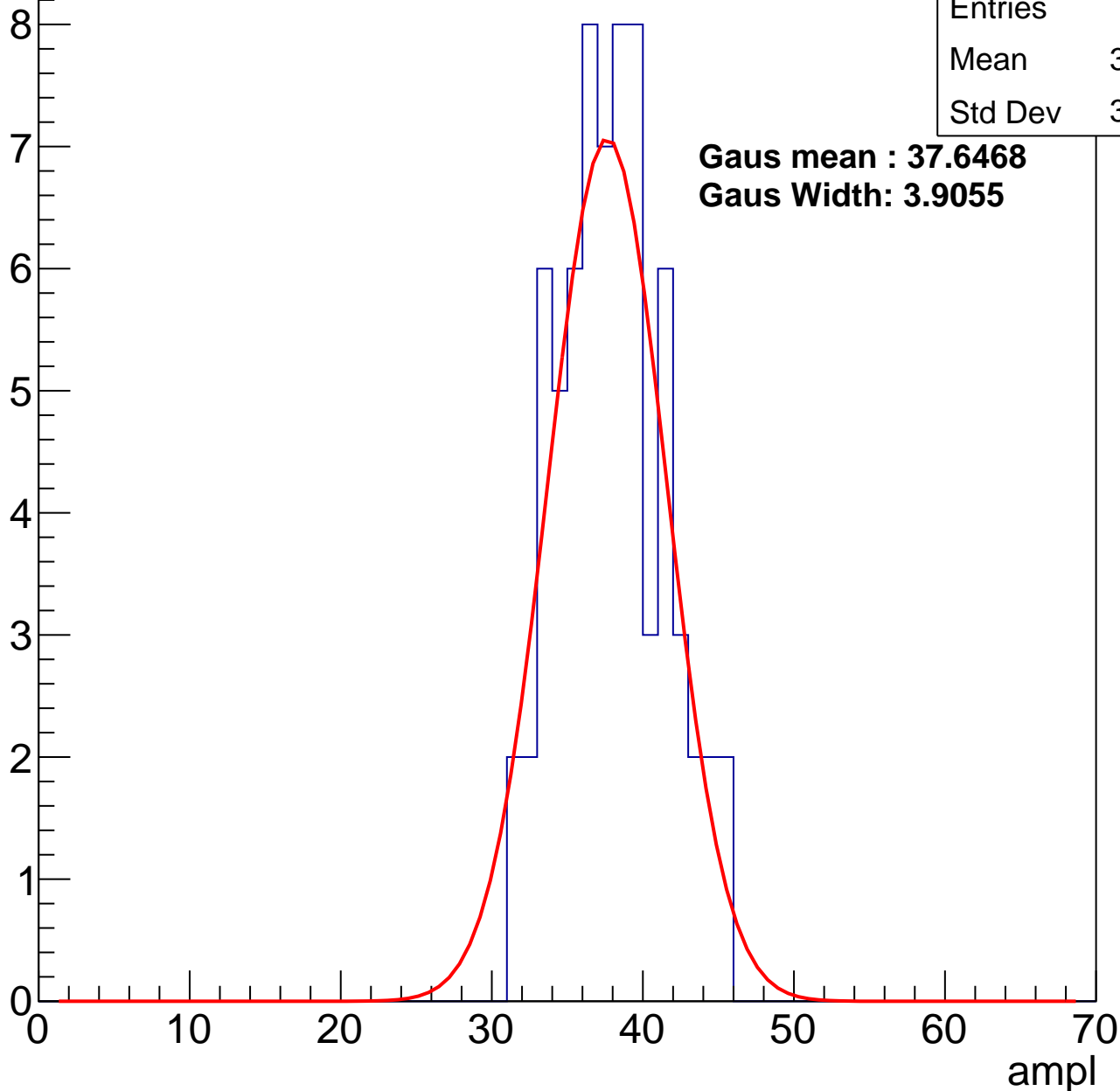
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	37.47
Std Dev	3.413

**Gaus mean : 37.6468**

**Gaus Width: 3.9055**



# B0L102S, U6-ch27, adc2

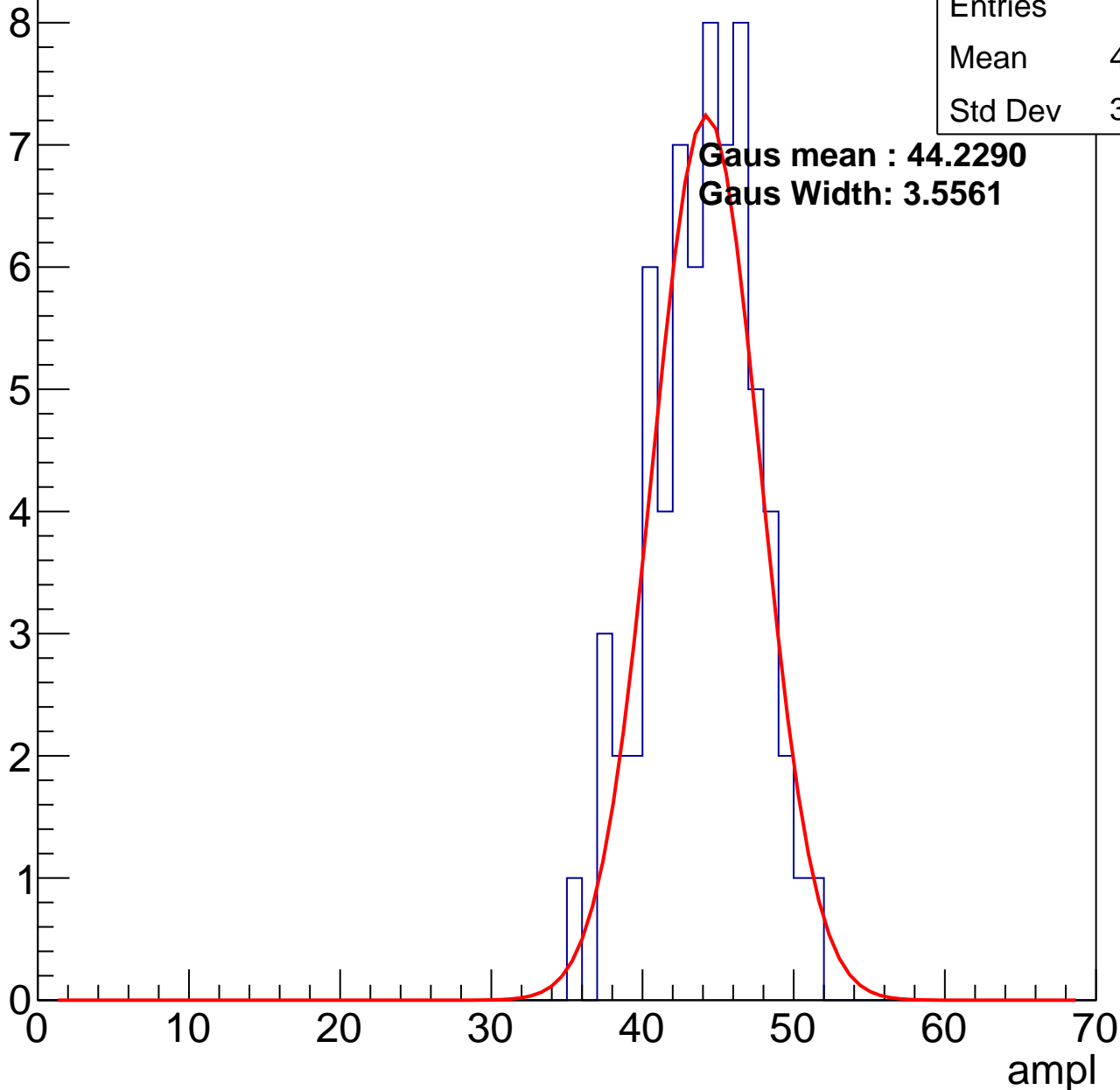
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	43.54
Std Dev	3.422

**Gaus mean : 44.2290**

**Gaus Width: 3.5561**

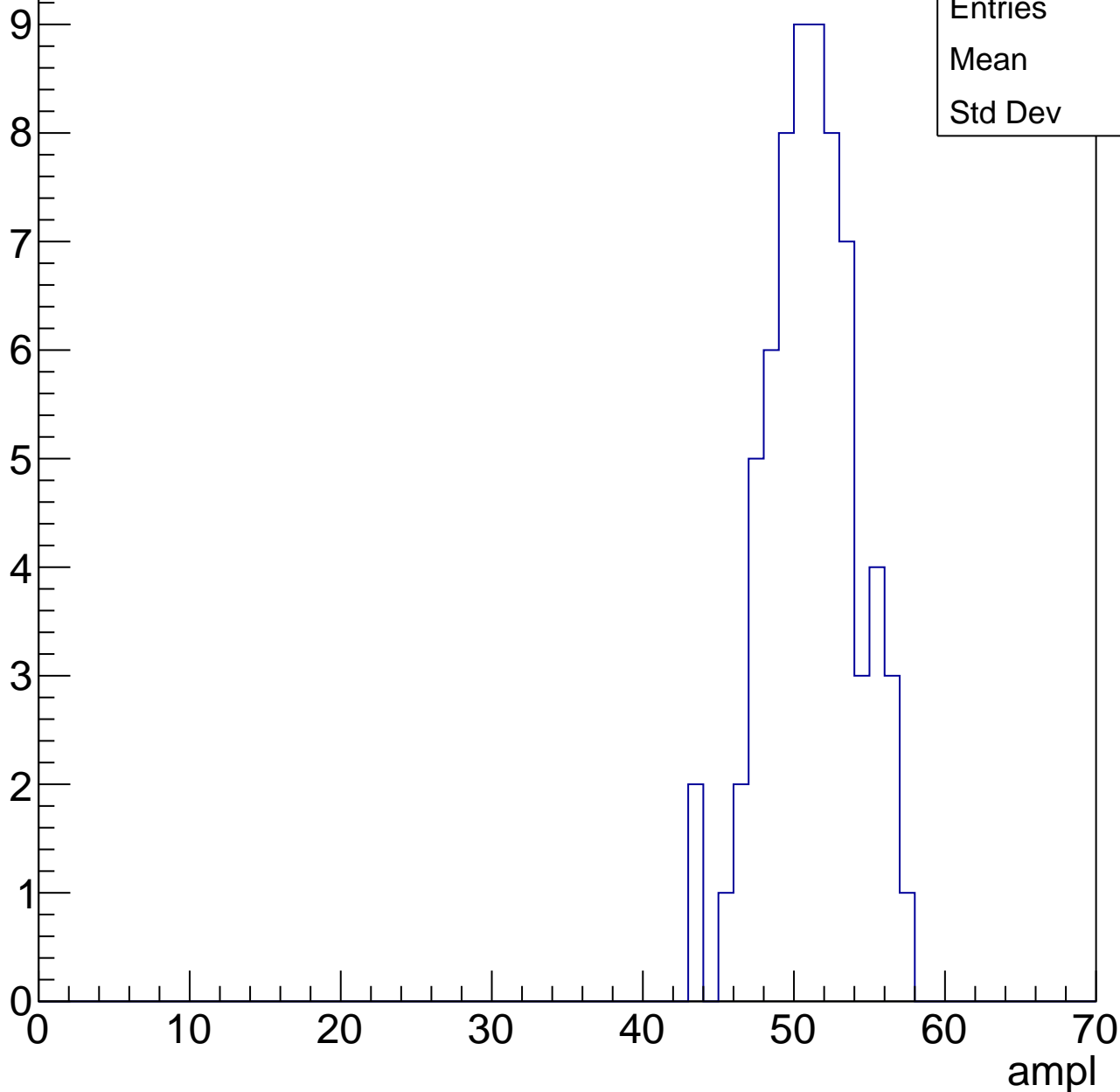


# B0L102S, U6-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	50.6
Std Dev	3.01

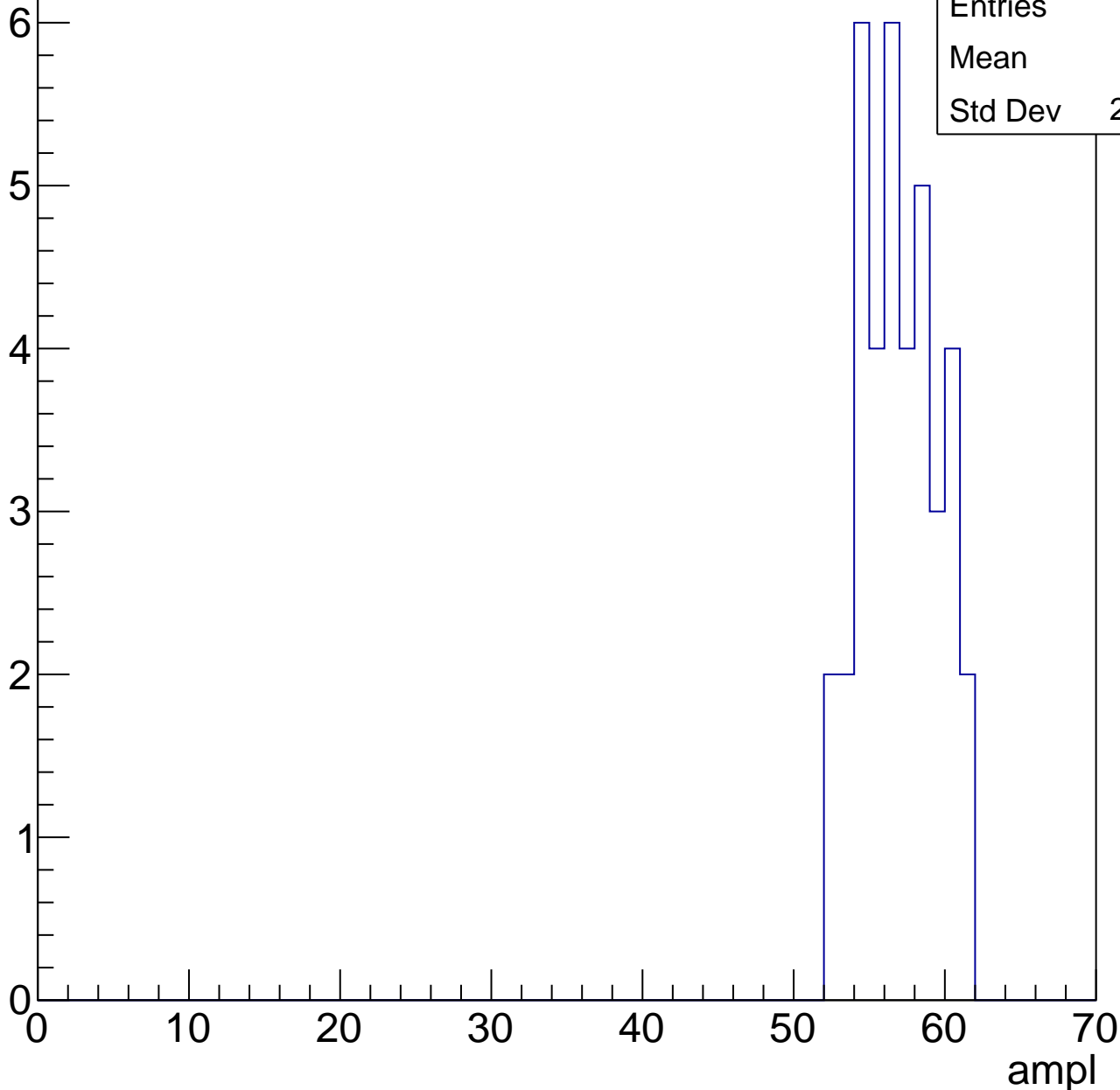


# B0L102S, U6-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	38
Mean	56.5
Std Dev	2.479

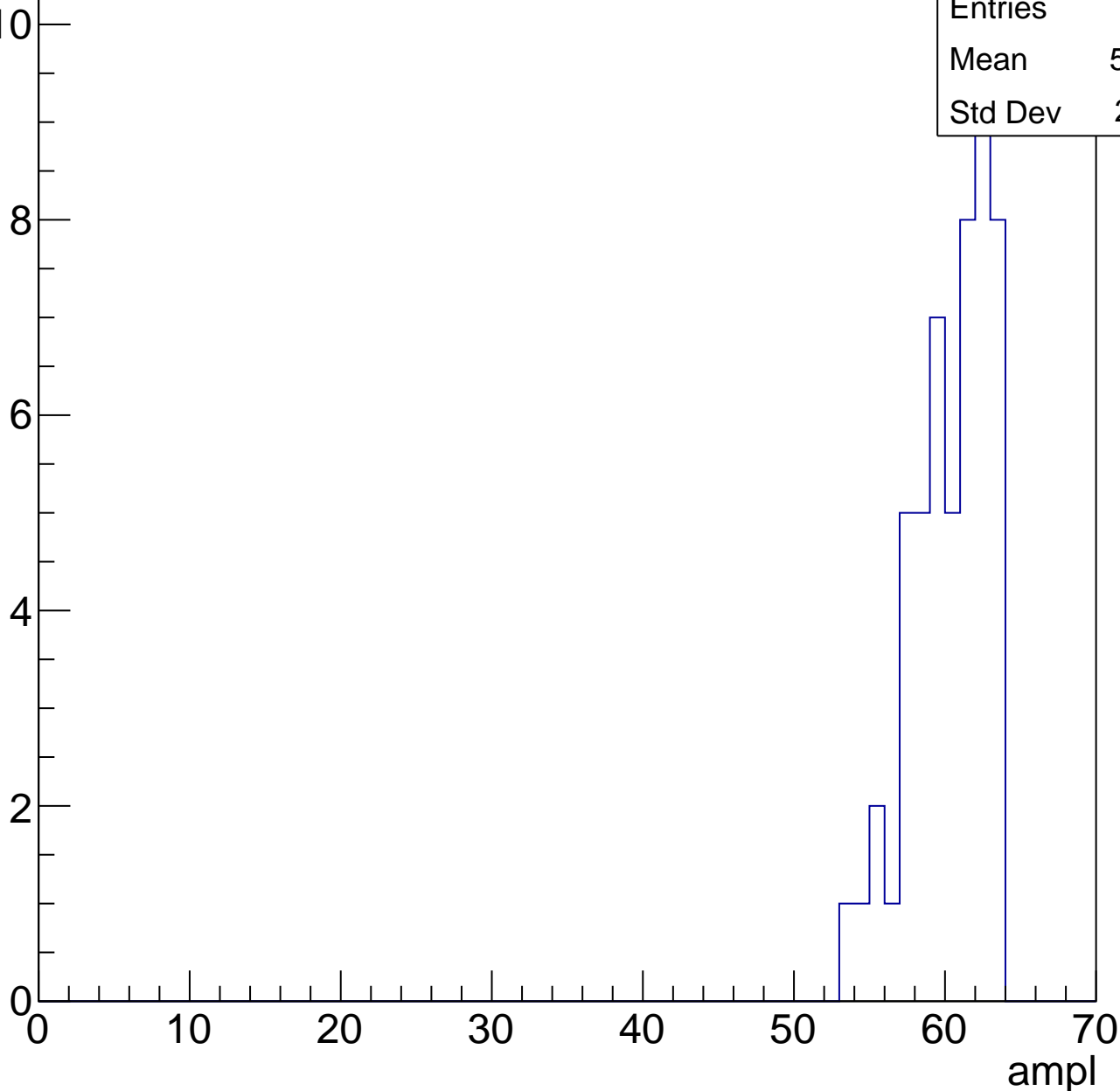


# B0L102S, U6-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	59.87
Std Dev	2.541



# B0L102S, U6-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



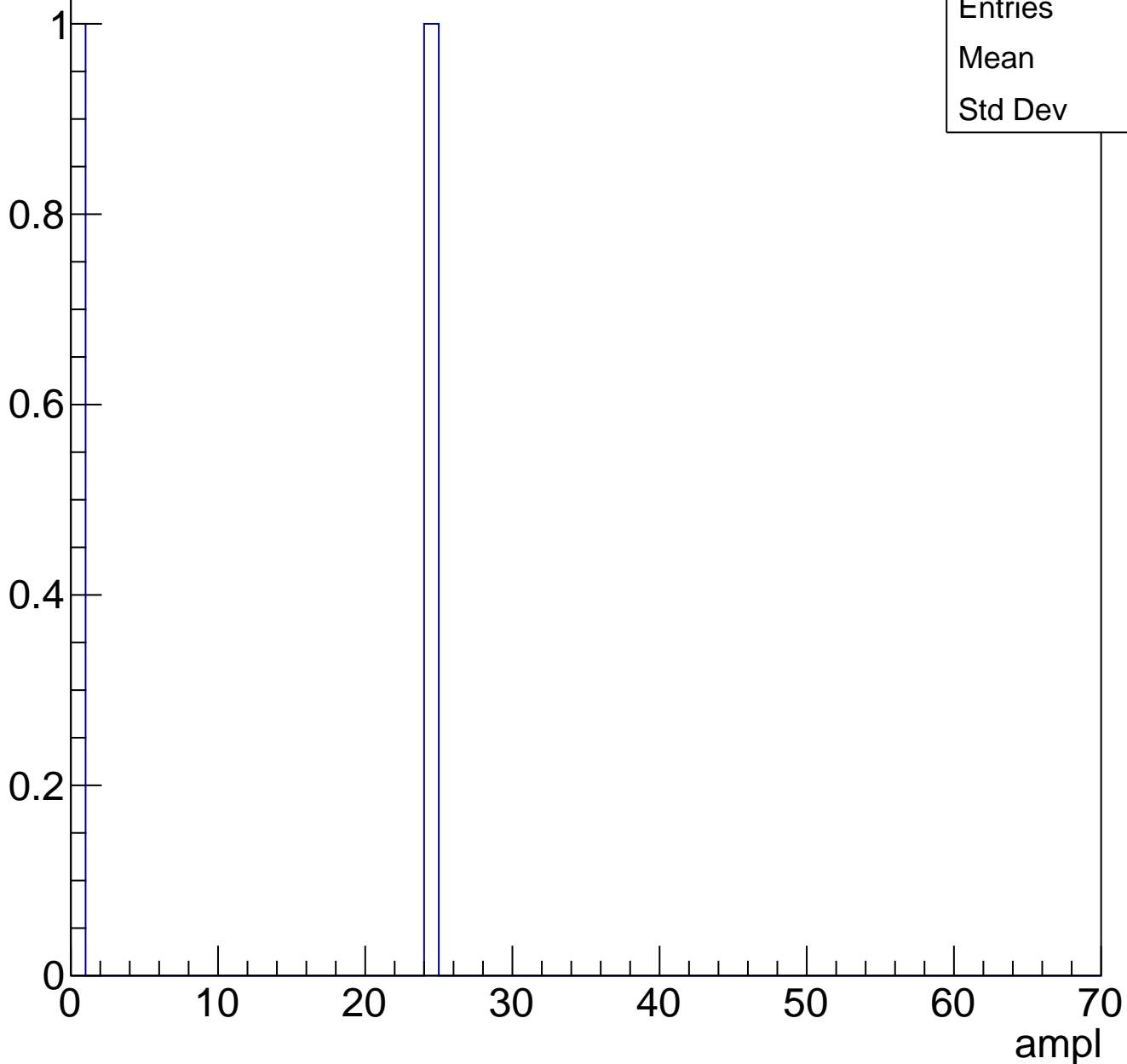
Entries	3
Mean	62
Std Dev	1.414



# B0L102S, U6-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch28, adc0

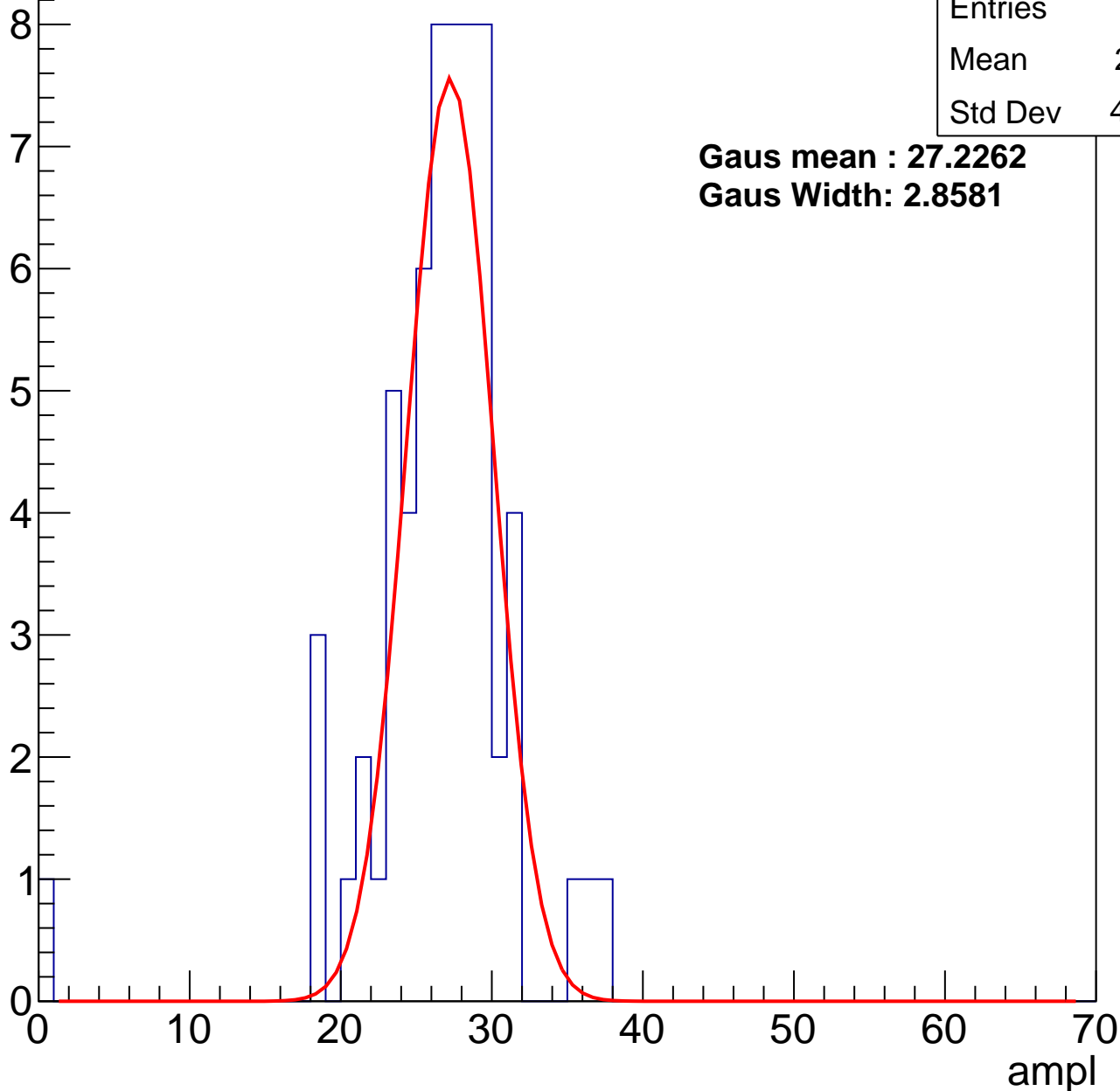
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	26.11
Std Dev	4.972

**Gaus mean : 27.2262**

**Gaus Width: 2.8581**



# B0L102S, U6-ch28, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	84
Mean	34.37
Std Dev	3.738

**Gaus mean : 33.8636**

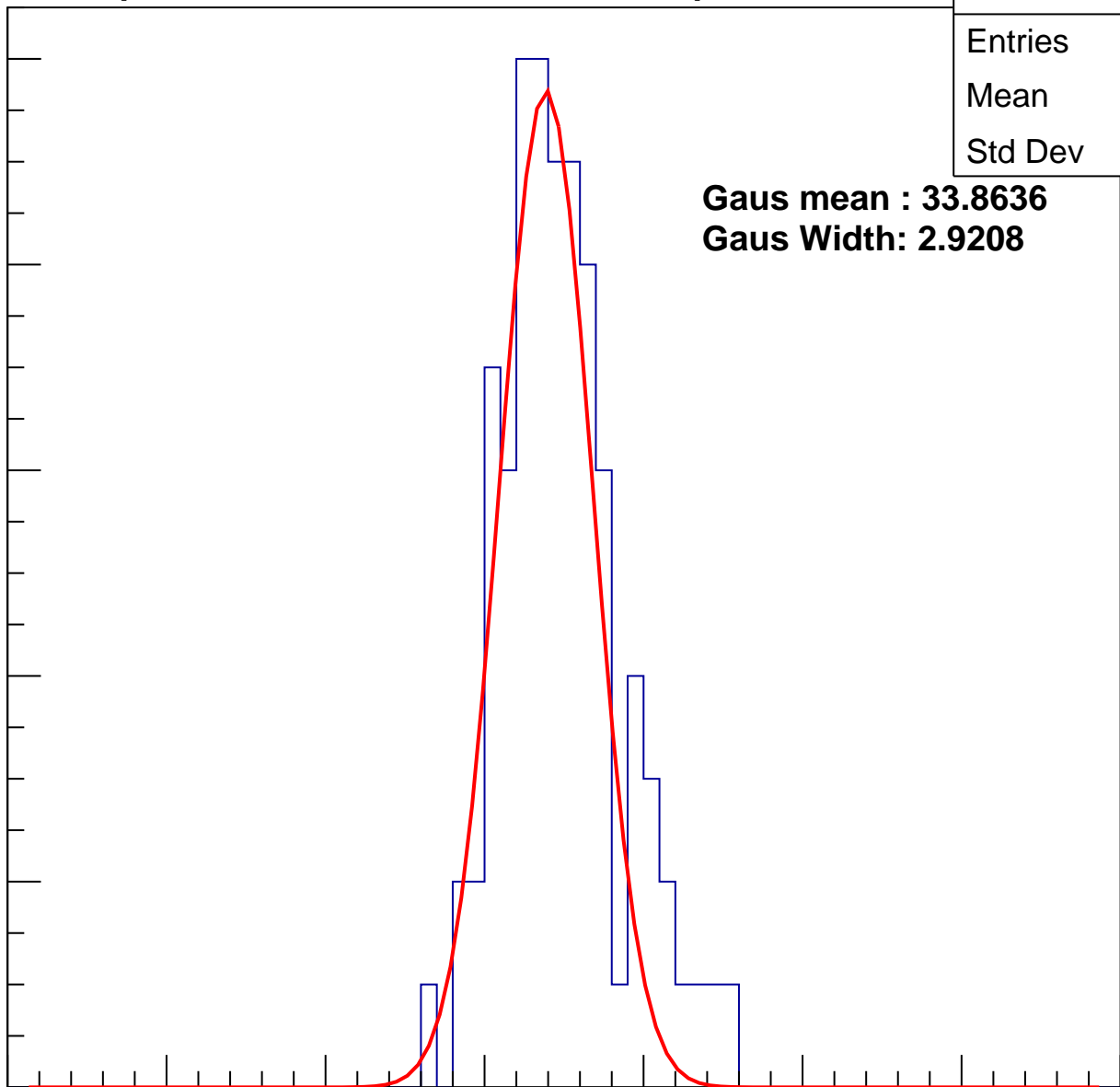
**Gaus Width: 2.9208**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch28, adc2

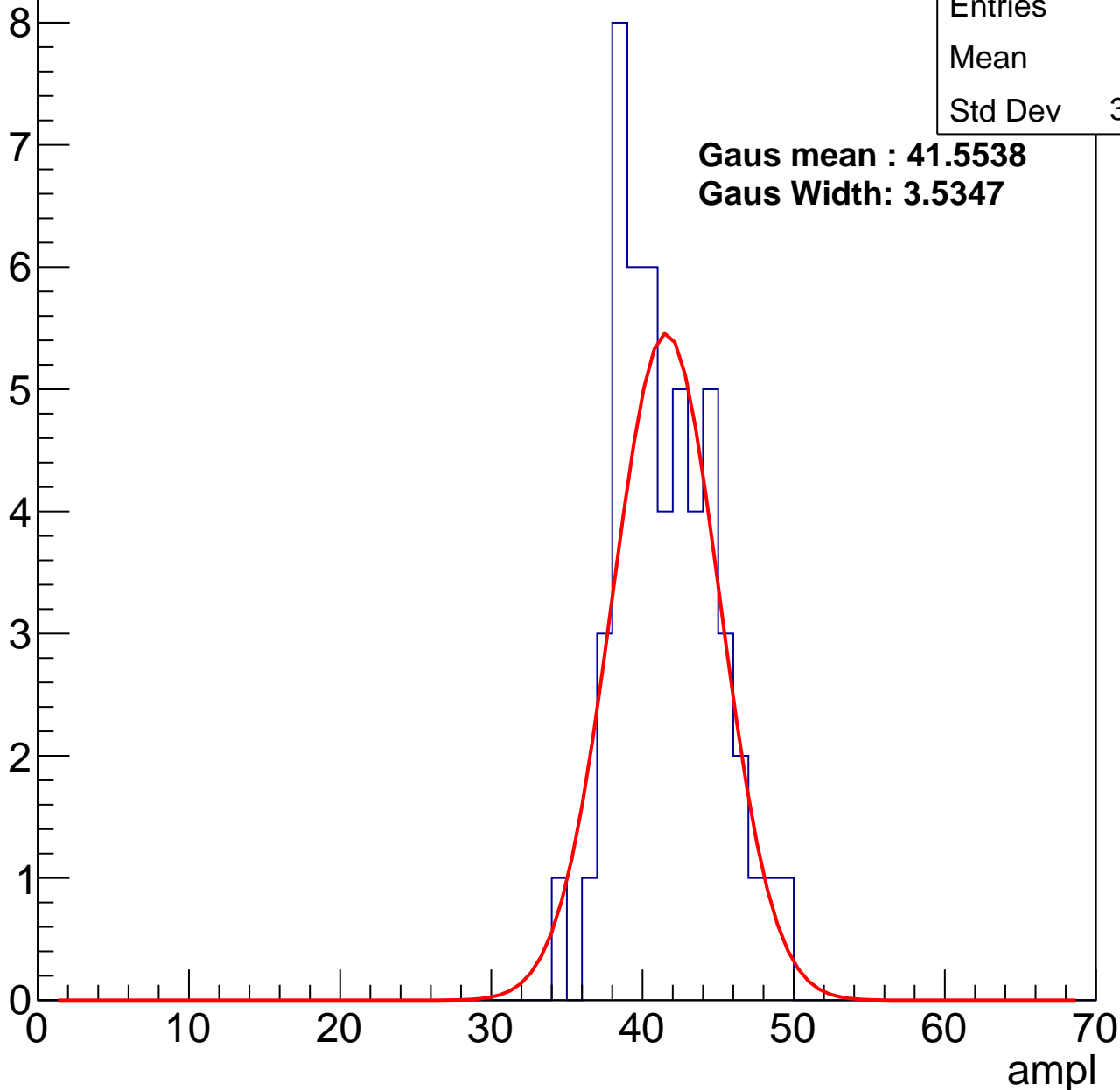
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	41.1
Std Dev	3.243

**Gaus mean : 41.5538**

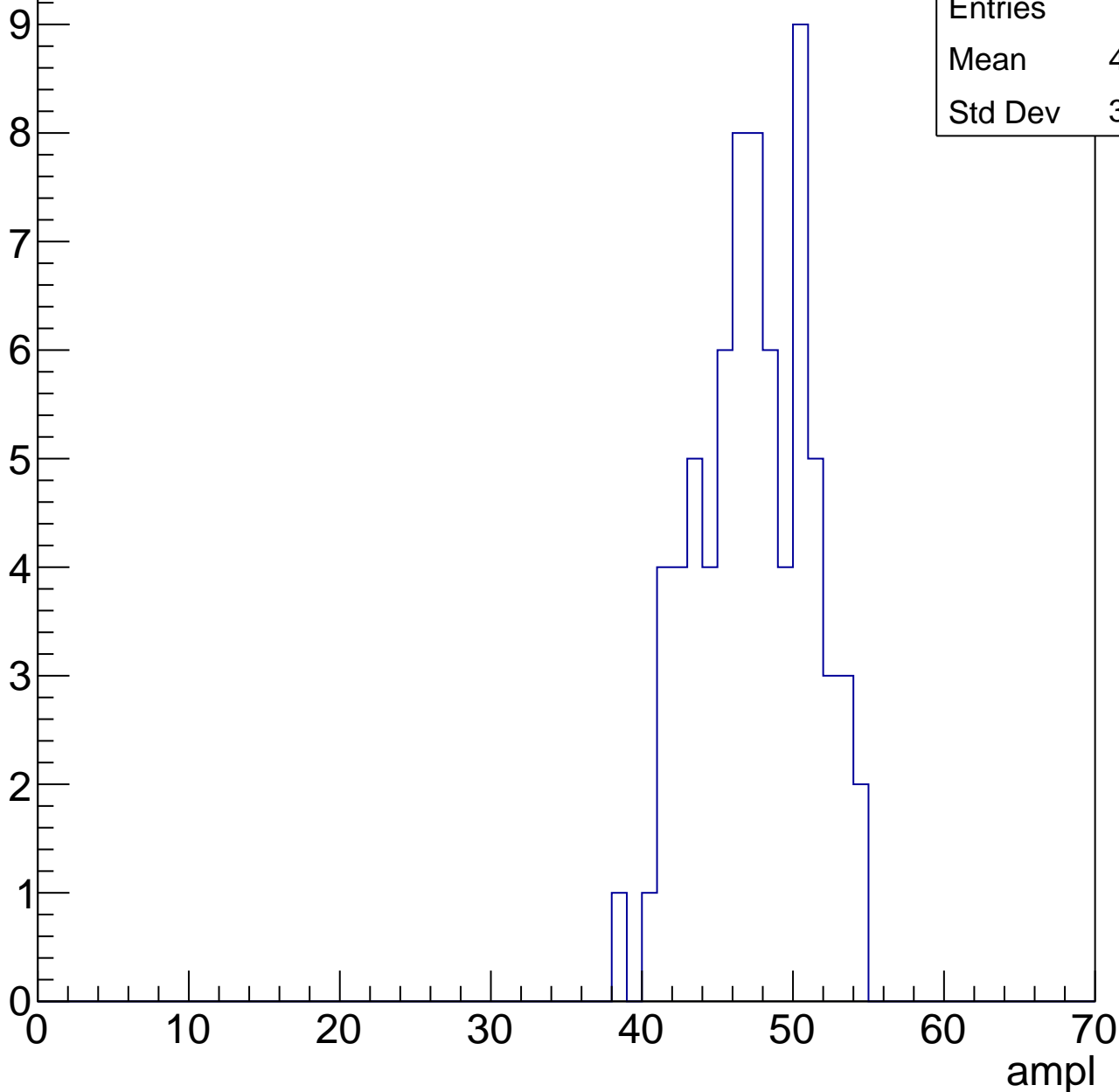
**Gaus Width: 3.5347**



# B0L102S, U6-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

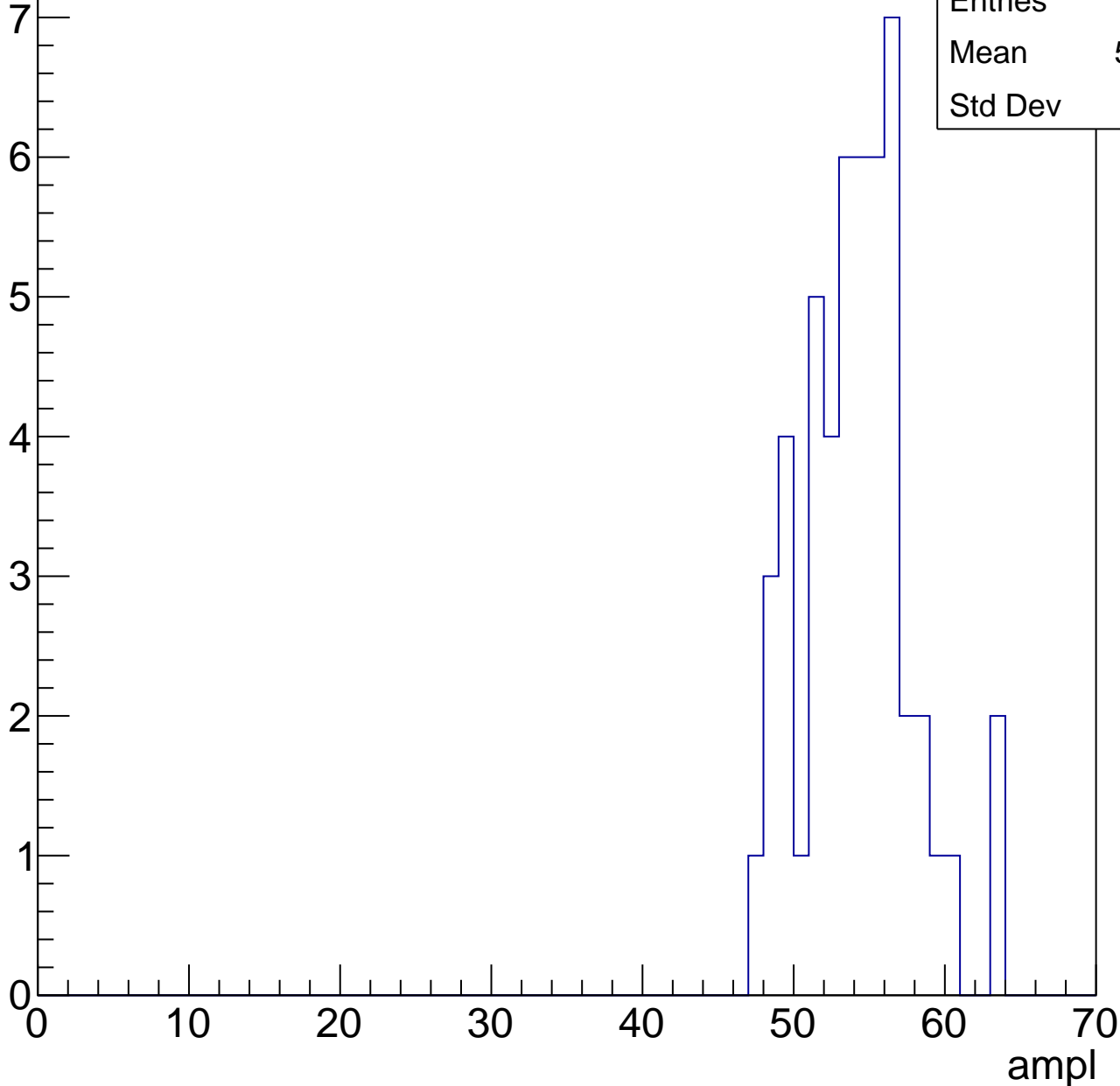


# B0L102S, U6-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	53.71
Std Dev	3.55

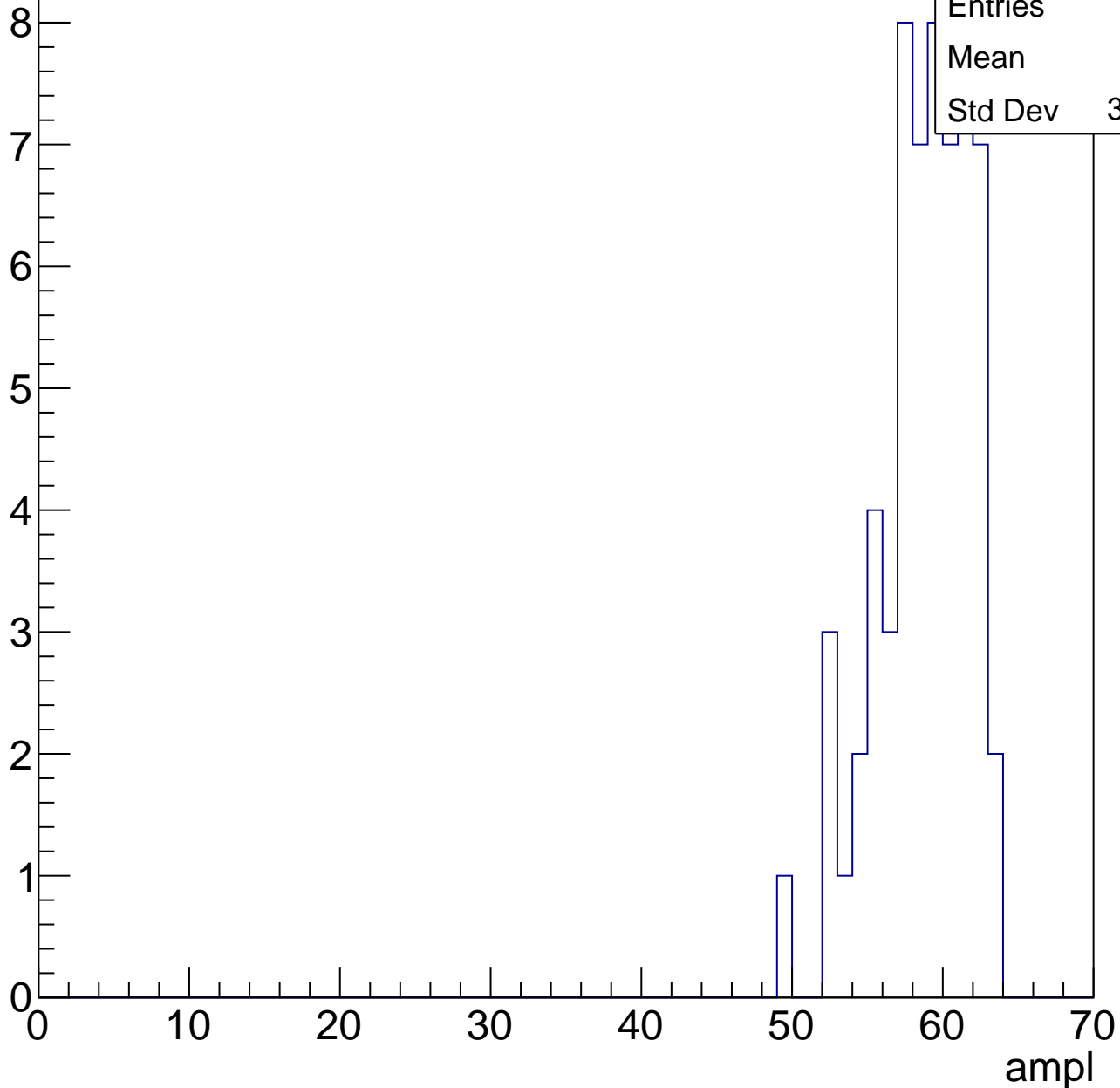


# B0L102S, U6-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	58.3
Std Dev	3.048

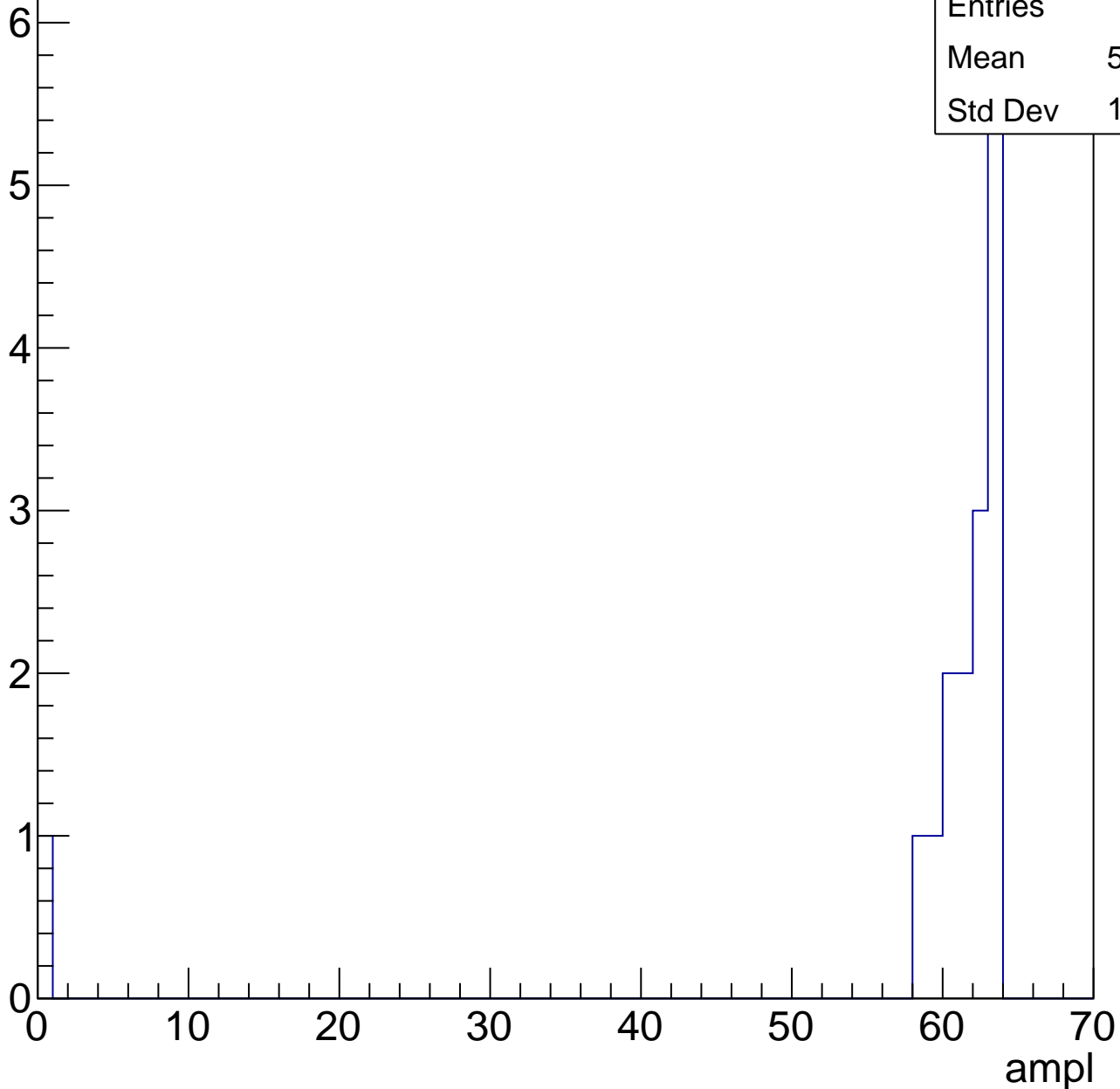


# B0L102S, U6-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	16
Mean	57.69
Std Dev	14.97





# B0L102S, U6-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch29, adc0

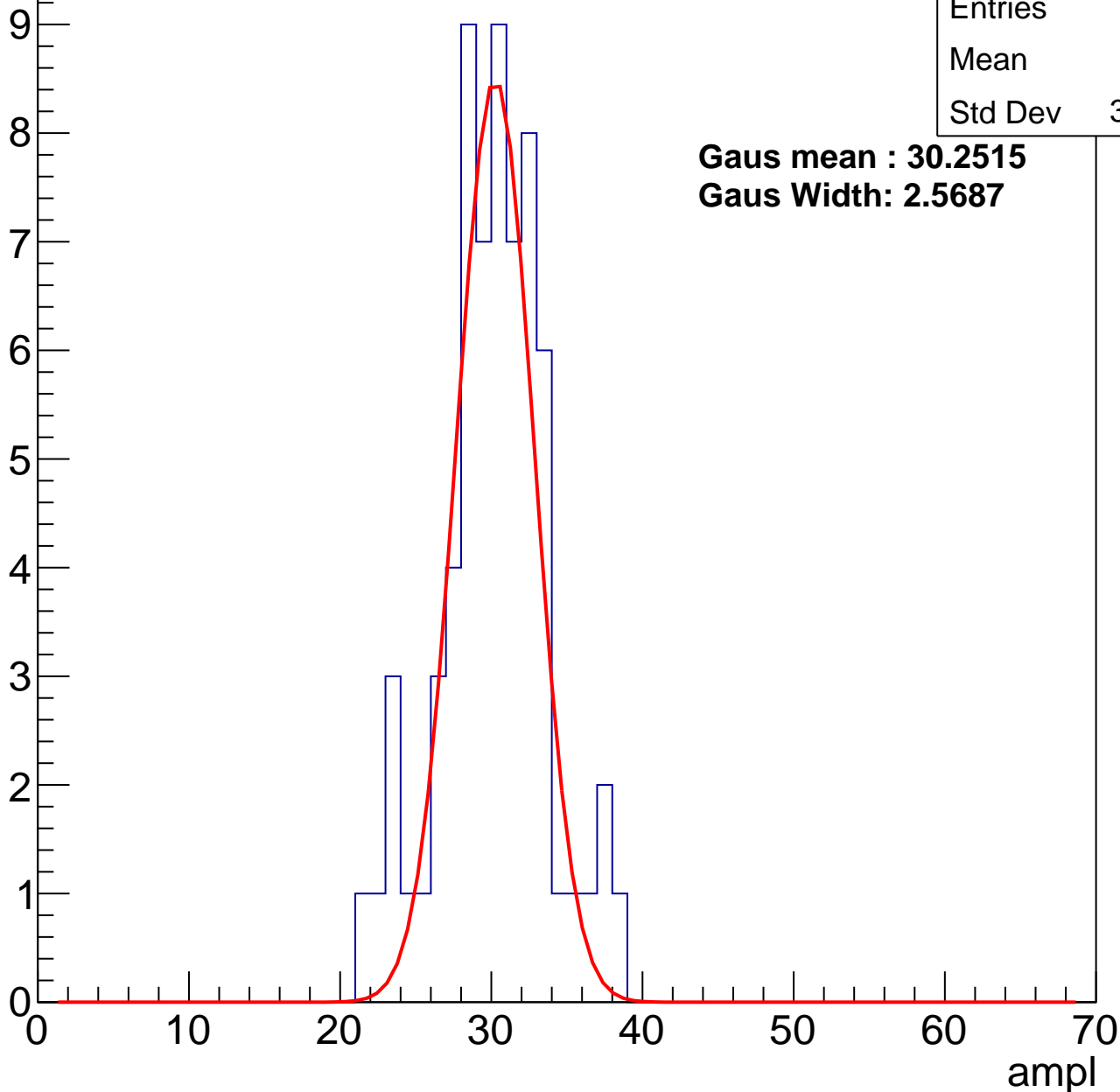
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	29.7
Std Dev	3.494

**Gaus mean : 30.2515**

**Gaus Width: 2.5687**



# B0L102S, U6-ch29, adc1

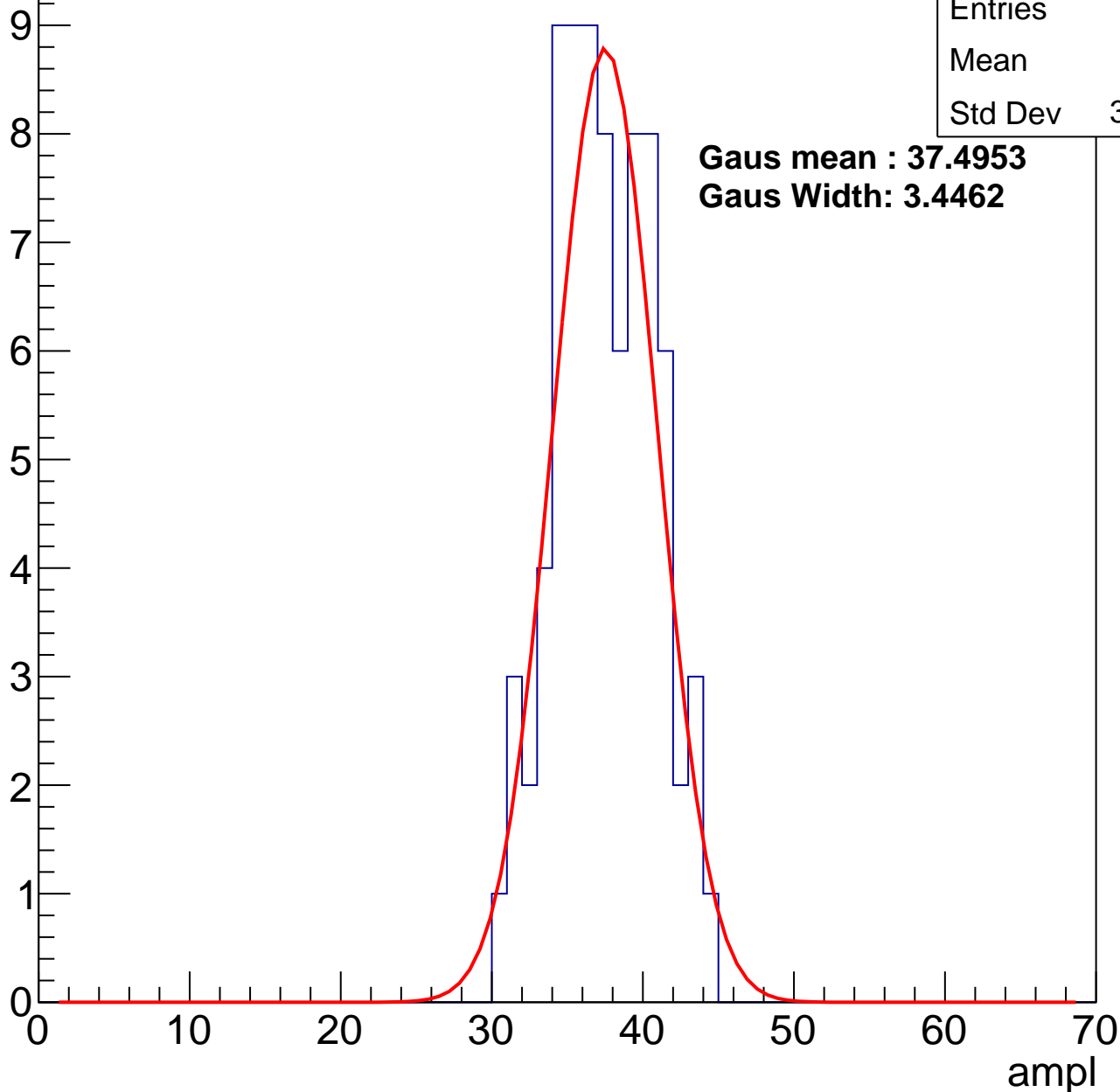
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	37
Std Dev	3.202

**Gaus mean : 37.4953**

**Gaus Width: 3.4462**



# B0L102S, U6-ch29, adc2

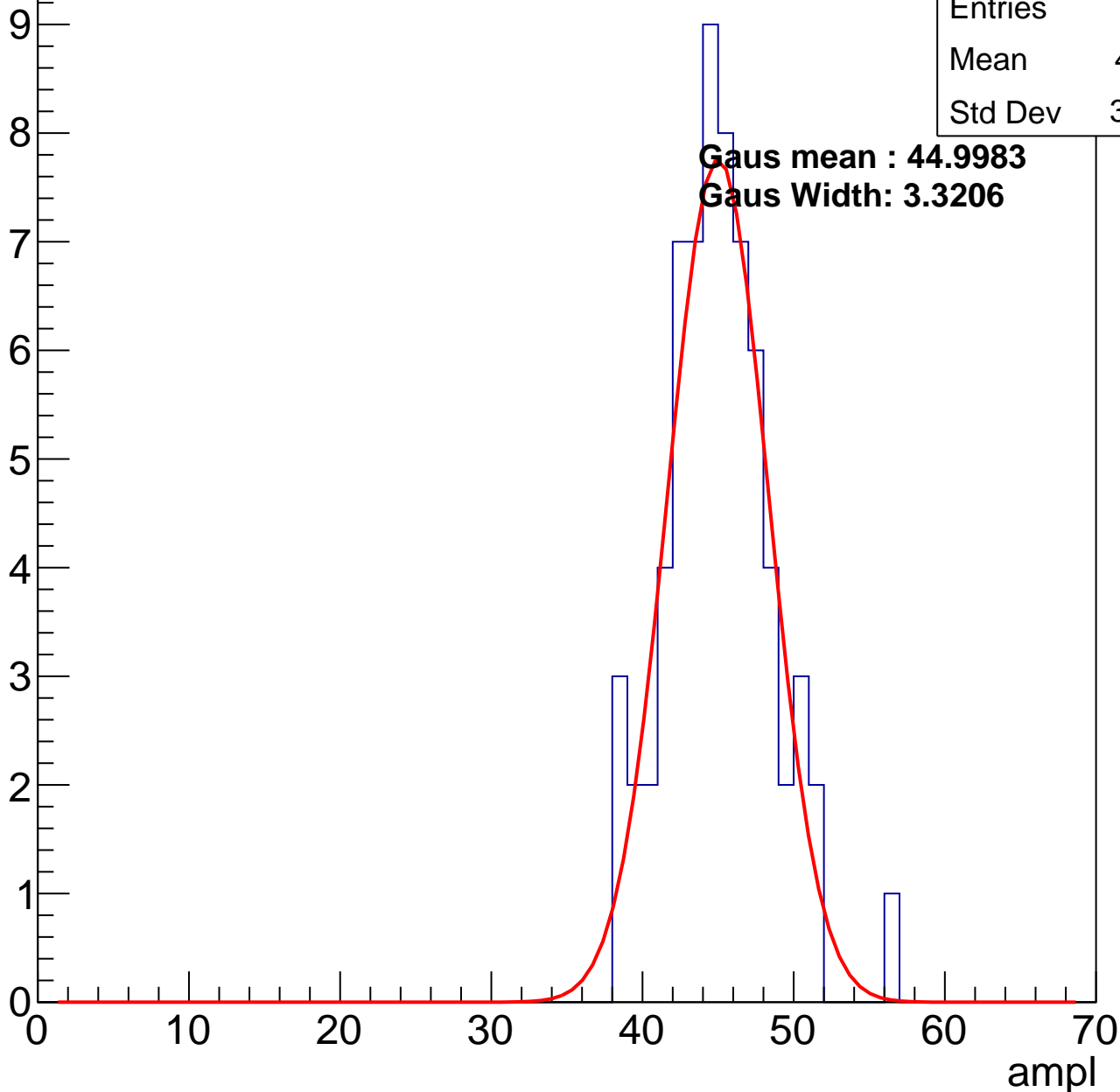
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	44.61
Std Dev	3.434

**Gaus mean : 44.9983**

**Gaus Width: 3.3206**

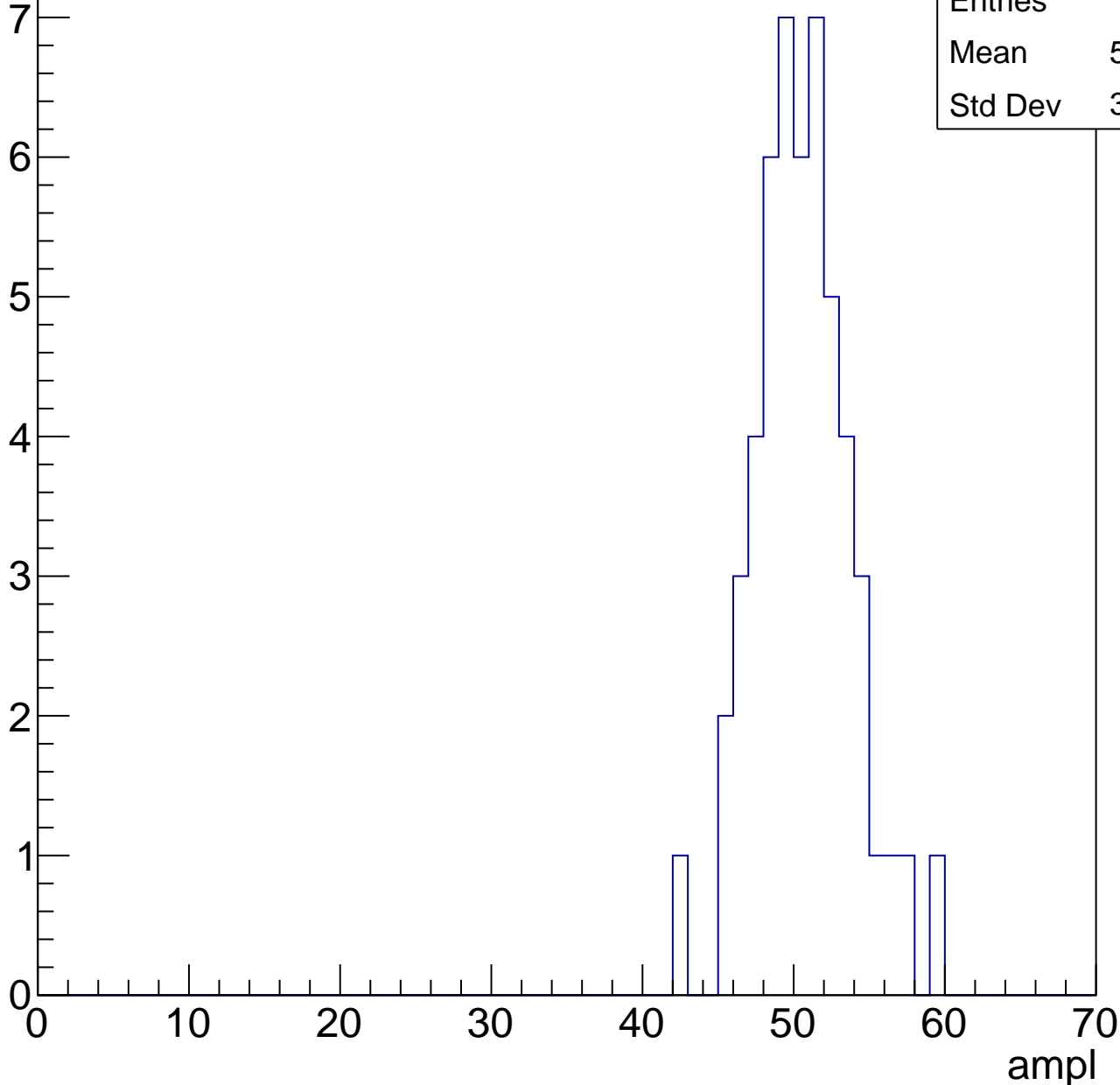


# B0L102S, U6-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

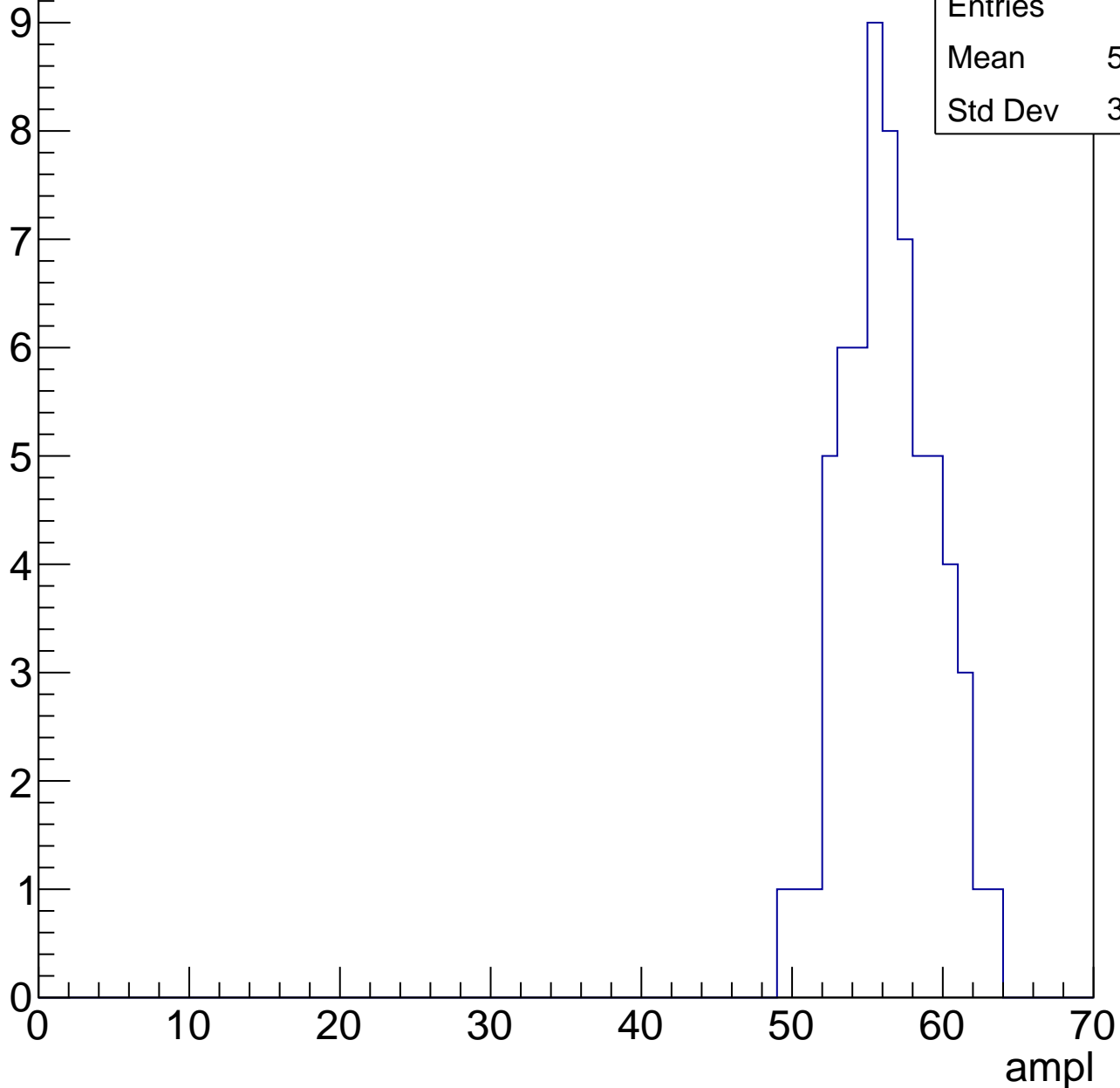
Entries	52
Mean	50.13
Std Dev	3.193



# B0L102S, U6-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



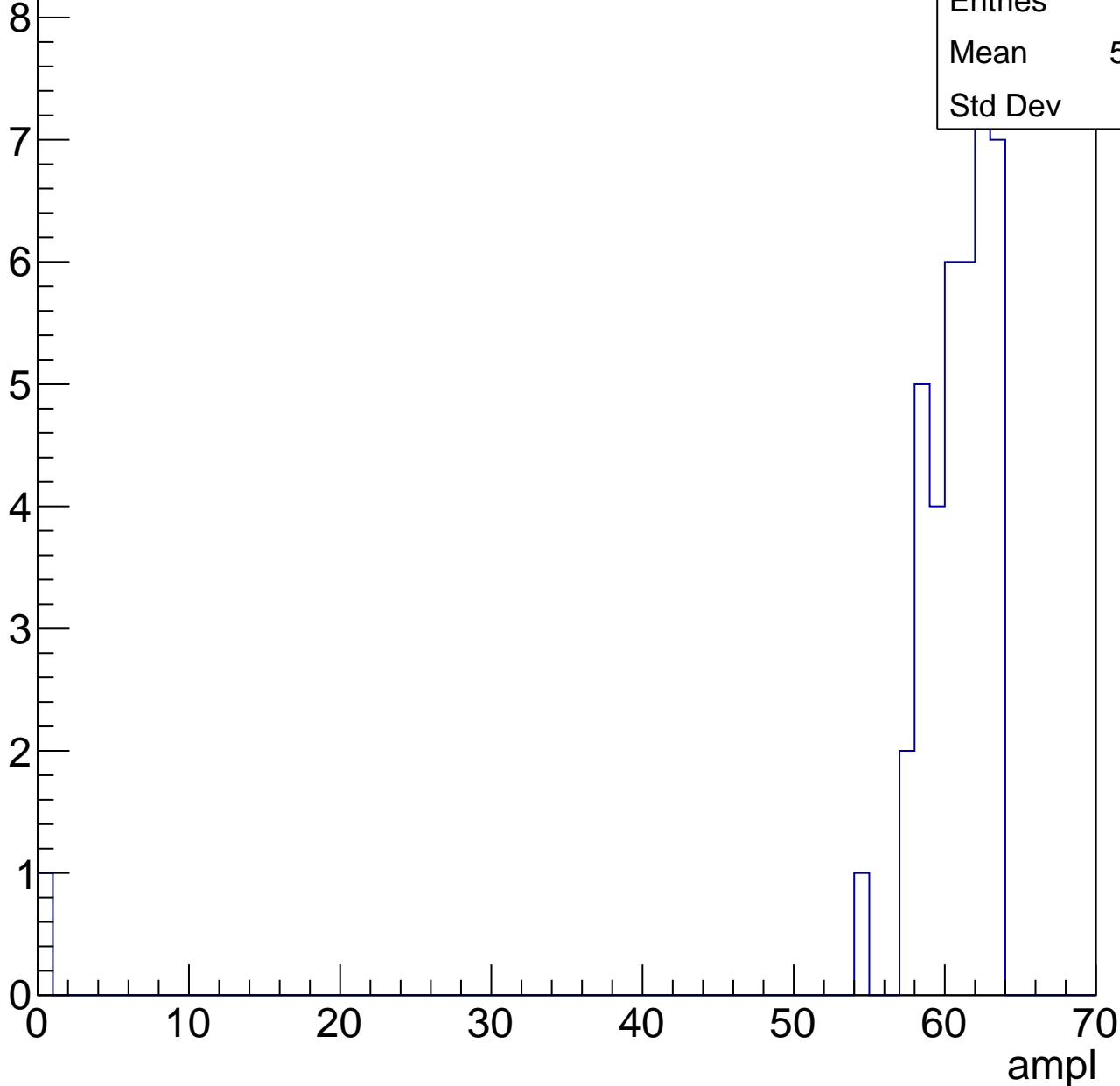
Entries	63
Mean	55.98
Std Dev	3.016

# B0L102S, U6-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	58.92
Std Dev	9.66



# B0L102S, U6-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

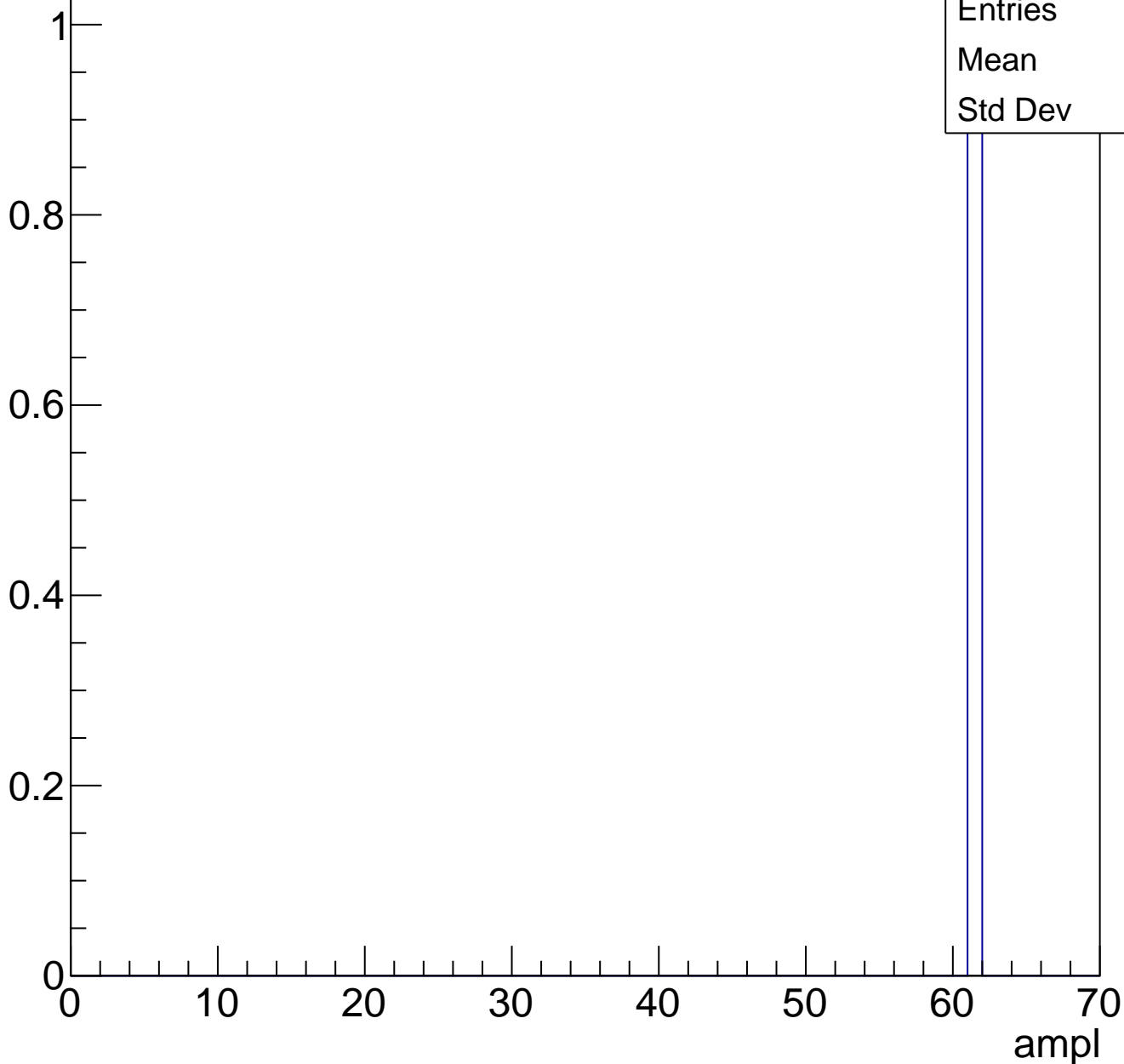
0 10 20 30 40 50 60 70



# B0L102S, U6-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch30, adc0

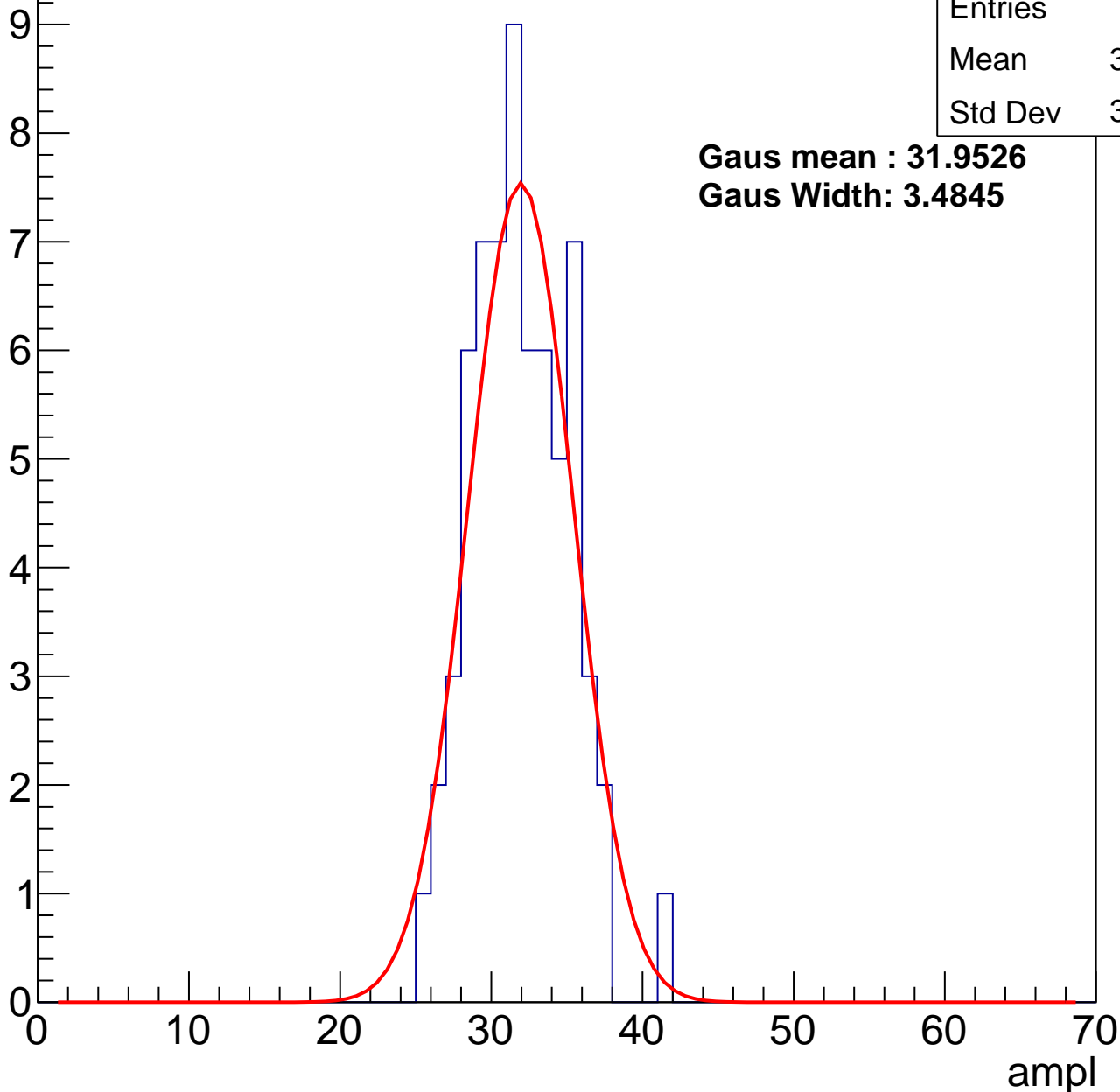
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	31.48
Std Dev	3.143

**Gaus mean : 31.9526**

**Gaus Width: 3.4845**



# B0L102S, U6-ch30, adc1

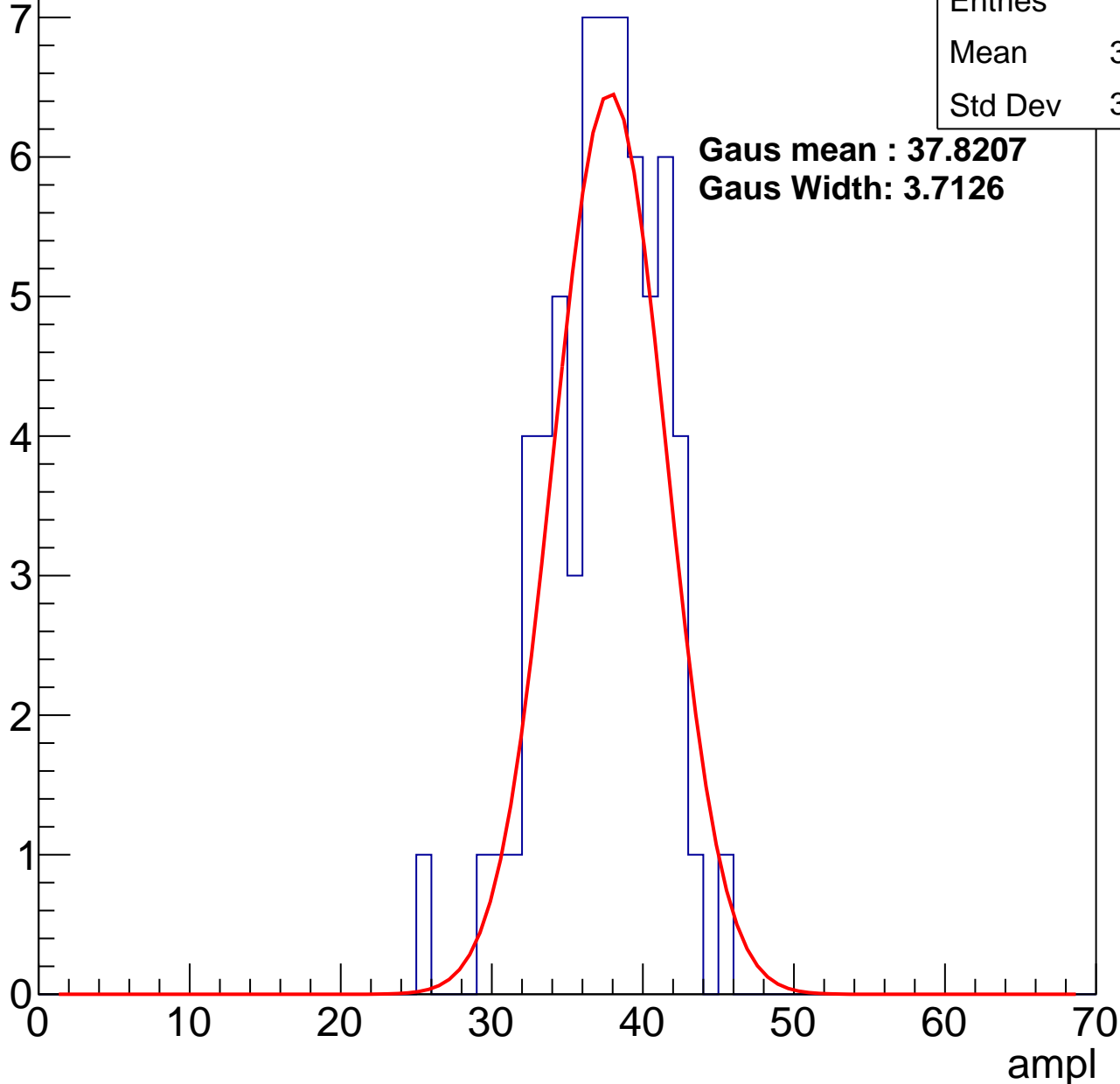
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	36.92
Std Dev	3.735

**Gaus mean : 37.8207**

**Gaus Width: 3.7126**



# B0L102S, U6-ch30, adc2

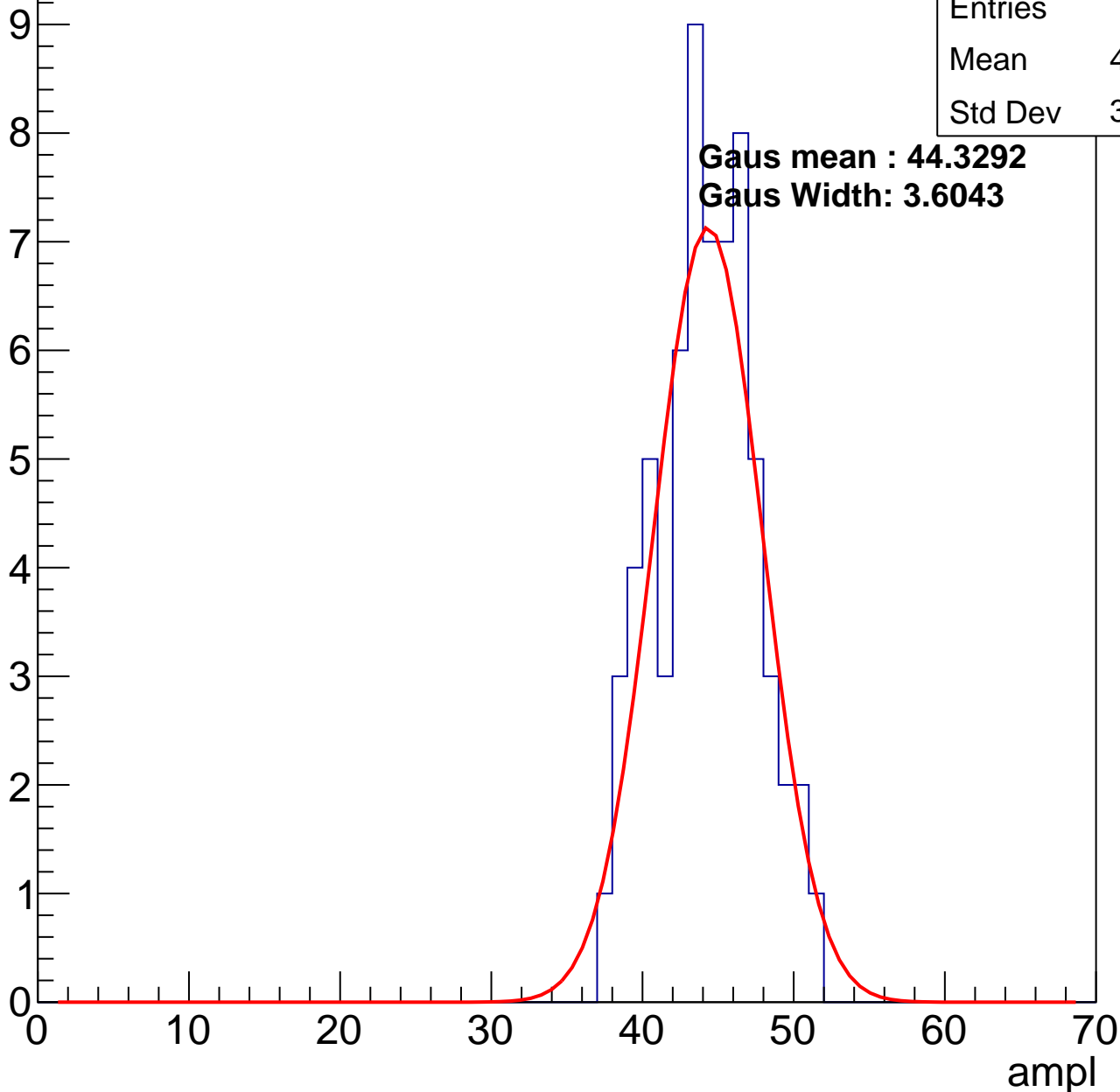
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	43.76
Std Dev	3.248

**Gaus mean : 44.3292**

**Gaus Width: 3.6043**

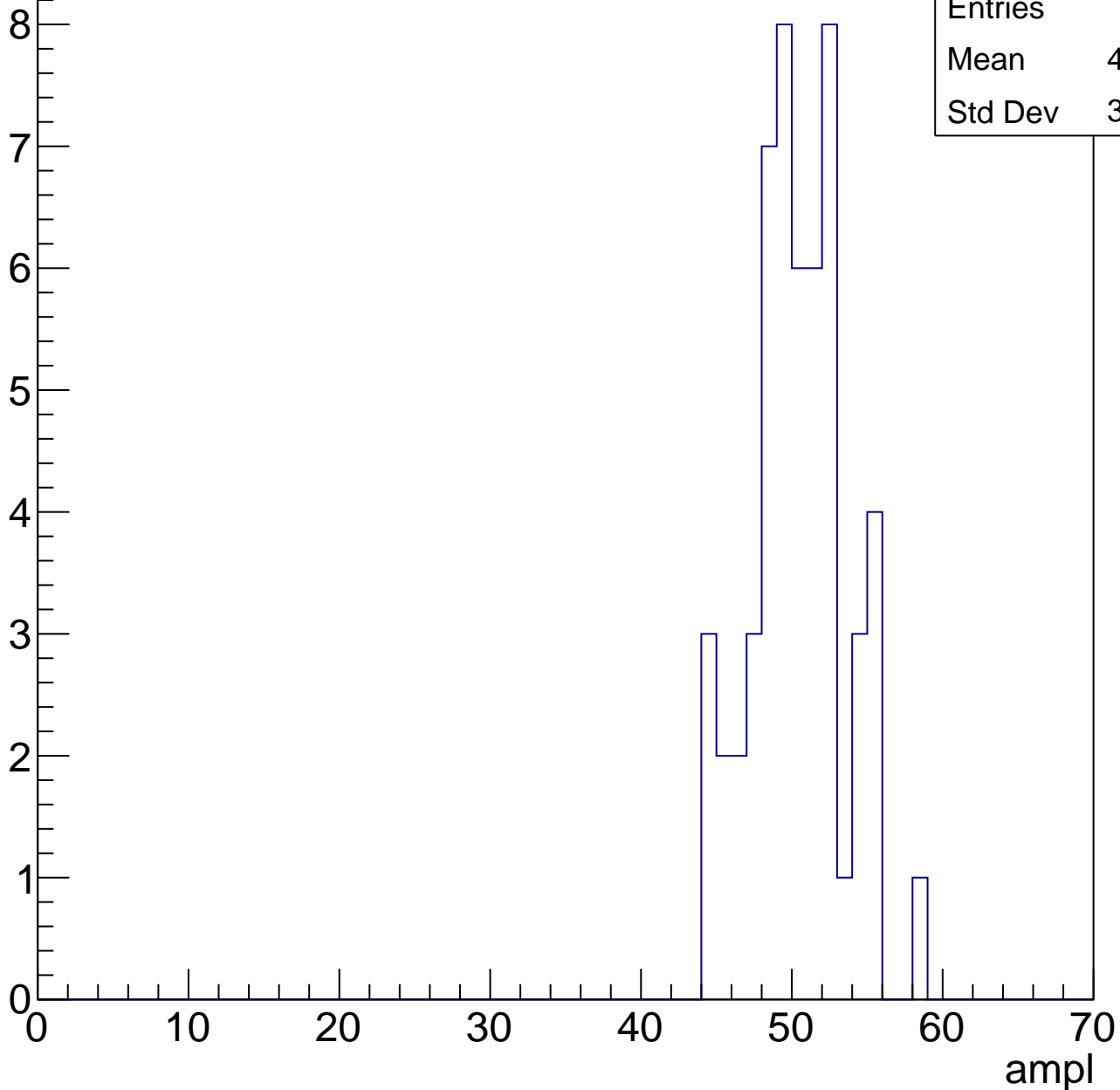


# B0L102S, U6-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	49.96
Std Dev	3.079



# B0L102S, U6-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	69
Mean	55.8
Std Dev	3.433

Entry

10

8

6

4

2

0

0

10

20

30

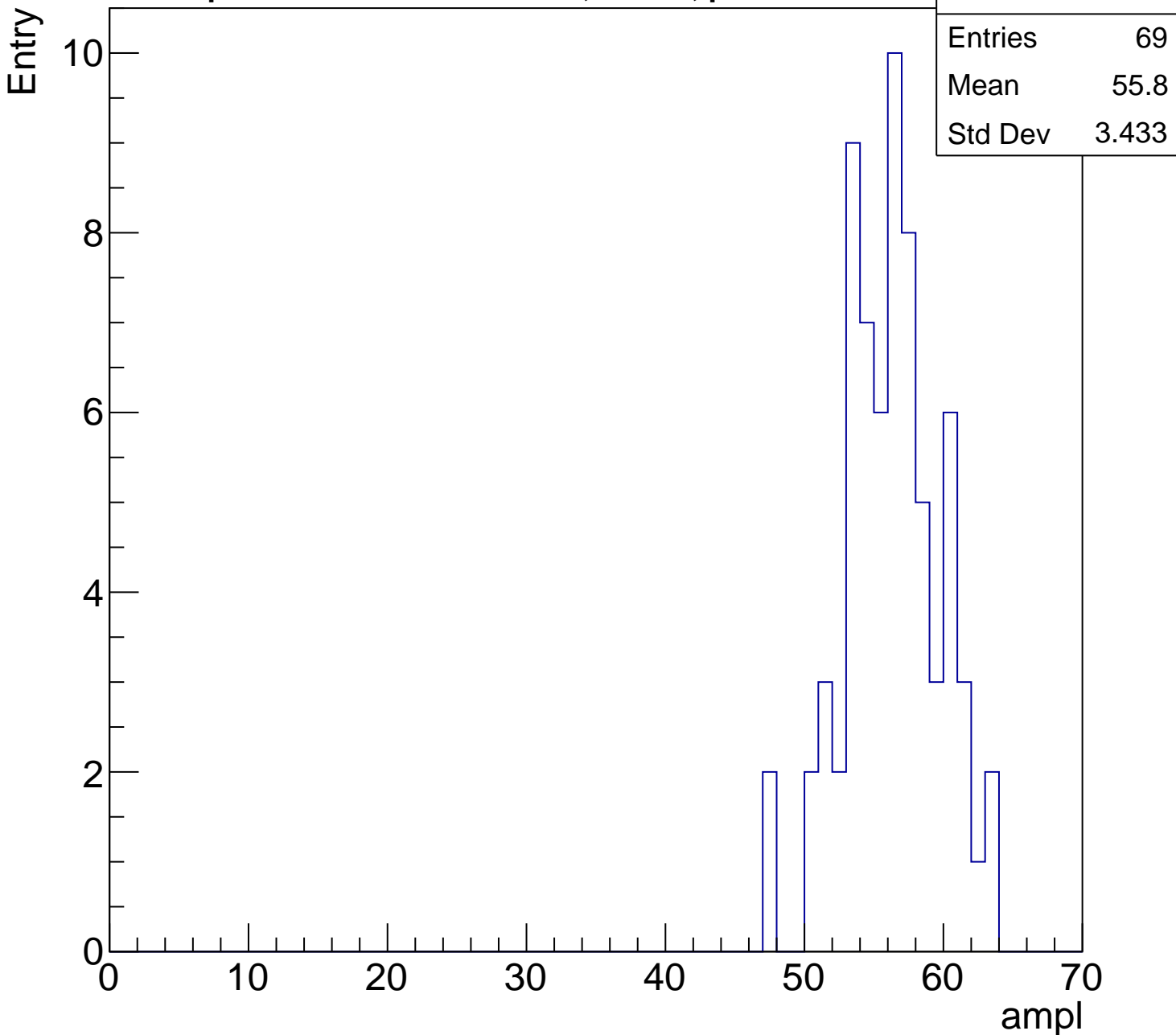
40

50

60

ampl

70



# B0L102S, U6-ch30, adc5

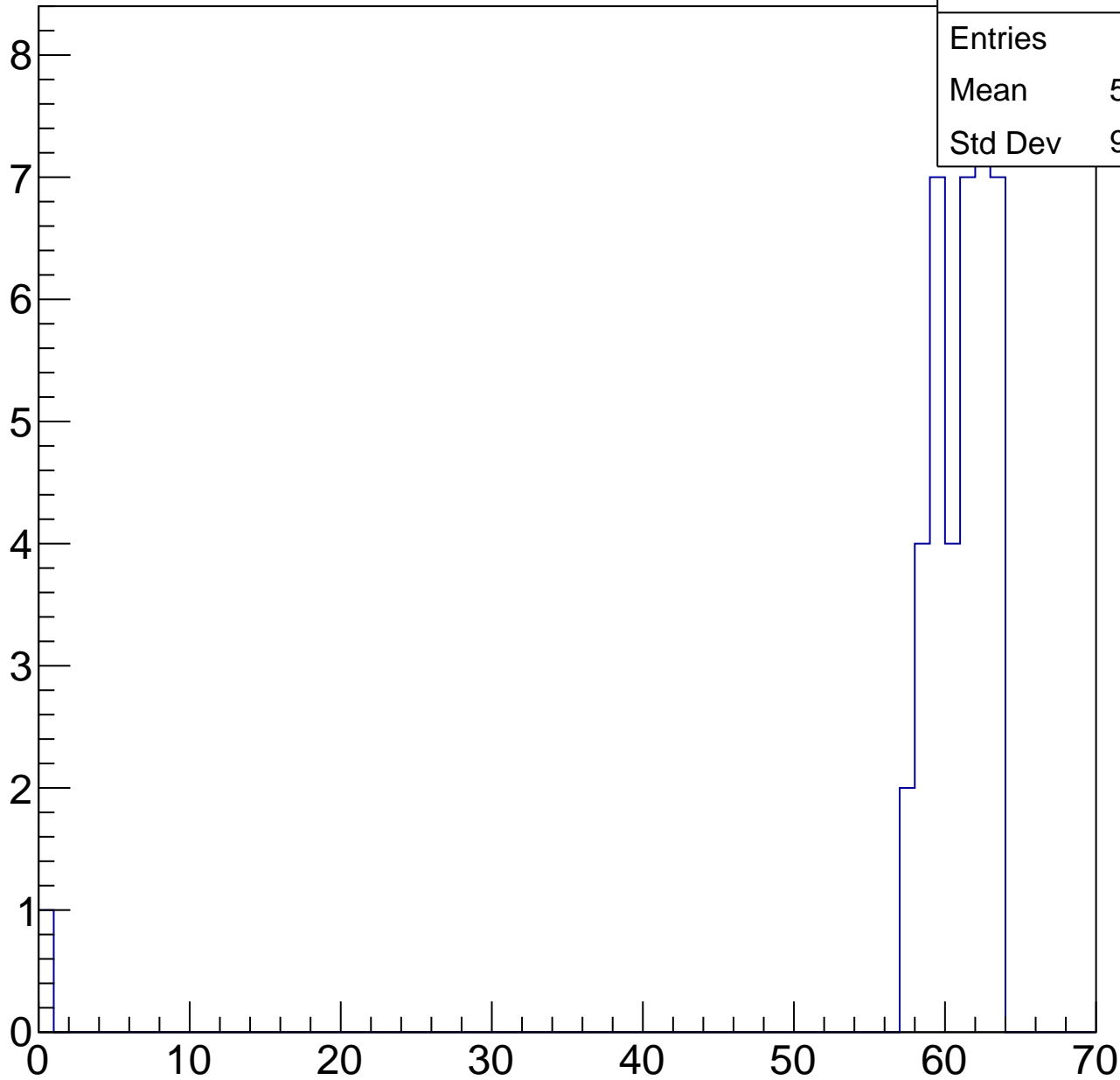
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	59.08
Std Dev	9.629

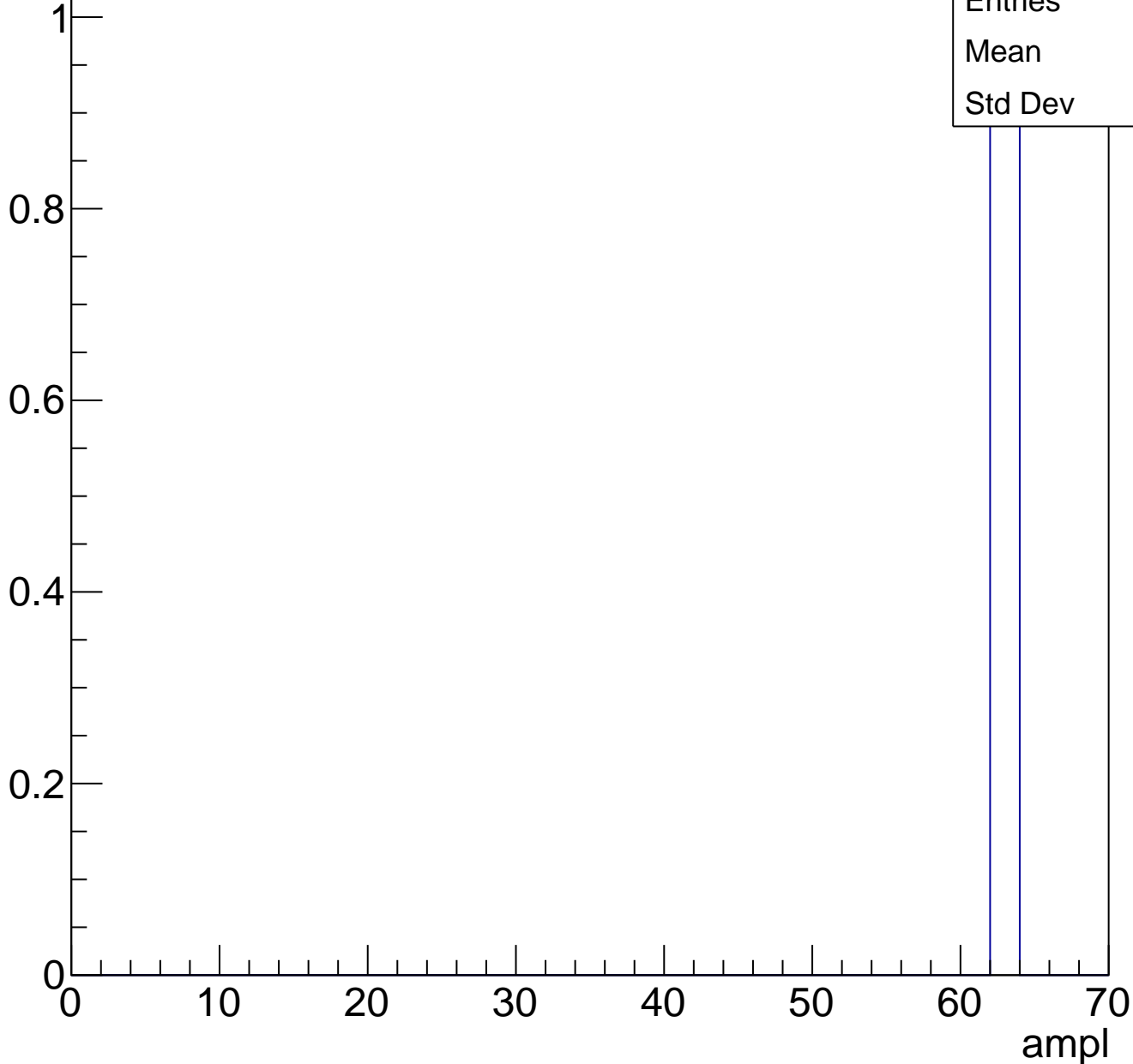
ampl



# B0L102S, U6-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch31, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	29.74
Std Dev	3.62

**Gaus mean : 30.0452**

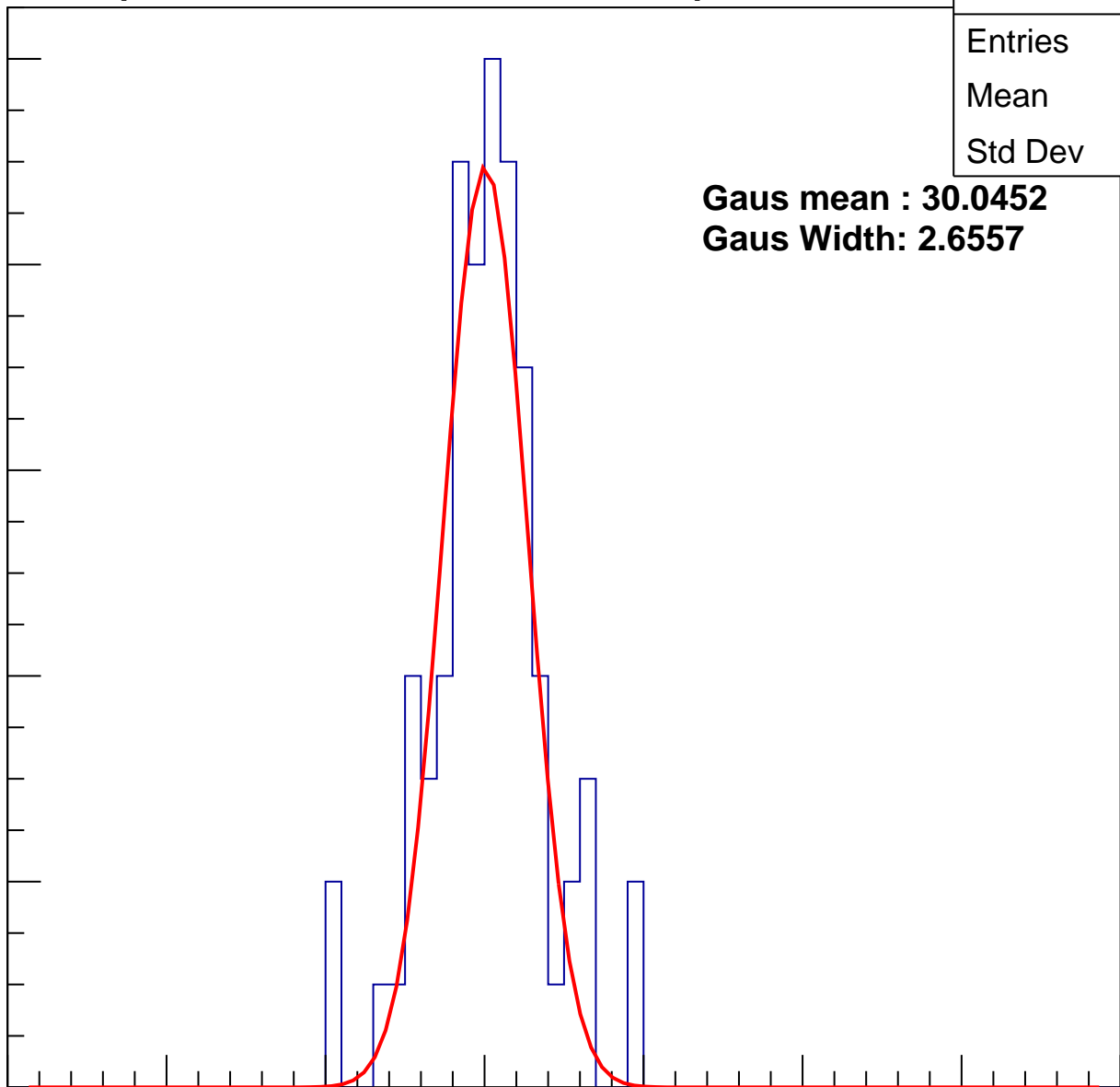
**Gaus Width: 2.6557**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch31, adc1

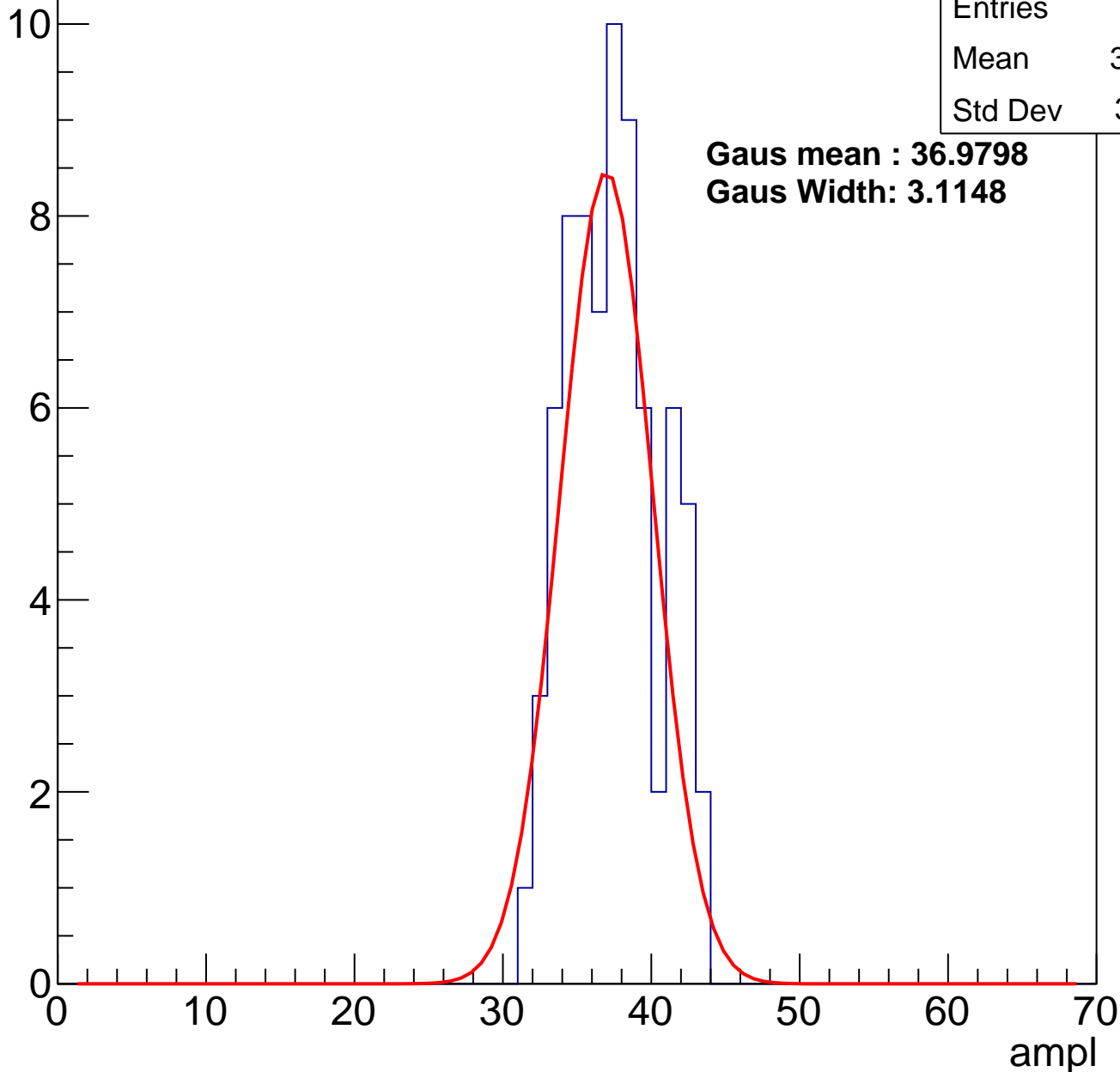
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	36.95
Std Dev	3.011

**Gaus mean : 36.9798**

**Gaus Width: 3.1148**

Entry



# B0L102S, U6-ch31, adc2

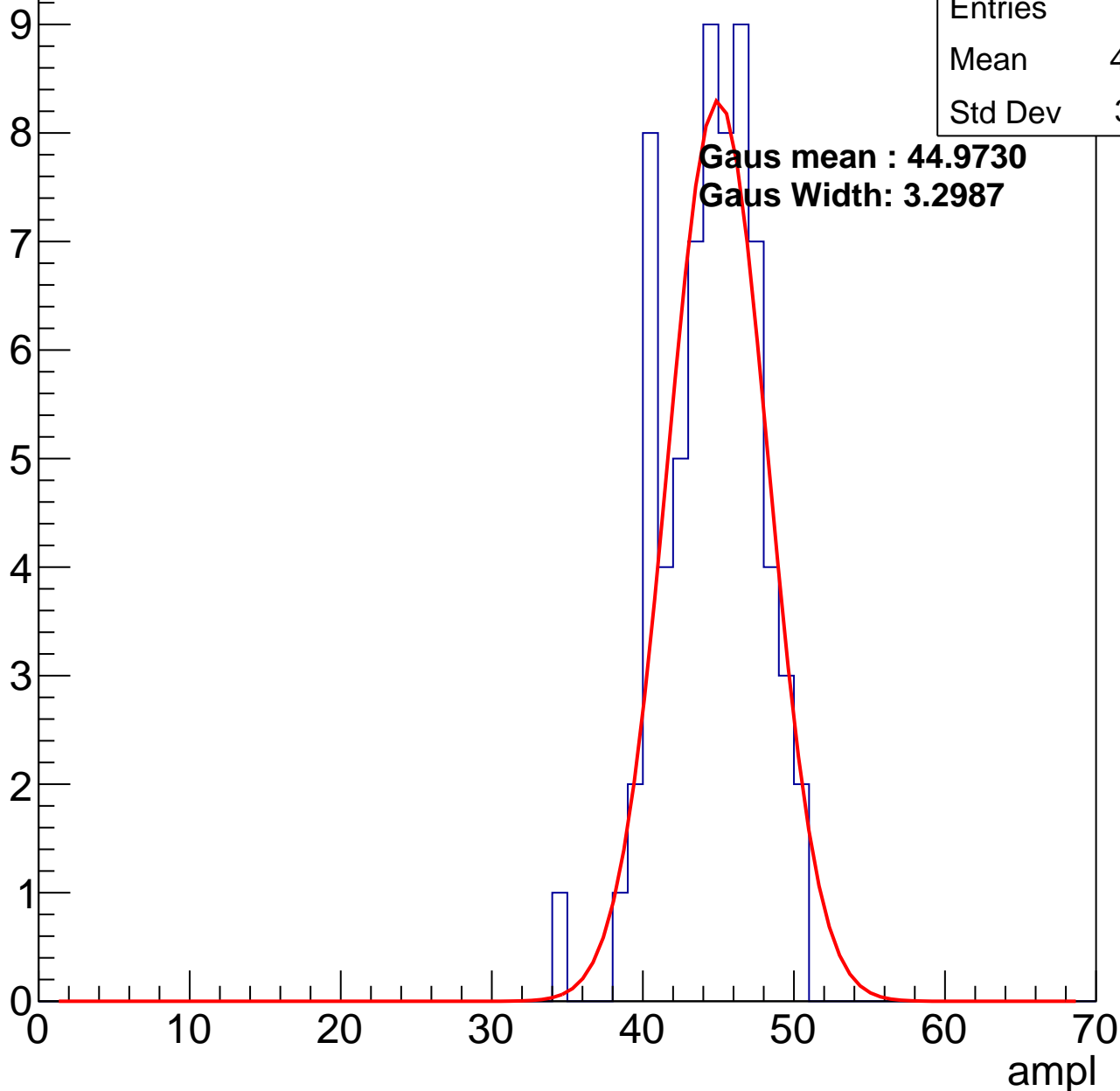
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	44.04
Std Dev	3.151

**Gaus mean : 44.9730**

**Gaus Width: 3.2987**

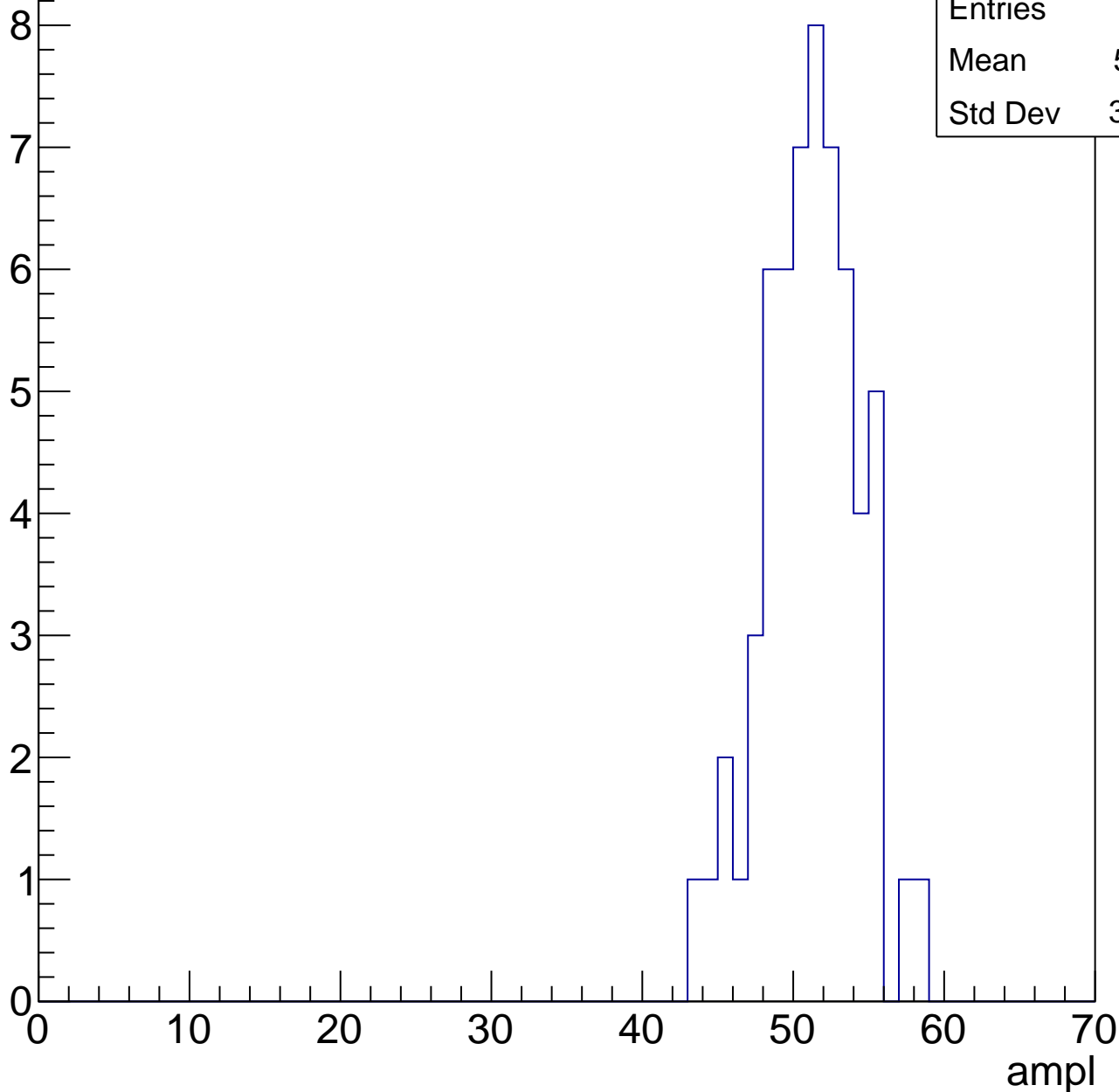


# B0L102S, U6-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	50.71
Std Dev	3.108

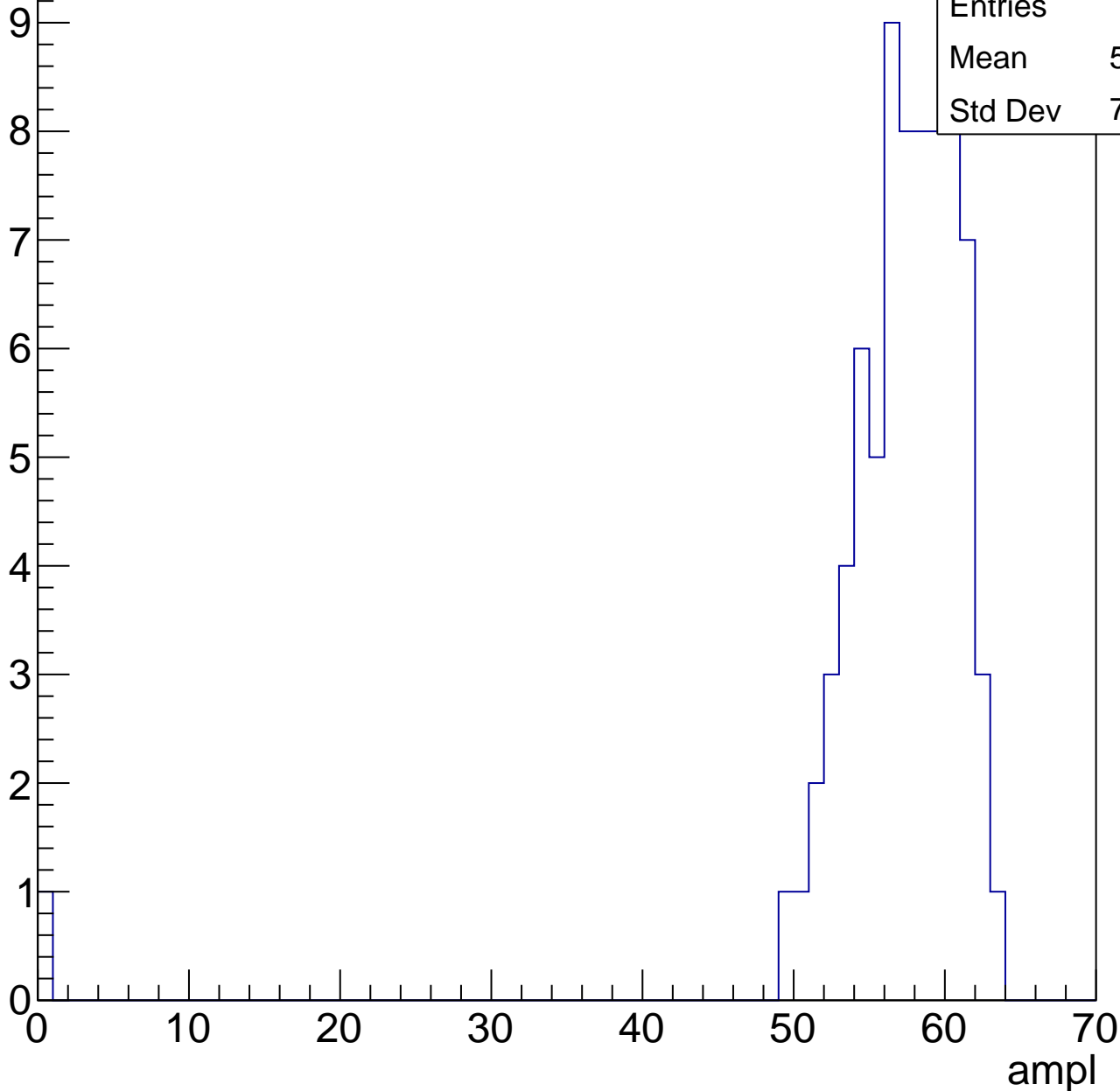


# B0L102S, U6-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	56.27
Std Dev	7.258

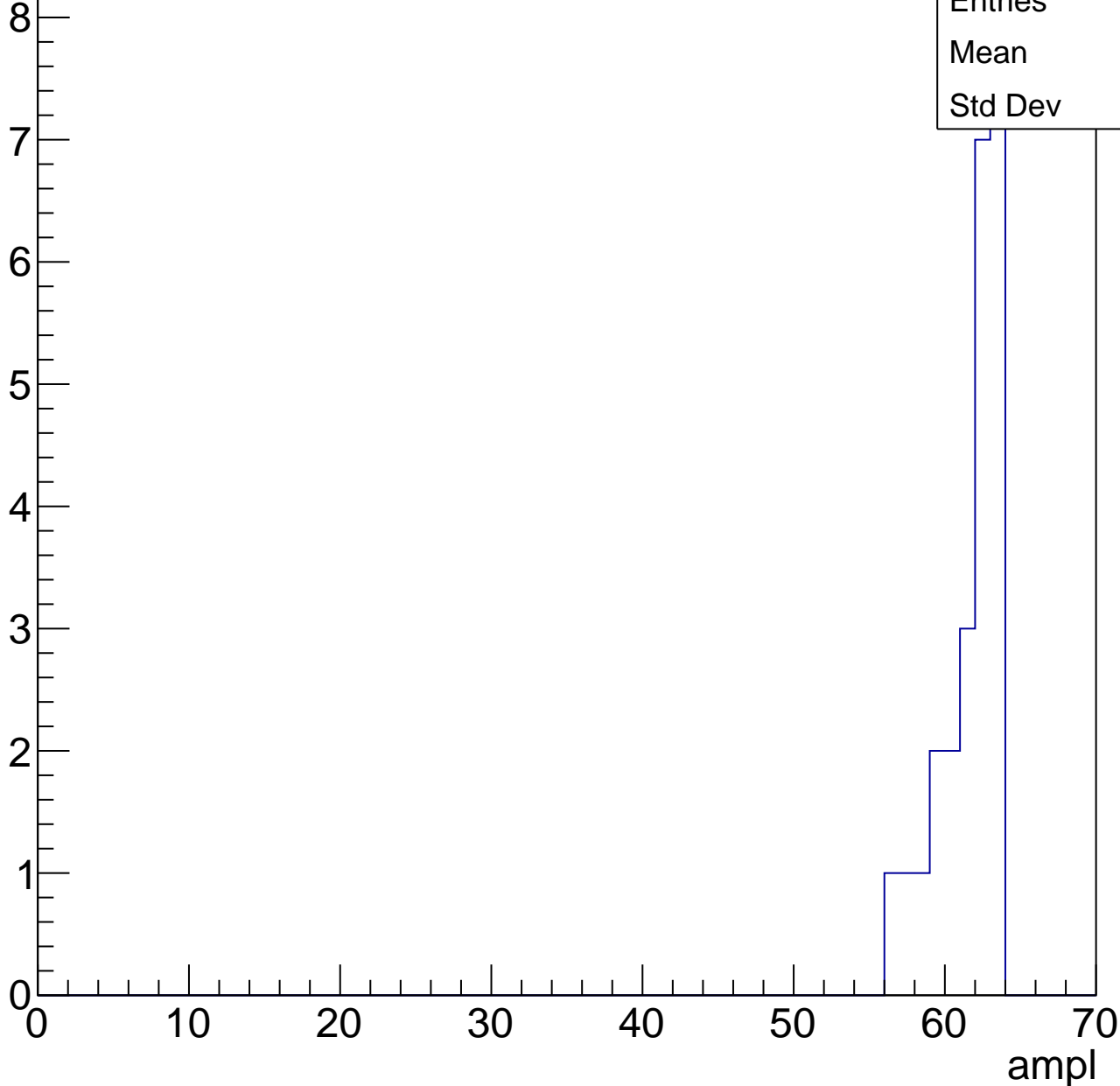


# B0L102S, U6-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

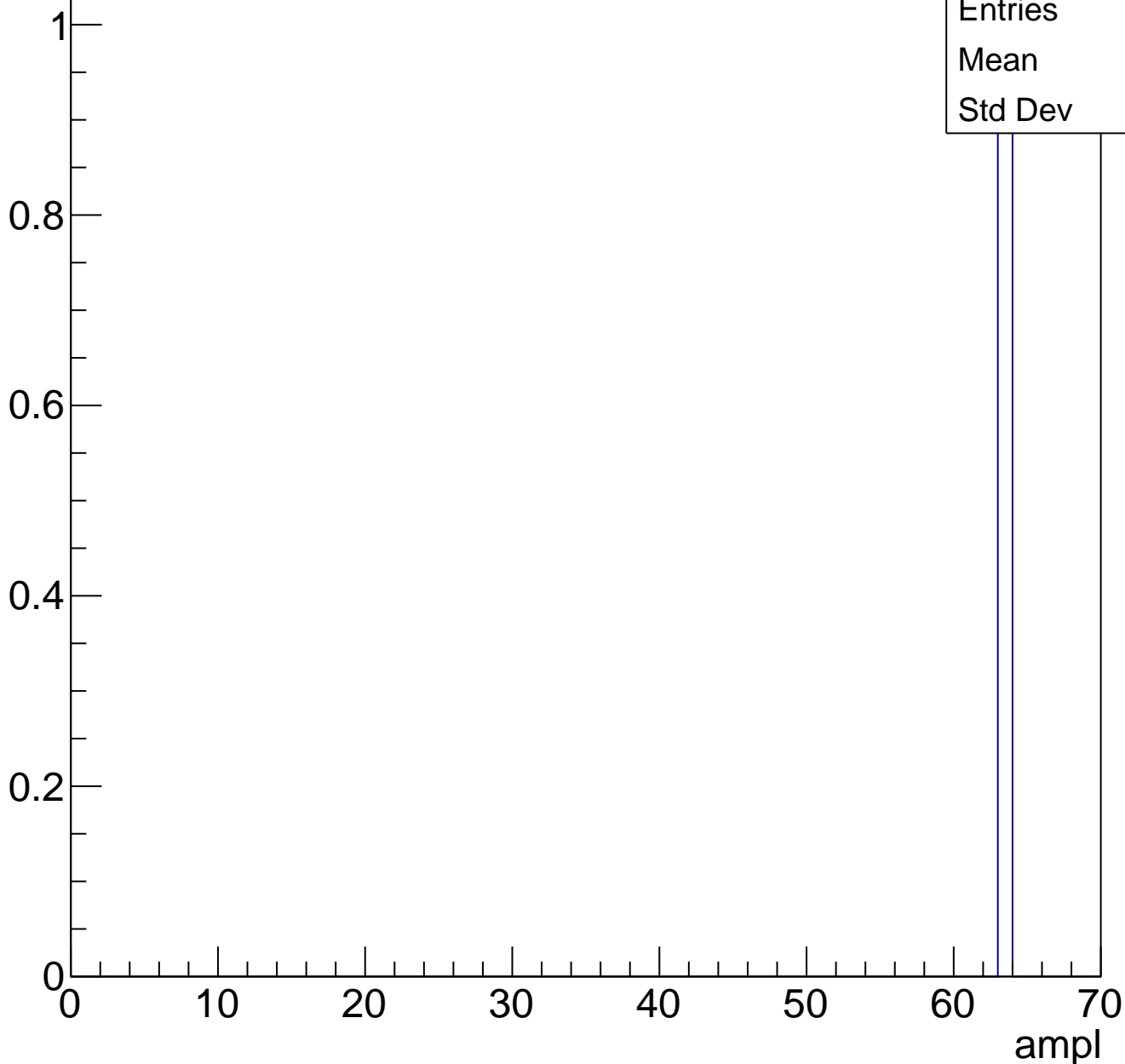
Entries	25
Mean	61.2
Std Dev	1.98



# B0L102S, U6-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch32, adc0

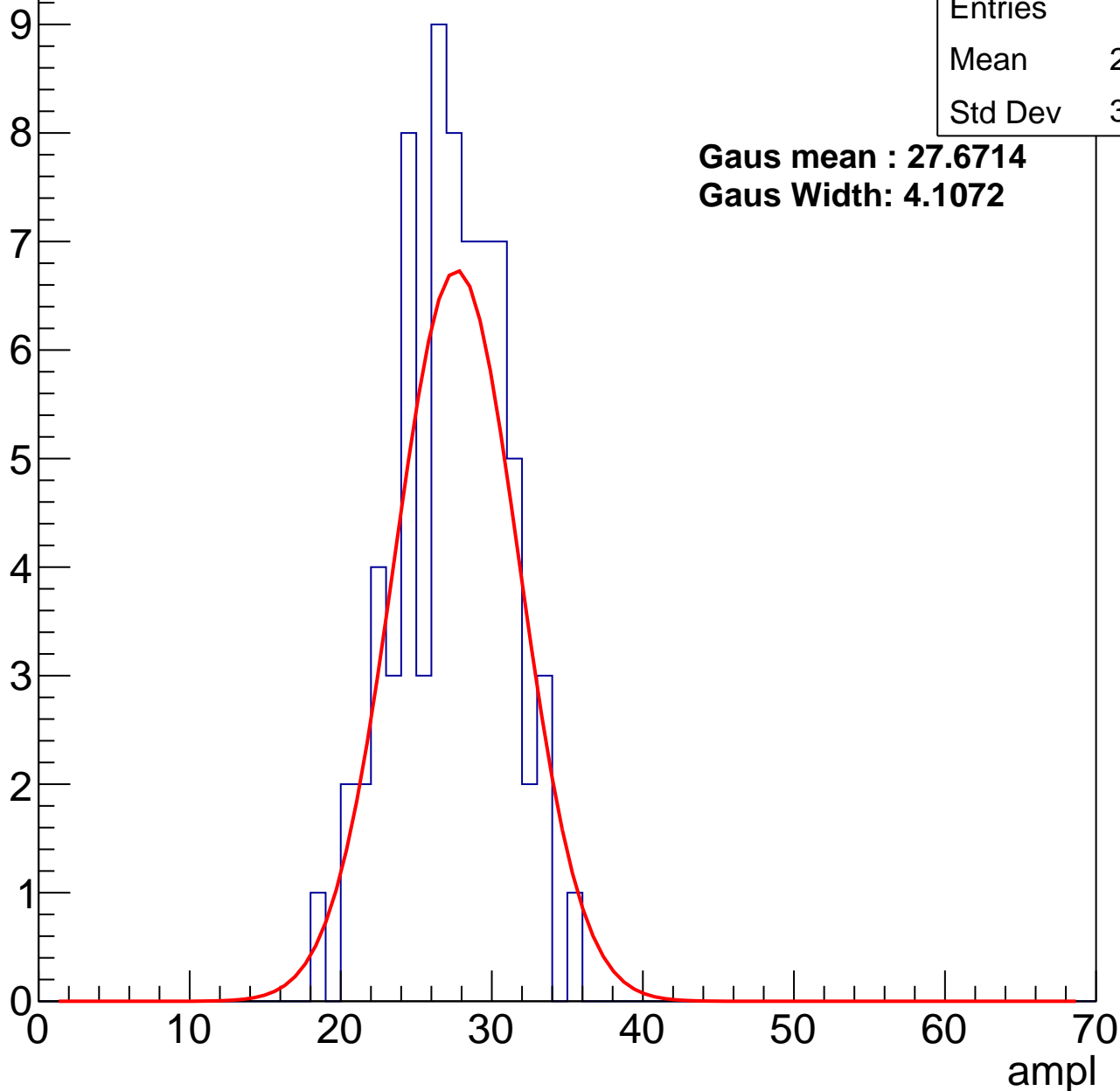
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	26.89
Std Dev	3.518

**Gaus mean : 27.6714**

**Gaus Width: 4.1072**



# B0L102S, U6-ch32, adc1

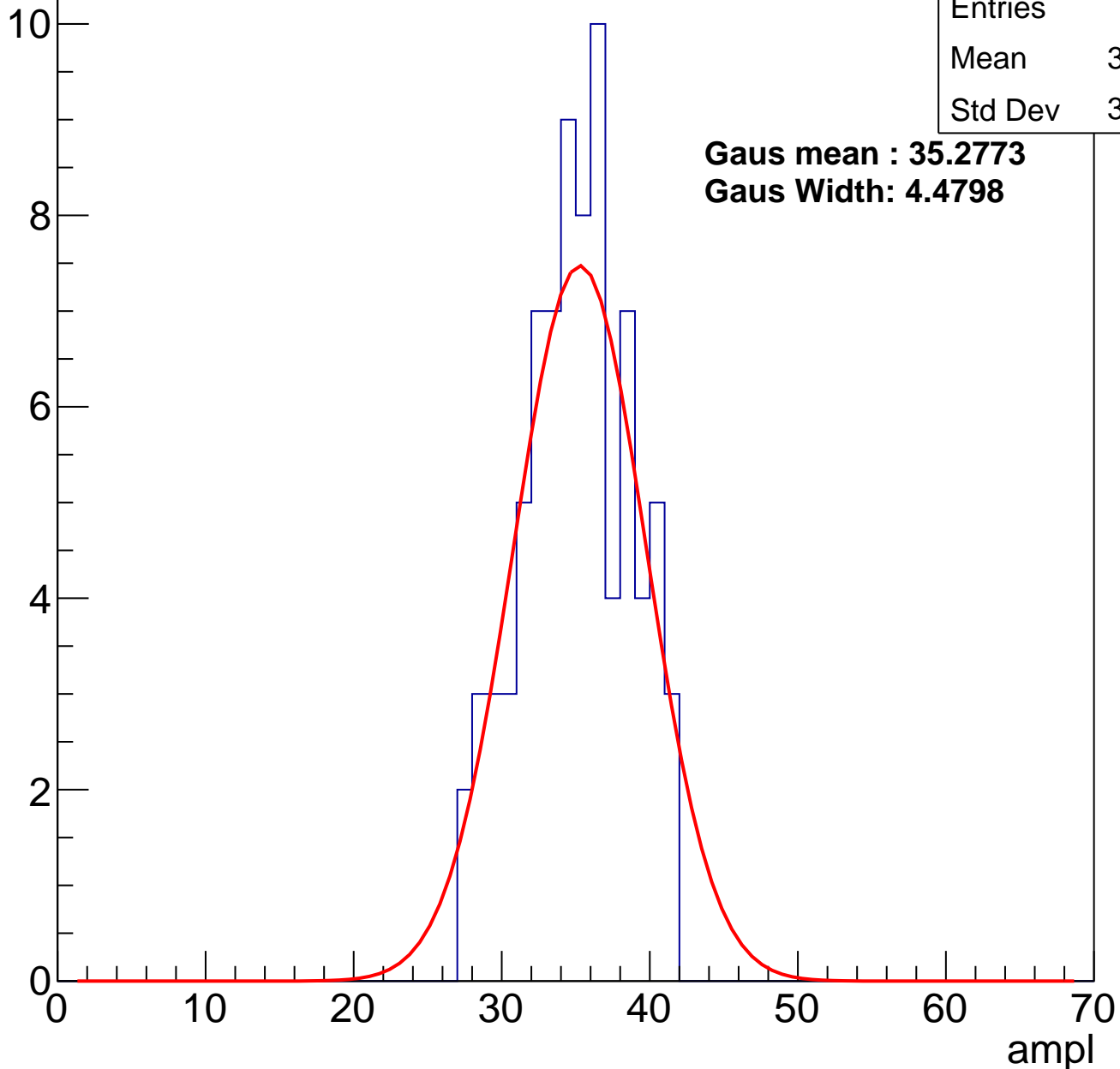
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	80
Mean	34.55
Std Dev	3.549

**Gaus mean : 35.2773**

**Gaus Width: 4.4798**

Entry



# B0L102S, U6-ch32, adc2

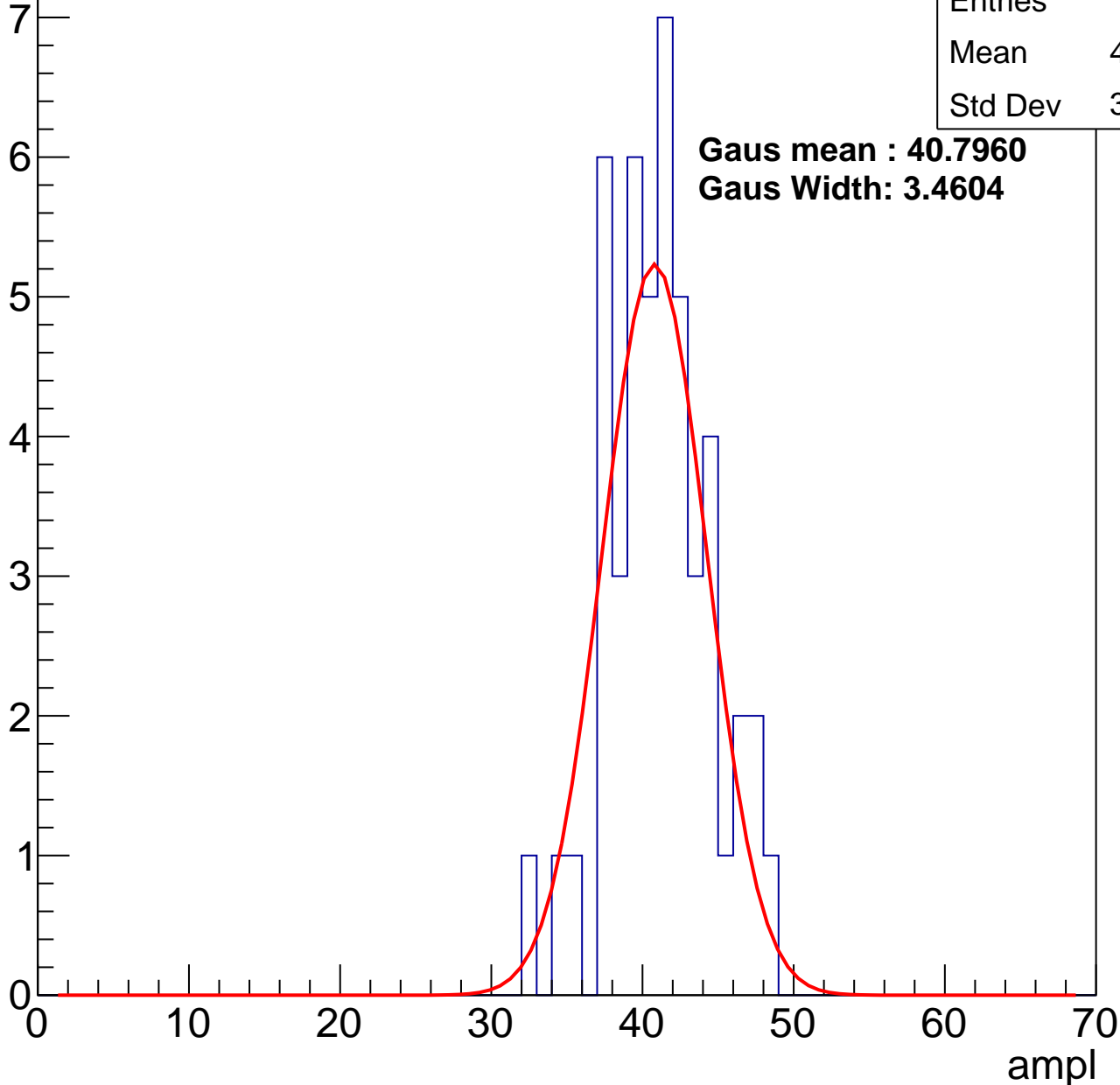
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	40.67
Std Dev	3.393

**Gaus mean : 40.7960**

**Gaus Width: 3.4604**

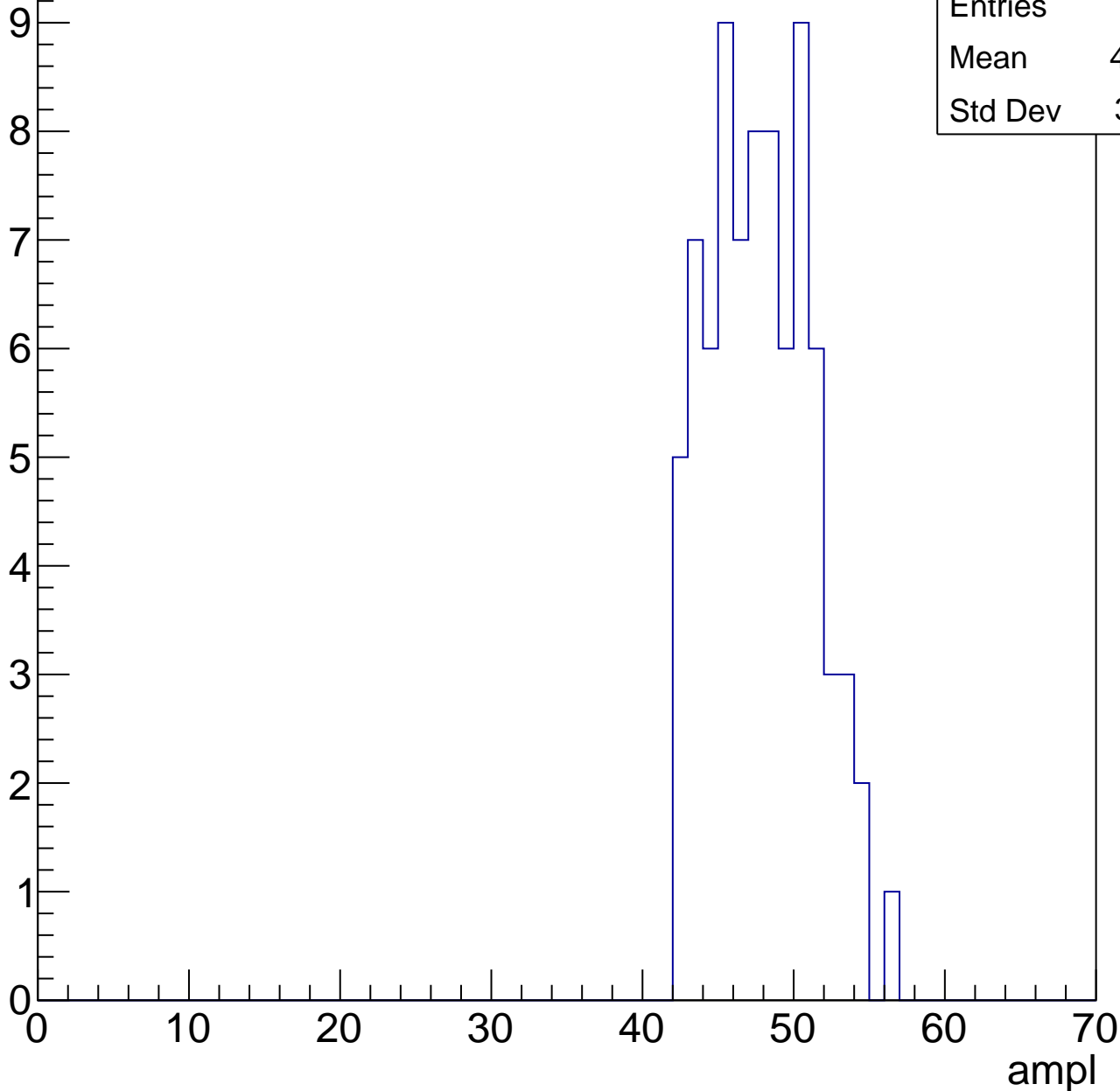


# B0L102S, U6-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	47.39
Std Dev	3.341

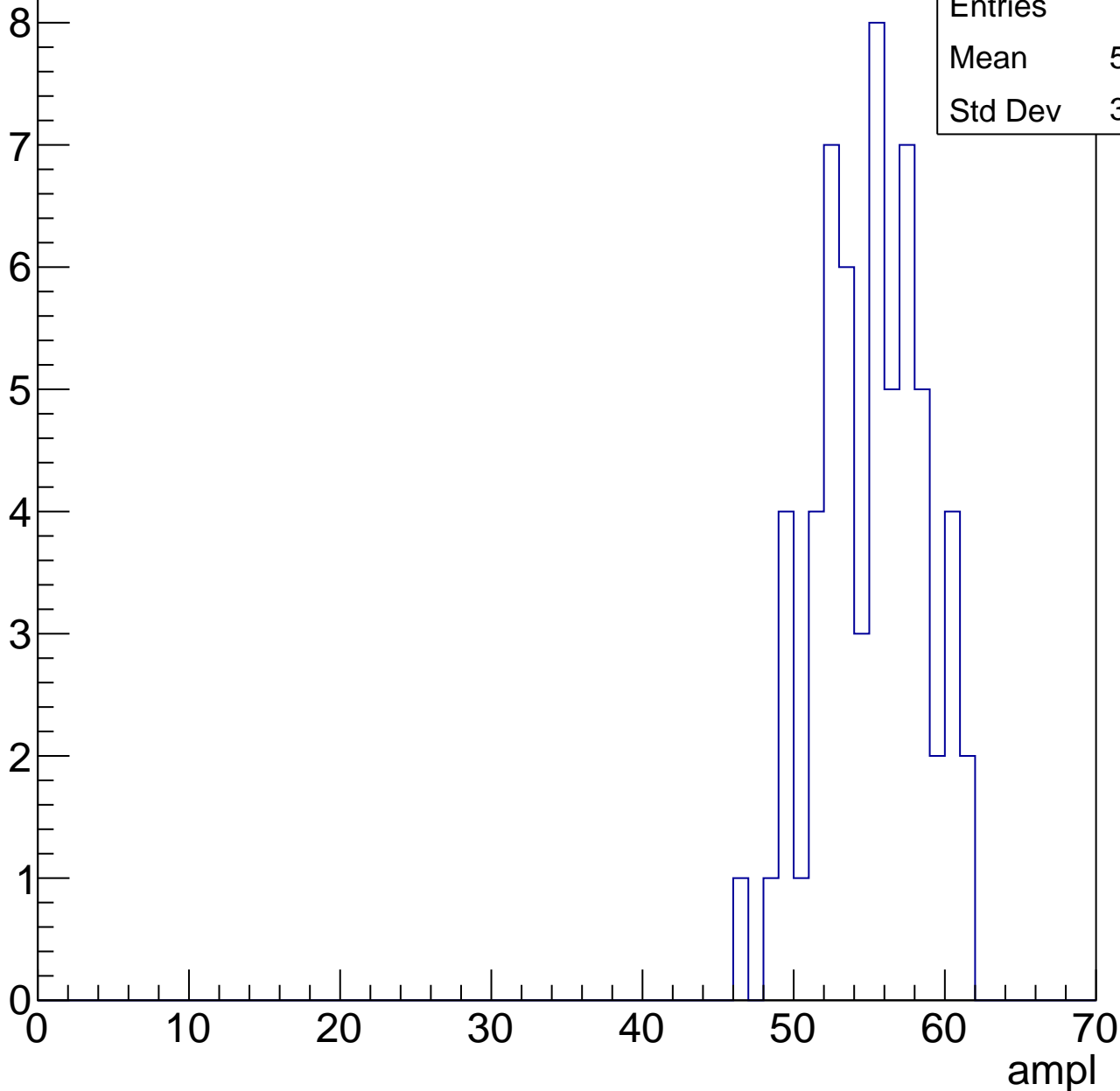


# B0L102S, U6-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	54.62
Std Dev	3.479

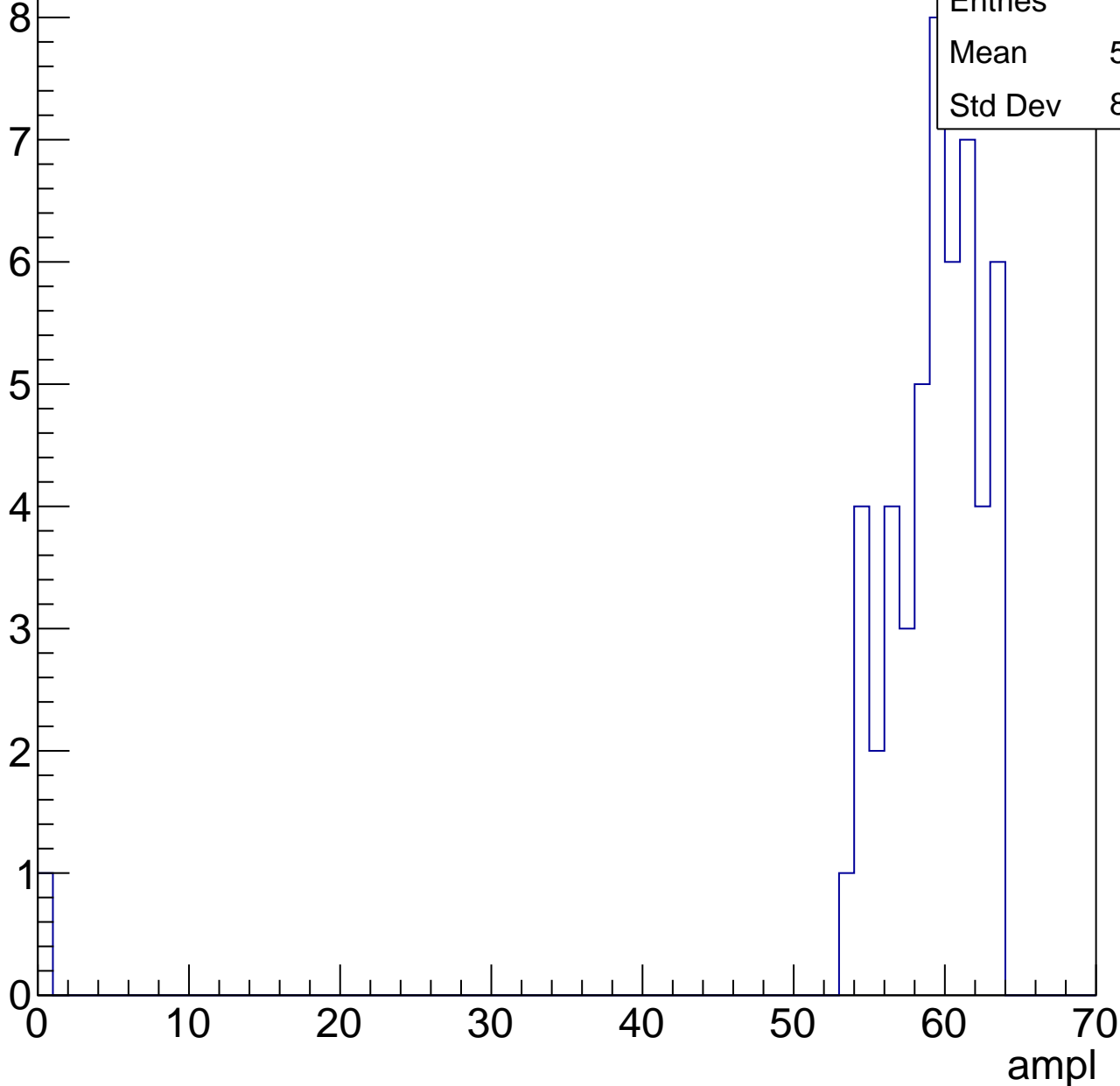


# B0L102S, U6-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	57.82
Std Dev	8.629

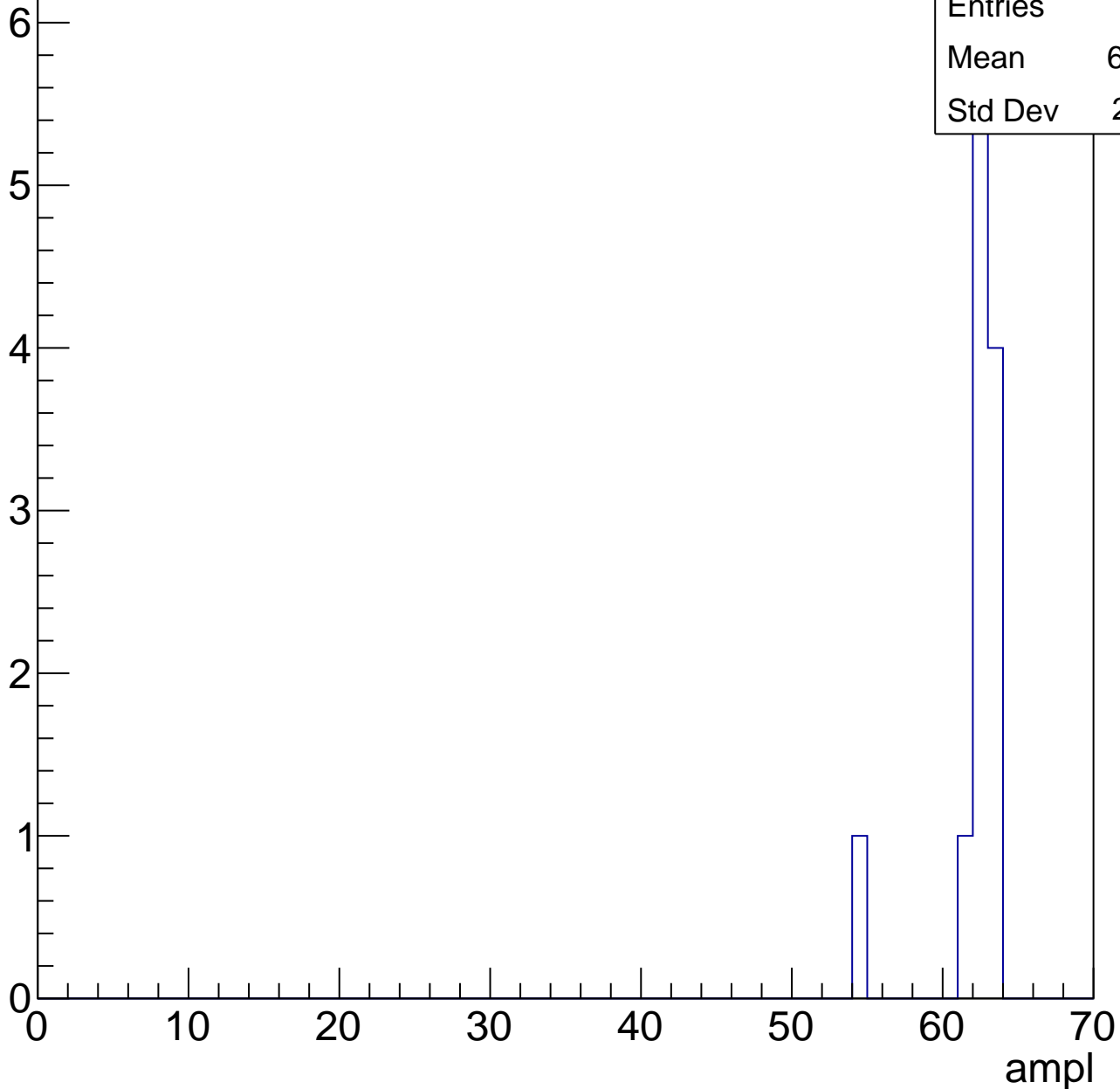


# B0L102S, U6-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	61.58
Std Dev	2.361





# B0L102S, U6-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch33, adc0

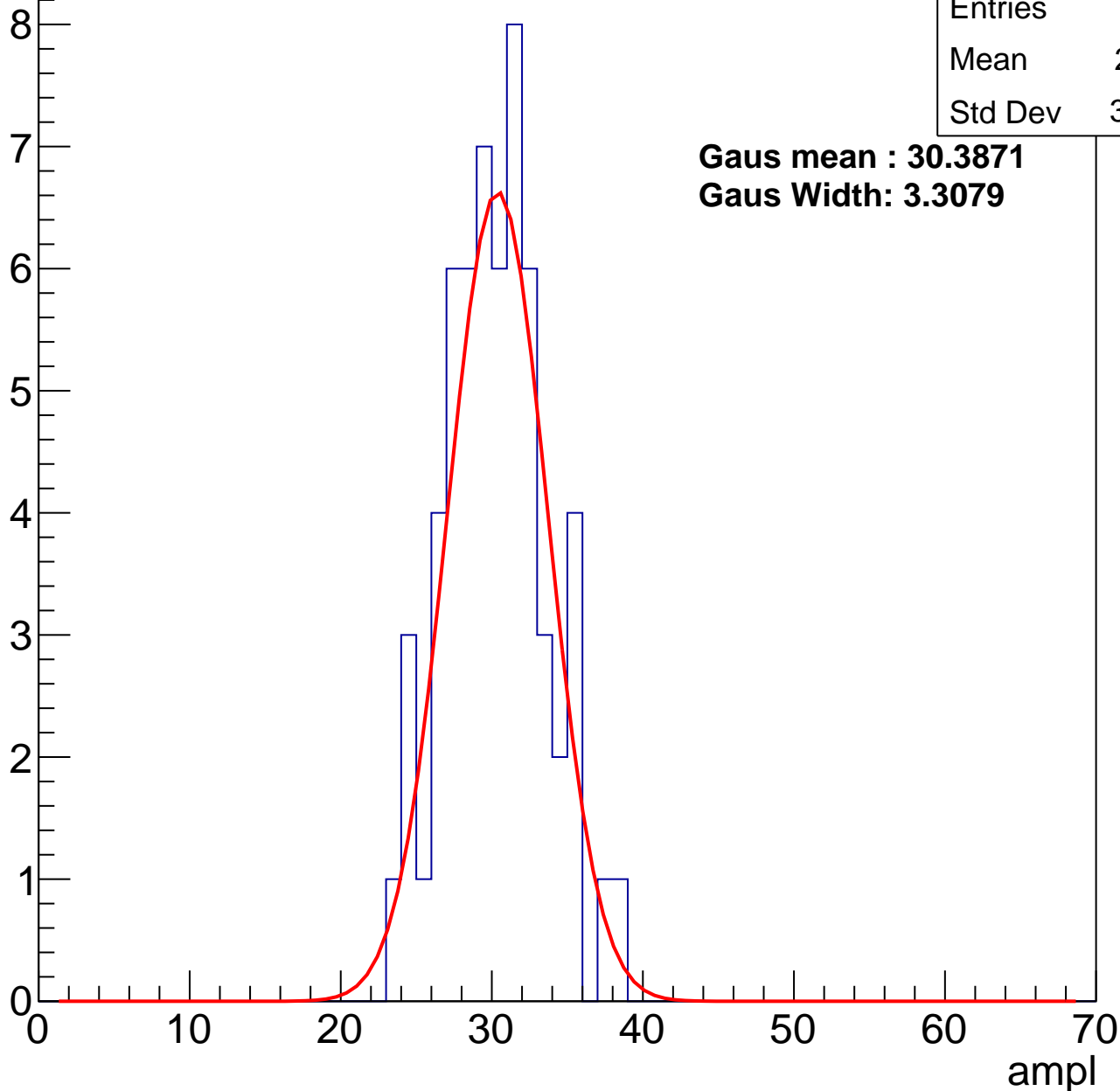
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	29.81
Std Dev	3.275

**Gaus mean : 30.3871**

**Gaus Width: 3.3079**



# B0L102S, U6-ch33, adc1

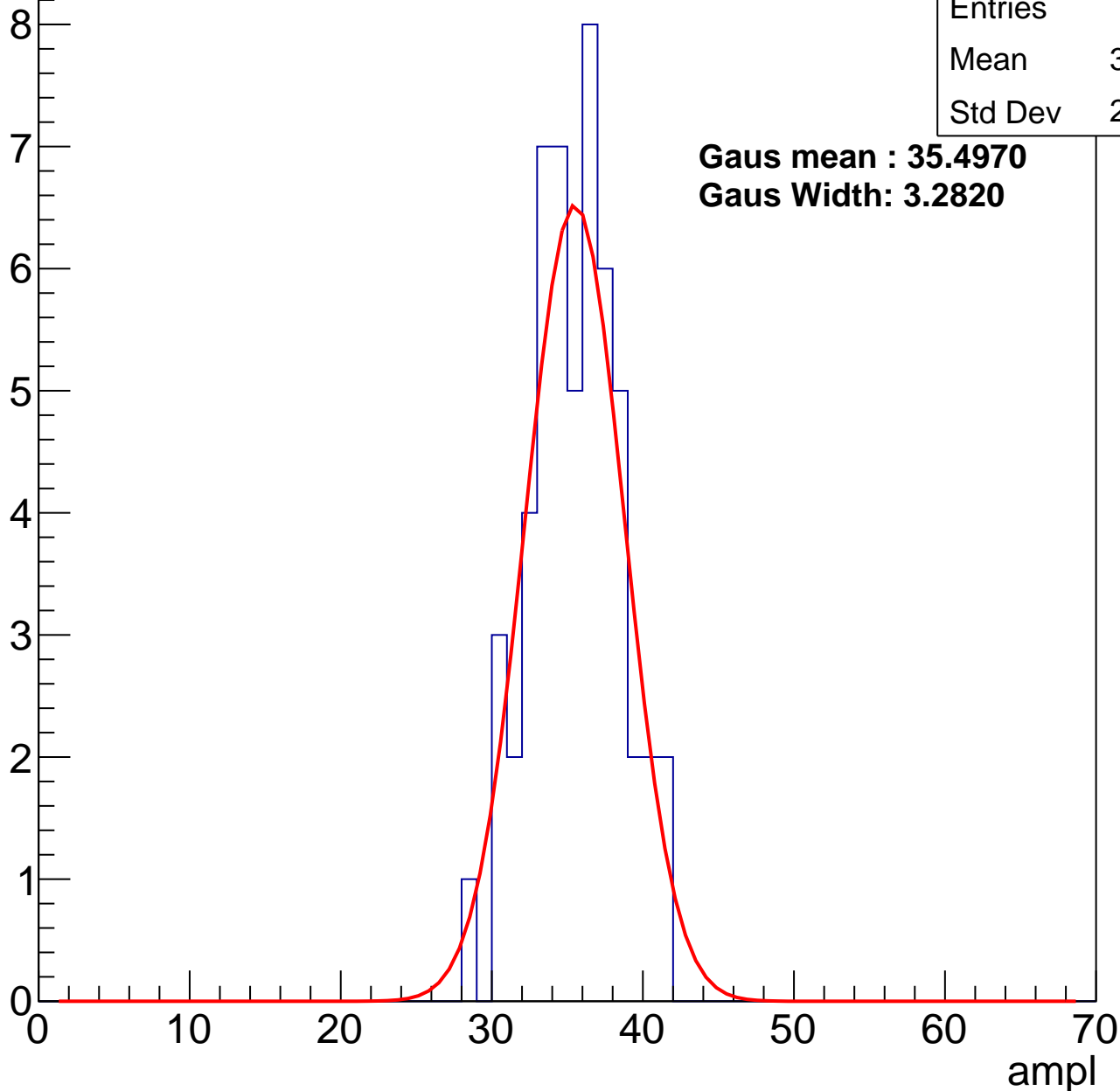
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	35.04
Std Dev	2.912

**Gaus mean : 35.4970**

**Gaus Width: 3.2820**



# B0L102S, U6-ch33, adc2

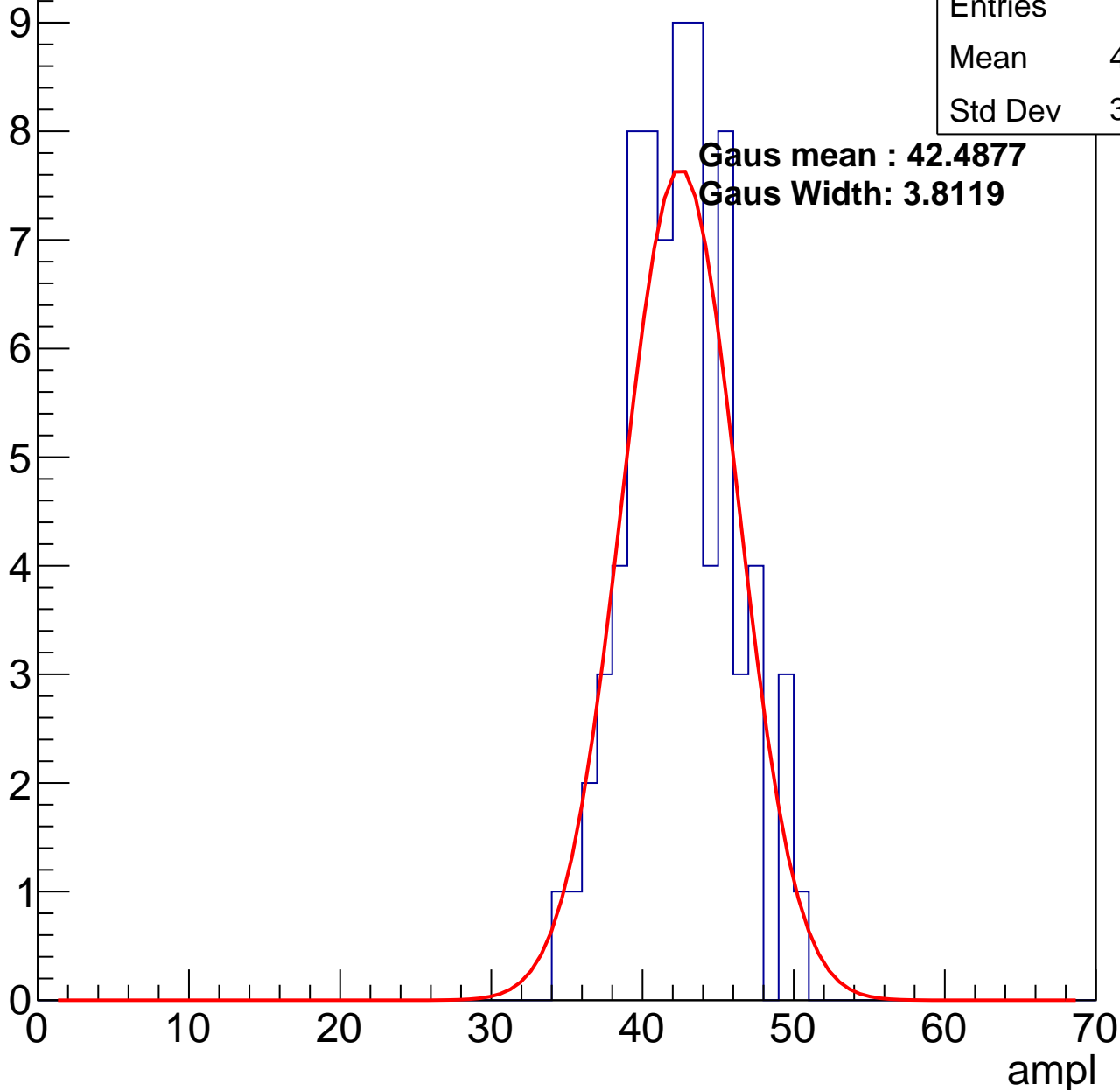
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	41.96
Std Dev	3.447

**Gaus mean : 42.4877**

**Gaus Width: 3.8119**

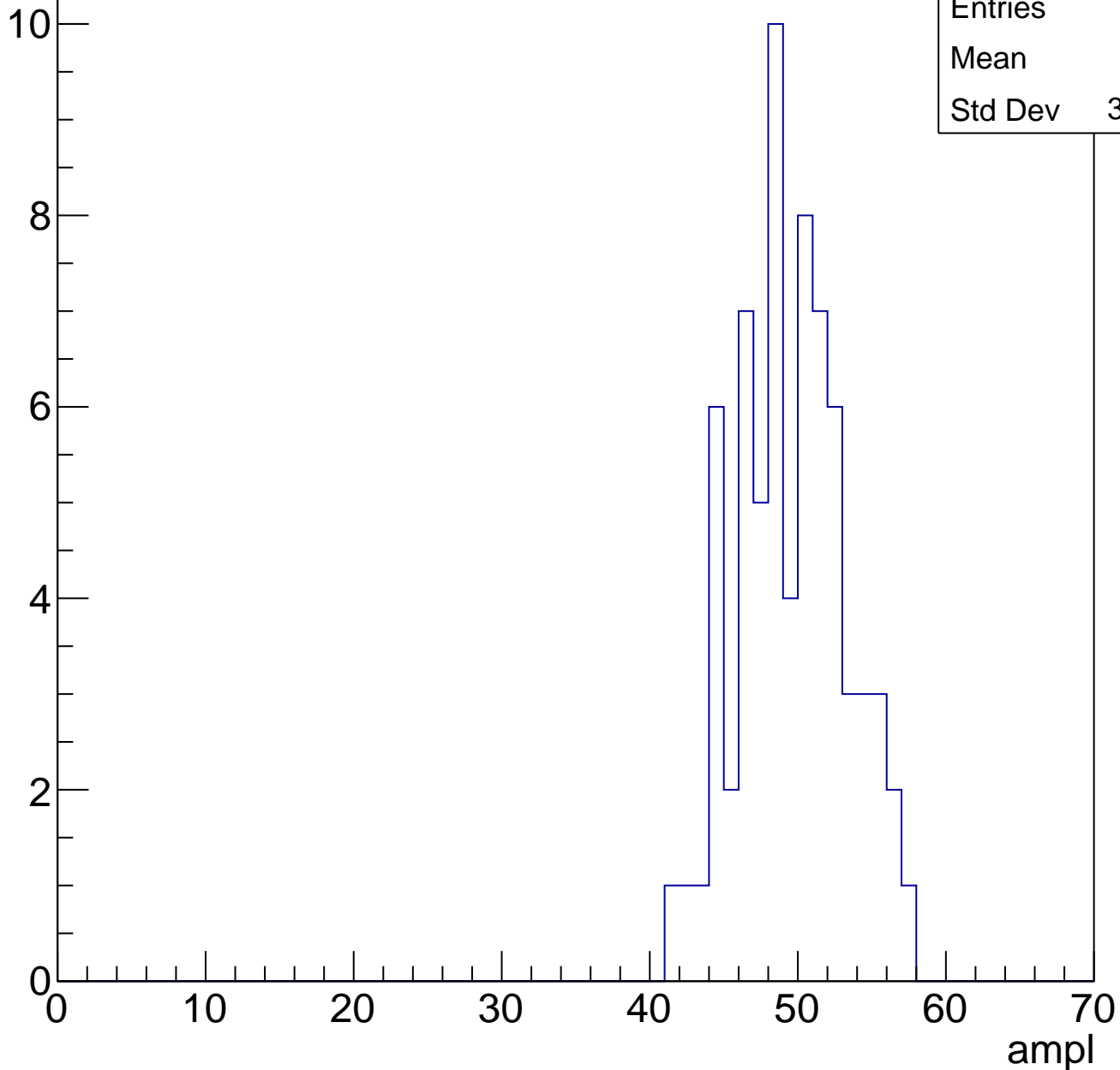


# B0L102S, U6-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	49.1
Std Dev	3.598

Entry

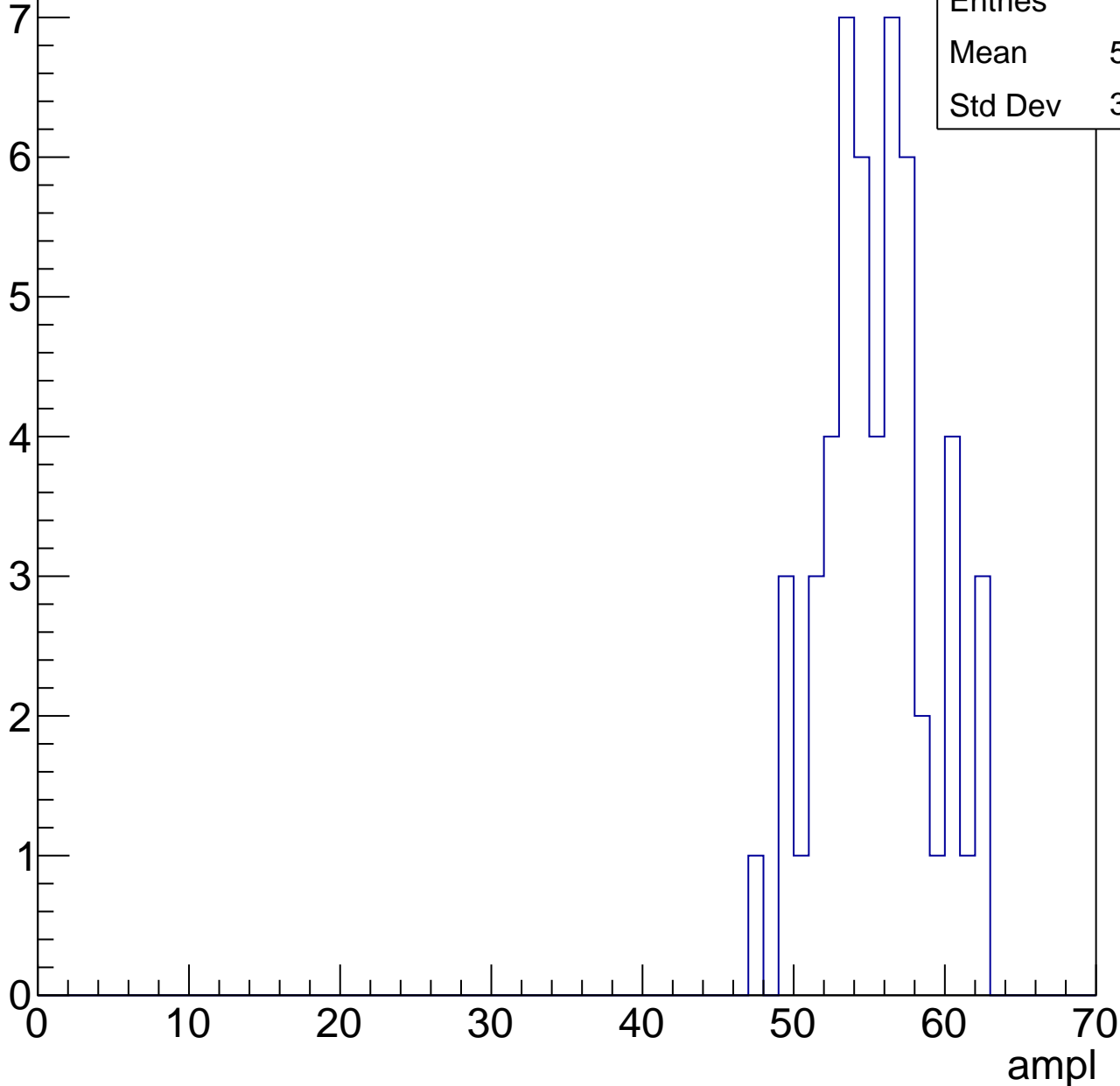


# B0L102S, U6-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	55.02
Std Dev	3.537



# B0L102S, U6-ch33, adc5

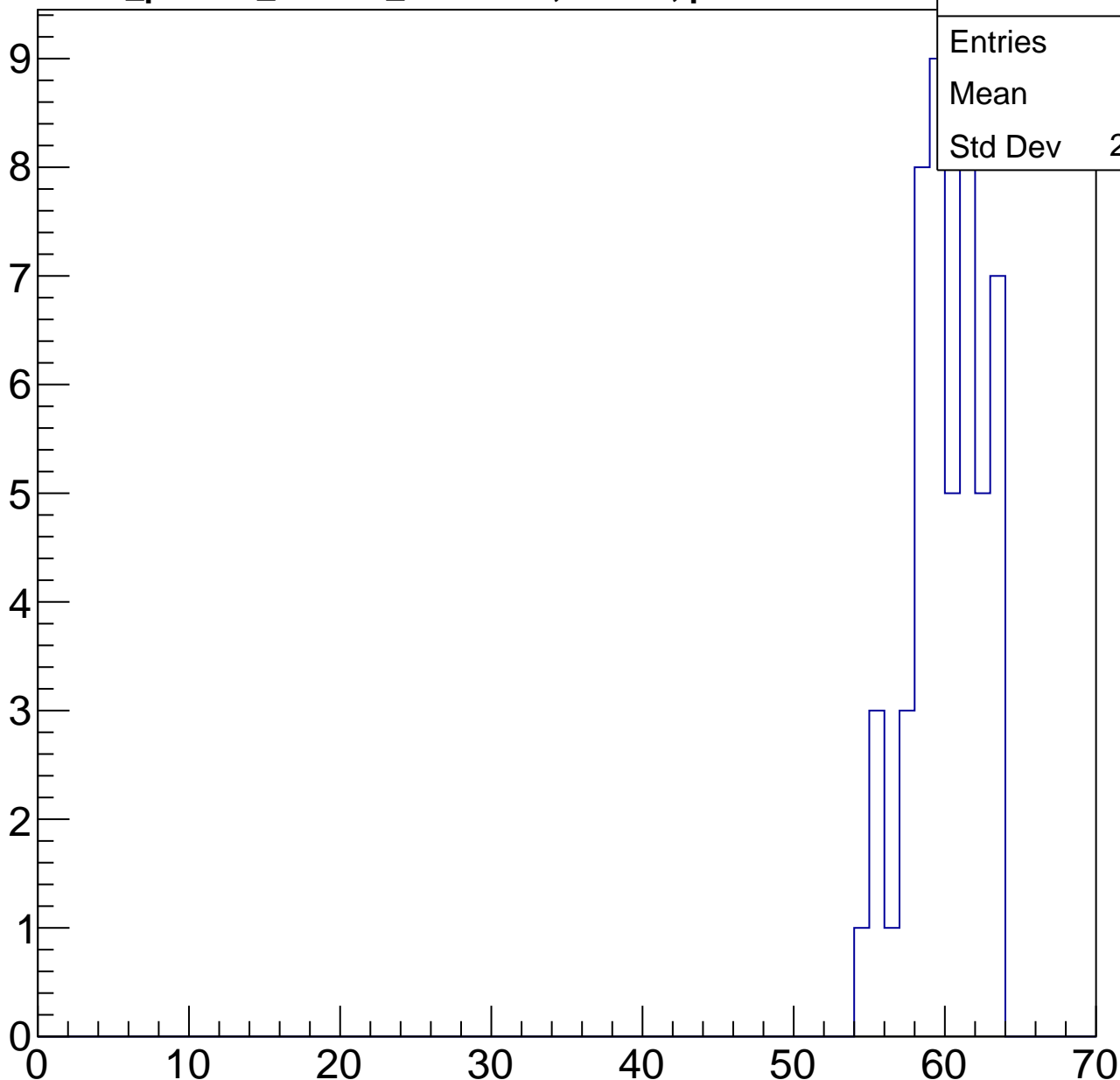
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	59.6
Std Dev	2.358

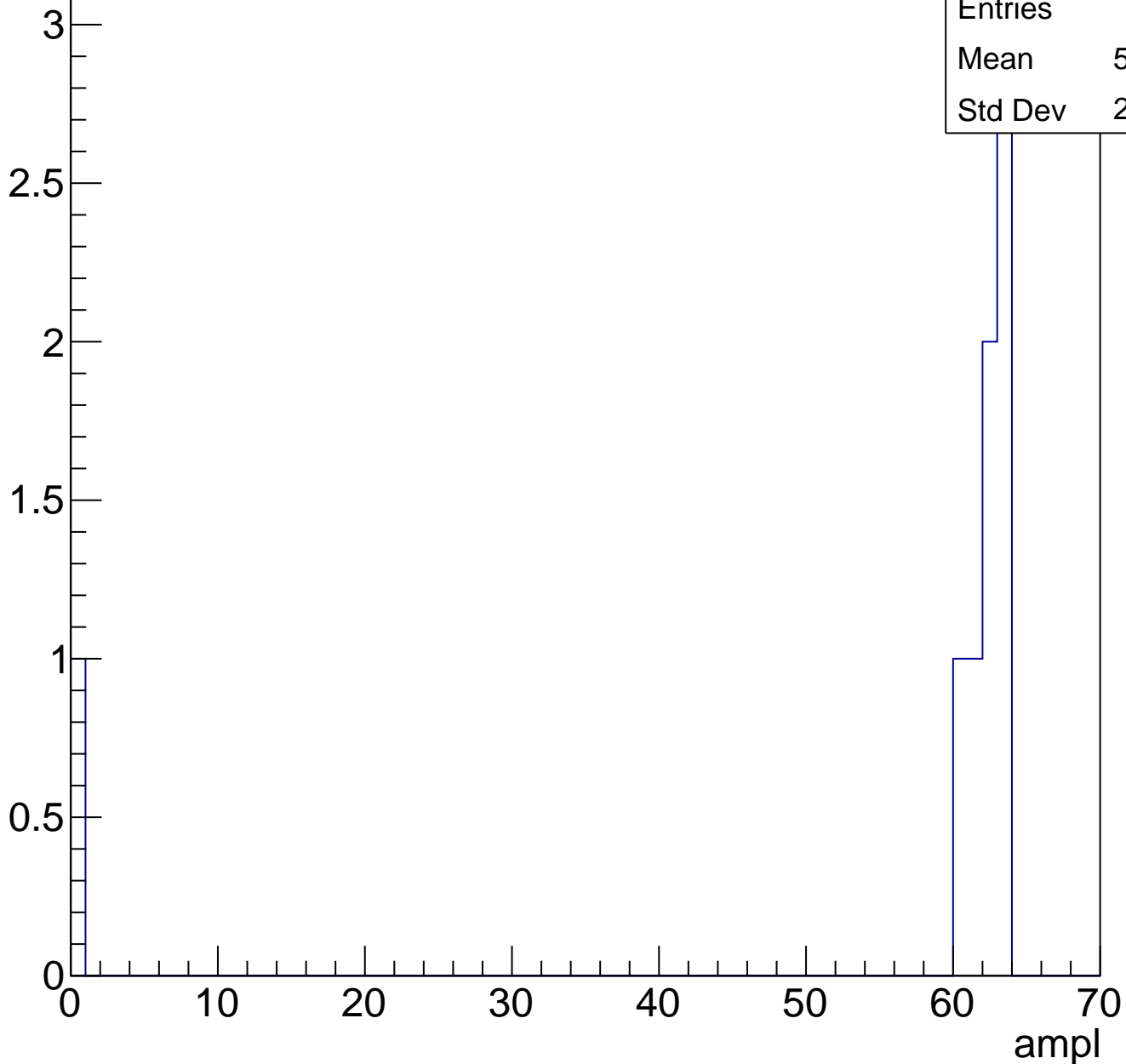
ampl



# B0L102S, U6-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch34, adc0

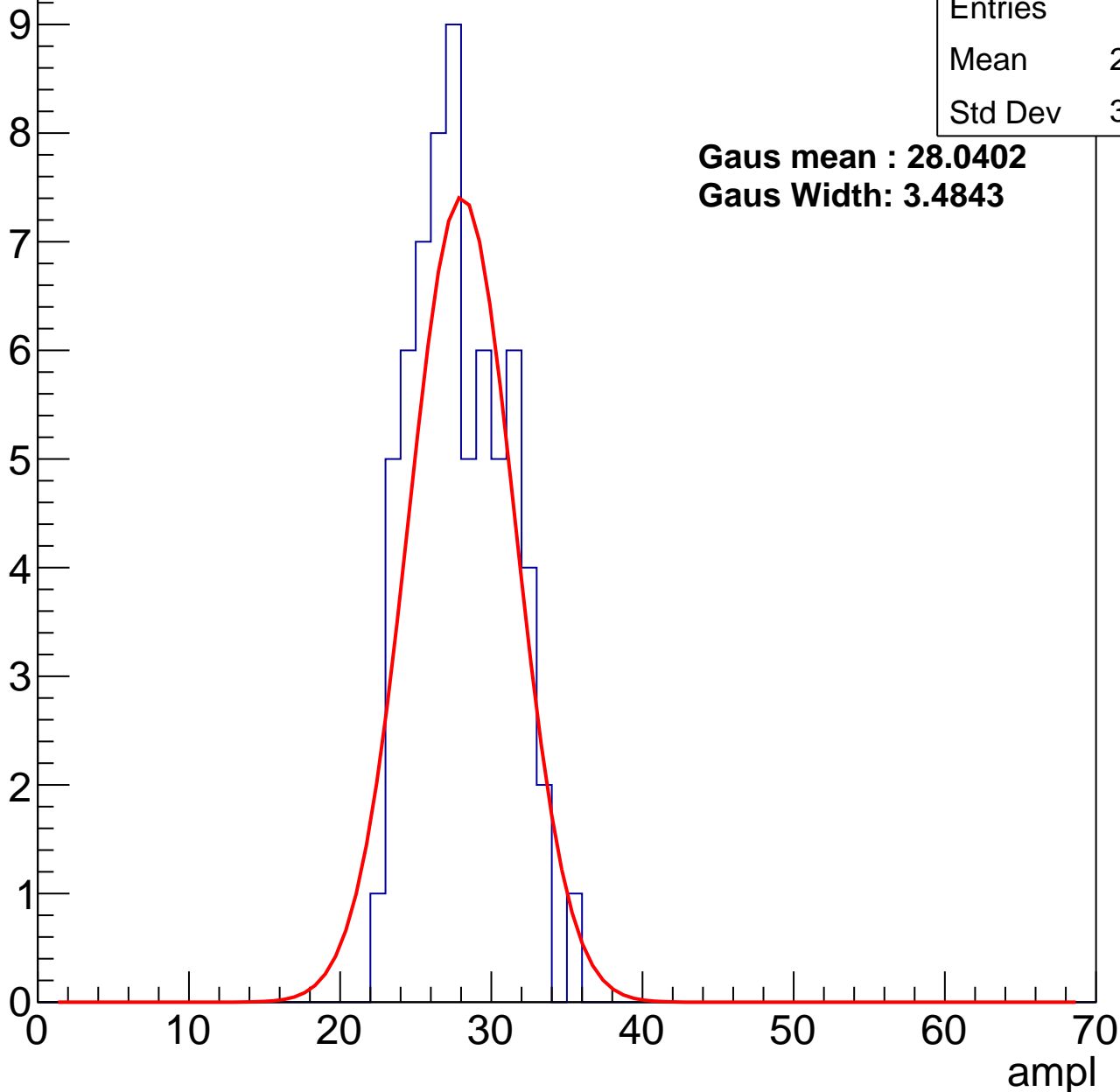
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	27.48
Std Dev	3.003

**Gaus mean : 28.0402**

**Gaus Width: 3.4843**



# B0L102S, U6-ch34, adc1

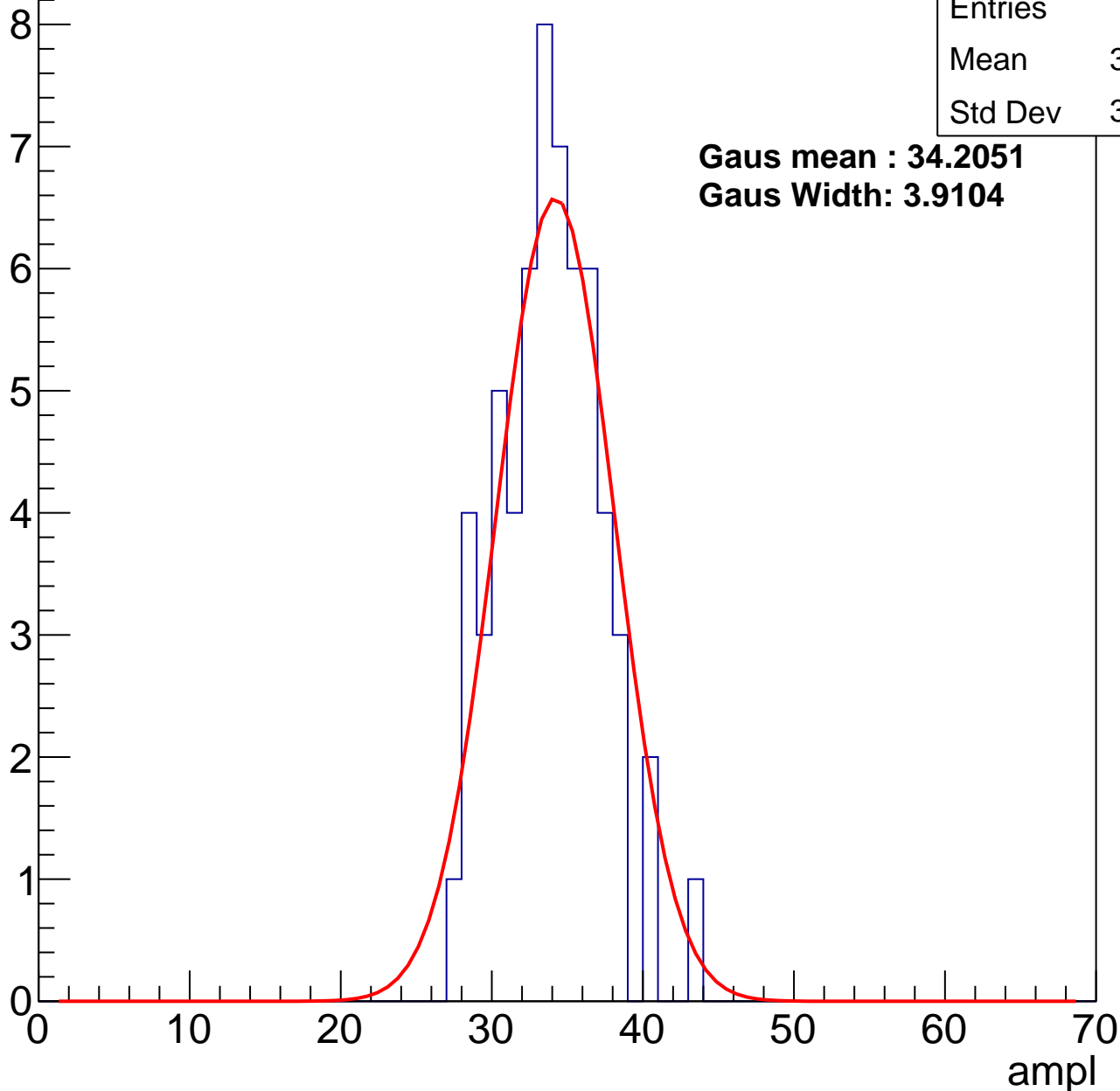
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	33.42
Std Dev	3.323

**Gaus mean : 34.2051**

**Gaus Width: 3.9104**



# B0L102S, U6-ch34, adc2

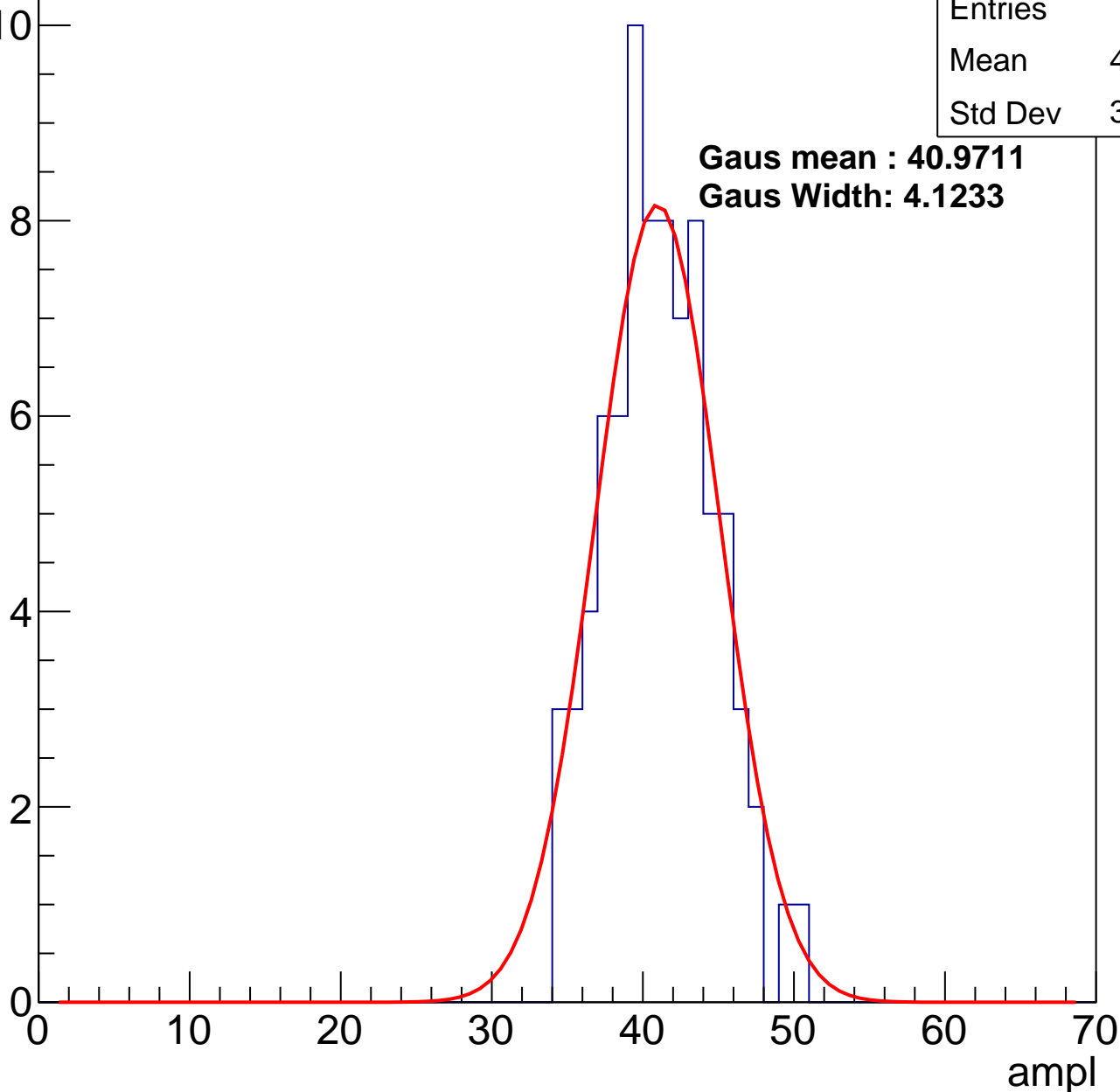
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	40.66
Std Dev	3.535

**Gaus mean : 40.9711**

**Gaus Width: 4.1233**

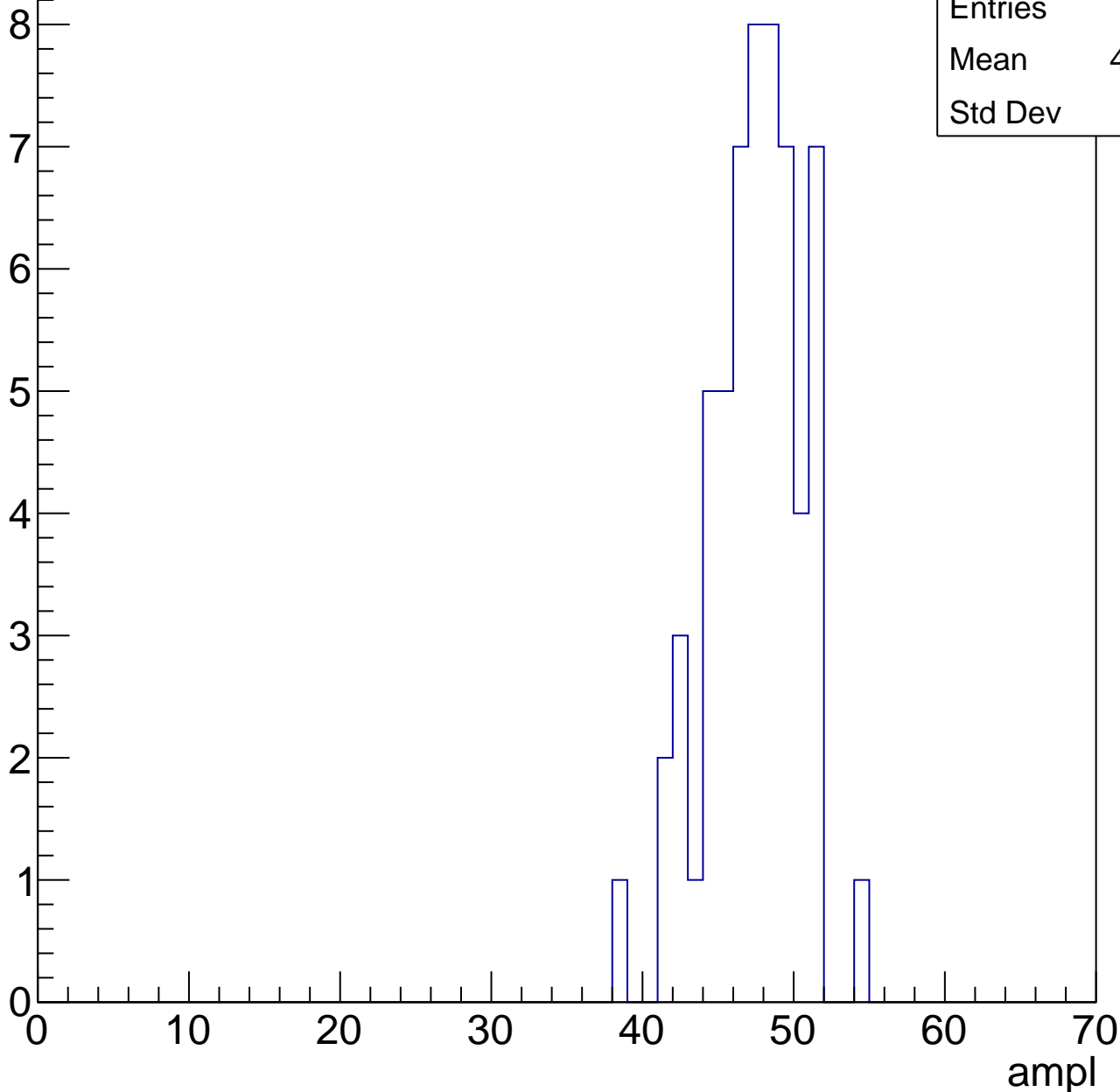


# B0L102S, U6-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

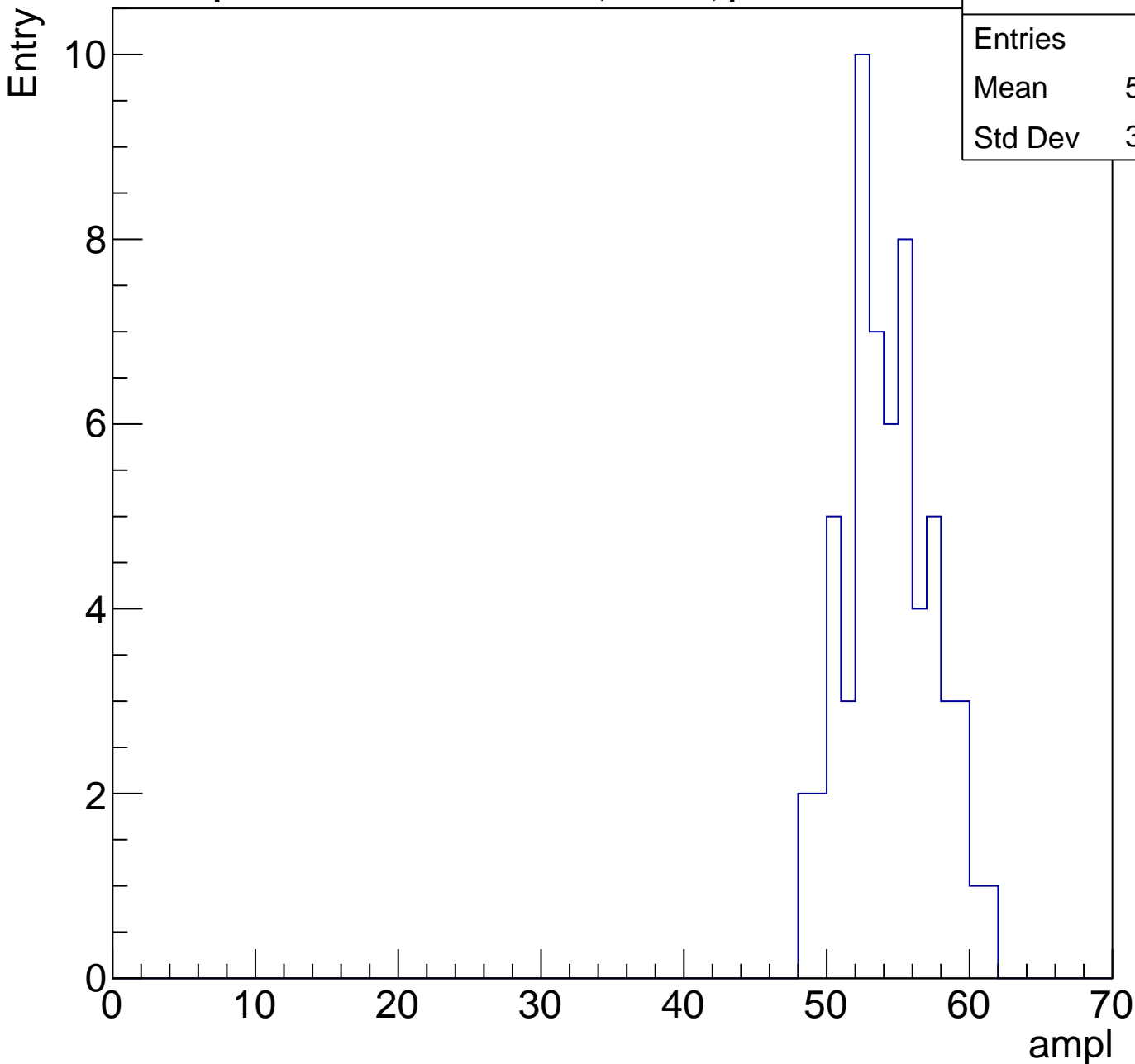
Entries	59
Mean	46.95
Std Dev	3.05



# B0L102S, U6-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	60
Mean	53.88
Std Dev	3.034



# B0L102S, U6-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	58.72
Std Dev	2.803

ampl

0 10 20 30 40 50 60 70

3

4

5

6

7

8

0

1

2

3

4

5

6

7

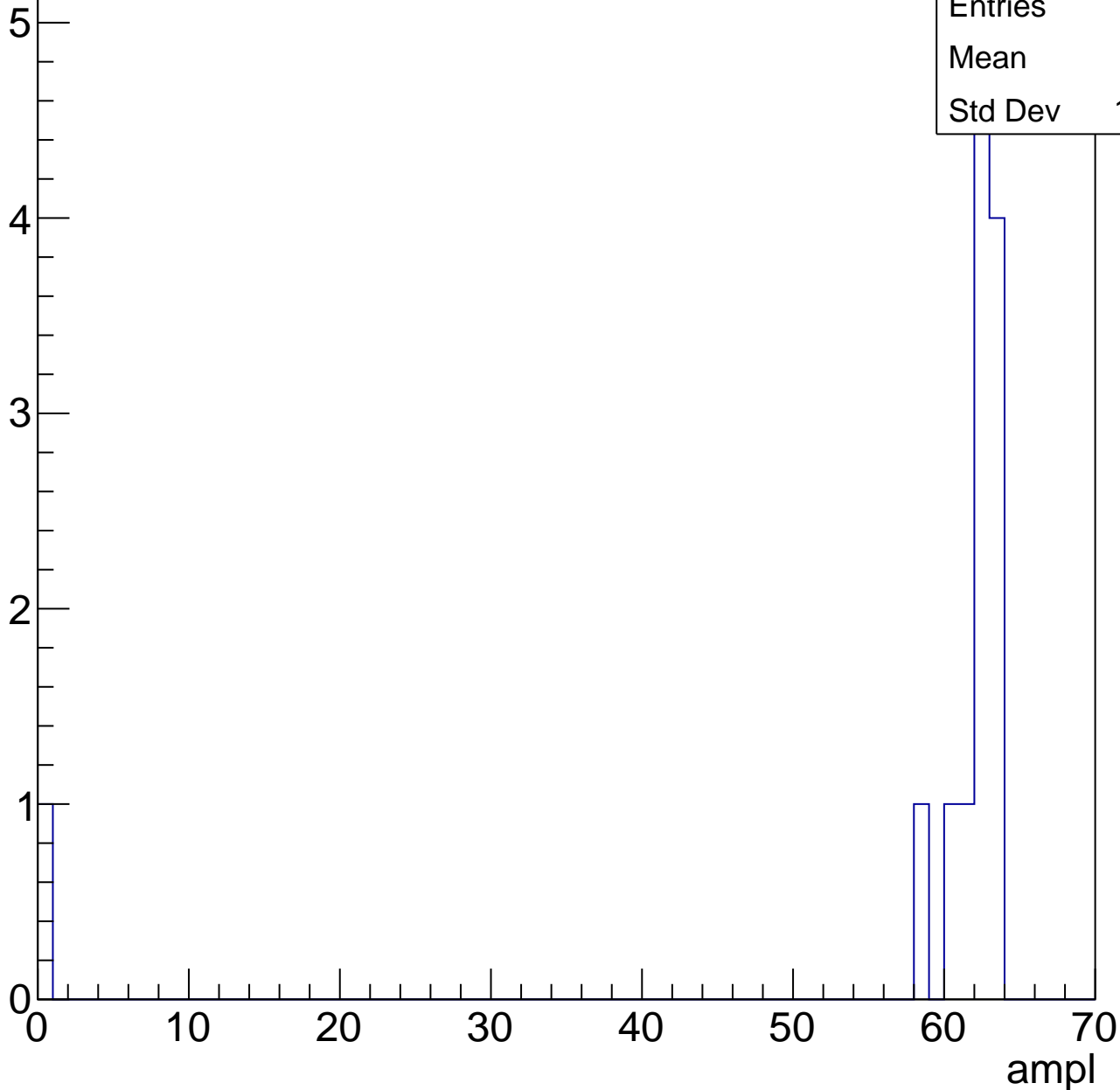
8

# B0L102S, U6-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	57
Std Dev	16.51





# B0L102S, U6-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch35, adc0

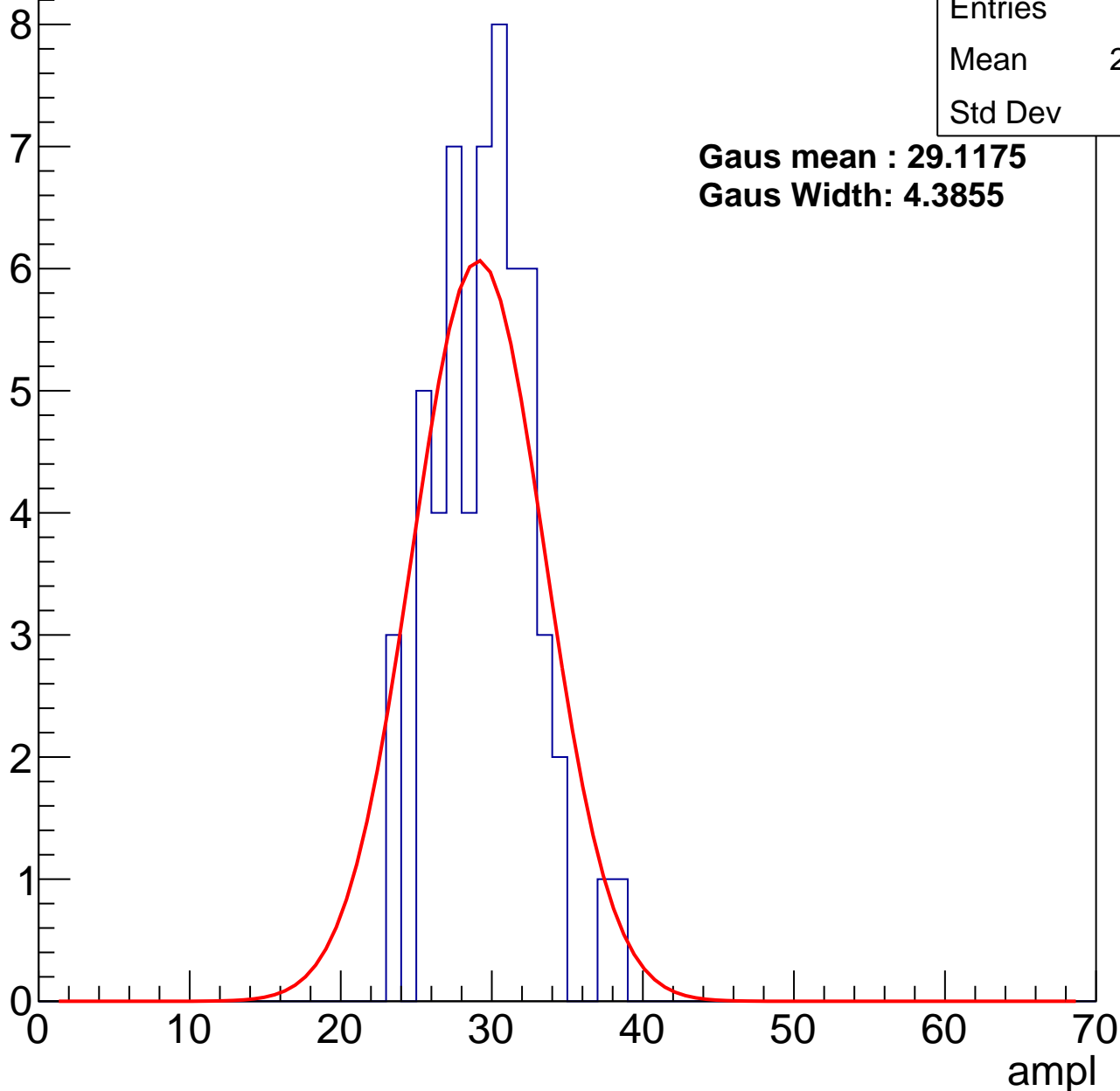
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	29.16
Std Dev	3.2

**Gaus mean : 29.1175**

**Gaus Width: 4.3855**



# B0L102S, U6-ch35, adc1

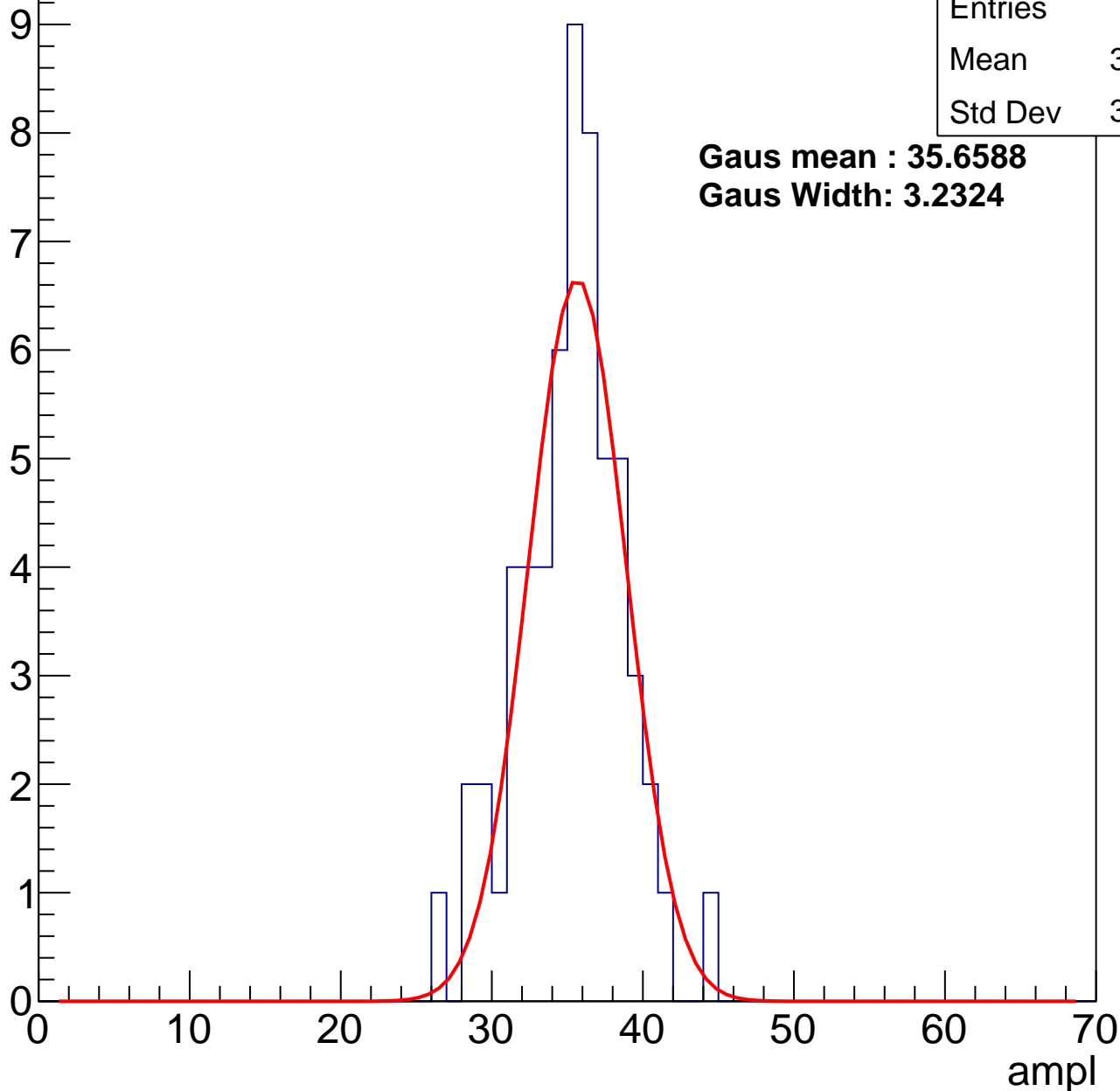
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	34.79
Std Dev	3.433

**Gaus mean : 35.6588**

**Gaus Width: 3.2324**



# B0L102S, U6-ch35, adc2

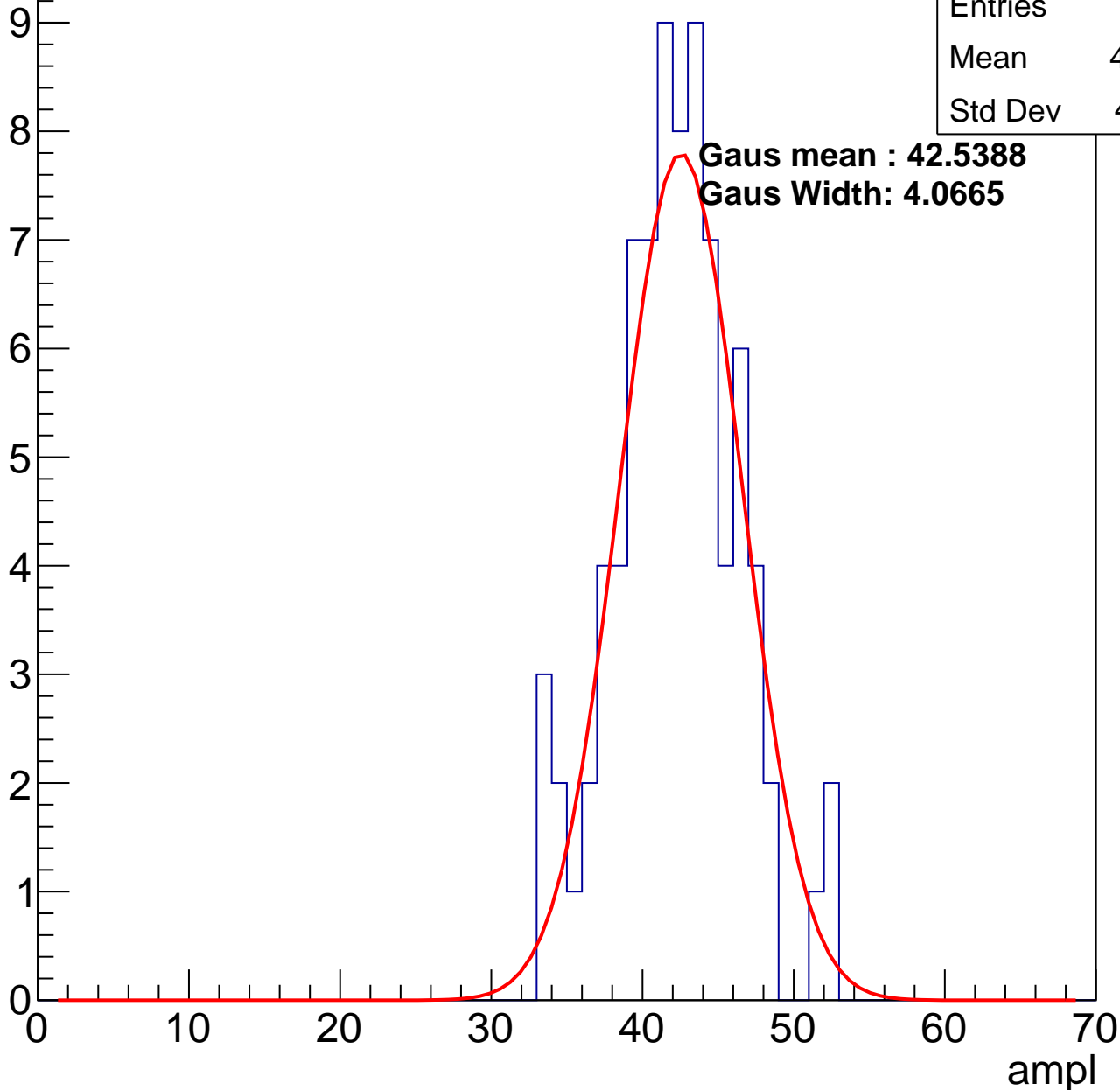
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	82
Mean	41.73
Std Dev	4.091

**Gaus mean : 42.5388**

**Gaus Width: 4.0665**

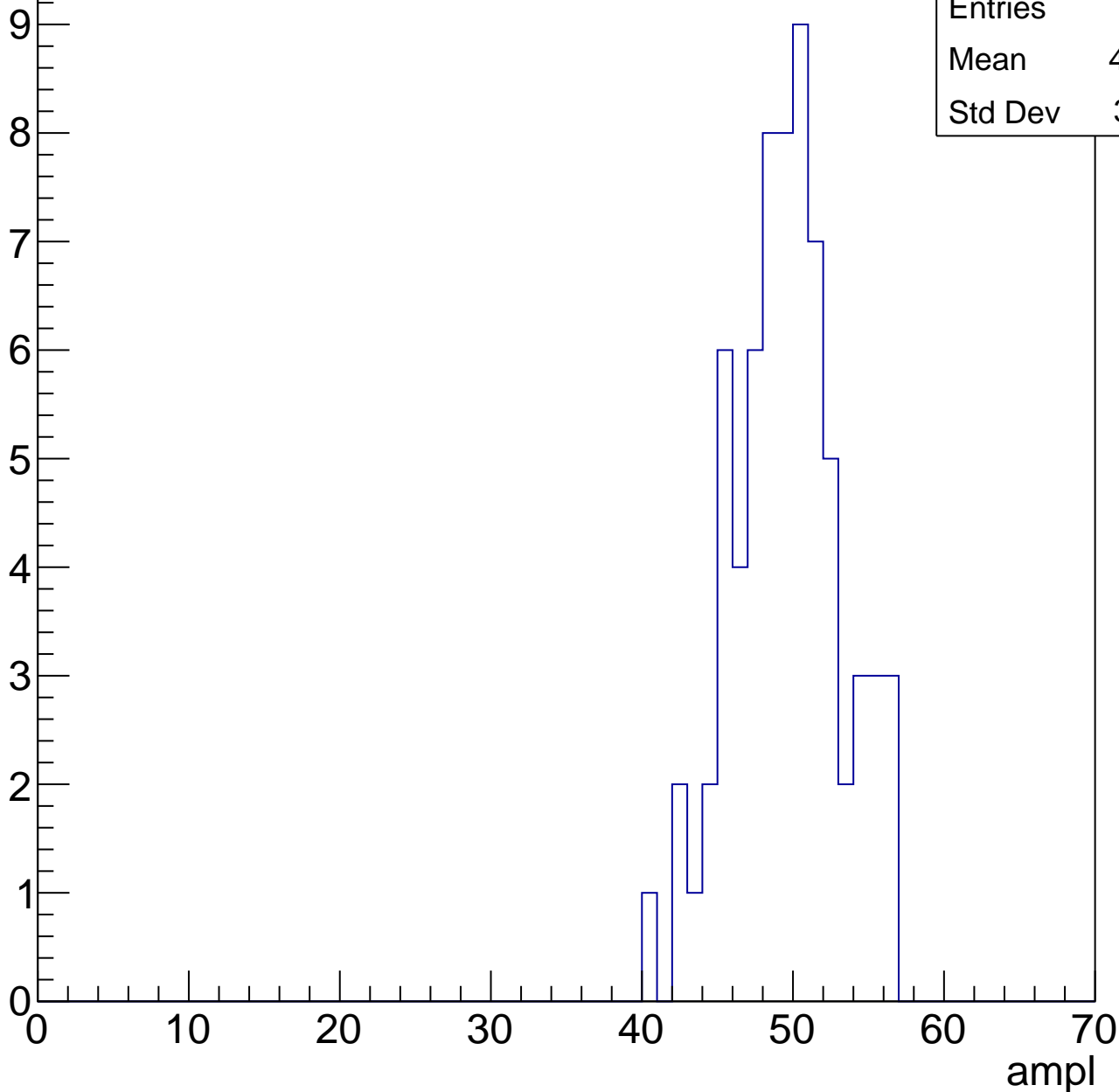


# B0L102S, U6-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	49.07
Std Dev	3.531

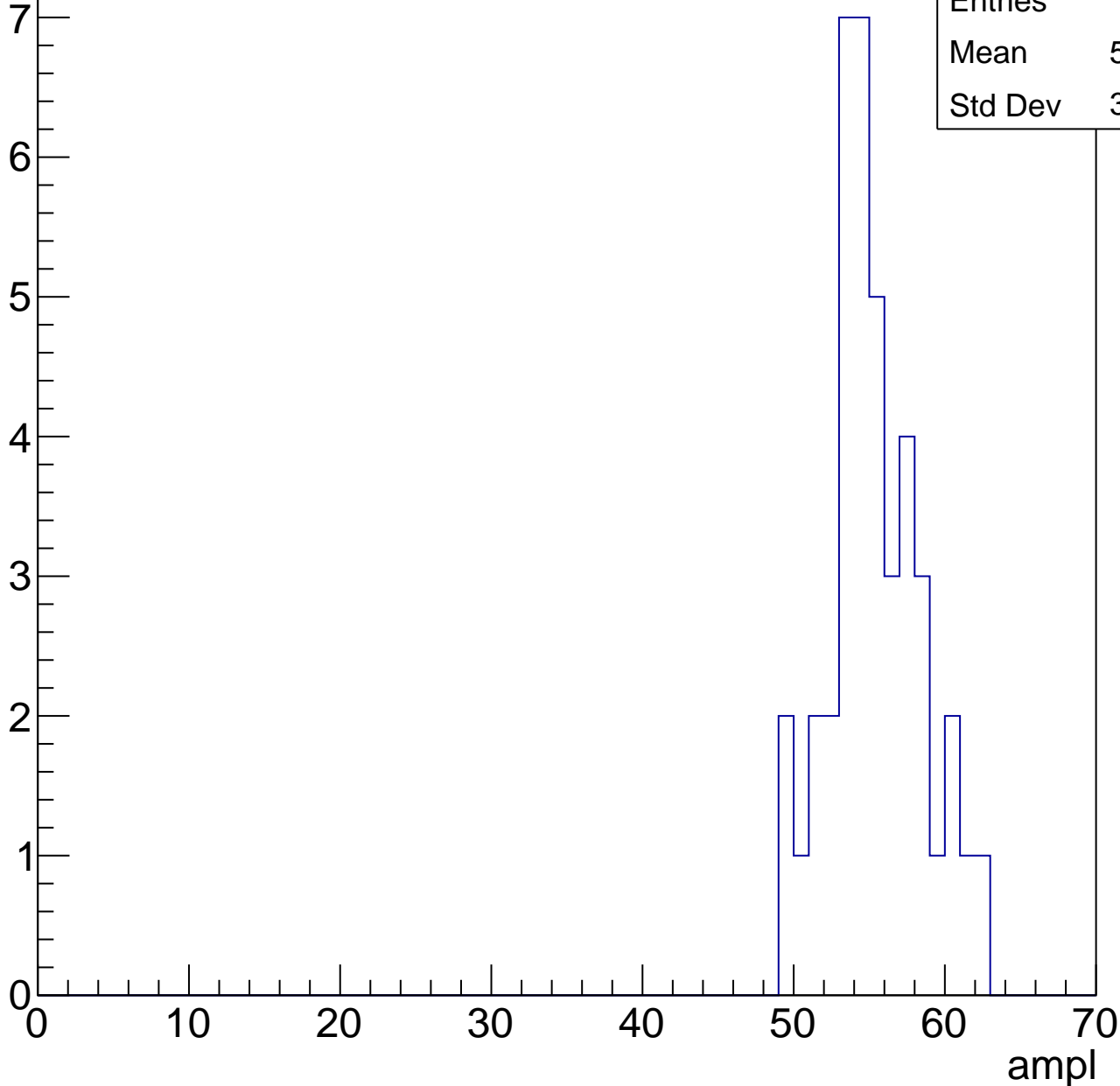


# B0L102S, U6-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	41
Mean	54.88
Std Dev	3.038

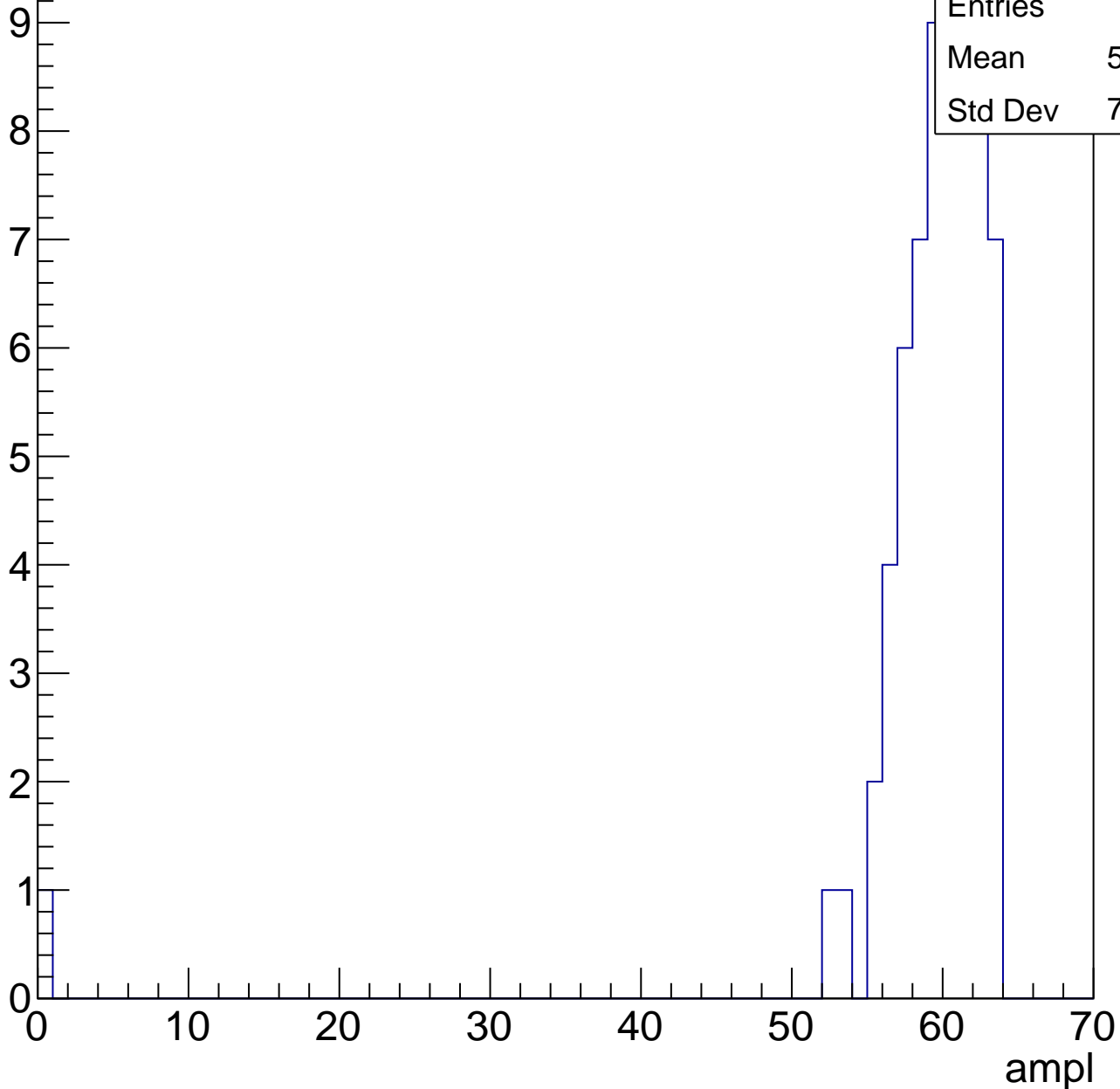


# B0L102S, U6-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	58.48
Std Dev	7.843



# B0L102S, U6-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch36, adc0

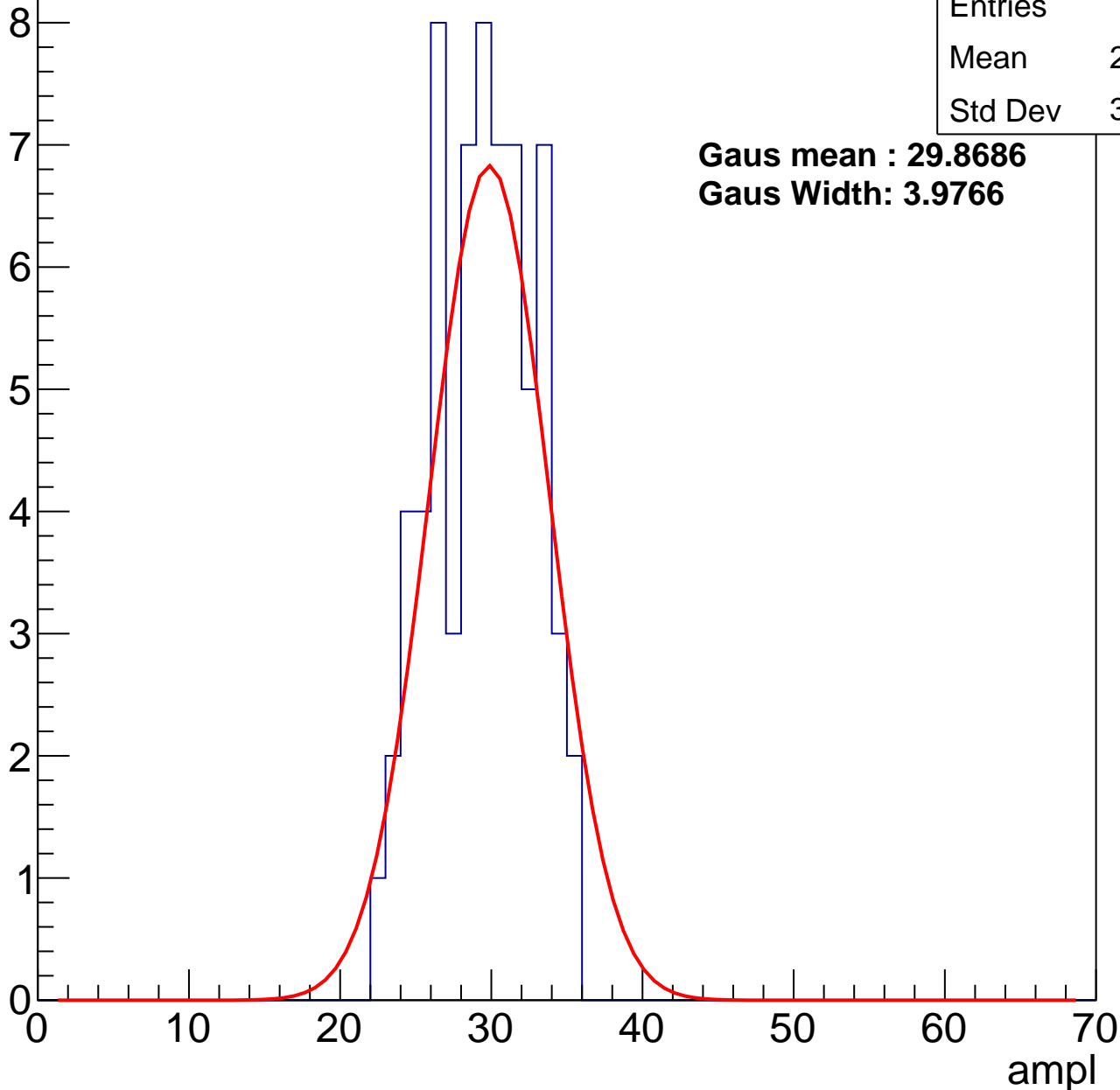
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.99
Std Dev	3.243

**Gaus mean : 29.8686**

**Gaus Width: 3.9766**



# B0L102S, U6-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	80
Mean	36.24
Std Dev	4.154

**Gaus mean : 37.6707**

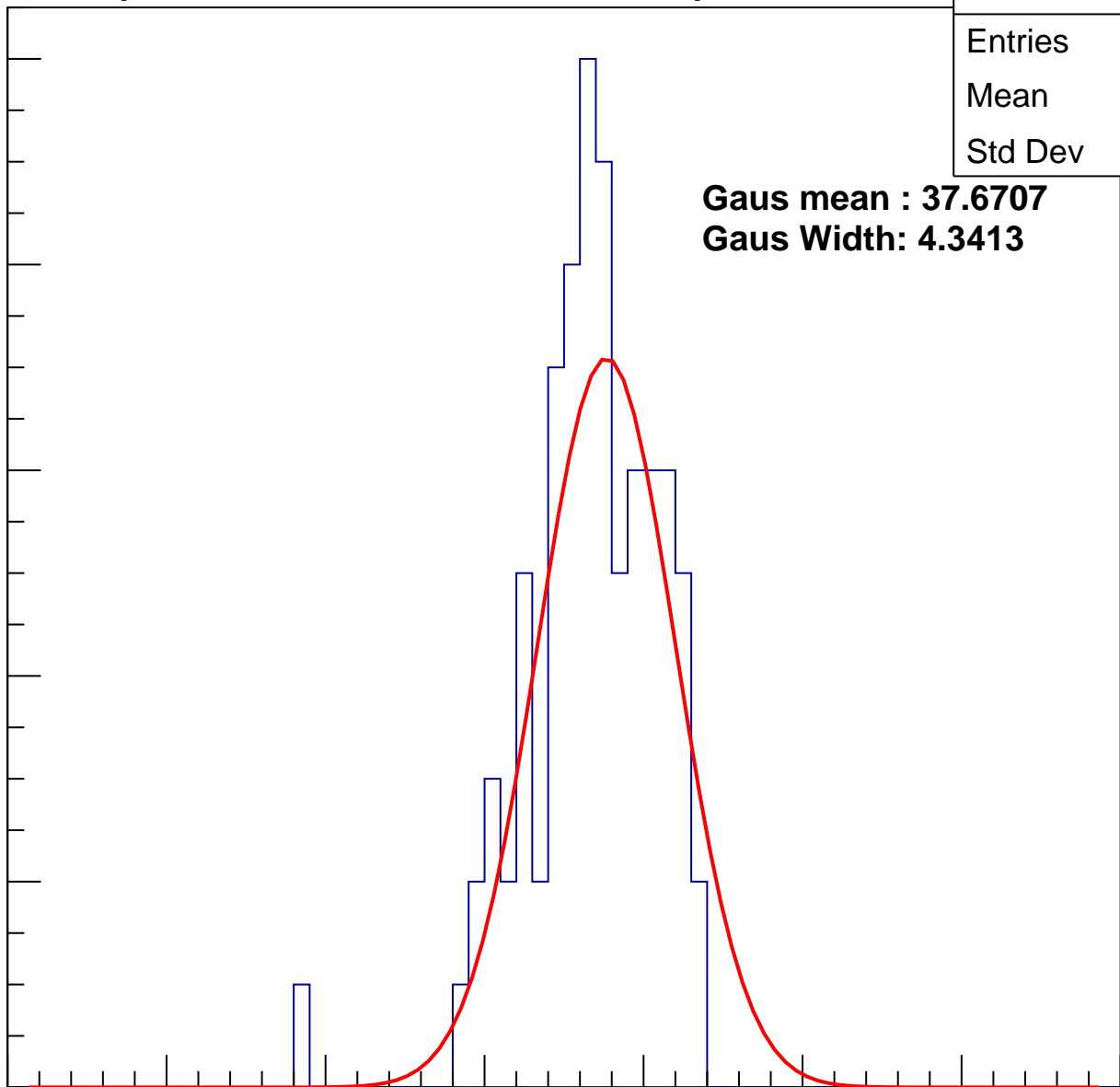
**Gaus Width: 4.3413**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch36, adc2

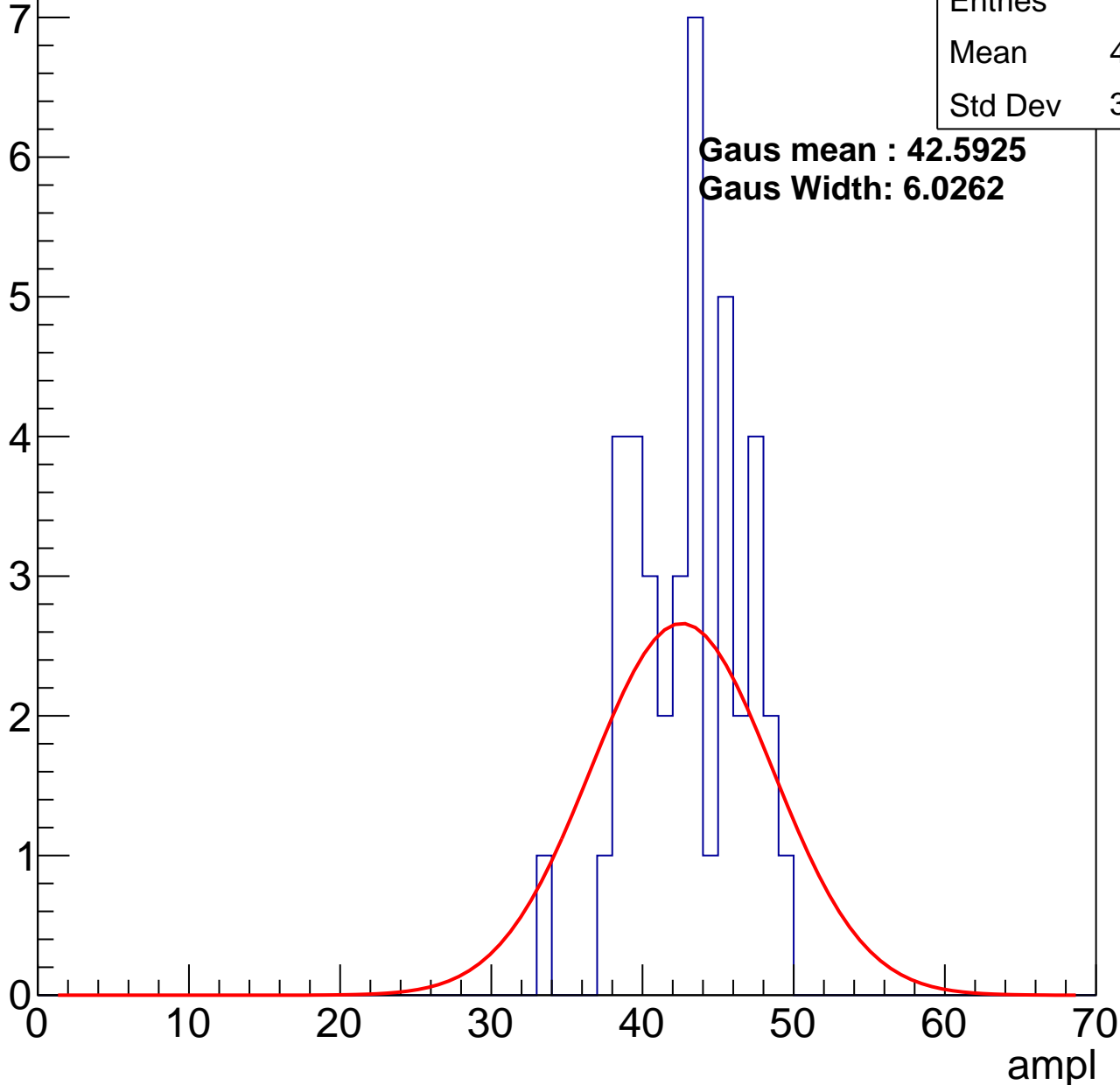
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	42.52
Std Dev	3.599

**Gaus mean : 42.5925**

**Gaus Width: 6.0262**

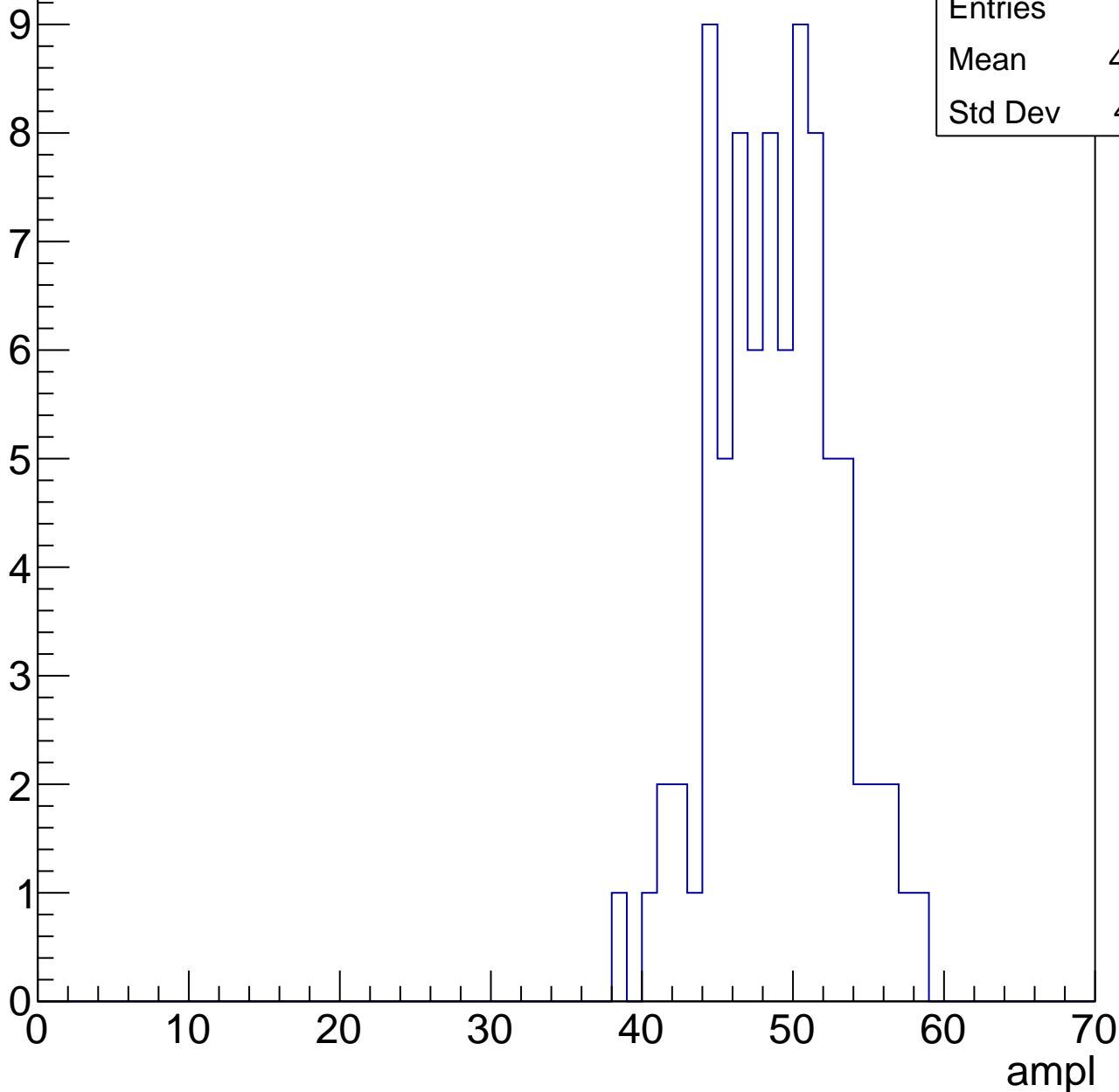


# B0L102S, U6-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	48.38
Std Dev	4.041

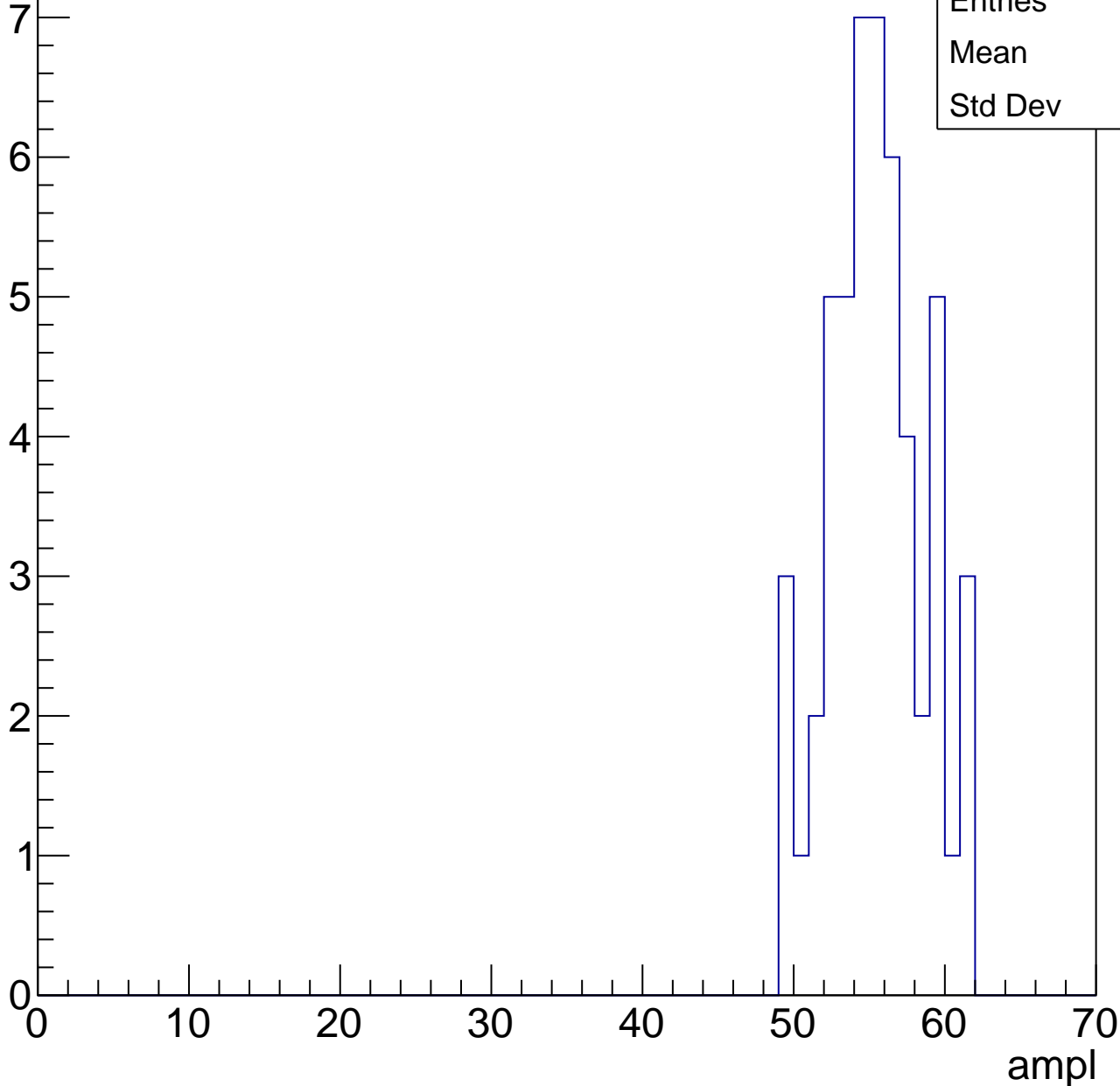


# B0L102S, U6-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	55
Std Dev	3.1

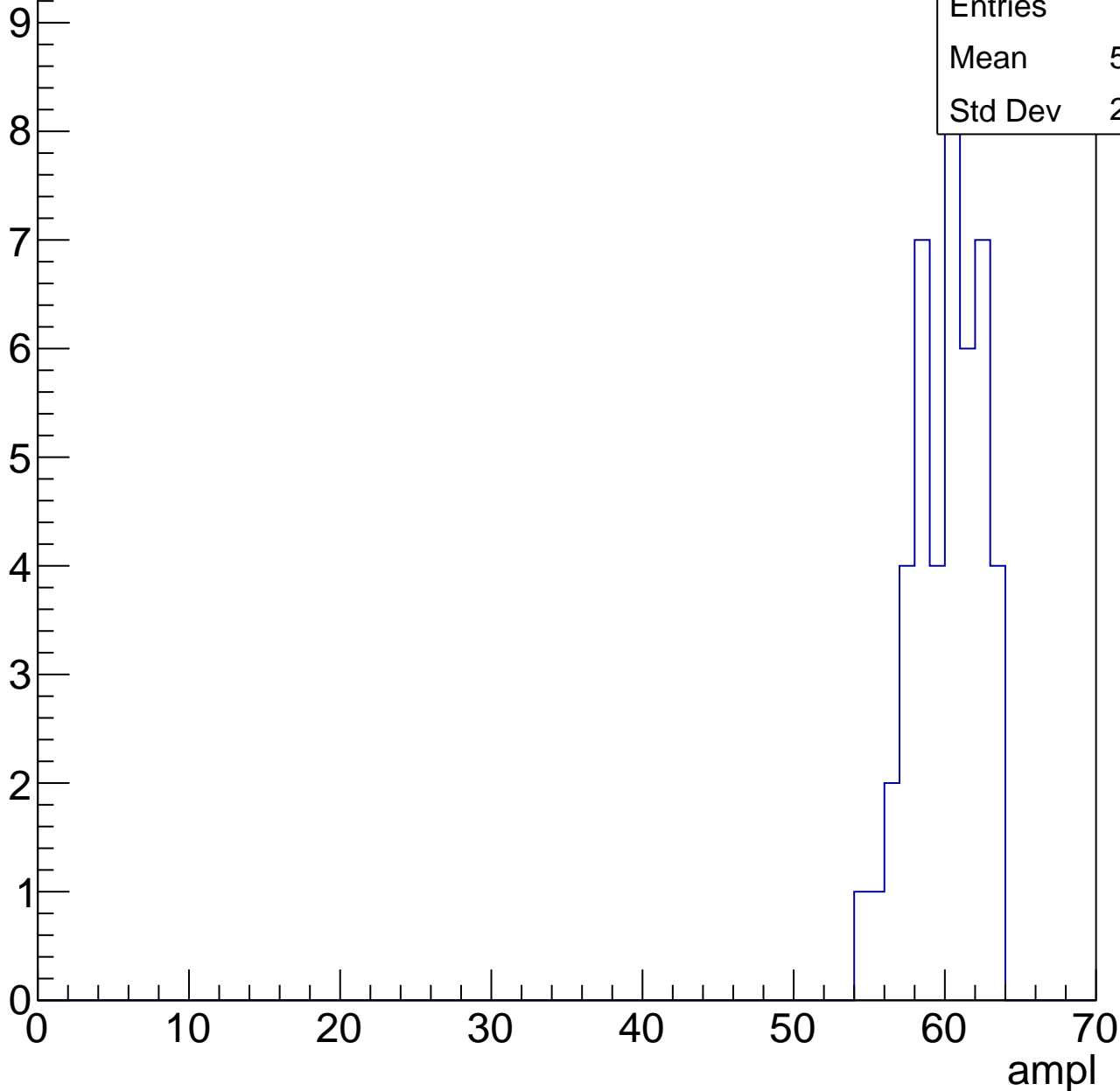


# B0L102S, U6-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	59.62
Std Dev	2.234

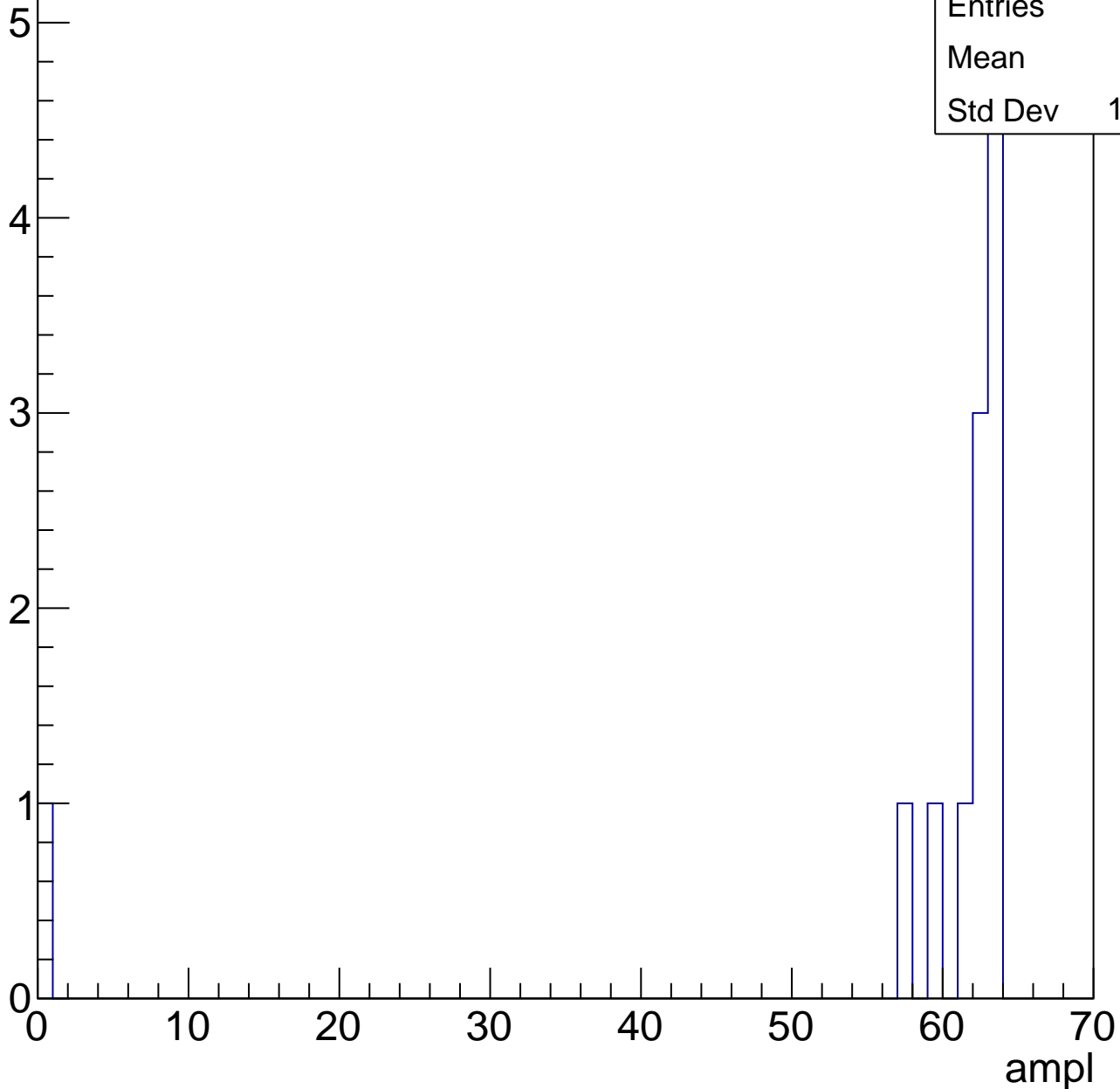


# B0L102S, U6-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	56.5
Std Dev	17.13





# B0L102S, U6-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch37, adc0

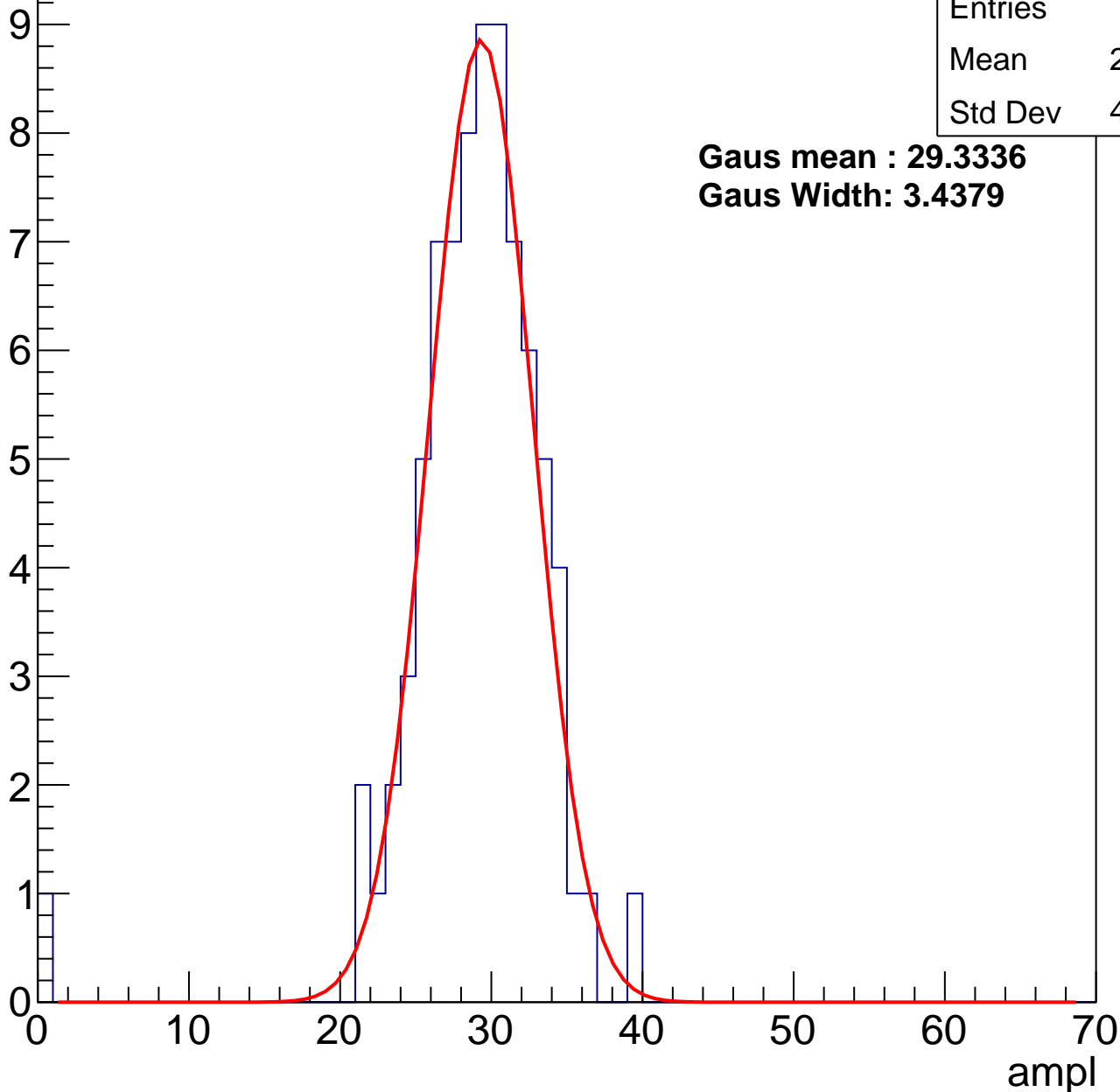
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	28.52
Std Dev	4.749

**Gaus mean : 29.3336**

**Gaus Width: 3.4379**



# B0L102S, U6-ch37, adc1

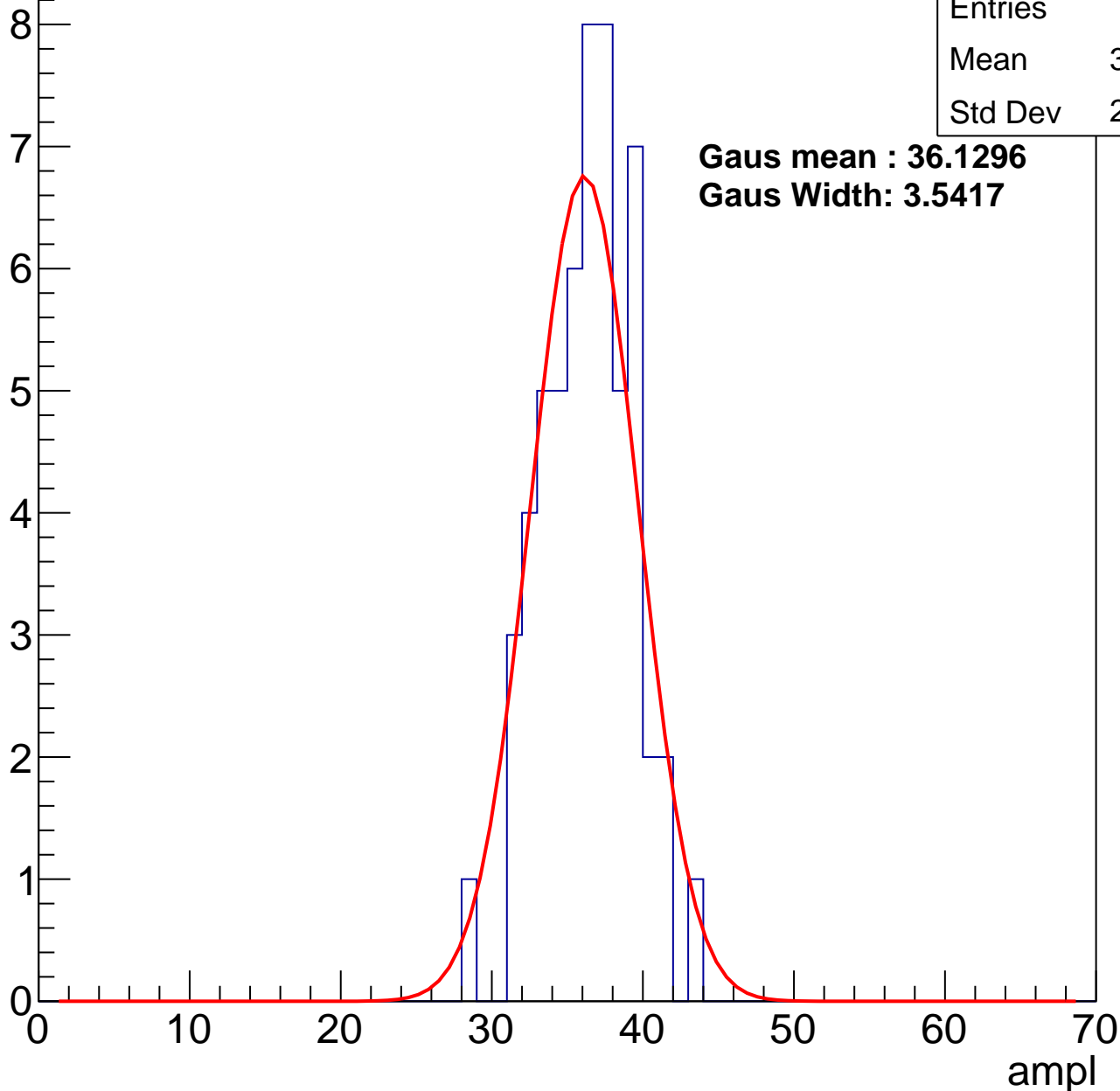
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	35.89
Std Dev	2.948

**Gaus mean : 36.1296**

**Gaus Width: 3.5417**



# B0L102S, U6-ch37, adc2

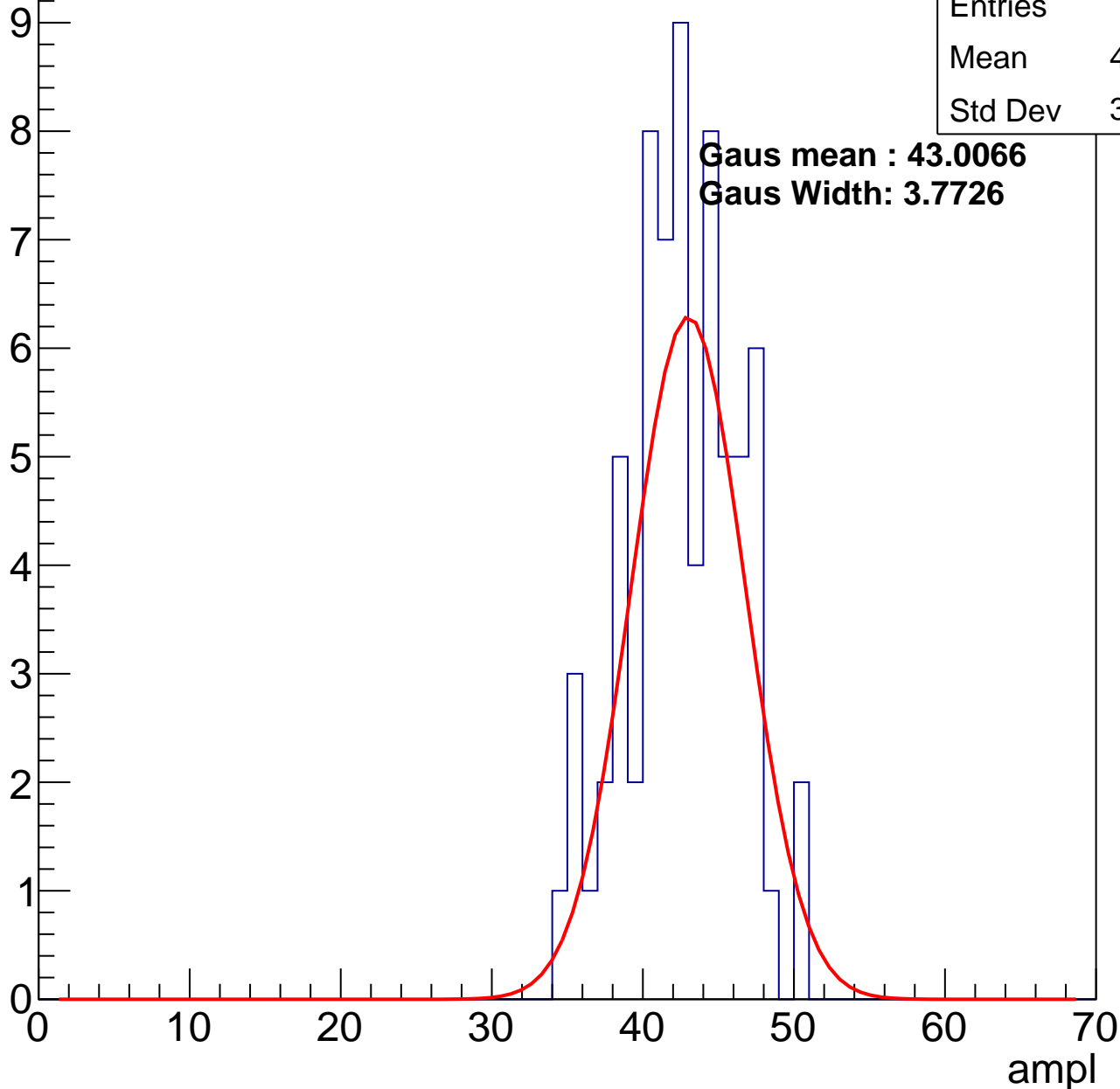
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	42.19
Std Dev	3.625

**Gaus mean : 43.0066**

**Gaus Width: 3.7726**



# B0L102S, U6-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

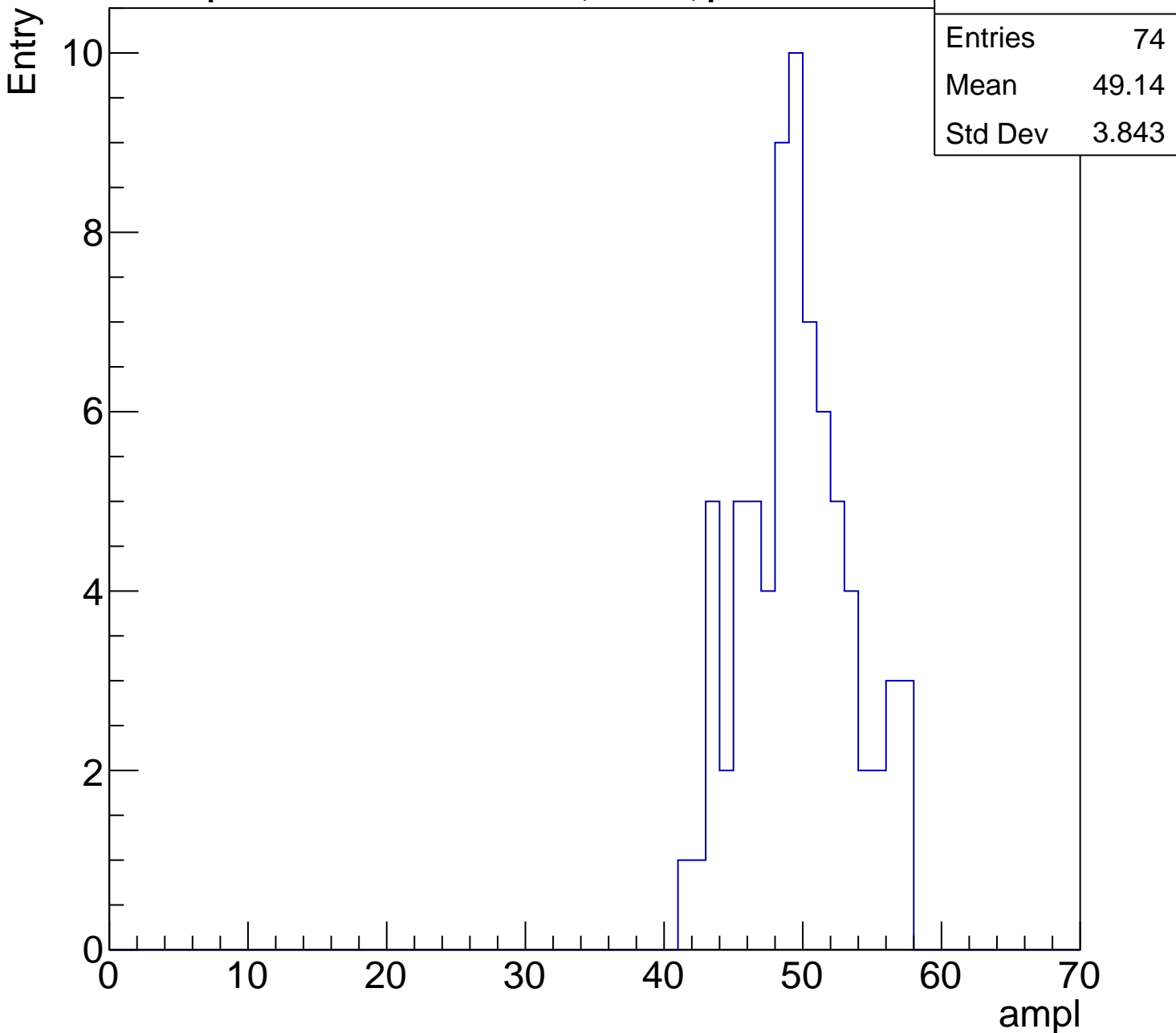
Entries	74
Mean	49.14
Std Dev	3.843

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

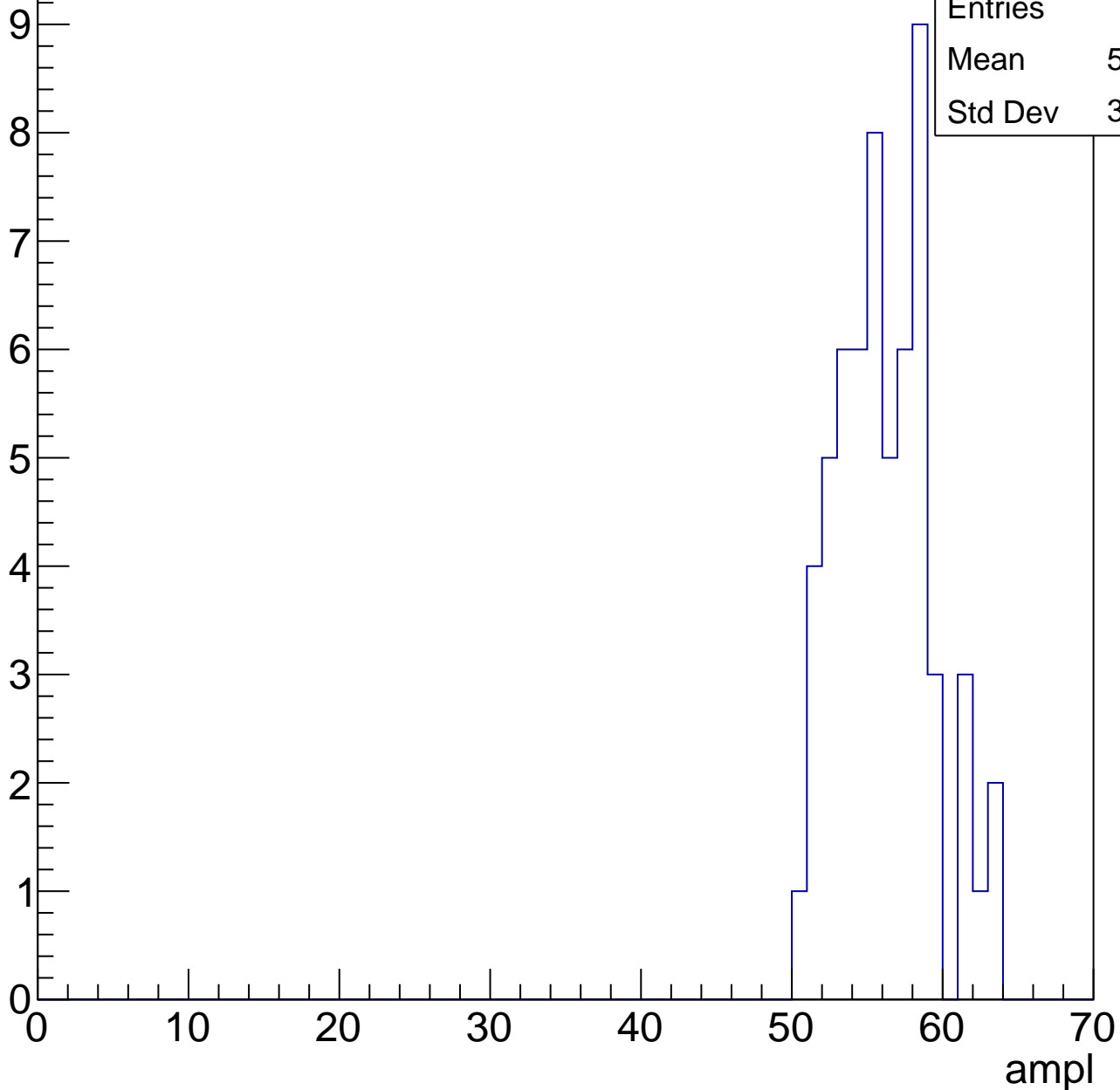


# B0L102S, U6-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

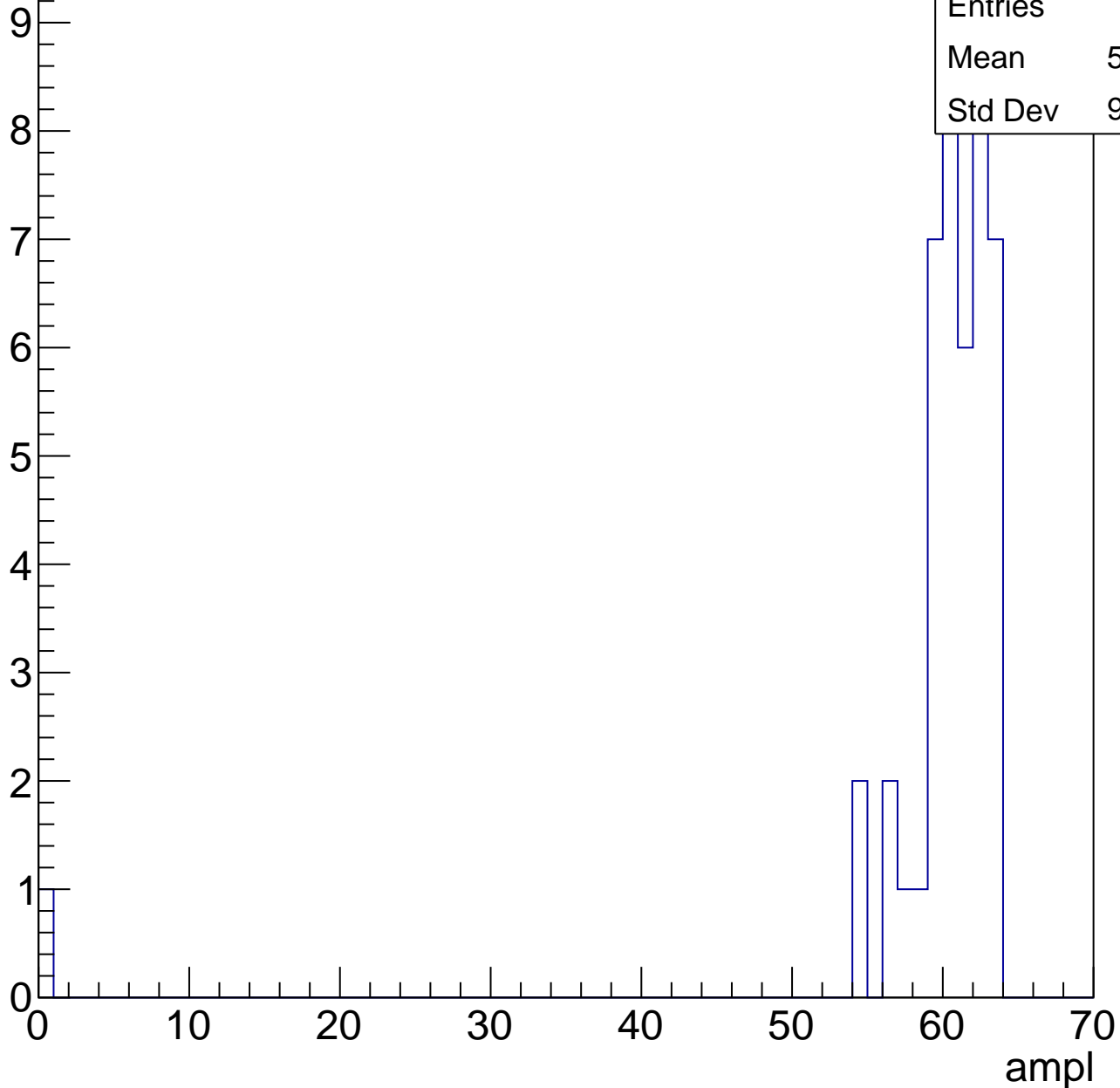
Entries	59
Mean	55.73
Std Dev	3.124



# B0L102S, U6-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch38, adc0

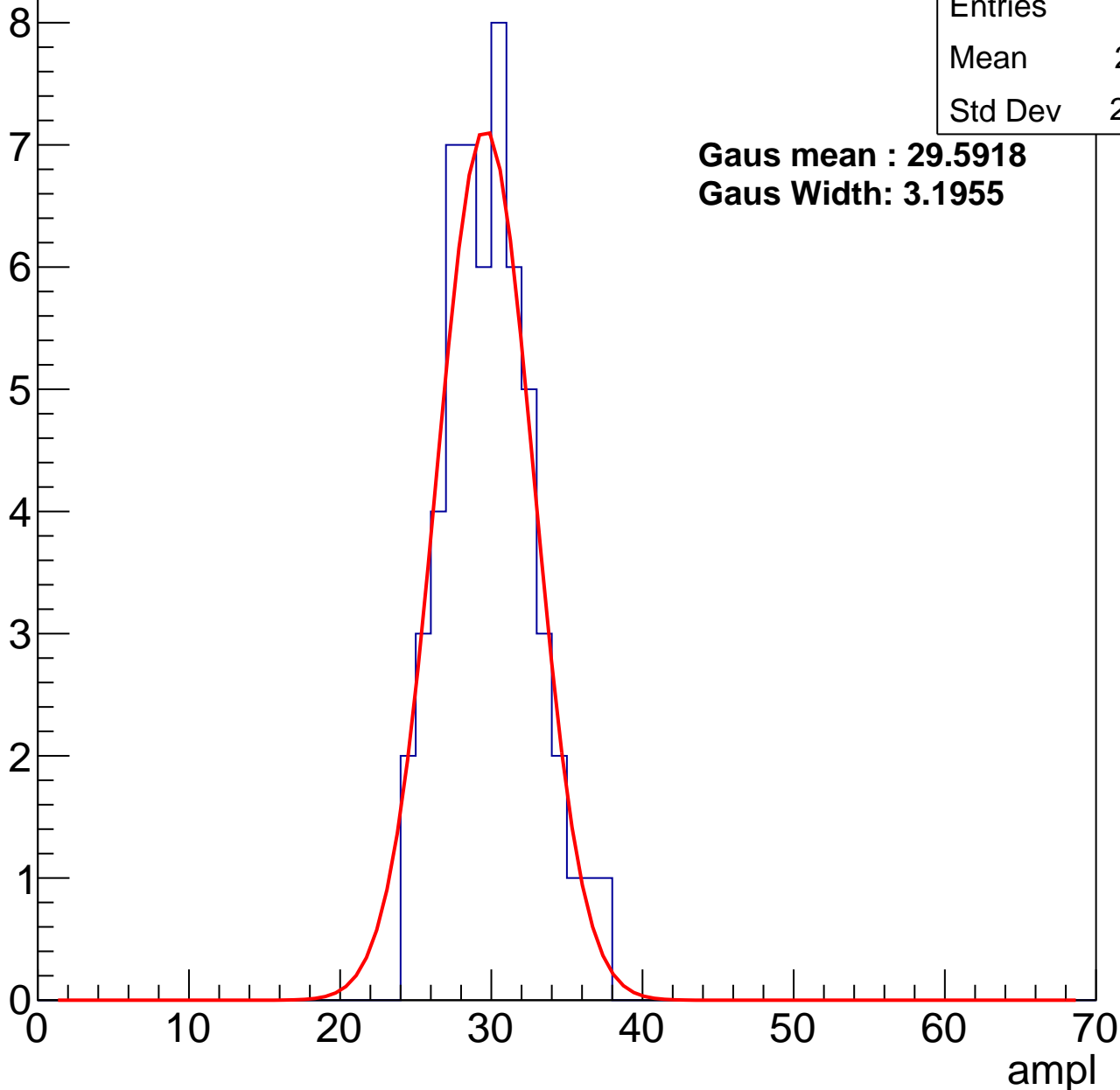
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	29.41
Std Dev	2.939

**Gaus mean : 29.5918**

**Gaus Width: 3.1955**



# B0L102S, U6-ch38, adc1

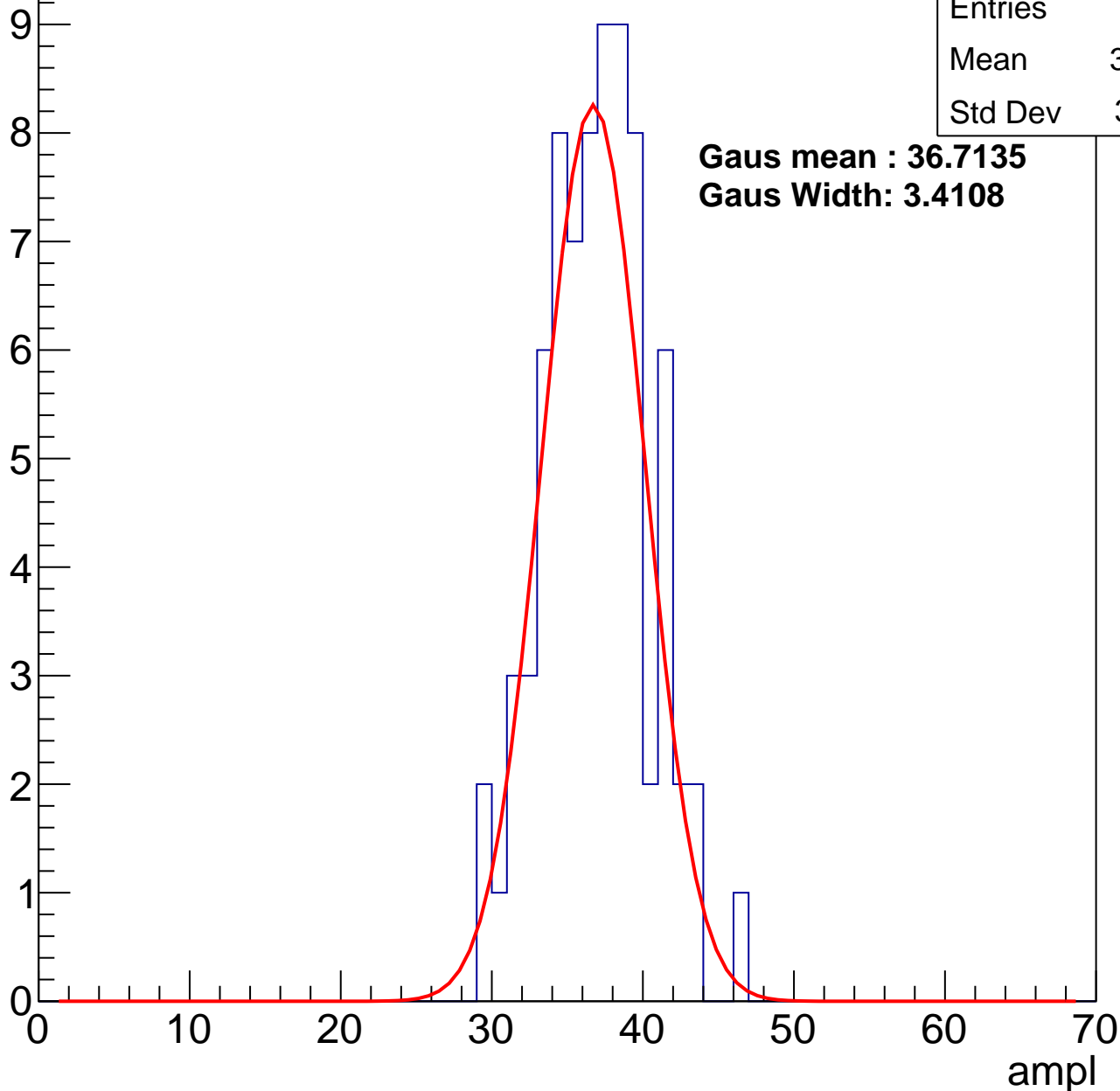
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	36.48
Std Dev	3.421

**Gaus mean : 36.7135**

**Gaus Width: 3.4108**



# B0L102S, U6-ch38, adc2

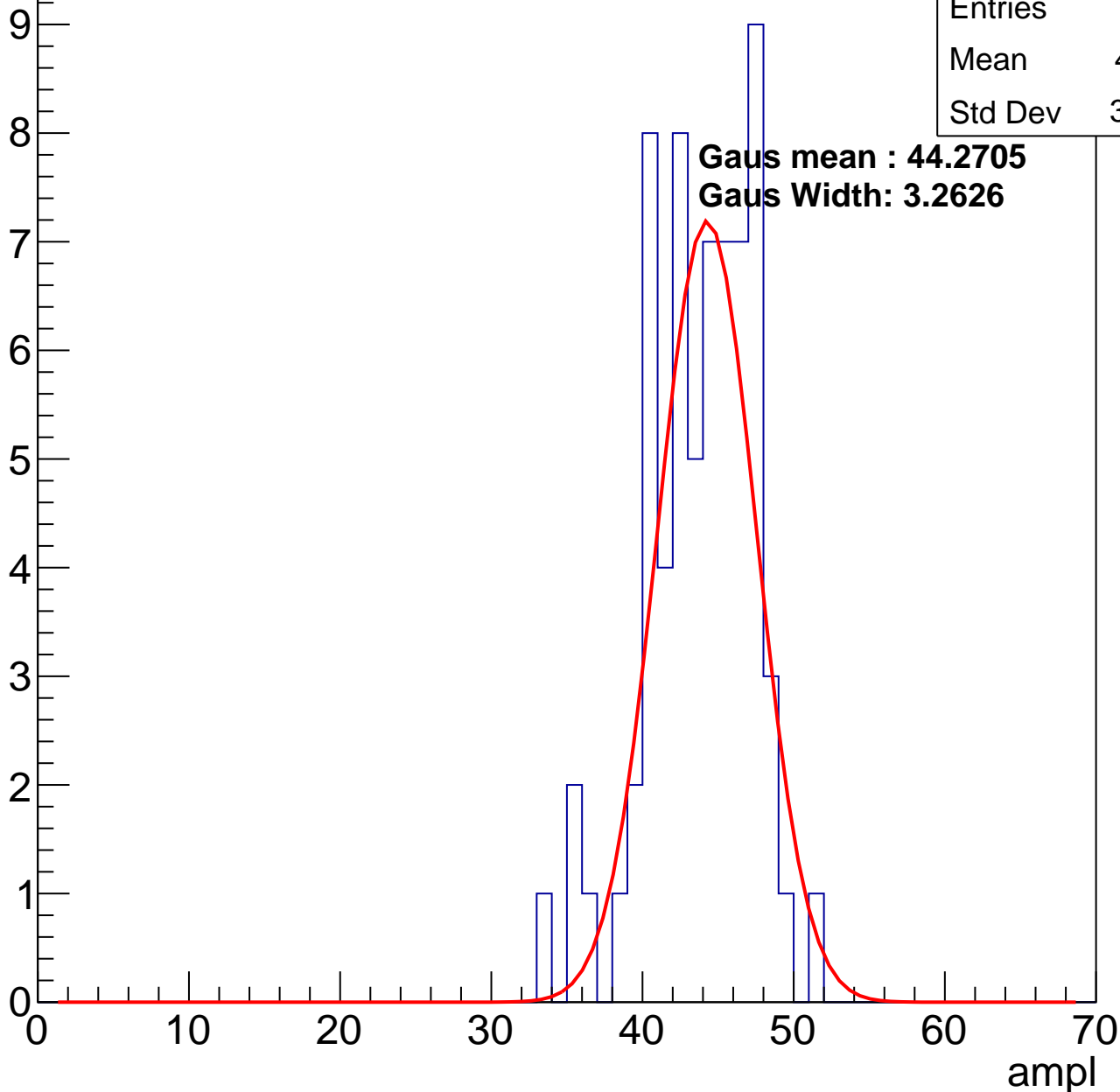
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	43.31
Std Dev	3.542

**Gaus mean : 44.2705**

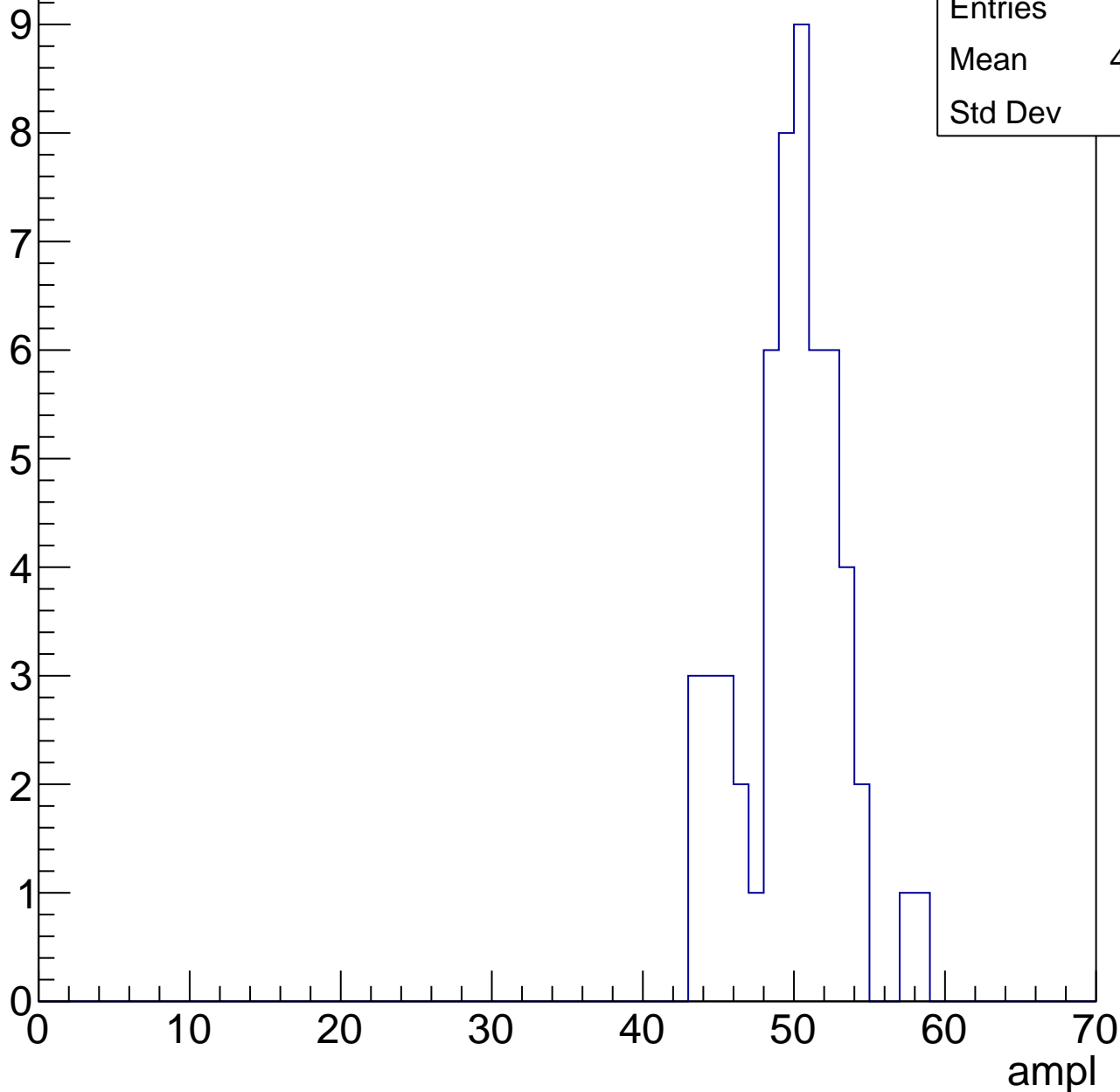
**Gaus Width: 3.2626**



# B0L102S, U6-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

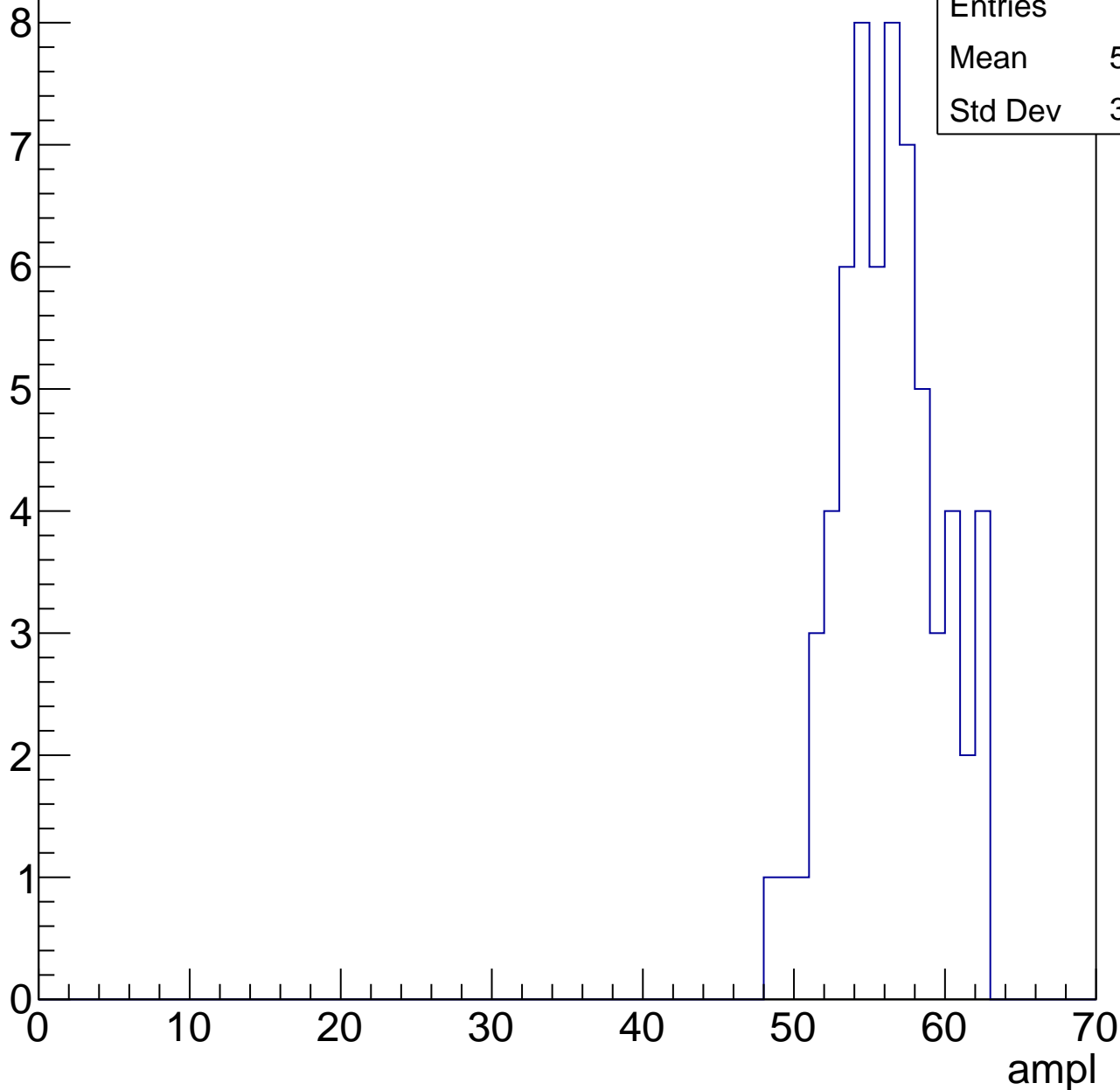


# B0L102S, U6-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.75
Std Dev	3.304

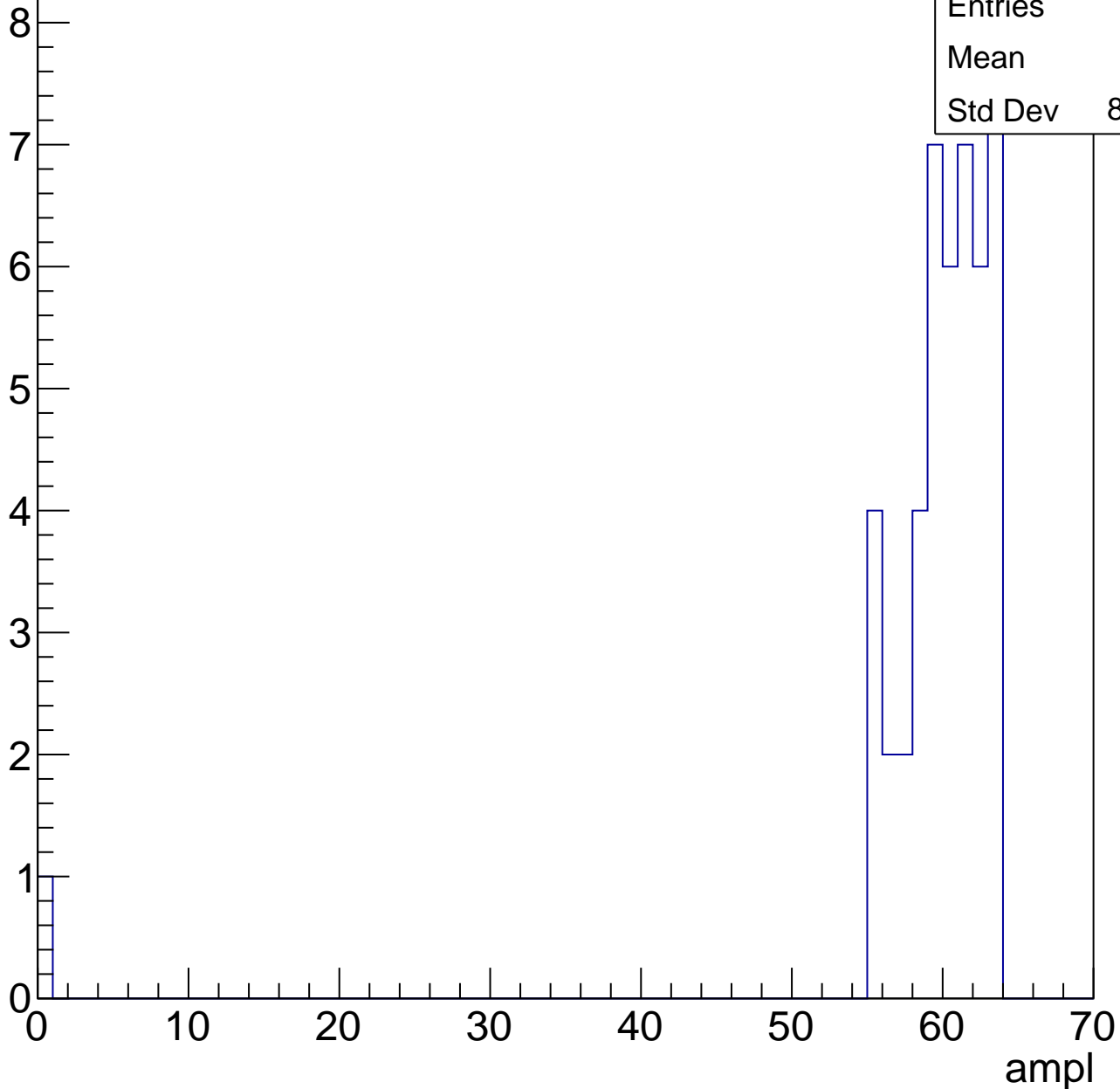


# B0L102S, U6-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	58.6
Std Dev	8.972



# B0L102S, U6-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

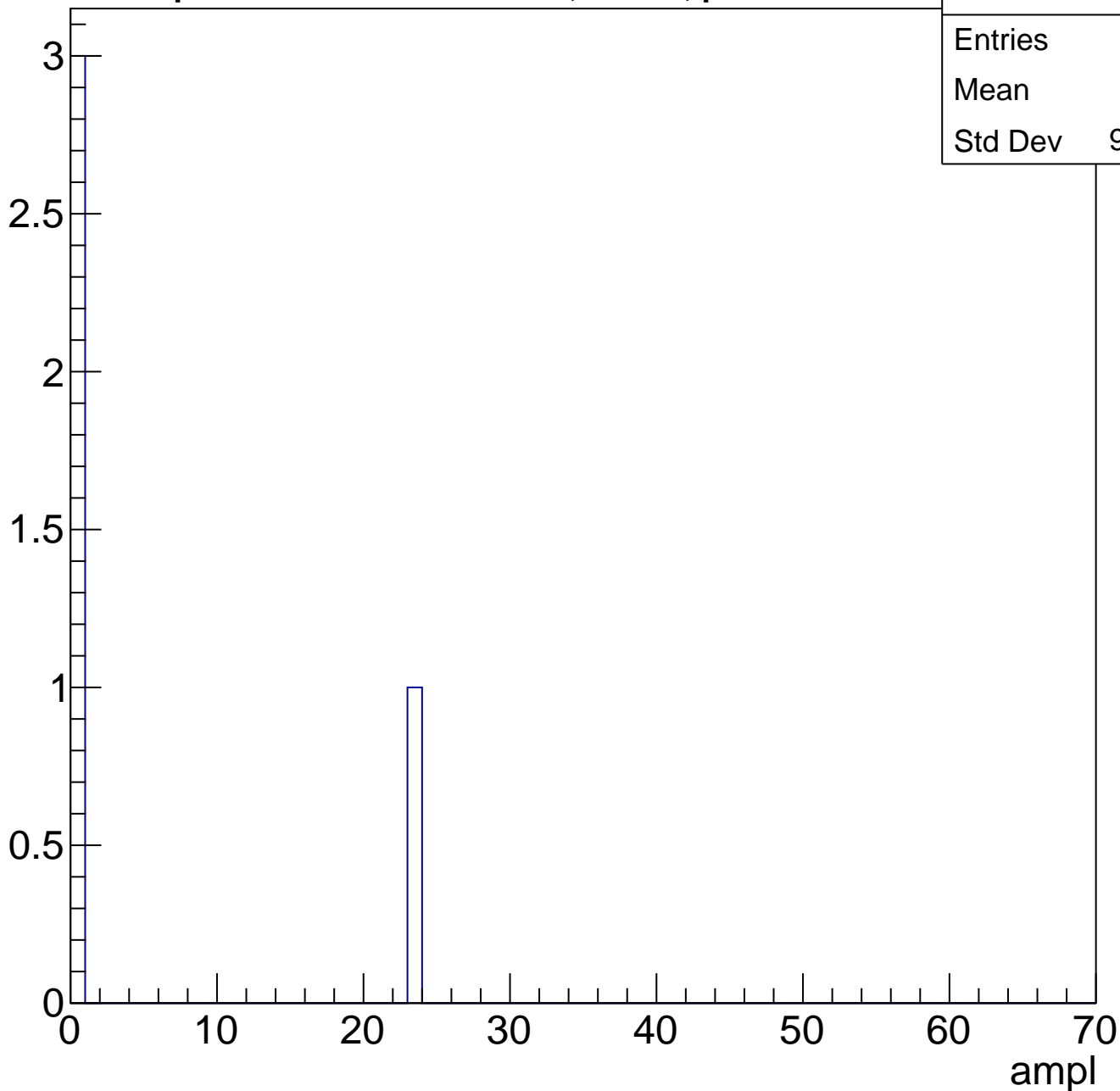




# B0L102S, U6-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch39, adc0

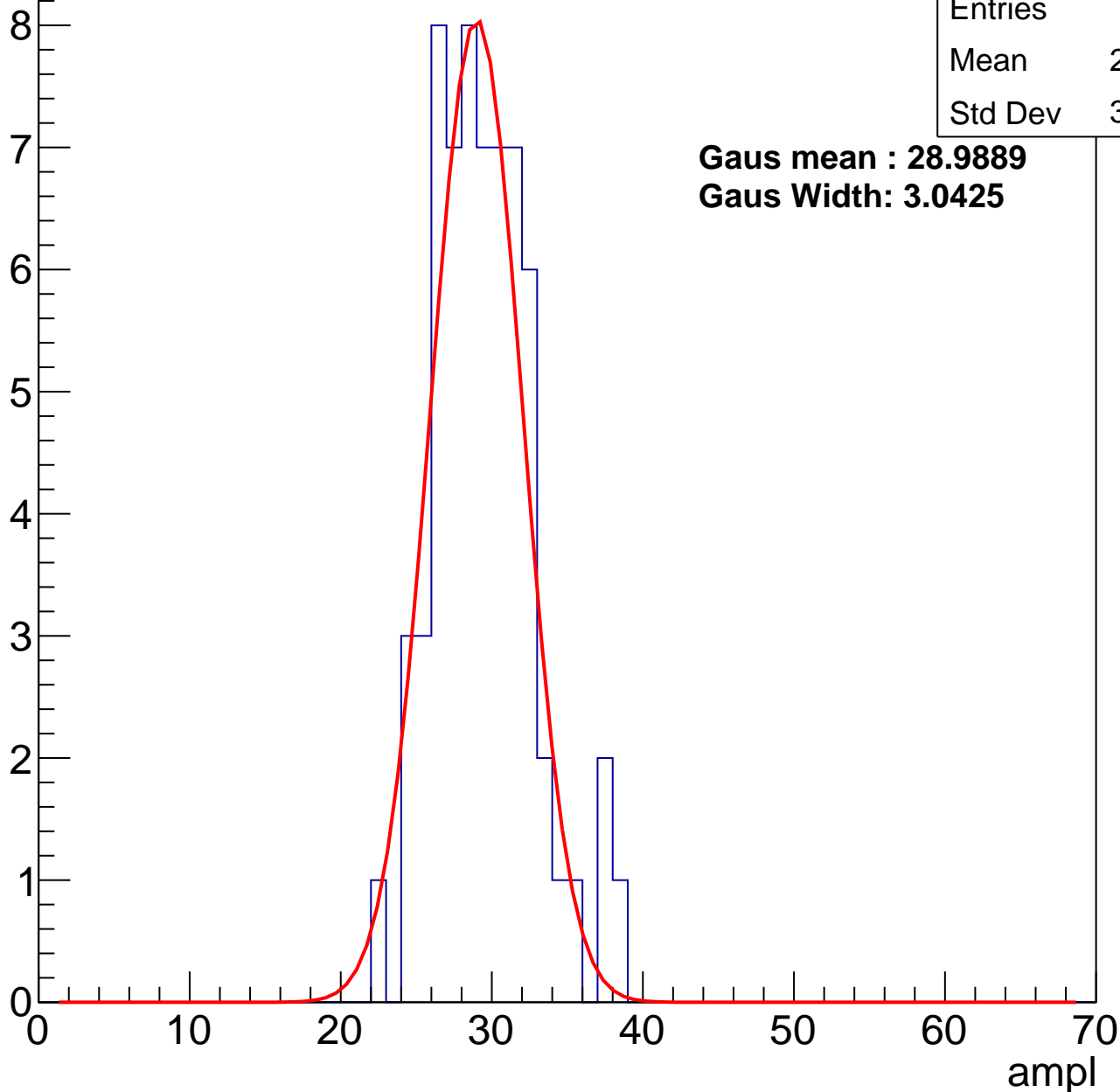
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	29.05
Std Dev	3.238

**Gaus mean : 28.9889**

**Gaus Width: 3.0425**



# B0L102S, U6-ch39, adc1

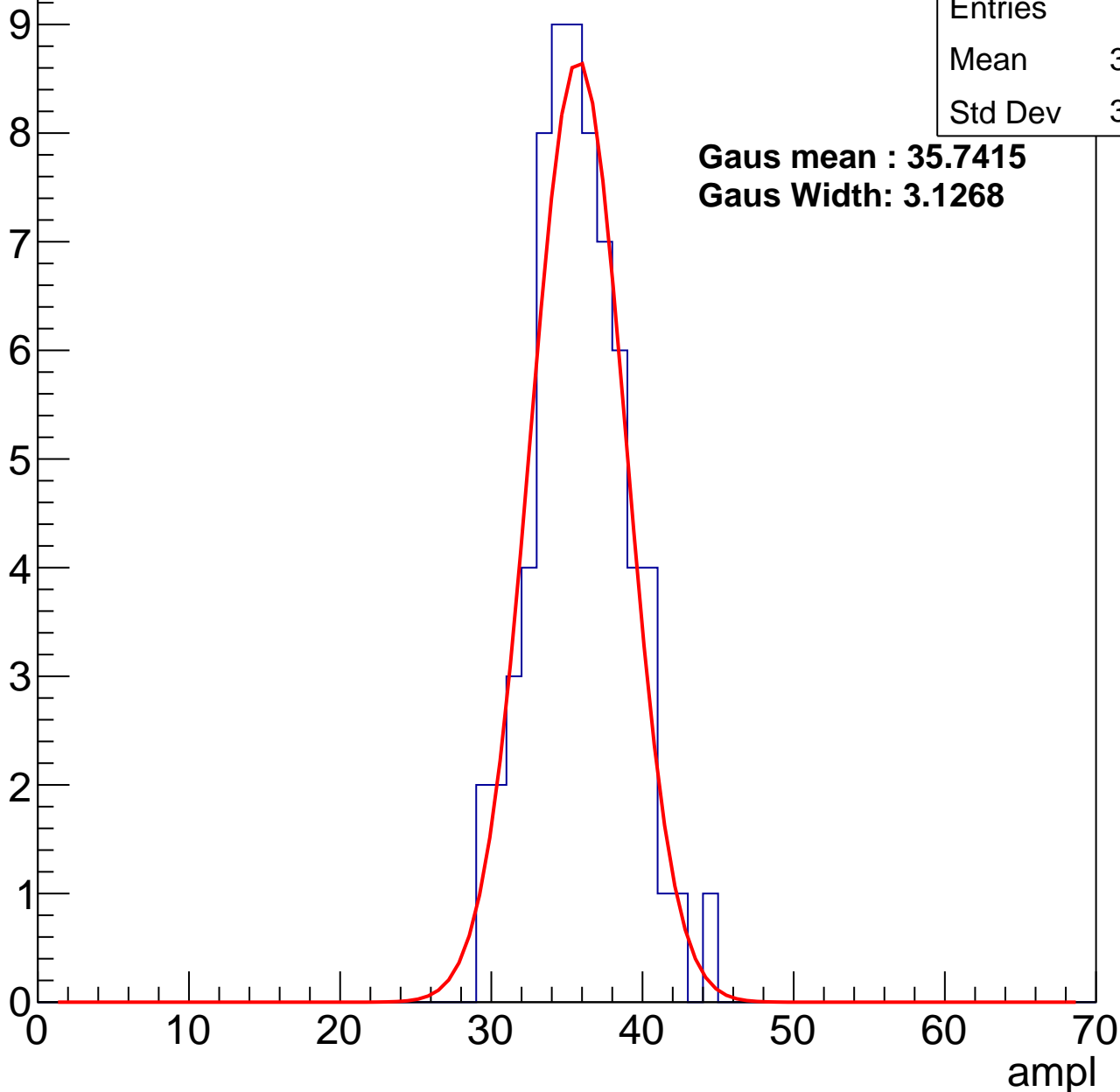
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	35.39
Std Dev	3.084

**Gaus mean : 35.7415**

**Gaus Width: 3.1268**



# B0L102S, U6-ch39, adc2

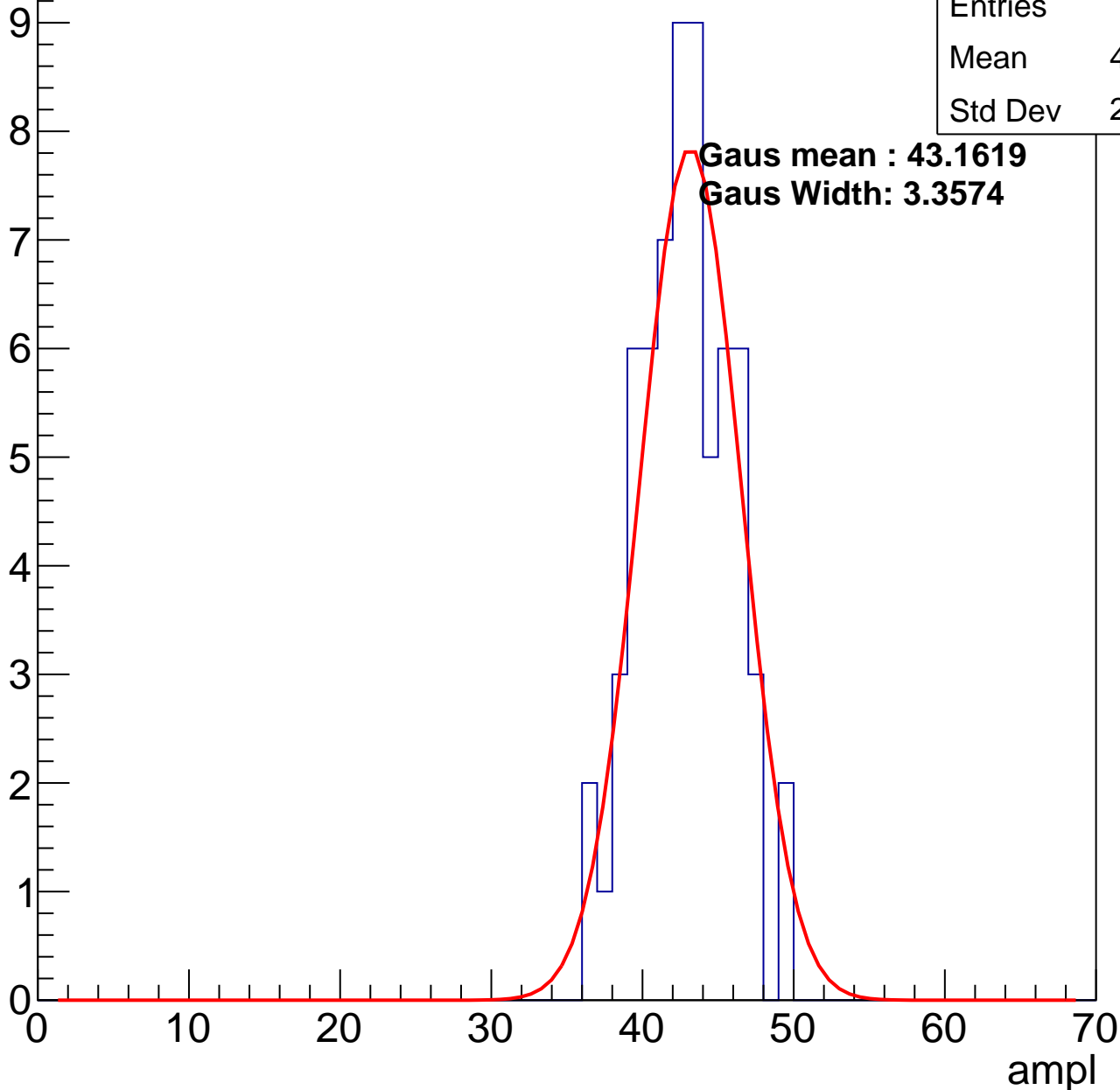
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.37
Std Dev	2.969

**Gaus mean : 43.1619**

**Gaus Width: 3.3574**

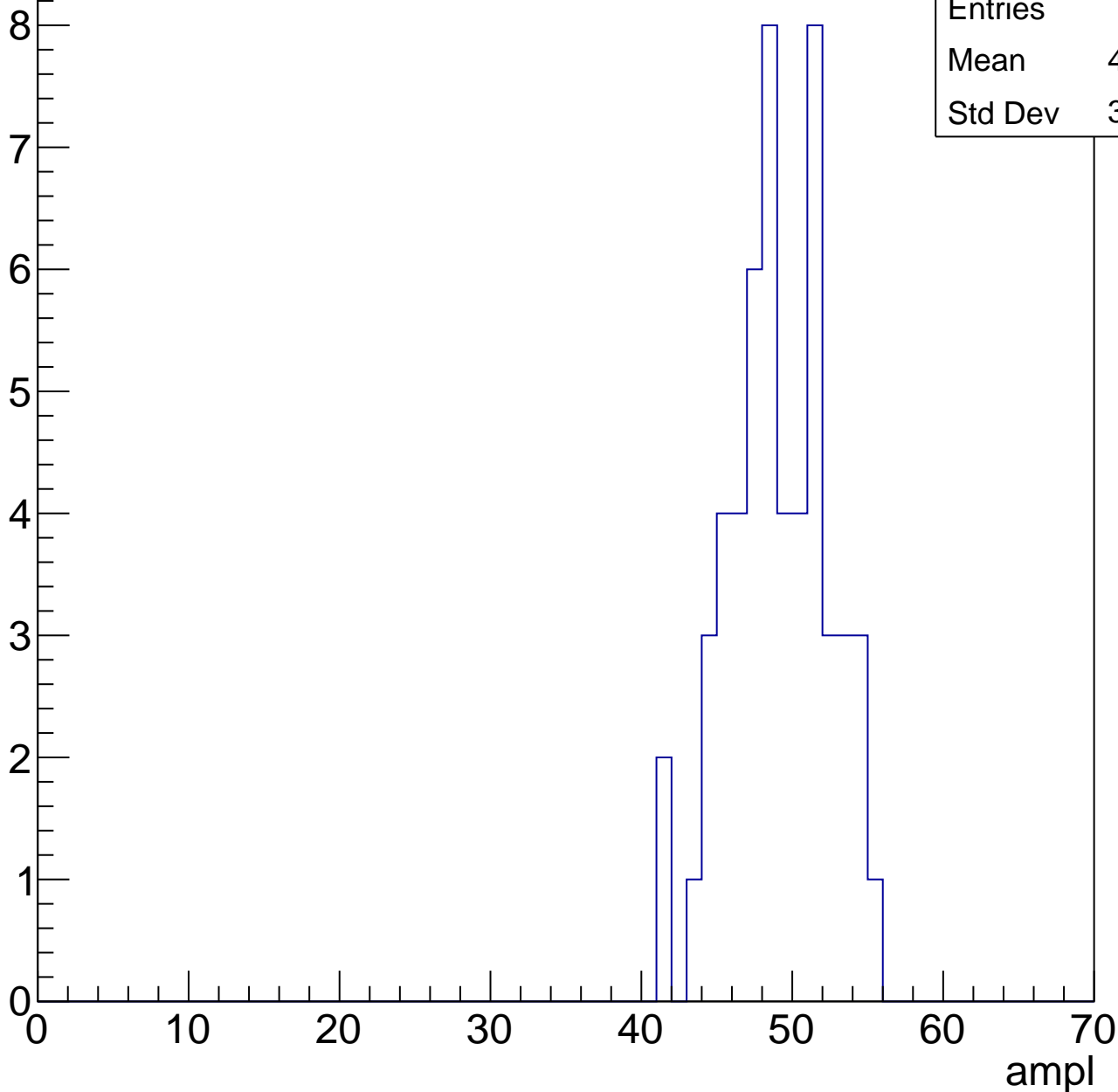


# B0L102S, U6-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	48.57
Std Dev	3.286

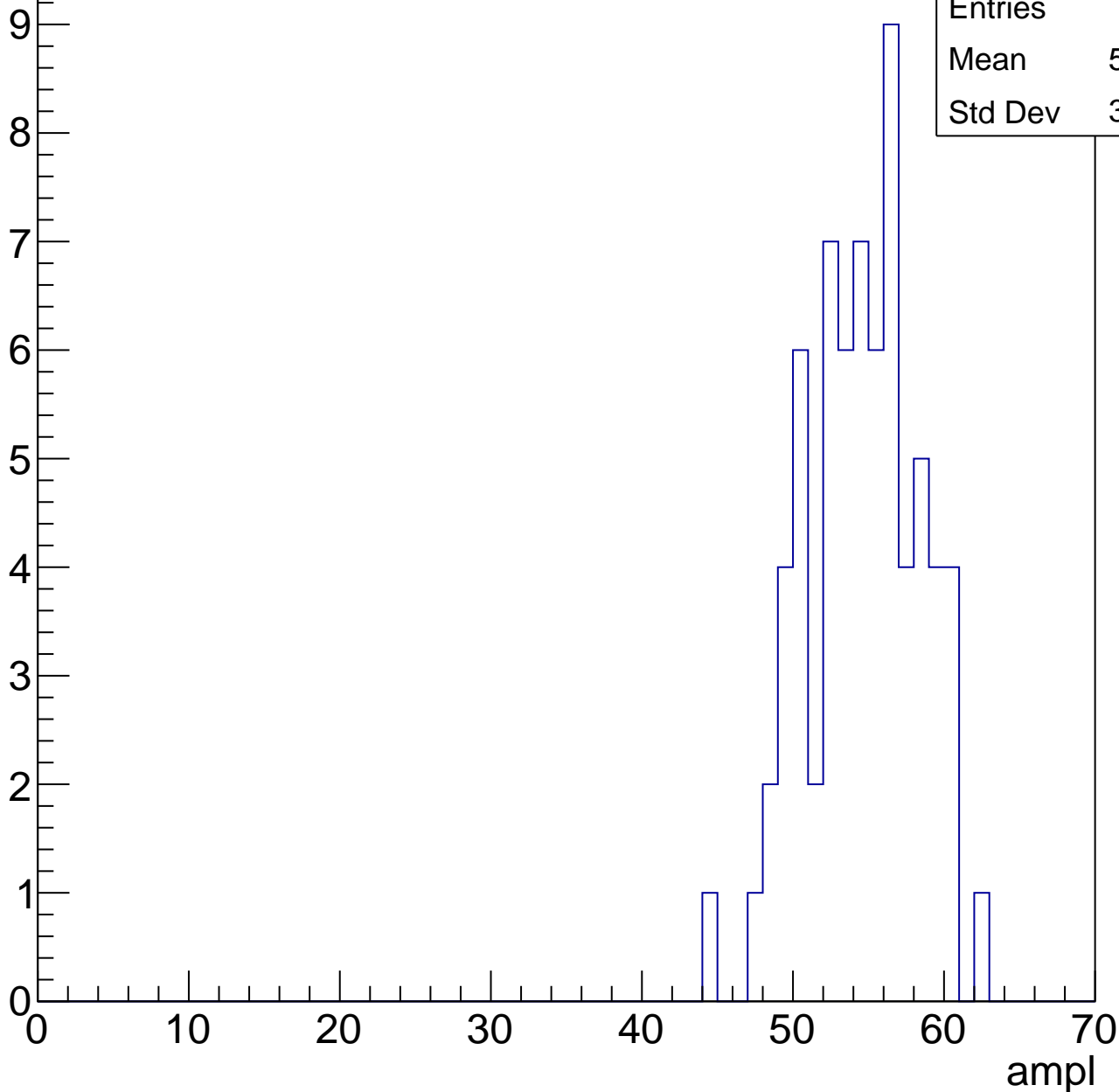


# B0L102S, U6-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	54.13
Std Dev	3.679

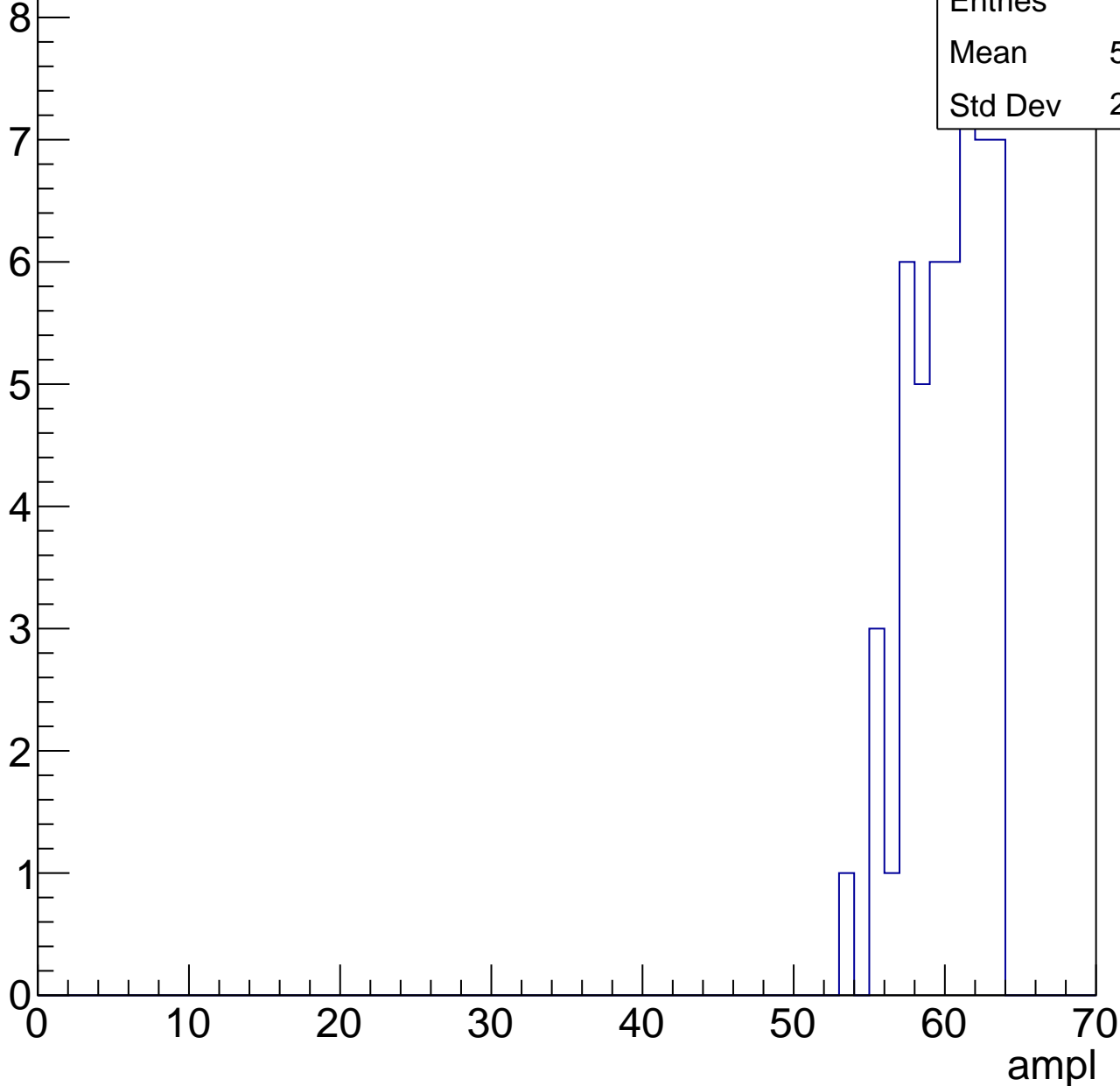


# B0L102S, U6-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

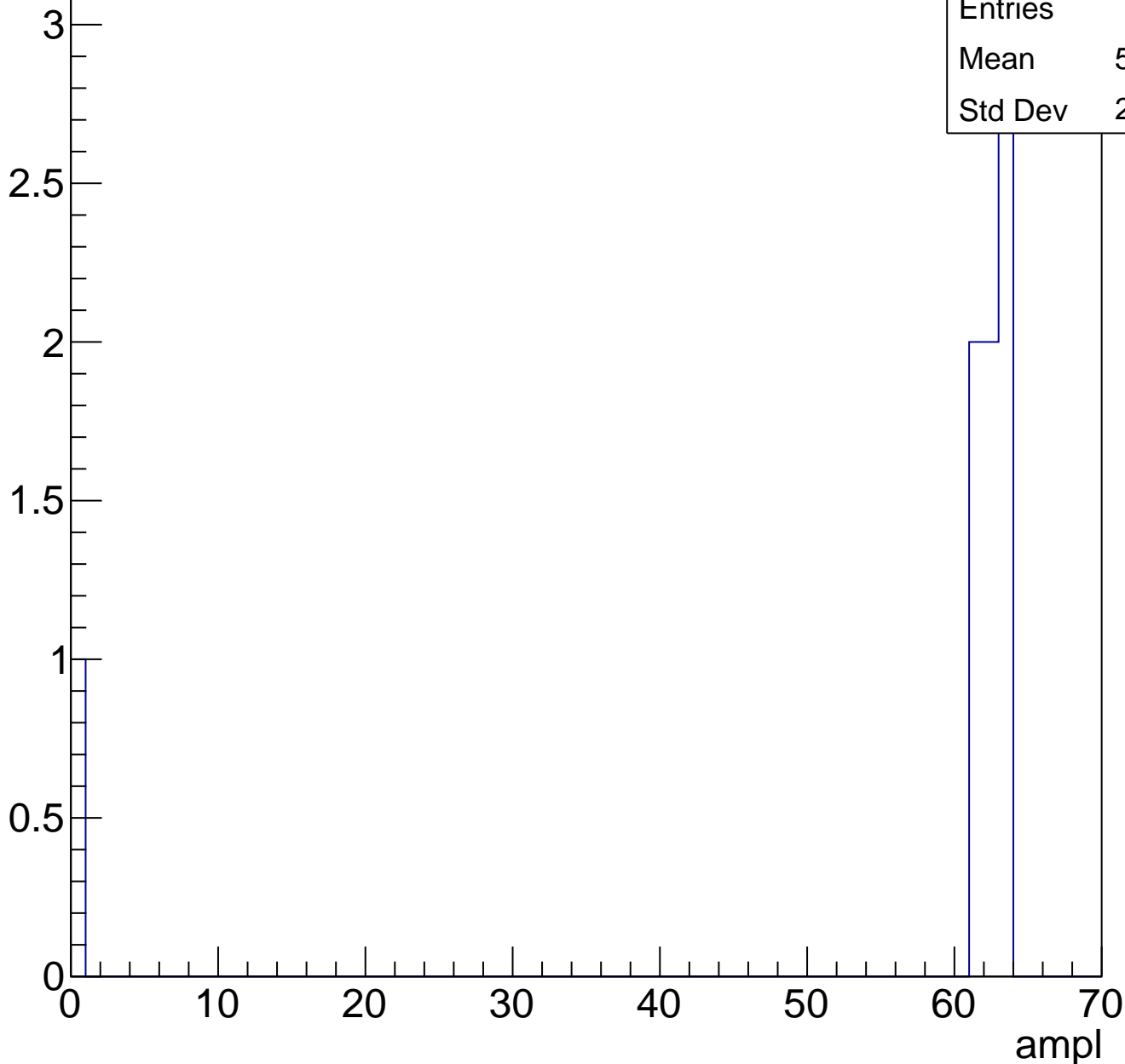
Entries	50
Mean	59.66
Std Dev	2.503



# B0L102S, U6-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch40, adc0

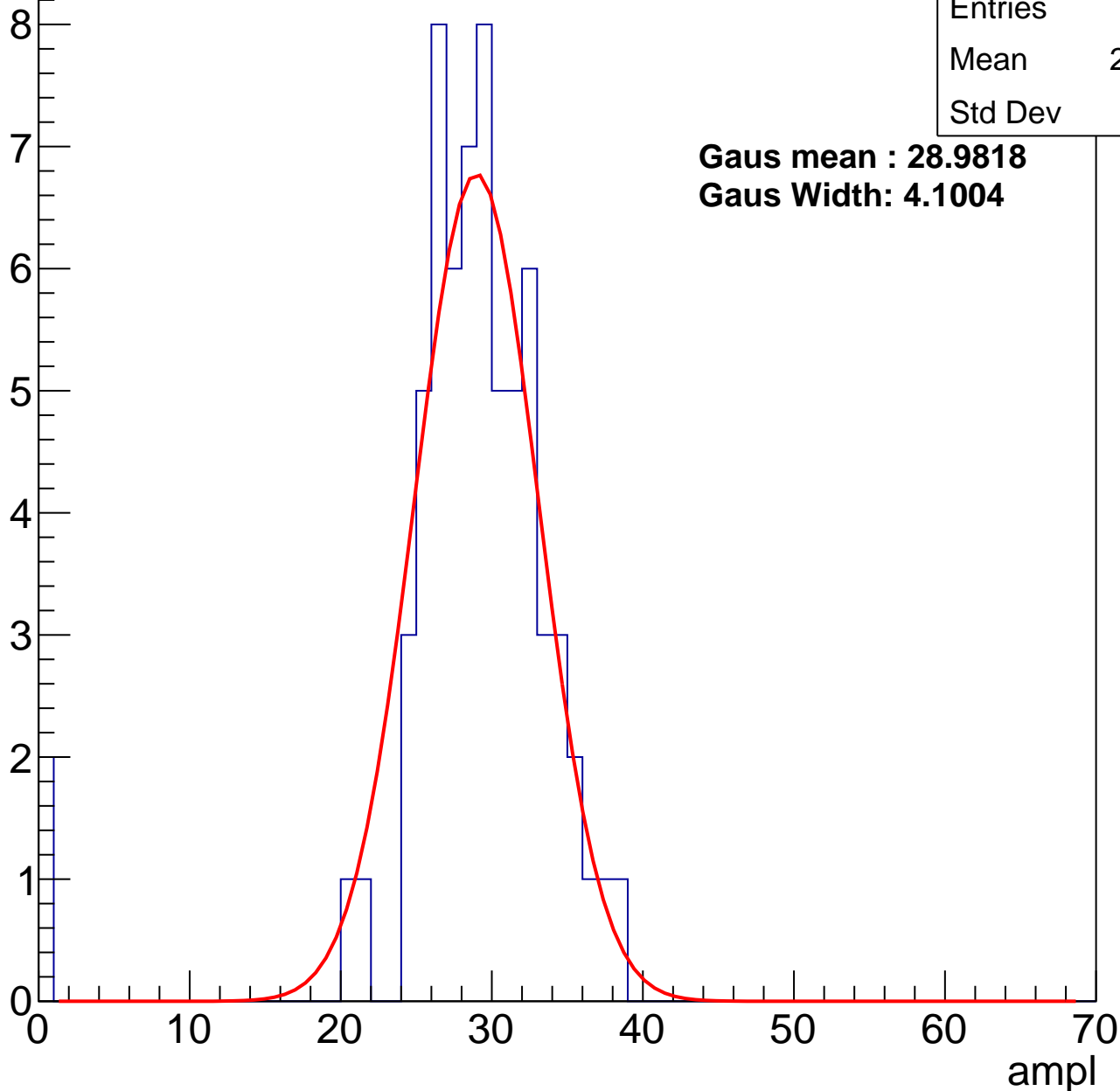
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.16
Std Dev	6.07

**Gaus mean : 28.9818**

**Gaus Width: 4.1004**



# B0L102S, U6-ch40, adc1

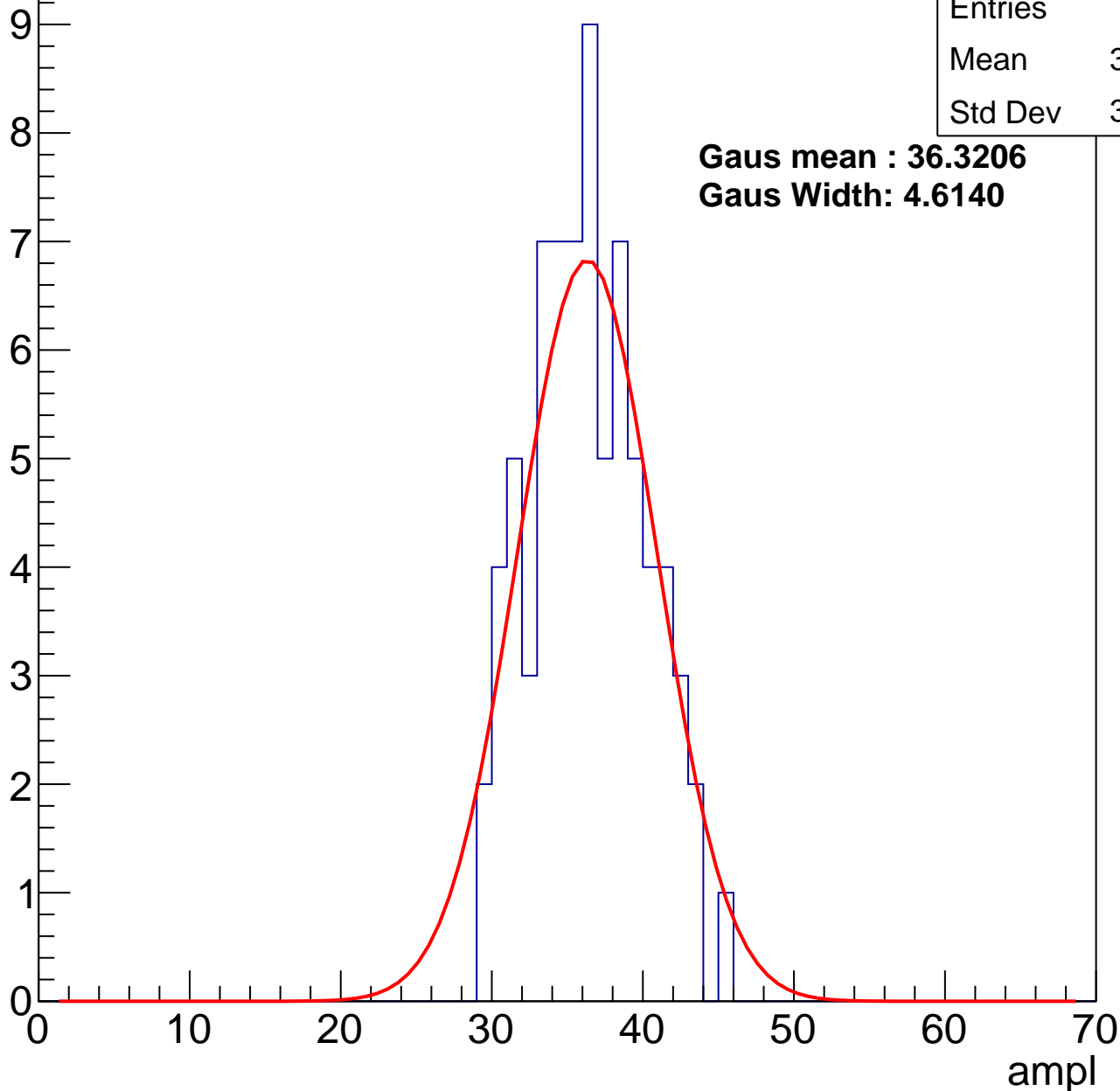
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	35.92
Std Dev	3.727

**Gaus mean : 36.3206**

**Gaus Width: 4.6140**



# B0L102S, U6-ch40, adc2

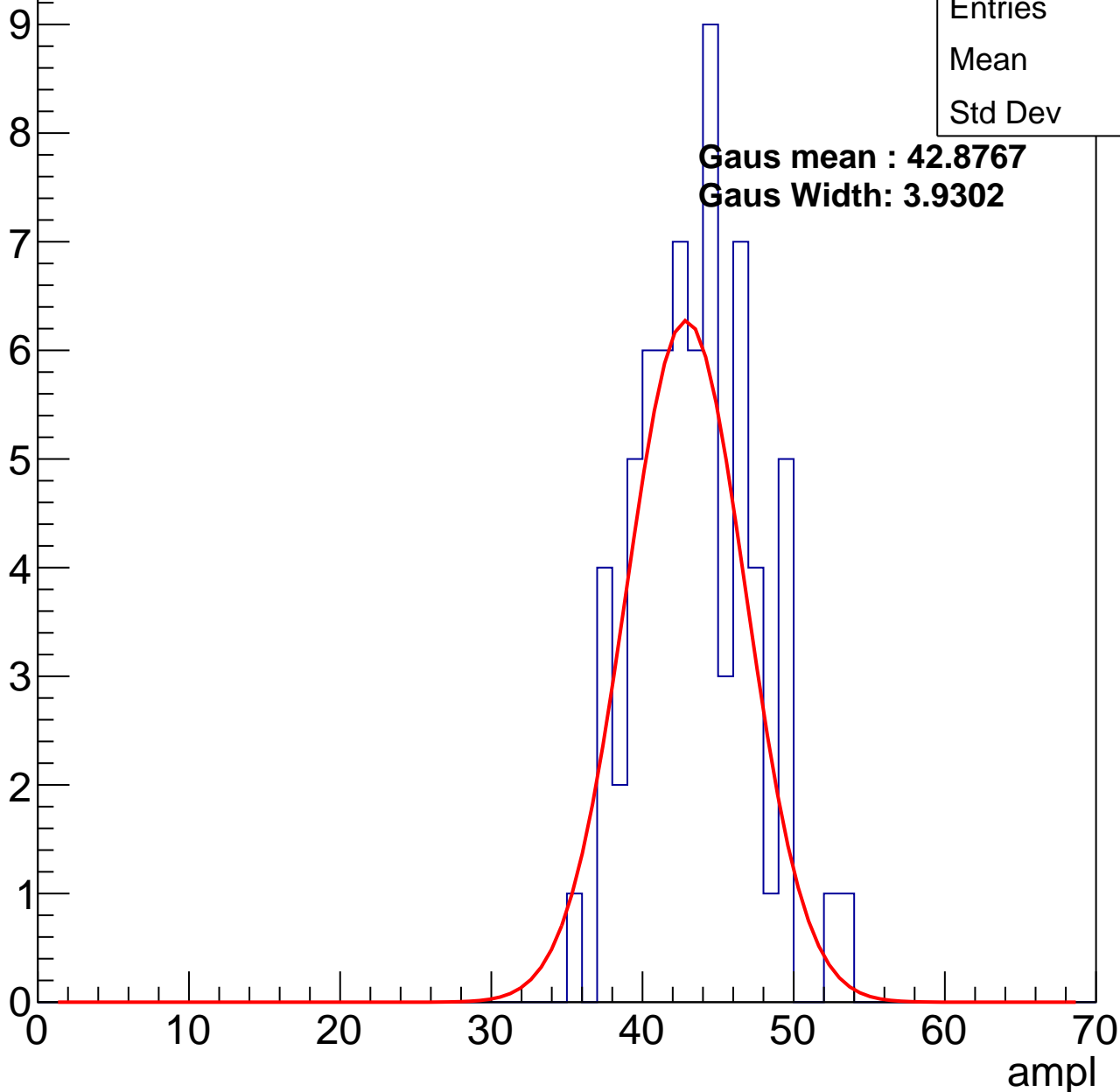
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	43.1
Std Dev	3.75

**Gaus mean : 42.8767**

**Gaus Width: 3.9302**



# B0L102S, U6-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

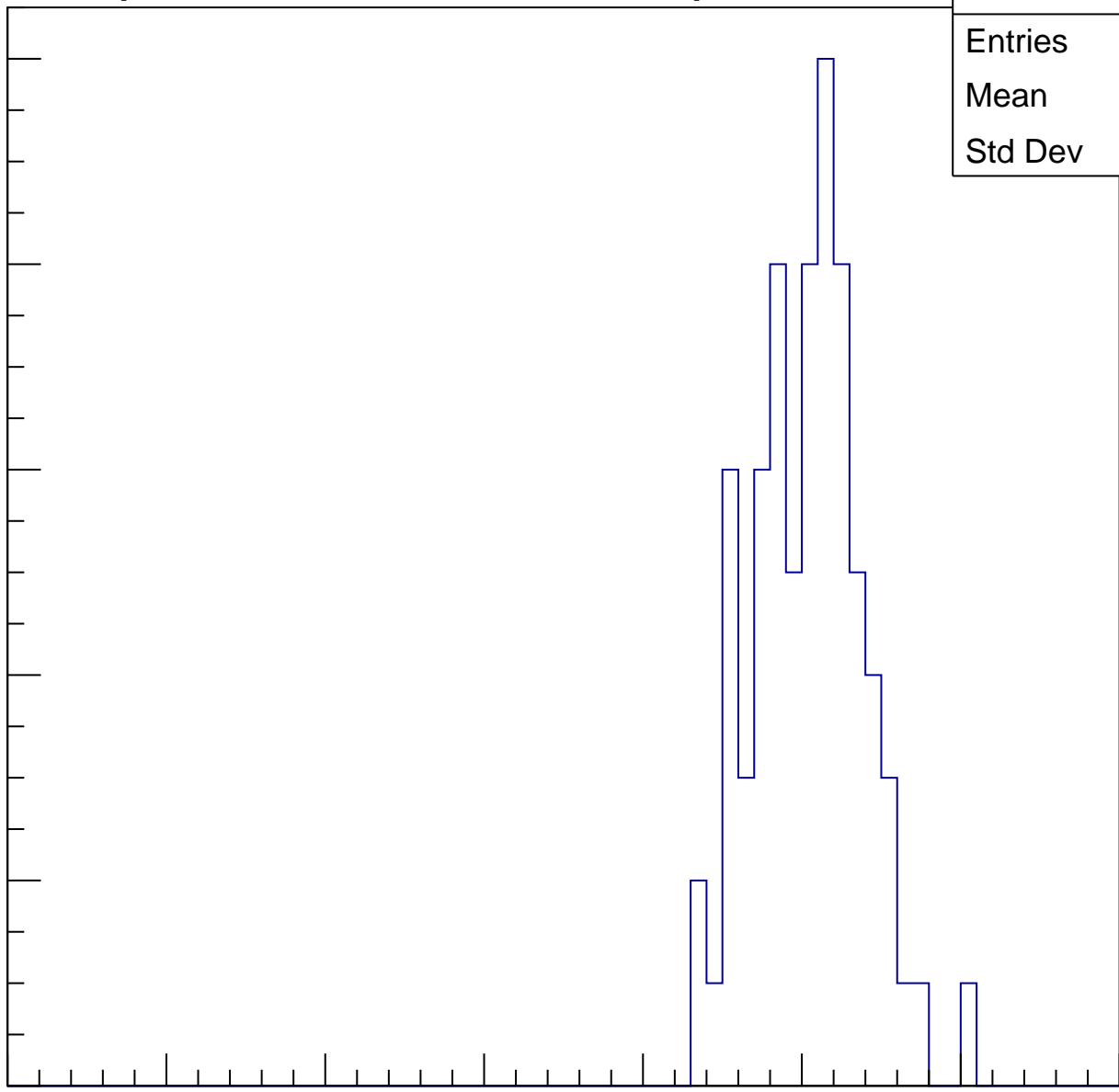
Entries	72
Mean	49.92
Std Dev	3.402

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

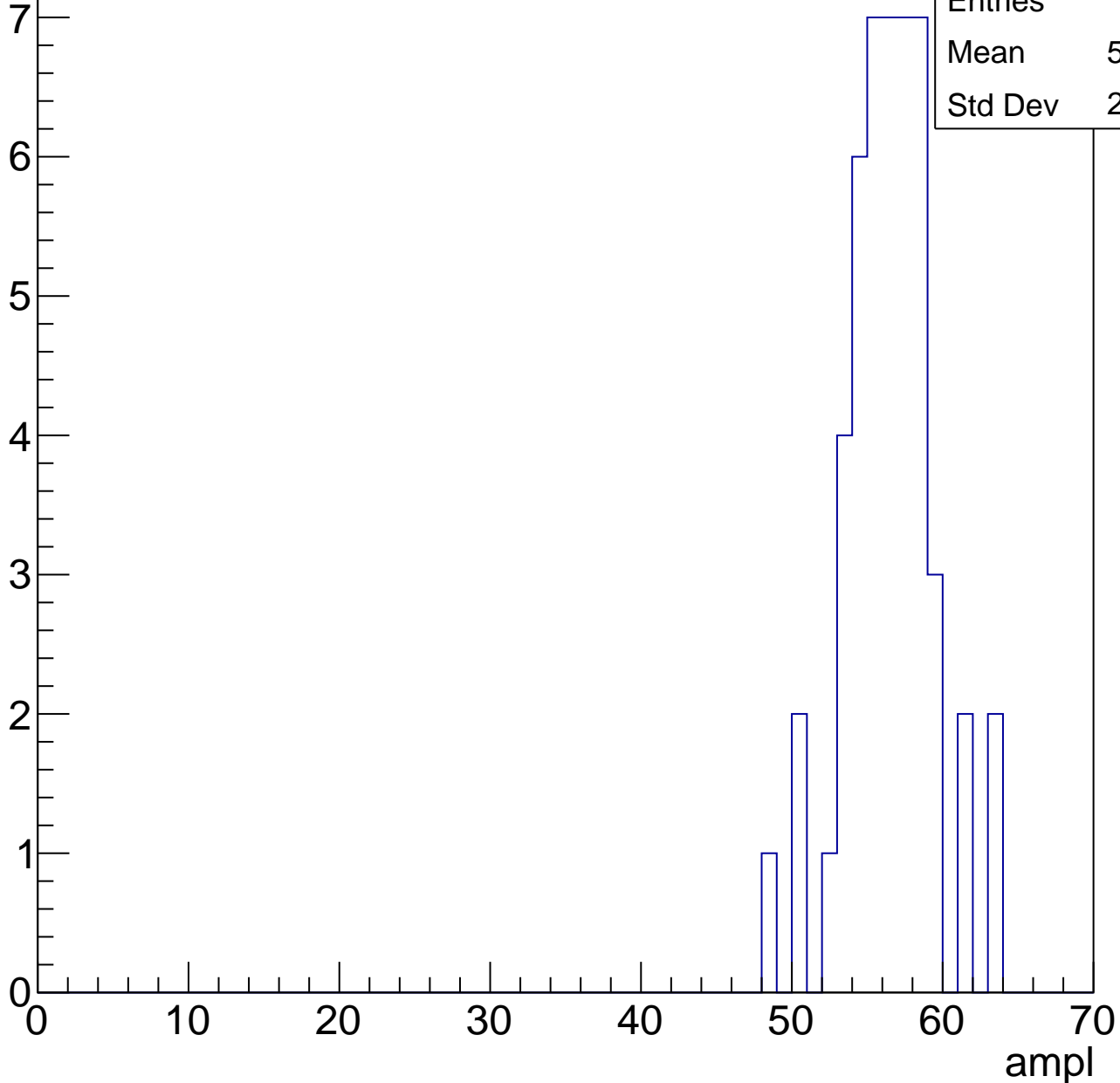


# B0L102S, U6-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	55.98
Std Dev	2.959

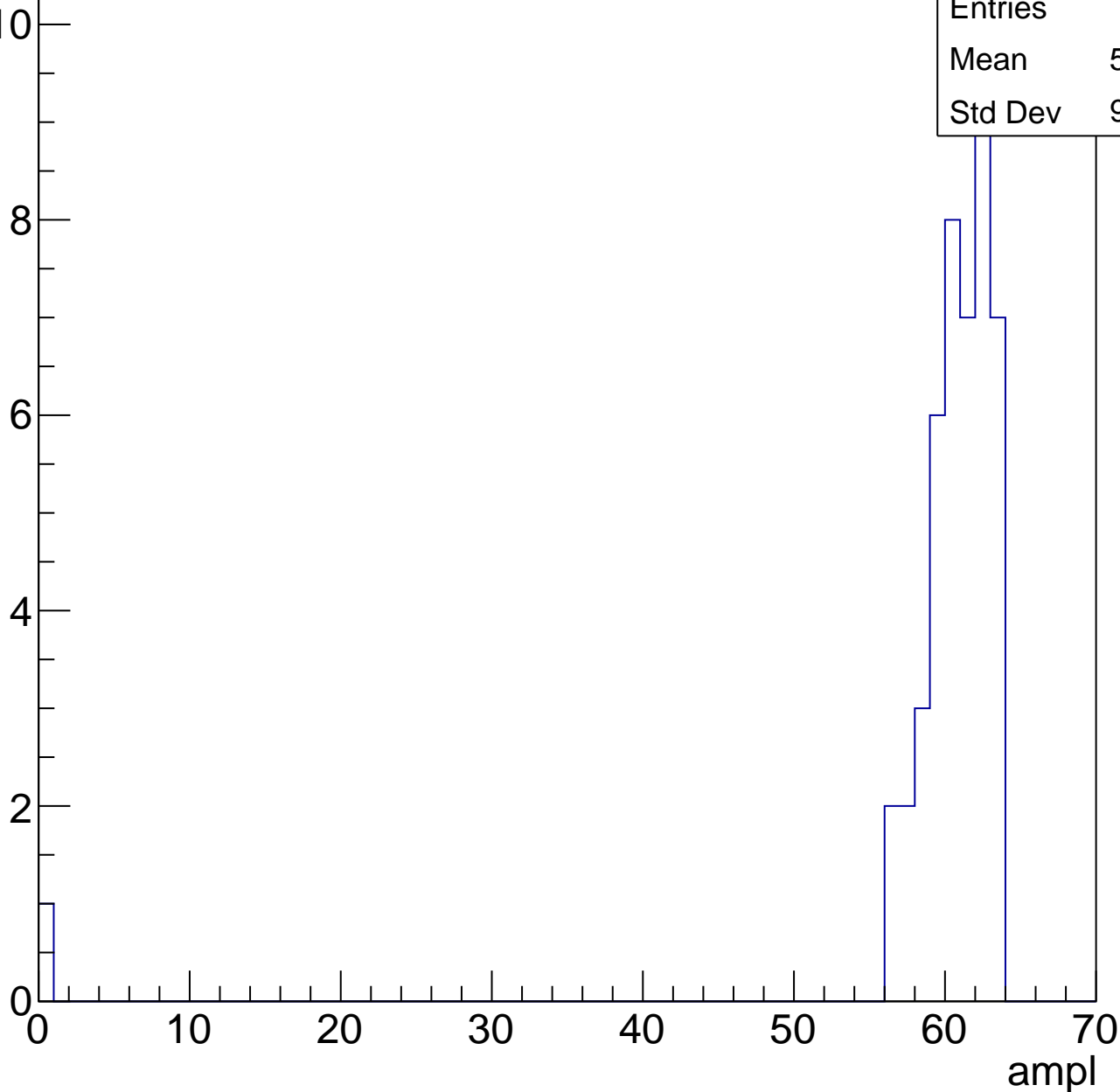


# B0L102S, U6-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

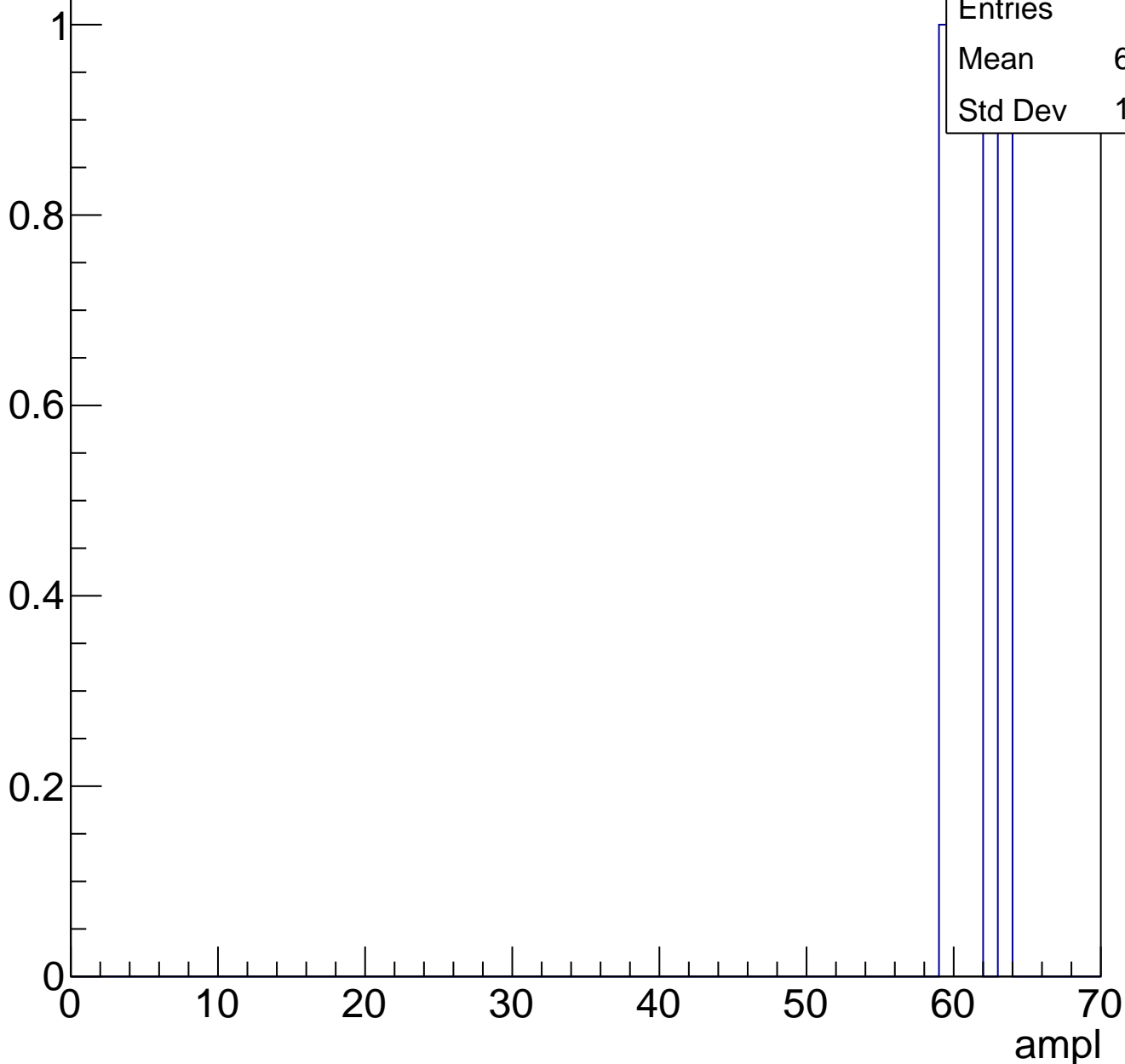
Entries	46
Mean	59.17
Std Dev	9.025



# B0L102S, U6-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch41, adc0

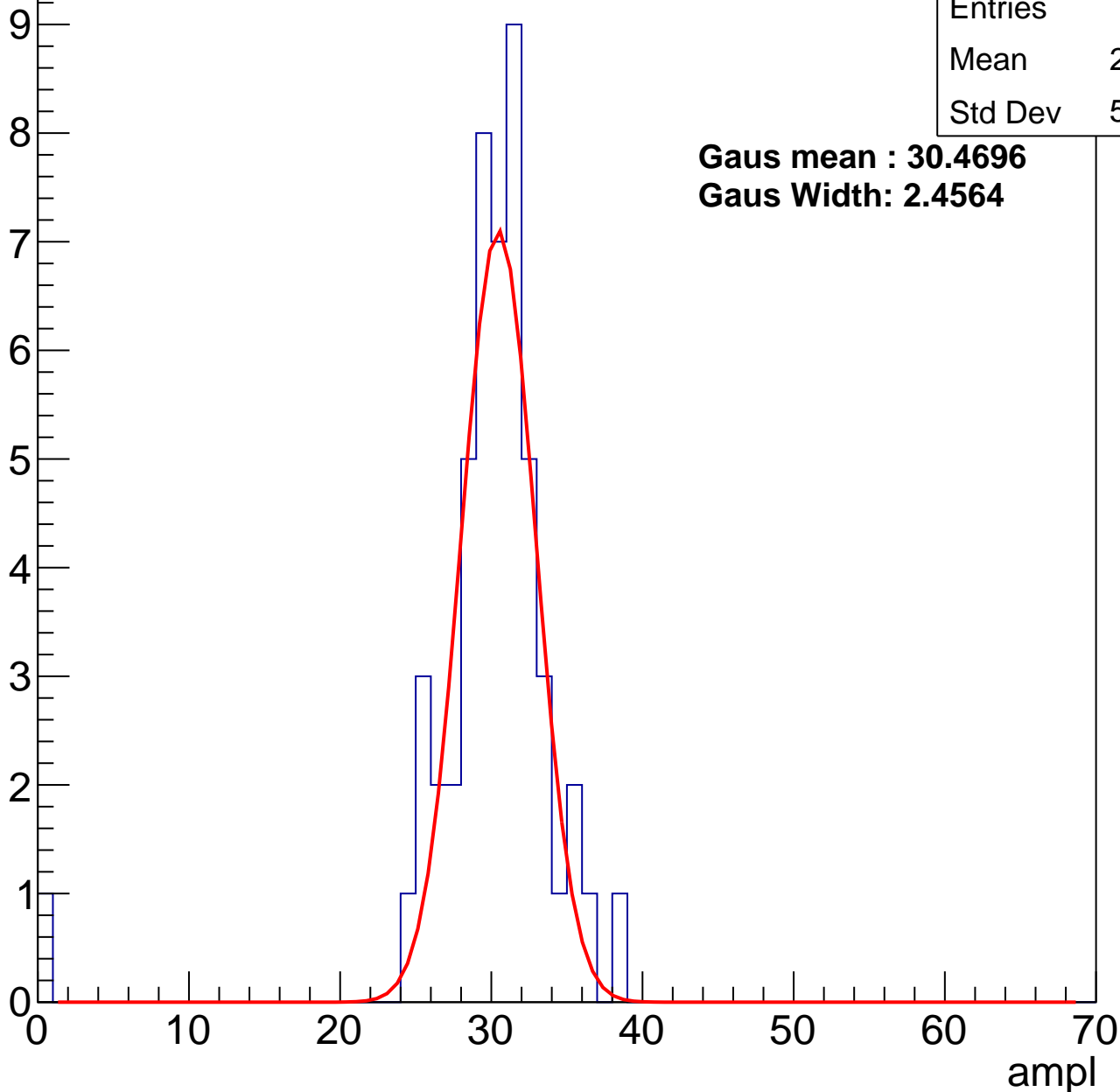
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	29.47
Std Dev	5.042

**Gaus mean : 30.4696**

**Gaus Width: 2.4564**



# B0L102S, U6-ch41, adc1

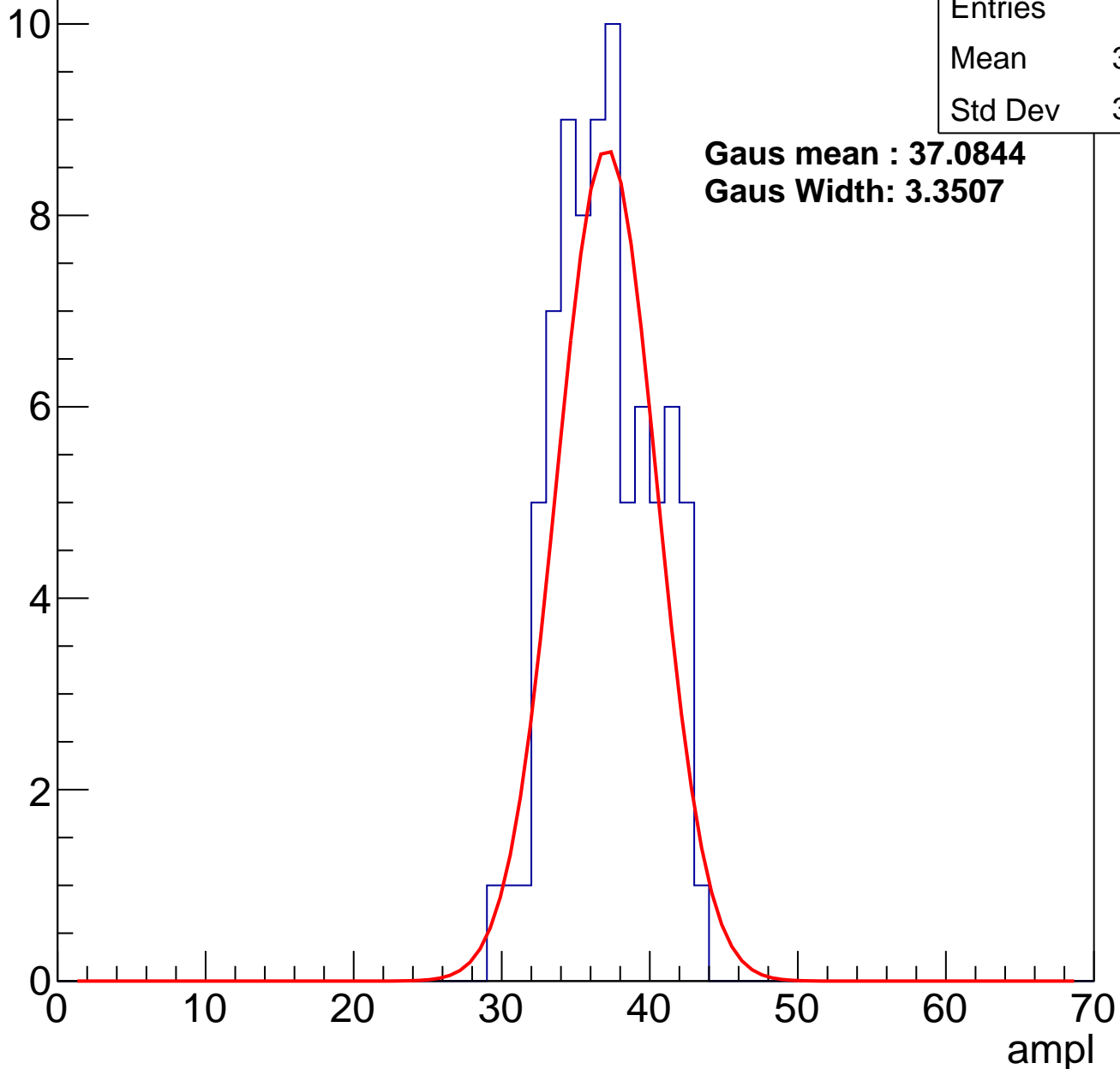
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	79
Mean	36.51
Std Dev	3.221

**Gaus mean : 37.0844**

**Gaus Width: 3.3507**

Entry



# B0L102S, U6-ch41, adc2

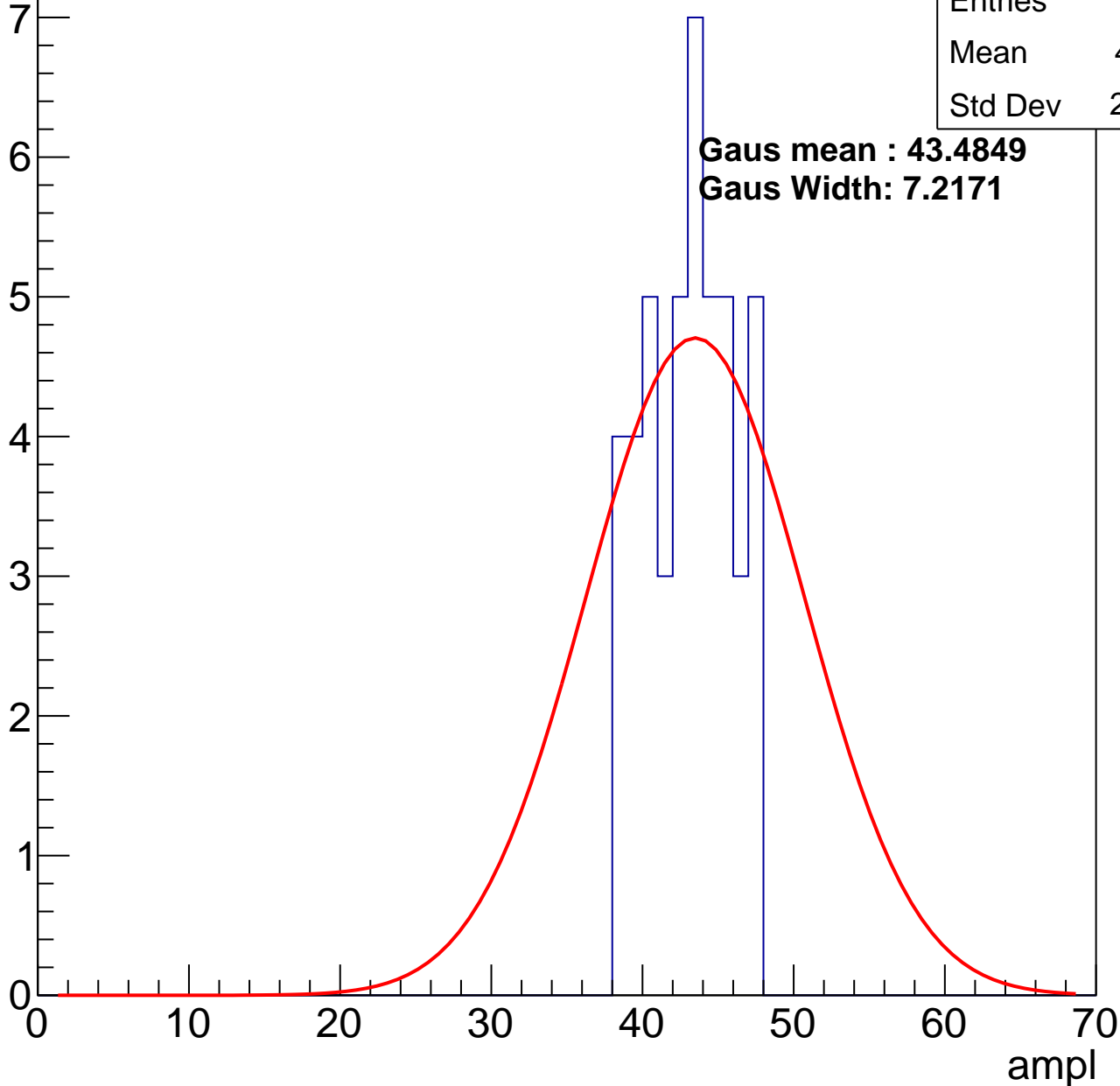
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	42.61
Std Dev	2.762

**Gaus mean : 43.4849**

**Gaus Width: 7.2171**

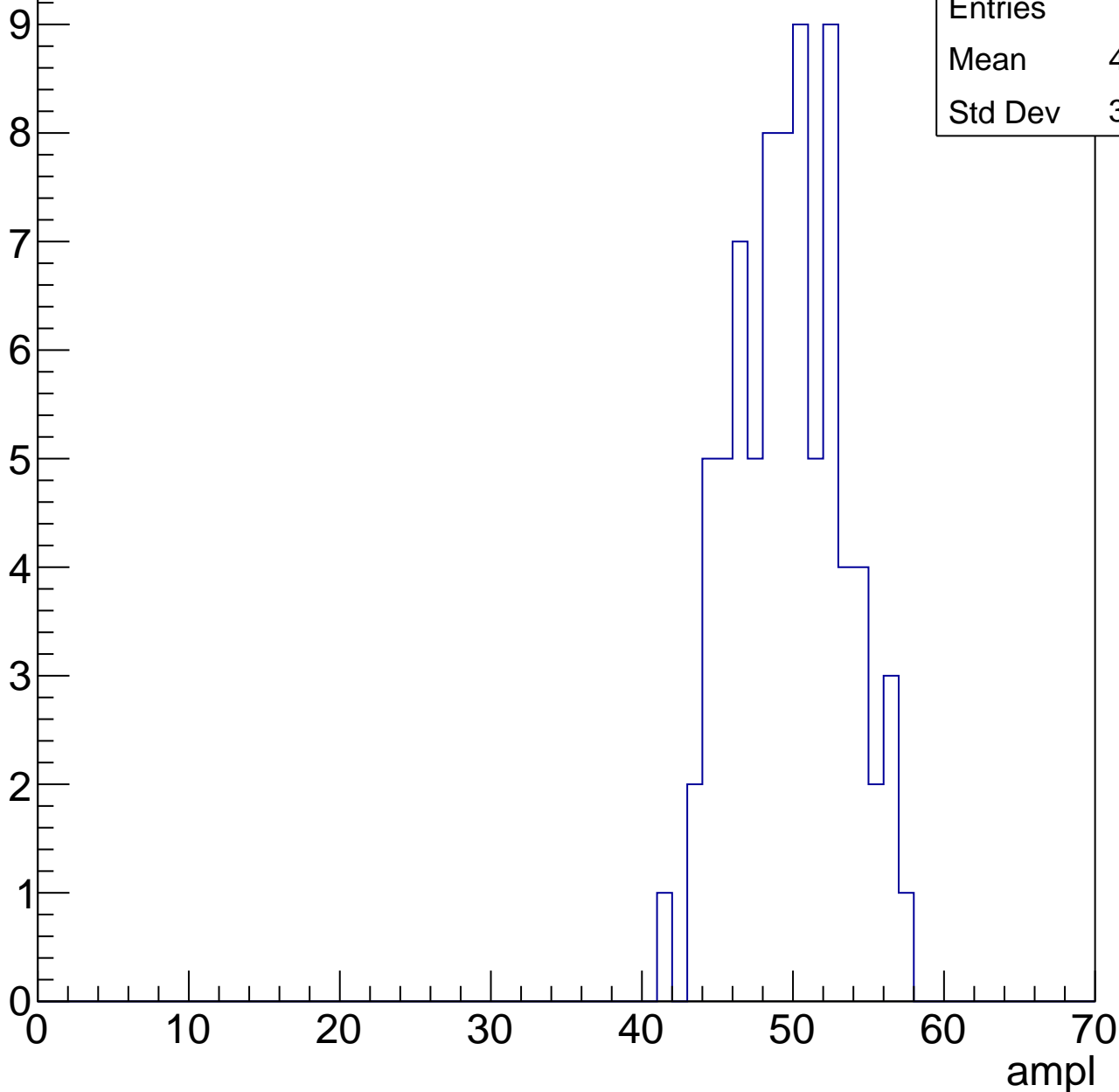


# B0L102S, U6-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	49.24
Std Dev	3.552



# B0L102S, U6-ch41, adc4

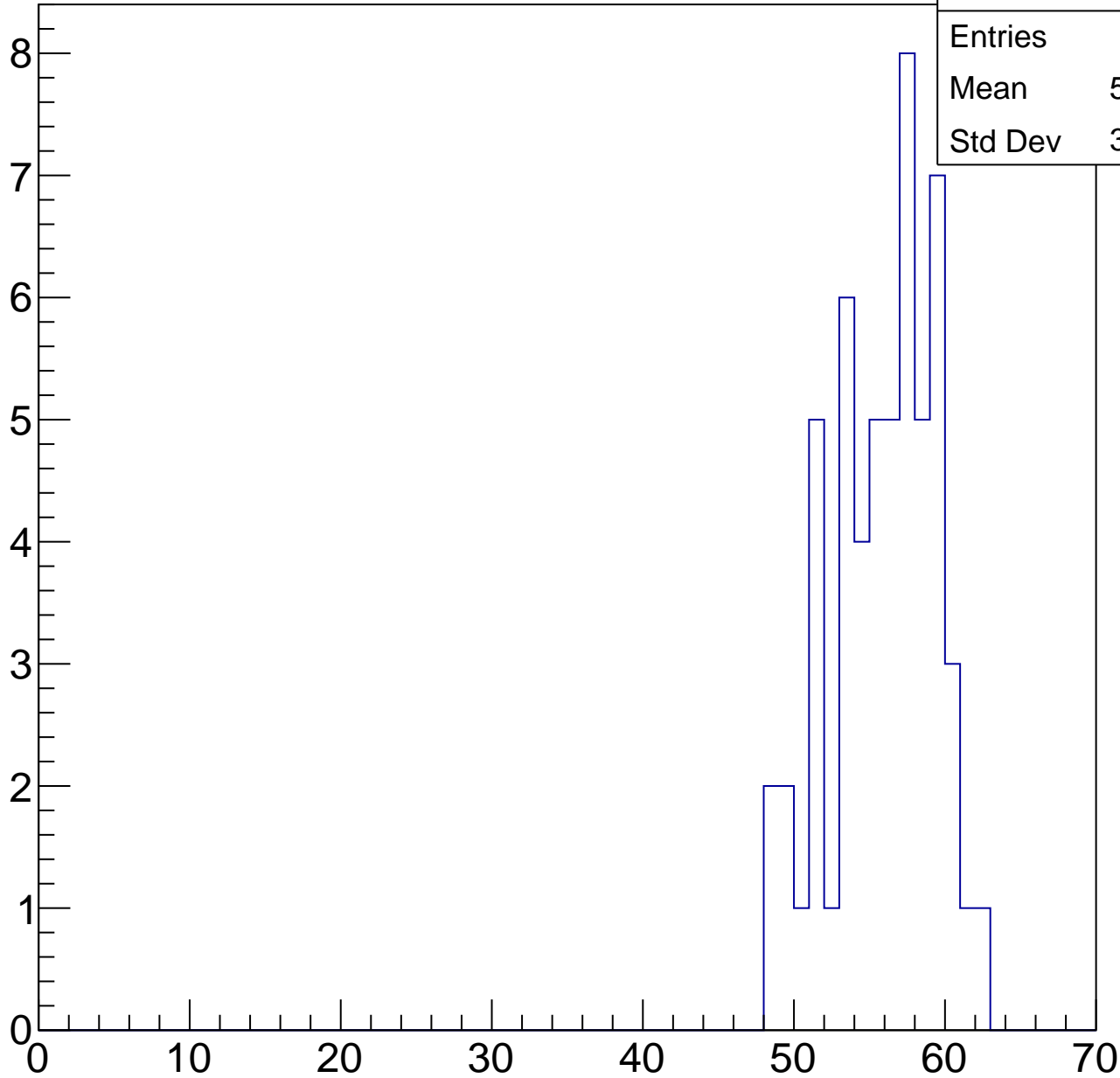
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	55.39
Std Dev	3.426

ampl



# B0L102S, U6-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

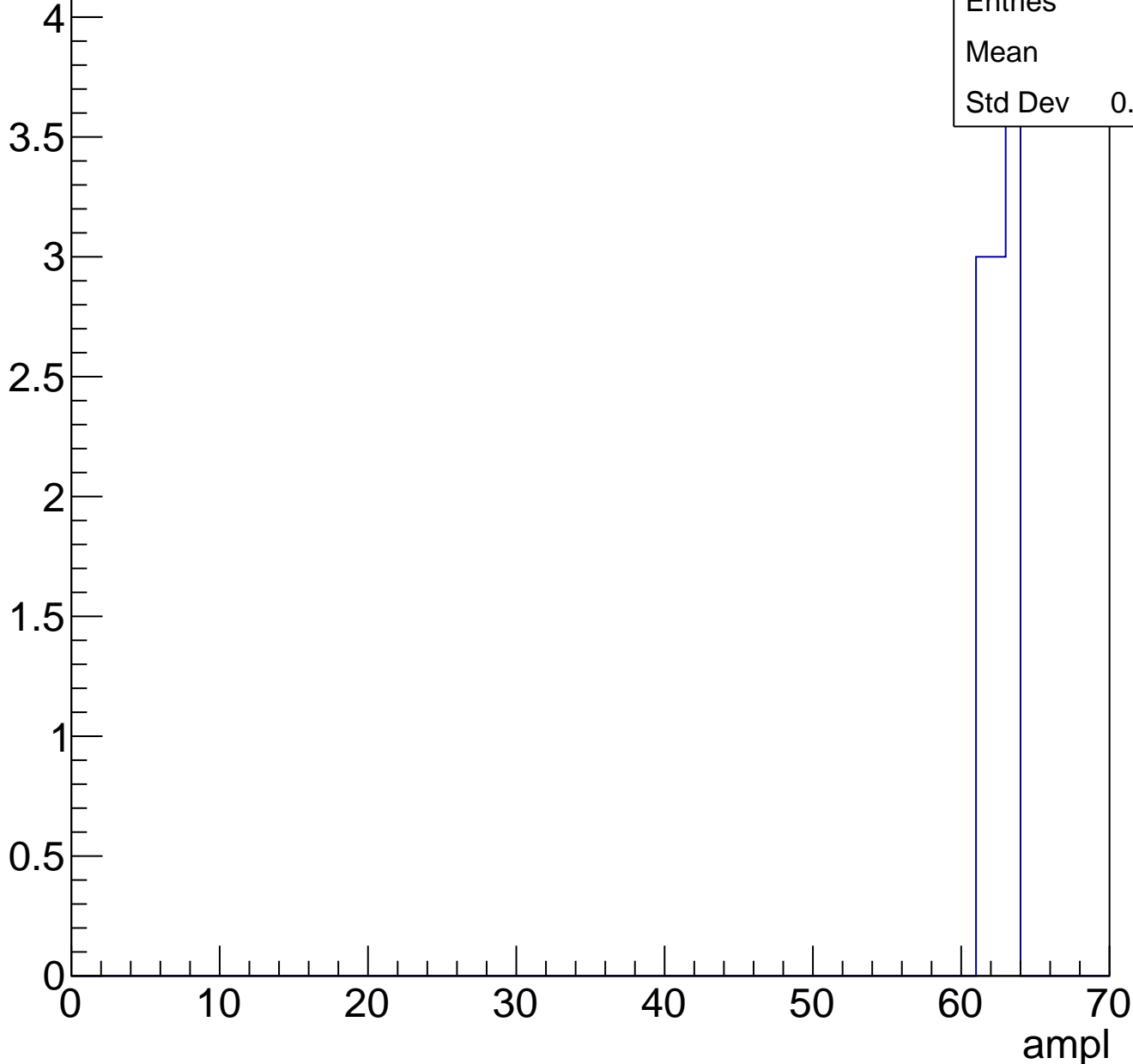
Entries	42
Mean	58.21
Std Dev	9.463

ampl

# B0L102S, U6-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

# B0L102S, U6-ch42, adc0

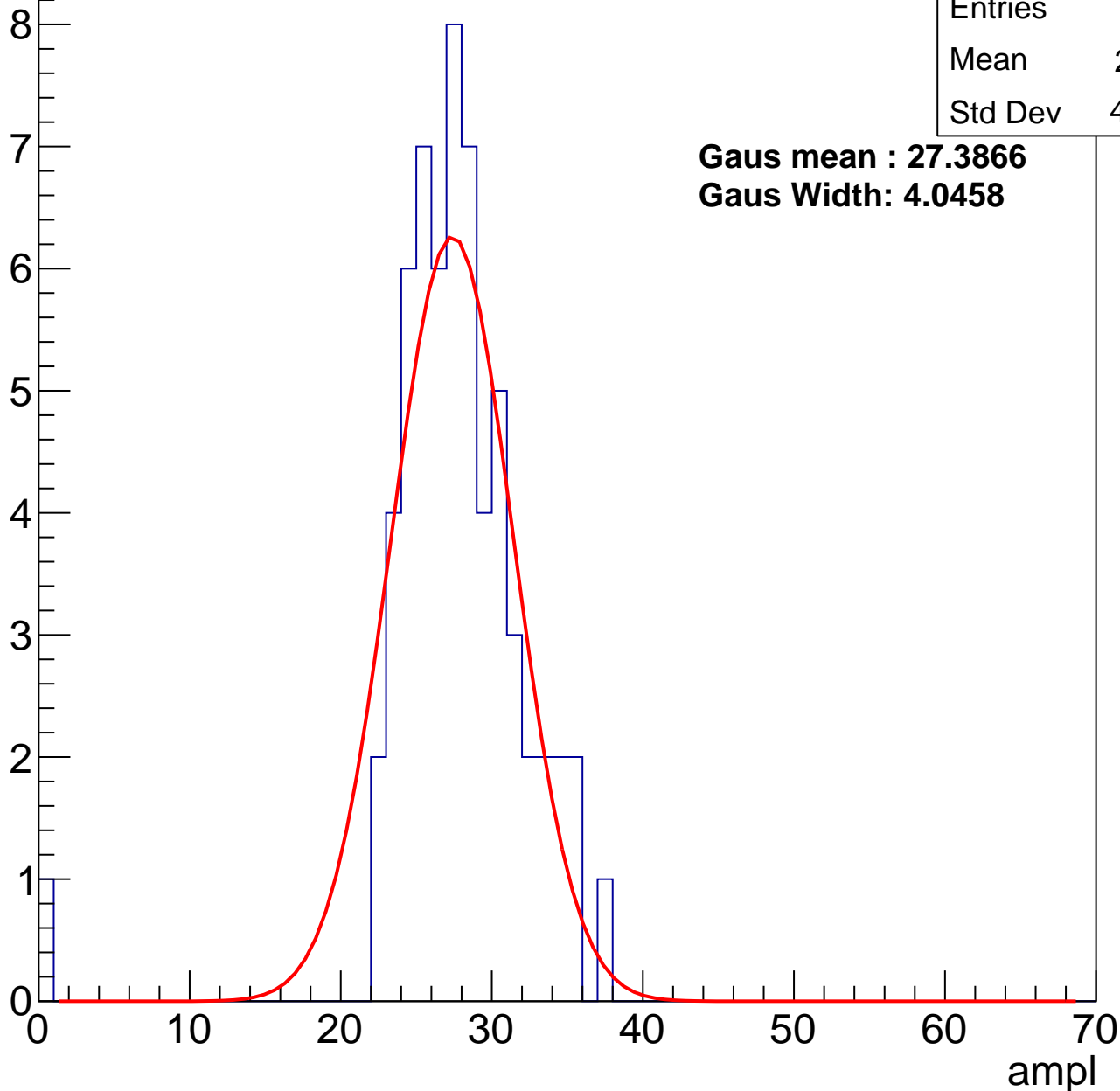
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	27.21
Std Dev	4.906

**Gaus mean : 27.3866**

**Gaus Width: 4.0458**



# B0L102S, U6-ch42, adc1

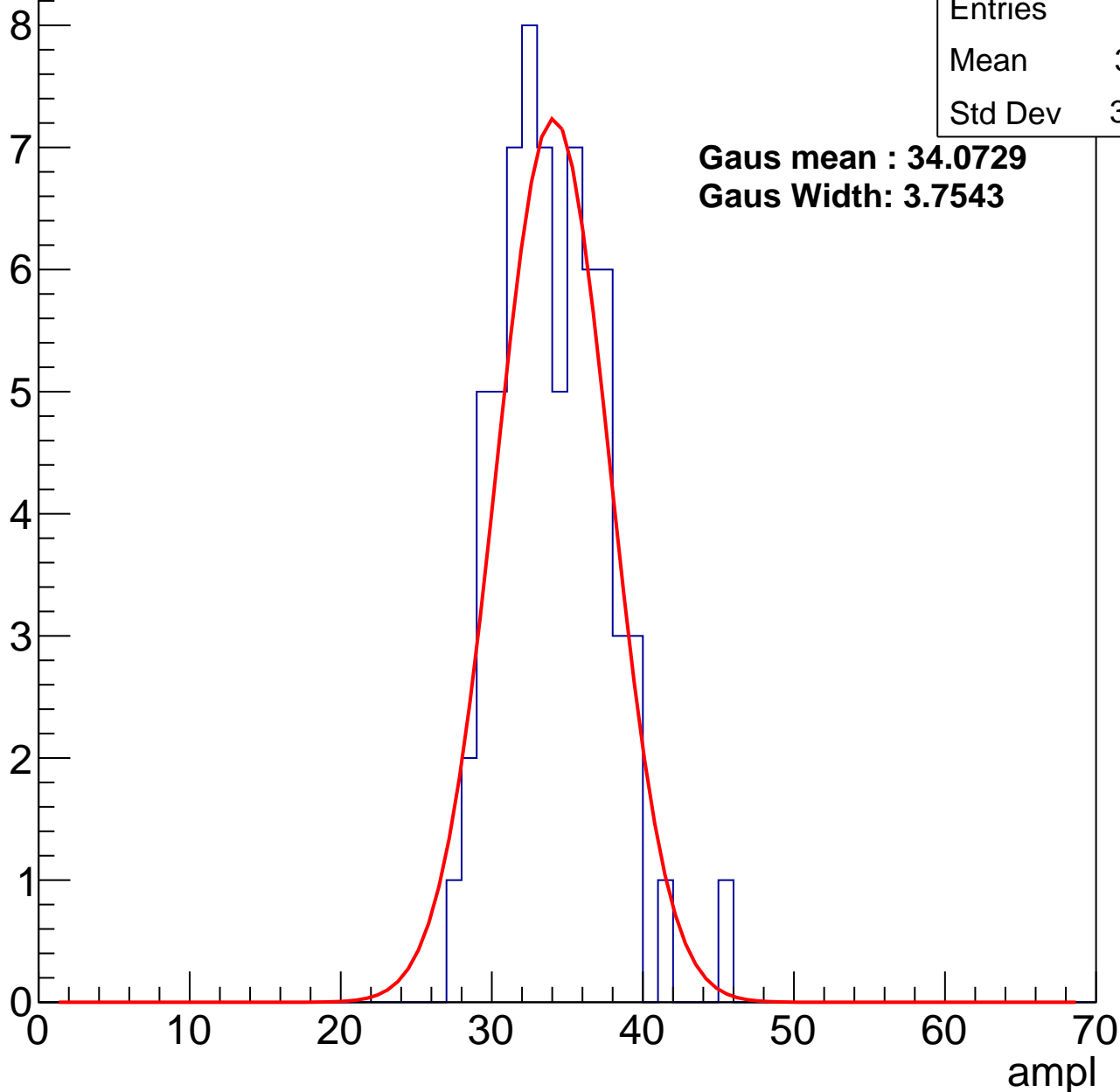
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	33.61
Std Dev	3.447

**Gaus mean : 34.0729**

**Gaus Width: 3.7543**



# B0L102S, U6-ch42, adc2

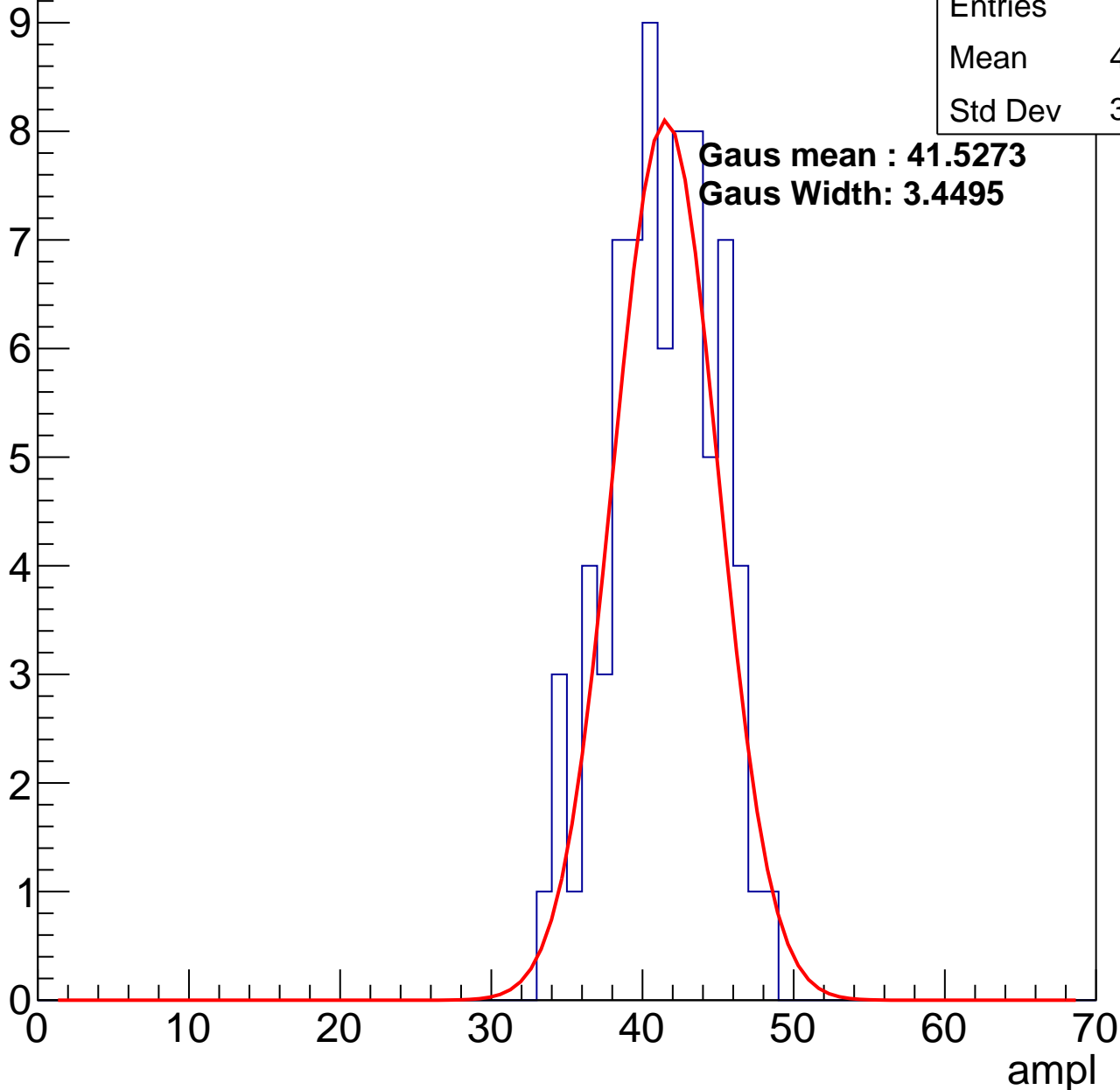
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	40.85
Std Dev	3.416

**Gaus mean : 41.5273**

**Gaus Width: 3.4495**

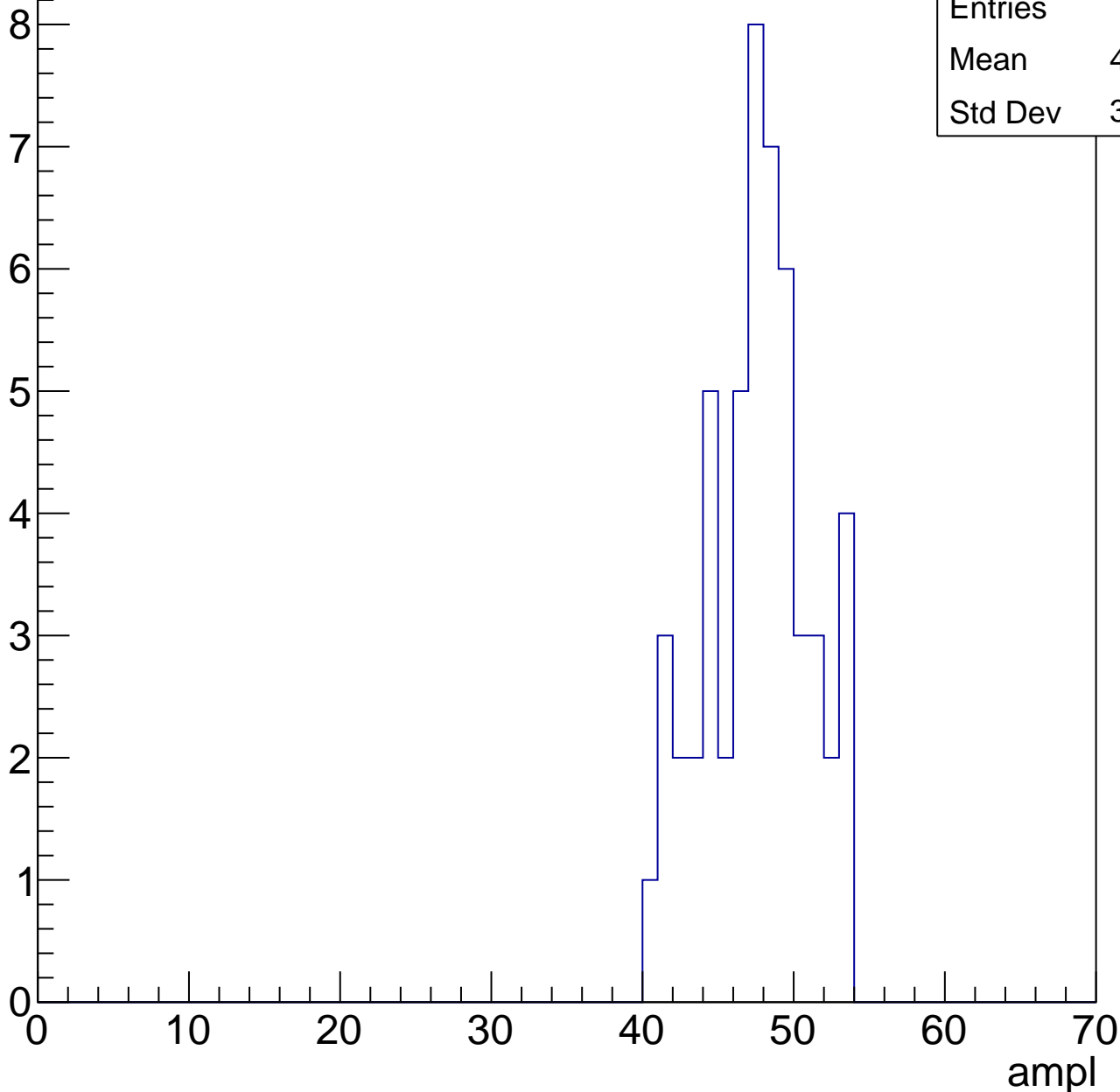


# B0L102S, U6-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	47.13
Std Dev	3.354

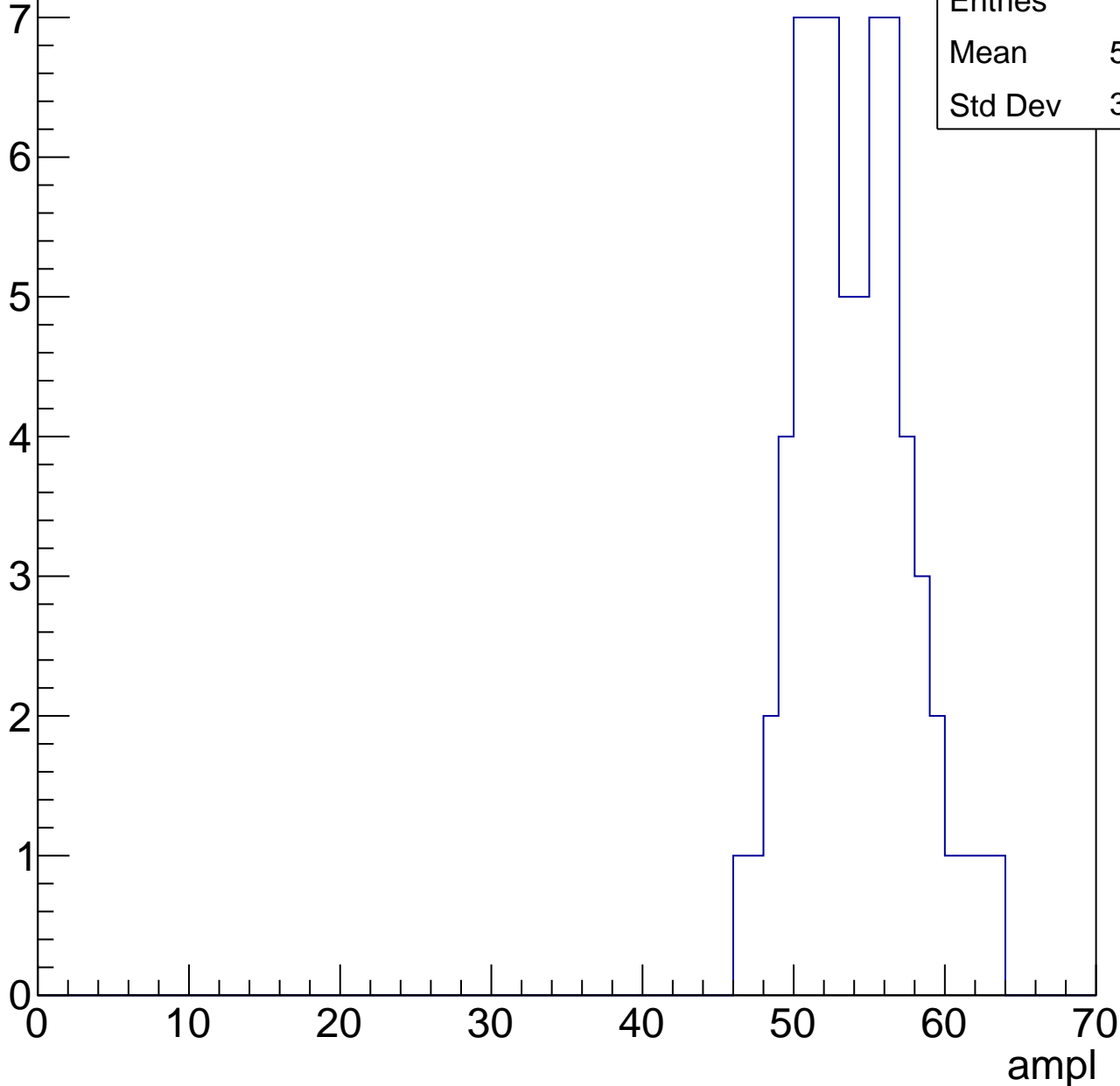


# B0L102S, U6-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

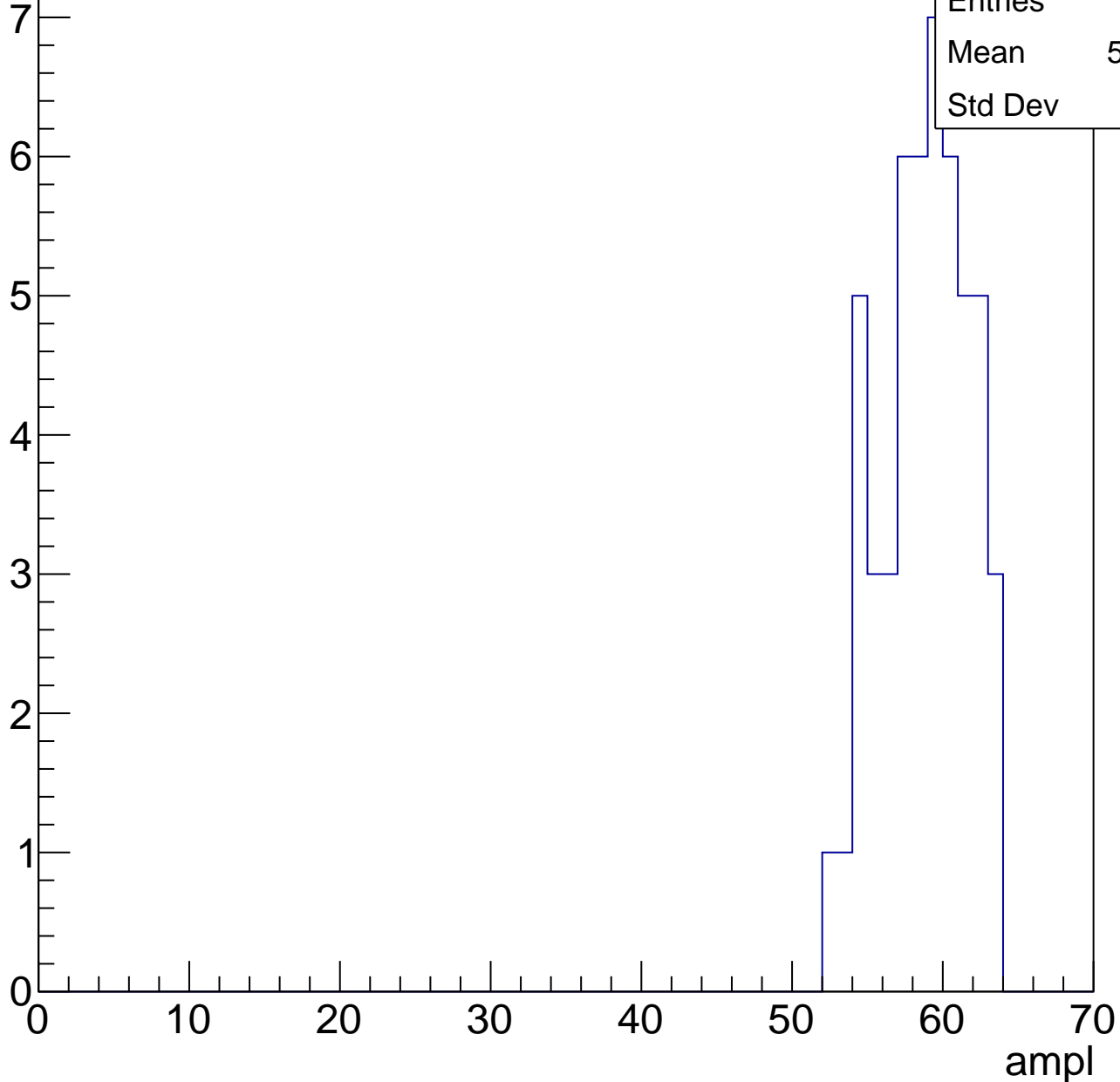
Entries	66
Mean	53.55
Std Dev	3.648



# B0L102S, U6-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

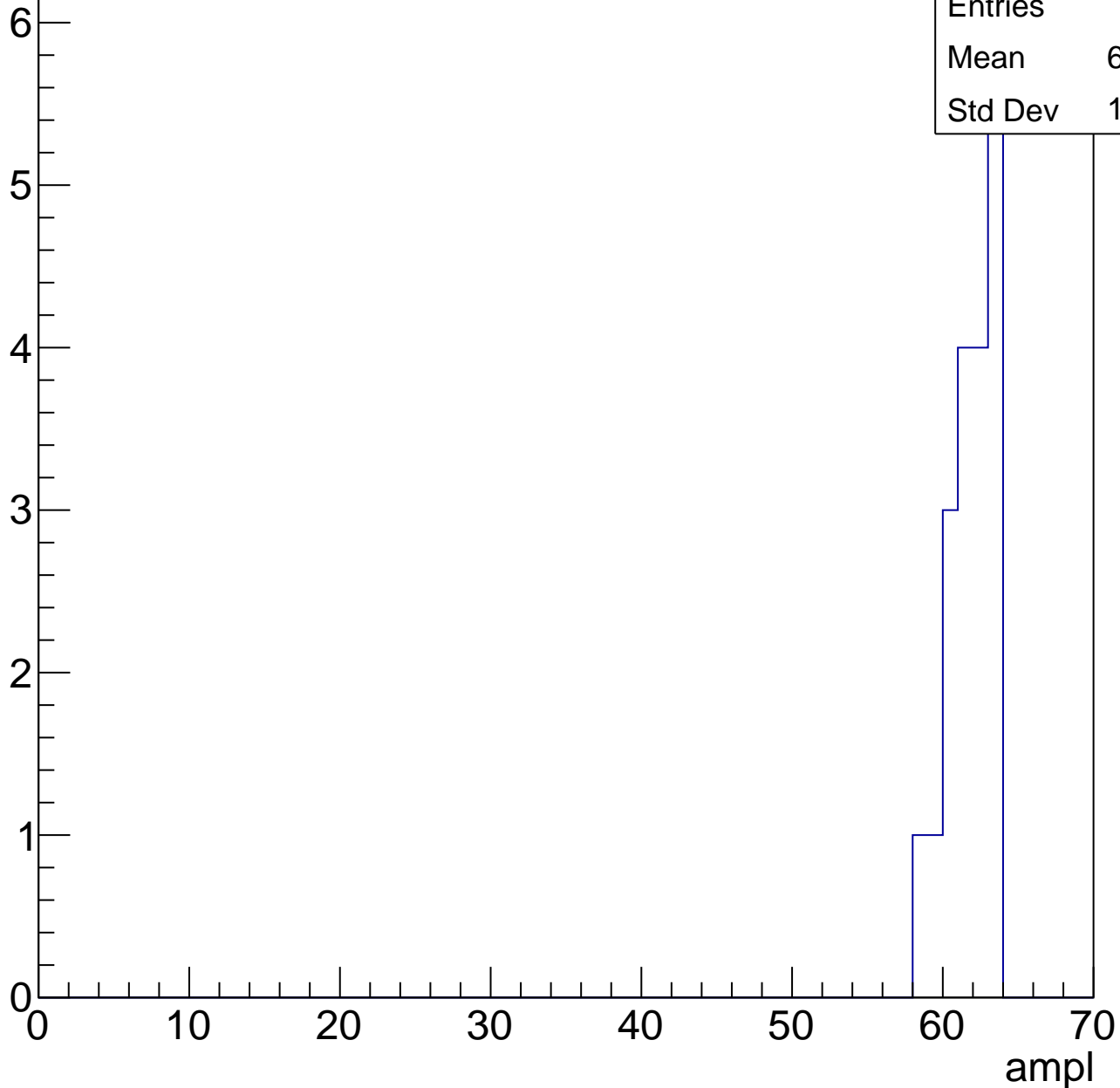


# B0L102S, U6-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	19
Mean	61.42
Std Dev	1.462





# B0L102S, U6-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L102S, U6-ch43, adc0

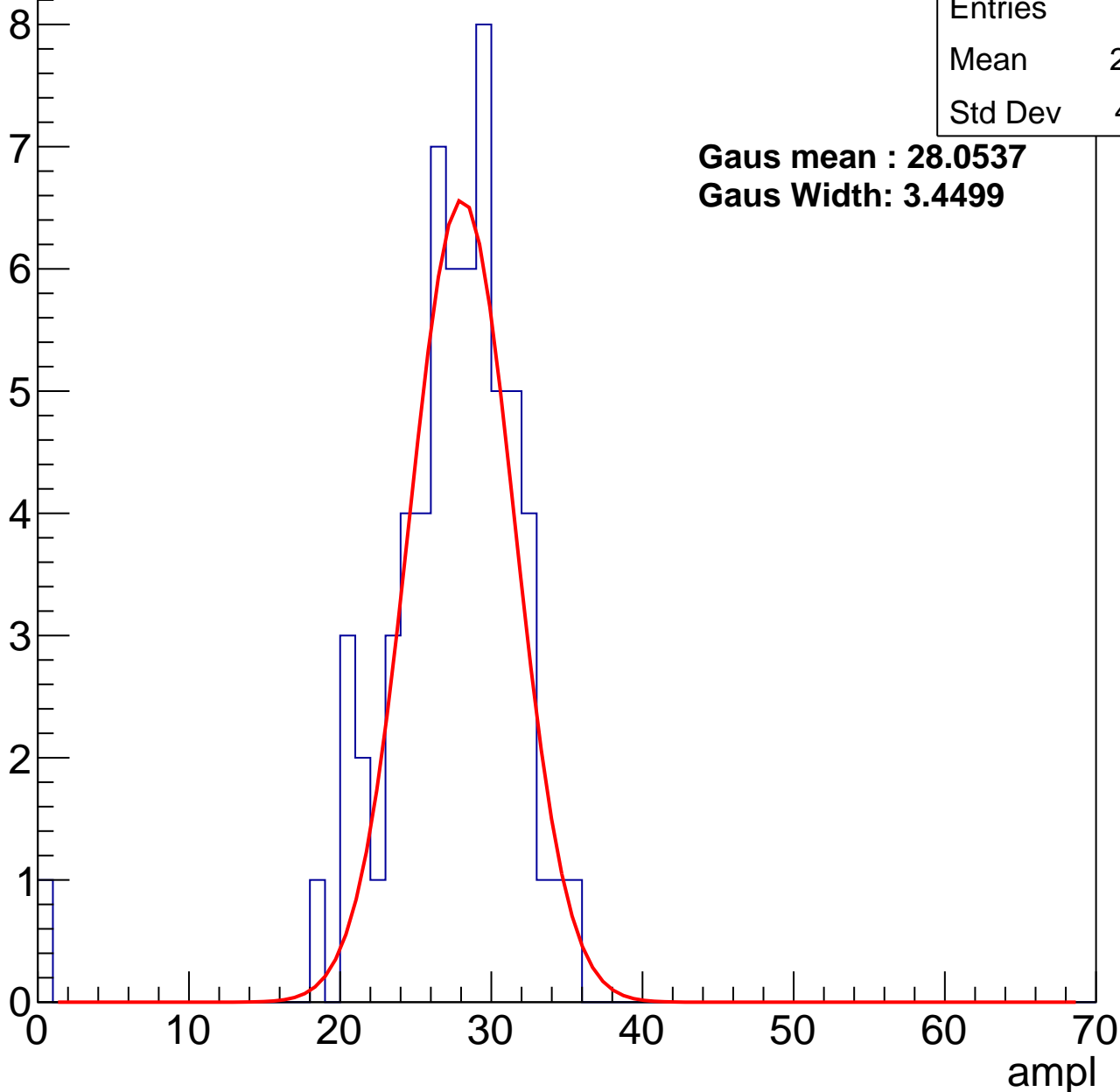
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	26.76
Std Dev	4.991

**Gaus mean : 28.0537**

**Gaus Width: 3.4499**



# B0L102S, U6-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	75
Mean	34.25
Std Dev	3.655

**Gaus mean : 35.1443**

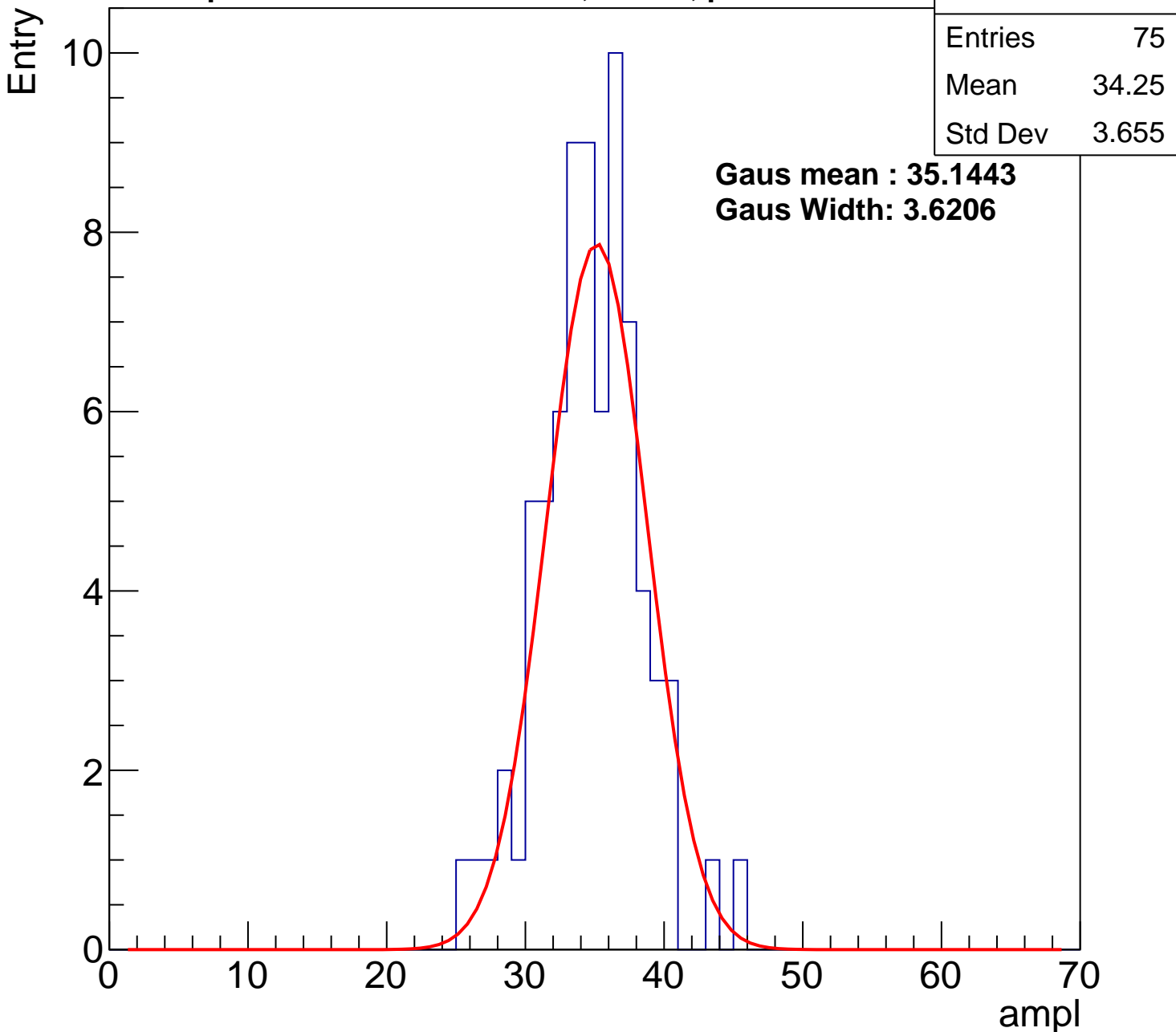
**Gaus Width: 3.6206**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch43, adc2

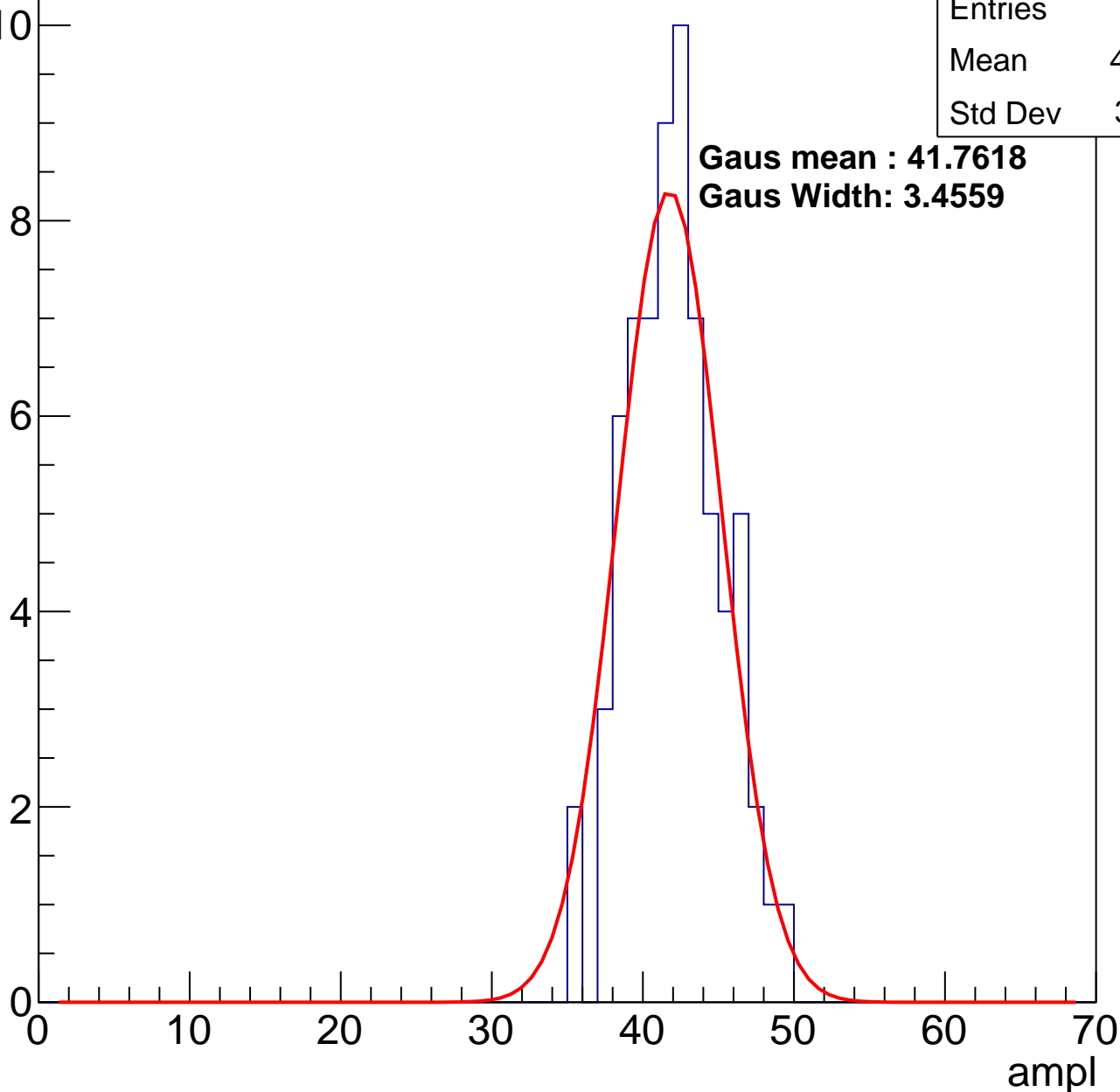
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	41.64
Std Dev	3.041

**Gaus mean : 41.7618**

**Gaus Width: 3.4559**

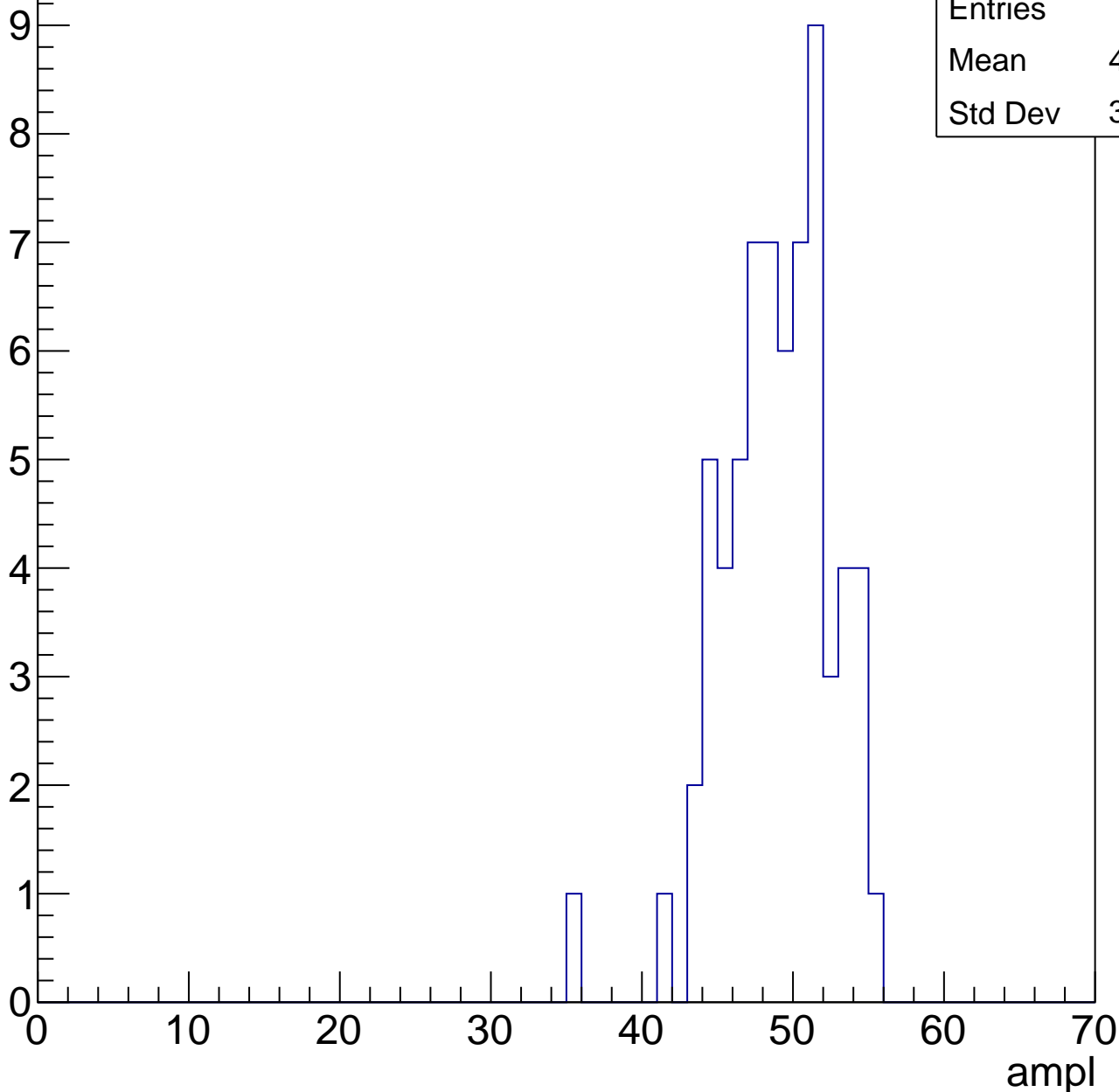


# B0L102S, U6-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	48.47
Std Dev	3.594

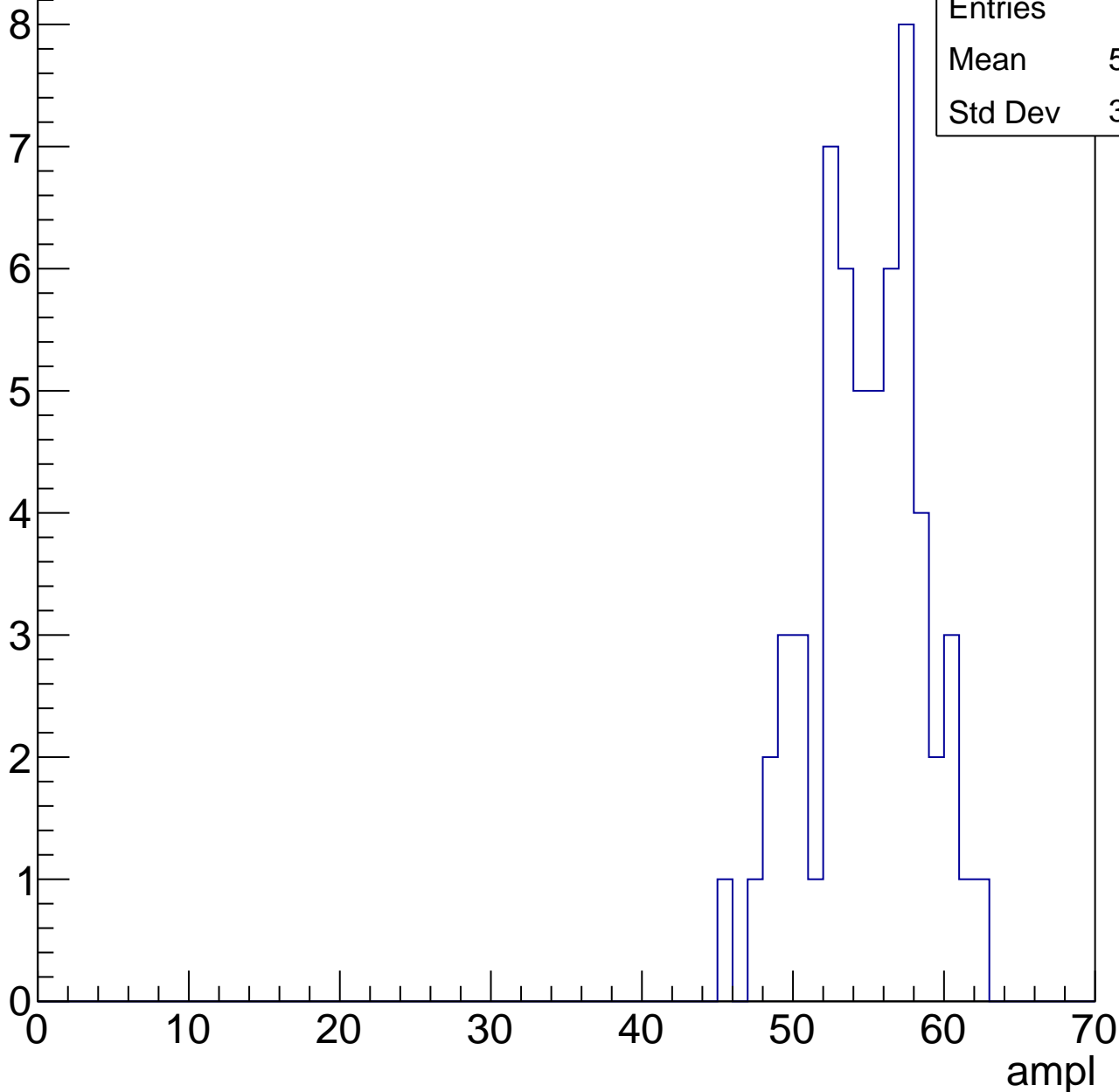


# B0L102S, U6-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	54.37
Std Dev	3.673

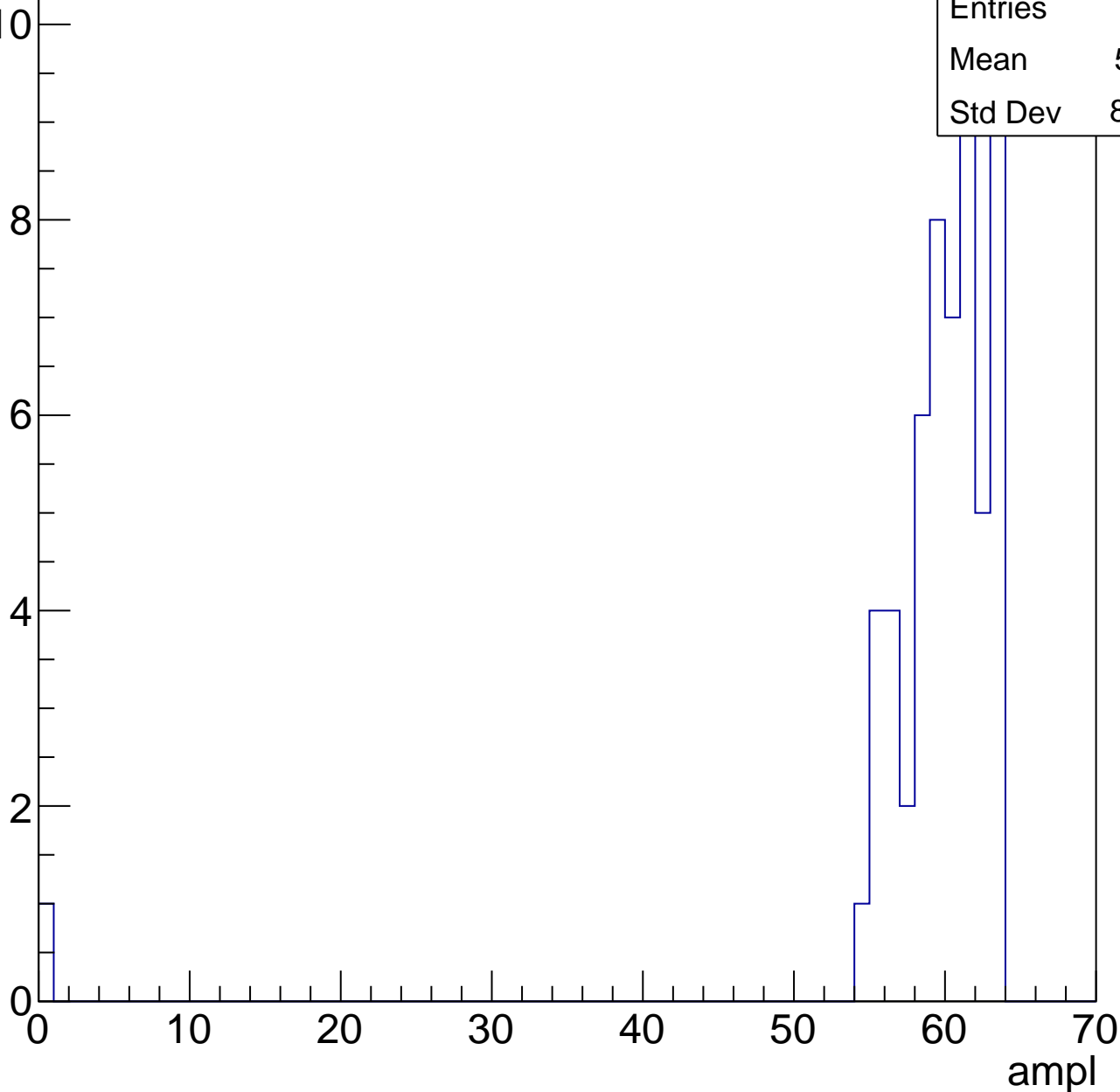


# B0L102S, U6-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

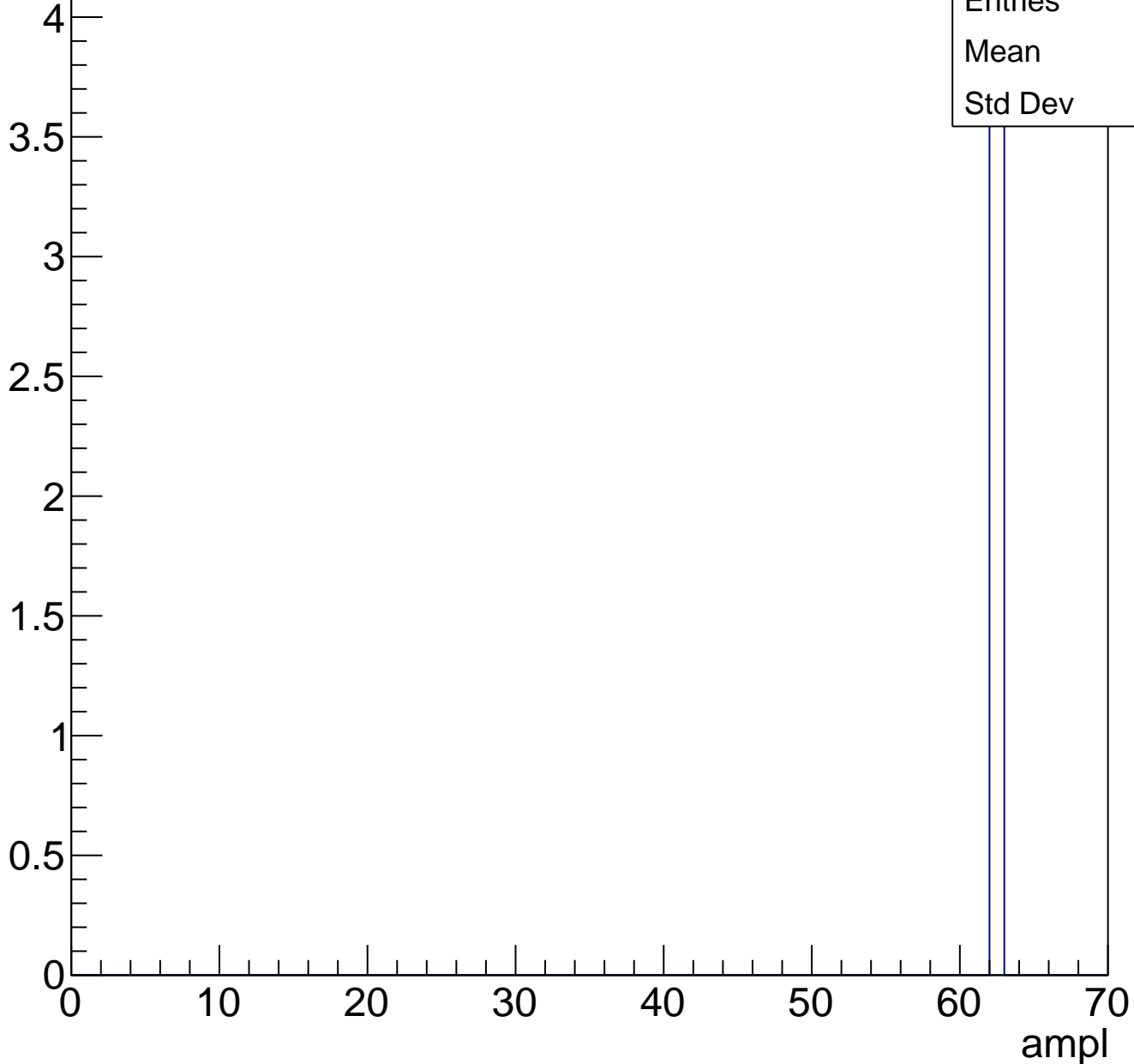
Entries	57
Mean	58.61
Std Dev	8.229



# B0L102S, U6-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch44, adc0

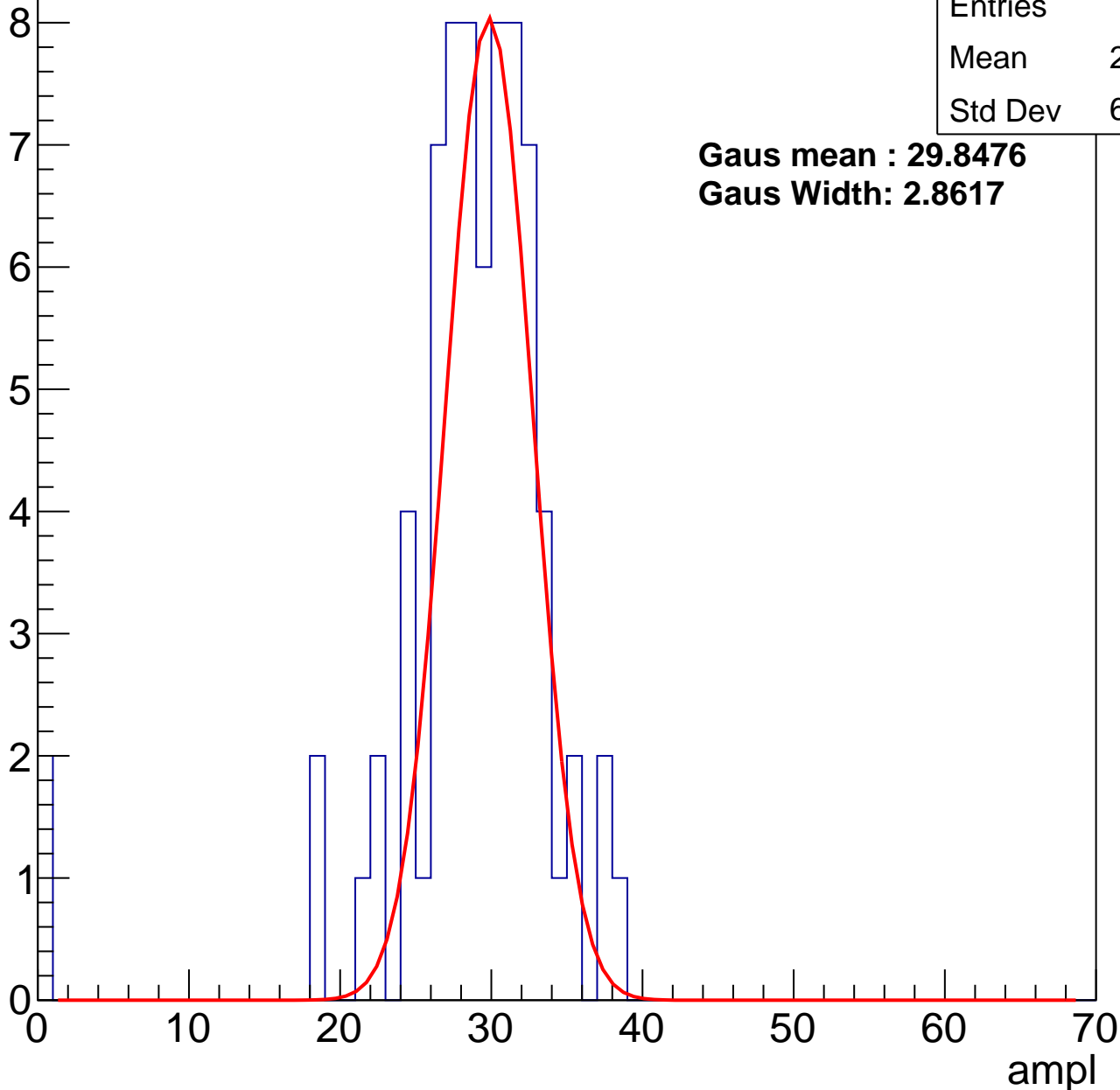
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	28.08
Std Dev	6.058

**Gaus mean : 29.8476**

**Gaus Width: 2.8617**



# B0L102S, U6-ch44, adc1

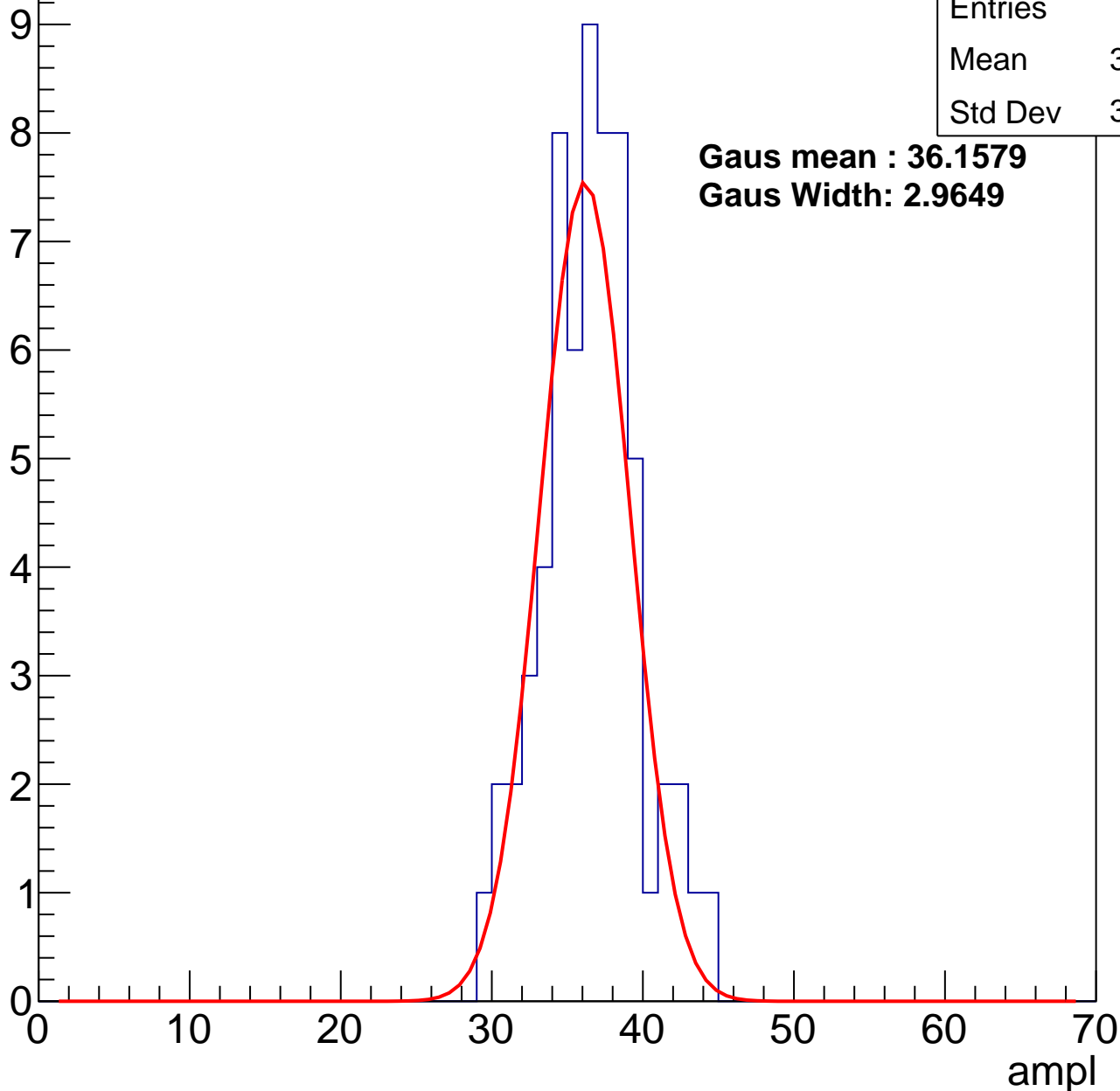
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	36.08
Std Dev	3.159

**Gaus mean : 36.1579**

**Gaus Width: 2.9649**



# B0L102S, U6-ch44, adc2

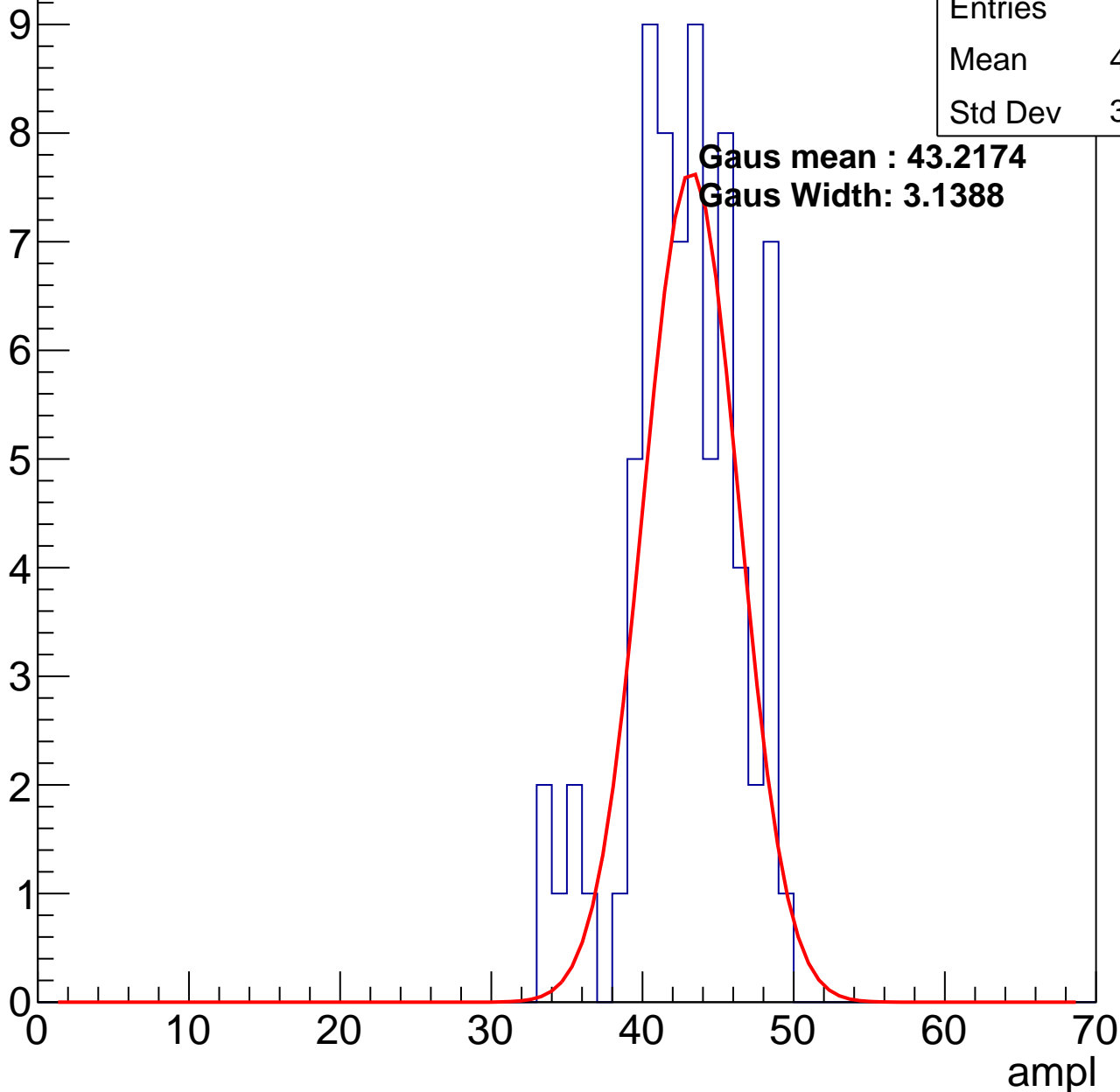
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	42.38
Std Dev	3.672

**Gaus mean : 43.2174**

**Gaus Width: 3.1388**

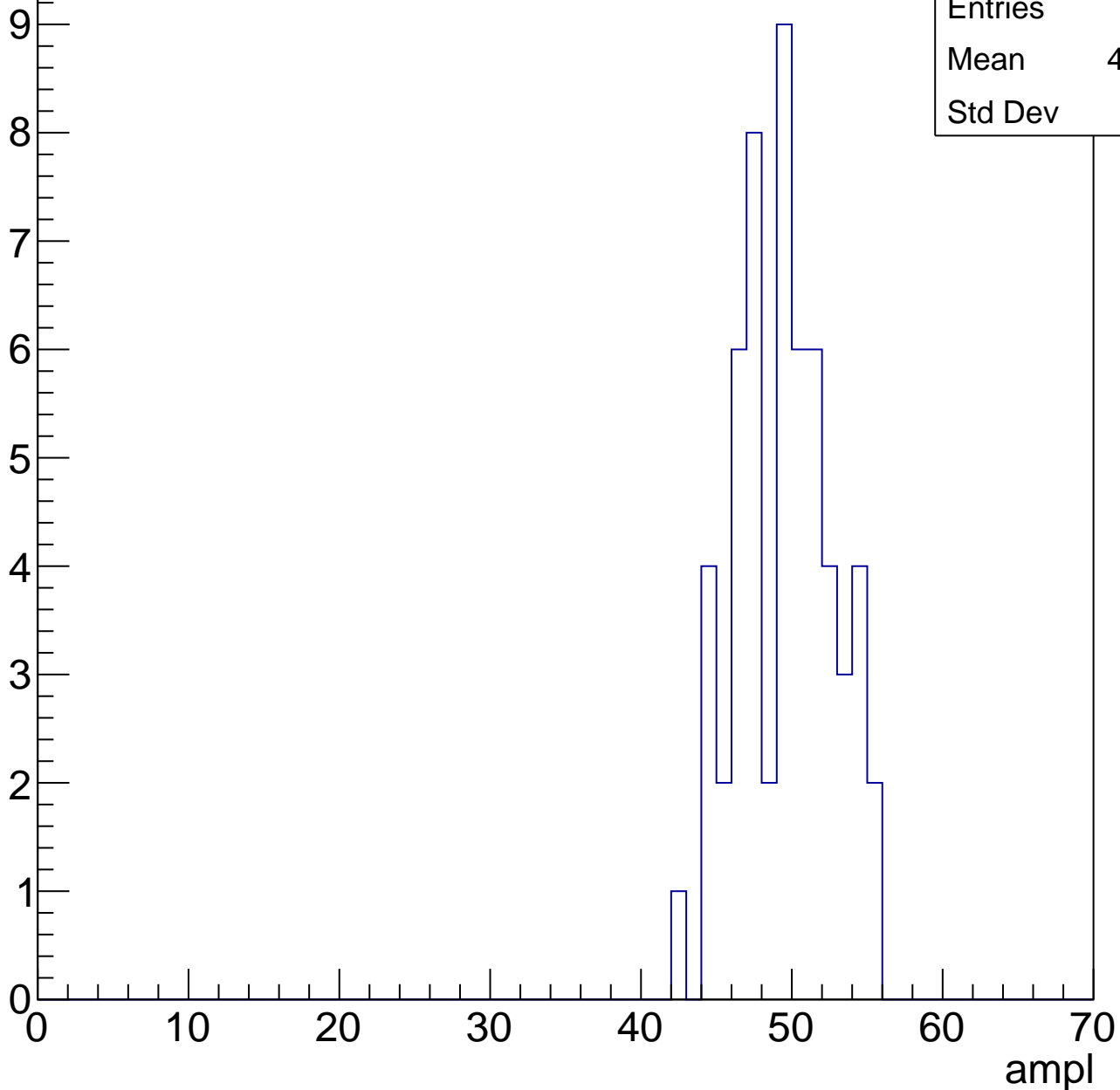


# B0L102S, U6-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	49.05
Std Dev	3.12

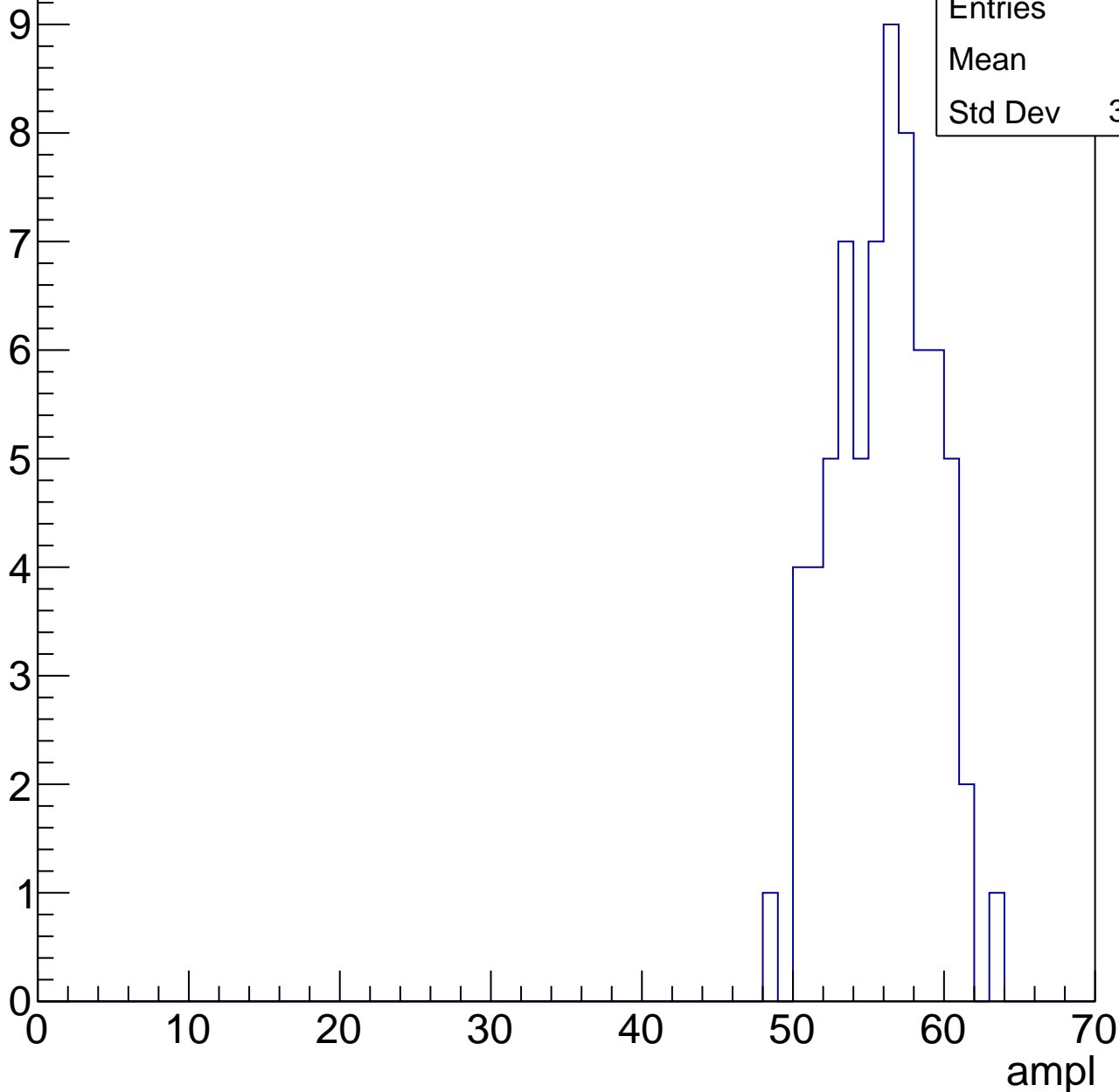


# B0L102S, U6-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	55.5
Std Dev	3.219

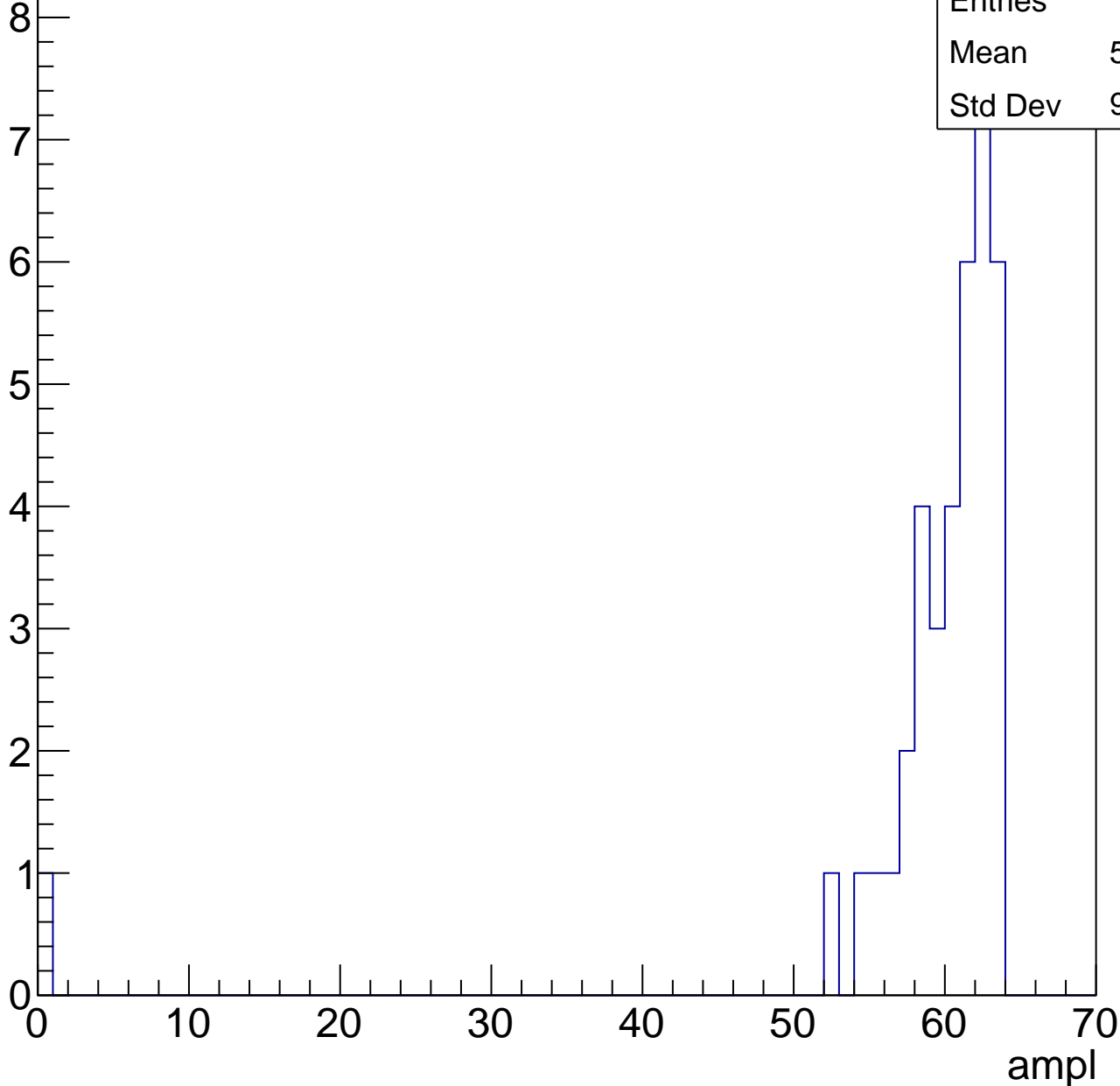


# B0L102S, U6-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	38
Mean	58.42
Std Dev	9.967



# B0L102S, U6-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

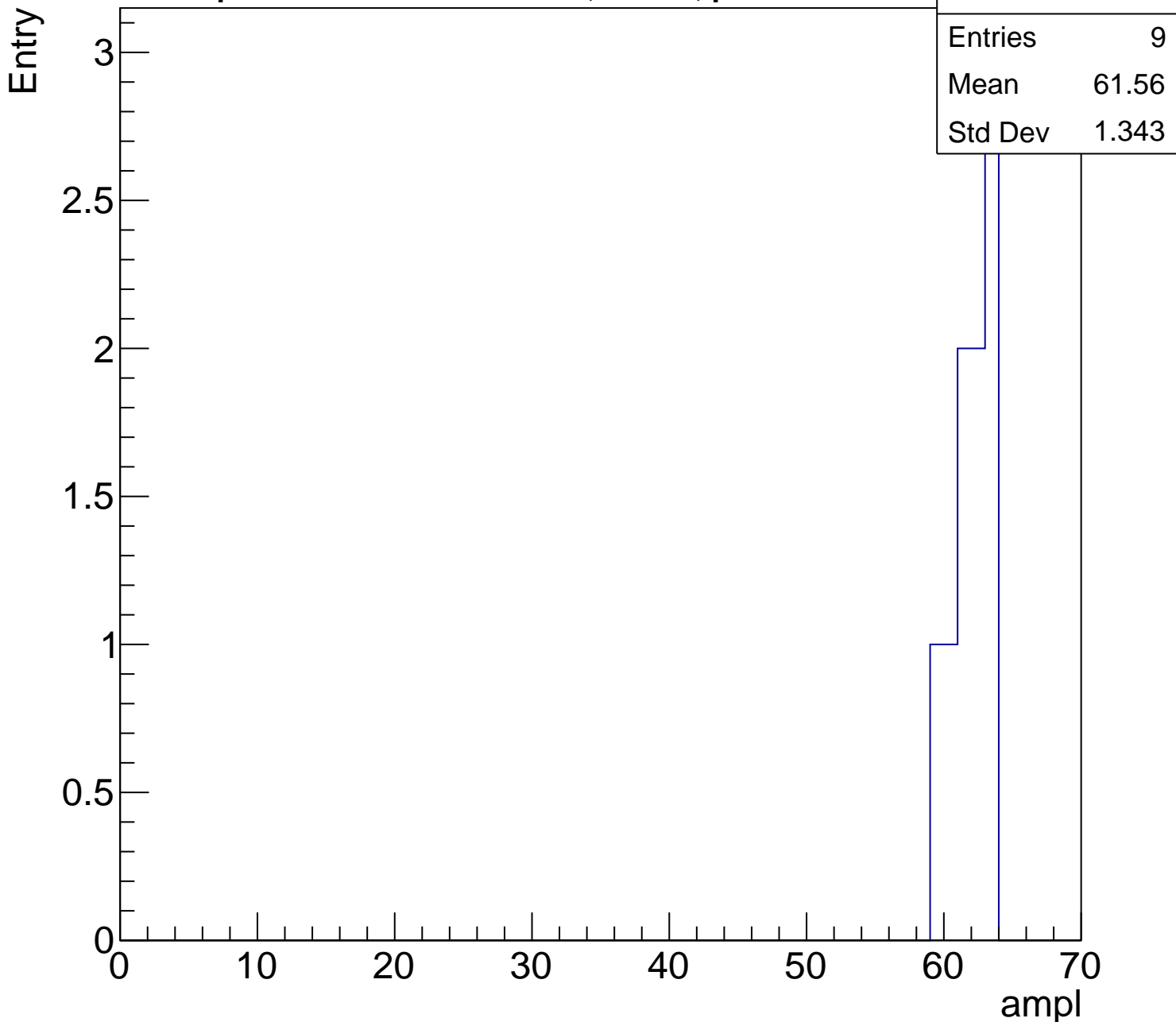
9

Mean

61.56

Std Dev

1.343





# B0L102S, U6-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch45, adc0

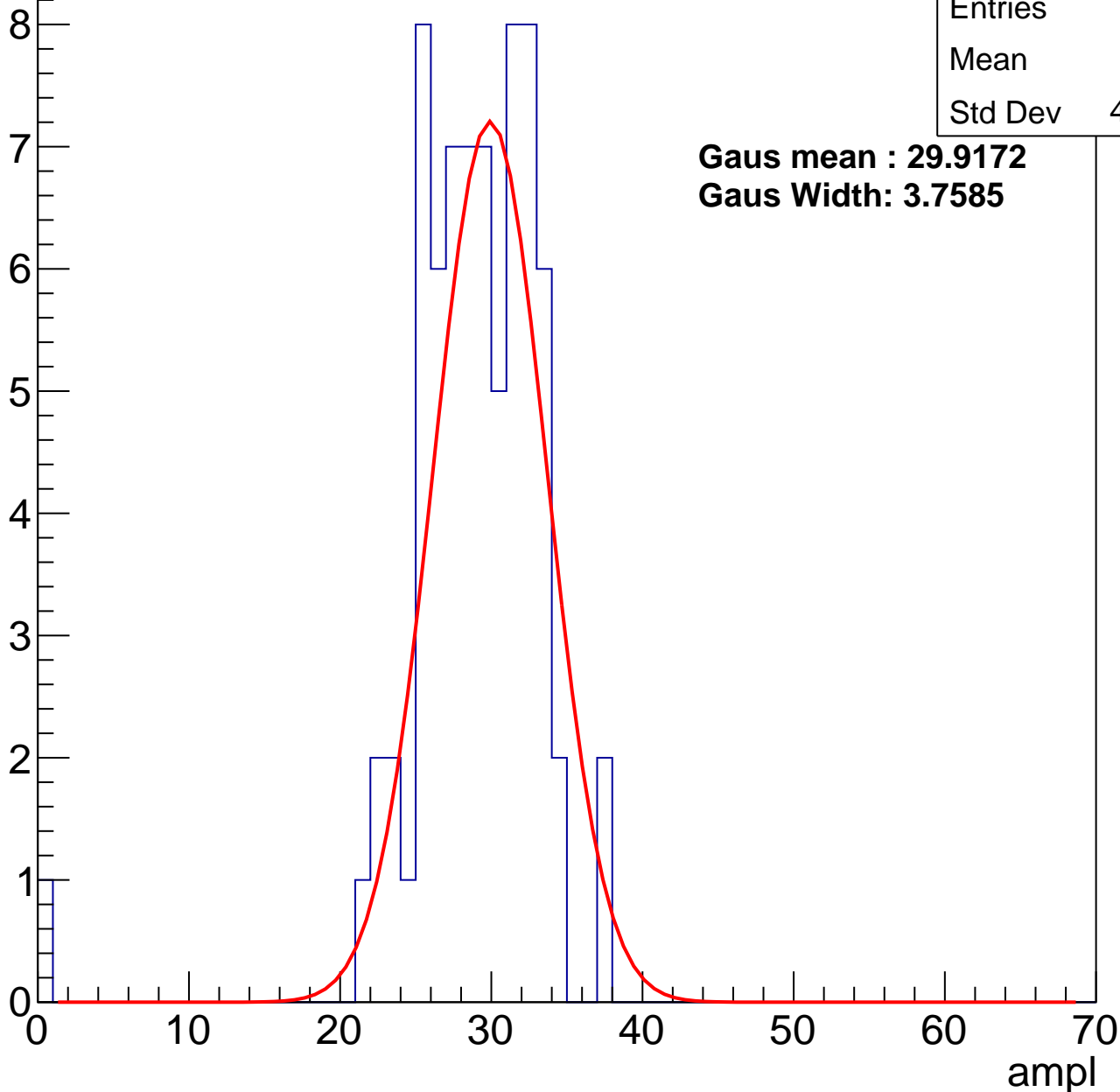
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	28.4
Std Dev	4.788

**Gaus mean : 29.9172**

**Gaus Width: 3.7585**



# B0L102S, U6-ch45, adc1

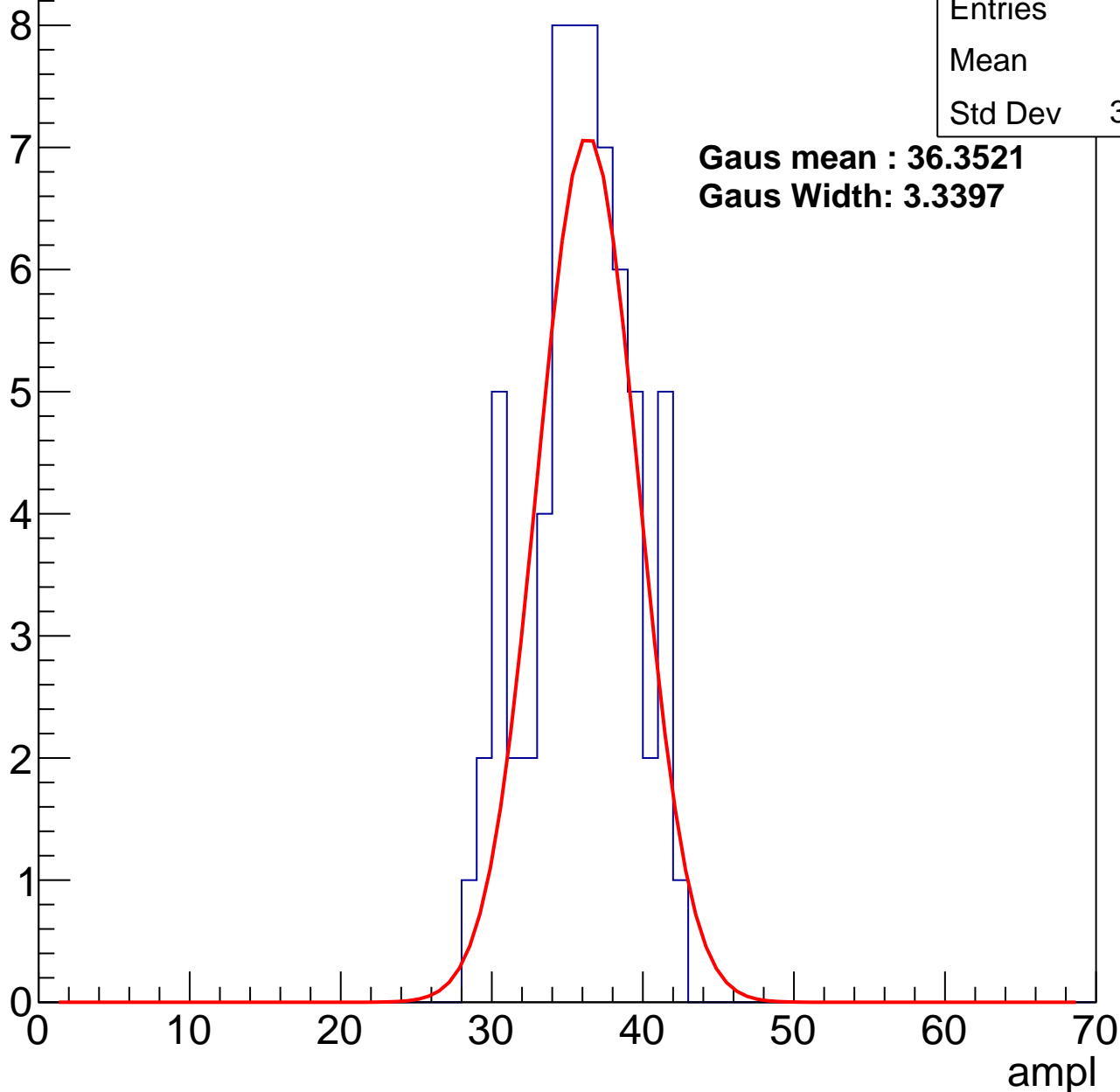
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	35.5
Std Dev	3.377

**Gaus mean : 36.3521**

**Gaus Width: 3.3397**



# B0L102S, U6-ch45, adc2

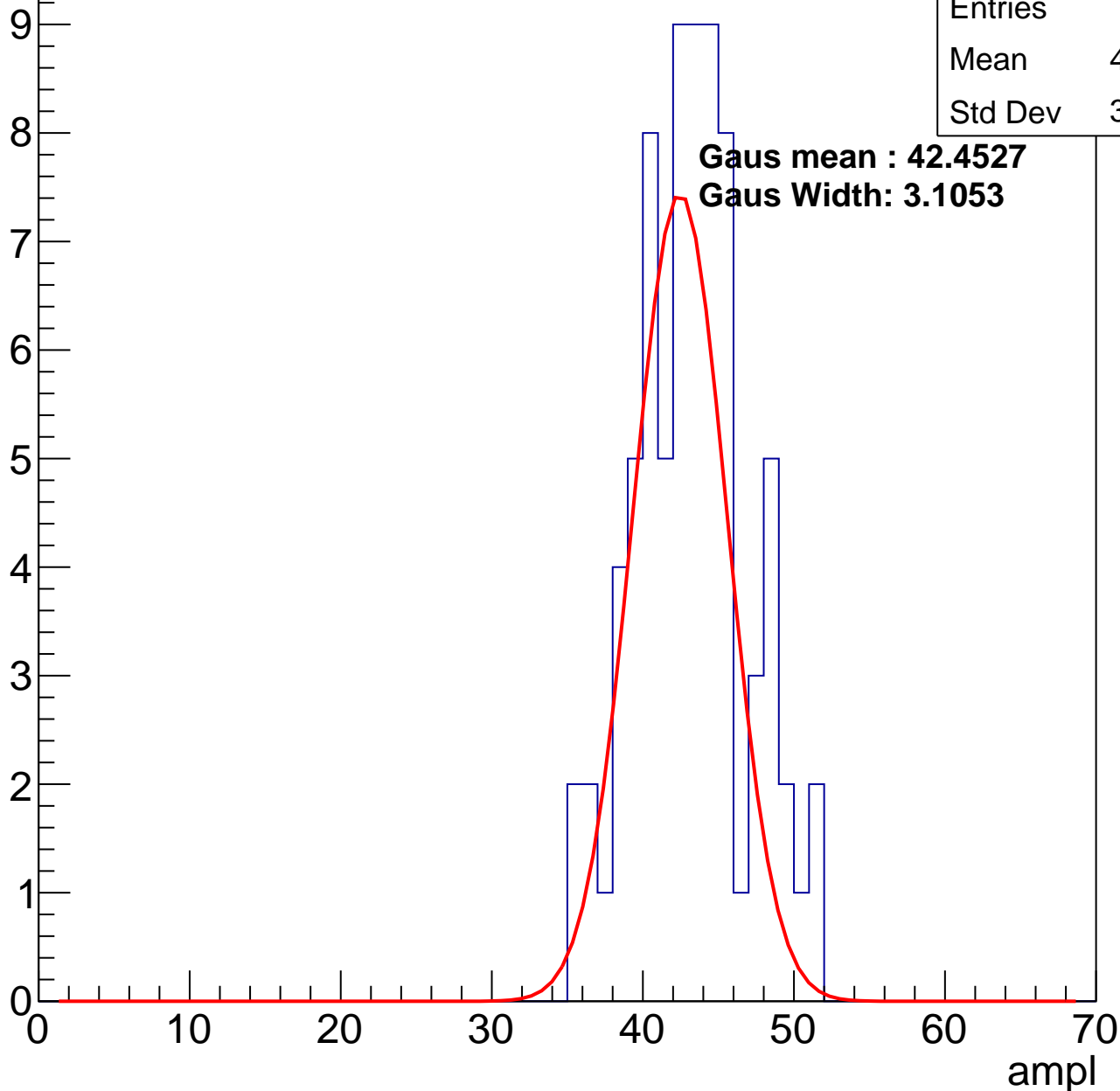
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	42.75
Std Dev	3.646

**Gaus mean : 42.4527**

**Gaus Width: 3.1053**

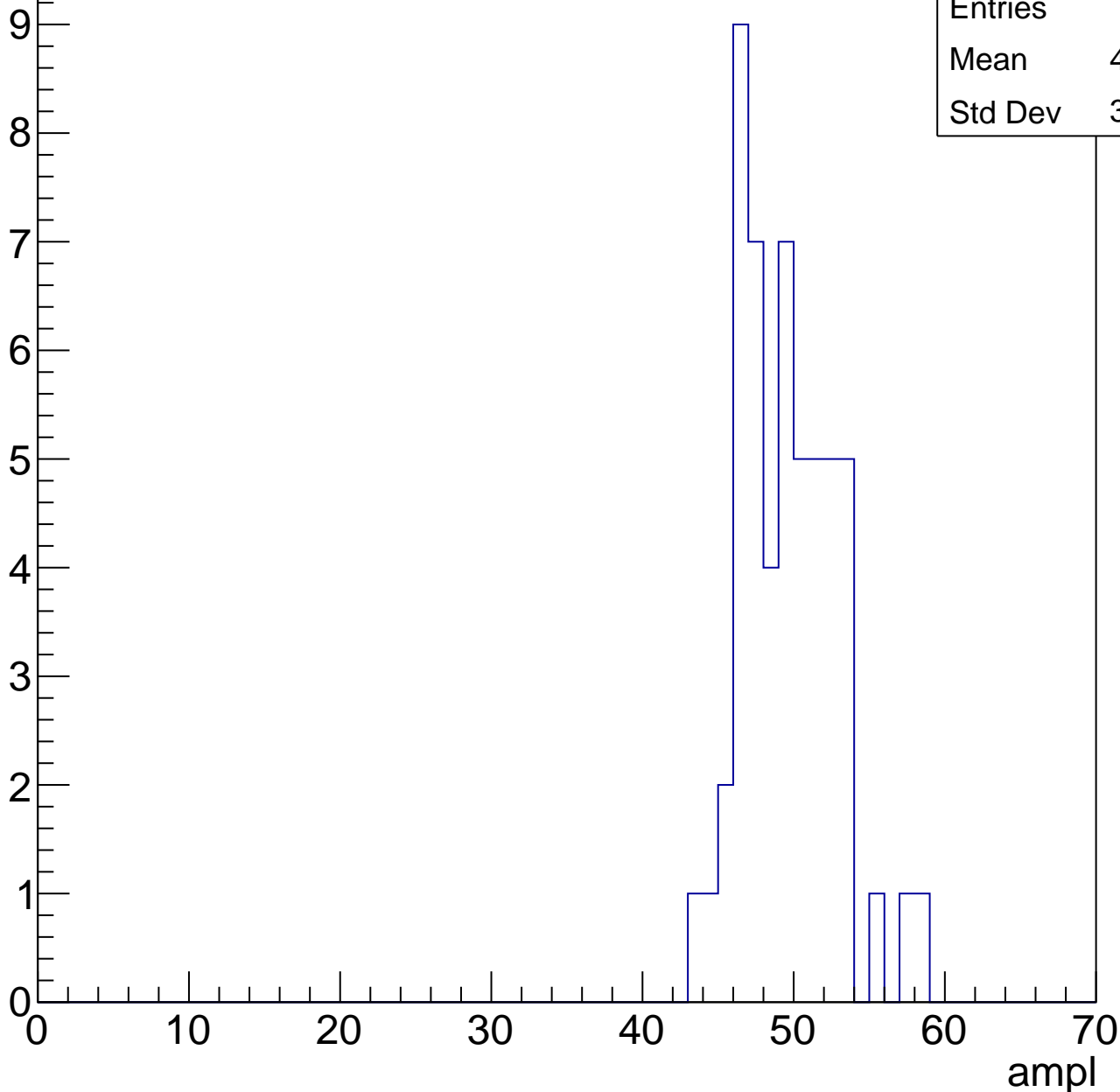


# B0L102S, U6-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

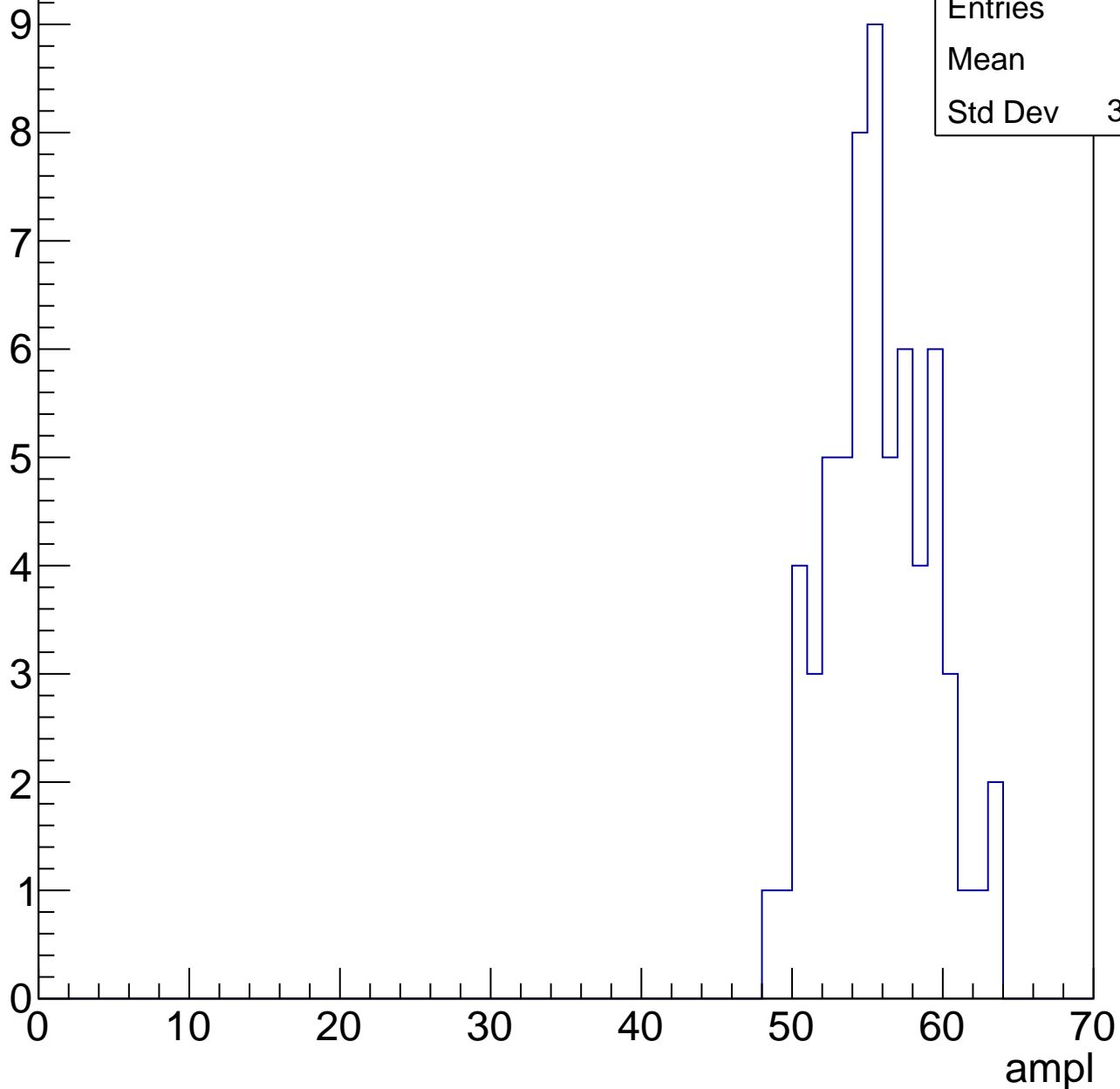
Entries	54
Mean	49.17
Std Dev	3.149



# B0L102S, U6-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



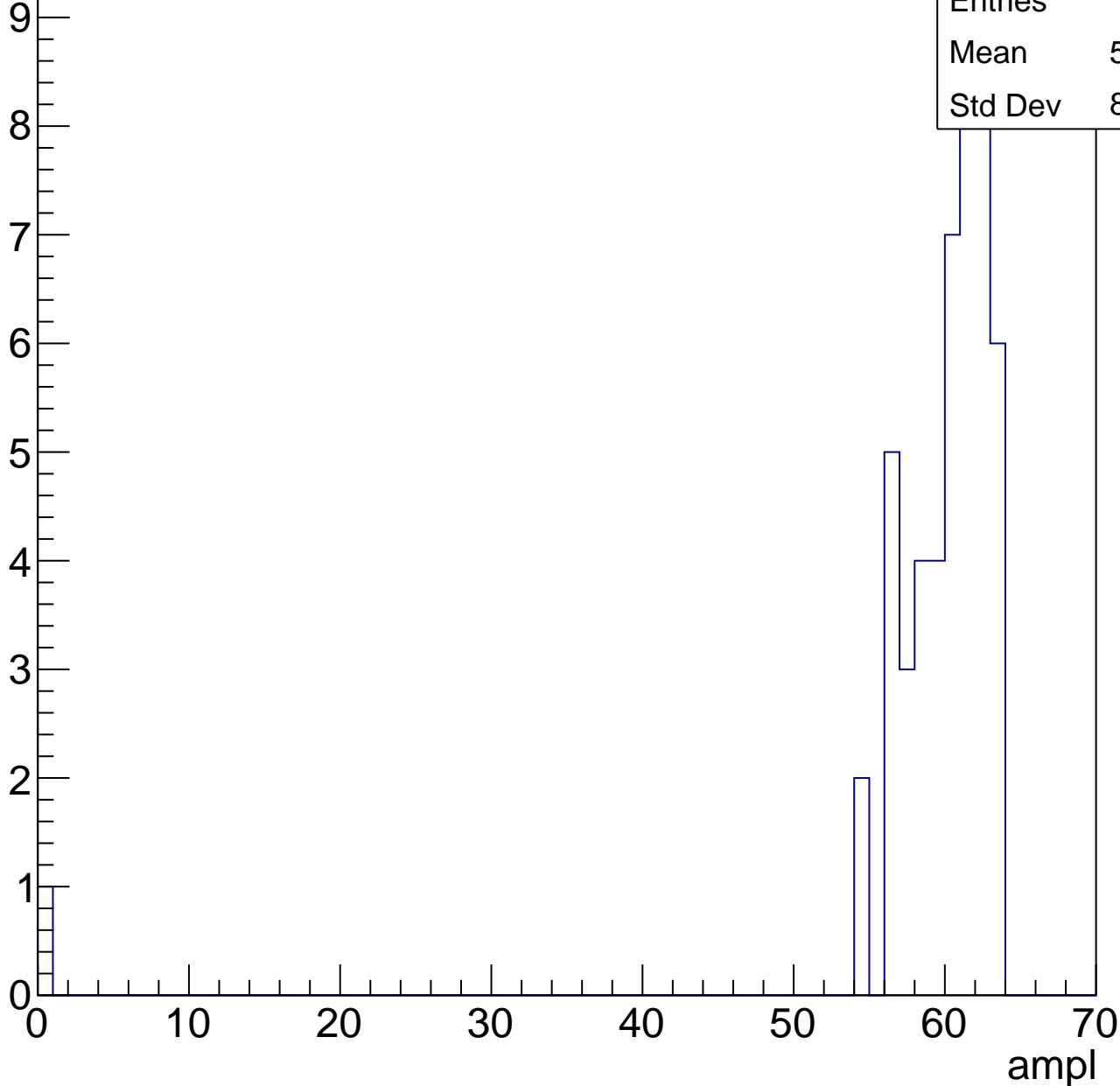
Entries	64
Mean	55.3
Std Dev	3.422

# B0L102S, U6-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	58.57
Std Dev	8.799



# B0L102S, U6-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch46, adc0

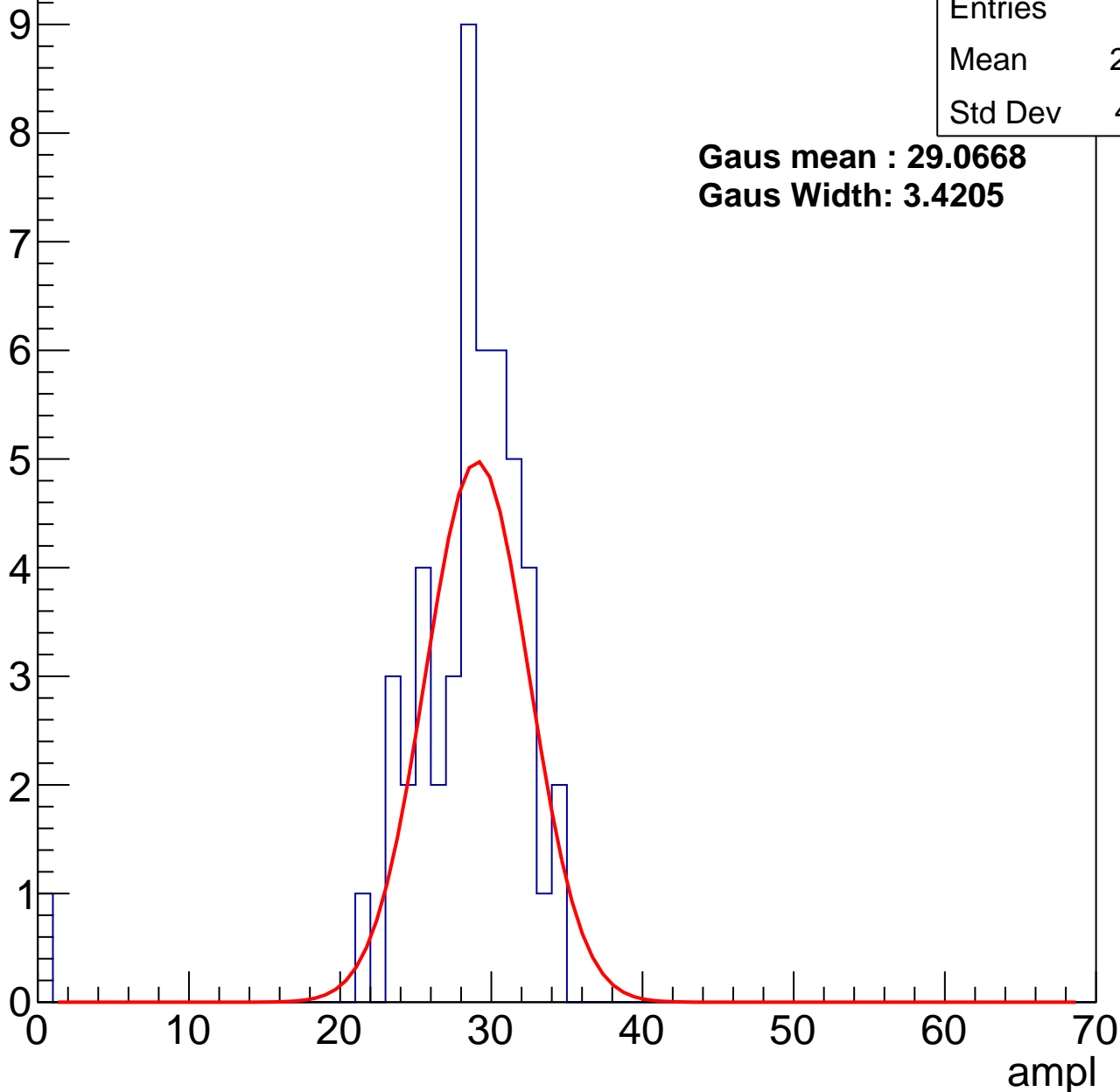
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	27.78
Std Dev	4.991

**Gaus mean : 29.0668**

**Gaus Width: 3.4205**



# B0L102S, U6-ch46, adc1

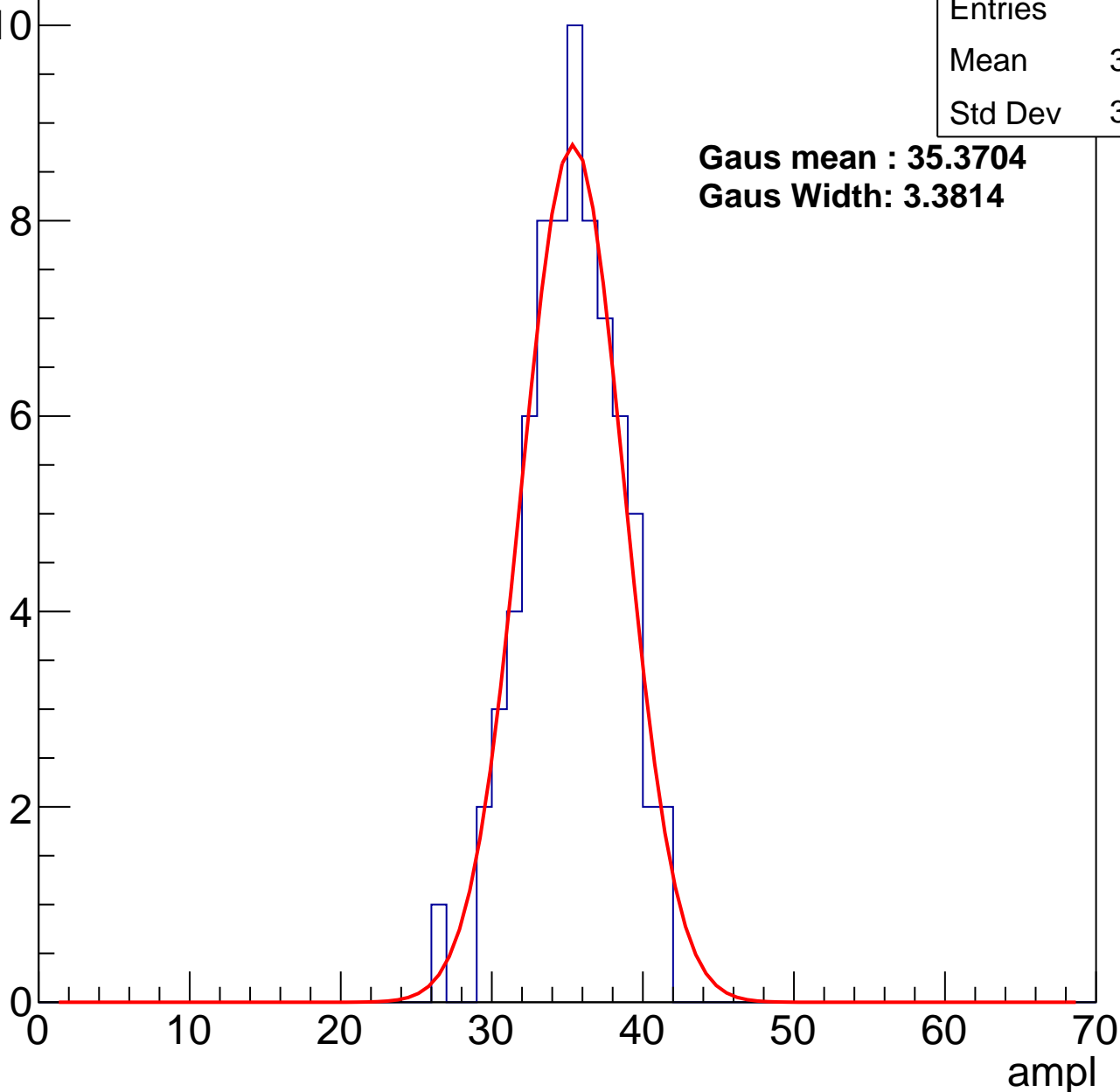
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	34.83
Std Dev	3.064

**Gaus mean : 35.3704**

**Gaus Width: 3.3814**



# B0L102S, U6-ch46, adc2

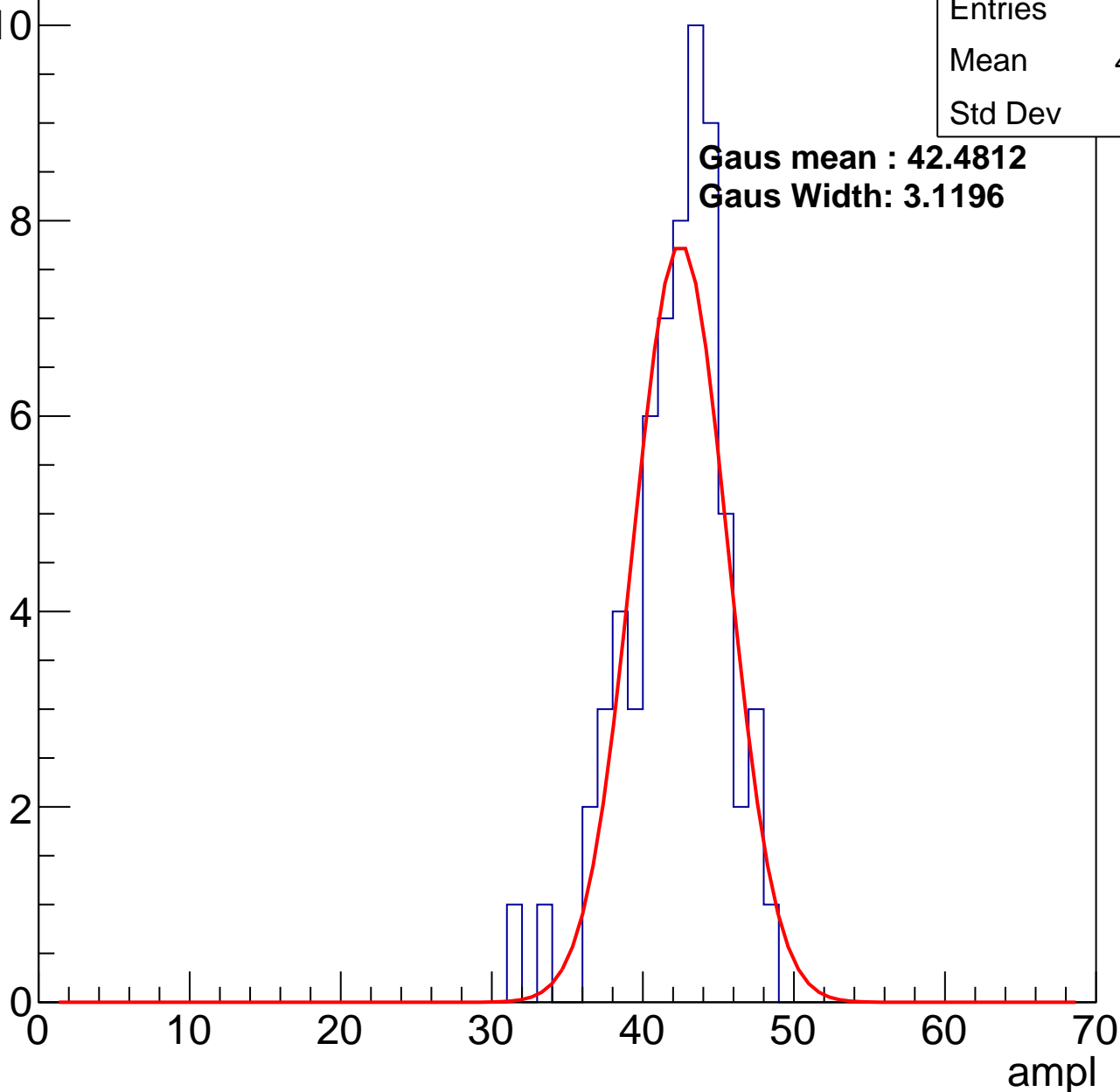
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.71
Std Dev	3.28

**Gaus mean : 42.4812**

**Gaus Width: 3.1196**

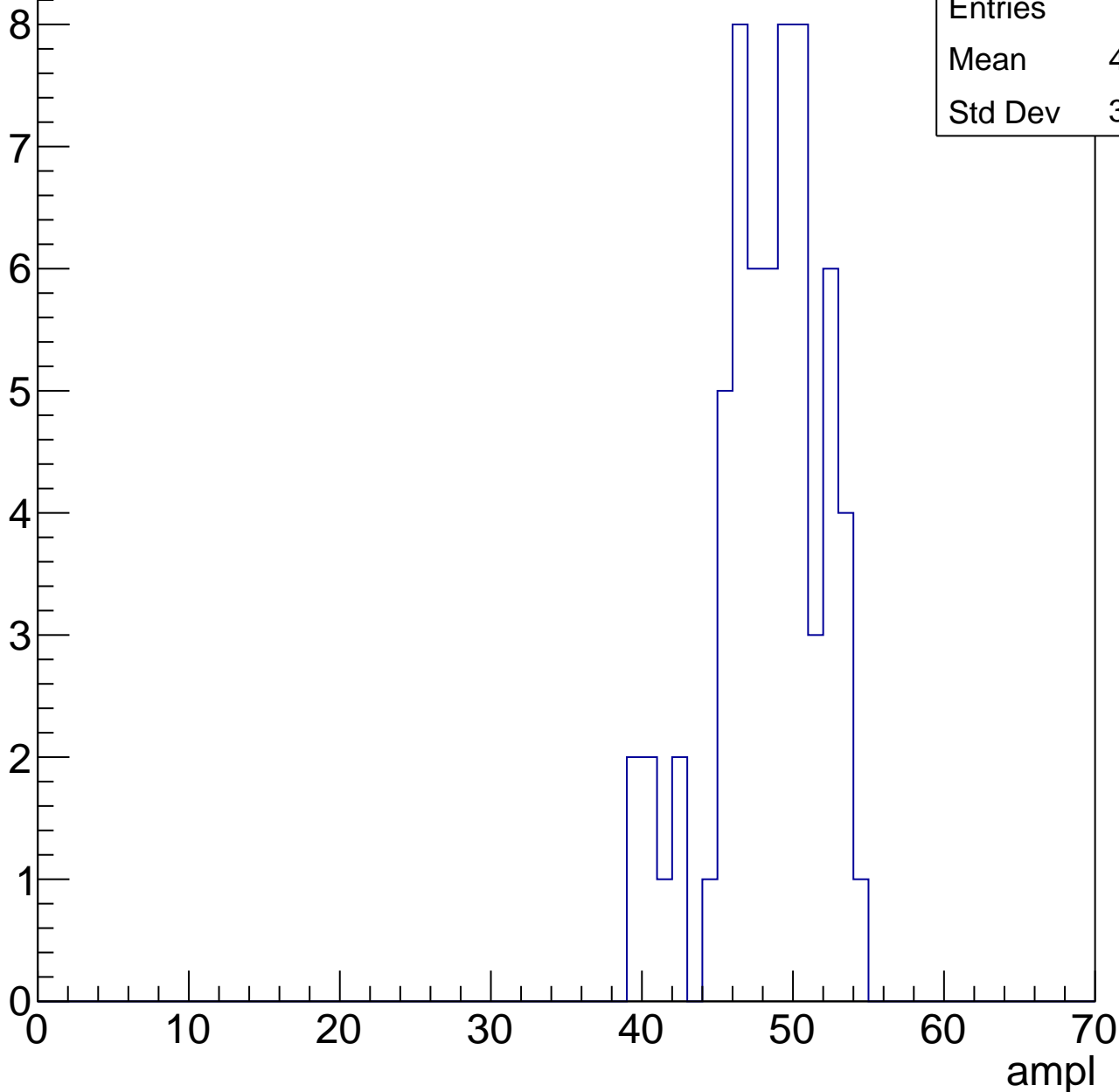


# B0L102S, U6-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	47.83
Std Dev	3.579

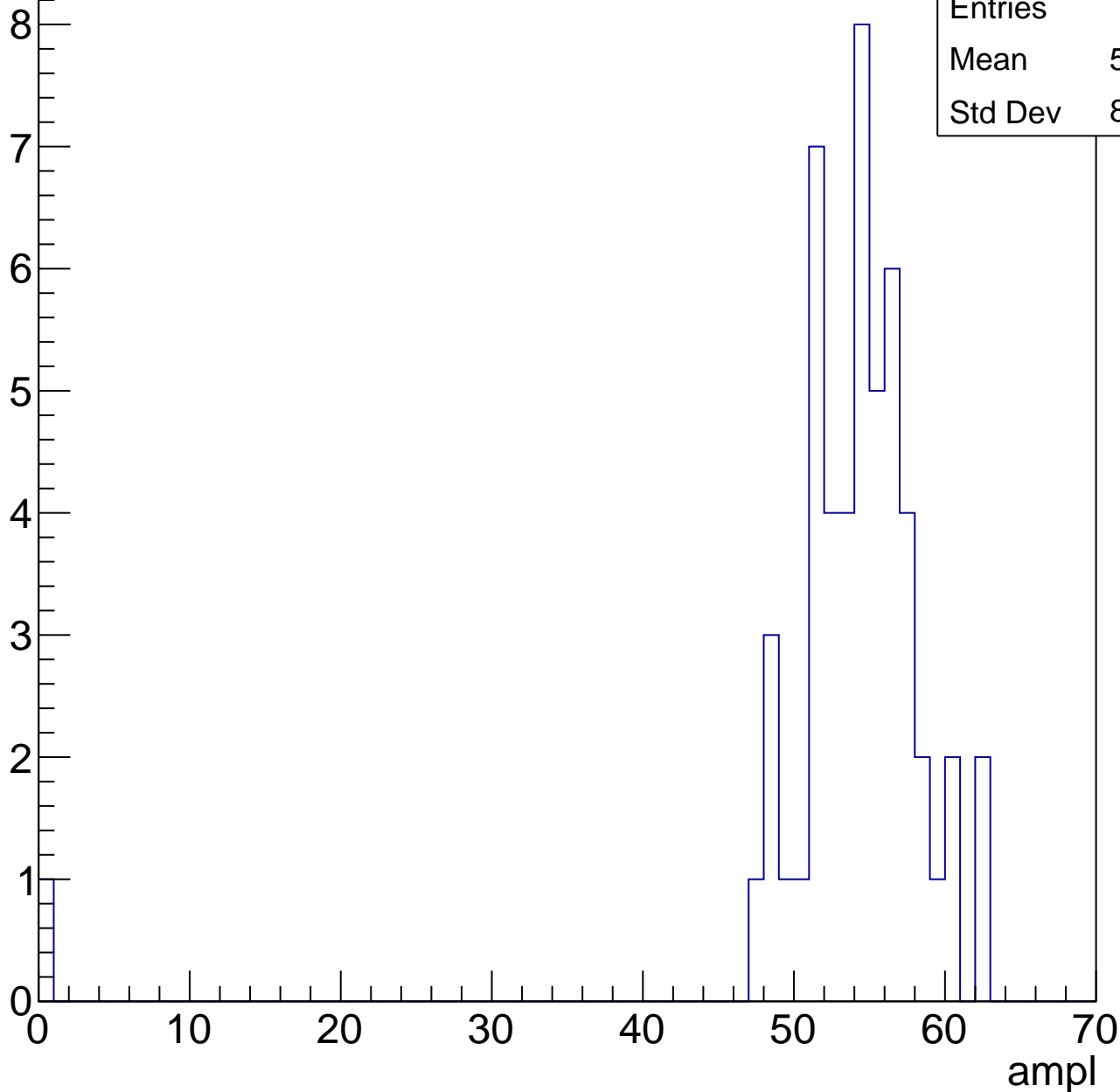


# B0L102S, U6-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

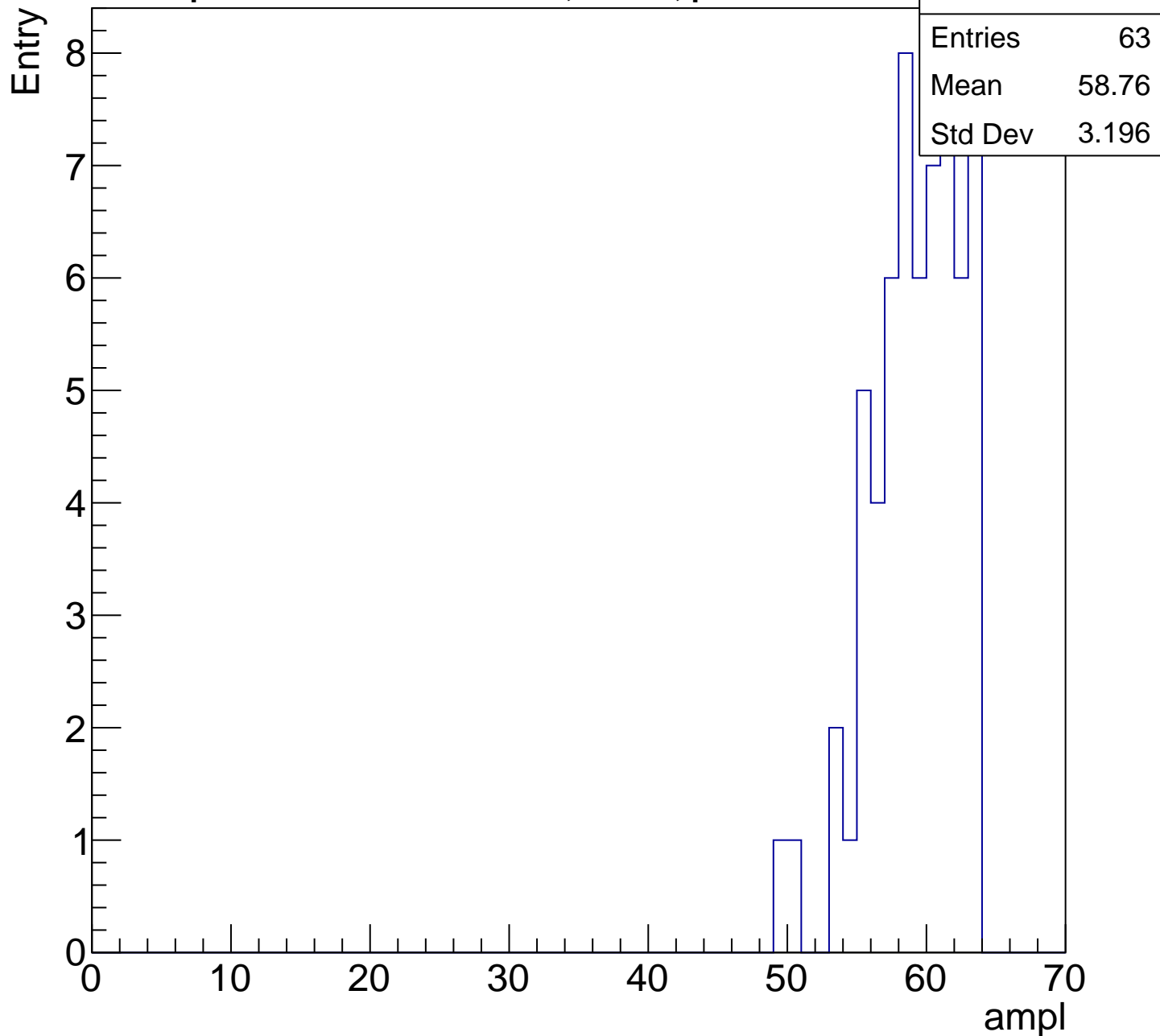
Entry

Entries	52
Mean	53.02
Std Dev	8.168



# B0L102S, U6-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

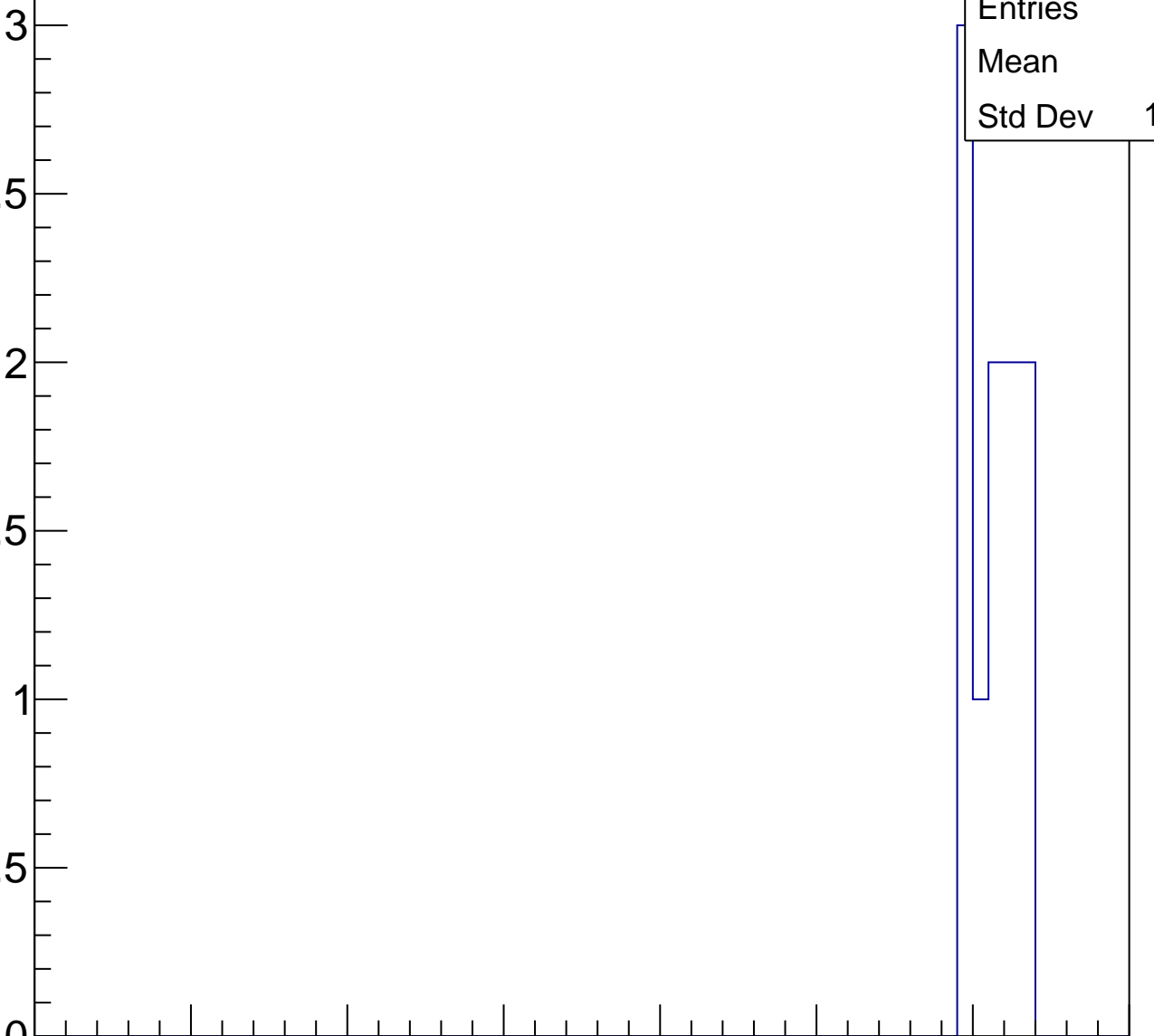
Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	10
Mean	60.9
Std Dev	1.513

ampl

0 10 20 30 40 50 60 70





# B0L102S, U6-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch47, adc0

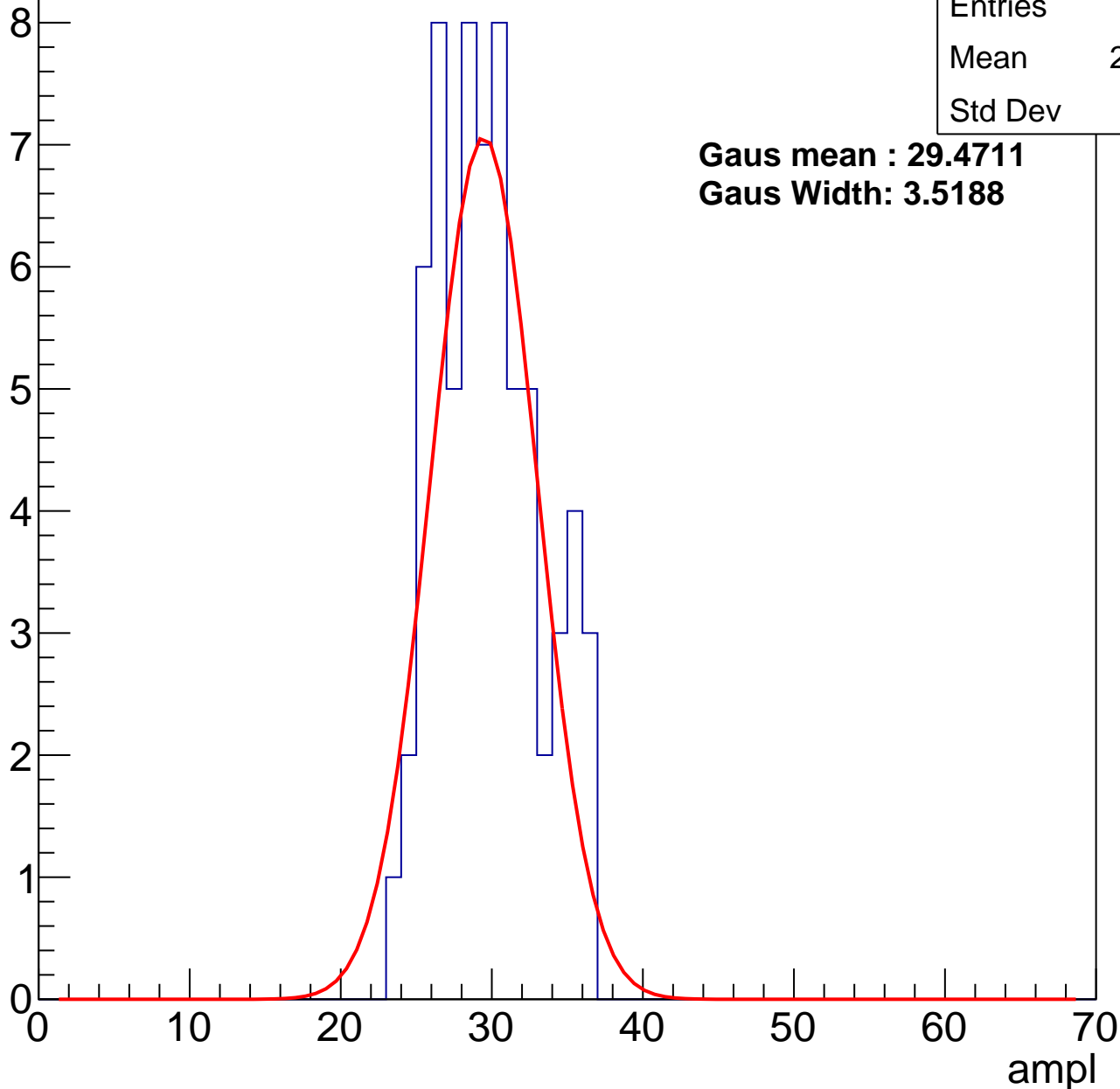
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	29.28
Std Dev	3.34

**Gaus mean : 29.4711**

**Gaus Width: 3.5188**



# B0L102S, U6-ch47, adc1

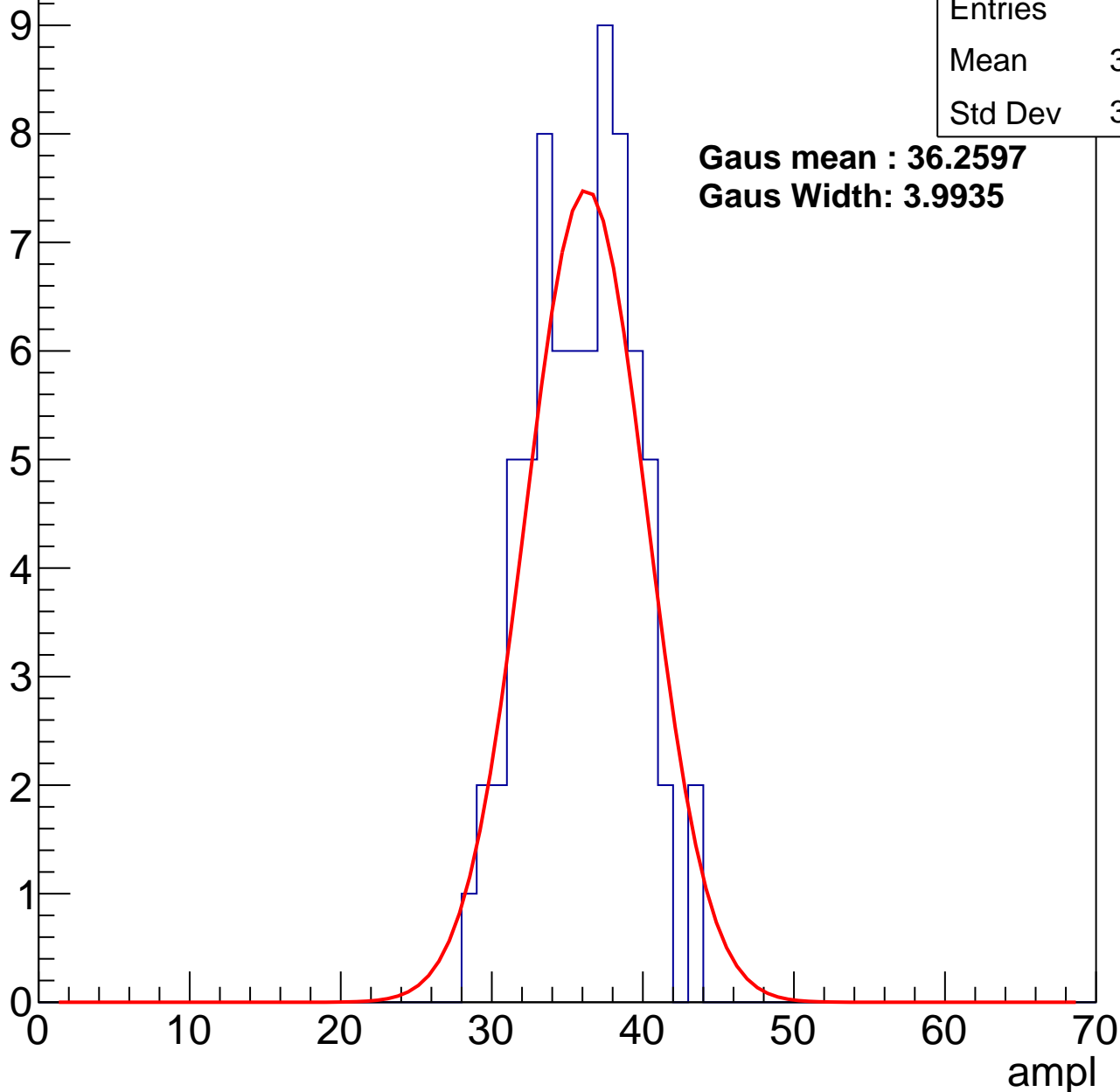
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	35.53
Std Dev	3.409

**Gaus mean : 36.2597**

**Gaus Width: 3.9935**



# B0L102S, U6-ch47, adc2

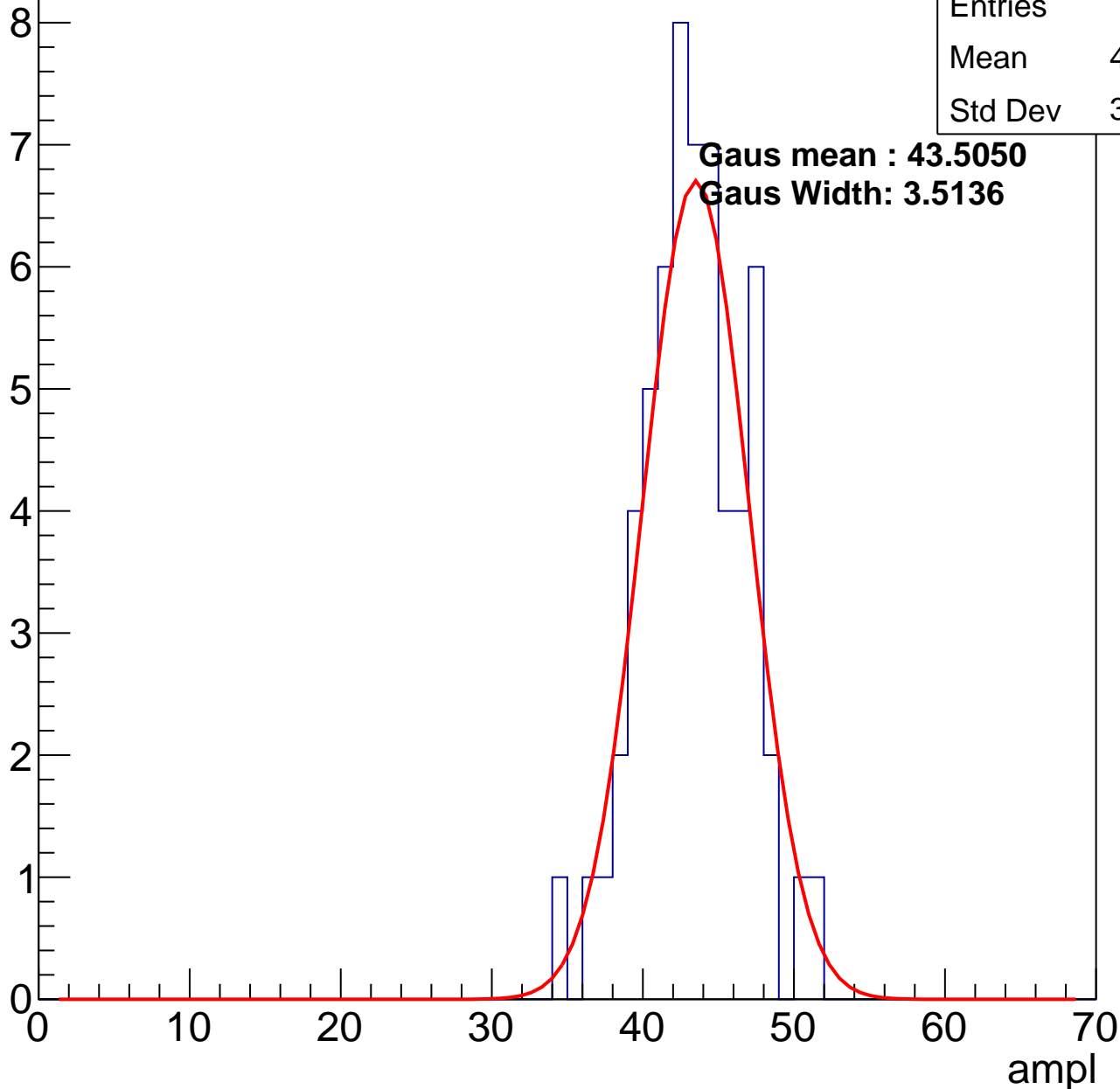
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.88
Std Dev	3.352

**Gaus mean : 43.5050**

**Gaus Width: 3.5136**



# B0L102S, U6-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

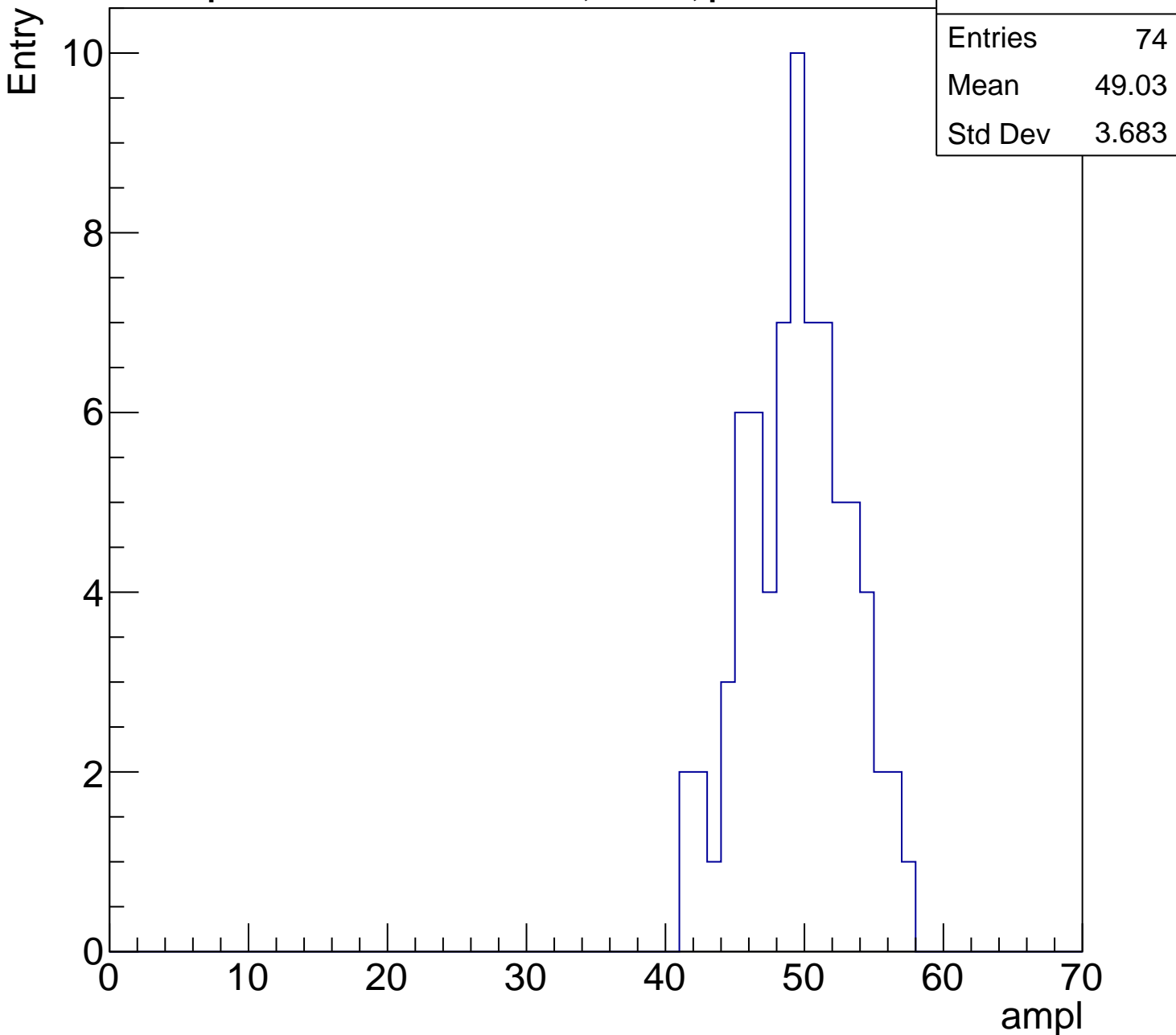
Entries	74
Mean	49.03
Std Dev	3.683

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

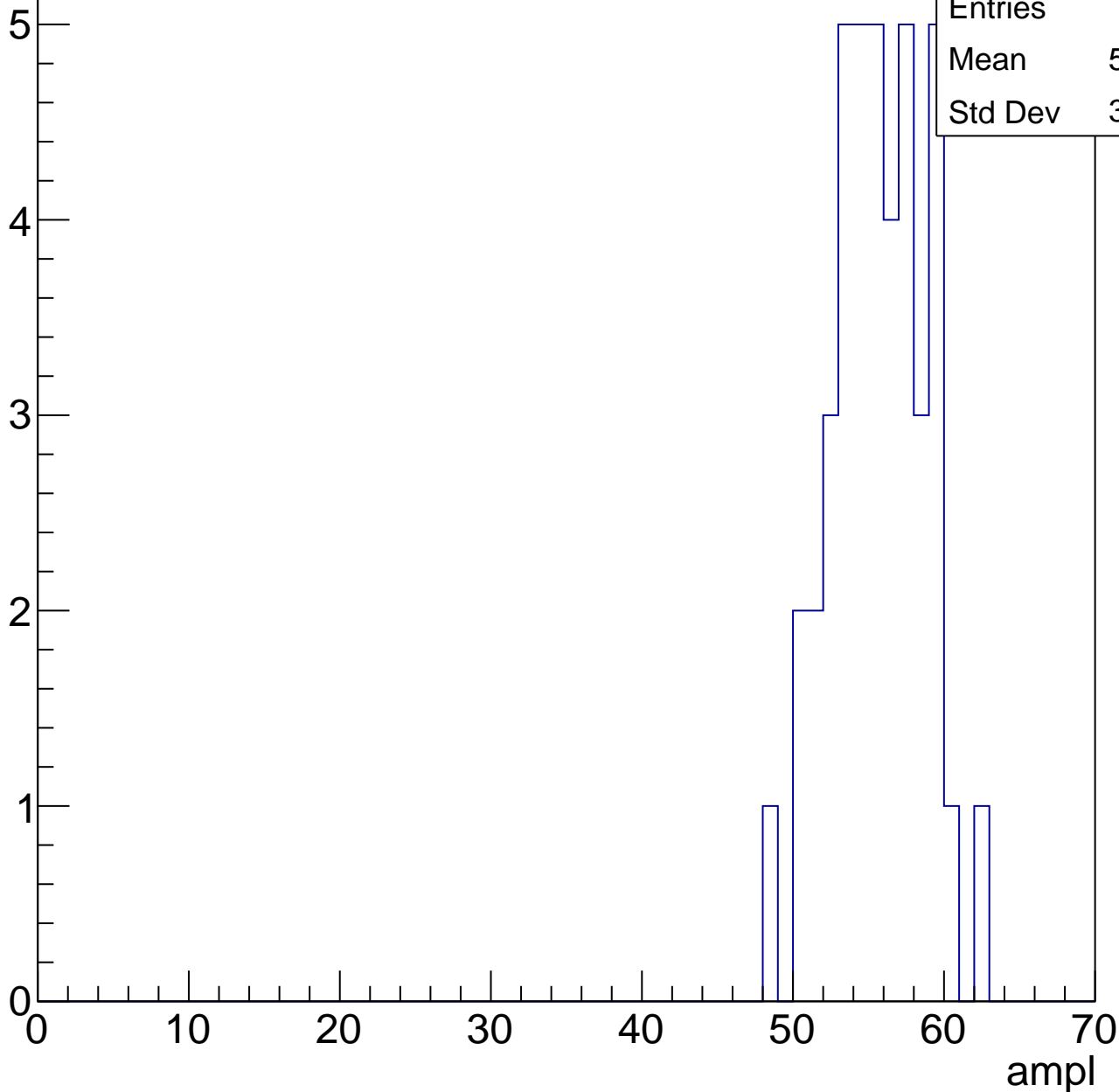


# B0L102S, U6-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	42
Mean	55.14
Std Dev	3.036

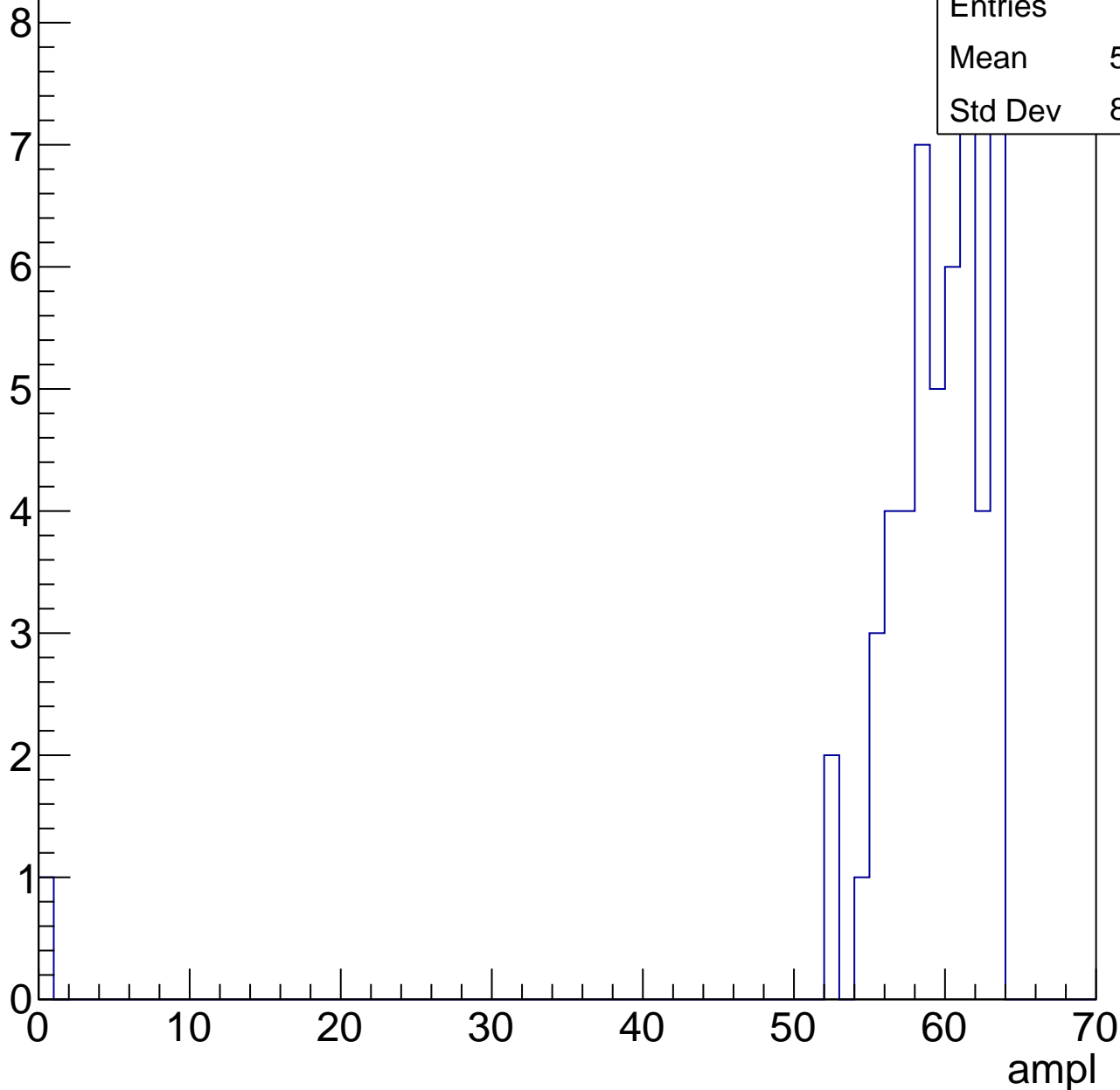


# B0L102S, U6-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	58.04
Std Dev	8.538

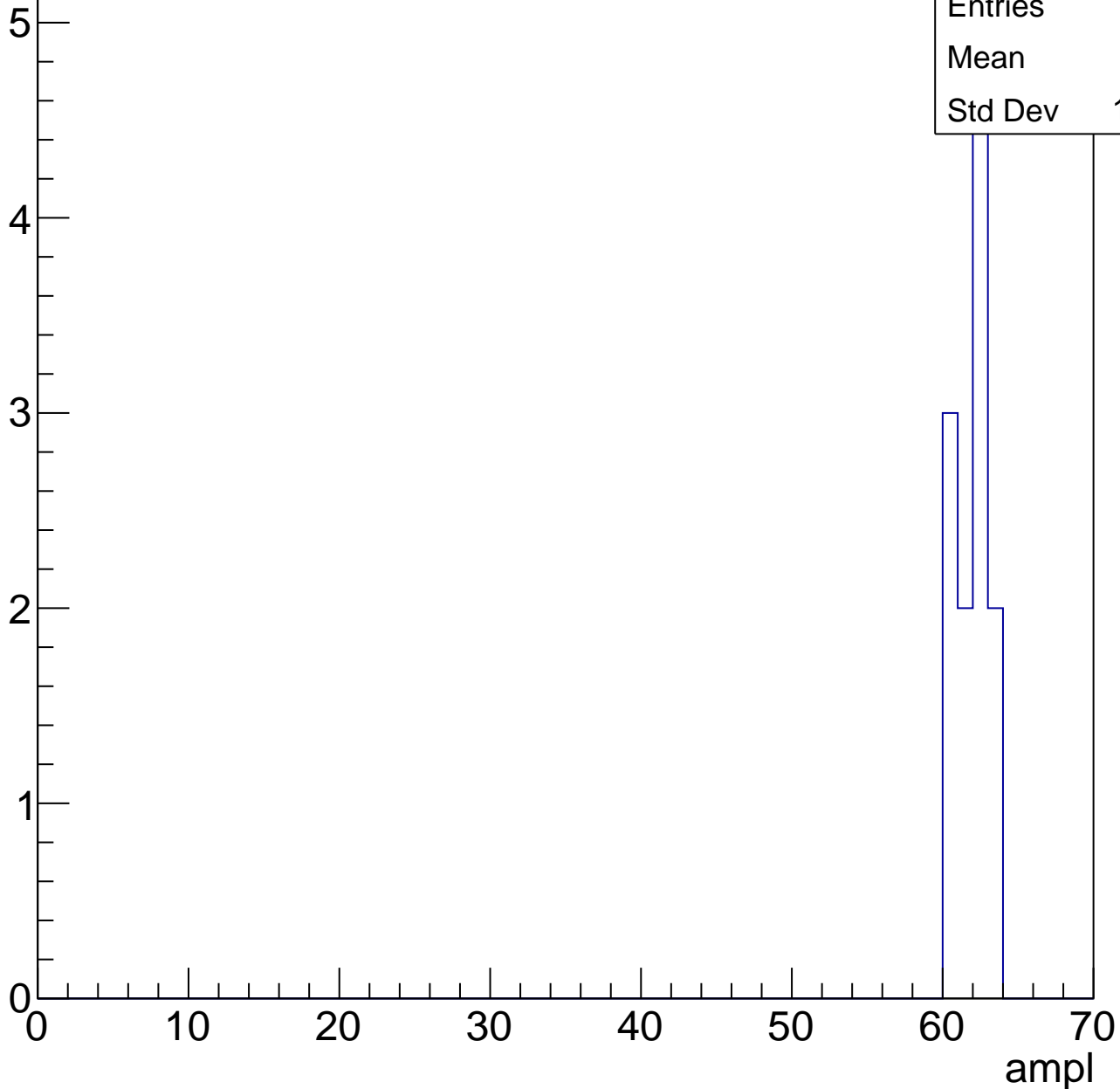


# B0L102S, U6-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	61.5
Std Dev	1.041





# B0L102S, U6-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch48, adc0

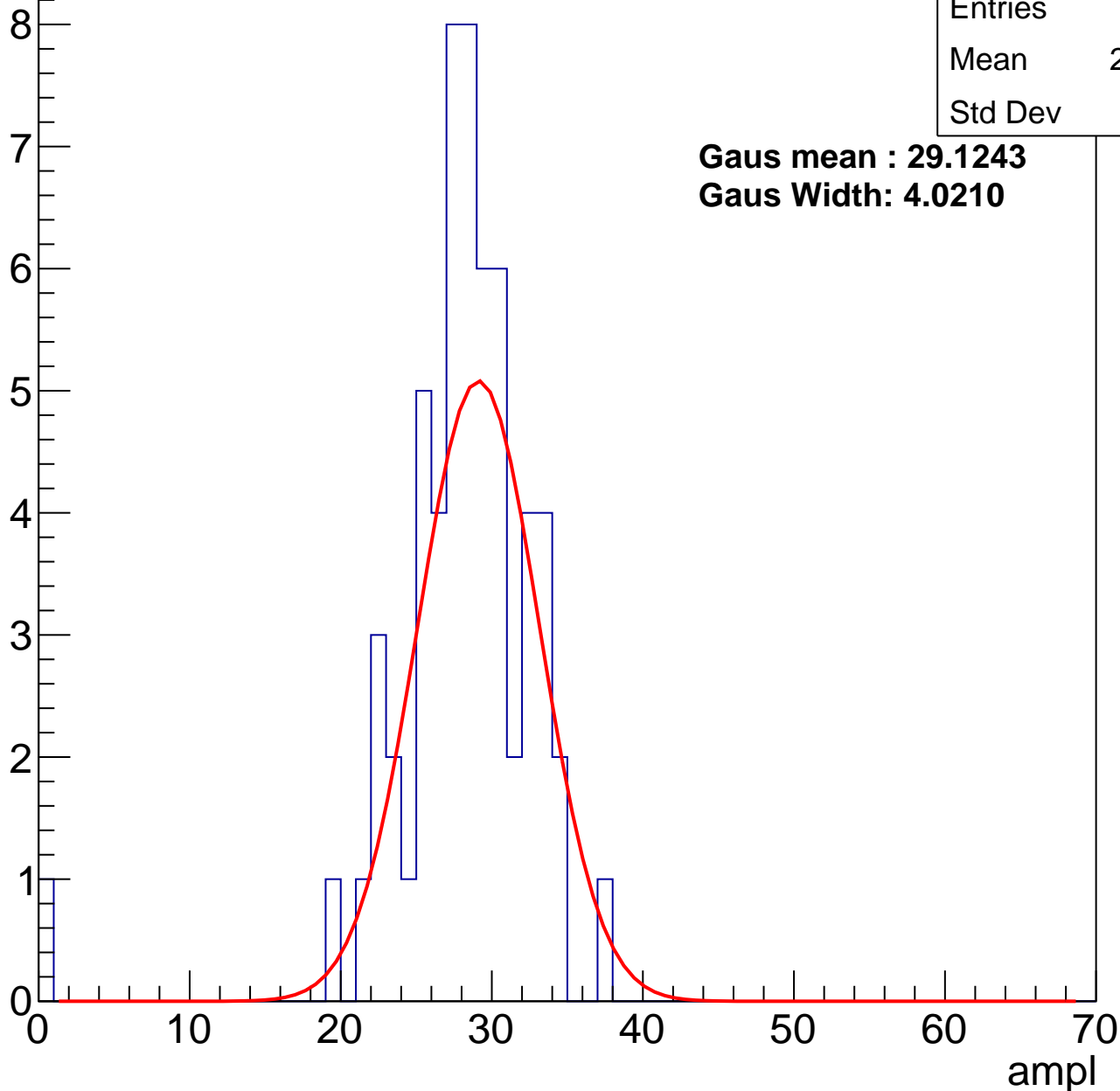
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	27.56
Std Dev	5.06

**Gaus mean : 29.1243**

**Gaus Width: 4.0210**



# B0L102S, U6-ch48, adc1

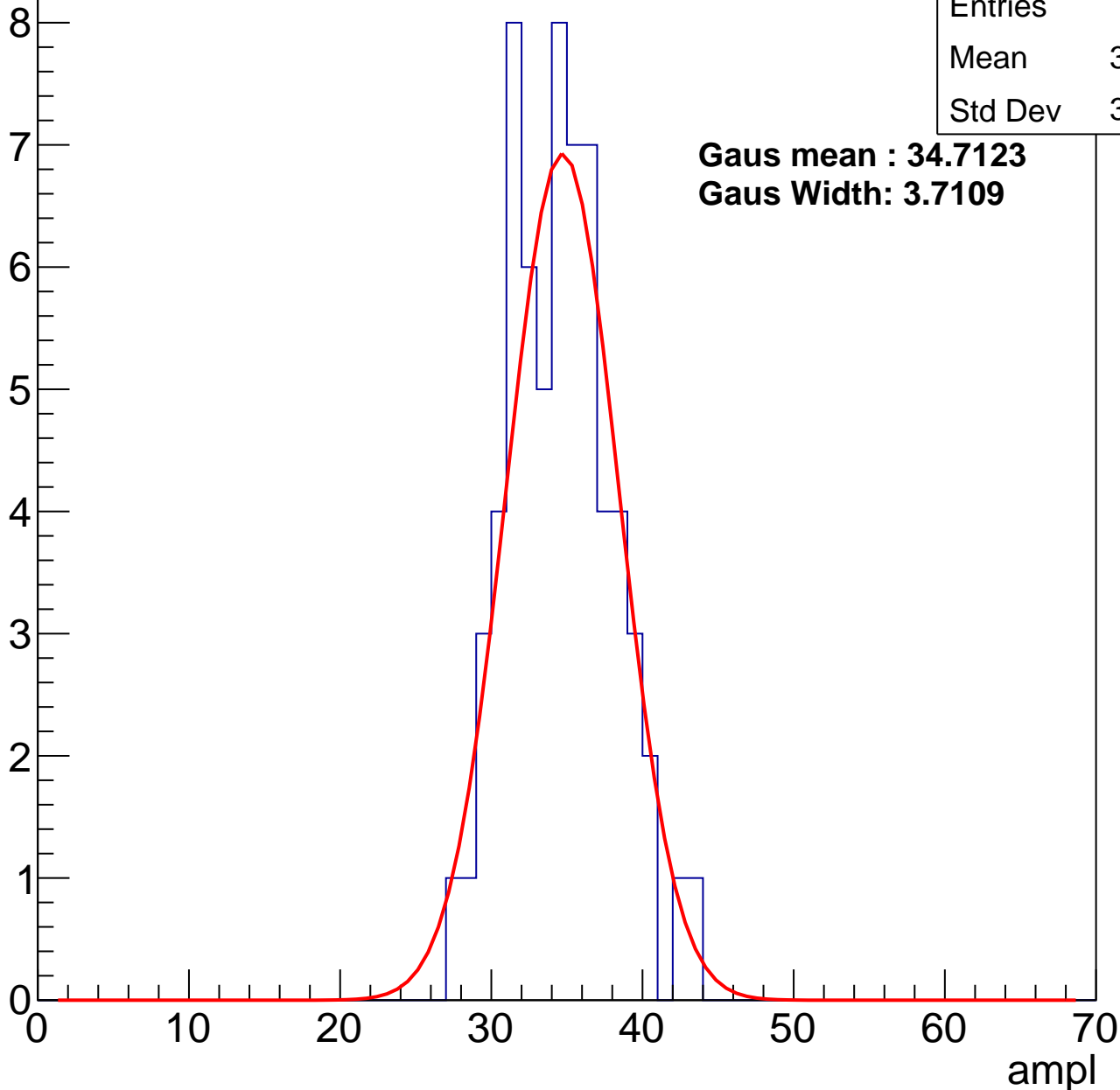
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	34.12
Std Dev	3.399

**Gaus mean : 34.7123**

**Gaus Width: 3.7109**



# B0L102S, U6-ch48, adc2

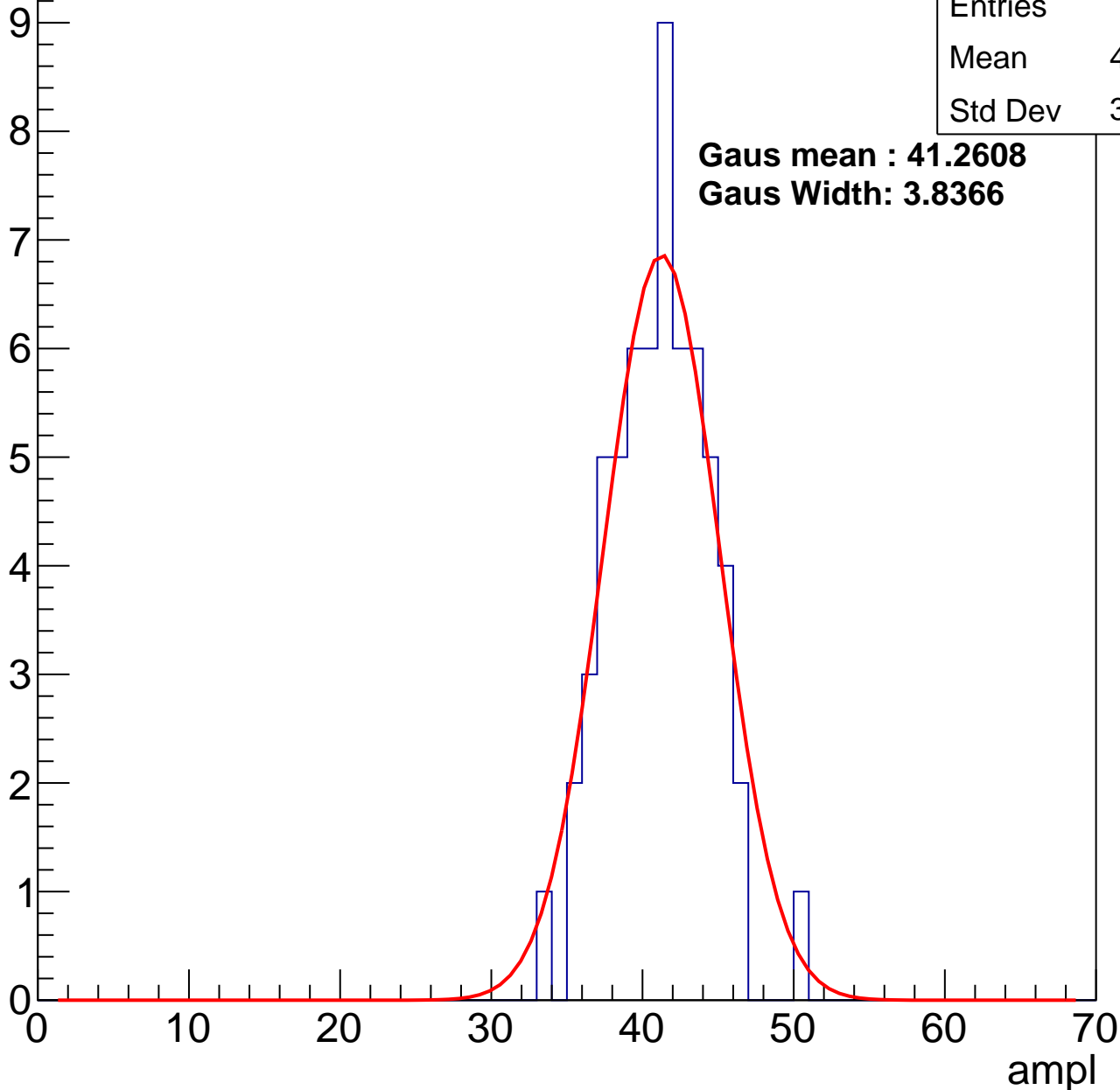
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	40.67
Std Dev	3.212

**Gaus mean : 41.2608**

**Gaus Width: 3.8366**



# B0L102S, U6-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

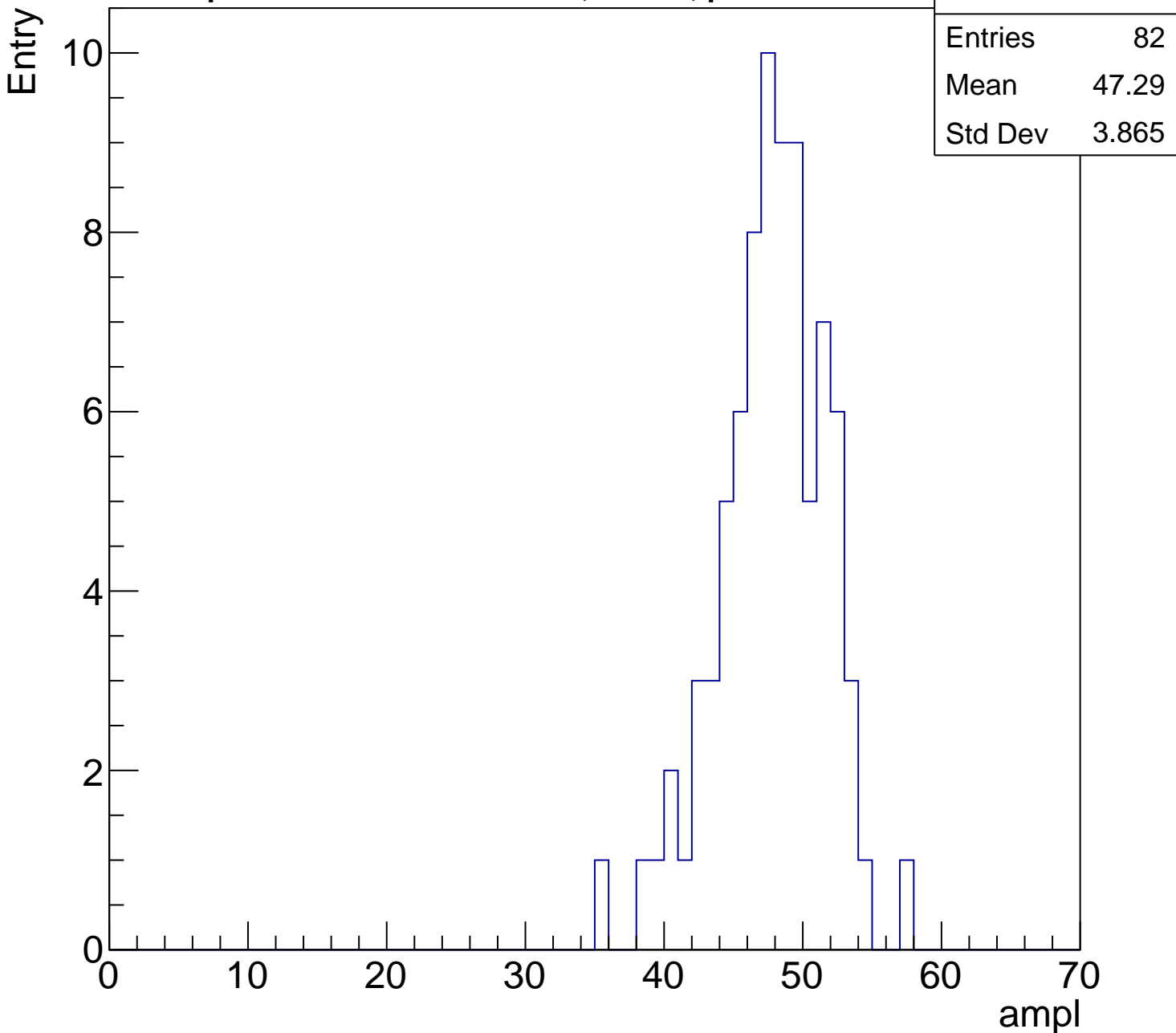
Entries	82
Mean	47.29
Std Dev	3.865

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

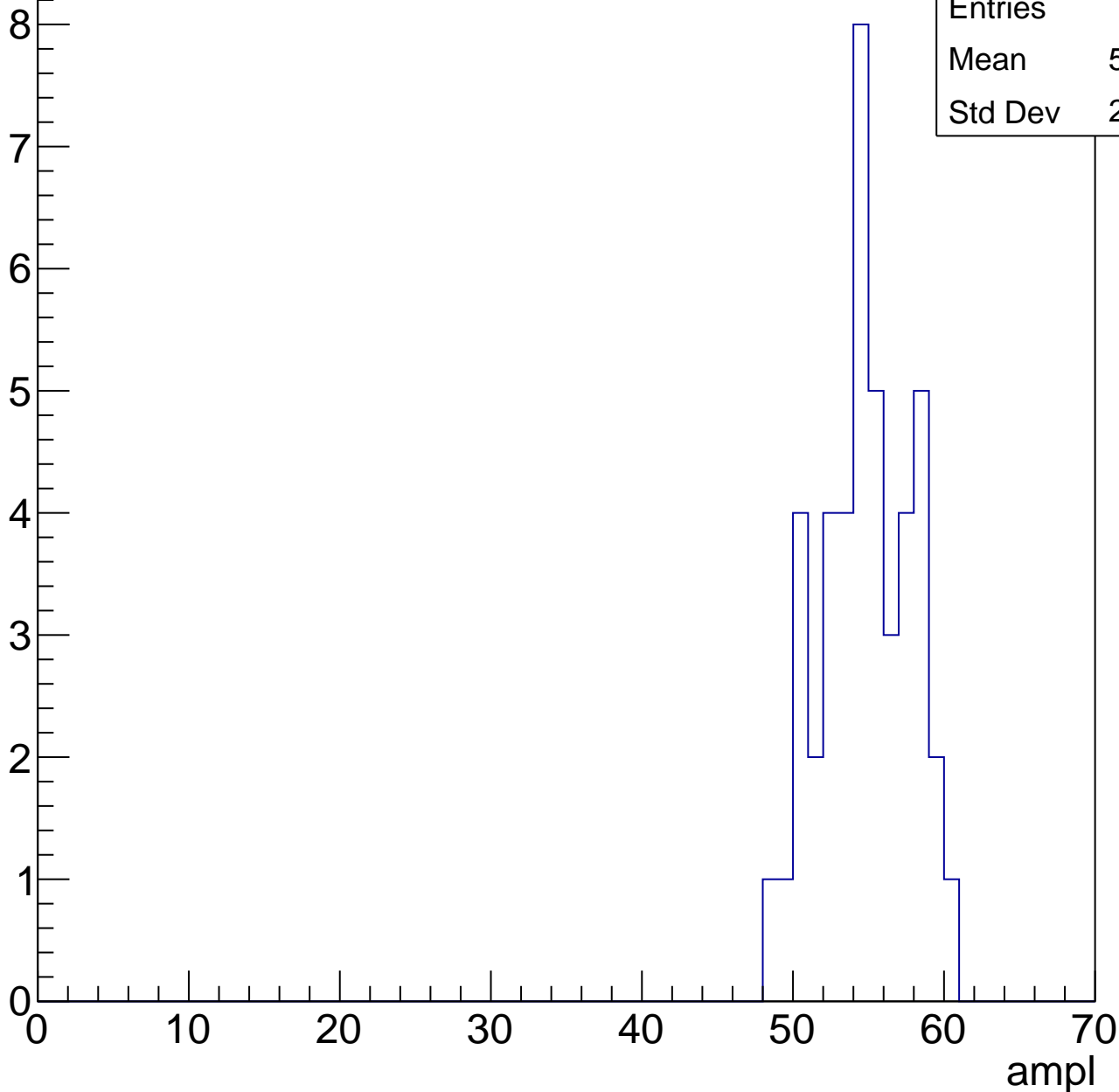


# B0L102S, U6-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	54.32
Std Dev	2.929

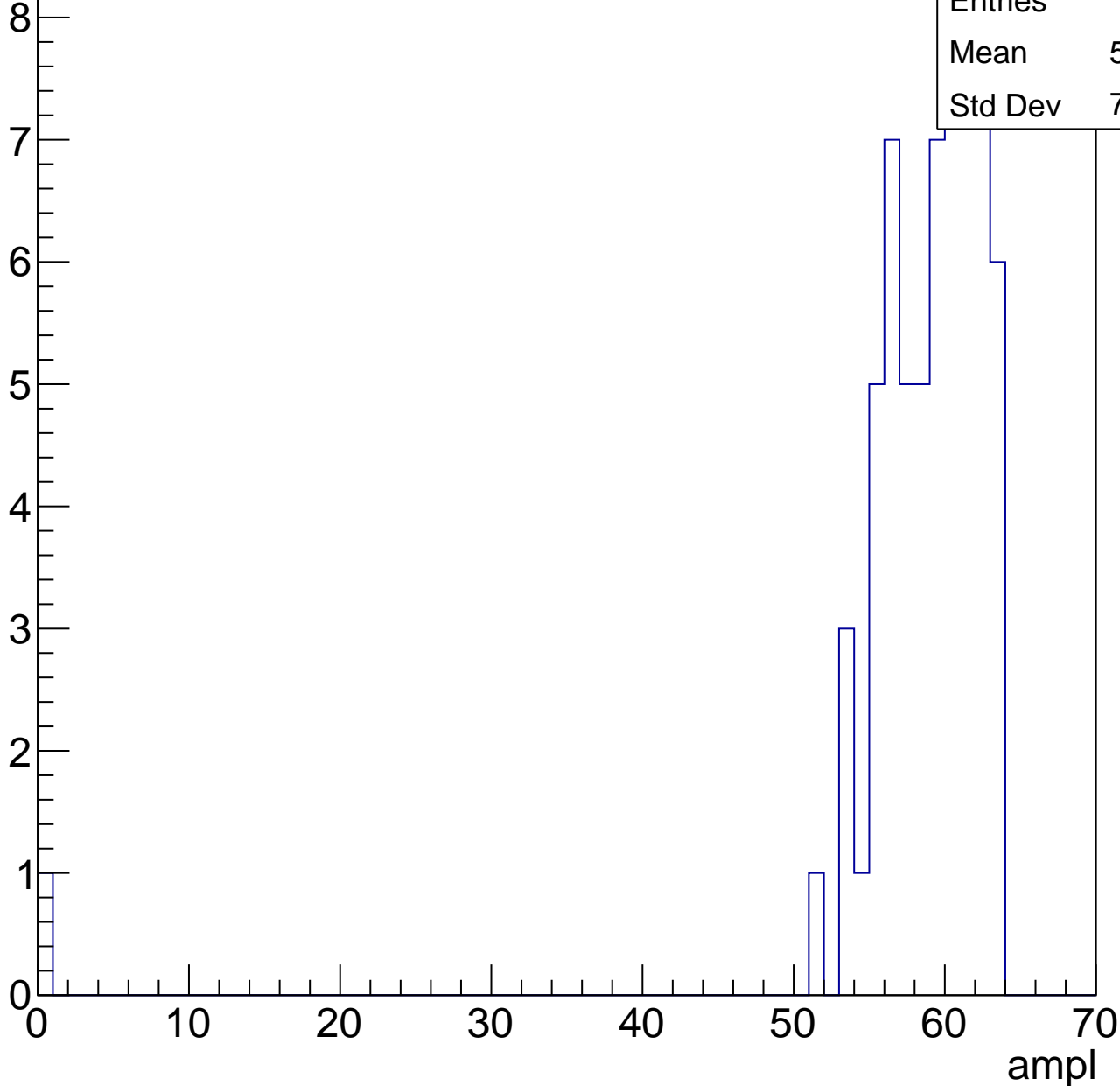


# B0L102S, U6-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

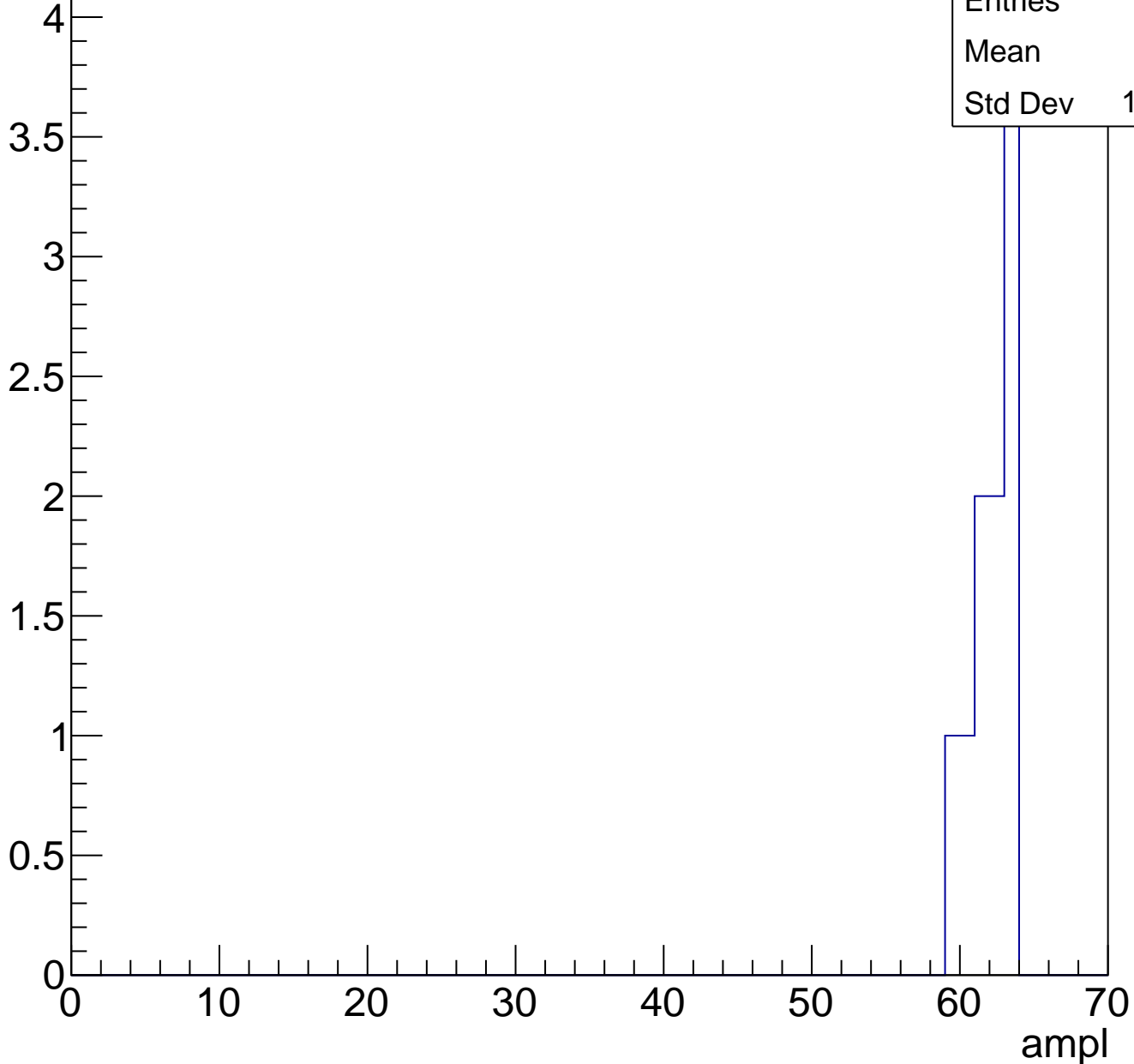
Entries	65
Mean	57.86
Std Dev	7.815



# B0L102S, U6-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	10
Mean	61.7
Std Dev	1.345



# B0L102S, U6-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch49, adc0

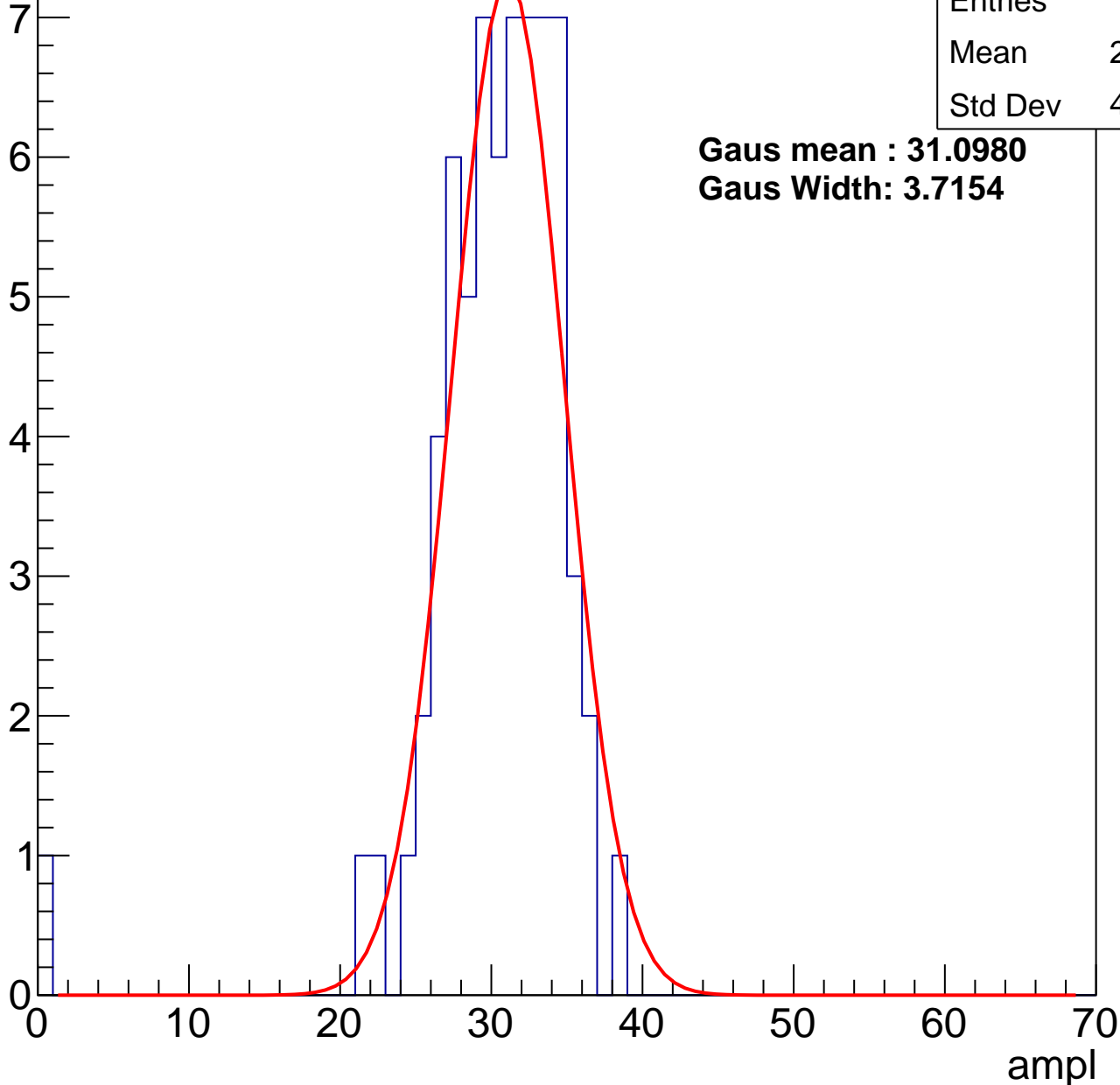
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	29.87
Std Dev	4.994

**Gaus mean : 31.0980**

**Gaus Width: 3.7154**



# B0L102S, U6-ch49, adc1

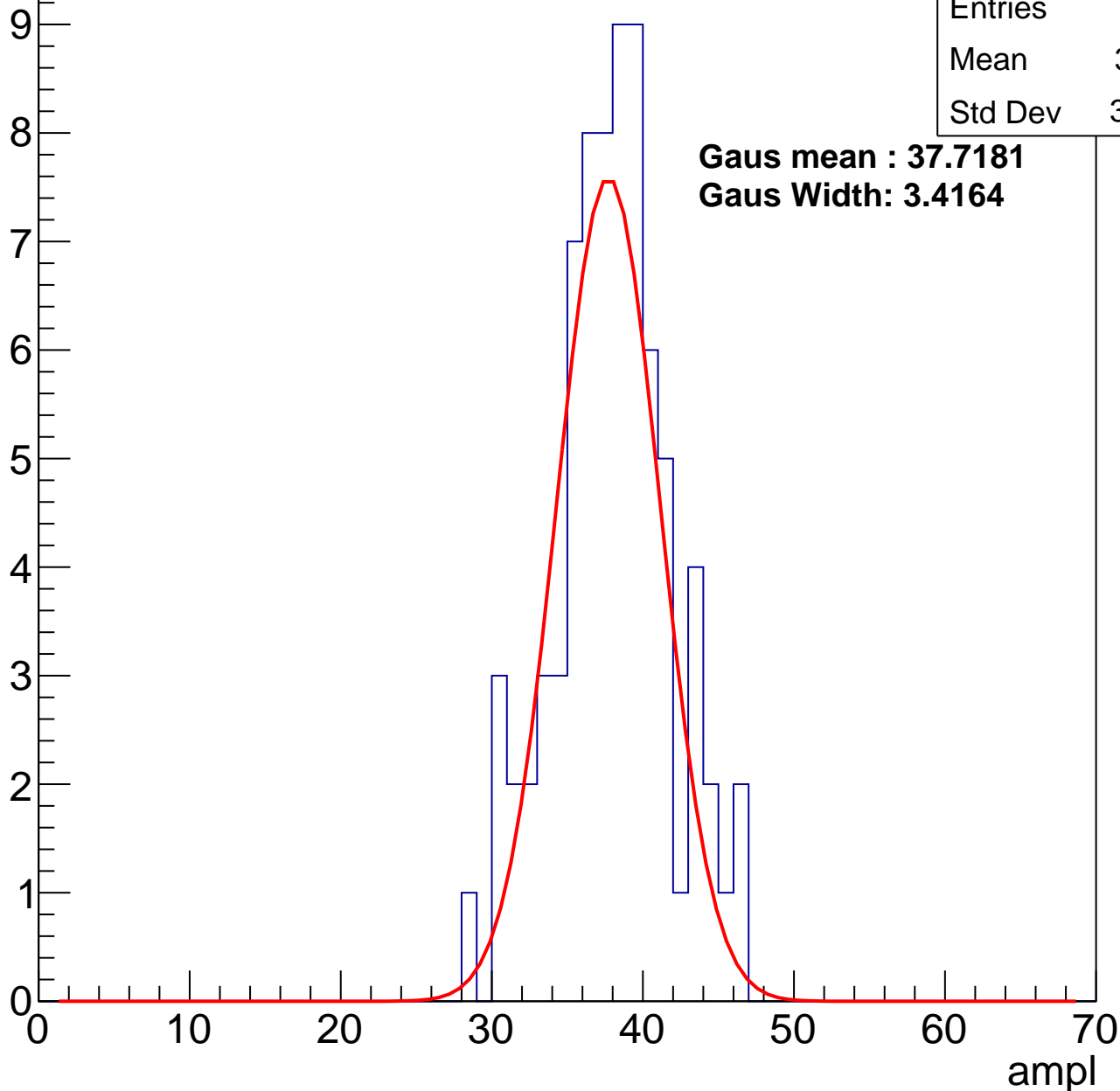
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	37.51
Std Dev	3.827

**Gaus mean : 37.7181**

**Gaus Width: 3.4164**



# B0L102S, U6-ch49, adc2

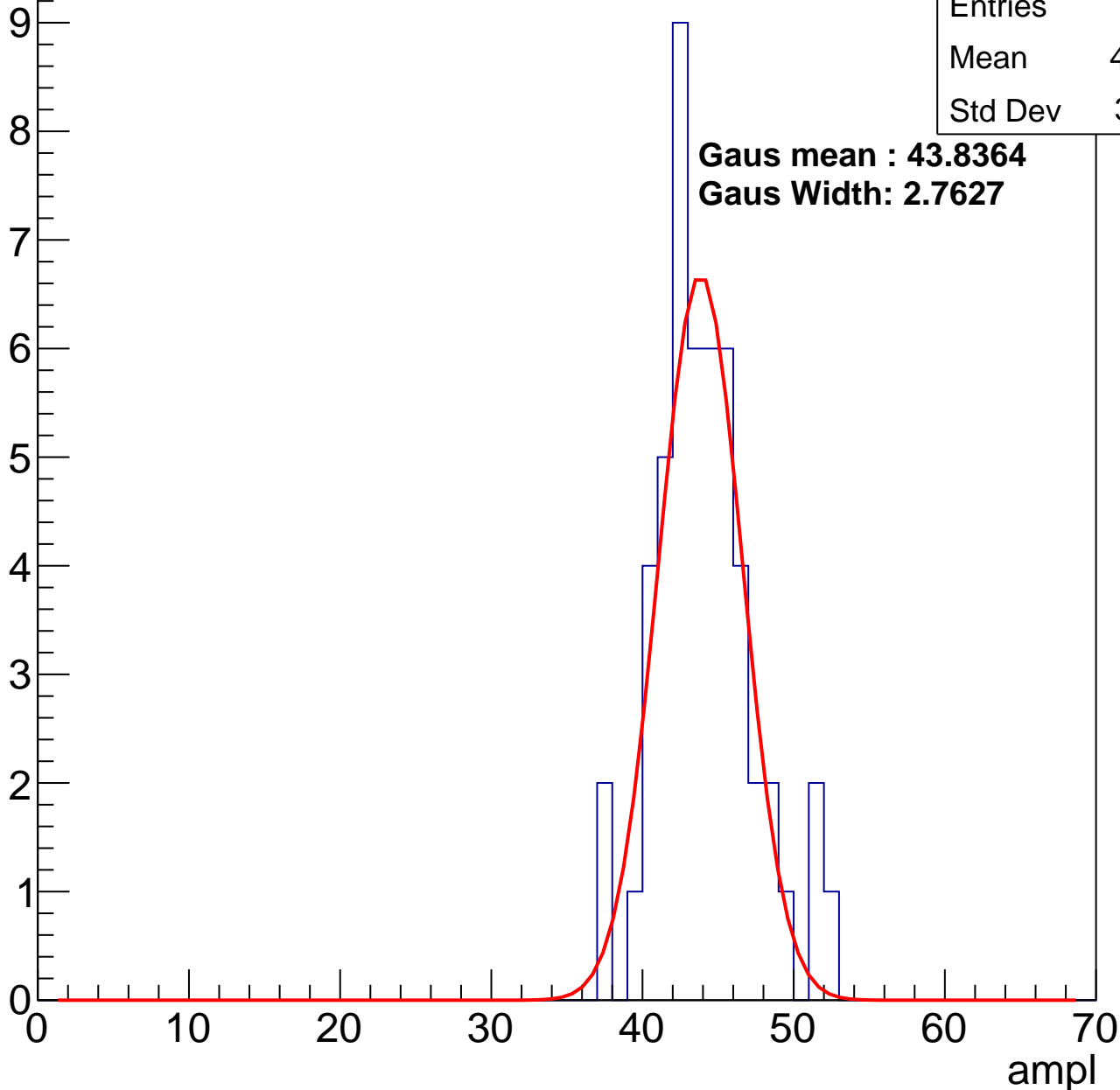
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	43.63
Std Dev	3.211

**Gaus mean : 43.8364**

**Gaus Width: 2.7627**

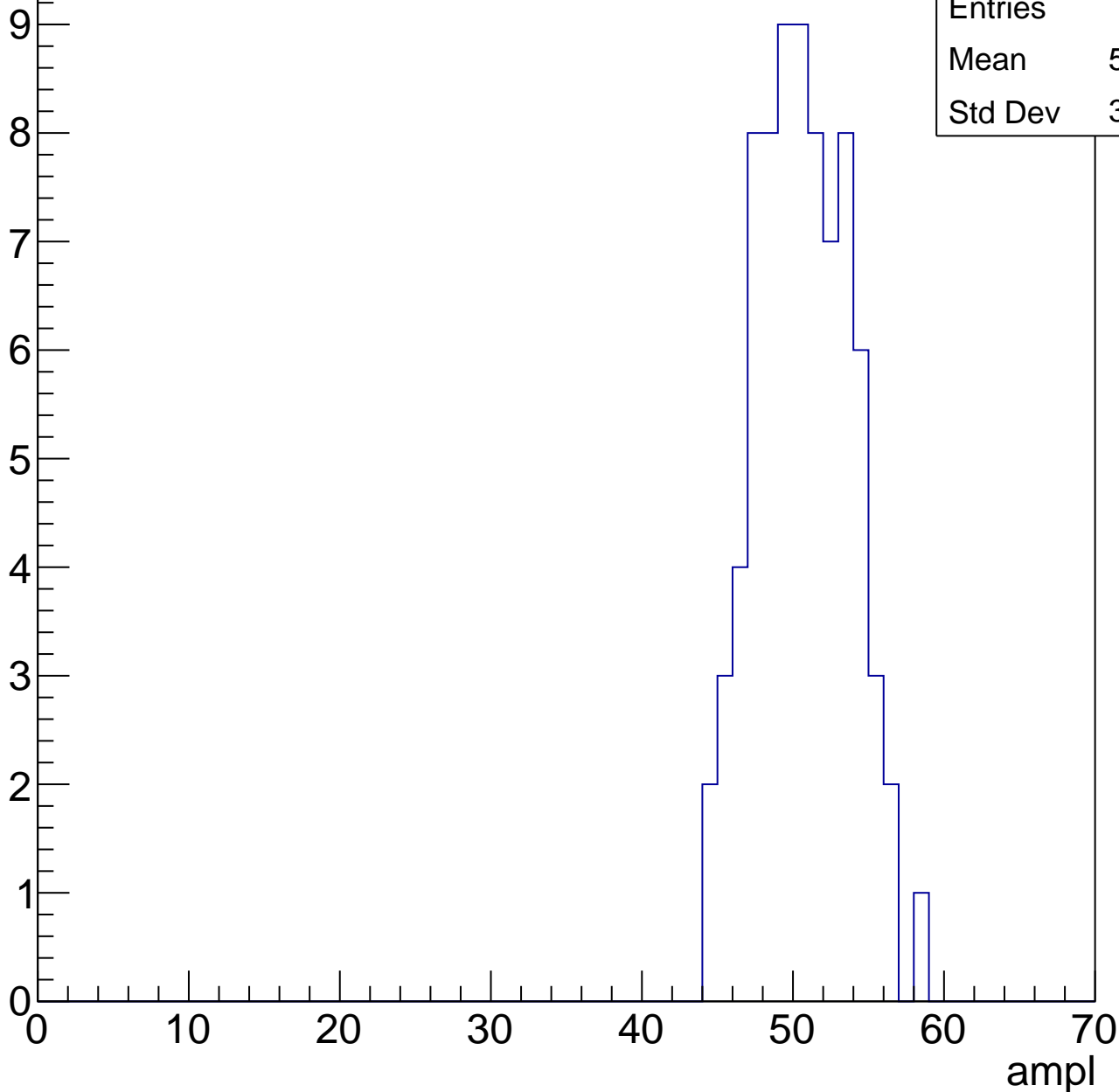


# B0L102S, U6-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

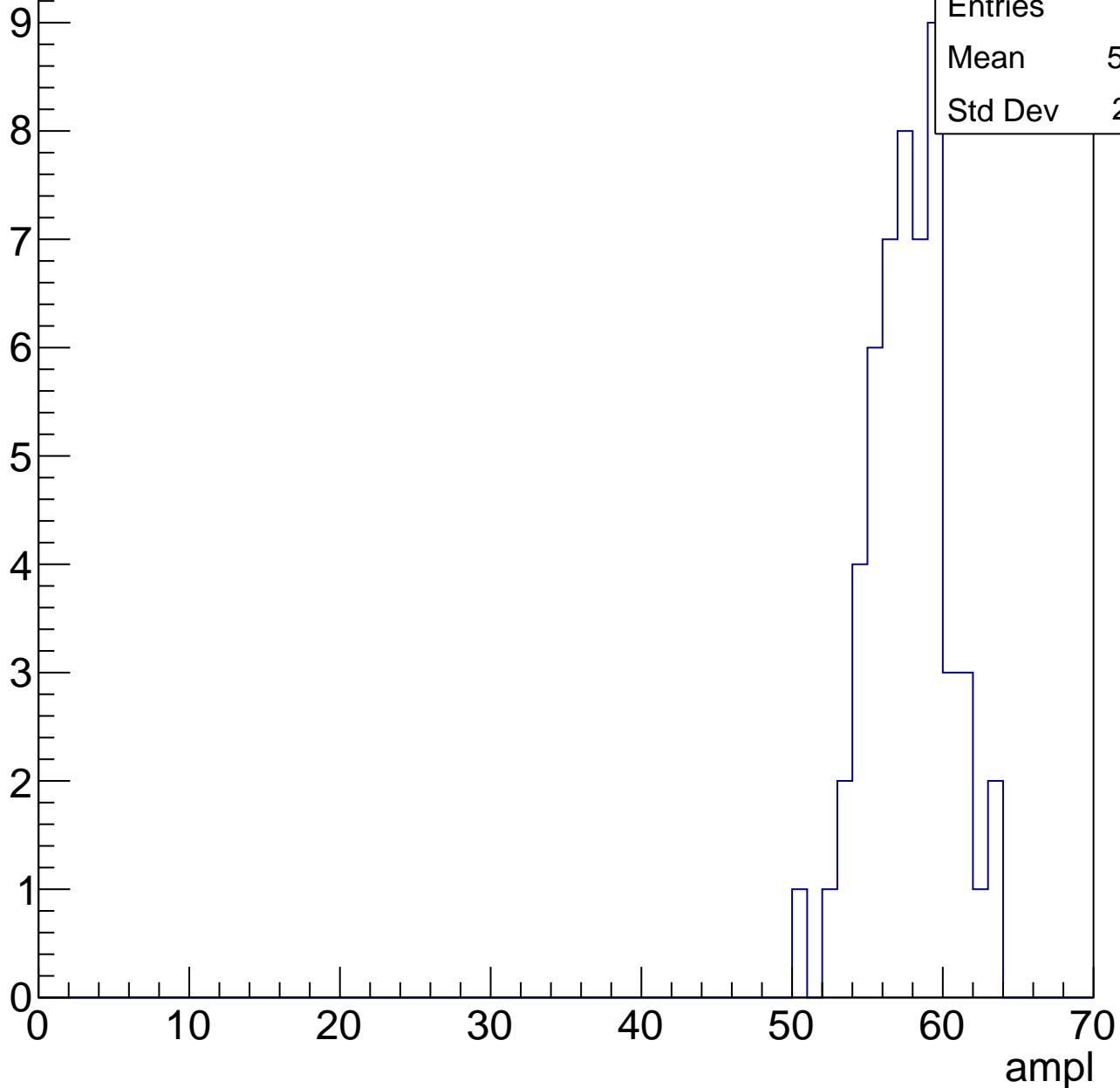
Entries	78
Mean	50.17
Std Dev	3.074



# B0L102S, U6-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



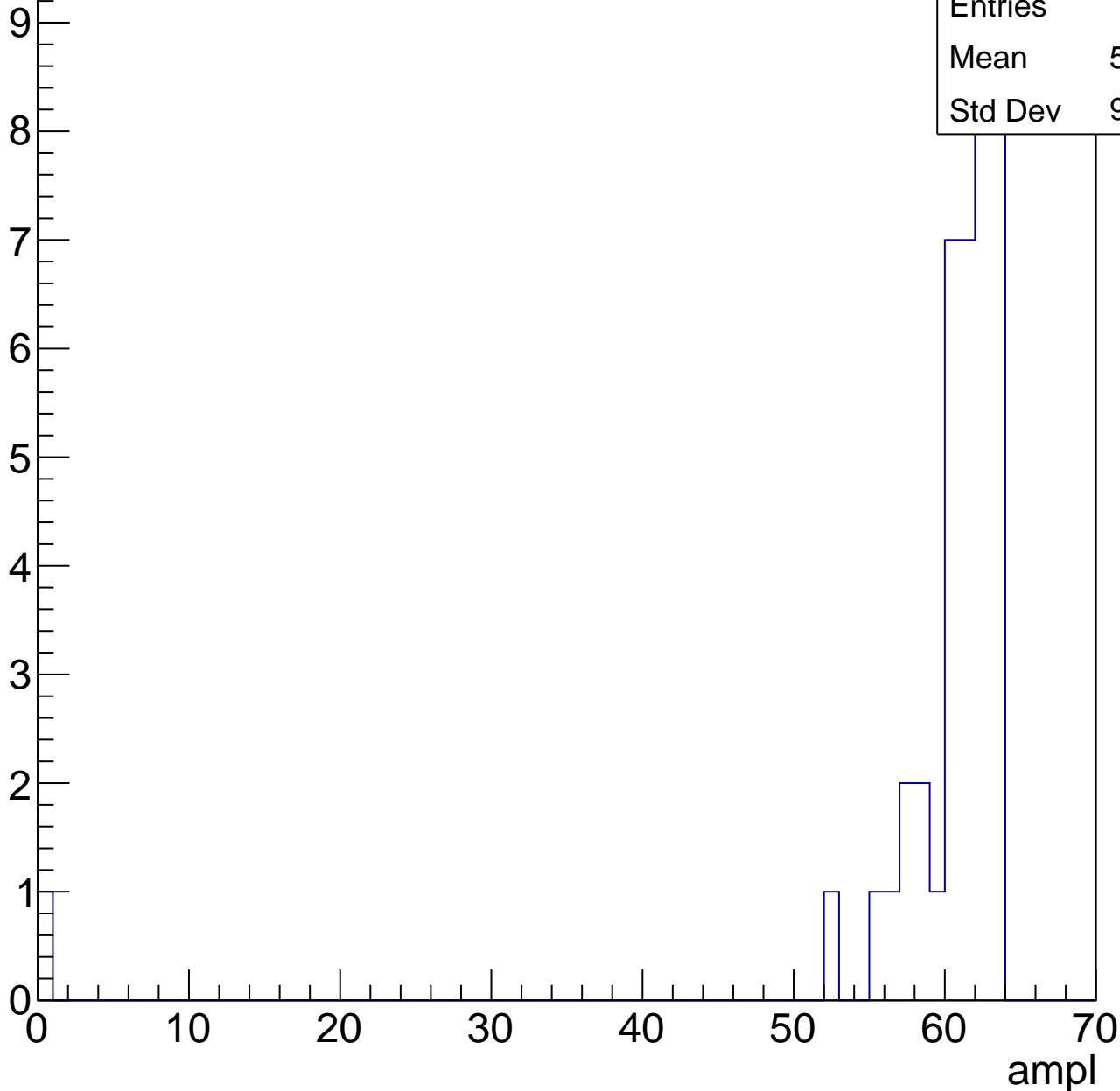
Entries	54
Mean	57.22
Std Dev	2.671

# B0L102S, U6-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	59.02
Std Dev	9.758



# B0L102S, U6-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L102S, U6-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch50, adc0

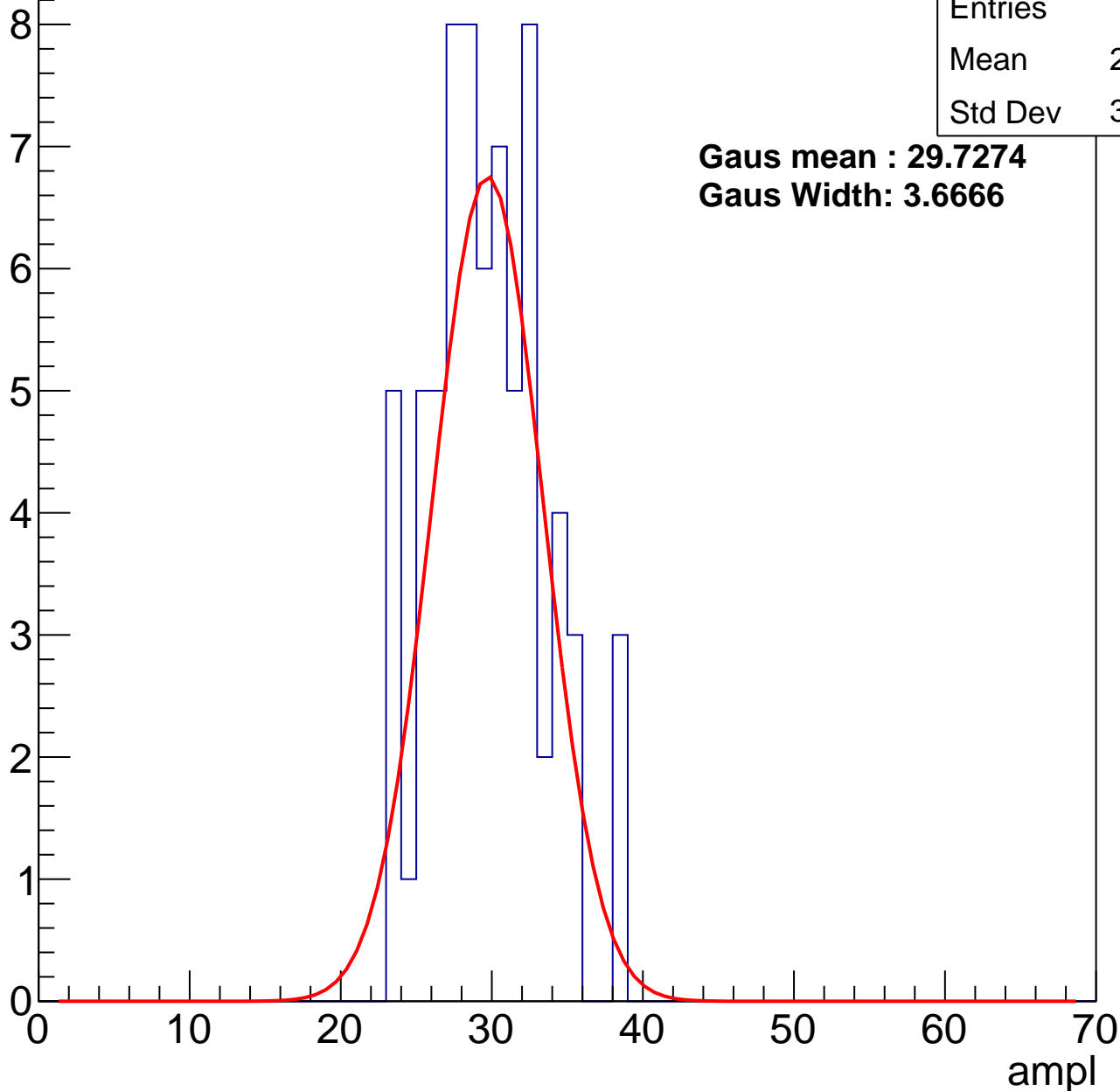
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	29.29
Std Dev	3.677

**Gaus mean : 29.7274**

**Gaus Width: 3.6666**



# B0L102S, U6-ch50, adc1

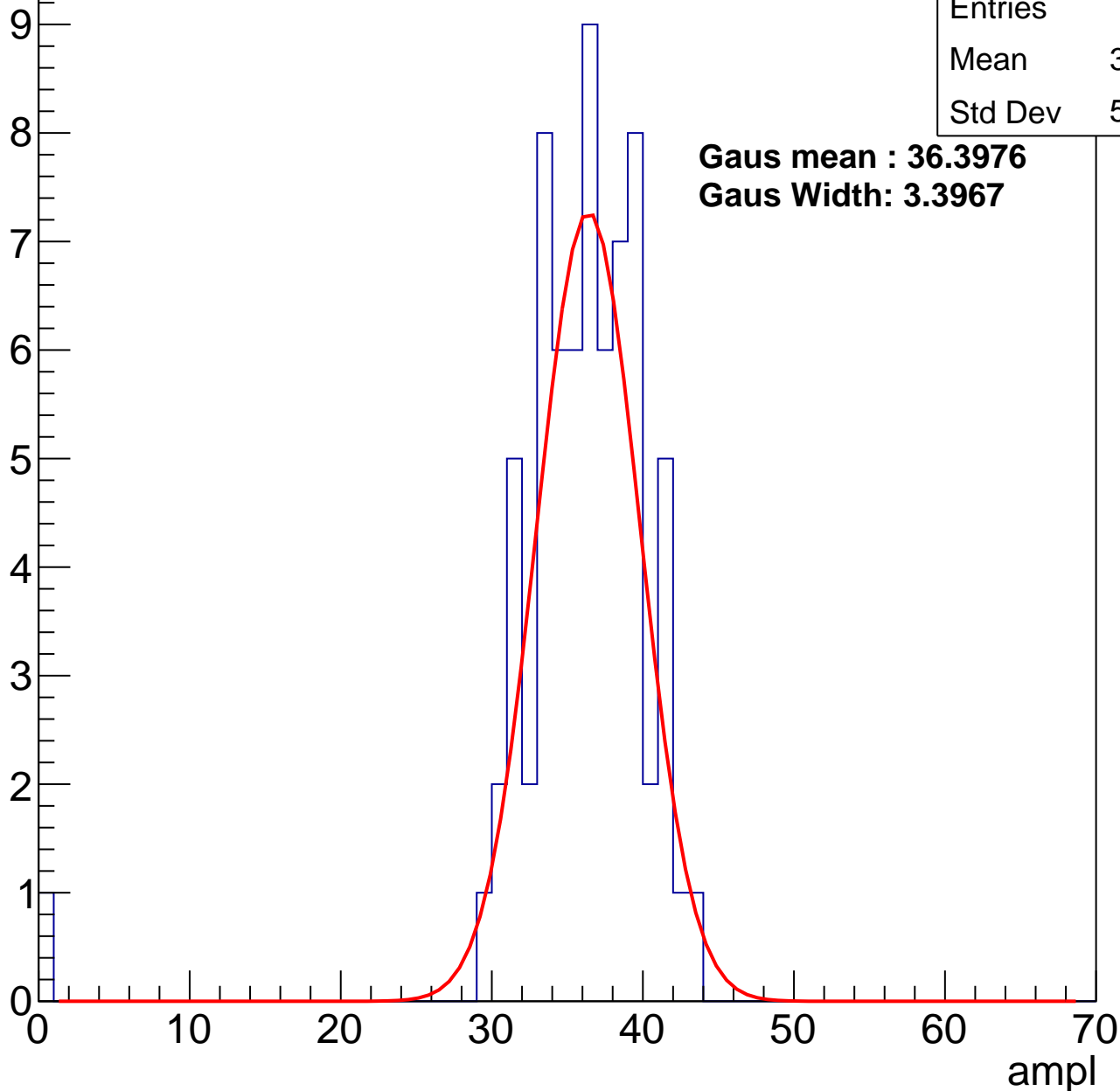
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	35.43
Std Dev	5.347

**Gaus mean : 36.3976**

**Gaus Width: 3.3967**



# B0L102S, U6-ch50, adc2

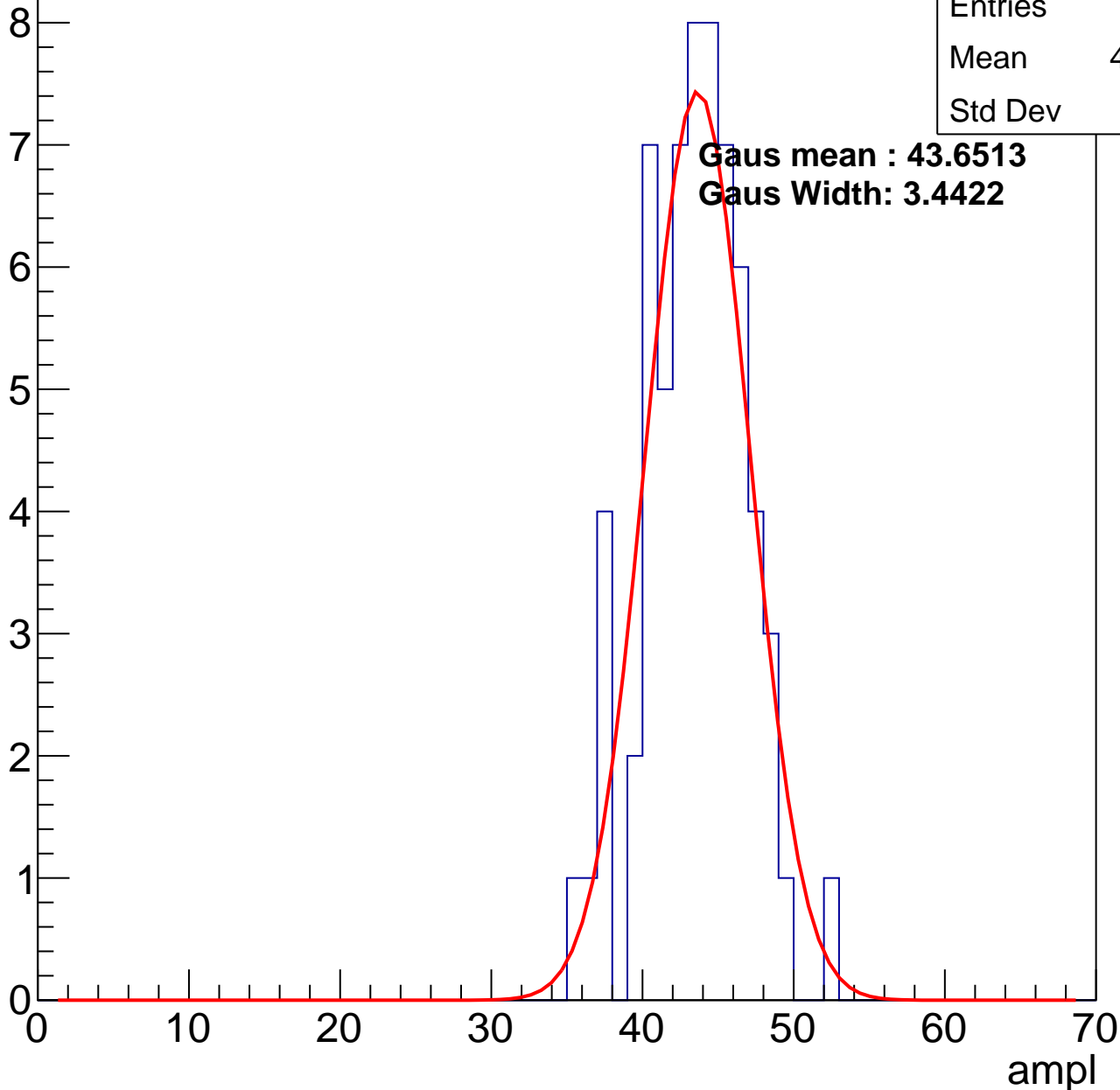
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	43.02
Std Dev	3.34

**Gaus mean : 43.6513**

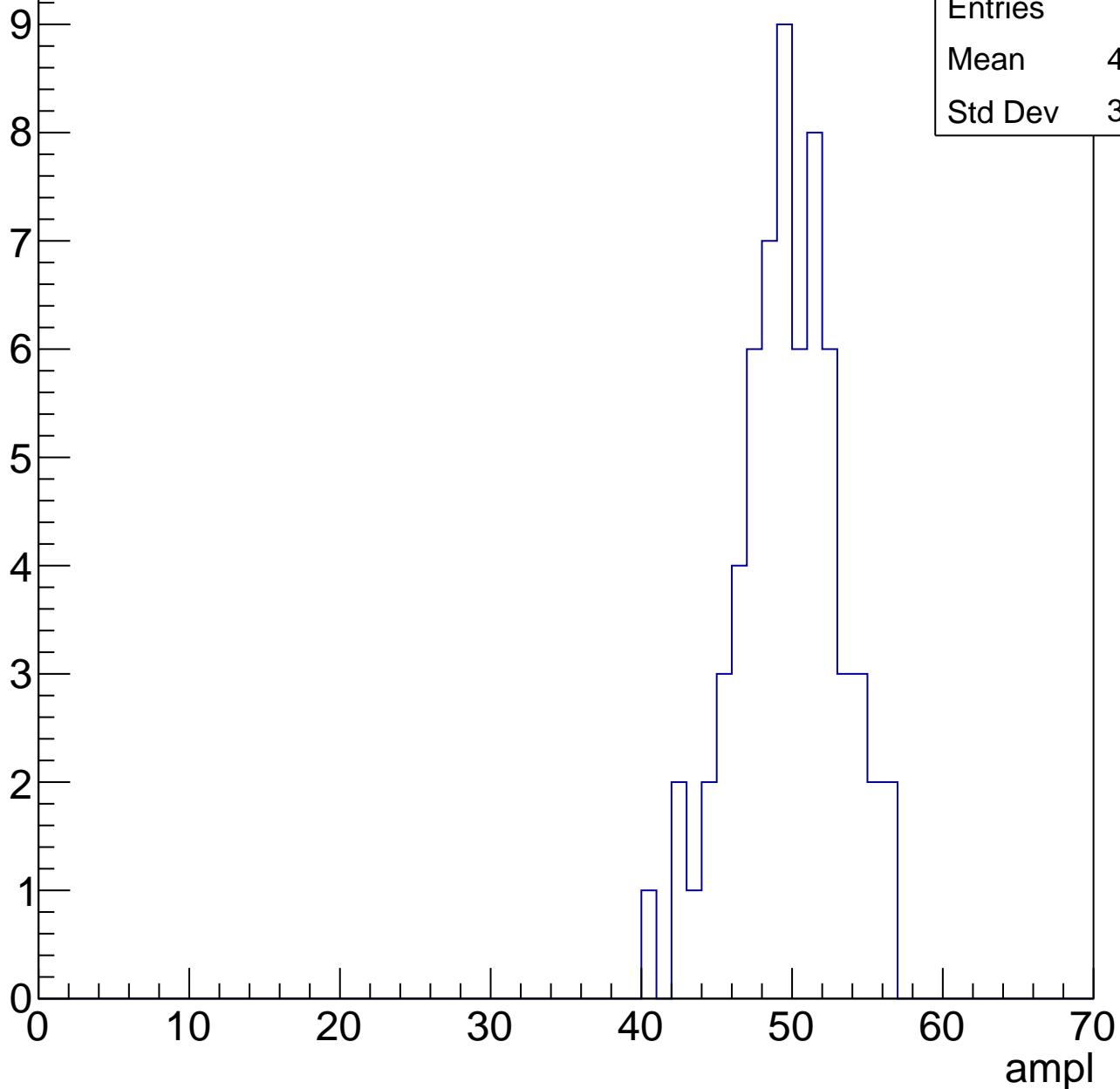
**Gaus Width: 3.4422**



# B0L102S, U6-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

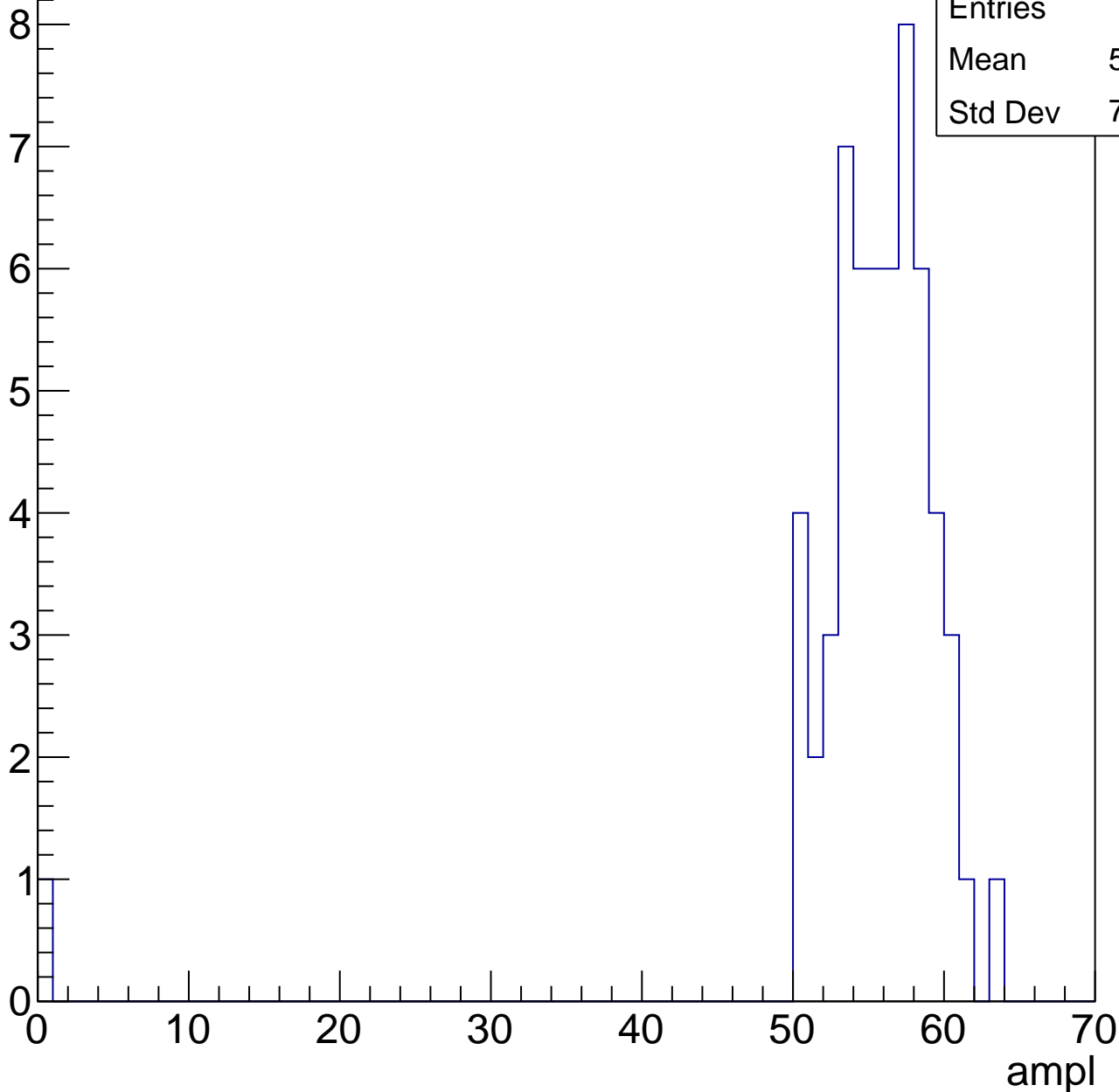


# B0L102S, U6-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	54.53
Std Dev	7.809



# B0L102S, U6-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	60.04
Std Dev	2.361

ampl

0

10

20

30

40

50

60

70

# B0L102S, U6-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

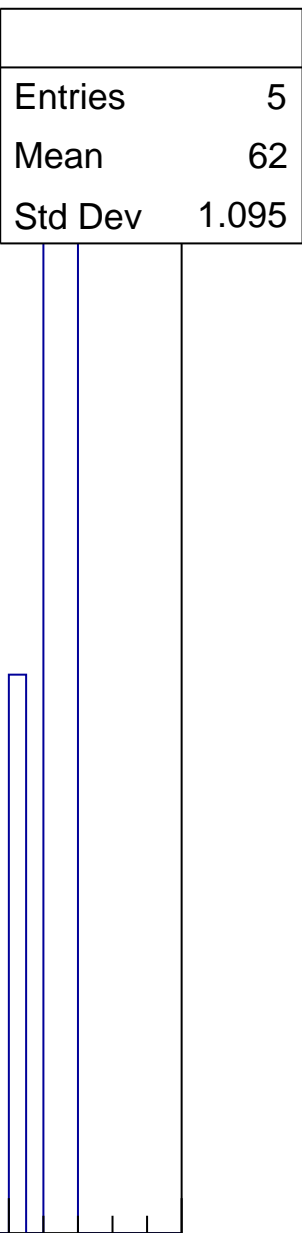
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62
Std Dev	1.095

0 10 20 30 40 50 60 70

ampl

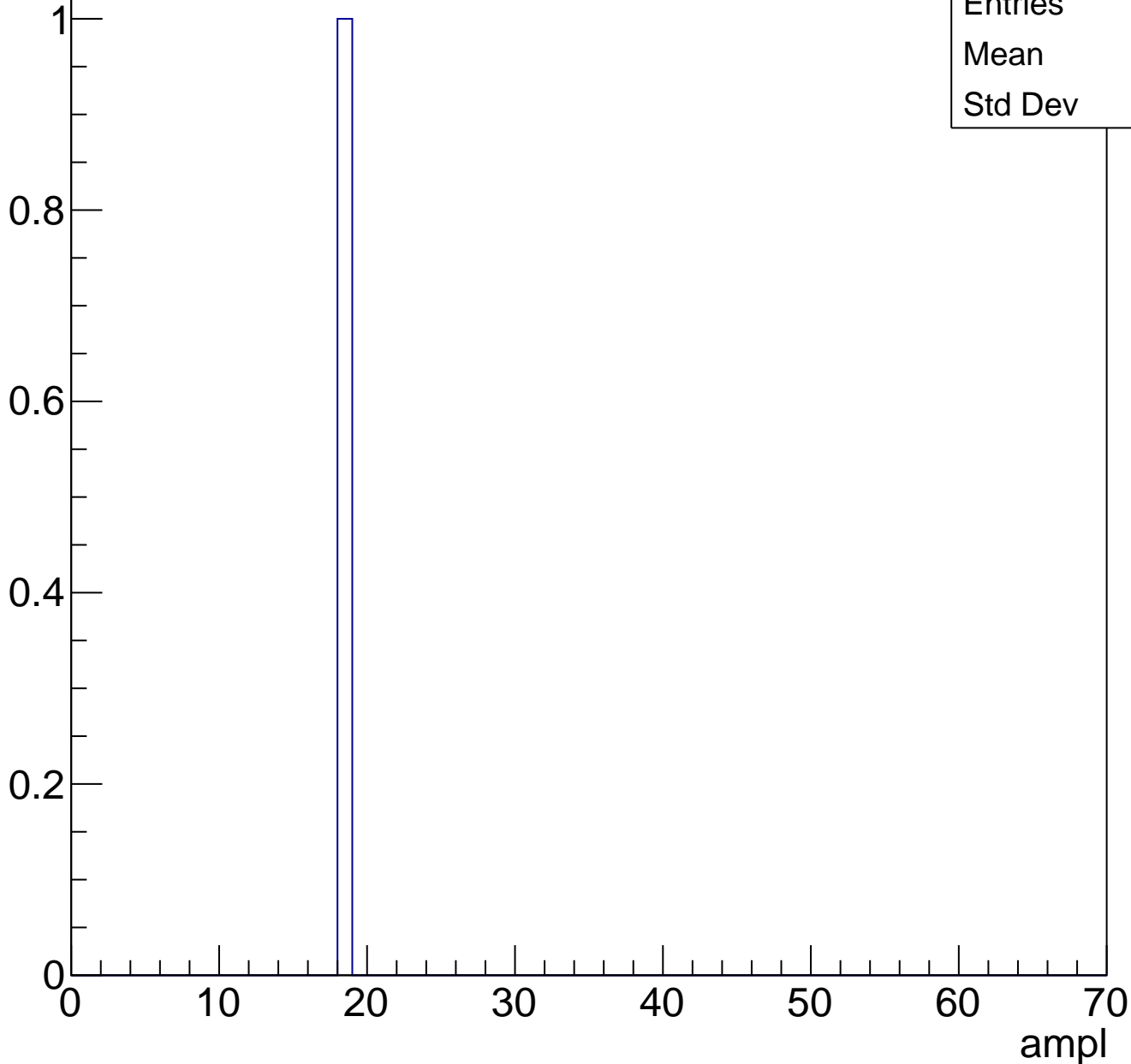




# B0L102S, U6-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch51, adc0

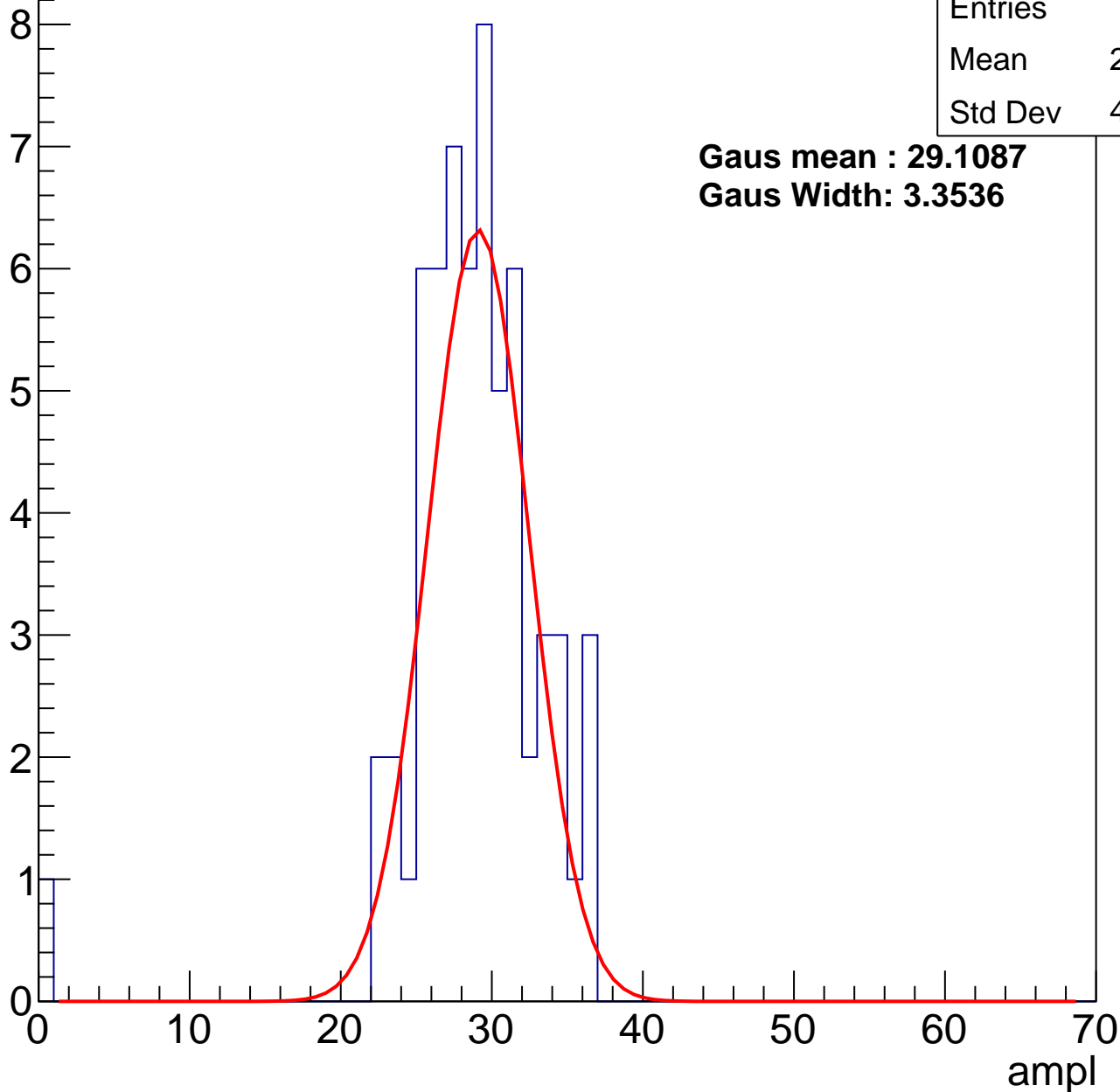
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	28.27
Std Dev	4.984

**Gaus mean : 29.1087**

**Gaus Width: 3.3536**



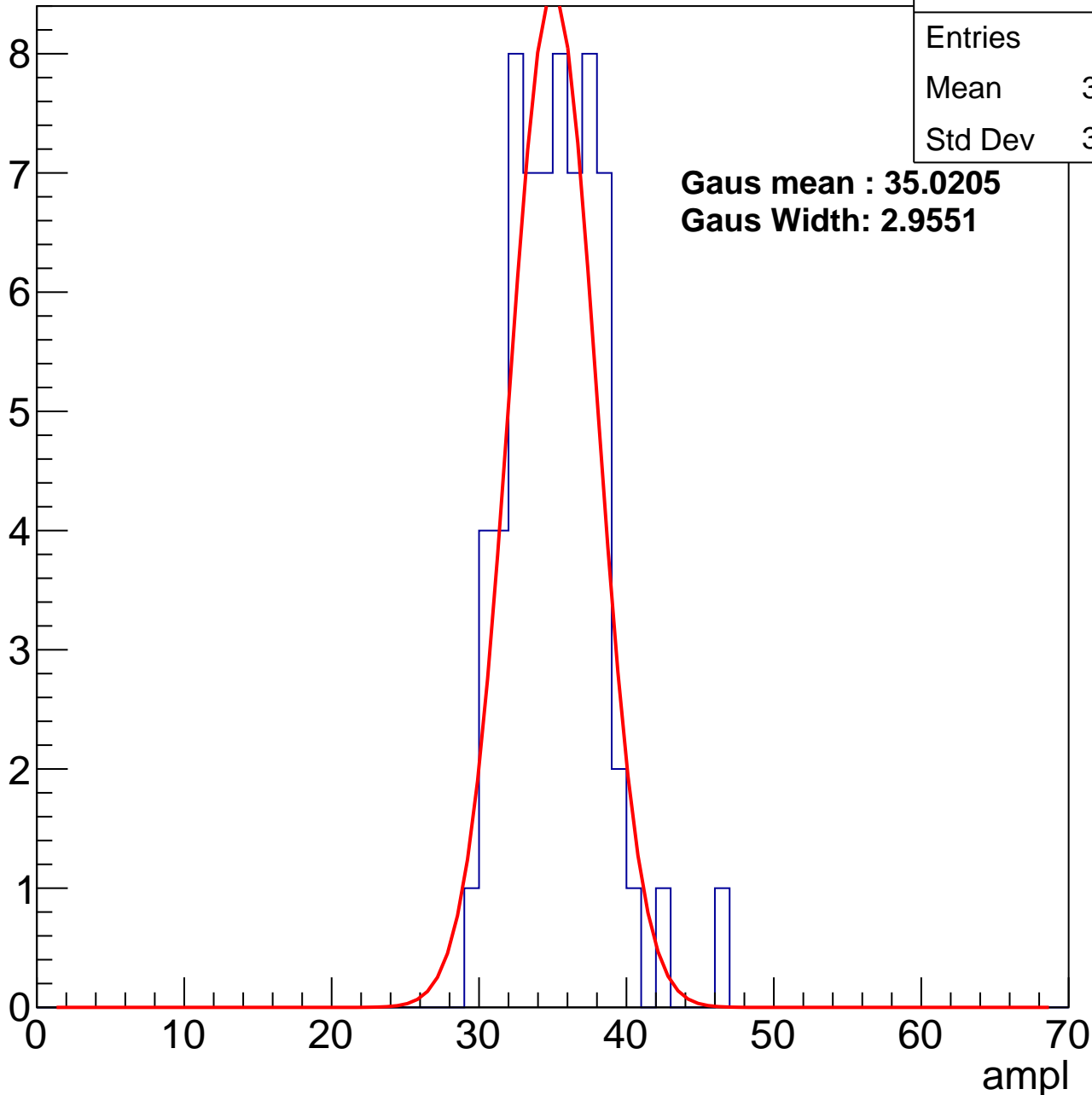
# B0L102S, U6-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	34.82
Std Dev	3.099

**Gaus mean : 35.0205**  
**Gaus Width: 2.9551**



# B0L102S, U6-ch51, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	65
Mean	40.94
Std Dev	5.423

**Gaus mean : 41.9435**

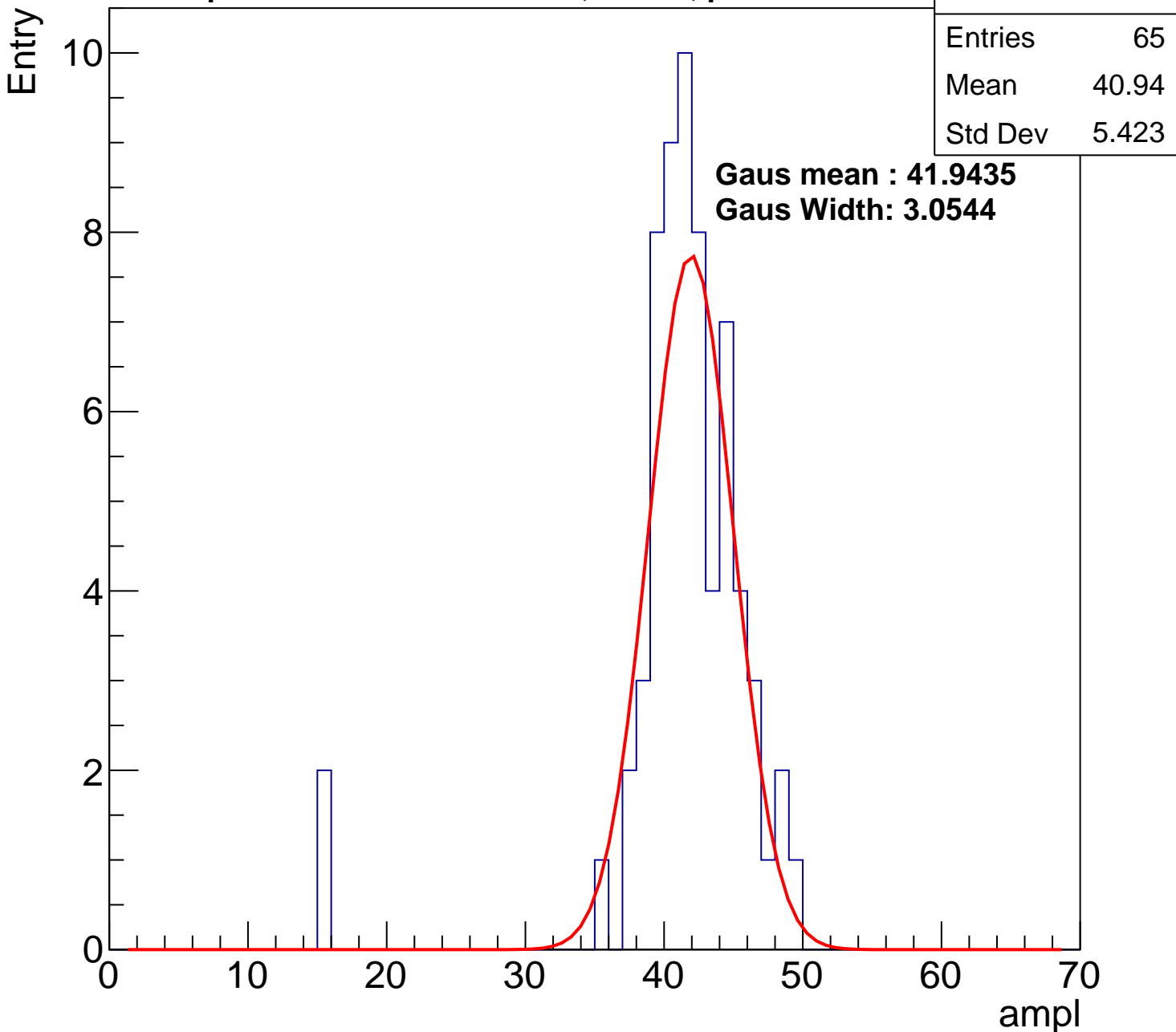
**Gaus Width: 3.0544**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

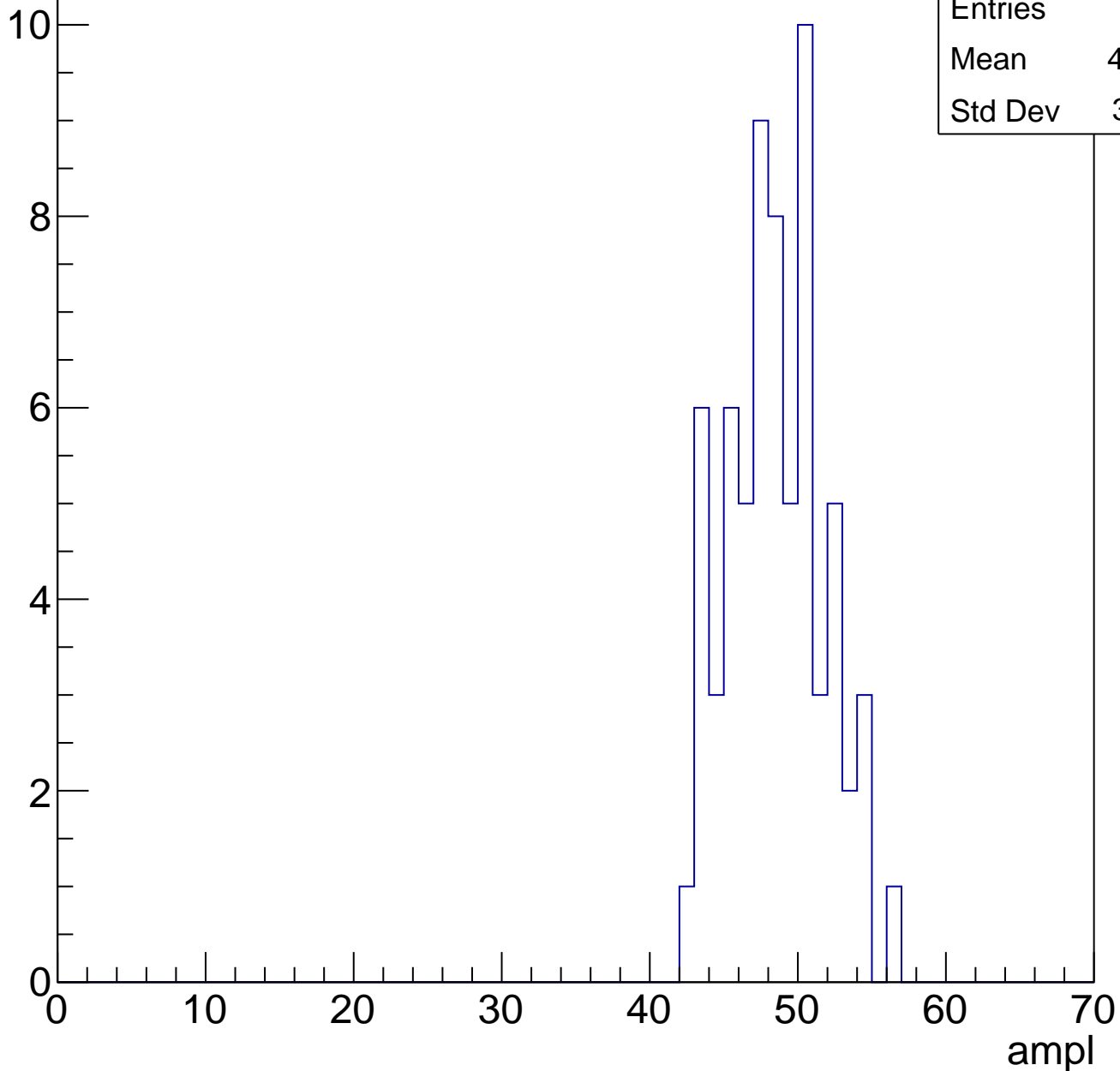


# B0L102S, U6-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	67
Mean	48.07
Std Dev	3.211

Entry

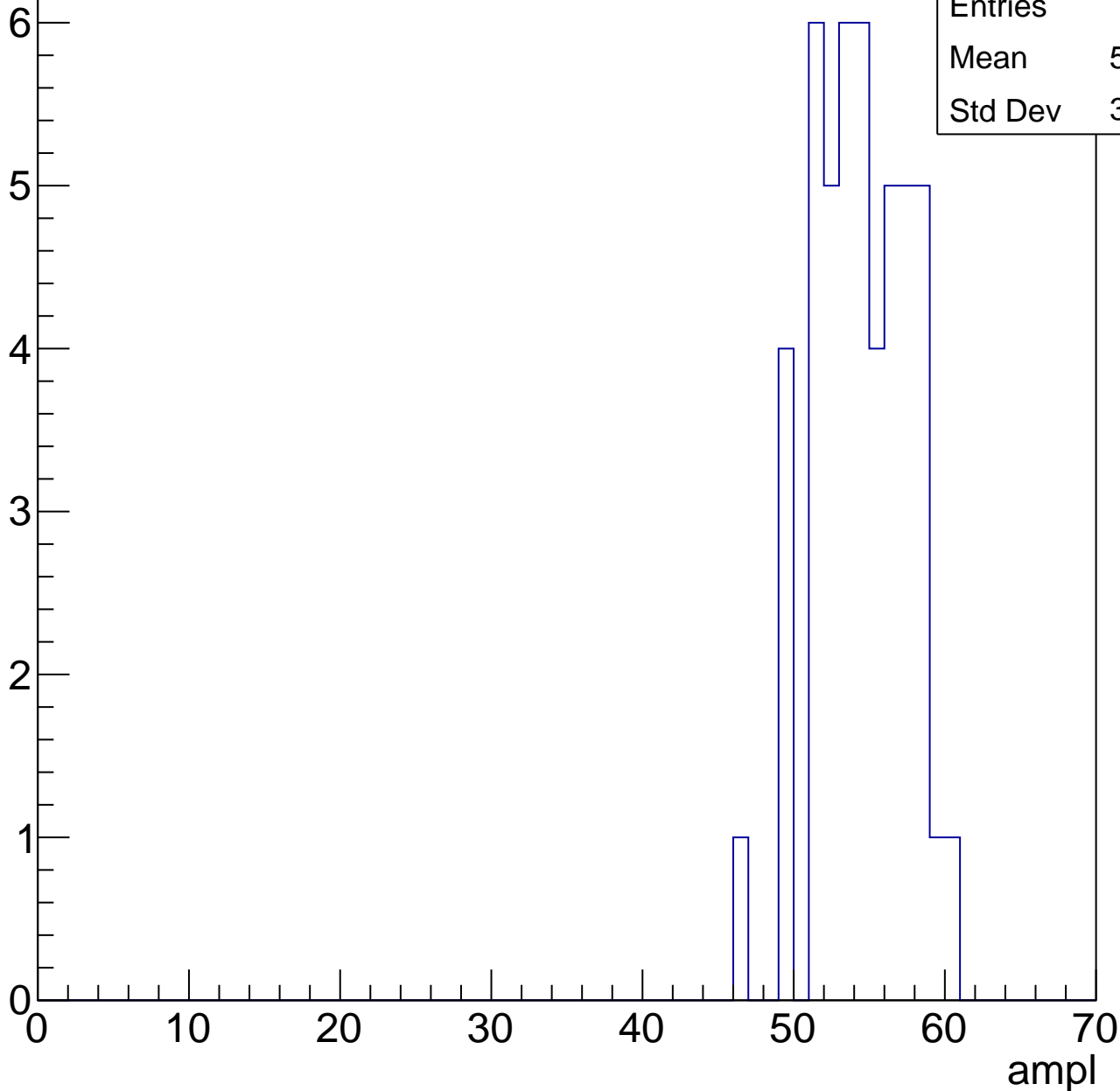


# B0L102S, U6-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	53.96
Std Dev	3.044

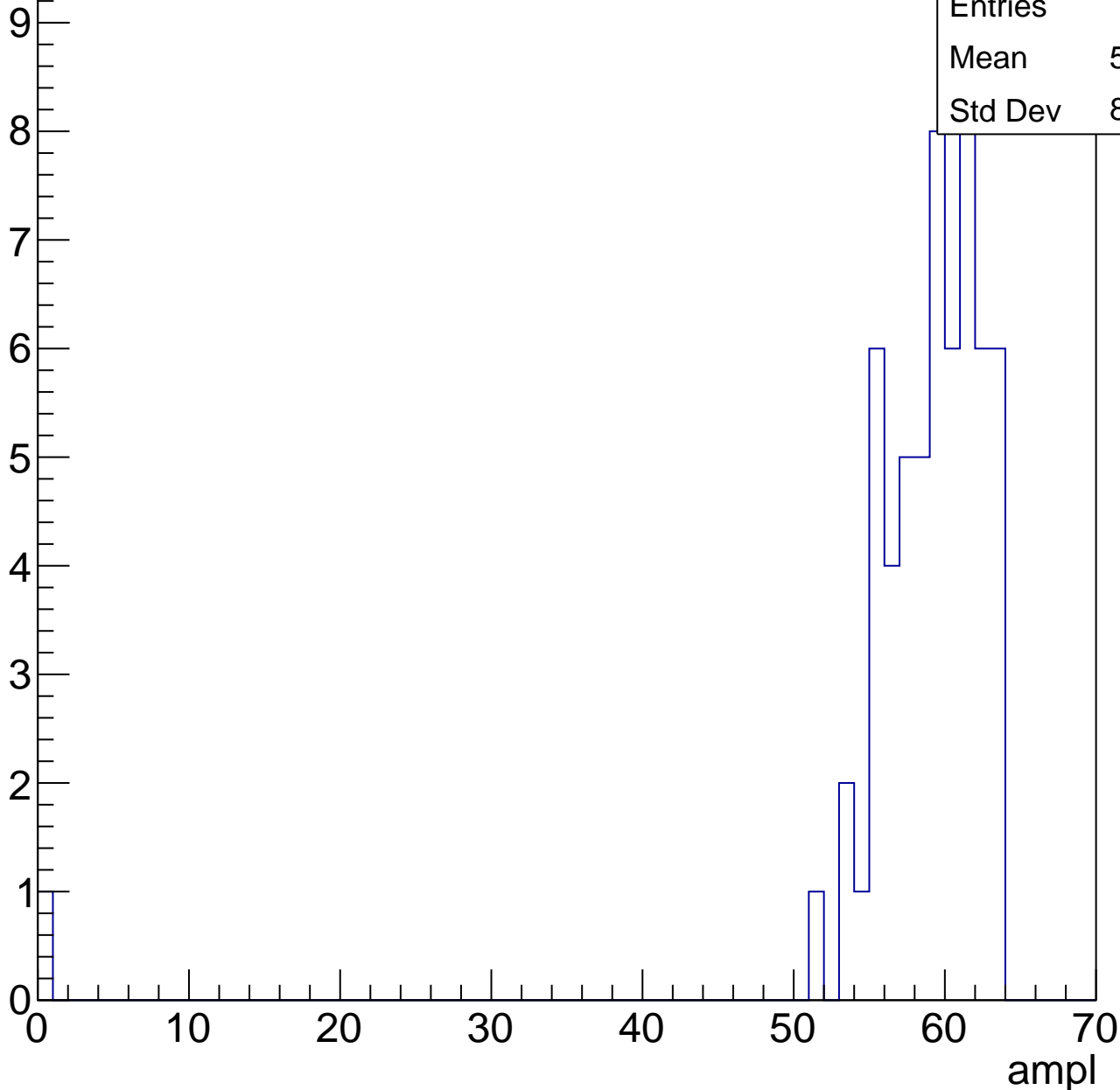


# B0L102S, U6-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

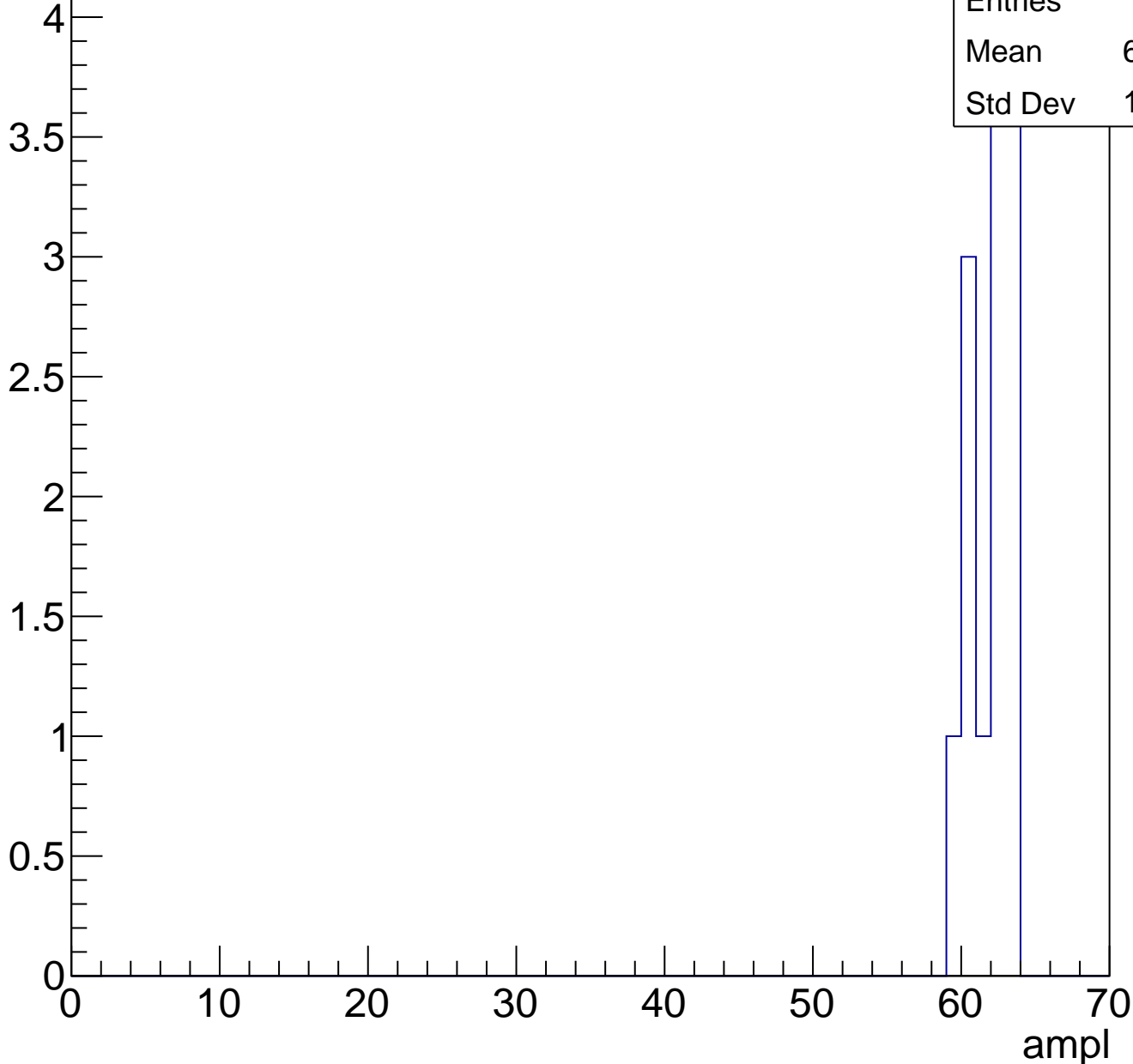
Entries	60
Mean	57.85
Std Dev	8.074



# B0L102S, U6-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch52, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	71
Mean	26.86
Std Dev	3.62

**Gaus mean : 27.1288**

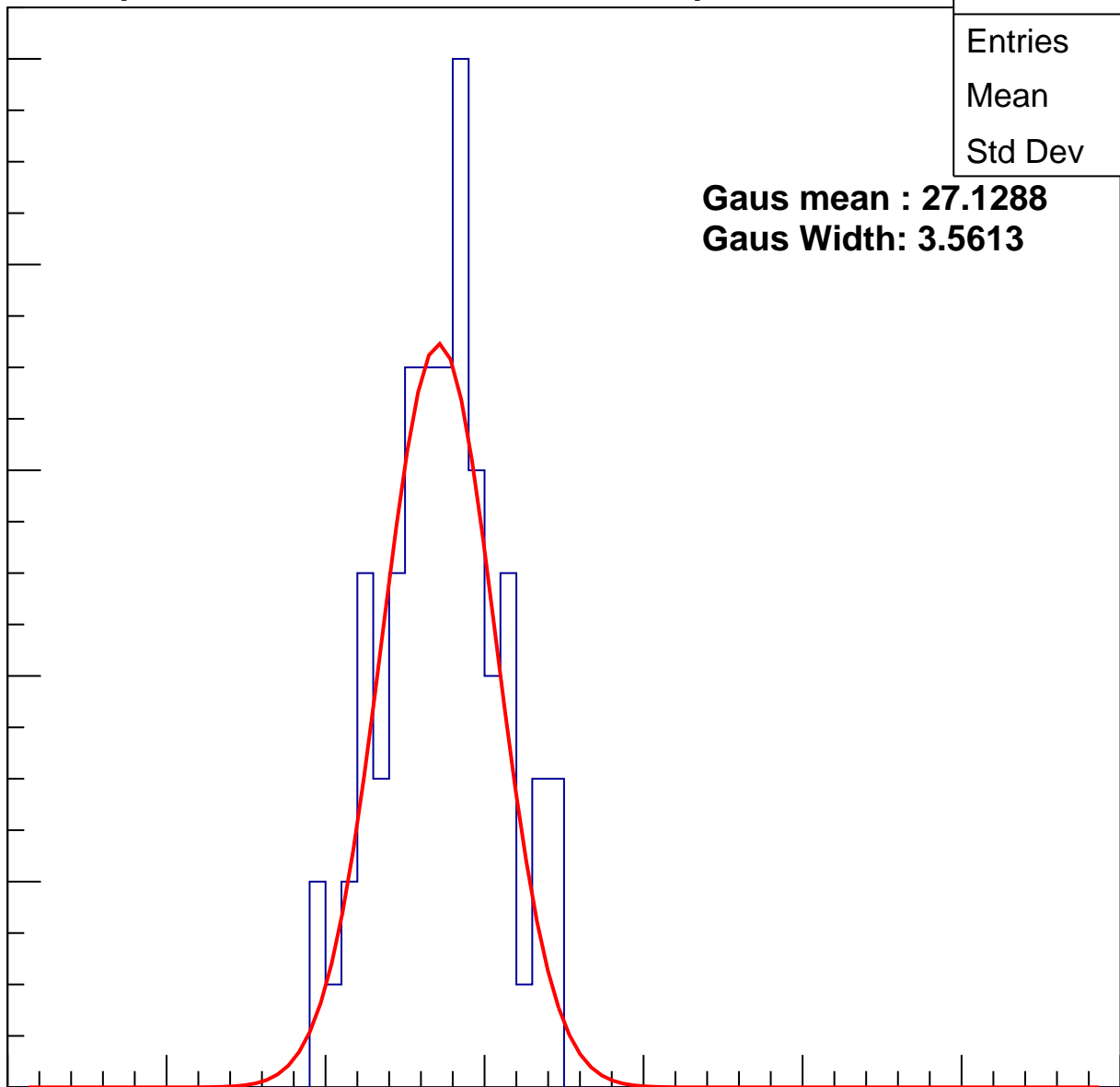
**Gaus Width: 3.5613**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch52, adc1

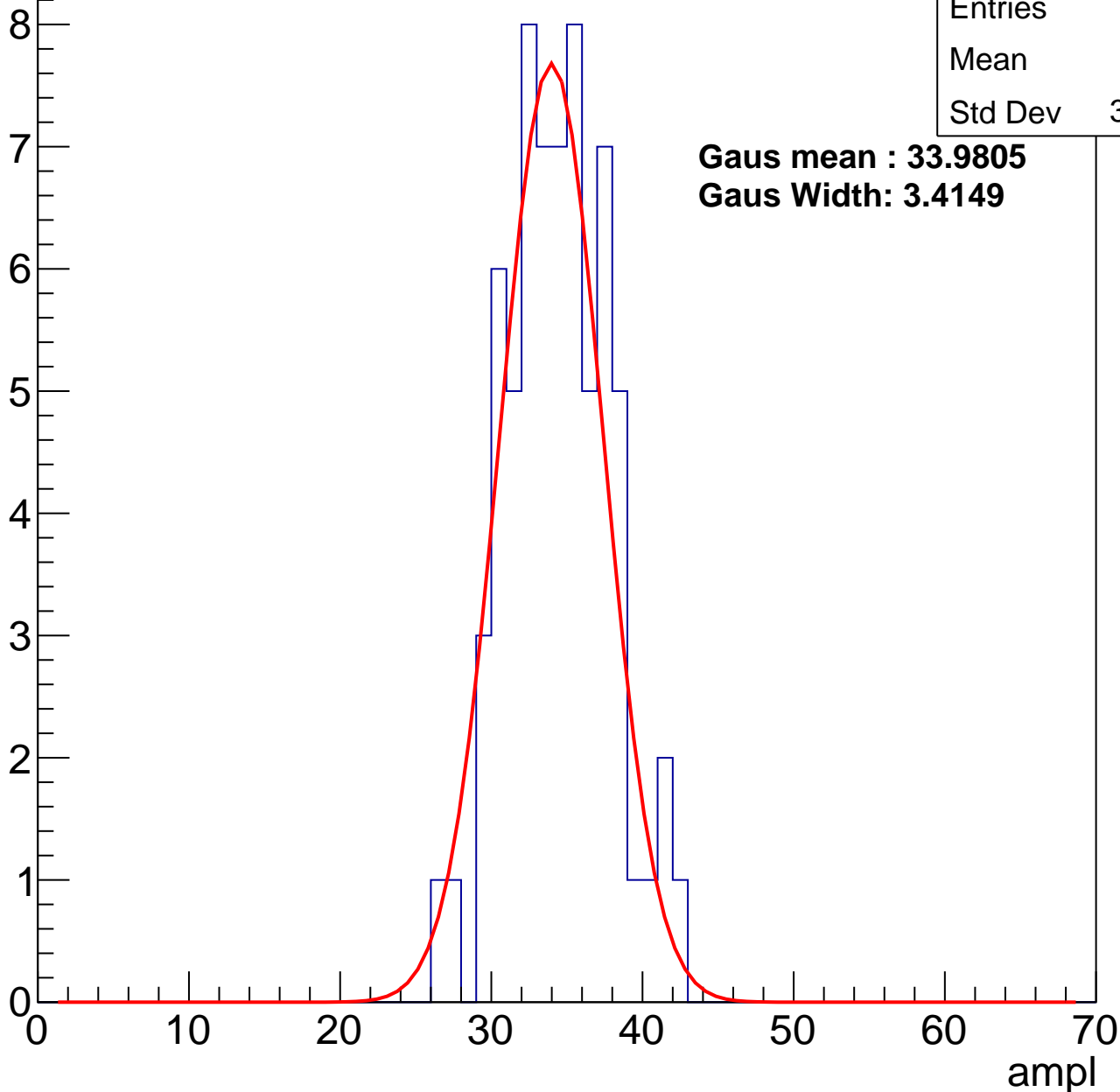
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	34
Std Dev	3.348

**Gaus mean : 33.9805**

**Gaus Width: 3.4149**



# B0L102S, U6-ch52, adc2

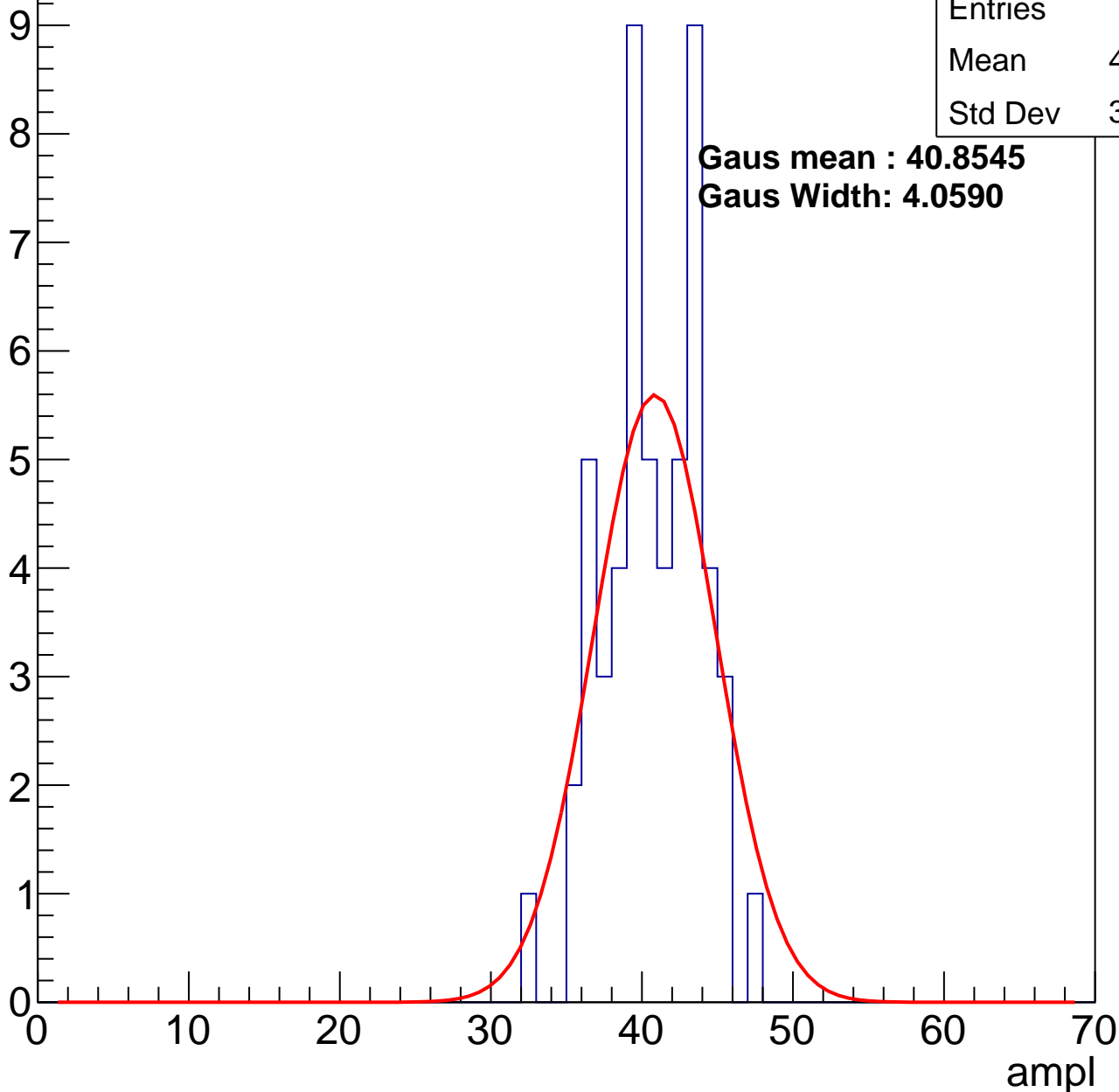
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	40.27
Std Dev	3.119

**Gaus mean : 40.8545**

**Gaus Width: 4.0590**



# B0L102S, U6-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

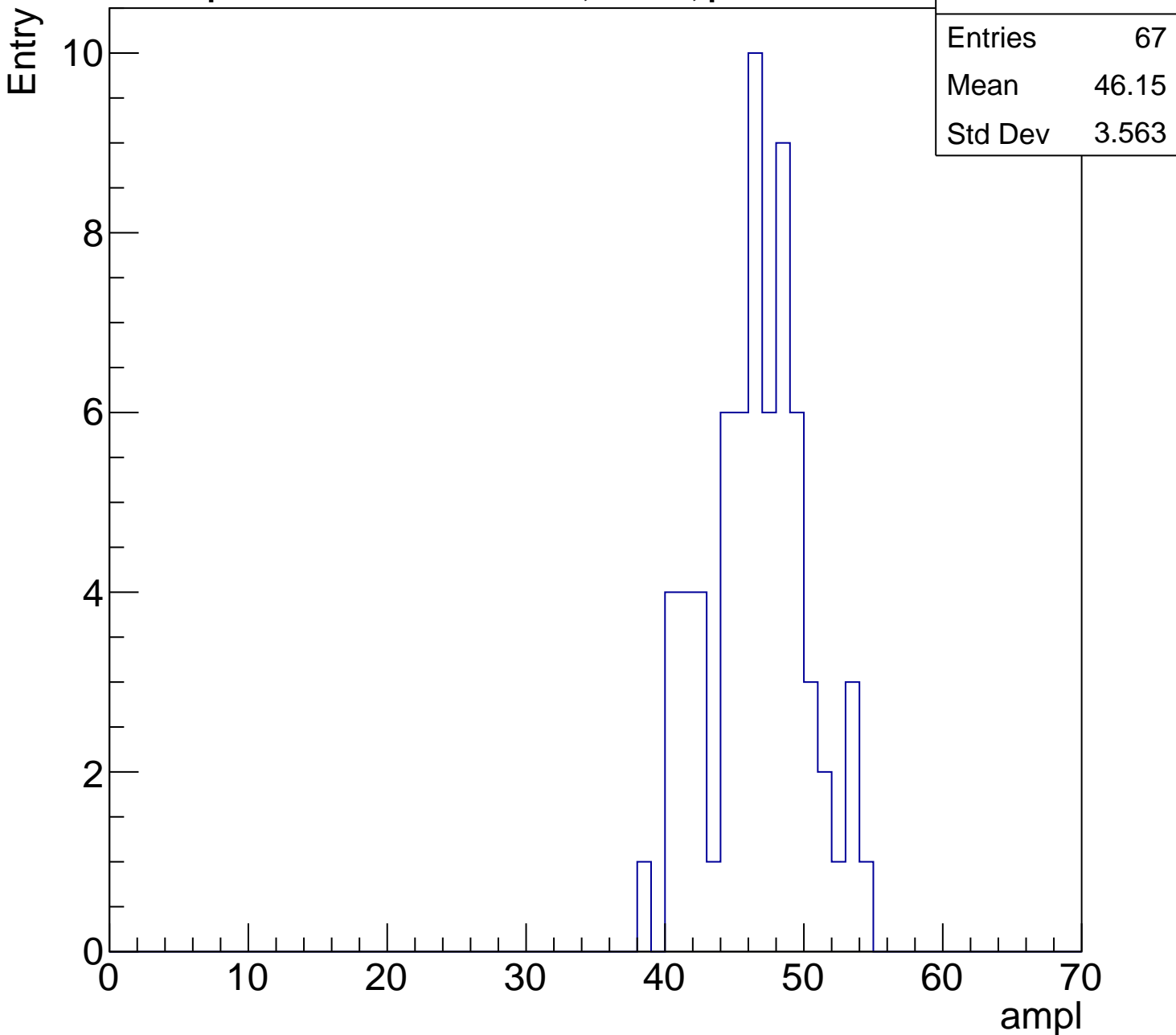
Entries	67
Mean	46.15
Std Dev	3.563

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

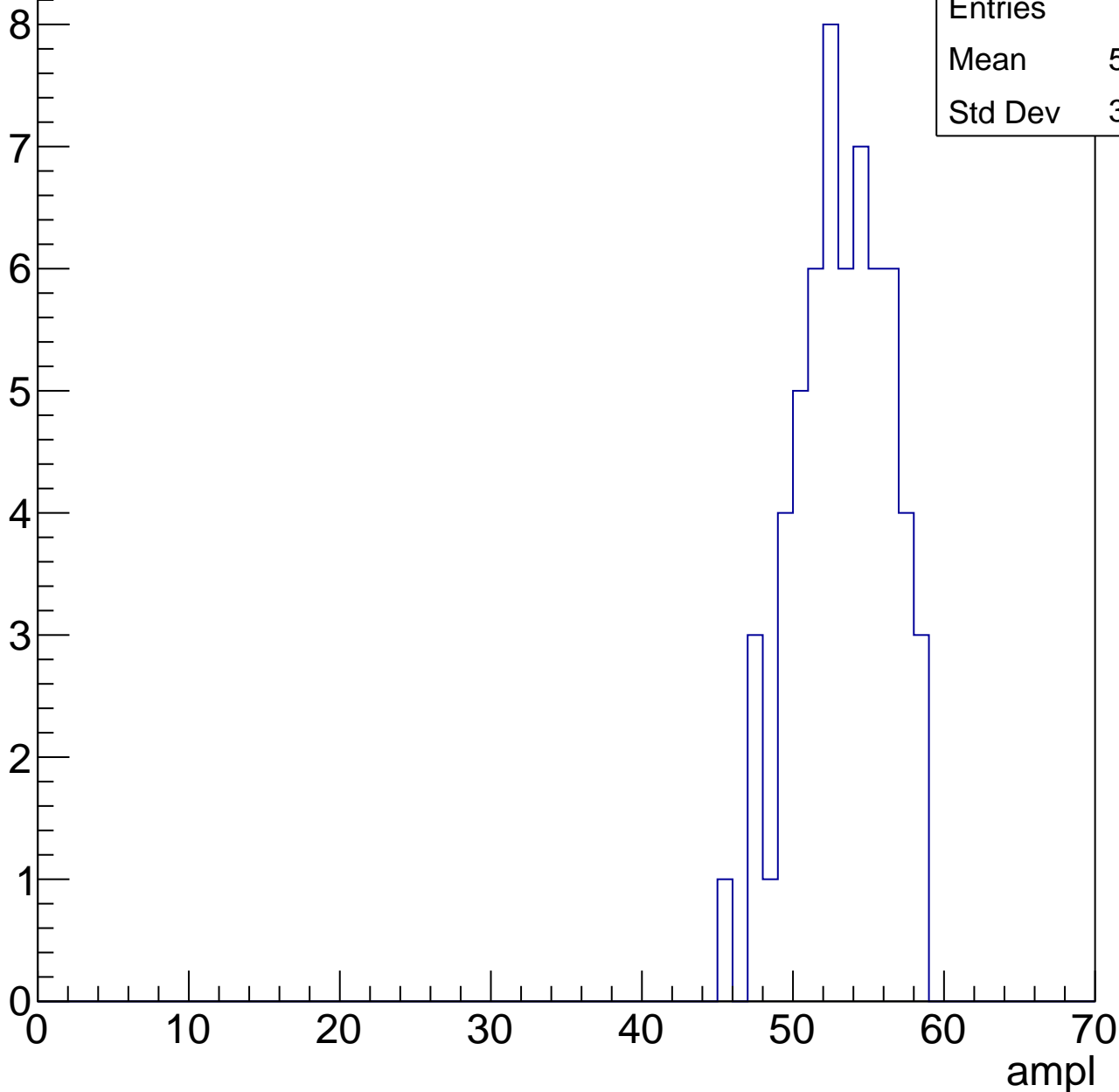


# B0L102S, U6-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	52.77
Std Dev	3.052



# B0L102S, U6-ch52, adc5

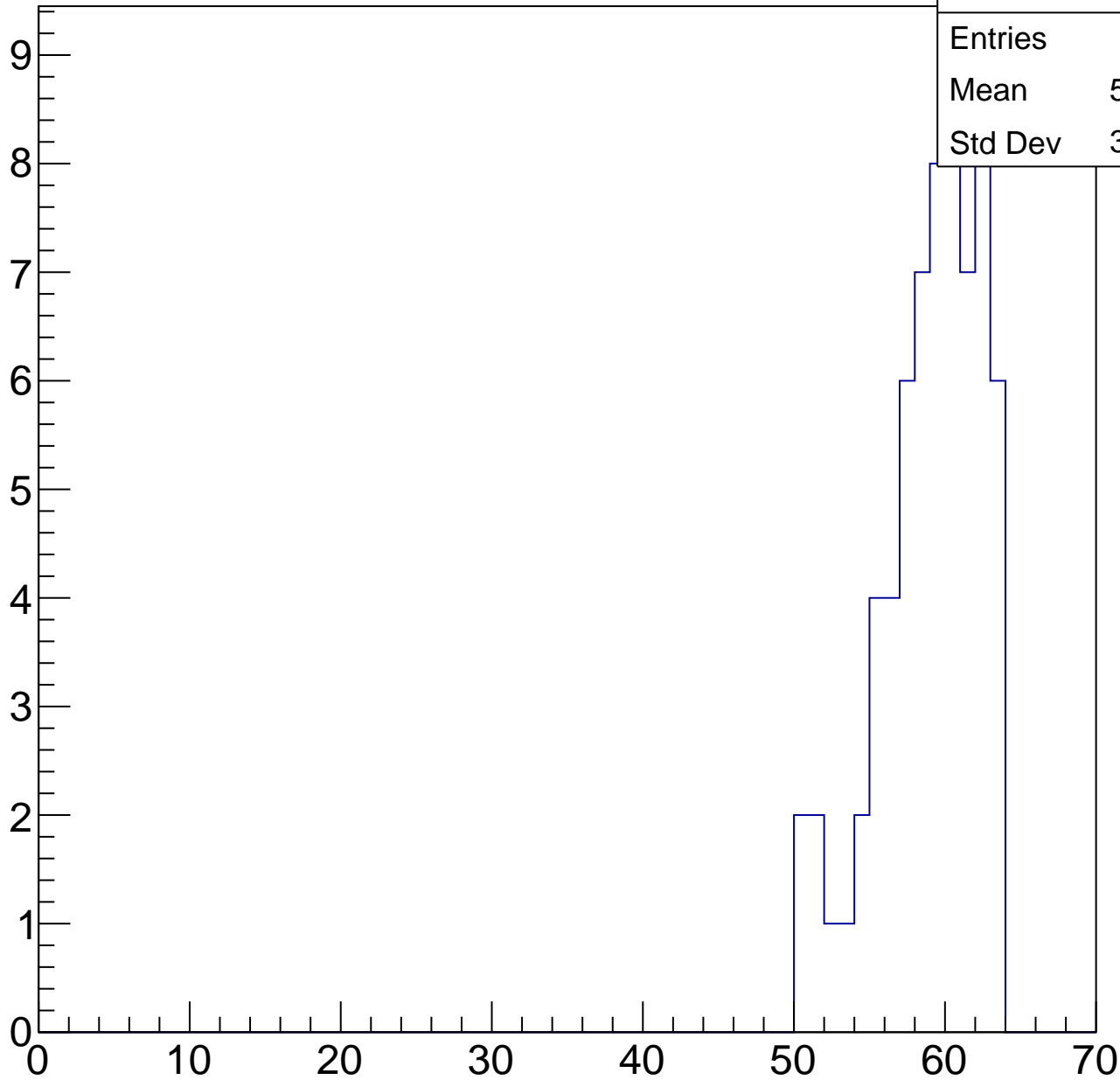
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	67
Mean	58.54
Std Dev	3.356

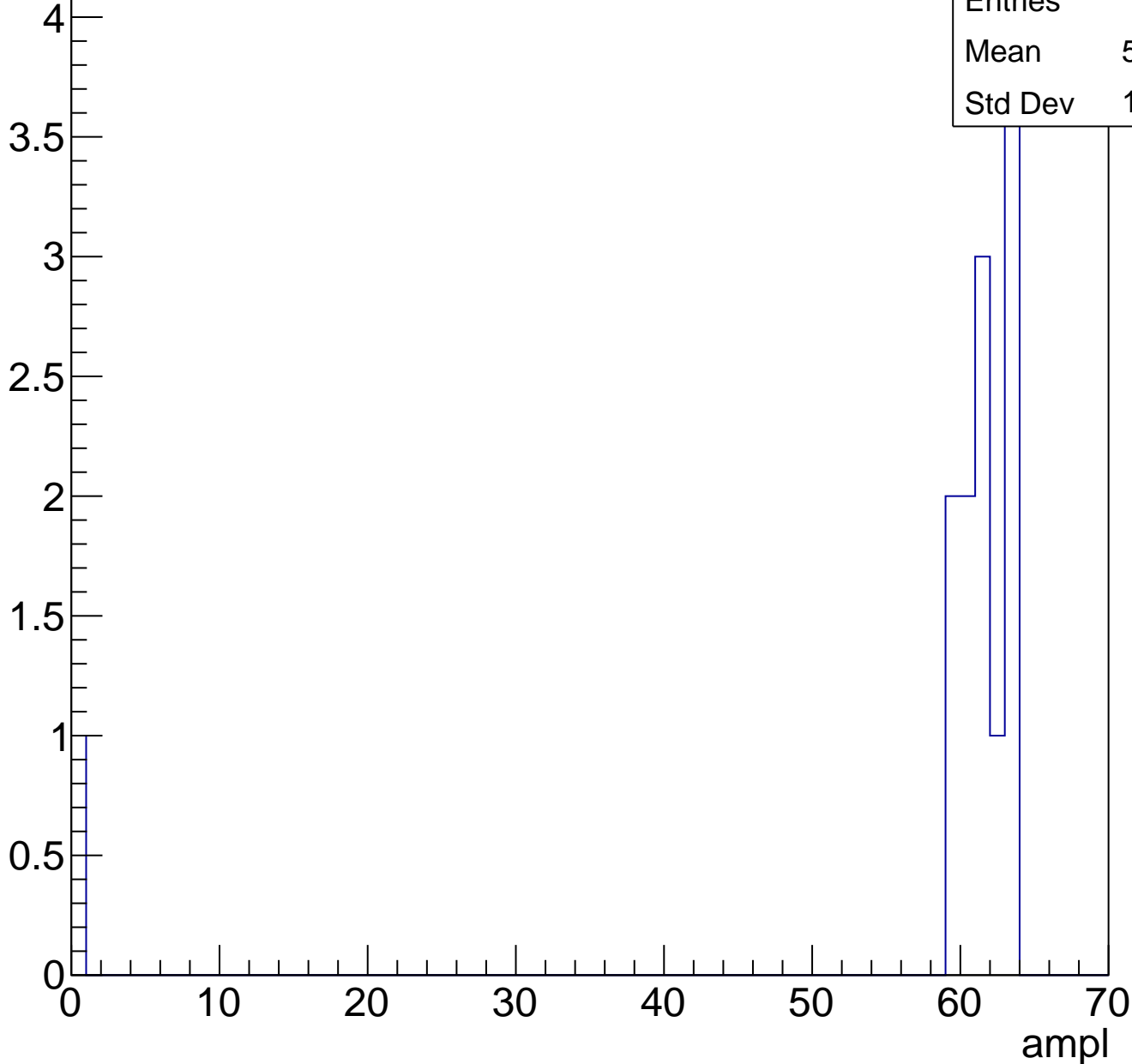
ampl



# B0L102S, U6-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch53, adc0

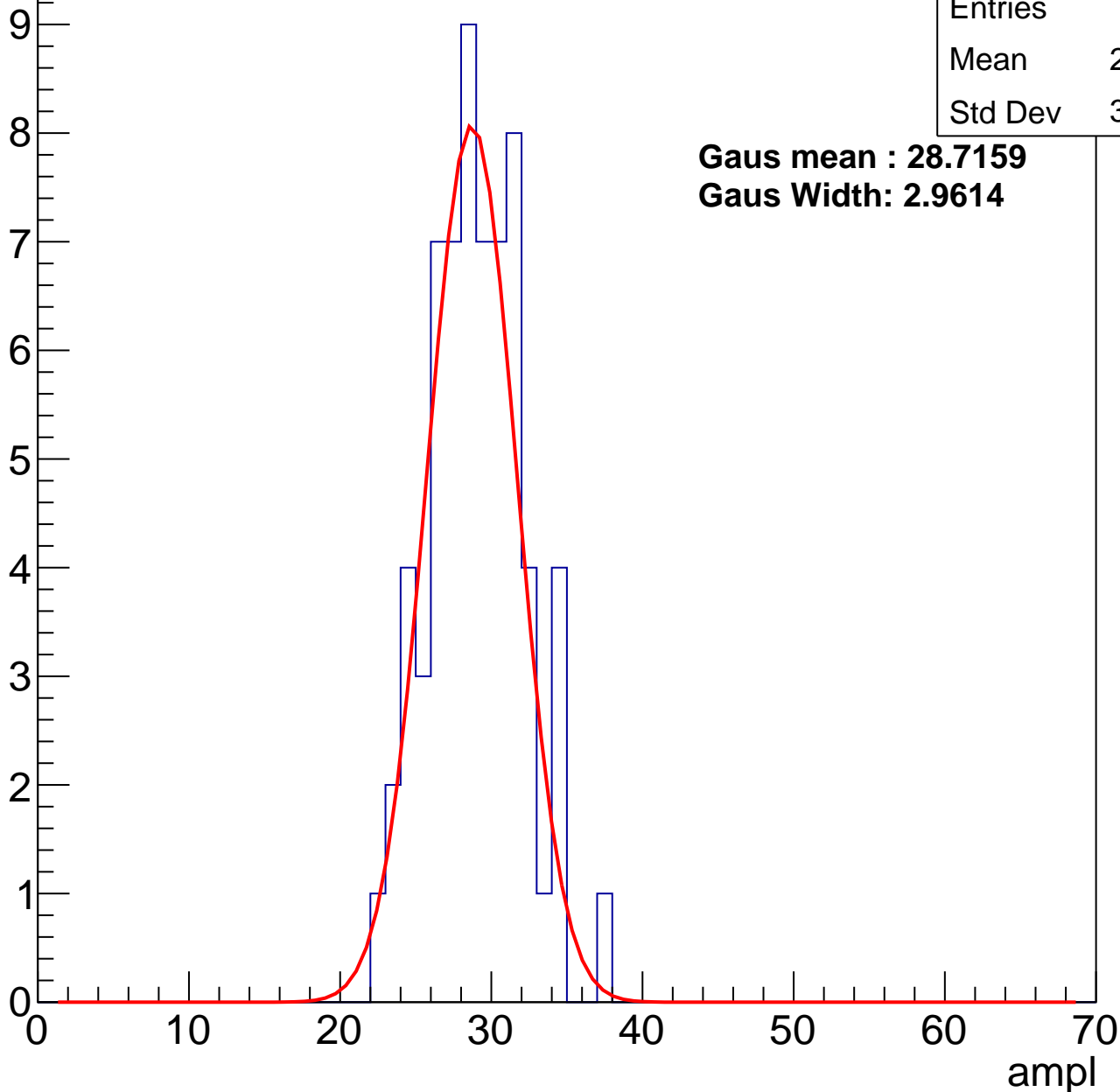
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	28.57
Std Dev	3.068

**Gaus mean : 28.7159**

**Gaus Width: 2.9614**



# B0L102S, U6-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	79
Mean	35.85
Std Dev	3.057

**Gaus mean : 36.6515**

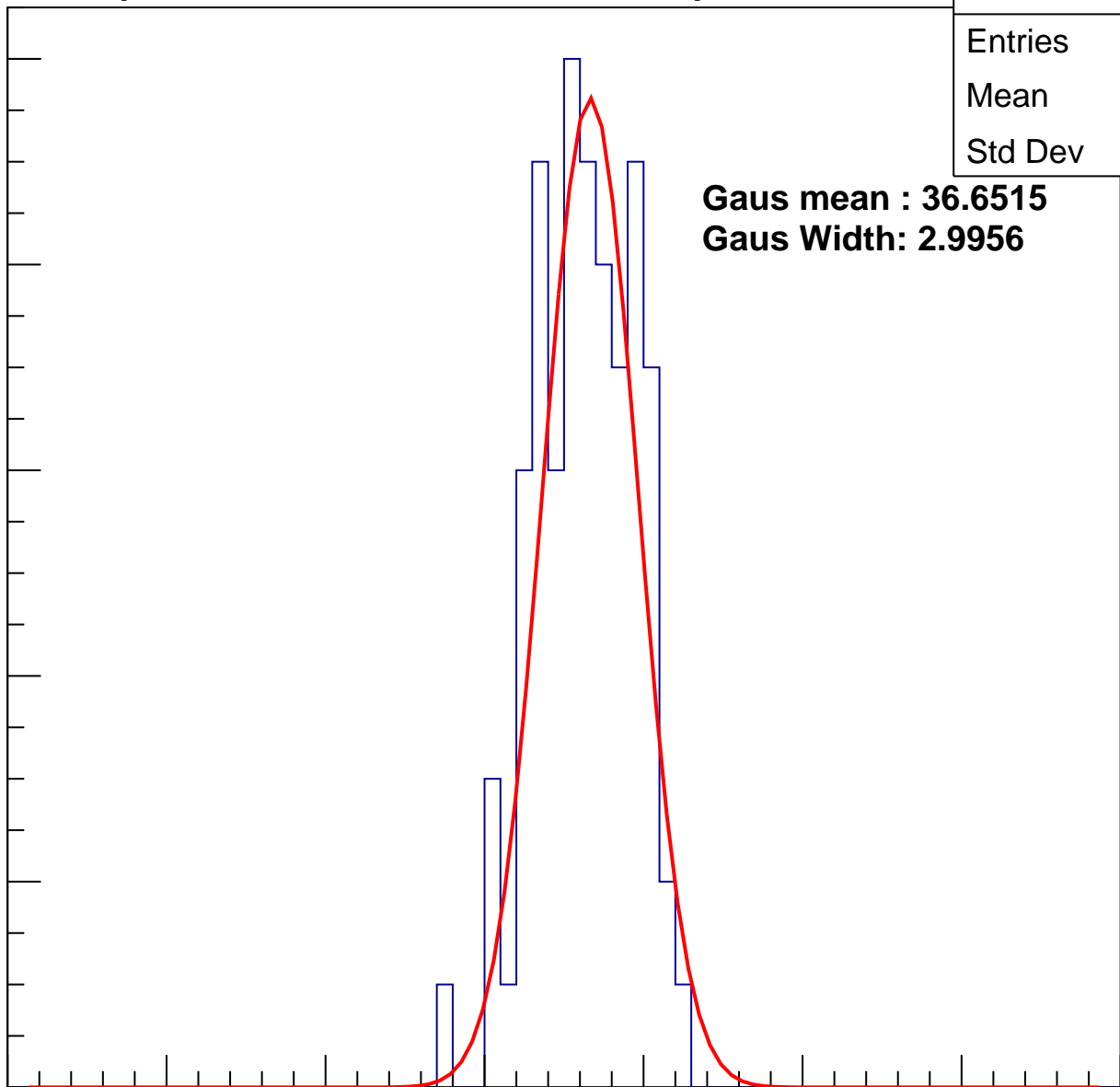
**Gaus Width: 2.9956**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch53, adc2

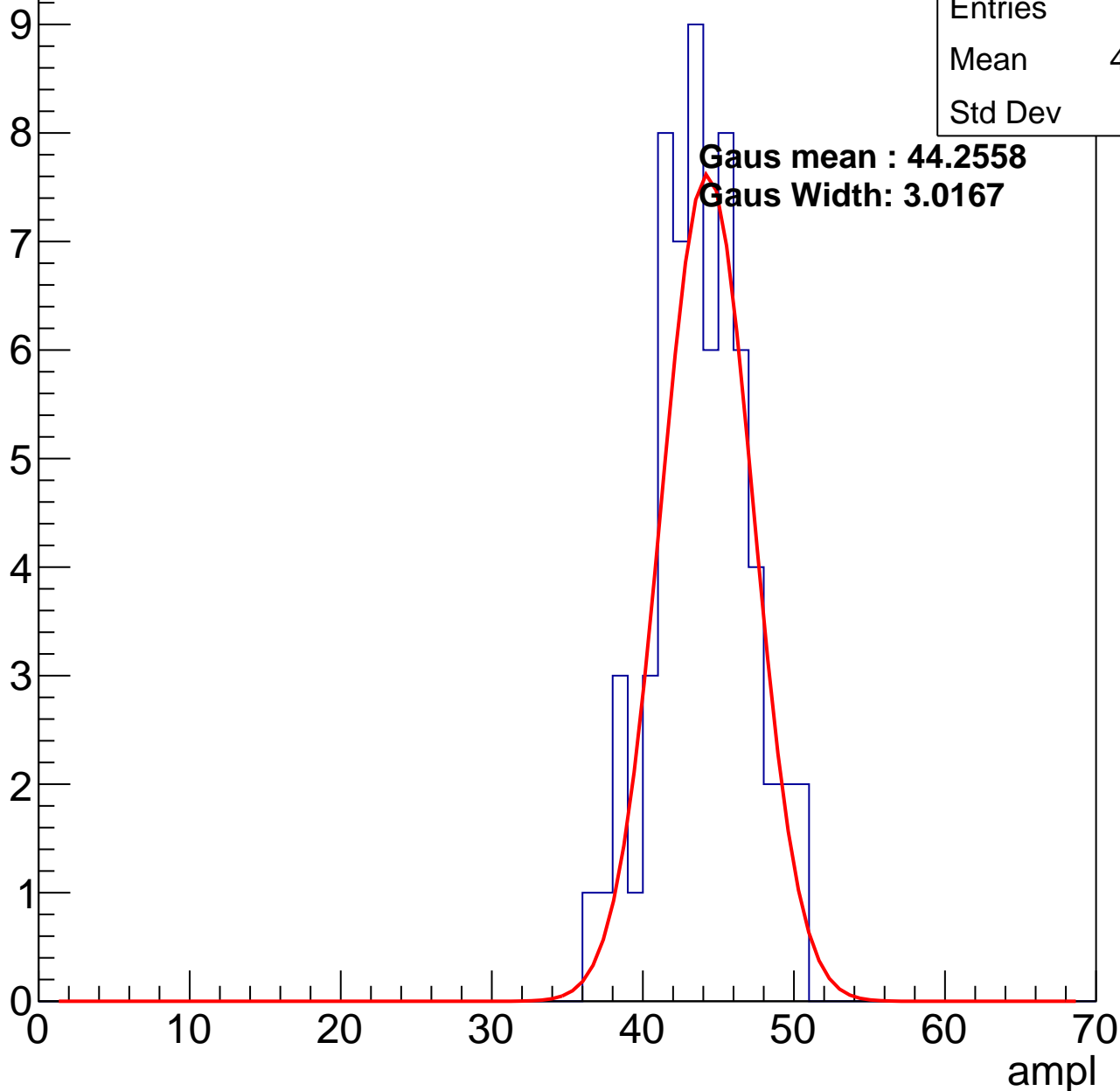
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.44
Std Dev	3.1

**Gaus mean : 44.2558**

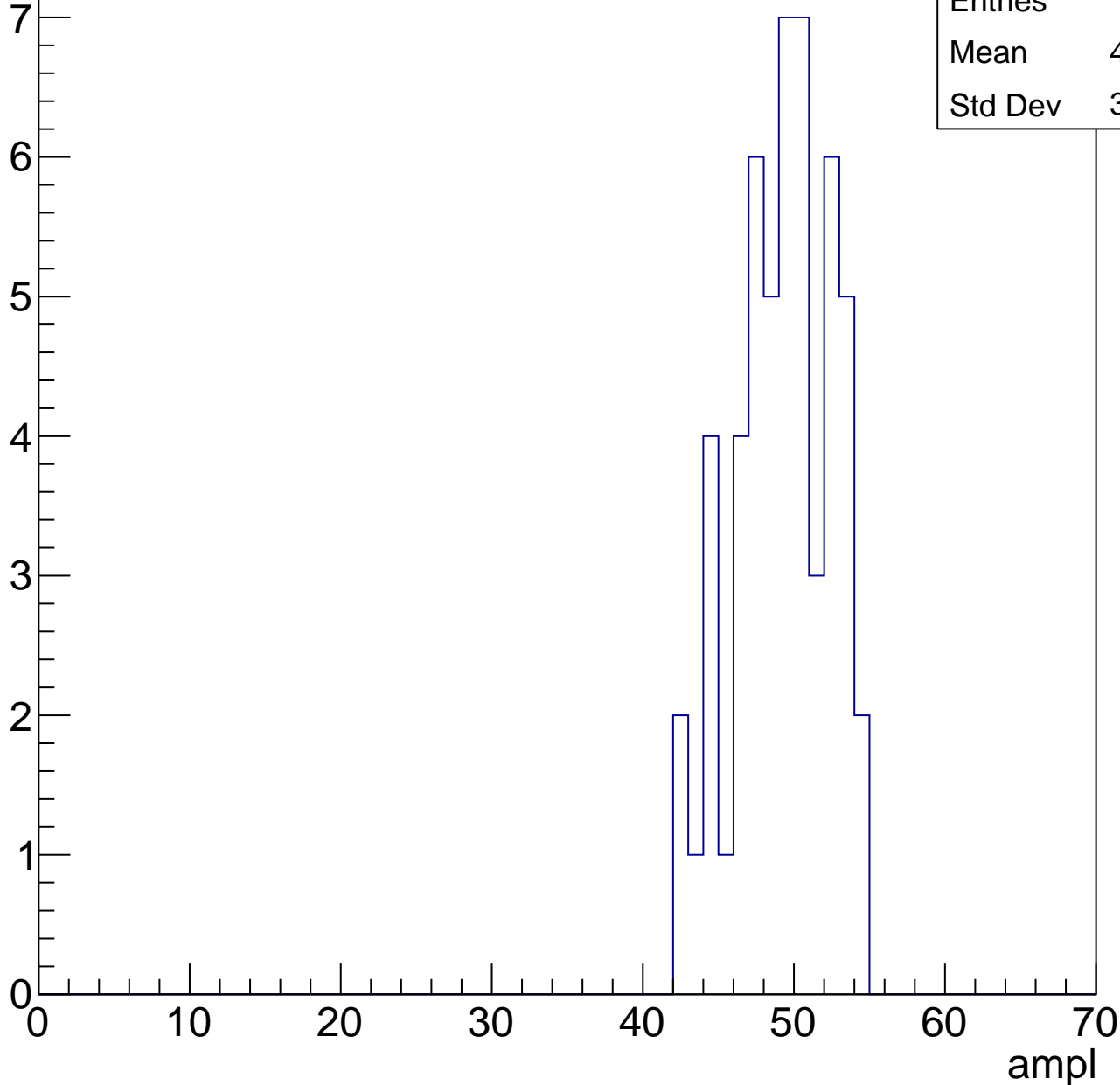
**Gaus Width: 3.0167**



# B0L102S, U6-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



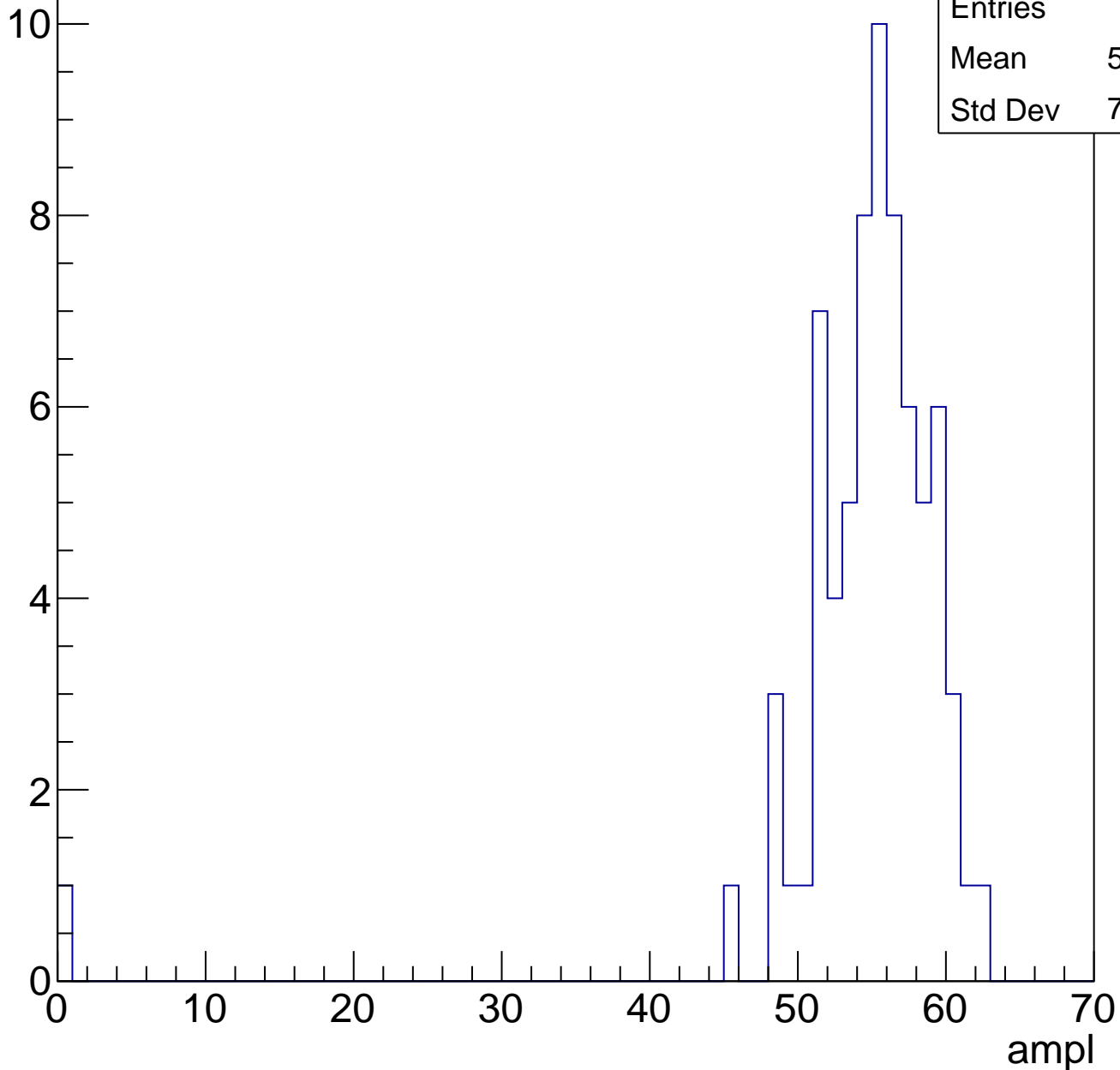
Entries	53
Mean	48.77
Std Dev	3.118

# B0L102S, U6-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	71
Mean	54.04
Std Dev	7.294

Entry



# B0L102S, U6-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

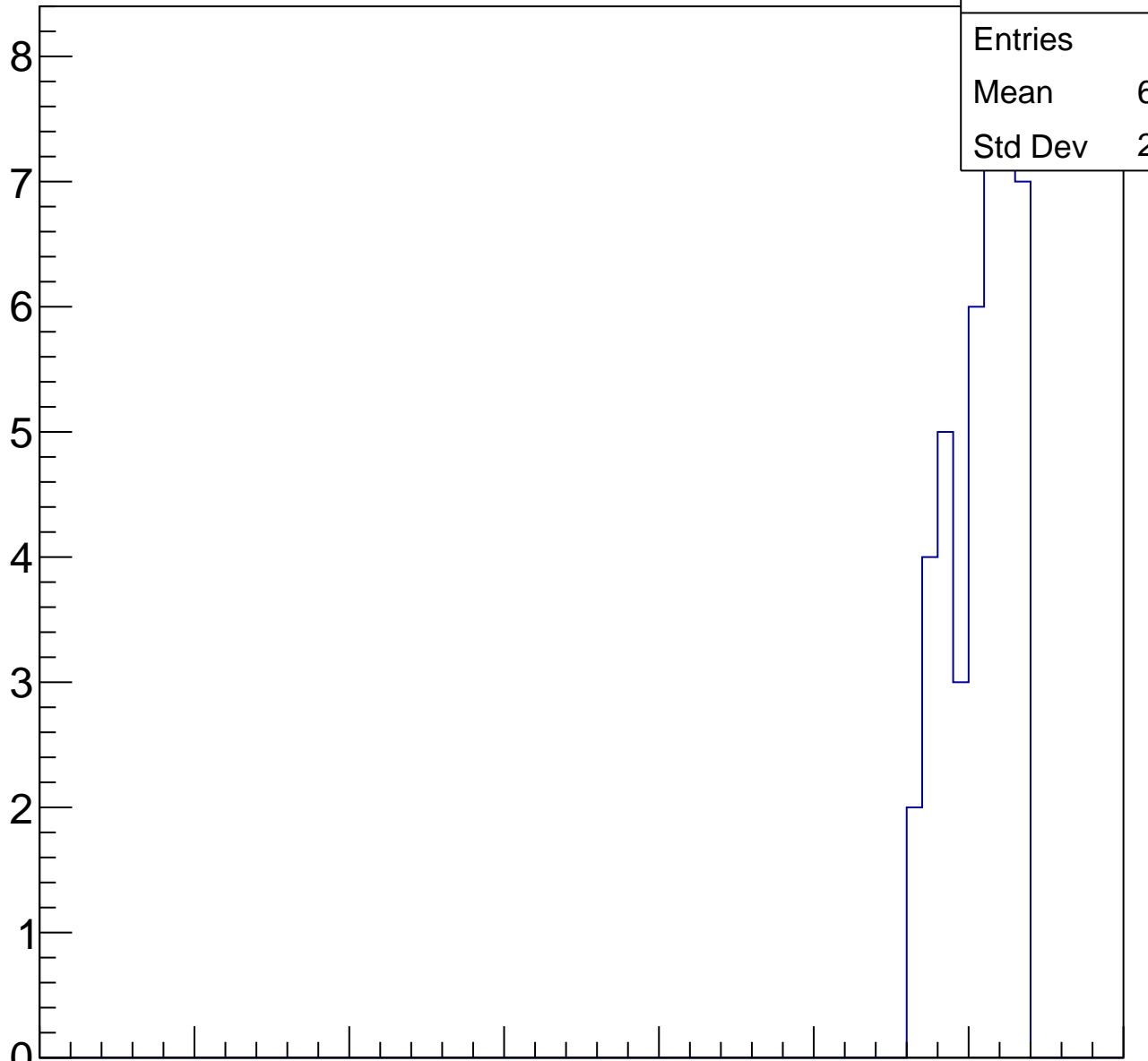
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	60.28
Std Dev	2.106

ampl

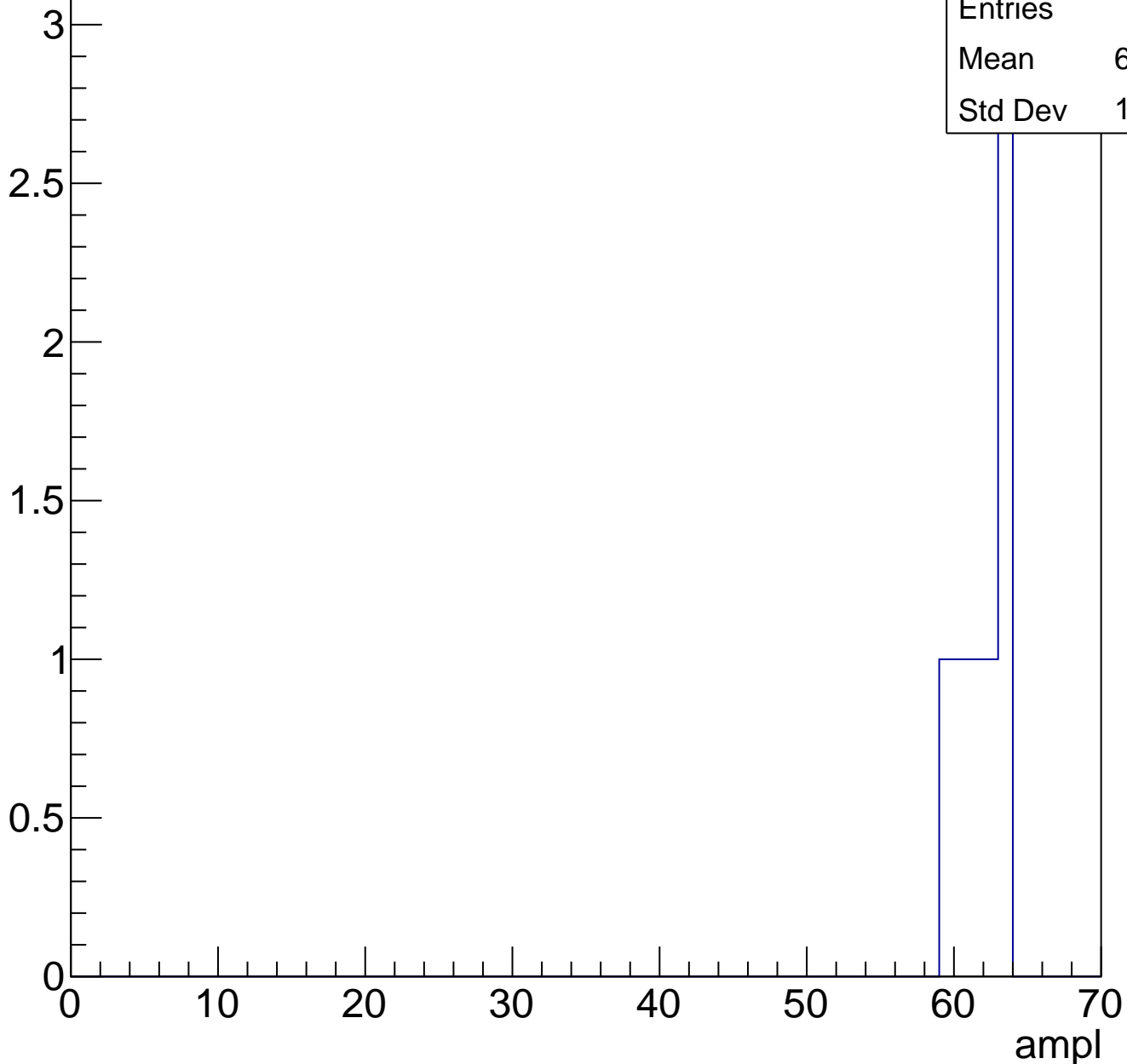
0 10 20 30 40 50 60 70



# B0L102S, U6-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch54, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	92
Mean	27.34
Std Dev	5.557

**Gaus mean : 28.4972**

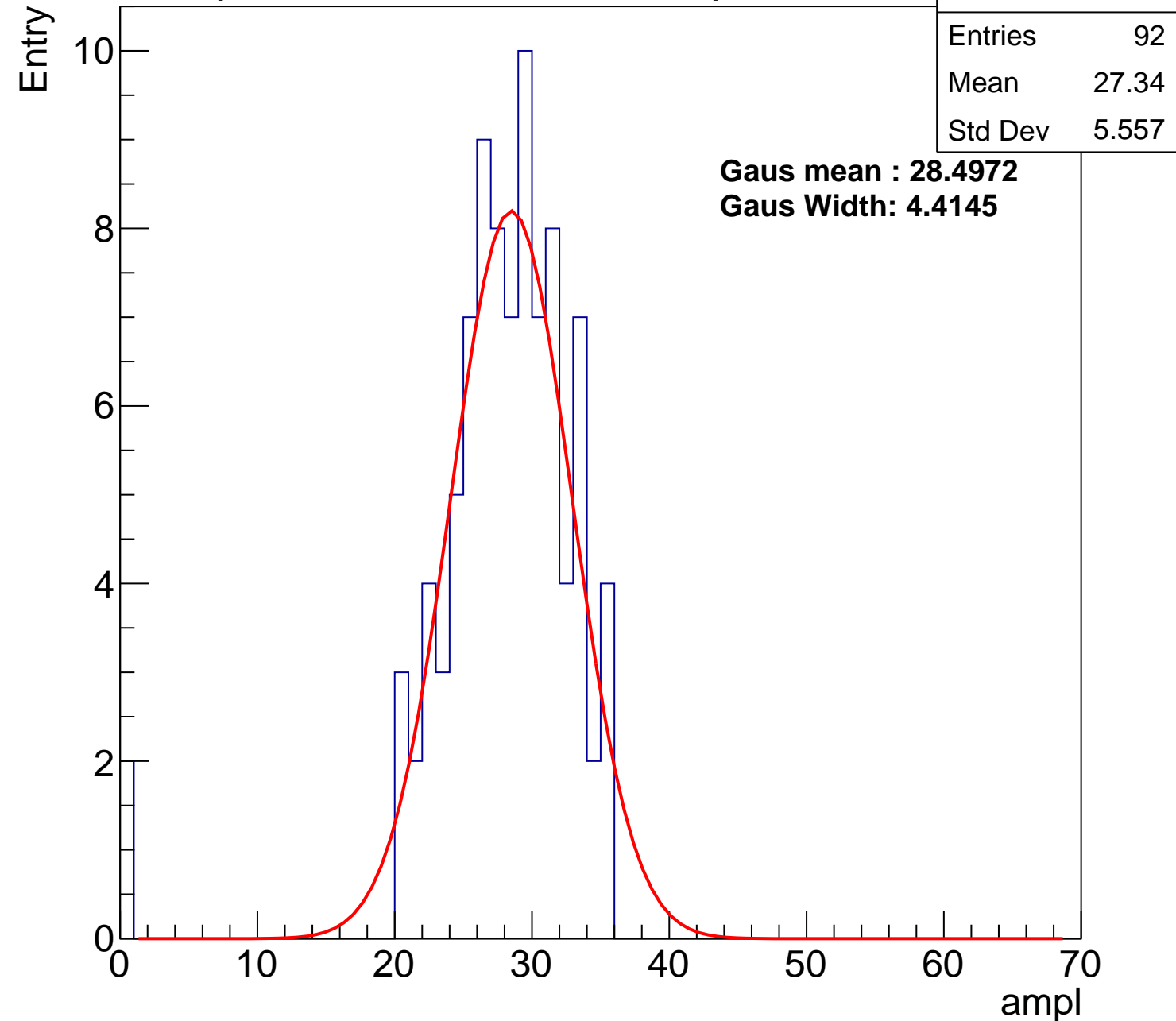
**Gaus Width: 4.4145**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch54, adc1

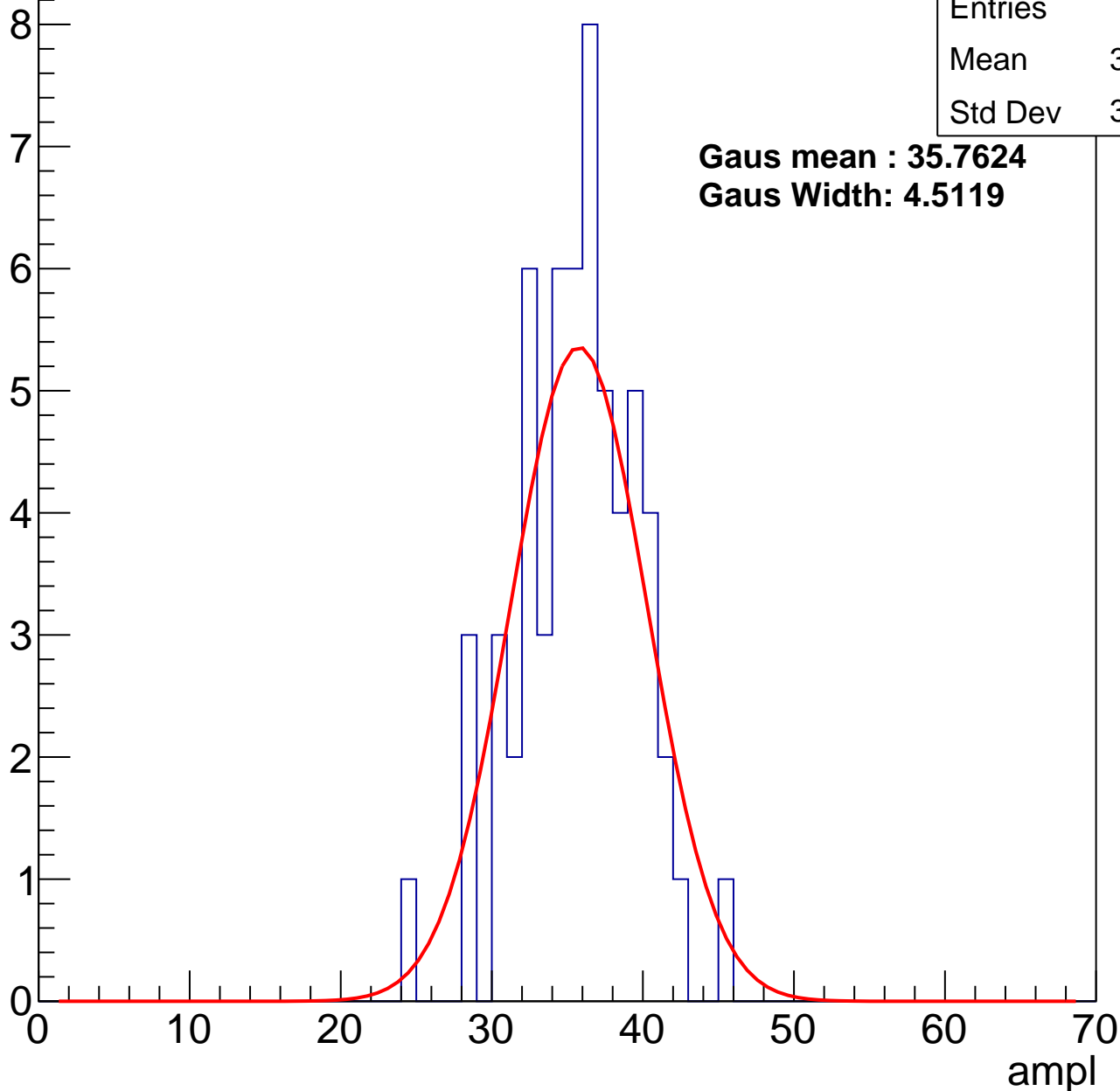
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	35.23
Std Dev	3.892

**Gaus mean : 35.7624**

**Gaus Width: 4.5119**



# B0L102S, U6-ch54, adc2

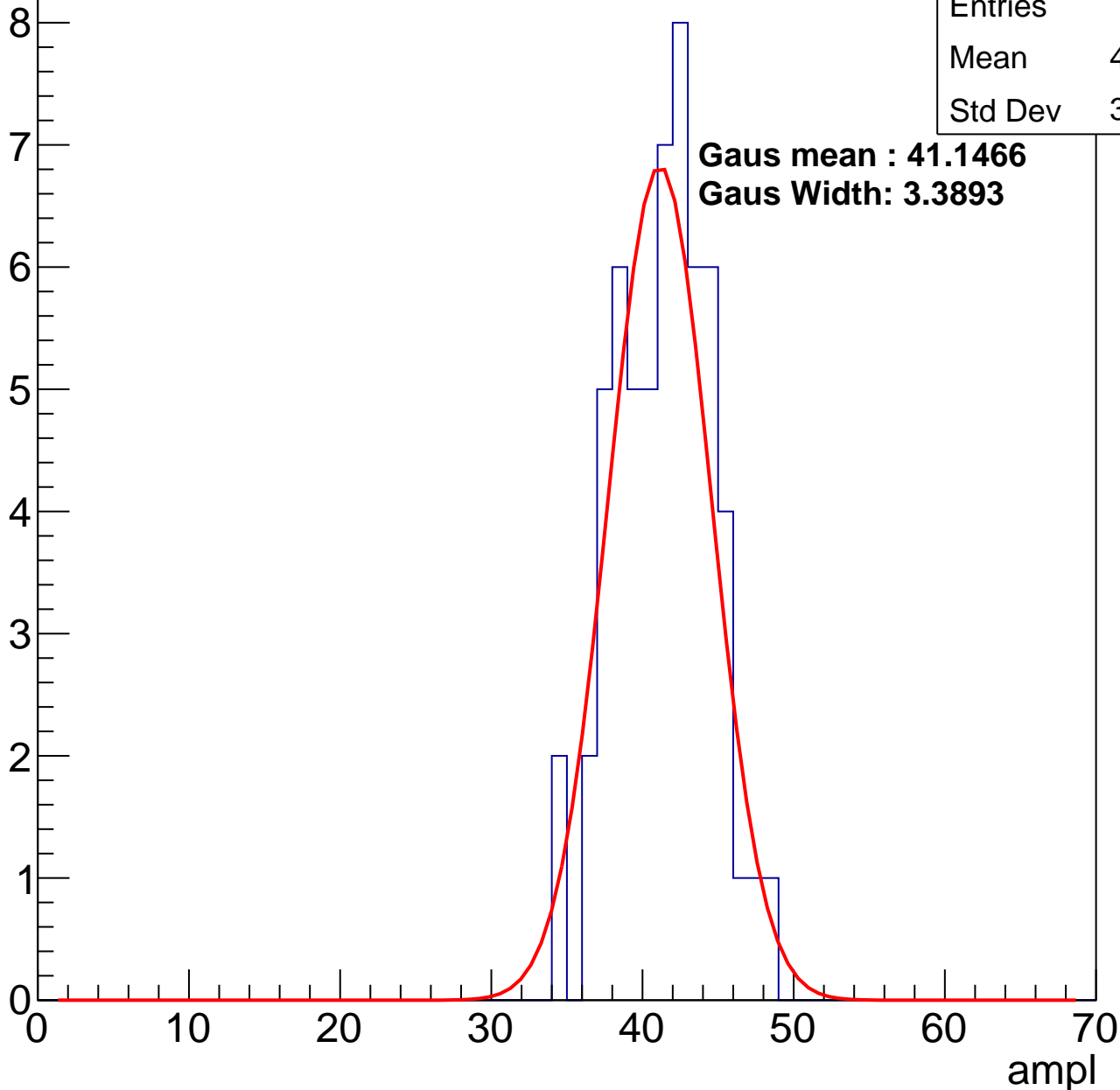
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	40.92
Std Dev	3.099

**Gaus mean : 41.1466**

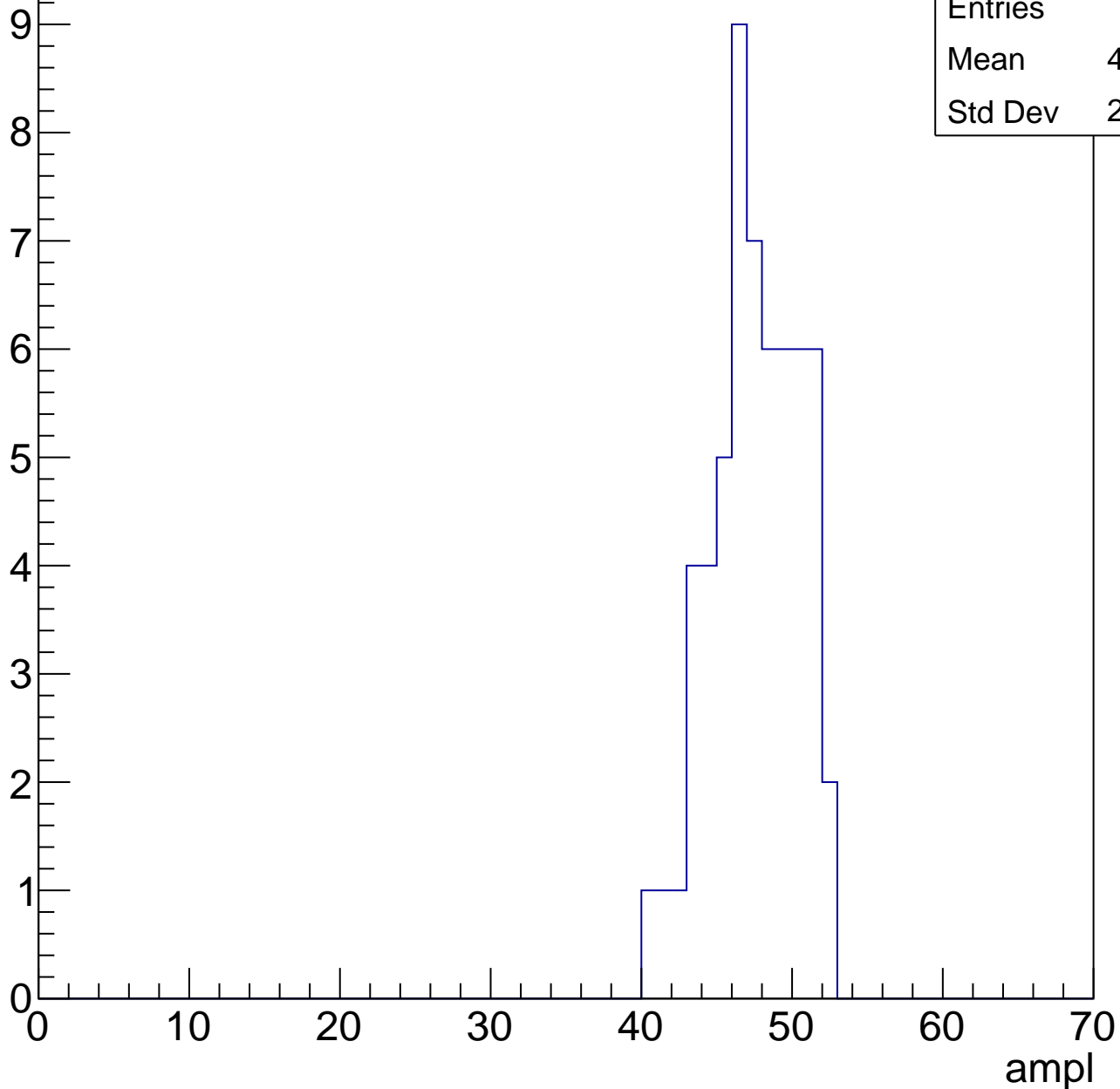
**Gaus Width: 3.3893**



# B0L102S, U6-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



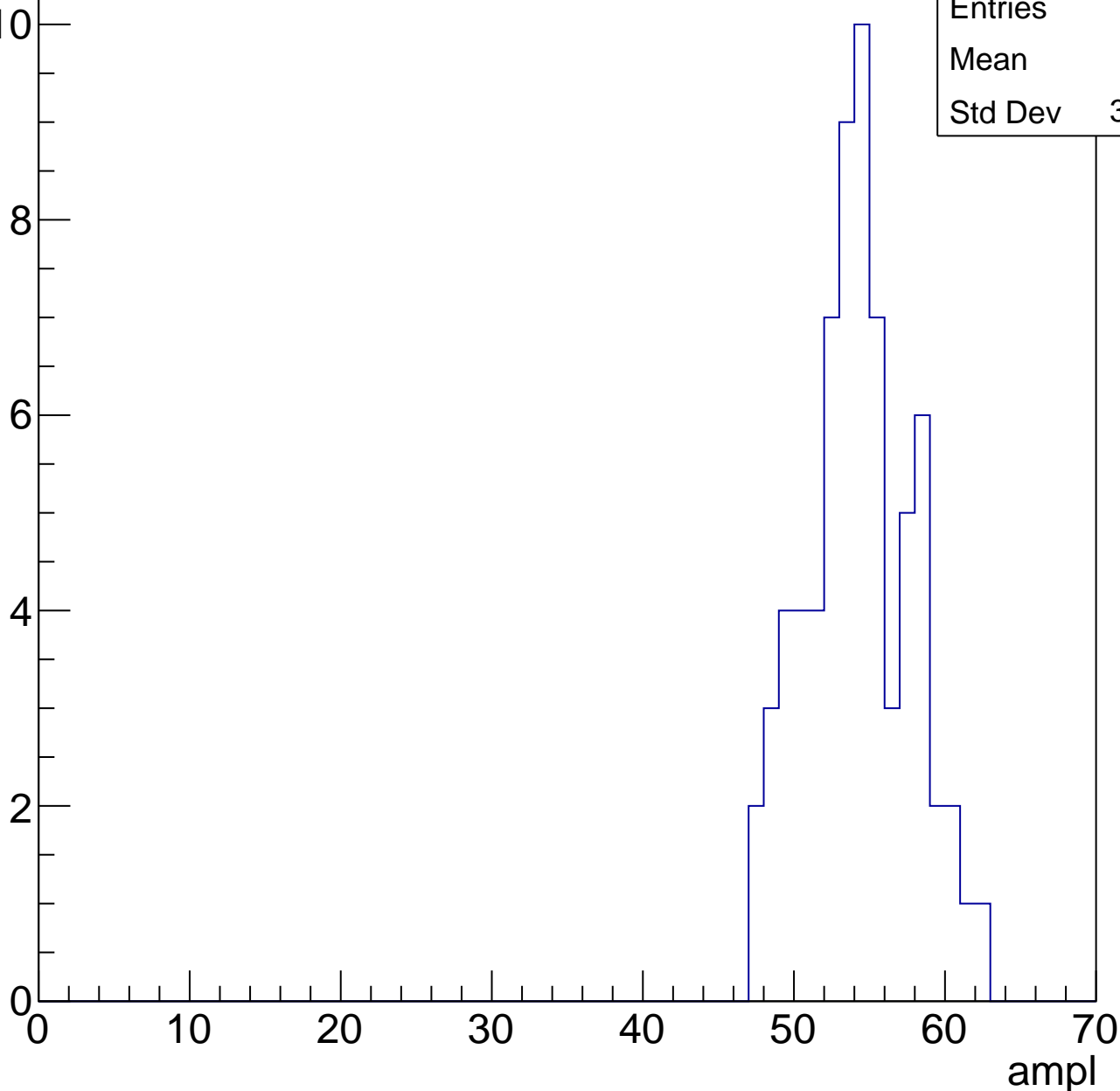
Entries	58
Mean	47.09
Std Dev	2.842

# B0L102S, U6-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

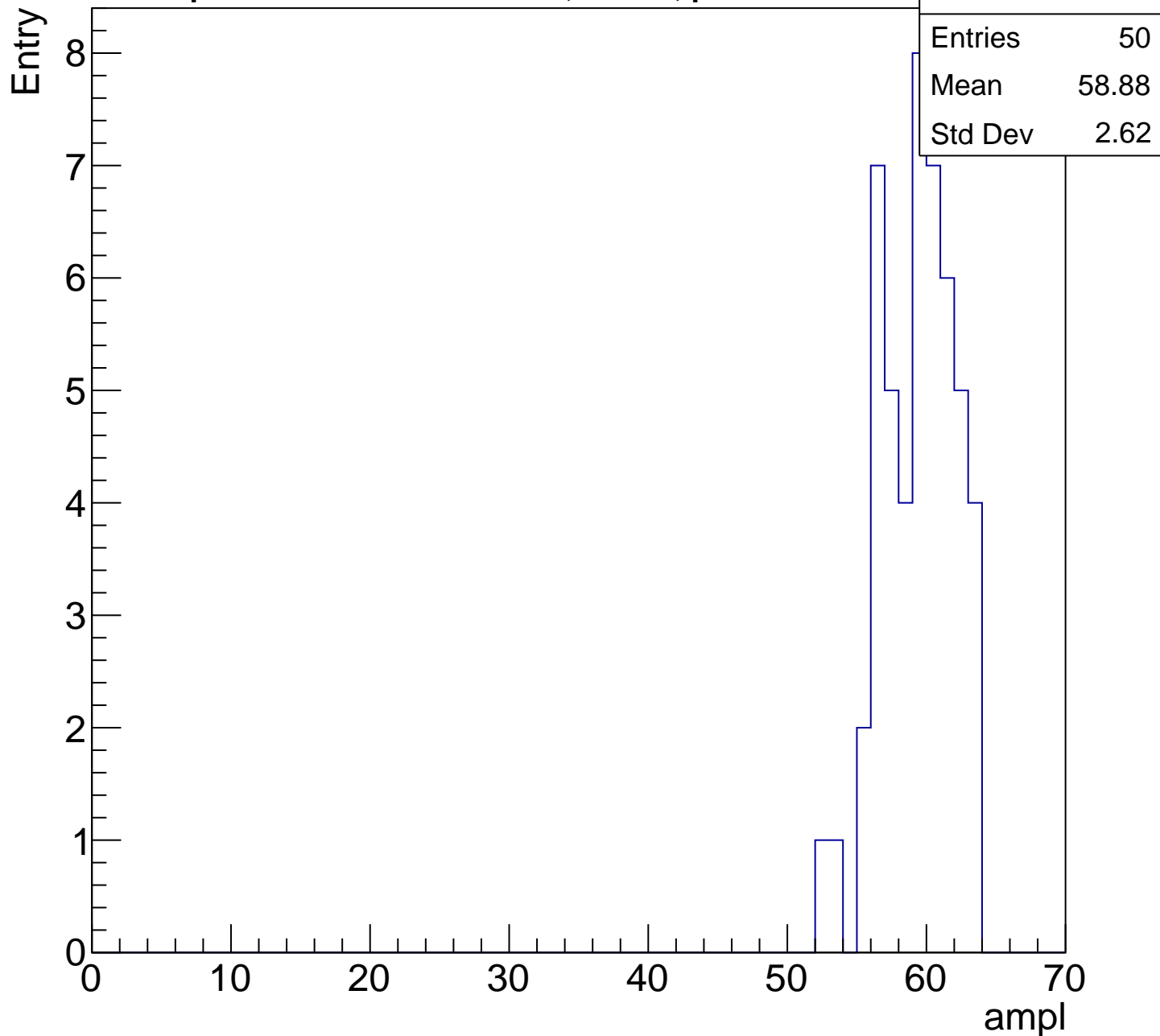
Entry

Entries	70
Mean	53.8
Std Dev	3.454



# B0L102S, U6-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

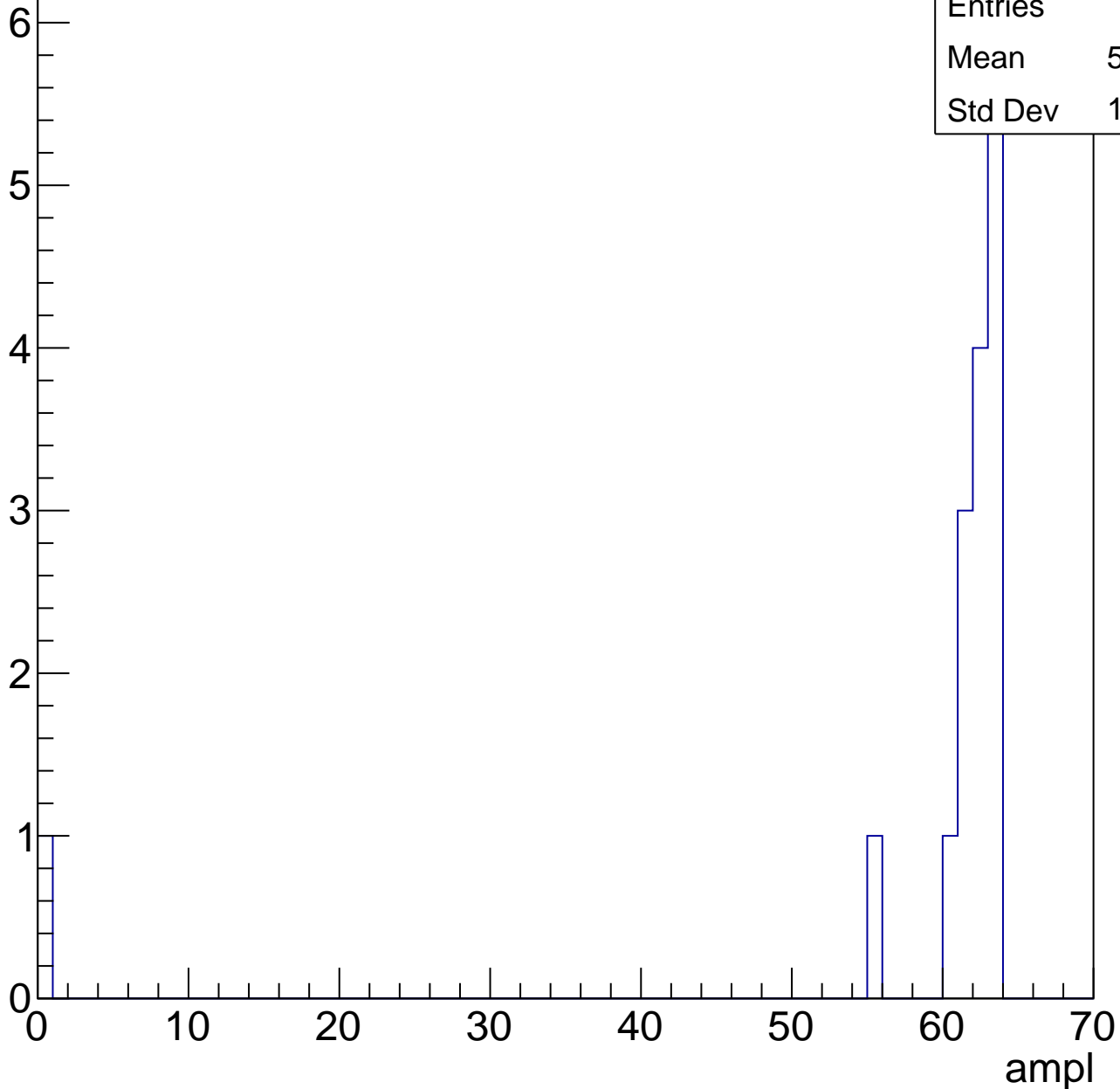


# B0L102S, U6-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	16
Mean	57.75
Std Dev	15.04





# B0L102S, U6-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch55, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	67
Mean	29.1
Std Dev	4.741

**Gaus mean : 30.0246**

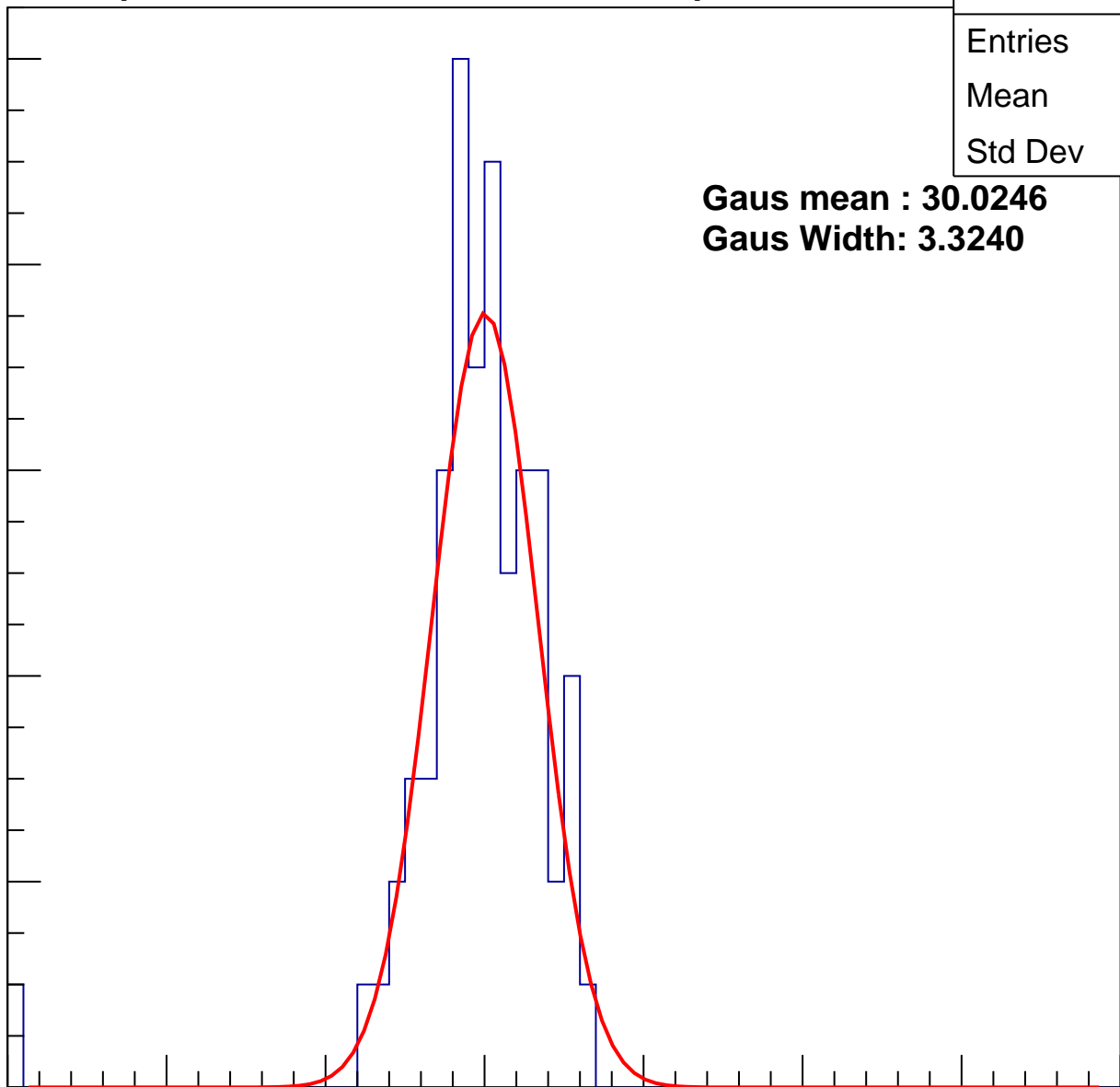
**Gaus Width: 3.3240**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch55, adc1

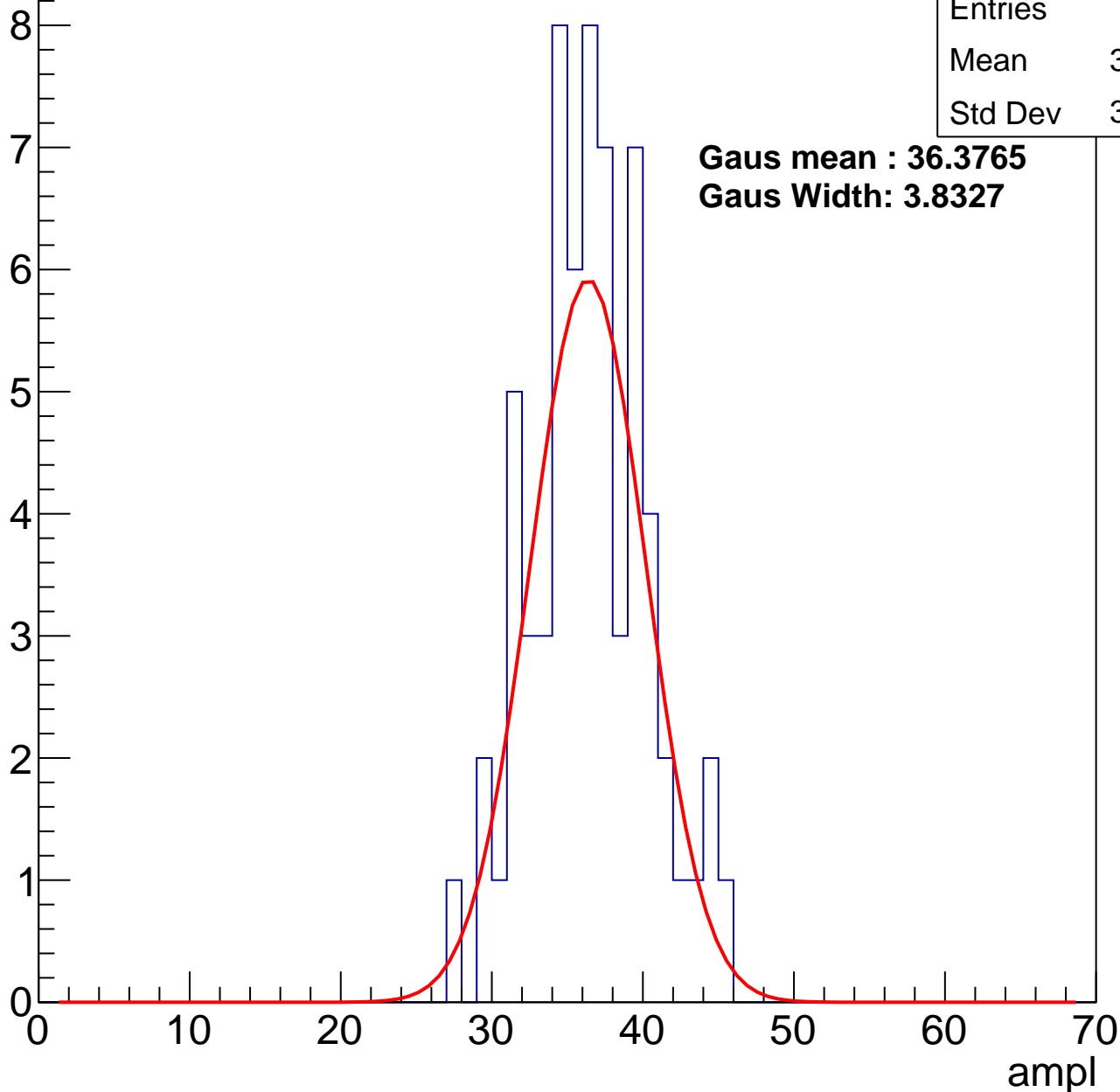
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36.02
Std Dev	3.809

**Gaus mean : 36.3765**

**Gaus Width: 3.8327**



# B0L102S, U6-ch55, adc2

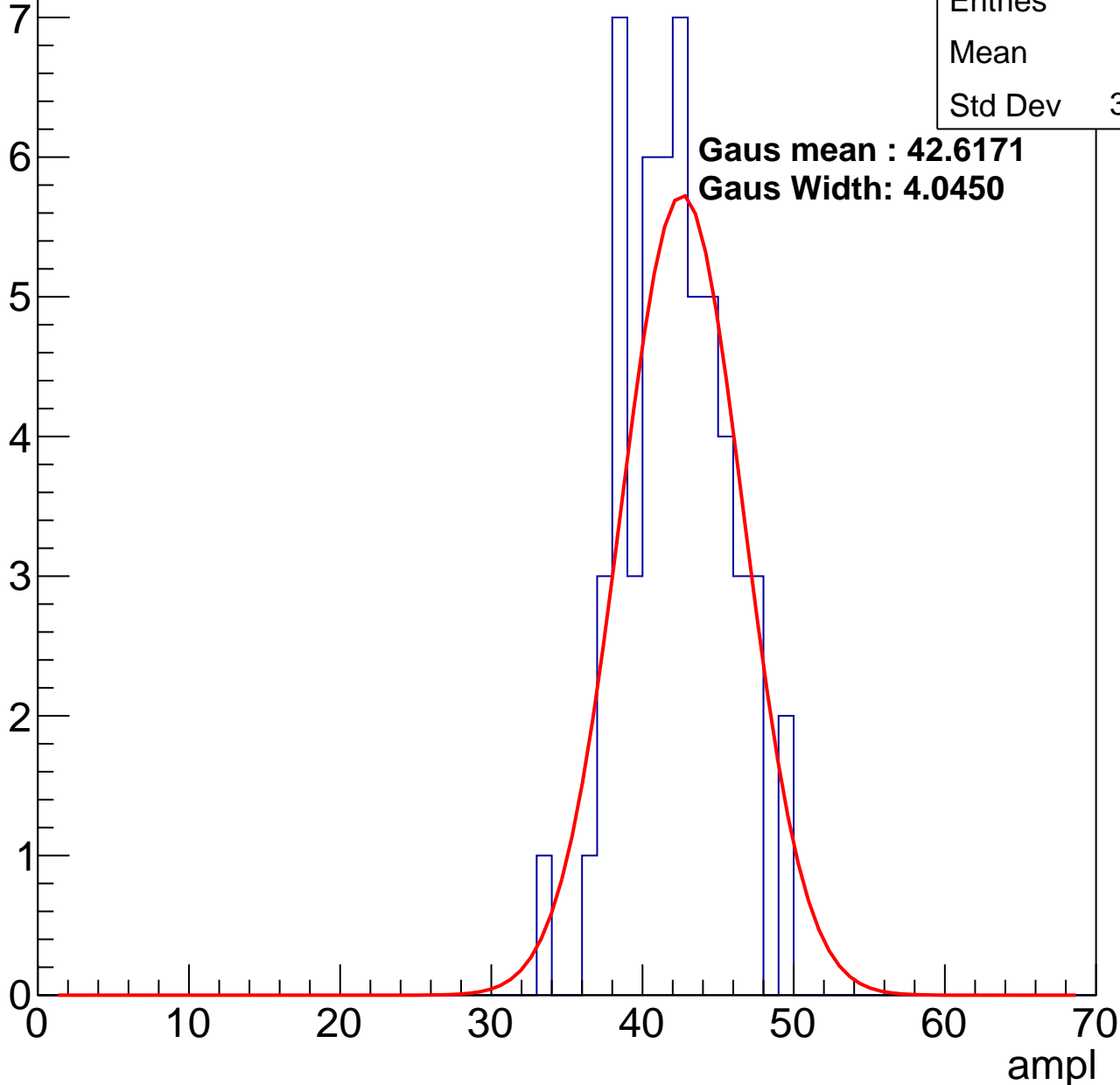
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	41.7
Std Dev	3.375

**Gaus mean : 42.6171**

**Gaus Width: 4.0450**

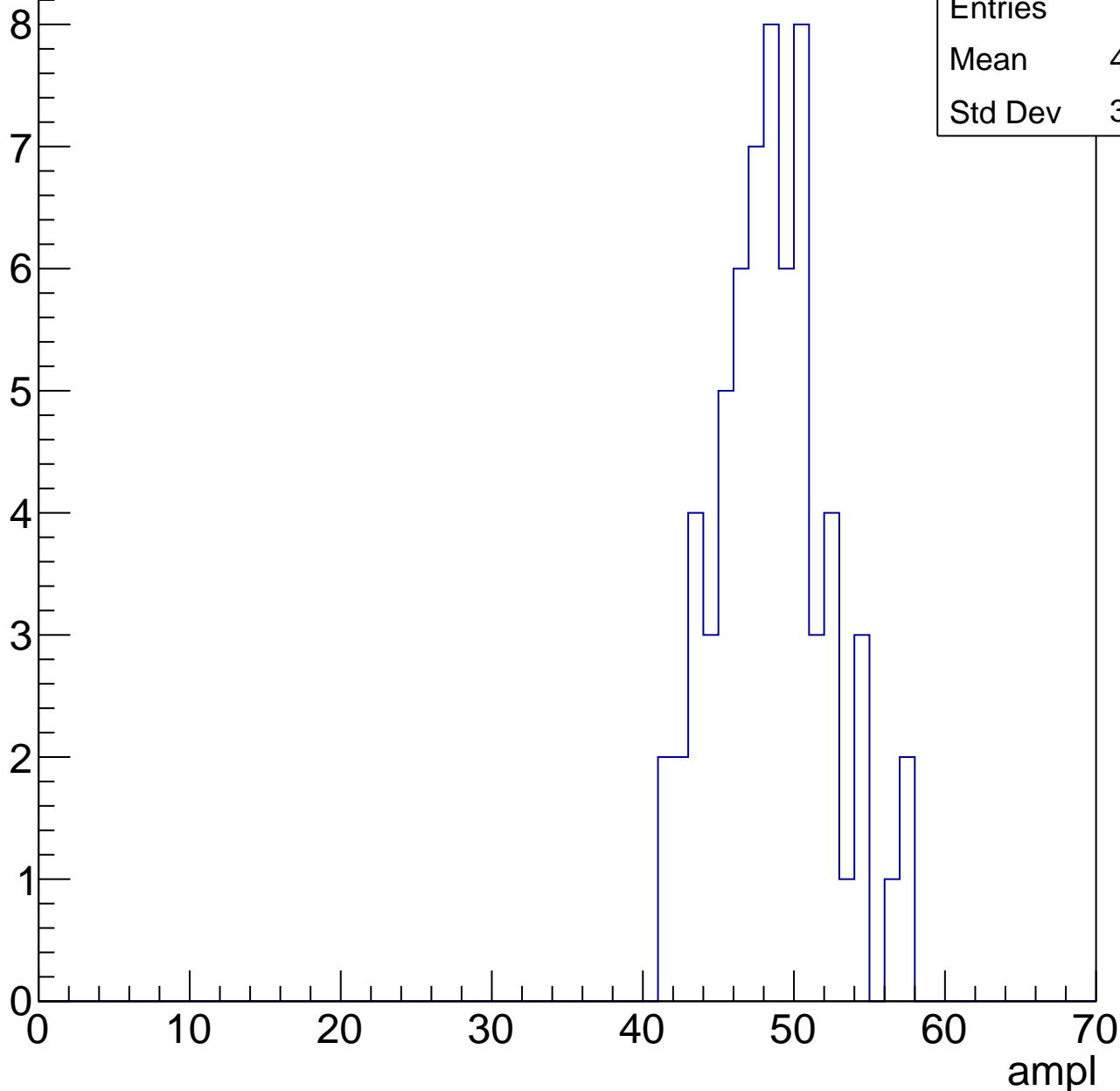


# B0L102S, U6-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	48.06
Std Dev	3.683

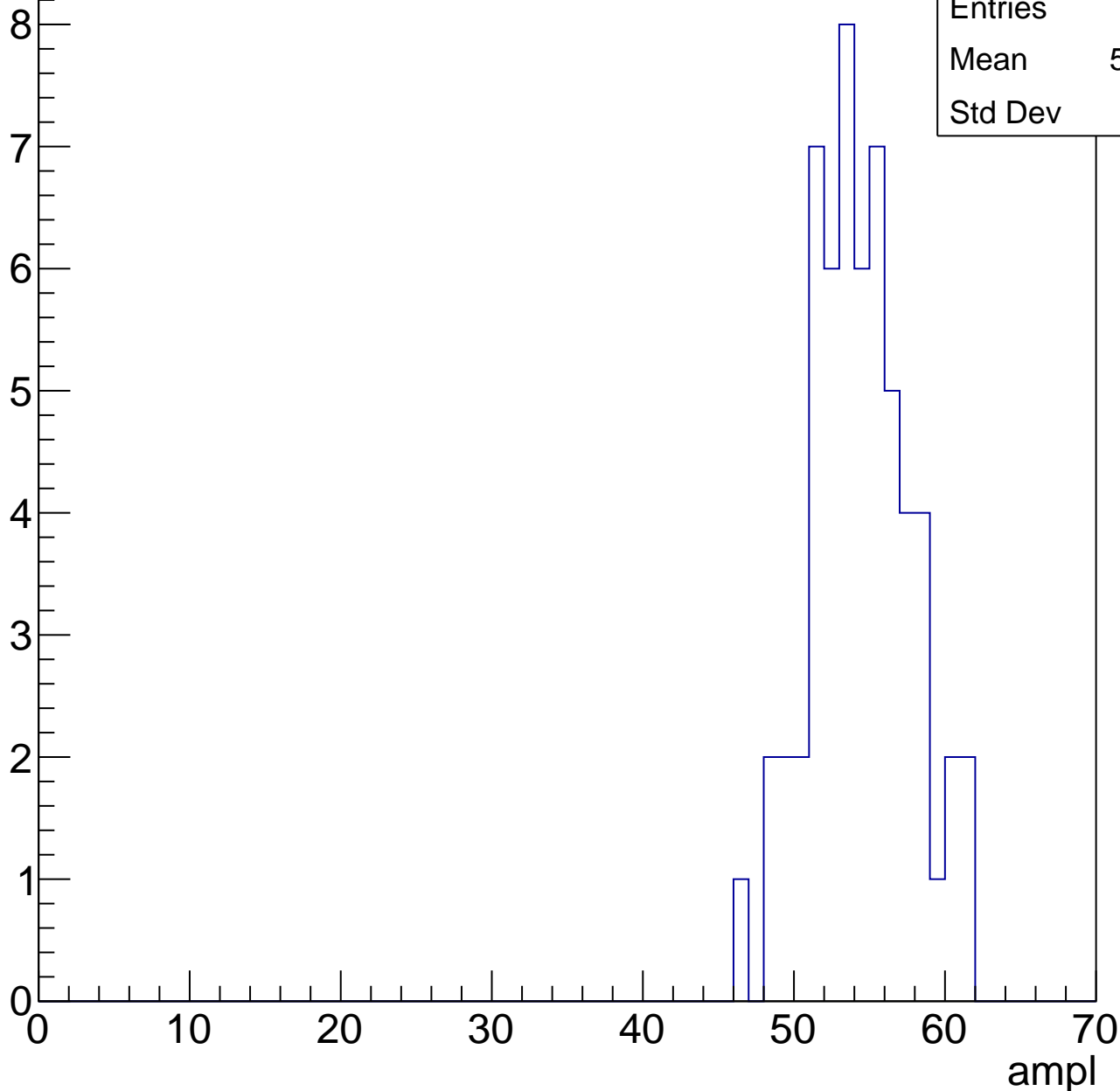


# B0L102S, U6-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	53.95
Std Dev	3.28



# B0L102S, U6-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.15
Std Dev	8.374

ampl

0

10

20

30

40

50

60

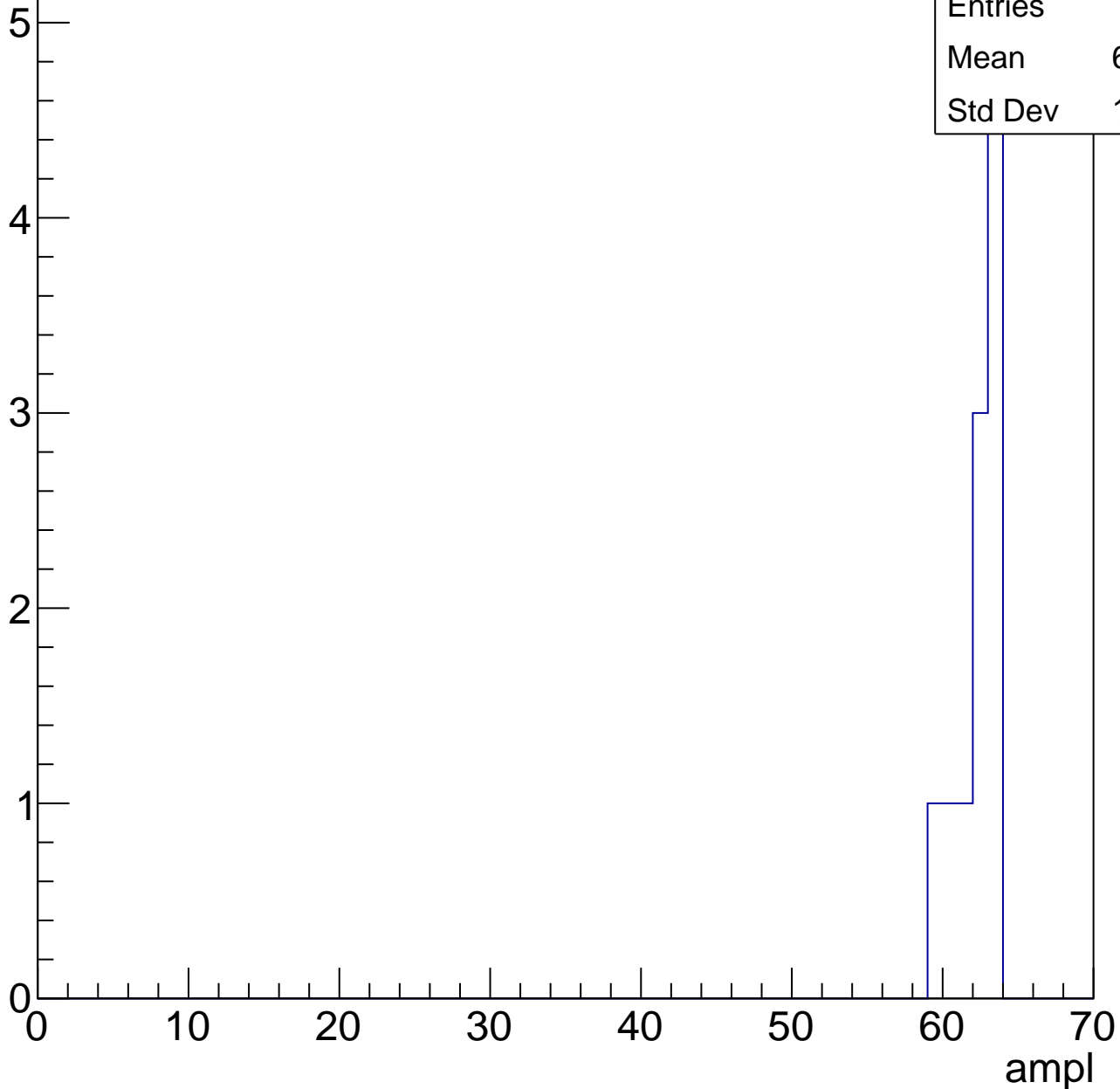
70

# B0L102S, U6-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	11
Mean	61.91
Std Dev	1.311

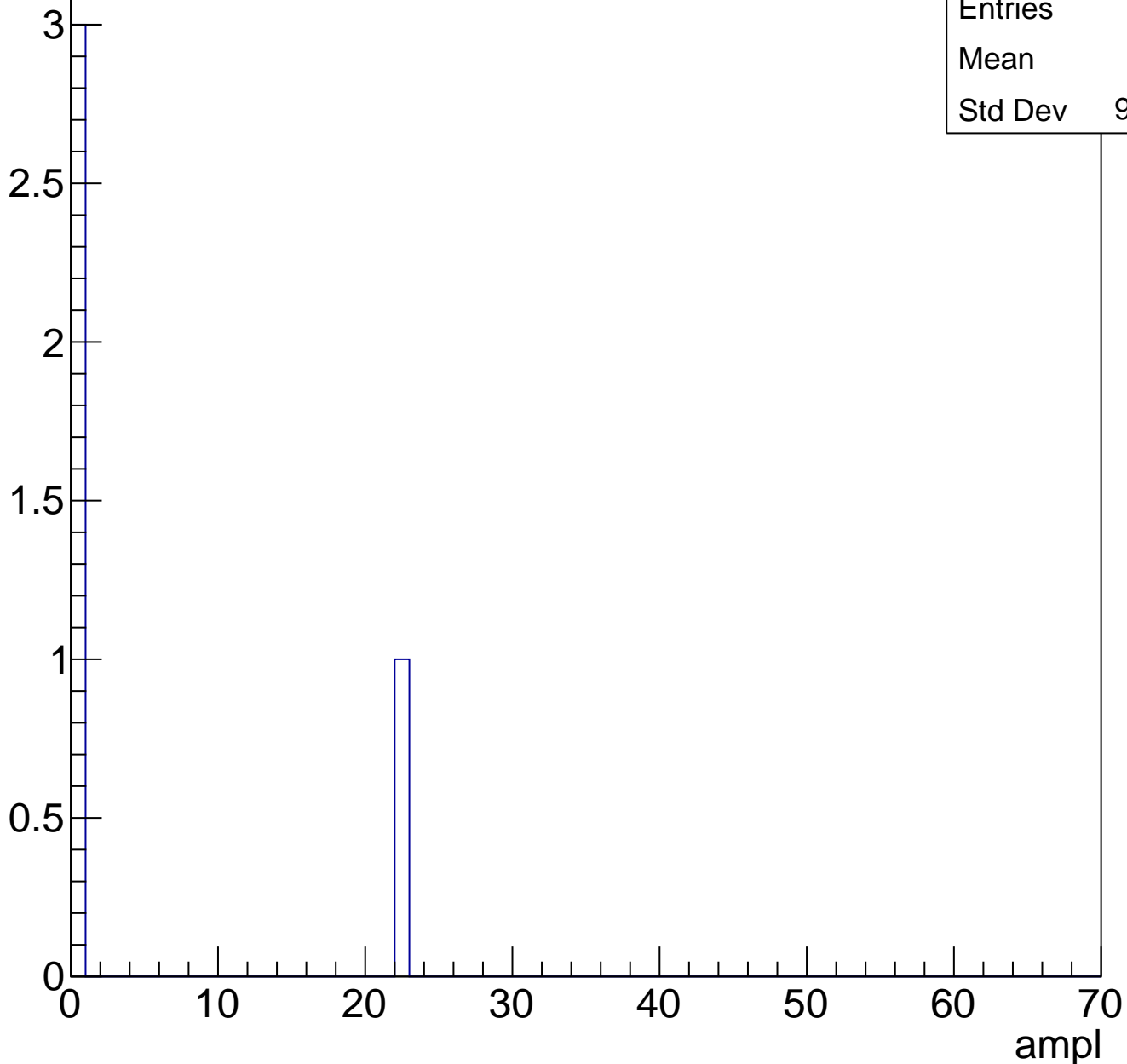




# B0L102S, U6-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	4
Mean	5.5
Std Dev	9.526

# B0L102S, U6-ch56, adc0

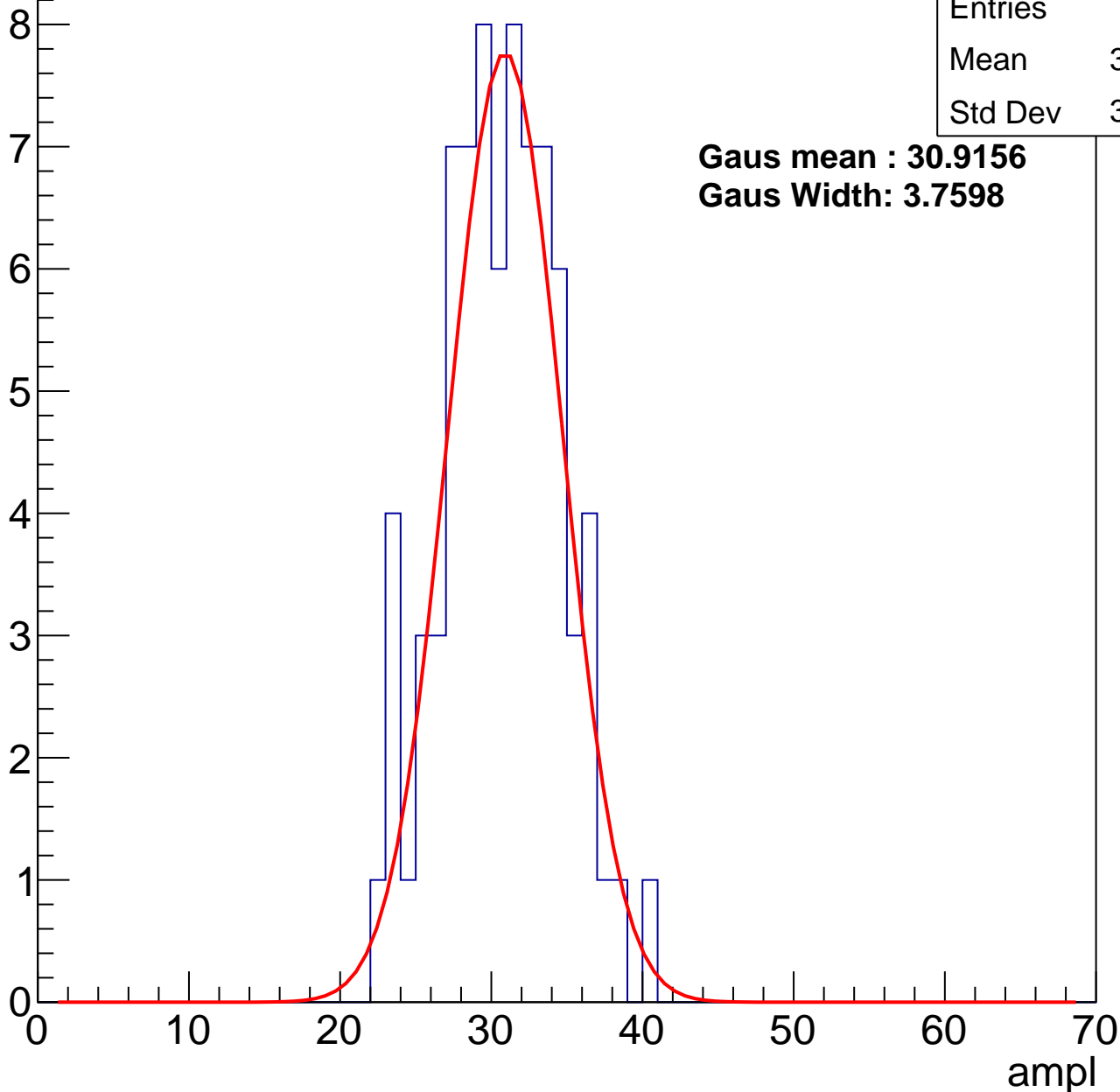
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	30.24
Std Dev	3.824

**Gaus mean : 30.9156**

**Gaus Width: 3.7598**



# B0L102S, U6-ch56, adc1

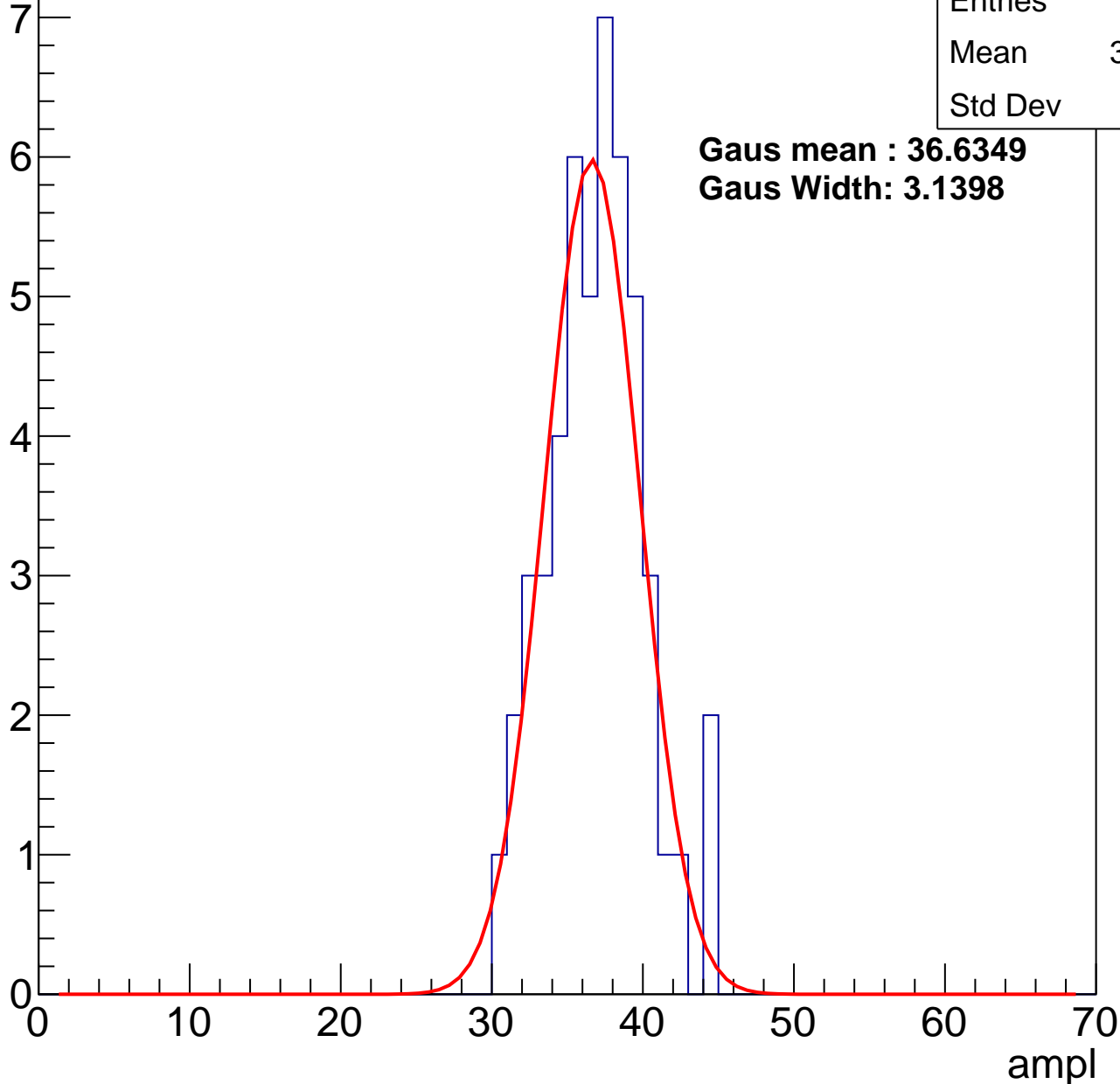
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	36.45
Std Dev	3.15

**Gaus mean : 36.6349**

**Gaus Width: 3.1398**



# B0L102S, U6-ch56, adc2

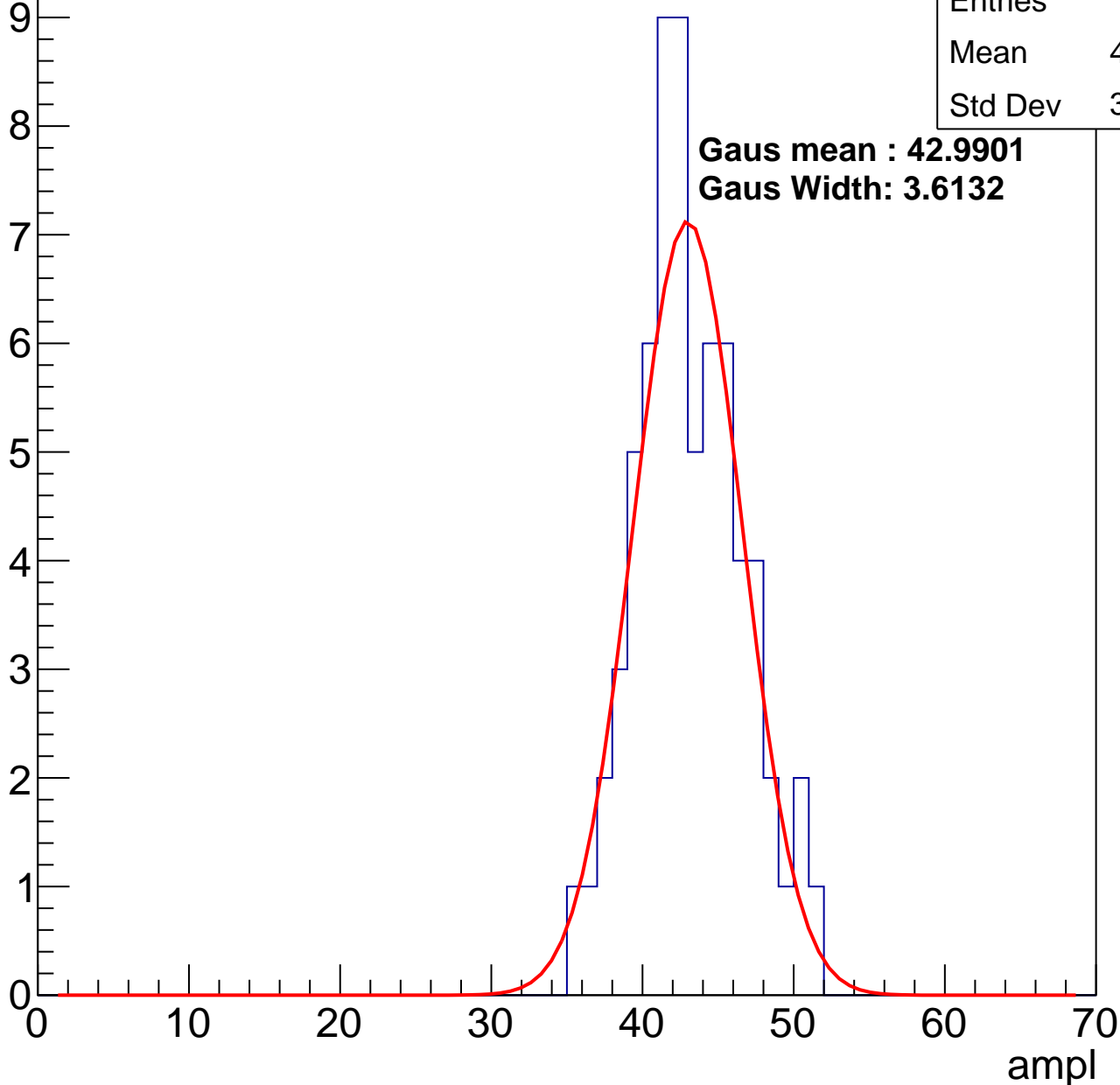
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	42.66
Std Dev	3.475

**Gaus mean : 42.9901**

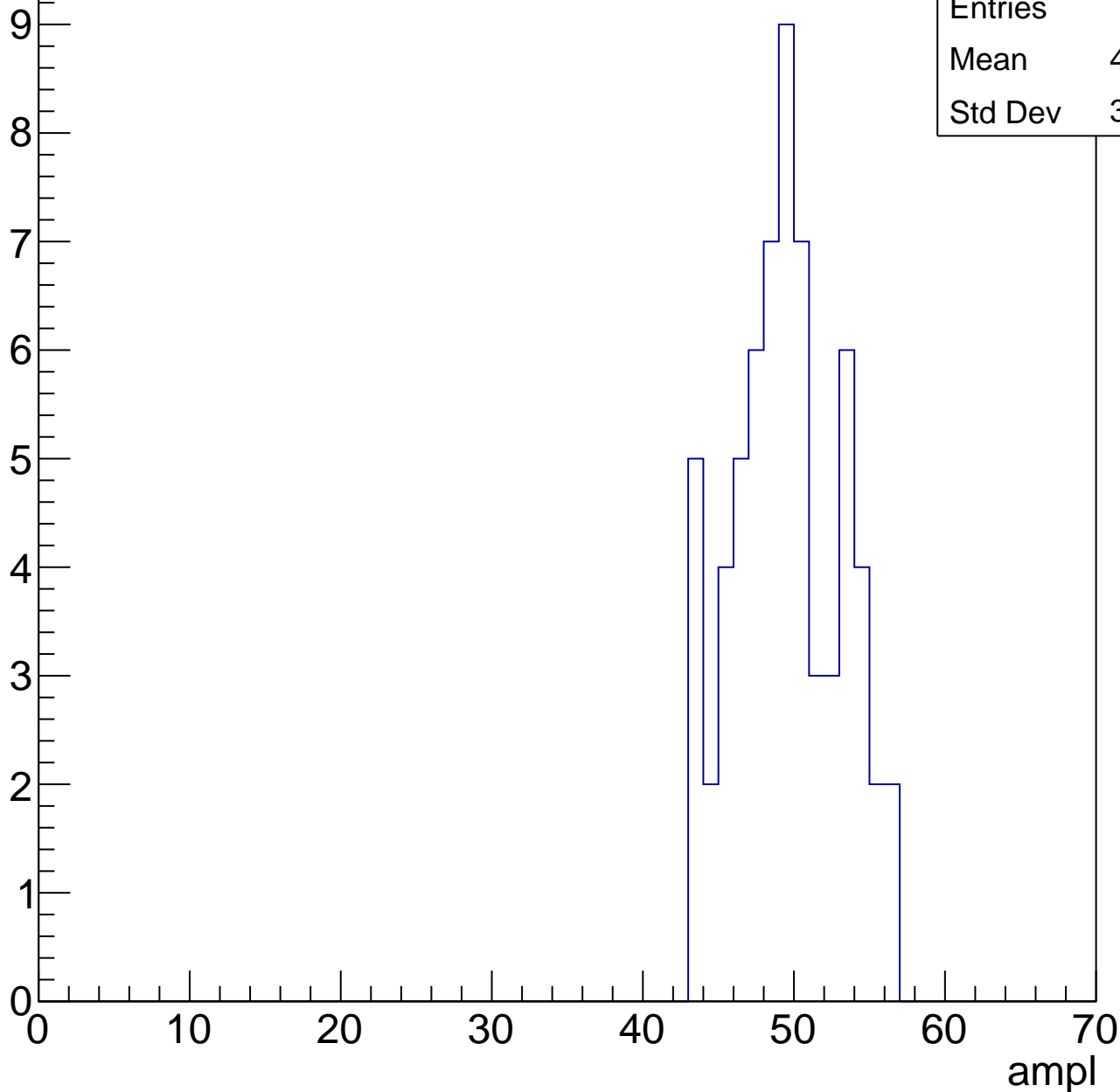
**Gaus Width: 3.6132**



# B0L102S, U6-ch56, adc3

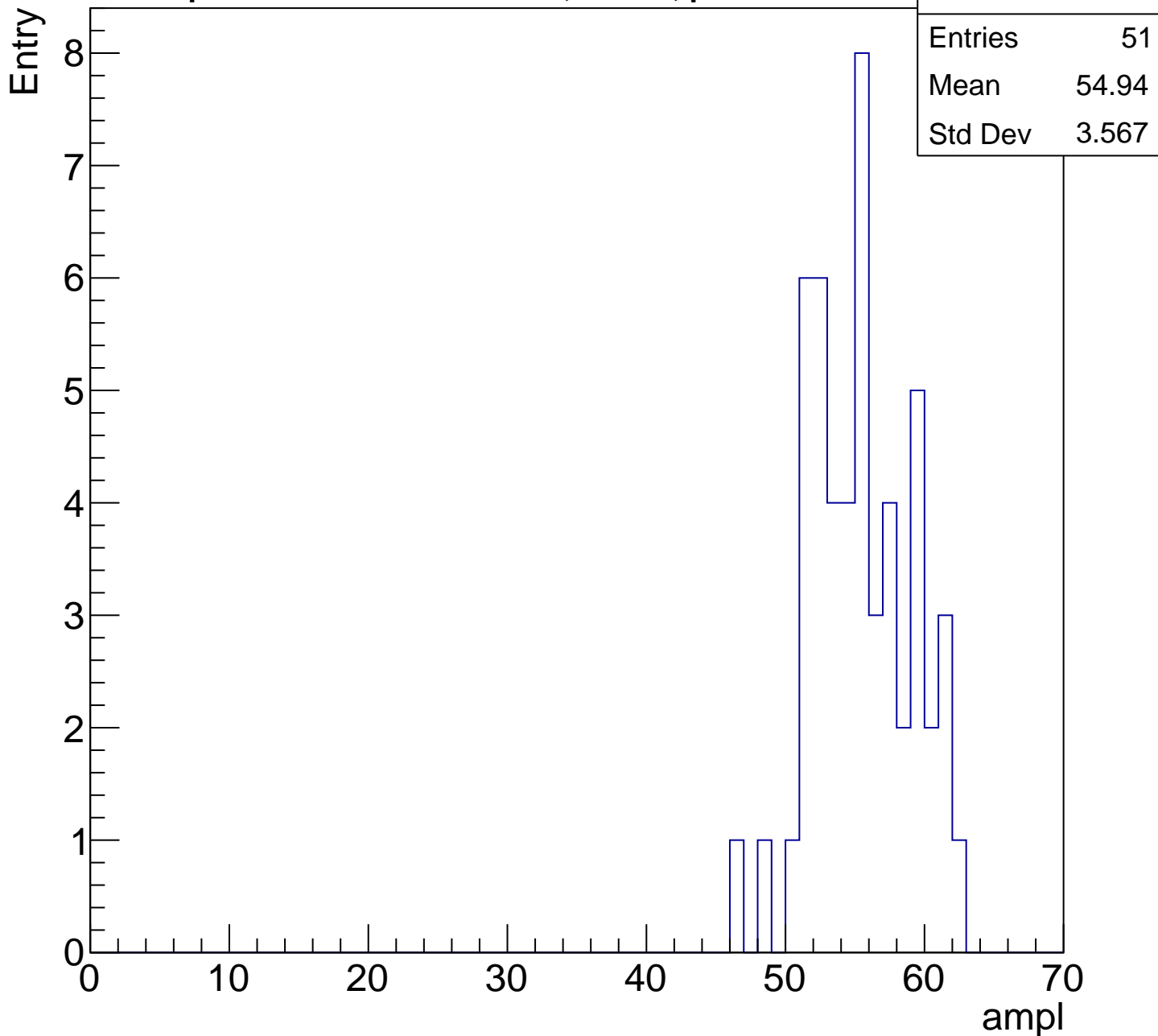
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

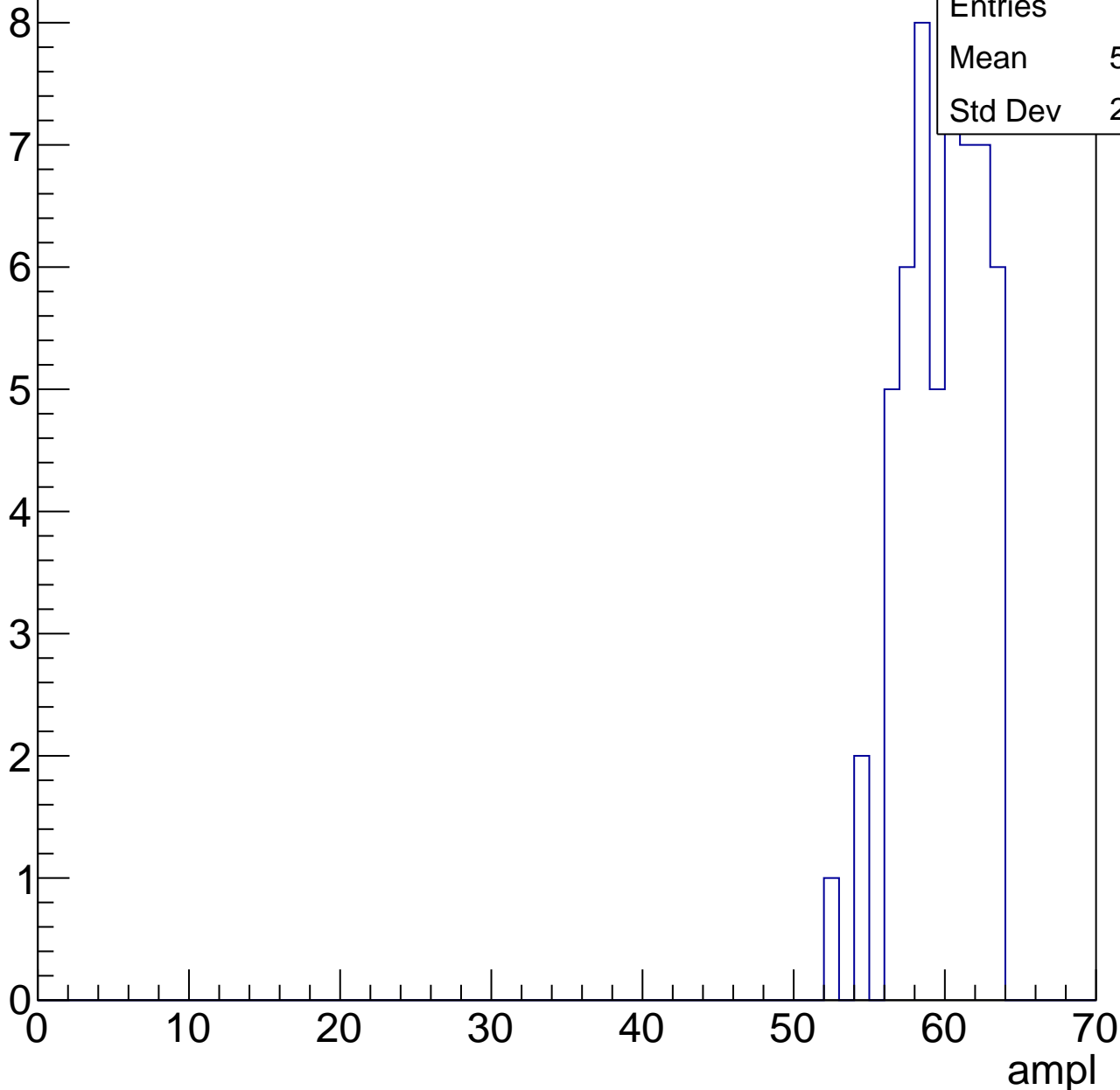


# B0L102S, U6-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

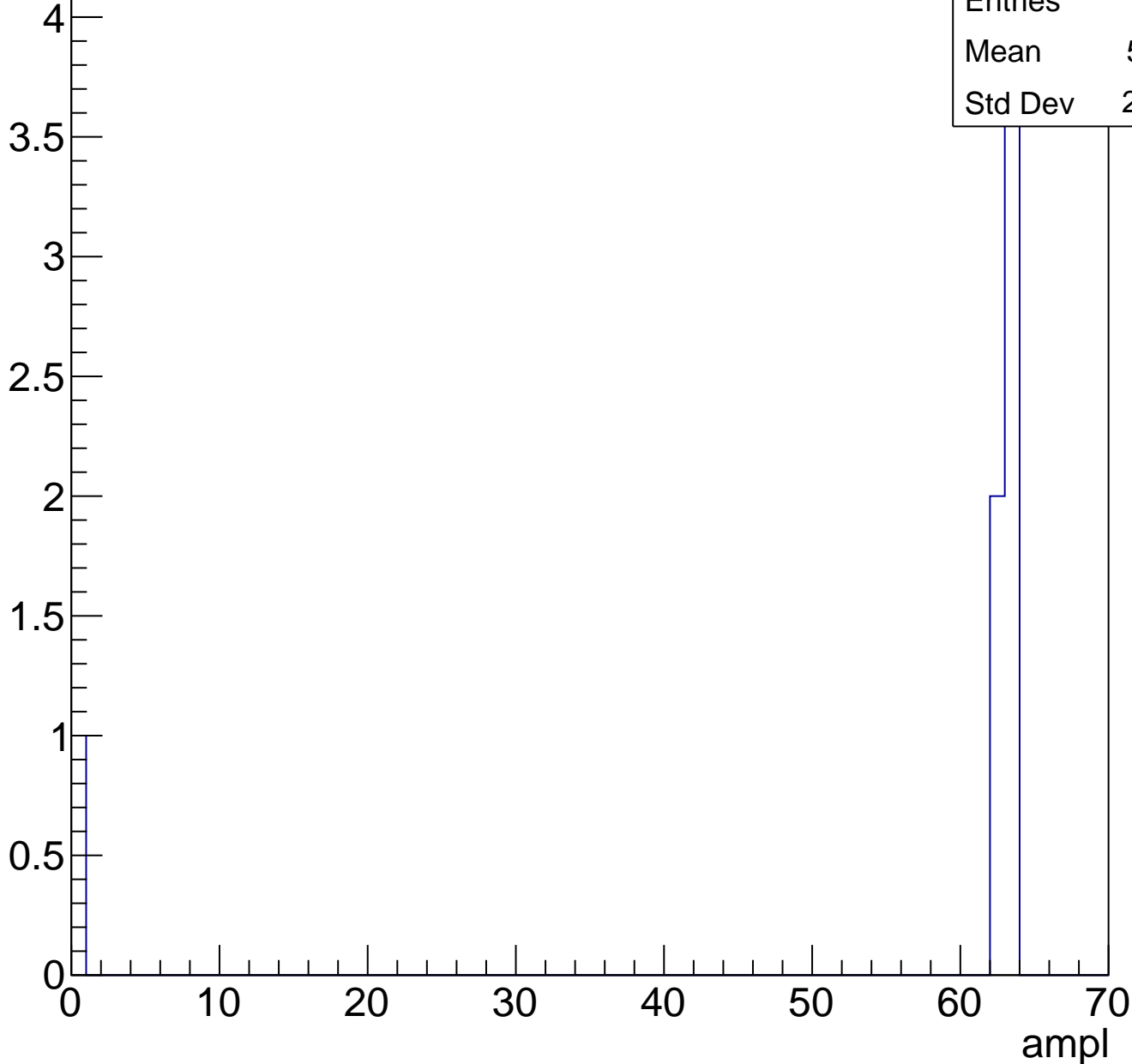
Entries	55
Mean	59.27
Std Dev	2.583



# B0L102S, U6-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch57, adc0

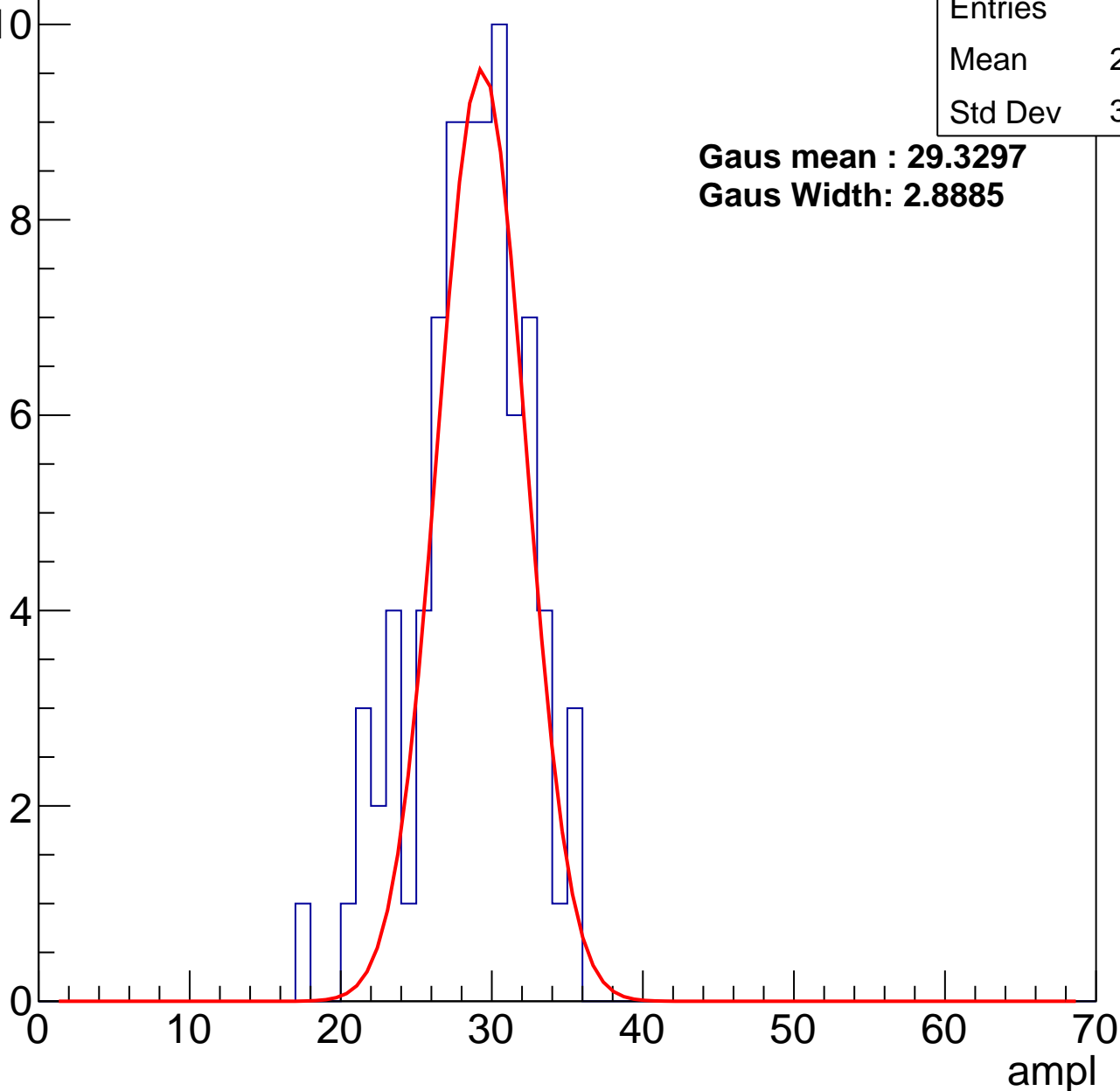
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	28.14
Std Dev	3.668

**Gaus mean : 29.3297**

**Gaus Width: 2.8885**



# B0L102S, U6-ch57, adc1

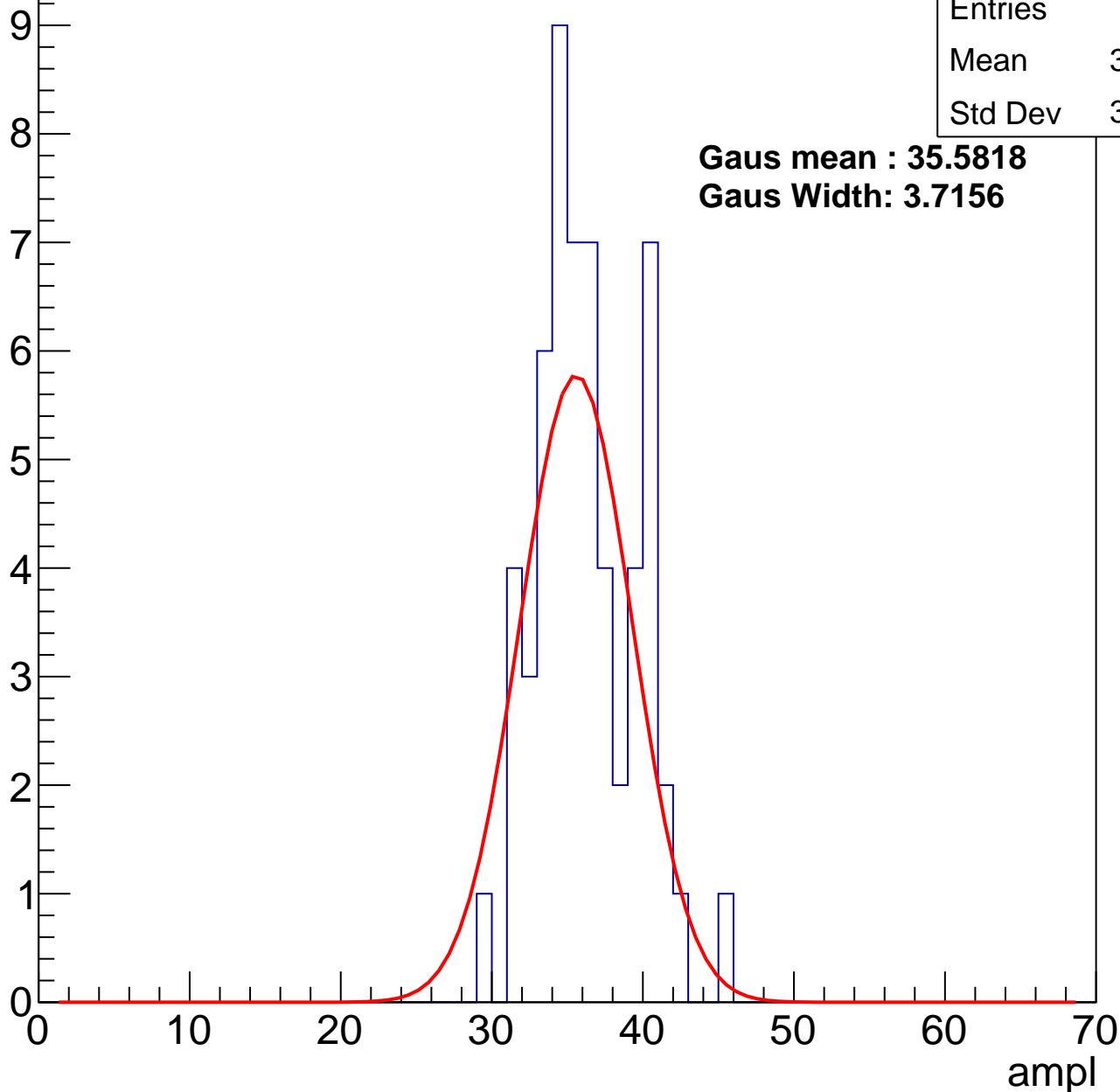
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	35.84
Std Dev	3.268

**Gaus mean : 35.5818**

**Gaus Width: 3.7156**



# B0L102S, U6-ch57, adc2

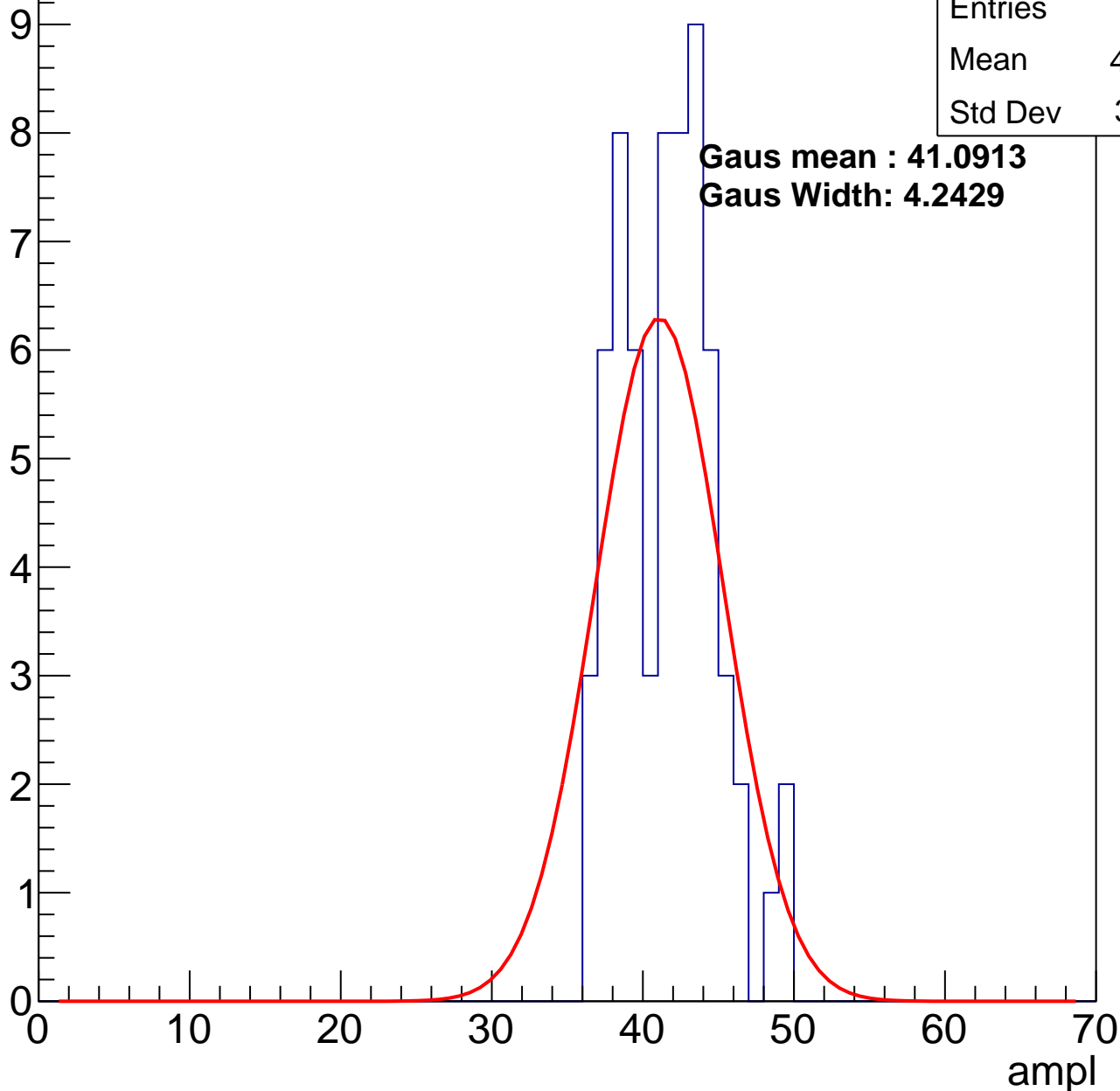
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.17
Std Dev	3.141

**Gaus mean : 41.0913**

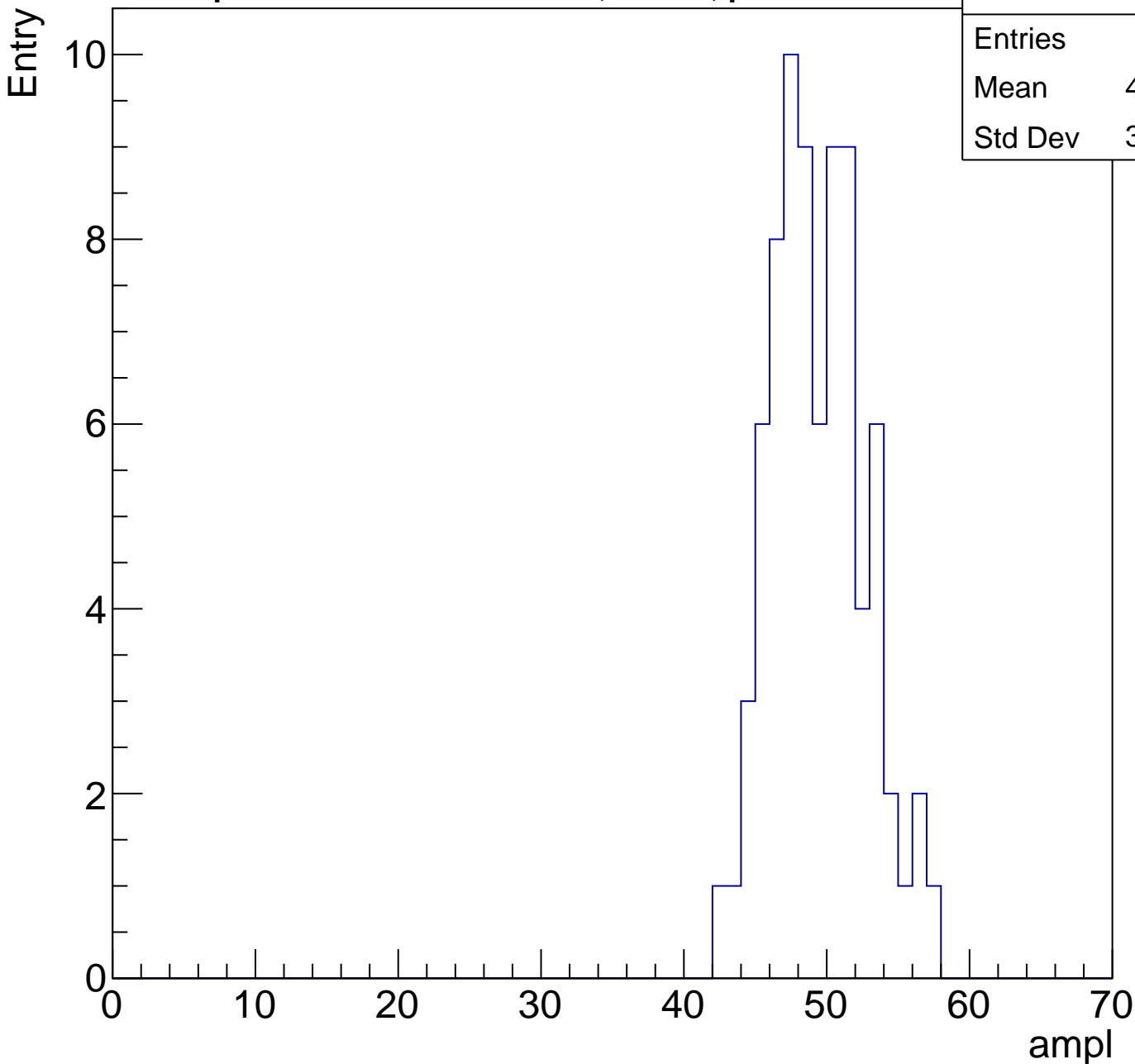
**Gaus Width: 4.2429**



# B0L102S, U6-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	78
Mean	48.95
Std Dev	3.206



# B0L102S, U6-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

6

5

4

3

2

1

0

Entries 41

Mean 54.68

Std Dev 3.432

0

10

20

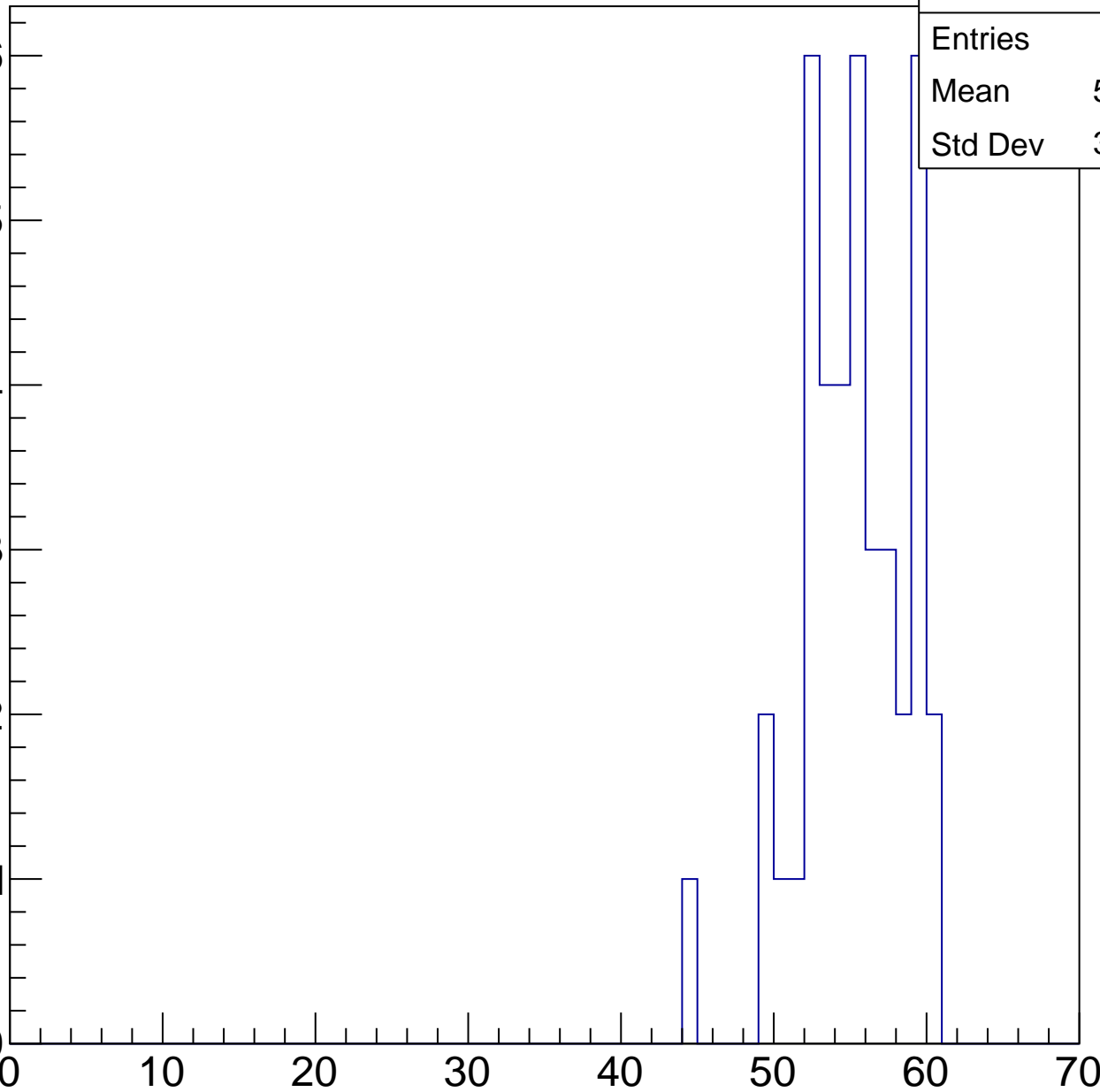
30

40

50

60

ampl



# B0L102S, U6-ch57, adc5

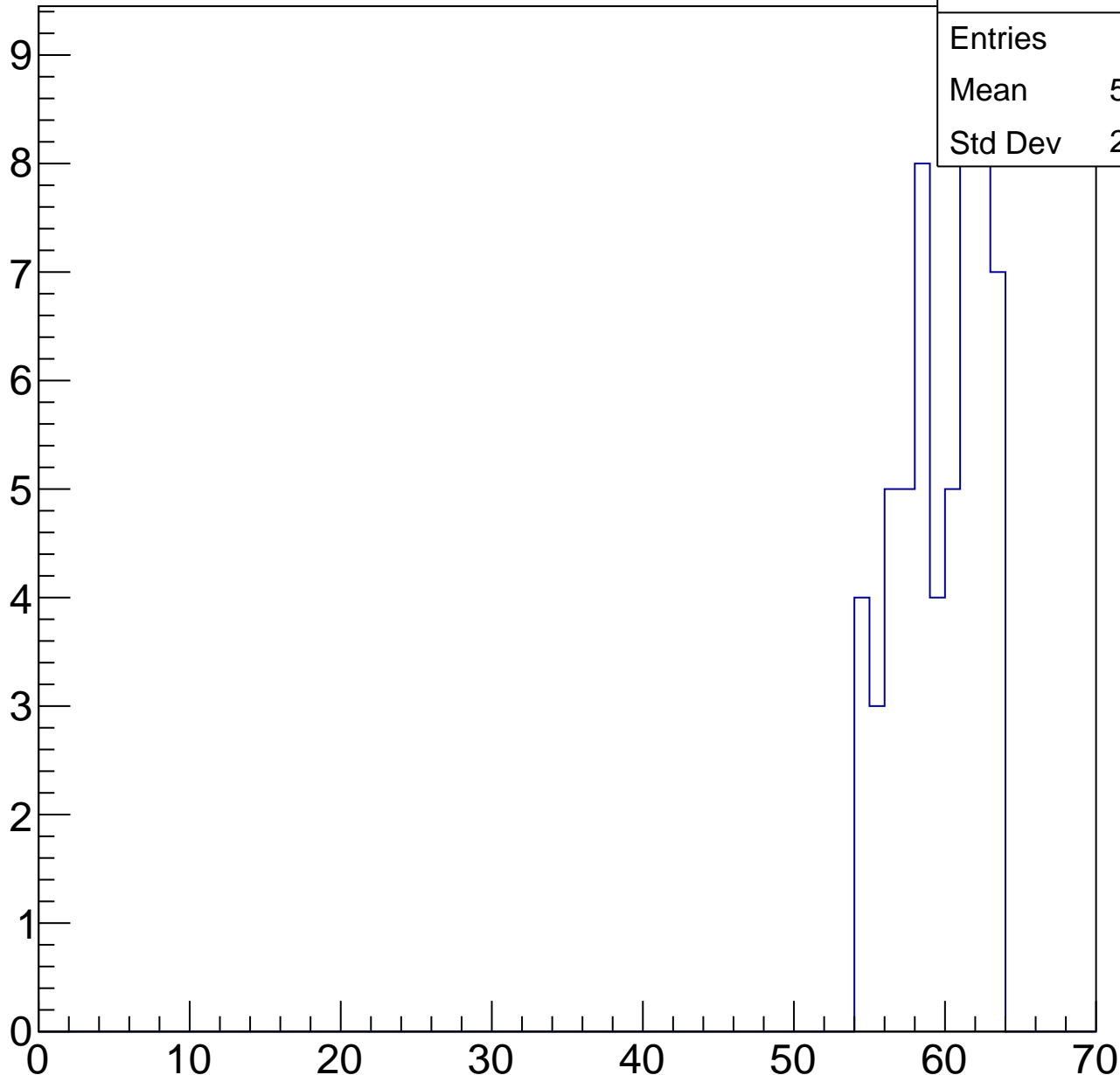
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	59.19
Std Dev	2.782

ampl



# B0L102S, U6-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

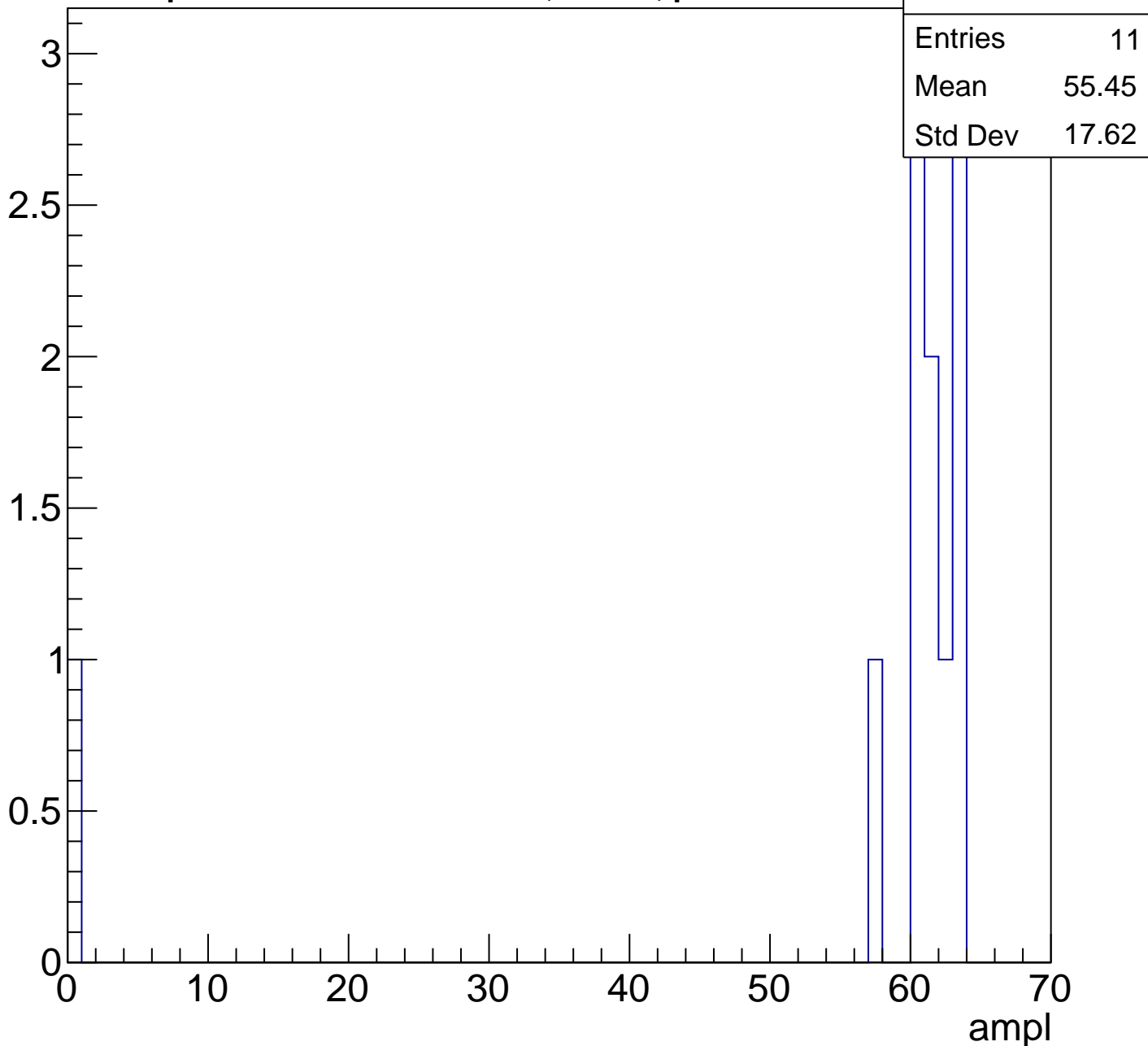
Mean

55.45

Std Dev

17.62

ampl





# B0L102S, U6-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch58, adc0

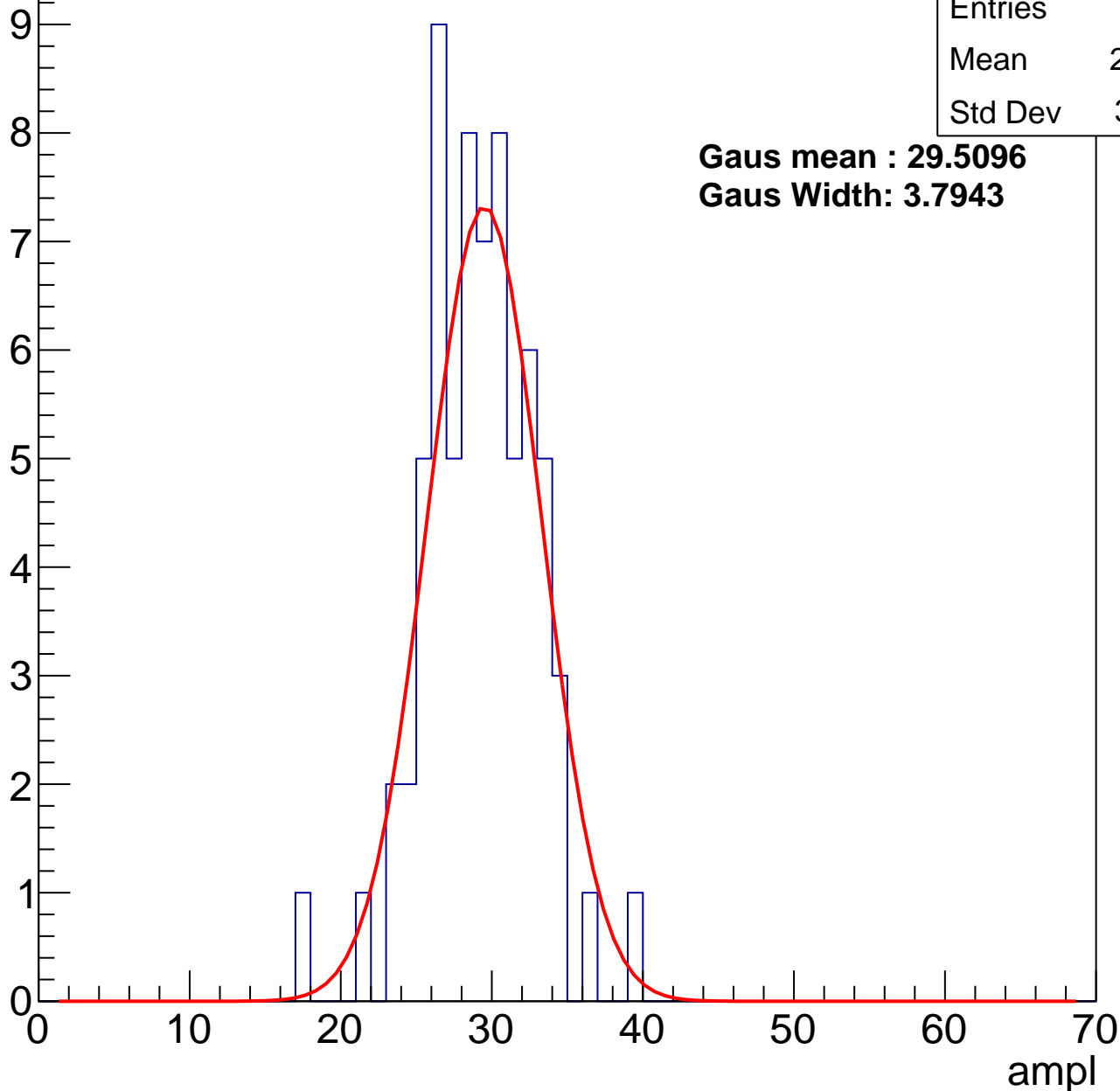
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	28.72
Std Dev	3.611

**Gaus mean : 29.5096**

**Gaus Width: 3.7943**



# B0L102S, U6-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	59
Mean	34.17
Std Dev	3.618

**Gaus mean : 35.8335**

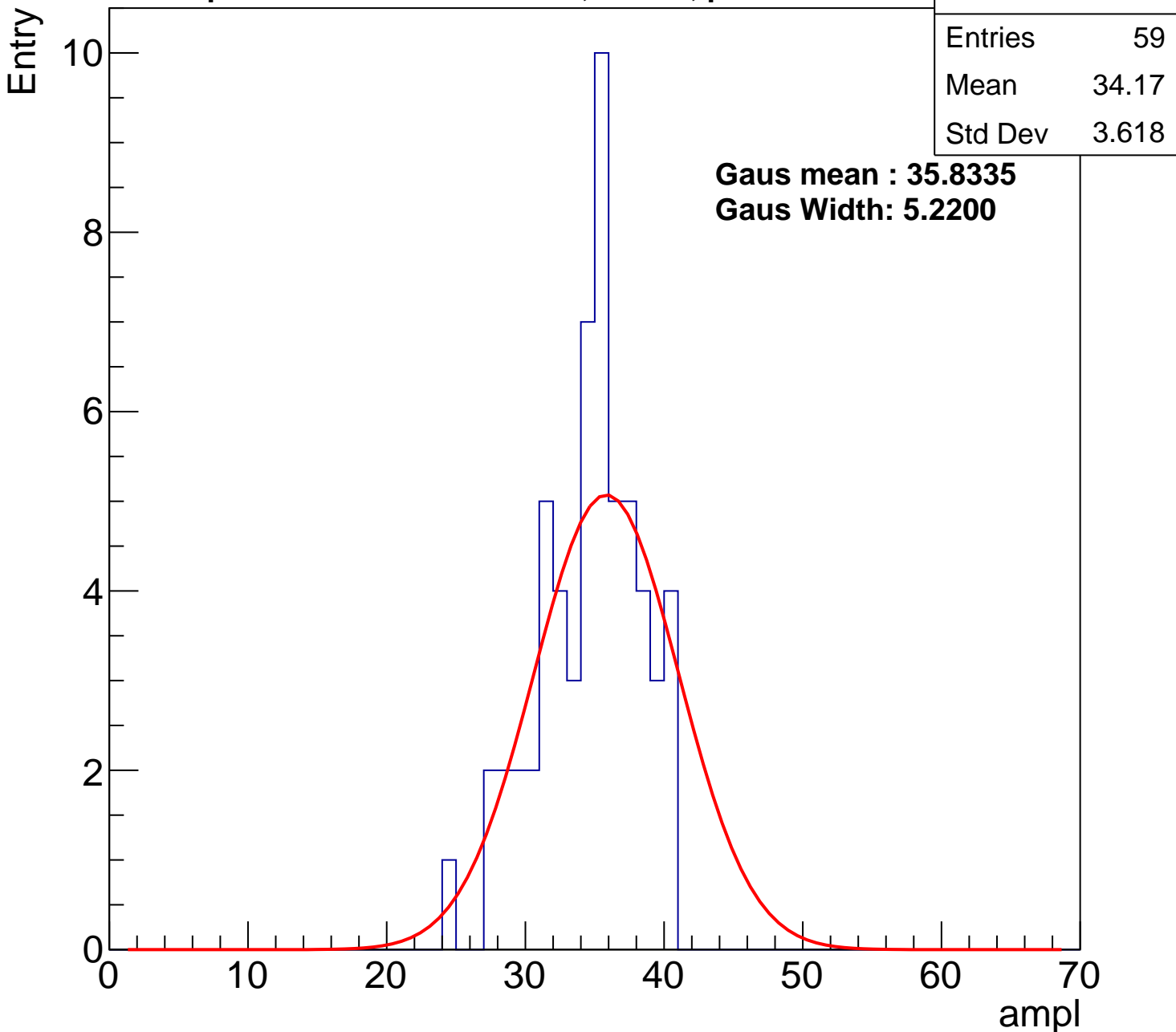
**Gaus Width: 5.2200**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U6-ch58, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	72
Mean	41.19
Std Dev	3.29

**Gaus mean : 41.2590**

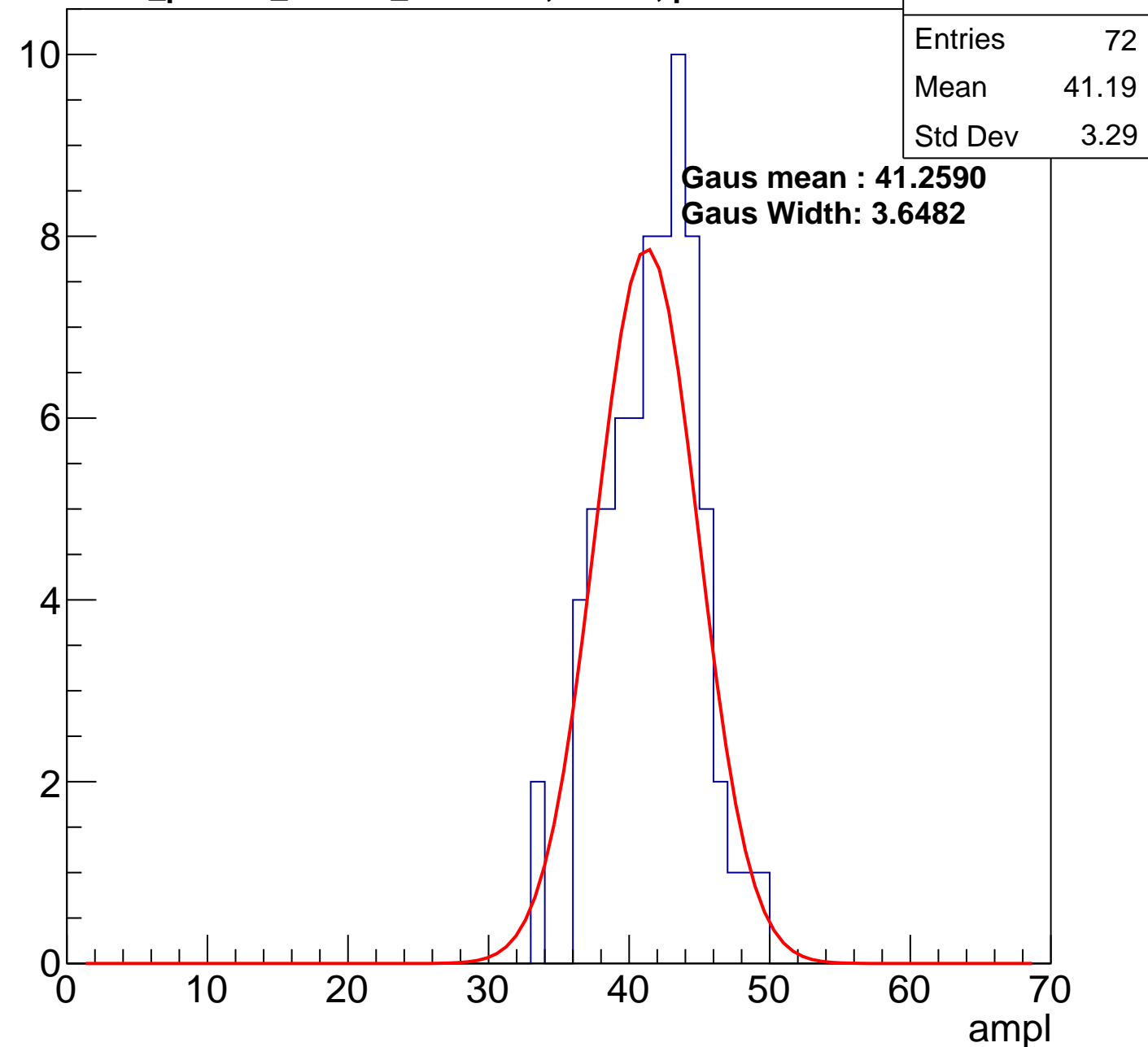
**Gaus Width: 3.6482**

Entry

10  
8  
6  
4  
2  
0

ampl

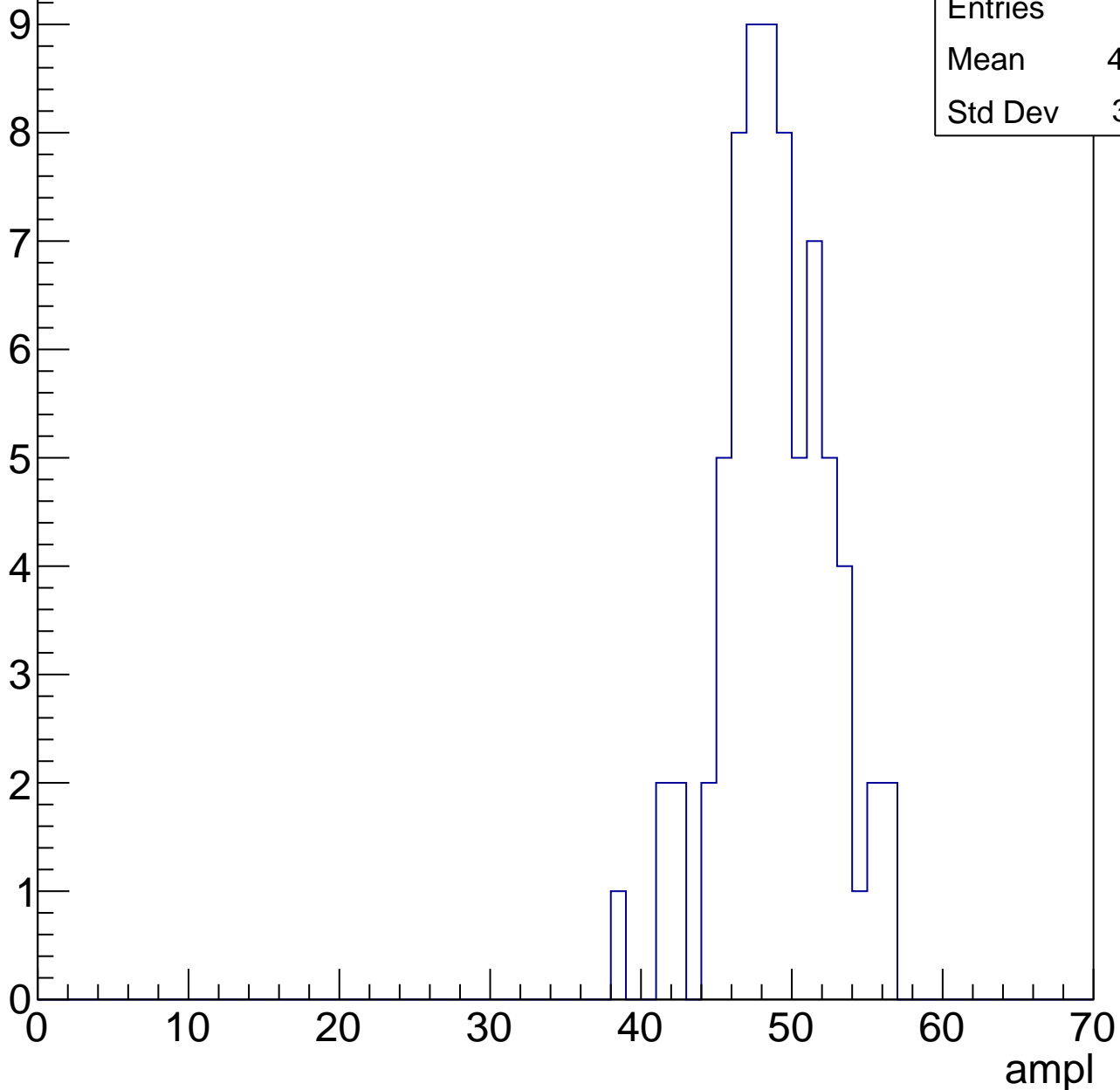
0 10 20 30 40 50 60 70



# B0L102S, U6-ch58, adc3

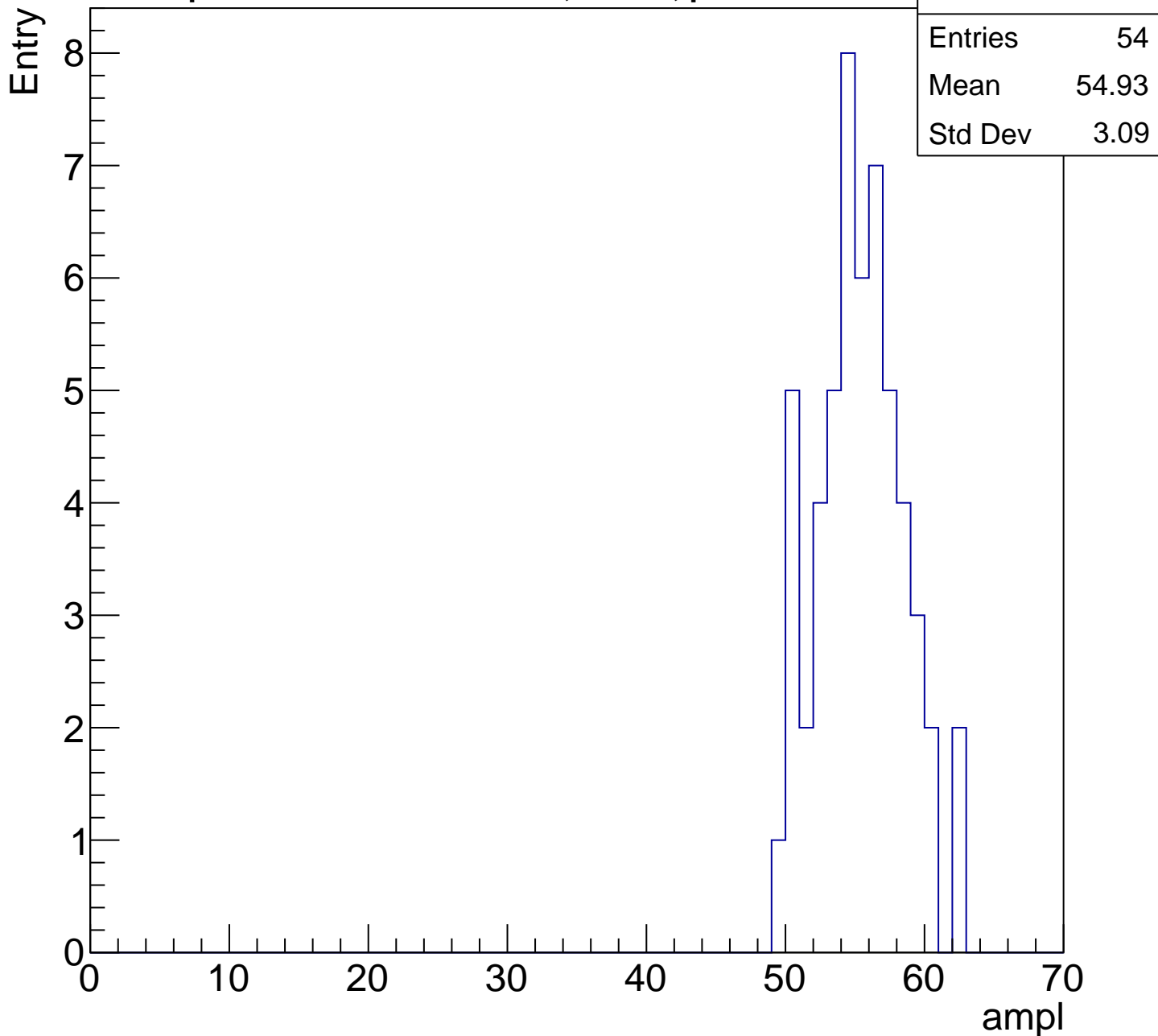
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

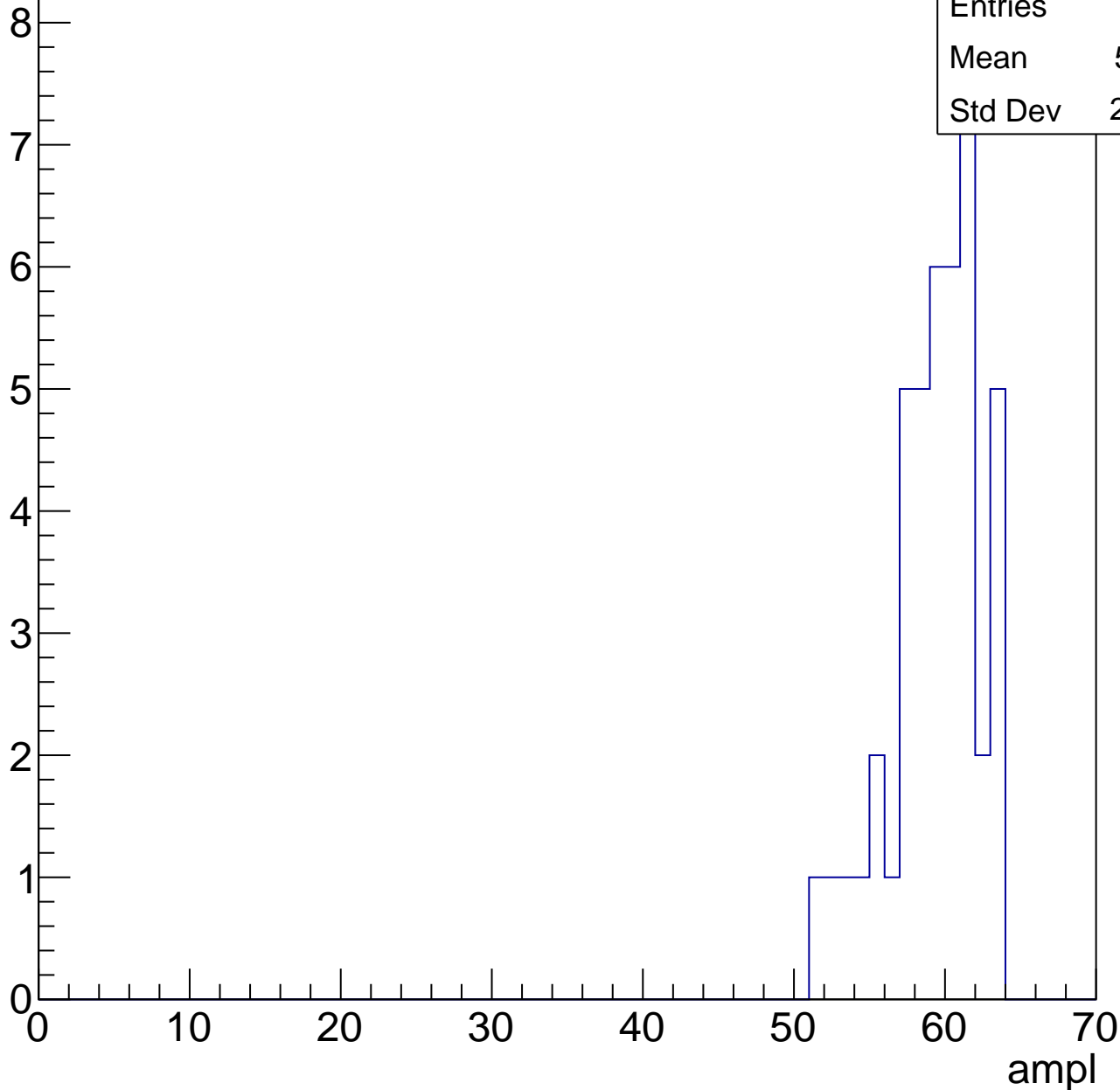


# B0L102S, U6-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	58.91
Std Dev	2.922

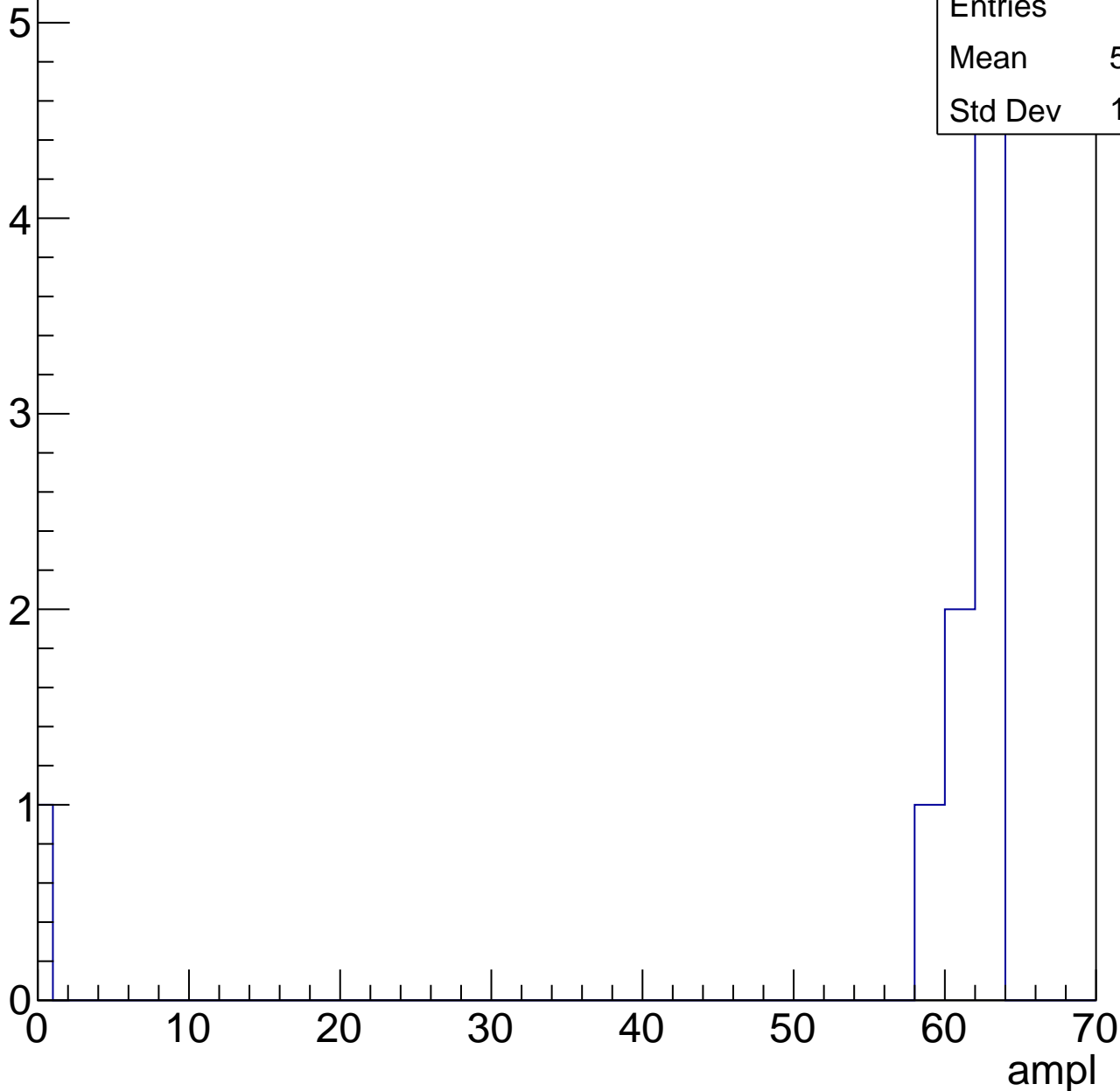


# B0L102S, U6-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	17
Mean	57.88
Std Dev	14.54





# B0L102S, U6-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch59, adc0

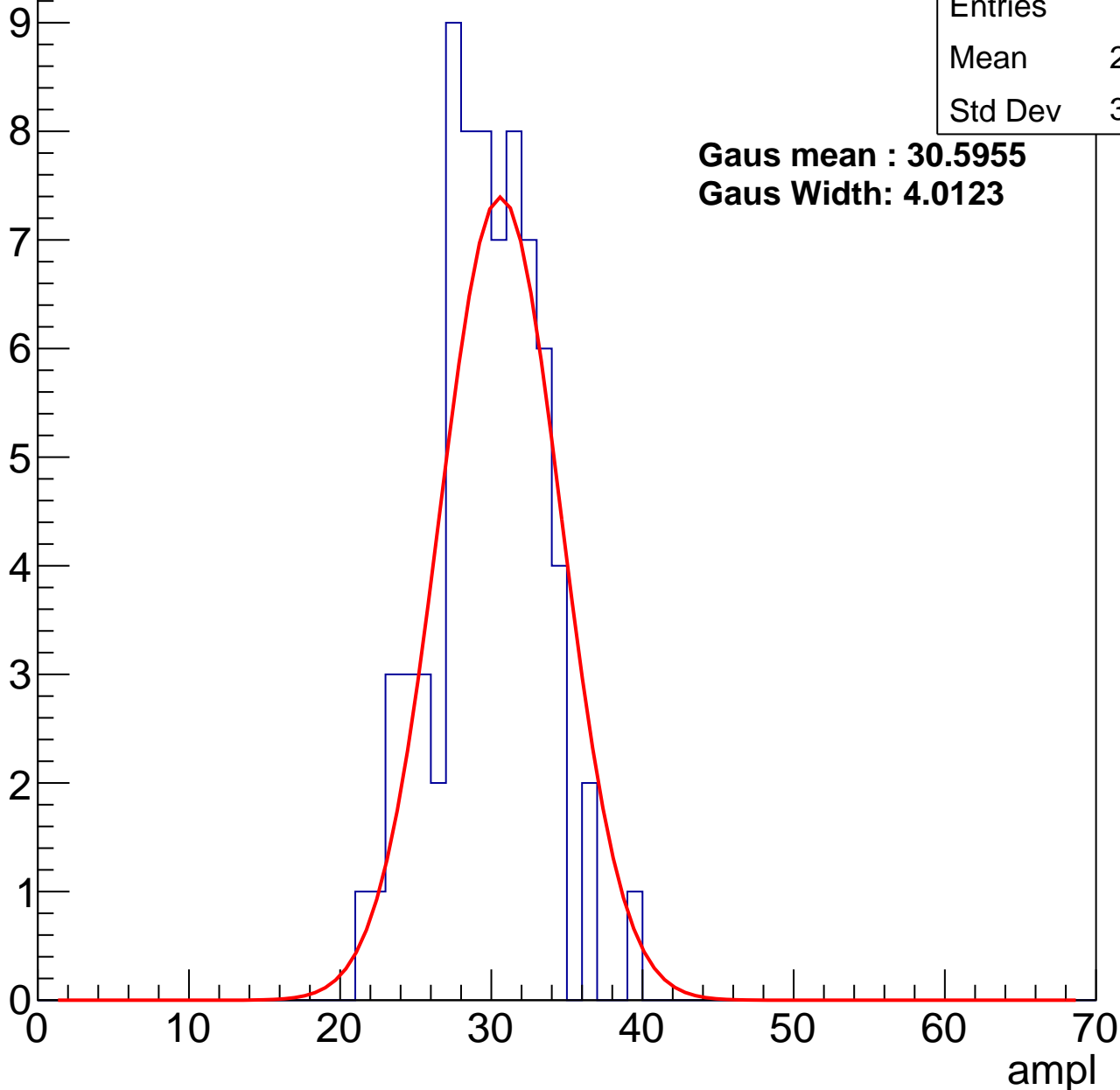
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	29.27
Std Dev	3.504

**Gaus mean : 30.5955**

**Gaus Width: 4.0123**



# B0L102S, U6-ch59, adc1

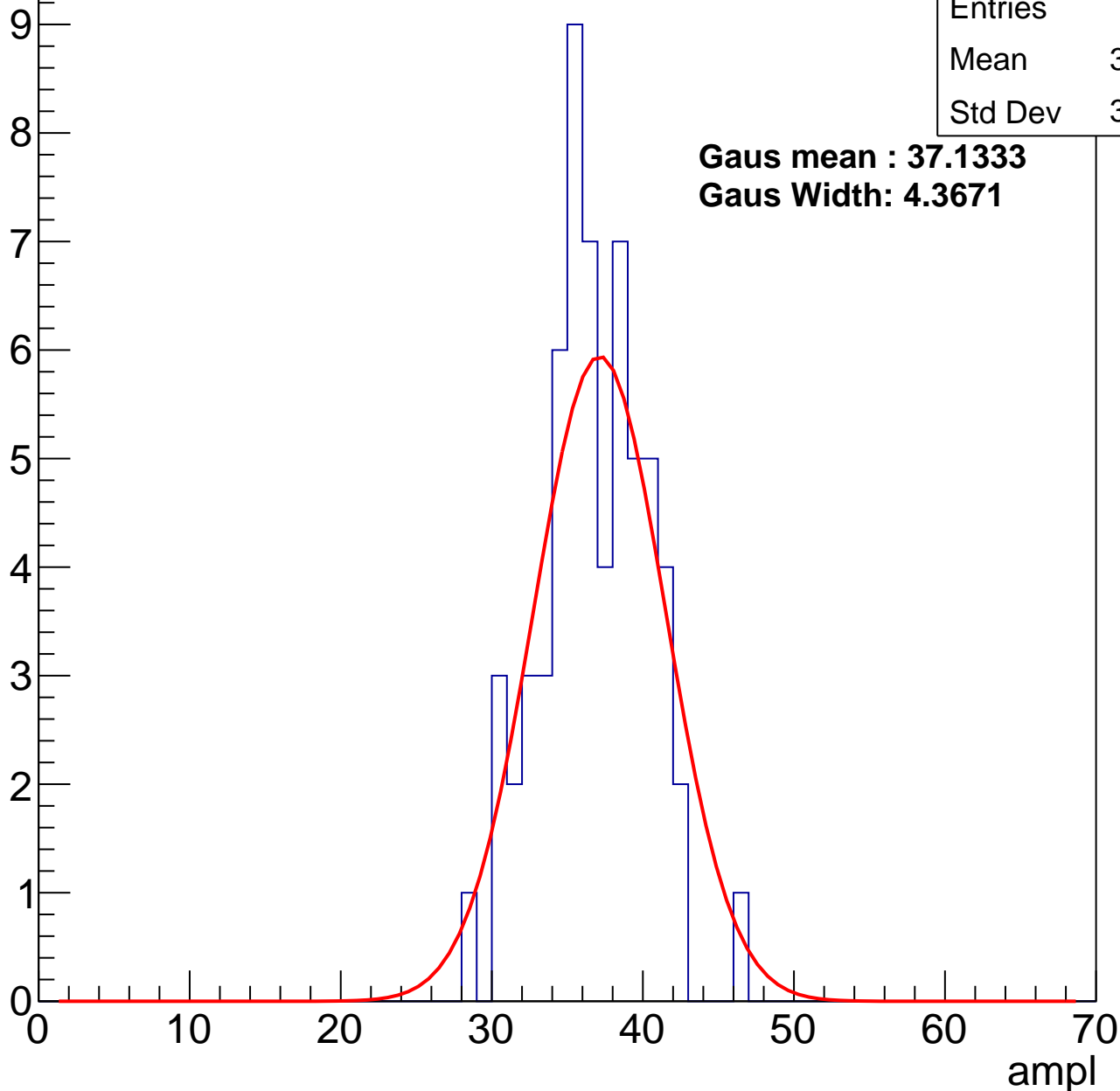
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	36.27
Std Dev	3.488

**Gaus mean : 37.1333**

**Gaus Width: 4.3671**



# B0L102S, U6-ch59, adc2

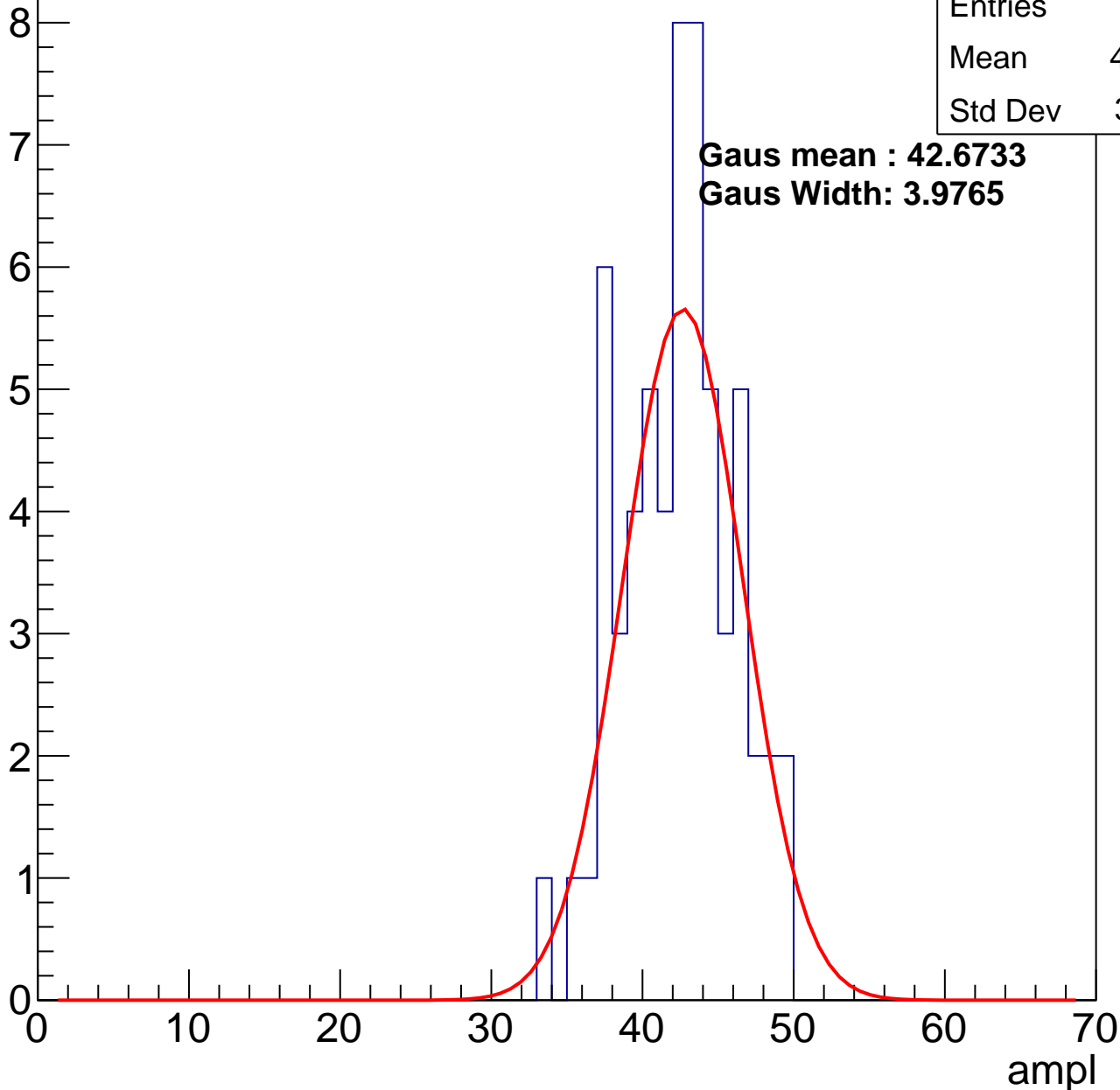
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.88
Std Dev	3.601

**Gaus mean : 42.6733**

**Gaus Width: 3.9765**

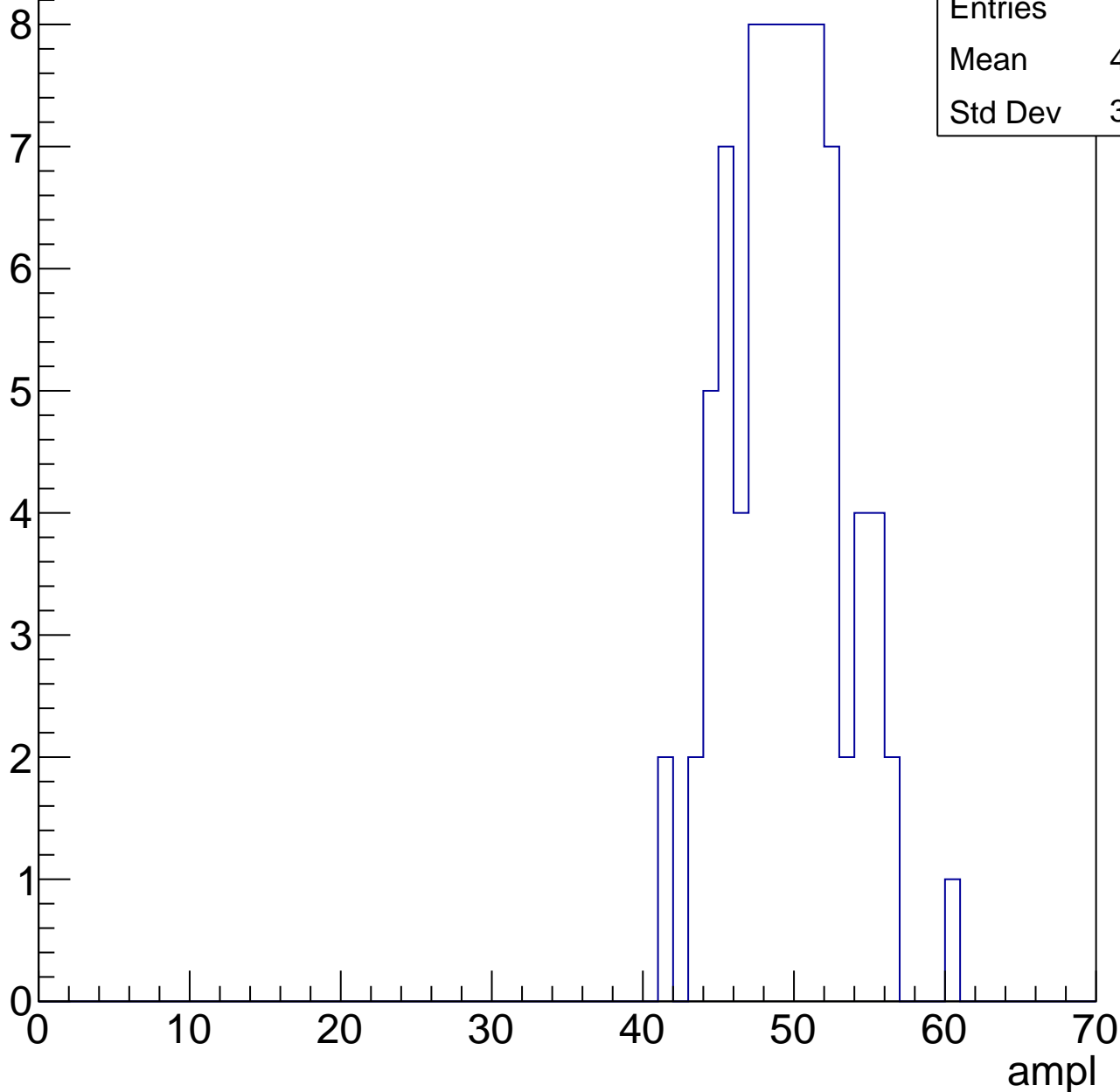


# B0L102S, U6-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	49.06
Std Dev	3.726

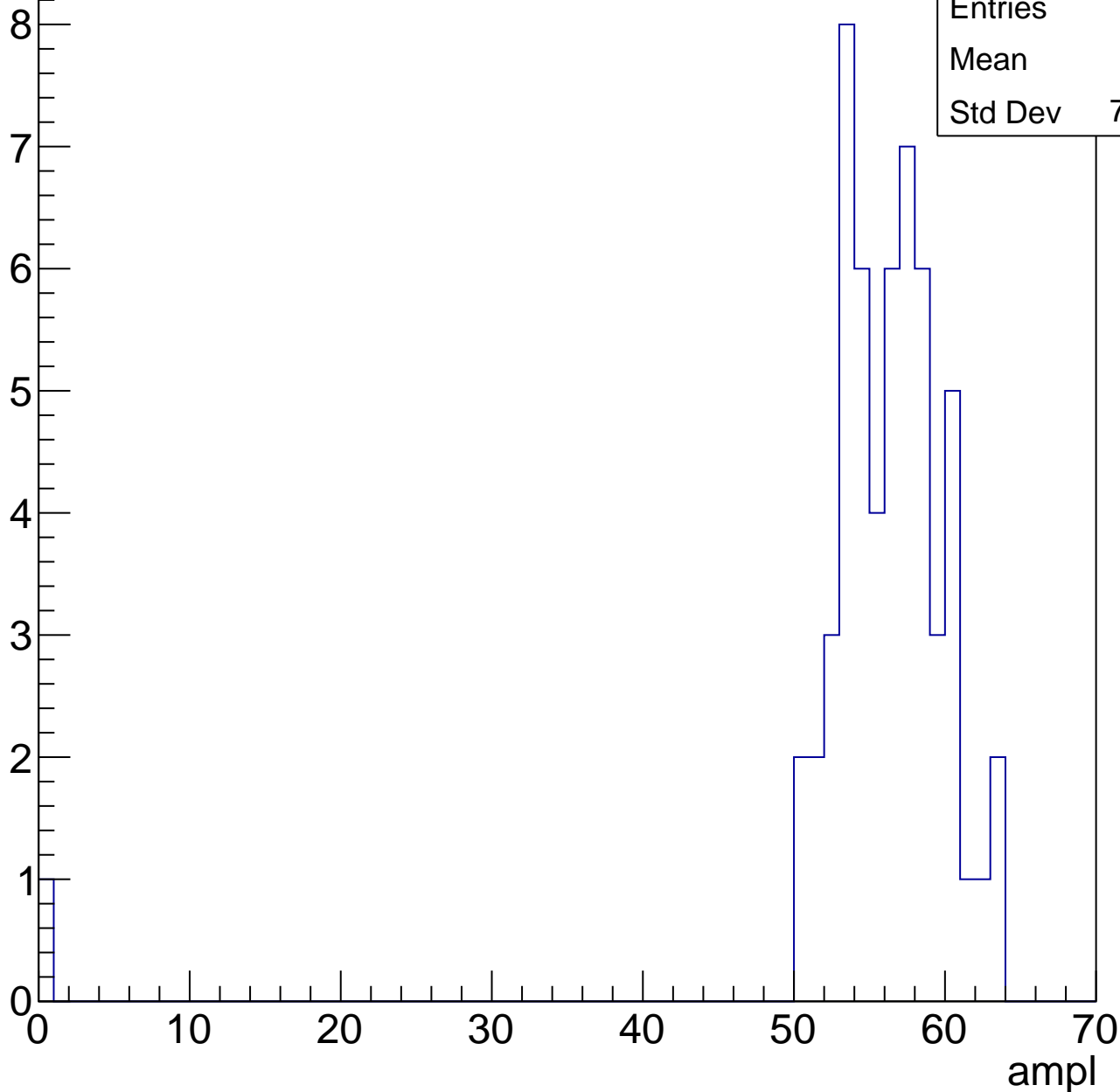


# B0L102S, U6-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	55
Std Dev	7.998



# B0L102S, U6-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

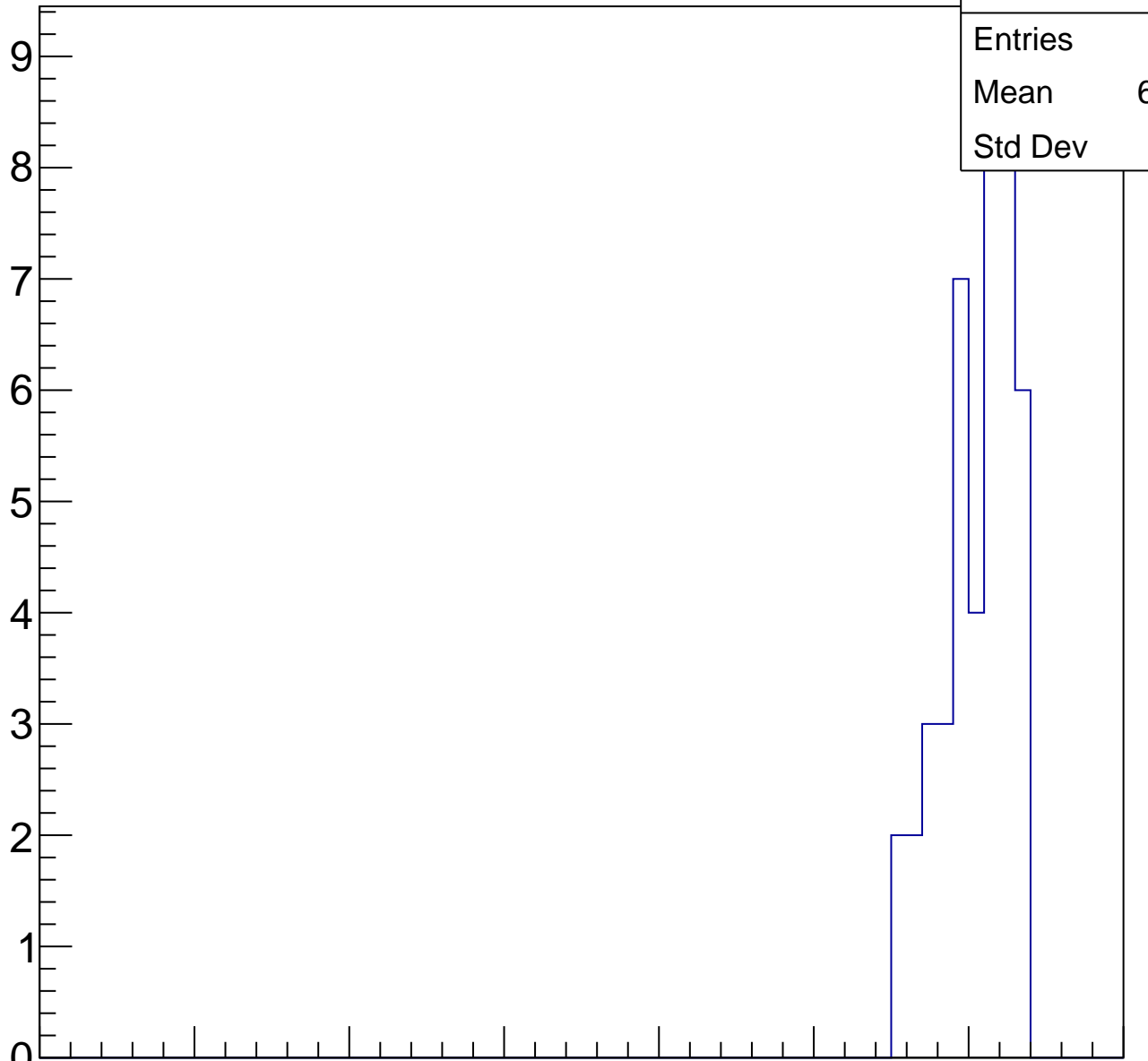
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	60.07
Std Dev	2.25

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

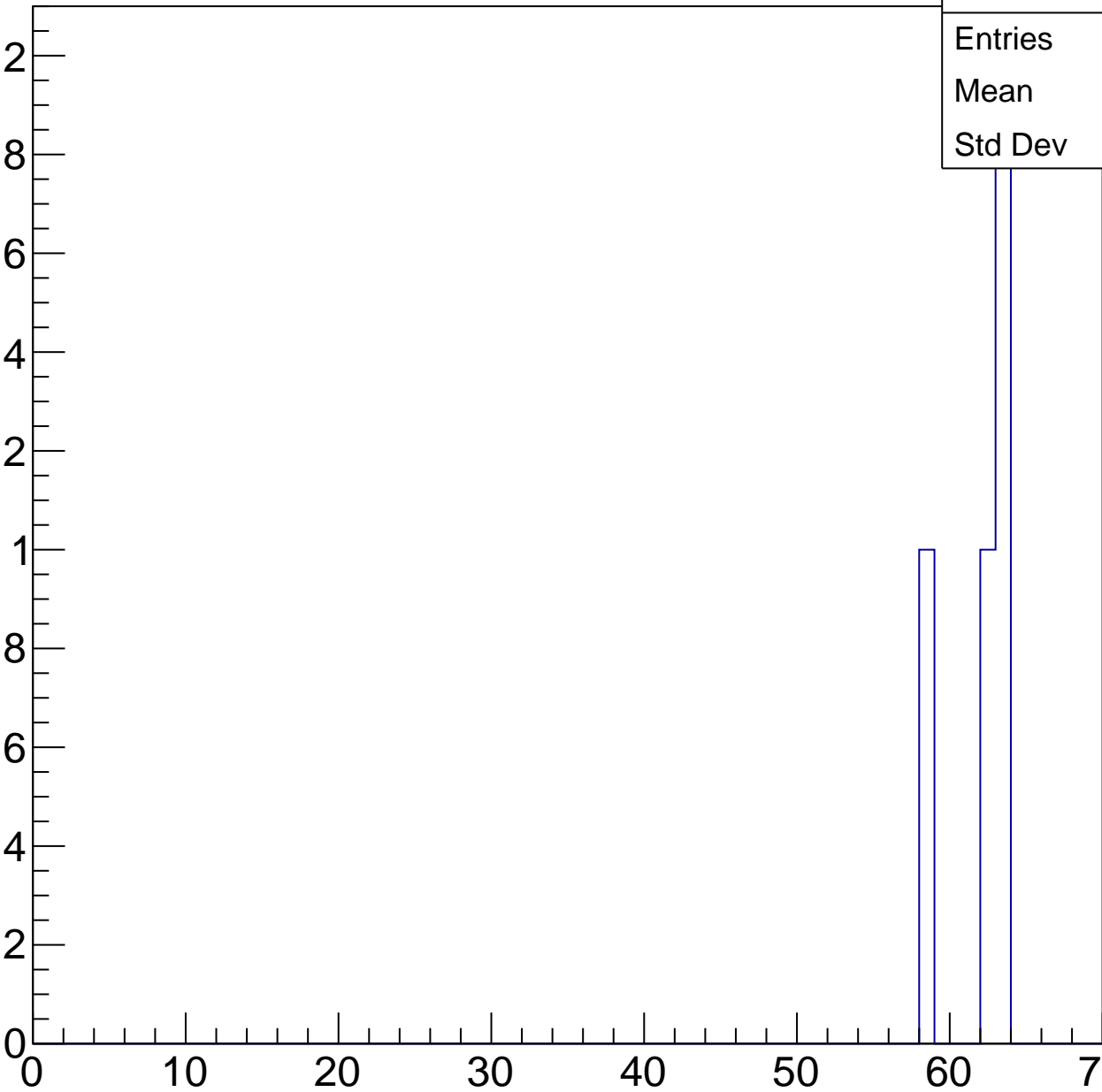
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.5
Std Dev	2.062

0 10 20 30 40 50 60 70

ampl





# B0L102S, U6-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch60, adc0

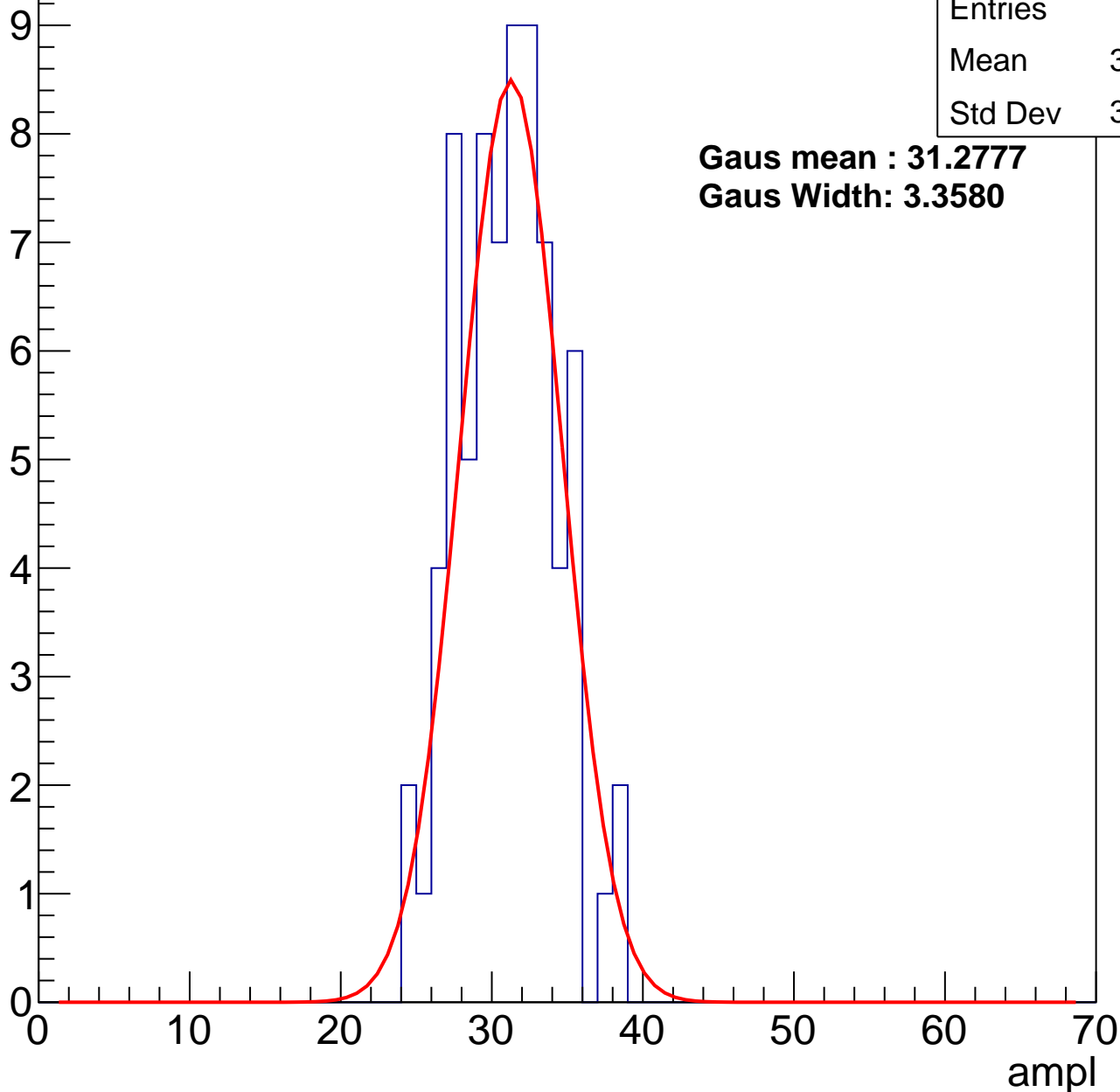
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	30.58
Std Dev	3.175

**Gaus mean : 31.2777**

**Gaus Width: 3.3580**



# B0L102S, U6-ch60, adc1

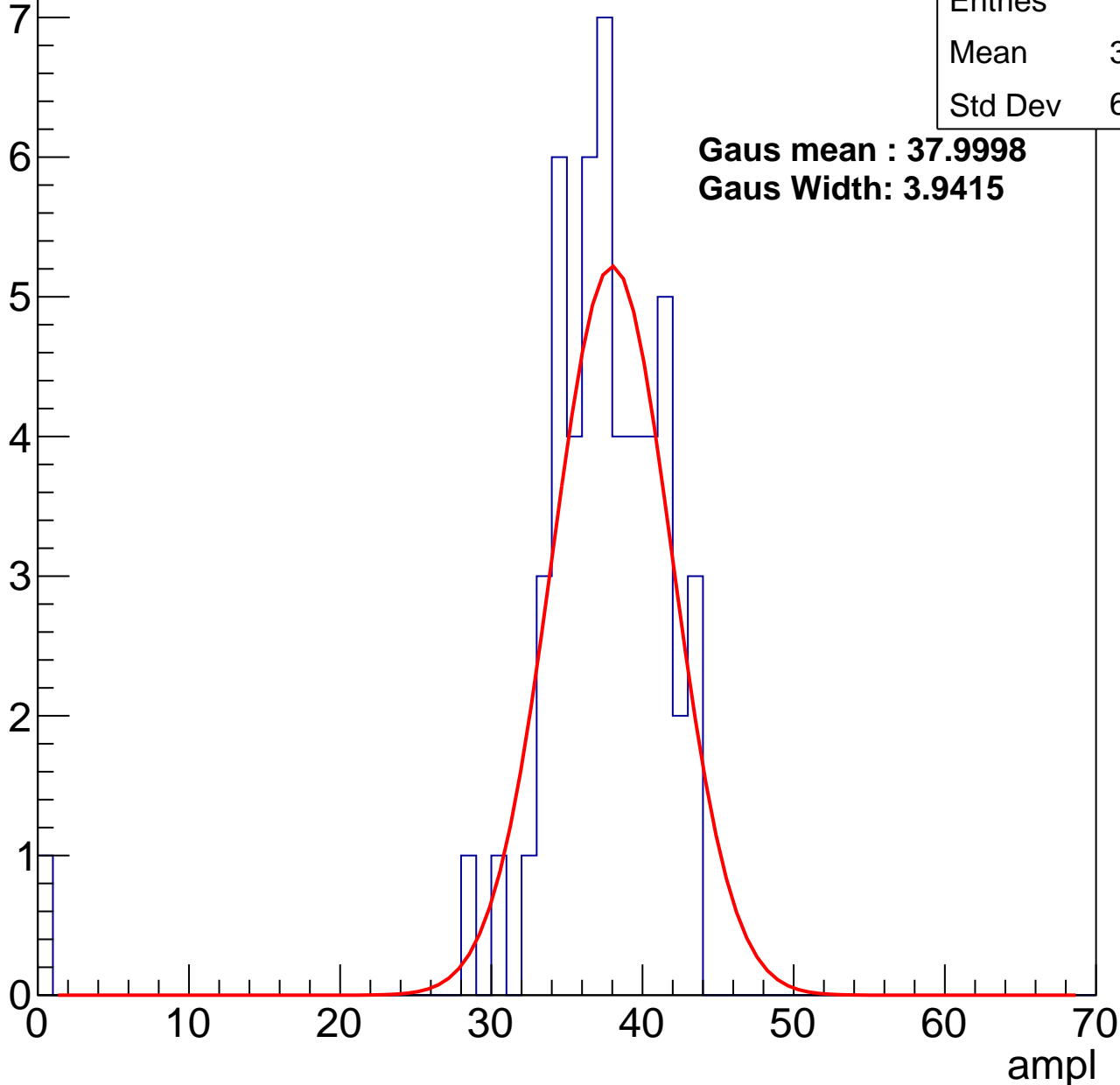
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	36.42
Std Dev	6.084

**Gaus mean : 37.9998**

**Gaus Width: 3.9415**



# B0L102S, U6-ch60, adc2

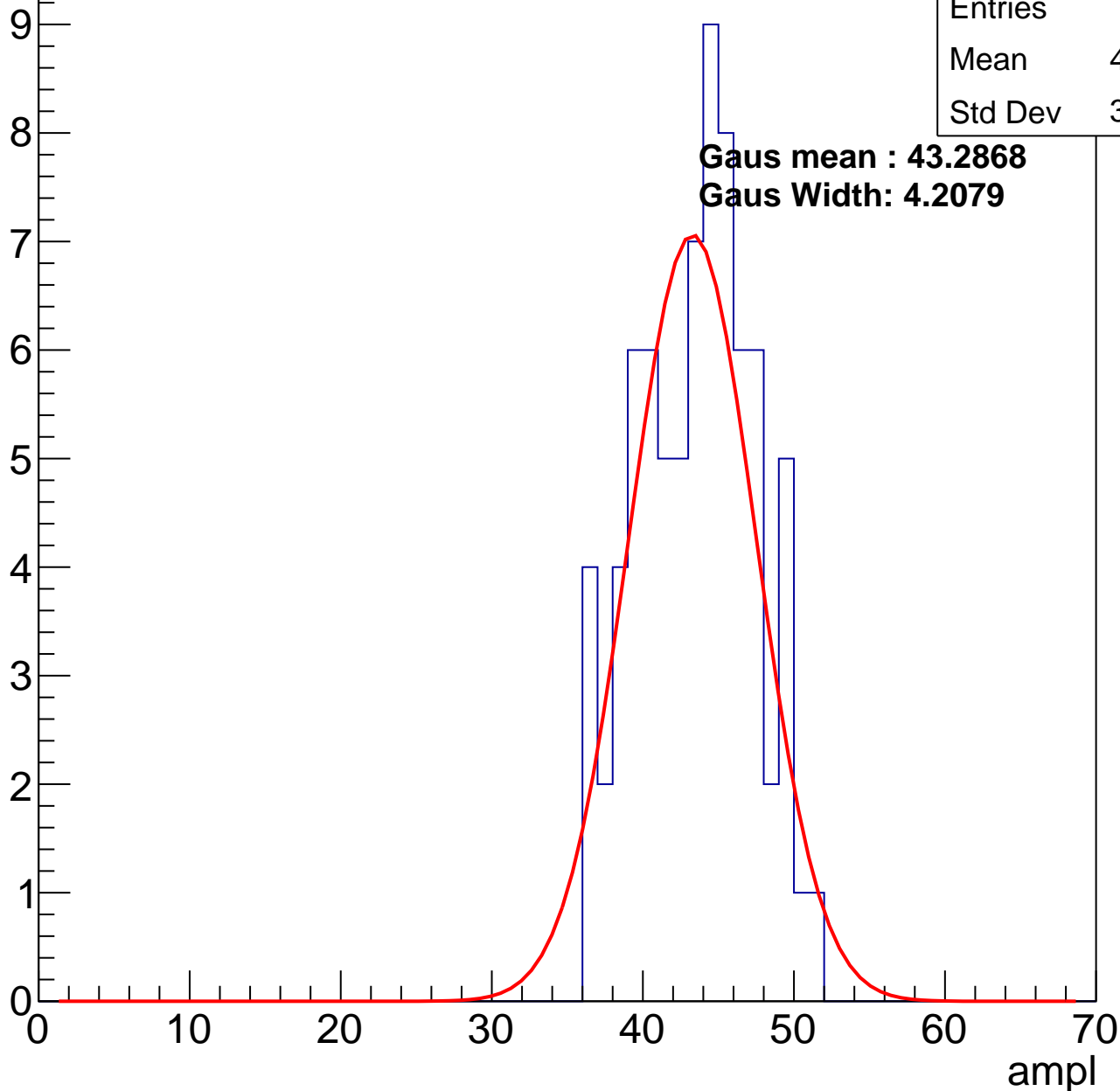
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	43.06
Std Dev	3.739

**Gaus mean : 43.2868**

**Gaus Width: 4.2079**

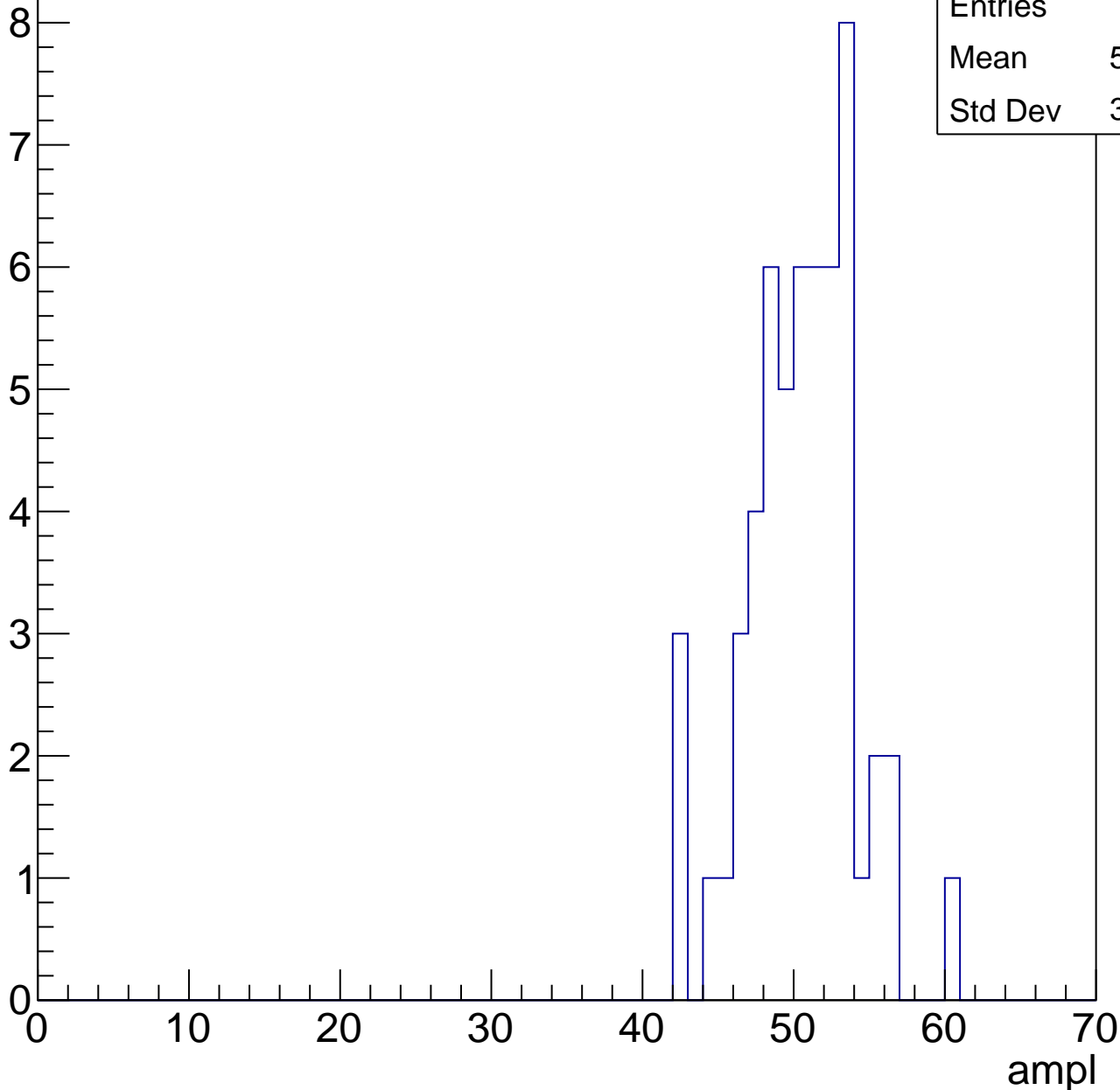


# B0L102S, U6-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	50.04
Std Dev	3.583

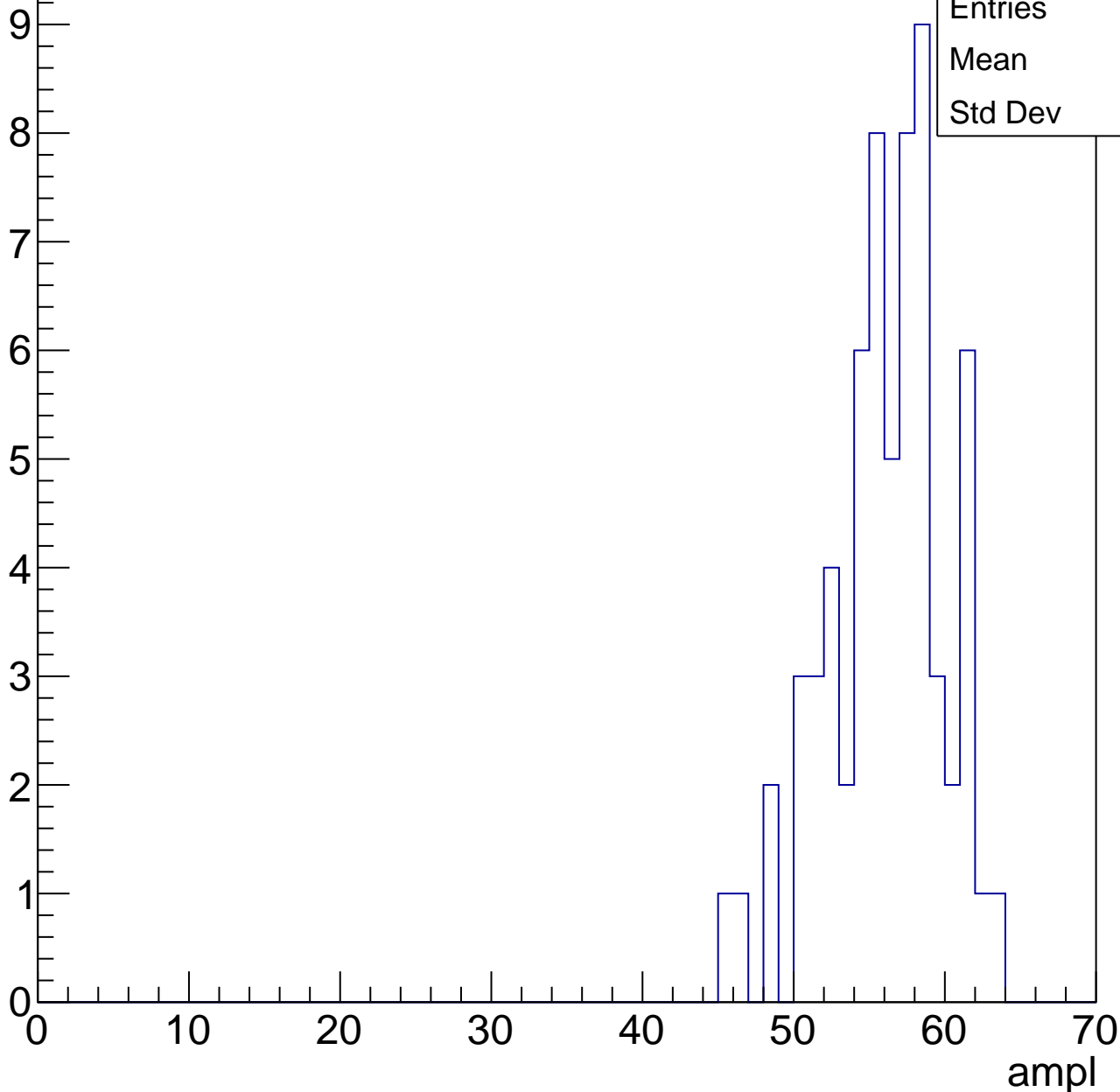


# B0L102S, U6-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

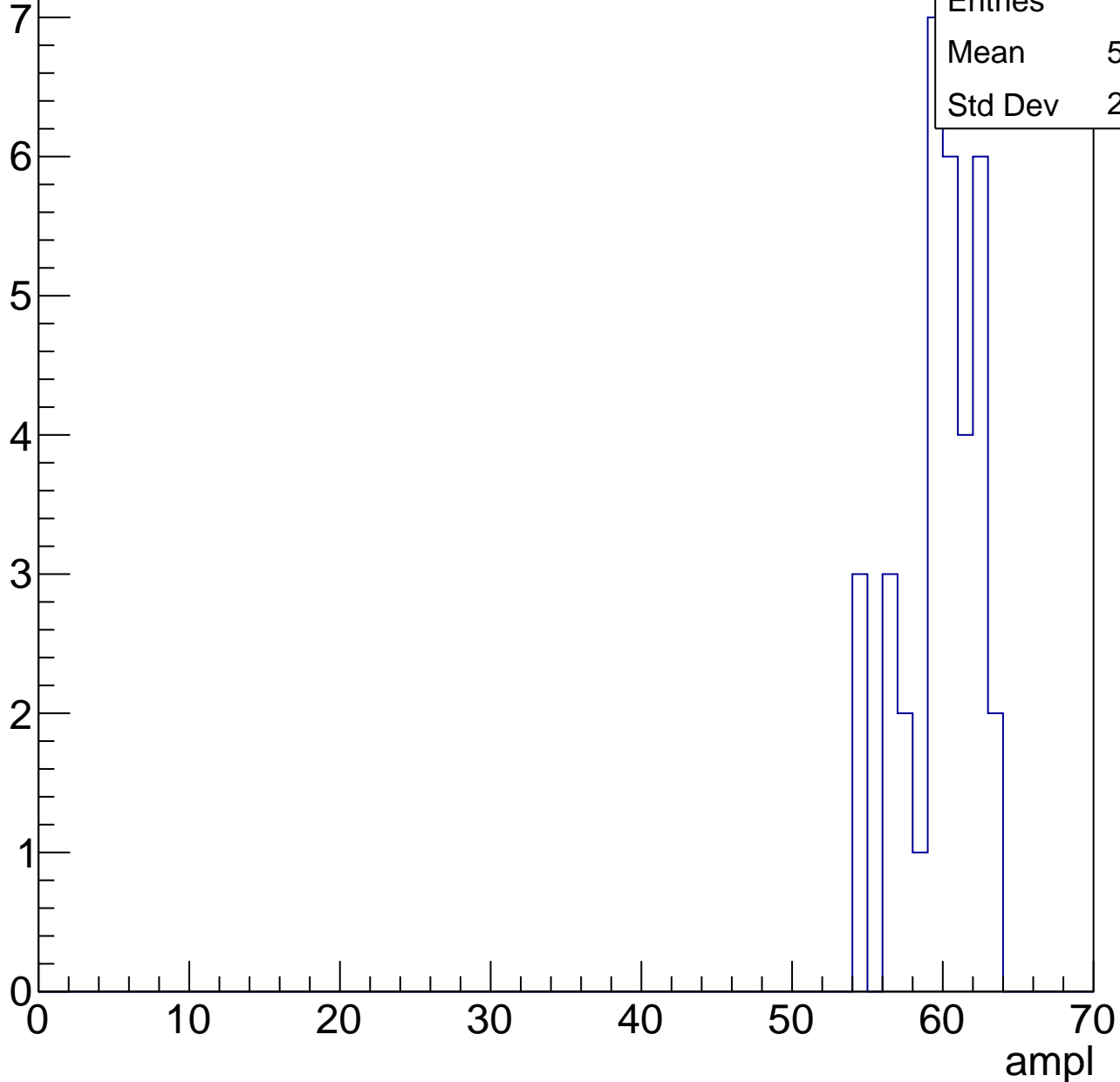
Entries	65
Mean	55.6
Std Dev	3.87



# B0L102S, U6-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

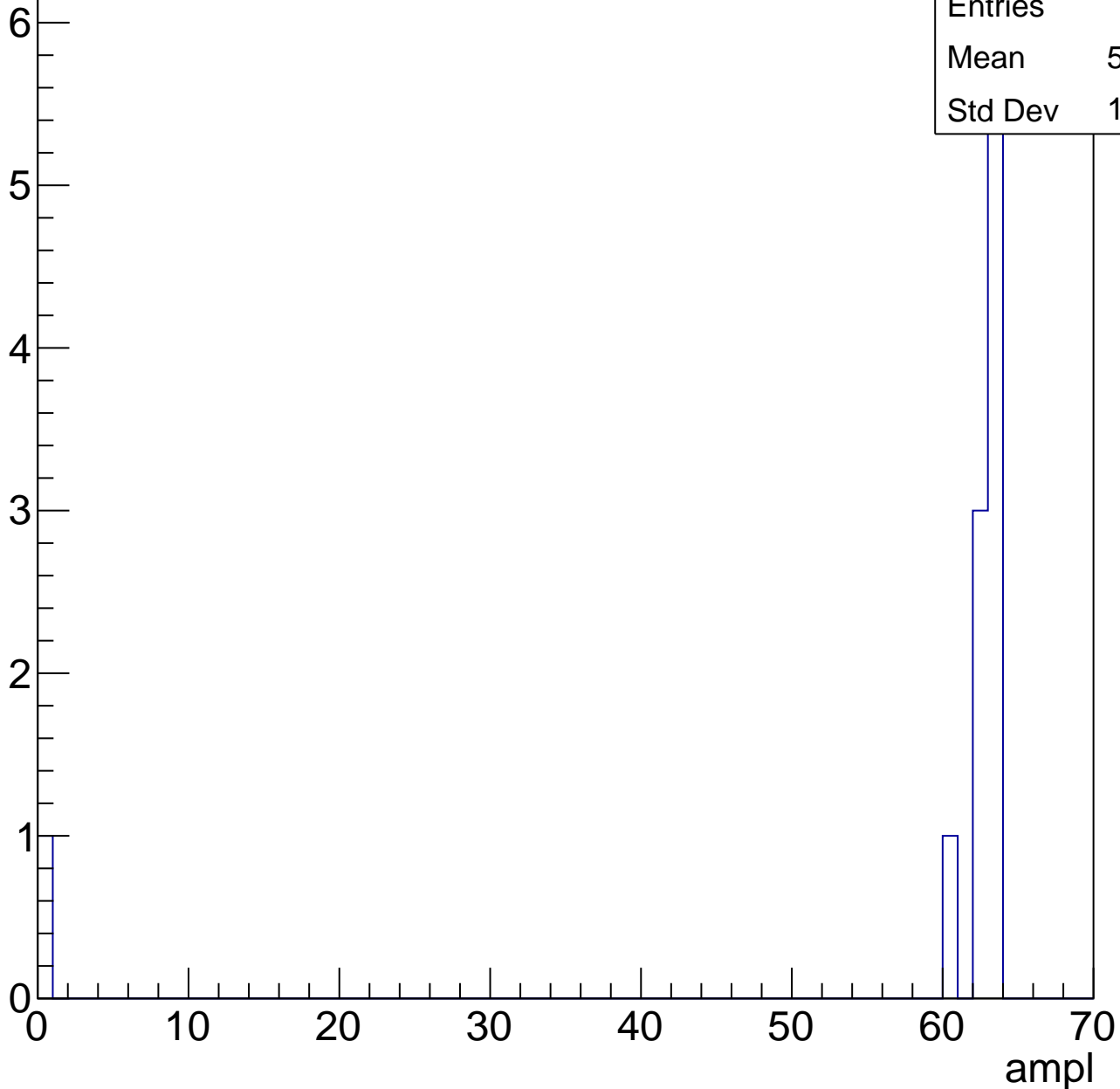


# B0L102S, U6-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	11
Mean	56.73
Std Dev	17.96





# B0L102S, U6-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch61, adc0

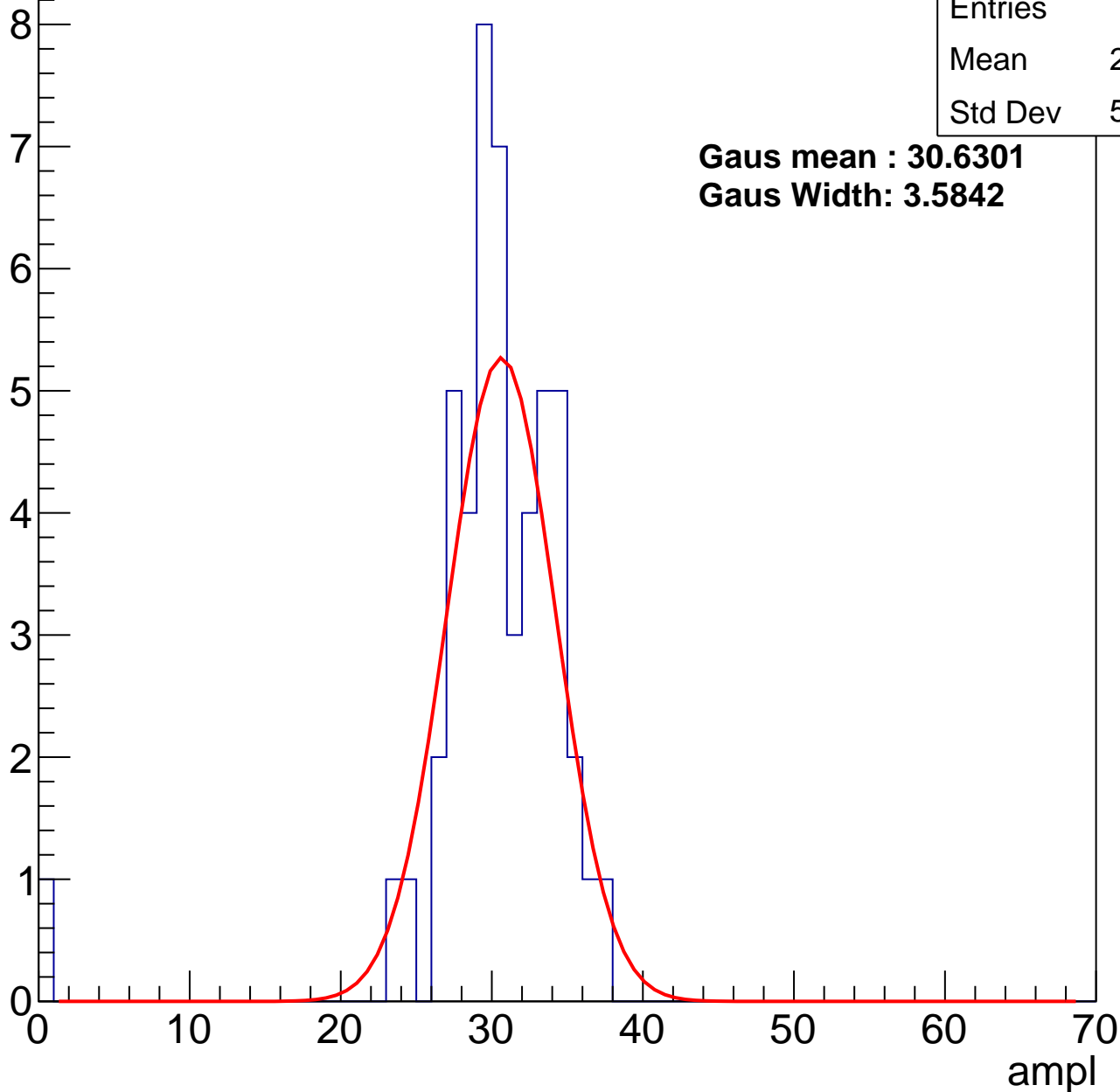
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	29.74
Std Dev	5.218

**Gaus mean : 30.6301**

**Gaus Width: 3.5842**



# B0L102S, U6-ch61, adc1

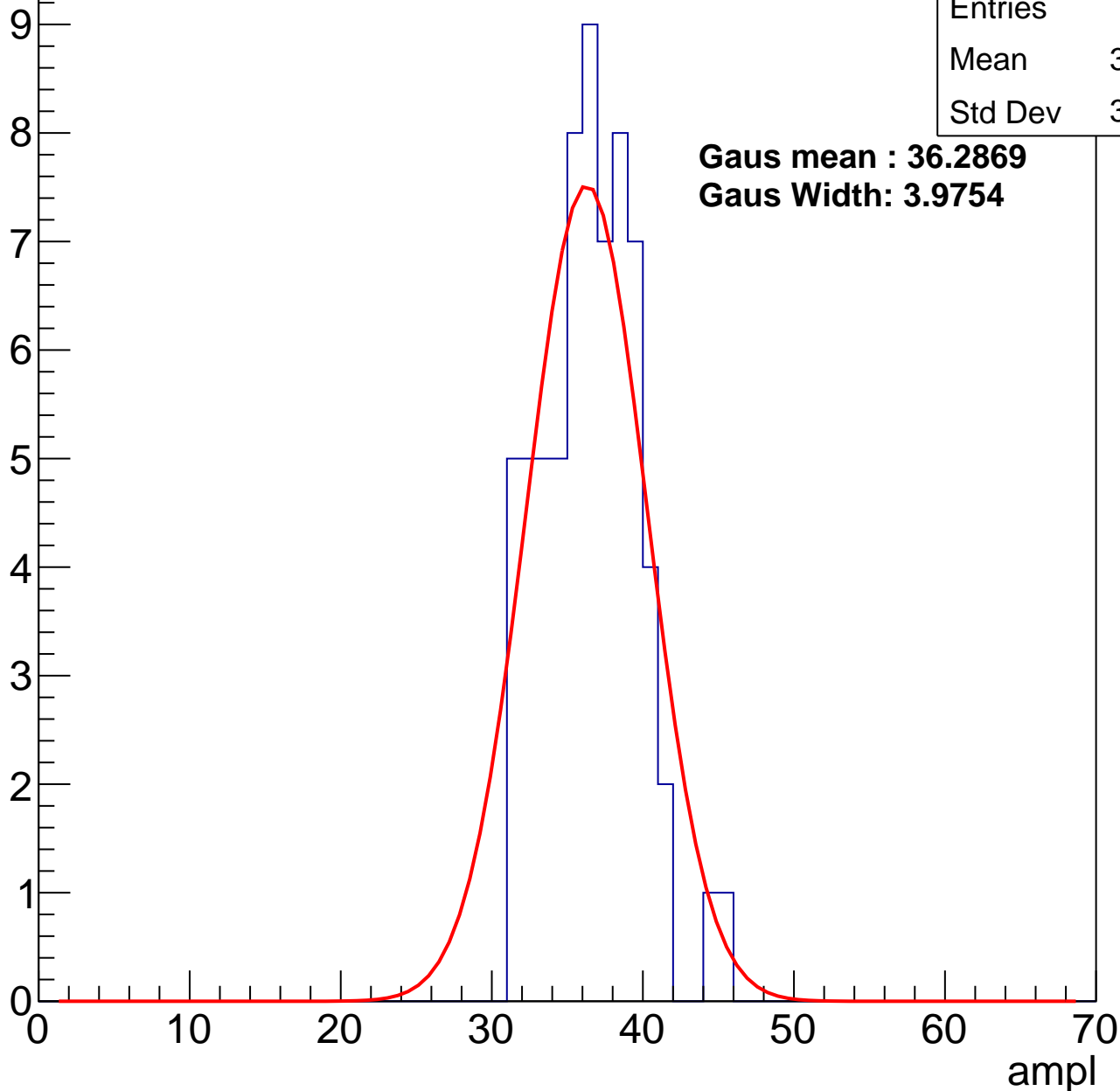
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	36.13
Std Dev	3.085

**Gaus mean : 36.2869**

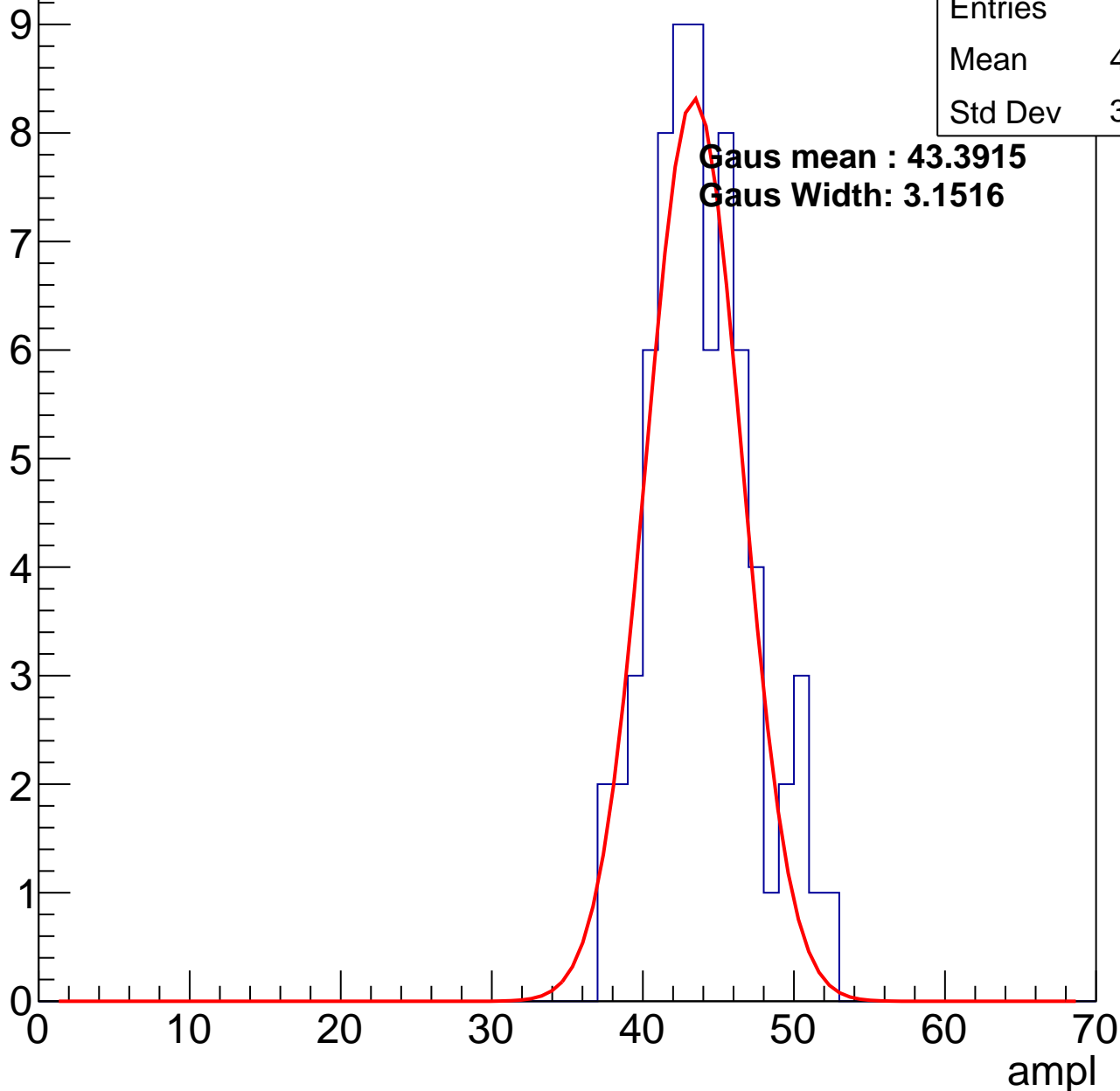
**Gaus Width: 3.9754**



# B0L102S, U6-ch61, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

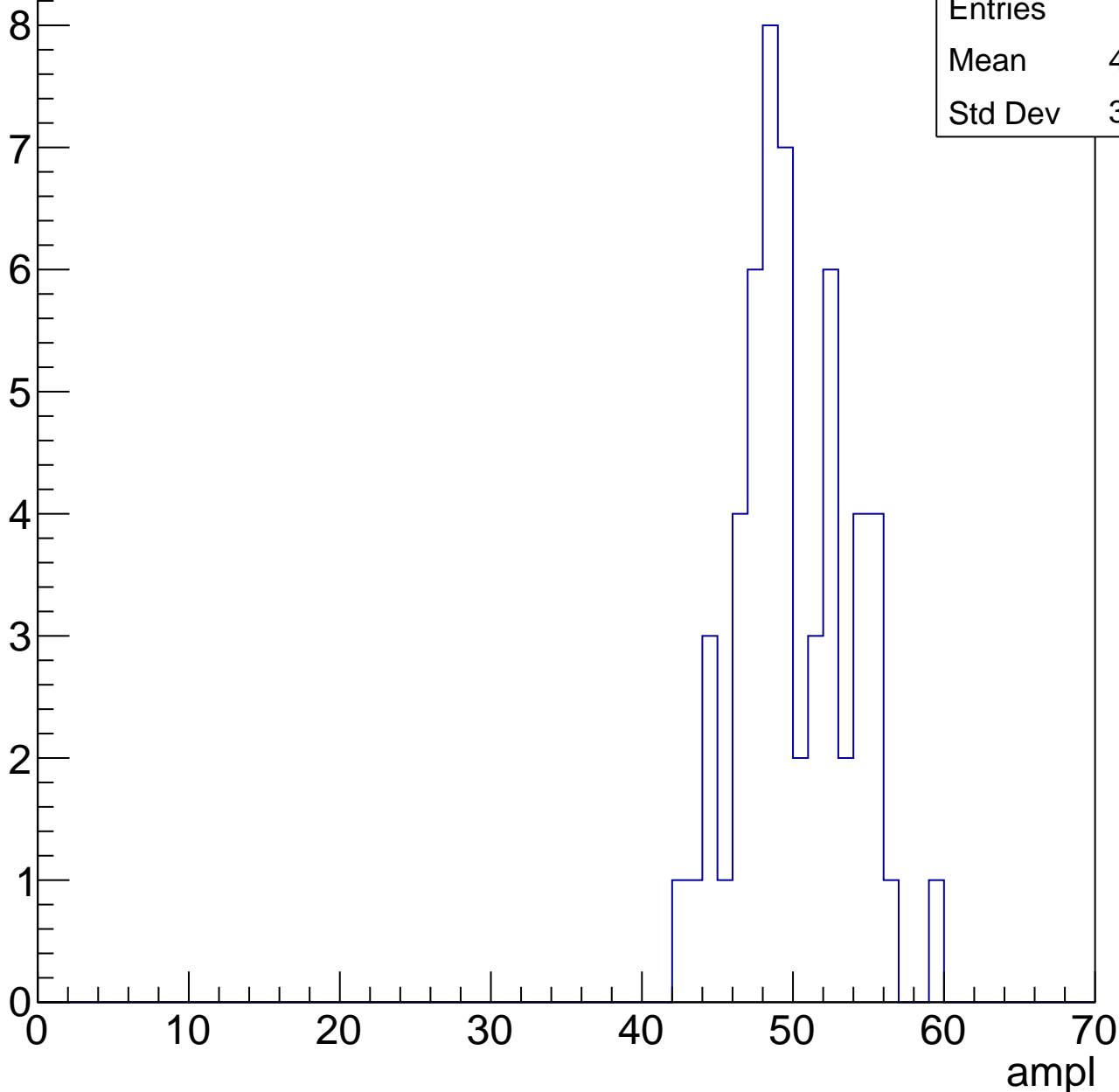


# B0L102S, U6-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

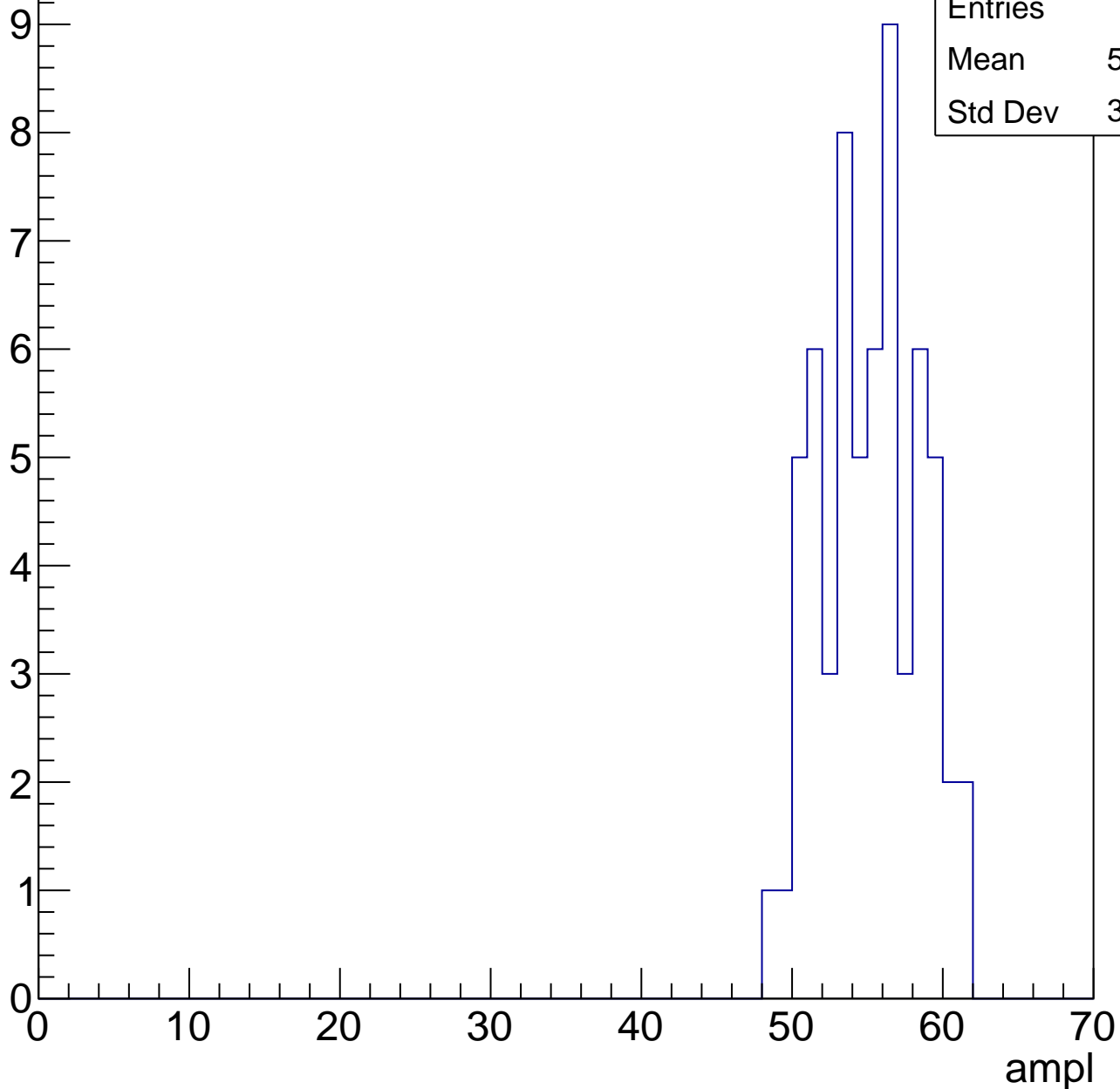
Entries	54
Mean	49.57
Std Dev	3.649



# B0L102S, U6-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	60.07
Std Dev	2.257

ampl

0

10

20

30

40

50

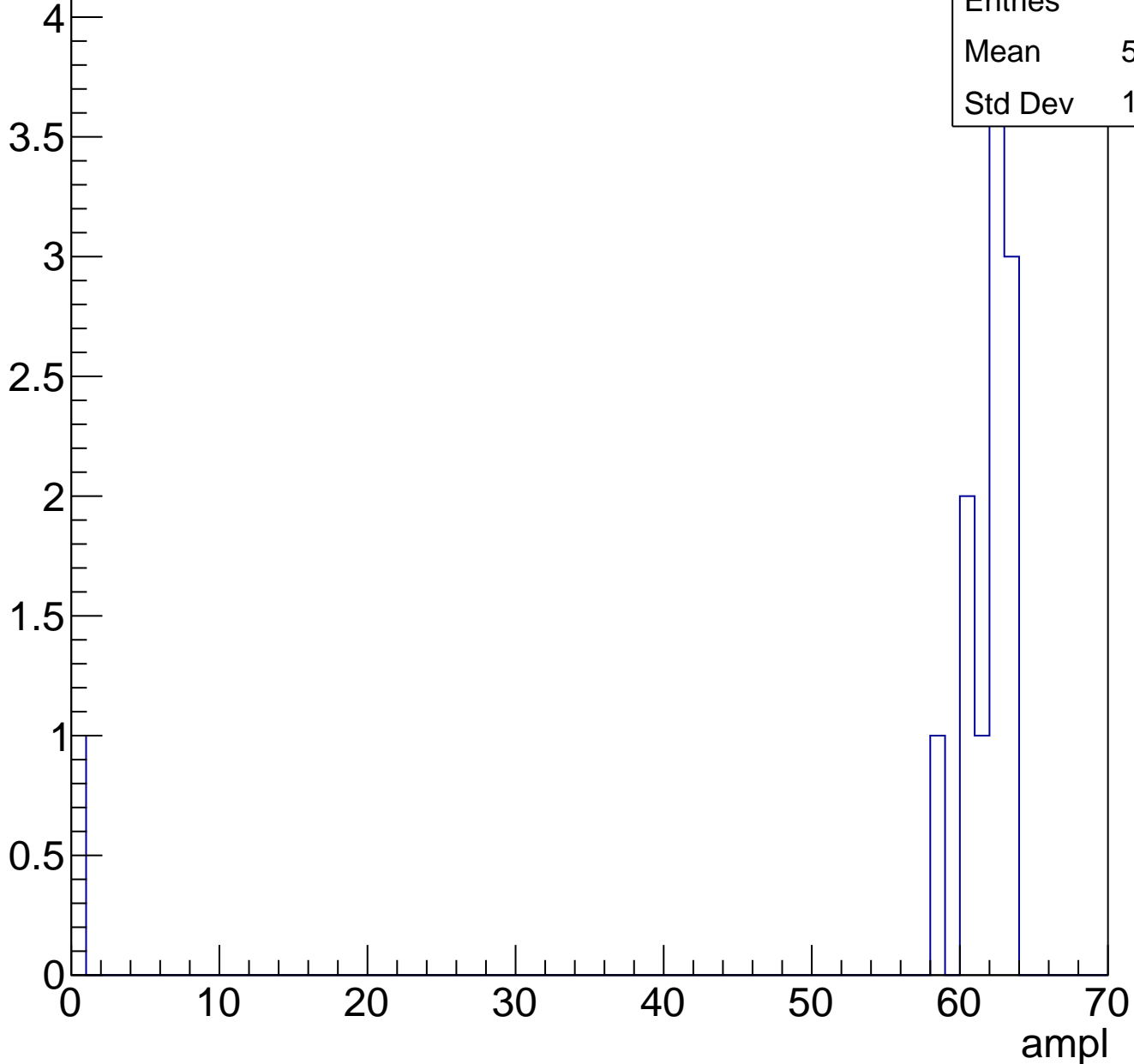
60

70

# B0L102S, U6-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch62, adc0

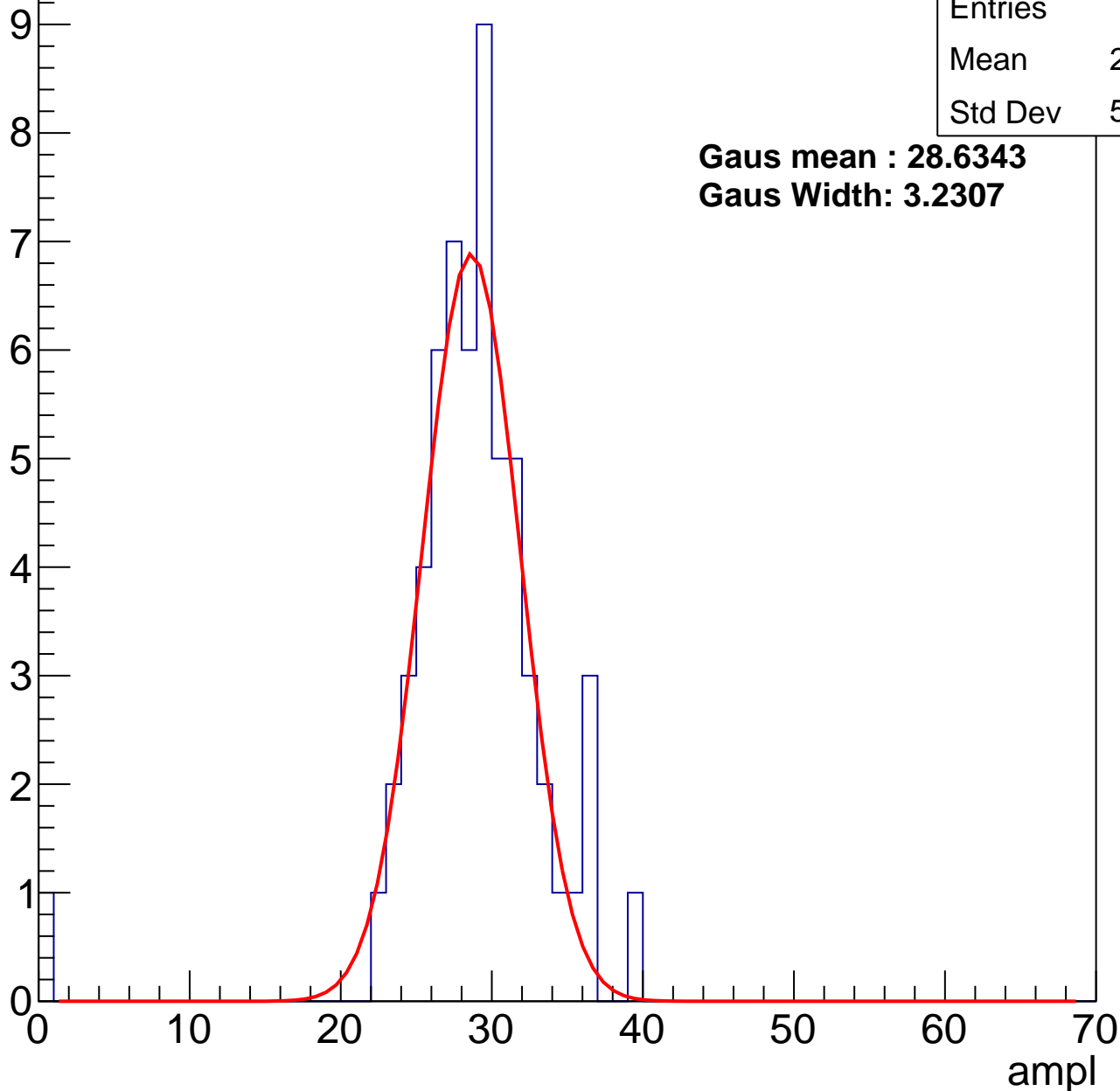
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	28.28
Std Dev	5.083

**Gaus mean : 28.6343**

**Gaus Width: 3.2307**



# B0L102S, U6-ch62, adc1

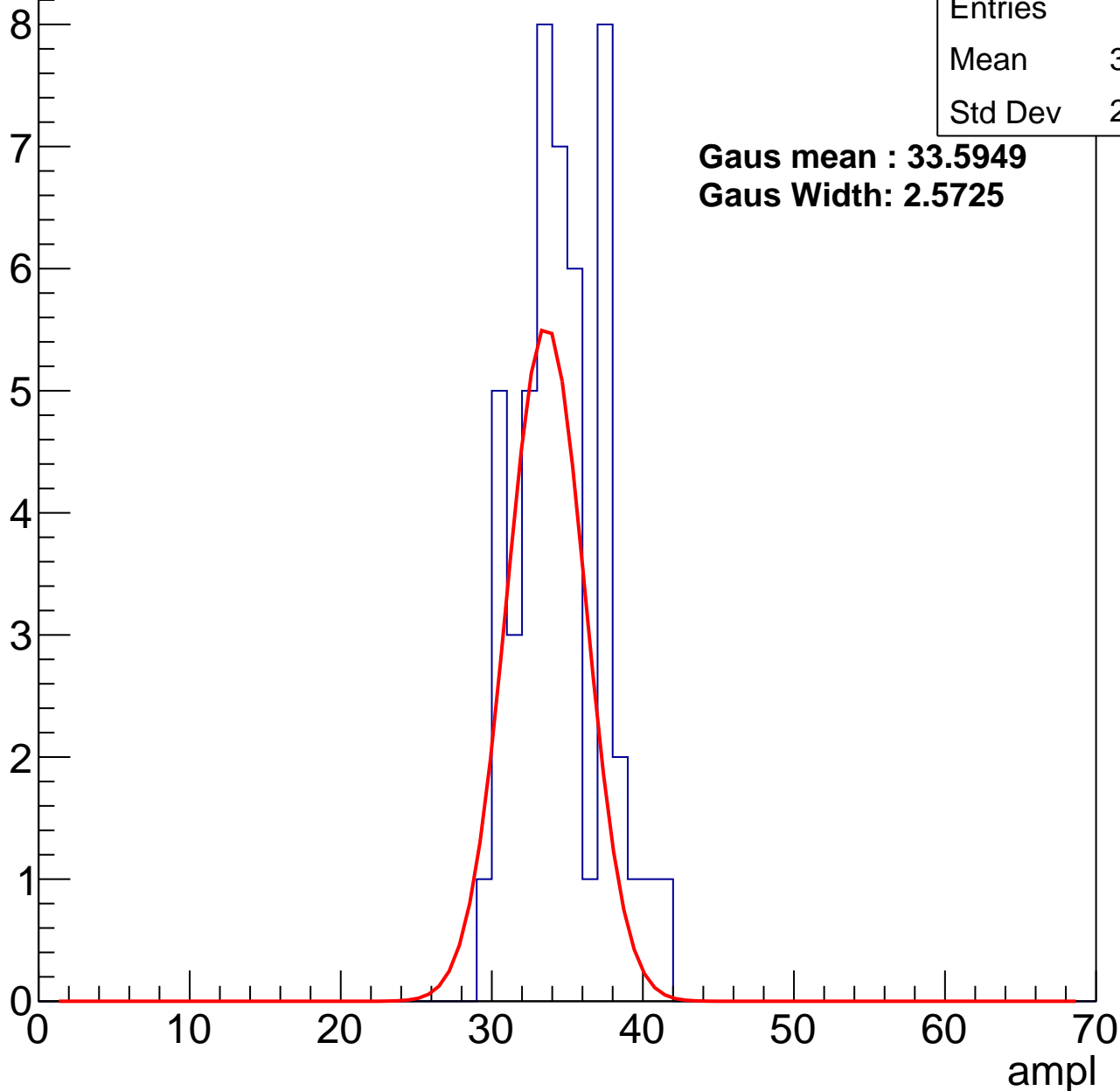
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	34.12
Std Dev	2.797

**Gaus mean : 33.5949**

**Gaus Width: 2.5725**



# B0L102S, U6-ch62, adc2

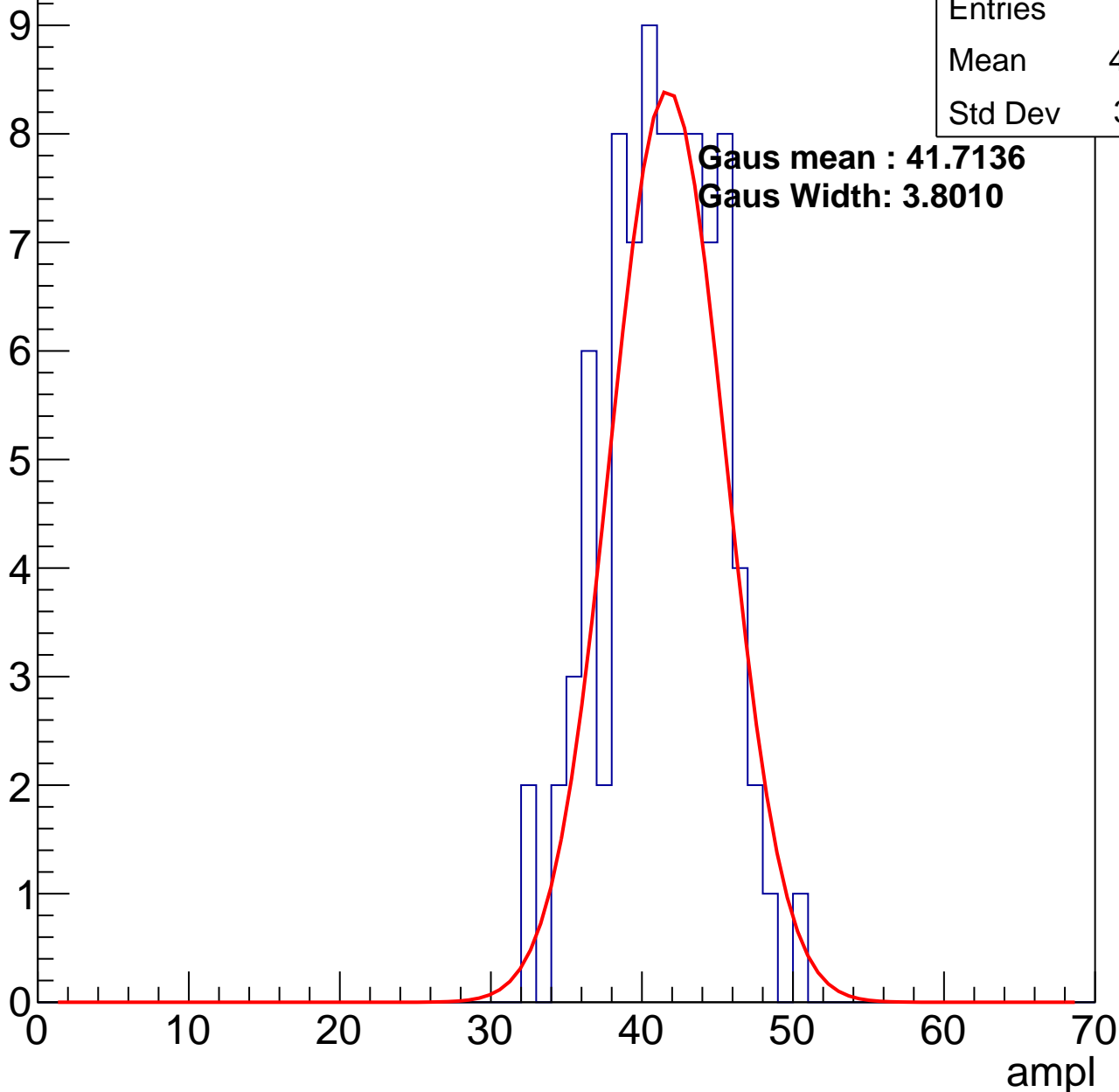
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	86
Mean	40.88
Std Dev	3.721

**Gaus mean : 41.7136**

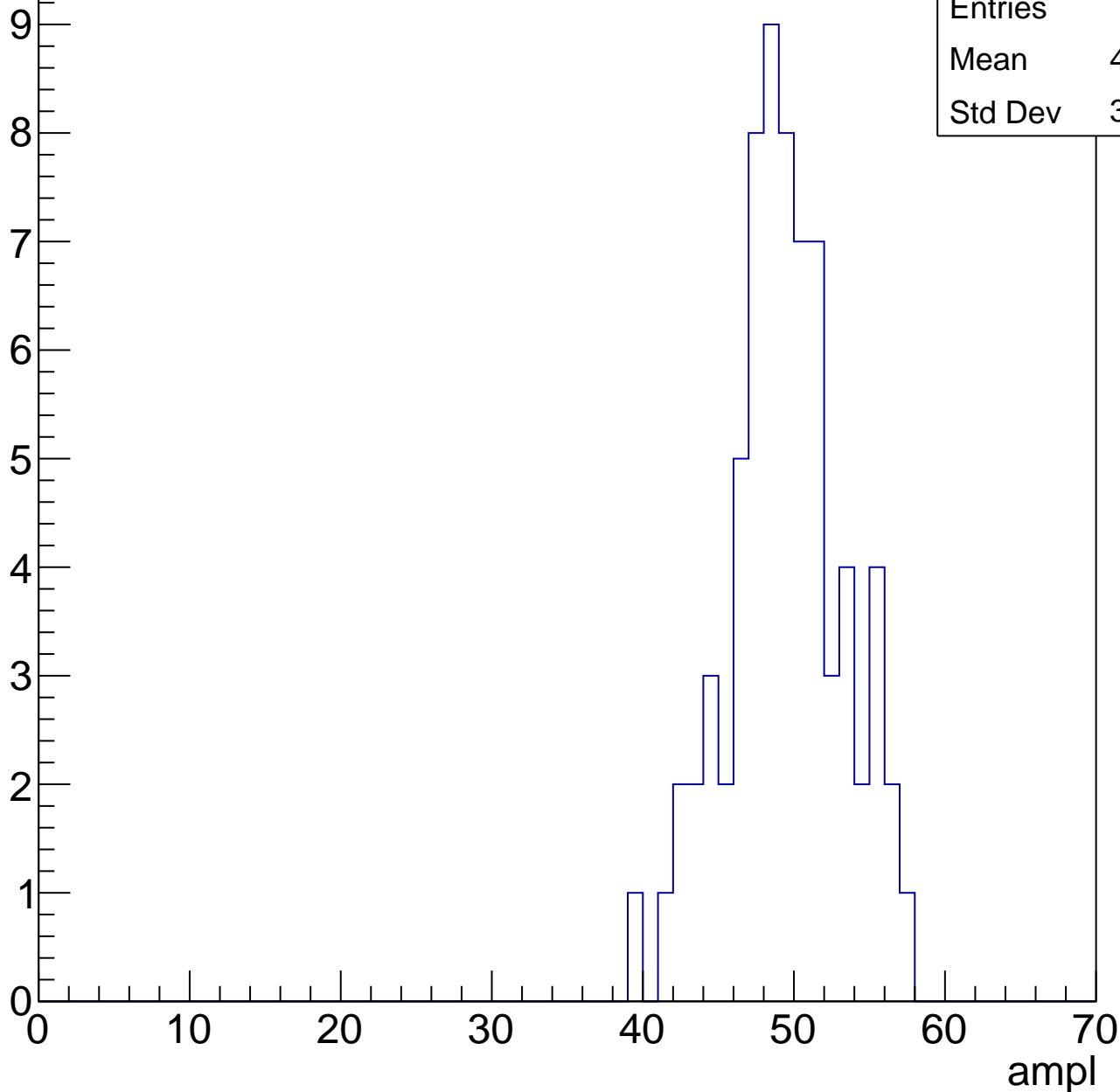
**Gaus Width: 3.8010**



# B0L102S, U6-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



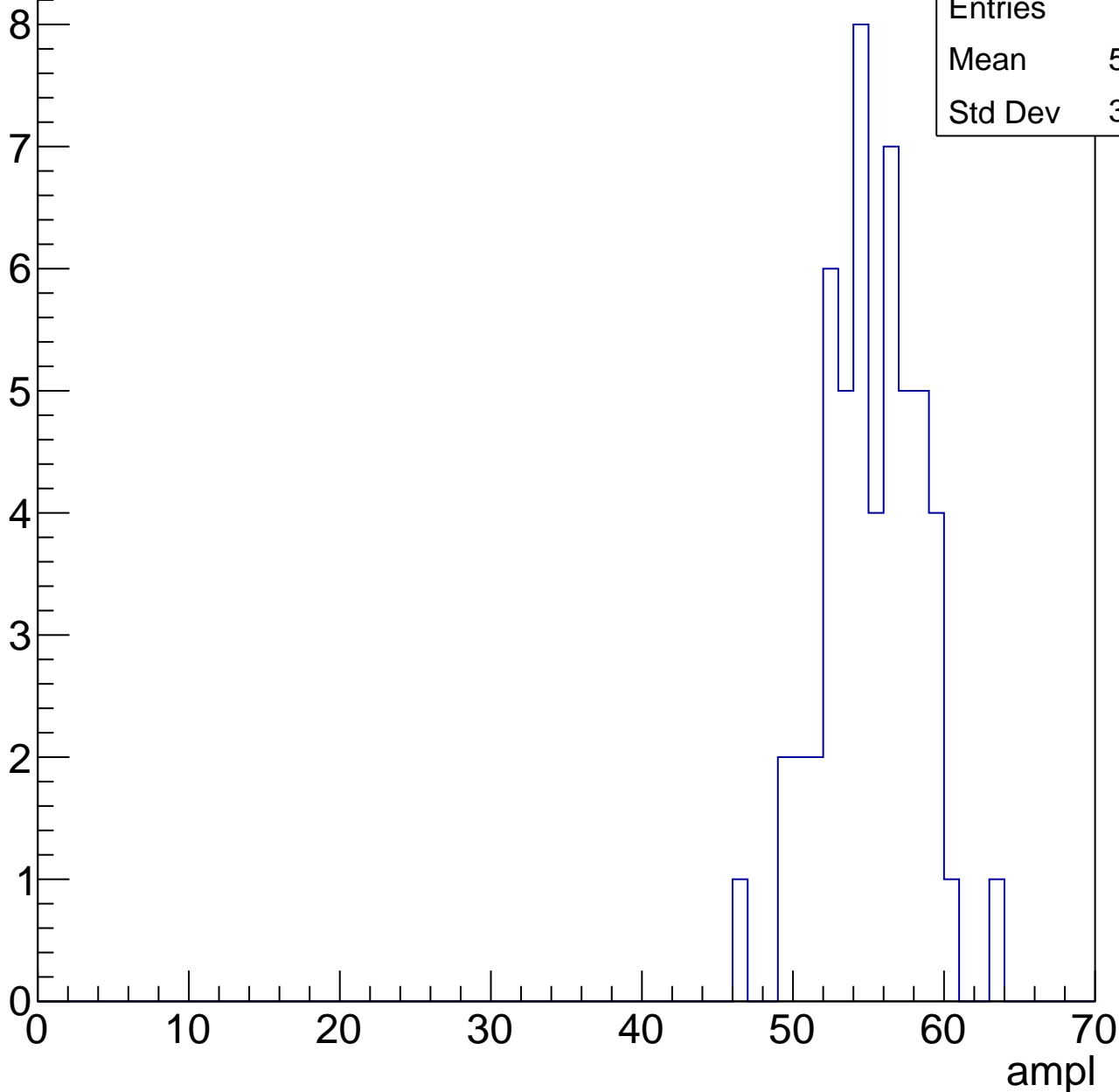
Entries	71
Mean	48.93
Std Dev	3.769

# B0L102S, U6-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	54.74
Std Dev	3.187



# B0L102S, U6-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	40
Mean	58.85
Std Dev	2.78

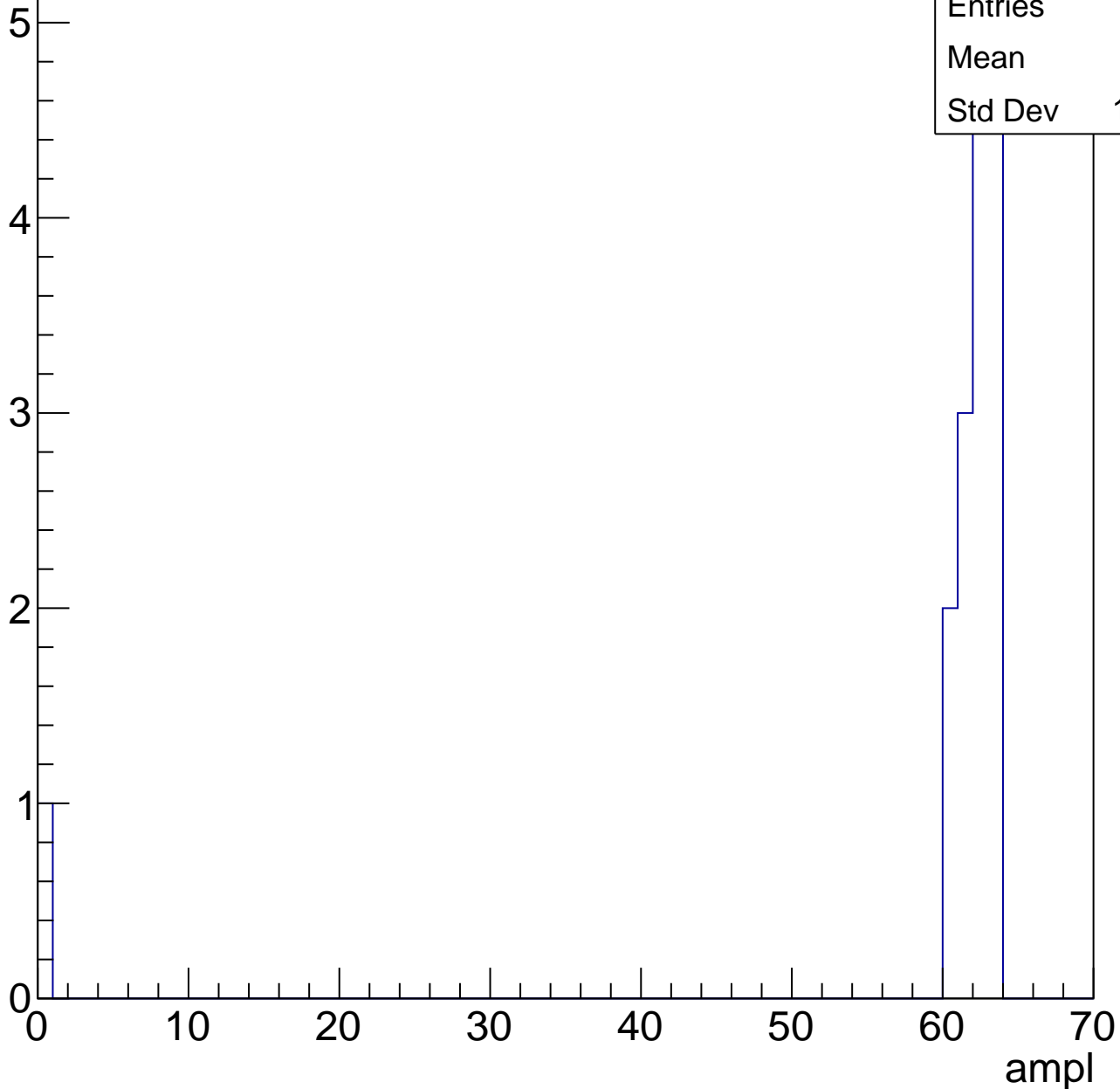
ampl

# B0L102S, U6-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	16
Mean	58
Std Dev	15.01

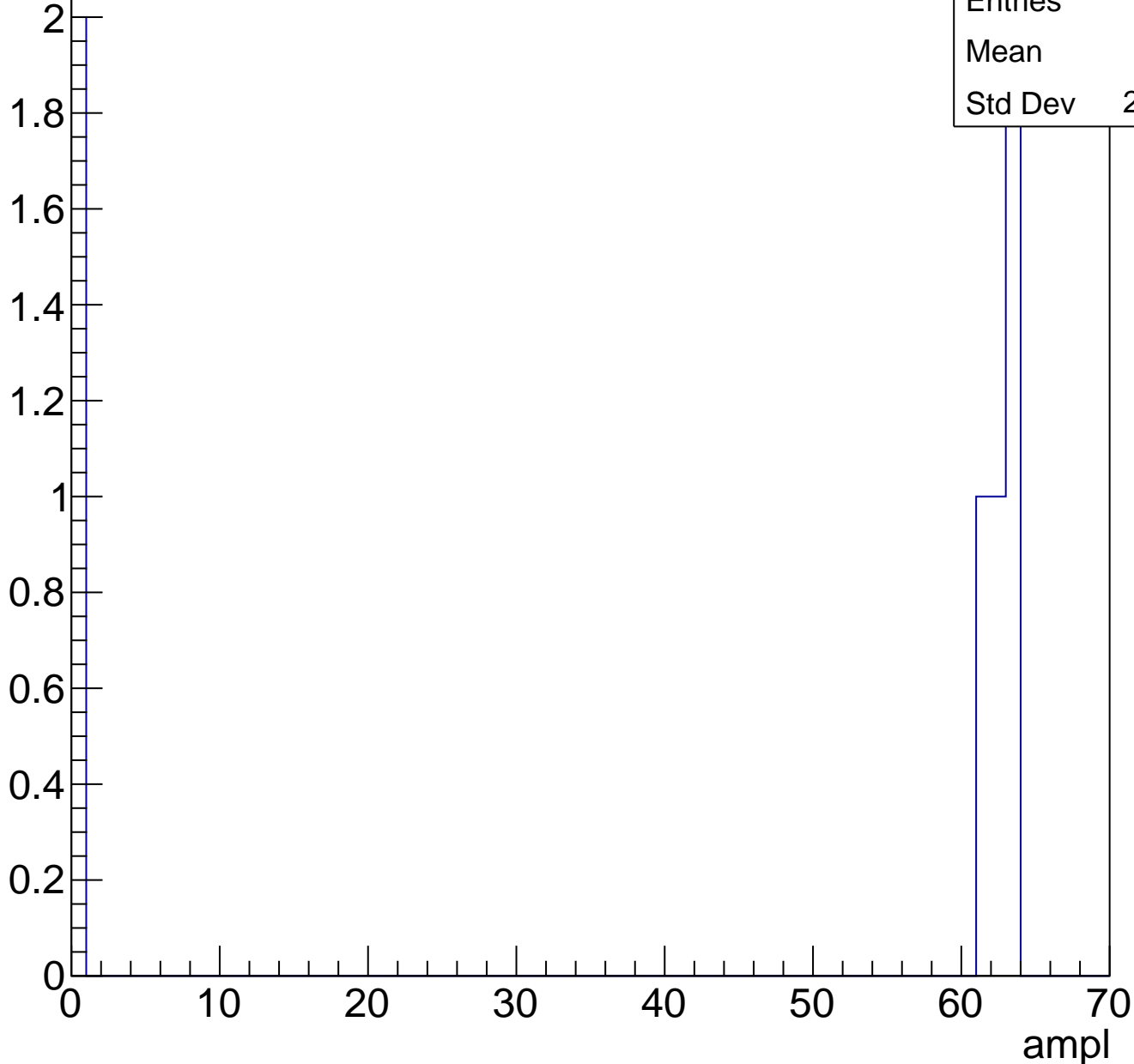




# B0L102S, U6-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch63, adc0

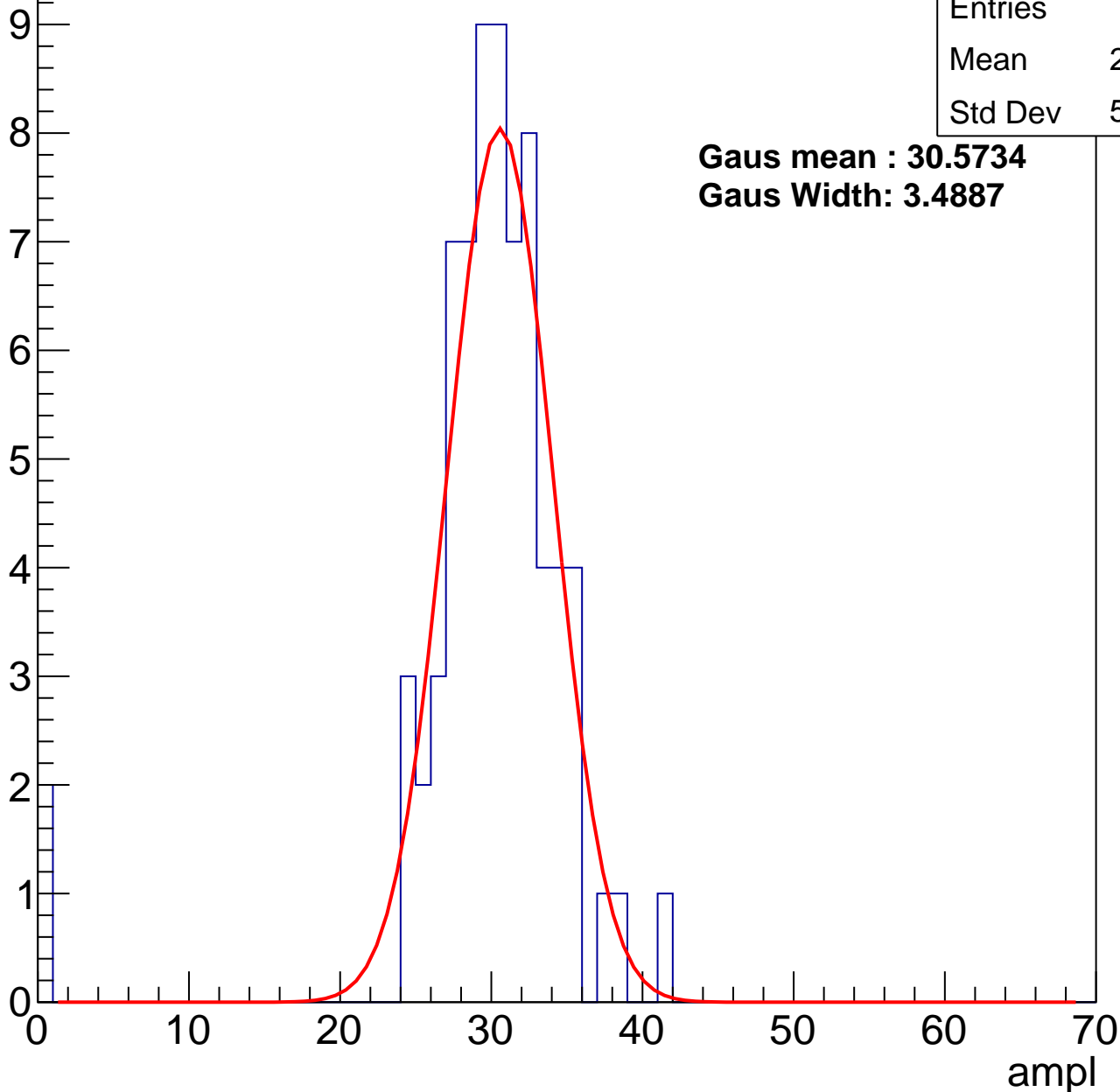
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	29.35
Std Dev	5.949

**Gaus mean : 30.5734**

**Gaus Width: 3.4887**



# B0L102S, U6-ch63, adc1

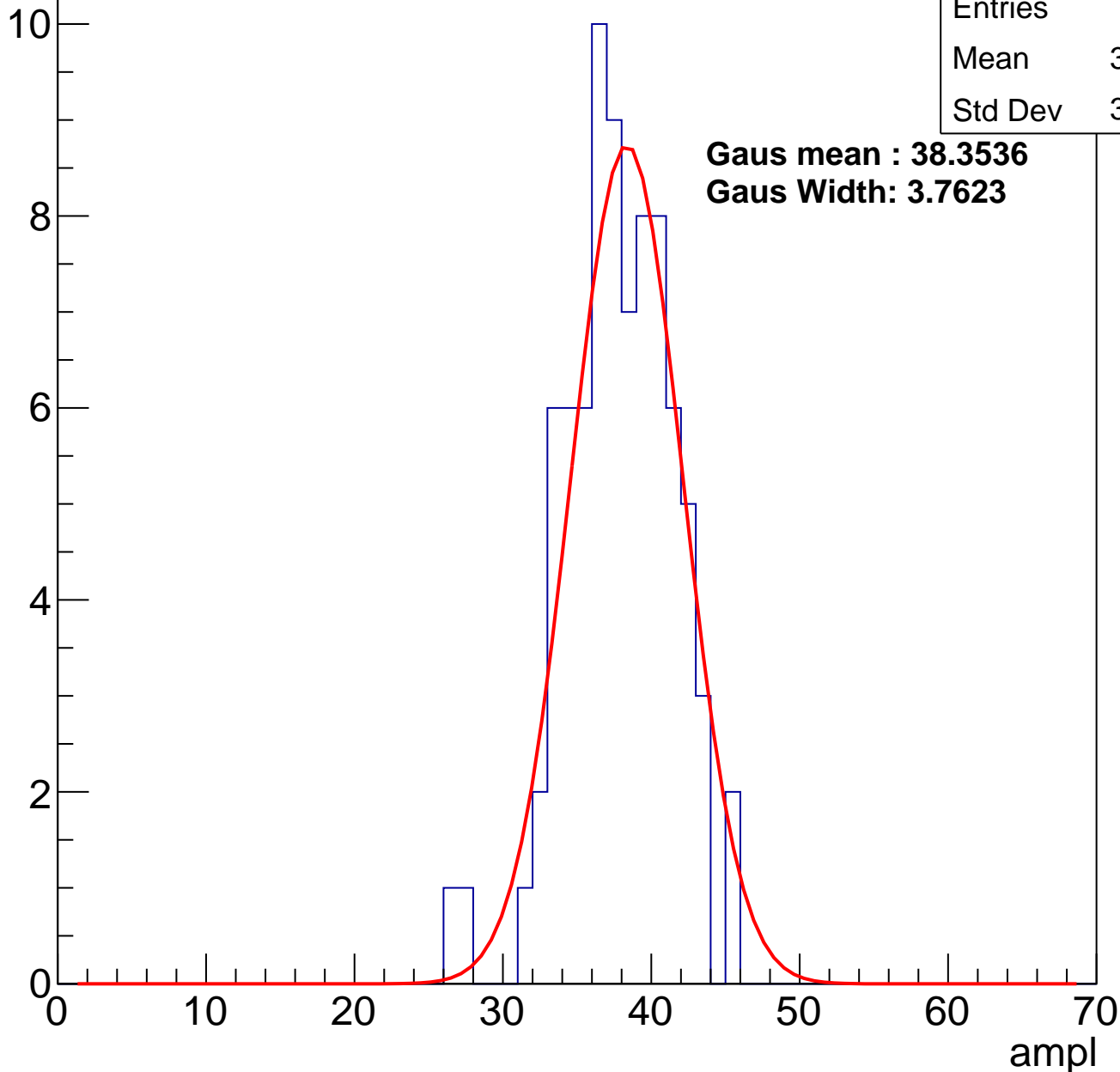
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	81
Mean	37.36
Std Dev	3.598

**Gaus mean : 38.3536**

**Gaus Width: 3.7623**

Entry



# B0L102S, U6-ch63, adc2

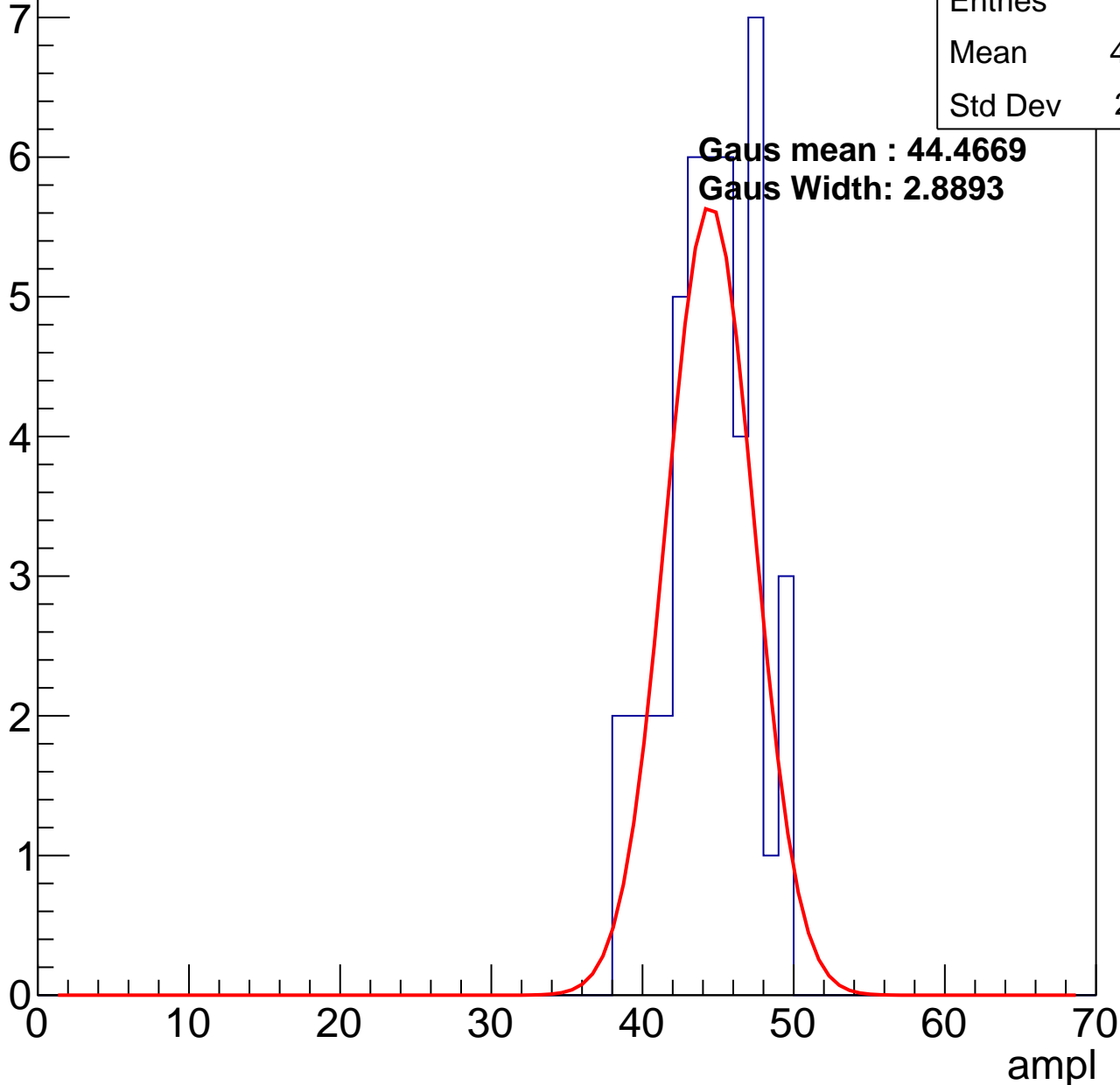
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	44.04
Std Dev	2.851

**Gaus mean : 44.4669**

**Gaus Width: 2.8893**

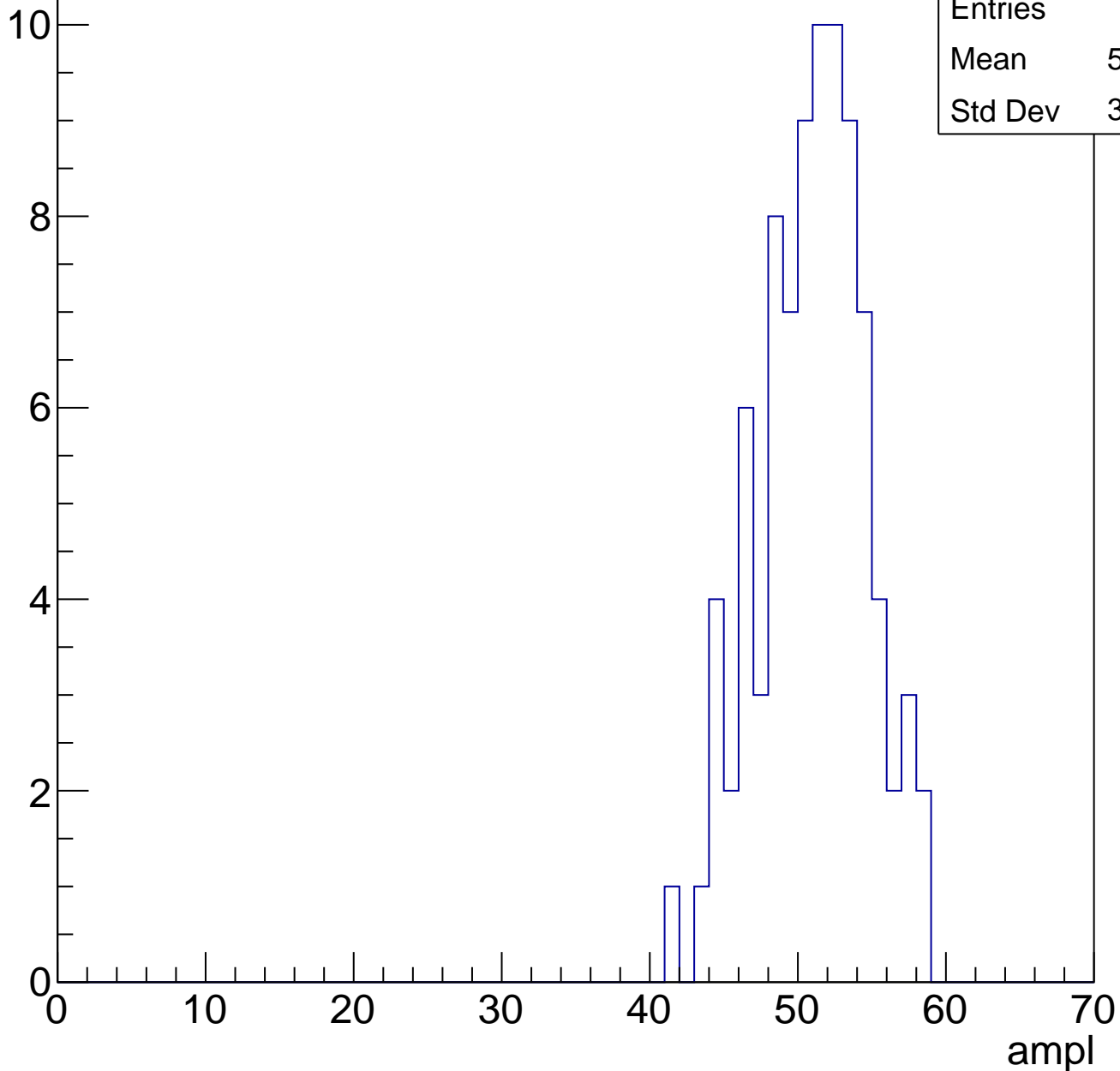


# B0L102S, U6-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	88
Mean	50.55
Std Dev	3.615

Entry

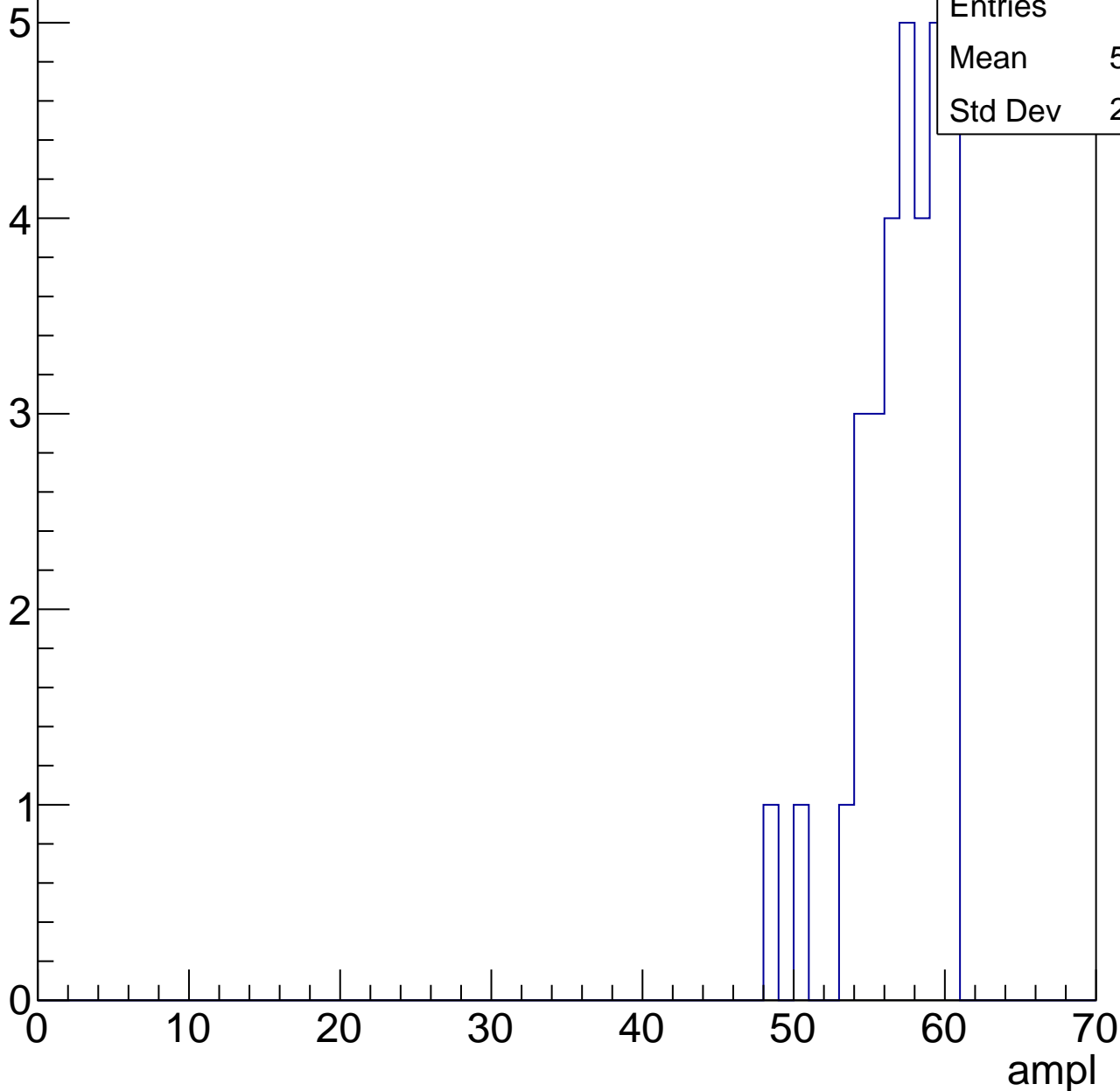


# B0L102S, U6-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	32
Mean	56.69
Std Dev	2.822

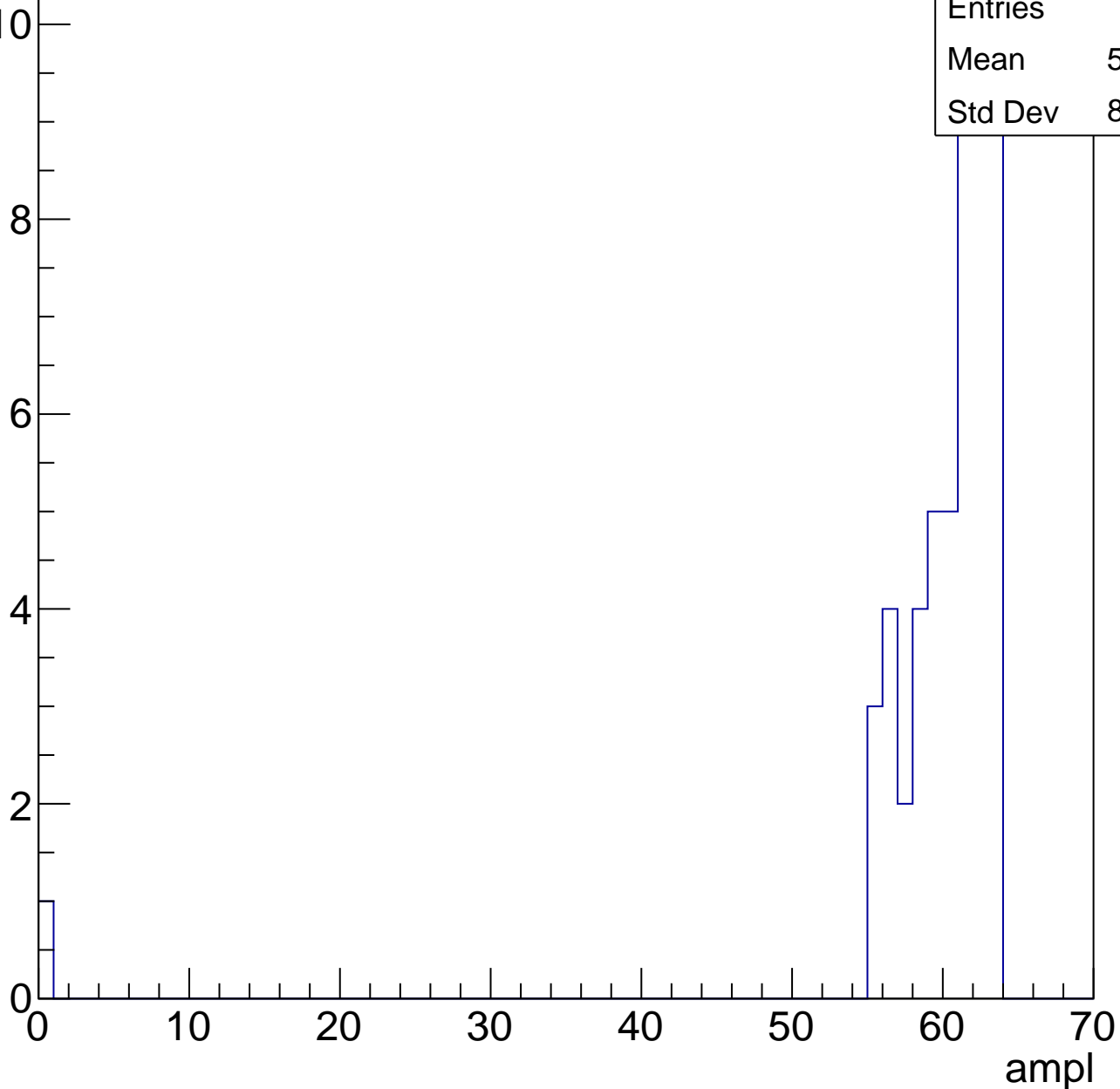


# B0L102S, U6-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

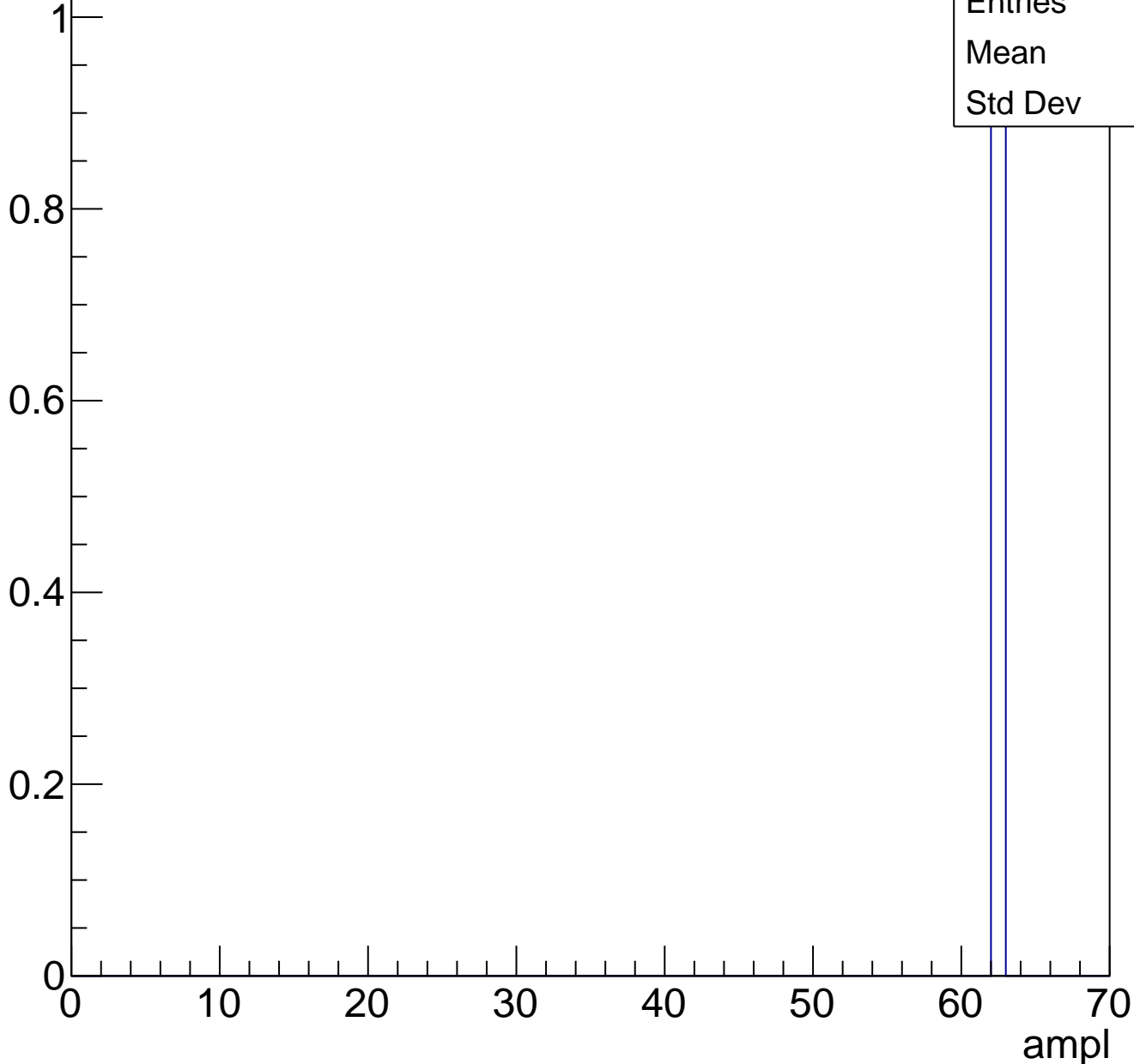
Entries	53
Mean	59.02
Std Dev	8.537



# B0L102S, U6-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch64, adc0

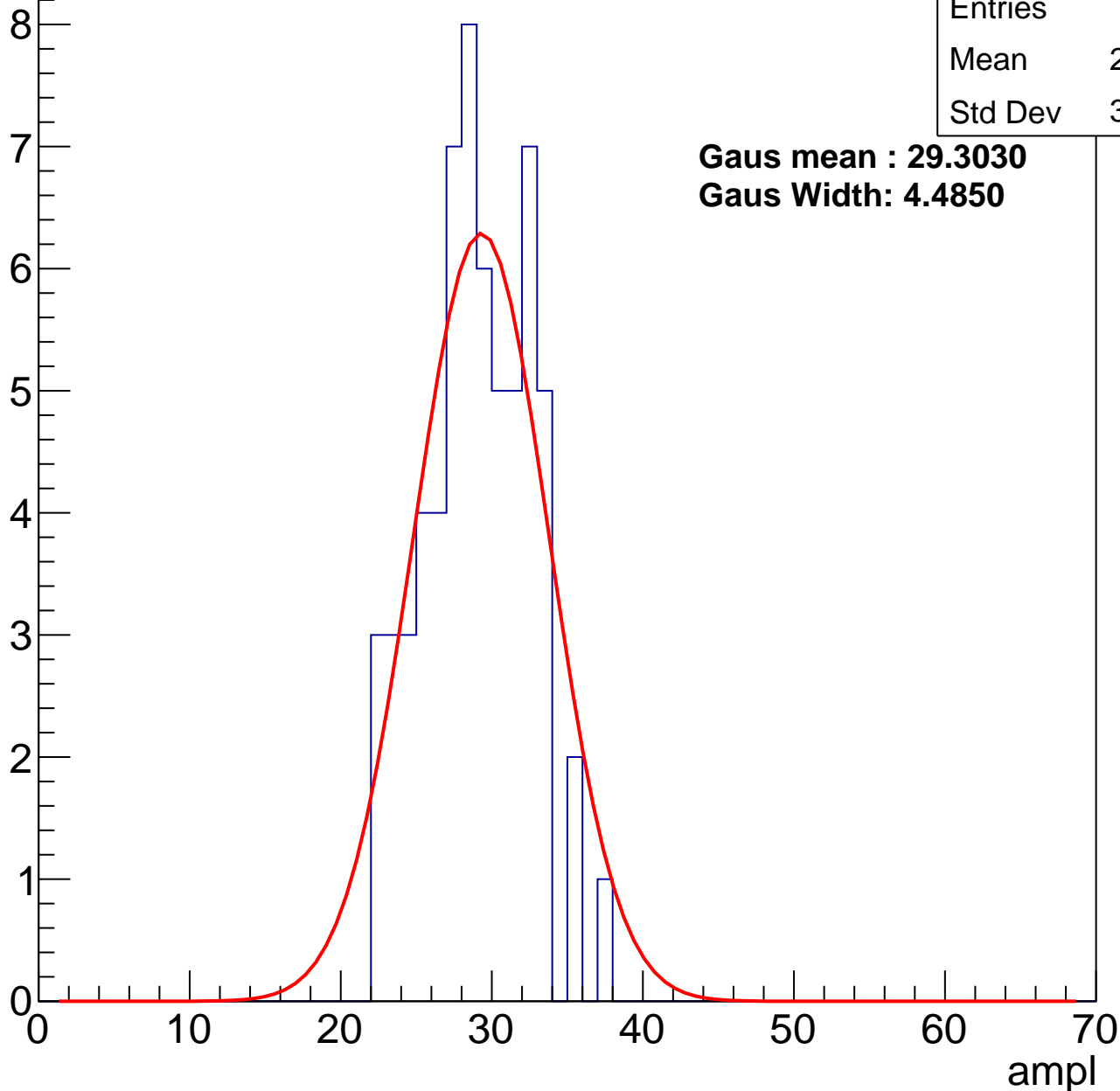
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	28.56
Std Dev	3.468

**Gaus mean : 29.3030**

**Gaus Width: 4.4850**



# B0L102S, U6-ch64, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	83
Mean	36.11
Std Dev	3.871

**Gaus mean : 36.9590**

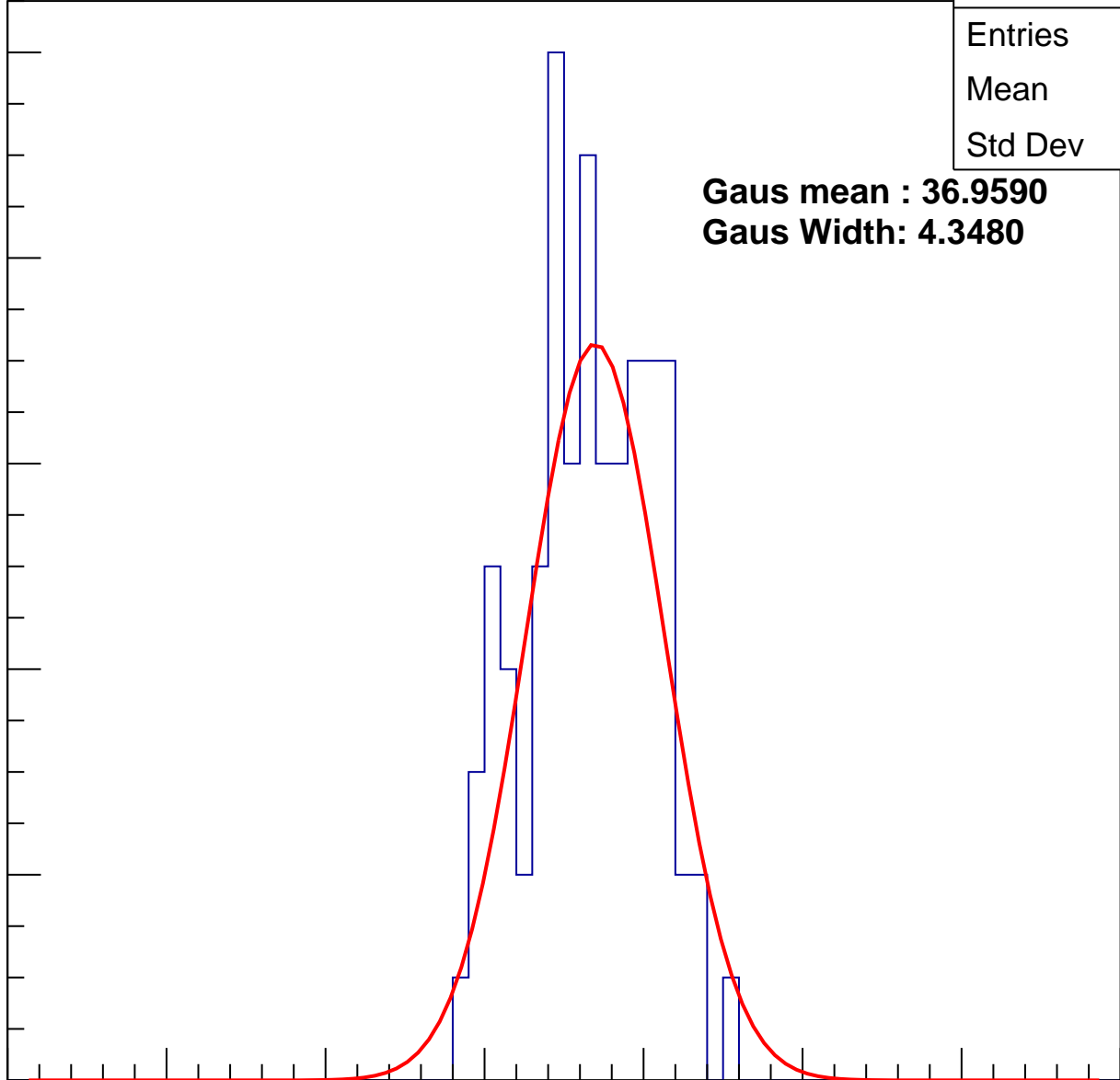
**Gaus Width: 4.3480**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch64, adc2

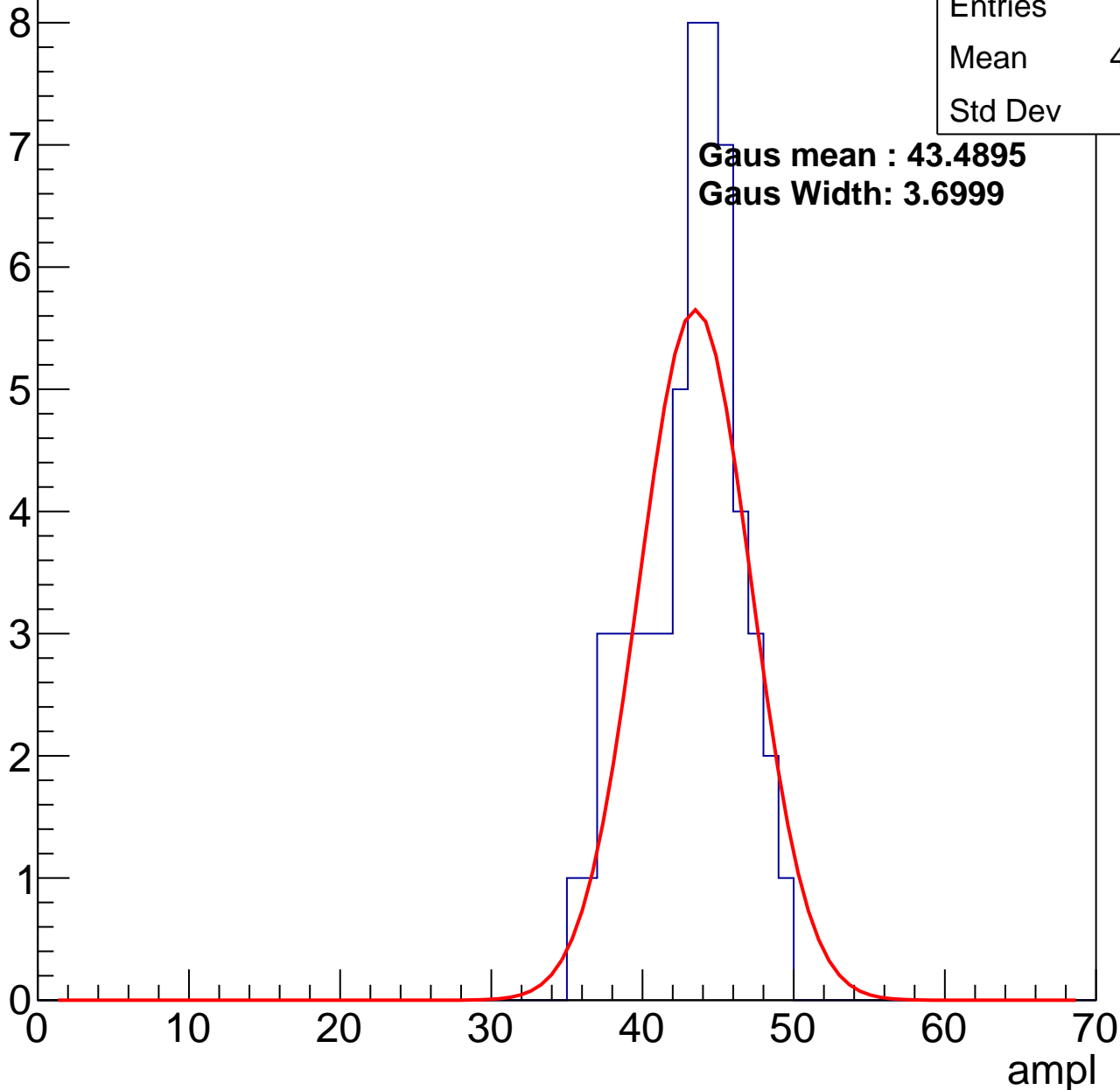
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	42.67
Std Dev	3.27

**Gaus mean : 43.4895**

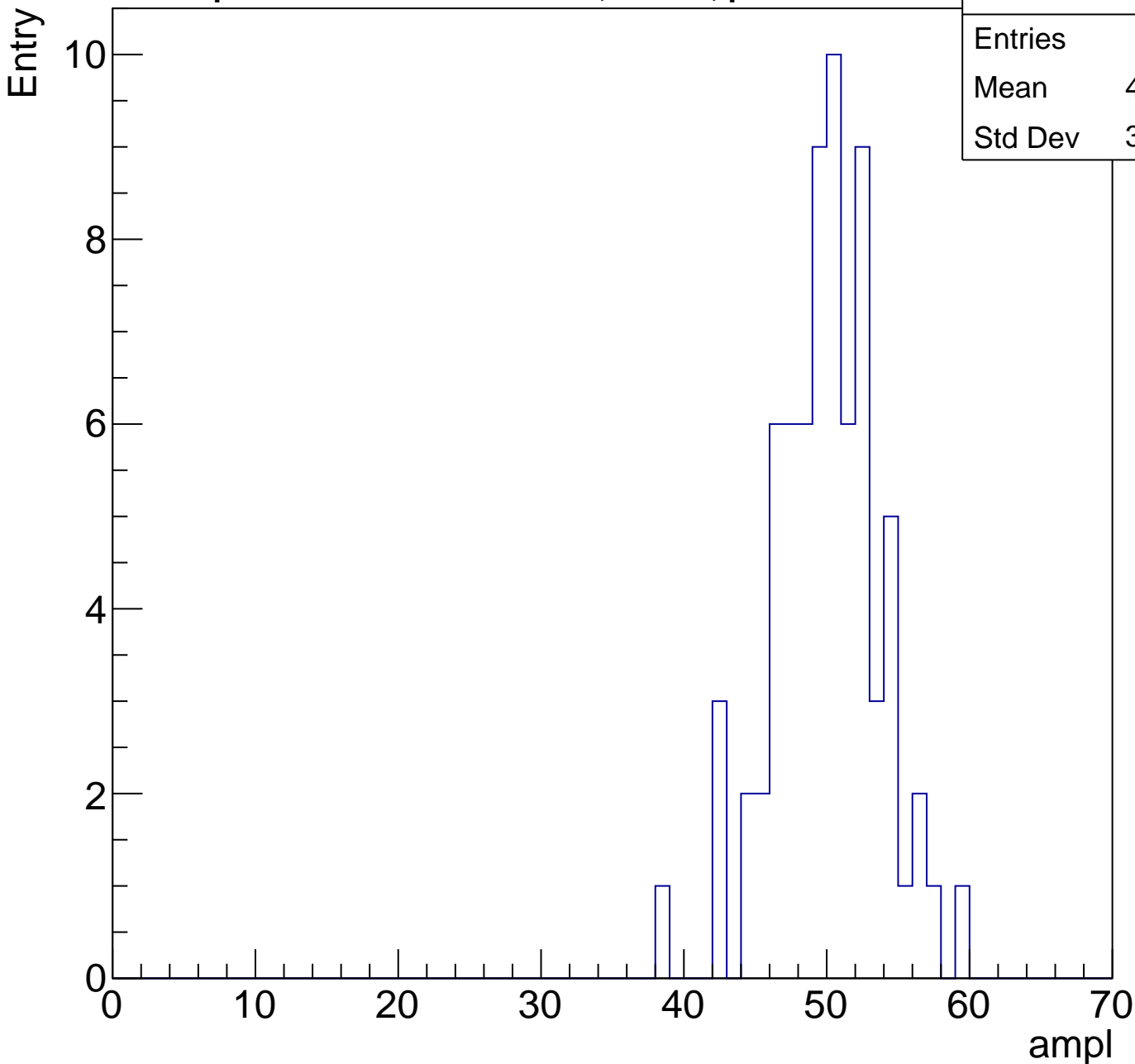
**Gaus Width: 3.6999**



# B0L102S, U6-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	49.52
Std Dev	3.702

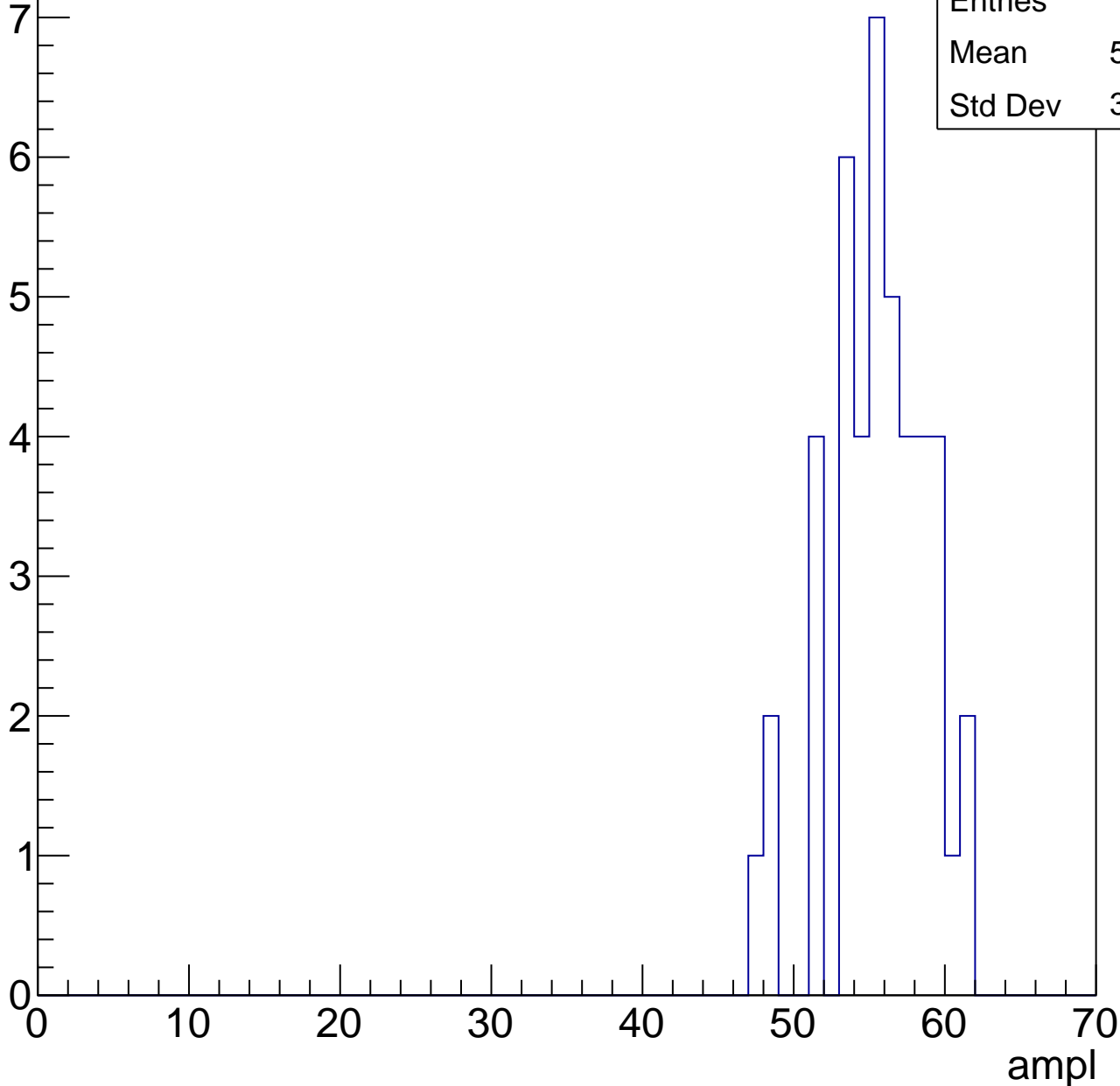


# B0L102S, U6-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	55.09
Std Dev	3.274

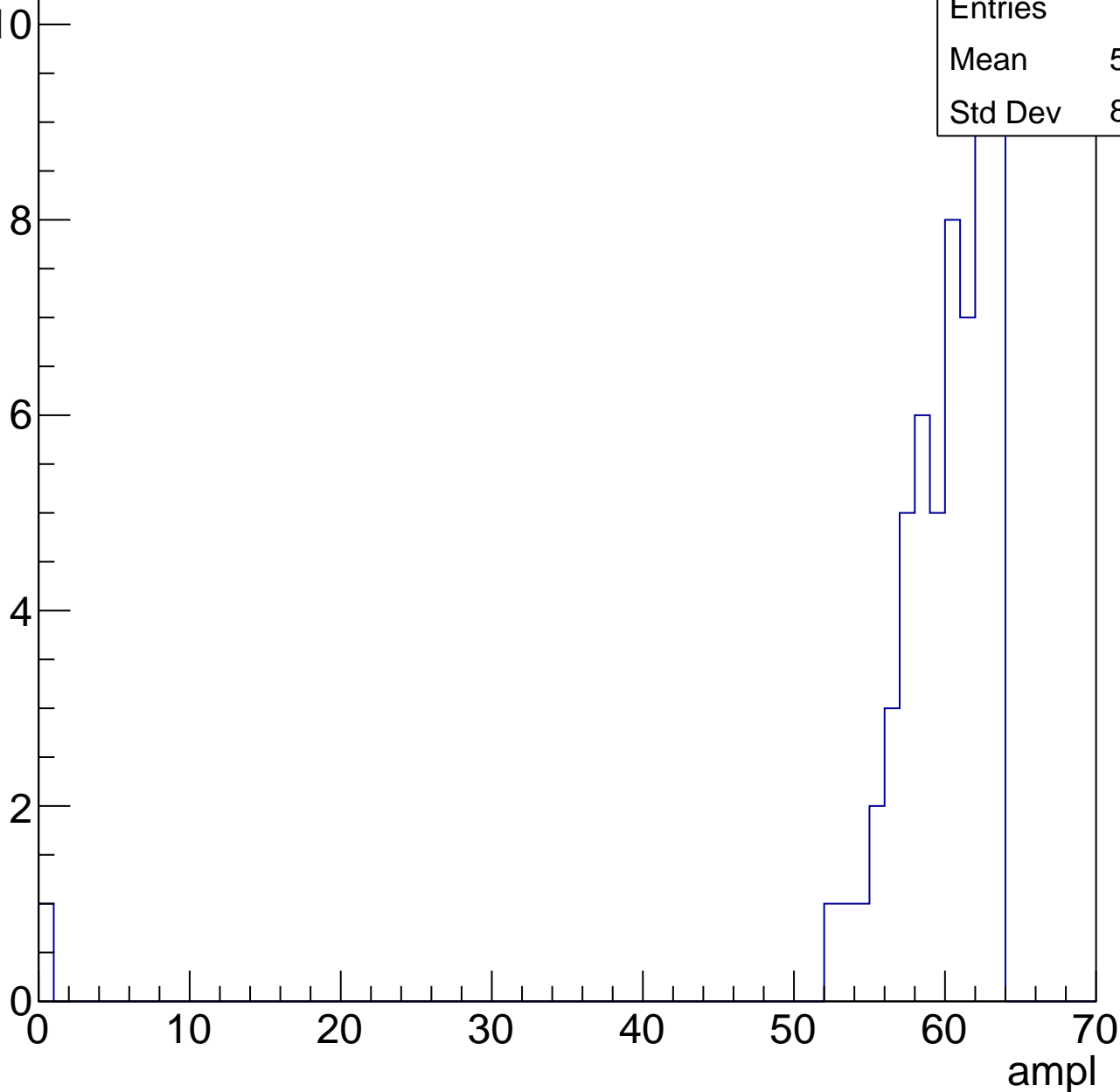


# B0L102S, U6-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	58.64
Std Dev	8.178



# B0L102S, U6-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

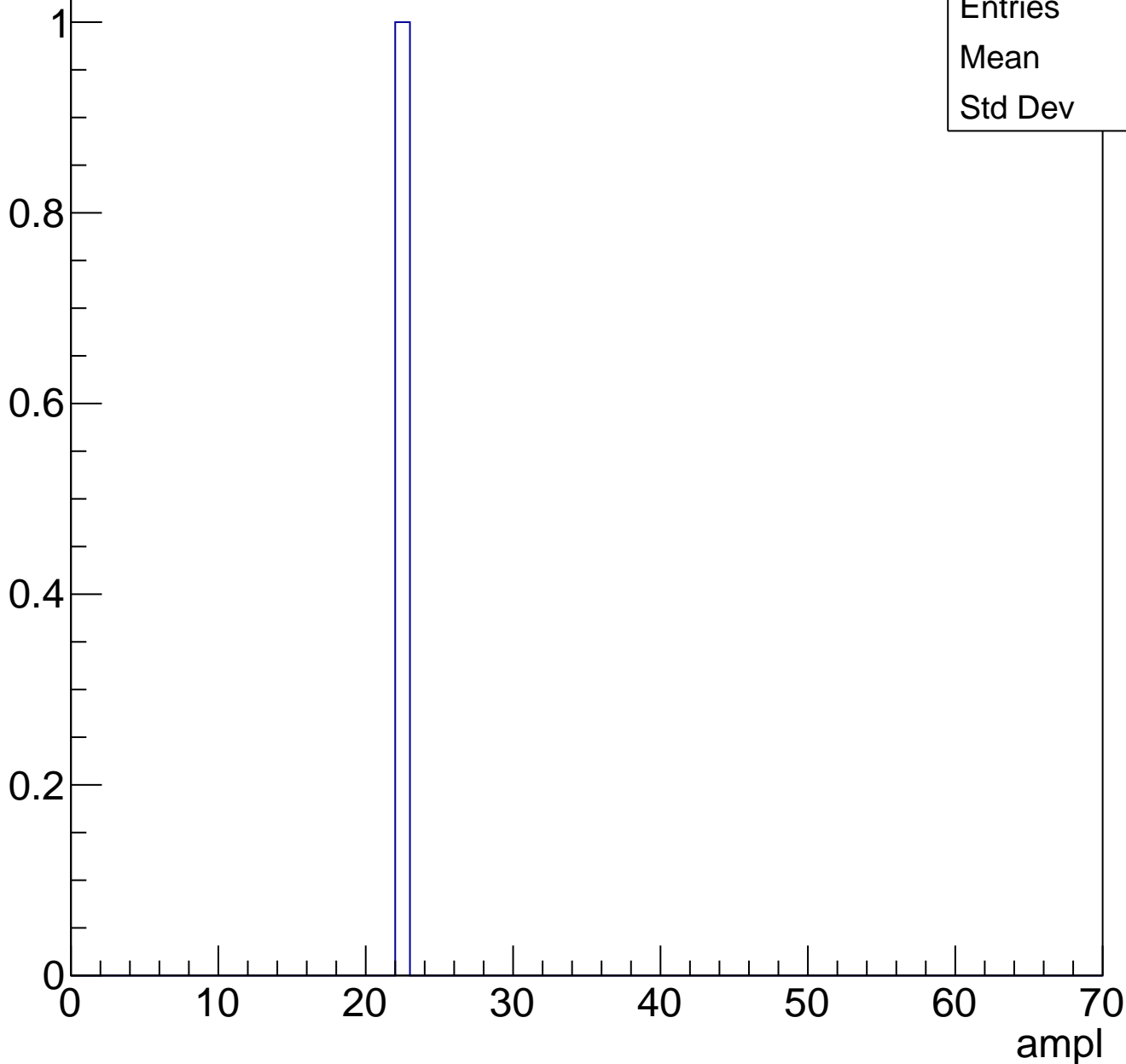




# B0L102S, U6-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch65, adc0

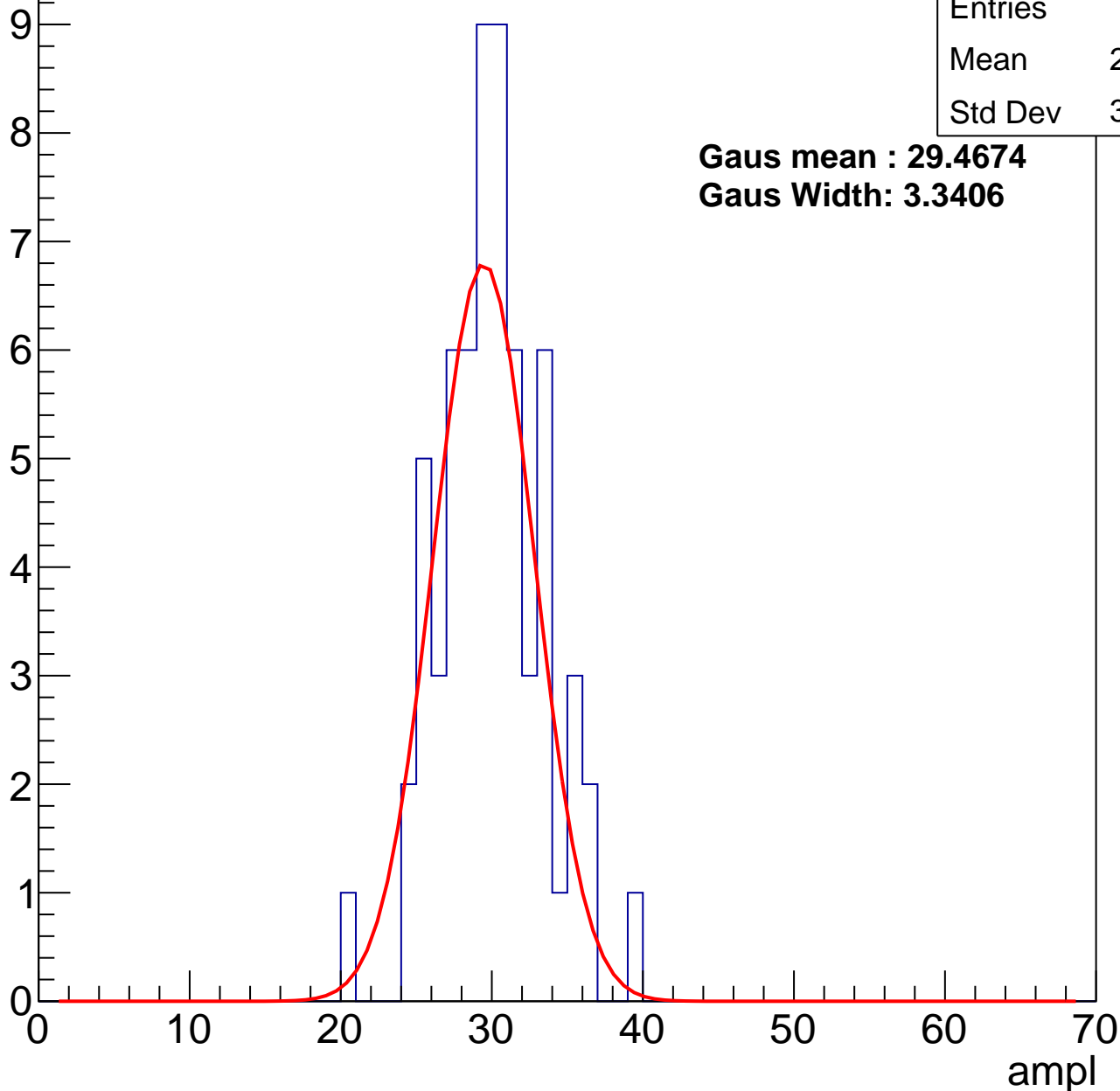
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	29.56
Std Dev	3.417

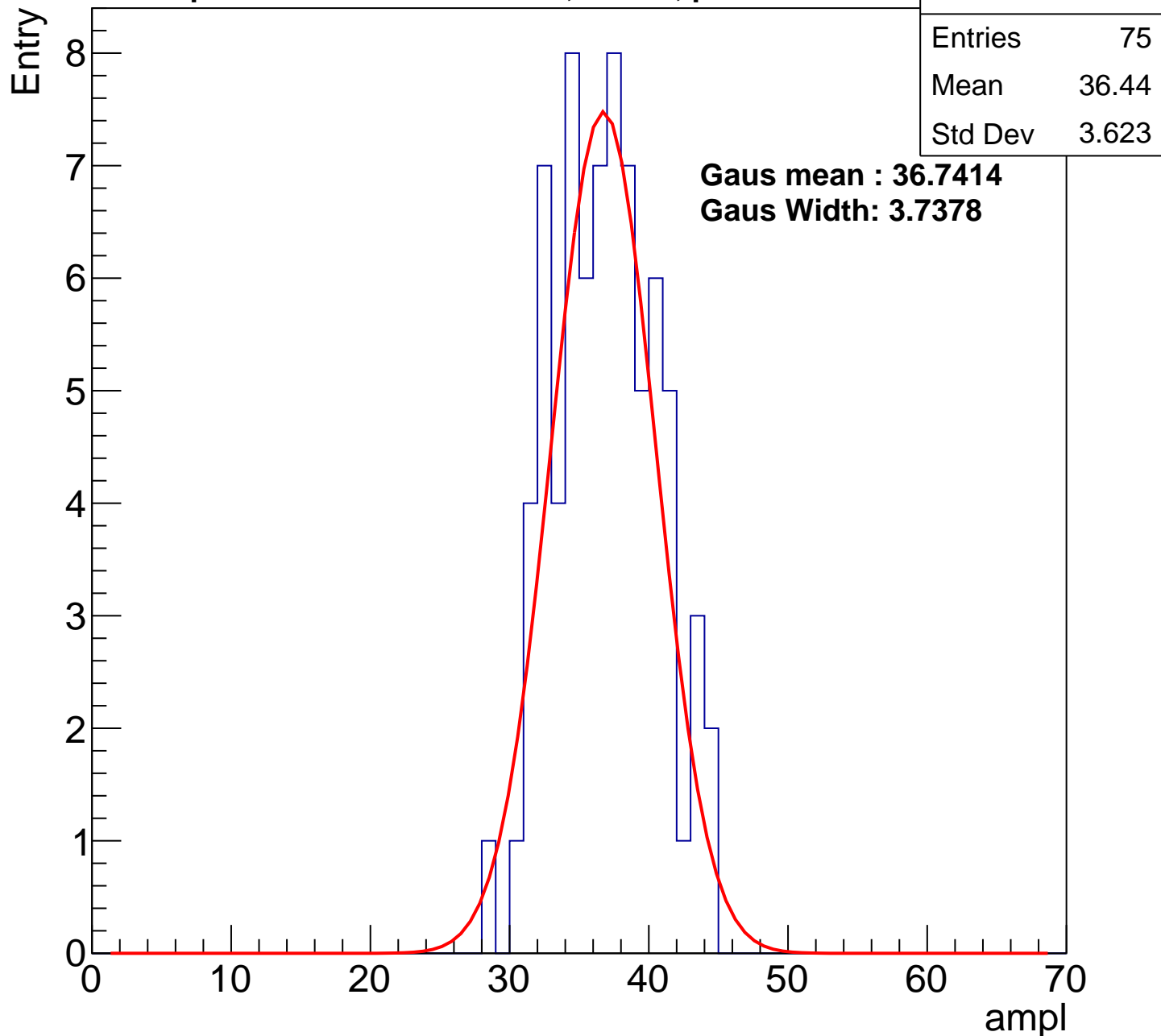
**Gaus mean : 29.4674**

**Gaus Width: 3.3406**



# B0L102S, U6-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch65, adc2

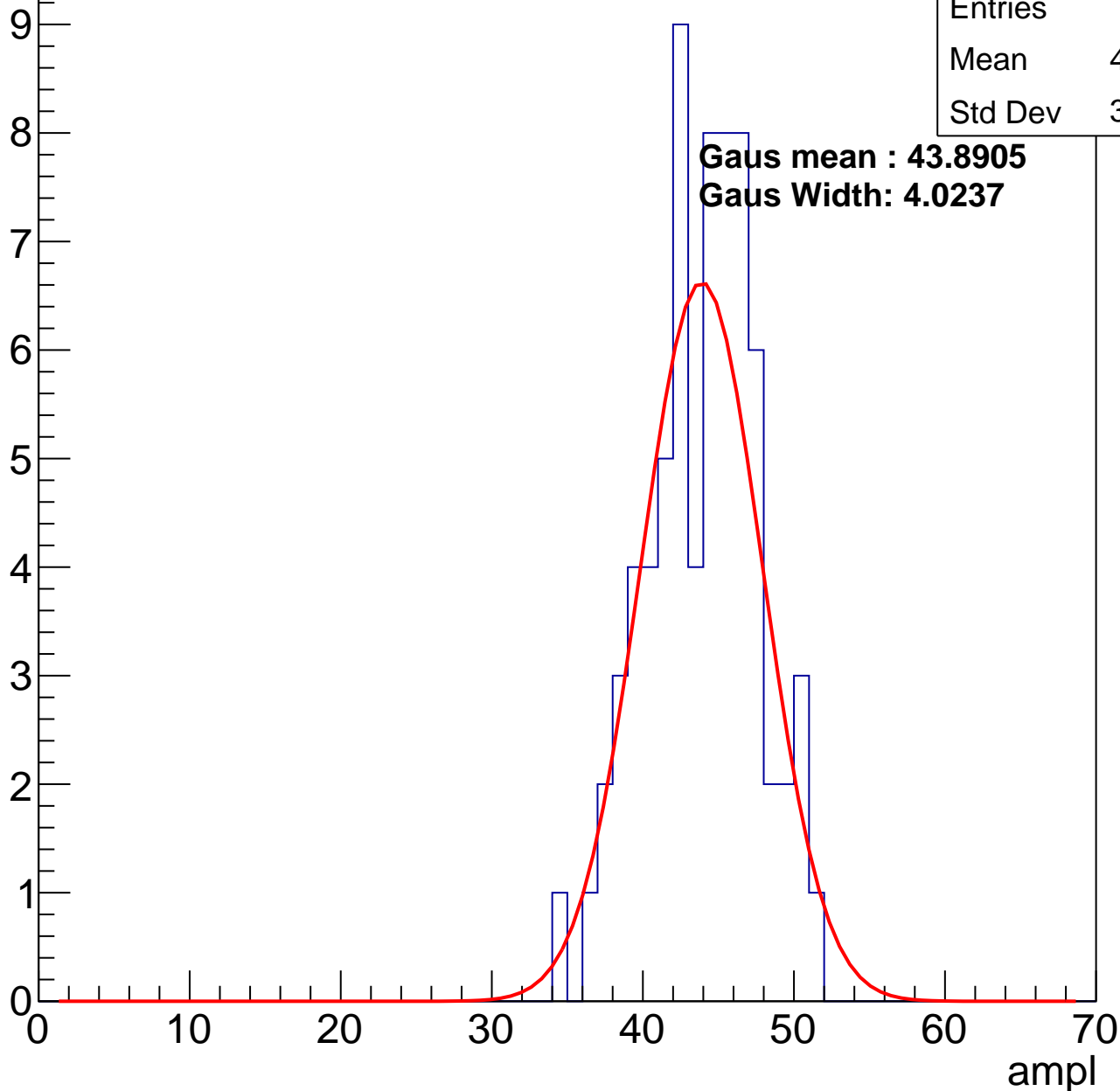
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	43.46
Std Dev	3.622

**Gaus mean : 43.8905**

**Gaus Width: 4.0237**

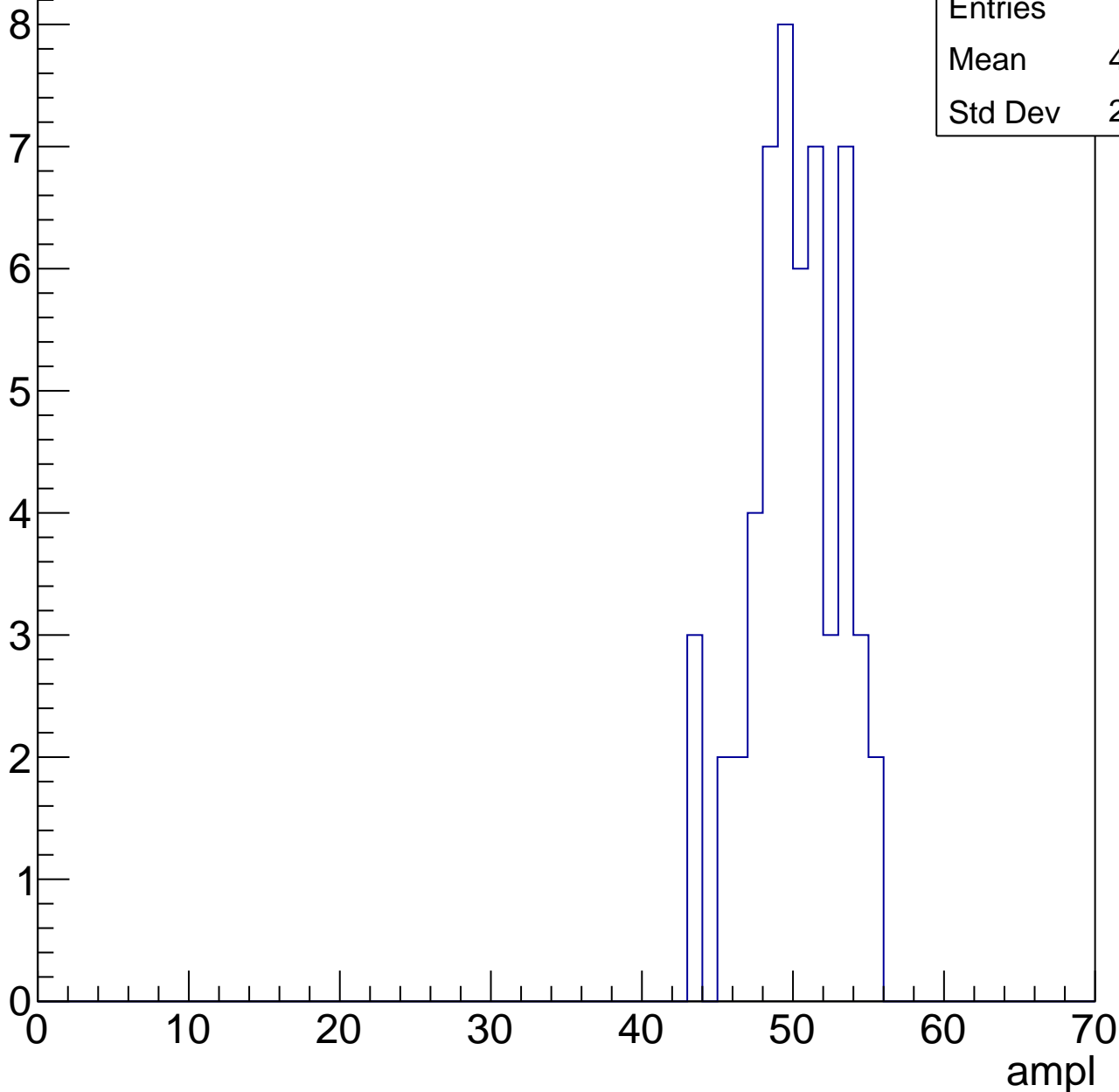


# B0L102S, U6-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	49.69
Std Dev	2.968



# B0L102S, U6-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	56.2
Std Dev	3.031

Entry

10

8

6

4

2

0

0

10

20

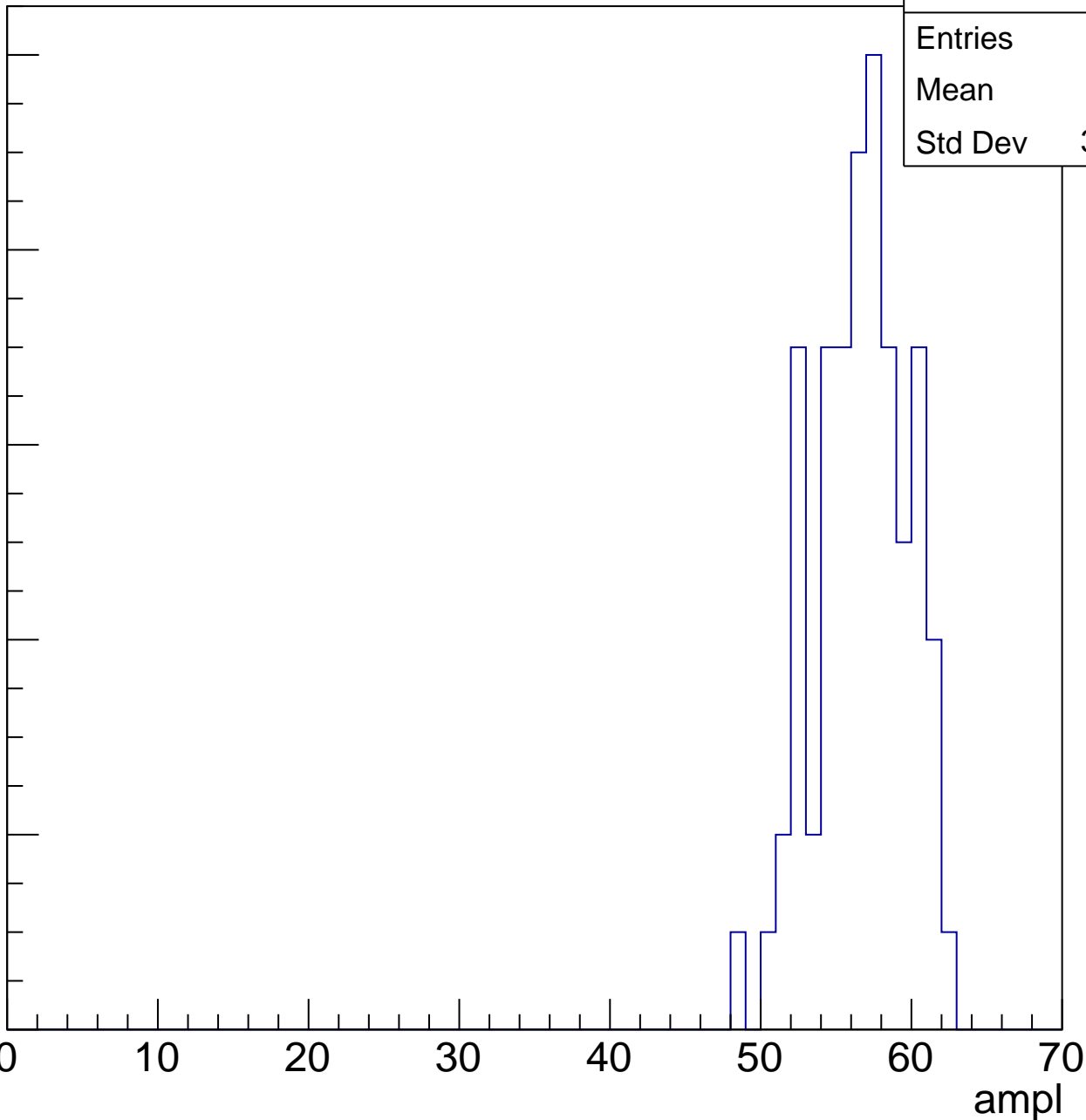
30

40

50

60

ampl

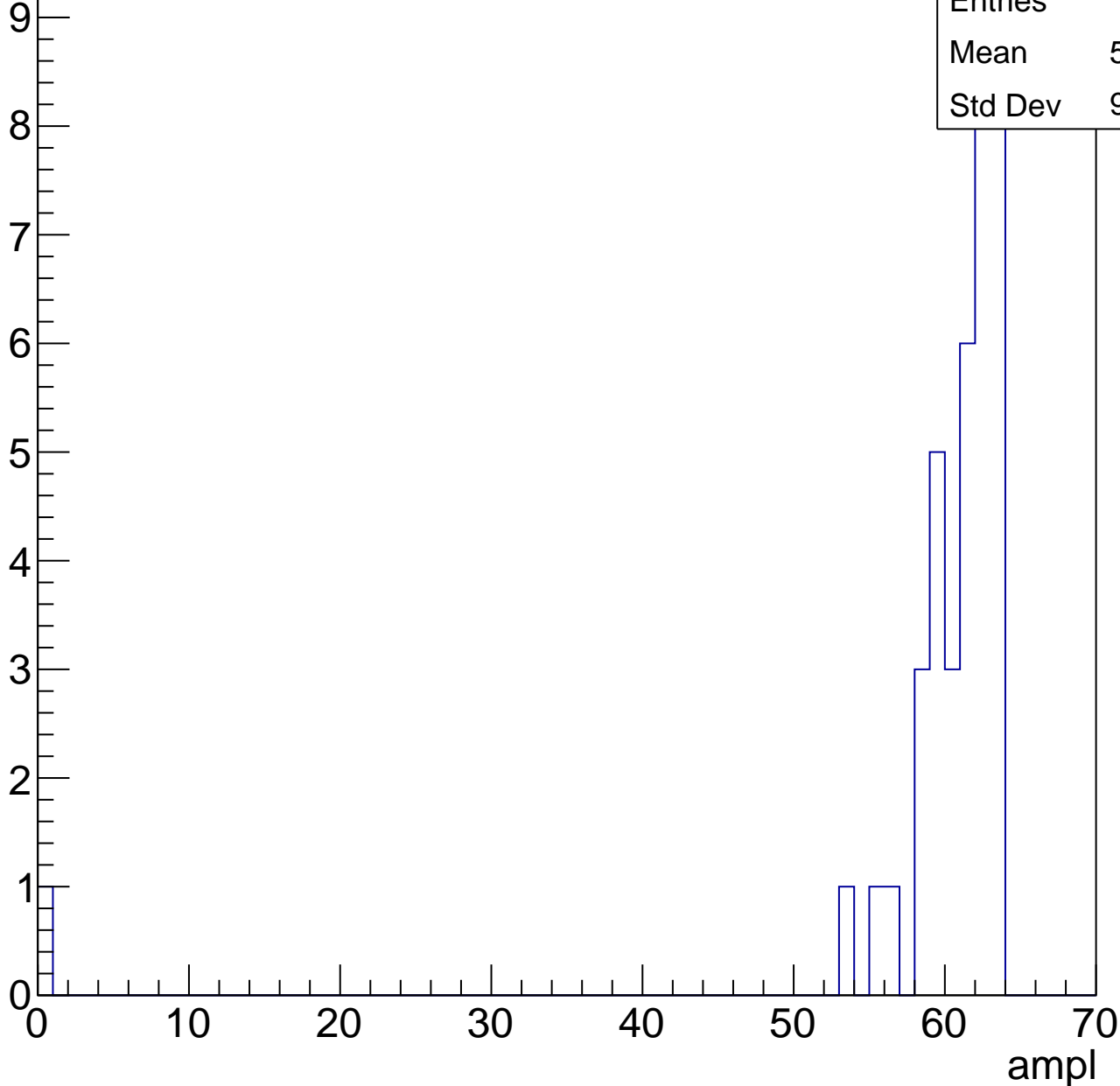


# B0L102S, U6-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	39
Mean	59.08
Std Dev	9.867



# B0L102S, U6-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L102S, U6-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch66, adc0

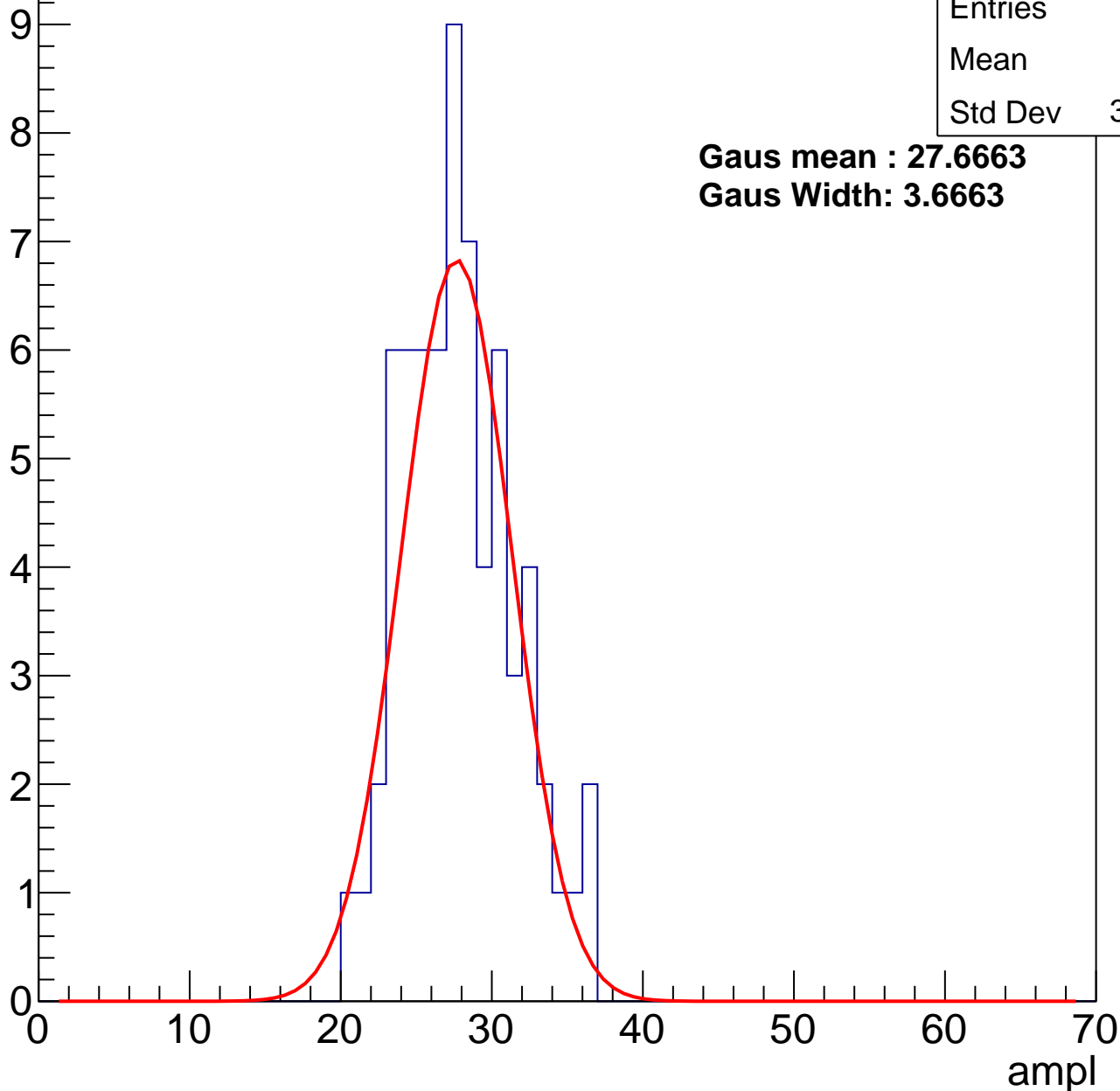
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	27.4
Std Dev	3.616

**Gaus mean : 27.6663**

**Gaus Width: 3.6663**



# B0L102S, U6-ch66, adc1

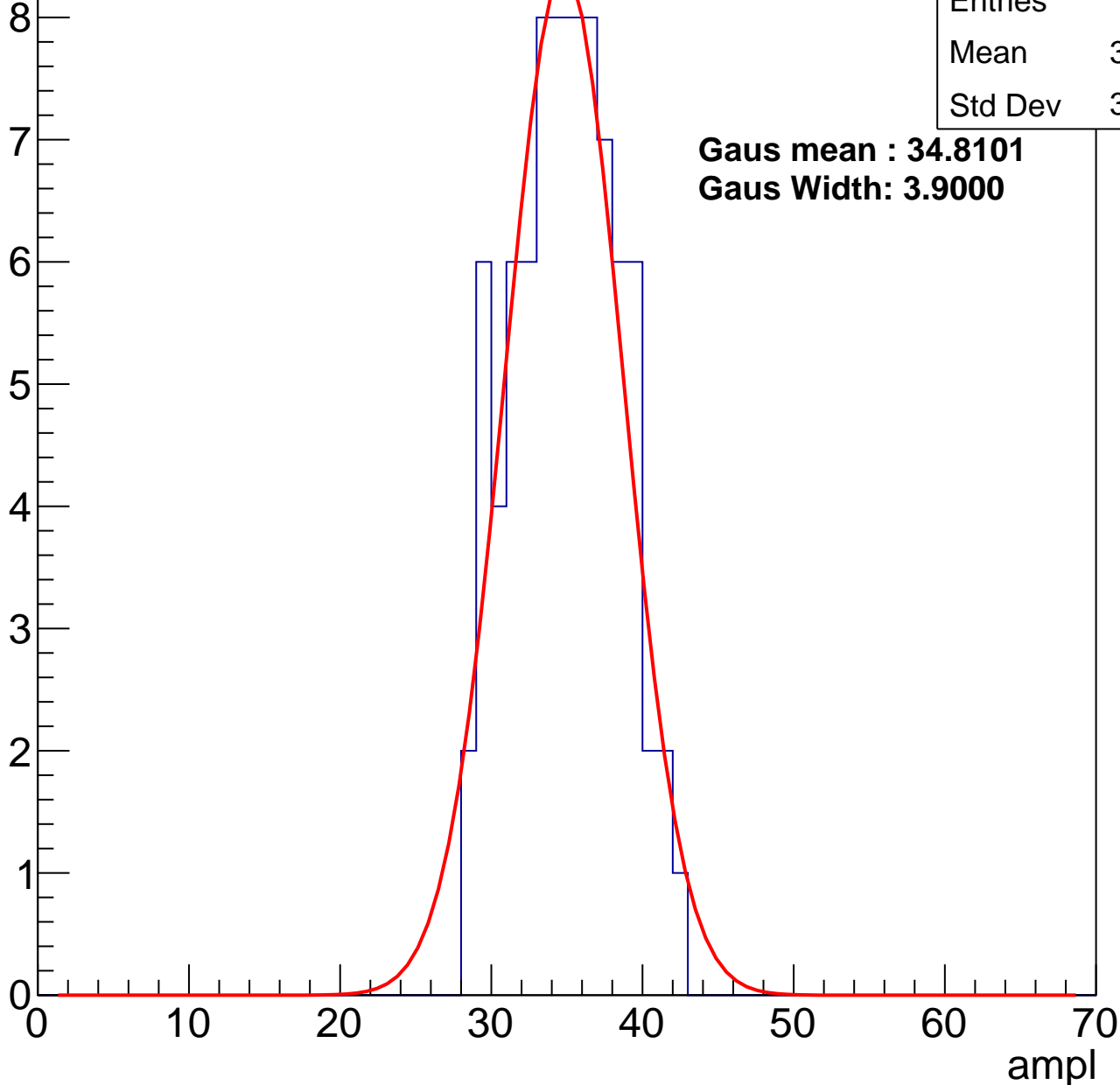
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	34.46
Std Dev	3.424

**Gaus mean : 34.8101**

**Gaus Width: 3.9000**



# B0L102S, U6-ch66, adc2

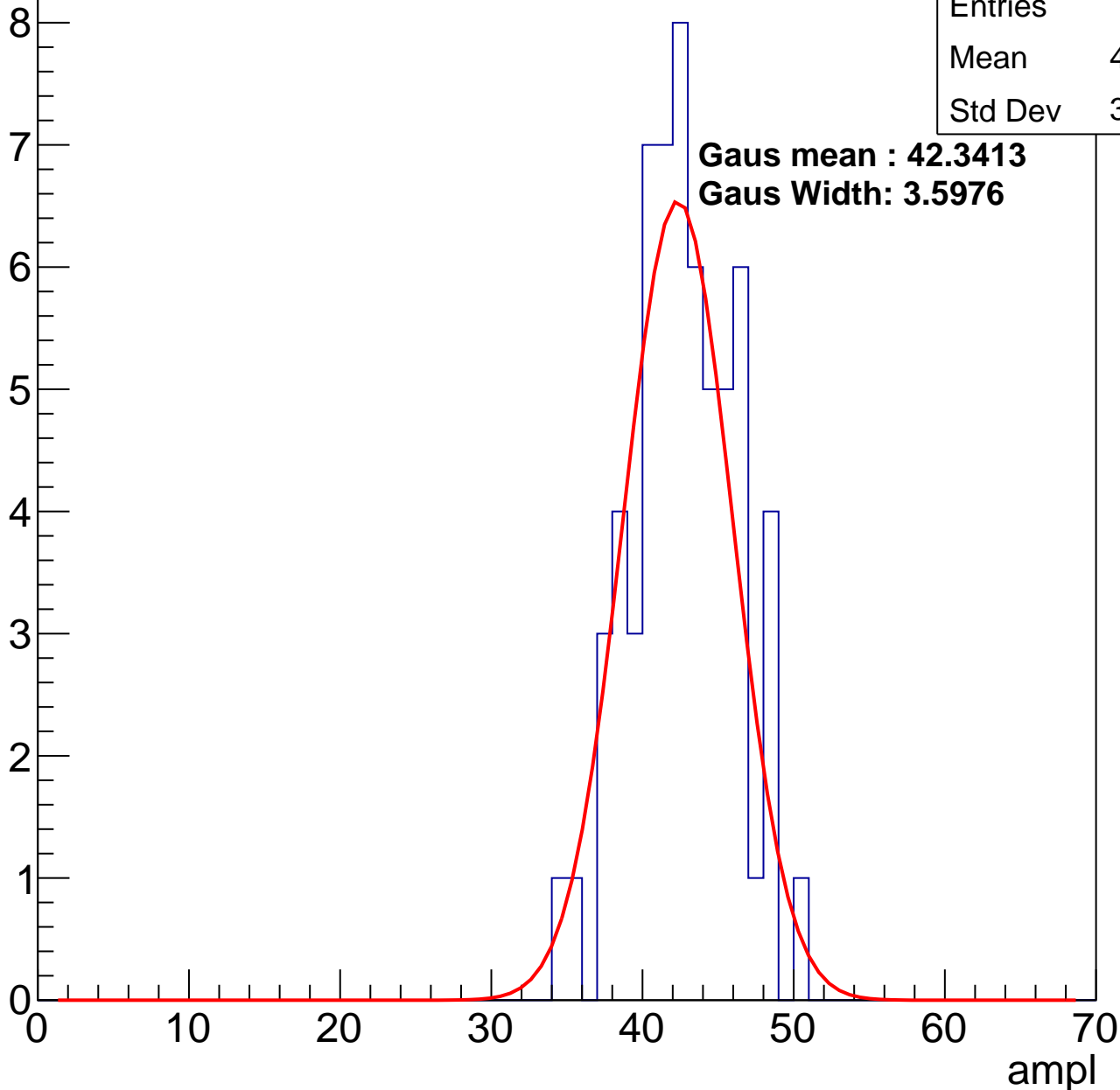
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.26
Std Dev	3.384

**Gaus mean : 42.3413**

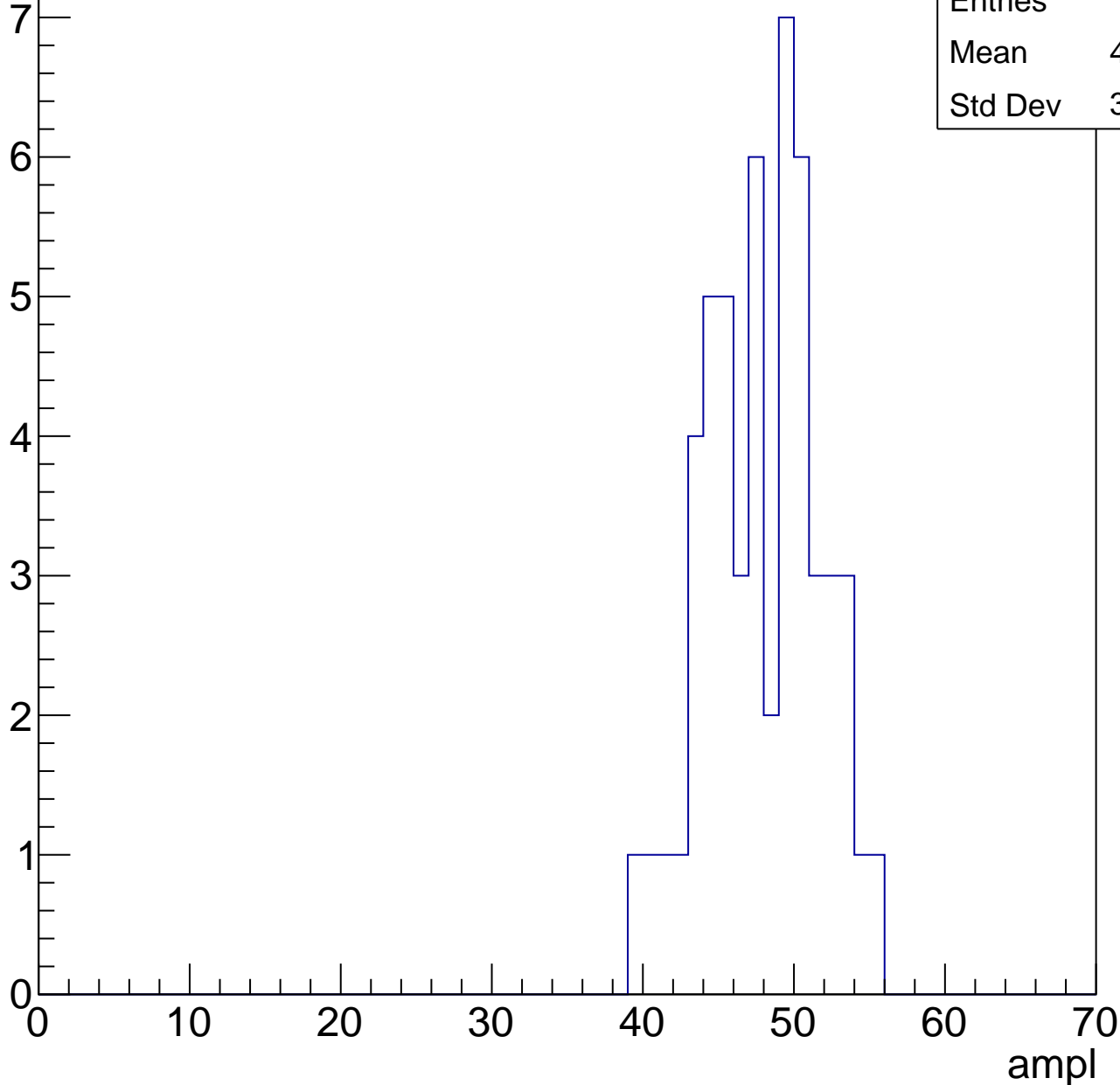
**Gaus Width: 3.5976**



# B0L102S, U6-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

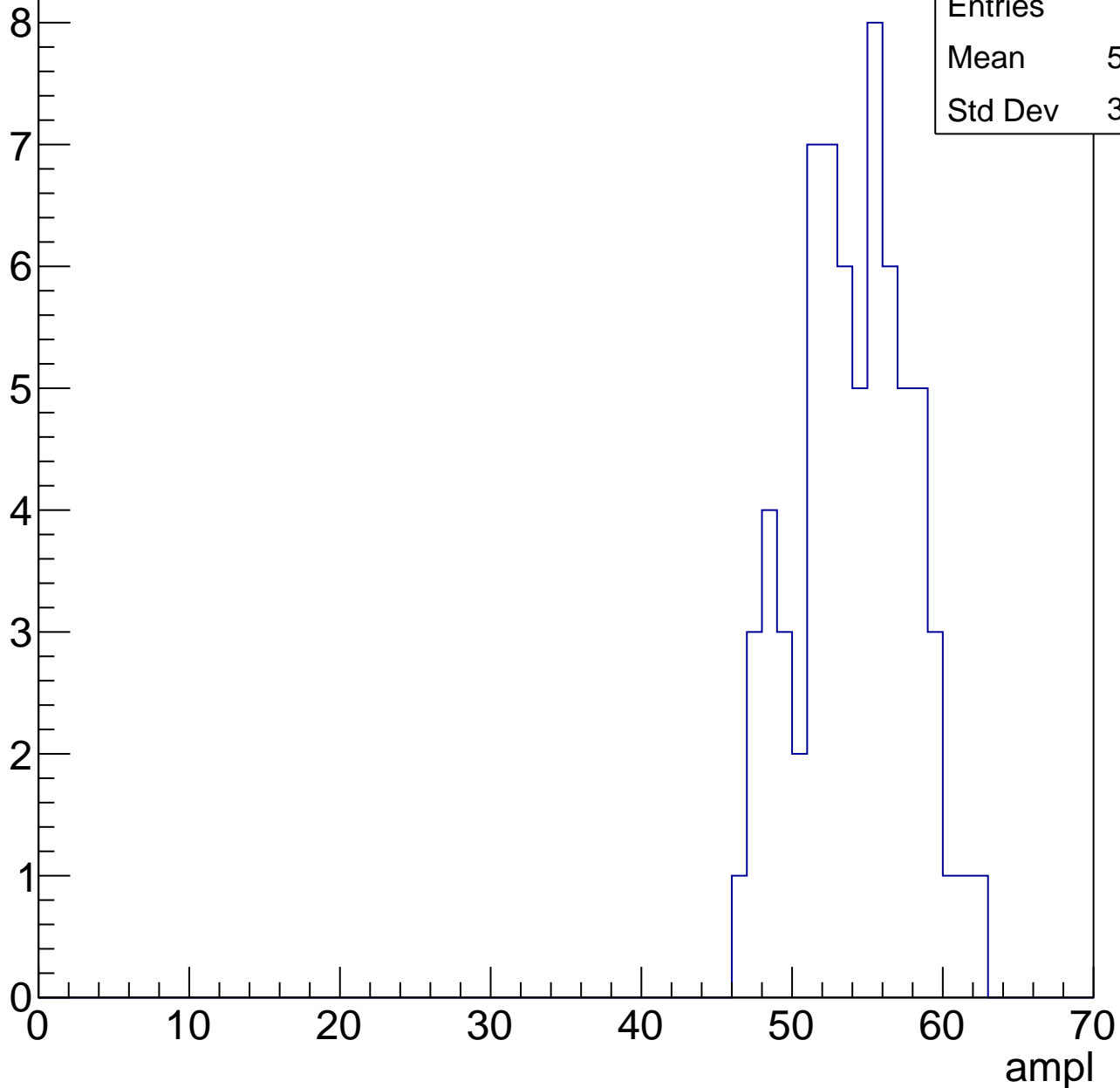


Entries	53
Mean	47.45
Std Dev	3.694

# B0L102S, U6-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	68
Mean	53.62
Std Dev	3.698

# B0L102S, U6-ch66, adc5

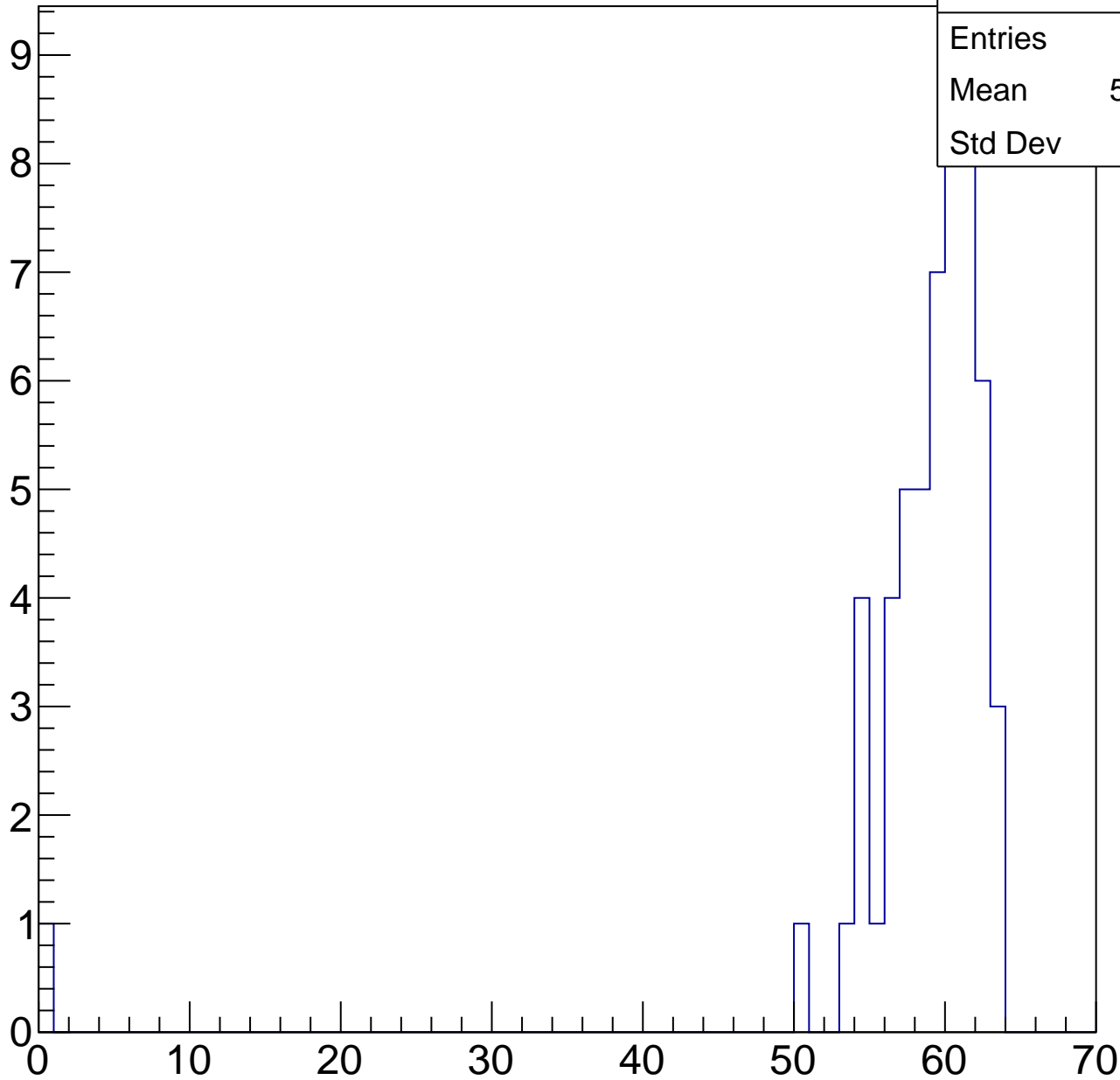
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	57.75
Std Dev	8.35

ampl

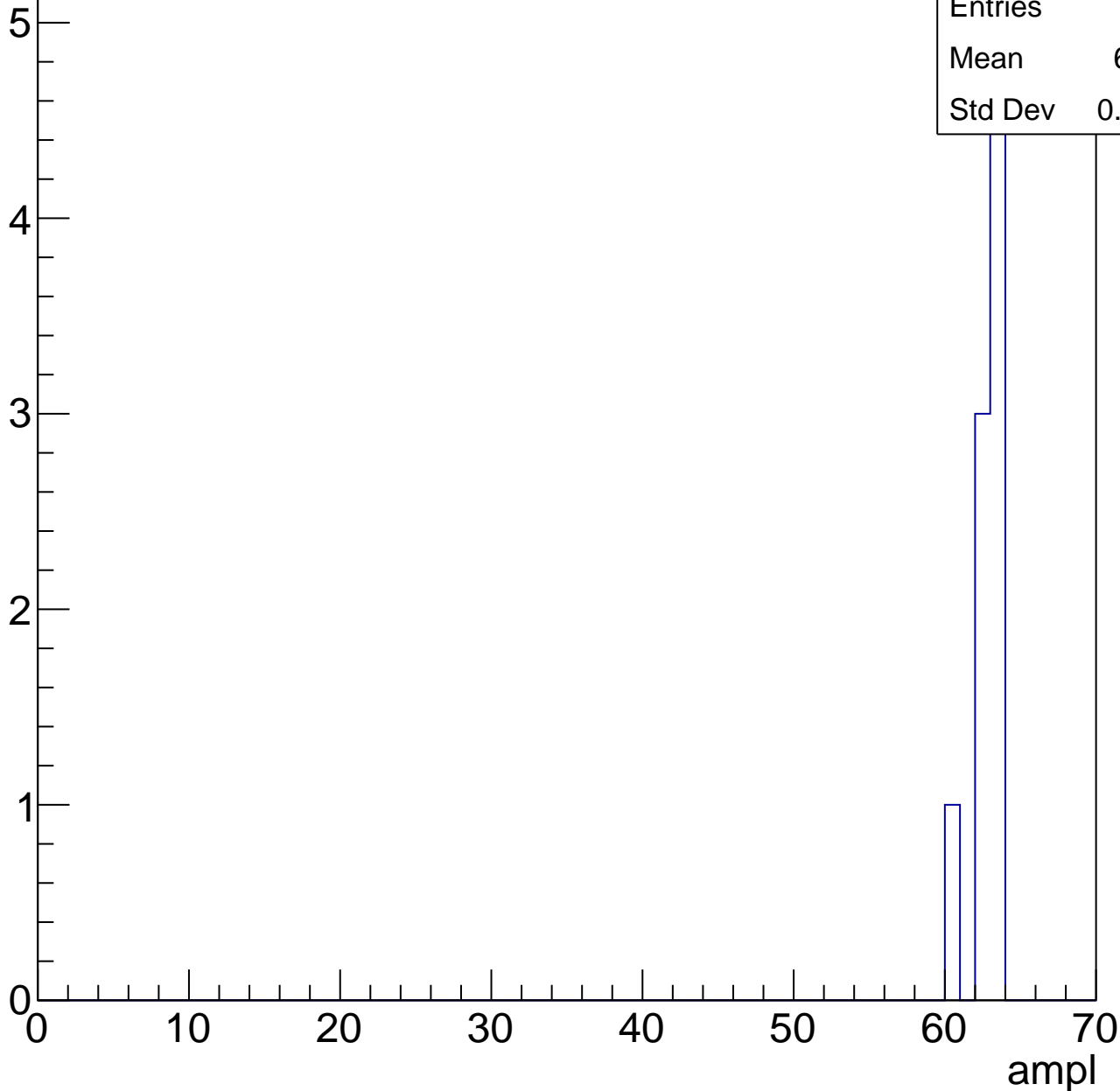


# B0L102S, U6-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	9
Mean	62.33
Std Dev	0.9428





# B0L102S, U6-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	48.33
Std Dev	20.74

0 10 20 30 40 50 60 70

ampl

1

# B0L102S, U6-ch67, adc0

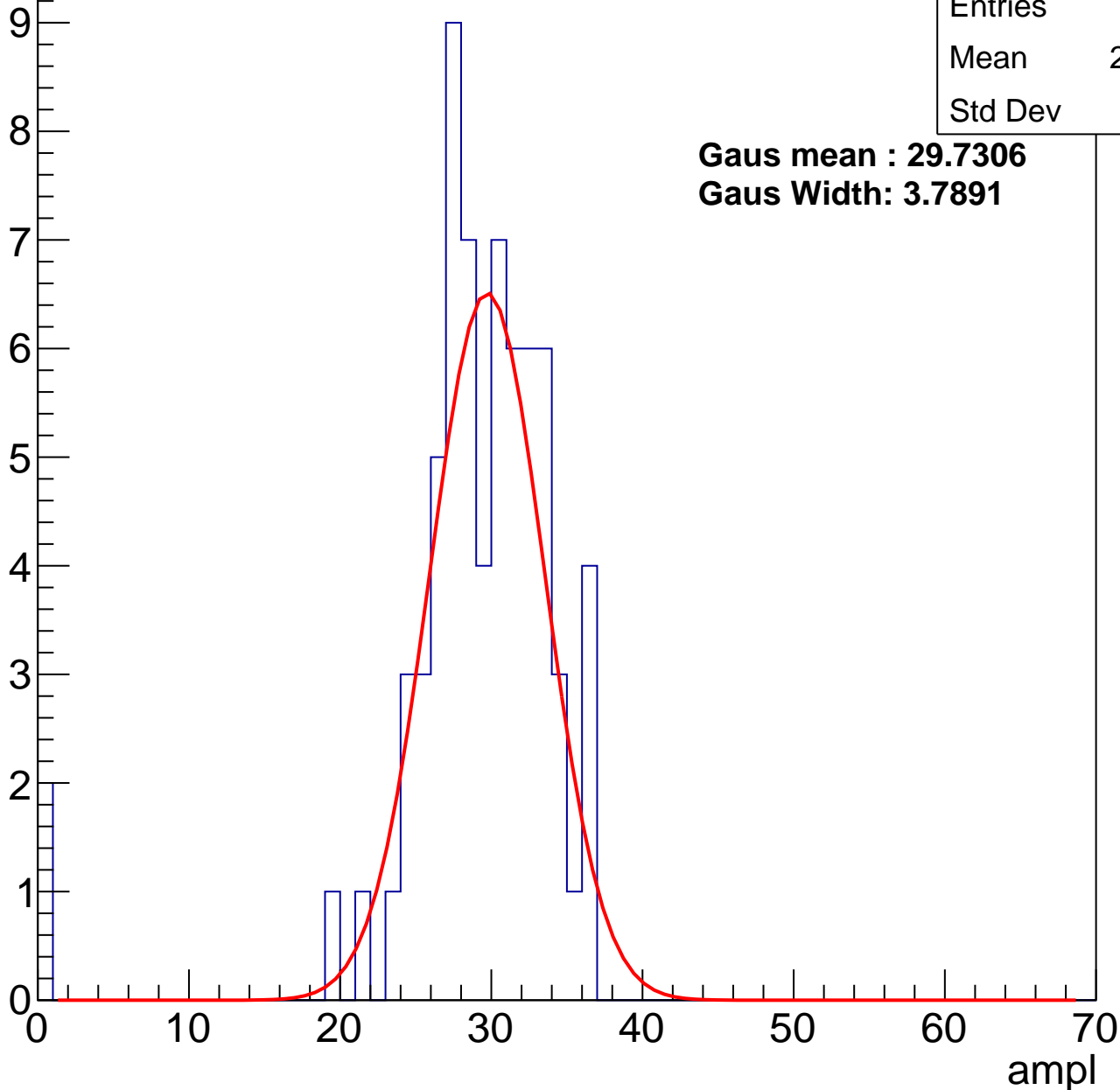
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	28.43
Std Dev	6.09

**Gaus mean : 29.7306**

**Gaus Width: 3.7891**



# B0L102S, U6-ch67, adc1

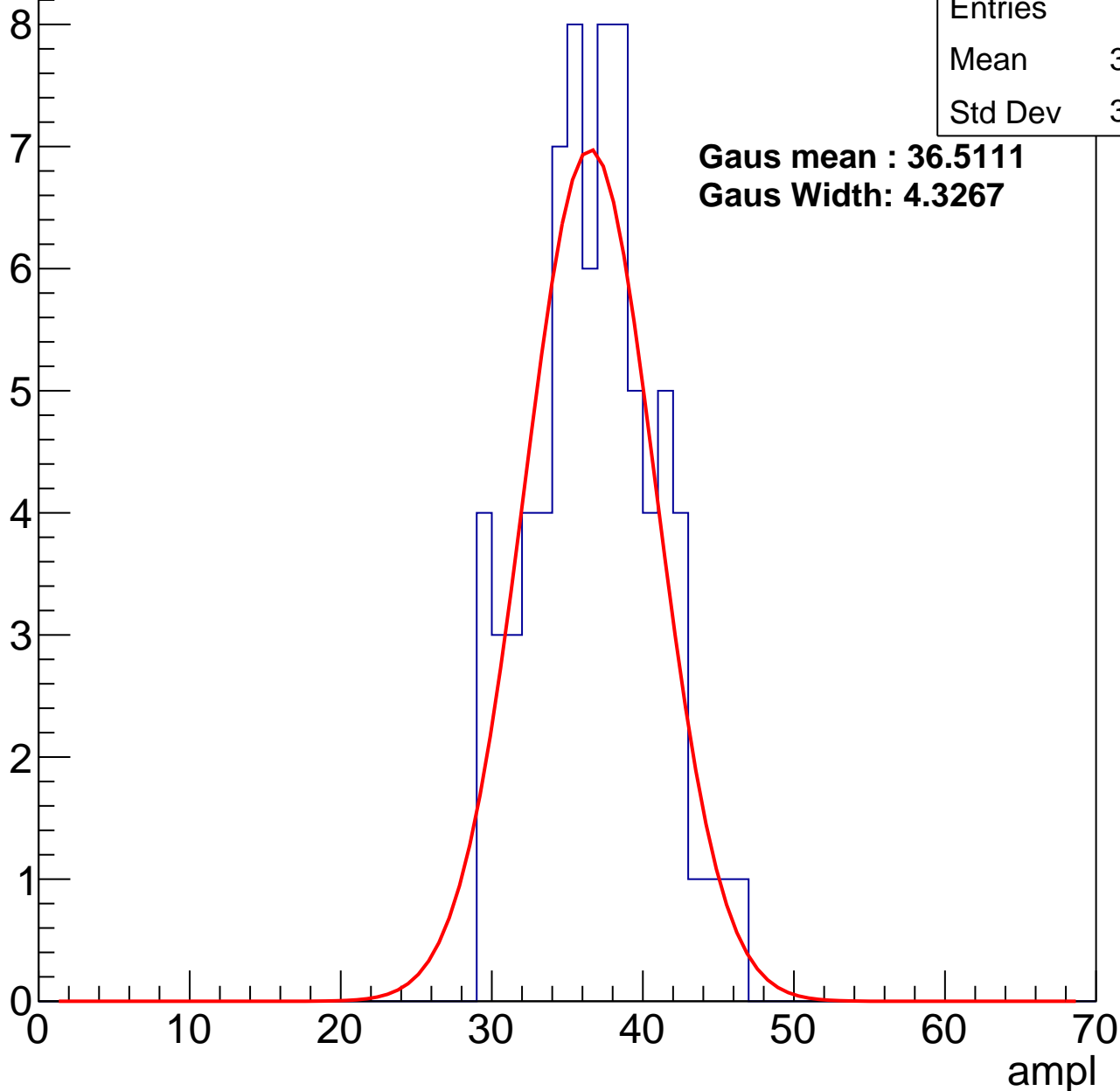
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	36.35
Std Dev	3.976

**Gaus mean : 36.5111**

**Gaus Width: 4.3267**



# B0L102S, U6-ch67, adc2

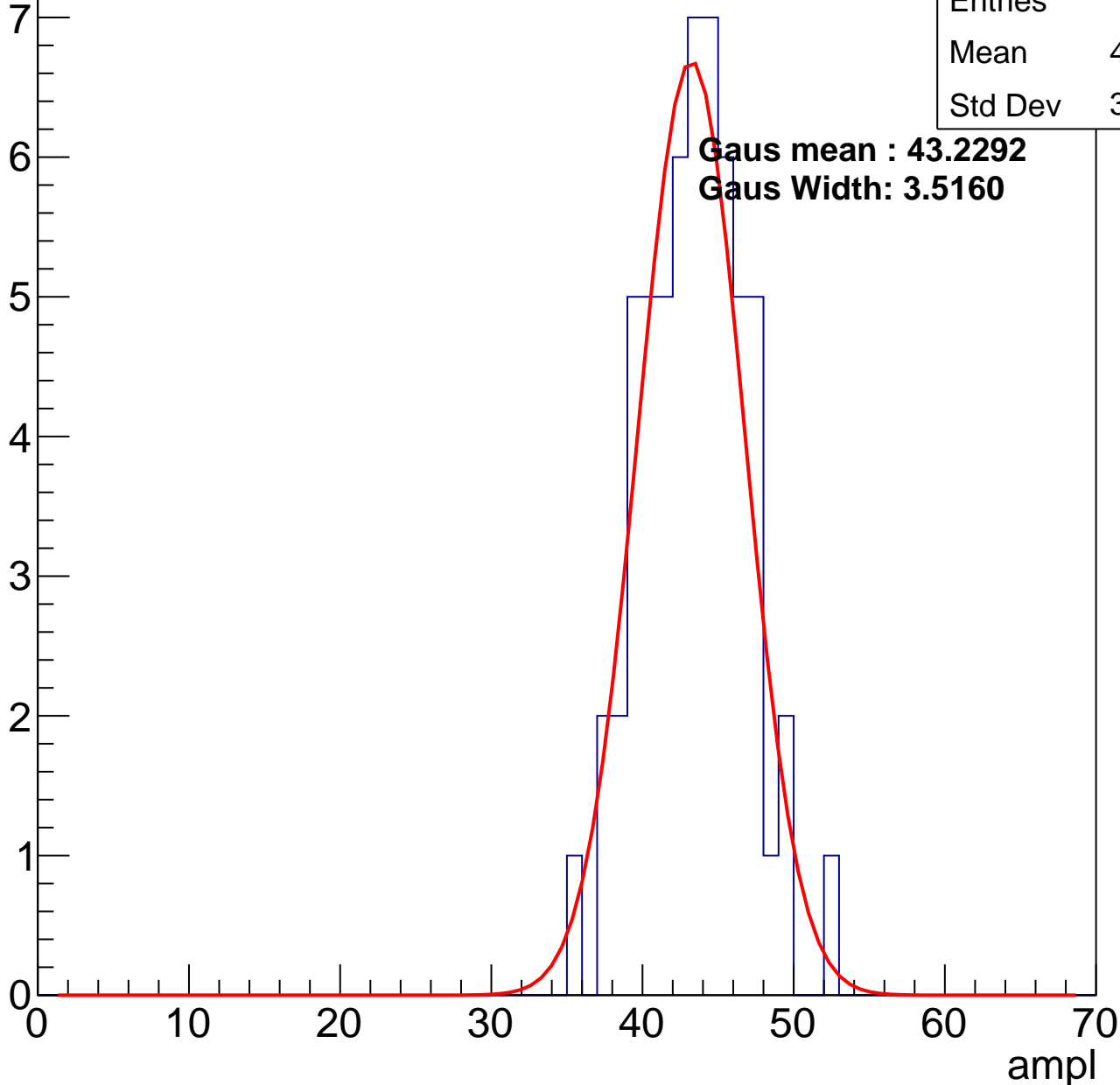
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.98
Std Dev	3.344

**Gaus mean : 43.2292**

**Gaus Width: 3.5160**



# B0L102S, U6-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

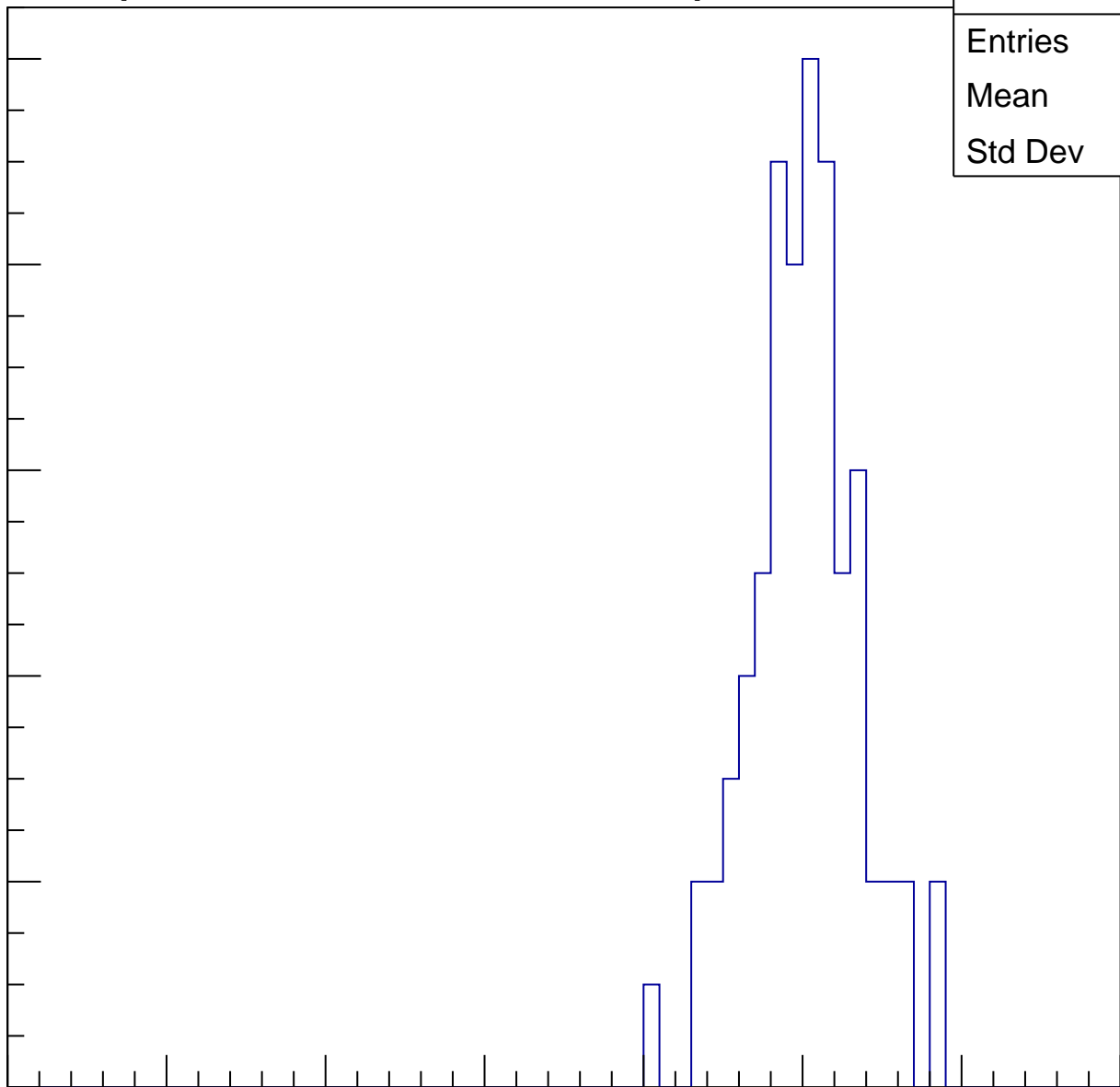
Entries	72
Mean	49.65
Std Dev	3.445

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

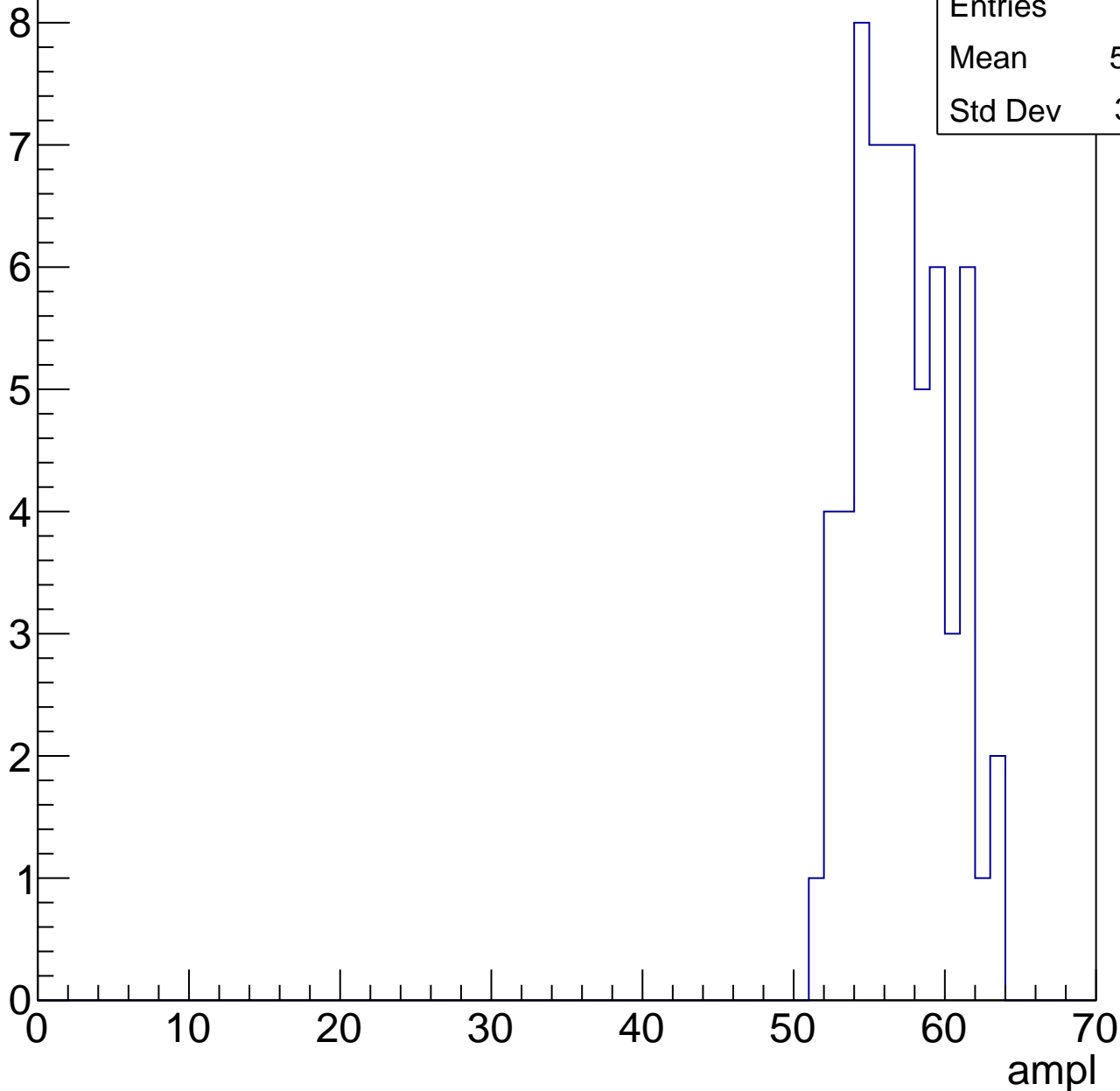


# B0L102S, U6-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	56.67
Std Dev	3.001

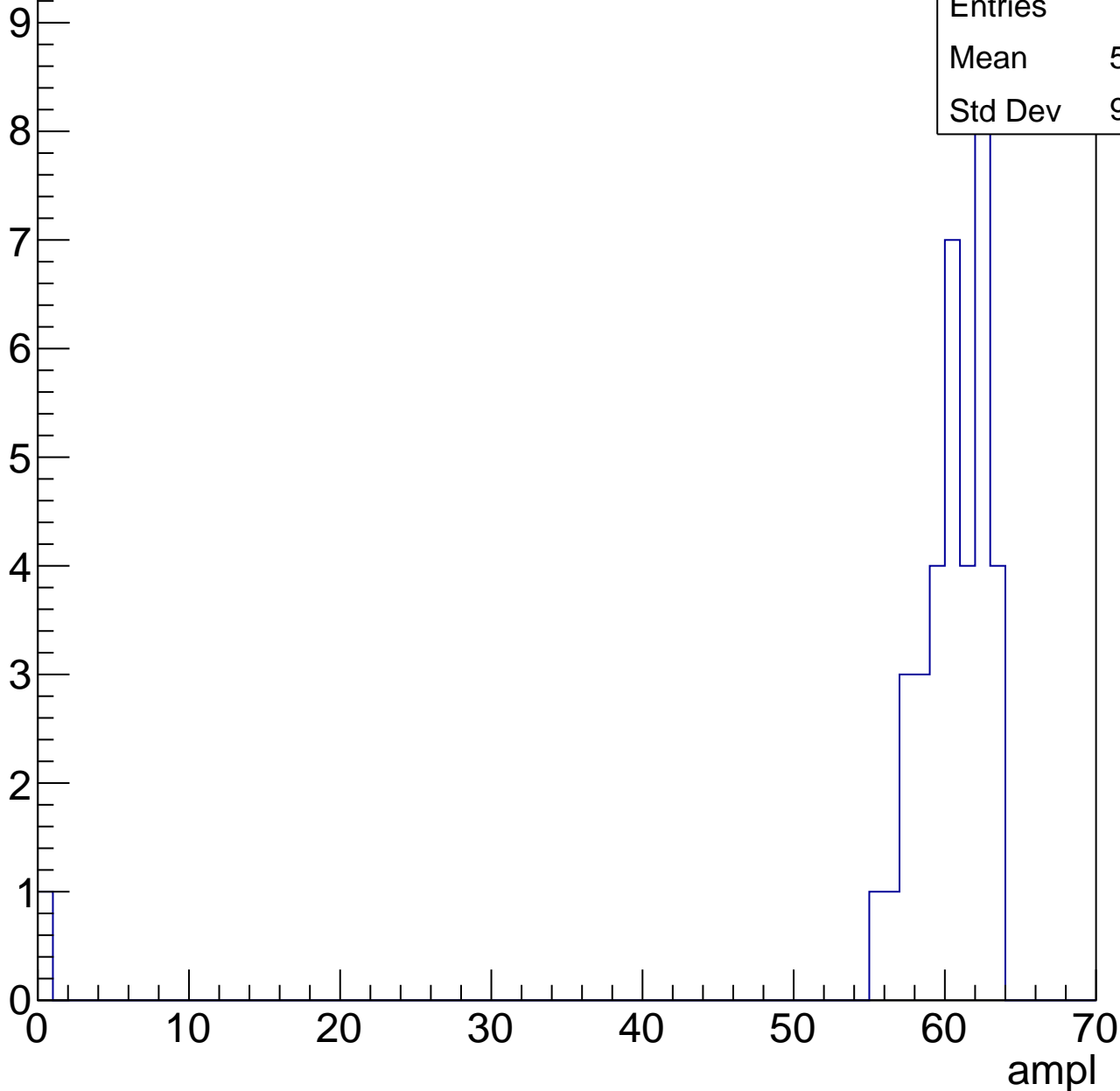


# B0L102S, U6-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

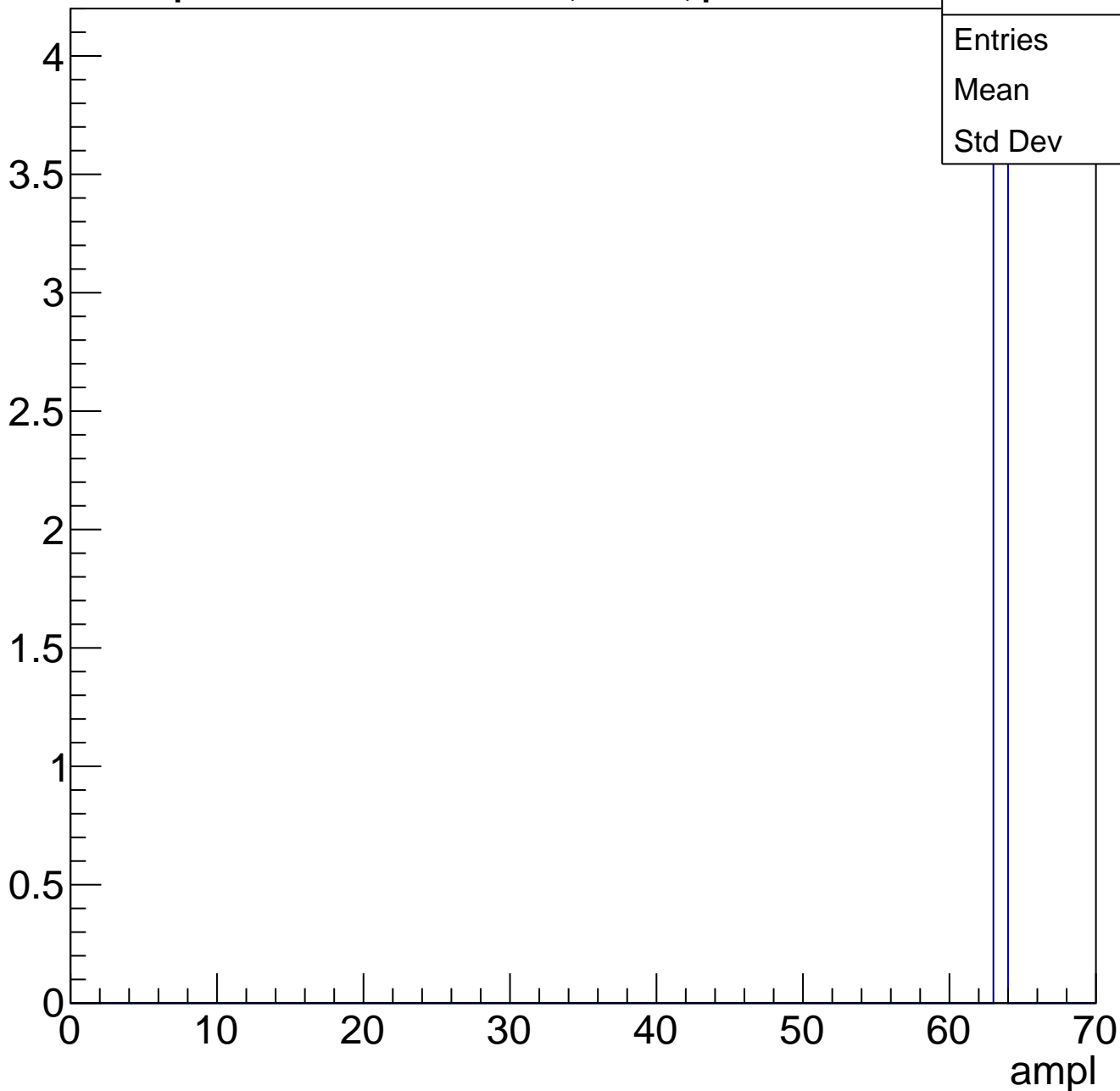
Entries	37
Mean	58.54
Std Dev	9.975



# B0L102S, U6-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch68, adc0

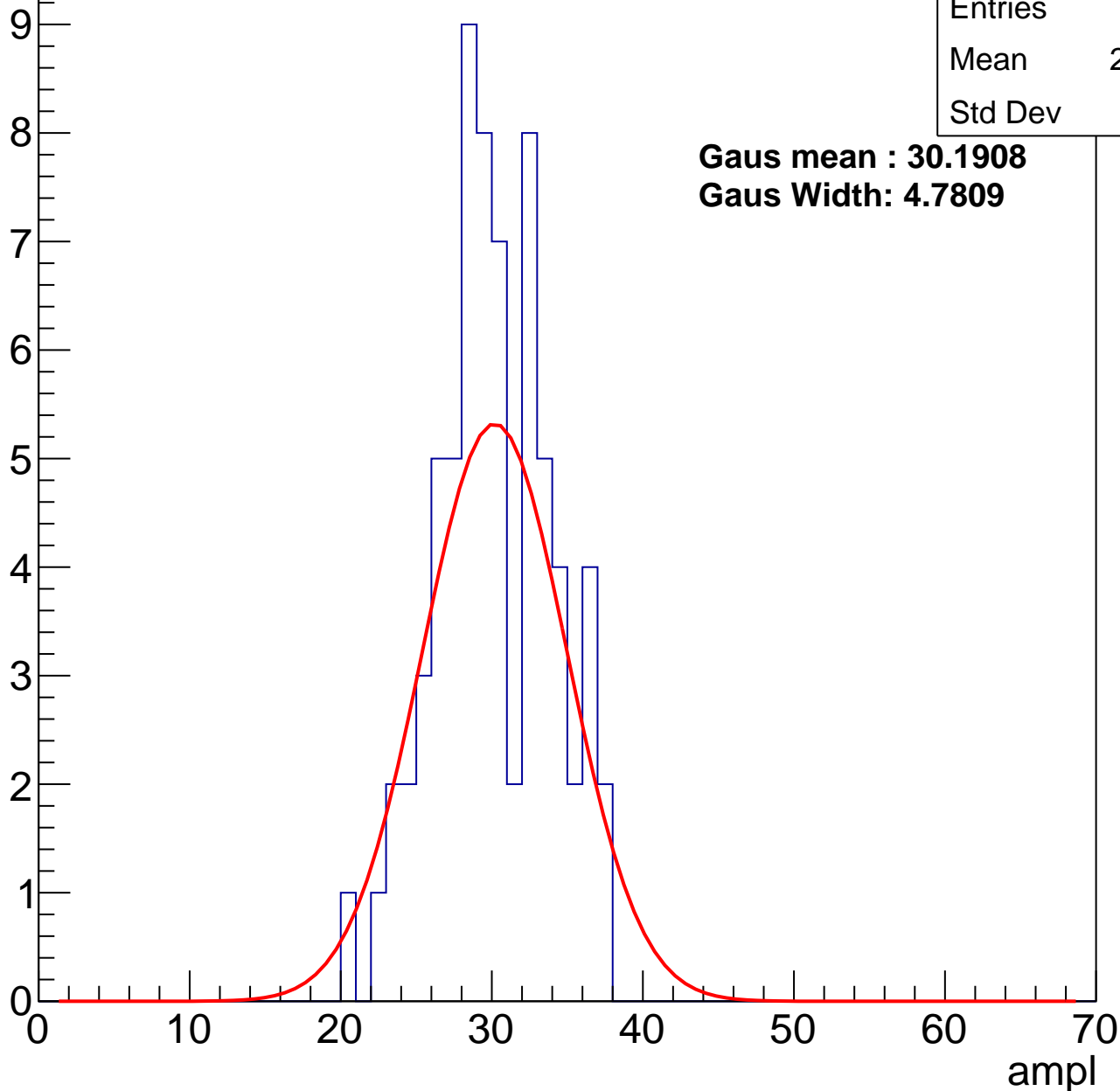
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	29.67
Std Dev	3.79

**Gaus mean : 30.1908**

**Gaus Width: 4.7809**



# B0L102S, U6-ch68, adc1

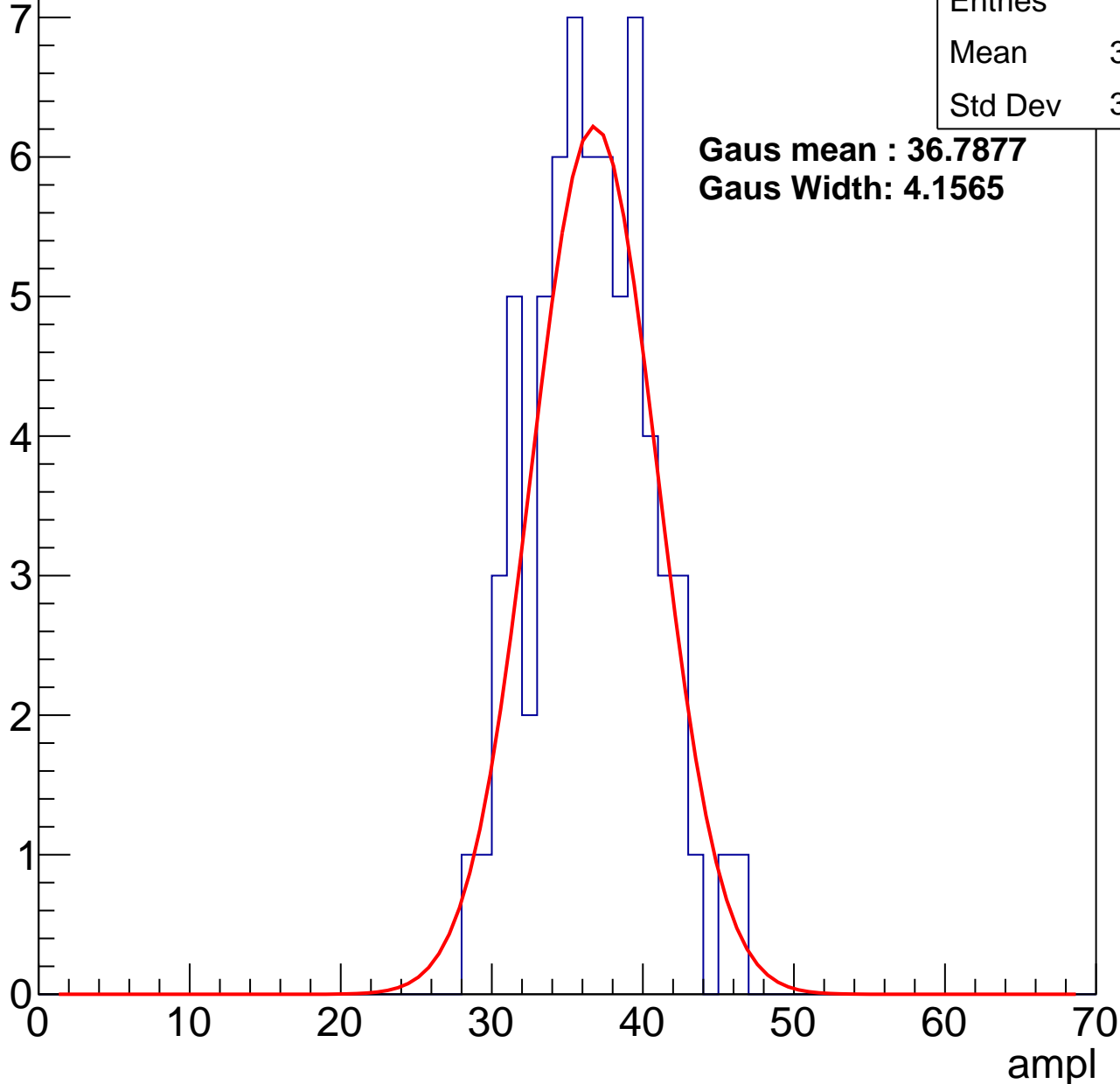
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	36.18
Std Dev	3.898

**Gaus mean : 36.7877**

**Gaus Width: 4.1565**



# B0L102S, U6-ch68, adc2

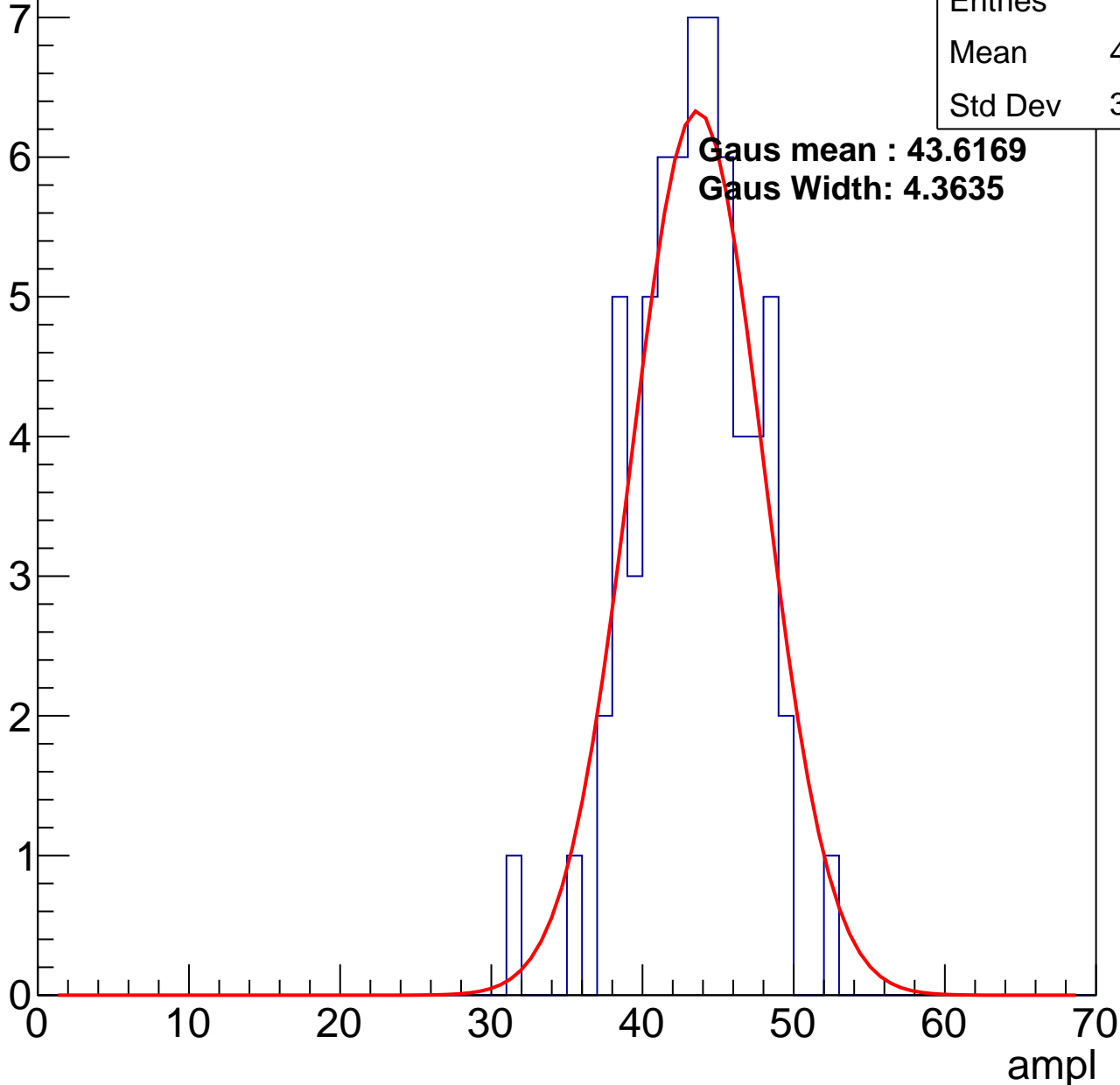
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.86
Std Dev	3.794

**Gaus mean : 43.6169**

**Gaus Width: 4.3635**

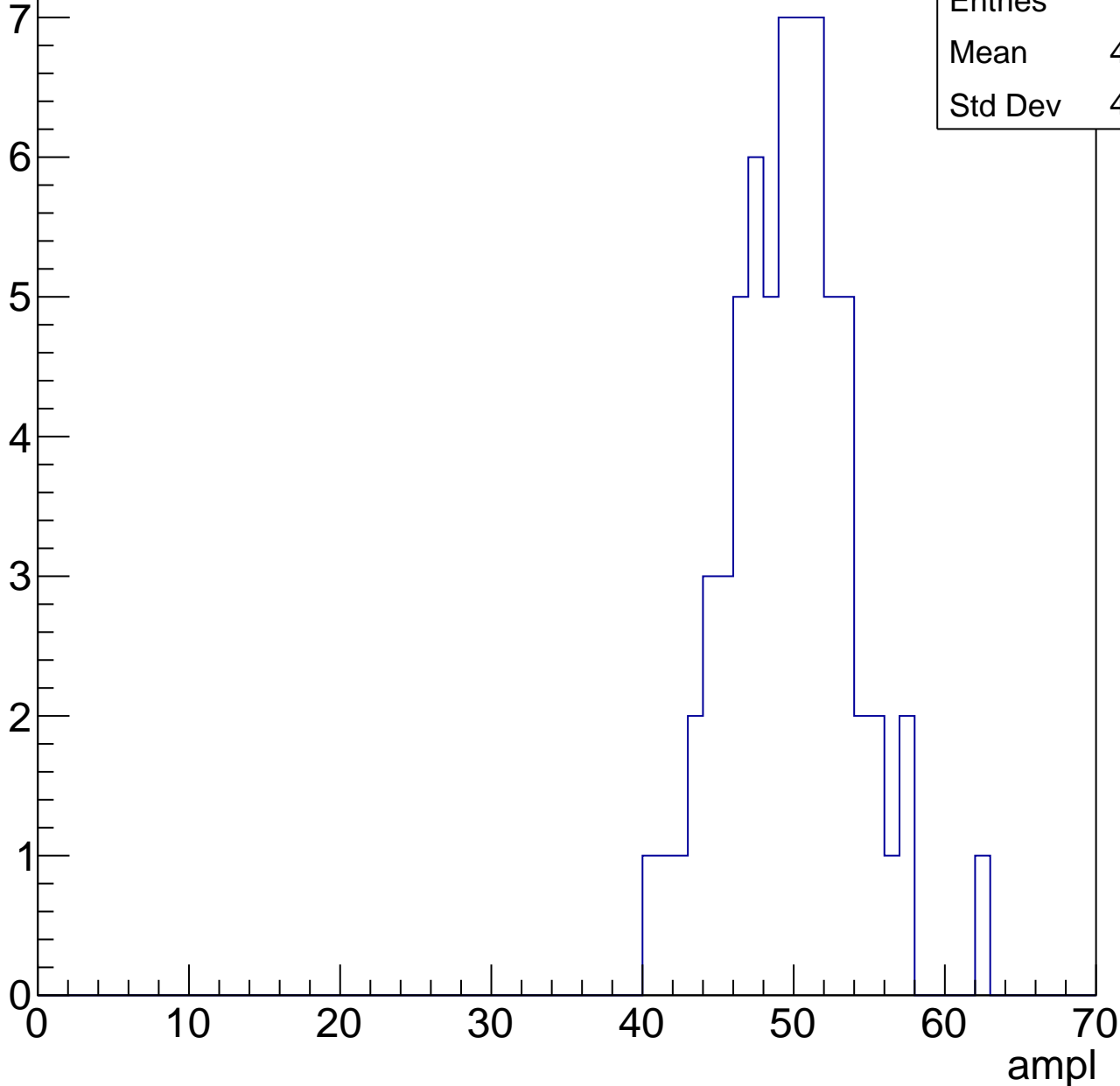


# B0L102S, U6-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	49.29
Std Dev	4.056

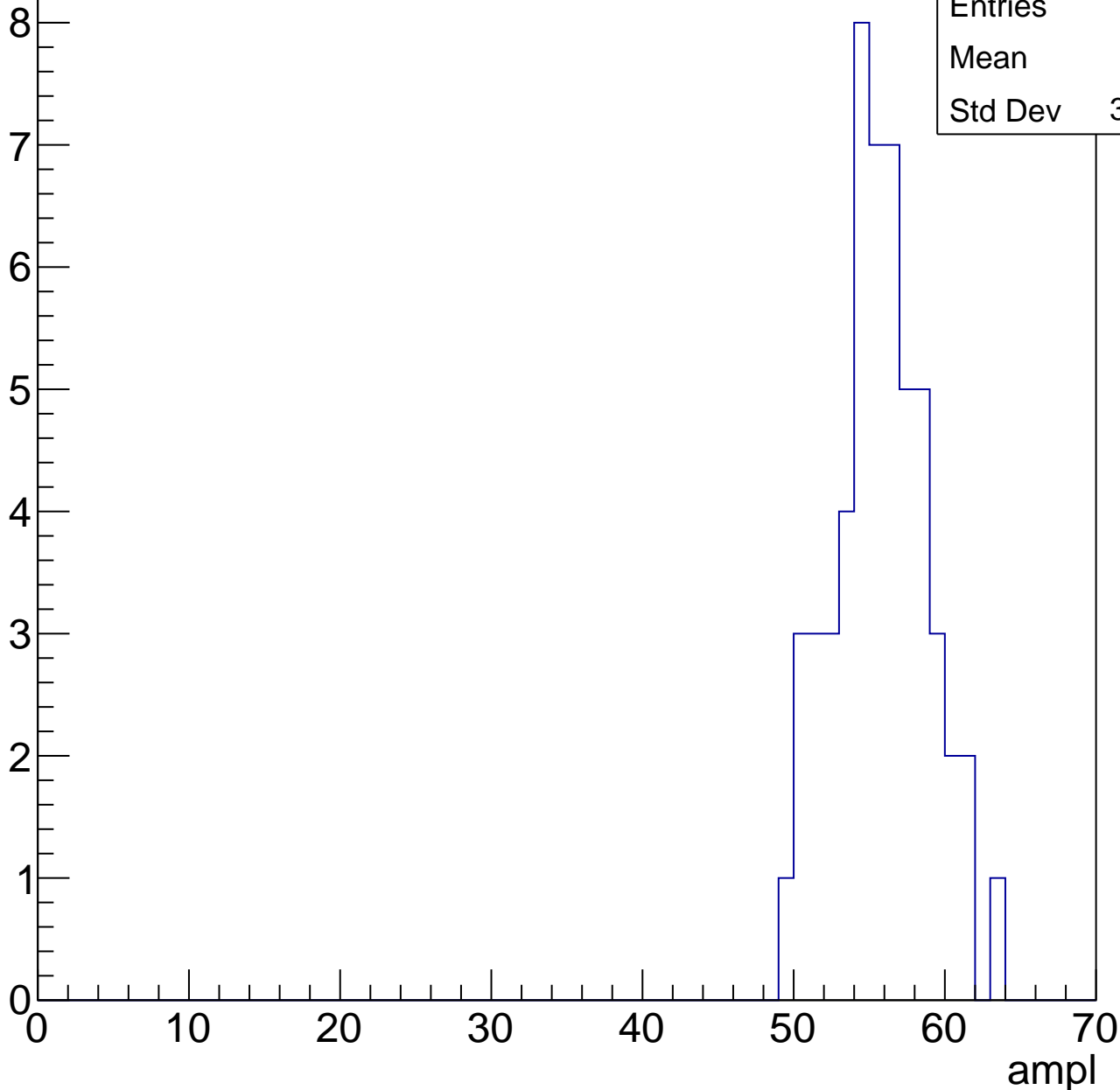


# B0L102S, U6-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	55.3
Std Dev	3.077

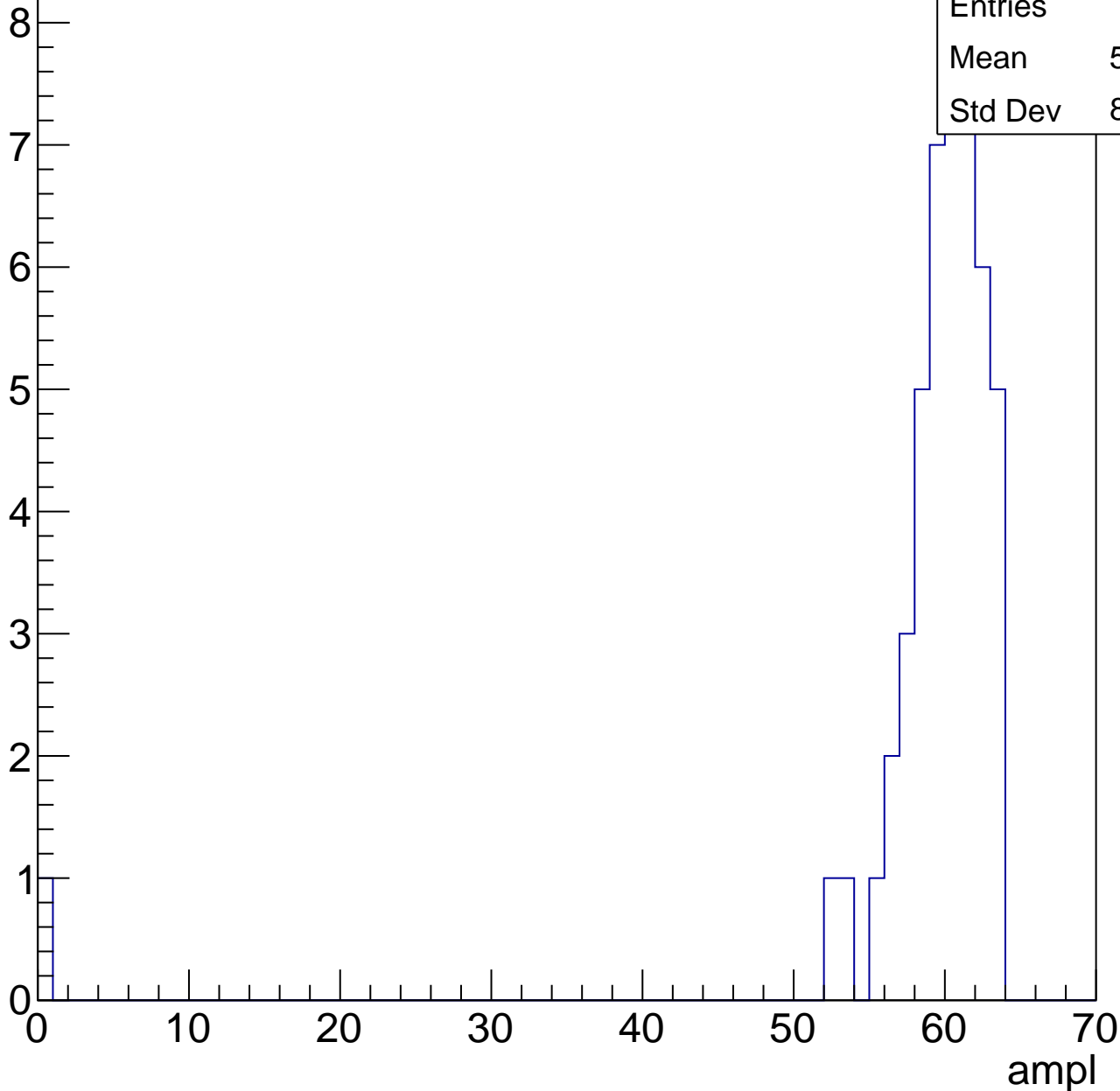


# B0L102S, U6-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

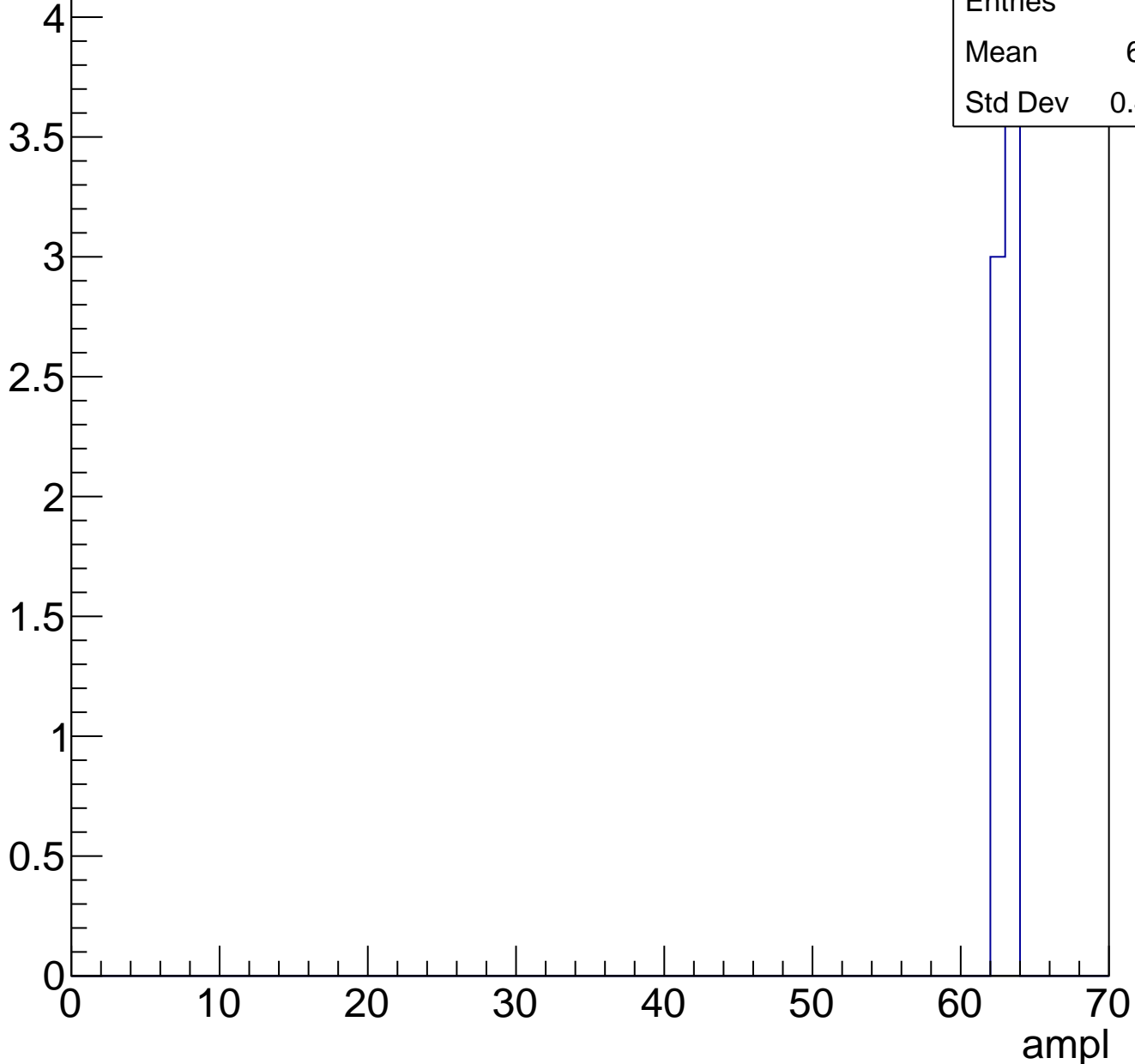
Entries	48
Mean	58.35
Std Dev	8.864



# B0L102S, U6-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch69, adc0

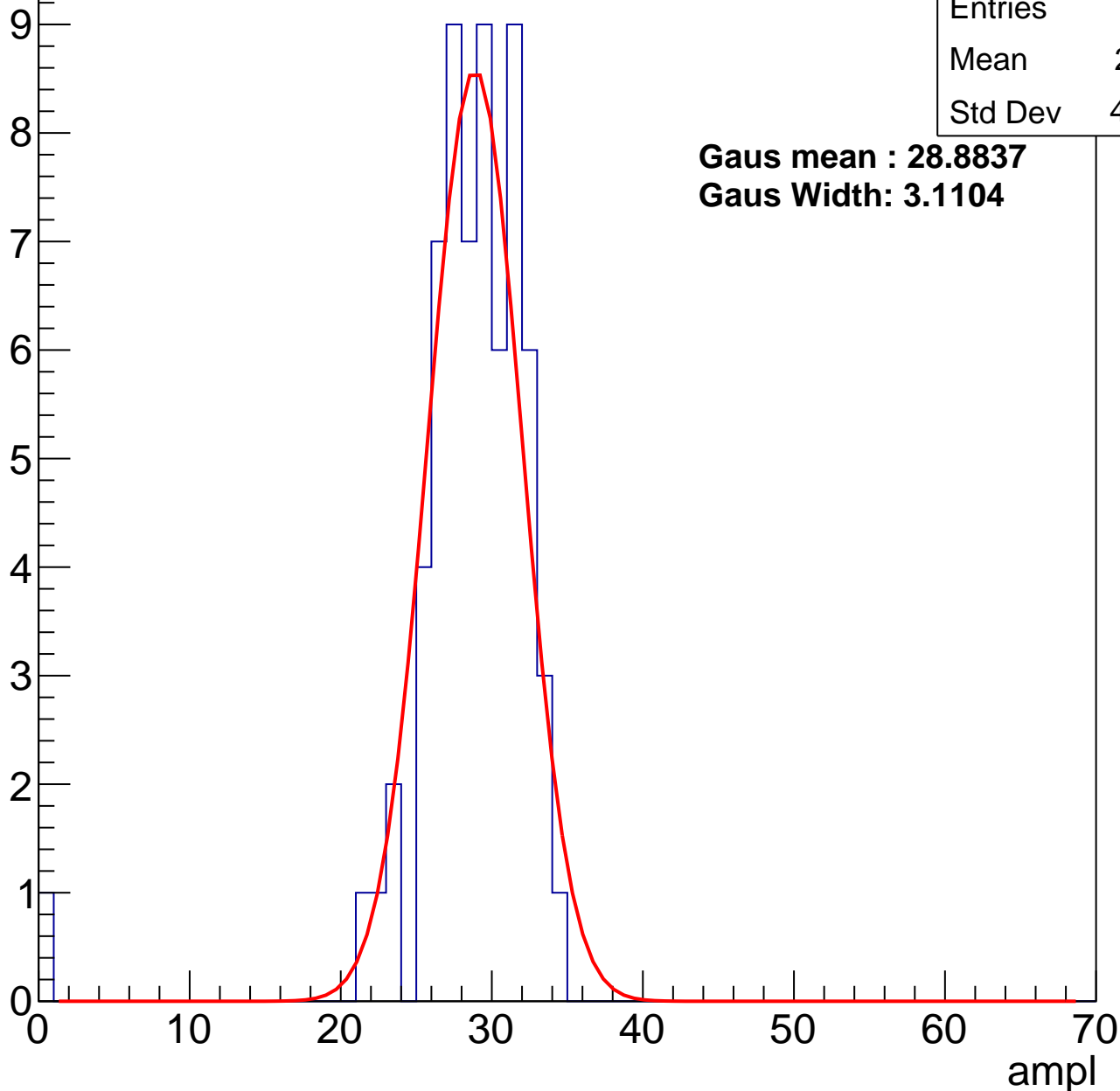
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	28.11
Std Dev	4.459

**Gaus mean : 28.8837**

**Gaus Width: 3.1104**



# B0L102S, U6-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	35.51
Std Dev	3.065

**Gaus mean : 36.2770**

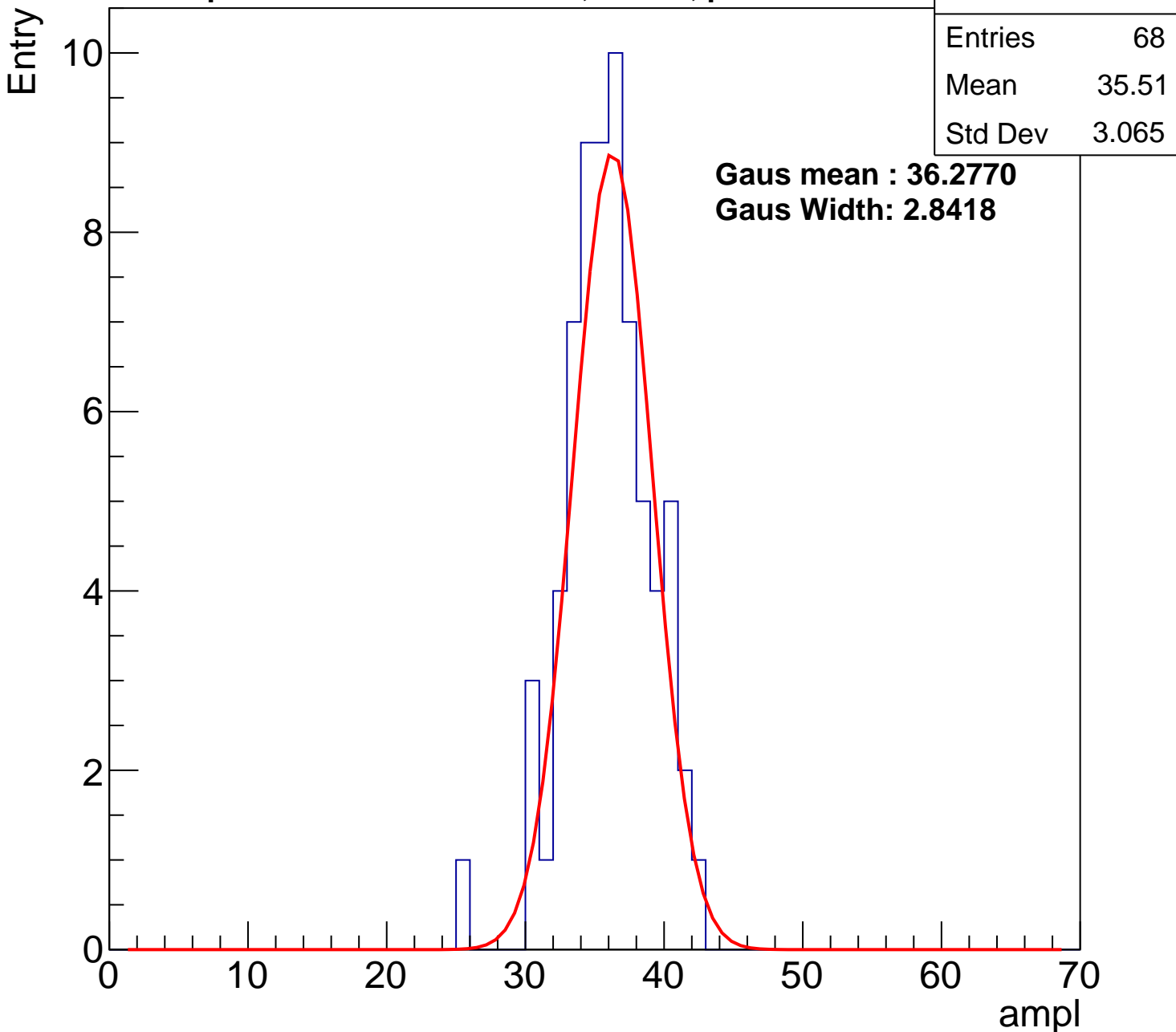
**Gaus Width: 2.8418**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U6-ch69, adc2

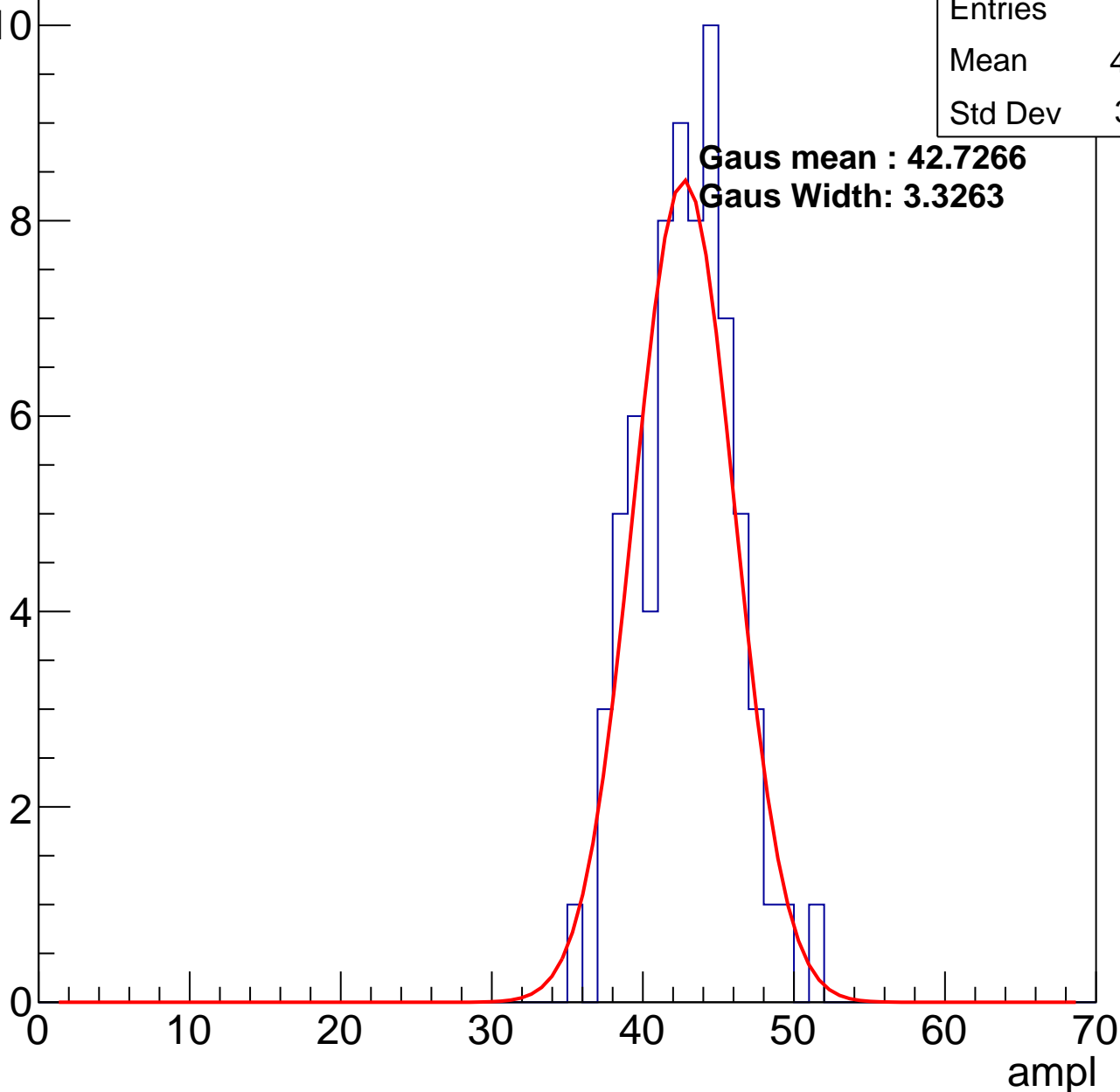
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	42.42
Std Dev	3.121

**Gaus mean : 42.7266**

**Gaus Width: 3.3263**

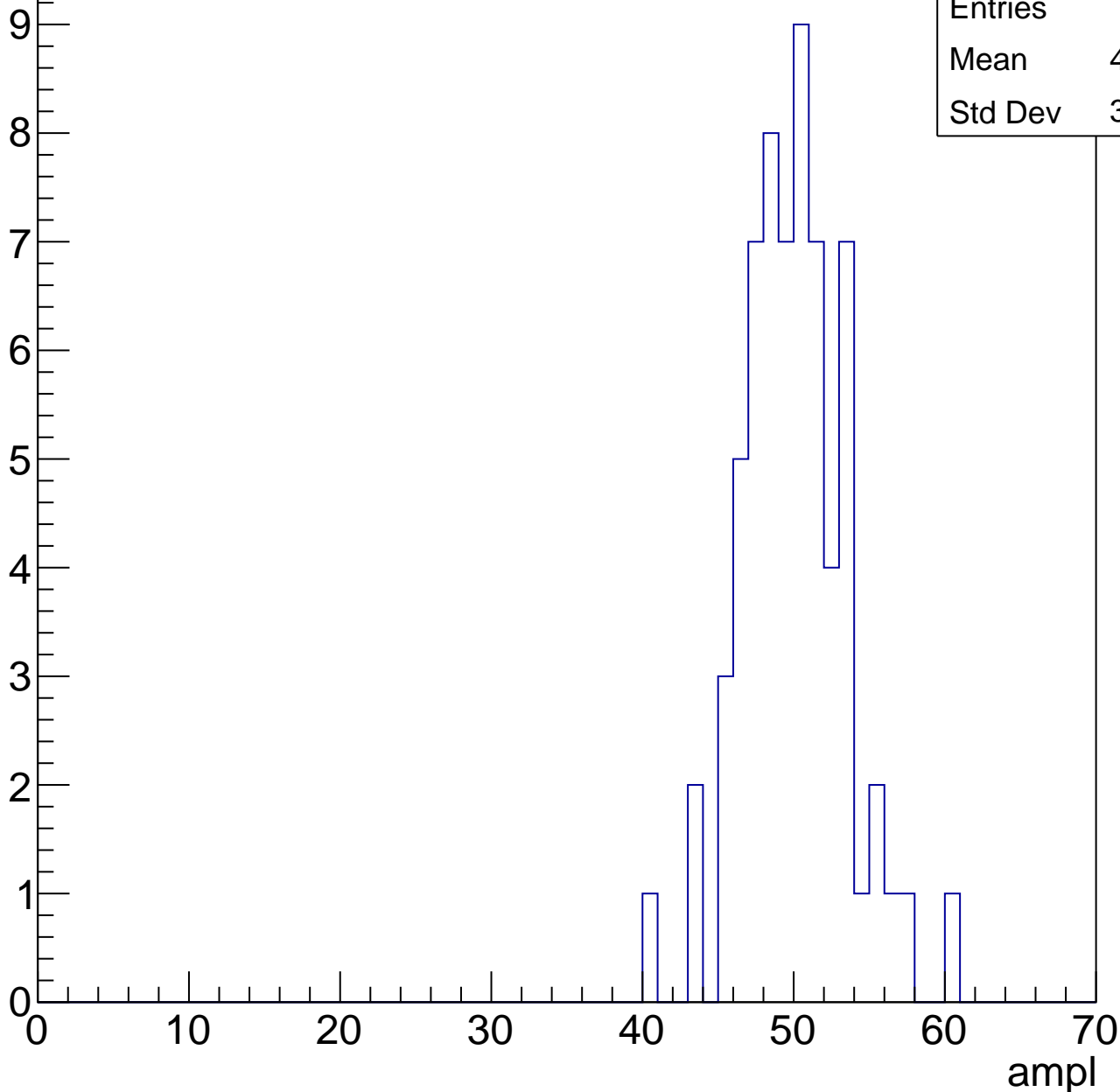


# B0L102S, U6-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	49.55
Std Dev	3.434

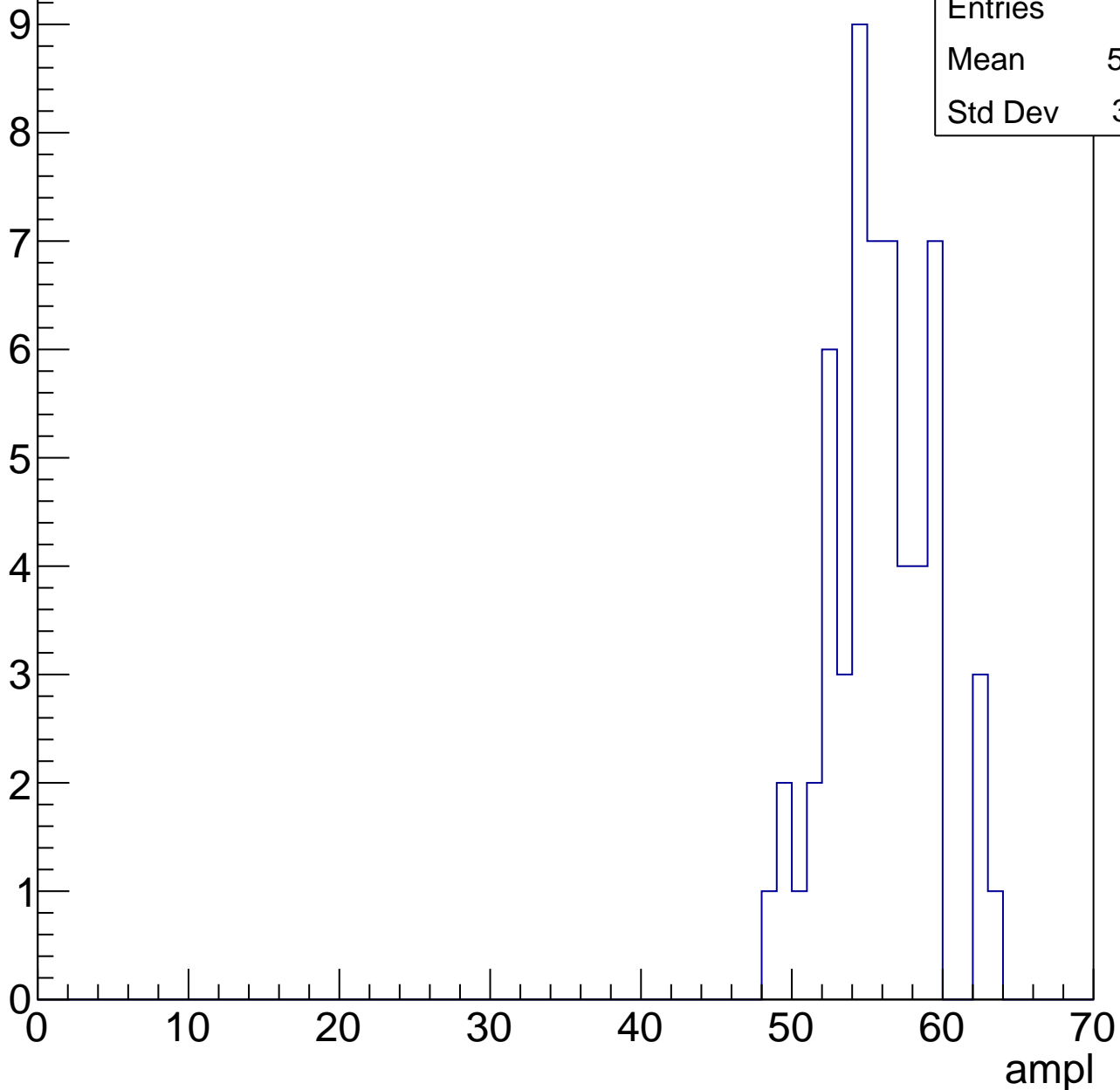


# B0L102S, U6-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	55.33
Std Dev	3.321

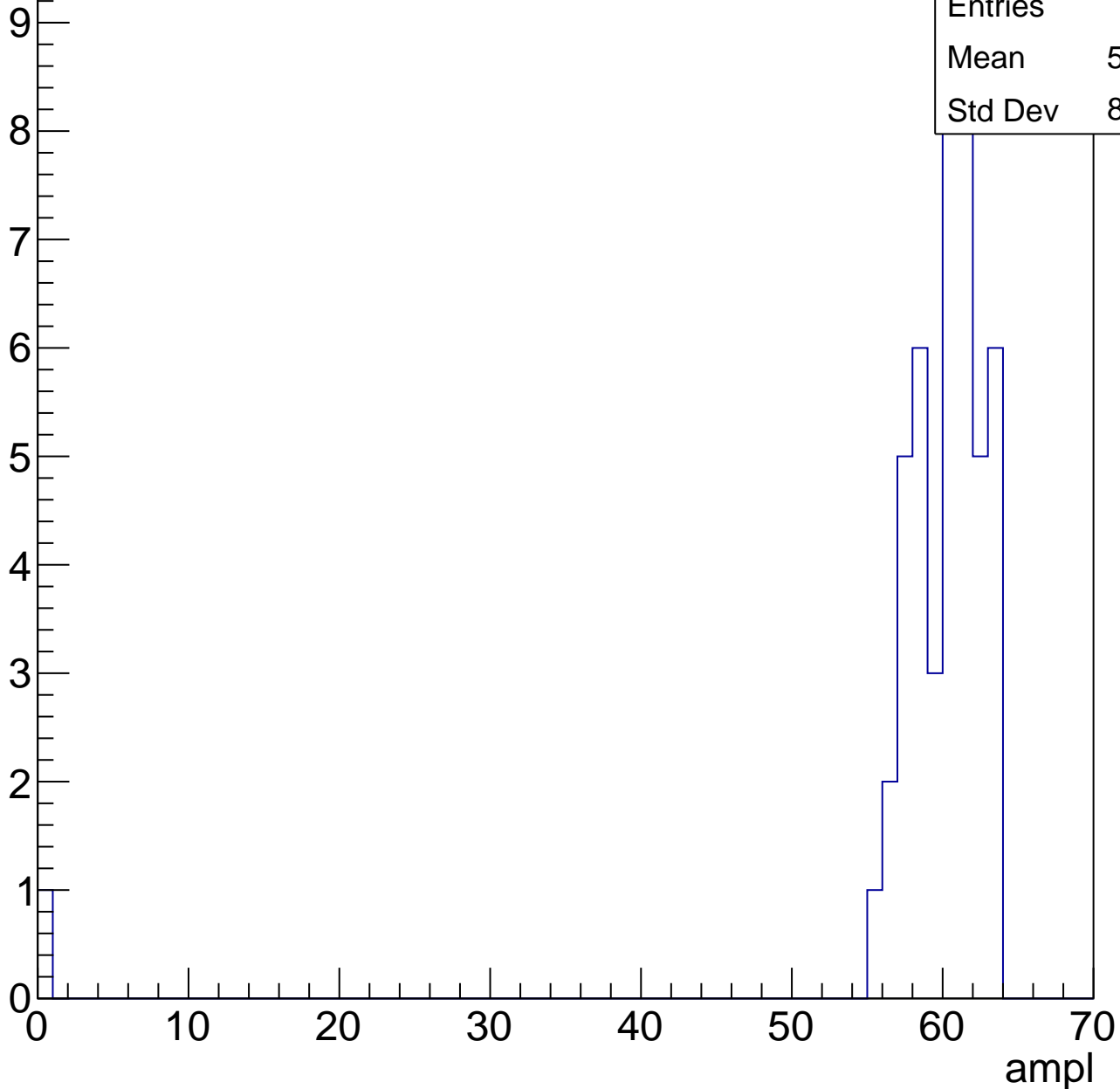


# B0L102S, U6-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

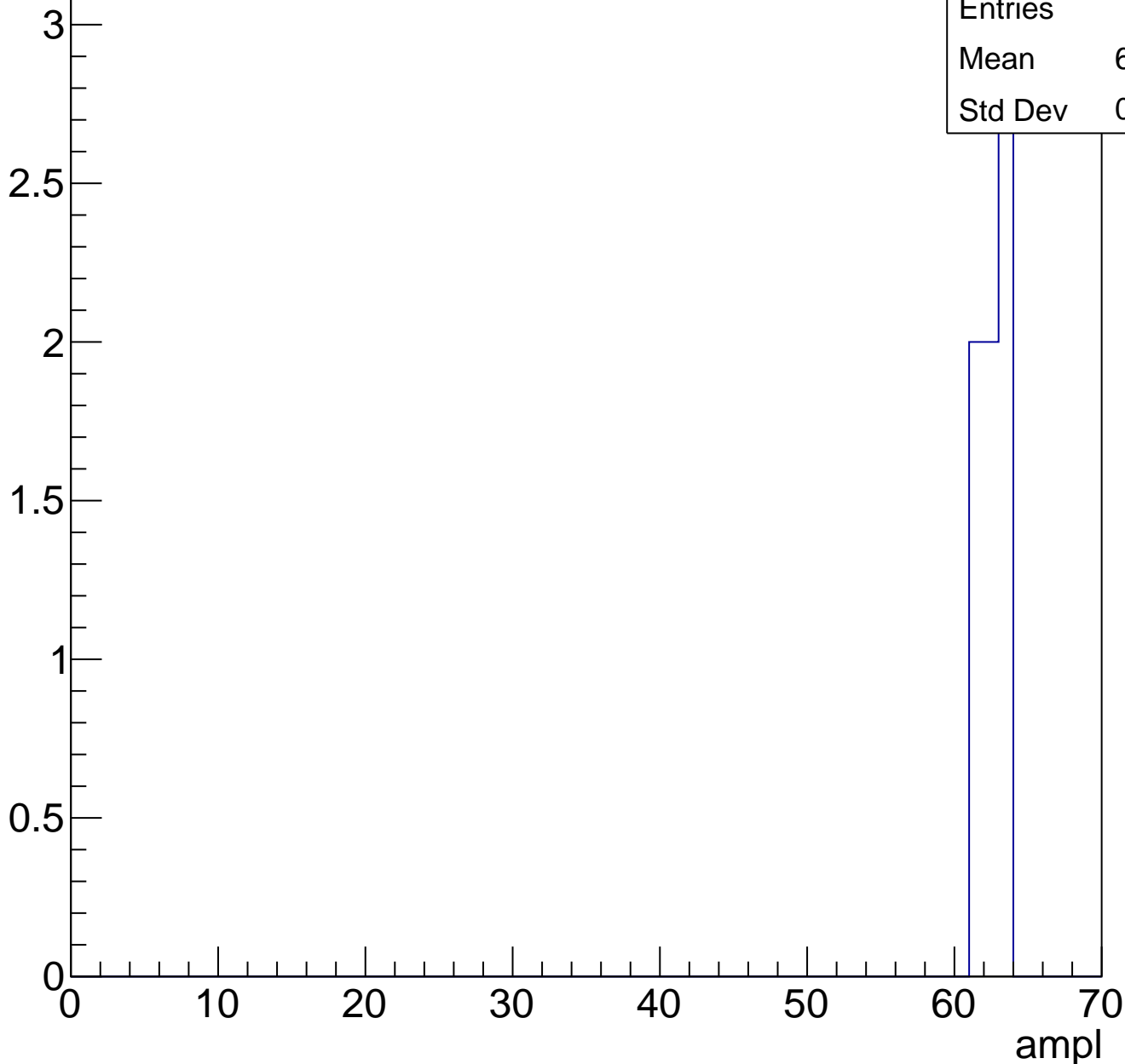
Entries	46
Mean	58.54
Std Dev	8.985



# B0L102S, U6-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	9
Std Dev	9

# B0L102S, U6-ch70, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	26.66
Std Dev	5.578

**Gaus mean : 28.0508**

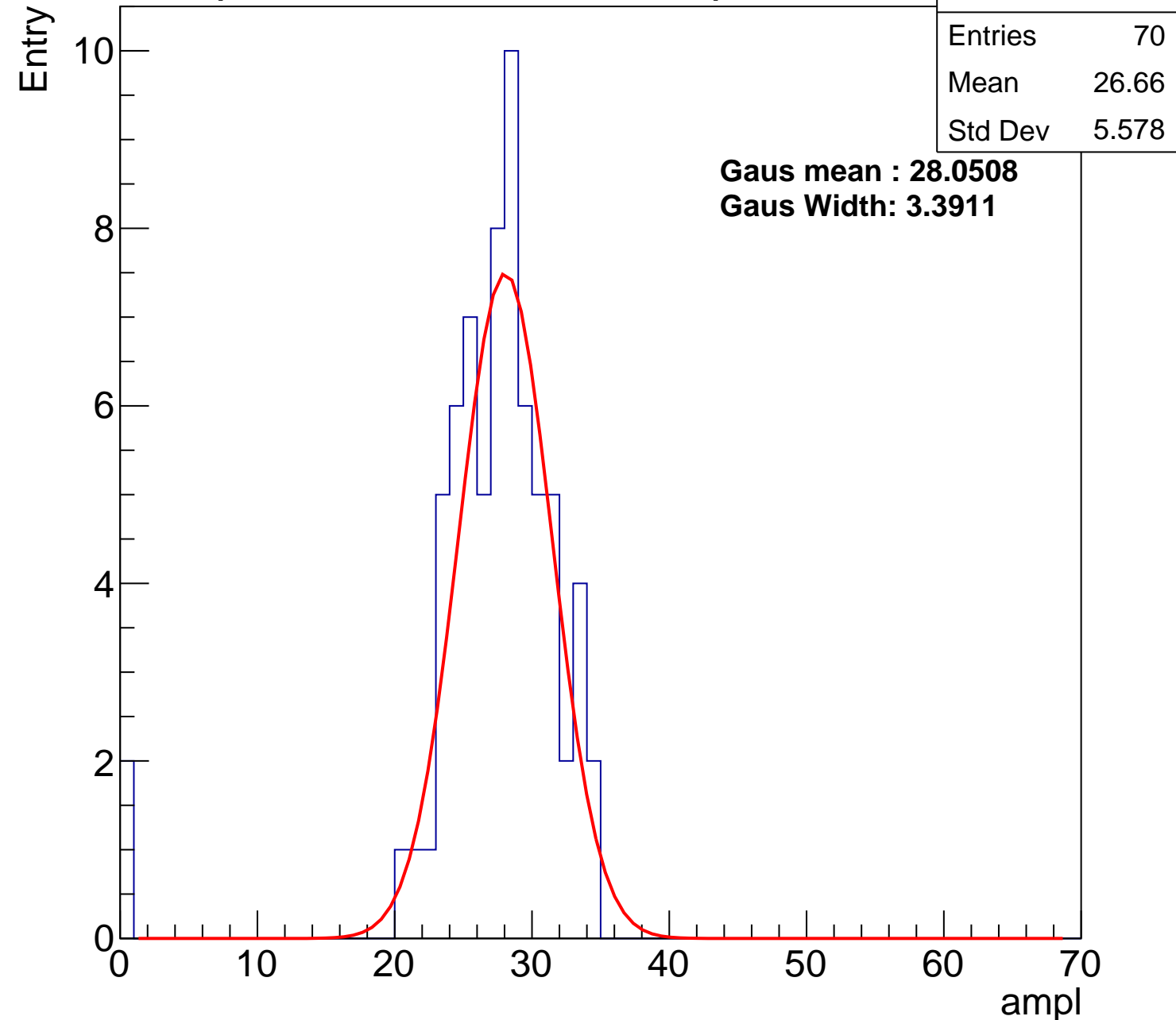
**Gaus Width: 3.3911**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch70, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	88
Mean	35.06
Std Dev	4.16

**Gaus mean : 35.1061**

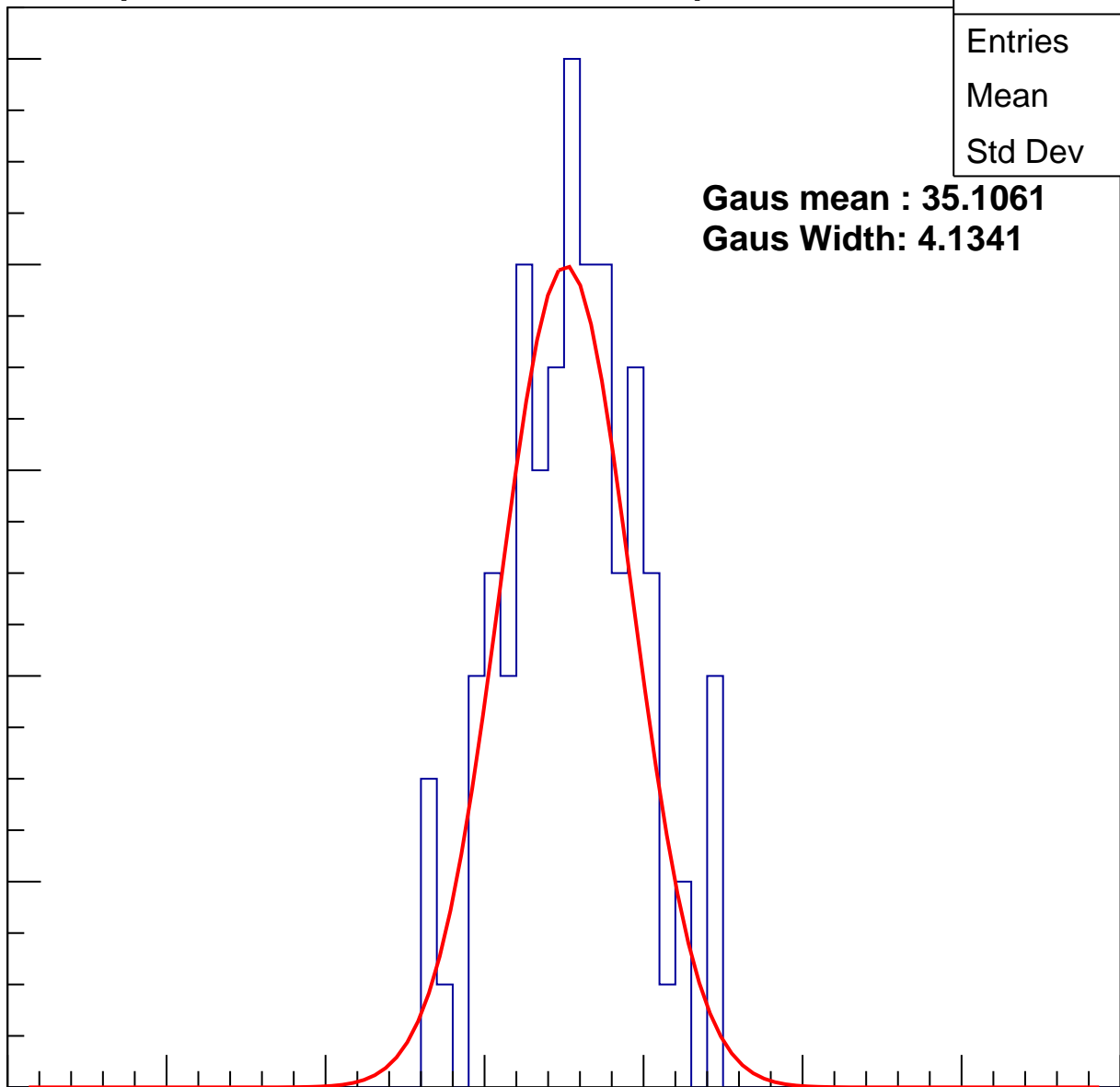
**Gaus Width: 4.1341**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch70, adc2

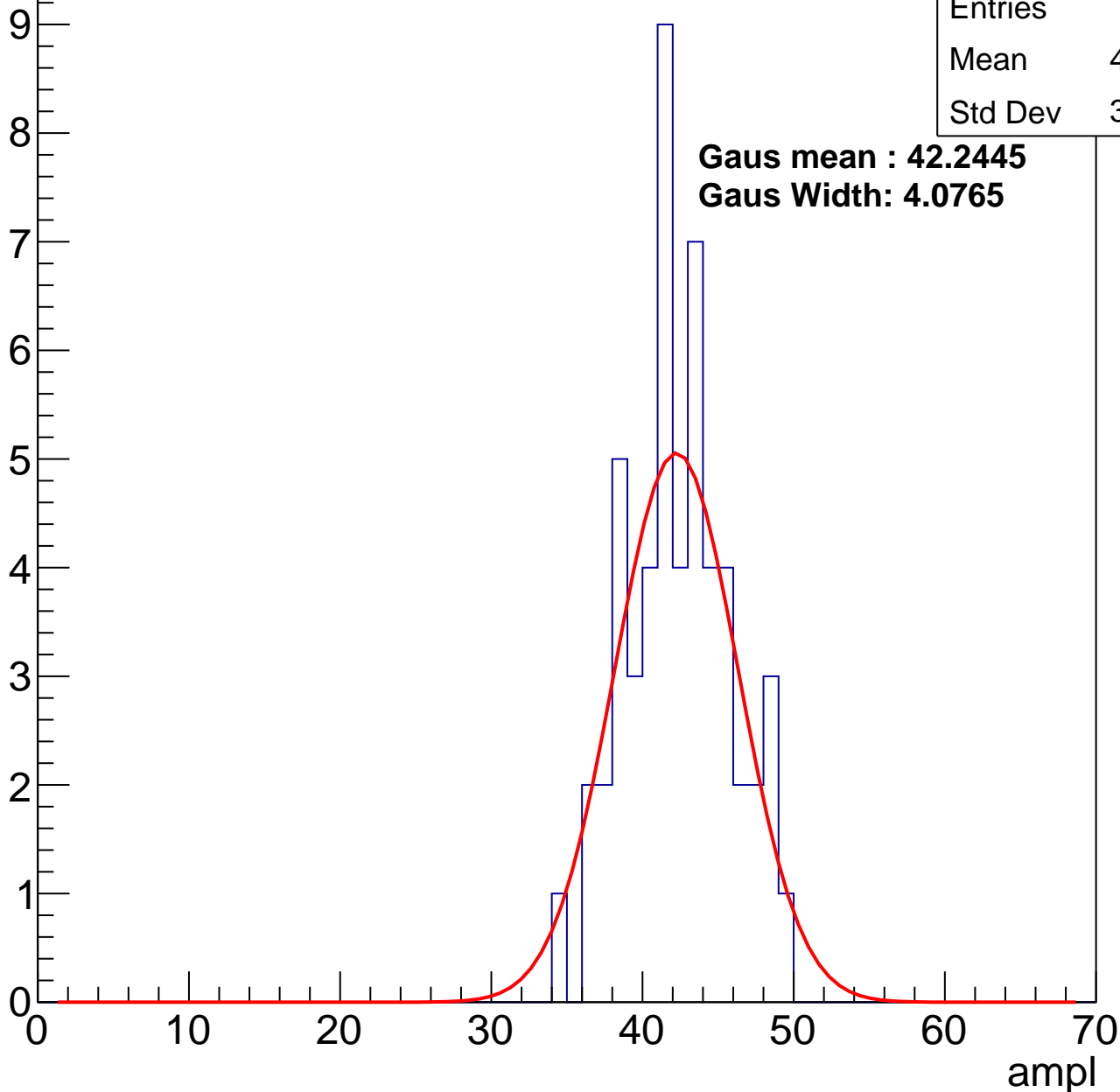
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	41.89
Std Dev	3.407

**Gaus mean : 42.2445**

**Gaus Width: 4.0765**

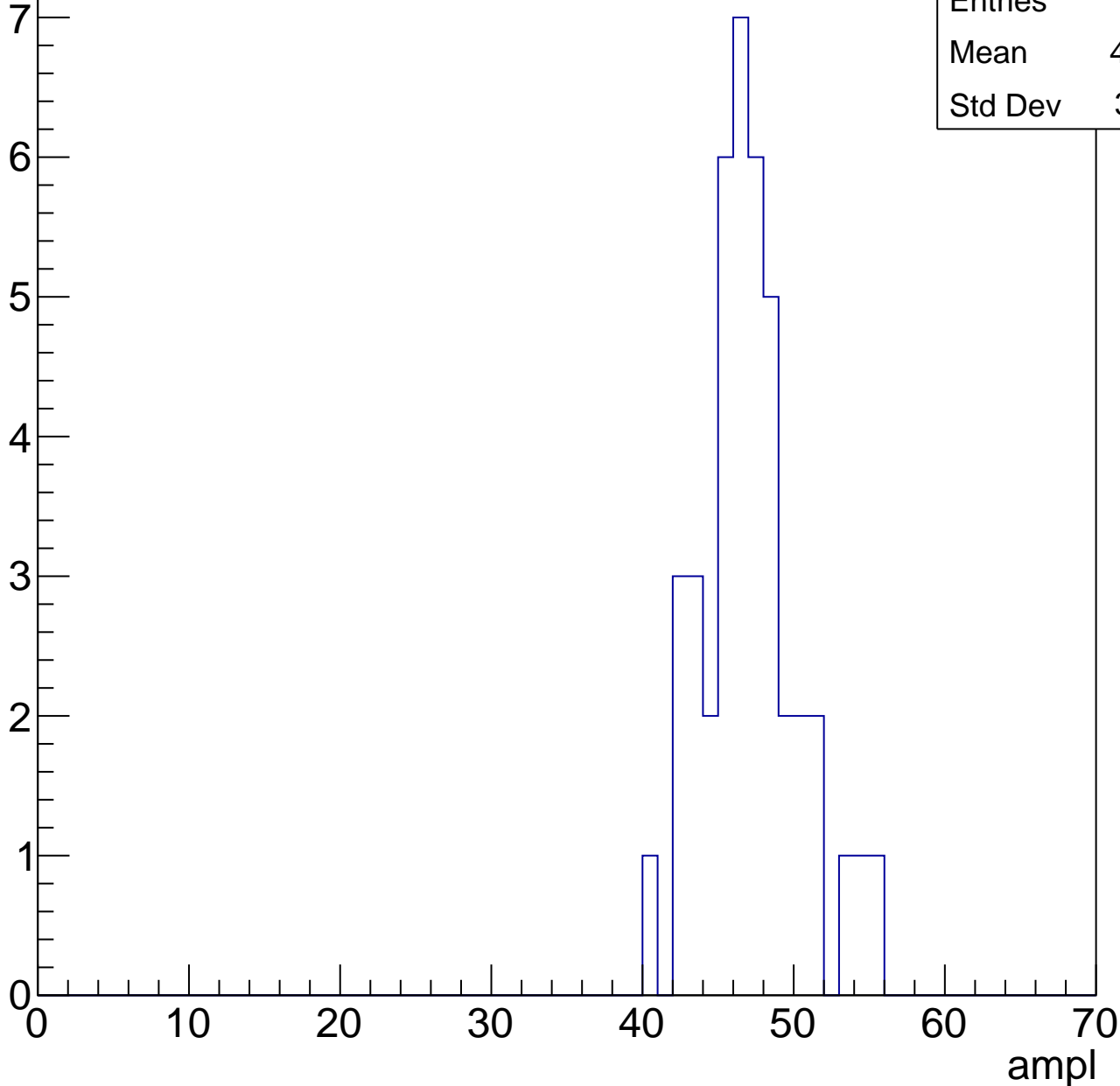


# B0L102S, U6-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	42
Mean	46.64
Std Dev	3.191

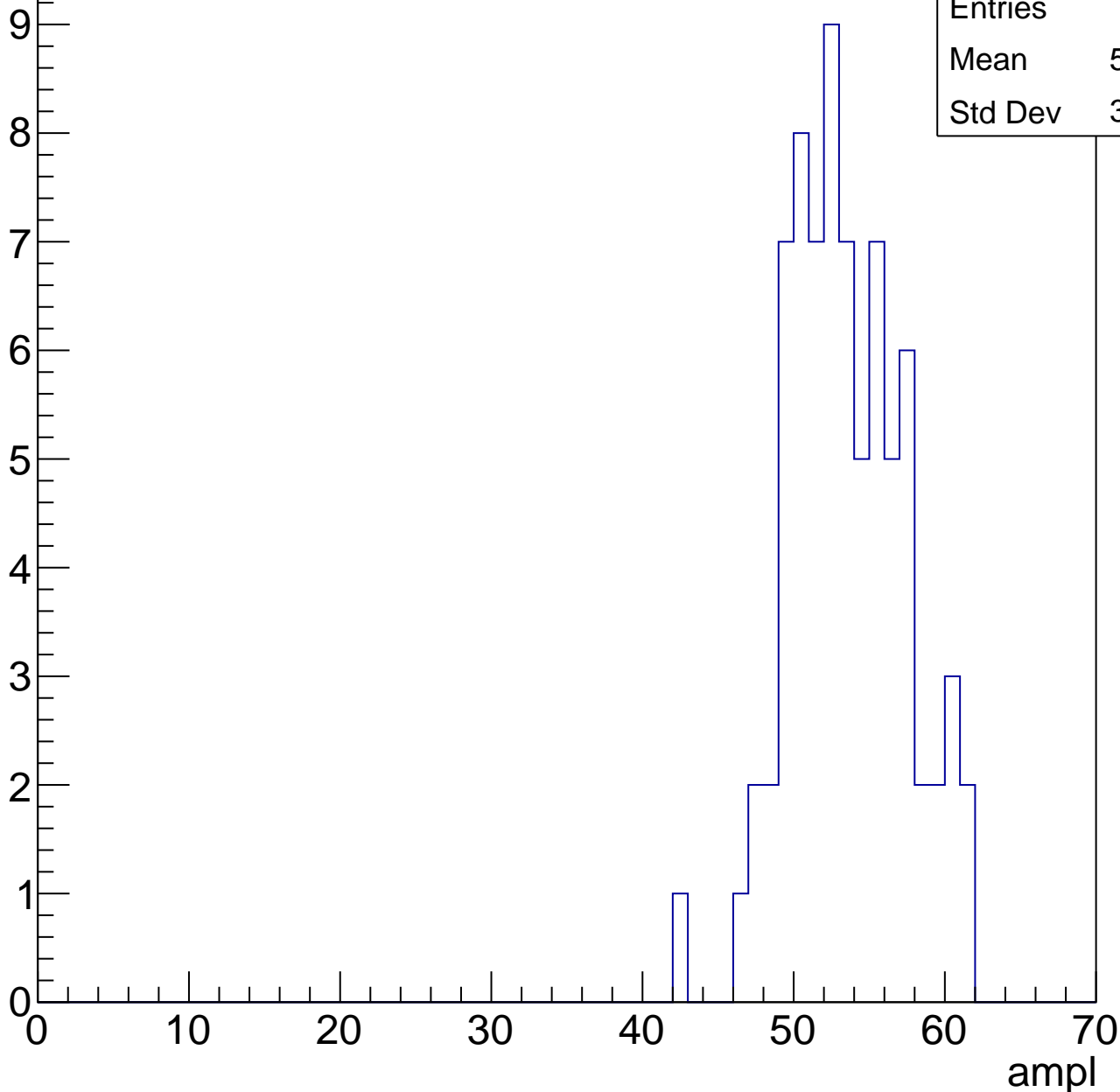


# B0L102S, U6-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

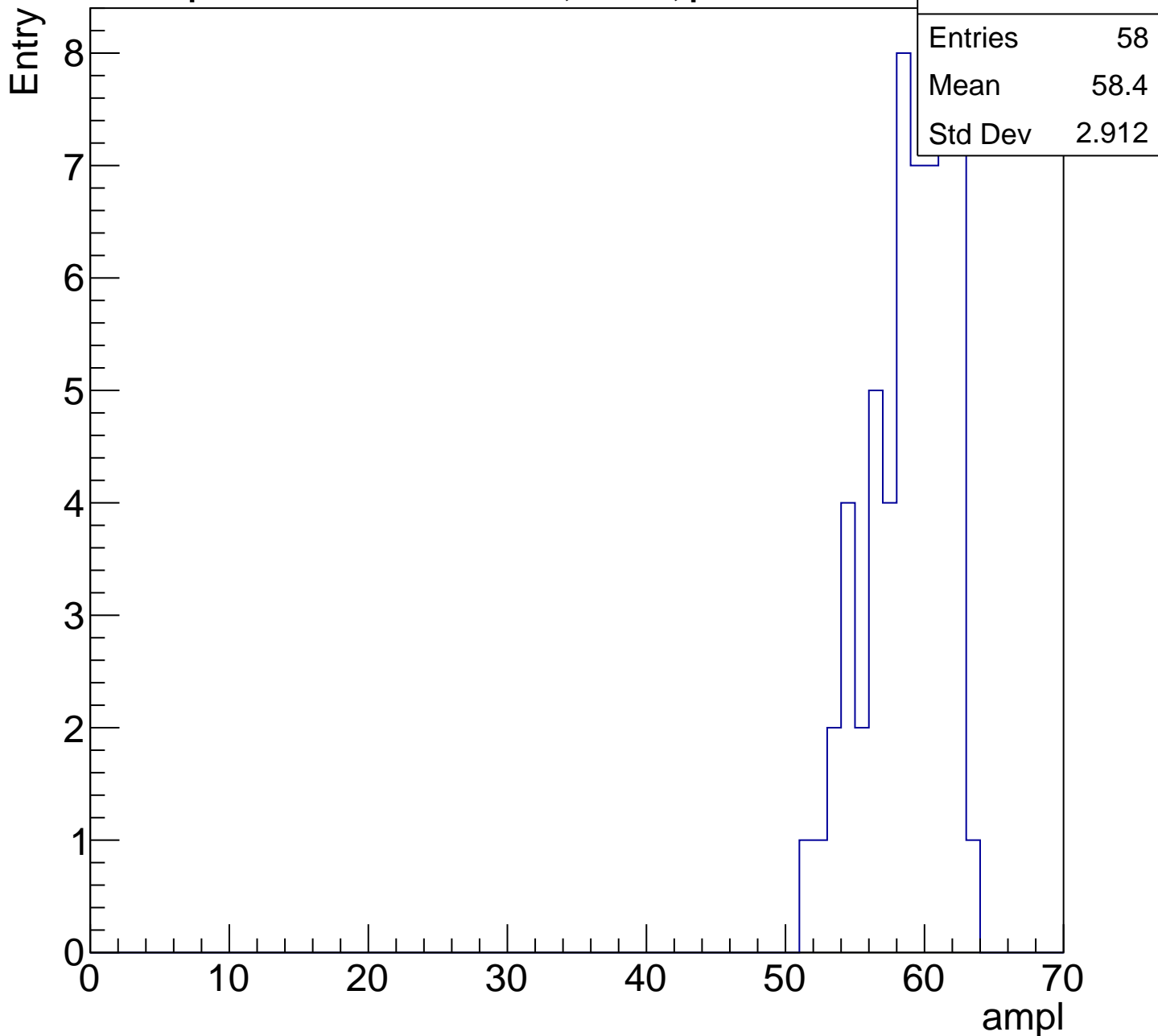
Entry

Entries	76
Mean	53.03
Std Dev	3.777



# B0L102S, U6-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

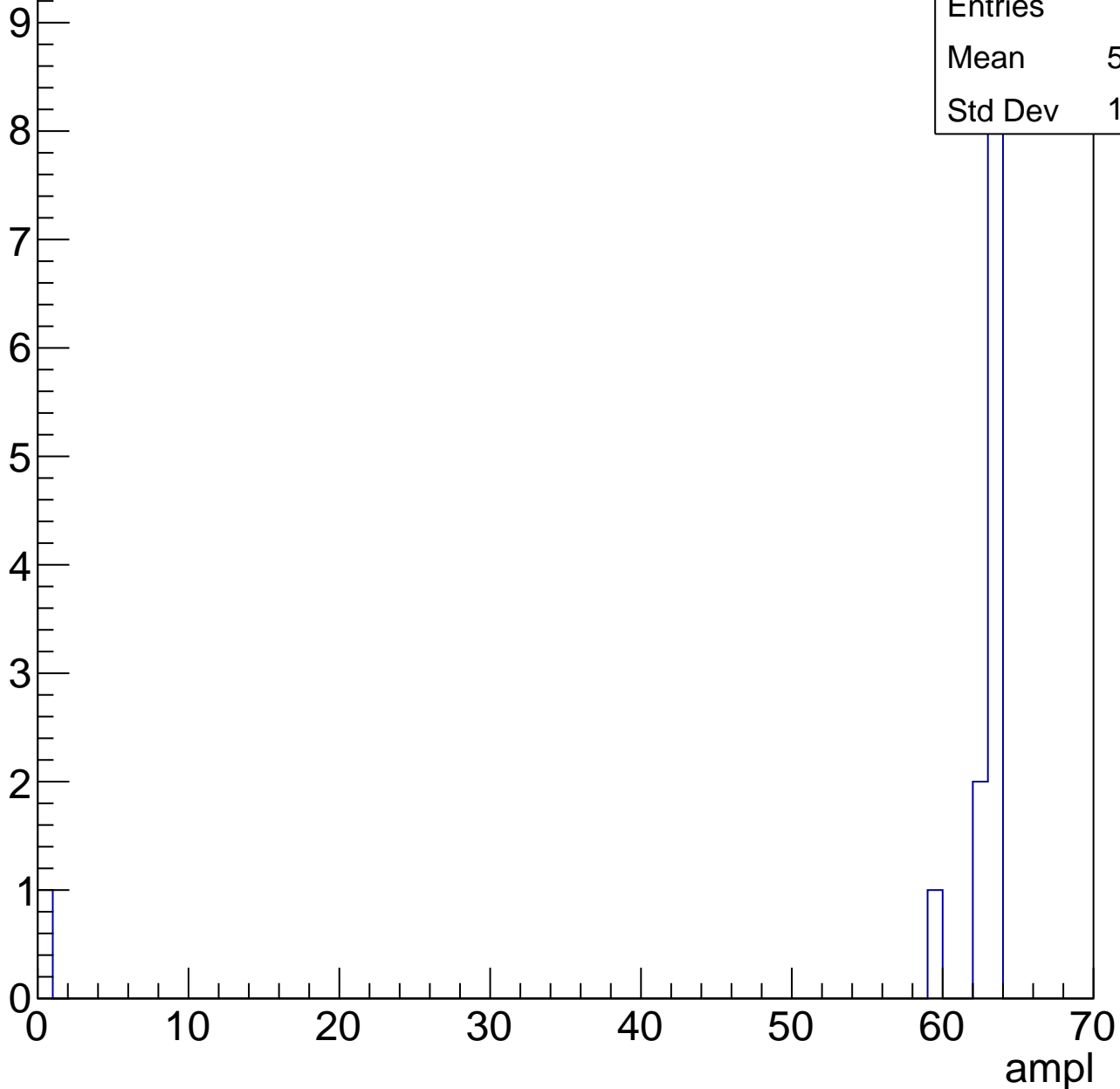


# B0L102S, U6-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	57.69
Std Dev	16.69





# B0L102S, U6-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch71, adc0

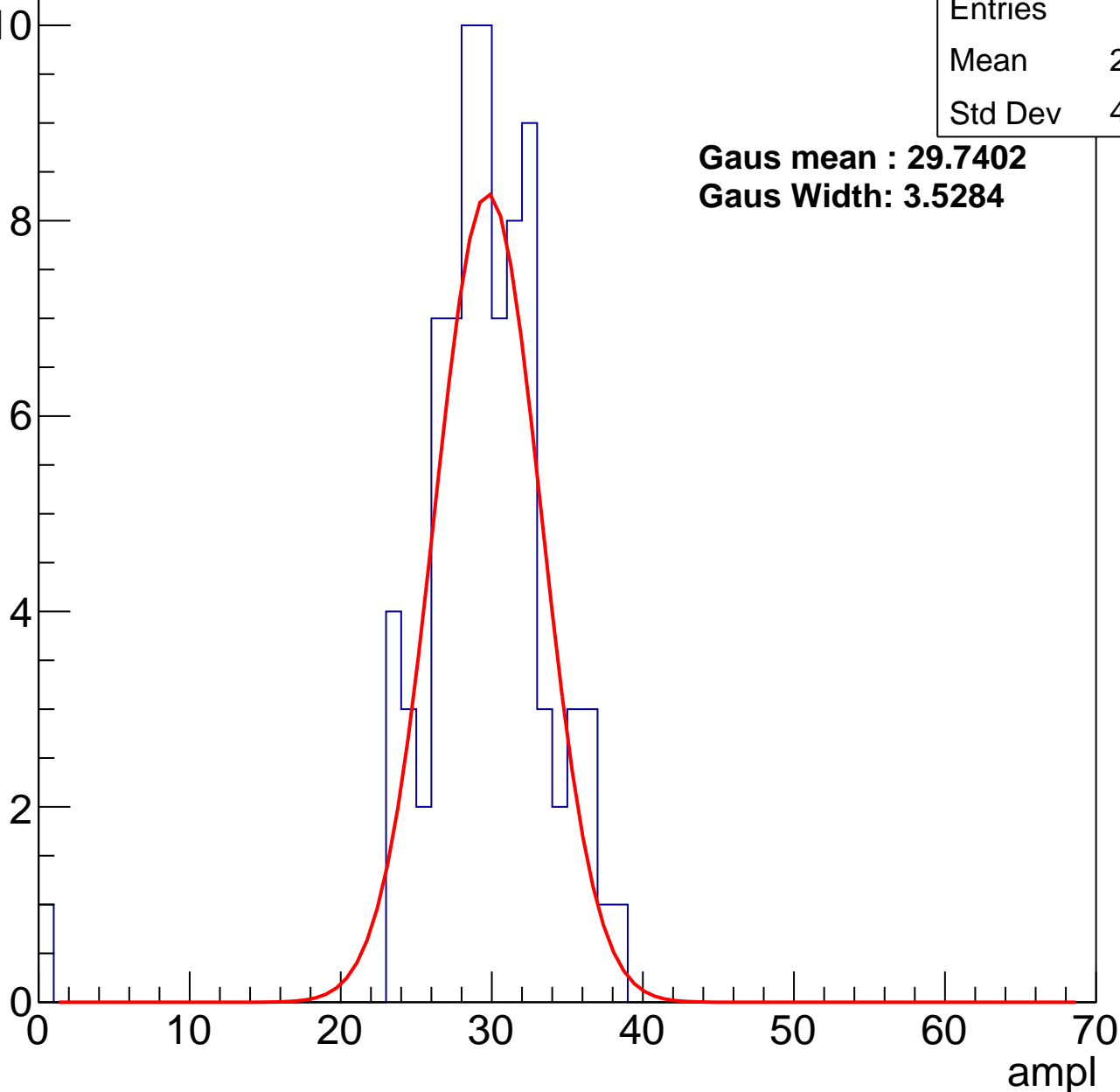
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	29.09
Std Dev	4.728

**Gaus mean : 29.7402**

**Gaus Width: 3.5284**



# B0L102S, U6-ch71, adc1

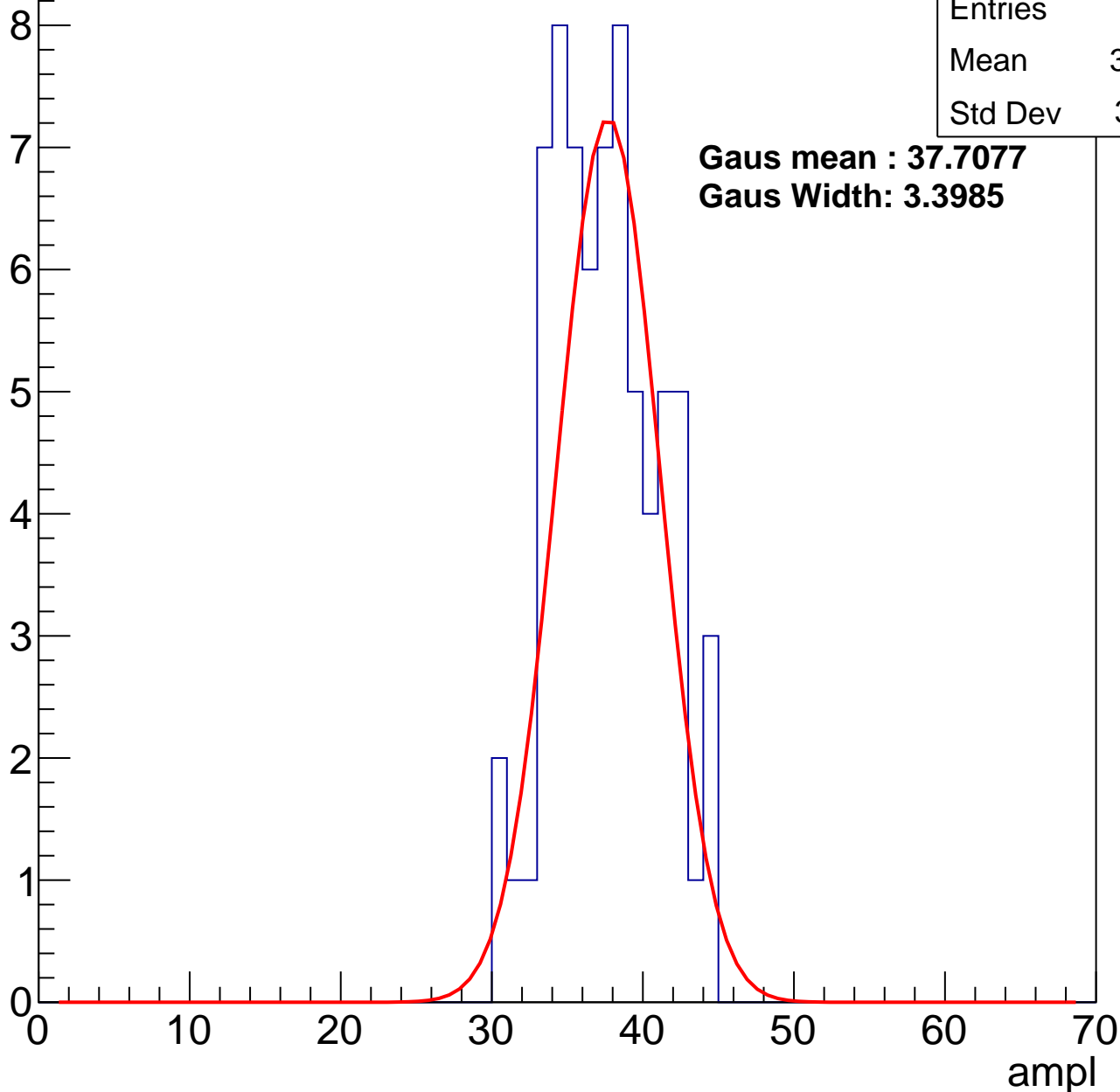
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	37.07
Std Dev	3.441

**Gaus mean : 37.7077**

**Gaus Width: 3.3985**



# B0L102S, U6-ch71, adc2

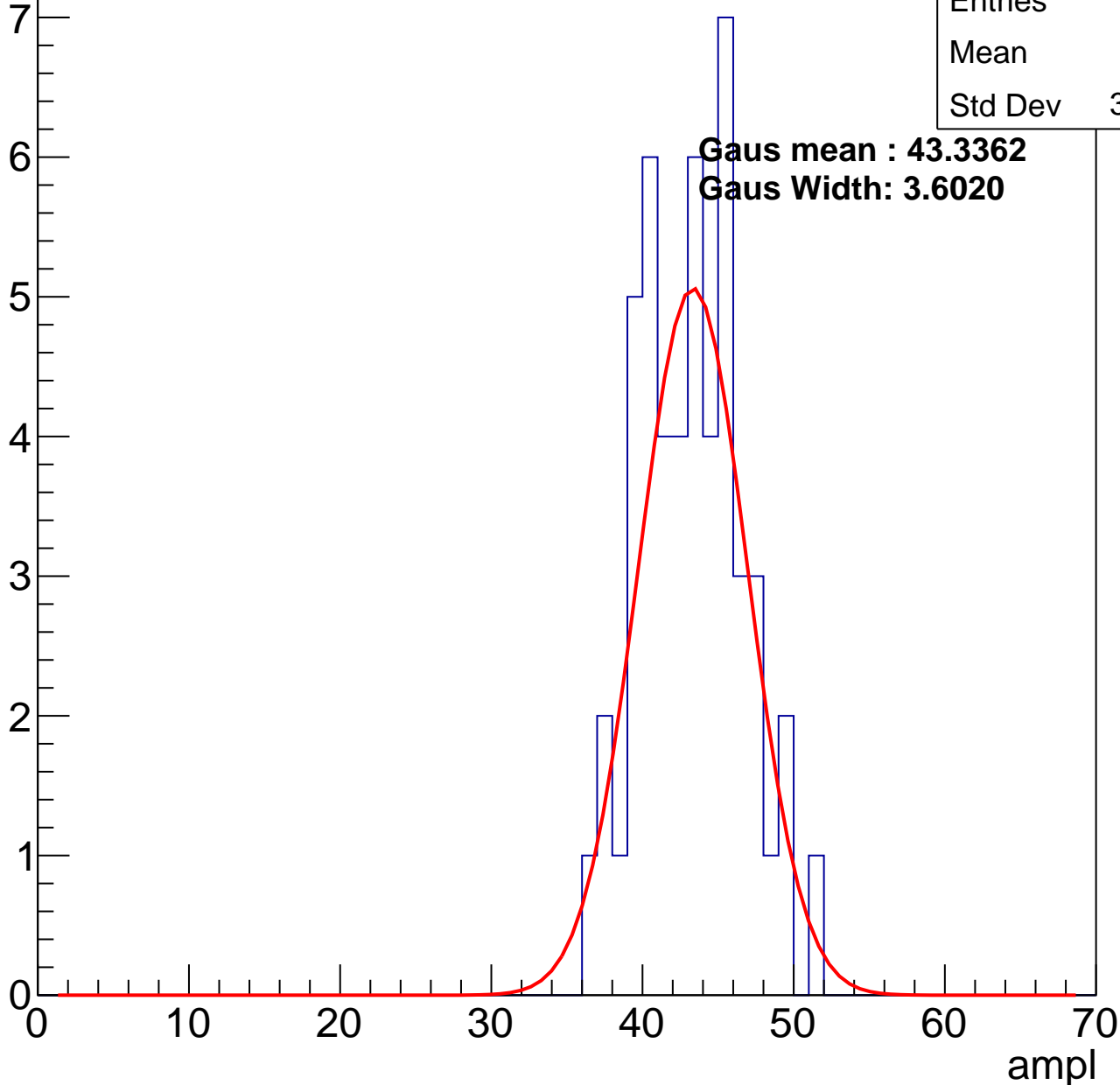
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	42.8
Std Dev	3.365

**Gaus mean : 43.3362**

**Gaus Width: 3.6020**

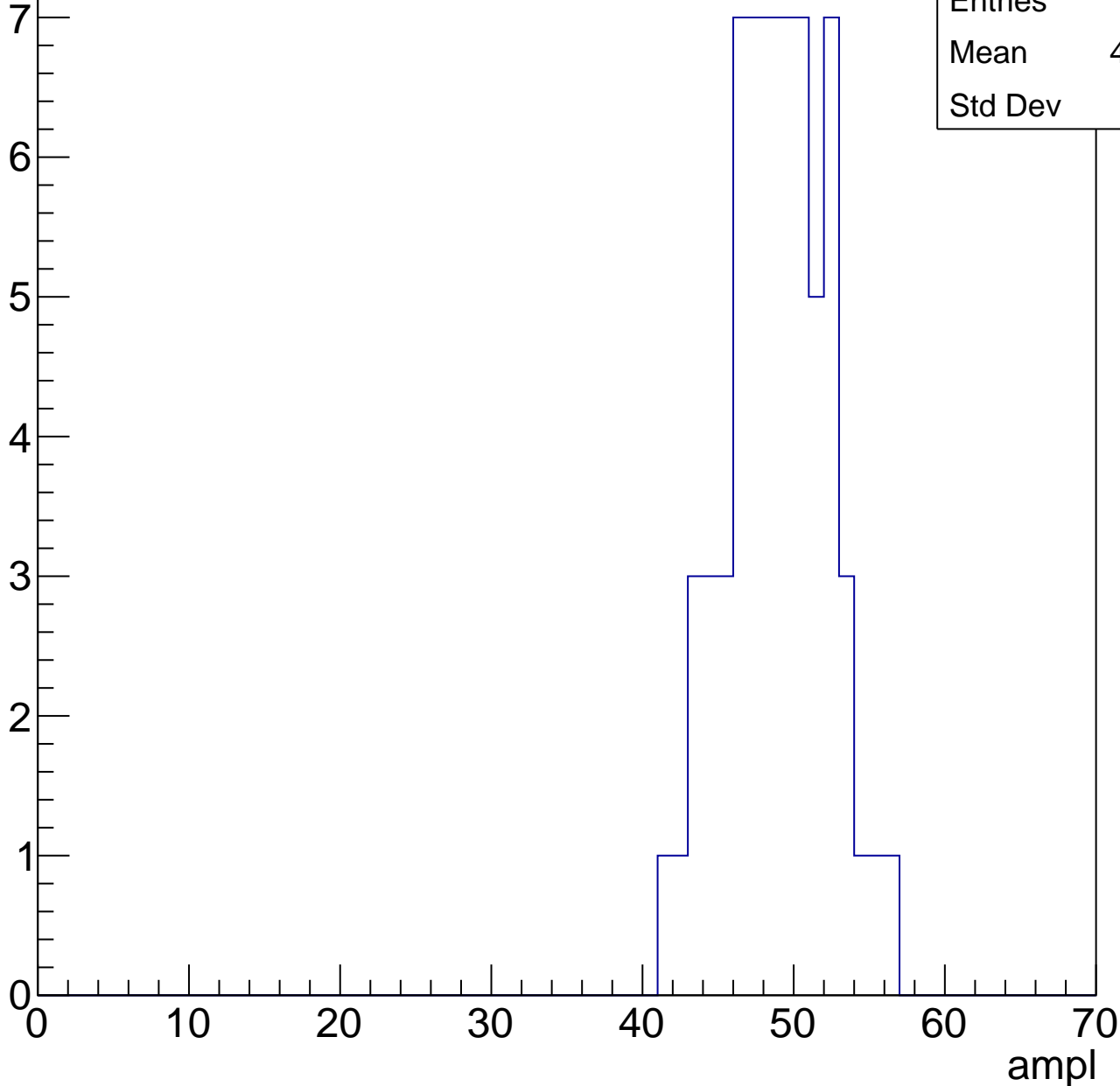


# B0L102S, U6-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

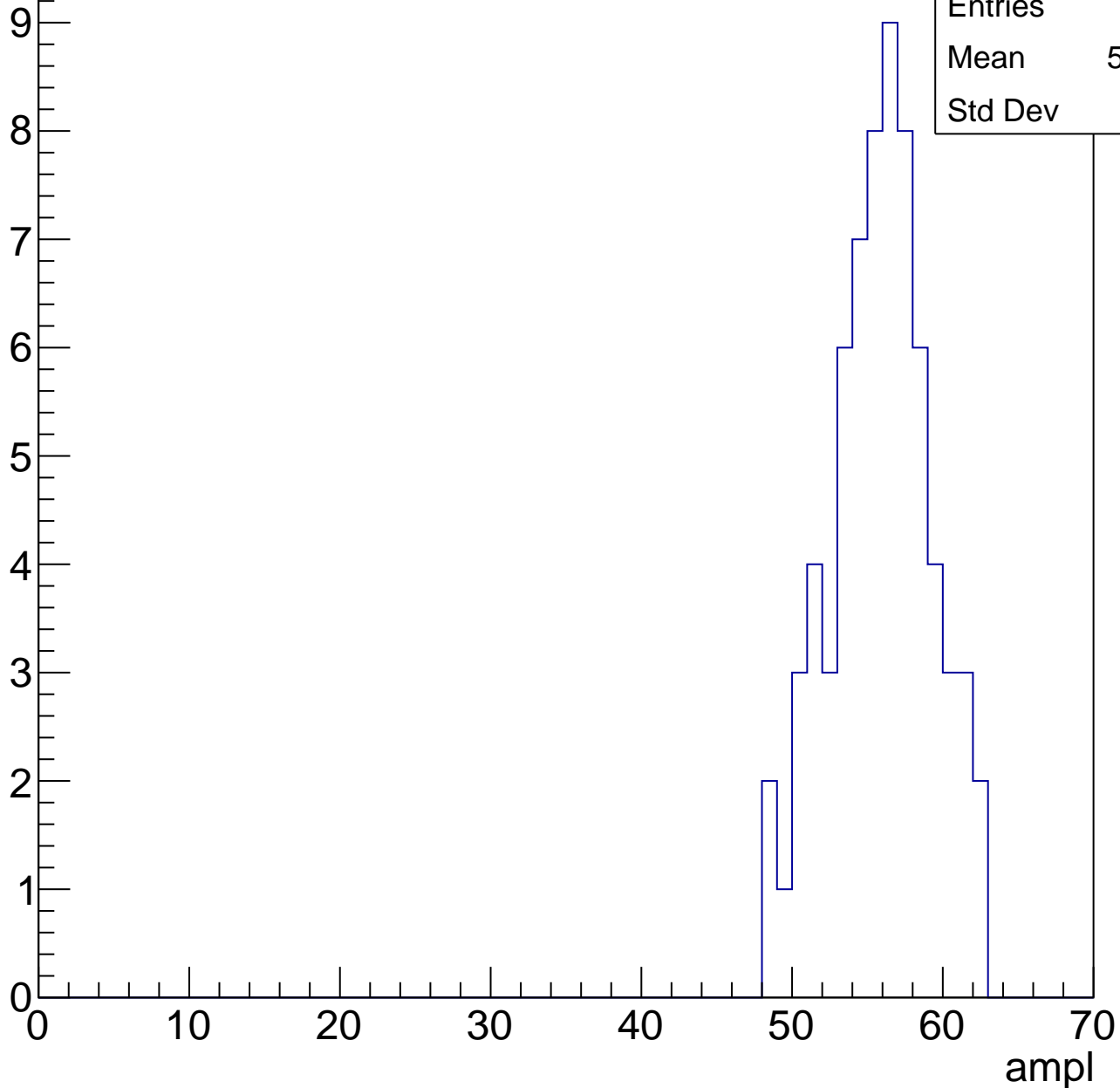
Entries	64
Mean	48.47
Std Dev	3.24



# B0L102S, U6-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



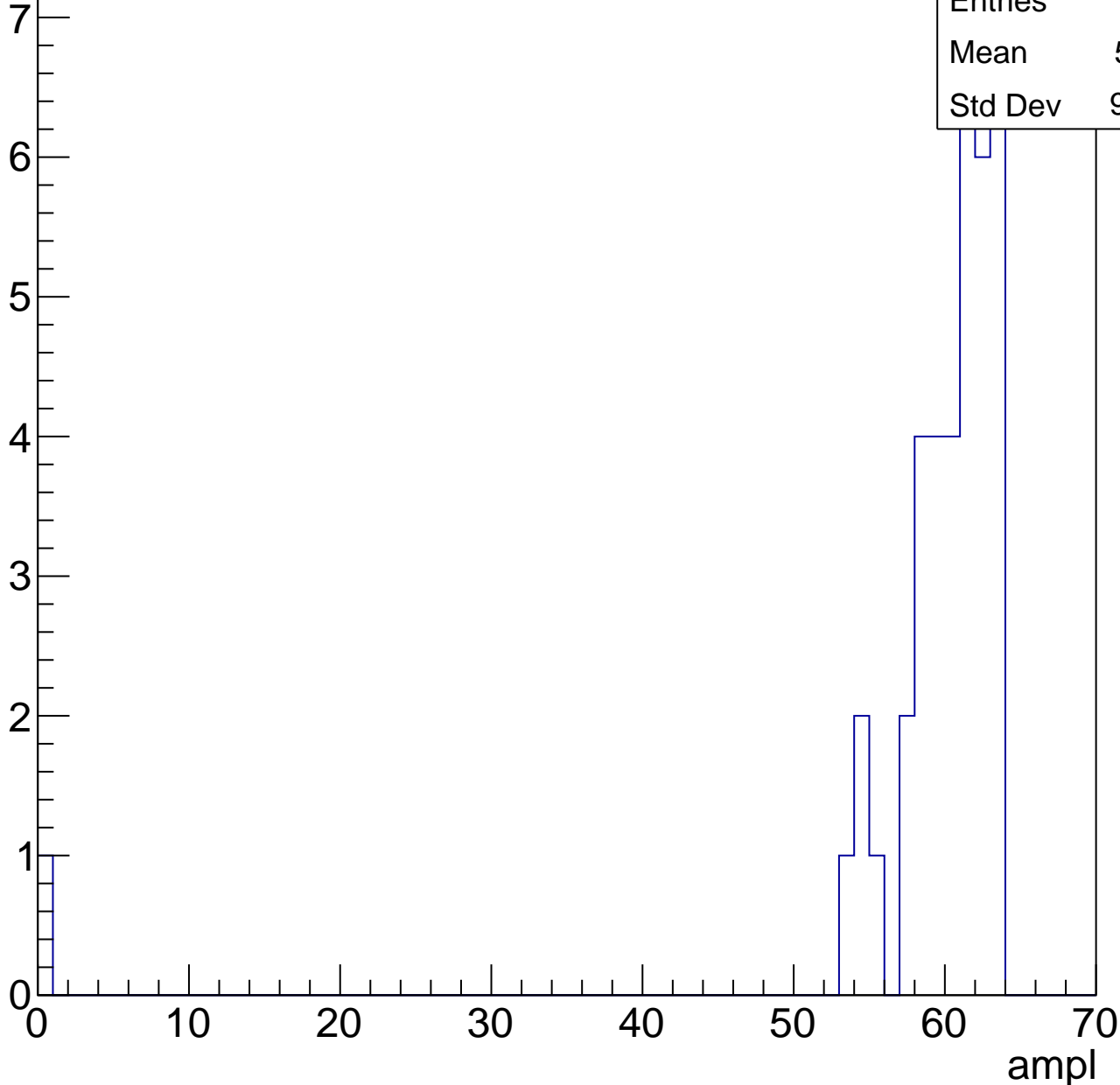
Entries	69
Mean	55.39
Std Dev	3.32

# B0L102S, U6-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	39
Mean	58.41
Std Dev	9.844



# B0L102S, U6-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries	10
Mean	61.1
Std Dev	1.578



# B0L102S, U6-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch72, adc0

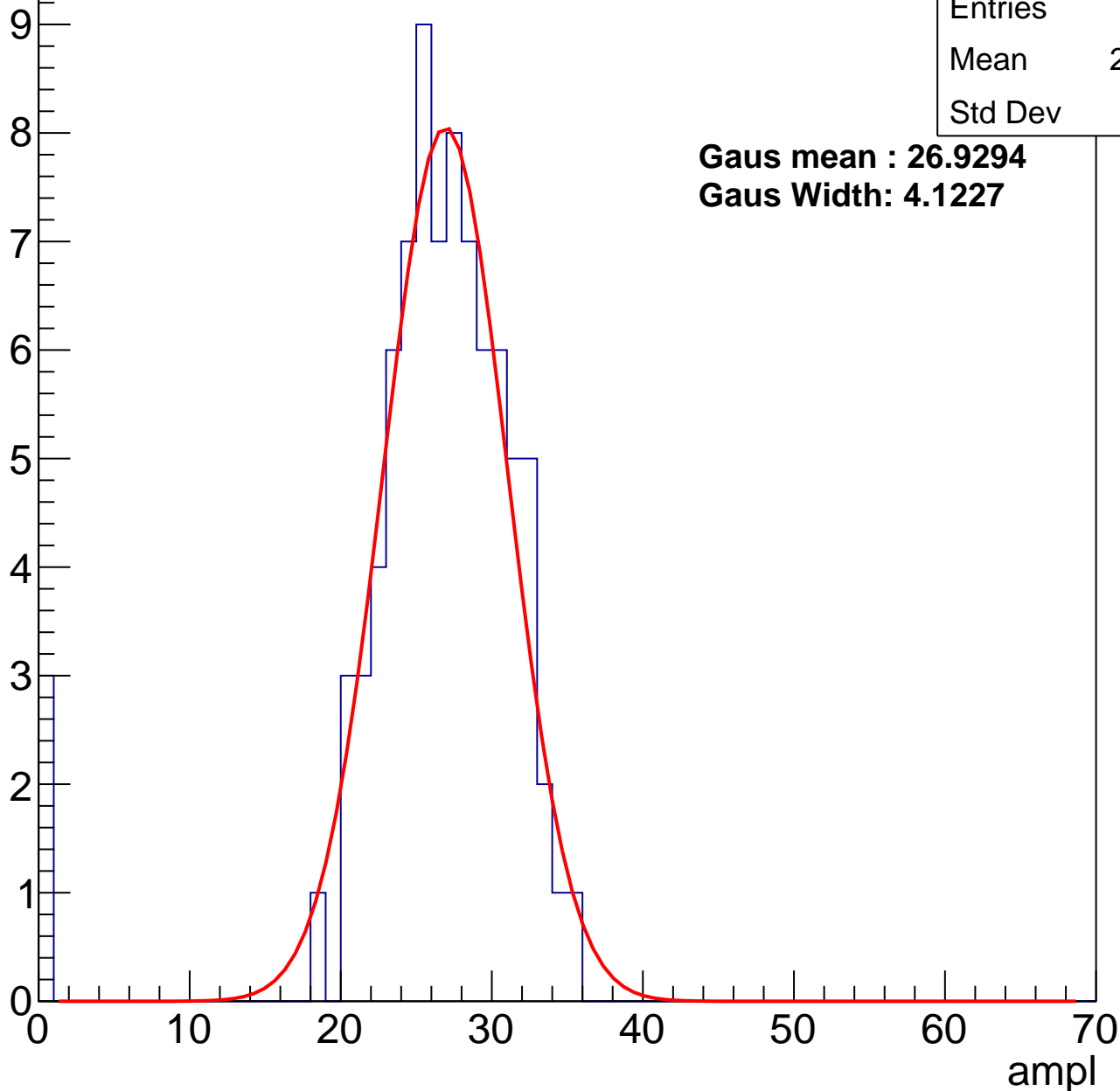
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	25.69
Std Dev	6.13

**Gaus mean : 26.9294**

**Gaus Width: 4.1227**



# B0L102S, U6-ch72, adc1

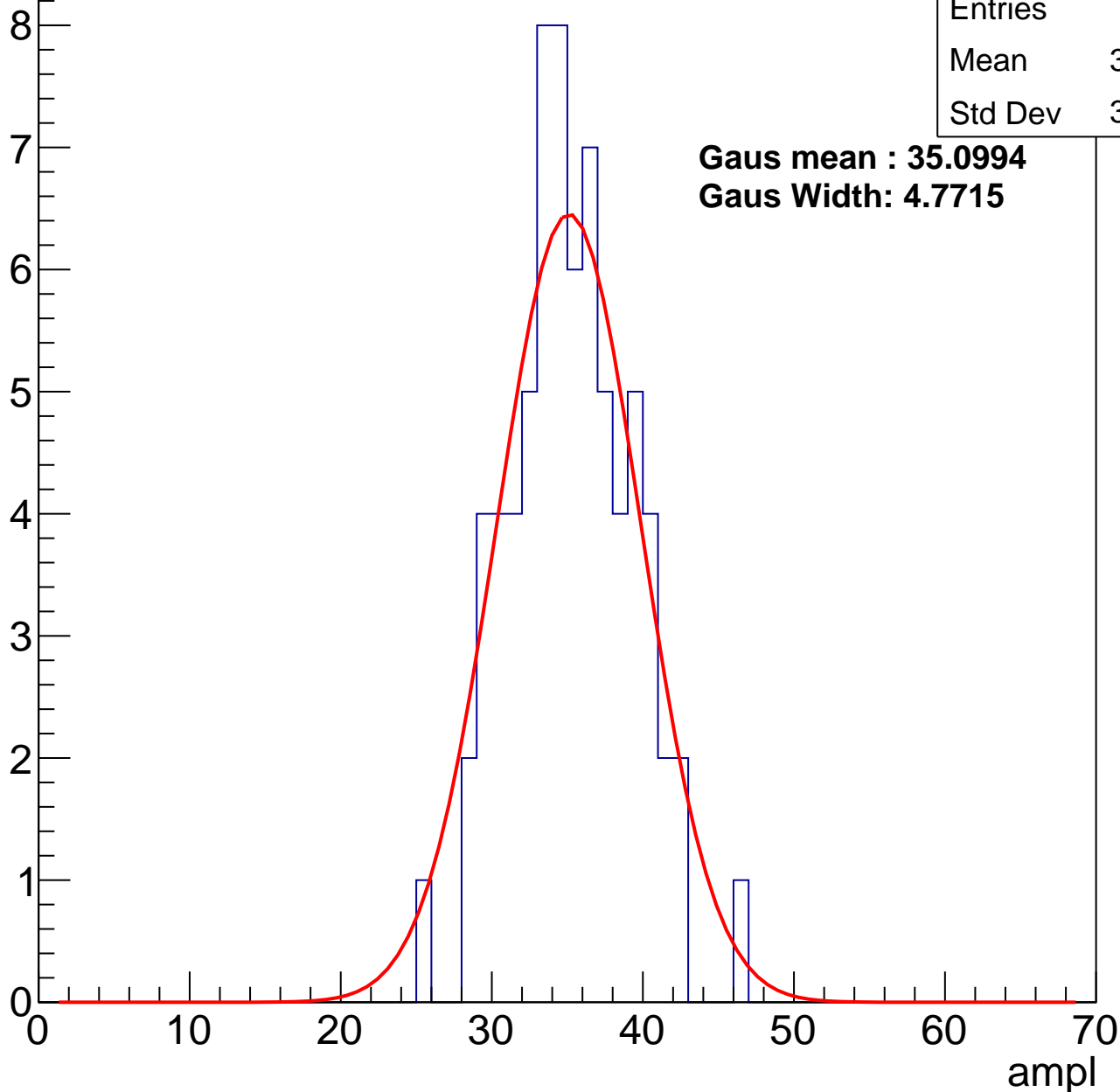
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	34.76
Std Dev	3.946

**Gaus mean : 35.0994**

**Gaus Width: 4.7715**



# B0L102S, U6-ch72, adc2

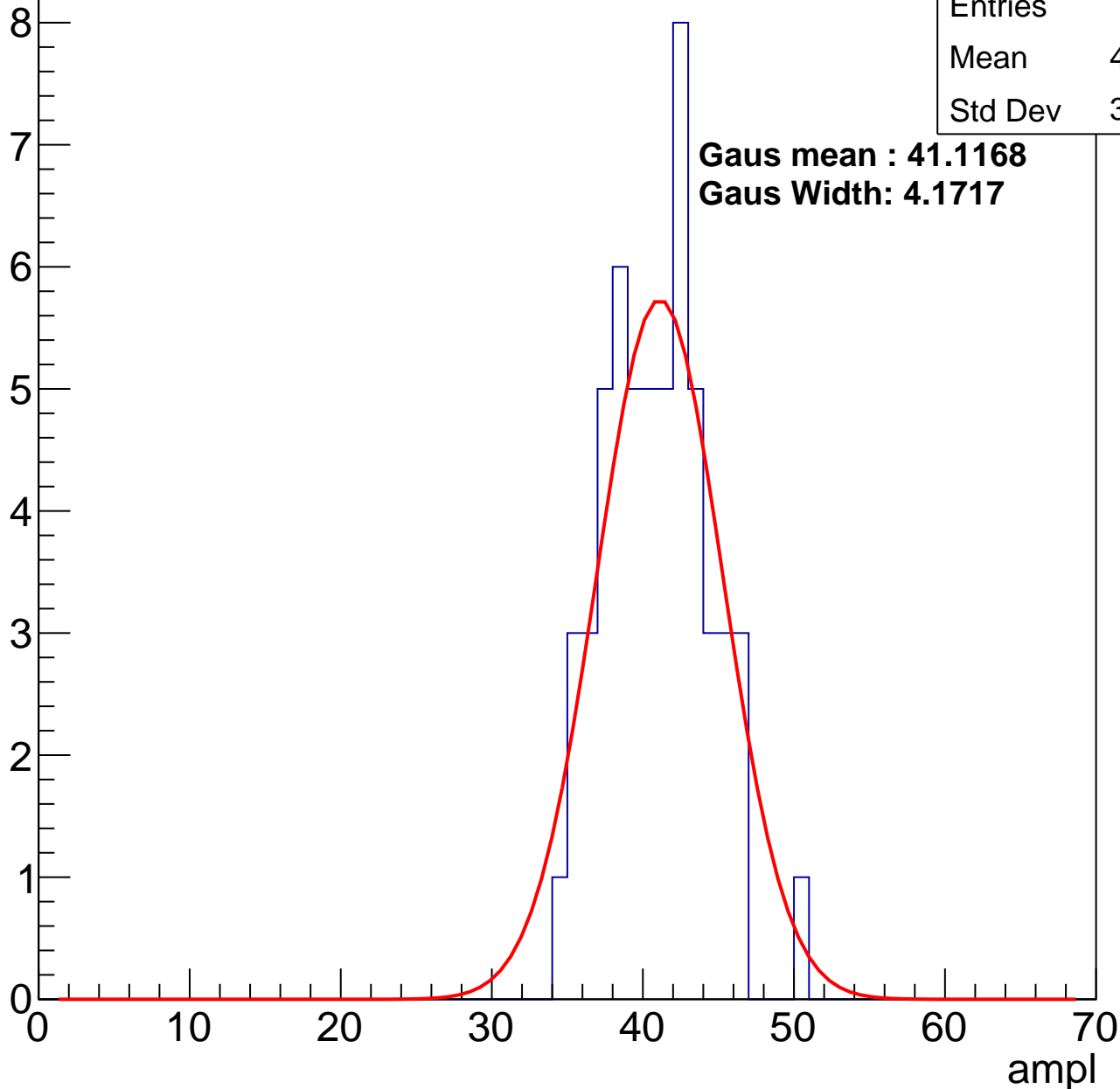
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	40.46
Std Dev	3.365

**Gaus mean : 41.1168**

**Gaus Width: 4.1717**

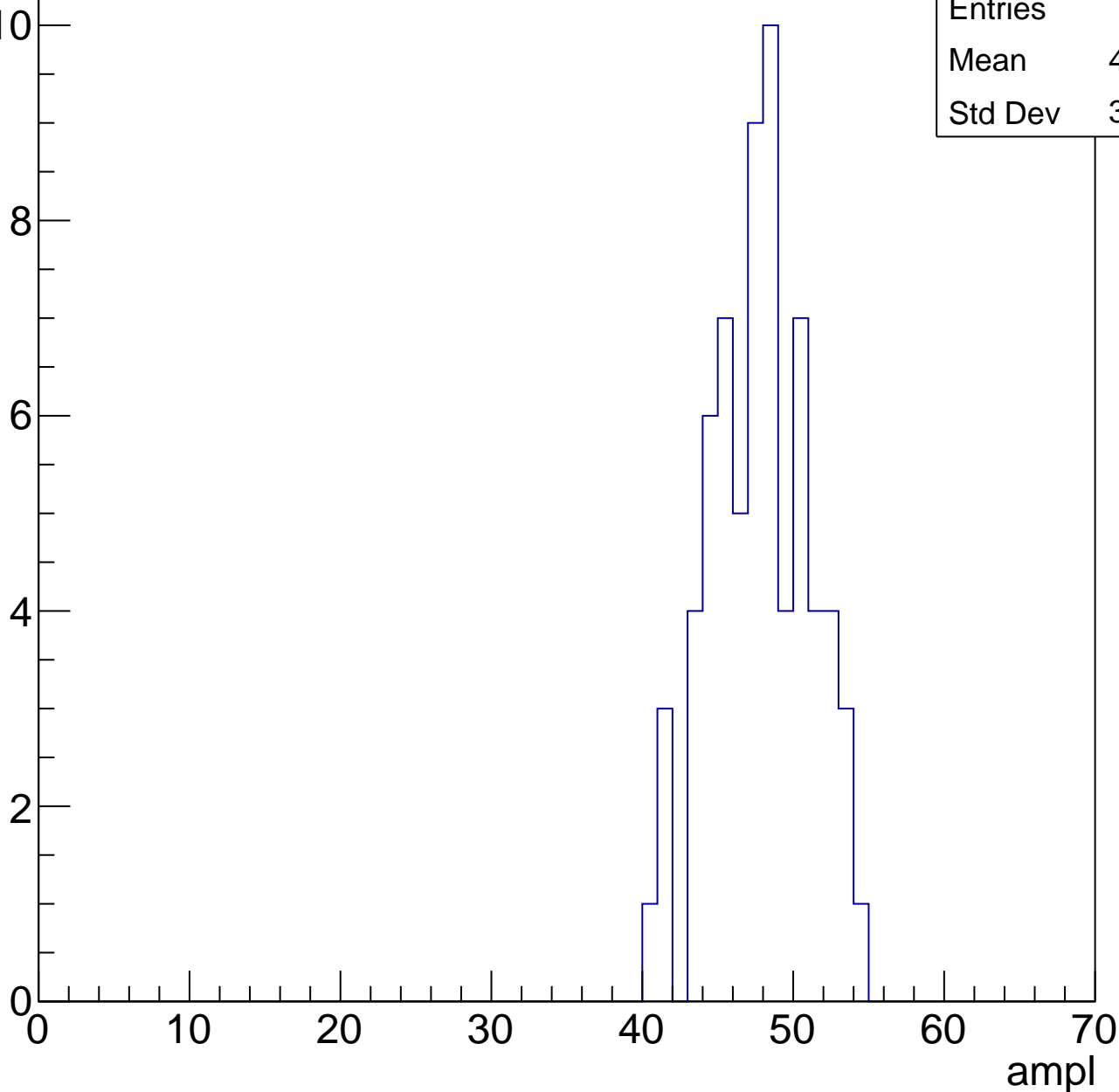


# B0L102S, U6-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	47.32
Std Dev	3.233

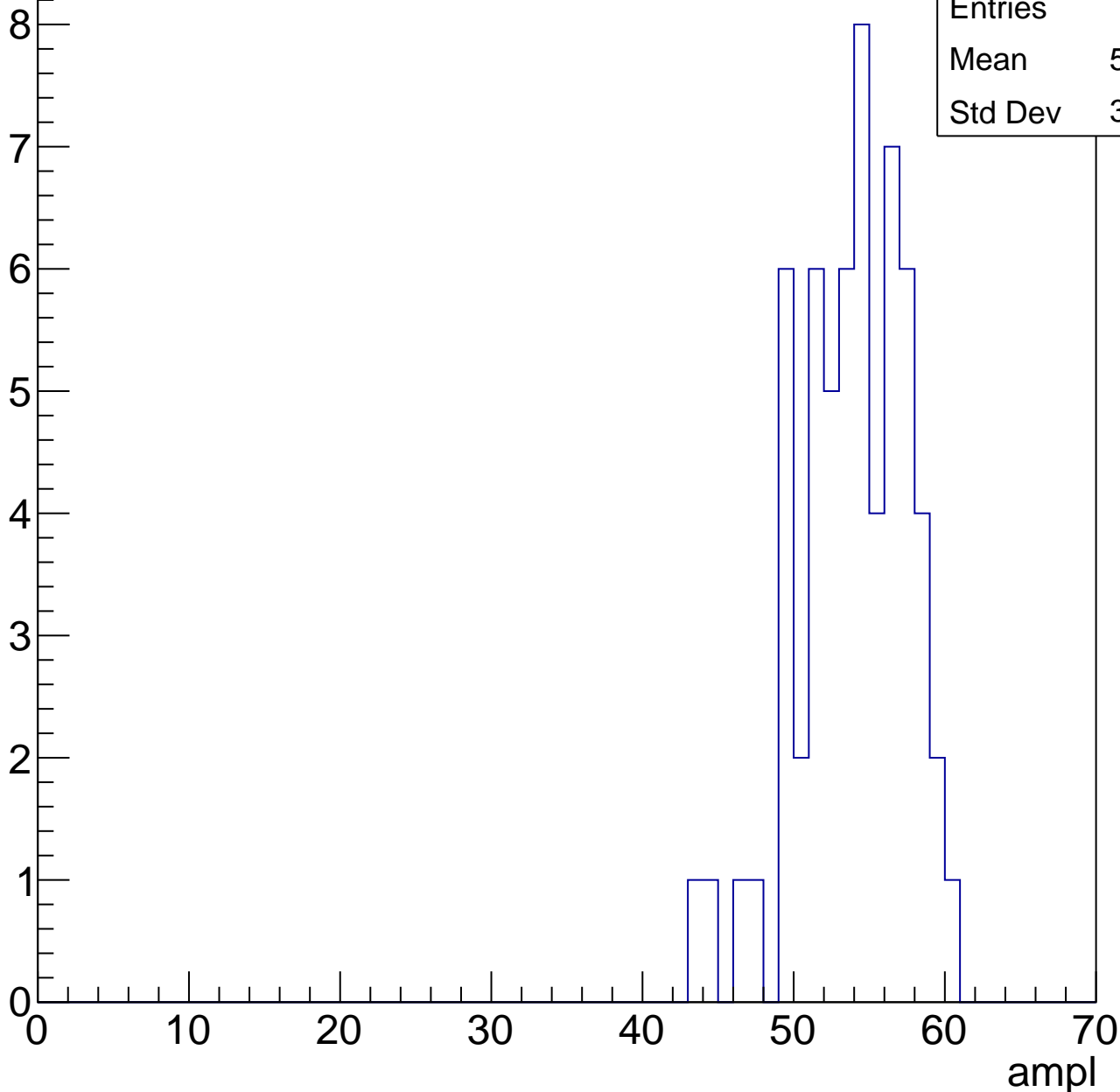


# B0L102S, U6-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

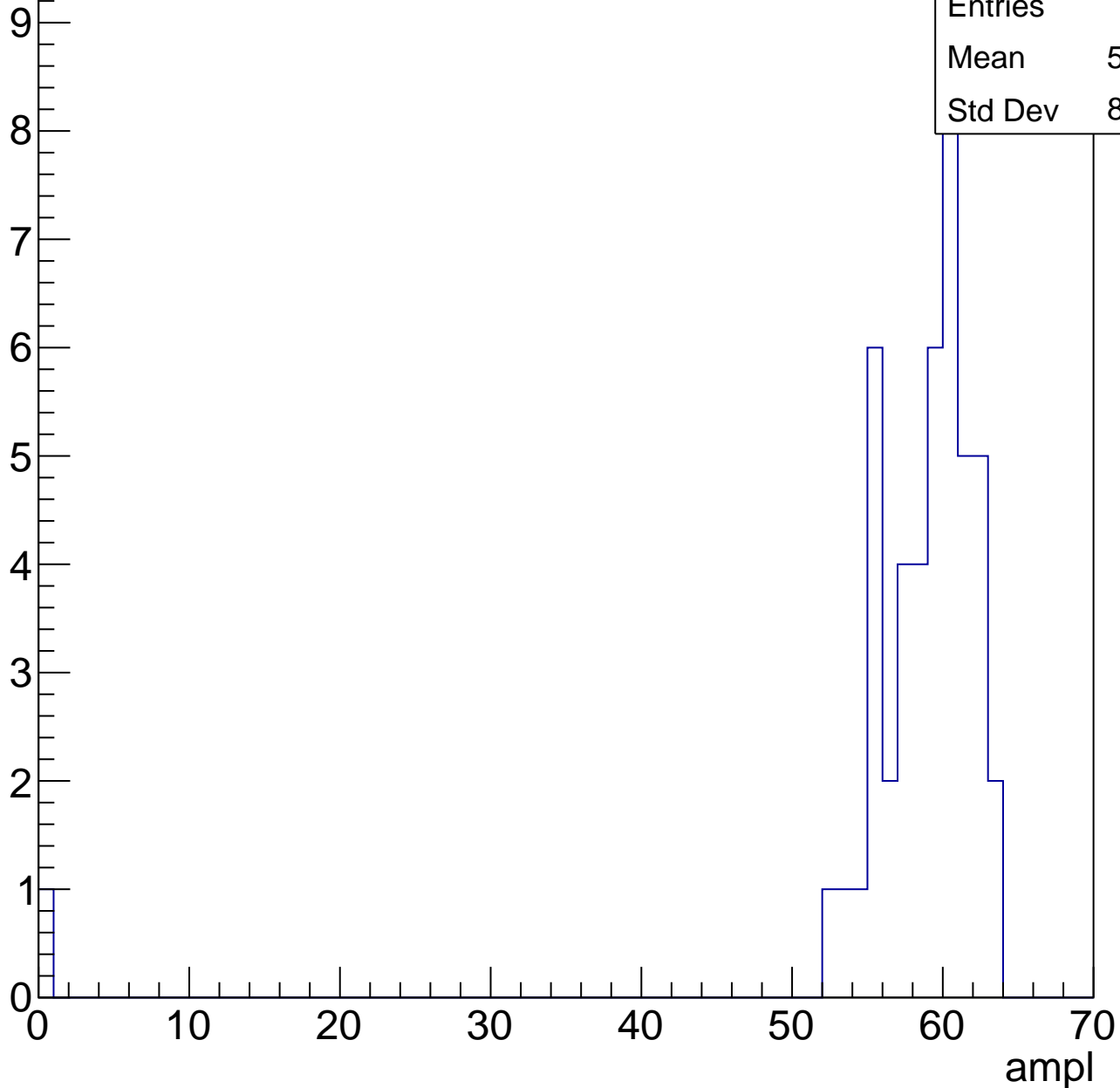
Entries	61
Mean	53.34
Std Dev	3.634



# B0L102S, U6-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

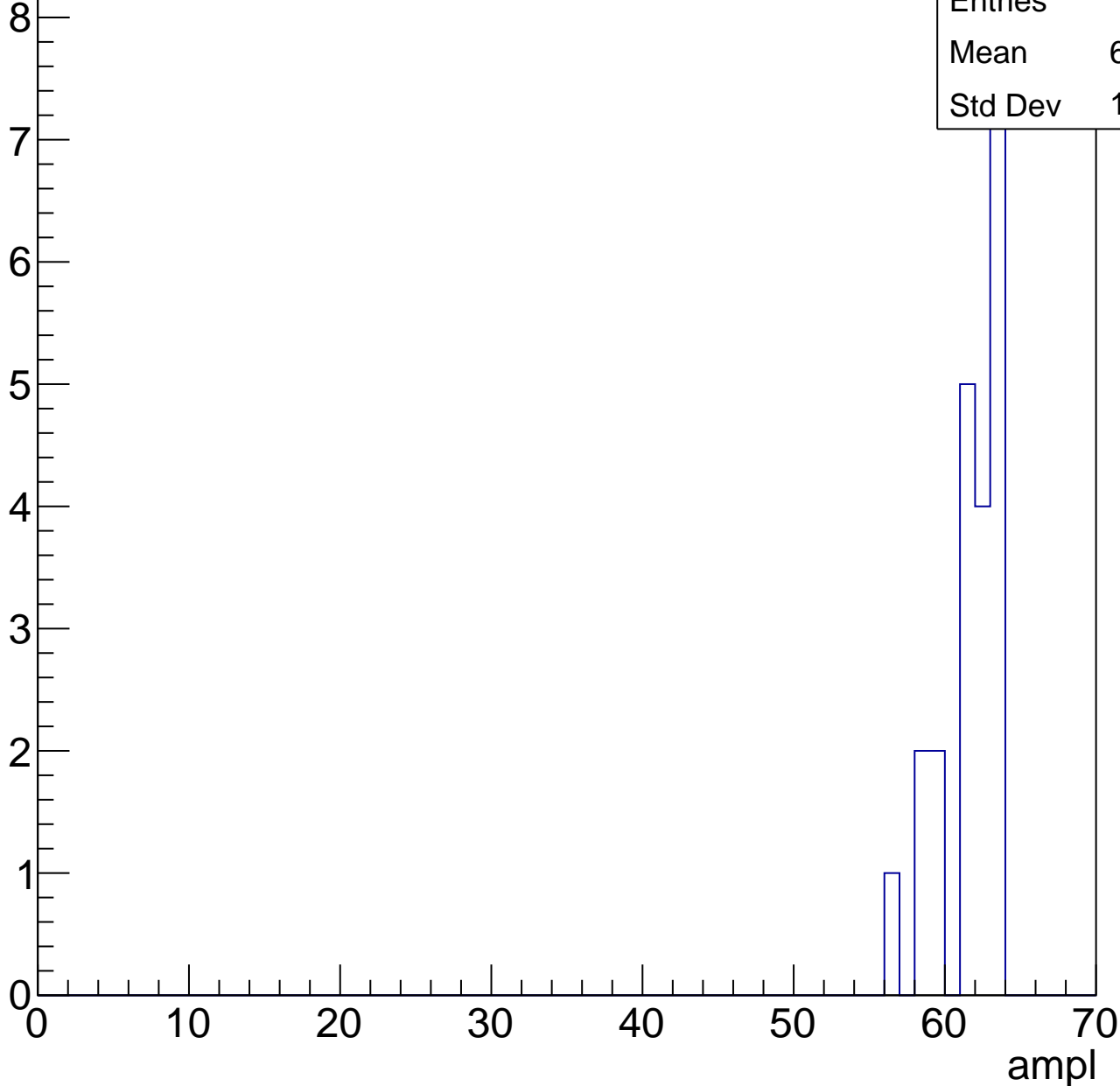


# B0L102S, U6-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	22
Mean	61.23
Std Dev	1.976





# B0L102S, U6-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	30
Std Dev	3.148

**Gaus mean : 30.4125**

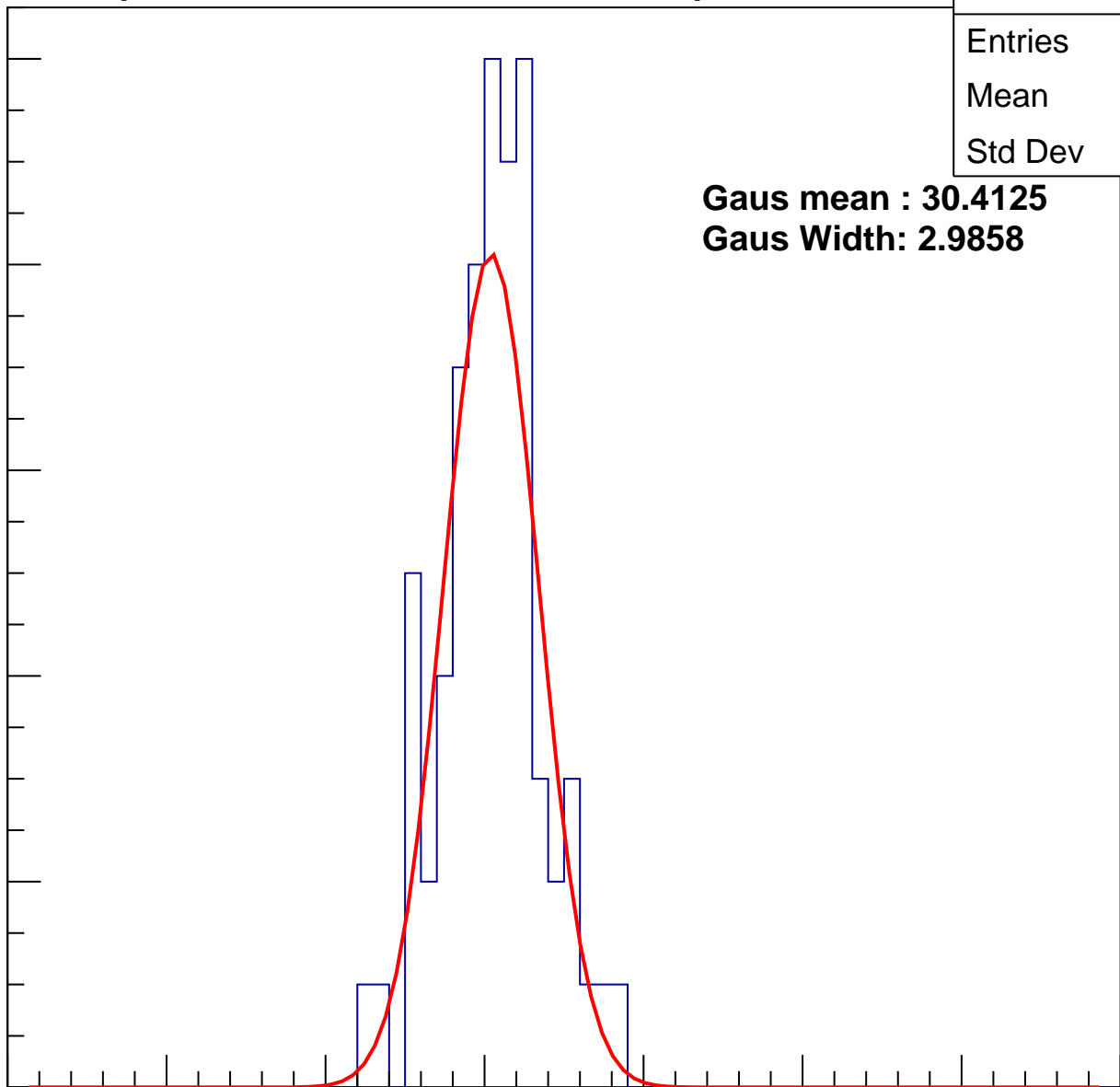
**Gaus Width: 2.9858**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch73, adc1

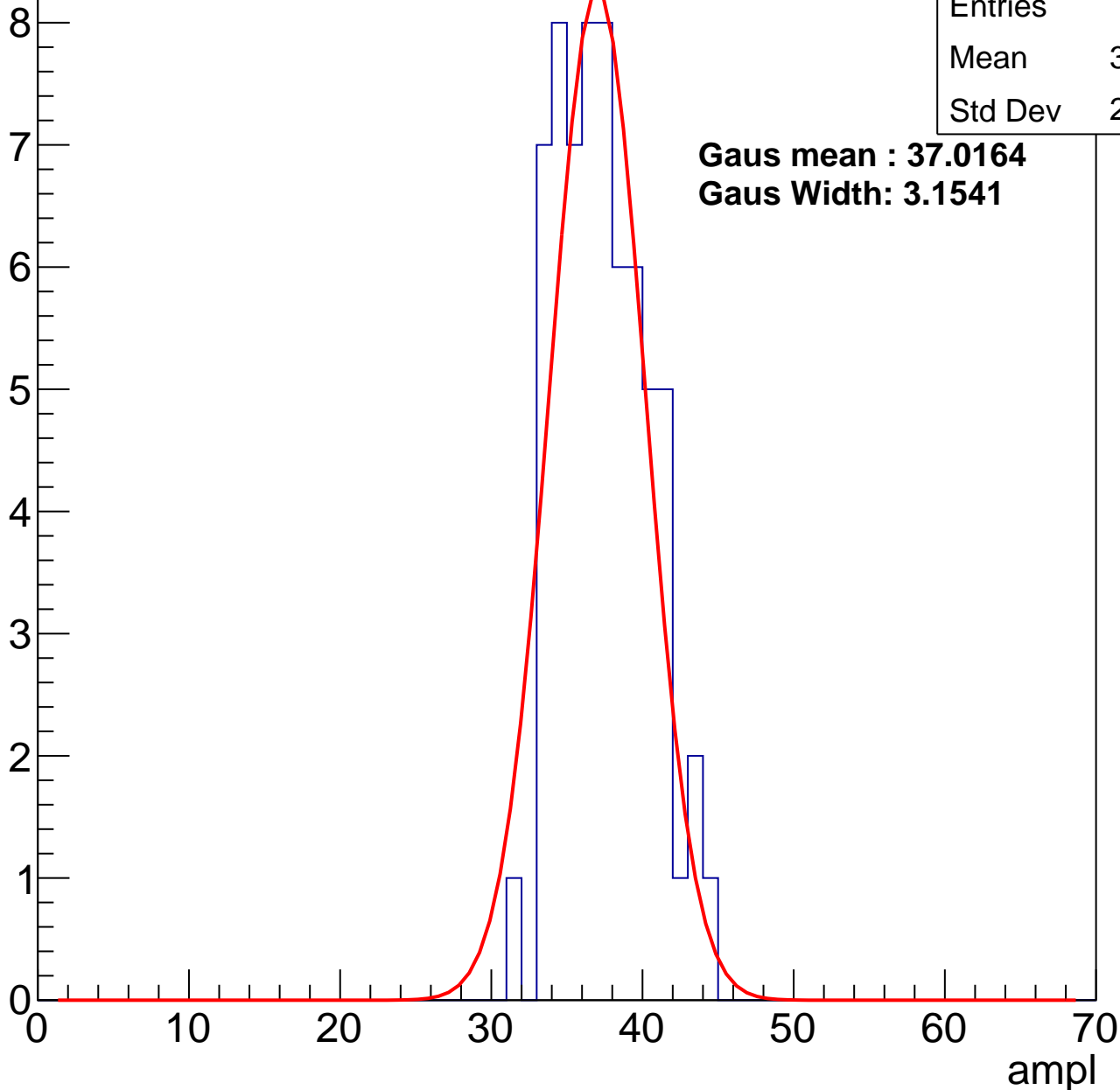
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36.95
Std Dev	2.927

**Gaus mean : 37.0164**

**Gaus Width: 3.1541**



# B0L102S, U6-ch73, adc2

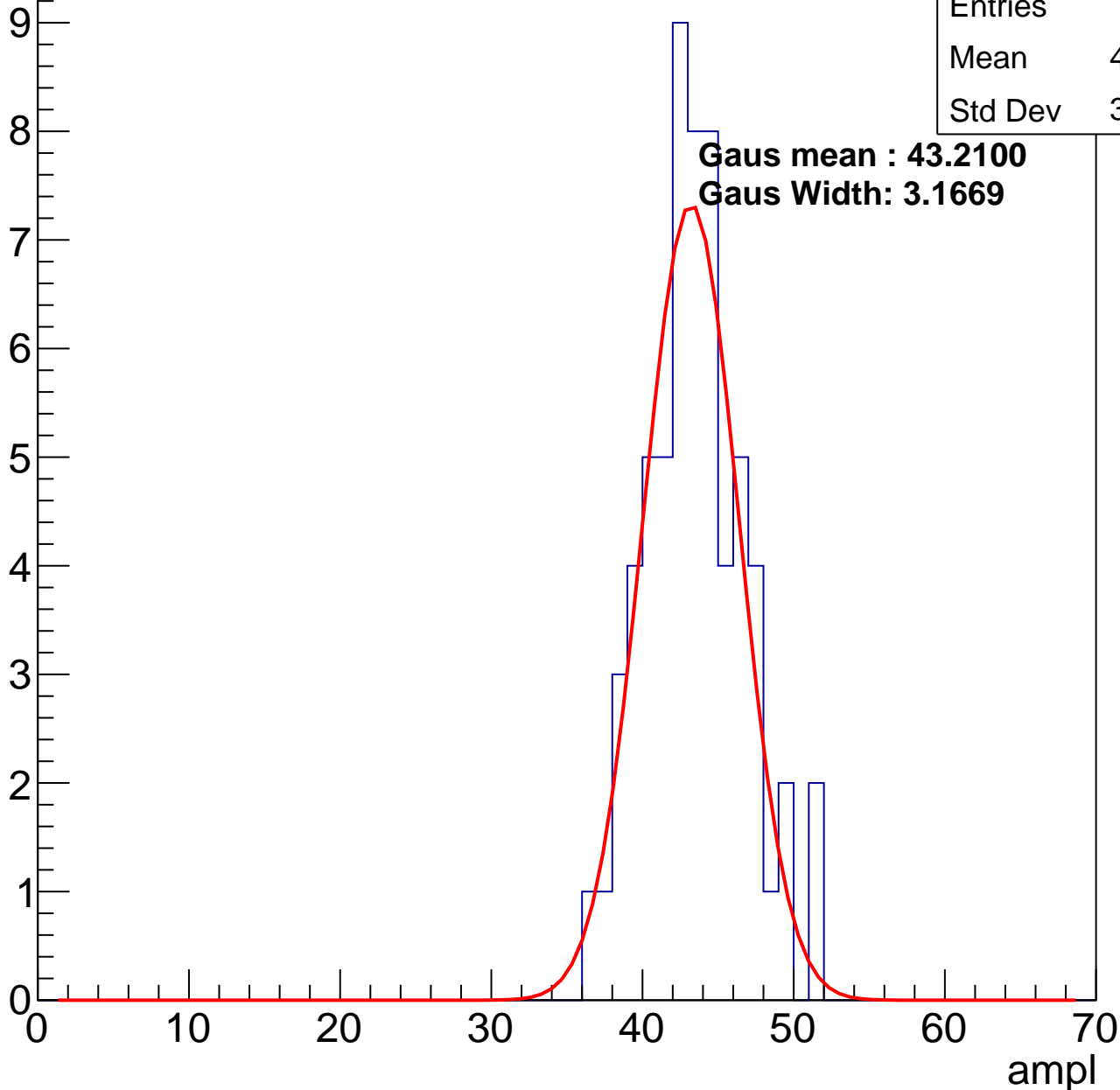
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	43.03
Std Dev	3.253

**Gaus mean : 43.2100**

**Gaus Width: 3.1669**

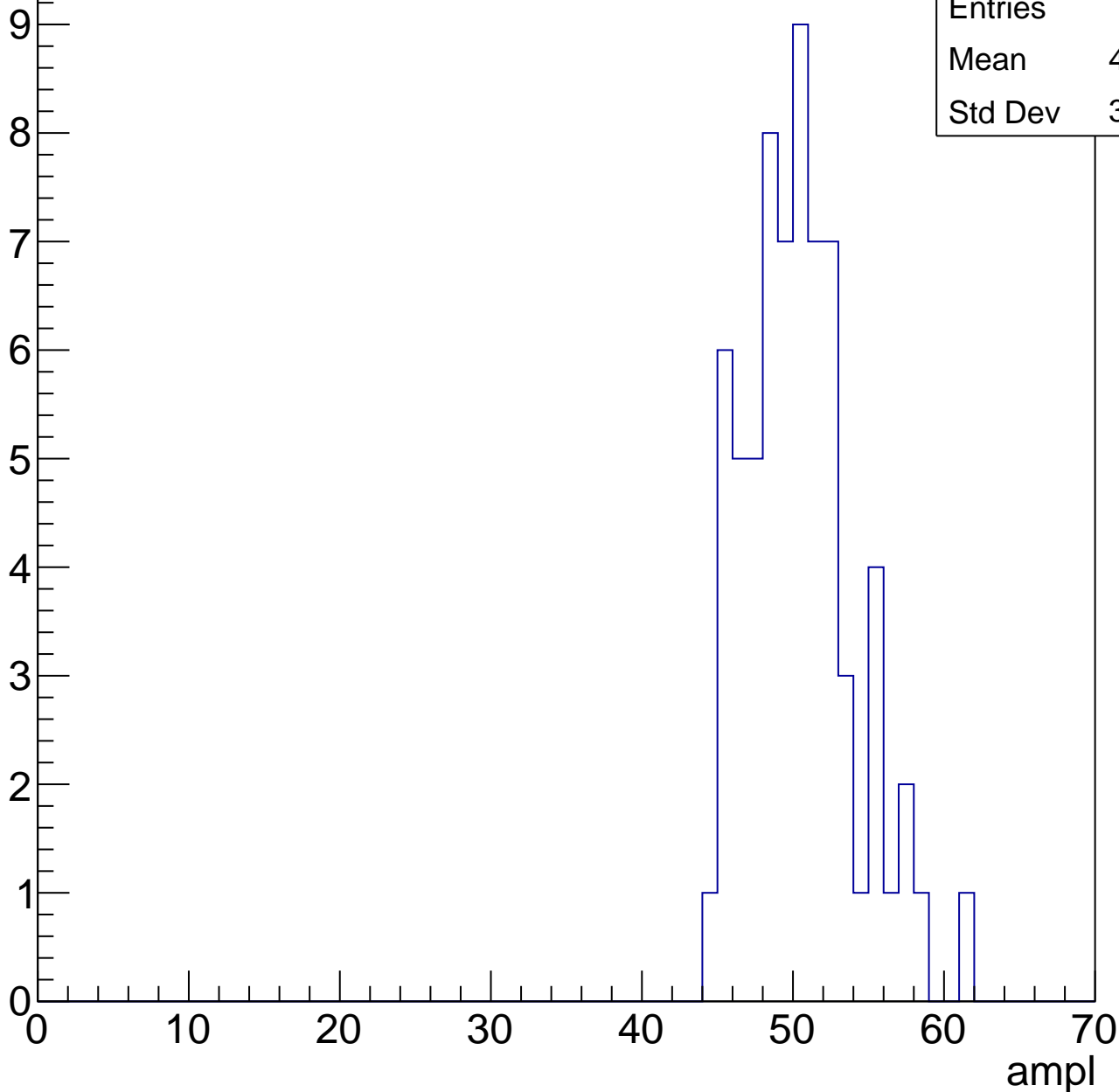


# B0L102S, U6-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	49.99
Std Dev	3.529

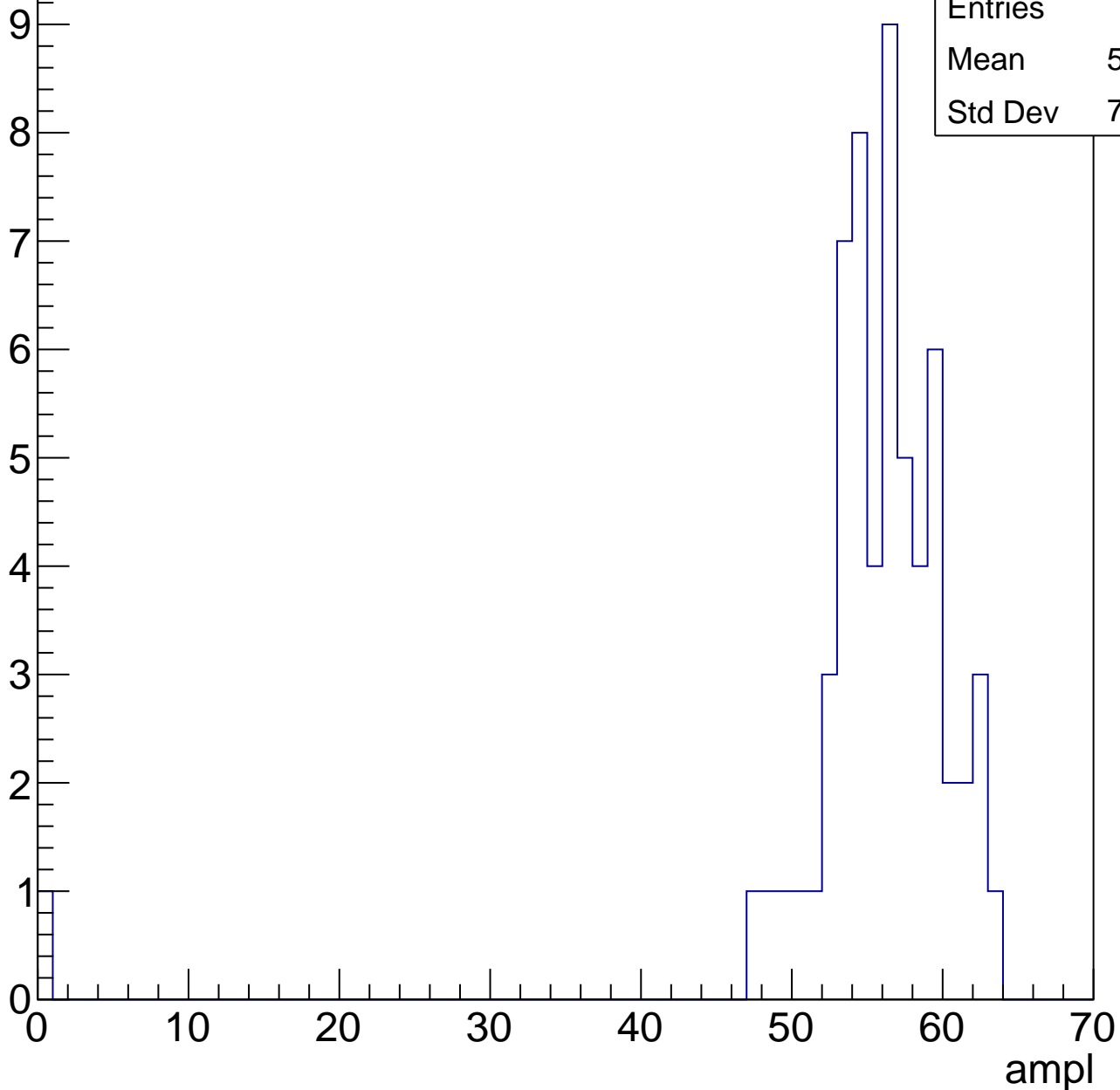


# B0L102S, U6-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	54.83
Std Dev	7.927



# B0L102S, U6-ch73, adc5

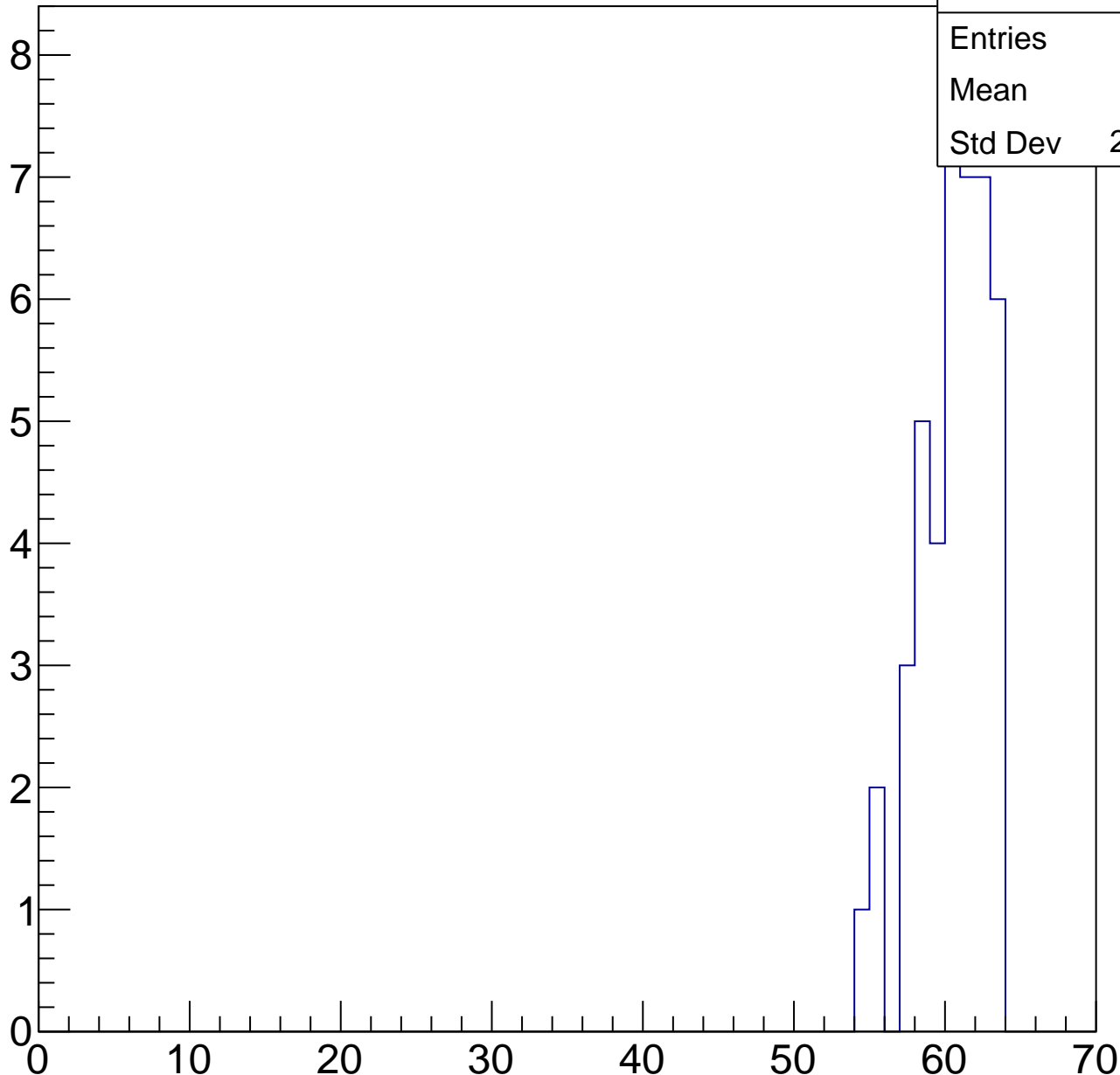
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	60
Std Dev	2.293

ampl



# B0L102S, U6-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch74, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	77
Mean	26.97
Std Dev	3.636

**Gaus mean : 27.7726**

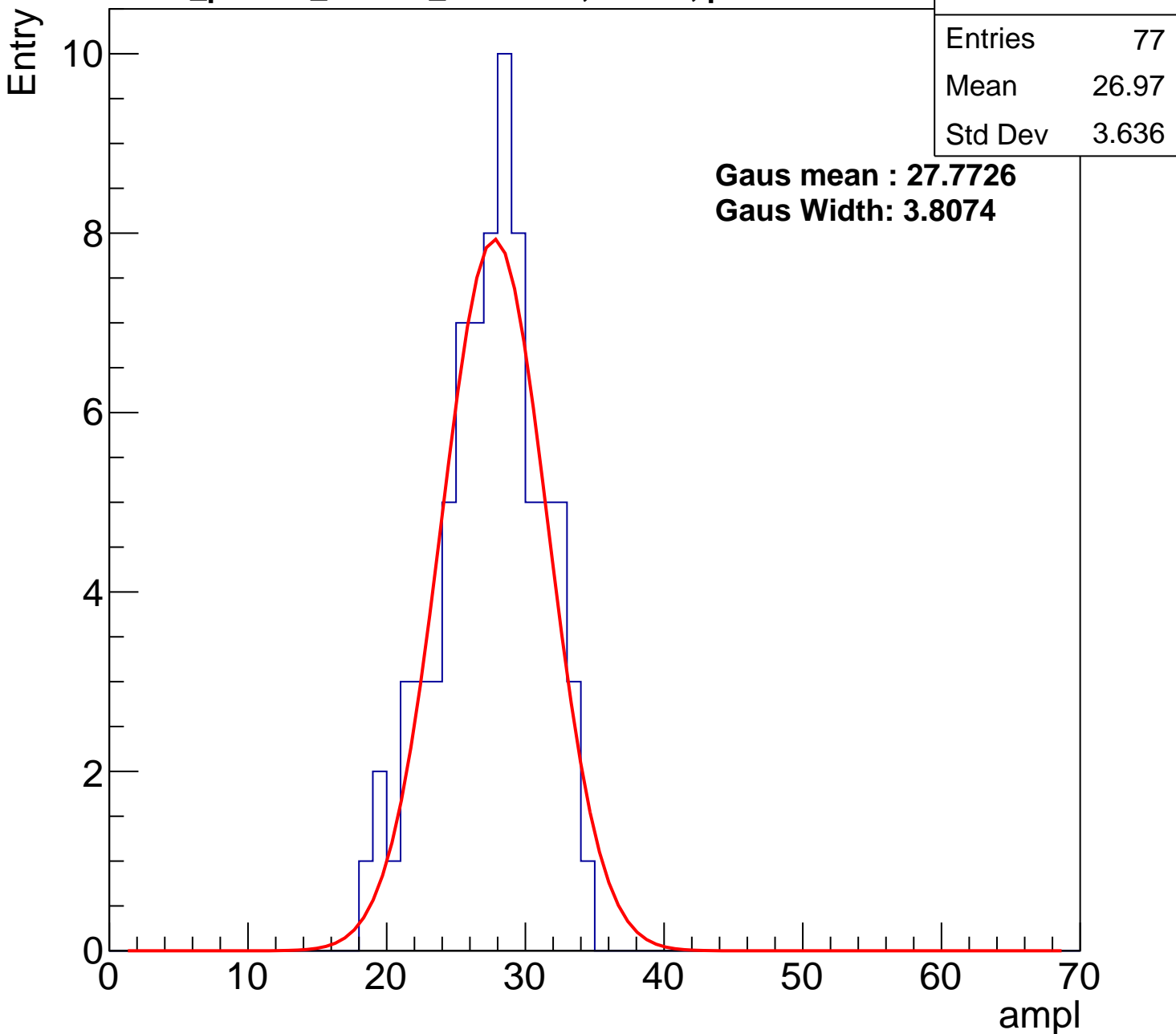
**Gaus Width: 3.8074**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	69
Mean	33.9
Std Dev	5.446

**Gaus mean : 34.6434**

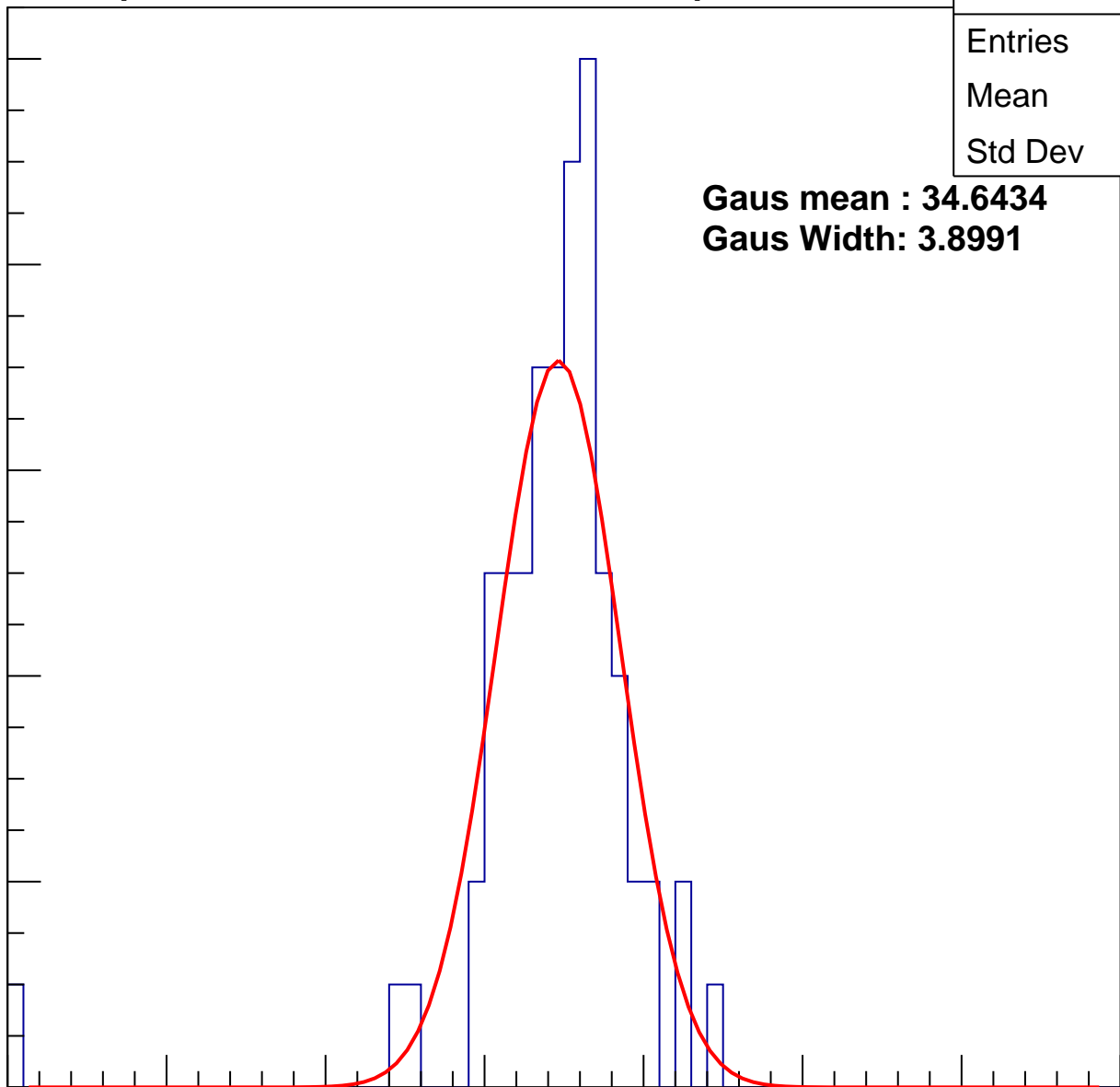
**Gaus Width: 3.8991**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch74, adc2

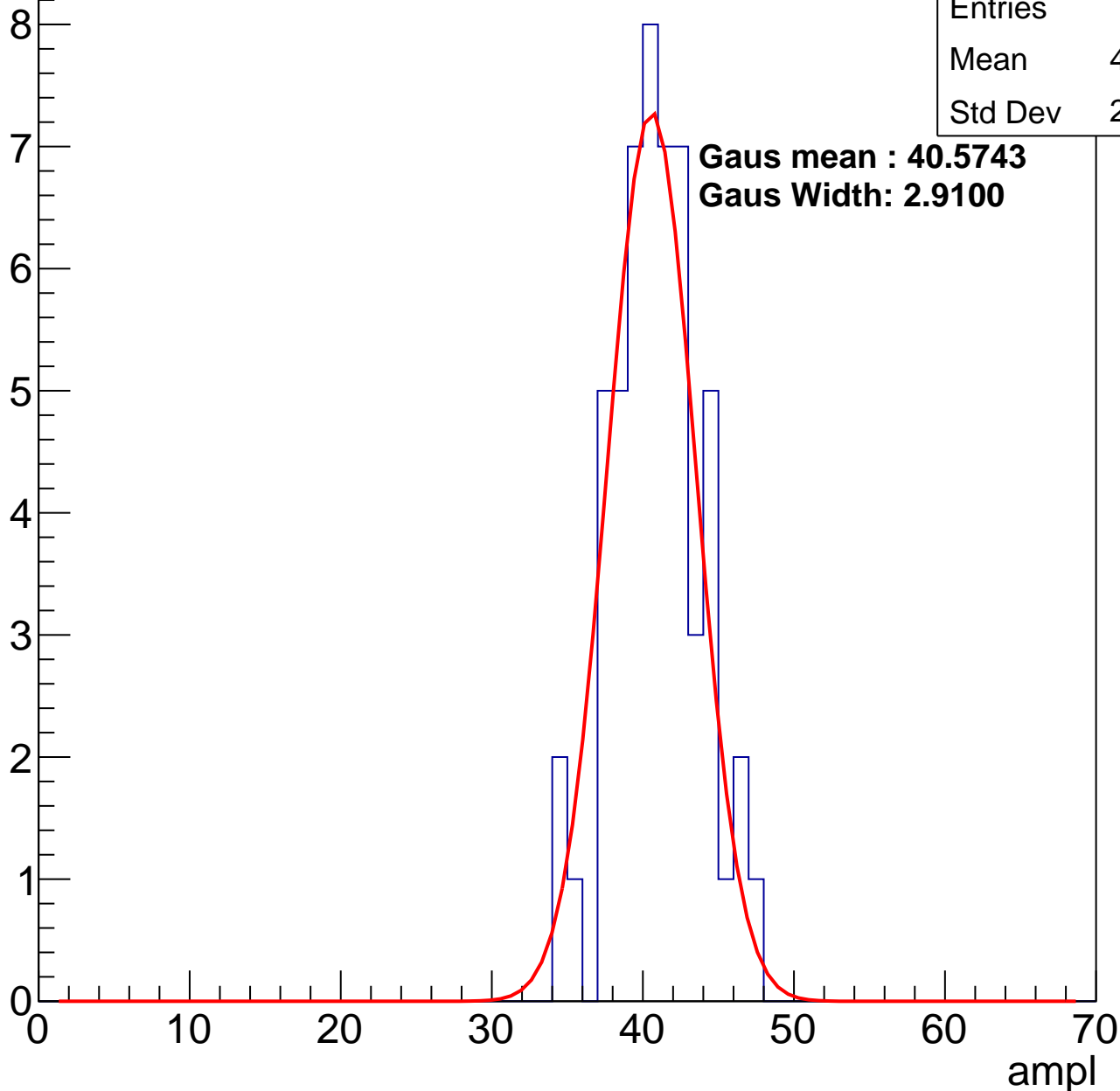
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	40.46
Std Dev	2.872

**Gaus mean : 40.5743**

**Gaus Width: 2.9100**

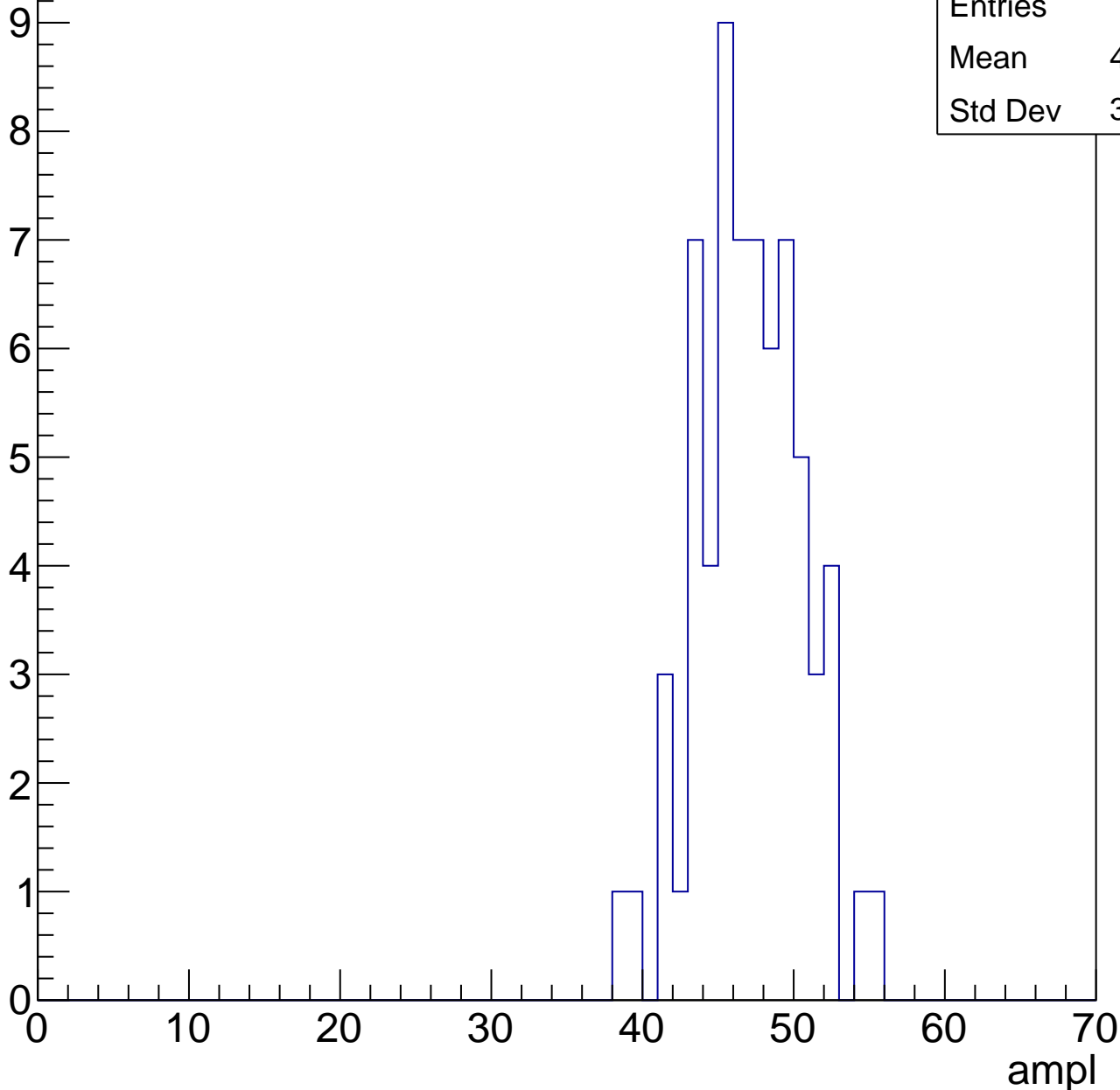


# B0L102S, U6-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	46.66
Std Dev	3.458

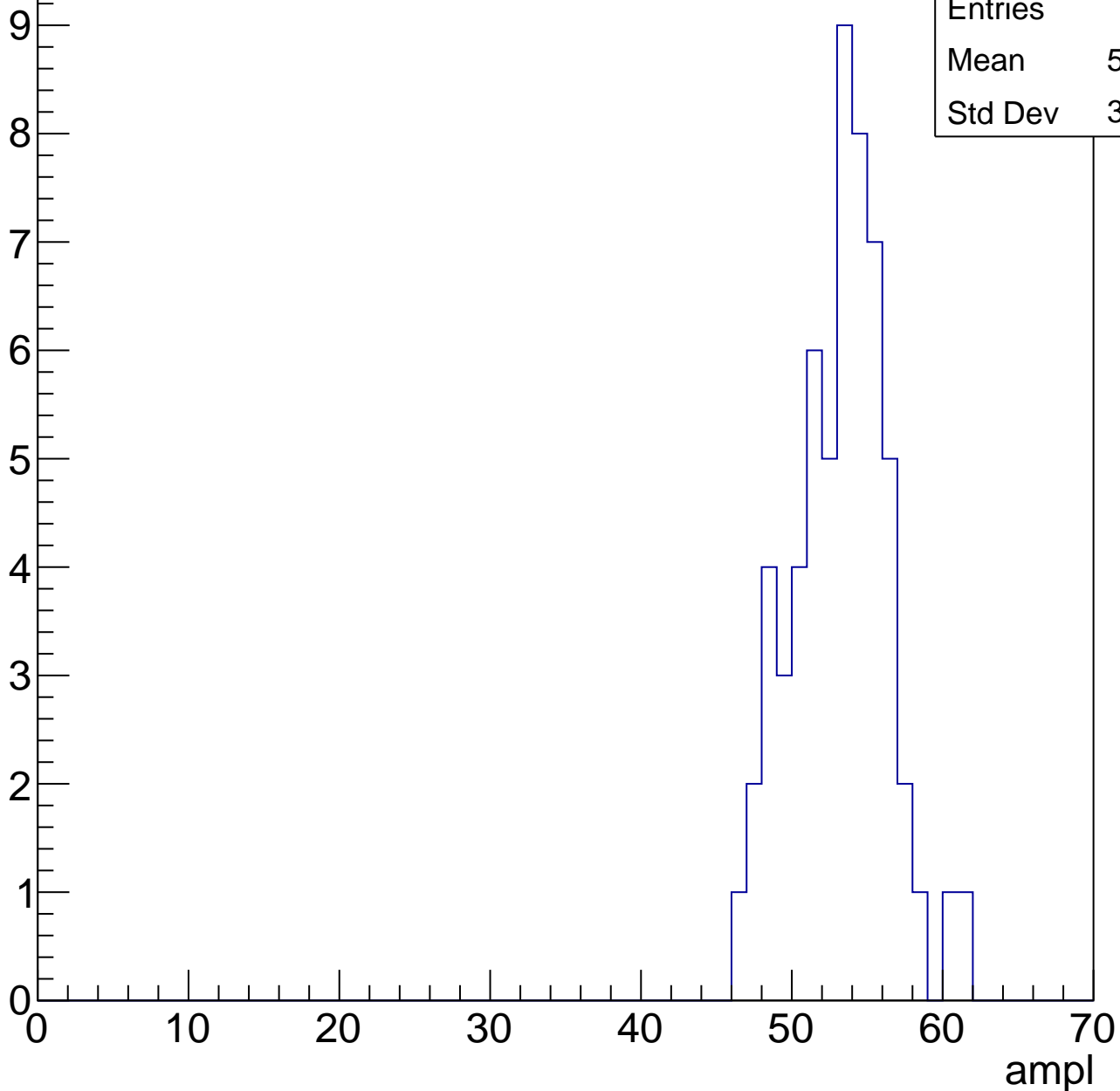


# B0L102S, U6-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

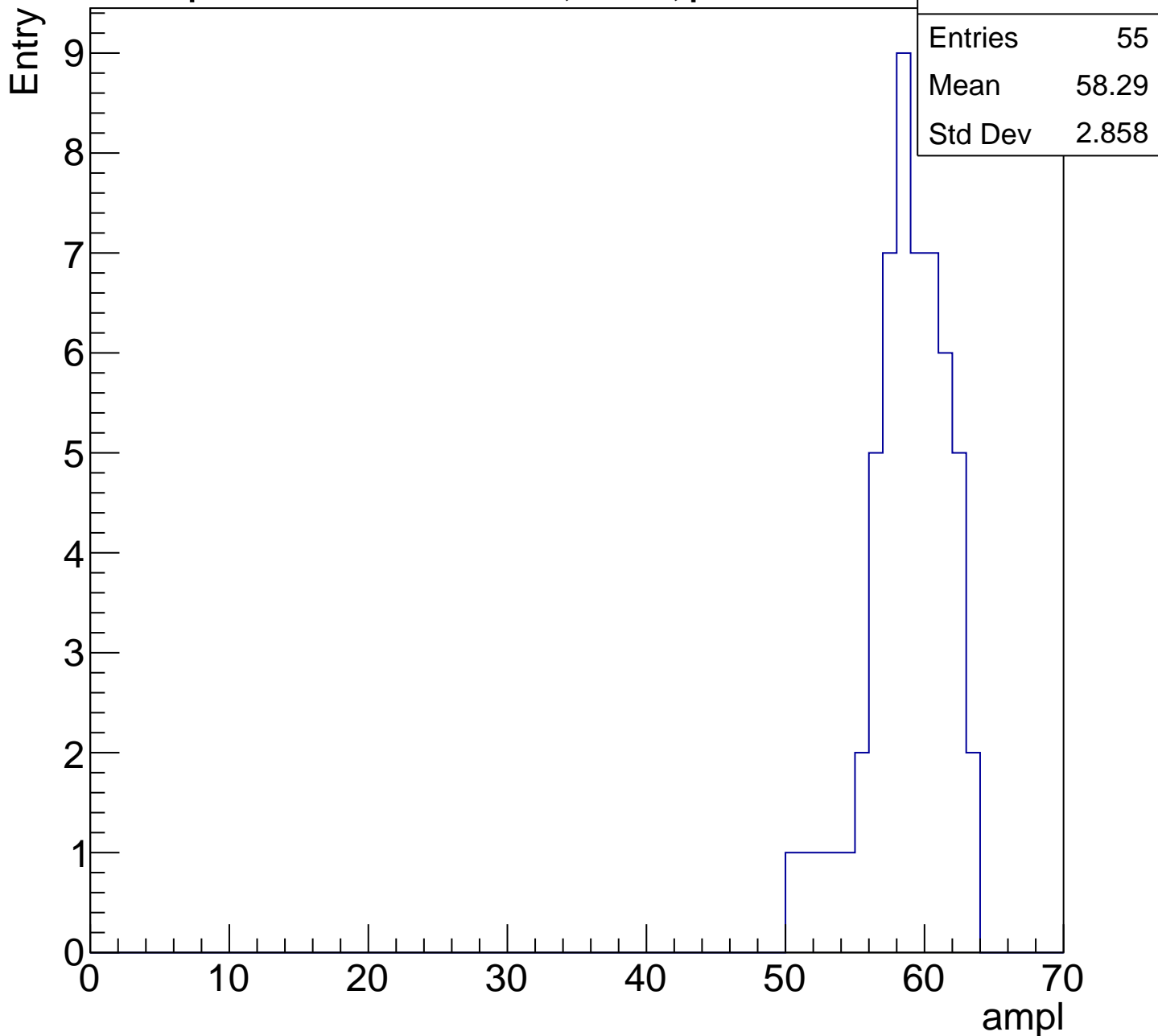
Entry

Entries	59
Mean	52.75
Std Dev	3.139



# B0L102S, U6-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

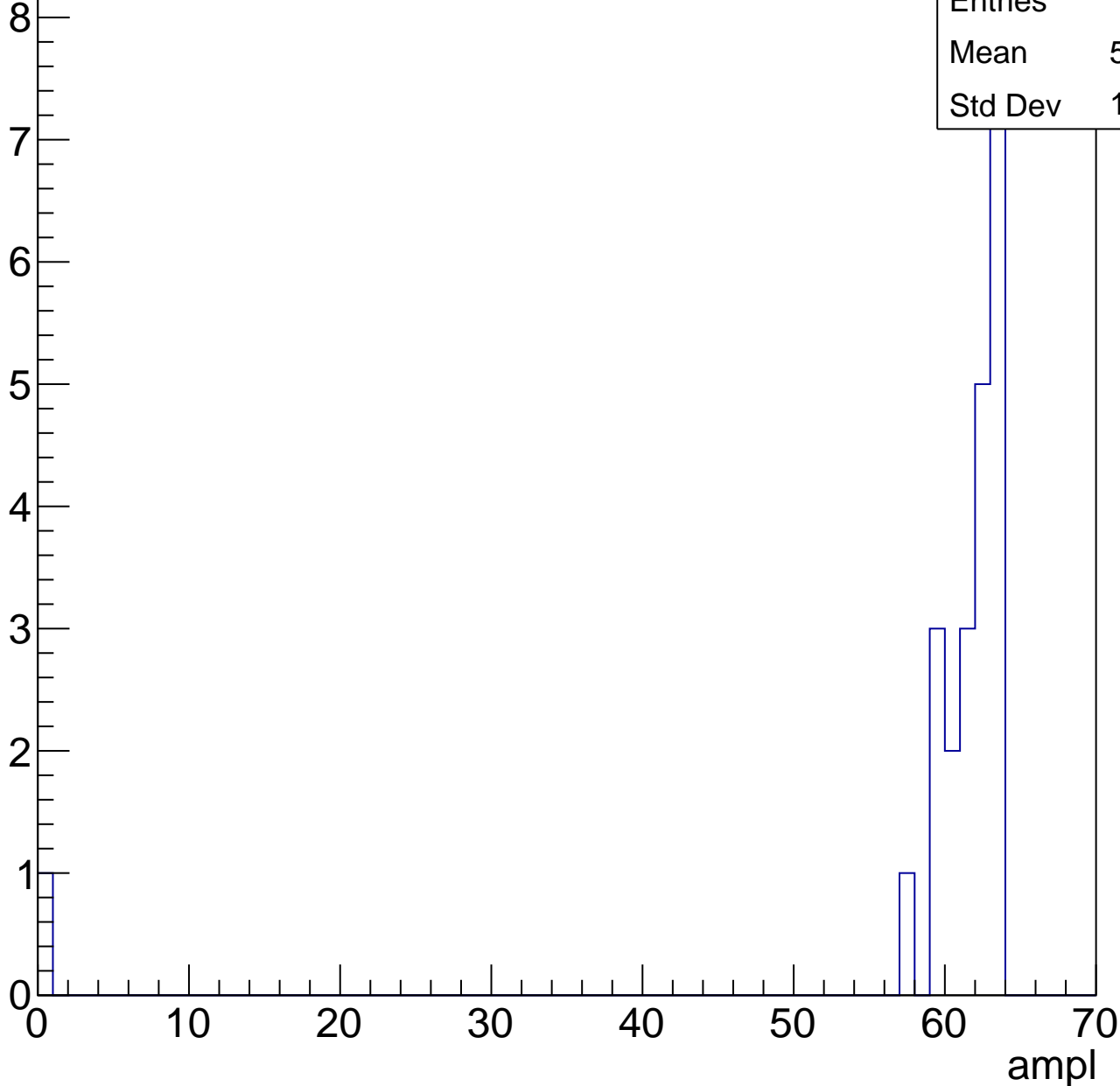


# B0L102S, U6-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	23
Mean	58.74
Std Dev	12.63





# B0L102S, U6-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch75, adc0

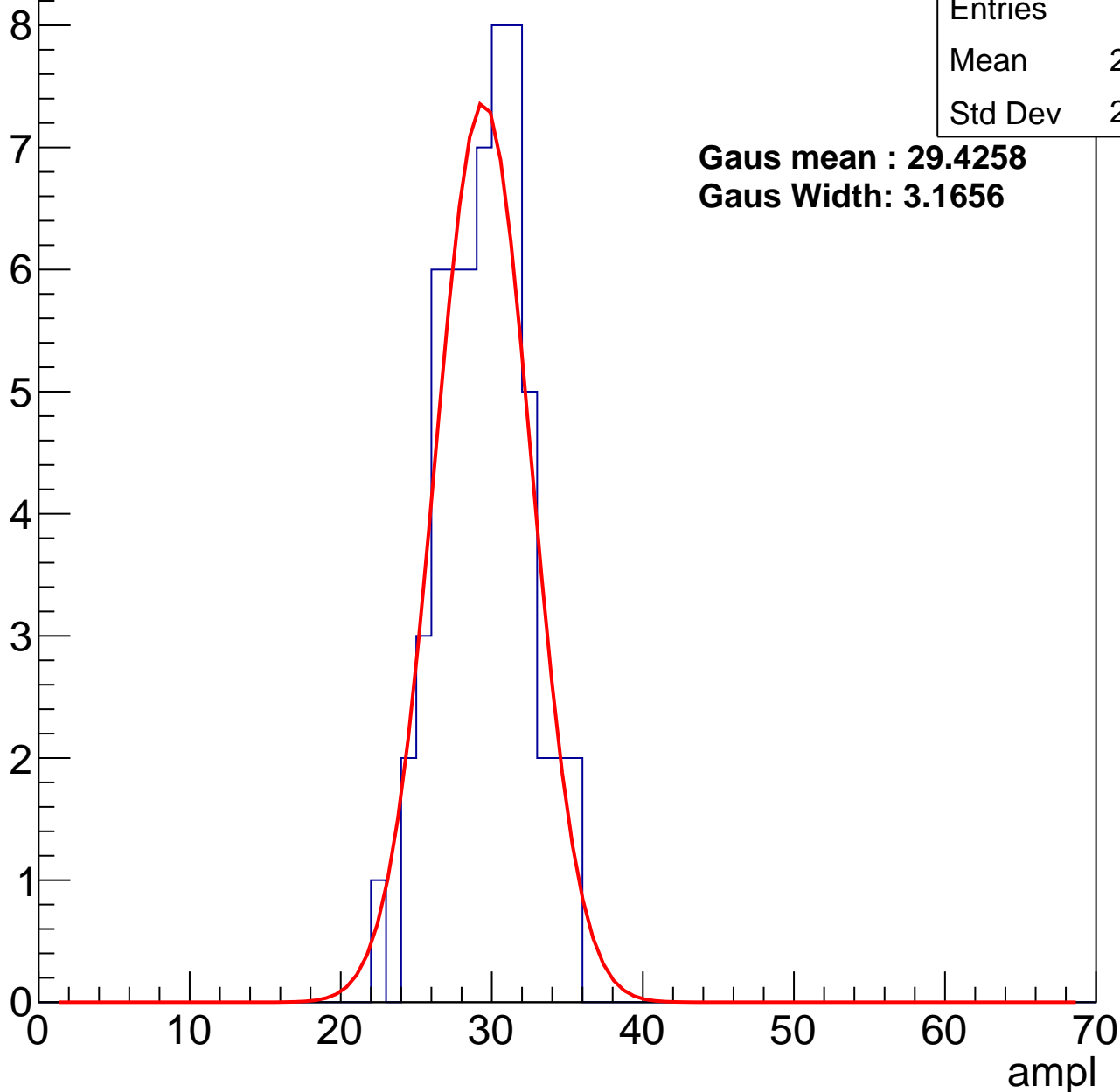
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	29.07
Std Dev	2.846

**Gaus mean : 29.4258**

**Gaus Width: 3.1656**



# B0L102S, U6-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	80
Mean	36.26
Std Dev	3.535

**Gaus mean : 37.0258**

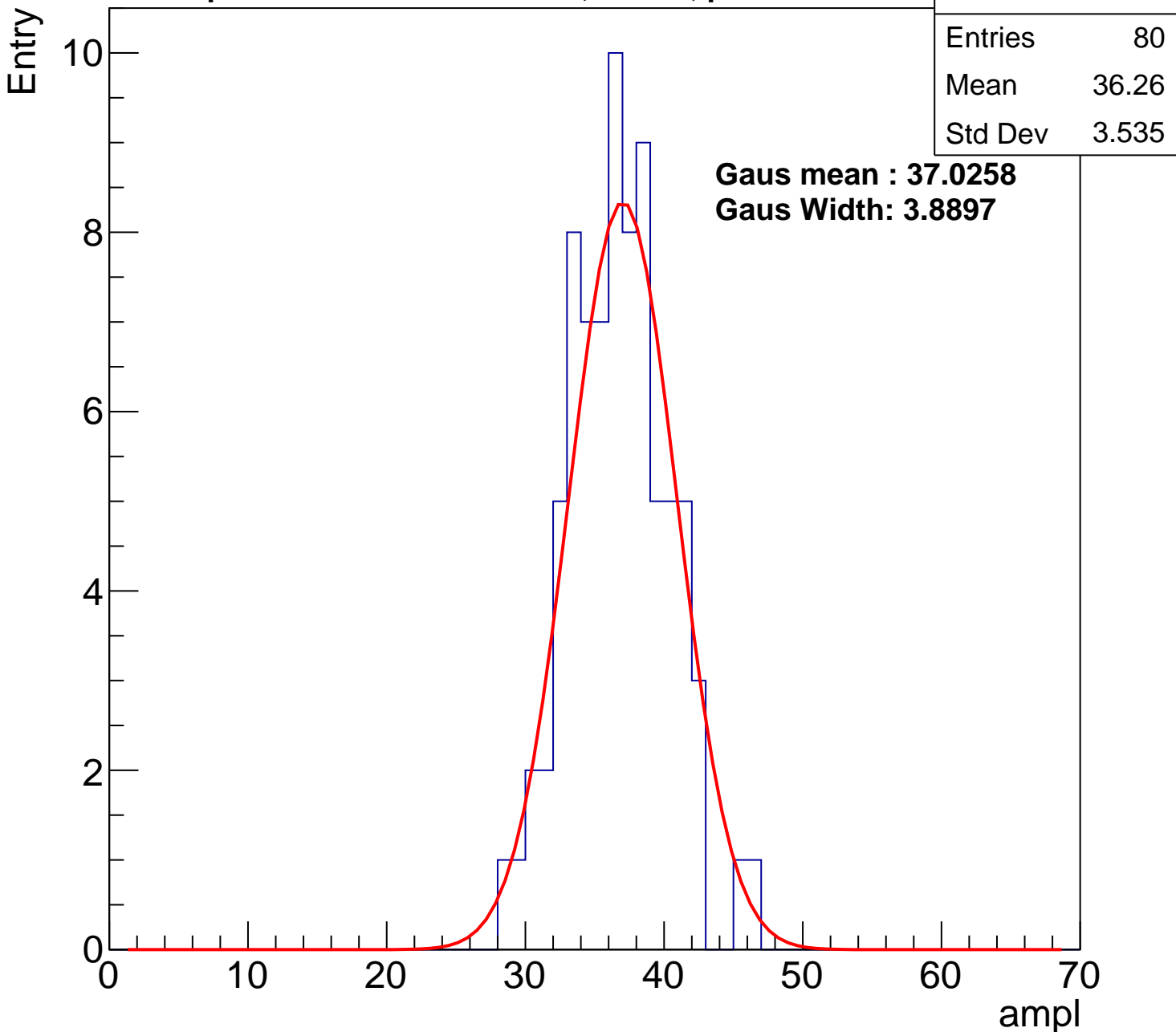
**Gaus Width: 3.8897**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U6-ch75, adc2

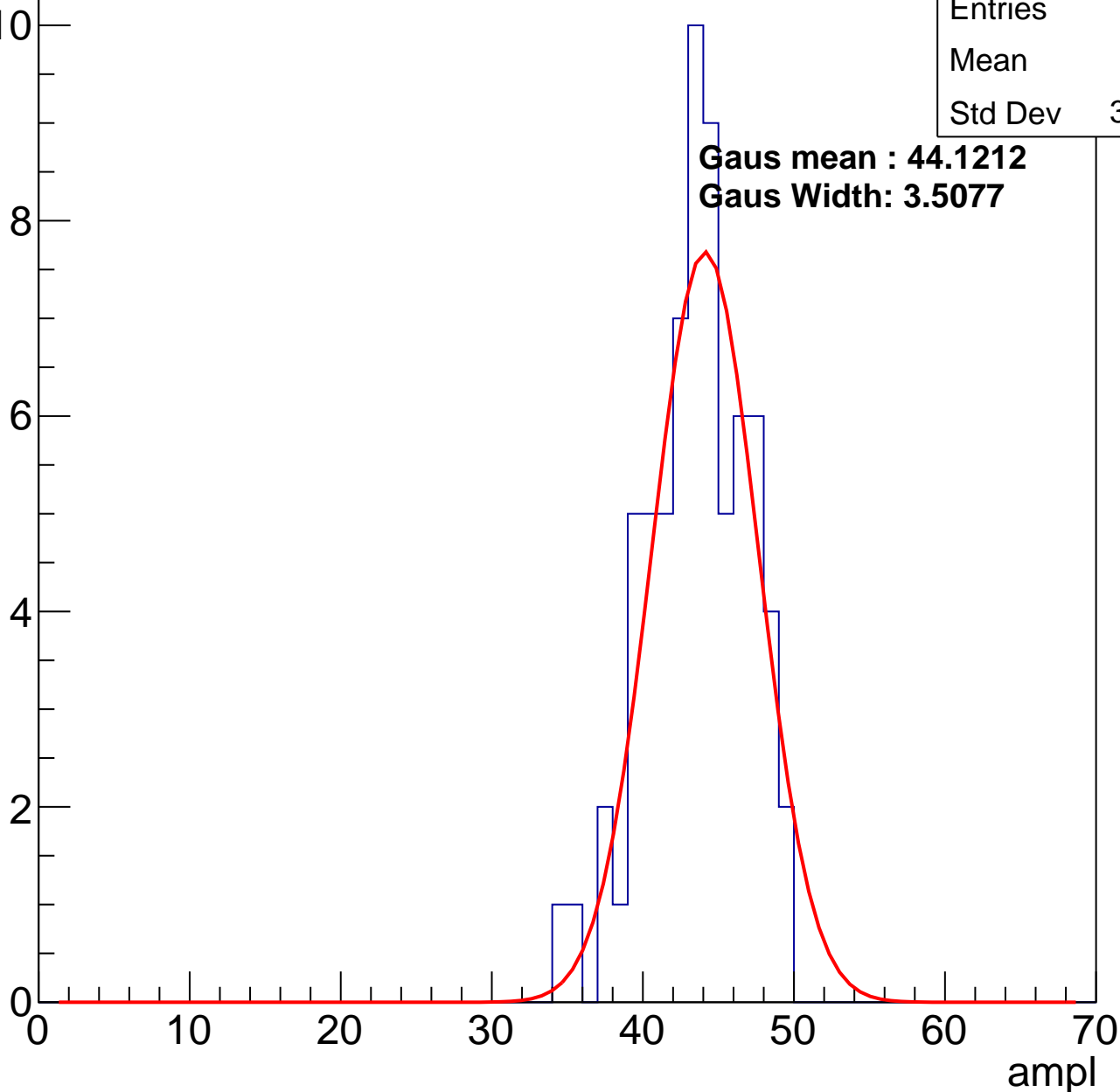
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	43.1
Std Dev	3.284

**Gaus mean : 44.1212**

**Gaus Width: 3.5077**

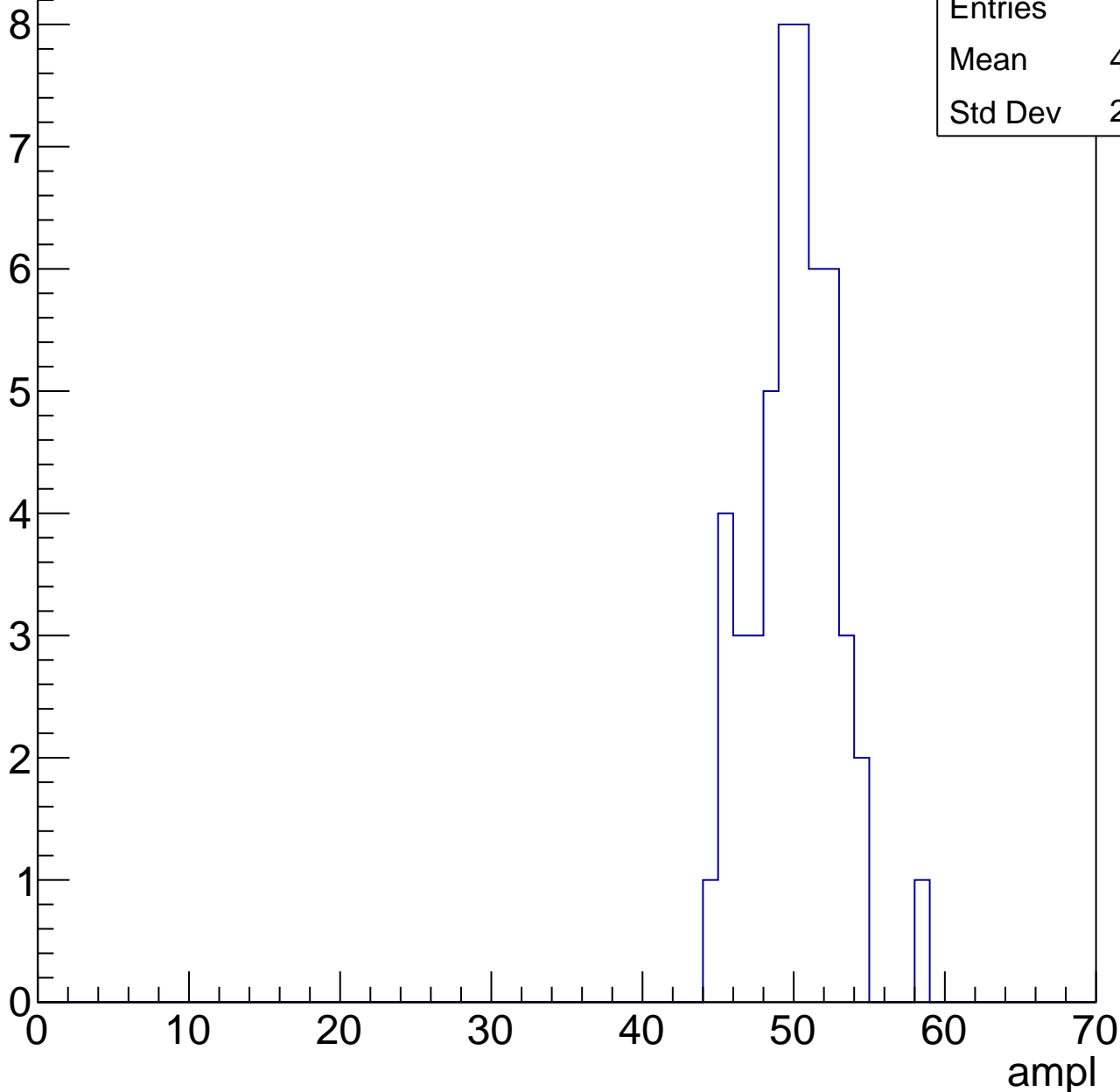


# B0L102S, U6-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	49.56
Std Dev	2.765

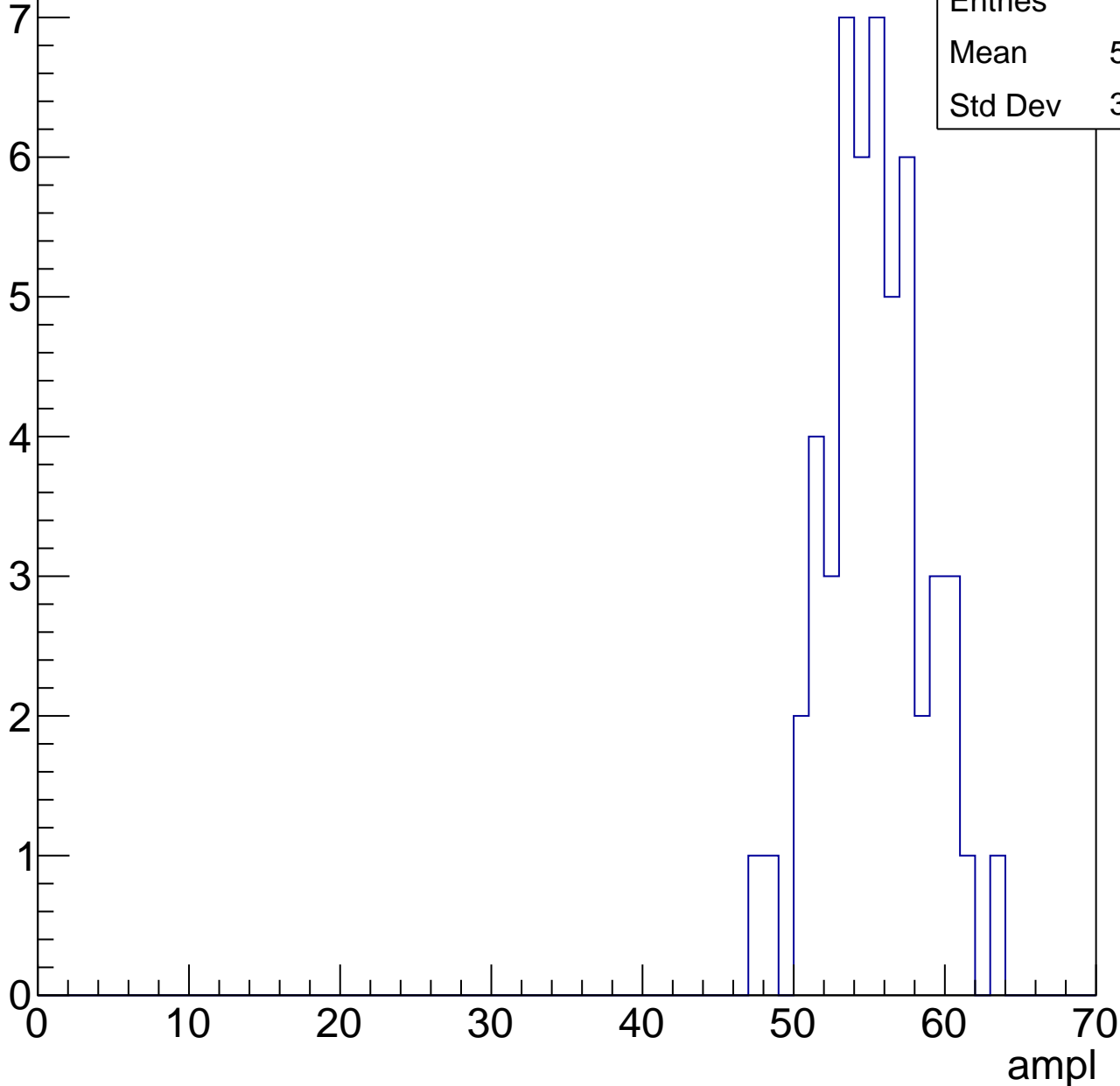


# B0L102S, U6-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	54.88
Std Dev	3.274

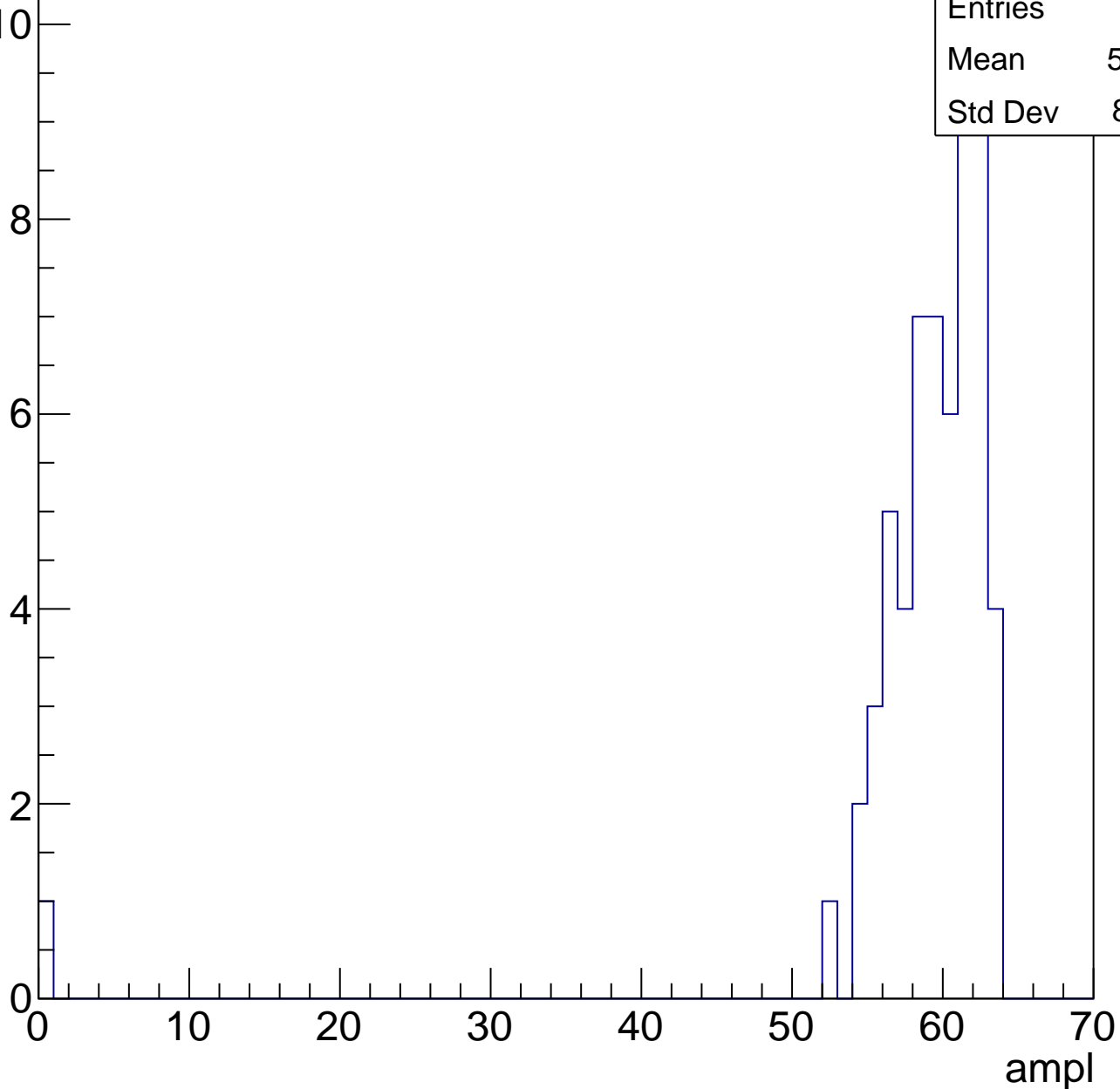


# B0L102S, U6-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	58.19
Std Dev	8.081

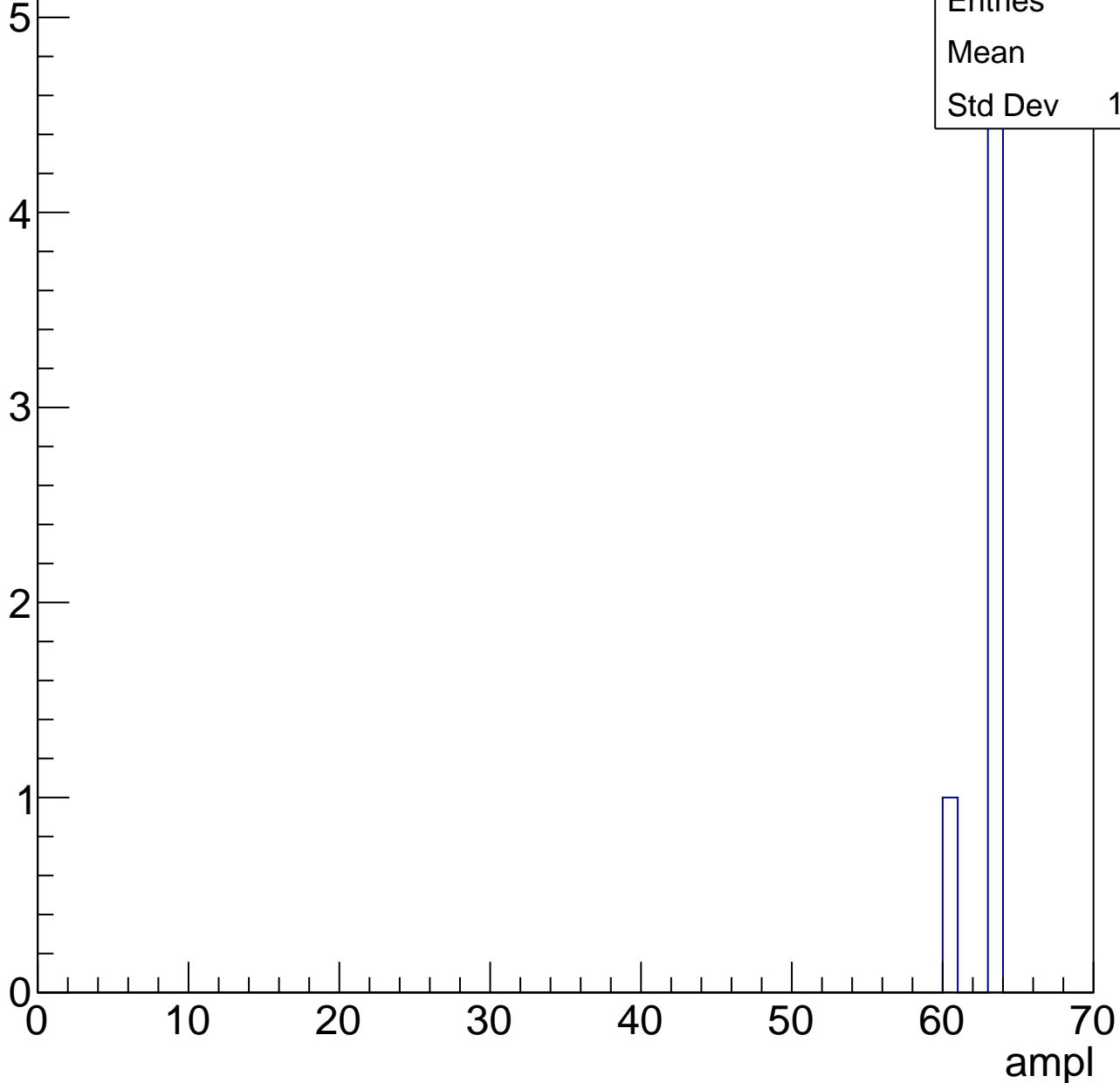


# B0L102S, U6-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	6
Mean	62.5
Std Dev	1.118





# B0L102S, U6-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch76, adc0

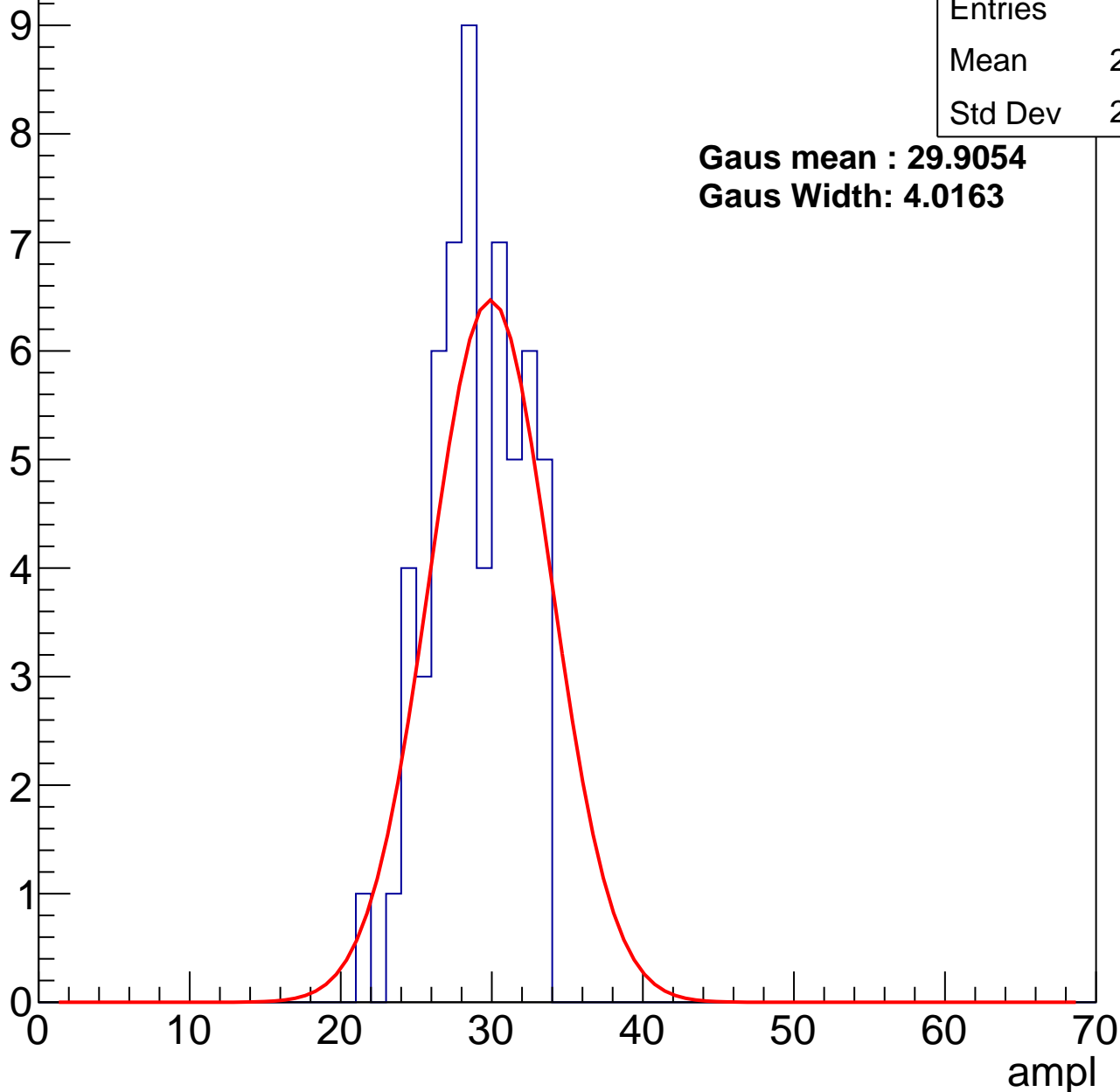
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	28.45
Std Dev	2.884

**Gaus mean : 29.9054**

**Gaus Width: 4.0163**



# B0L102S, U6-ch76, adc1

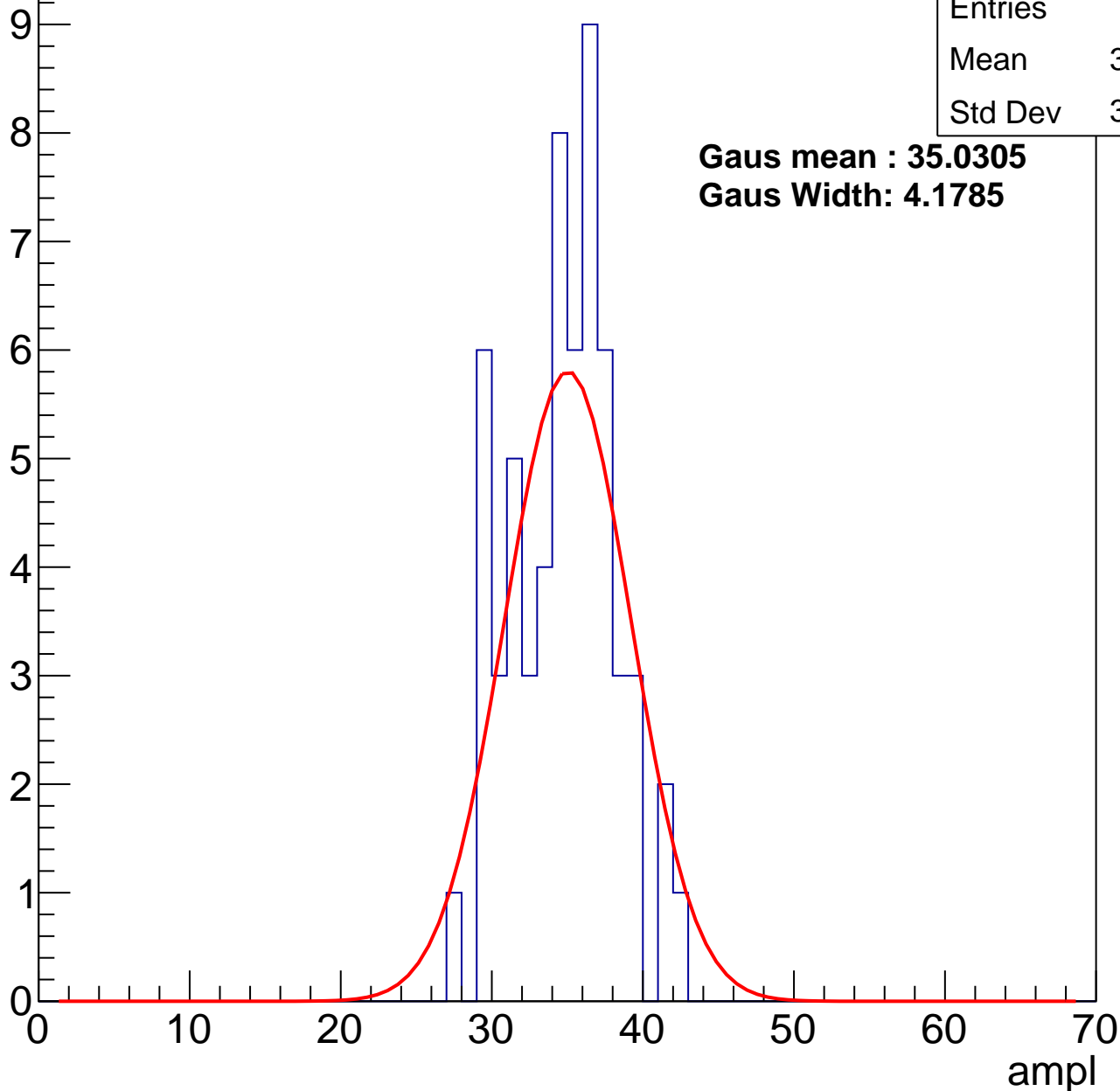
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	34.28
Std Dev	3.377

**Gaus mean : 35.0305**

**Gaus Width: 4.1785**



# B0L102S, U6-ch76, adc2

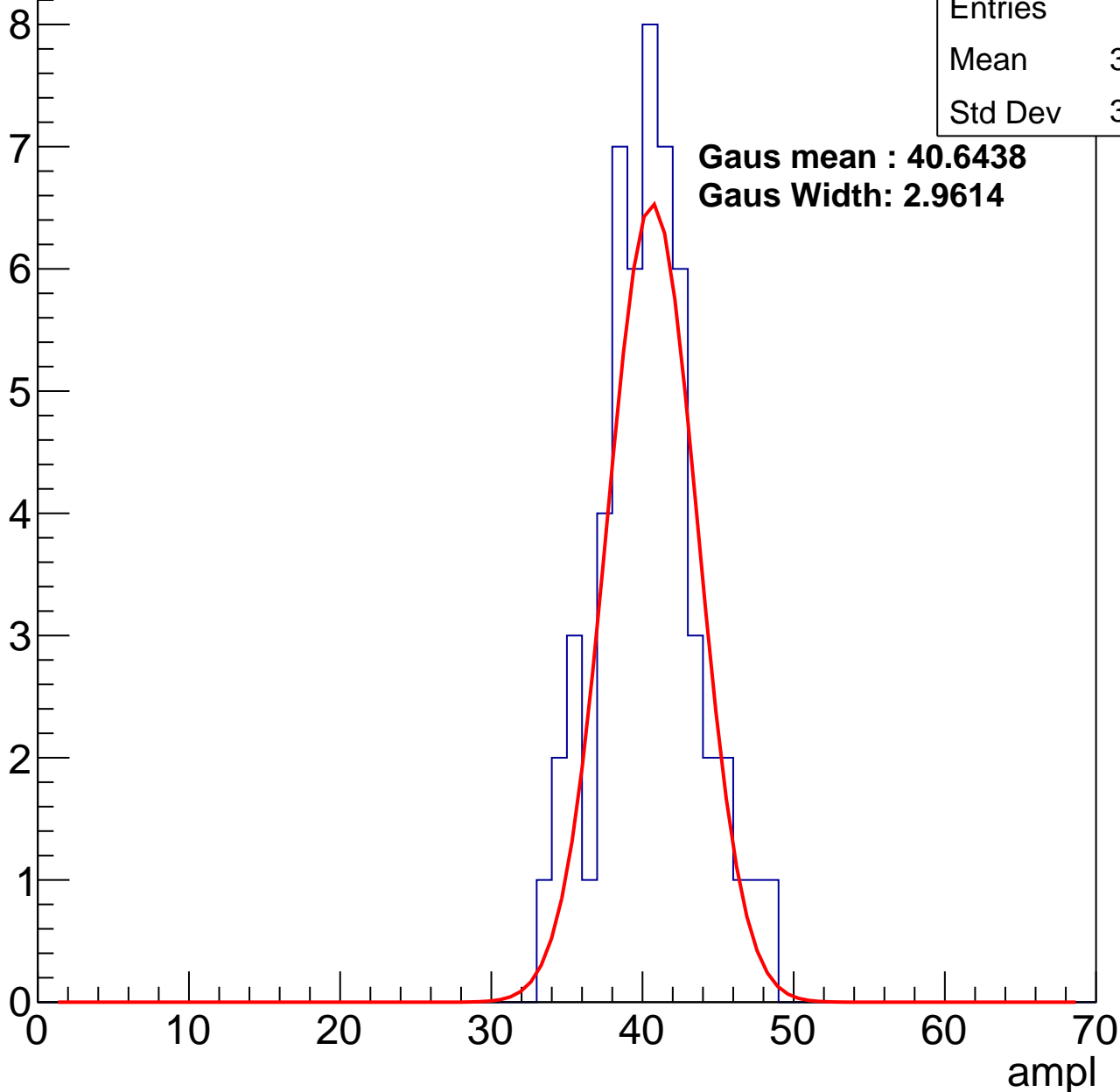
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	39.95
Std Dev	3.222

**Gaus mean : 40.6438**

**Gaus Width: 2.9614**

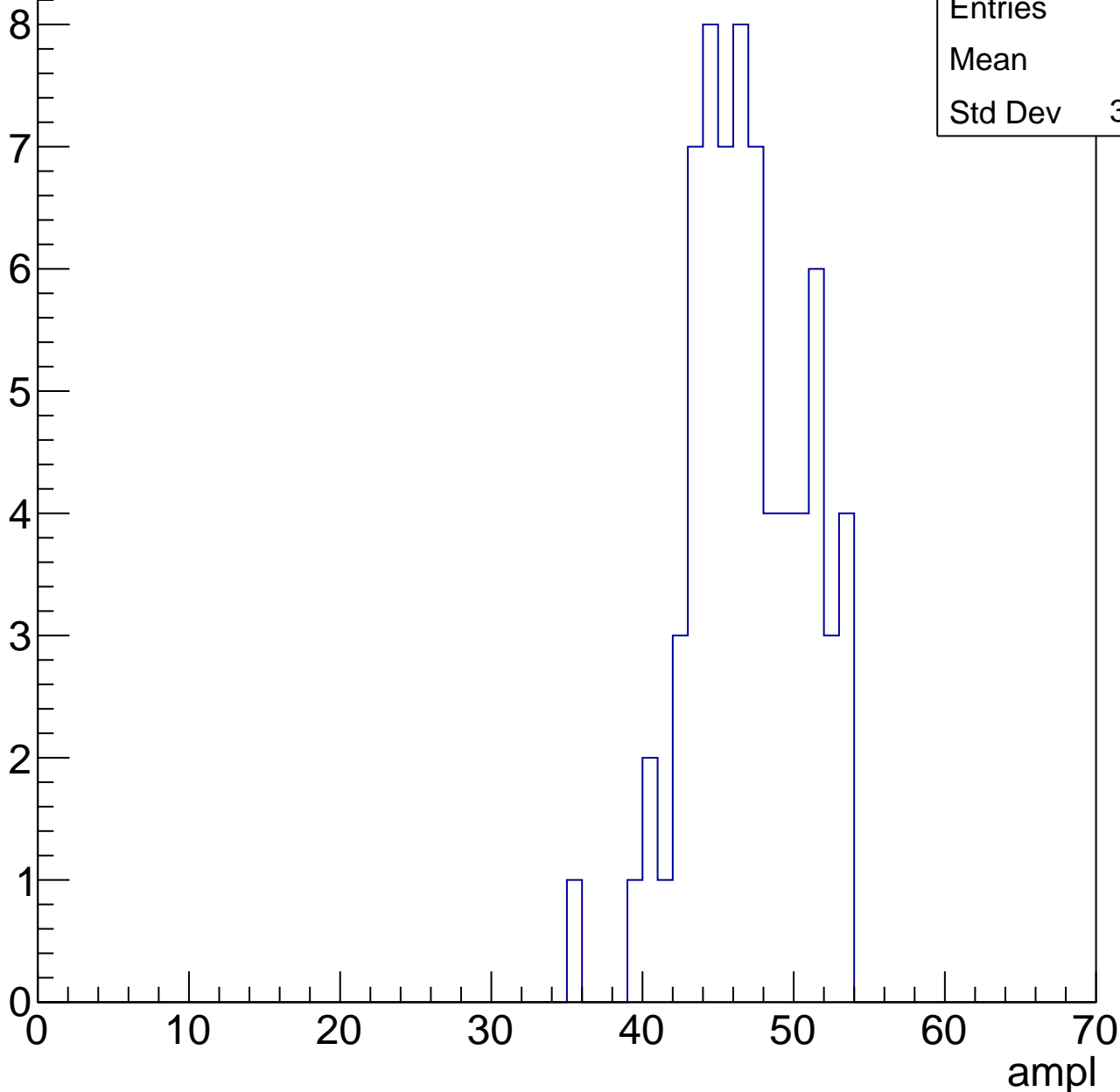


# B0L102S, U6-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	46.4
Std Dev	3.747

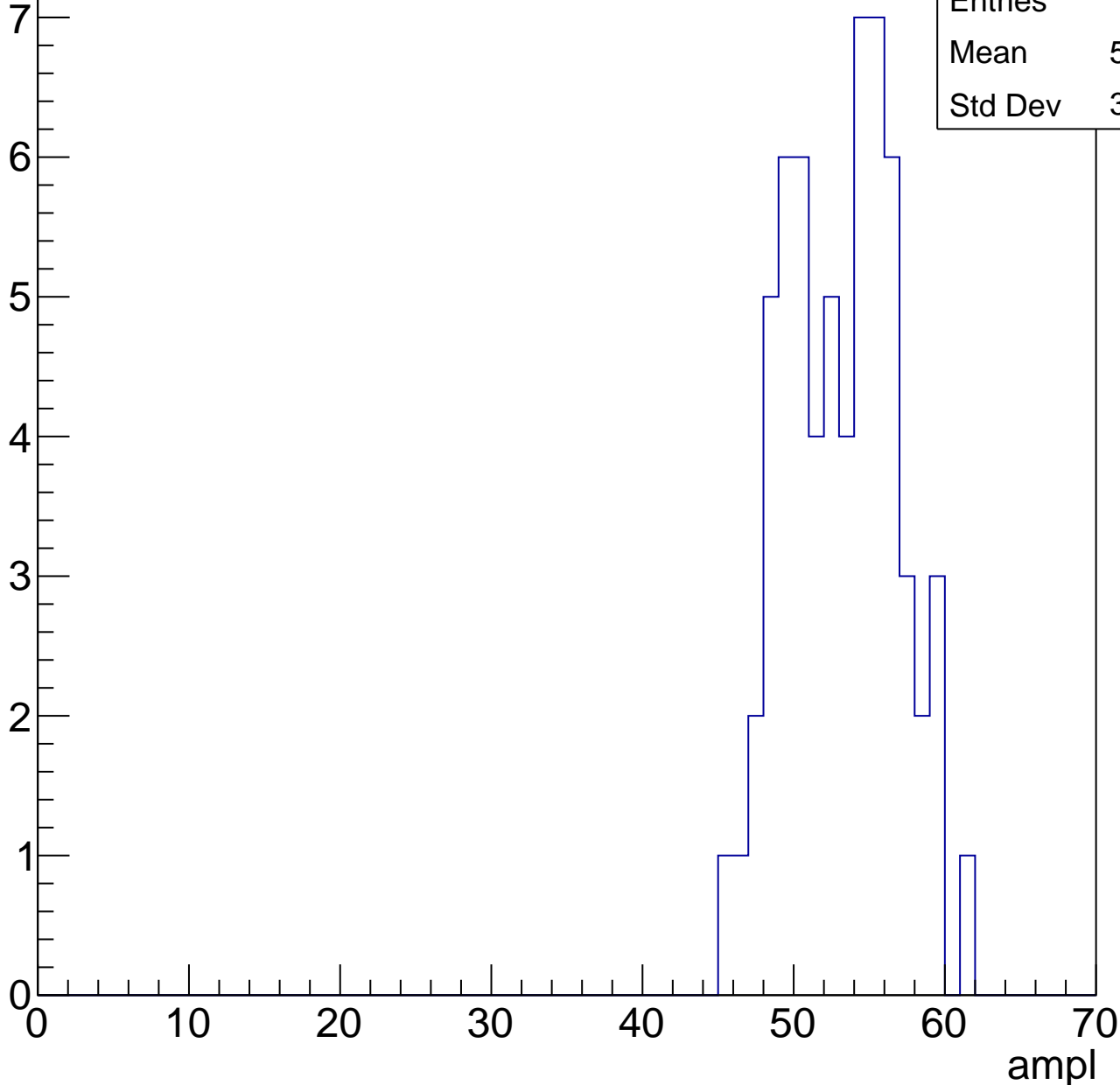


# B0L102S, U6-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

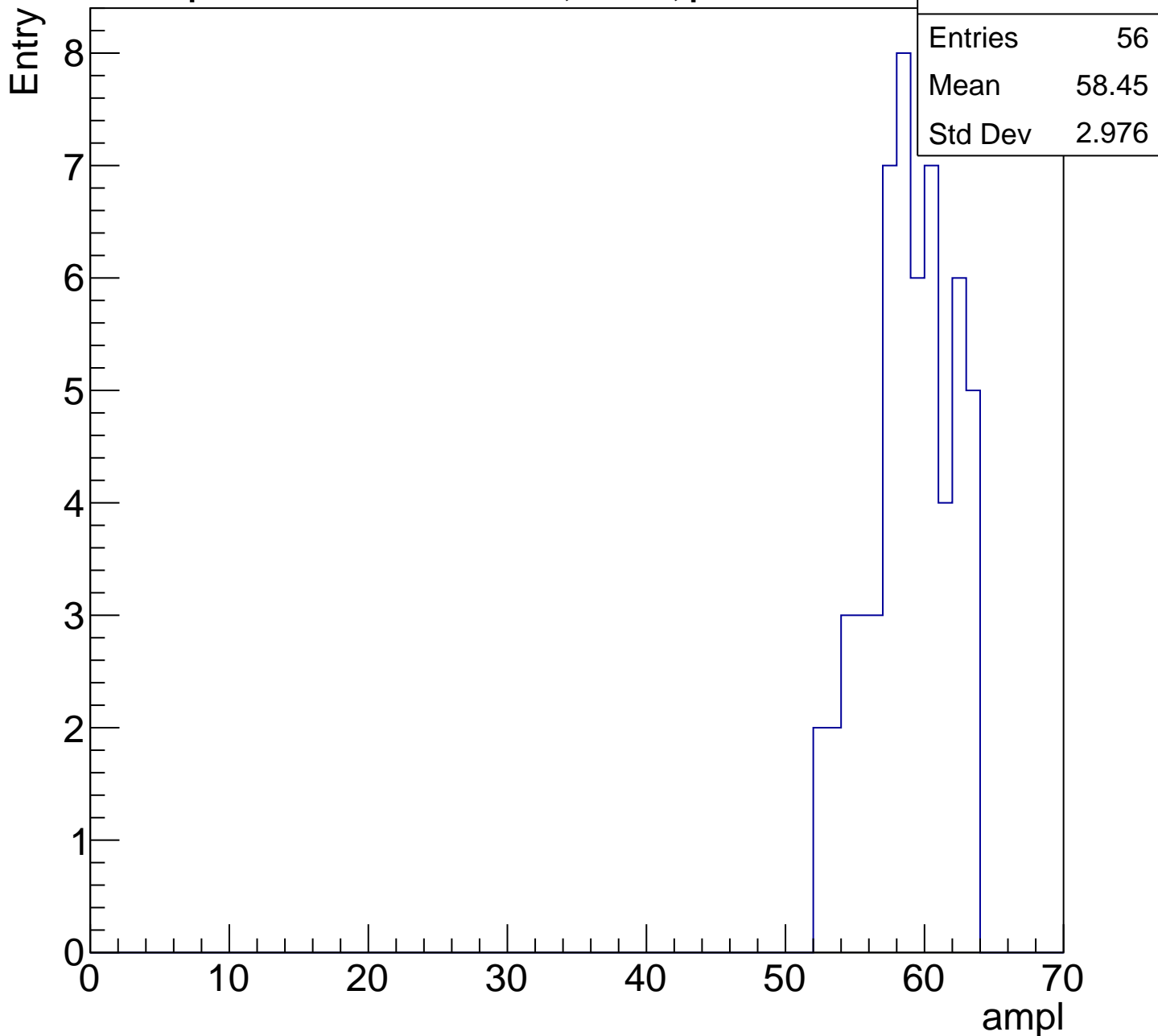
Entry

Entries	63
Mean	52.68
Std Dev	3.638



# B0L102S, U6-ch76, adc5

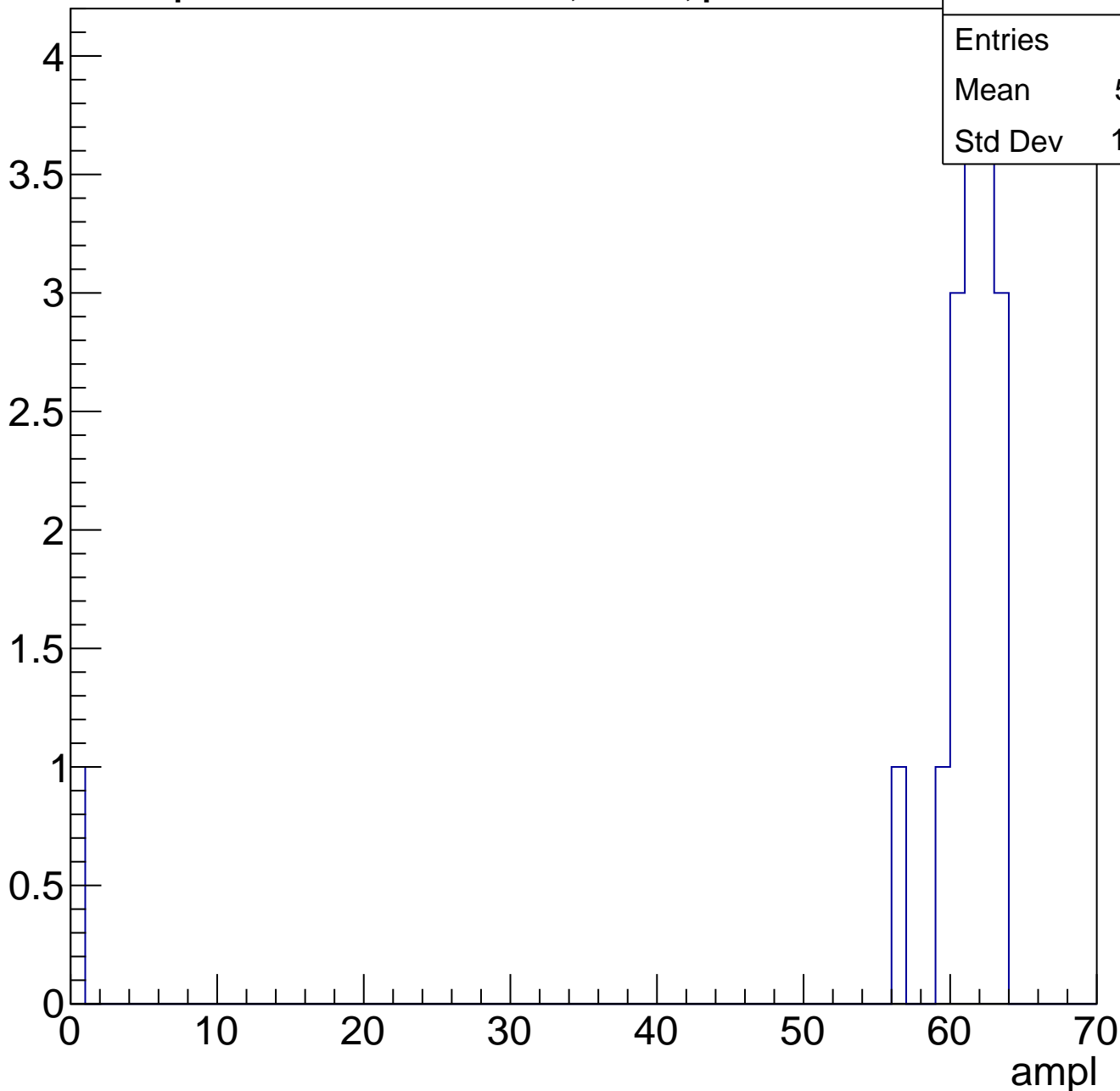
calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	96
Mean	28.24
Std Dev	4.745

**Gaus mean : 29.2290**

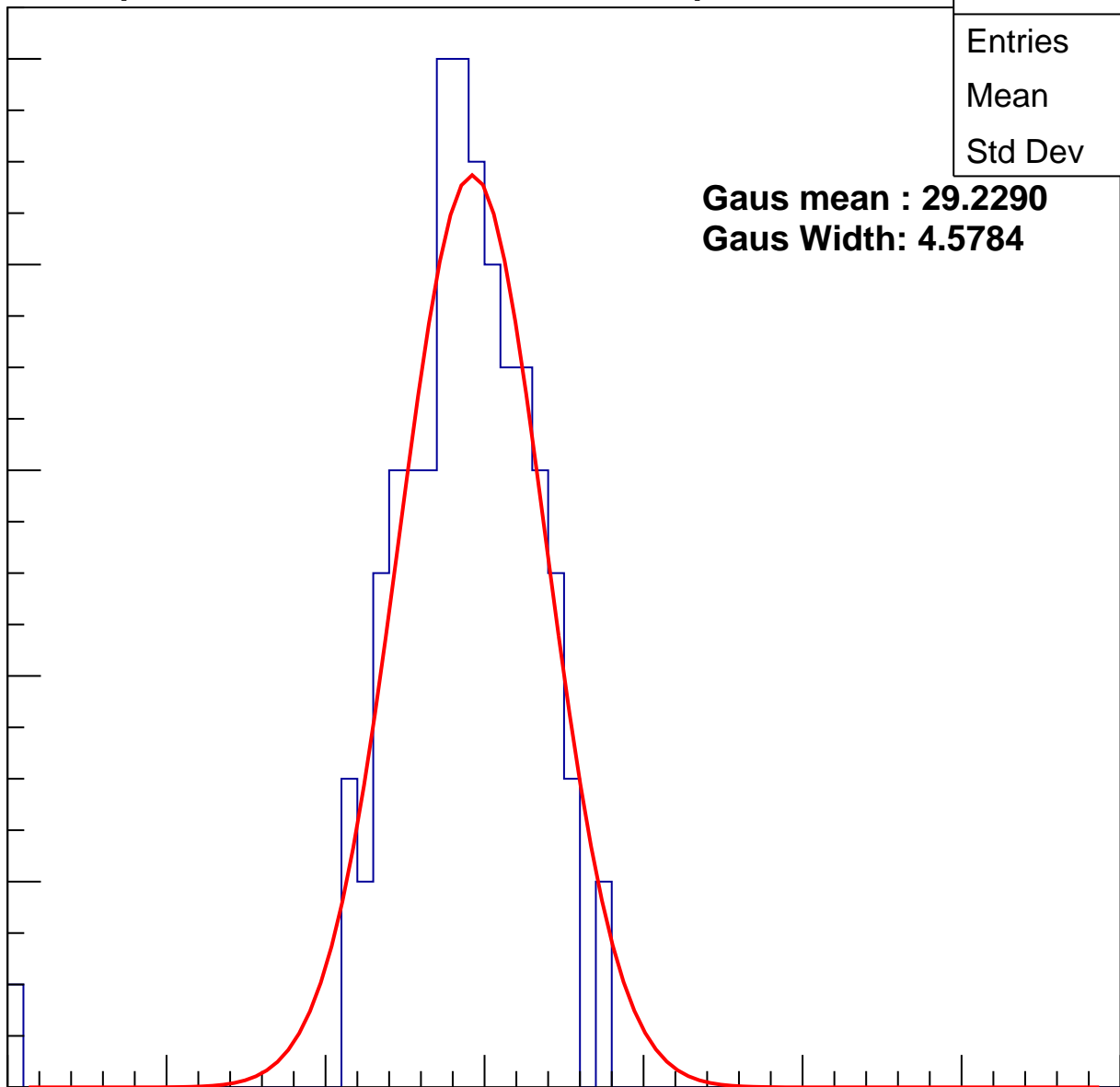
**Gaus Width: 4.5784**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch77, adc1

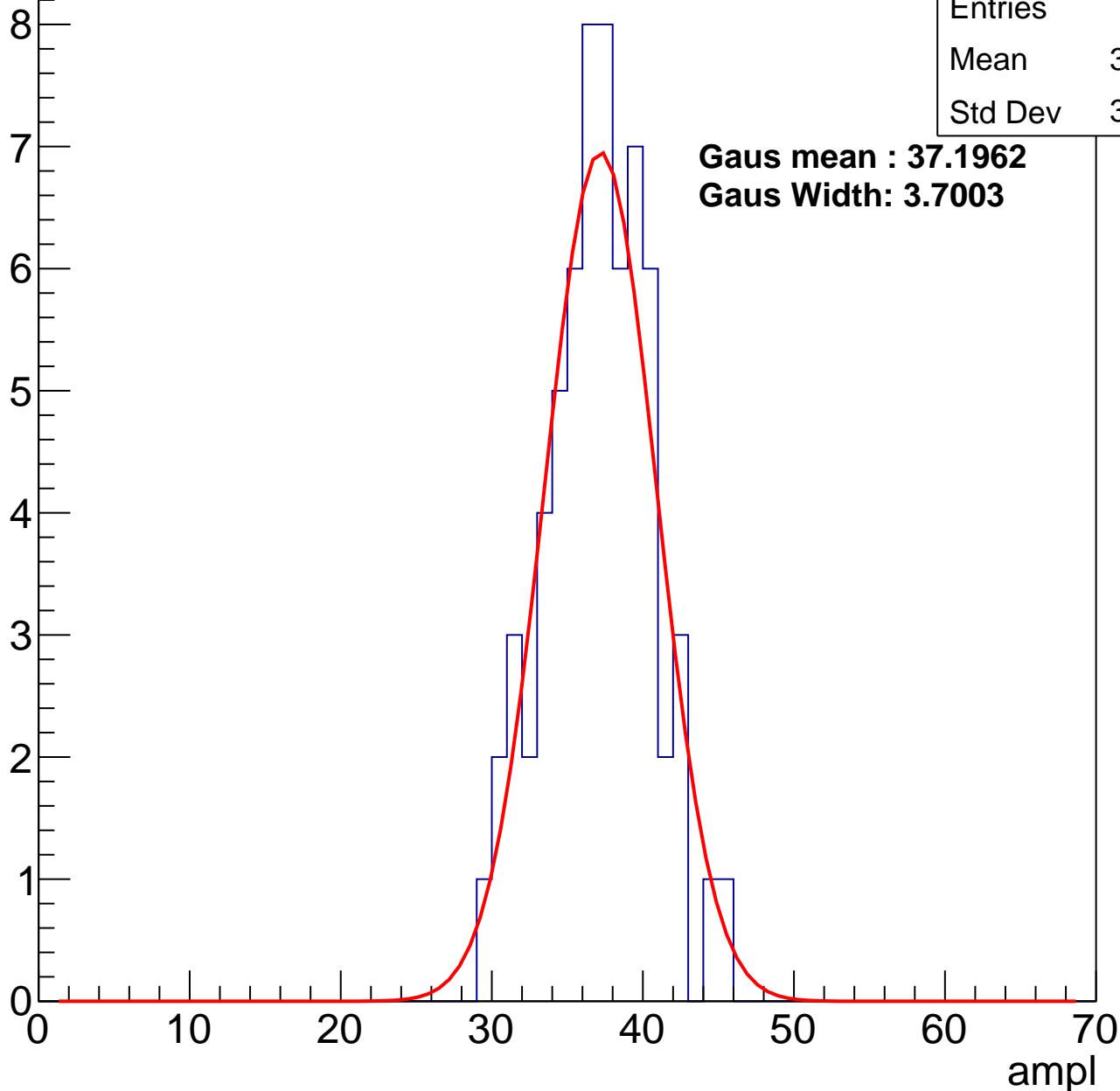
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36.62
Std Dev	3.418

**Gaus mean : 37.1962**

**Gaus Width: 3.7003**



# B0L102S, U6-ch77, adc2

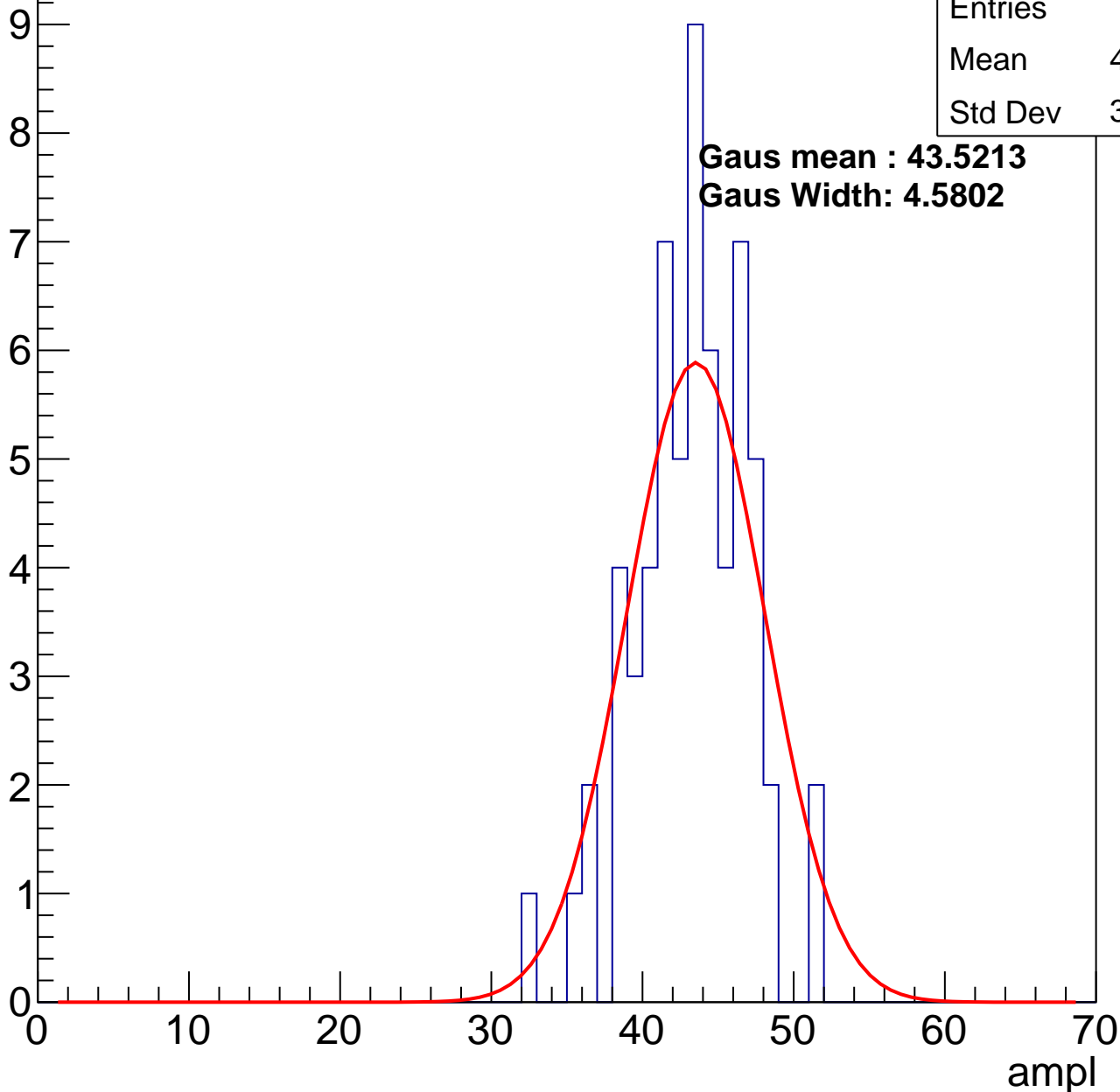
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.76
Std Dev	3.679

**Gaus mean : 43.5213**

**Gaus Width: 4.5802**

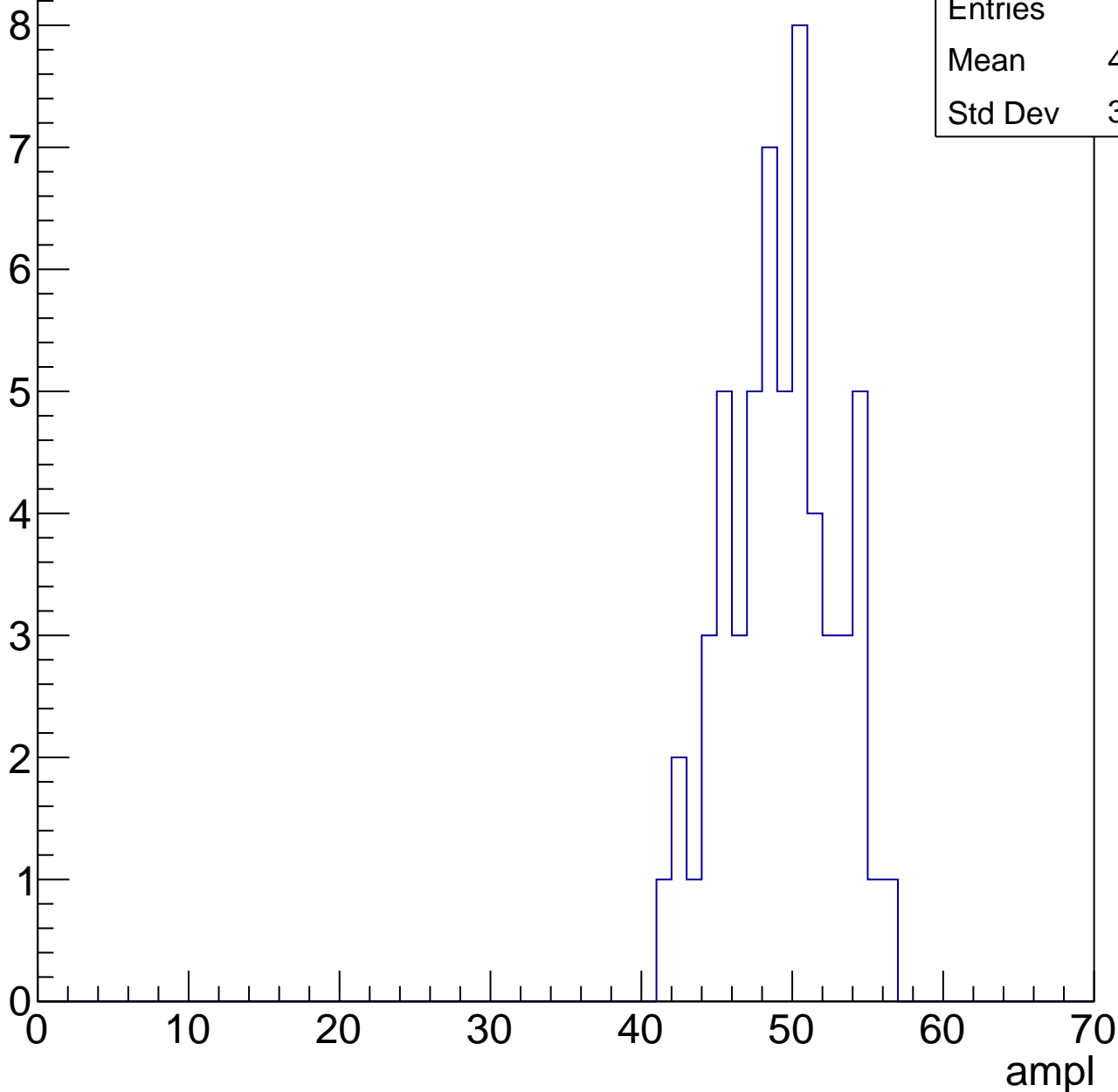


# B0L102S, U6-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	48.75
Std Dev	3.536

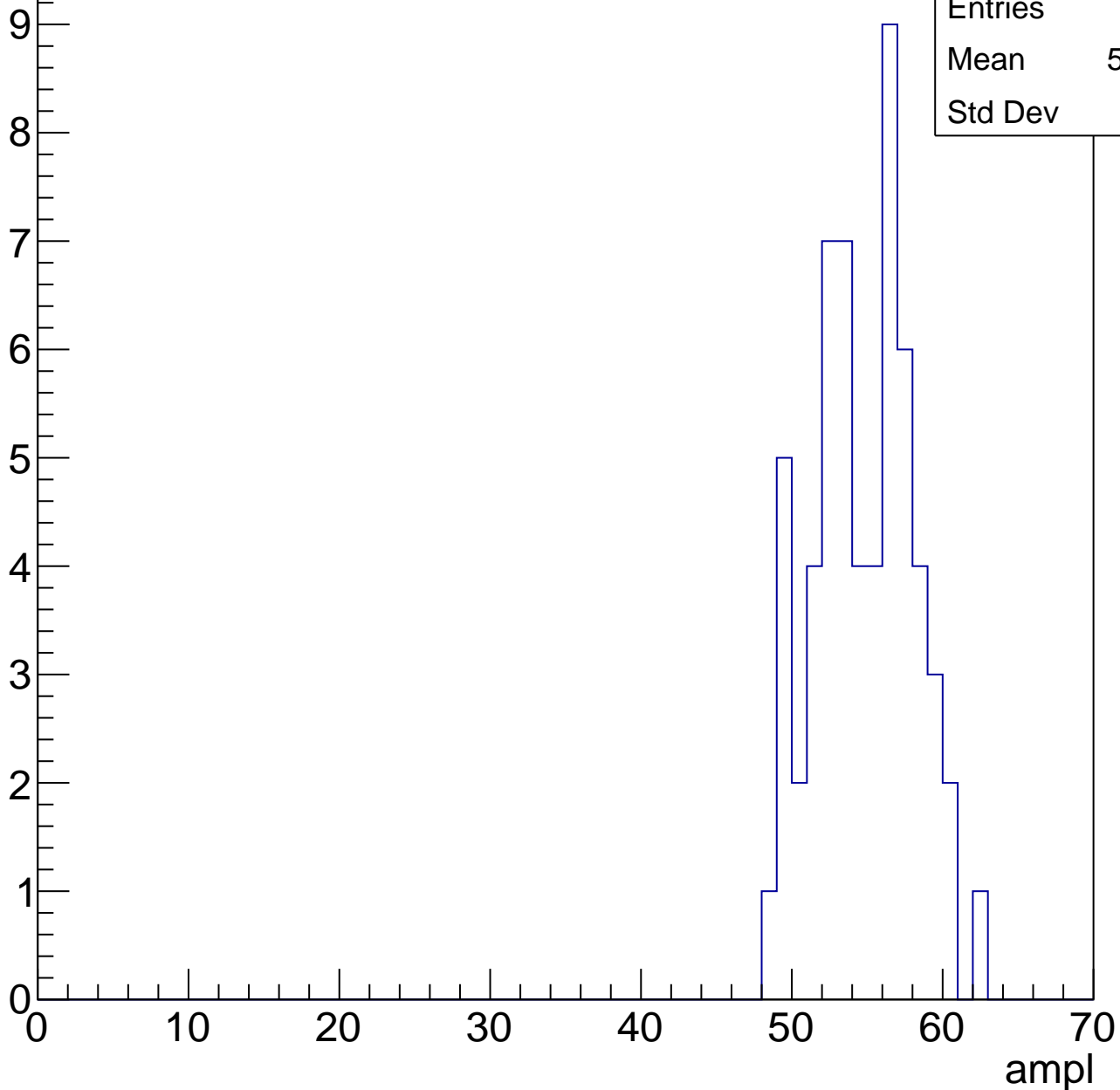


# B0L102S, U6-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	54.32
Std Dev	3.26

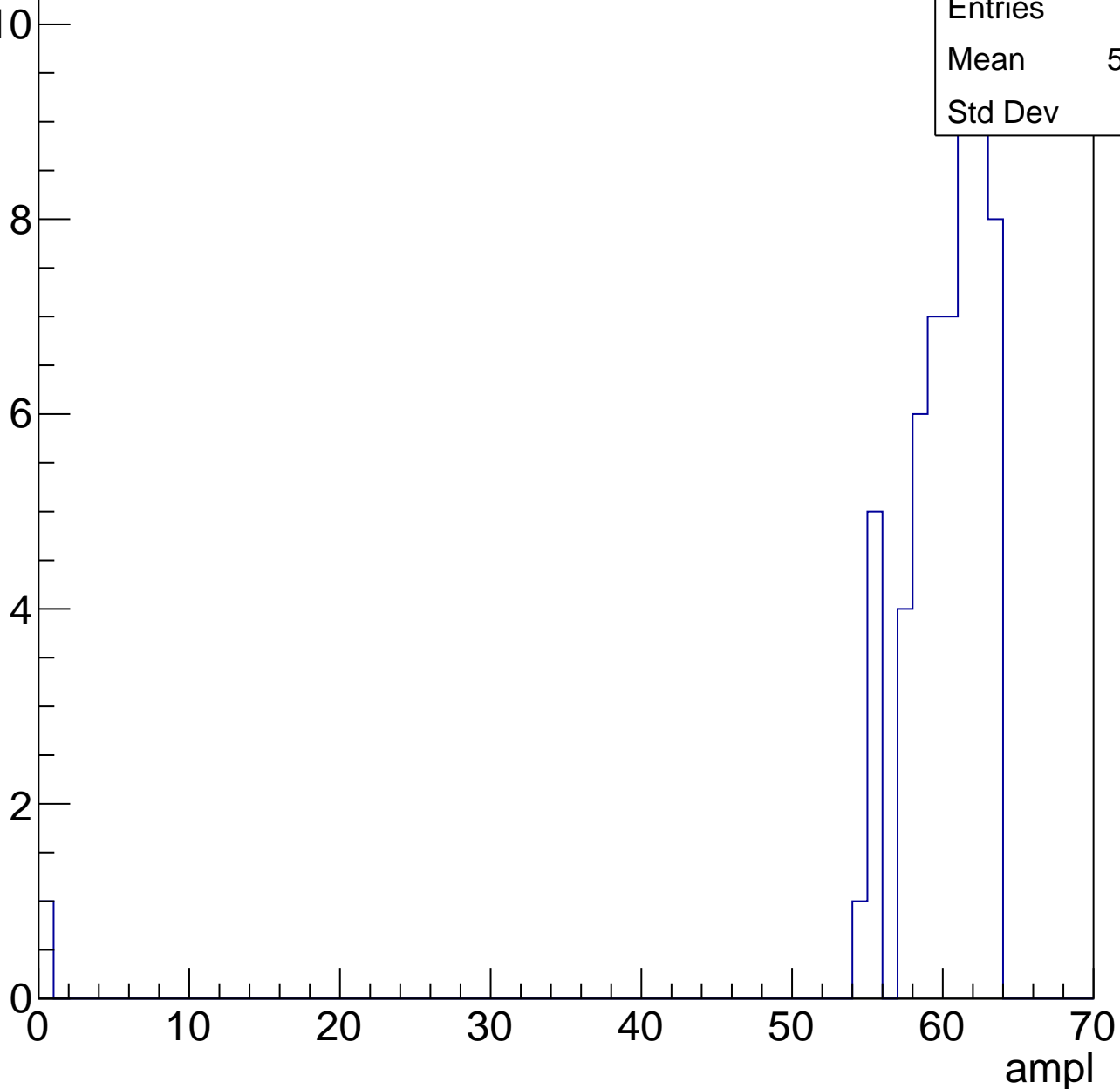


# B0L102S, U6-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	58.79
Std Dev	8.16



# B0L102S, U6-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

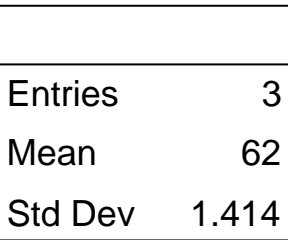
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62
Std Dev	1.414

0 10 20 30 40 50 60 70

ampl





# B0L102S, U6-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch78, adc0

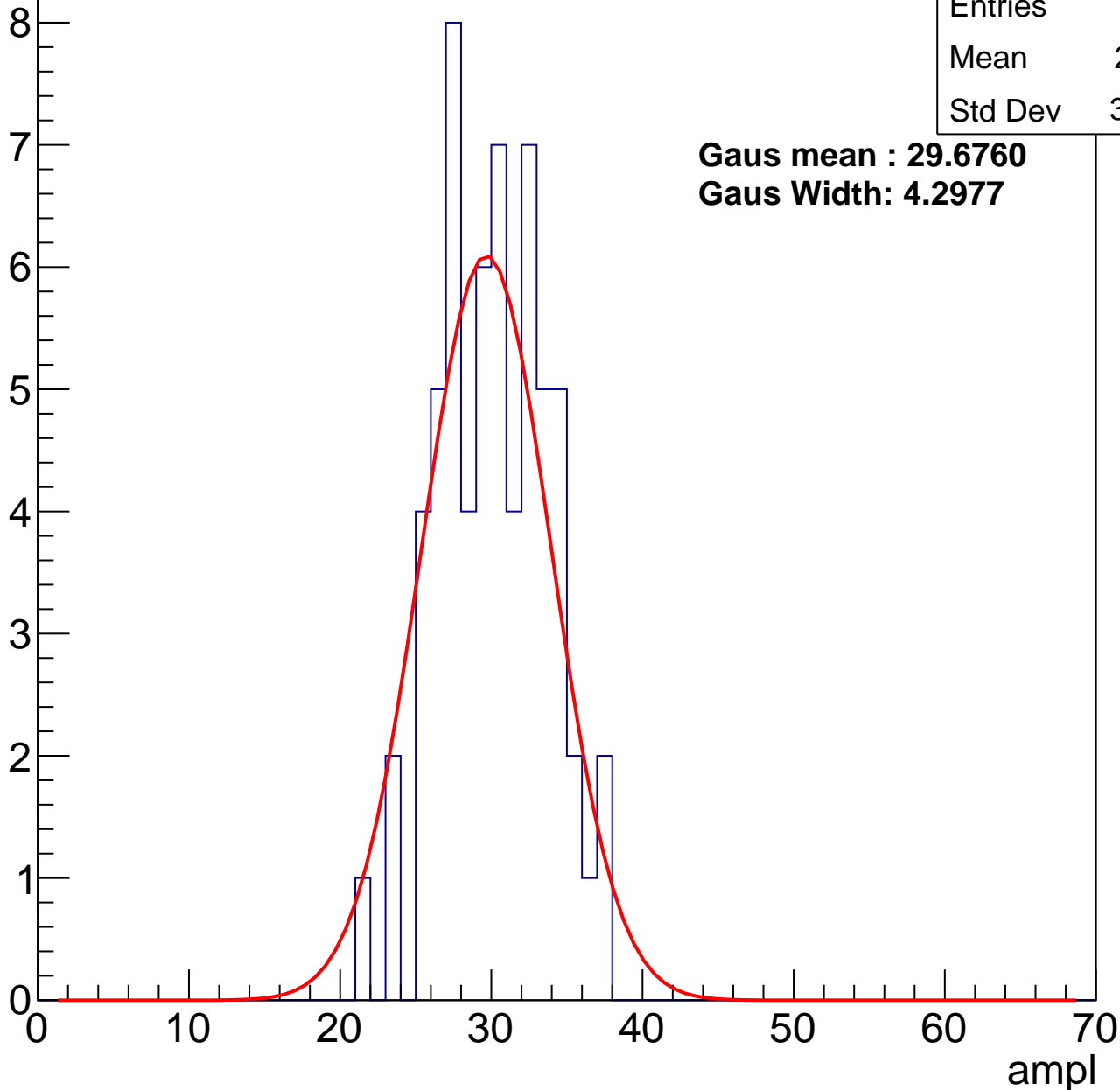
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	29.71
Std Dev	3.539

**Gaus mean : 29.6760**

**Gaus Width: 4.2977**



# B0L102S, U6-ch78, adc1

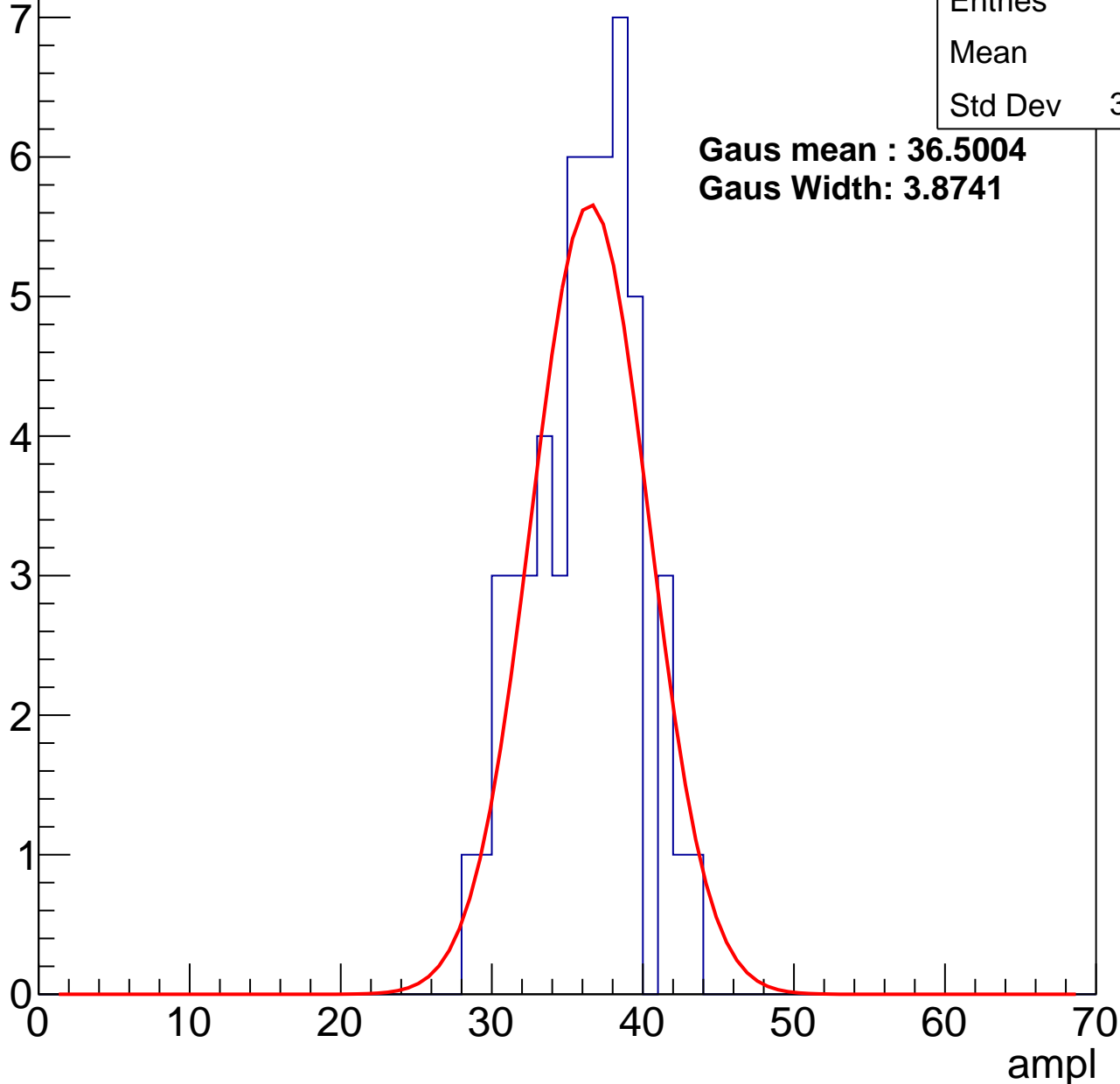
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	35.6
Std Dev	3.444

**Gaus mean : 36.5004**

**Gaus Width: 3.8741**



# B0L102S, U6-ch78, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	84
Mean	42.63
Std Dev	4.085

**Gaus mean : 43.5060**

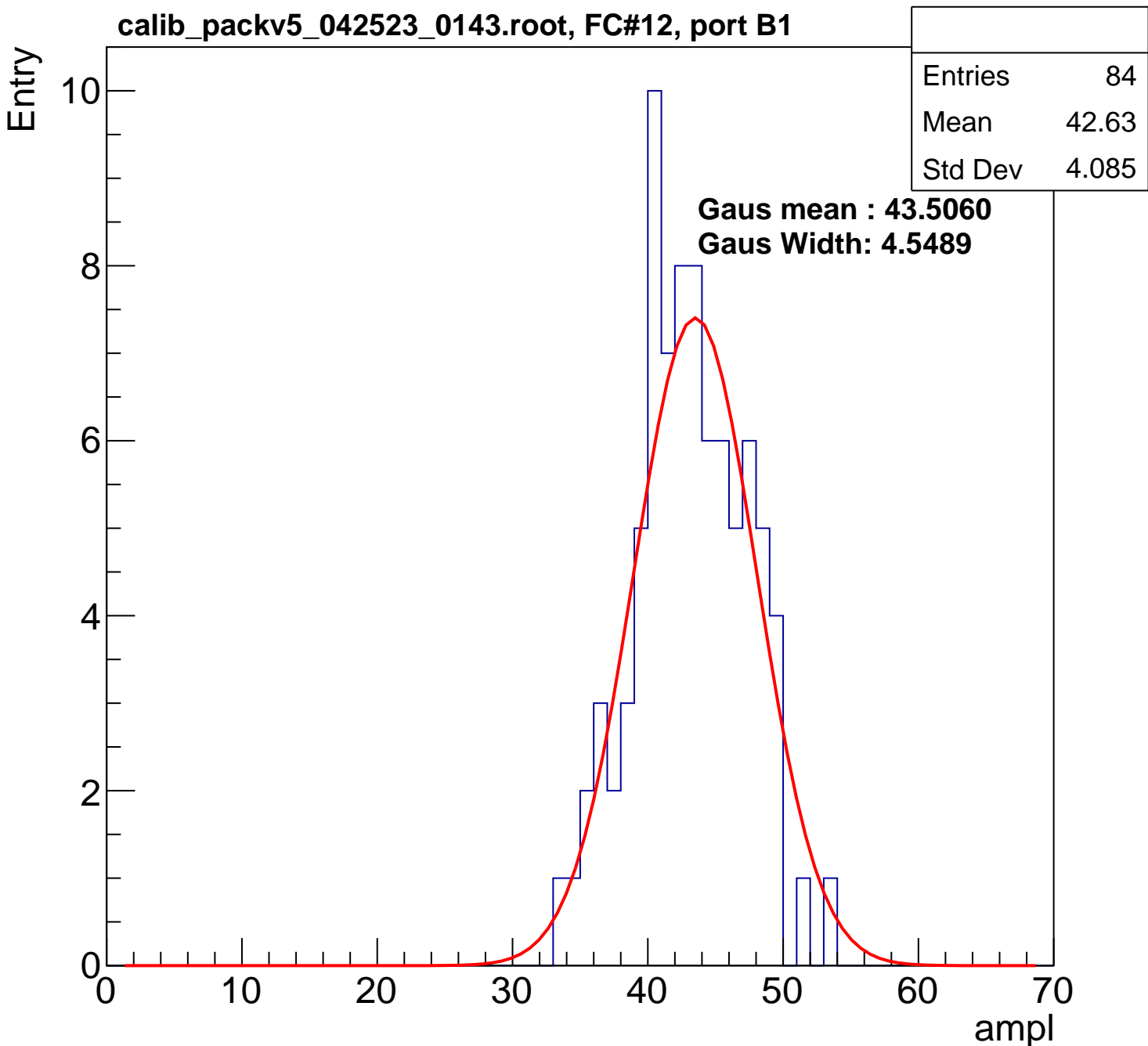
**Gaus Width: 4.5489**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

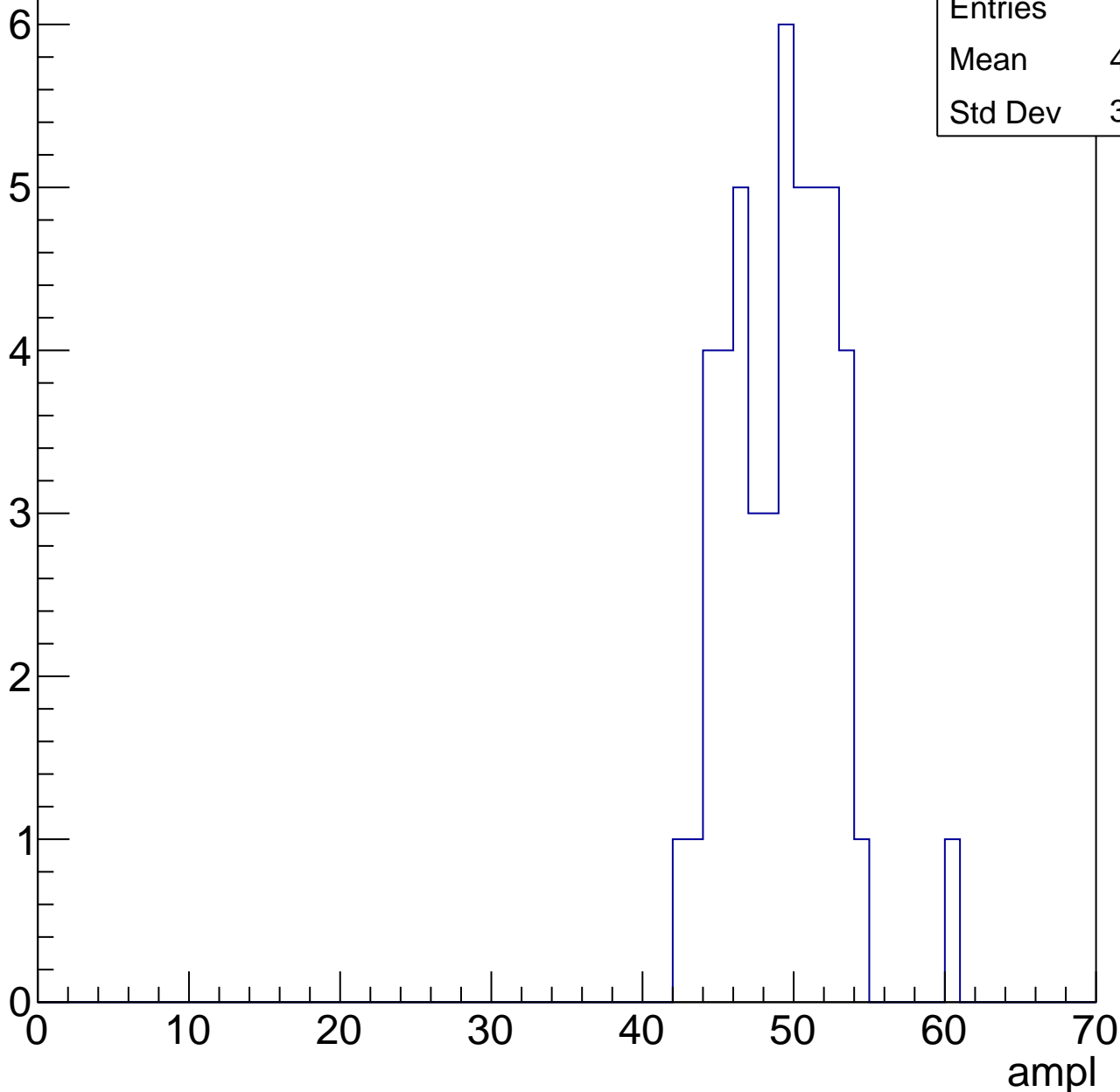


# B0L102S, U6-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	48.77
Std Dev	3.495

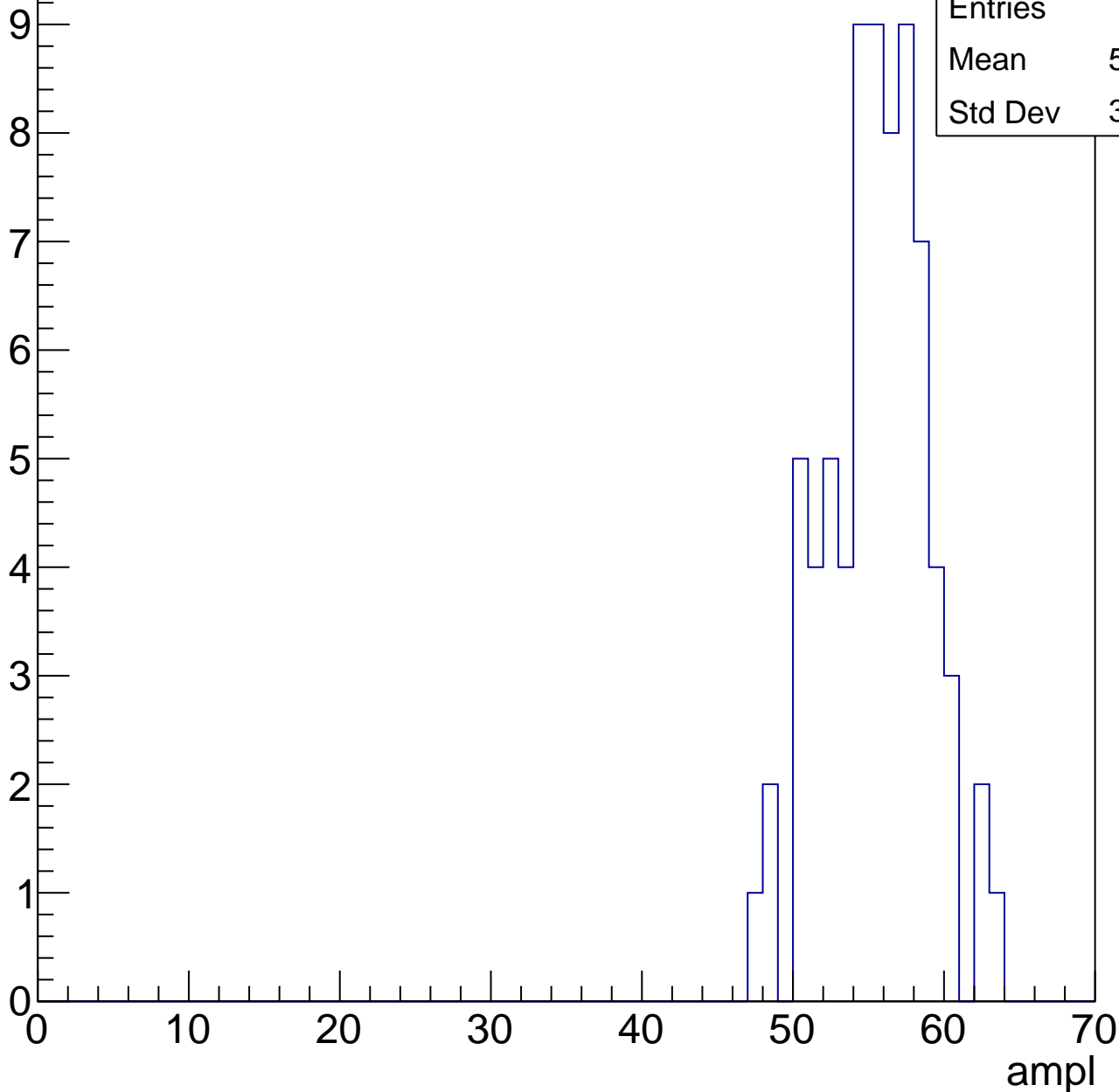


# B0L102S, U6-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	55.07
Std Dev	3.369



# B0L102S, U6-ch78, adc5

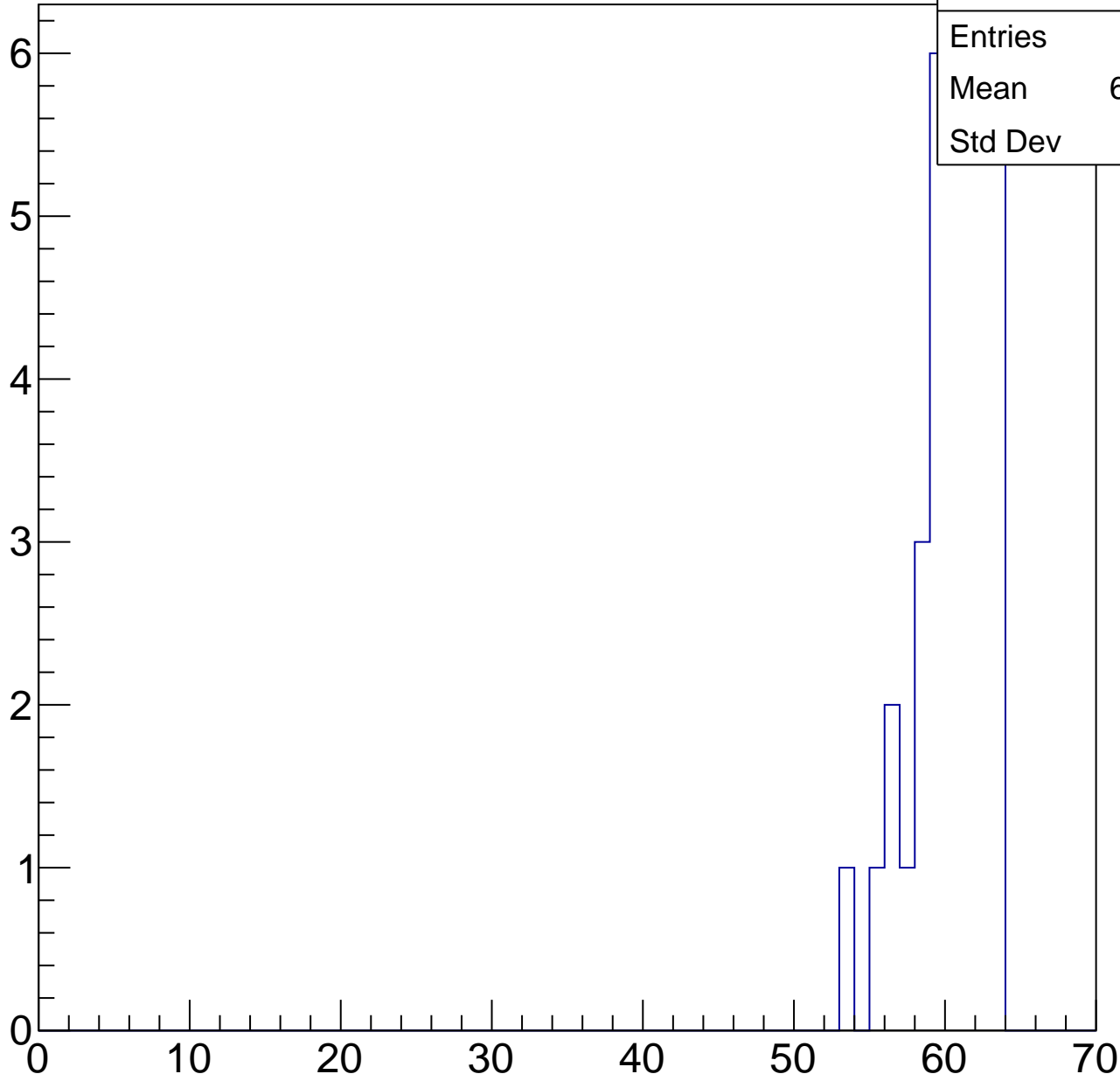
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

6  
5  
4  
3  
2  
1  
0

Entries	38
Mean	60.03
Std Dev	2.39

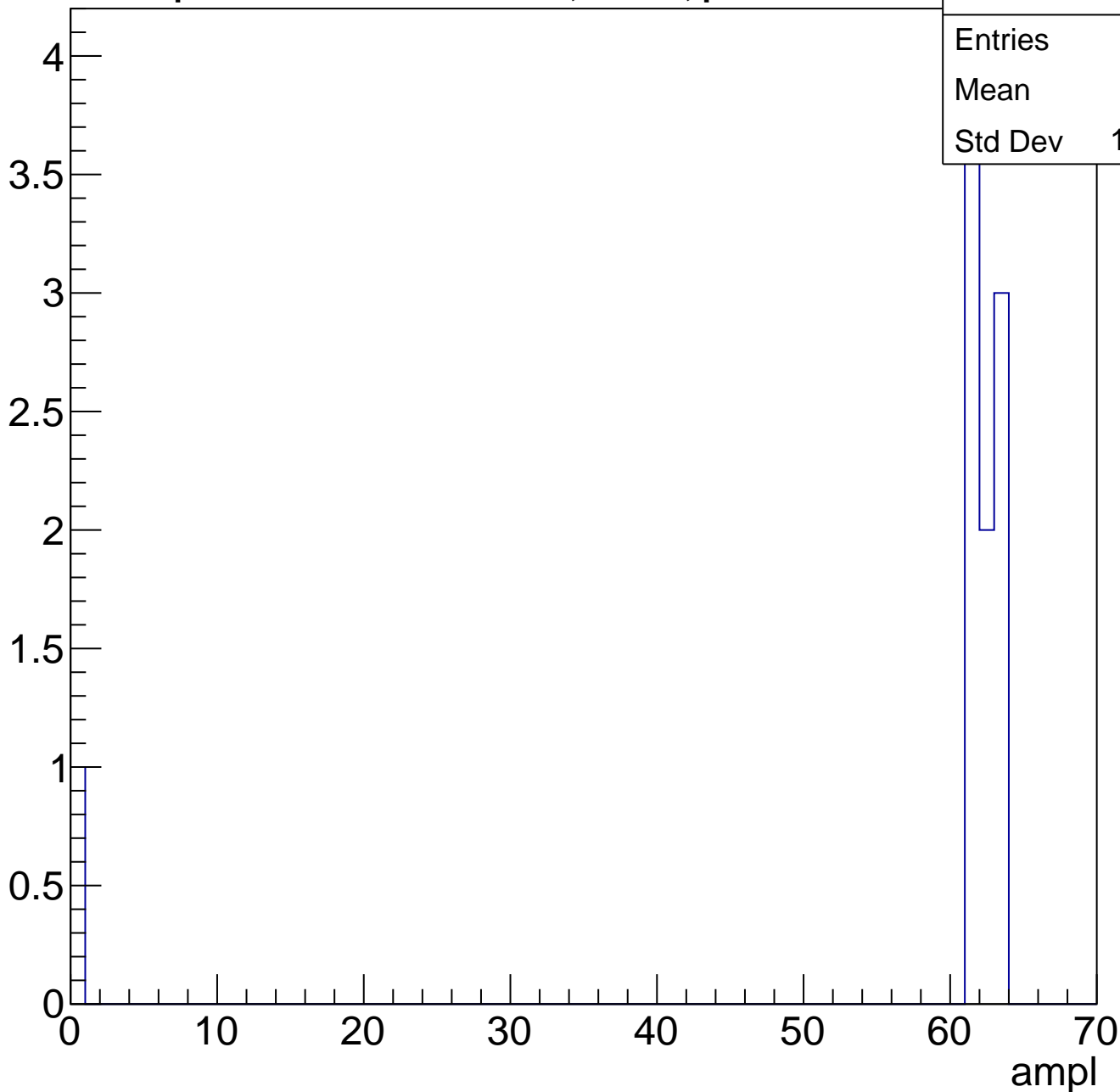
ampl



# B0L102S, U6-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch79, adc0

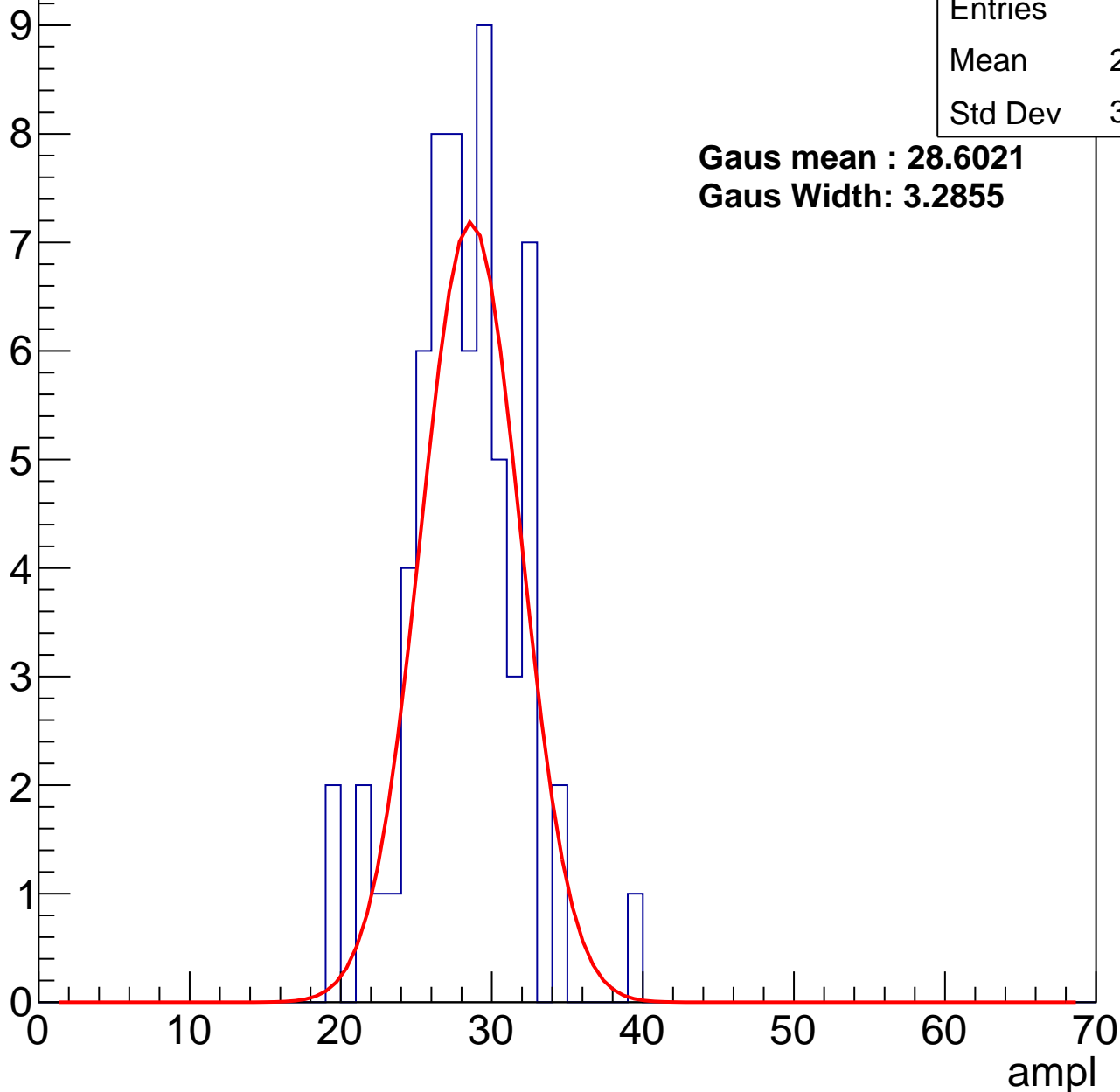
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	27.66
Std Dev	3.579

**Gaus mean : 28.6021**

**Gaus Width: 3.2855**



# B0L102S, U6-ch79, adc1

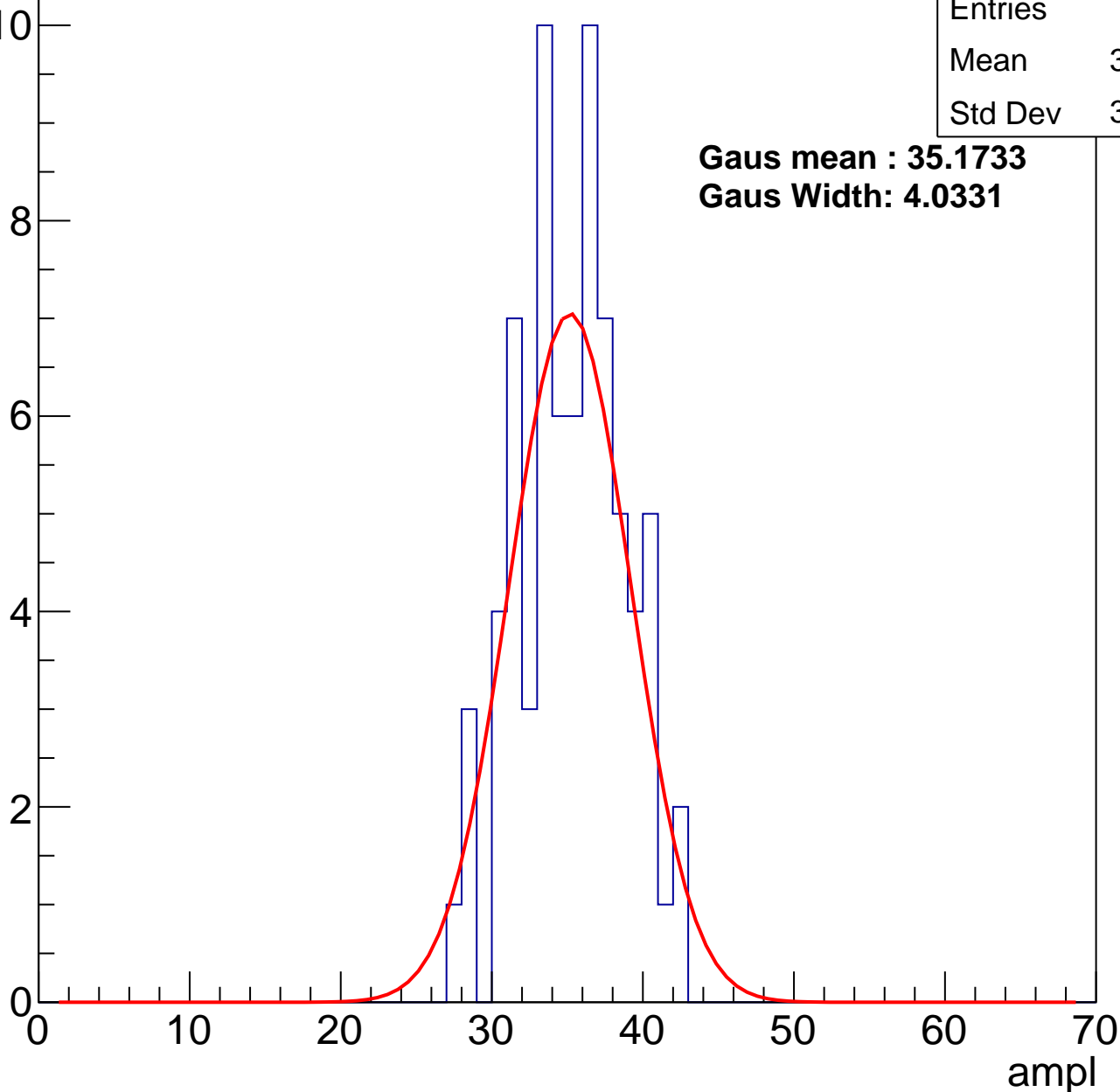
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	34.84
Std Dev	3.488

**Gaus mean : 35.1733**

**Gaus Width: 4.0331**



# B0L102S, U6-ch79, adc2

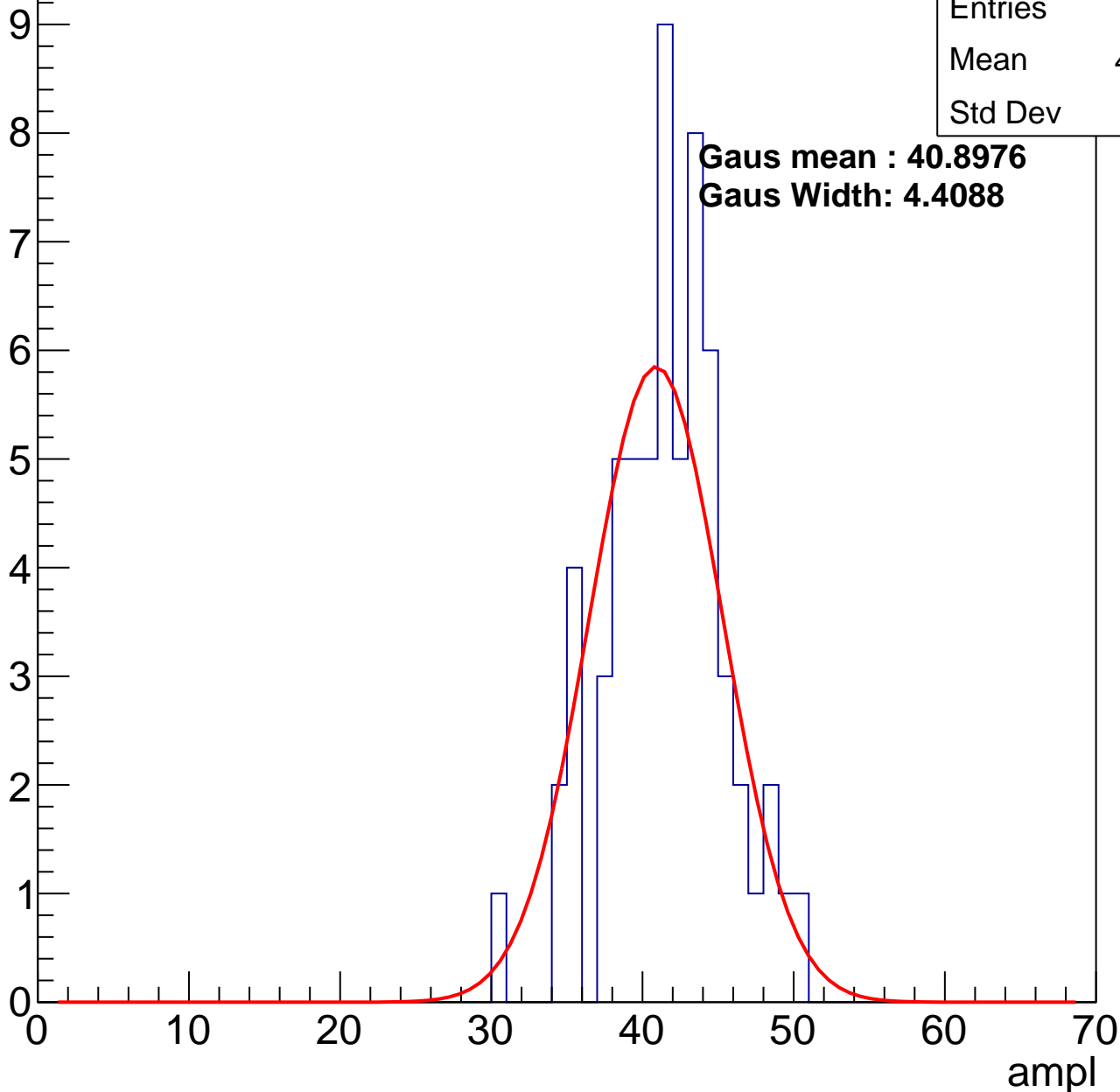
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	41.11
Std Dev	3.88

**Gaus mean : 40.8976**

**Gaus Width: 4.4088**

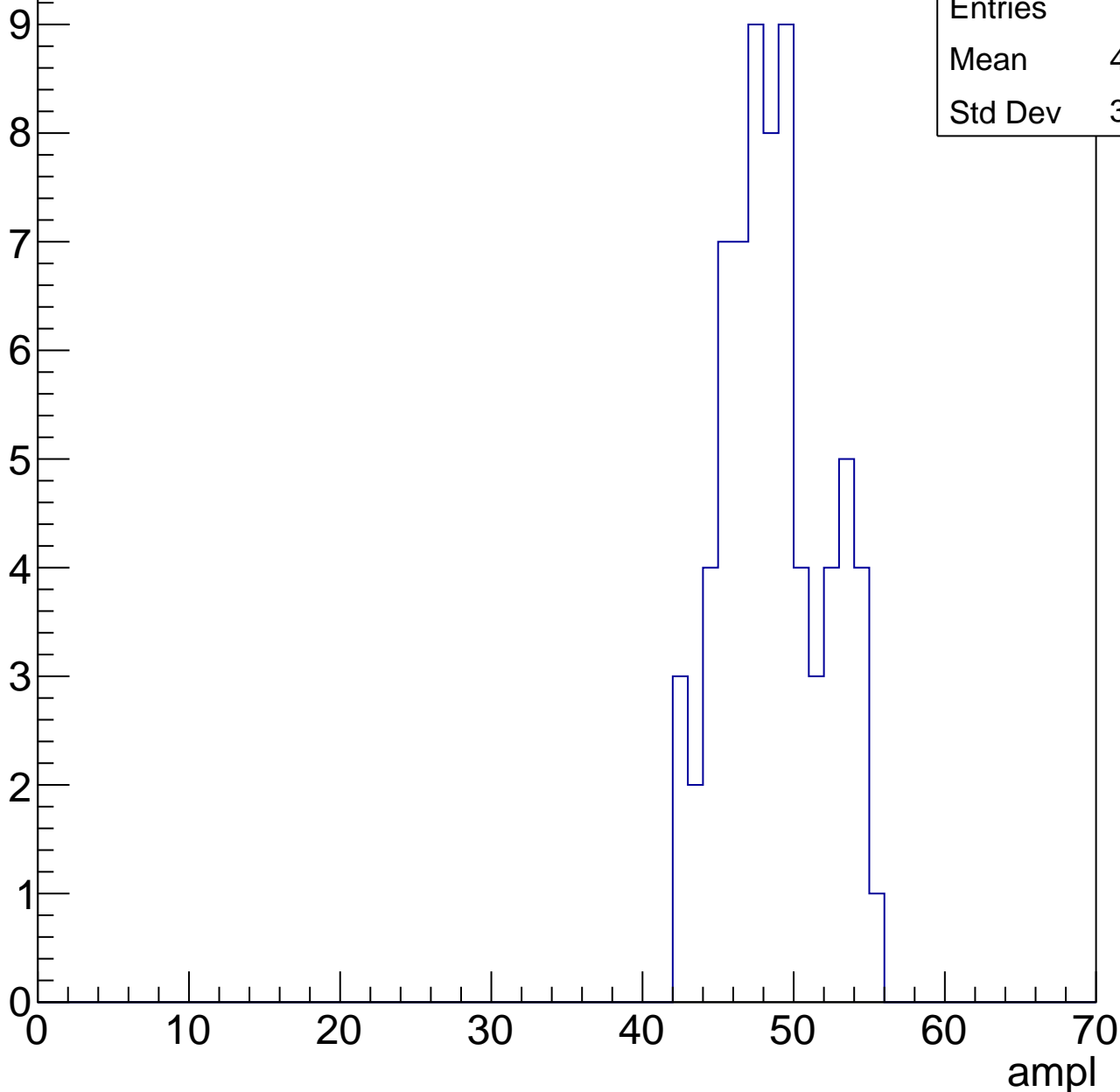


# B0L102S, U6-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	48.14
Std Dev	3.283

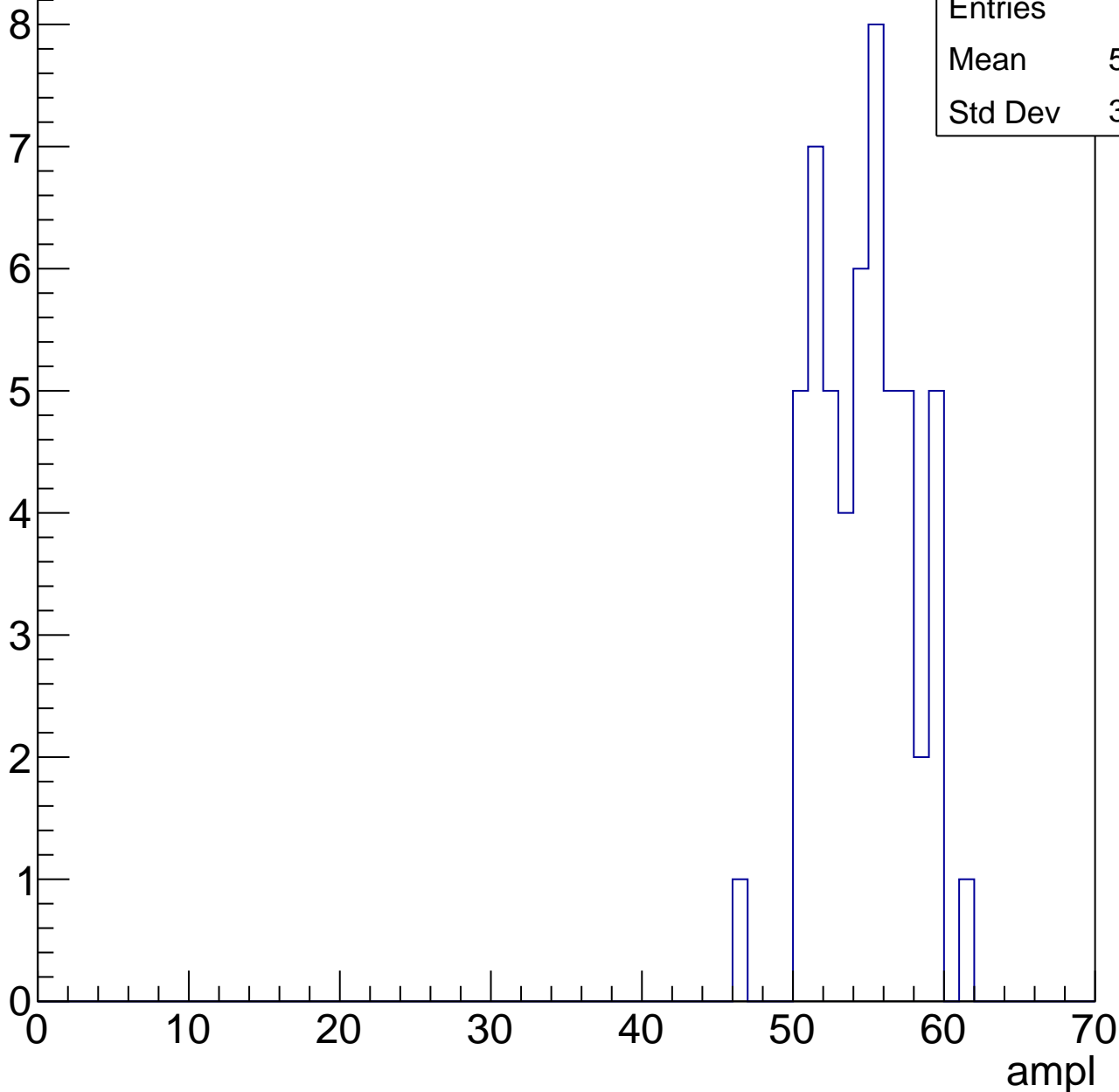


# B0L102S, U6-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	54.19
Std Dev	3.068



# B0L102S, U6-ch79, adc5

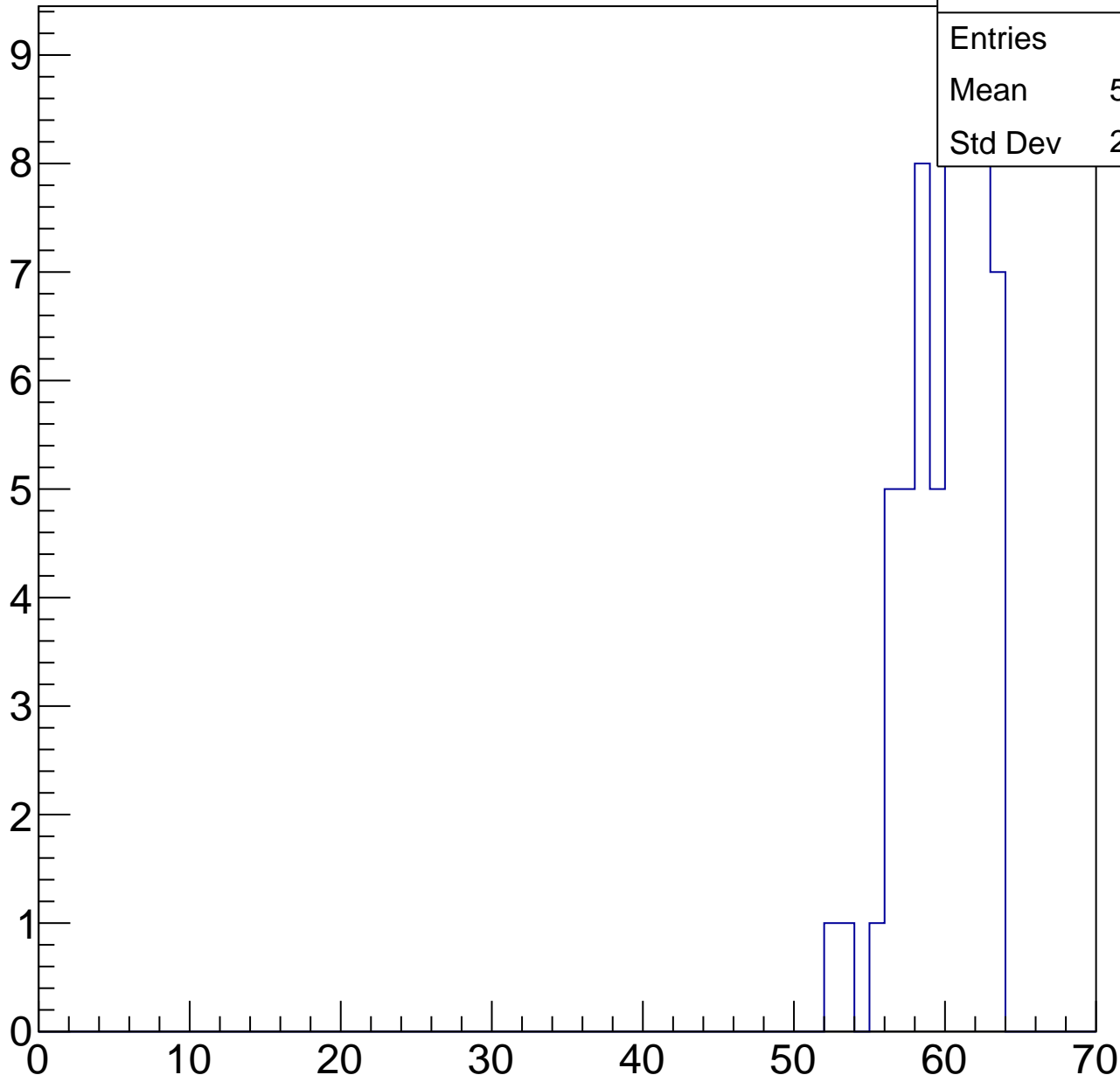
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	59.53
Std Dev	2.566

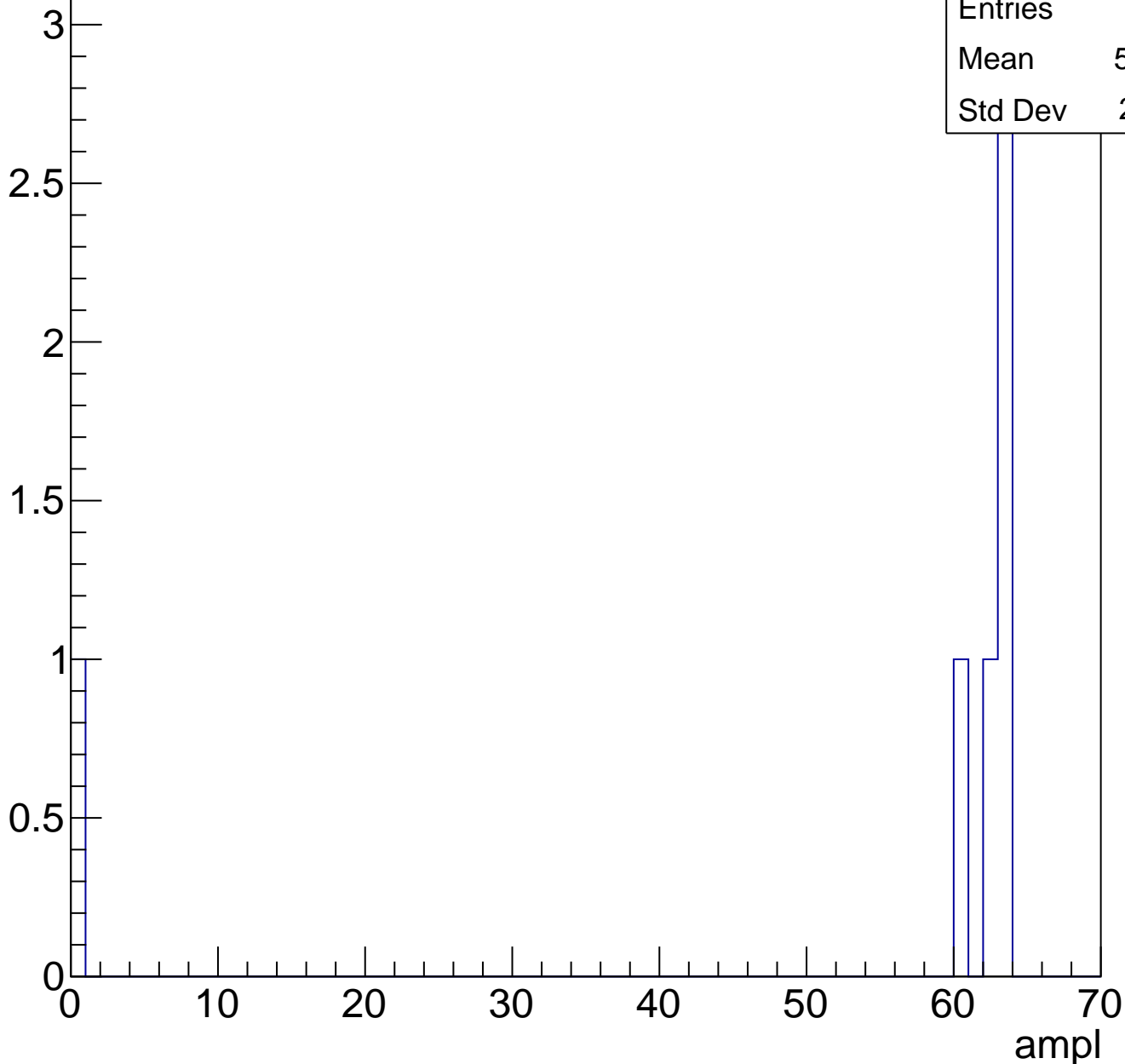
ampl



# B0L102S, U6-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch80, adc0

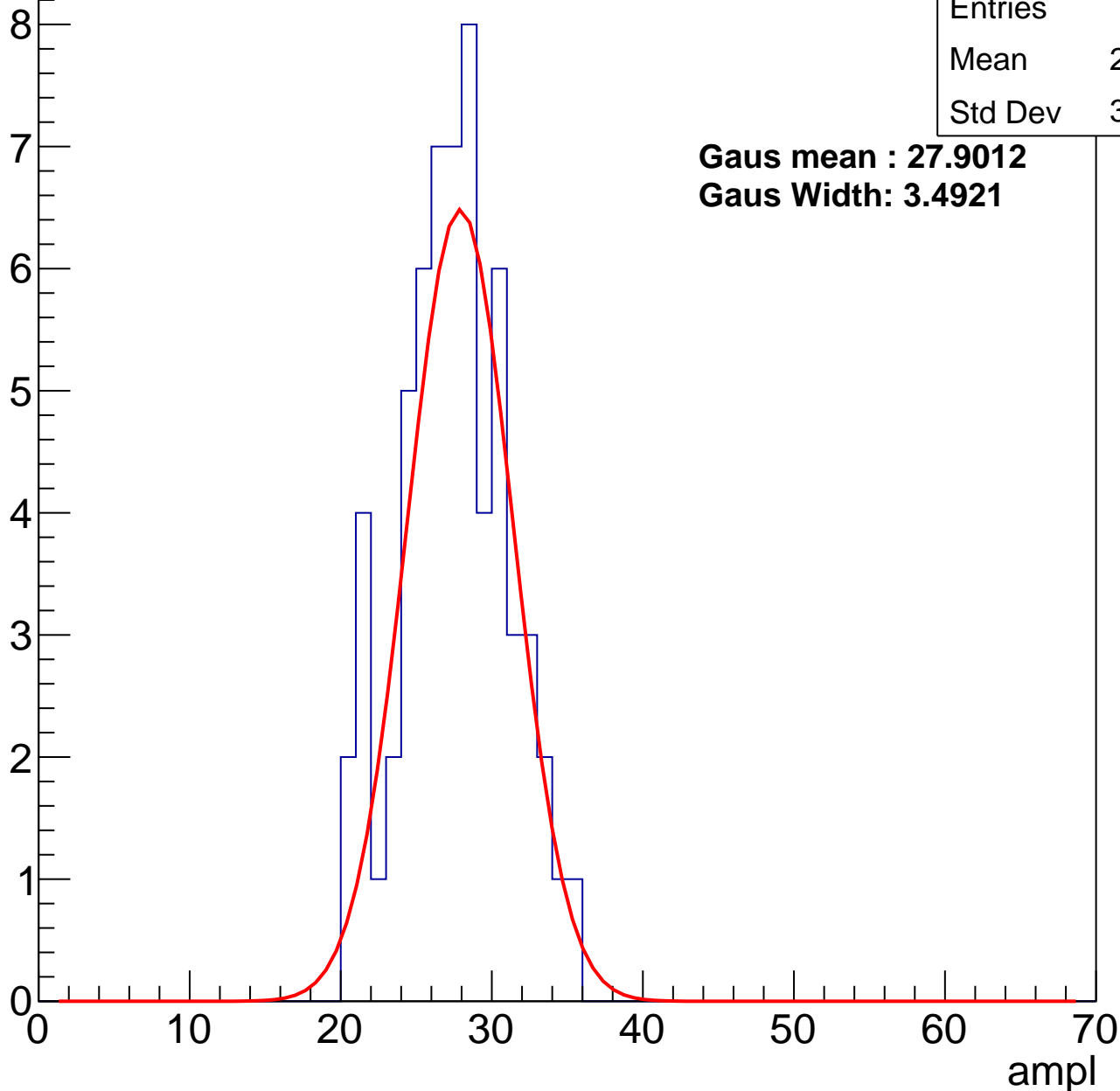
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	27.05
Std Dev	3.503

**Gaus mean : 27.9012**

**Gaus Width: 3.4921**



# B0L102S, U6-ch80, adc1

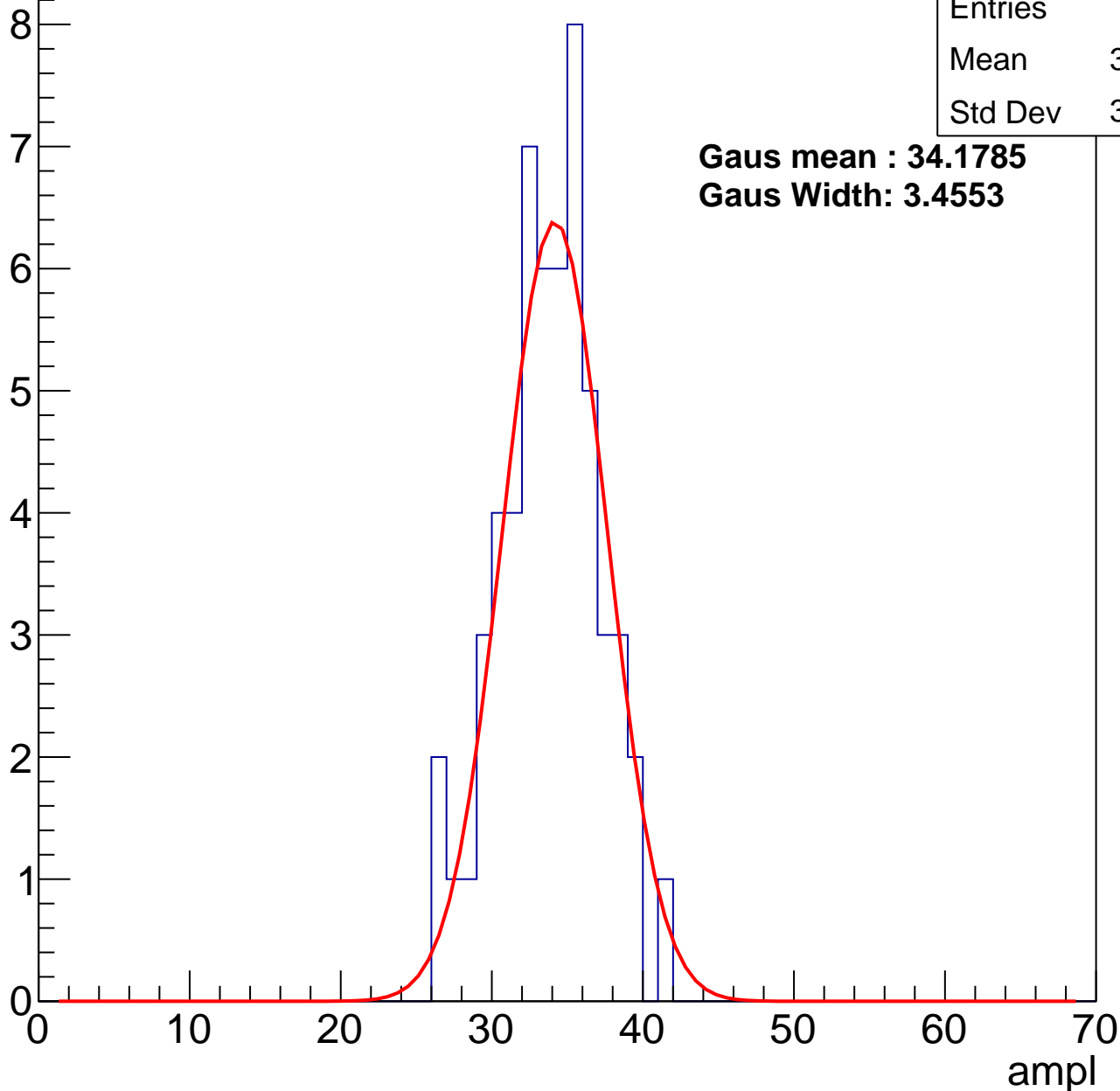
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	33.36
Std Dev	3.276

**Gaus mean : 34.1785**

**Gaus Width: 3.4553**



# B0L102S, U6-ch80, adc2

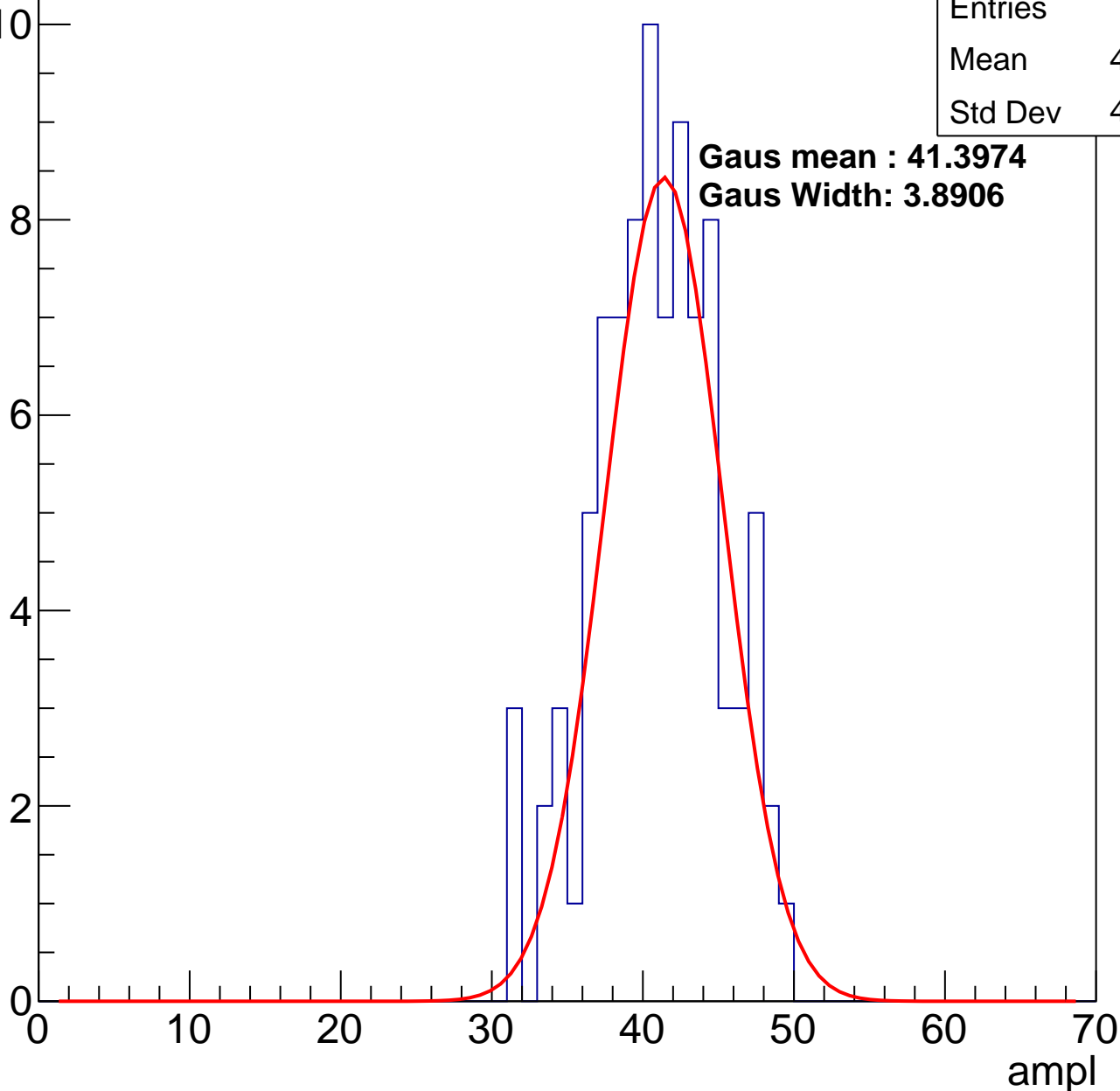
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	91
Mean	40.48
Std Dev	4.064

**Gaus mean : 41.3974**

**Gaus Width: 3.8906**

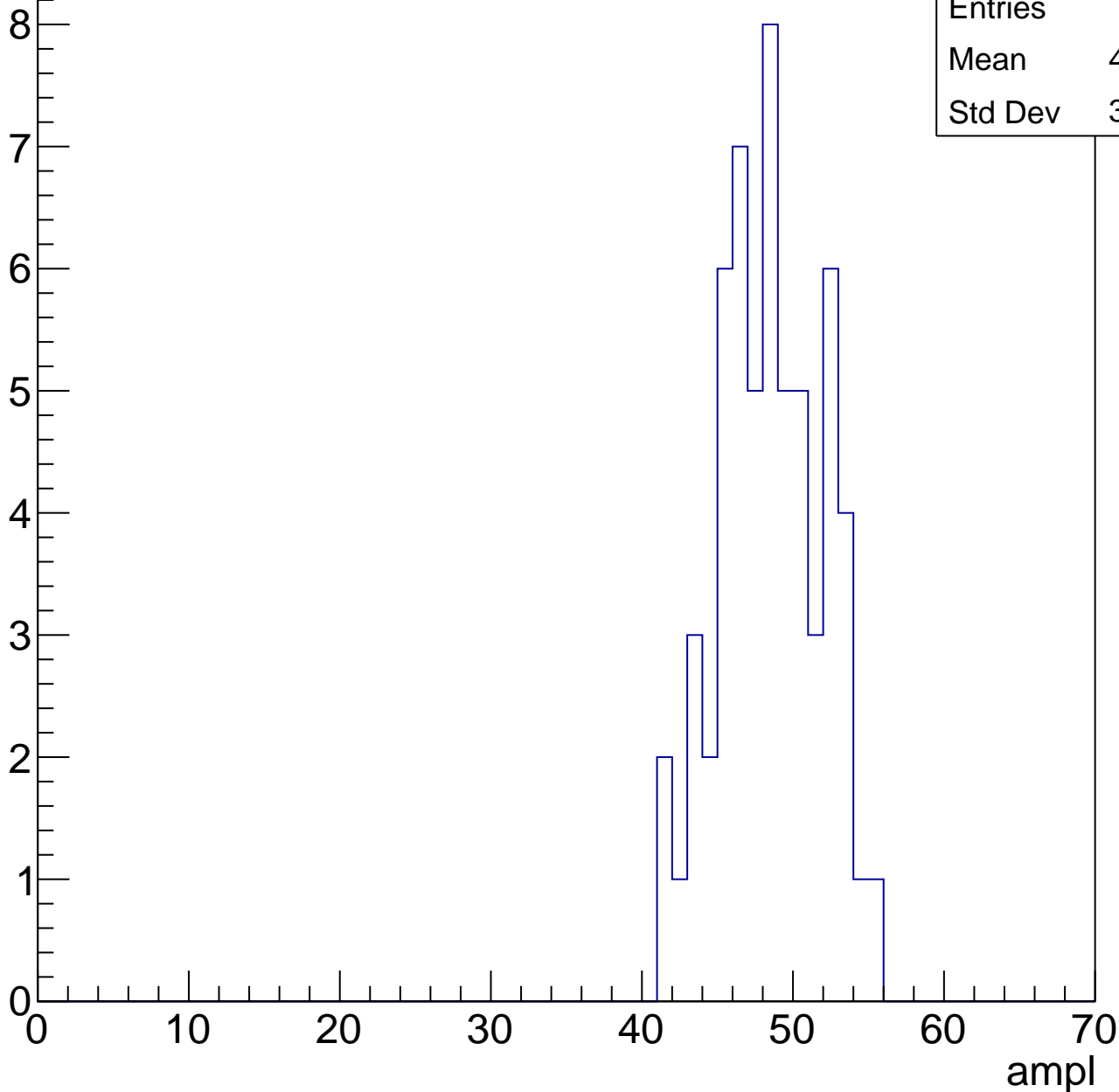


# B0L102S, U6-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	48.02
Std Dev	3.347

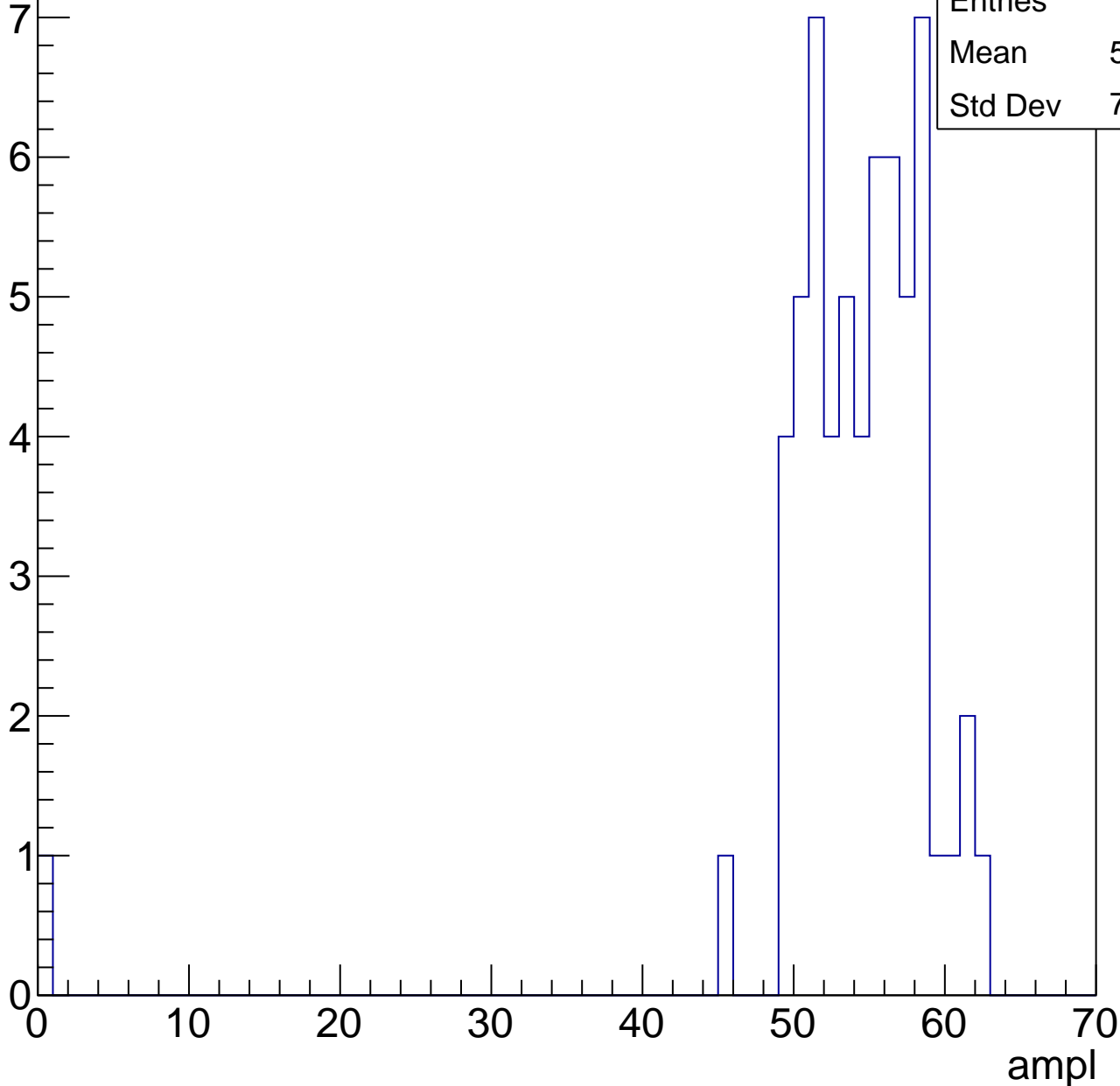


# B0L102S, U6-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	53.28
Std Dev	7.789



# B0L102S, U6-ch80, adc5

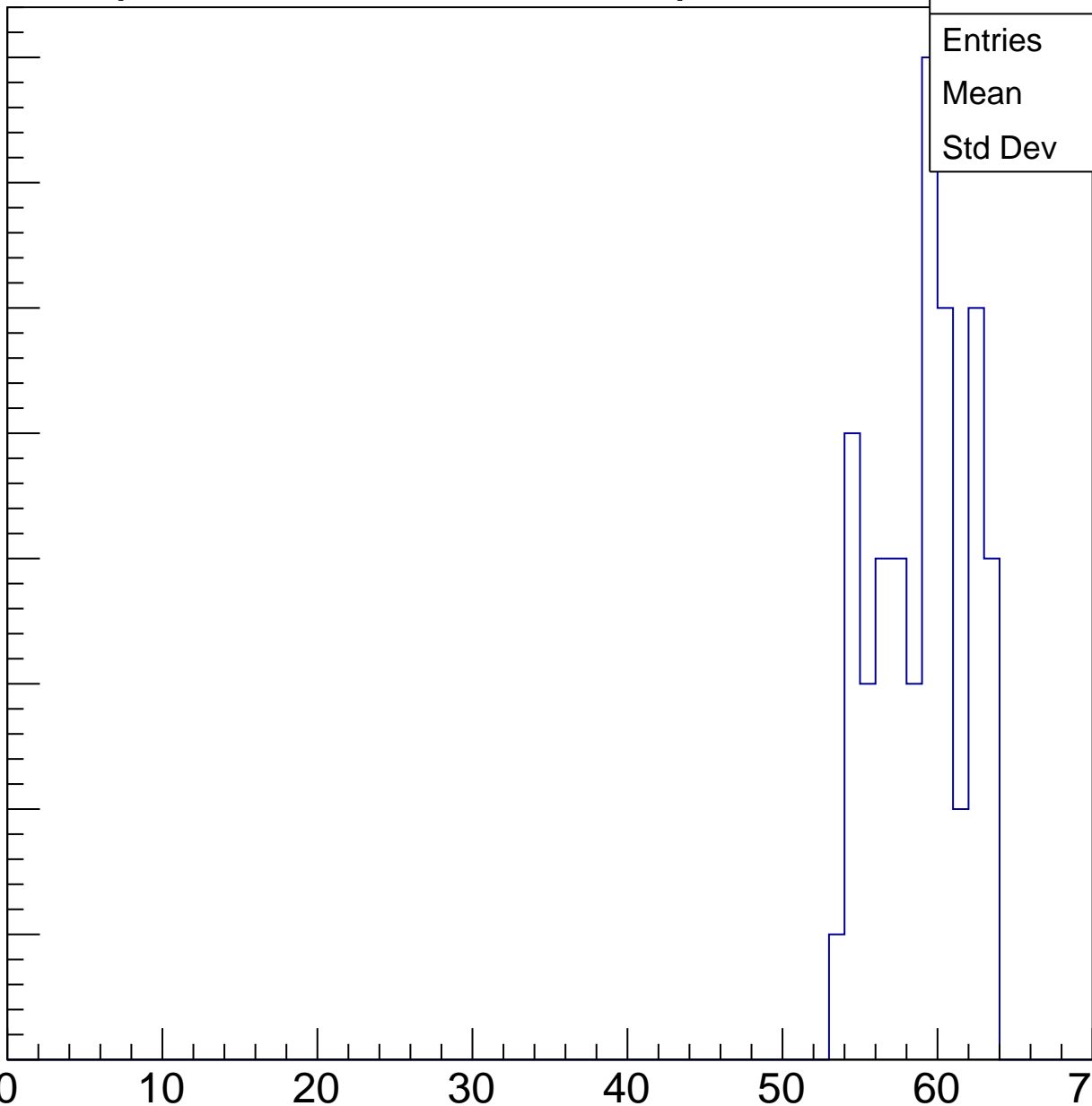
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	58.52
Std Dev	2.895

ampl

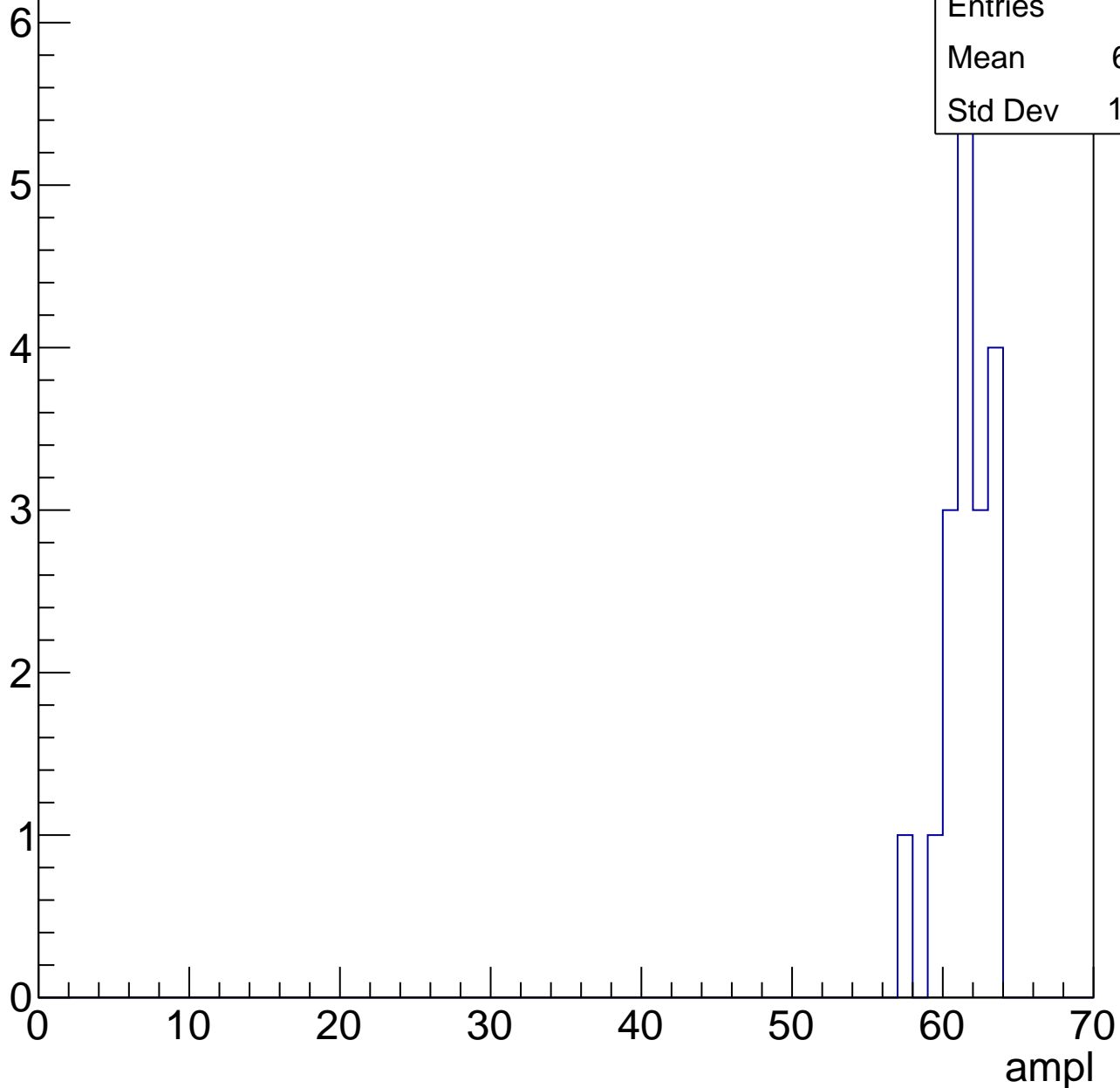


# B0L102S, U6-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	18
Mean	61.11
Std Dev	1.523





# B0L102S, U6-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	48
Std Dev	21.21

ampl

0 10 20 30 40 50 60 70

1

# B0L102S, U6-ch81, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	60
Mean	30.92
Std Dev	5.021

**Gaus mean : 31.3028**

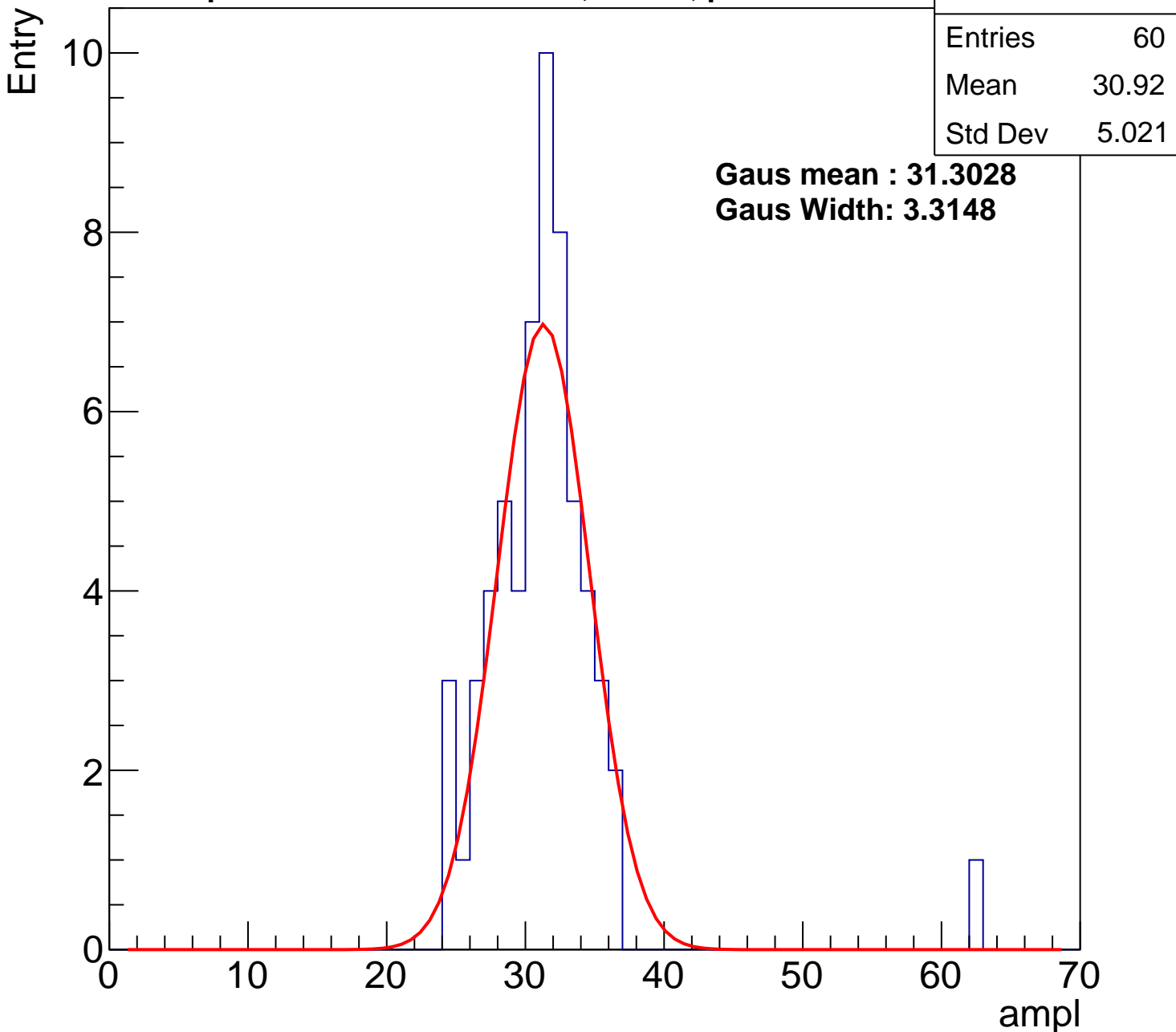
**Gaus Width: 3.3148**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	74
Mean	37.38
Std Dev	3.061

**Gaus mean : 37.5605**

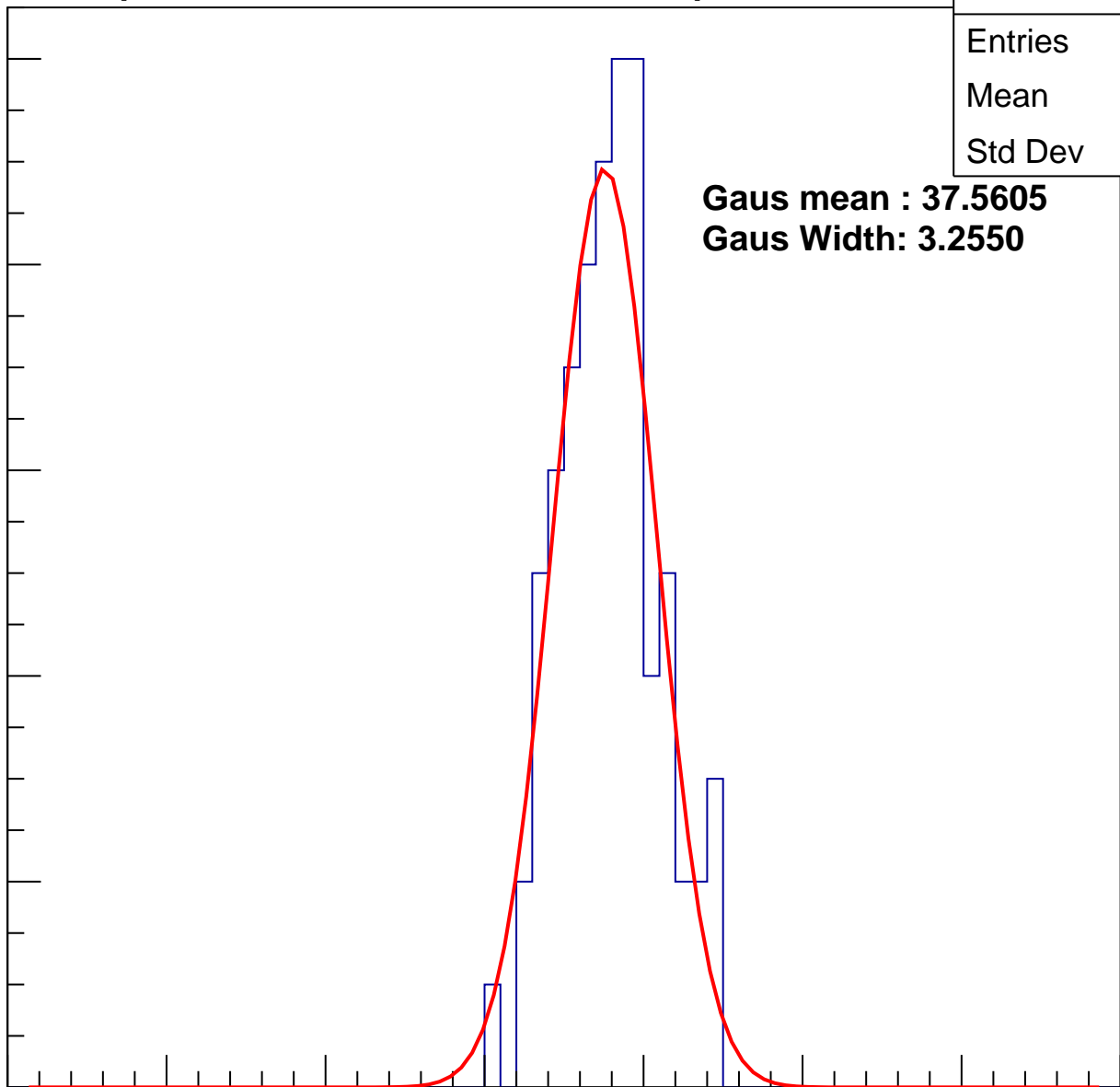
**Gaus Width: 3.2550**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U6-ch81, adc2

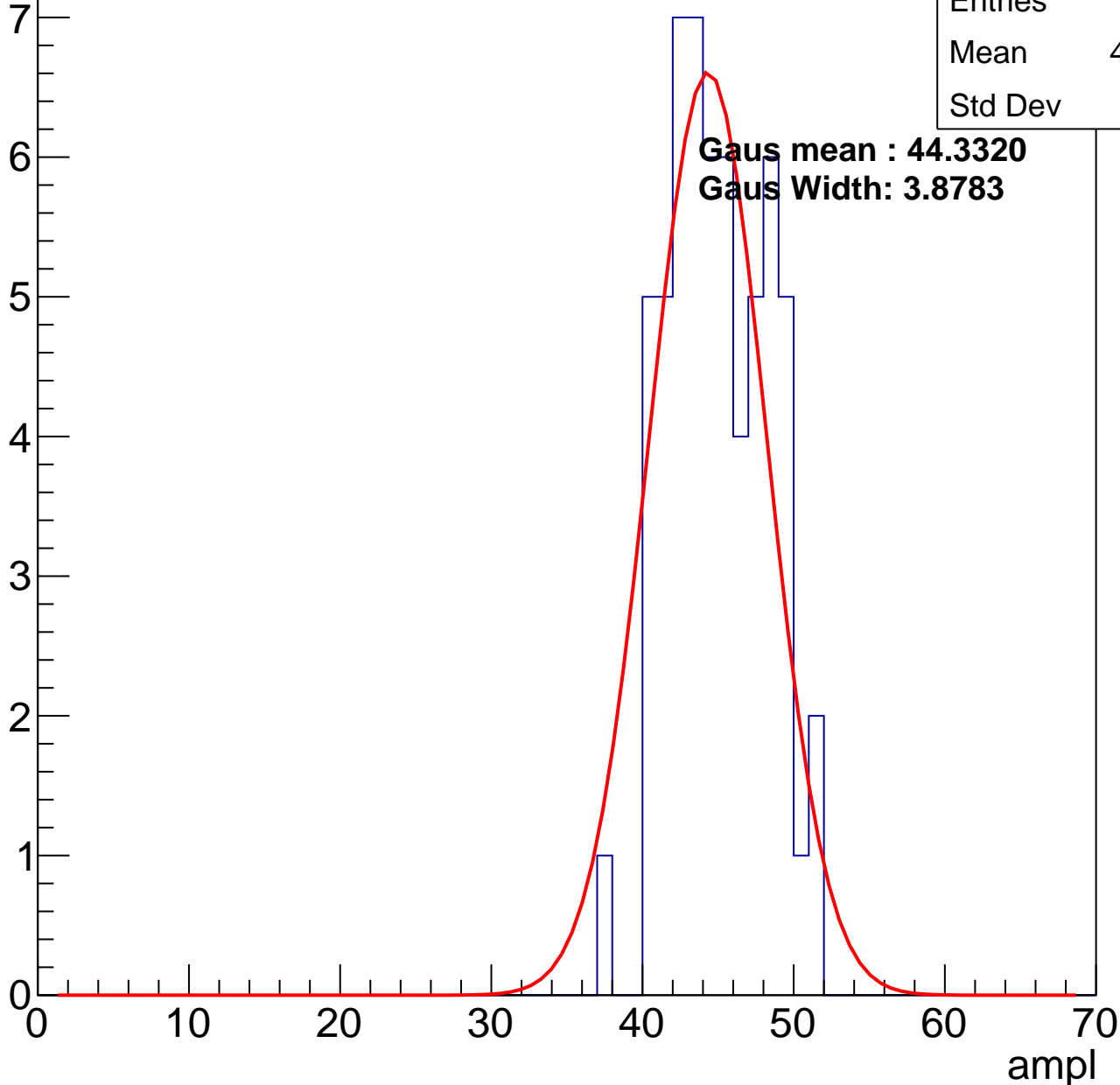
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	44.58
Std Dev	3.19

**Gaus mean : 44.3320**

**Gaus Width: 3.8783**

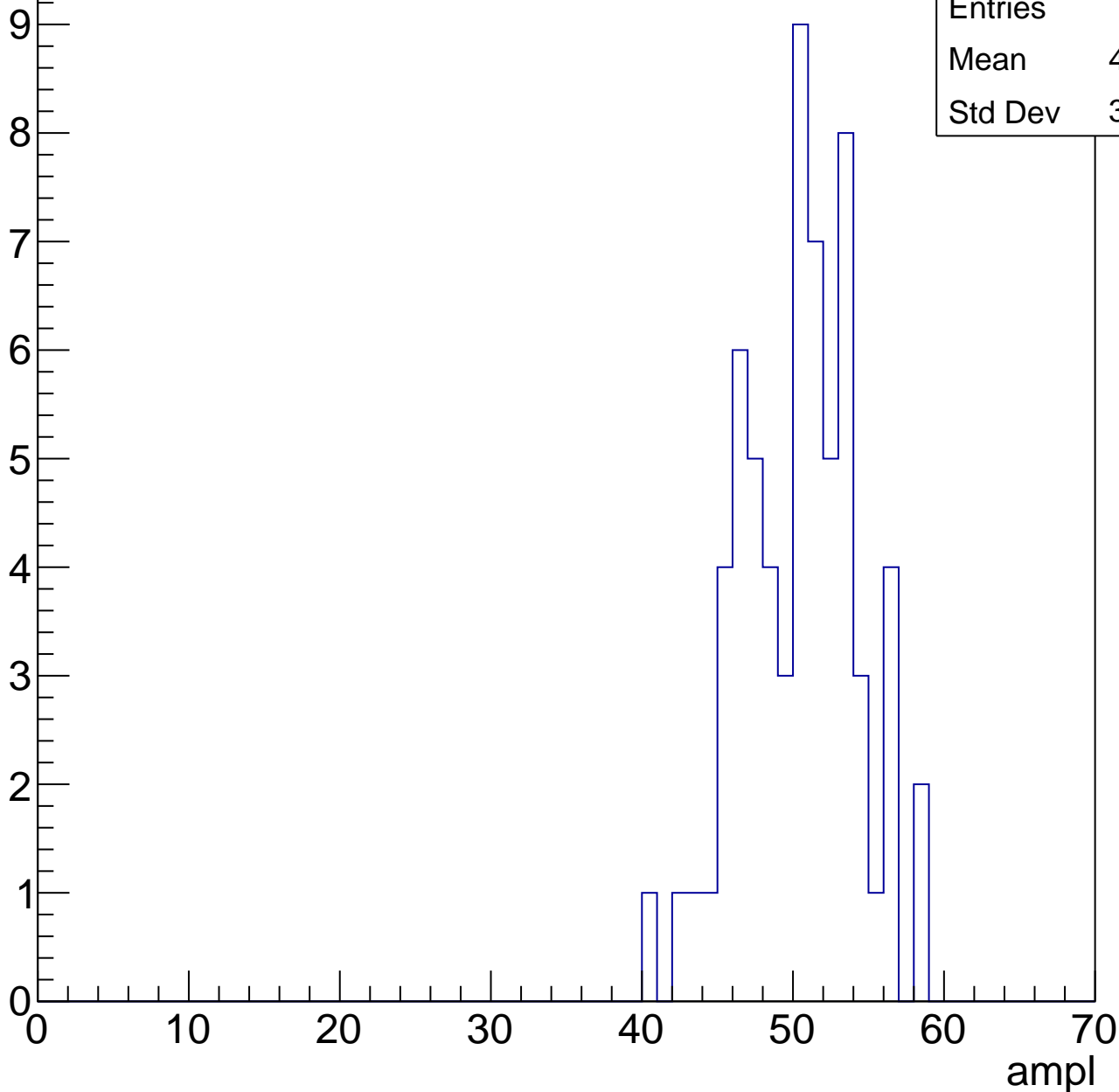


# B0L102S, U6-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	49.95
Std Dev	3.829

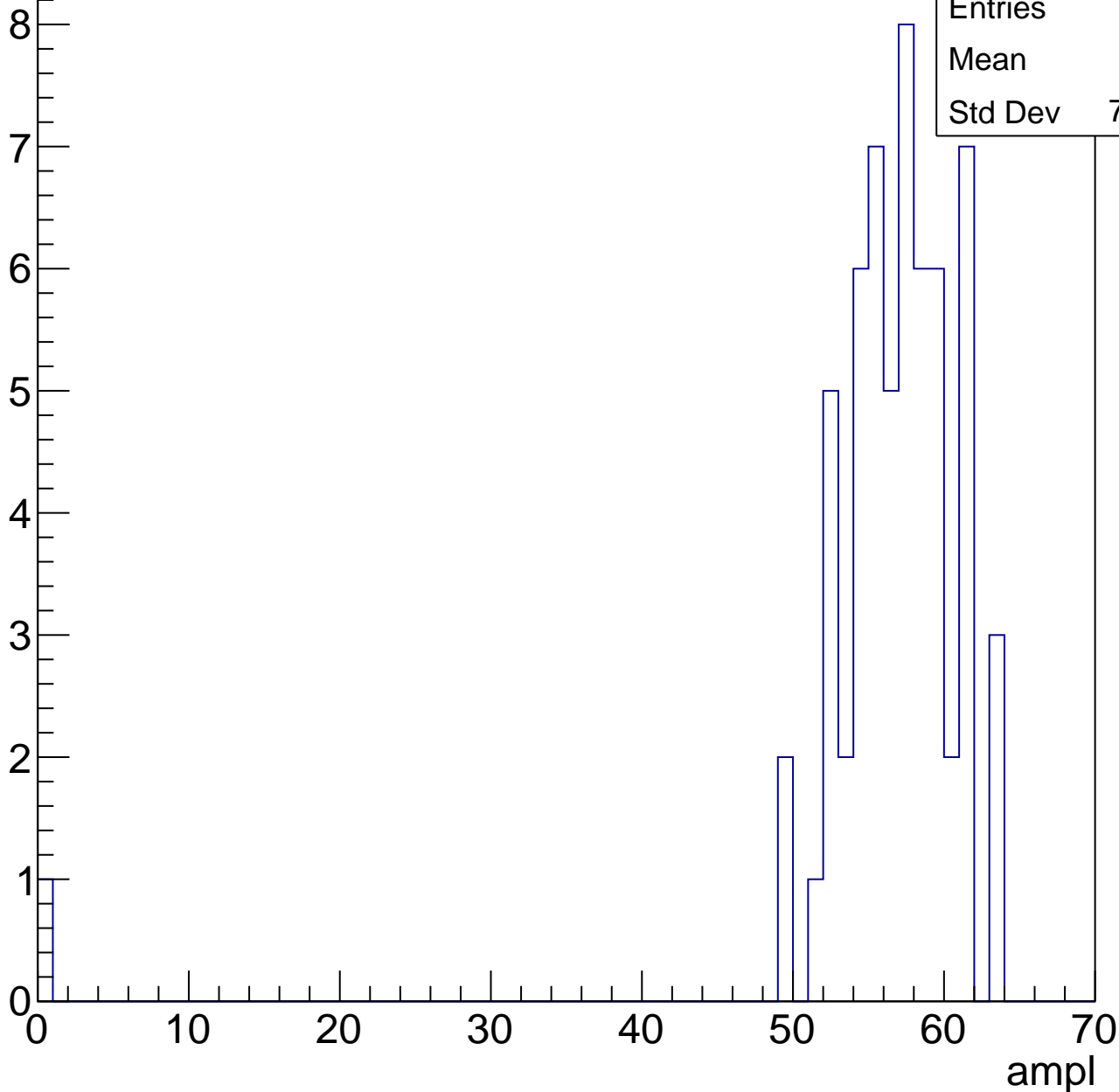


# B0L102S, U6-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	55.7
Std Dev	7.916



# B0L102S, U6-ch81, adc5

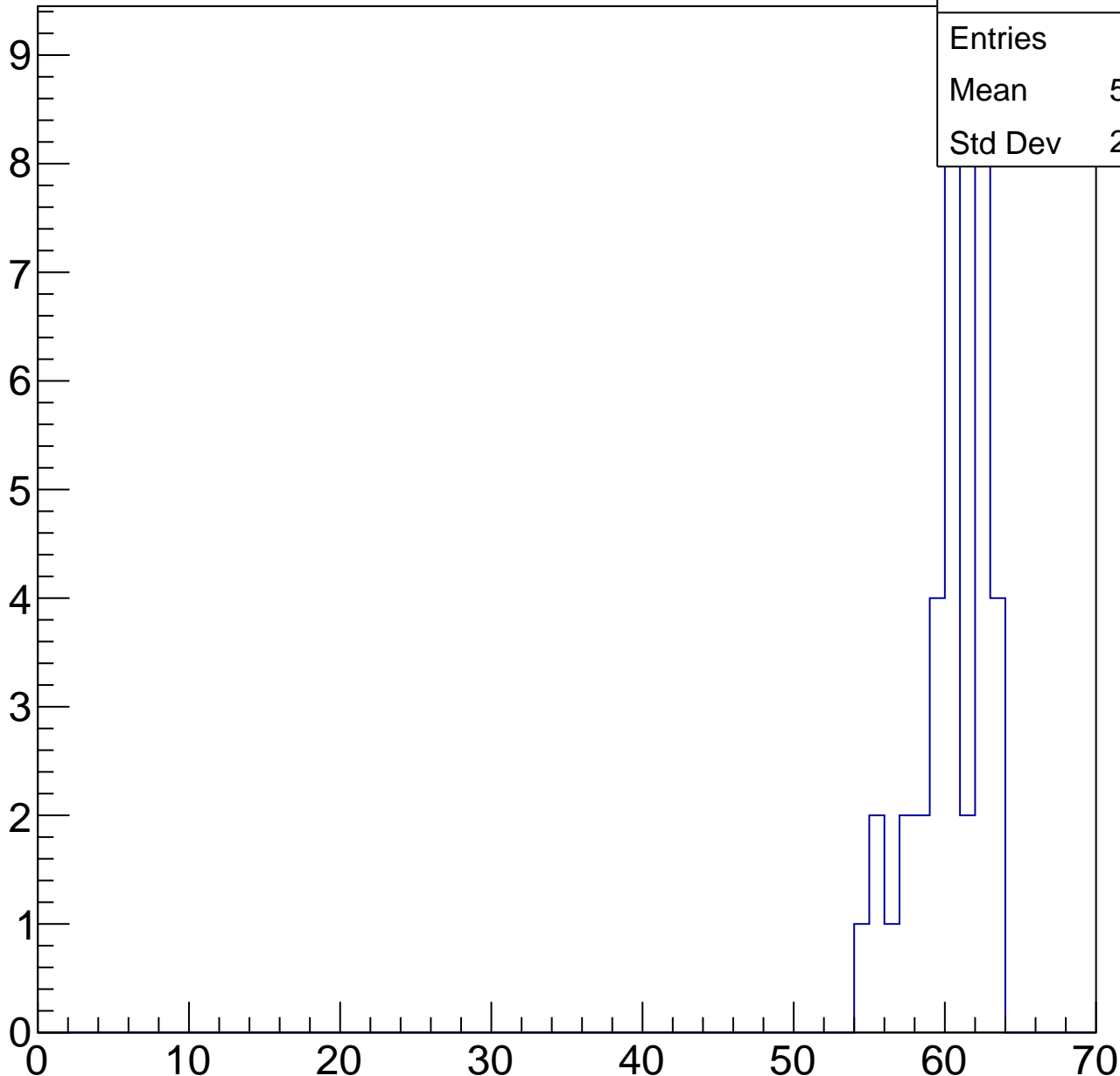
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	35
Mean	59.94
Std Dev	2.425

ampl



# B0L102S, U6-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	5
Mean	62.4
Std Dev	0.8



# B0L102S, U6-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B0L102S, U6-ch82, adc0

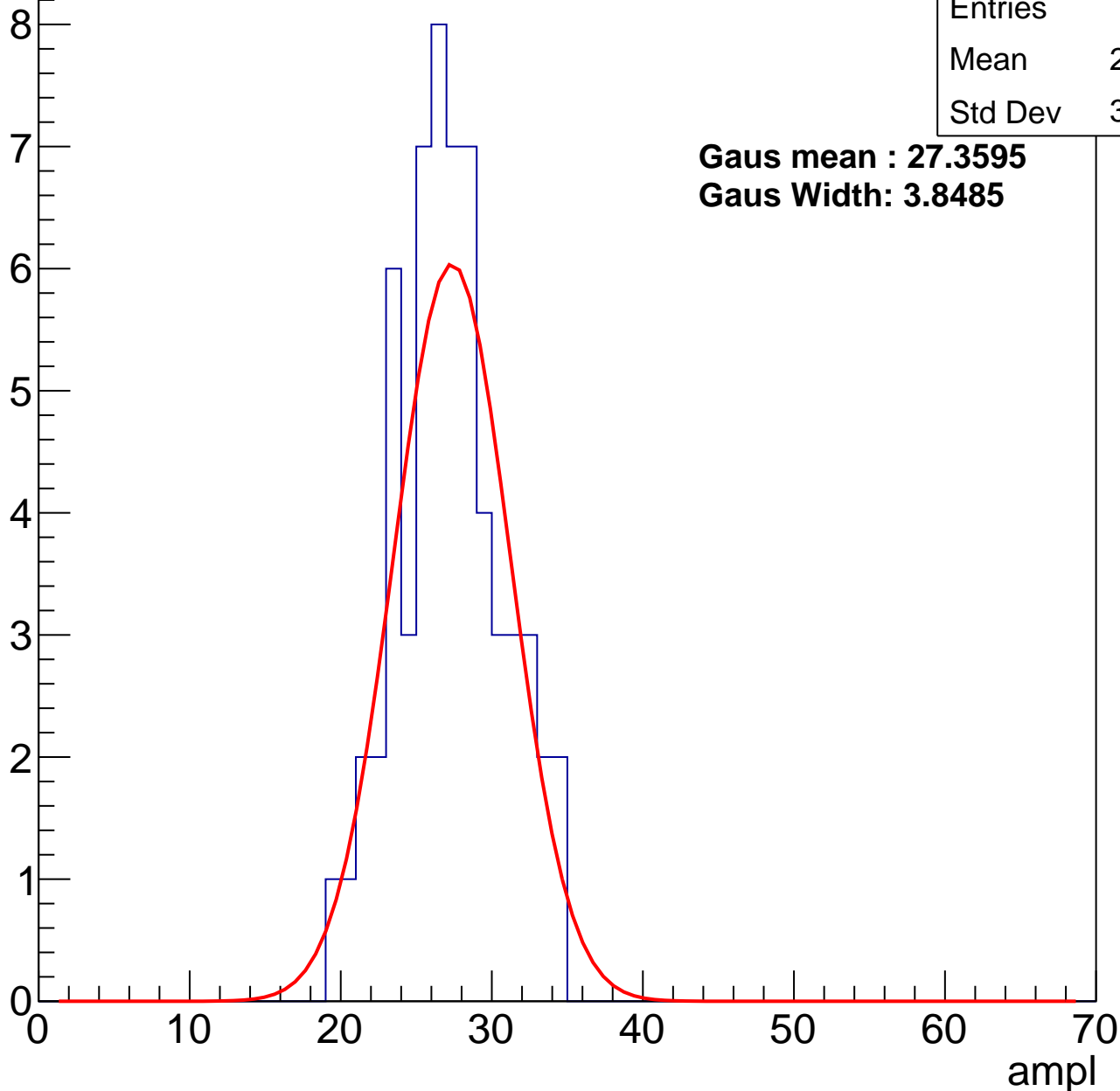
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	26.75
Std Dev	3.467

**Gaus mean : 27.3595**

**Gaus Width: 3.8485**



# B0L102S, U6-ch82, adc1

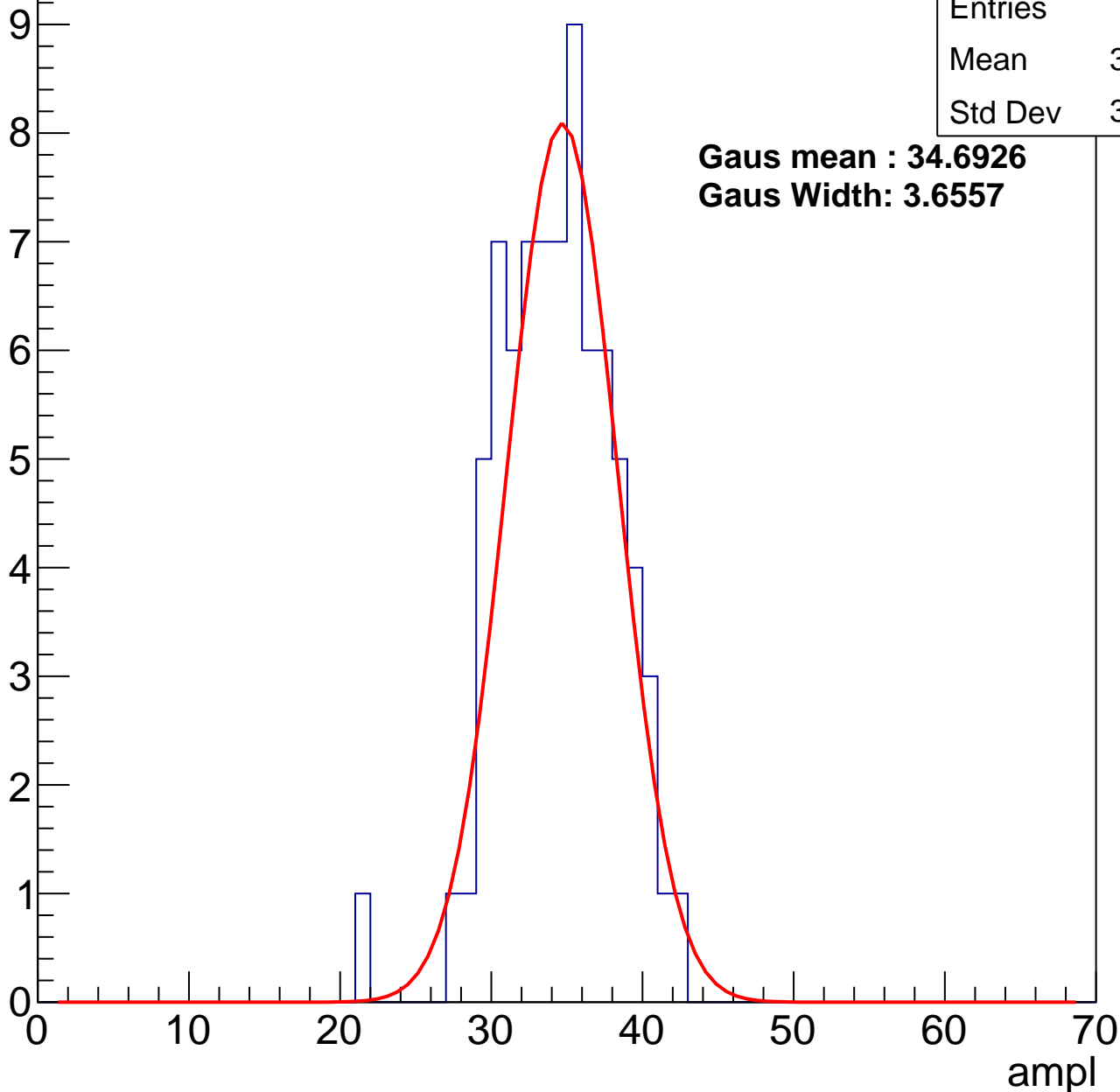
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	33.92
Std Dev	3.723

**Gaus mean : 34.6926**

**Gaus Width: 3.6557**



# B0L102S, U6-ch82, adc2

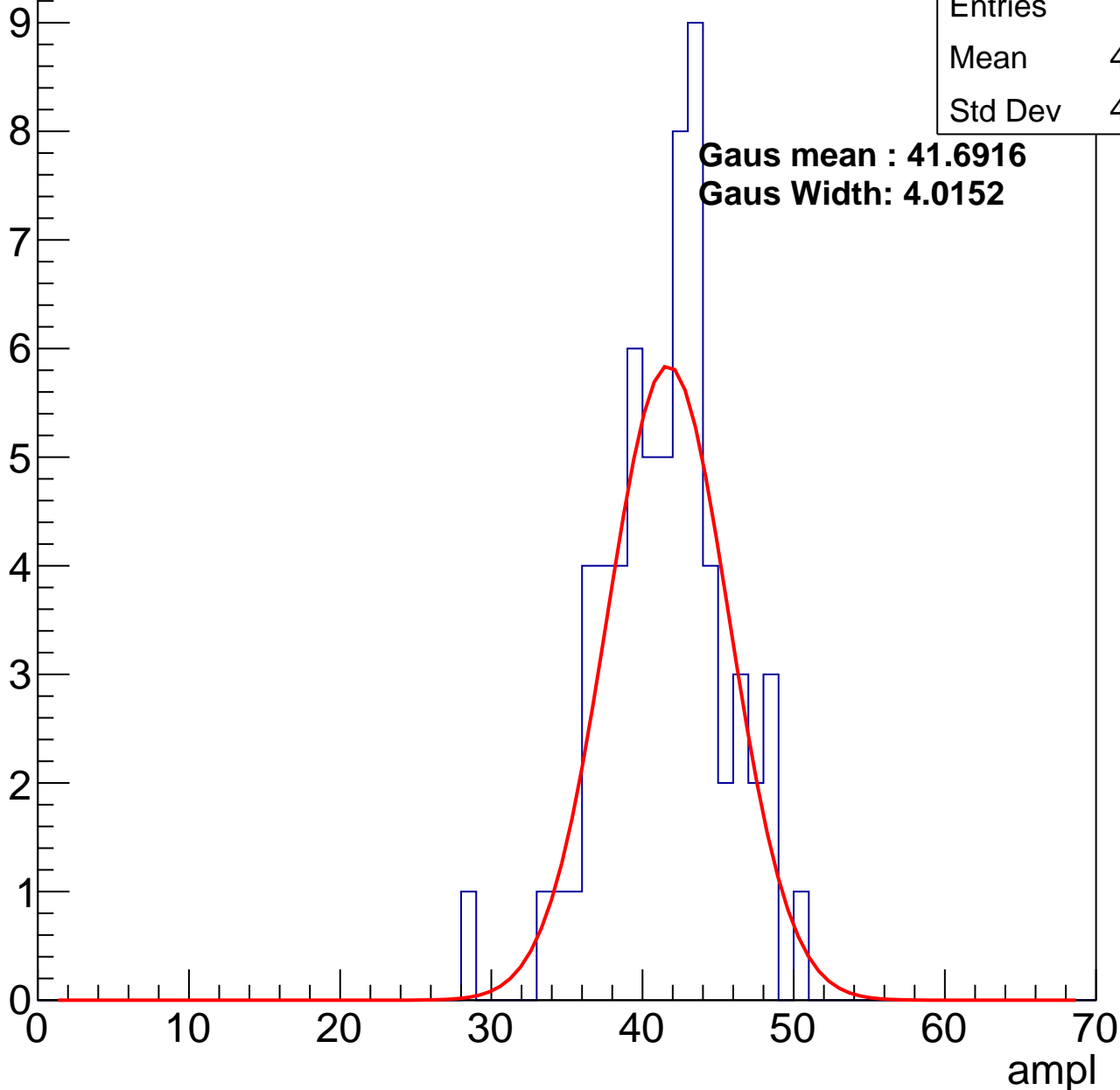
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	41.06
Std Dev	4.019

**Gaus mean : 41.6916**

**Gaus Width: 4.0152**

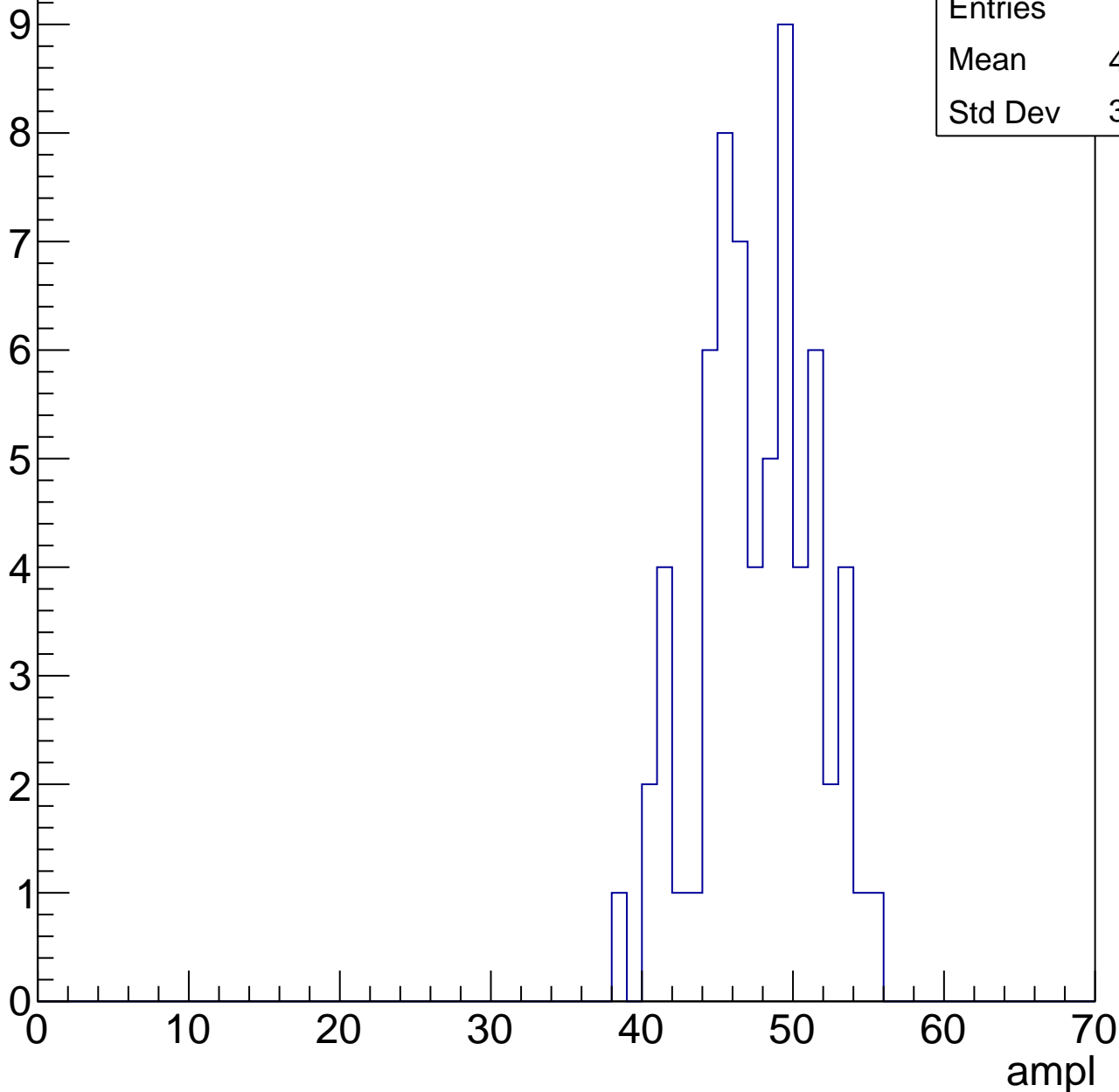


# B0L102S, U6-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	47.17
Std Dev	3.764

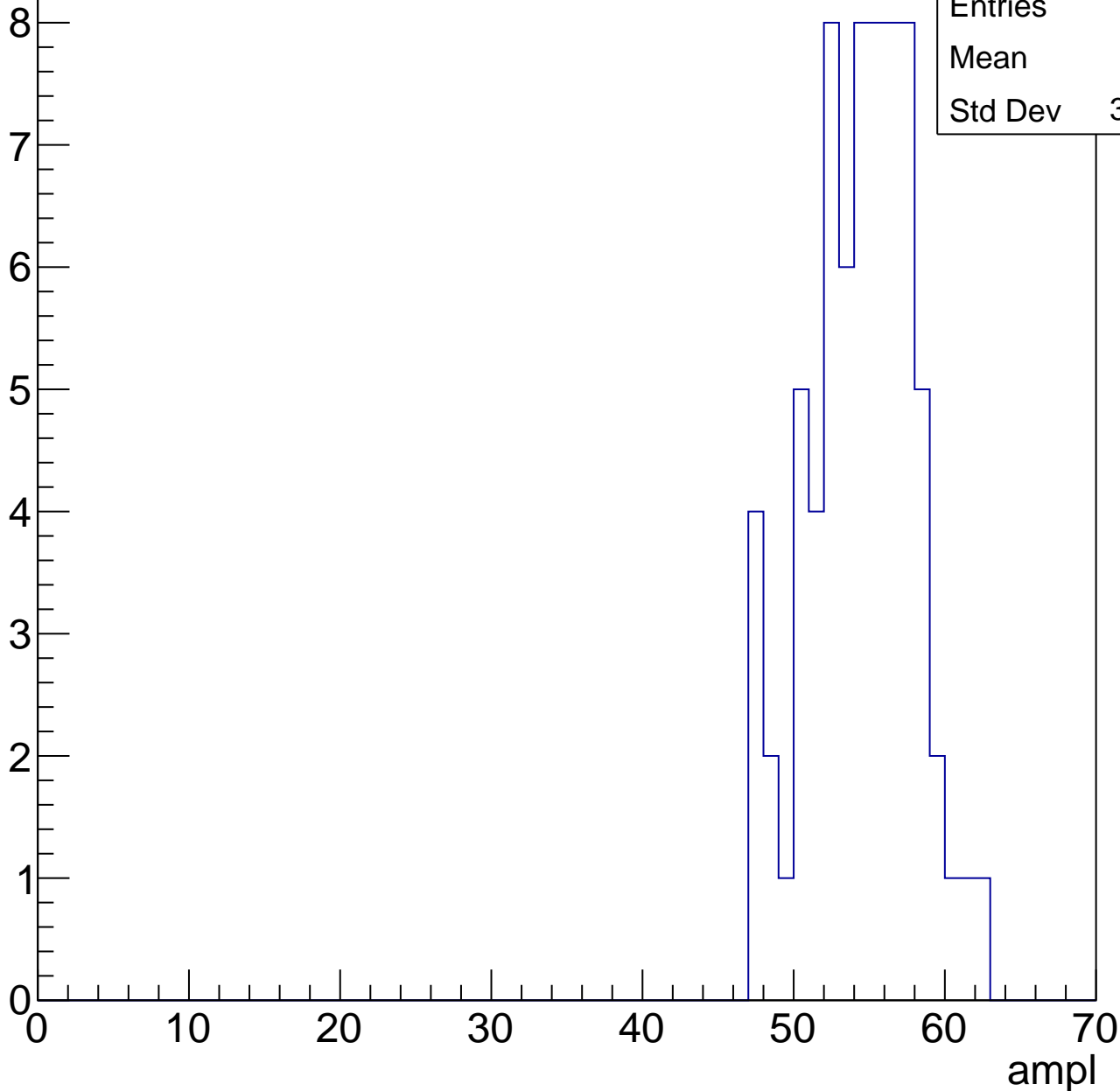


# B0L102S, U6-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

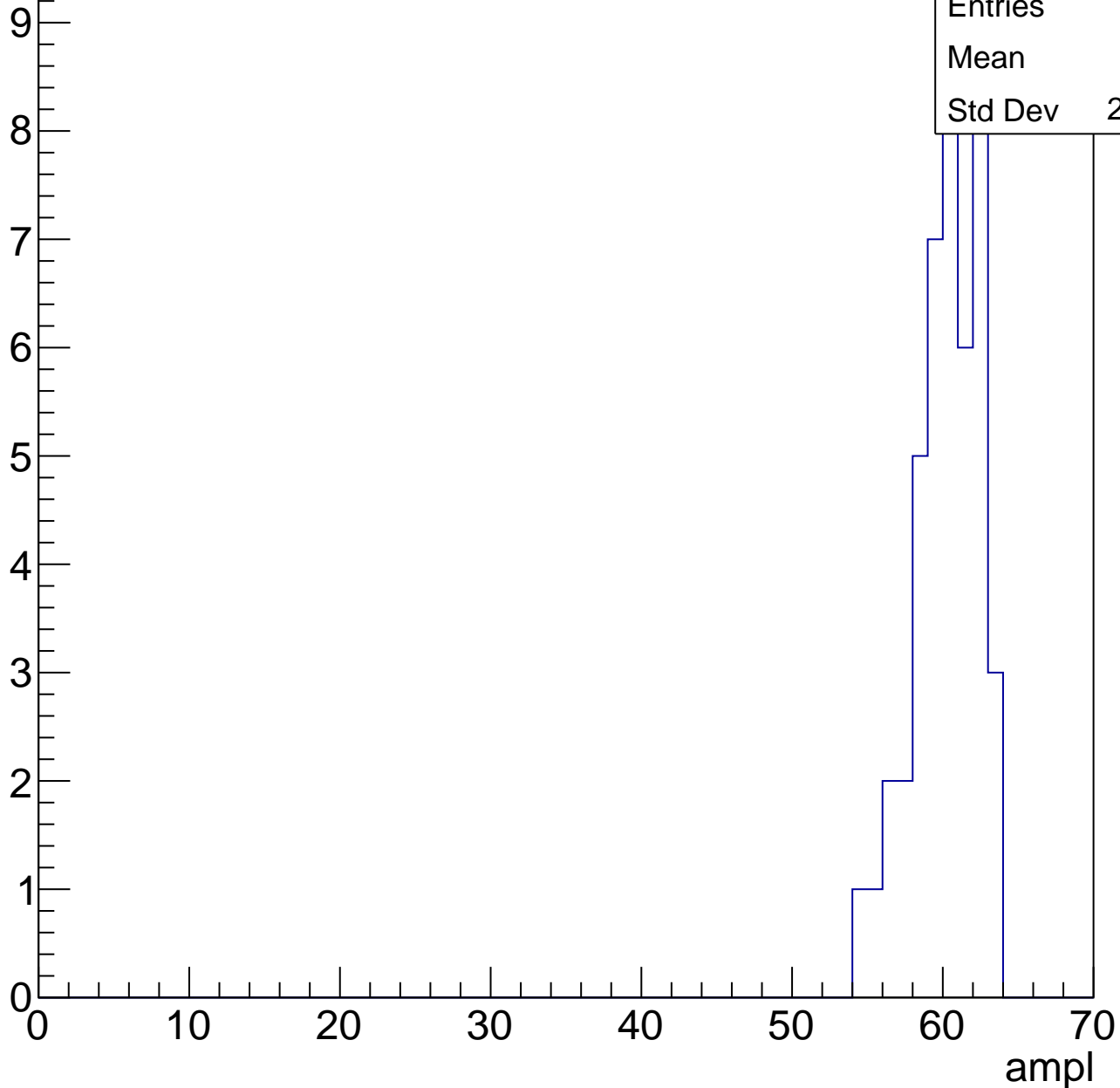
Entries	72
Mean	54
Std Dev	3.412



# B0L102S, U6-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

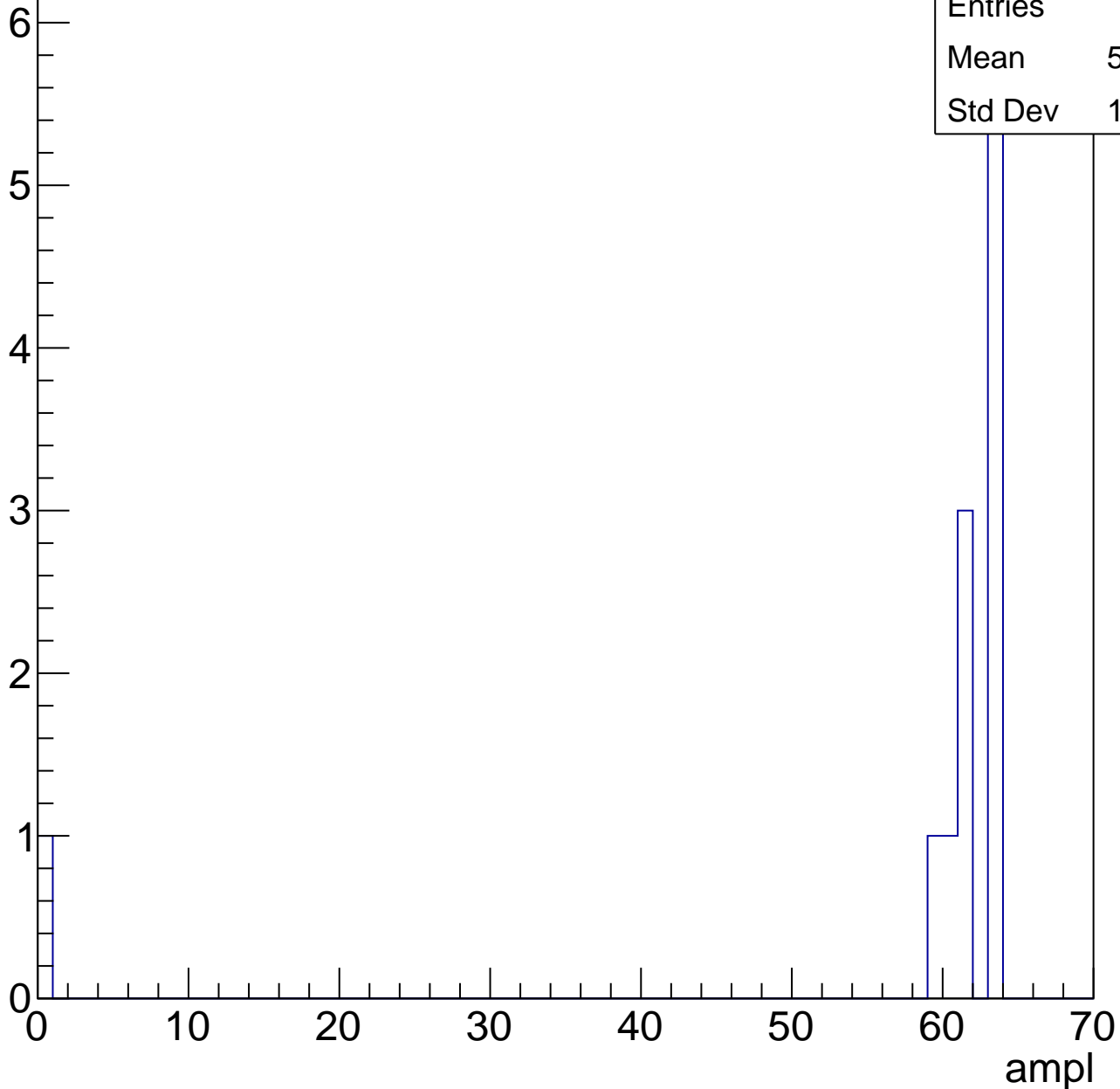


# B0L102S, U6-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	56.67
Std Dev	17.14

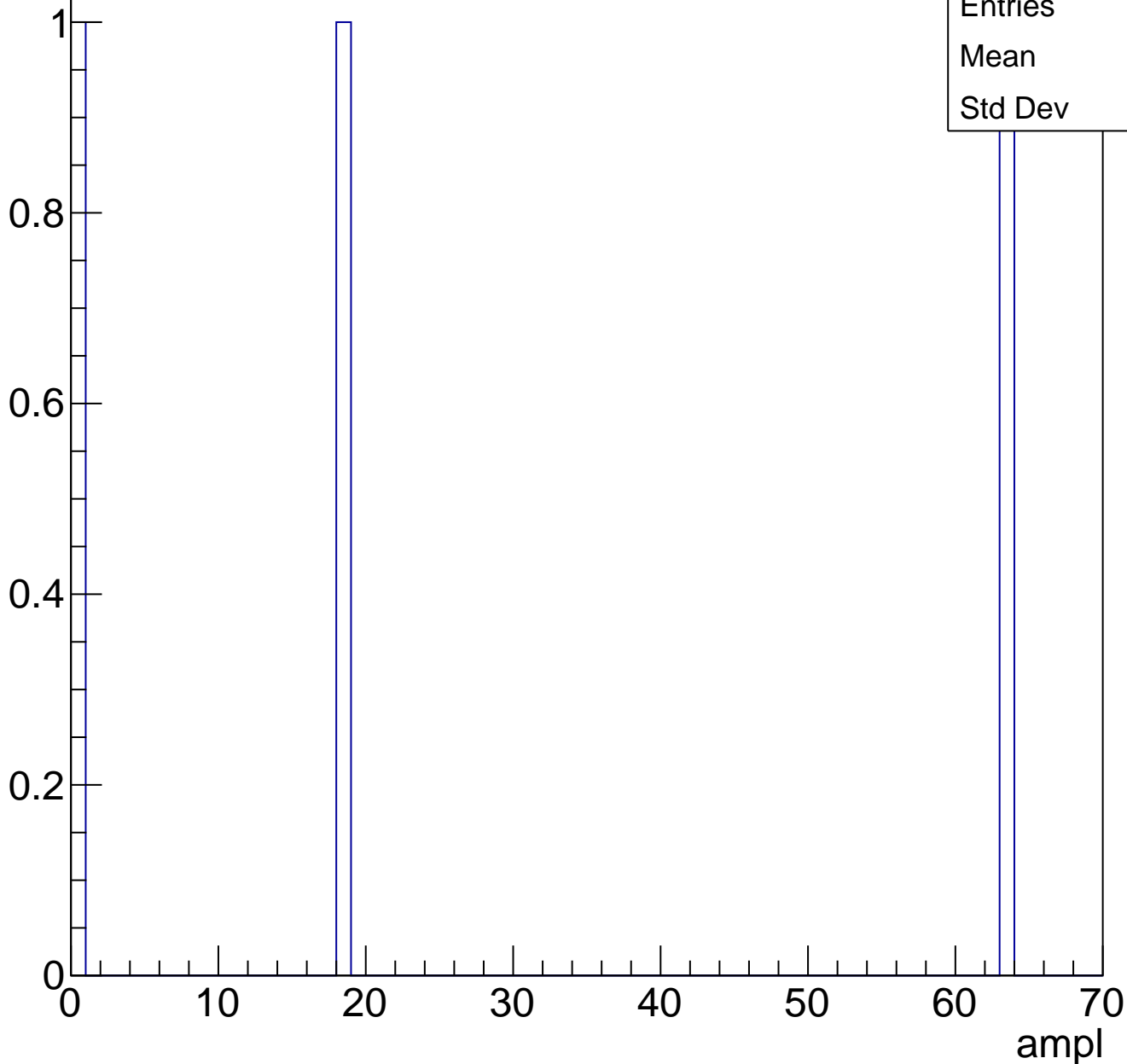




# B0L102S, U6-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch83, adc0

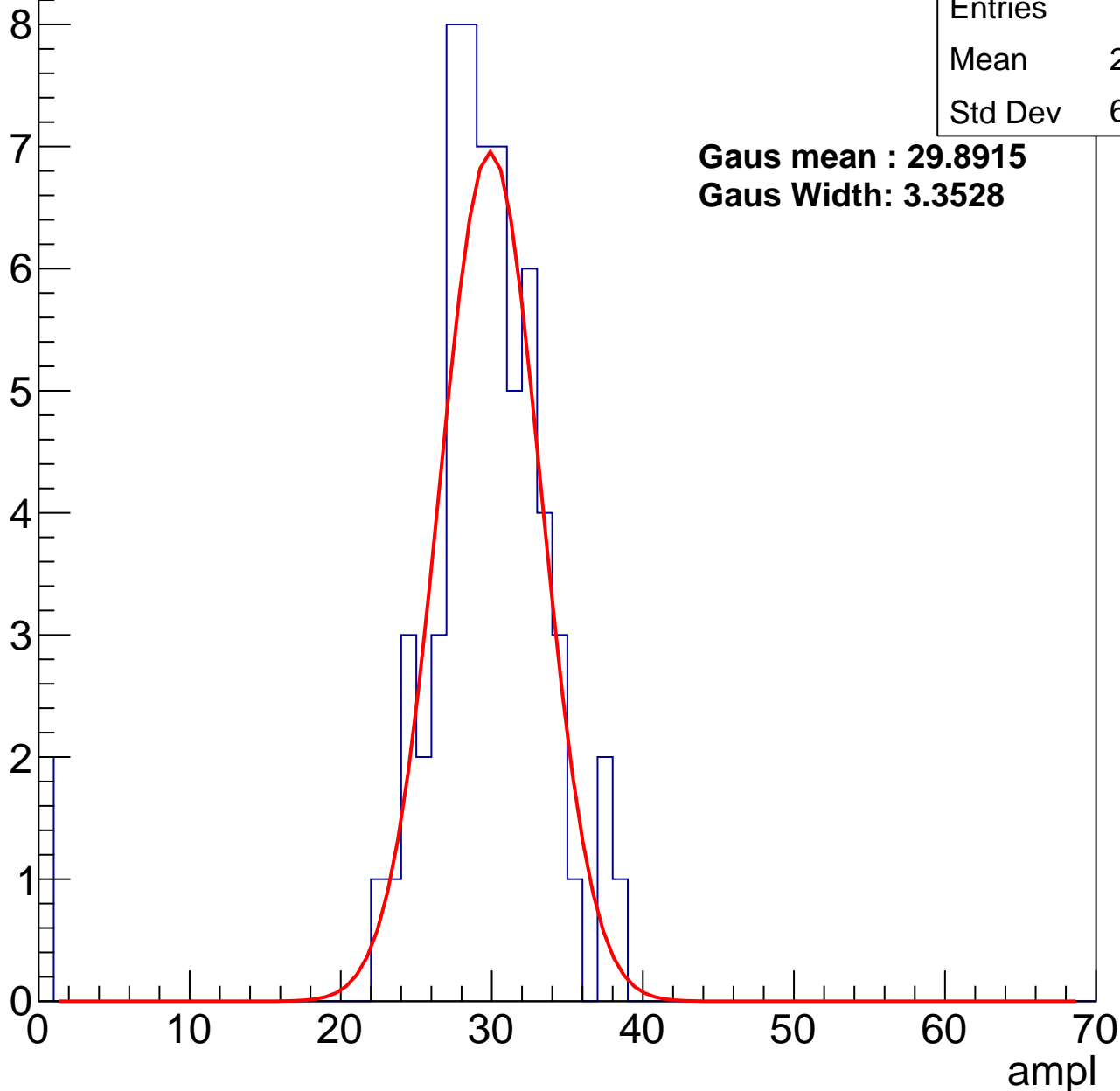
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	28.53
Std Dev	6.113

**Gaus mean : 29.8915**

**Gaus Width: 3.3528**



# B0L102S, U6-ch83, adc1

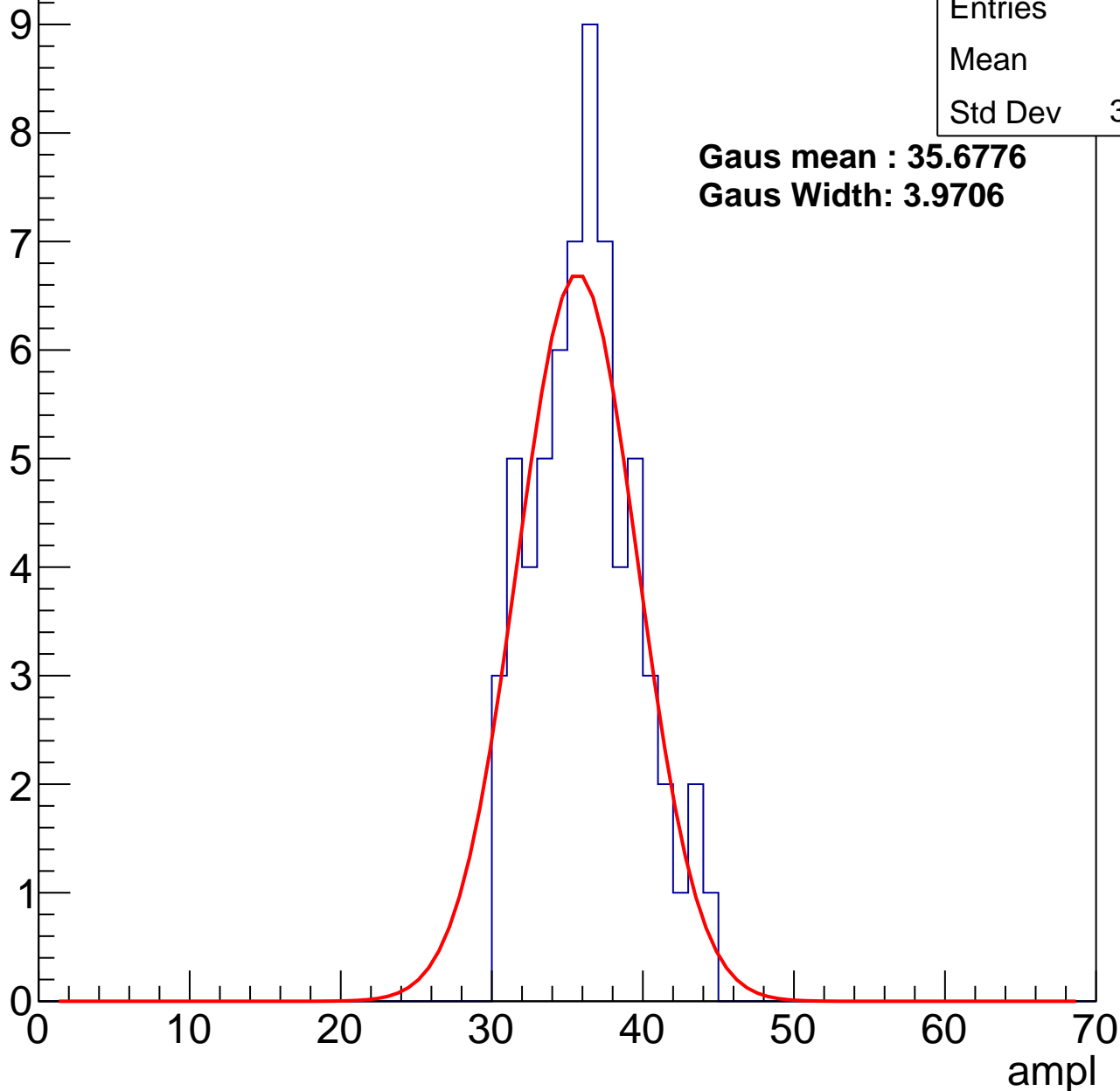
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.8
Std Dev	3.387

**Gaus mean : 35.6776**

**Gaus Width: 3.9706**



# B0L102S, U6-ch83, adc2

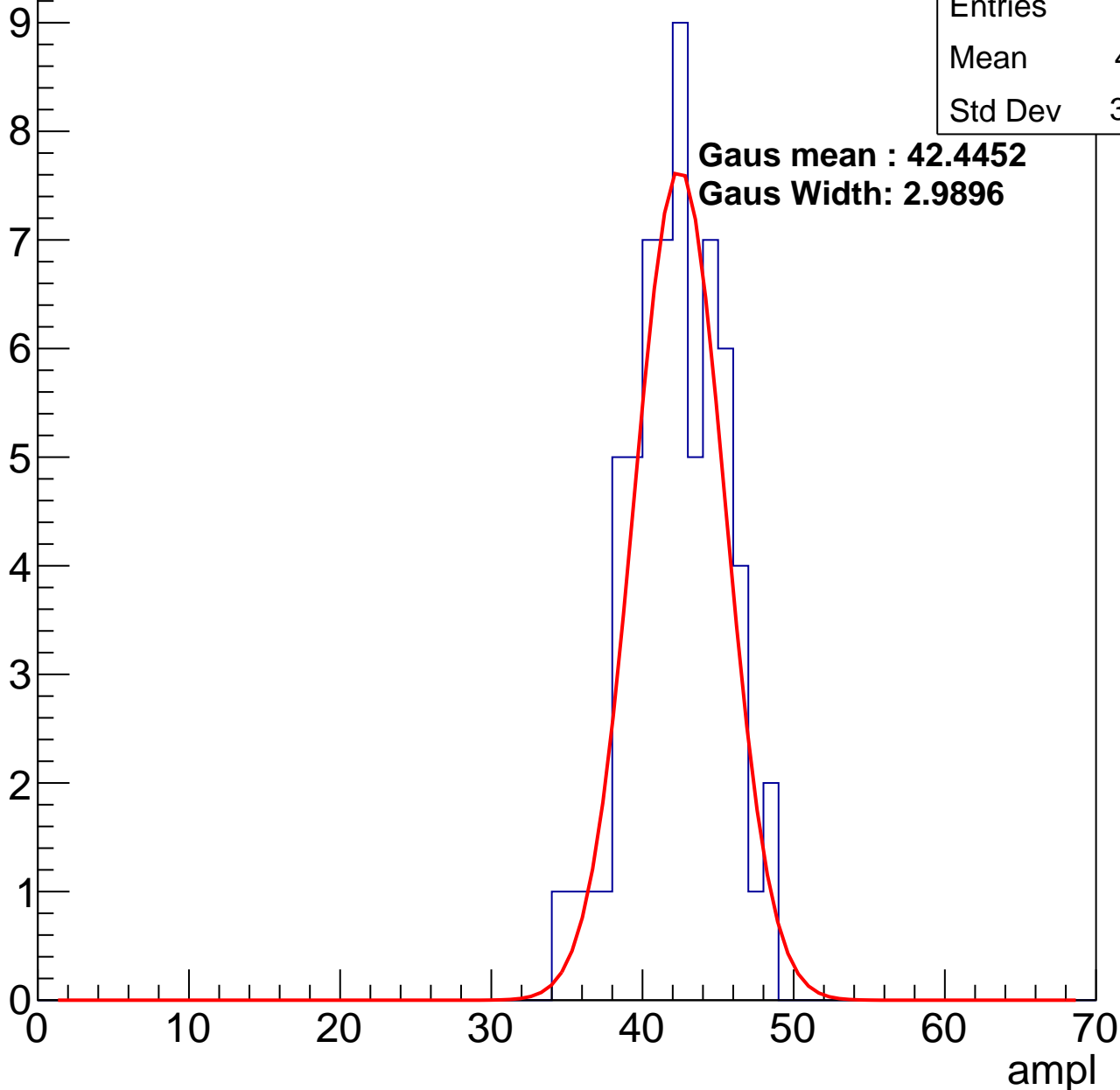
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	41.81
Std Dev	3.058

**Gaus mean : 42.4452**

**Gaus Width: 2.9896**

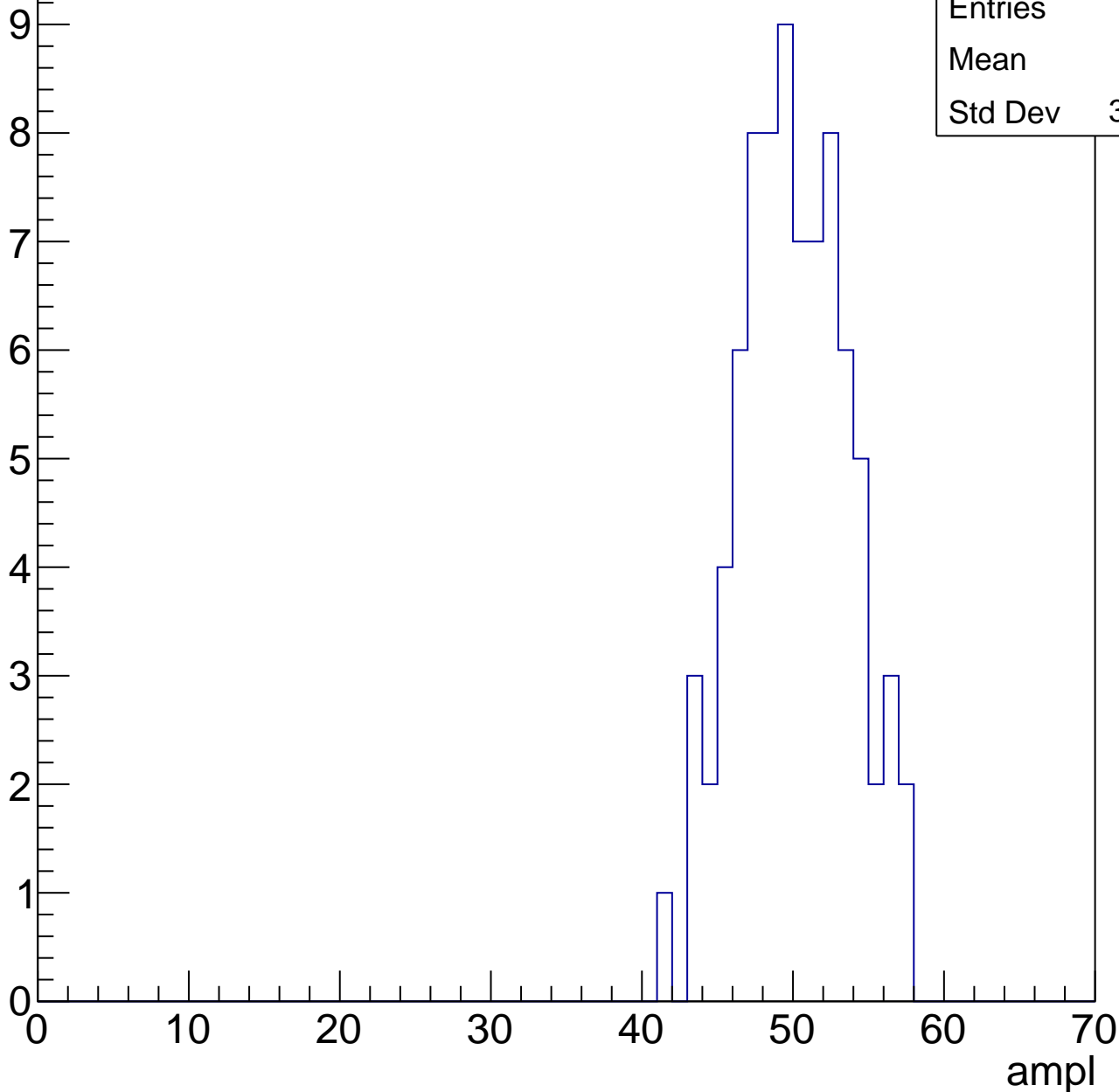


# B0L102S, U6-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

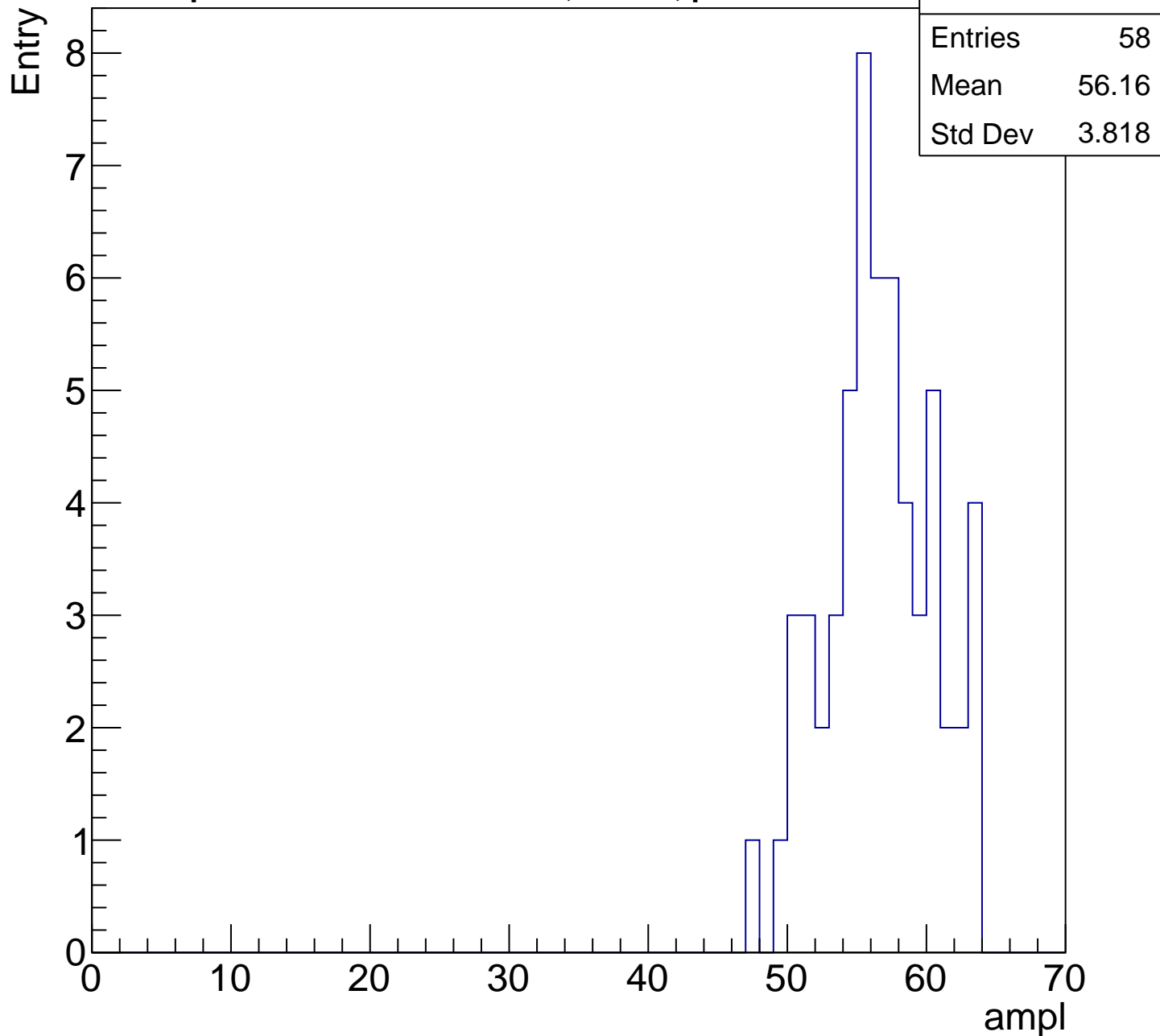
Entry

Entries	81
Mean	49.6
Std Dev	3.558



# B0L102S, U6-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	39
Mean	58.36
Std Dev	9.718

1

2

3

4

5

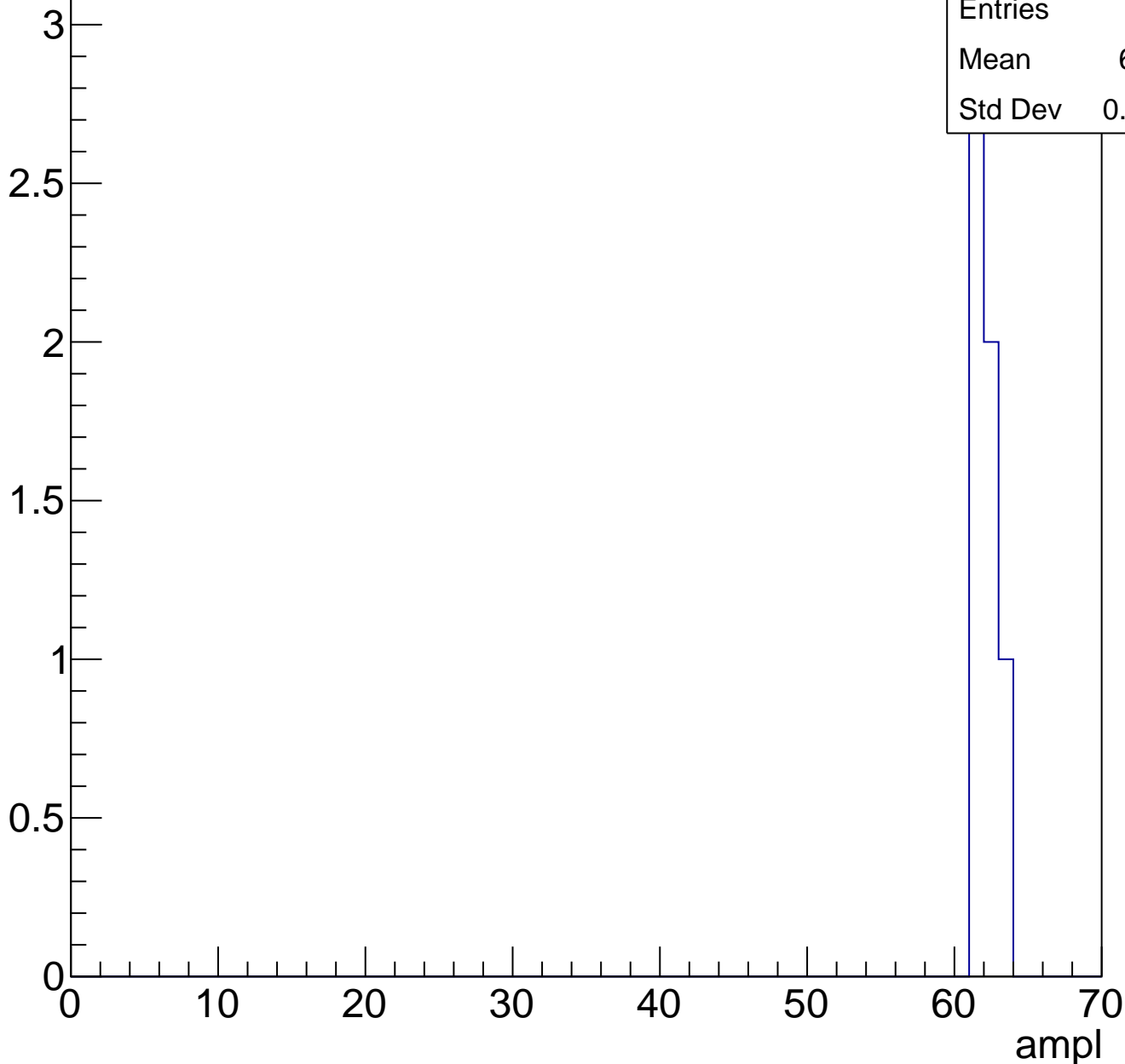
6

7

# B0L102S, U6-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	6
Mean	61.67
Std Dev	0.7454



# B0L102S, U6-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch84, adc0

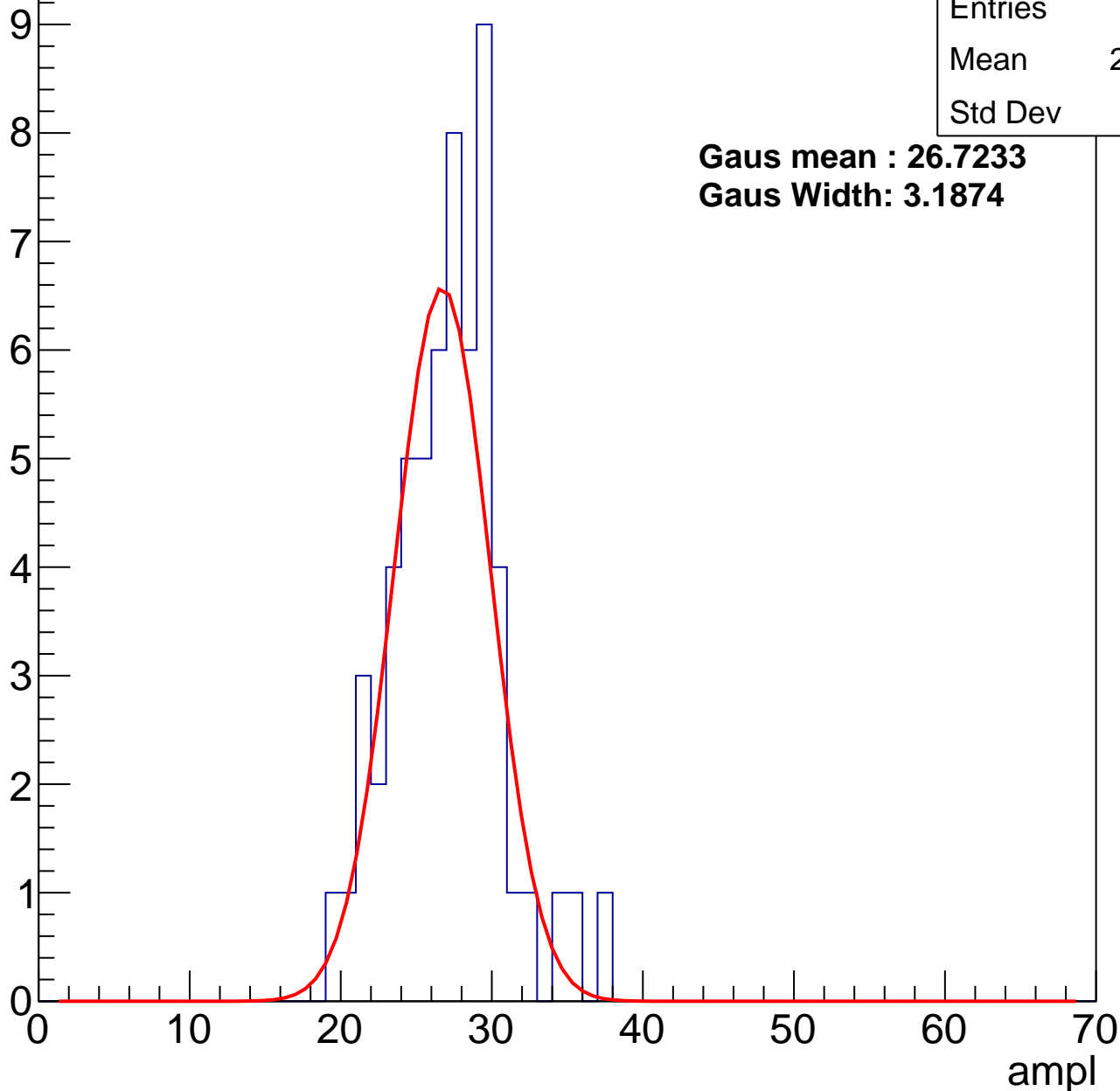
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	26.66
Std Dev	3.52

**Gaus mean : 26.7233**

**Gaus Width: 3.1874**



# B0L102S, U6-ch84, adc1

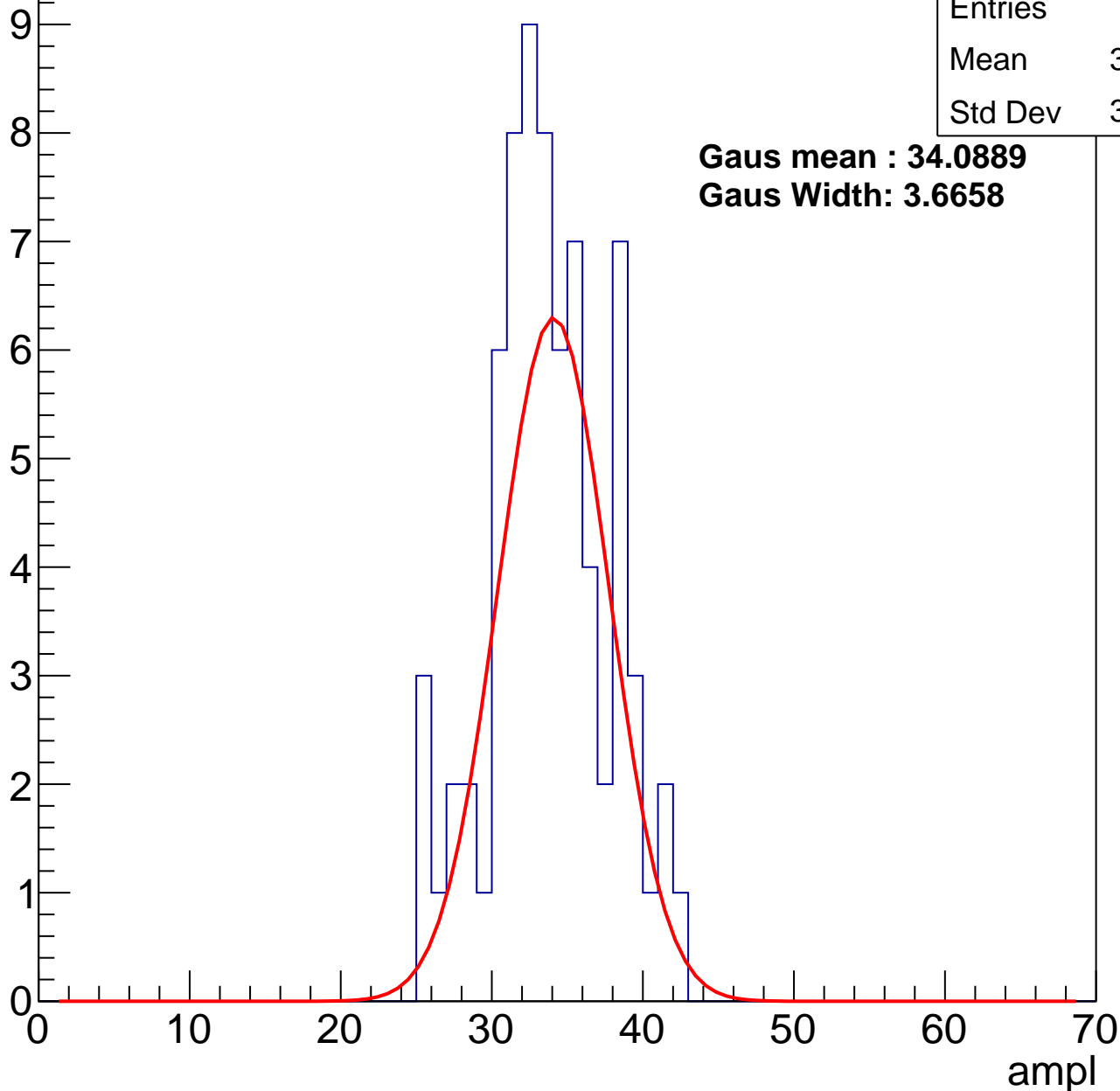
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	33.34
Std Dev	3.897

**Gaus mean : 34.0889**

**Gaus Width: 3.6658**



# B0L102S, U6-ch84, adc2

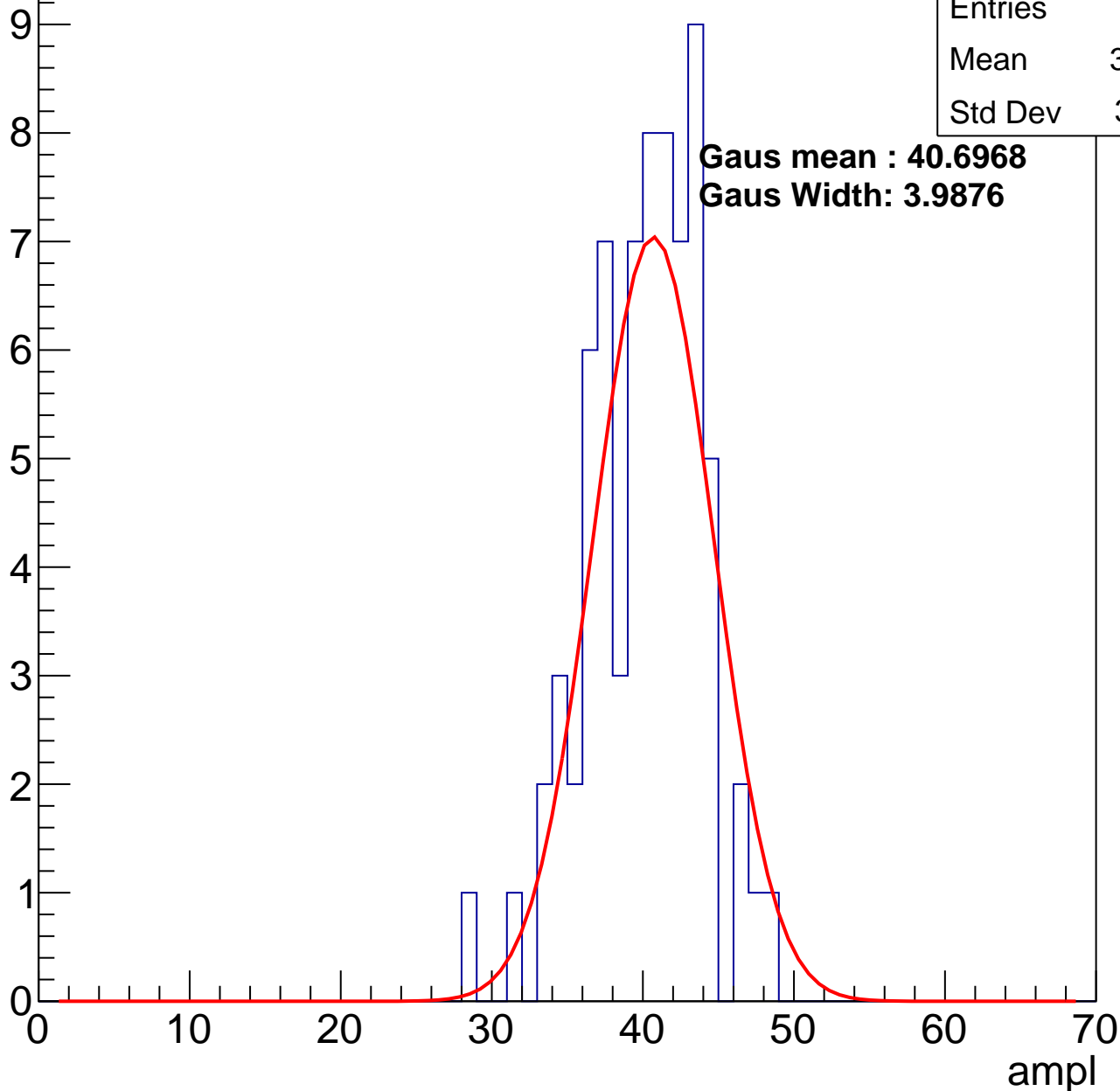
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	39.66
Std Dev	3.761

**Gaus mean : 40.6968**

**Gaus Width: 3.9876**

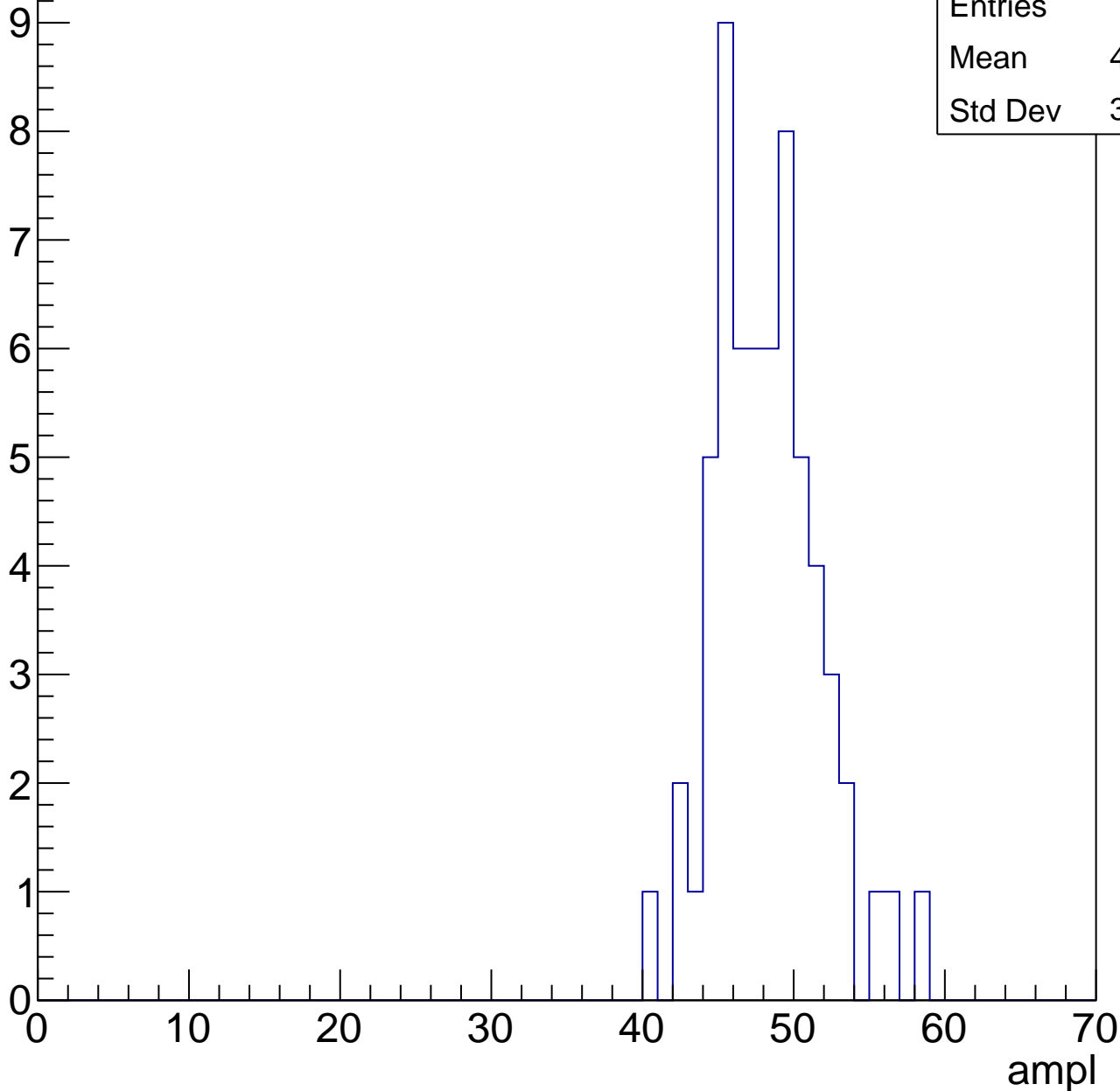


# B0L102S, U6-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

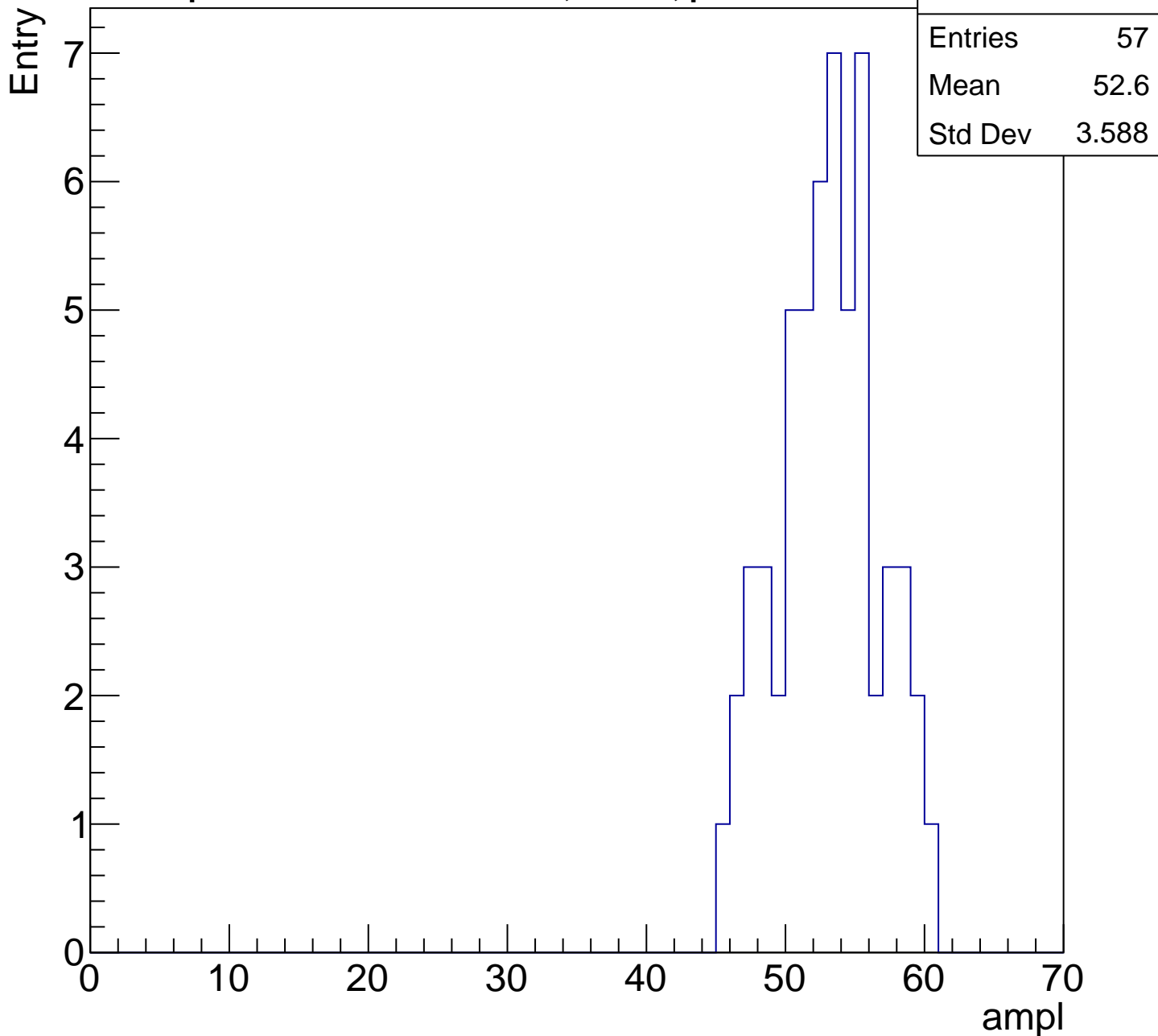
Entry

Entries	61
Mean	47.79
Std Dev	3.446



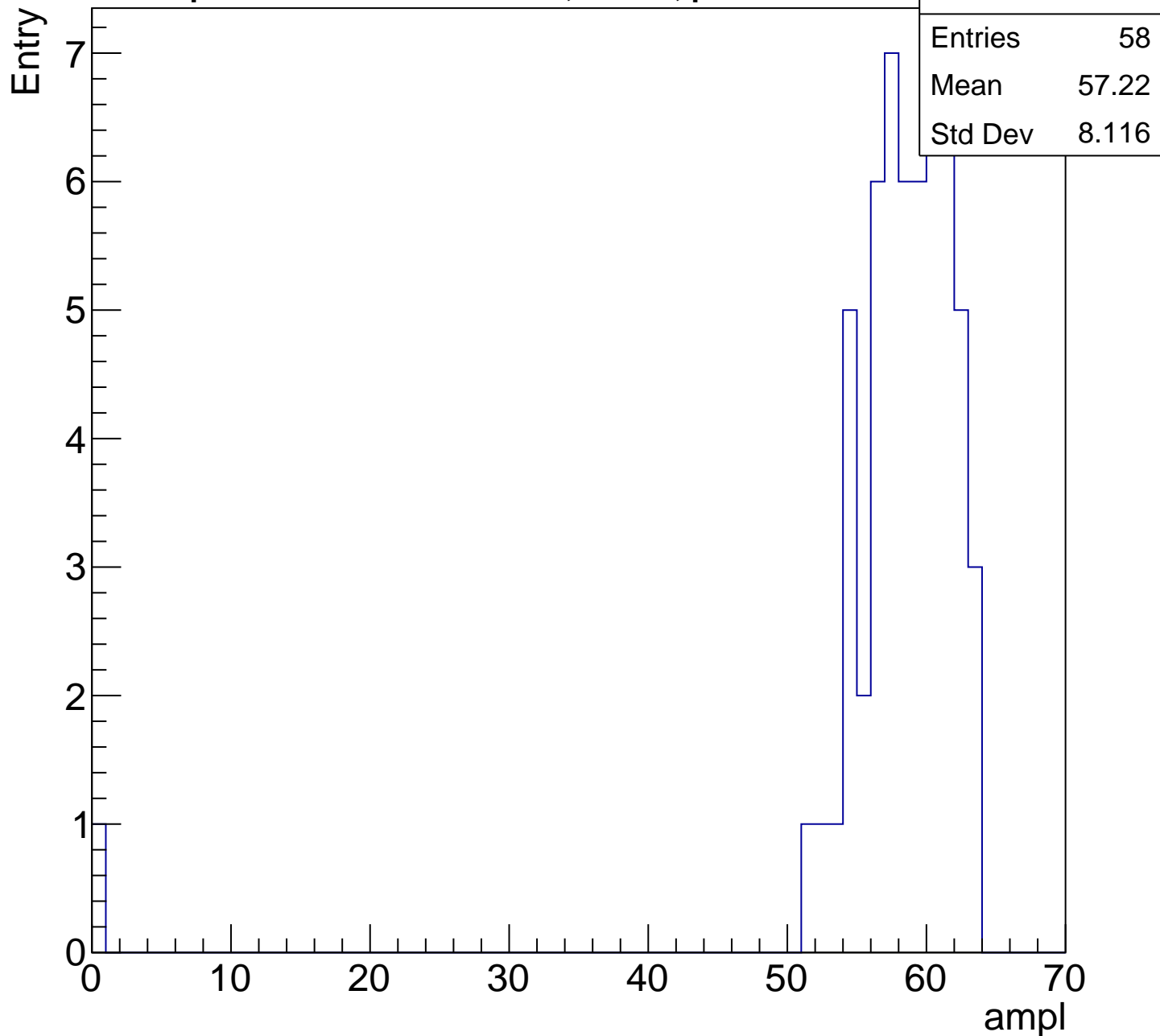
# B0L102S, U6-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

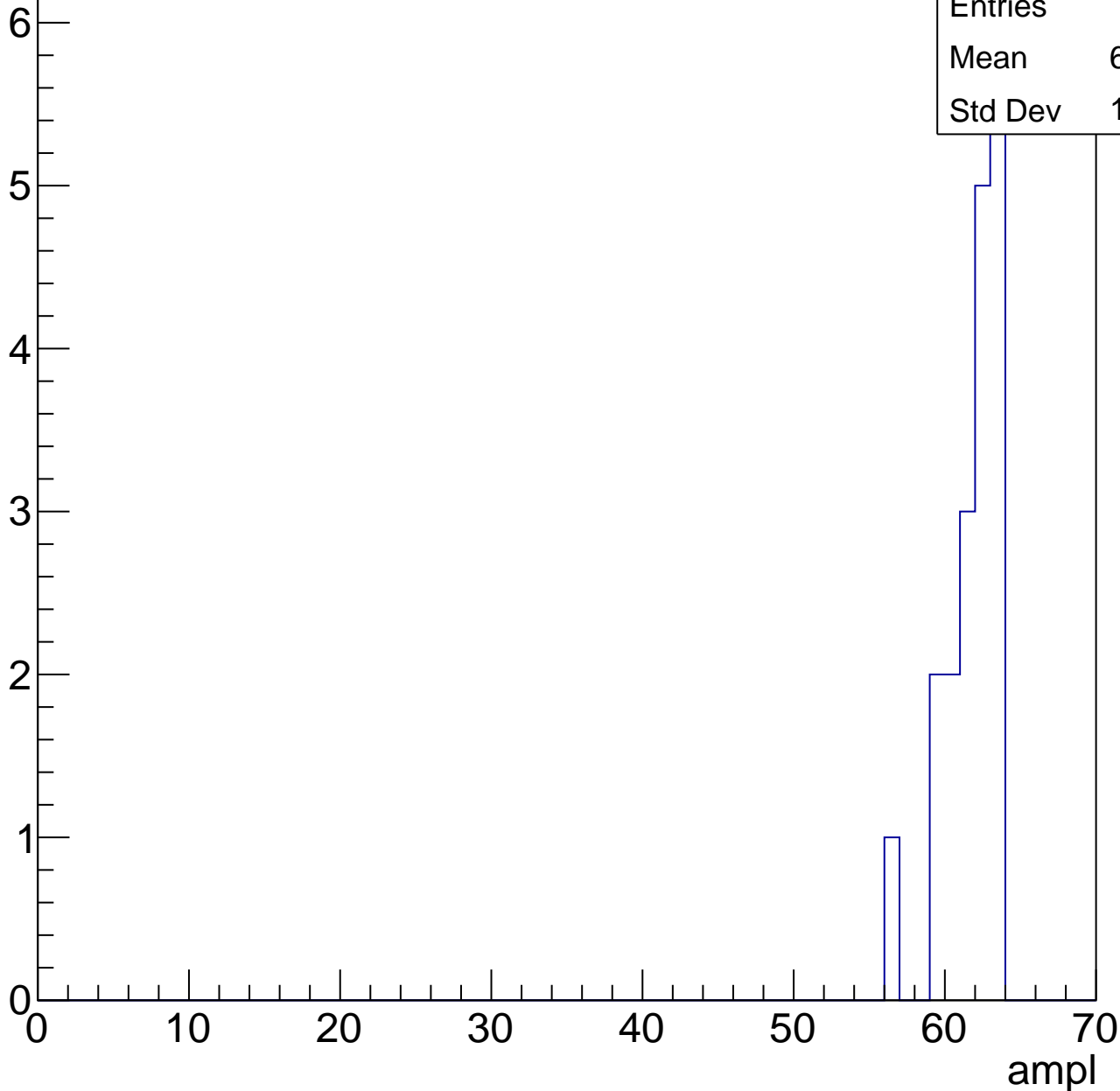


# B0L102S, U6-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	19
Mean	61.32
Std Dev	1.808

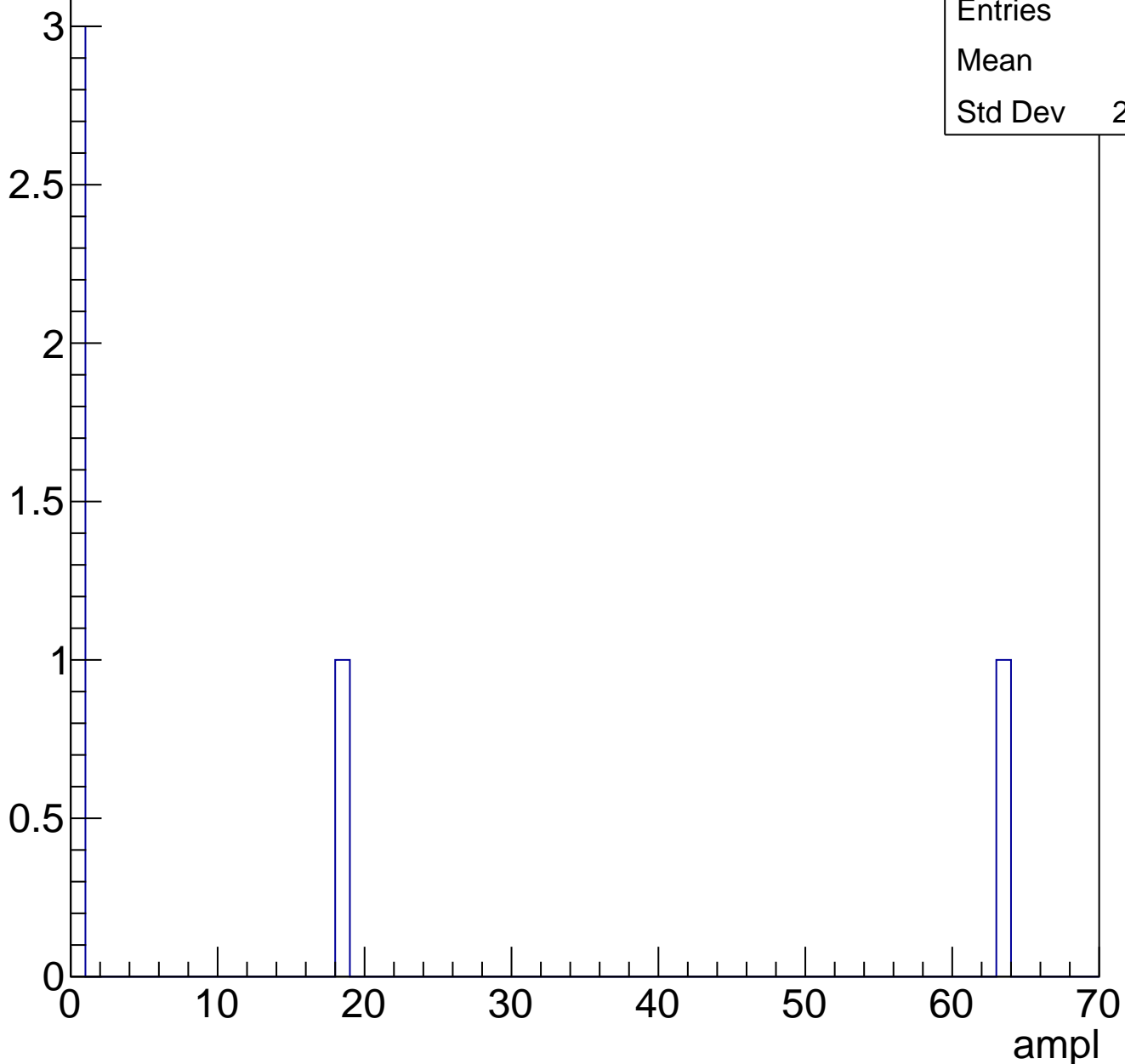




# B0L102S, U6-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	5
Mean	16.2
Std Dev	24.42

# B0L102S, U6-ch85, adc0

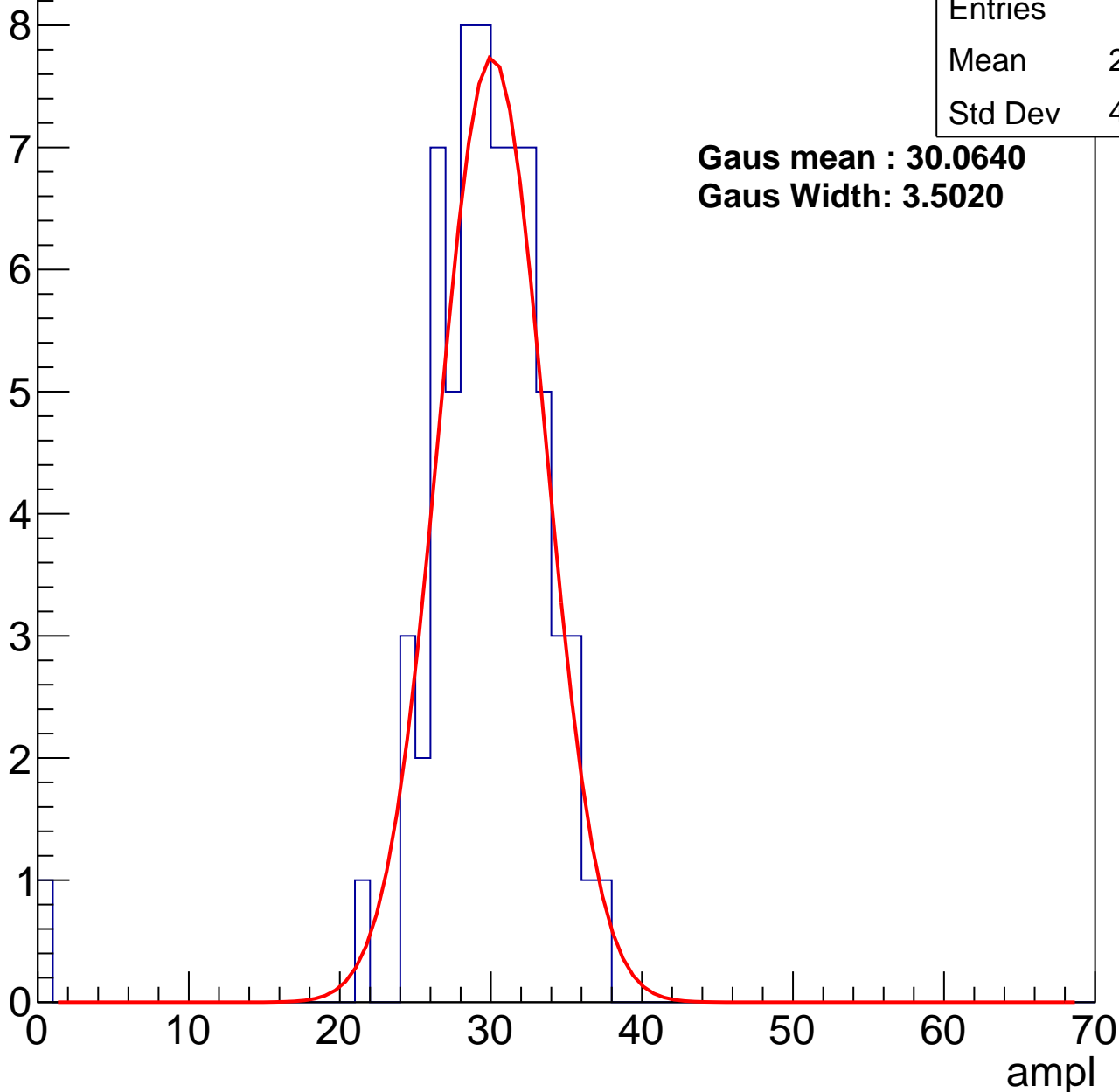
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	29.16
Std Dev	4.778

**Gaus mean : 30.0640**

**Gaus Width: 3.5020**



# B0L102S, U6-ch85, adc1

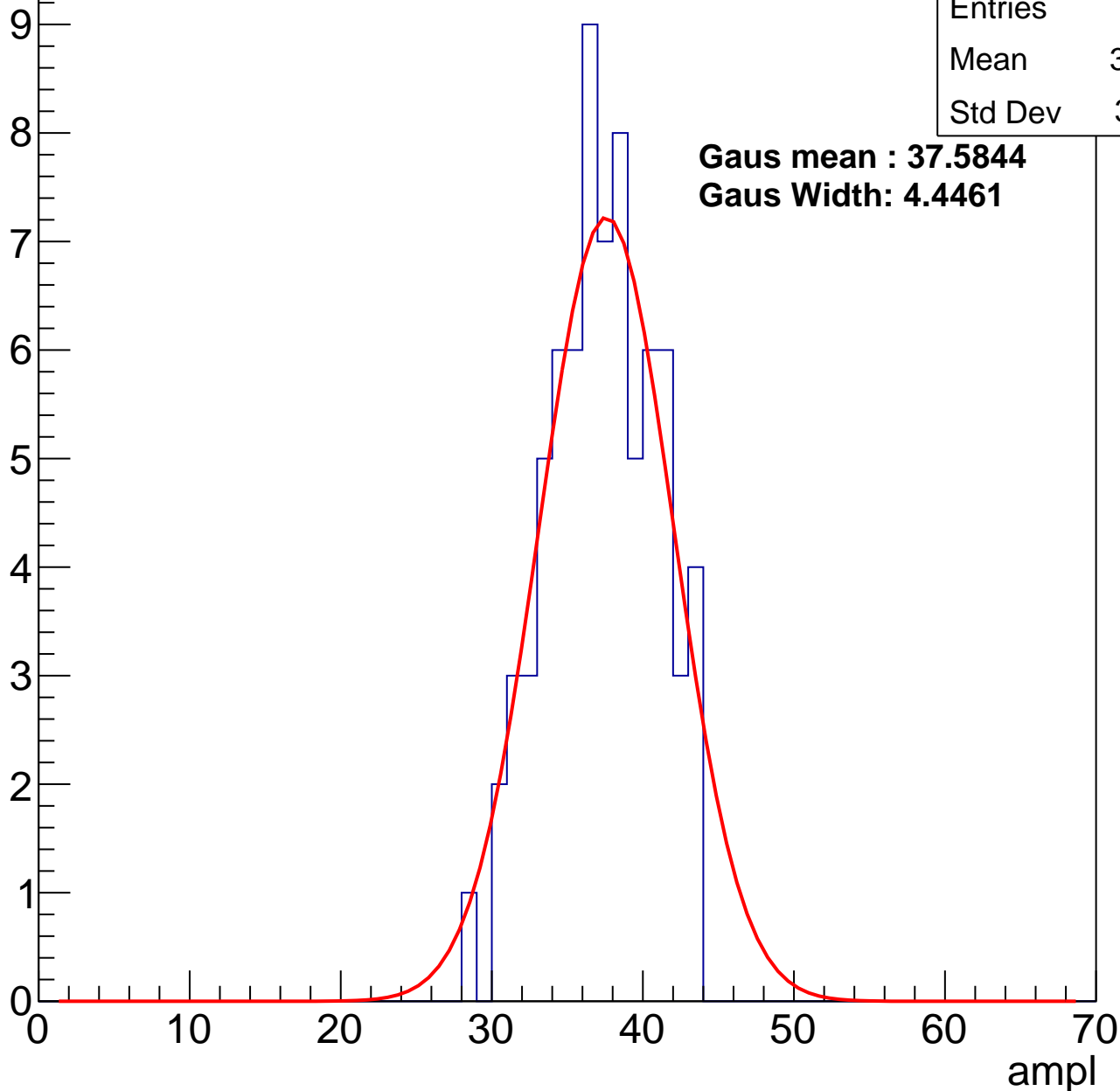
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	36.78
Std Dev	3.531

**Gaus mean : 37.5844**

**Gaus Width: 4.4461**



# B0L102S, U6-ch85, adc2

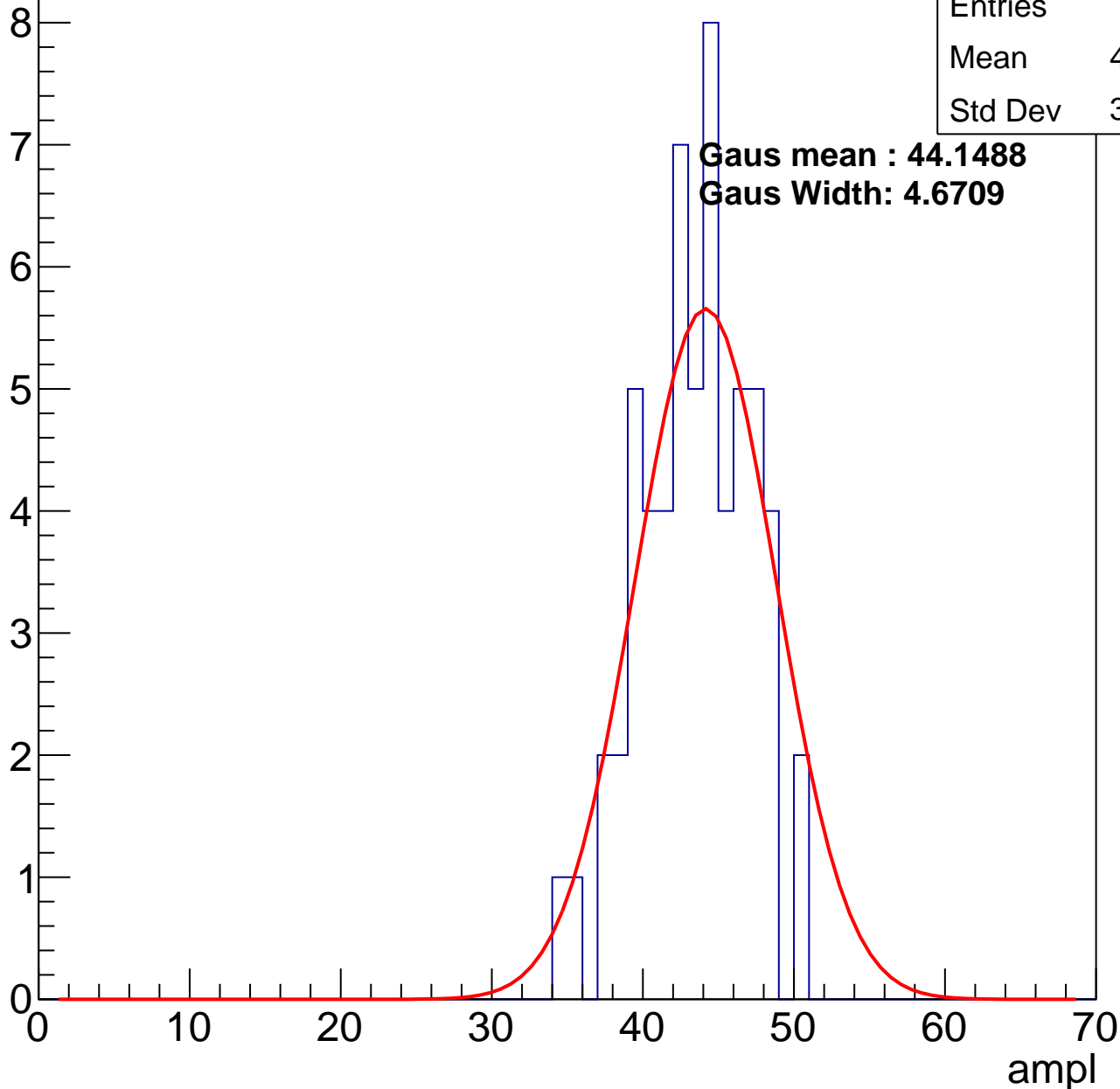
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	42.98
Std Dev	3.572

**Gaus mean : 44.1488**

**Gaus Width: 4.6709**

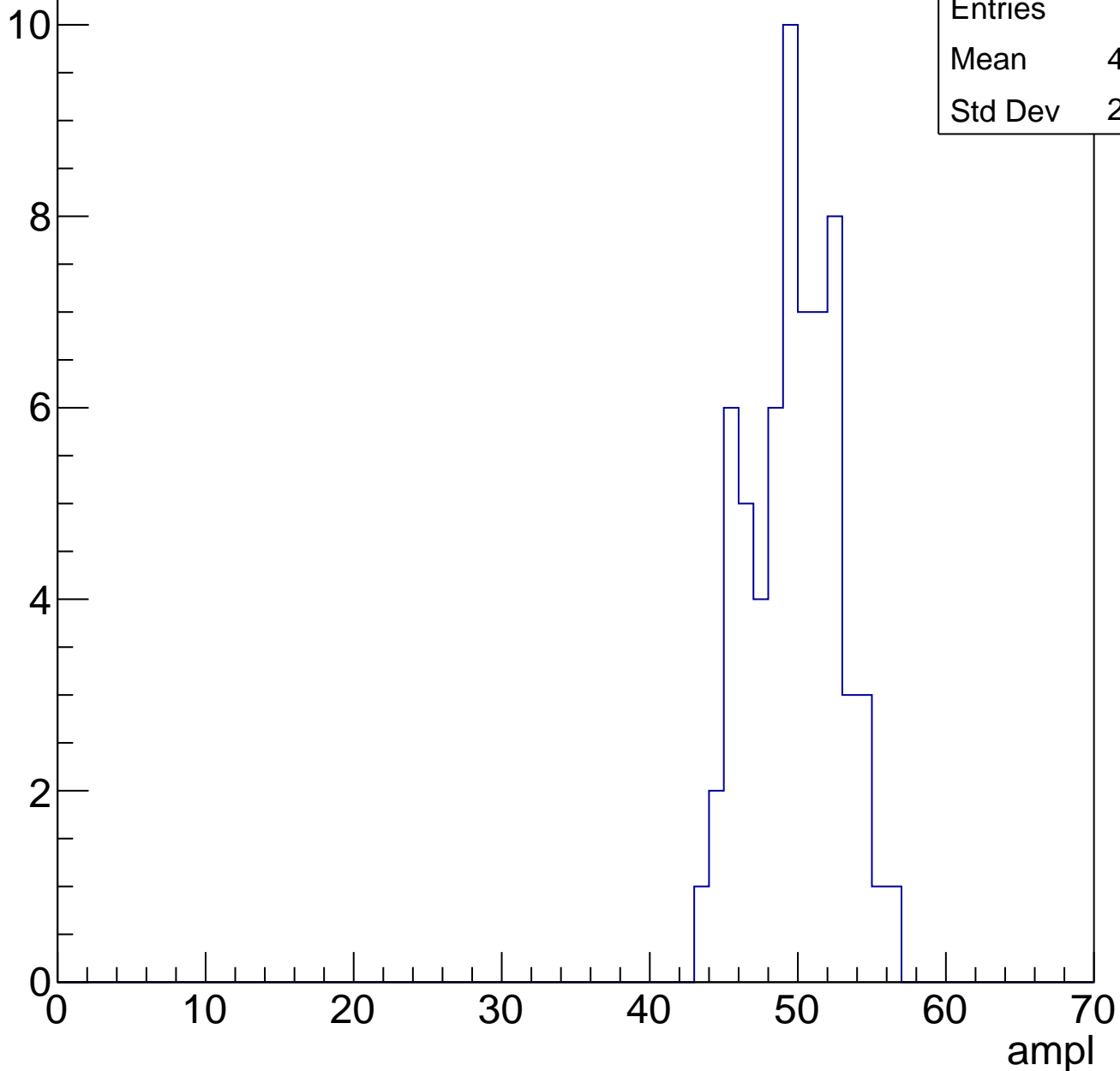


# B0L102S, U6-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

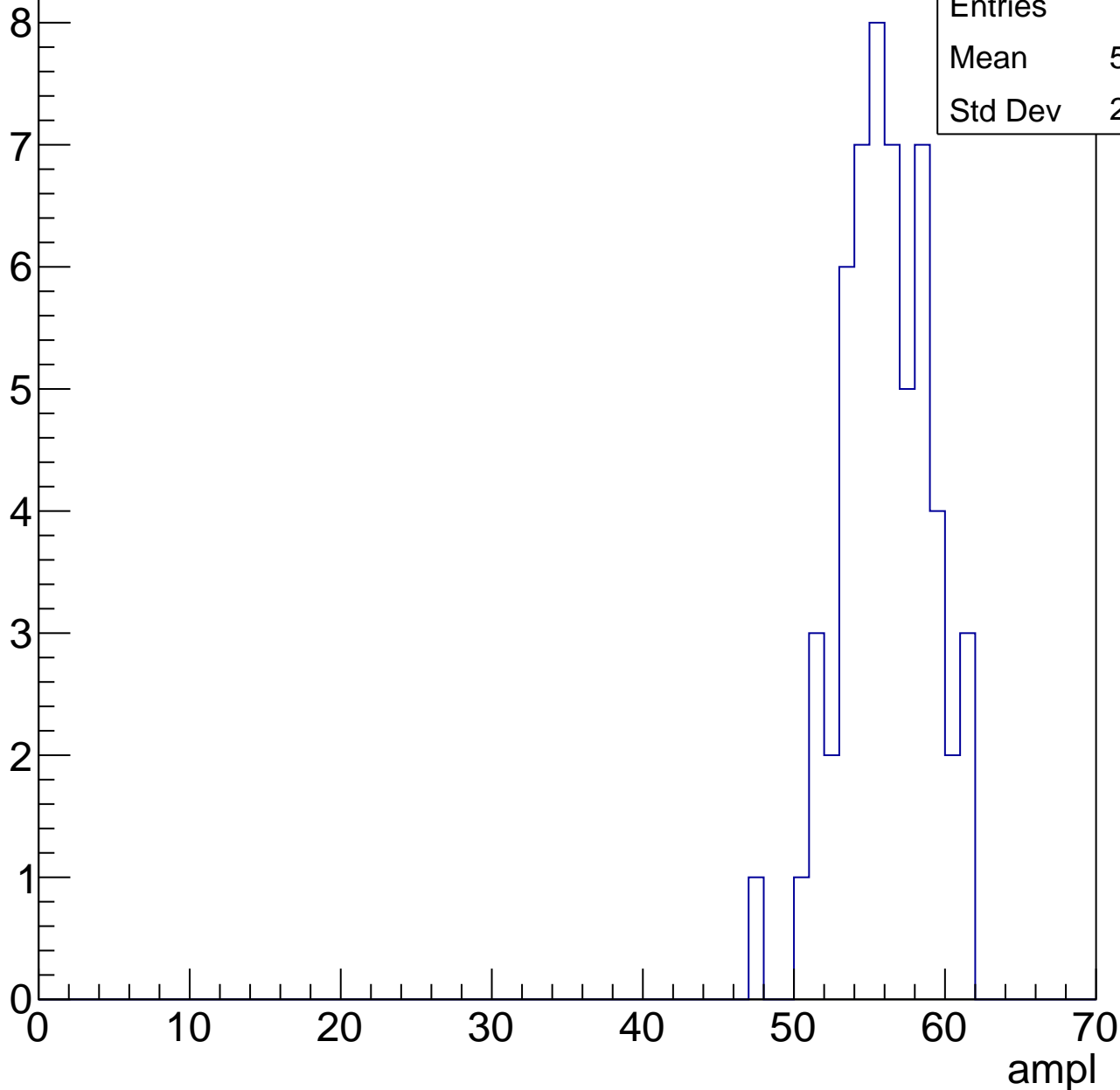
Entries	64
Mean	49.25
Std Dev	2.958



# B0L102S, U6-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	56
Mean	55.57
Std Dev	2.927

# B0L102S, U6-ch85, adc5

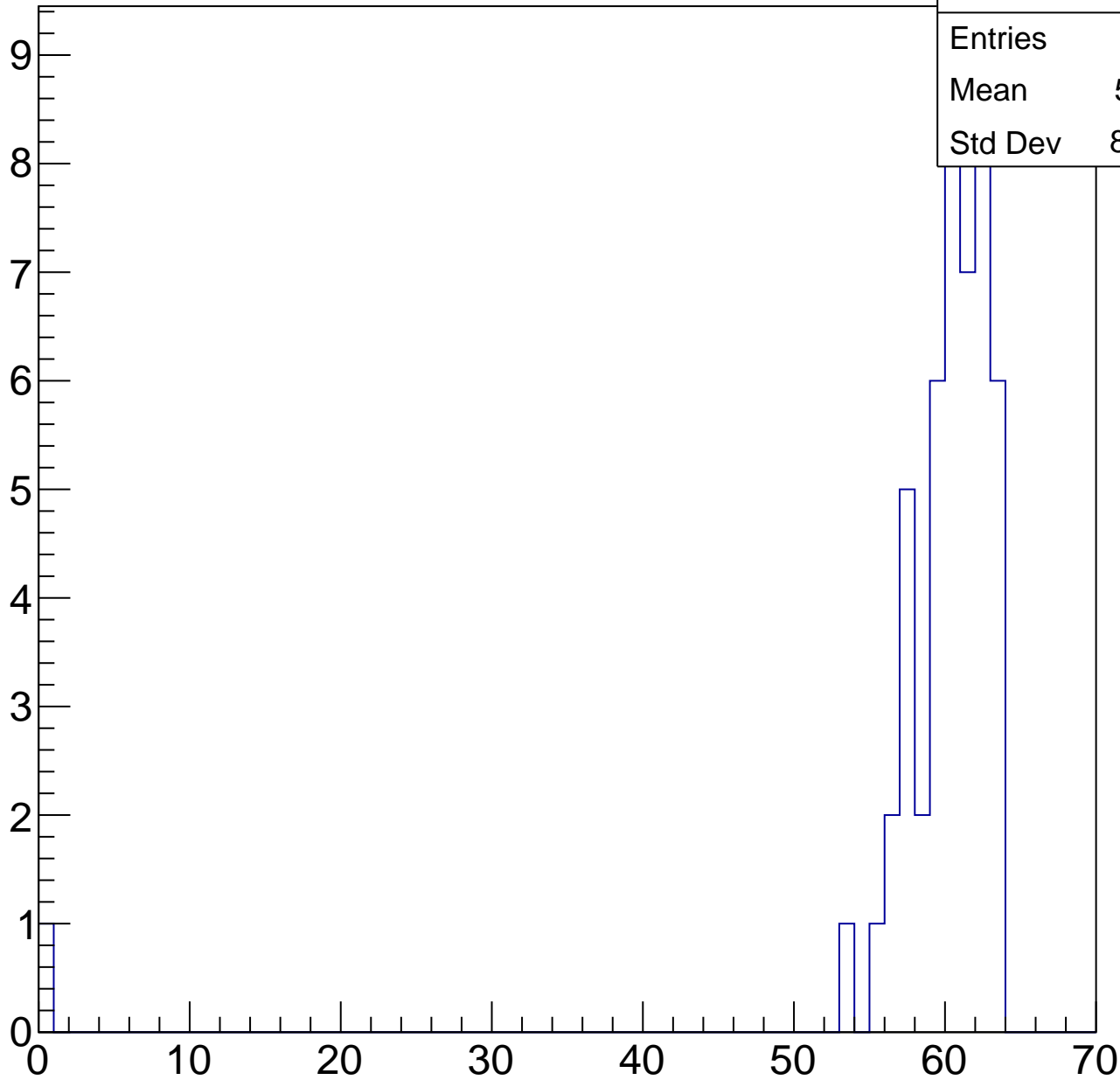
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	58.71
Std Dev	8.876

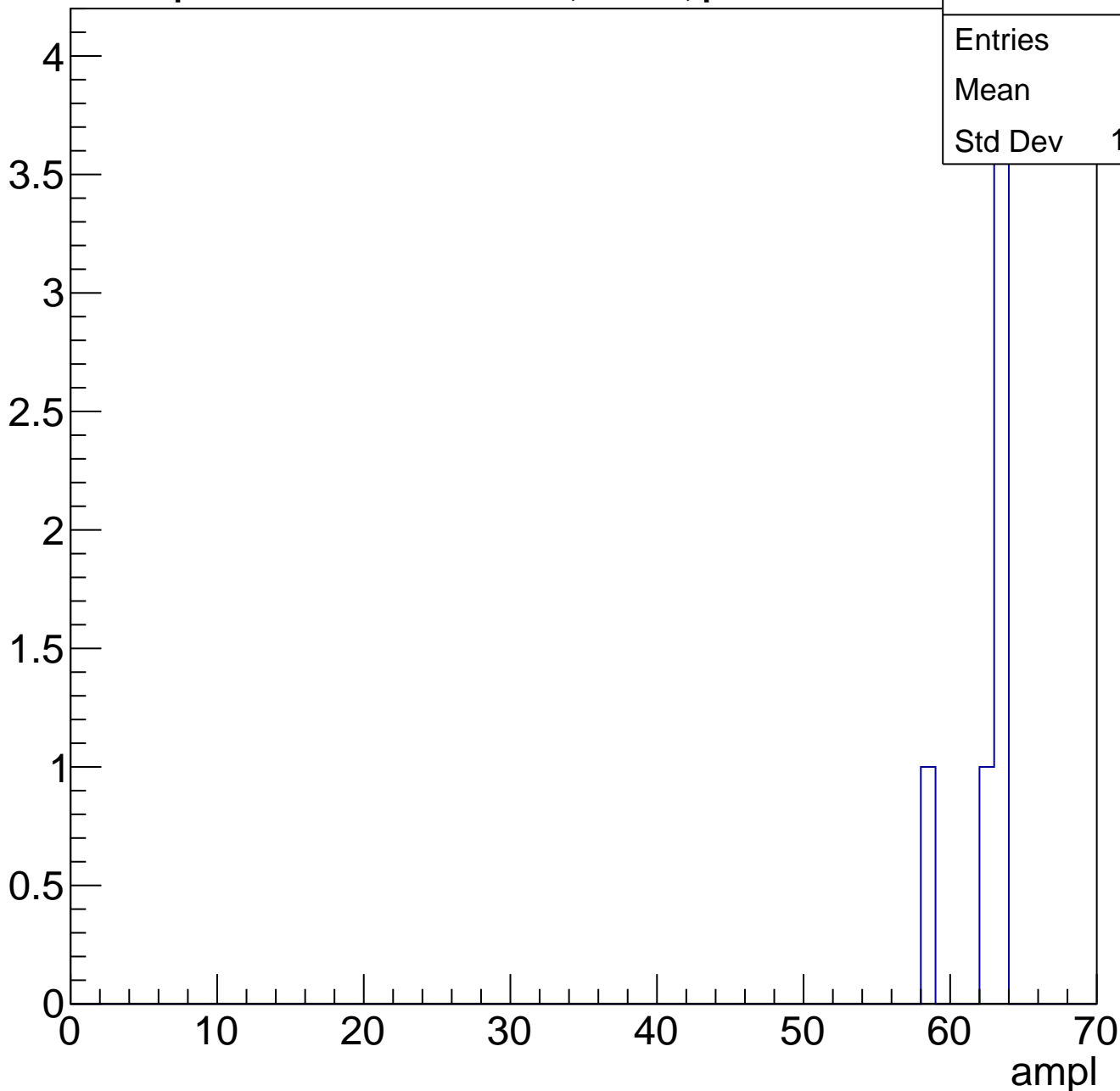
ampl



# B0L102S, U6-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch86, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	104
Mean	27.41
Std Dev	5.527

**Gaus mean : 27.8592**

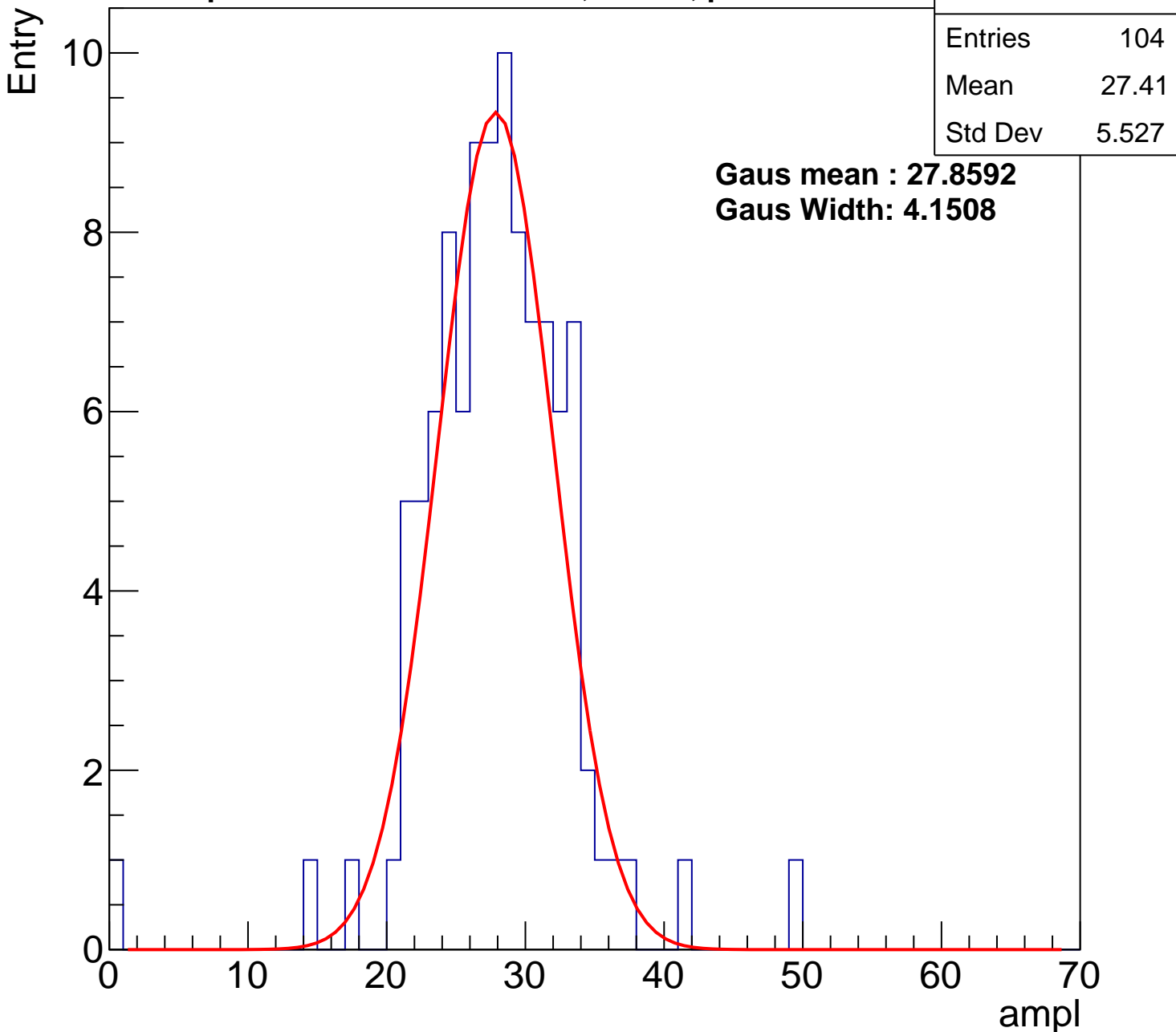
**Gaus Width: 4.1508**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch86, adc1

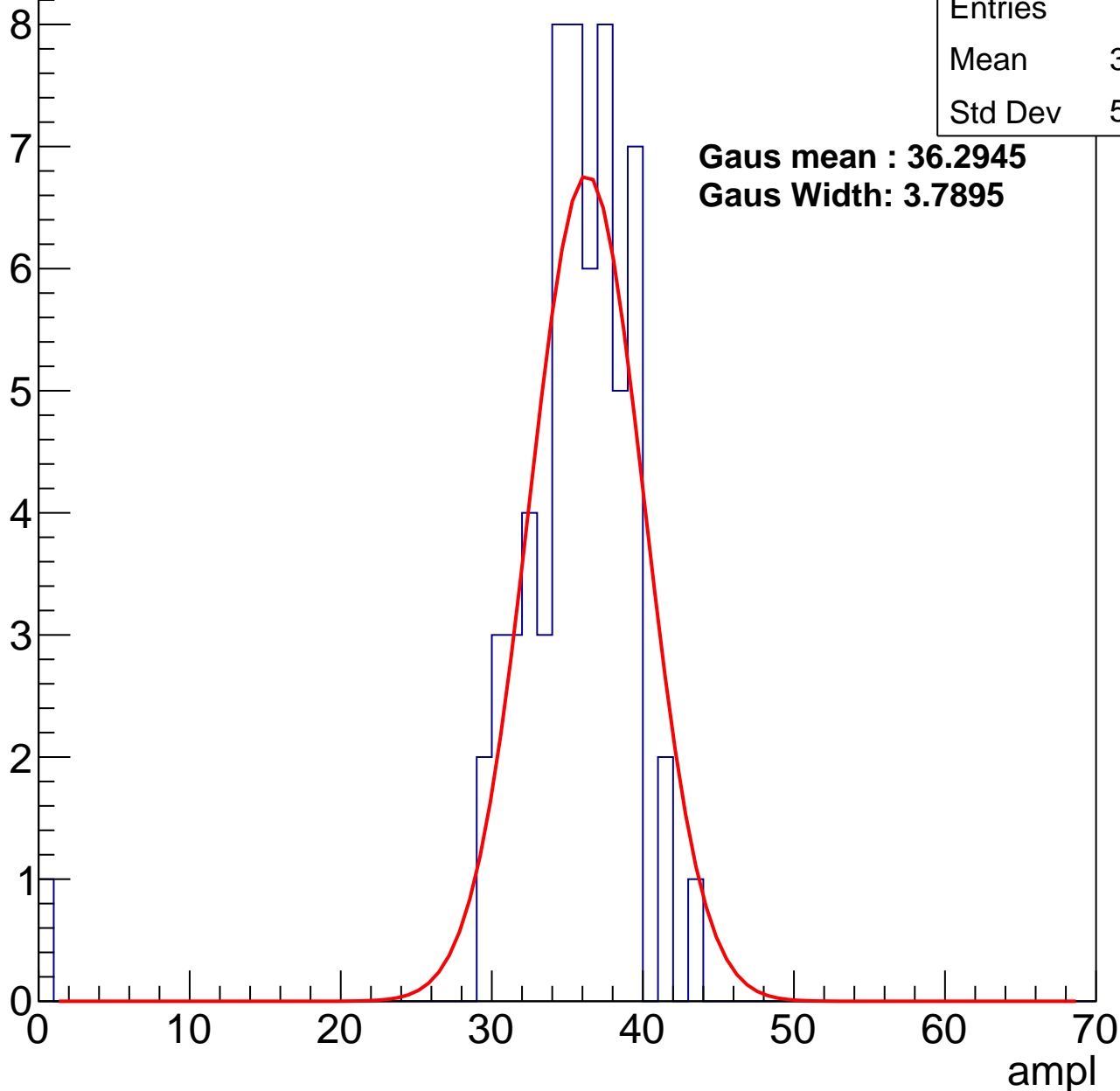
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	34.75
Std Dev	5.437

**Gaus mean : 36.2945**

**Gaus Width: 3.7895**



# B0L102S, U6-ch86, adc2

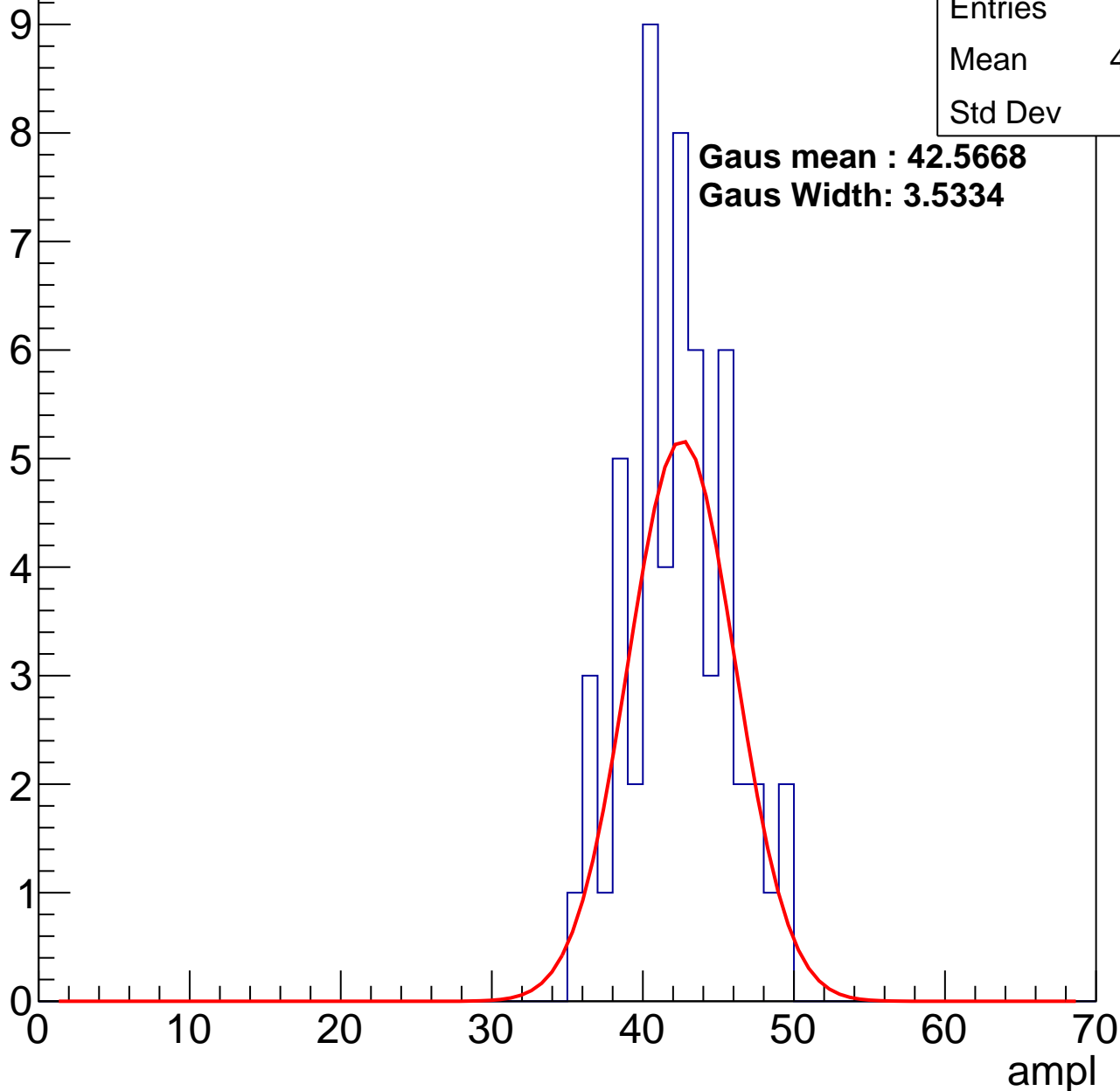
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	41.82
Std Dev	3.32

**Gaus mean : 42.5668**

**Gaus Width: 3.5334**

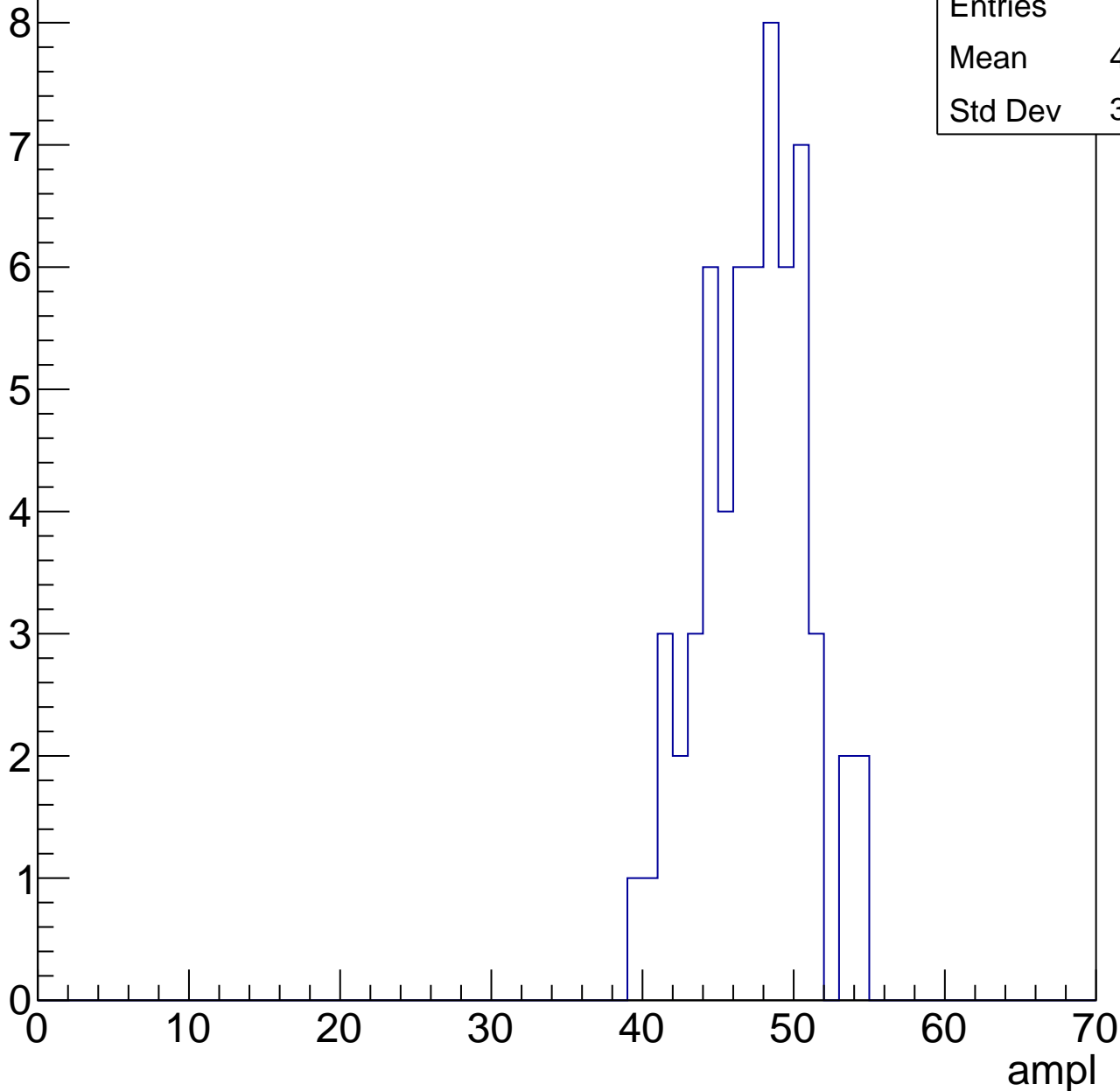


# B0L102S, U6-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	46.87
Std Dev	3.433

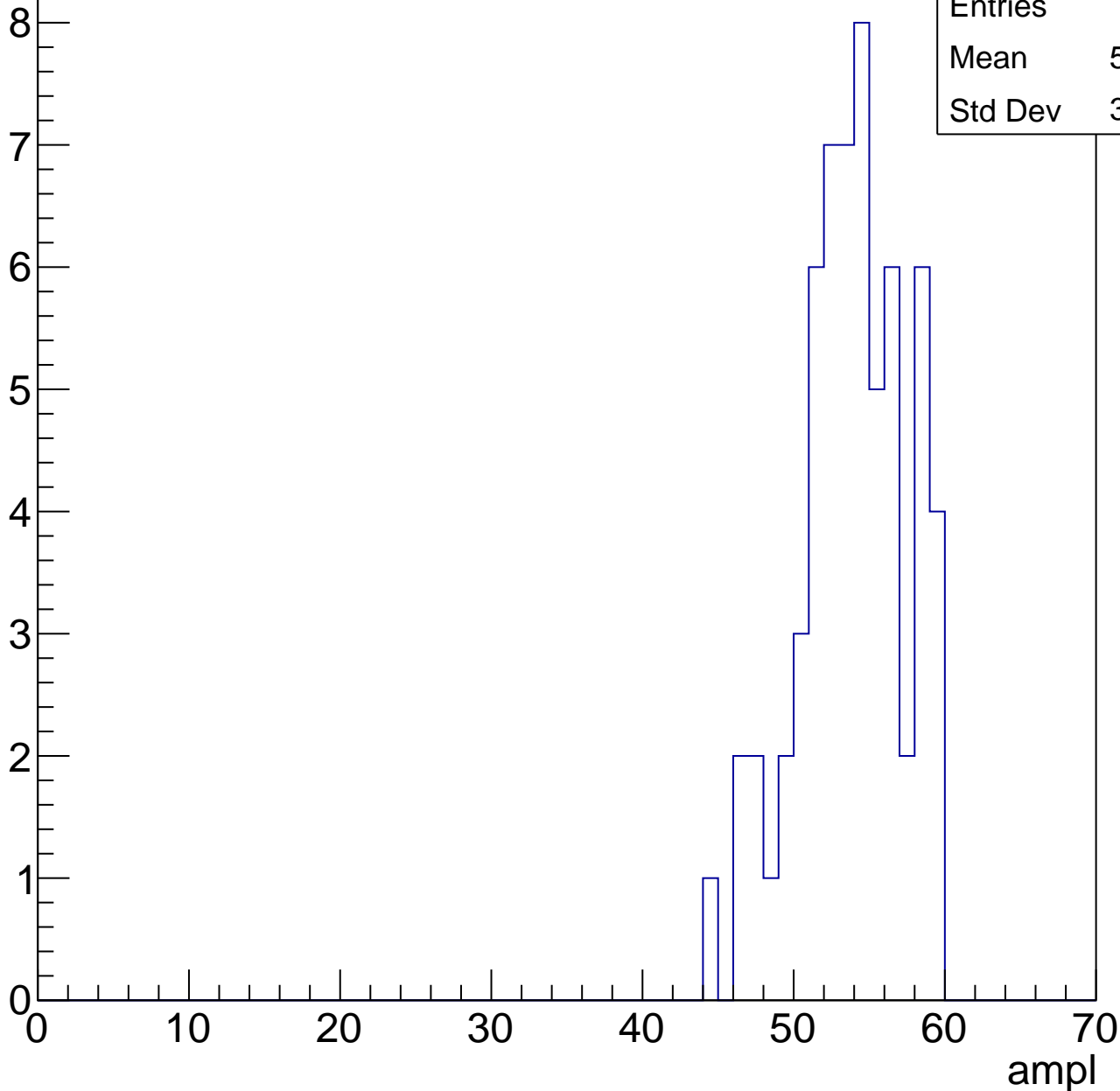


# B0L102S, U6-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	53.35
Std Dev	3.515



# B0L102S, U6-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

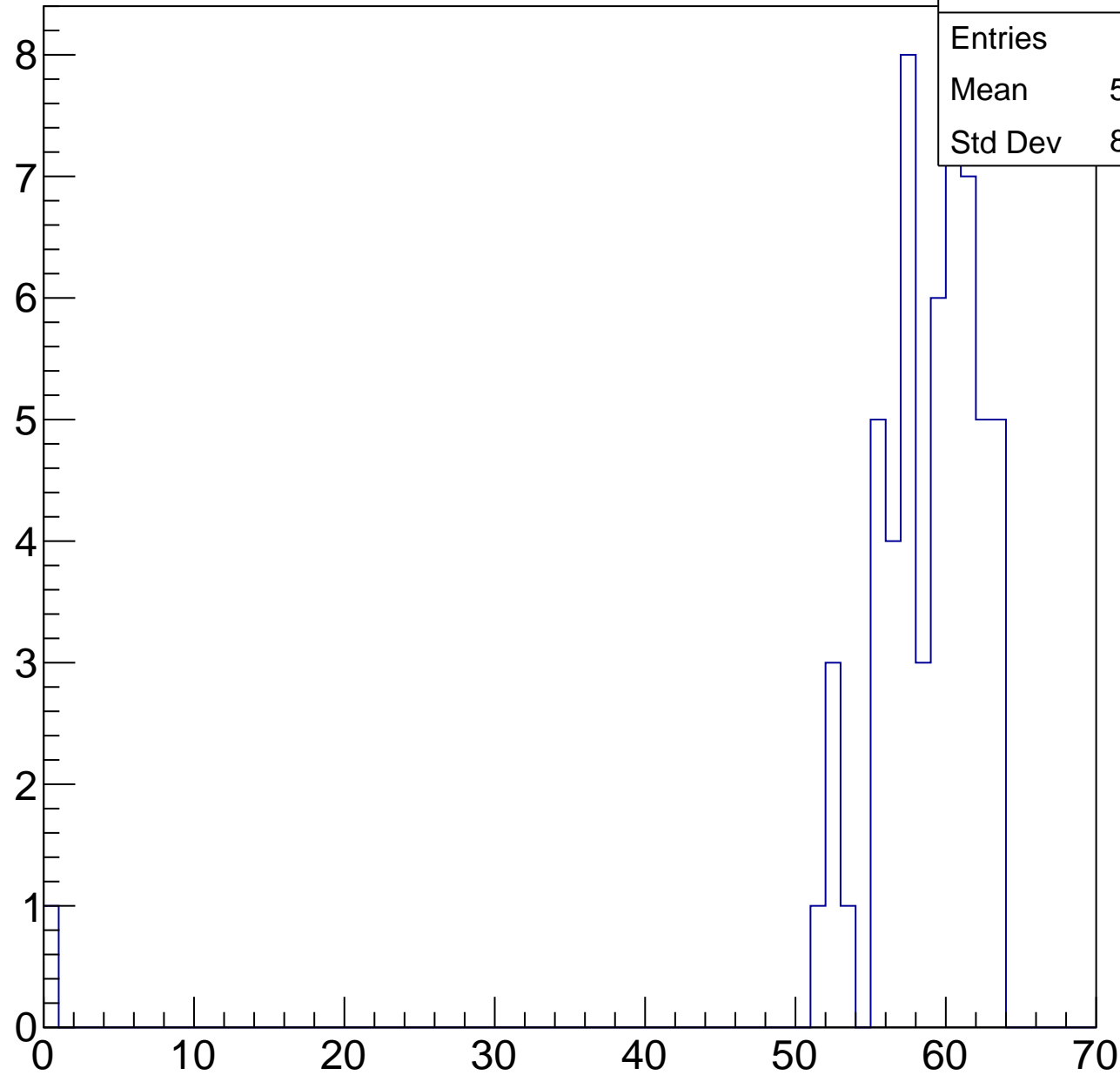
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	57
Mean	57.46
Std Dev	8.276

ampl

0 10 20 30 40 50 60 70

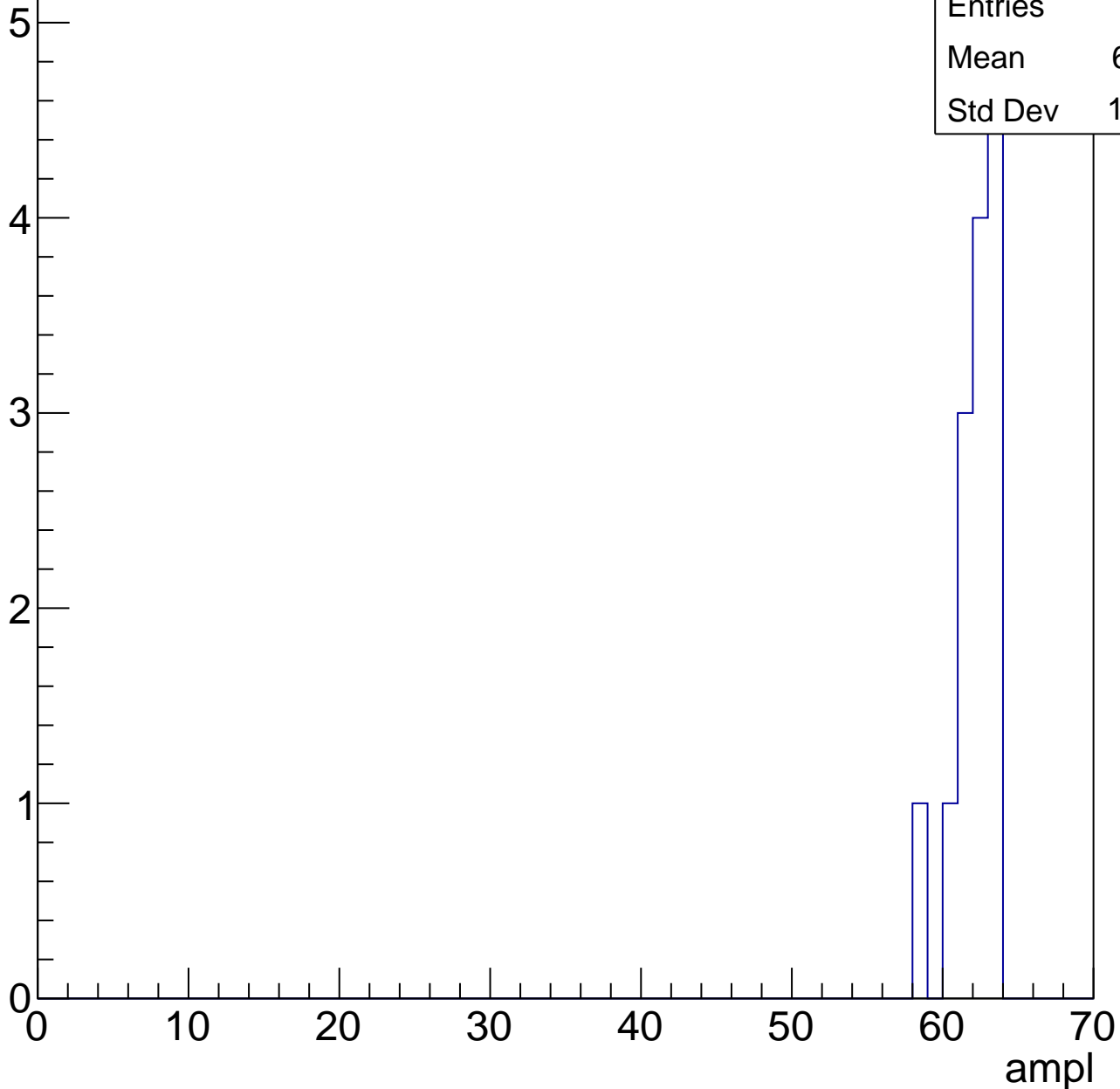


# B0L102S, U6-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	14
Mean	61.71
Std Dev	1.385

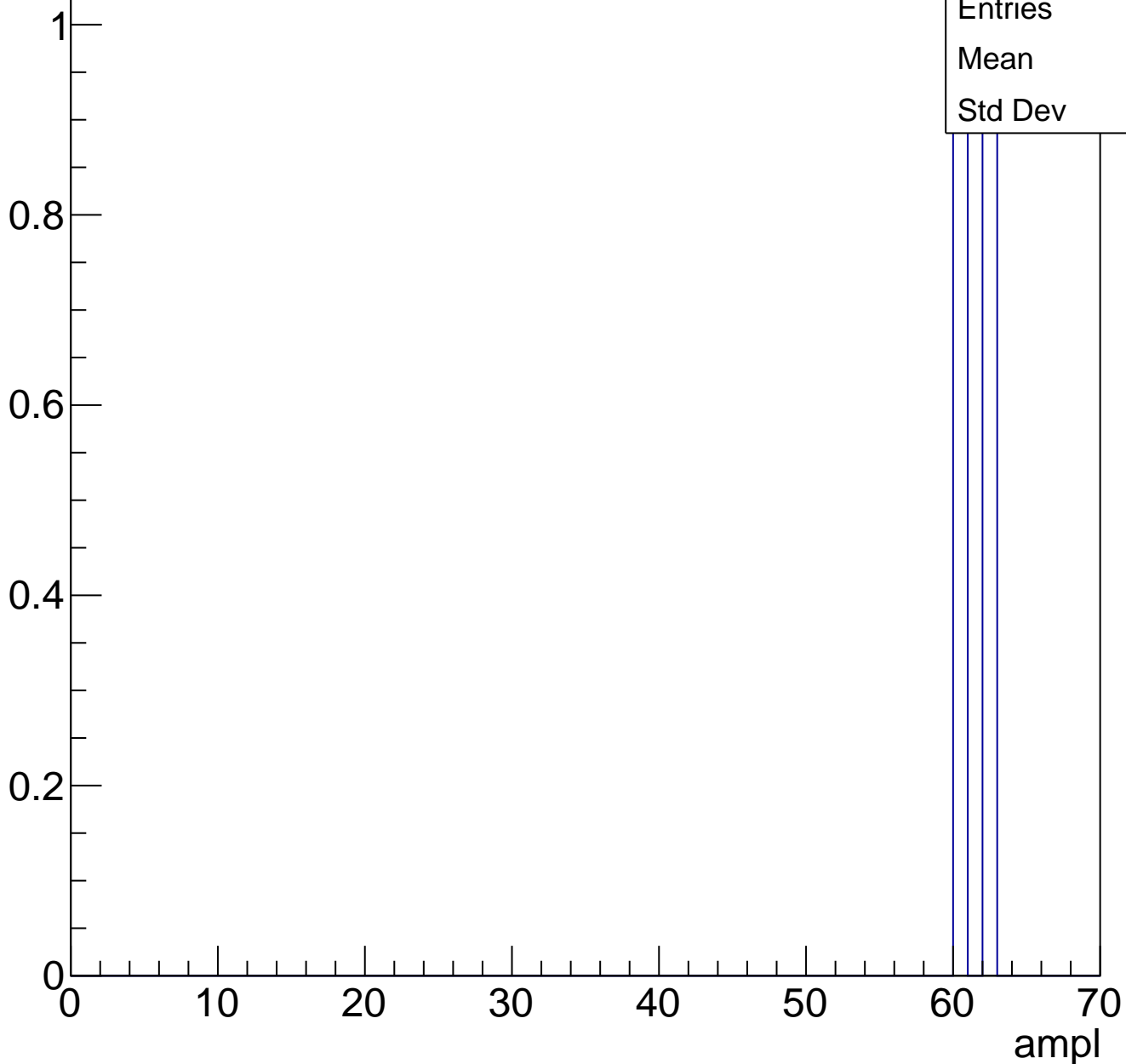




# B0L102S, U6-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch87, adc0

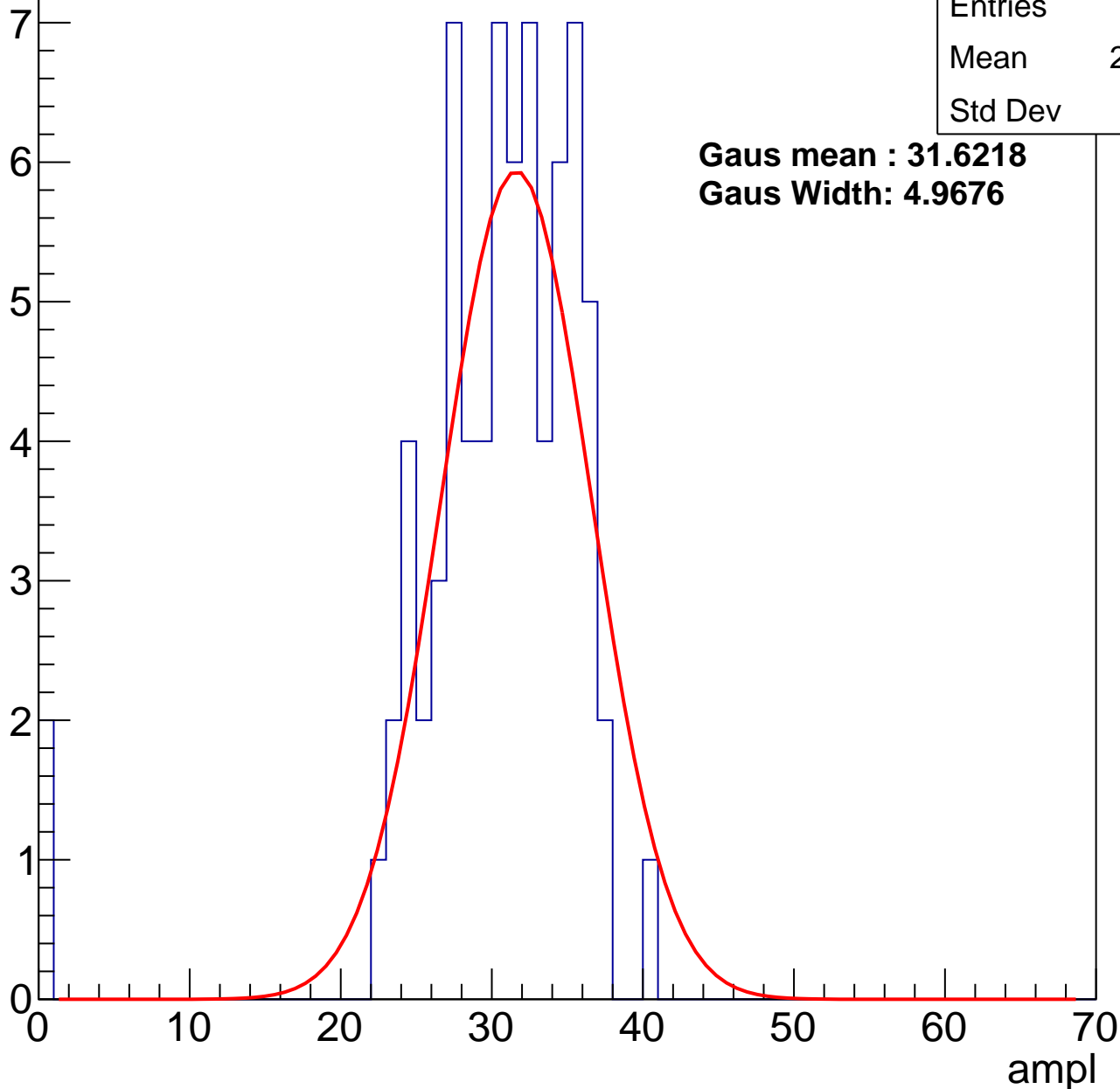
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	29.78
Std Dev	6.37

**Gaus mean : 31.6218**

**Gaus Width: 4.9676**



# B0L102S, U6-ch87, adc1

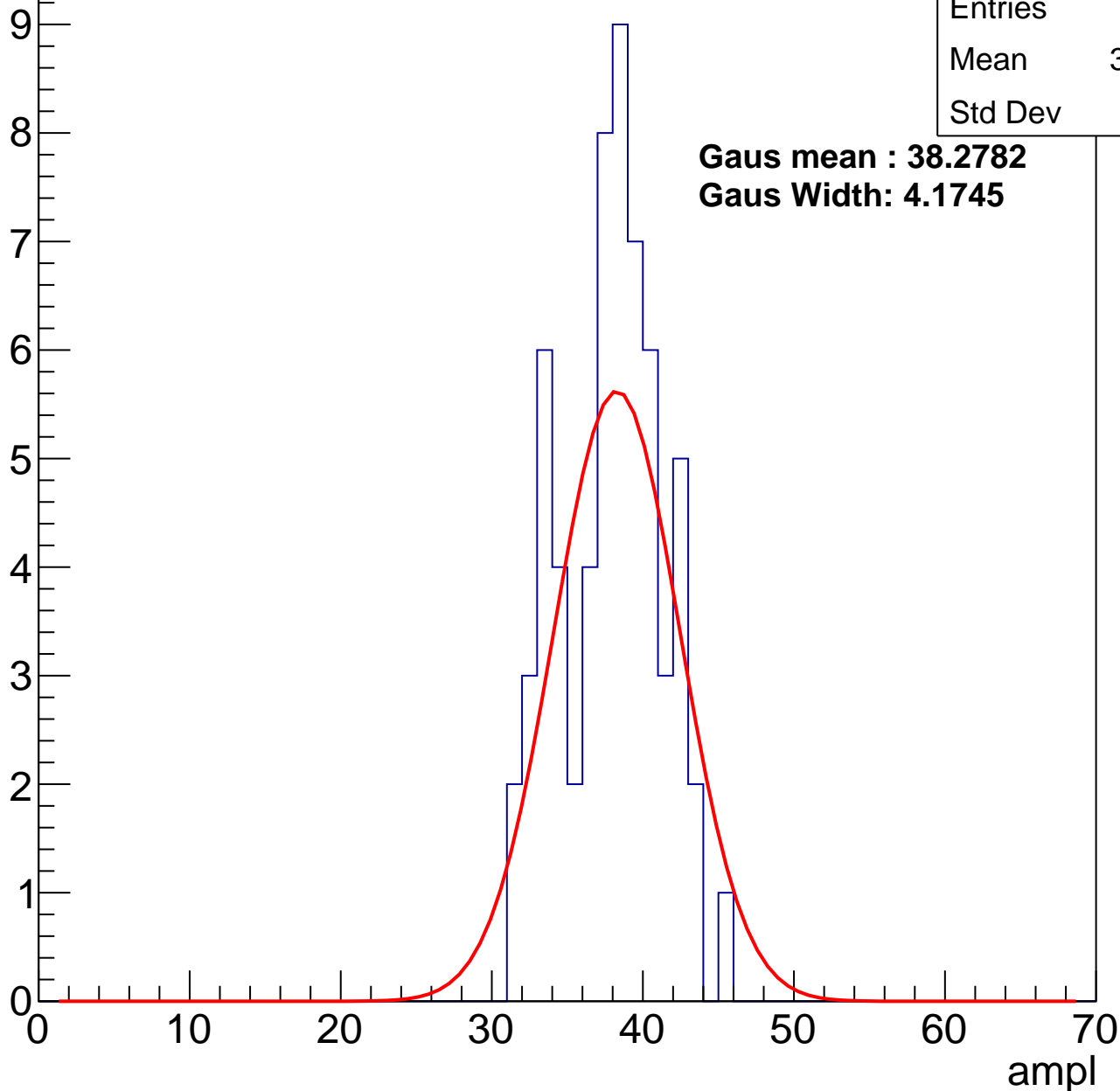
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	37.44
Std Dev	3.31

**Gaus mean : 38.2782**

**Gaus Width: 4.1745**



# B0L102S, U6-ch87, adc2

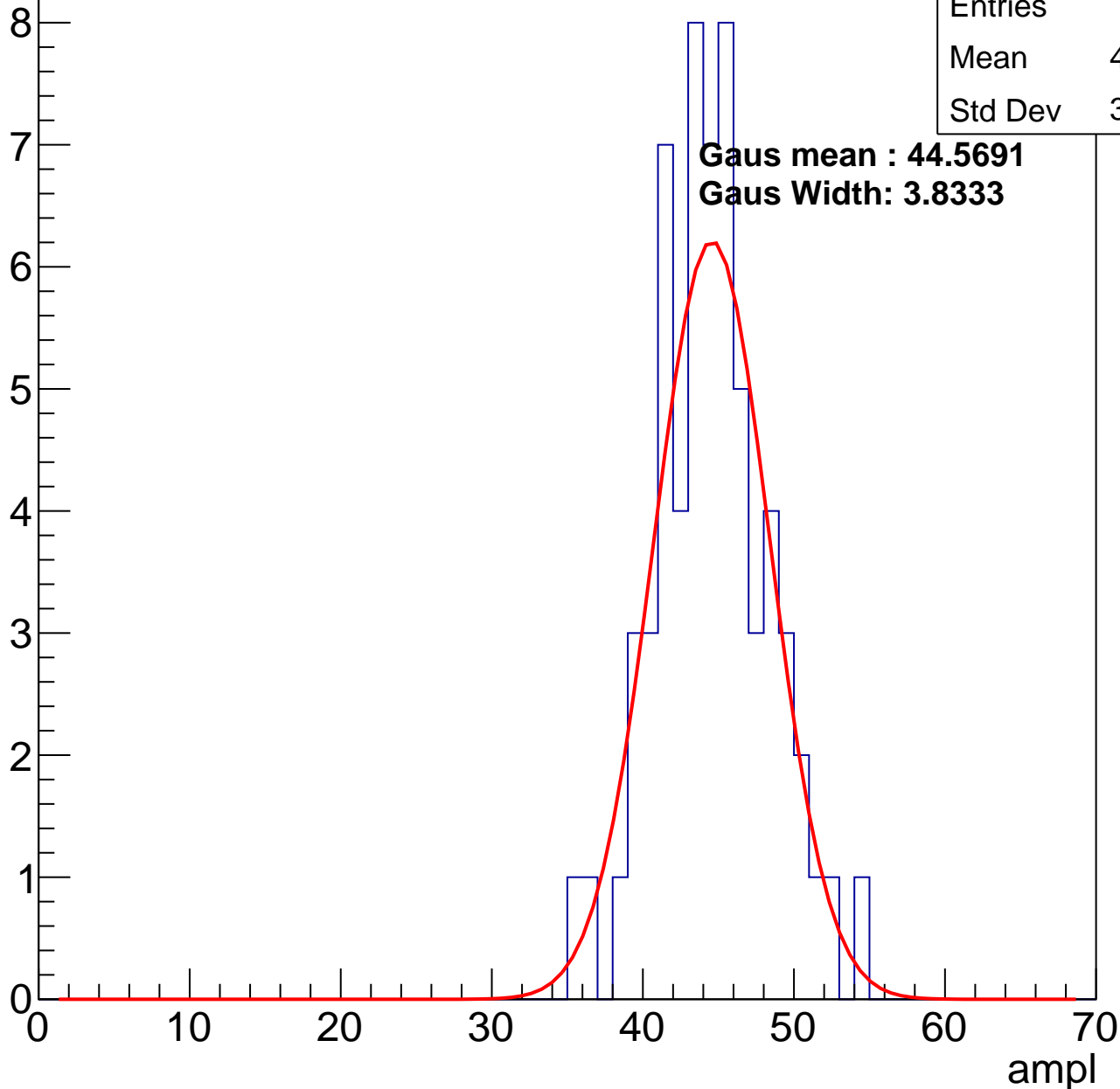
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	44.13
Std Dev	3.714

**Gaus mean : 44.5691**

**Gaus Width: 3.8333**

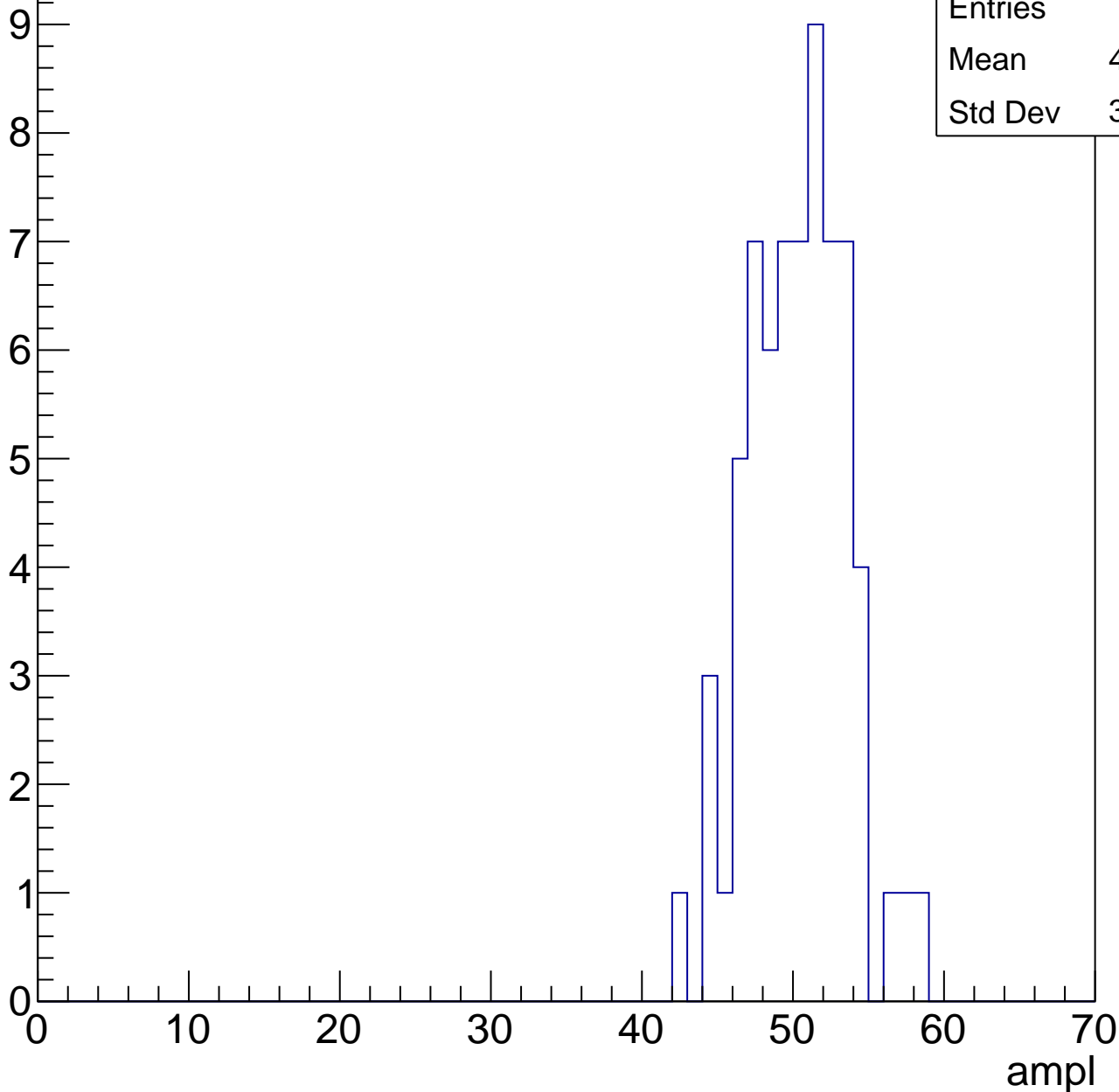


# B0L102S, U6-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

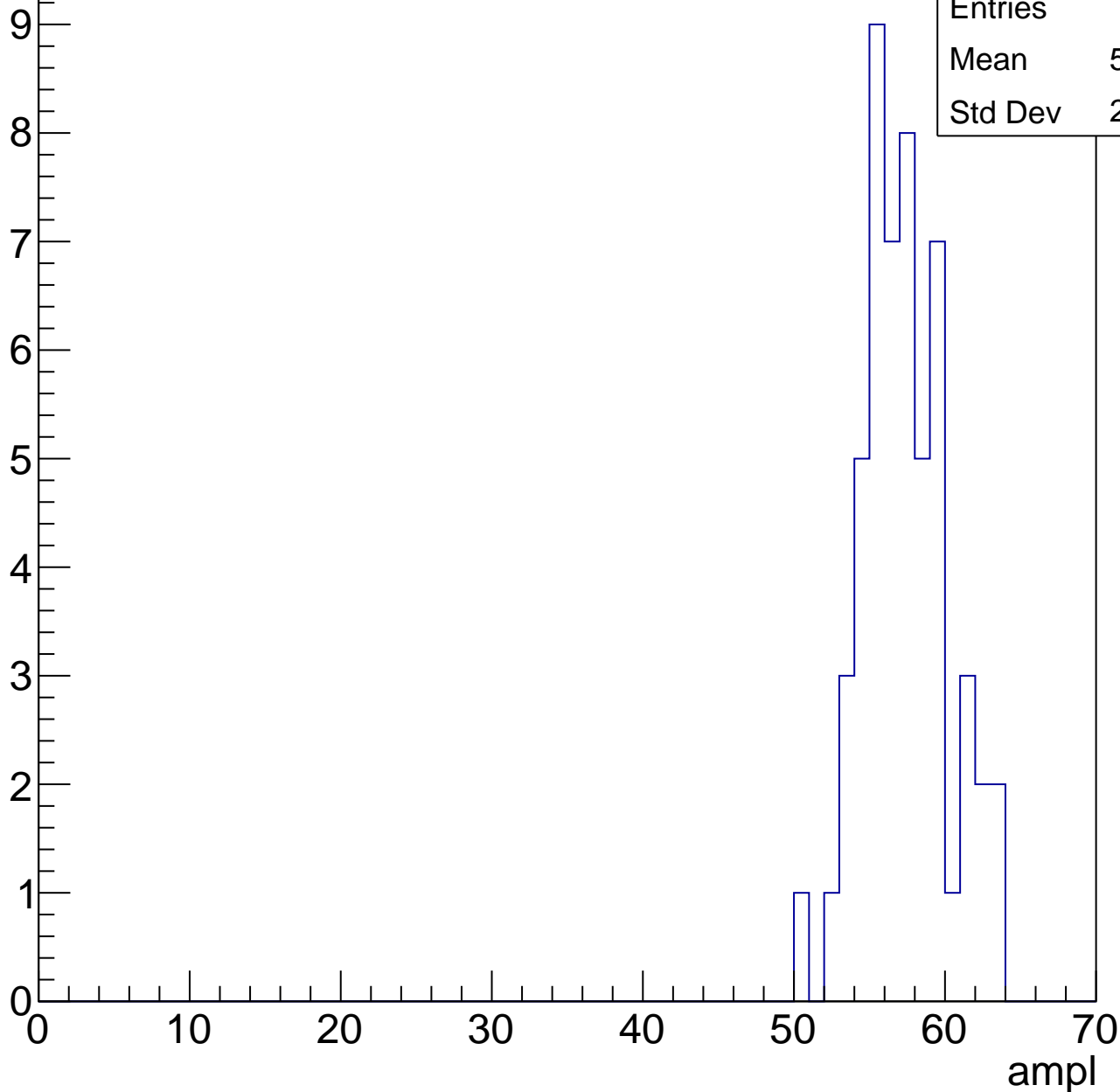
Entries	67
Mean	49.85
Std Dev	3.192



# B0L102S, U6-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch87, adc5

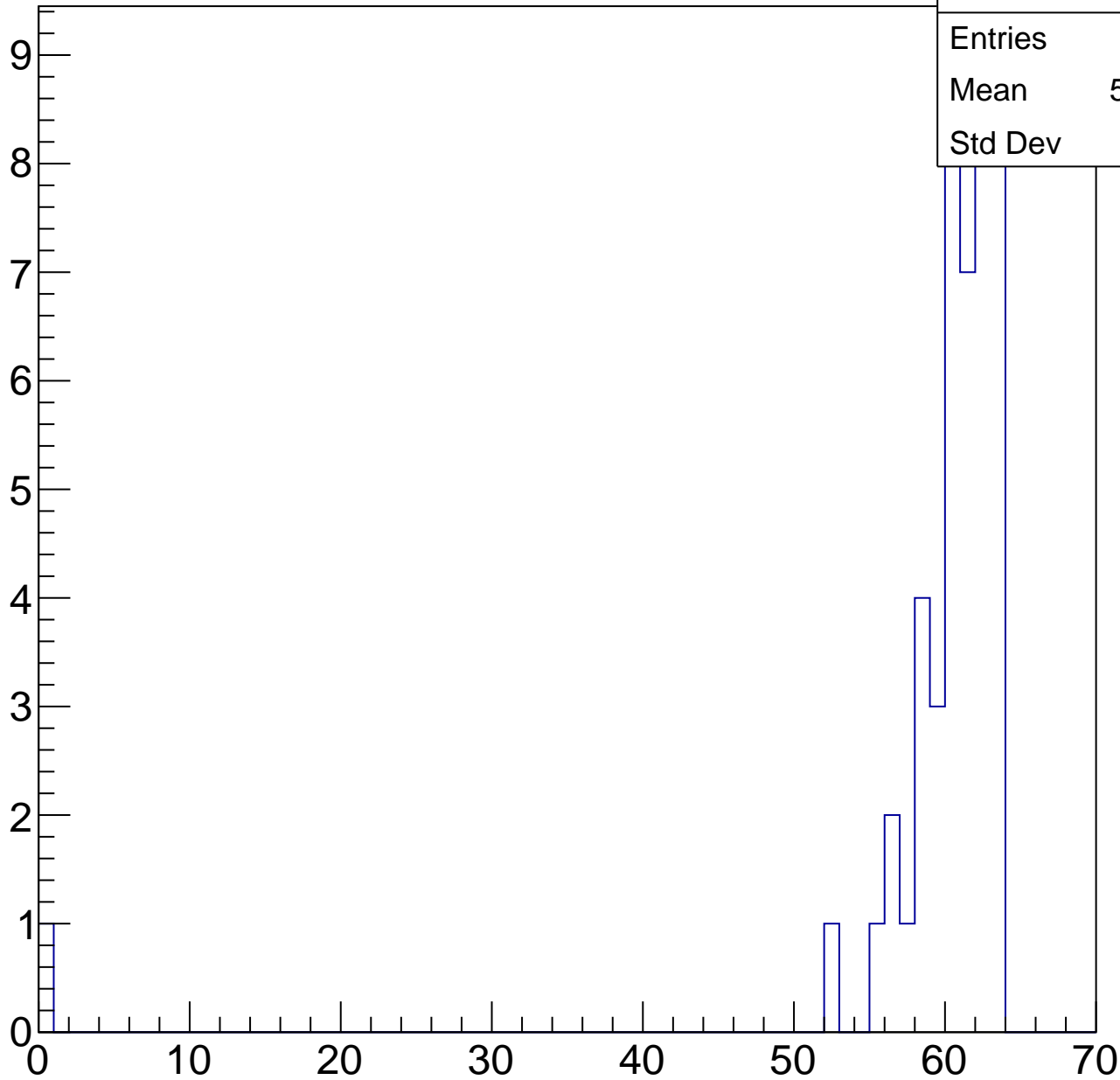
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	58.93
Std Dev	9.2

ampl



# B0L102S, U6-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L102S, U6-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch88, adc0

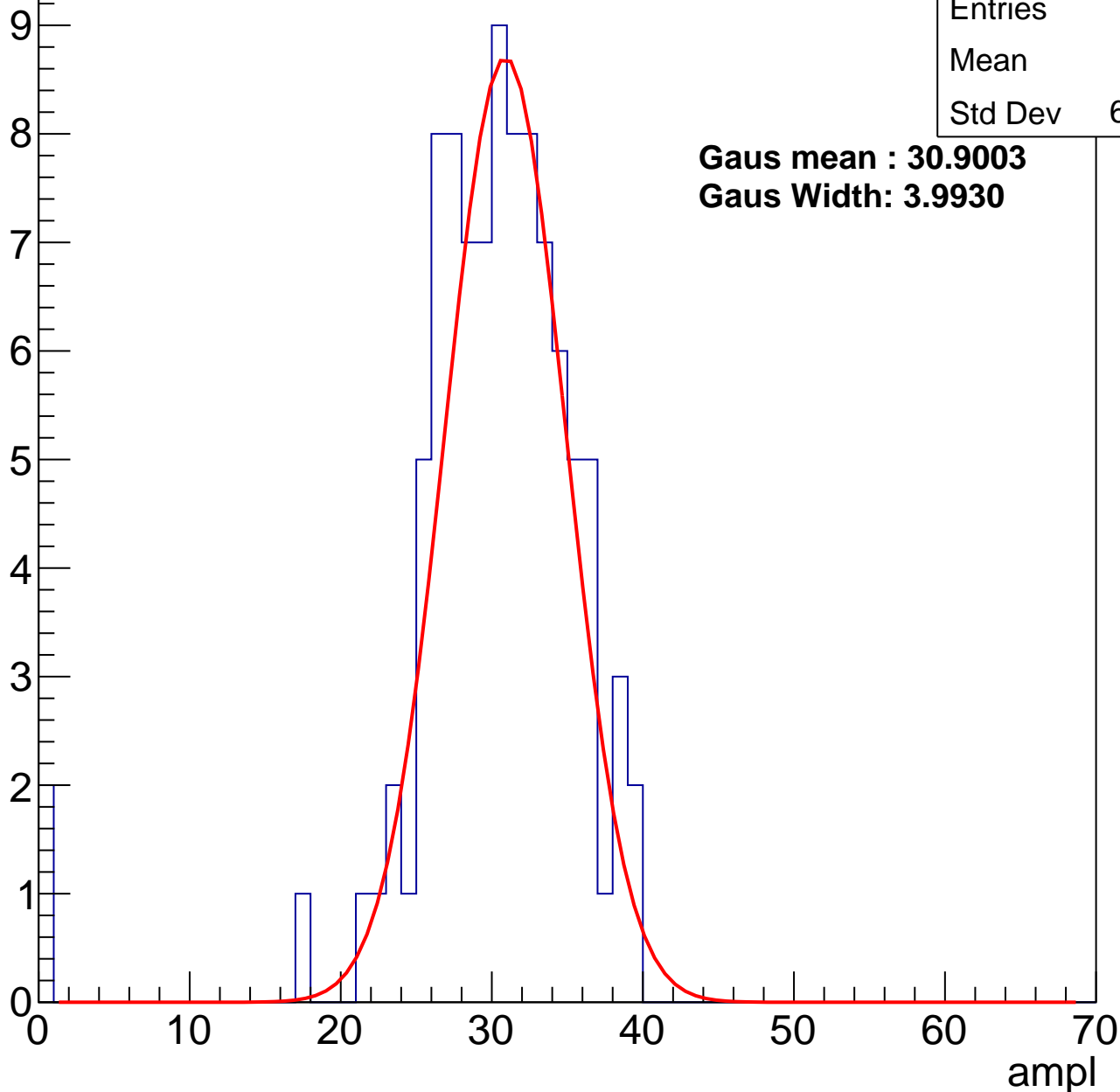
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	97
Mean	29.6
Std Dev	6.006

**Gaus mean : 30.9003**

**Gaus Width: 3.9930**



# B0L102S, U6-ch88, adc1

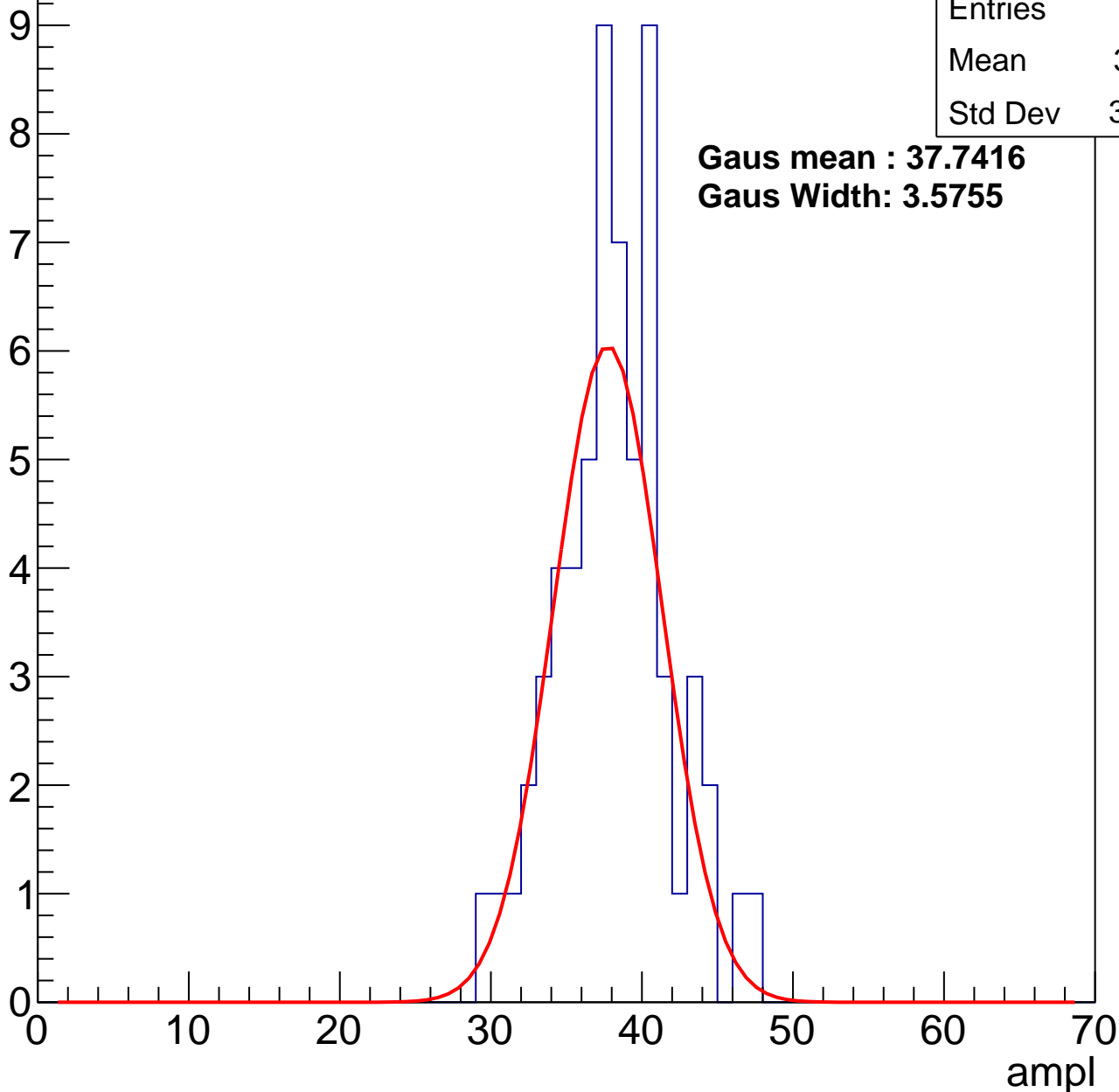
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	37.71
Std Dev	3.683

**Gaus mean : 37.7416**

**Gaus Width: 3.5755**



# B0L102S, U6-ch88, adc2

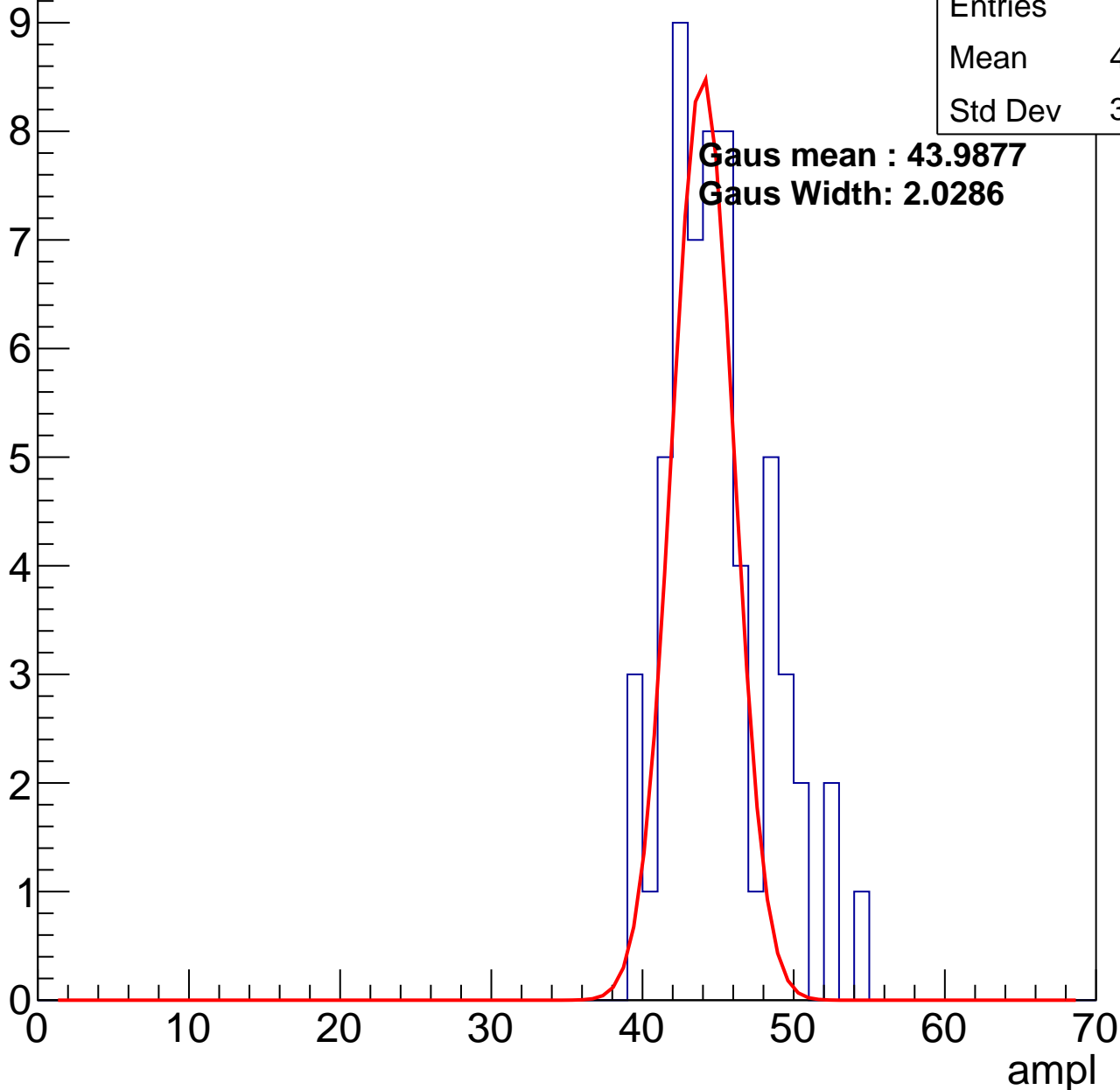
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	44.56
Std Dev	3.315

**Gaus mean : 43.9877**

**Gaus Width: 2.0286**

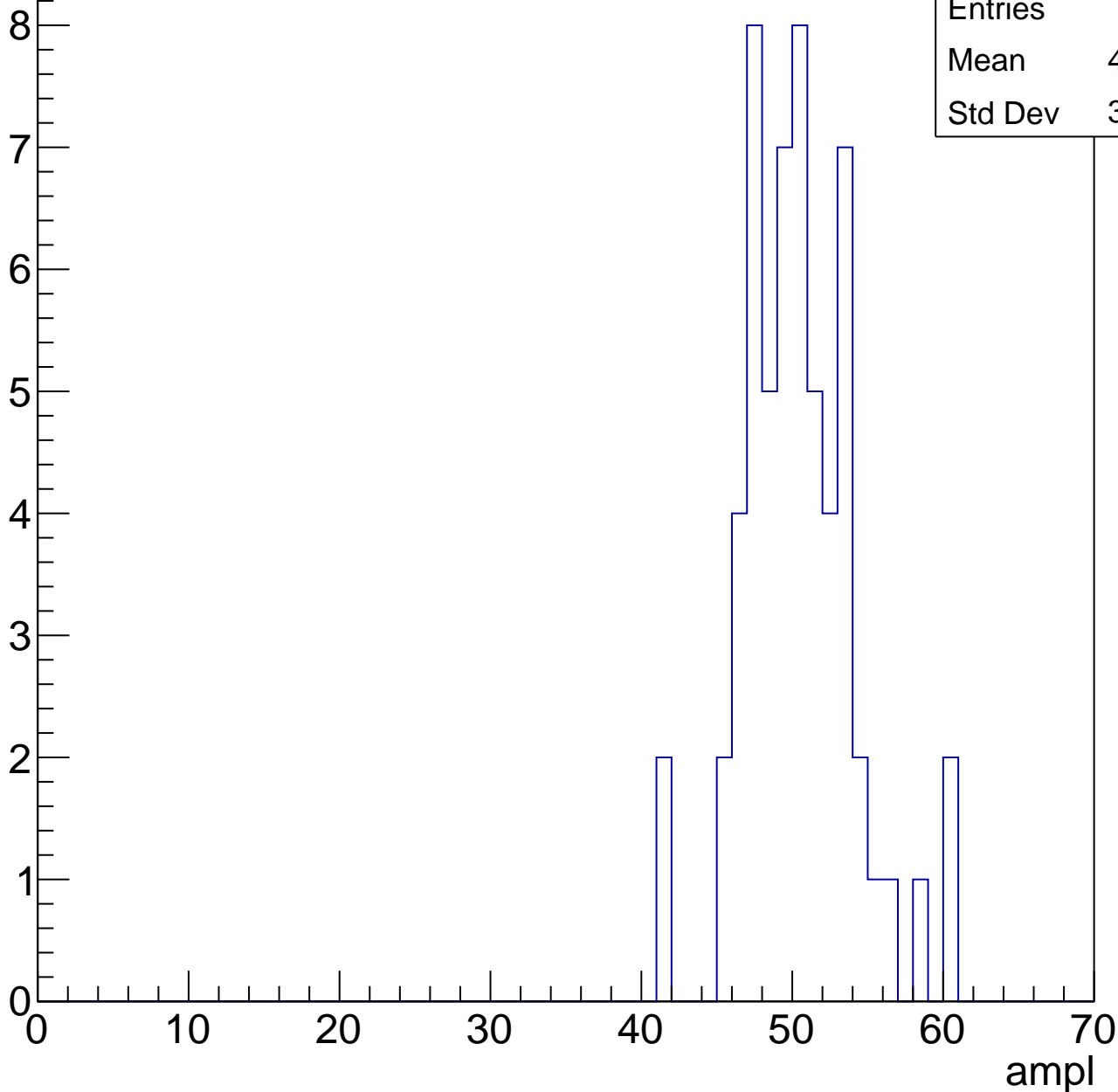


# B0L102S, U6-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	49.93
Std Dev	3.718

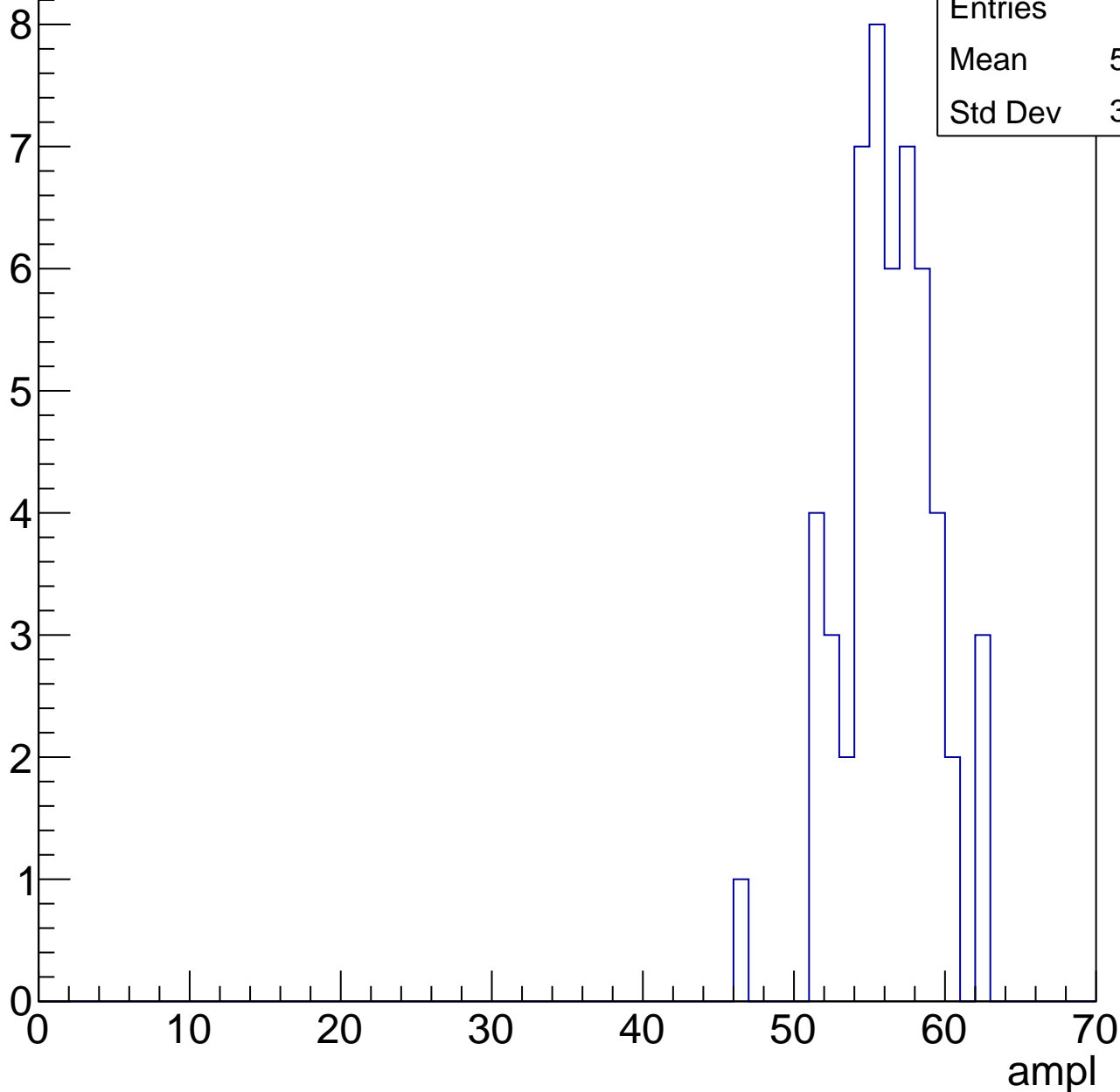


# B0L102S, U6-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	55.75
Std Dev	3.089



# B0L102S, U6-ch88, adc5

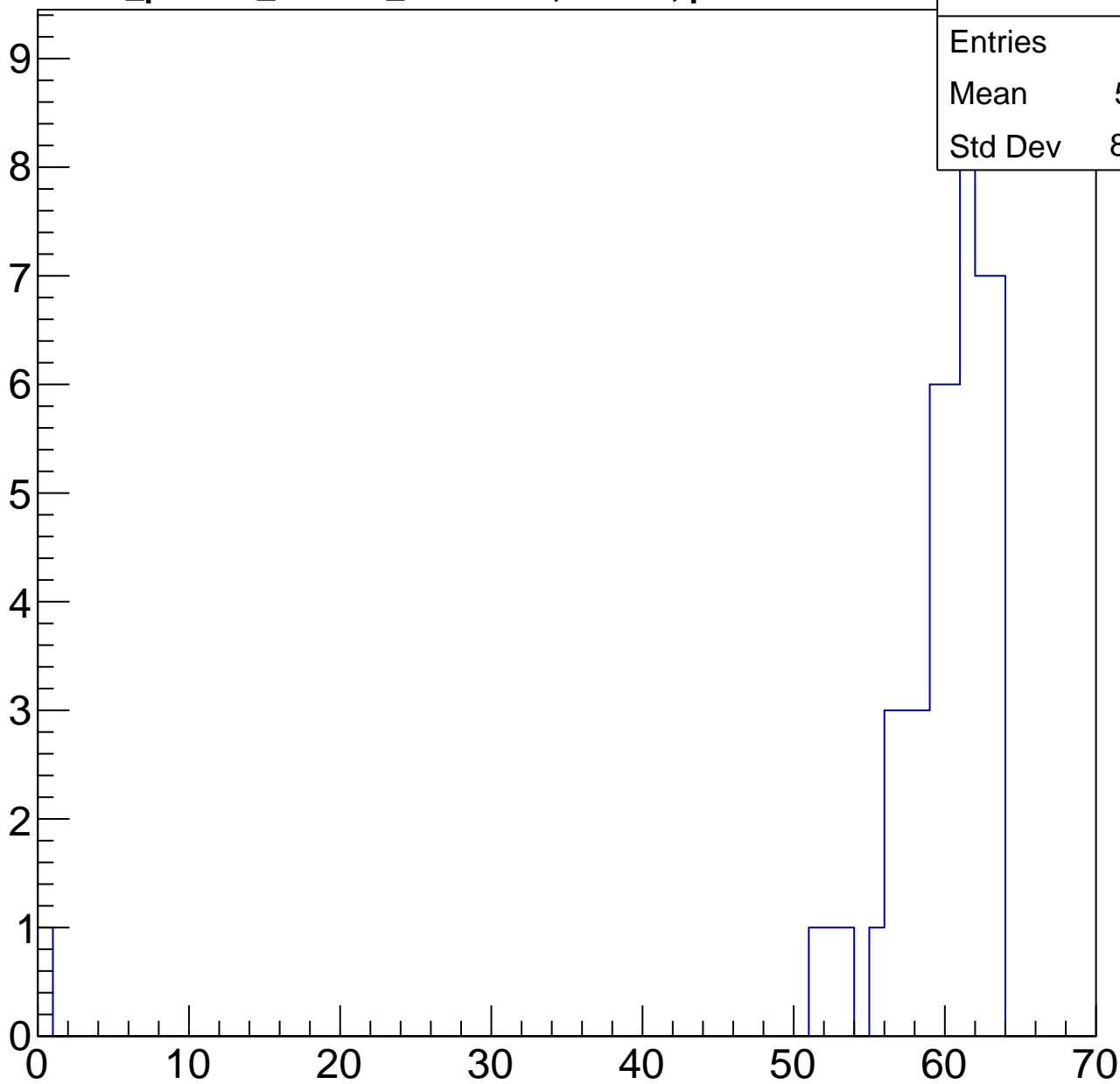
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.41
Std Dev	8.908

ampl



# B0L102S, U6-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch89, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	84
Mean	27.69
Std Dev	4.857

**Gaus mean : 28.3132**

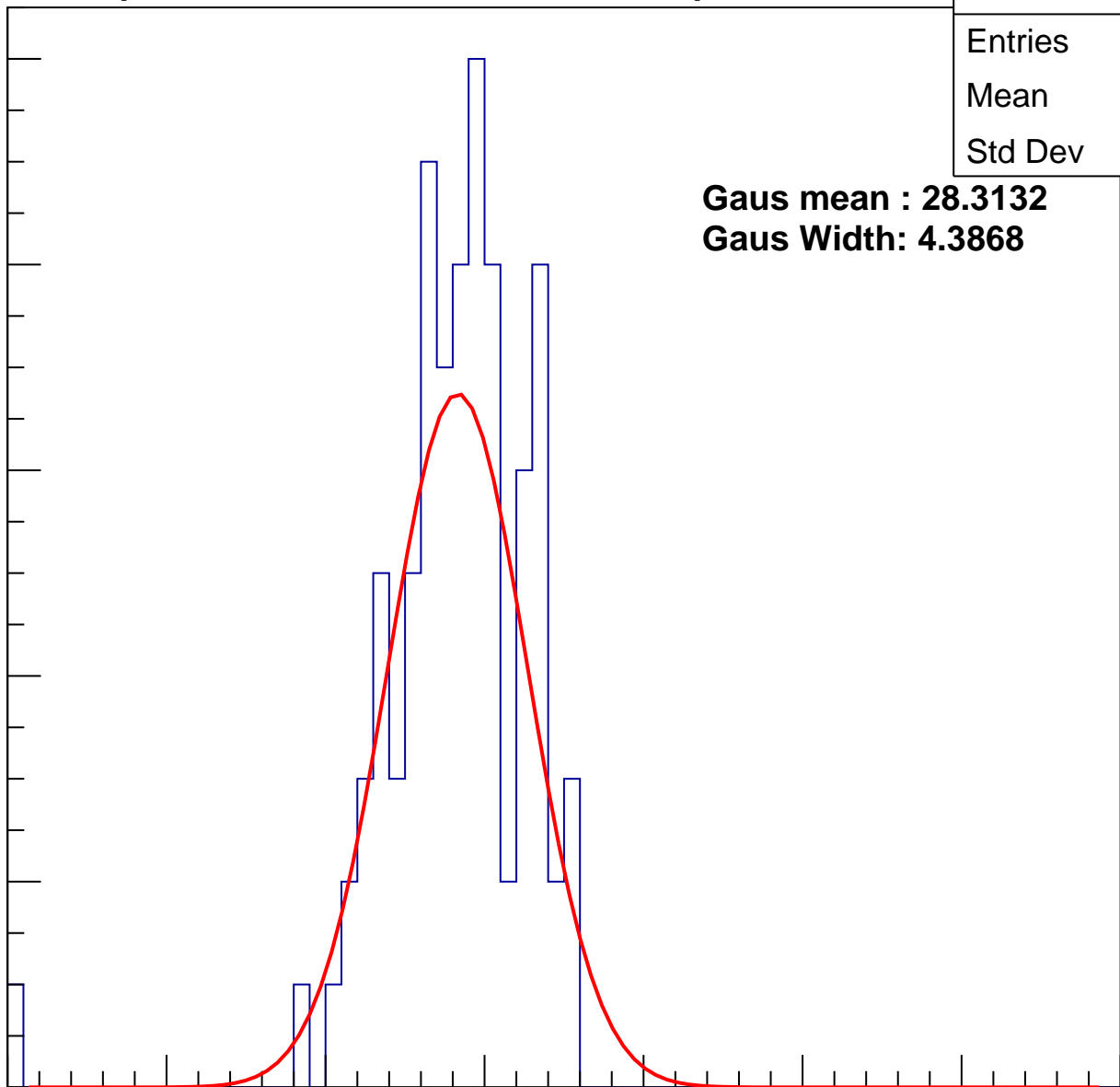
**Gaus Width: 4.3868**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch89, adc1

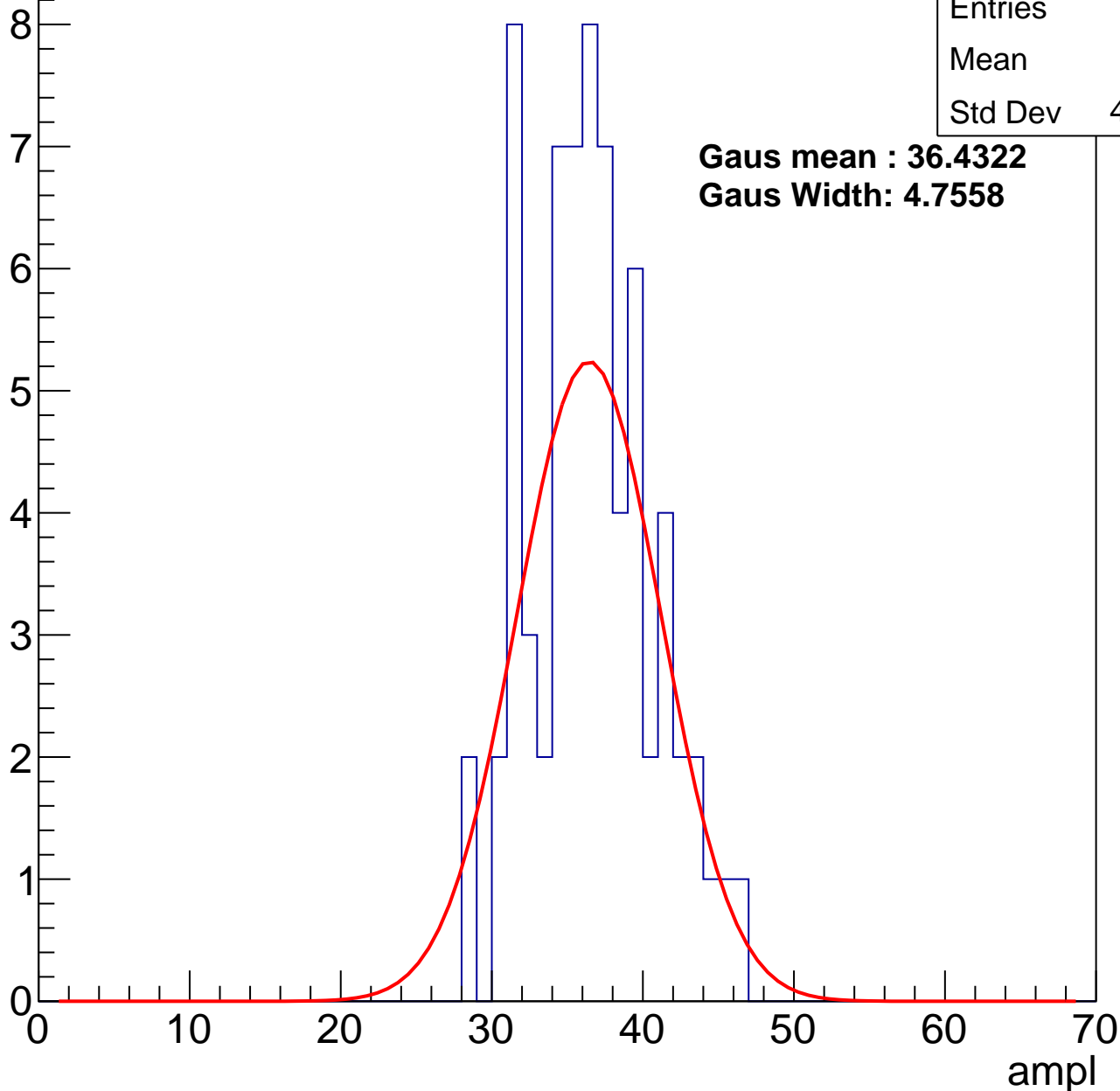
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	36.1
Std Dev	4.037

**Gaus mean : 36.4322**

**Gaus Width: 4.7558**



# B0L102S, U6-ch89, adc2

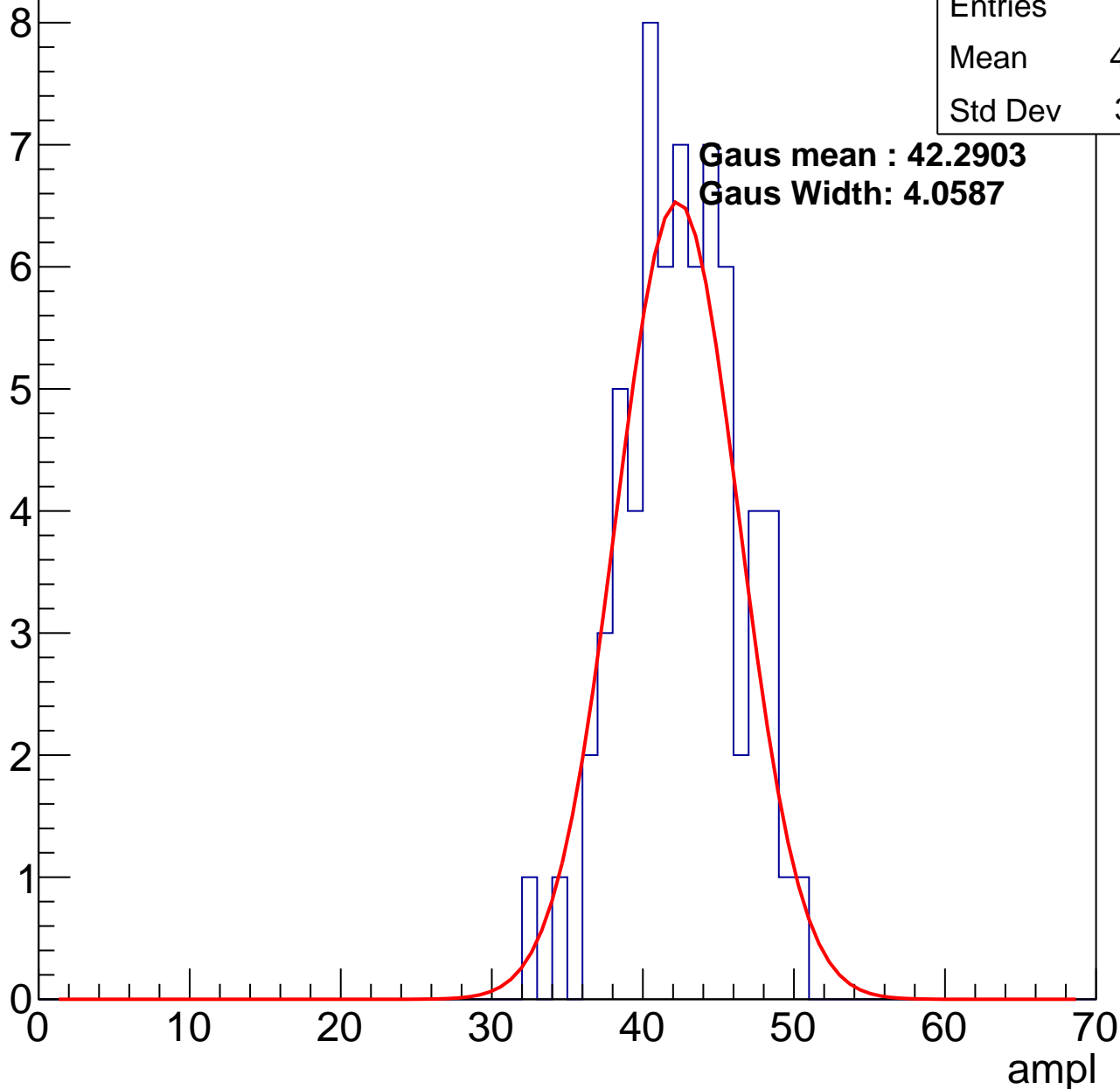
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	42.09
Std Dev	3.721

**Gaus mean : 42.2903**

**Gaus Width: 4.0587**

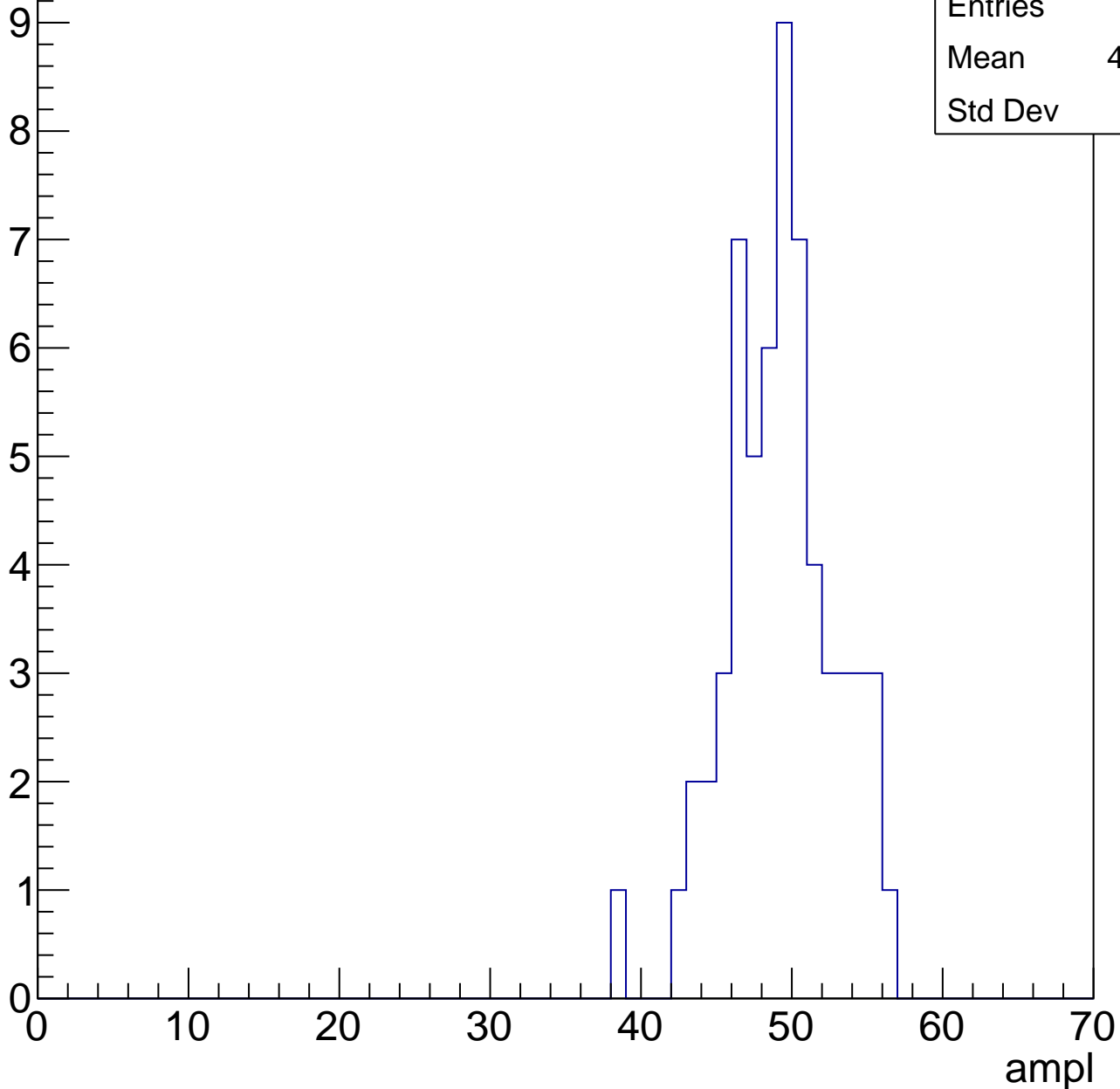


# B0L102S, U6-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	48.78
Std Dev	3.55

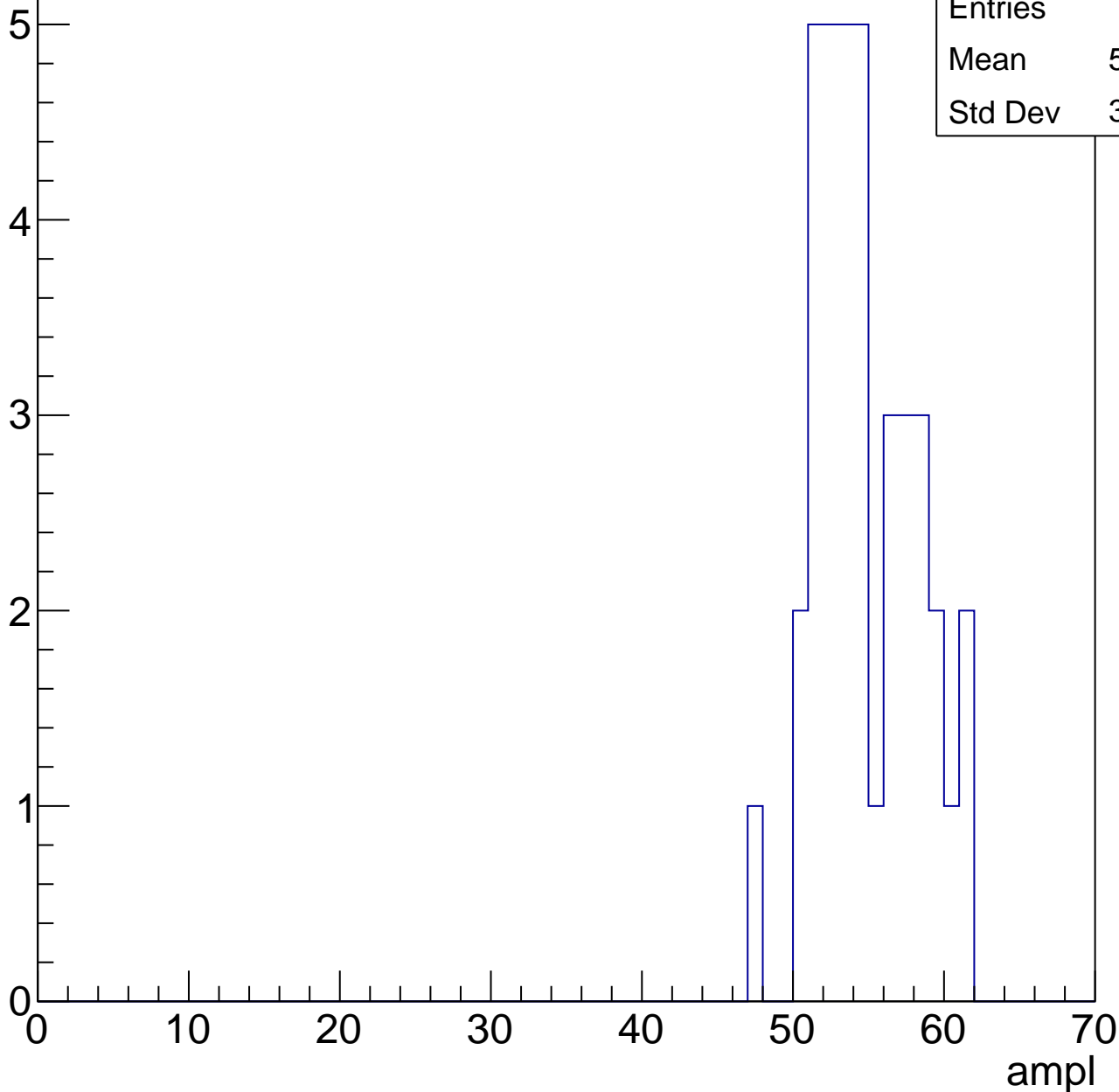


# B0L102S, U6-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

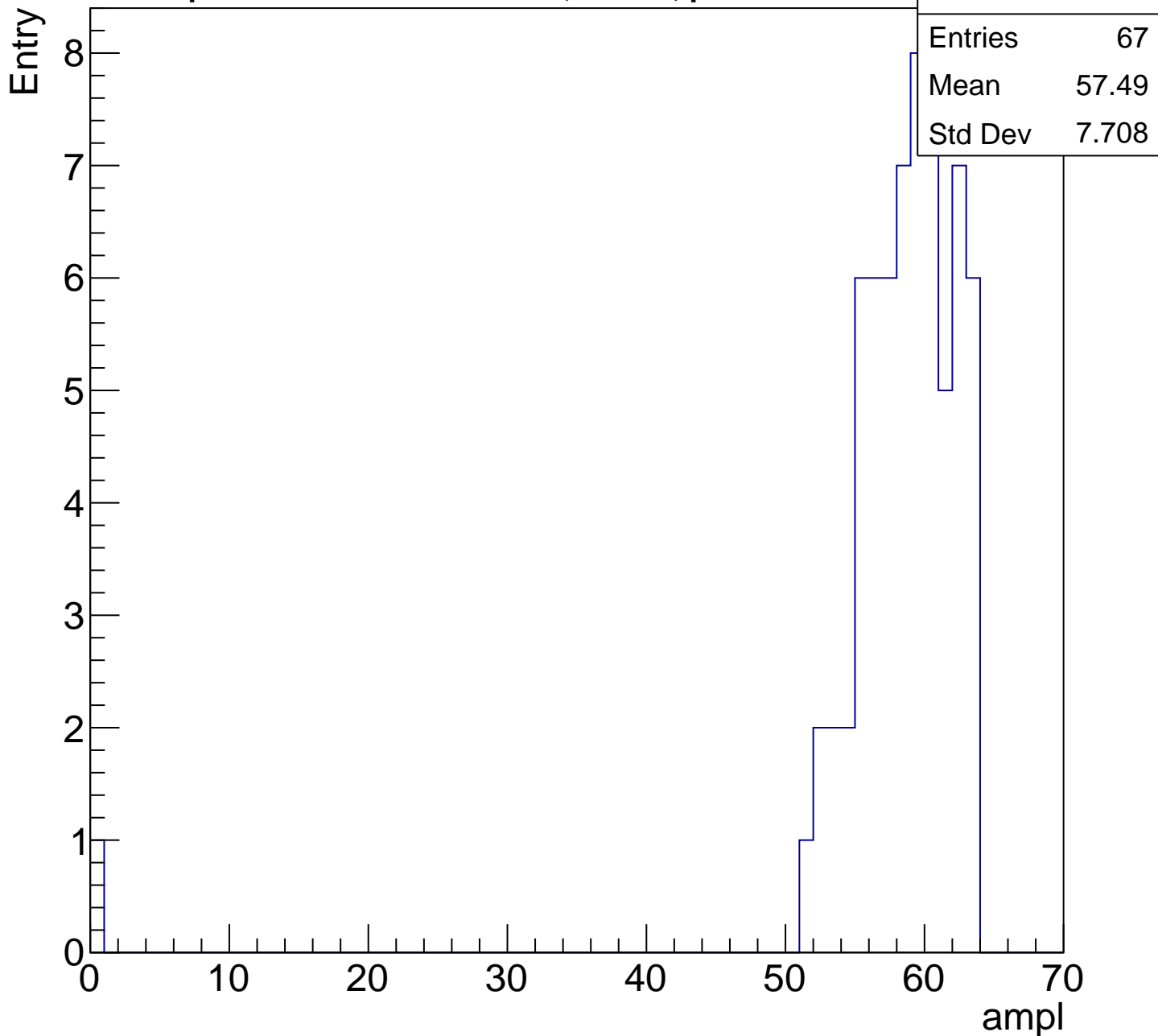
Entry

Entries	38
Mean	54.34
Std Dev	3.319



# B0L102S, U6-ch89, adc5

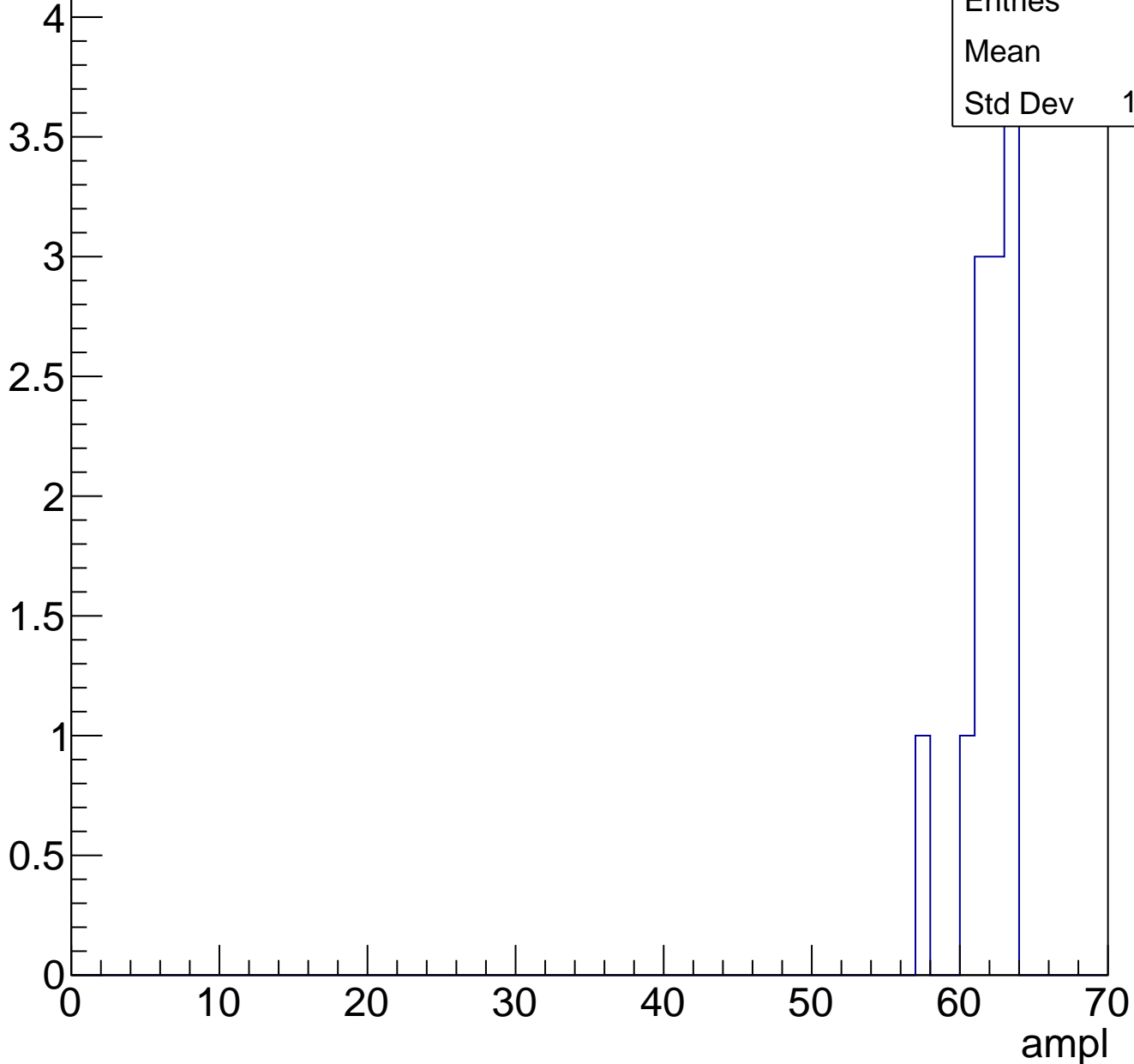
calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch90, adc0

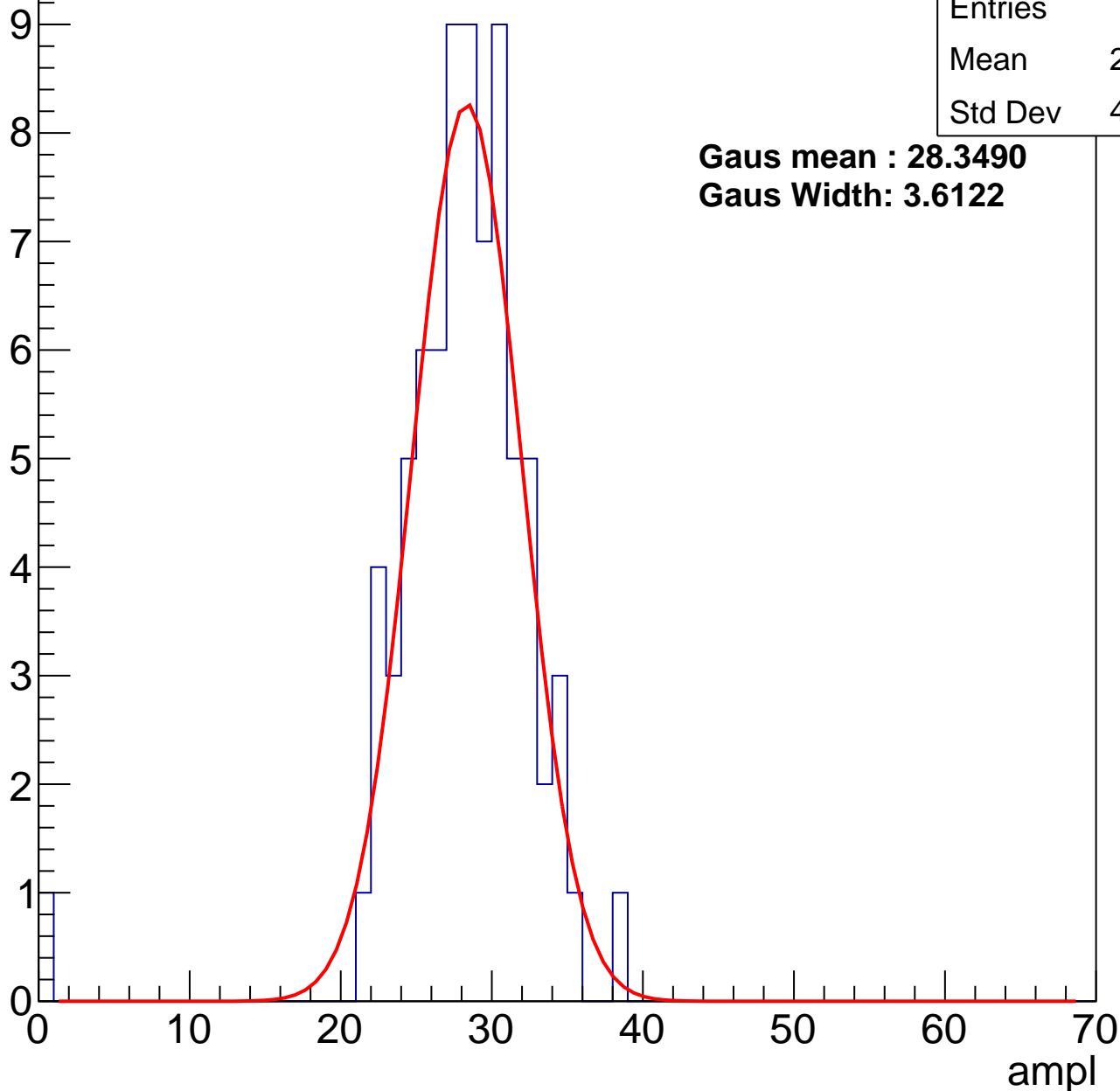
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	27.64
Std Dev	4.676

**Gaus mean : 28.3490**

**Gaus Width: 3.6122**



# B0L102S, U6-ch90, adc1

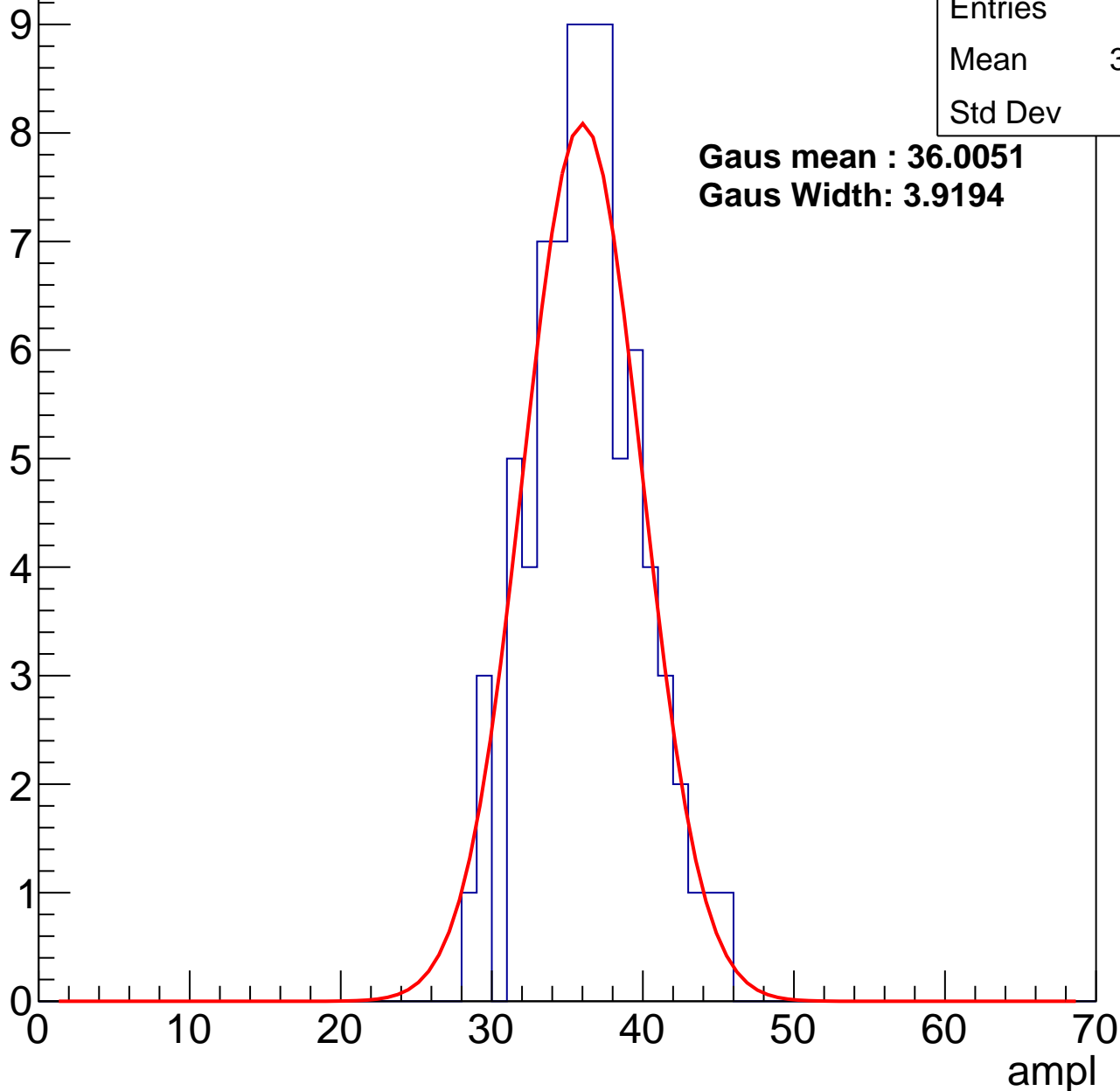
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	35.87
Std Dev	3.58

**Gaus mean : 36.0051**

**Gaus Width: 3.9194**



# B0L102S, U6-ch90, adc2

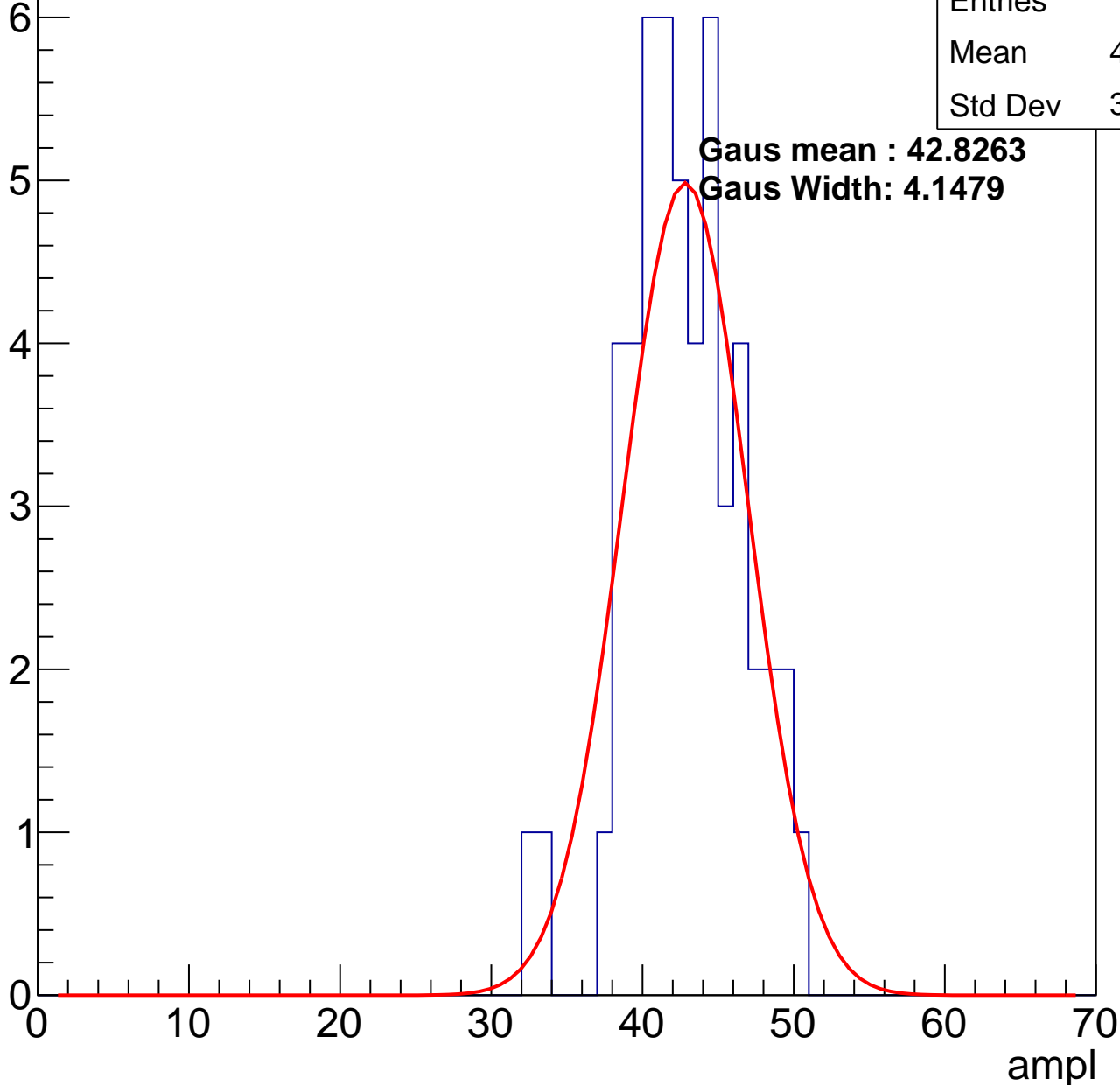
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	42.29
Std Dev	3.759

**Gaus mean : 42.8263**

**Gaus Width: 4.1479**

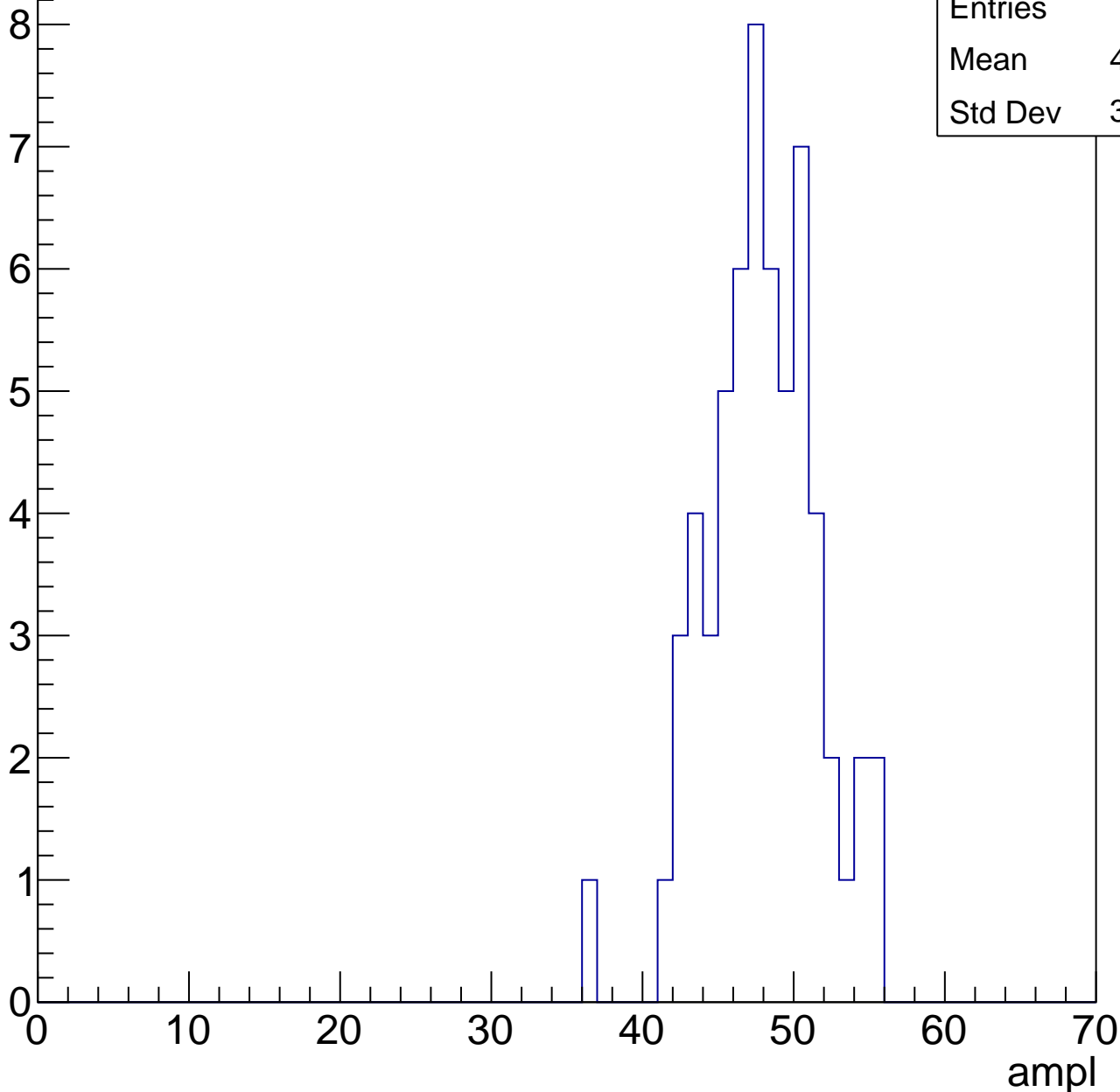


# B0L102S, U6-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

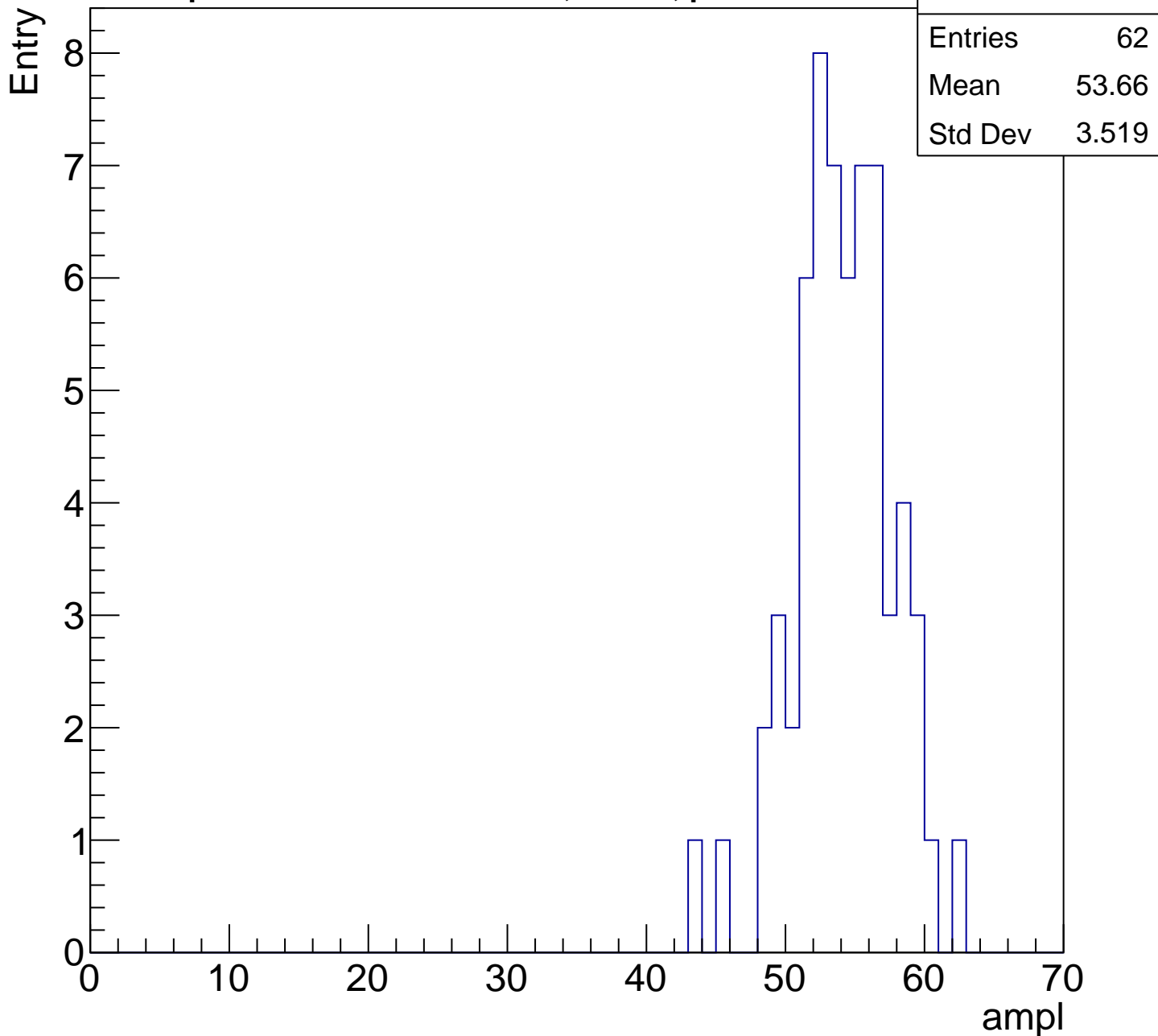
Entry

Entries	60
Mean	47.43
Std Dev	3.658



# B0L102S, U6-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1



# B0L102S, U6-ch90, adc5

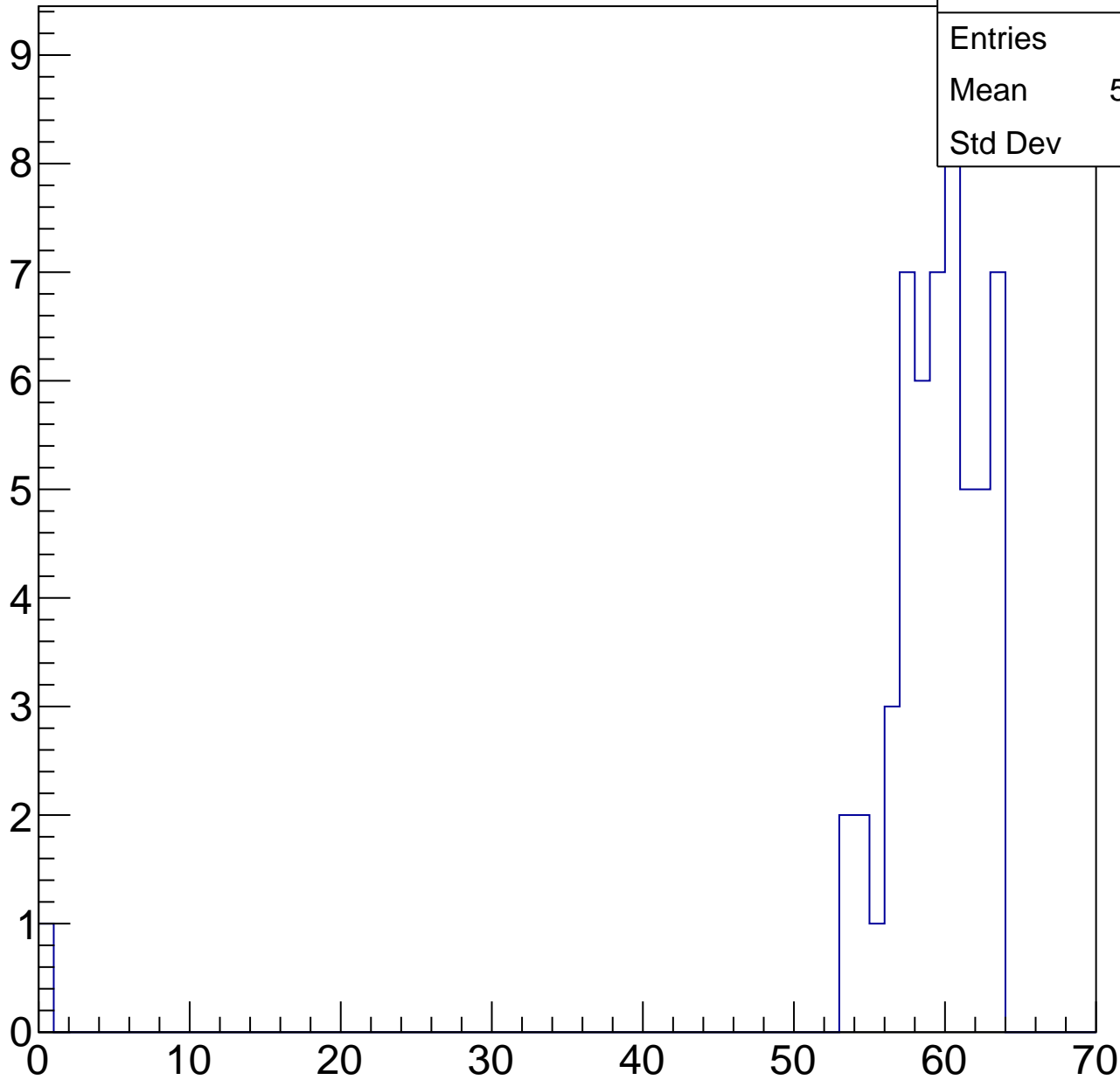
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	58.05
Std Dev	8.33

ampl

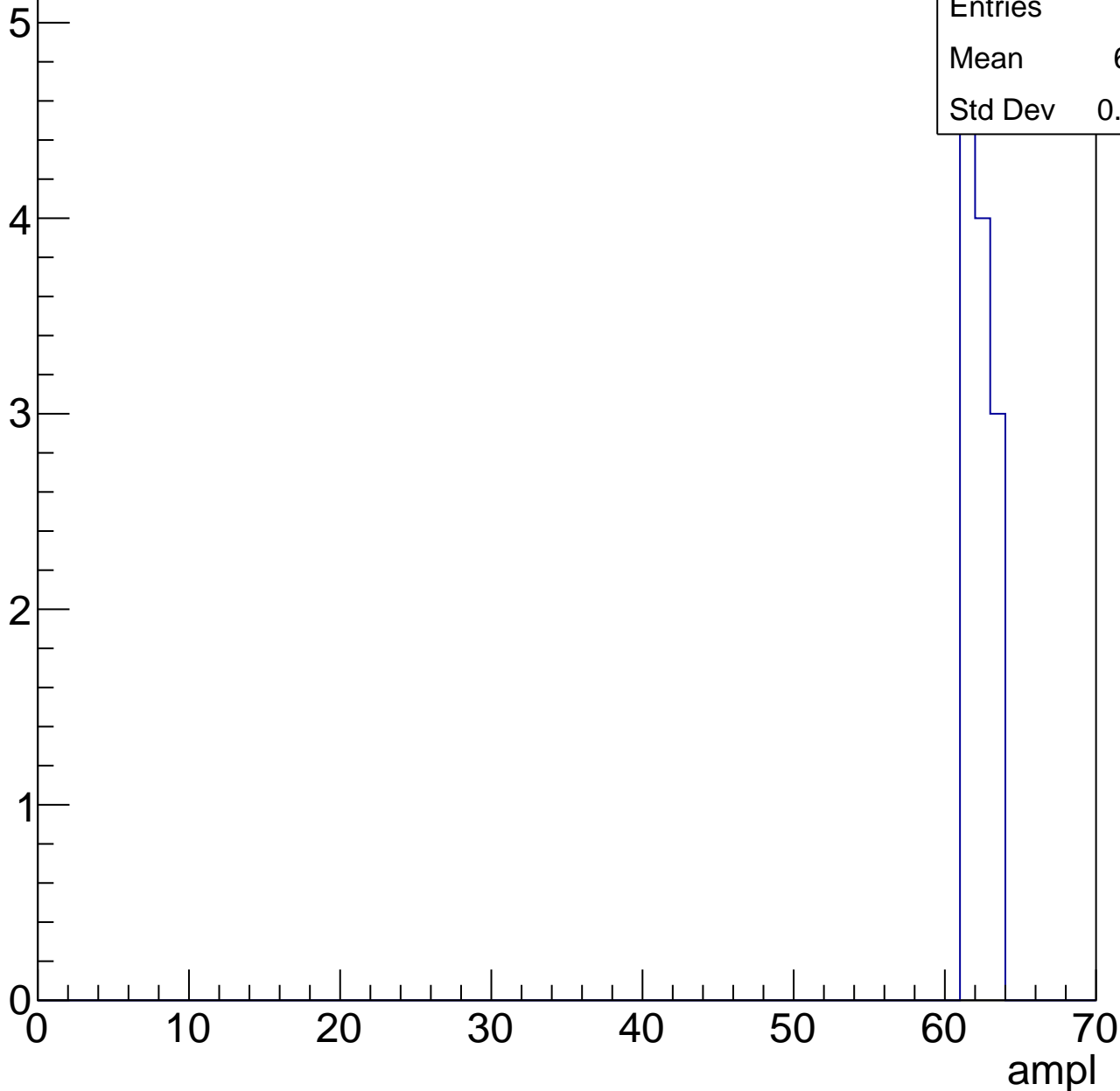


# B0L102S, U6-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	61.83
Std Dev	0.7993





# B0L102S, U6-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch91, adc0

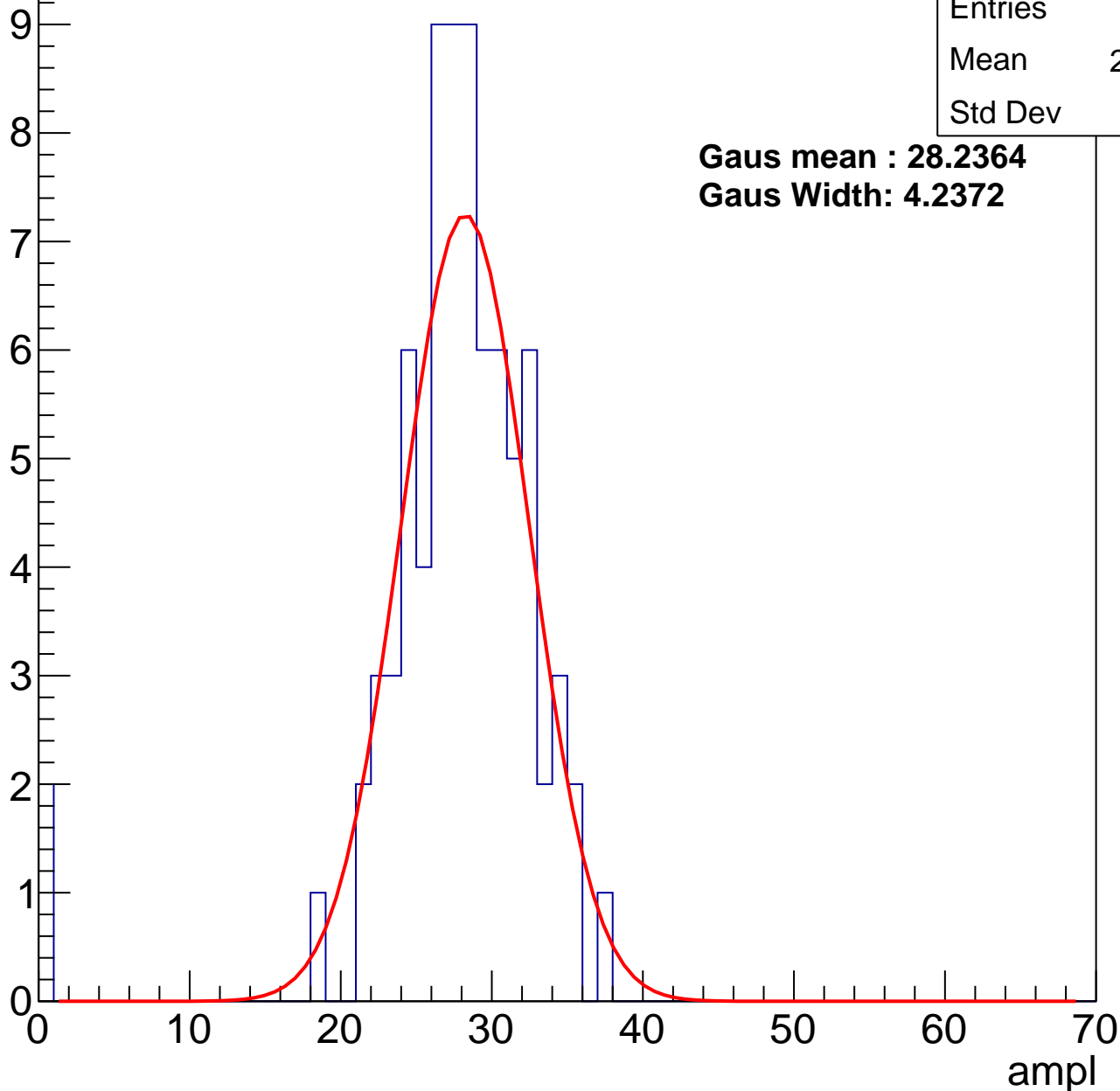
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	27.14
Std Dev	5.71

**Gaus mean : 28.2364**

**Gaus Width: 4.2372**



# B0L102S, U6-ch91, adc1

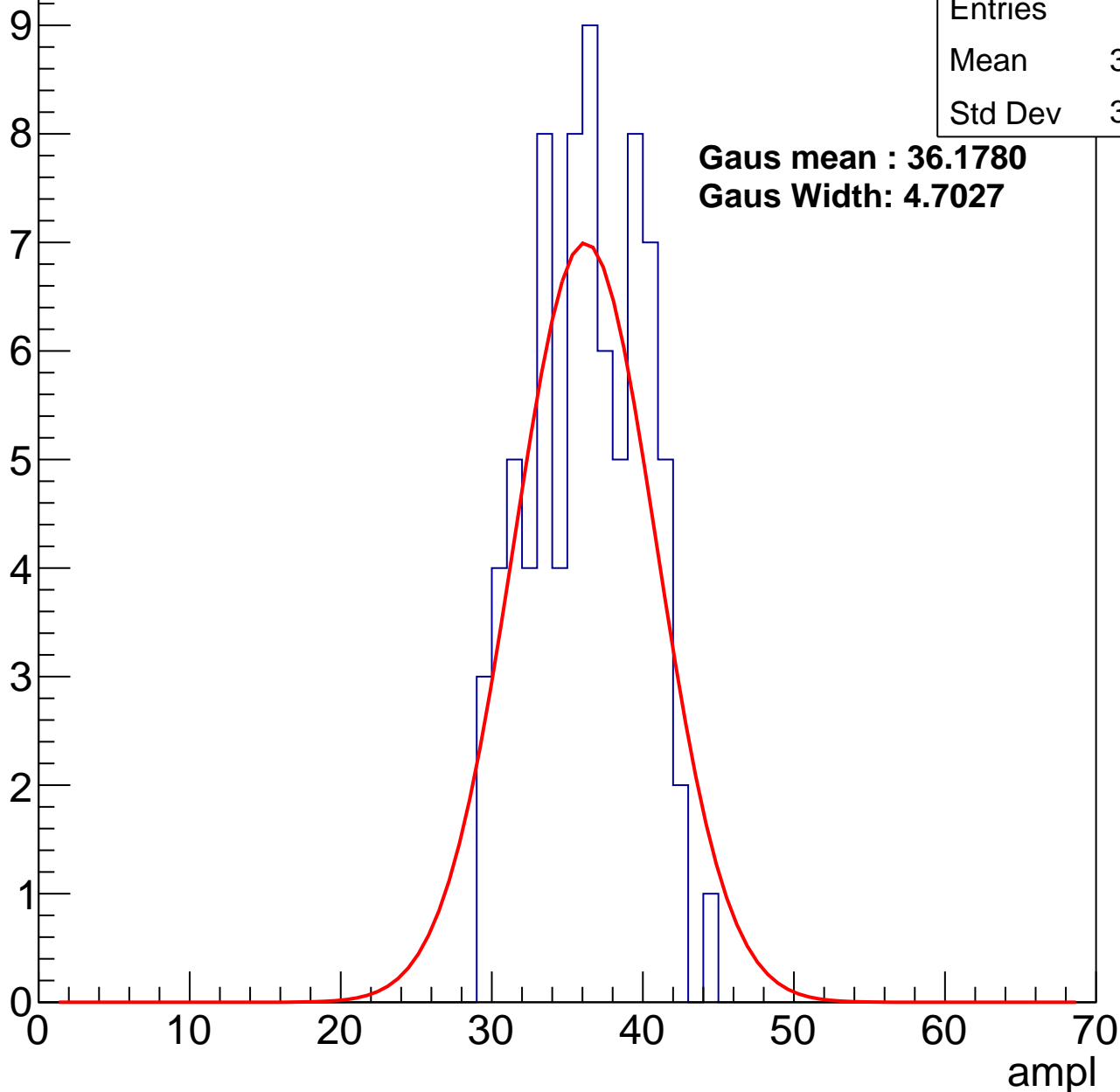
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.84
Std Dev	3.637

**Gaus mean : 36.1780**

**Gaus Width: 4.7027**



# B0L102S, U6-ch91, adc2

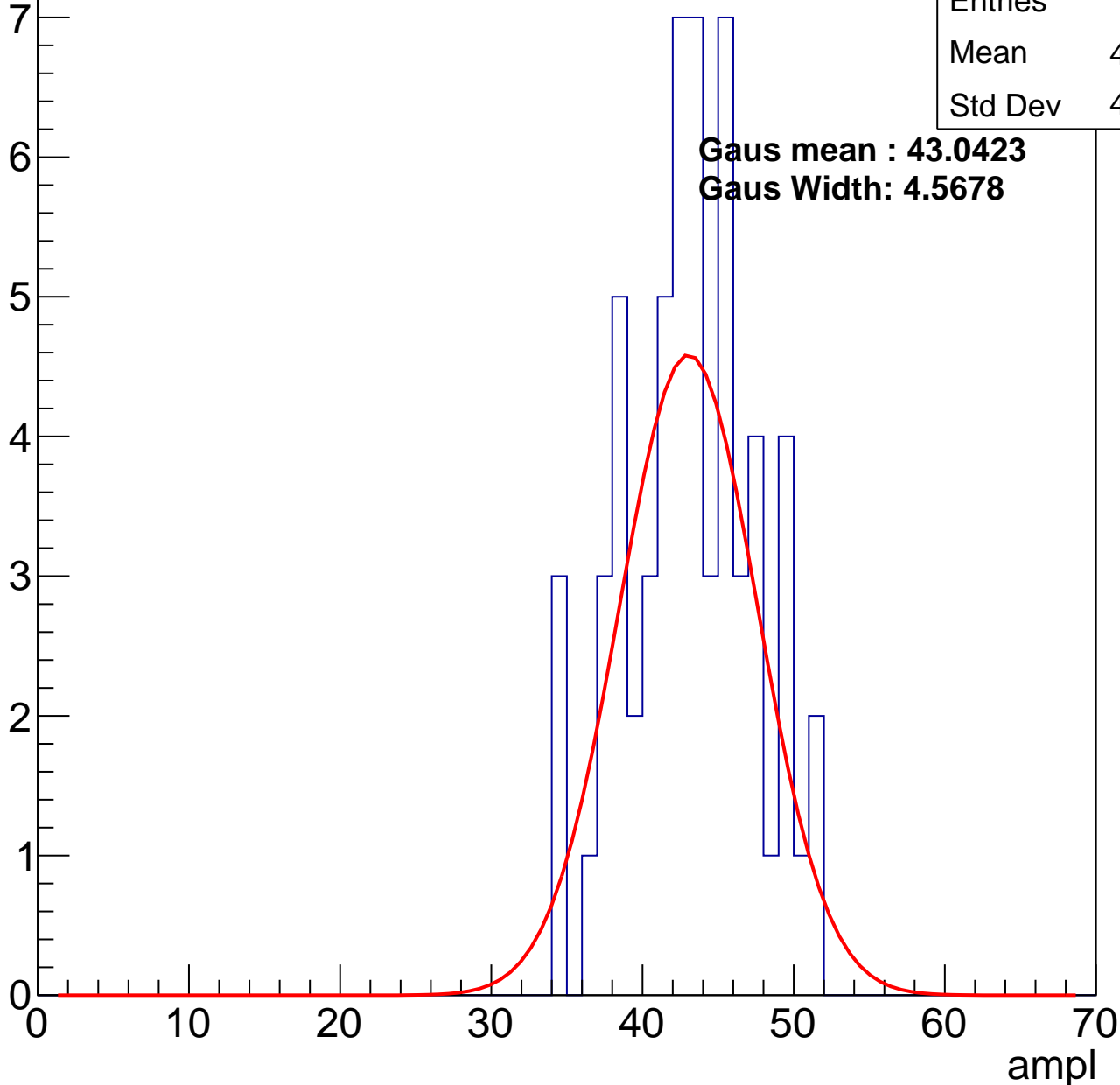
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	42.72
Std Dev	4.197

**Gaus mean : 43.0423**

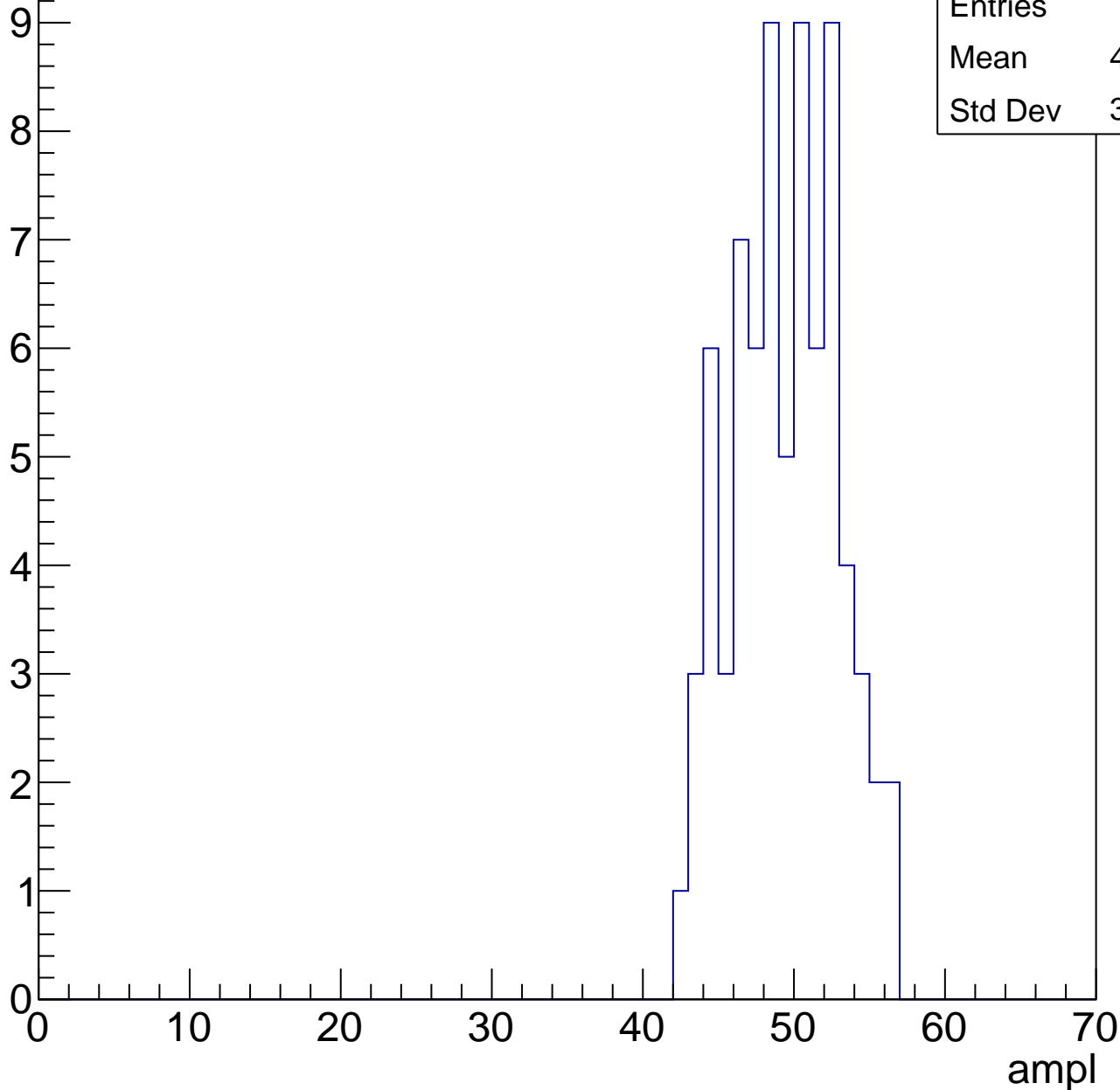
**Gaus Width: 4.5678**



# B0L102S, U6-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

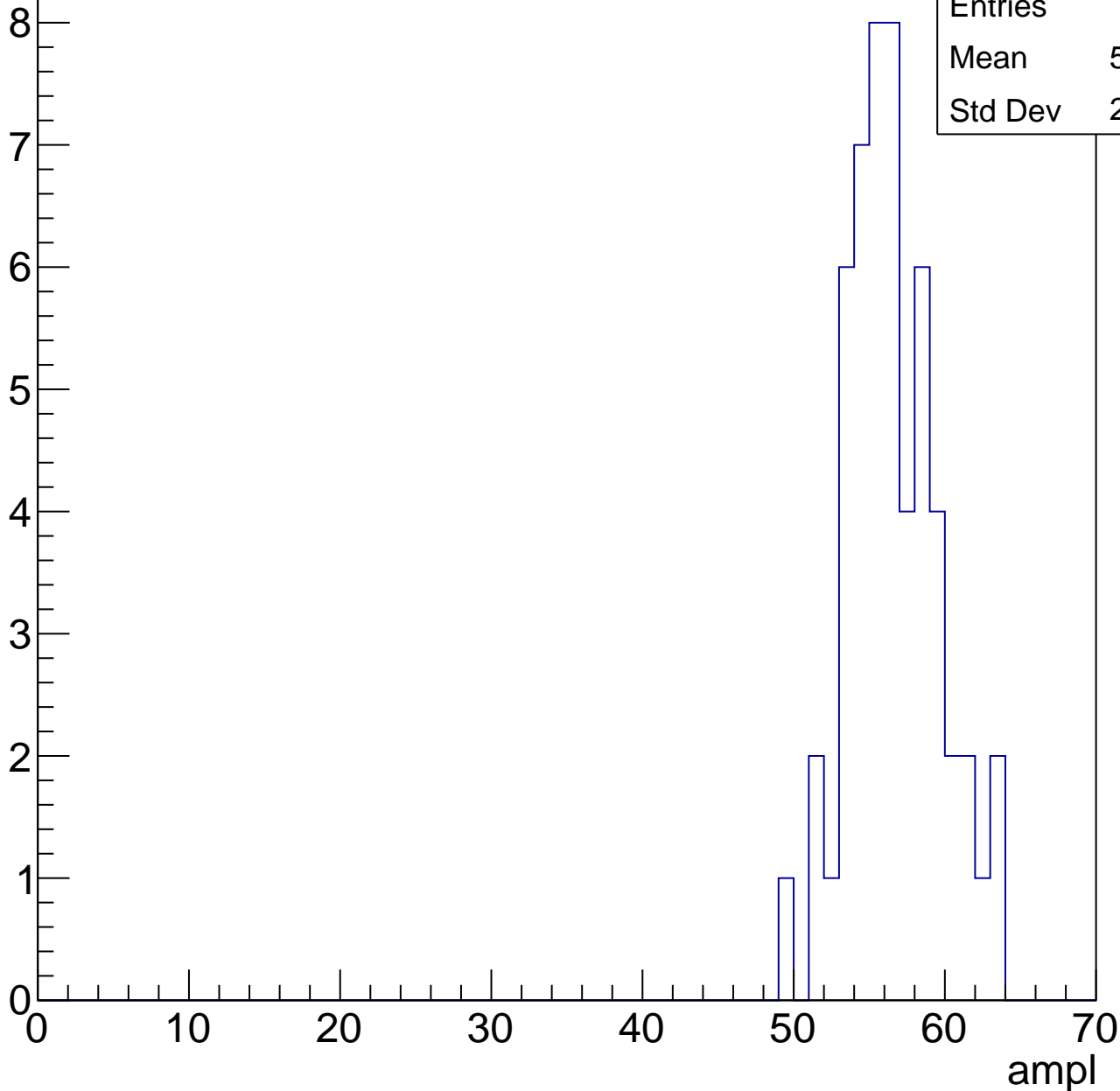


# B0L102S, U6-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	56.09
Std Dev	2.995

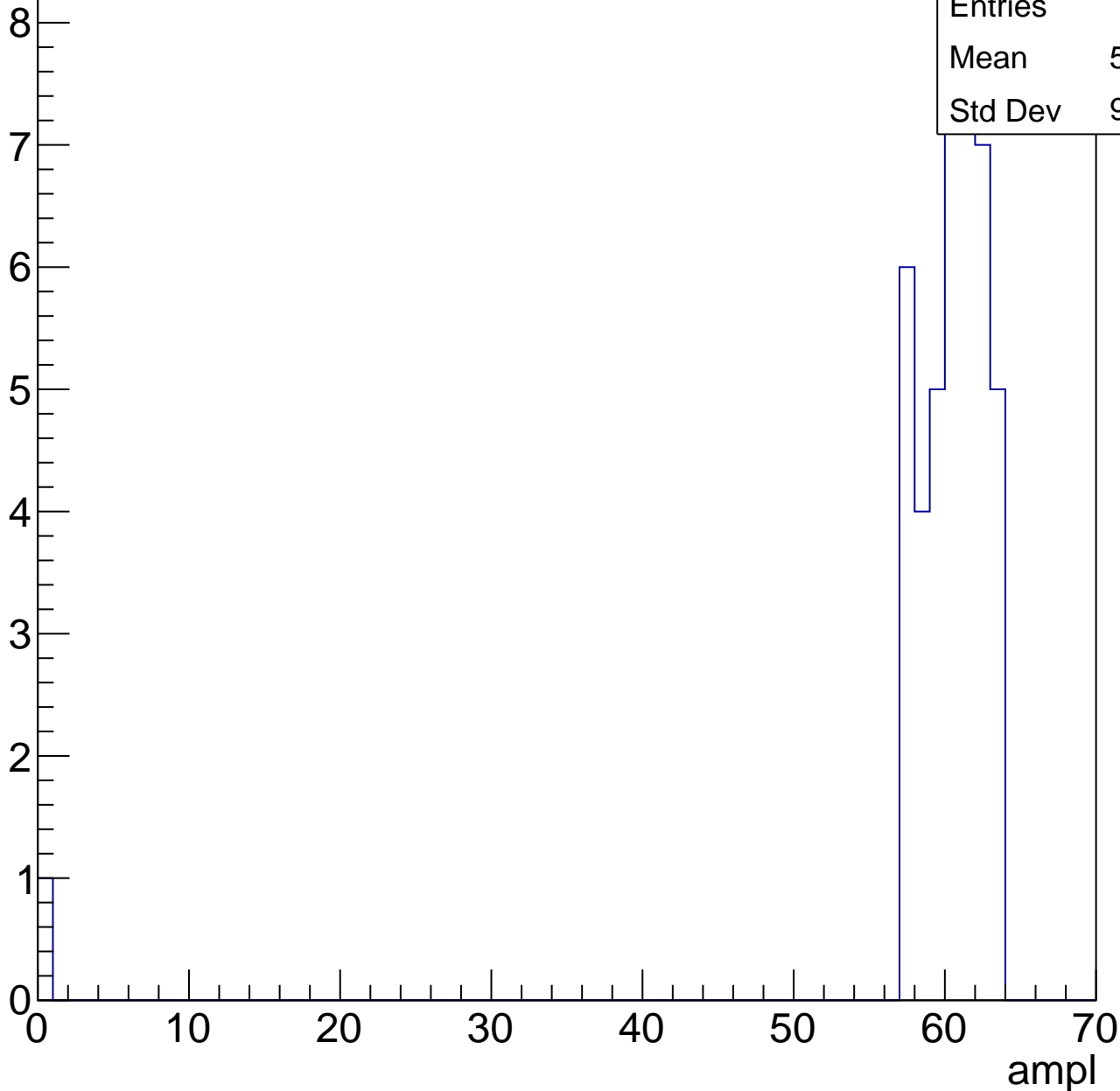


# B0L102S, U6-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	58.77
Std Dev	9.157



# B0L102S, U6-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch92, adc0

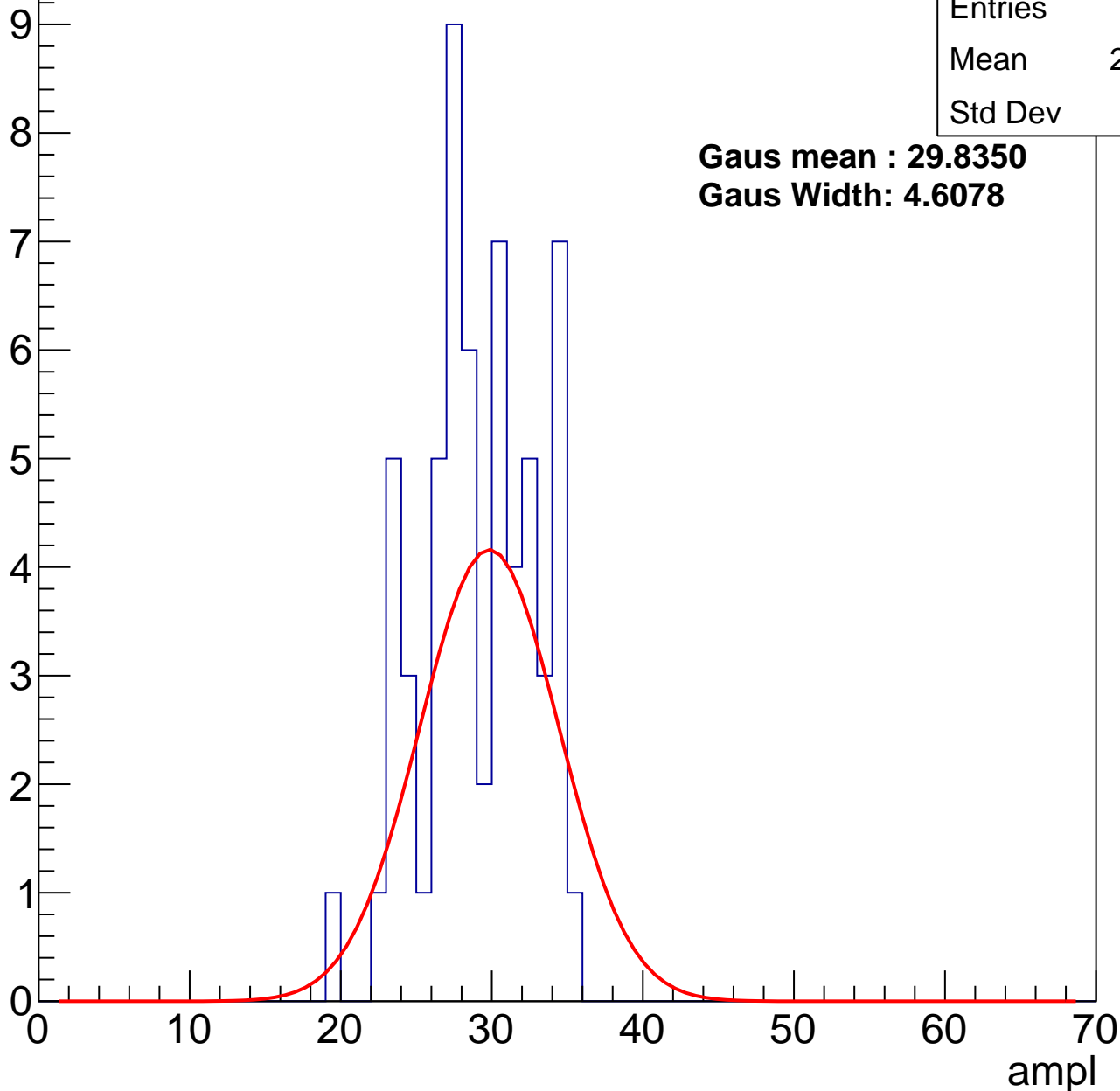
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	28.63
Std Dev	3.71

**Gaus mean : 29.8350**

**Gaus Width: 4.6078**



# B0L102S, U6-ch92, adc1

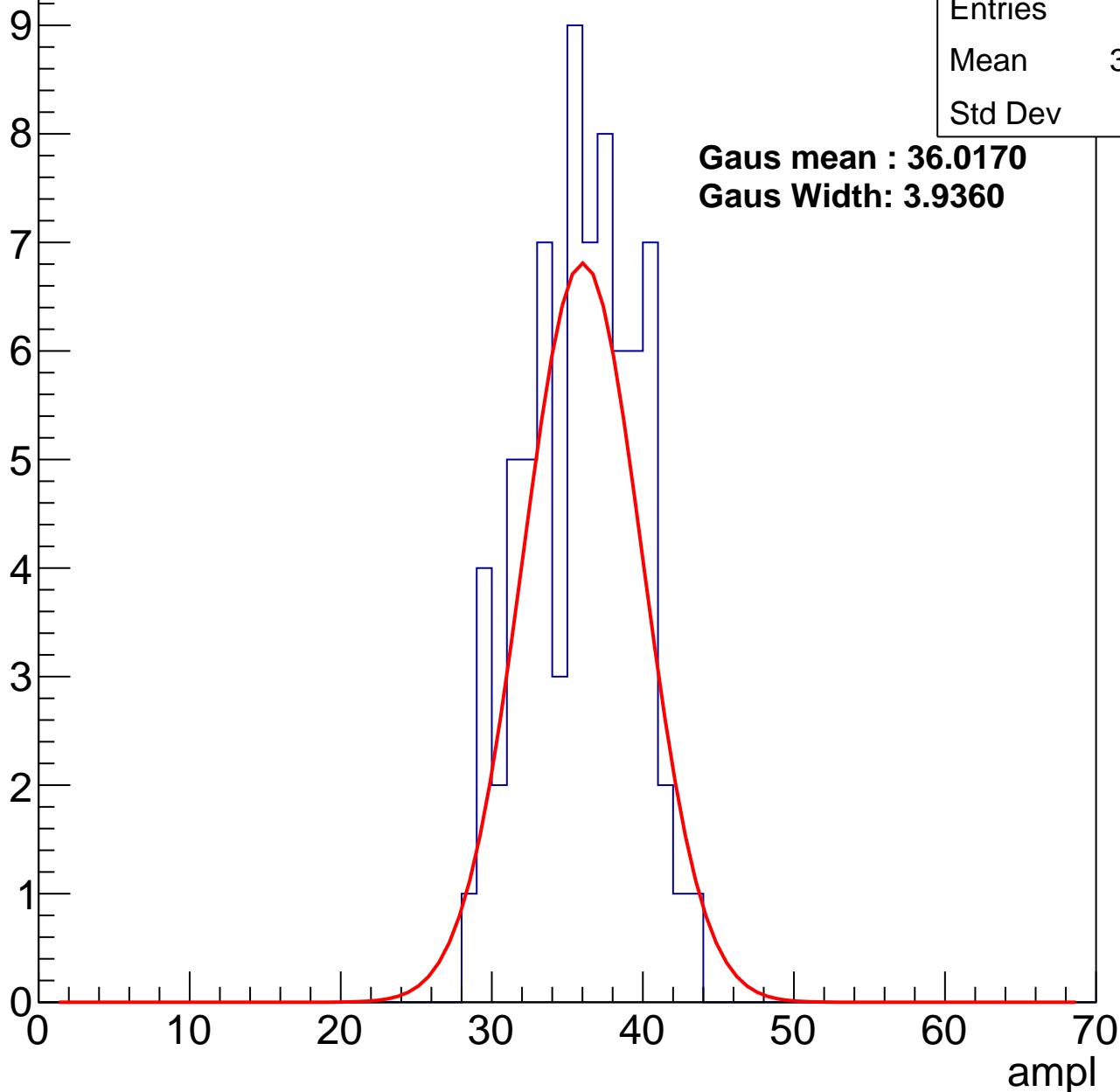
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	35.46
Std Dev	3.55

**Gaus mean : 36.0170**

**Gaus Width: 3.9360**



# B0L102S, U6-ch92, adc2

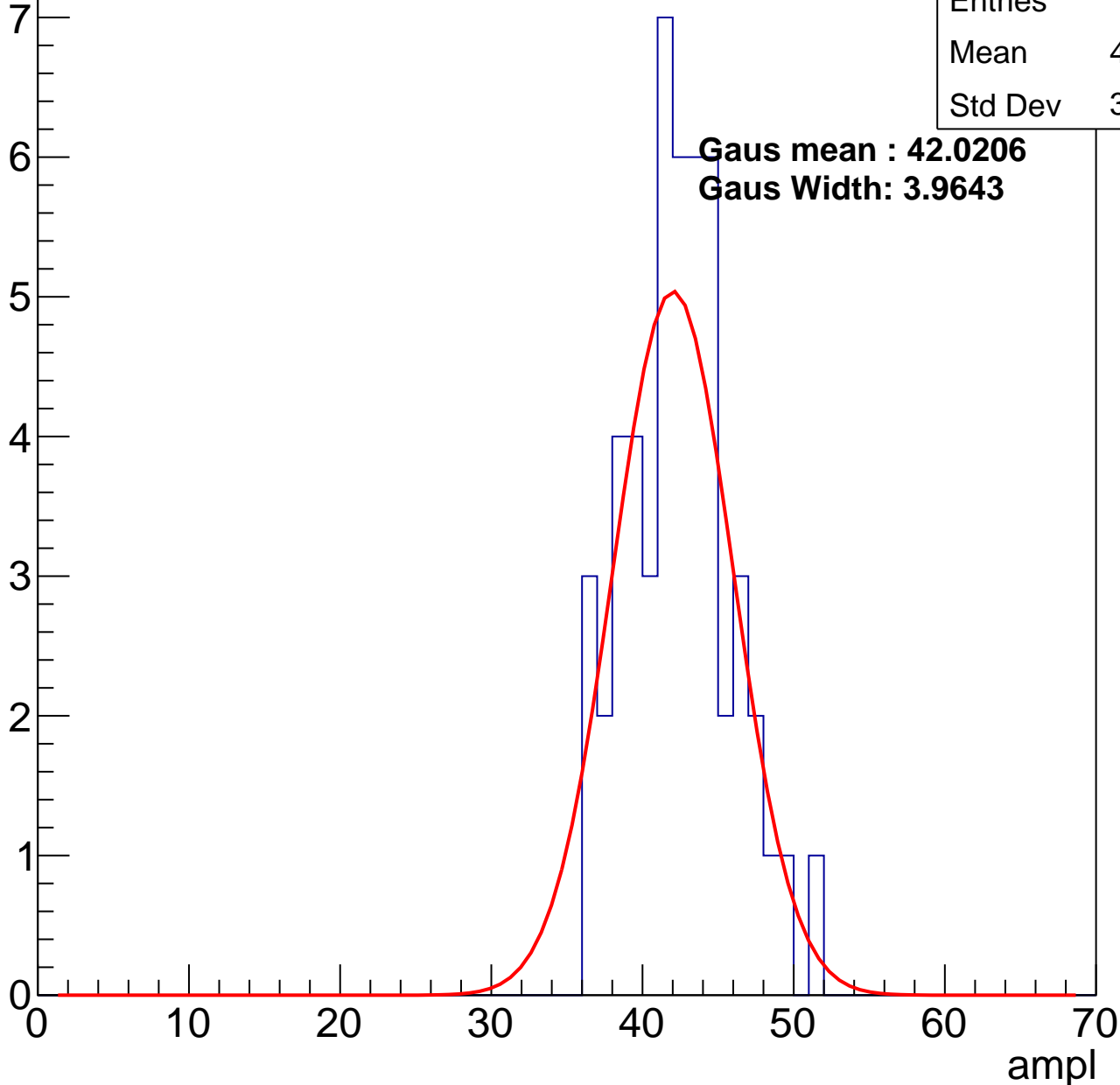
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	41.98
Std Dev	3.398

**Gaus mean : 42.0206**

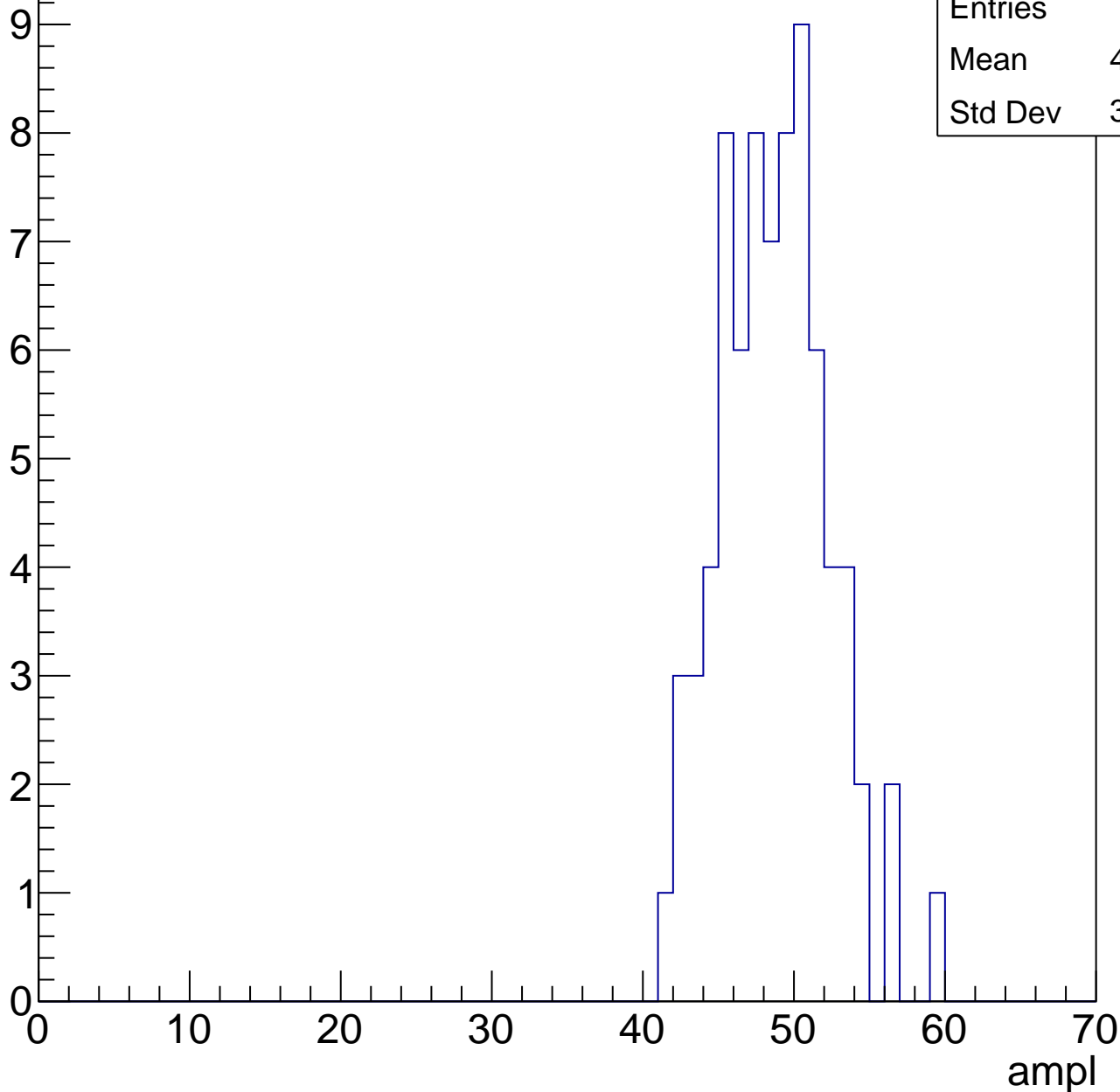
**Gaus Width: 3.9643**



# B0L102S, U6-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

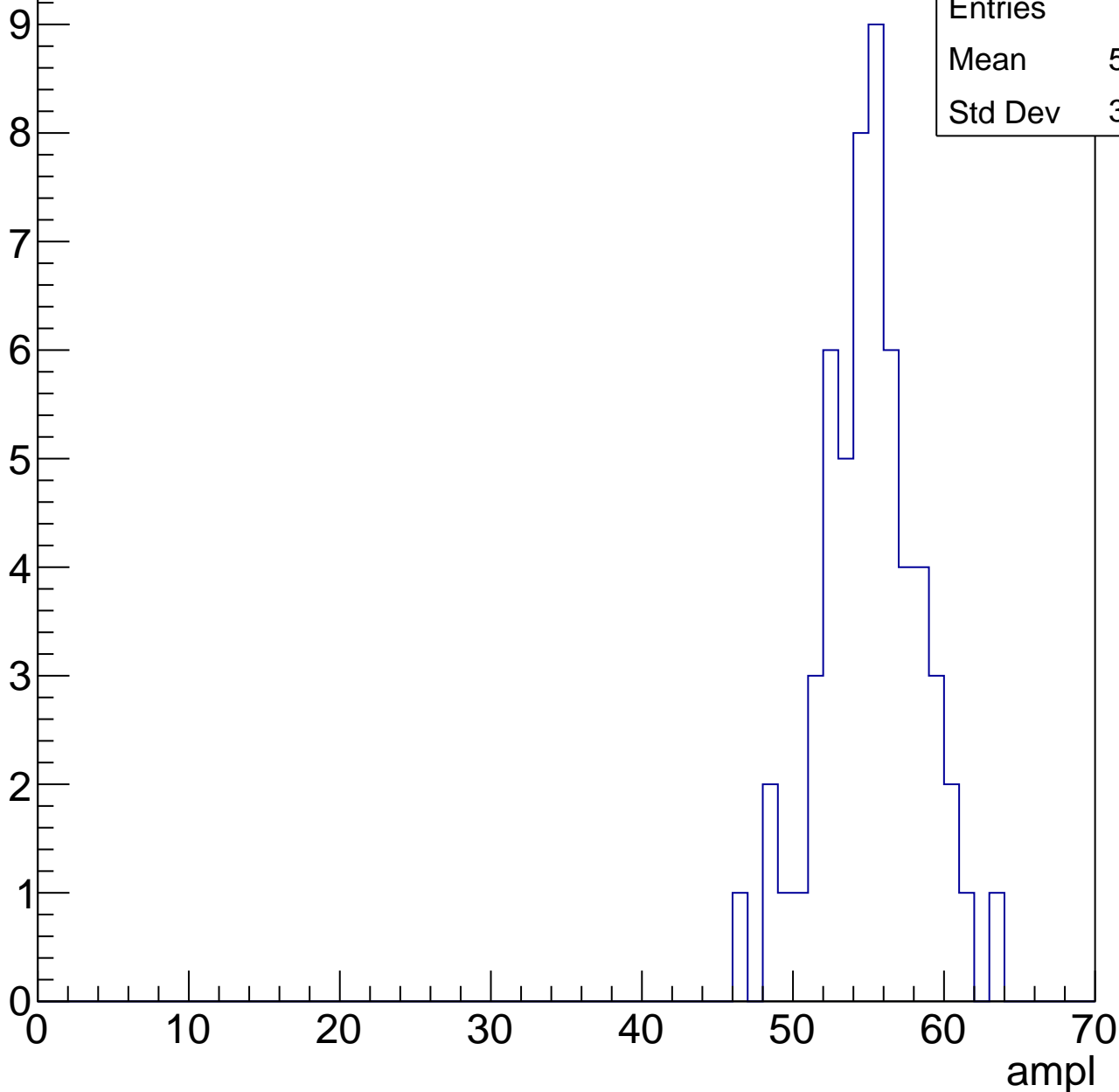
Entry



# B0L102S, U6-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

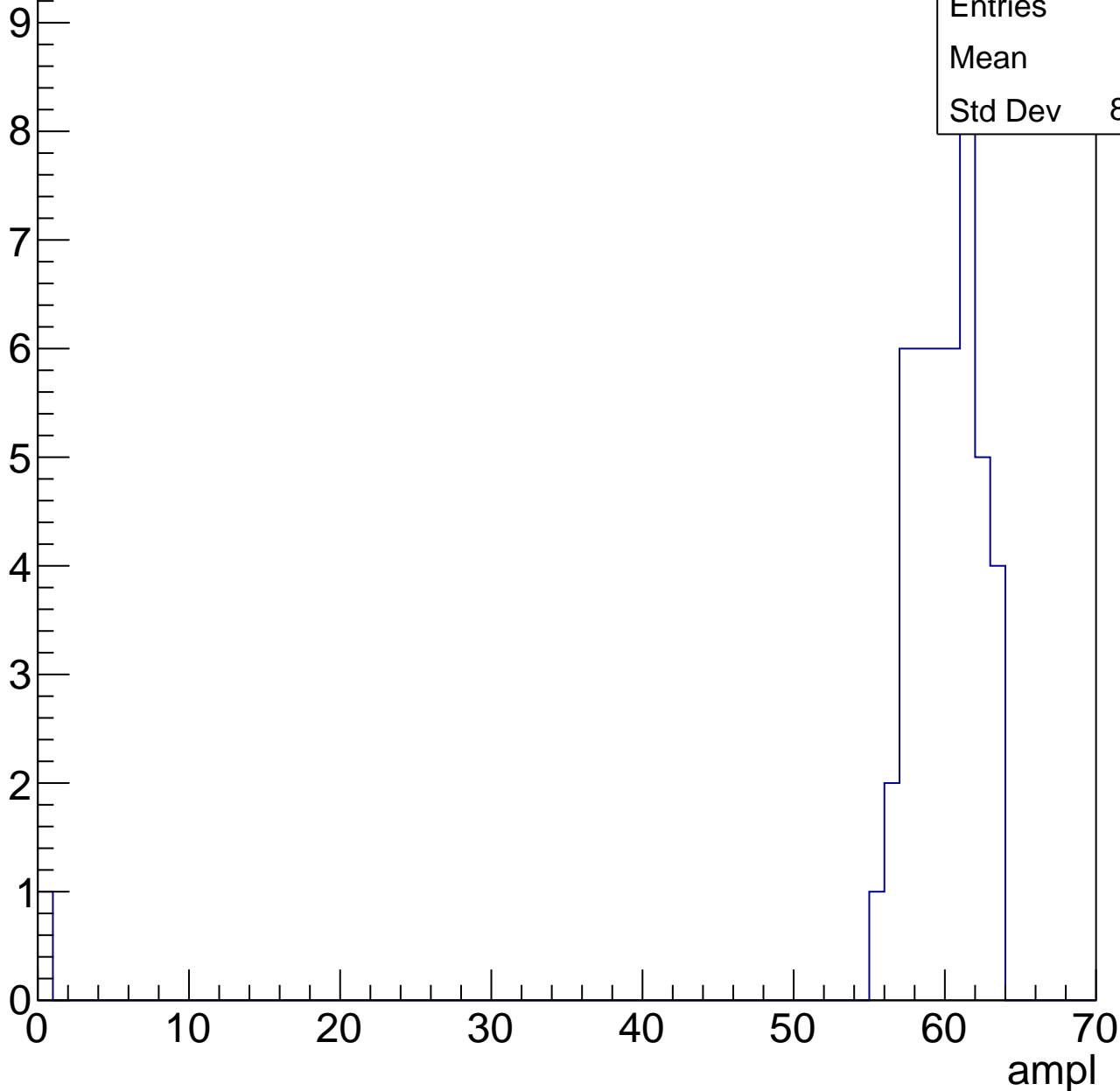


# B0L102S, U6-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	58.3
Std Dev	8.937

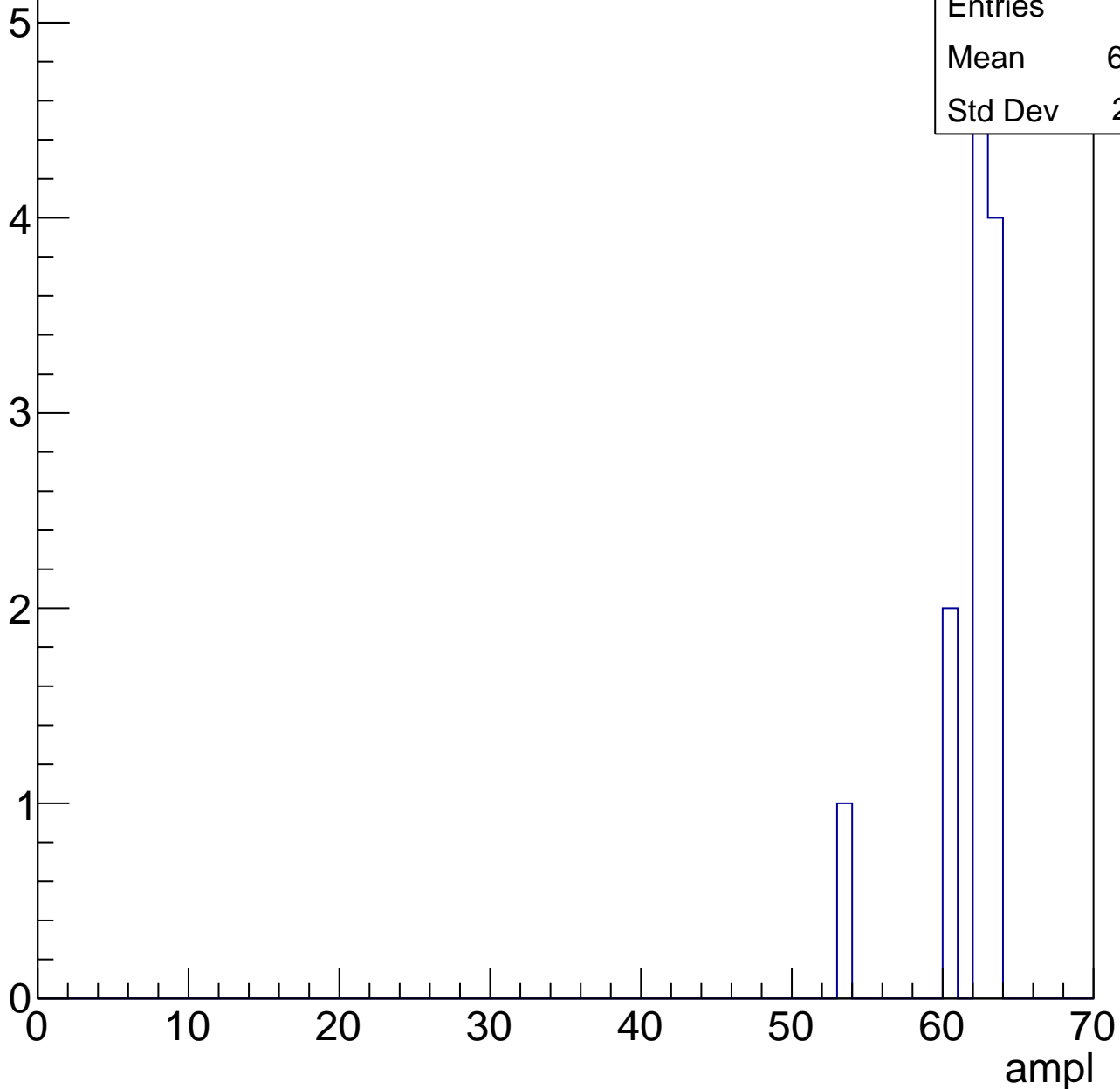


# B0L102S, U6-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	61.25
Std Dev	2.681

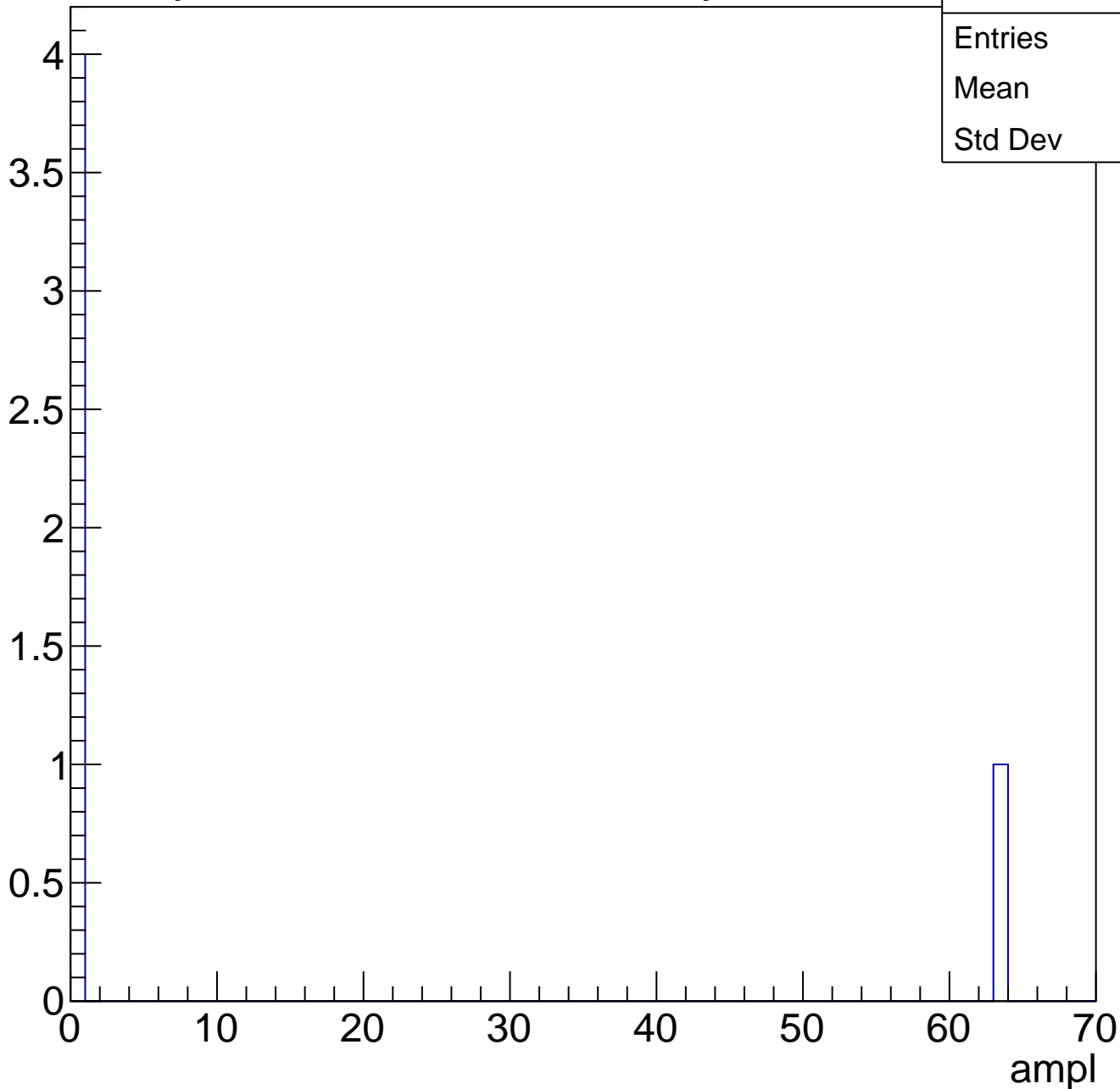




# B0L102S, U6-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch93, adc0

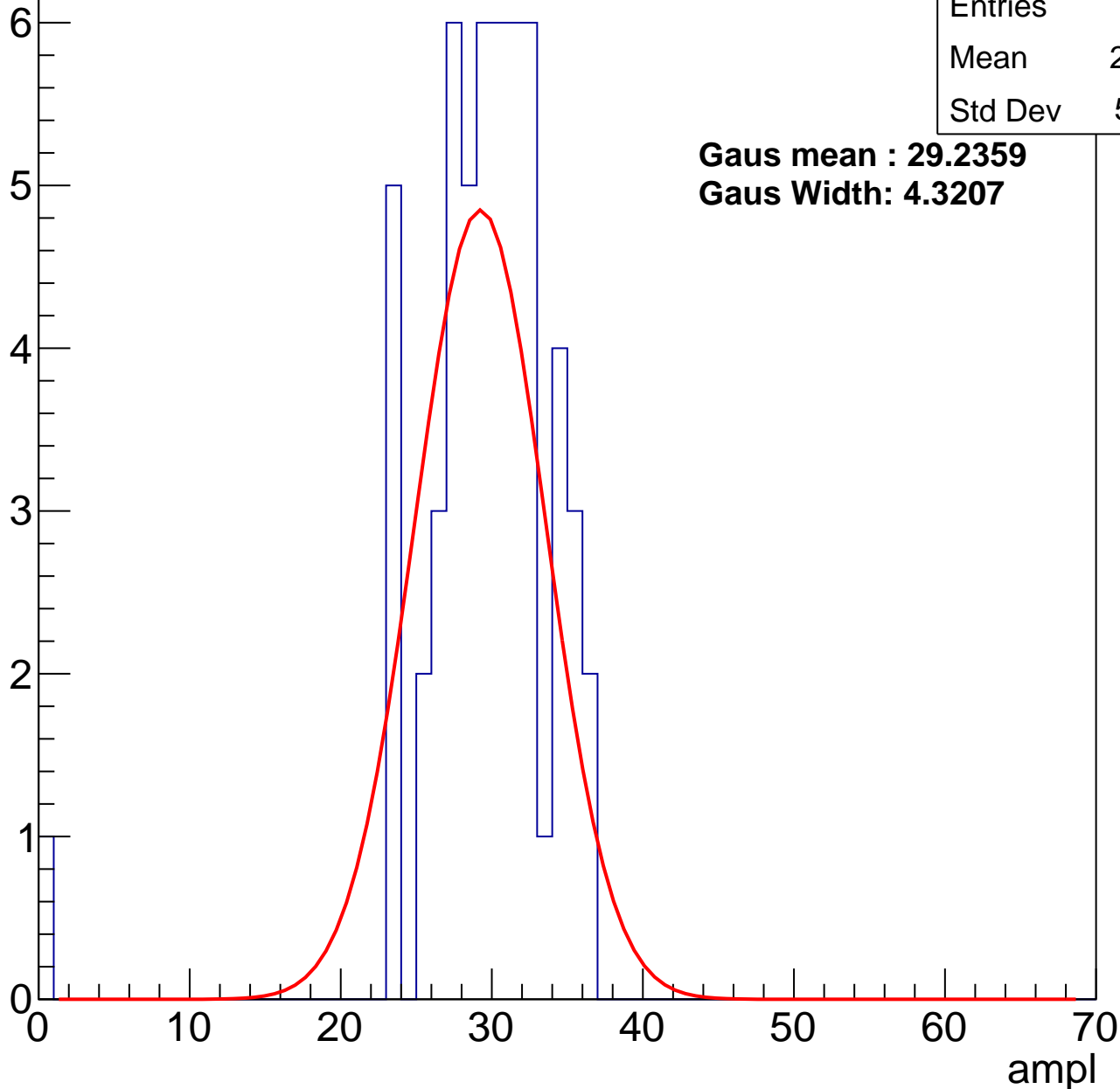
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	28.98
Std Dev	5.201

**Gaus mean : 29.2359**

**Gaus Width: 4.3207**



# B0L102S, U6-ch93, adc1

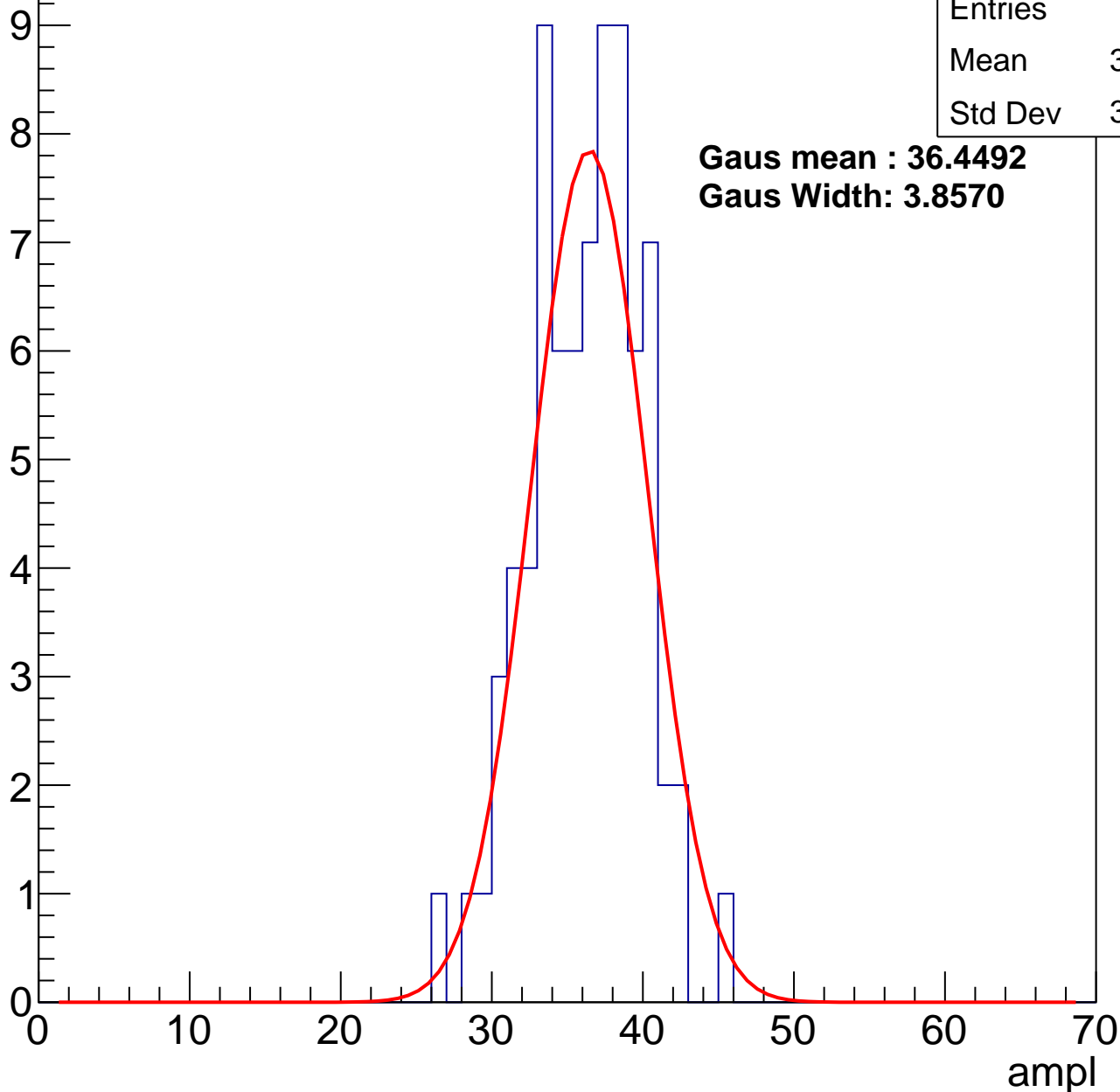
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	35.74
Std Dev	3.589

**Gaus mean : 36.4492**

**Gaus Width: 3.8570**



# B0L102S, U6-ch93, adc2

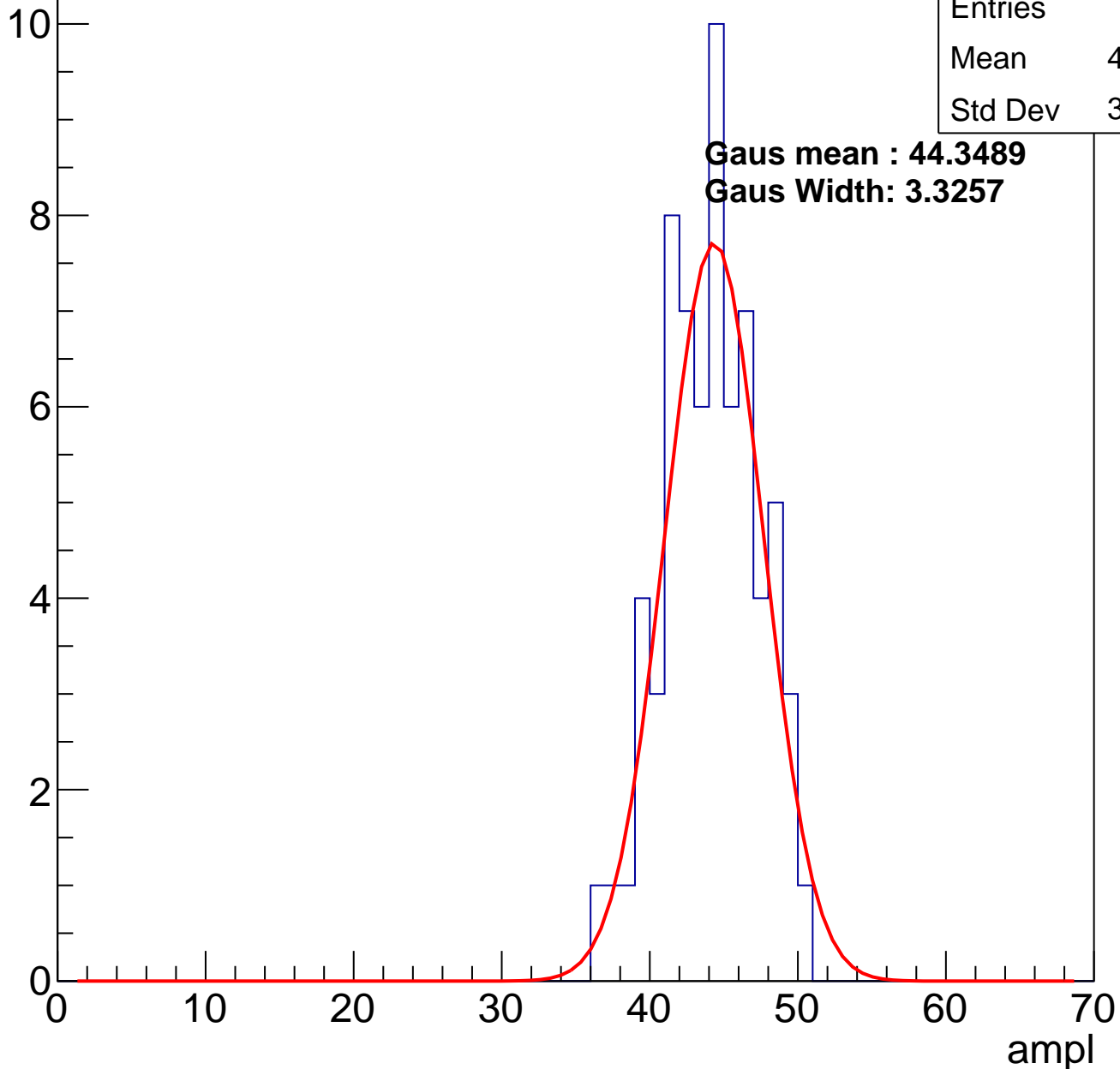
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	67
Mean	43.64
Std Dev	3.128

**Gaus mean : 44.3489**

**Gaus Width: 3.3257**

Entry

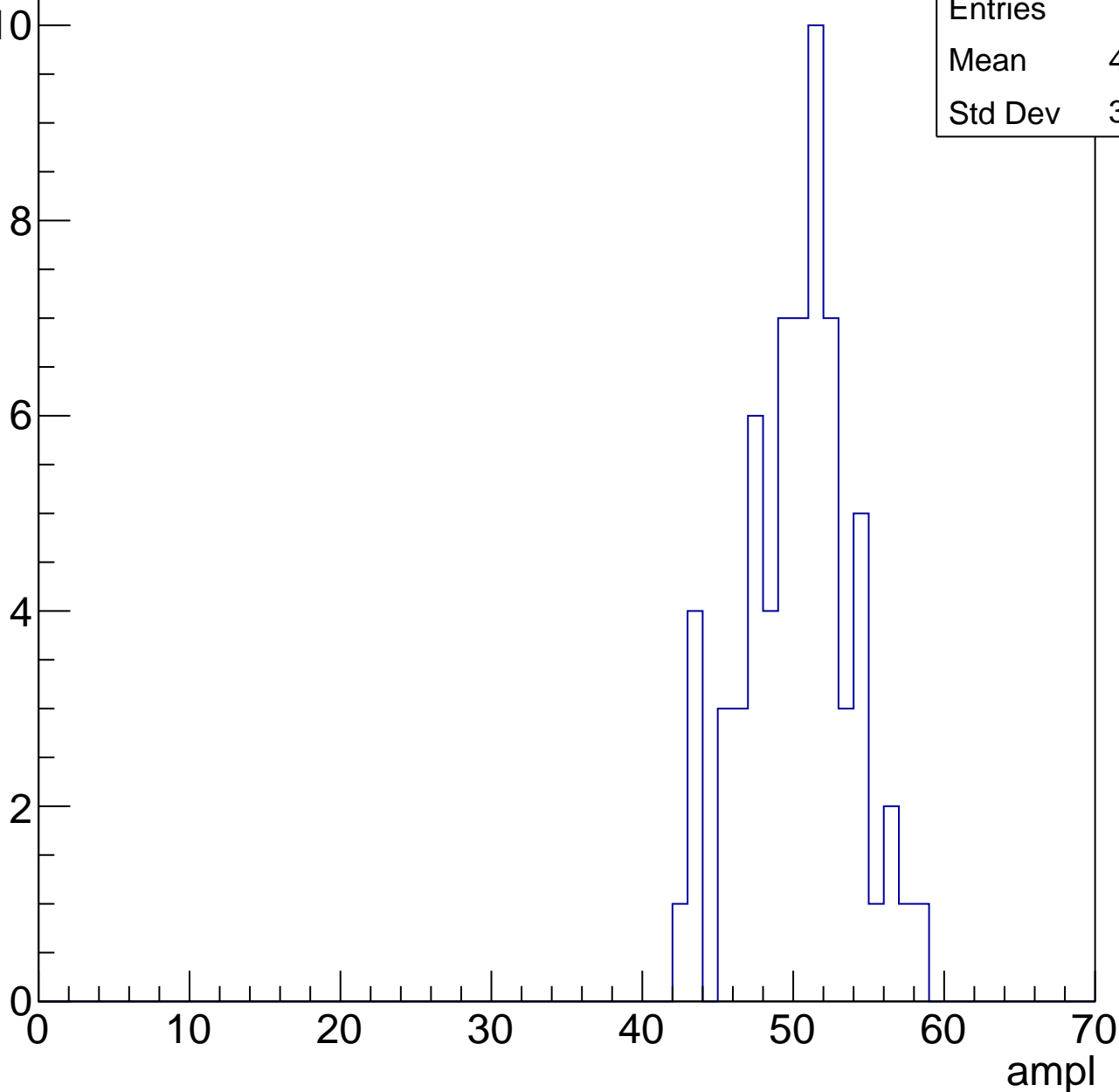


# B0L102S, U6-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	49.83
Std Dev	3.537

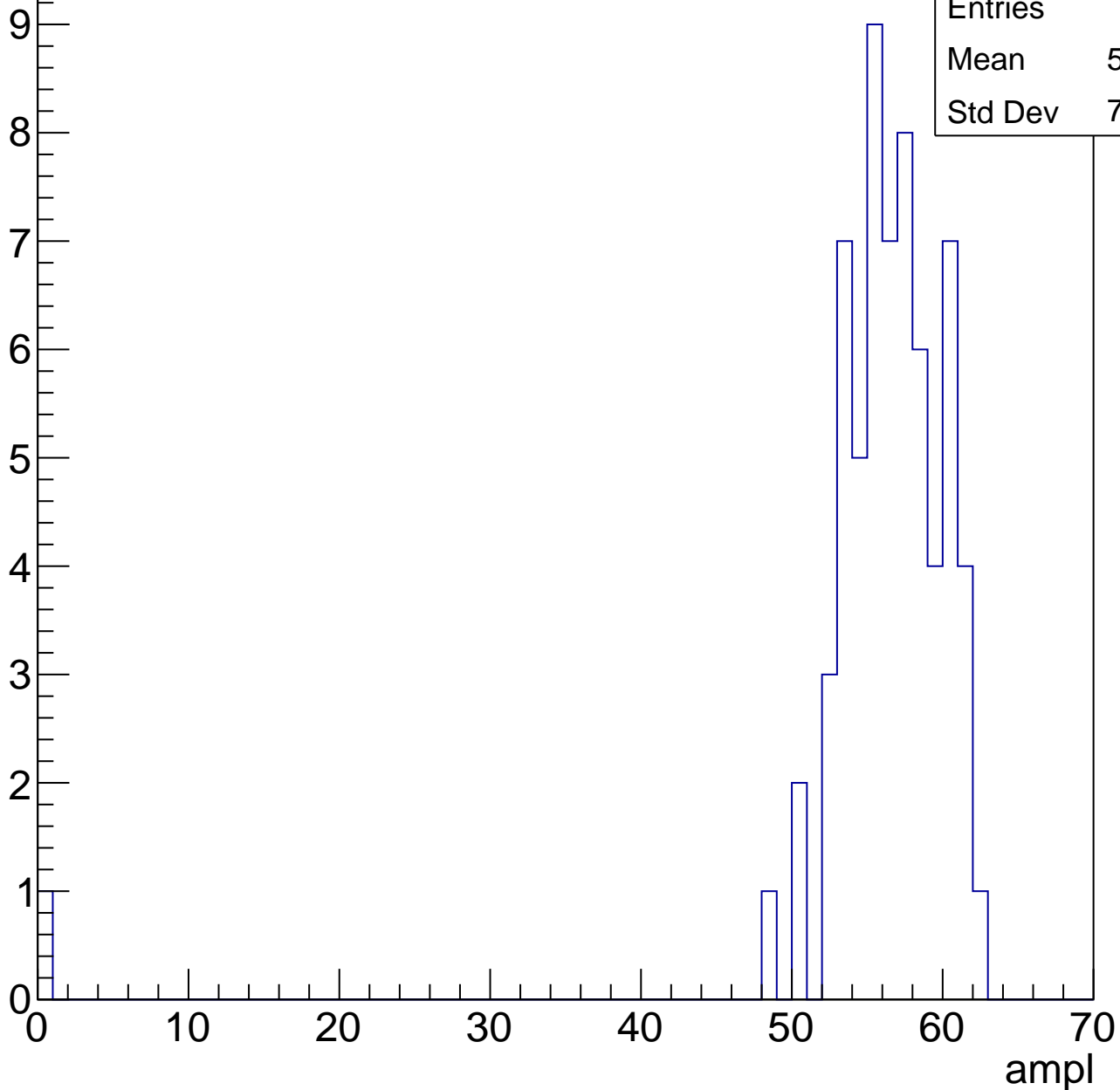


# B0L102S, U6-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	55.35
Std Dev	7.546

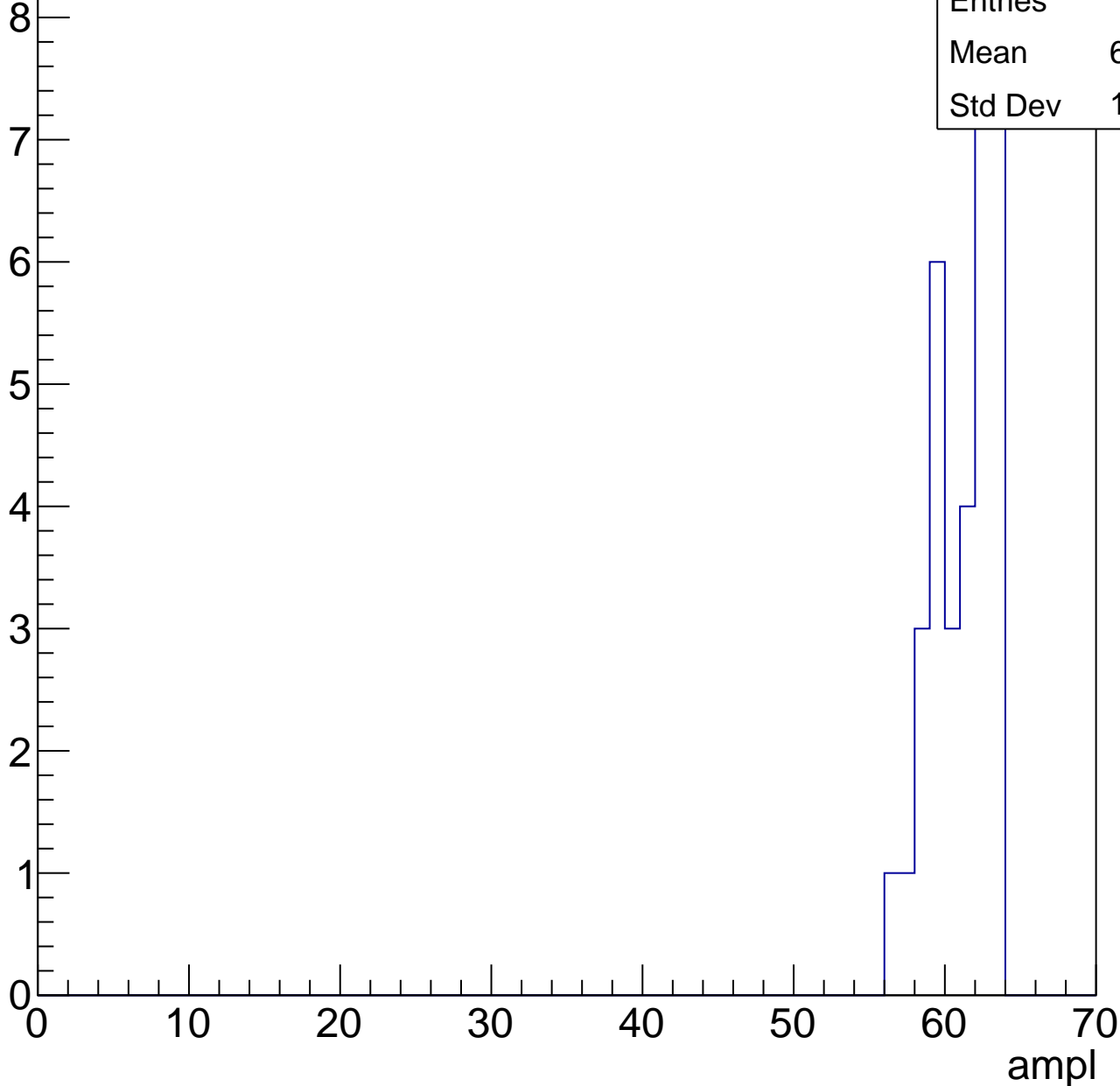


# B0L102S, U6-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

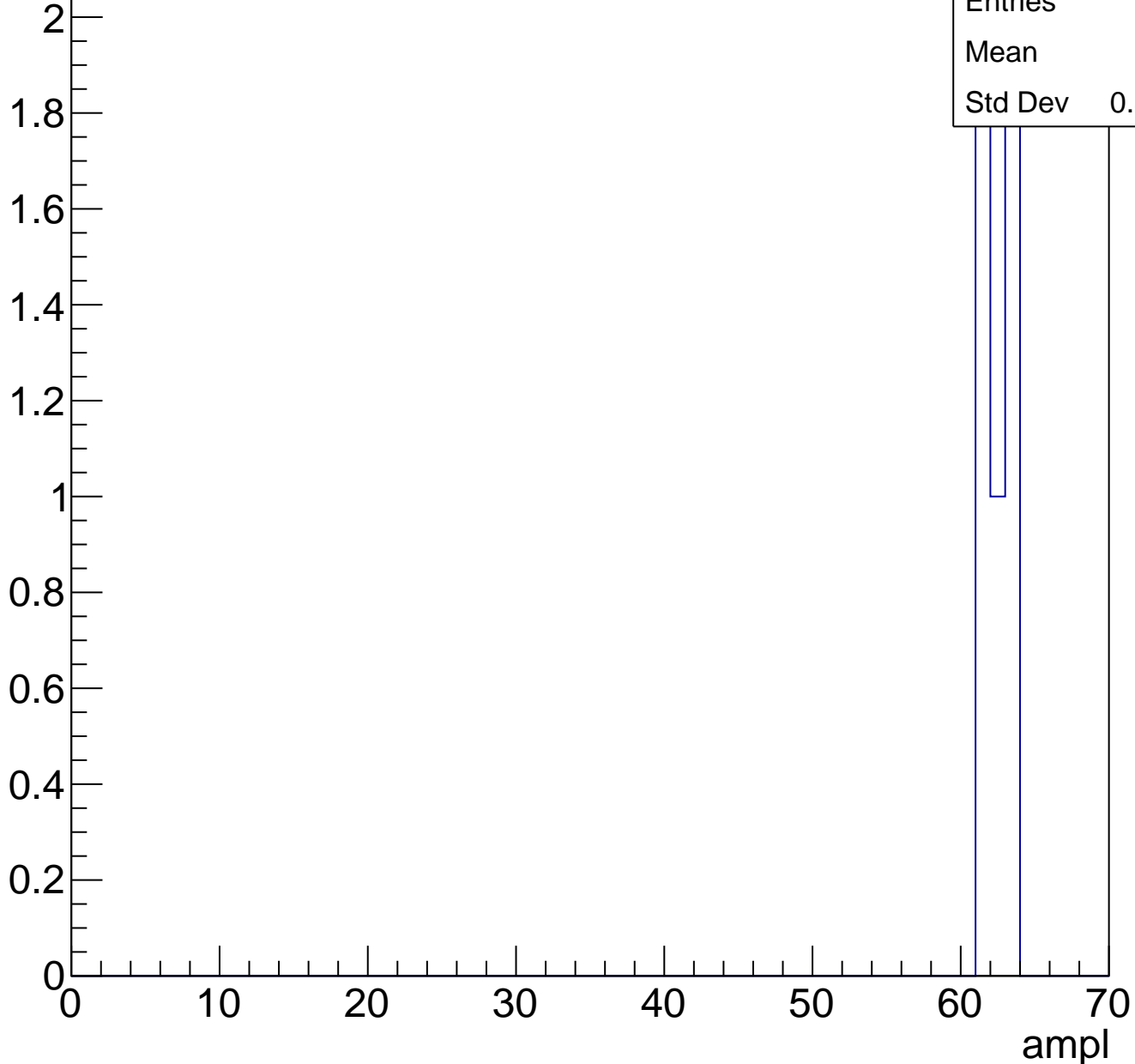
Entries	34
Mean	60.74
Std Dev	1.975



# B0L102S, U6-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch94, adc0

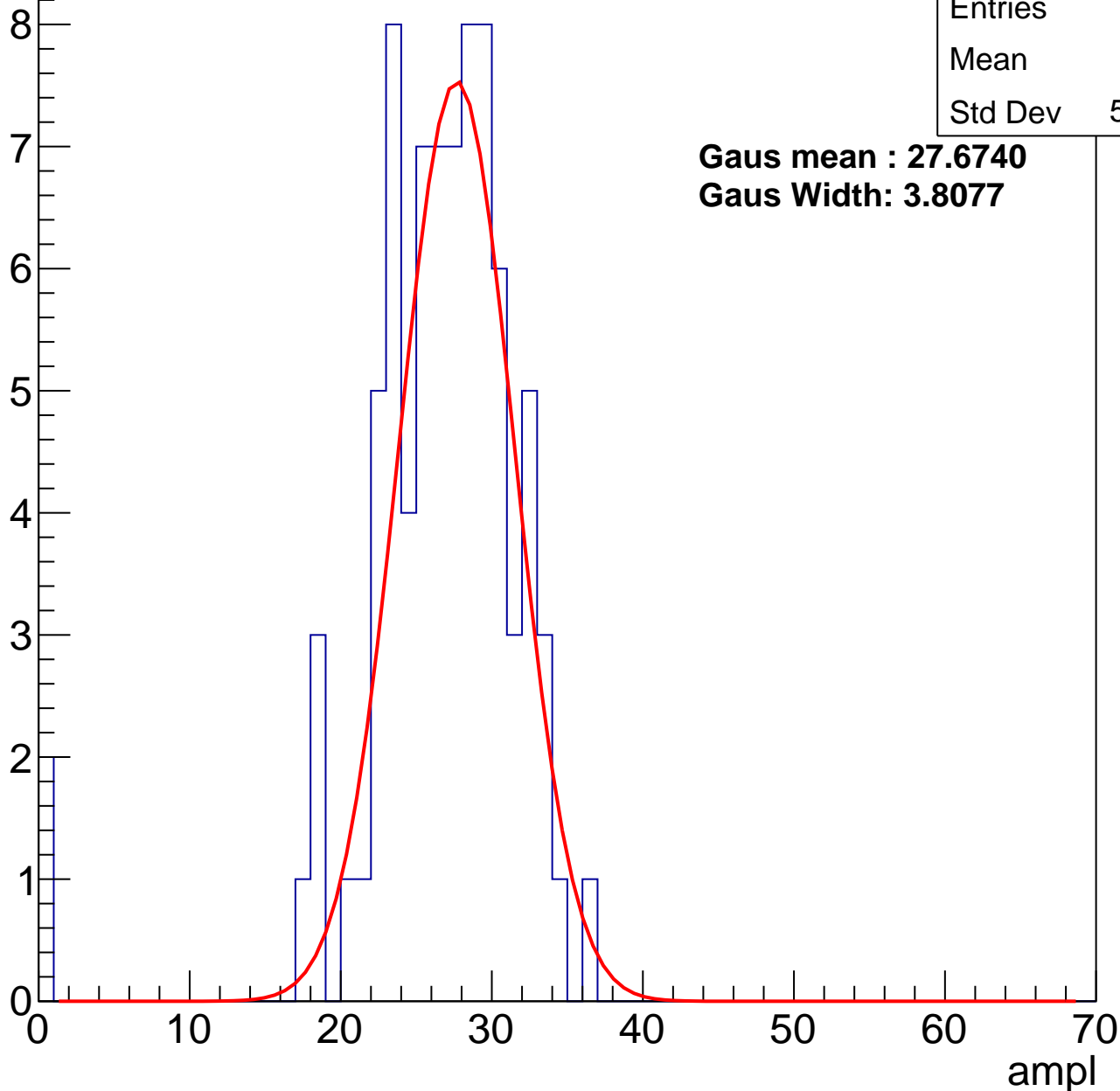
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	26
Std Dev	5.703

**Gaus mean : 27.6740**

**Gaus Width: 3.8077**



# B0L102S, U6-ch94, adc1

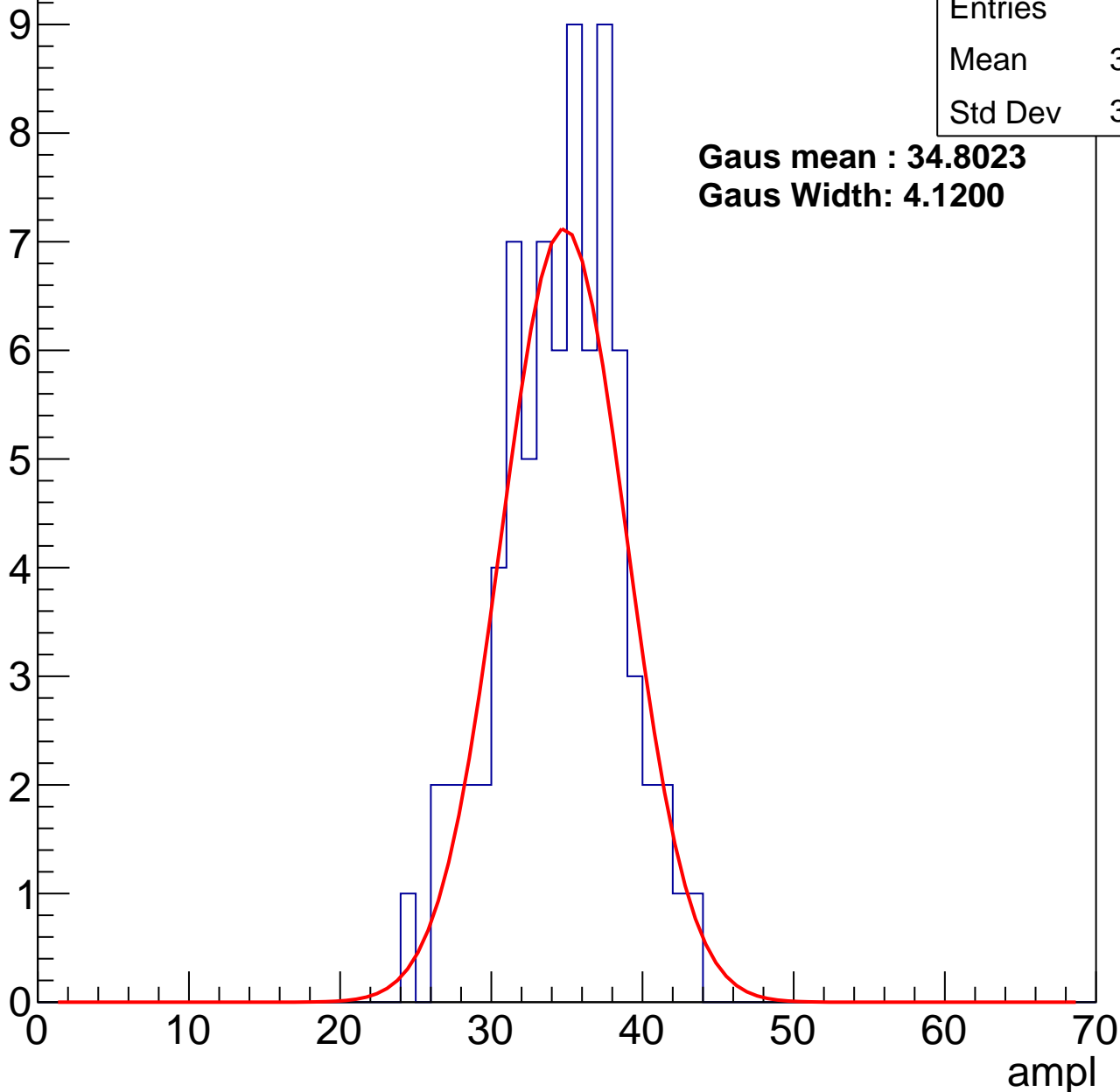
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	34.18
Std Dev	3.947

**Gaus mean : 34.8023**

**Gaus Width: 4.1200**



# B0L102S, U6-ch94, adc2

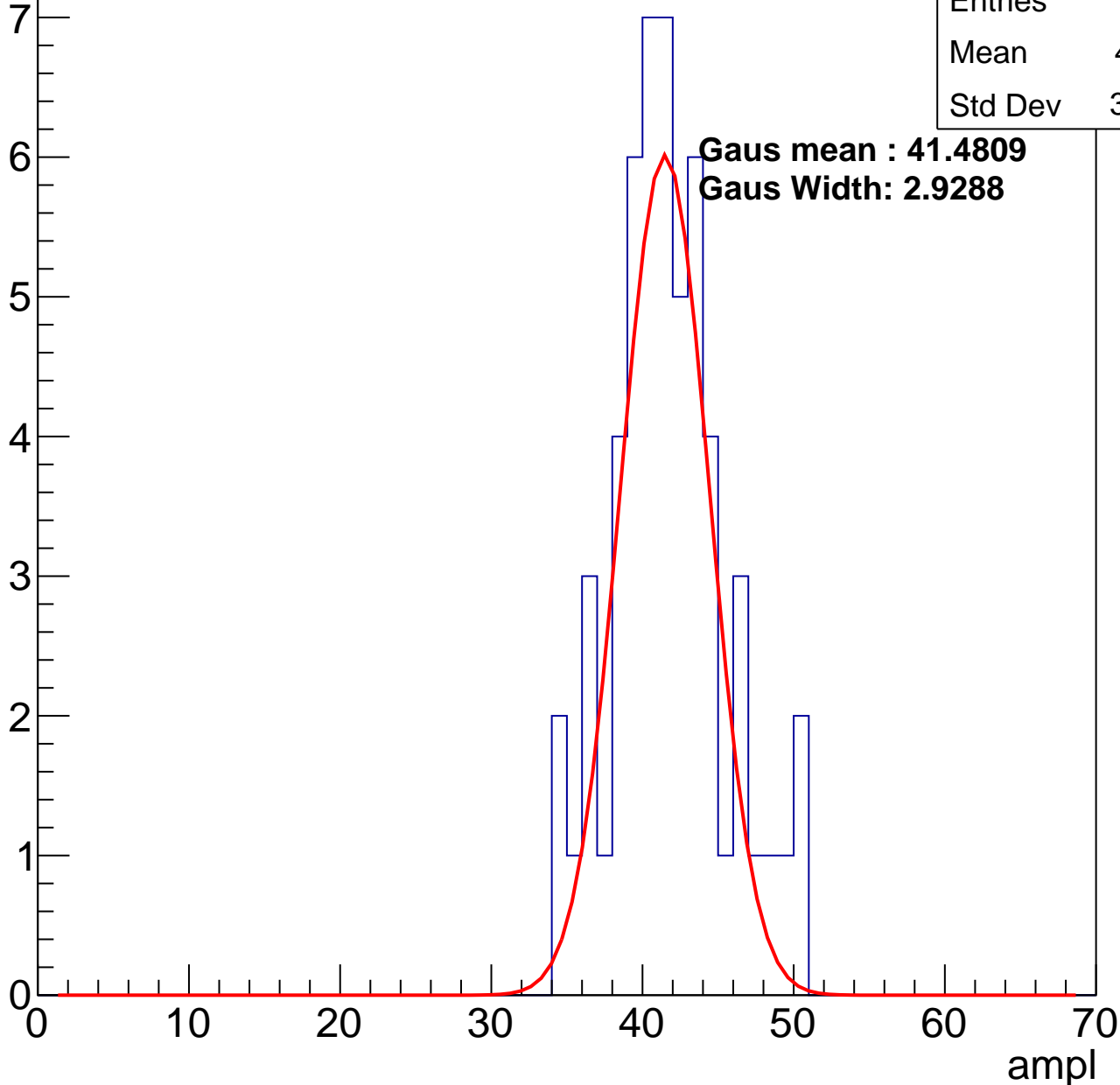
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	41.31
Std Dev	3.702

**Gaus mean : 41.4809**

**Gaus Width: 2.9288**

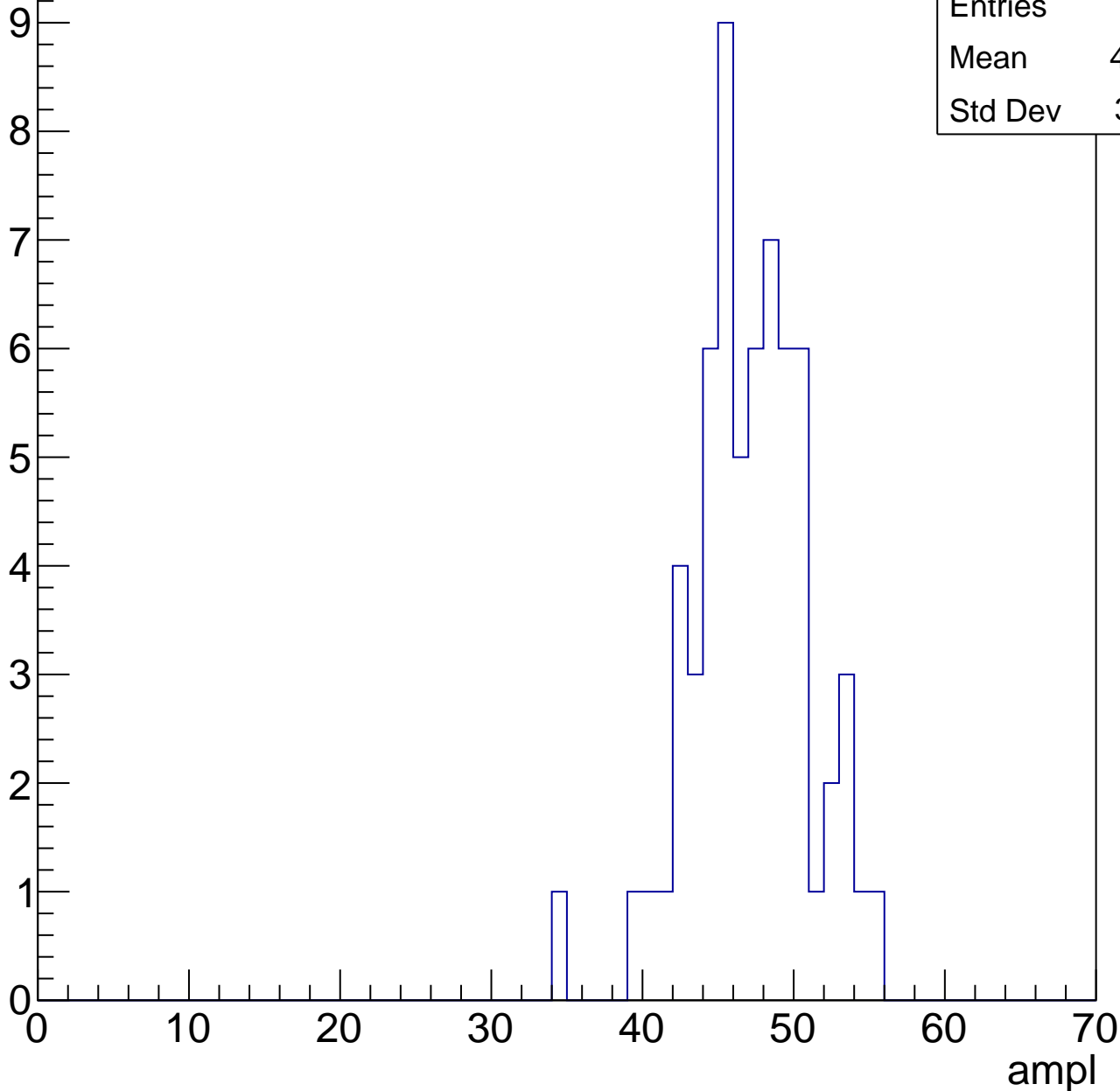


# B0L102S, U6-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

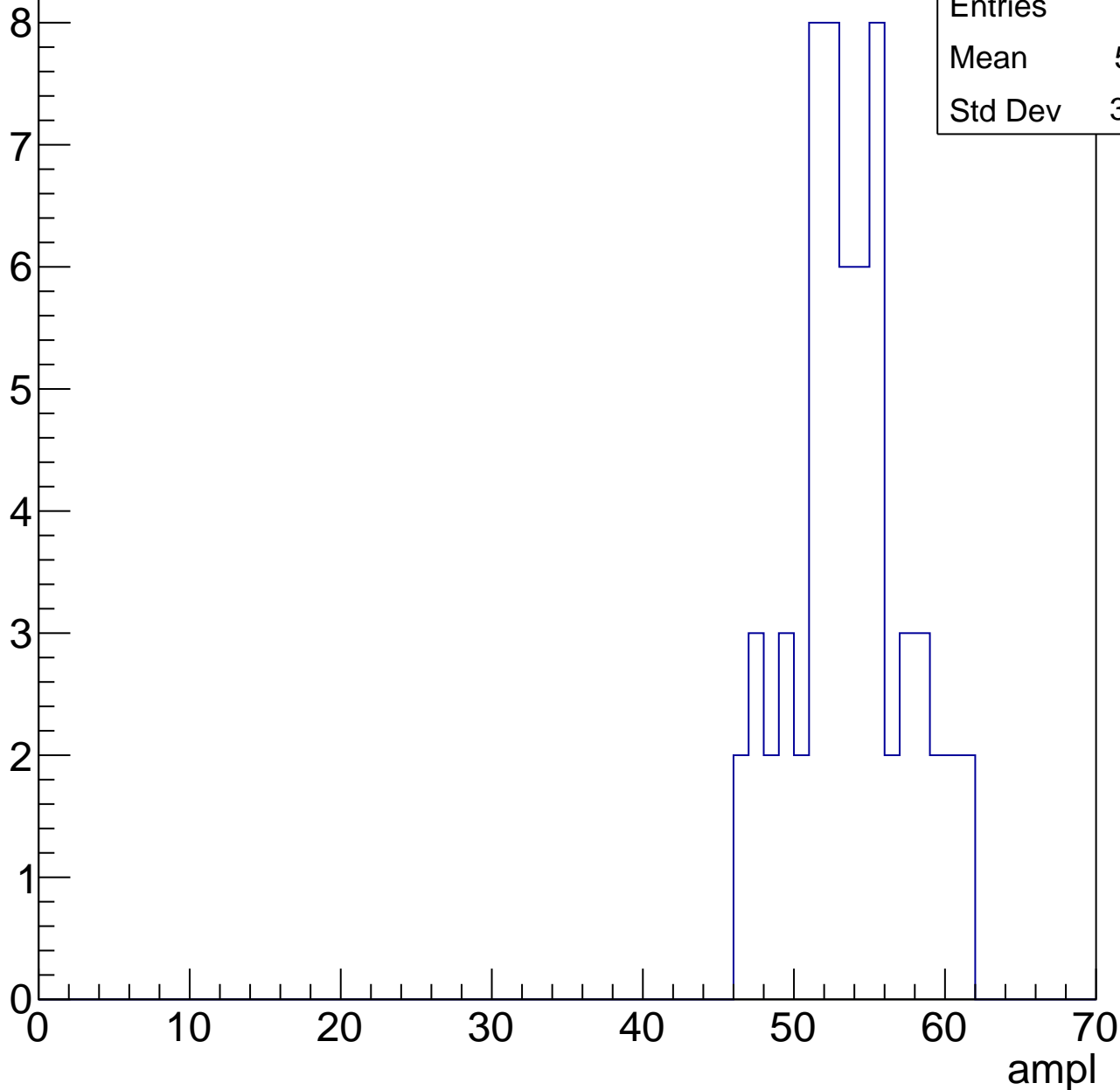
Entries	64
Mean	46.64
Std Dev	3.801



# B0L102S, U6-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

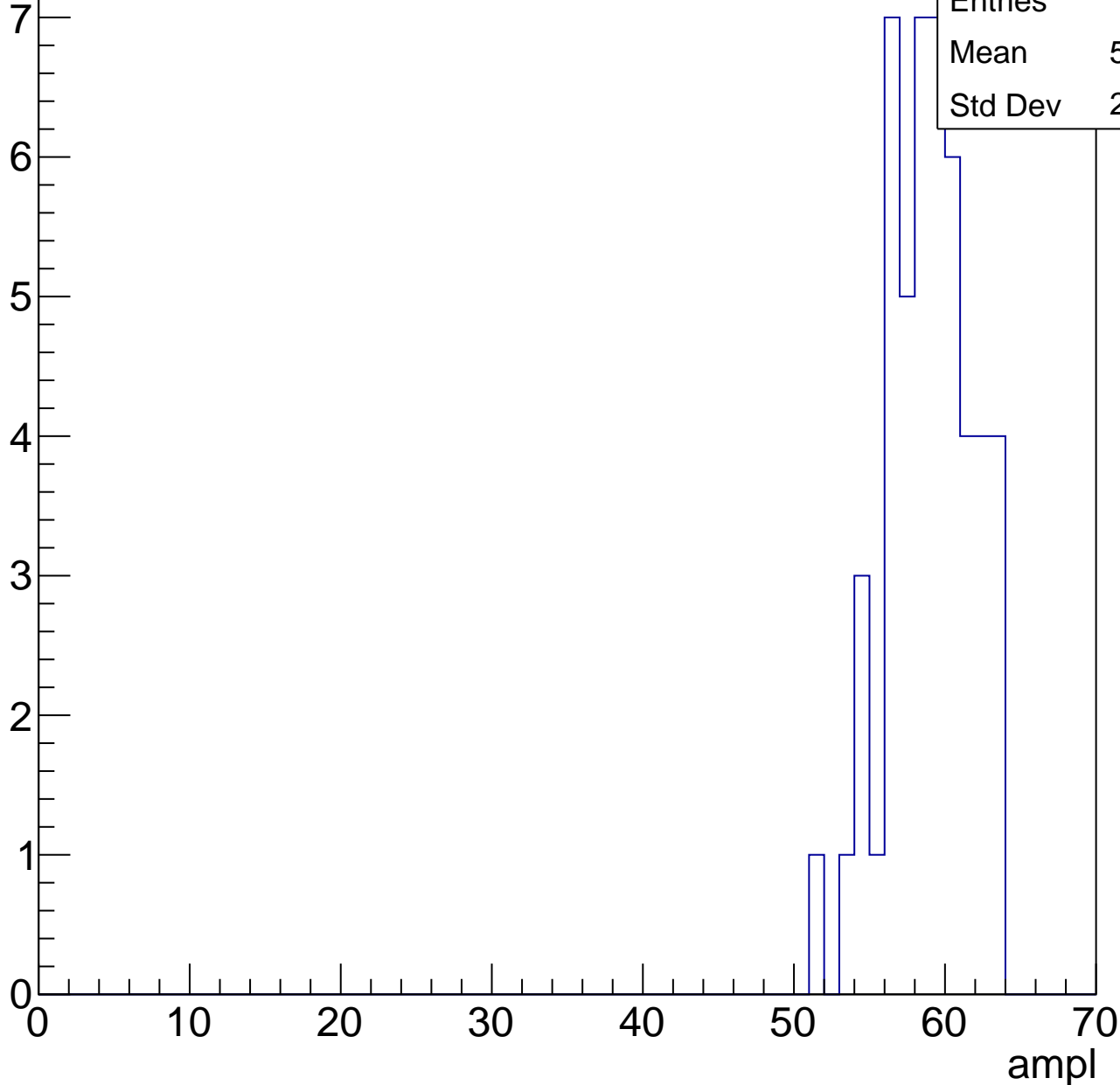
Entry



# B0L102S, U6-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

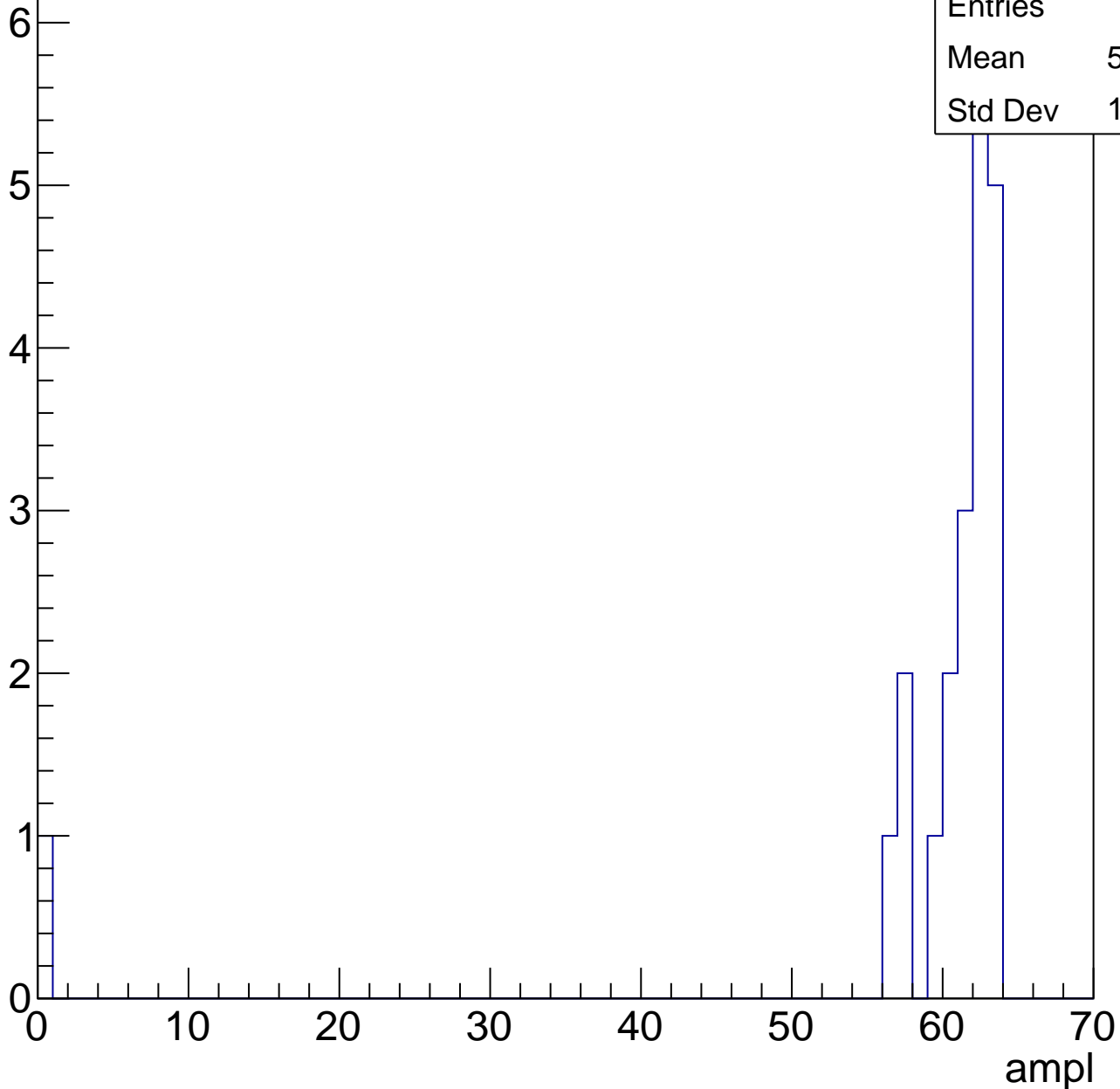


# B0L102S, U6-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	21
Mean	58.05
Std Dev	13.14

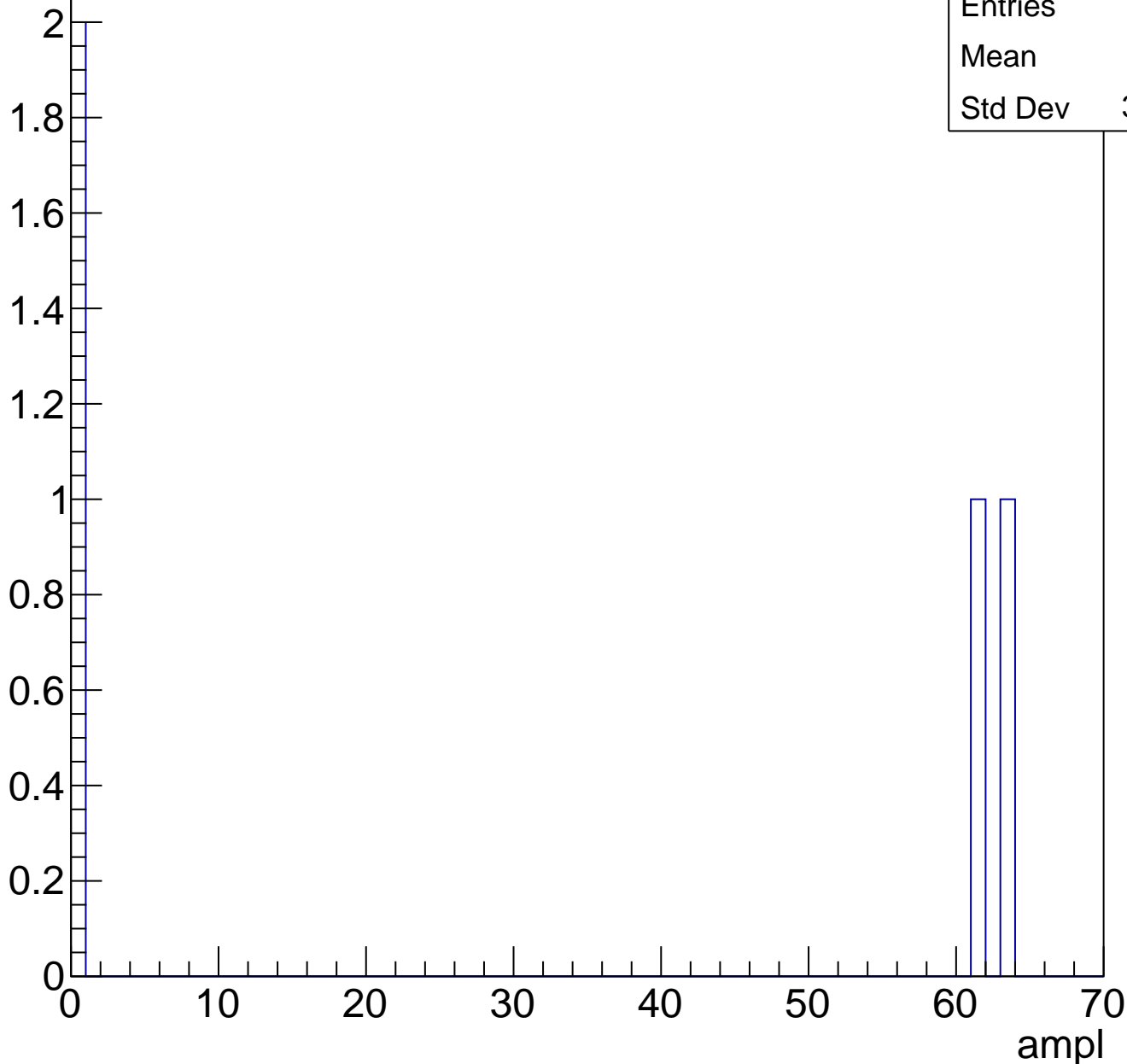




# B0L102S, U6-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	4
Mean	31
Std Dev	31.01

# B0L102S, U6-ch95, adc0

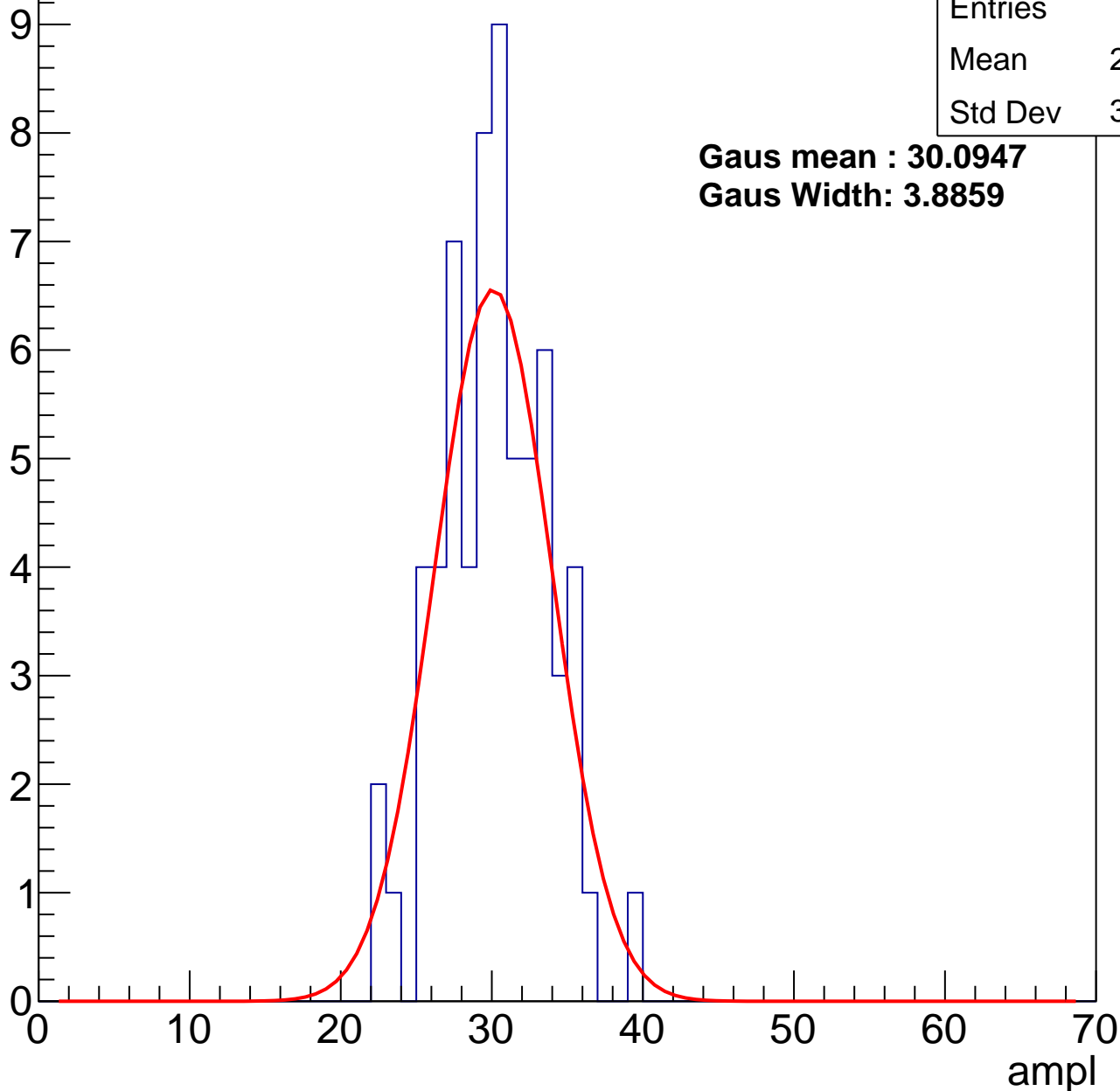
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	29.75
Std Dev	3.464

**Gaus mean : 30.0947**

**Gaus Width: 3.8859**



# B0L102S, U6-ch95, adc1

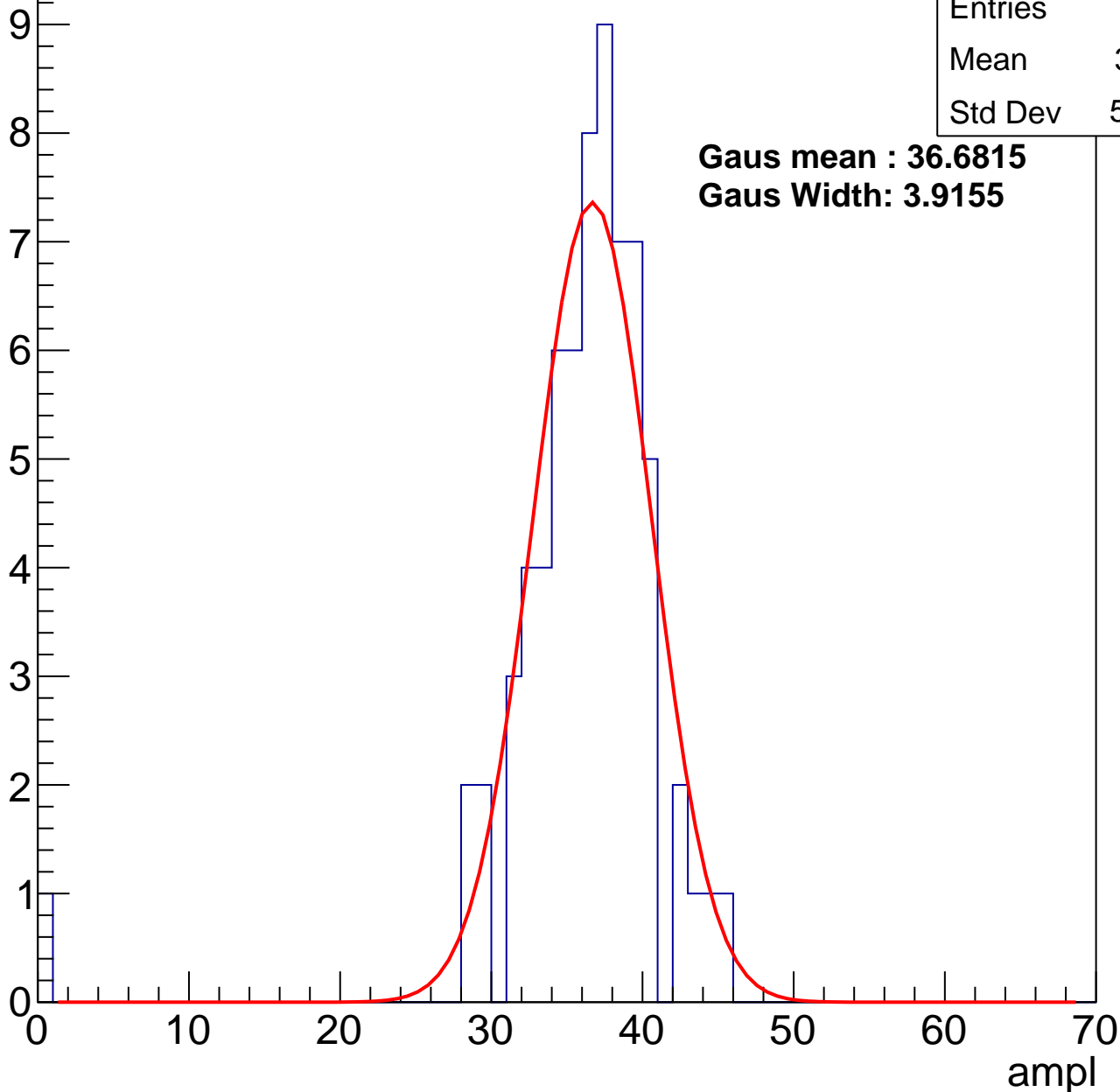
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	35.61
Std Dev	5.598

**Gaus mean : 36.6815**

**Gaus Width: 3.9155**



# B0L102S, U6-ch95, adc2

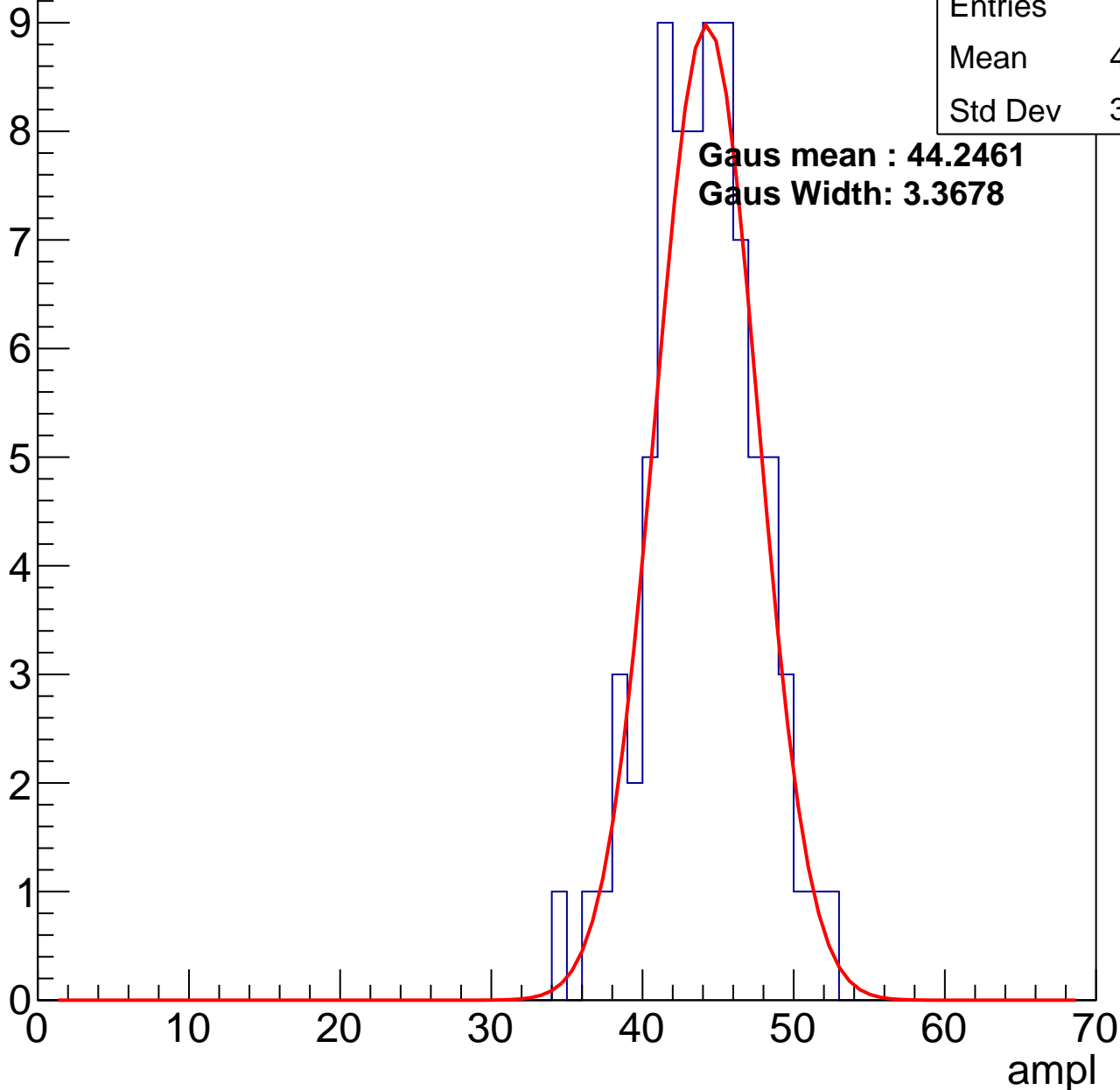
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	43.62
Std Dev	3.462

**Gaus mean : 44.2461**

**Gaus Width: 3.3678**

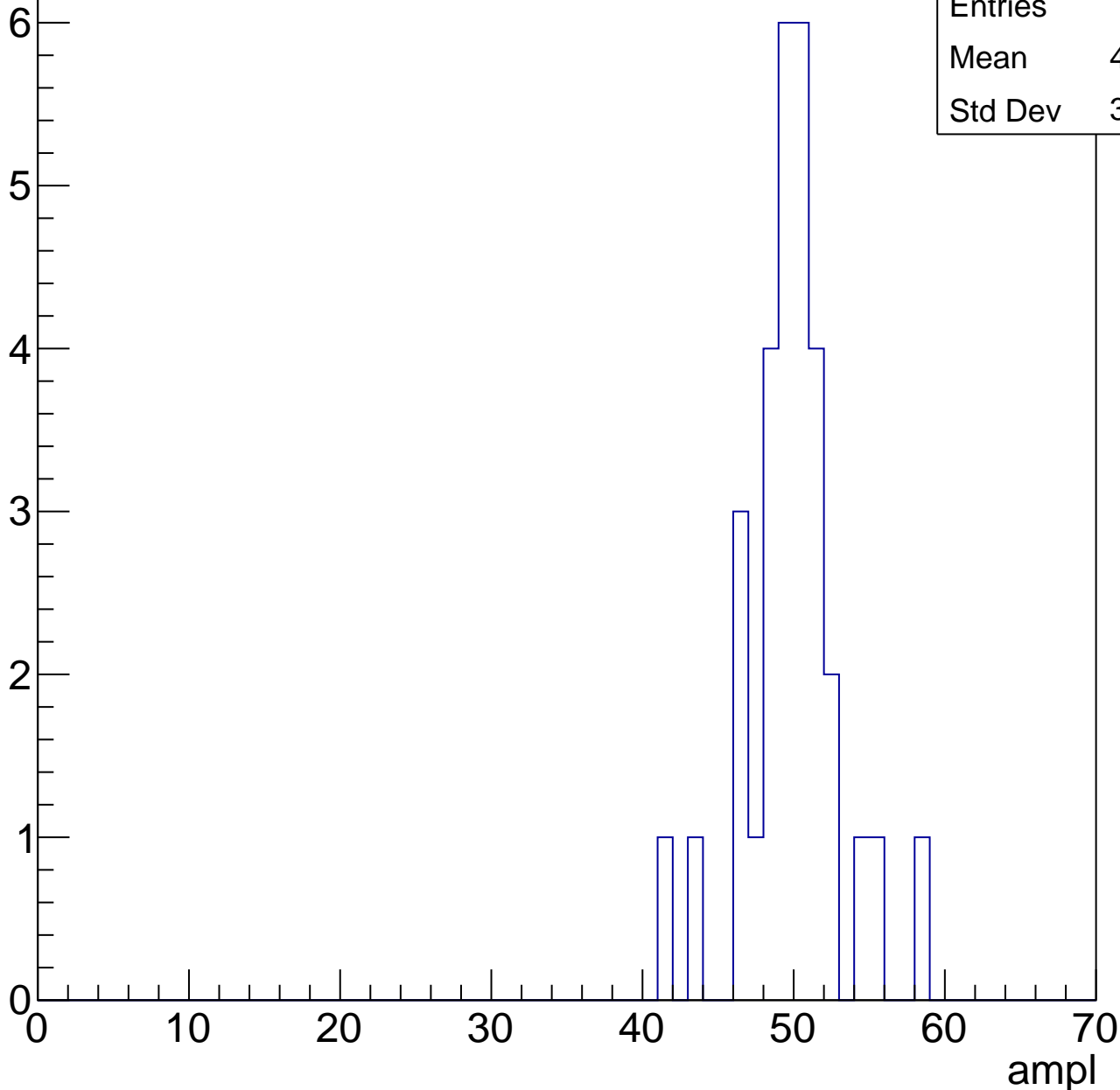


# B0L102S, U6-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	31
Mean	49.35
Std Dev	3.178



# B0L102S, U6-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

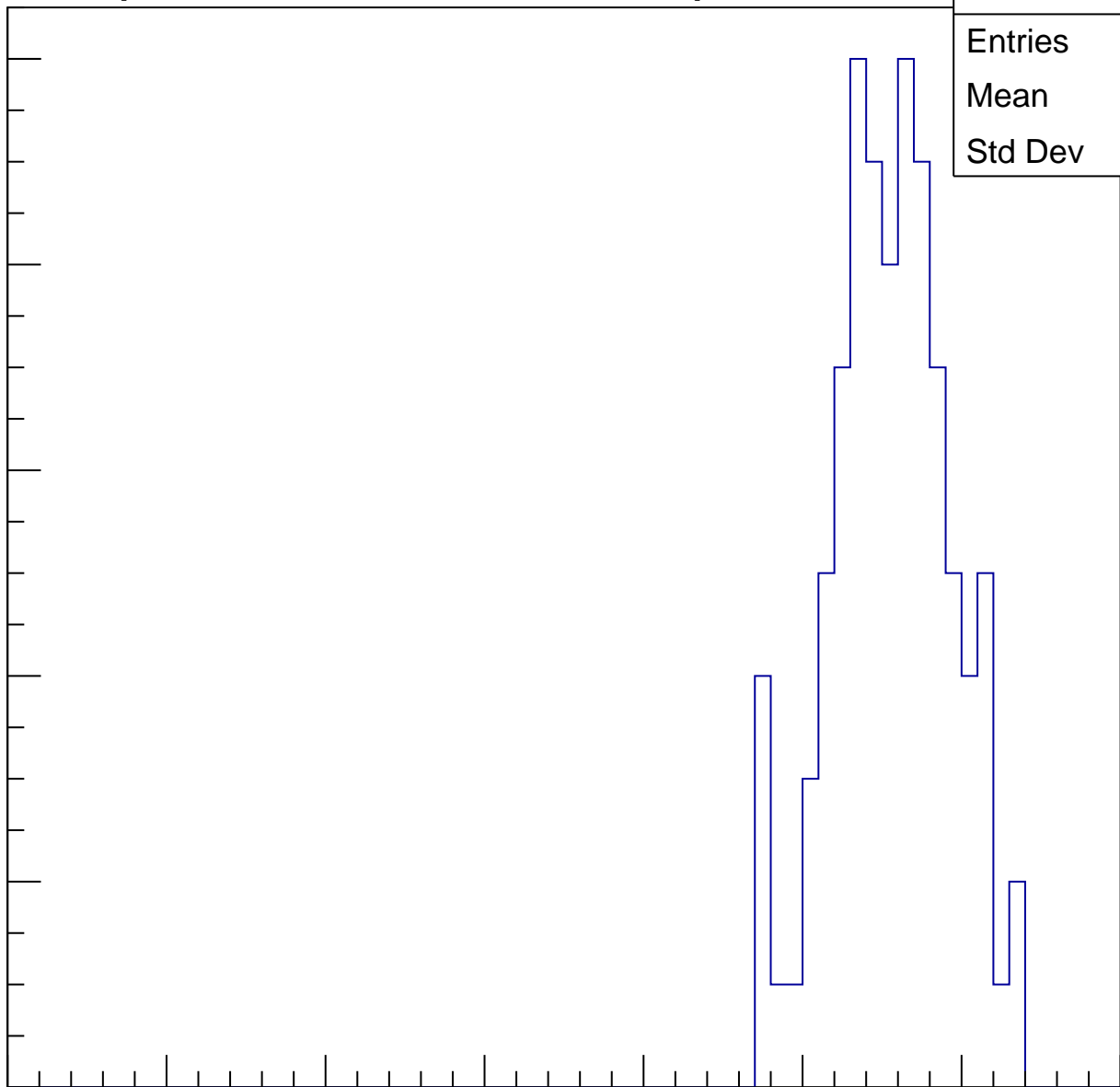
Entries	91
Mean	55.13
Std Dev	3.71

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

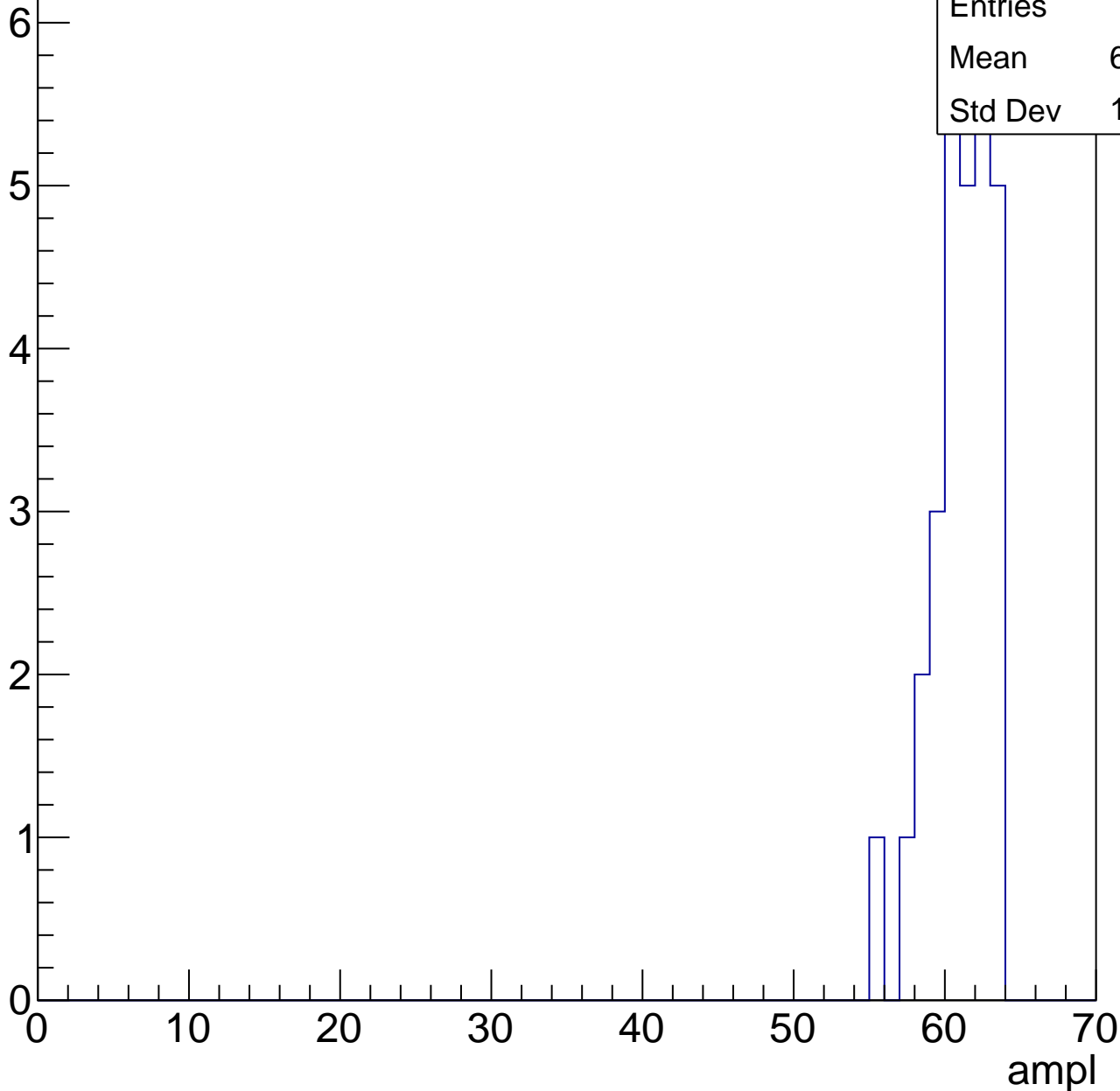


# B0L102S, U6-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

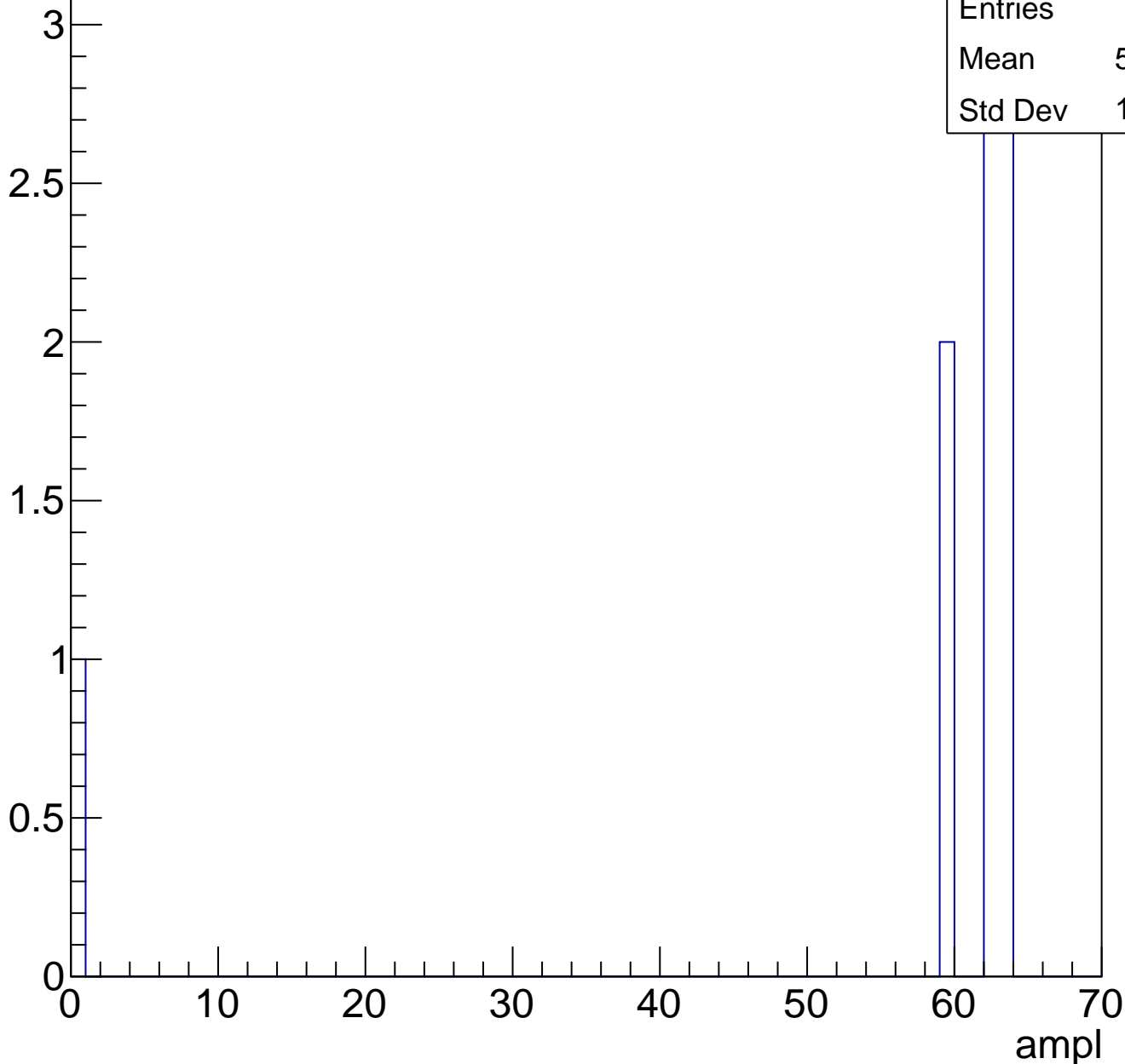
Entries	29
Mean	60.59
Std Dev	1.939



# B0L102S, U6-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch96, adc0

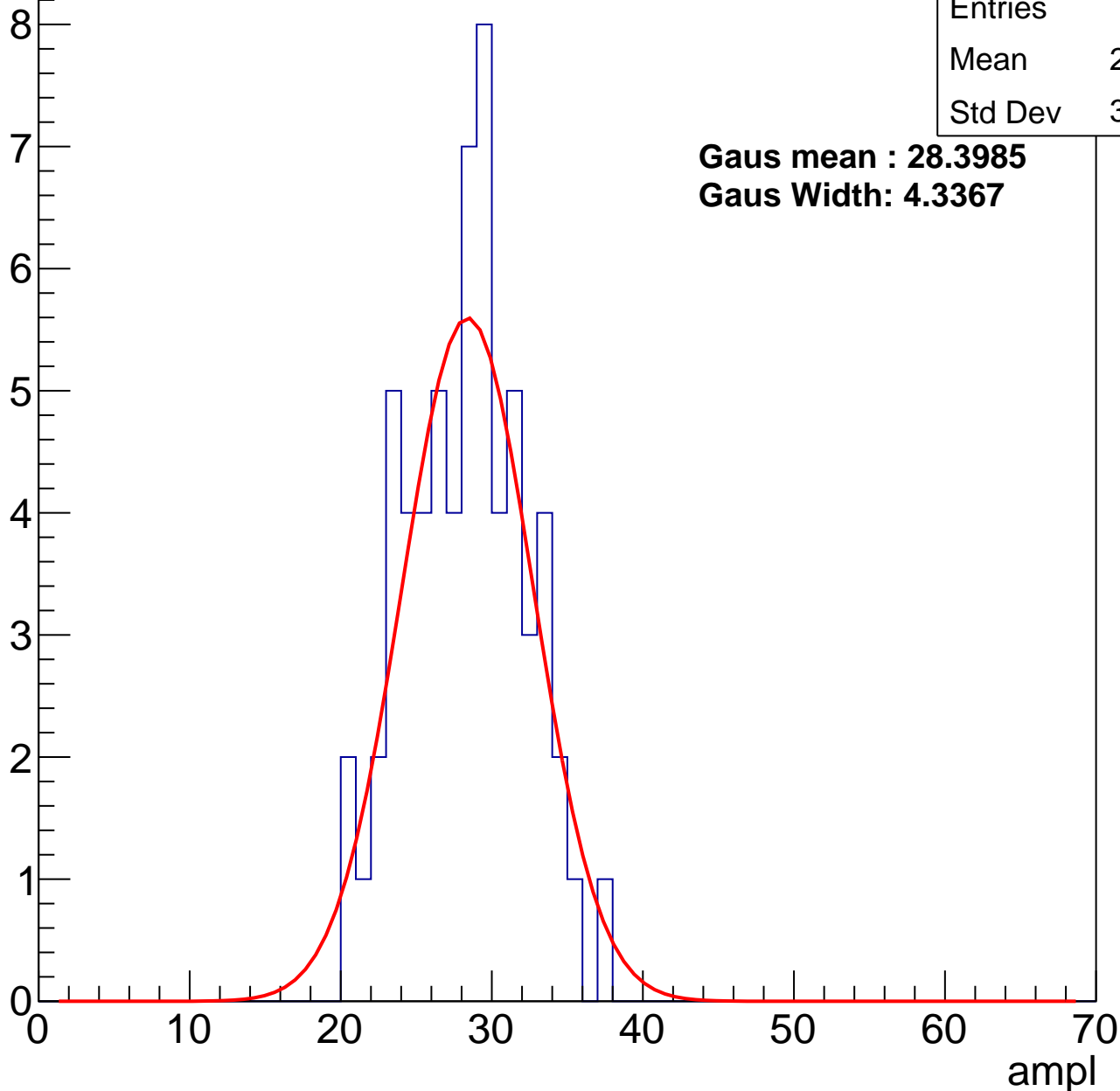
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	27.82
Std Dev	3.842

**Gaus mean : 28.3985**

**Gaus Width: 4.3367**



# B0L102S, U6-ch96, adc1

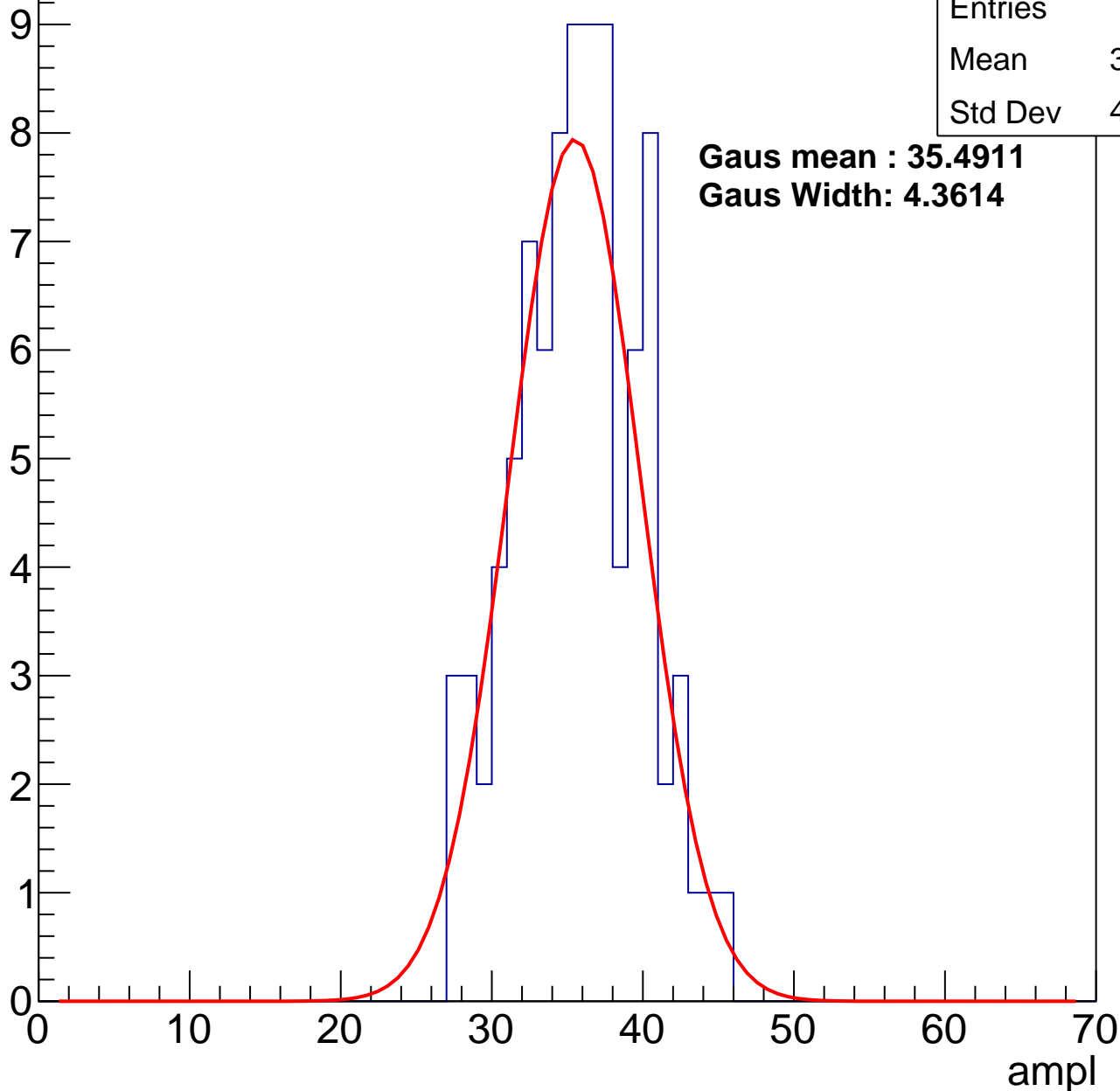
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	91
Mean	35.27
Std Dev	4.087

**Gaus mean : 35.4911**

**Gaus Width: 4.3614**



# B0L102S, U6-ch96, adc2

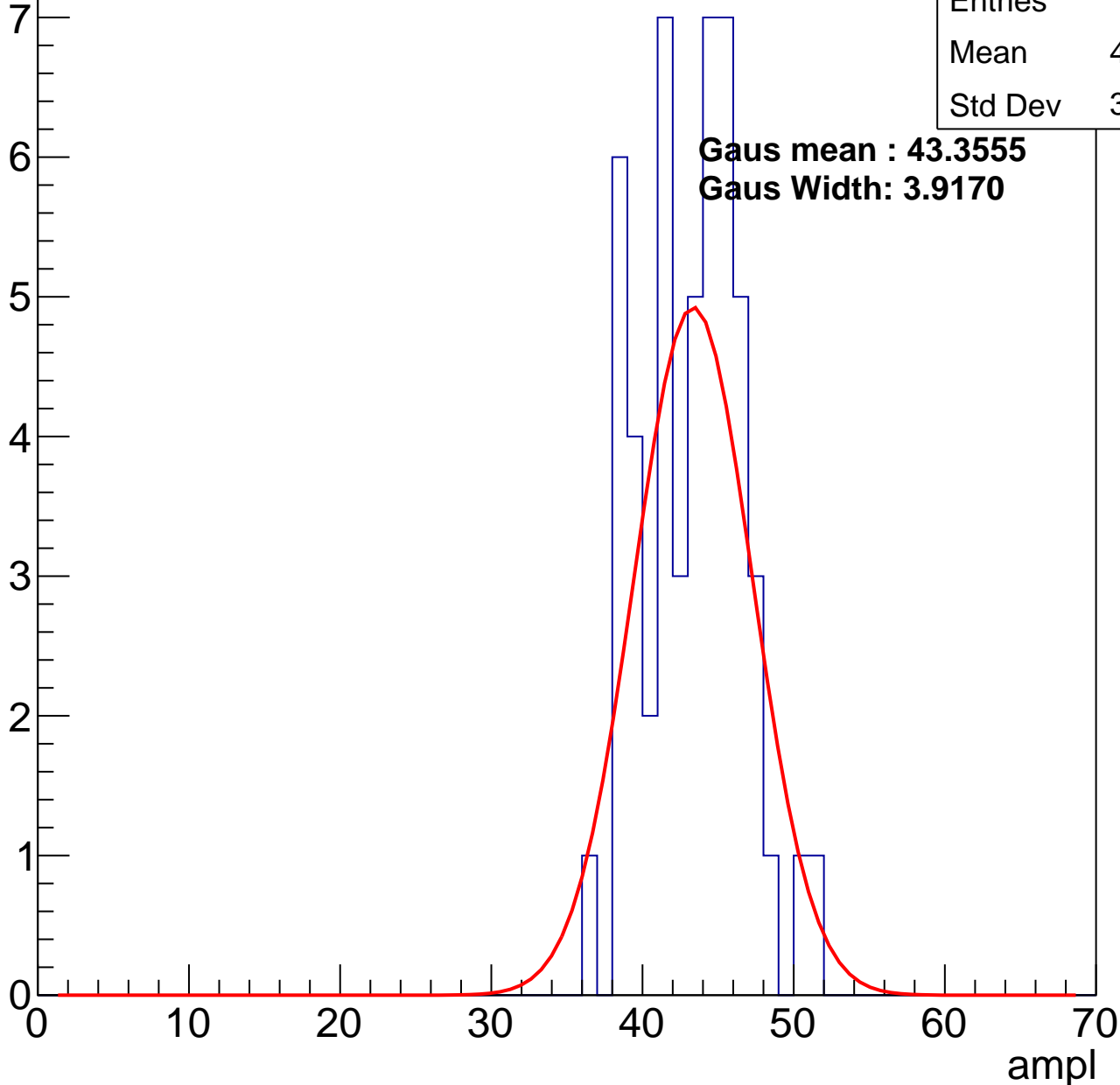
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	42.85
Std Dev	3.299

**Gaus mean : 43.3555**

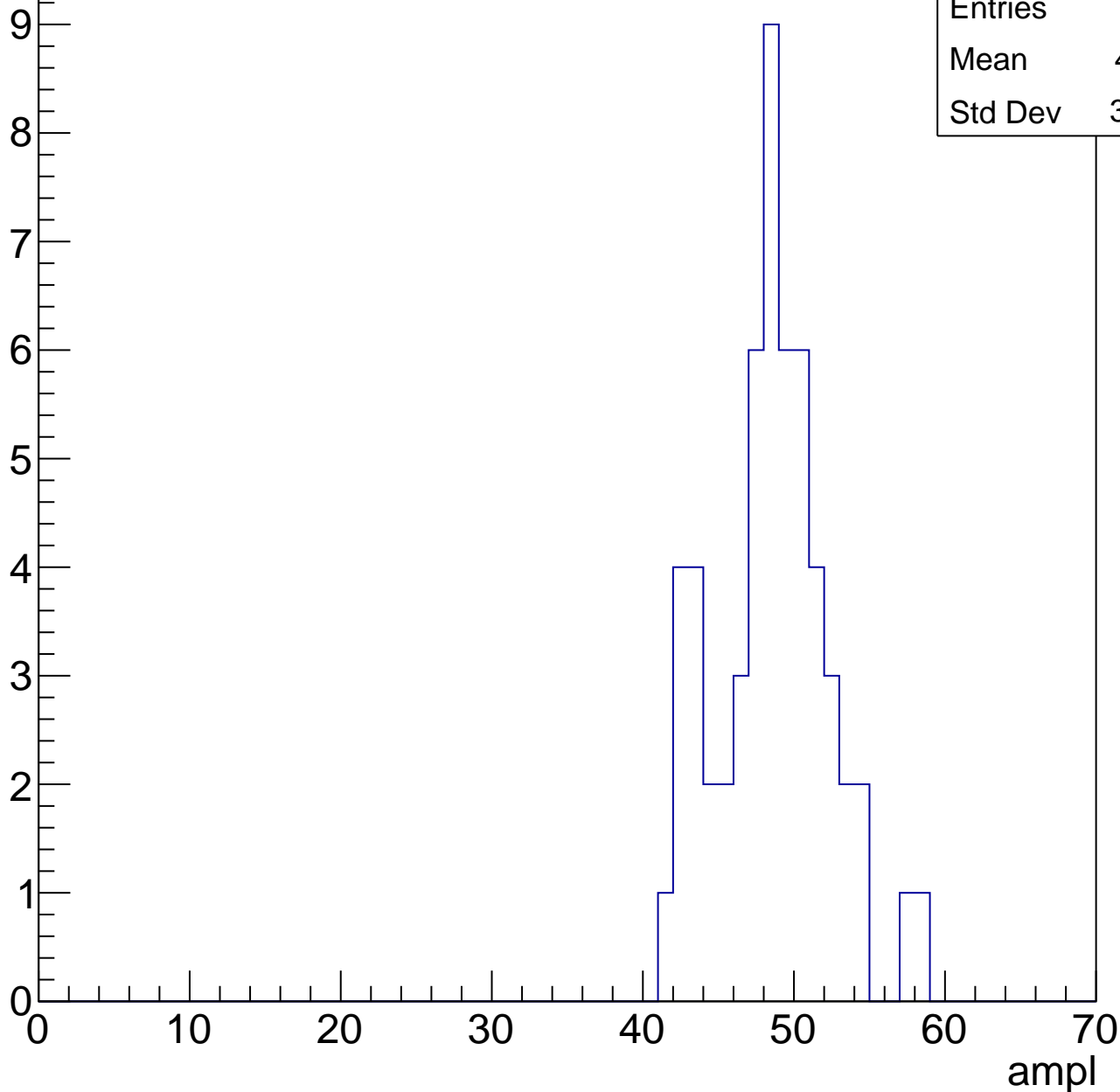
**Gaus Width: 3.9170**



# B0L102S, U6-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	54.37
Std Dev	3.505

Entry

10  
8  
6  
4  
2  
0

0

10

20

30

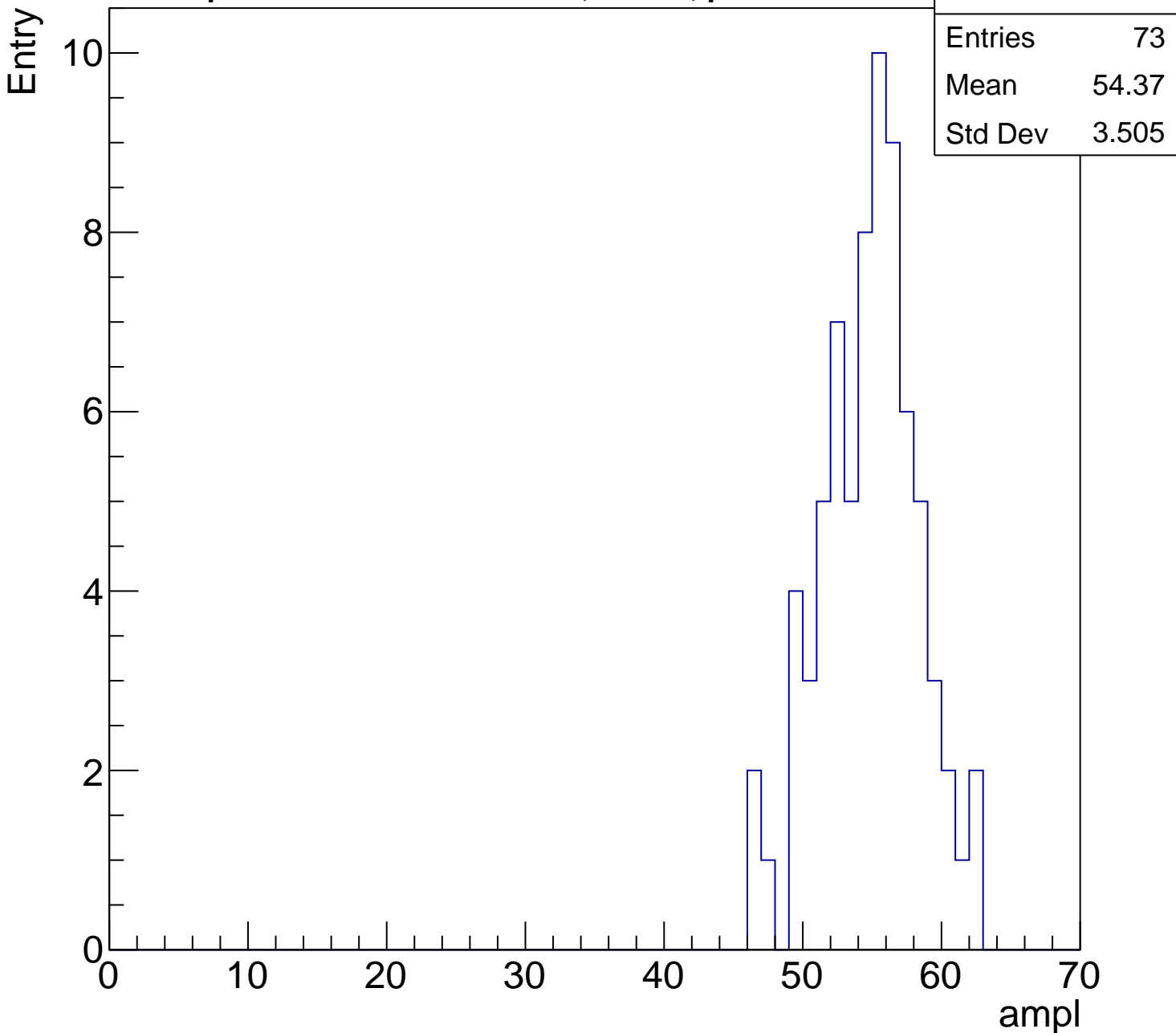
40

50

60

70

ampl

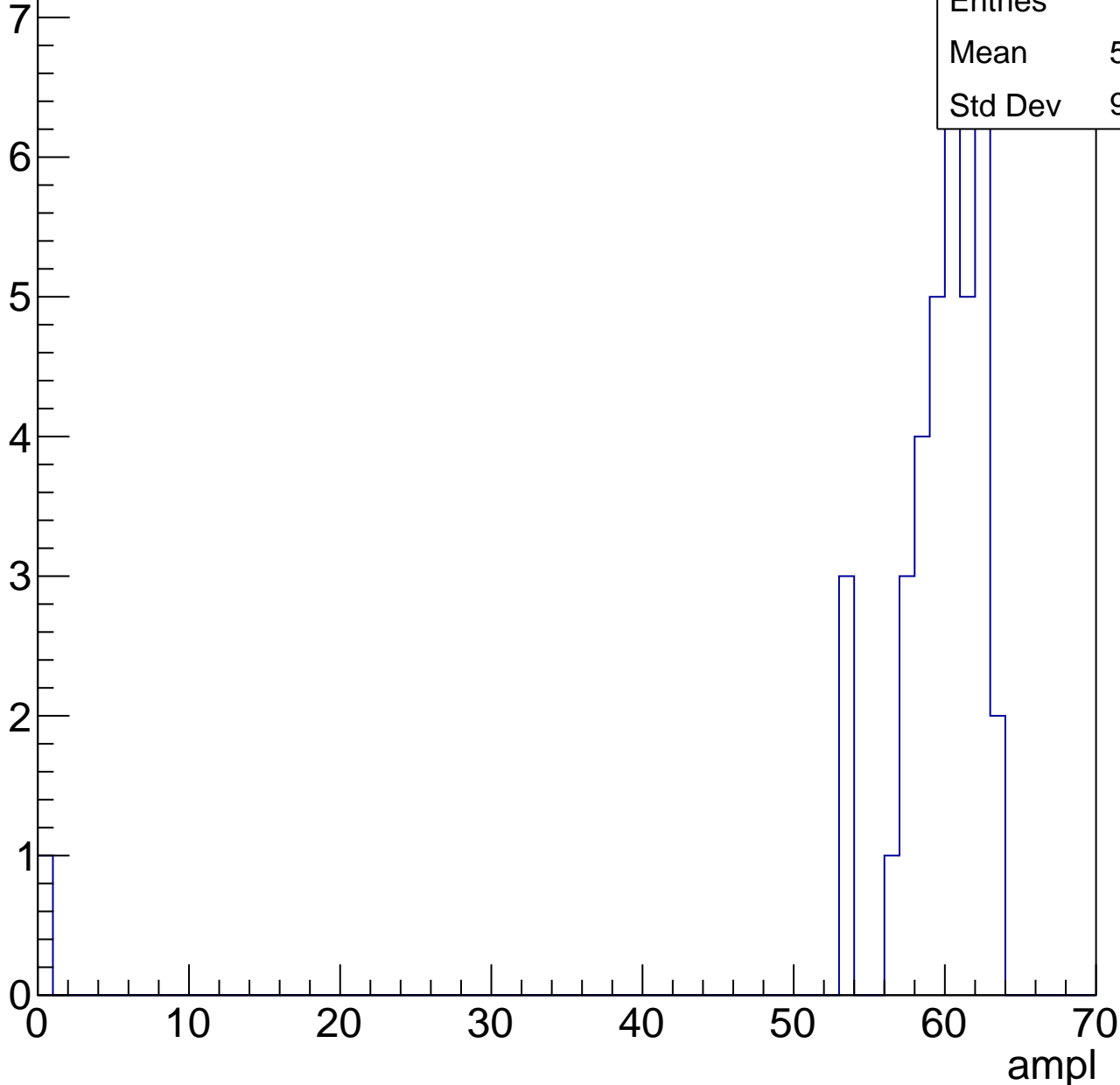


# B0L102S, U6-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	38
Mean	57.84
Std Dev	9.848

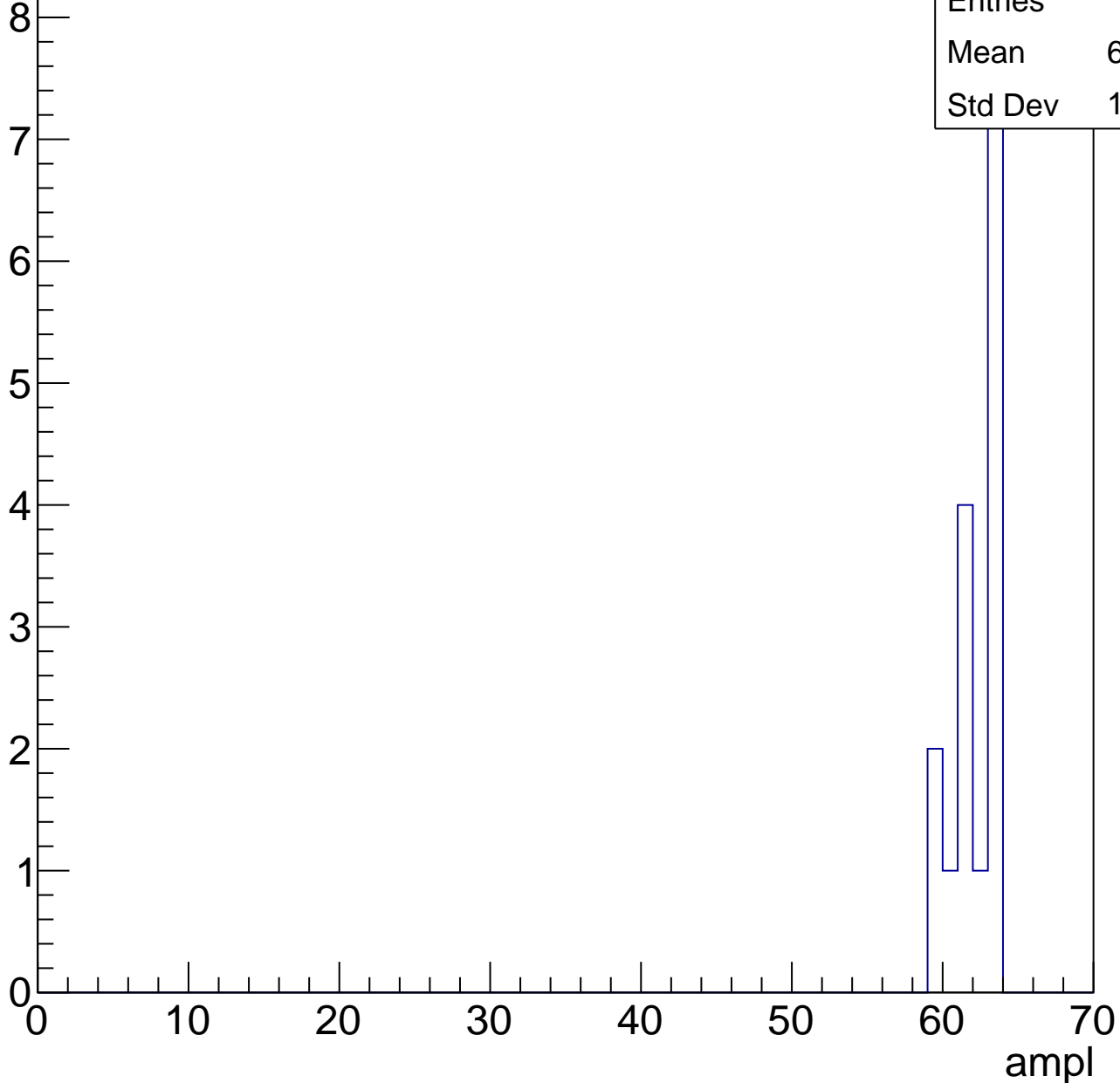


# B0L102S, U6-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	16
Mean	61.75
Std Dev	1.436





# B0L102S, U6-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch97, adc0

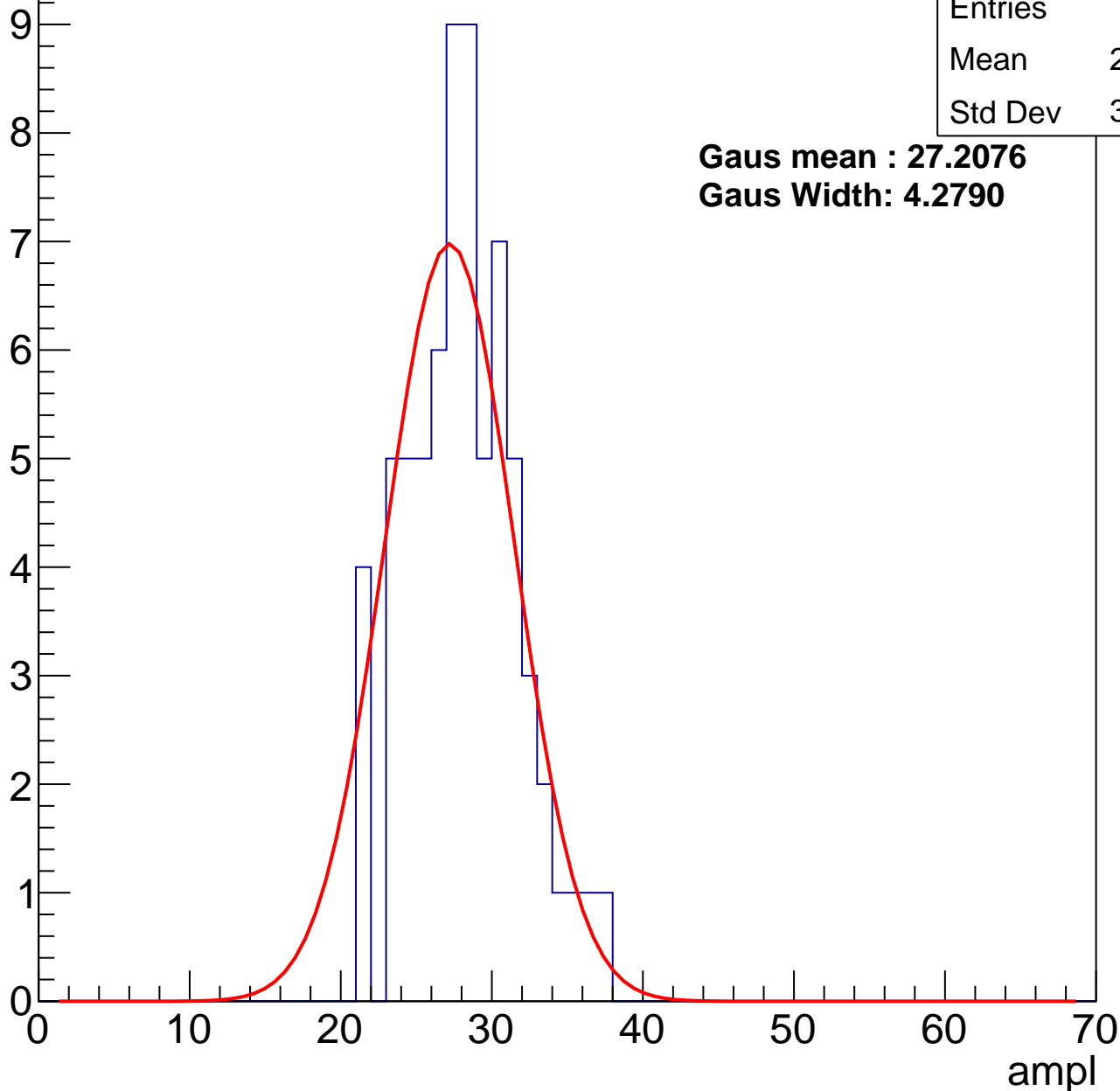
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	27.67
Std Dev	3.566

**Gaus mean : 27.2076**

**Gaus Width: 4.2790**



# B0L102S, U6-ch97, adc1

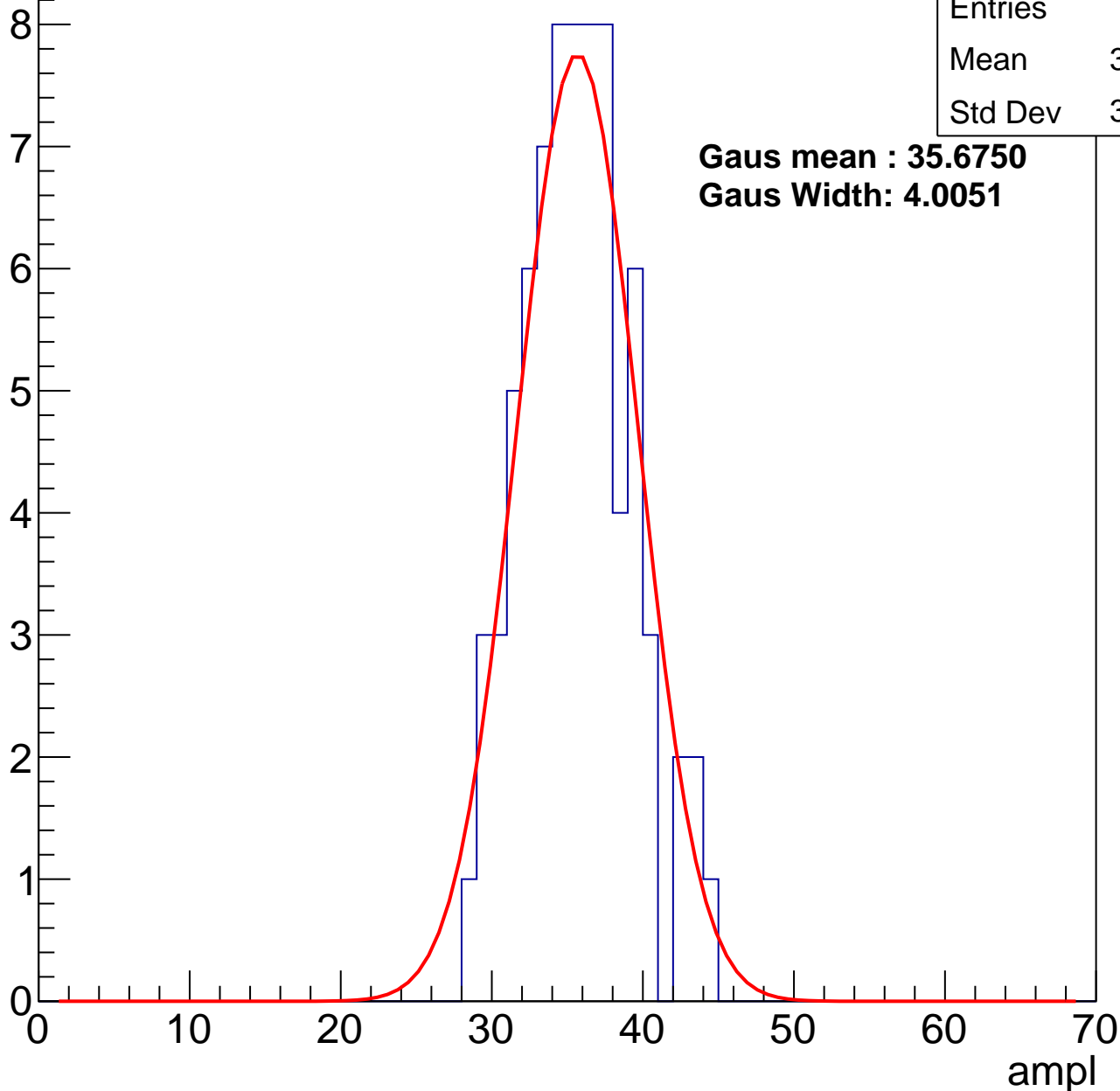
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	35.19
Std Dev	3.565

**Gaus mean : 35.6750**

**Gaus Width: 4.0051**



# B0L102S, U6-ch97, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	42.23
Std Dev	3.929

**Gaus mean : 43.2707**

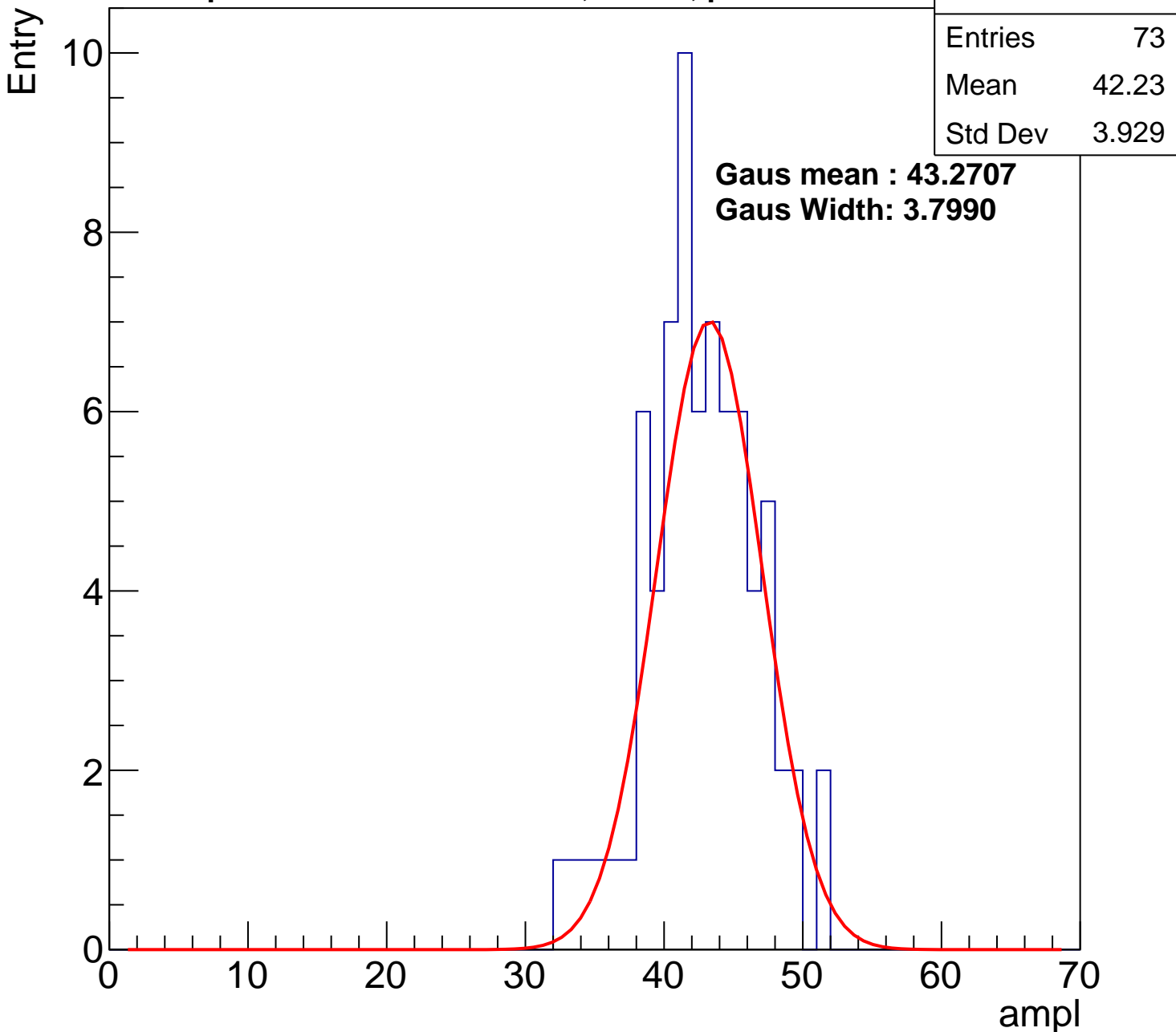
**Gaus Width: 3.7990**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

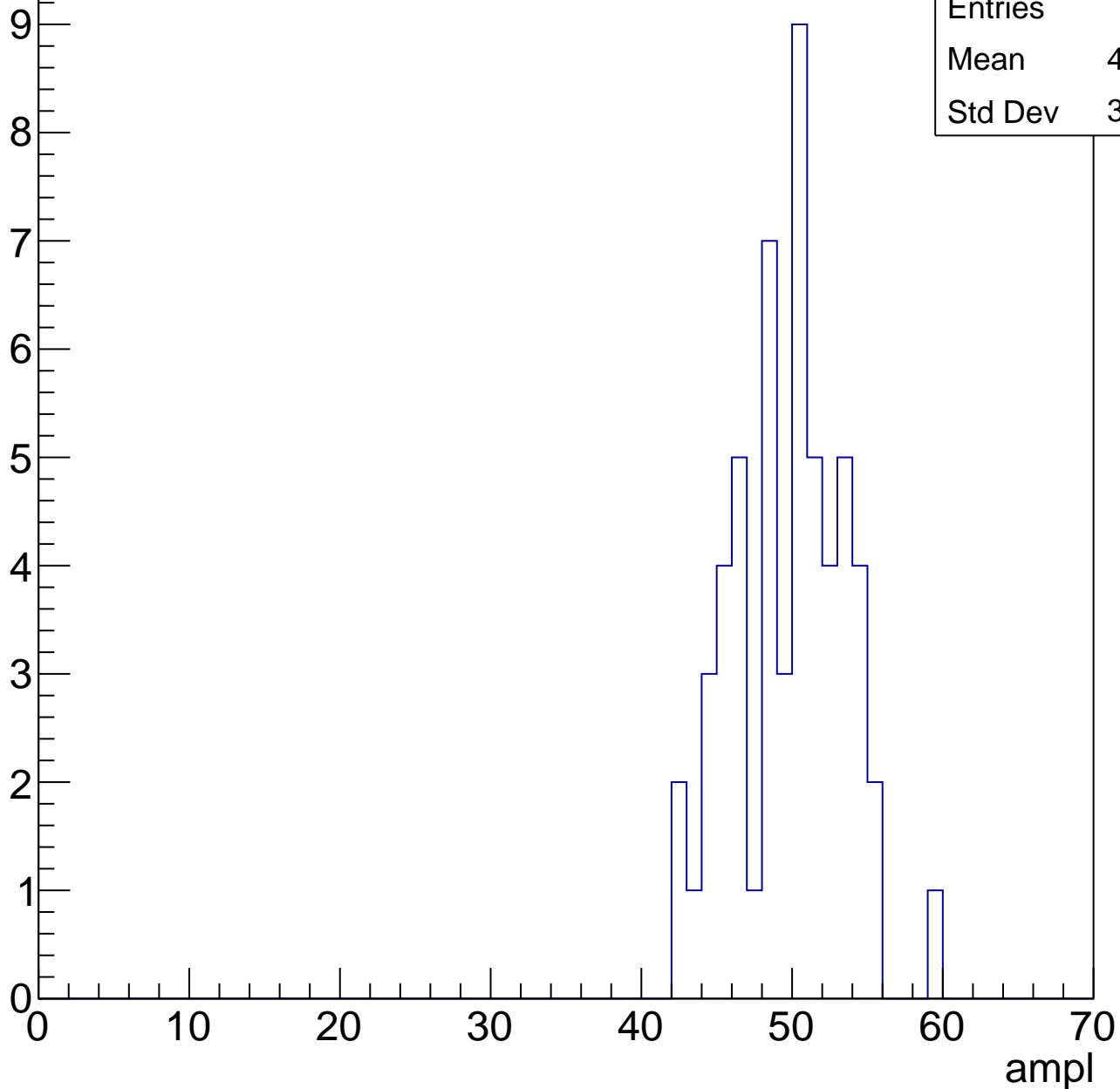
ampl



# B0L102S, U6-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



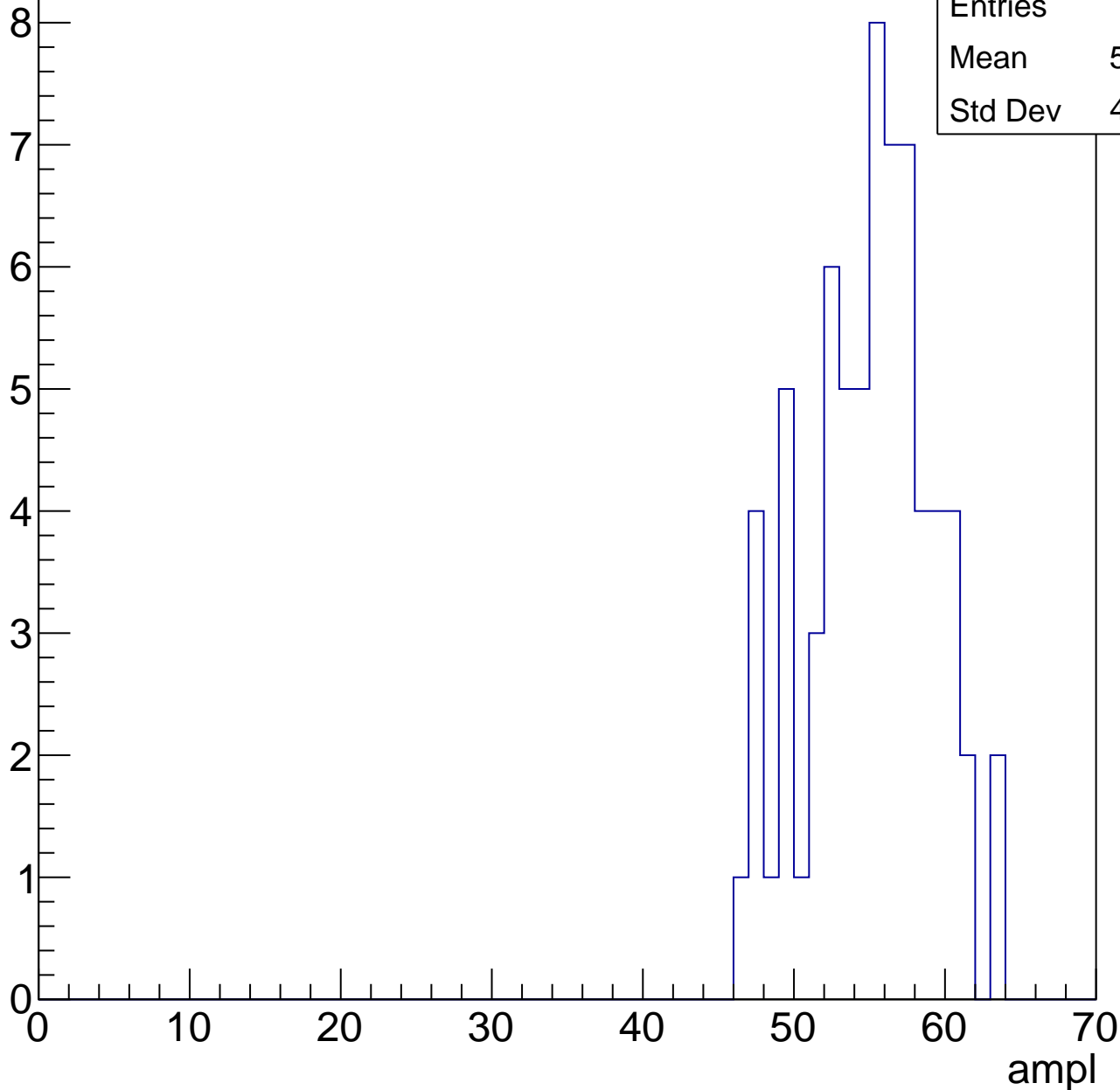
Entries	56
Mean	49.32
Std Dev	3.636

# B0L102S, U6-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	54.55
Std Dev	4.063

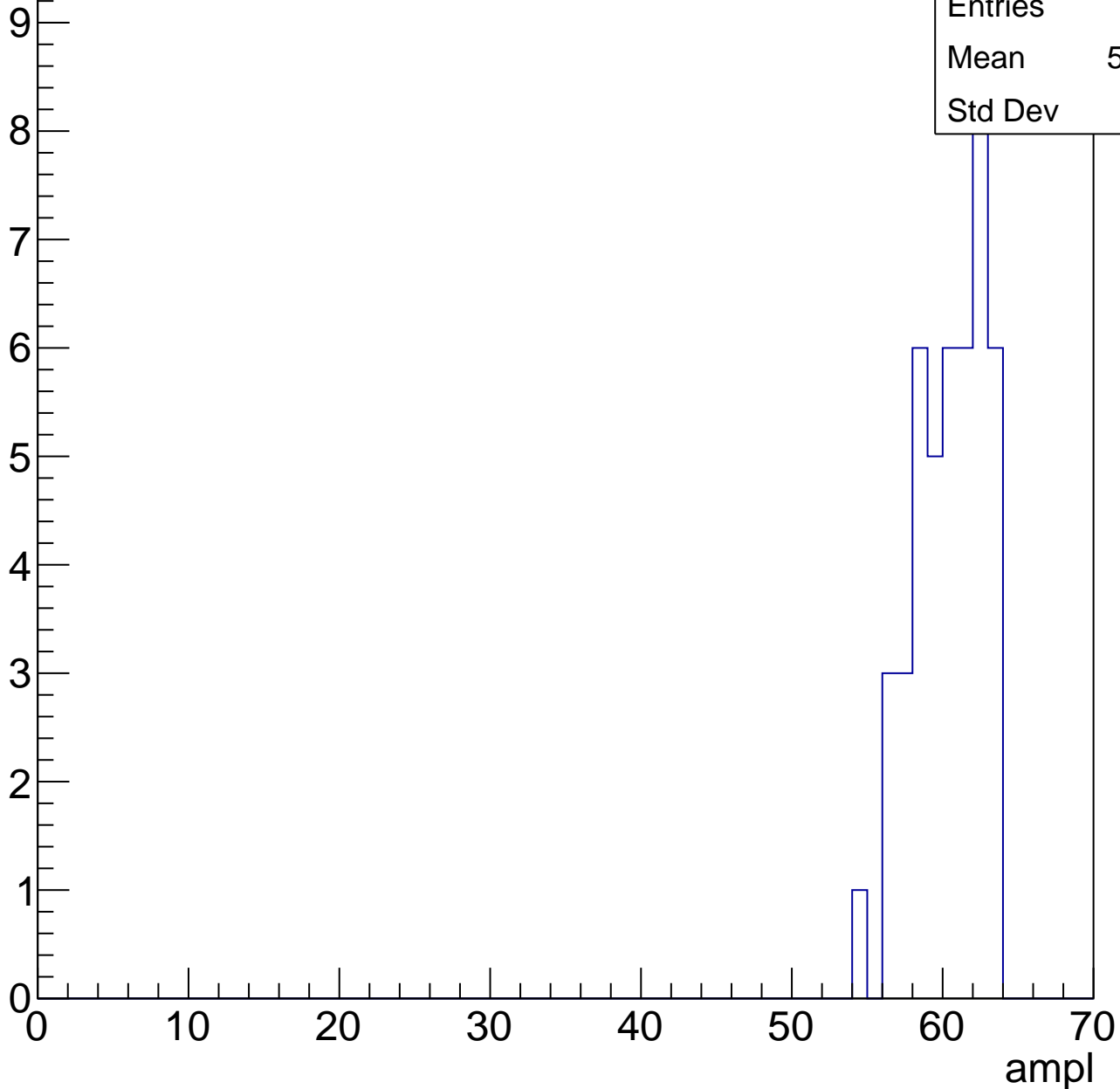


# B0L102S, U6-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	59.96
Std Dev	2.29



# B0L102S, U6-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

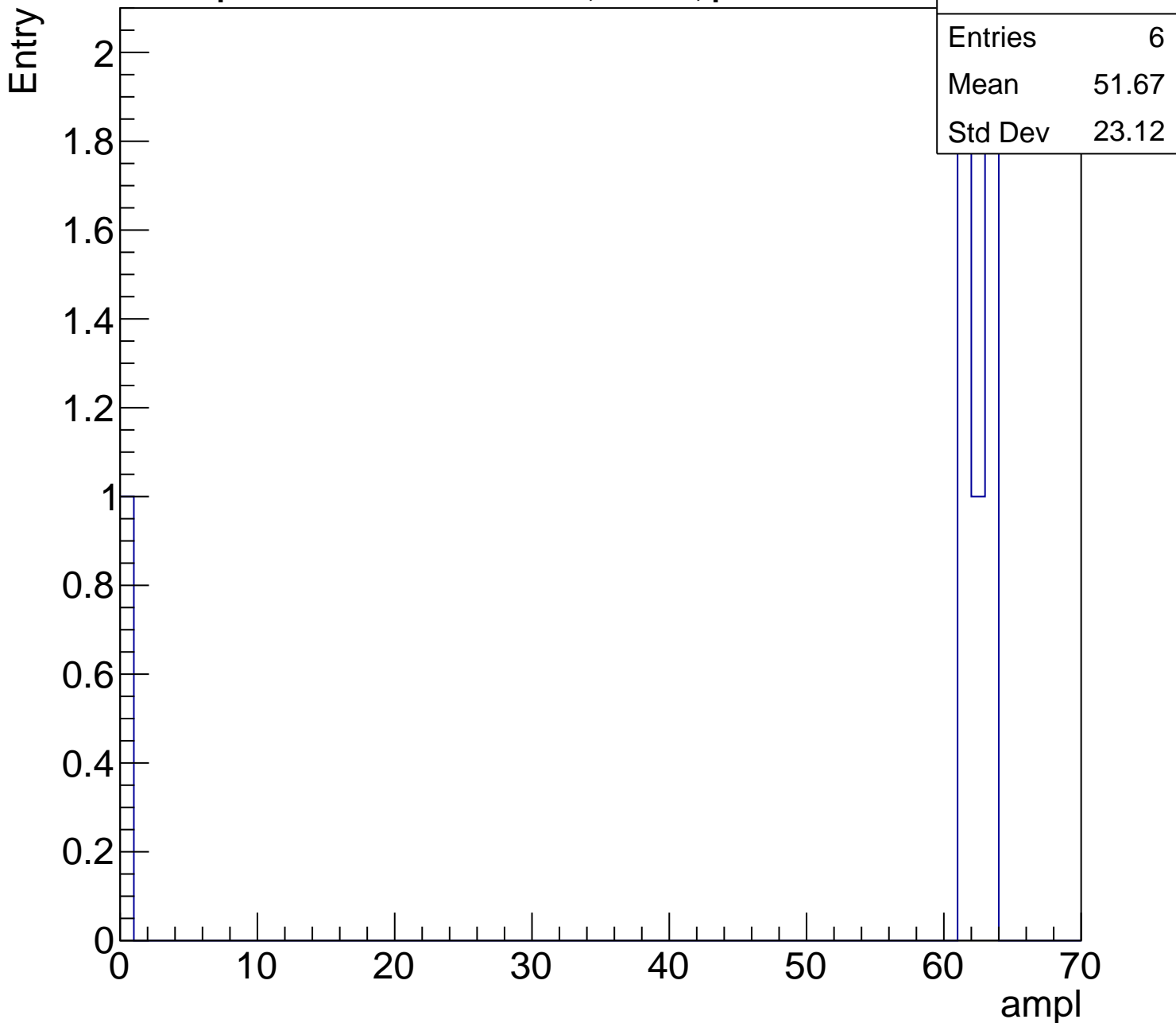
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.67
Std Dev	23.12

0 10 20 30 40 50 60 70

ampl





# B0L102S, U6-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch98, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	79
Mean	25.9
Std Dev	6.443

**Gaus mean : 27.6610**

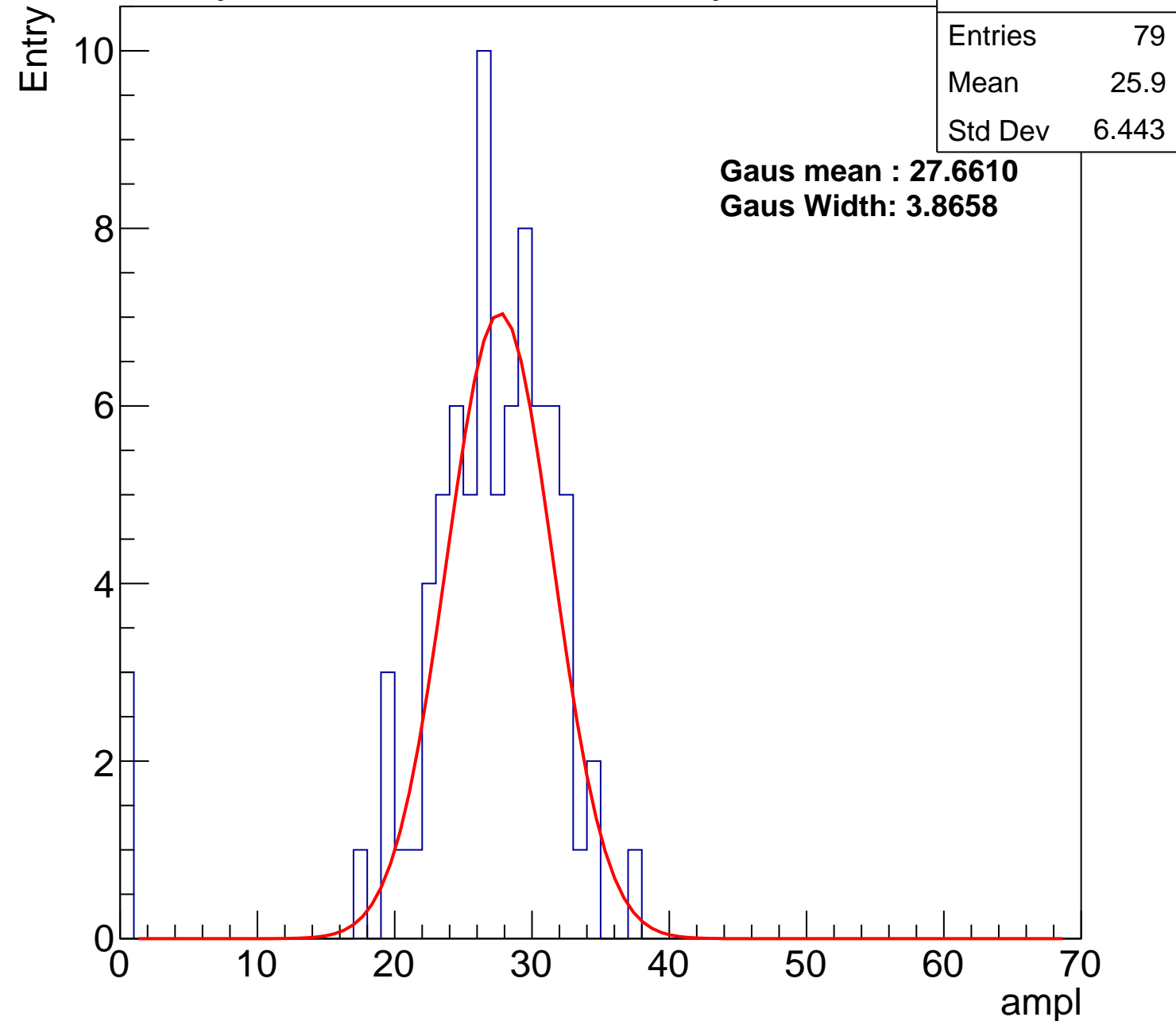
**Gaus Width: 3.8658**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch98, adc1

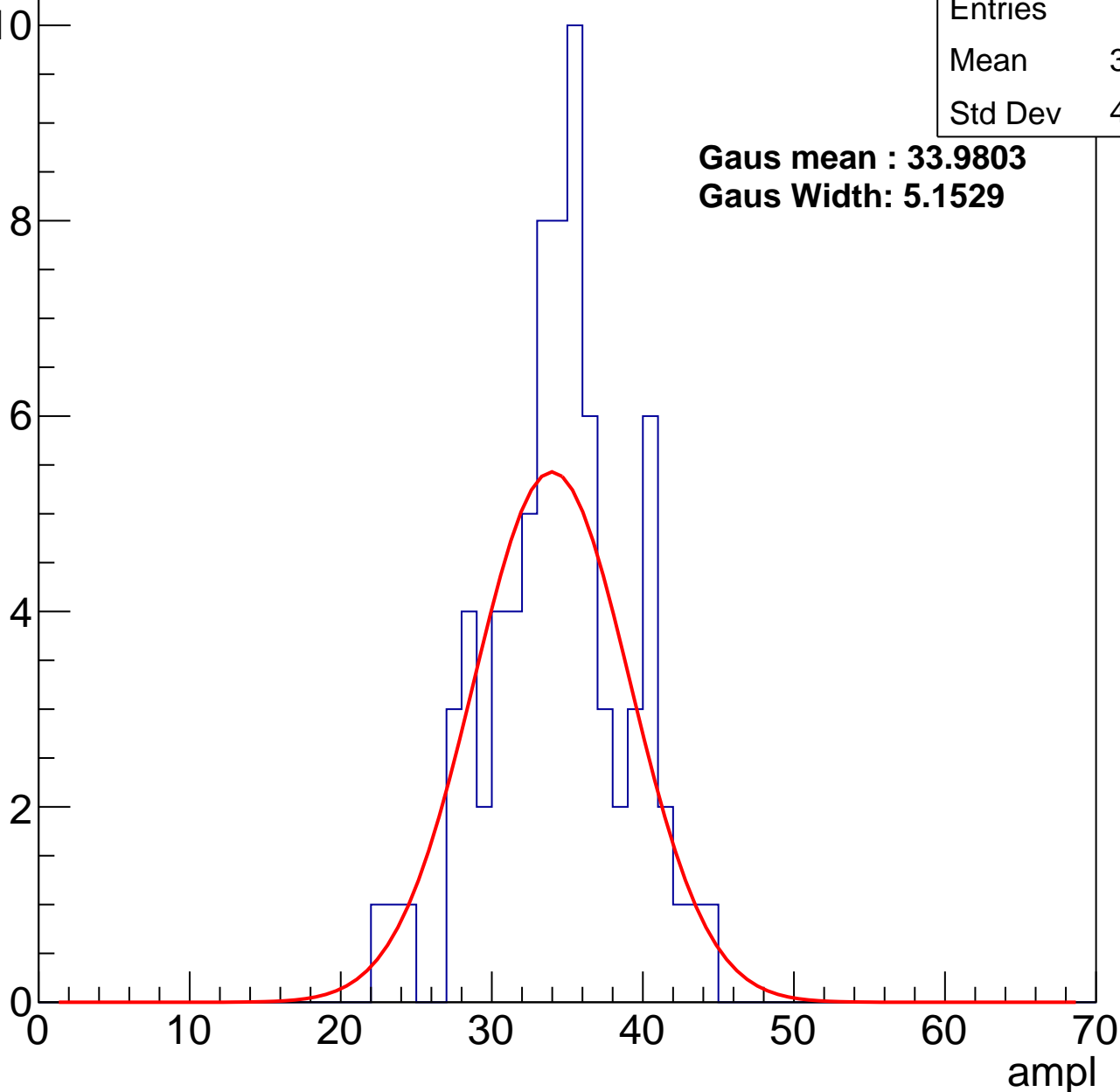
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	33.96
Std Dev	4.532

**Gaus mean : 33.9803**

**Gaus Width: 5.1529**



# B0L102S, U6-ch98, adc2

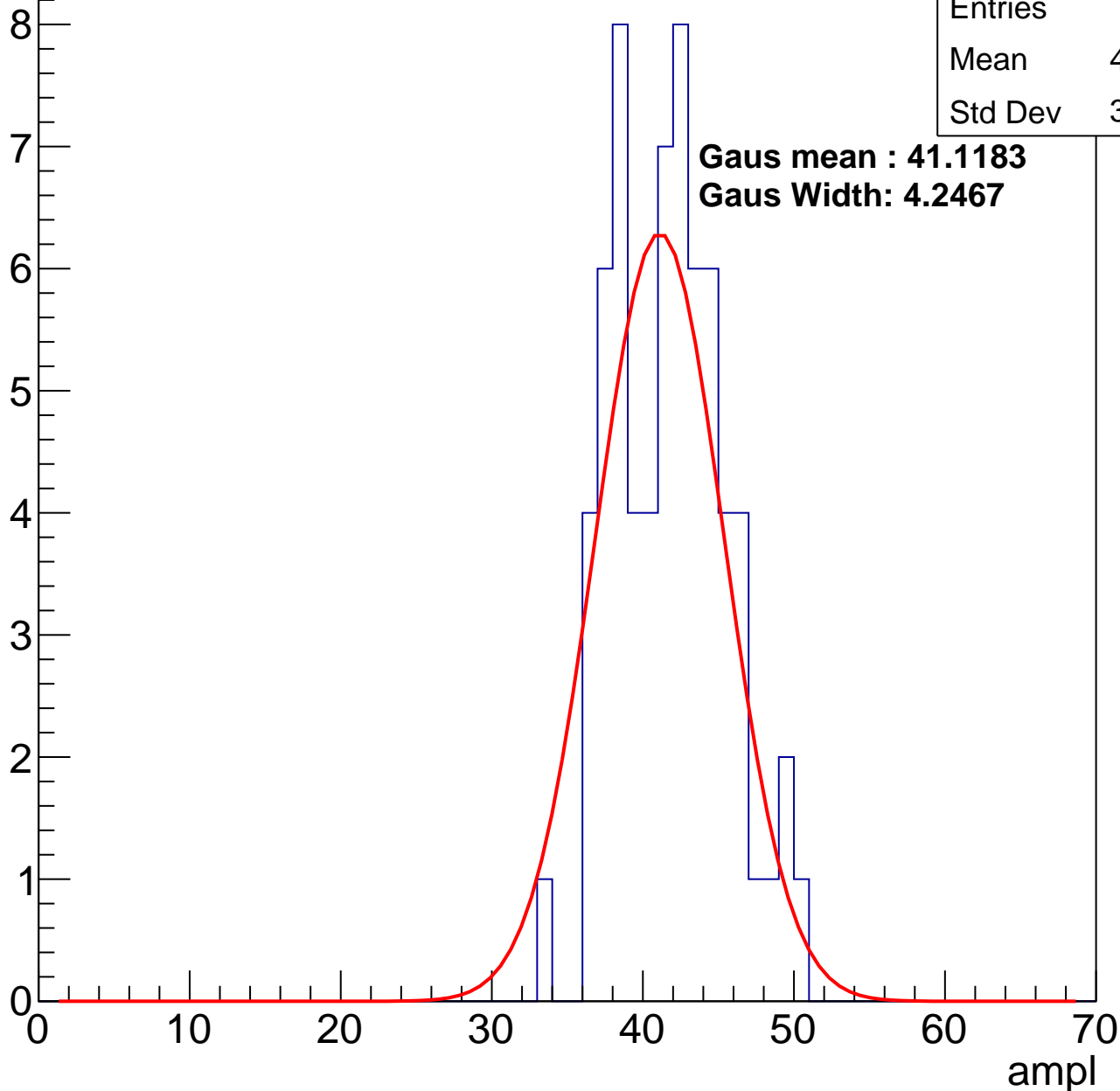
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	41.36
Std Dev	3.639

**Gaus mean : 41.1183**

**Gaus Width: 4.2467**

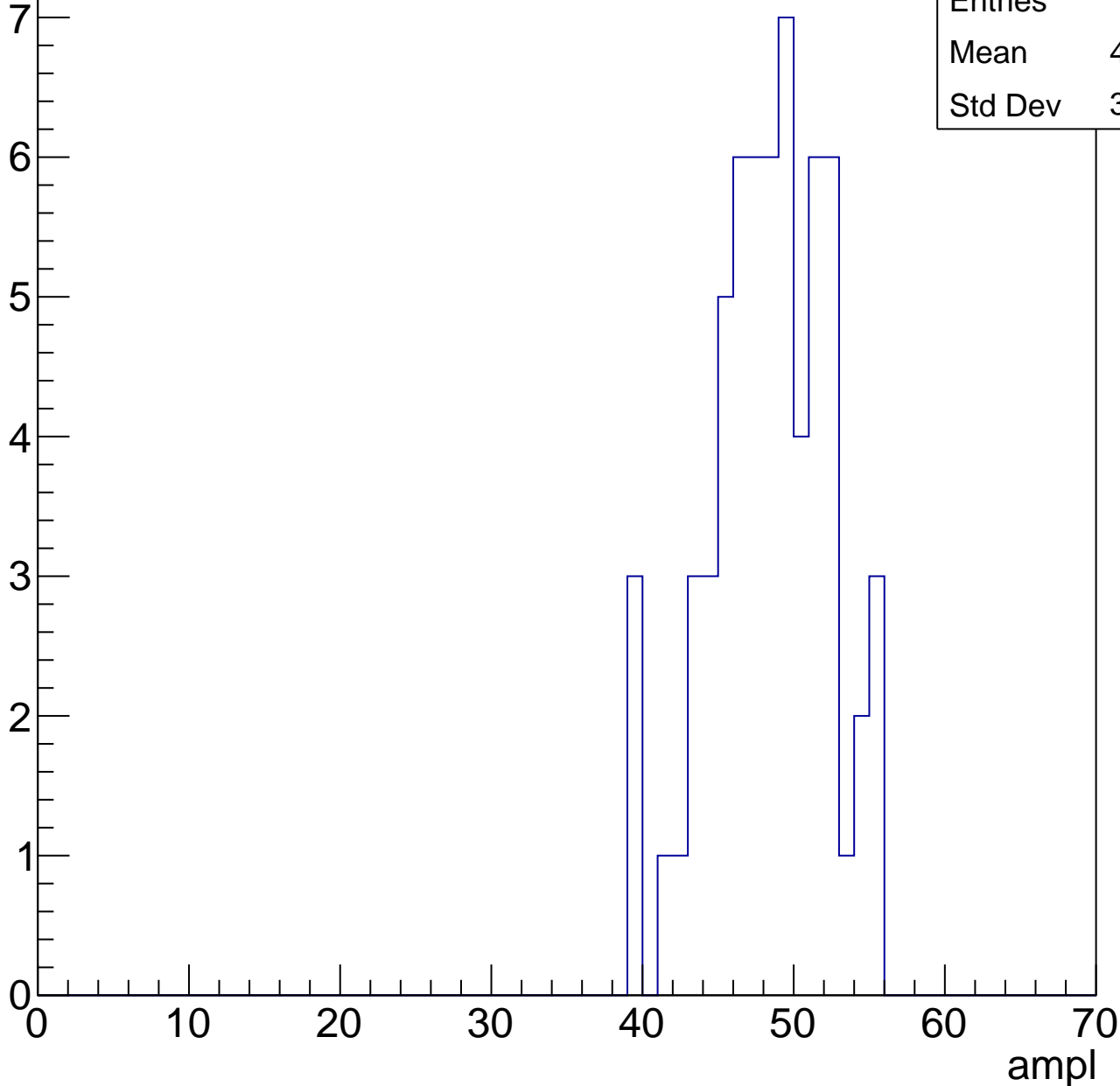


# B0L102S, U6-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	47.92
Std Dev	3.868

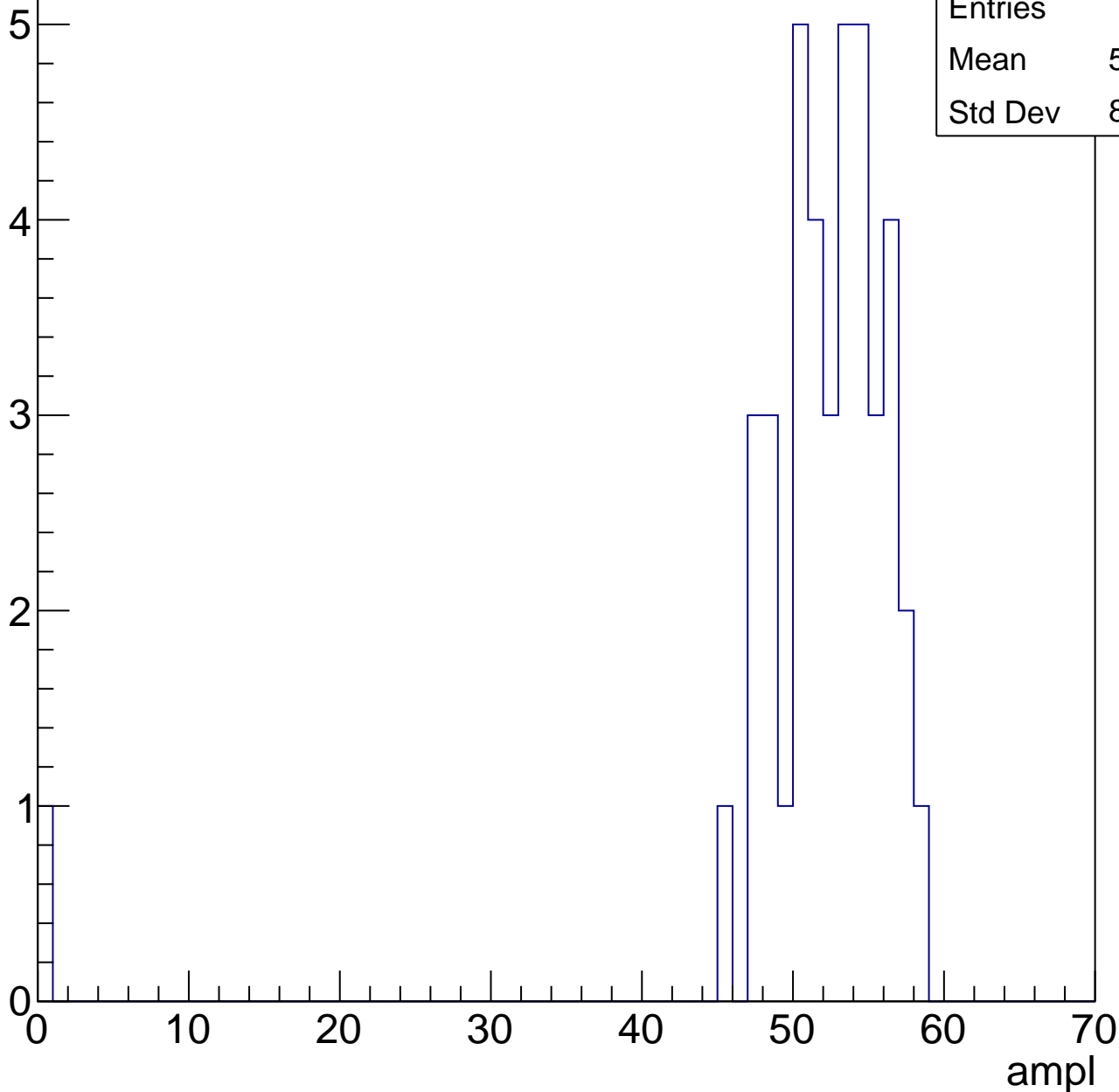


# B0L102S, U6-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

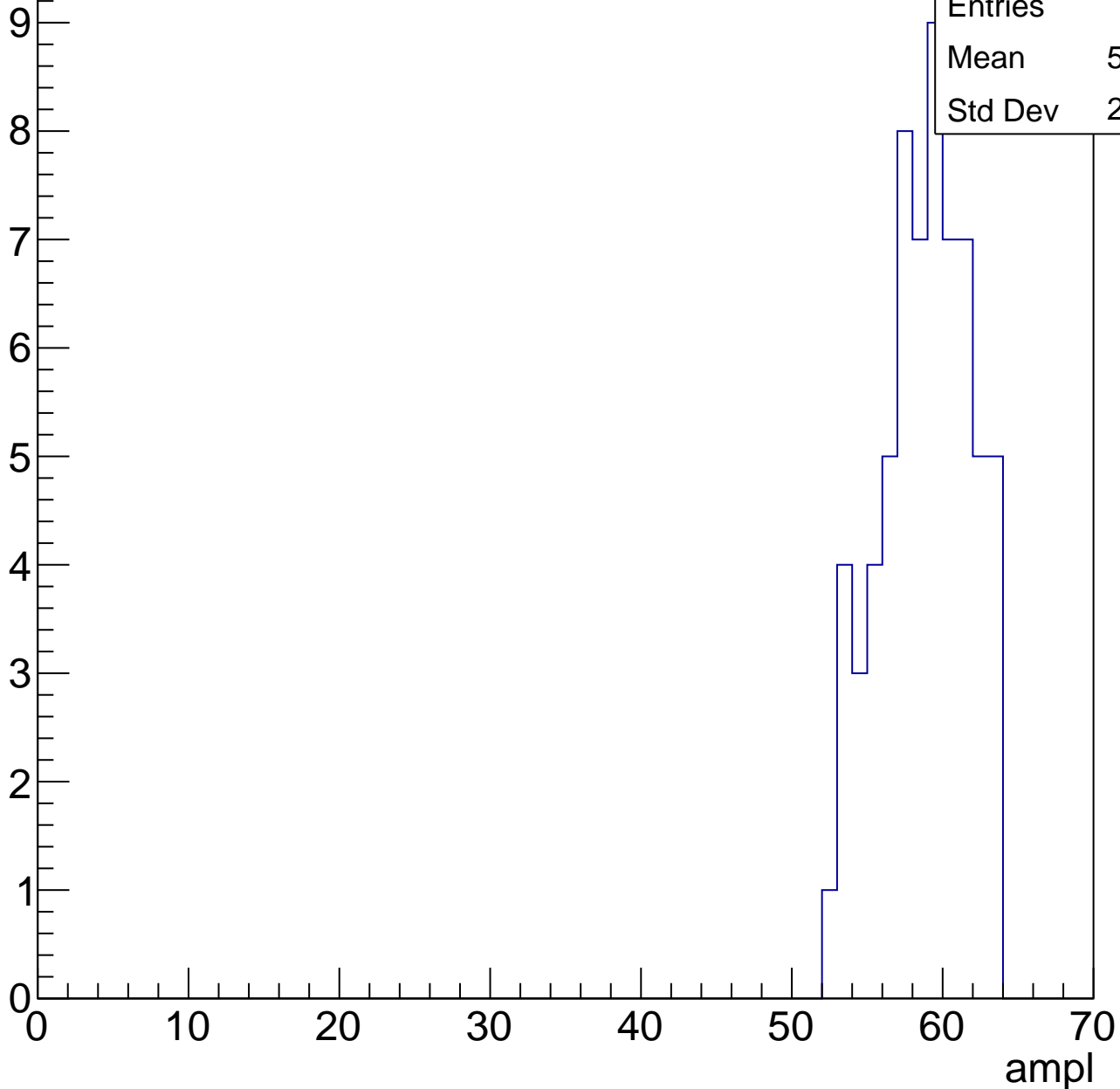
Entries	41
Mean	50.85
Std Dev	8.635



# B0L102S, U6-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

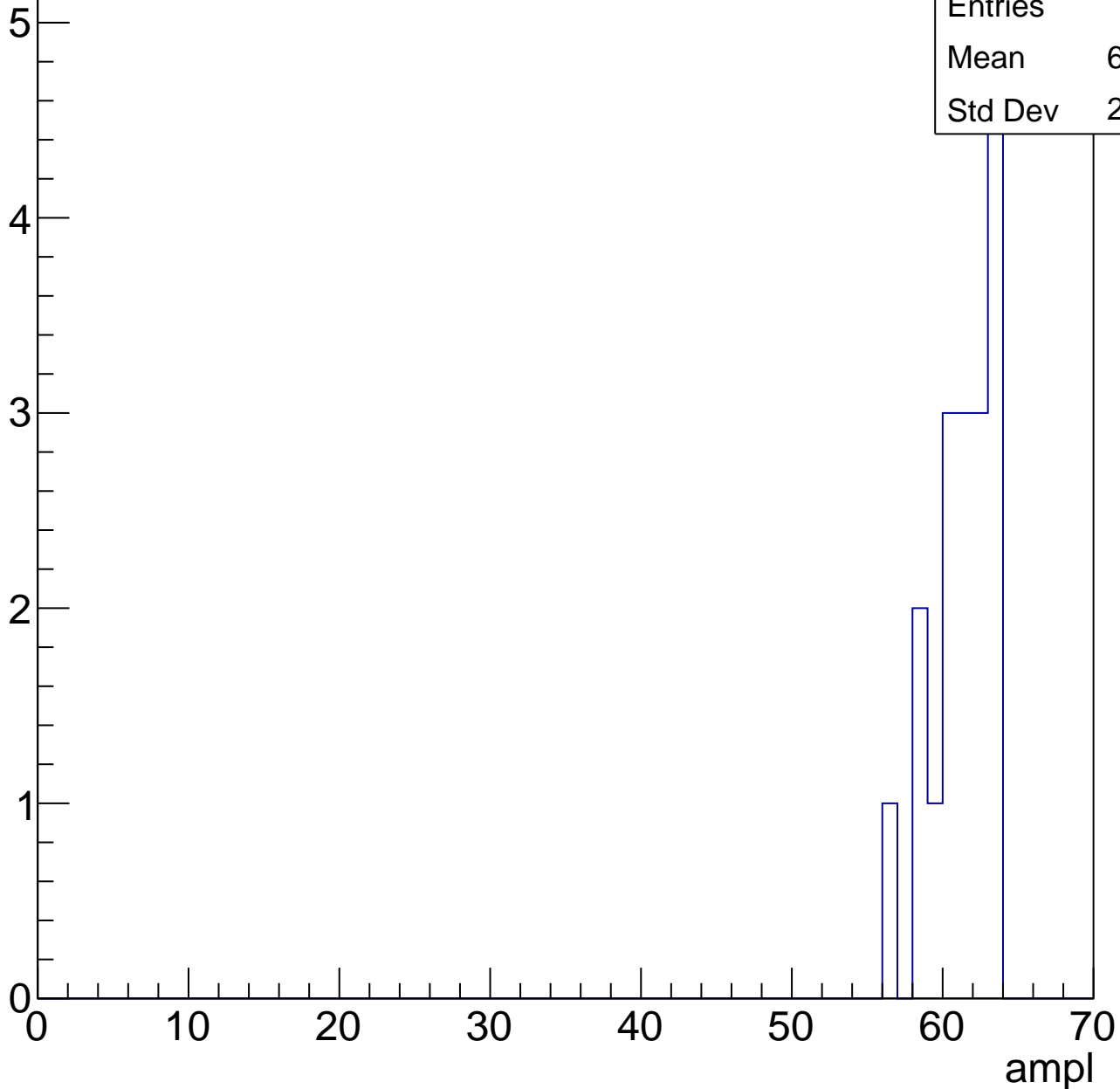


# B0L102S, U6-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	18
Mean	60.83
Std Dev	2.007





# B0L102S, U6-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



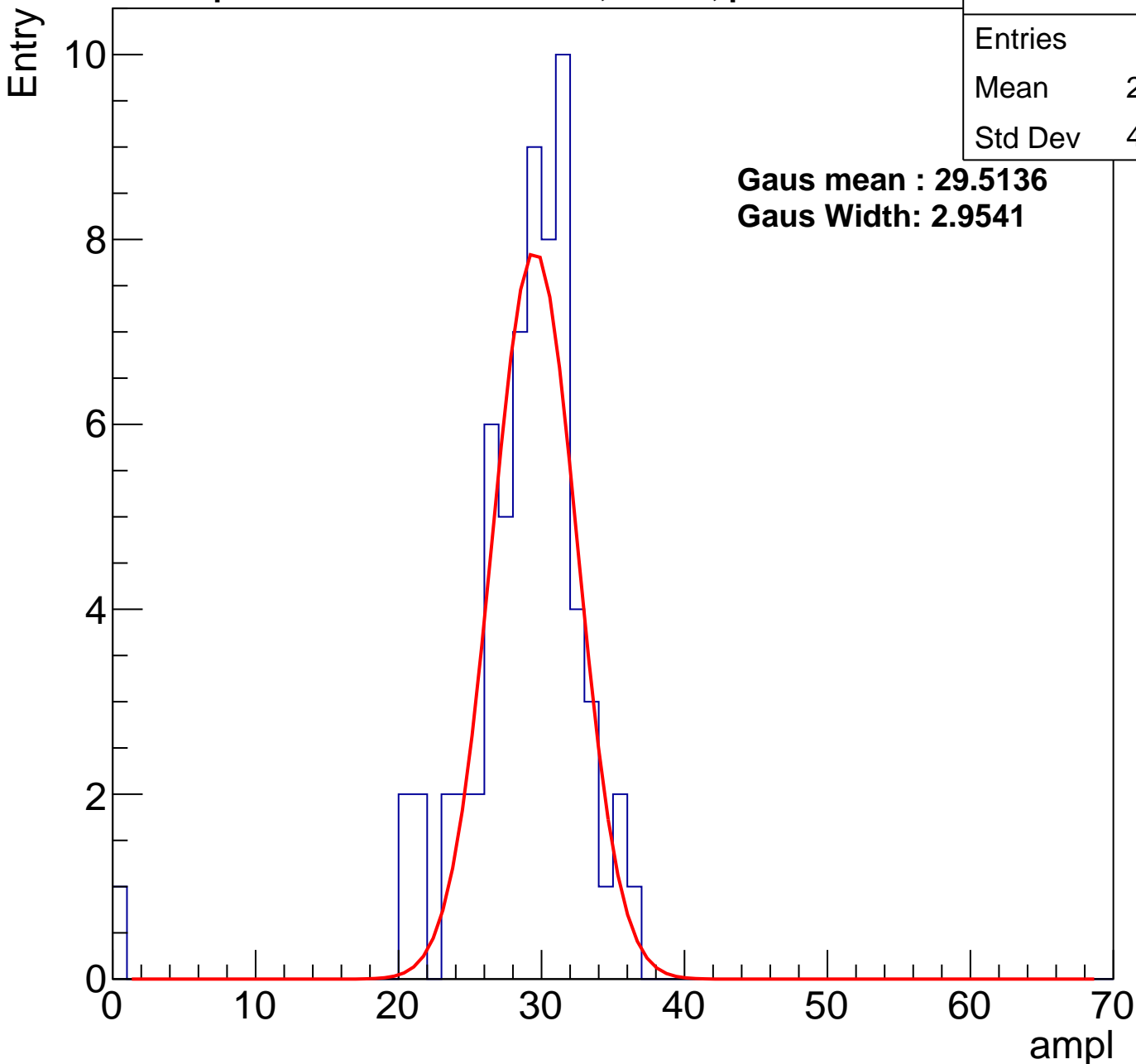
# B0L102S, U6-ch99, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	67
Mean	28.22
Std Dev	4.892

**Gaus mean : 29.5136**

**Gaus Width: 2.9541**



# B0L102S, U6-ch99, adc1

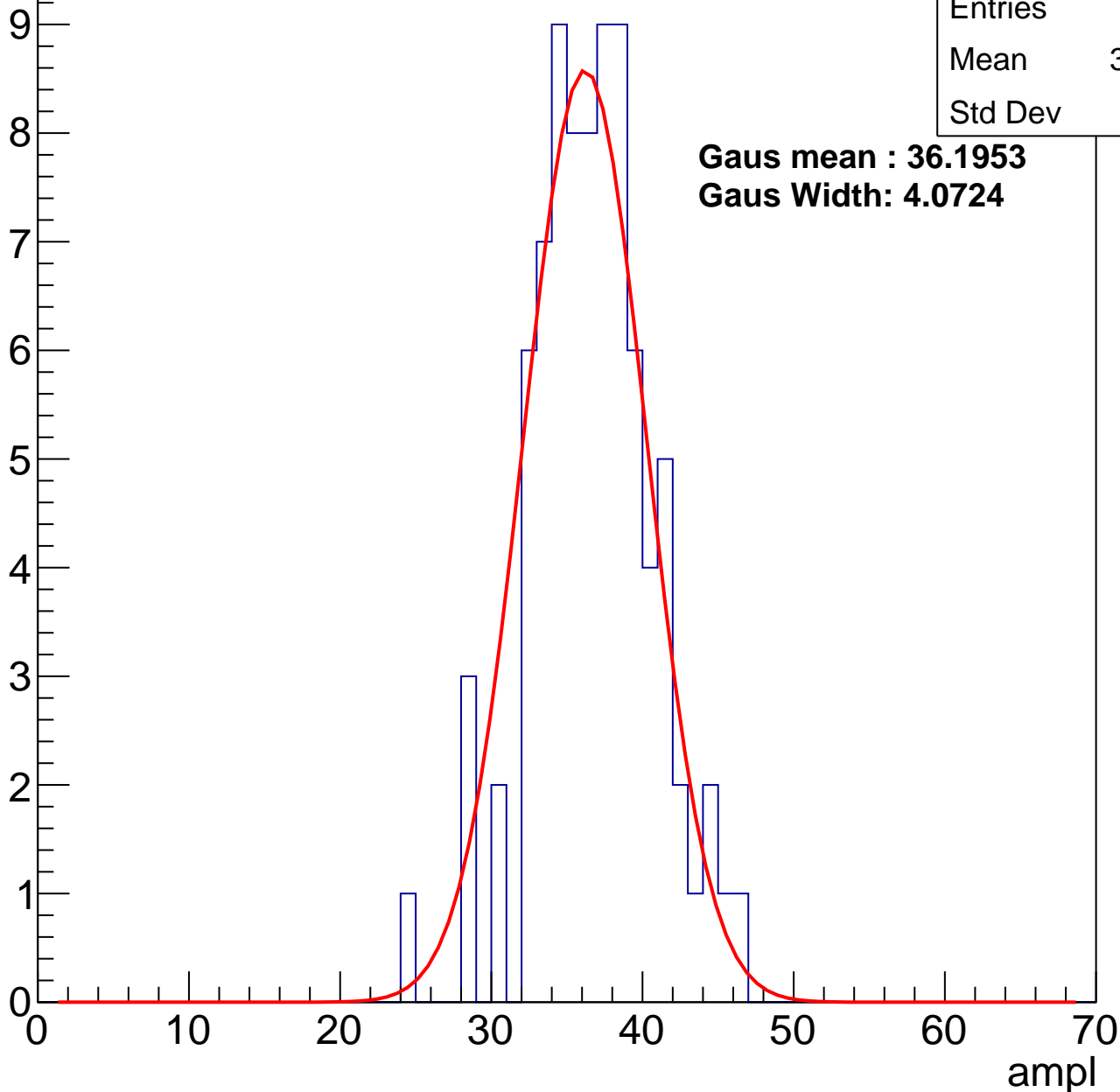
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	36.25
Std Dev	3.97

**Gaus mean : 36.1953**

**Gaus Width: 4.0724**



# B0L102S, U6-ch99, adc2

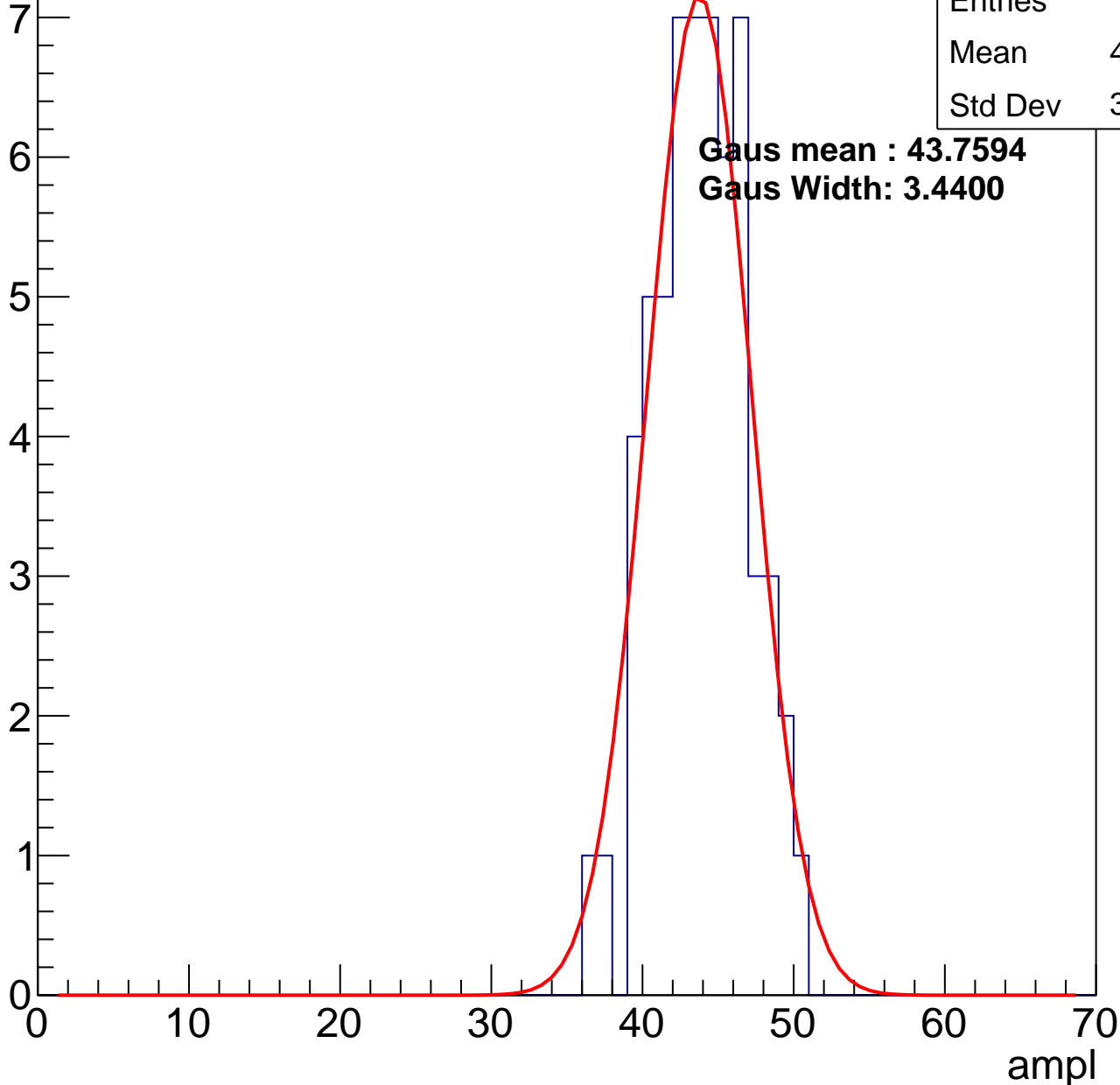
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	43.42
Std Dev	3.049

**Gaus mean : 43.7594**

**Gaus Width: 3.4400**

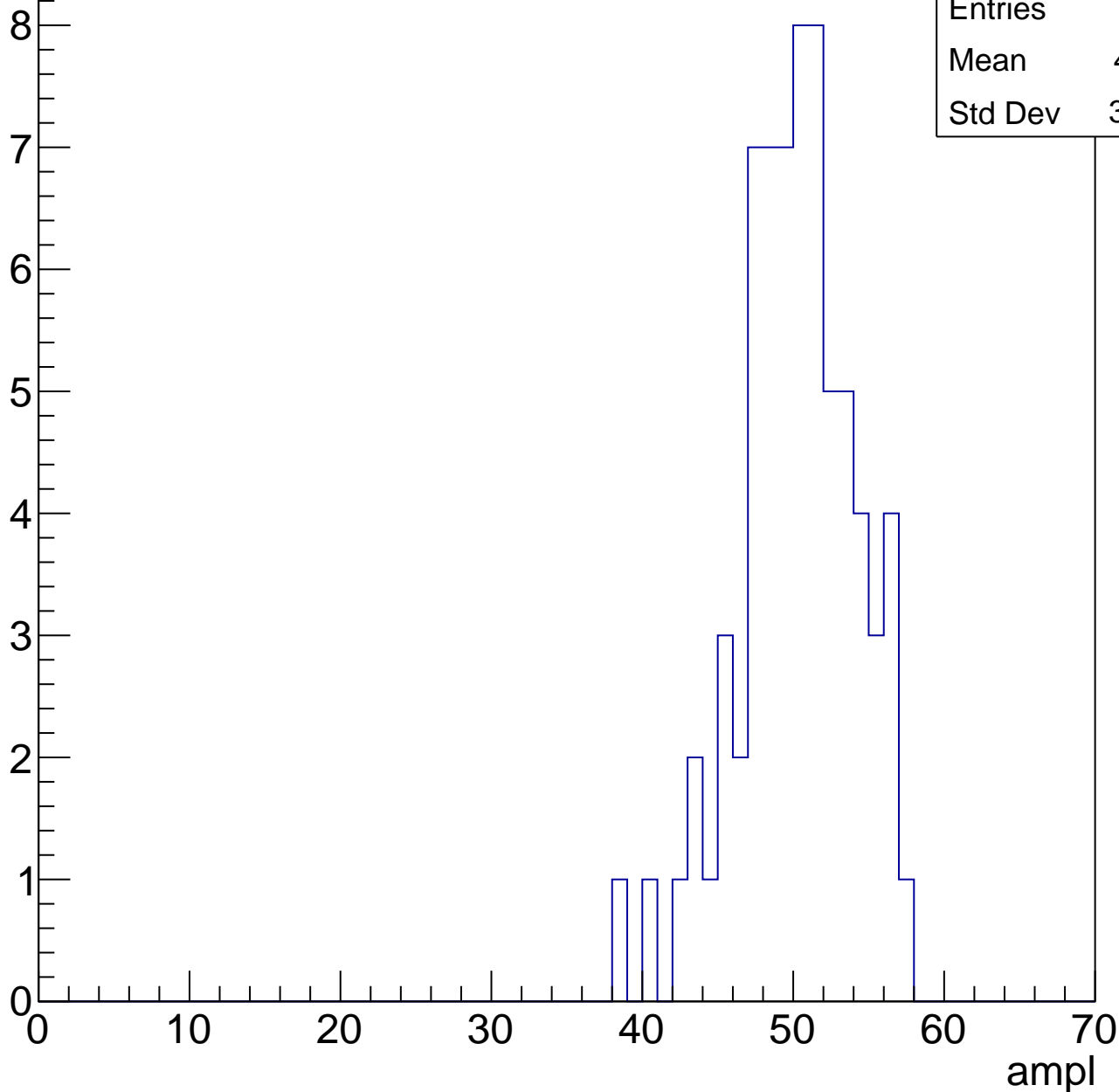


# B0L102S, U6-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	49.71
Std Dev	3.866



# B0L102S, U6-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

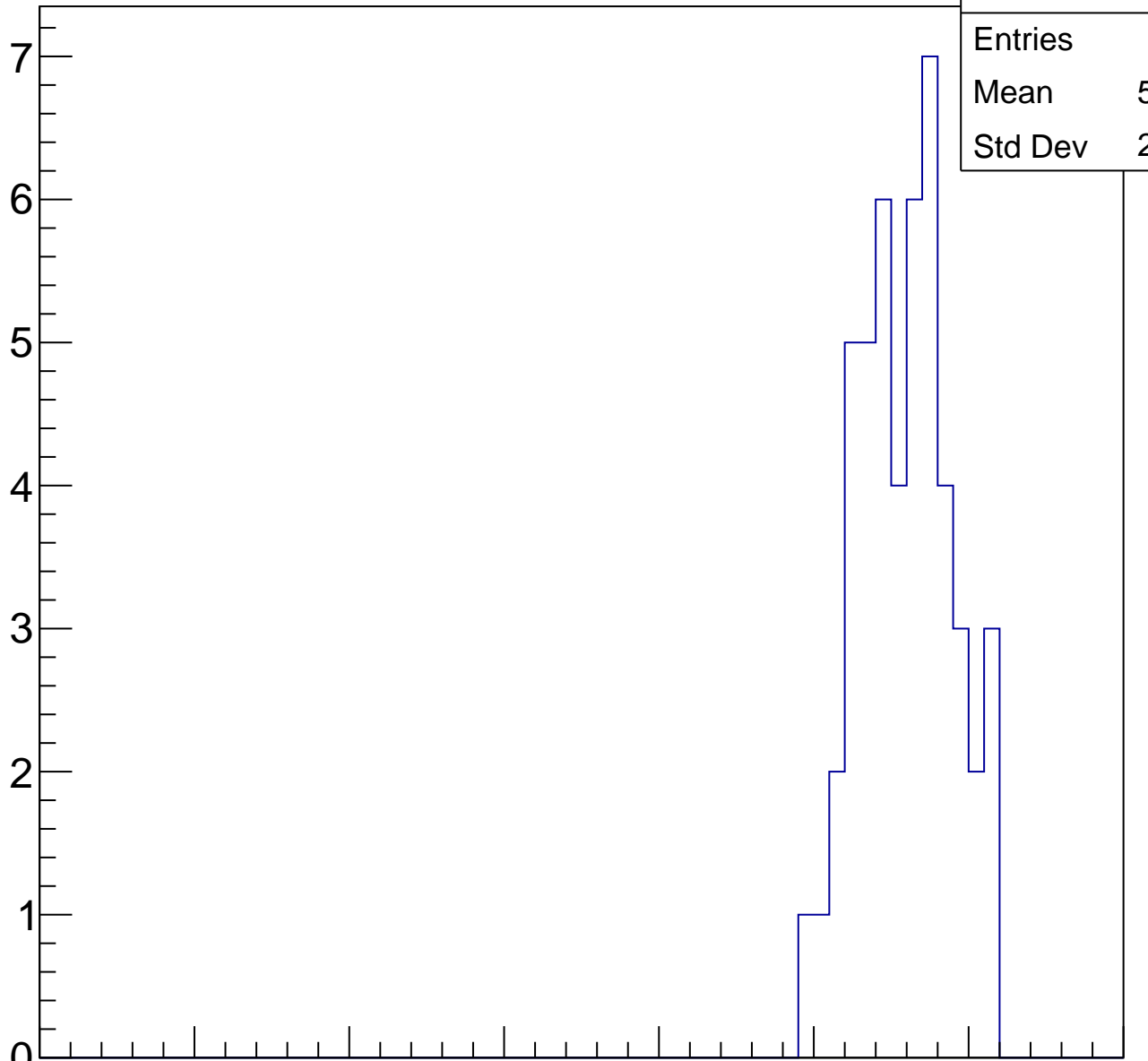
Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	55.45
Std Dev	2.963

ampl

0 10 20 30 40 50 60 70

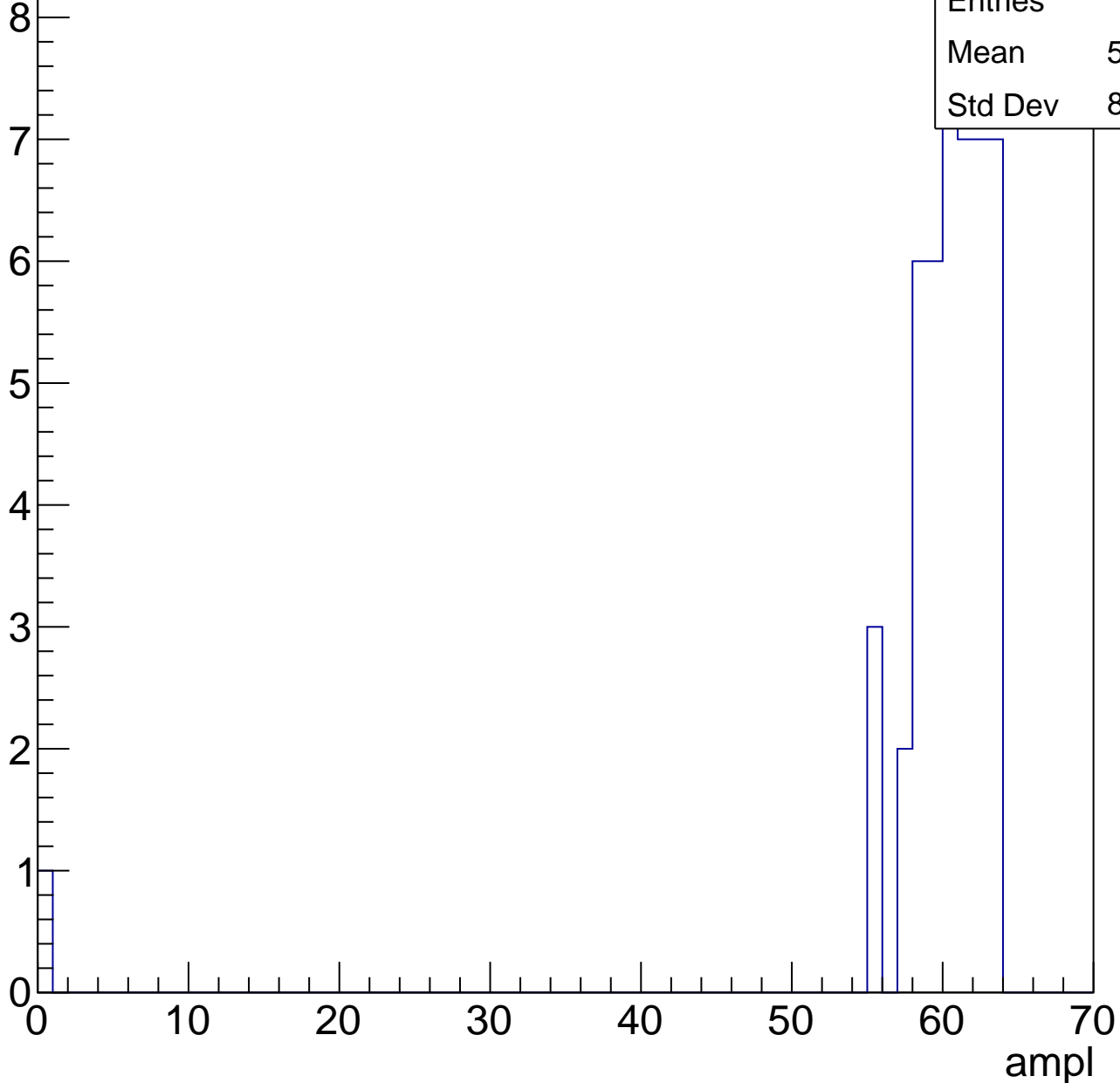


# B0L102S, U6-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

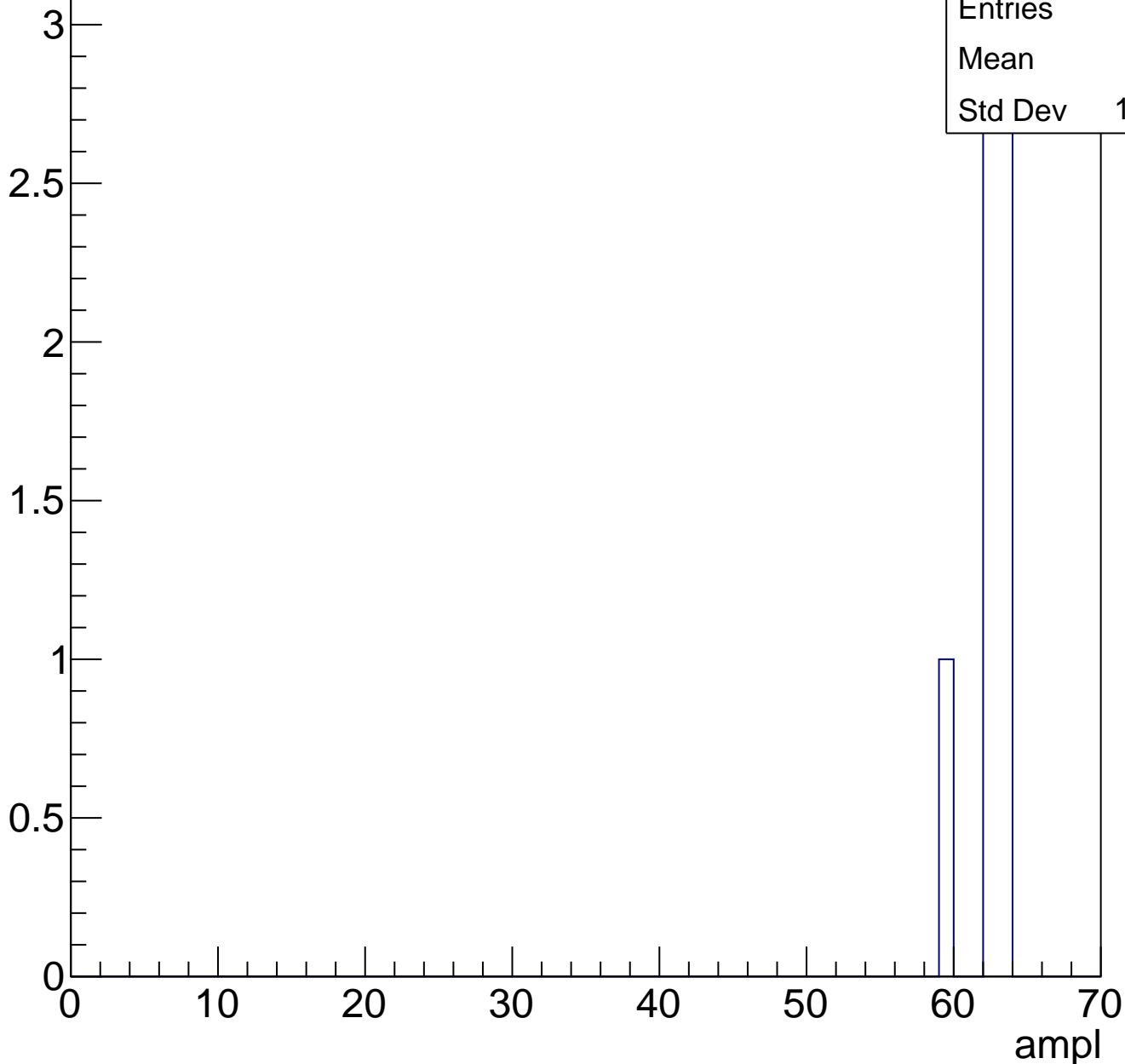
Entries	47
Mean	58.79
Std Dev	8.935



# B0L102S, U6-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch100, adc0

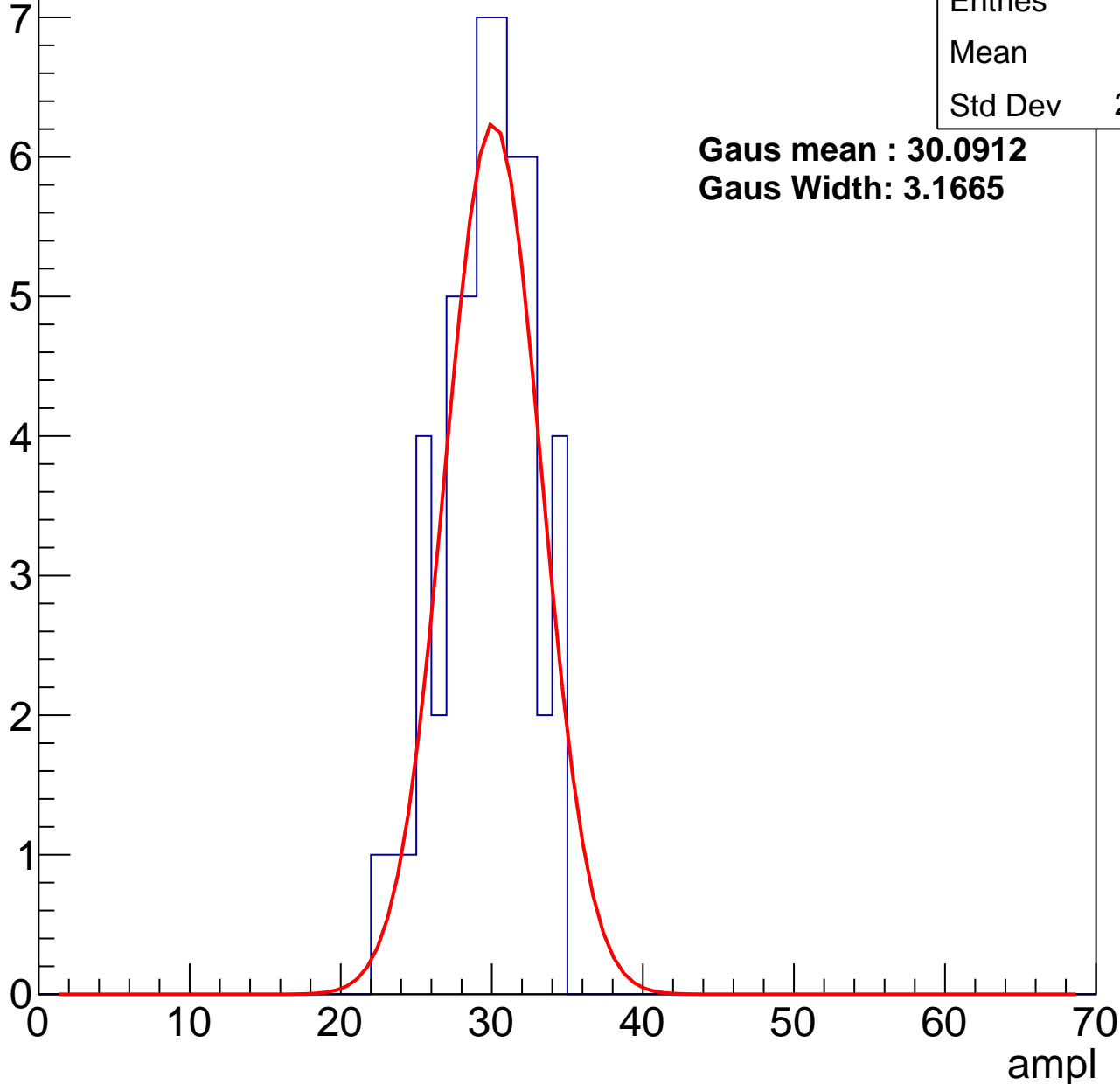
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	29.2
Std Dev	2.911

**Gaus mean : 30.0912**

**Gaus Width: 3.1665**



# B0L102S, U6-ch100, adc1

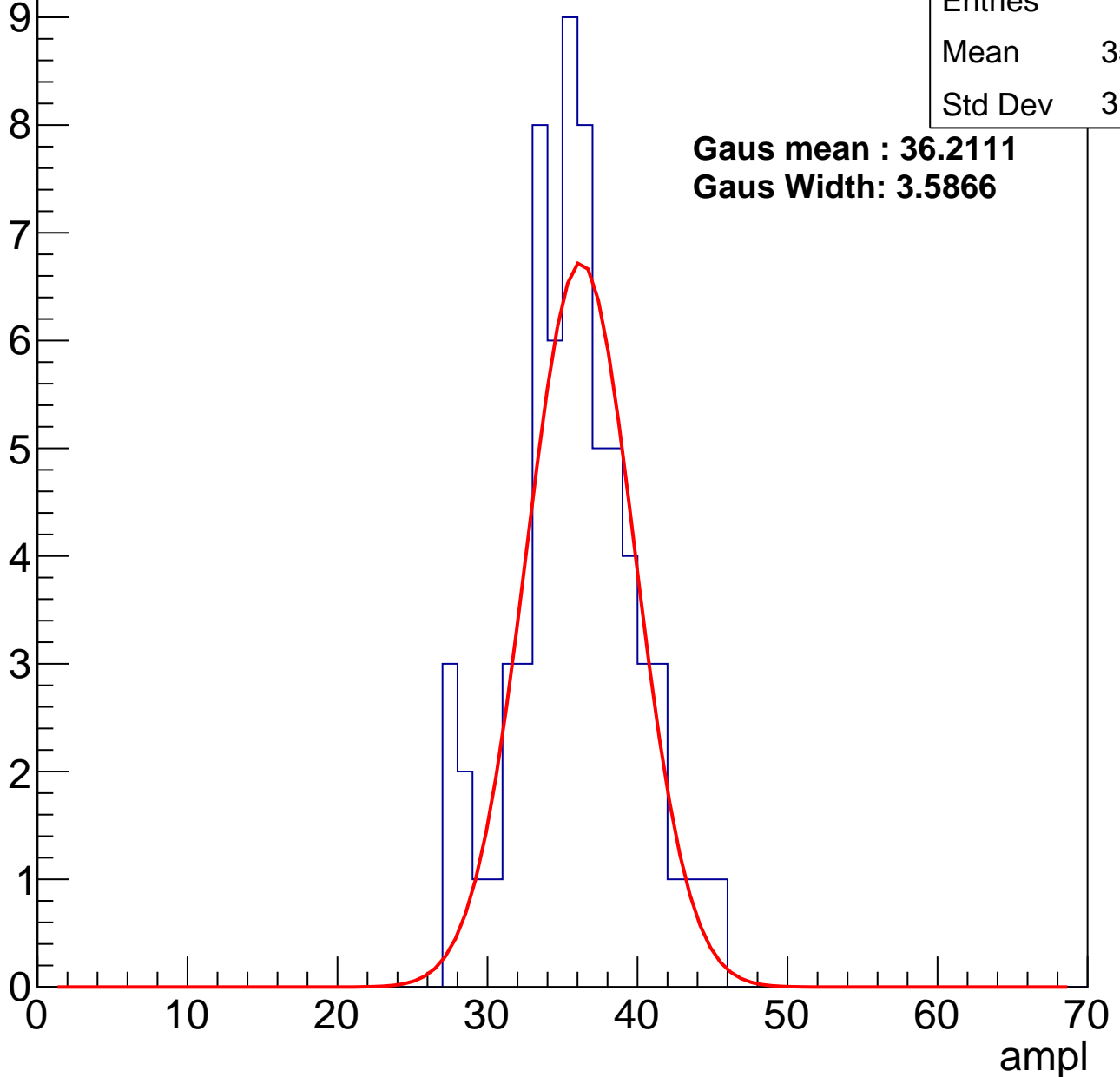
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	35.35
Std Dev	3.966

**Gaus mean : 36.2111**

**Gaus Width: 3.5866**



# B0L102S, U6-ch100, adc2

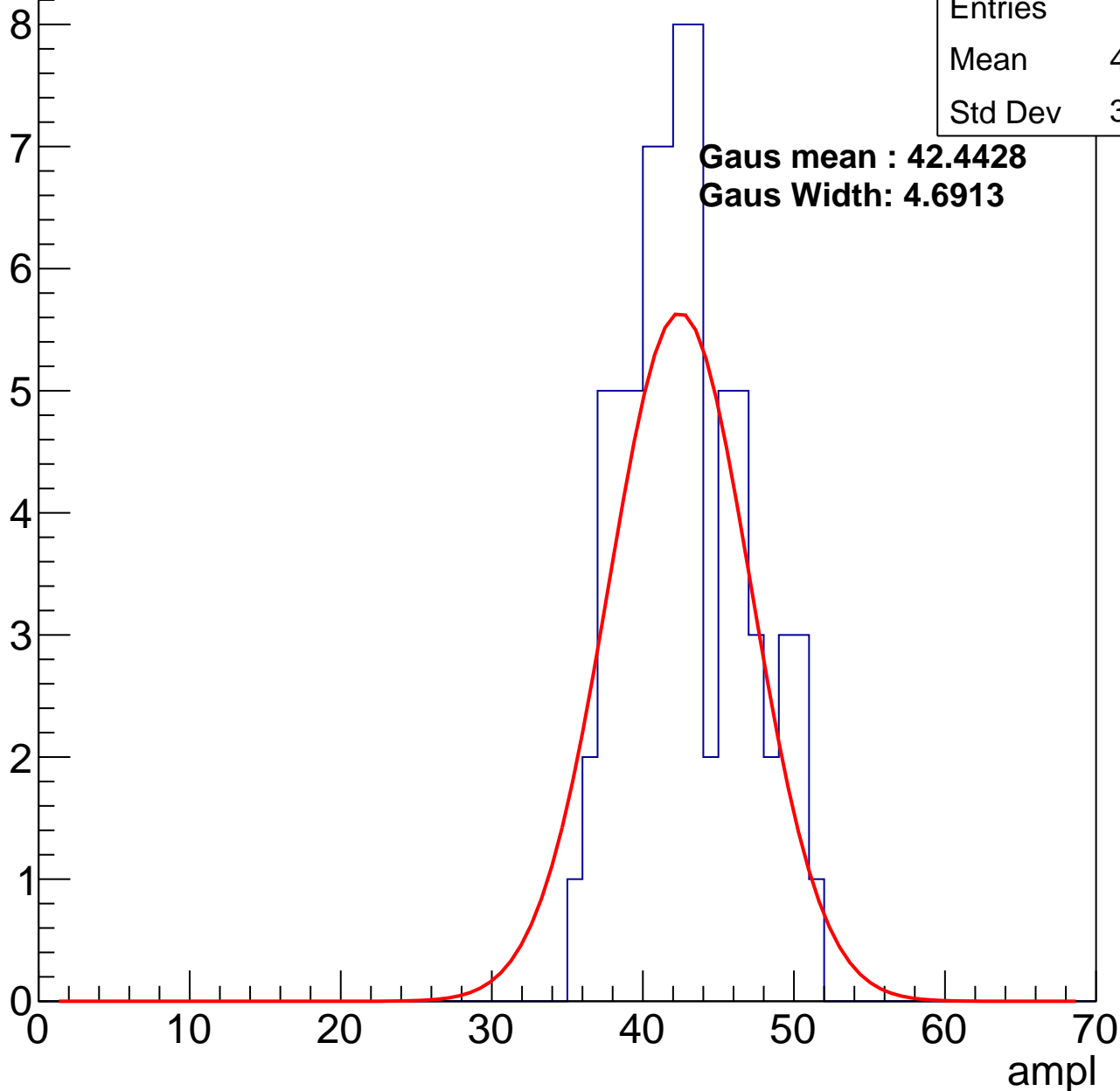
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	42.39
Std Dev	3.914

**Gaus mean : 42.4428**

**Gaus Width: 4.6913**

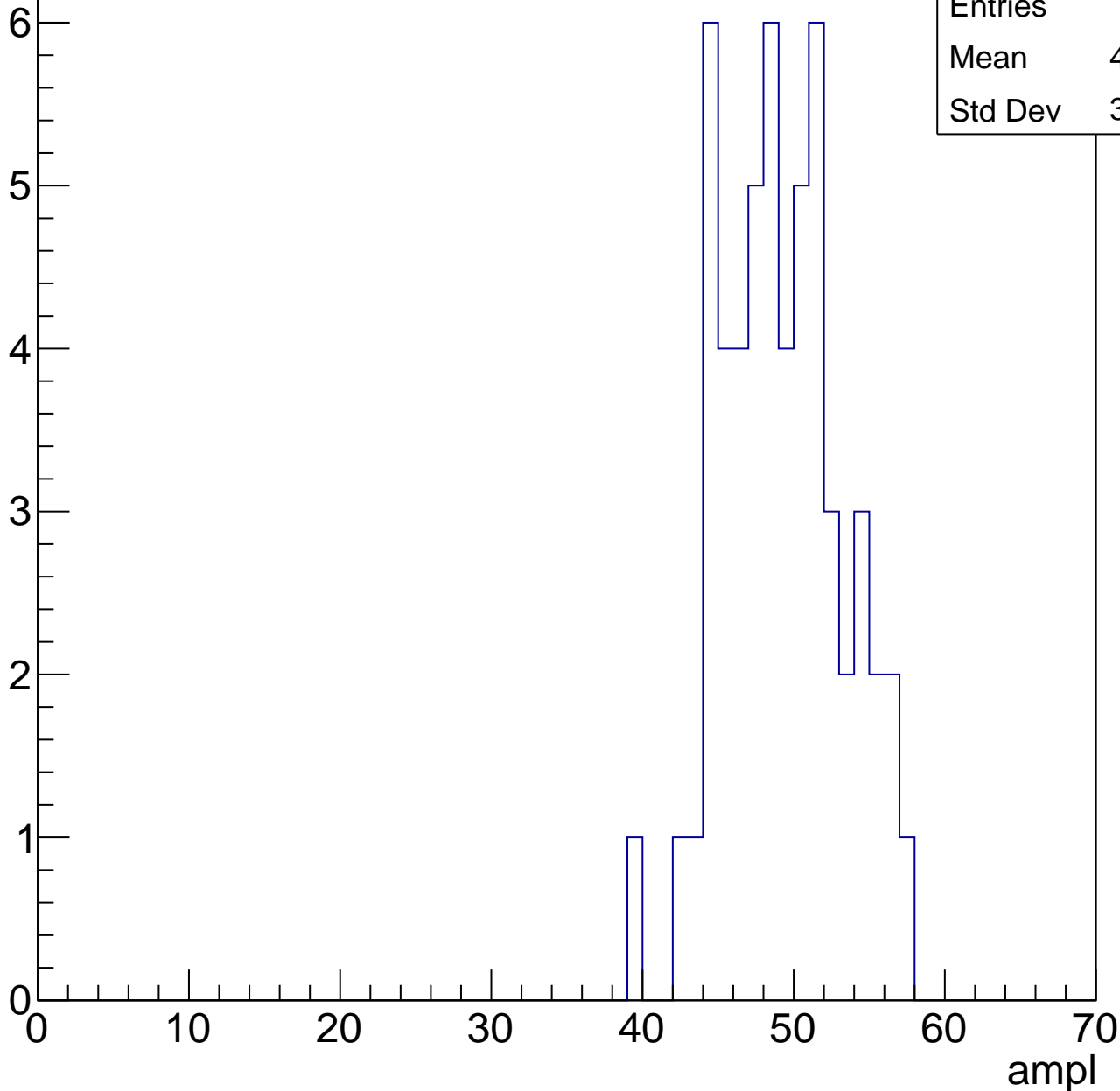


# B0L102S, U6-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	48.75
Std Dev	3.906

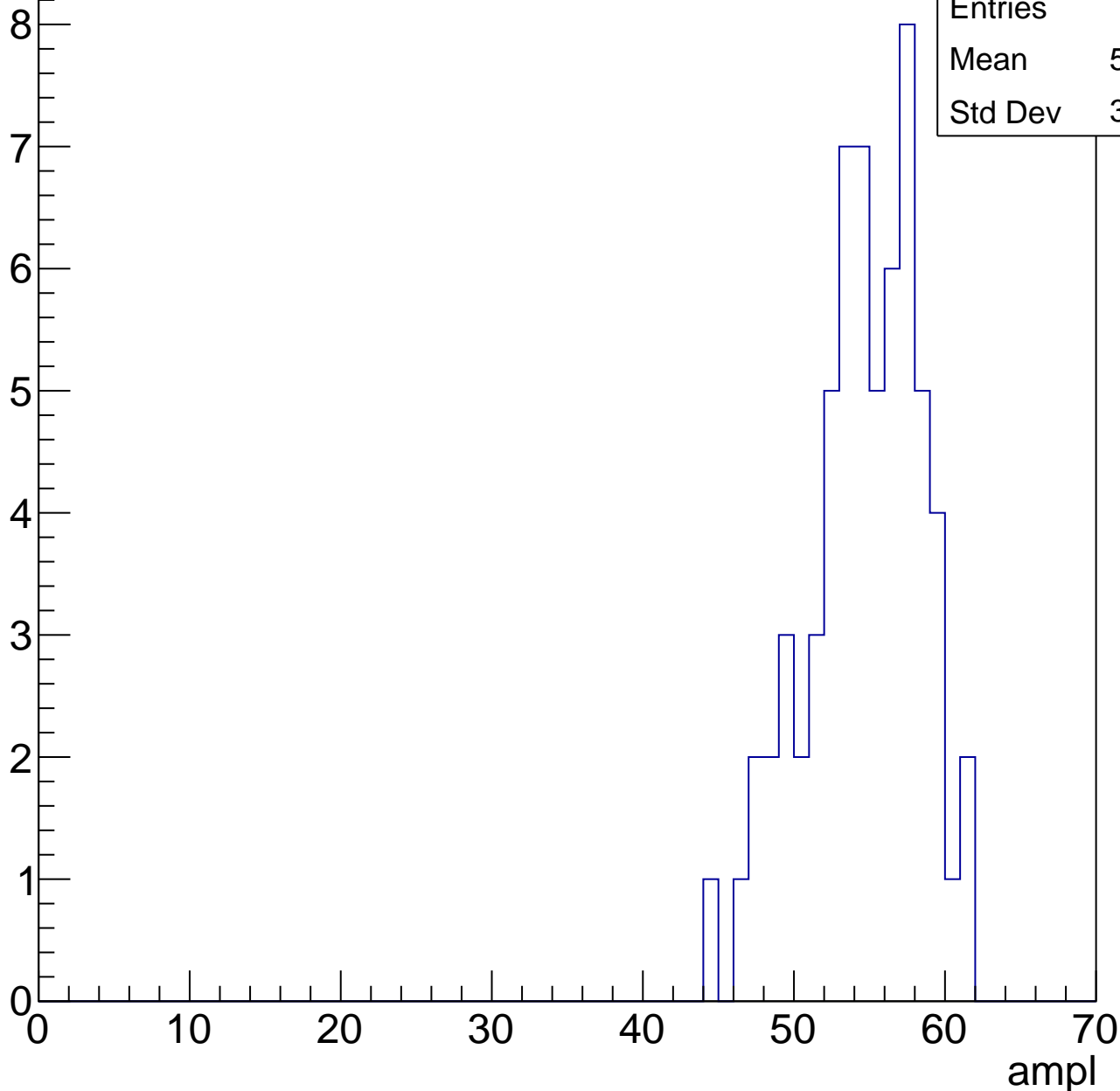


# B0L102S, U6-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	54.12
Std Dev	3.769

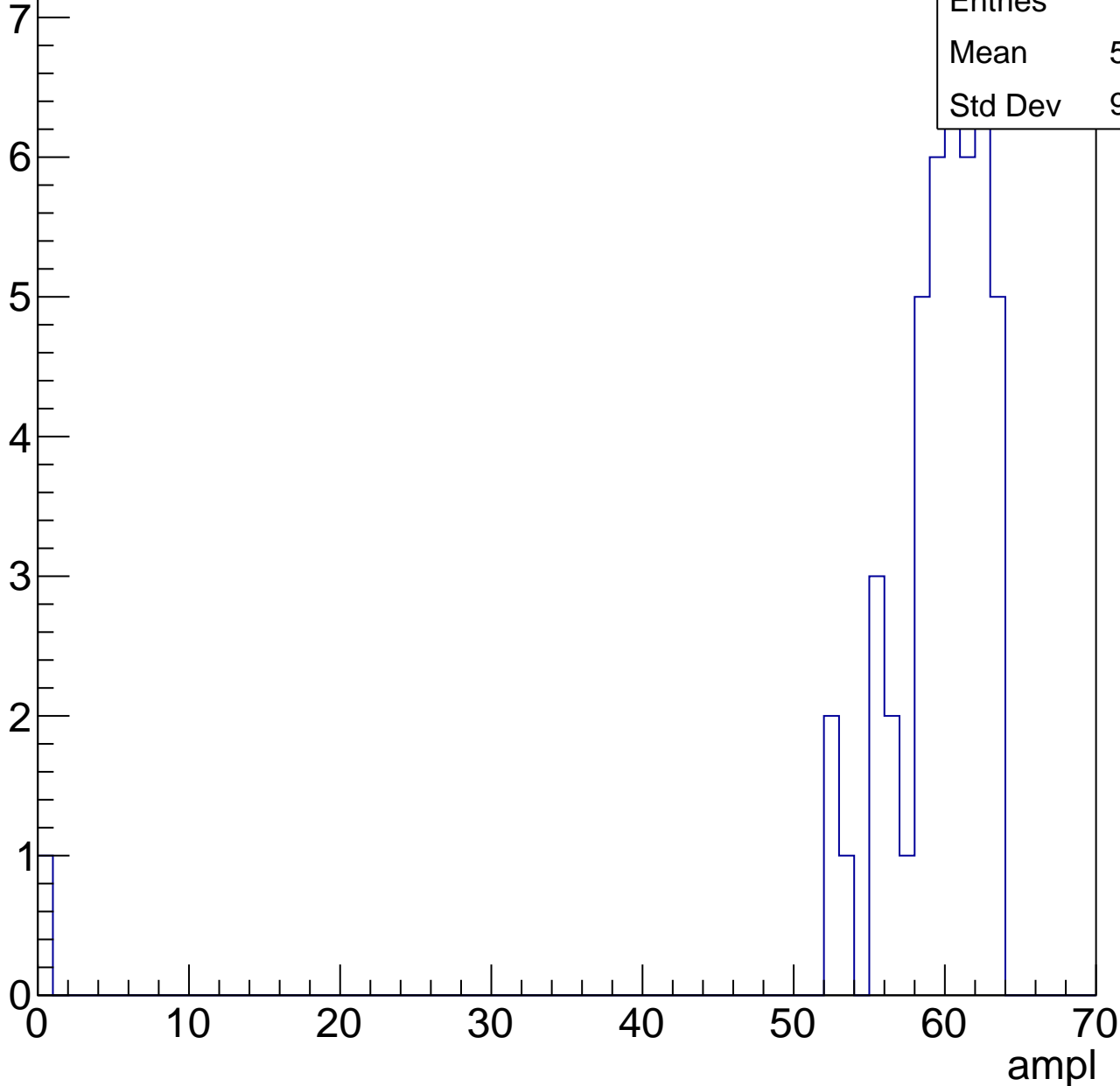


# B0L102S, U6-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	58.04
Std Dev	9.115

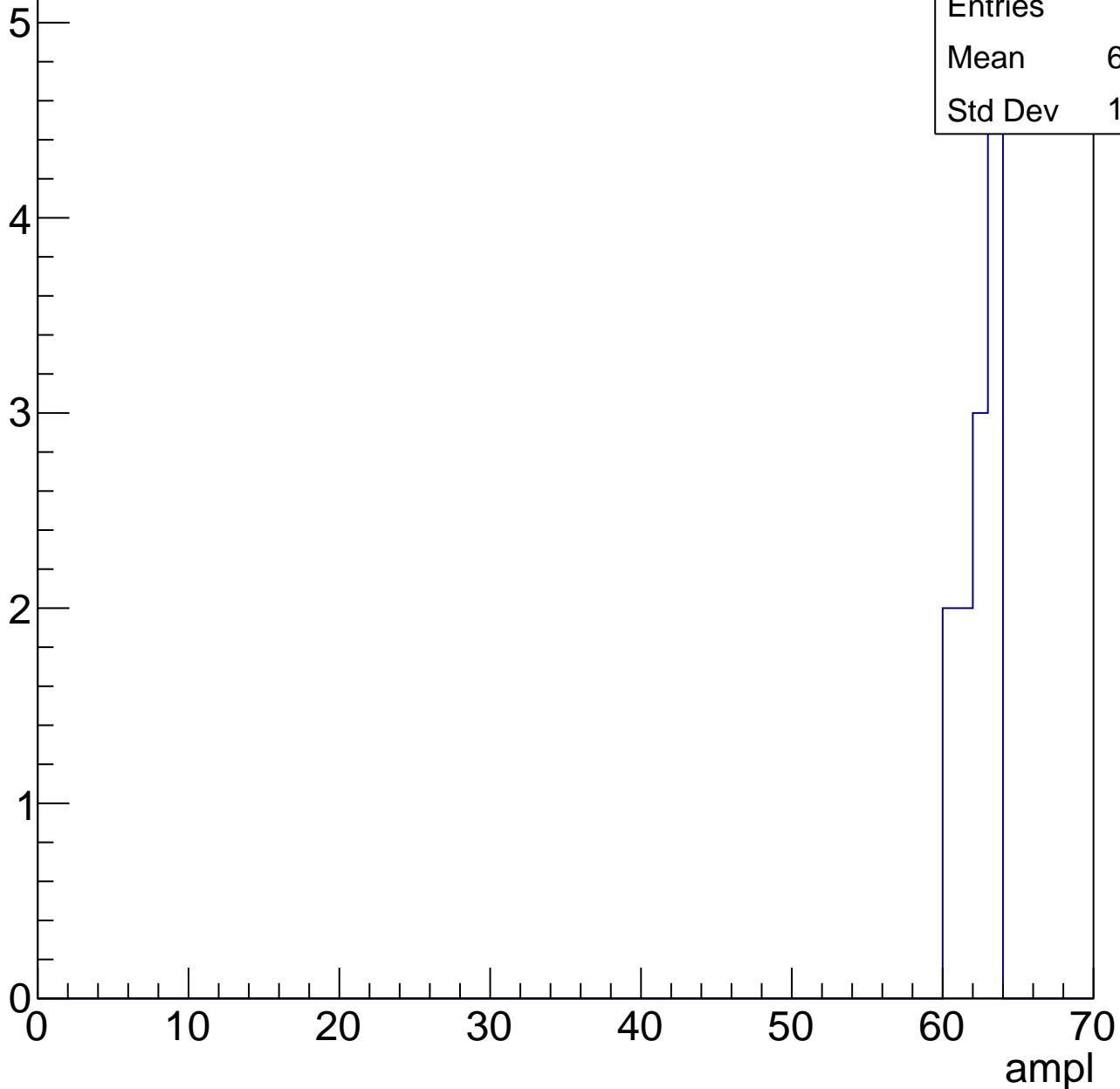


# B0L102S, U6-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	61.92
Std Dev	1.115

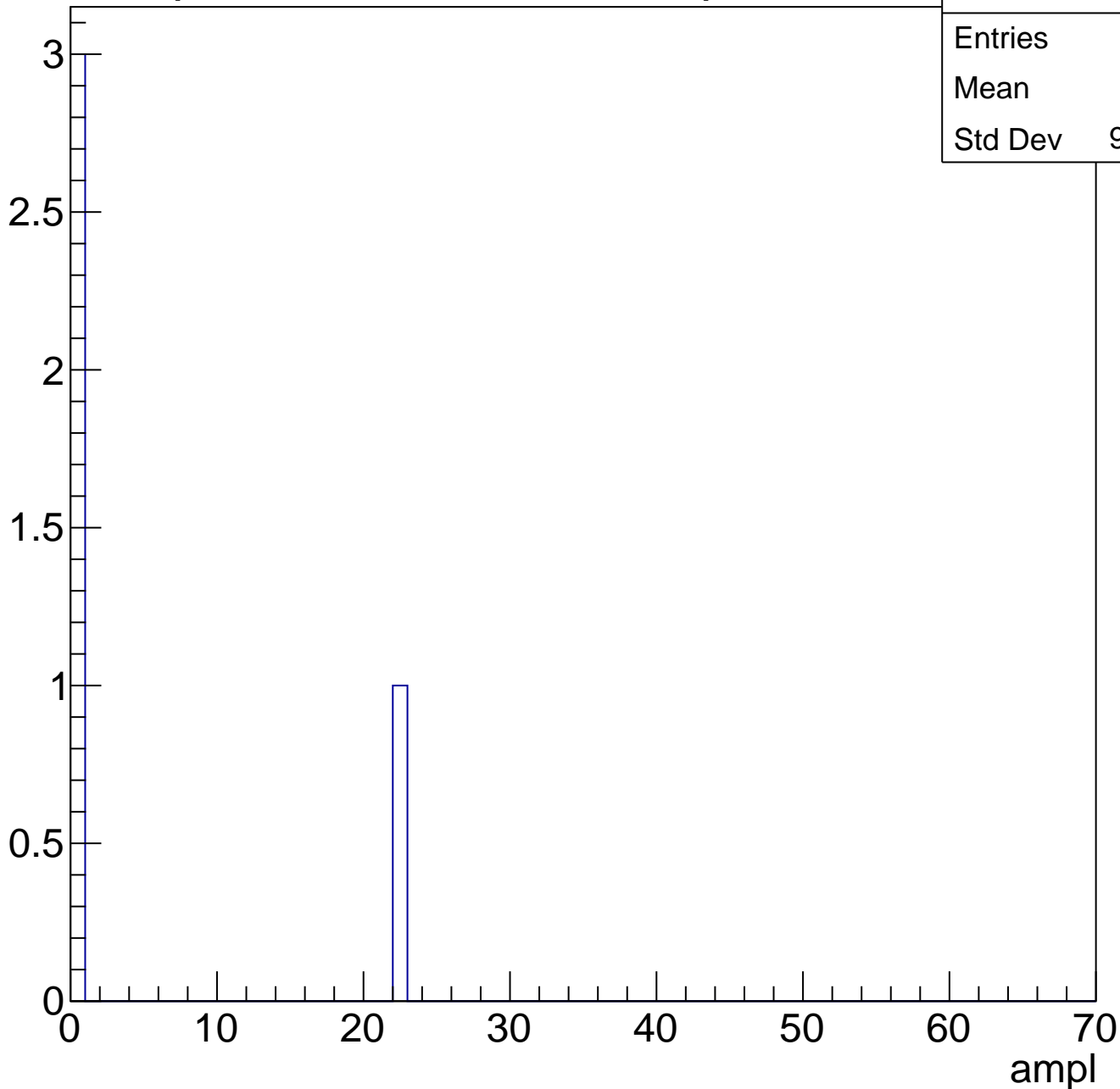




# B0L102S, U6-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch101, adc0

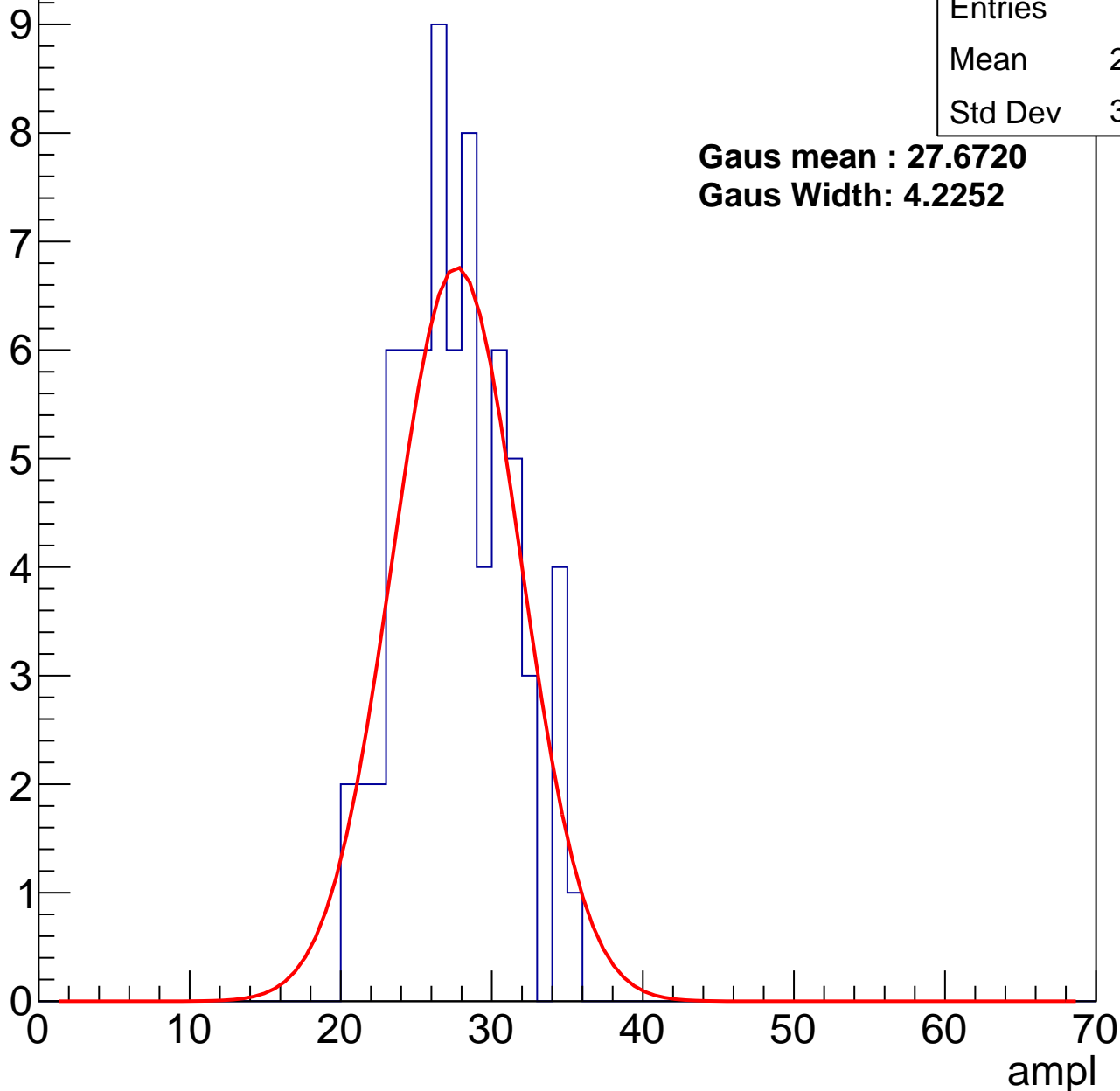
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	27.09
Std Dev	3.577

**Gaus mean : 27.6720**

**Gaus Width: 4.2252**



# B0L102S, U6-ch101, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	79
Mean	34.19
Std Dev	3.691

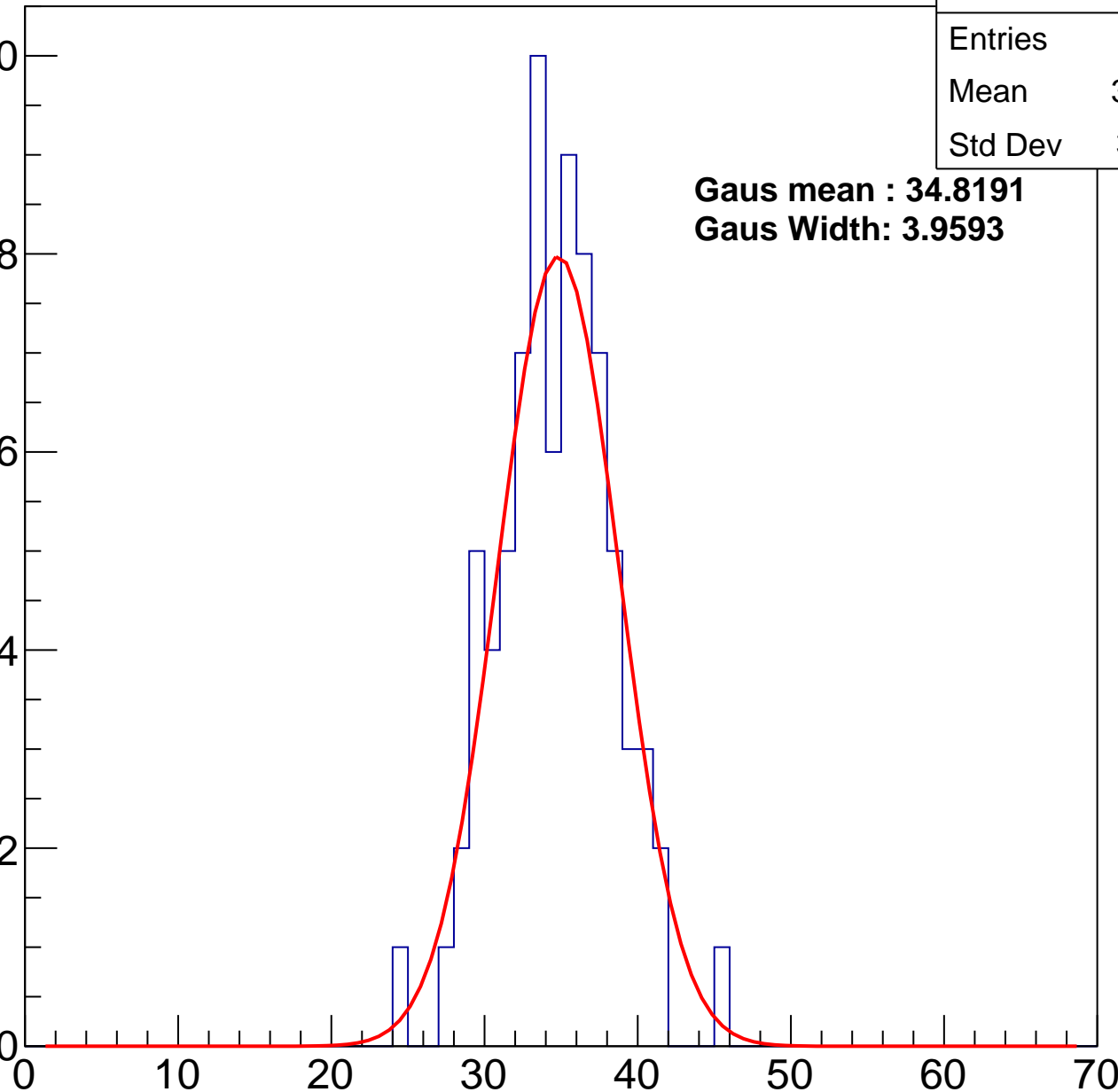
**Gaus mean : 34.8191**

**Gaus Width: 3.9593**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L102S, U6-ch101, adc2

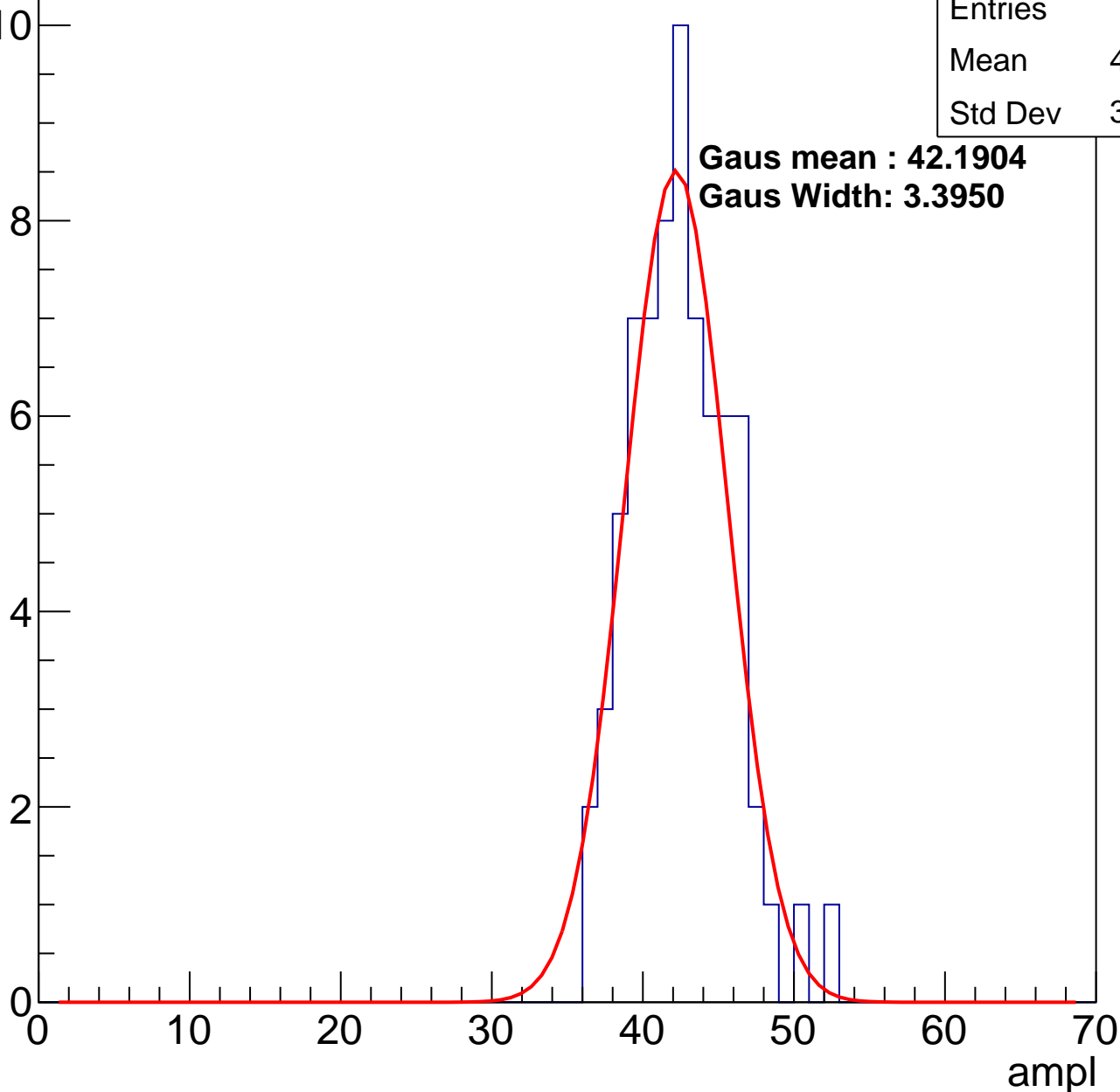
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	42.07
Std Dev	3.242

**Gaus mean : 42.1904**

**Gaus Width: 3.3950**

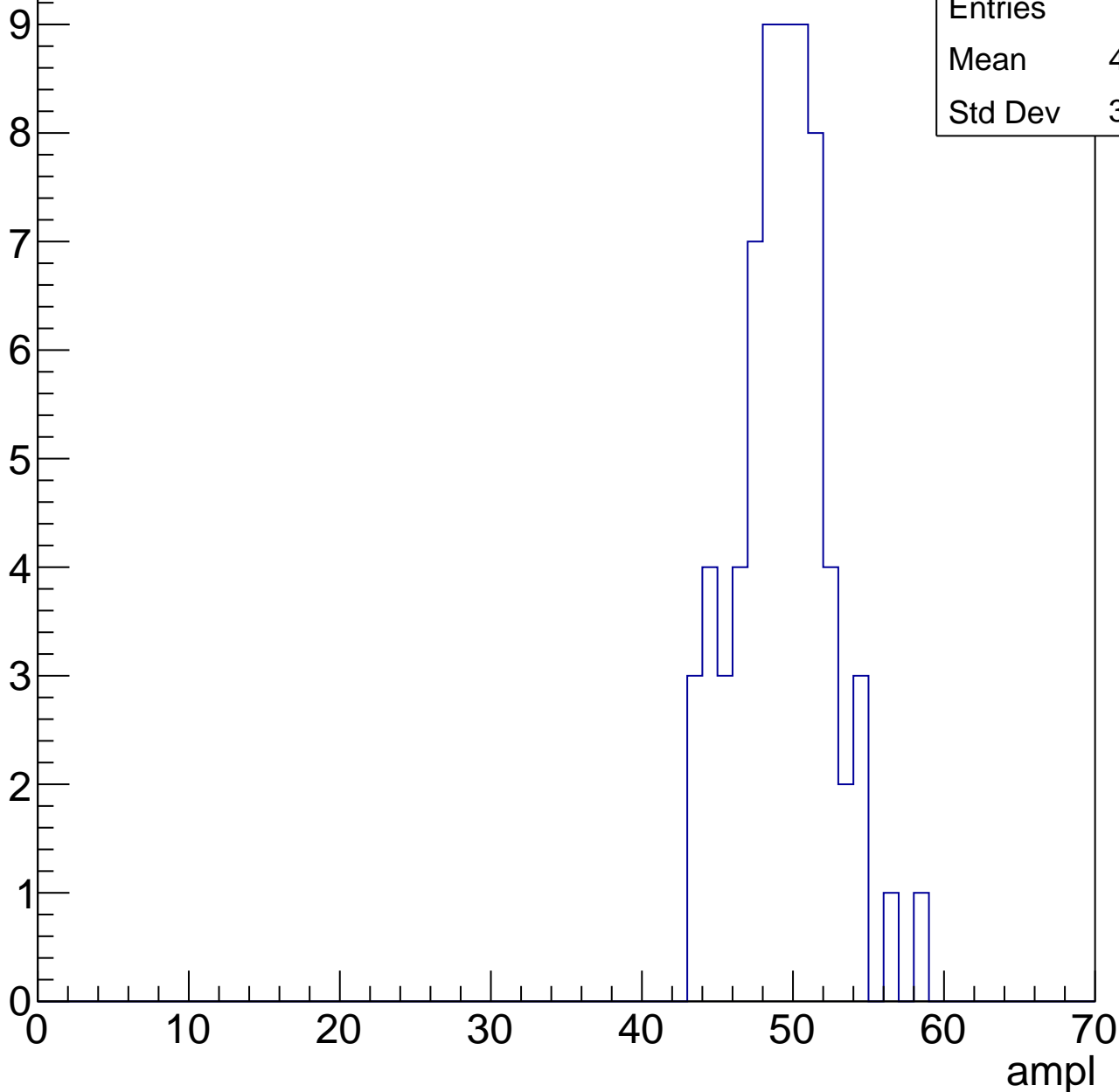


# B0L102S, U6-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	48.87
Std Dev	3.095

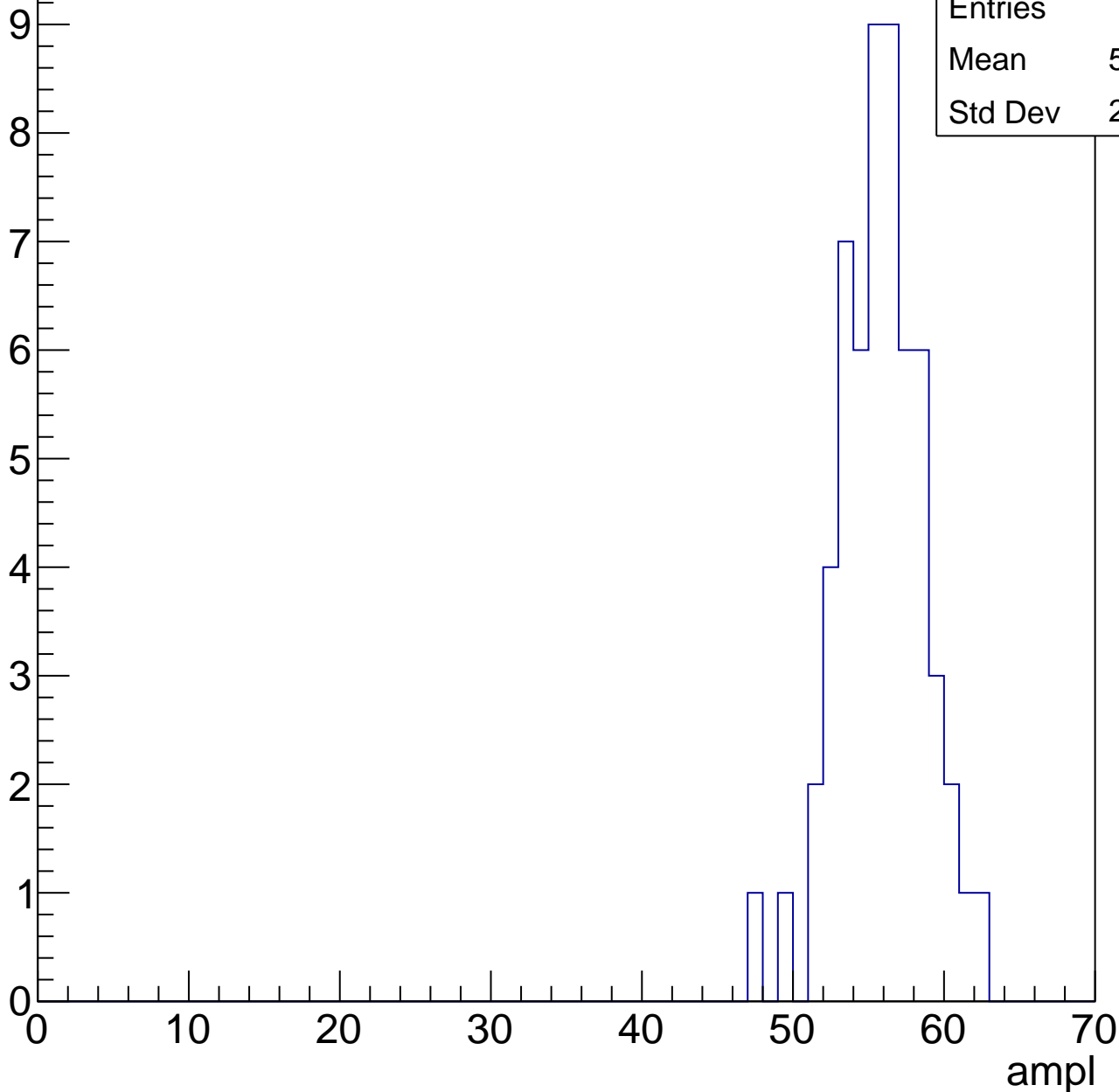


# B0L102S, U6-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	55.34
Std Dev	2.832

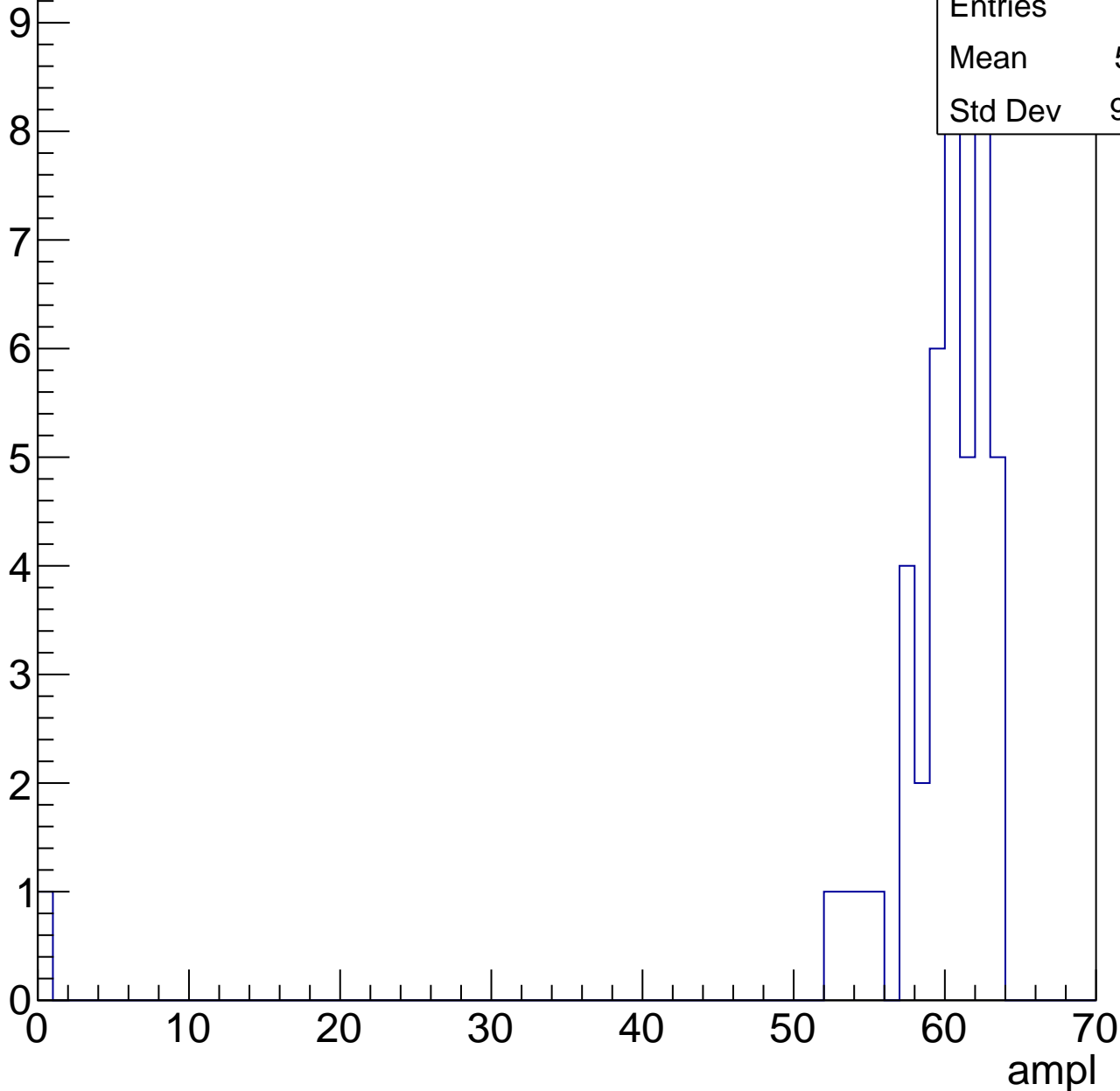


# B0L102S, U6-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	58.41
Std Dev	9.292

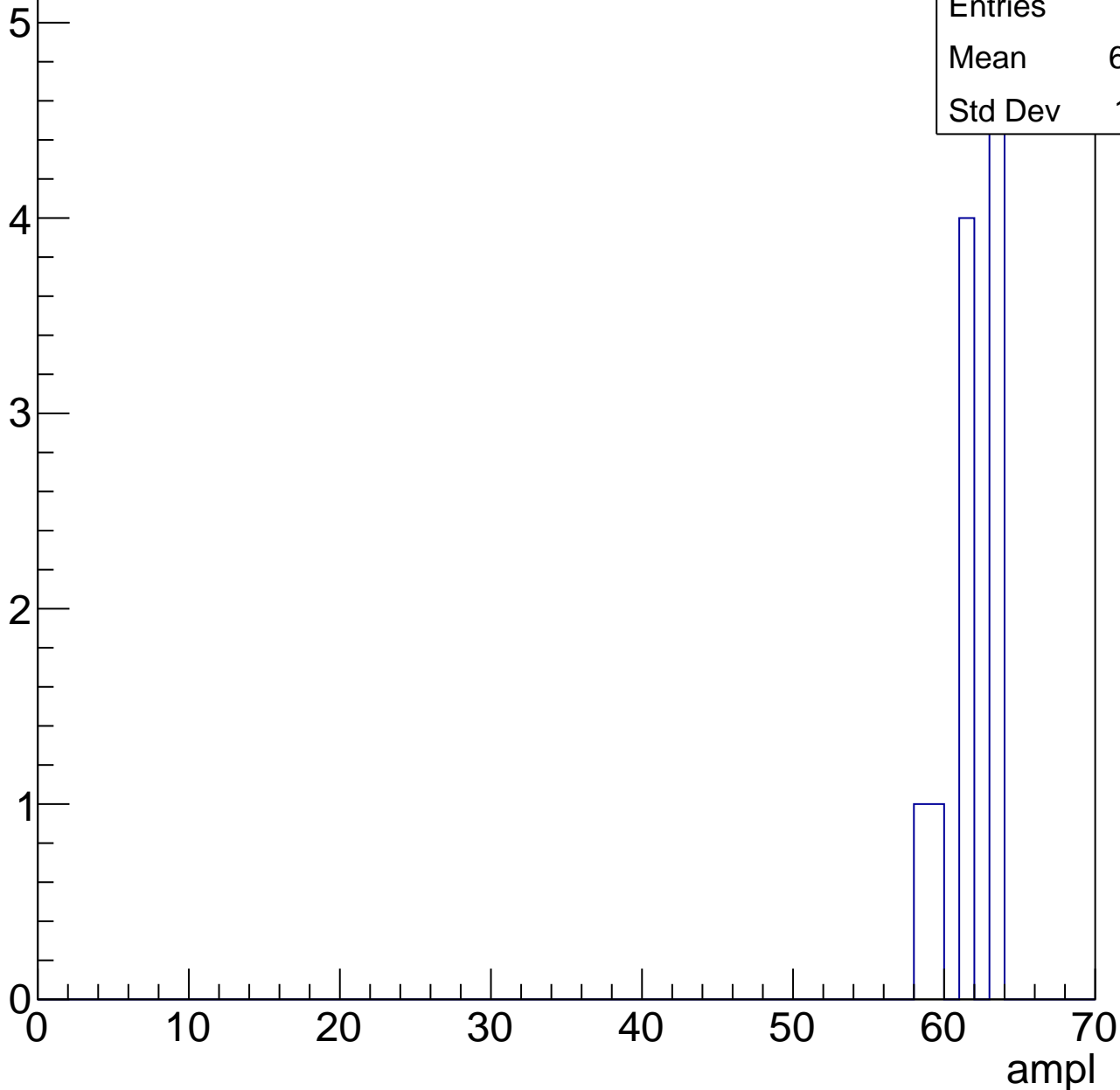


# B0L102S, U6-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	11
Mean	61.45
Std Dev	1.671





# B0L102S, U6-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch102, adc0

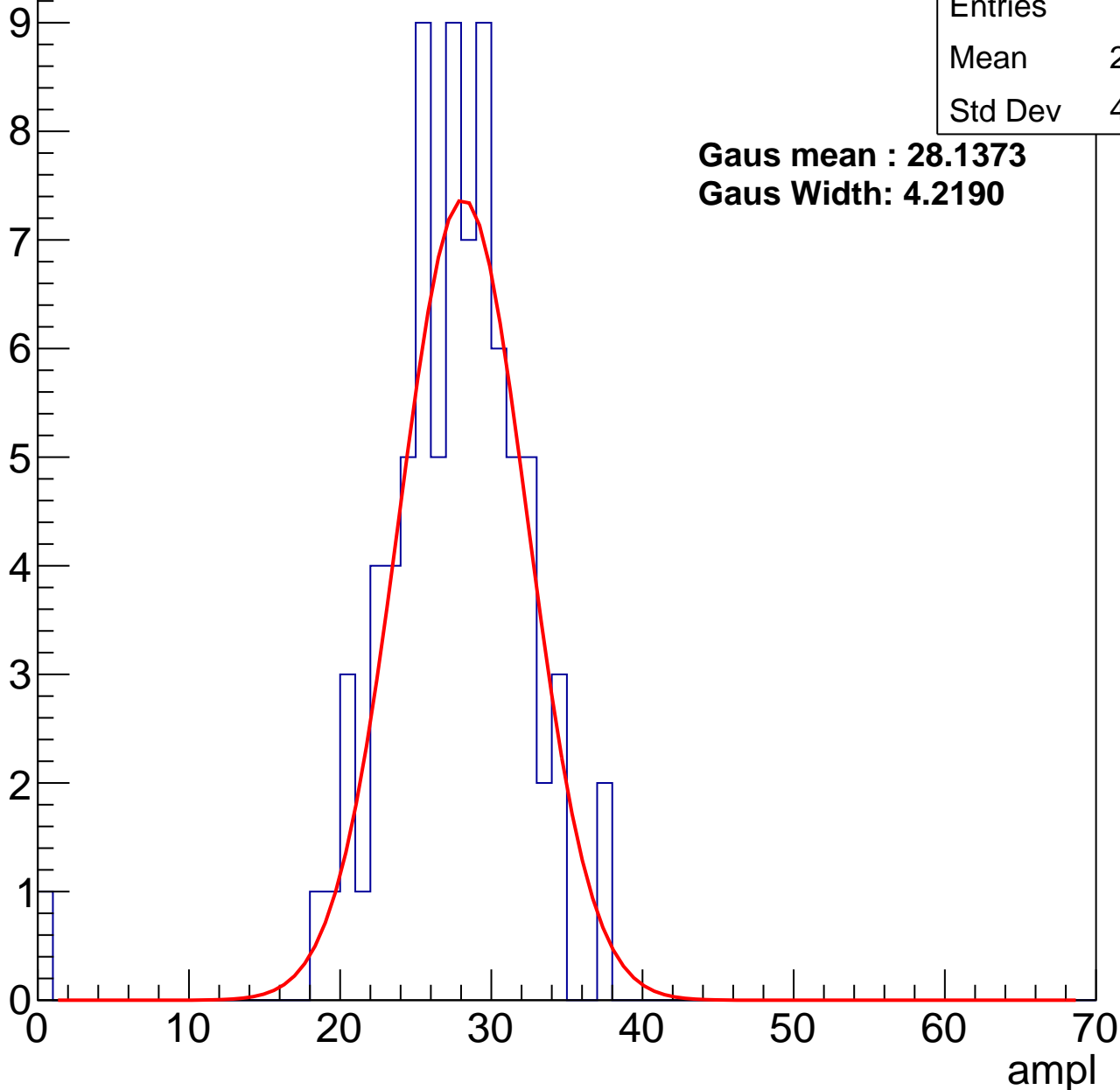
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	82
Mean	26.95
Std Dev	4.973

**Gaus mean : 28.1373**

**Gaus Width: 4.2190**



# B0L102S, U6-ch102, adc1

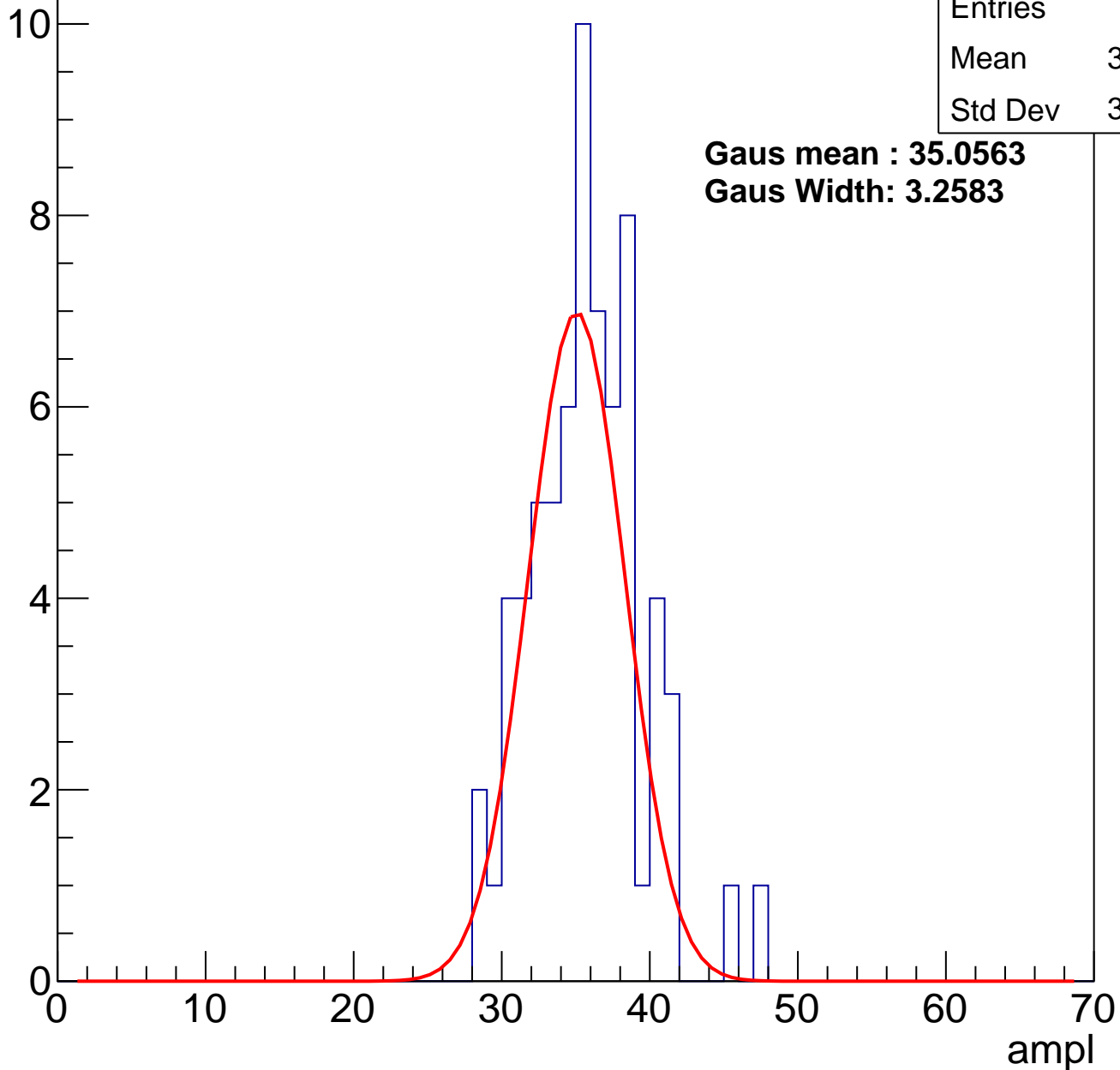
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	35.29
Std Dev	3.706

**Gaus mean : 35.0563**

**Gaus Width: 3.2583**

Entry



# B0L102S, U6-ch102, adc2

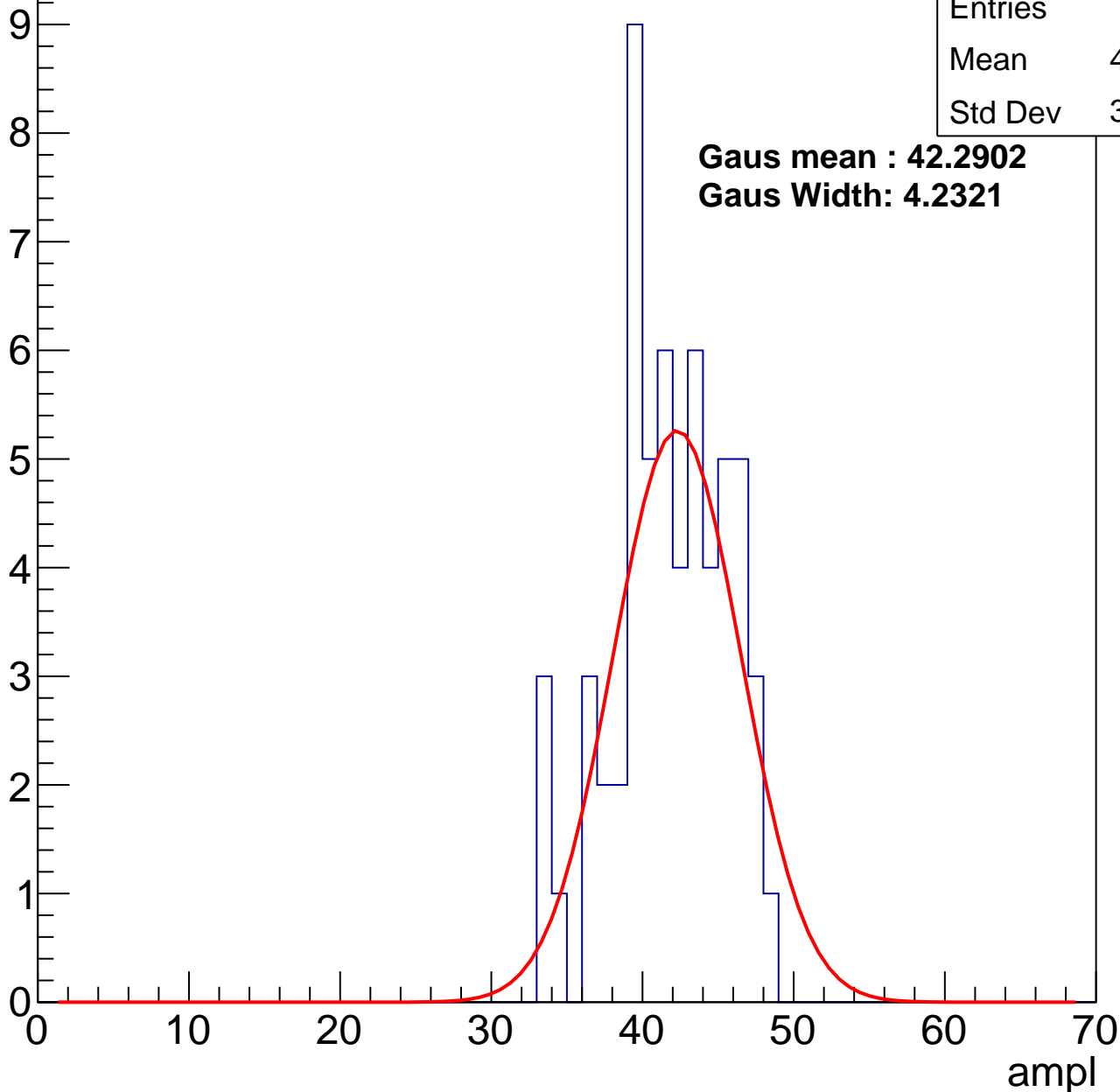
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	41.25
Std Dev	3.749

**Gaus mean : 42.2902**

**Gaus Width: 4.2321**

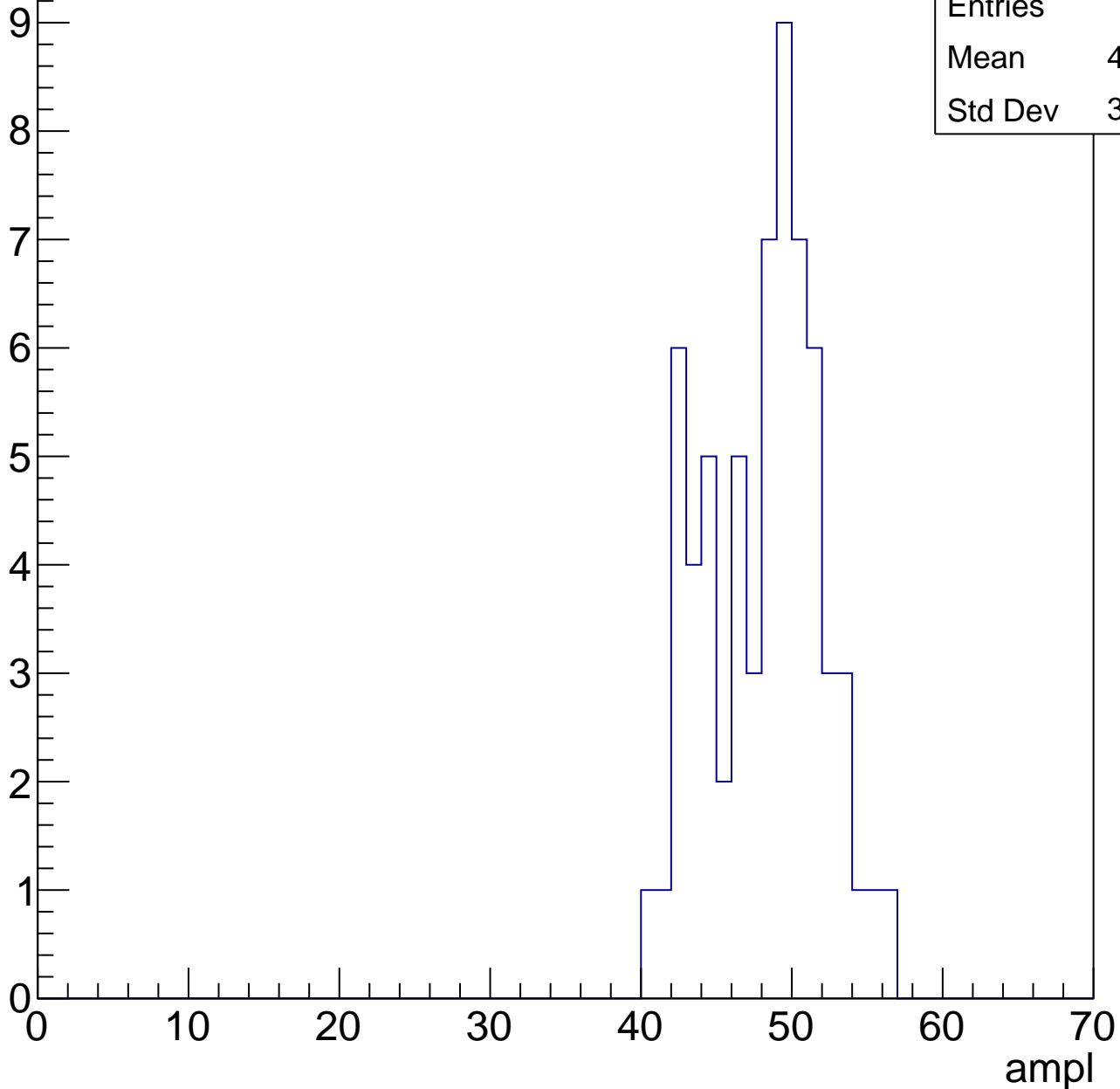


# B0L102S, U6-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	47.68
Std Dev	3.734

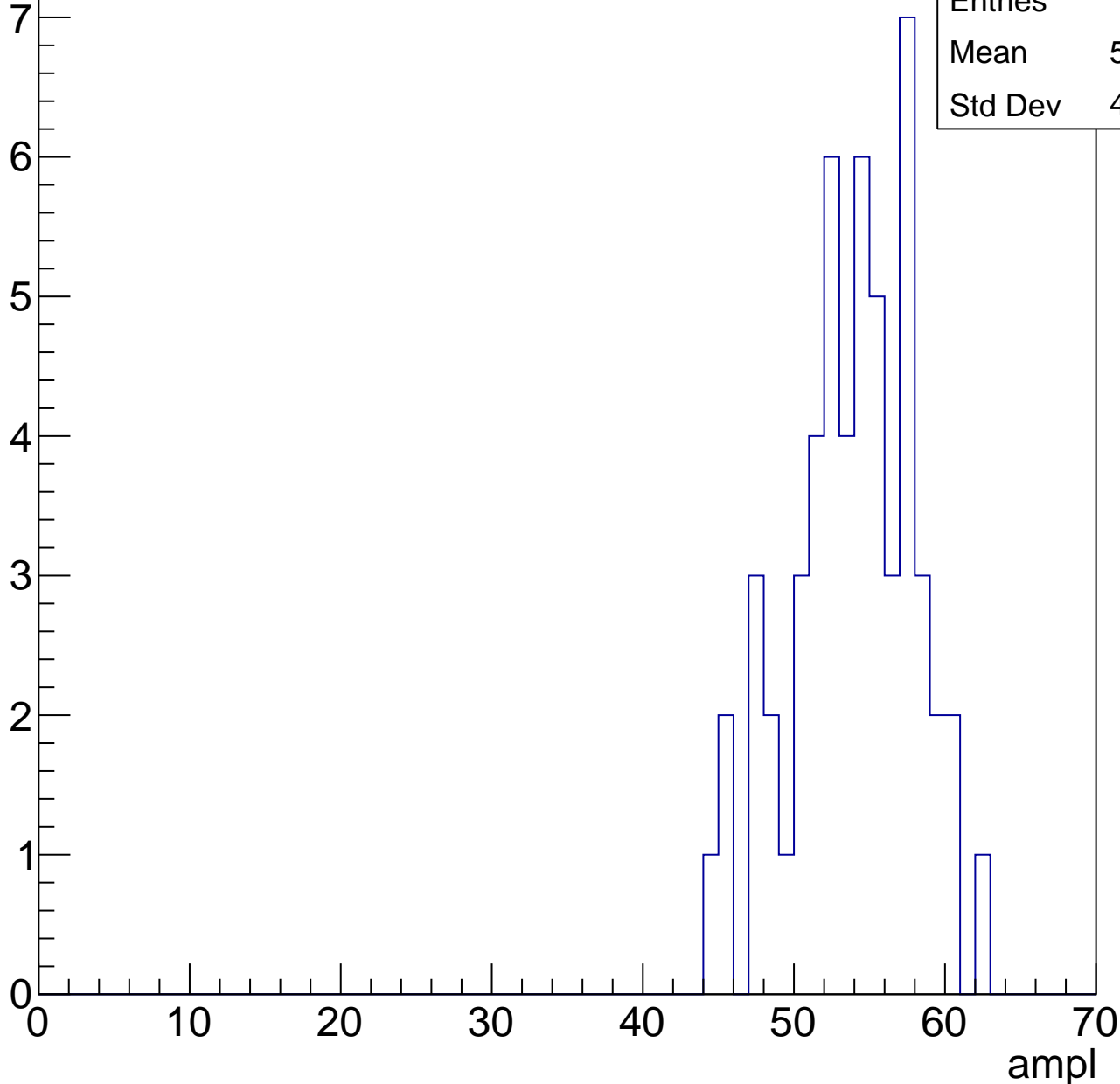


# B0L102S, U6-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	53.42
Std Dev	4.075



# B0L102S, U6-ch102, adc5

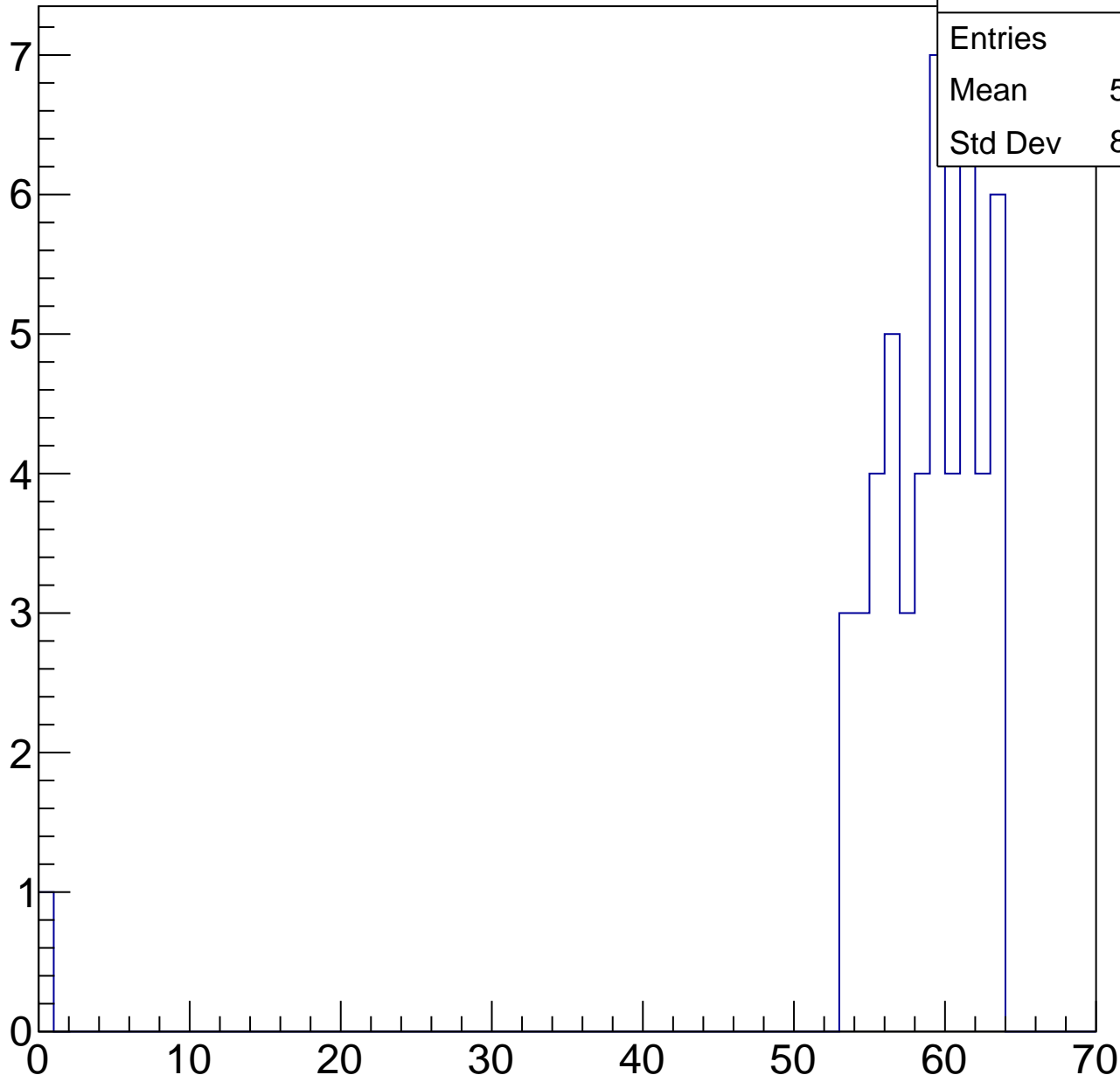
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	57.45
Std Dev	8.667

ampl

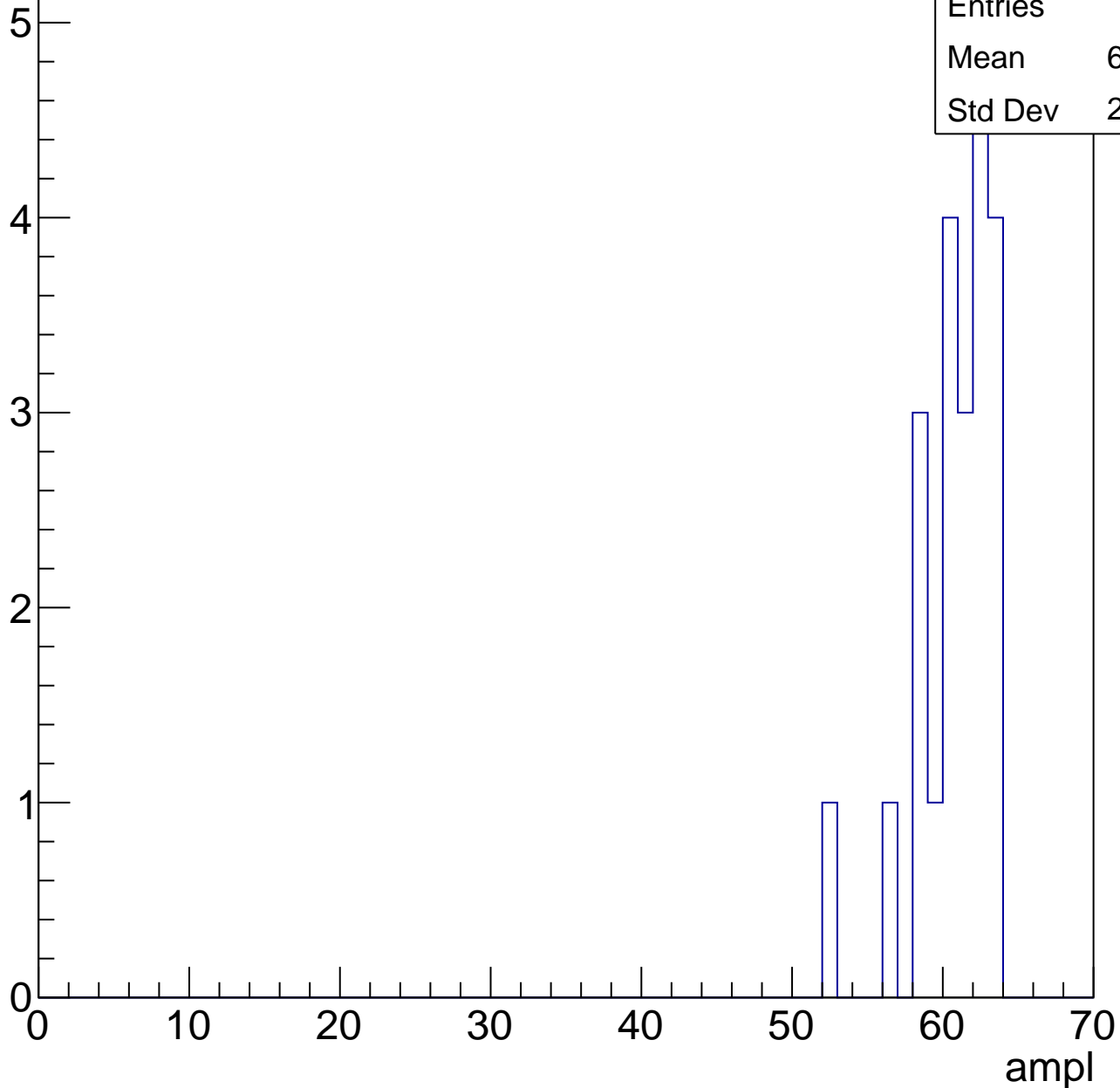


# B0L102S, U6-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	22
Mean	60.27
Std Dev	2.614





# B0L102S, U6-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch103, adc0

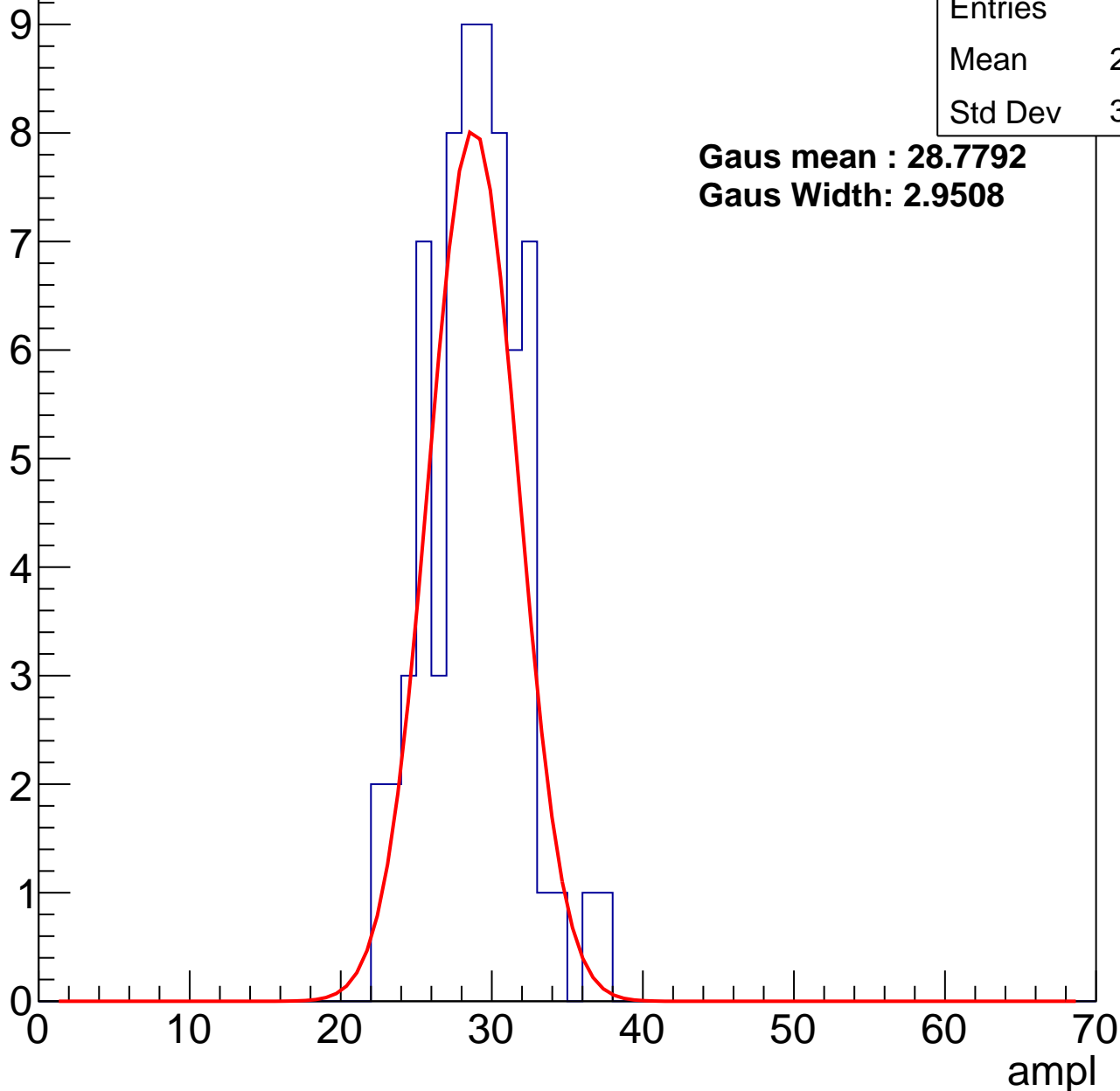
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.44
Std Dev	3.098

**Gaus mean : 28.7792**

**Gaus Width: 2.9508**



# B0L102S, U6-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	35.99
Std Dev	3.594

**Gaus mean : 35.3242**

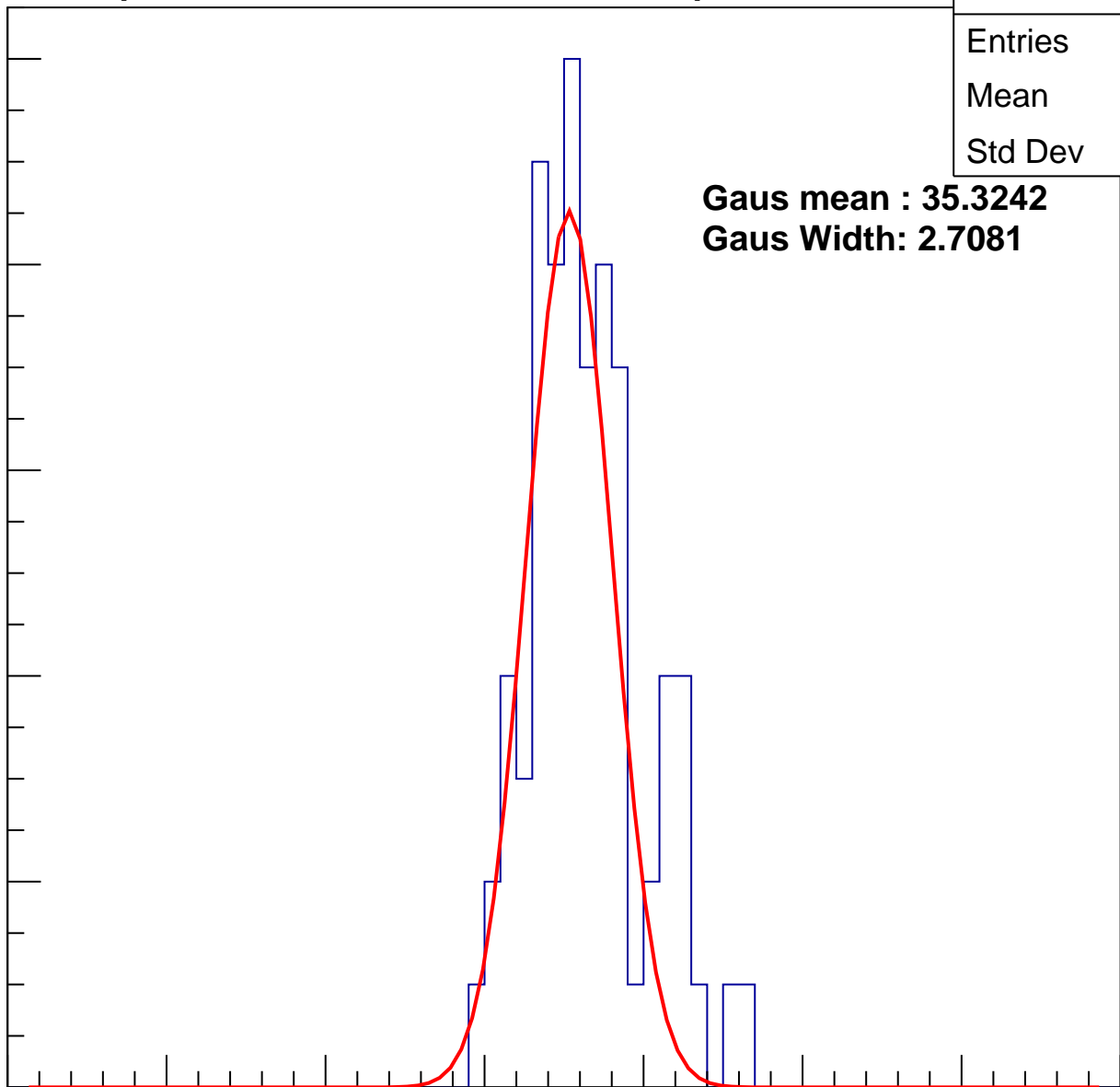
**Gaus Width: 2.7081**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch103, adc2

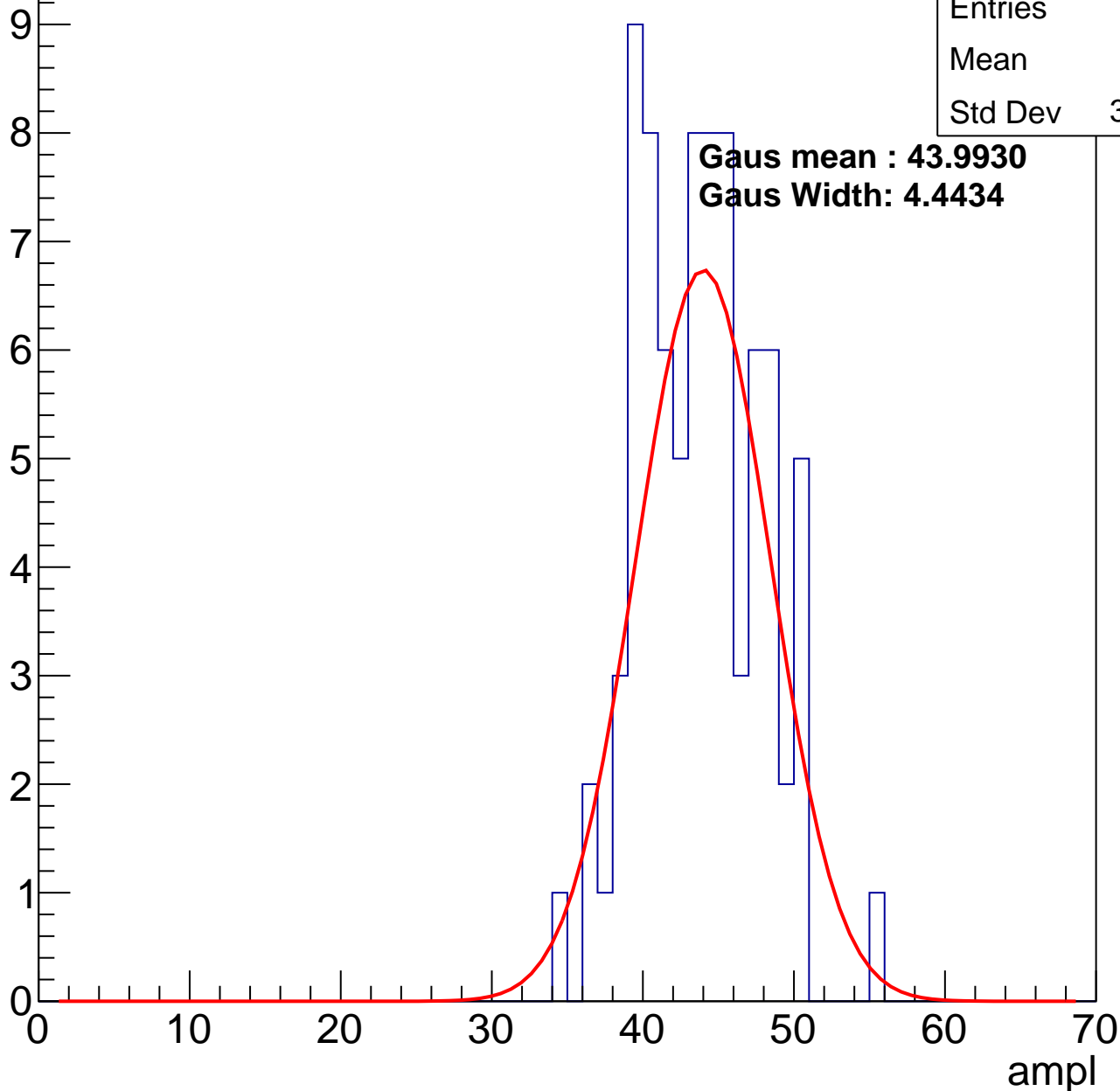
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	82
Mean	43.3
Std Dev	3.984

**Gaus mean : 43.9930**

**Gaus Width: 4.4434**

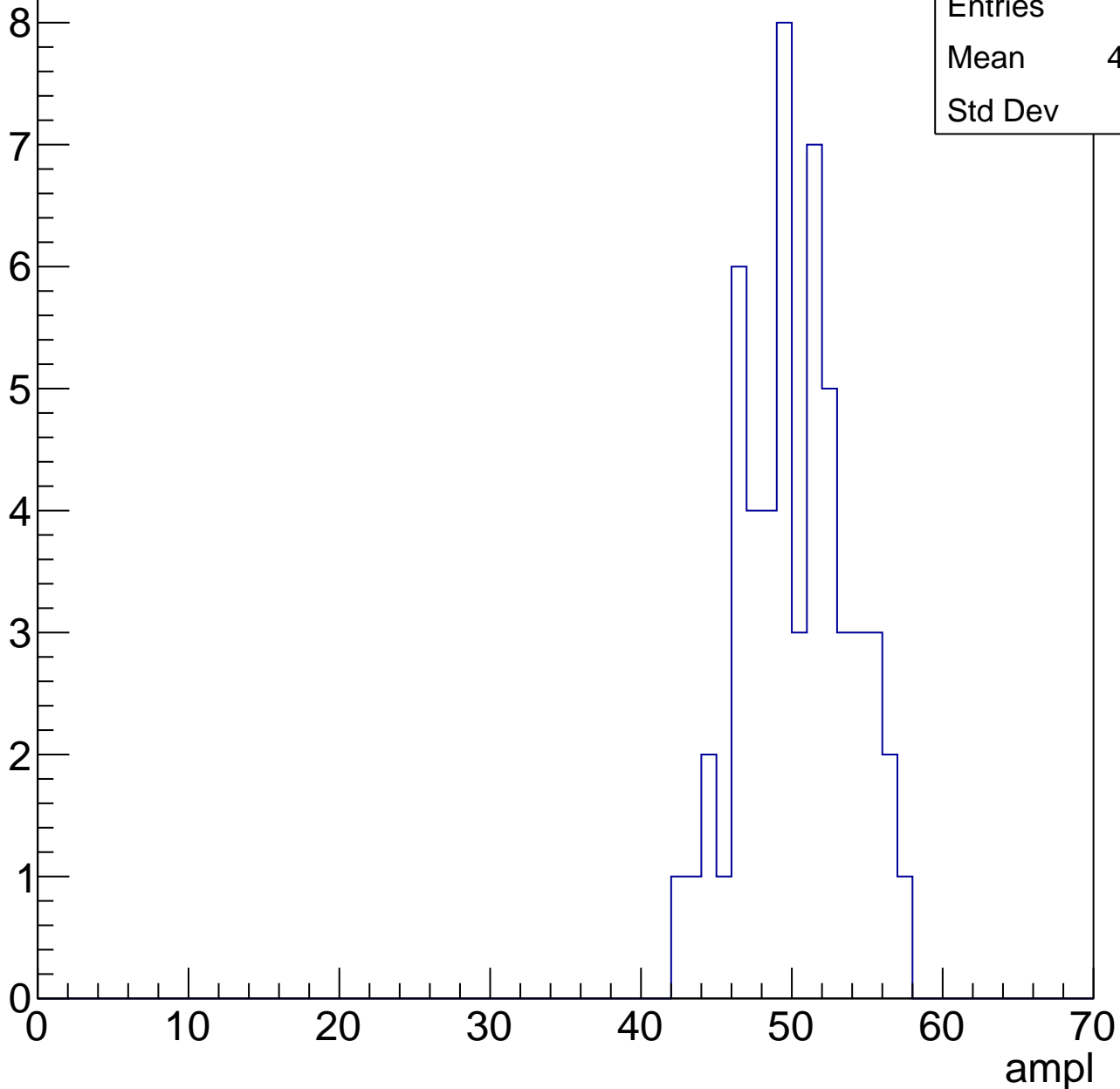


# B0L102S, U6-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	49.78
Std Dev	3.5

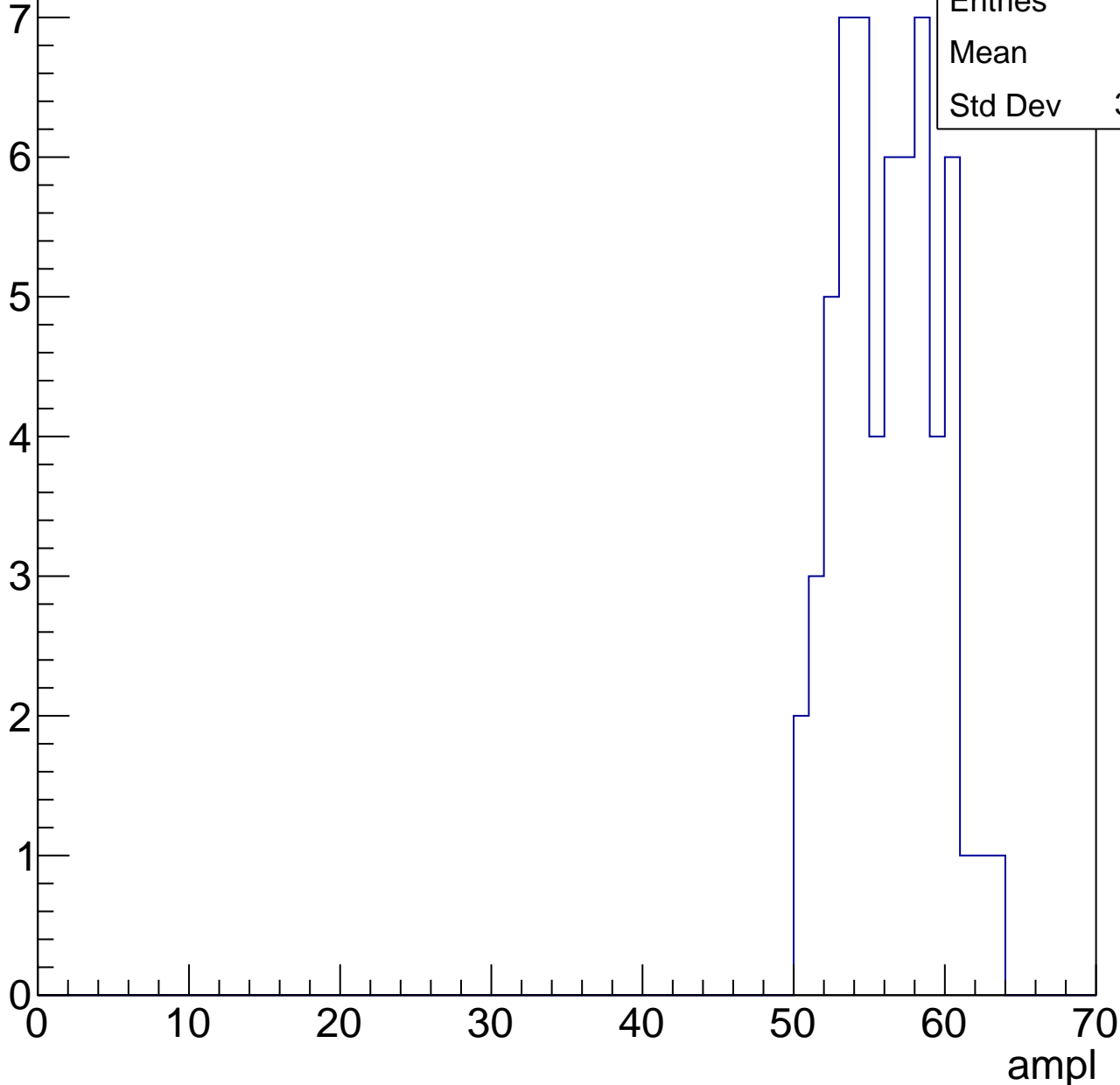


# B0L102S, U6-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

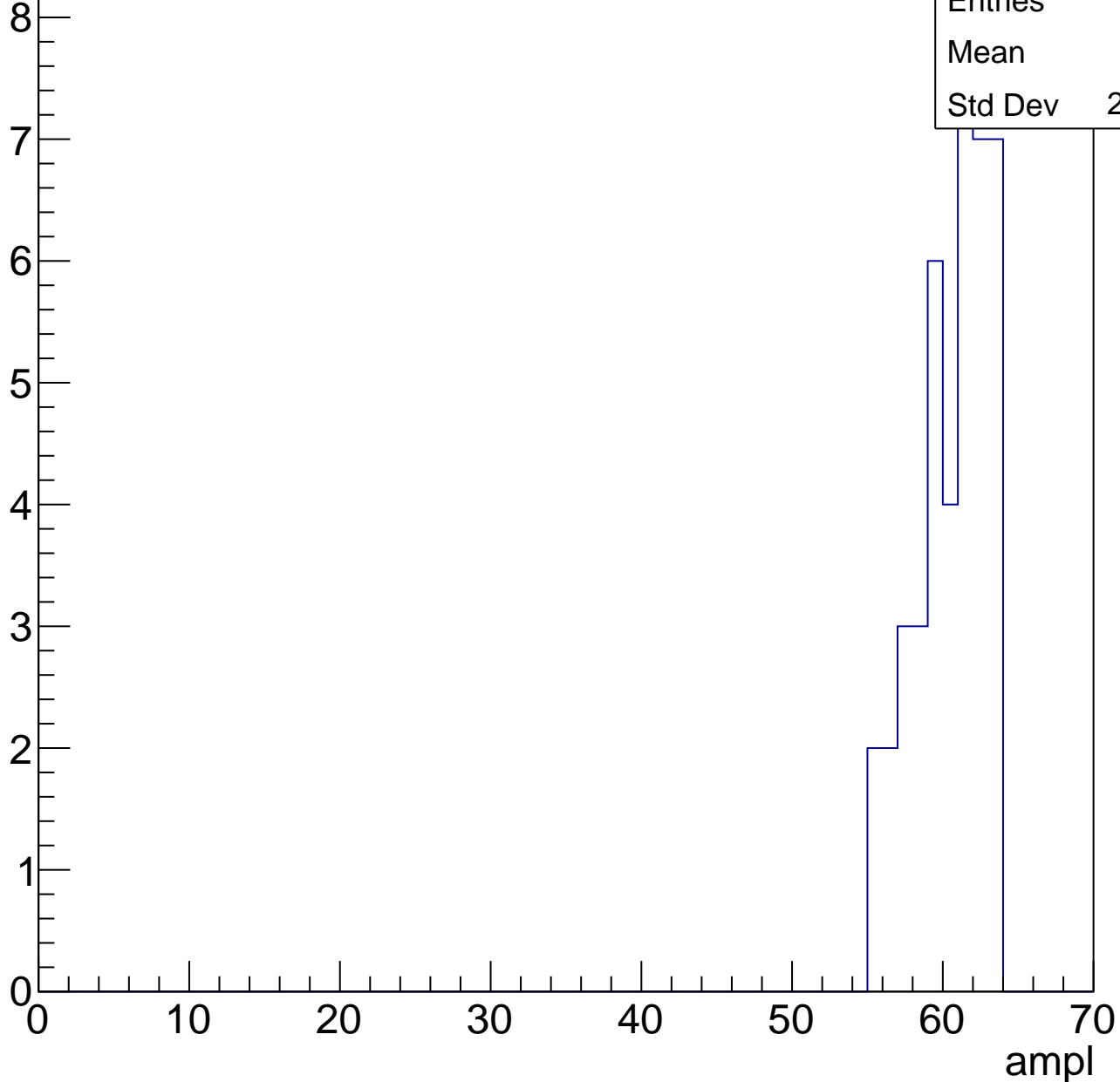
Entries	60
Mean	55.8
Std Dev	3.151



# B0L102S, U6-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	42
Mean	60.1
Std Dev	2.318

# B0L102S, U6-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	6
Mean	51.83
Std Dev	23.19



# B0L102S, U6-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch104, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	87
Mean	24.15
Std Dev	5.132

**Gaus mean : 25.1625**

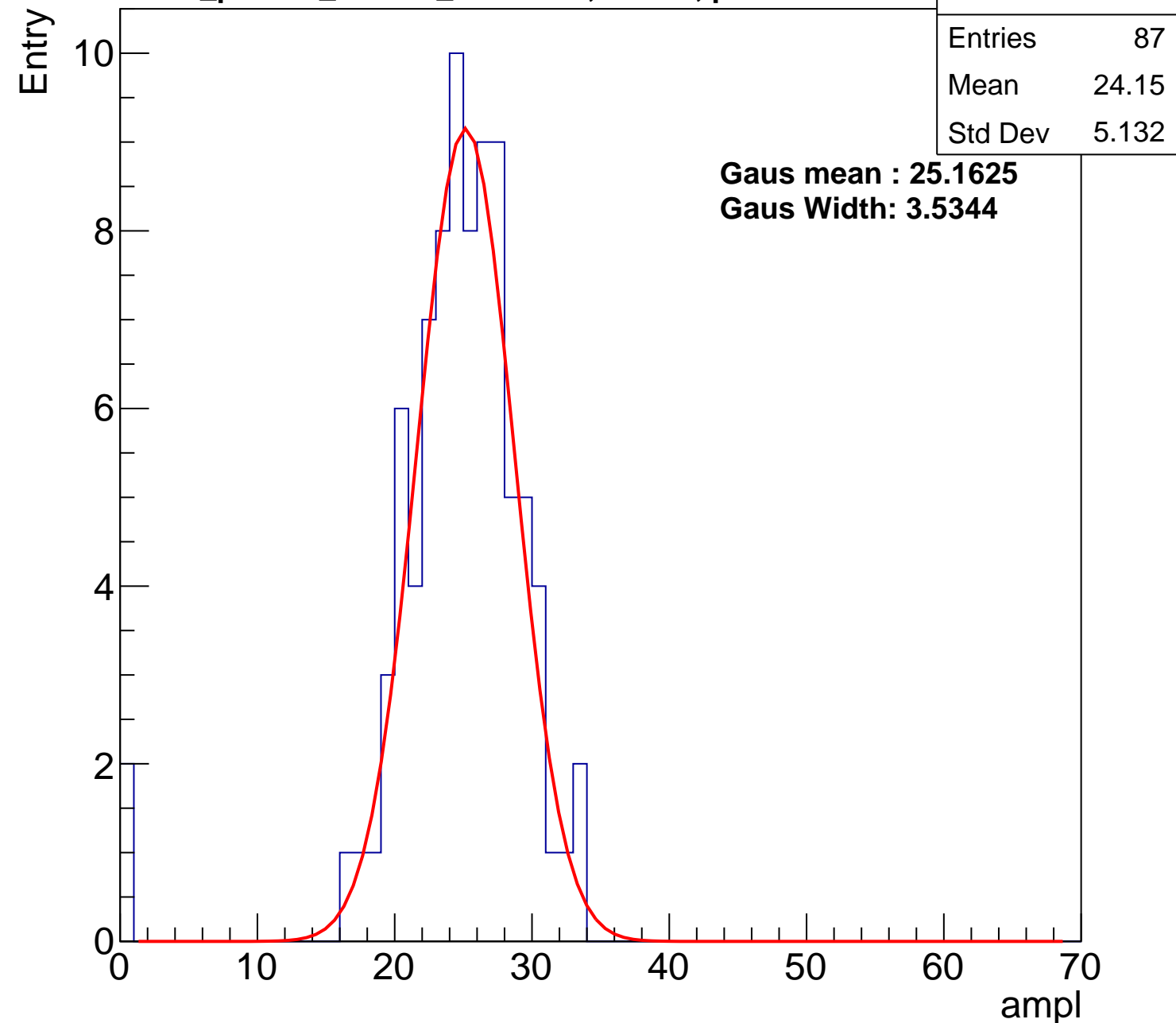
**Gaus Width: 3.5344**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



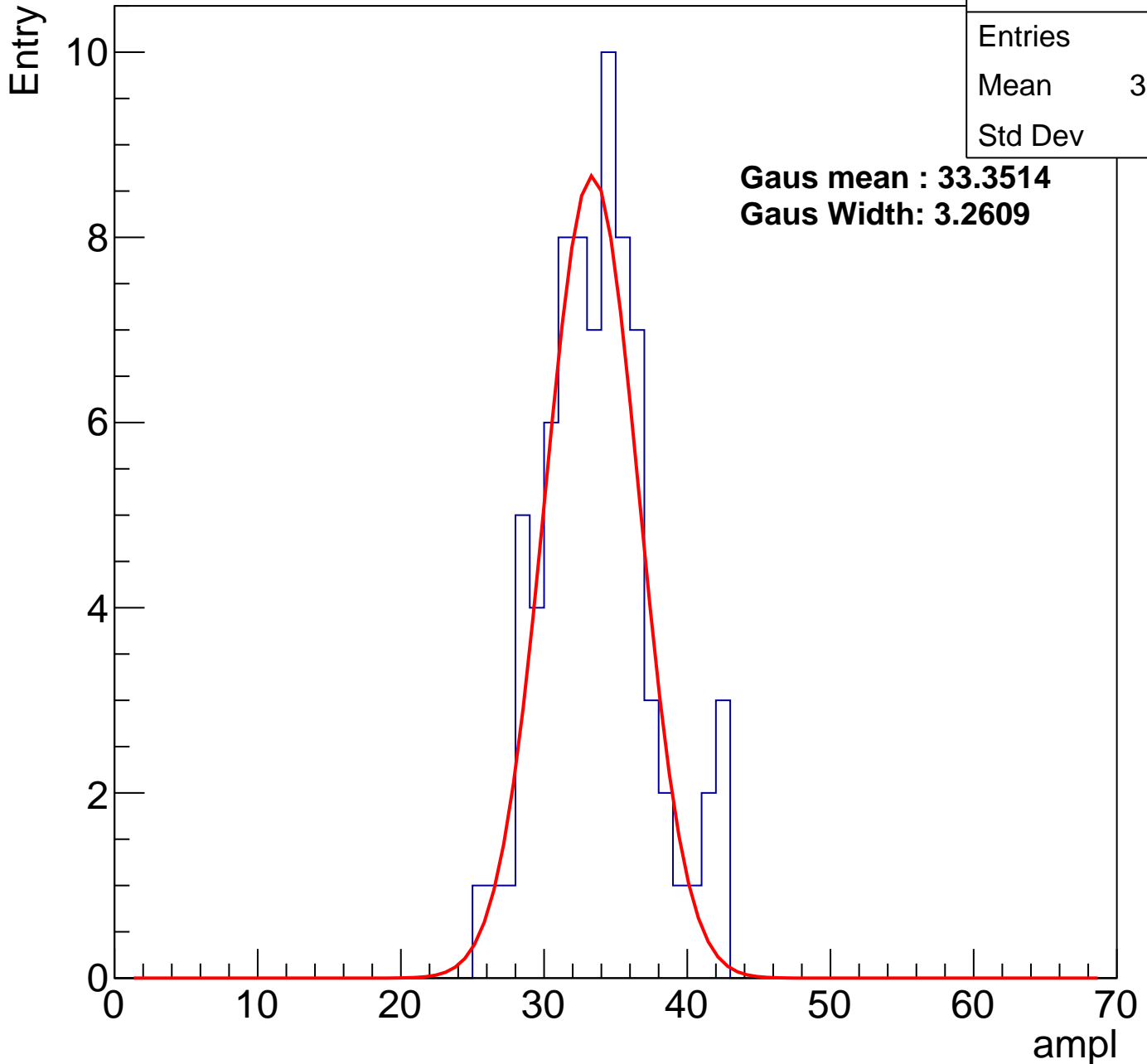
# B0L102S, U6-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	78
Mean	33.27
Std Dev	3.73

**Gaus mean : 33.3514**

**Gaus Width: 3.2609**



# B0L102S, U6-ch104, adc2

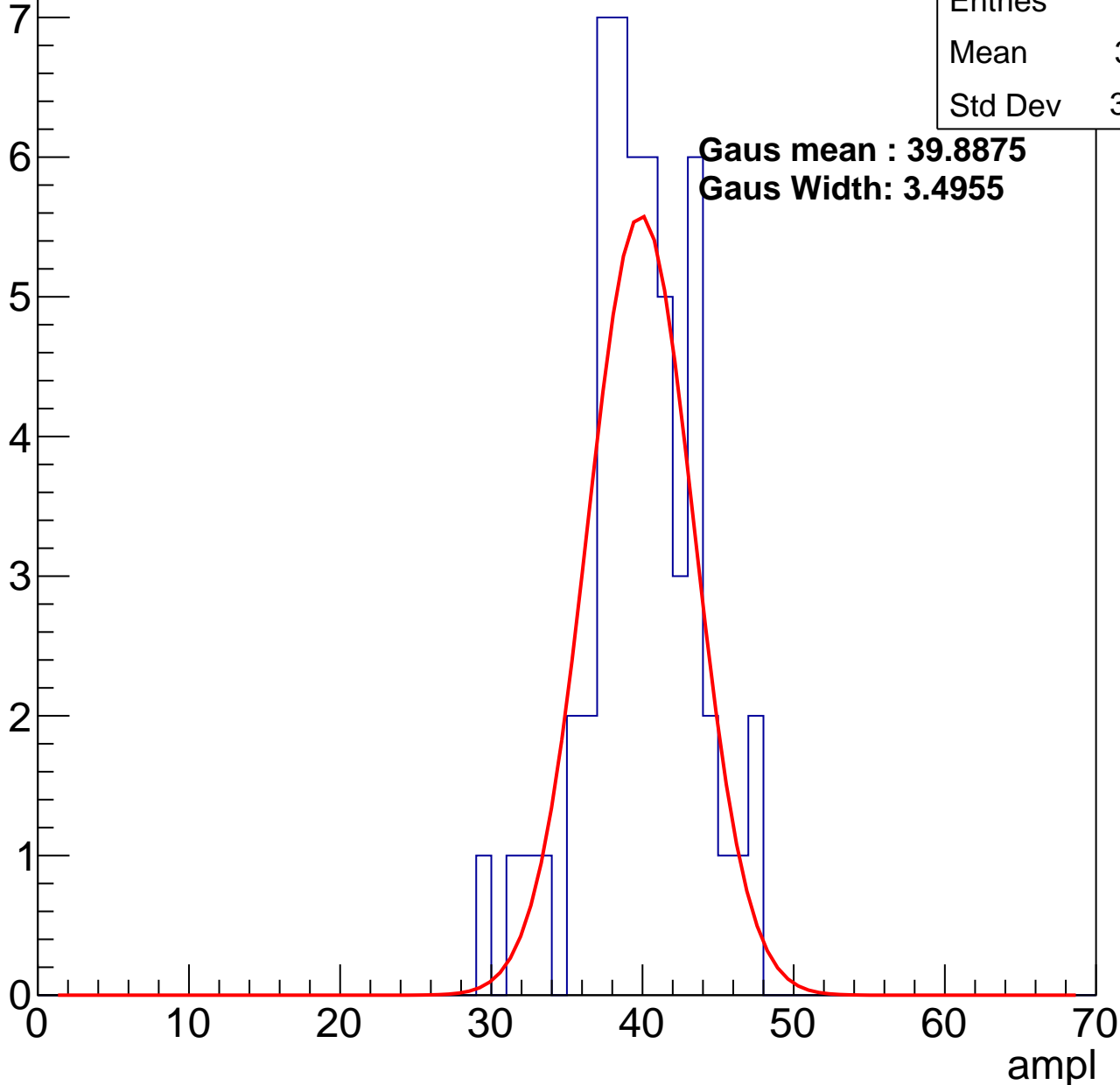
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	39.41
Std Dev	3.694

**Gaus mean : 39.8875**

**Gaus Width: 3.4955**

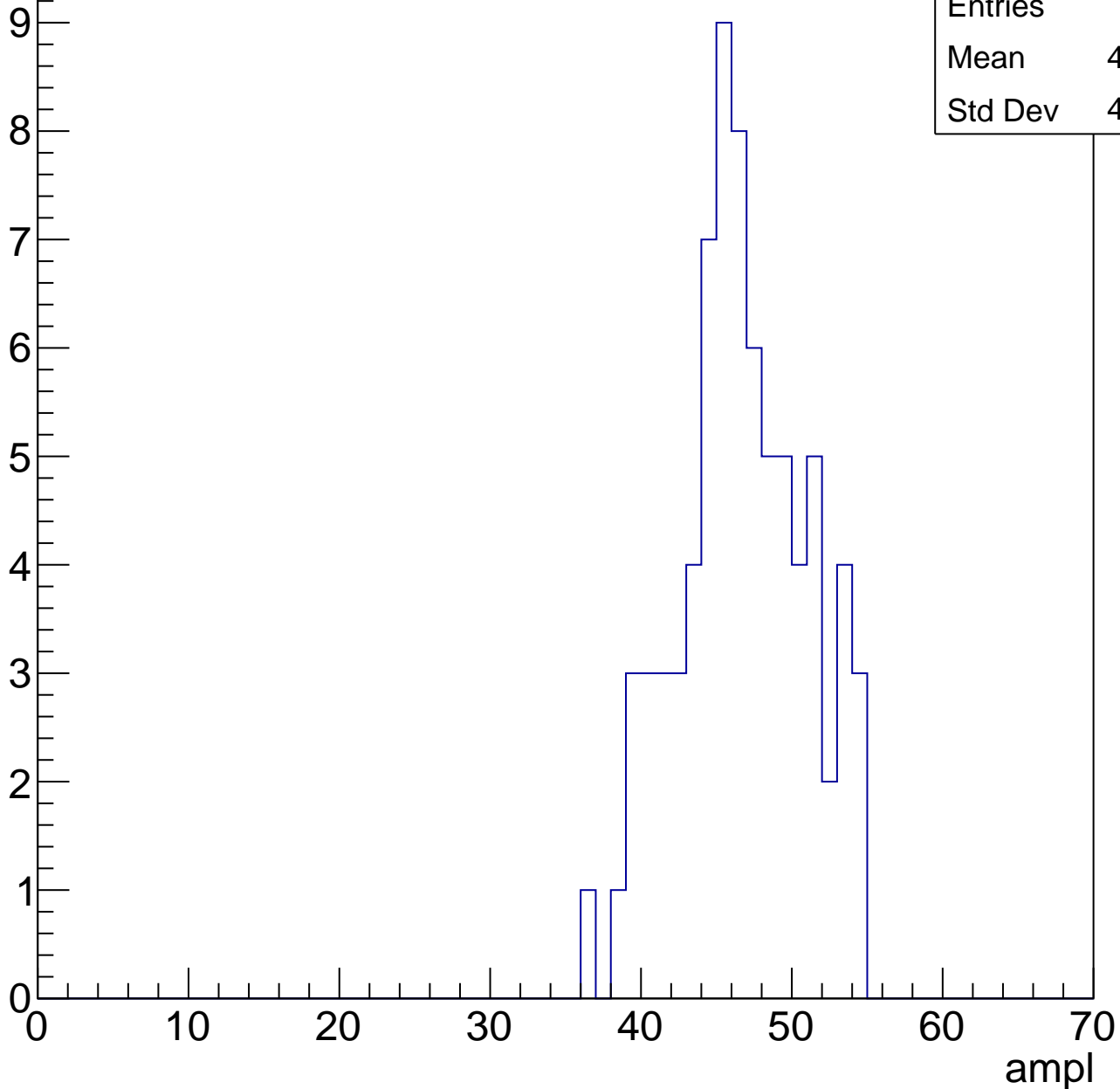


# B0L102S, U6-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	46.22
Std Dev	4.185

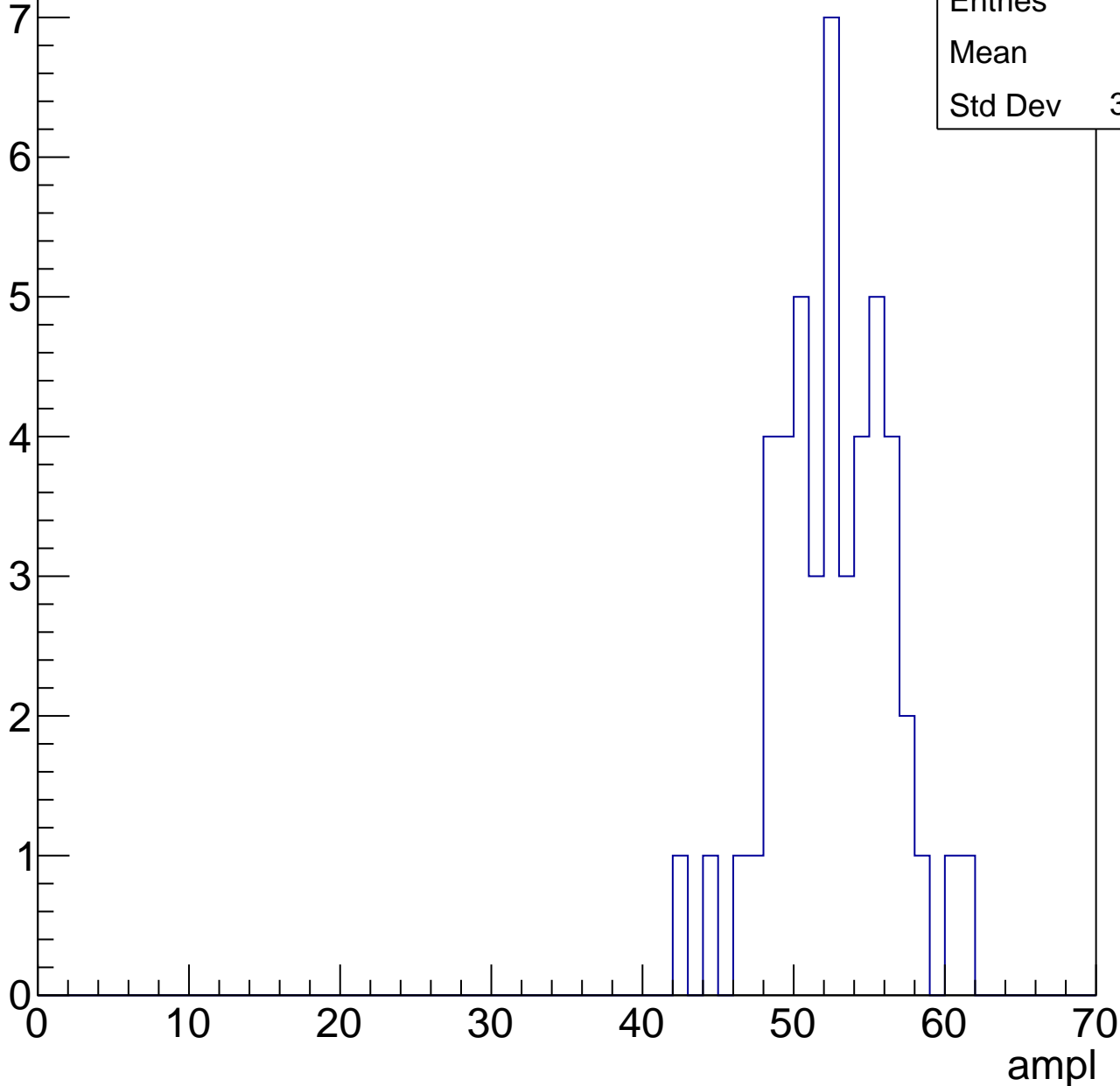


# B0L102S, U6-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	52.1
Std Dev	3.836

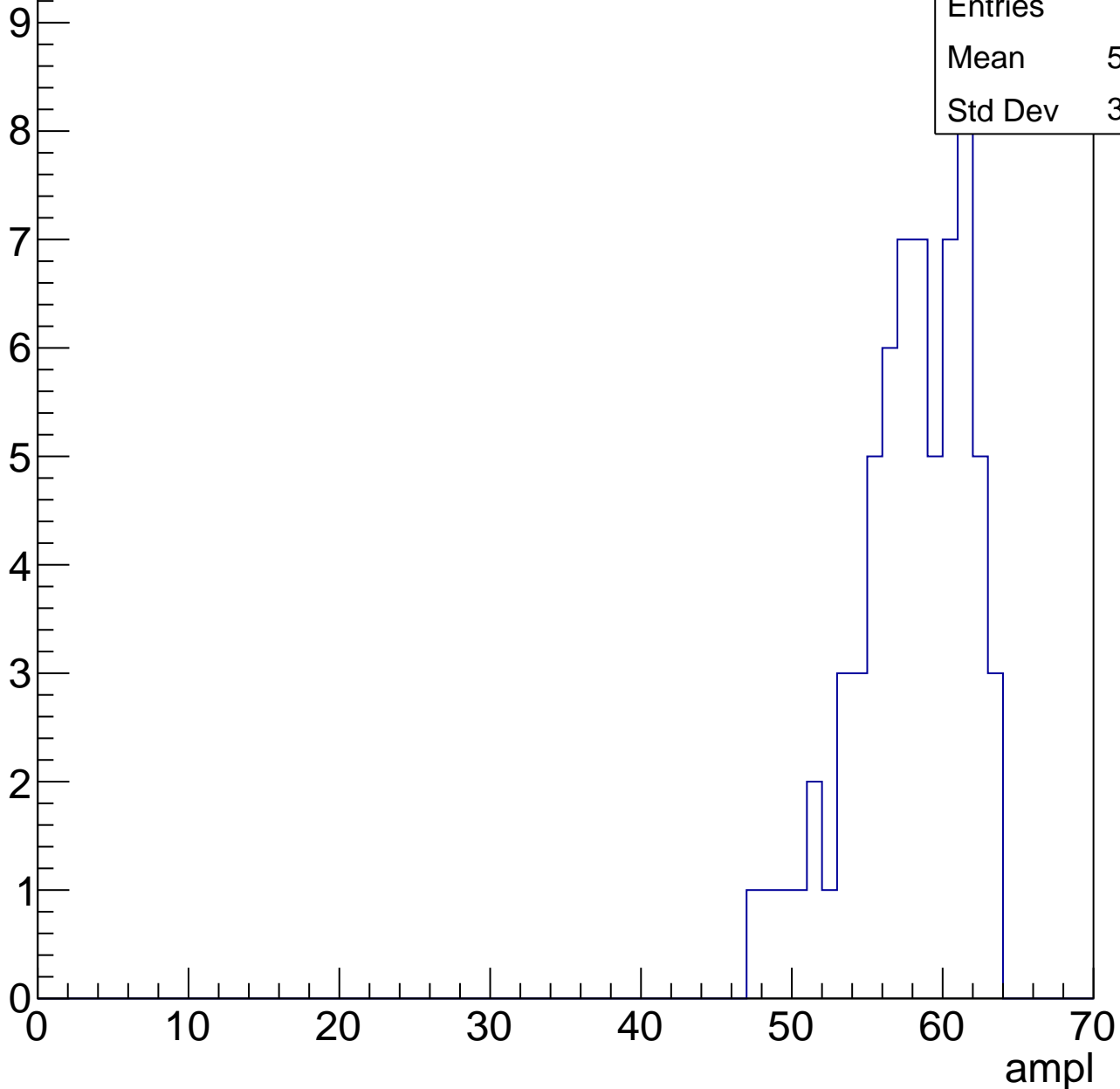


# B0L102S, U6-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

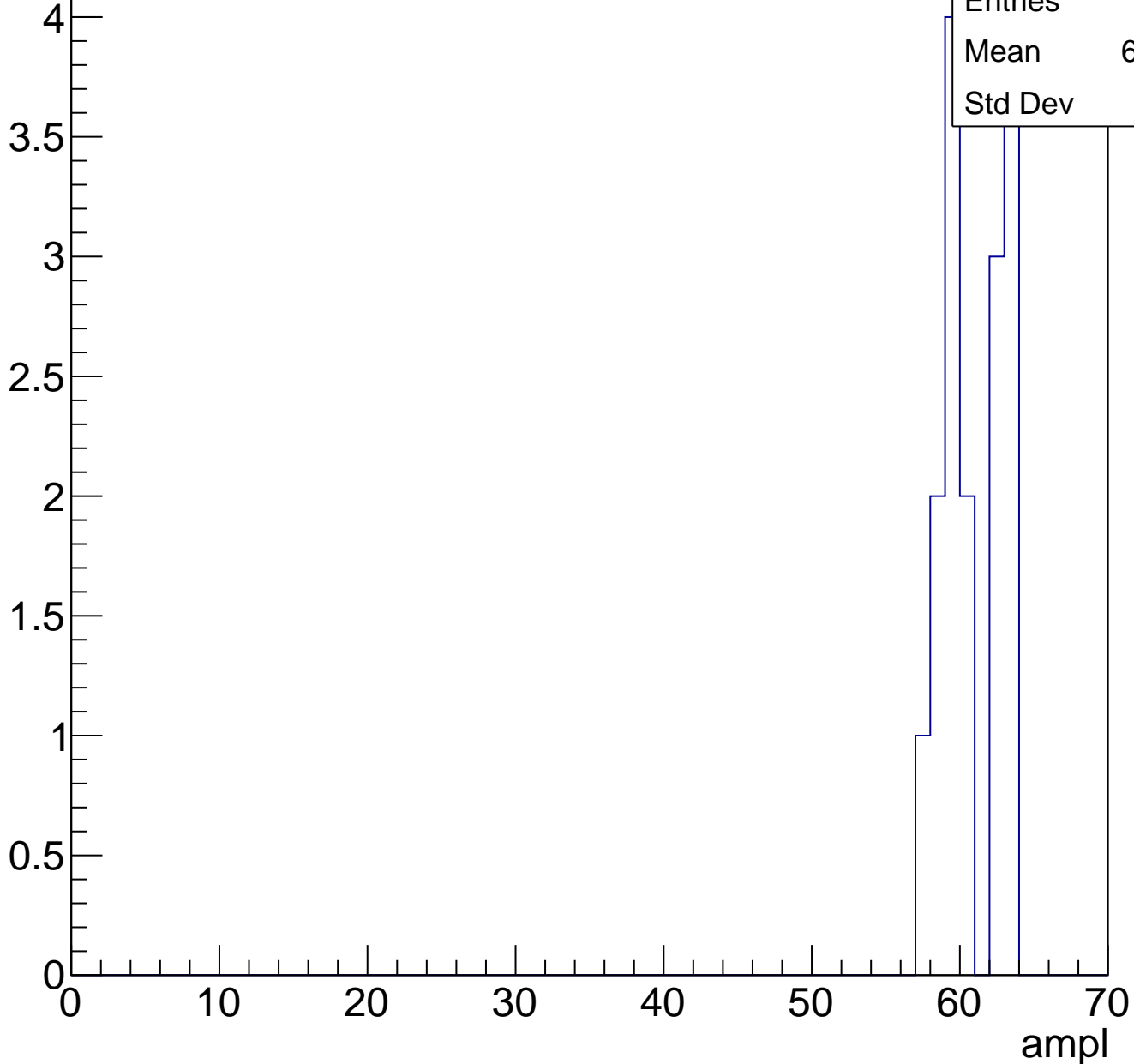
Entries	67
Mean	57.43
Std Dev	3.758



# B0L102S, U6-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



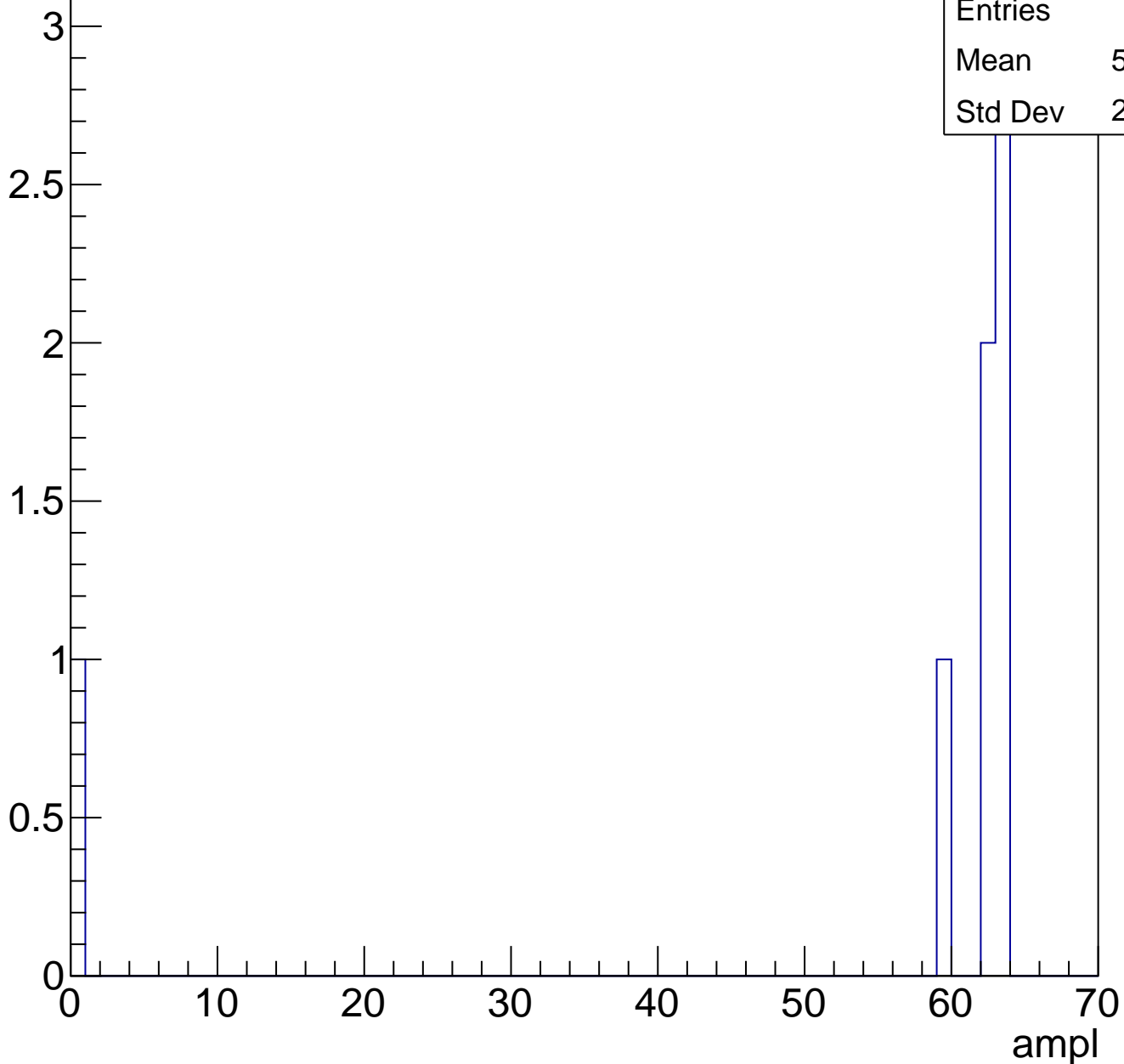
Entries	16
Mean	60.44
Std Dev	2.03



# B0L102S, U6-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch105, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	85
Mean	28.45
Std Dev	3.638

**Gaus mean : 28.7241**

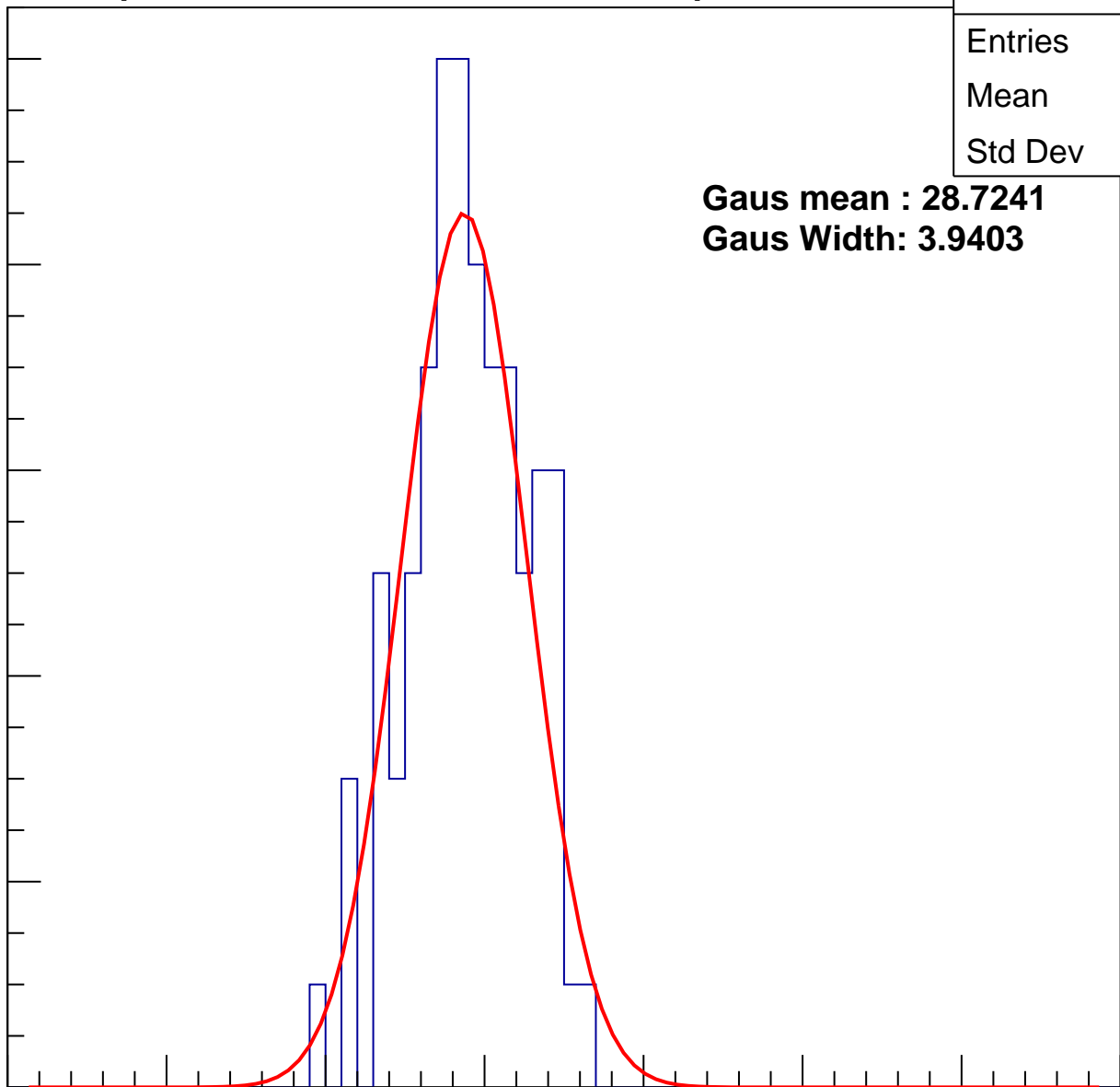
**Gaus Width: 3.9403**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch105, adc1

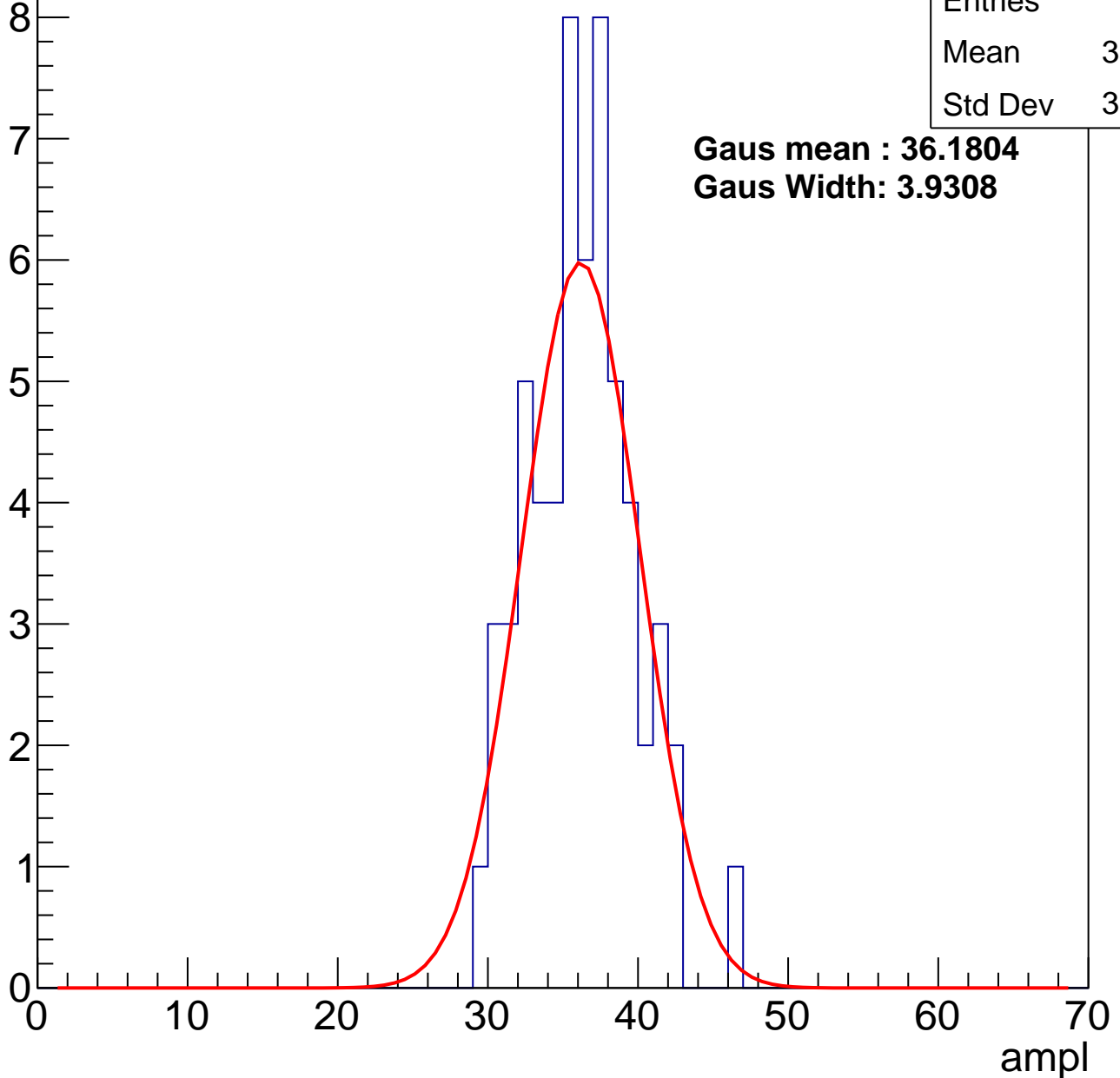
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	35.78
Std Dev	3.474

**Gaus mean : 36.1804**

**Gaus Width: 3.9308**

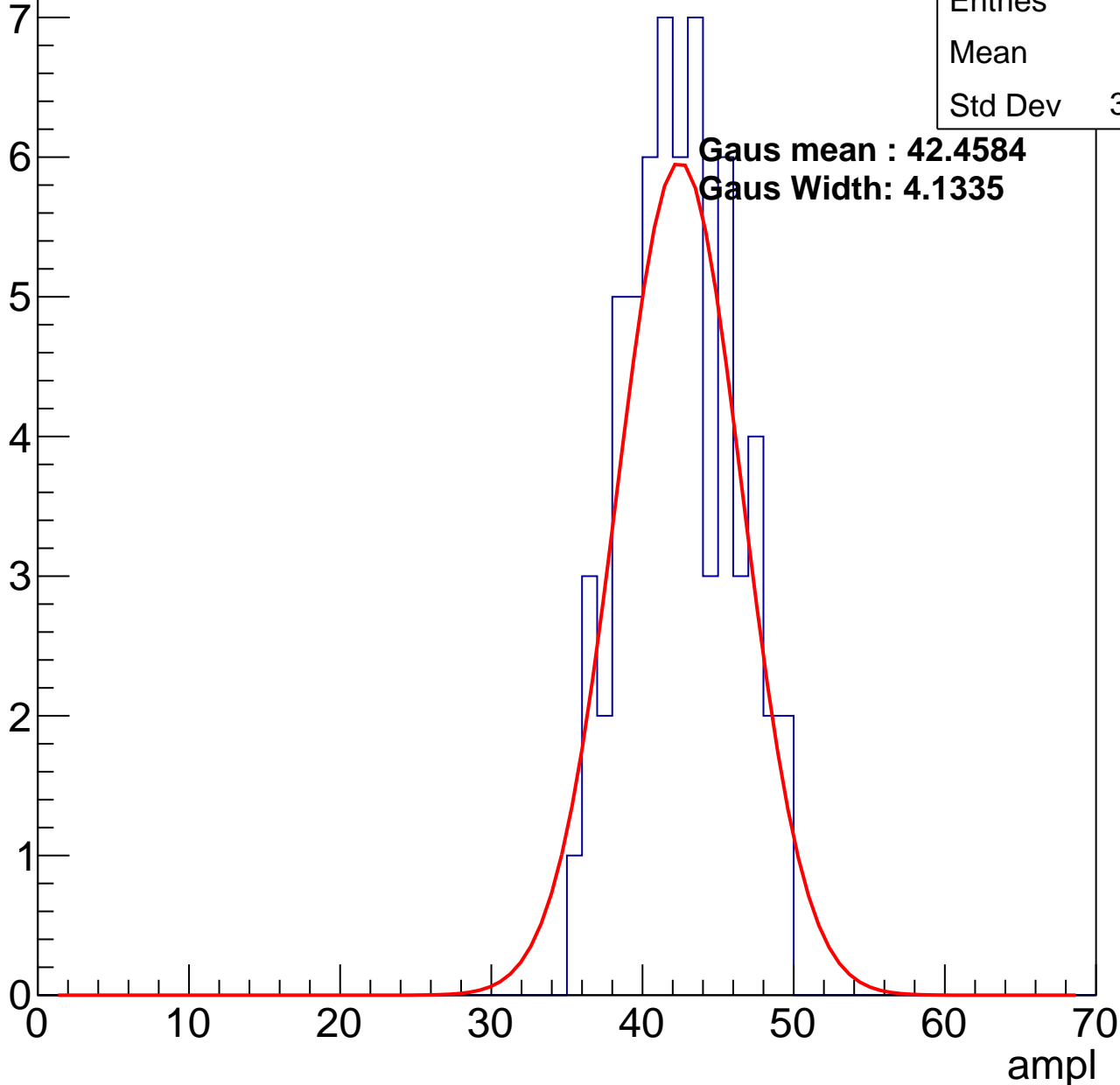


# B0L102S, U6-ch105, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42
Std Dev	3.487

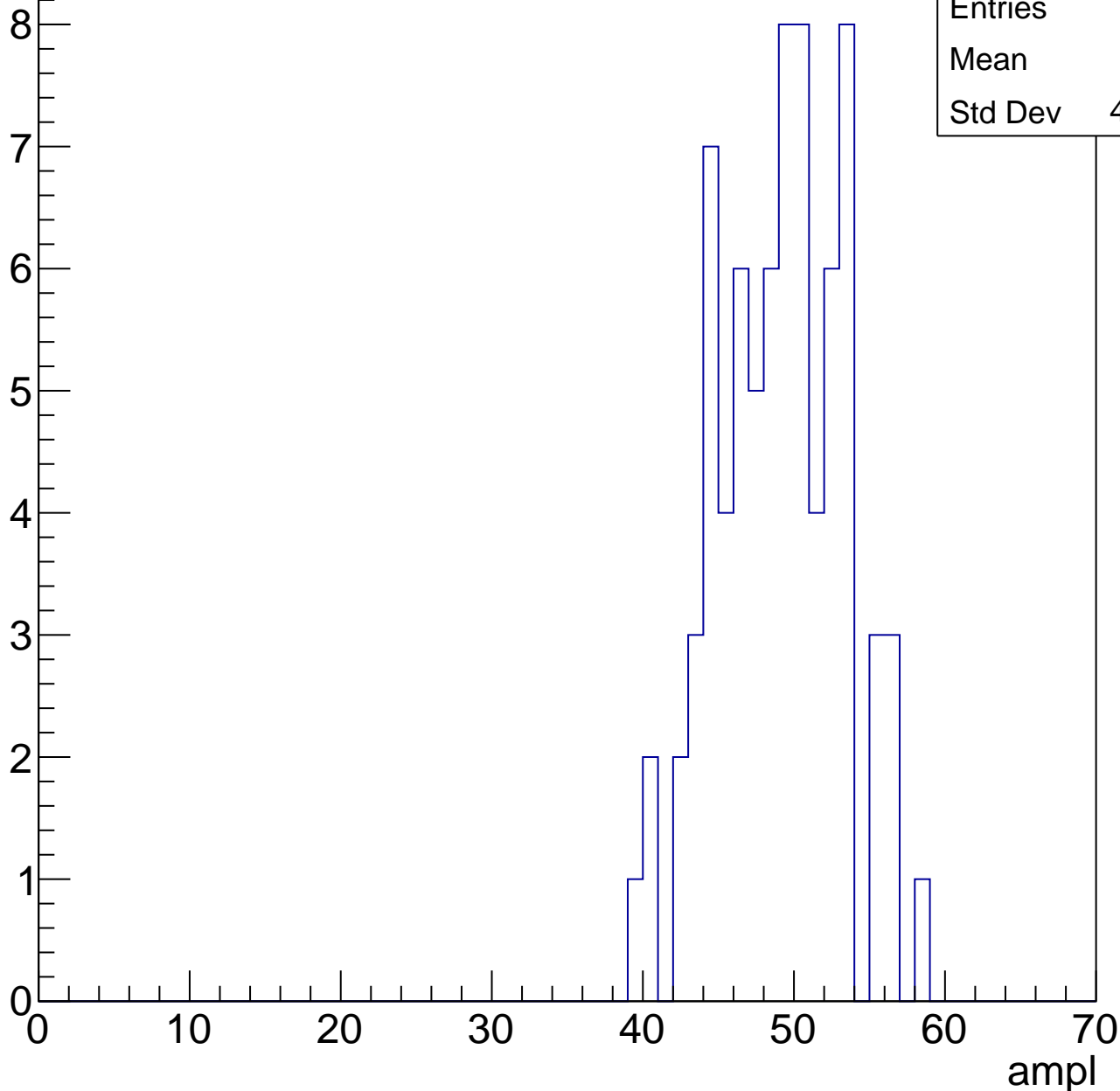


# B0L102S, U6-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	48.6
Std Dev	4.135

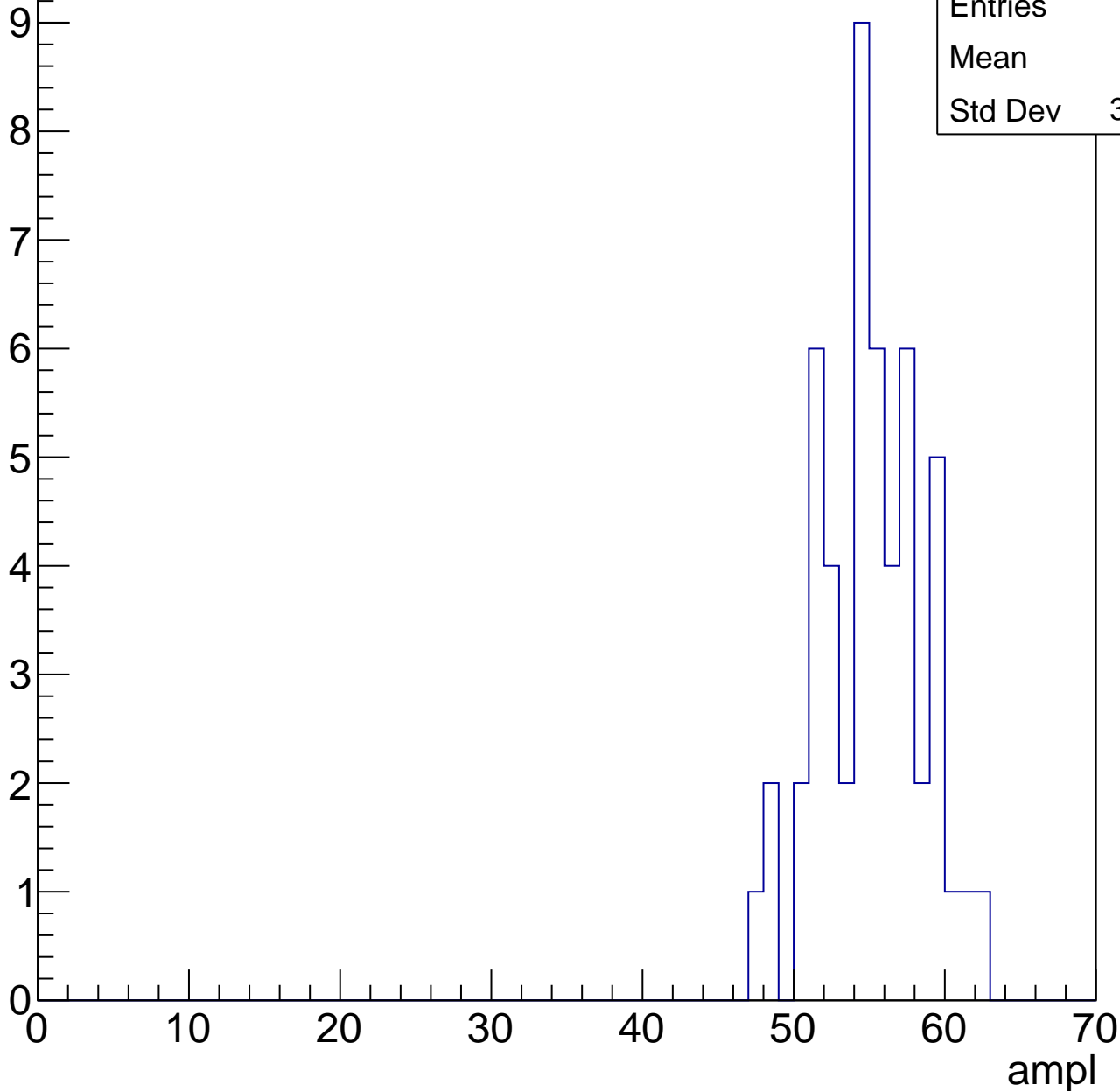


# B0L102S, U6-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	54.6
Std Dev	3.364



# B0L102S, U6-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	58.73
Std Dev	8.547

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

# B0L102S, U6-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

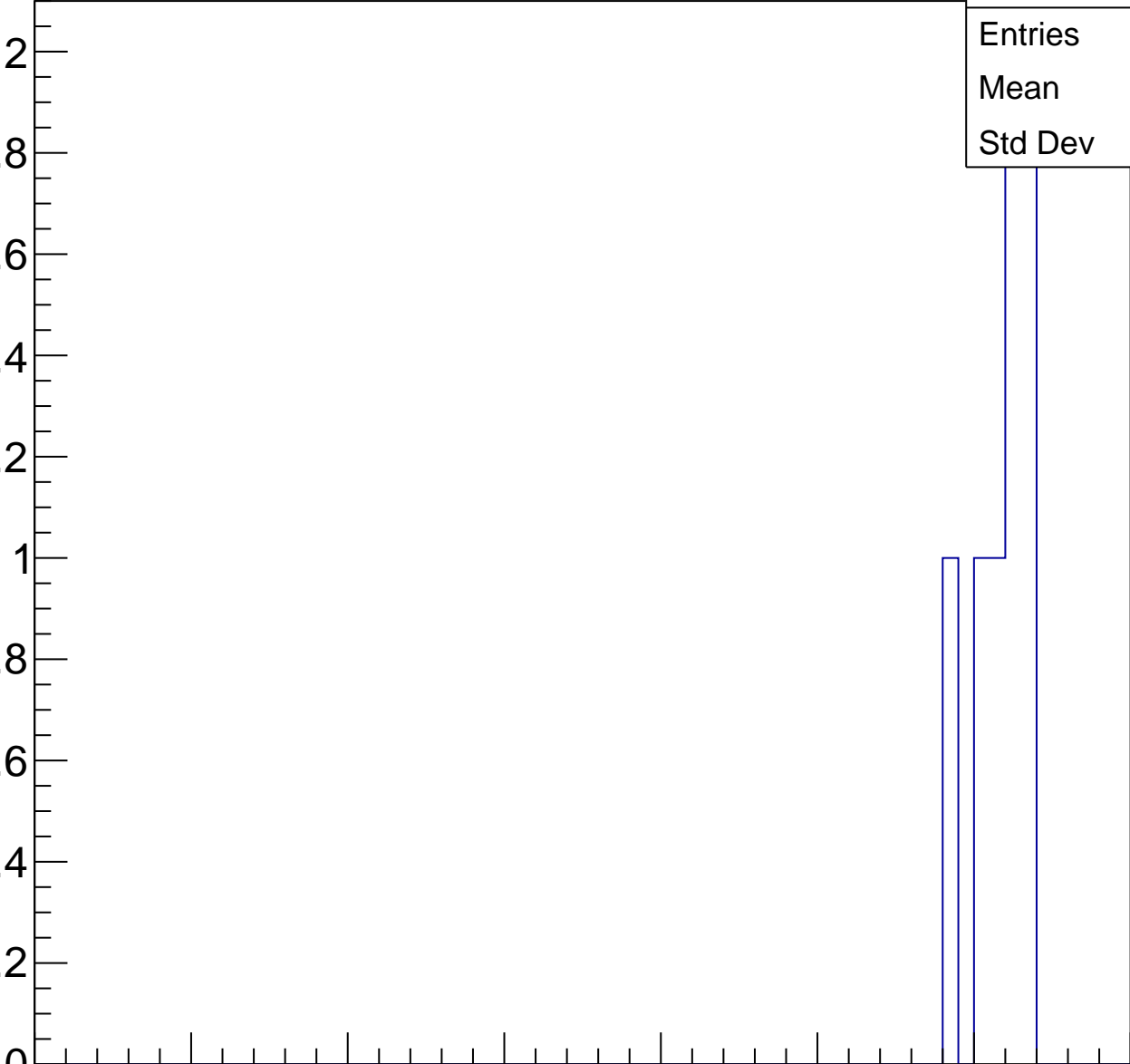
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.29
Std Dev	1.666

ampl

0 10 20 30 40 50 60 70





# B0L102S, U6-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch106, adc0

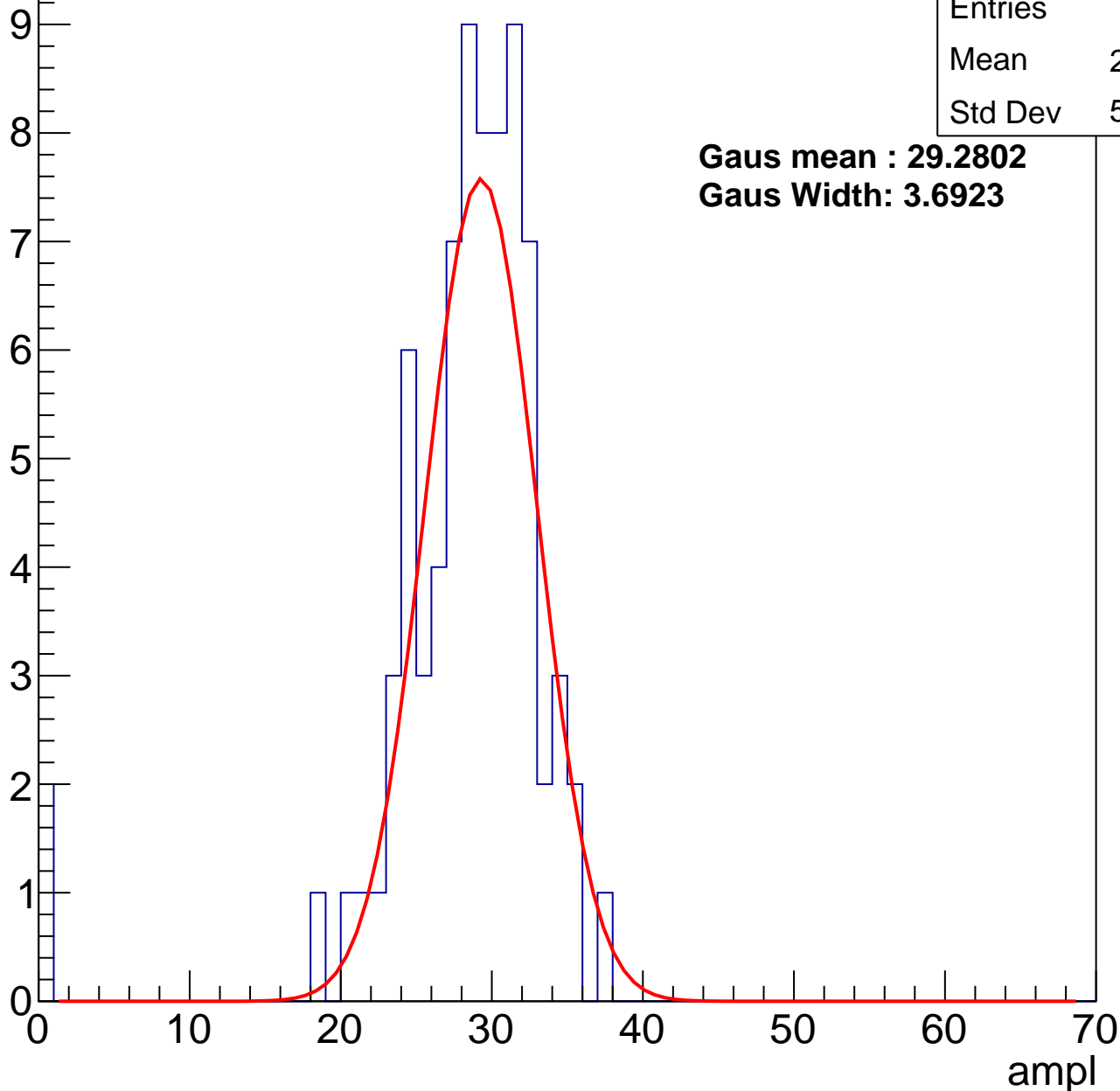
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	27.74
Std Dev	5.777

**Gaus mean : 29.2802**

**Gaus Width: 3.6923**



# B0L102S, U6-ch106, adc1

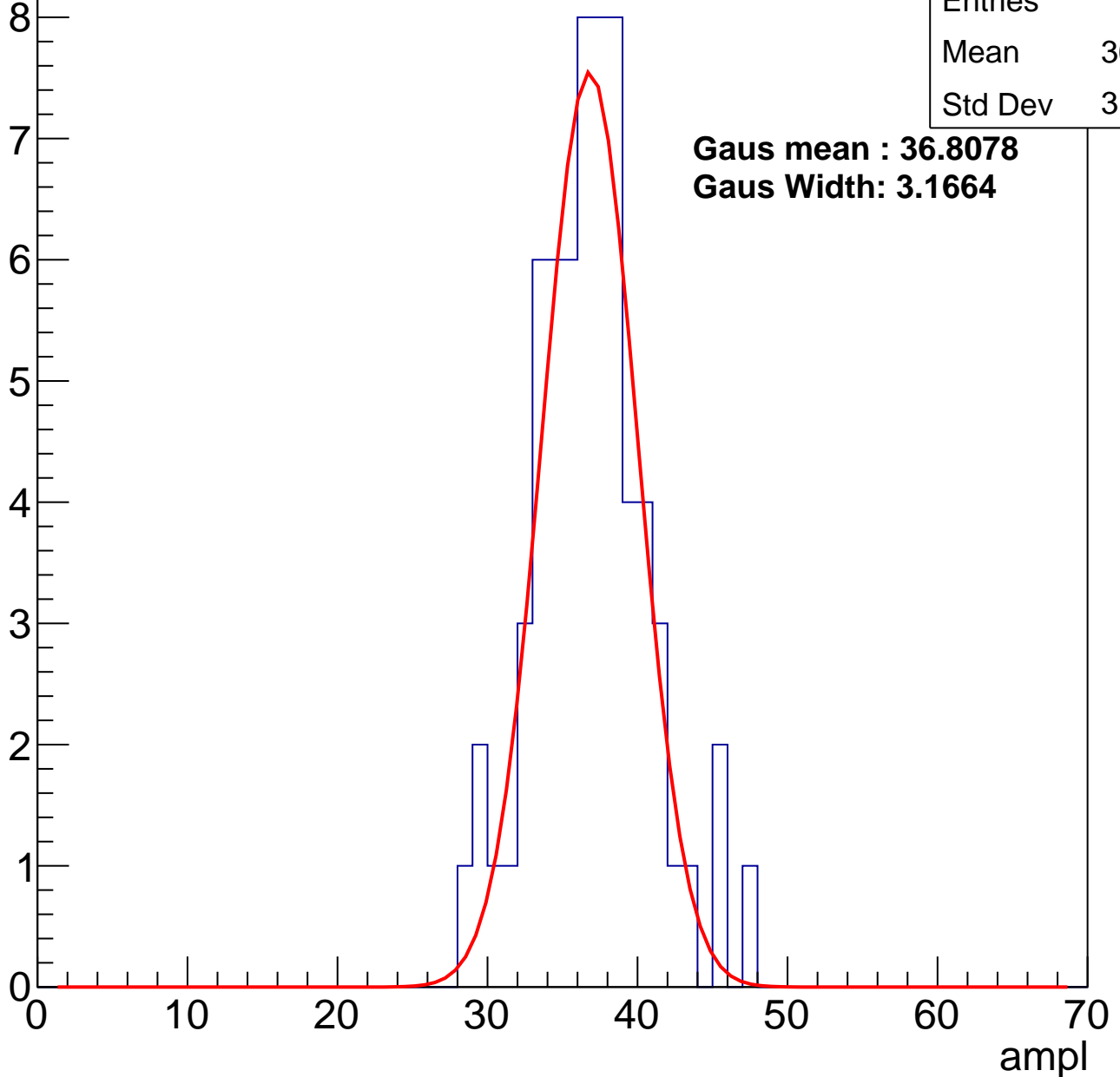
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	36.42
Std Dev	3.746

**Gaus mean : 36.8078**

**Gaus Width: 3.1664**



# B0L102S, U6-ch106, adc2

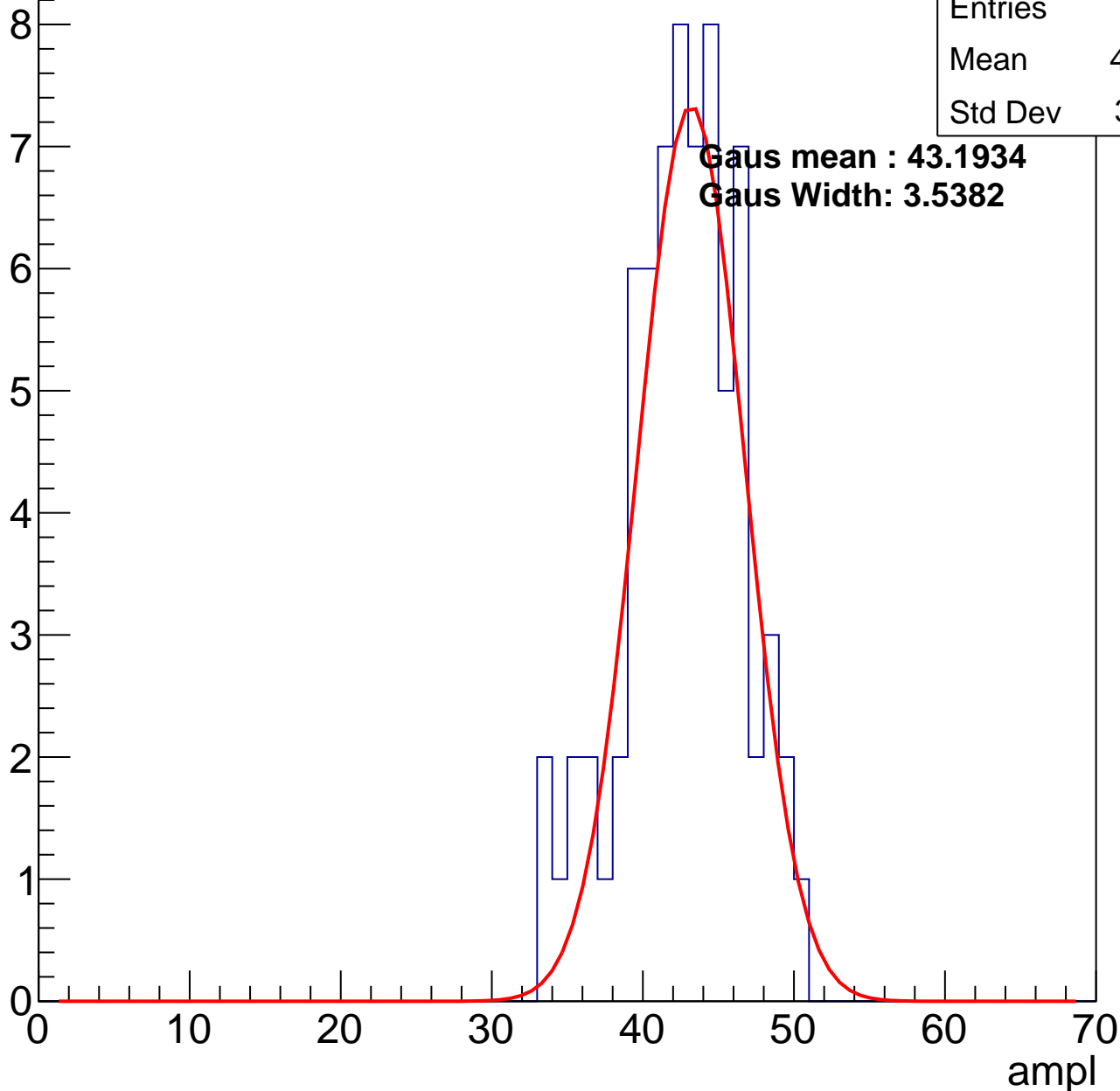
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	42.19
Std Dev	3.821

**Gaus mean : 43.1934**

**Gaus Width: 3.5382**

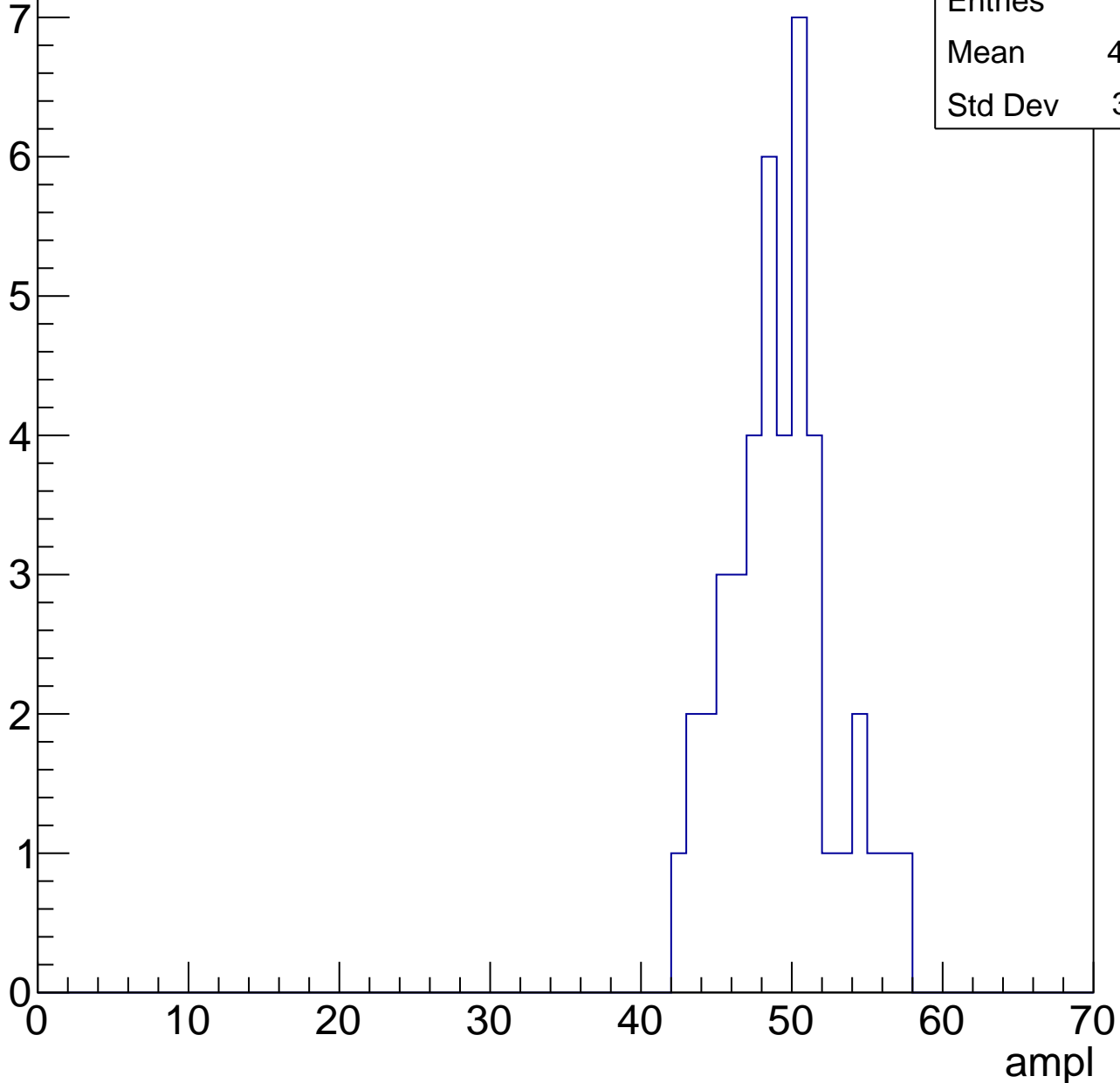


# B0L102S, U6-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	43
Mean	48.74
Std Dev	3.451

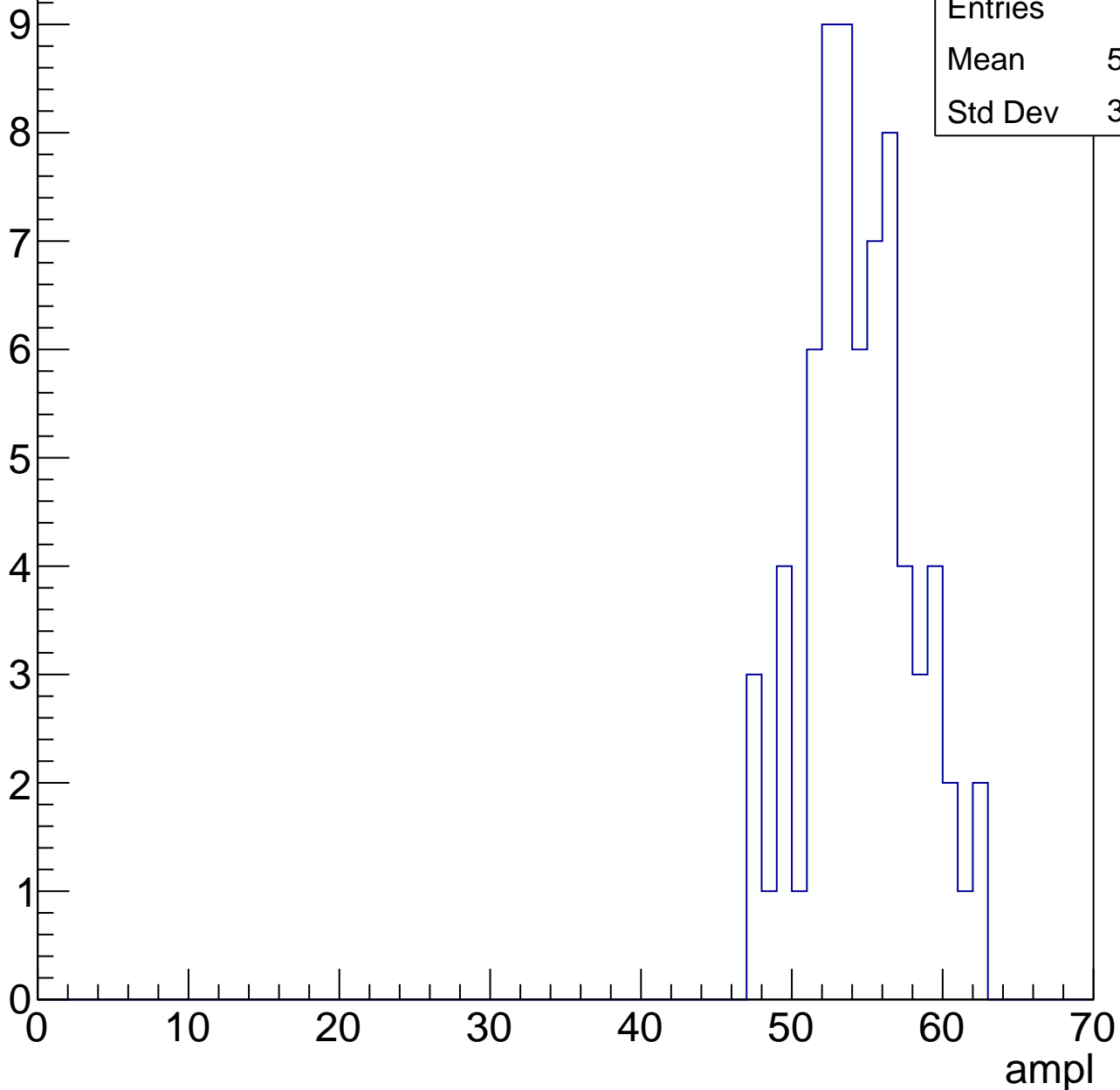


# B0L102S, U6-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	54.09
Std Dev	3.524

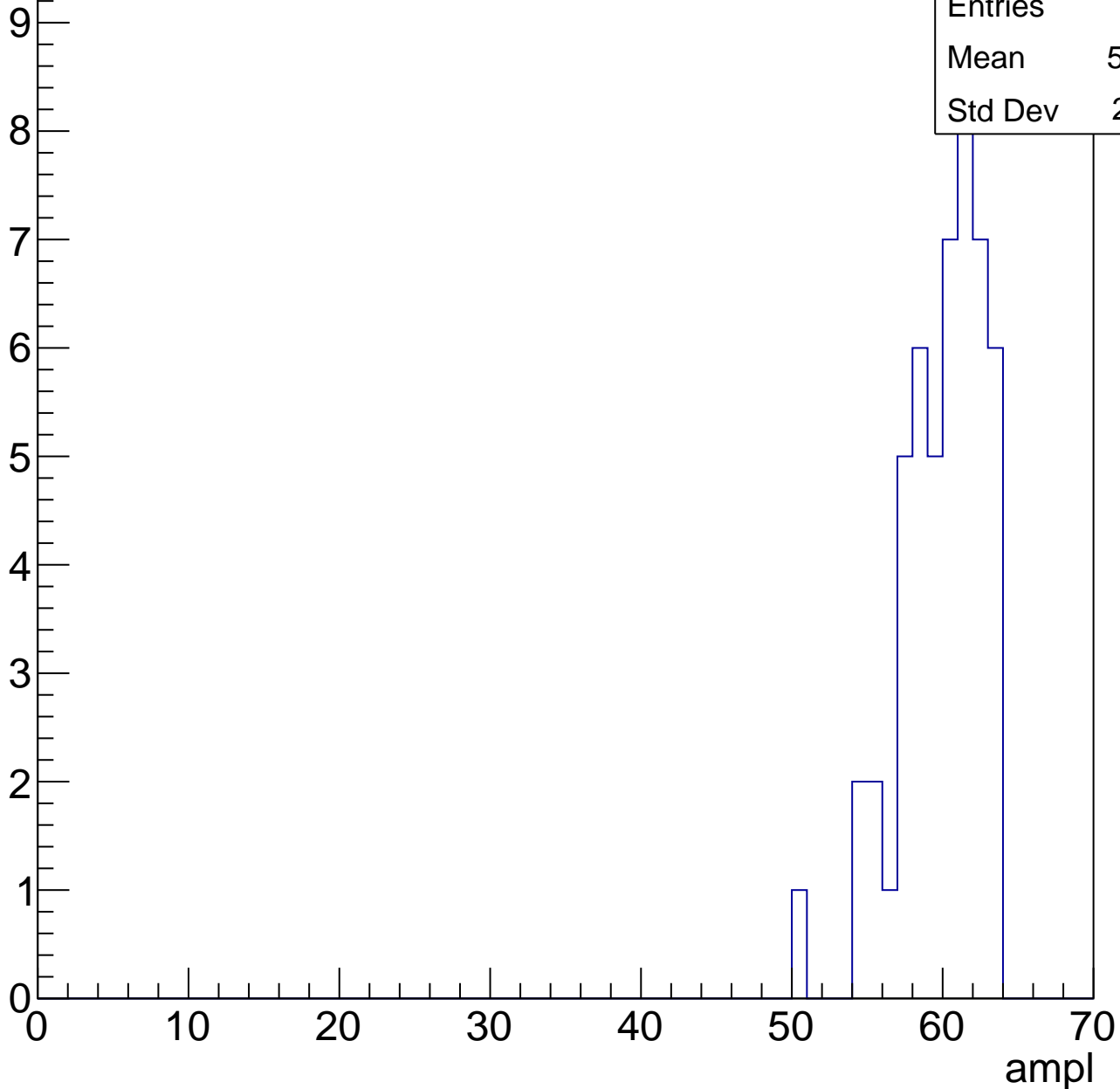


# B0L102S, U6-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

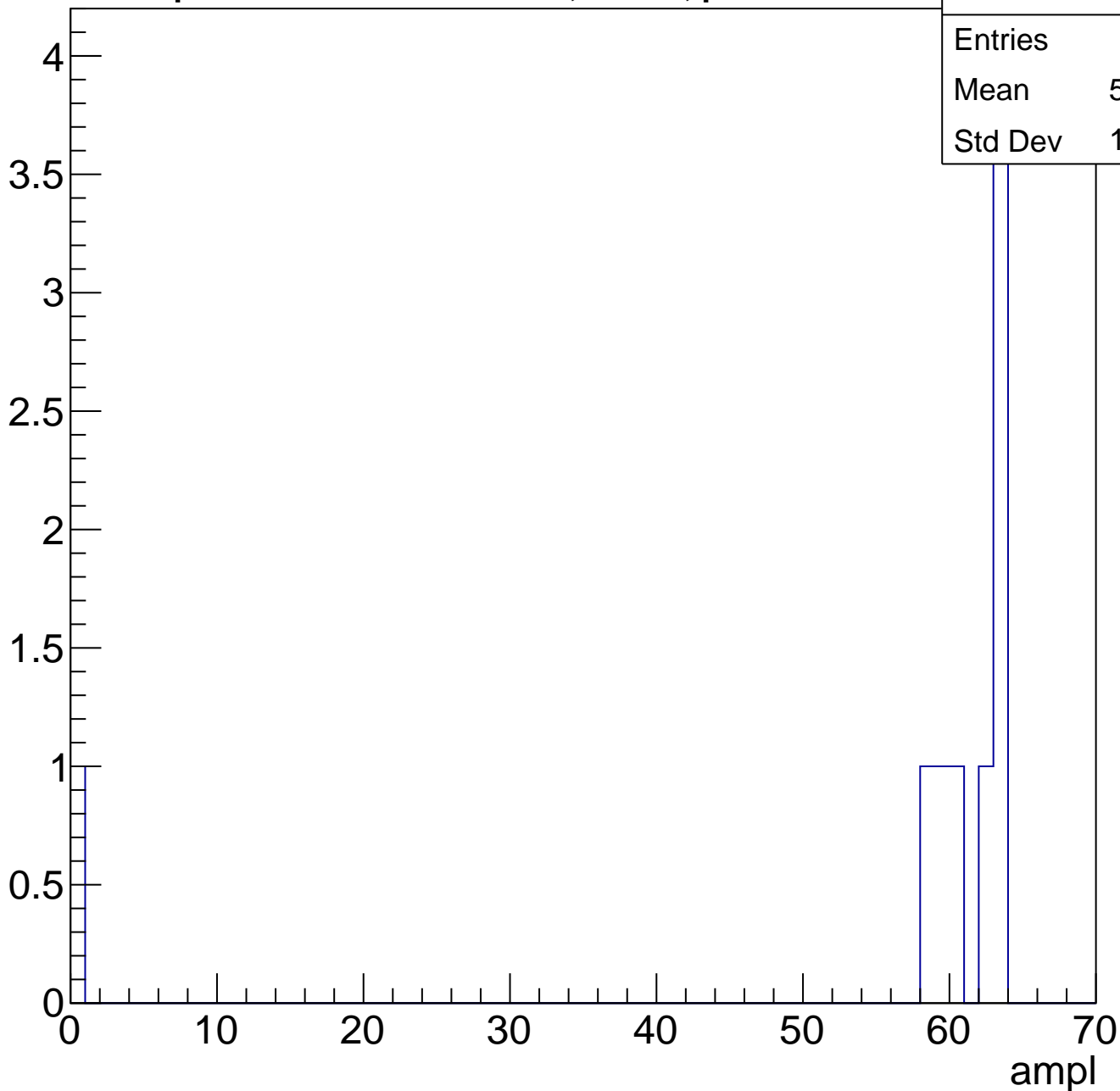
Entries	51
Mean	59.47
Std Dev	2.761



# B0L102S, U6-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U6-ch107, adc0

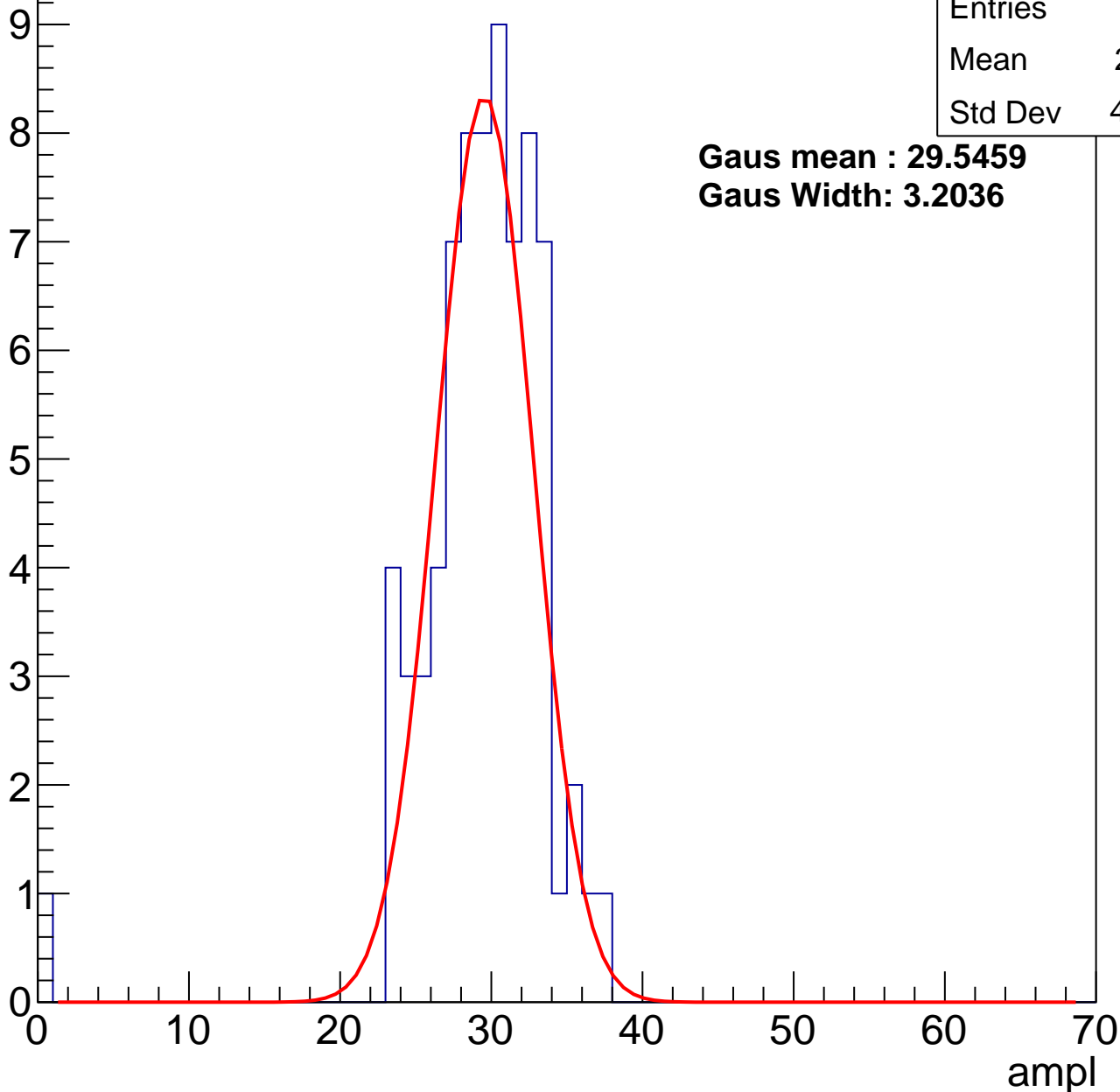
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	28.91
Std Dev	4.665

**Gaus mean : 29.5459**

**Gaus Width: 3.2036**



# B0L102S, U6-ch107, adc1

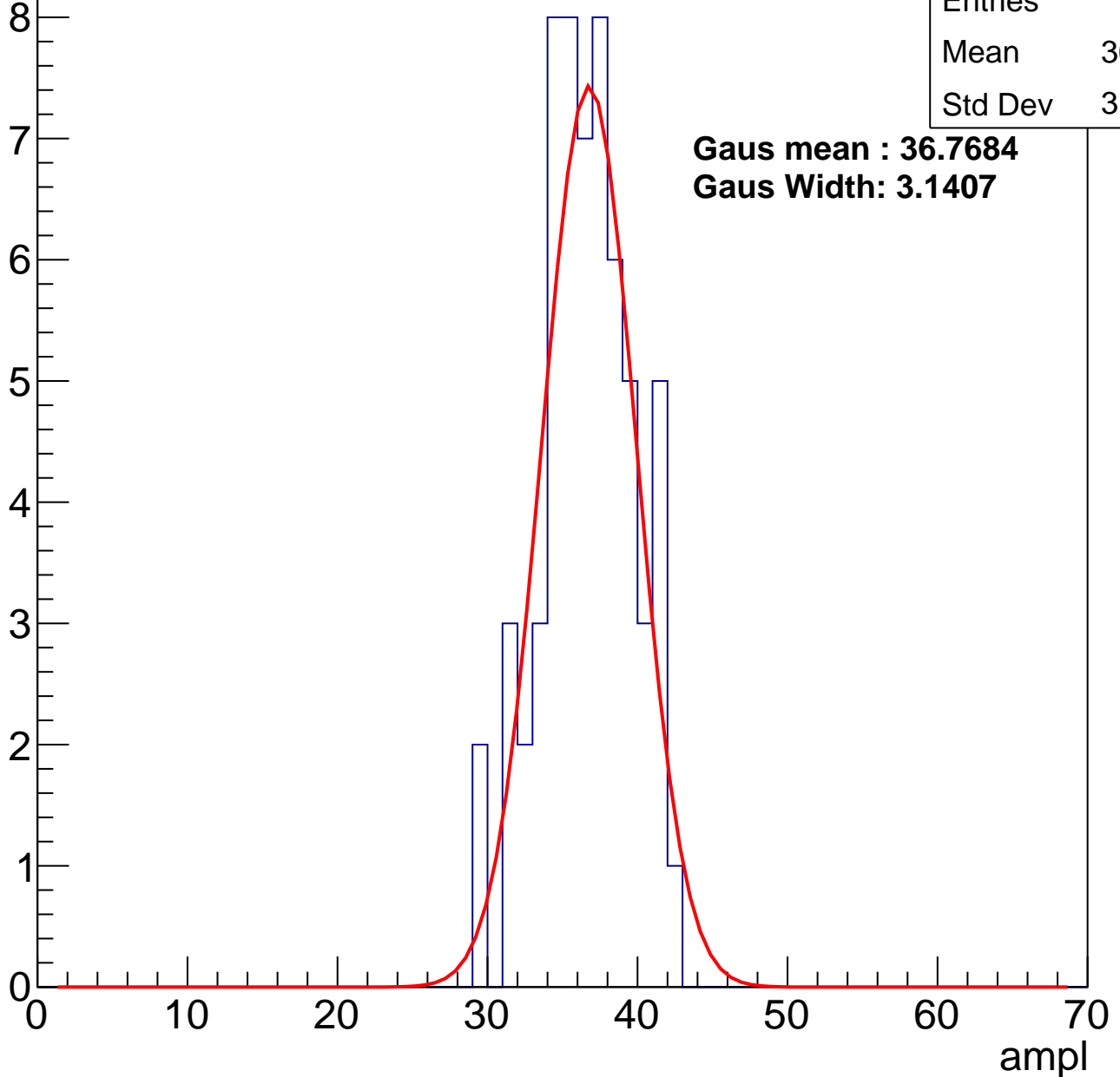
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	36.13
Std Dev	3.022

**Gaus mean : 36.7684**

**Gaus Width: 3.1407**



# B0L102S, U6-ch107, adc2

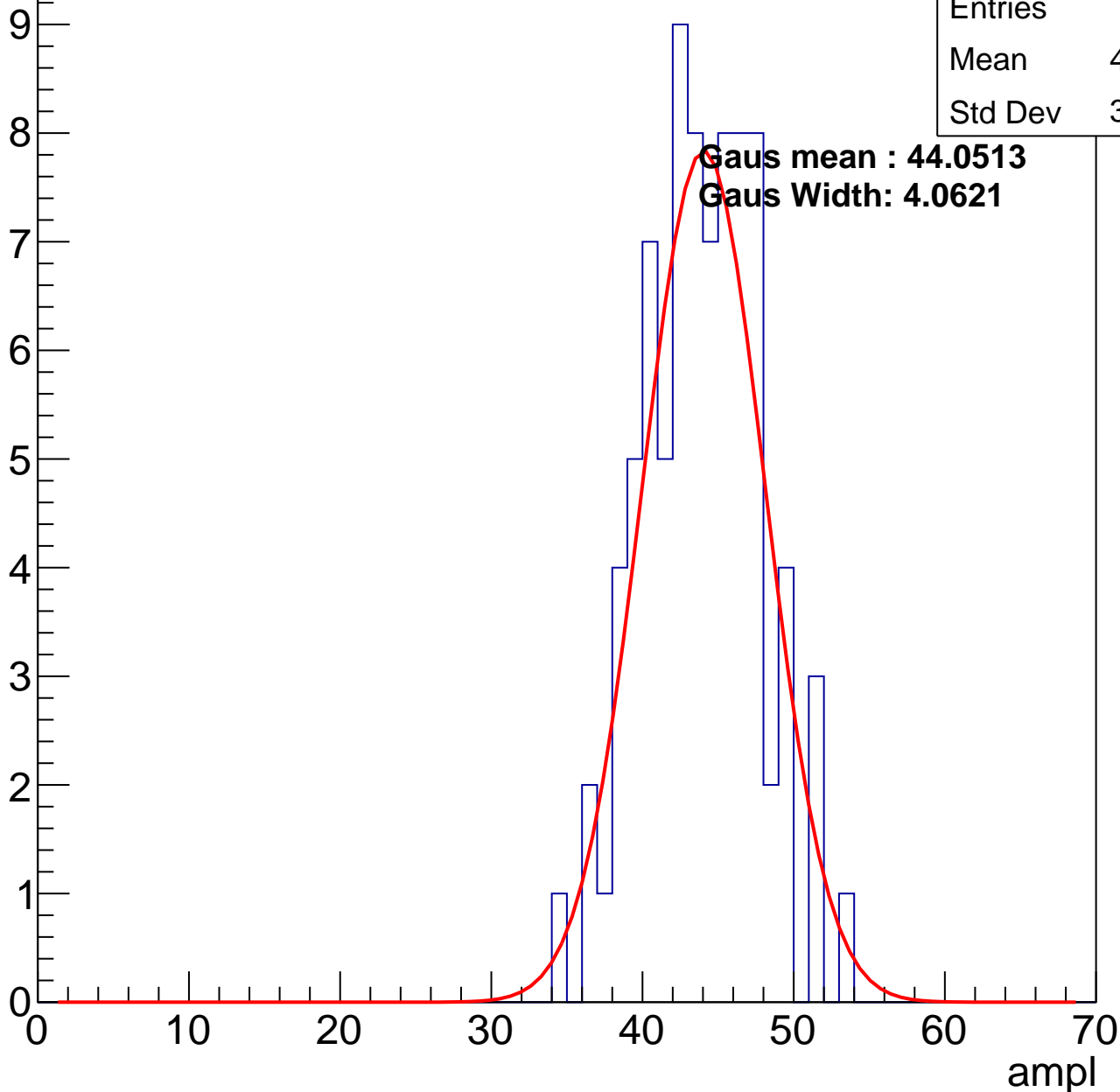
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	83
Mean	43.46
Std Dev	3.794

**Gaus mean : 44.0513**

**Gaus Width: 4.0621**

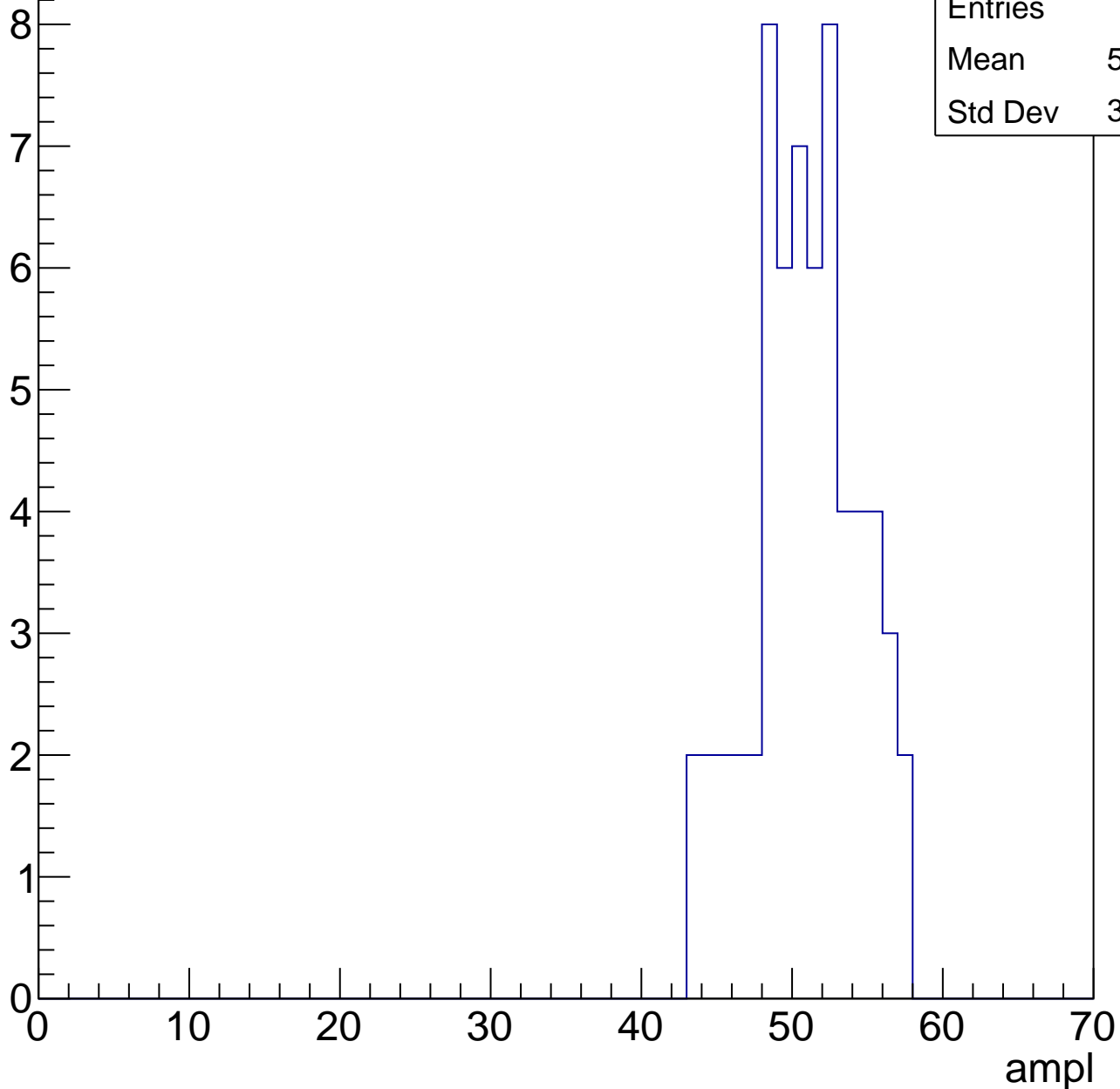


# B0L102S, U6-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	50.48
Std Dev	3.449

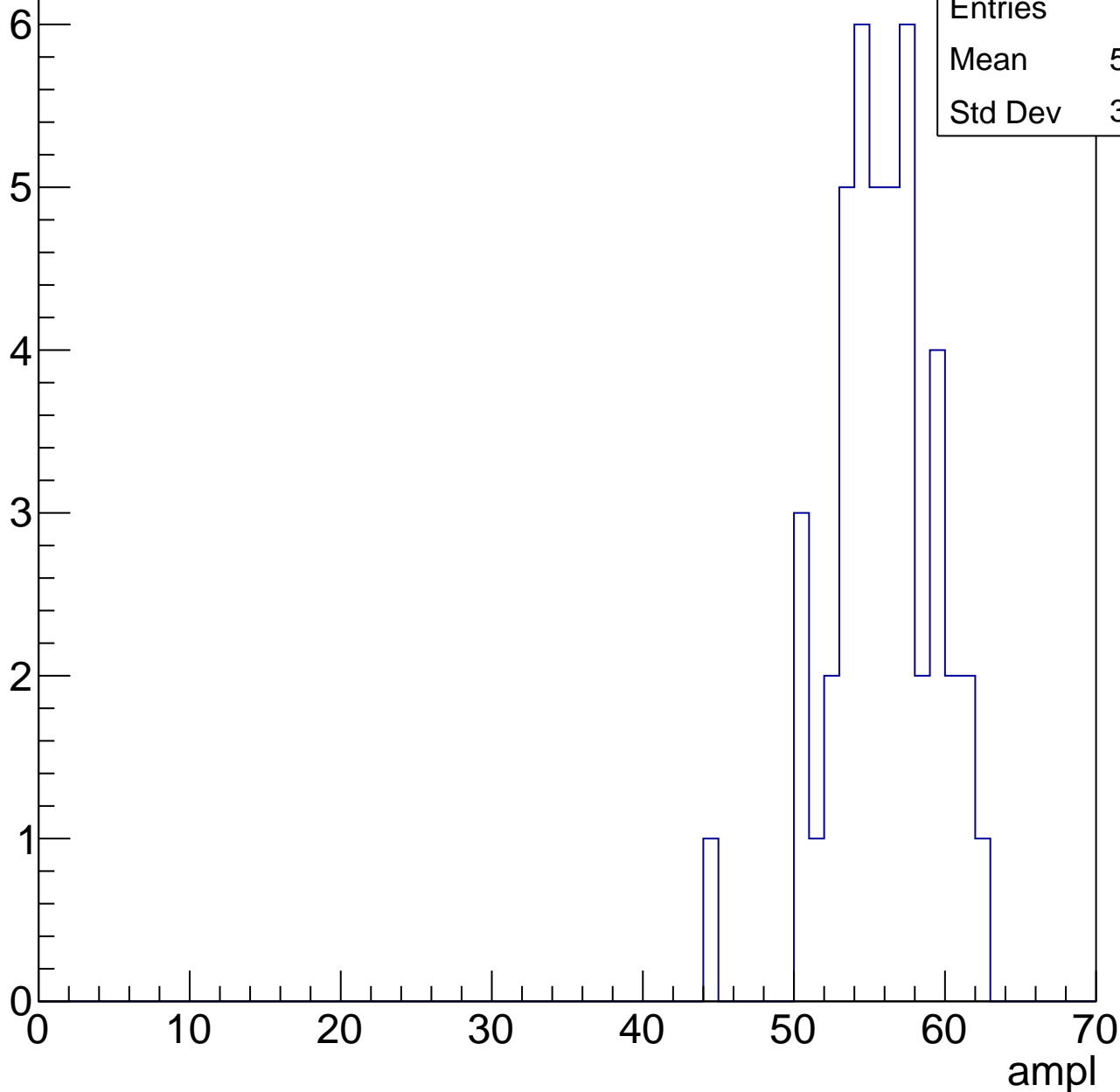


# B0L102S, U6-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	55.36
Std Dev	3.439

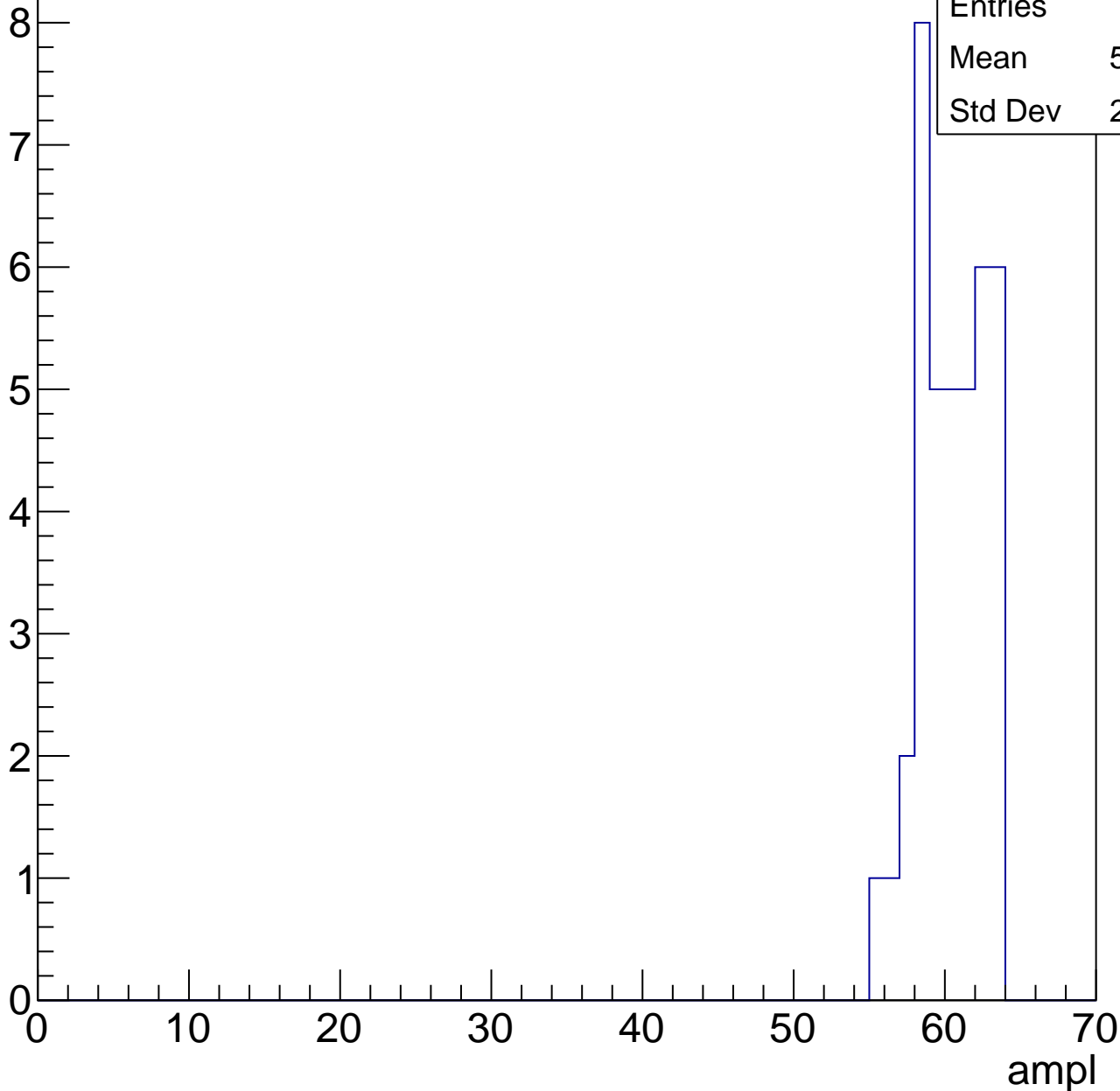


# B0L102S, U6-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

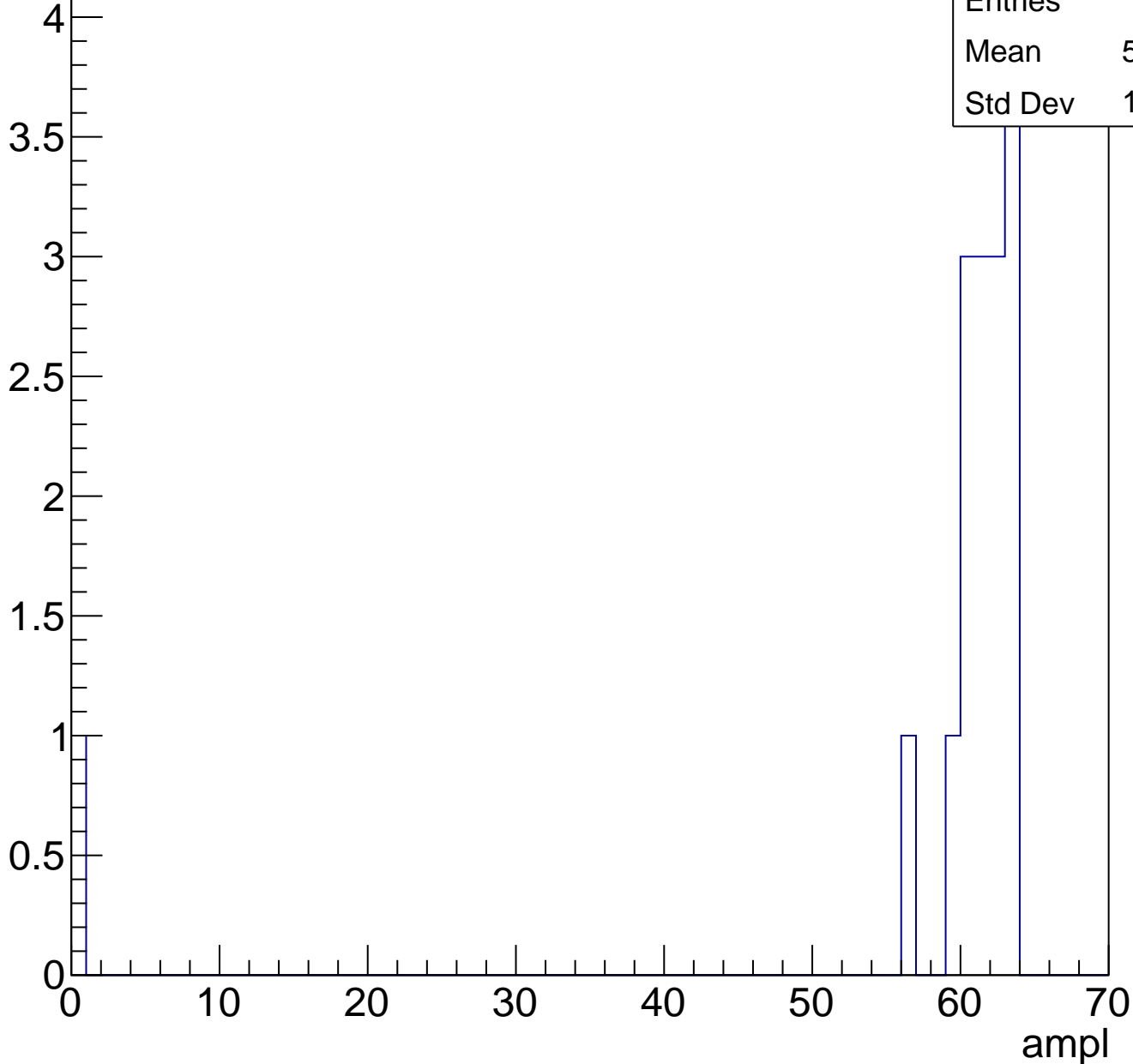
Entries	39
Mean	59.97
Std Dev	2.142



# B0L102S, U6-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch108, adc0

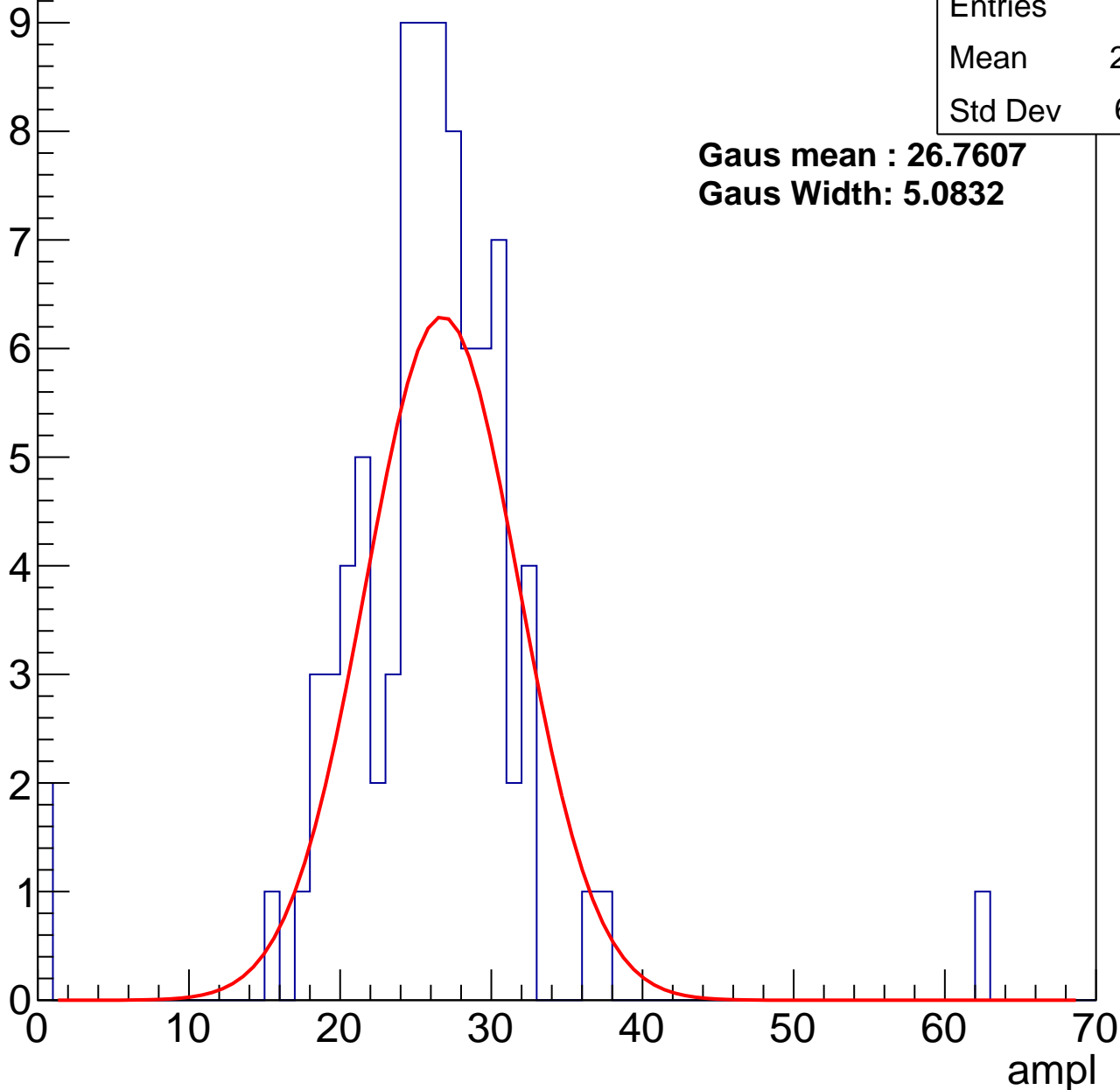
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	87
Mean	25.39
Std Dev	6.901

**Gaus mean : 26.7607**

**Gaus Width: 5.0832**



# B0L102S, U6-ch108, adc1

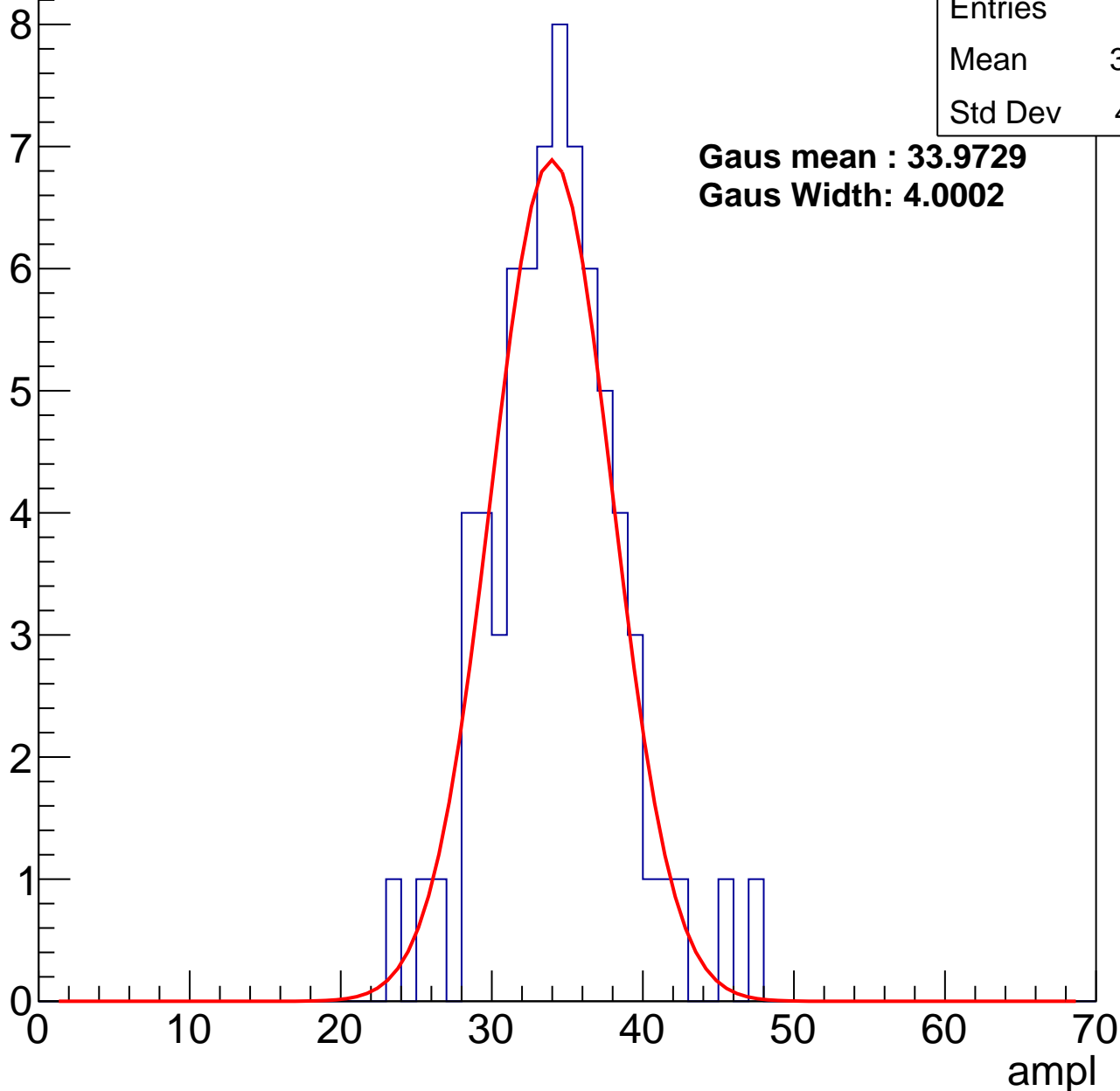
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	33.85
Std Dev	4.271

**Gaus mean : 33.9729**

**Gaus Width: 4.0002**



# B0L102S, U6-ch108, adc2

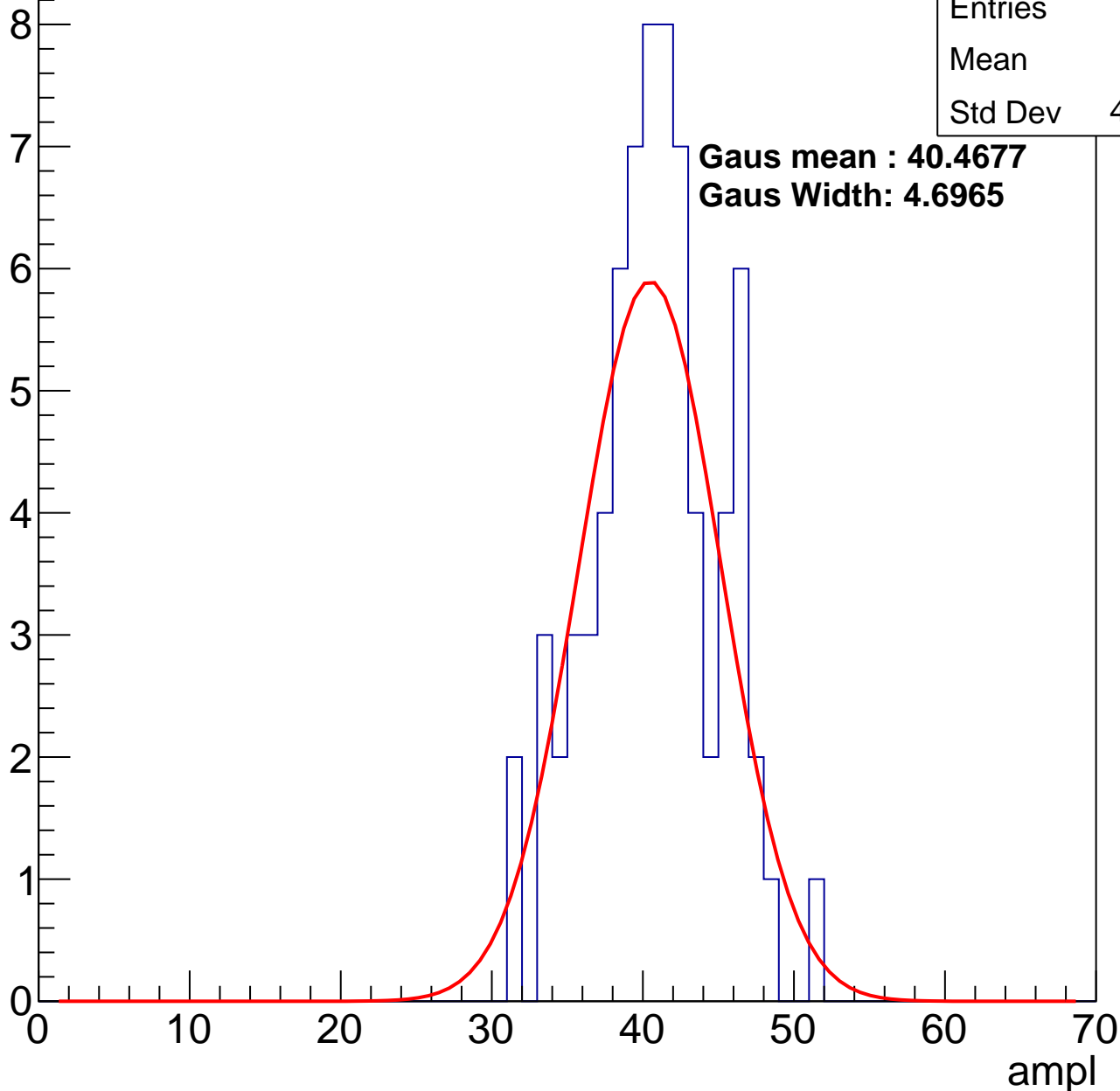
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	40.3
Std Dev	4.167

**Gaus mean : 40.4677**

**Gaus Width: 4.6965**

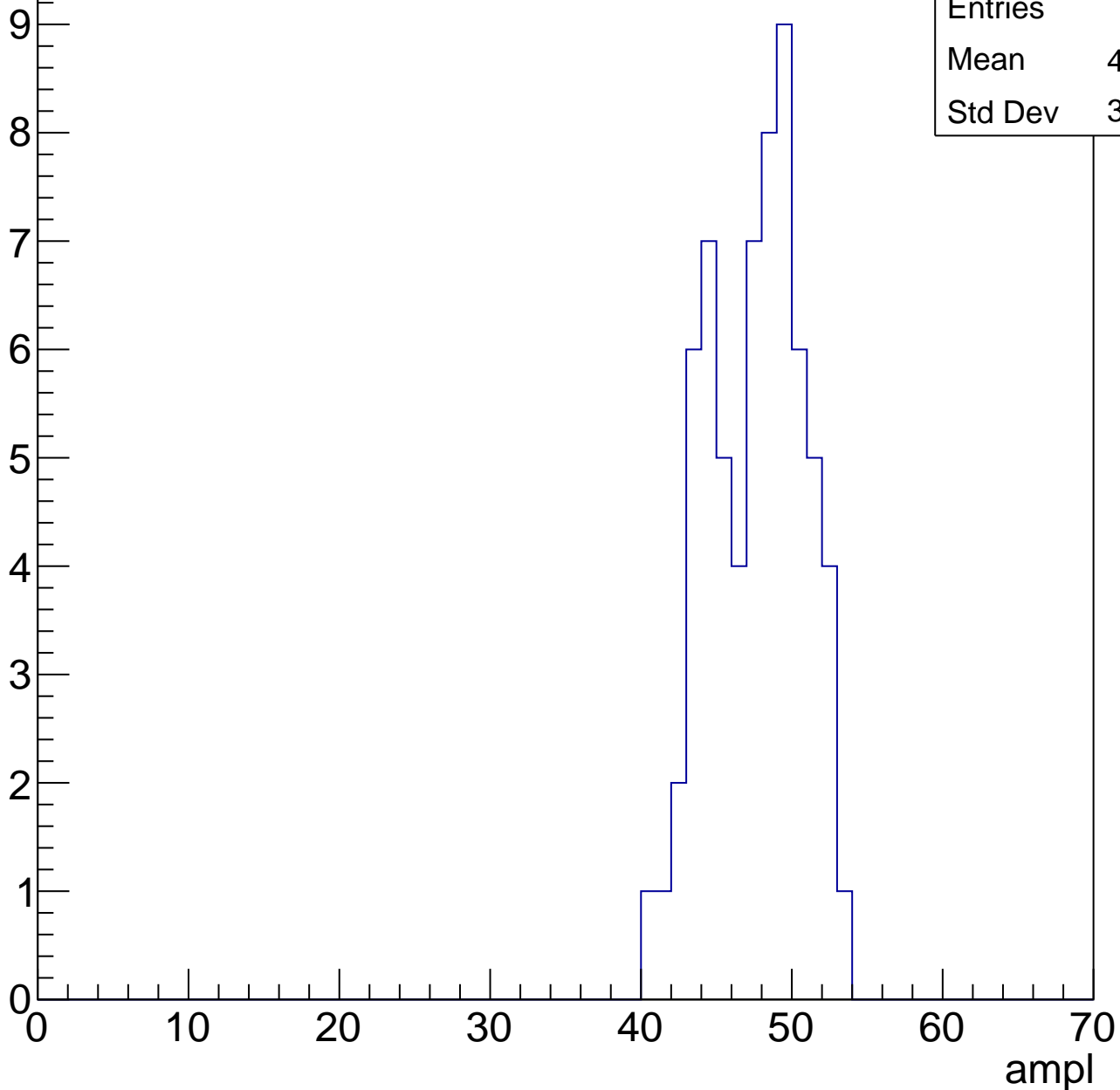


# B0L102S, U6-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	47.12
Std Dev	3.092

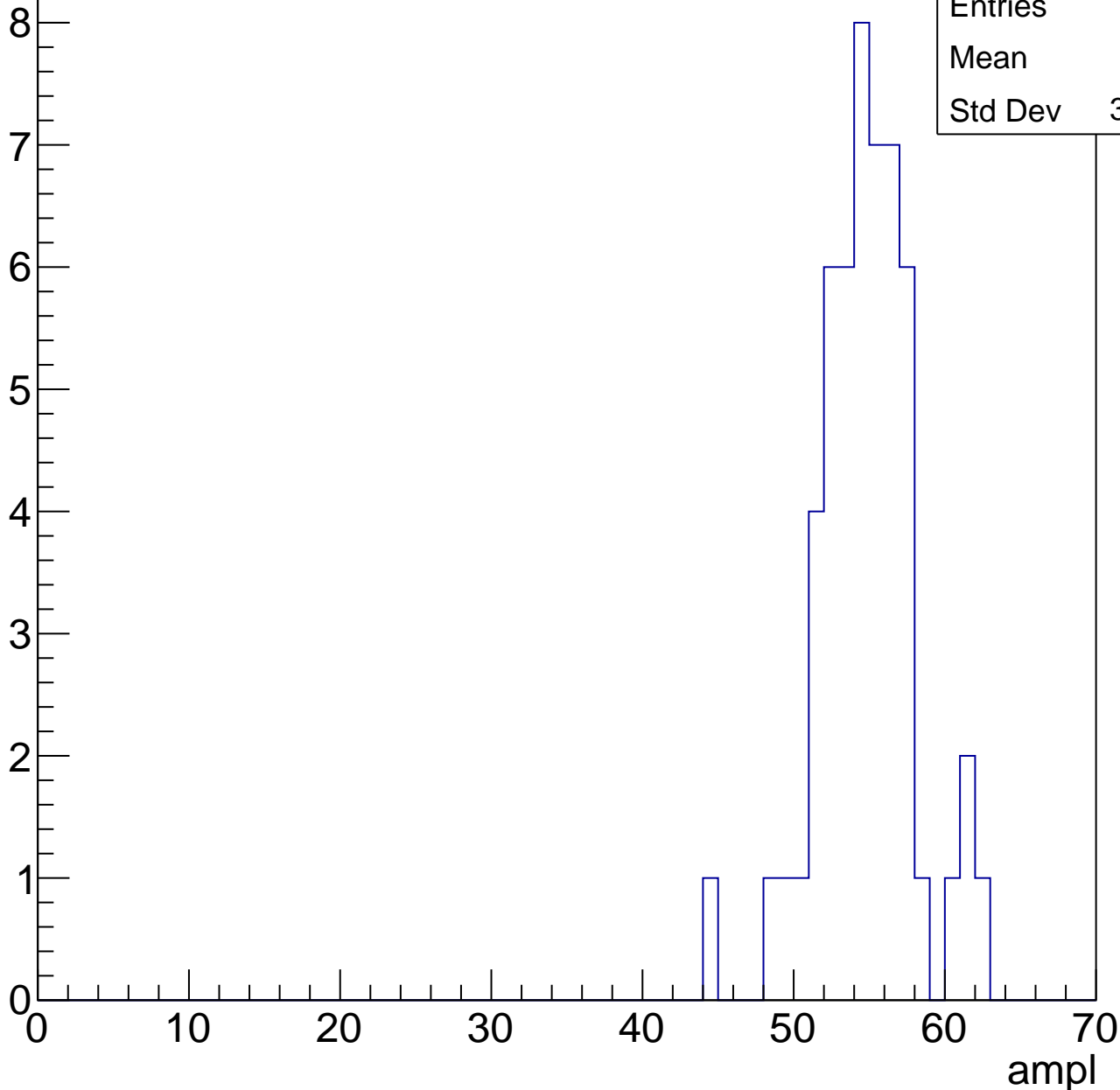


# B0L102S, U6-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	54.3
Std Dev	3.195

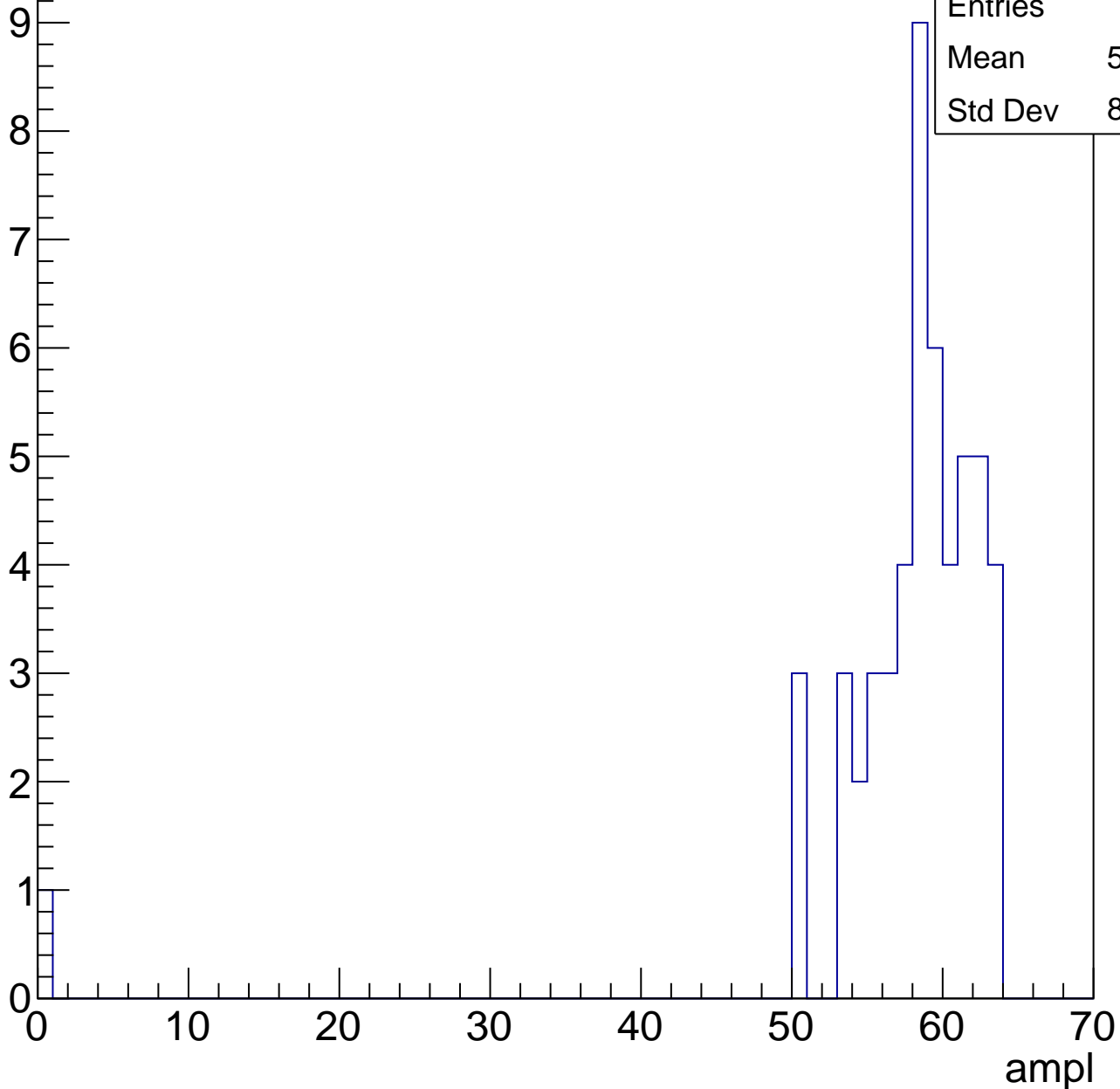


# B0L102S, U6-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	56.94
Std Dev	8.655

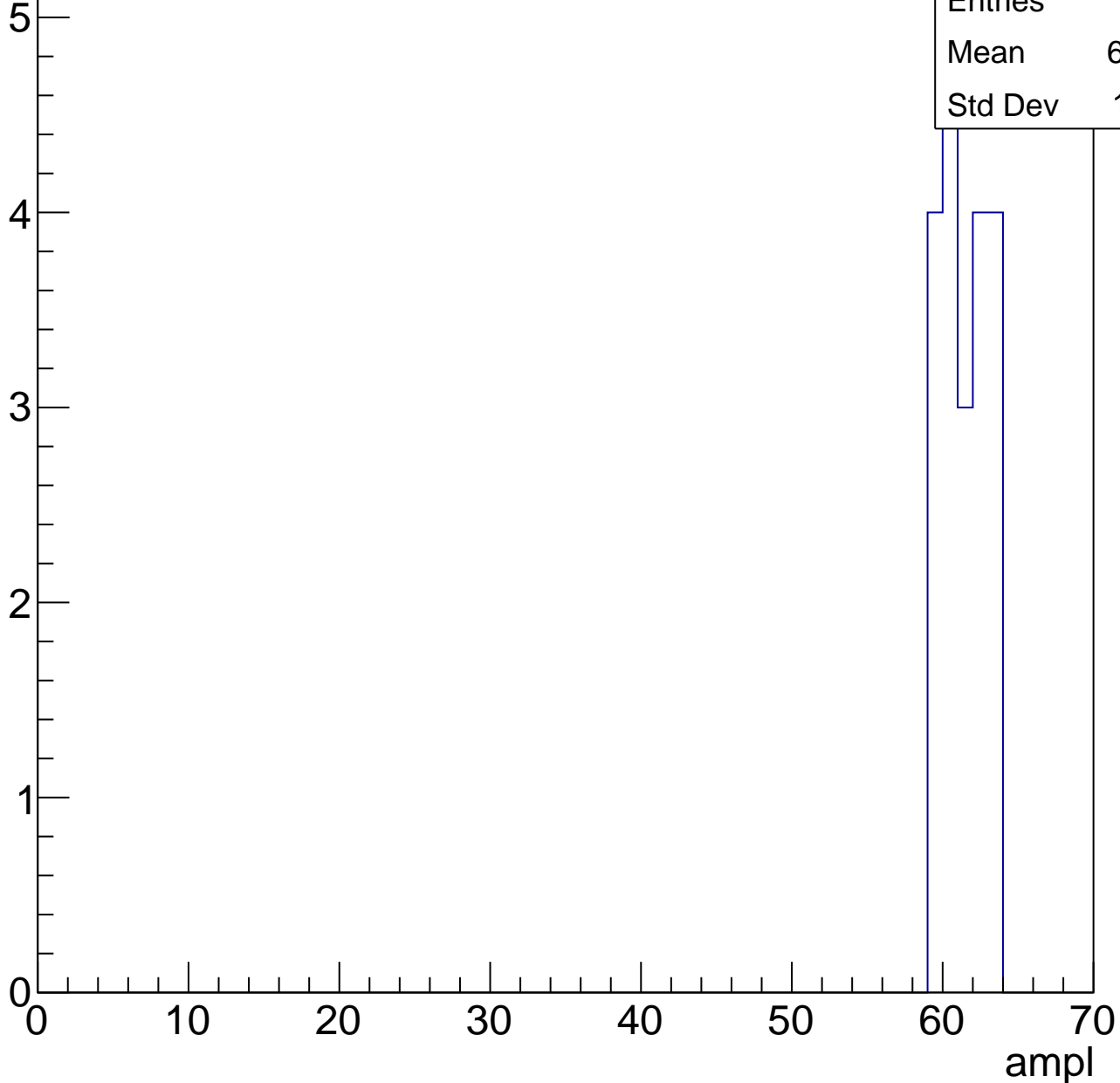


# B0L102S, U6-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	20
Mean	60.95
Std Dev	1.431





# B0L102S, U6-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	42
Std Dev	29.7

0 10 20 30 40 50 60 70

ampl

# B0L102S, U6-ch109, adc0

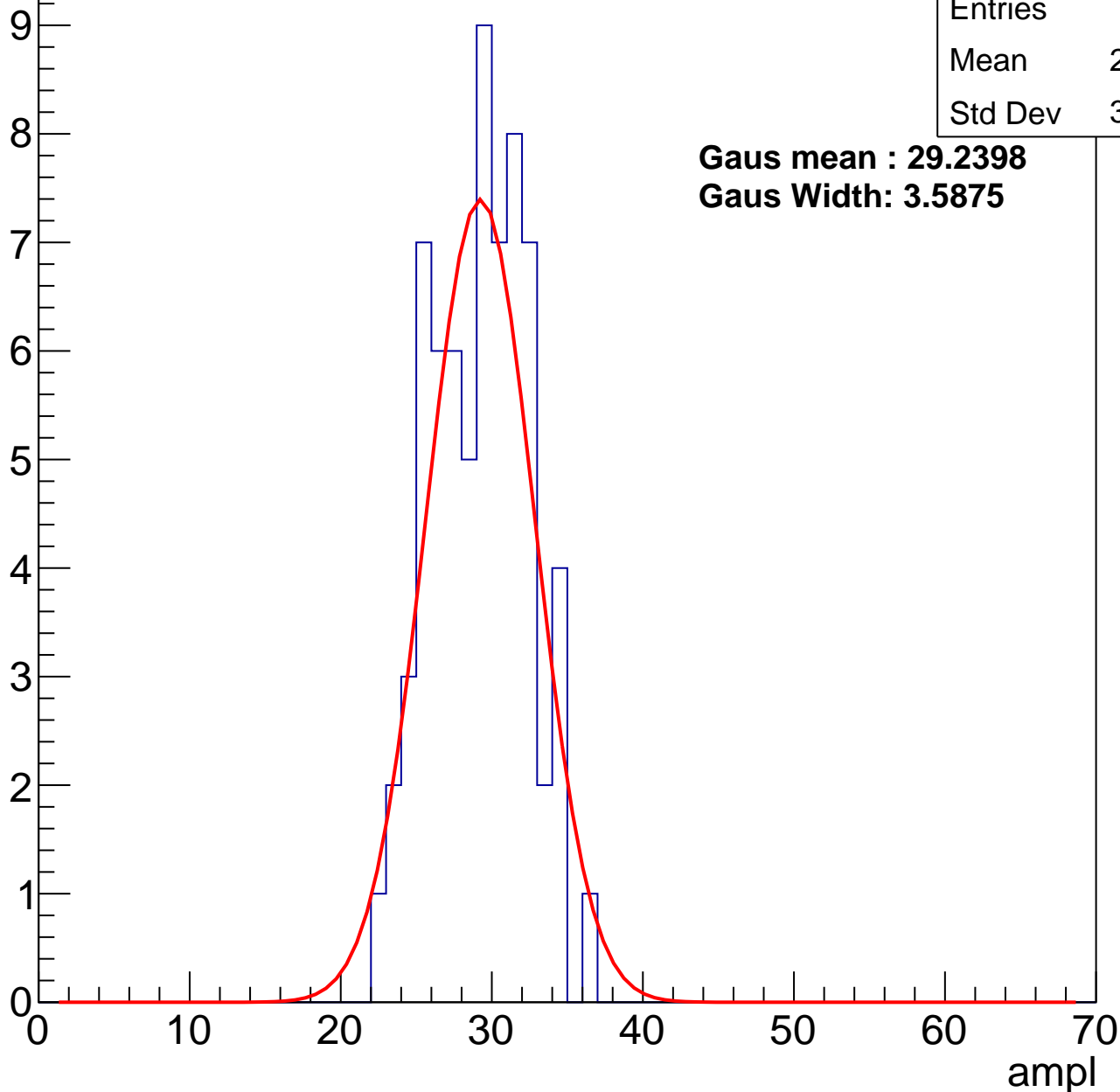
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.74
Std Dev	3.137

**Gaus mean : 29.2398**

**Gaus Width: 3.5875**



# B0L102S, U6-ch109, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	35.75
Std Dev	3.885

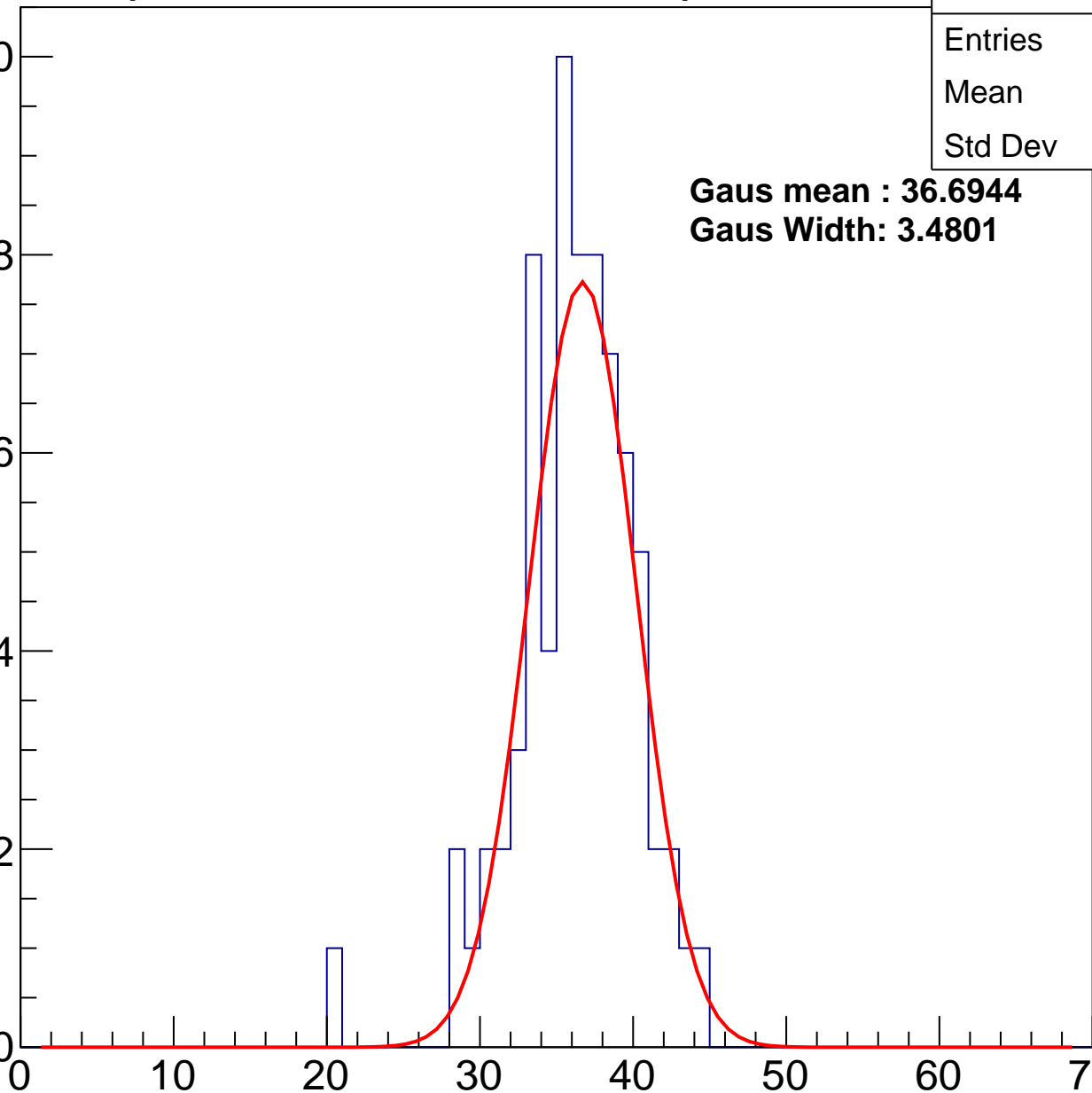
**Gaus mean : 36.6944**

**Gaus Width: 3.4801**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L102S, U6-ch109, adc2

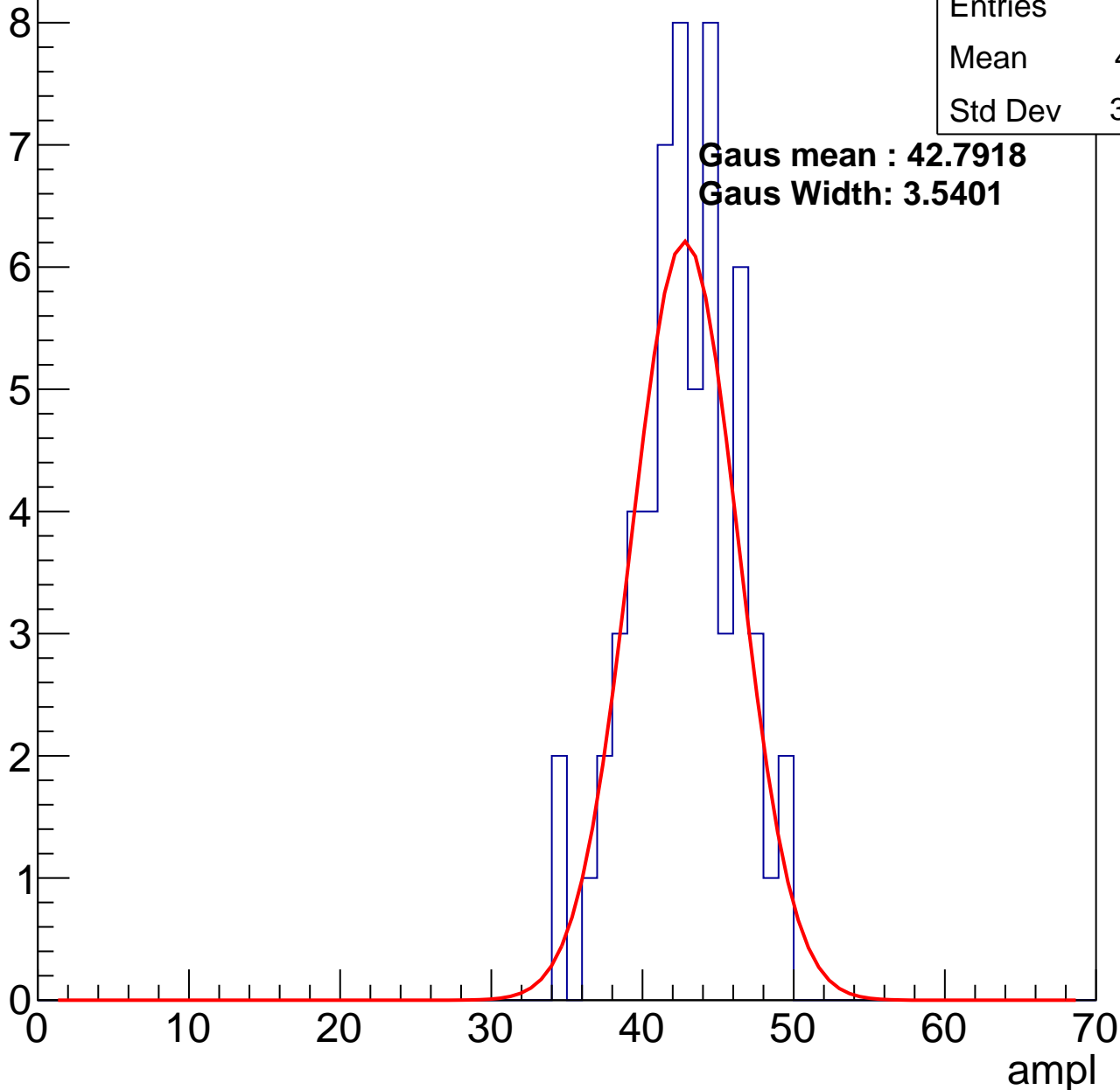
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	42.31
Std Dev	3.406

**Gaus mean : 42.7918**

**Gaus Width: 3.5401**



# B0L102S, U6-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	49.01
Std Dev	3.744

Entry

10

8

6

4

2

0

0

10

20

30

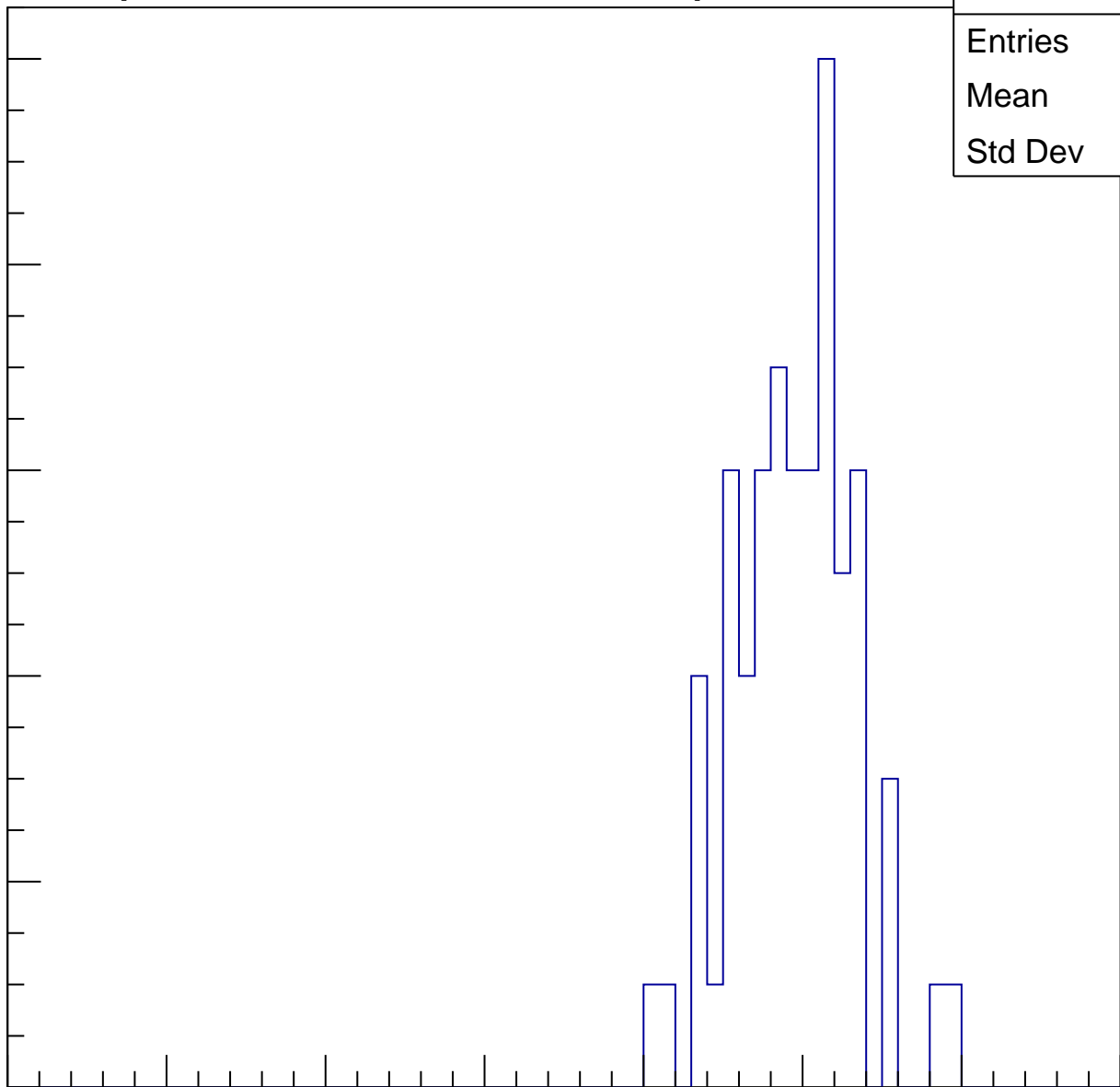
40

50

60

70

ampl

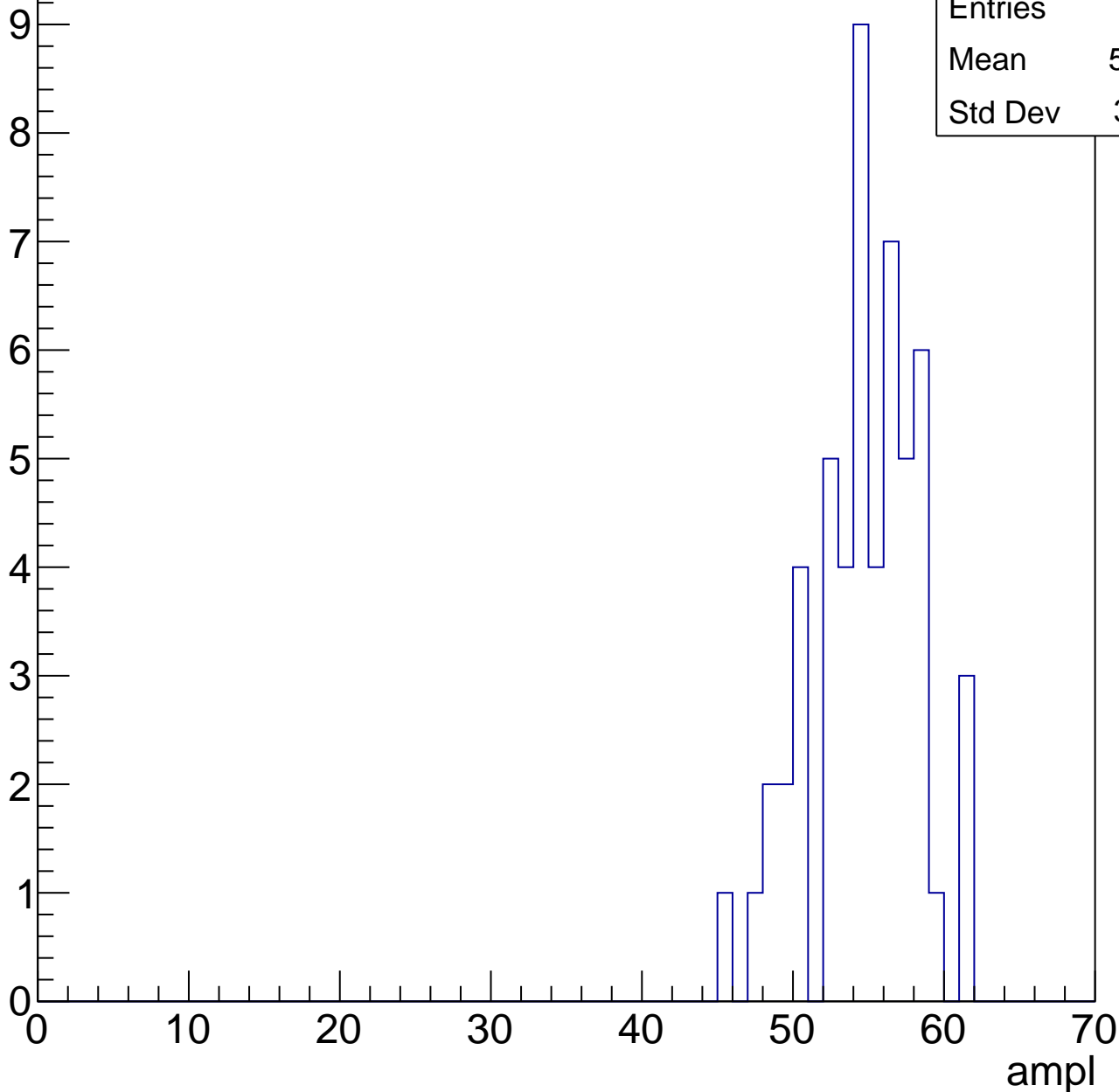


# B0L102S, U6-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

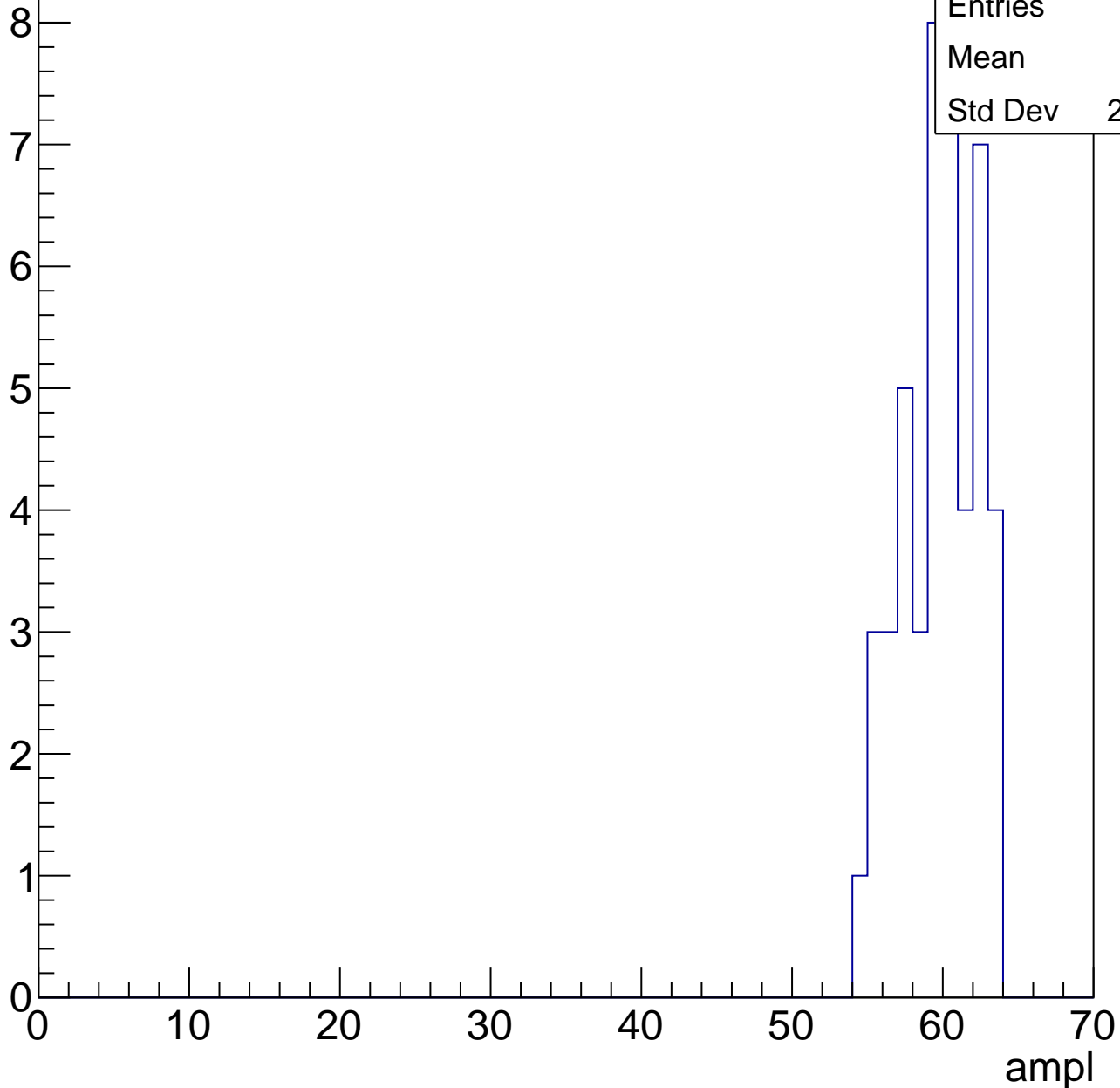
Entries	54
Mean	54.28
Std Dev	3.551



# B0L102S, U6-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

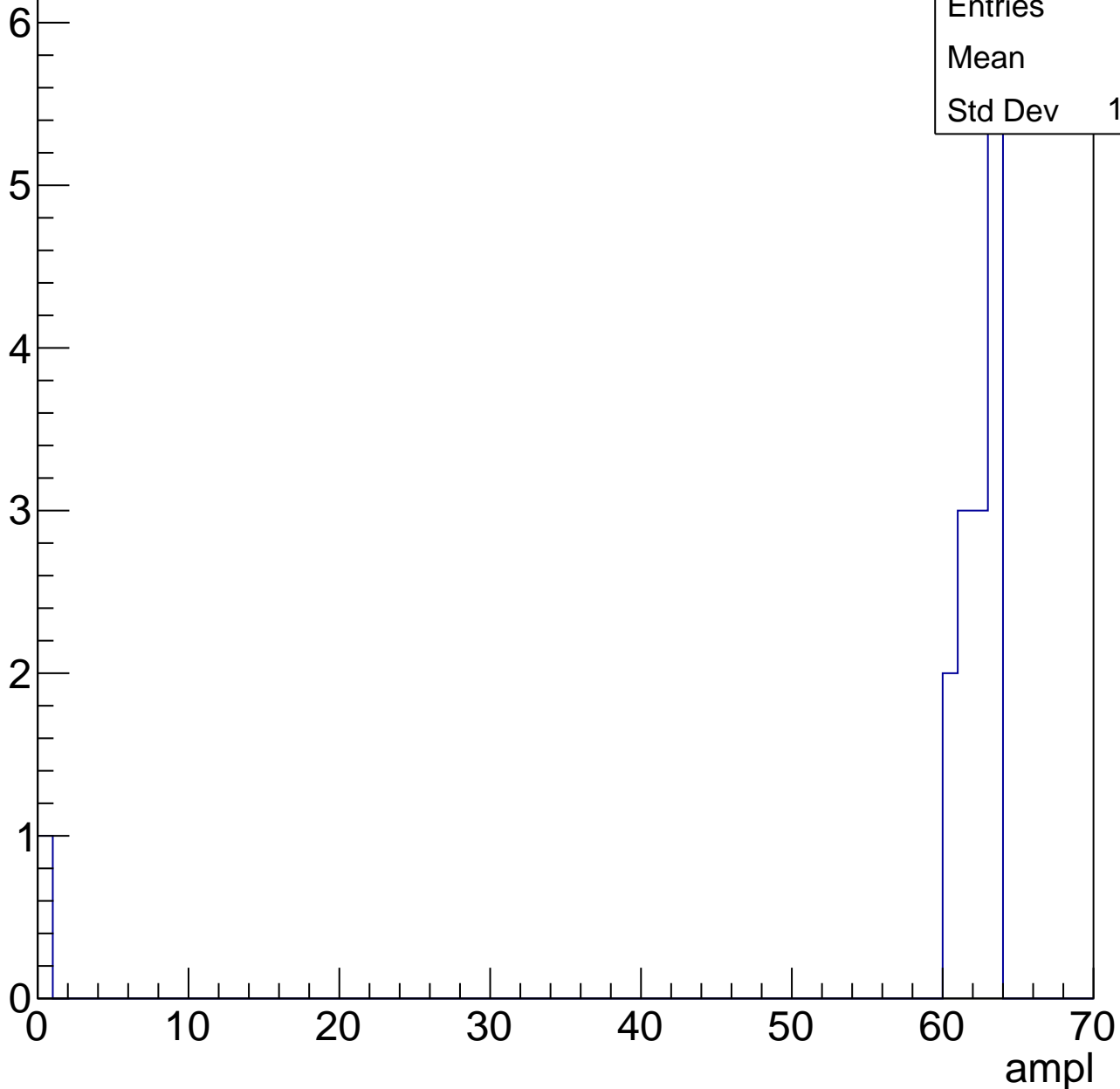


# B0L102S, U6-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	57.8
Std Dev	15.48





# B0L102S, U6-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch110, adc0

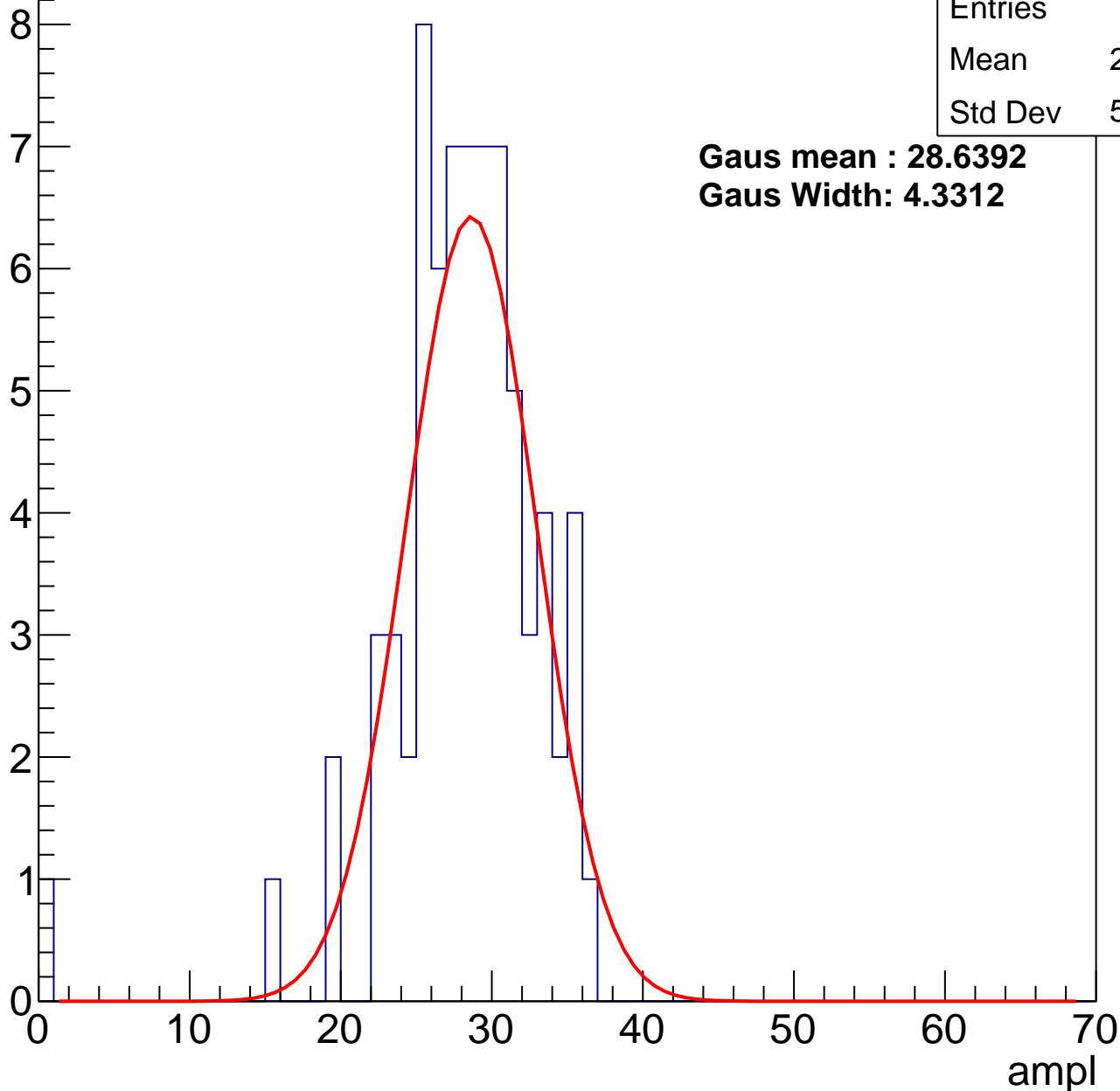
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	27.63
Std Dev	5.212

**Gaus mean : 28.6392**

**Gaus Width: 4.3312**



# B0L102S, U6-ch110, adc1

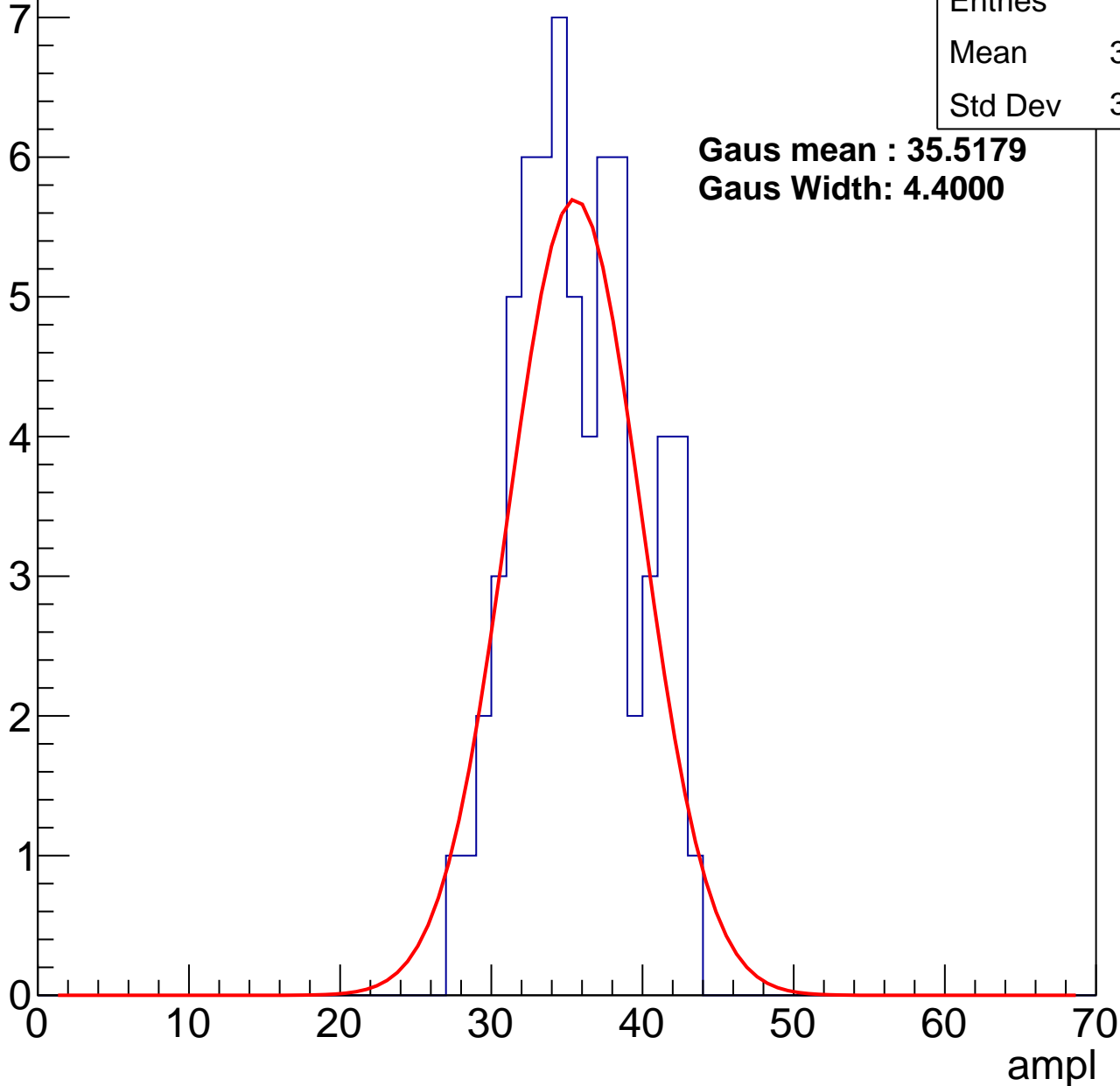
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	35.27
Std Dev	3.918

**Gaus mean : 35.5179**

**Gaus Width: 4.4000**



# B0L102S, U6-ch110, adc2

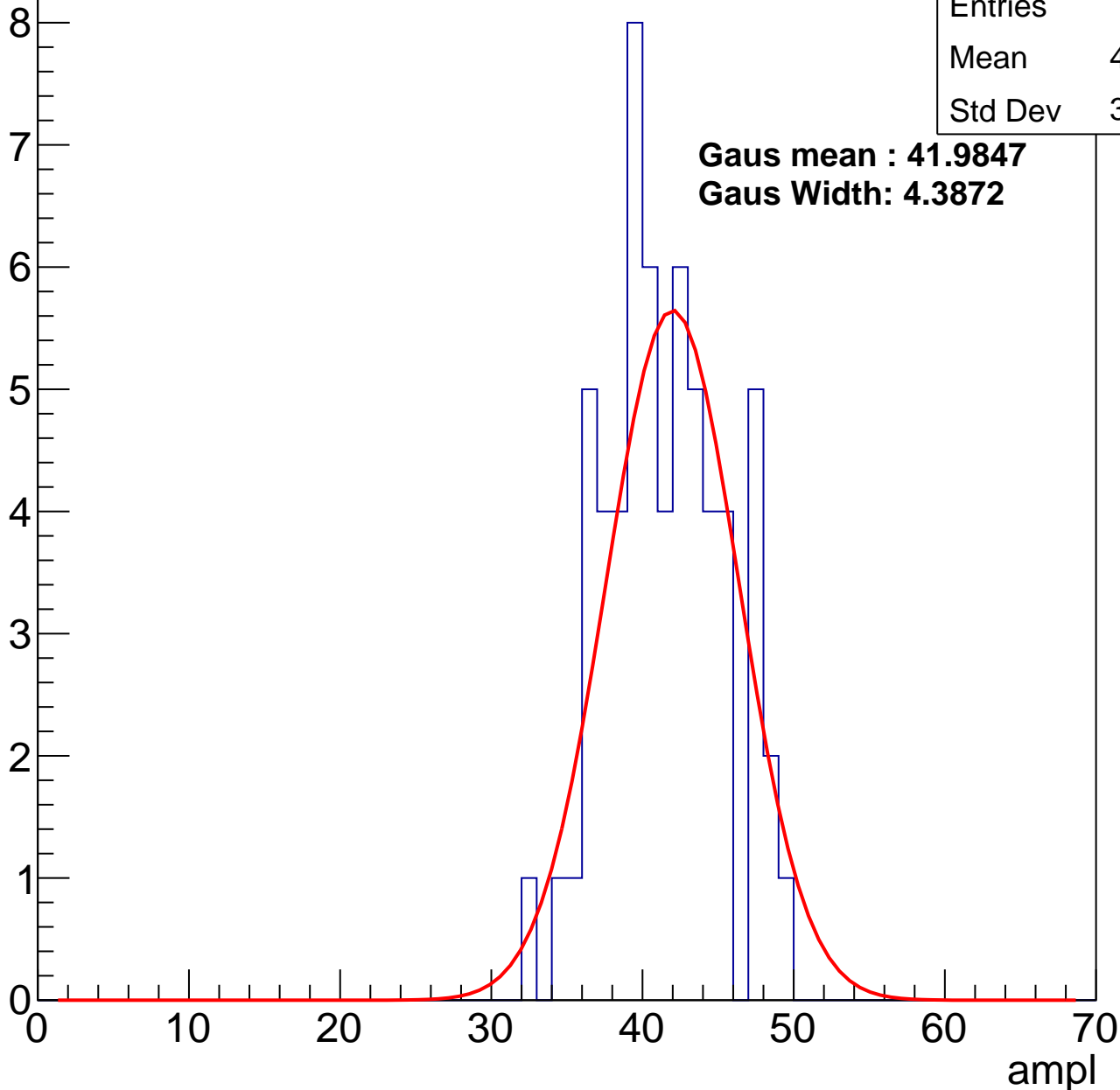
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	40.98
Std Dev	3.835

**Gaus mean : 41.9847**

**Gaus Width: 4.3872**

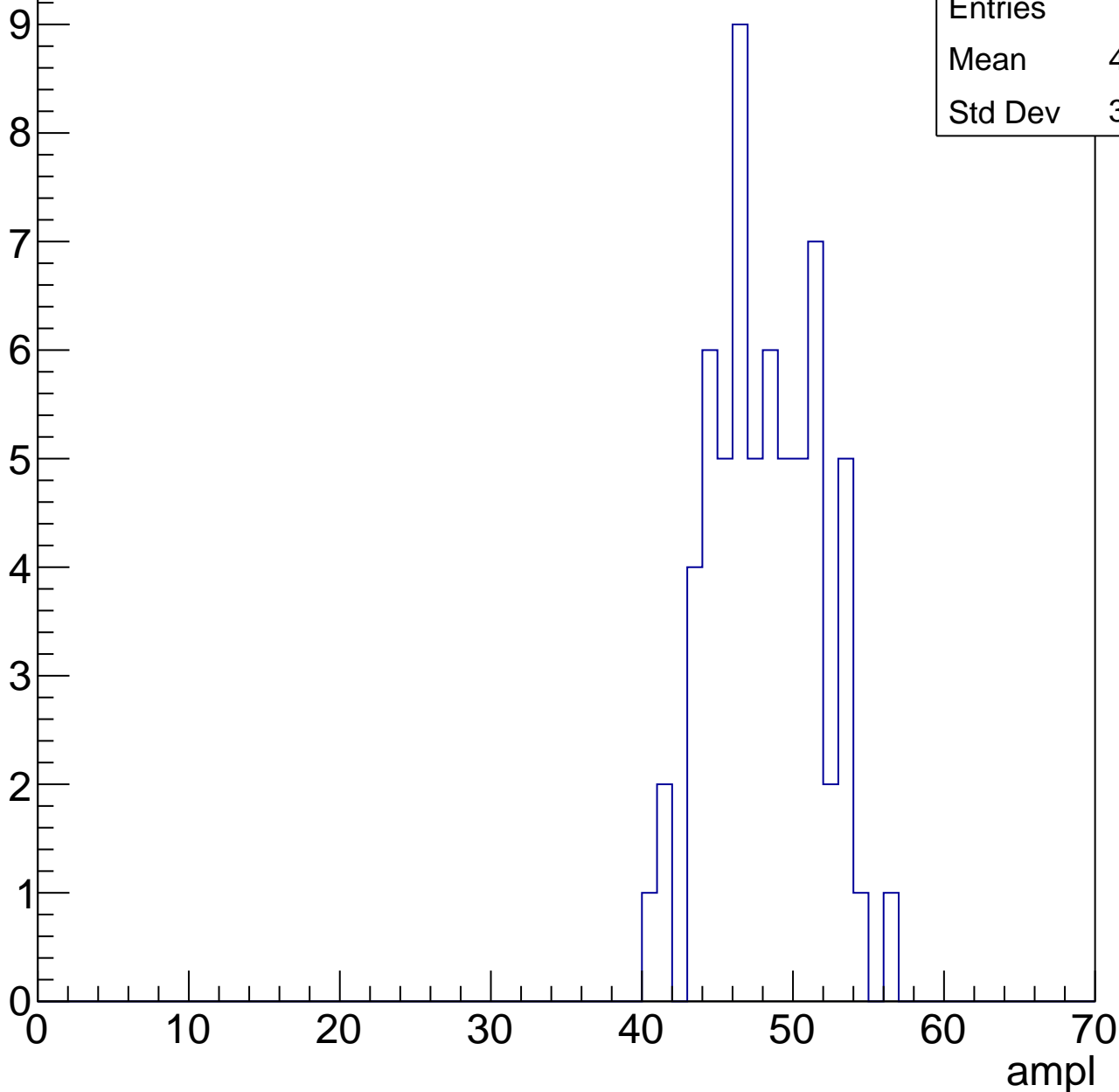


# B0L102S, U6-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	47.67
Std Dev	3.496

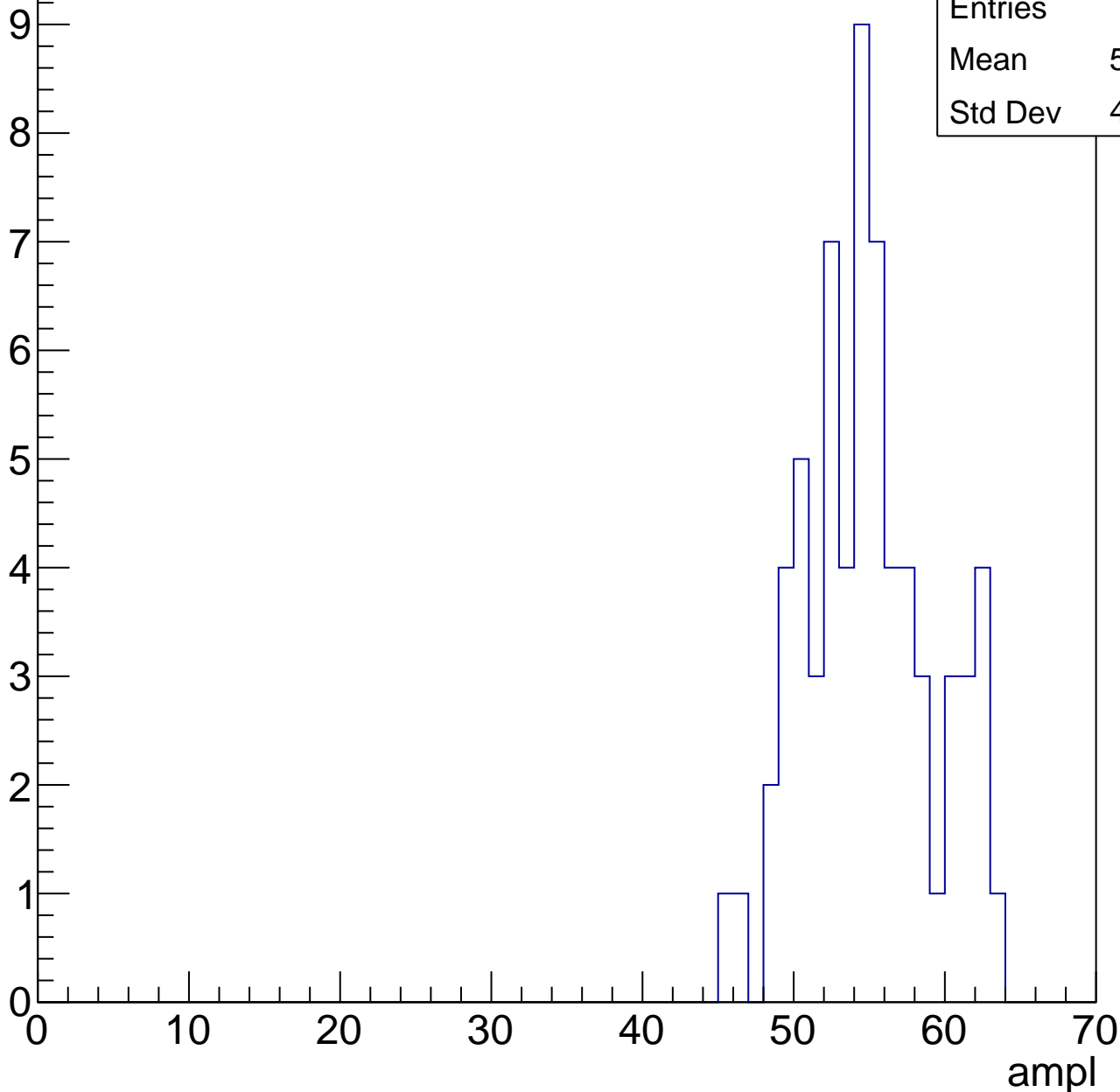


# B0L102S, U6-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	54.42
Std Dev	4.207

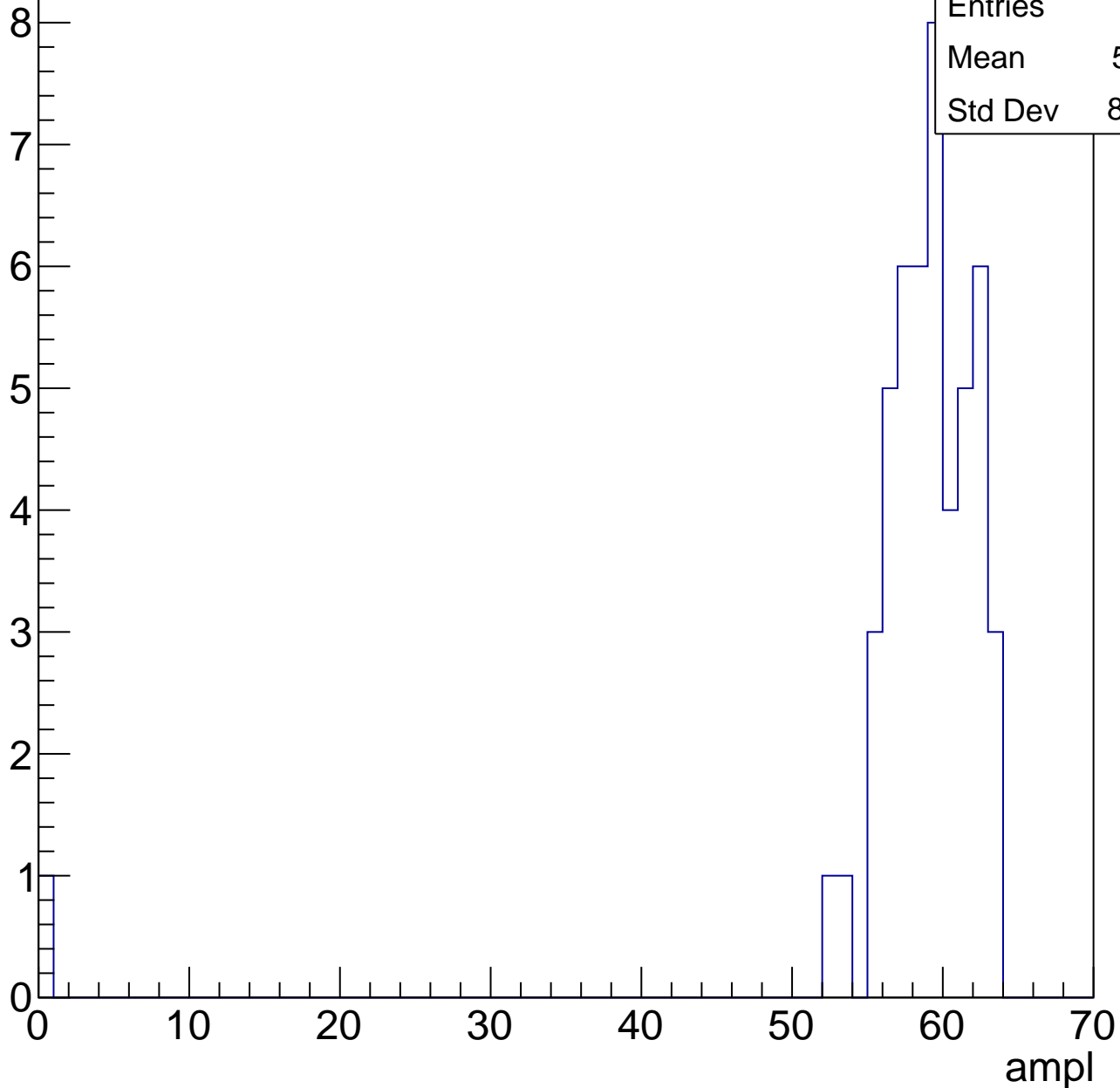


# B0L102S, U6-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	57.51
Std Dev	8.697

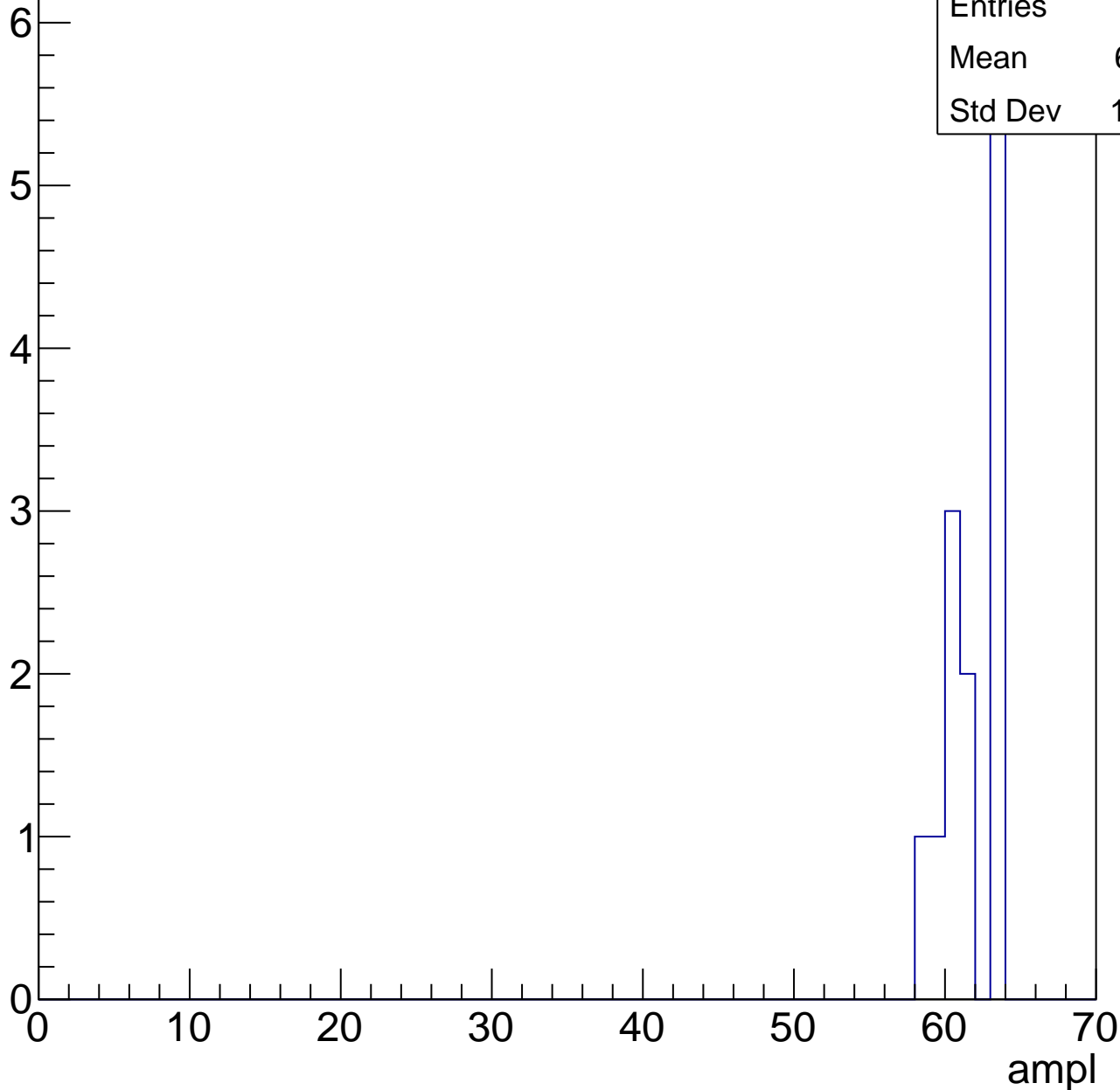


# B0L102S, U6-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	61.31
Std Dev	1.727





# B0L102S, U6-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch111, adc0

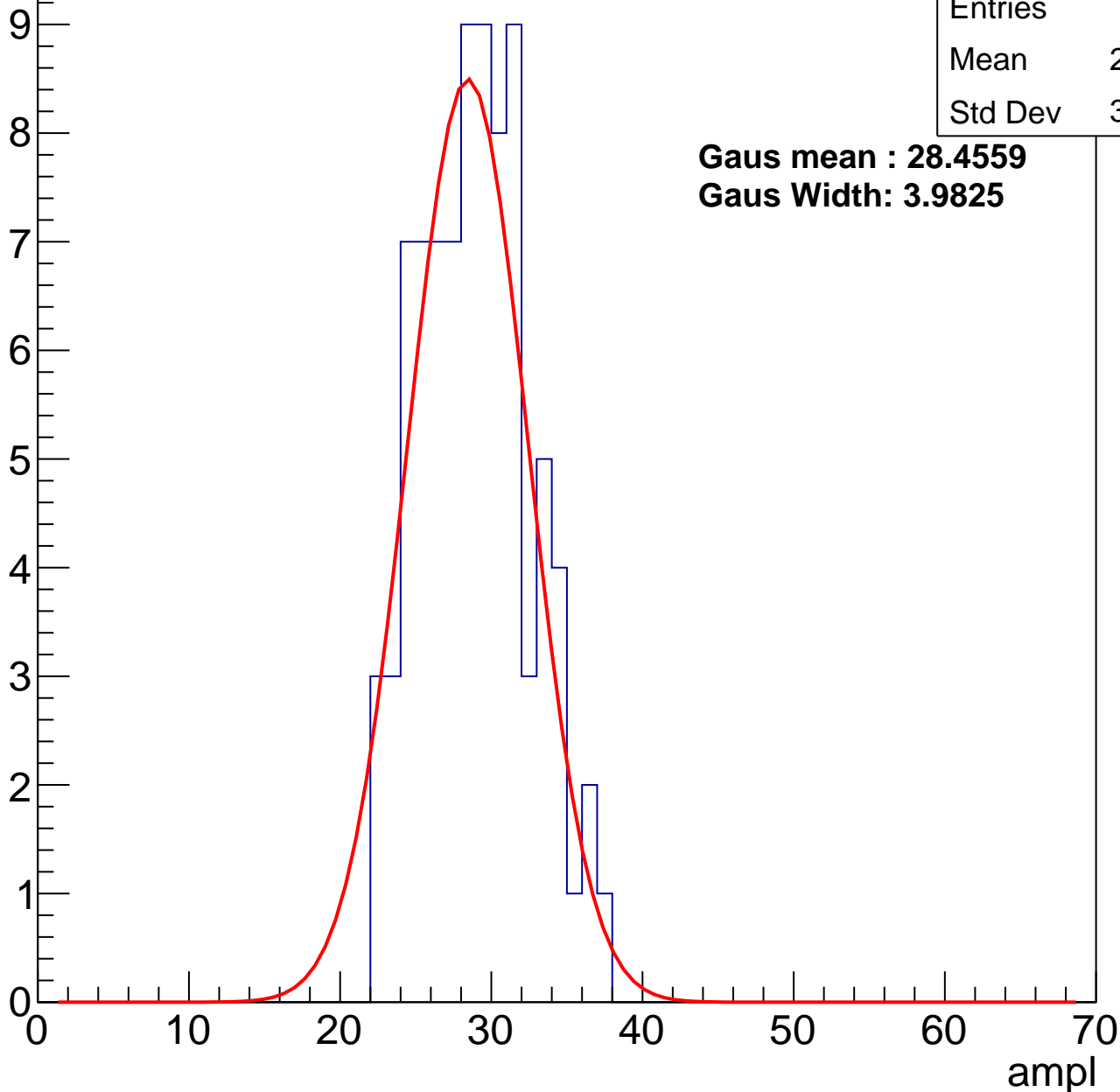
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	28.49
Std Dev	3.547

**Gaus mean : 28.4559**

**Gaus Width: 3.9825**



# B0L102S, U6-ch111, adc1

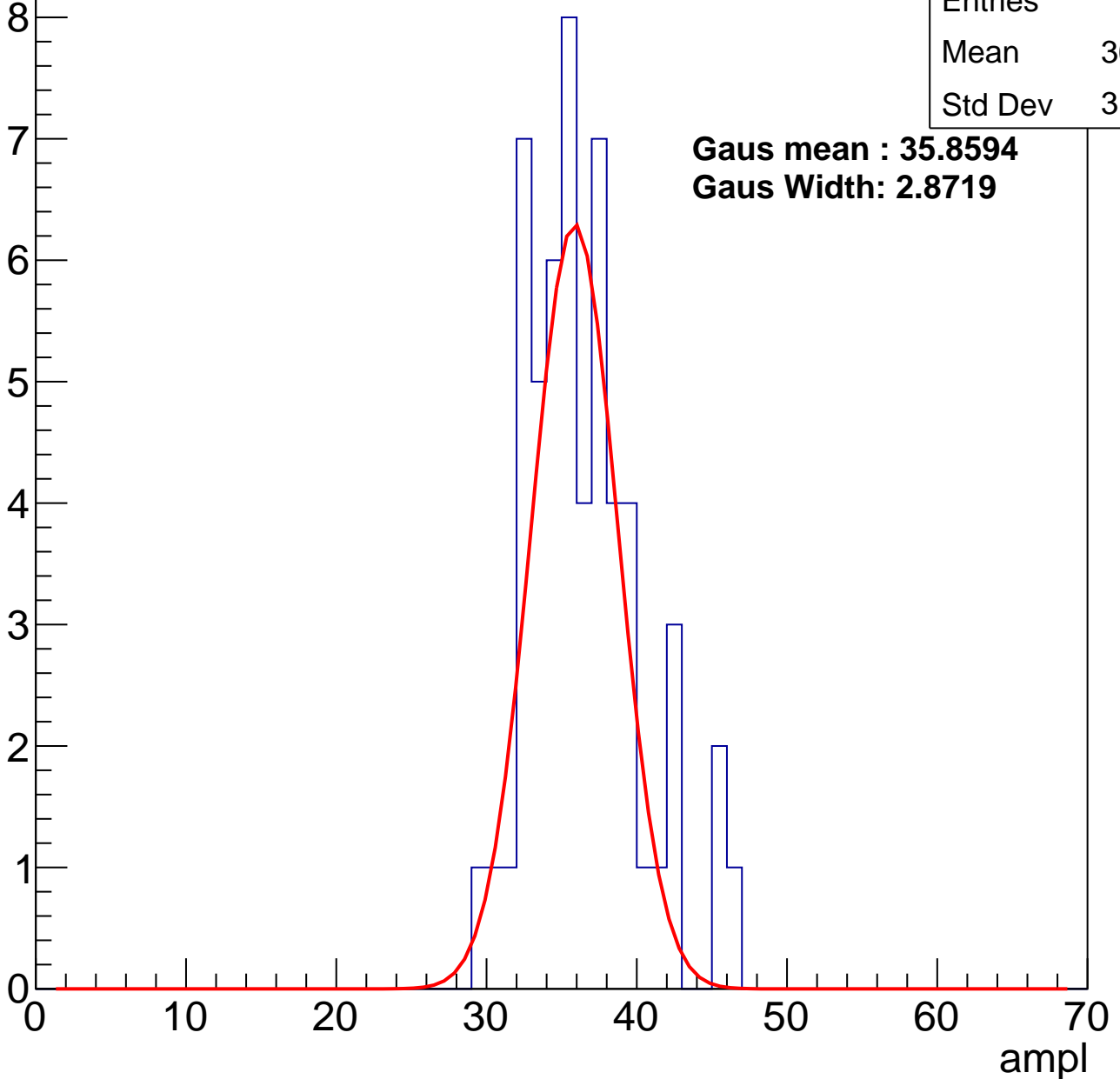
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	36.02
Std Dev	3.706

**Gaus mean : 35.8594**

**Gaus Width: 2.8719**



# B0L102S, U6-ch111, adc2

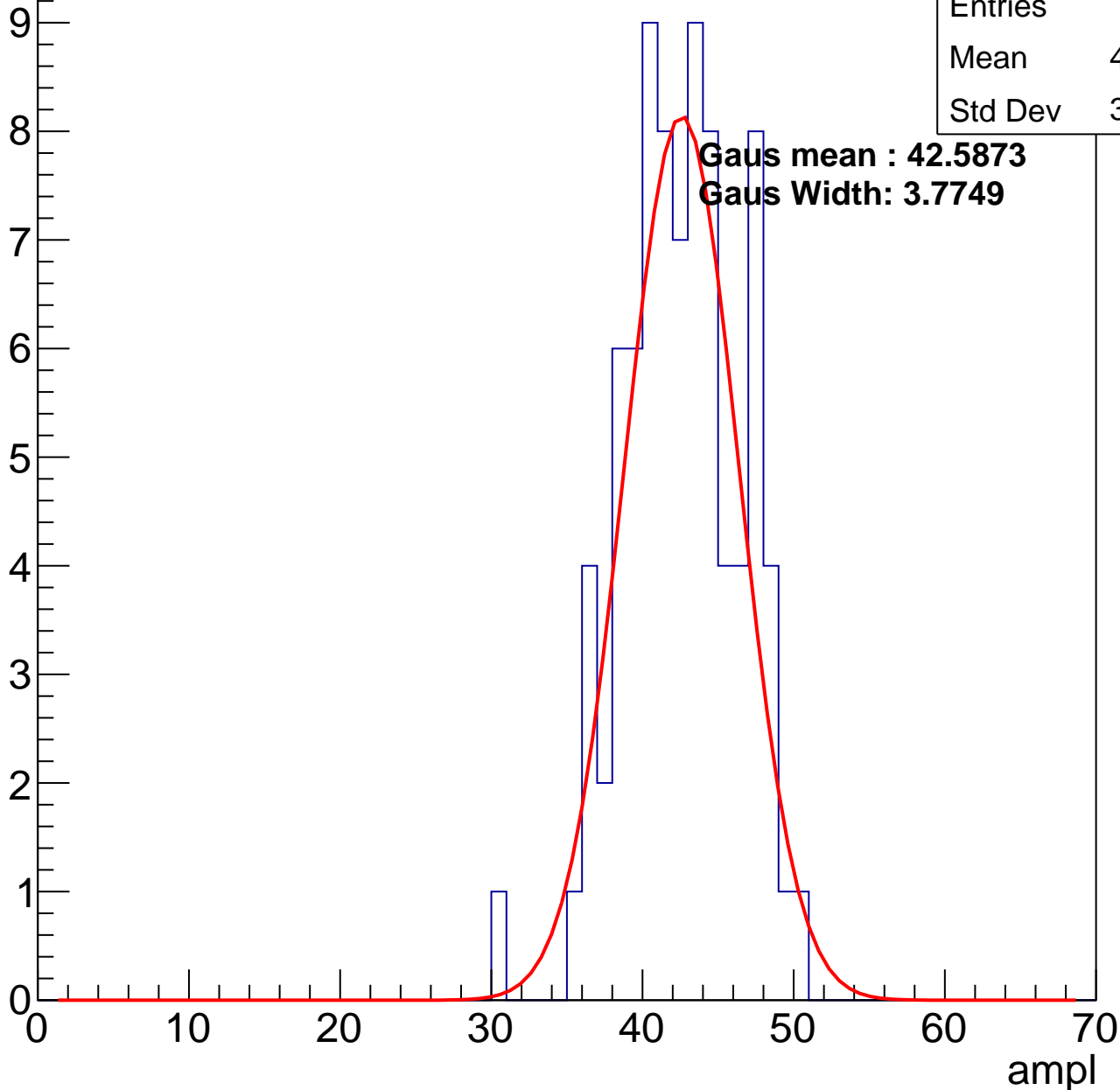
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	83
Mean	42.13
Std Dev	3.767

**Gaus mean : 42.5873**

**Gaus Width: 3.7749**

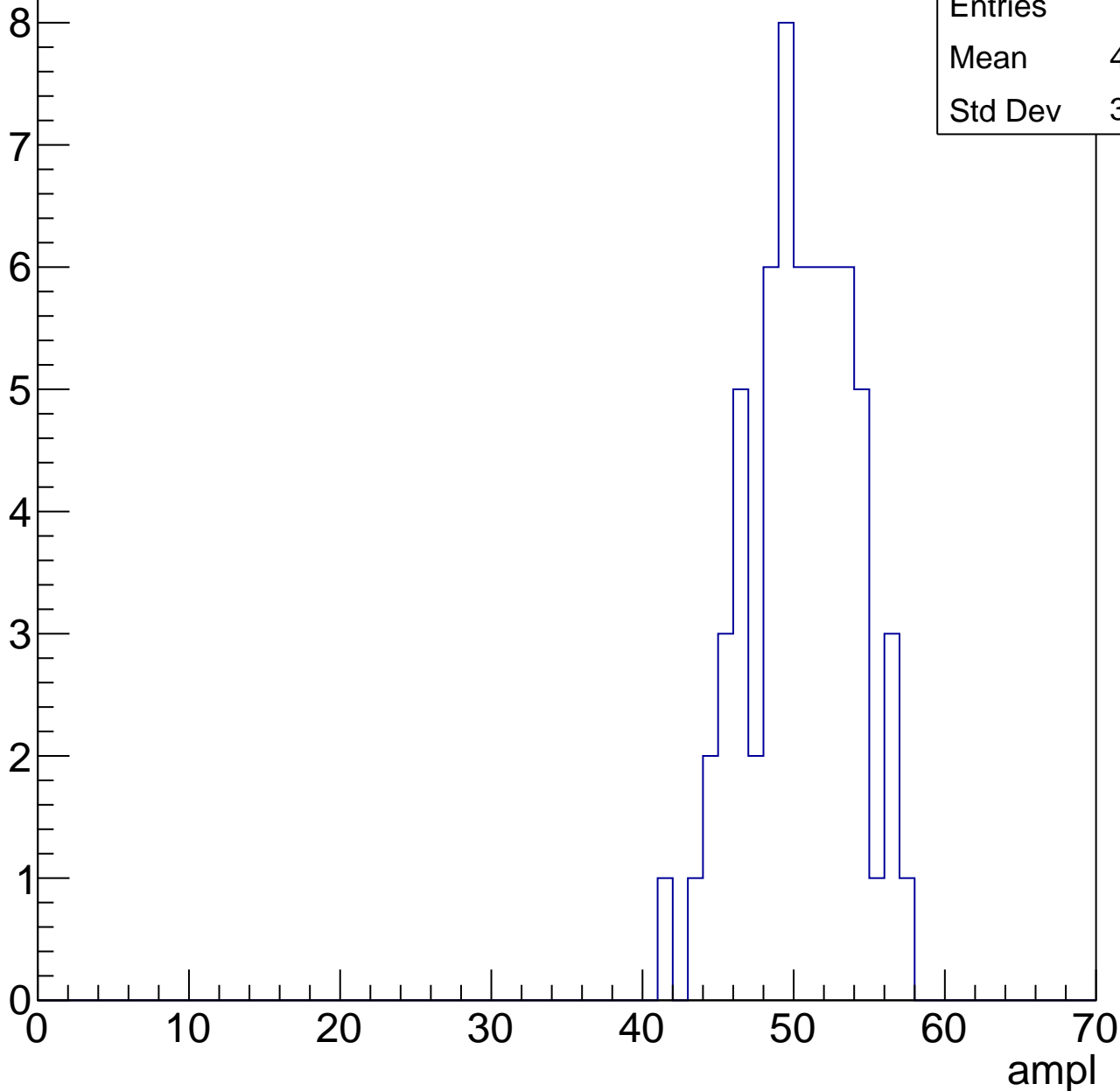


# B0L102S, U6-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	49.95
Std Dev	3.485

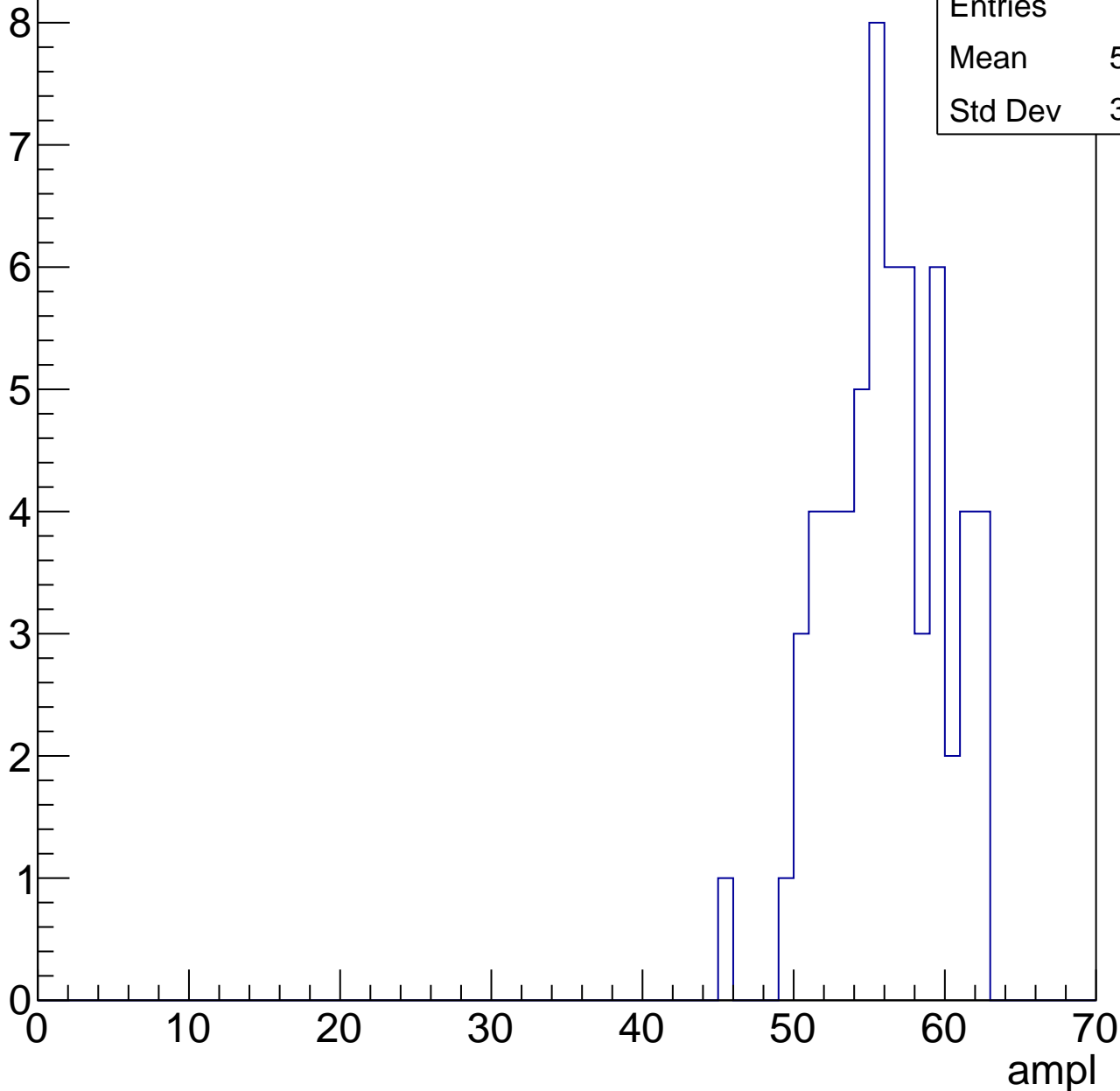


# B0L102S, U6-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	55.67
Std Dev	3.727



# B0L102S, U6-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	58.56
Std Dev	9.508

ampl

0

10

20

30

40

50

60

70

# B0L102S, U6-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.6
Std Dev	1.2

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch112, adc0

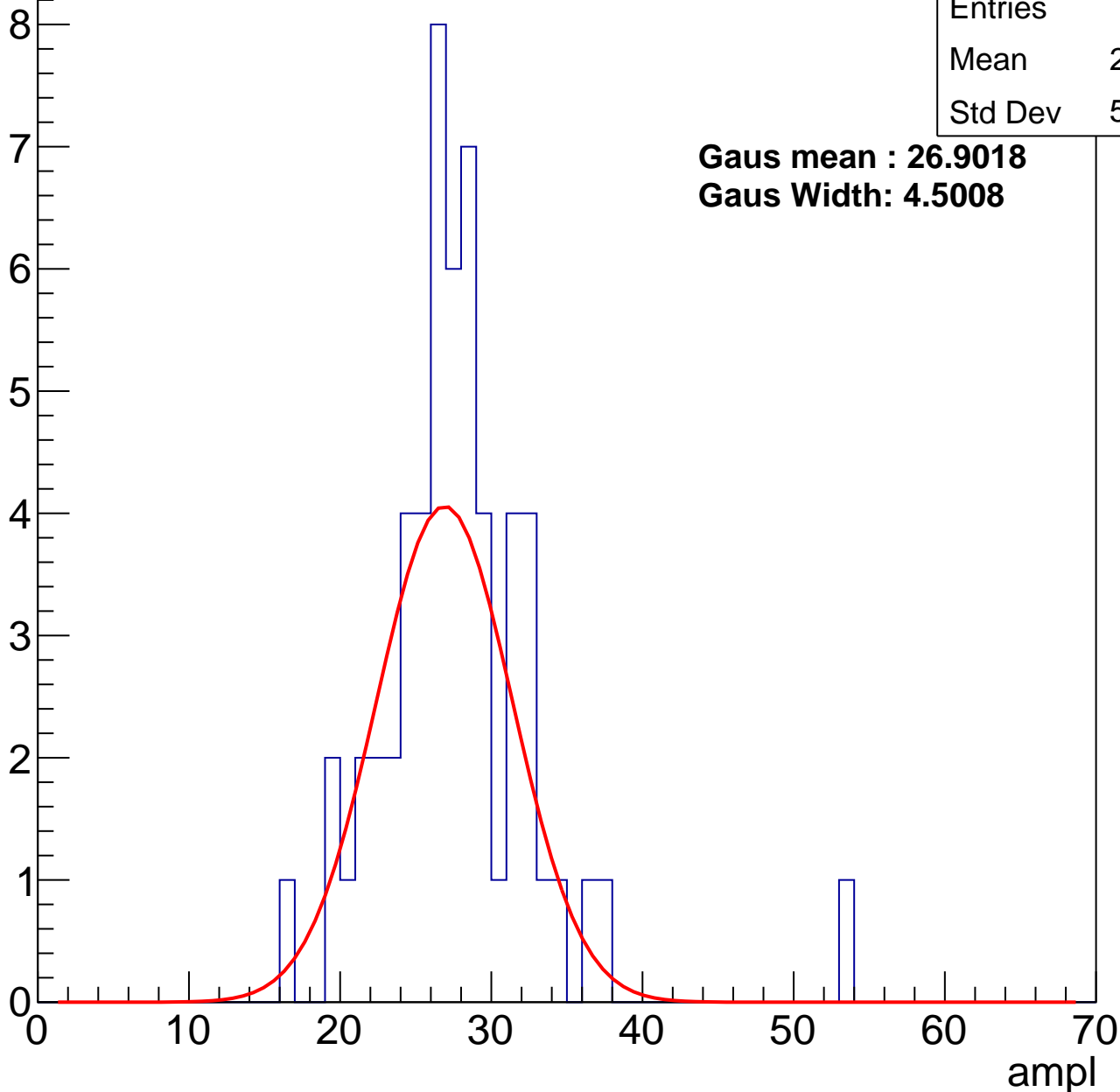
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	27.35
Std Dev	5.369

**Gaus mean : 26.9018**

**Gaus Width: 4.5008**



# B0L102S, U6-ch112, adc1

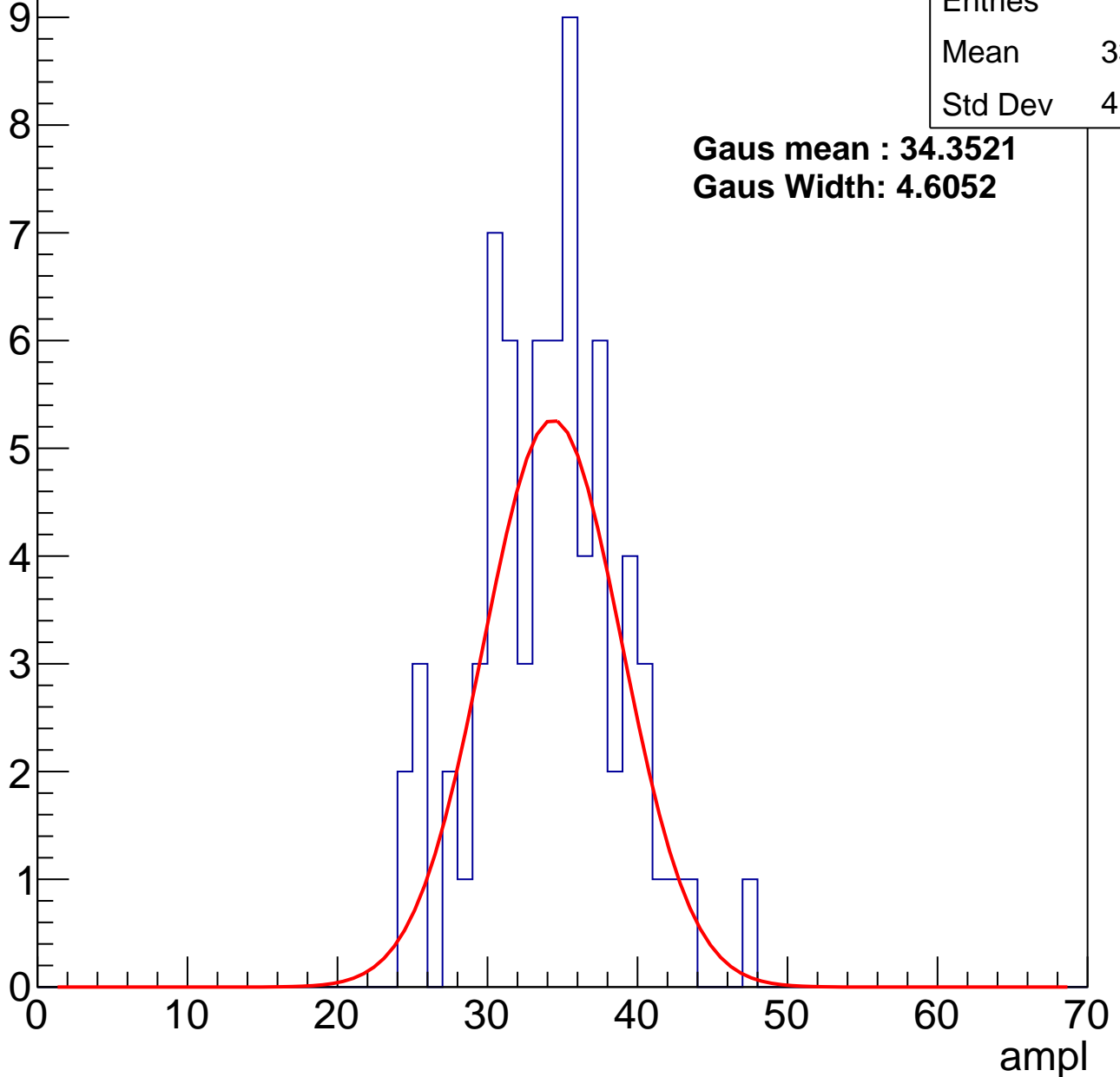
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	33.69
Std Dev	4.613

**Gaus mean : 34.3521**

**Gaus Width: 4.6052**



# B0L102S, U6-ch112, adc2

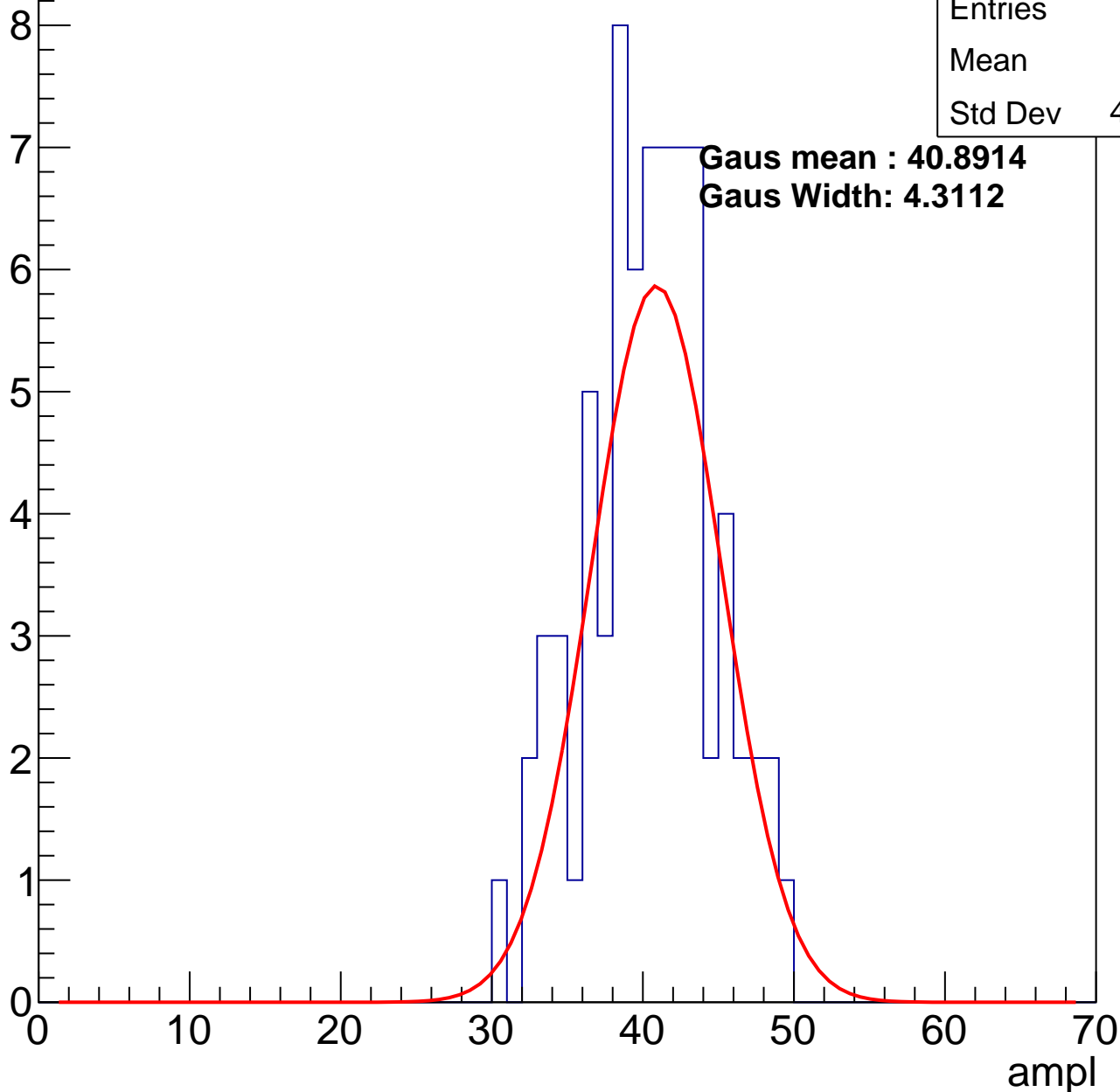
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	40
Std Dev	4.158

**Gaus mean : 40.8914**

**Gaus Width: 4.3112**

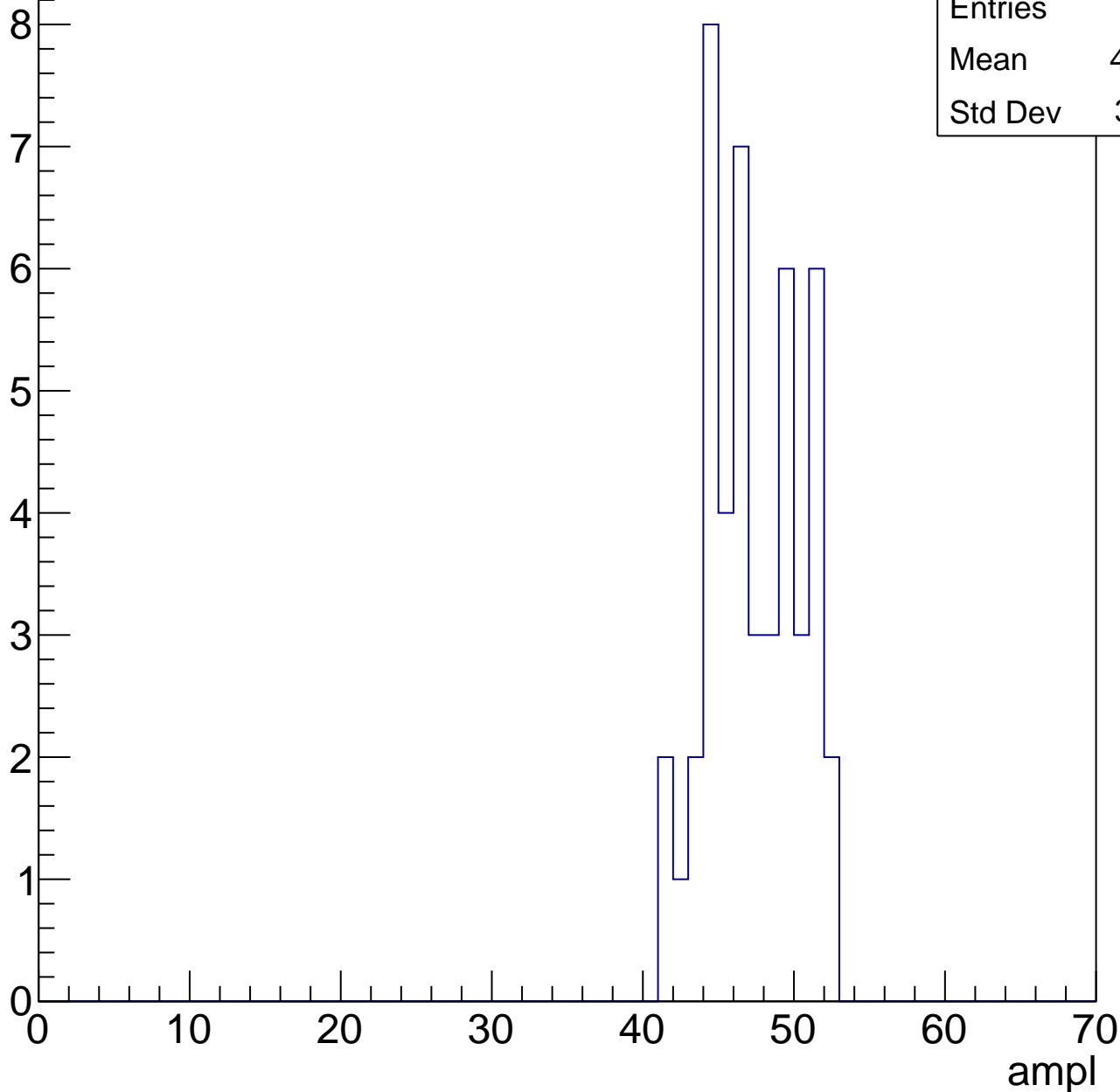


# B0L102S, U6-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	46.87
Std Dev	3.001

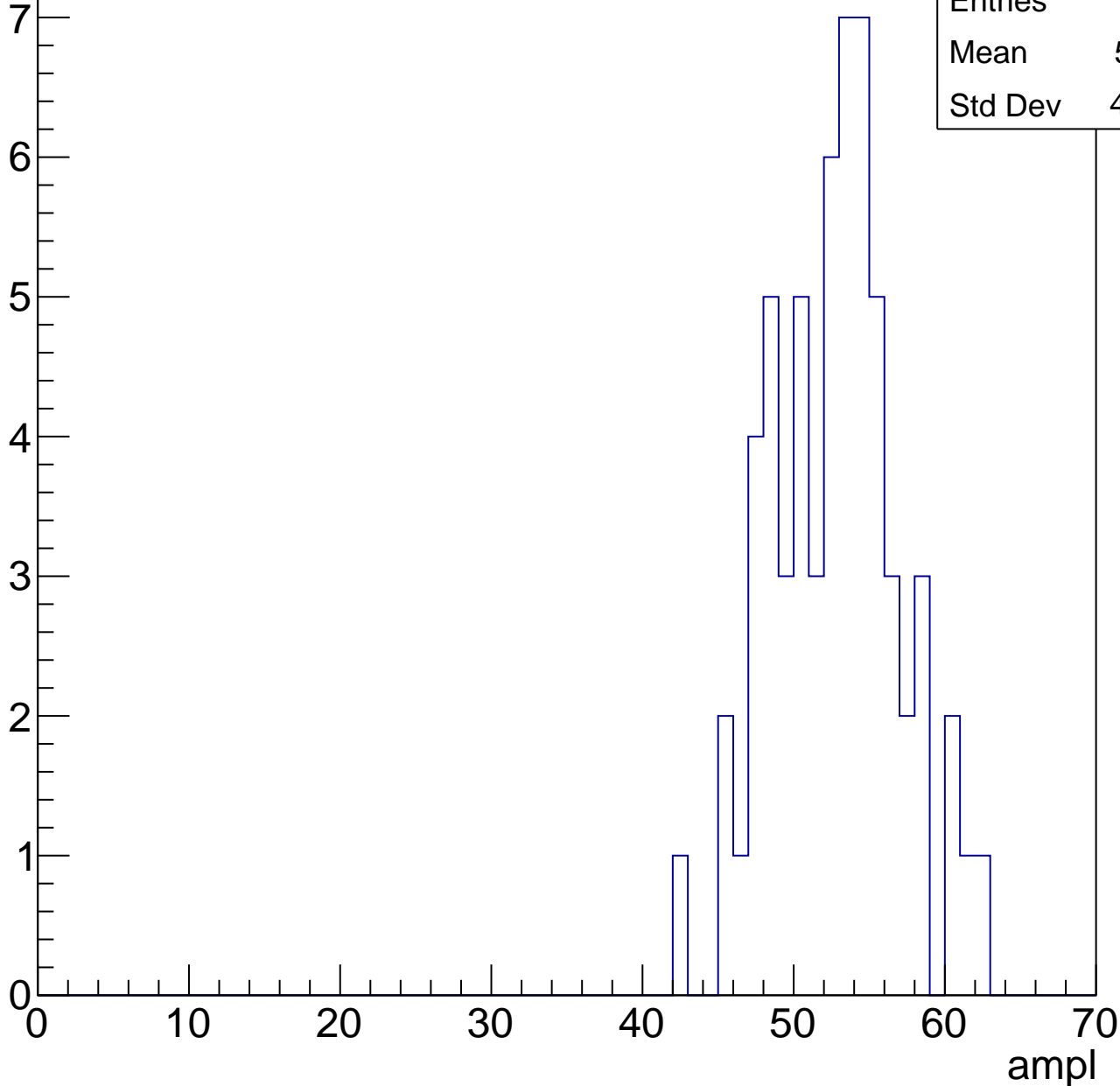


# B0L102S, U6-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	52.31
Std Dev	4.155

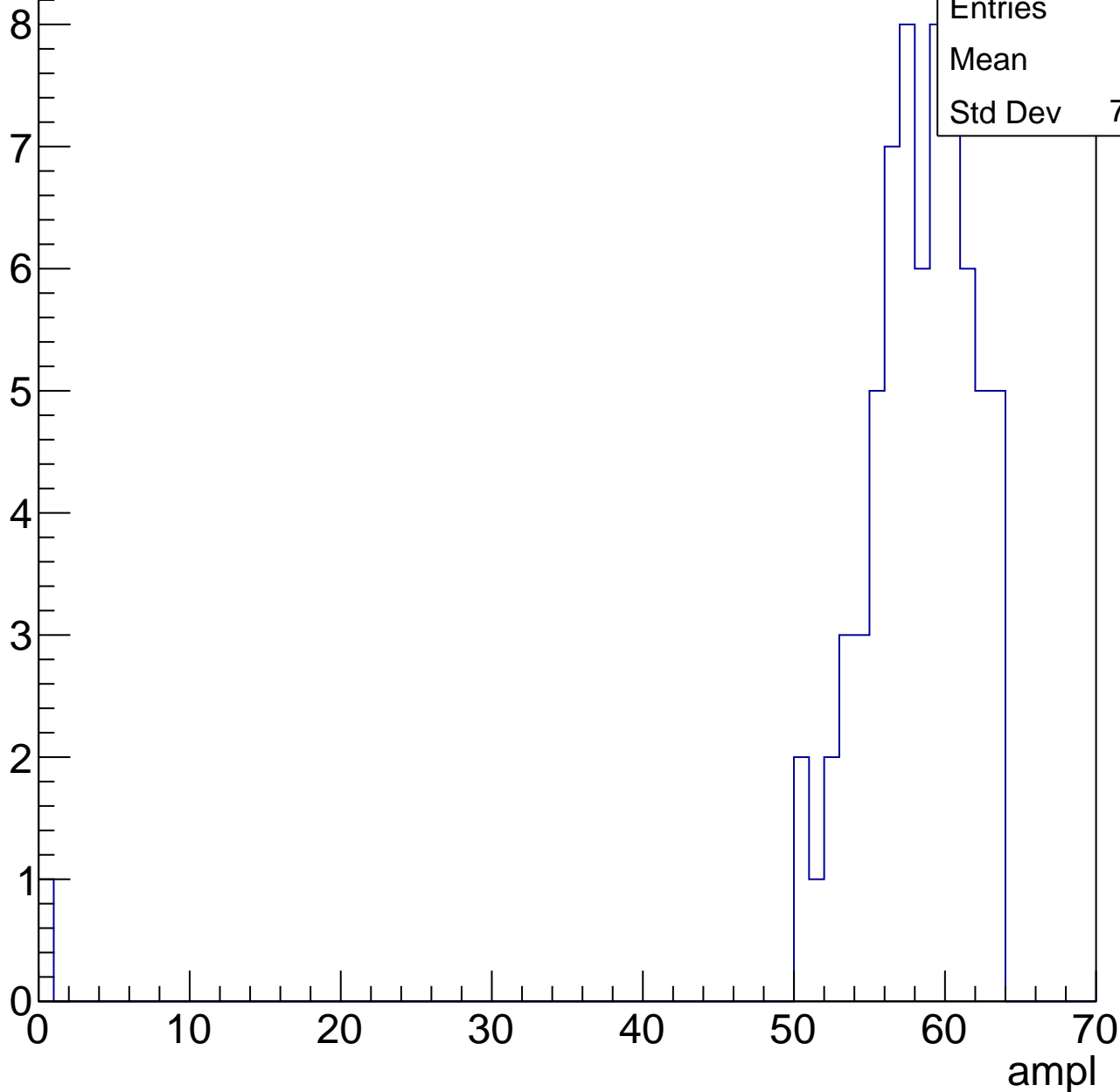


# B0L102S, U6-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

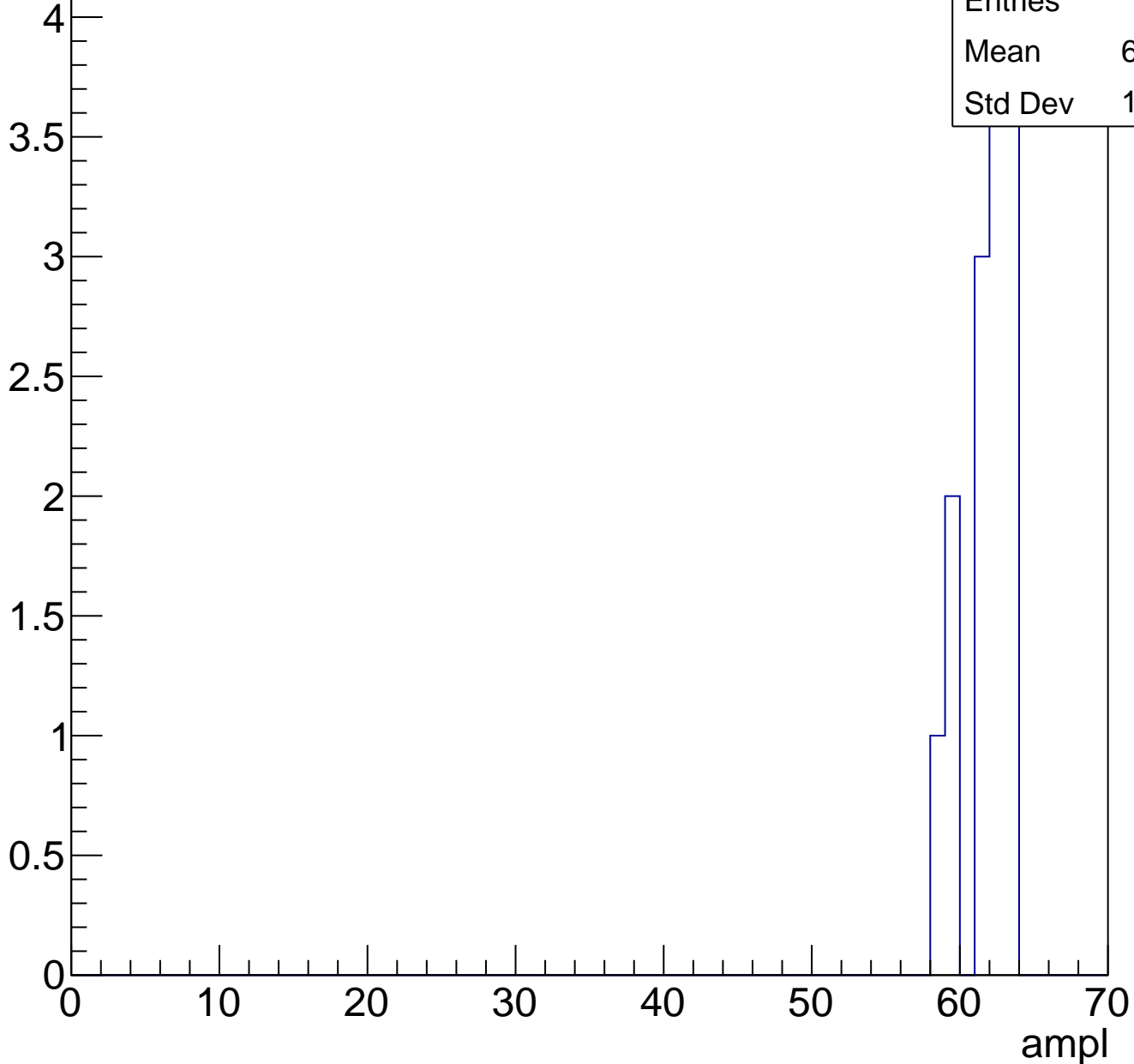
Entries	70
Mean	57
Std Dev	7.603



# B0L102S, U6-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch113, adc0

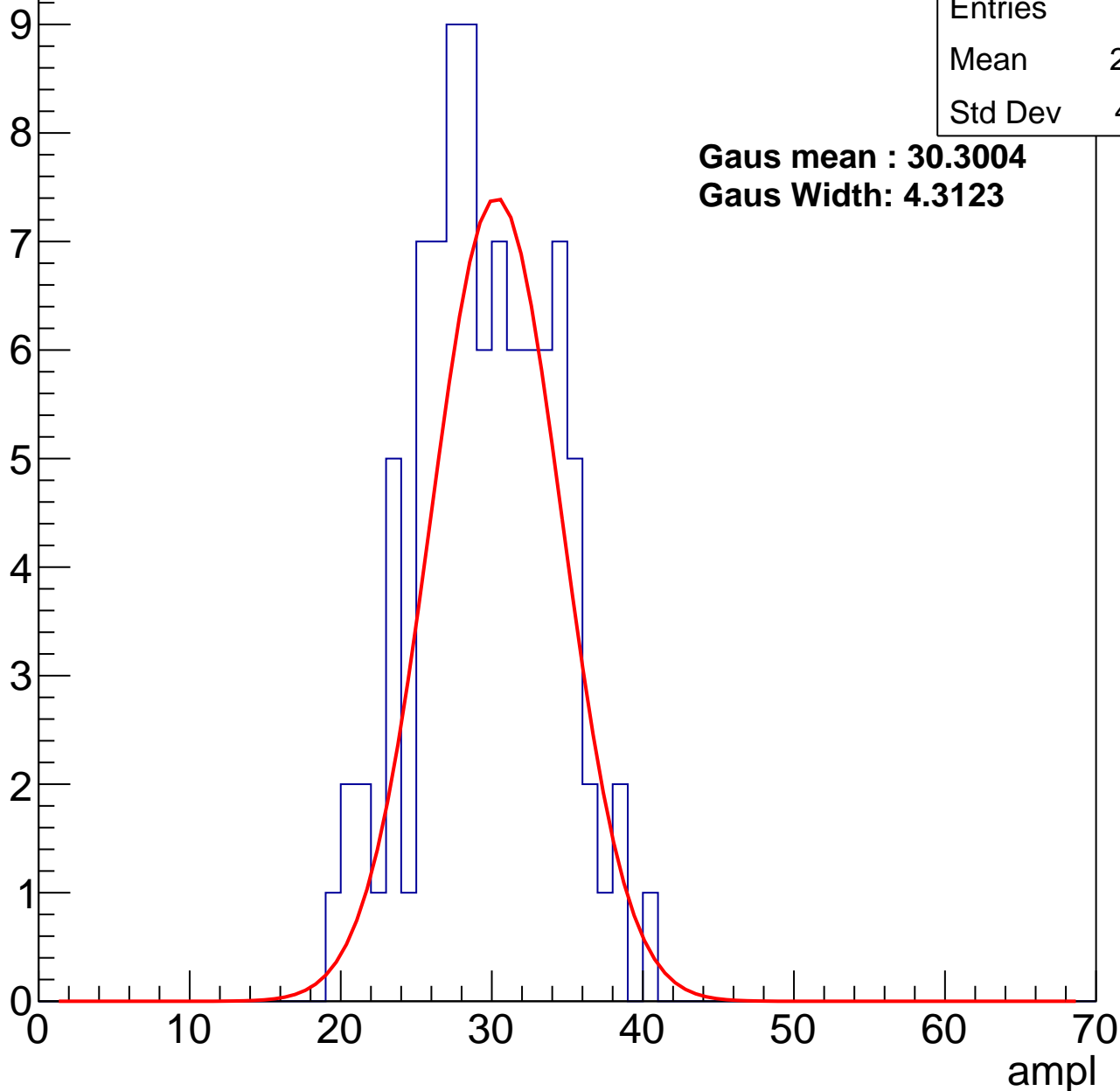
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	93
Mean	29.16
Std Dev	4.451

**Gaus mean : 30.3004**

**Gaus Width: 4.3123**



# B0L102S, U6-ch113, adc1

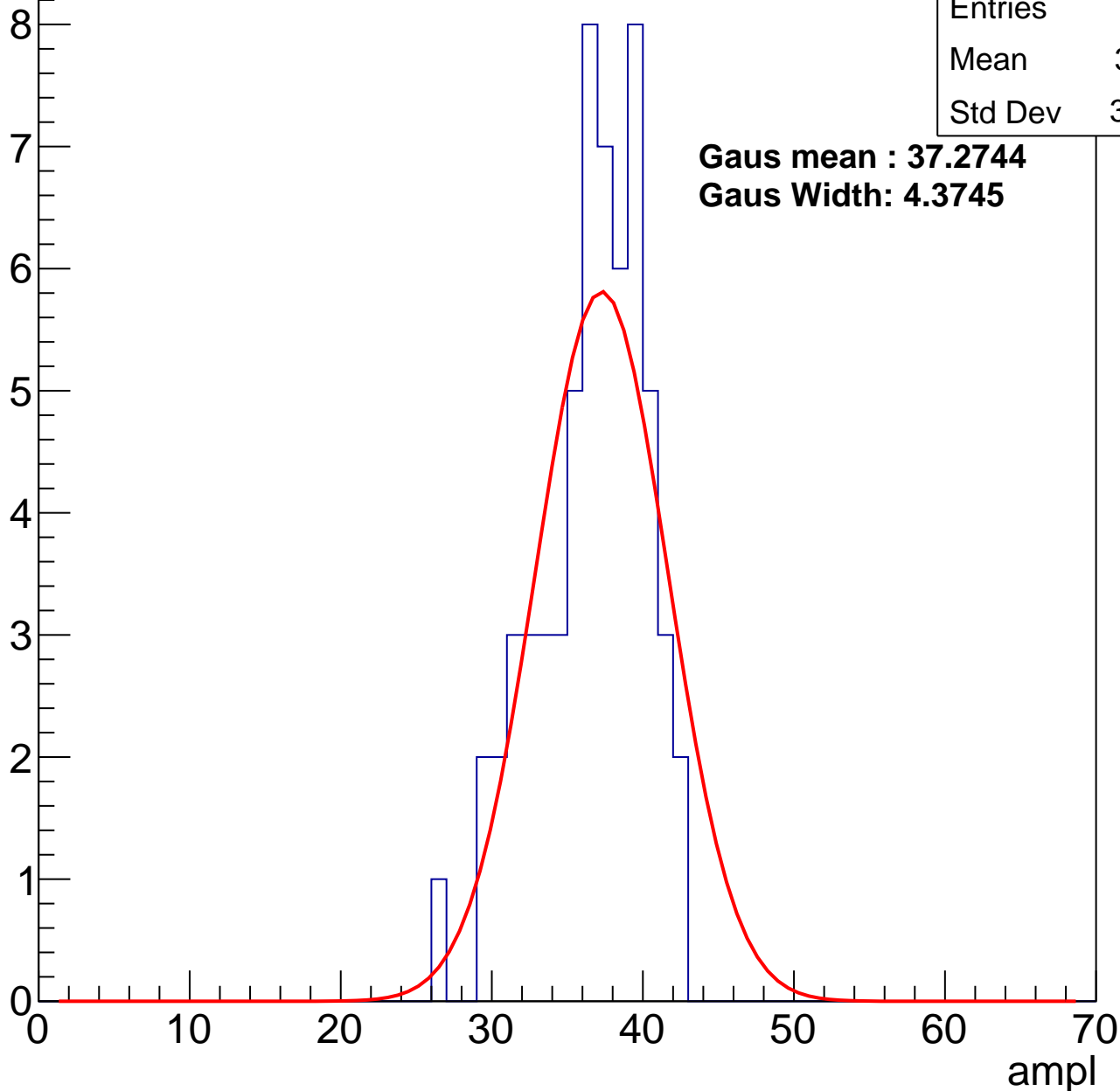
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	36.11
Std Dev	3.544

**Gaus mean : 37.2744**

**Gaus Width: 4.3745**



# B0L102S, U6-ch113, adc2

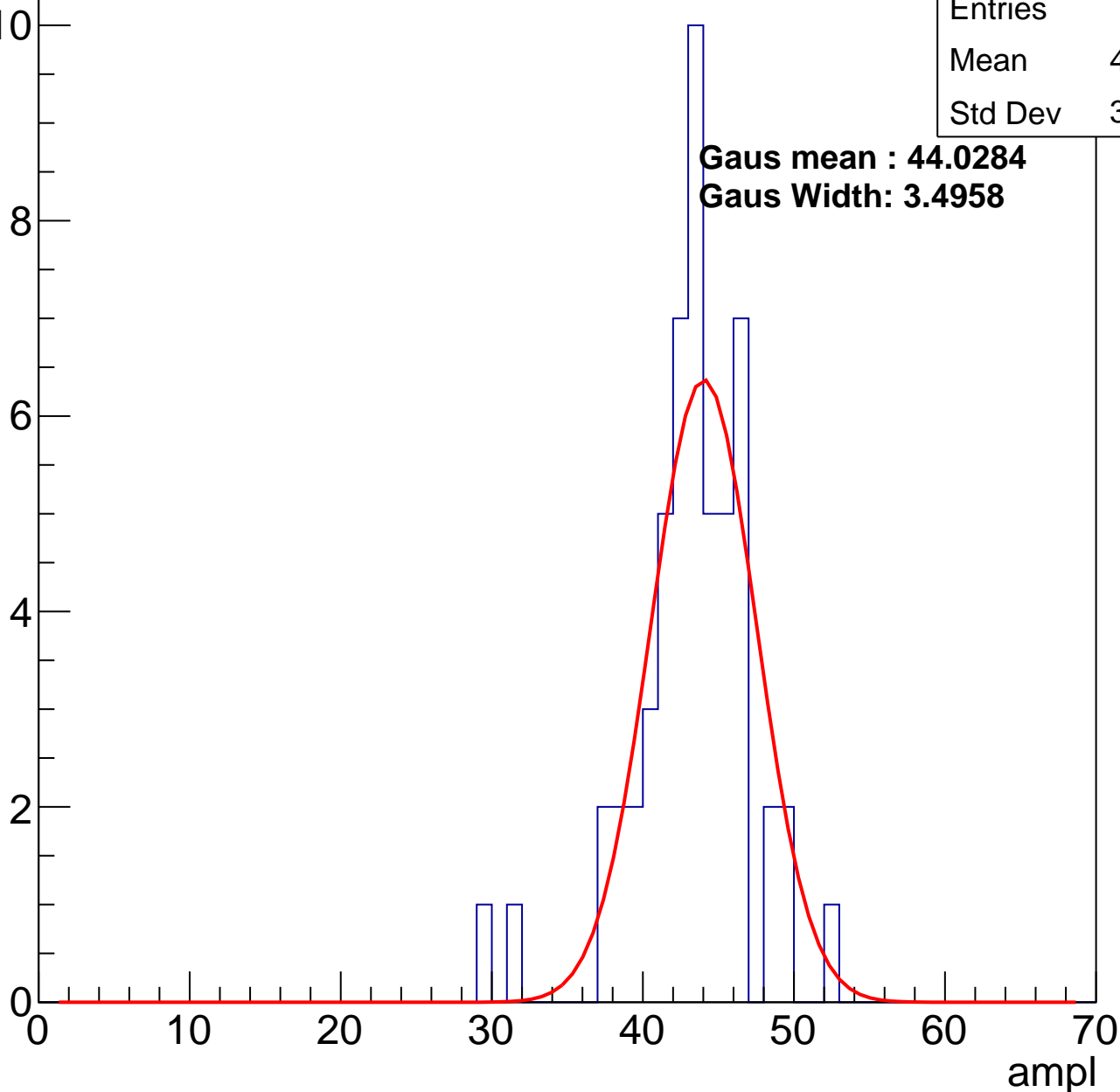
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	42.73
Std Dev	3.896

**Gaus mean : 44.0284**

**Gaus Width: 3.4958**



# B0L102S, U6-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

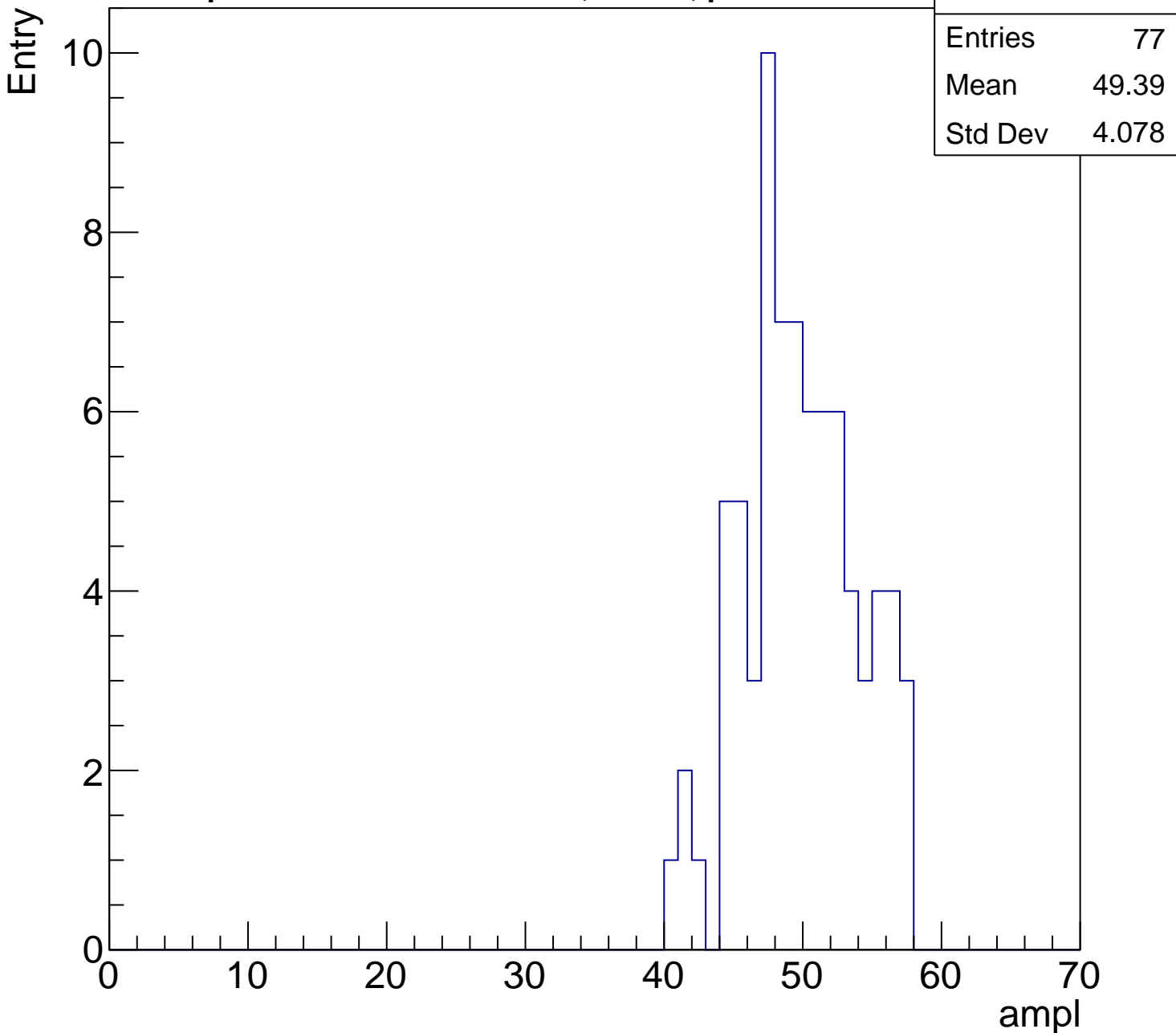
Entries	77
Mean	49.39
Std Dev	4.078

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

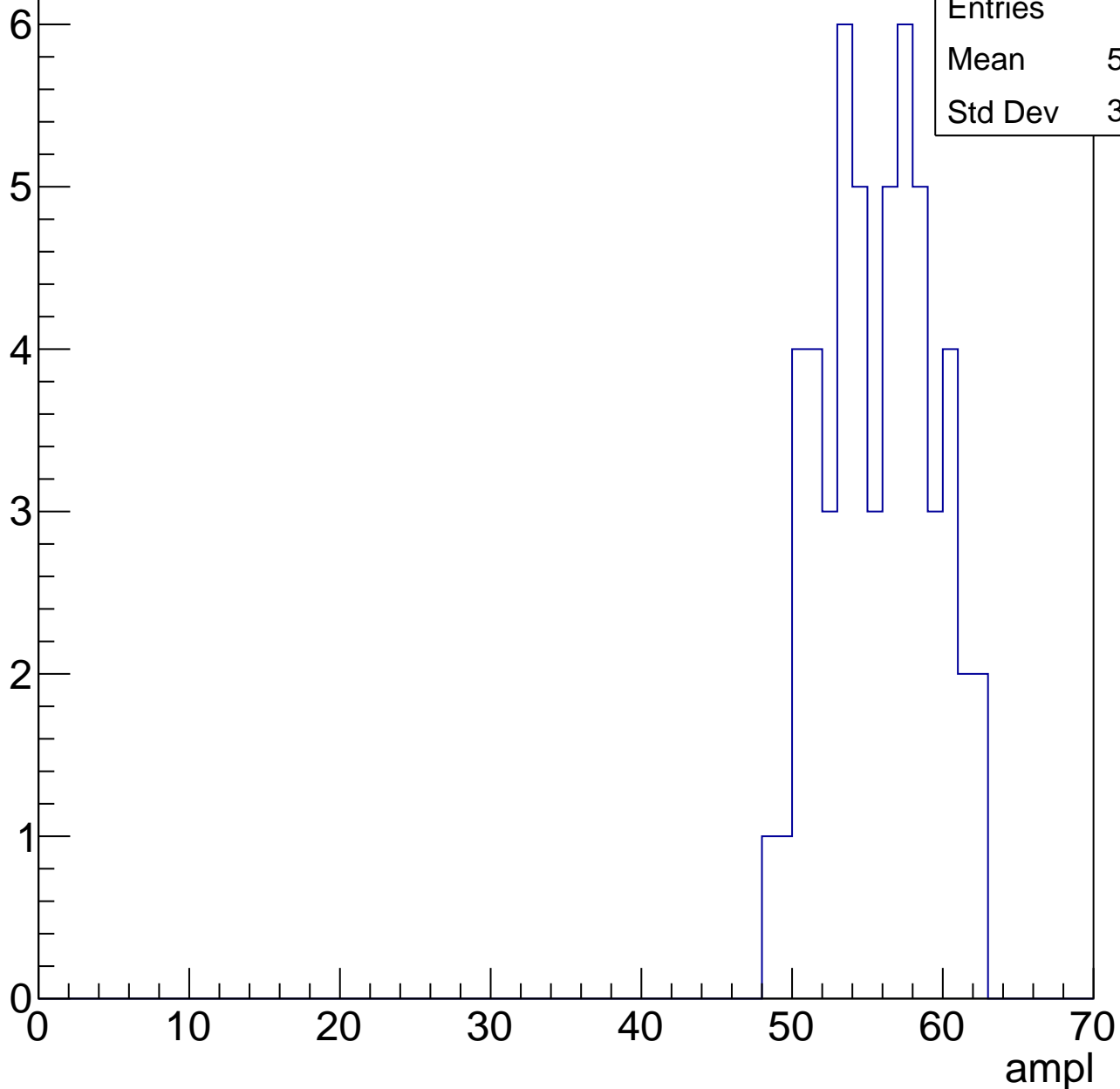
ampl



# B0L102S, U6-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



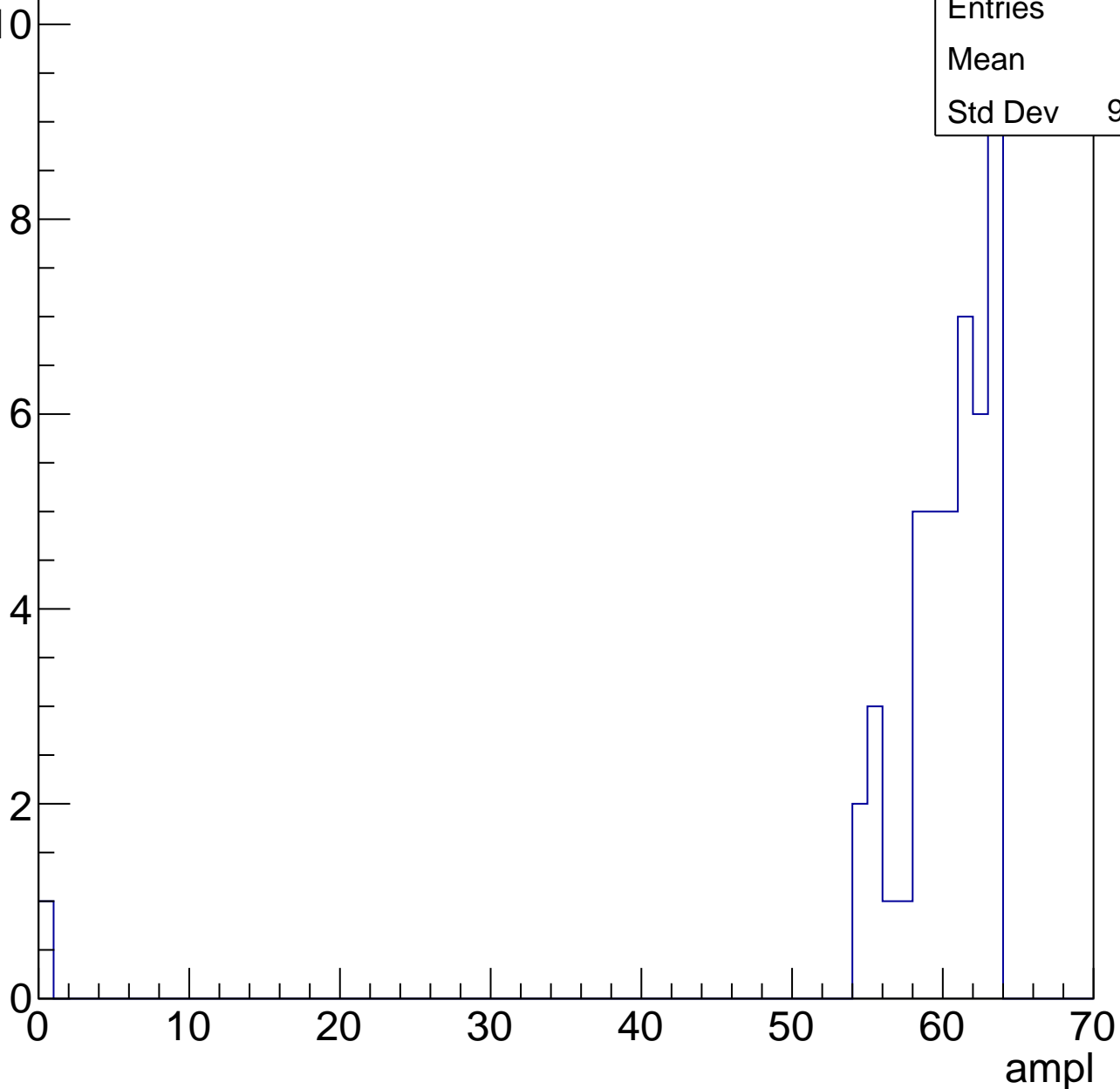
Entries	54
Mean	55.28
Std Dev	3.582

# B0L102S, U6-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	58.7
Std Dev	9.136



# B0L102S, U6-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	60.5
Std Dev	1.258

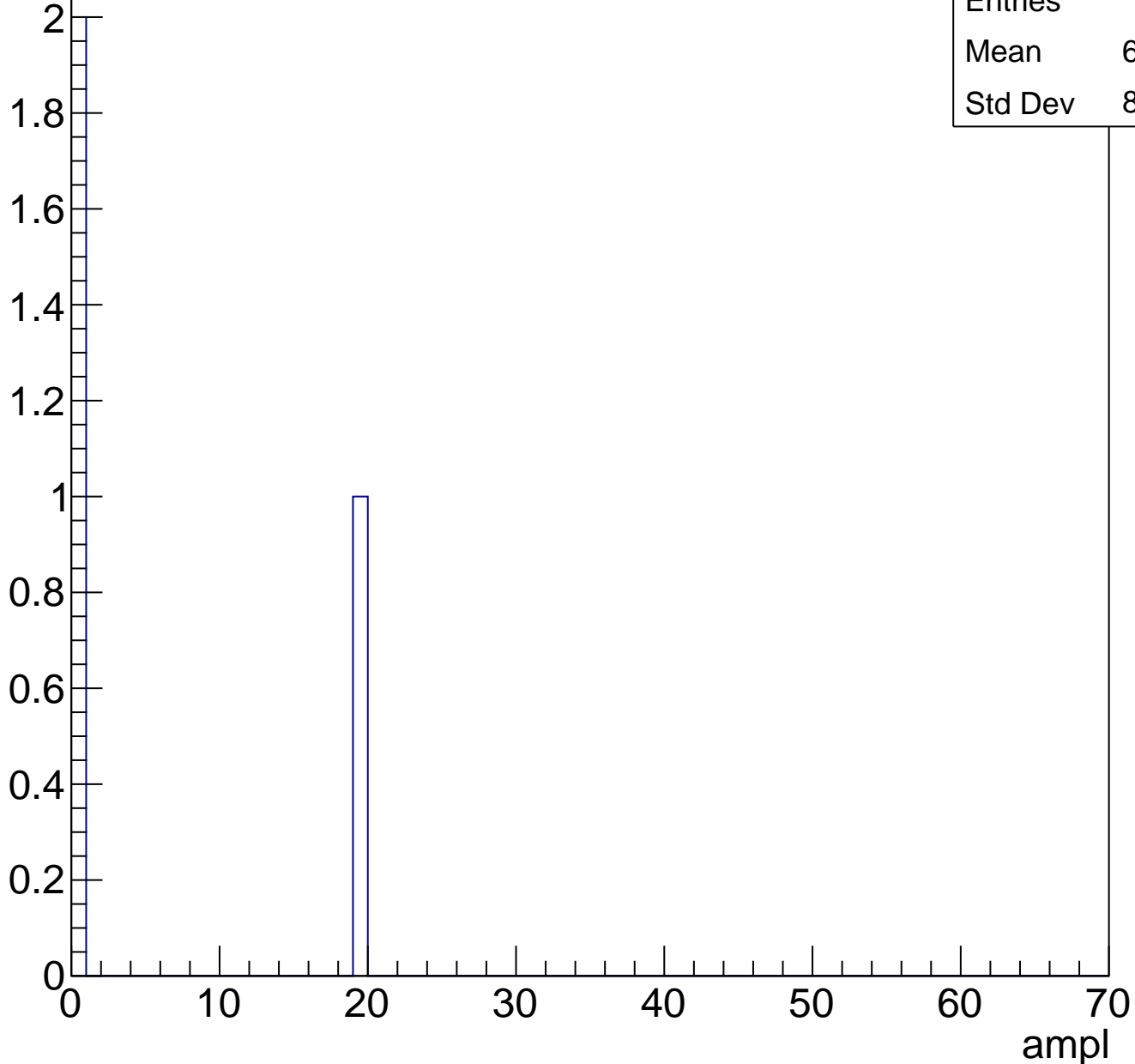
ampl



# B0L102S, U6-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

# B0L102S, U6-ch114, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	85
Mean	27.45
Std Dev	4.905

**Gaus mean : 28.5396**

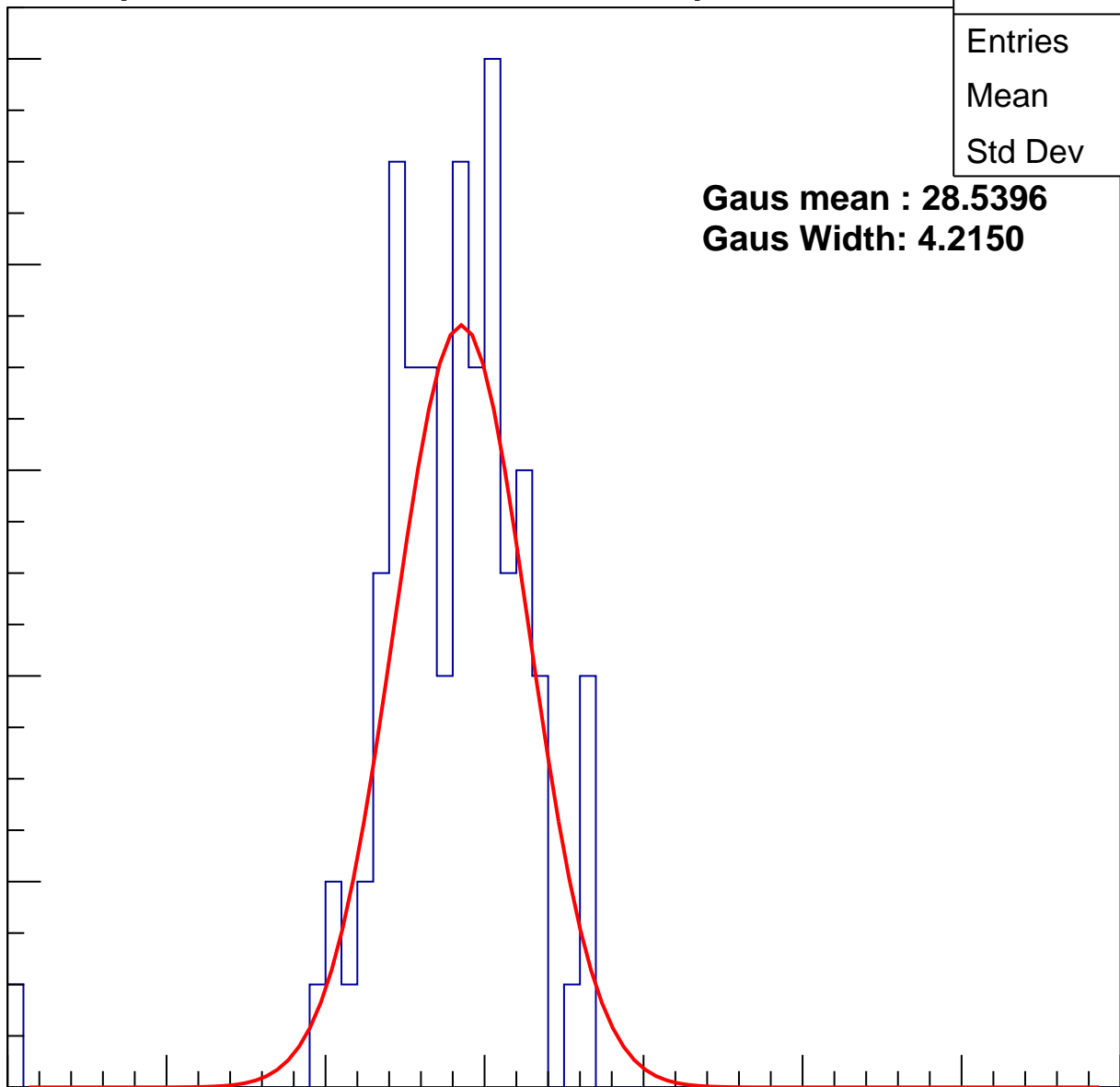
**Gaus Width: 4.2150**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch114, adc1

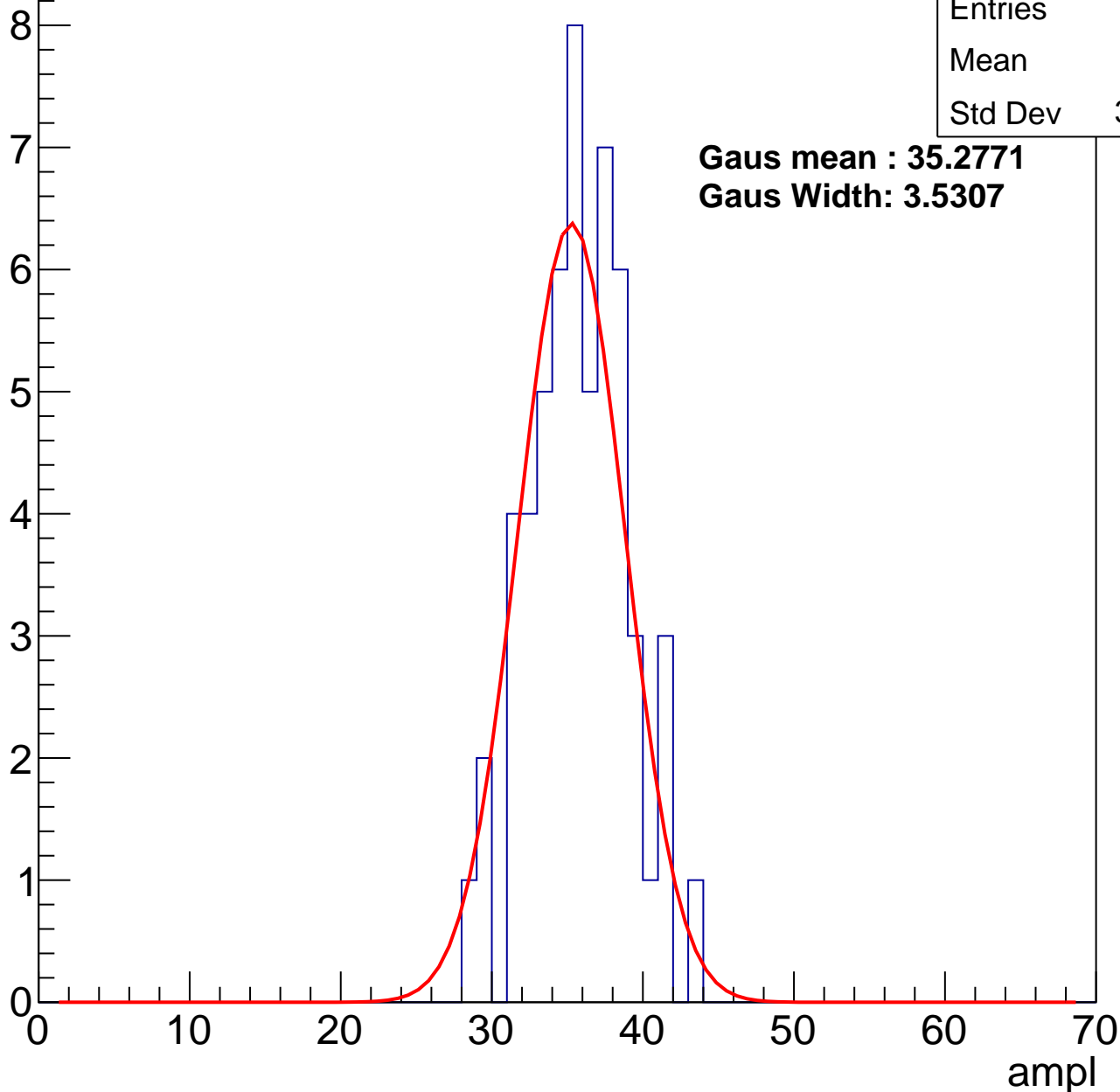
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	35.3
Std Dev	3.201

**Gaus mean : 35.2771**

**Gaus Width: 3.5307**



# B0L102S, U6-ch114, adc2

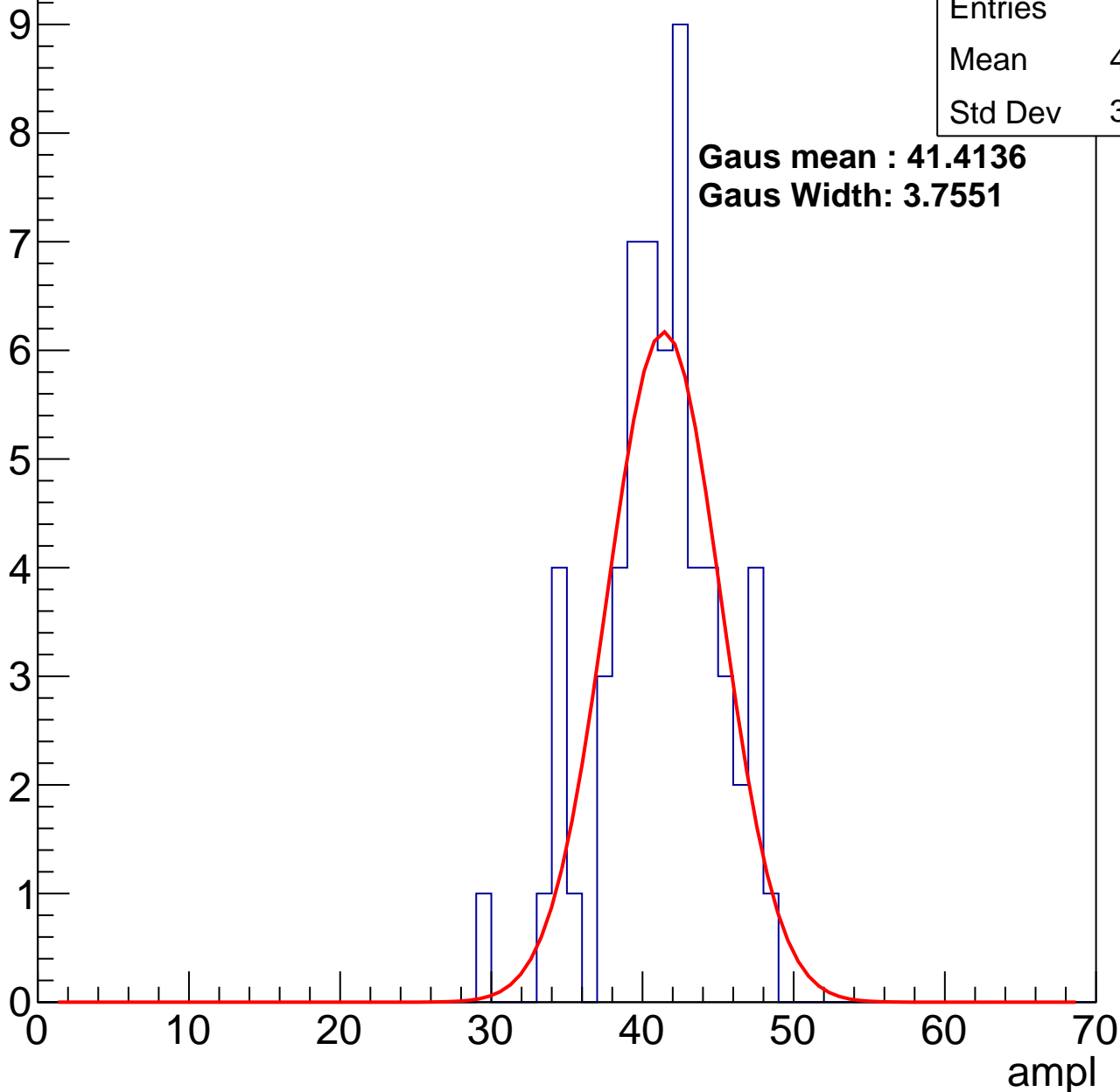
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	40.72
Std Dev	3.854

**Gaus mean : 41.4136**

**Gaus Width: 3.7551**

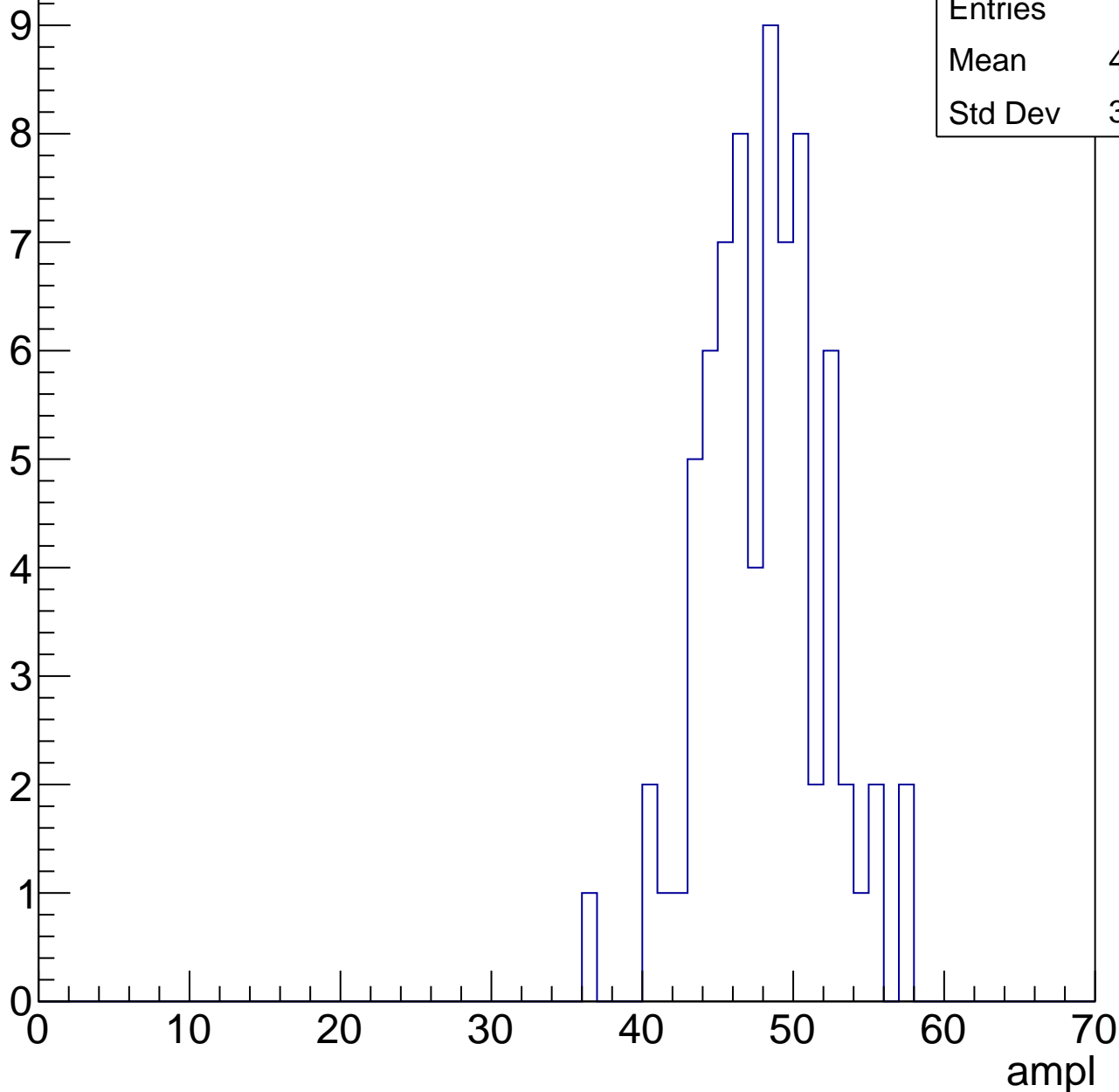


# B0L102S, U6-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

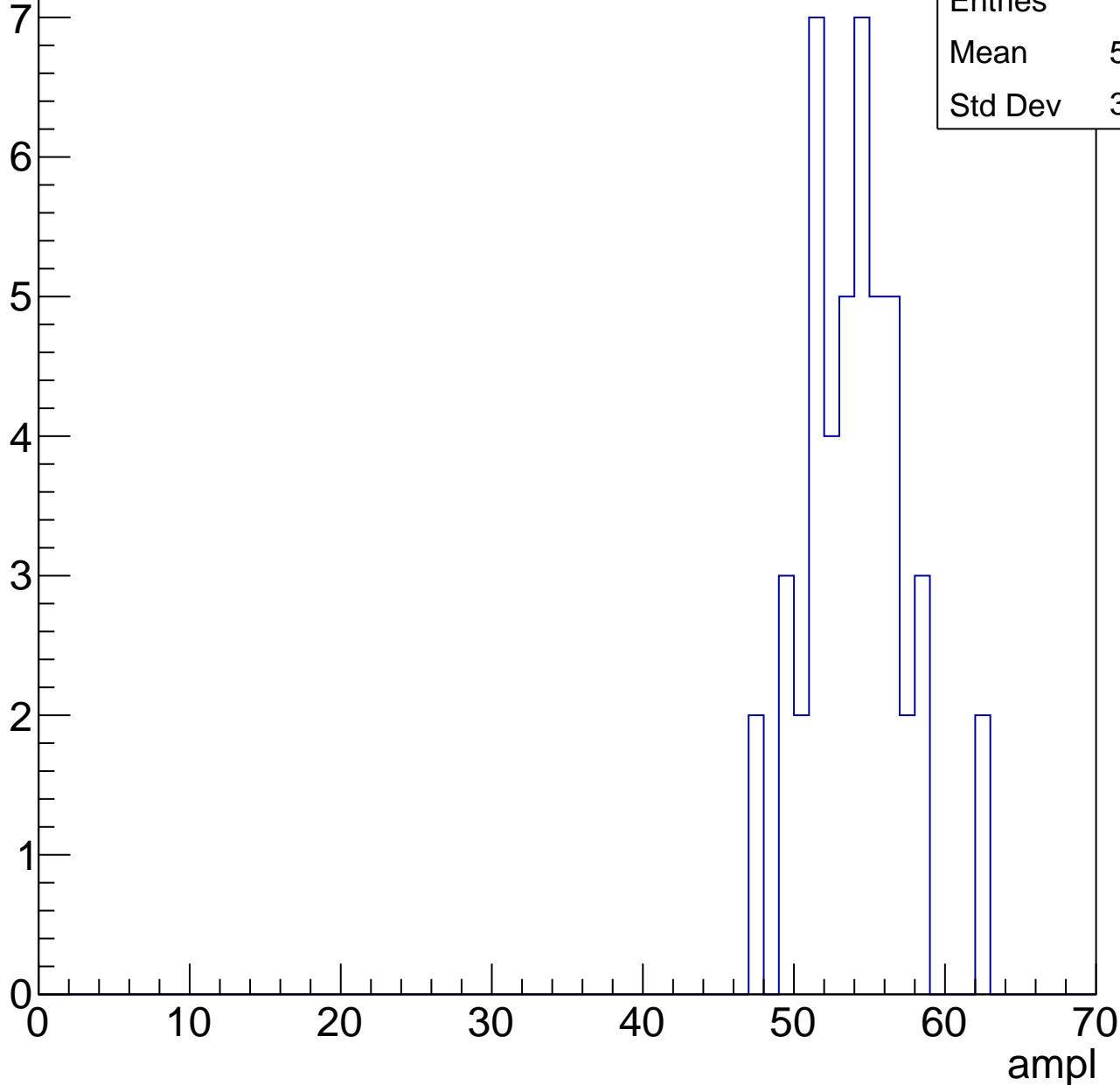
Entries	74
Mean	47.59
Std Dev	3.966



# B0L102S, U6-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



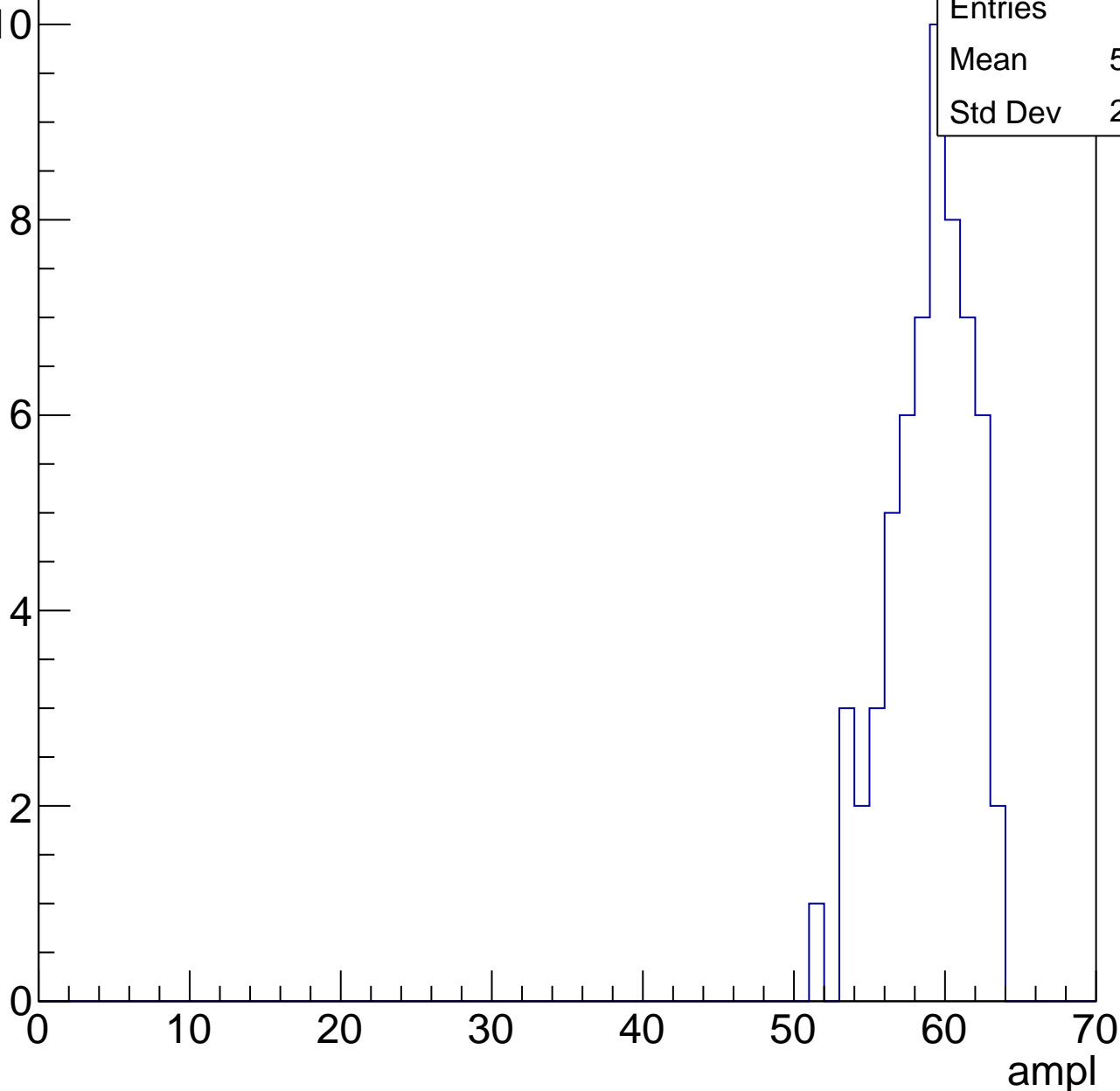
Entries	47
Mean	53.53
Std Dev	3.254

# B0L102S, U6-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	58.43
Std Dev	2.735

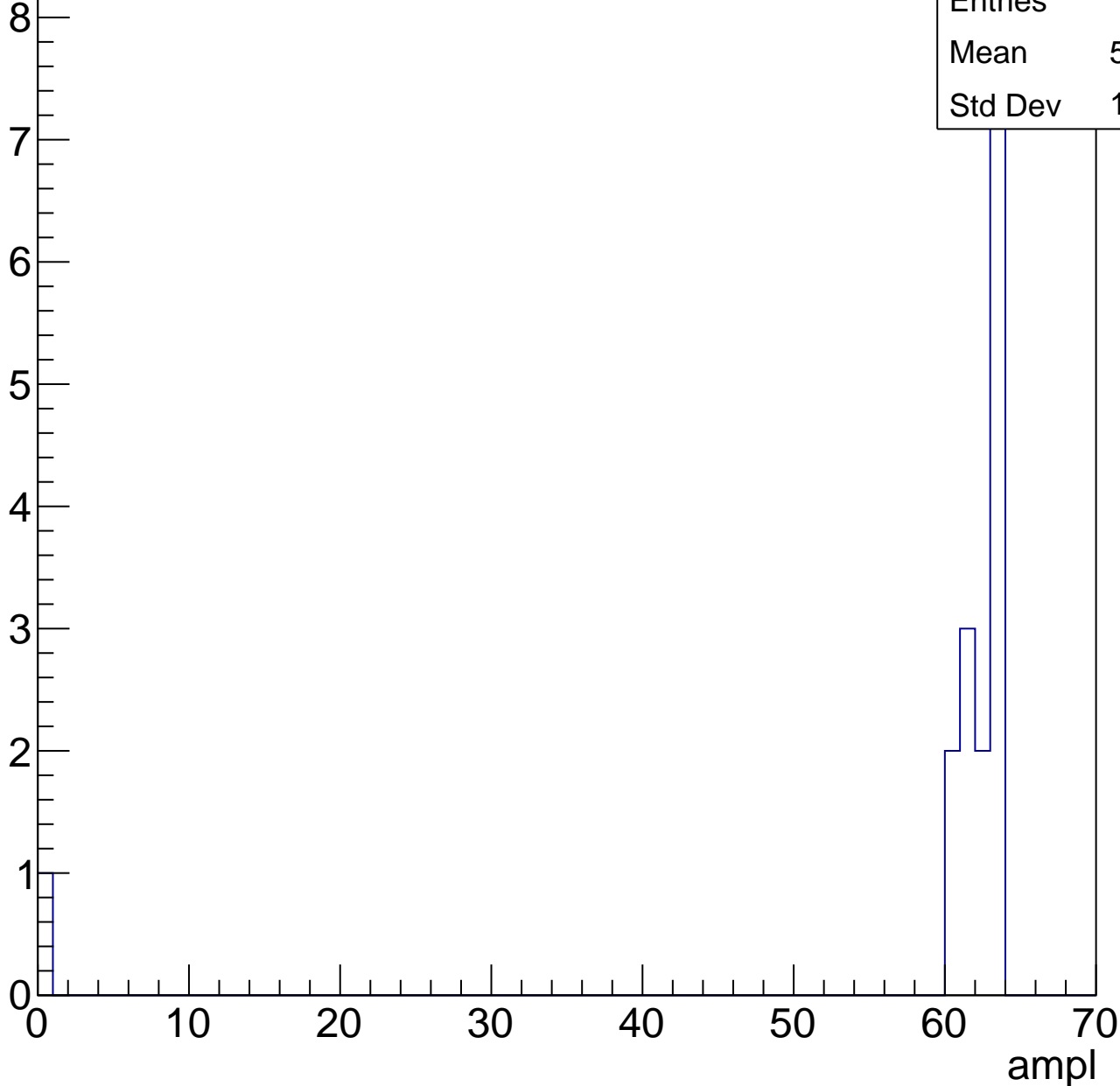


# B0L102S, U6-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	16
Mean	58.19
Std Dev	15.06





# B0L102S, U6-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch115, adc0

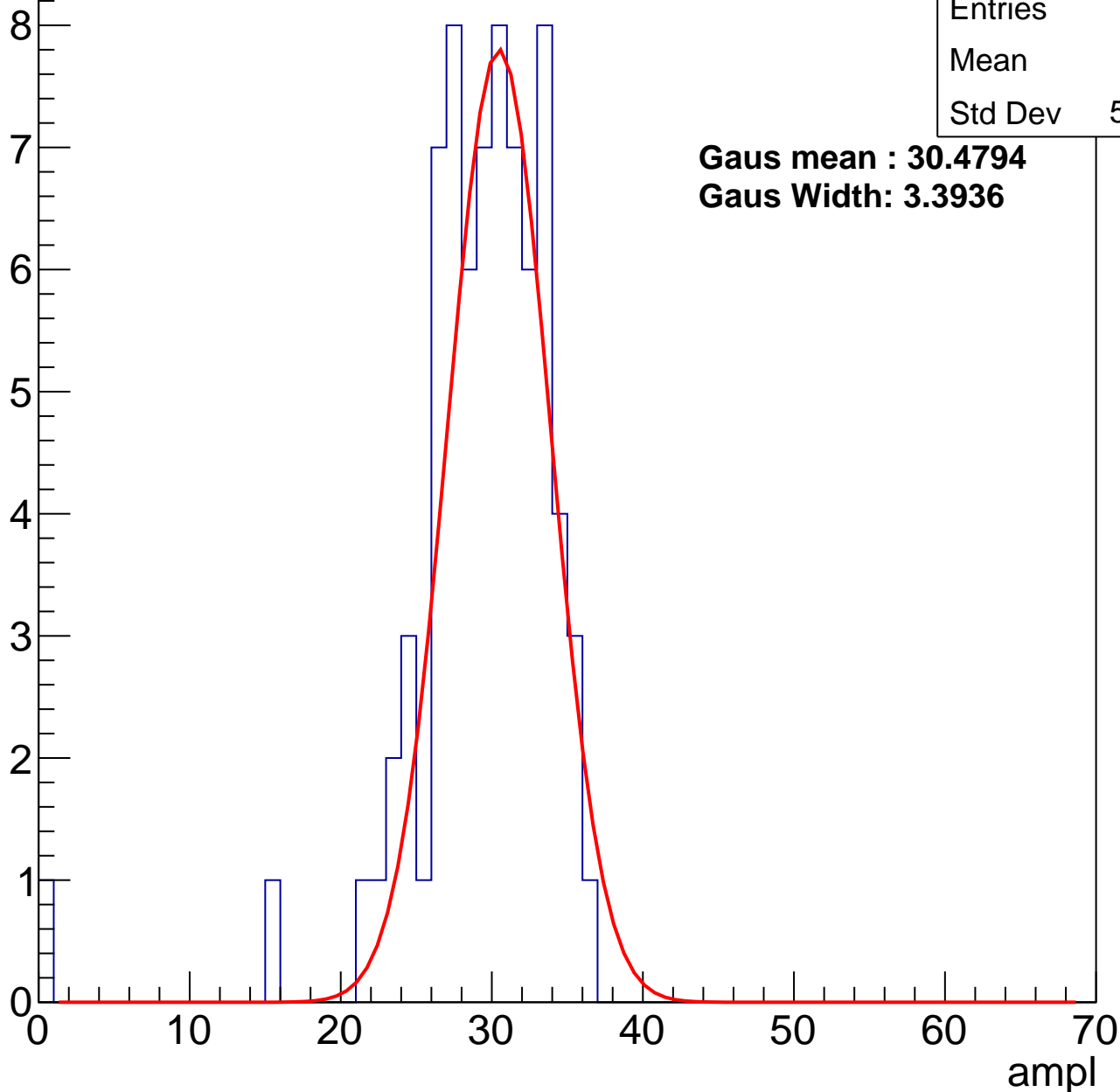
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	28.8
Std Dev	5.015

**Gaus mean : 30.4794**

**Gaus Width: 3.3936**



# B0L102S, U6-ch115, adc1

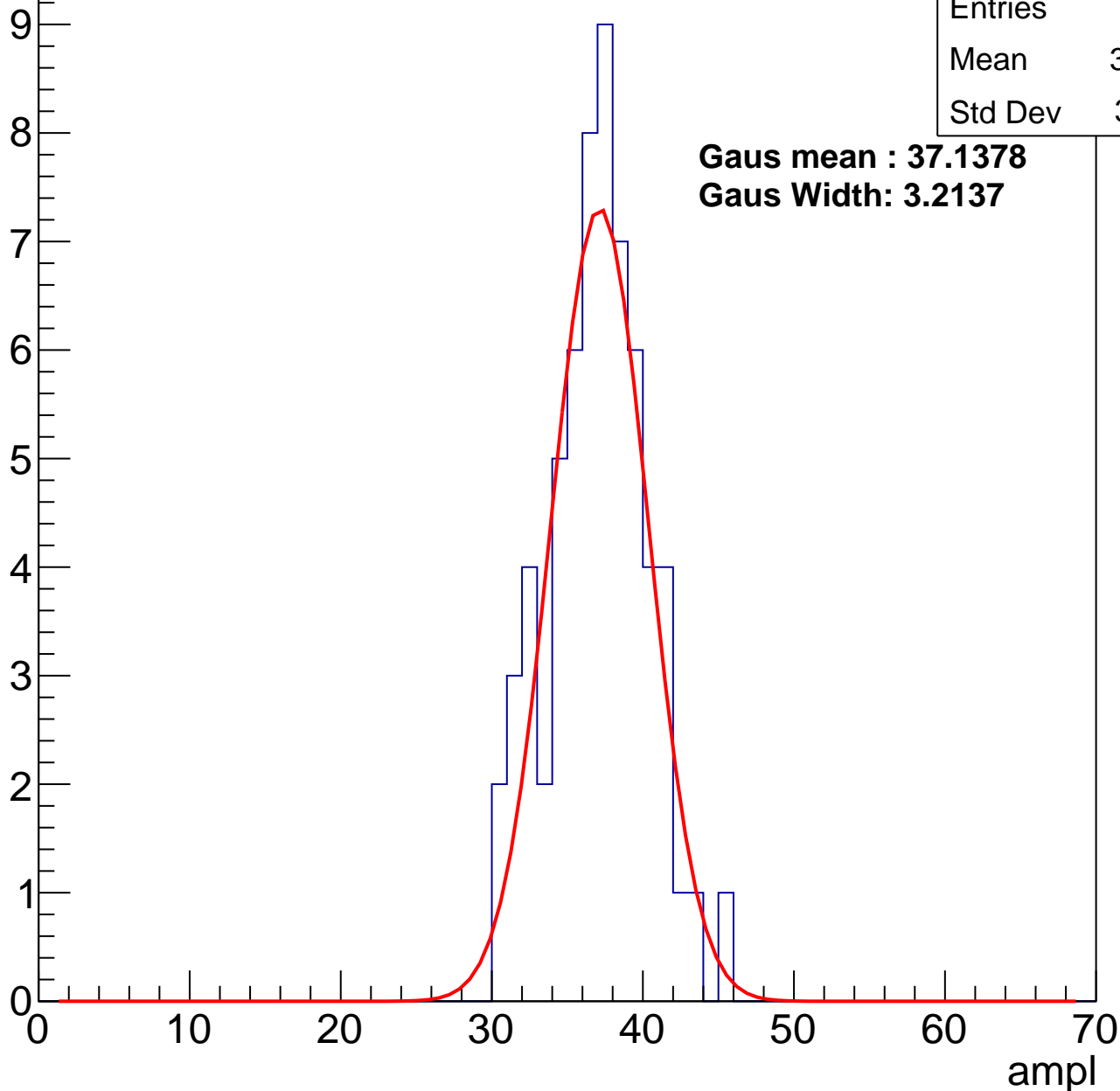
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	36.54
Std Dev	3.231

**Gaus mean : 37.1378**

**Gaus Width: 3.2137**



# B0L102S, U6-ch115, adc2

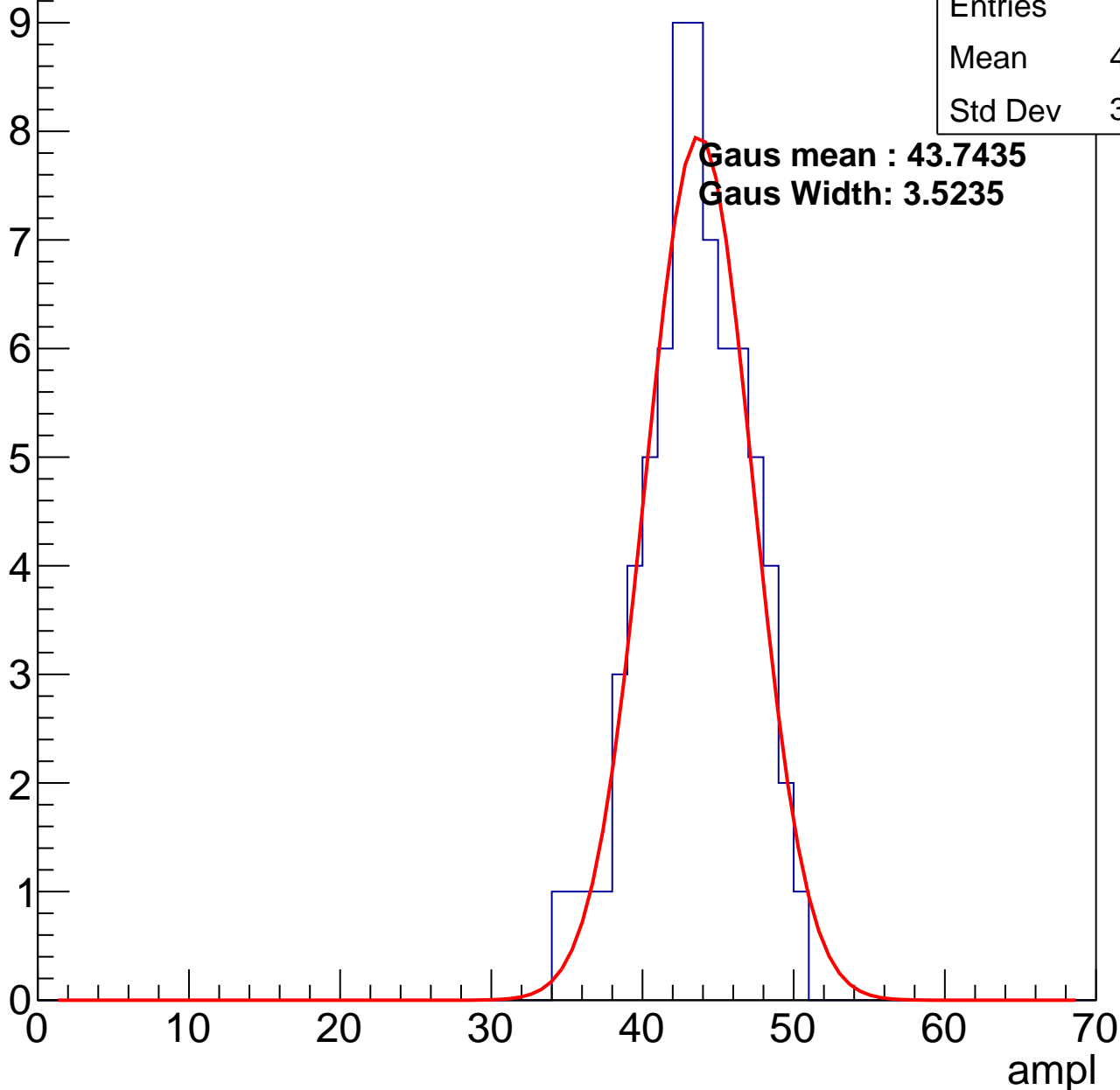
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	42.99
Std Dev	3.425

**Gaus mean : 43.7435**

**Gaus Width: 3.5235**

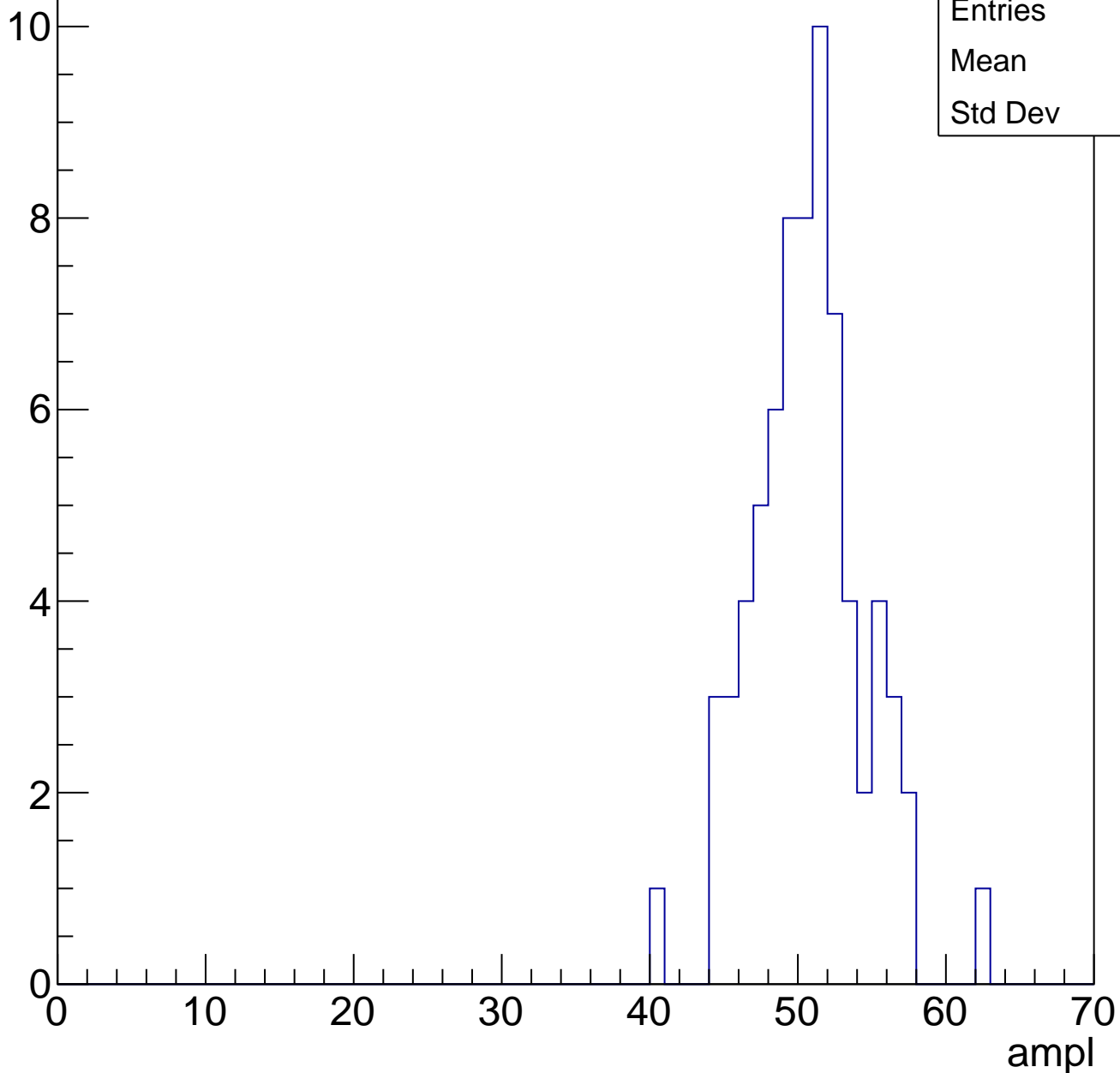


# B0L102S, U6-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	50.2
Std Dev	3.71

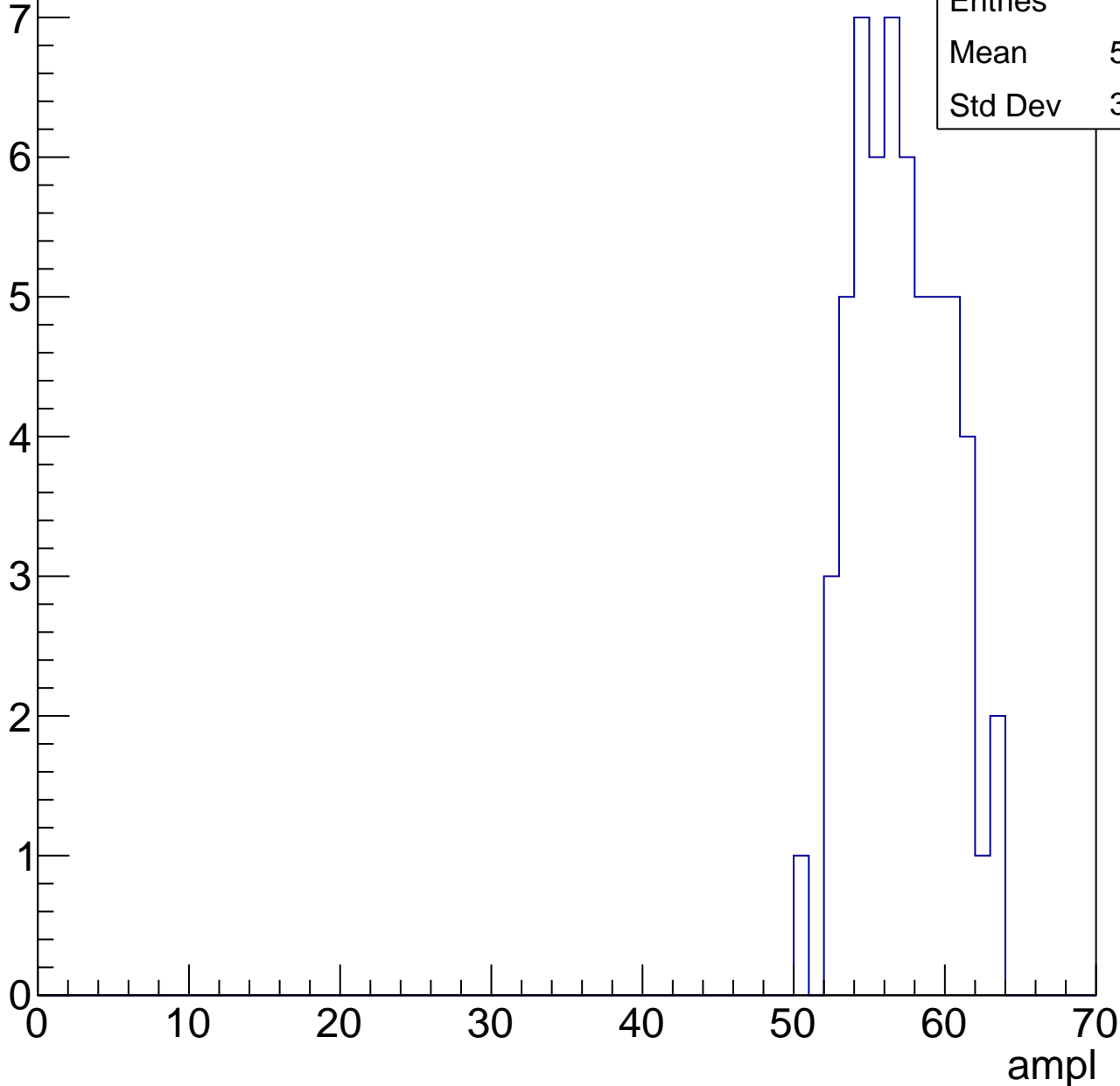


# B0L102S, U6-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	56.67
Std Dev	3.028

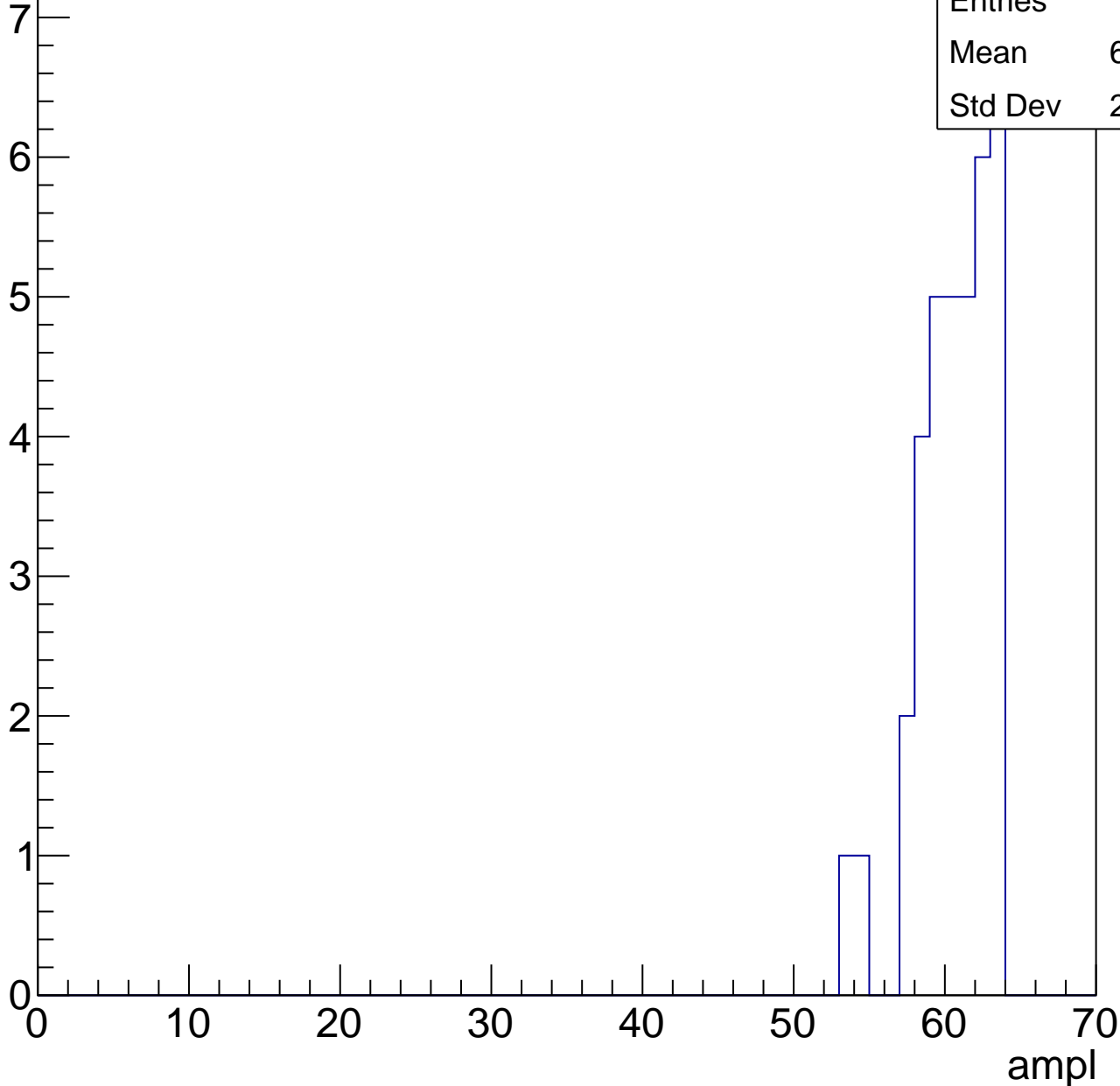


# B0L102S, U6-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	36
Mean	60.17
Std Dev	2.444



# B0L102S, U6-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

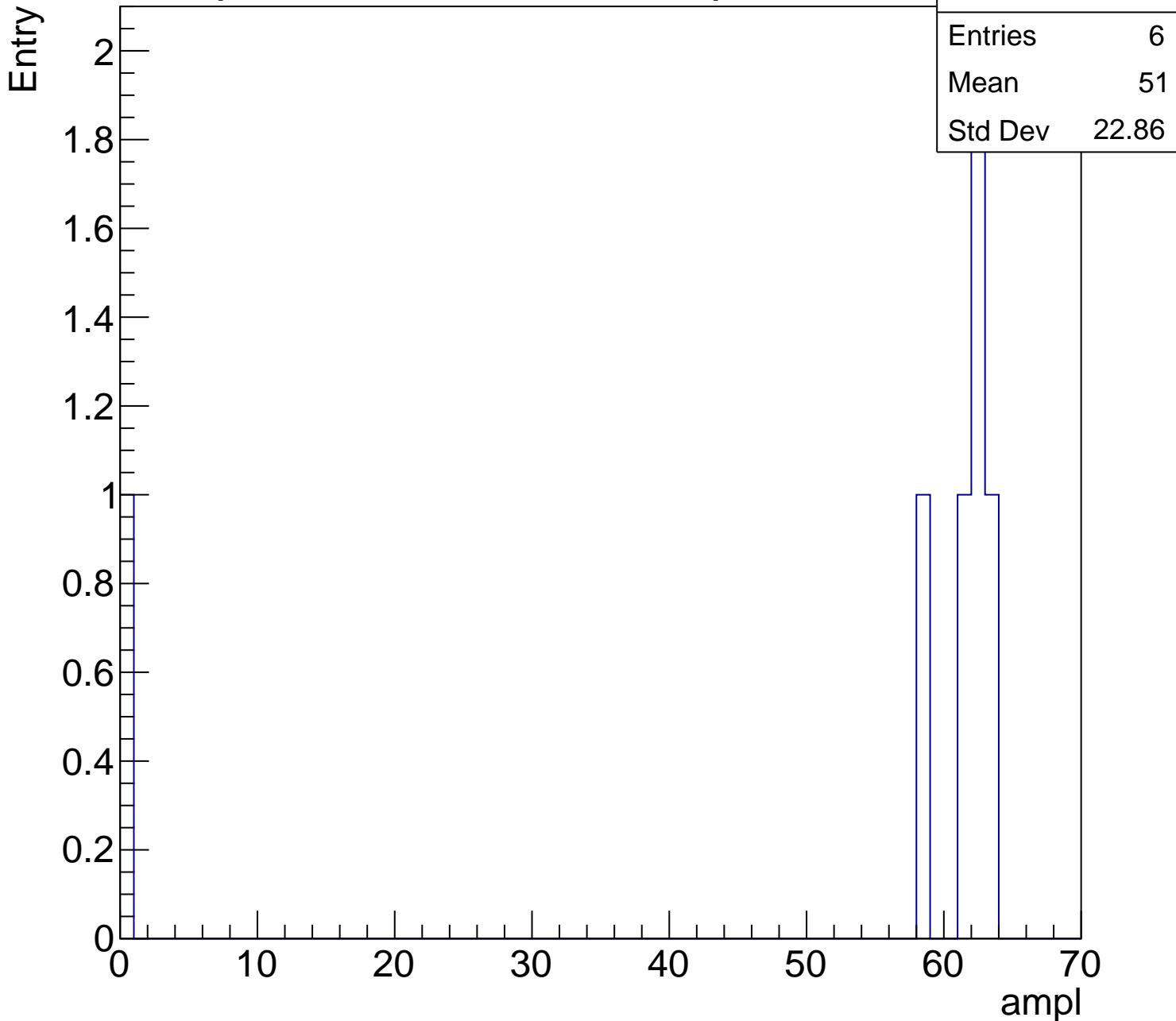
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51
Std Dev	22.86

0 10 20 30 40 50 60 70

ampl





# B0L102S, U6-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch116, adc0

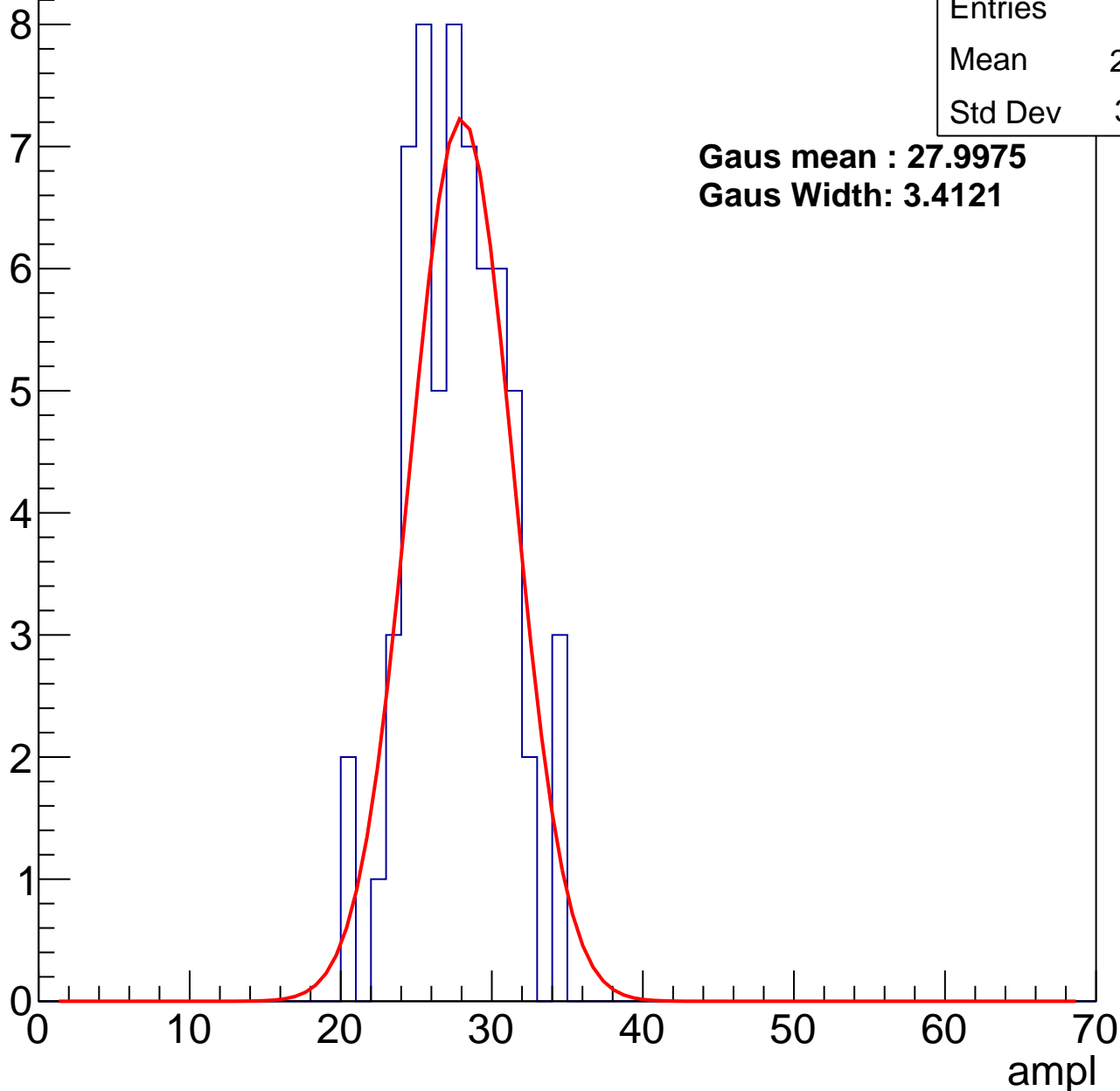
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	27.24
Std Dev	3.161

**Gaus mean : 27.9975**

**Gaus Width: 3.4121**



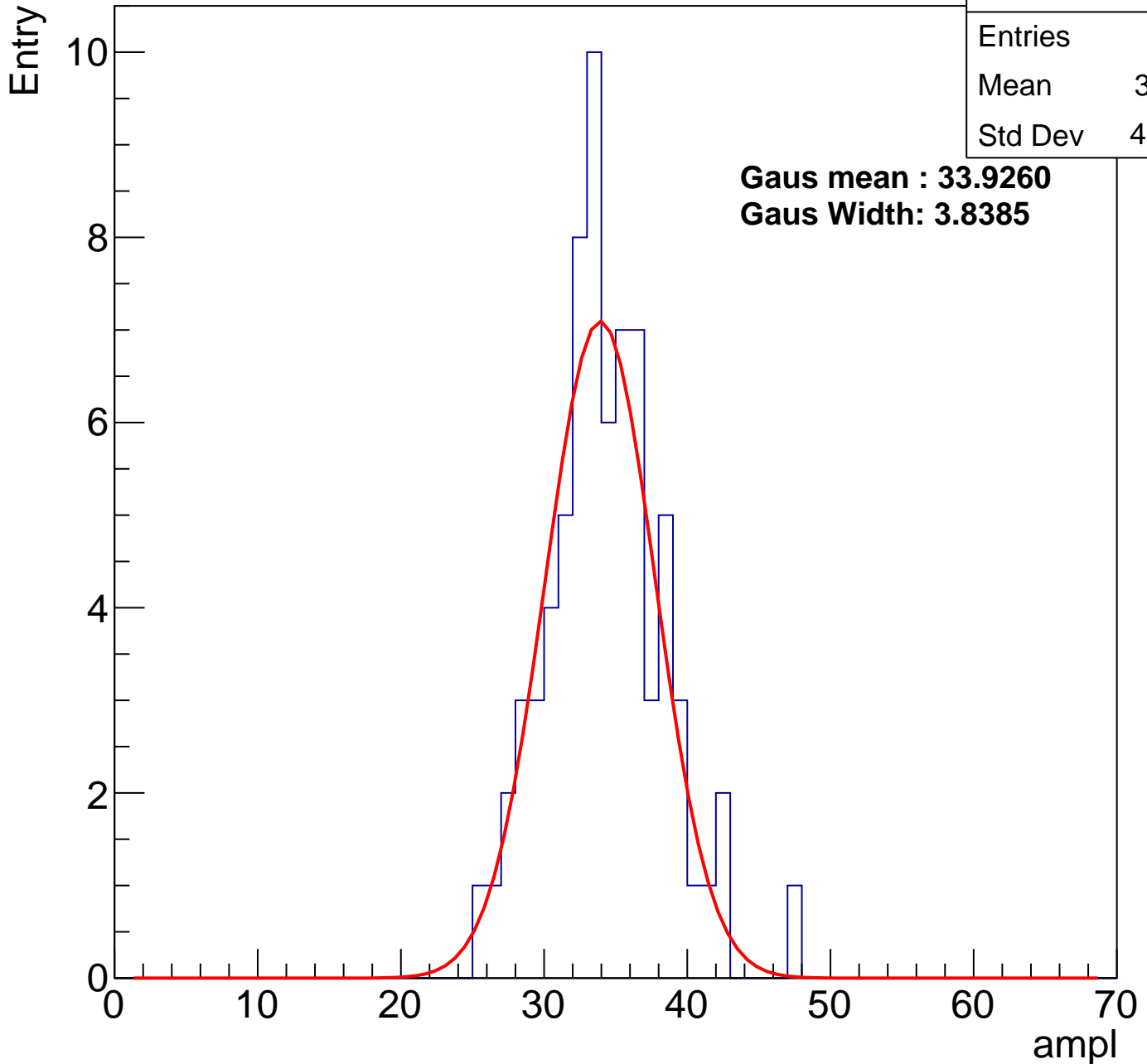
# B0L102S, U6-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	33.81
Std Dev	4.009

**Gaus mean : 33.9260**

**Gaus Width: 3.8385**



# B0L102S, U6-ch116, adc2

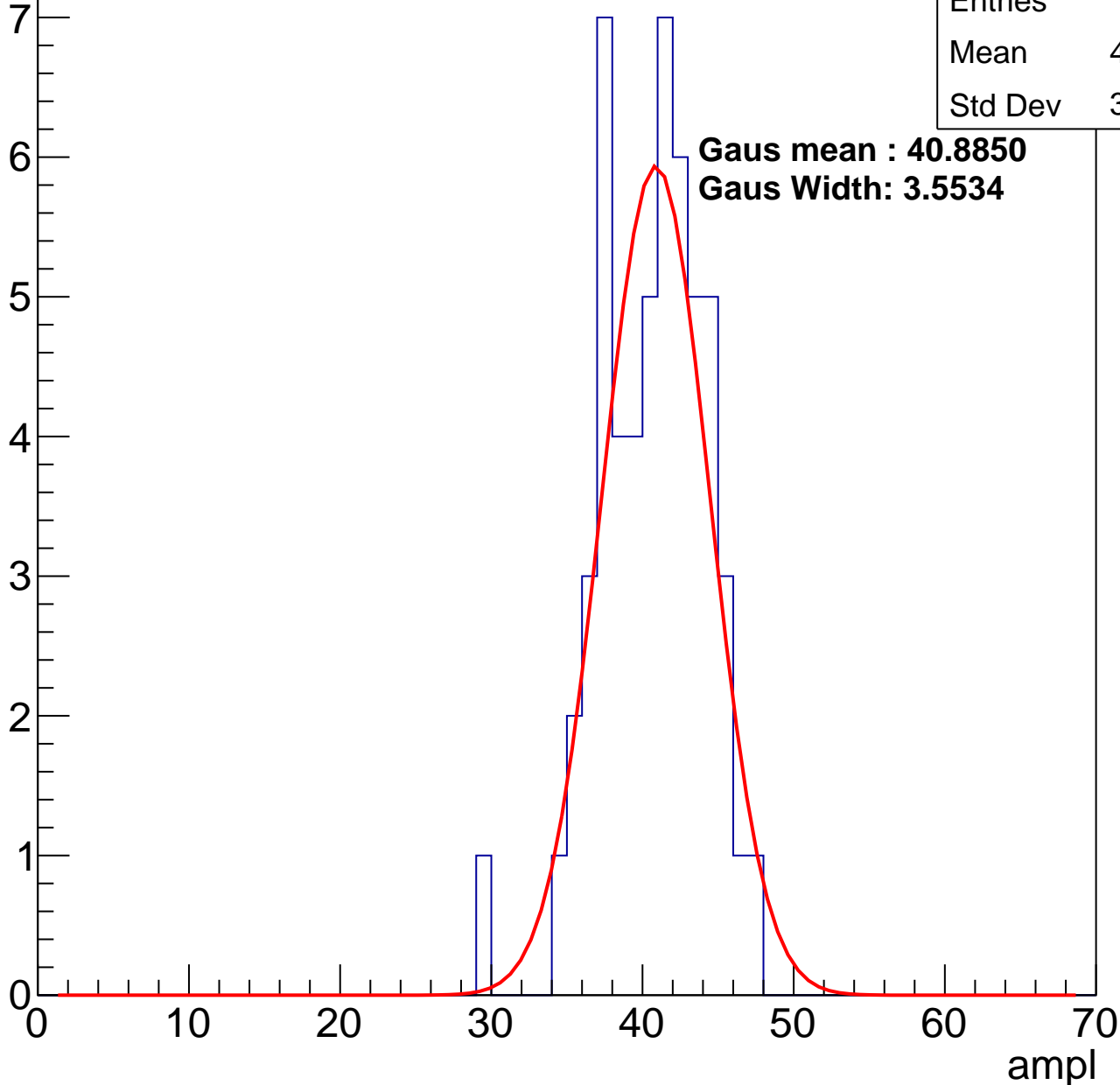
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	40.18
Std Dev	3.444

**Gaus mean : 40.8850**

**Gaus Width: 3.5534**

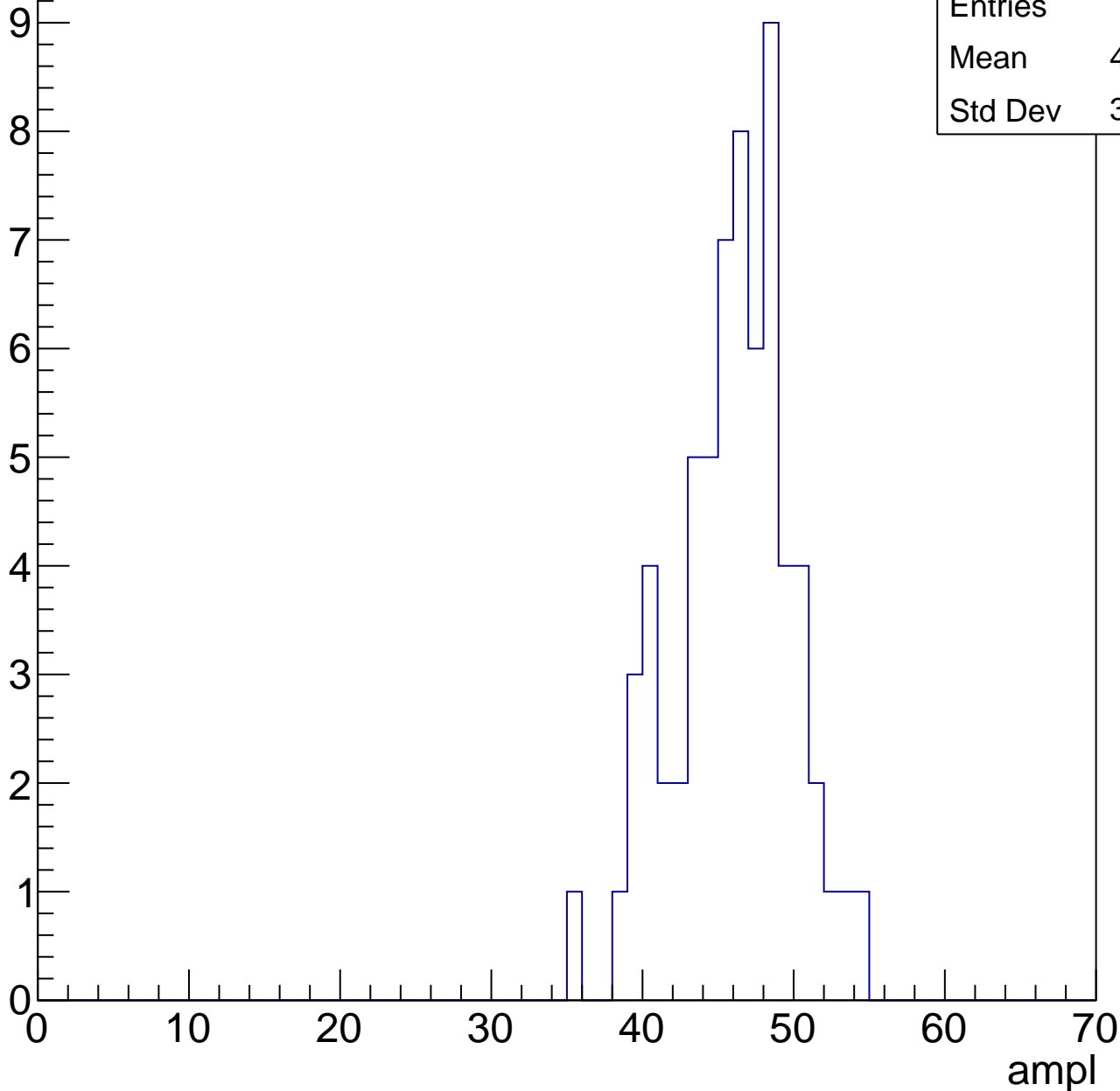


# B0L102S, U6-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	45.53
Std Dev	3.807

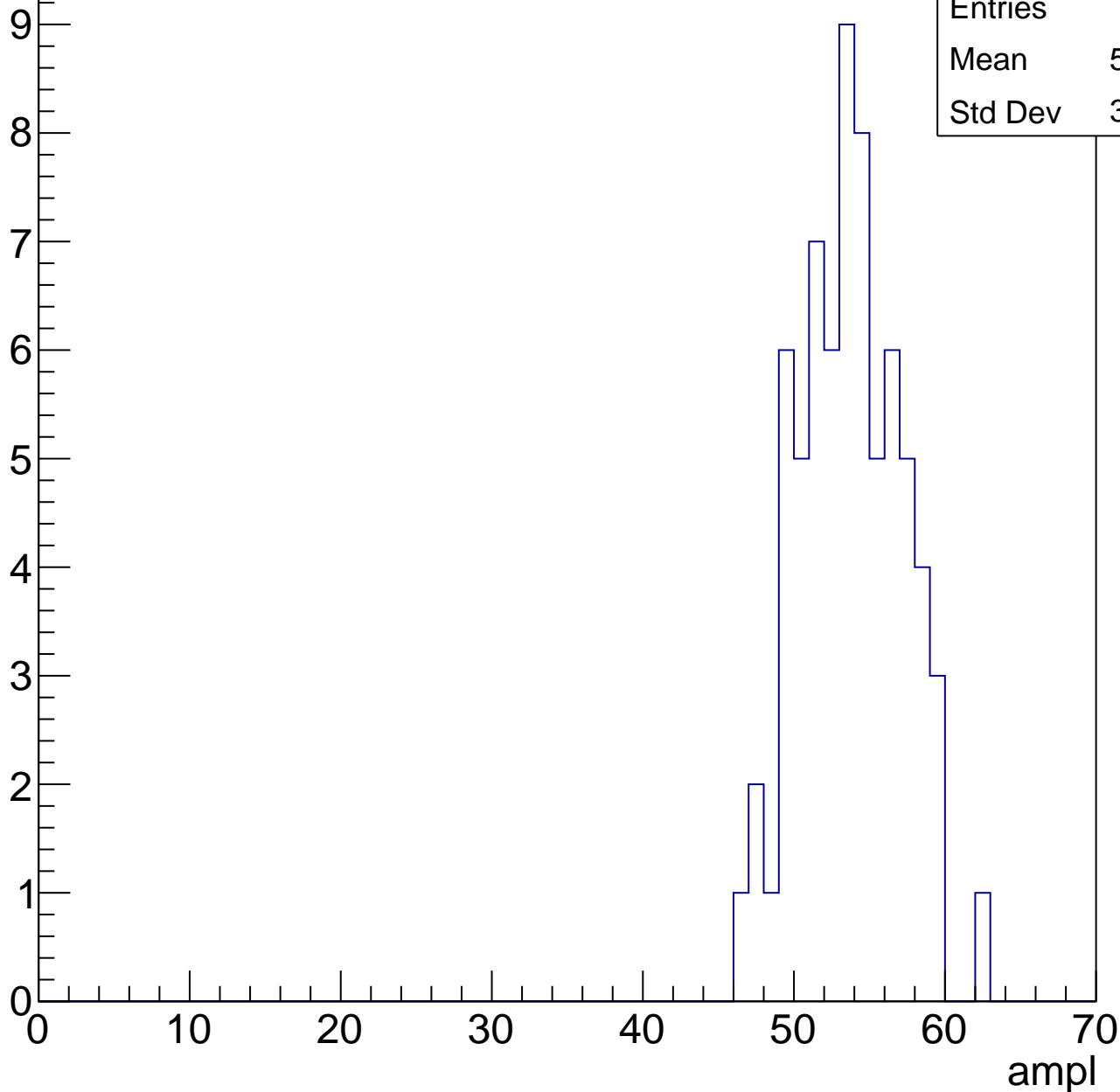


# B0L102S, U6-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	53.29
Std Dev	3.332

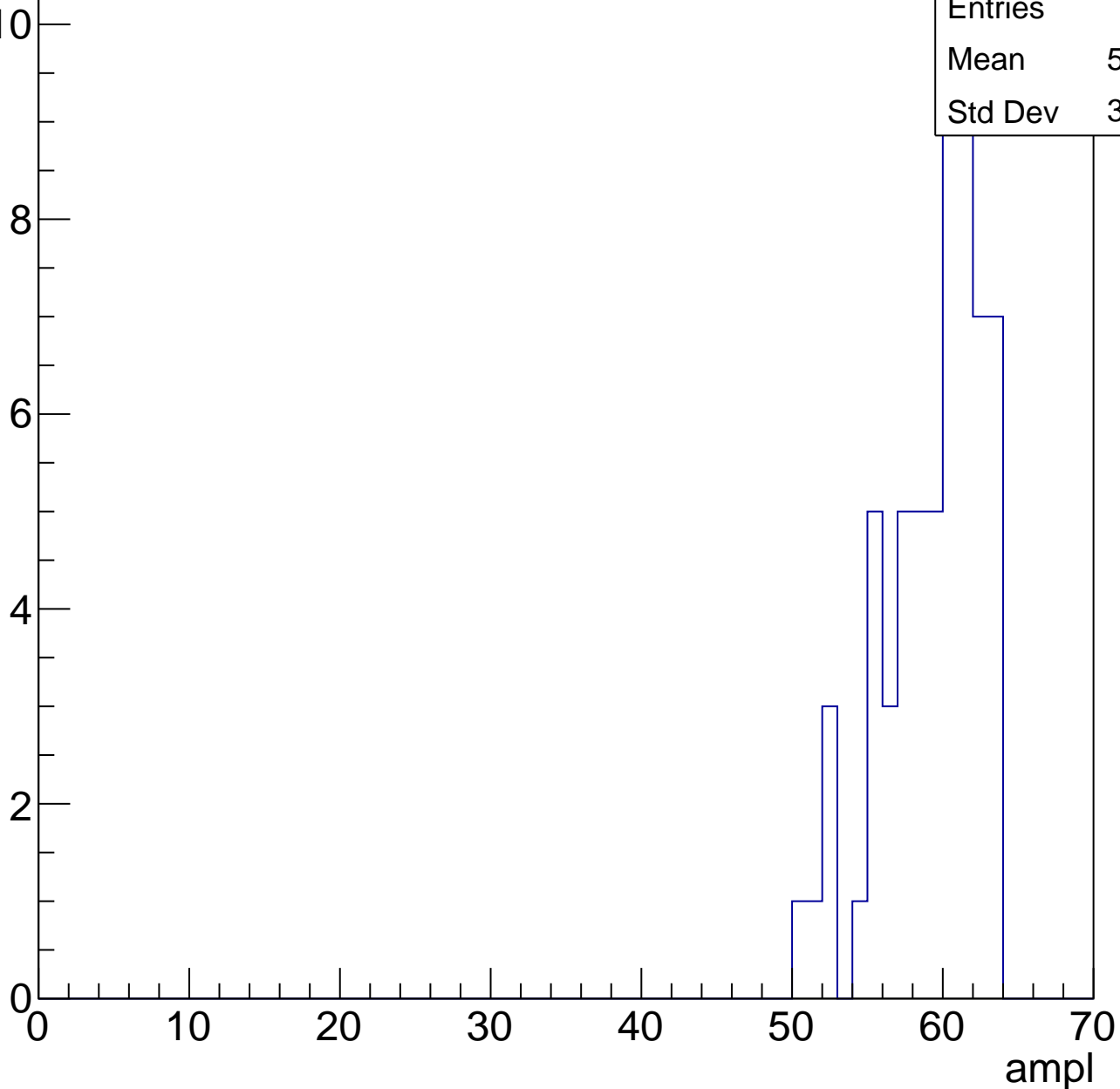


# B0L102S, U6-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

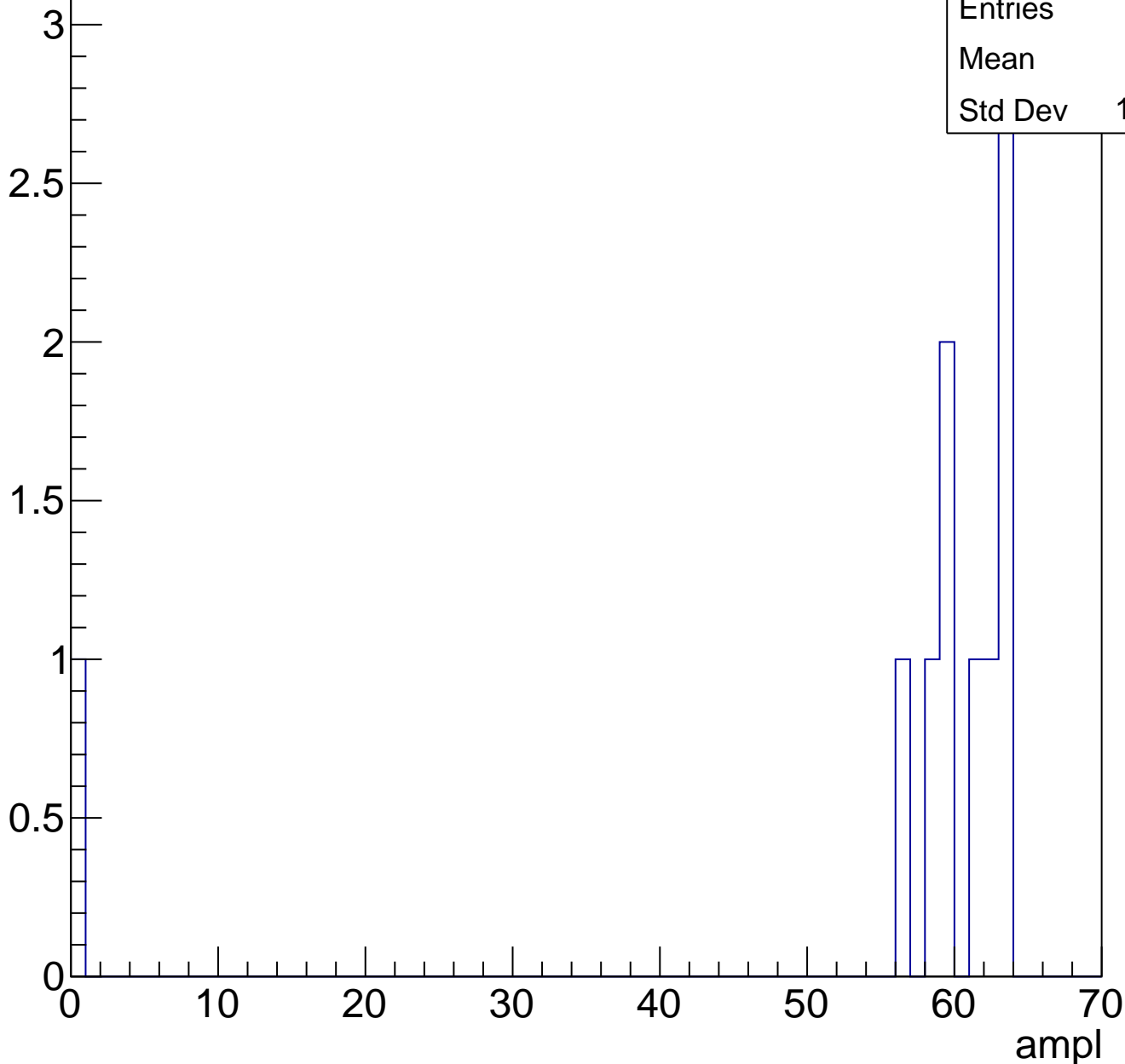
Entries	62
Mean	58.84
Std Dev	3.283



# B0L102S, U6-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B0L102S, U6-ch117, adc0

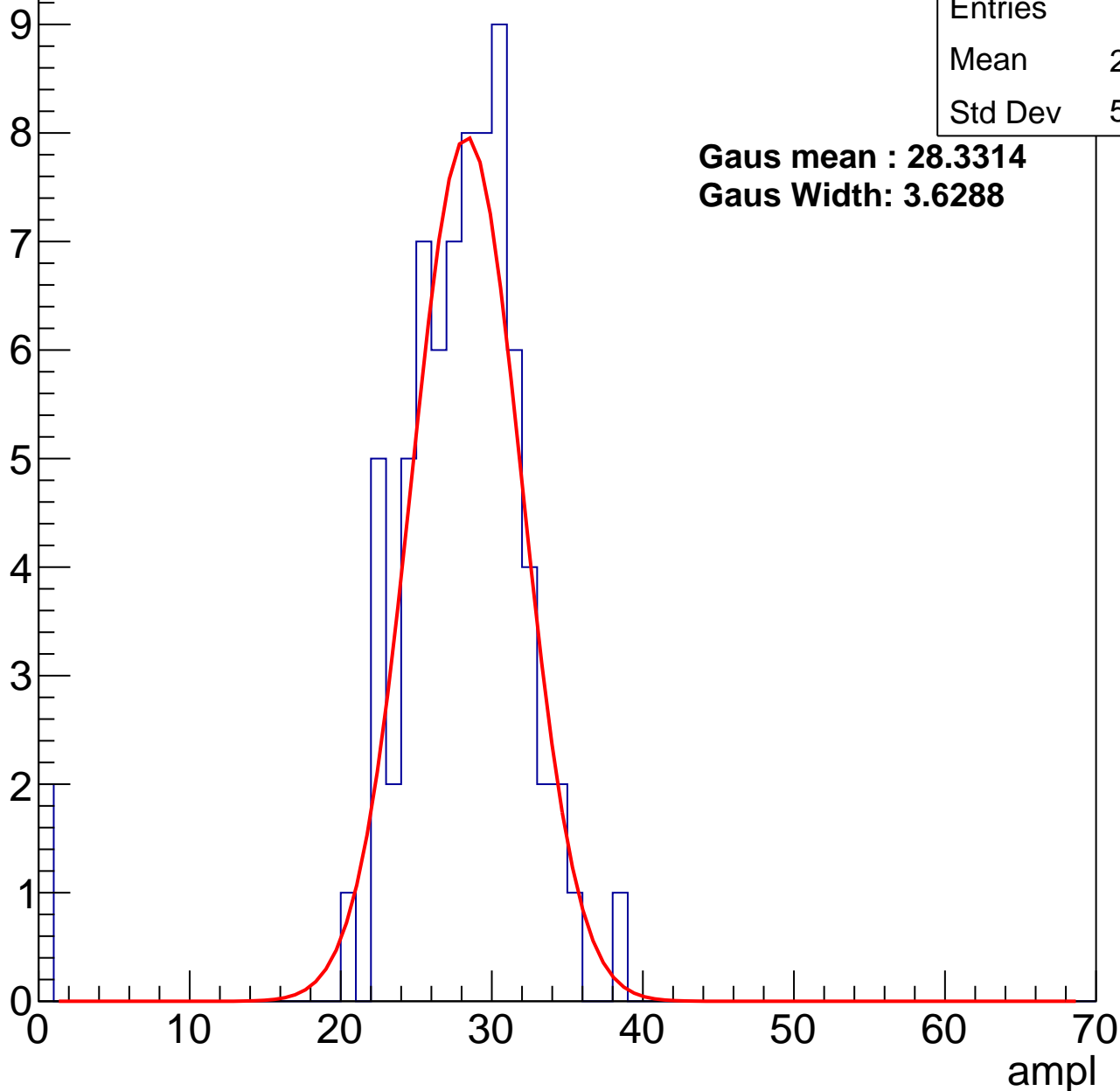
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	27.14
Std Dev	5.635

**Gaus mean : 28.3314**

**Gaus Width: 3.6288**



# B0L102S, U6-ch117, adc1

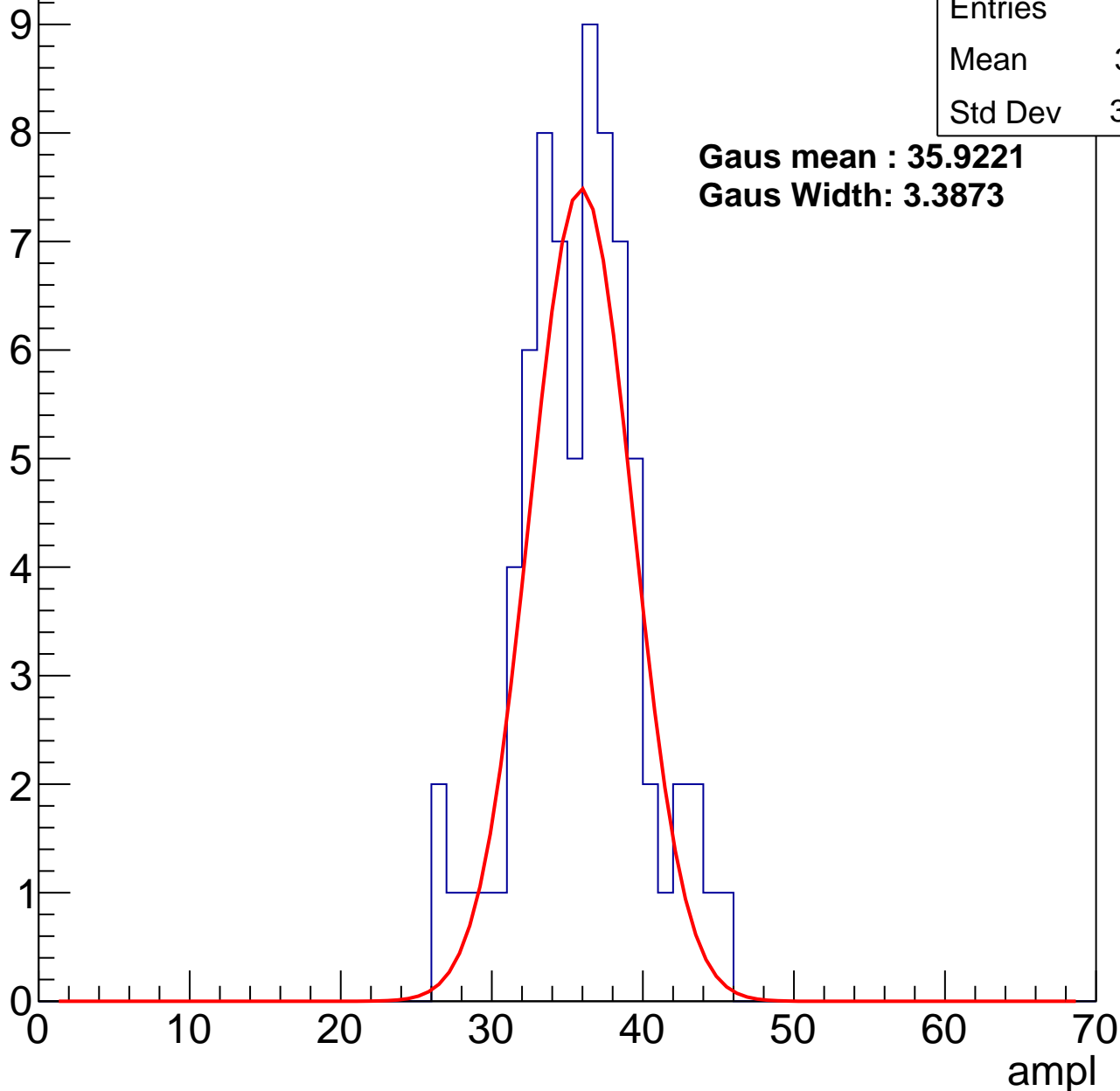
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	35.41
Std Dev	3.956

**Gaus mean : 35.9221**

**Gaus Width: 3.3873**



# B0L102S, U6-ch117, adc2

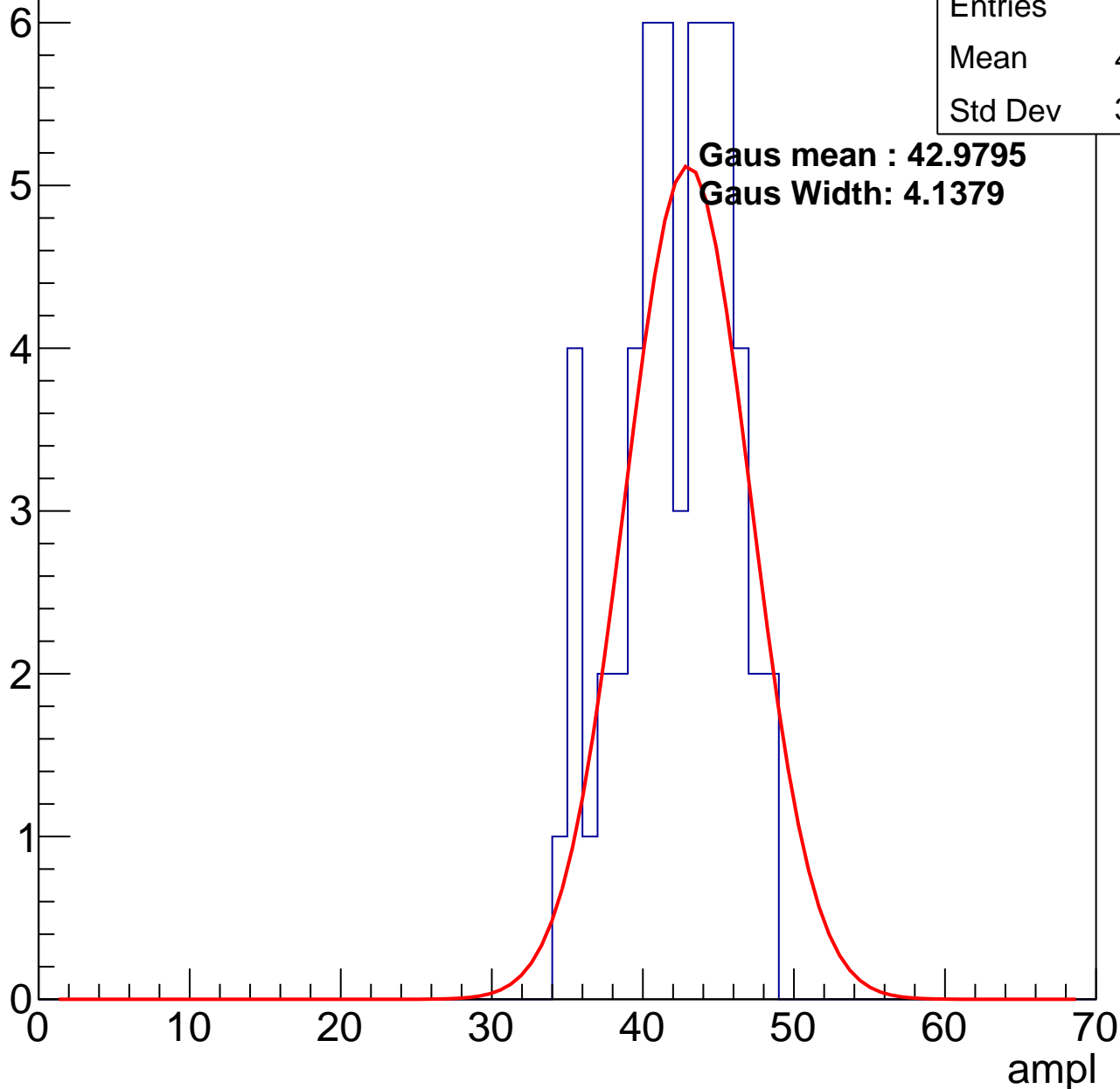
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	41.71
Std Dev	3.591

**Gaus mean : 42.9795**

**Gaus Width: 4.1379**

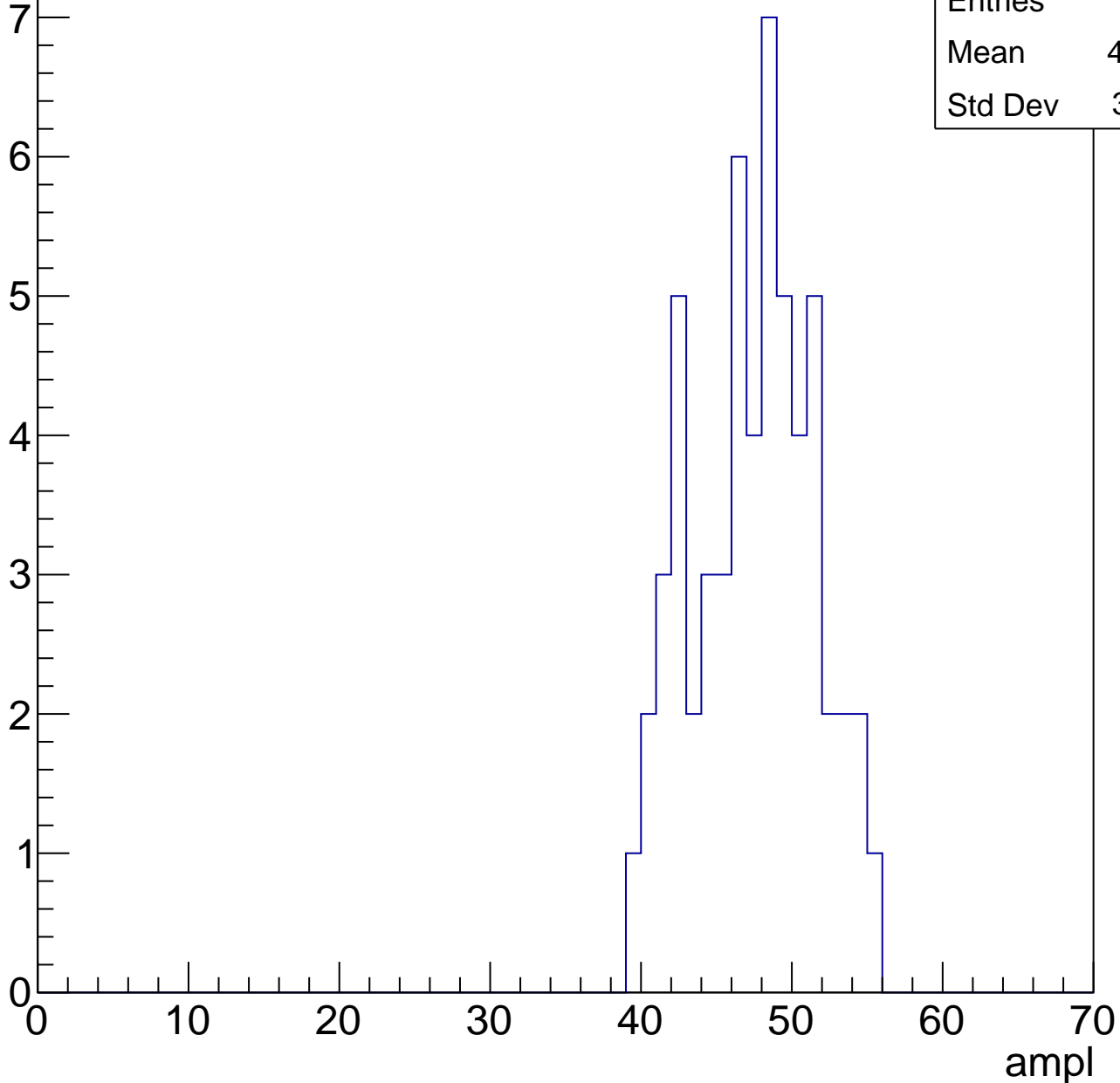


# B0L102S, U6-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	46.98
Std Dev	3.971

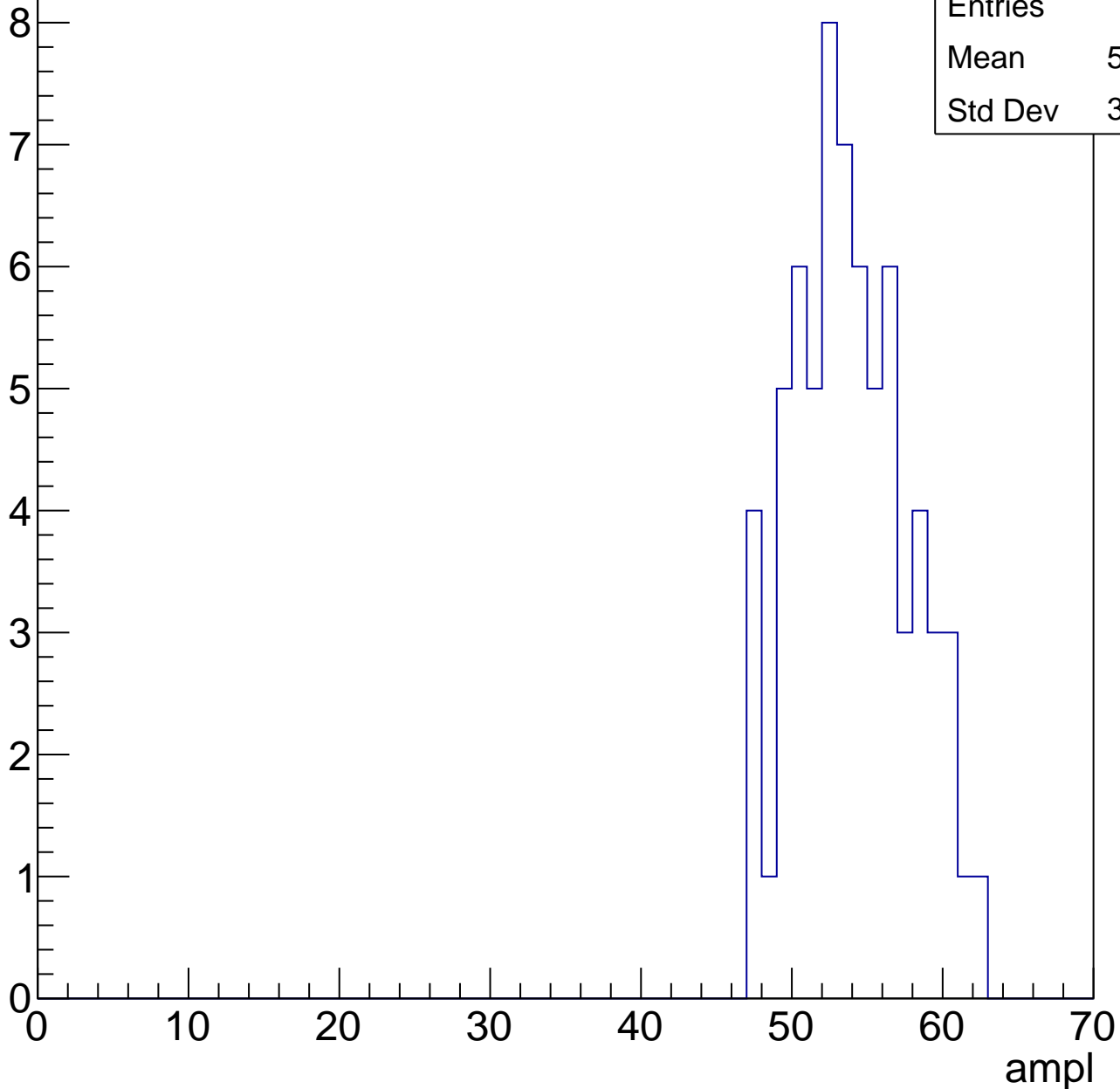


# B0L102S, U6-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	53.54
Std Dev	3.716

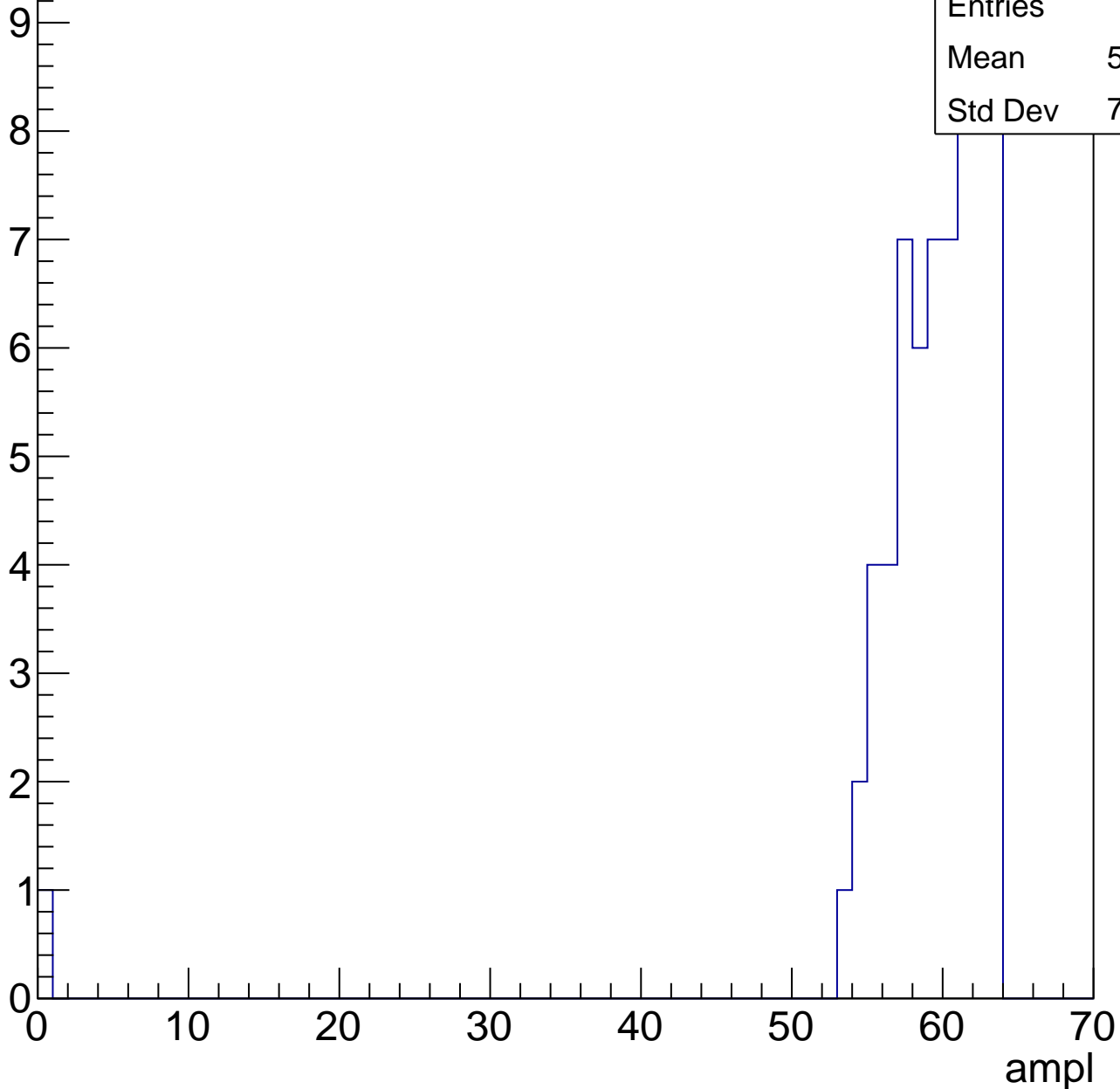


# B0L102S, U6-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	58.36
Std Dev	7.829



# B0L102S, U6-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl





# B0L102S, U6-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	20
Std Dev	0

ampl

# B0L102S, U6-ch118, adc0

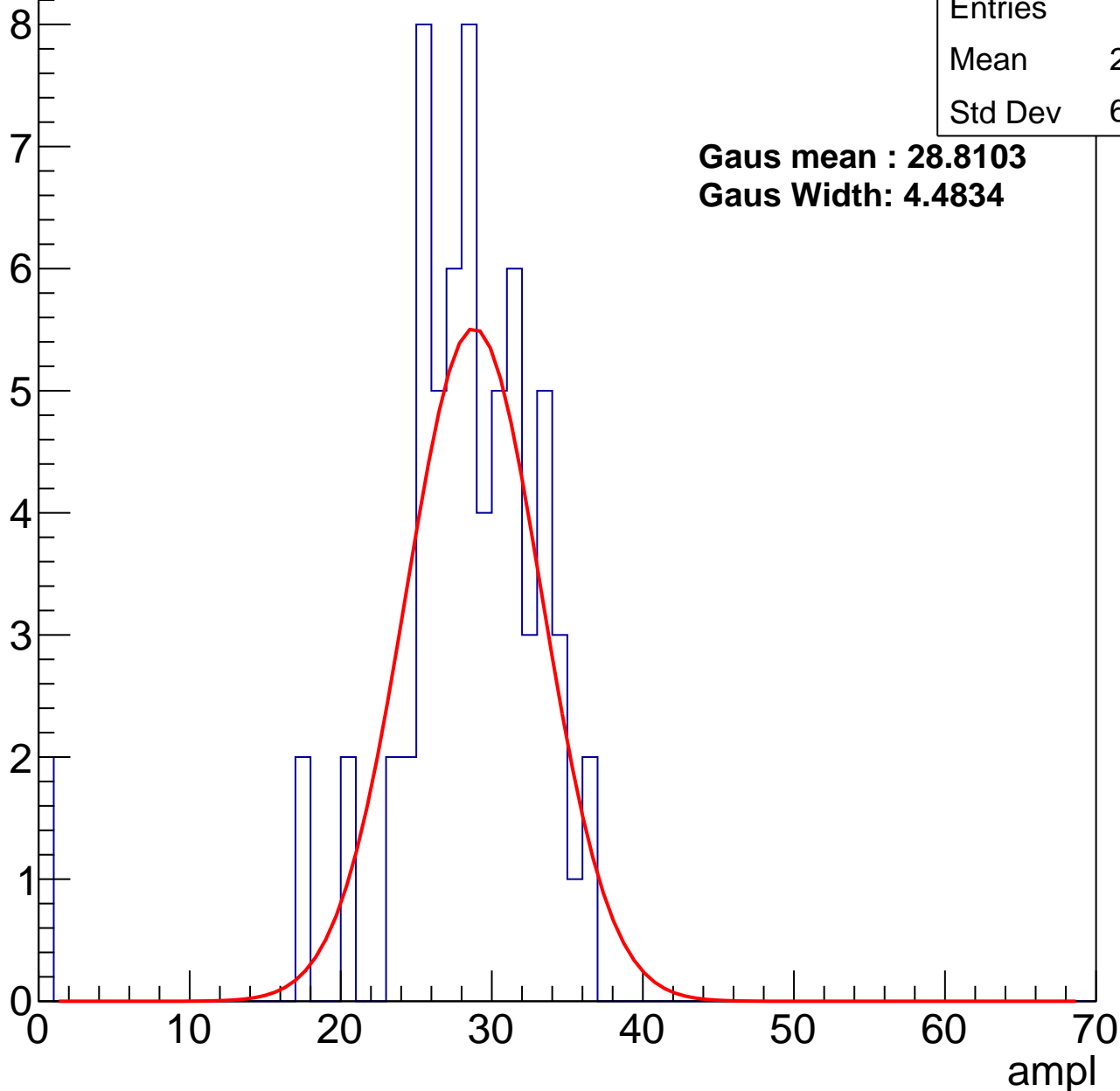
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	27.36
Std Dev	6.309

**Gaus mean : 28.8103**

**Gaus Width: 4.4834**



# B0L102S, U6-ch118, adc1

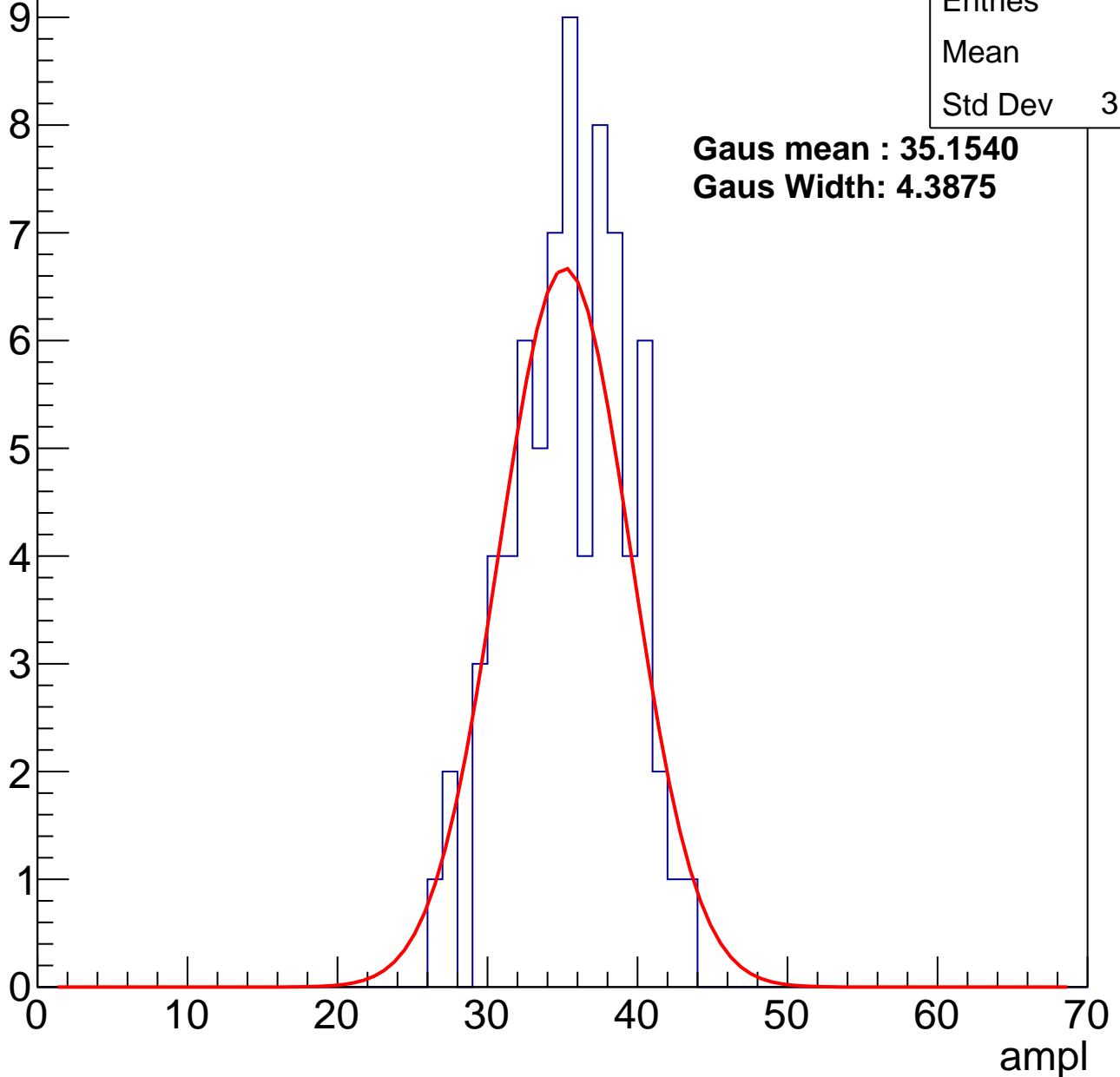
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	35
Std Dev	3.785

**Gaus mean : 35.1540**

**Gaus Width: 4.3875**



# B0L102S, U6-ch118, adc2

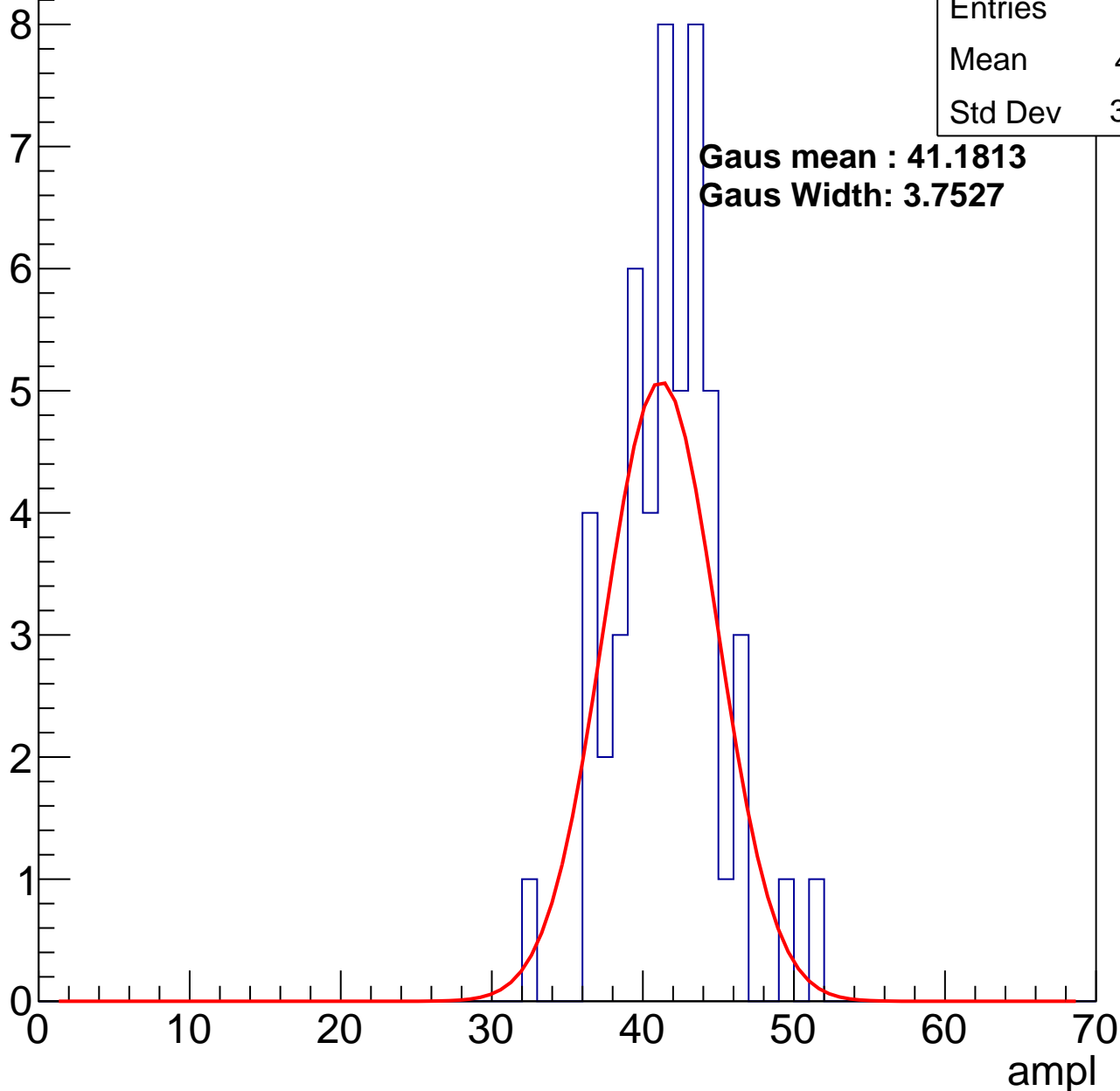
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	41.21
Std Dev	3.404

**Gaus mean : 41.1813**

**Gaus Width: 3.7527**

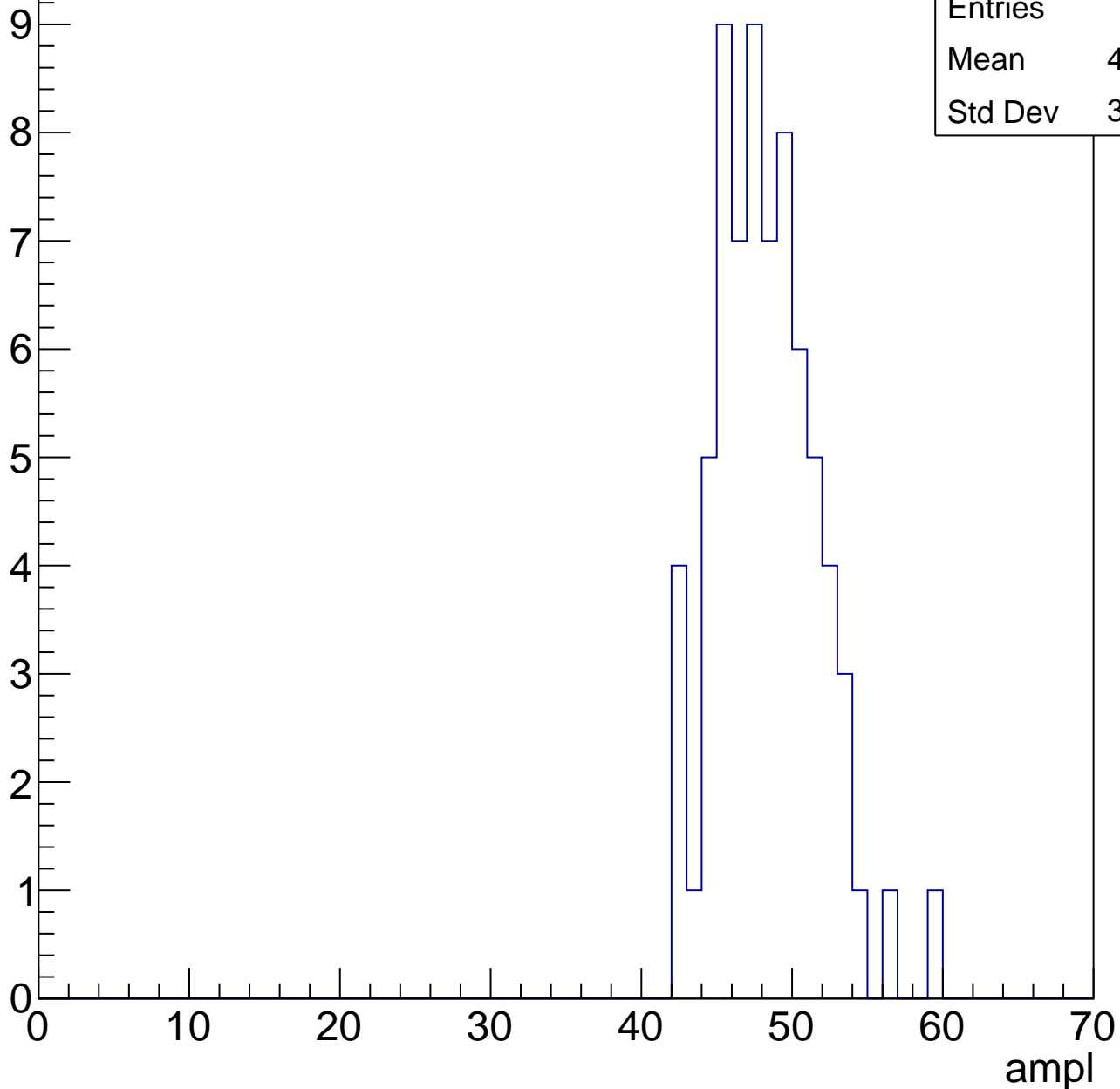


# B0L102S, U6-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

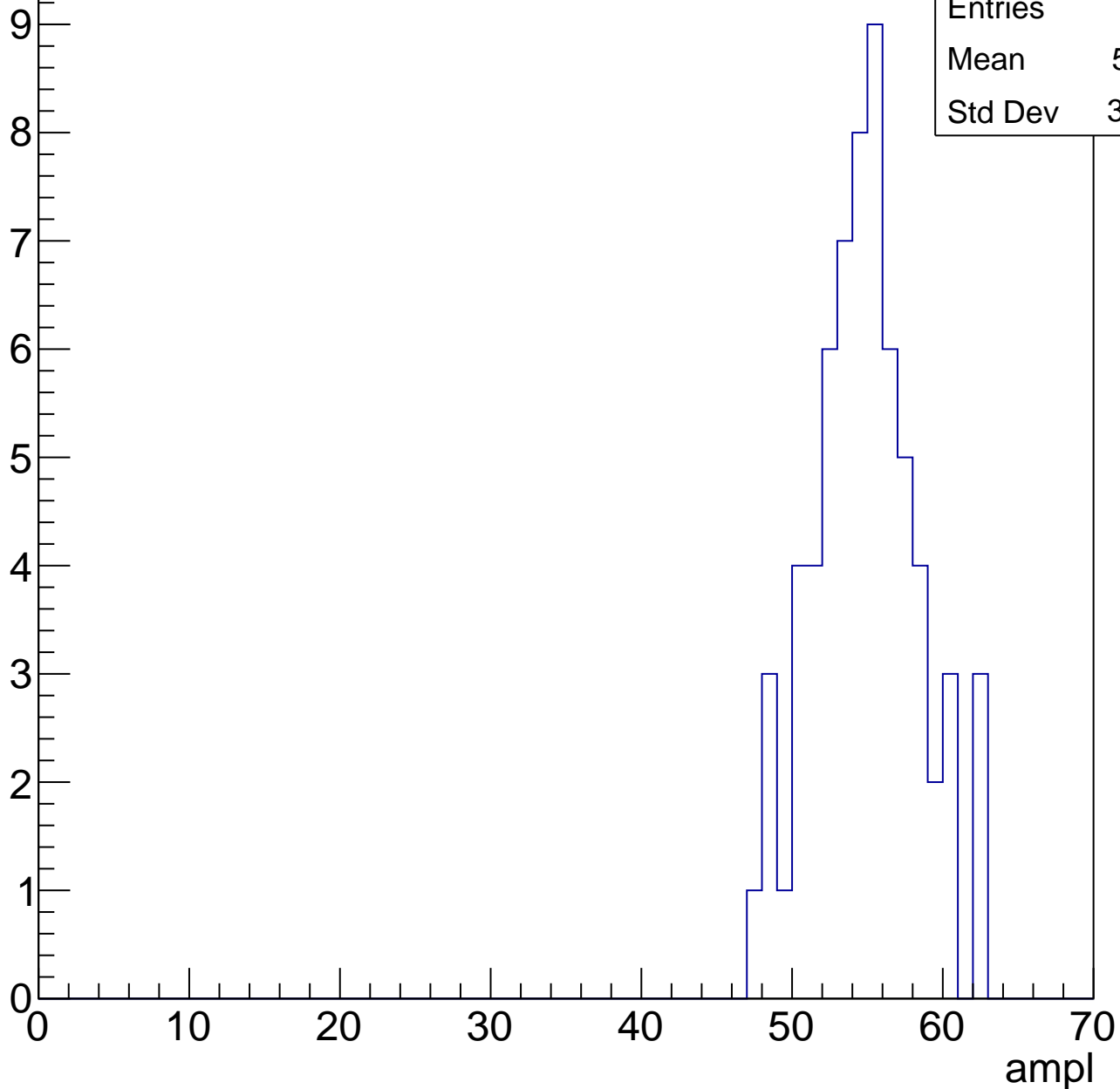
Entries	71
Mean	47.89
Std Dev	3.363



# B0L102S, U6-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



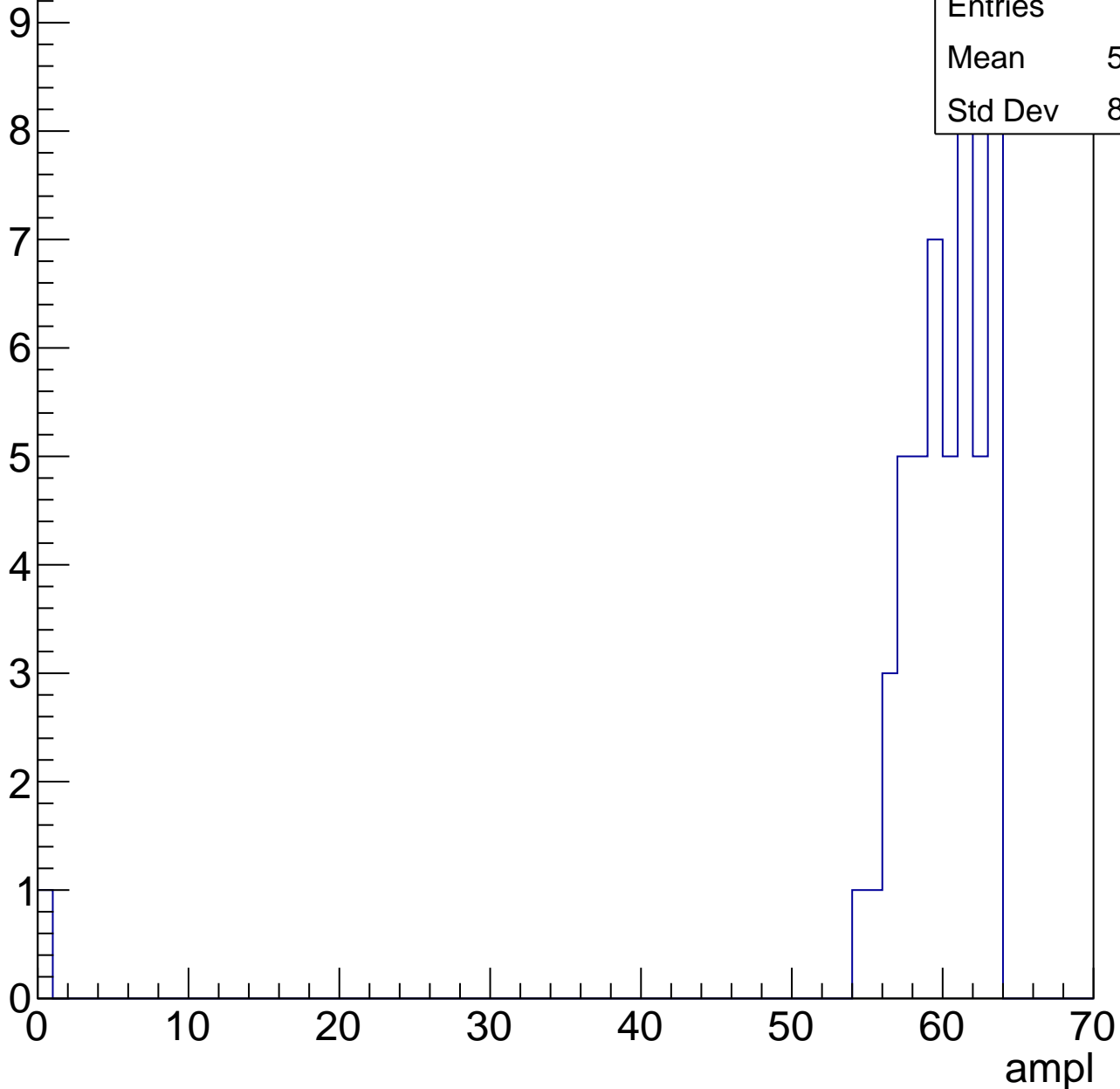
Entries	66
Mean	54.41
Std Dev	3.468

# B0L102S, U6-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	58.65
Std Dev	8.629



# B0L102S, U6-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

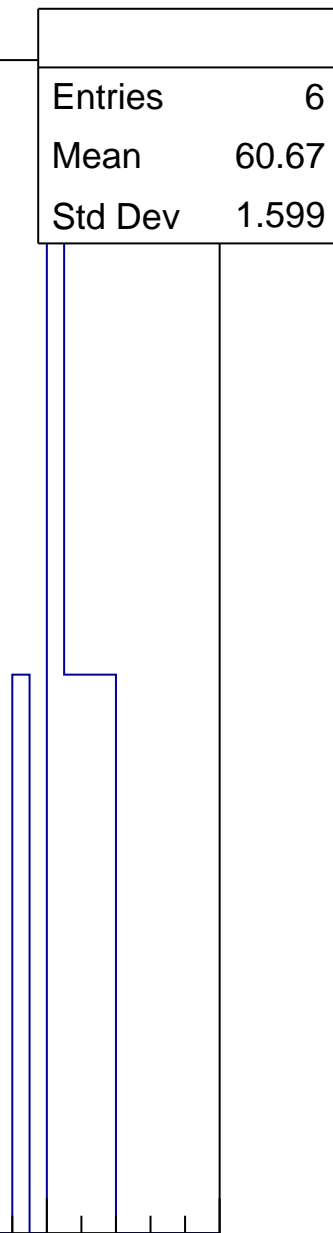
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	60.67
Std Dev	1.599

ampl

0 10 20 30 40 50 60 70





# B0L102S, U6-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch119, adc0

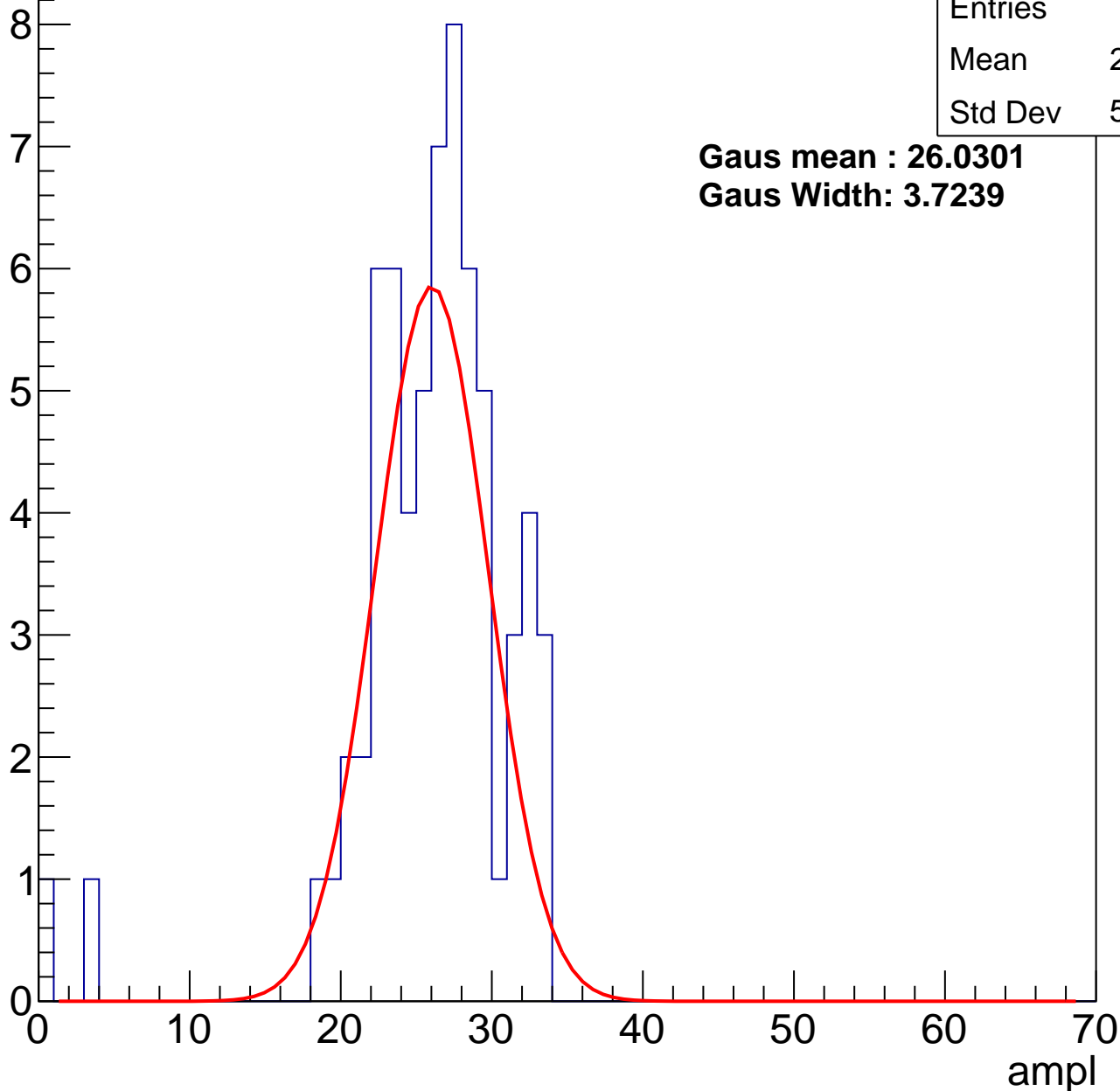
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	25.36
Std Dev	5.564

**Gaus mean : 26.0301**

**Gaus Width: 3.7239**



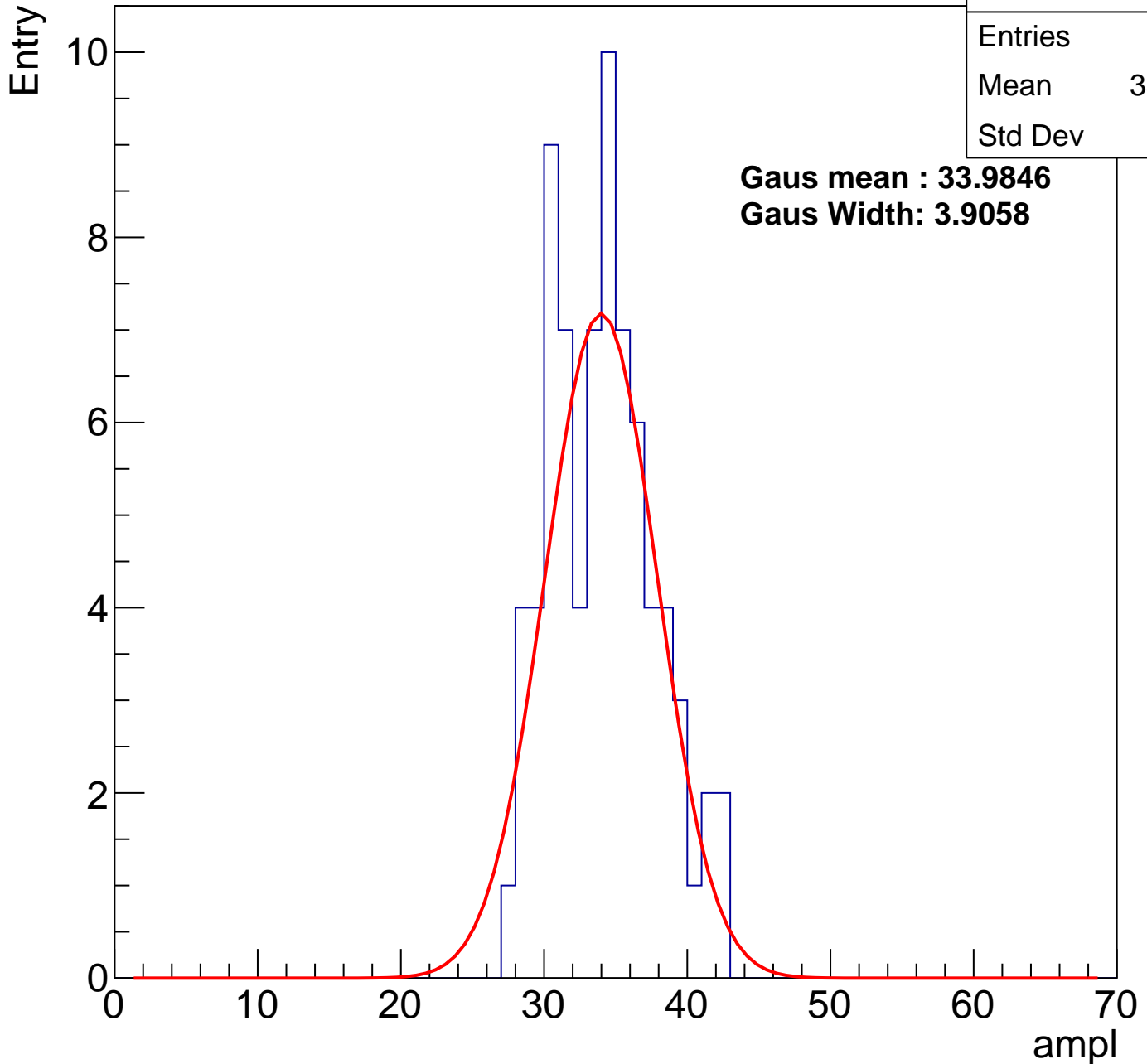
# B0L102S, U6-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	75
Mean	33.67
Std Dev	3.62

**Gaus mean : 33.9846**

**Gaus Width: 3.9058**



# B0L102S, U6-ch119, adc2

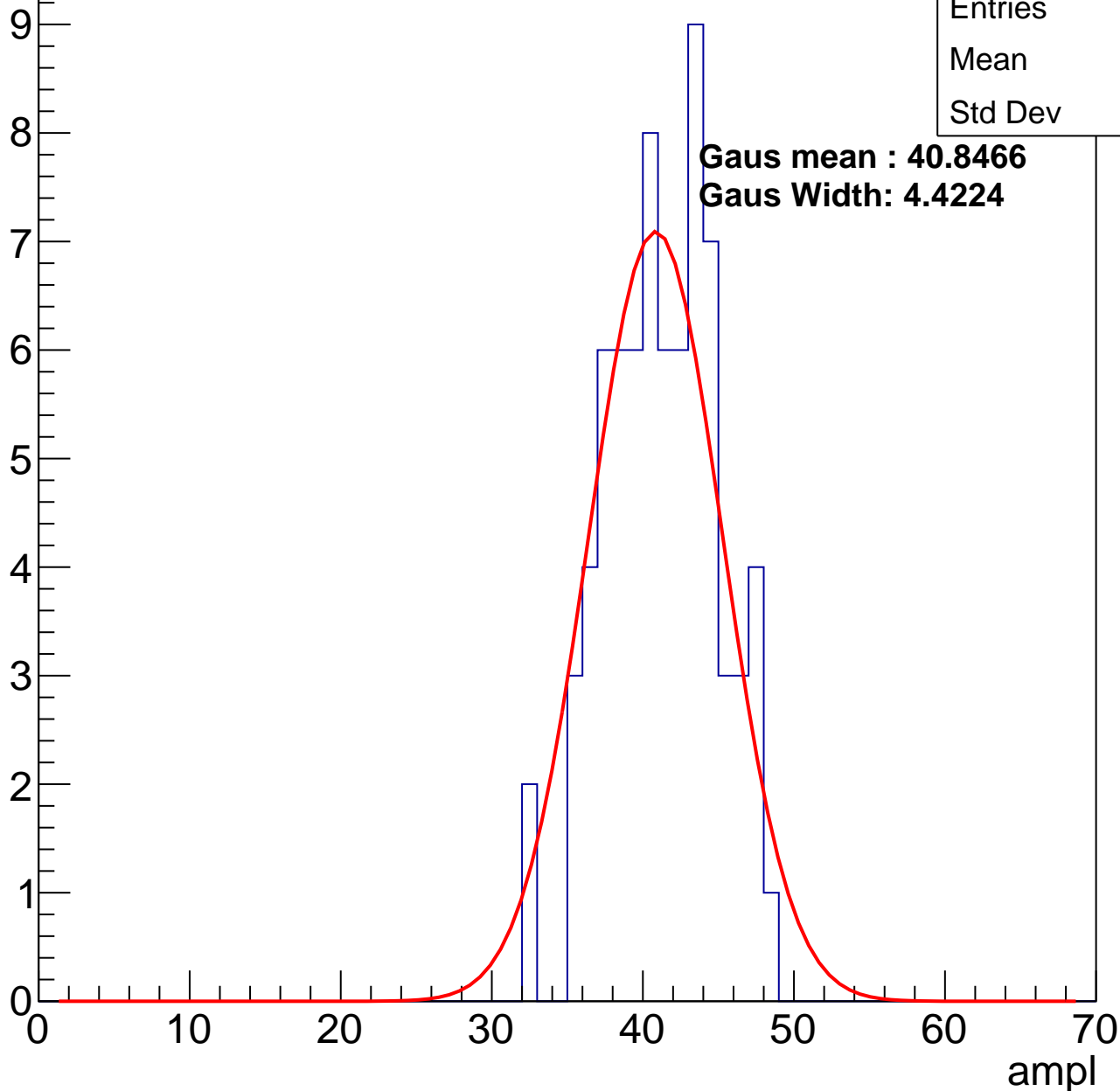
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	40.8
Std Dev	3.62

**Gaus mean : 40.8466**

**Gaus Width: 4.4224**

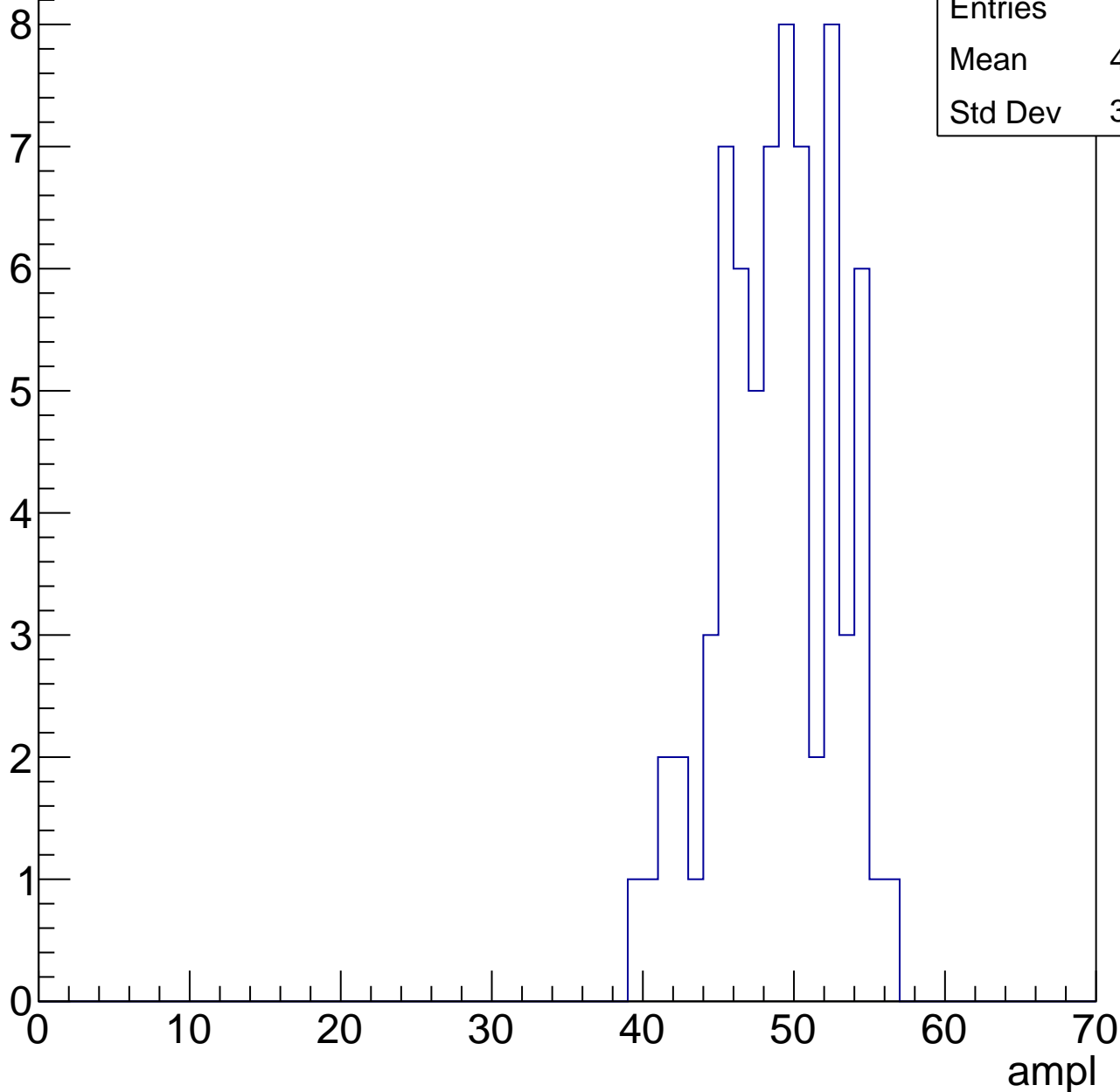


# B0L102S, U6-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	48.39
Std Dev	3.855

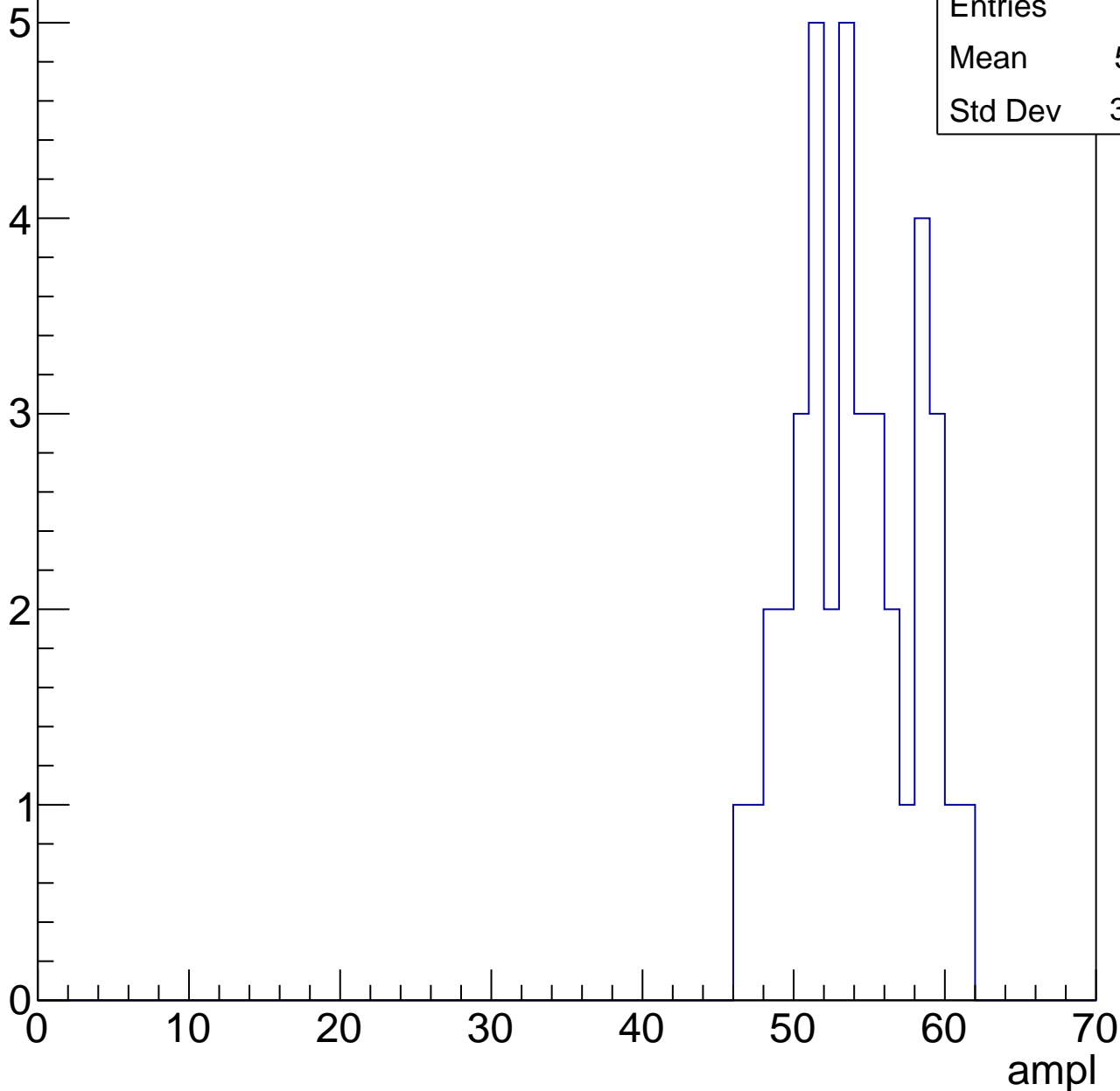


# B0L102S, U6-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	39
Mean	53.51
Std Dev	3.842

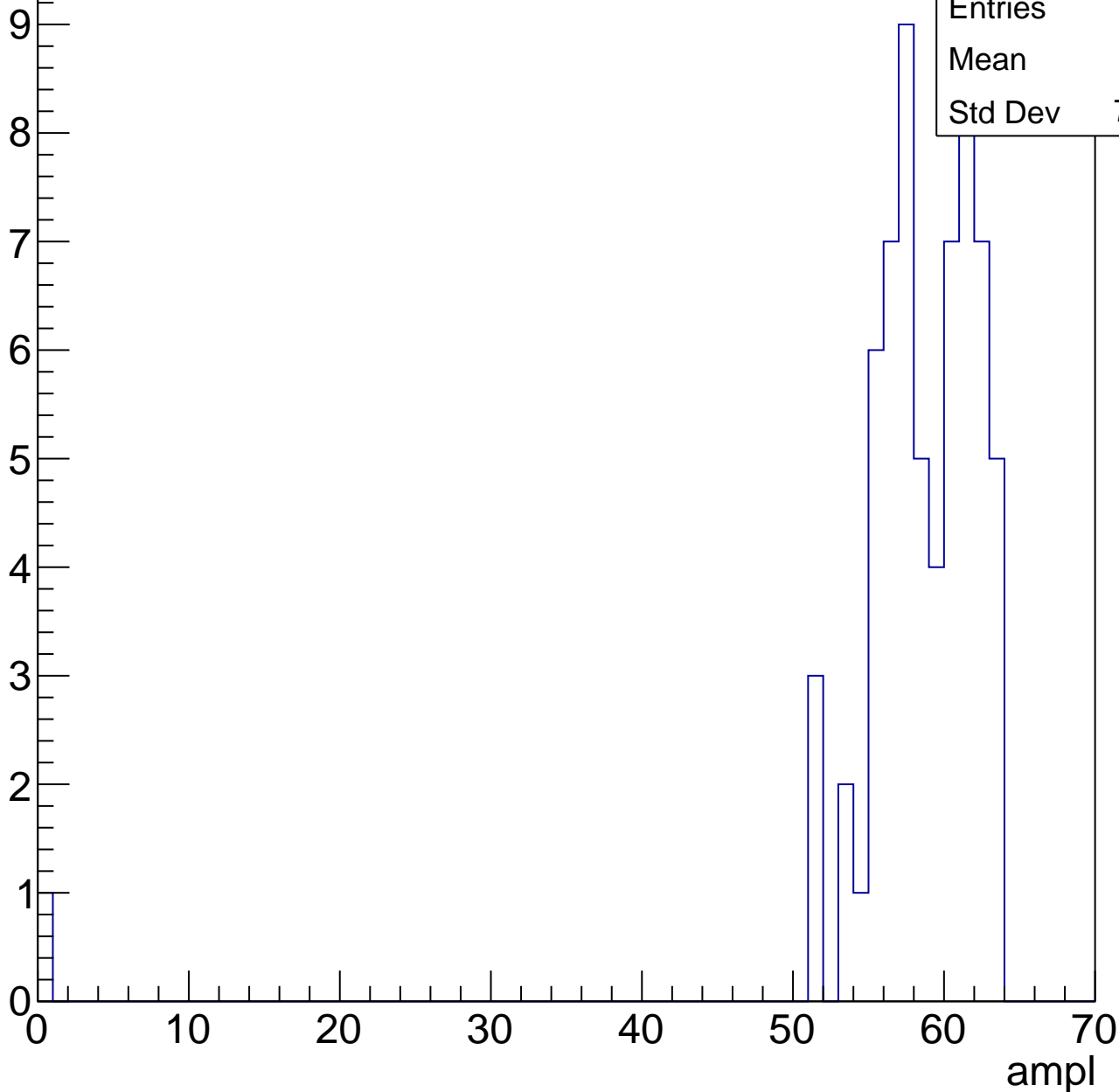


# B0L102S, U6-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	57.4
Std Dev	7.831

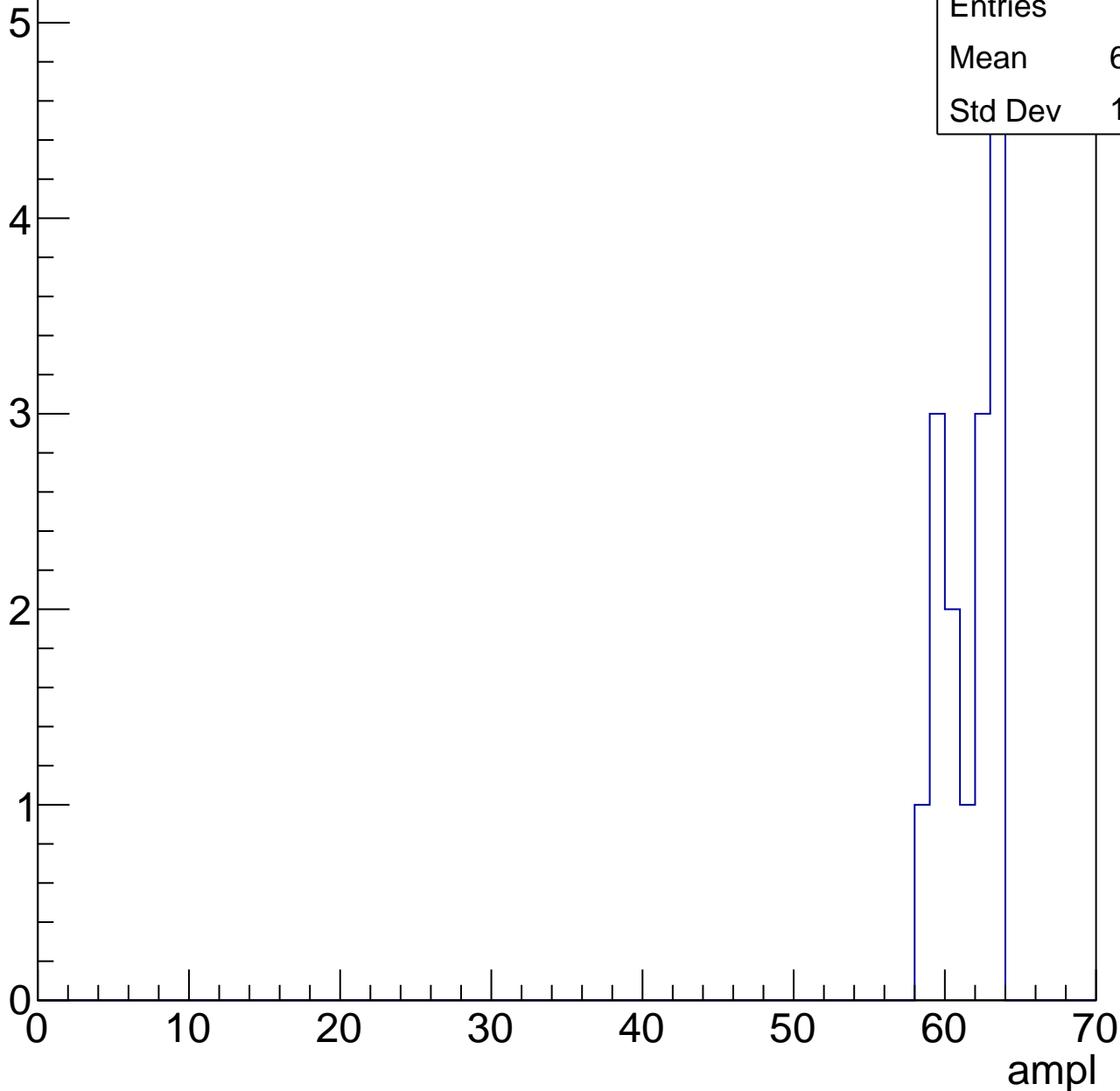


# B0L102S, U6-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	61.13
Std Dev	1.746





# B0L102S, U6-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch120, adc0

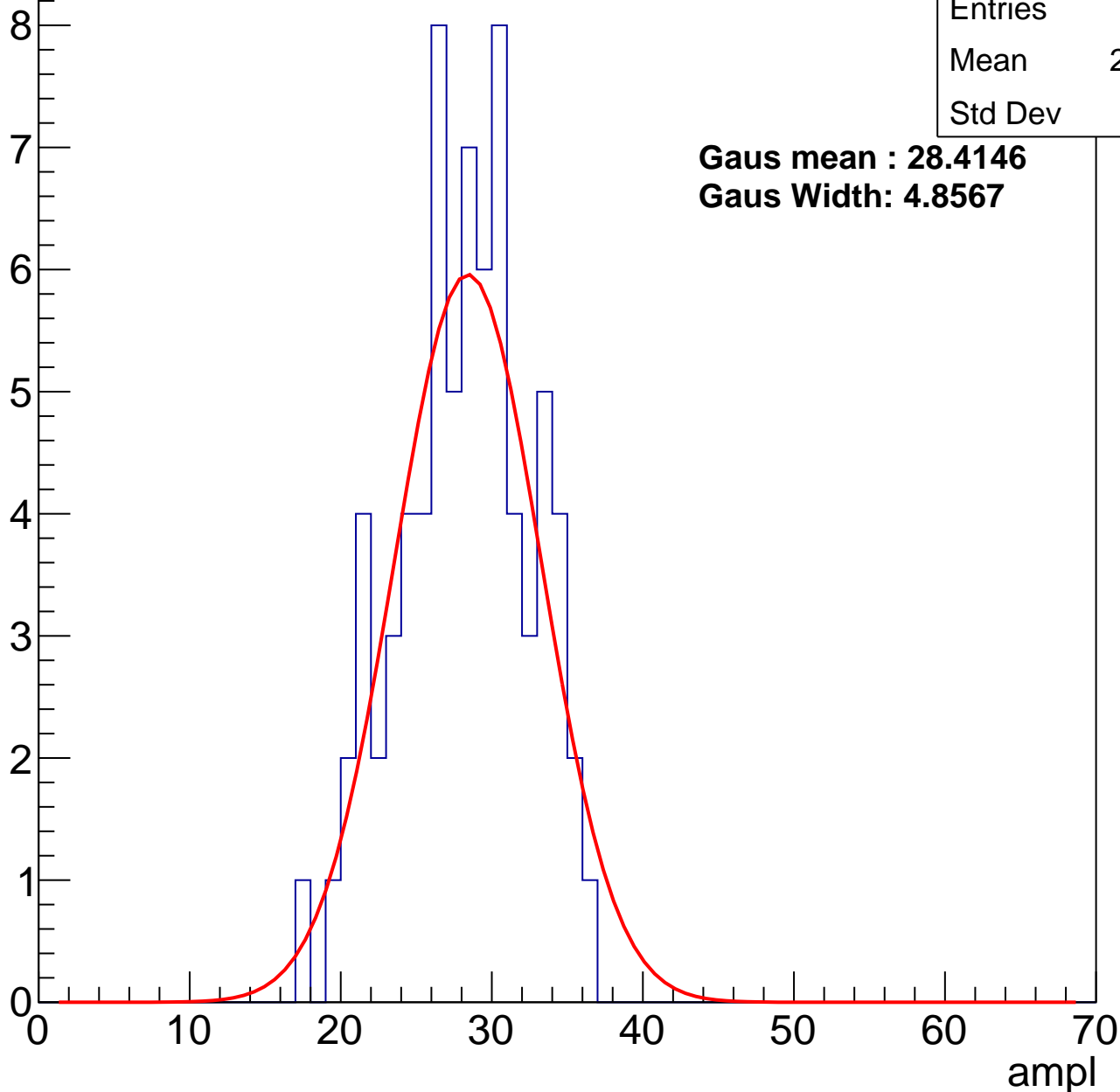
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	27.69
Std Dev	4.28

**Gaus mean : 28.4146**

**Gaus Width: 4.8567**



# B0L102S, U6-ch120, adc1

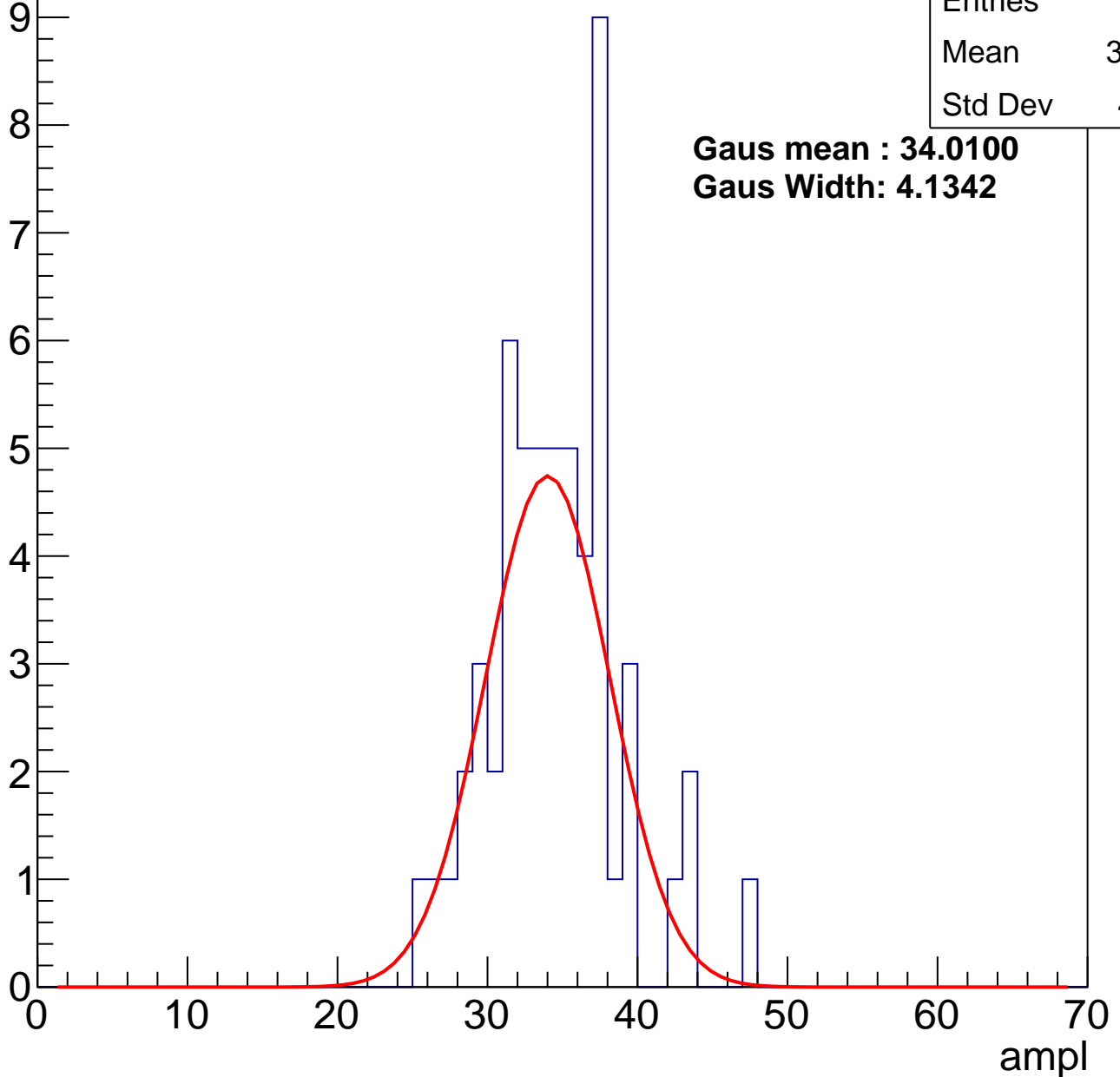
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	34.11
Std Dev	4.27

**Gaus mean : 34.0100**

**Gaus Width: 4.1342**



# B0L102S, U6-ch120, adc2

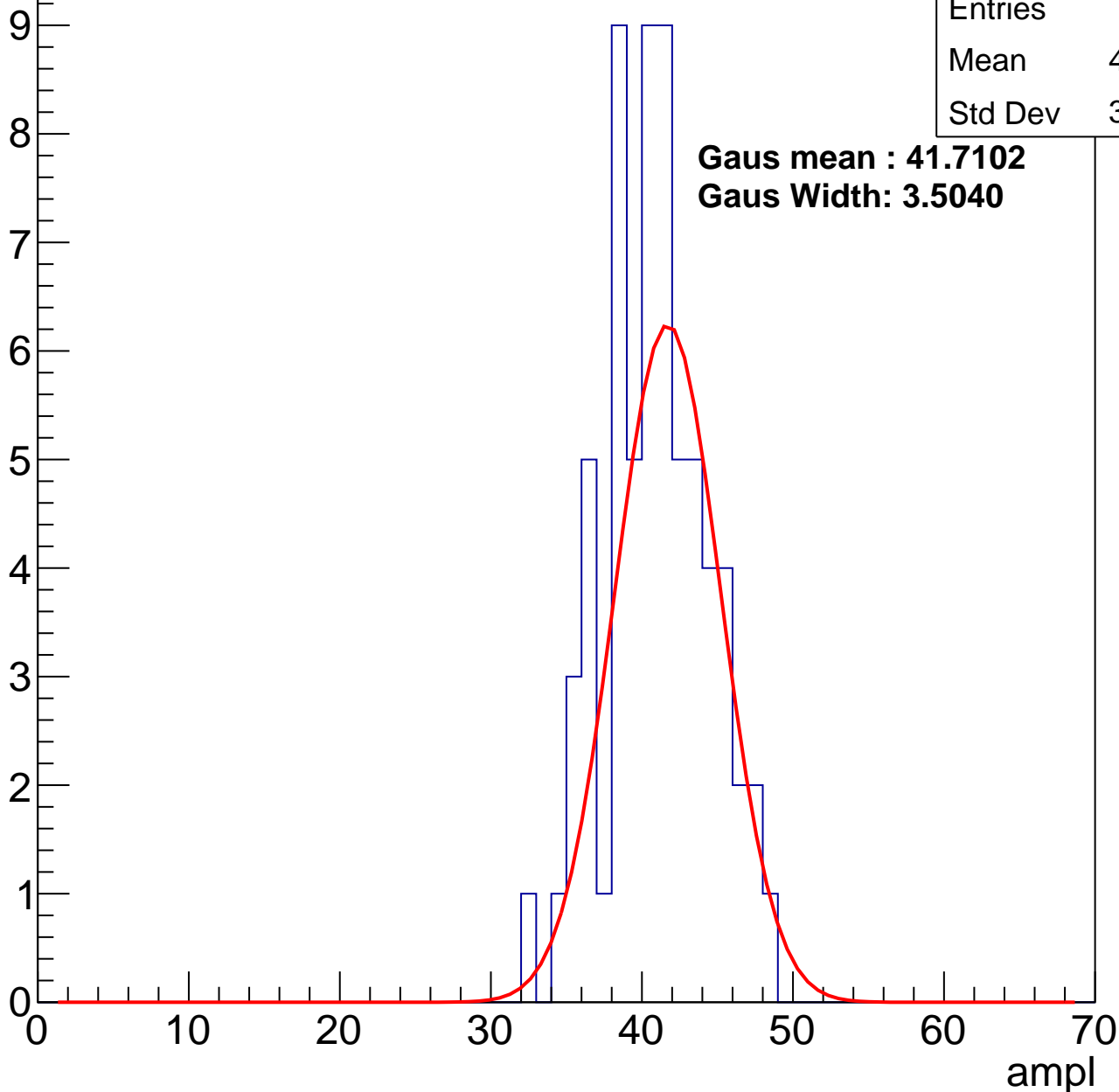
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	40.44
Std Dev	3.403

**Gaus mean : 41.7102**

**Gaus Width: 3.5040**

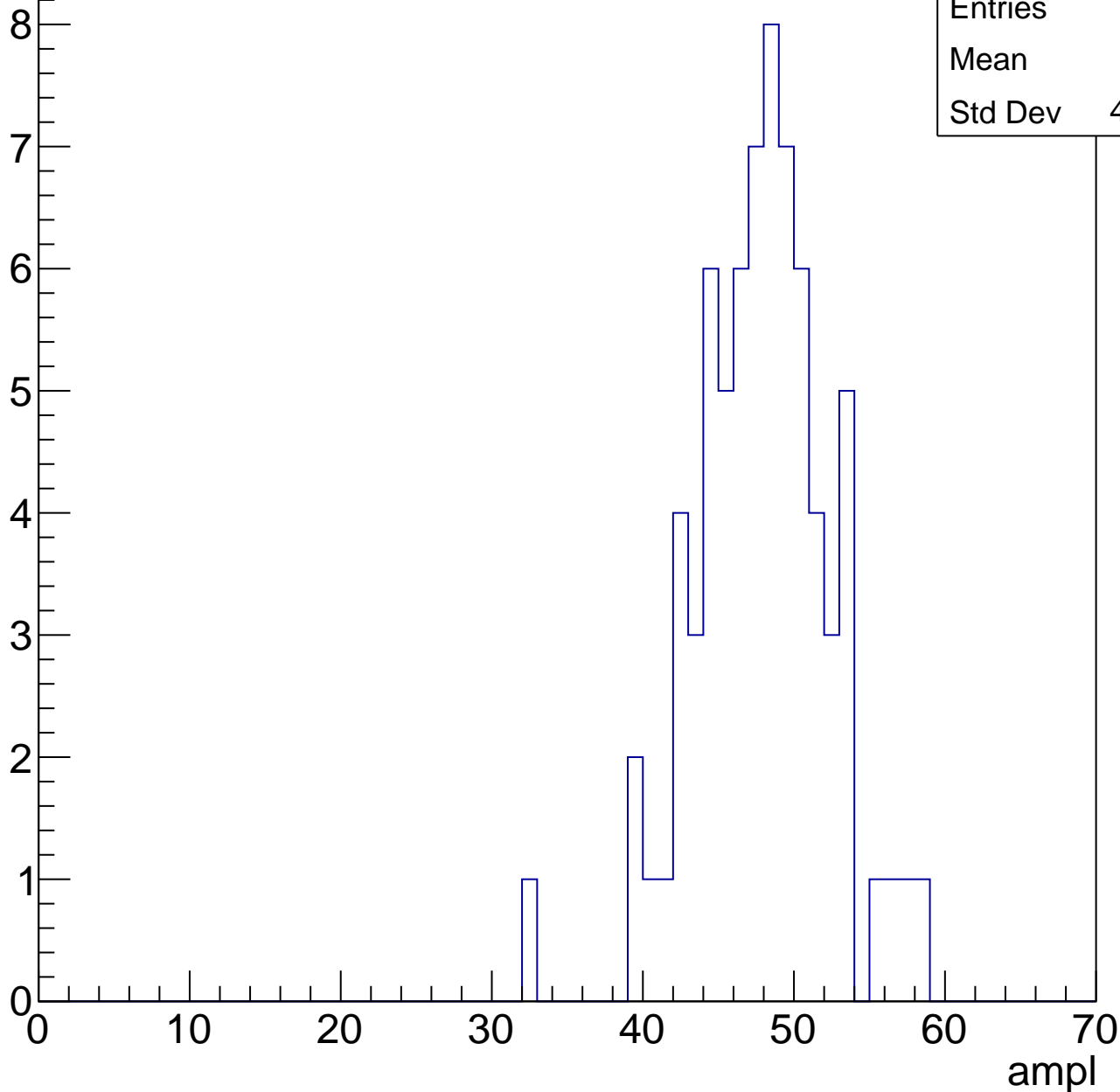


# B0L102S, U6-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	47.4
Std Dev	4.425

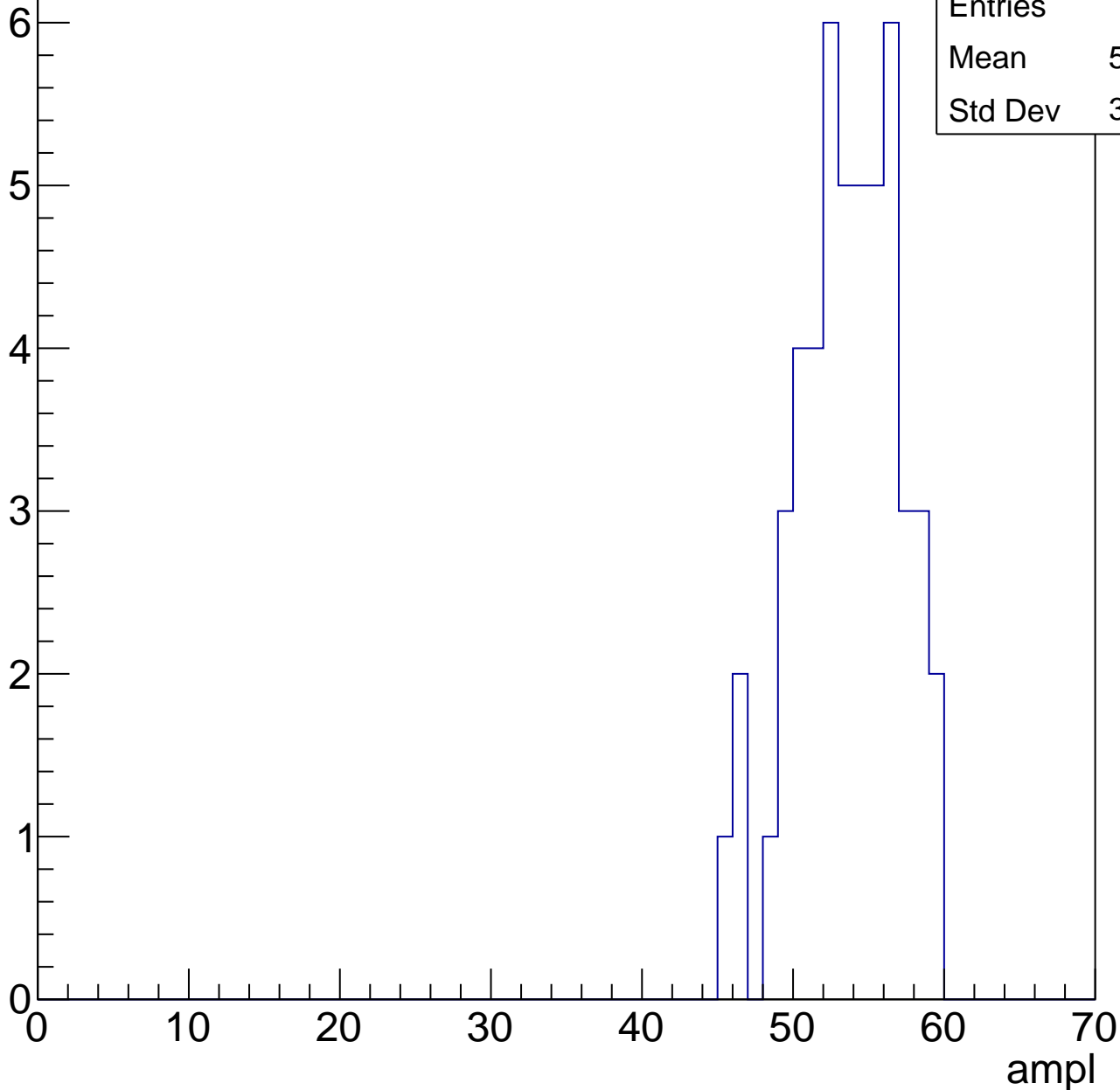


# B0L102S, U6-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	53.14
Std Dev	3.365

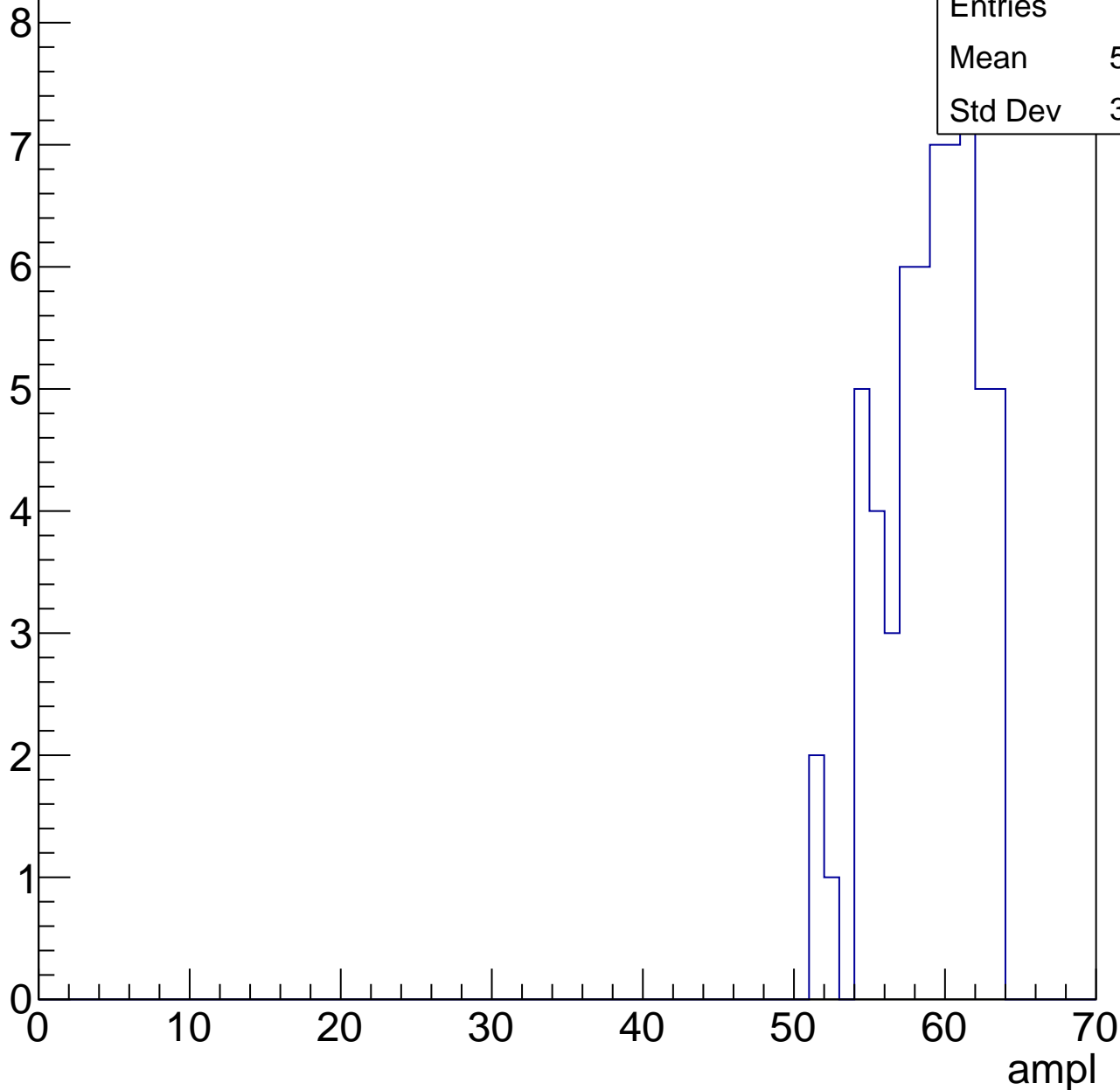


# B0L102S, U6-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	58.44
Std Dev	3.104

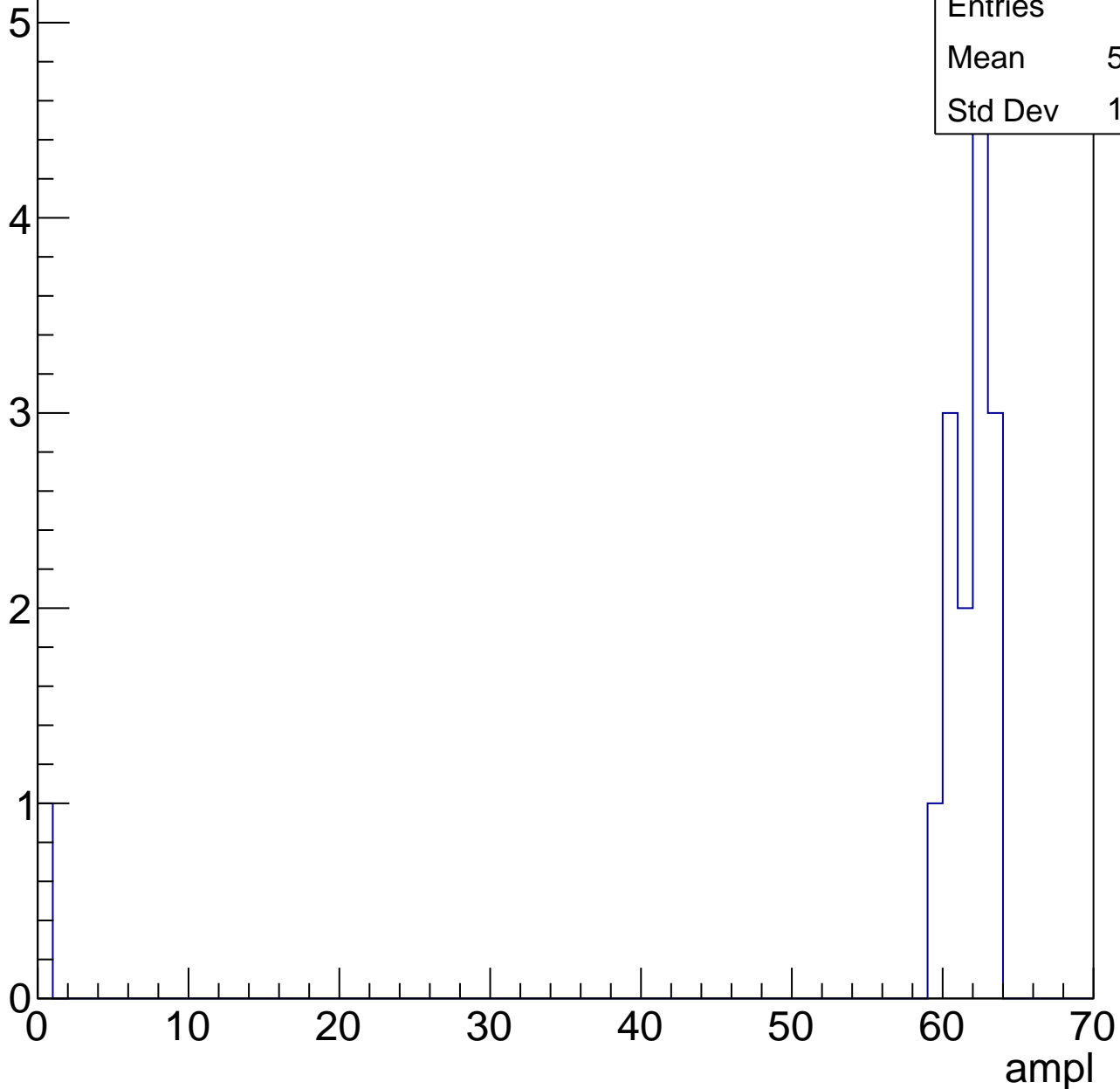


# B0L102S, U6-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	57.33
Std Dev	15.37





# B0L102S, U6-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch121, adc0

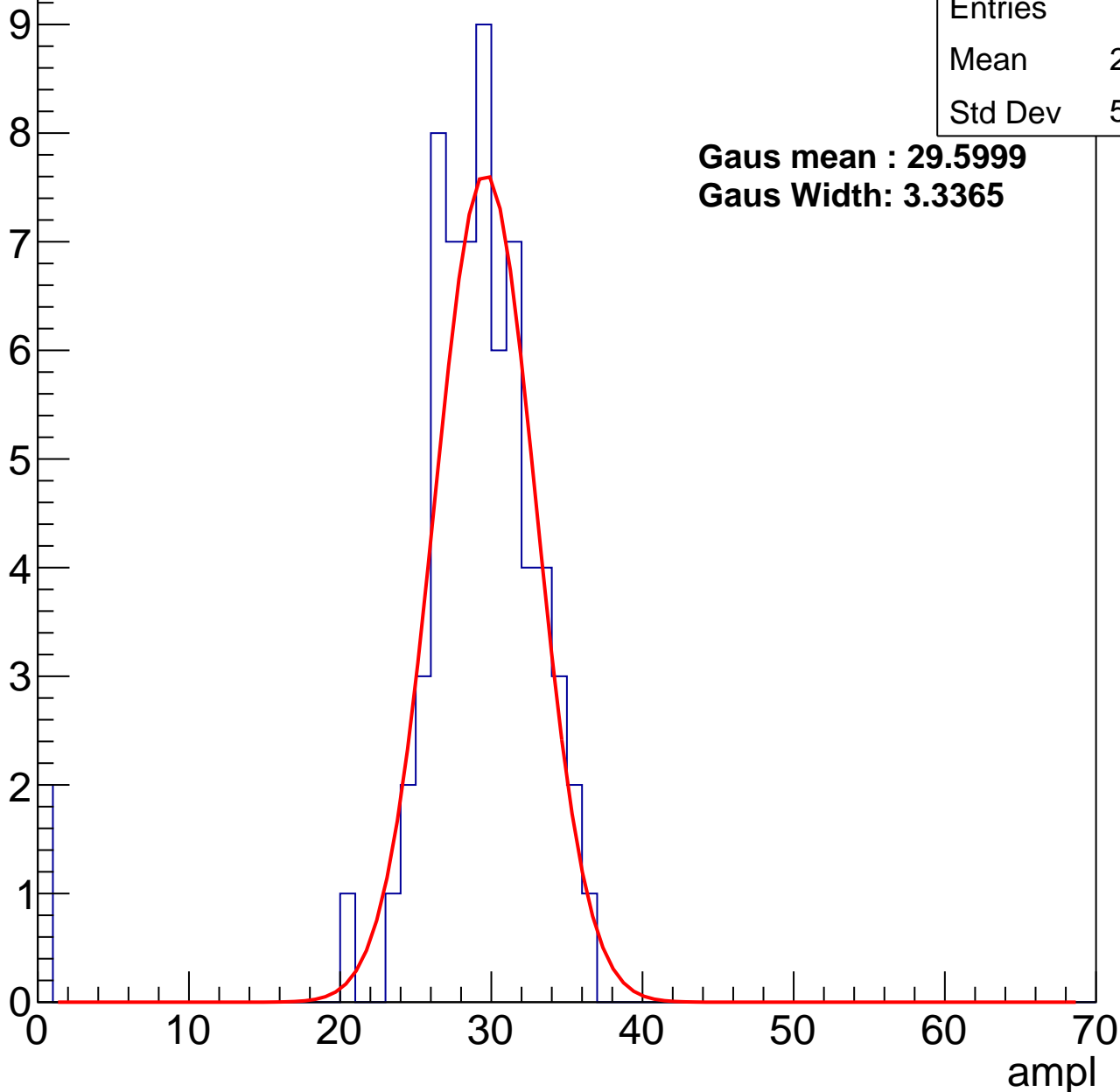
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	28.13
Std Dev	5.838

**Gaus mean : 29.5999**

**Gaus Width: 3.3365**



# B0L102S, U6-ch121, adc1

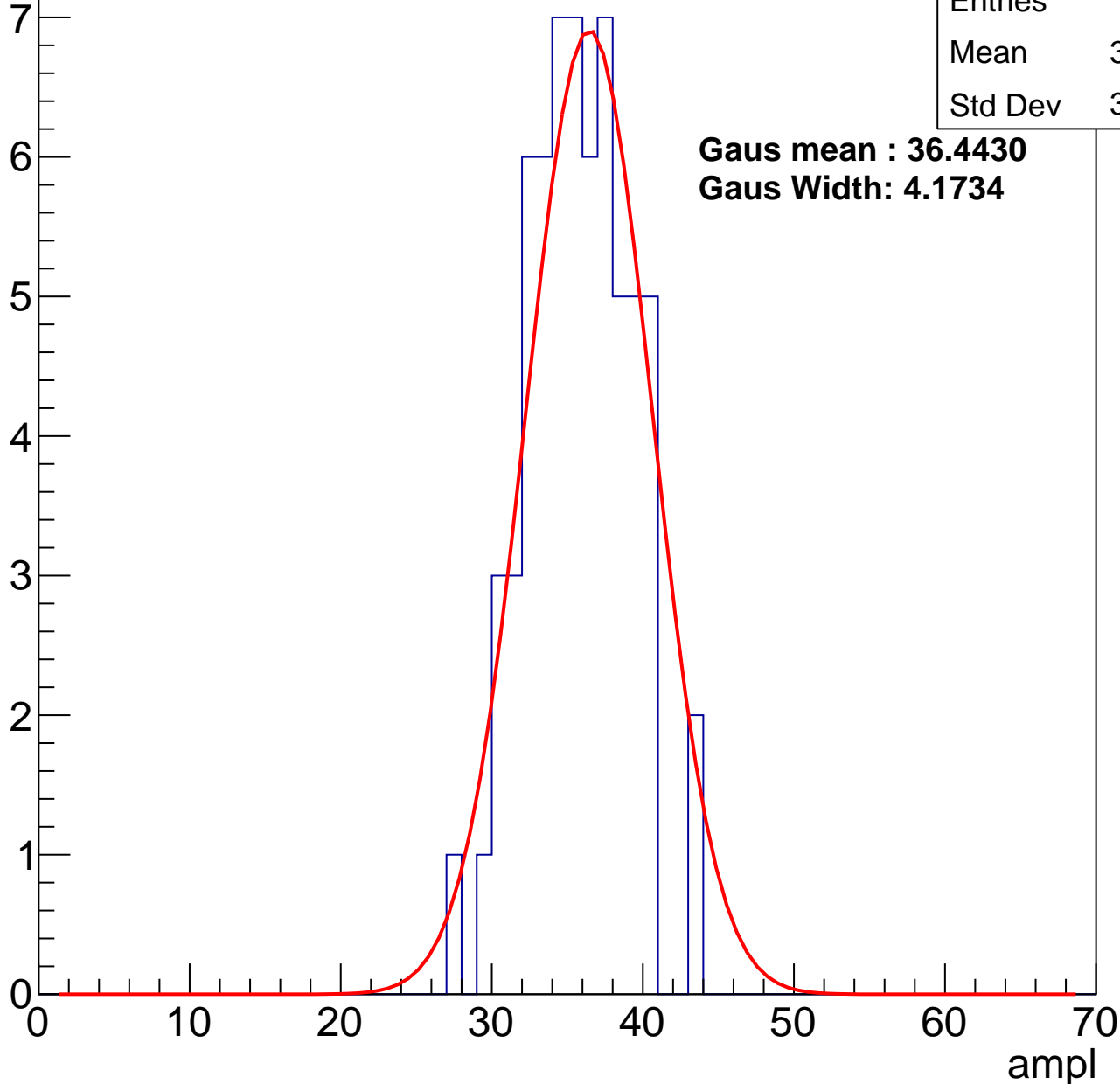
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.28
Std Dev	3.342

**Gaus mean : 36.4430**

**Gaus Width: 4.1734**



# B0L102S, U6-ch121, adc2

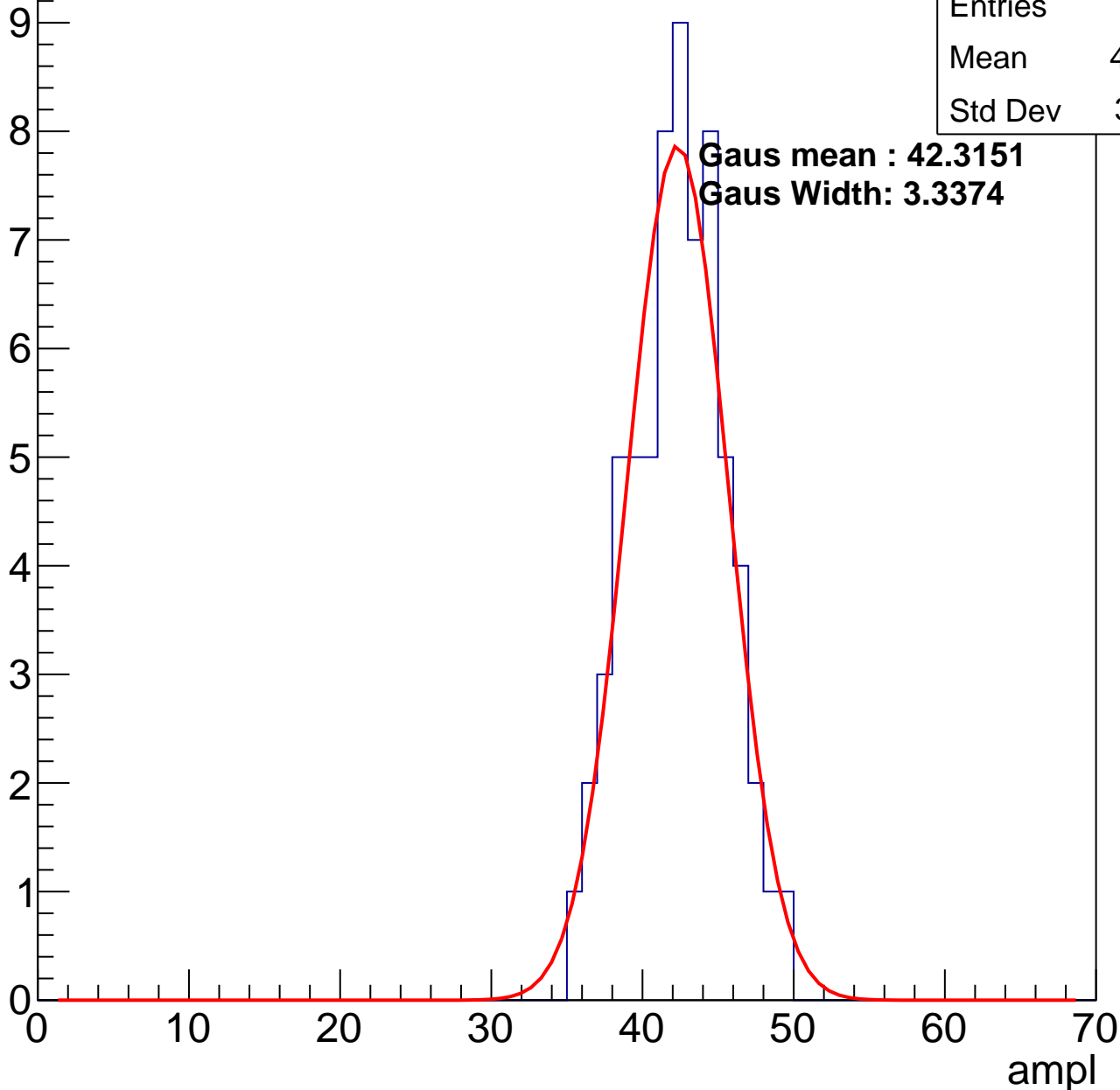
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	41.85
Std Dev	3.091

**Gaus mean : 42.3151**

**Gaus Width: 3.3374**

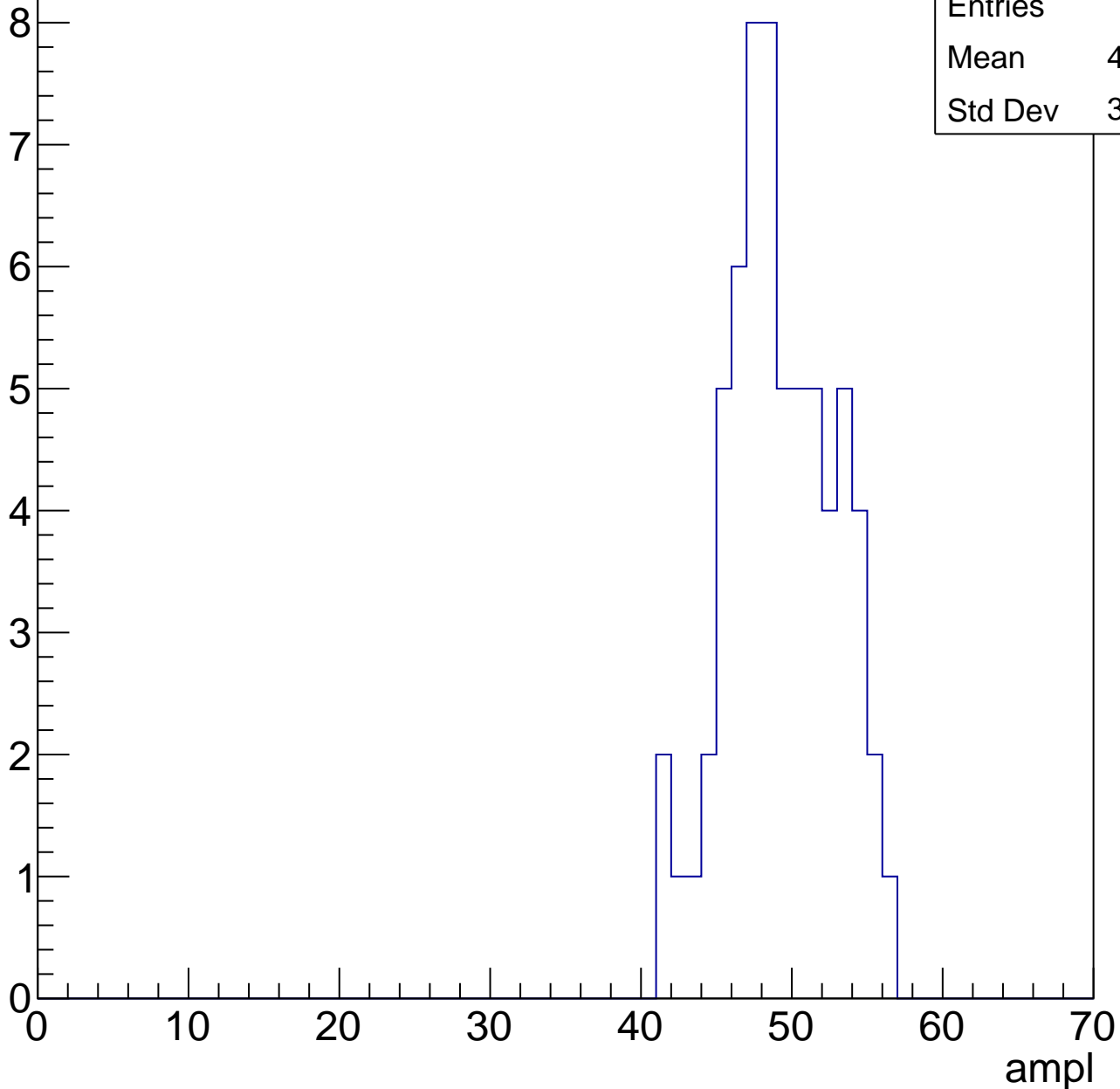


# B0L102S, U6-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	48.77
Std Dev	3.526

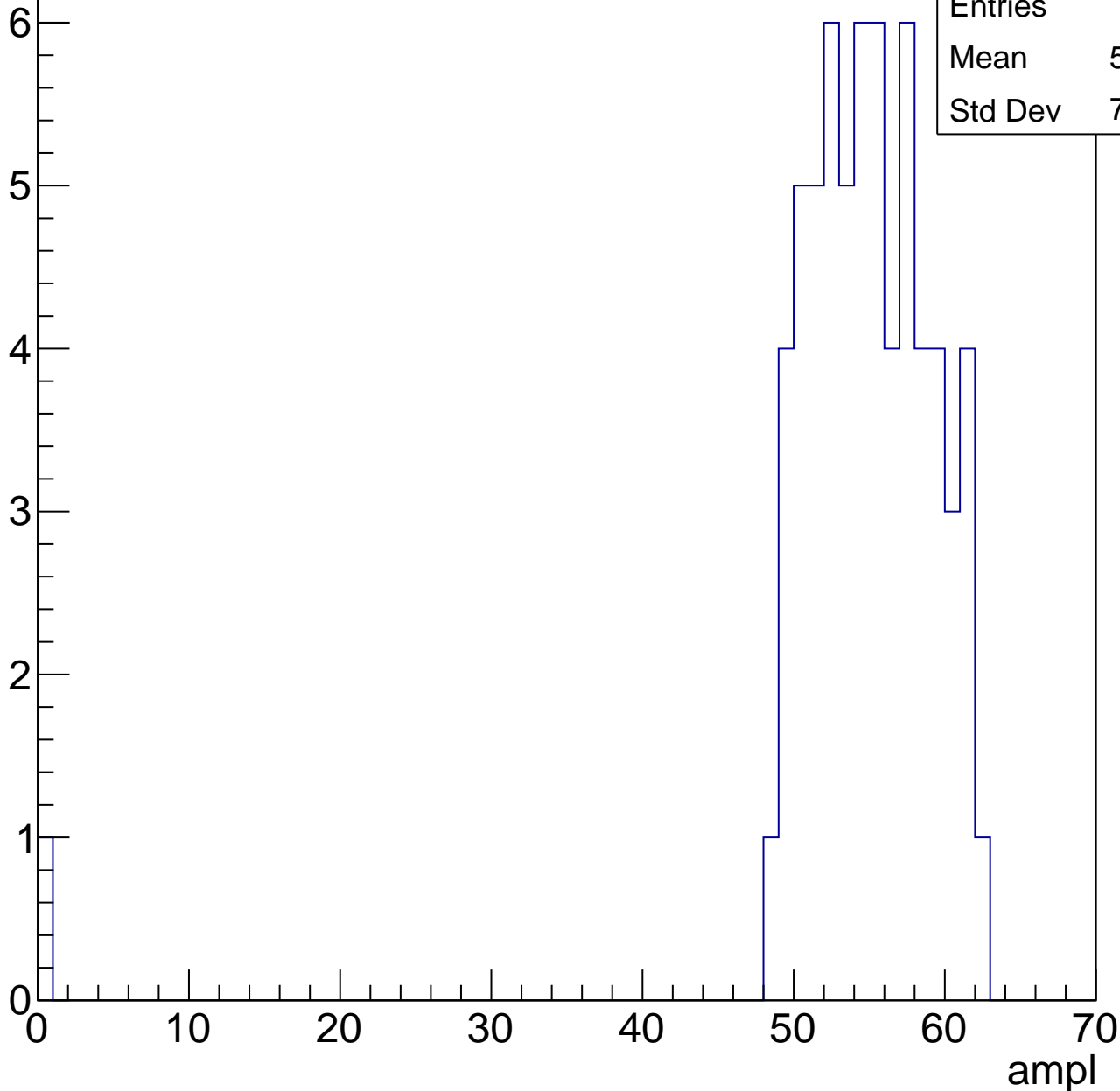


# B0L102S, U6-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	53.85
Std Dev	7.659

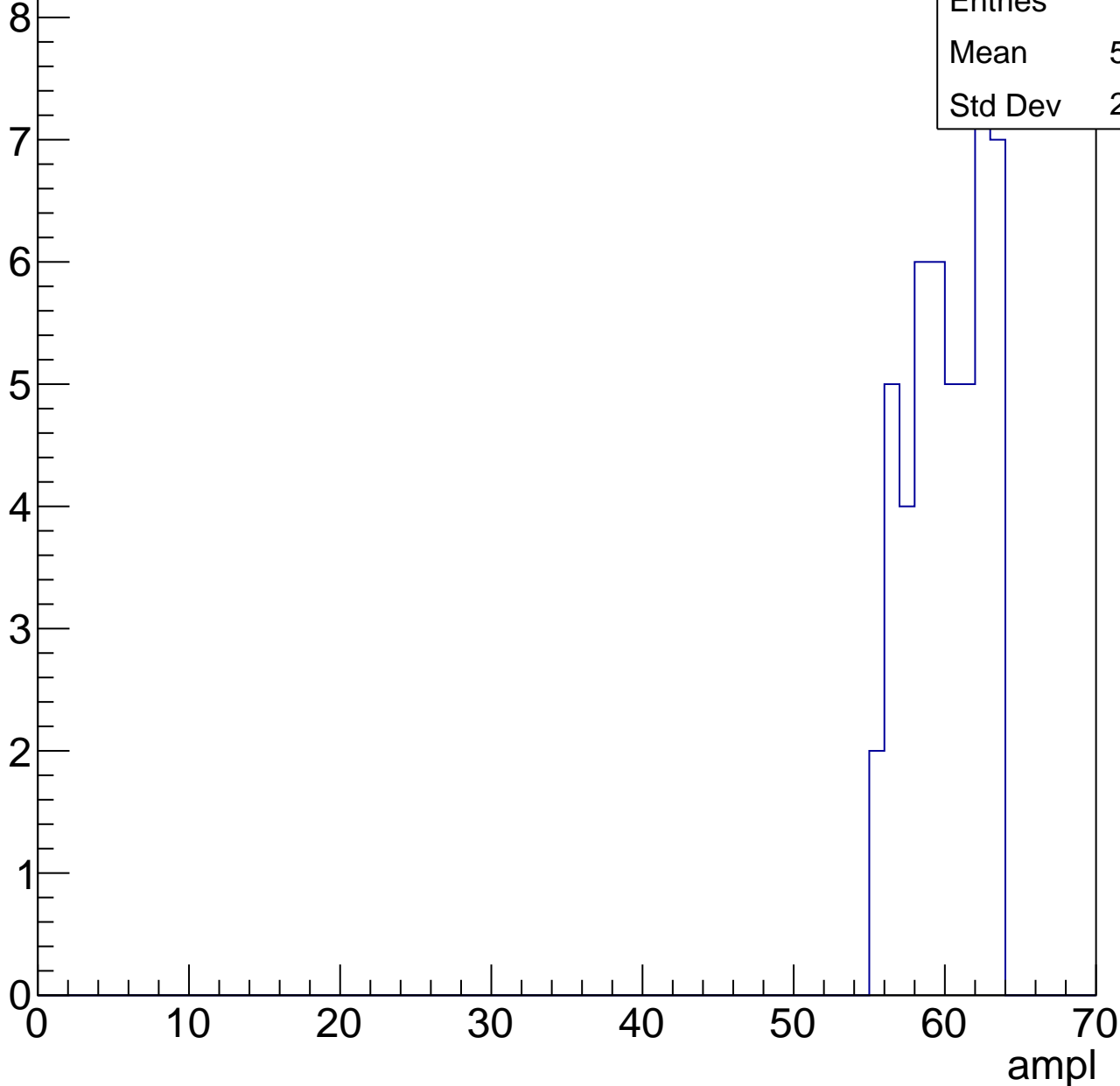


# B0L102S, U6-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

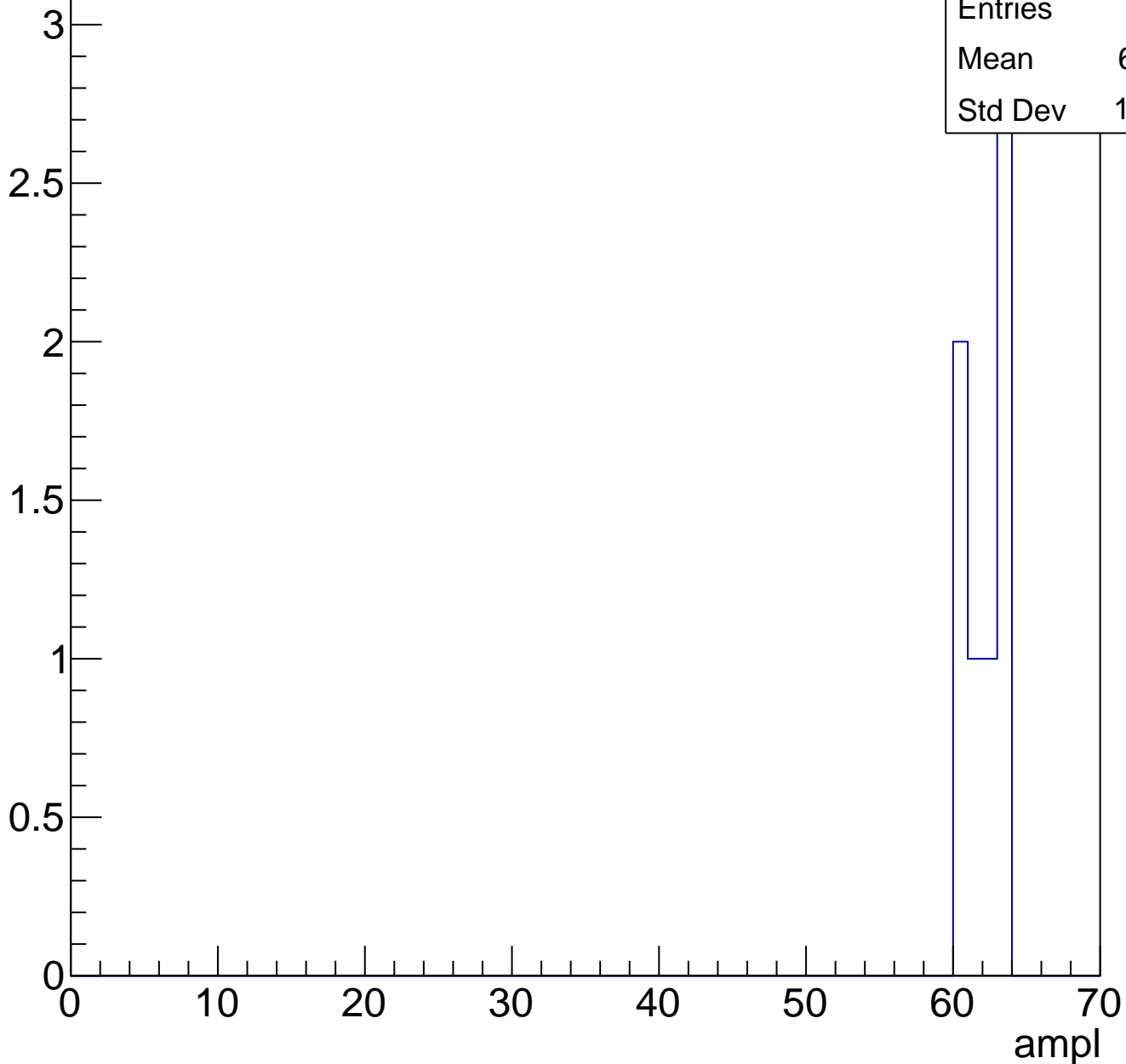
Entries	48
Mean	59.62
Std Dev	2.455



# B0L102S, U6-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U6-ch122, adc0

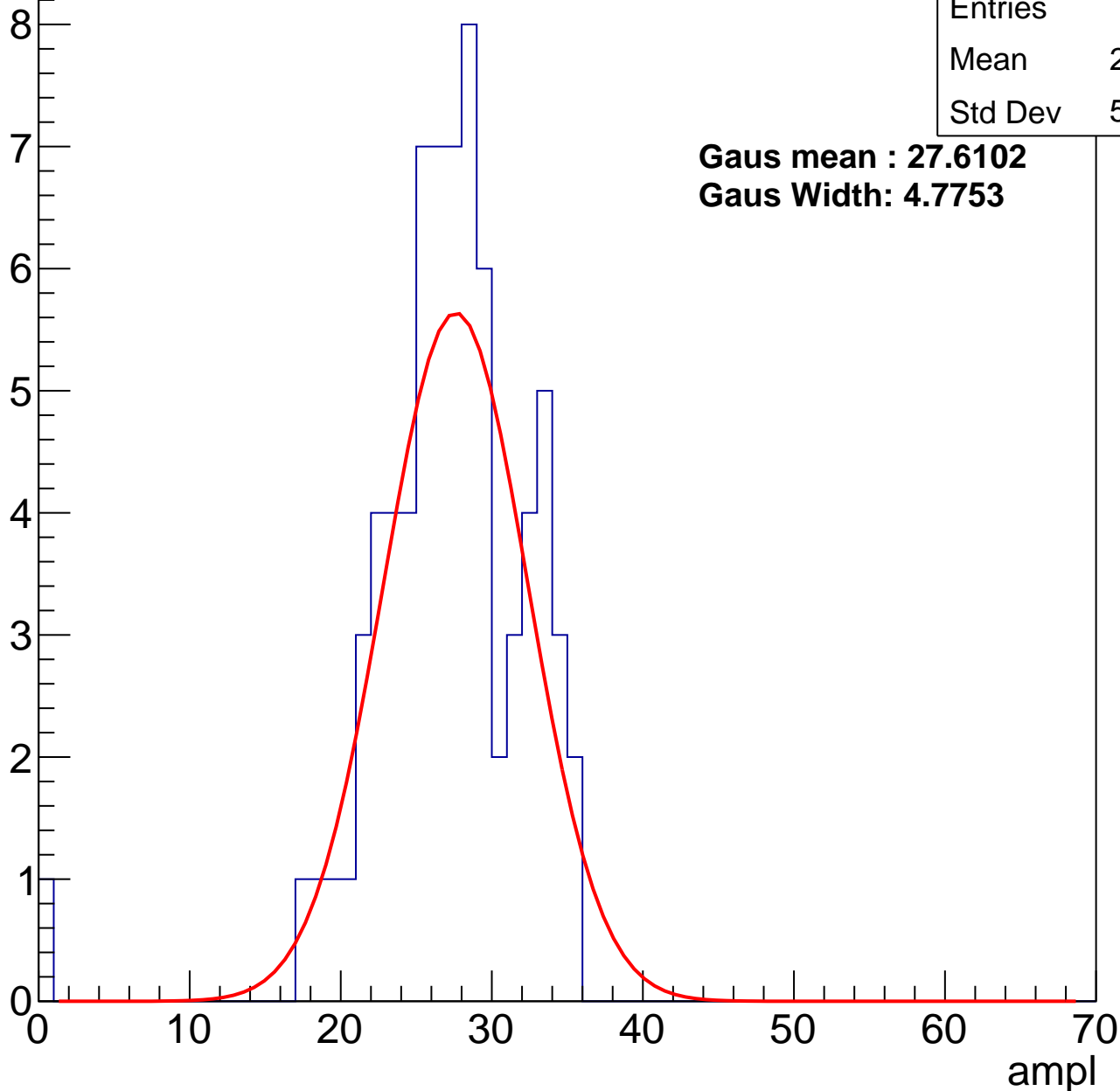
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	26.69
Std Dev	5.214

**Gaus mean : 27.6102**

**Gaus Width: 4.7753**



# B0L102S, U6-ch122, adc1

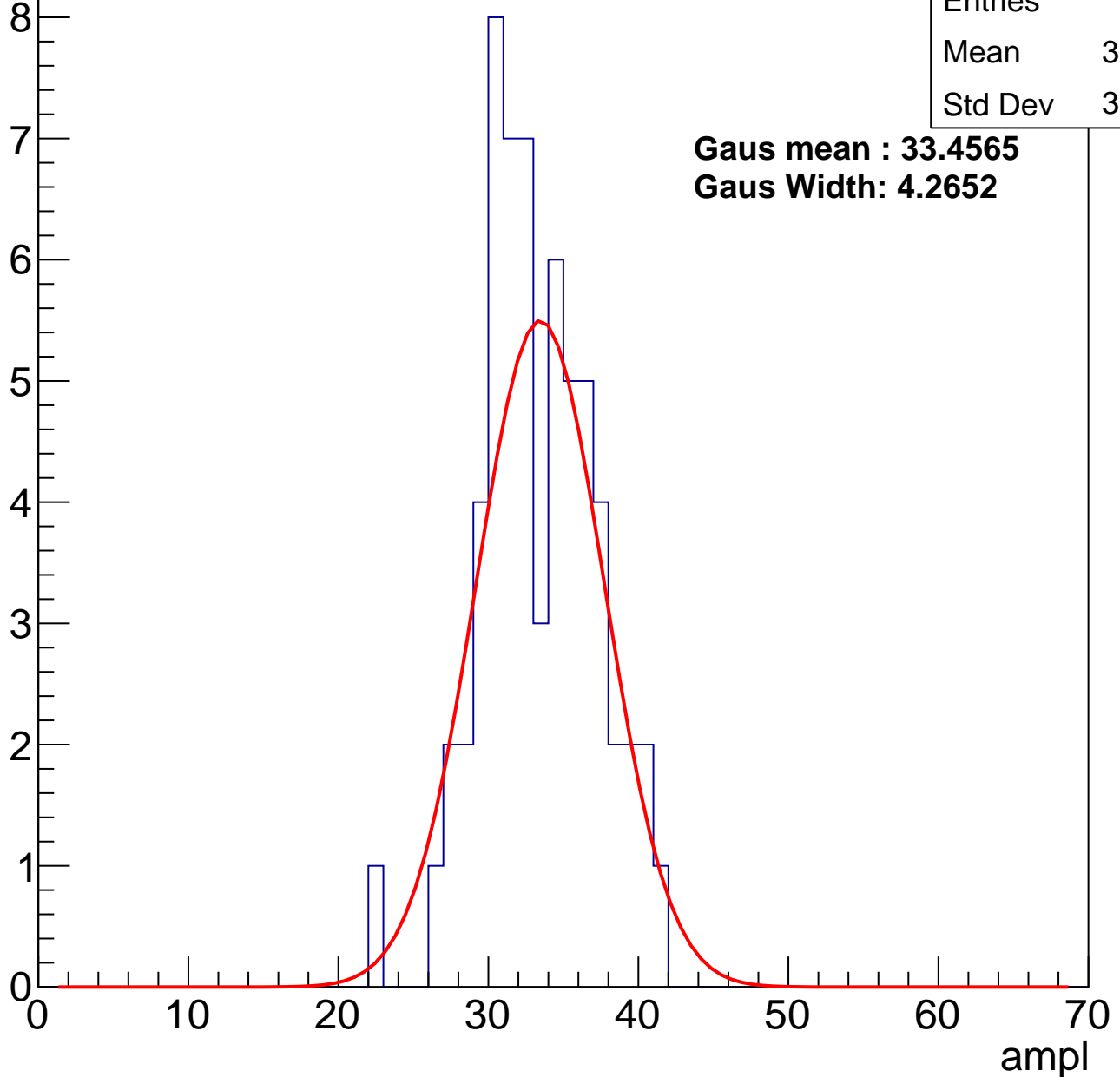
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	32.84
Std Dev	3.768

**Gaus mean : 33.4565**

**Gaus Width: 4.2652**



# B0L102S, U6-ch122, adc2

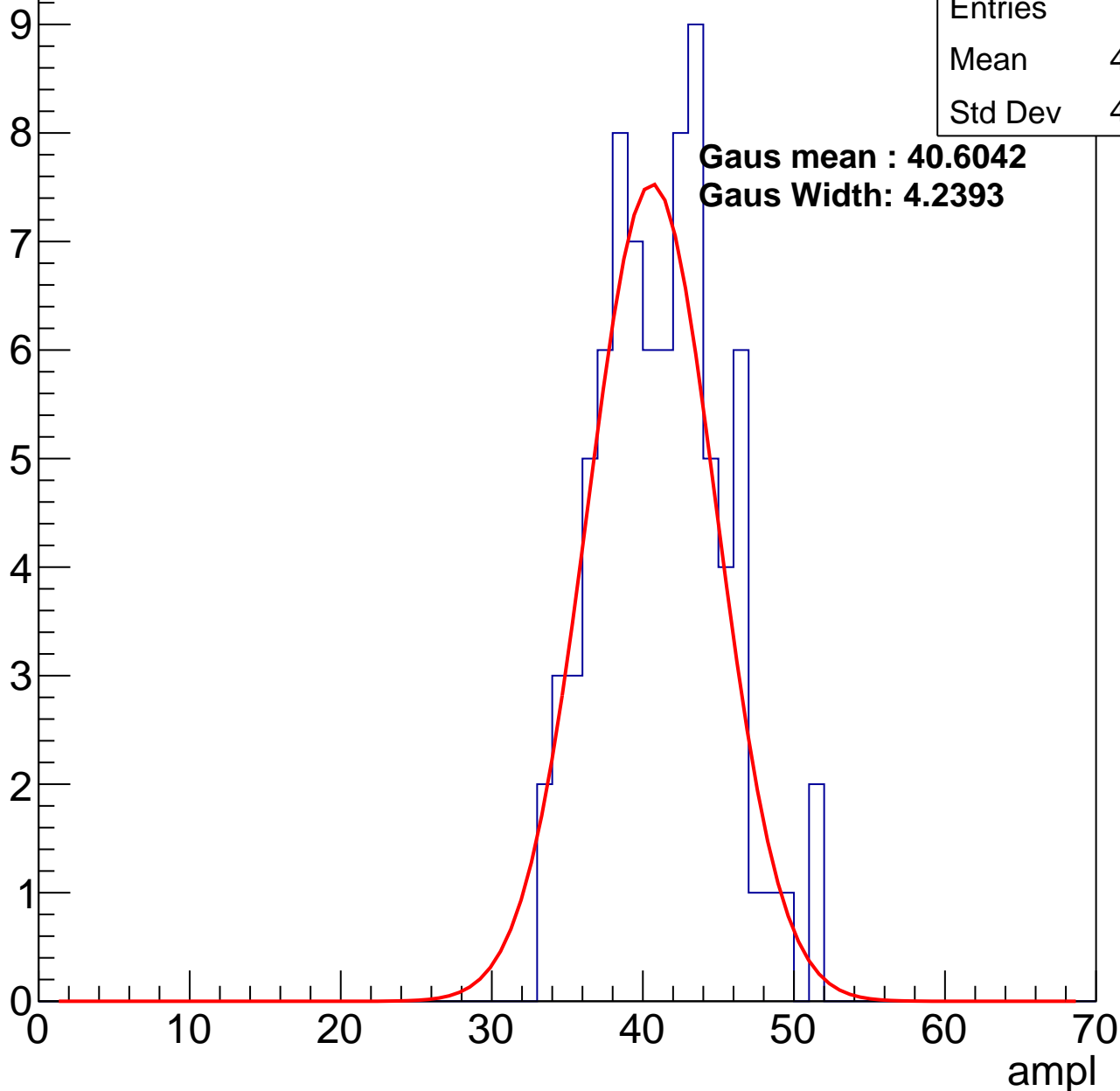
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	83
Mean	40.76
Std Dev	4.044

**Gaus mean : 40.6042**

**Gaus Width: 4.2393**

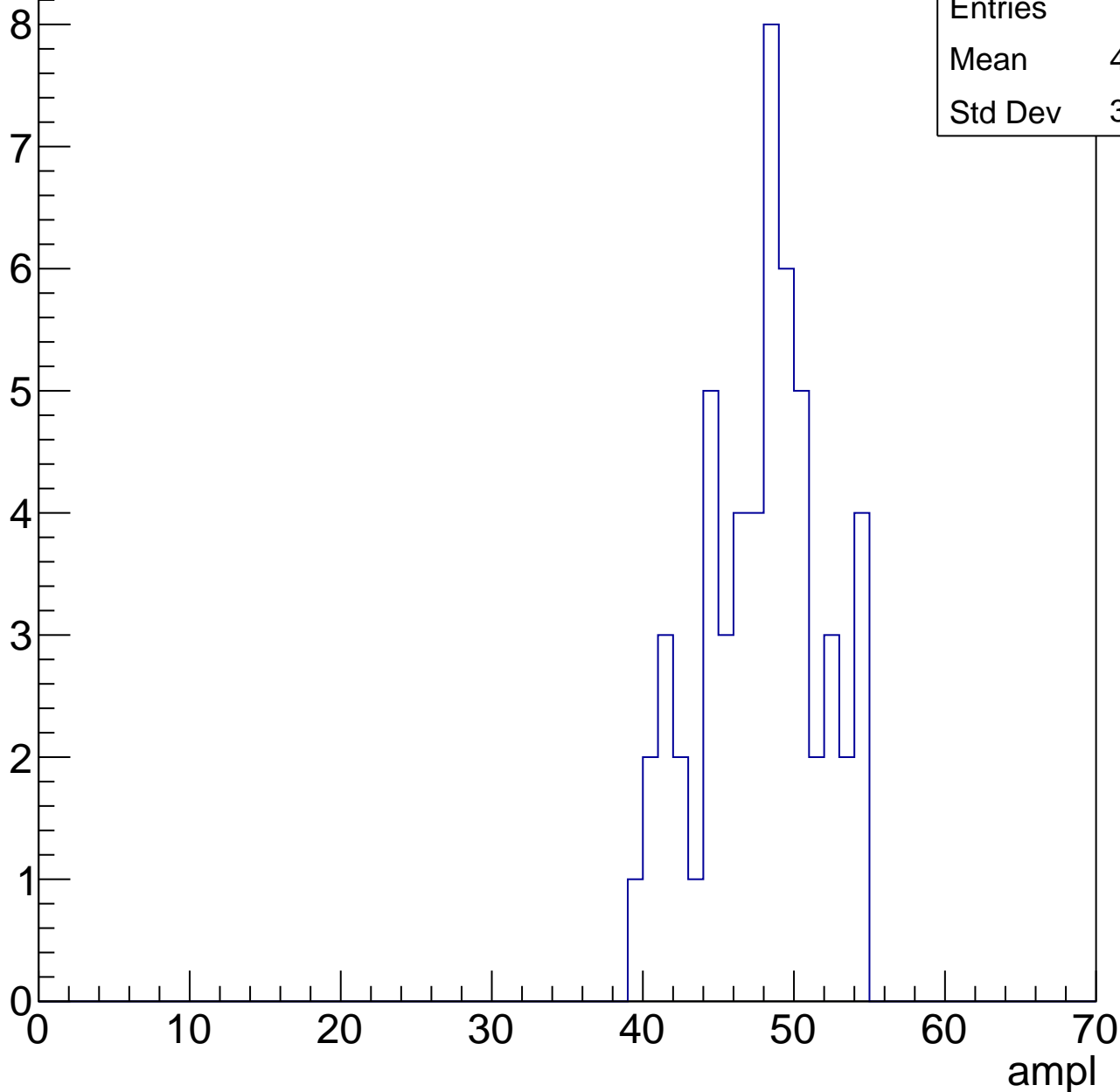


# B0L102S, U6-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	47.35
Std Dev	3.914

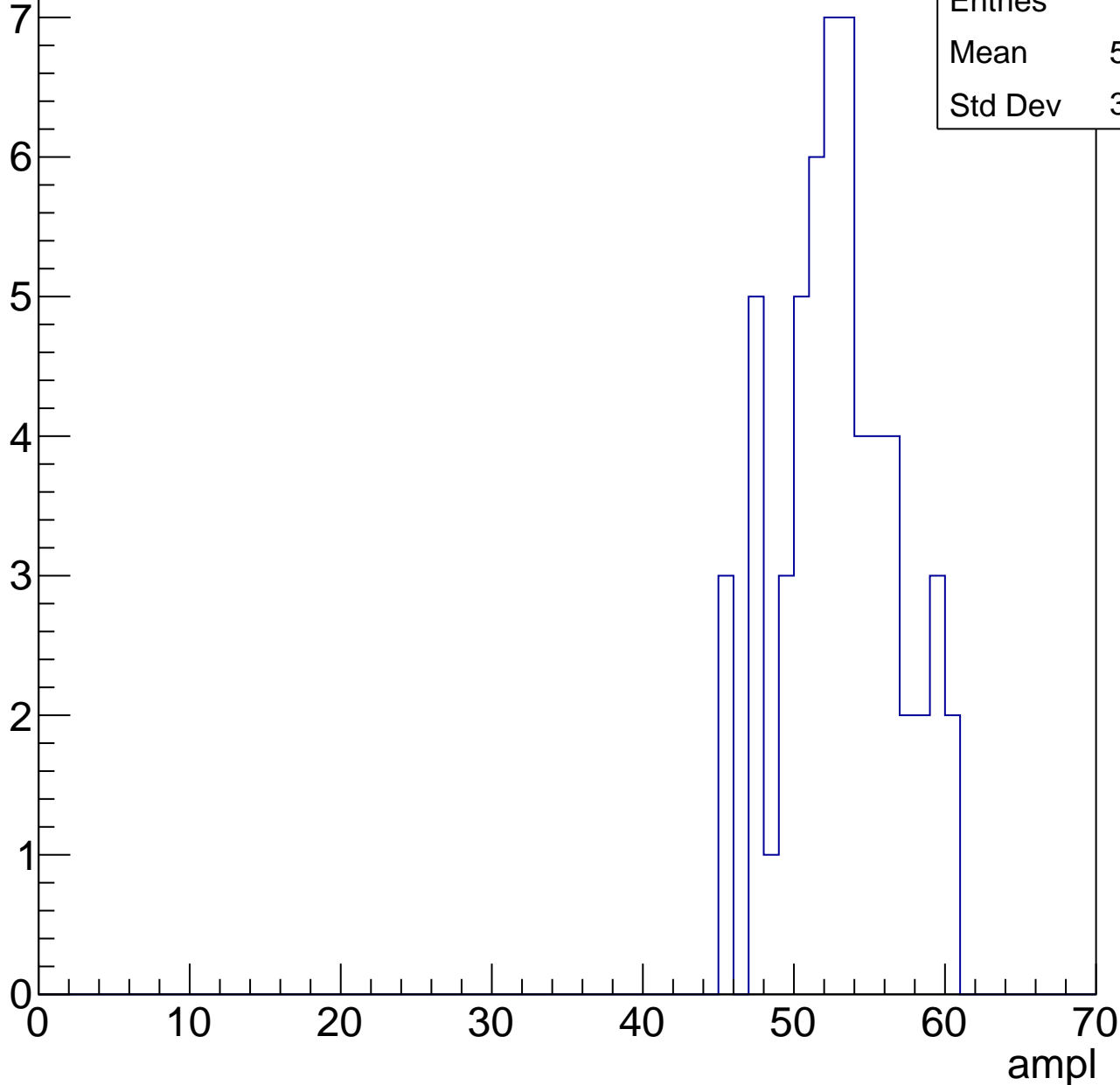


# B0L102S, U6-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	52.47
Std Dev	3.825

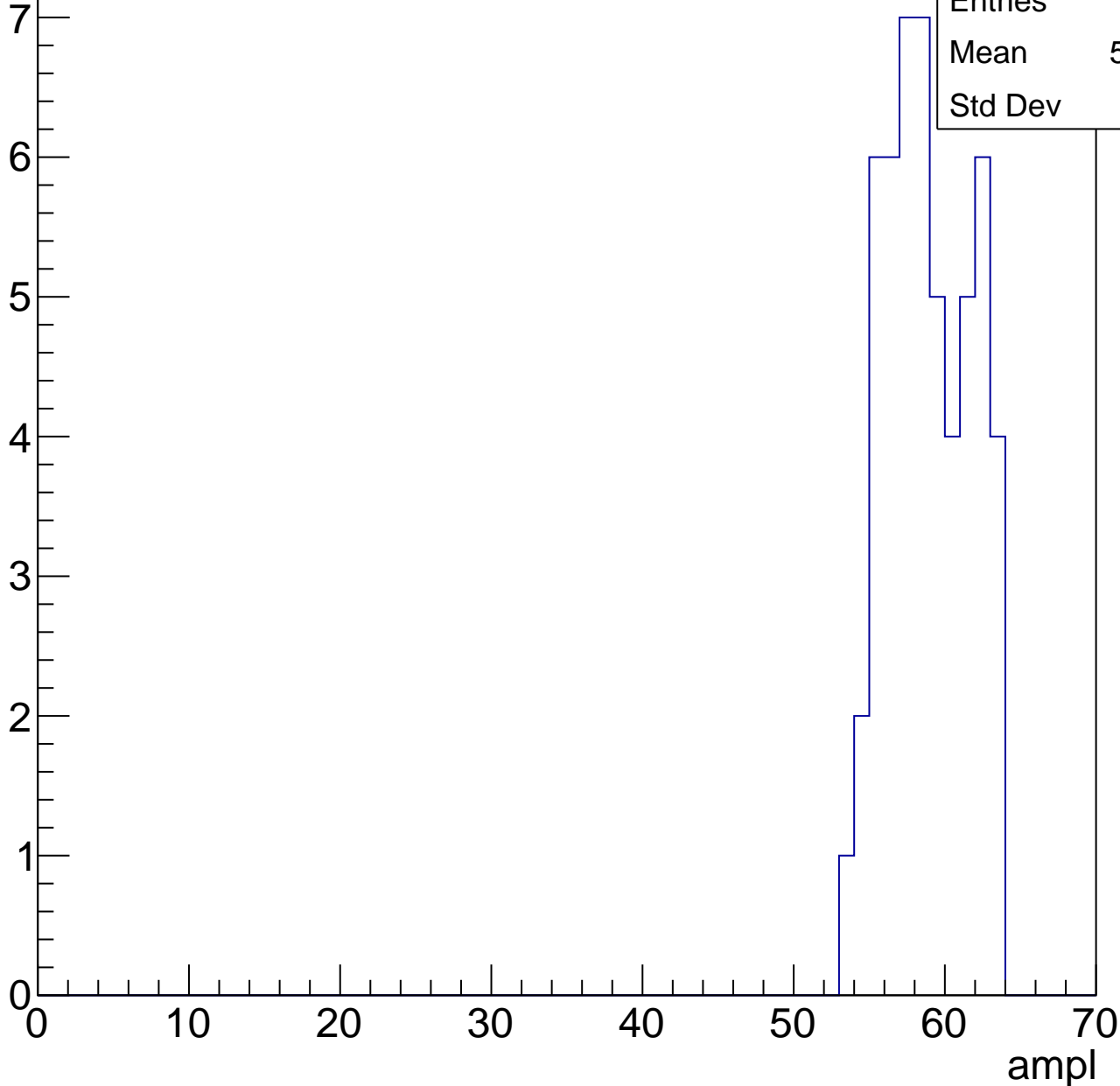


# B0L102S, U6-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

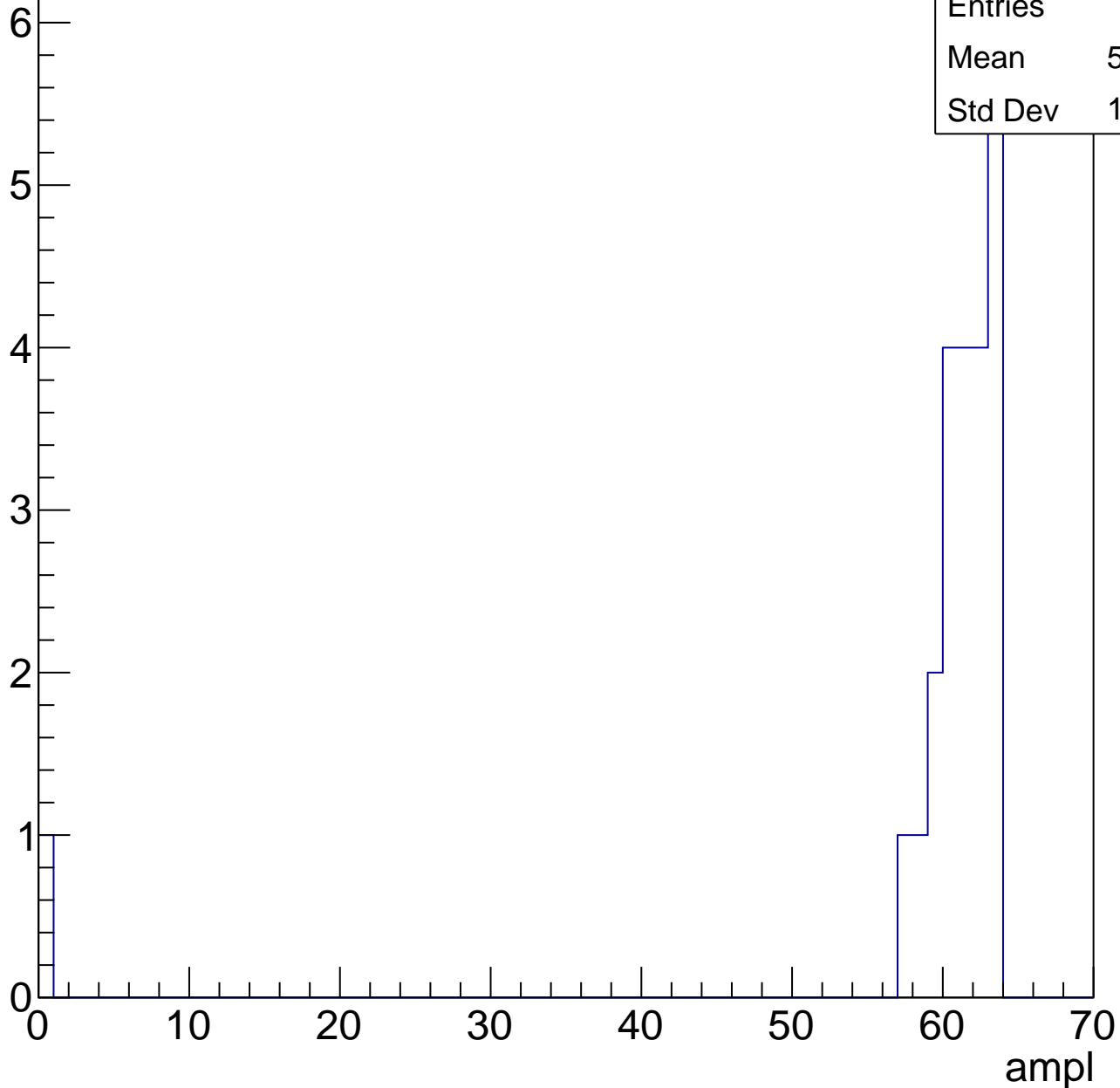
Entries	53
Mean	58.42
Std Dev	2.73



# B0L102S, U6-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch123, adc0

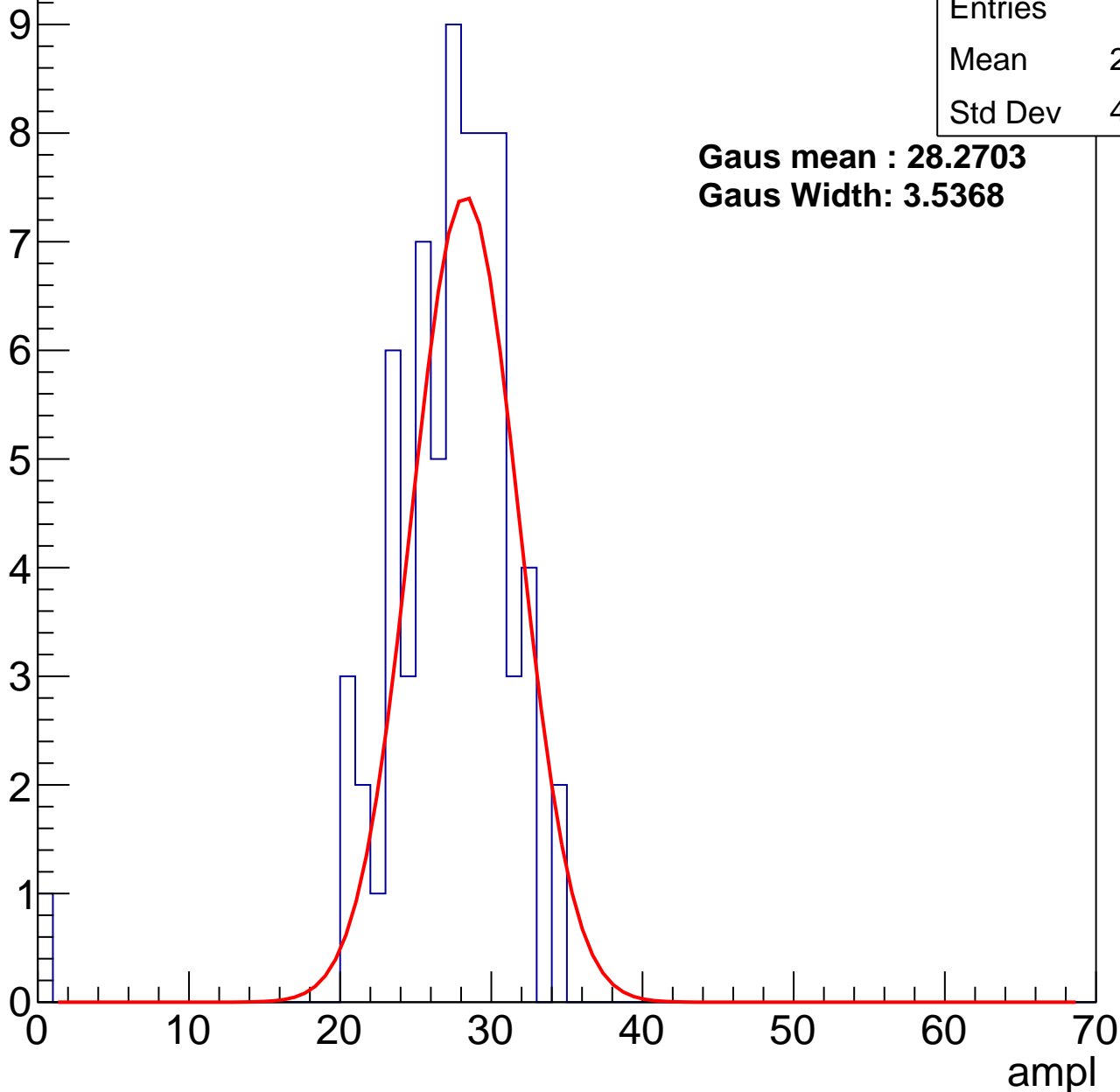
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	26.67
Std Dev	4.597

**Gaus mean : 28.2703**

**Gaus Width: 3.5368**



# B0L102S, U6-ch123, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	34.84
Std Dev	3.659

**Gaus mean : 35.2876**

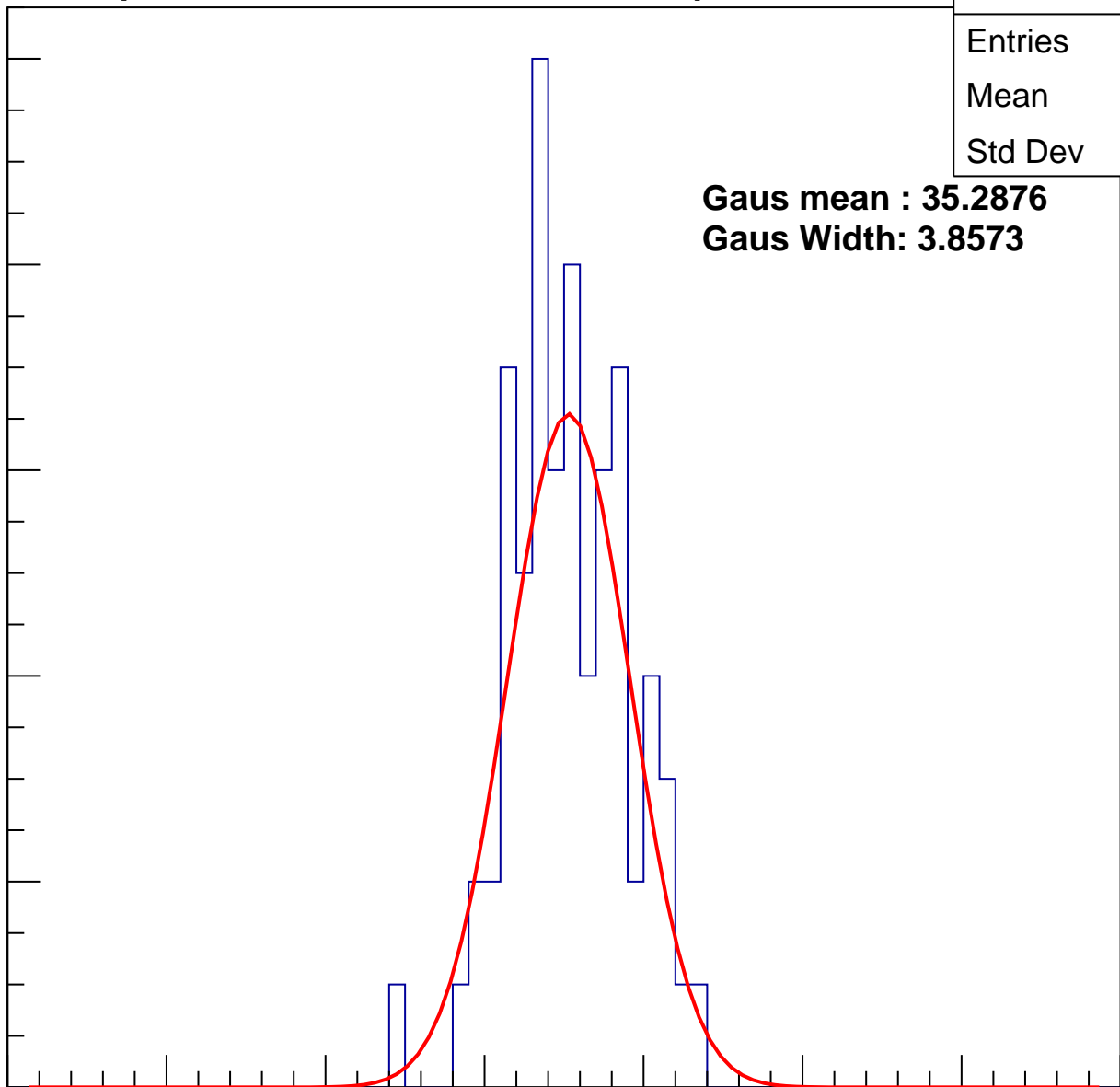
**Gaus Width: 3.8573**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U6-ch123, adc2

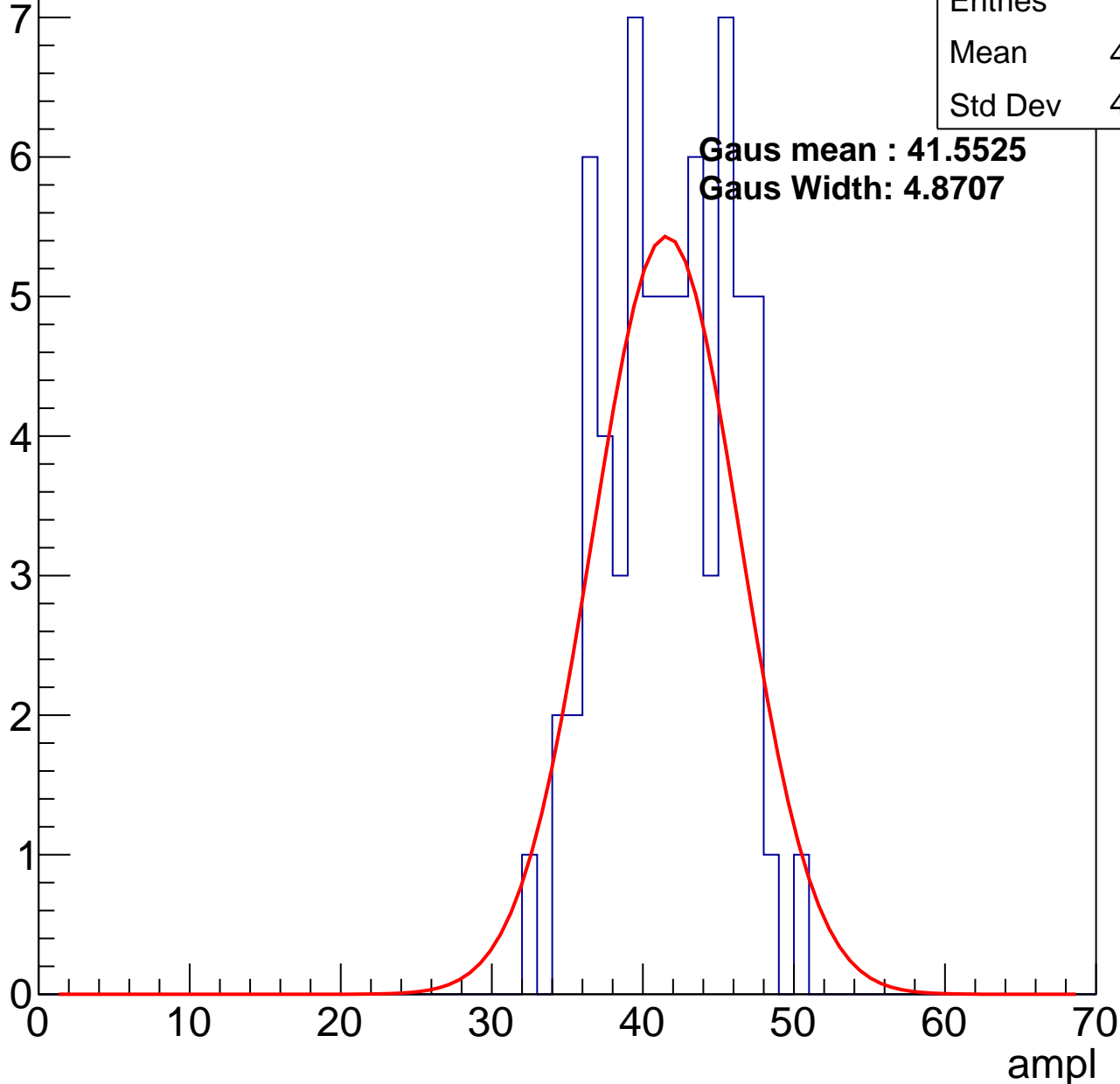
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	41.24
Std Dev	4.066

**Gaus mean : 41.5525**

**Gaus Width: 4.8707**

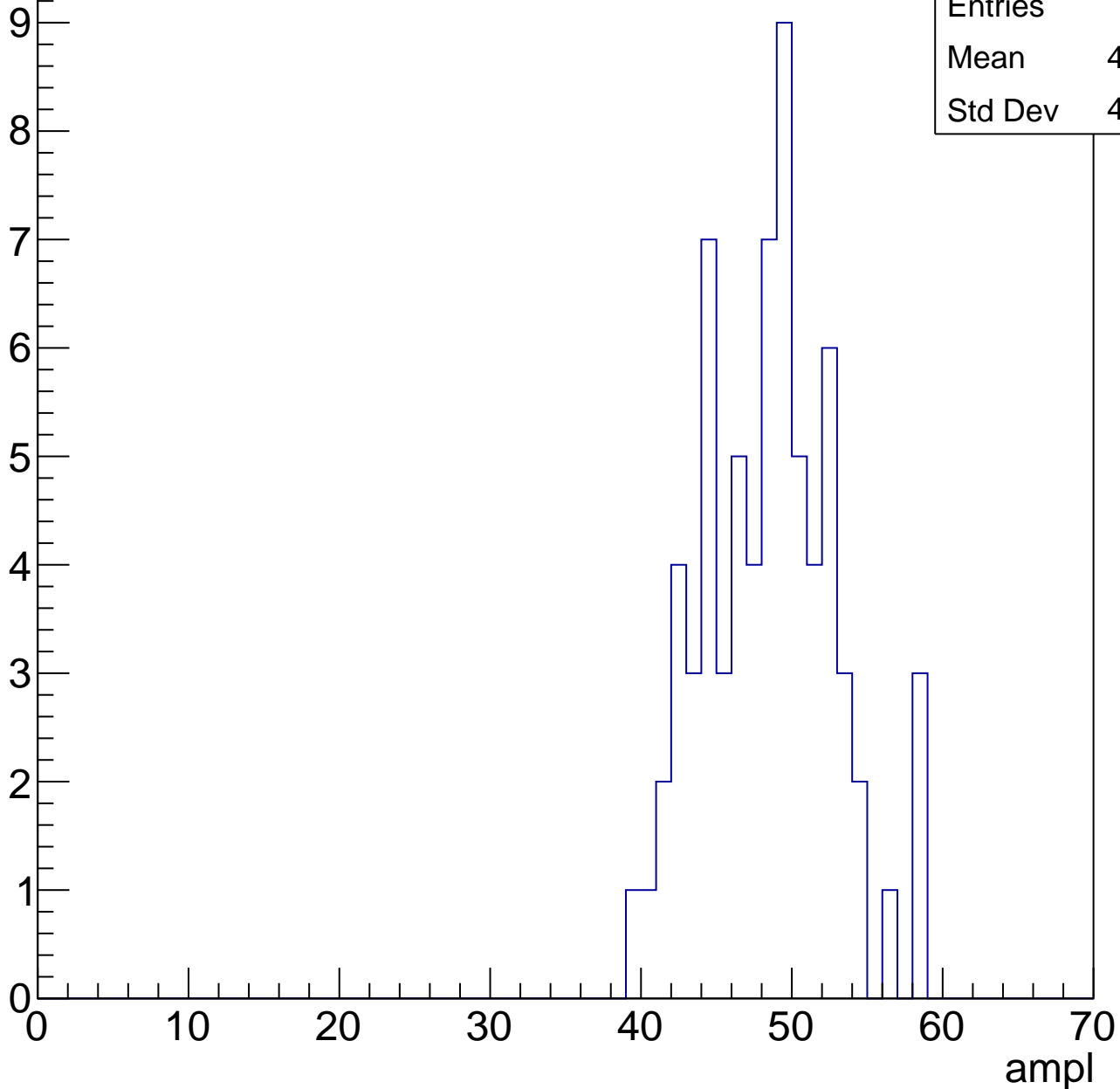


# B0L102S, U6-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	47.99
Std Dev	4.304

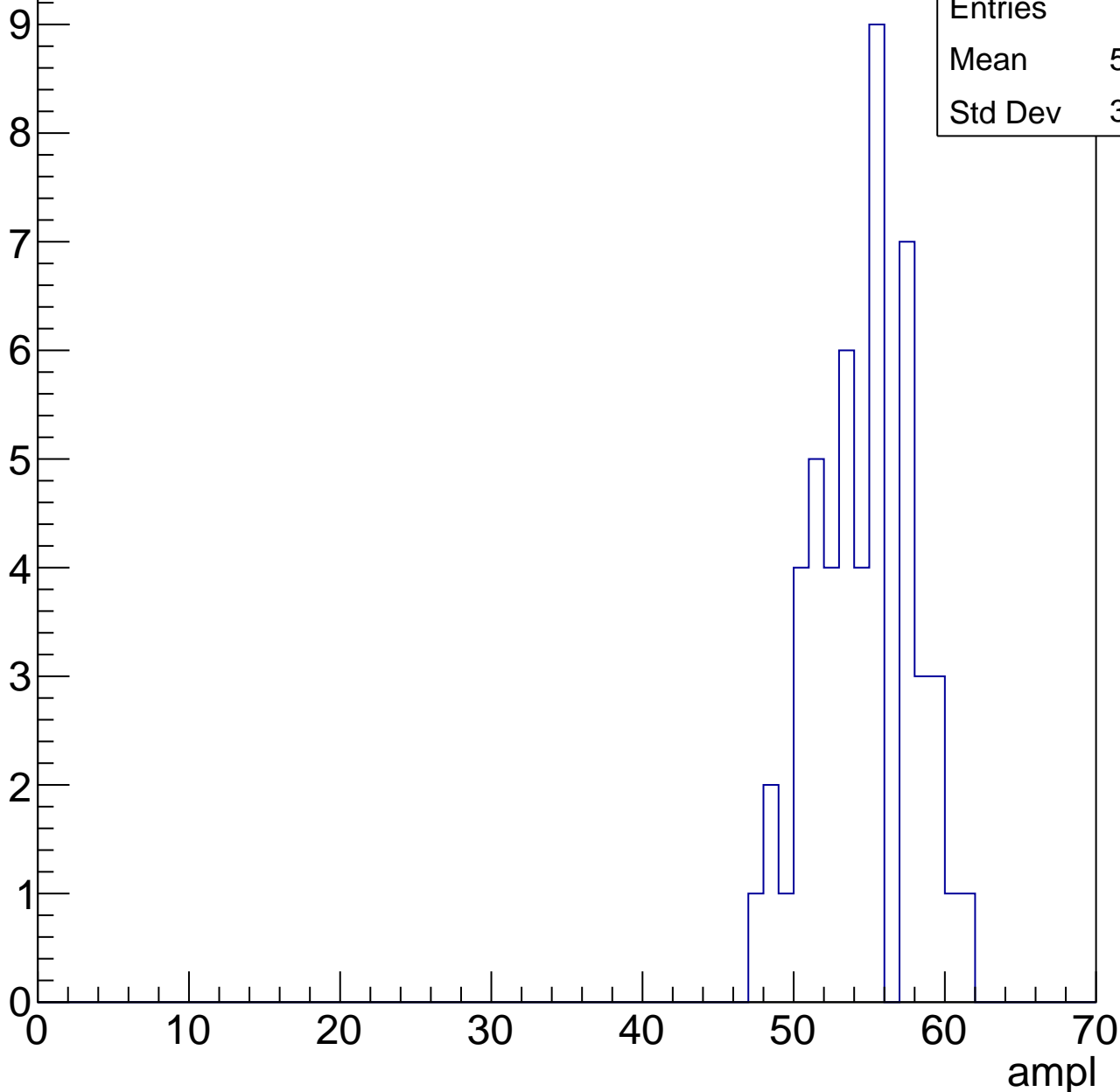


# B0L102S, U6-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	54.02
Std Dev	3.305

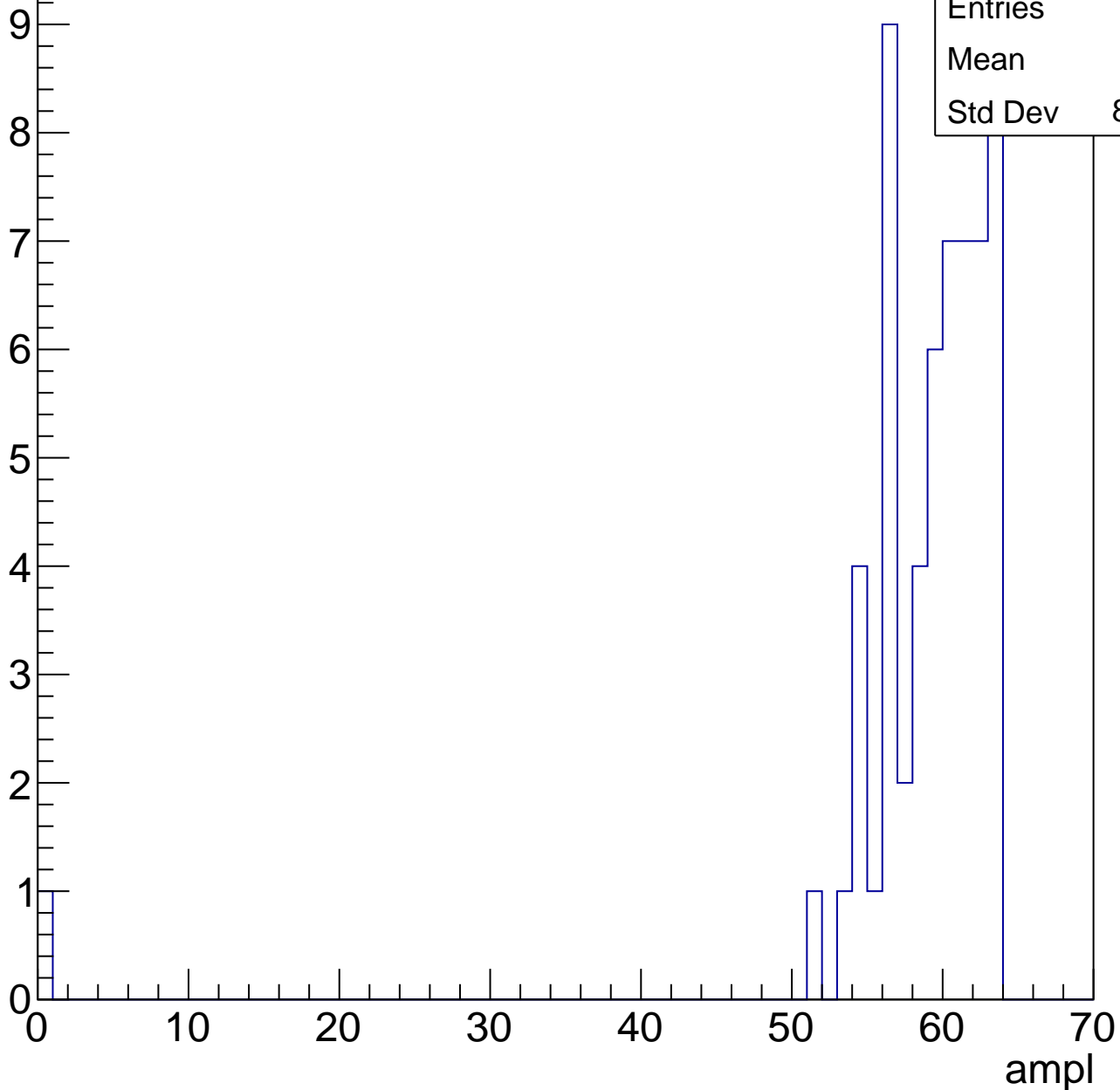


# B0L102S, U6-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

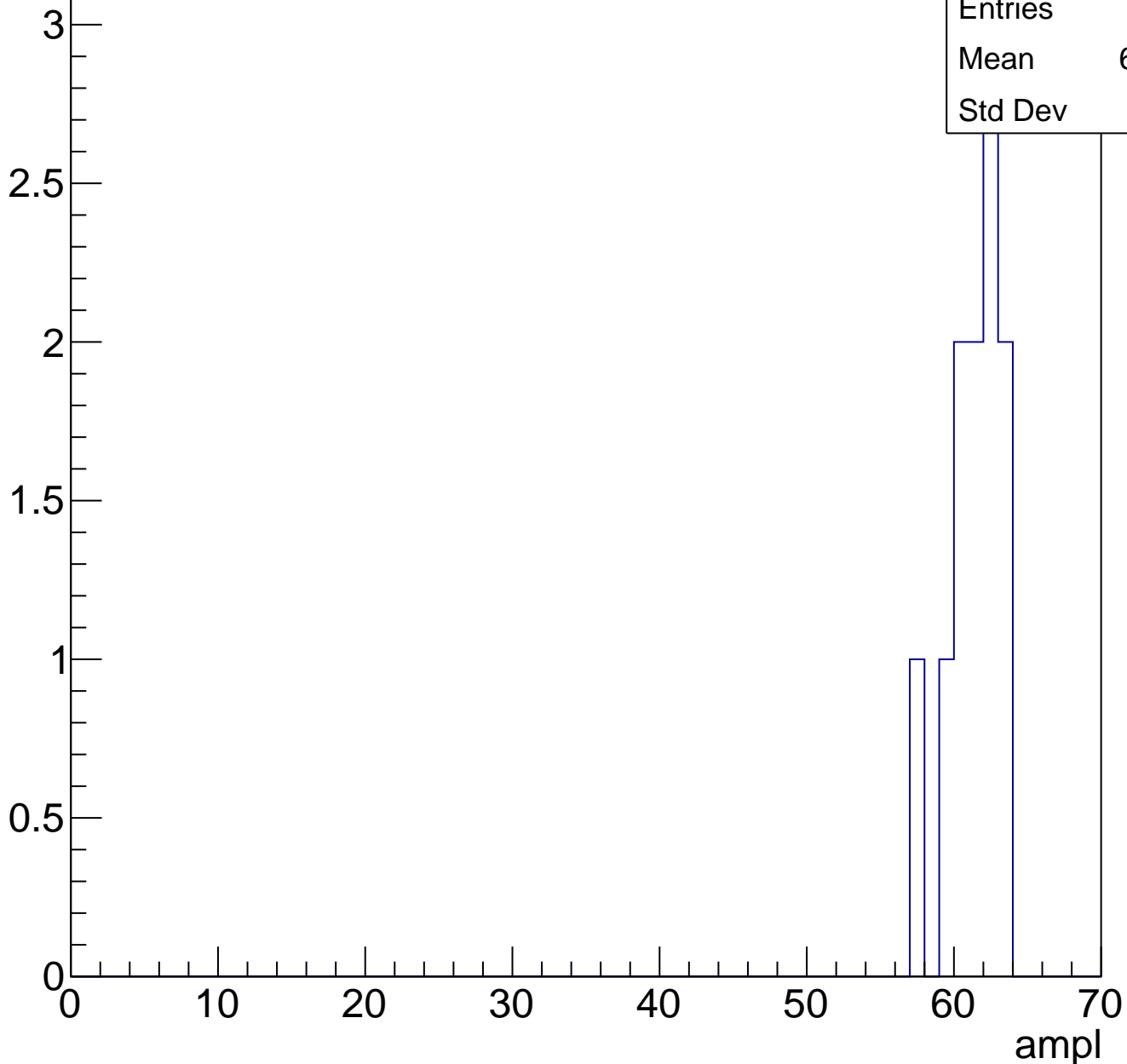
Entries	58
Mean	58
Std Dev	8.261



# B0L102S, U6-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U6-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch124, adc0

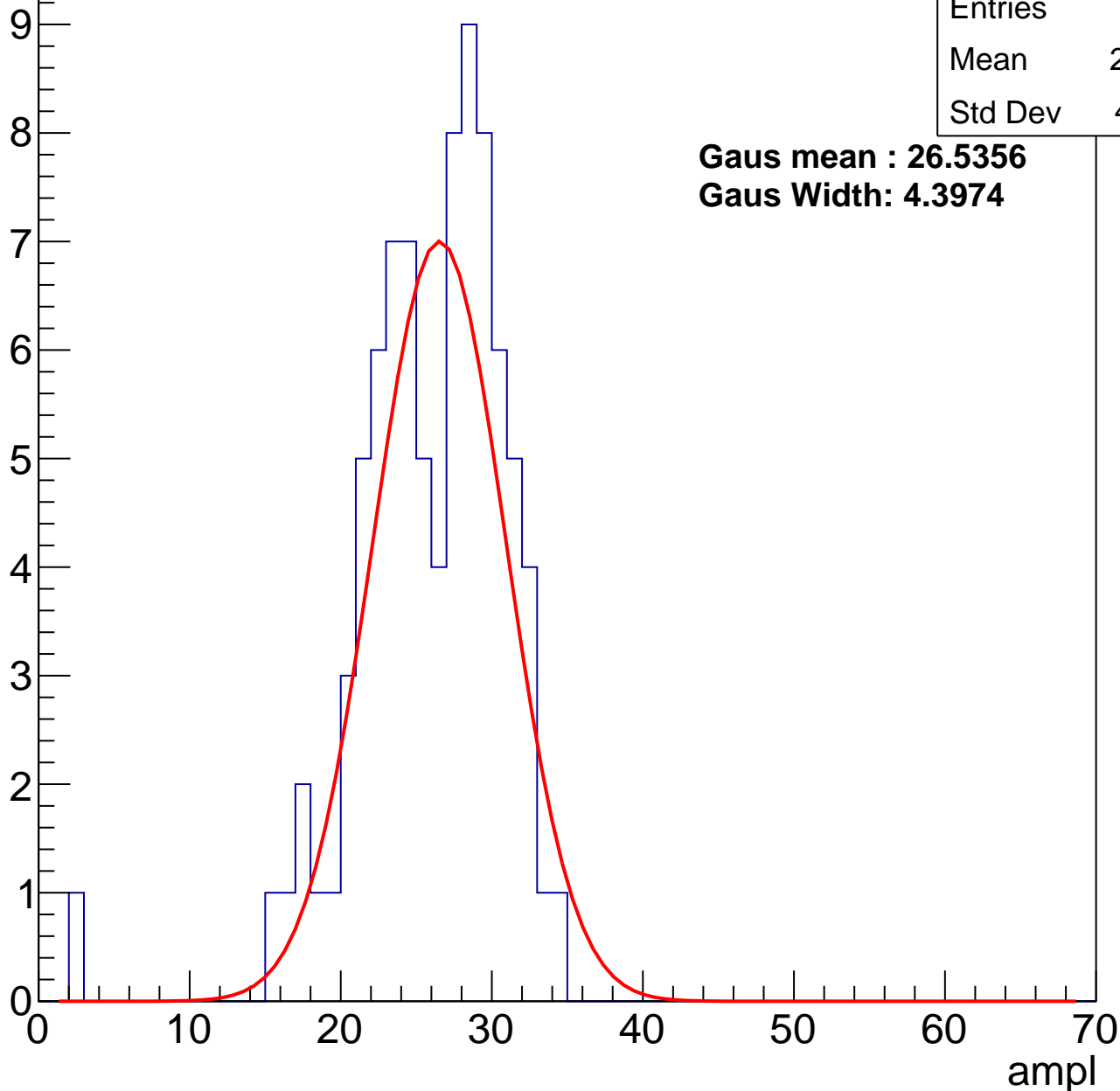
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	86
Mean	25.45
Std Dev	4.901

**Gaus mean : 26.5356**

**Gaus Width: 4.3974**



# B0L102S, U6-ch124, adc1

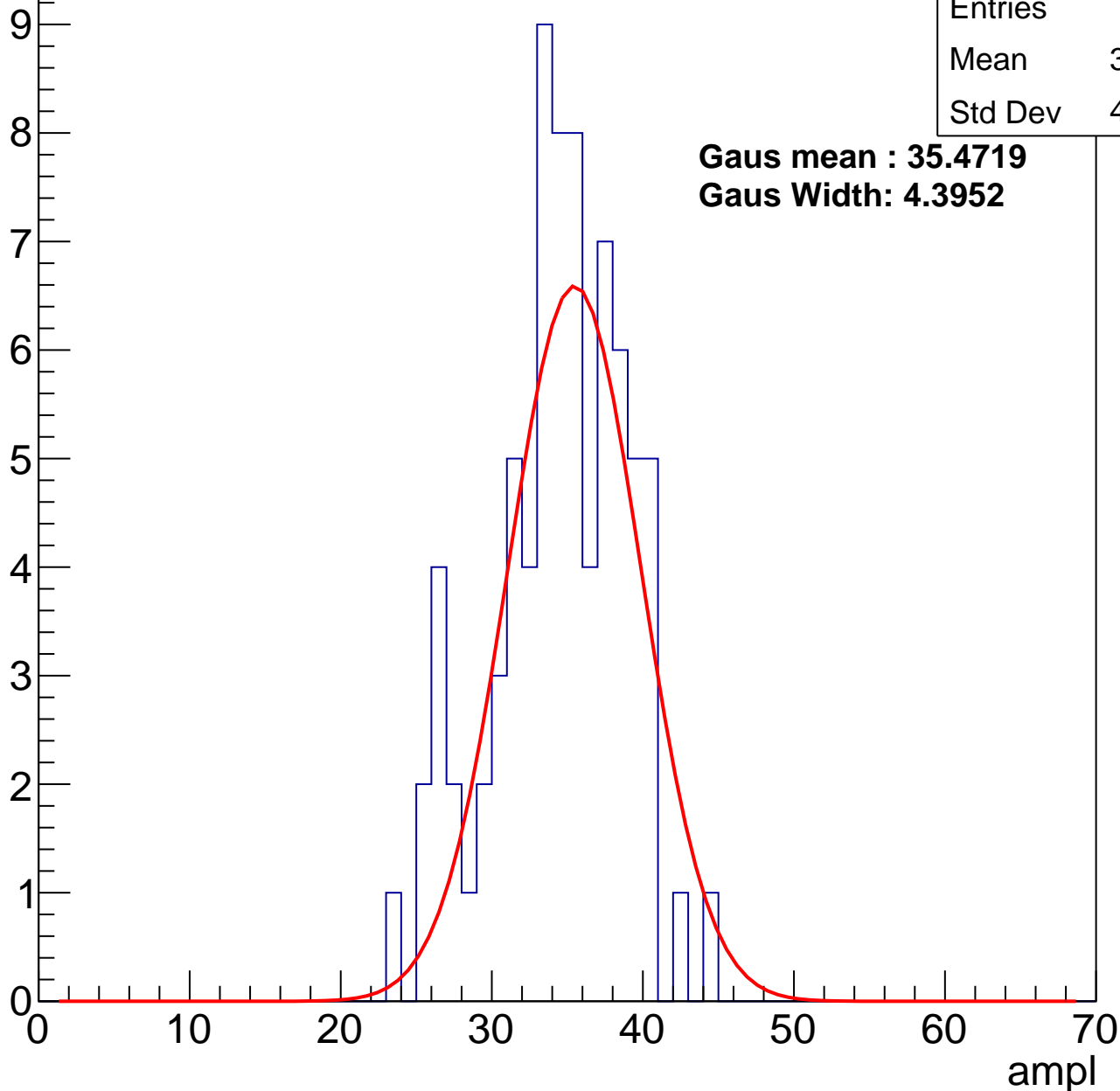
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	33.99
Std Dev	4.375

**Gaus mean : 35.4719**

**Gaus Width: 4.3952**



# B0L102S, U6-ch124, adc2

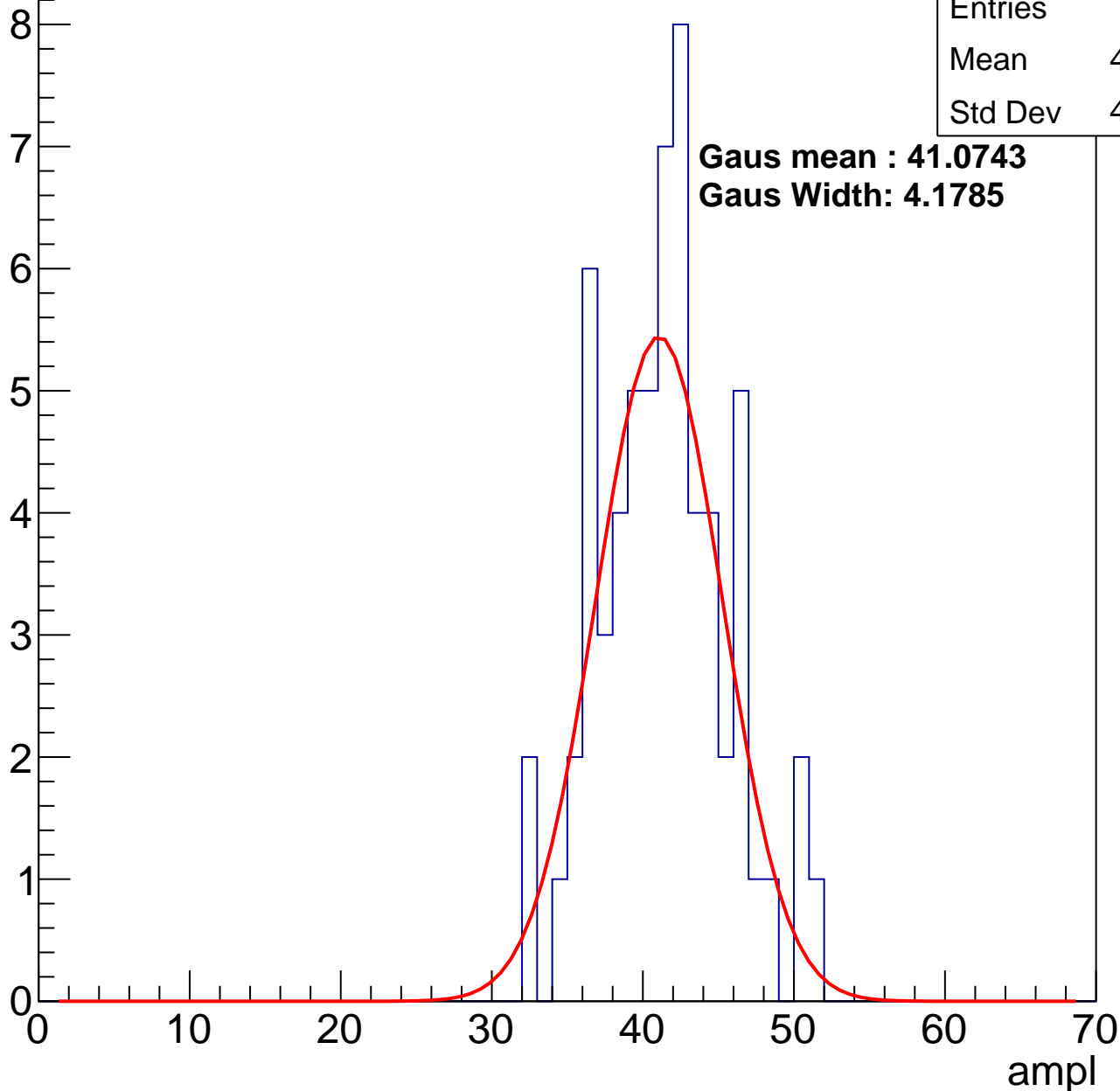
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	40.94
Std Dev	4.189

**Gaus mean : 41.0743**

**Gaus Width: 4.1785**

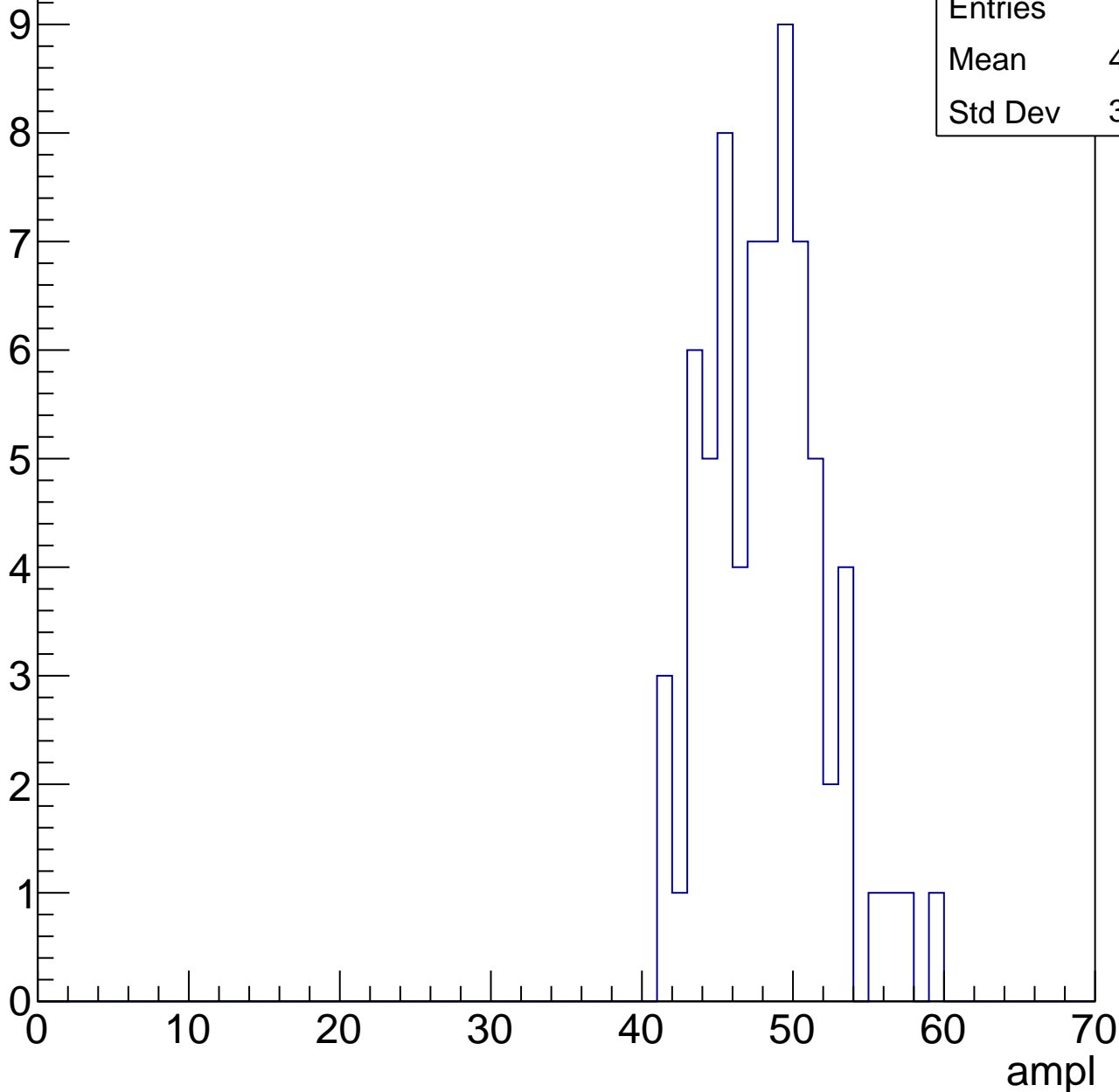


# B0L102S, U6-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	47.79
Std Dev	3.789

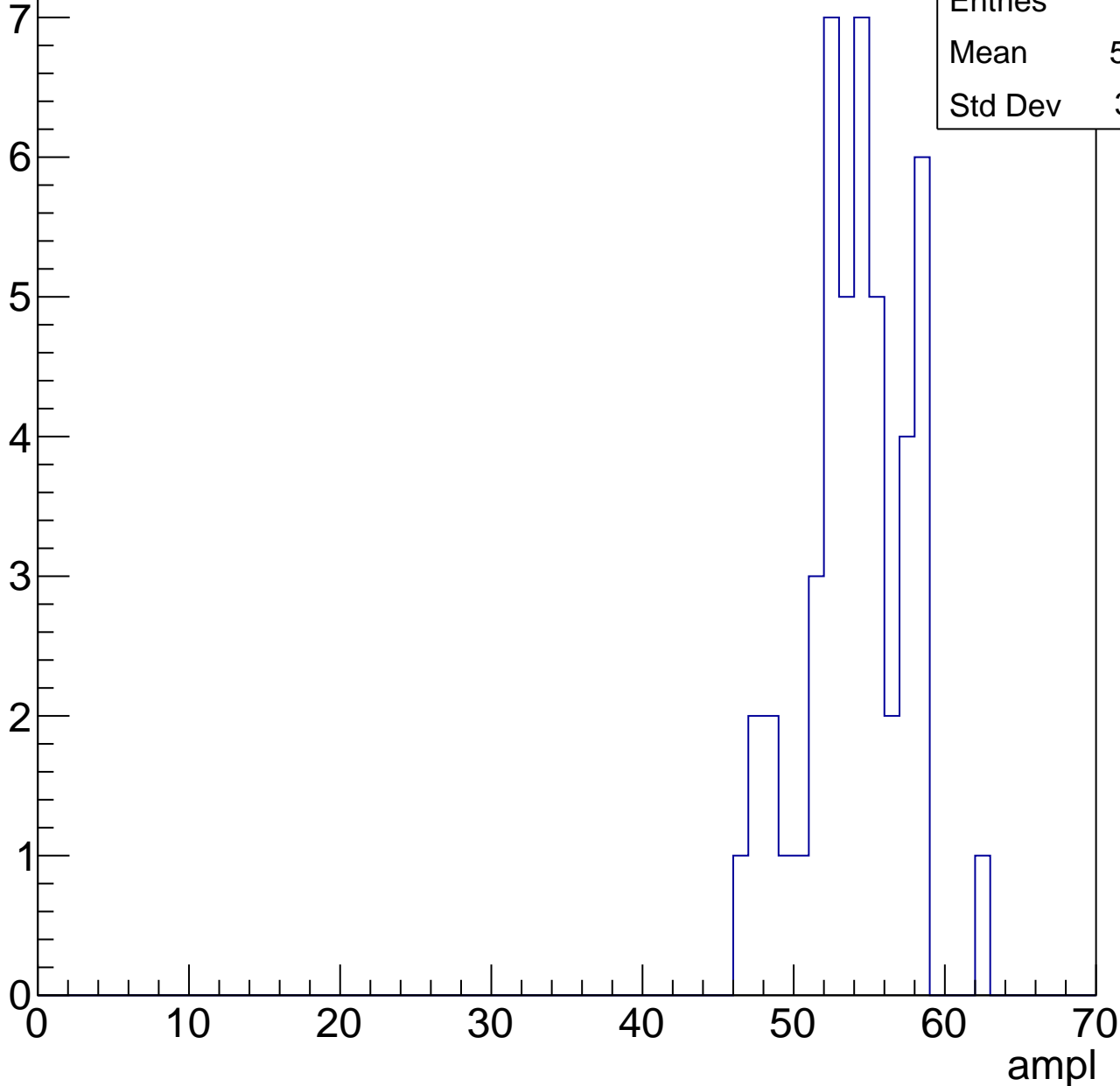


# B0L102S, U6-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	53.62
Std Dev	3.381

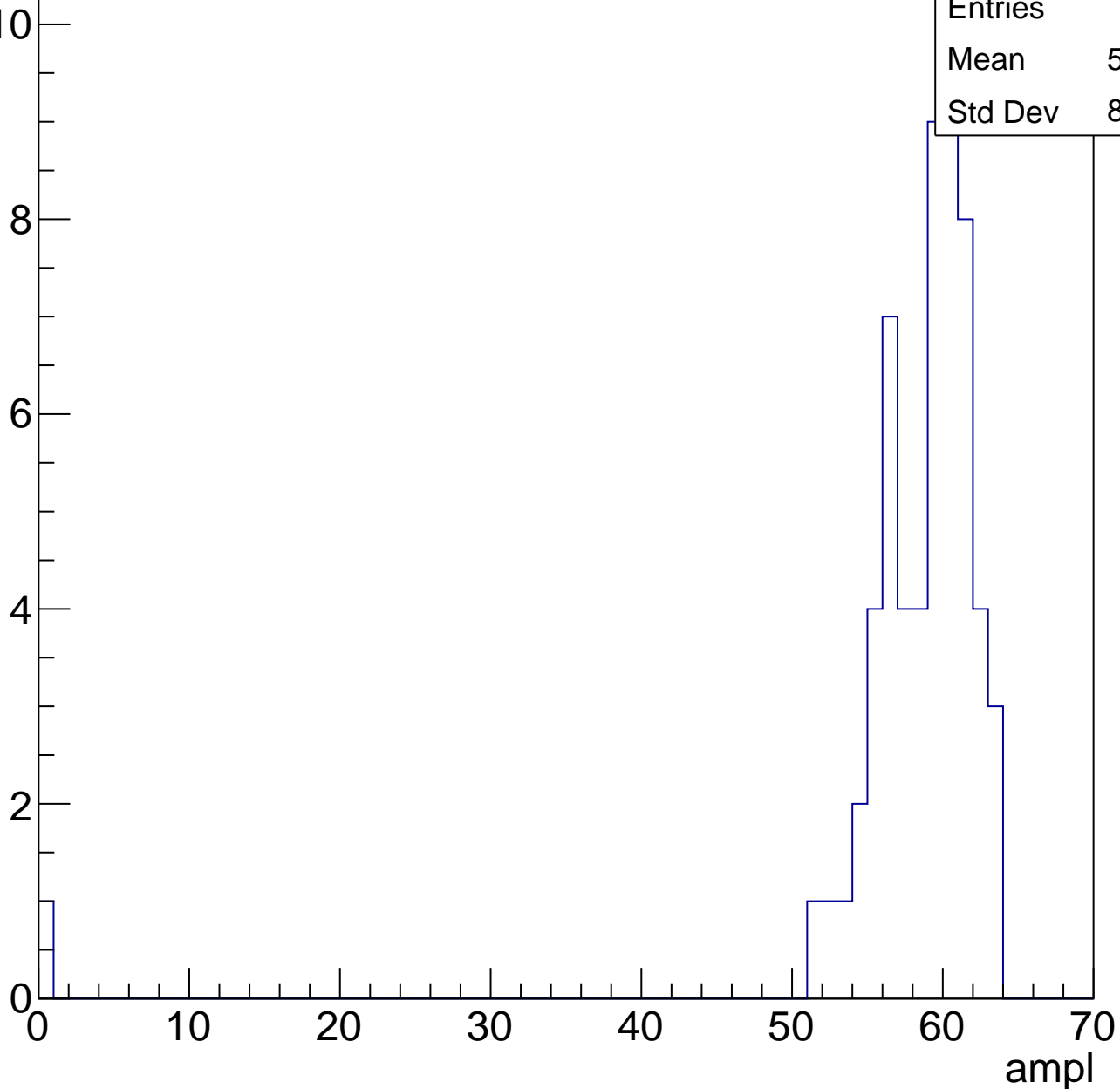


# B0L102S, U6-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	57.49
Std Dev	8.045

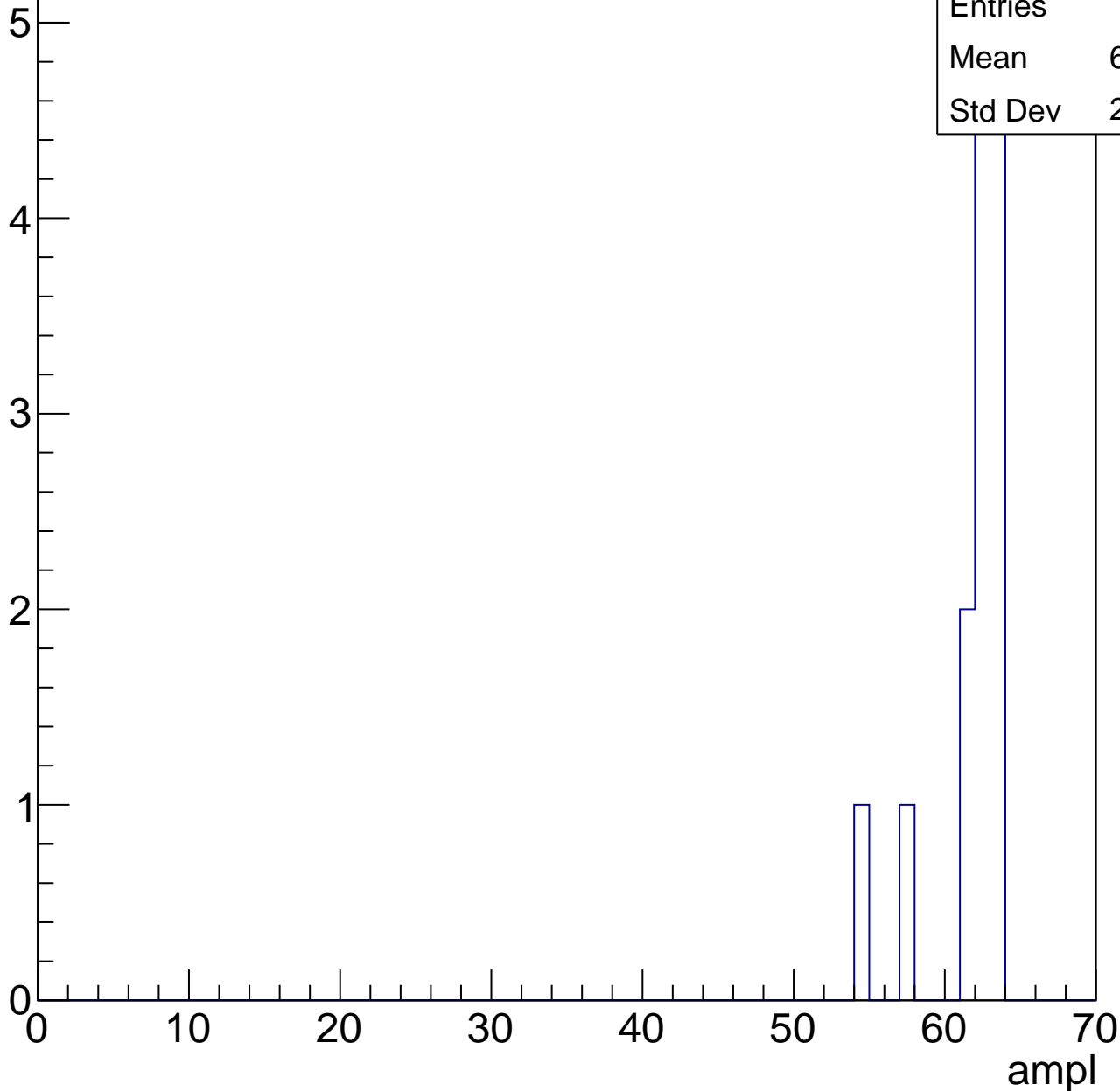


# B0L102S, U6-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	14
Mean	61.29
Std Dev	2.519





# B0L102S, U6-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch125, adc0

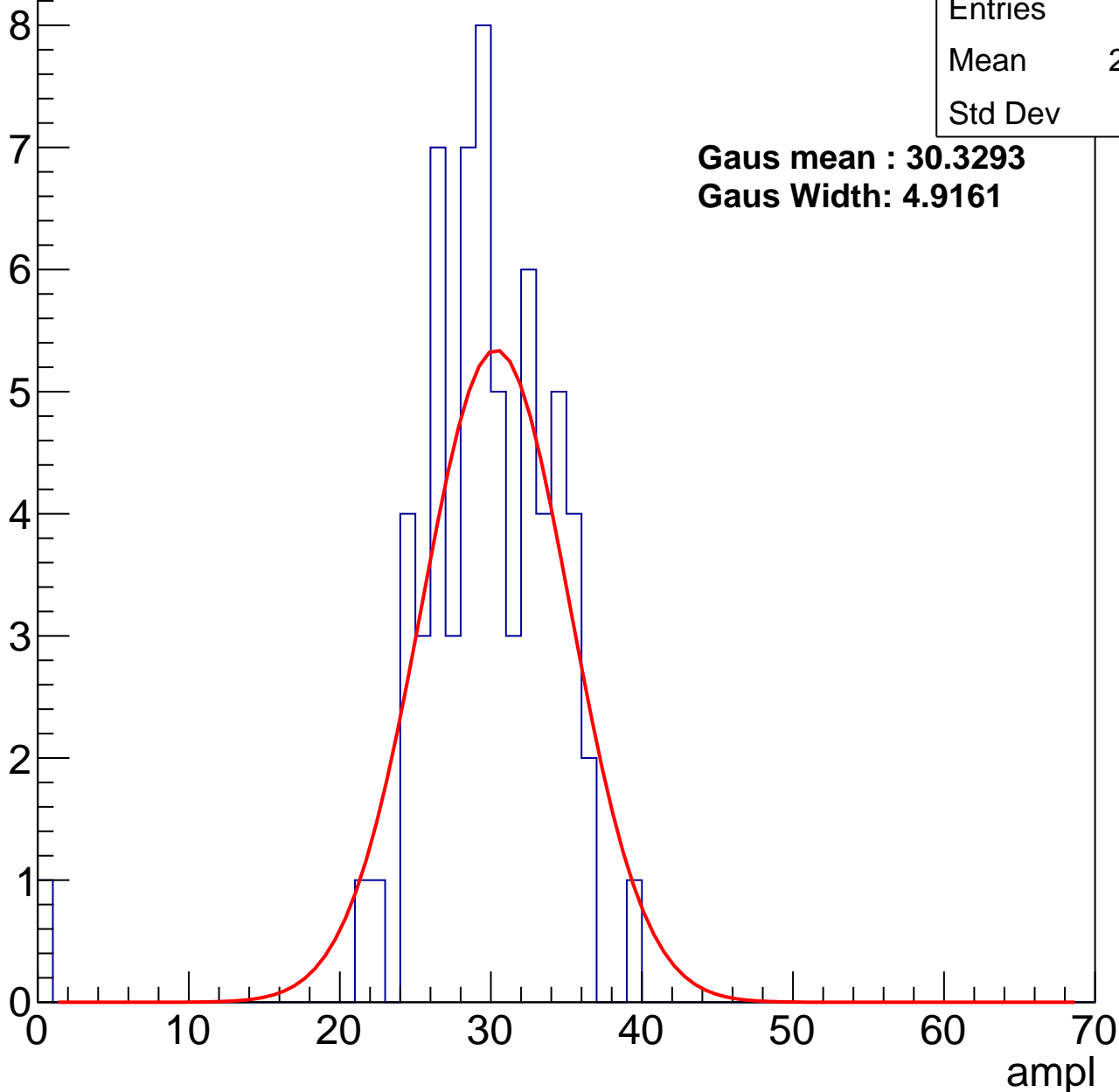
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	29.12
Std Dev	5.24

**Gaus mean : 30.3293**

**Gaus Width: 4.9161**



# B0L102S, U6-ch125, adc1

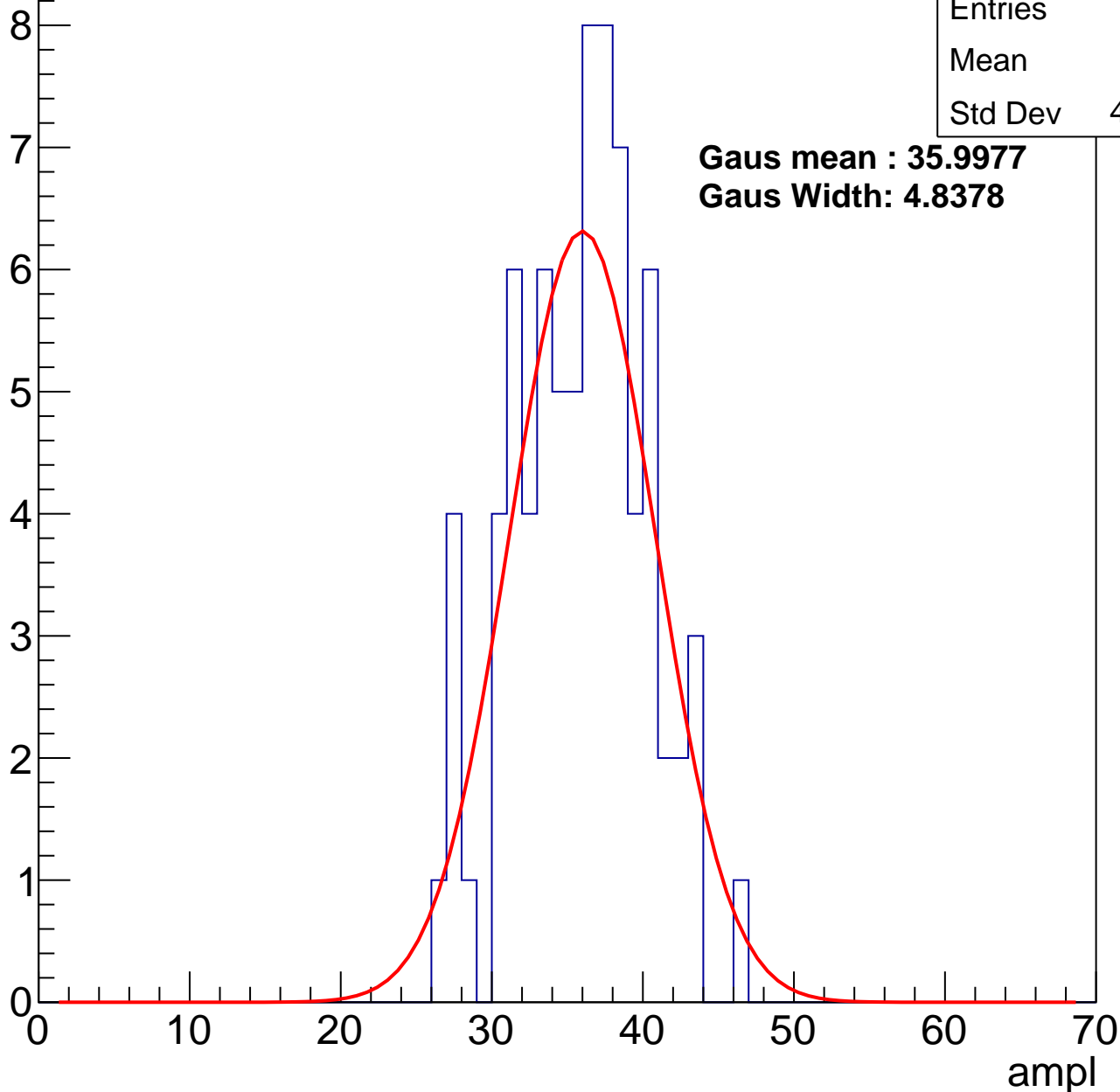
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	35.4
Std Dev	4.316

**Gaus mean : 35.9977**

**Gaus Width: 4.8378**

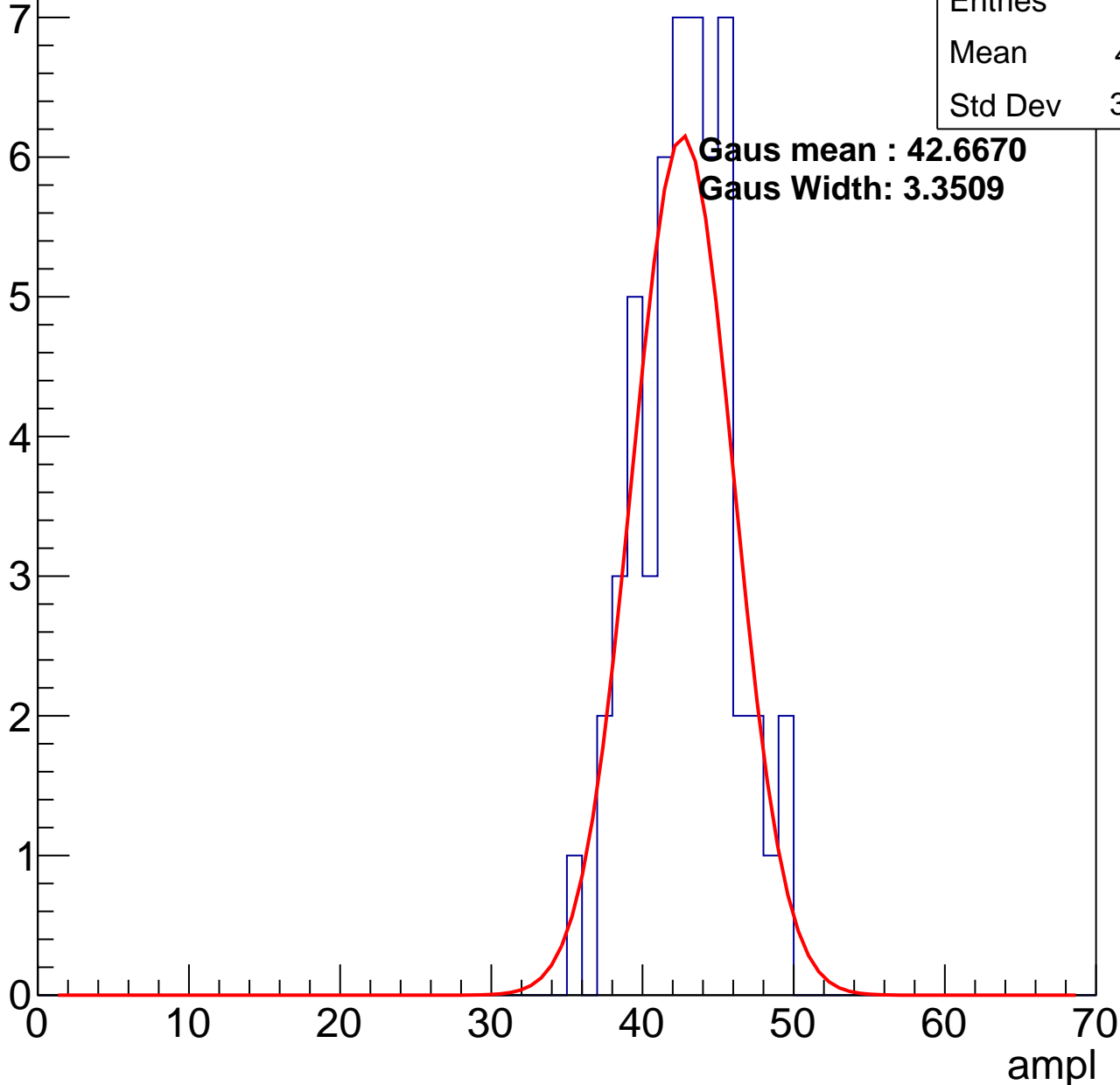


# B0L102S, U6-ch125, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	42.41
Std Dev	3.076

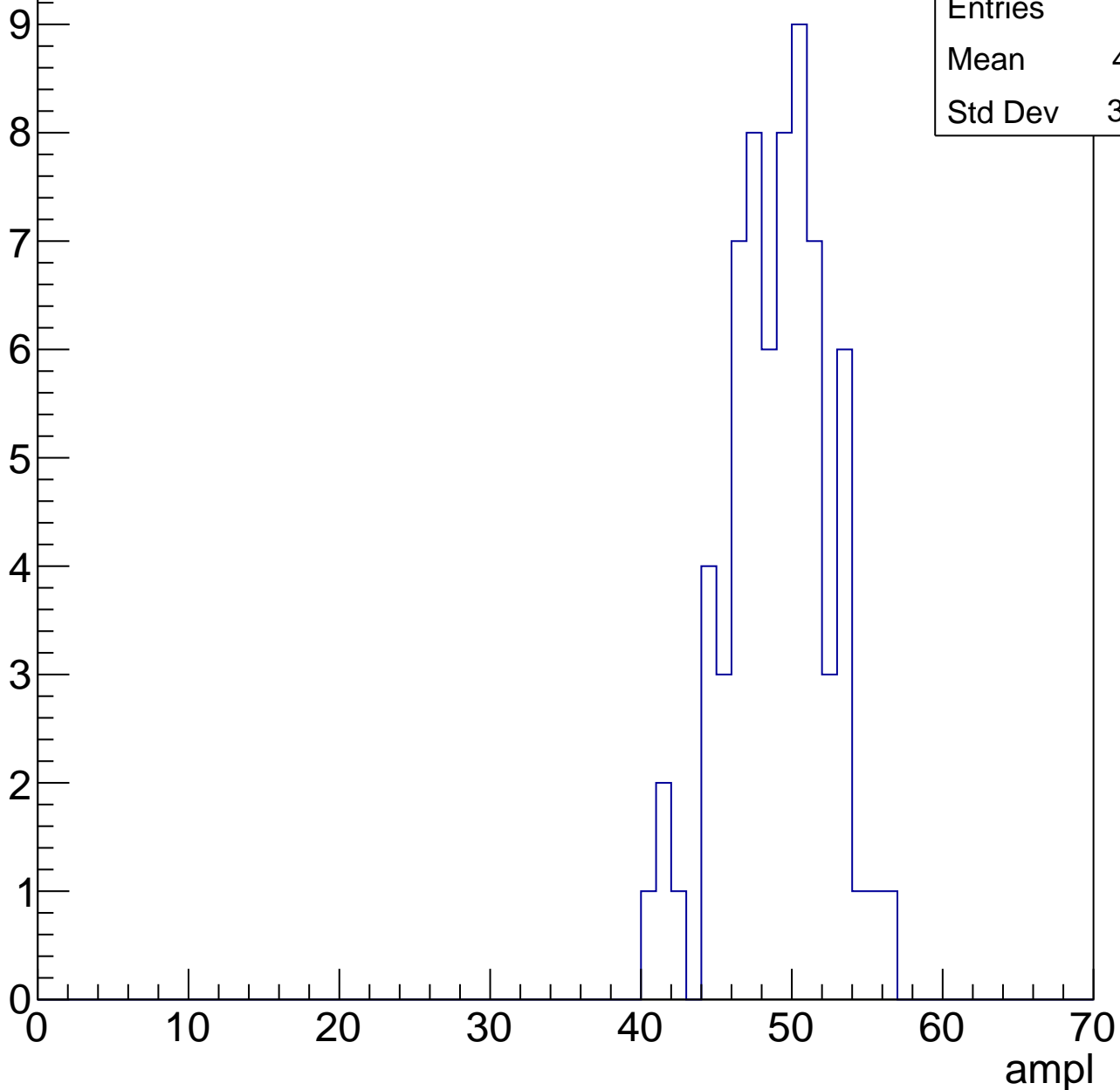


# B0L102S, U6-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	48.51
Std Dev	3.345

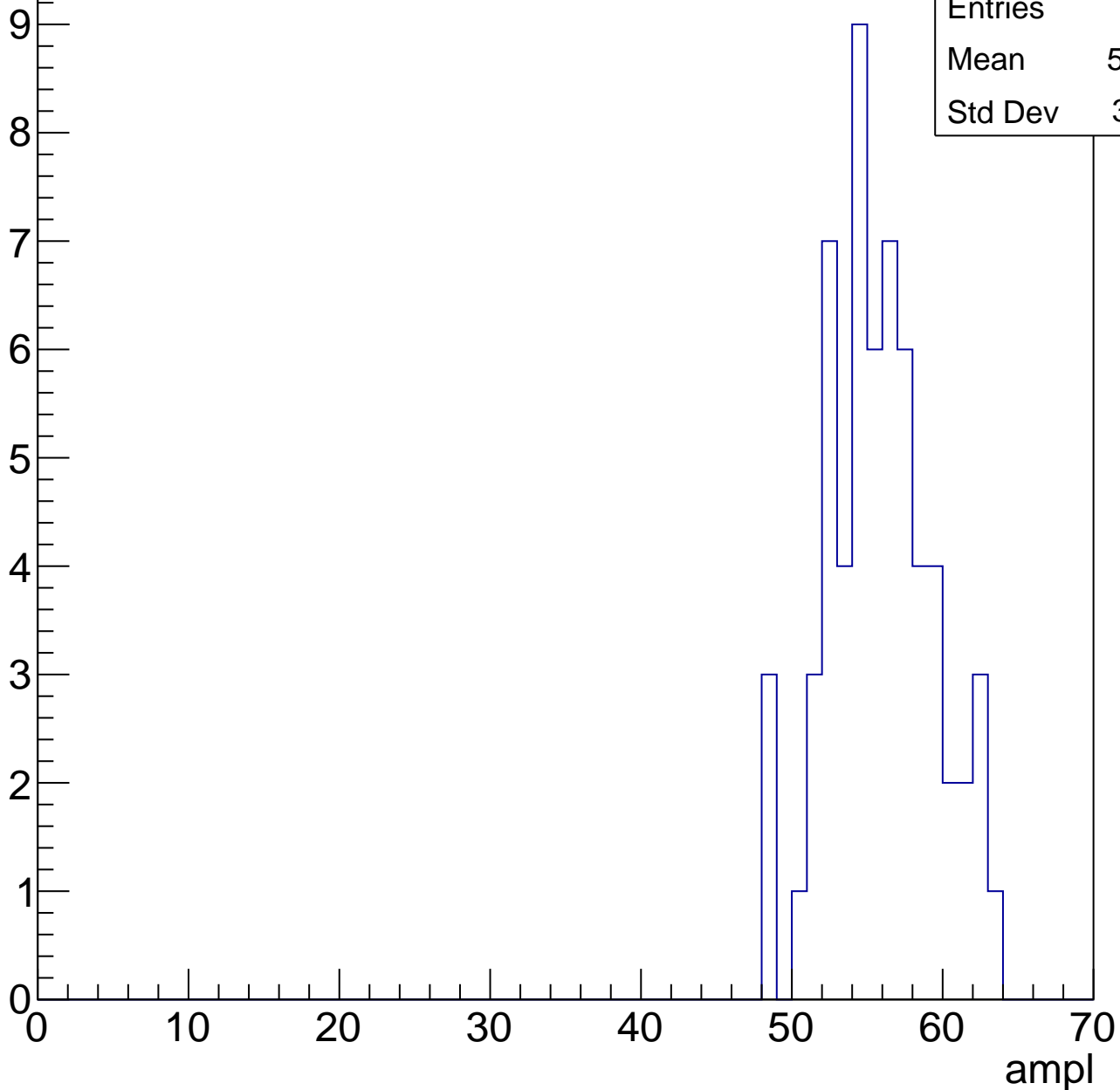


# B0L102S, U6-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	55.35
Std Dev	3.511

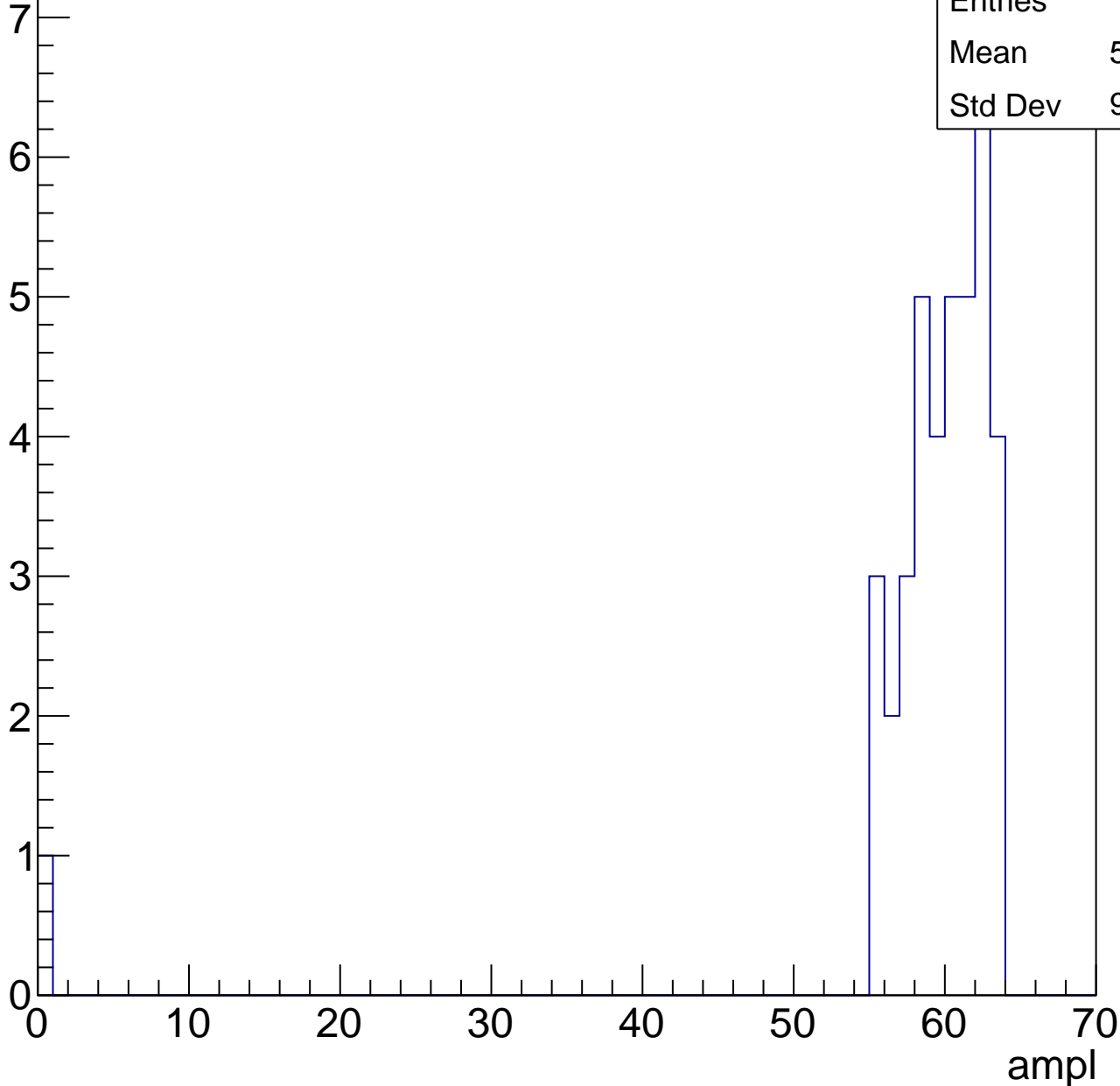


# B0L102S, U6-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	39
Mean	58.08
Std Dev	9.718

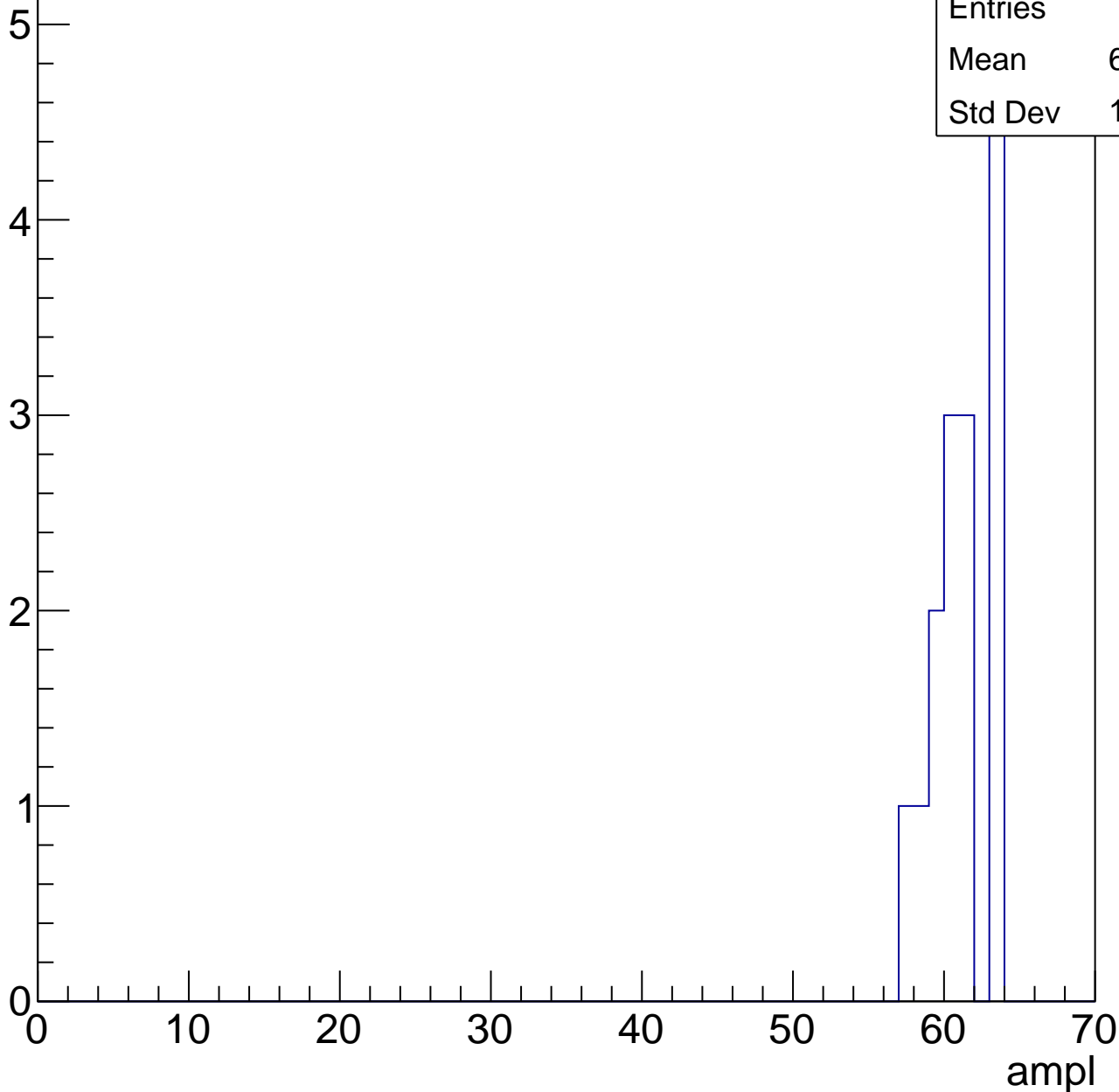


# B0L102S, U6-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	60.73
Std Dev	1.914





# B0L102S, U6-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U6-ch126, adc0

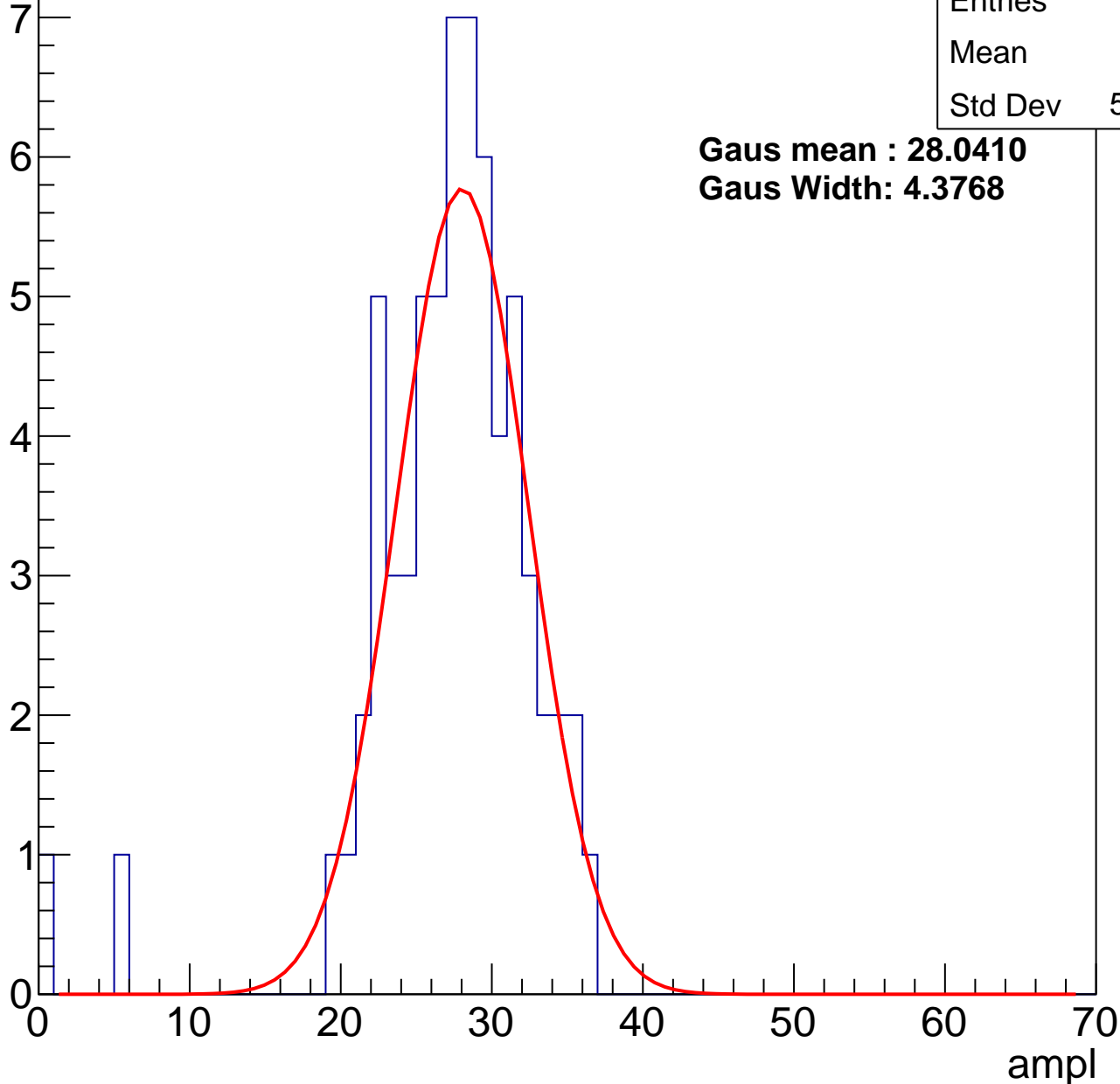
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	26.7
Std Dev	5.797

**Gaus mean : 28.0410**

**Gaus Width: 4.3768**



# B0L102S, U6-ch126, adc1

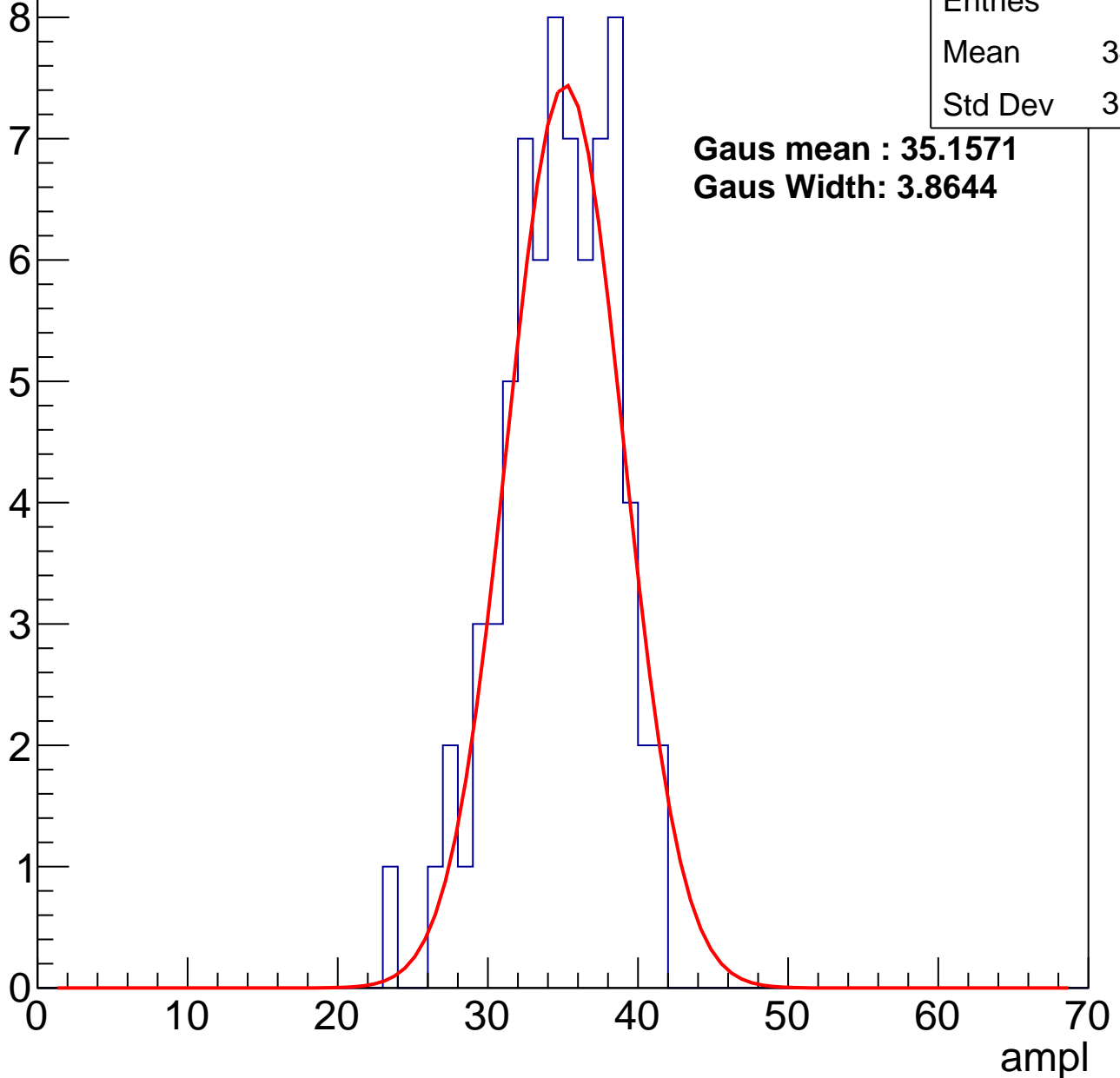
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	34.23
Std Dev	3.718

**Gaus mean : 35.1571**

**Gaus Width: 3.8644**



# B0L102S, U6-ch126, adc2

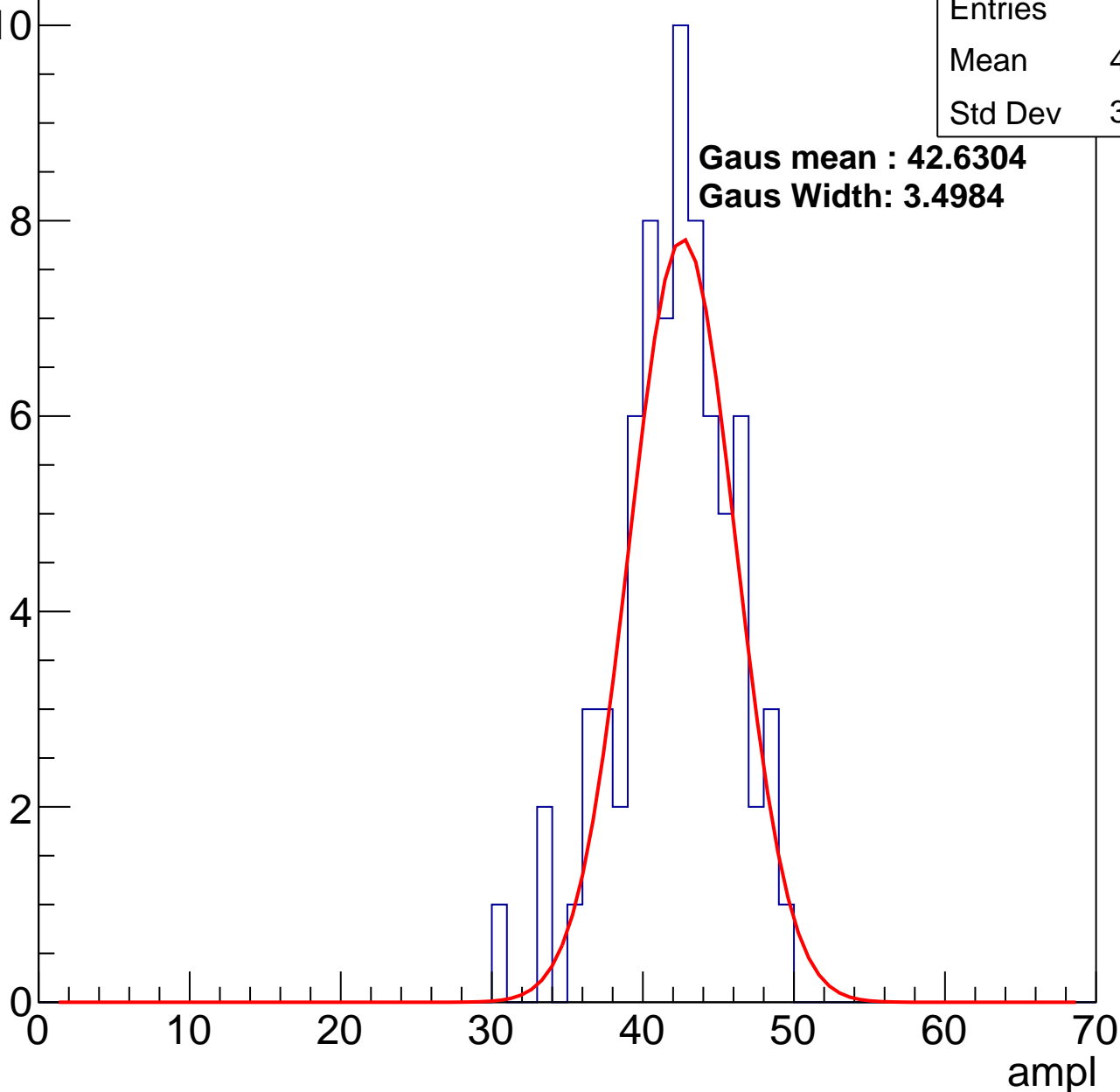
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	41.66
Std Dev	3.739

**Gaus mean : 42.6304**

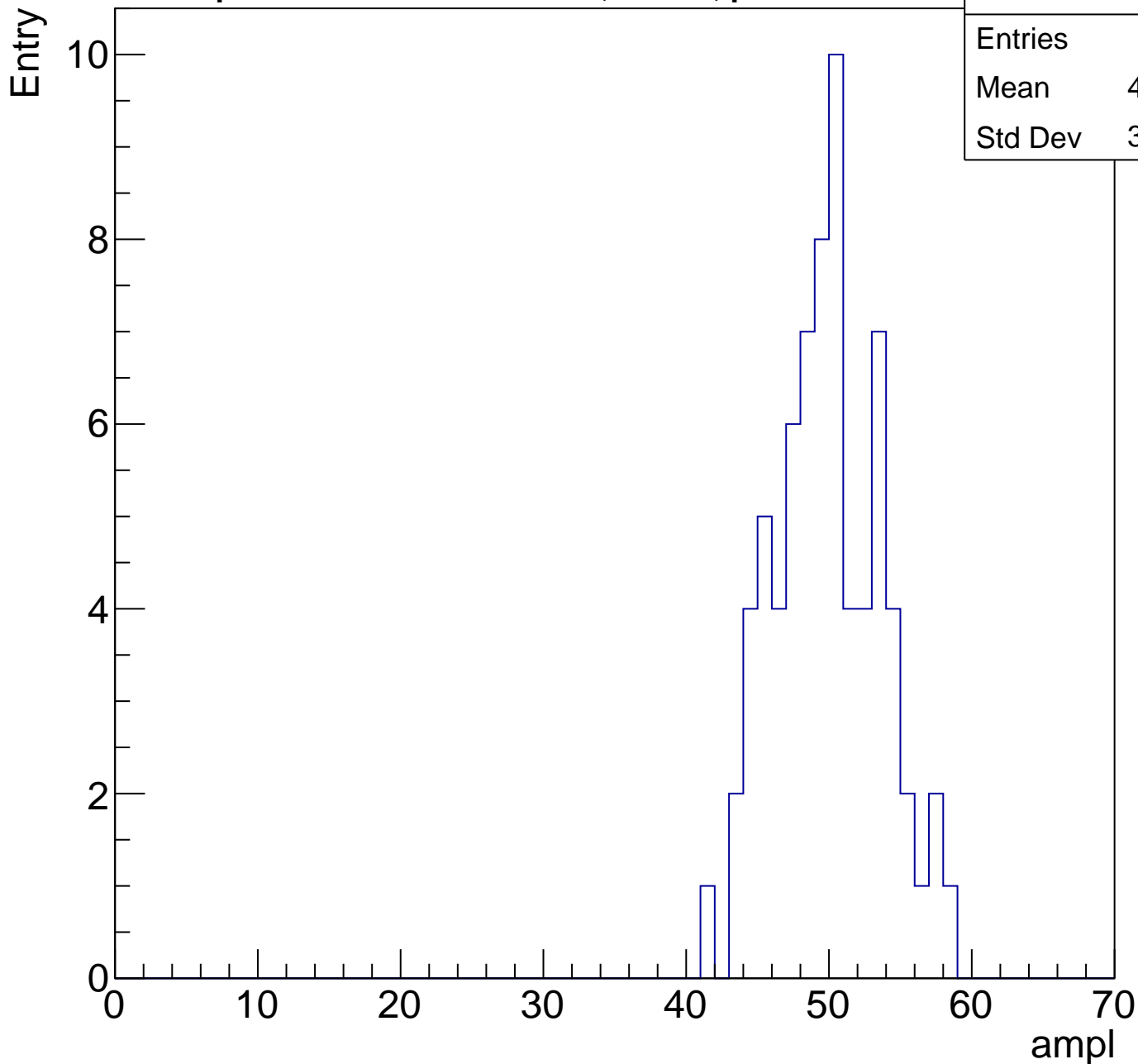
**Gaus Width: 3.4984**



# B0L102S, U6-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	72
Mean	49.43
Std Dev	3.677

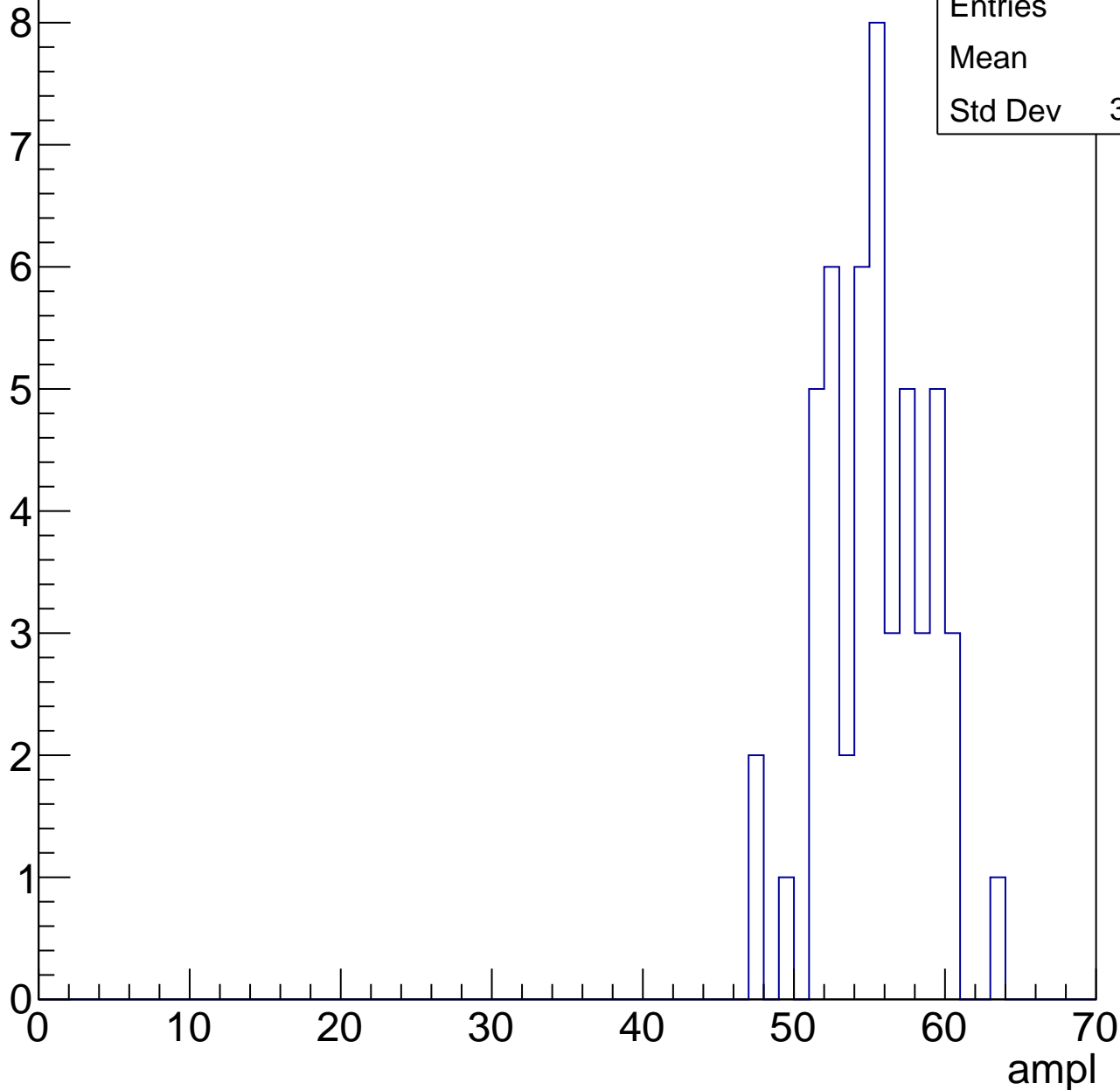


# B0L102S, U6-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	54.9
Std Dev	3.407

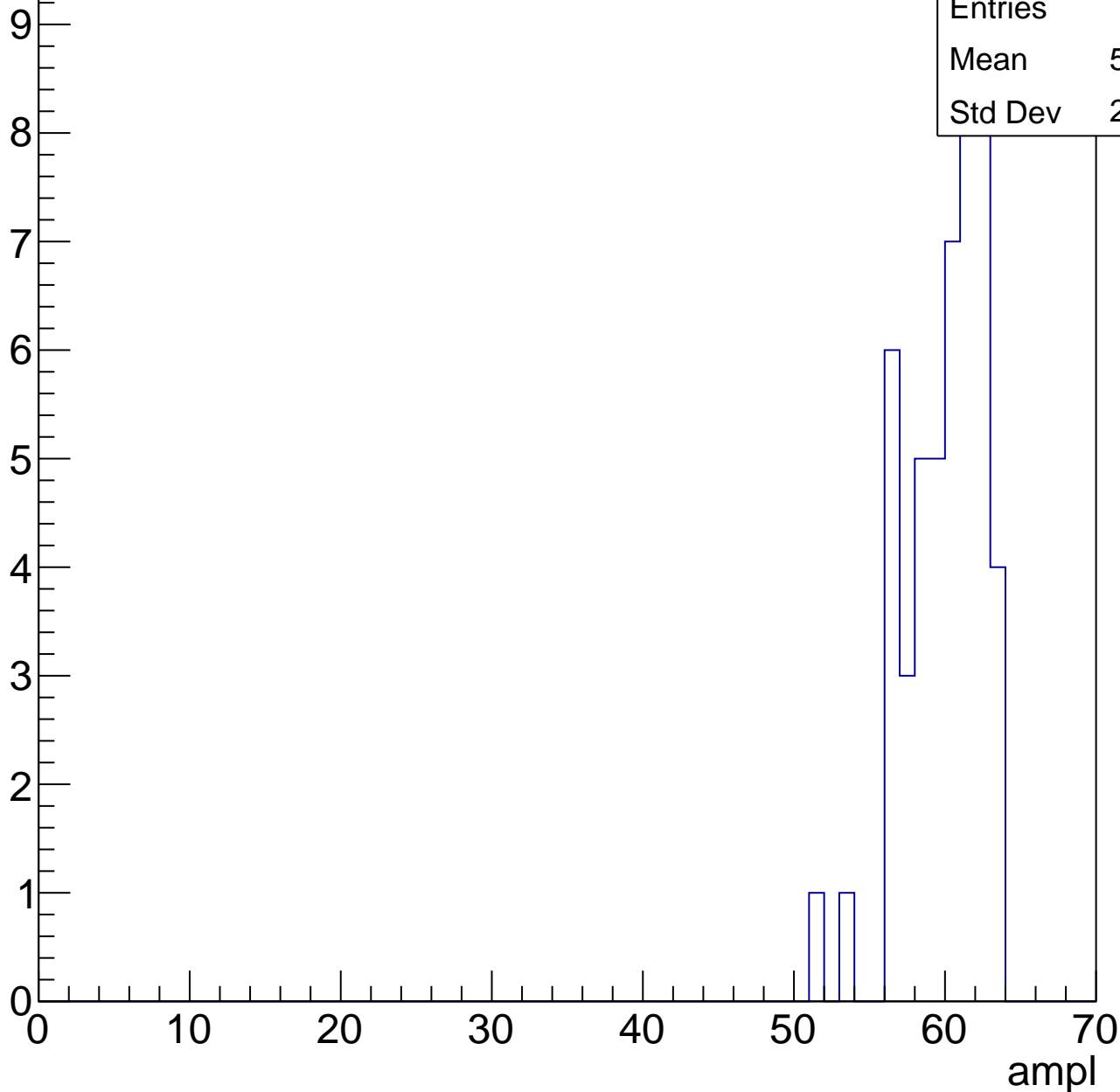


# B0L102S, U6-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

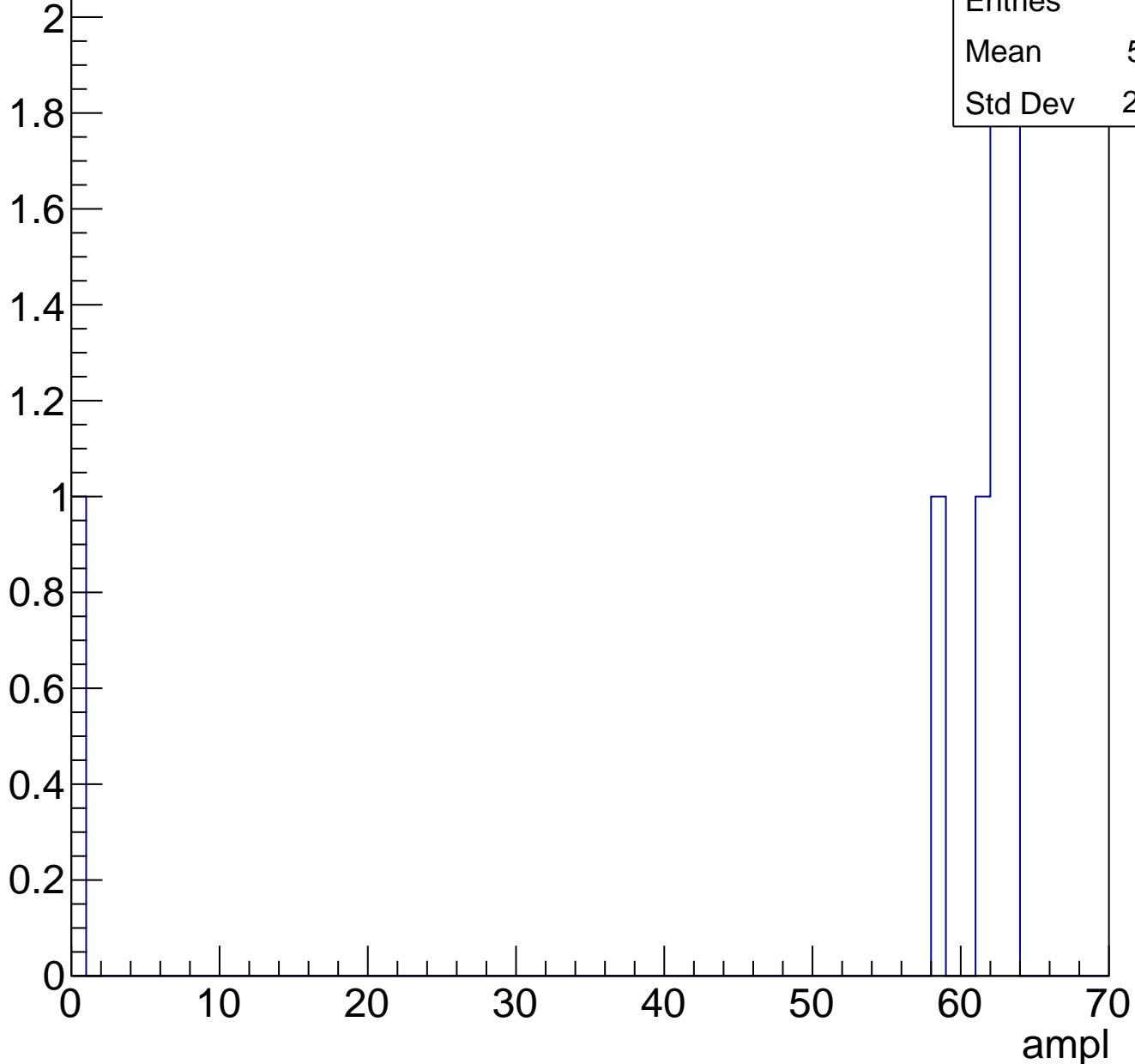
Entries	49
Mean	59.45
Std Dev	2.635



# B0L102S, U6-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

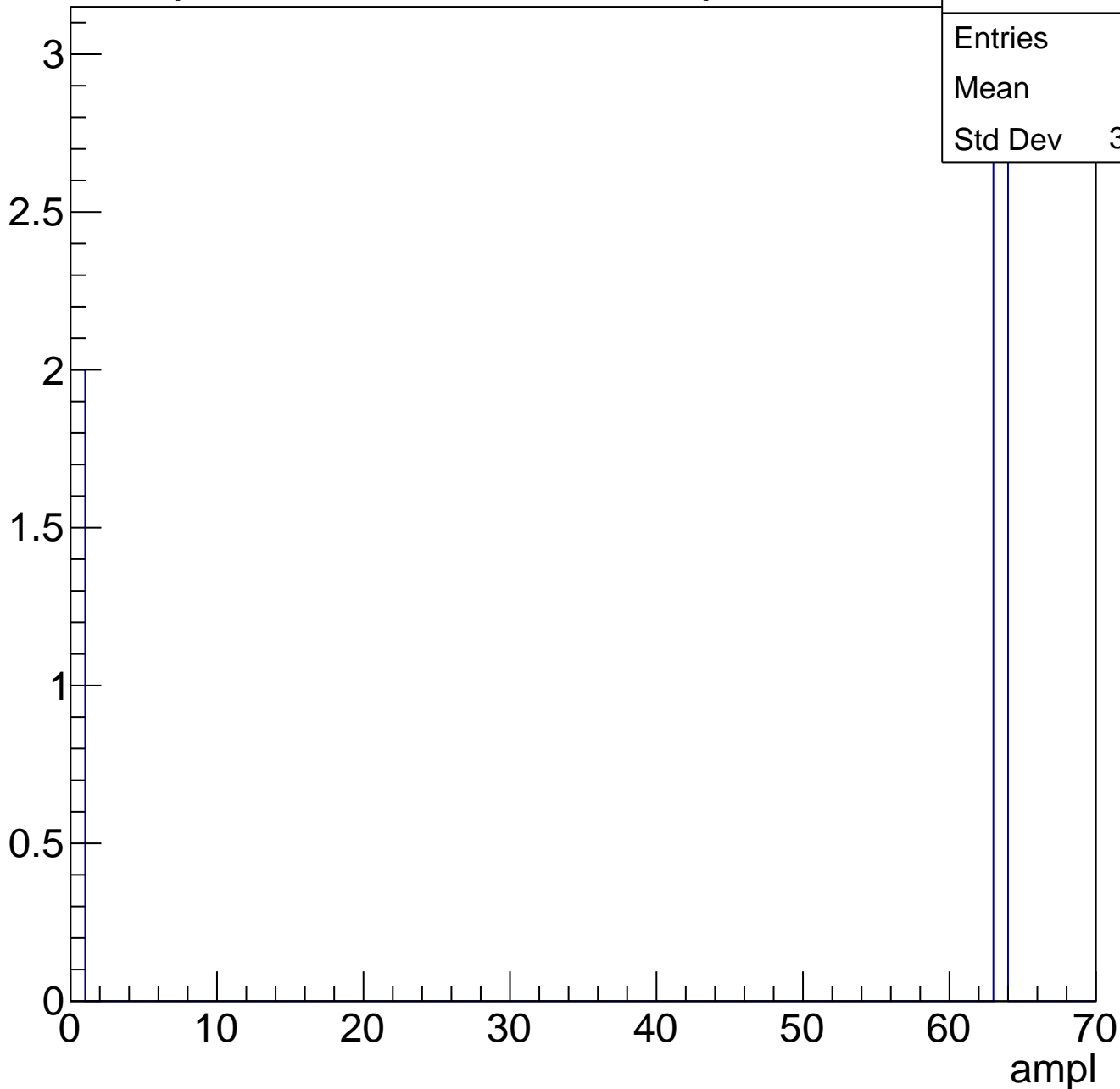




# B0L102S, U6-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U6-ch127, adc0

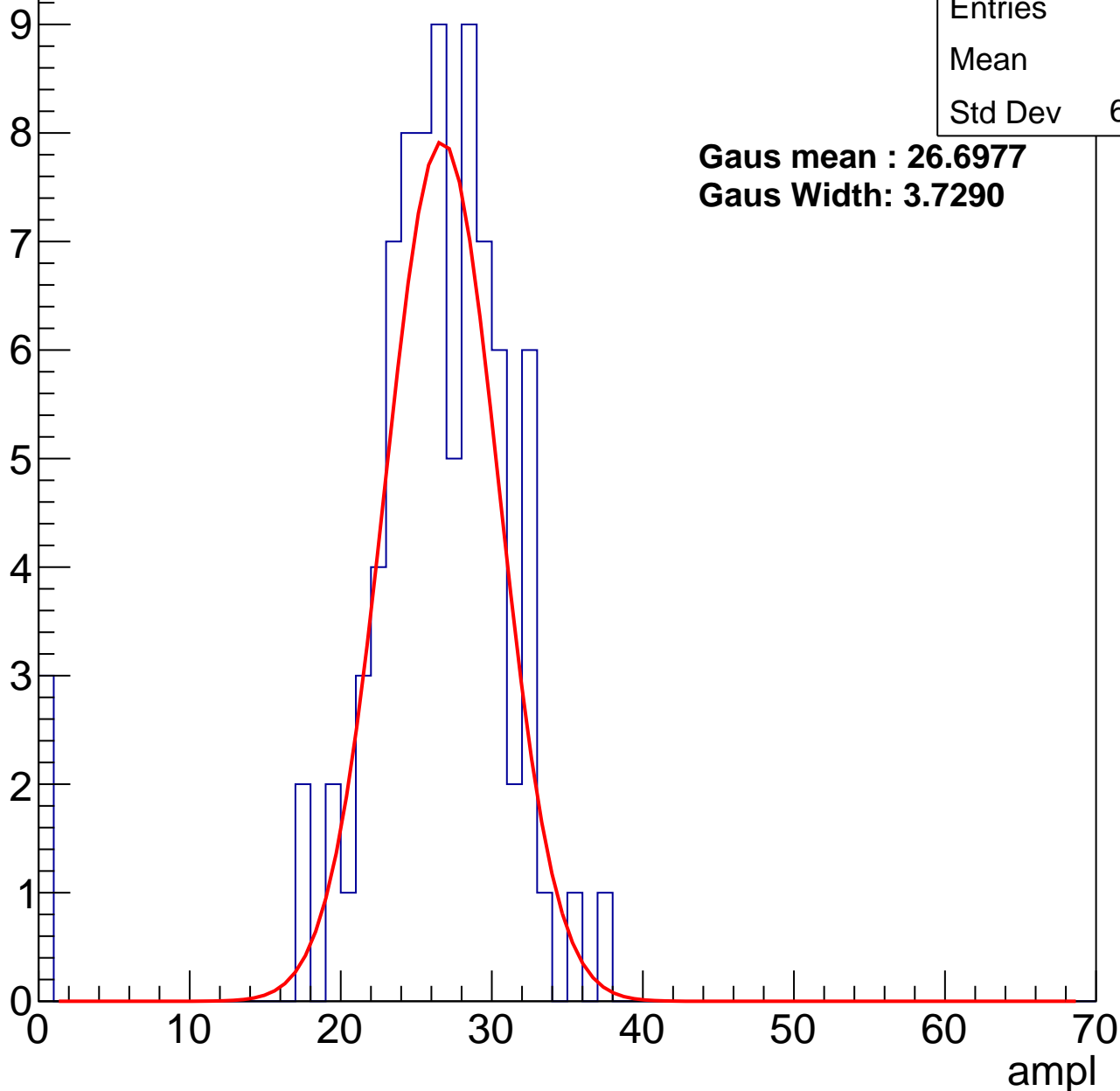
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	25.4
Std Dev	6.186

**Gaus mean : 26.6977**

**Gaus Width: 3.7290**



# B0L102S, U6-ch127, adc1

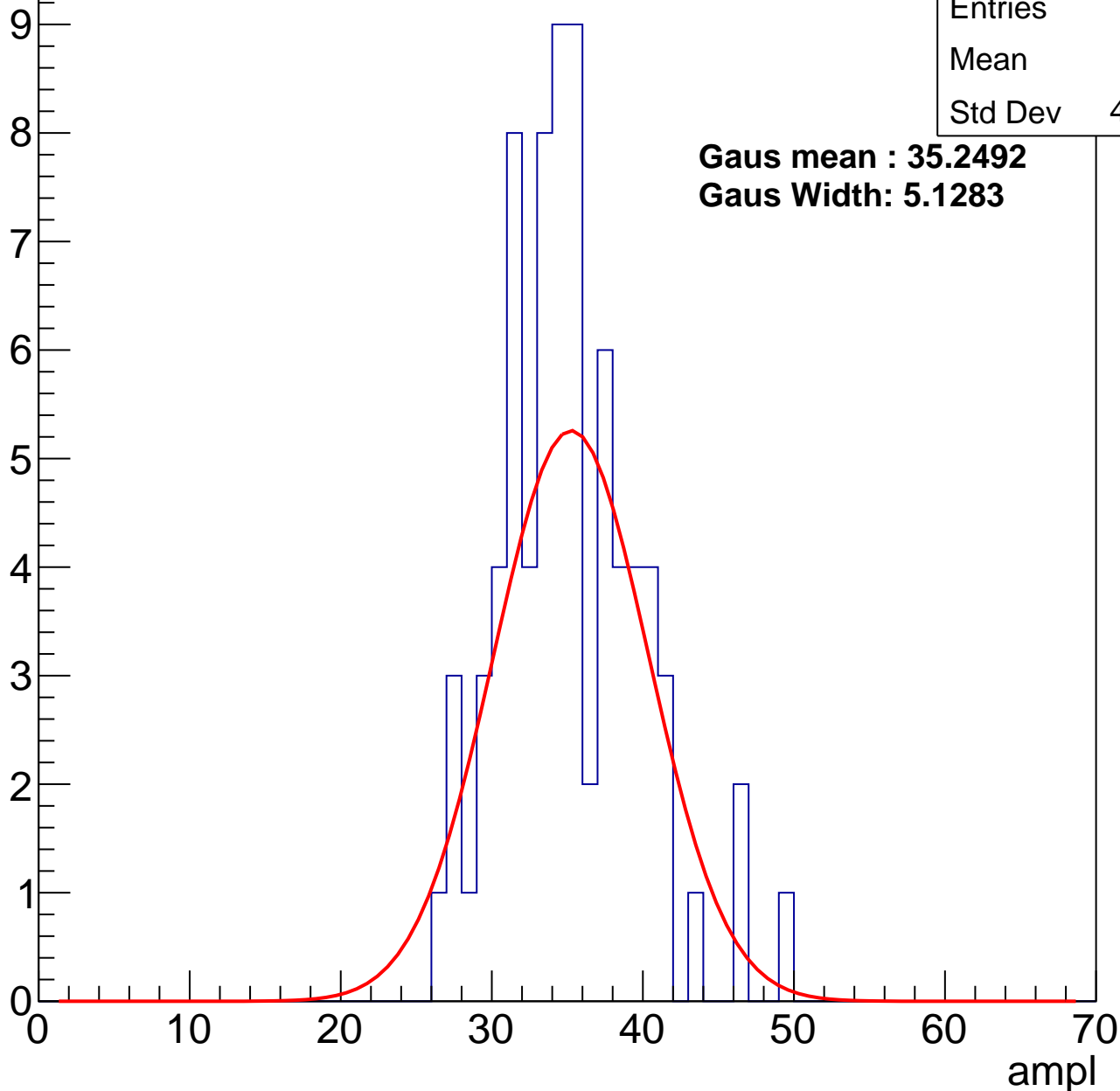
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	34.7
Std Dev	4.507

**Gaus mean : 35.2492**

**Gaus Width: 5.1283**



# B0L102S, U6-ch127, adc2

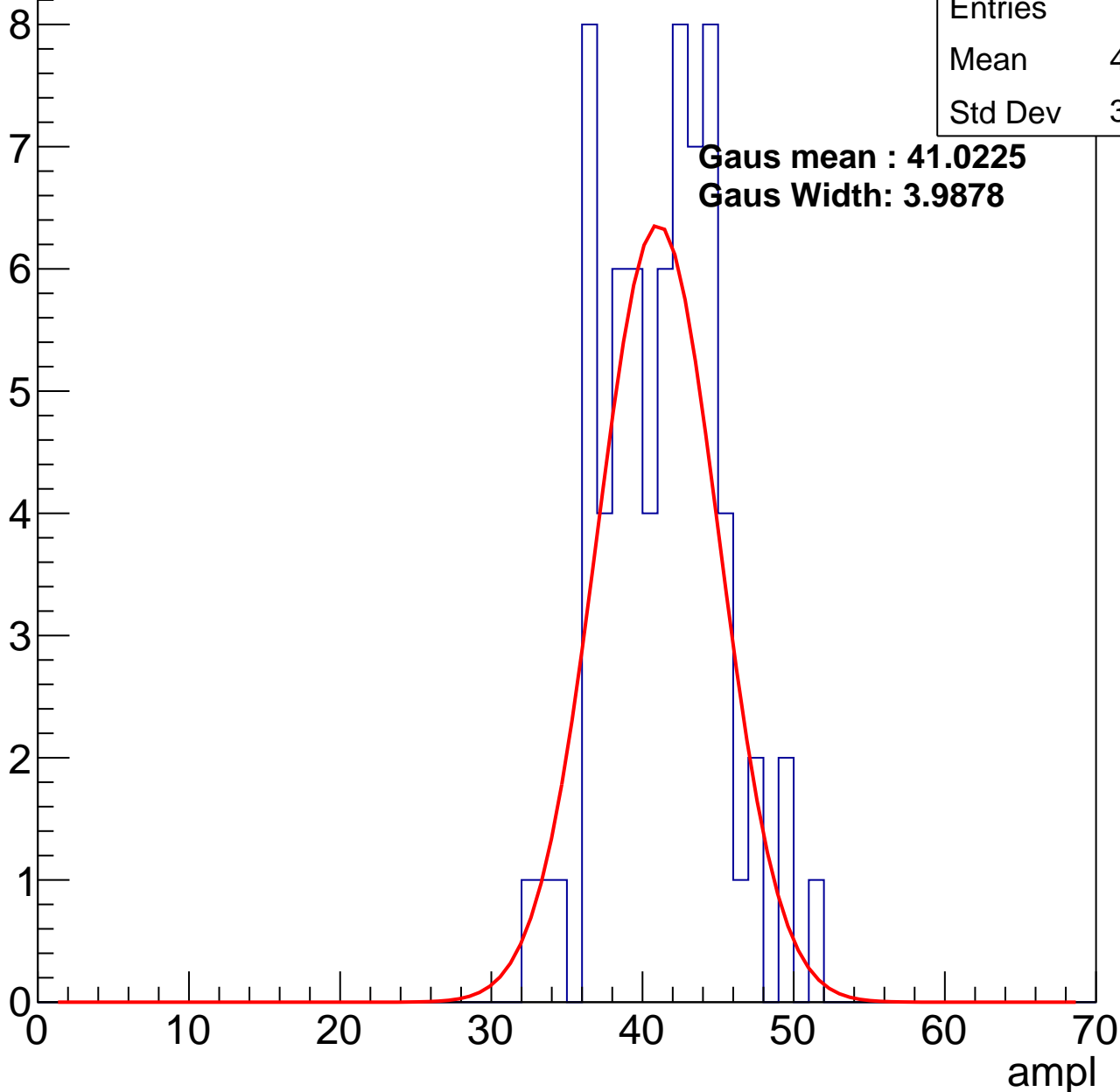
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	40.87
Std Dev	3.847

**Gaus mean : 41.0225**

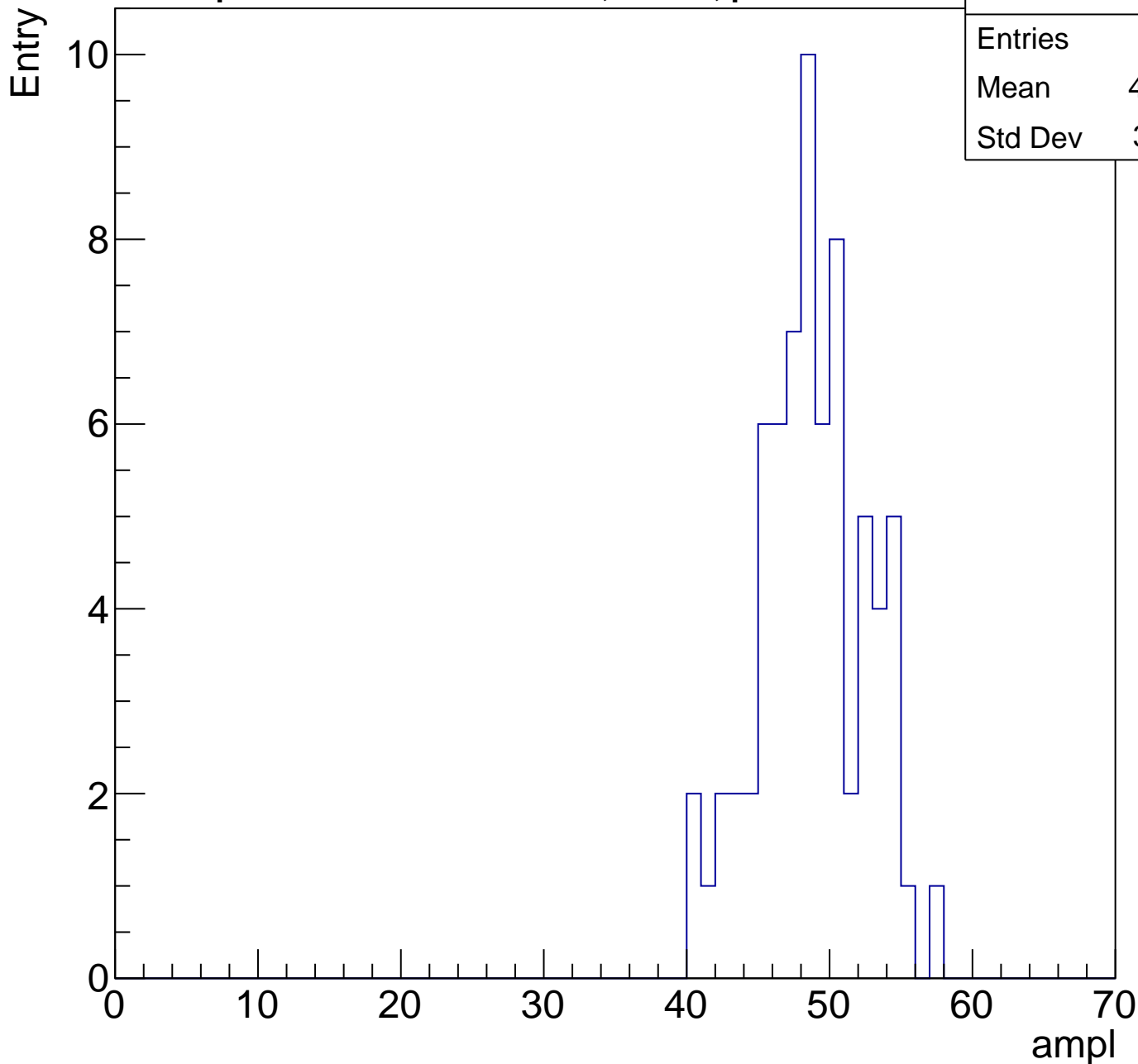
**Gaus Width: 3.9878**



# B0L102S, U6-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	48.34
Std Dev	3.691

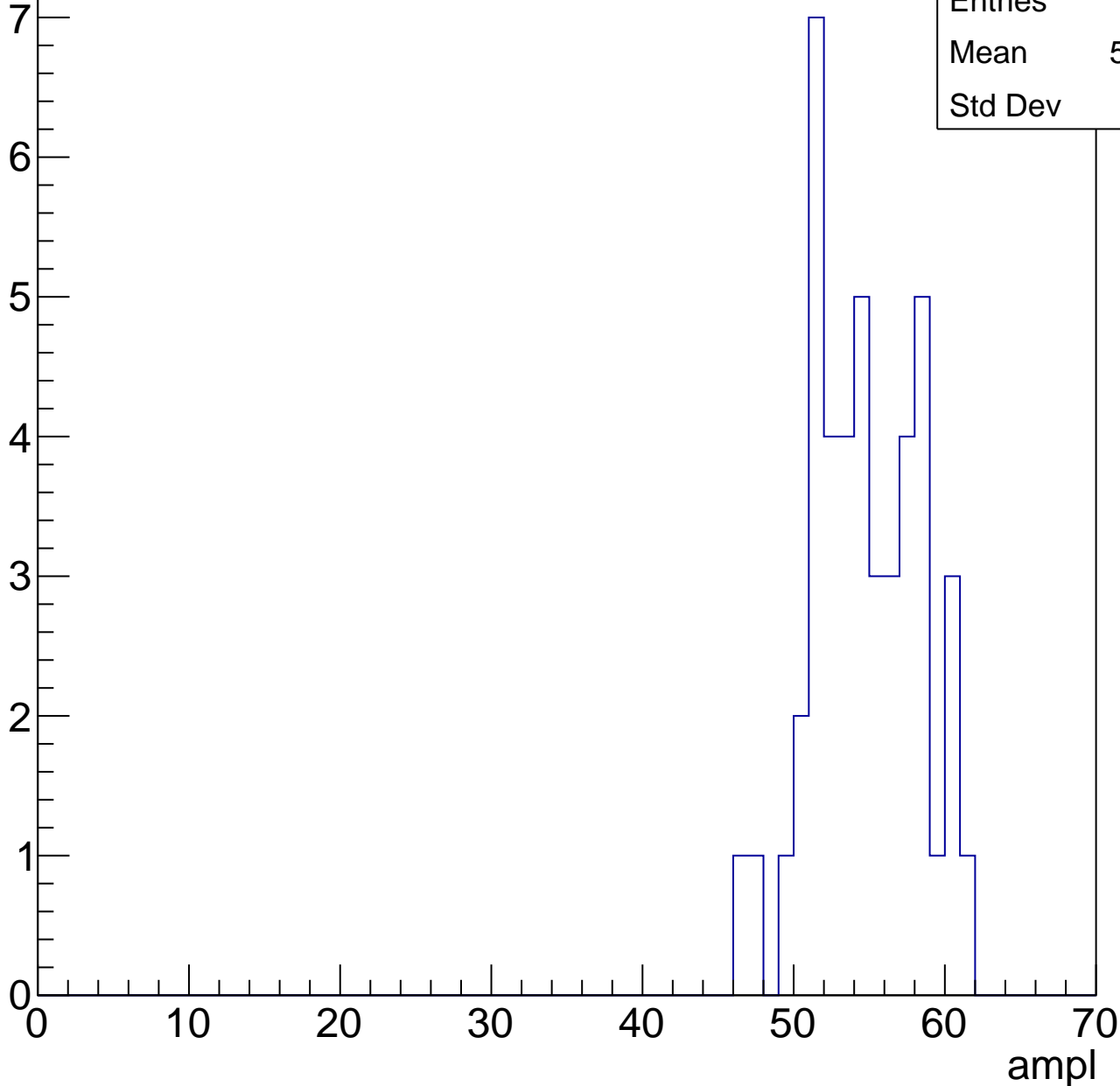


# B0L102S, U6-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	54.22
Std Dev	3.54

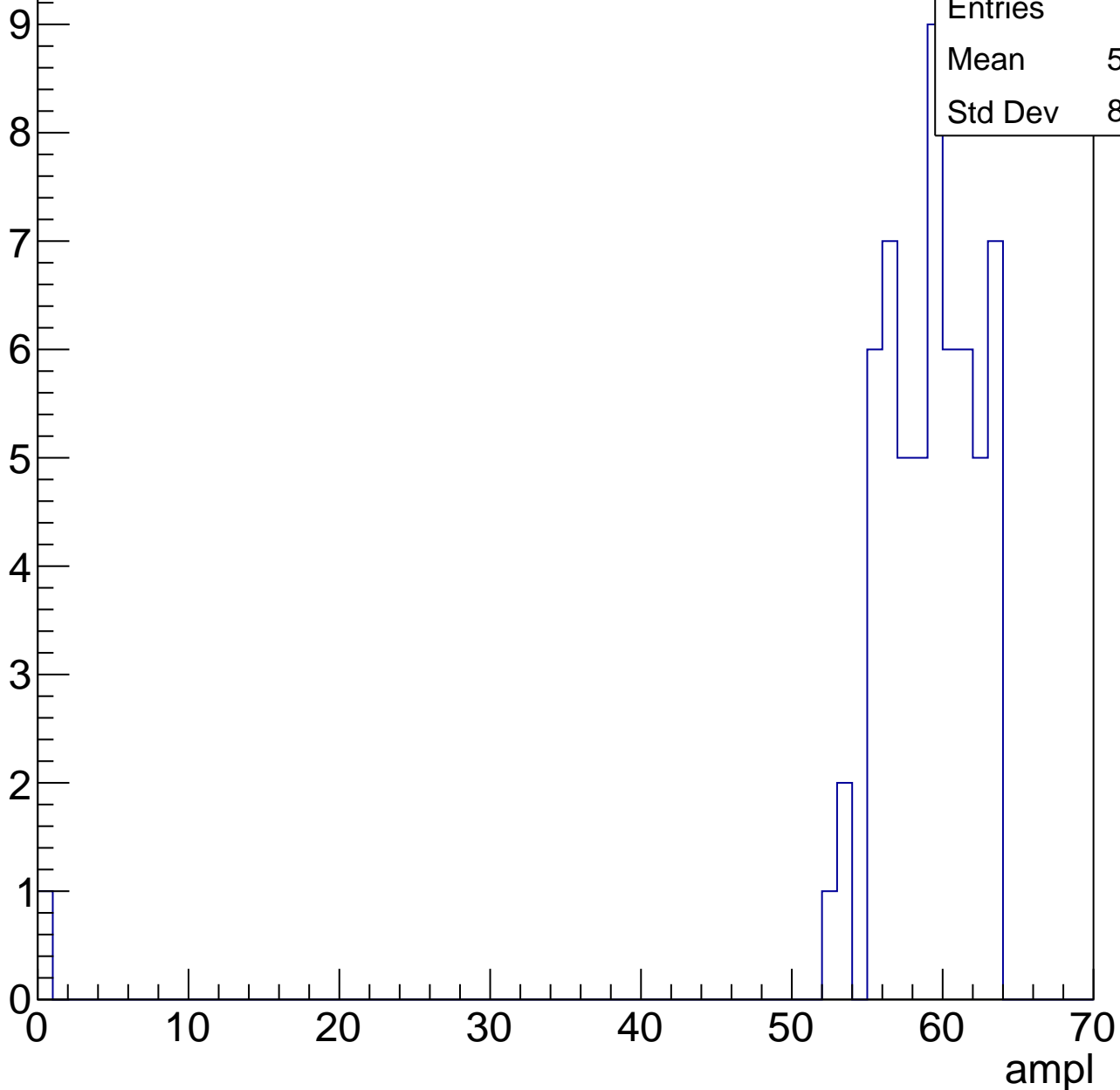


# B0L102S, U6-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

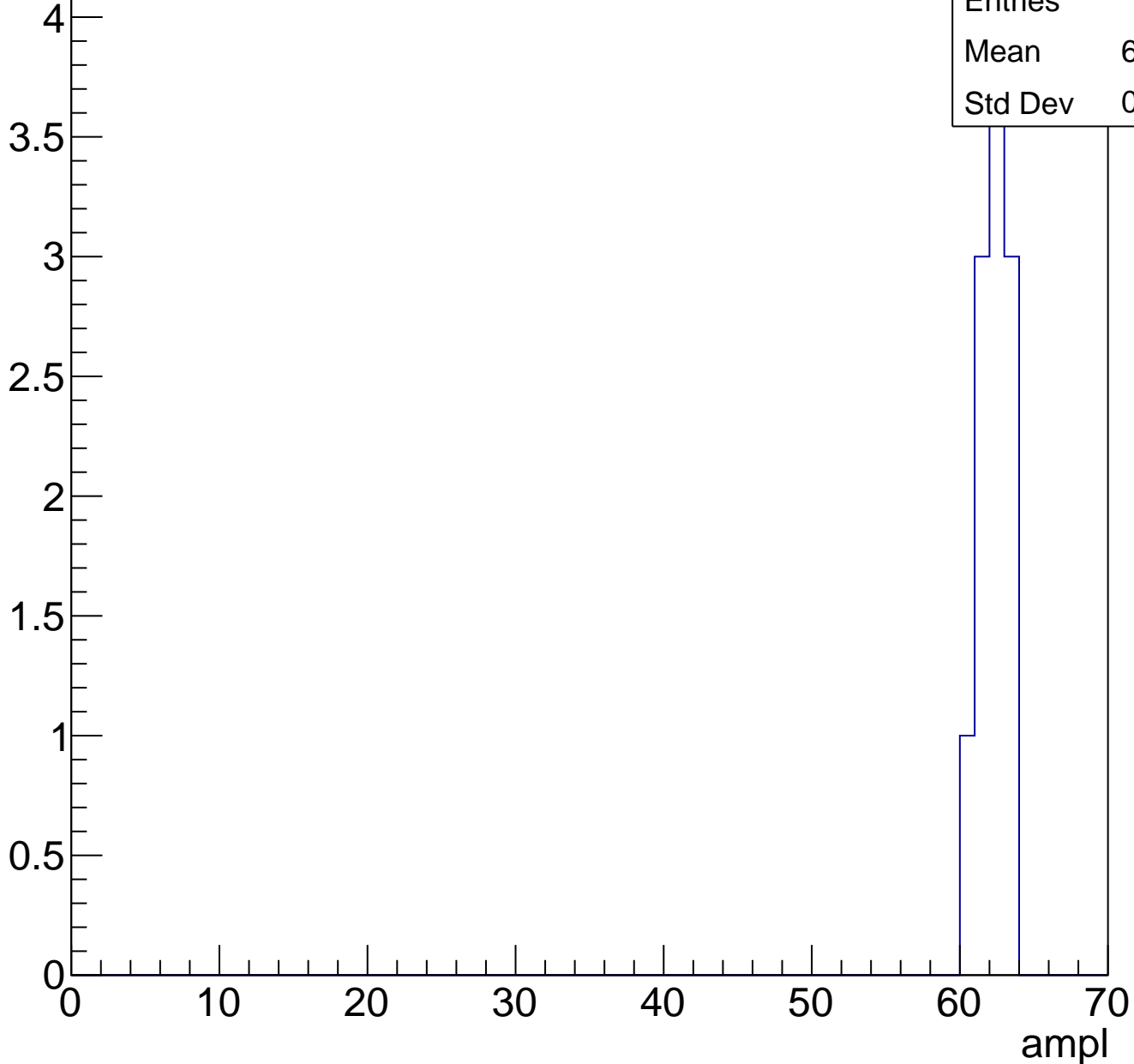
Entries	60
Mean	57.72
Std Dev	8.036



# B0L102S, U6-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

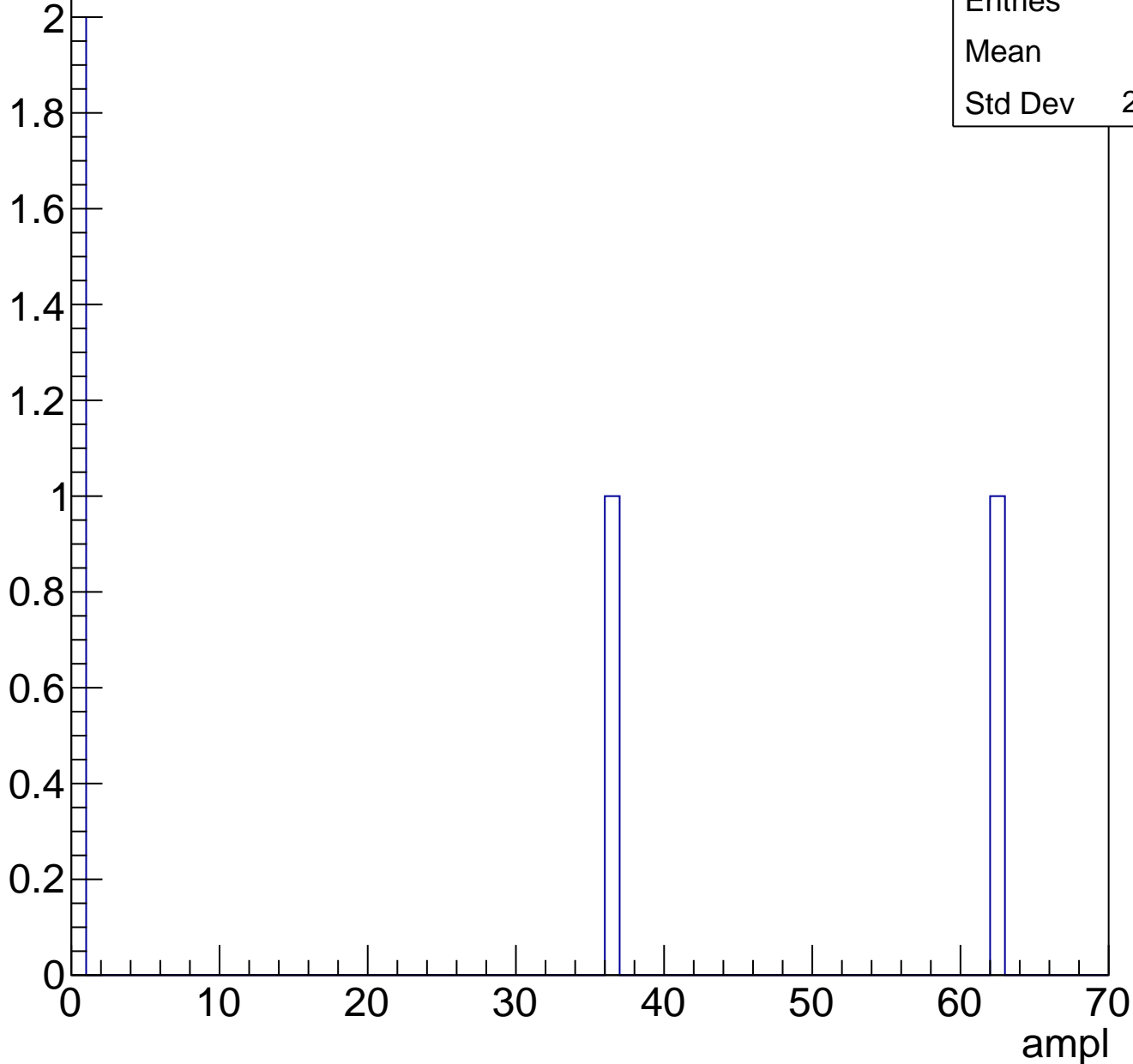




# B0L102S, U6-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

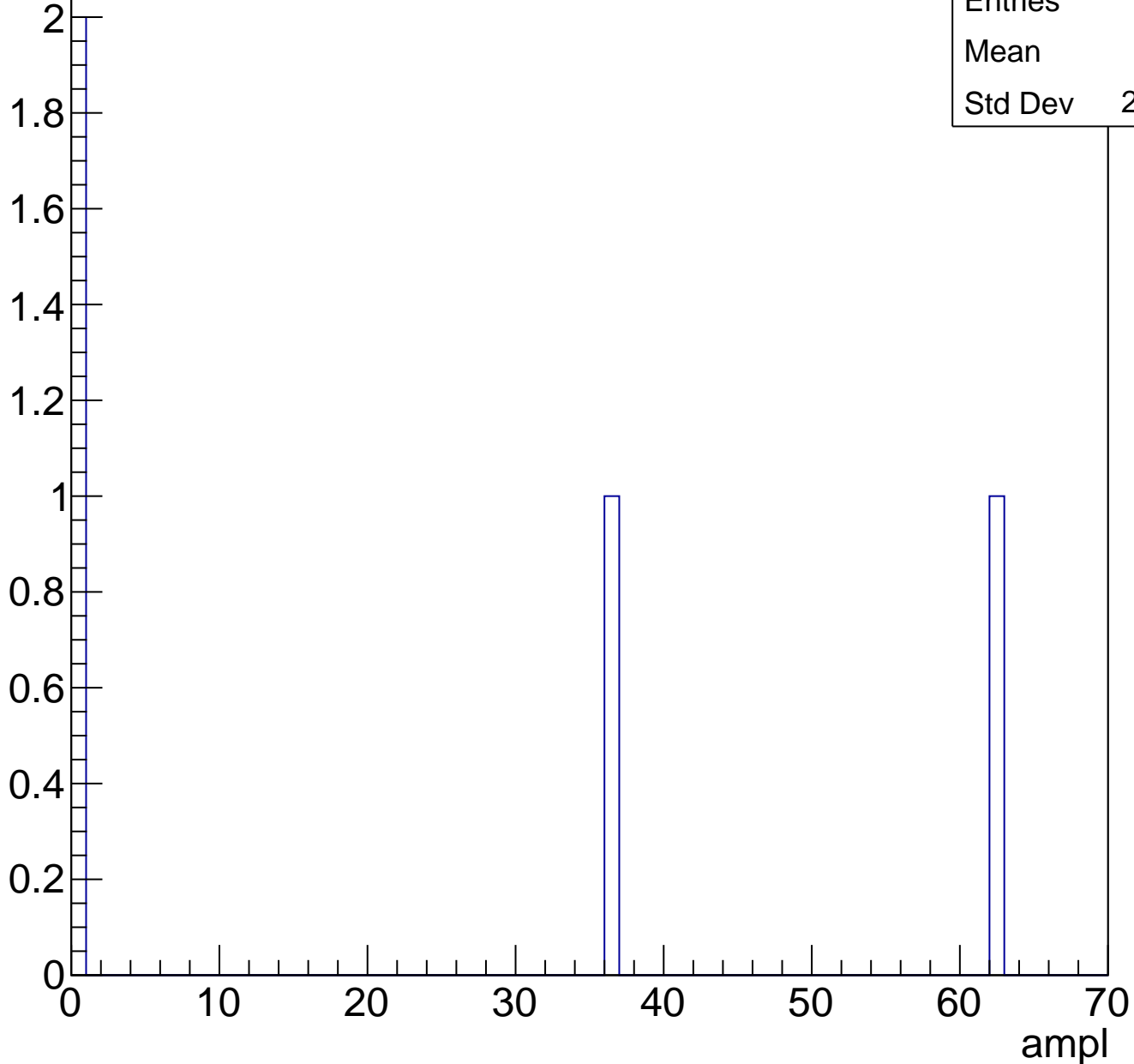


Entries	4
Mean	24.5
Std Dev	26.17

# B0L102S, U6-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	4
Mean	24.5
Std Dev	26.17