

B1L002S, U15-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.1069

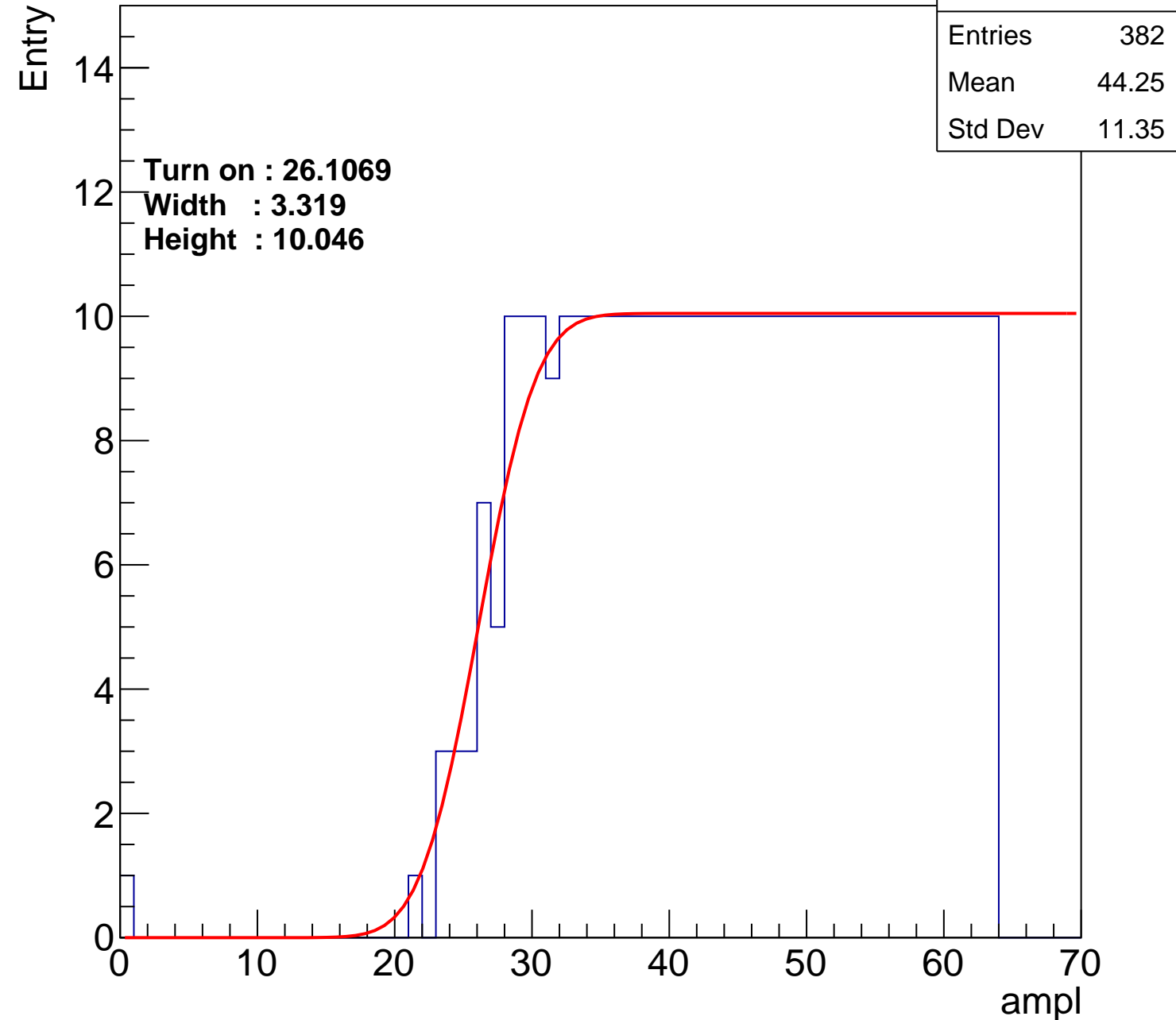
Width : 3.319

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.21
Std Dev	11.87

Turn on : 23.8948

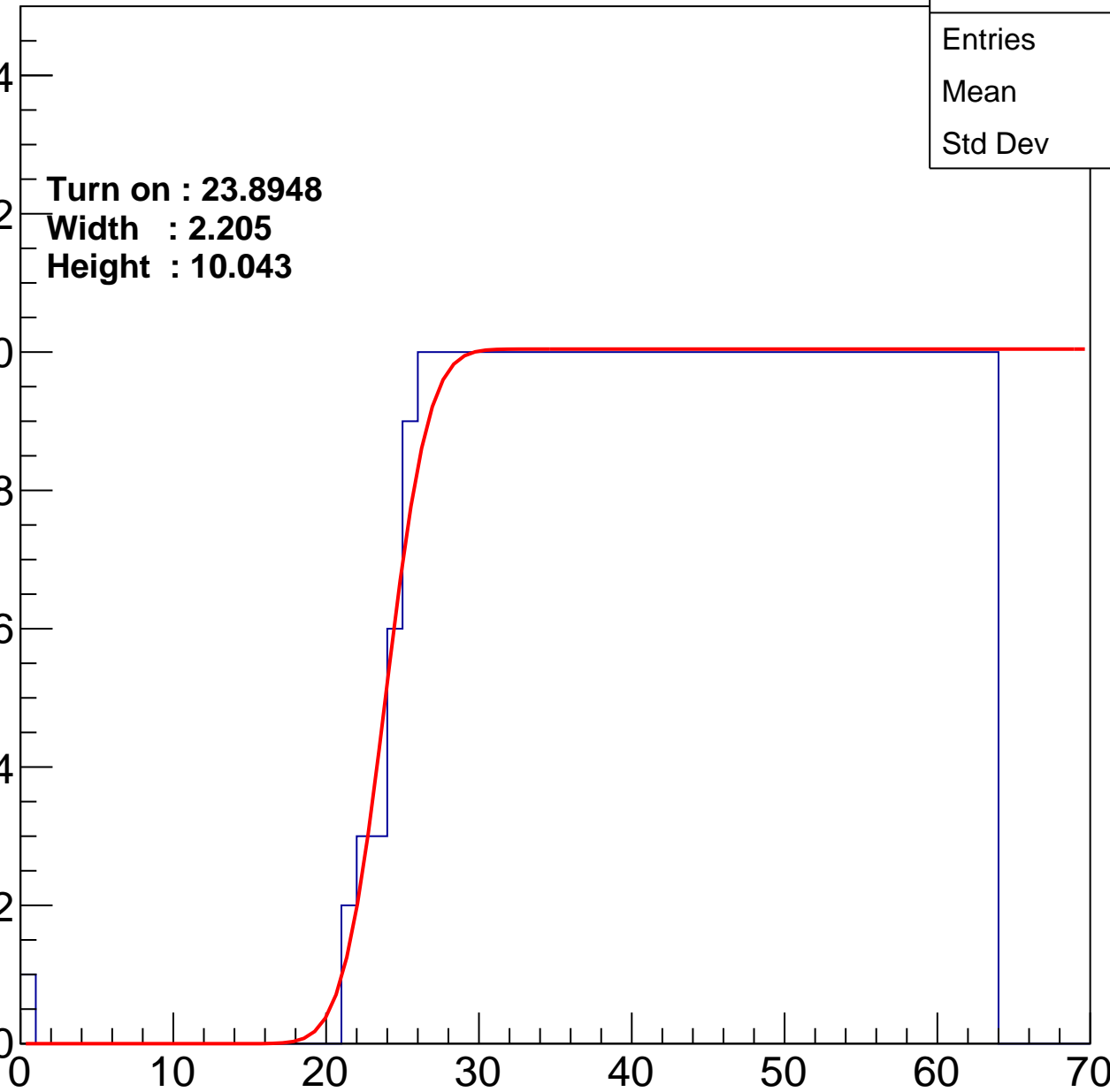
Width : 2.205

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.64
Std Dev	11.14

Turn on : 26.7029

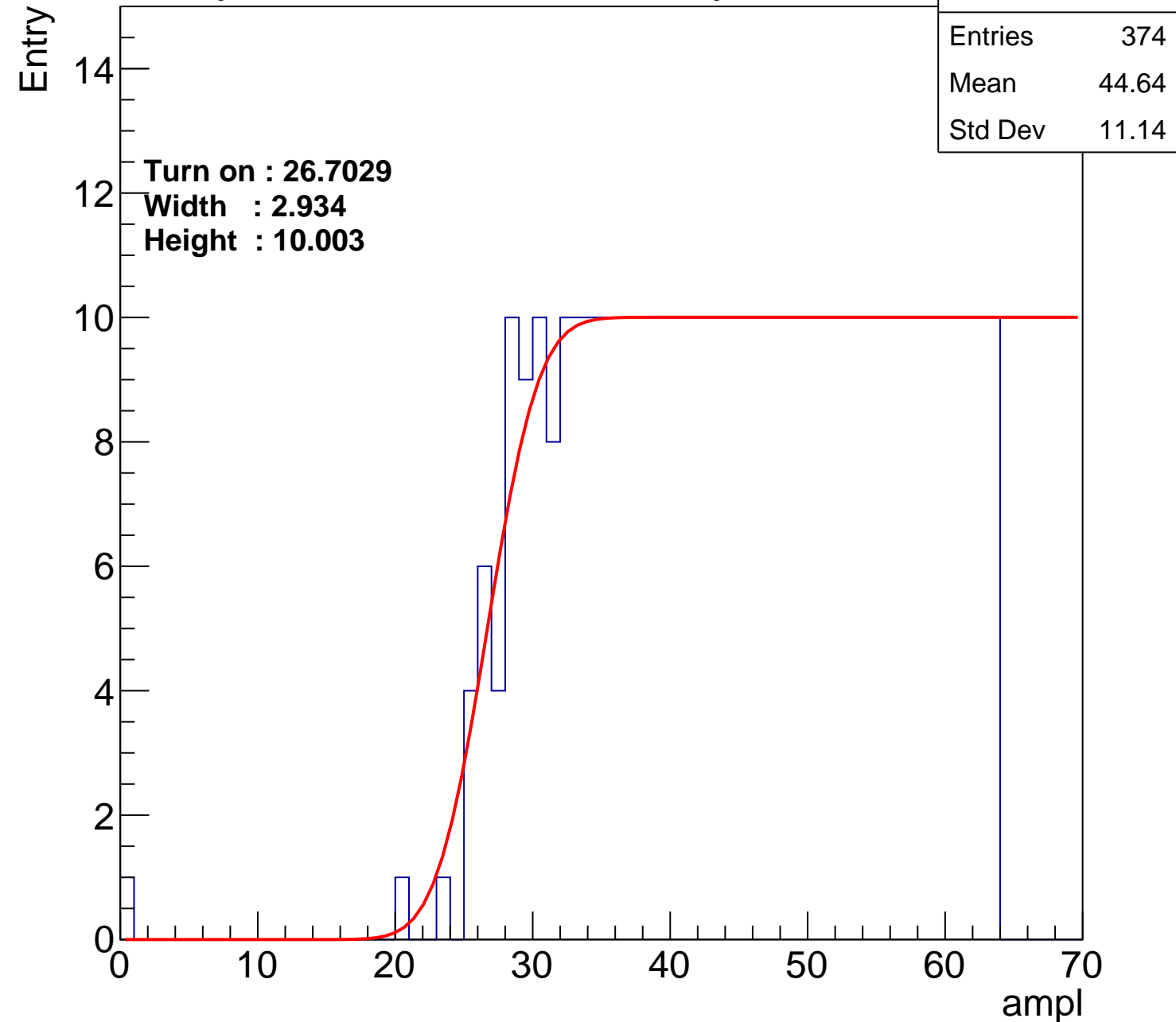
Width : 2.934

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.87
Std Dev	11.04

Turn on : 27.8095

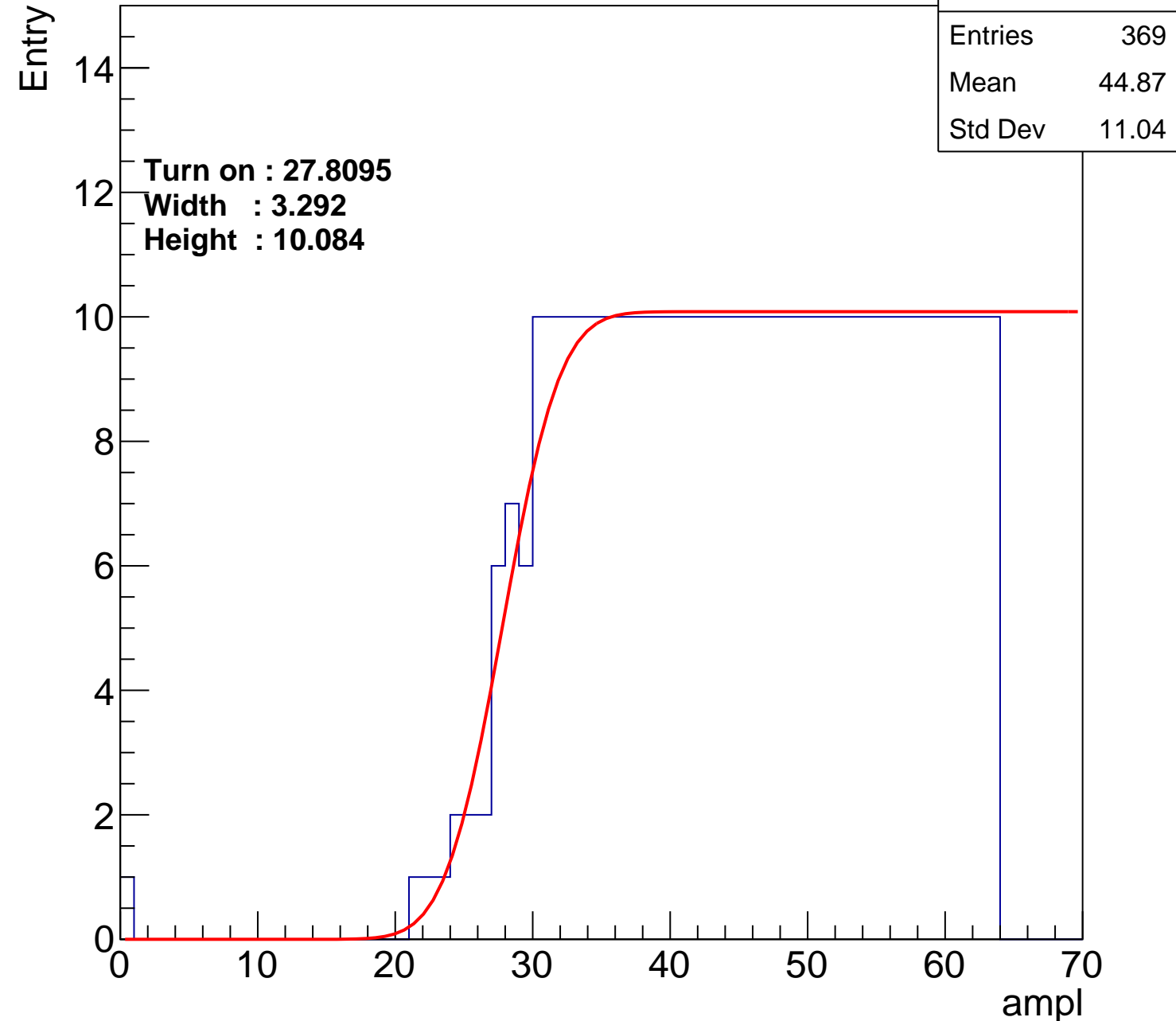
Width : 3.292

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.74
Std Dev	11.64

Turn on : 25.0441

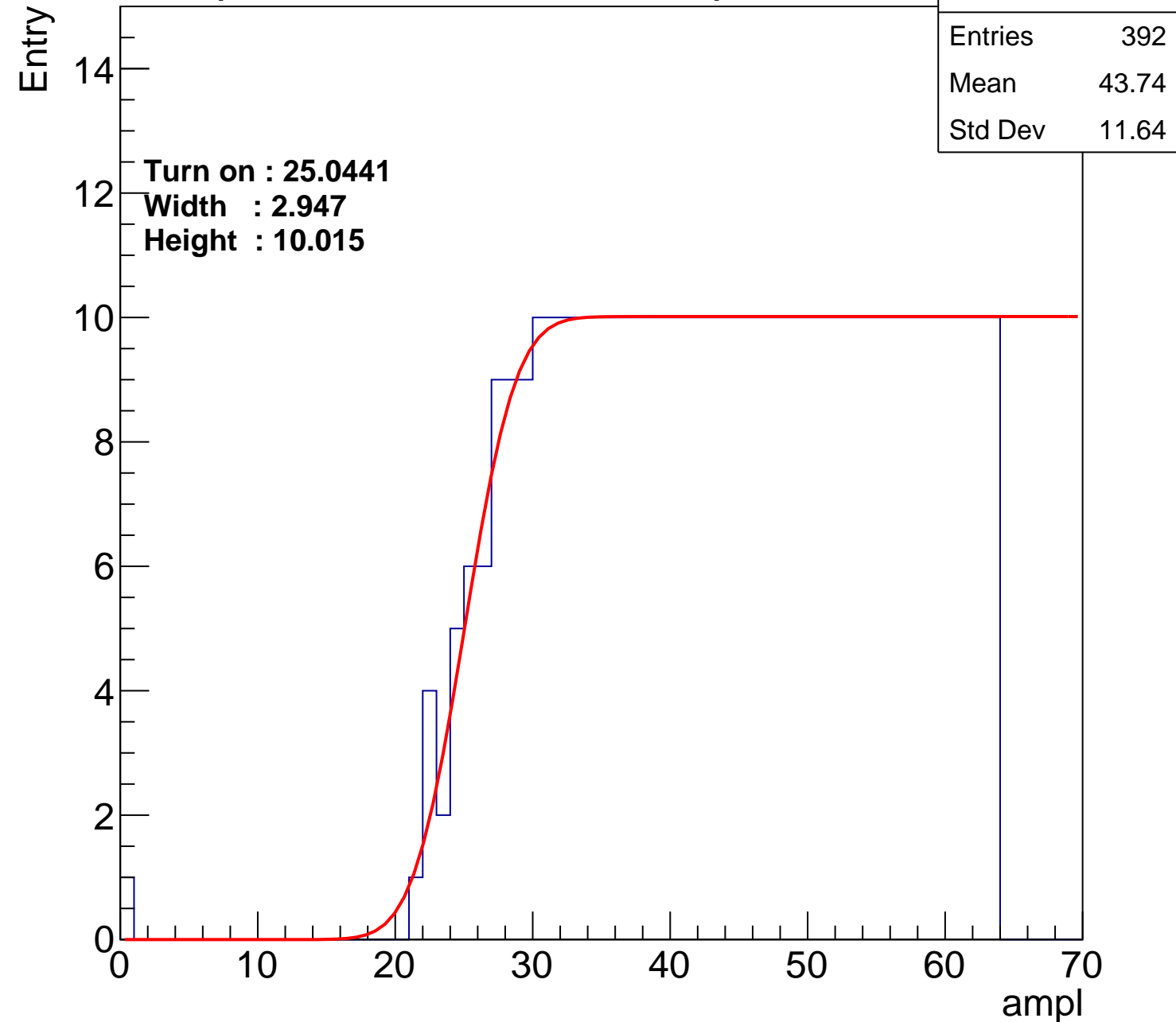
Width : 2.947

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.25
Std Dev	11.49

Turn on : 26.2295

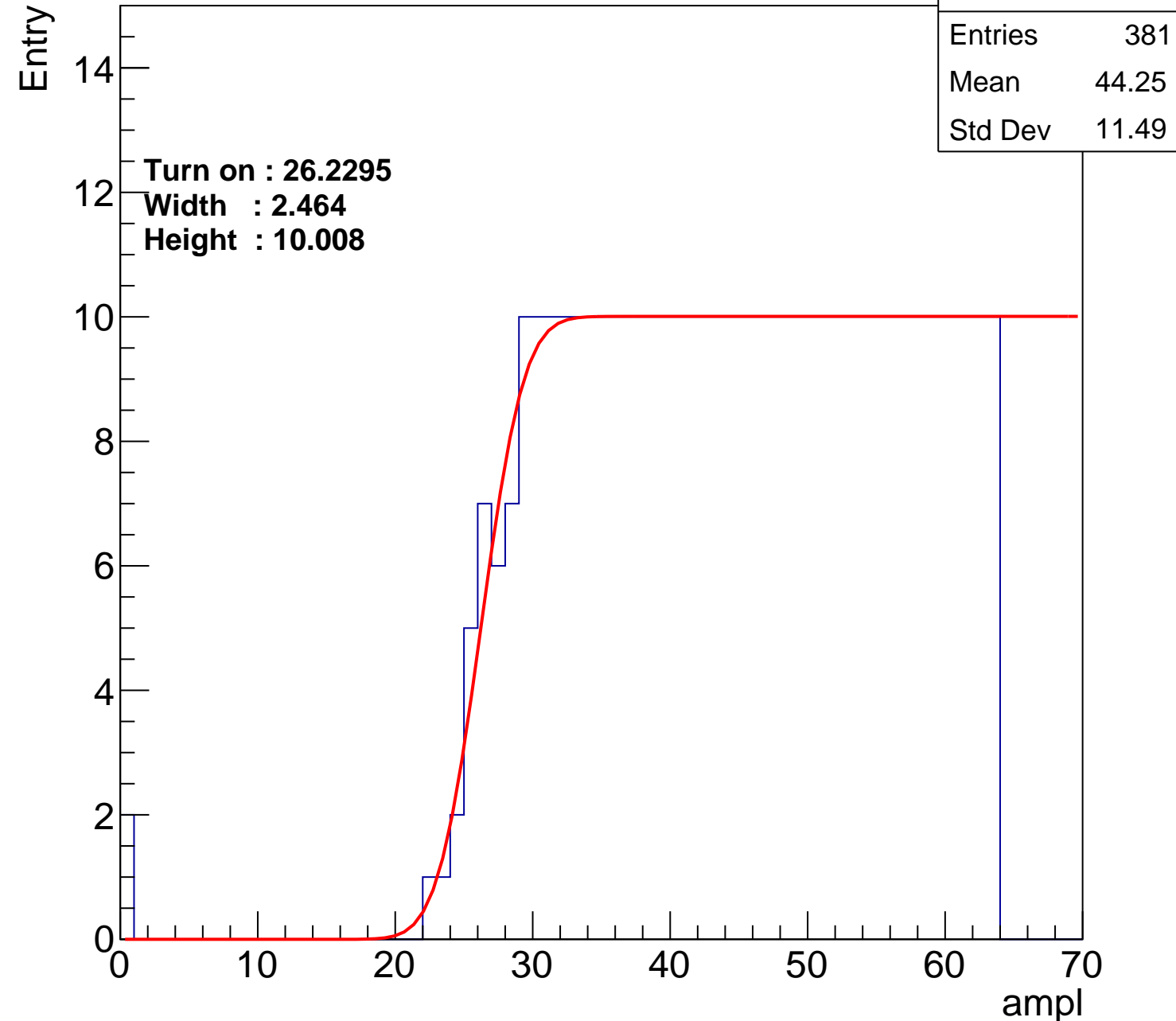
Width : 2.464

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.54
Std Dev	12.16

Turn on : 25.9072

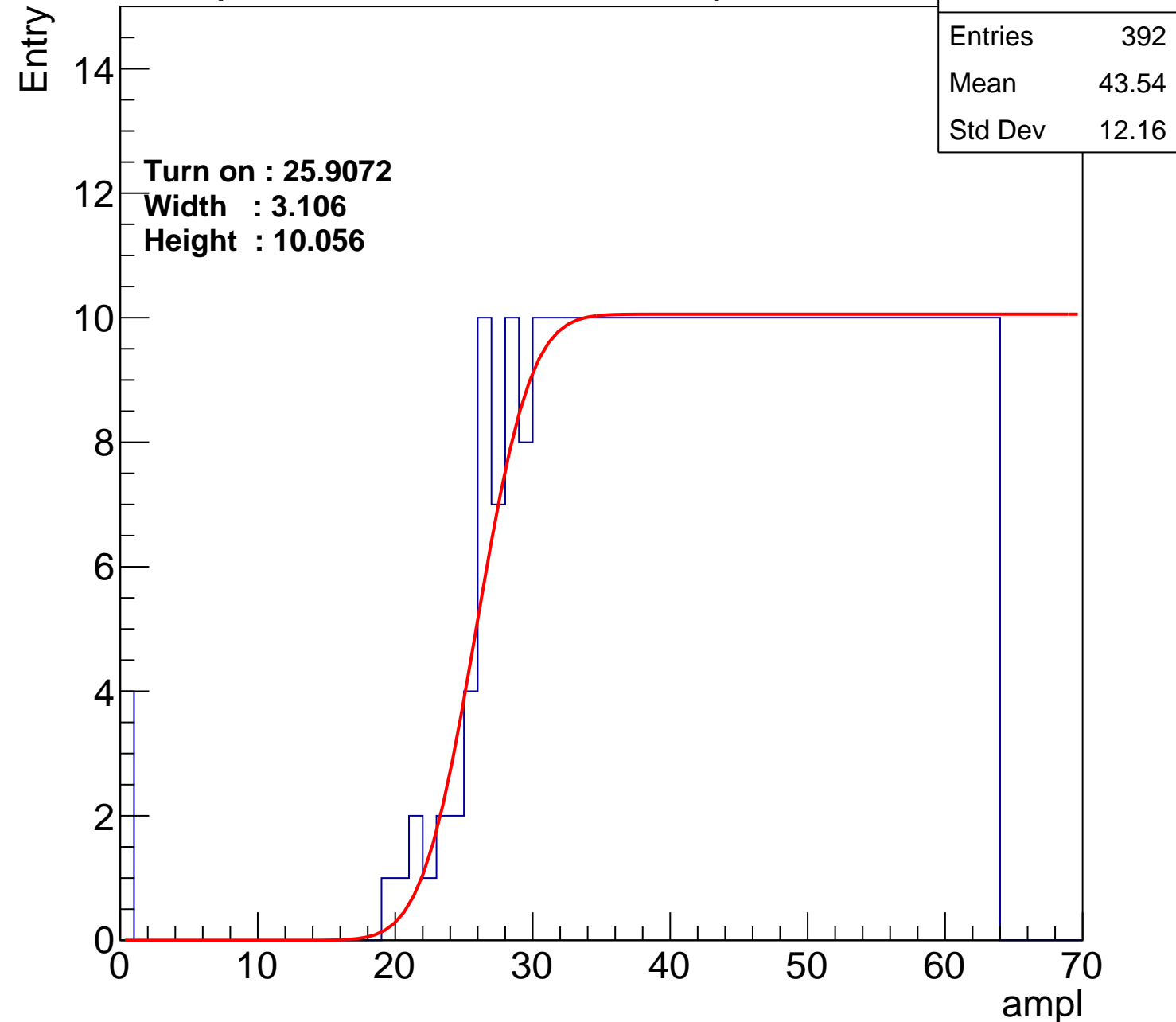
Width : 3.106

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.96
Std Dev	12.33

Turn on : 24.1637

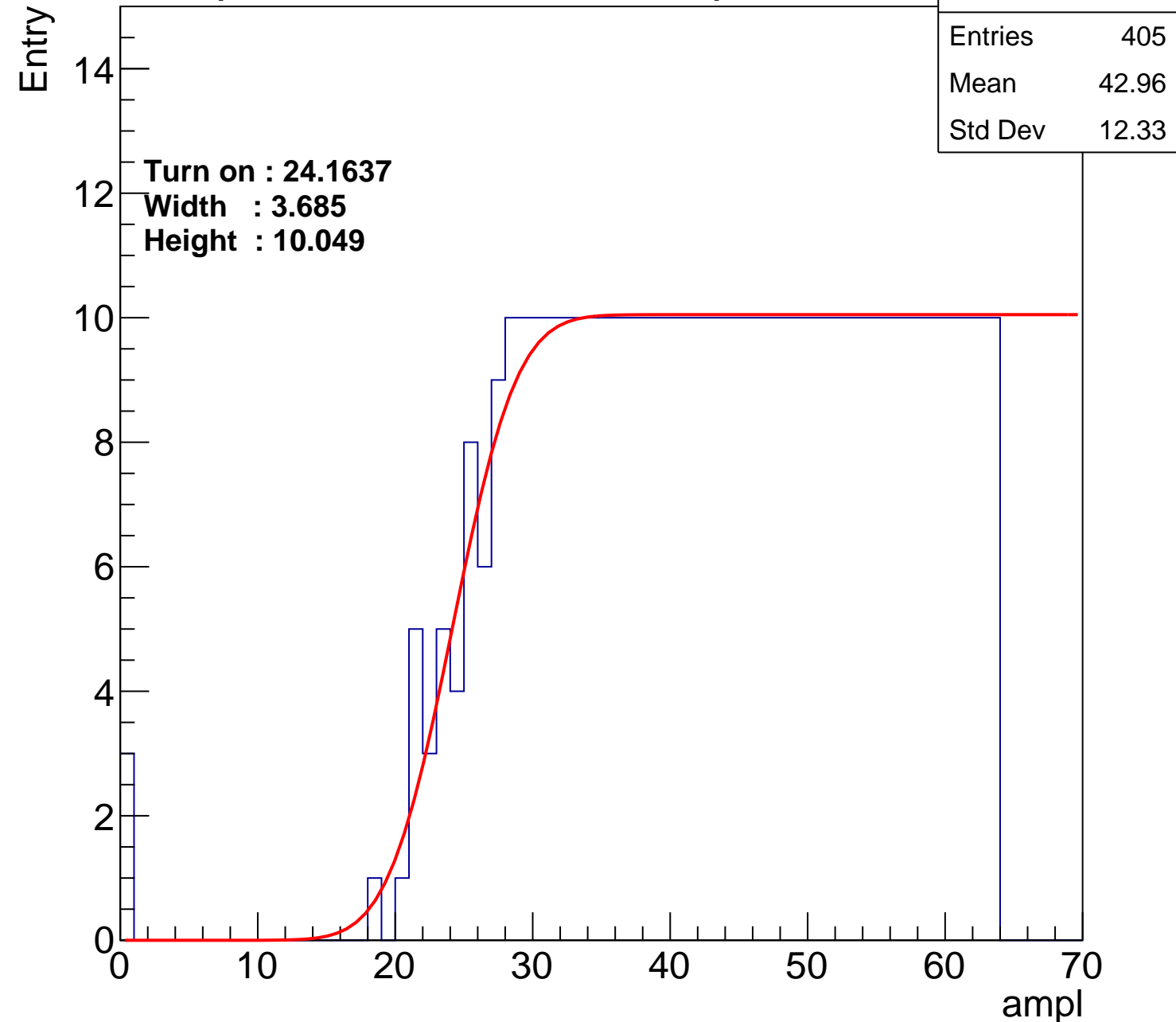
Width : 3.685

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.56
Std Dev	11.84

Turn on : 24.7642

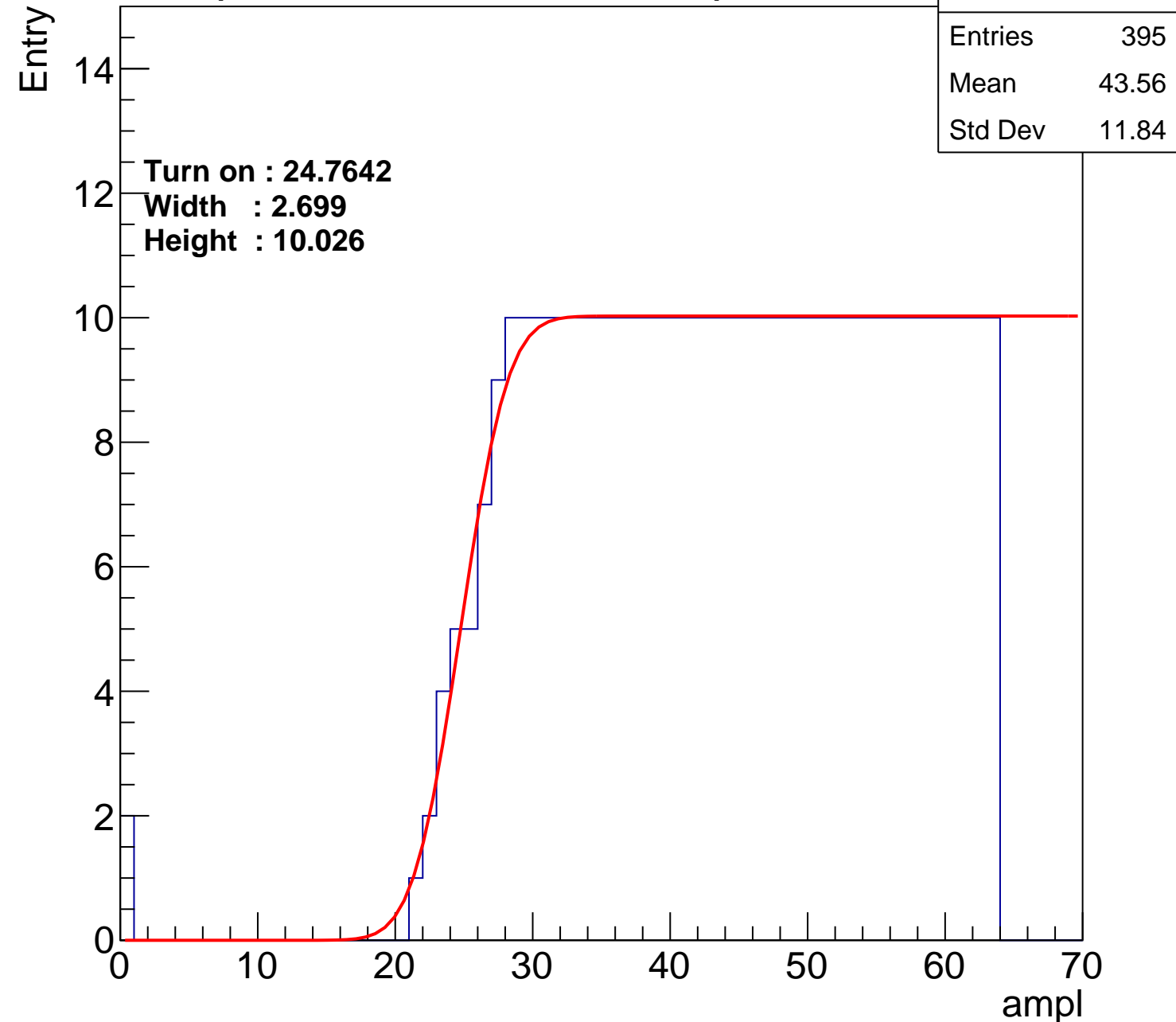
Width : 2.699

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.2999

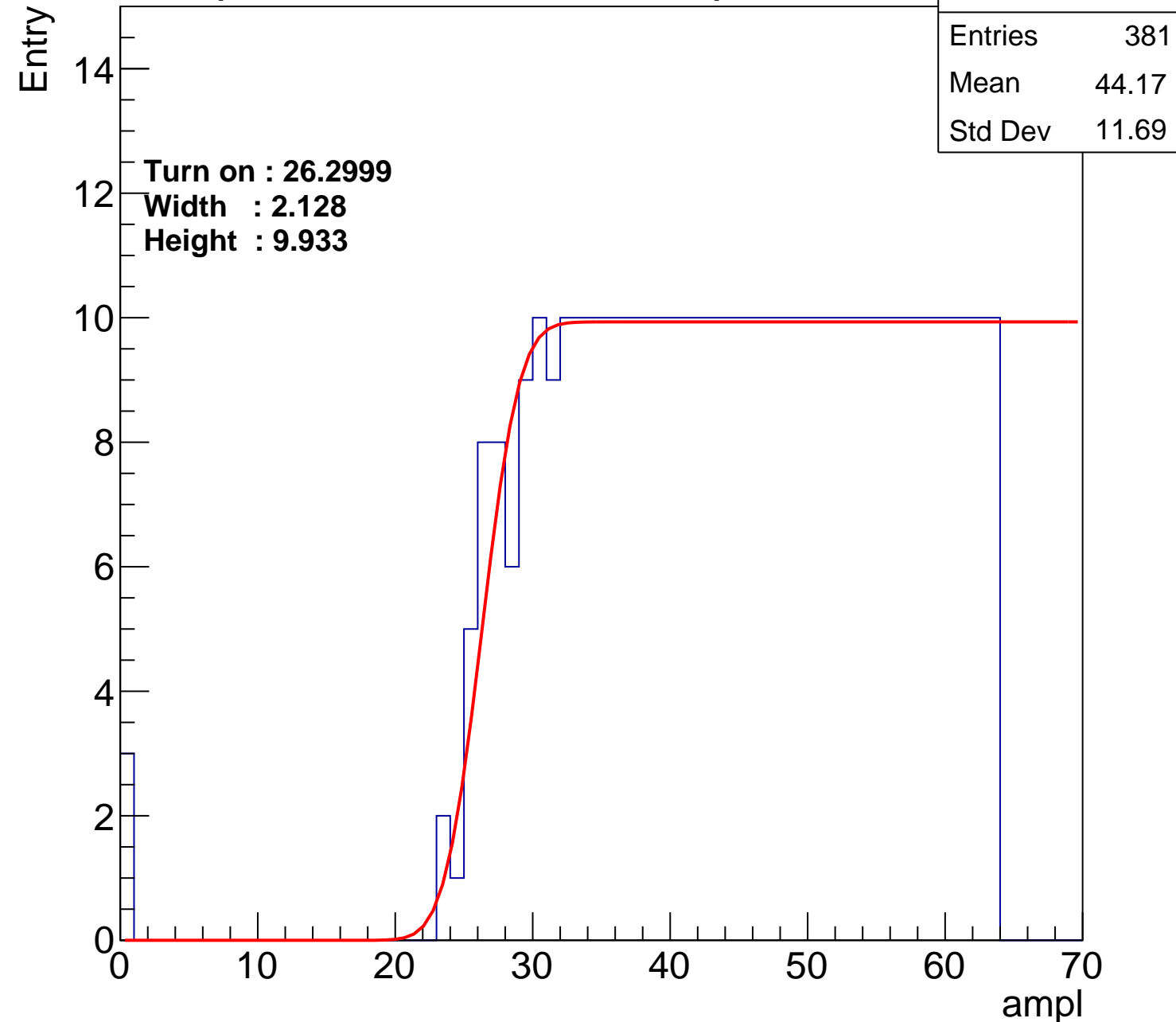
Width : 2.128

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.4
Std Dev	12

Turn on : 24.4659

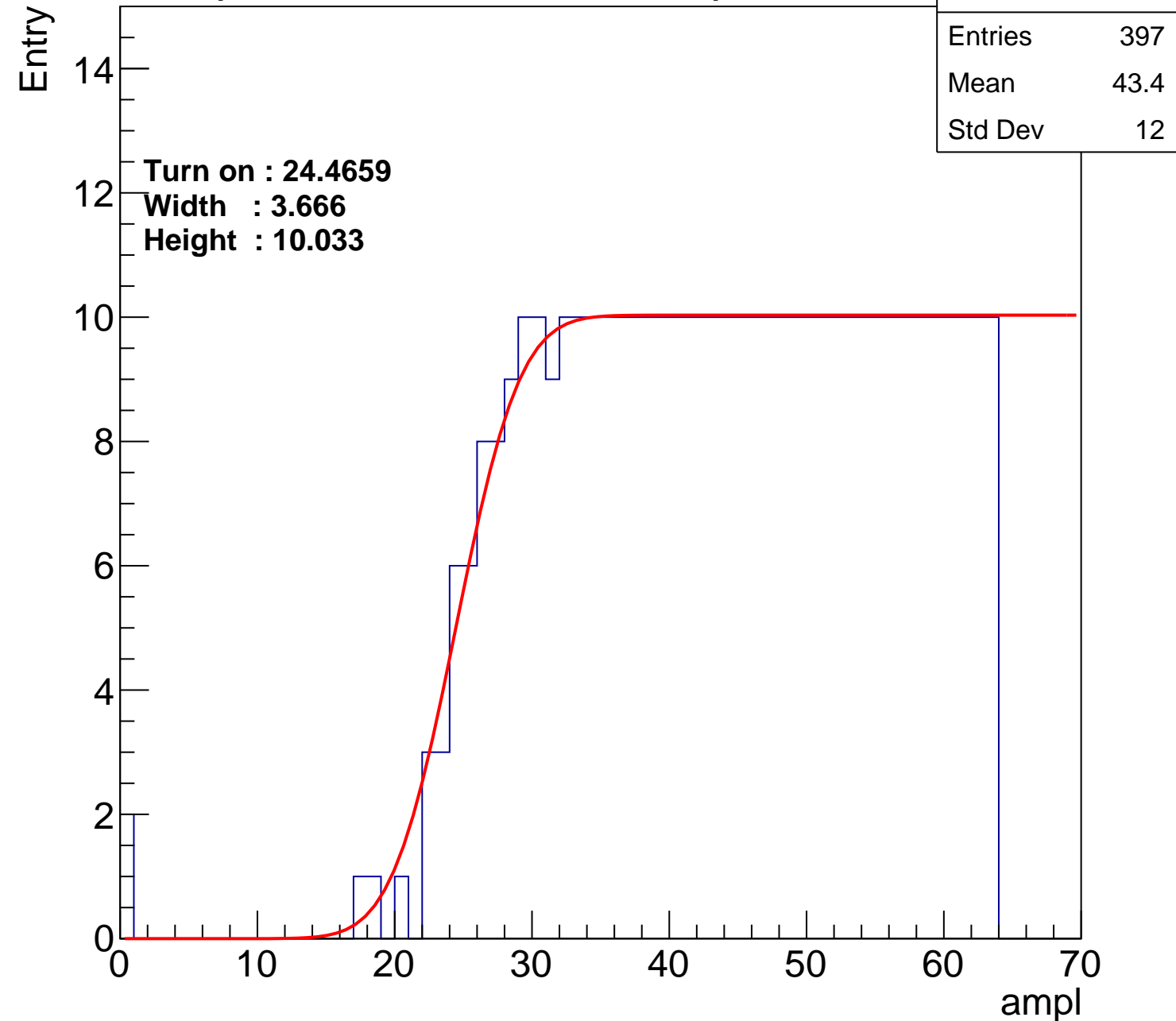
Width : 3.666

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 26.7414

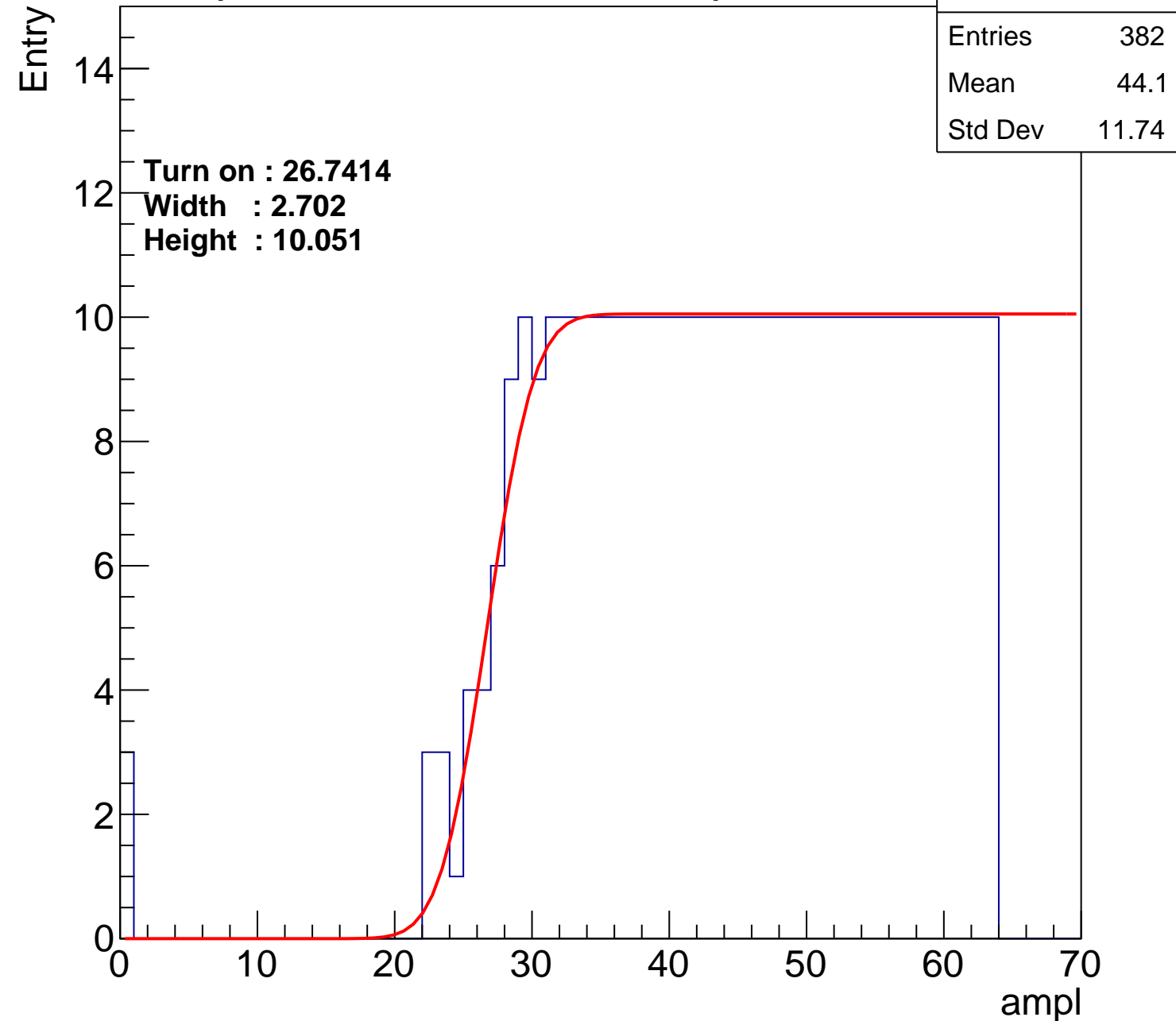
Width : 2.702

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.67
Std Dev	11.95

Turn on : 25.8069

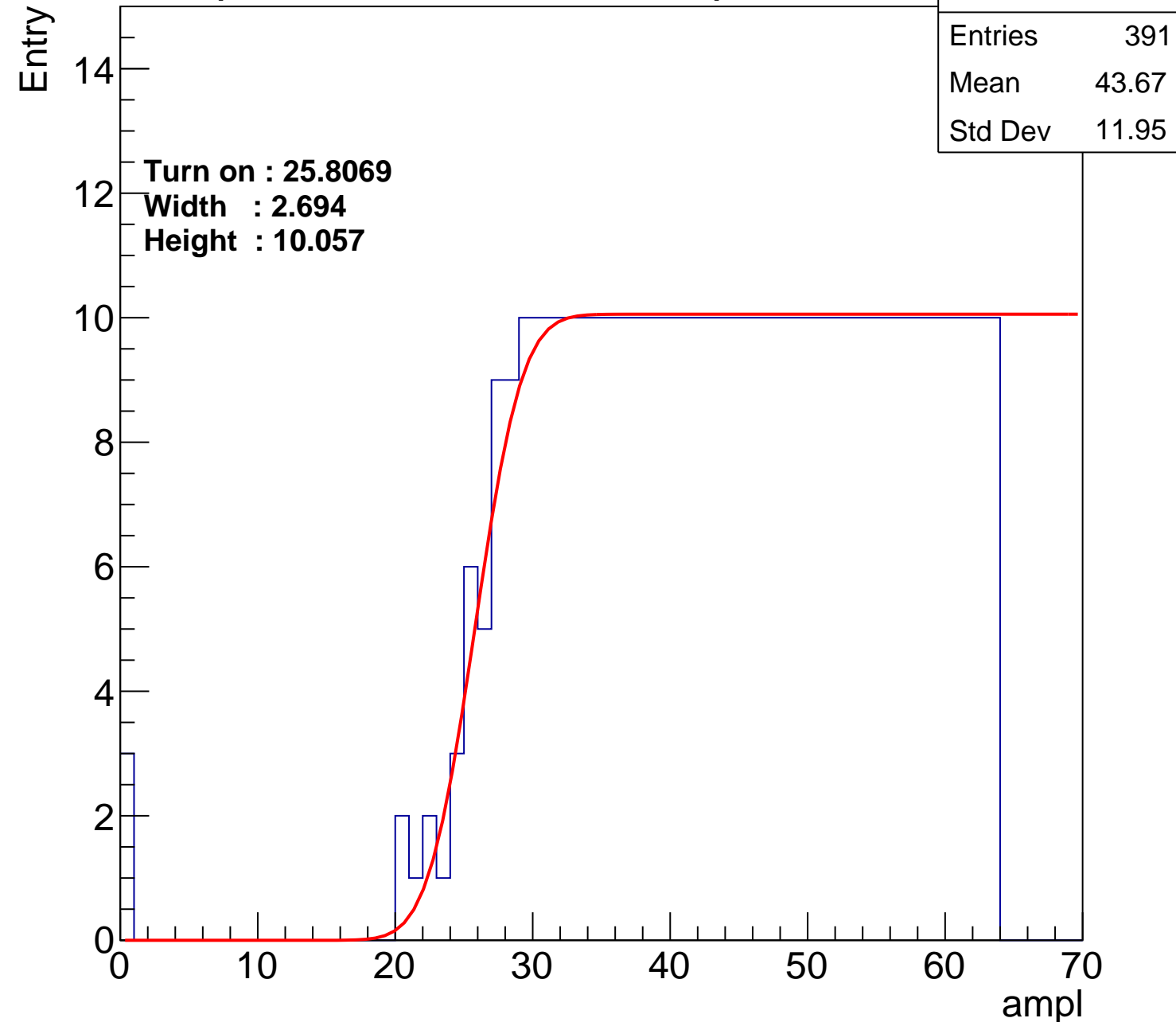
Width : 2.694

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.46
Std Dev	11.78

Turn on : 24.6044

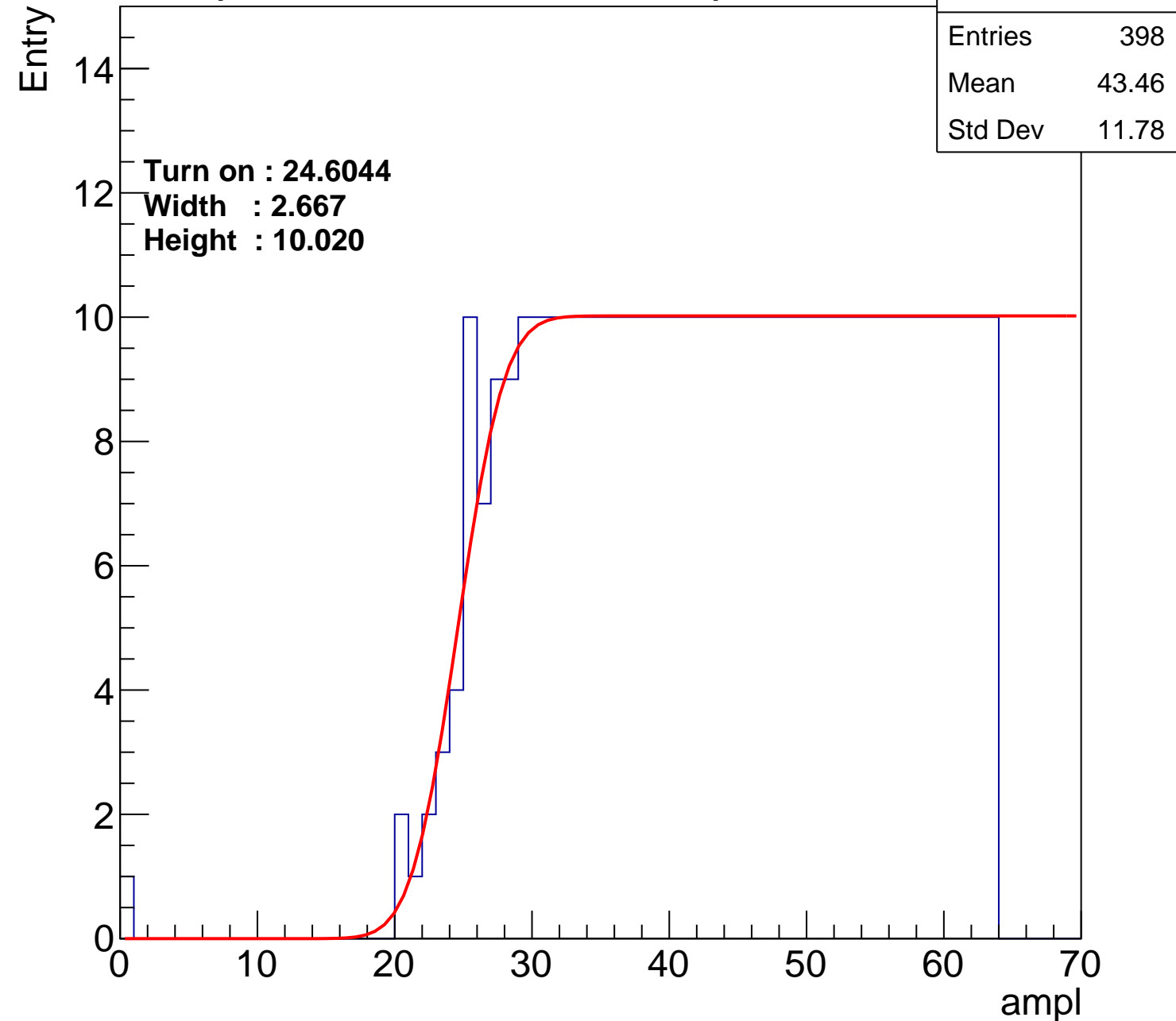
Width : 2.667

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch14

calib_packv5_042523_0143.root, FC#10, port B3

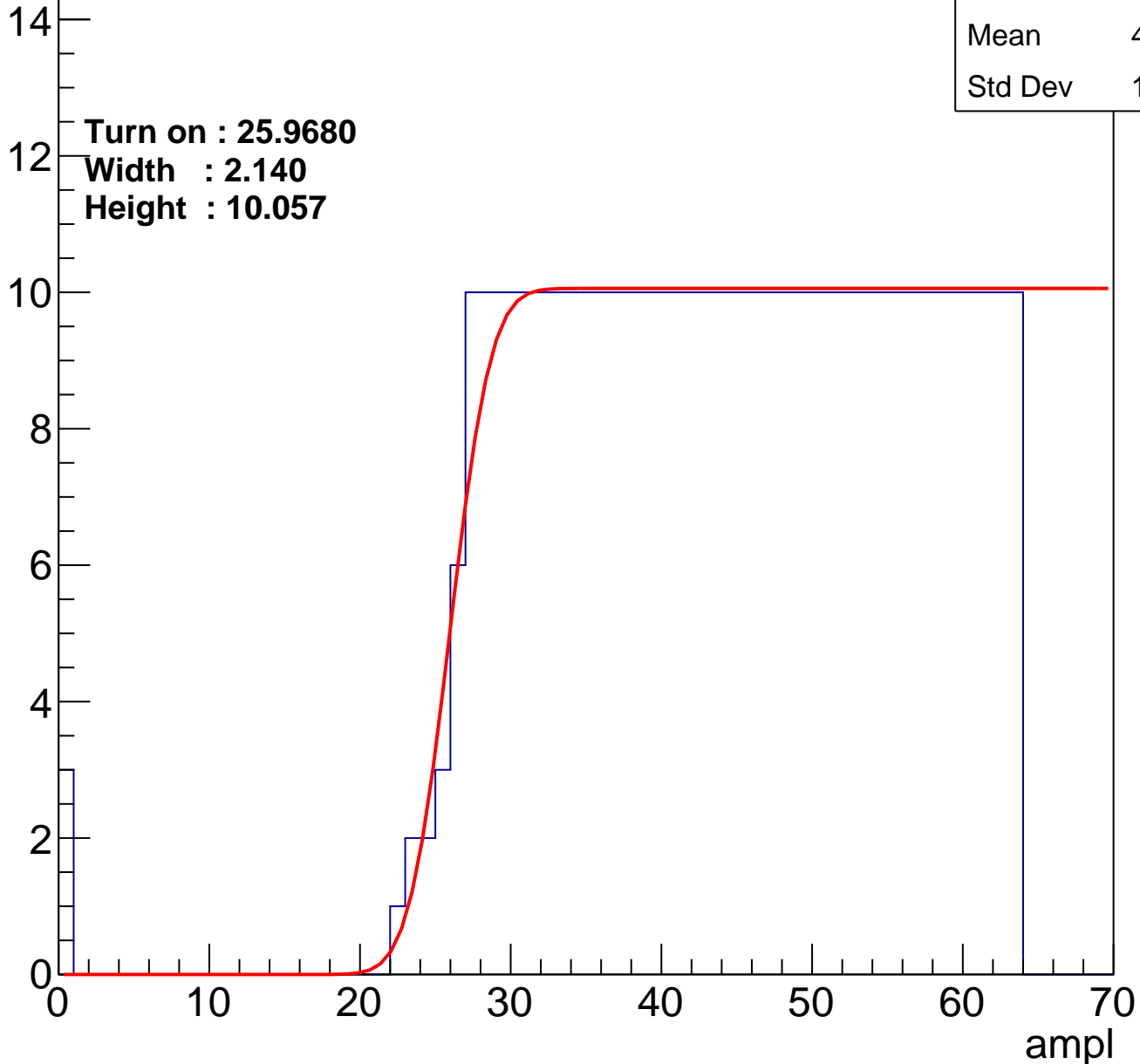
Entries	387
Mean	43.92
Std Dev	11.76

Turn on : 25.9680

Width : 2.140

Height : 10.057

Entry



B1L002S, U15-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.09
Std Dev	11.7

Turn on : 25.8218

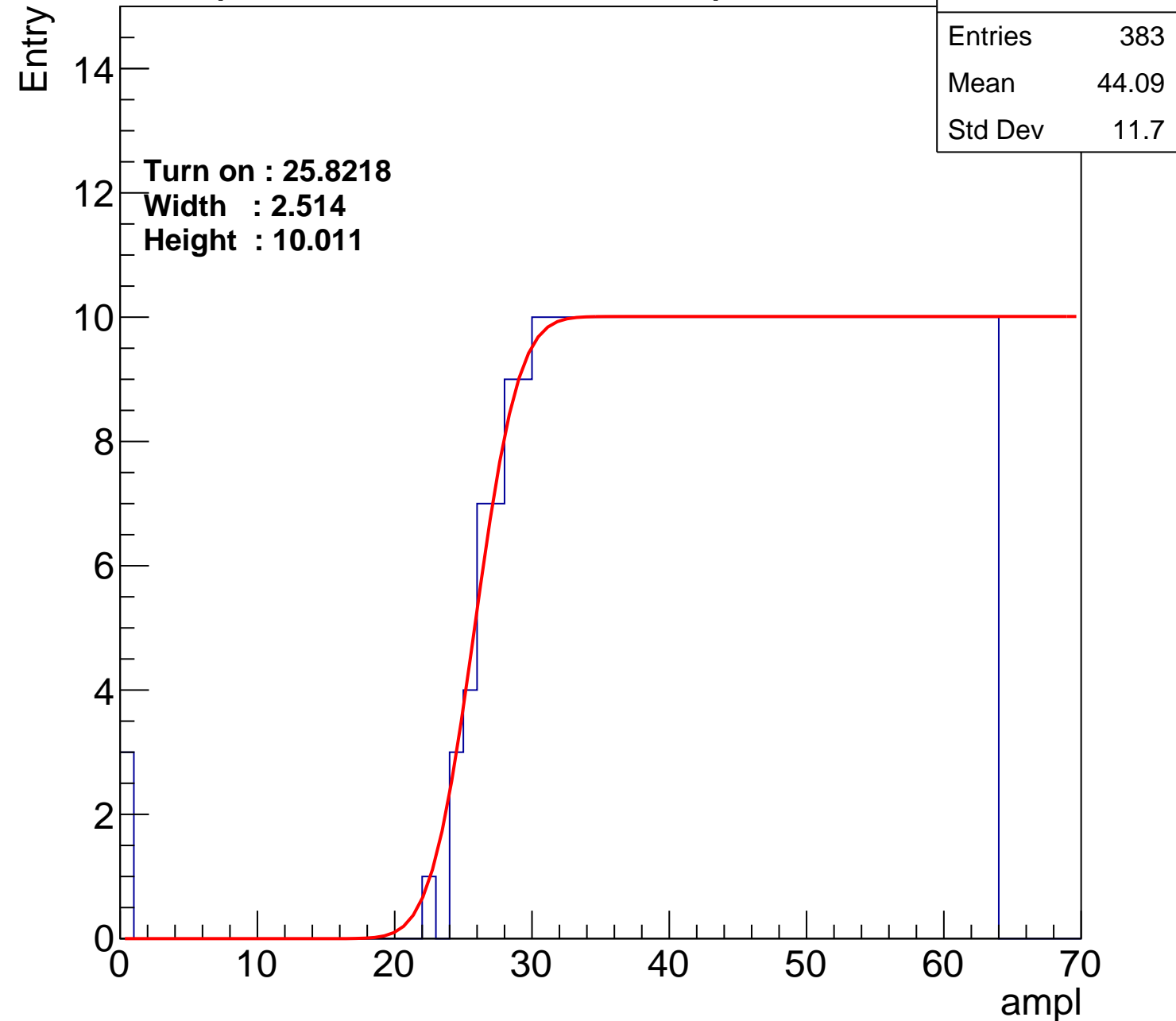
Width : 2.514

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch16

calib_packv5_042523_0143.root, FC#10, port B3

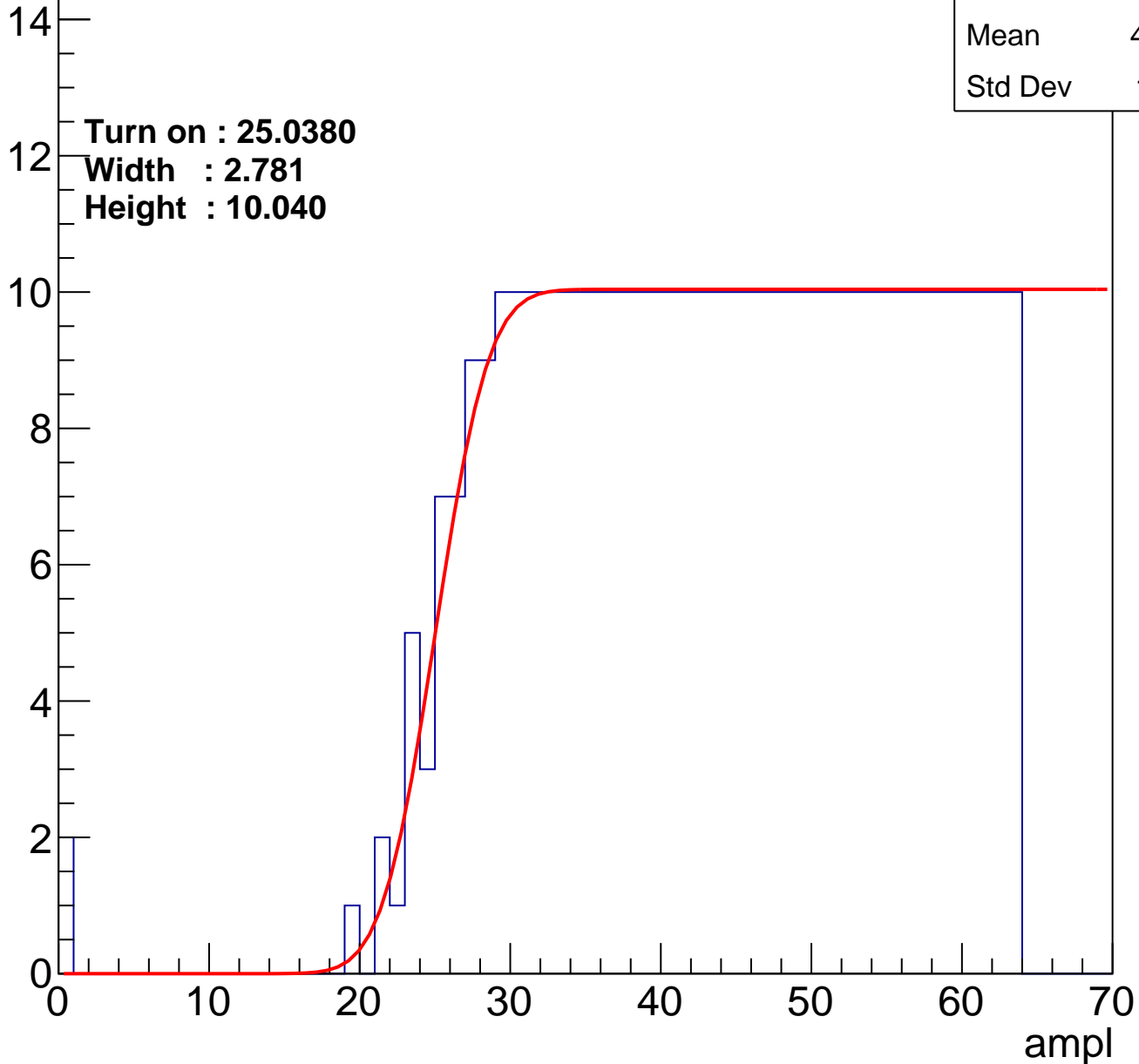
Entries	396
Mean	43.49
Std Dev	11.91

Turn on : 25.0380

Width : 2.781

Height : 10.040

Entry



B1L002S, U15-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.14
Std Dev	12.1

Turn on : 24.0638

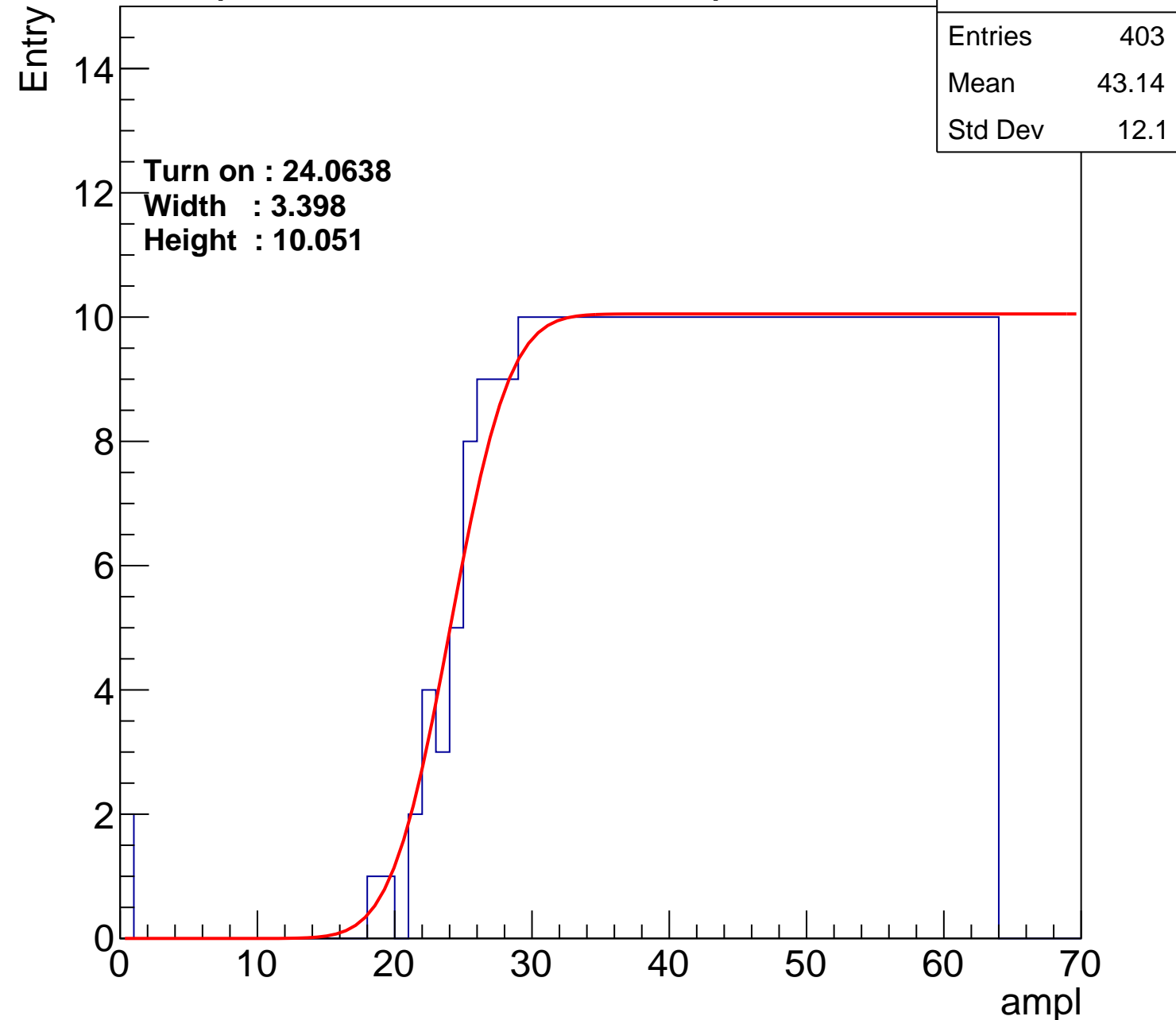
Width : 3.398

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.6829

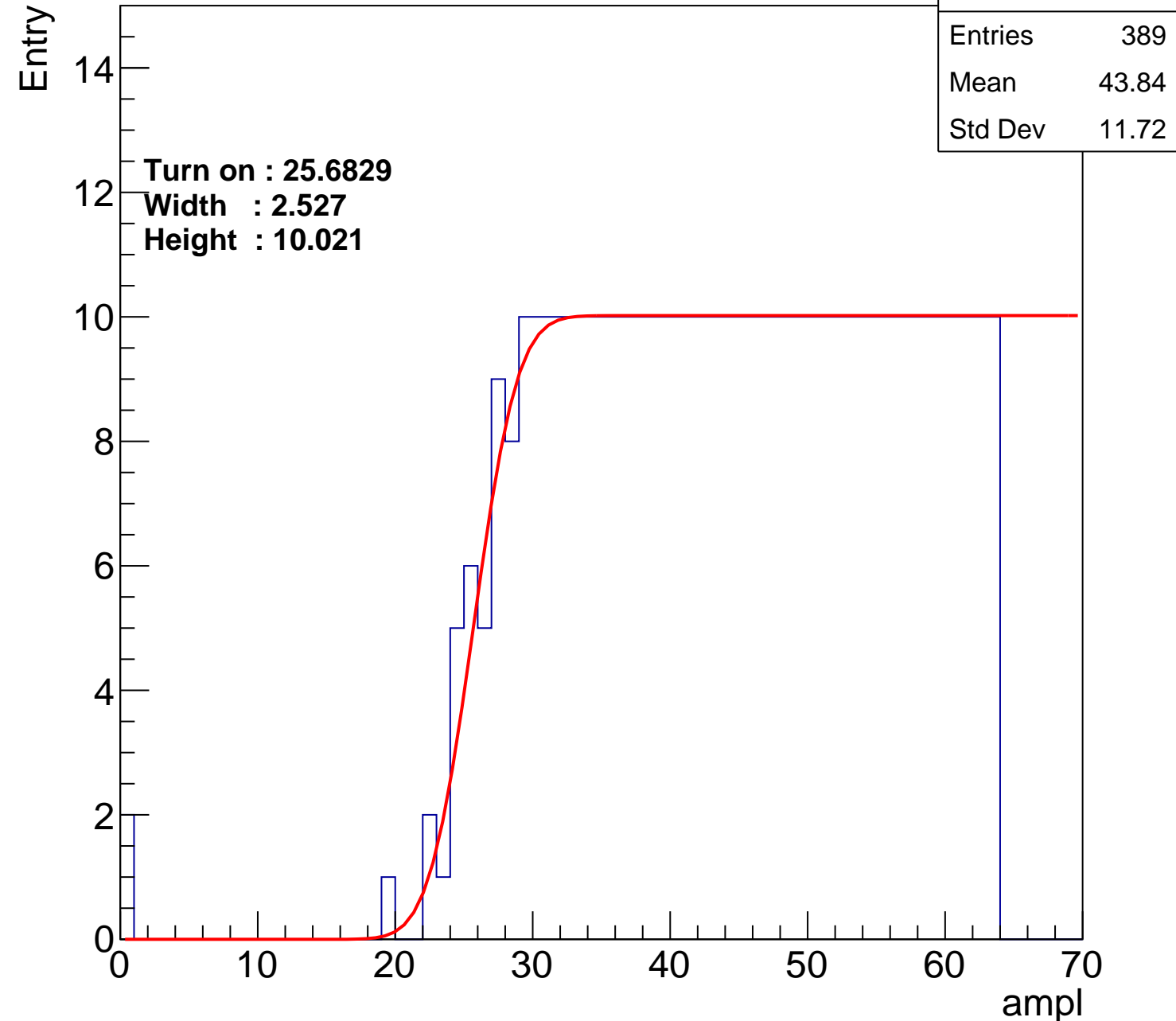
Width : 2.527

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.5934

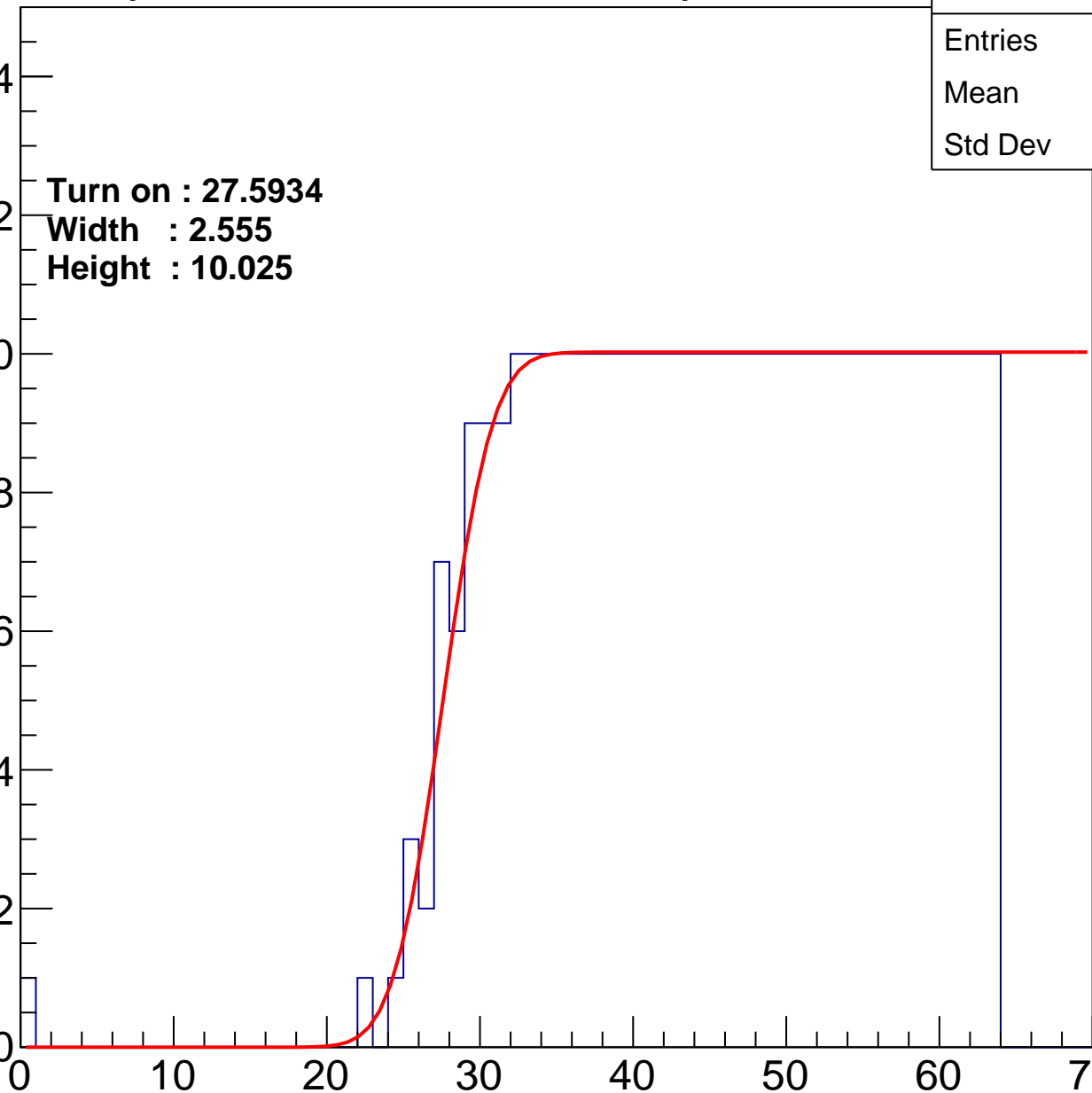
Width : 2.555

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.99
Std Dev	11.69

Turn on : 26.2756

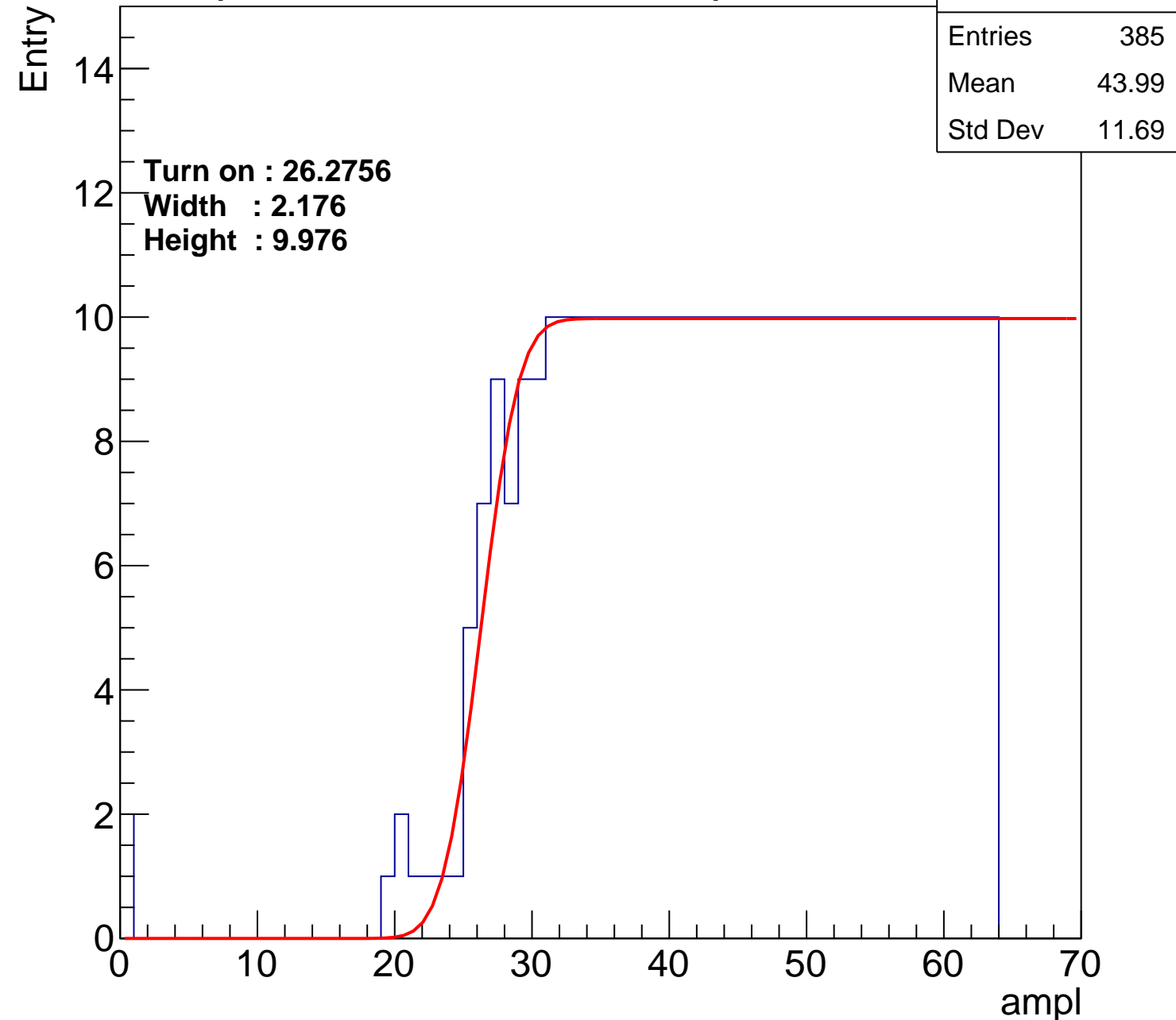
Width : 2.176

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.93
Std Dev	12.28

Turn on : 23.7626

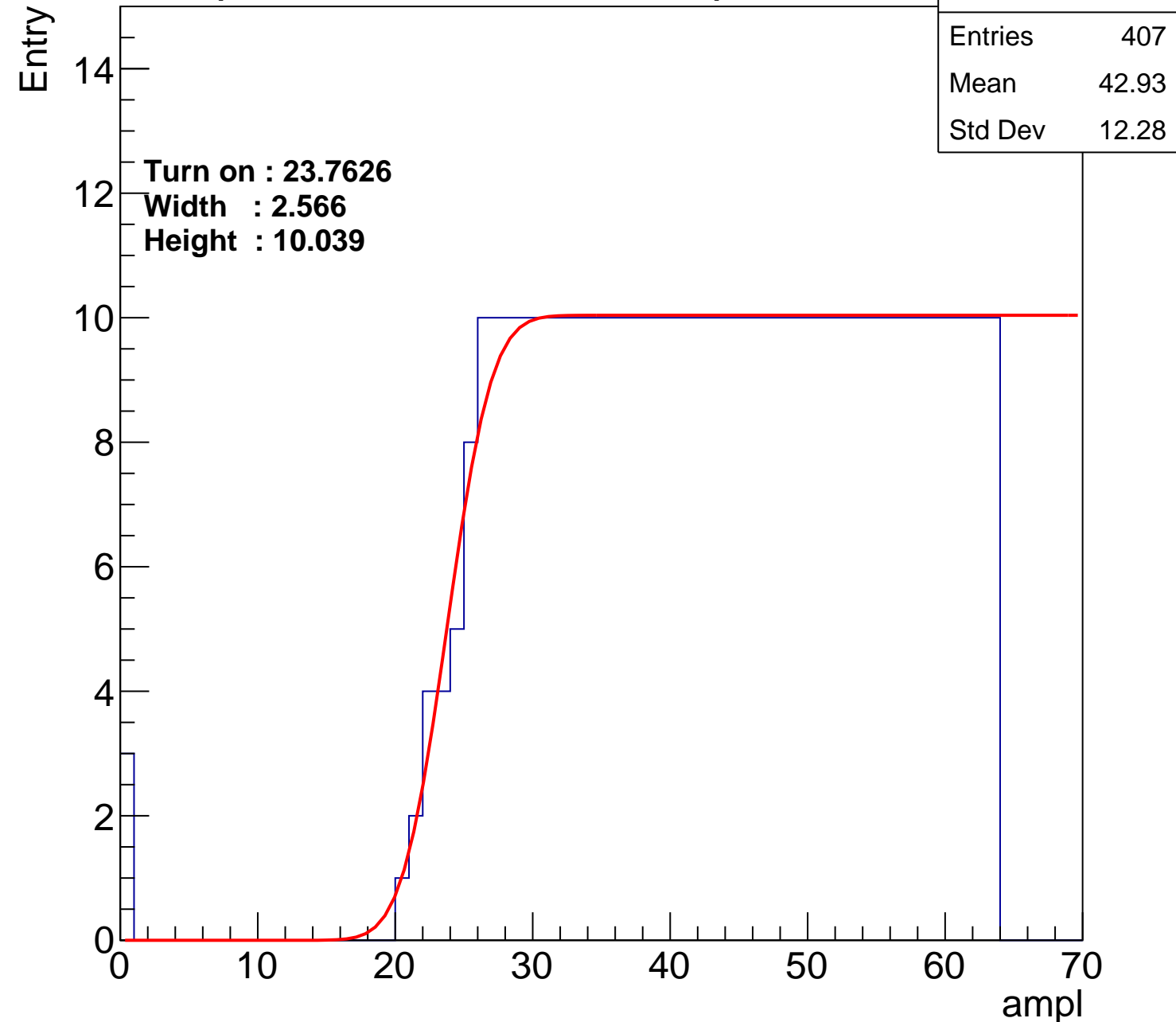
Width : 2.566

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch22

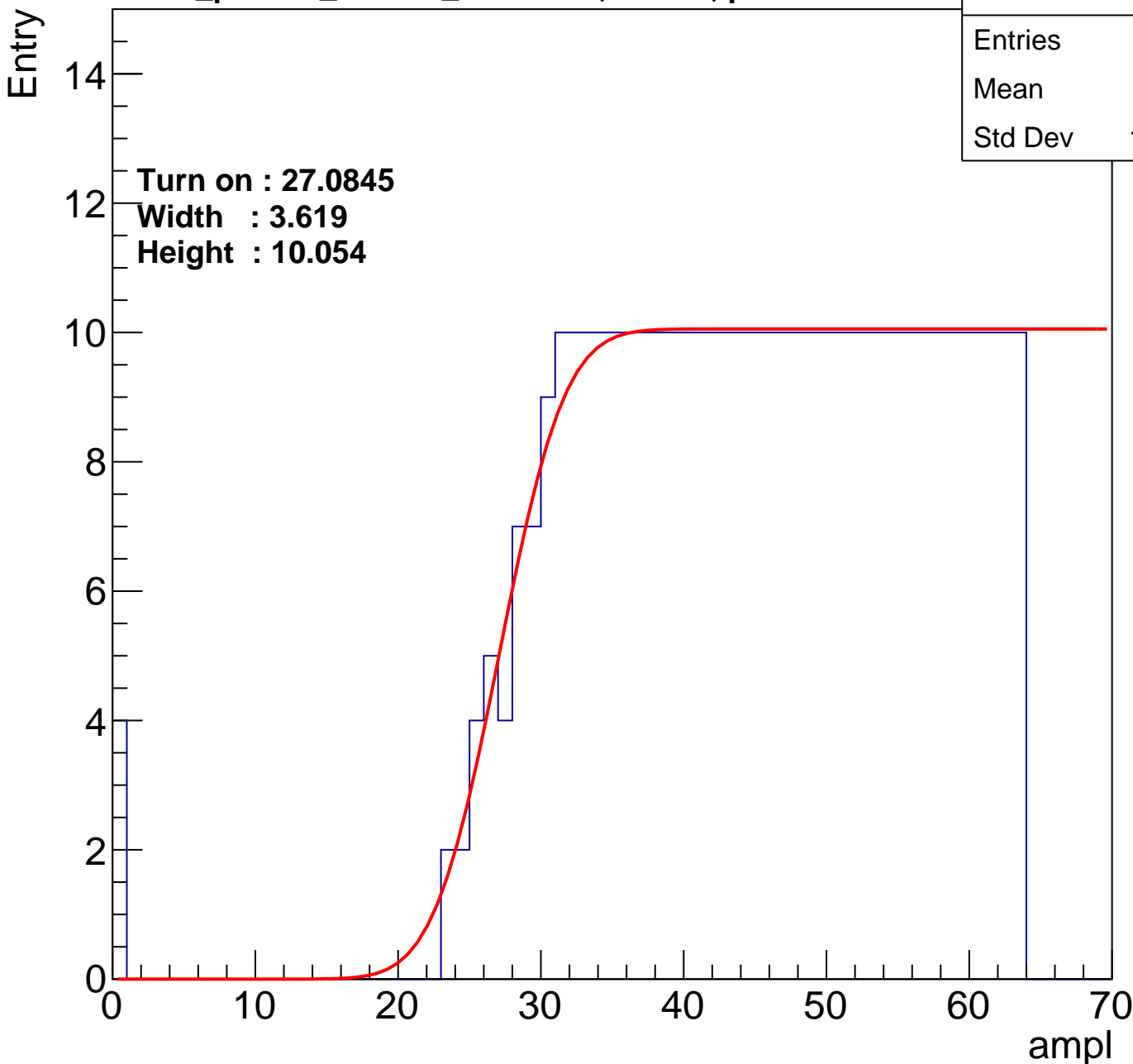
calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.41
Std Dev	11.75

Turn on : 27.0845

Width : 3.619

Height : 10.054



B1L002S, U15-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.33
Std Dev	12.11

Turn on : 24.9852

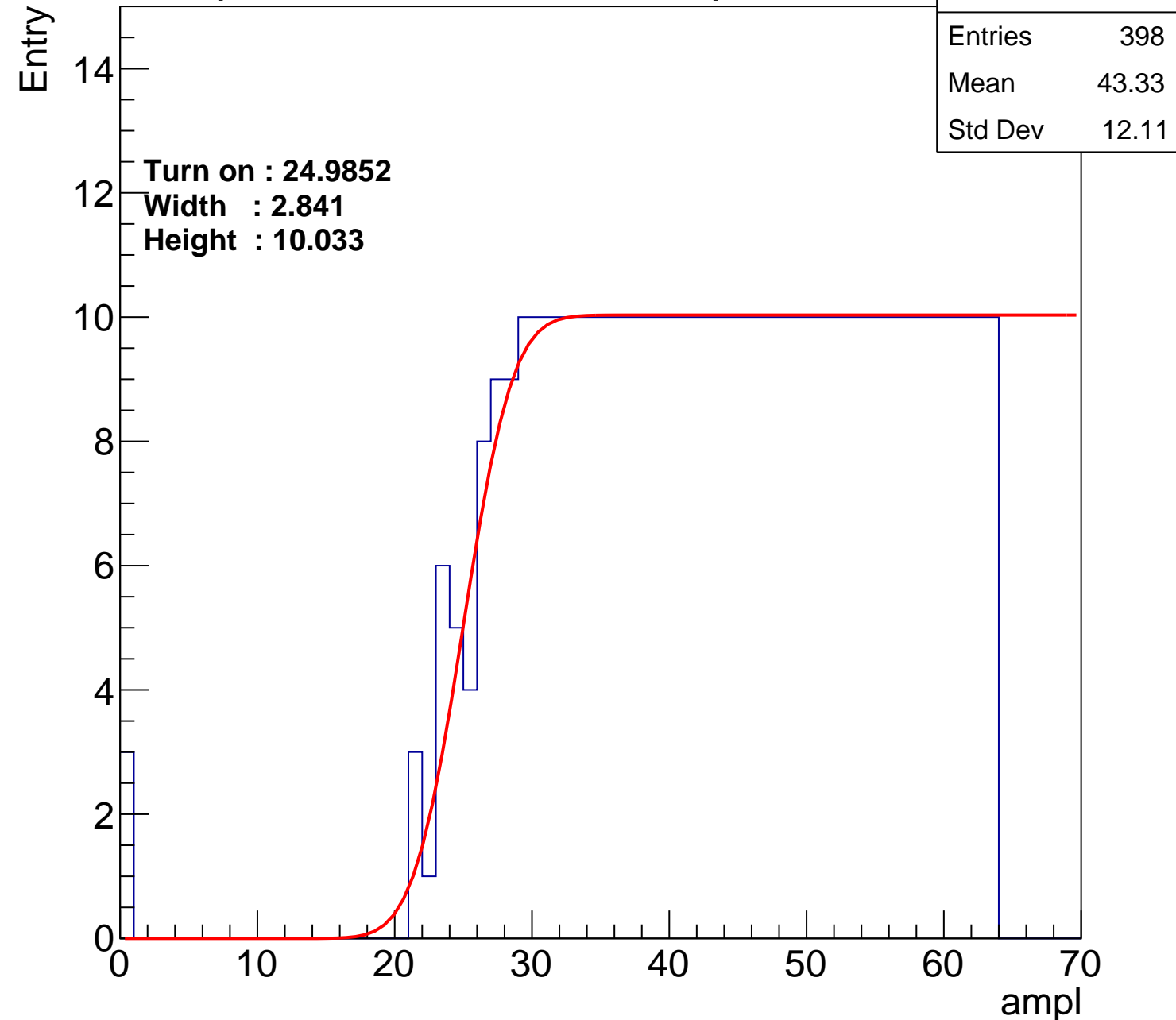
Width : 2.841

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.56
Std Dev	11.72

Turn on : 24.6248

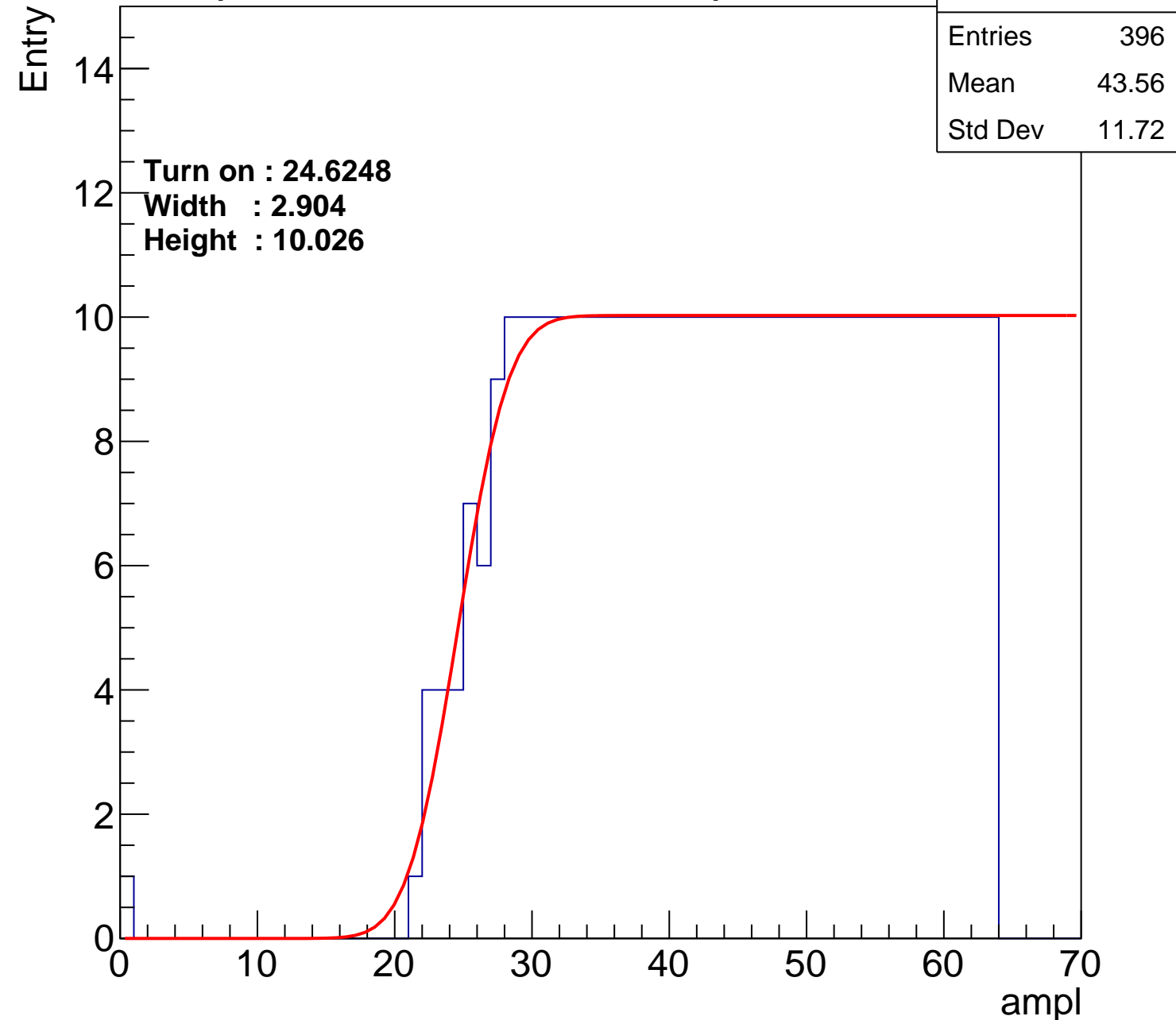
Width : 2.904

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.85
Std Dev	12.46

Turn on : 23.5863

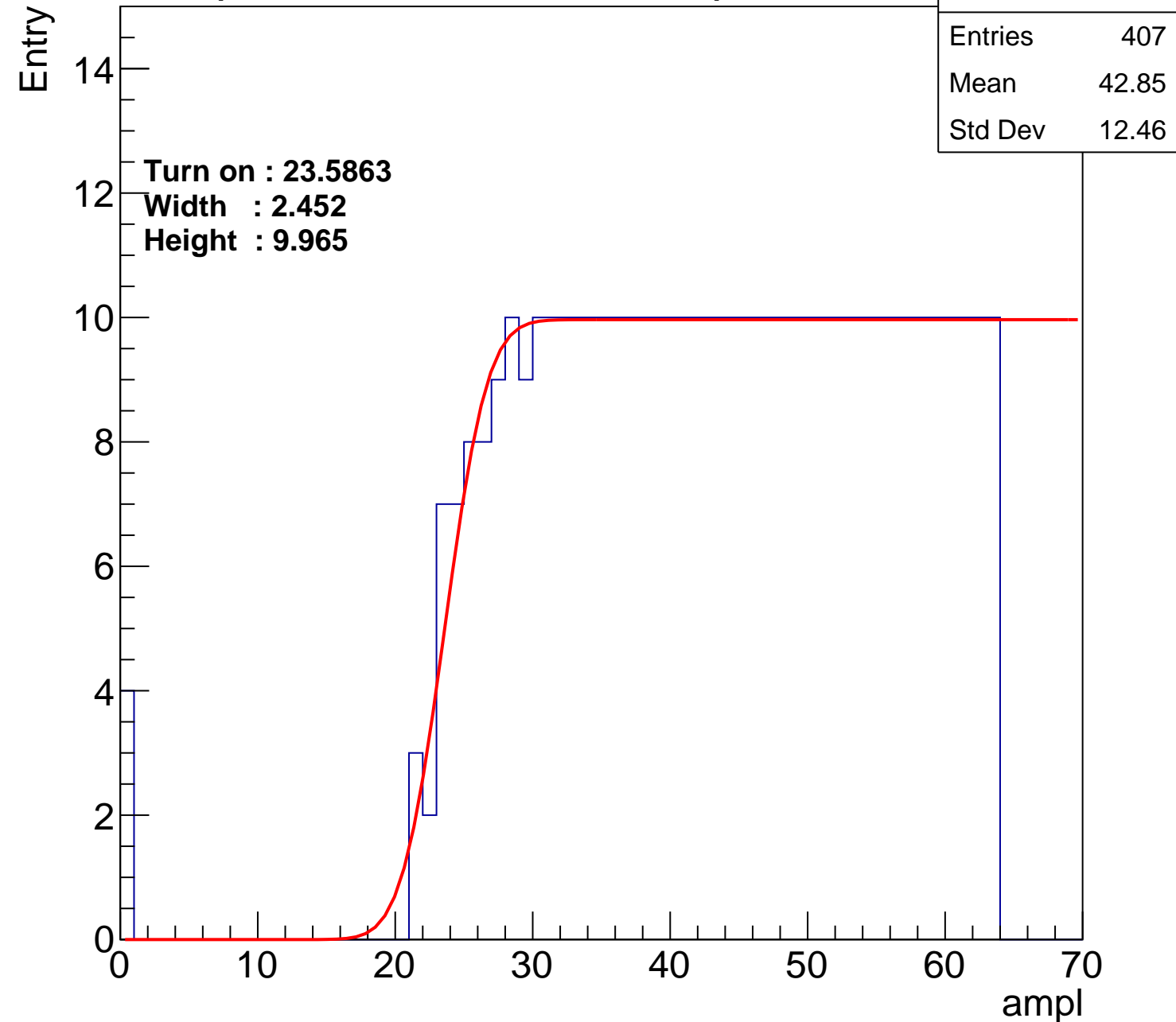
Width : 2.452

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.66

Turn on : 25.6663

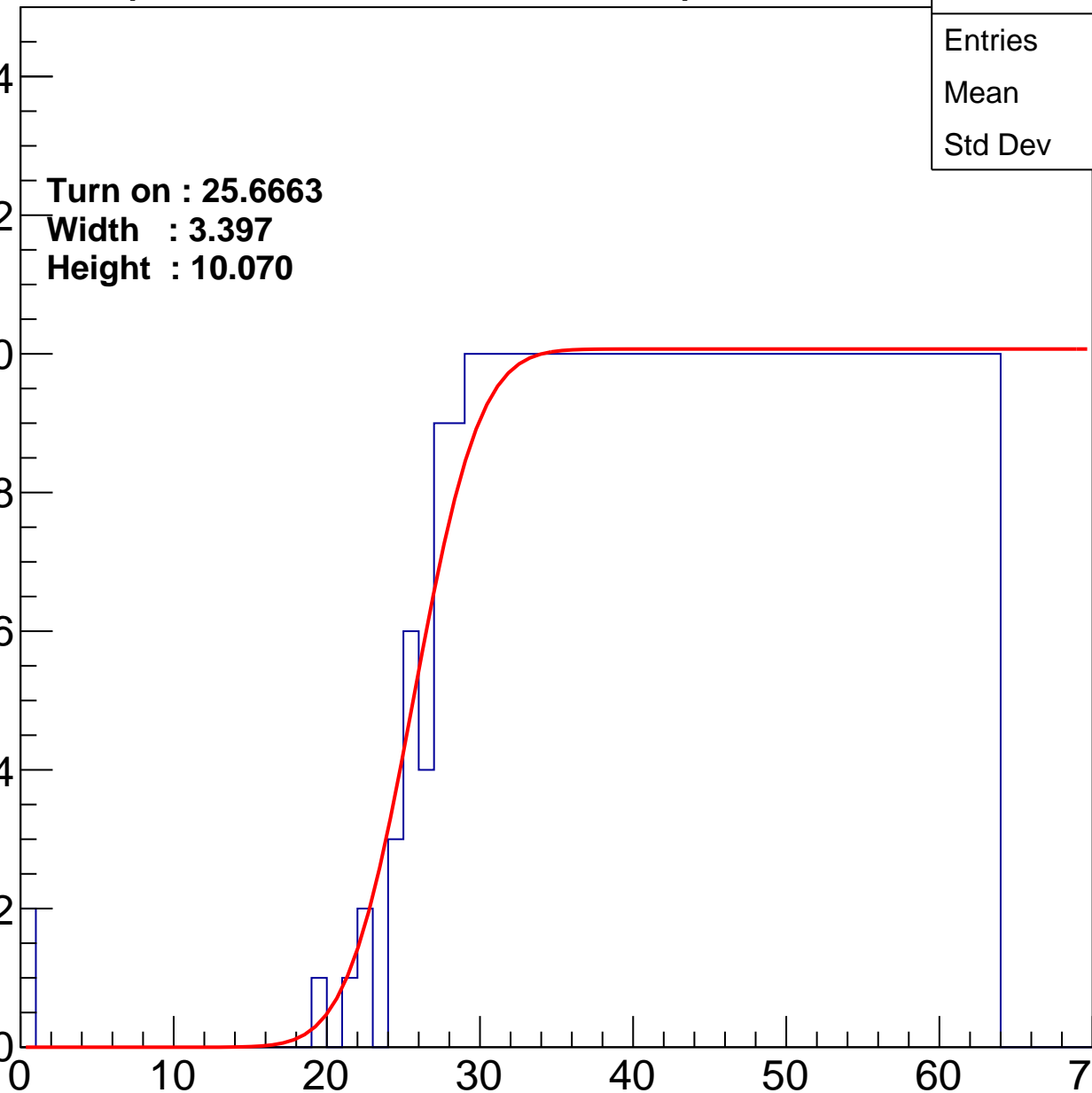
Width : 3.397

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.36
Std Dev	11.29

Turn on : 26.1854

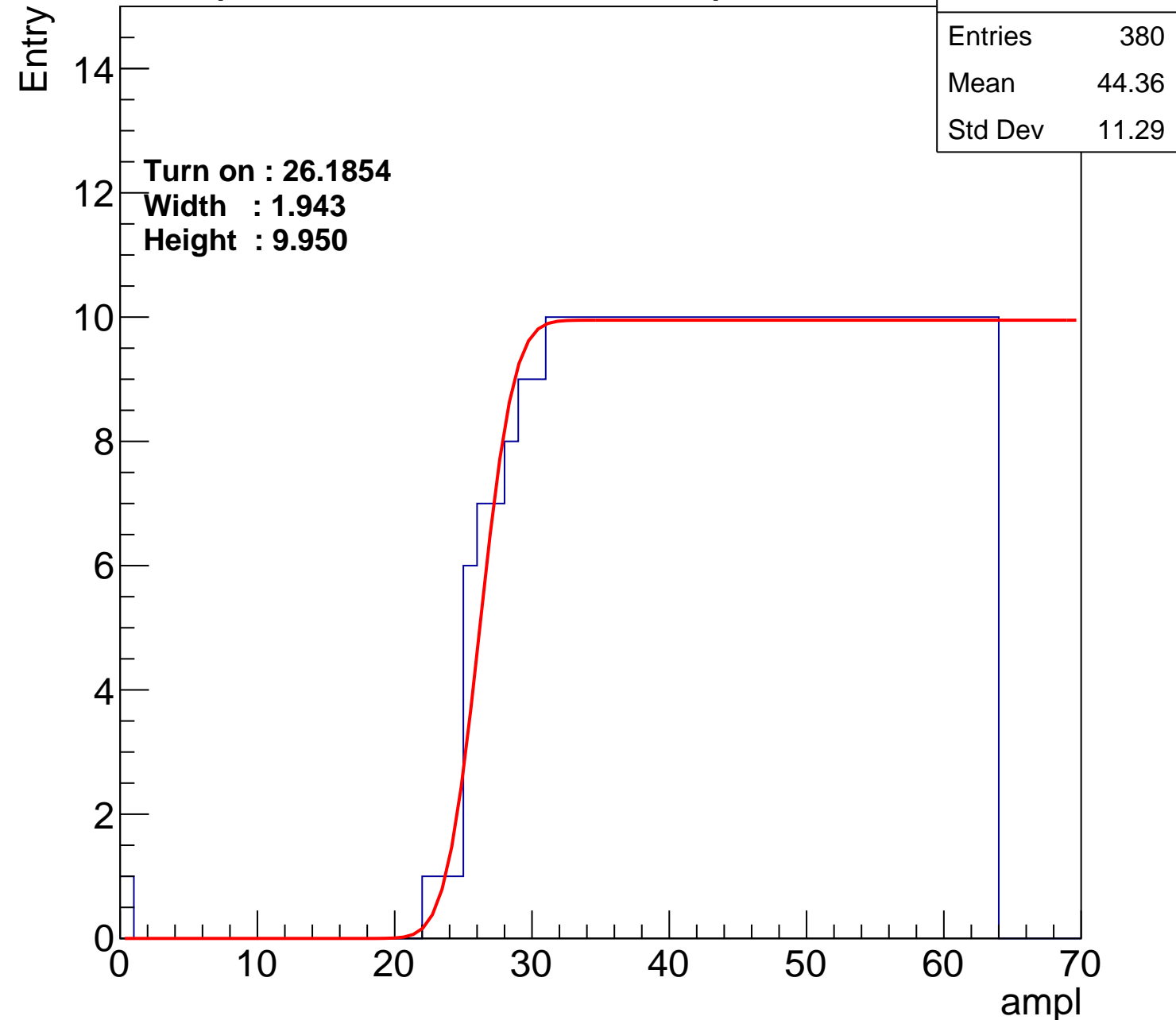
Width : 1.943

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.17
Std Dev	11.55

Turn on : 26.0798

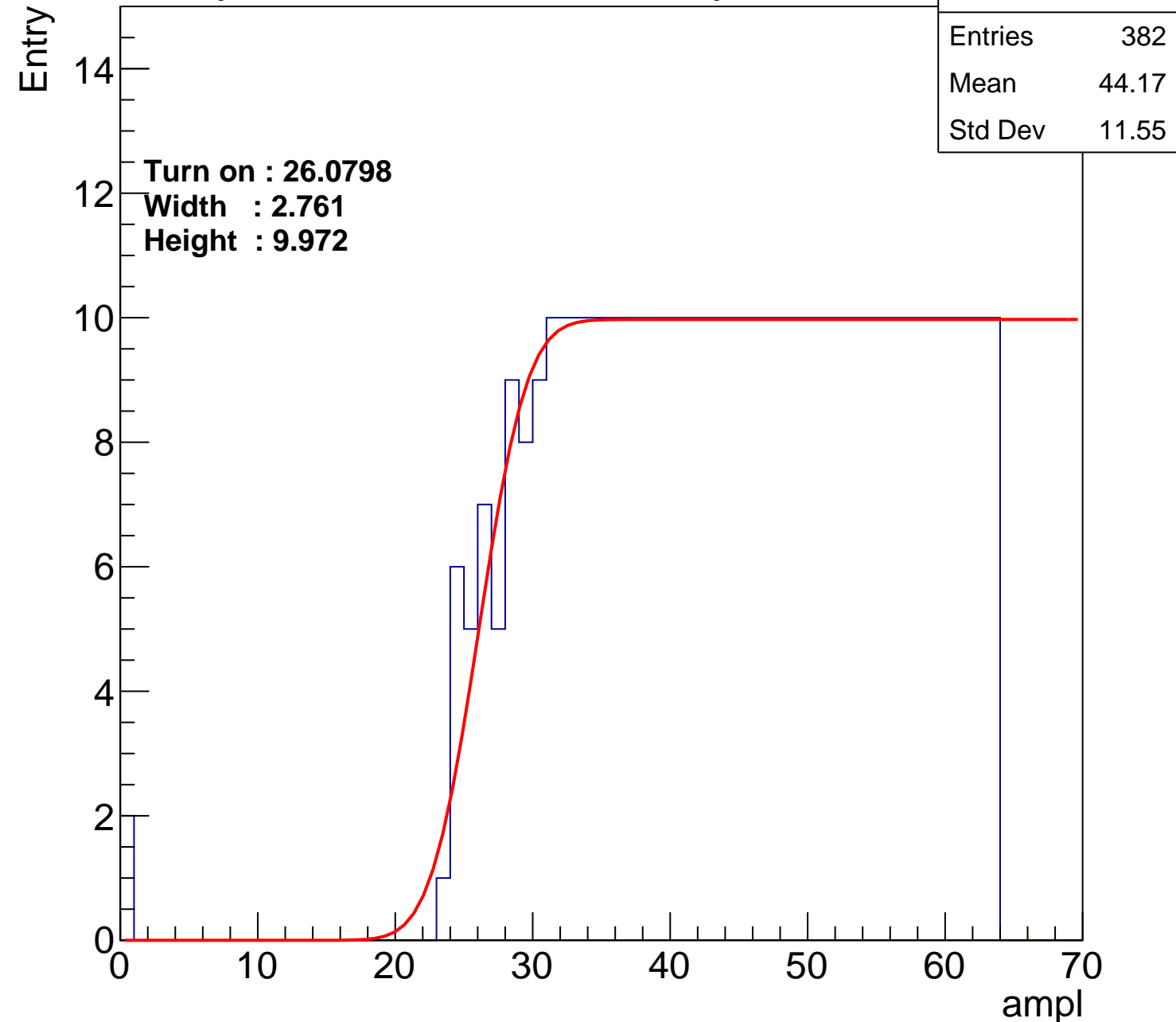
Width : 2.761

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.42
Std Dev	12.07

Turn on : 24.8311

Width : 2.472

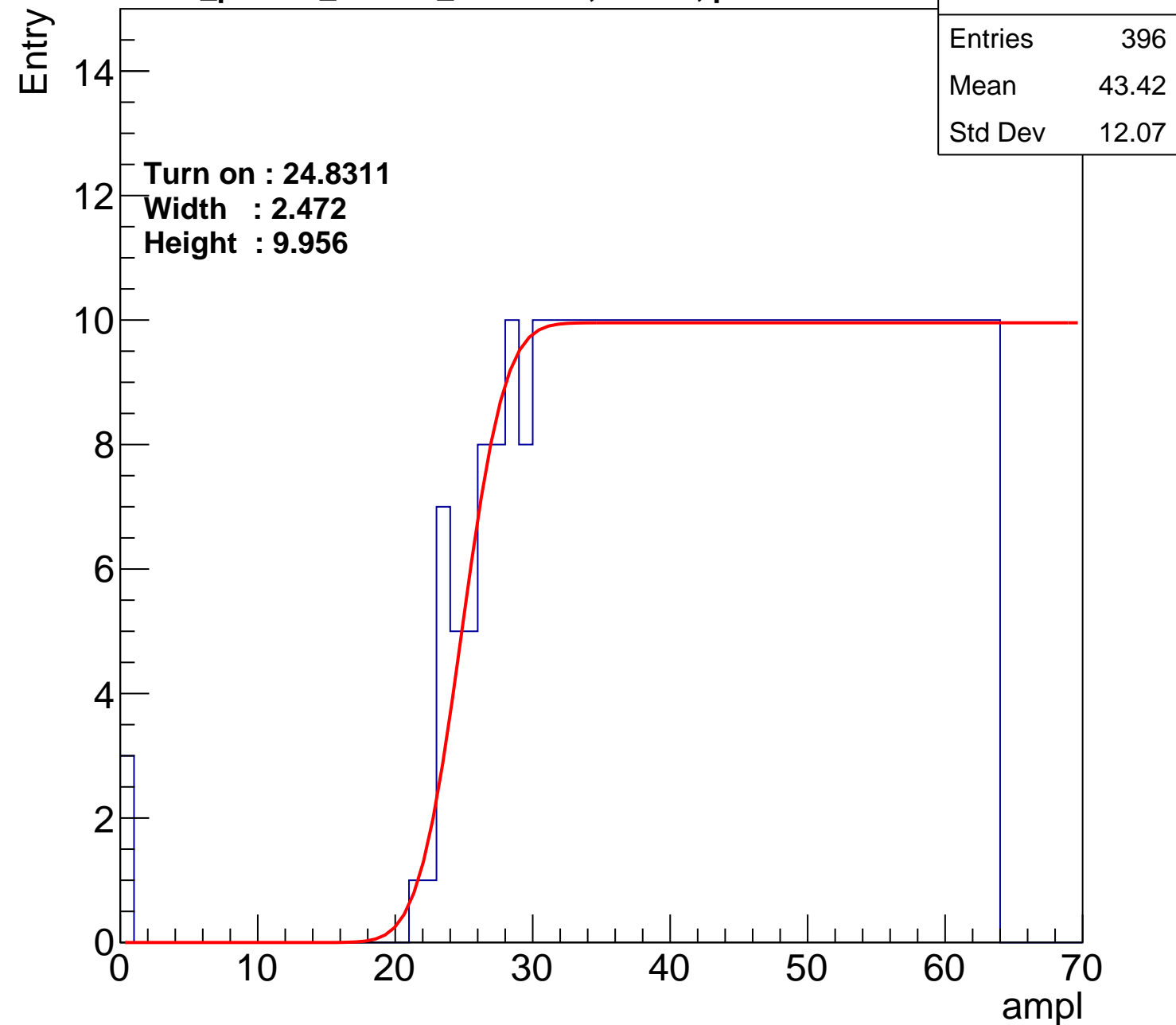
Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U15-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.17
Std Dev	11.68

Turn on : 26.2884

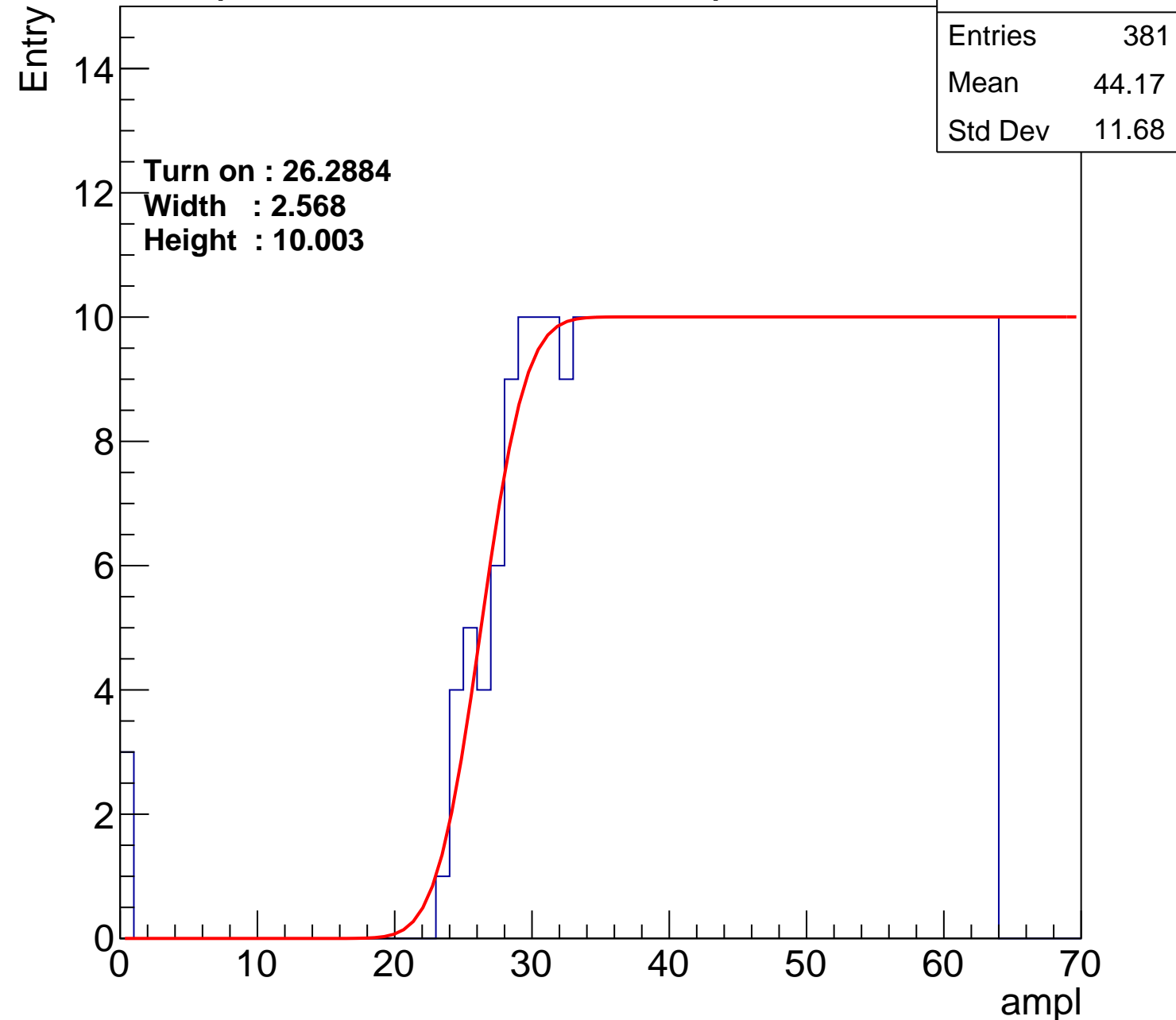
Width : 2.568

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch31

calib_packv5_042523_0143.root, FC#10, port B3

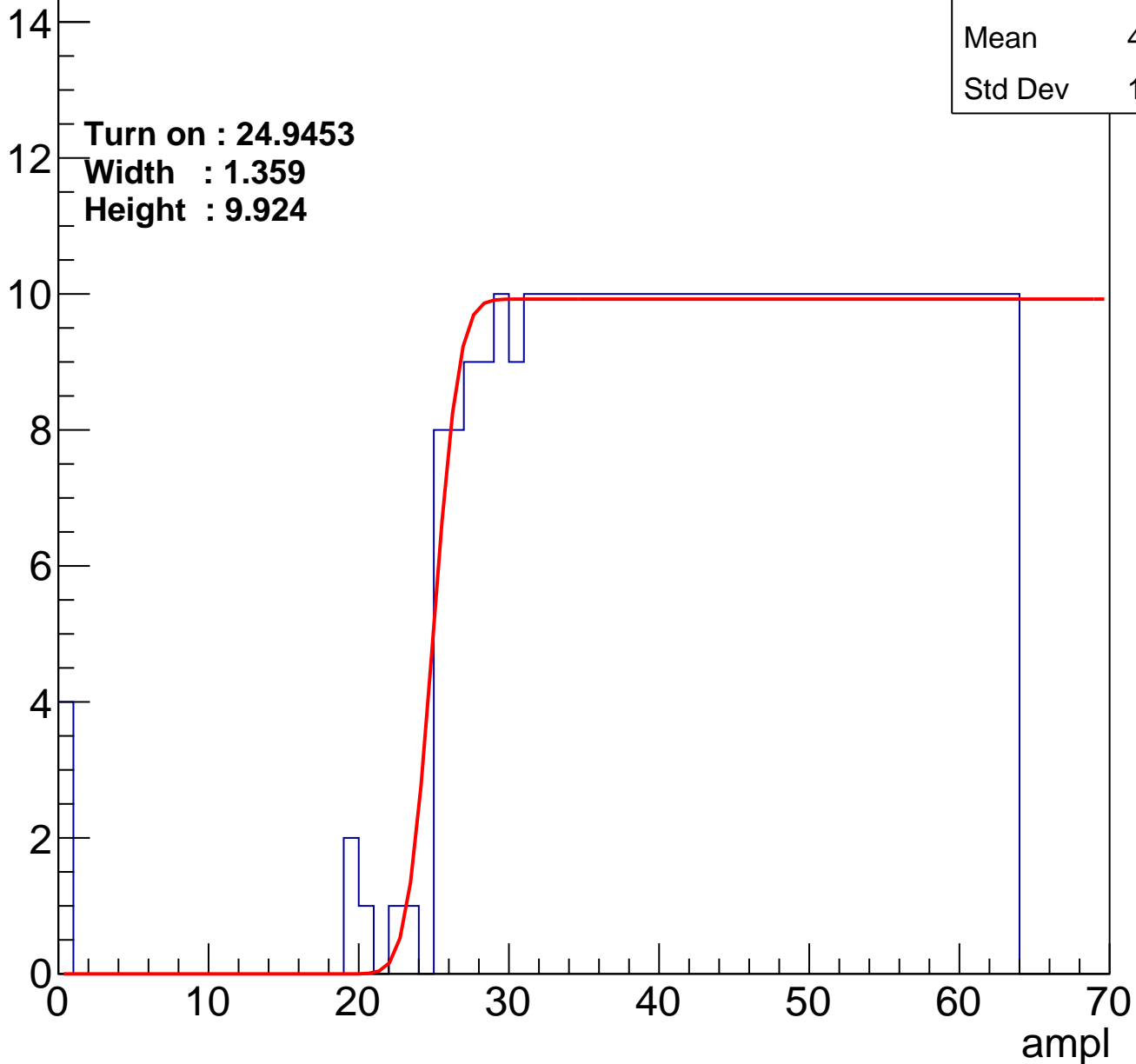
Entries	392
Mean	43.56
Std Dev	12.13

Turn on : 24.9453

Width : 1.359

Height : 9.924

Entry



B1L002S, U15-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 25.2221

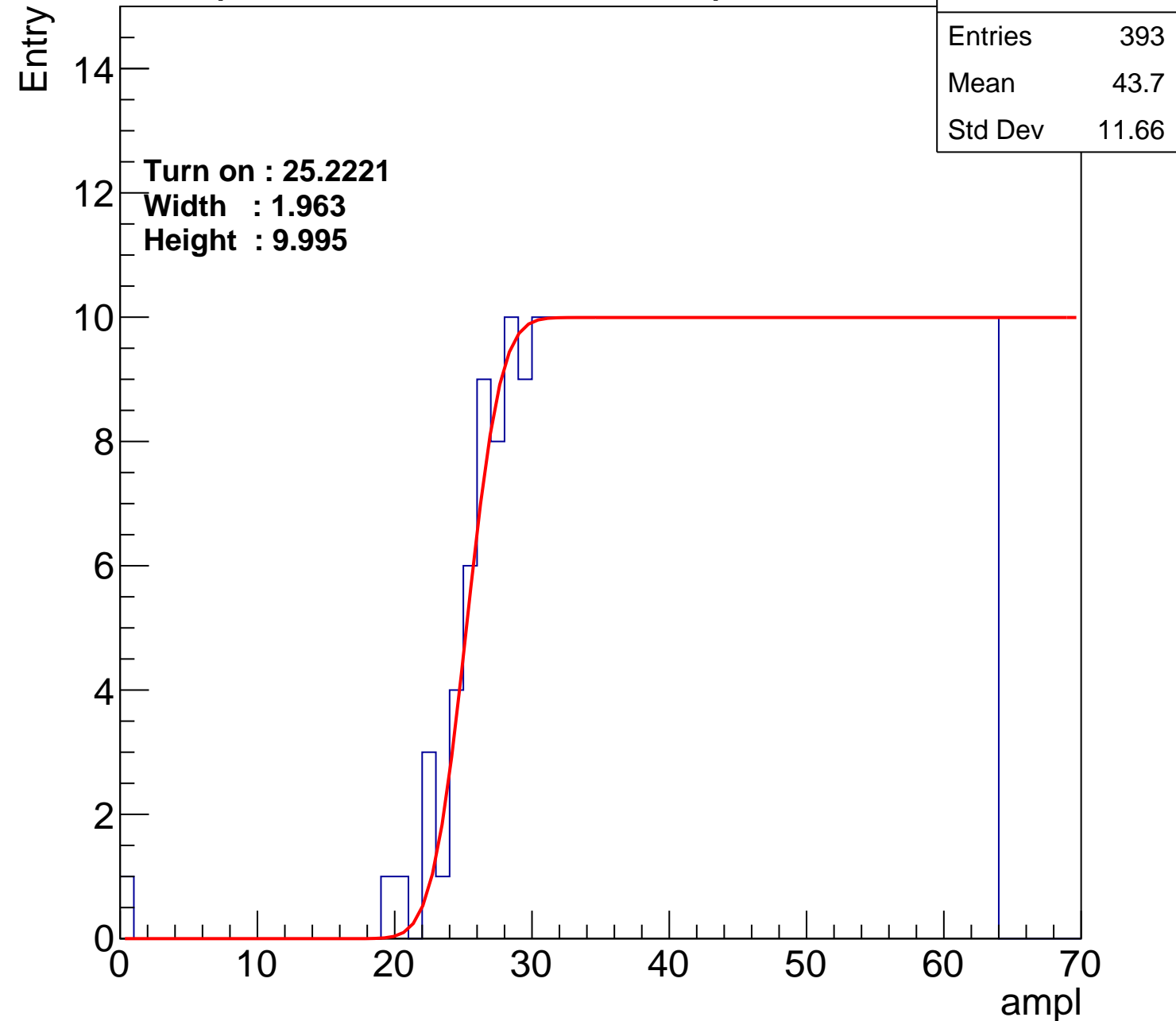
Width : 1.963

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.67
Std Dev	11.71

Turn on : 25.7056

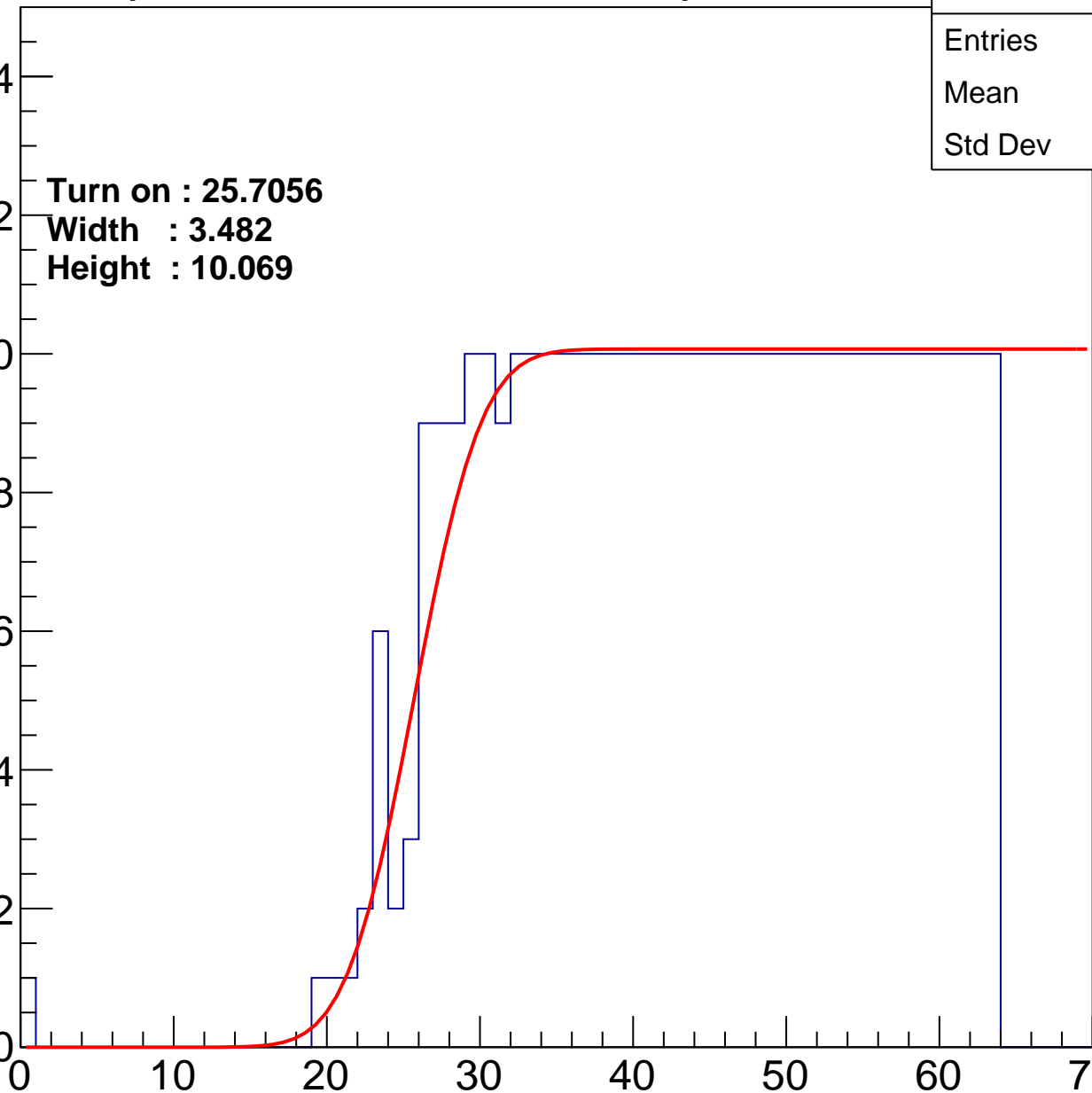
Width : 3.482

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.84
Std Dev	11.6

Turn on : 25.2712

Width : 2.619

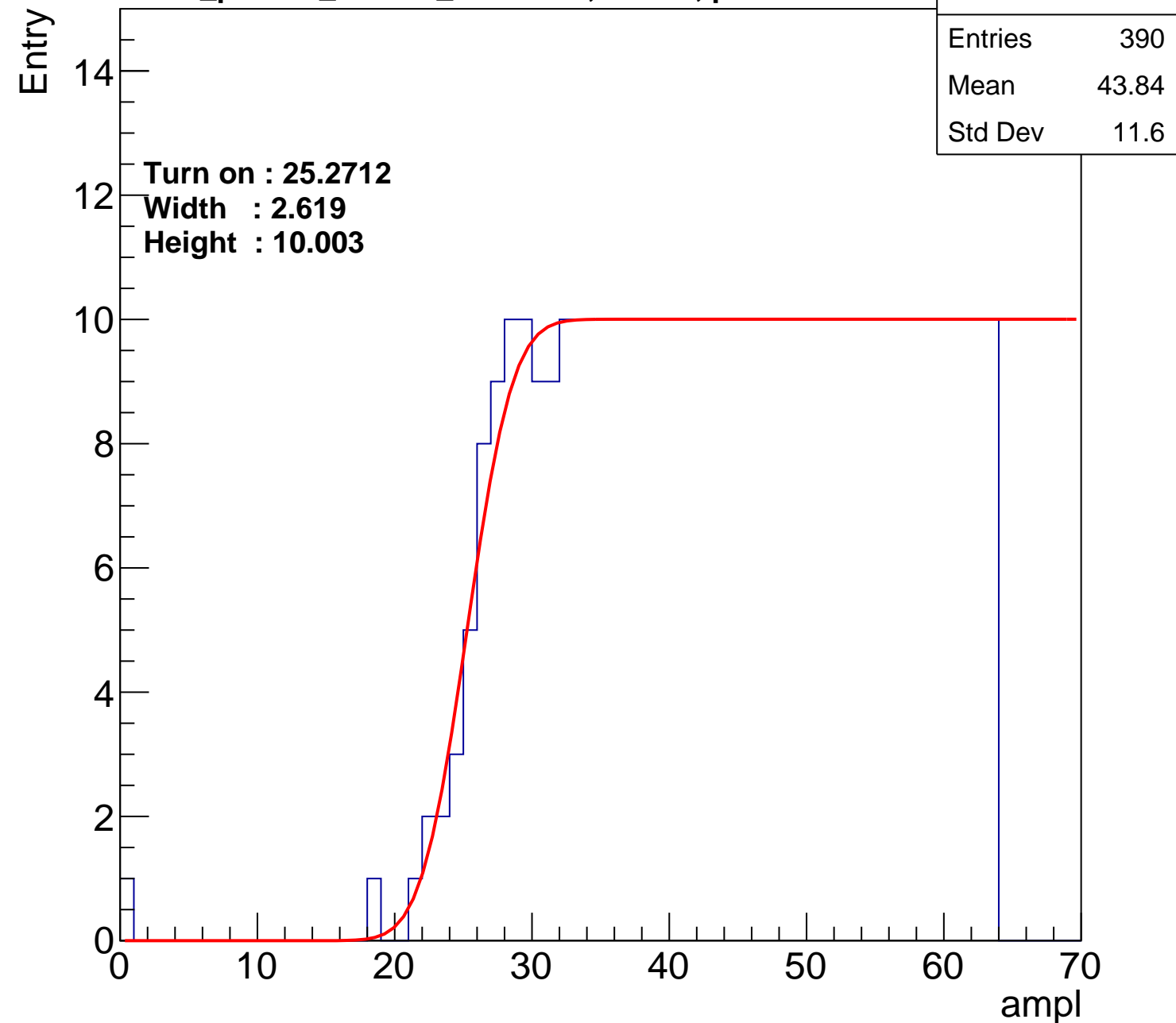
Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U15-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	363
Mean	45.02
Std Dev	11.29

Turn on : 28.4074

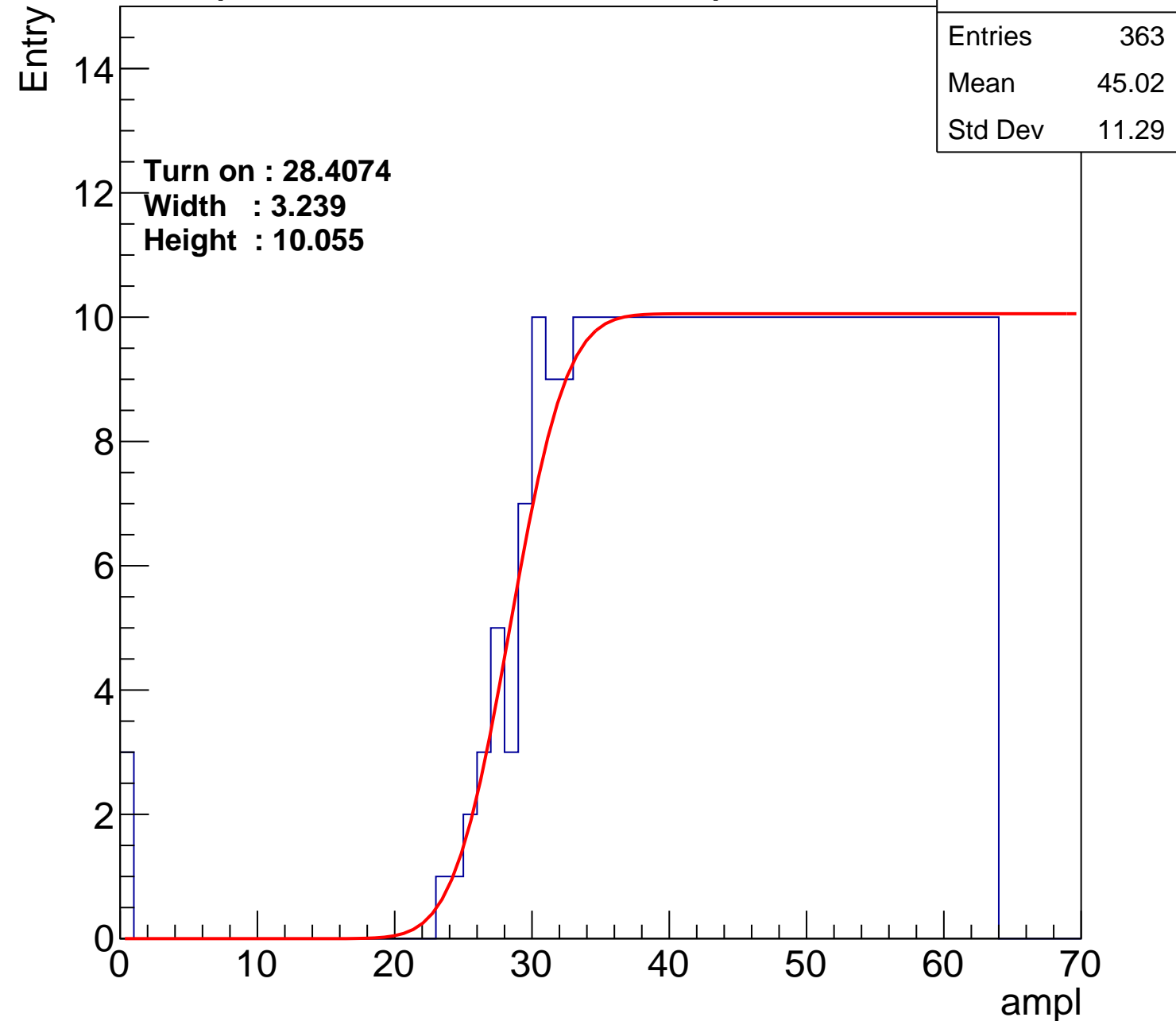
Width : 3.239

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.1249

Width : 2.785

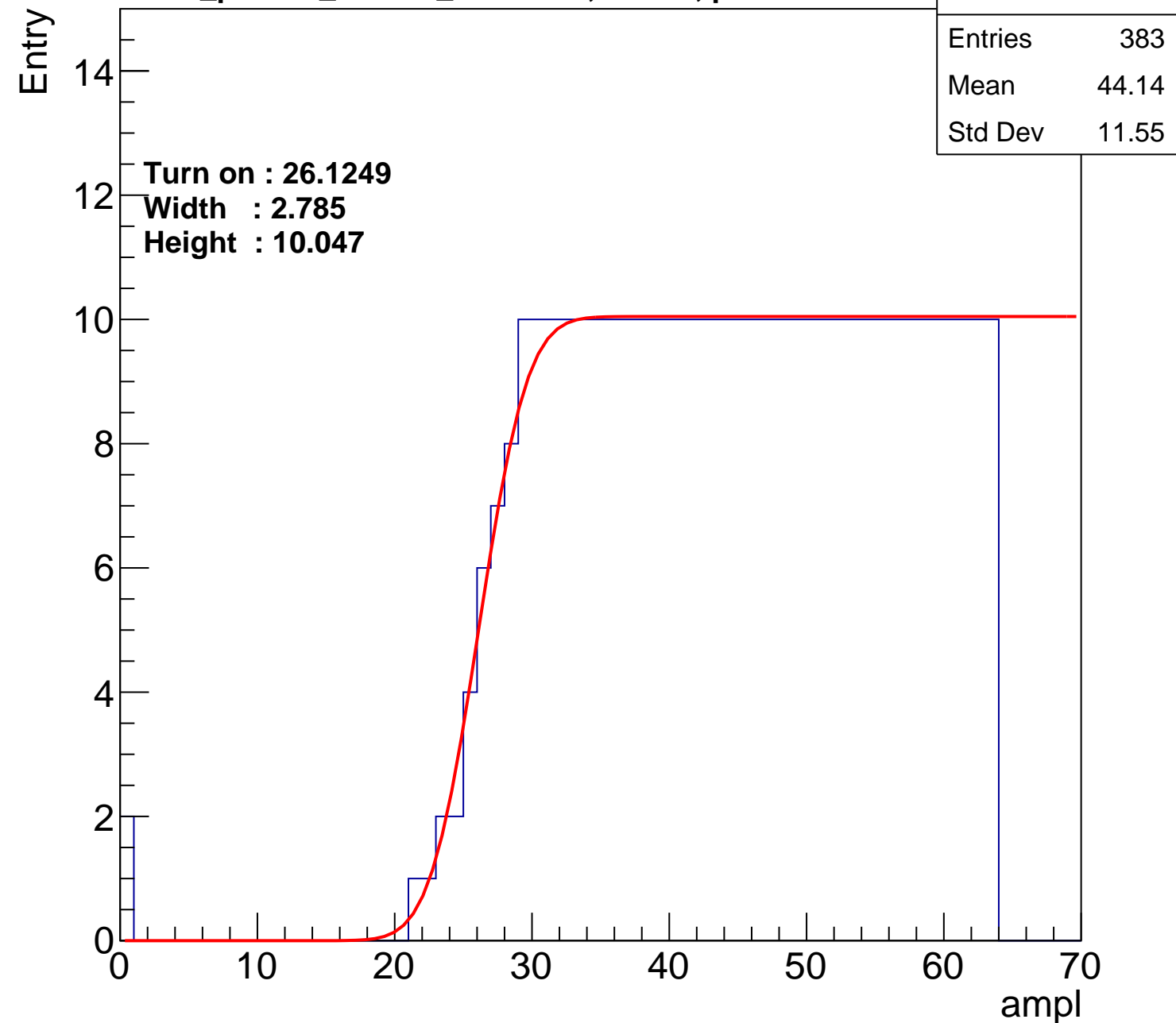
Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U15-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.91
Std Dev	11.66

Turn on : 25.3822

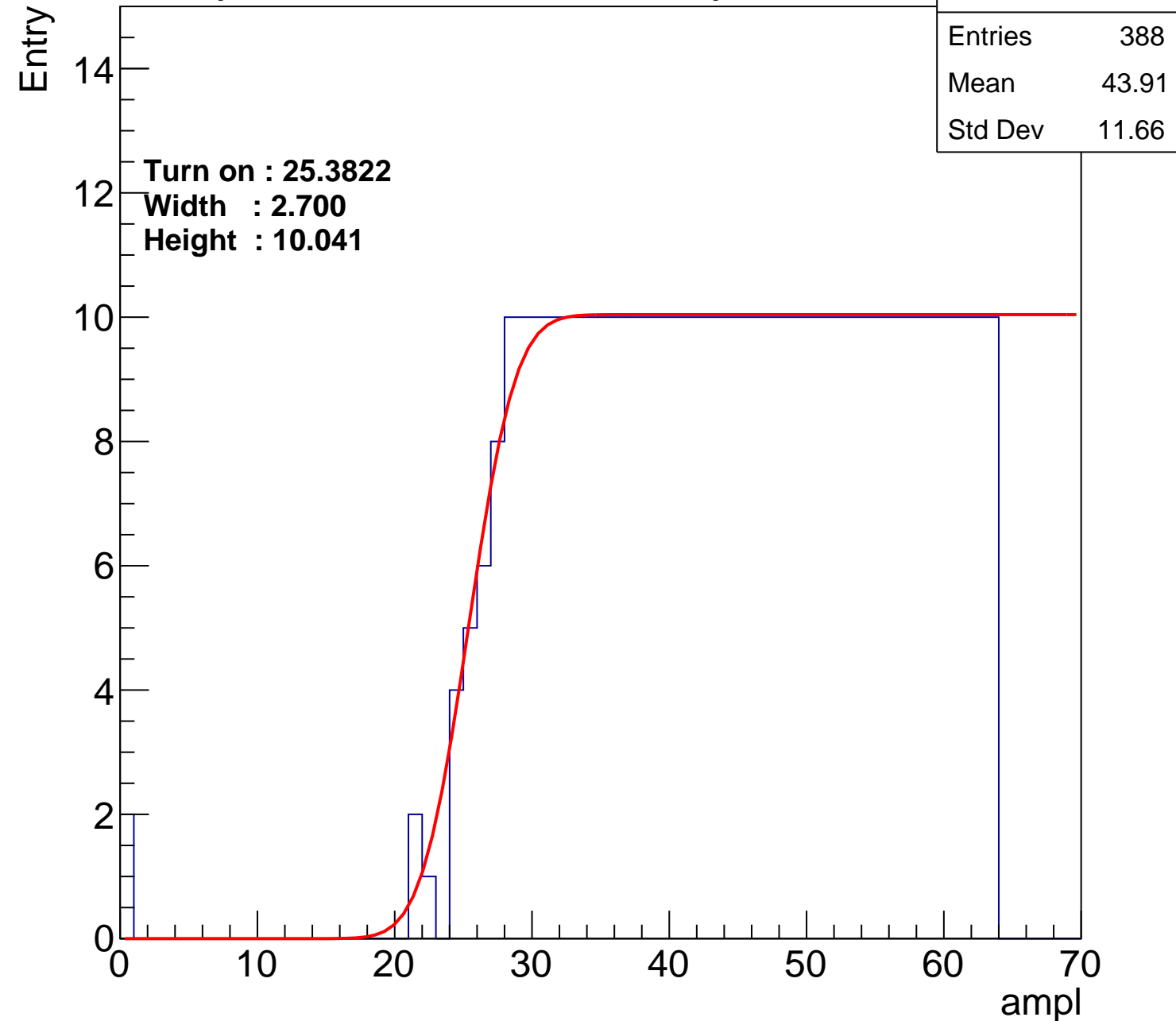
Width : 2.700

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 26.8549

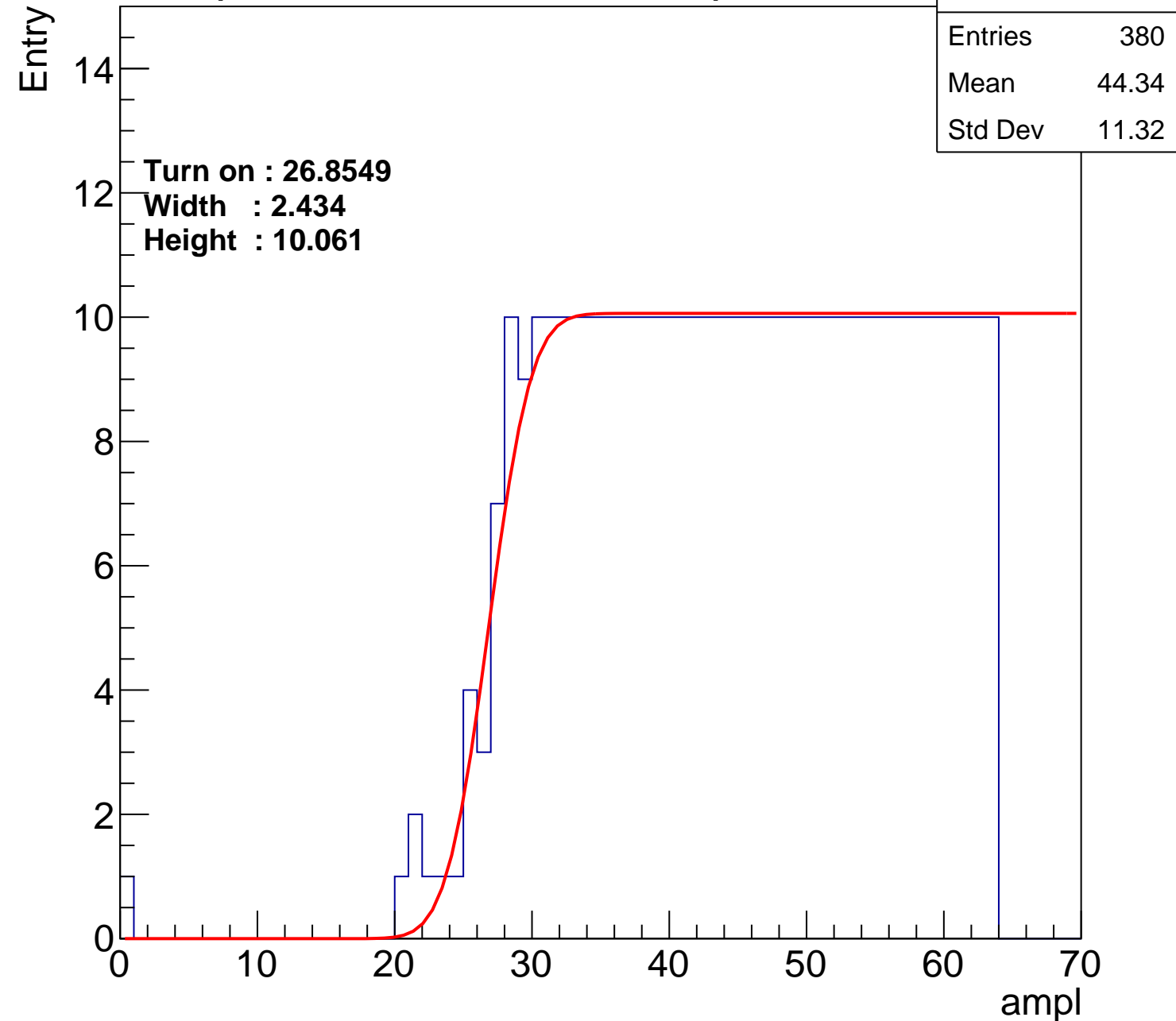
Width : 2.434

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.73
Std Dev	12.02

Turn on : 25.4935

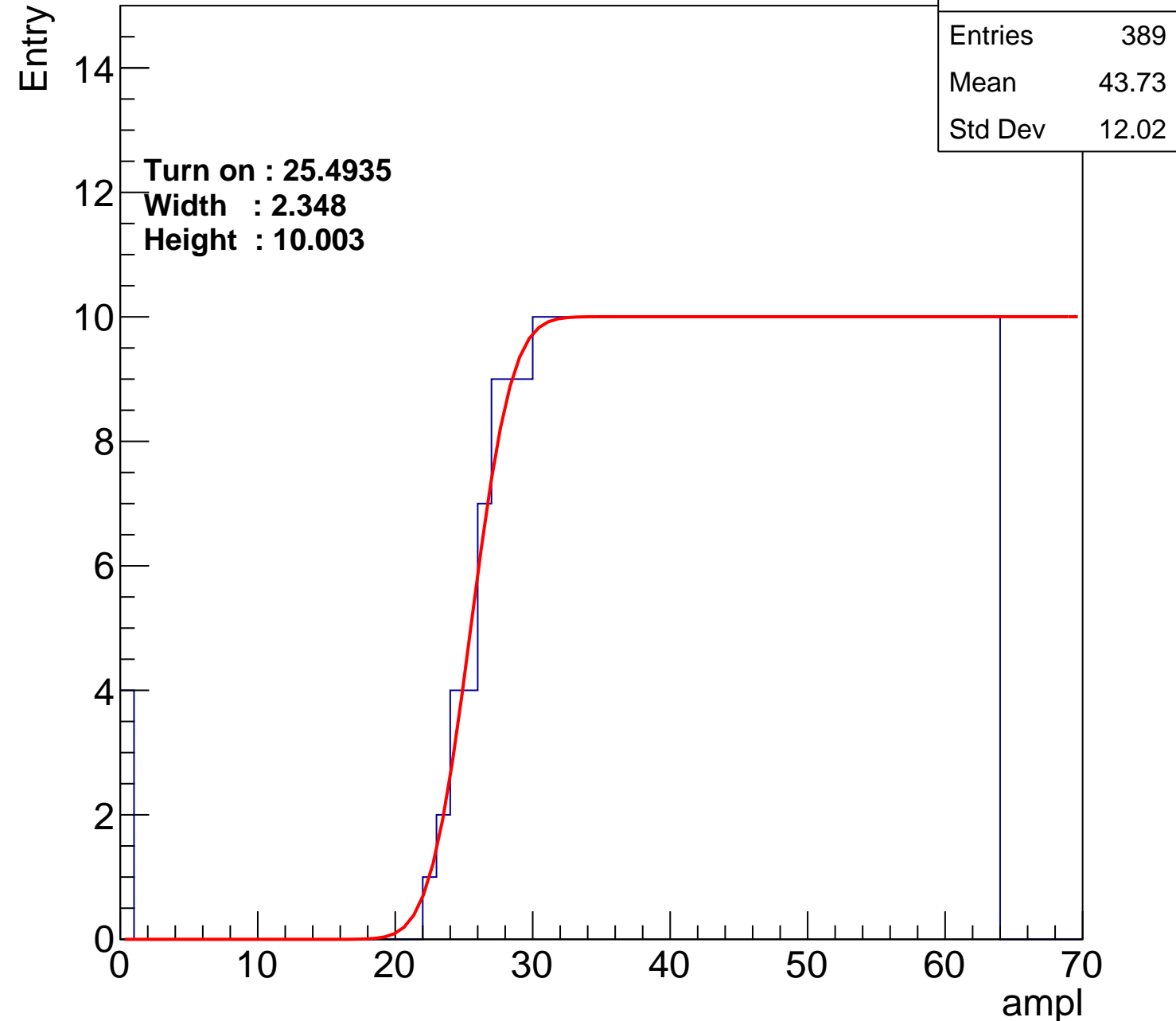
Width : 2.348

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch40

calib_packv5_042523_0143.root, FC#10, port B3

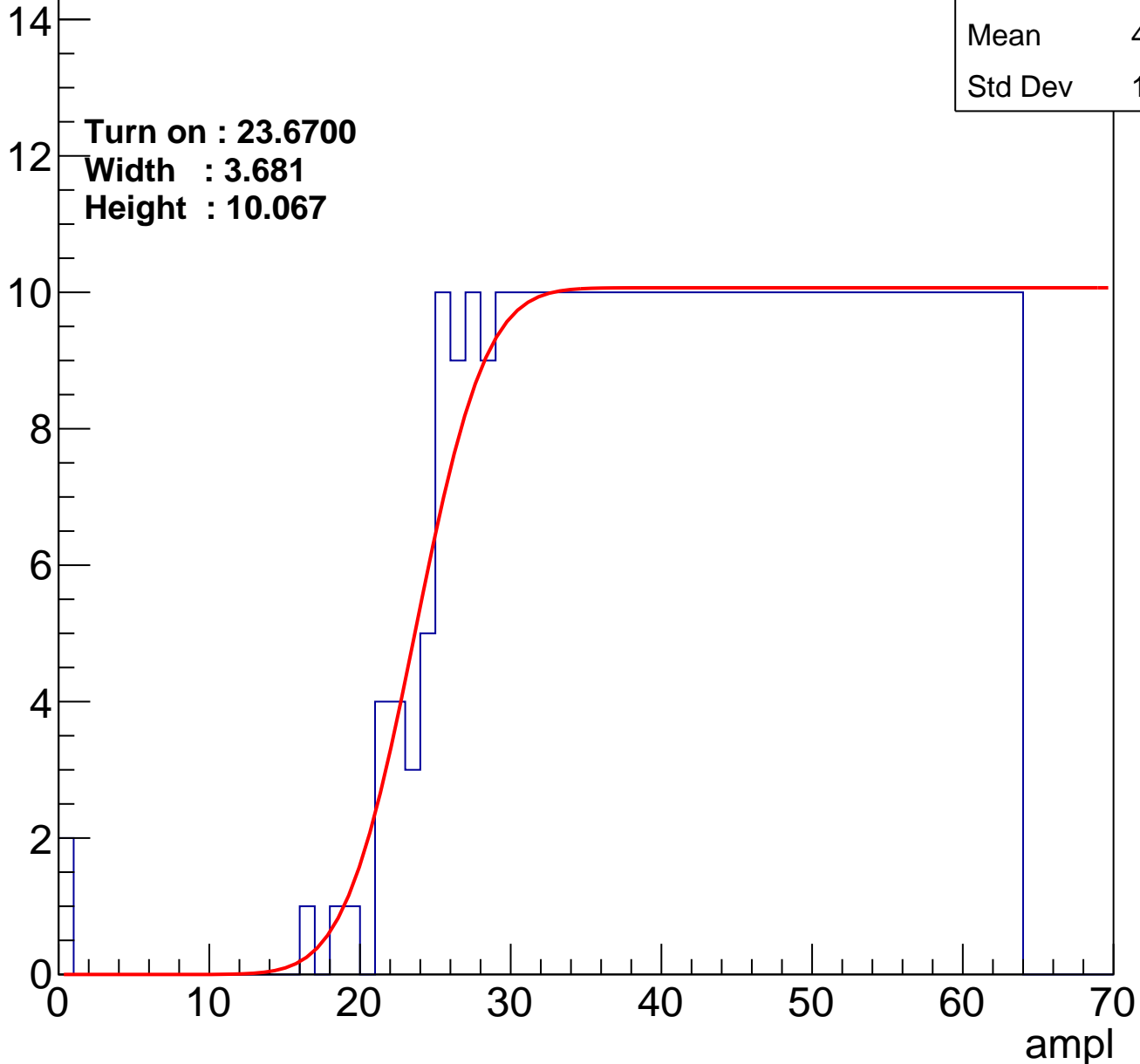
Entries	409
Mean	42.84
Std Dev	12.27

Turn on : 23.6700

Width : 3.681

Height : 10.067

Entry



B1L002S, U15-ch41

calib_packv5_042523_0143.root, FC#10, port B3

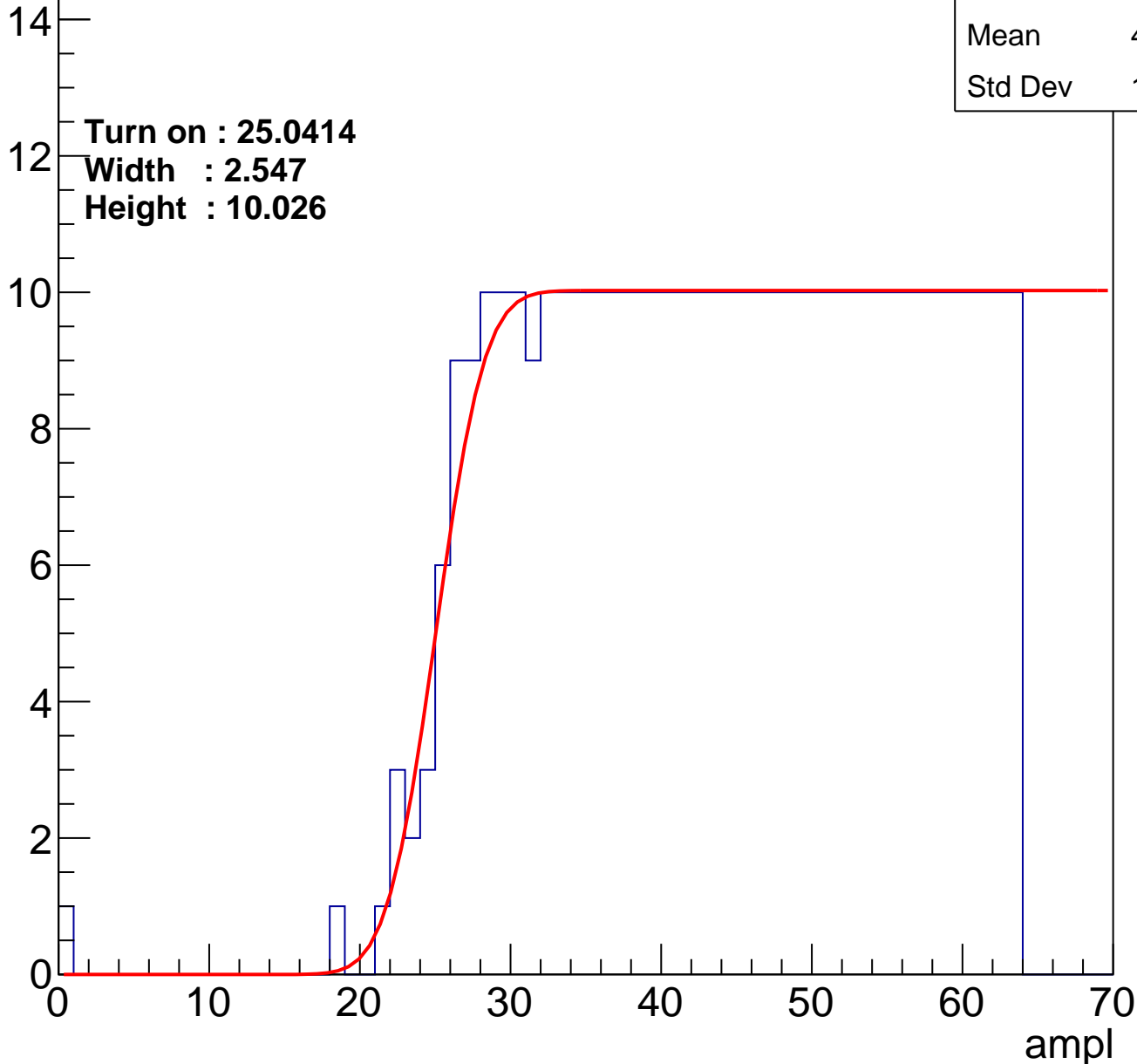
Entries	394
Mean	43.65
Std Dev	11.69

Turn on : 25.0414

Width : 2.547

Height : 10.026

Entry



B1L002S, U15-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.87
Std Dev	11.04

Turn on : 27.3505

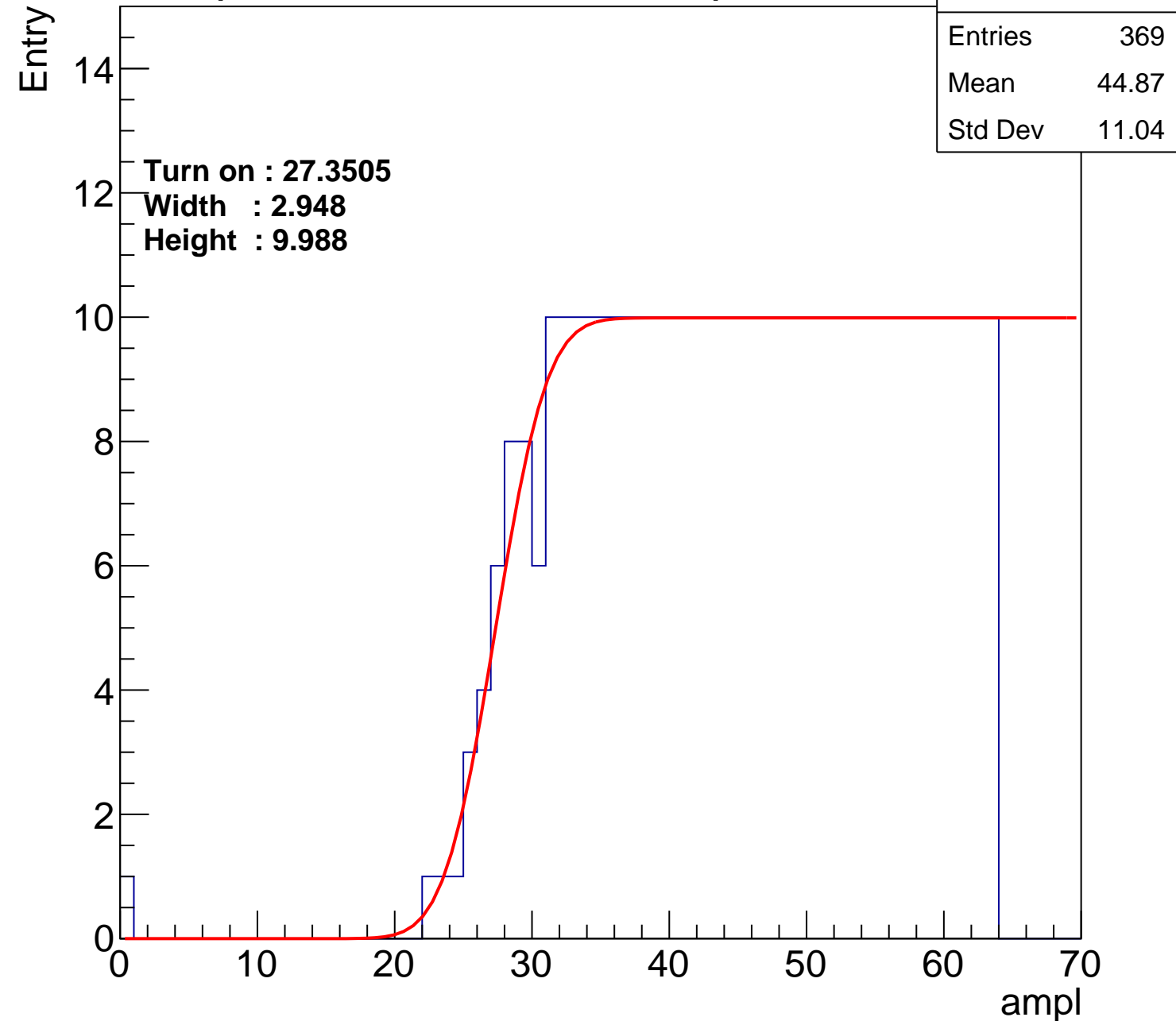
Width : 2.948

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	44
Std Dev	11.49

Turn on : 25.4697

Width : 2.326

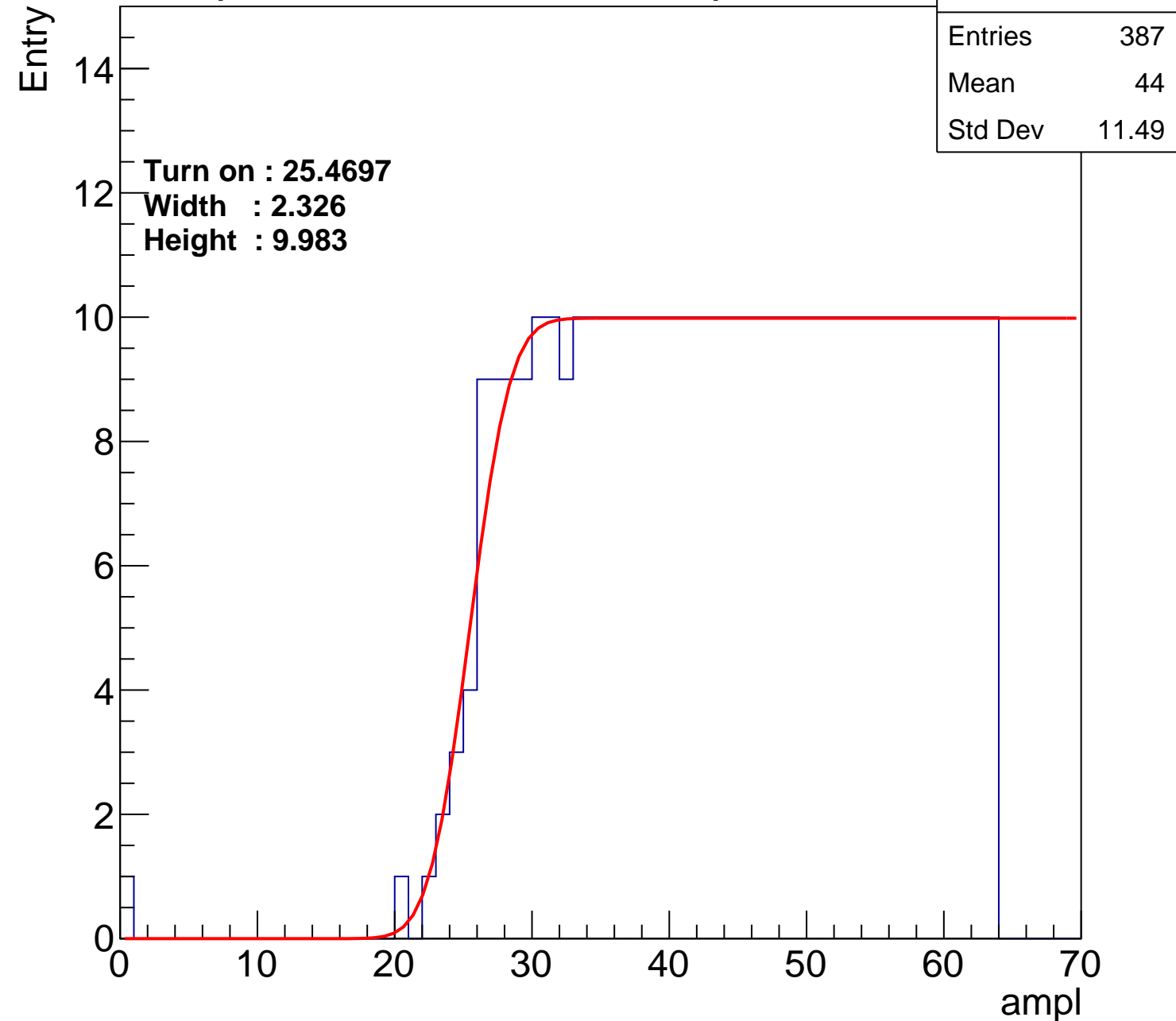
Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U15-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.16
Std Dev	11.38

Turn on : 25.6997

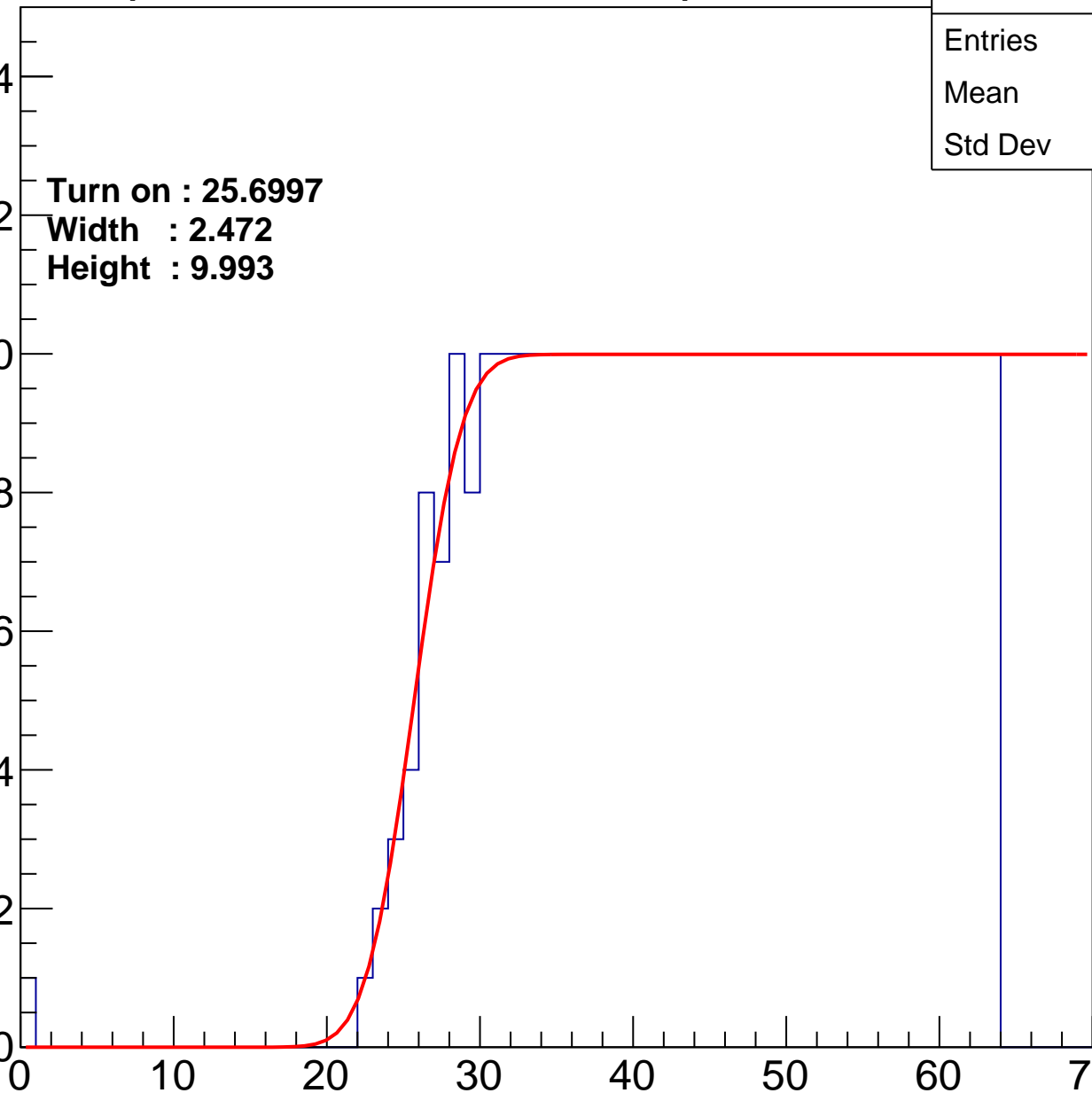
Width : 2.472

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.35
Std Dev	11.85

Turn on : 24.0956

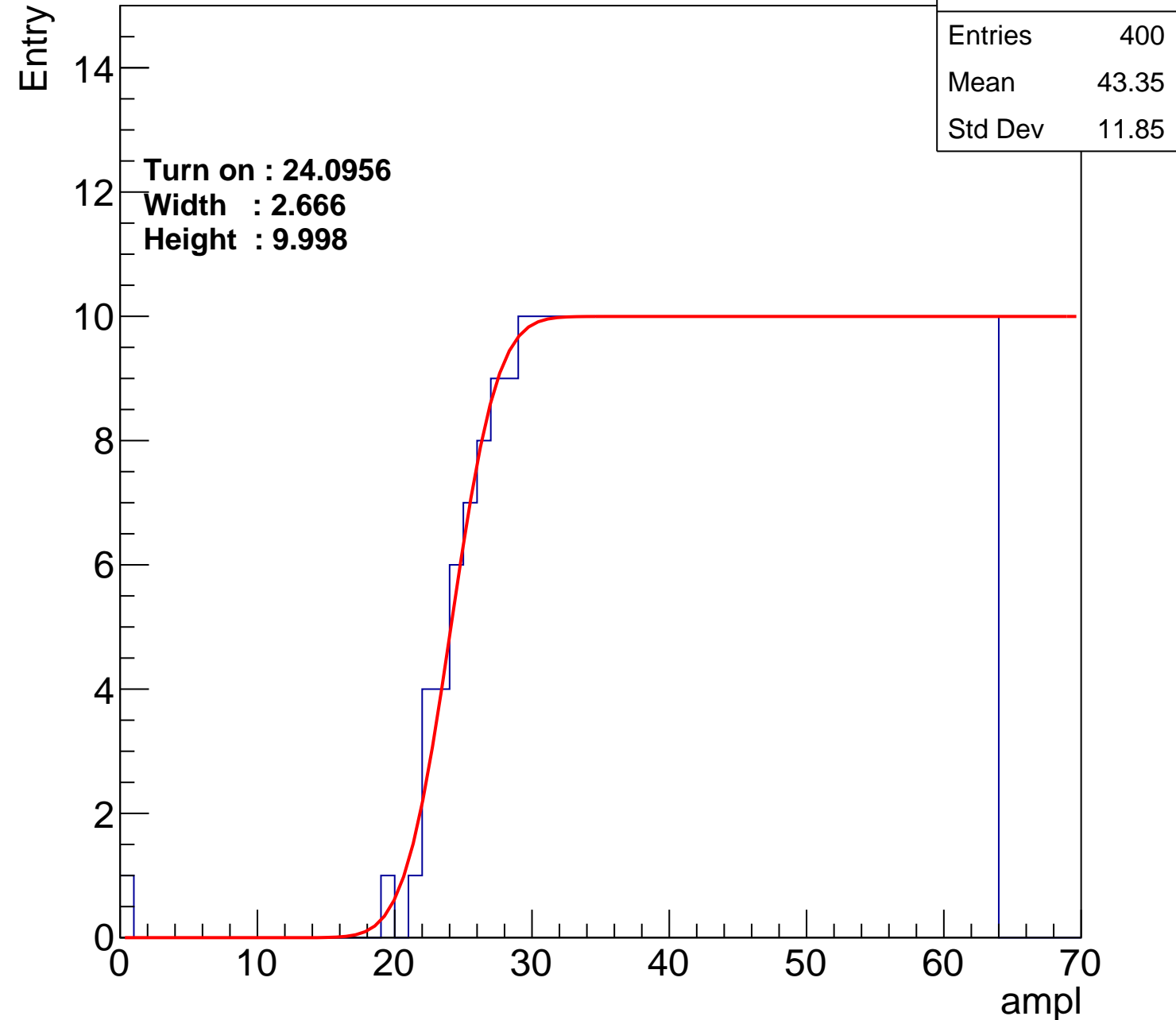
Width : 2.666

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.54
Std Dev	12

Turn on : 25.0520

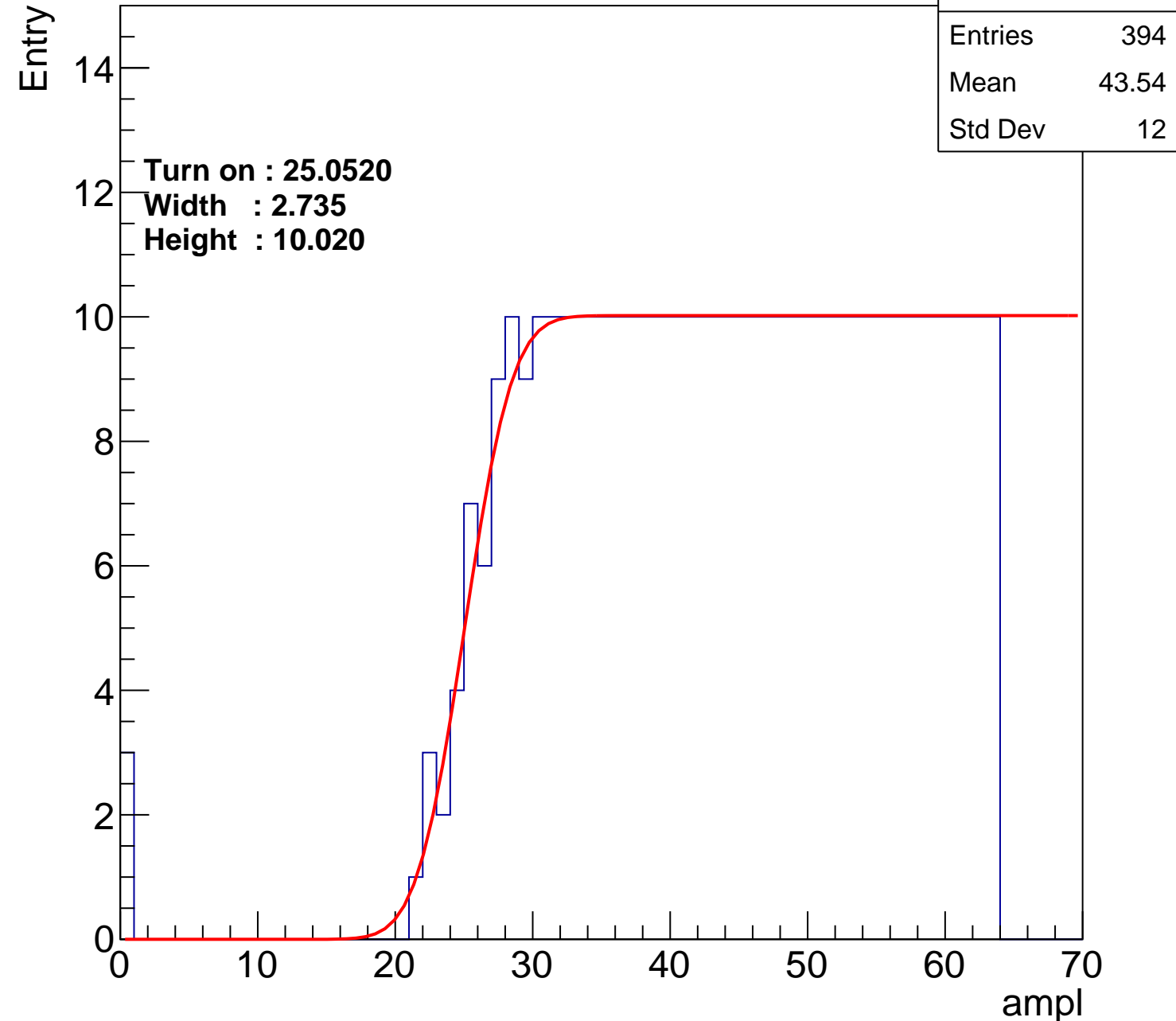
Width : 2.735

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch47

calib_packv5_042523_0143.root, FC#10, port B3

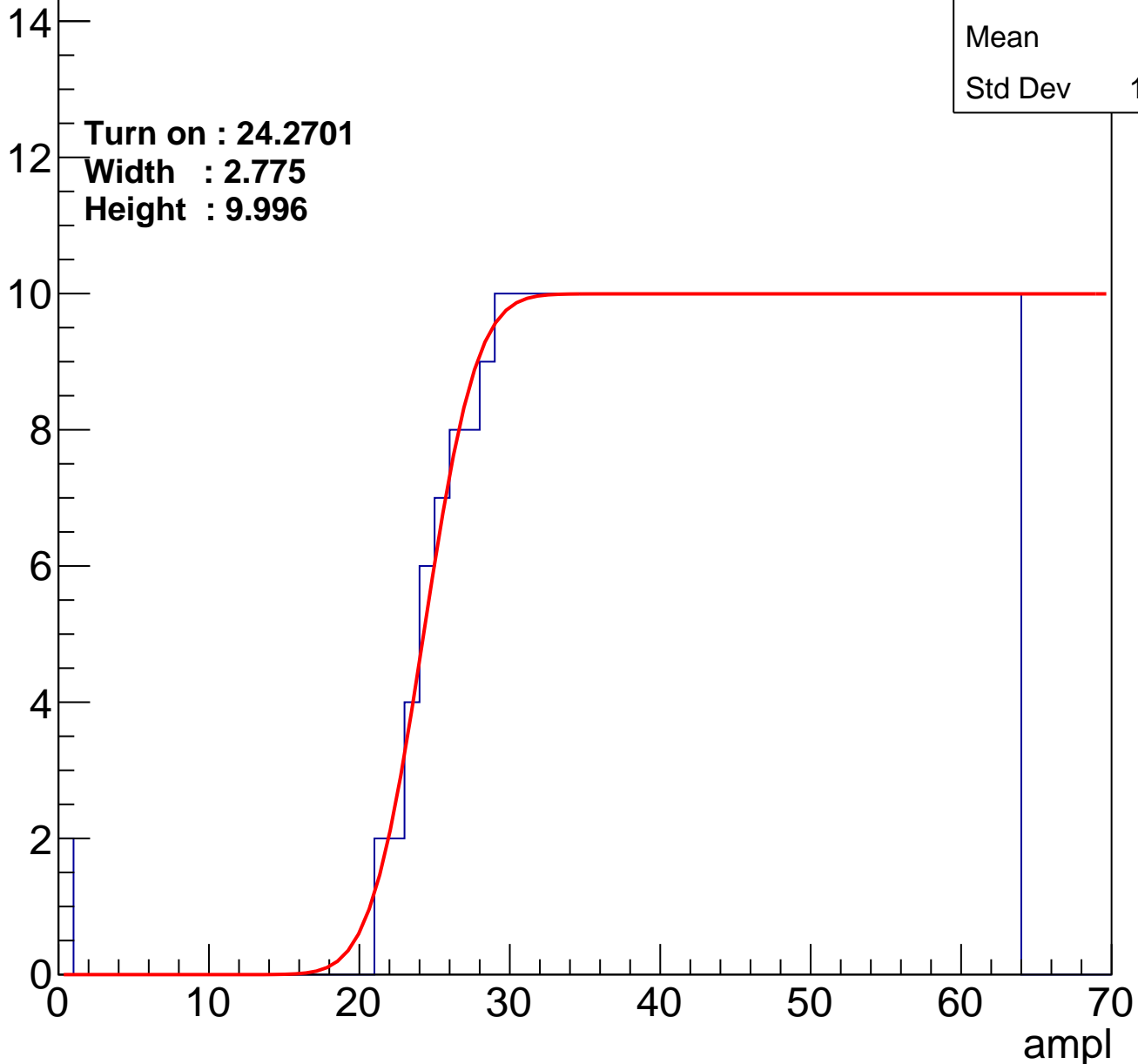
Entries	398
Mean	43.4
Std Dev	11.94

Turn on : 24.2701

Width : 2.775

Height : 9.996

Entry



B1L002S, U15-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.2862

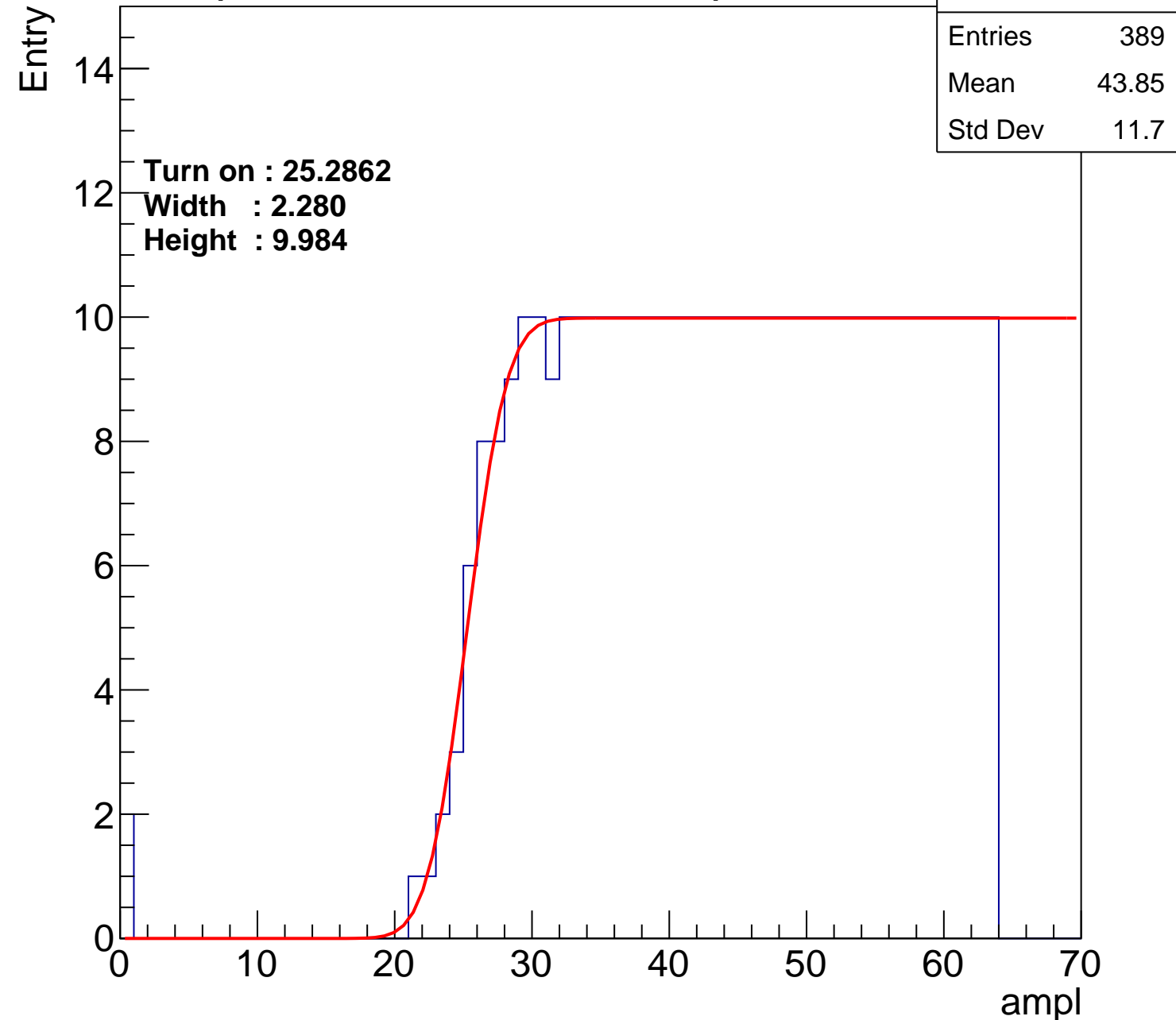
Width : 2.280

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch49

calib_packv5_042523_0143.root, FC#10, port B3

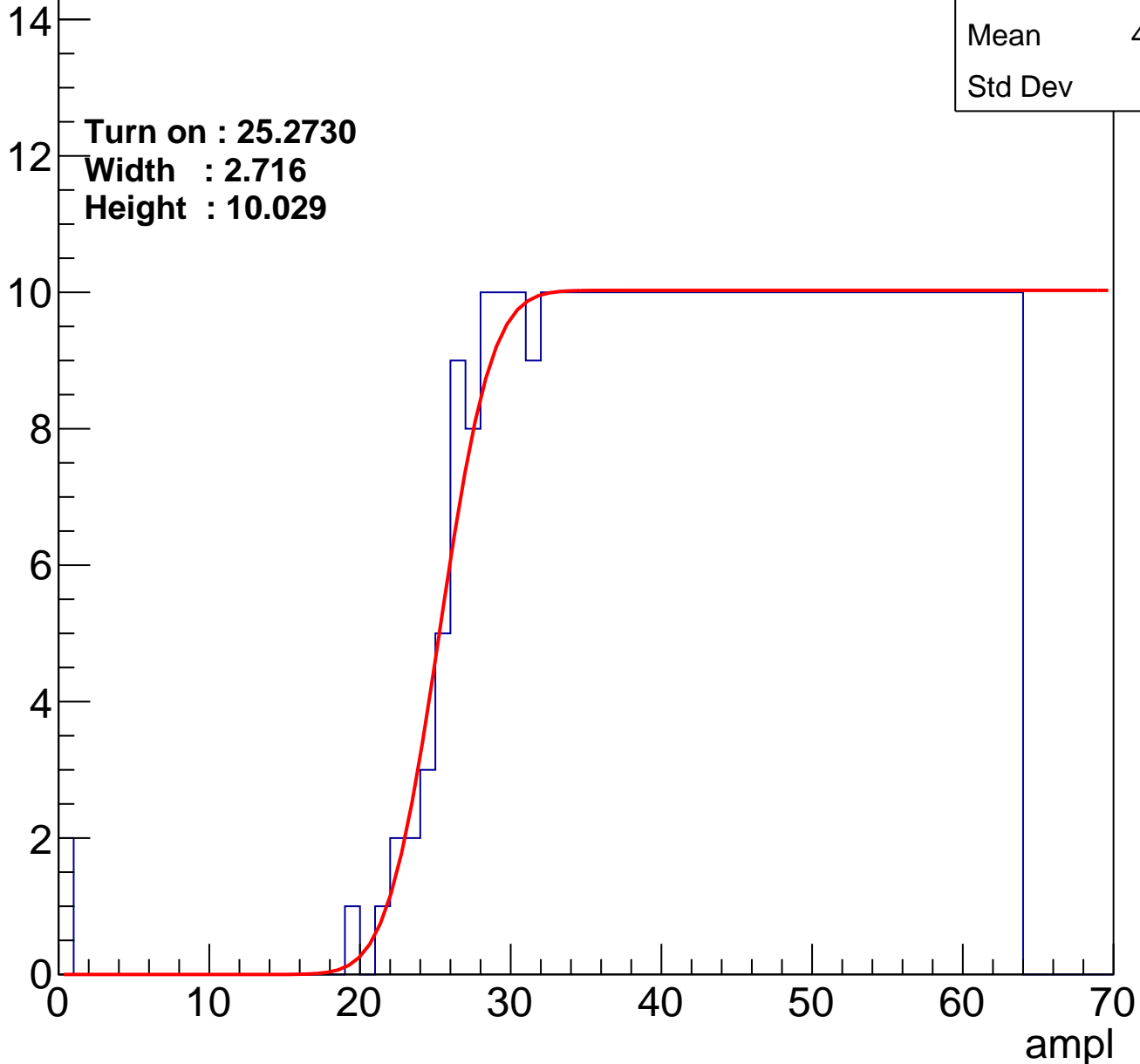
Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.2730

Width : 2.716

Height : 10.029

Entry



B1L002S, U15-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.47
Std Dev	11.89

Turn on : 24.4208

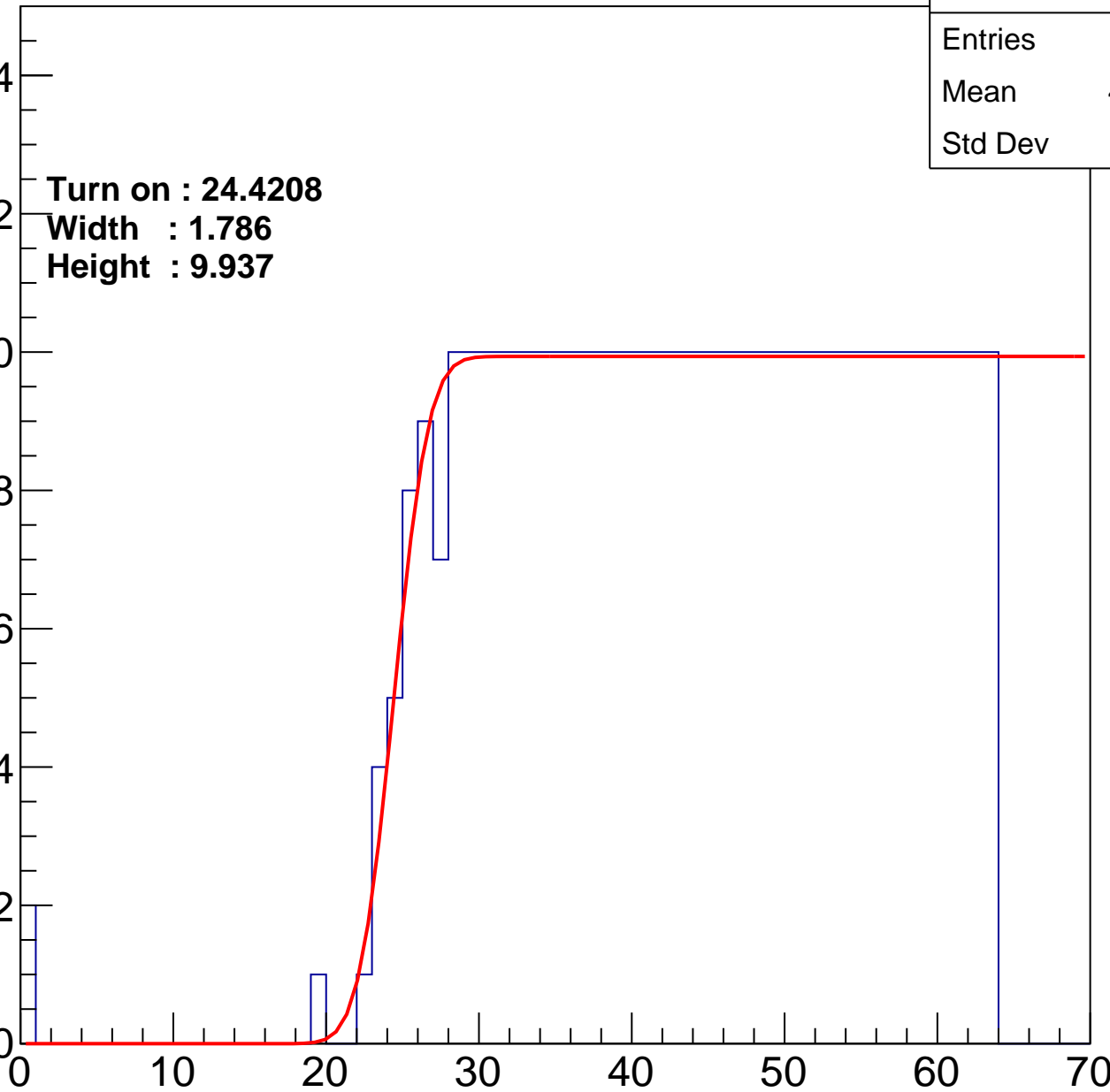
Width : 1.786

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.95
Std Dev	11.53

Turn on : 25.8688

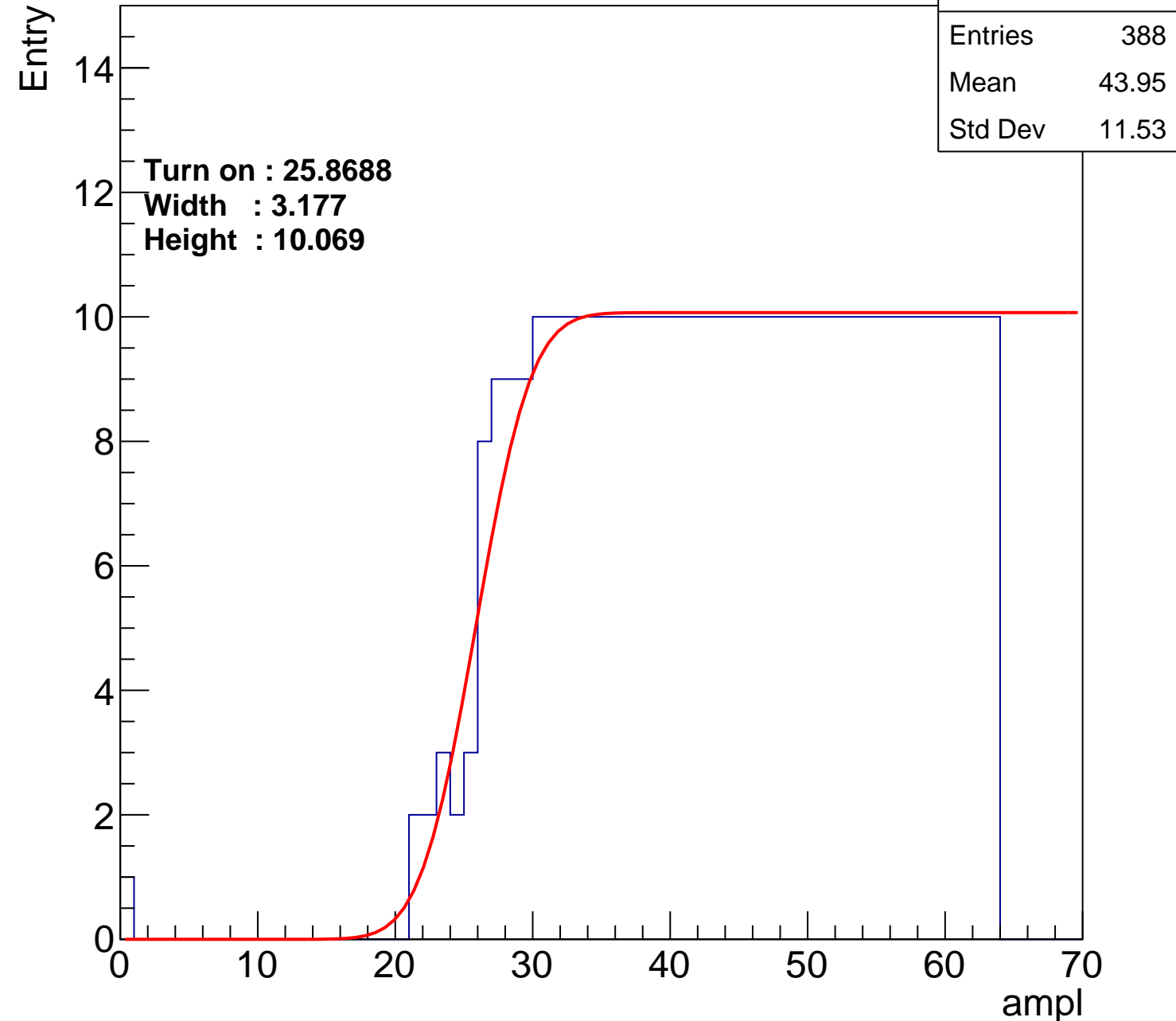
Width : 3.177

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.47
Std Dev	12.16

Turn on : 24.9905

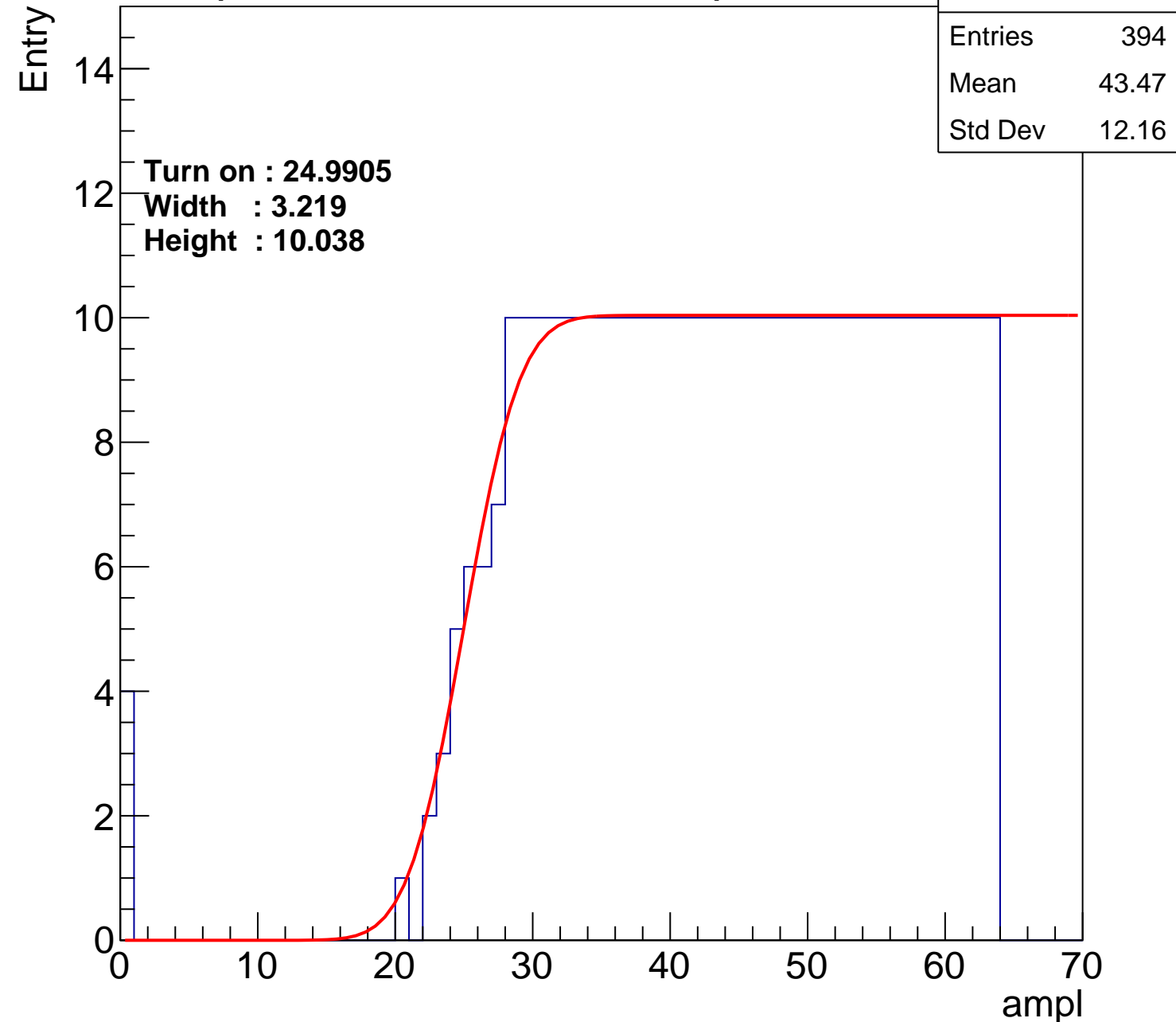
Width : 3.219

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl

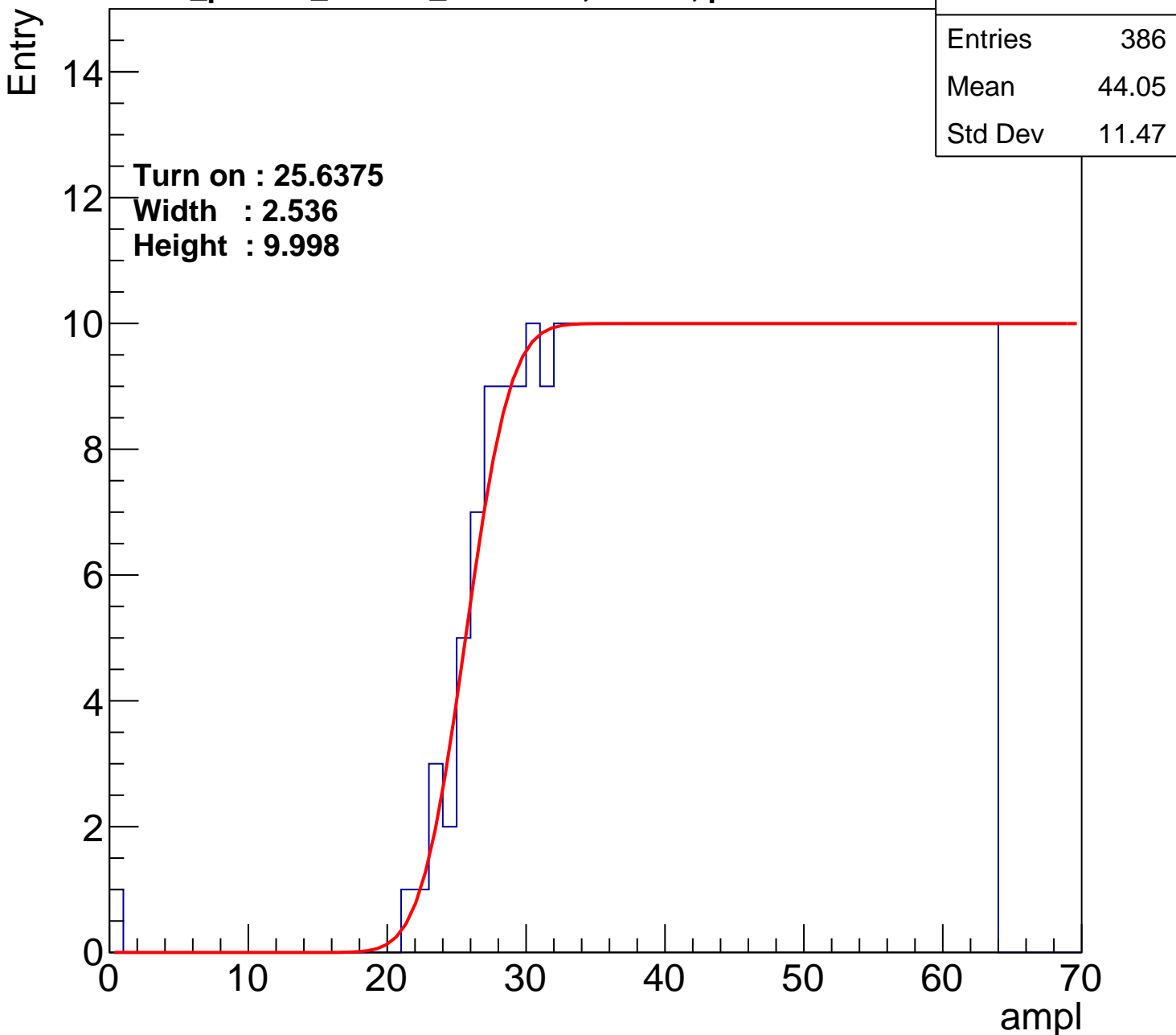


calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.05
Std Dev	11.47

Height : 9.998



B1L002S, U15-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.82
Std Dev	11.21

Turn on : 27.3934

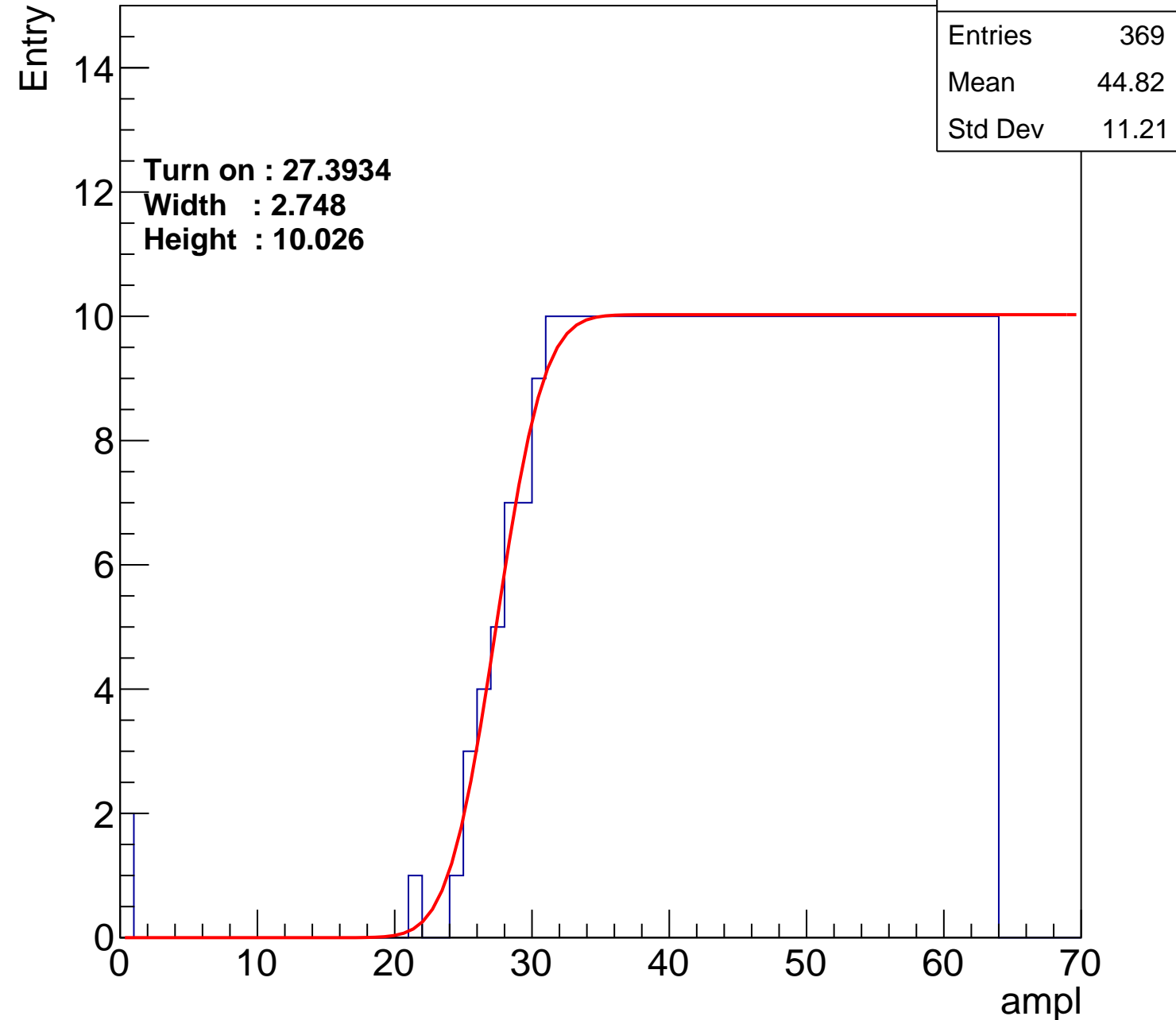
Width : 2.748

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch55

calib_packv5_042523_0143.root, FC#10, port B3

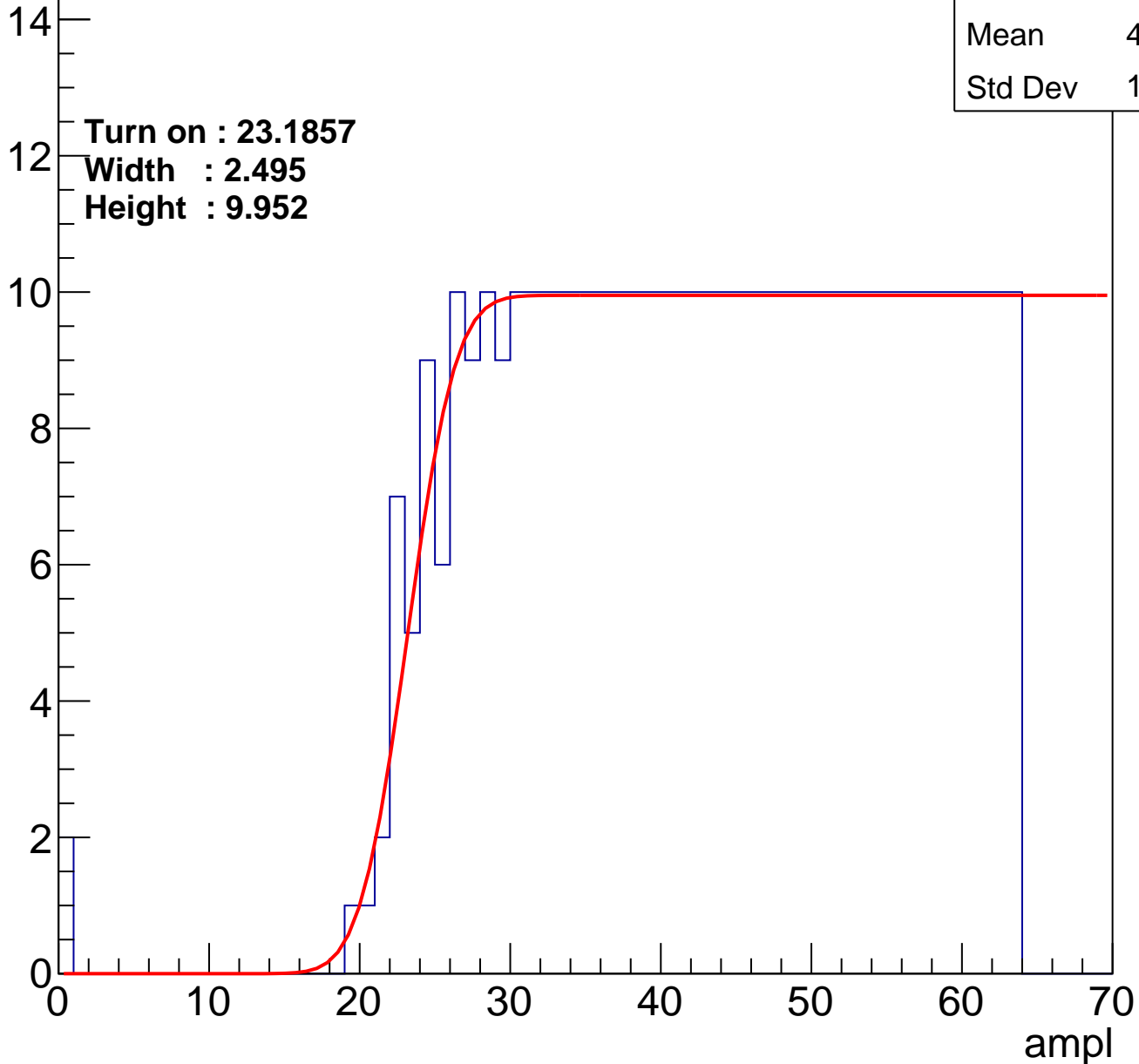
Entries	411
Mean	42.75
Std Dev	12.29

Turn on : 23.1857

Width : 2.495

Height : 9.952

Entry



B1L002S, U15-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.46
Std Dev	11.78

Turn on : 24.3590

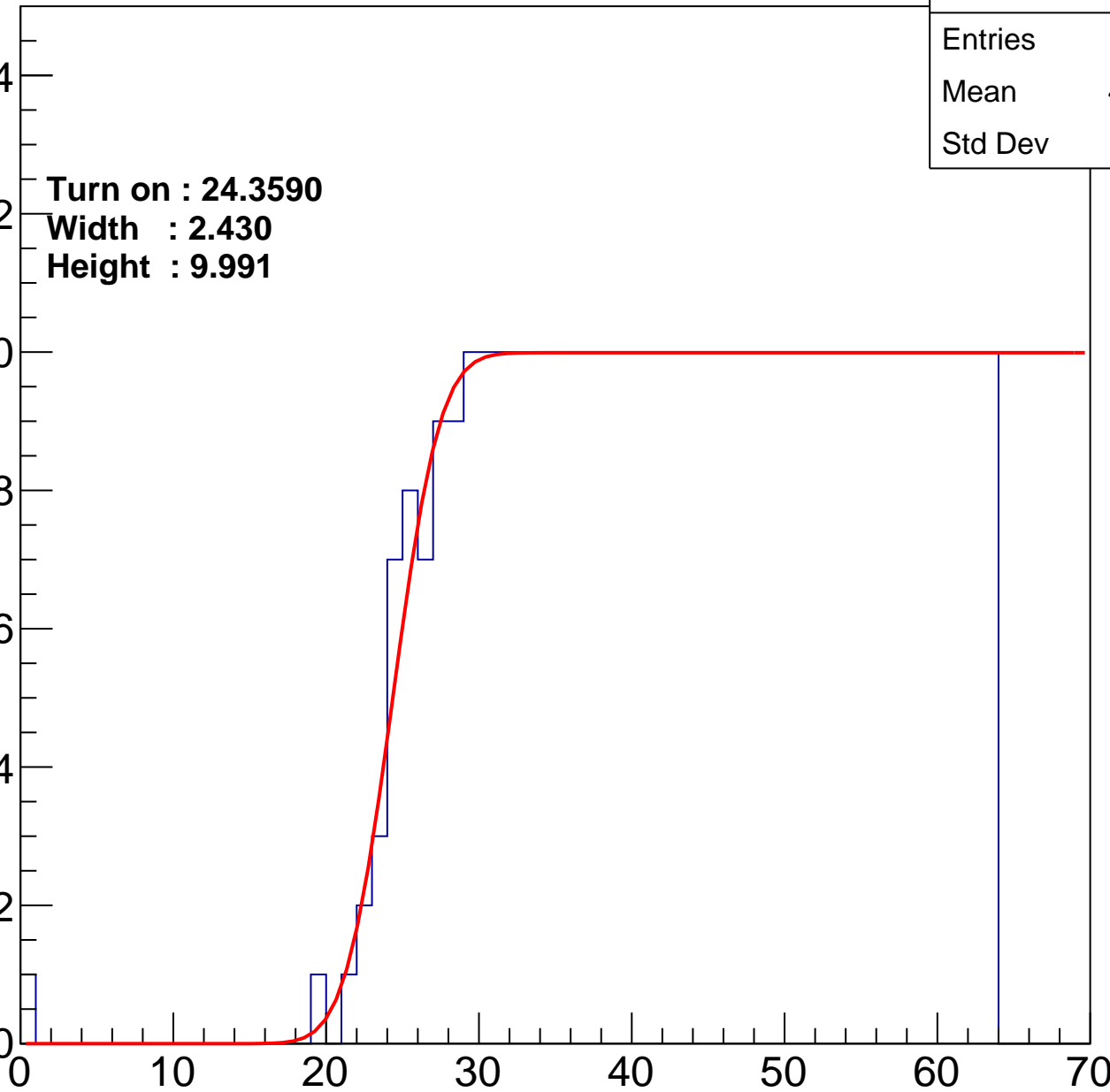
Width : 2.430

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.13
Std Dev	11.62

Turn on : 26.3662

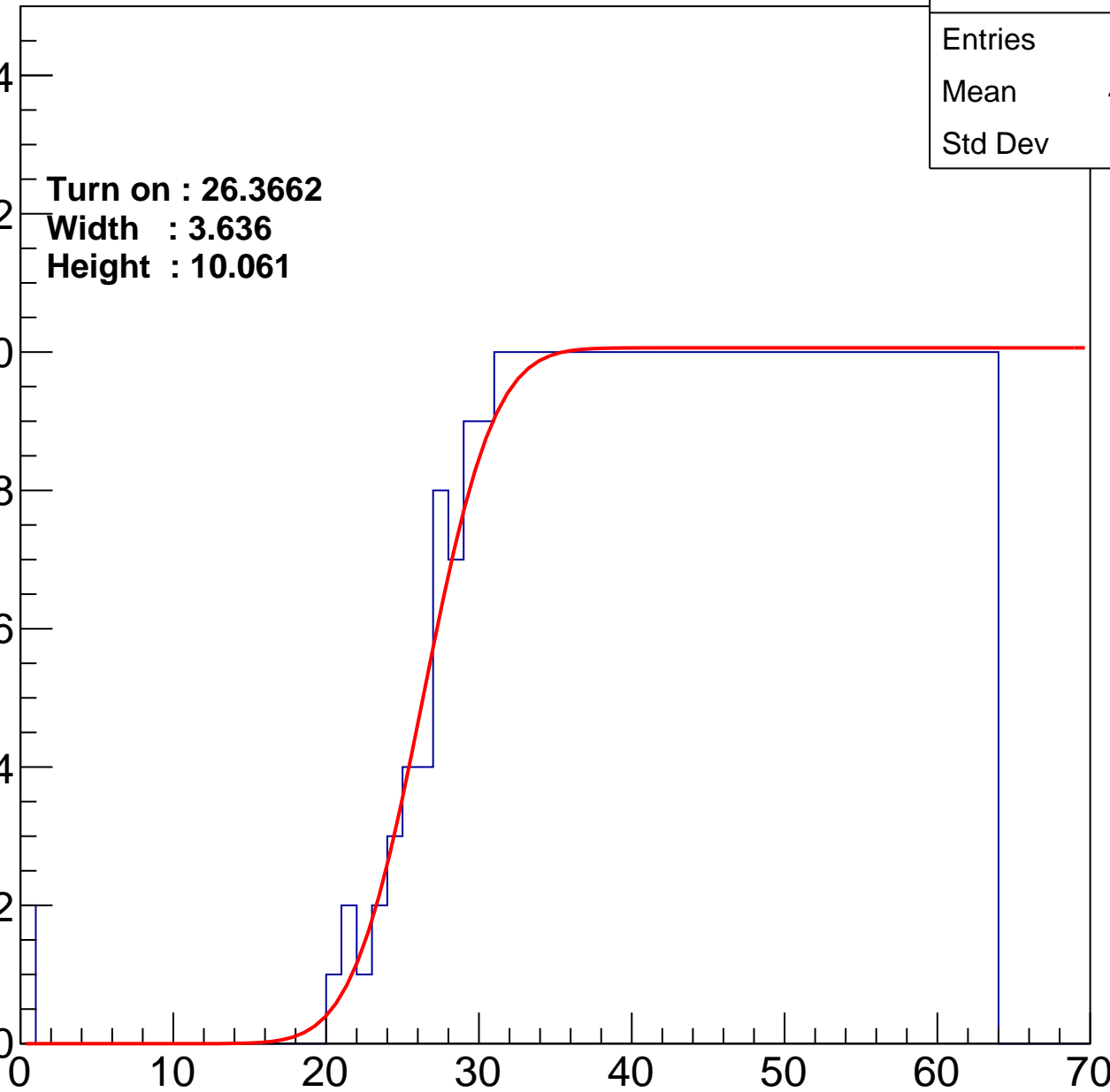
Width : 3.636

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.99
Std Dev	12.29

Turn on : 23.9331

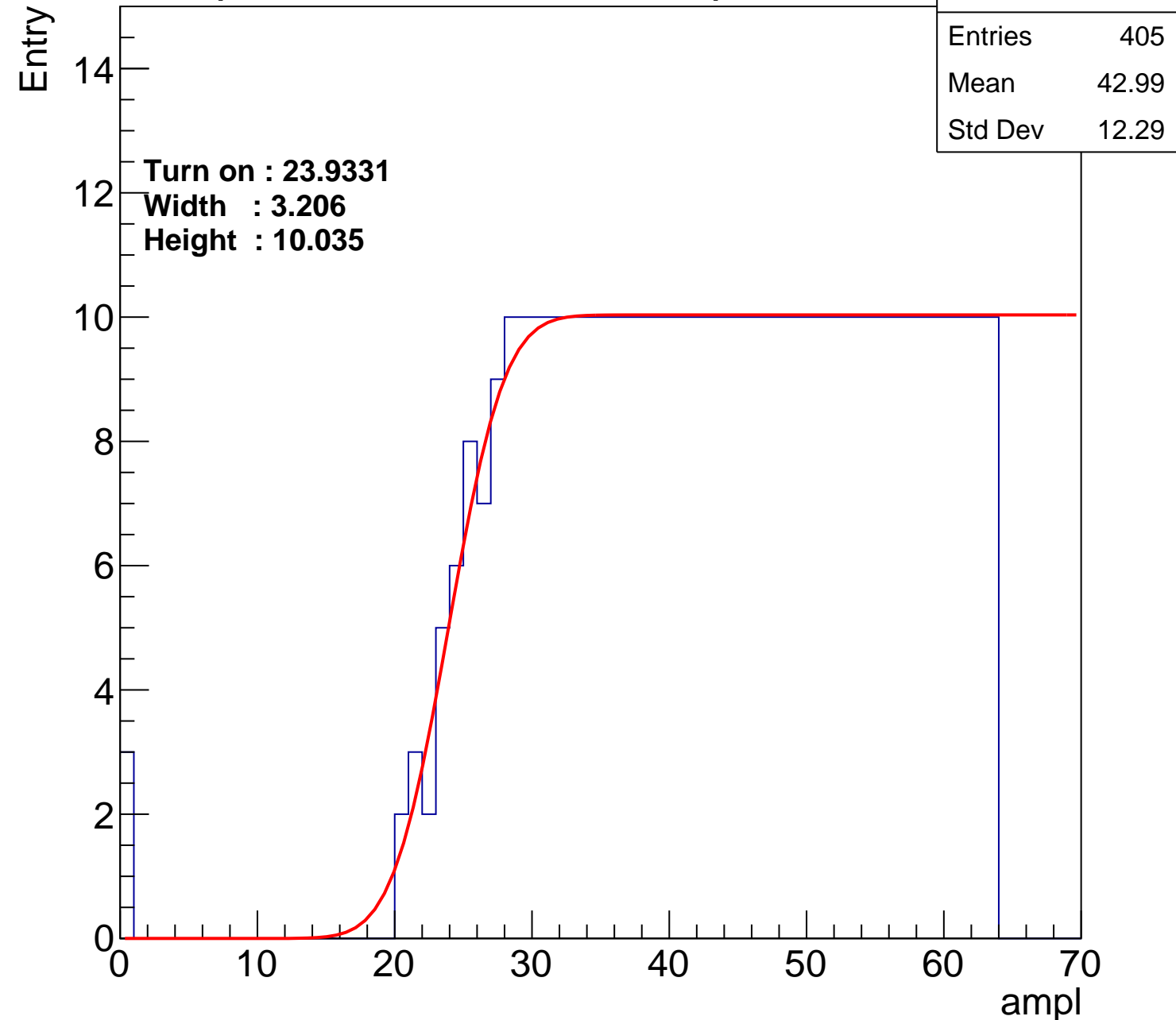
Width : 3.206

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.44
Std Dev	11.94

Turn on : 24.9312

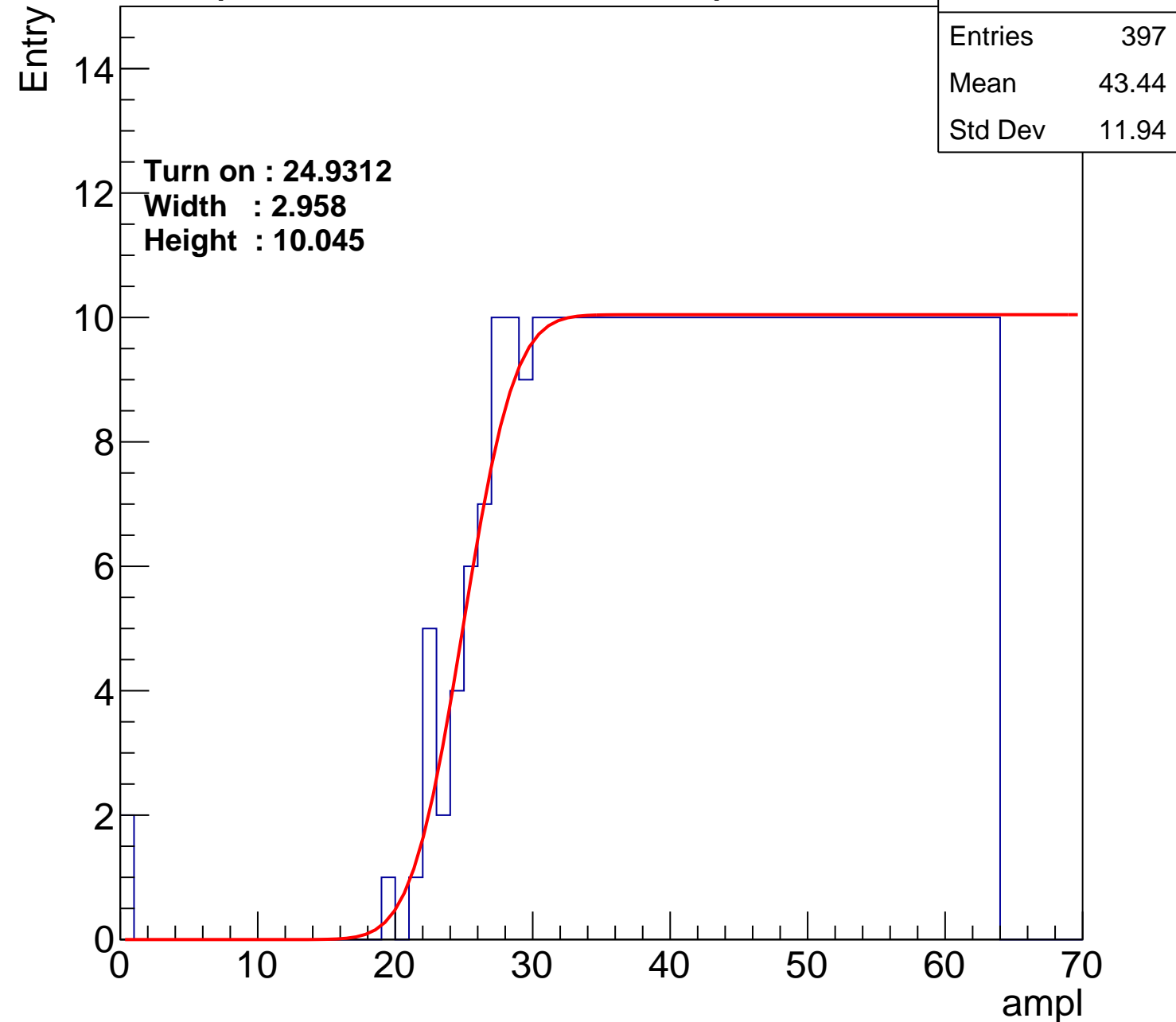
Width : 2.958

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.5
Std Dev	12.01

Turn on : 24.4840

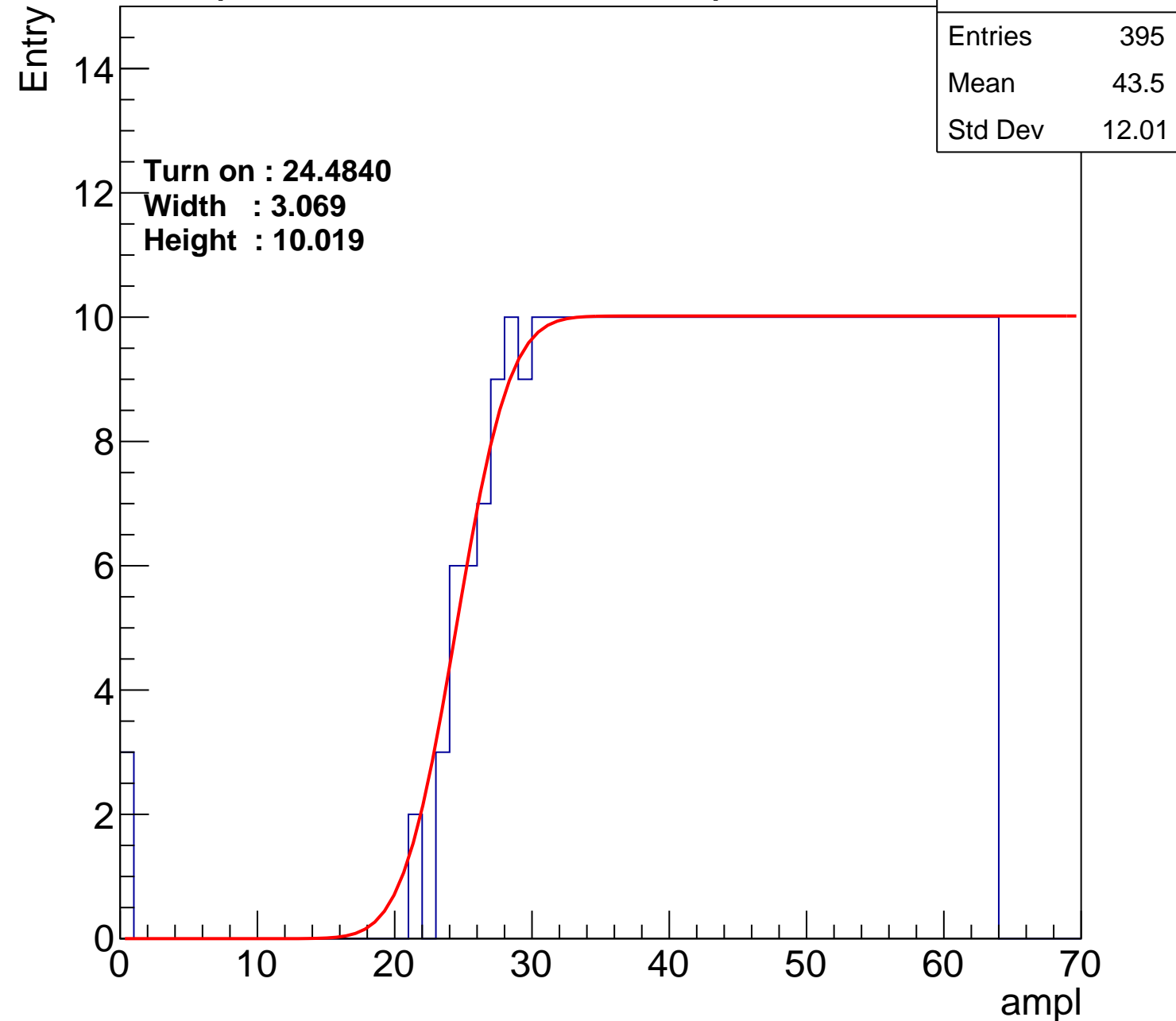
Width : 3.069

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.3009

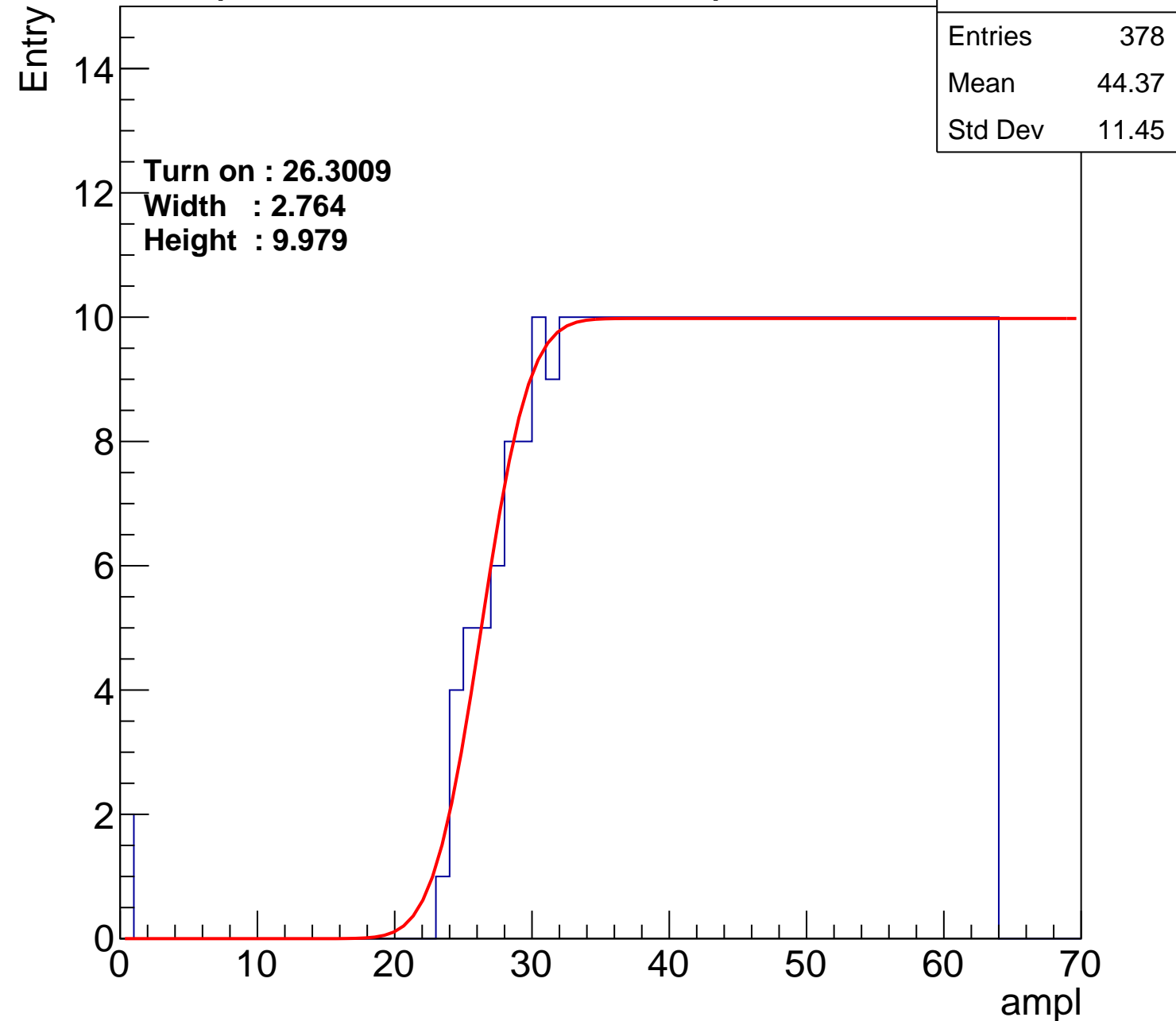
Width : 2.764

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.57
Std Dev	11.82

Turn on : 25.1410

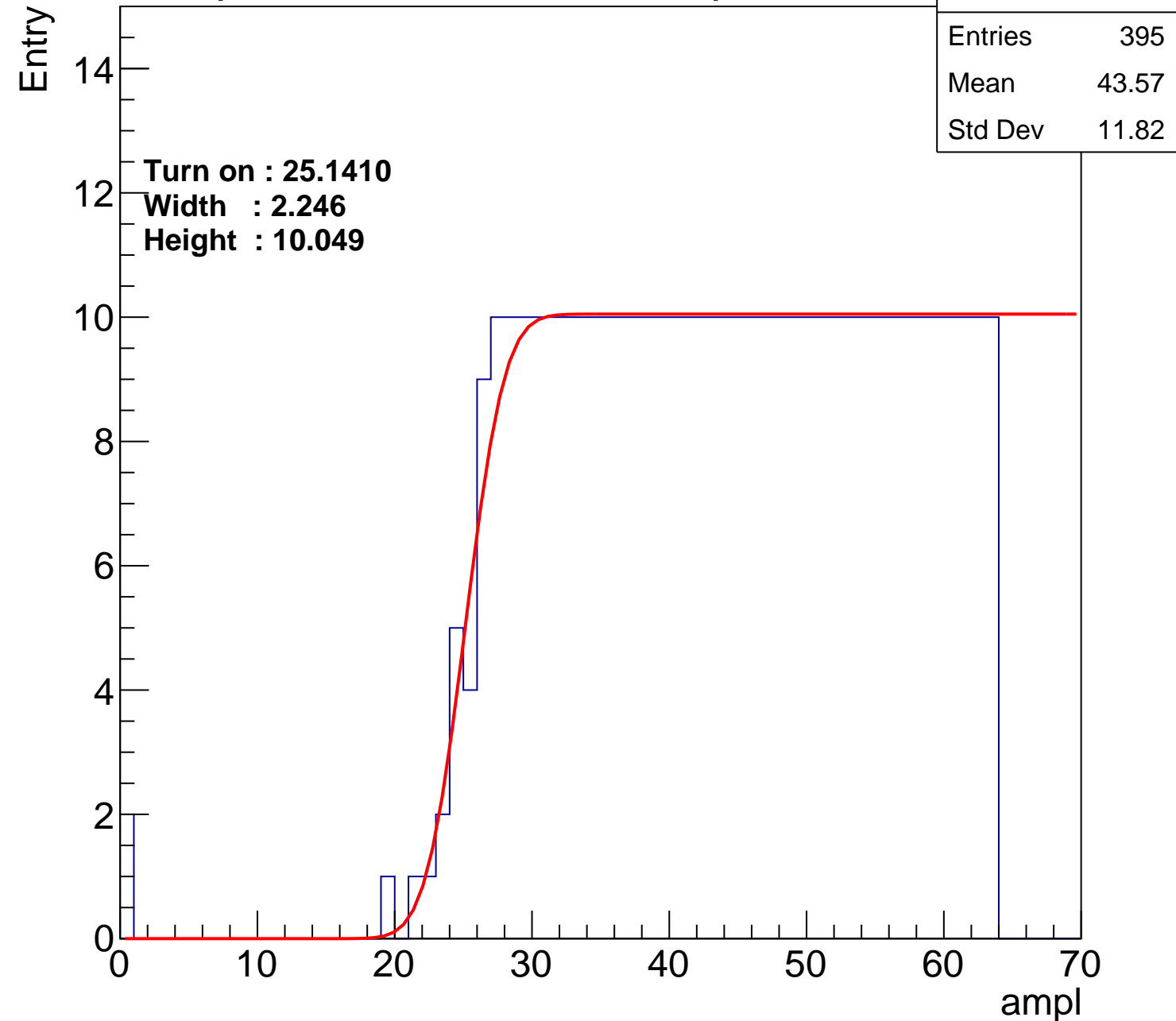
Width : 2.246

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch63

calib_packv5_042523_0143.root, FC#10, port B3

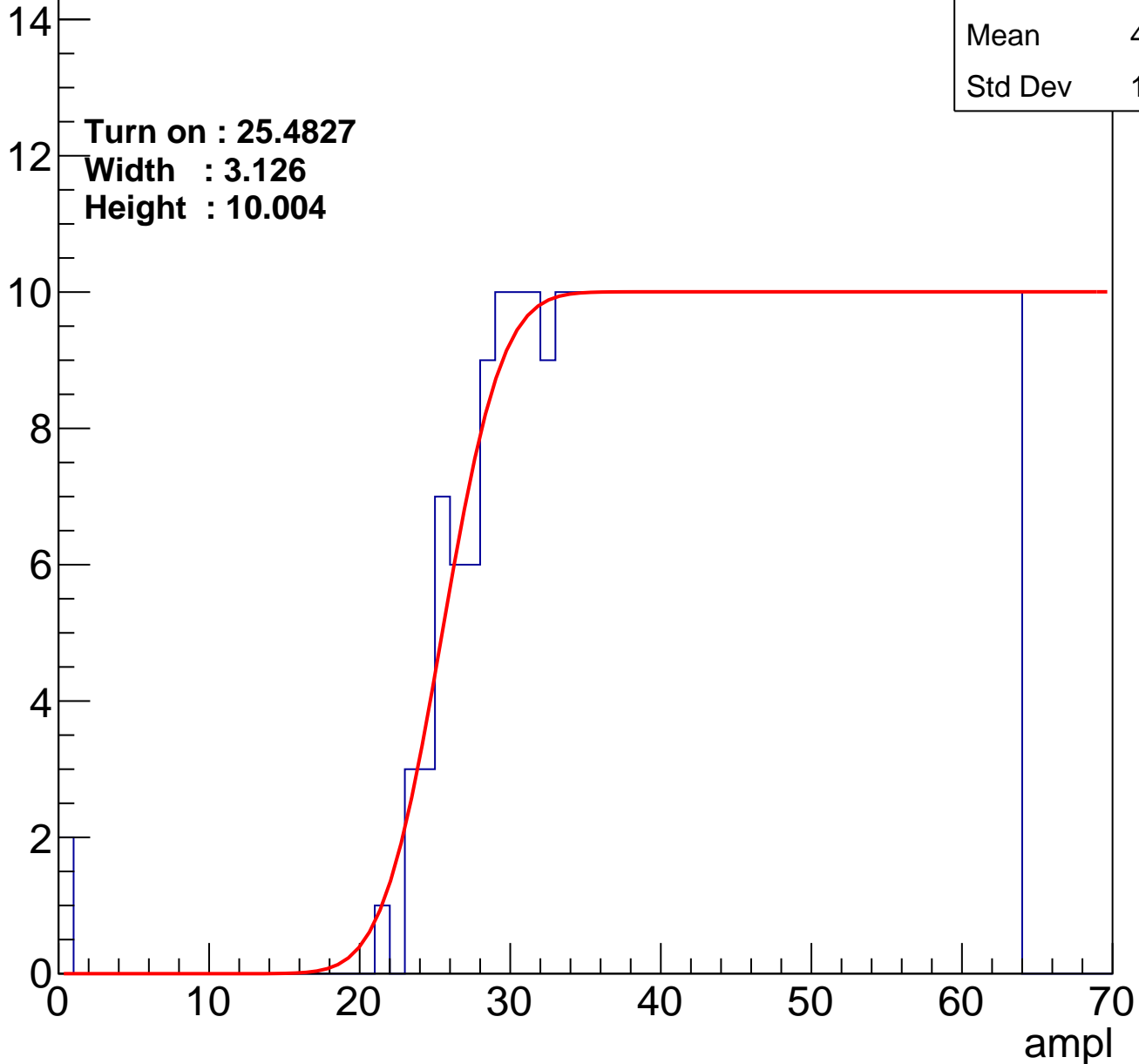
Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 25.4827

Width : 3.126

Height : 10.004

Entry



B1L002S, U15-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.08
Std Dev	11.85

Turn on : 26.3809

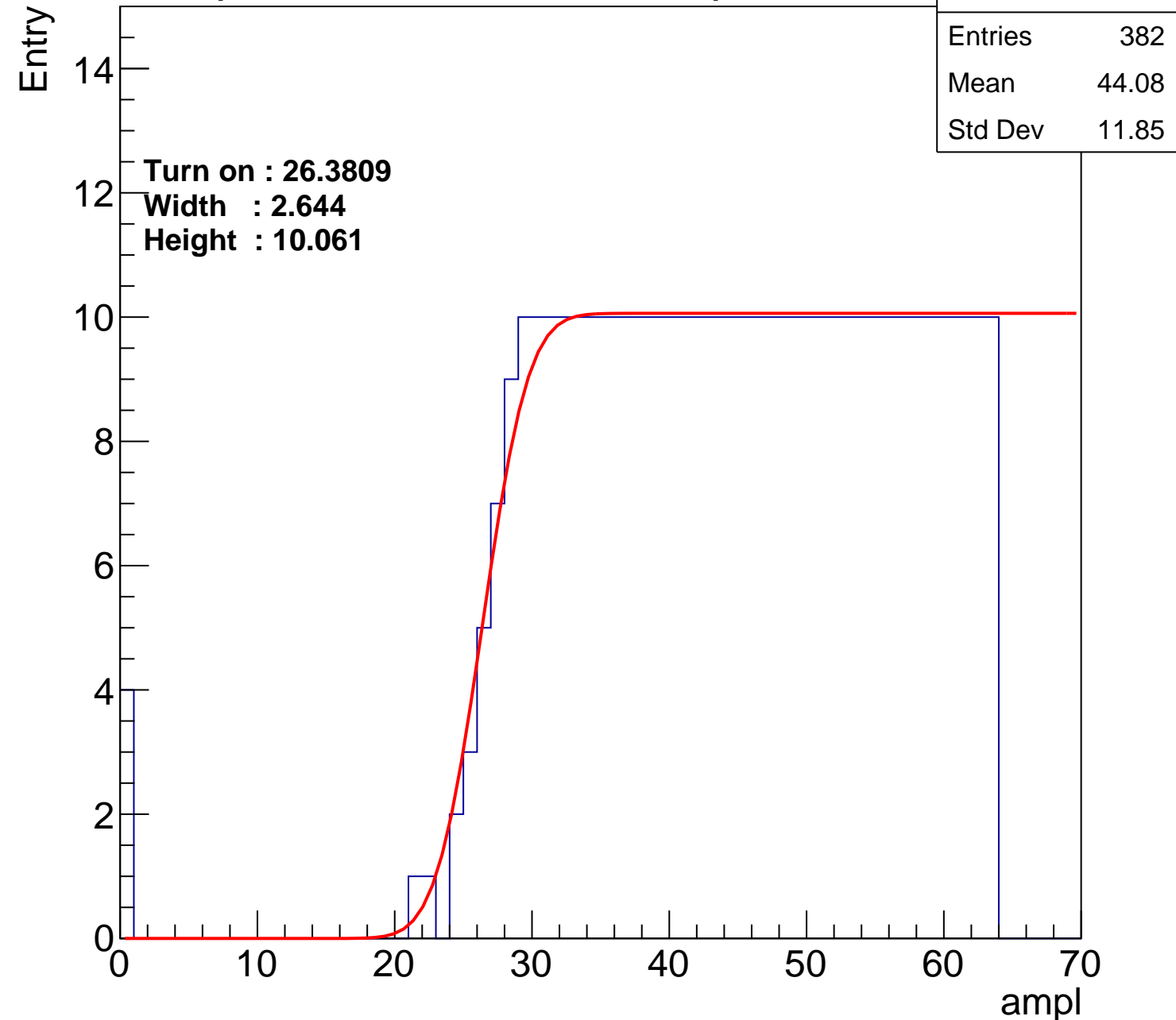
Width : 2.644

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.62
Std Dev	11.69

Turn on : 24.2312

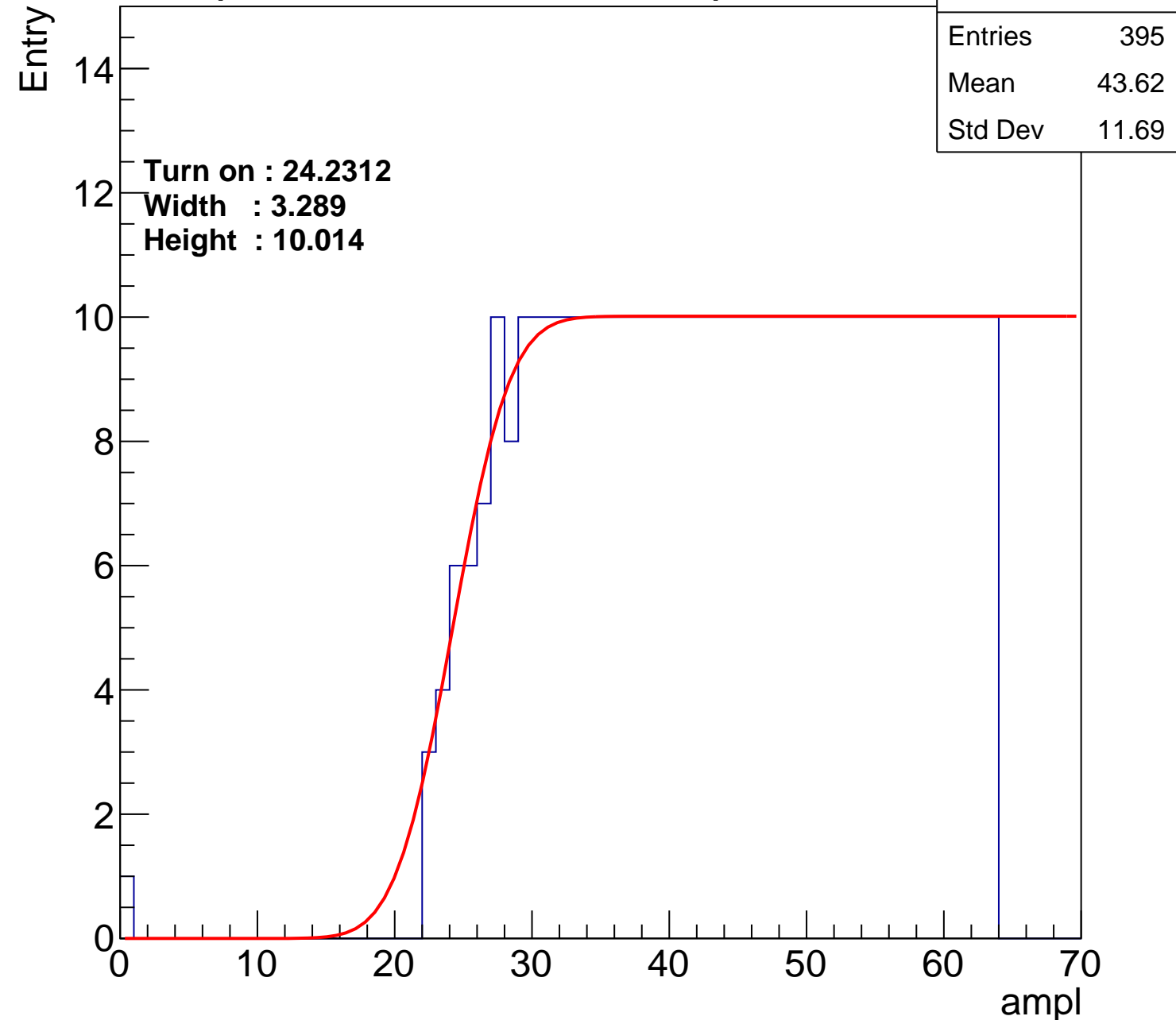
Width : 3.289

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch66

calib_packv5_042523_0143.root, FC#10, port B3

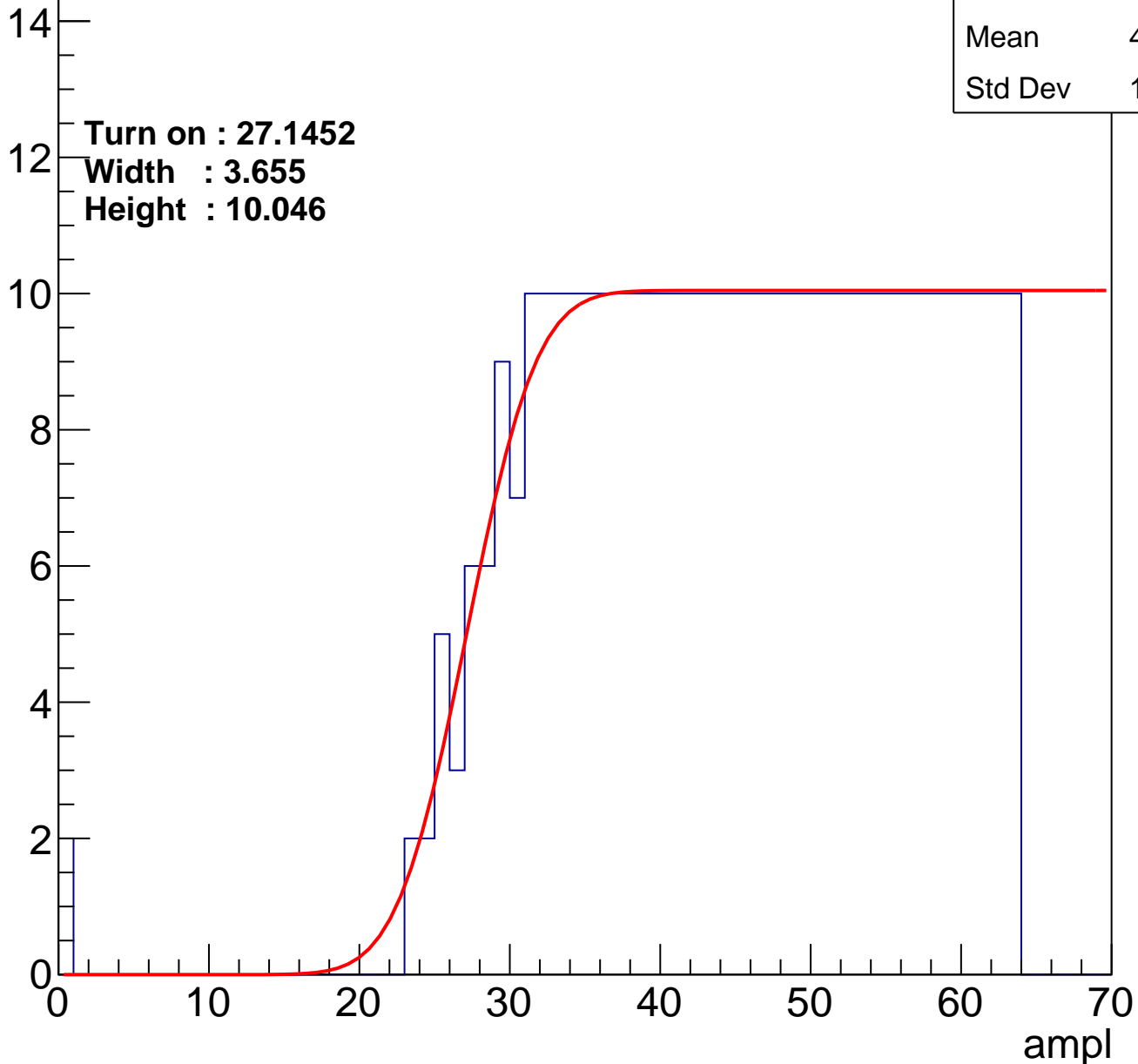
Entries	372
Mean	44.65
Std Dev	11.33

Turn on : 27.1452

Width : 3.655

Height : 10.046

Entry



B1L002S, U15-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.2
Std Dev	12.28

Turn on : 24.0502

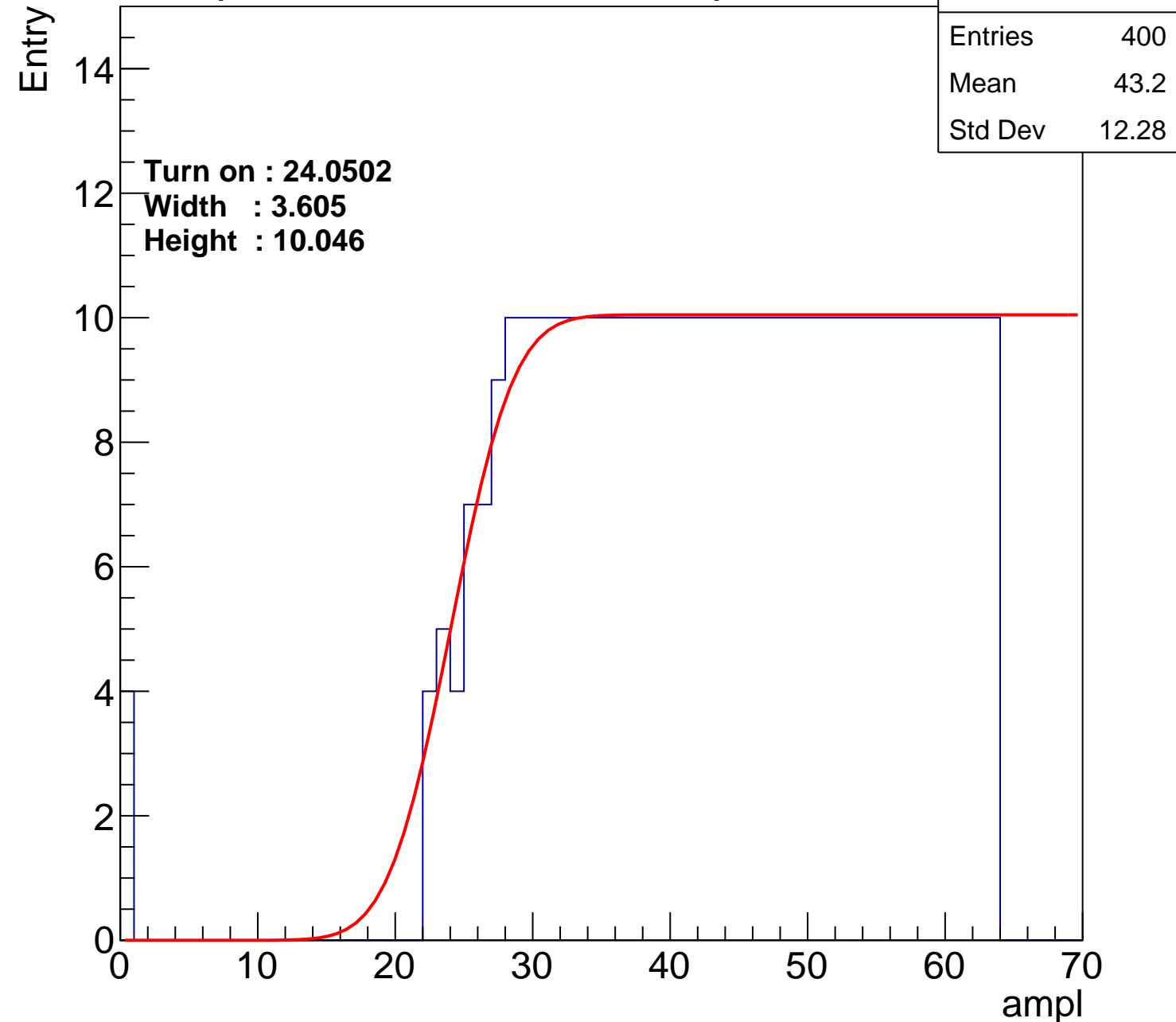
Width : 3.605

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch68

calib_packv5_042523_0143.root, FC#10, port B3

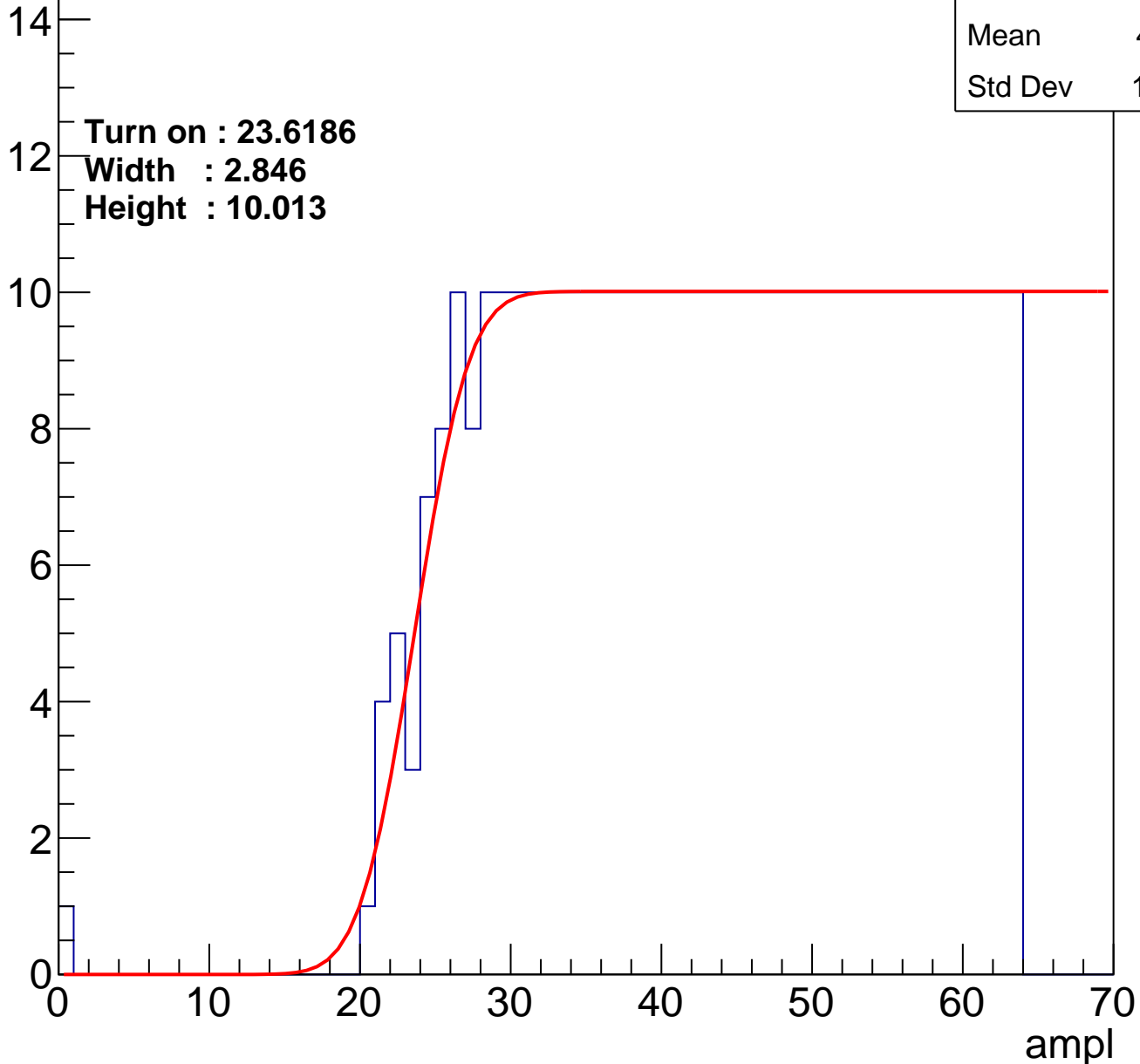
Entries	407
Mean	43.01
Std Dev	12.03

Turn on : 23.6186

Width : 2.846

Height : 10.013

Entry



B1L002S, U15-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.34
Std Dev	11.44

Turn on : 26.4173

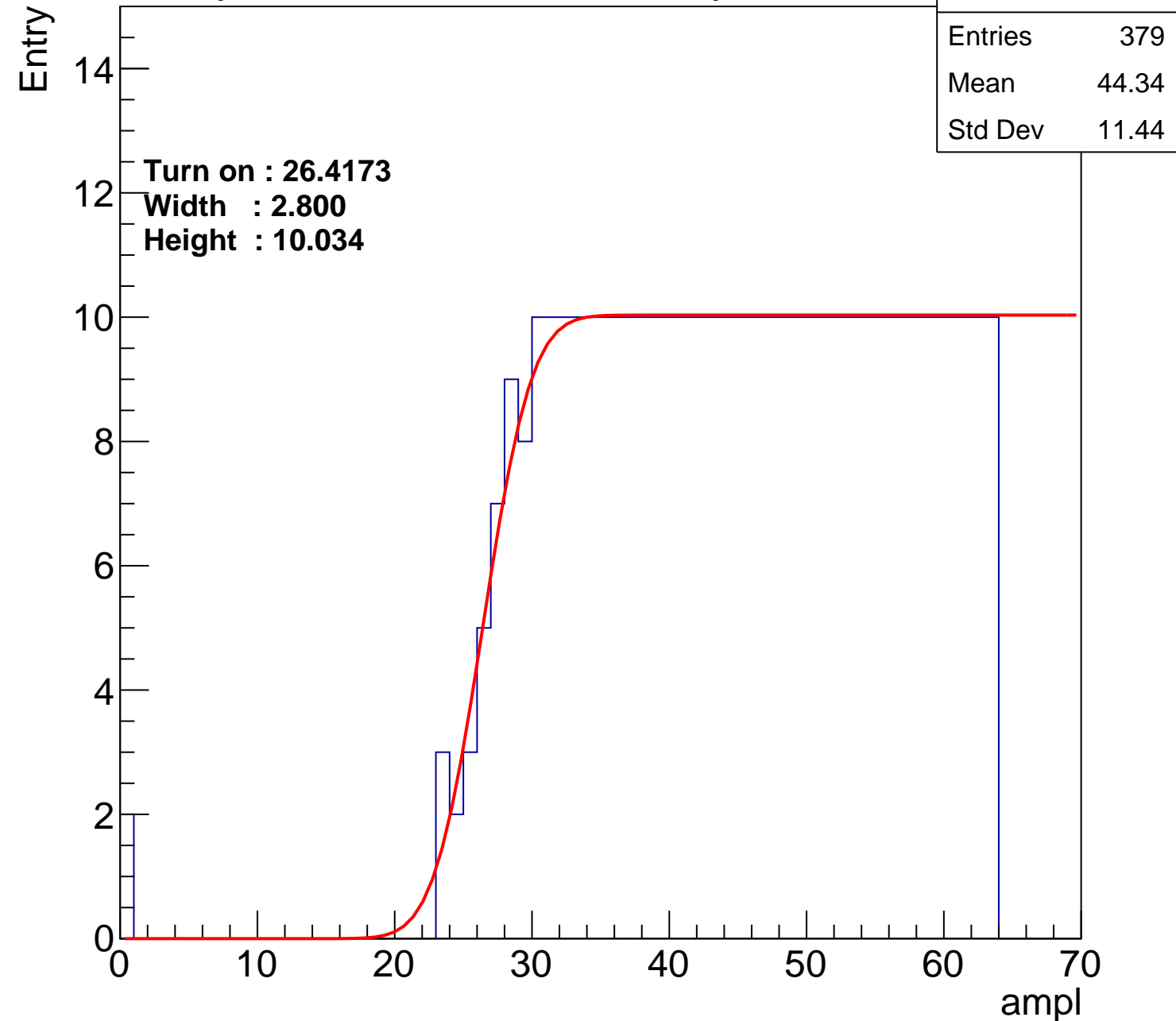
Width : 2.800

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.52
Std Dev	11.75

Turn on : 24.7693

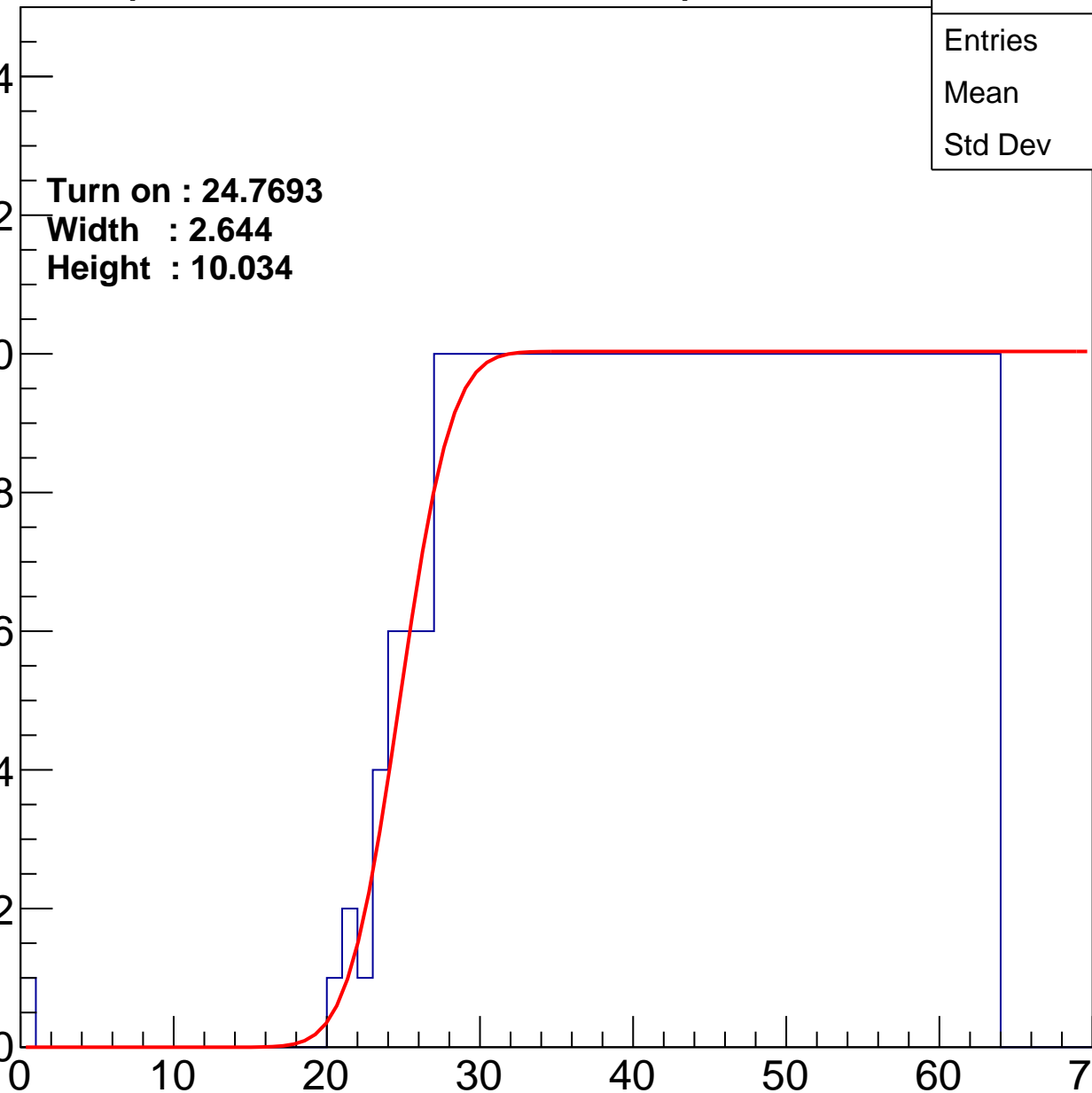
Width : 2.644

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.4885

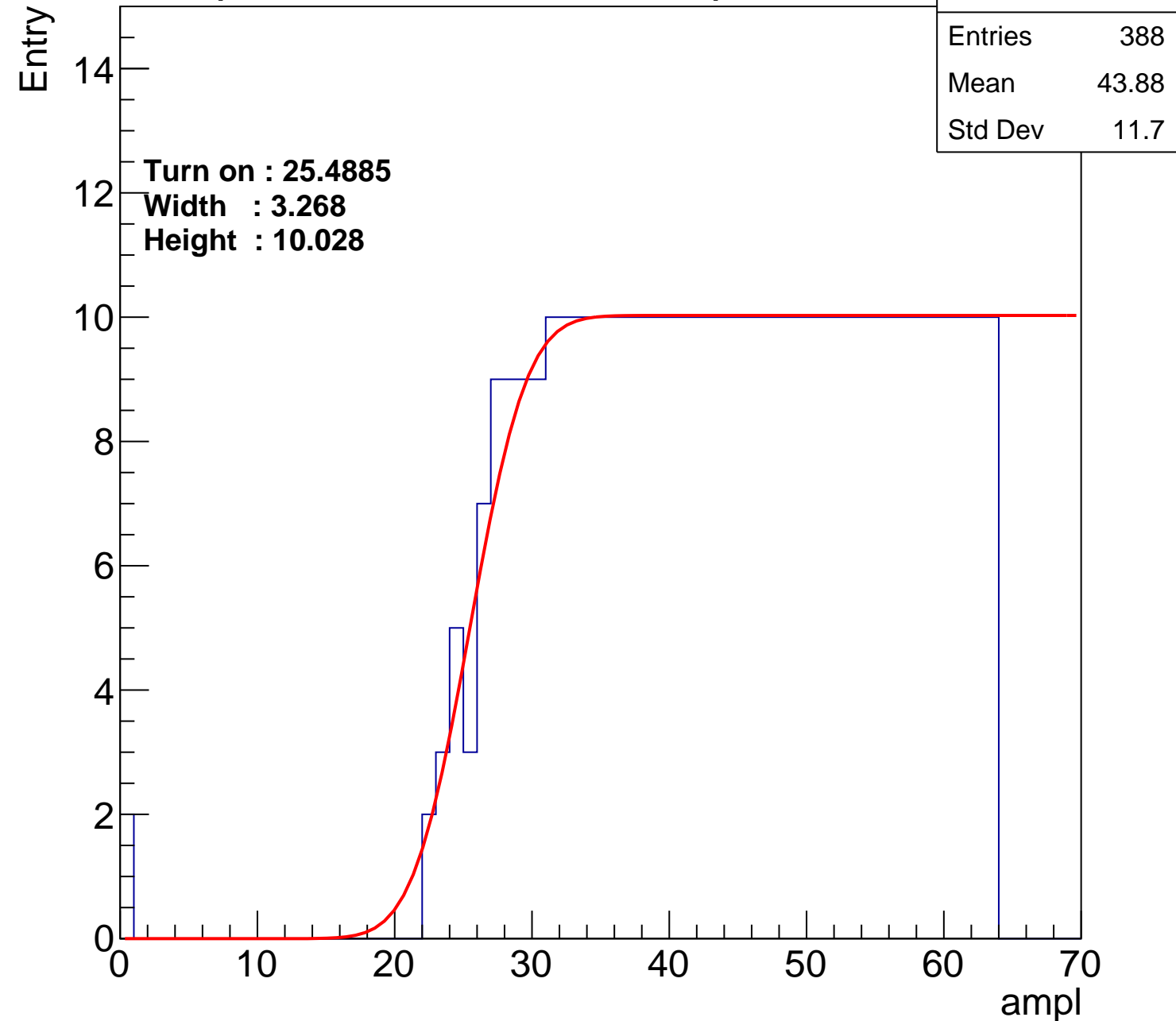
Width : 3.268

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.74
Std Dev	11.71

Turn on : 25.3511

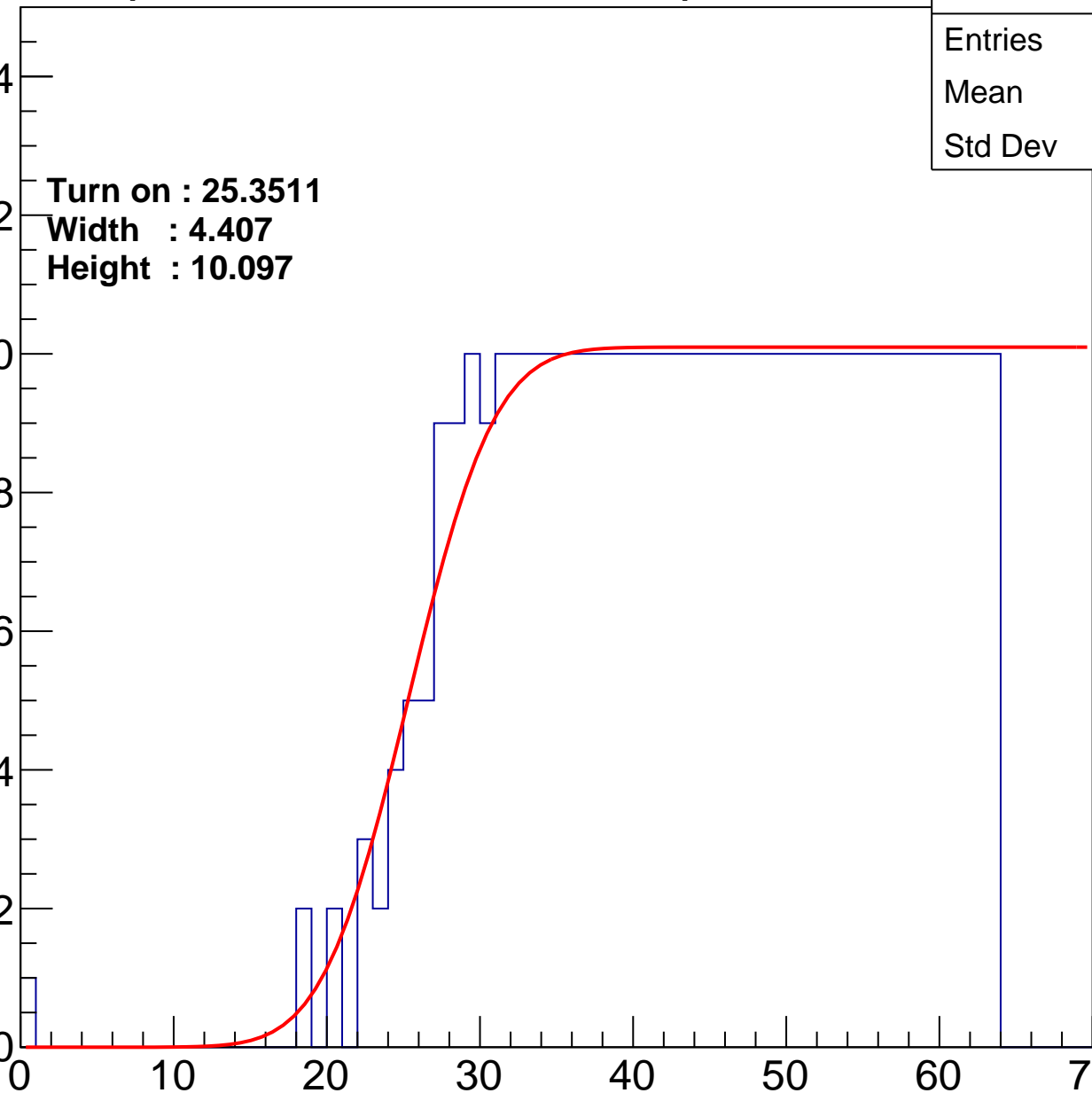
Width : 4.407

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.77
Std Dev	11.78

Turn on : 25.2560

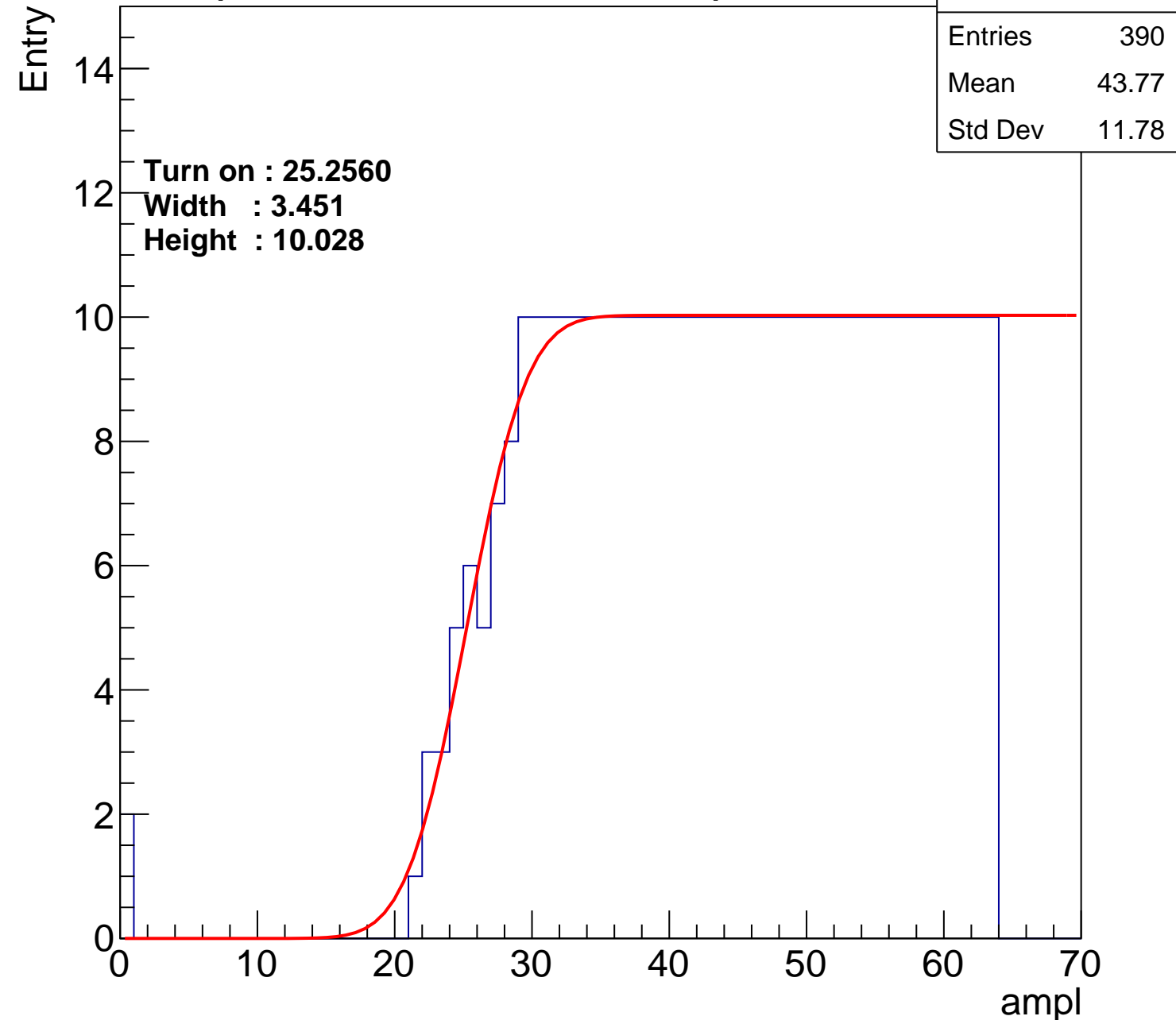
Width : 3.451

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.62
Std Dev	11.81

Turn on : 25.2112

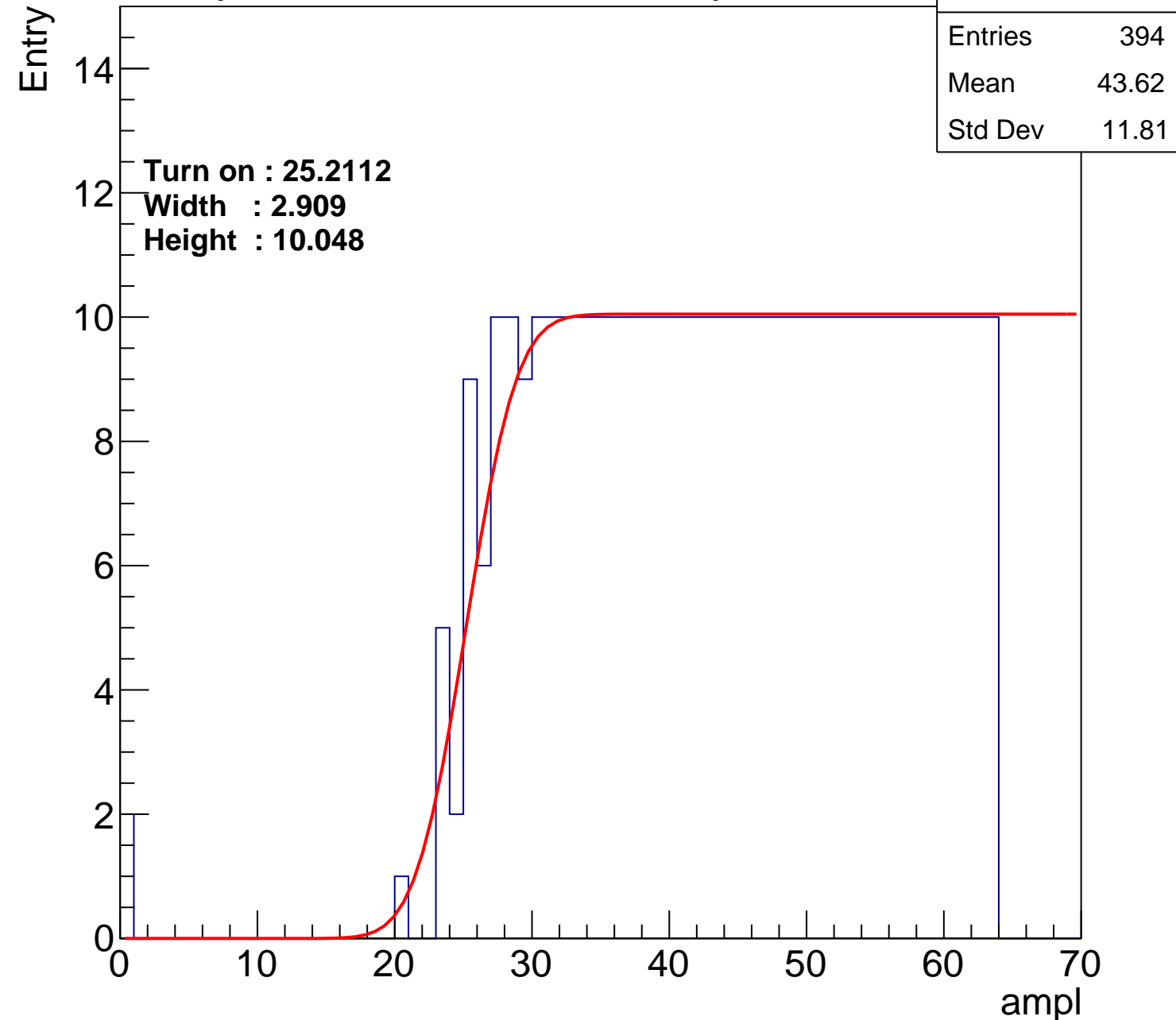
Width : 2.909

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.3
Std Dev	12.15

Turn on : 25.0865

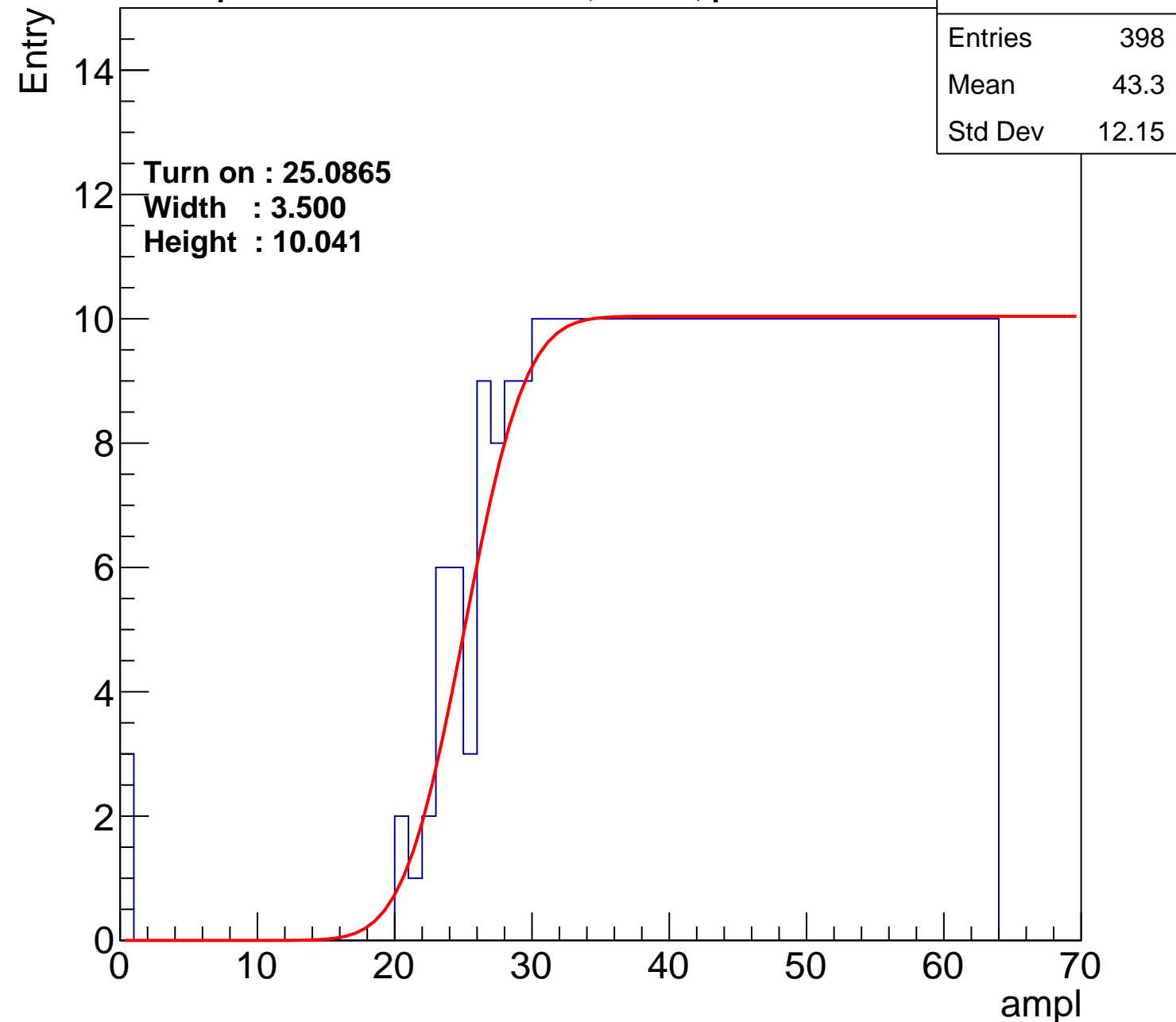
Width : 3.500

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch76

calib_packv5_042523_0143.root, FC#10, port B3

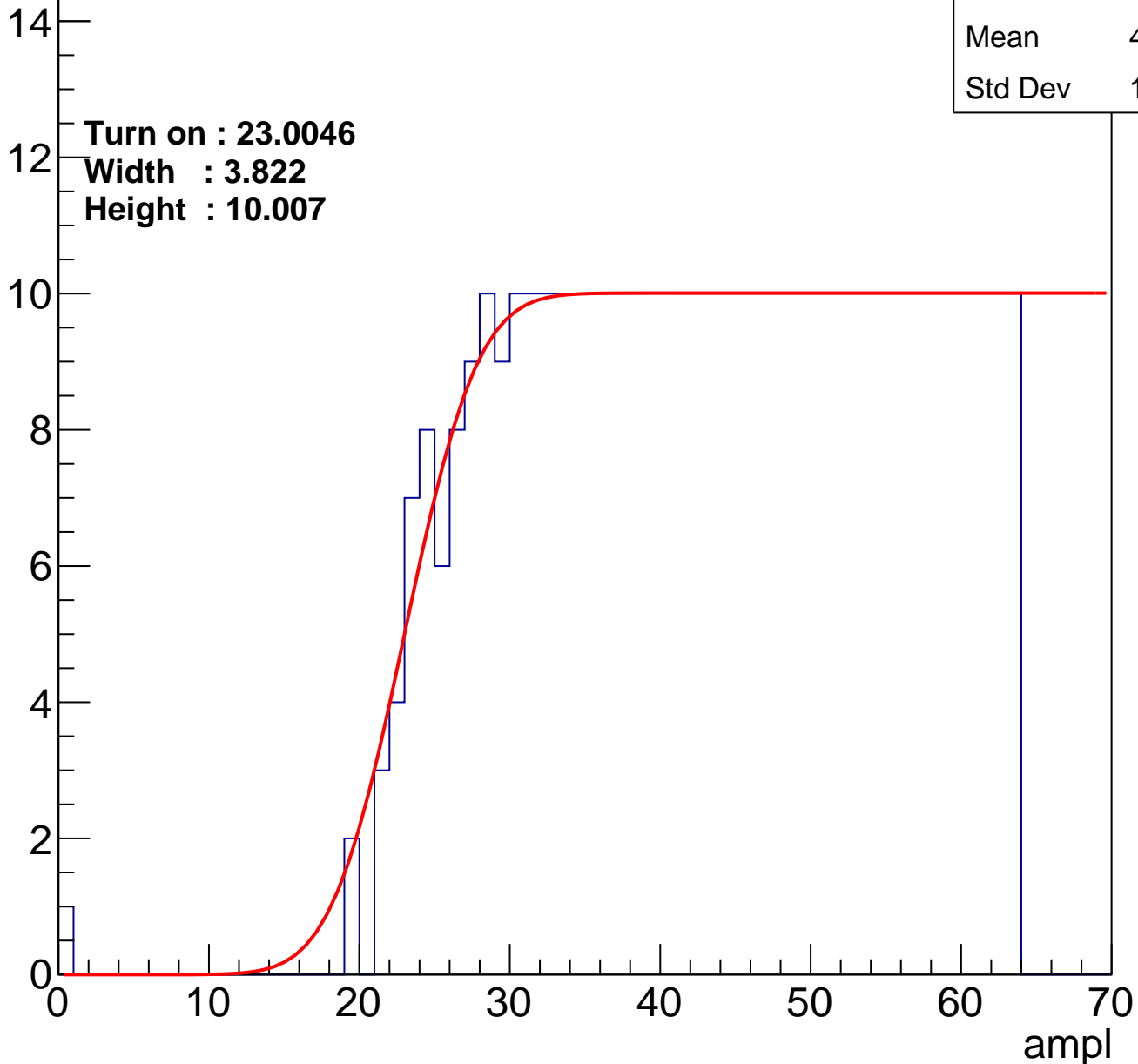
Entries	407
Mean	42.98
Std Dev	12.08

Turn on : 23.0046

Width : 3.822

Height : 10.007

Entry



B1L002S, U15-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.45
Std Dev	11.79

Turn on : 24.1984

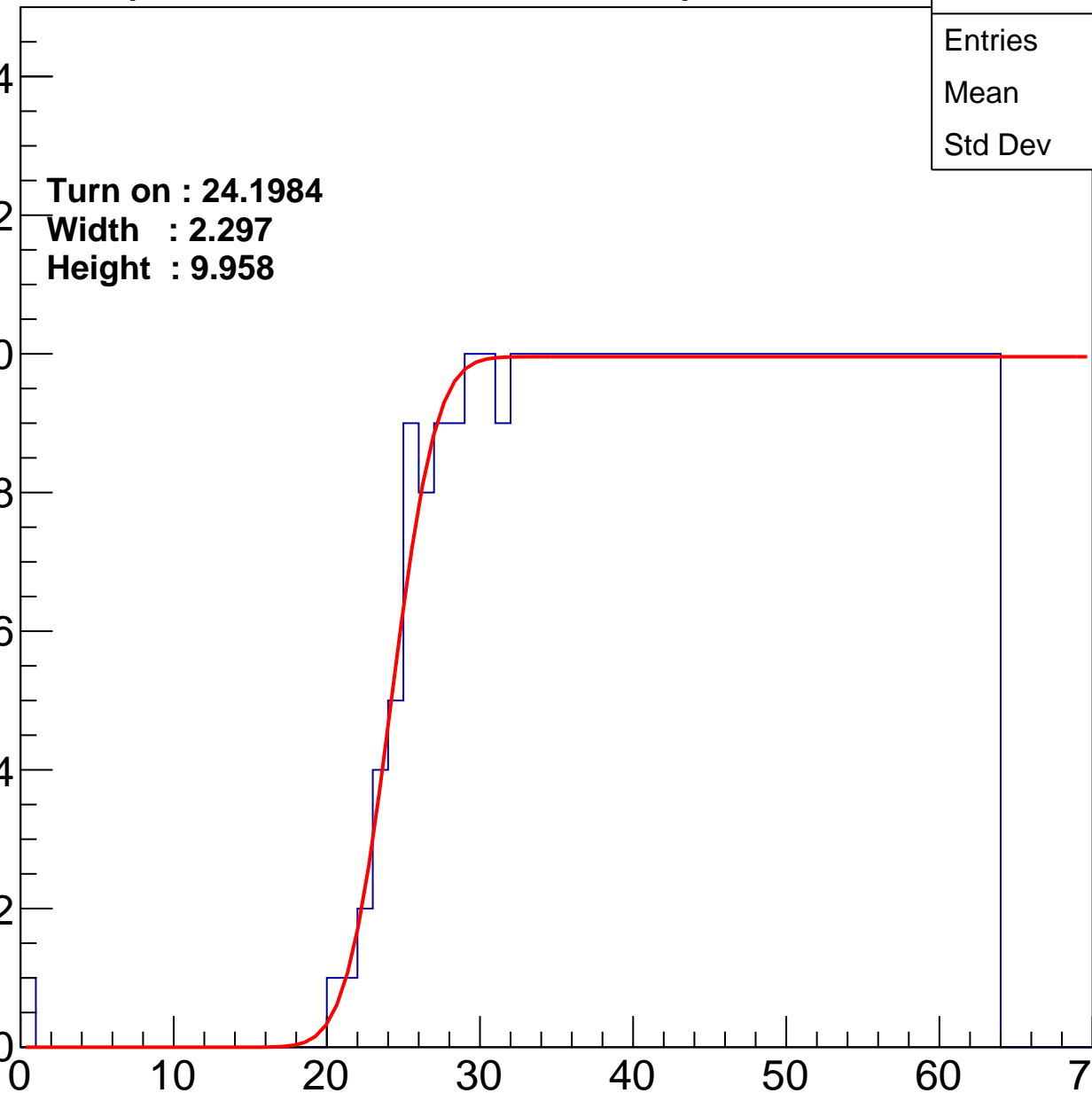
Width : 2.297

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.3
Std Dev	11.52

Turn on : 26.7189

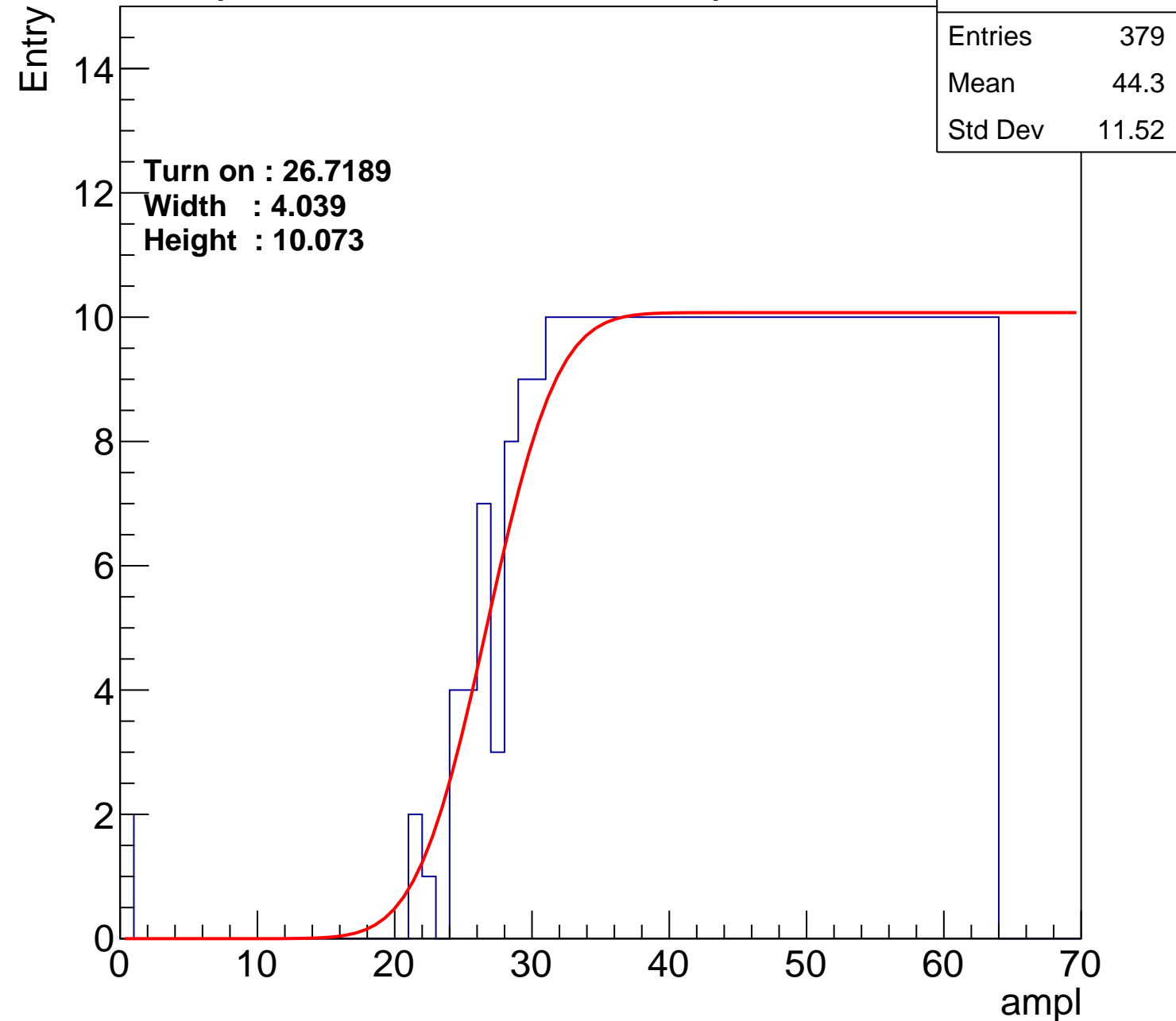
Width : 4.039

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.14
Std Dev	11.43

Turn on : 25.6737

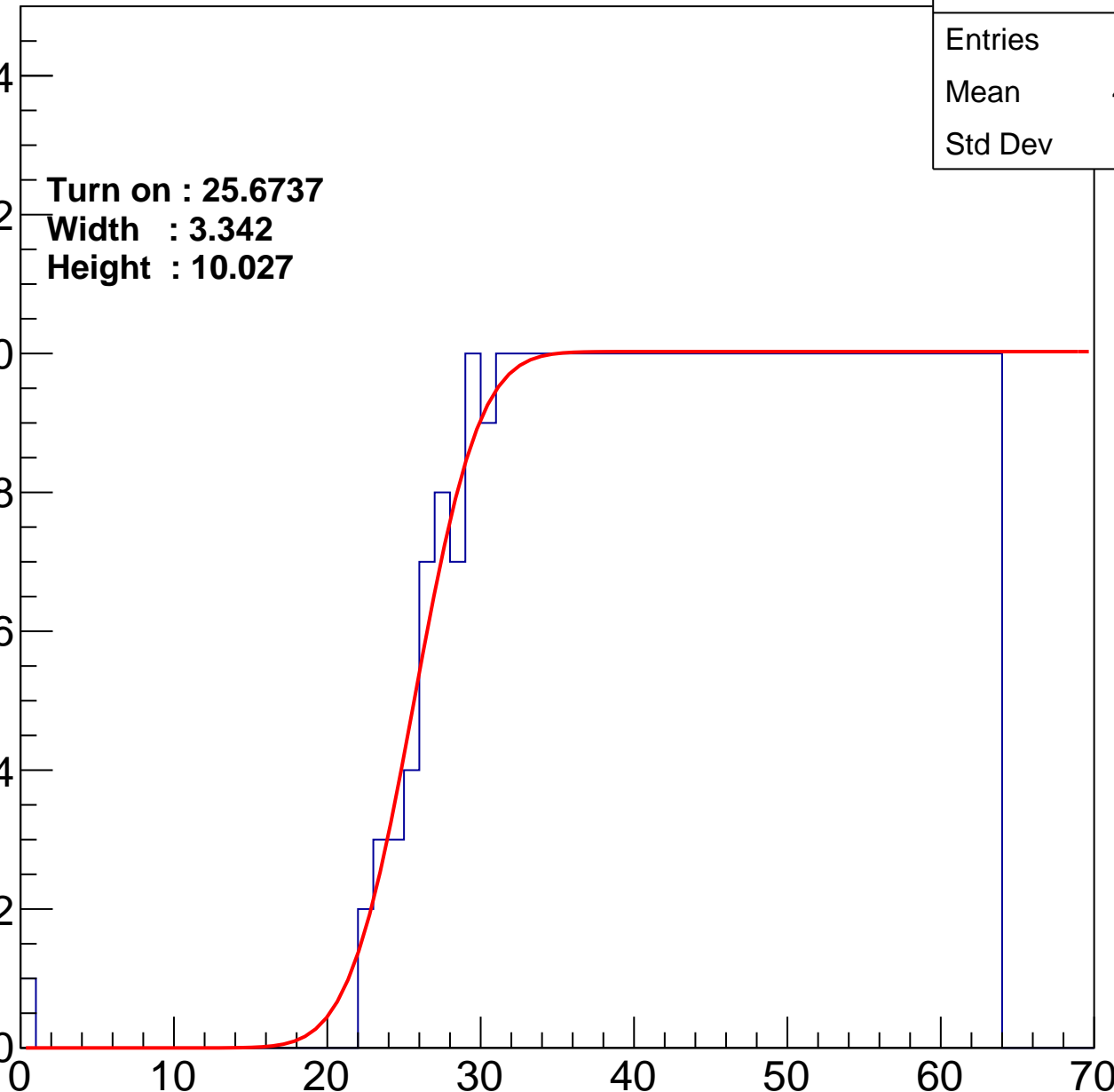
Width : 3.342

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 25.4496

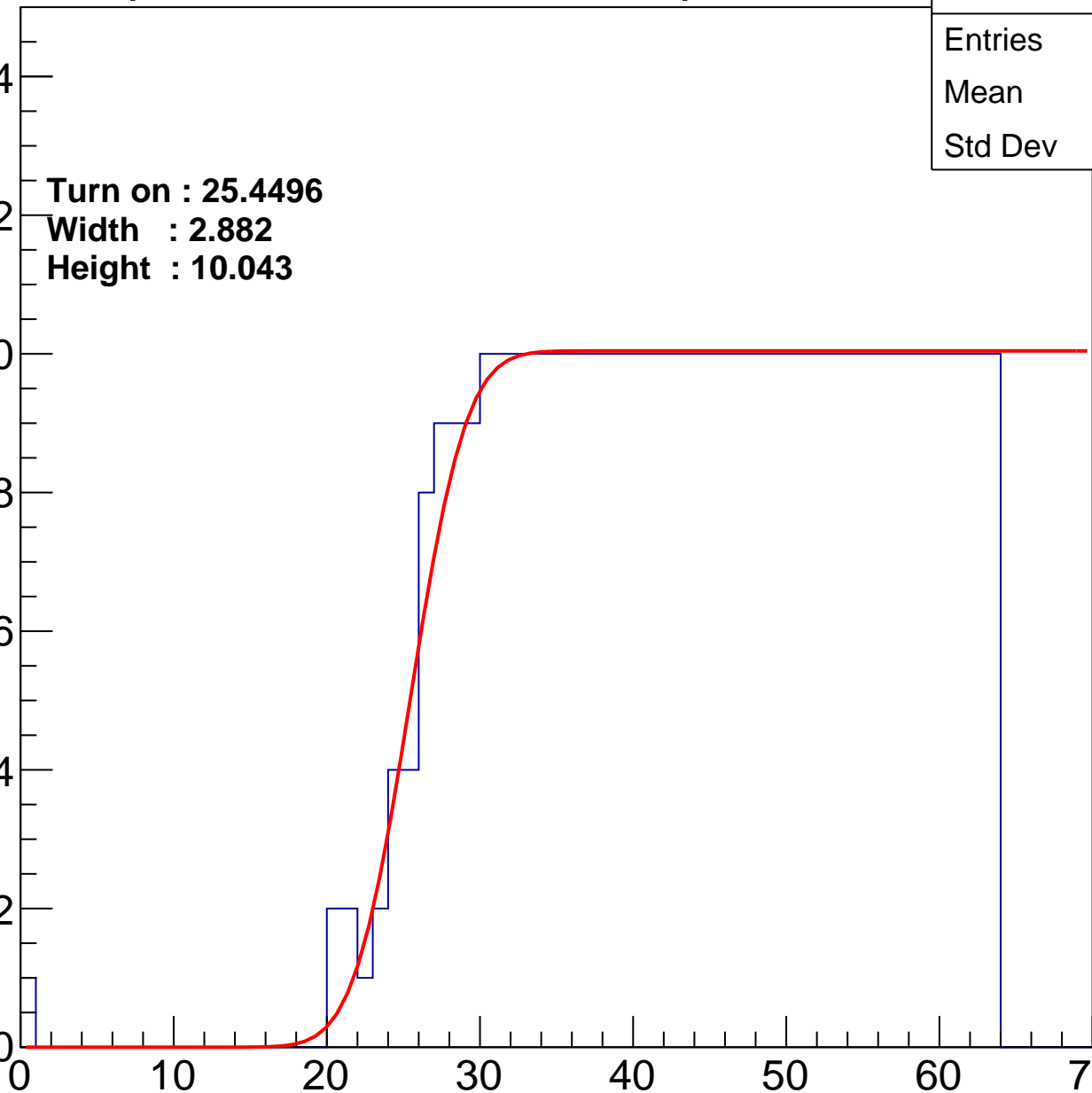
Width : 2.882

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	43.97
Std Dev	11.96

Turn on : 26.1957

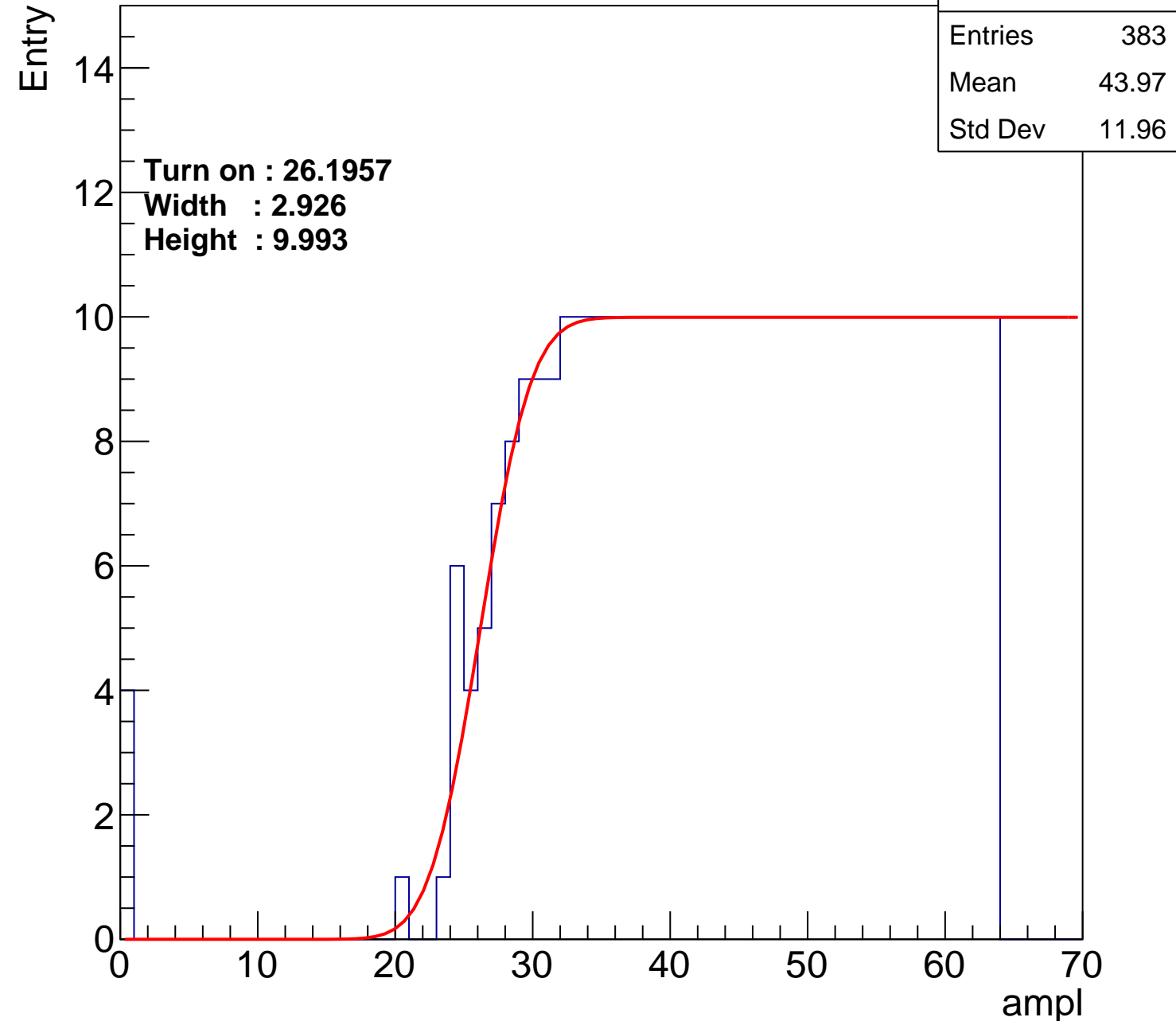
Width : 2.926

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.35
Std Dev	12.13

Turn on : 24.5515

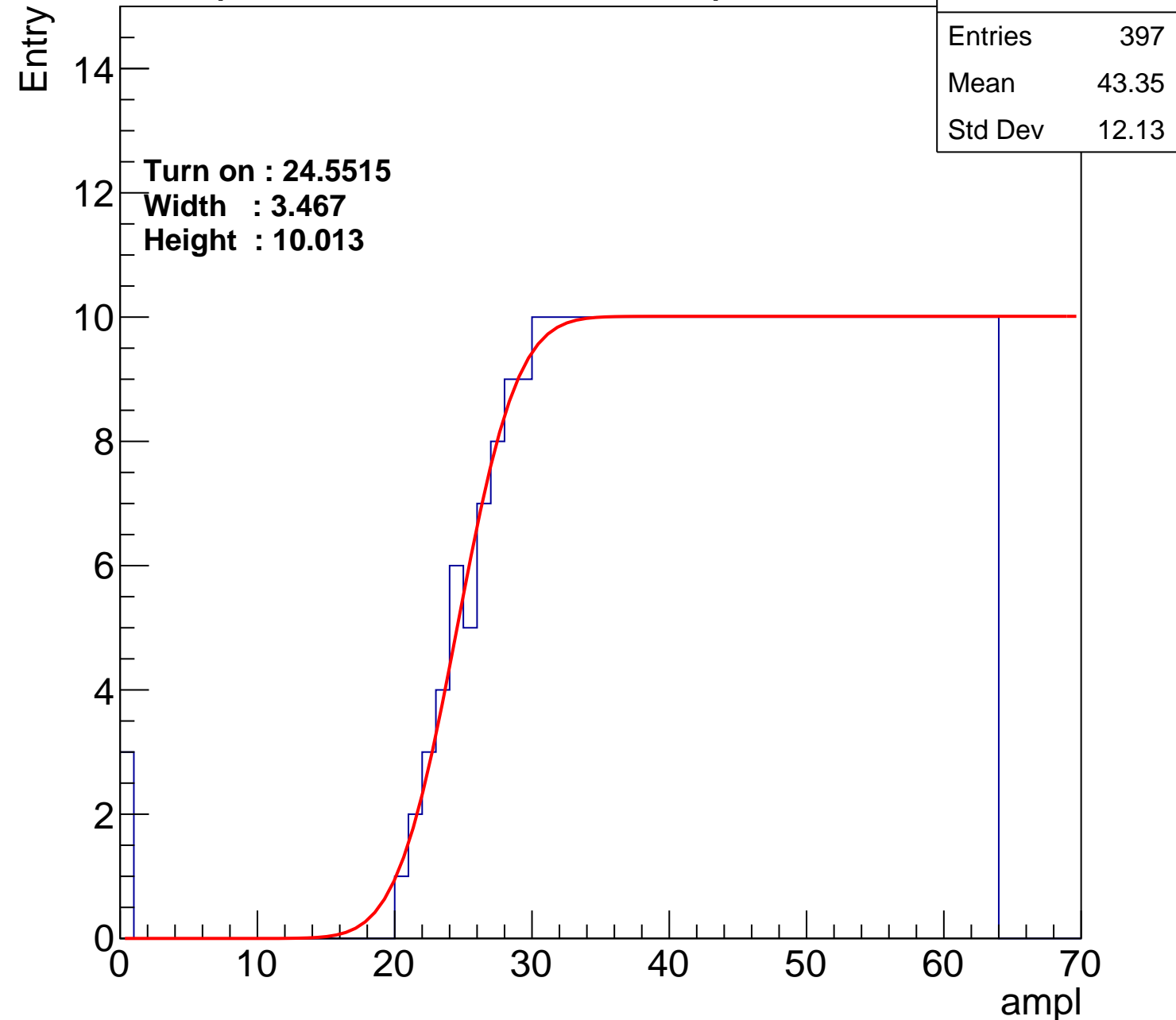
Width : 3.467

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.95
Std Dev	11.76

Turn on : 25.8718

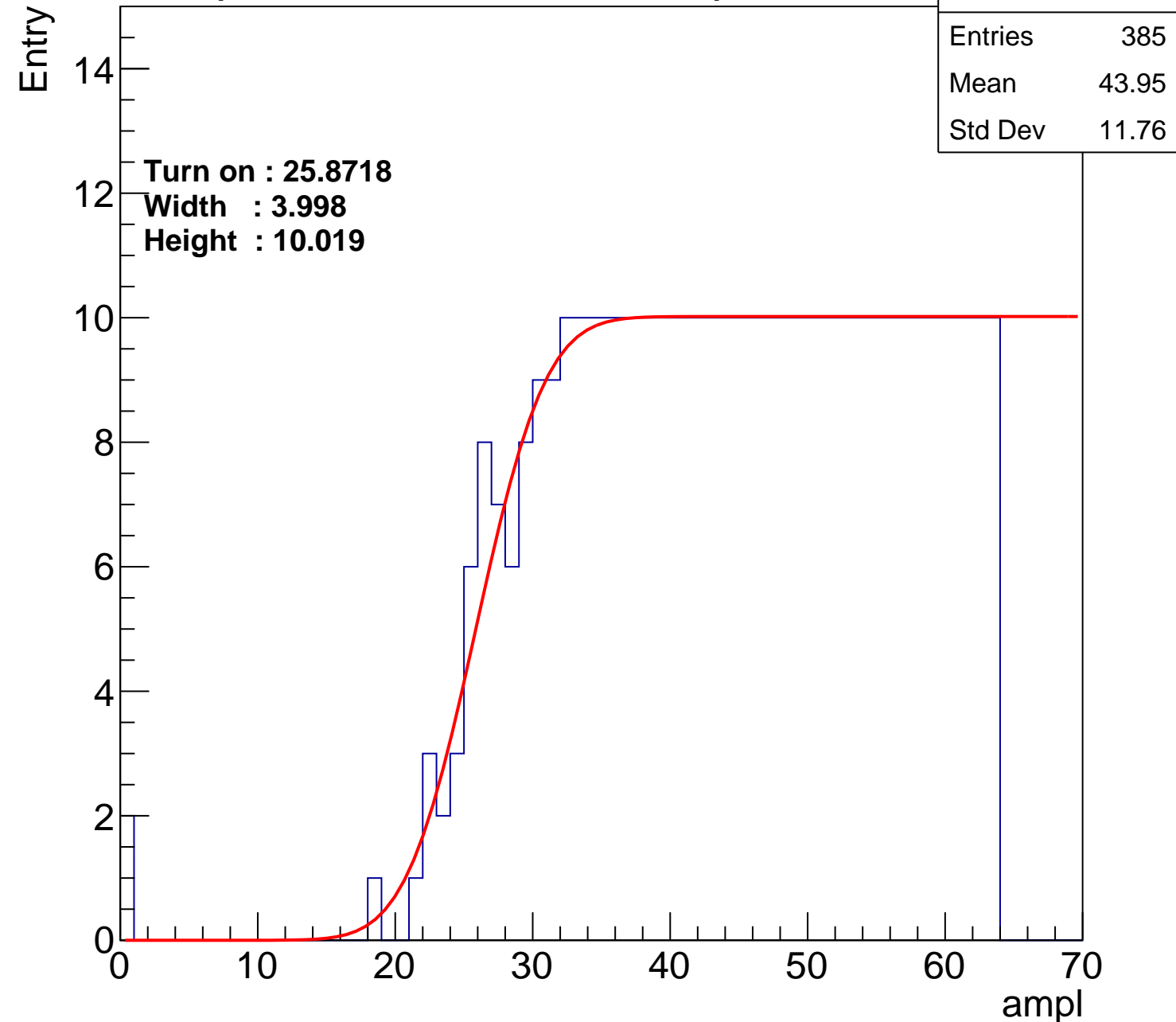
Width : 3.998

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.38
Std Dev	11.86

Turn on : 24.5292

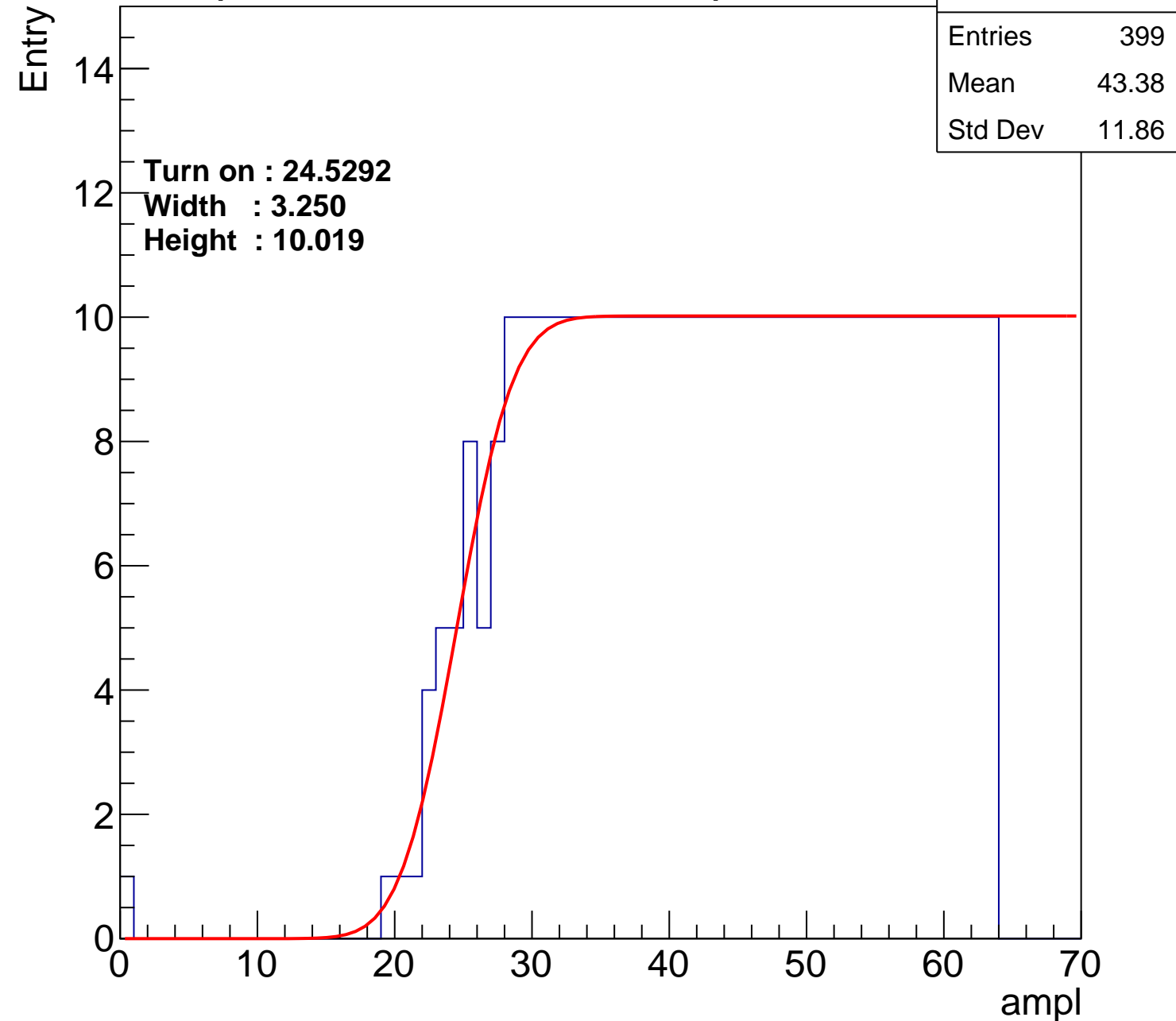
Width : 3.250

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.42
Std Dev	12.09

Turn on : 25.4732

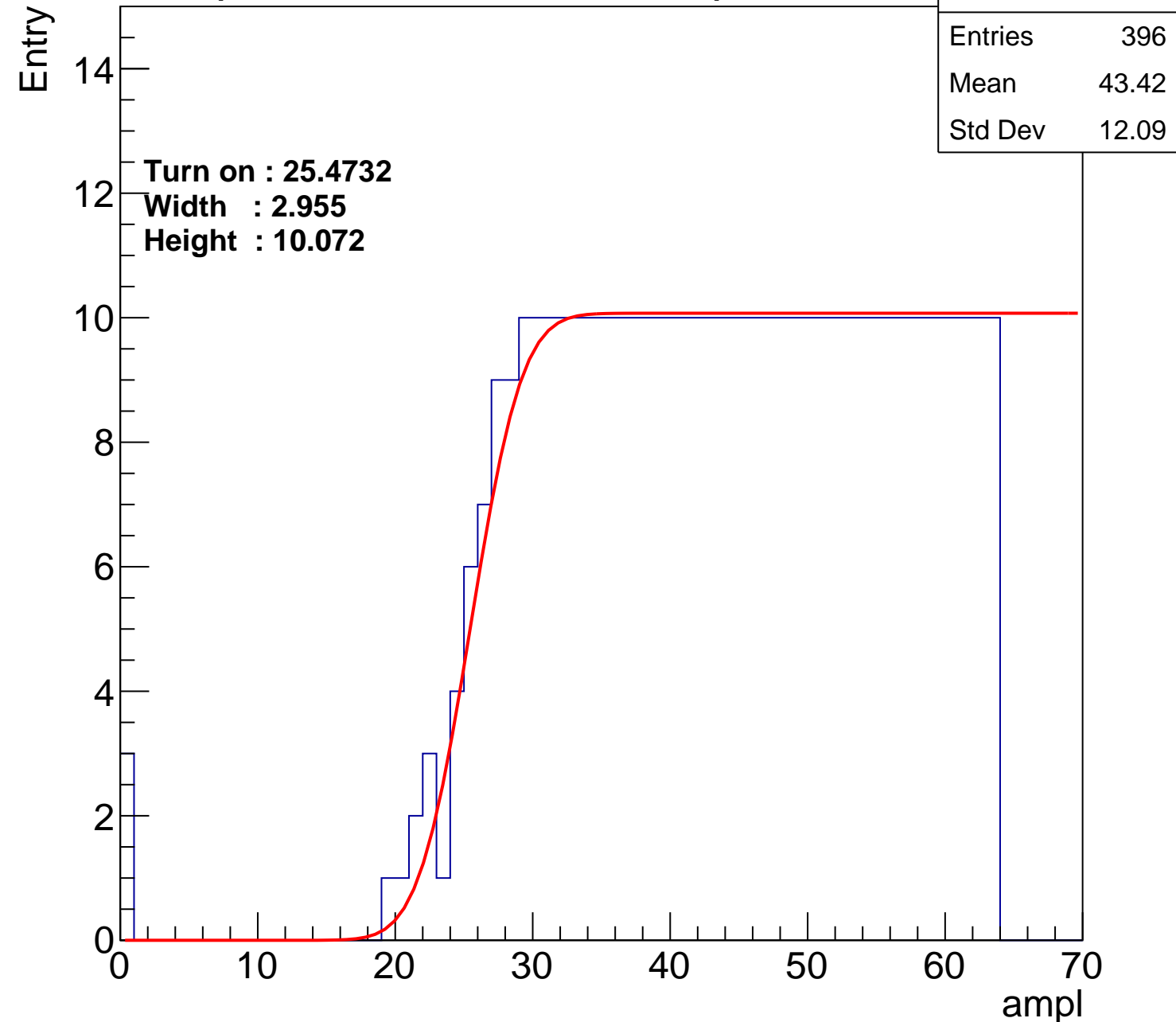
Width : 2.955

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch86

calib_packv5_042523_0143.root, FC#10, port B3

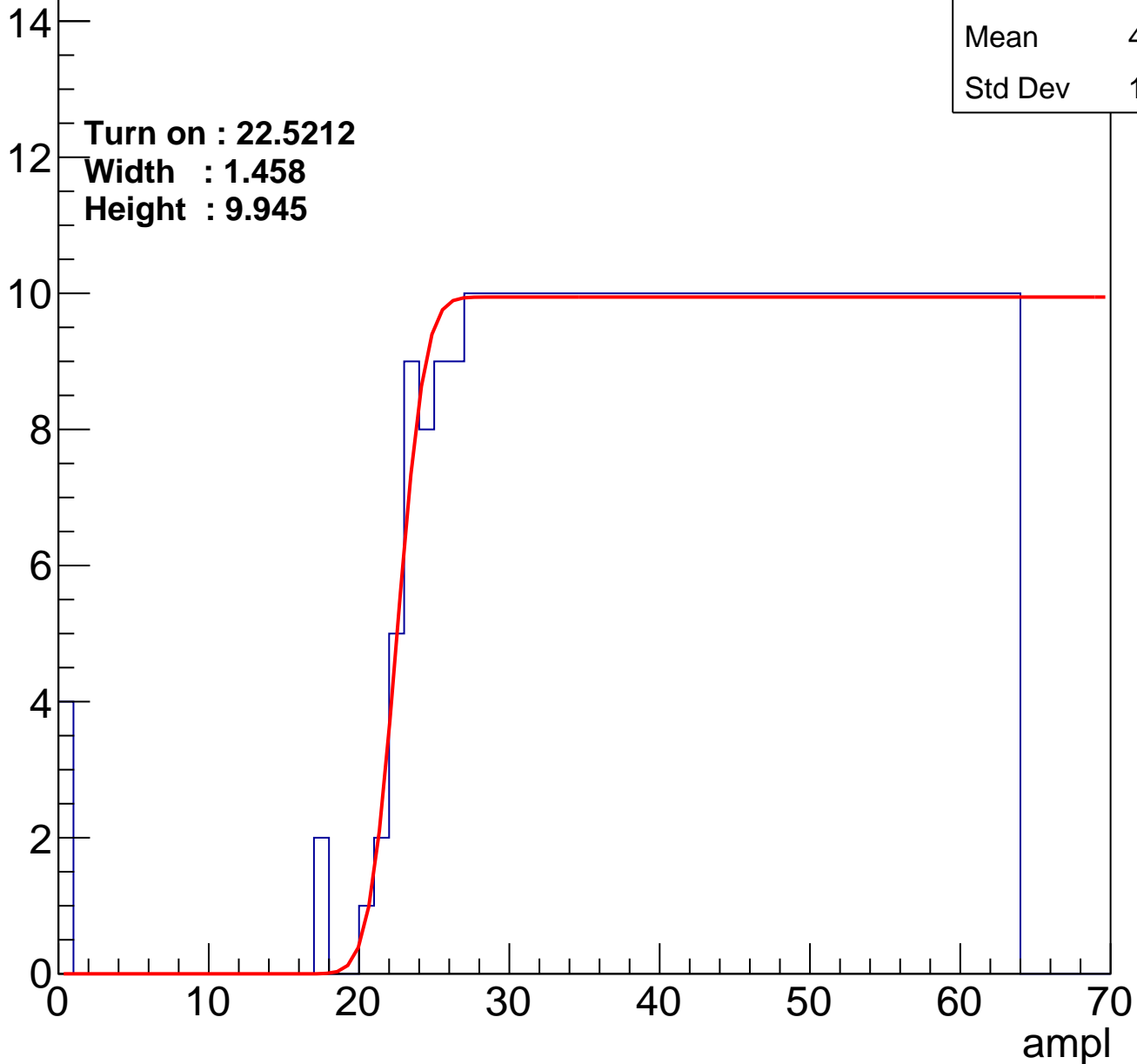
Entries	419
Mean	42.28
Std Dev	12.73

Turn on : 22.5212

Width : 1.458

Height : 9.945

Entry



B1L002S, U15-ch87

calib_packv5_042523_0143.root, FC#10, port B3

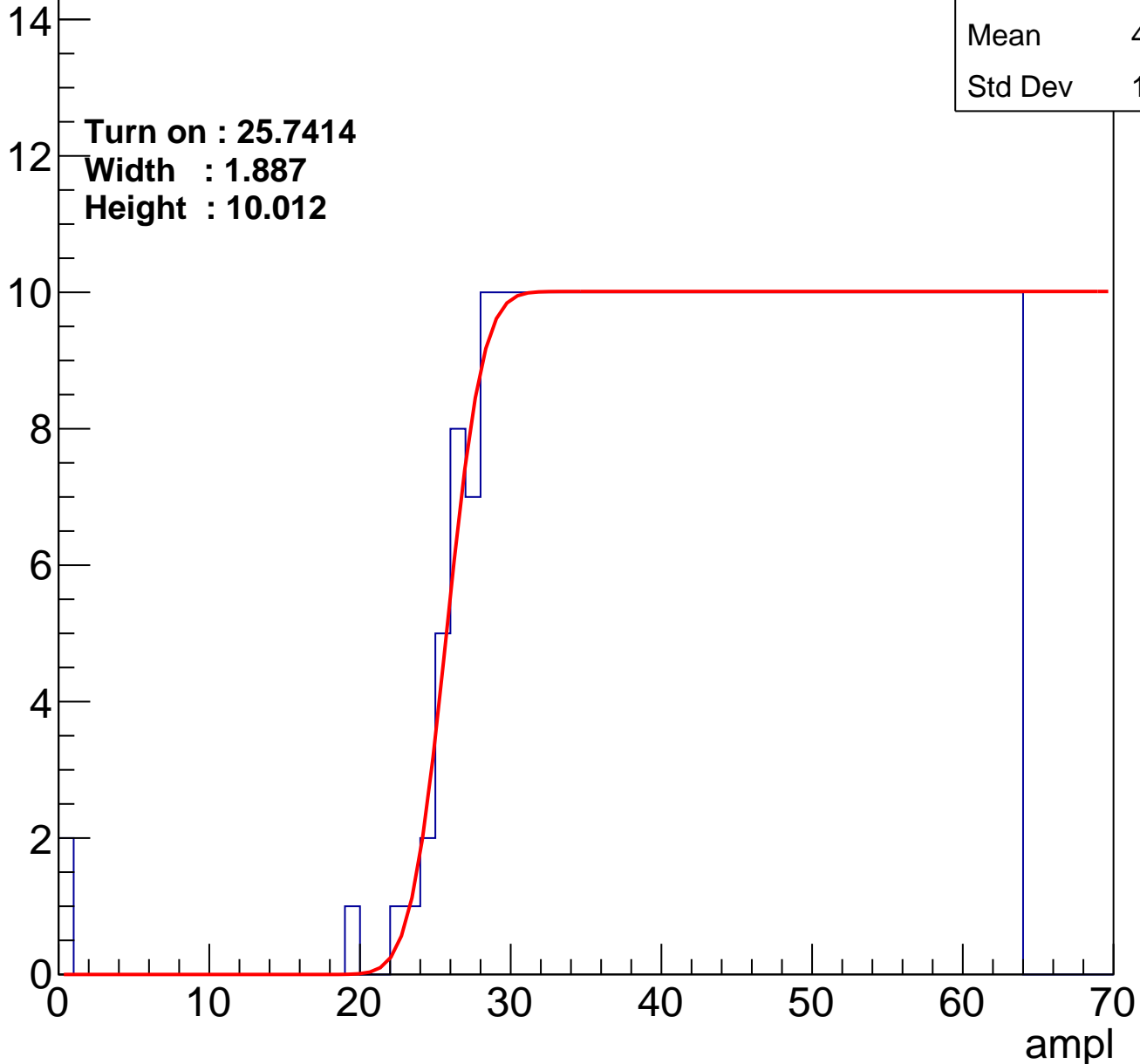
Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.7414

Width : 1.887

Height : 10.012

Entry



B1L002S, U15-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.37
Std Dev	11.93

Turn on : 23.9467

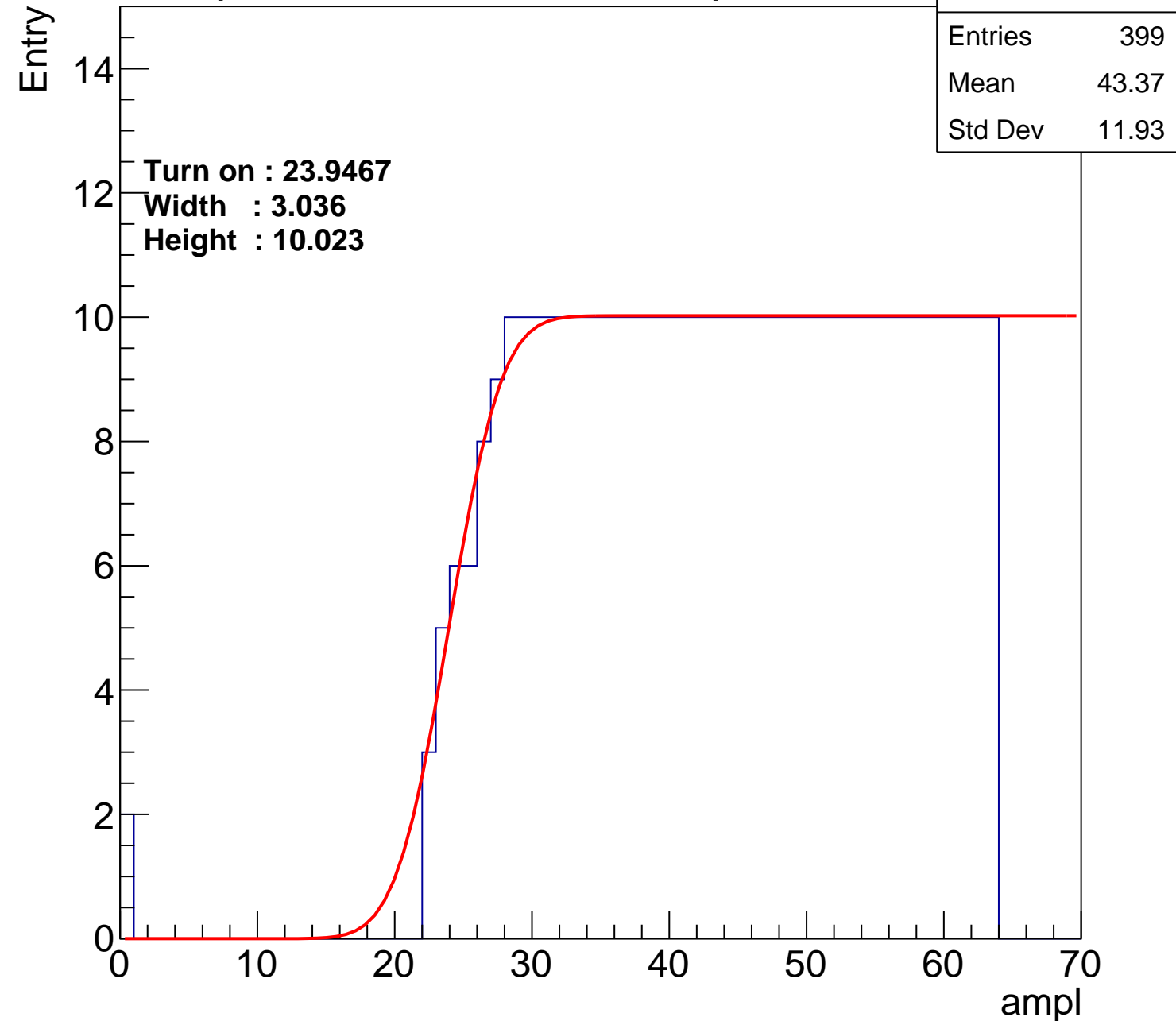
Width : 3.036

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.76
Std Dev	11.61

Turn on : 25.1101

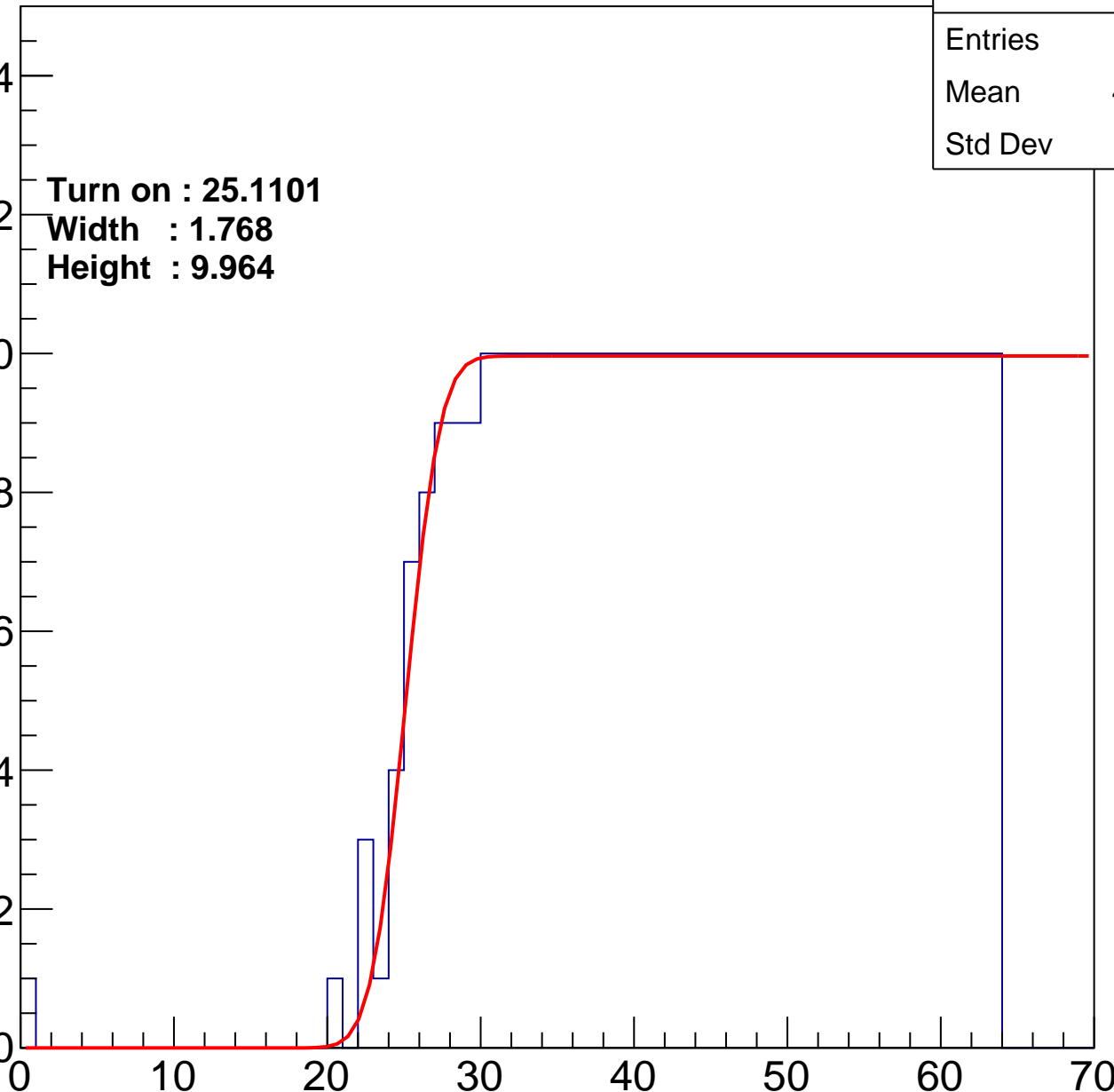
Width : 1.768

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.69
Std Dev	11.96

Turn on : 25.7610

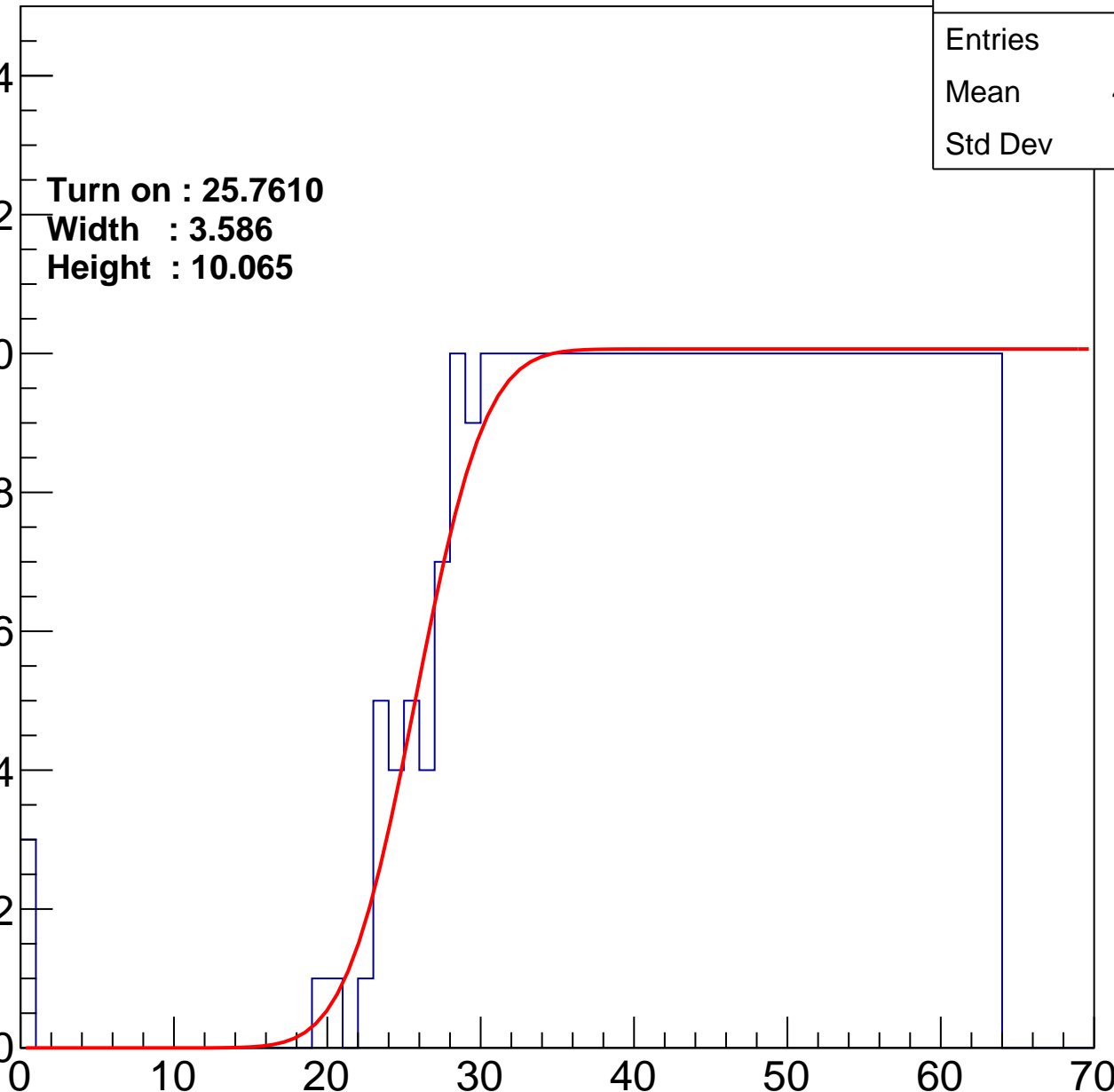
Width : 3.586

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.02
Std Dev	12.29

Turn on : 24.2478

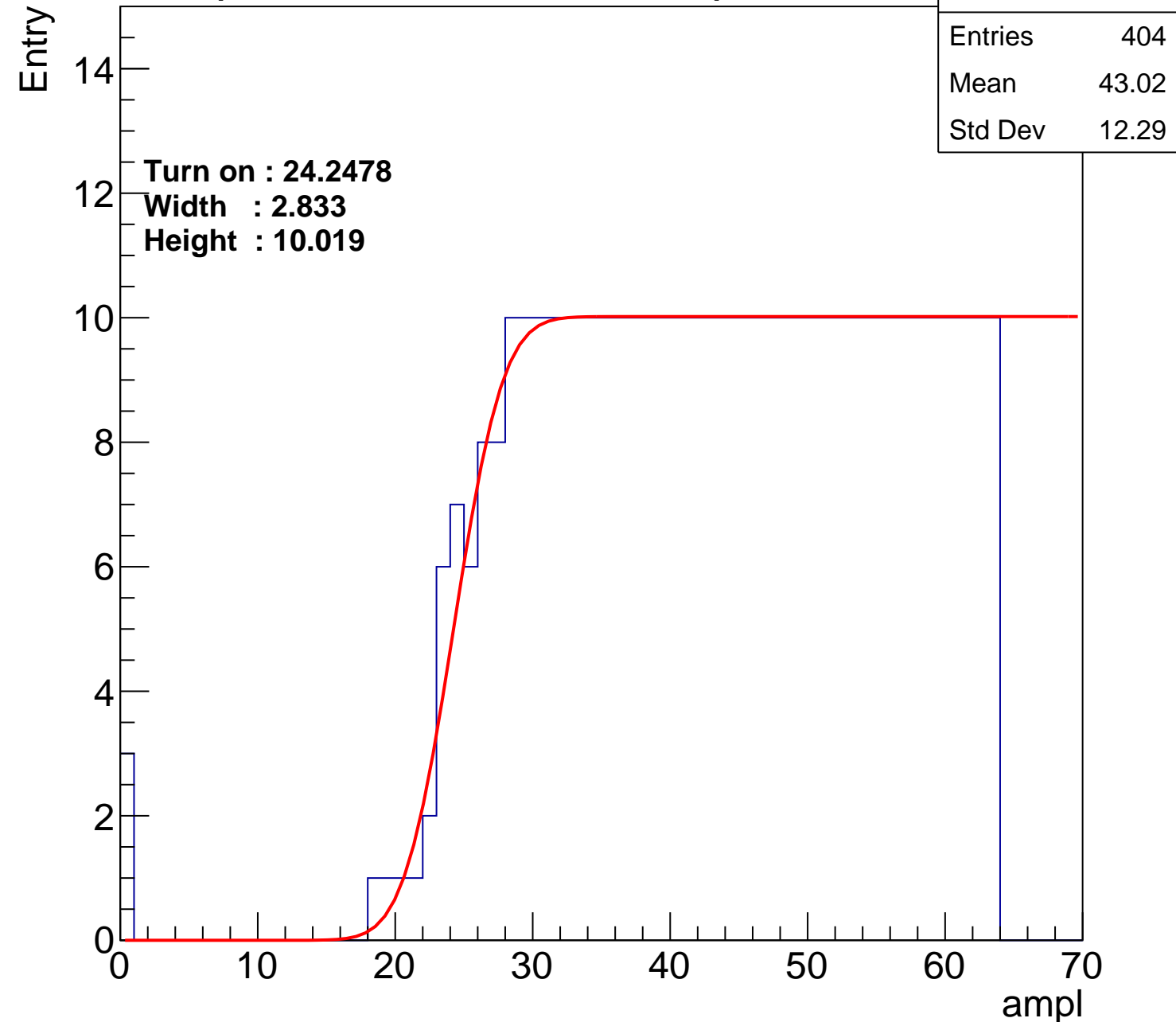
Width : 2.833

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch92

calib_packv5_042523_0143.root, FC#10, port B3

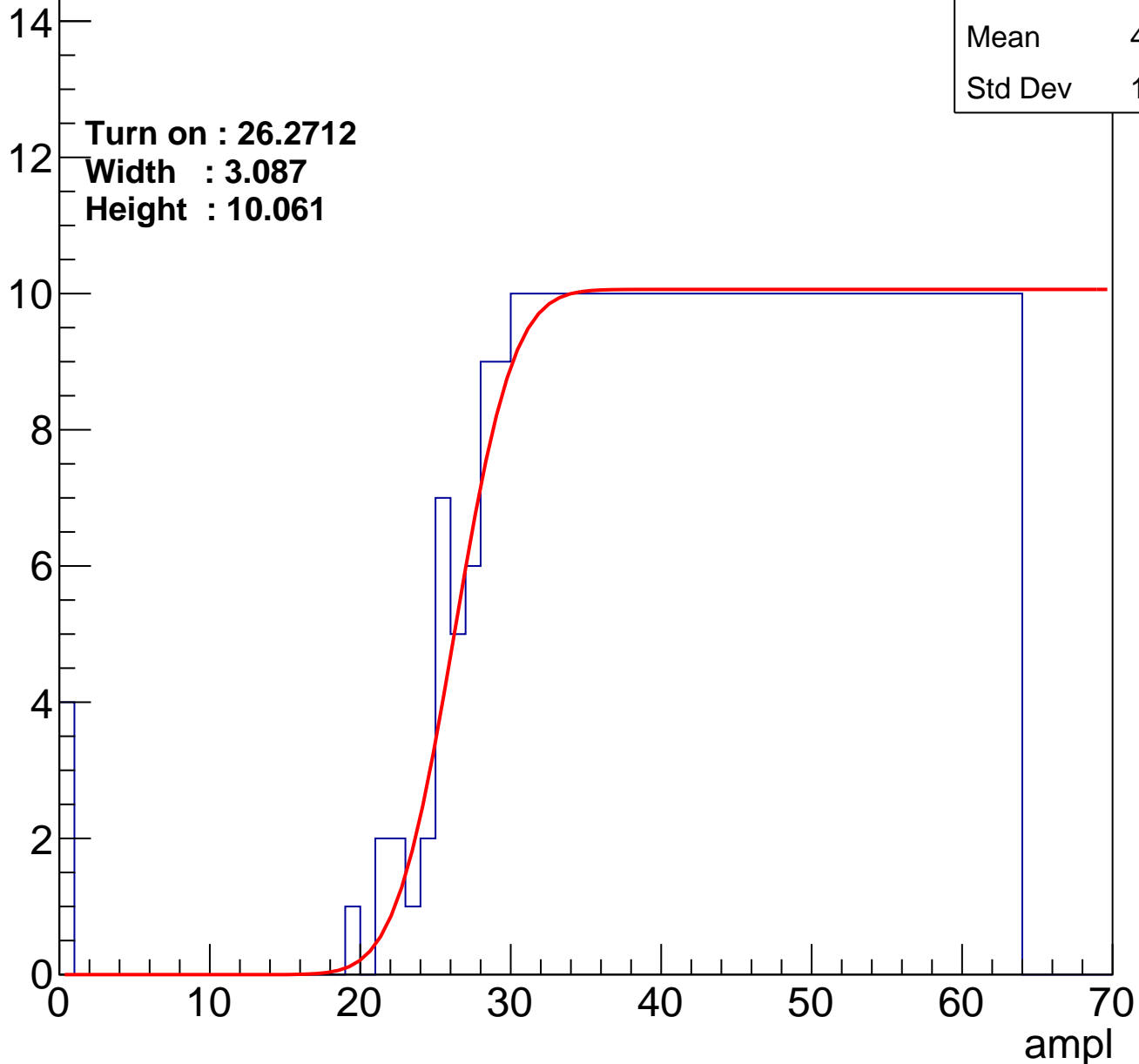
Entries	388
Mean	43.73
Std Dev	12.09

Turn on : 26.2712

Width : 3.087

Height : 10.061

Entry



B1L002S, U15-ch93

calib_packv5_042523_0143.root, FC#10, port B3

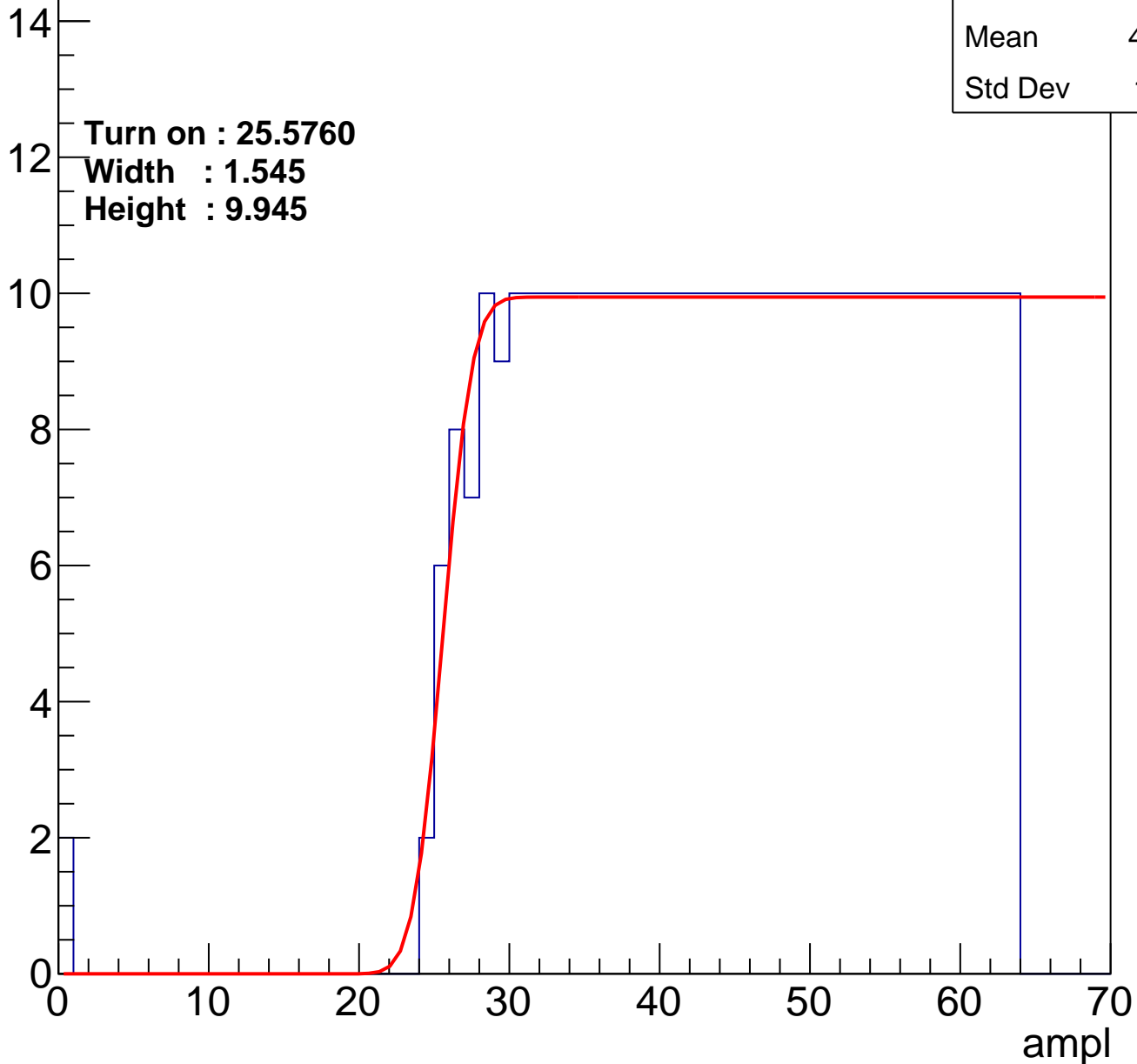
Entries	384
Mean	44.13
Std Dev	11.51

Turn on : 25.5760

Width : 1.545

Height : 9.945

Entry



B1L002S, U15-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.04
Std Dev	11.48

Turn on : 25.7981

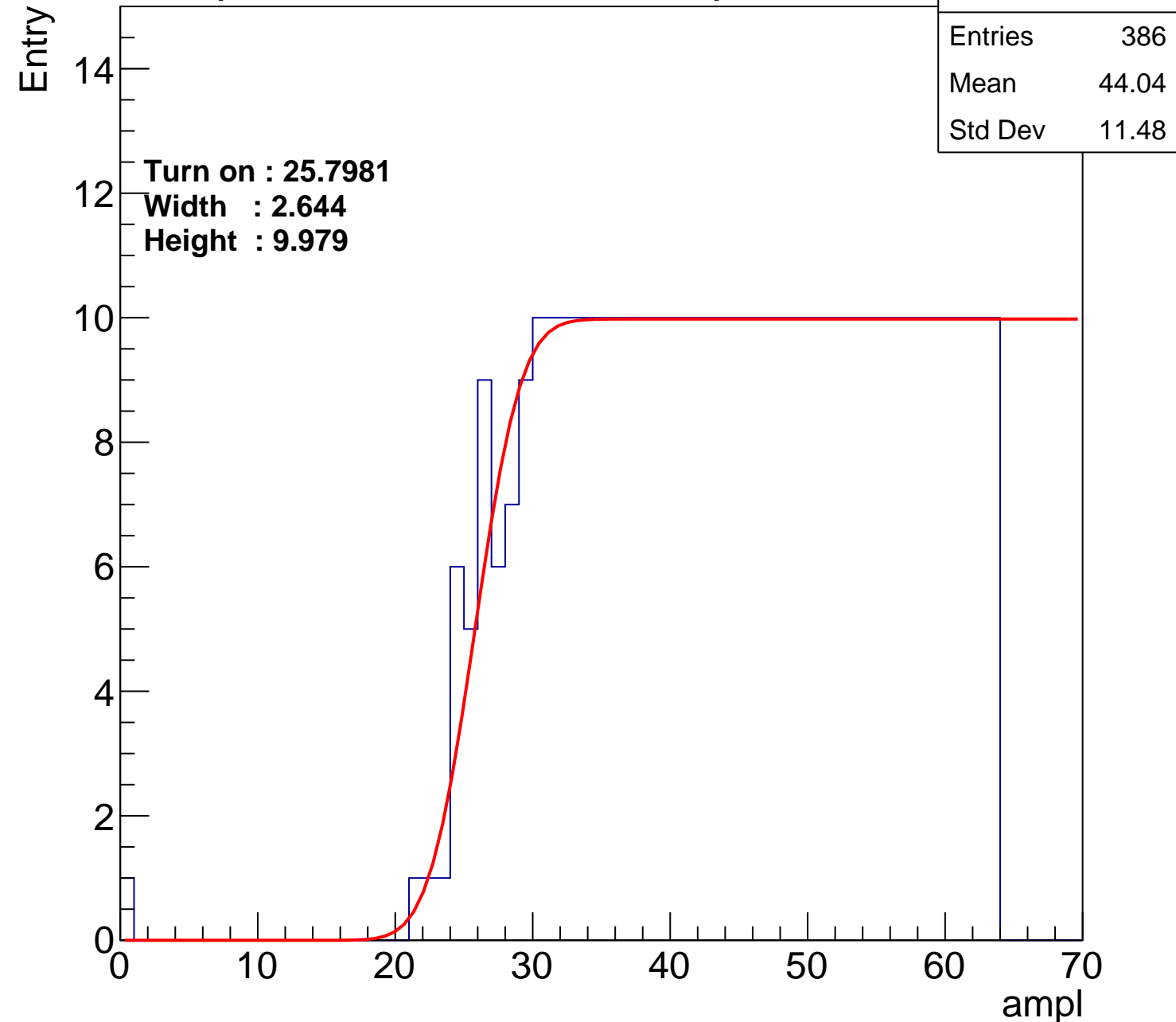
Width : 2.644

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 24.6222
Width : 3.233
Height : 10.075



B1L002S, U15-ch96

calib_packv5_042523_0143.root, FC#10, port B3

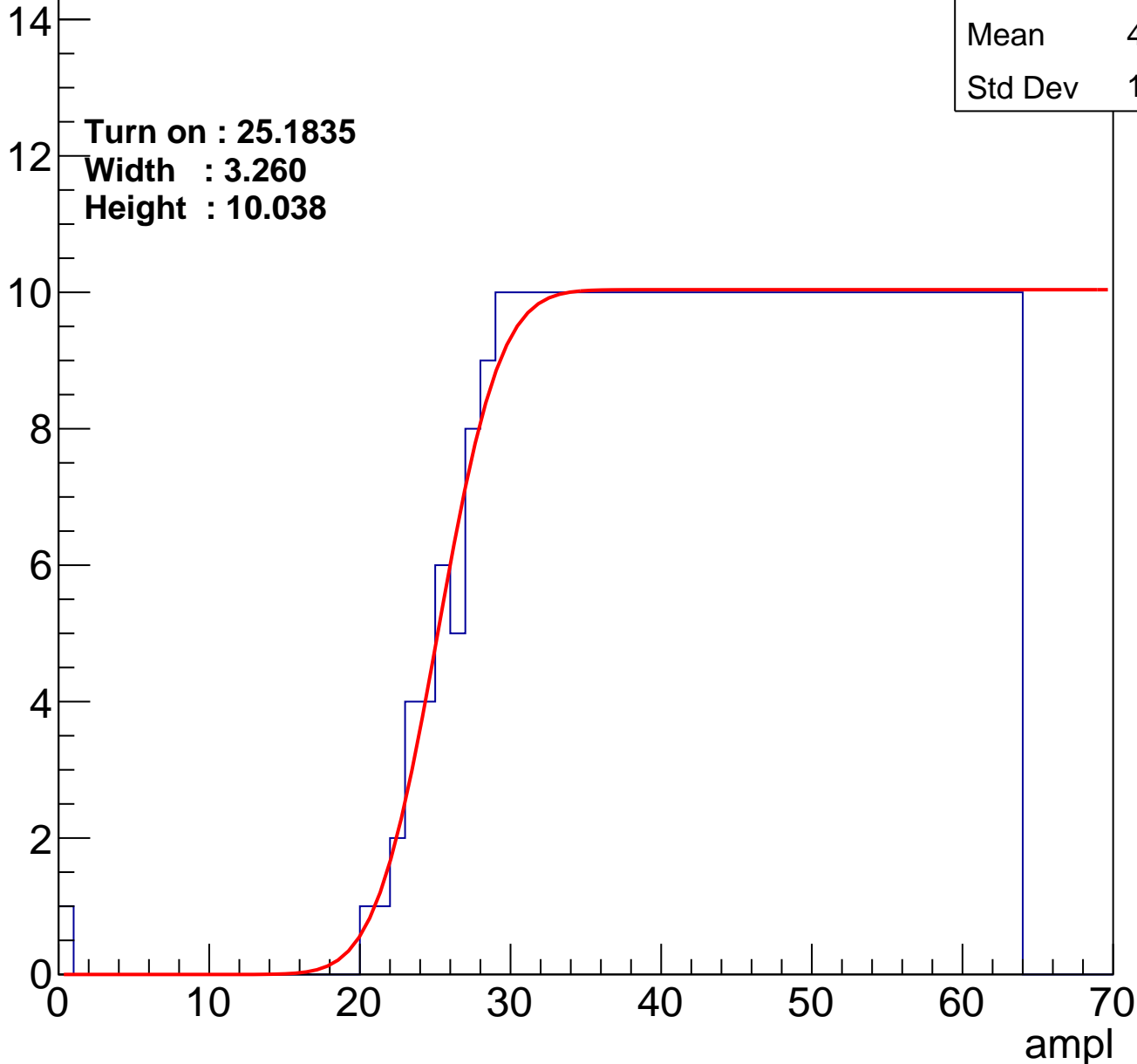
Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 25.1835

Width : 3.260

Height : 10.038

Entry



B1L002S, U15-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.9
Std Dev	11.84

Turn on : 26.2073

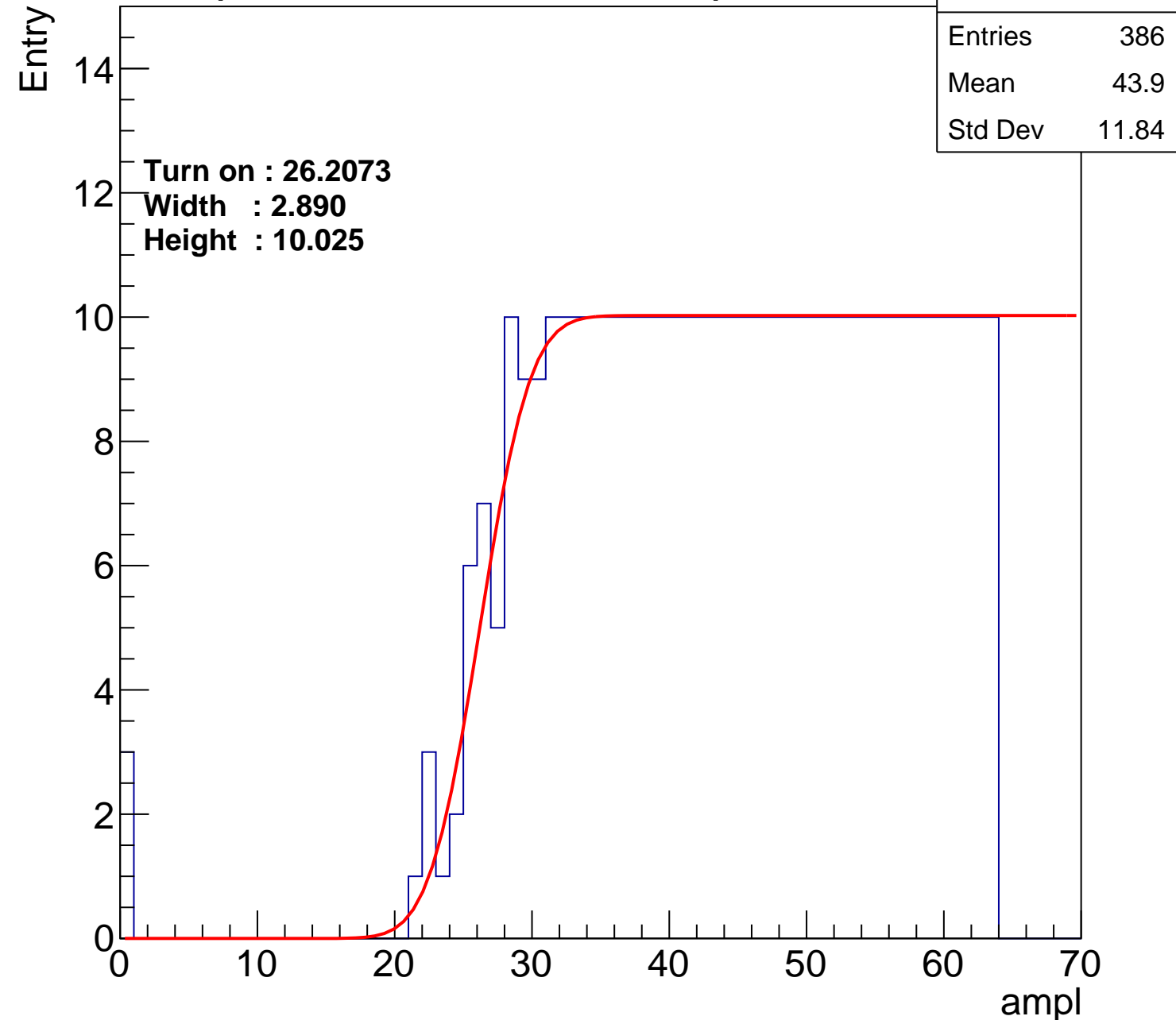
Width : 2.890

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	417
Mean	42.35
Std Dev	12.66

Turn on : 22.9448

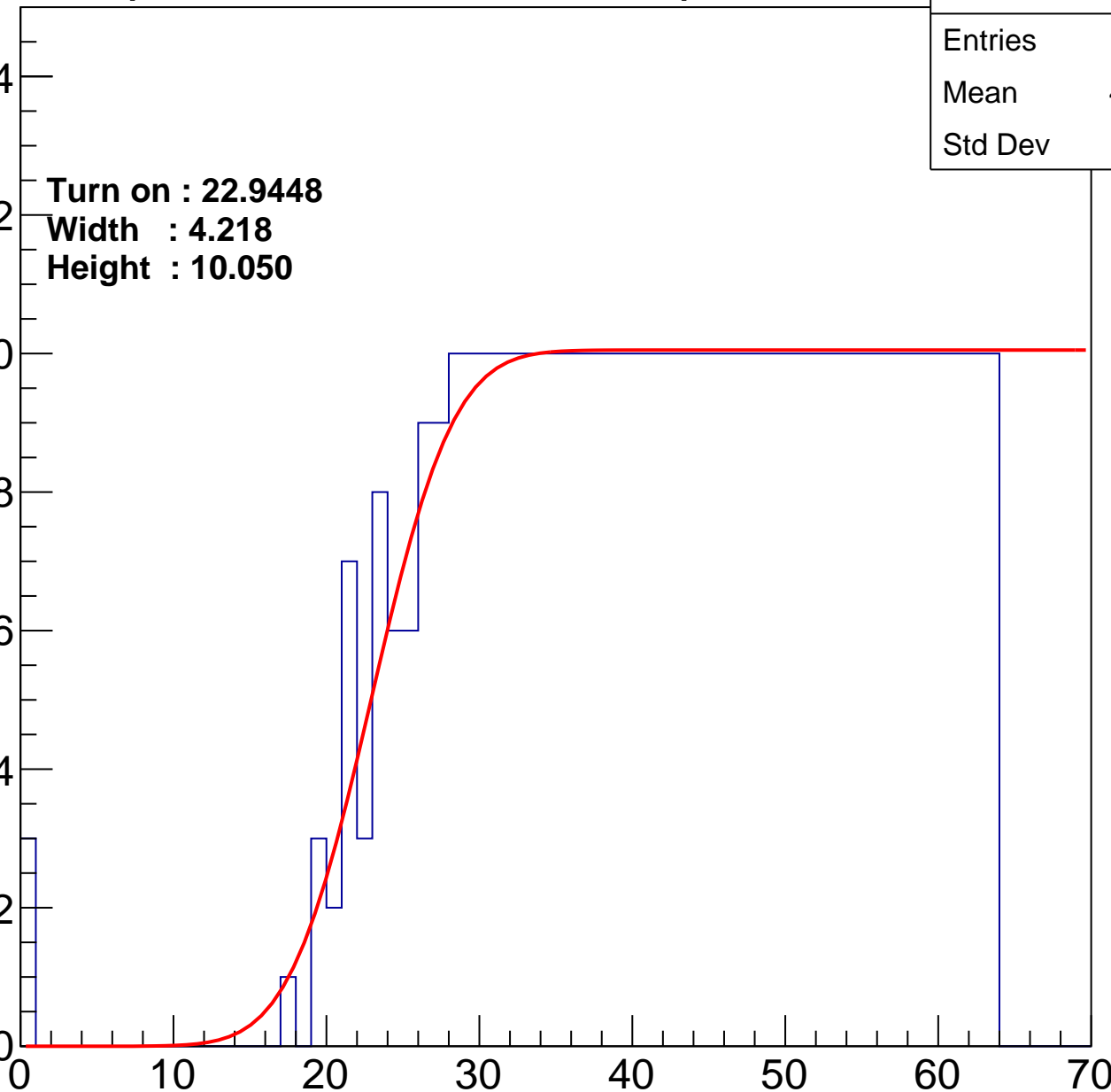
Width : 4.218

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.78
Std Dev	12.41

Turn on : 23.4864

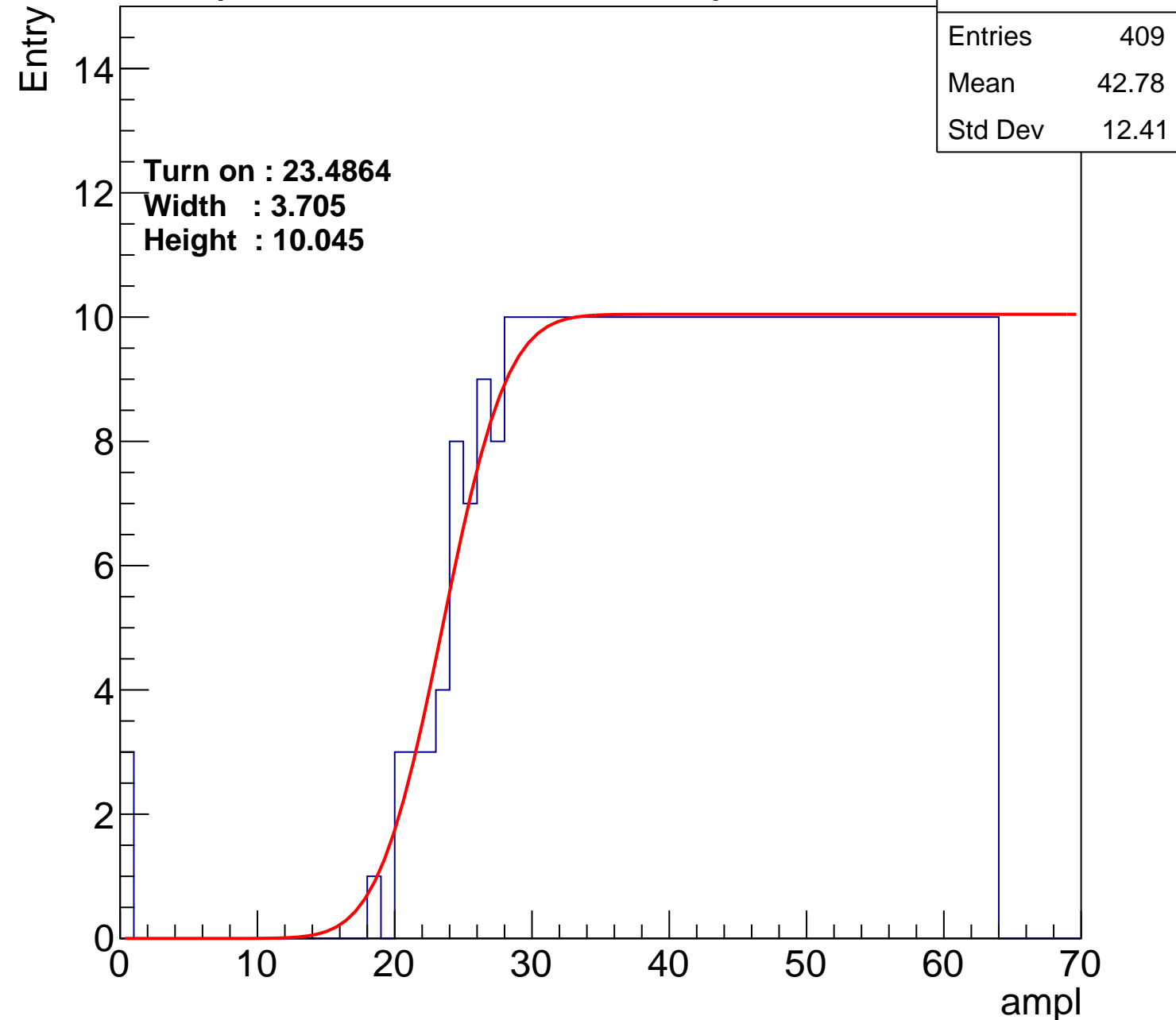
Width : 3.705

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.84

Turn on : 25.4817

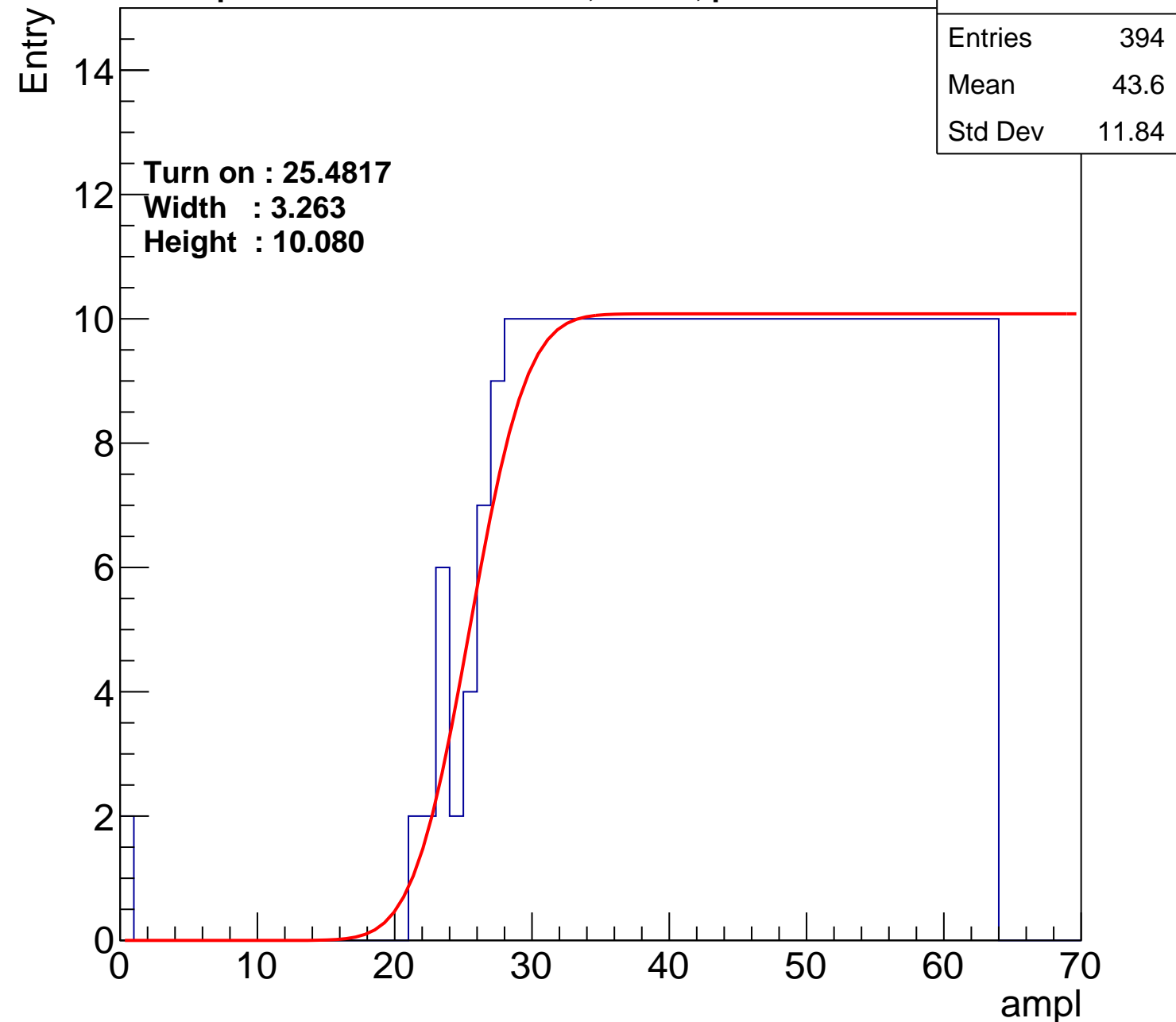
Width : 3.263

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch101

calib_packv5_042523_0143.root, FC#10, port B3

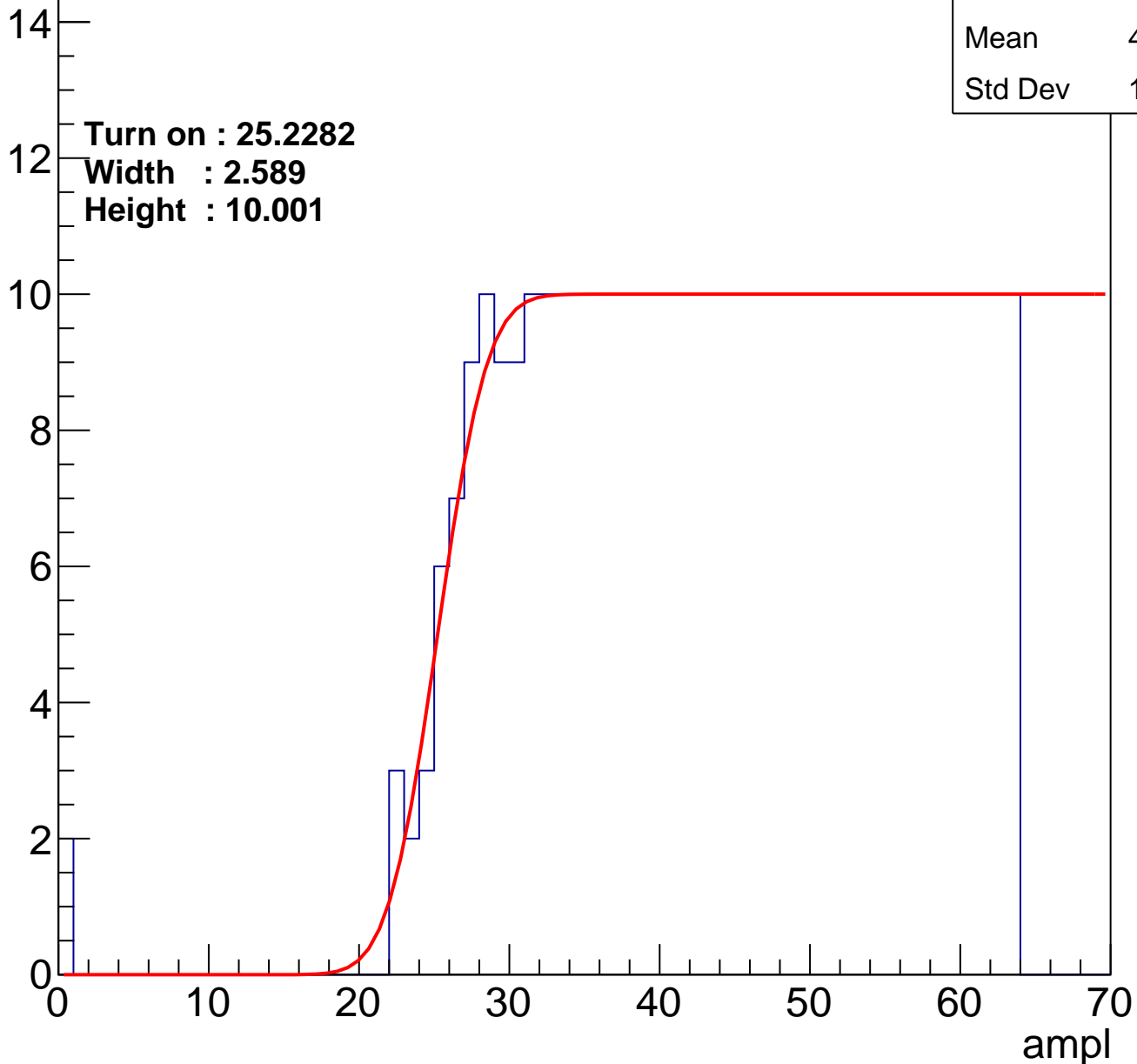
Entries	390
Mean	43.79
Std Dev	11.73

Turn on : 25.2282

Width : 2.589

Height : 10.001

Entry



B1L002S, U15-ch102

calib_packv5_042523_0143.root, FC#10, port B3

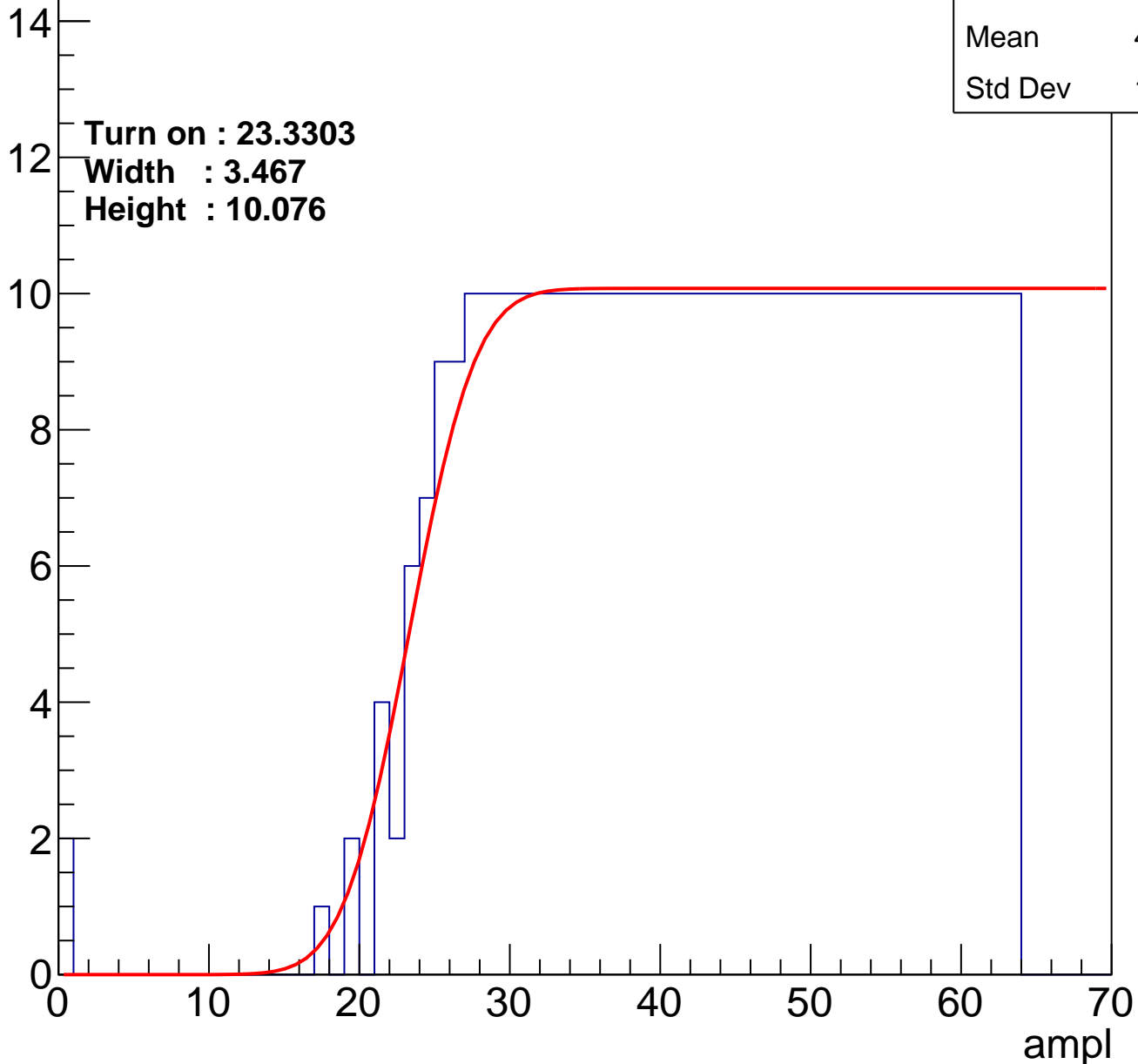
Entries	412
Mean	42.71
Std Dev	12.31

Turn on : 23.3303

Width : 3.467

Height : 10.076

Entry



B1L002S, U15-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.5040

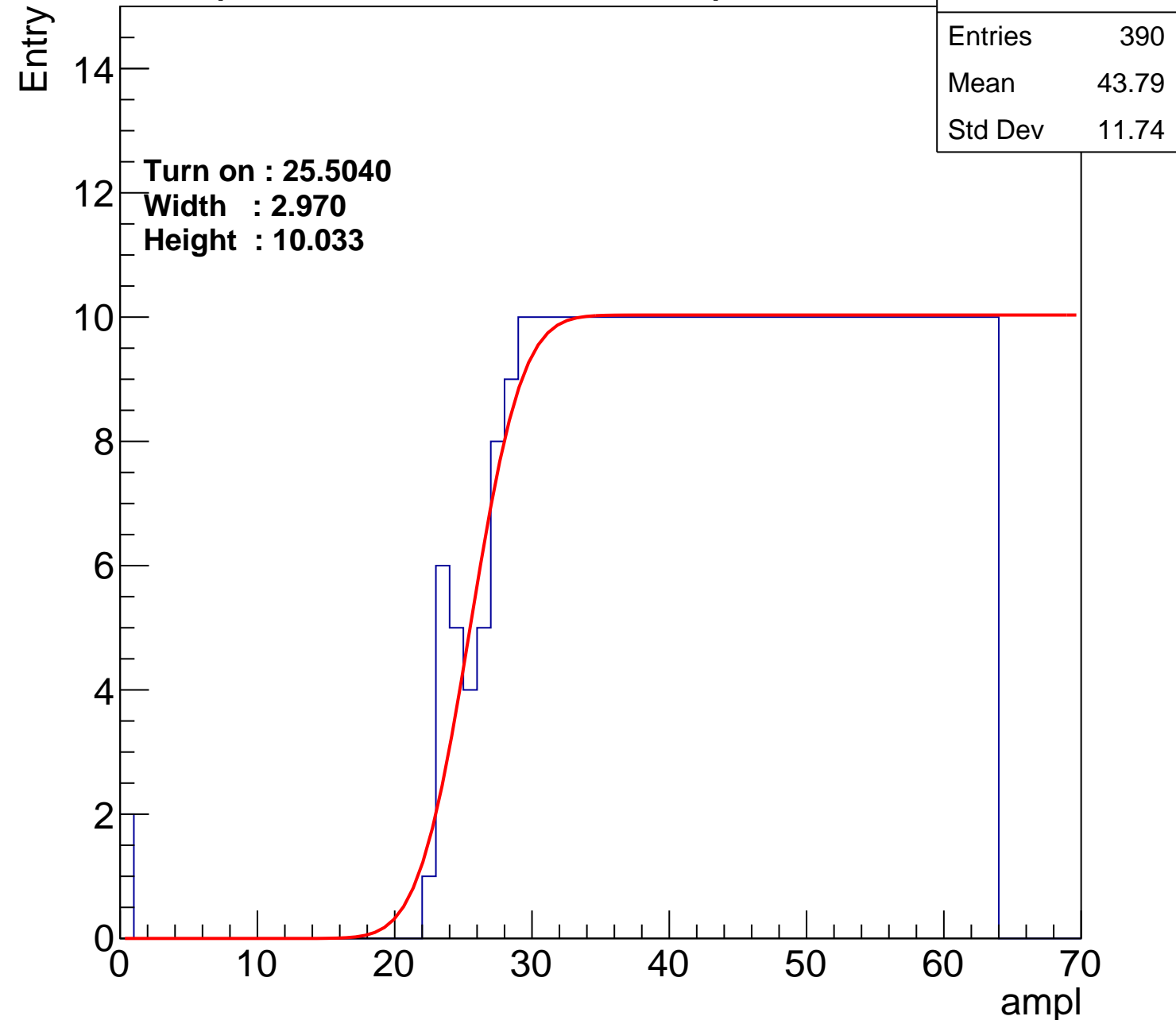
Width : 2.970

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch104

calib_packv5_042523_0143.root, FC#10, port B3

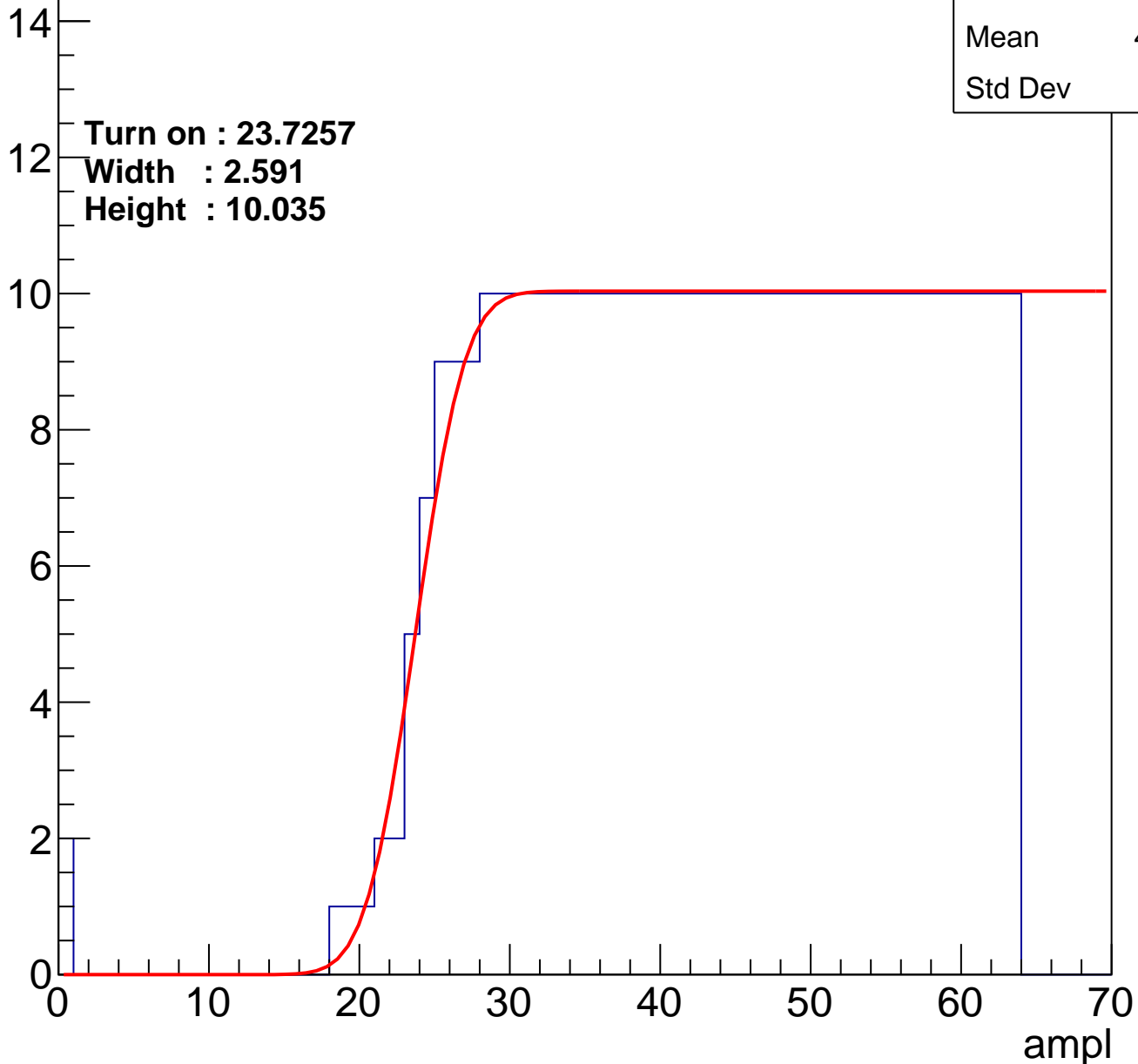
Entries	408
Mean	42.91
Std Dev	12.2

Turn on : 23.7257

Width : 2.591

Height : 10.035

Entry



B1L002S, U15-ch105

calib_packv5_042523_0143.root, FC#10, port B3

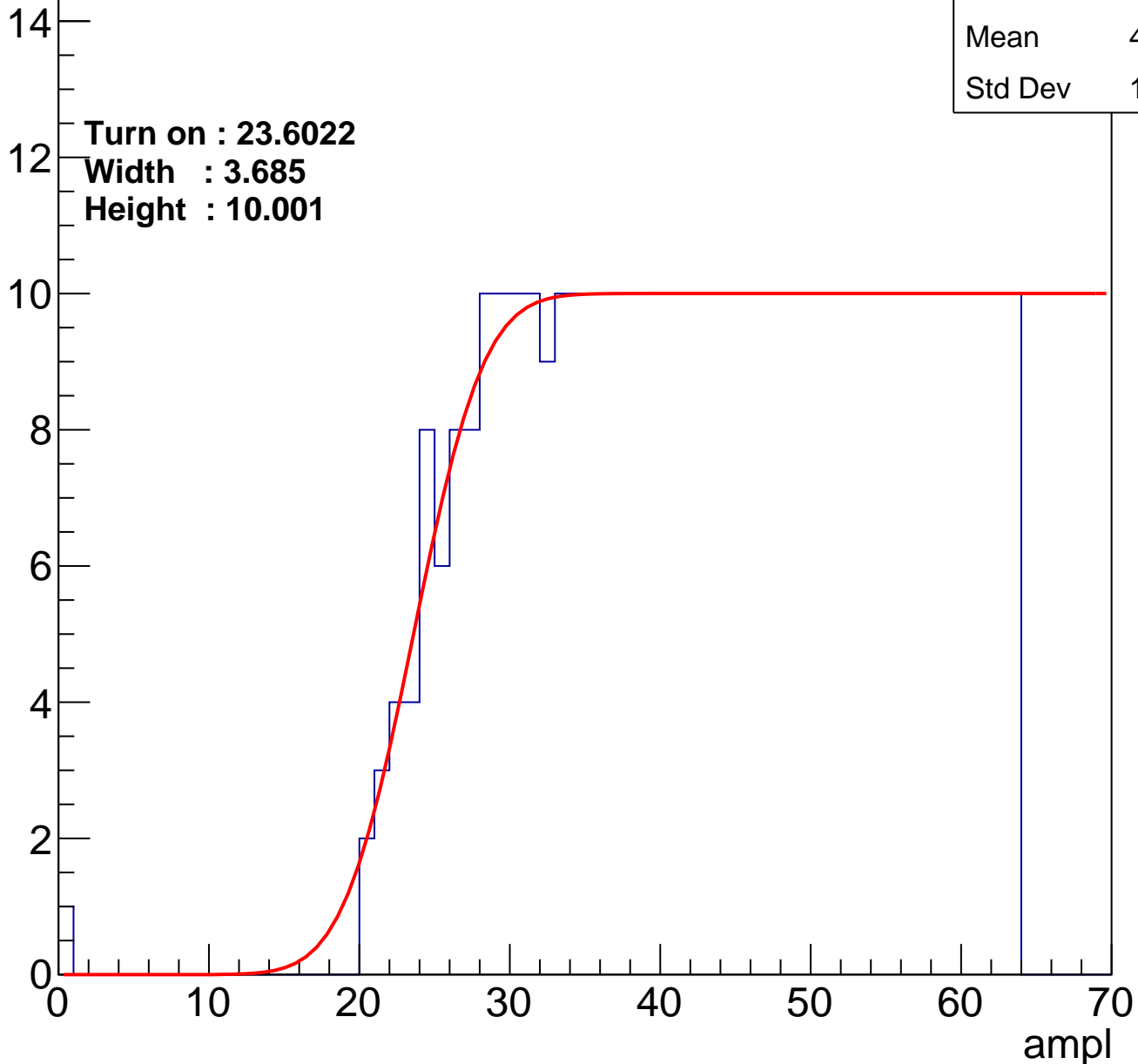
Entries	403
Mean	43.17
Std Dev	11.98

Turn on : 23.6022

Width : 3.685

Height : 10.001

Entry



B1L002S, U15-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.6
Std Dev	12

Turn on : 25.3507

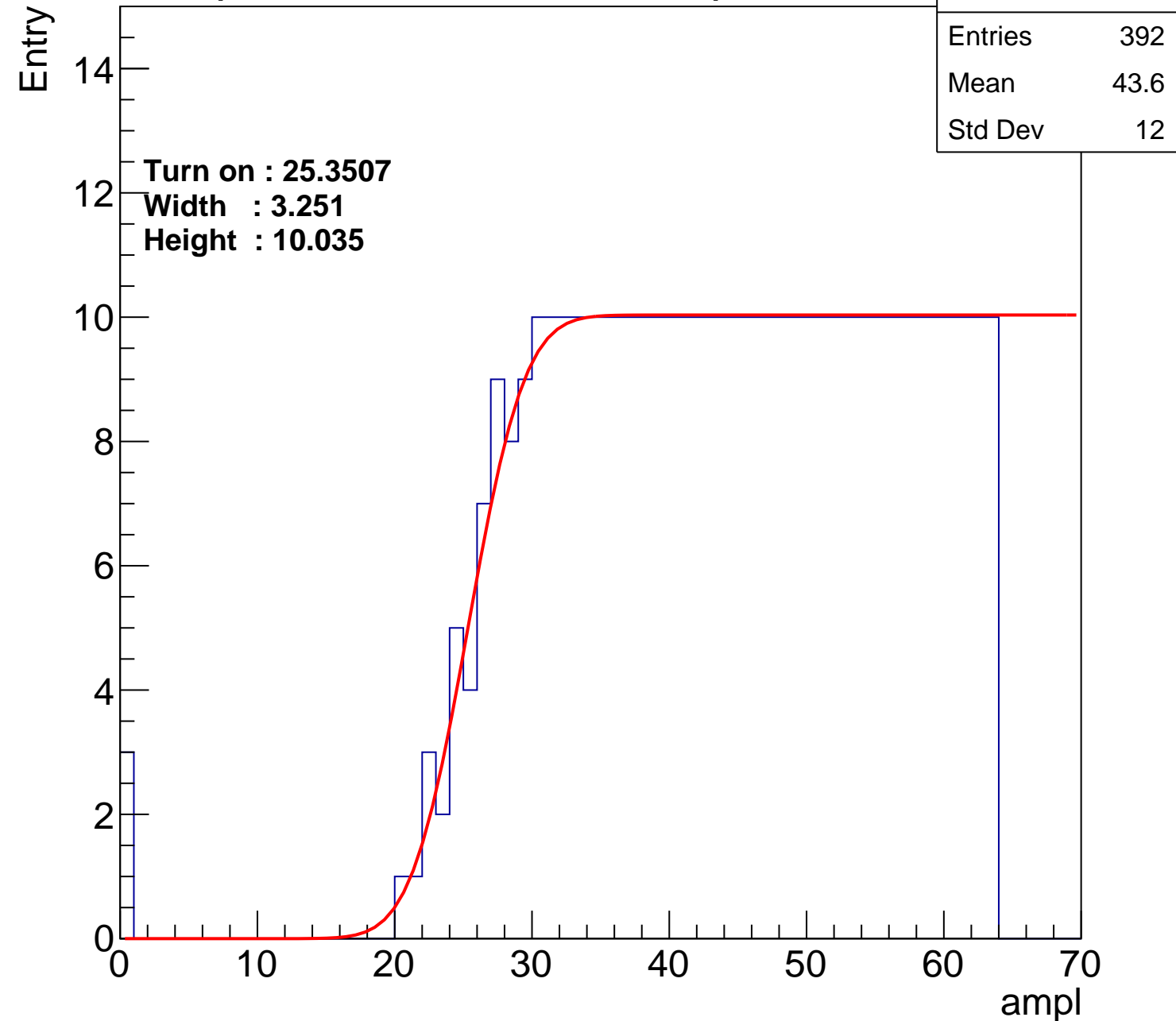
Width : 3.251

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 26.8026

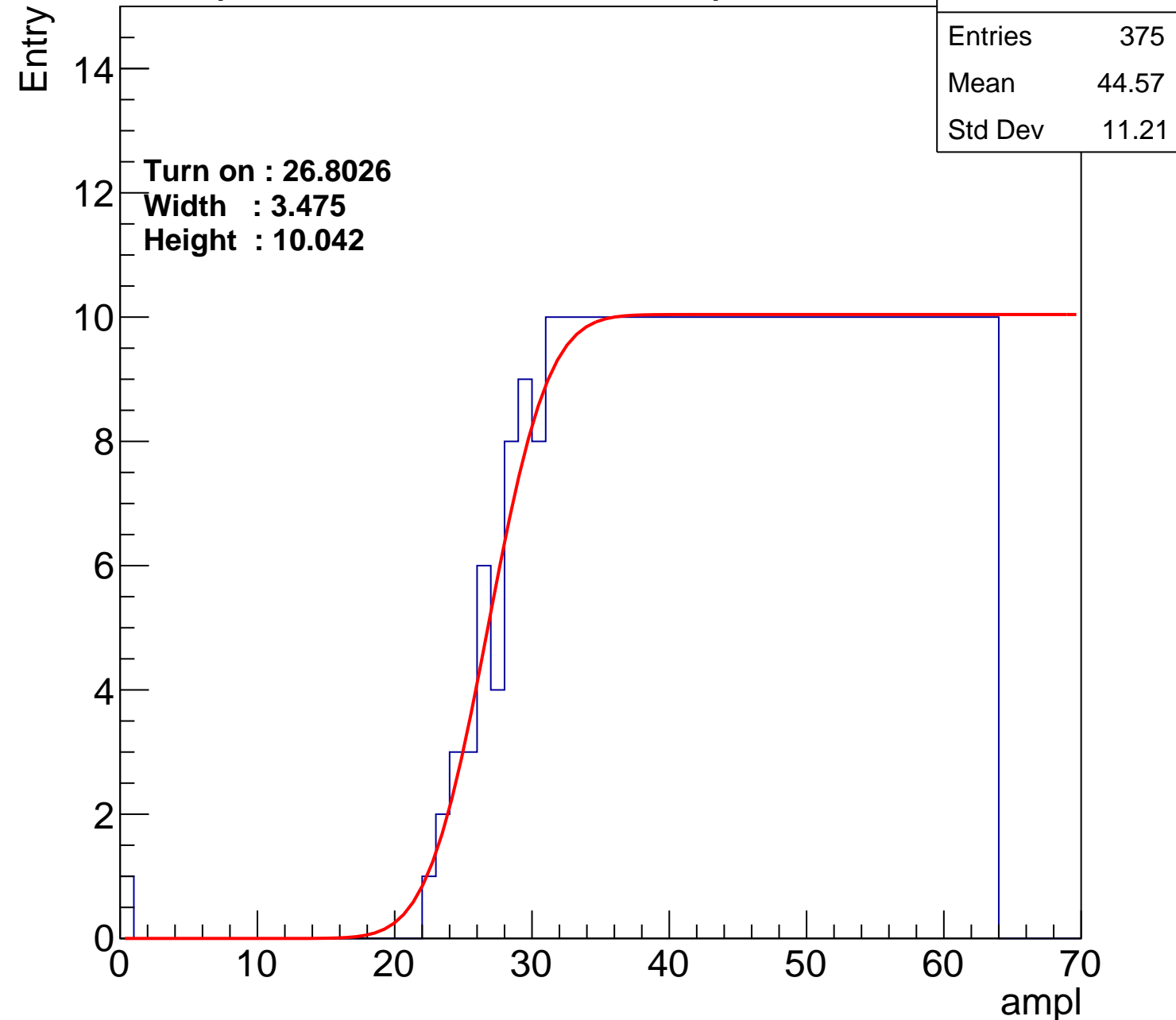
Width : 3.475

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.3
Std Dev	12.11

Turn on : 24.6789

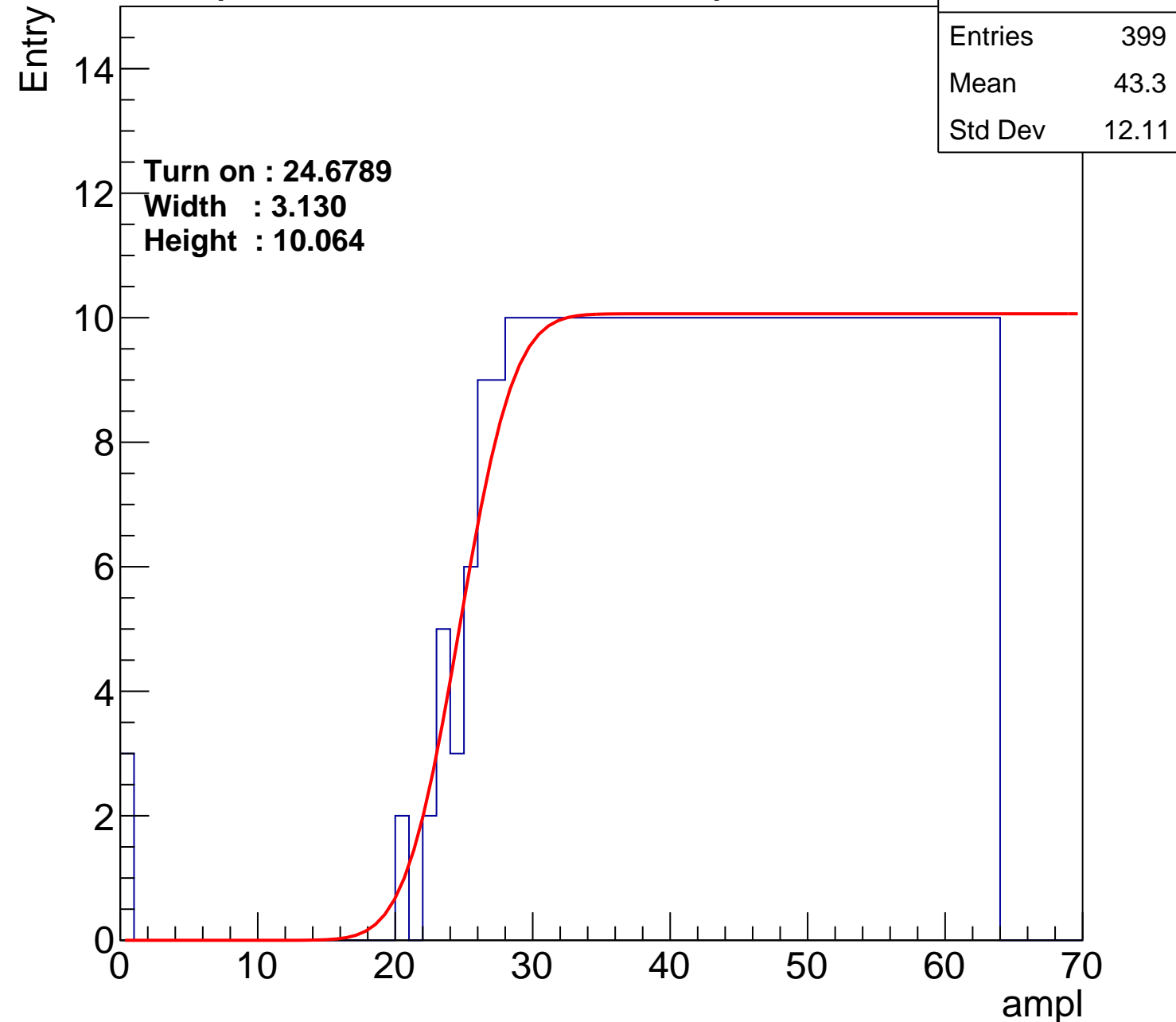
Width : 3.130

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch109

calib_packv5_042523_0143.root, FC#10, port B3

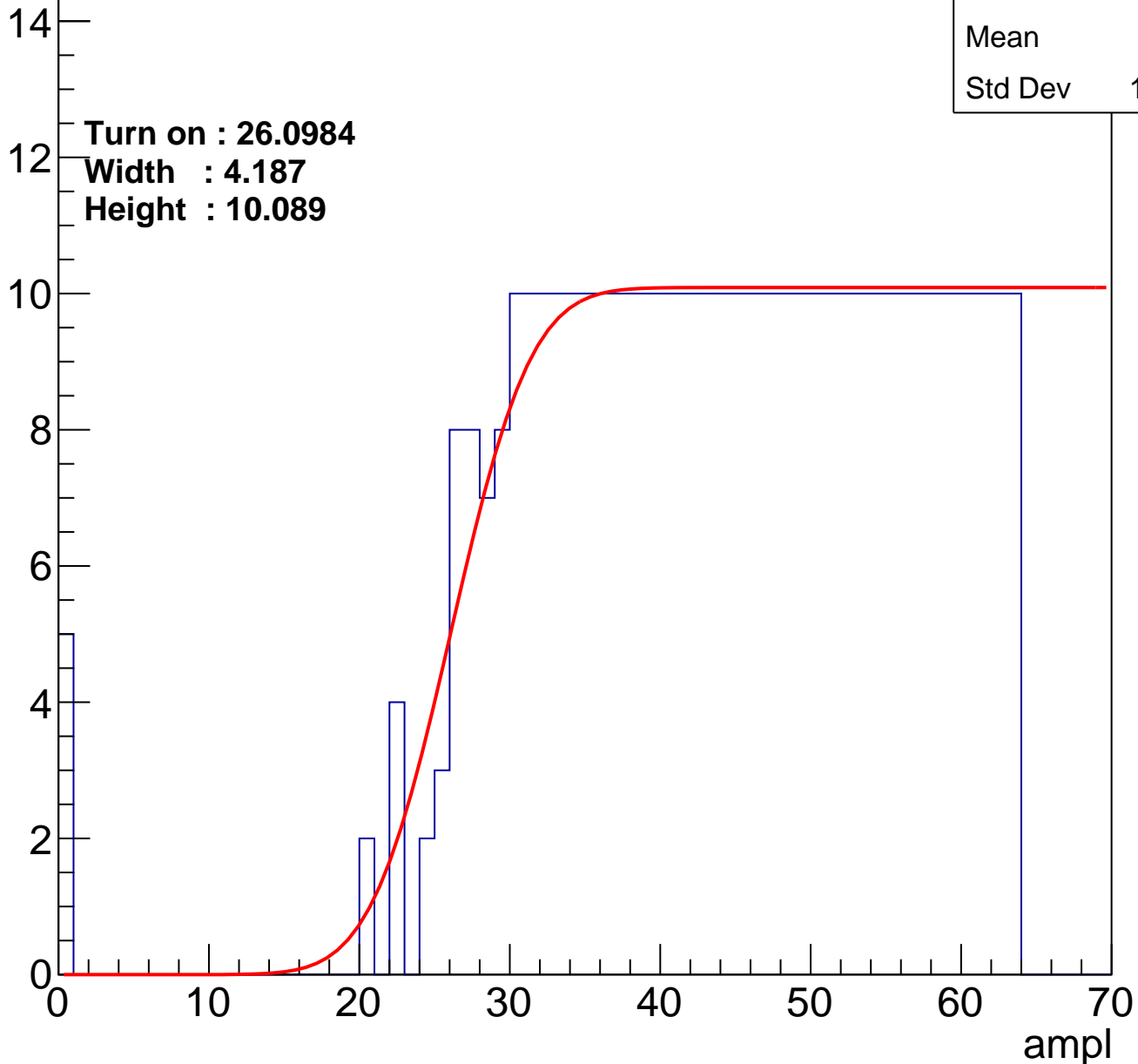
Entries	387
Mean	43.7
Std Dev	12.24

Turn on : 26.0984

Width : 4.187

Height : 10.089

Entry



B1L002S, U15-ch110

calib_packv5_042523_0143.root, FC#10, port B3

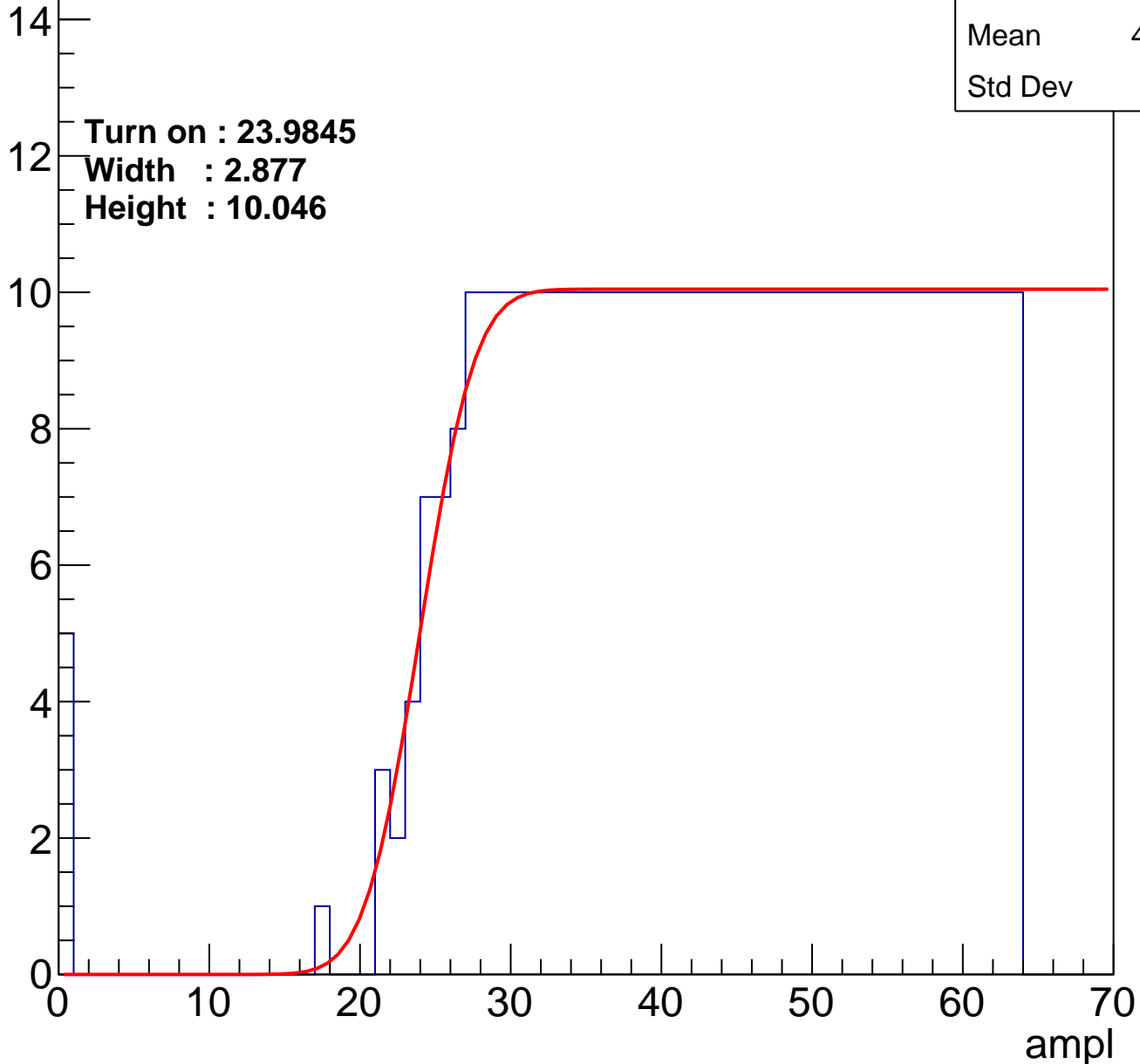
Entries	407
Mean	42.79
Std Dev	12.6

Turn on : 23.9845

Width : 2.877

Height : 10.046

Entry



B1L002S, U15-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.3
Std Dev	12.22

Turn on : 24.2123

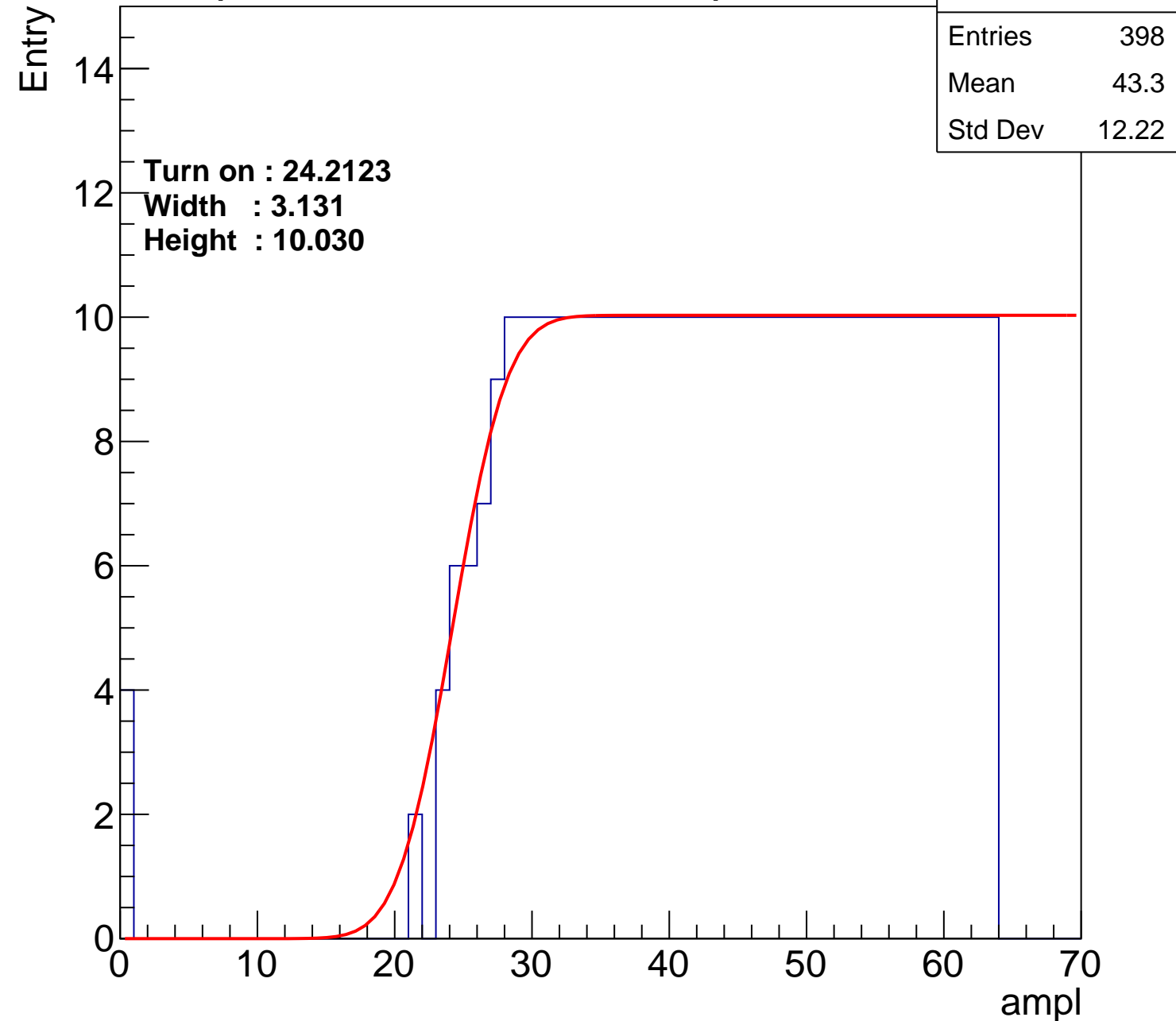
Width : 3.131

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.36
Std Dev	11.84

Turn on : 23.8630

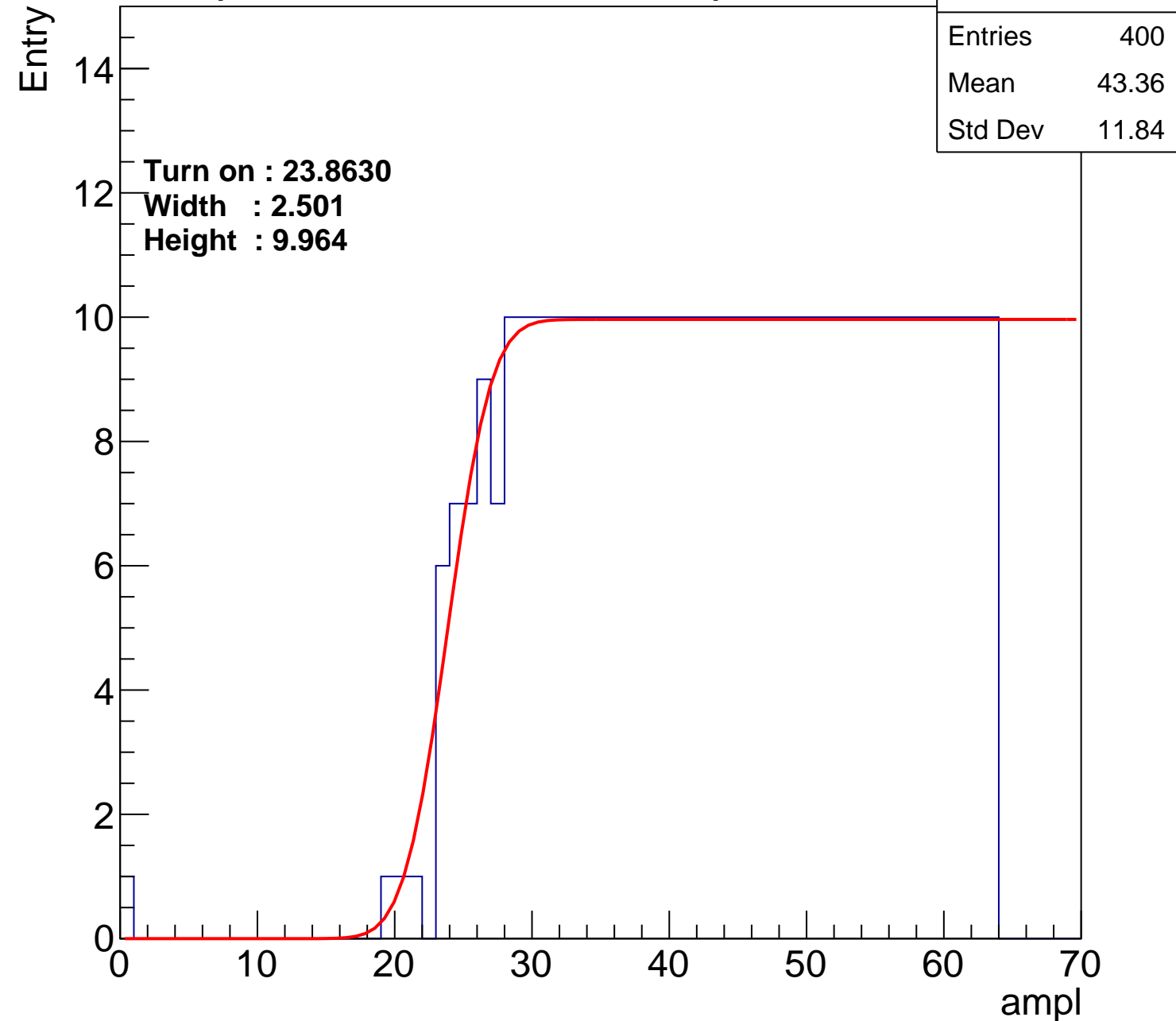
Width : 2.501

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.27
Std Dev	11.33

Turn on : 26.0835

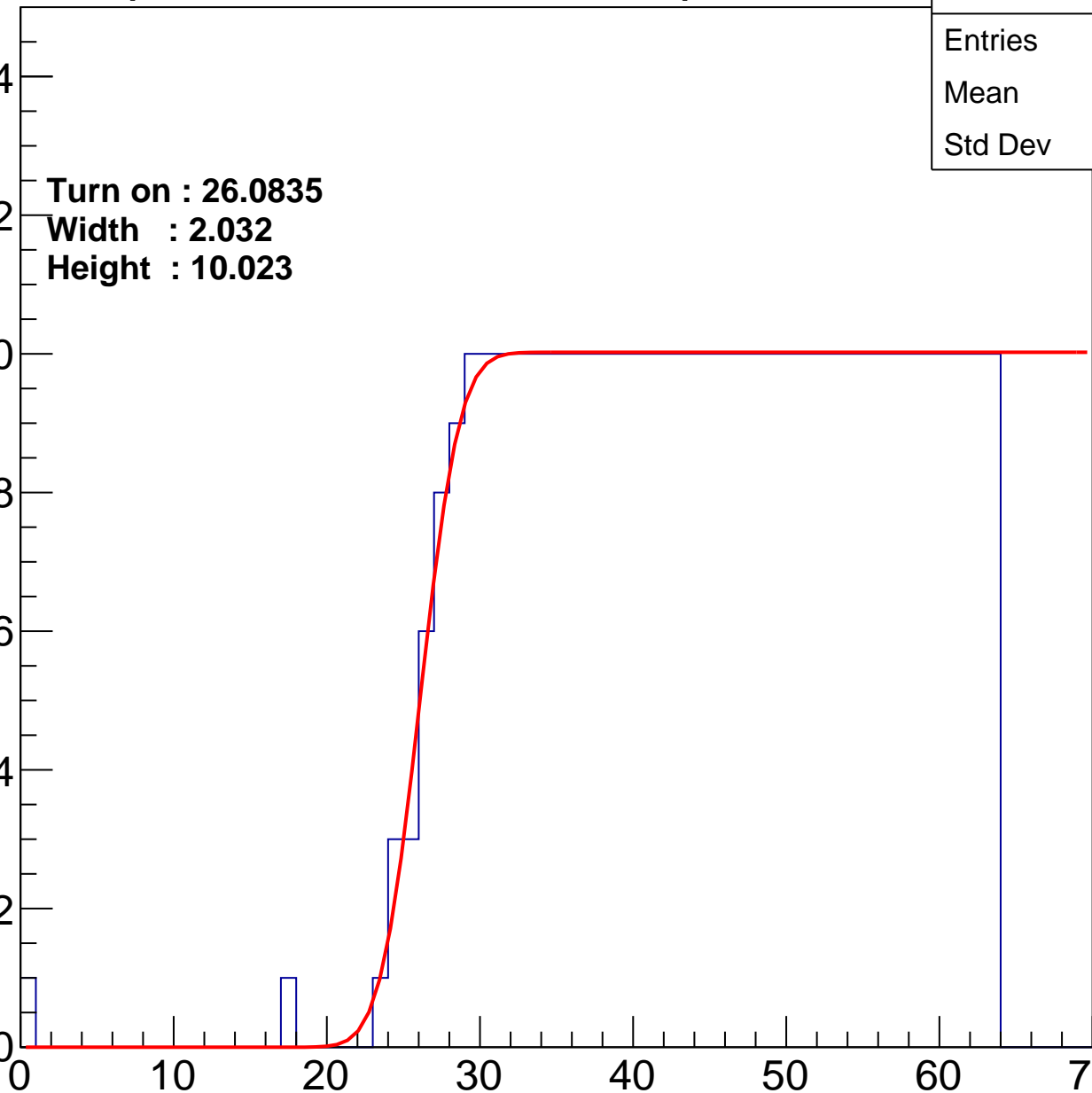
Width : 2.032

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.16
Std Dev	12.34

Turn on : 24.3087

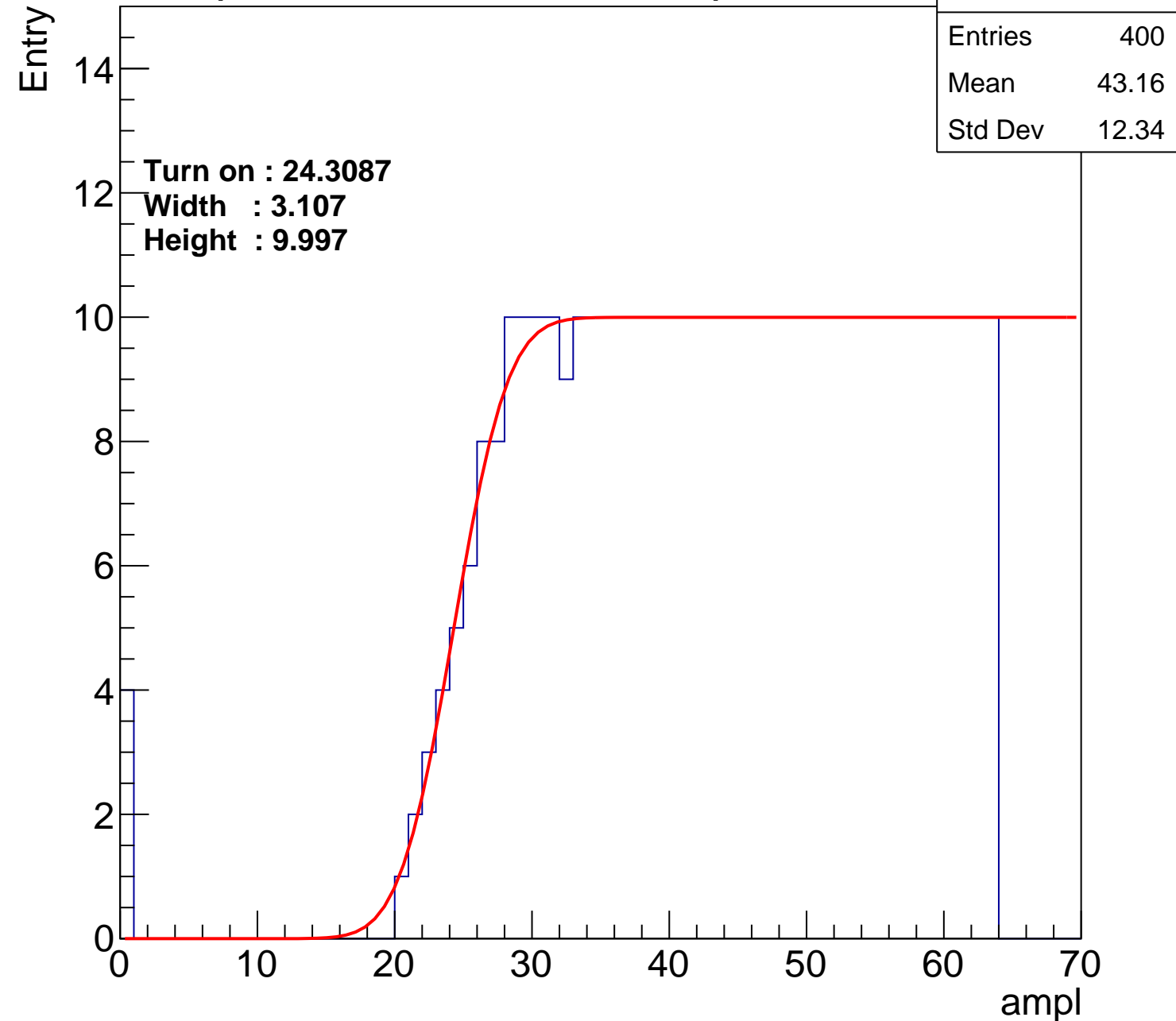
Width : 3.107

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch115

calib_packv5_042523_0143.root, FC#10, port B3

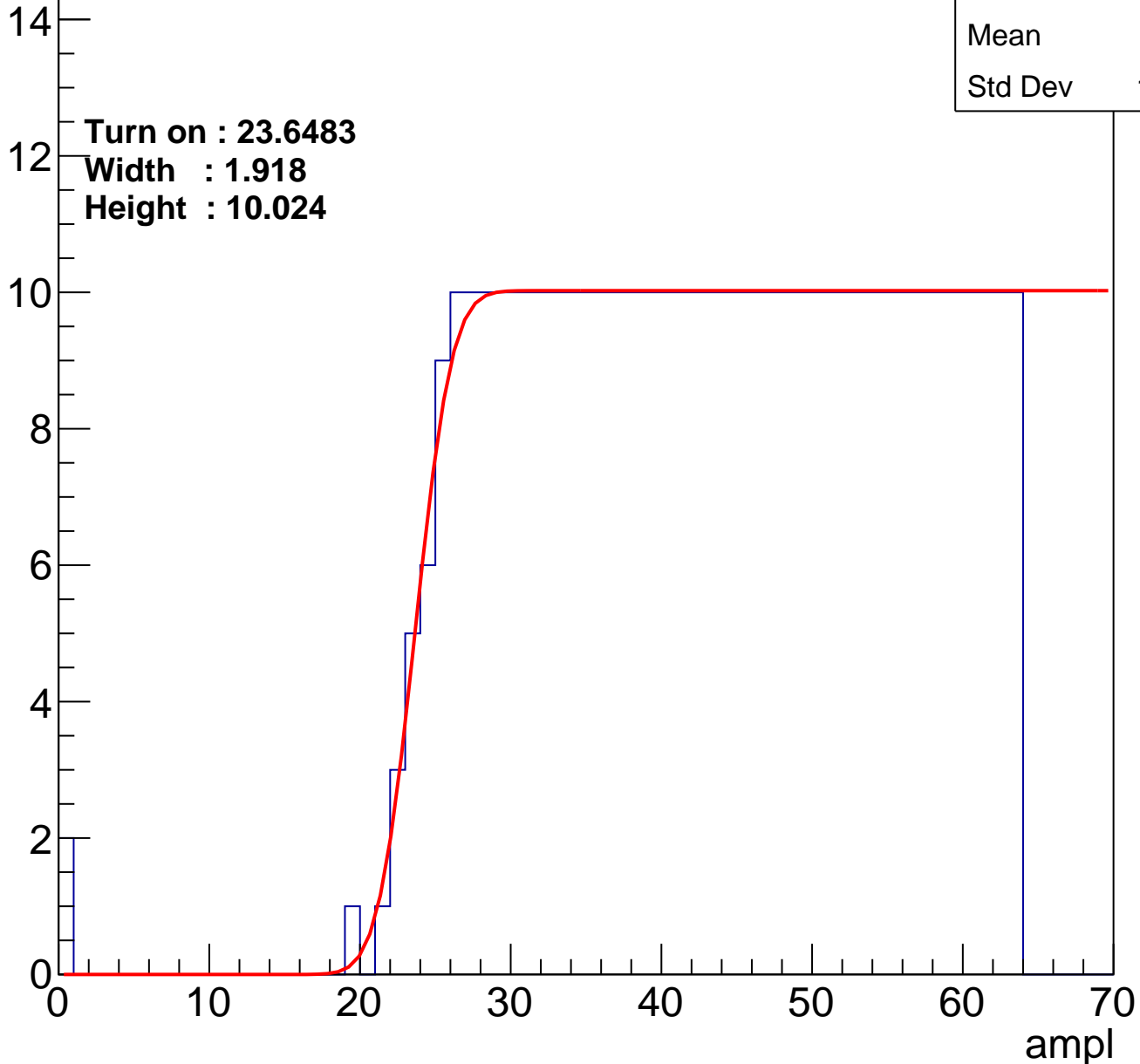
Entries	407
Mean	43
Std Dev	12.11

Turn on : 23.6483

Width : 1.918

Height : 10.024

Entry



B1L002S, U15-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.13
Std Dev	12.44

Turn on : 24.7583

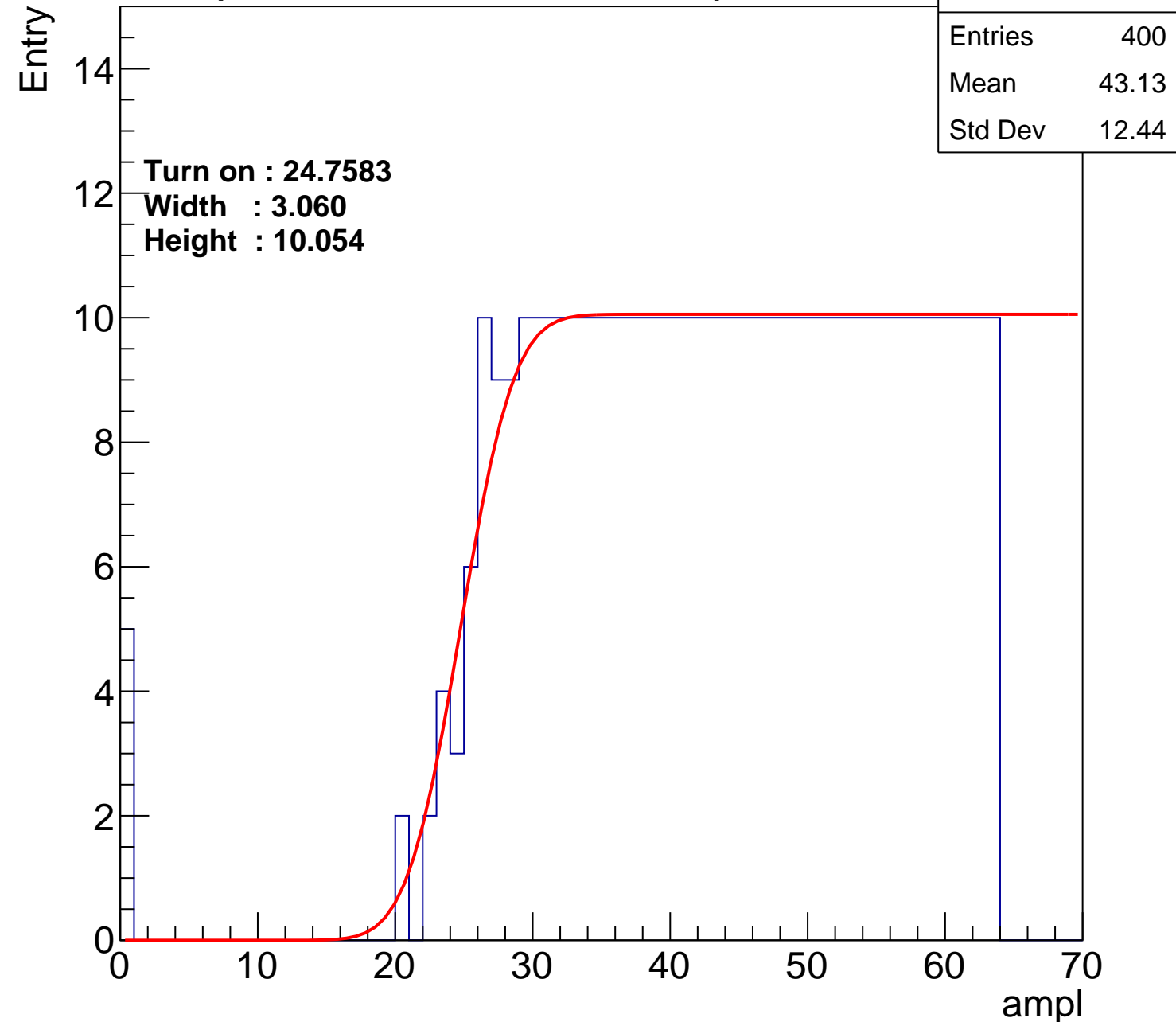
Width : 3.060

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch117

calib_packv5_042523_0143.root, FC#10, port B3

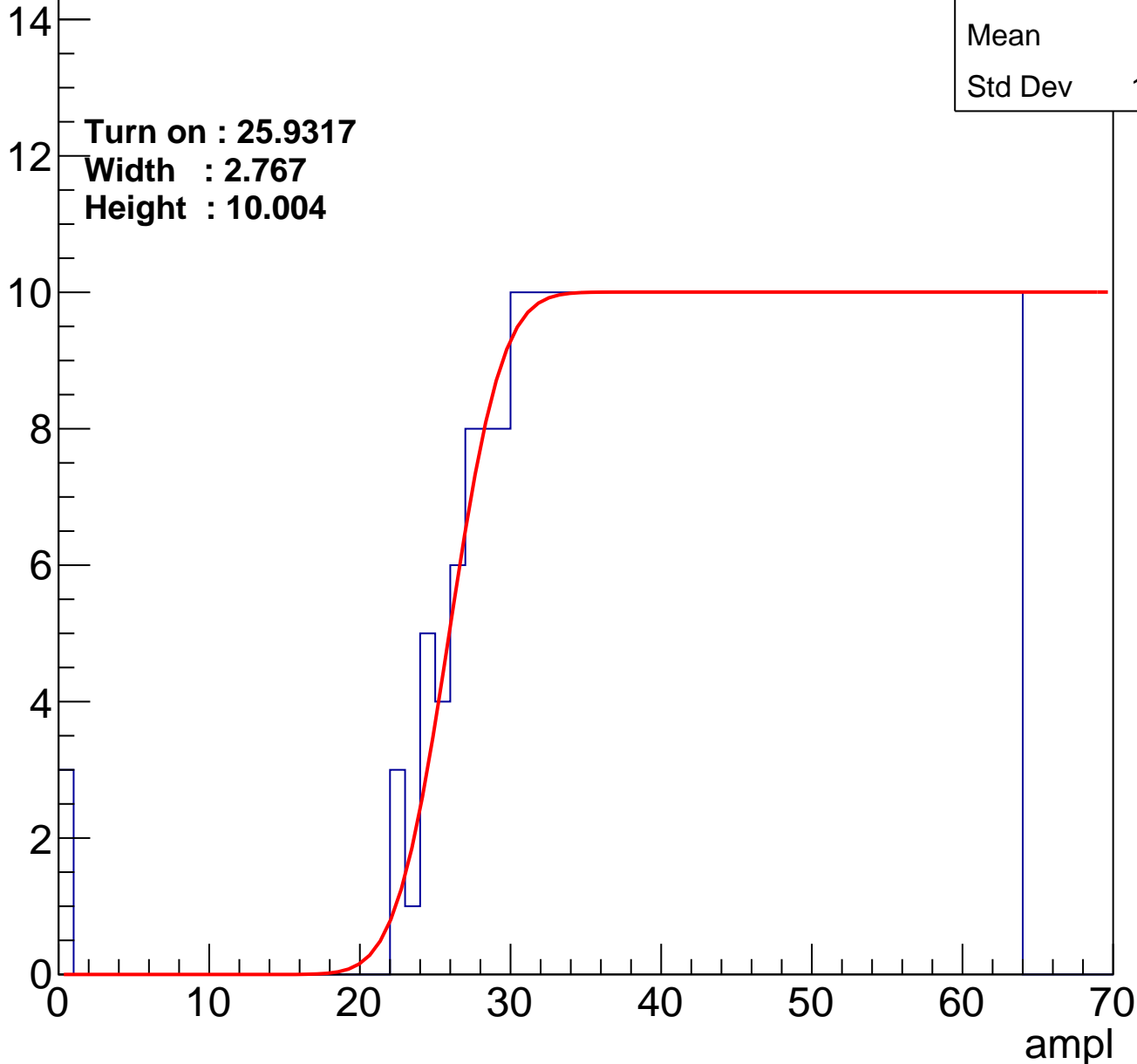
Entries	386
Mean	43.9
Std Dev	11.84

Turn on : 25.9317

Width : 2.767

Height : 10.004

Entry



B1L002S, U15-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.15
Std Dev	11.47

Turn on : 26.5906

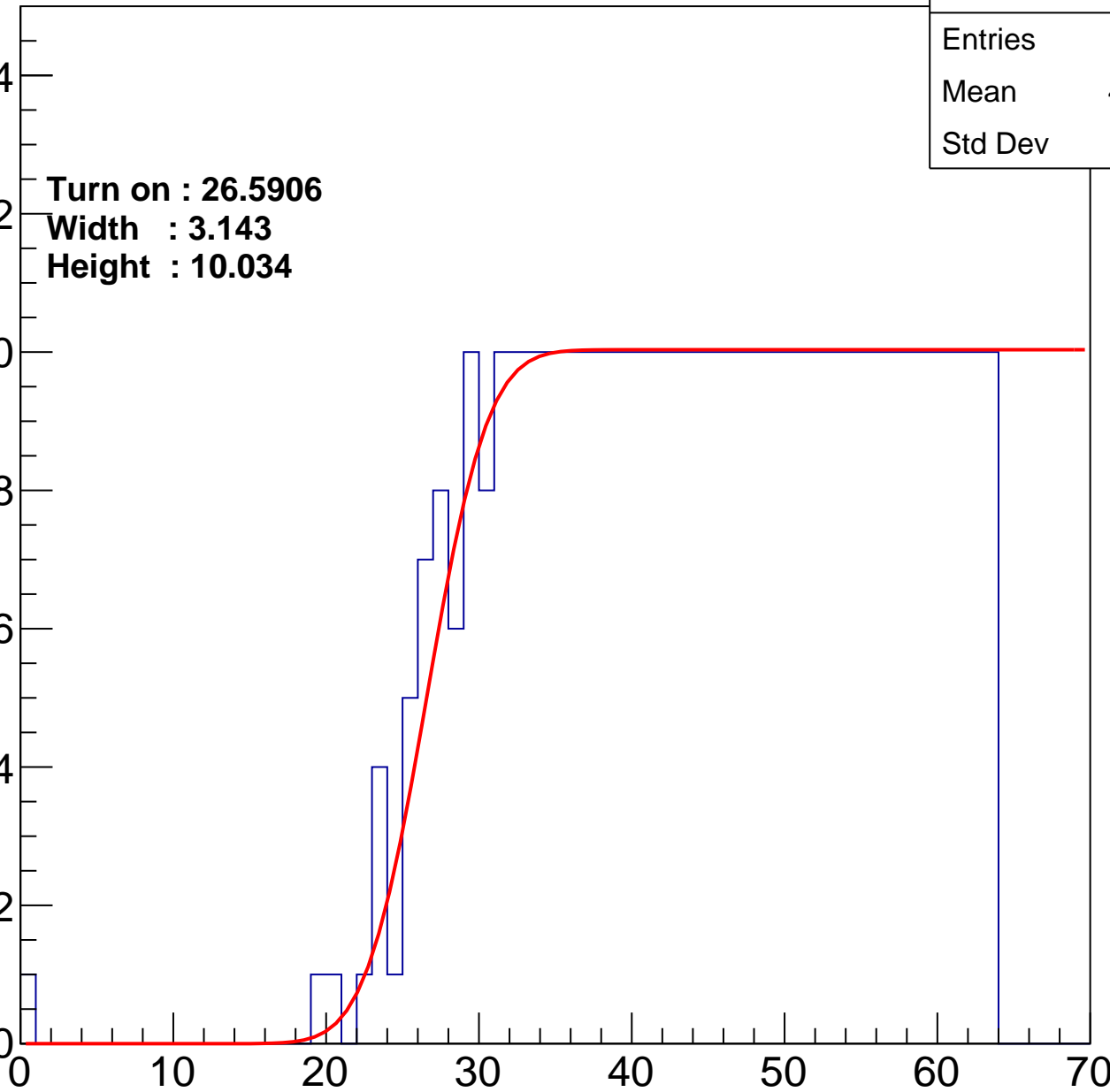
Width : 3.143

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch119

calib_packv5_042523_0143.root, FC#10, port B3

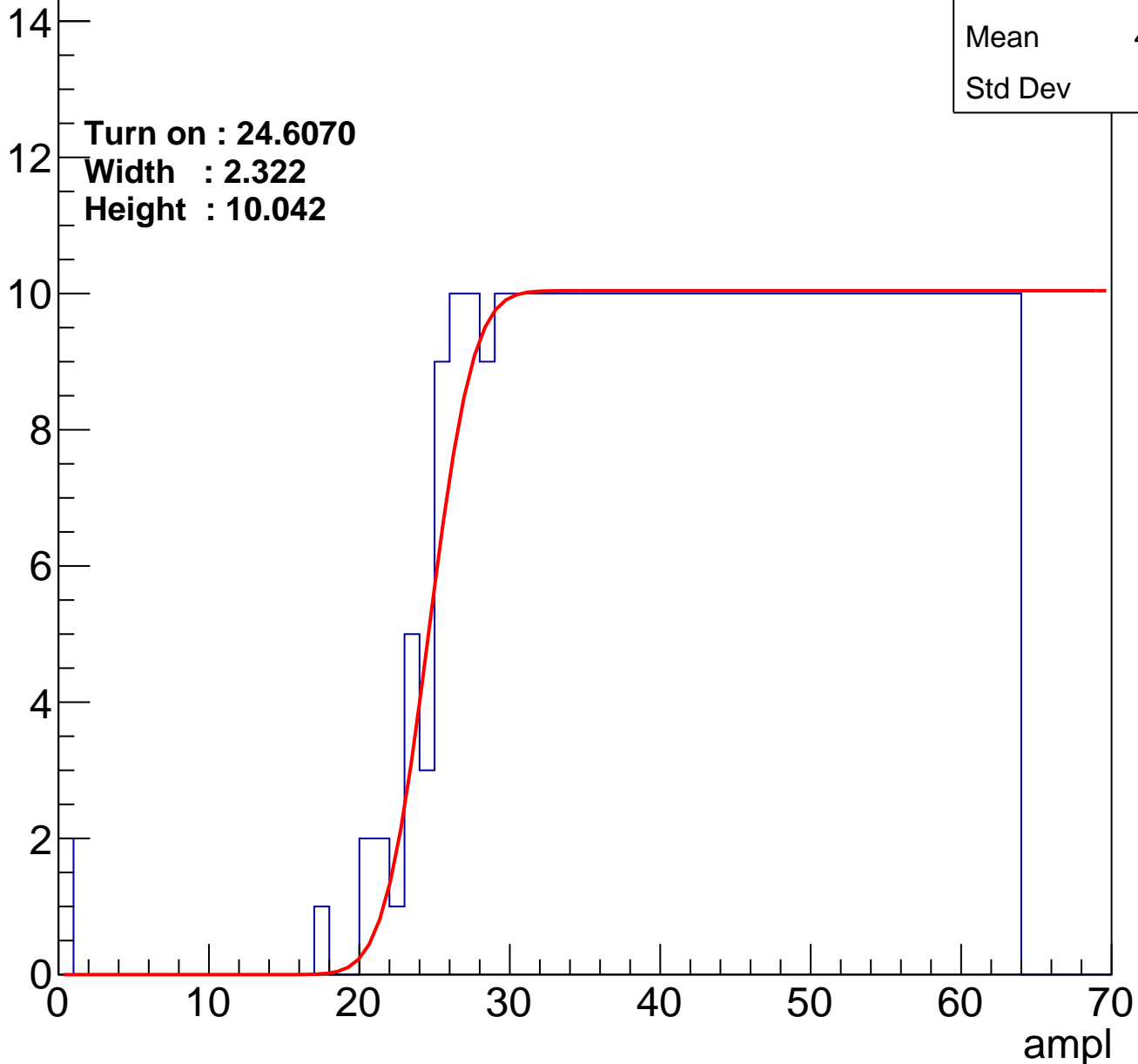
Entries	404
Mean	43.11
Std Dev	12.1

Turn on : 24.6070

Width : 2.322

Height : 10.042

Entry



B1L002S, U15-ch120

calib_packv5_042523_0143.root, FC#10, port B3

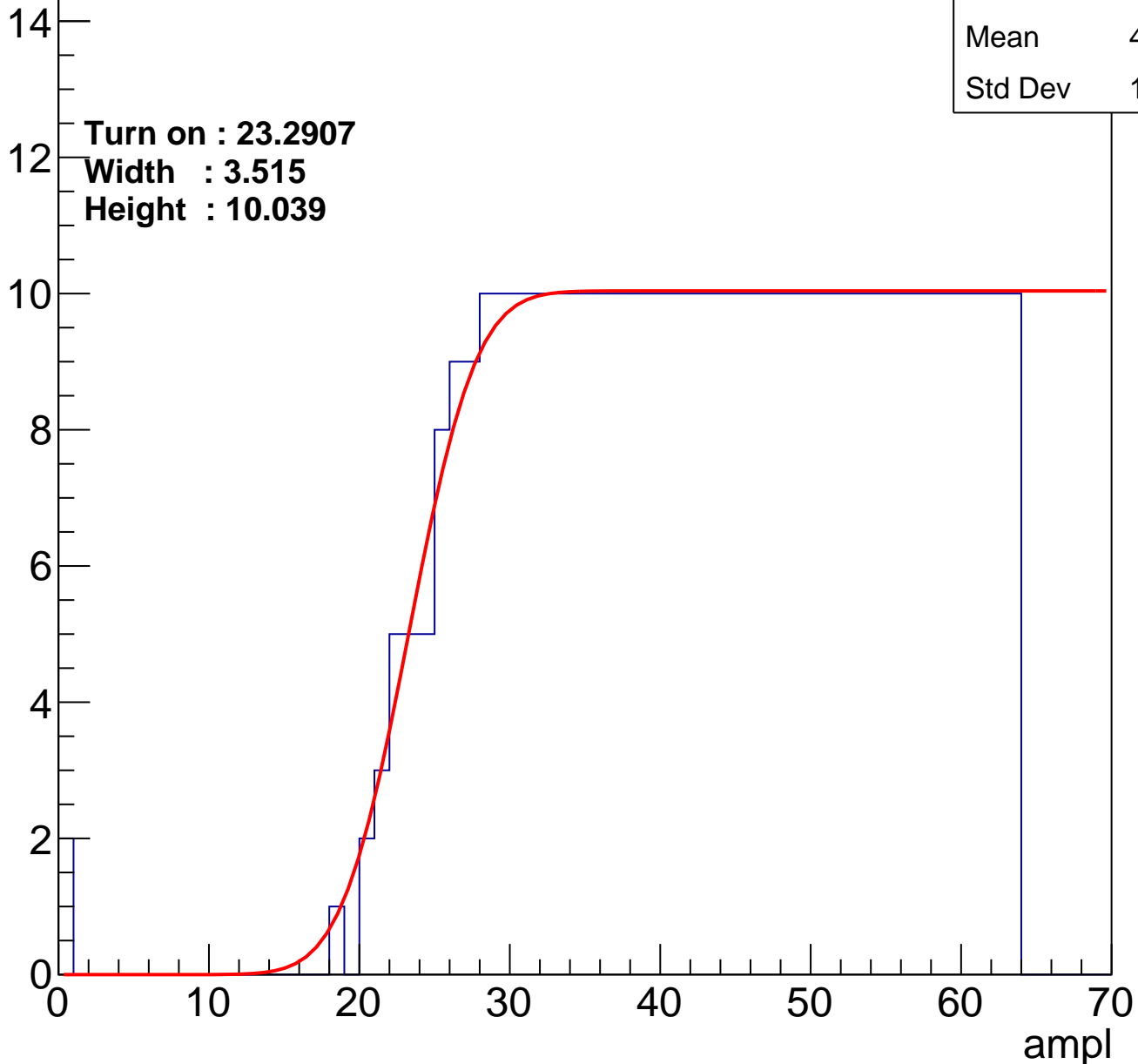
Entries	409
Mean	42.84
Std Dev	12.26

Turn on : 23.2907

Width : 3.515

Height : 10.039

Entry



B1L002S, U15-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.2768

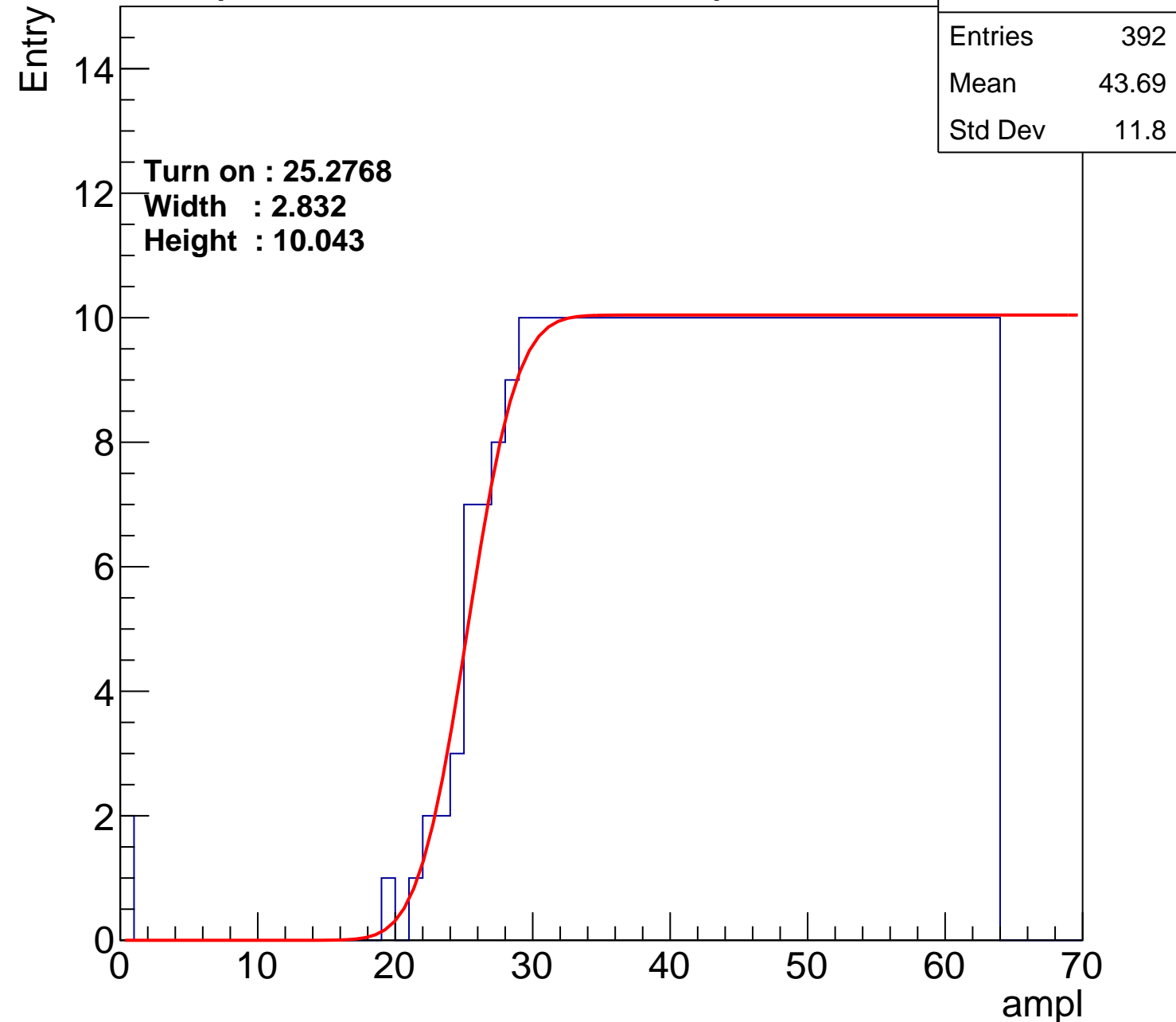
Width : 2.832

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 24.7689

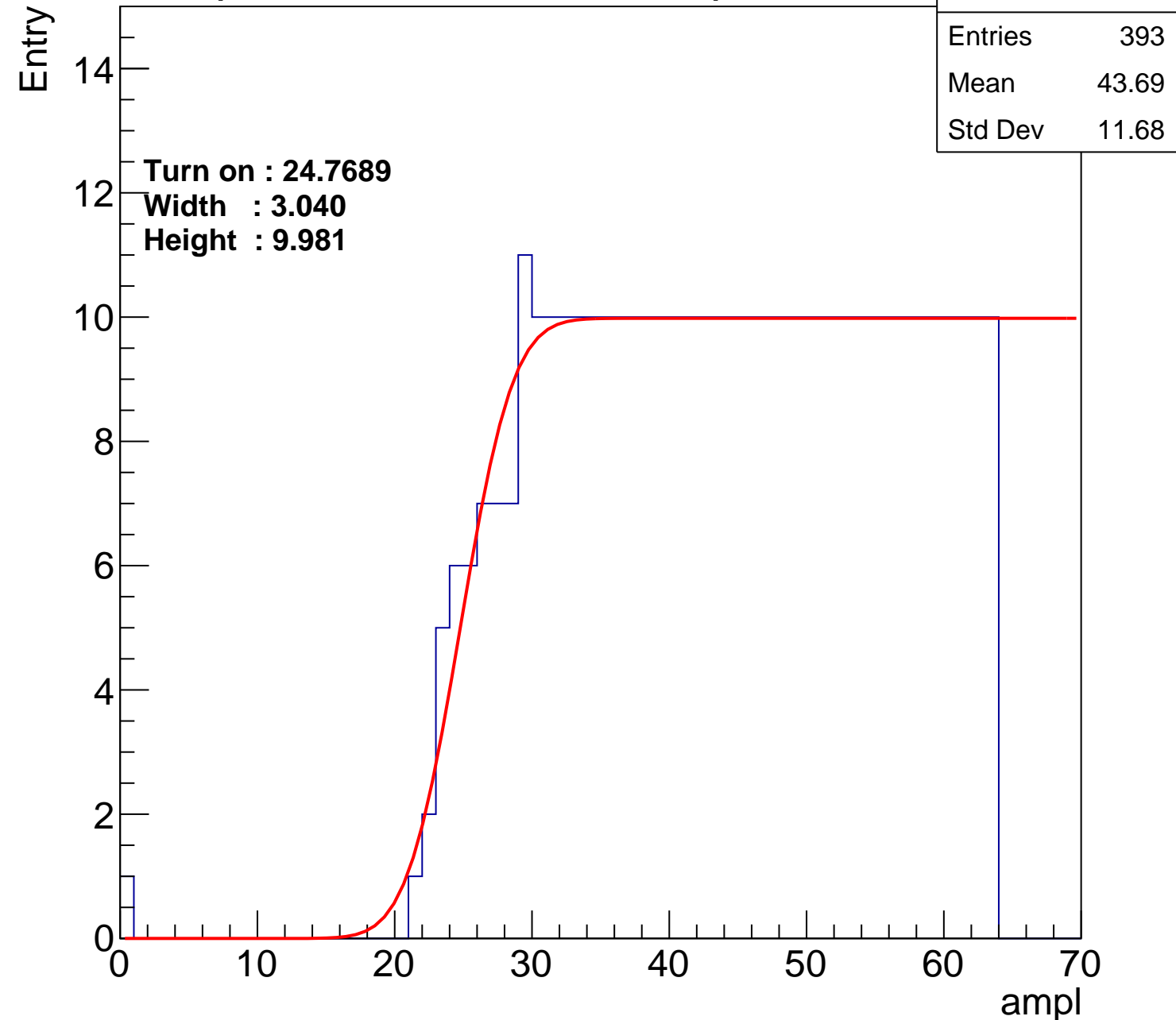
Width : 3.040

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	413
Mean	42.69
Std Dev	12.29

Turn on : 23.0167

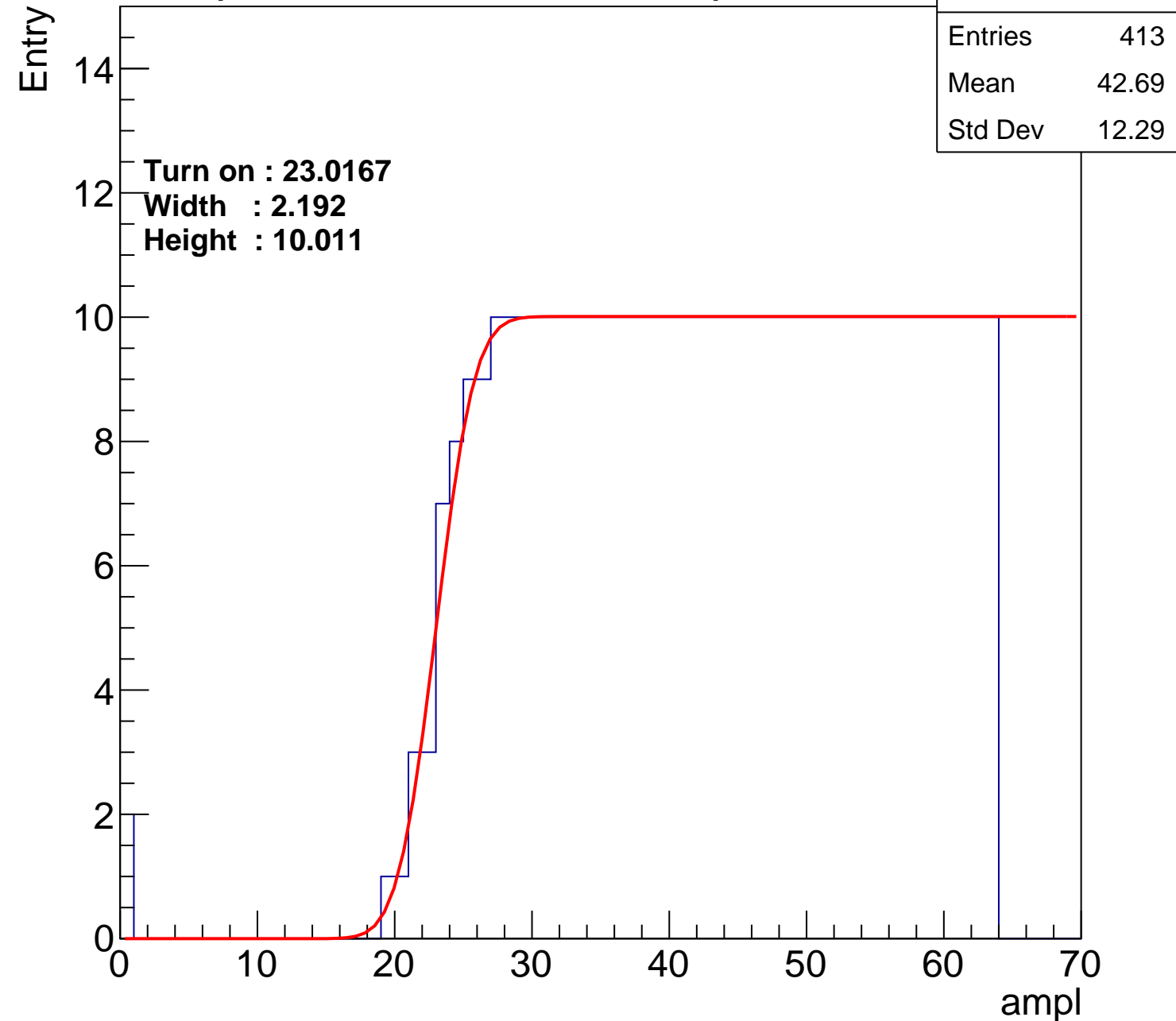
Width : 2.192

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.82
Std Dev	12.29

Turn on : 23.7146

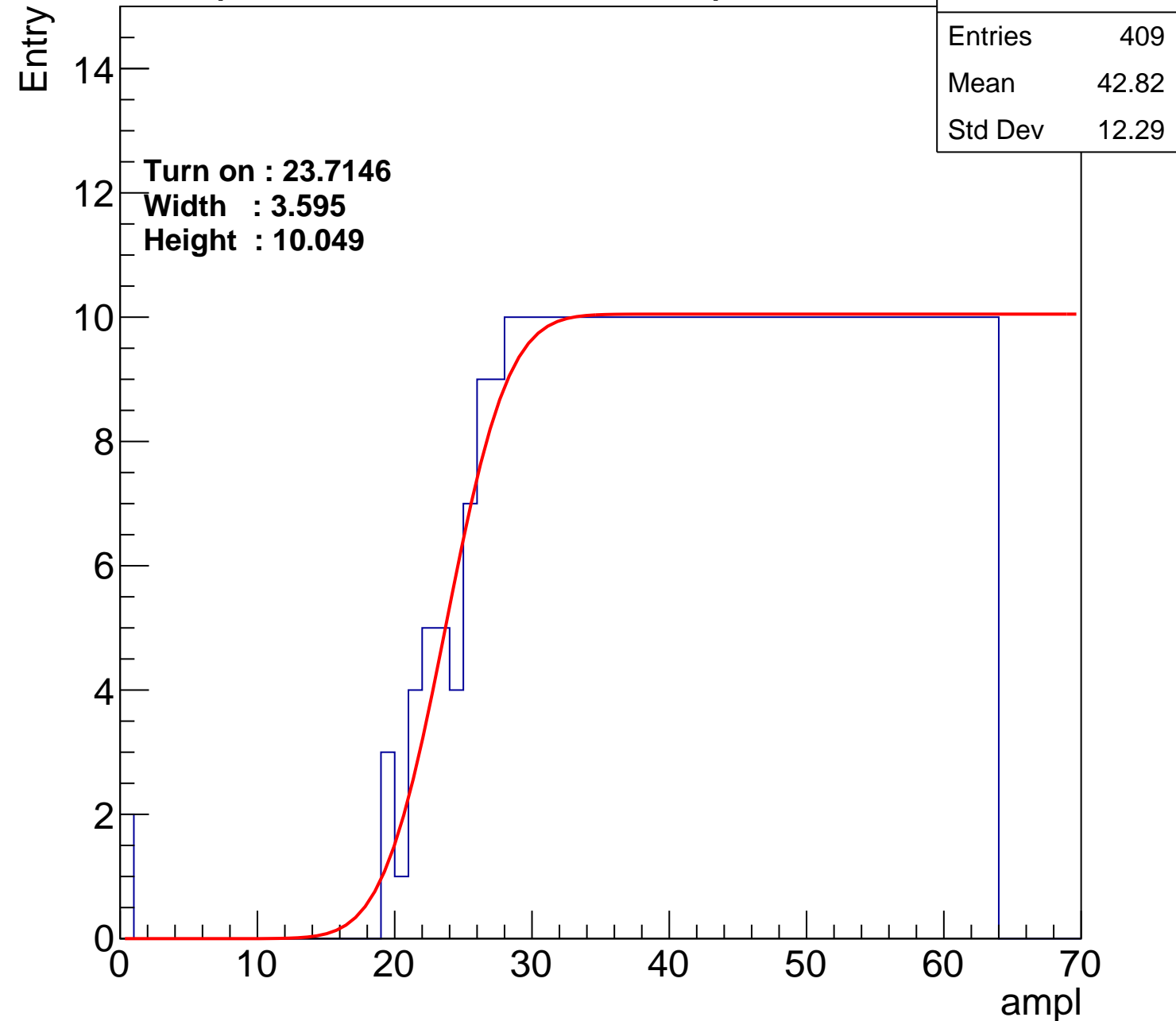
Width : 3.595

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch125

calib_packv5_042523_0143.root, FC#10, port B3

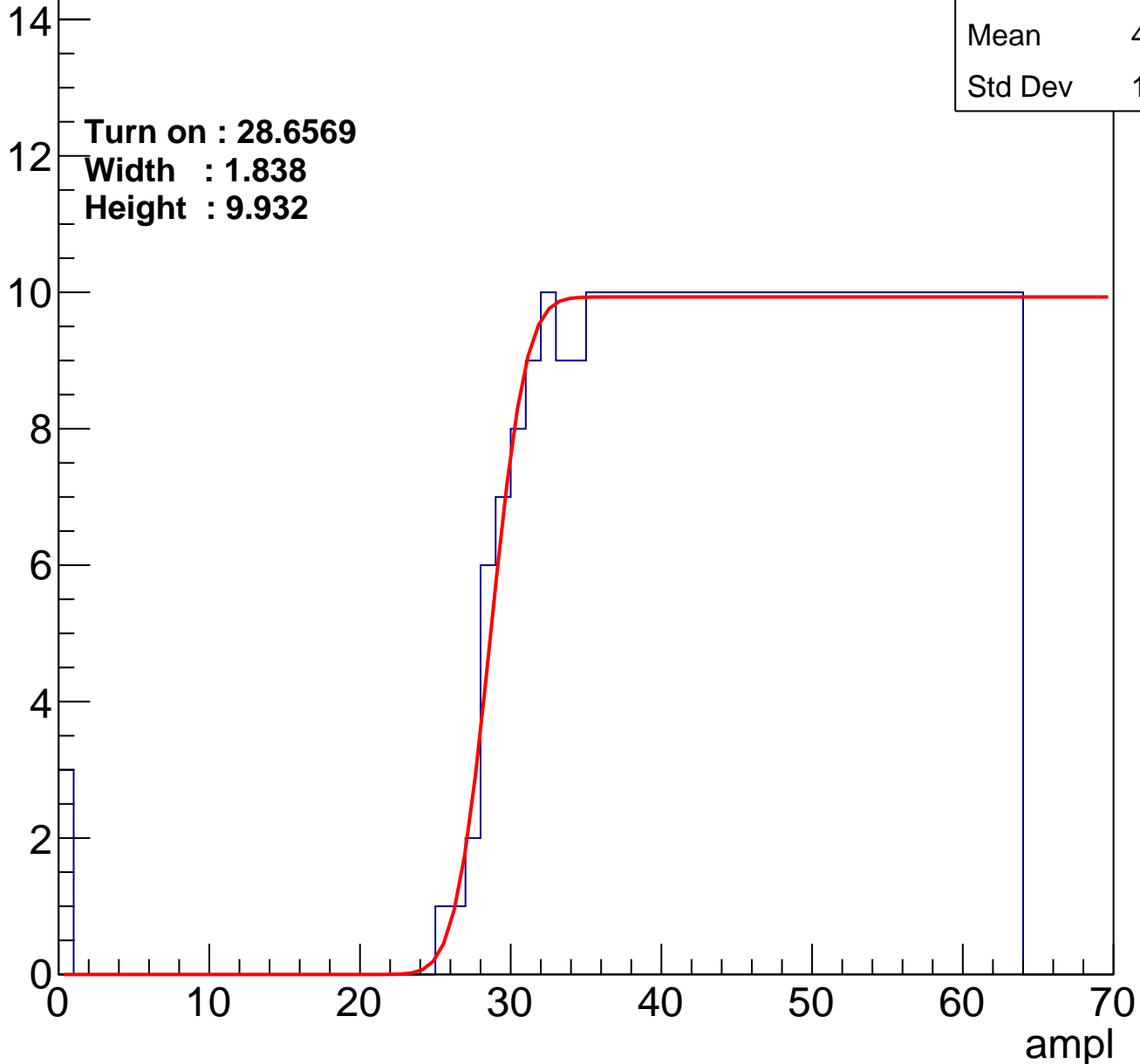
Entries	355
Mean	45.43
Std Dev	11.07

Turn on : 28.6569

Width : 1.838

Height : 9.932

Entry



B1L002S, U15-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.65
Std Dev	12.04

Turn on : 25.6525

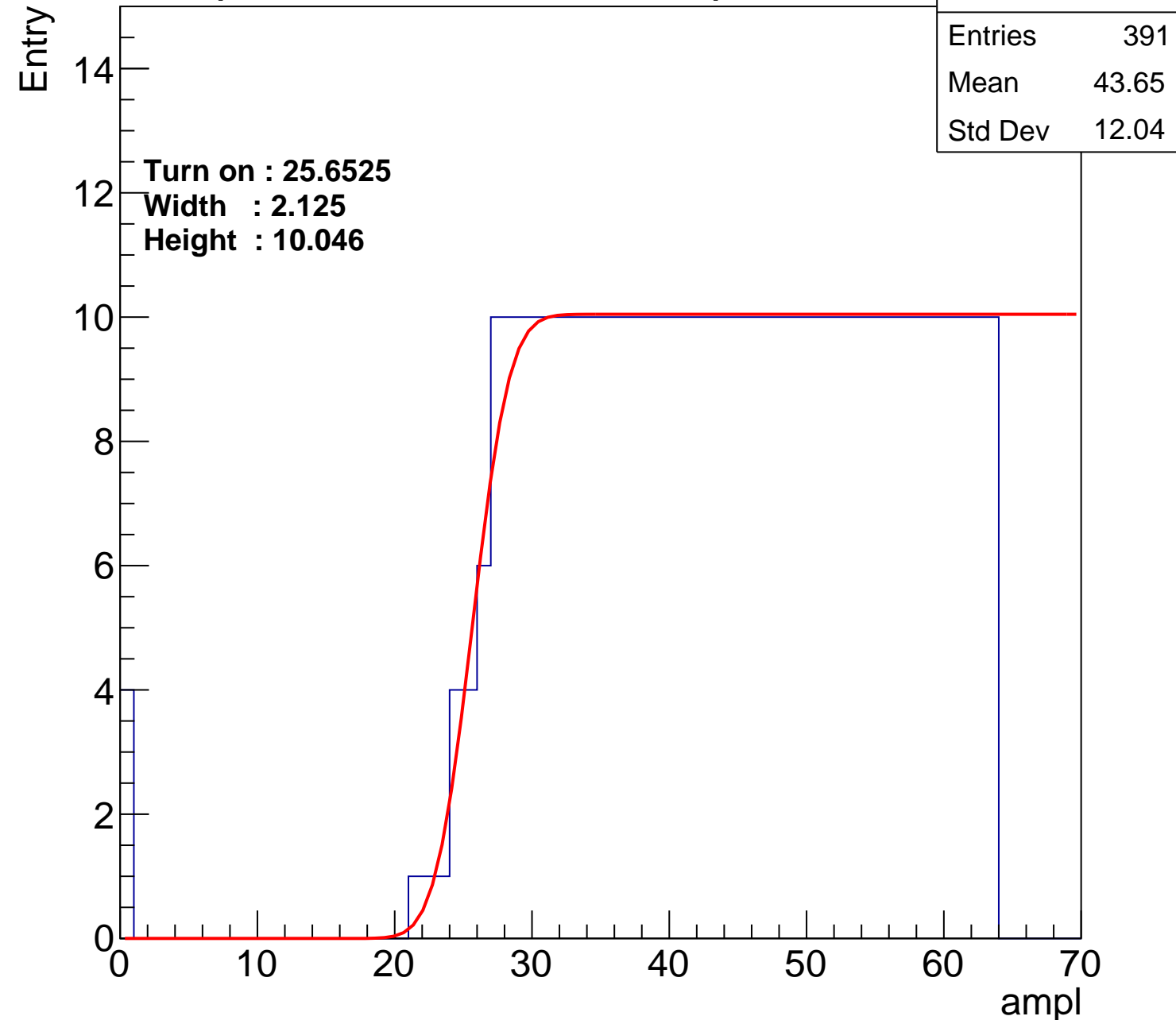
Width : 2.125

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.84

Turn on : 25.2955

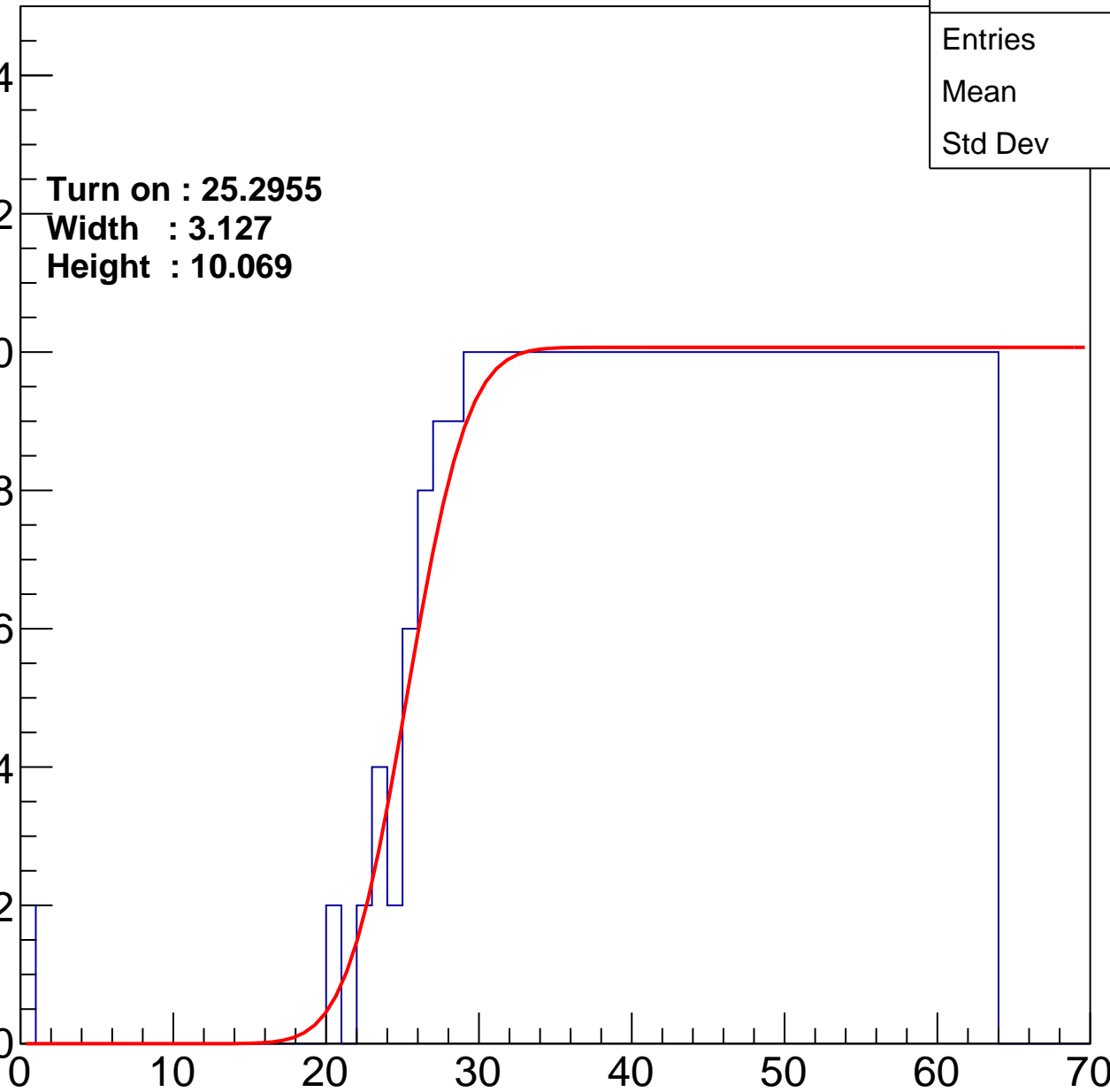
Width : 3.127

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U15-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.84

Turn on : 25.2955

Width : 3.127

Height : 10.069

Entry

