



# B0L002S, U23-ch0, adc0

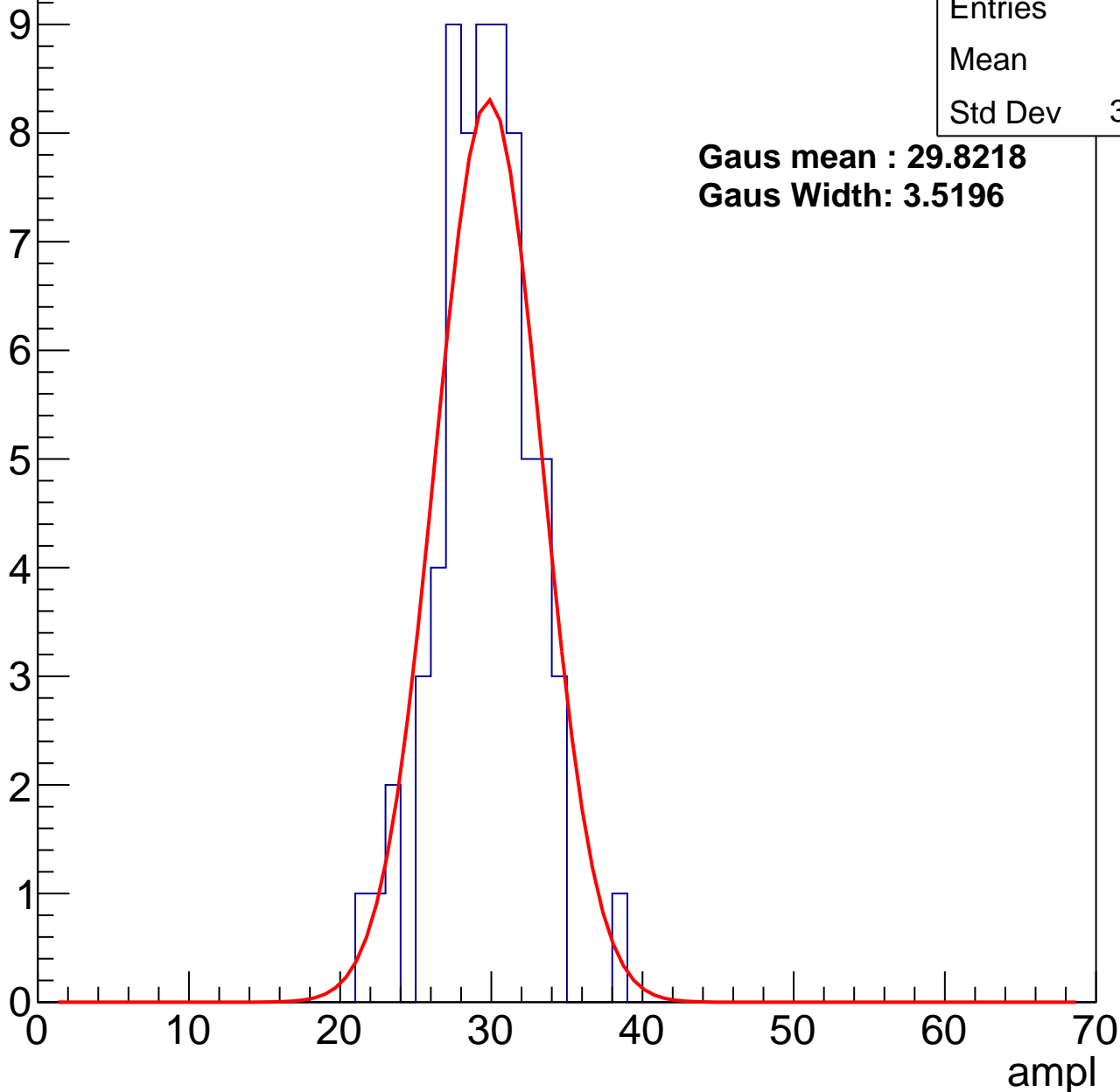
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.1
Std Dev	3.054

**Gaus mean : 29.8218**

**Gaus Width: 3.5196**



# B0L002S, U23-ch0, adc1

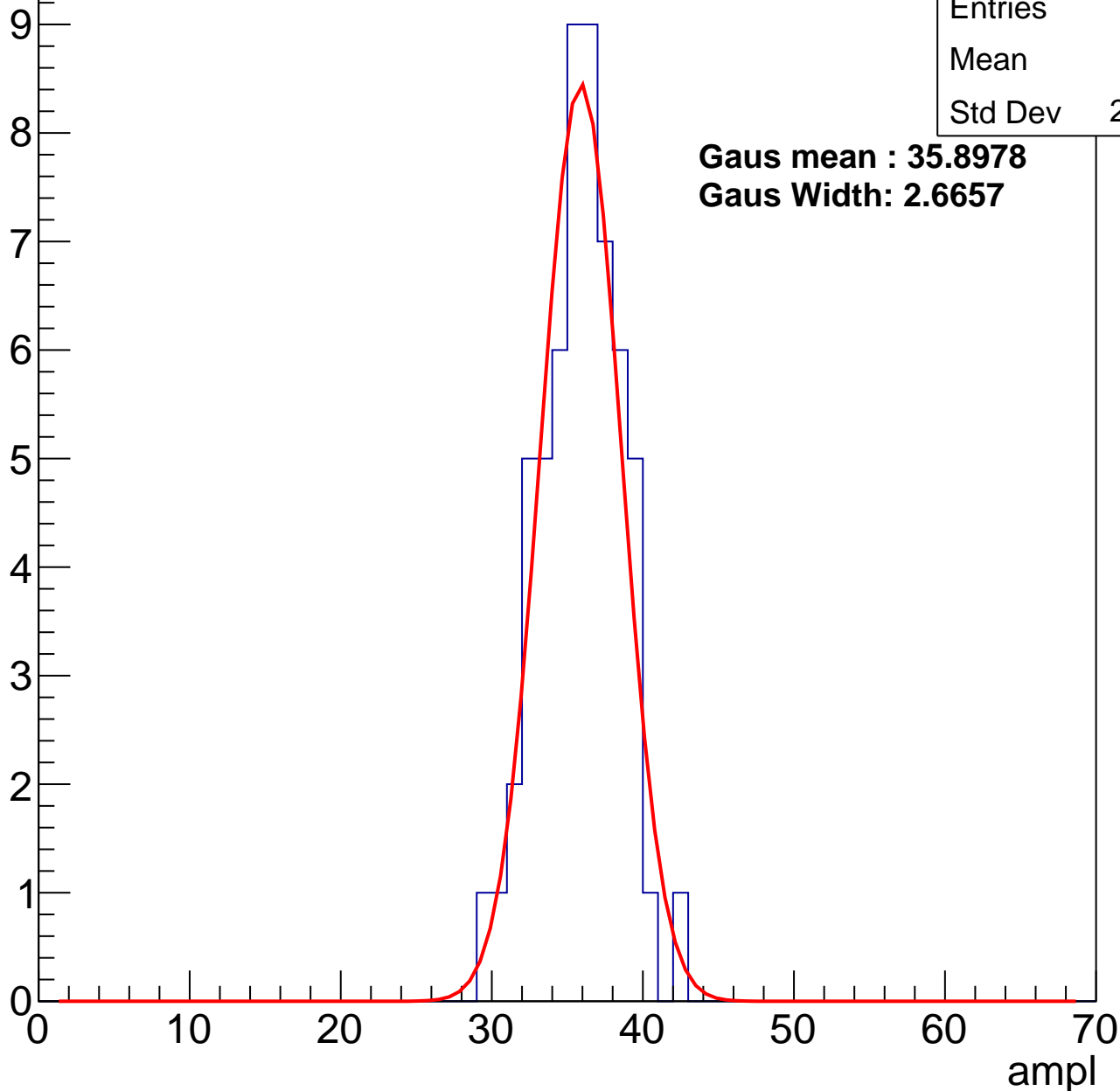
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	35.4
Std Dev	2.626

**Gaus mean : 35.8978**

**Gaus Width: 2.6657**



# B0L002S, U23-ch0, adc2

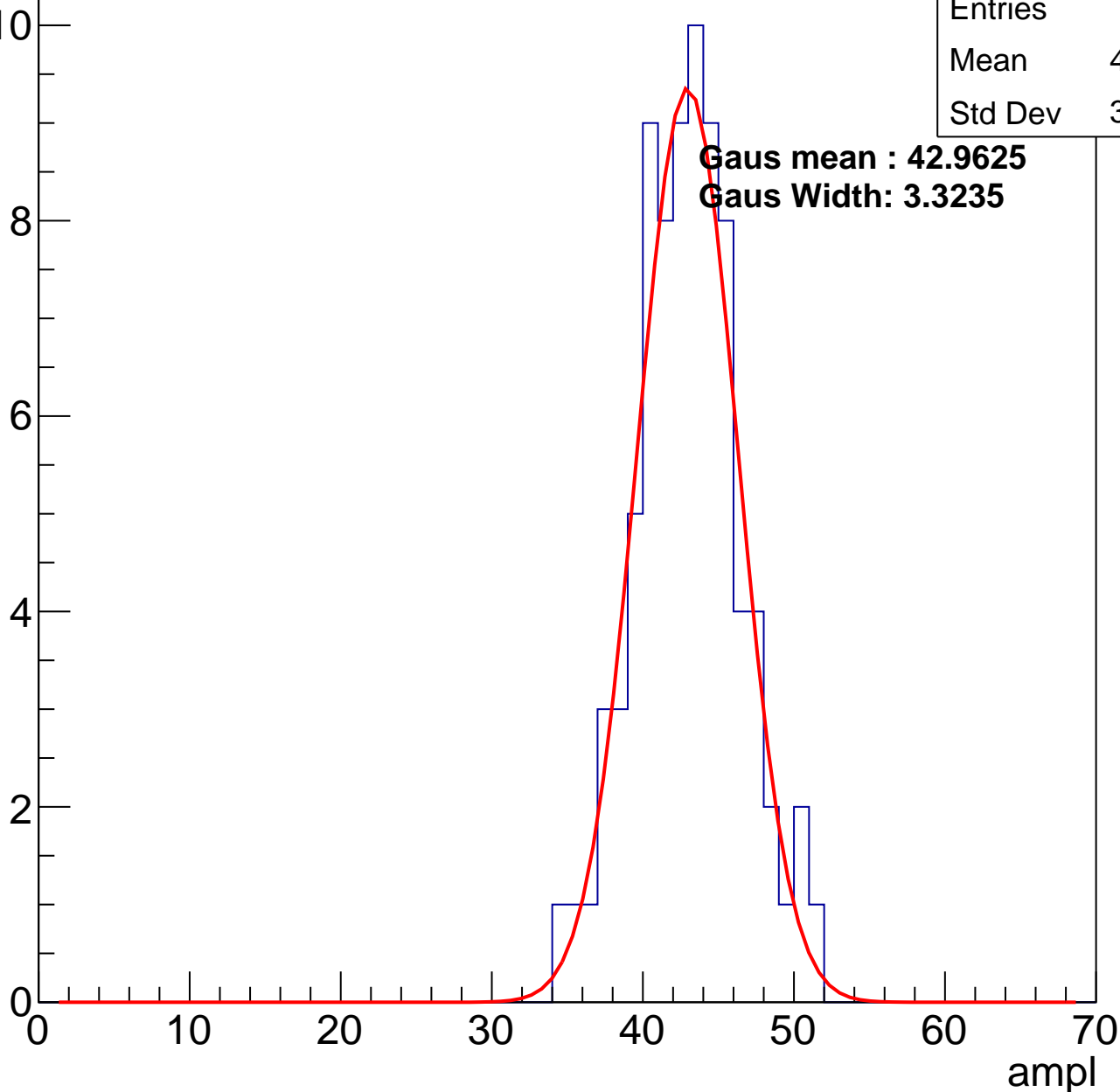
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	42.53
Std Dev	3.425

**Gaus mean : 42.9625**

**Gaus Width: 3.3235**

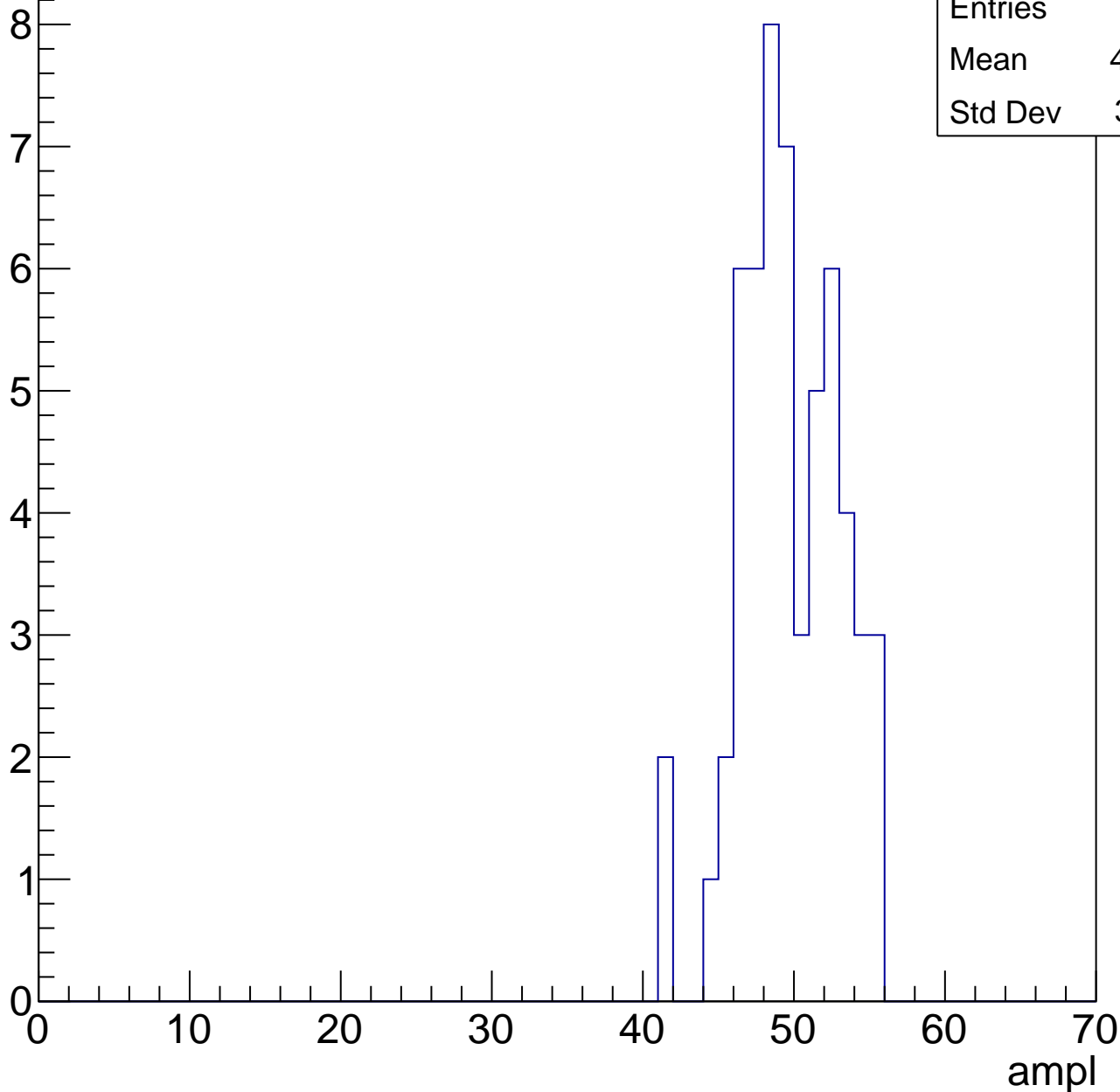


# B0L002S, U23-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

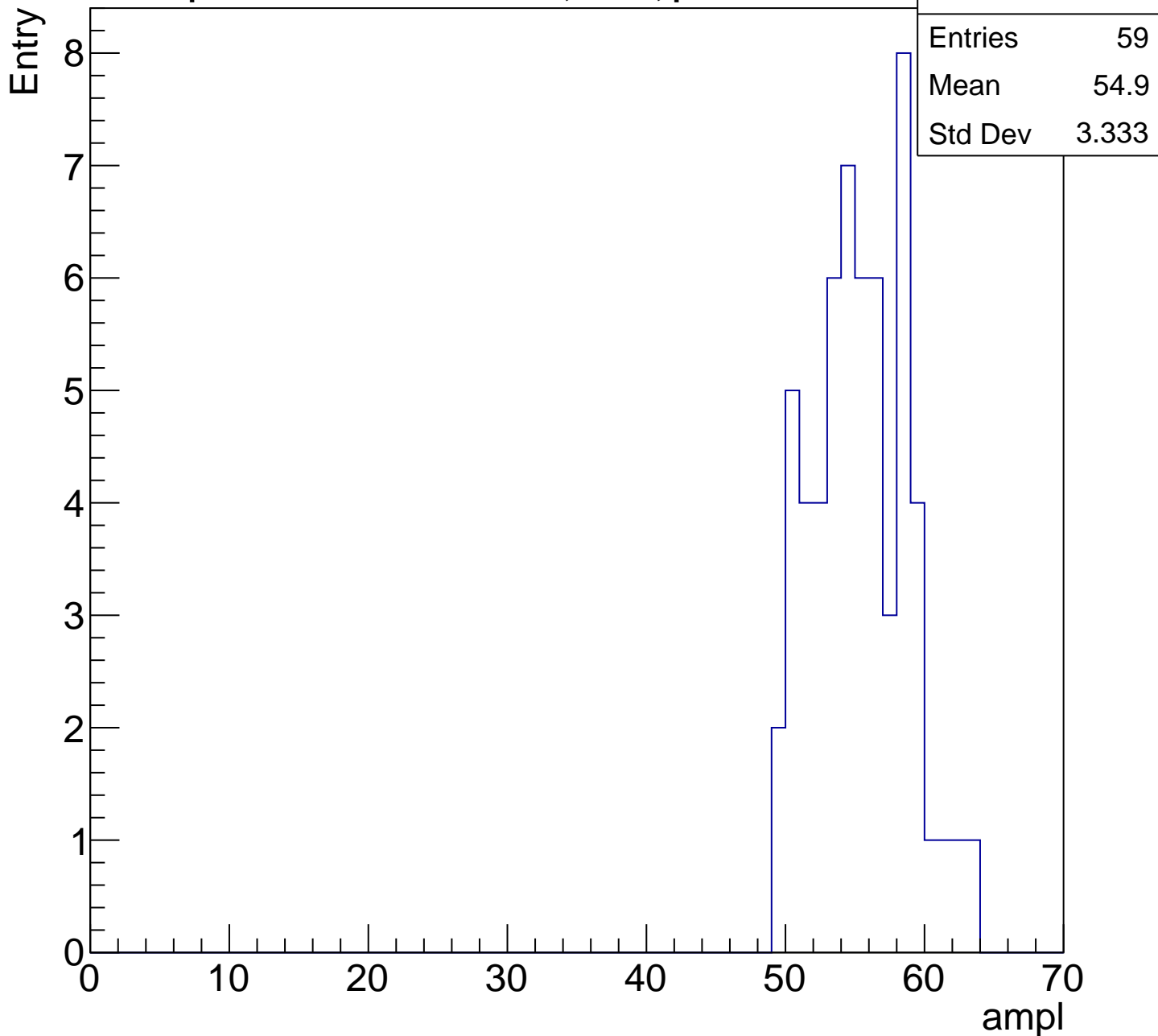
Entry

Entries	56
Mean	49.23
Std Dev	3.251



# B0L002S, U23-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

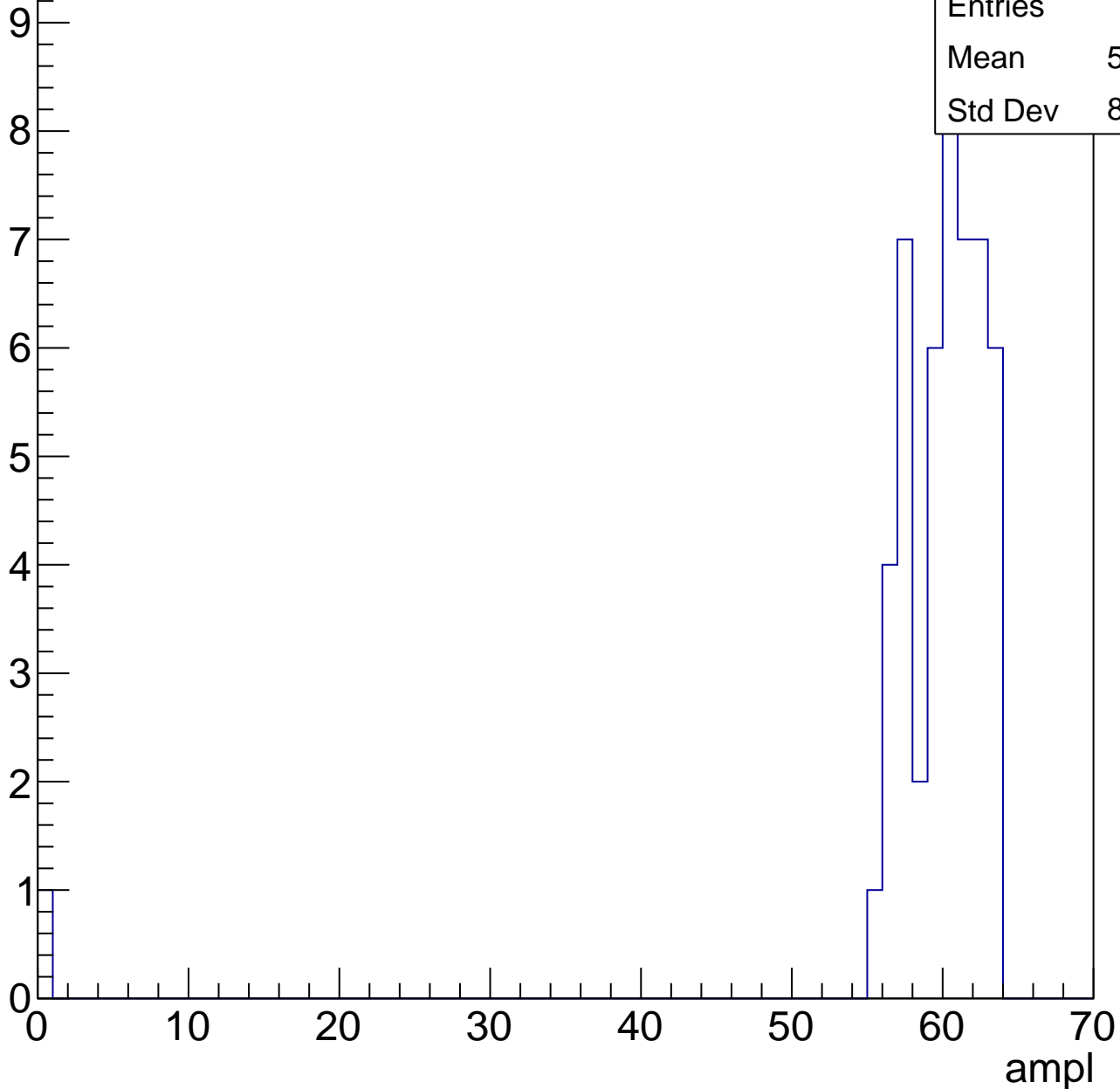


# B0L002S, U23-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

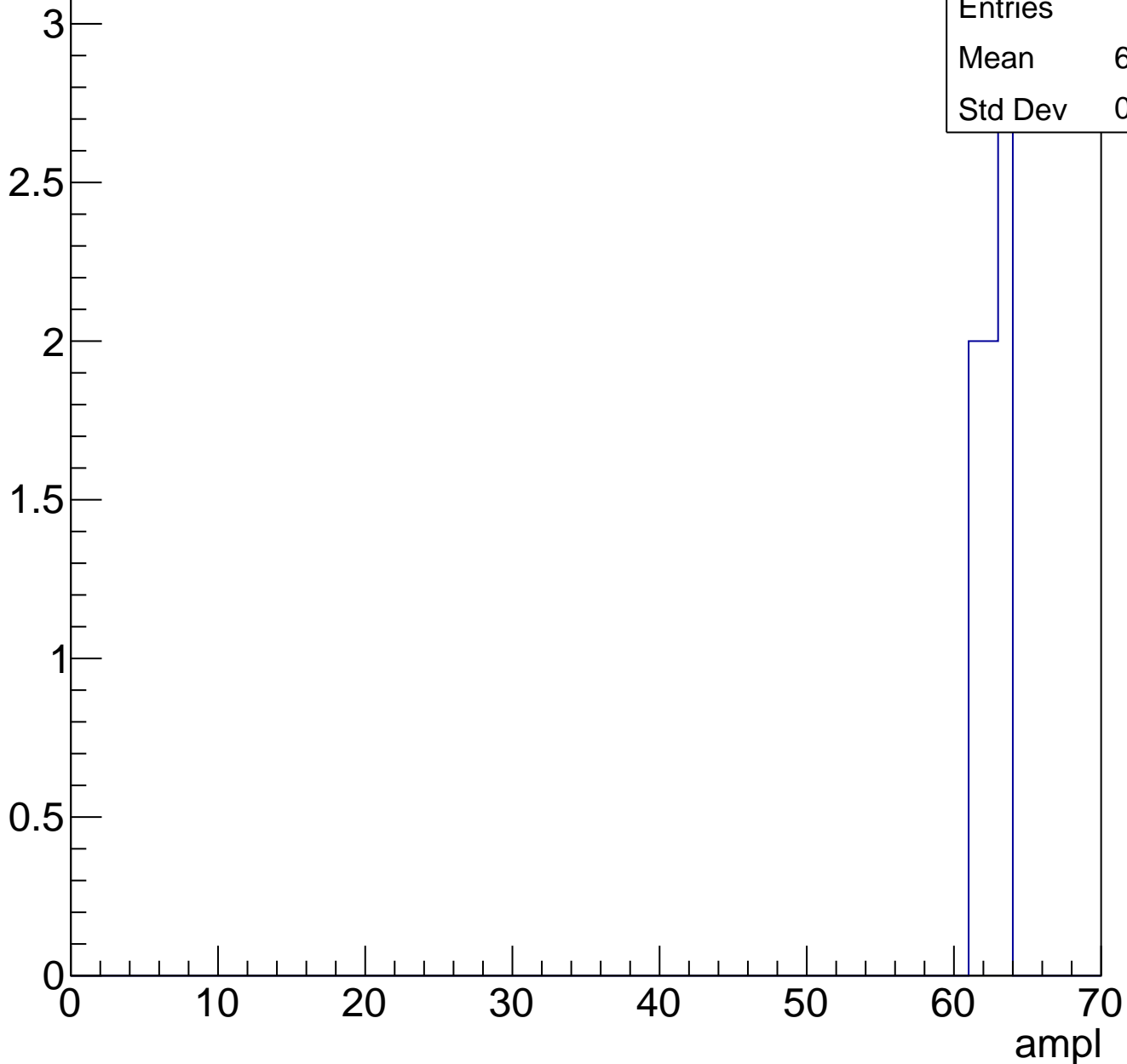
Entries	50
Mean	58.54
Std Dev	8.658



# B0L002S, U23-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch1, adc0

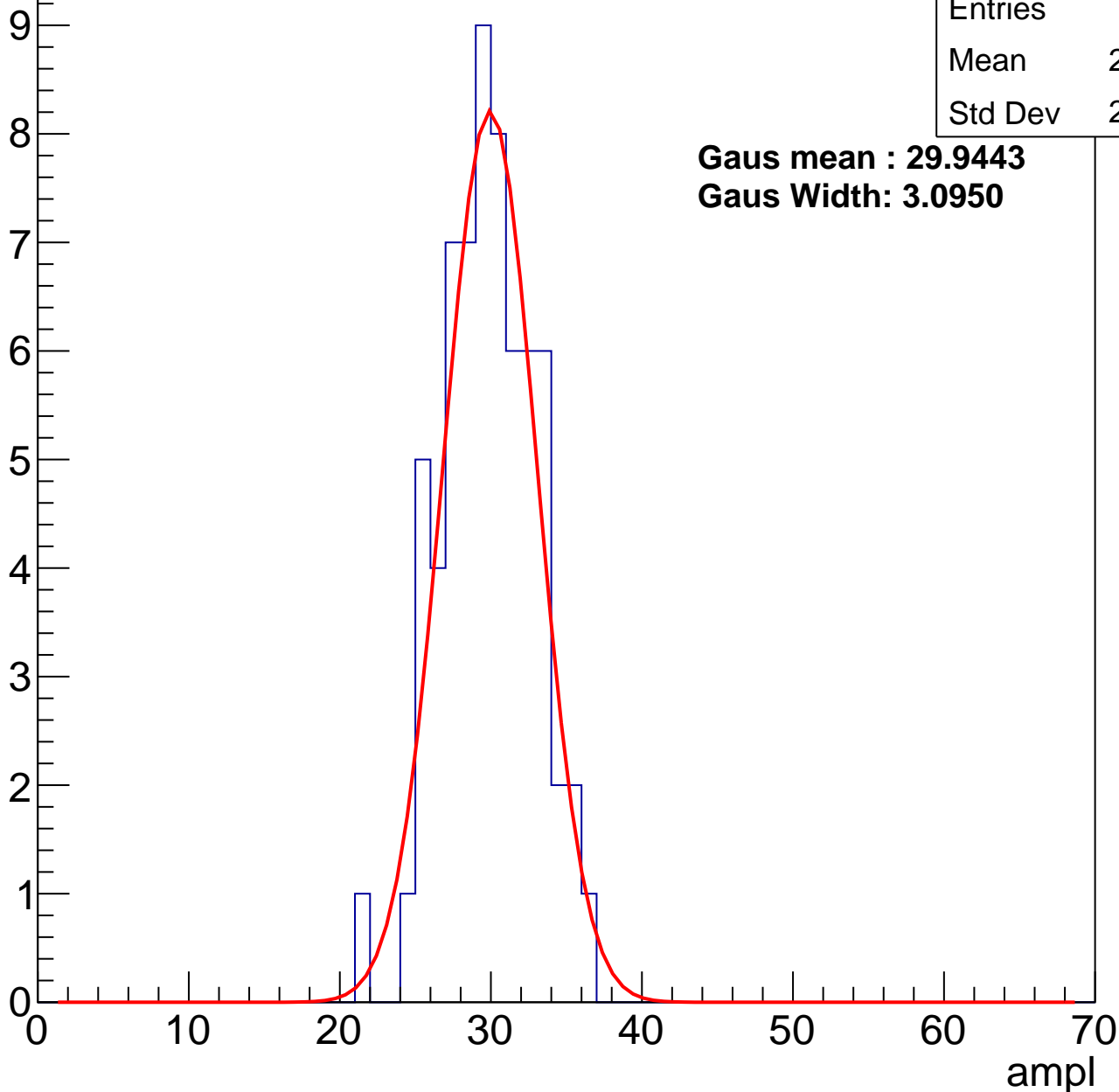
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.38
Std Dev	2.996

**Gaus mean : 29.9443**

**Gaus Width: 3.0950**



# B0L002S, U23-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	36.95
Std Dev	3.322

**Gaus mean : 37.4889**

**Gaus Width: 3.3861**

10

8

6

4

2

0

0

10

20

30

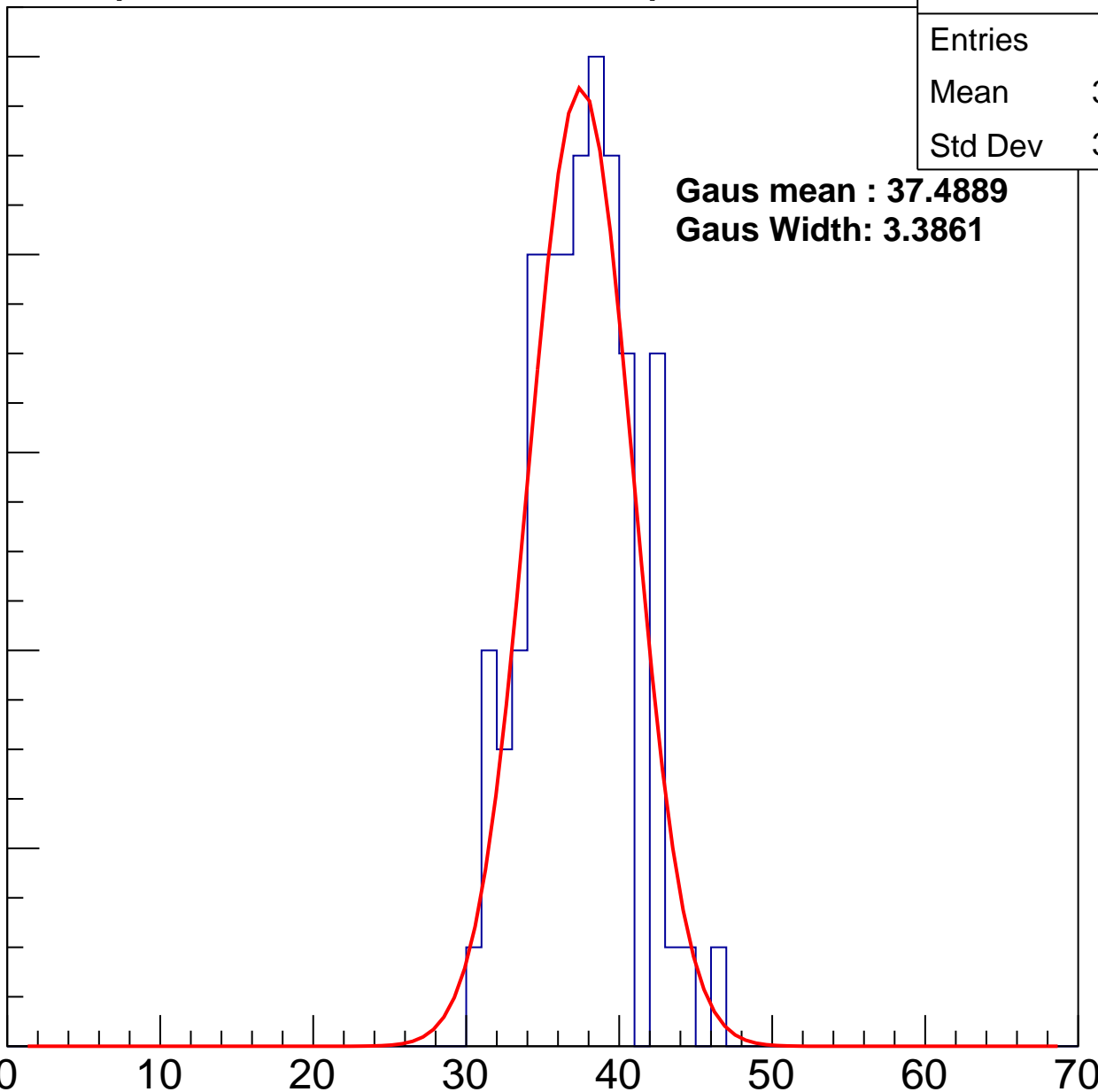
40

50

60

70

ampl



# B0L002S, U23-ch1, adc2

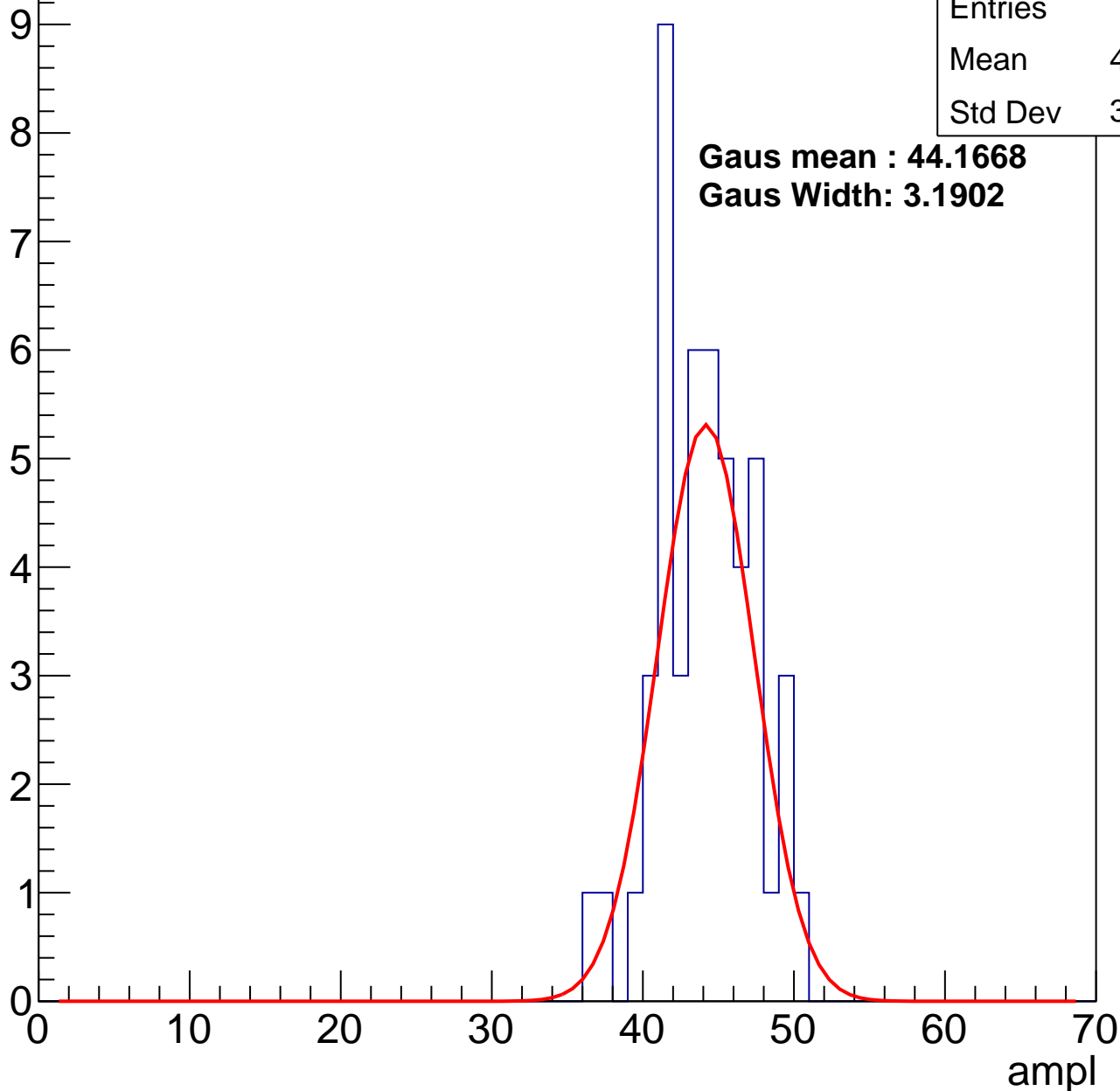
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	43.63
Std Dev	3.102

**Gaus mean : 44.1668**

**Gaus Width: 3.1902**

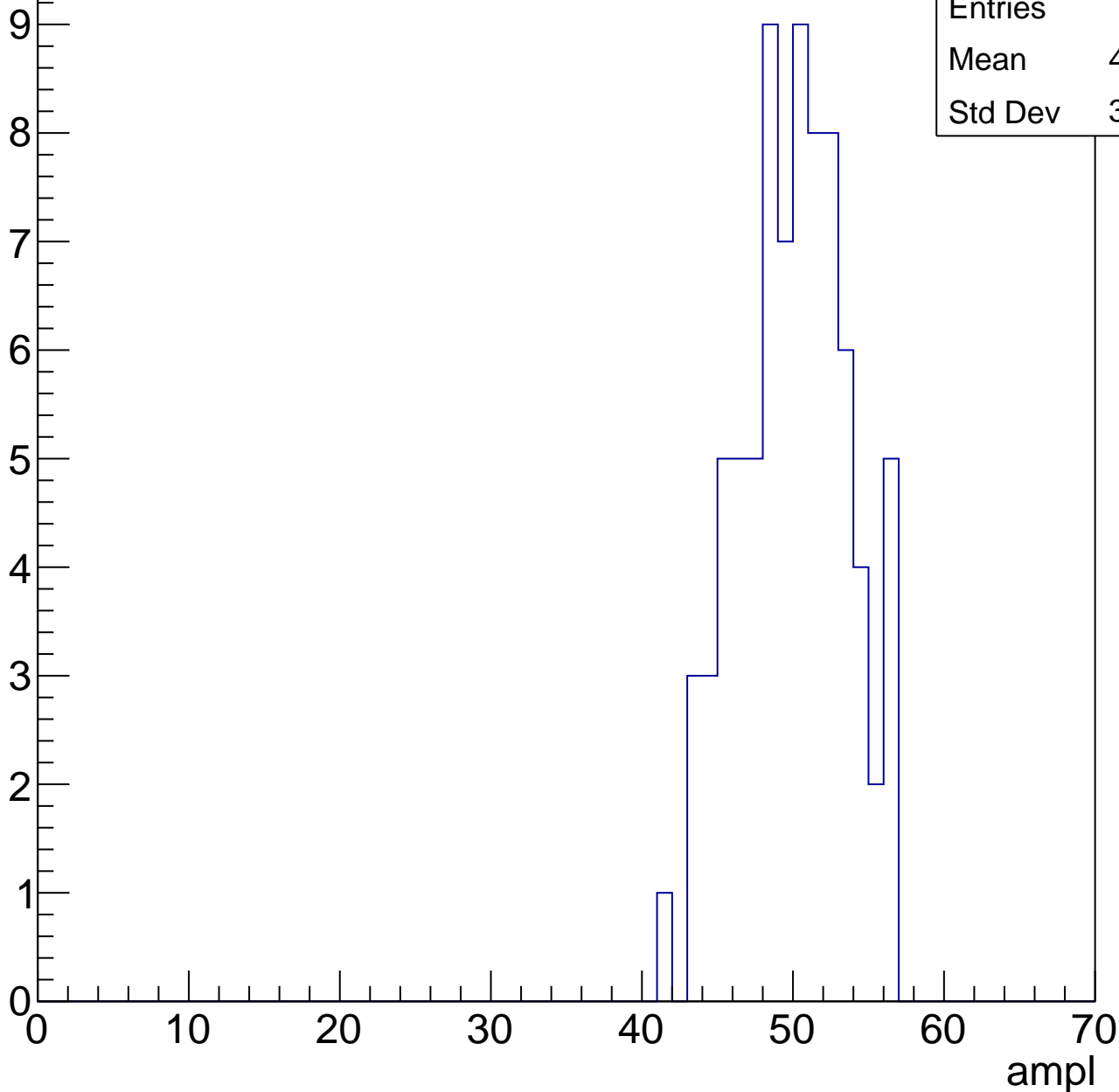


# B0L002S, U23-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

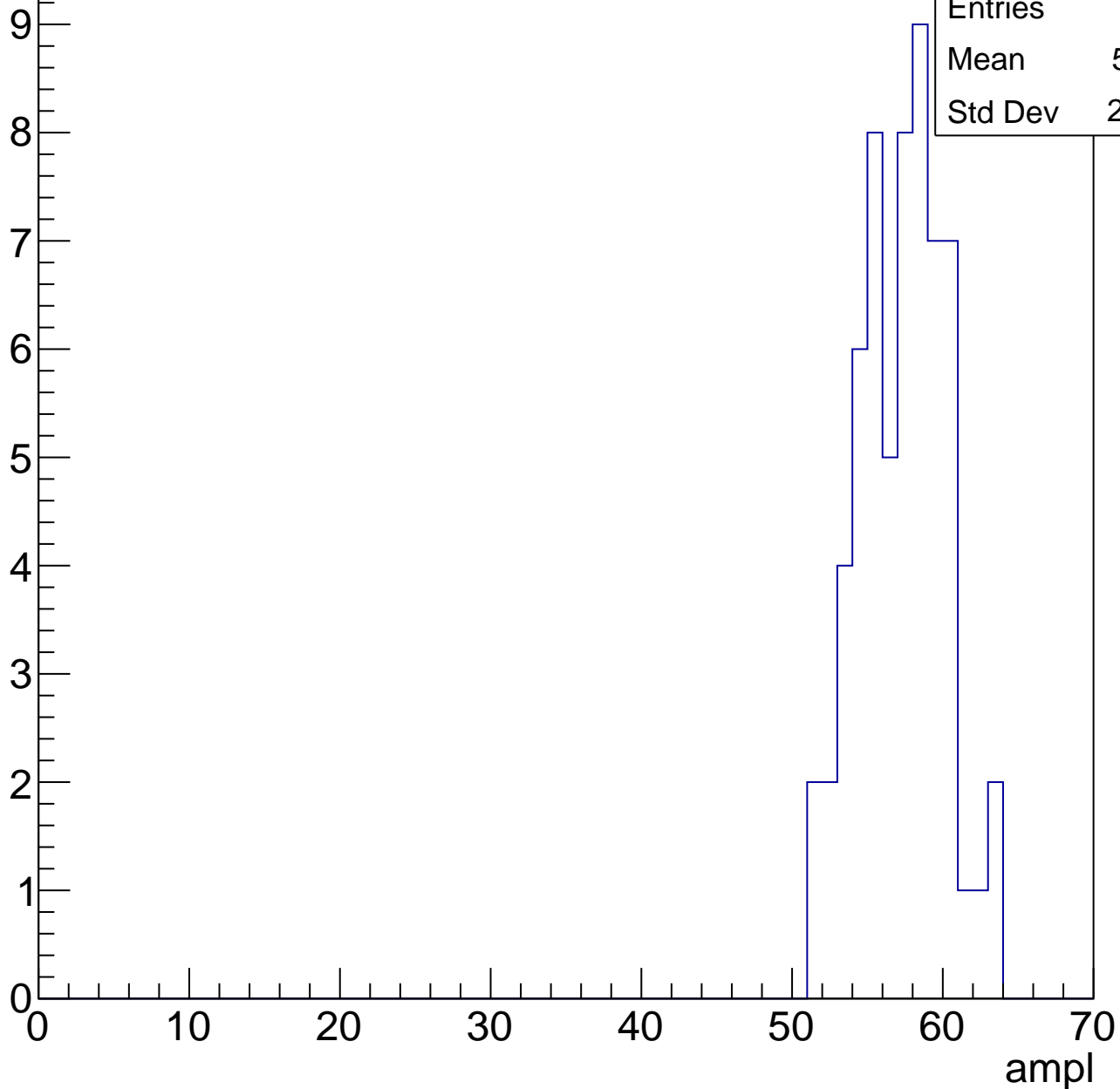
Entries	80
Mean	49.56
Std Dev	3.539



# B0L002S, U23-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



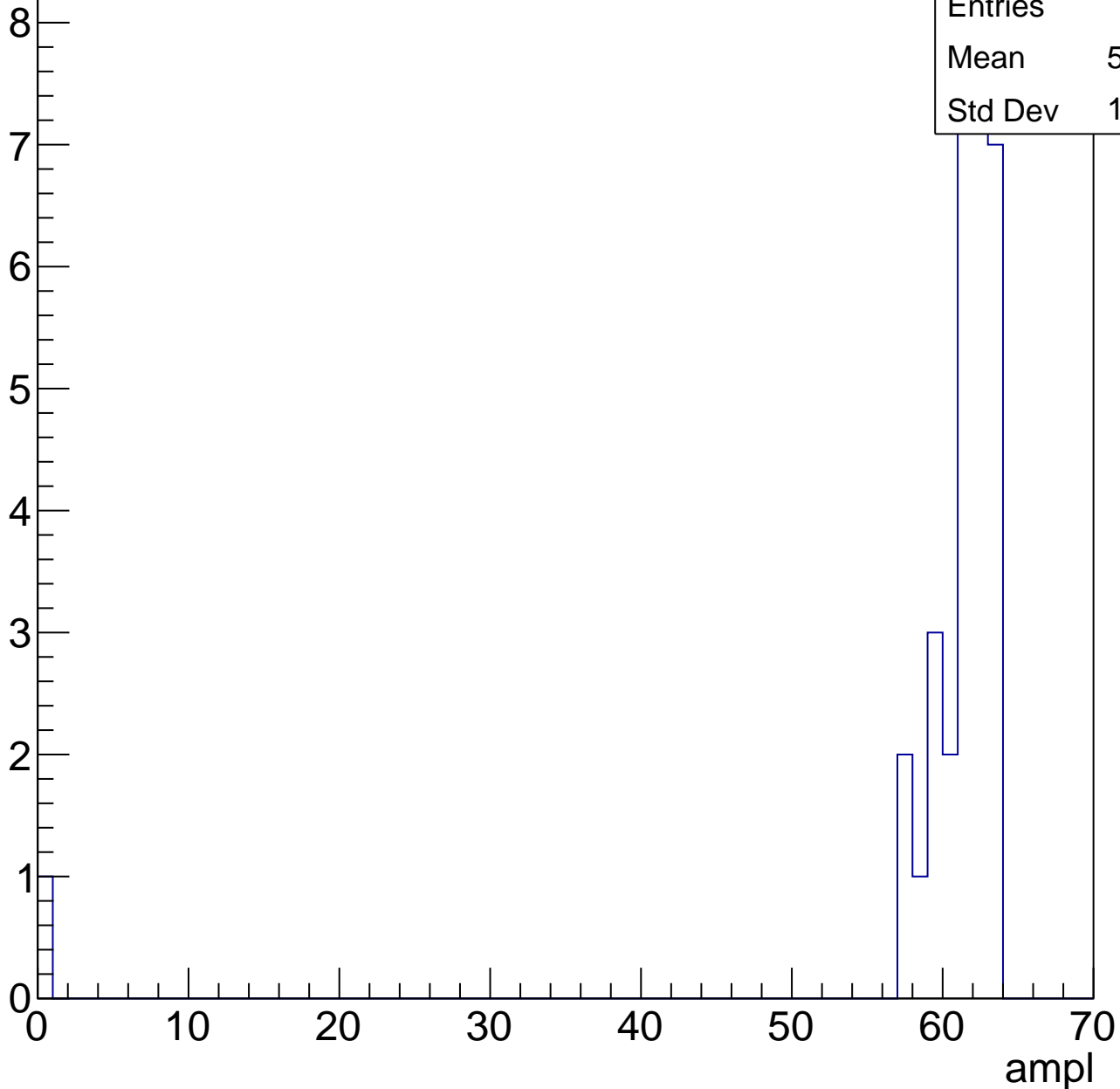
Entries	62
Mean	56.81
Std Dev	2.805

# B0L002S, U23-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59.19
Std Dev	10.76



# B0L002S, U23-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch2, adc0

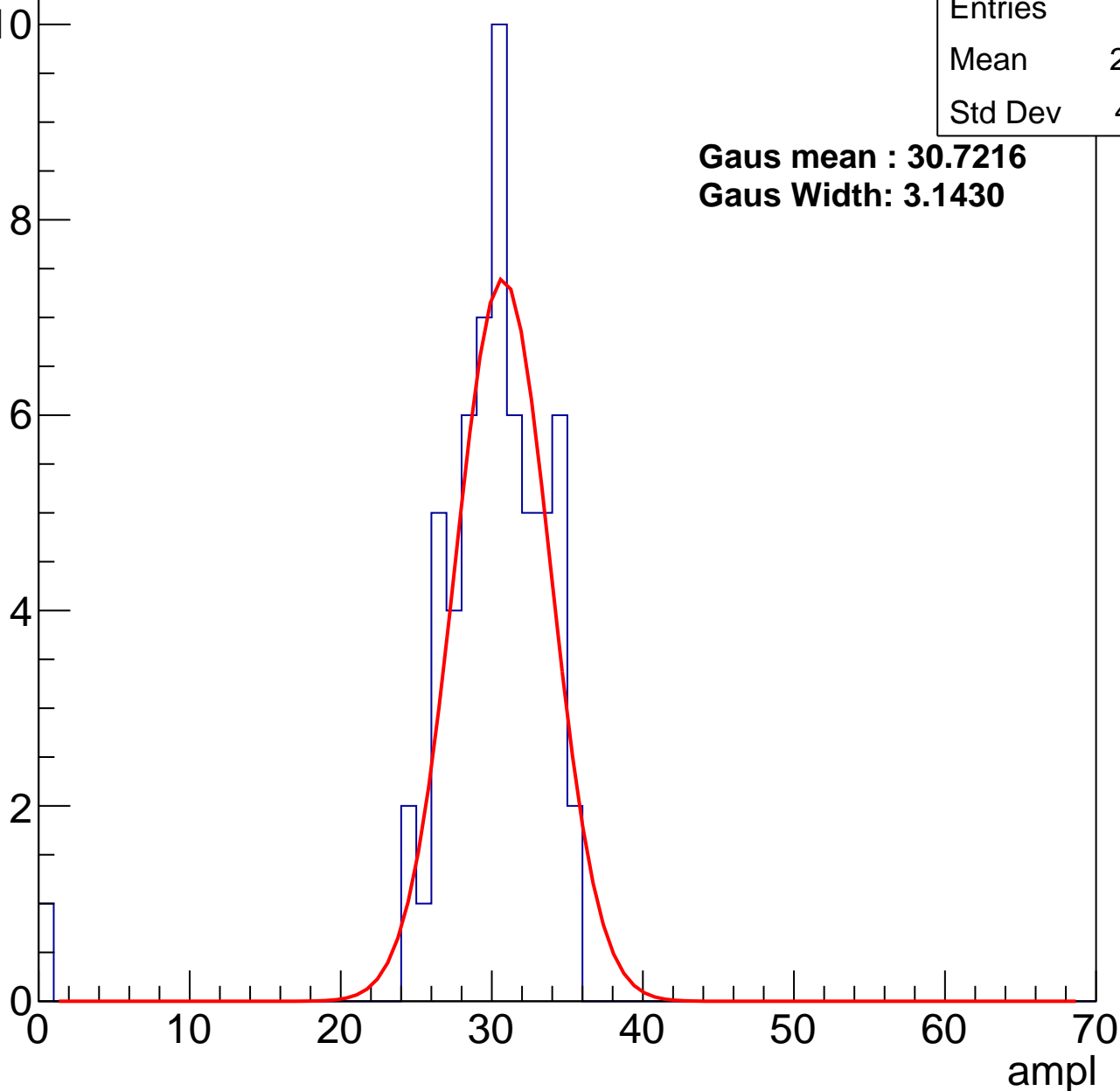
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.45
Std Dev	4.731

**Gaus mean : 30.7216**

**Gaus Width: 3.1430**



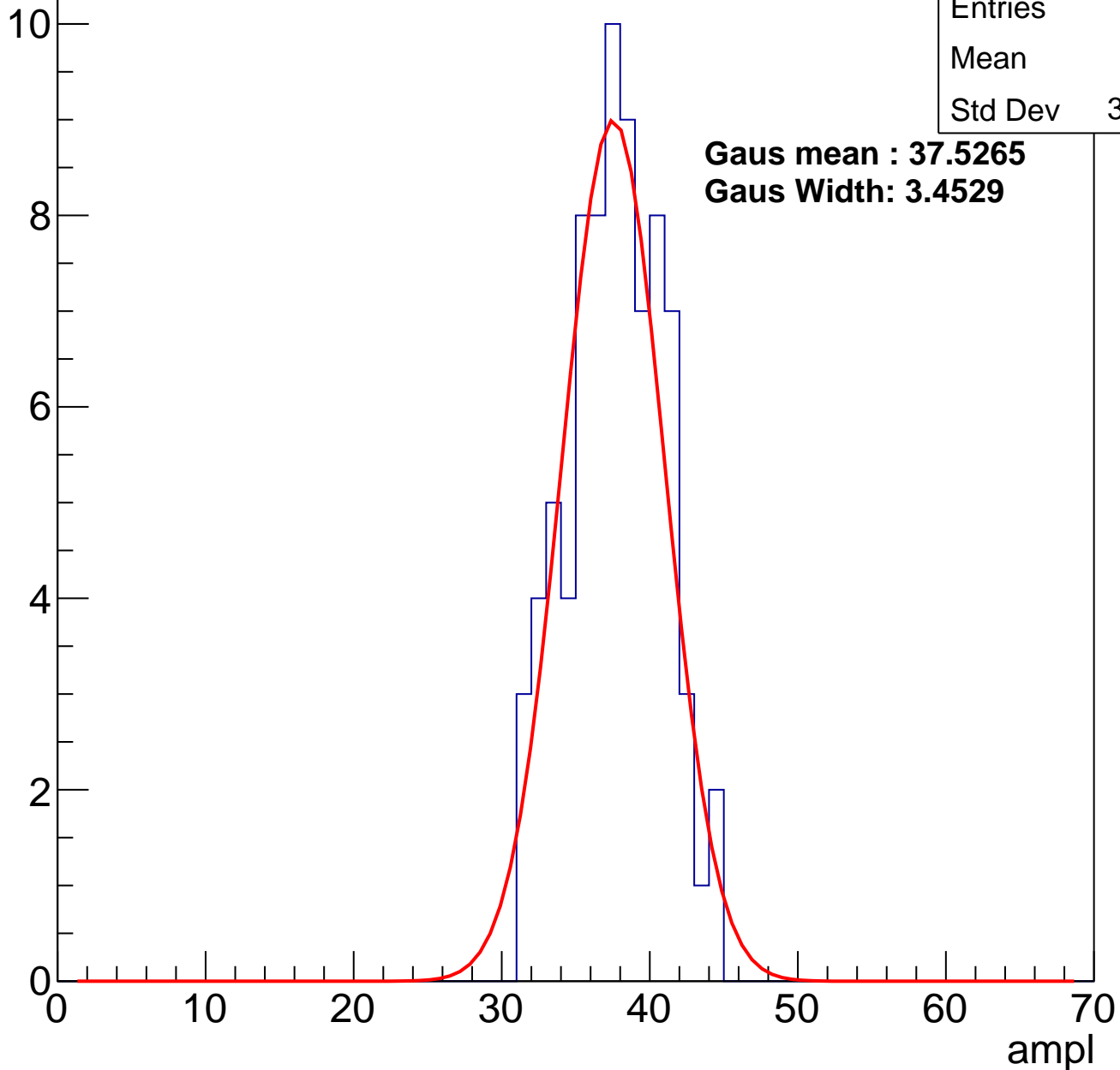
# B0L002S, U23-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	79
Mean	37.2
Std Dev	3.164

**Gaus mean : 37.5265**  
**Gaus Width: 3.4529**

Entry



# B0L002S, U23-ch2, adc2

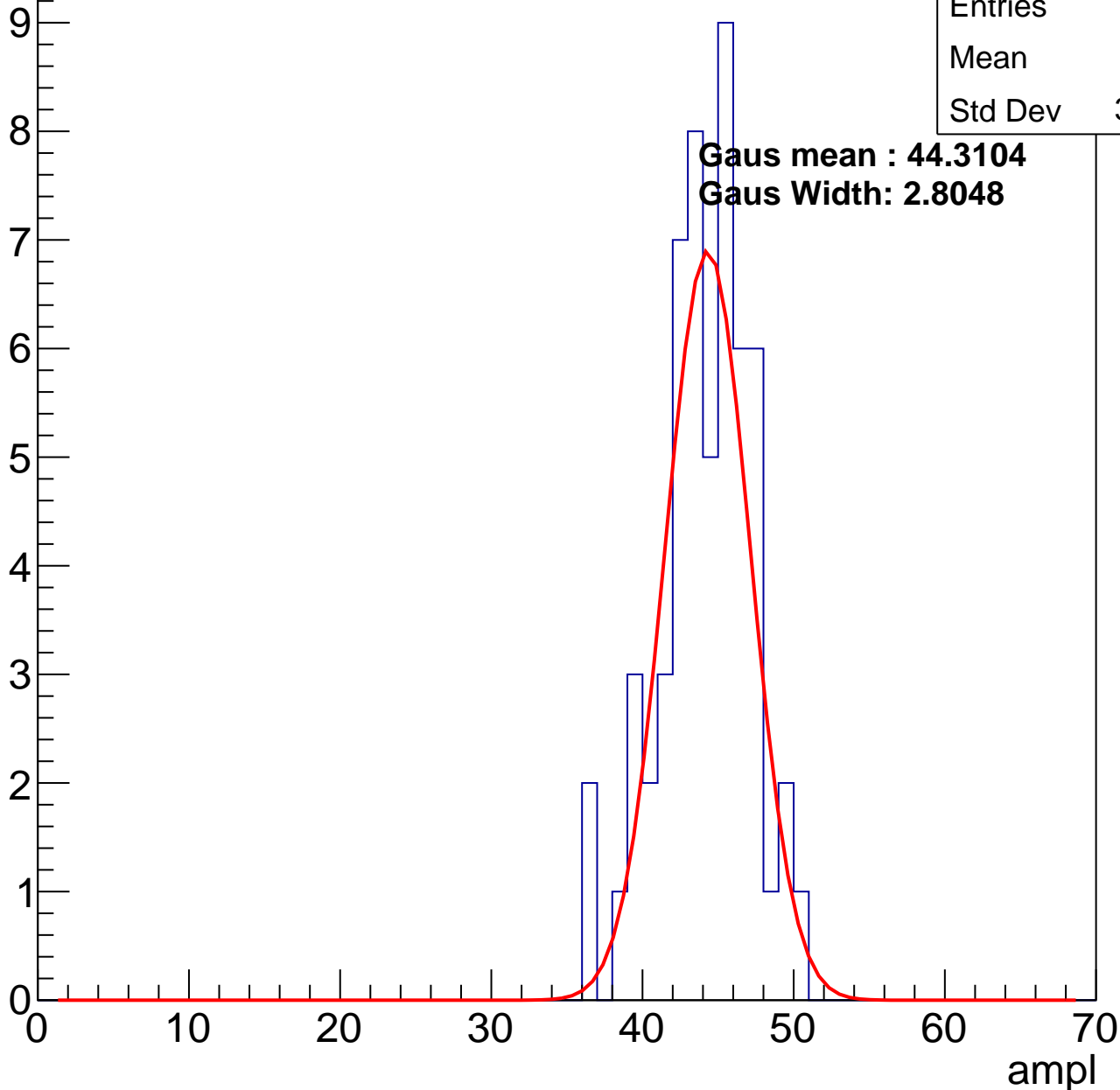
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.7
Std Dev	3.041

**Gaus mean : 44.3104**

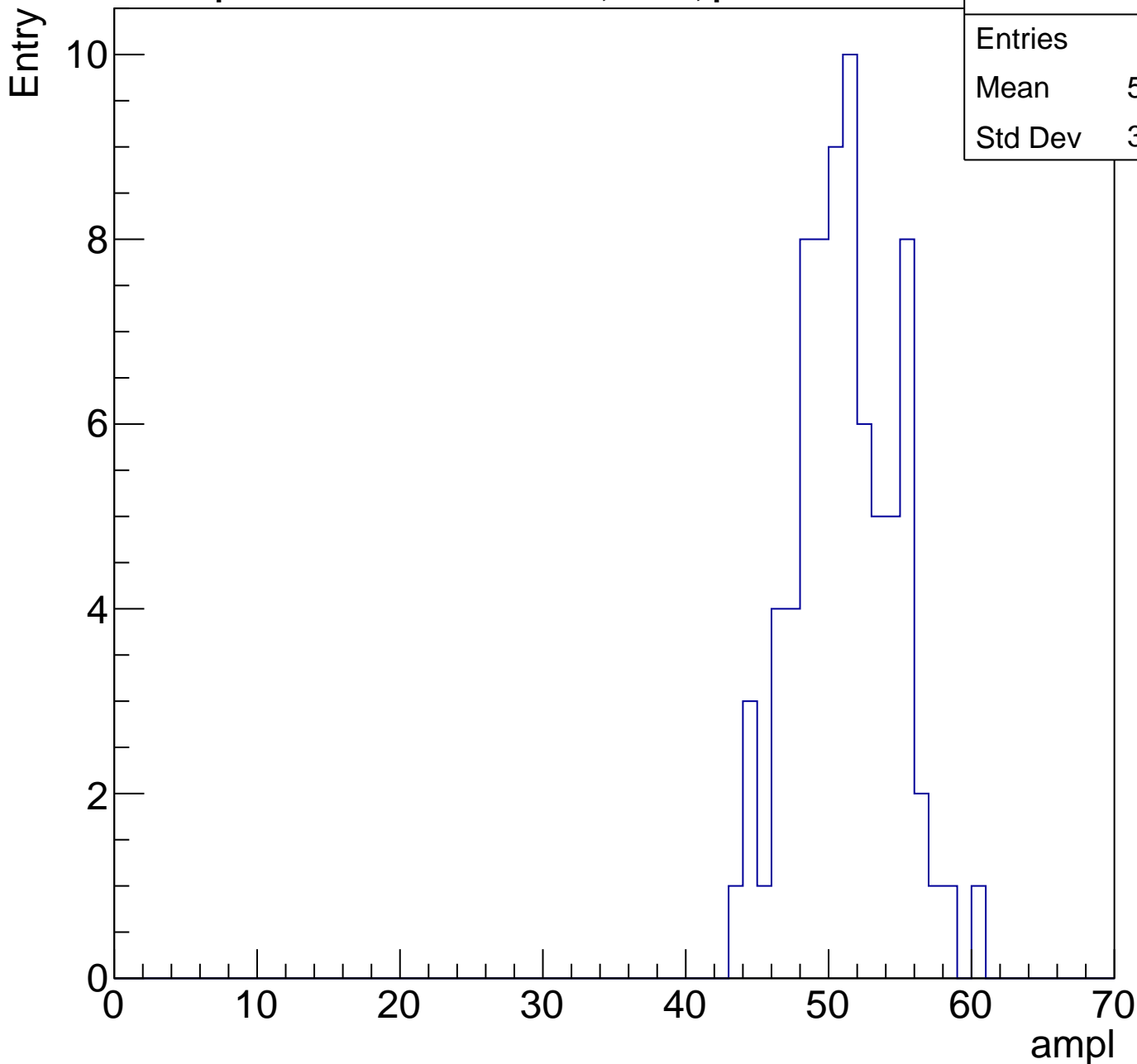
**Gaus Width: 2.8048**



# B0L002S, U23-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	77
Mean	50.68
Std Dev	3.477

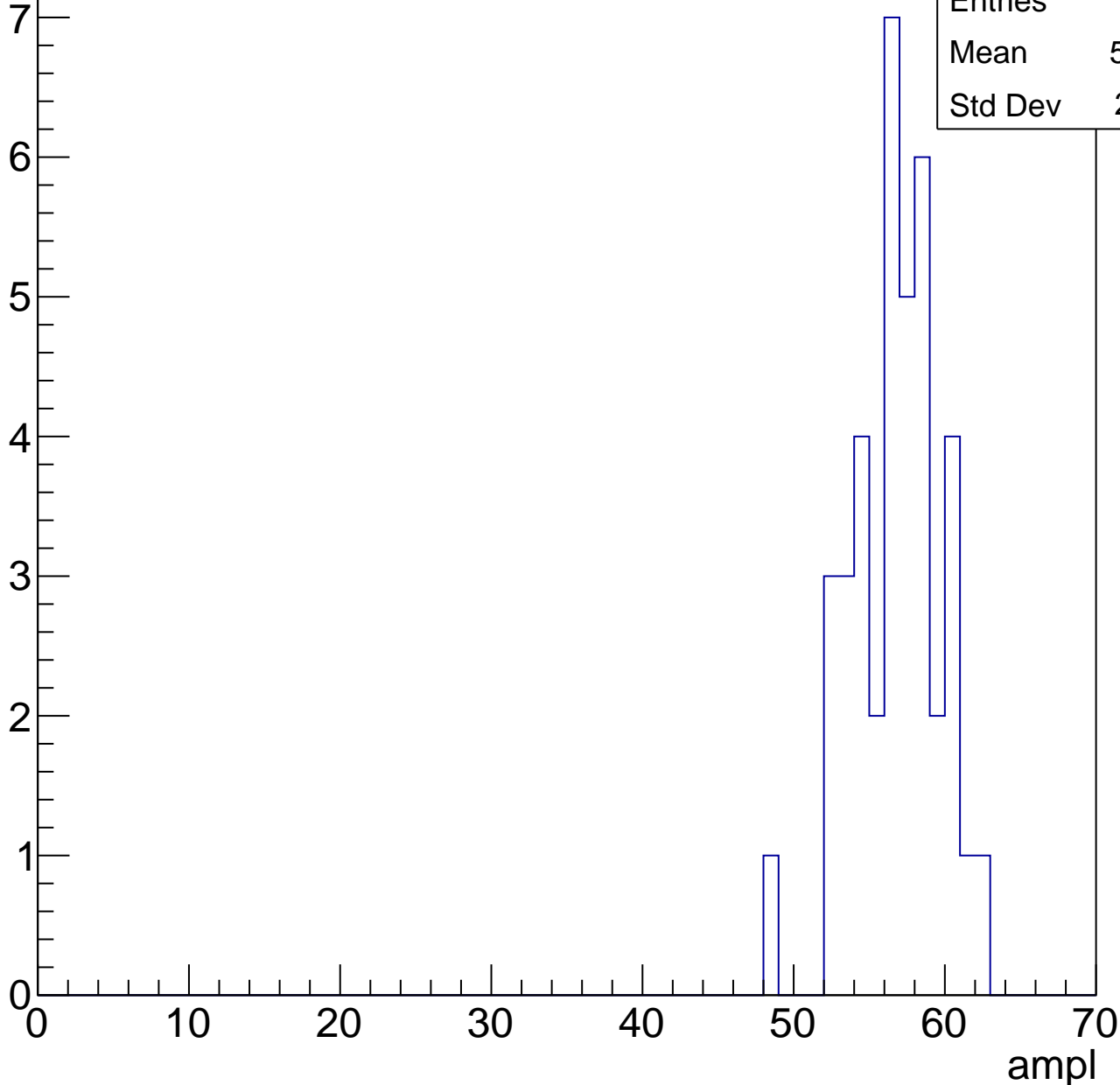


# B0L002S, U23-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	56.28
Std Dev	2.891

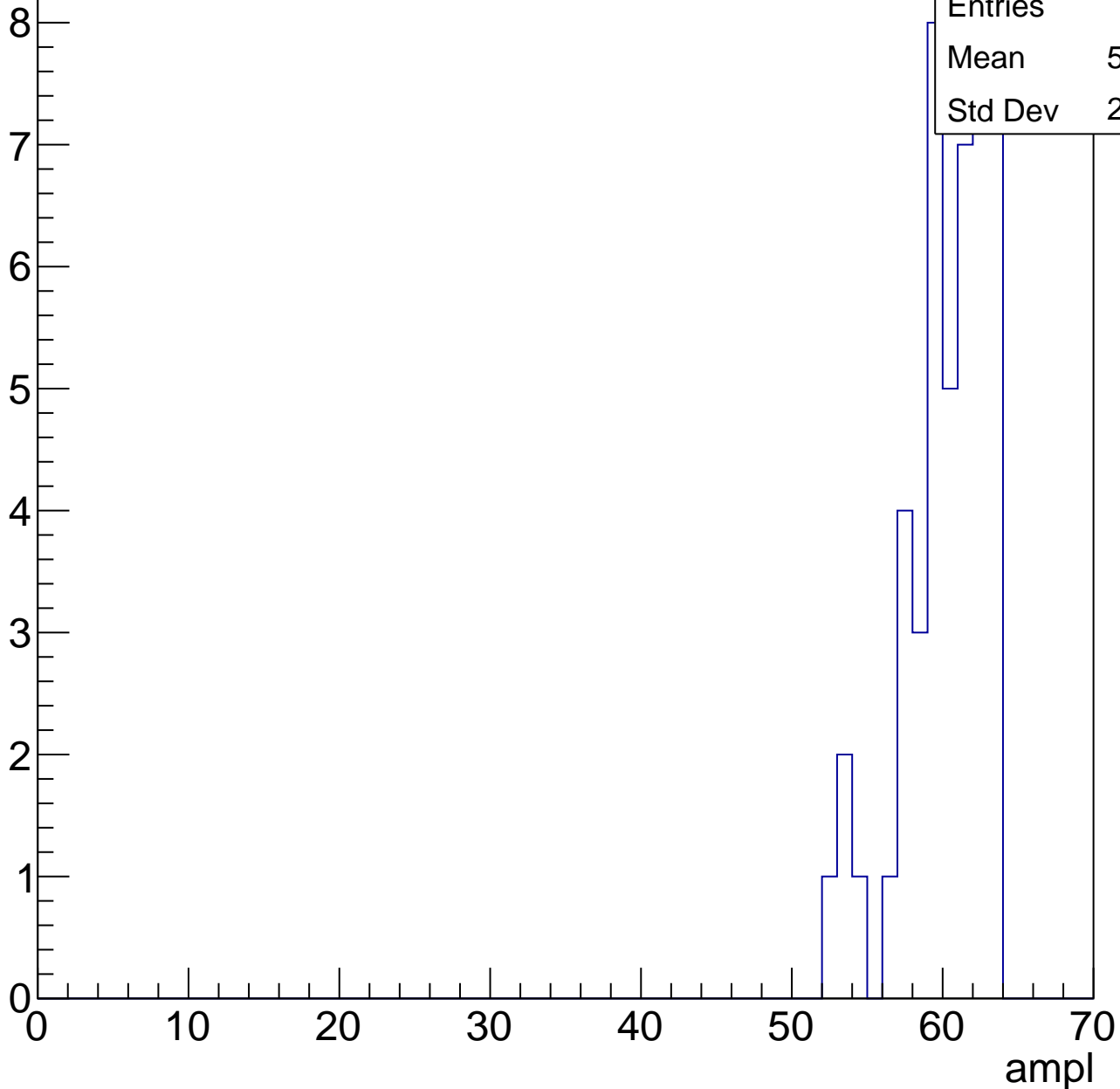


# B0L002S, U23-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

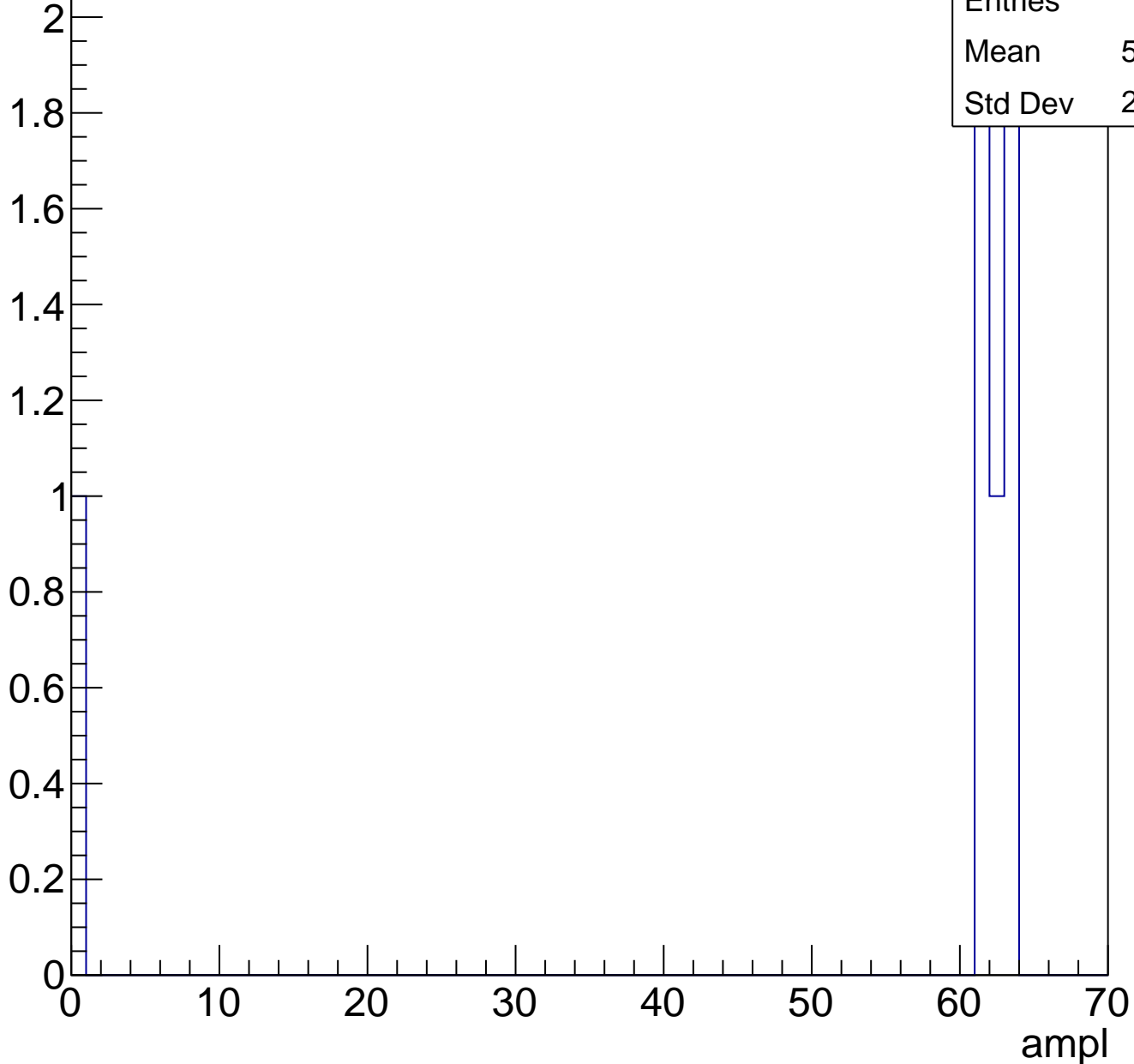
Entries	48
Mean	59.77
Std Dev	2.808



# B0L002S, U23-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch3, adc0

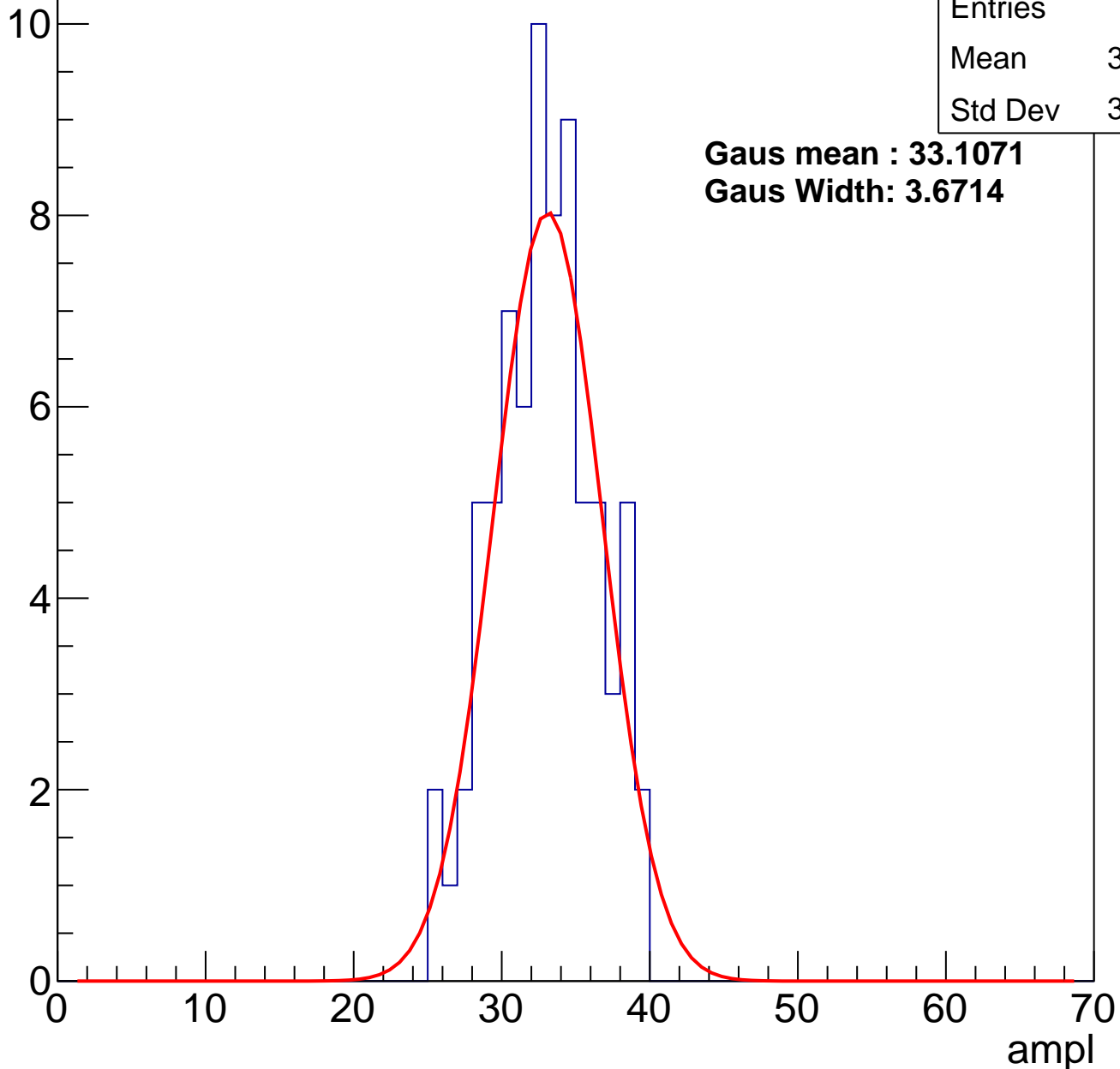
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	32.47
Std Dev	3.364

**Gaus mean : 33.1071**

**Gaus Width: 3.6714**

Entry



# B0L002S, U23-ch3, adc1

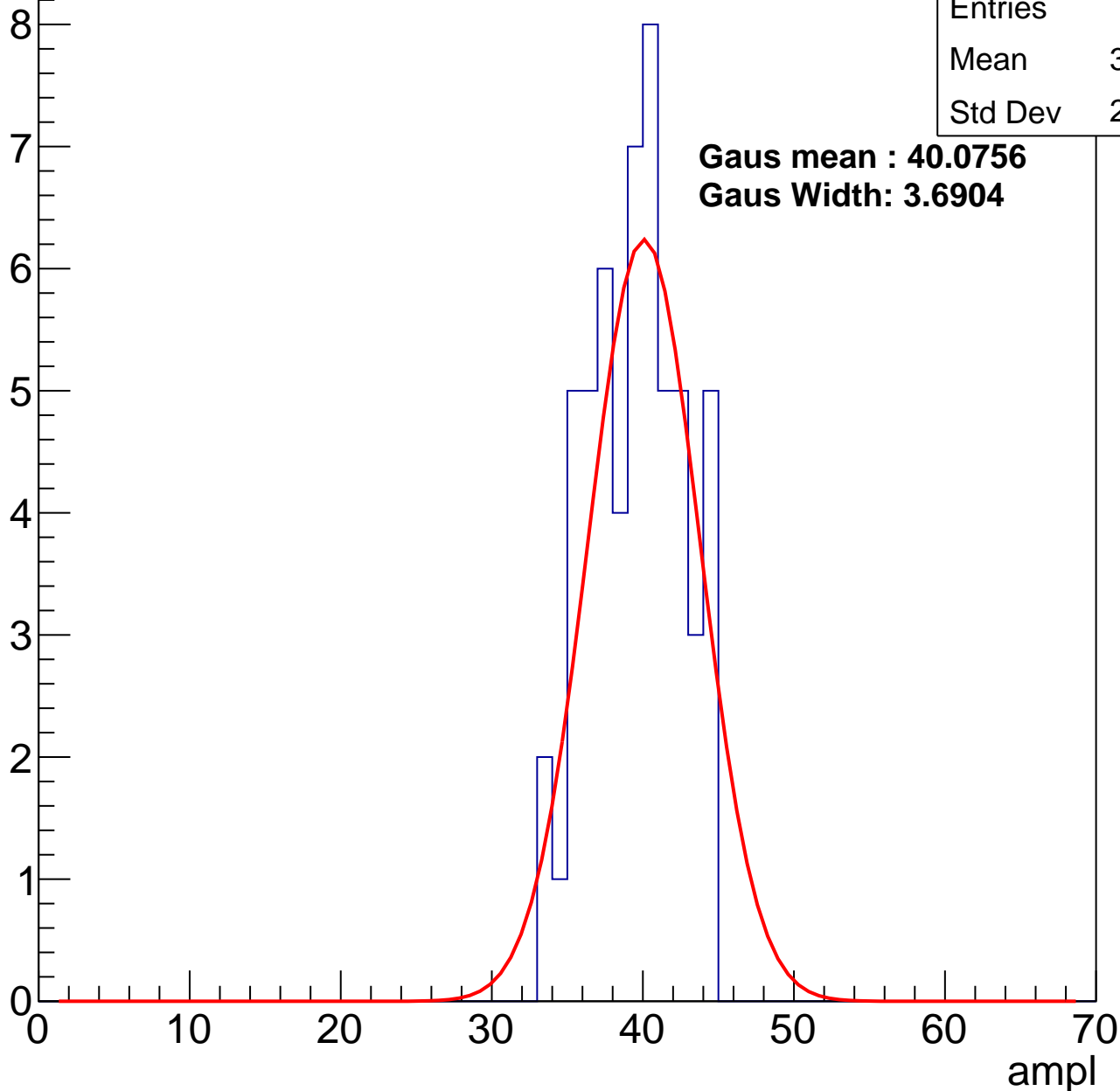
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	39.04
Std Dev	2.976

**Gaus mean : 40.0756**

**Gaus Width: 3.6904**



# B0L002S, U23-ch3, adc2

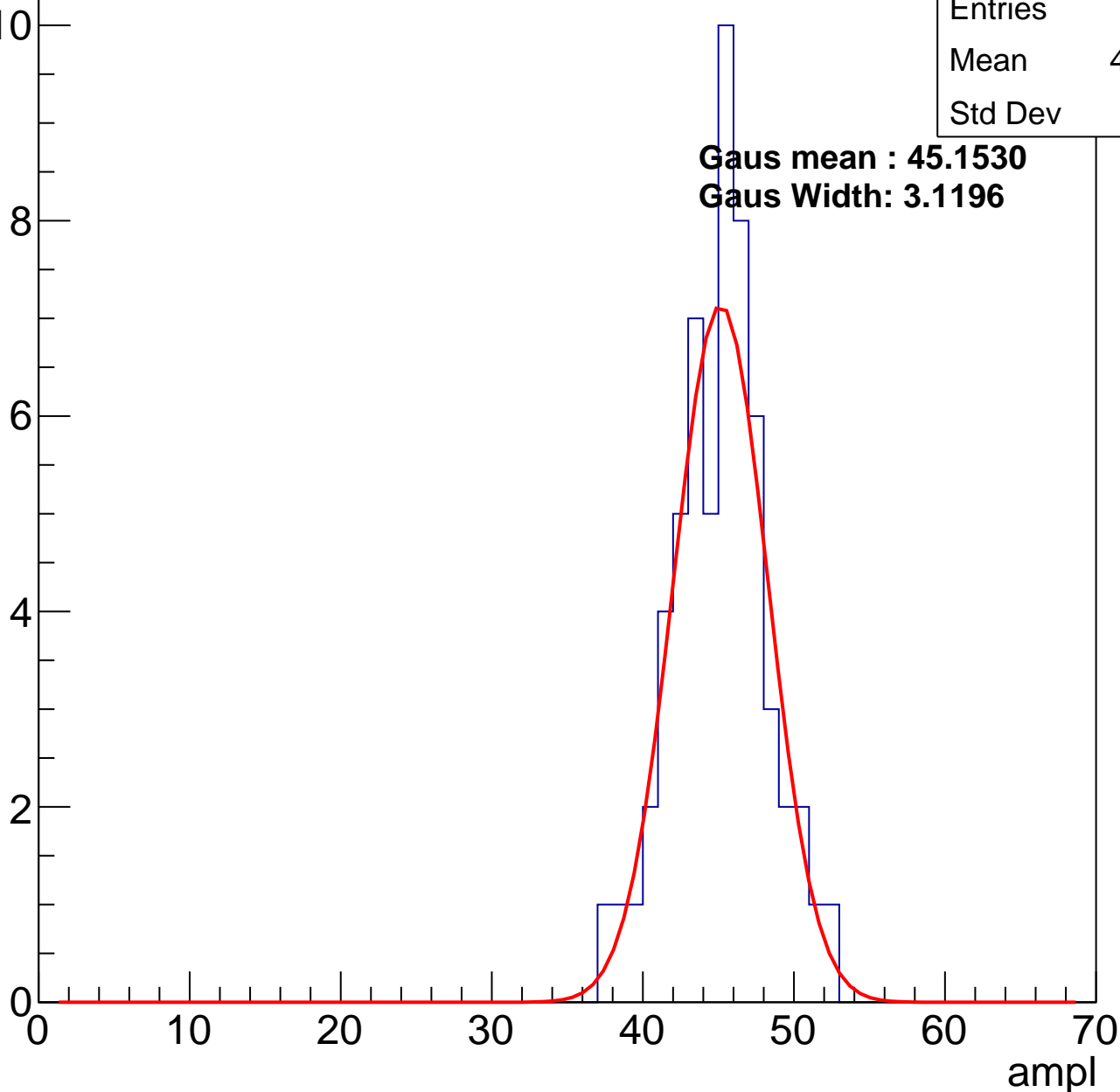
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44.64
Std Dev	3.08

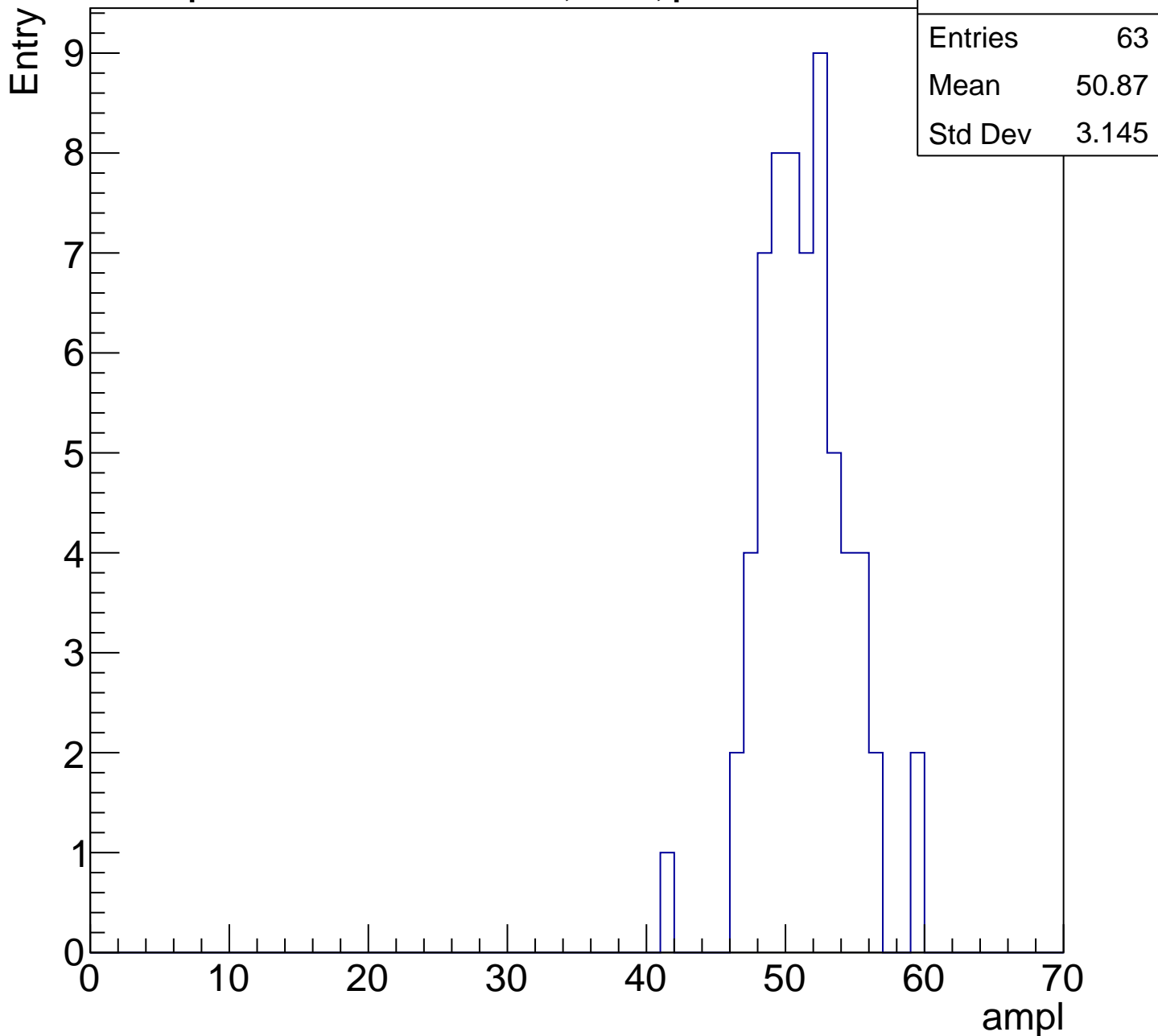
**Gaus mean : 45.1530**

**Gaus Width: 3.1196**



# B0L002S, U23-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U23-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

Entries 68

Mean 57.25

Std Dev 2.836

8

6

4

2

0

0

10

20

30

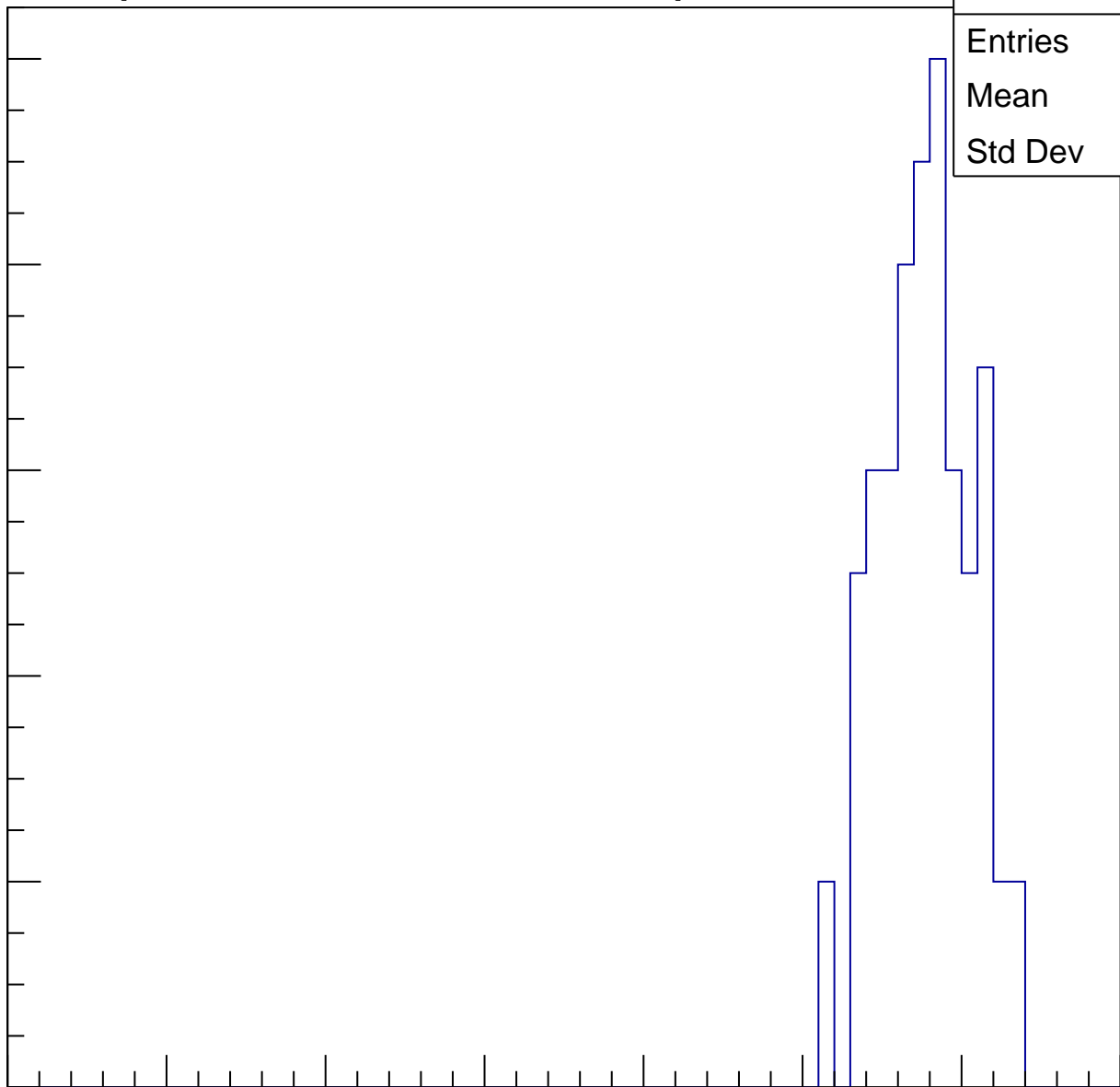
40

50

60

70

ampl

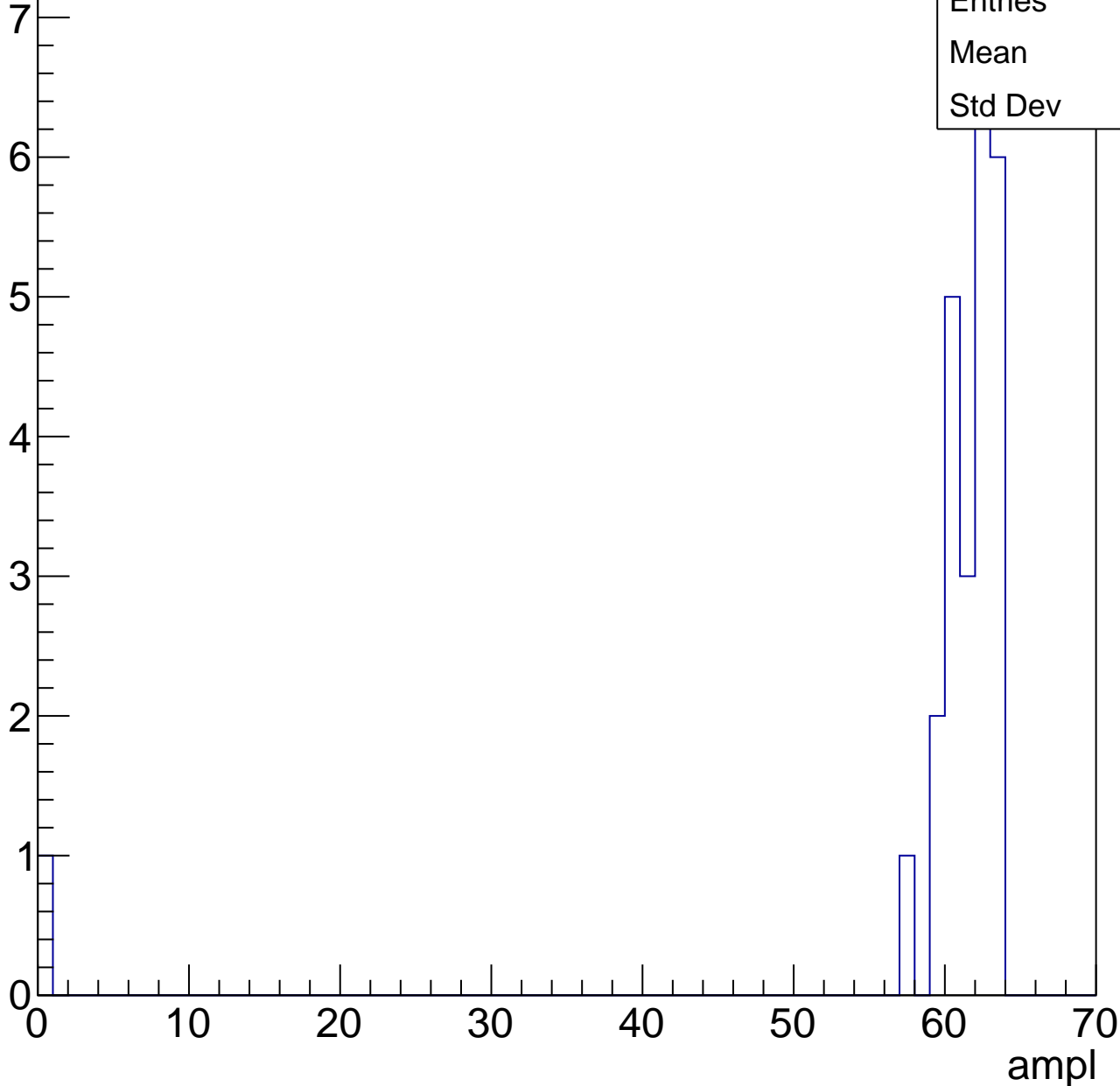


# B0L002S, U23-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.8
Std Dev	12.1



# B0L002S, U23-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch4, adc0

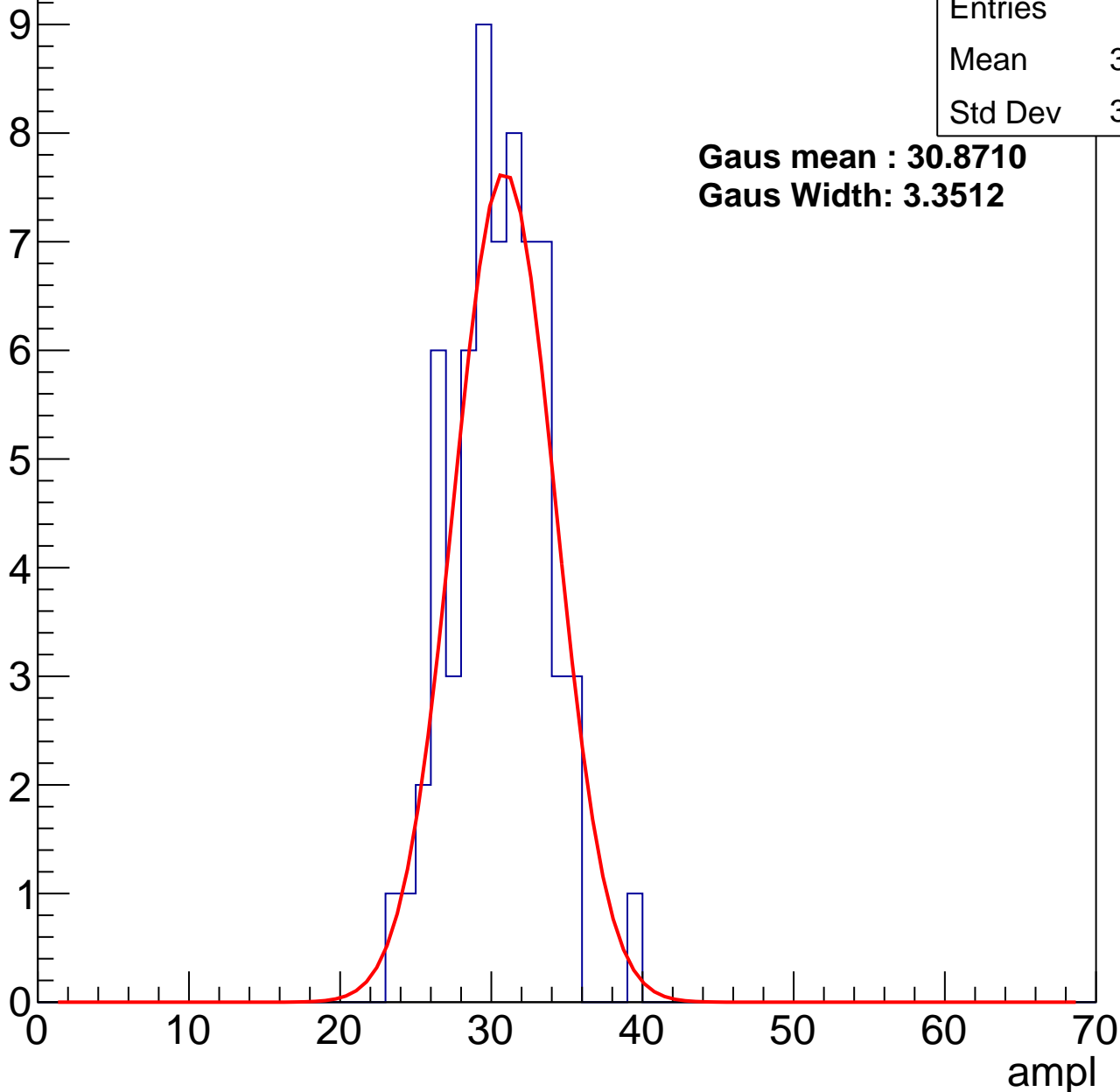
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.03
Std Dev	3.046

**Gaus mean : 30.8710**

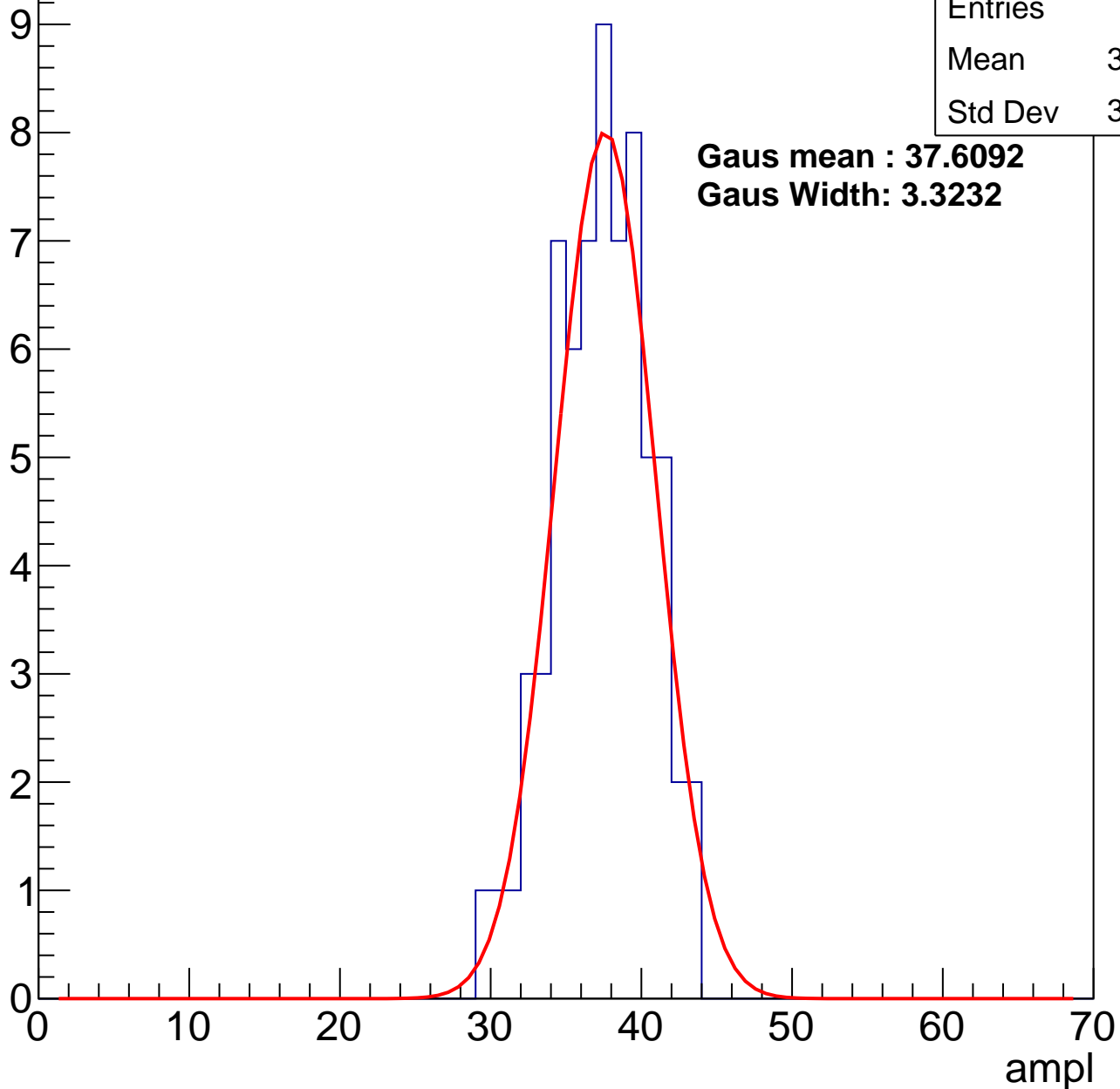
**Gaus Width: 3.3512**



# B0L002S, U23-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

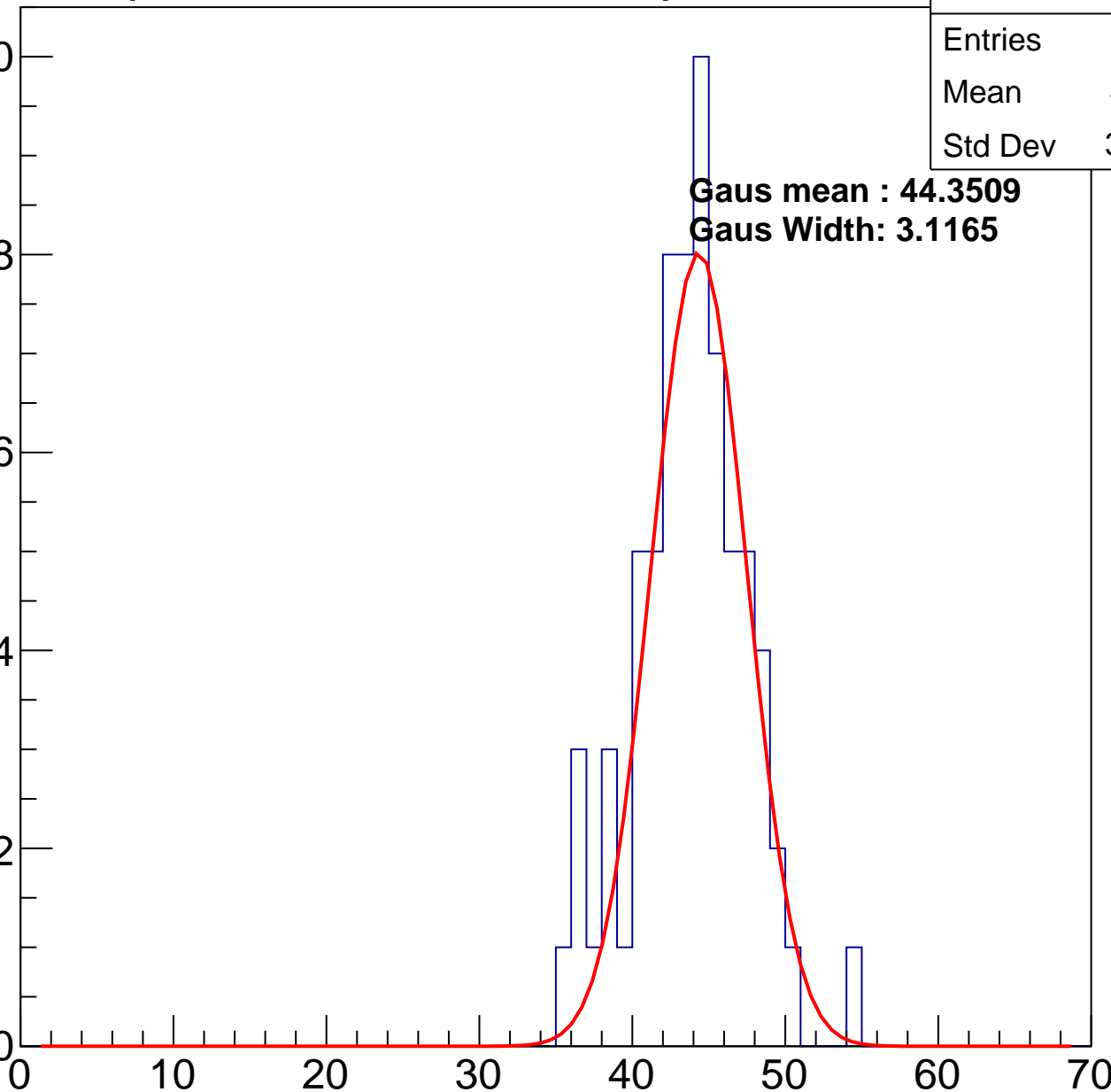
Entries	70
Mean	43.31
Std Dev	3.604

**Gaus mean : 44.3509**

**Gaus Width: 3.1165**

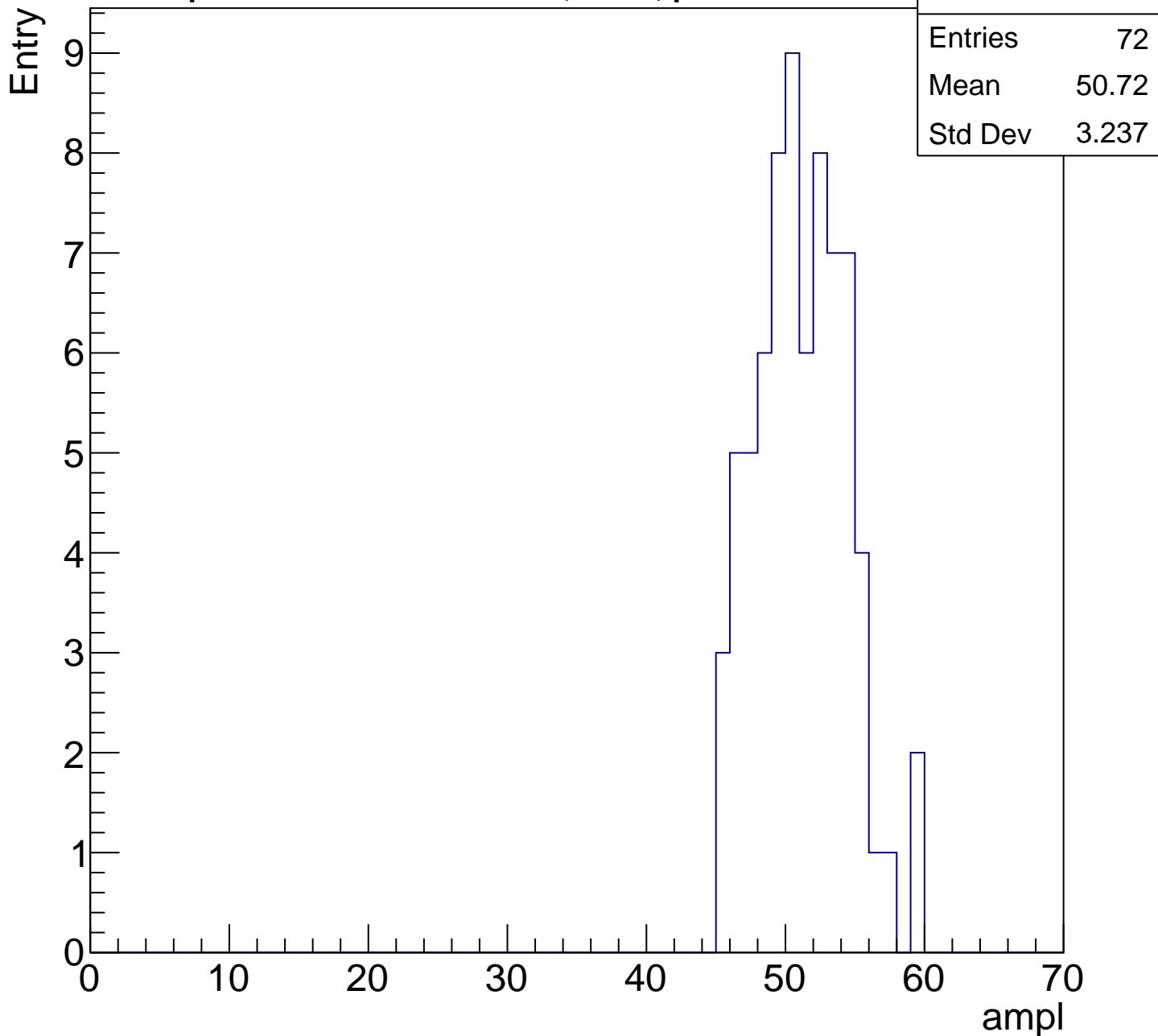
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

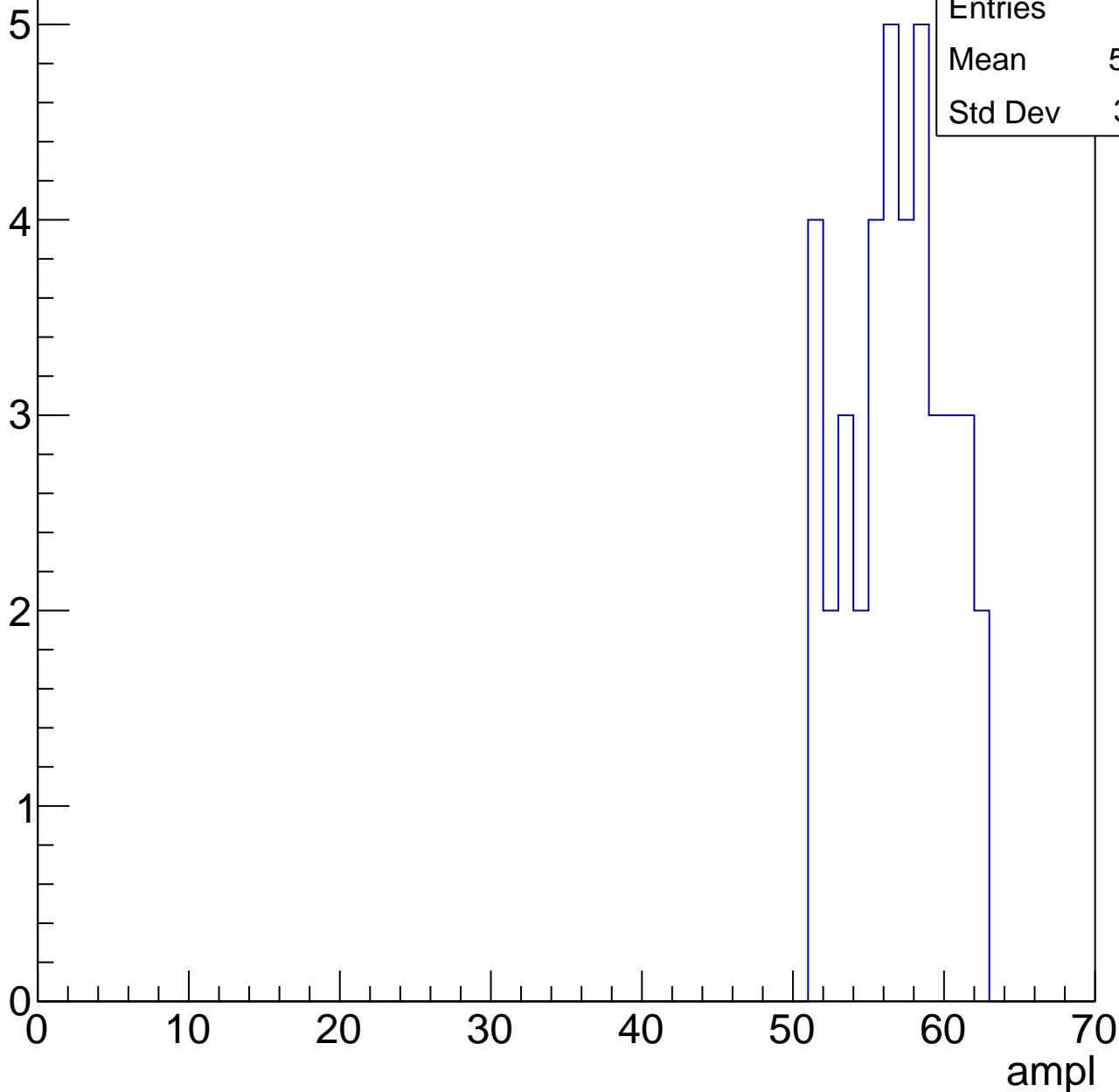


# B0L002S, U23-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	40
Mean	56.42
Std Dev	3.201



# B0L002S, U23-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

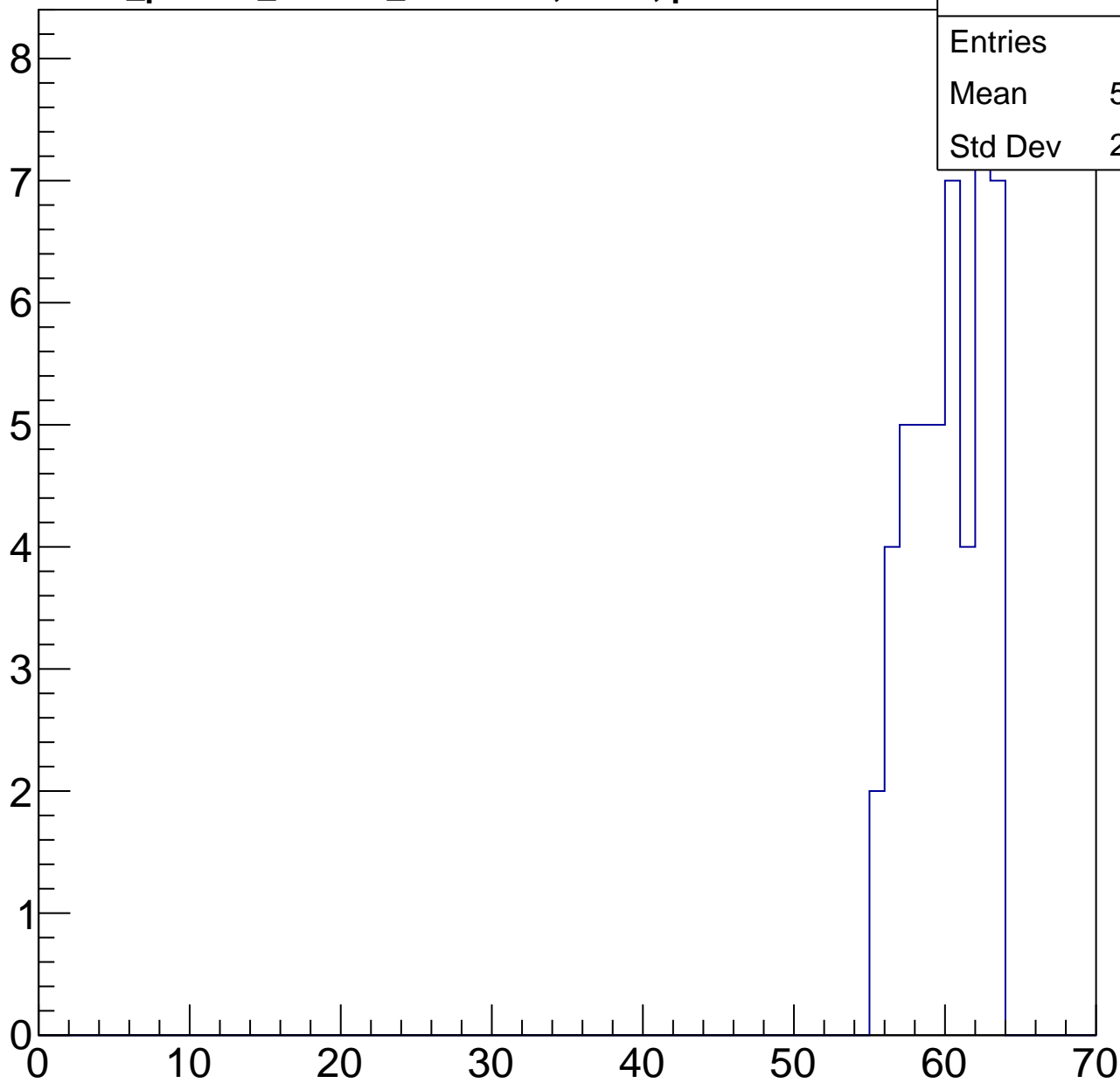
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	59.68
Std Dev	2.433

ampl

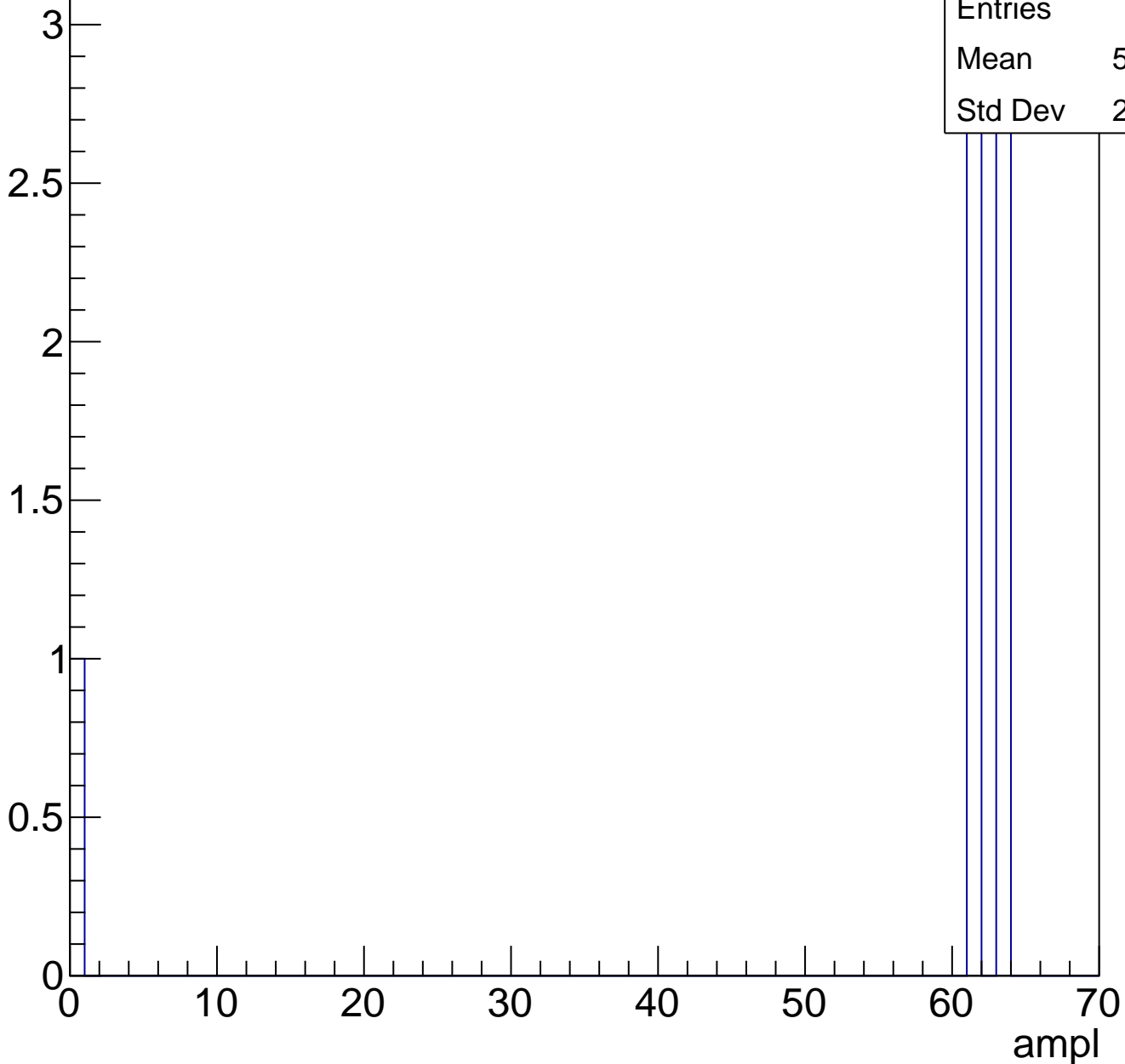
0 10 20 30 40 50 60 70



# B0L002S, U23-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch5, adc0

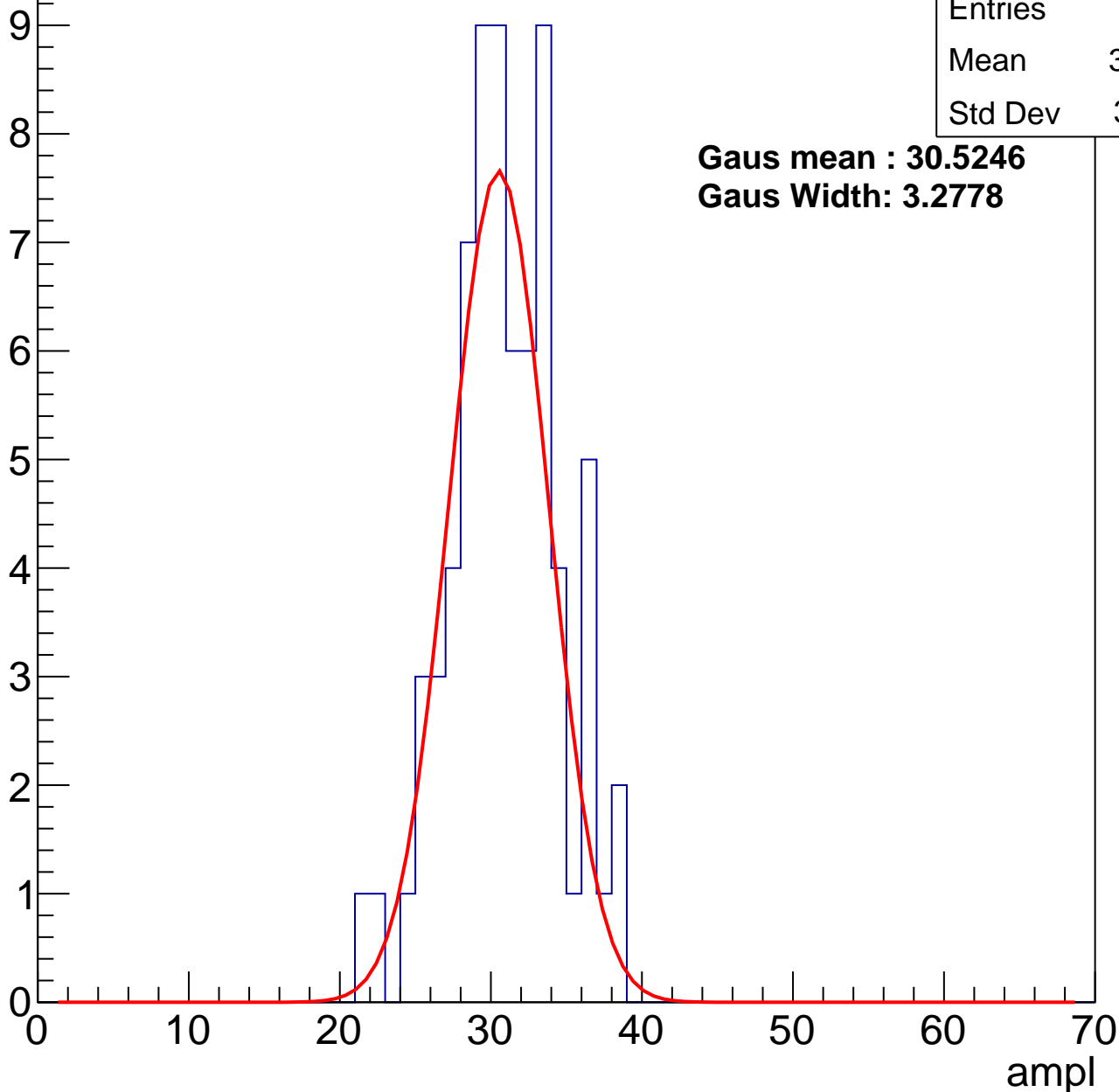
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.47
Std Dev	3.571

**Gaus mean : 30.5246**

**Gaus Width: 3.2778**



# B0L002S, U23-ch5, adc1

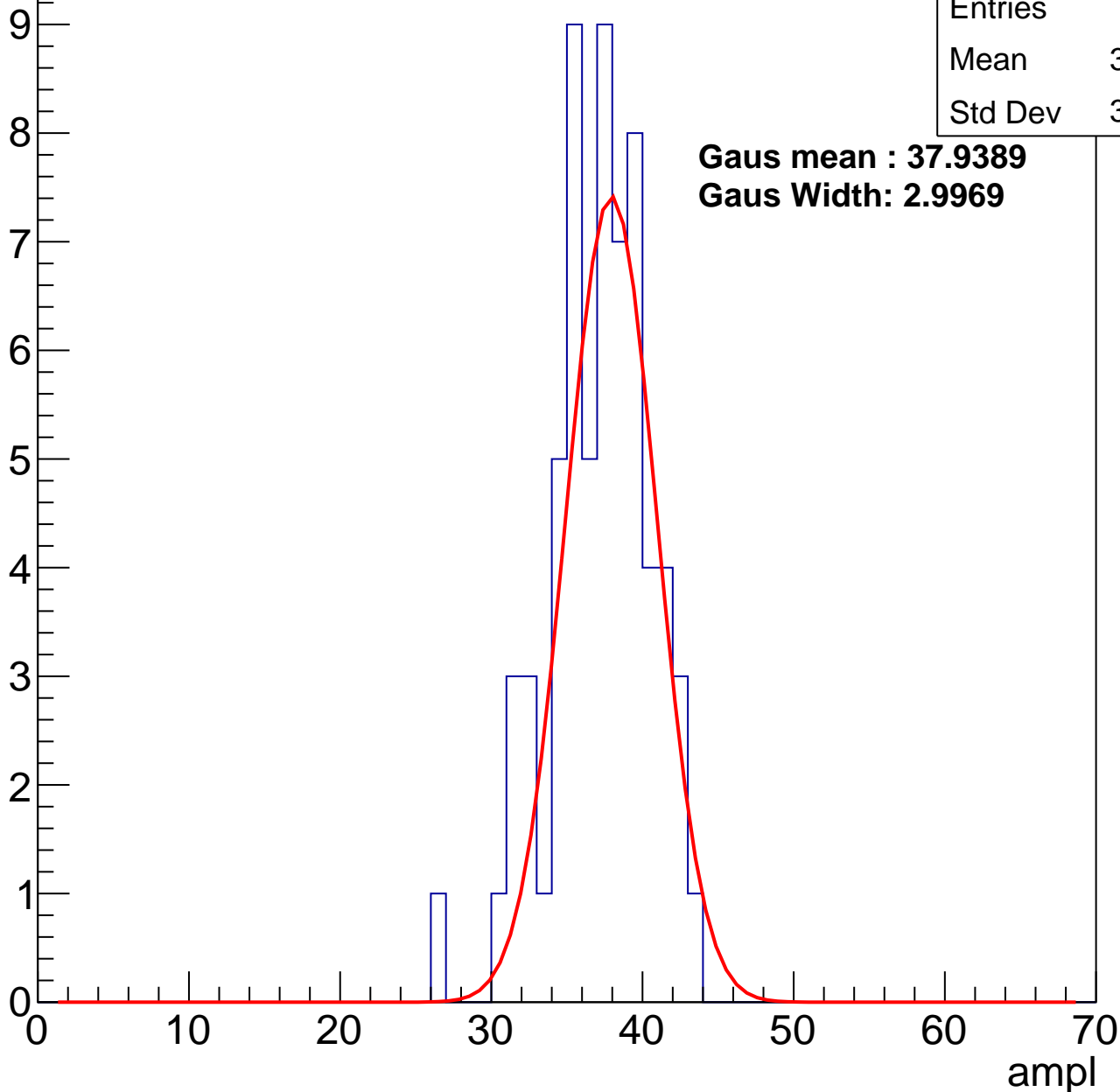
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.67
Std Dev	3.303

**Gaus mean : 37.9389**

**Gaus Width: 2.9969**



# B0L002S, U23-ch5, adc2

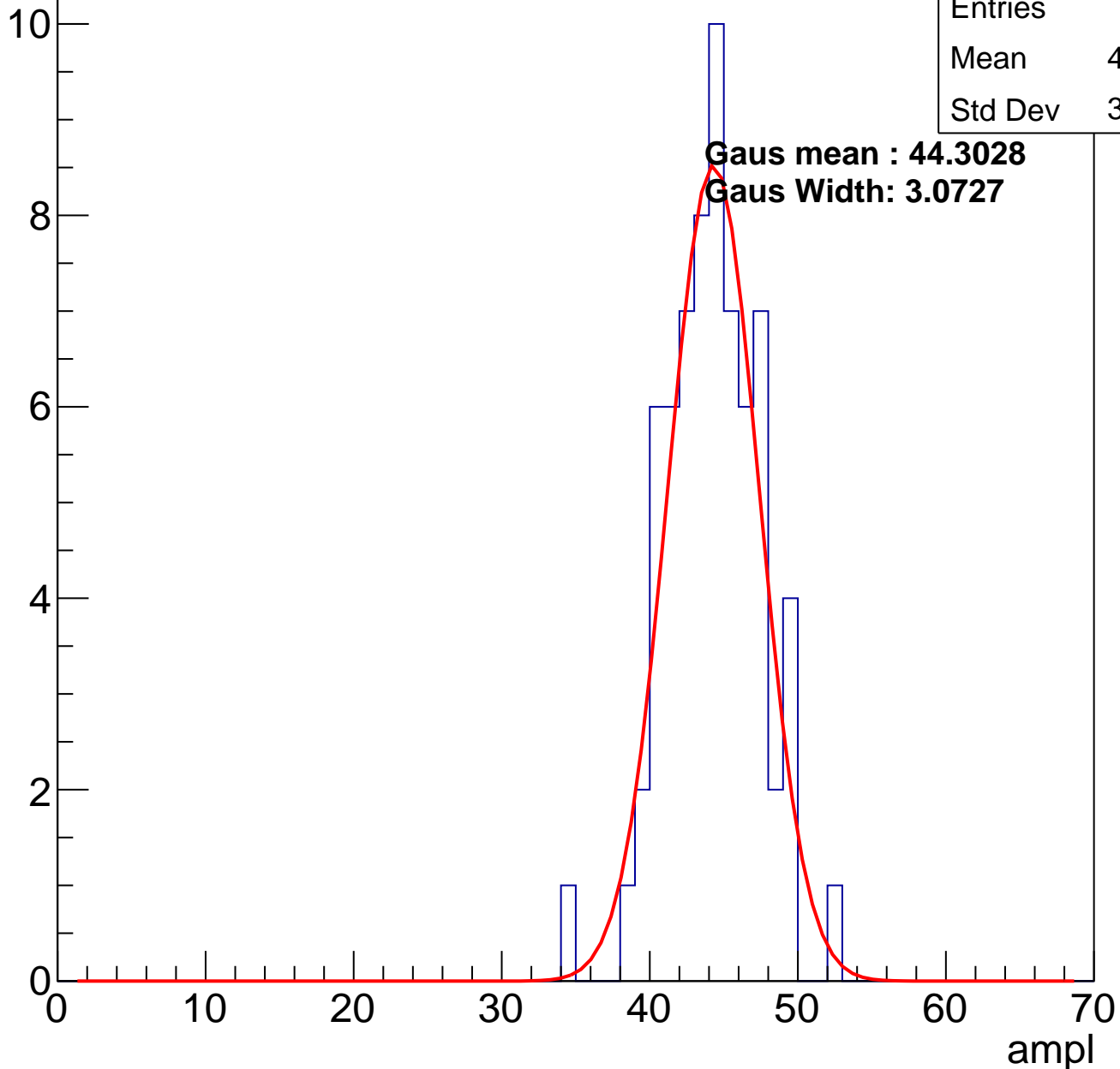
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	68
Mean	43.79
Std Dev	3.113

**Gaus mean : 44.3028**

**Gaus Width: 3.0727**

Entry

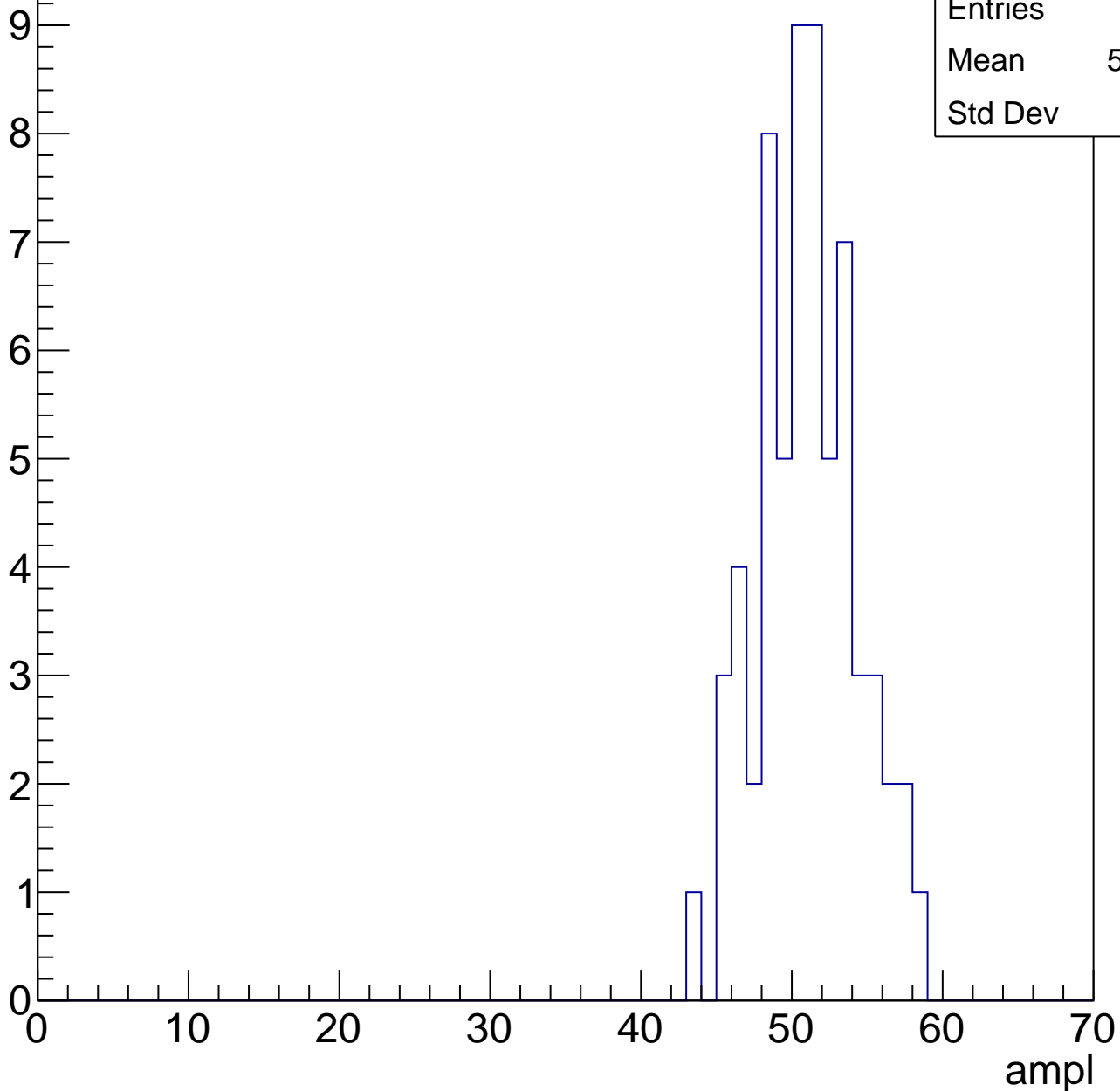


# B0L002S, U23-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	50.56
Std Dev	3.24

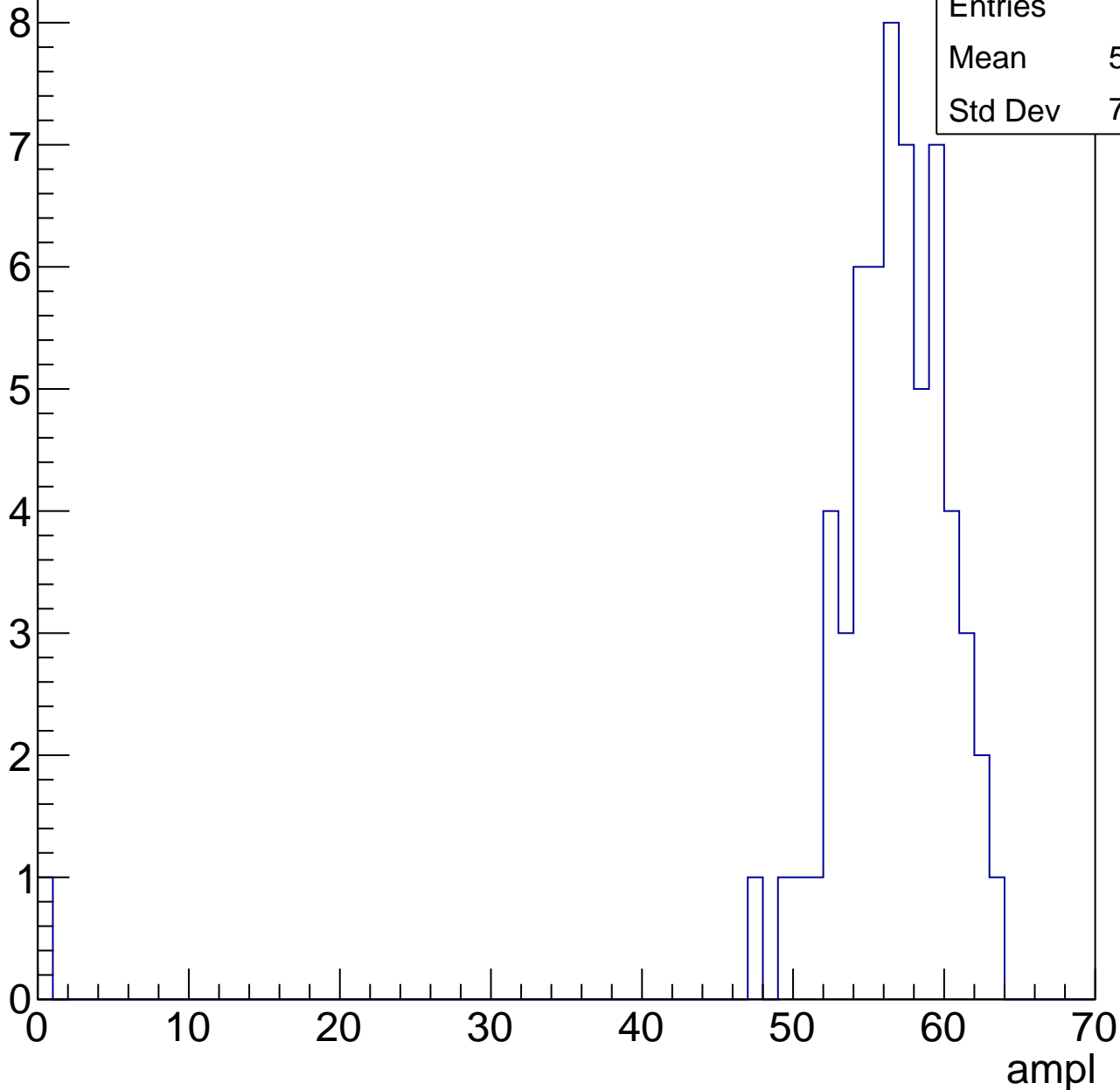


# B0L002S, U23-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	55.38
Std Dev	7.866

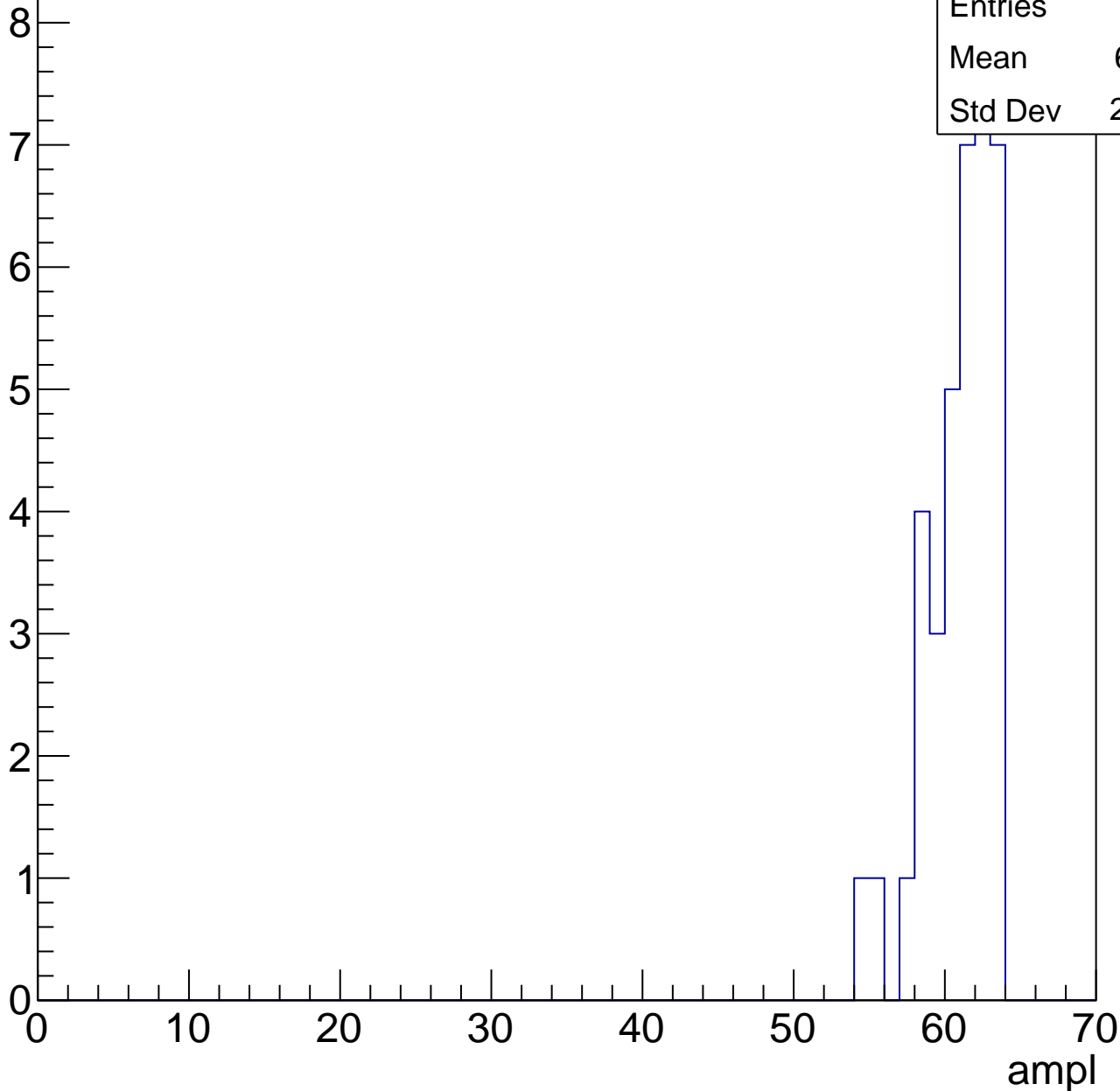


# B0L002S, U23-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	60.51
Std Dev	2.213



# B0L002S, U23-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

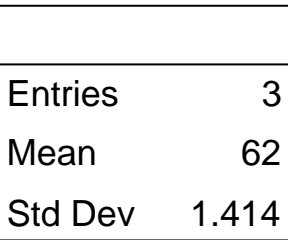
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62
Std Dev	1.414

0 10 20 30 40 50 60 70

ampl

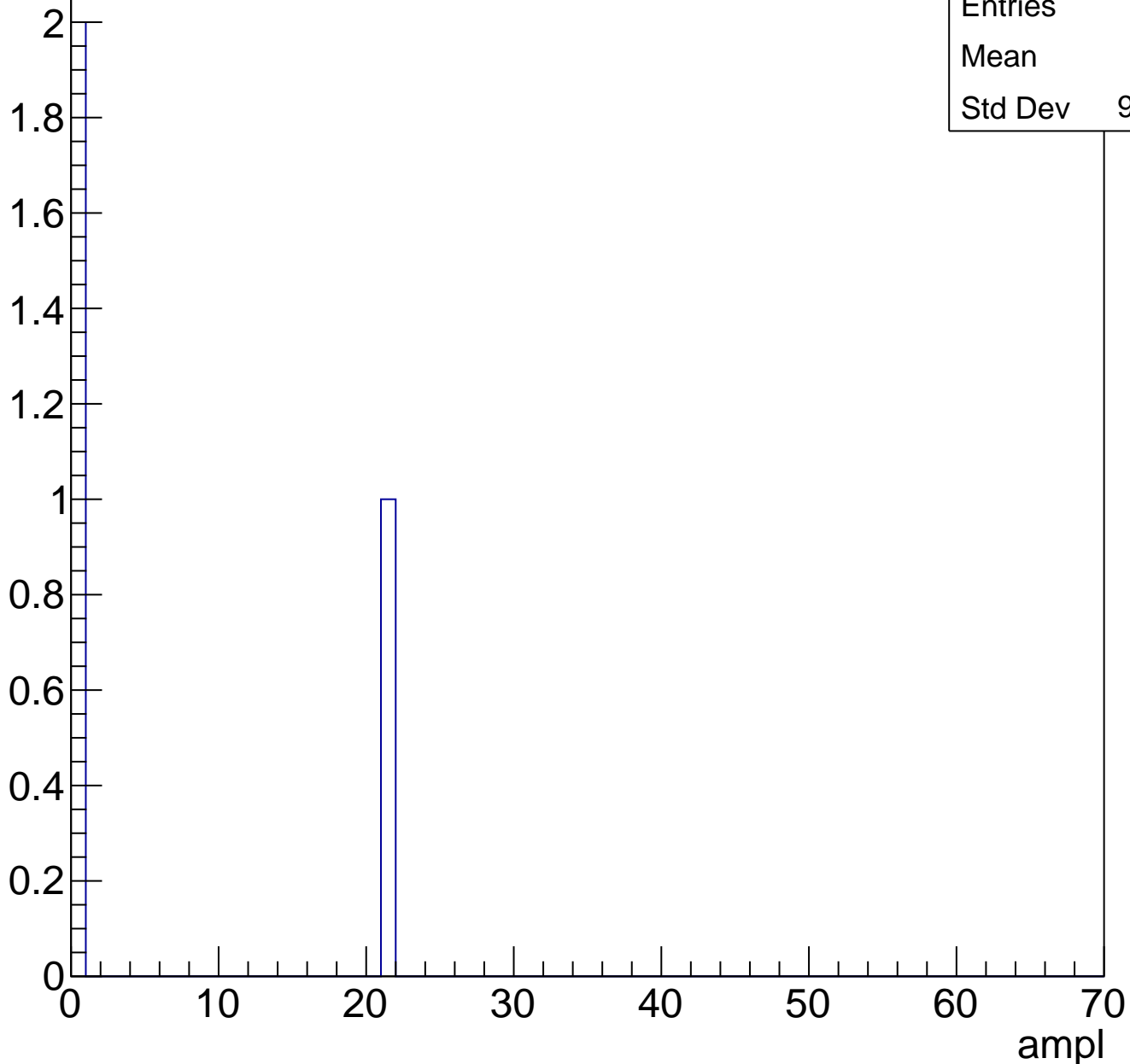




# B0L002S, U23-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

# B0L002S, U23-ch6, adc0

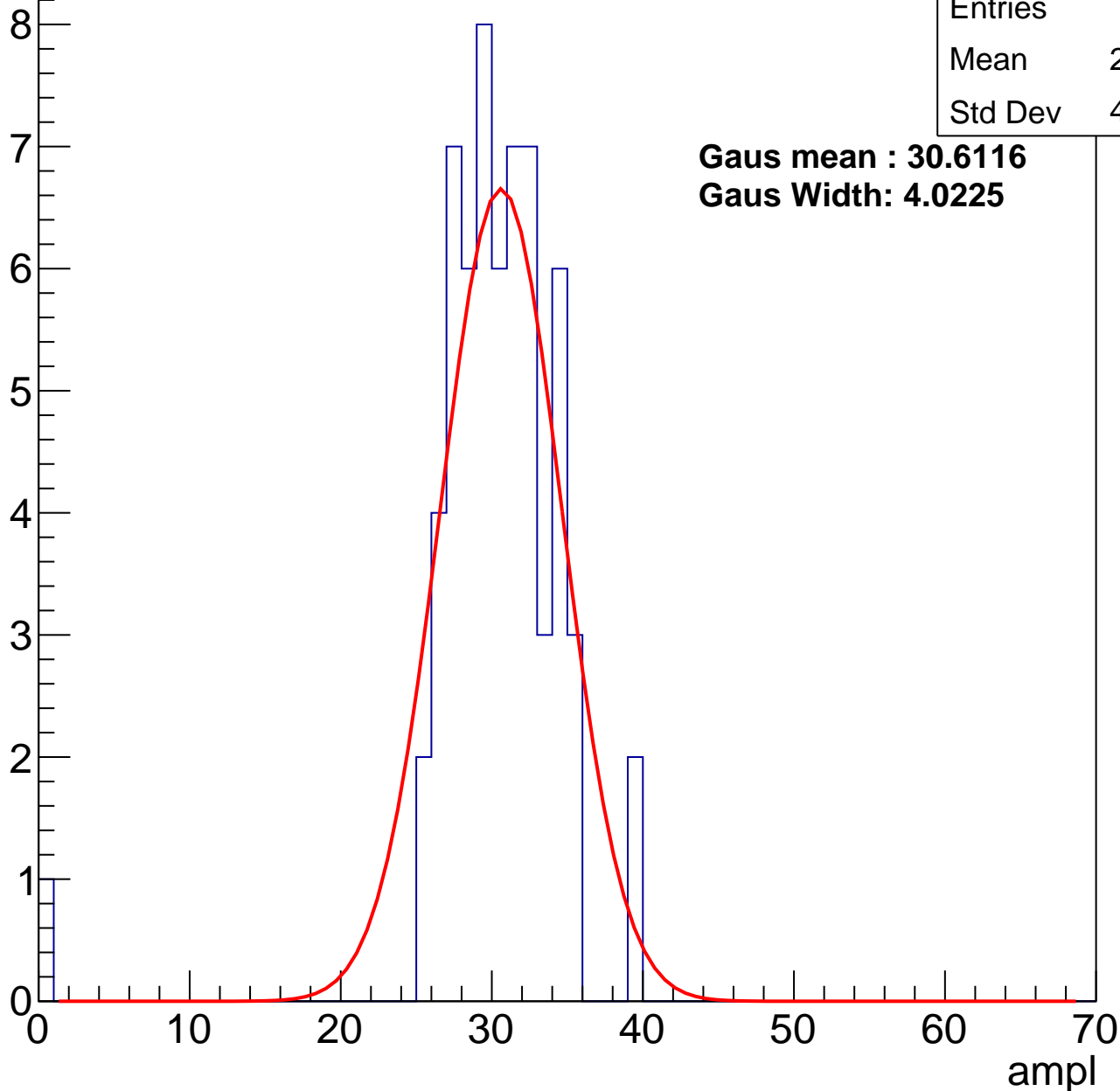
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.84
Std Dev	4.923

**Gaus mean : 30.6116**

**Gaus Width: 4.0225**



# B0L002S, U23-ch6, adc1

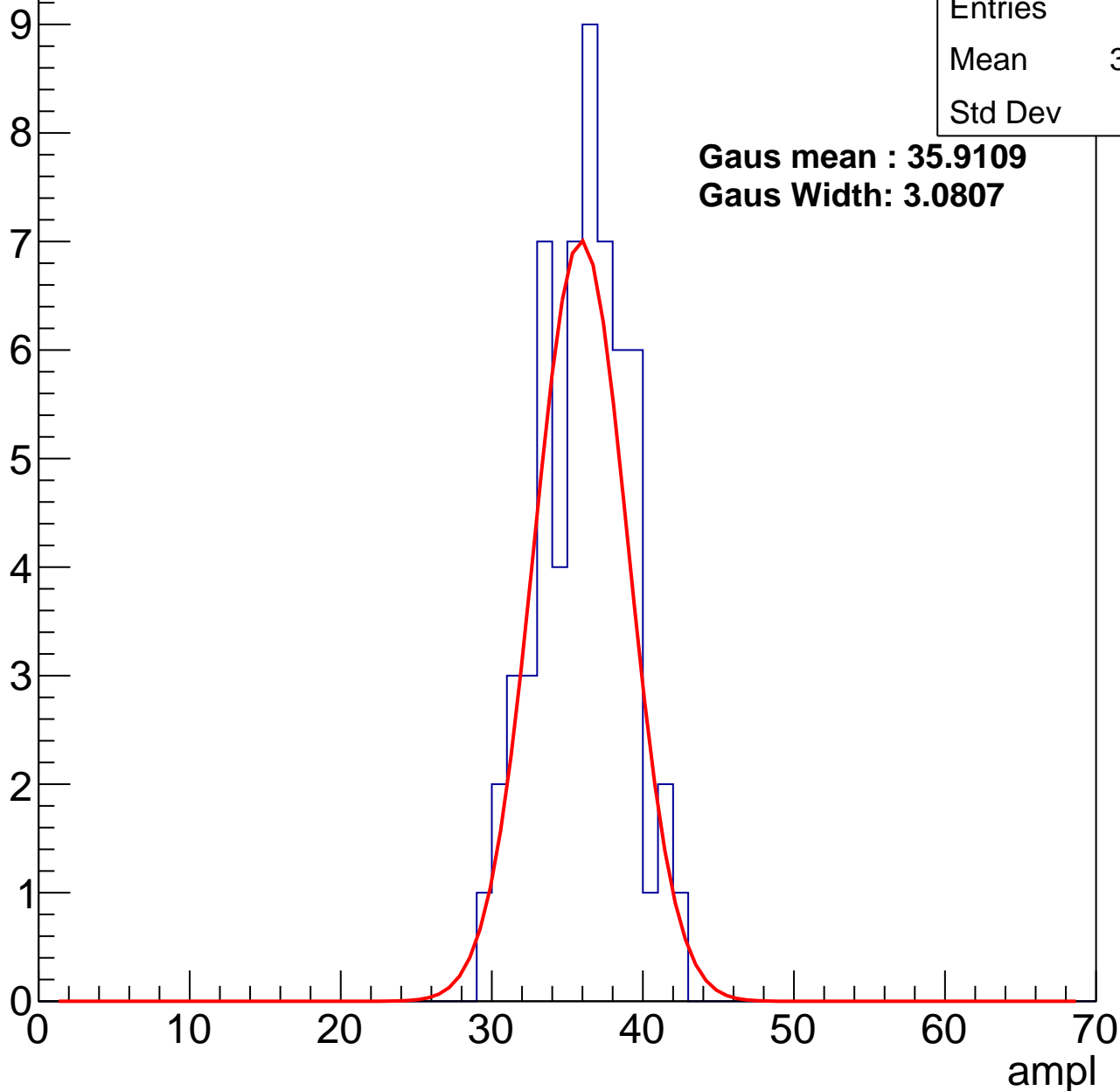
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	35.58
Std Dev	2.93

**Gaus mean : 35.9109**

**Gaus Width: 3.0807**



# B0L002S, U23-ch6, adc2

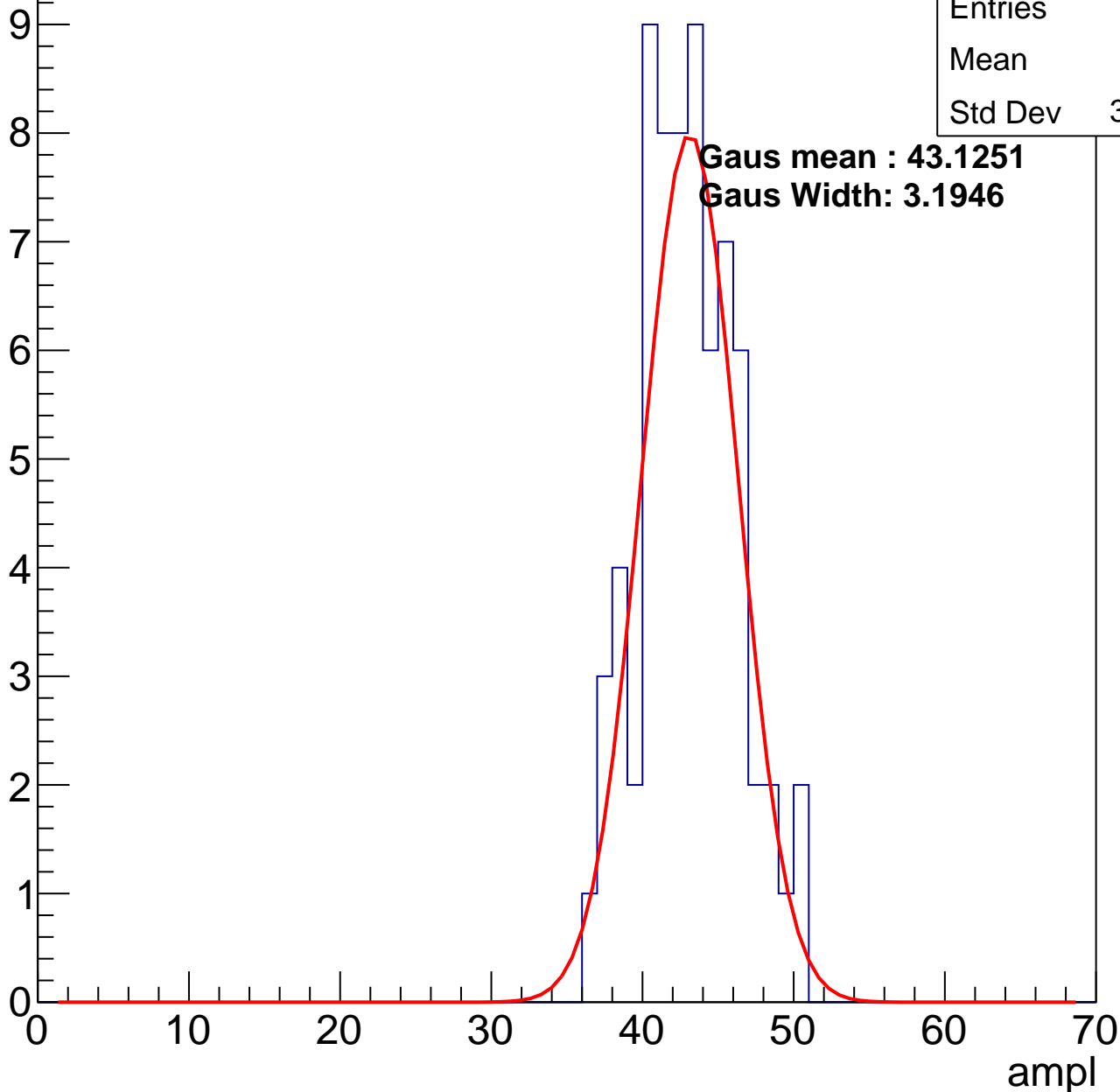
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.6
Std Dev	3.169

**Gaus mean : 43.1251**

**Gaus Width: 3.1946**

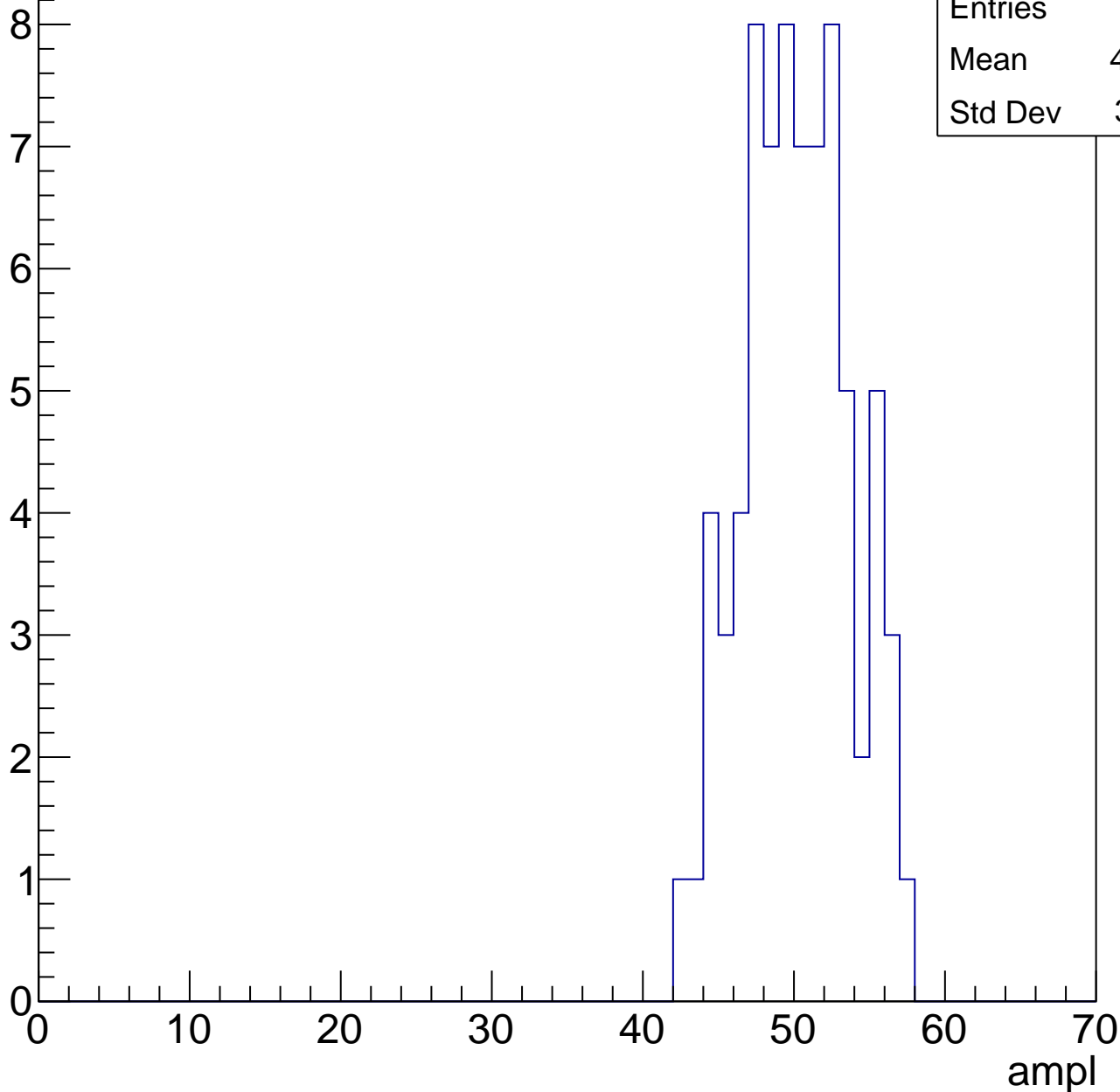


# B0L002S, U23-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

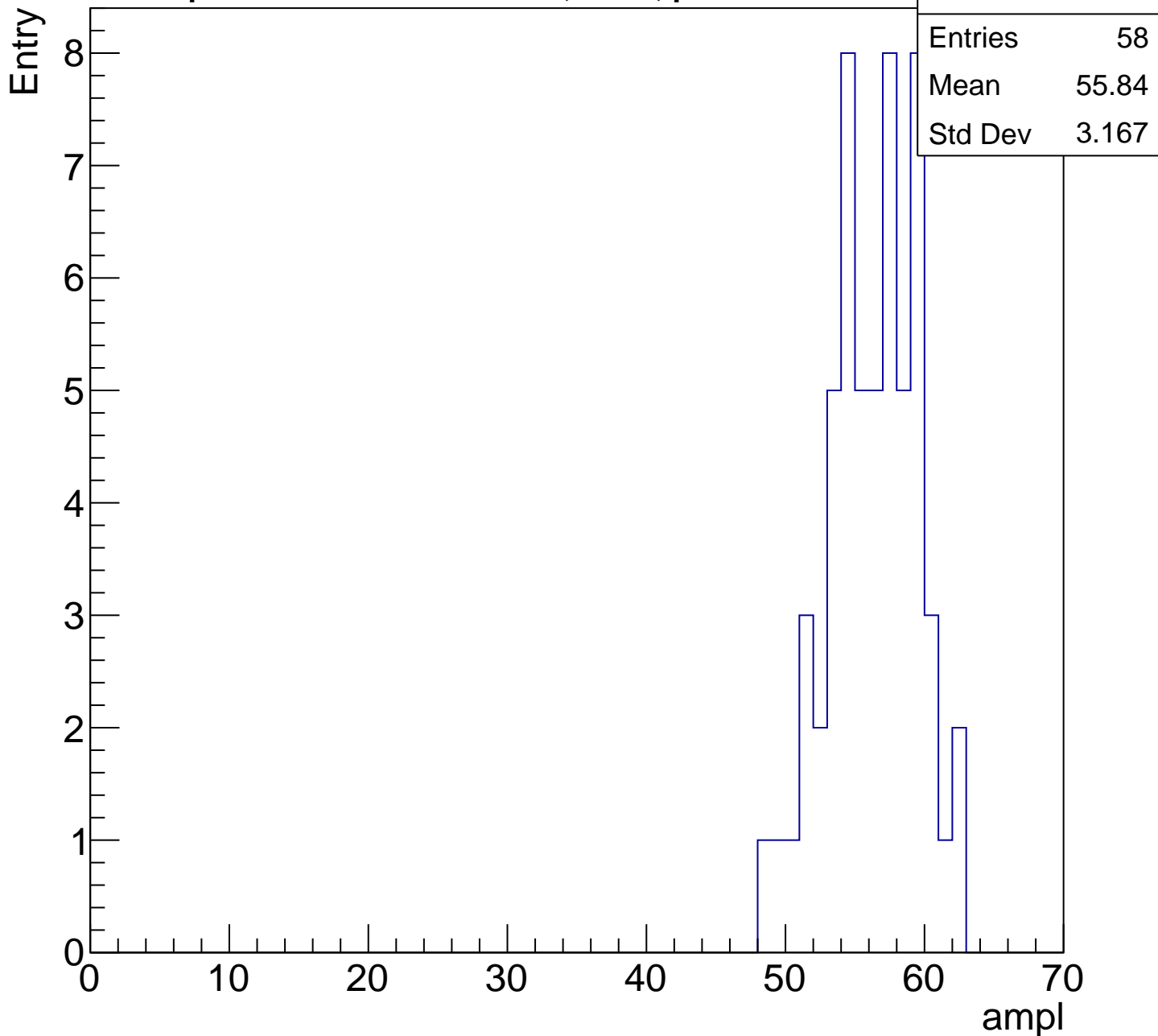
Entry

Entries	74
Mean	49.73
Std Dev	3.481



# B0L002S, U23-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U23-ch6, adc5

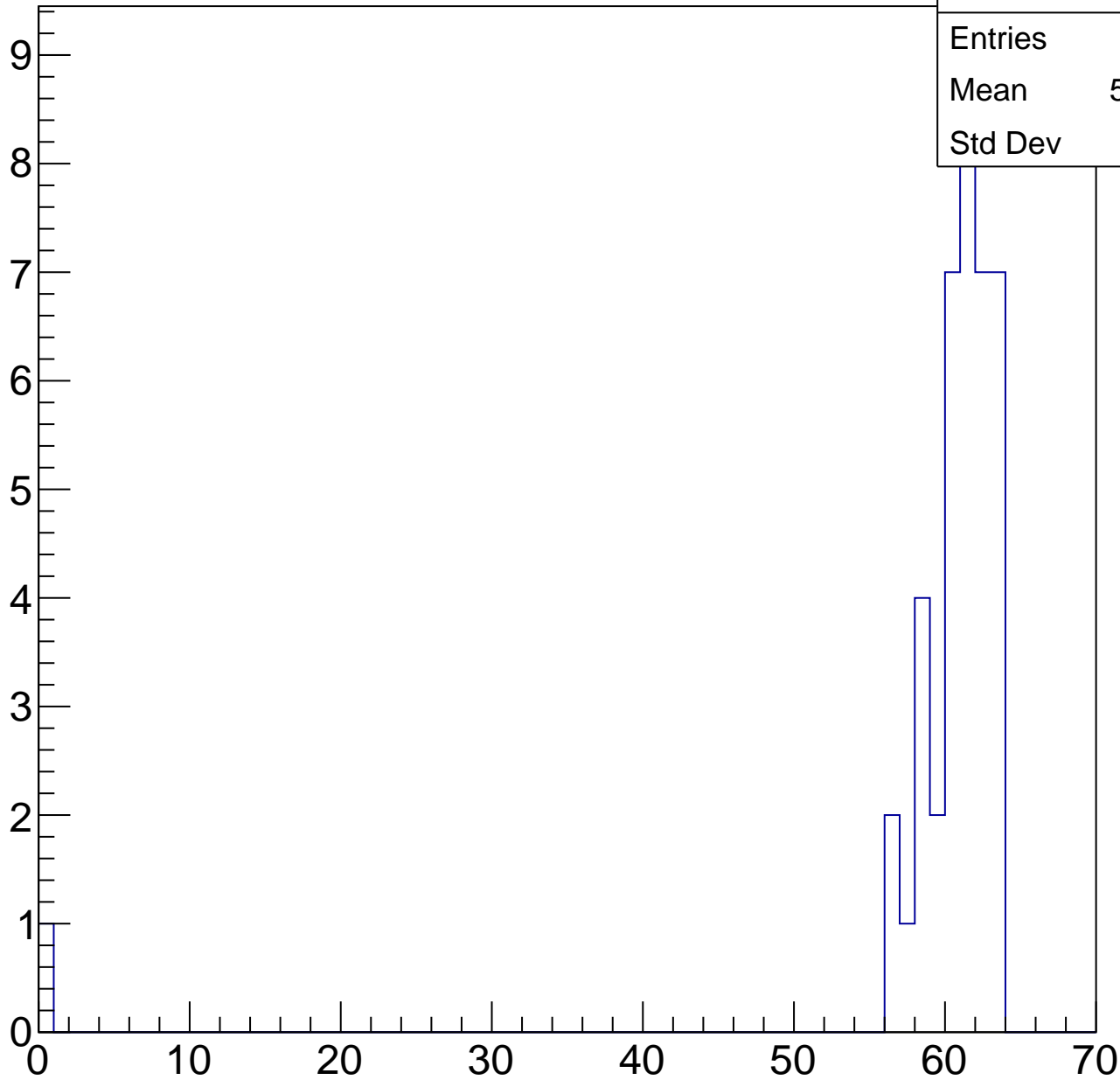
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	59.08
Std Dev	9.65

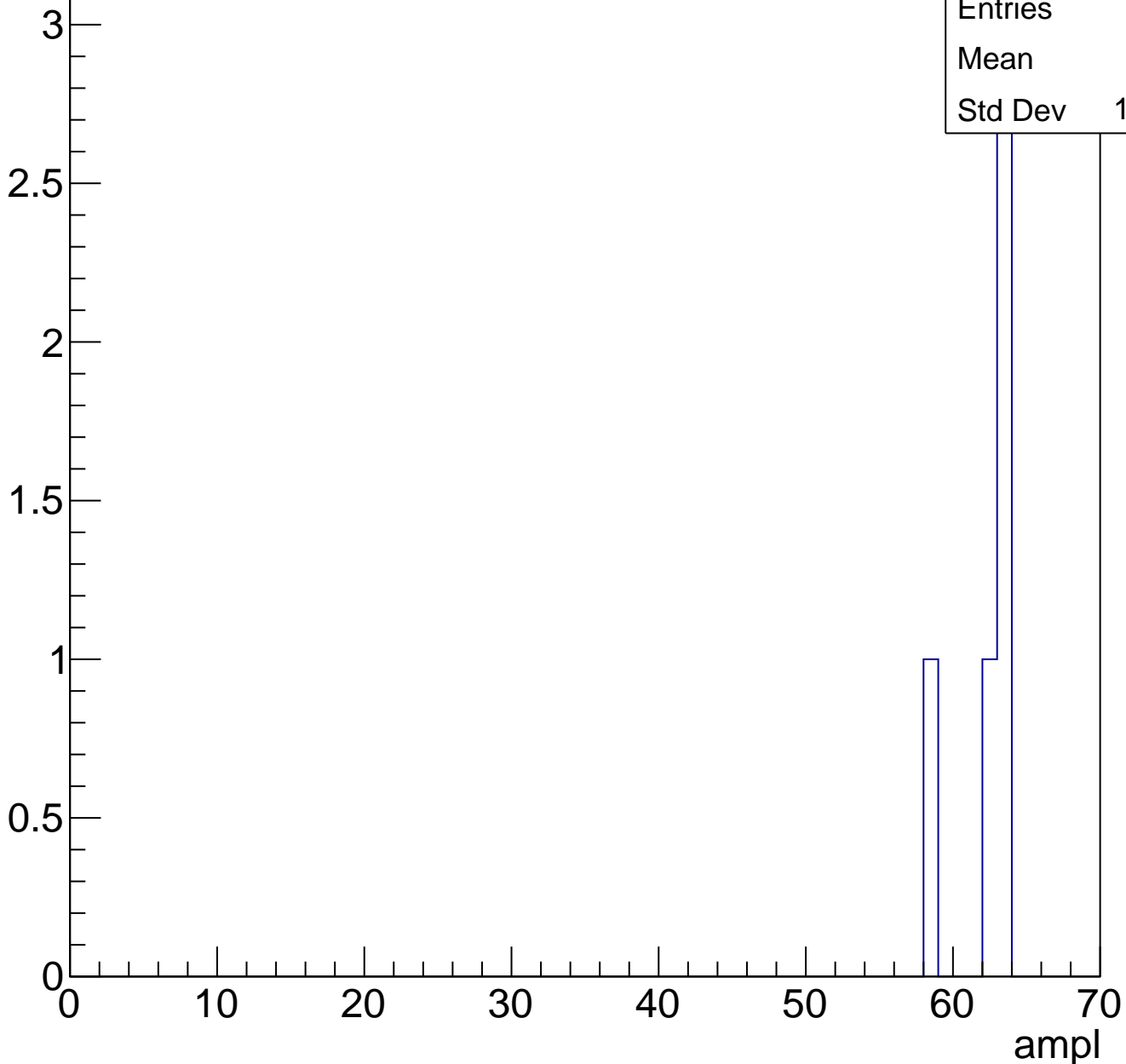
ampl



# B0L002S, U23-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch7, adc0

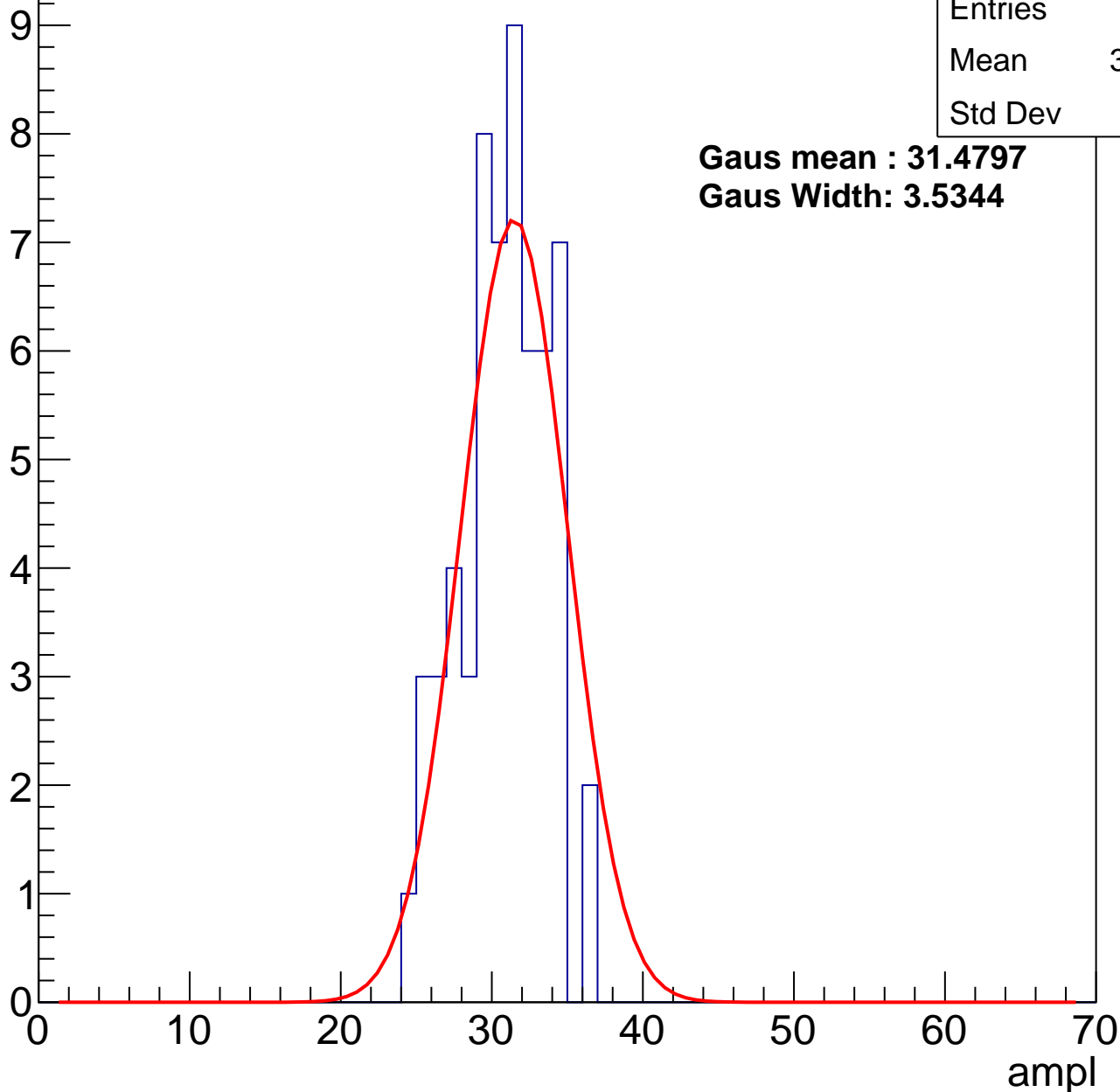
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.34
Std Dev	2.85

**Gaus mean : 31.4797**

**Gaus Width: 3.5344**



# B0L002S, U23-ch7, adc1

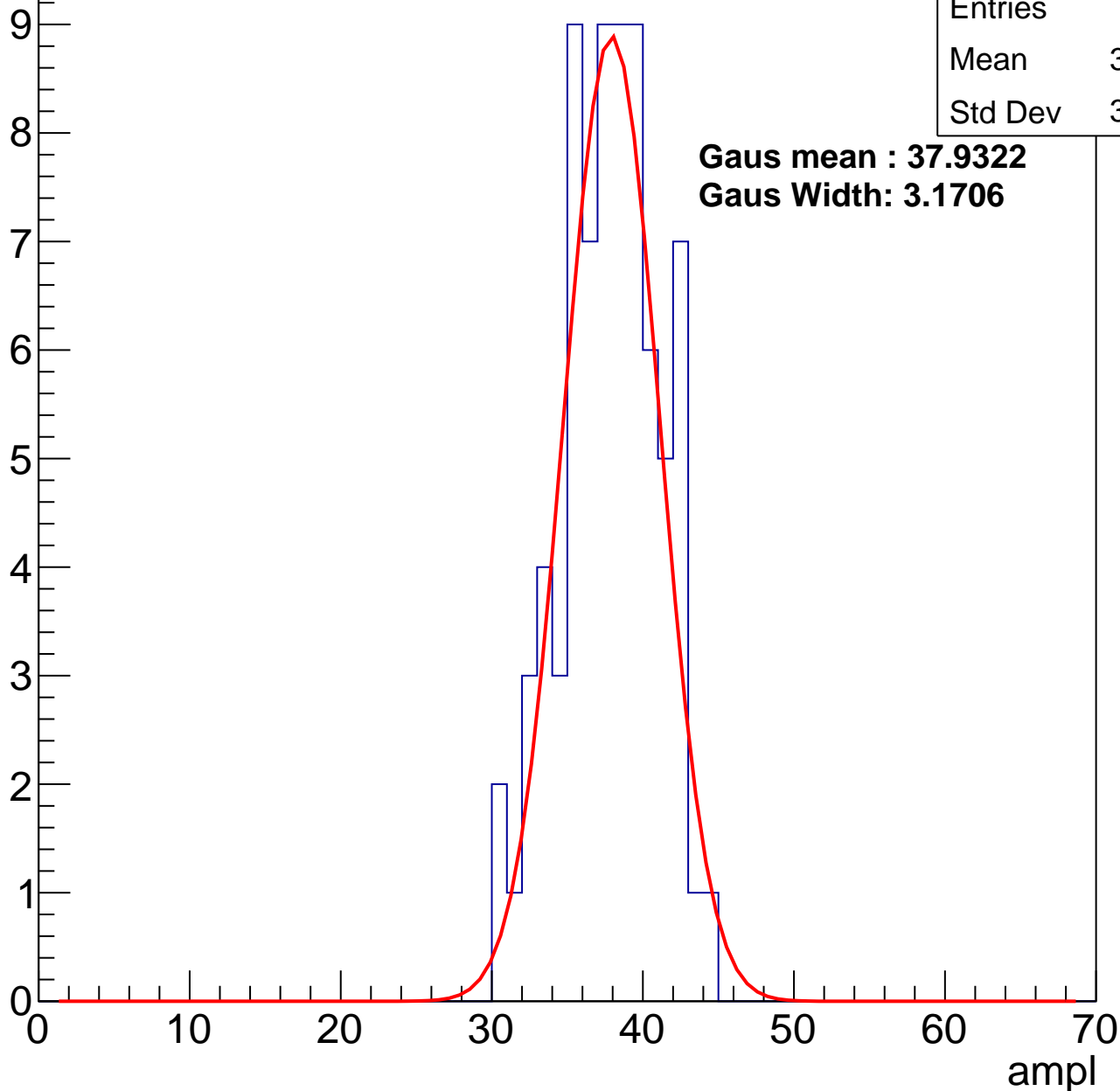
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.37
Std Dev	3.186

**Gaus mean : 37.9322**

**Gaus Width: 3.1706**



# B0L002S, U23-ch7, adc2

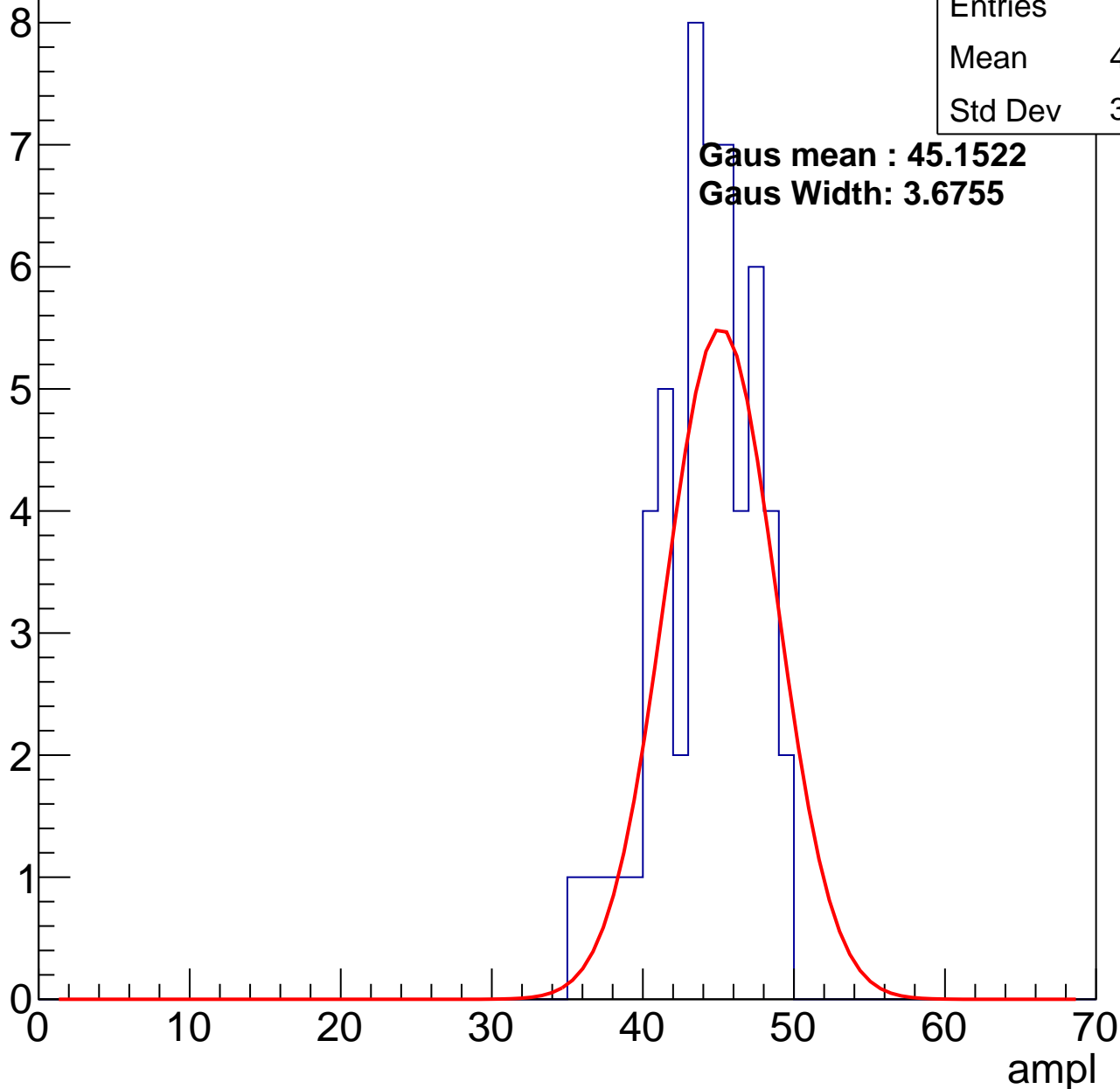
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.65
Std Dev	3.233

**Gaus mean : 45.1522**

**Gaus Width: 3.6755**

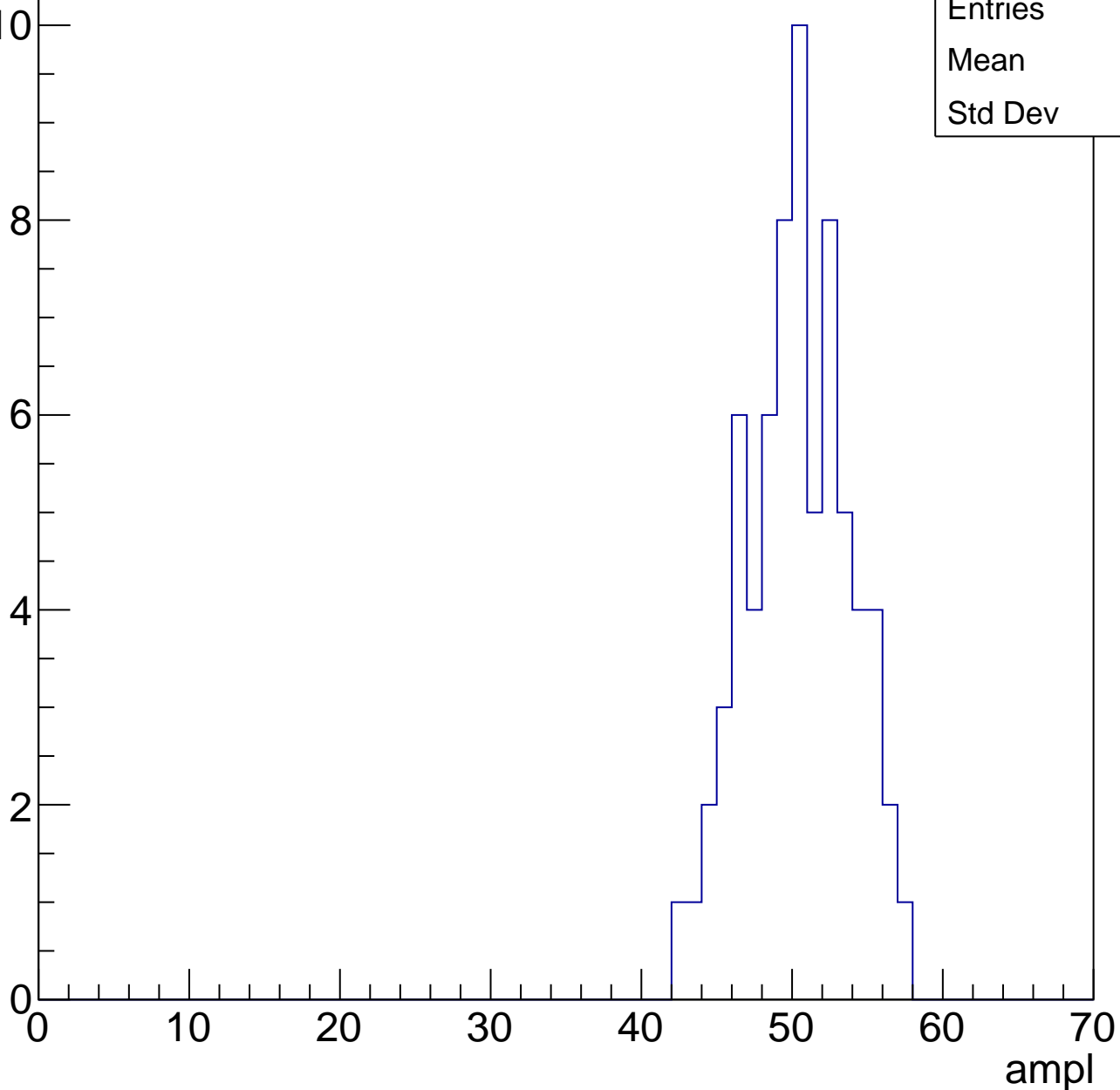


# B0L002S, U23-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

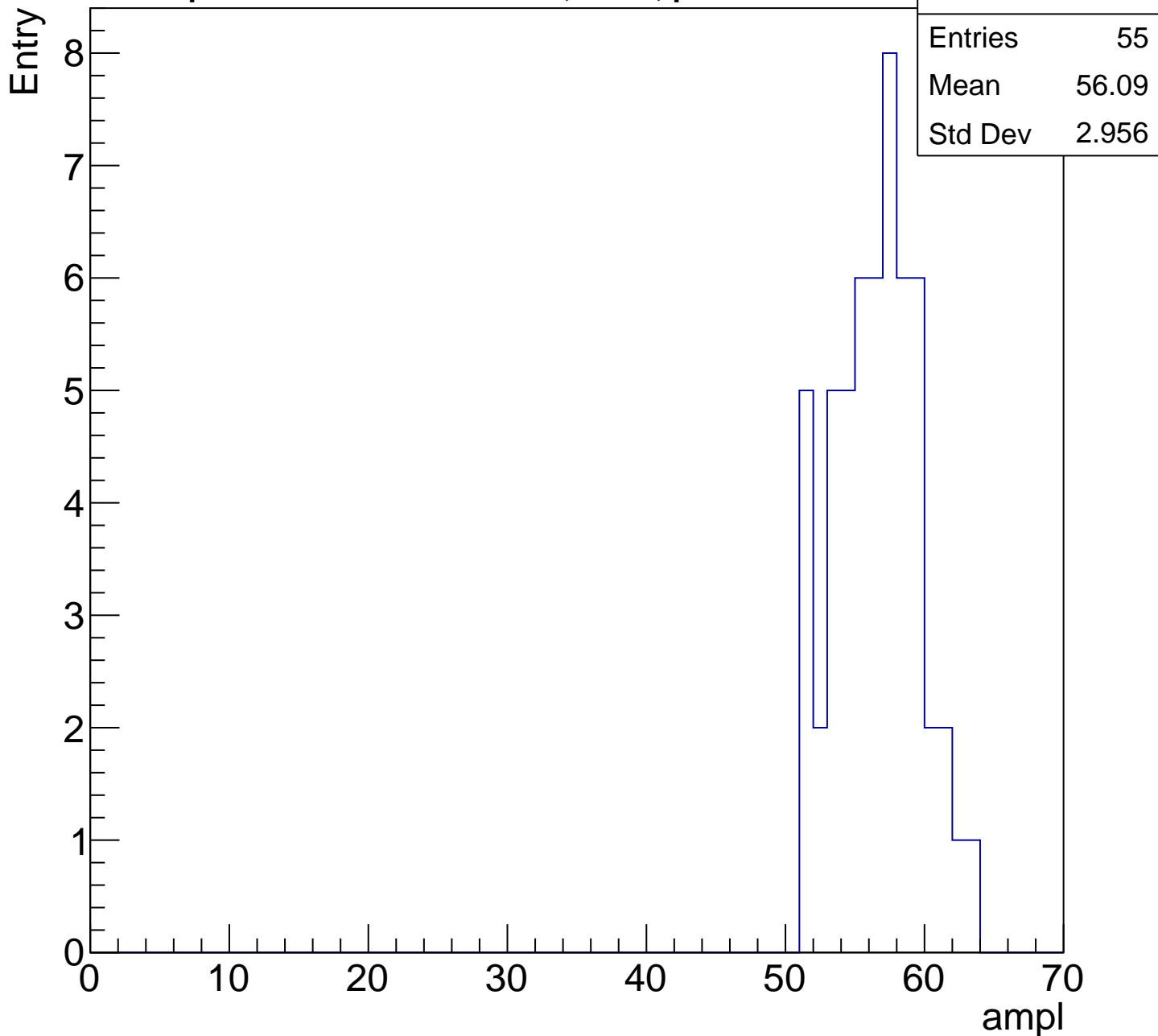
Entry

Entries	70
Mean	49.9
Std Dev	3.36



# B0L002S, U23-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

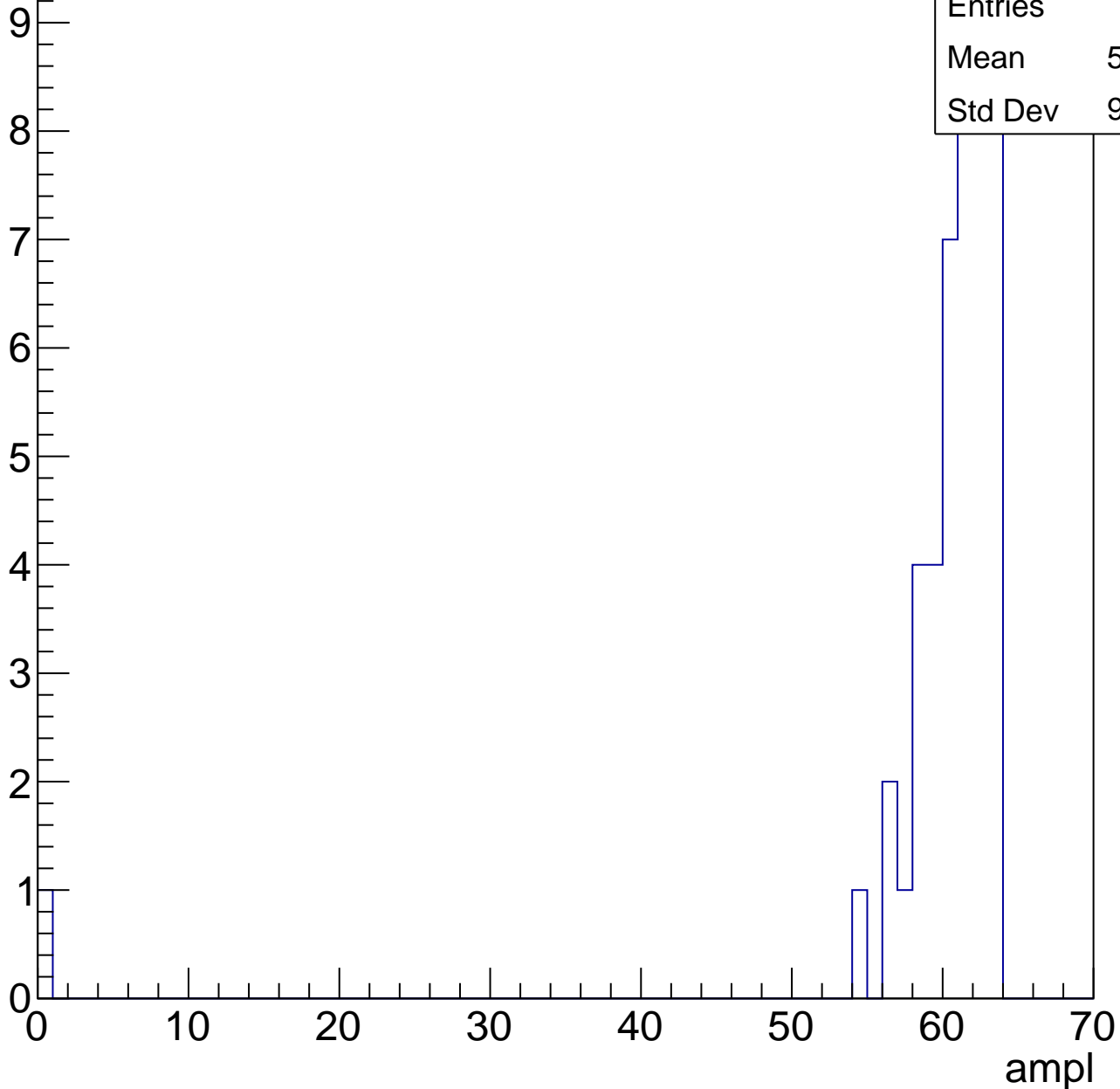


# B0L002S, U23-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	59.22
Std Dev	9.082



# B0L002S, U23-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

60

Std Dev

0



# B0L002S, U23-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch8, adc0

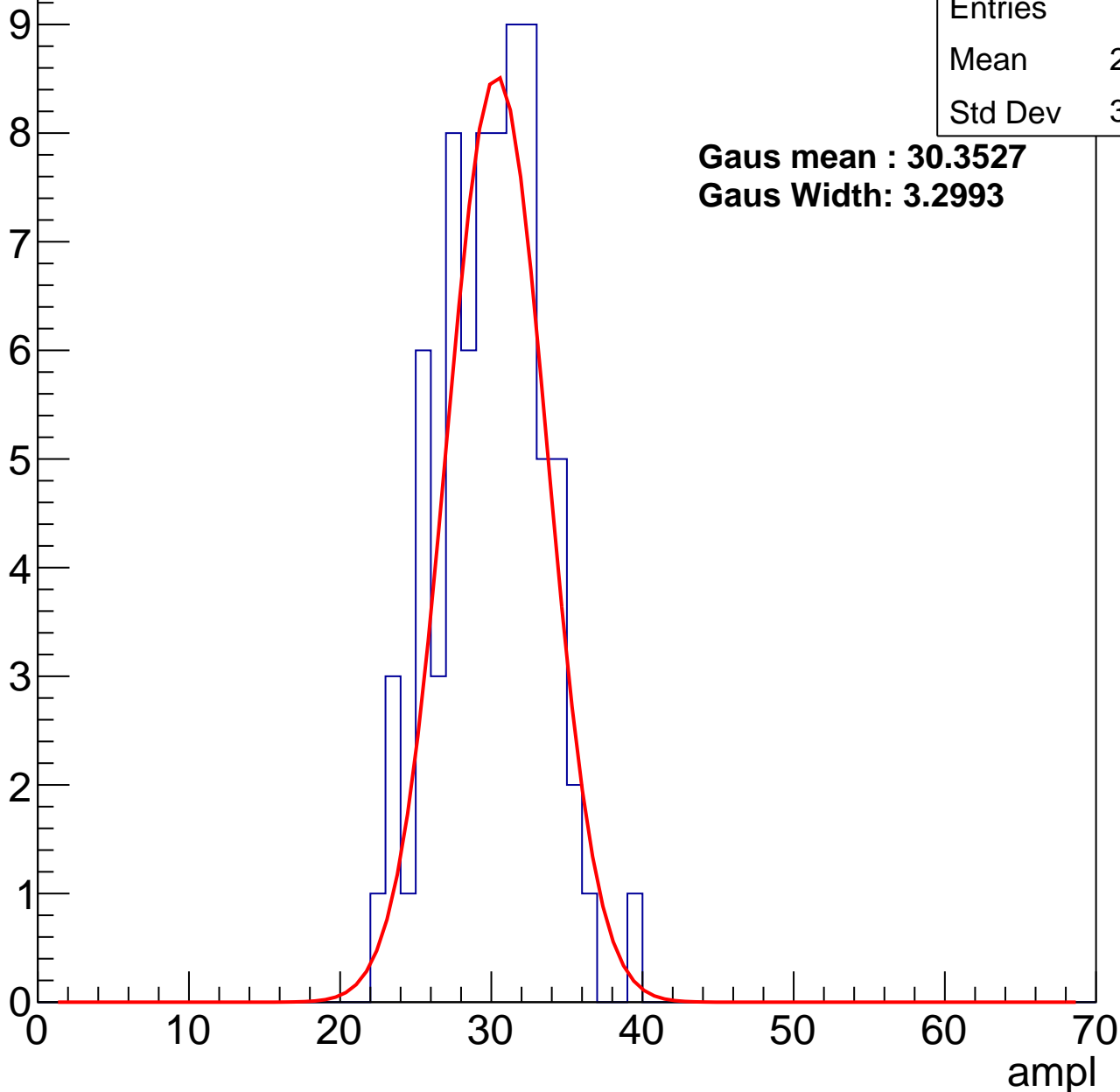
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	29.55
Std Dev	3.385

**Gaus mean : 30.3527**

**Gaus Width: 3.2993**



# B0L002S, U23-ch8, adc1

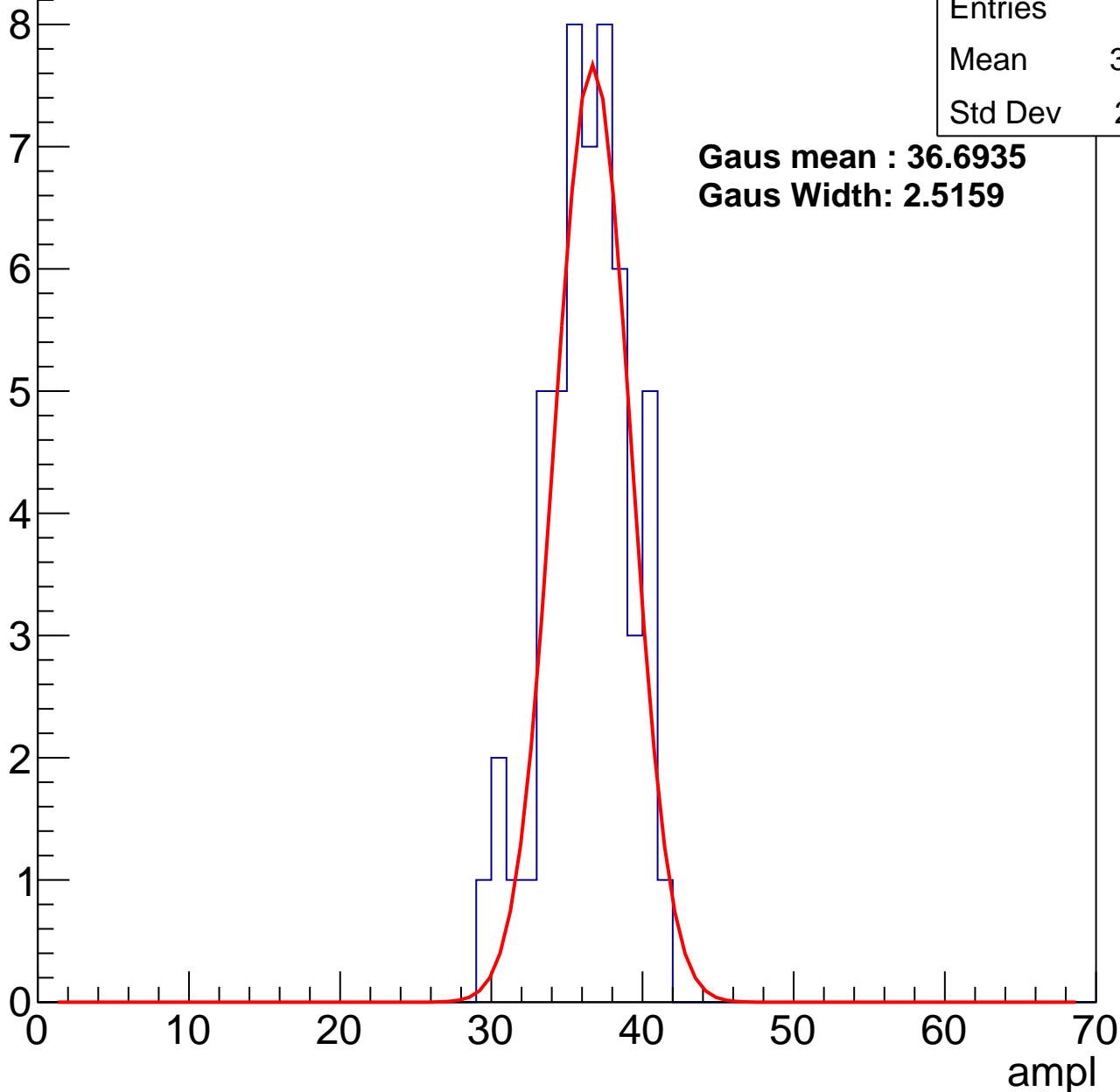
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	35.87
Std Dev	2.741

**Gaus mean : 36.6935**

**Gaus Width: 2.5159**



# B0L002S, U23-ch8, adc2

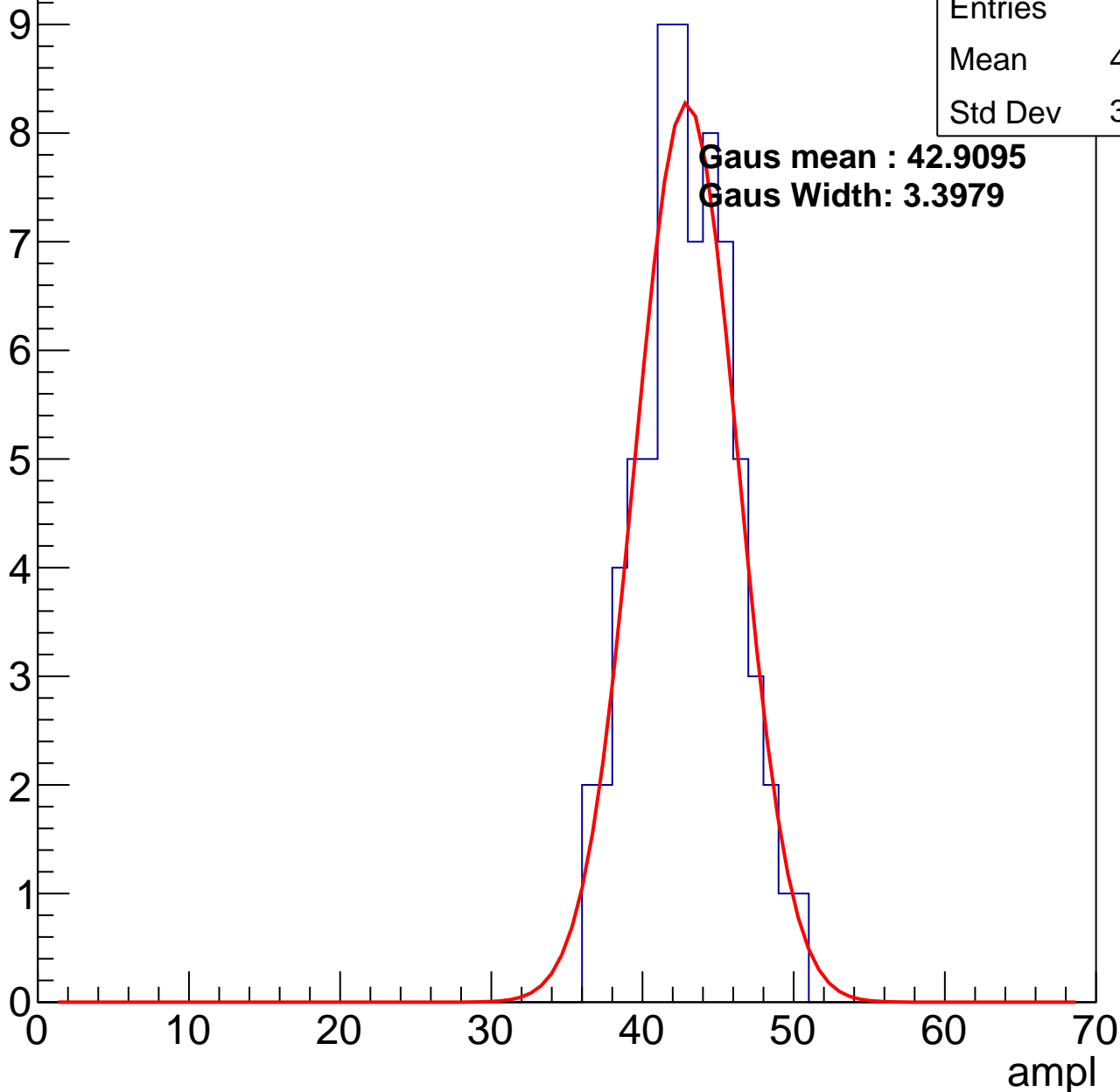
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.49
Std Dev	3.129

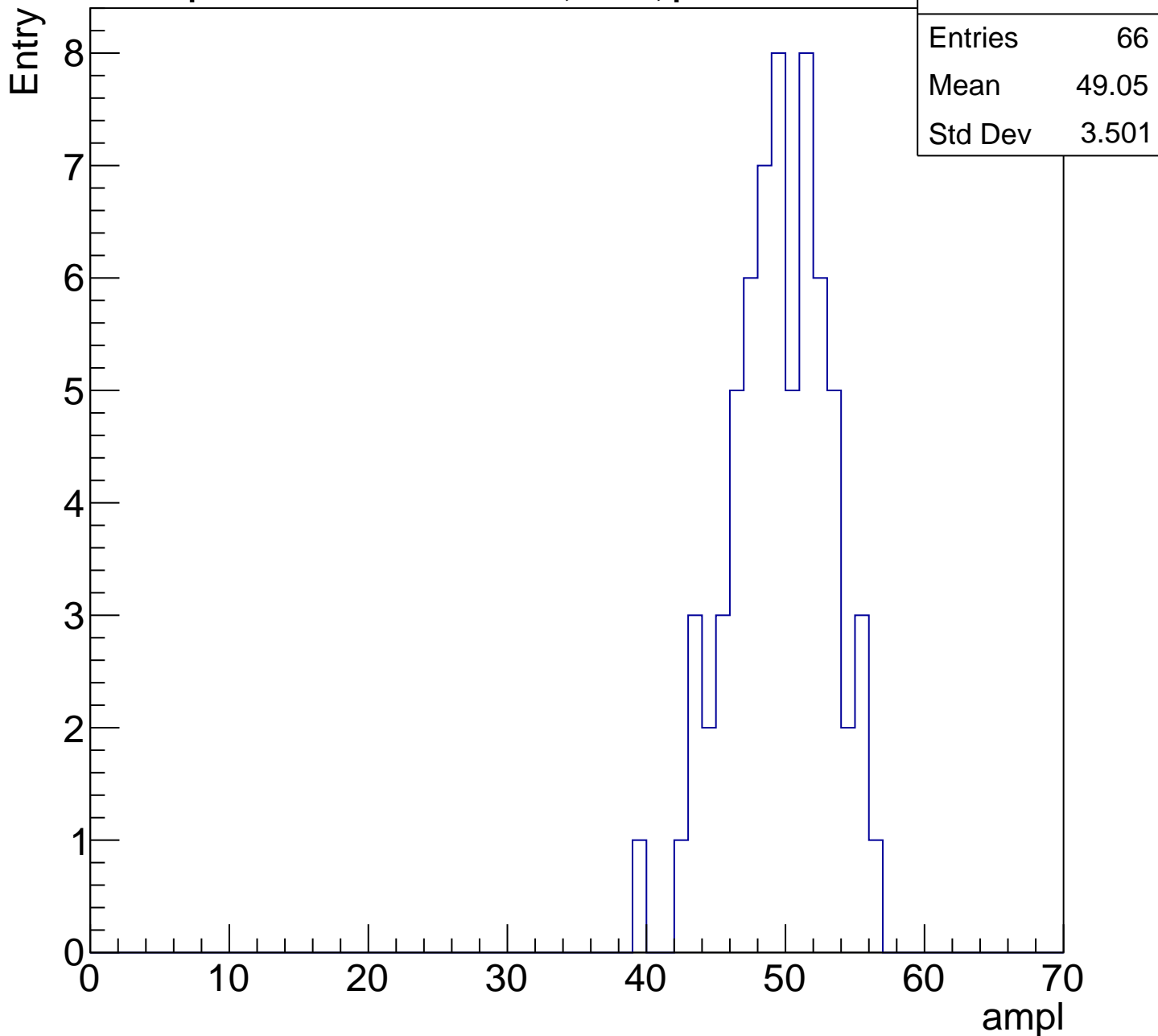
**Gaus mean : 42.9095**

**Gaus Width: 3.3979**



# B0L002S, U23-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

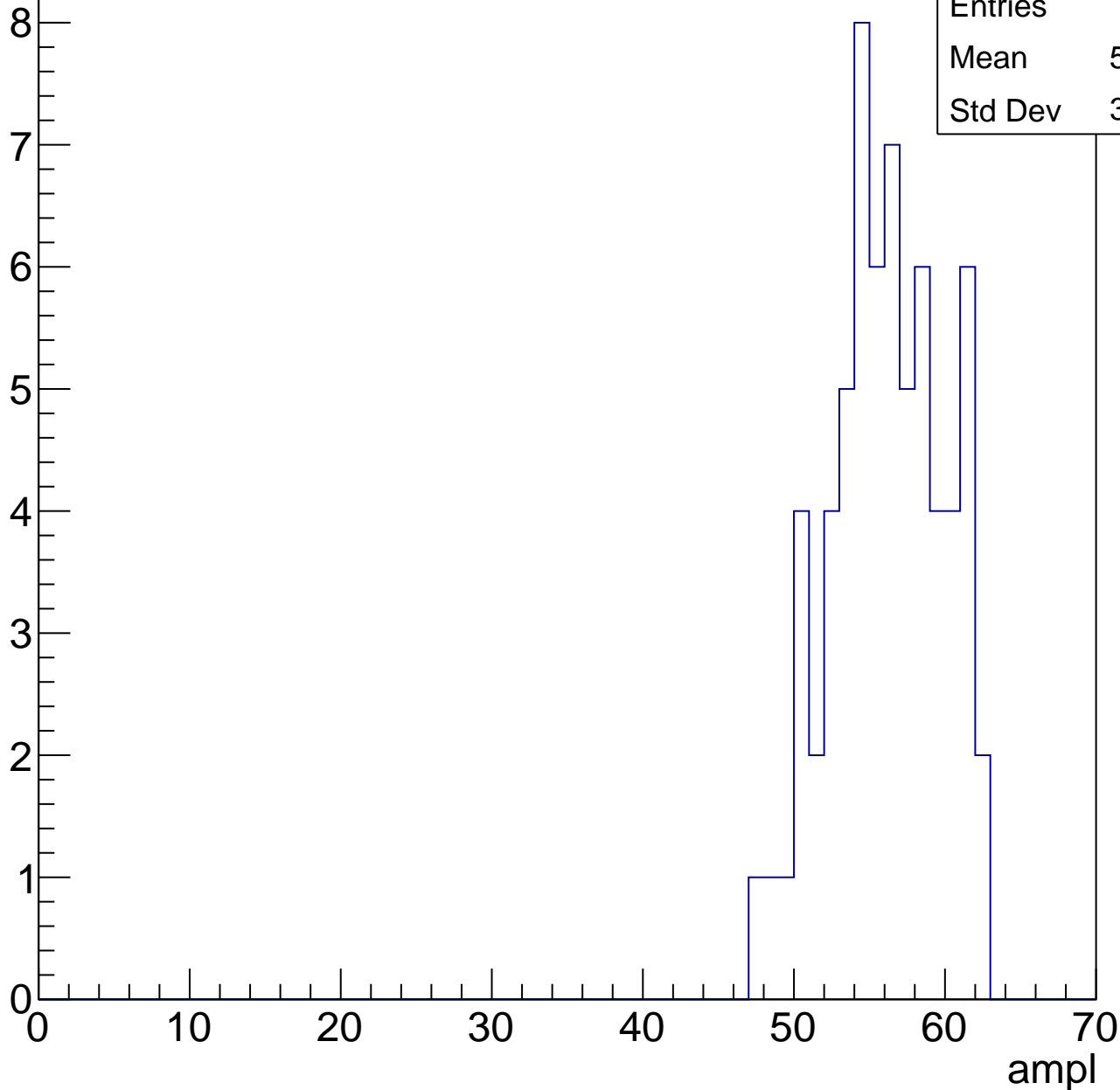


# B0L002S, U23-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	55.64
Std Dev	3.646

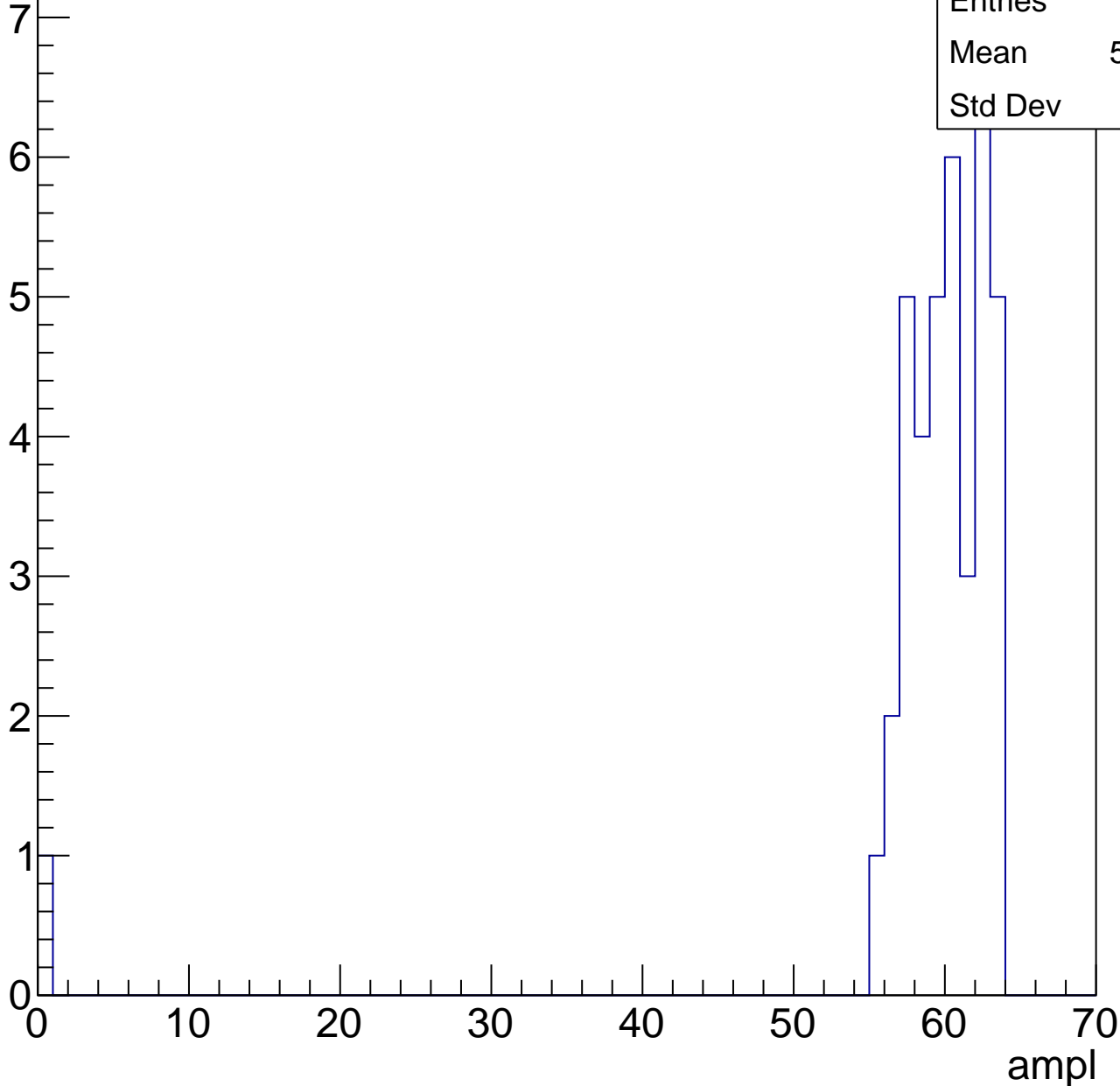


# B0L002S, U23-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	58.23
Std Dev	9.71

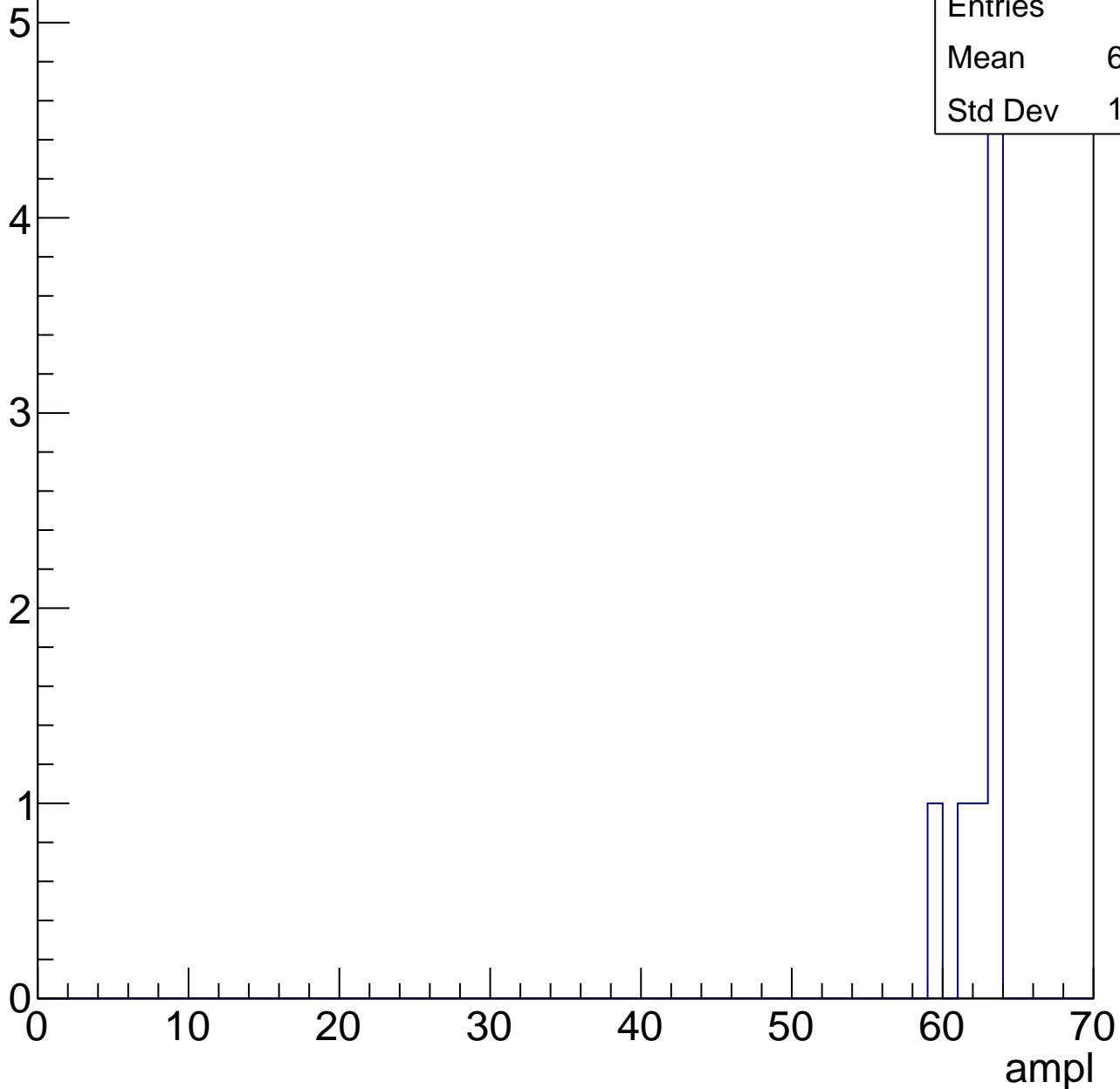


# B0L002S, U23-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	8
Mean	62.12
Std Dev	1.364





# B0L002S, U23-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch9, adc0

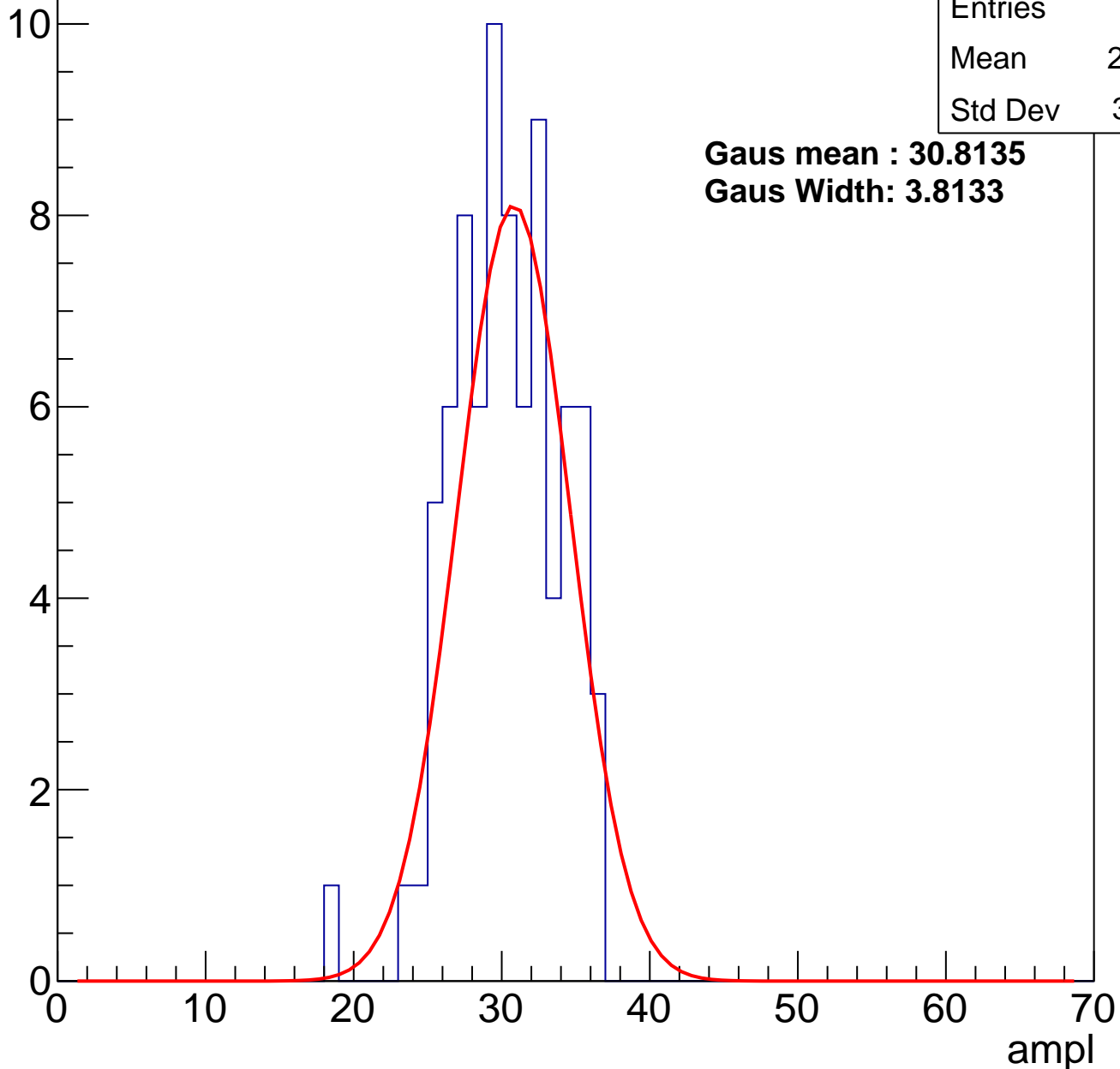
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	80
Mean	29.85
Std Dev	3.511

**Gaus mean : 30.8135**

**Gaus Width: 3.8133**

Entry

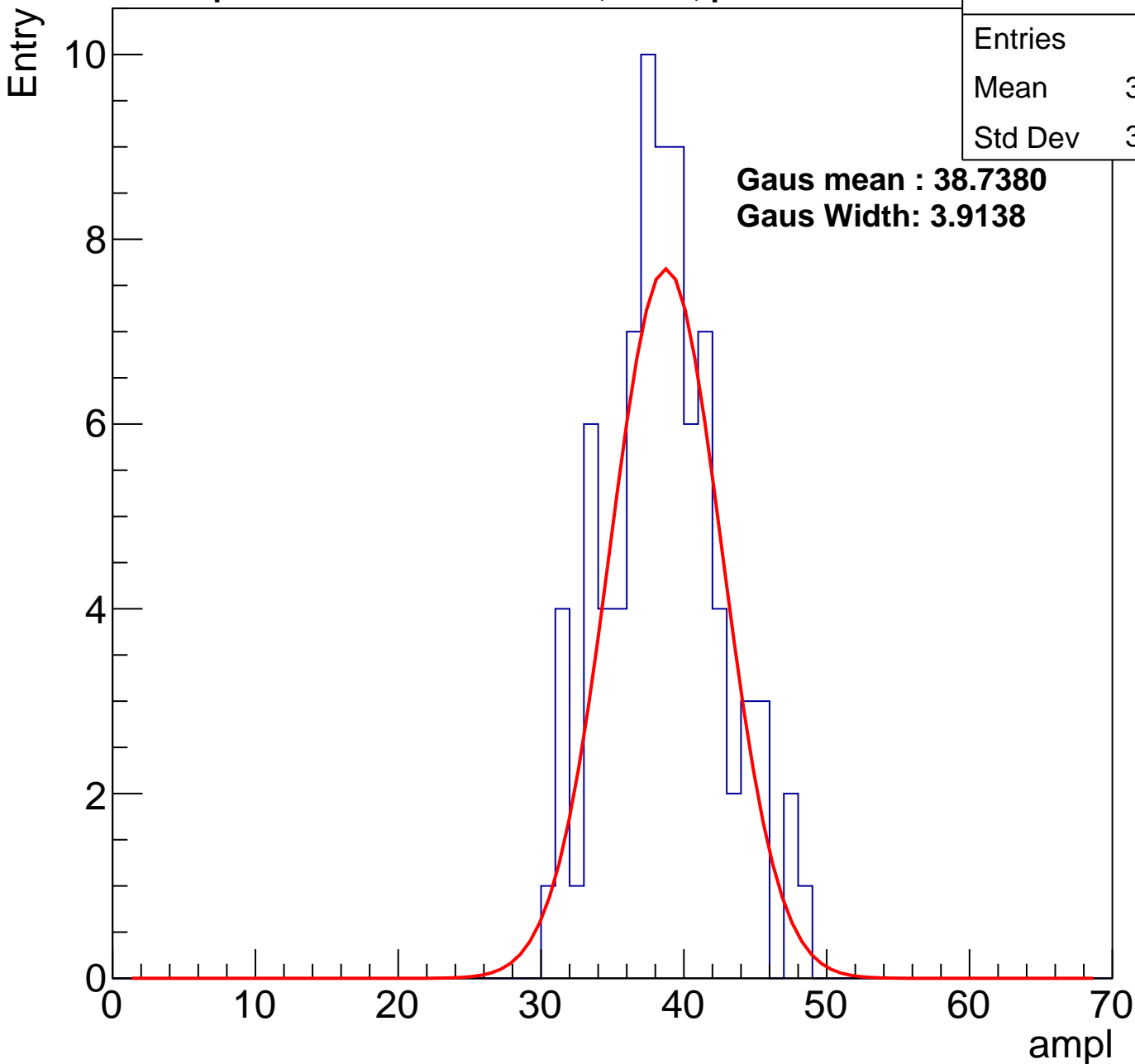


# B0L002S, U23-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	83
Mean	38.13
Std Dev	3.972

**Gaus mean : 38.7380**  
**Gaus Width: 3.9138**



# B0L002S, U23-ch9, adc2

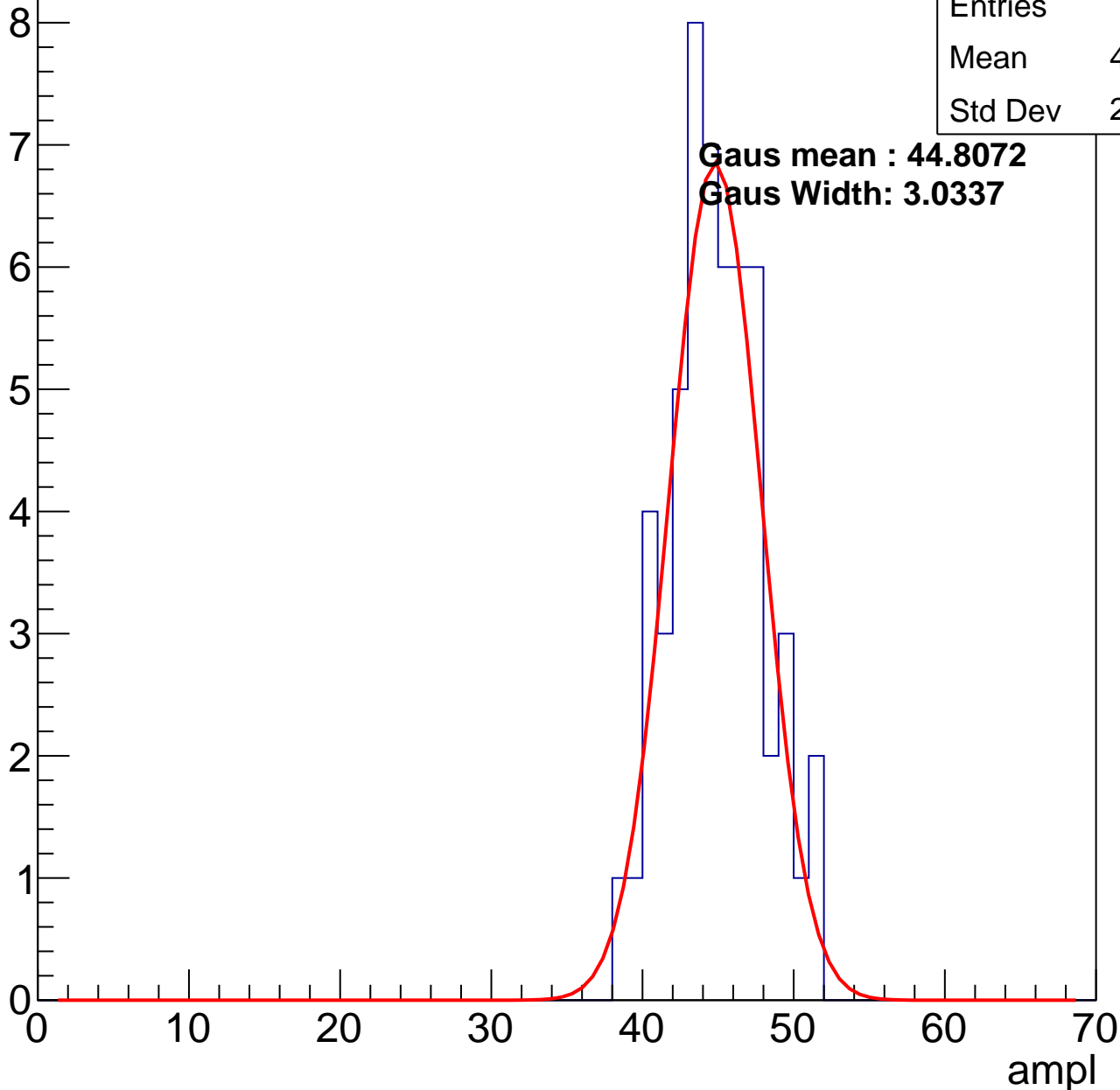
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.45
Std Dev	2.996

**Gaus mean : 44.8072**

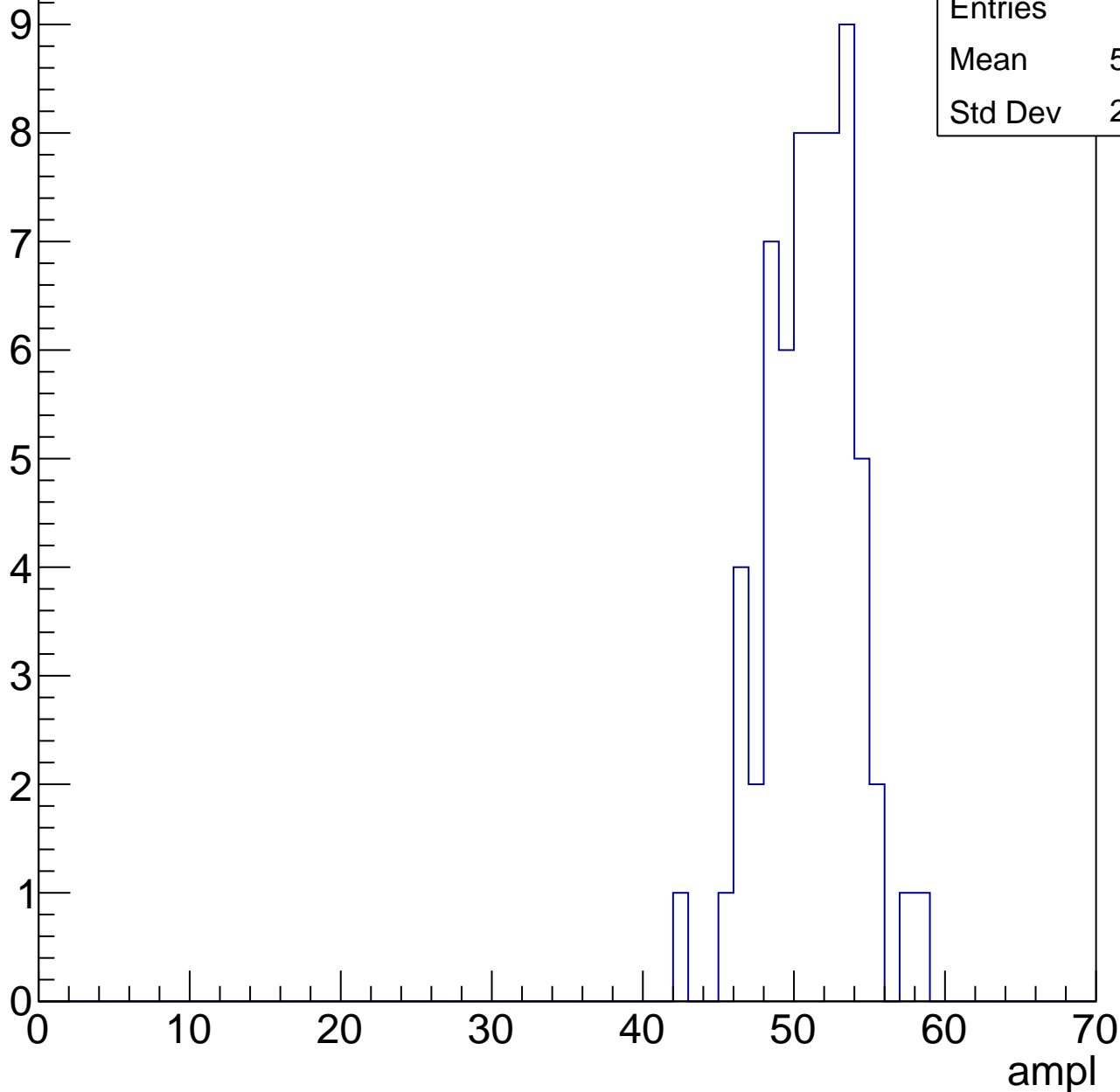
**Gaus Width: 3.0337**



# B0L002S, U23-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

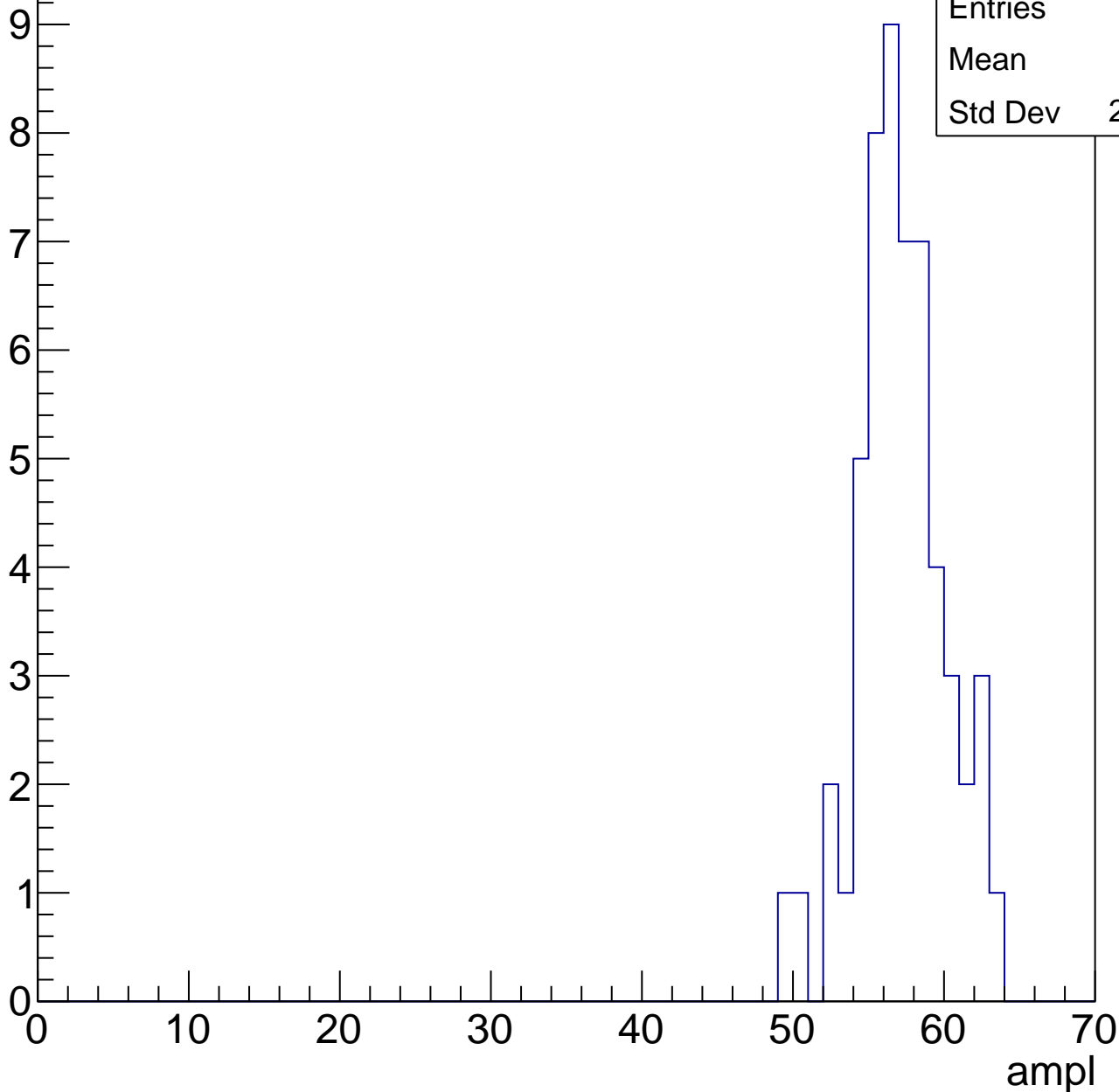


# B0L002S, U23-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	56.7
Std Dev	2.897



# B0L002S, U23-ch9, adc5

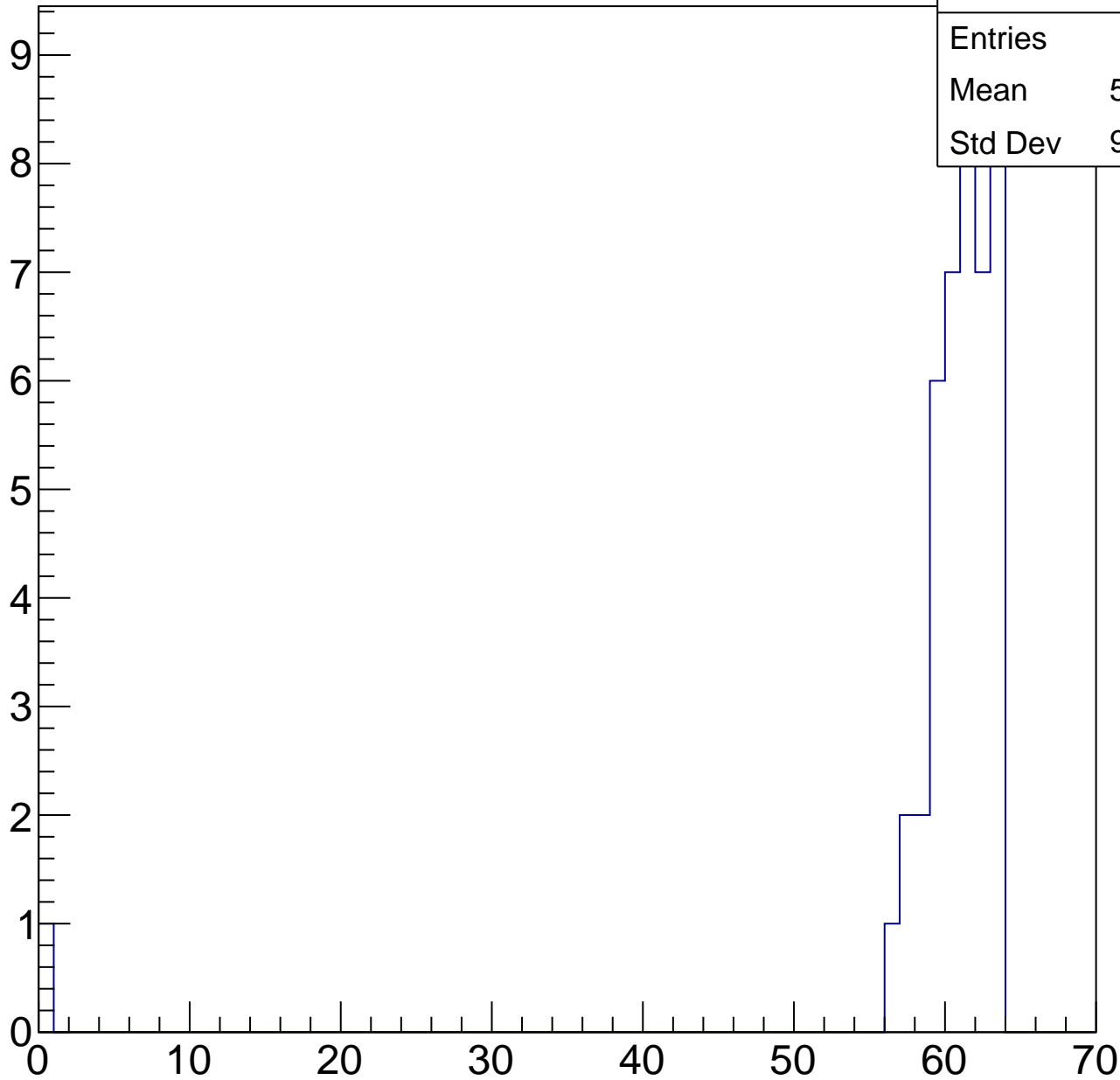
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	59.28
Std Dev	9.329

ampl



# B0L002S, U23-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch10, adc0

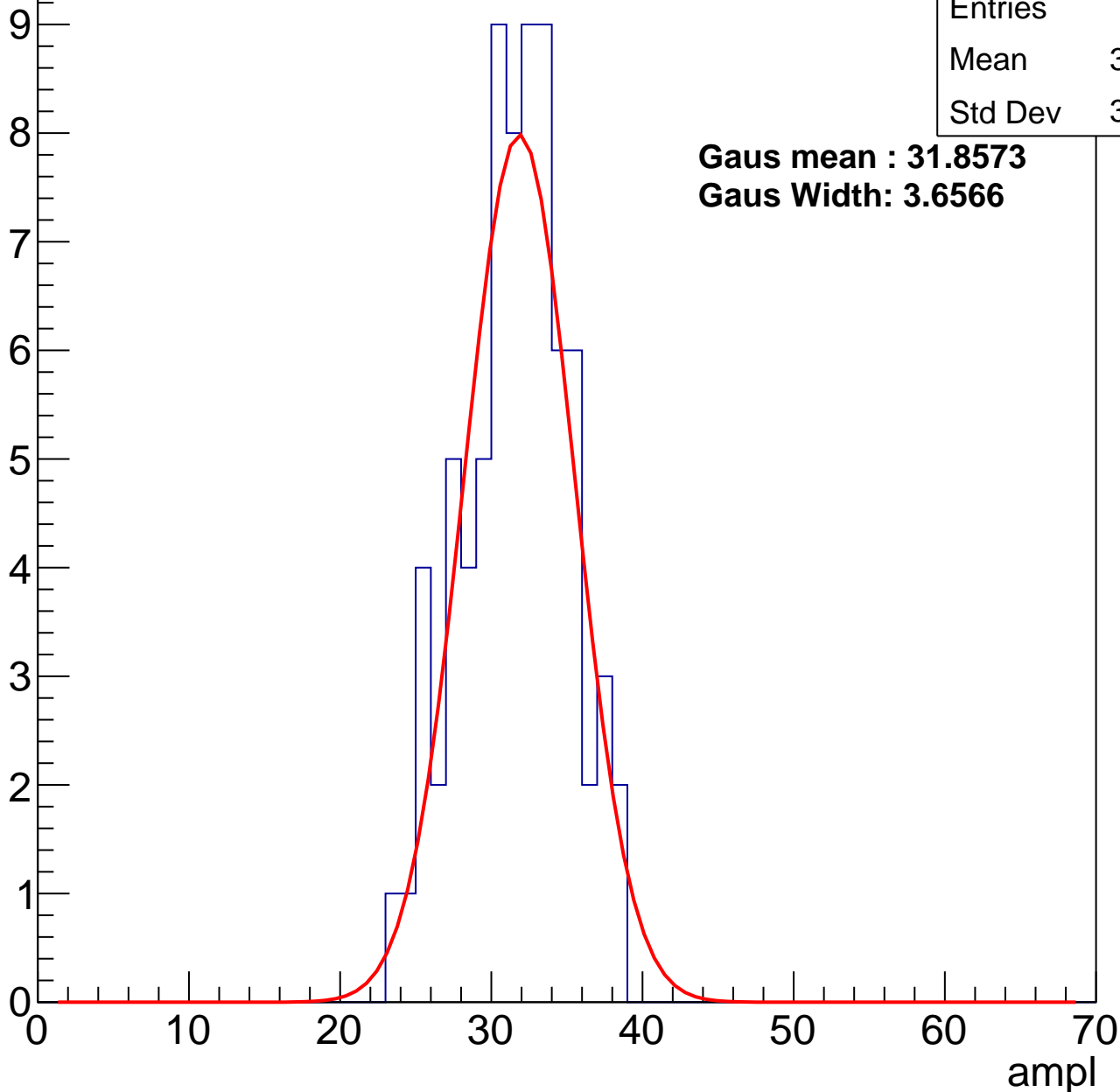
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.14
Std Dev	3.444

**Gaus mean : 31.8573**

**Gaus Width: 3.6566**



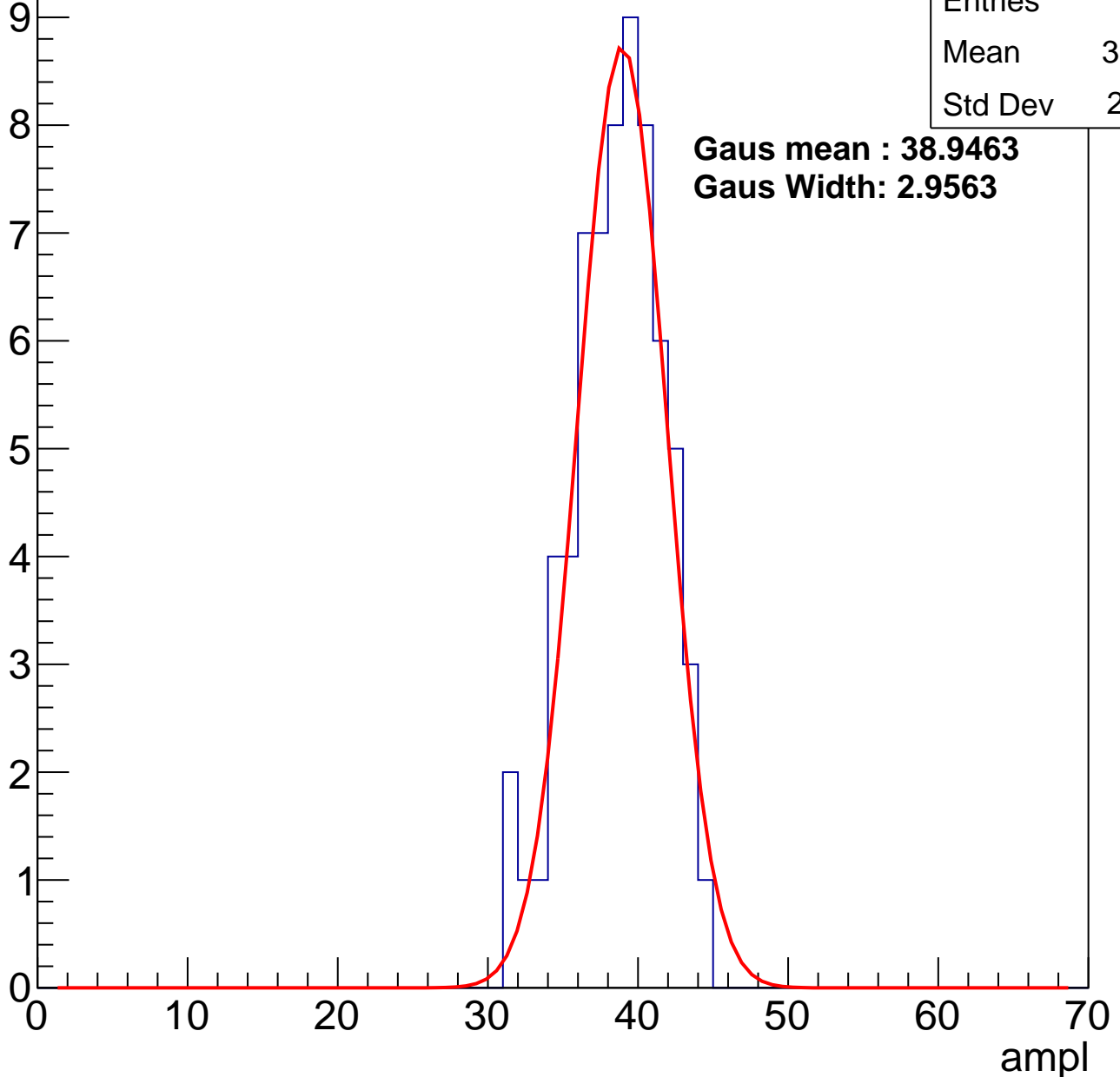
# B0L002S, U23-ch10, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.15
Std Dev	2.961

**Gaus mean : 38.9463**  
**Gaus Width: 2.9563**



# B0L002S, U23-ch10, adc2

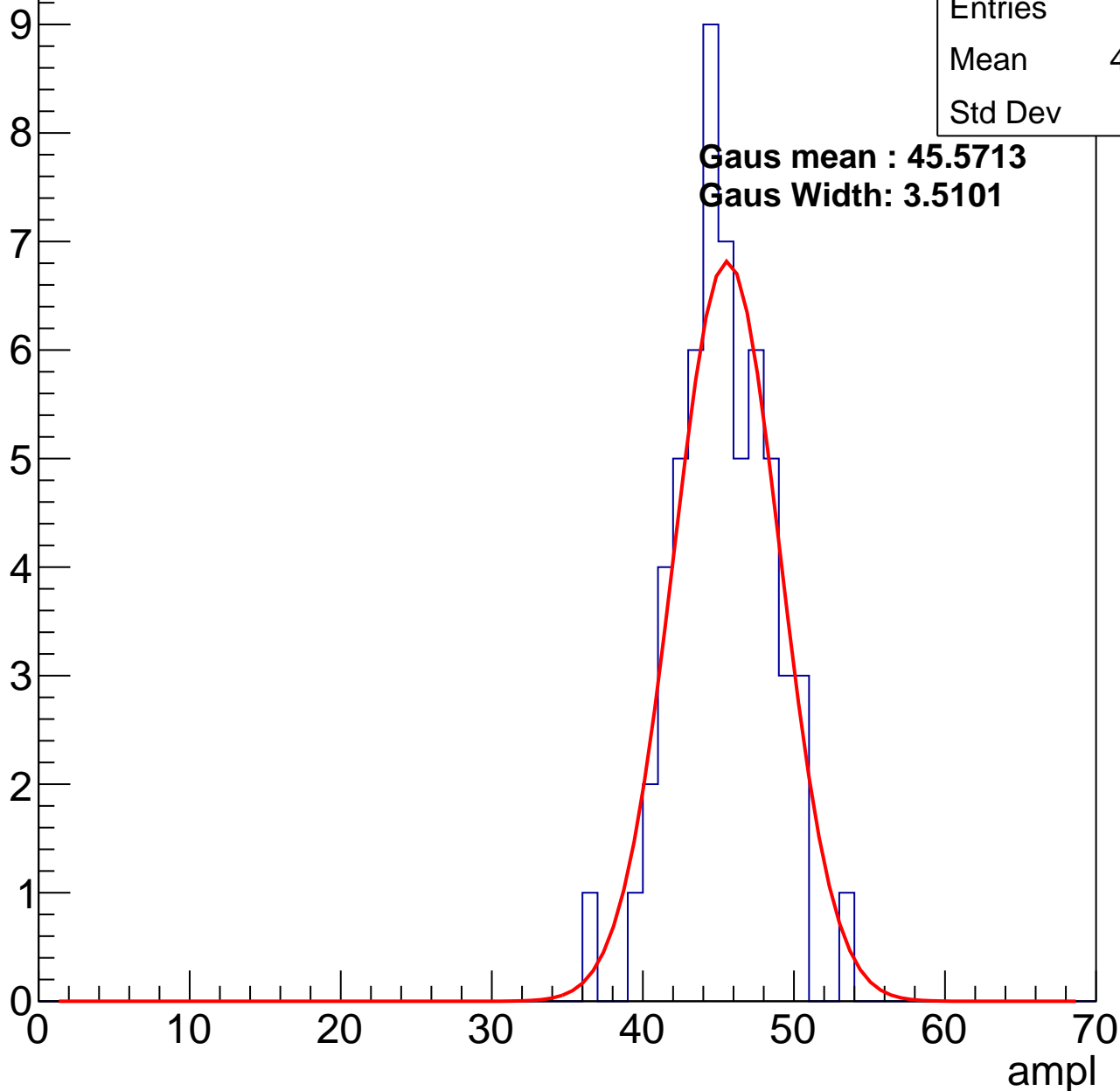
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.83
Std Dev	3.13

**Gaus mean : 45.5713**

**Gaus Width: 3.5101**

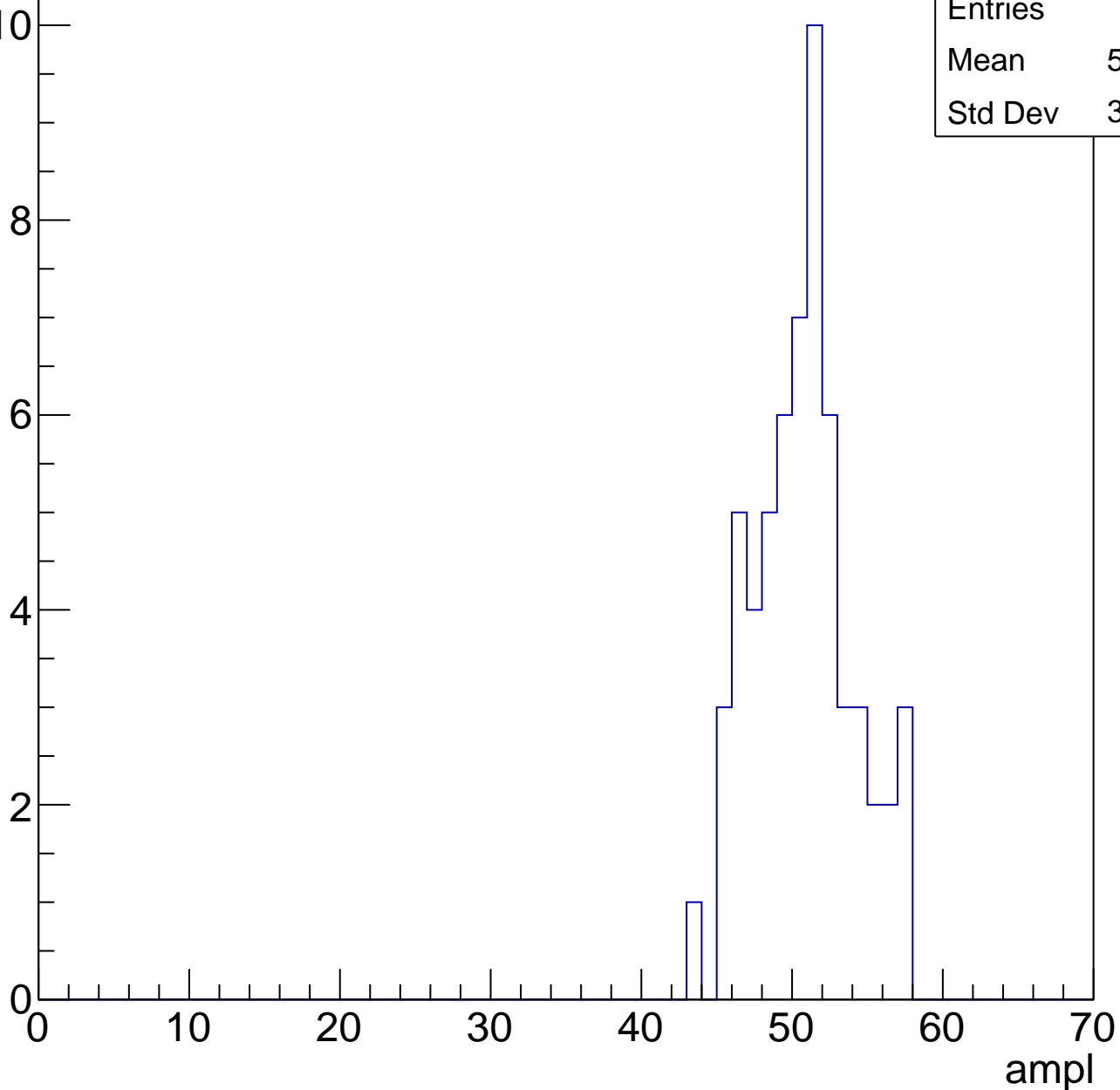


# B0L002S, U23-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.27
Std Dev	3.265

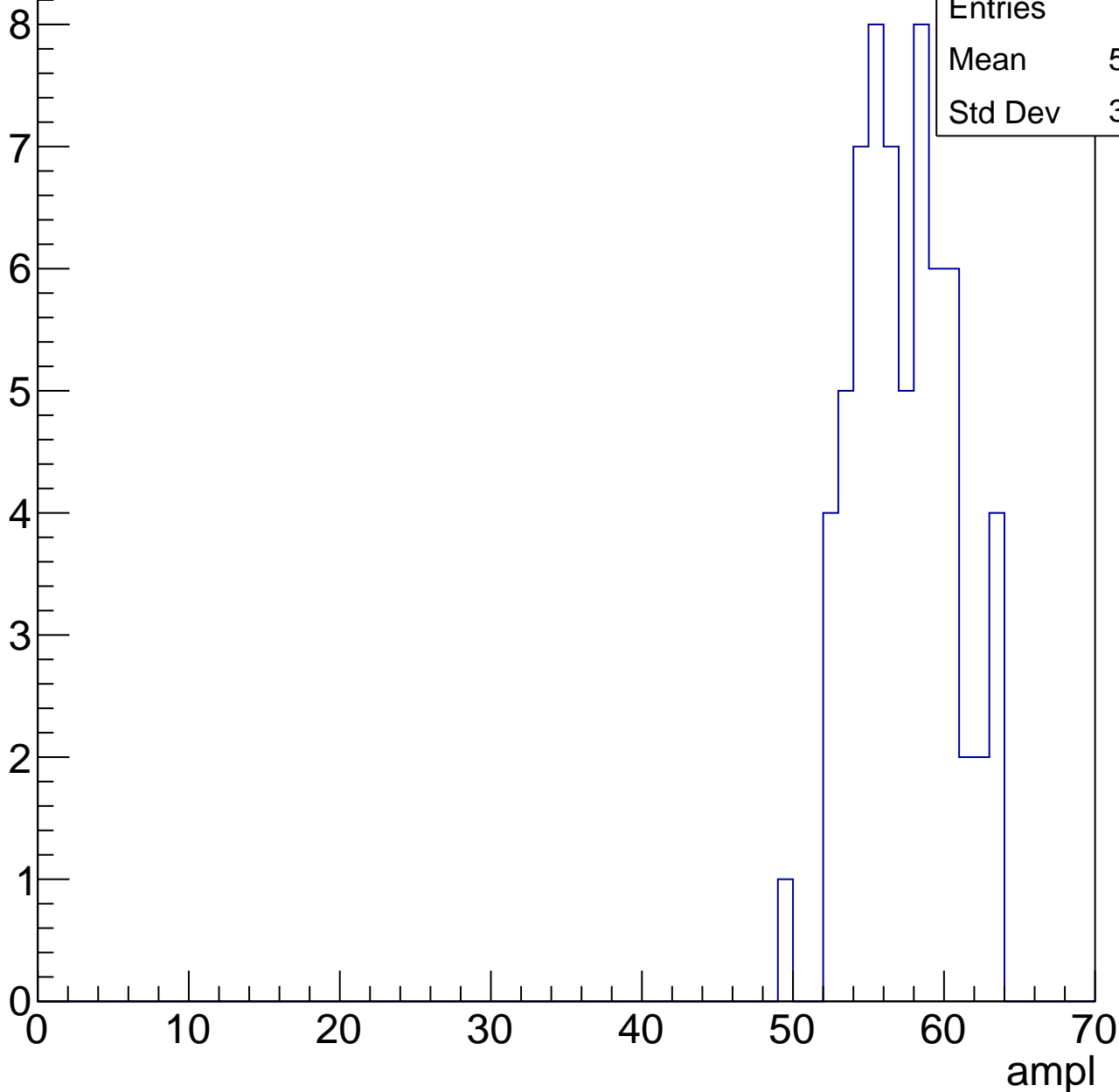


# B0L002S, U23-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	56.82
Std Dev	3.176

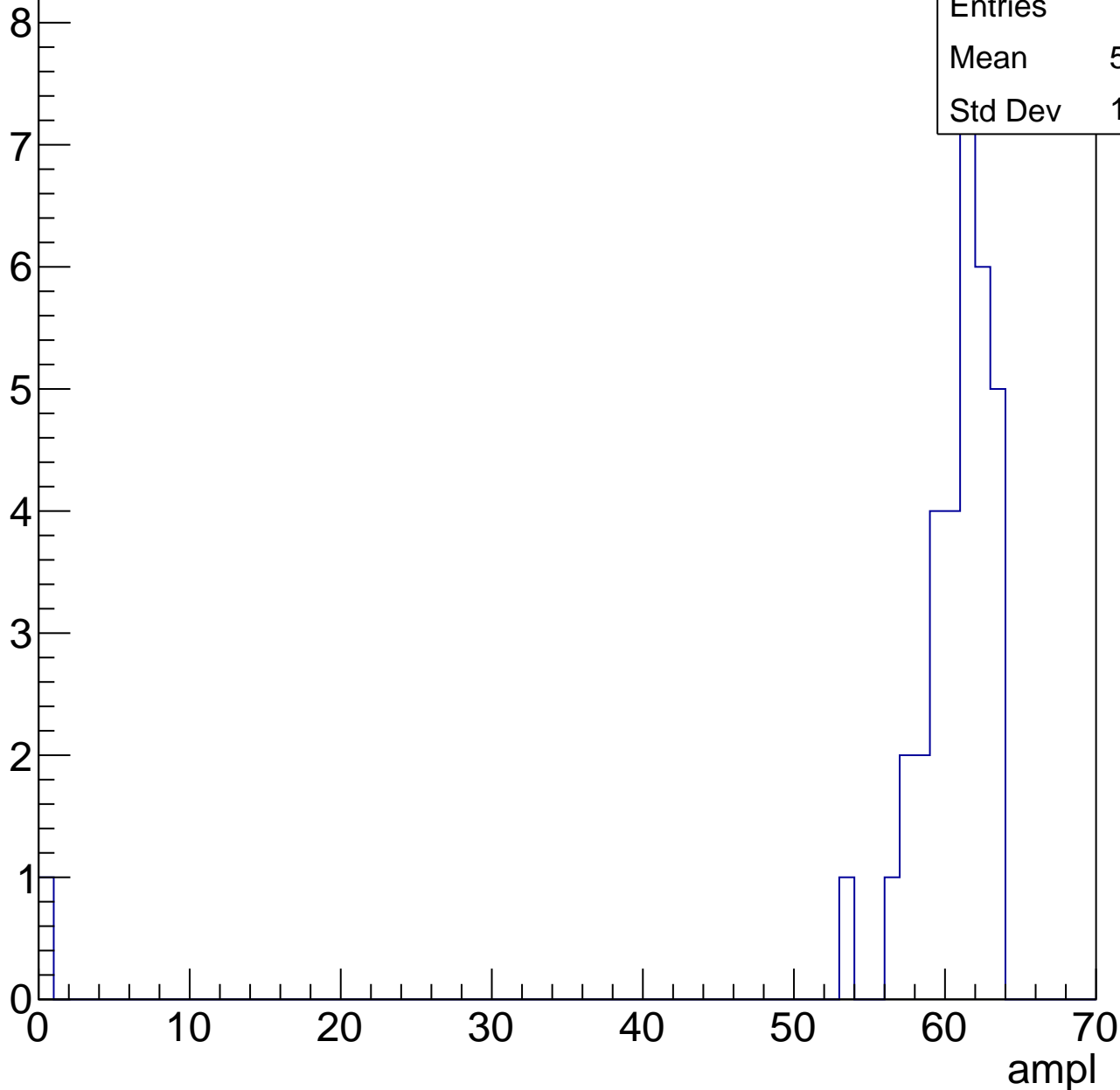


# B0L002S, U23-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.53
Std Dev	10.43



# B0L002S, U23-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



# B0L002S, U23-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch11, adc0

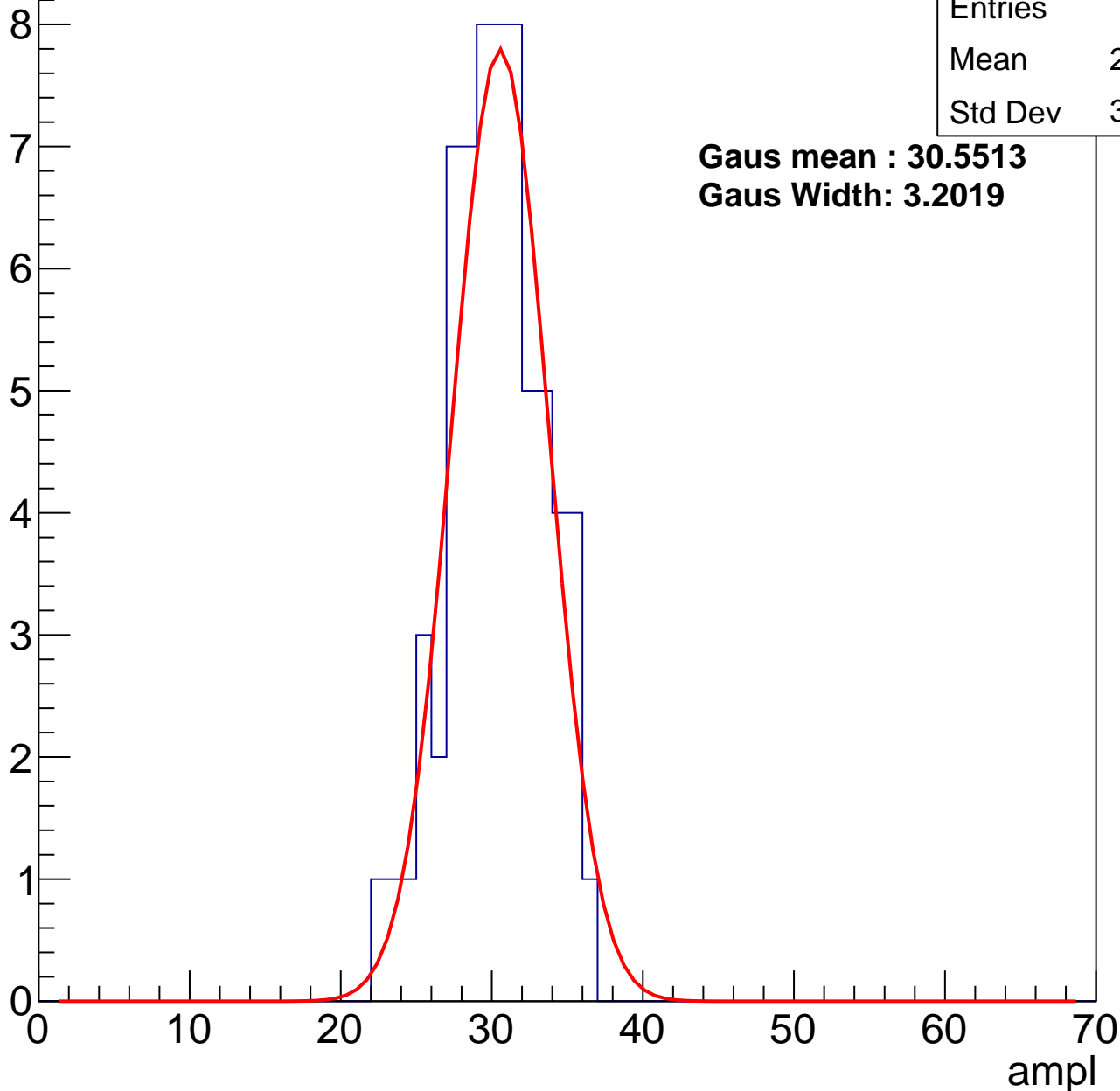
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.82
Std Dev	3.103

**Gaus mean : 30.5513**

**Gaus Width: 3.2019**



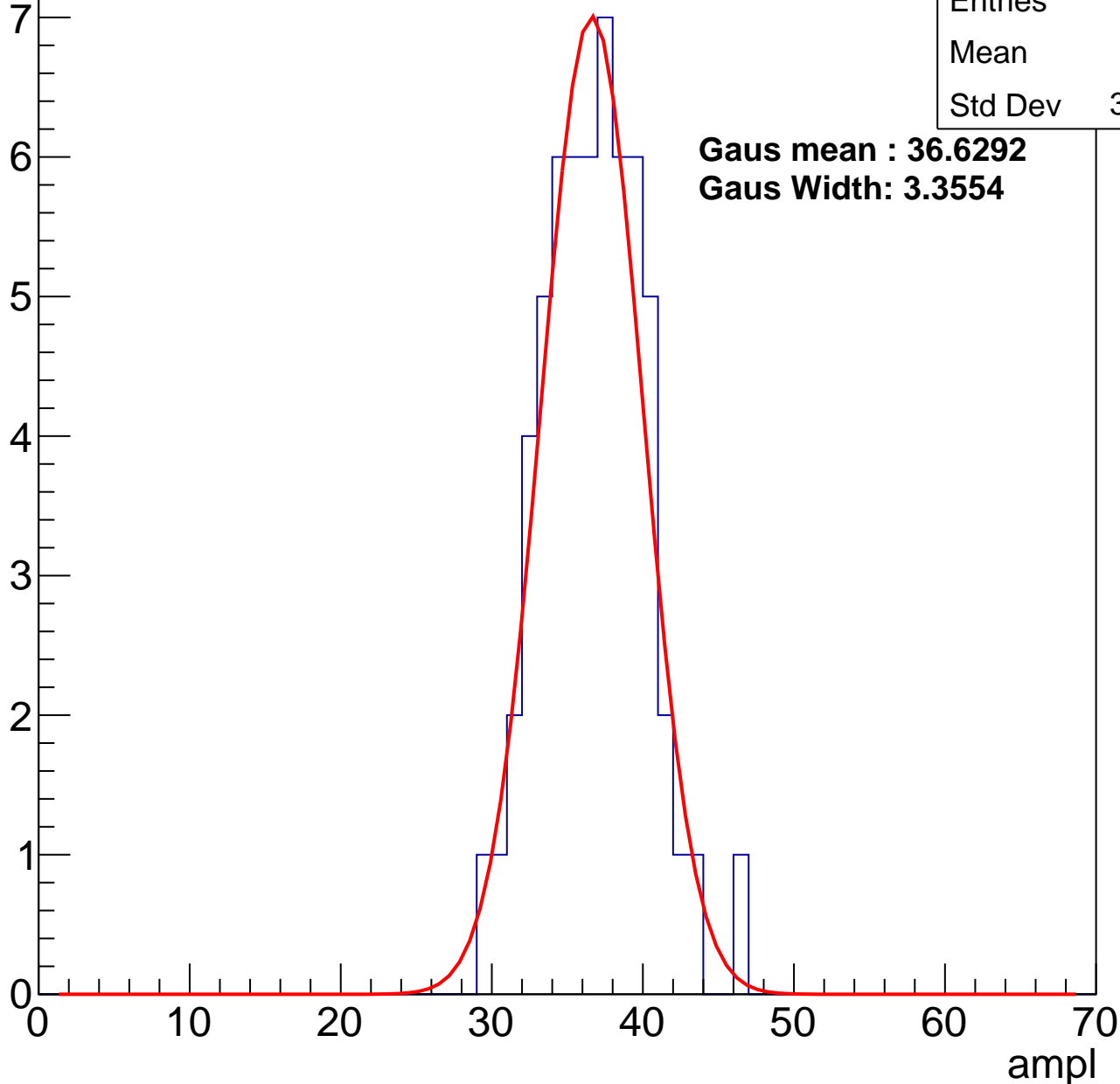
# B0L002S, U23-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.3
Std Dev	3.338

**Gaus mean : 36.6292**  
**Gaus Width: 3.3554**



# B0L002S, U23-ch11, adc2

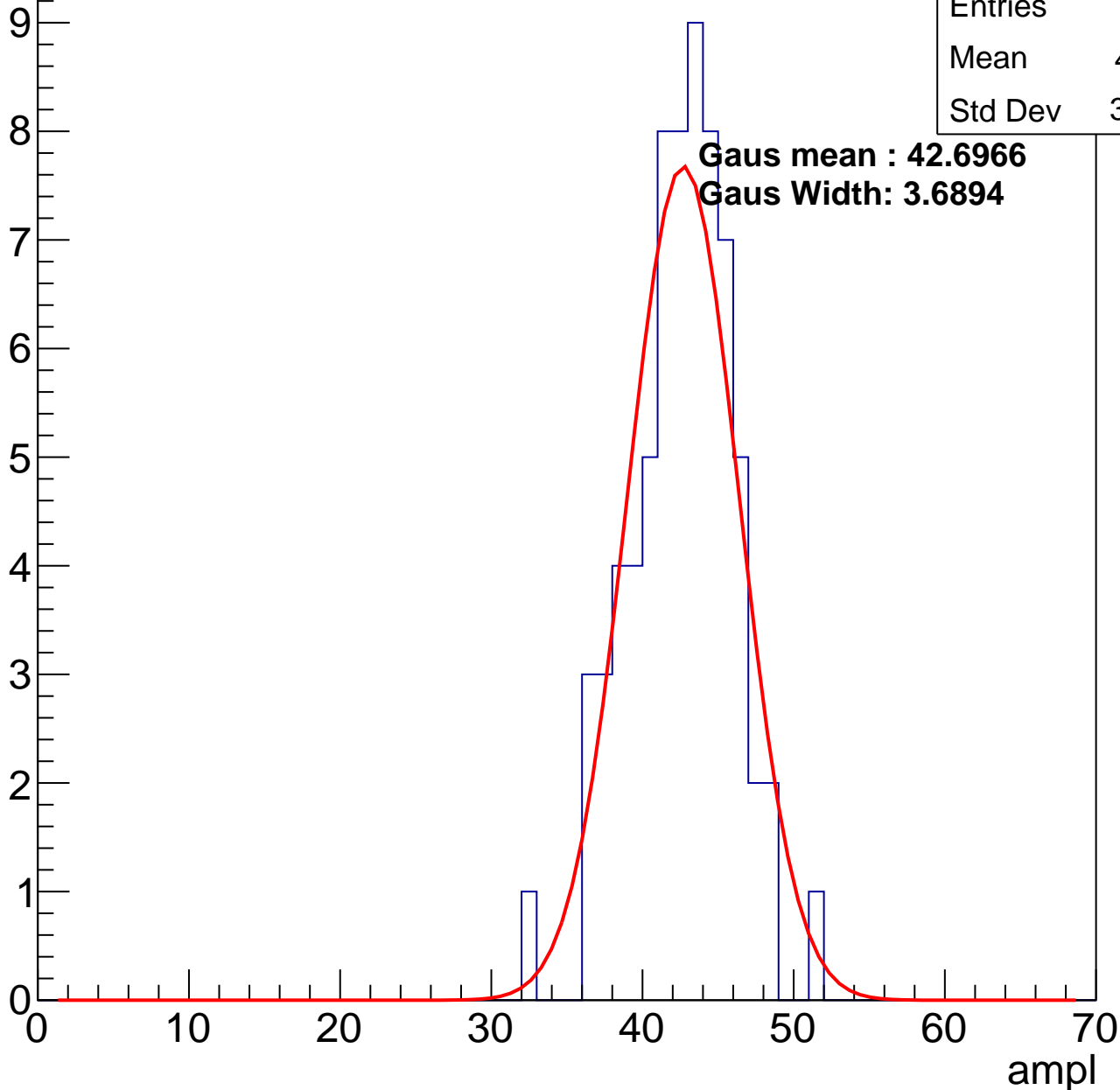
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.11
Std Dev	3.374

**Gaus mean : 42.6966**

**Gaus Width: 3.6894**

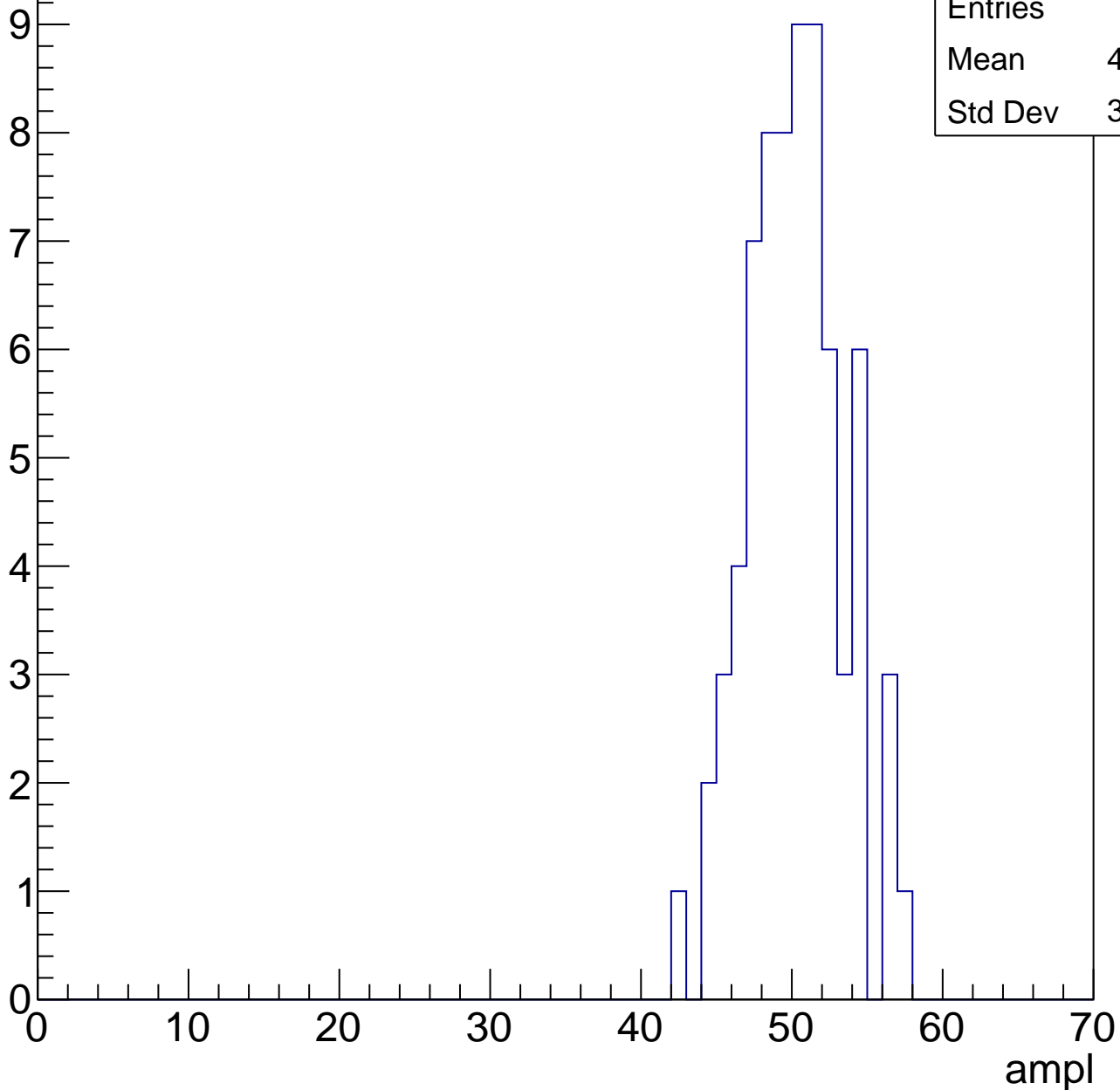


# B0L002S, U23-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

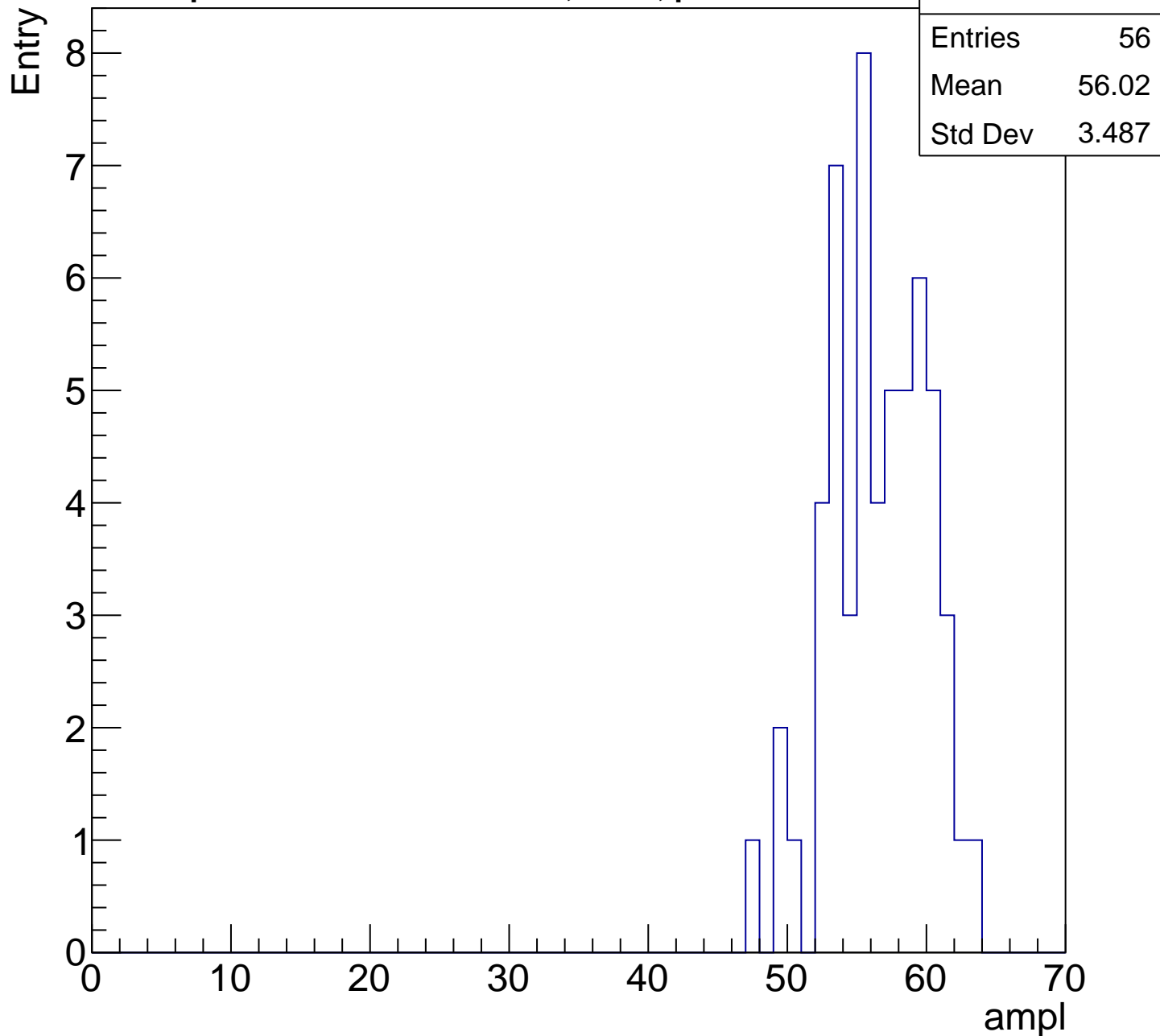
Entry

Entries	70
Mean	49.76
Std Dev	3.132



# B0L002S, U23-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

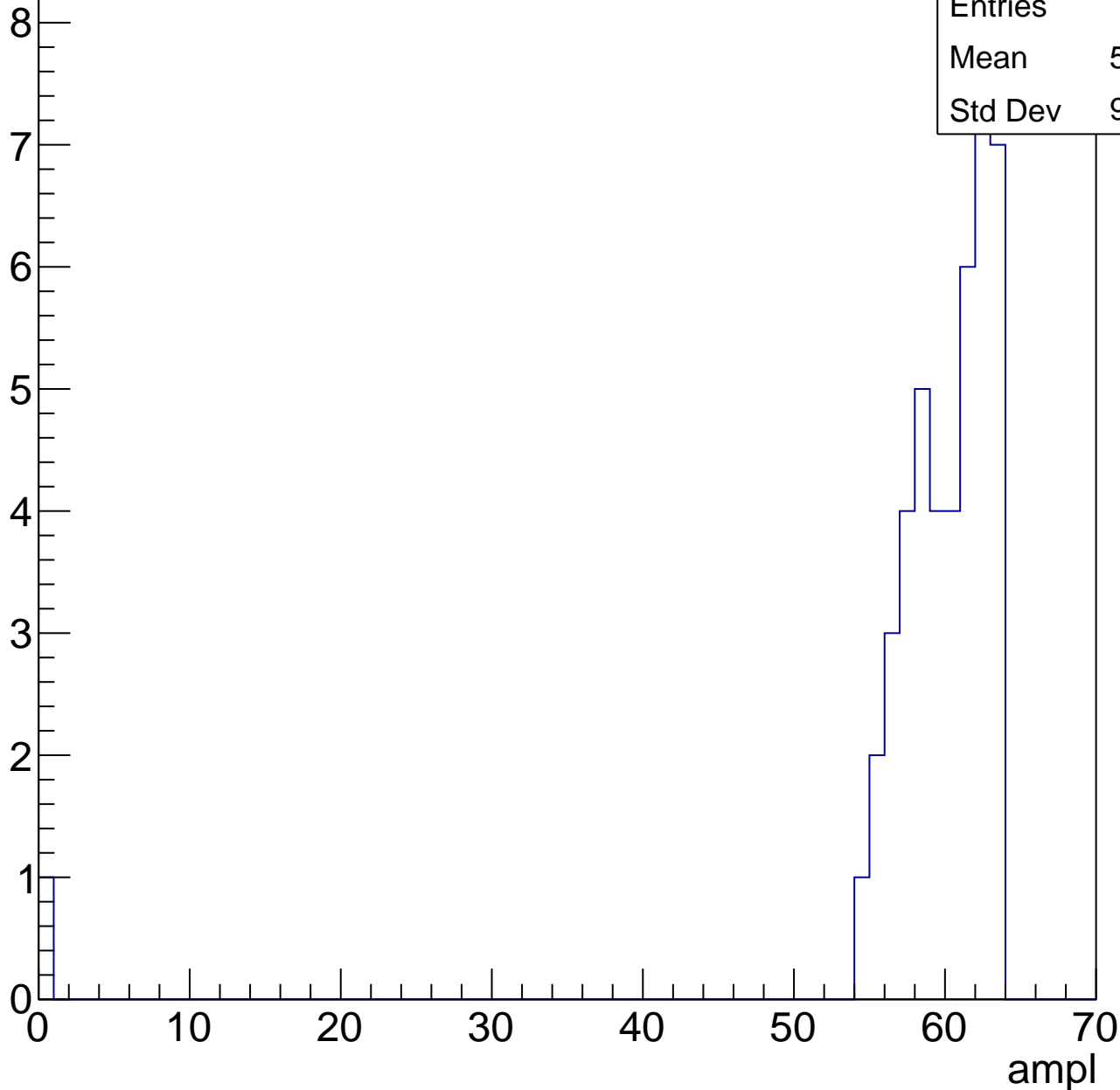


# B0L002S, U23-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	58.42
Std Dev	9.169



# B0L002S, U23-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch12, adc0

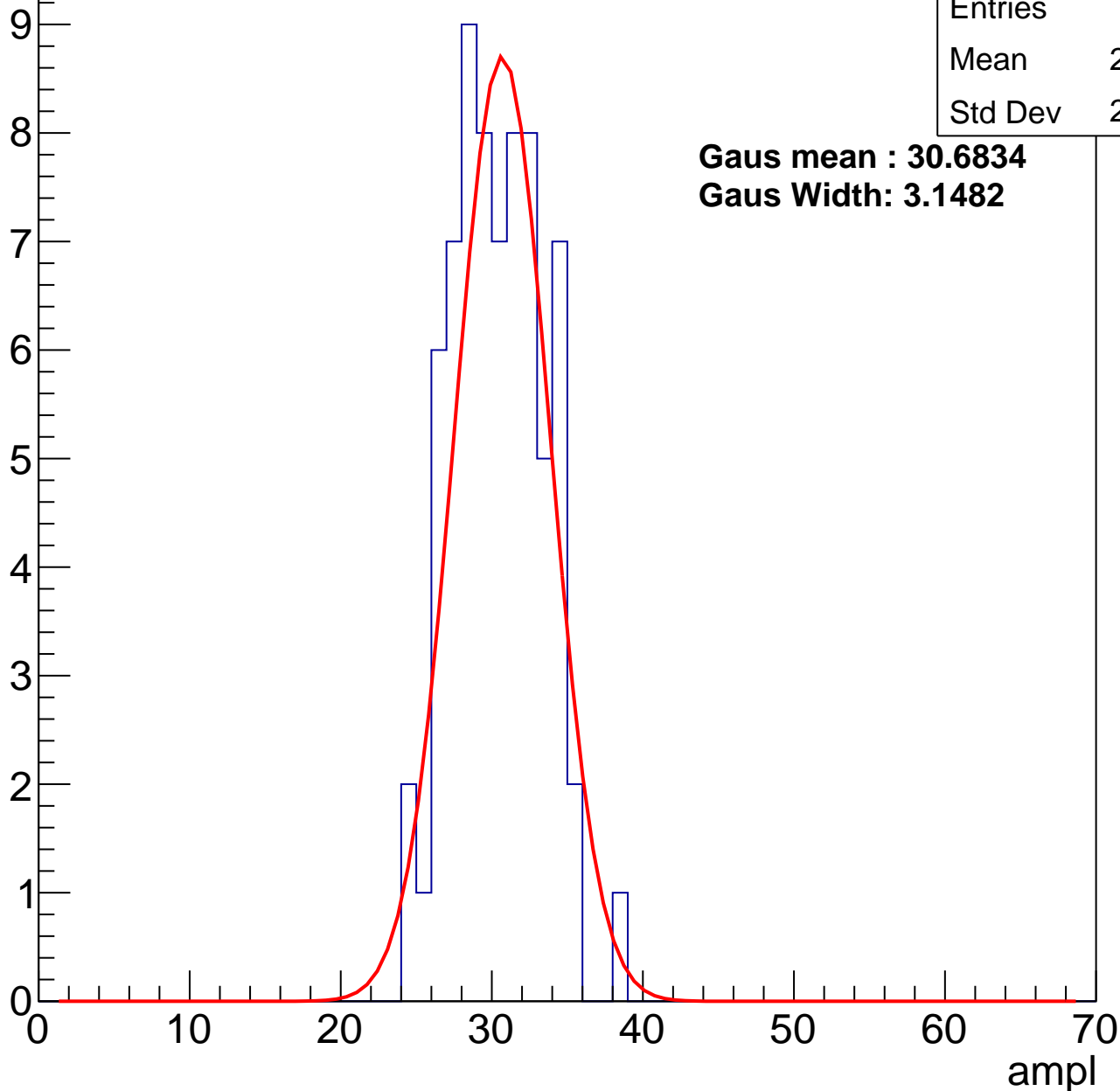
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.96
Std Dev	2.933

**Gaus mean : 30.6834**

**Gaus Width: 3.1482**

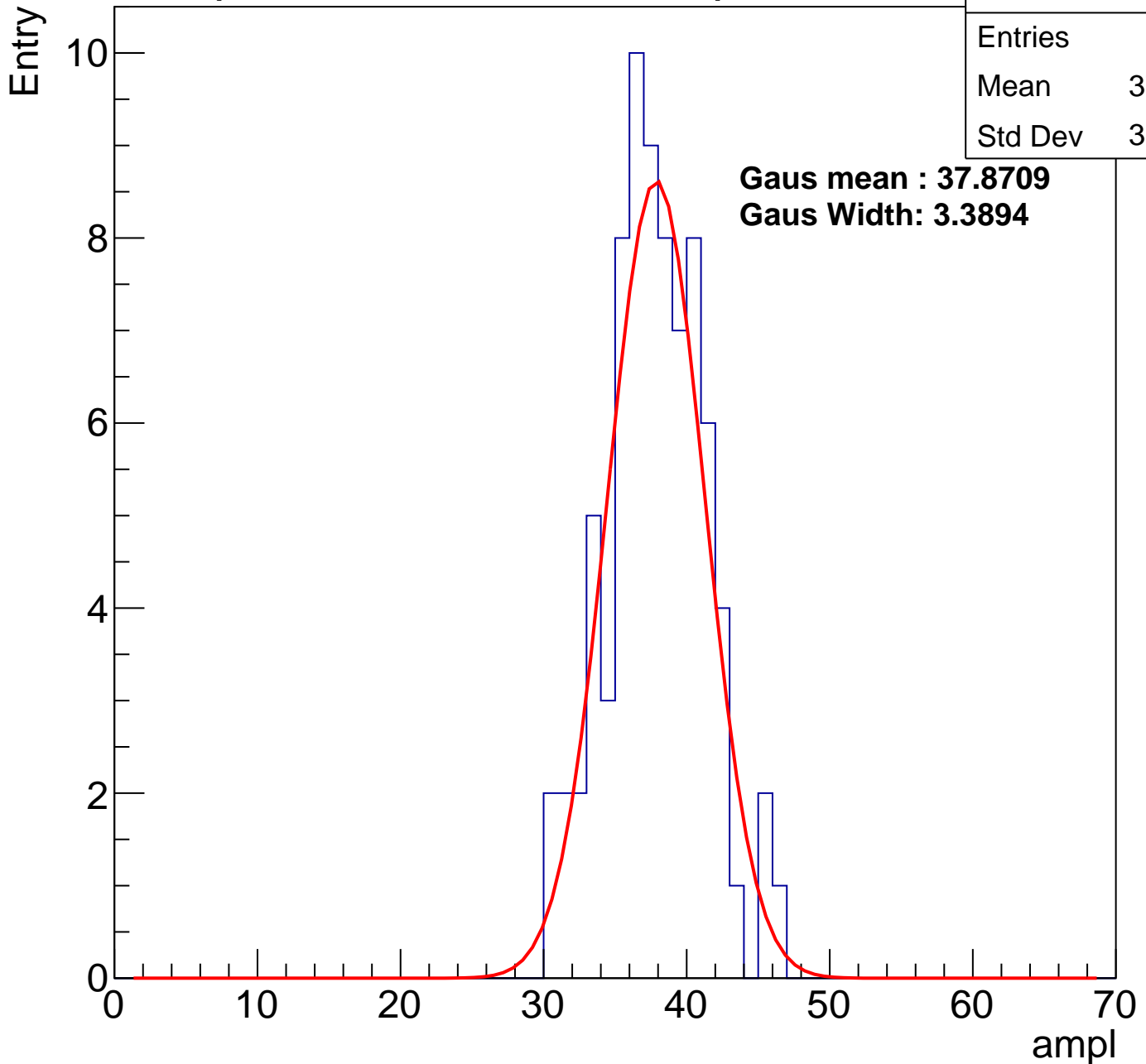


# B0L002S, U23-ch12, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	37.38
Std Dev	3.409

**Gaus mean : 37.8709**  
**Gaus Width: 3.3894**



# B0L002S, U23-ch12, adc2

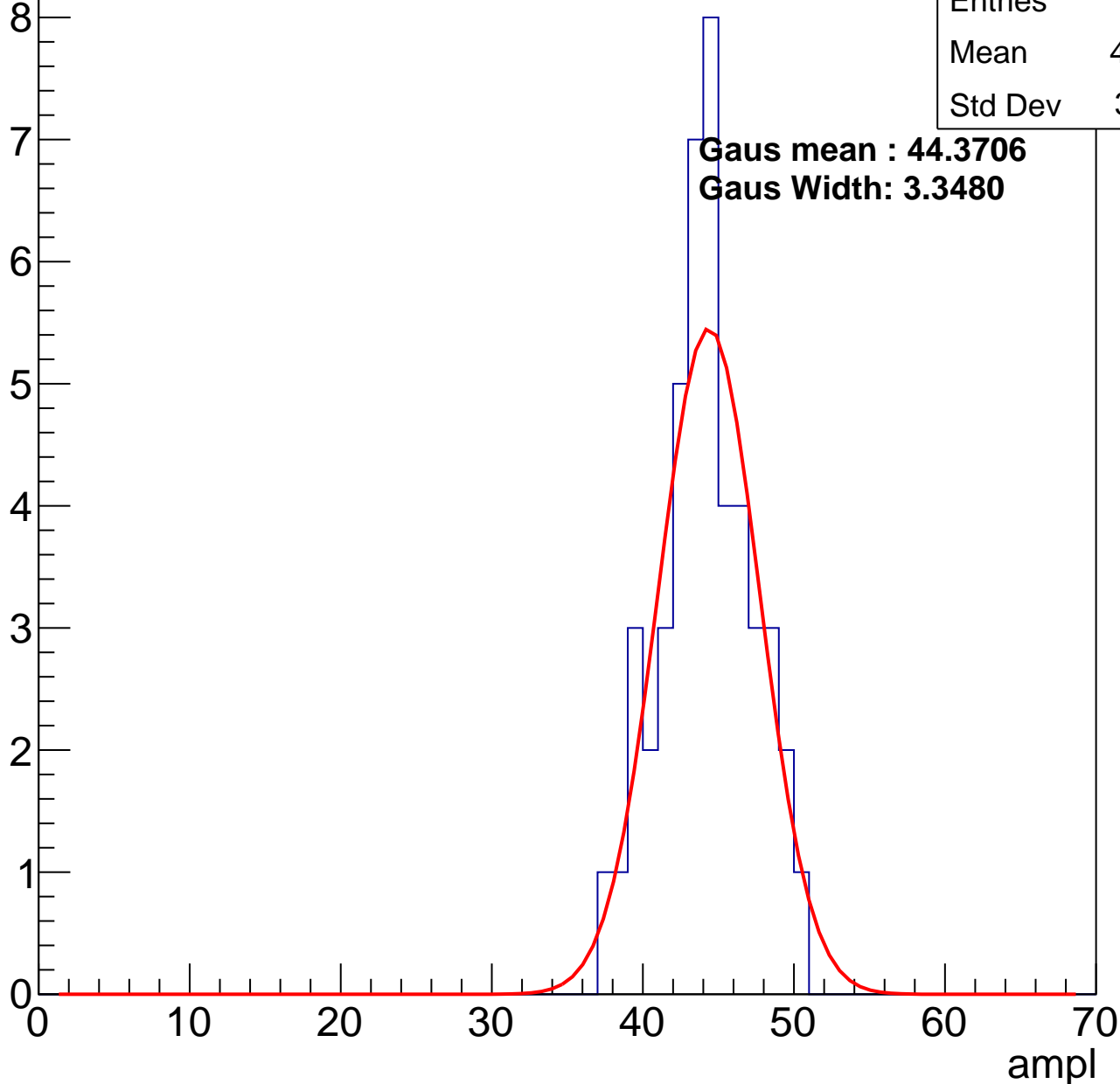
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	43.72
Std Dev	3.001

**Gaus mean : 44.3706**

**Gaus Width: 3.3480**

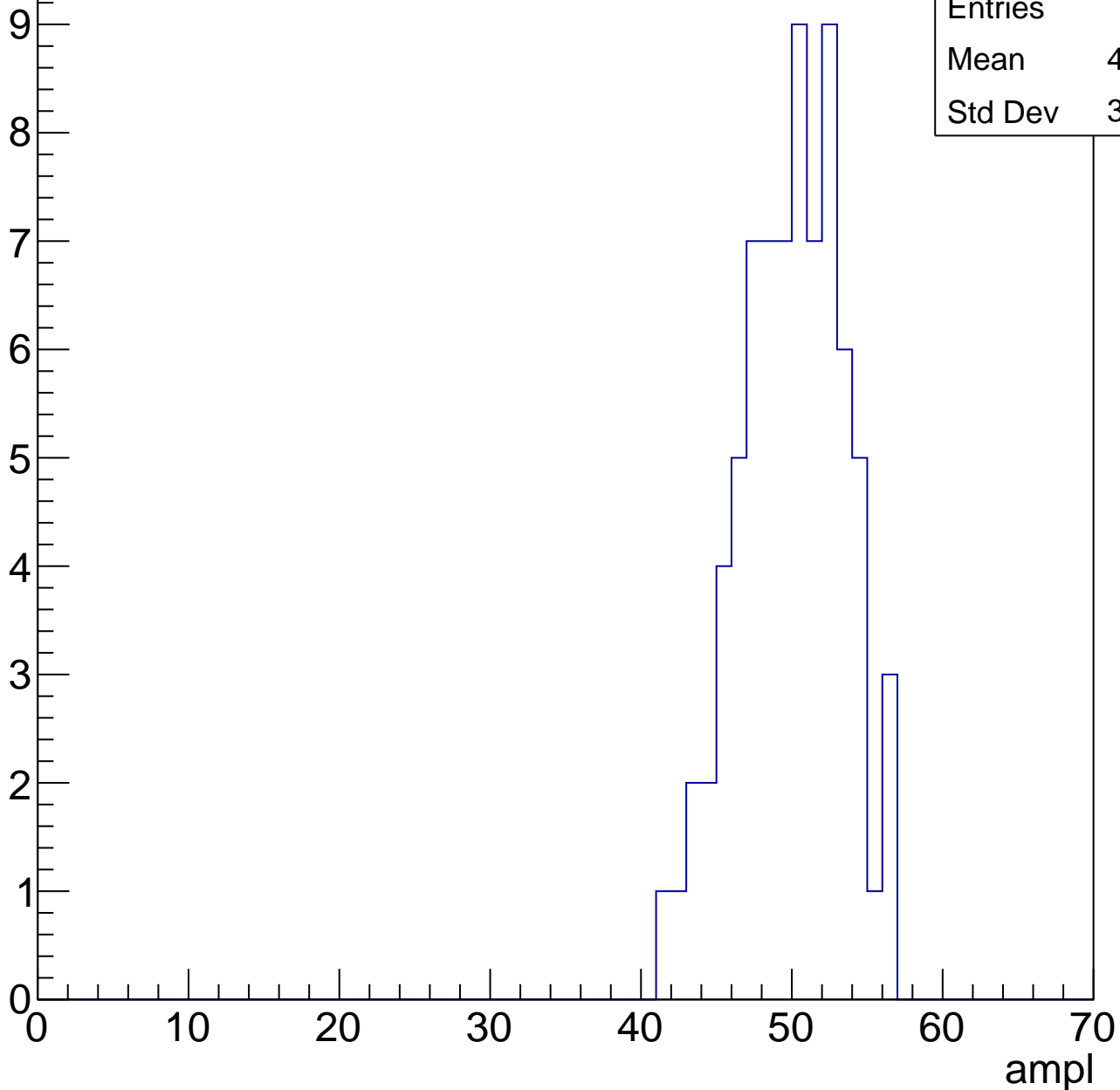


# B0L002S, U23-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	49.49
Std Dev	3.409

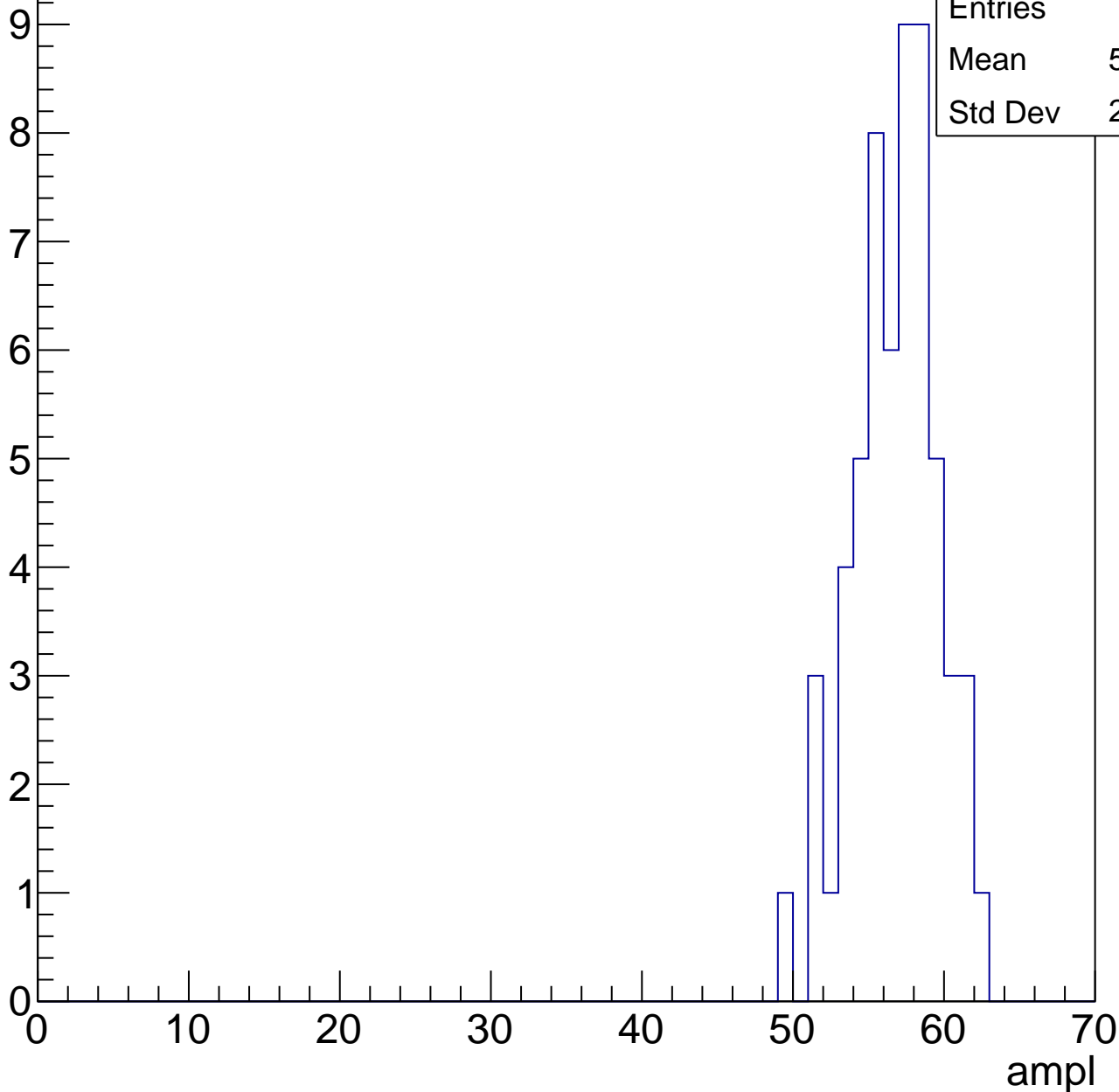


# B0L002S, U23-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.33
Std Dev	2.775

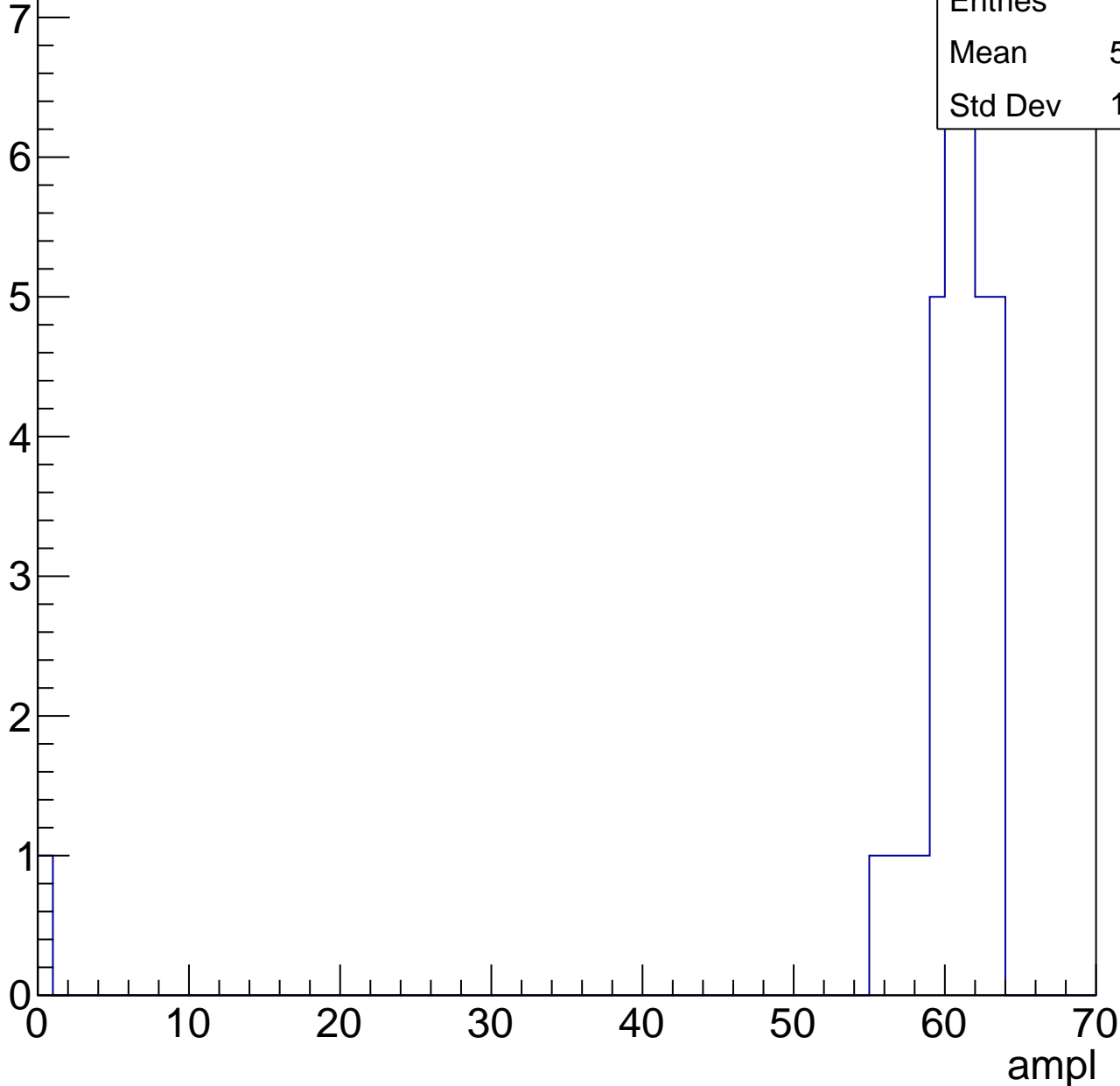


# B0L002S, U23-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.62
Std Dev	10.38

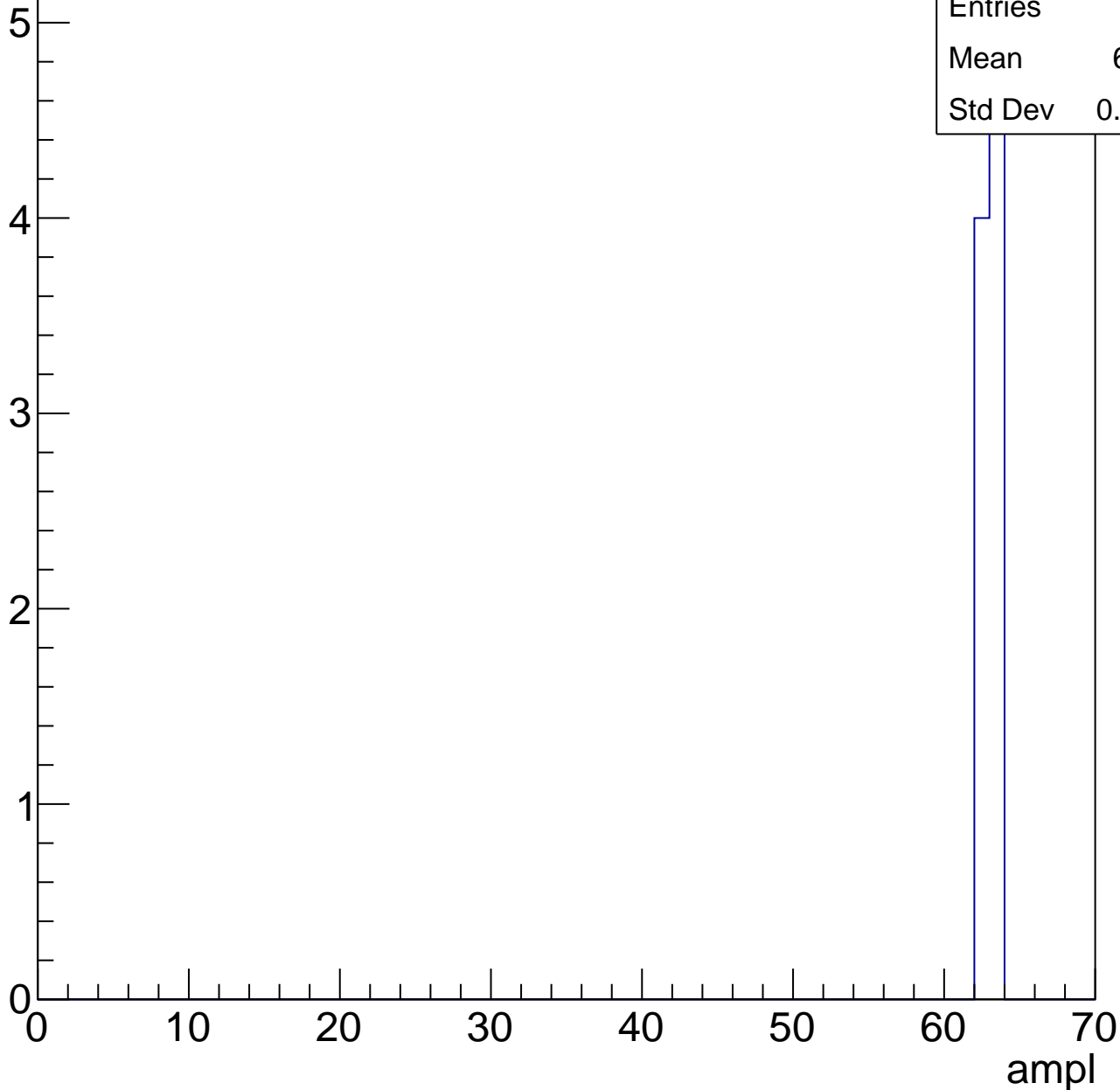


# B0L002S, U23-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	9
Mean	62.56
Std Dev	0.4969





# B0L002S, U23-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch13, adc0

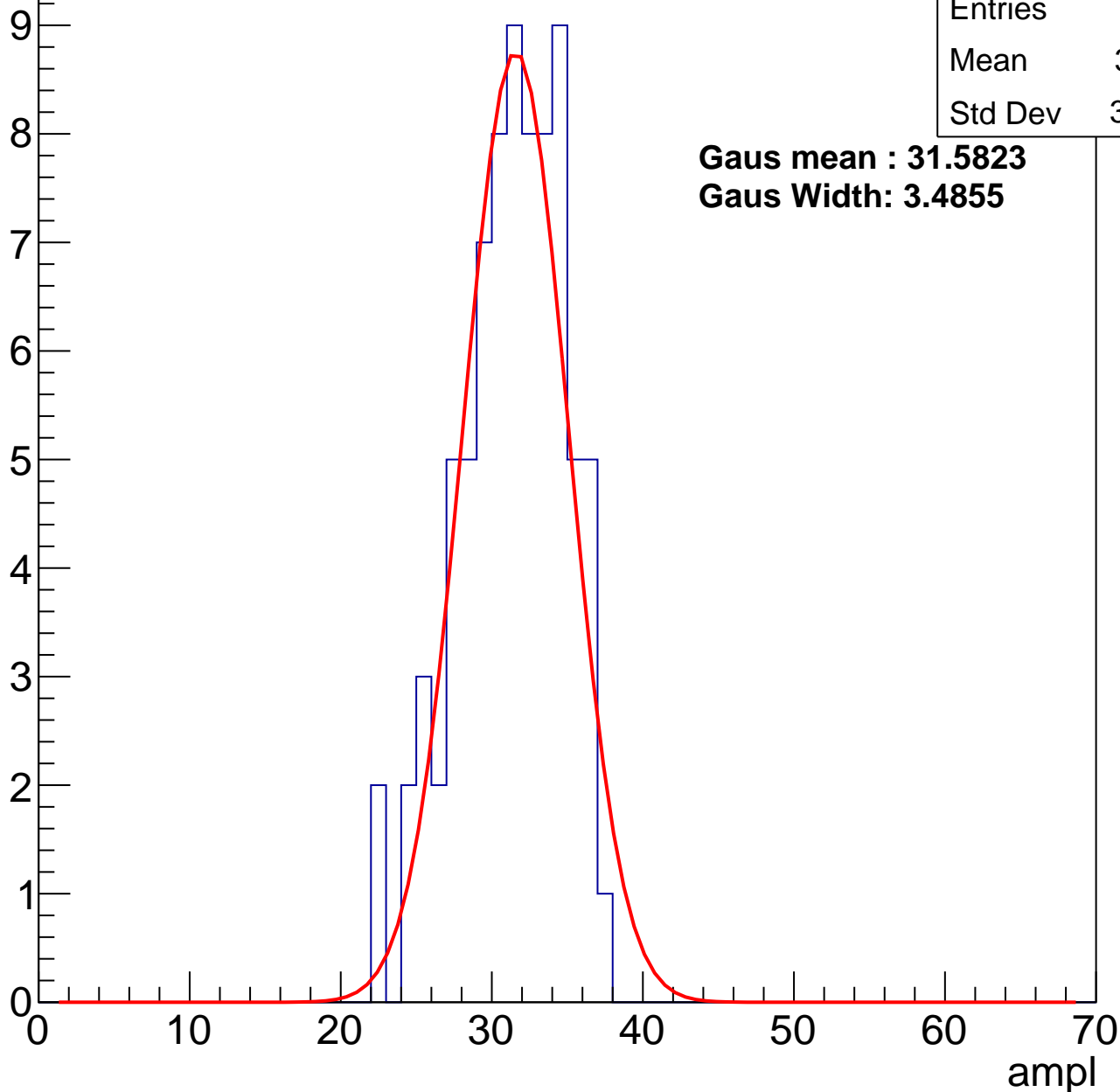
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.81
Std Dev	3.446

**Gaus mean : 31.5823**

**Gaus Width: 3.4855**



# B0L002S, U23-ch13, adc1

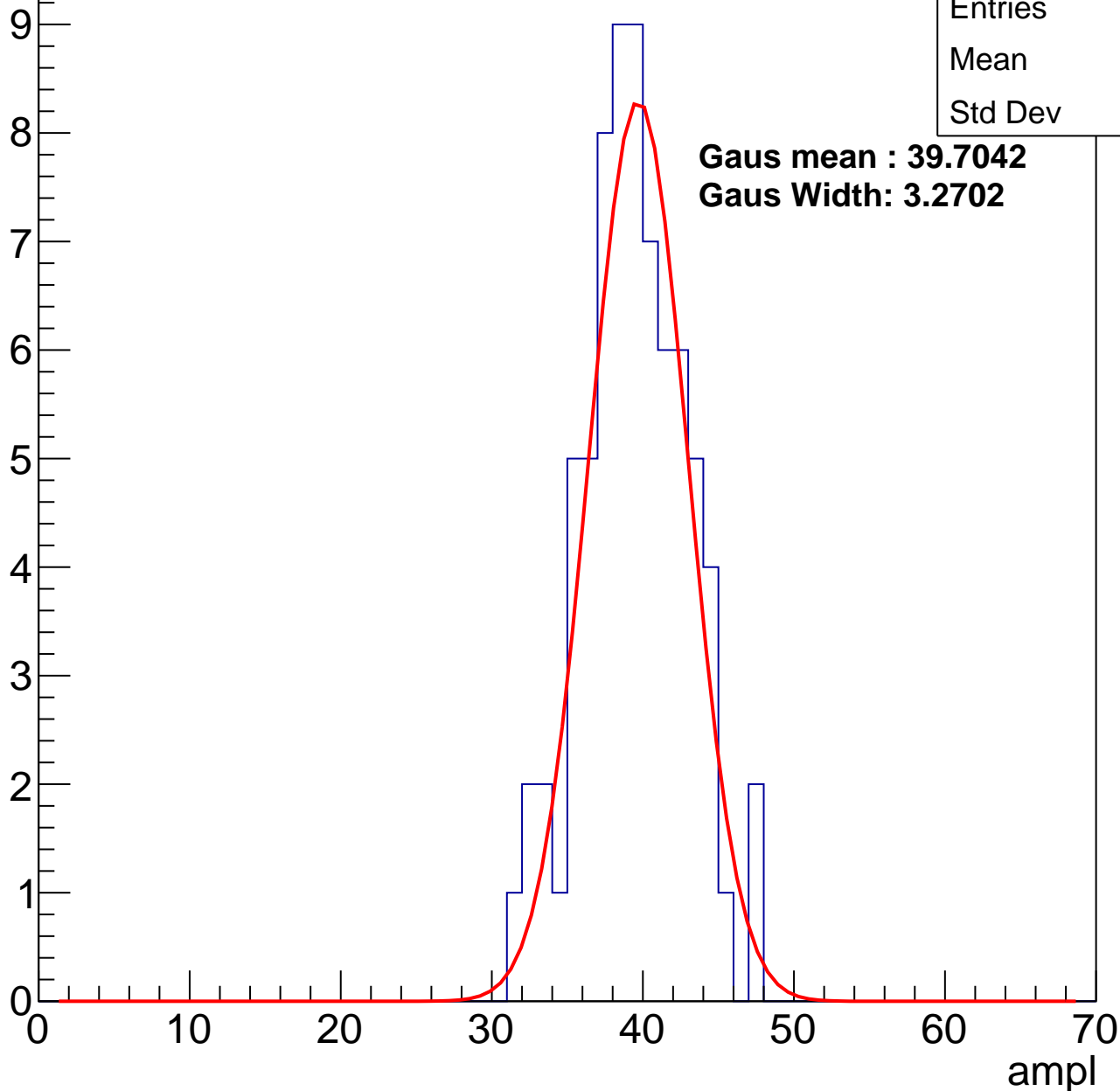
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	39
Std Dev	3.42

**Gaus mean : 39.7042**

**Gaus Width: 3.2702**



# B0L002S, U23-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	60
Mean	44.95
Std Dev	3.378

**Gaus mean : 45.5120**

**Gaus Width: 3.5465**

Entry

10

8

6

4

2

0

0

10

20

30

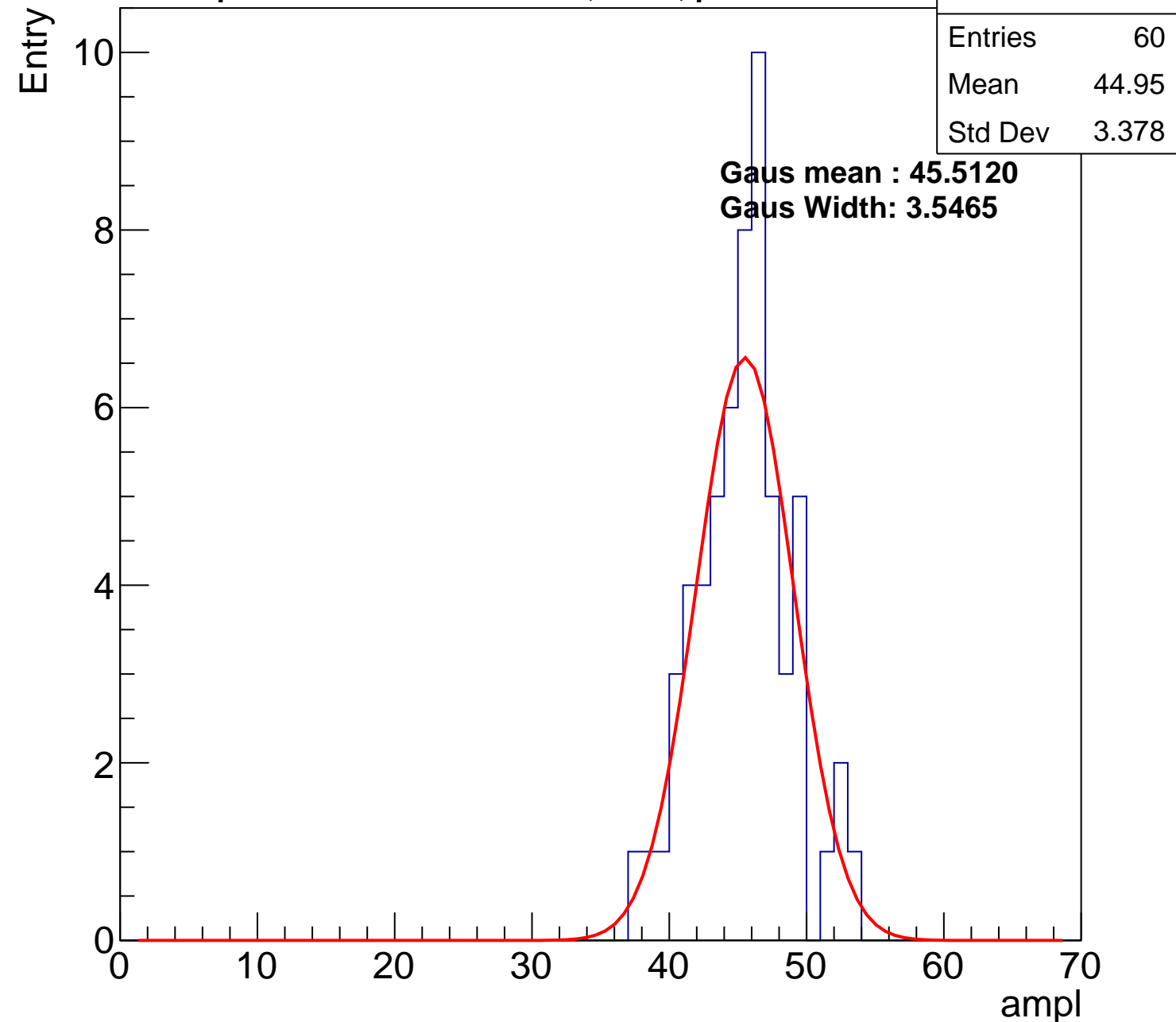
40

50

60

70

ampl



# B0L002S, U23-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

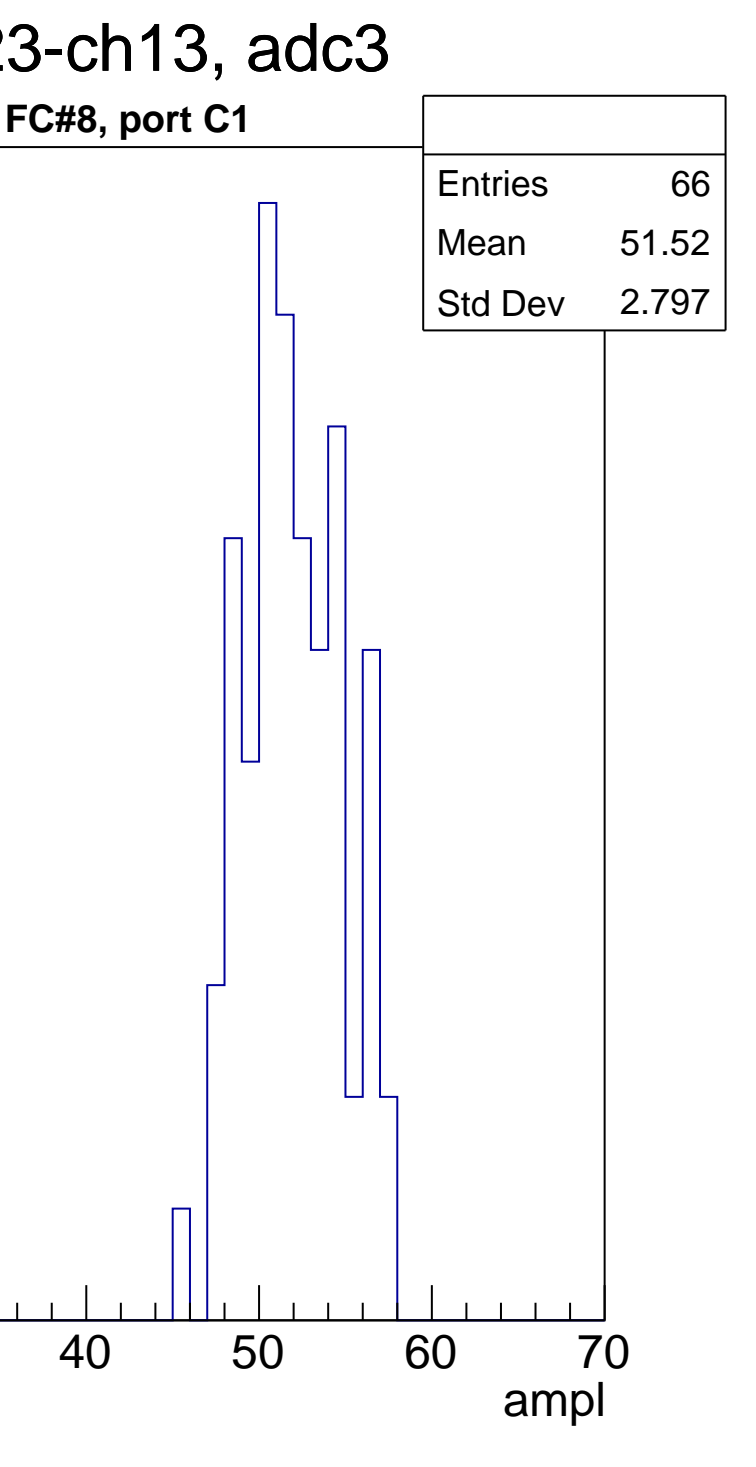
Entries	66
Mean	51.52
Std Dev	2.797

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

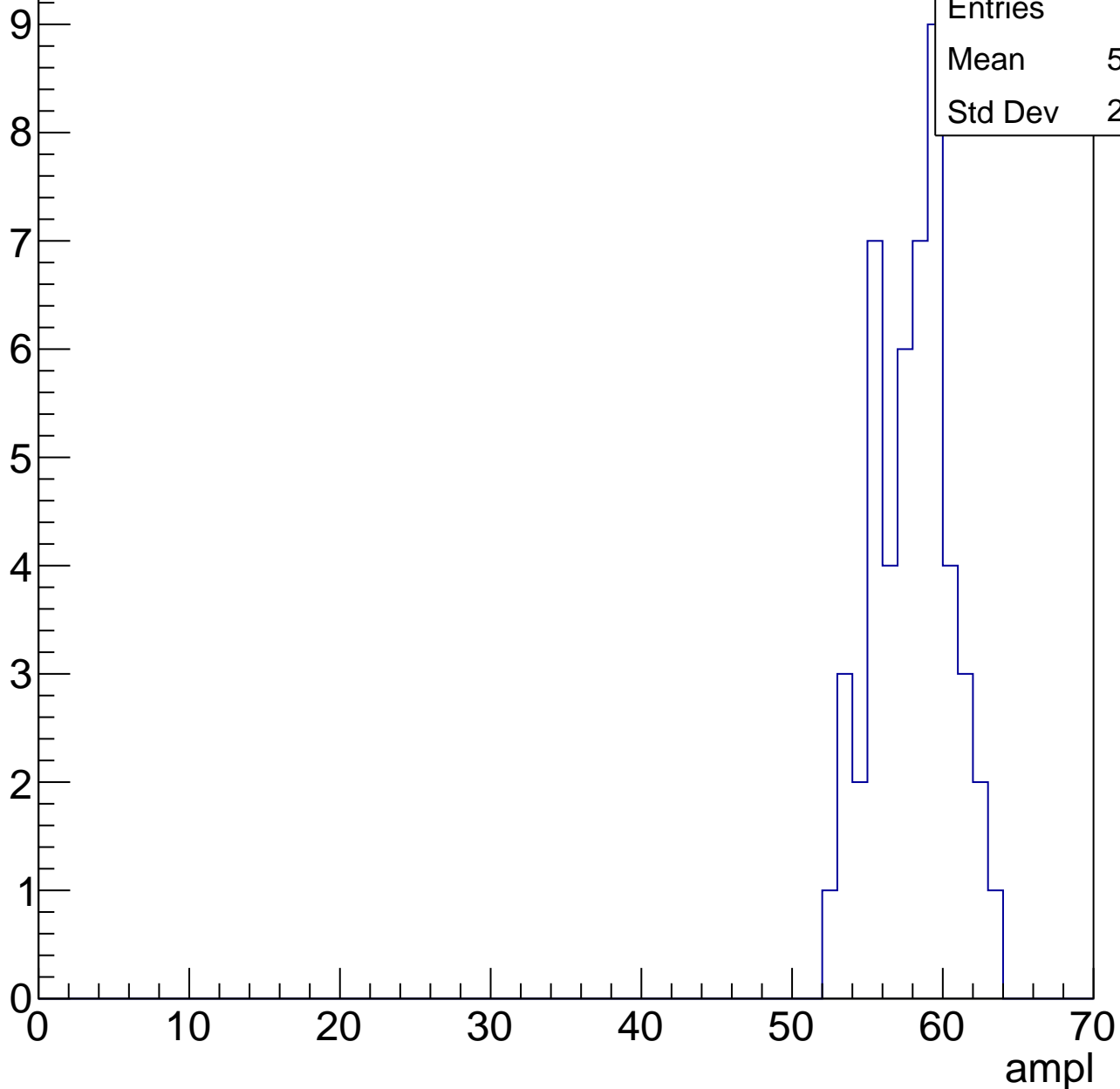
ampl



# B0L002S, U23-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	34
Mean	58.88
Std Dev	10.44

ampl

0

10

20

30

40

50

60

70

# B0L002S, U23-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch14, adc0

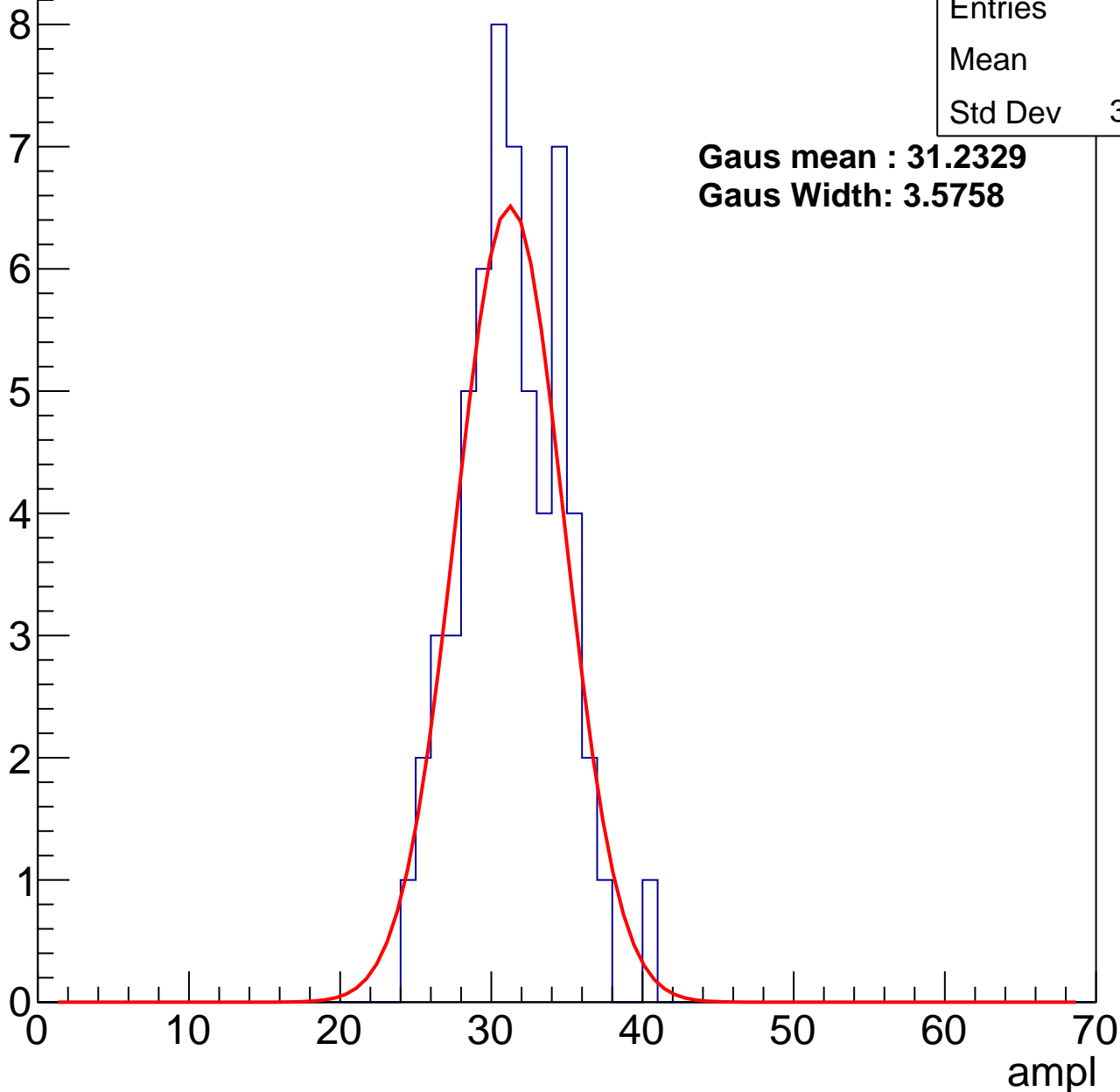
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.9
Std Dev	3.282

**Gaus mean : 31.2329**

**Gaus Width: 3.5758**



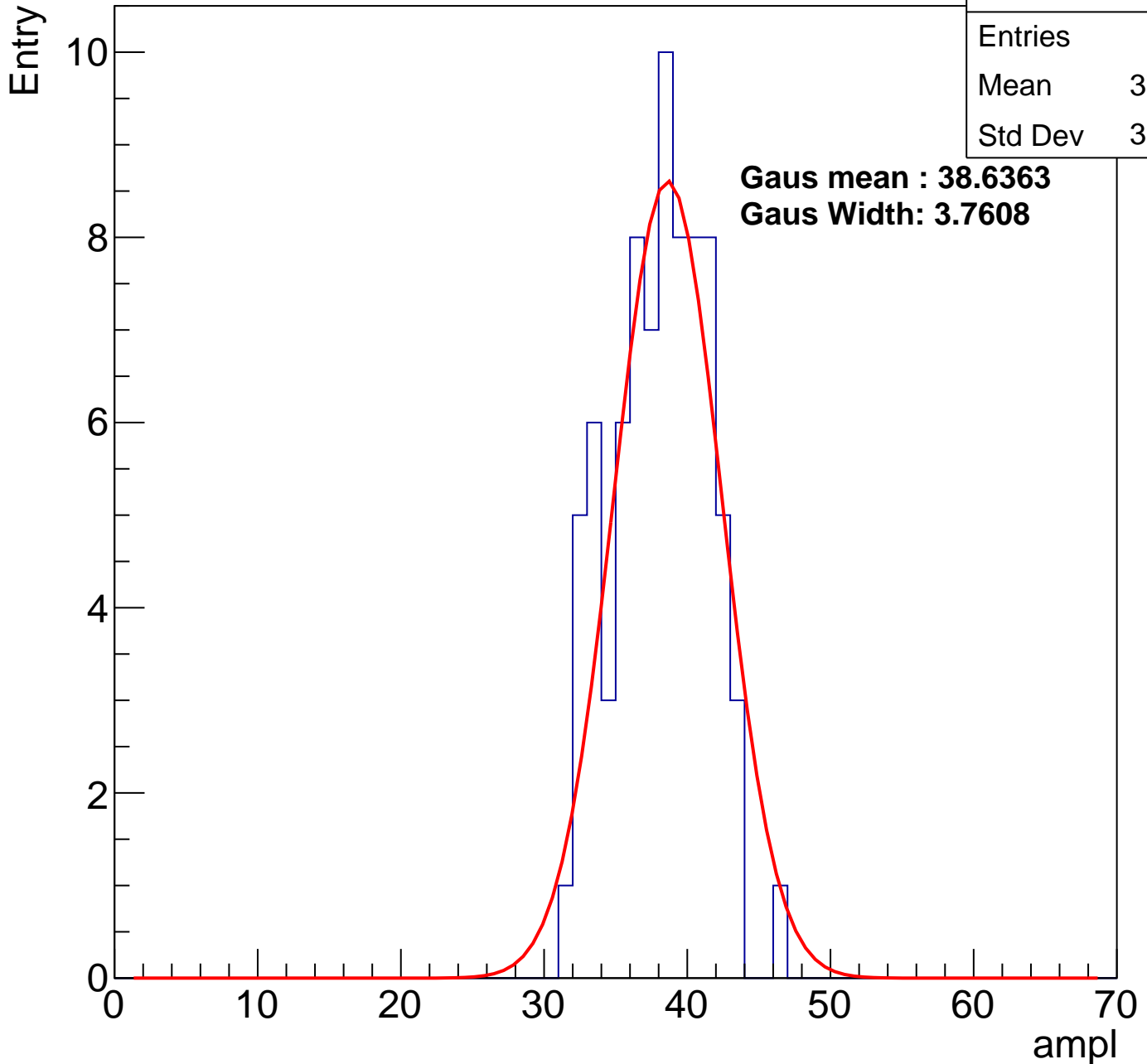
# B0L002S, U23-ch14, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	79
Mean	37.63
Std Dev	3.262

**Gaus mean : 38.6363**

**Gaus Width: 3.7608**



# B0L002S, U23-ch14, adc2

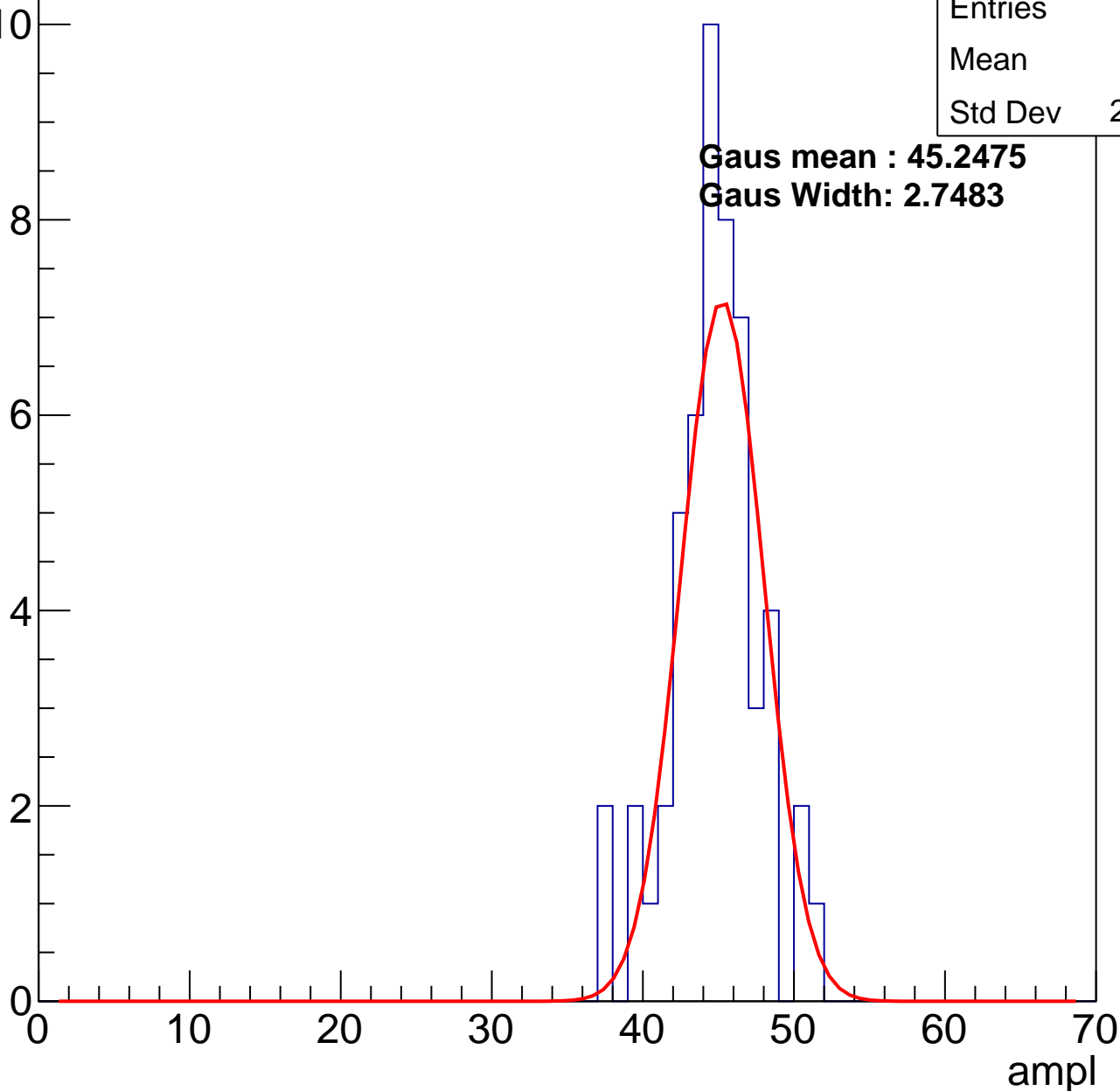
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.3
Std Dev	2.918

**Gaus mean : 45.2475**

**Gaus Width: 2.7483**

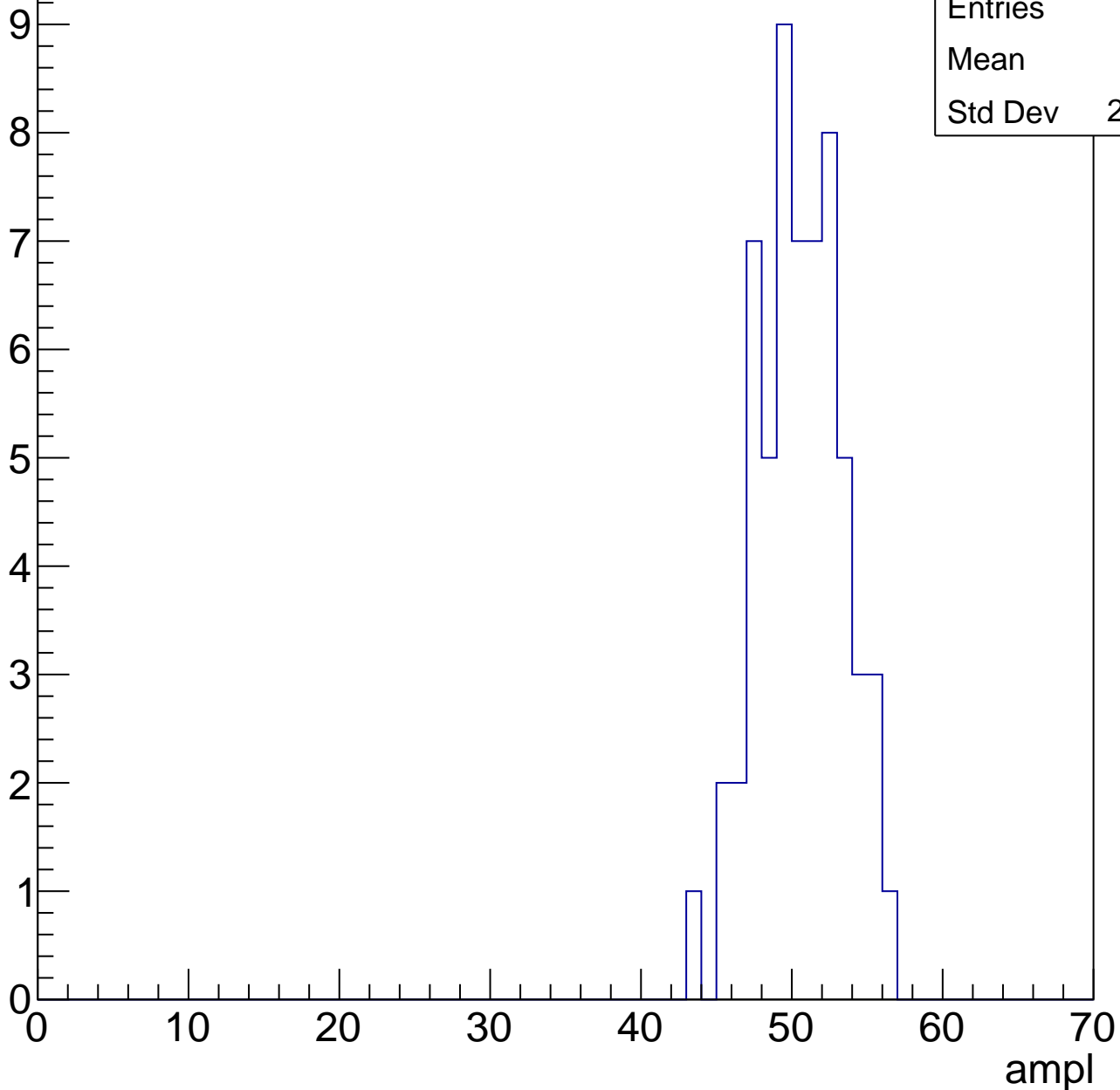


# B0L002S, U23-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.1
Std Dev	2.785



# B0L002S, U23-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	61
Mean	54.92
Std Dev	7.661

Entry

10

8

6

4

2

0

0

10

20

30

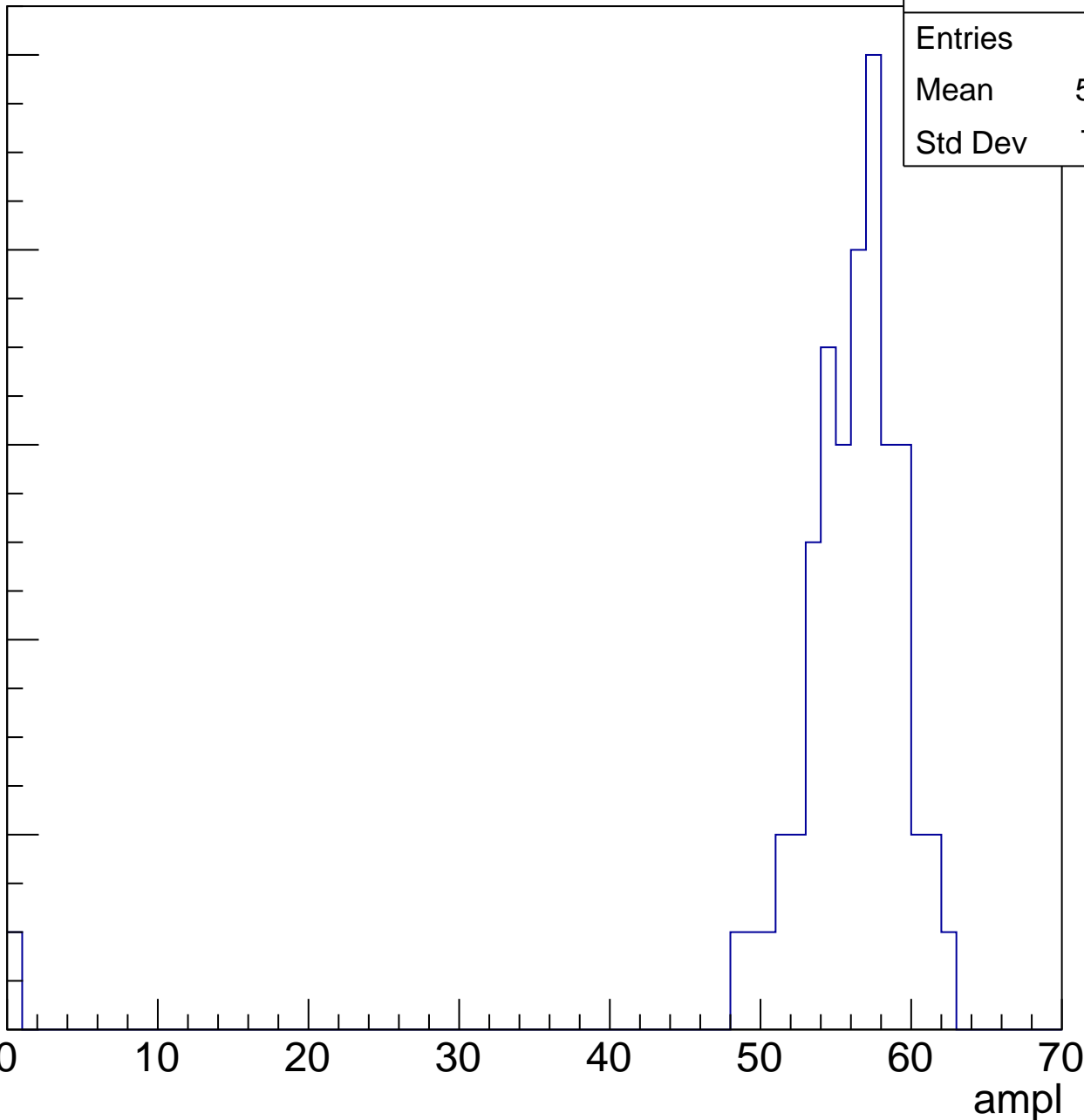
40

50

60

70

ampl

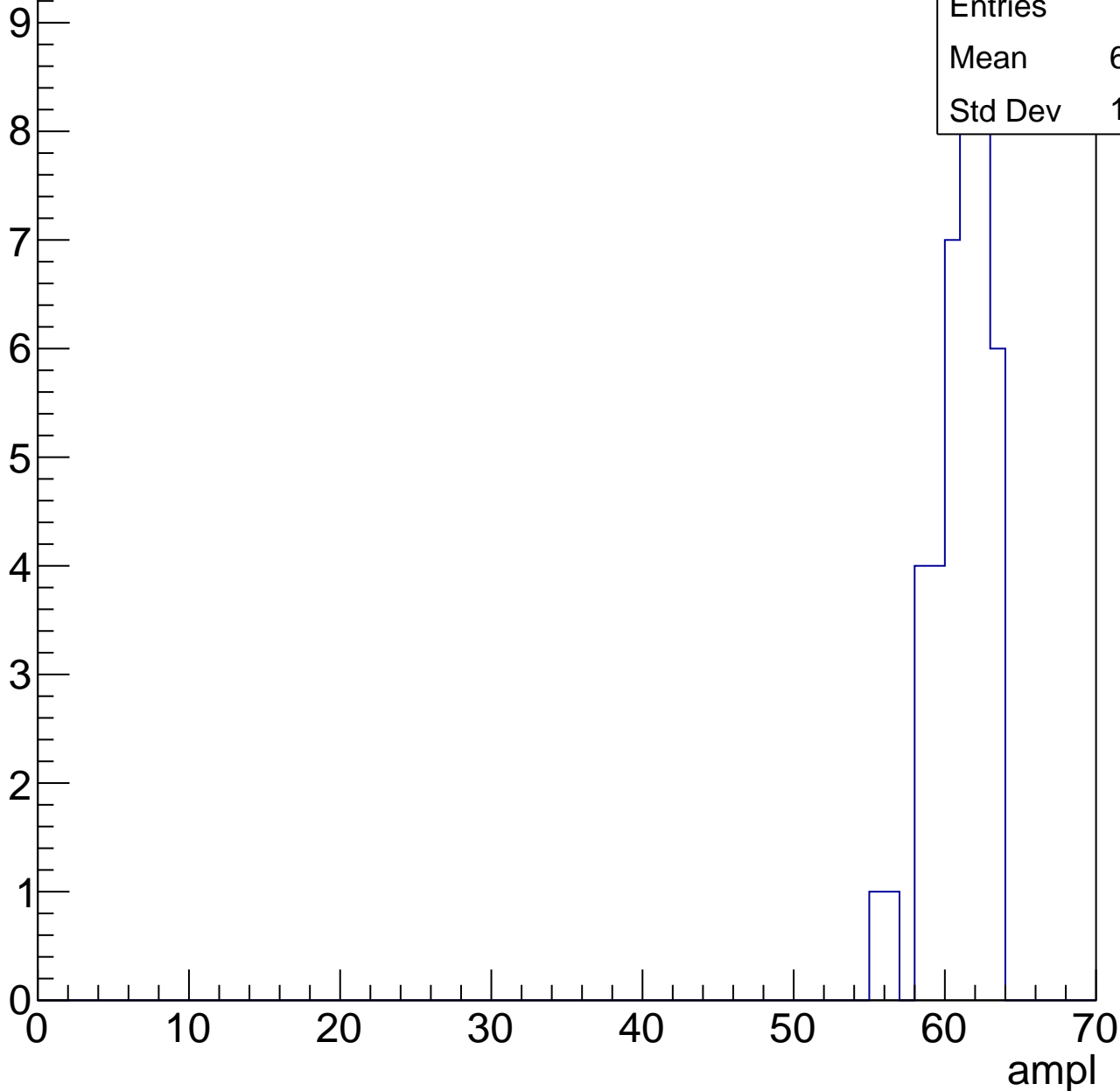


# B0L002S, U23-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

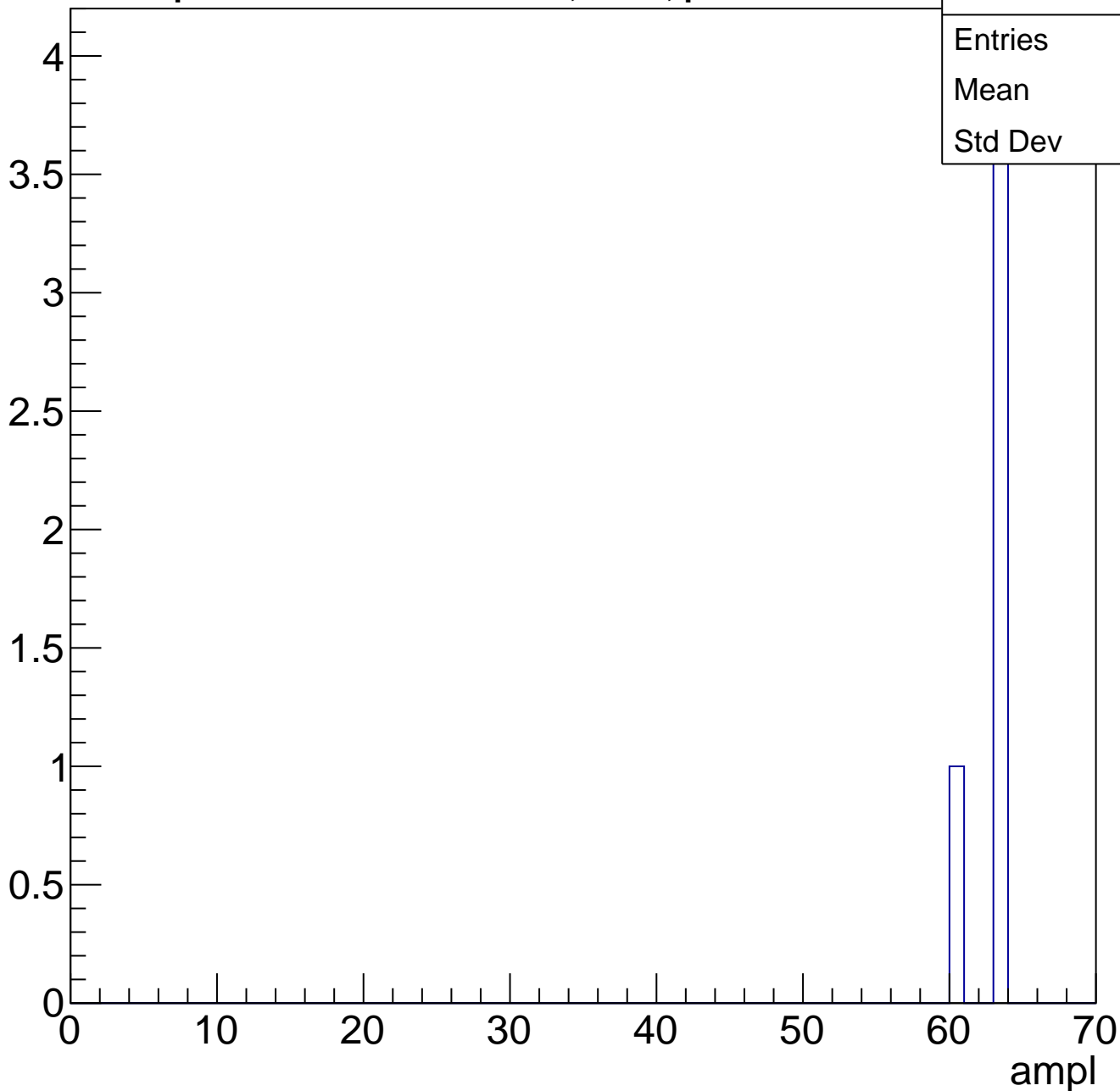
Entries	40
Mean	60.58
Std Dev	1.909



# B0L002S, U23-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch15, adc0

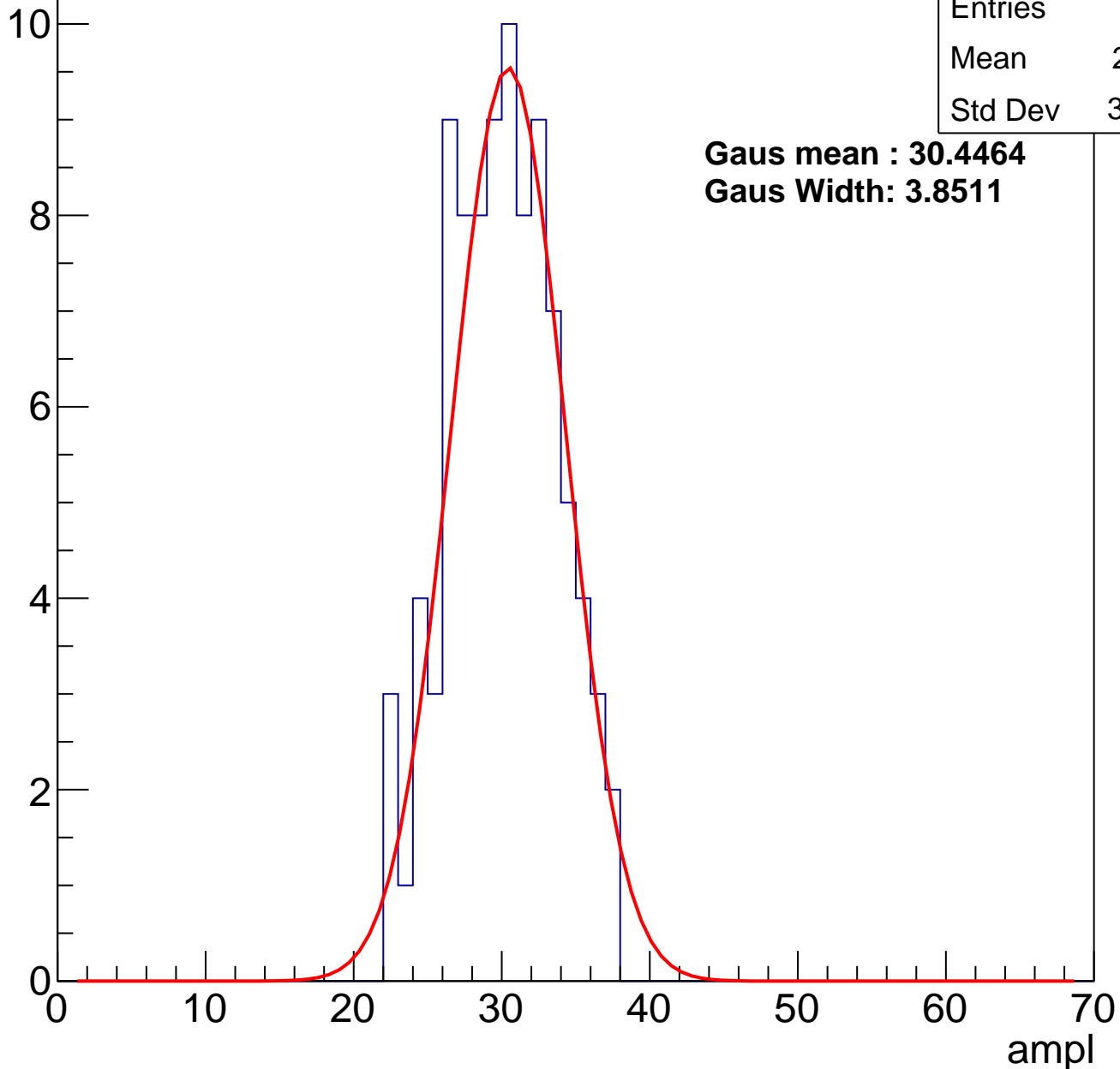
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	93
Mean	29.61
Std Dev	3.586

**Gaus mean : 30.4464**

**Gaus Width: 3.8511**

Entry



# B0L002S, U23-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	70
Mean	38.07
Std Dev	2.929

**Gaus mean : 38.3880**

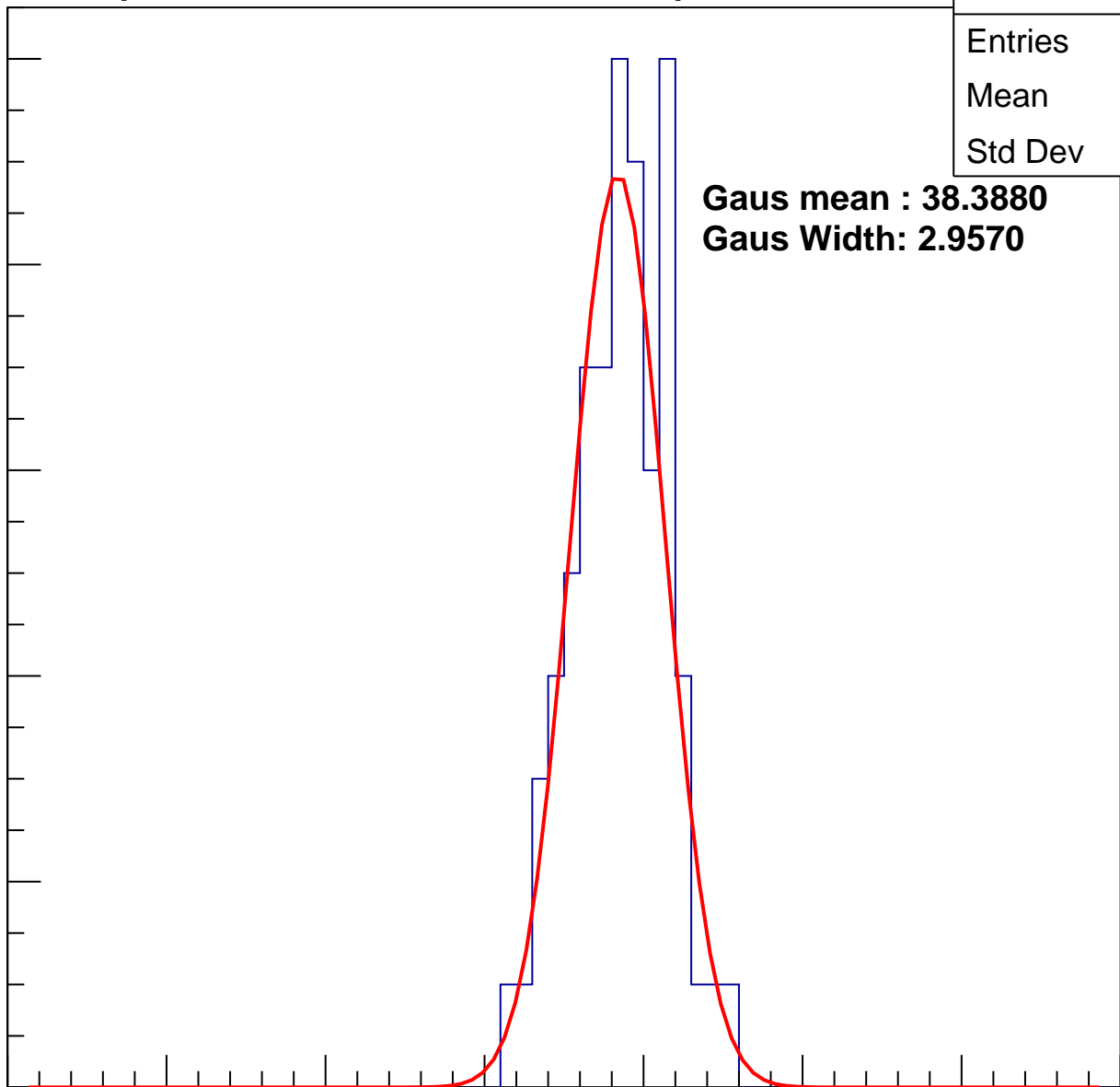
**Gaus Width: 2.9570**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch15, adc2

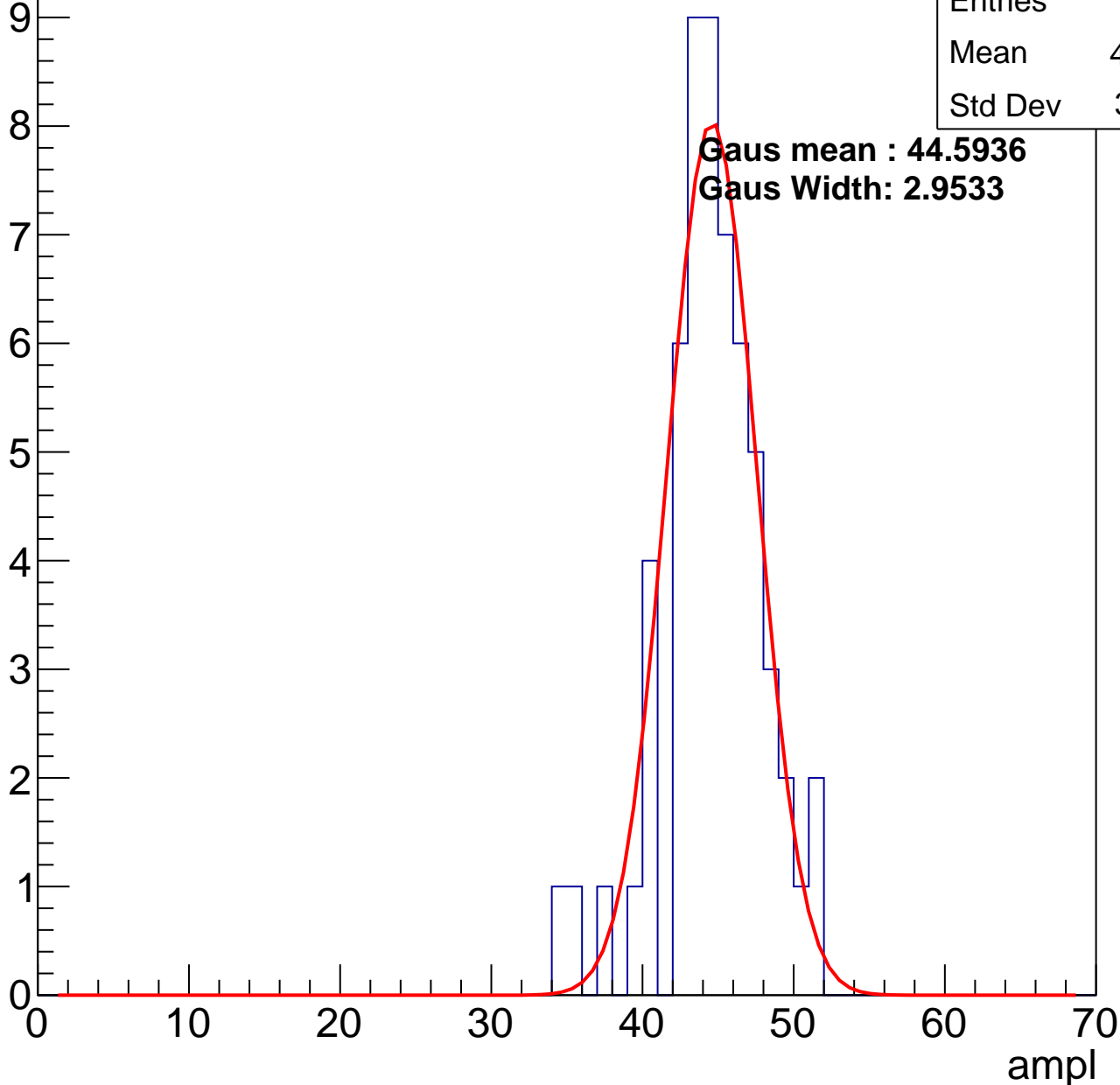
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.14
Std Dev	3.371

**Gaus mean : 44.5936**

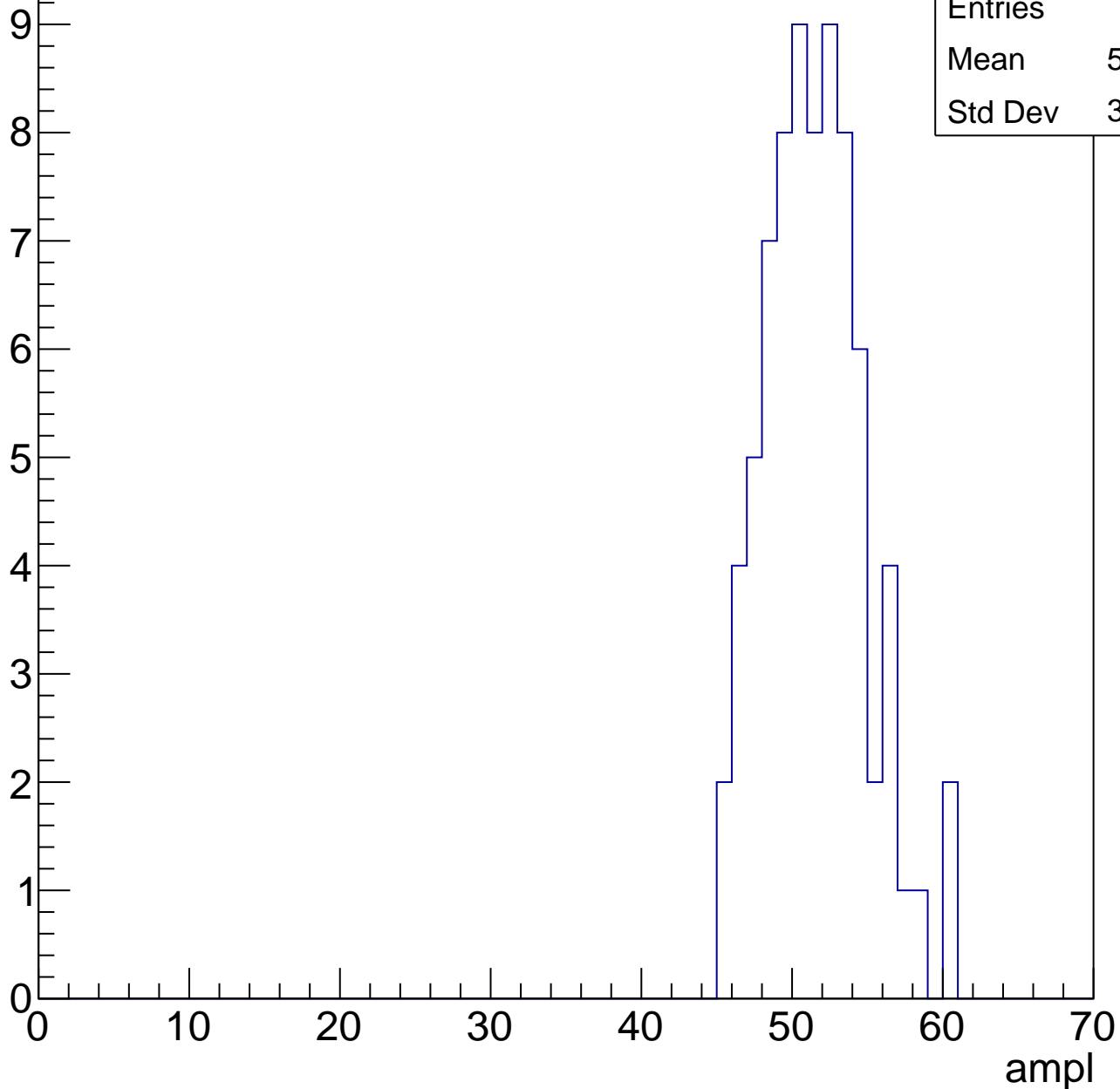
**Gaus Width: 2.9533**



# B0L002S, U23-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

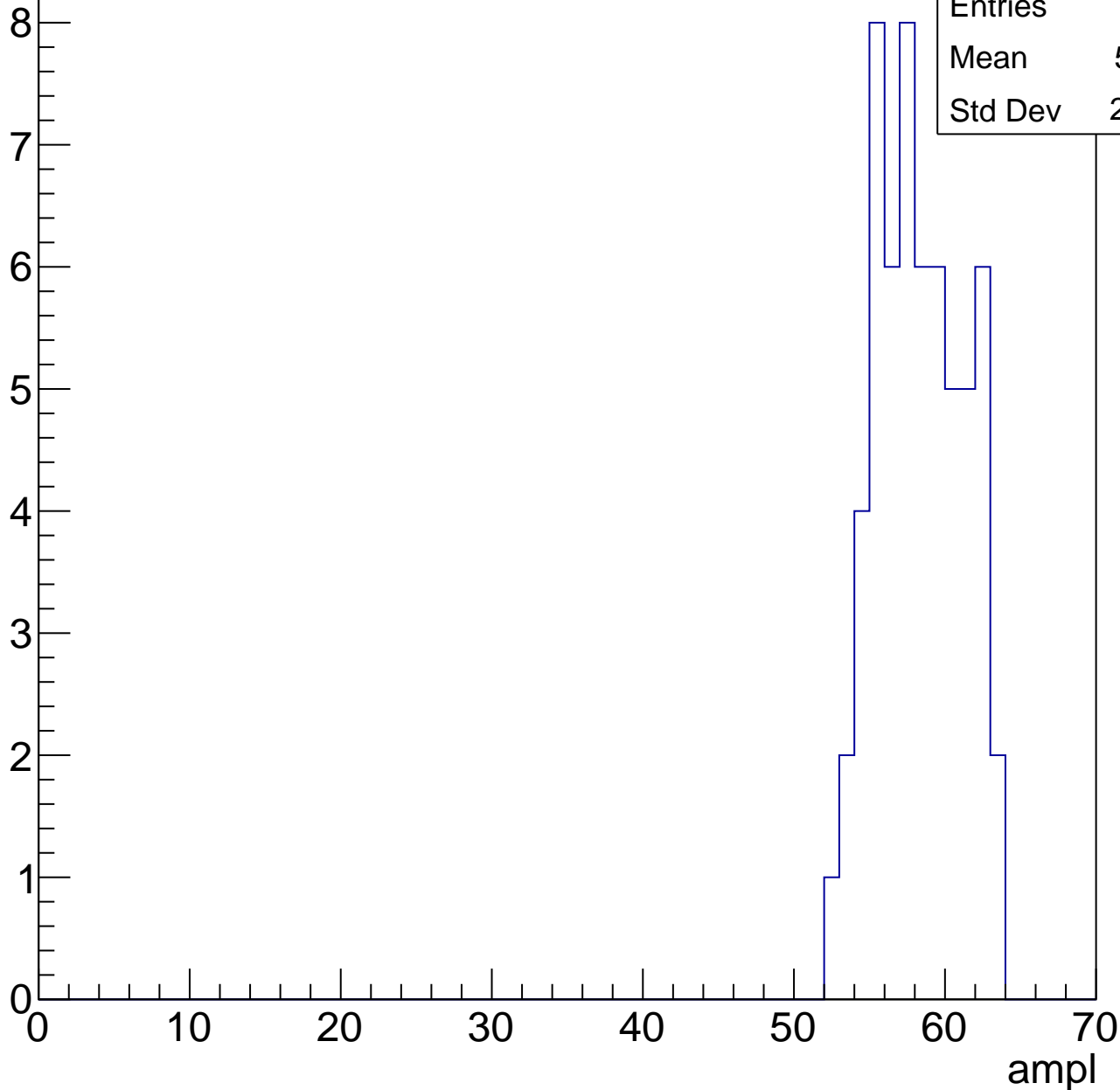


# B0L002S, U23-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	57.81
Std Dev	2.819

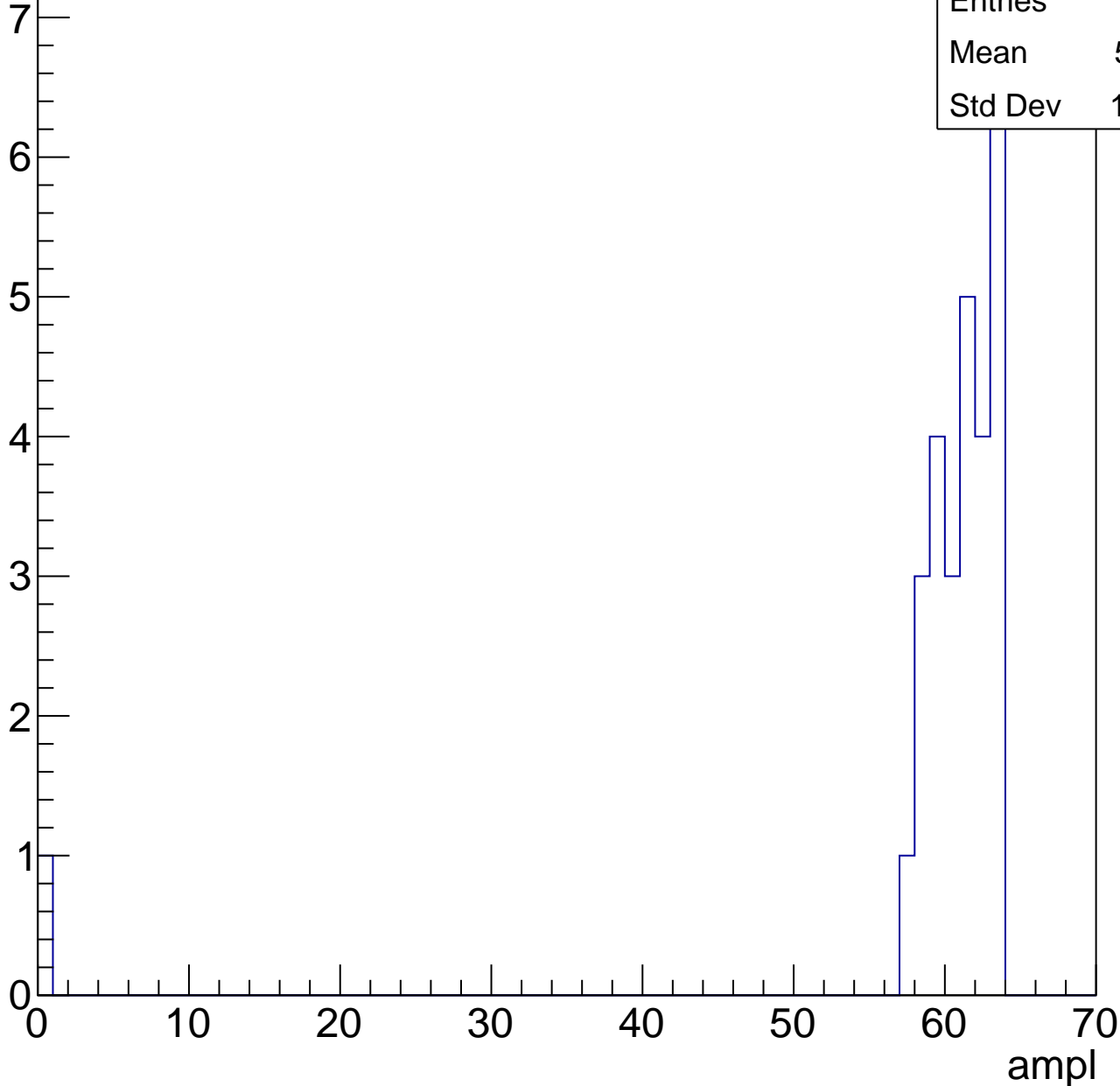


# B0L002S, U23-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.61
Std Dev	11.42



# B0L002S, U23-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch16, adc0

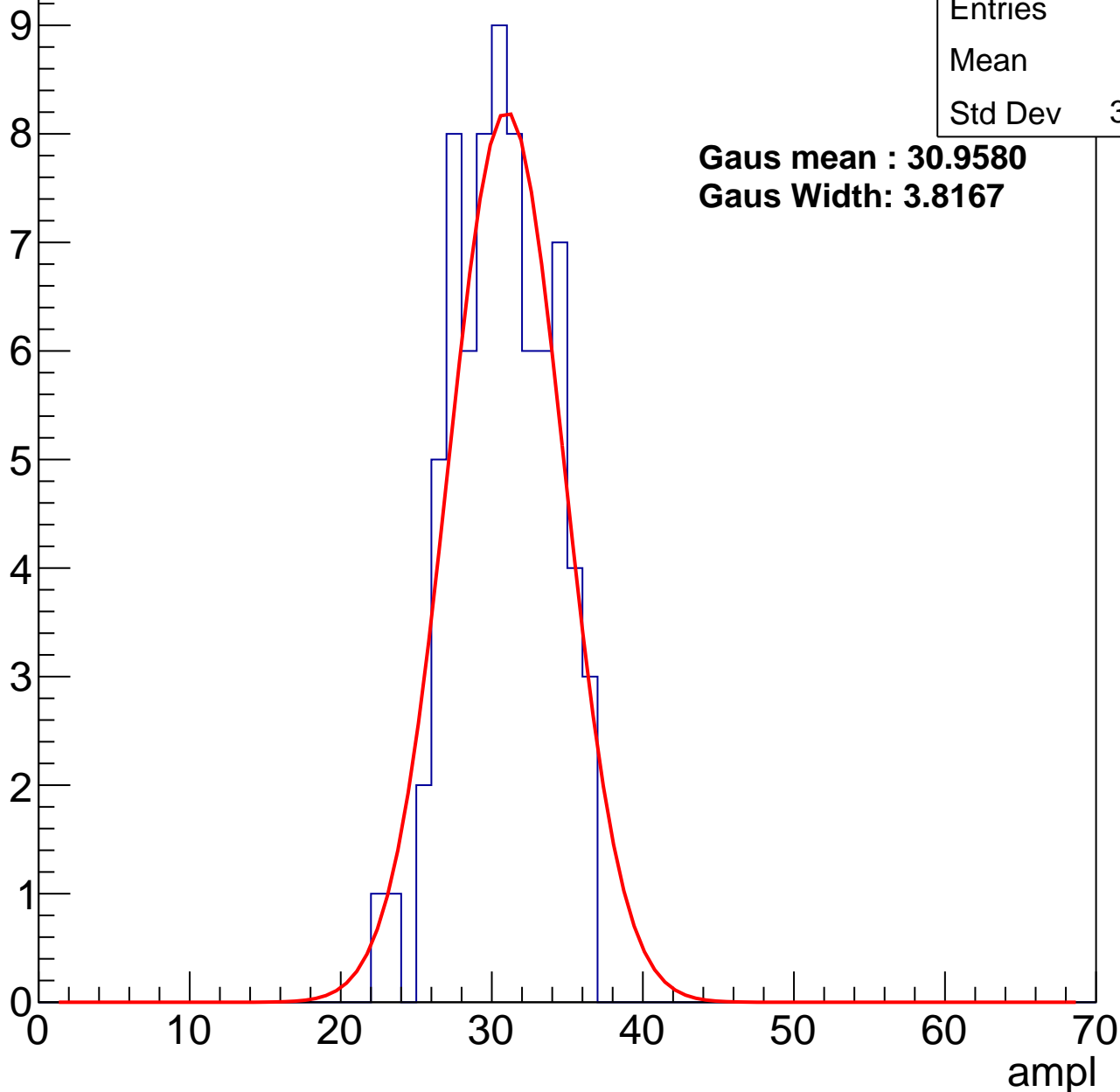
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	30.2
Std Dev	3.183

**Gaus mean : 30.9580**

**Gaus Width: 3.8167**

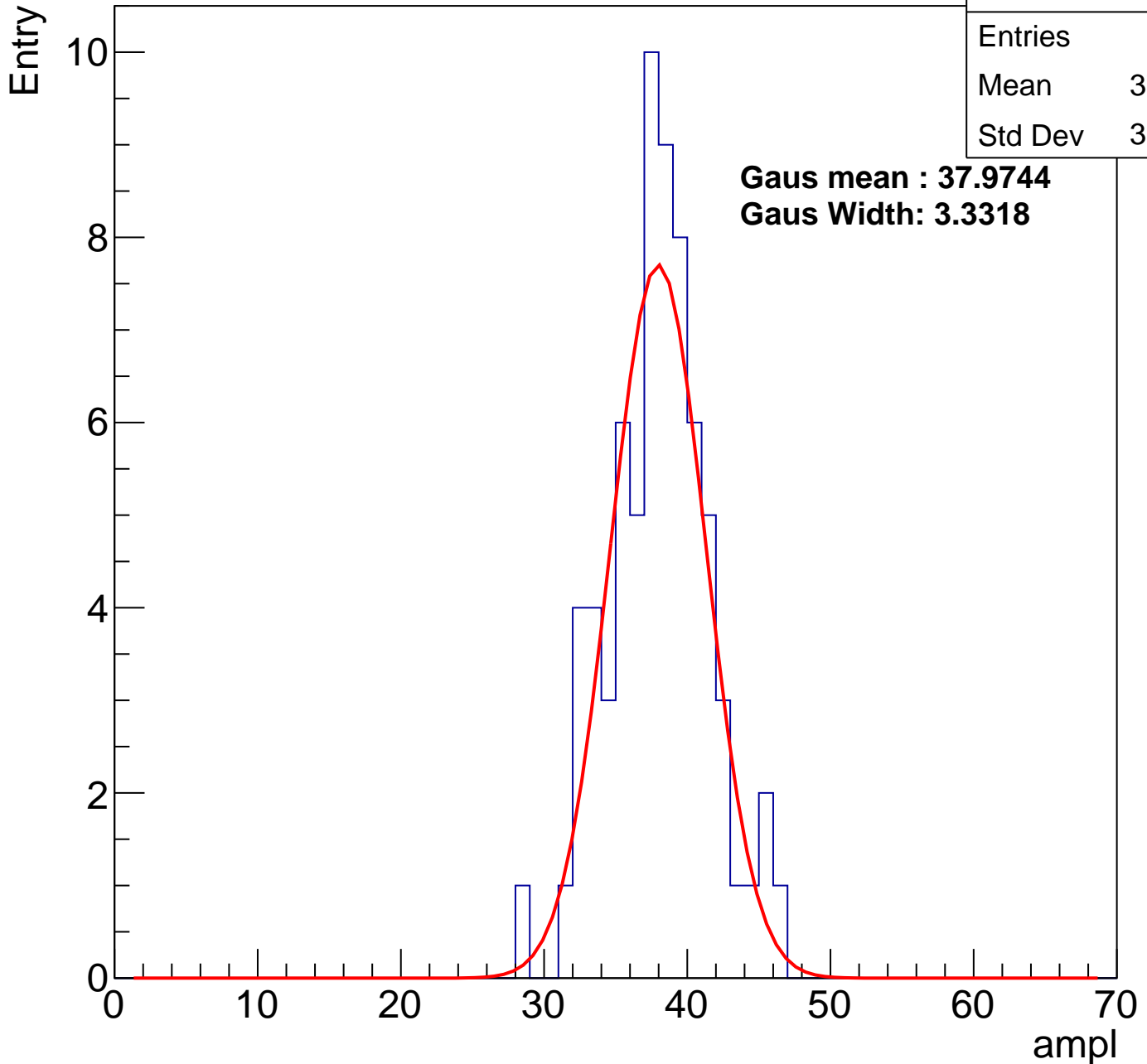


# B0L002S, U23-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	70
Mean	37.56
Std Dev	3.504

**Gaus mean : 37.9744**  
**Gaus Width: 3.3318**



# B0L002S, U23-ch16, adc2

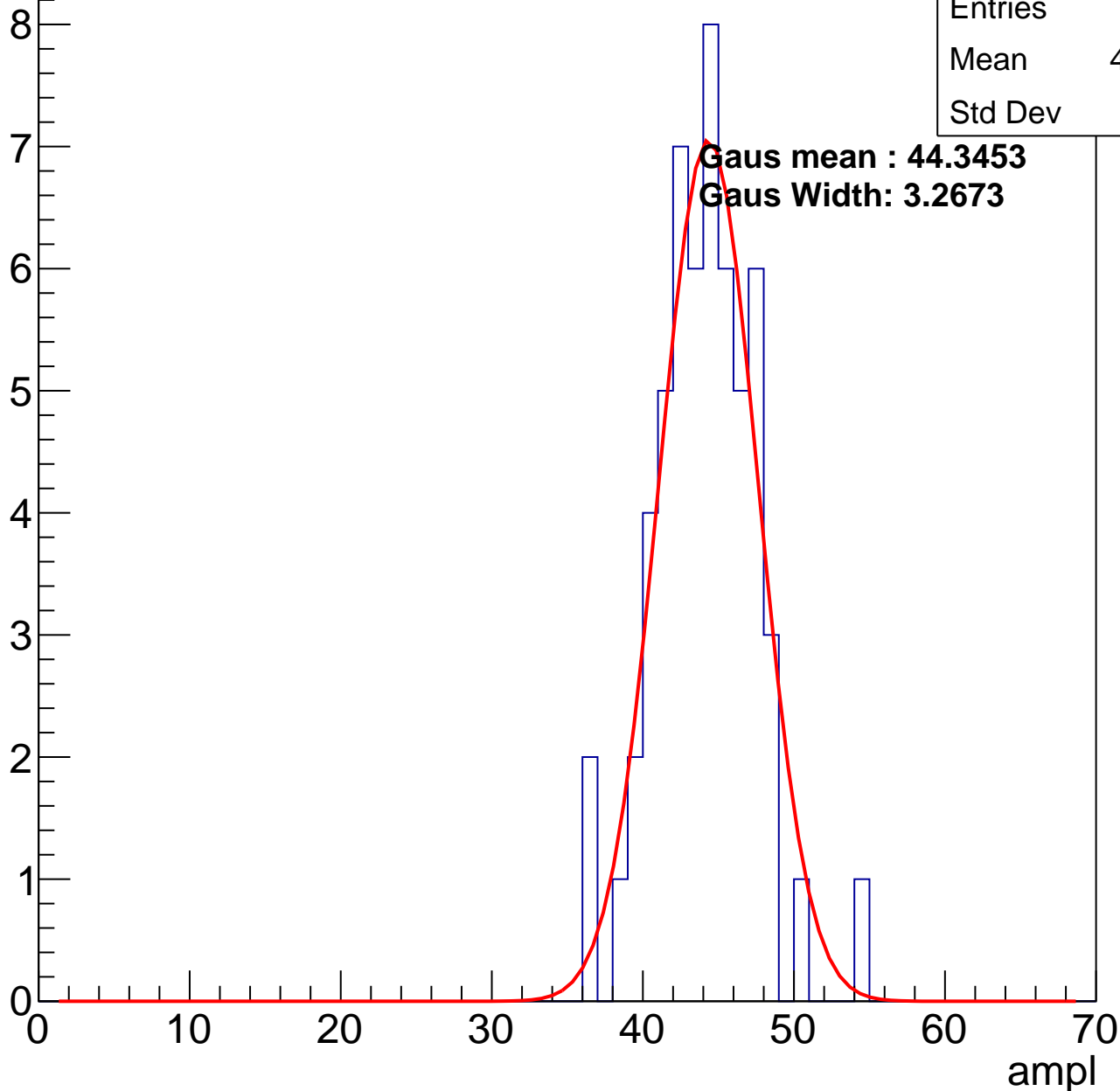
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.63
Std Dev	3.28

**Gaus mean : 44.3453**

**Gaus Width: 3.2673**

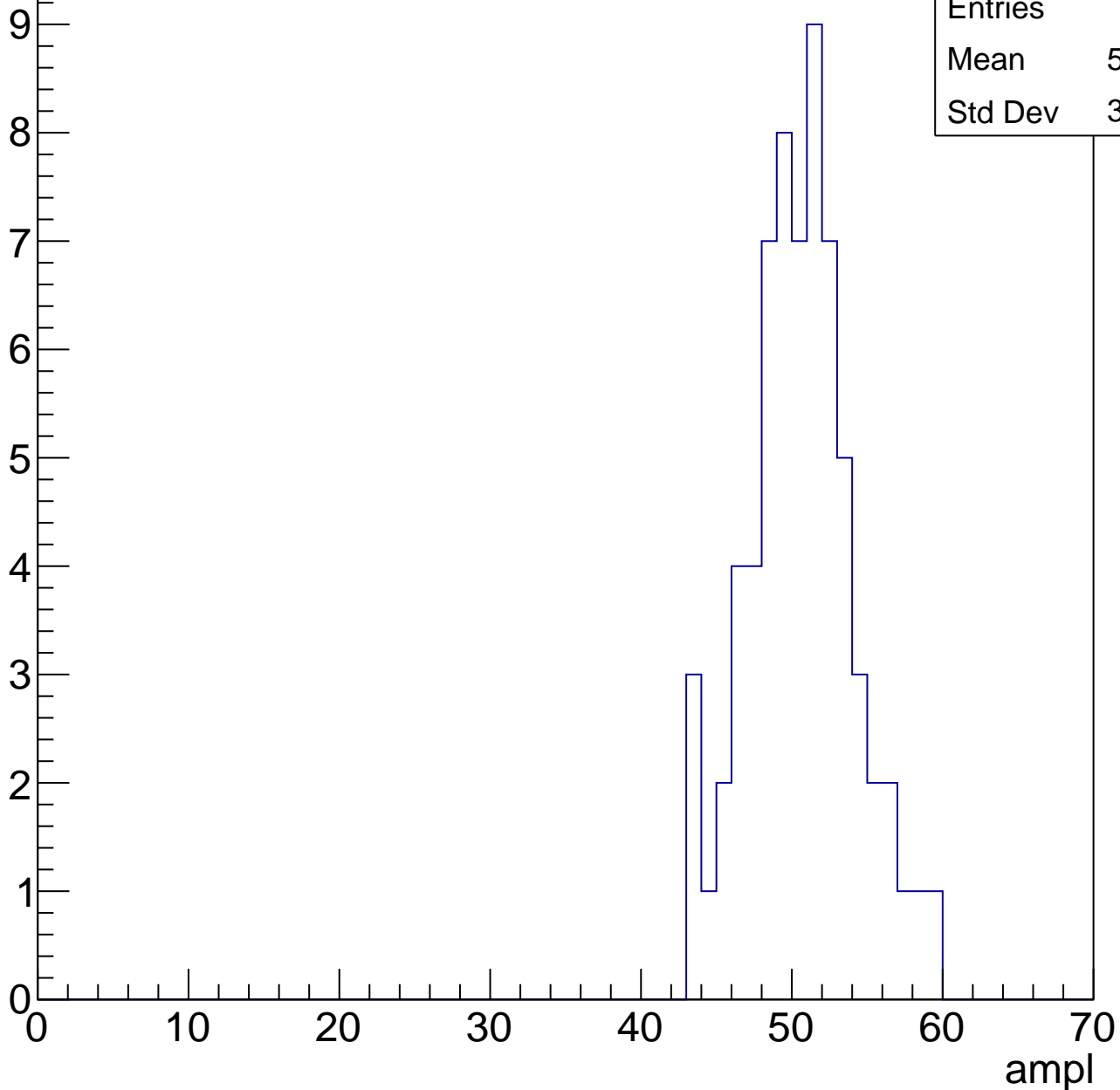


# B0L002S, U23-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

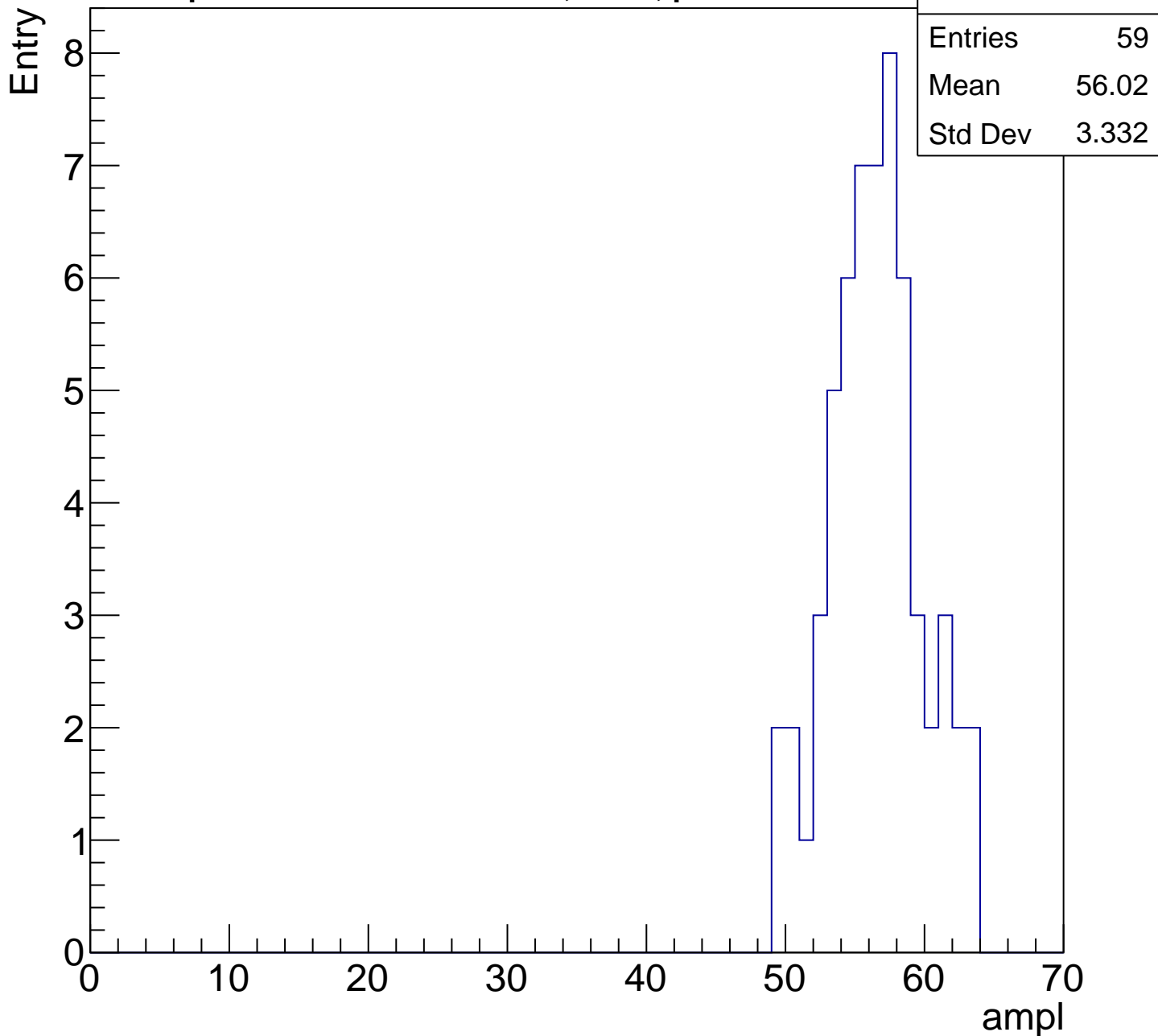
Entry

Entries	67
Mean	50.13
Std Dev	3.485



# B0L002S, U23-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

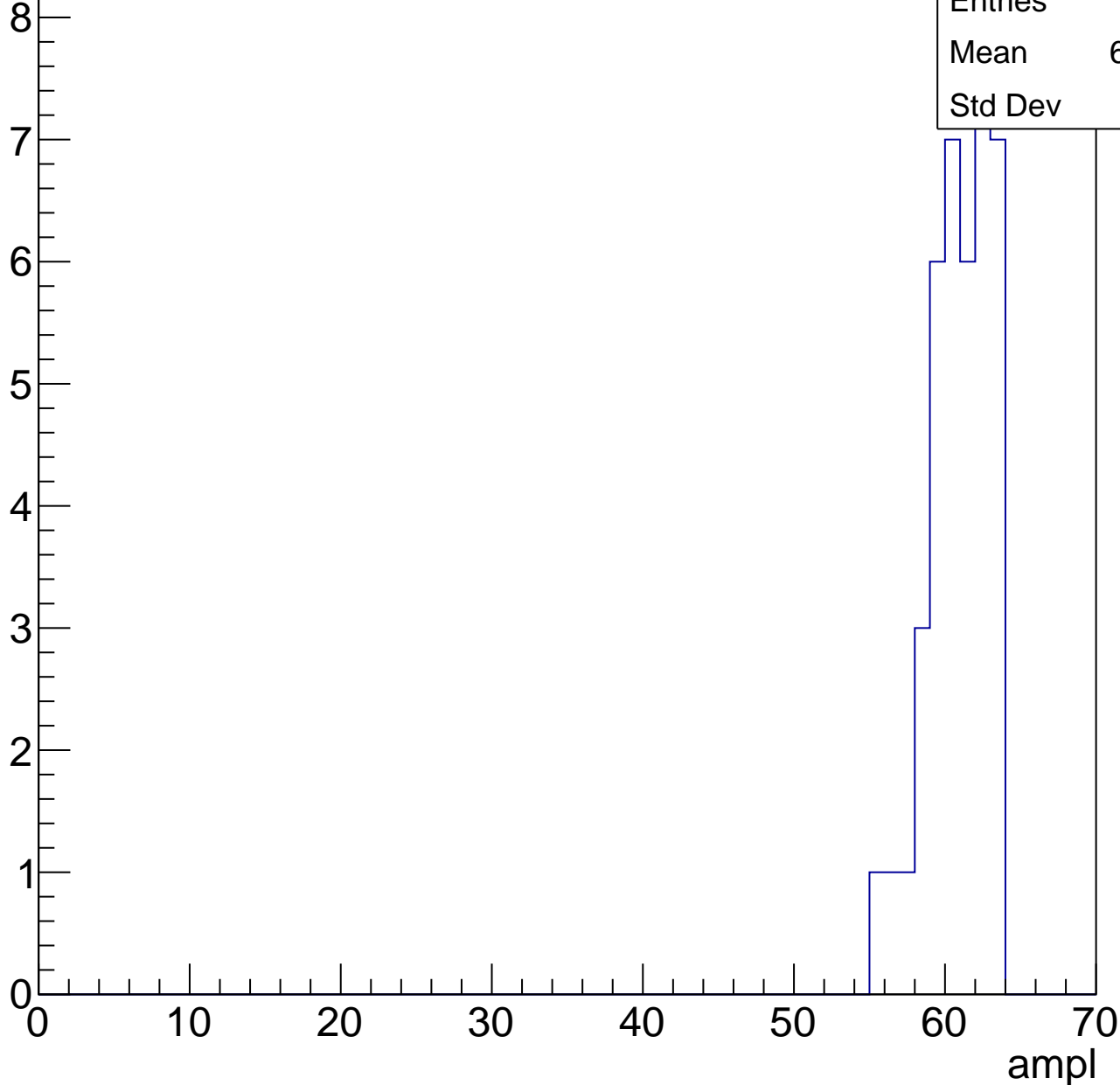


# B0L002S, U23-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

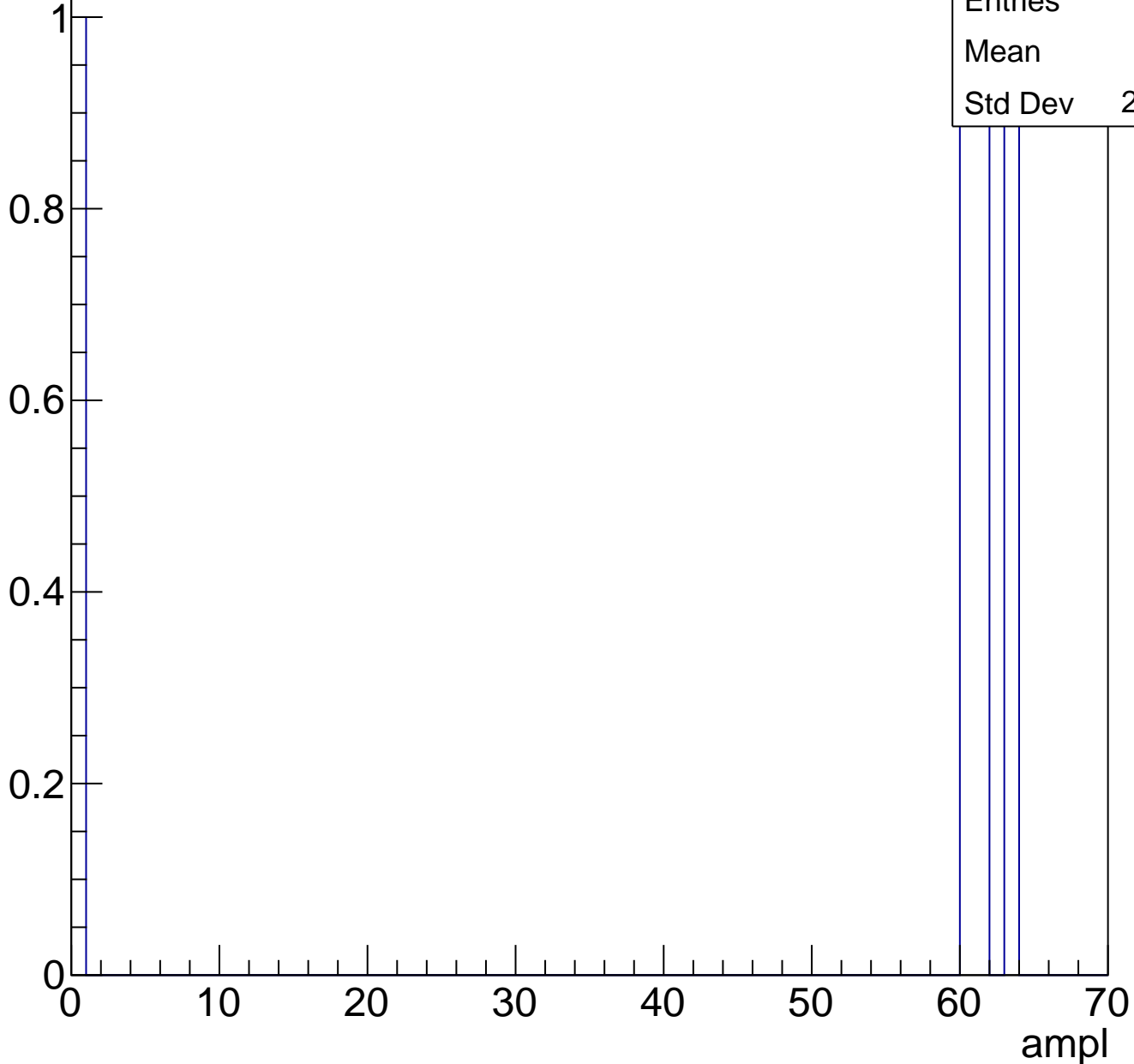
Entries	40
Mean	60.48
Std Dev	2



# B0L002S, U23-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch17, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	94
Mean	32.27
Std Dev	3.893

**Gaus mean : 33.0738**

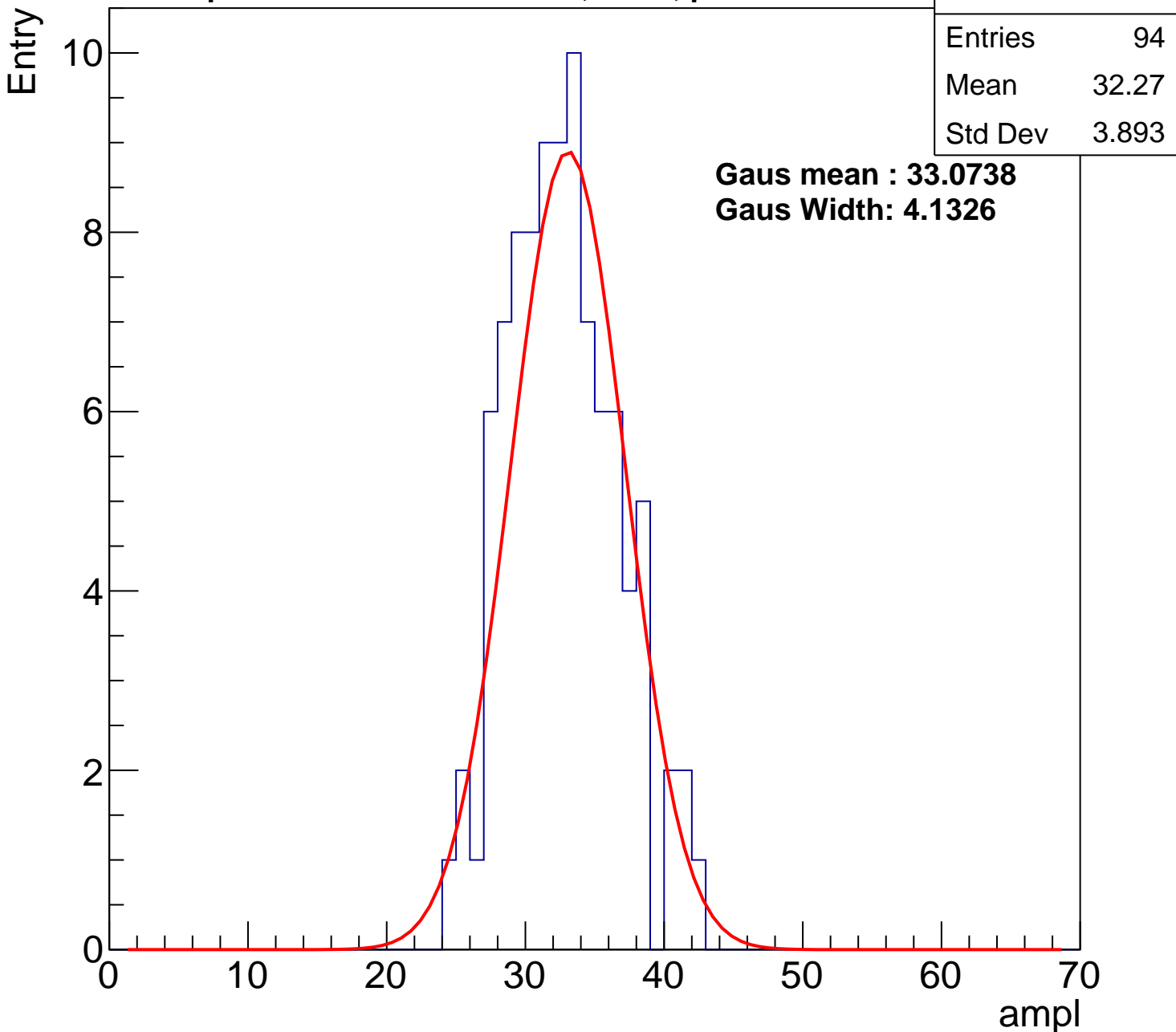
**Gaus Width: 4.1326**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch17, adc1

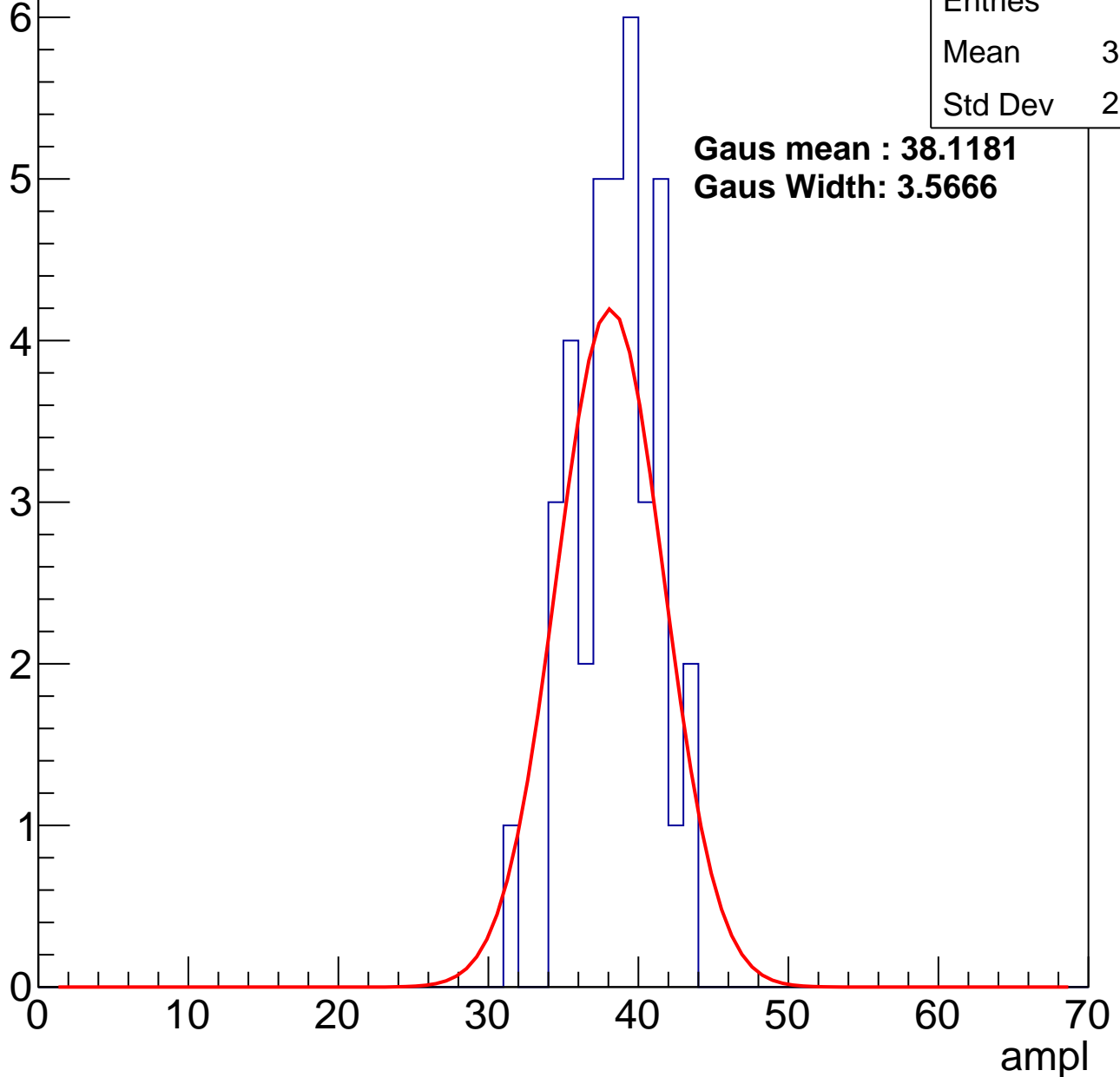
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	38.03
Std Dev	2.726

**Gaus mean : 38.1181**

**Gaus Width: 3.5666**



# B0L002S, U23-ch17, adc2

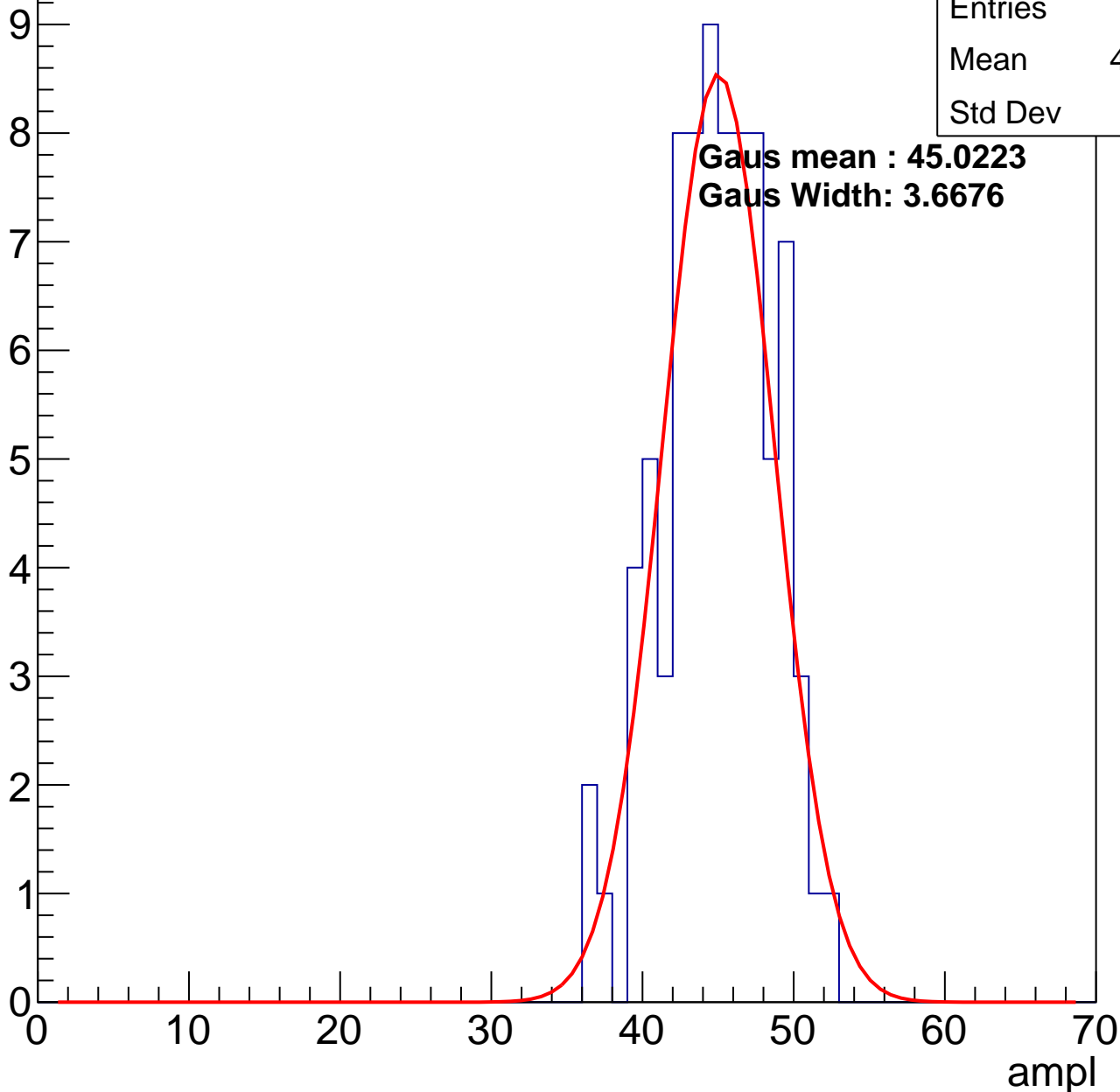
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	44.49
Std Dev	3.5

**Gaus mean : 45.0223**

**Gaus Width: 3.6676**

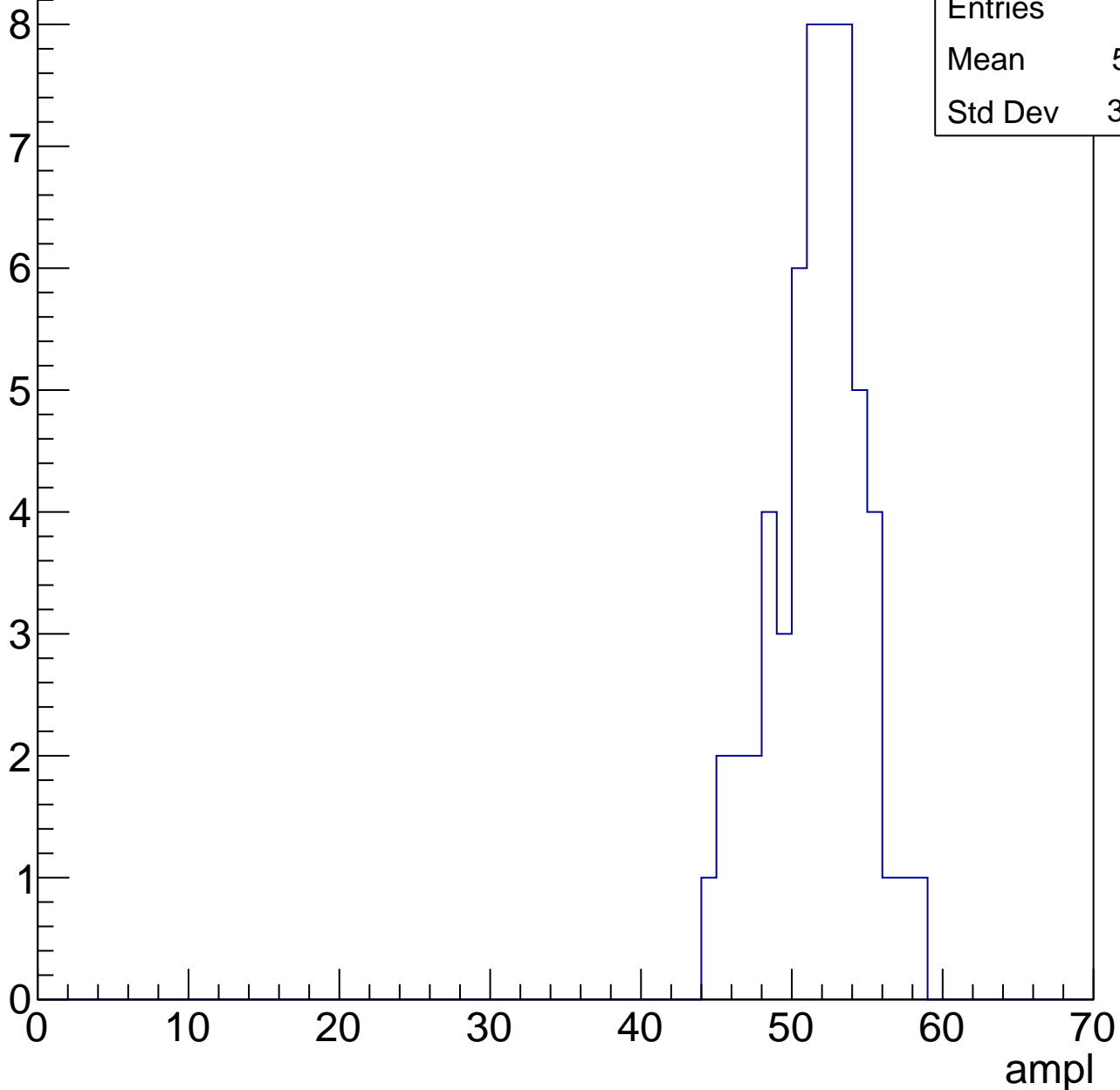


# B0L002S, U23-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

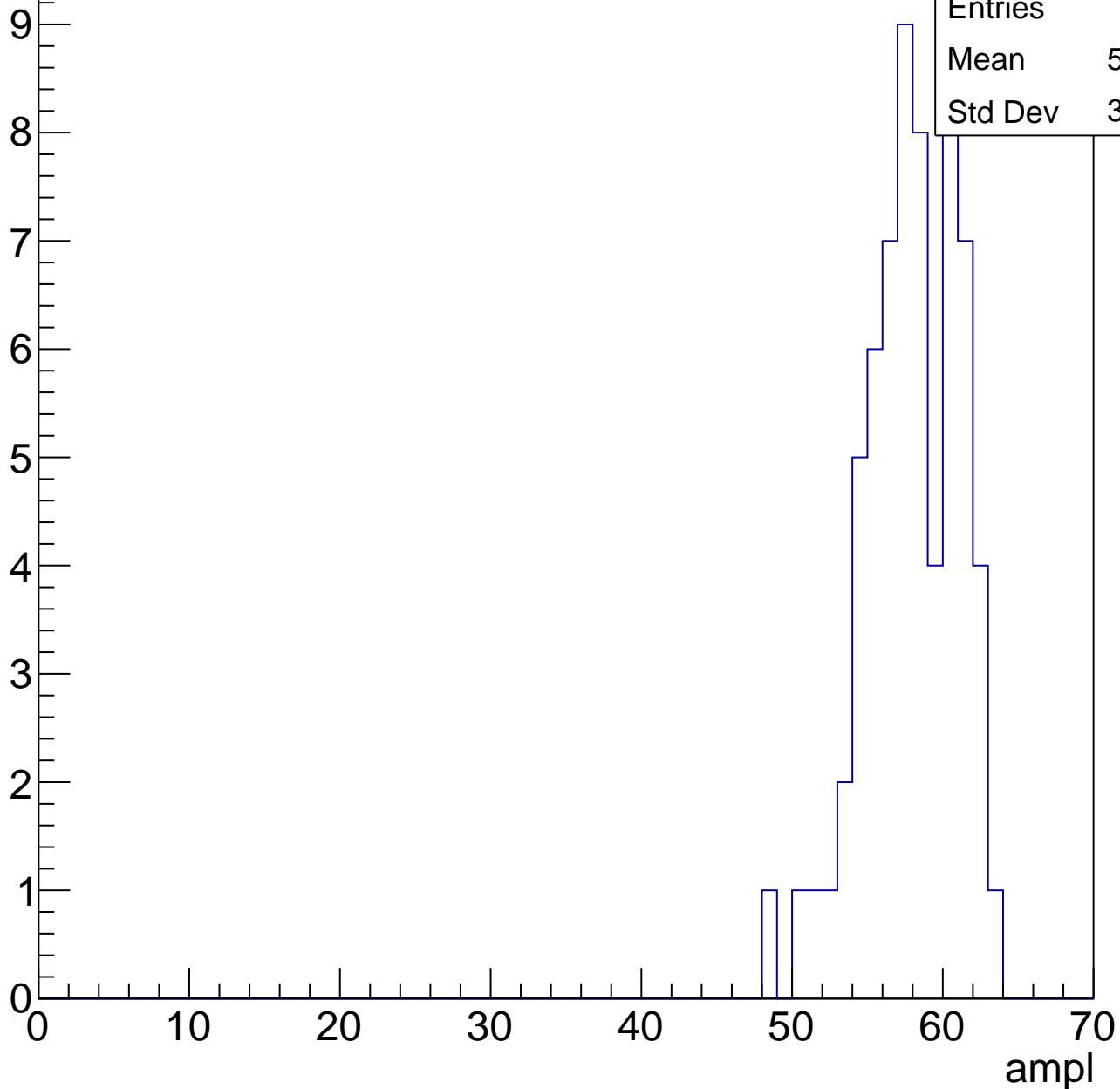
Entries	56
Mean	51.21
Std Dev	3.028



# B0L002S, U23-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



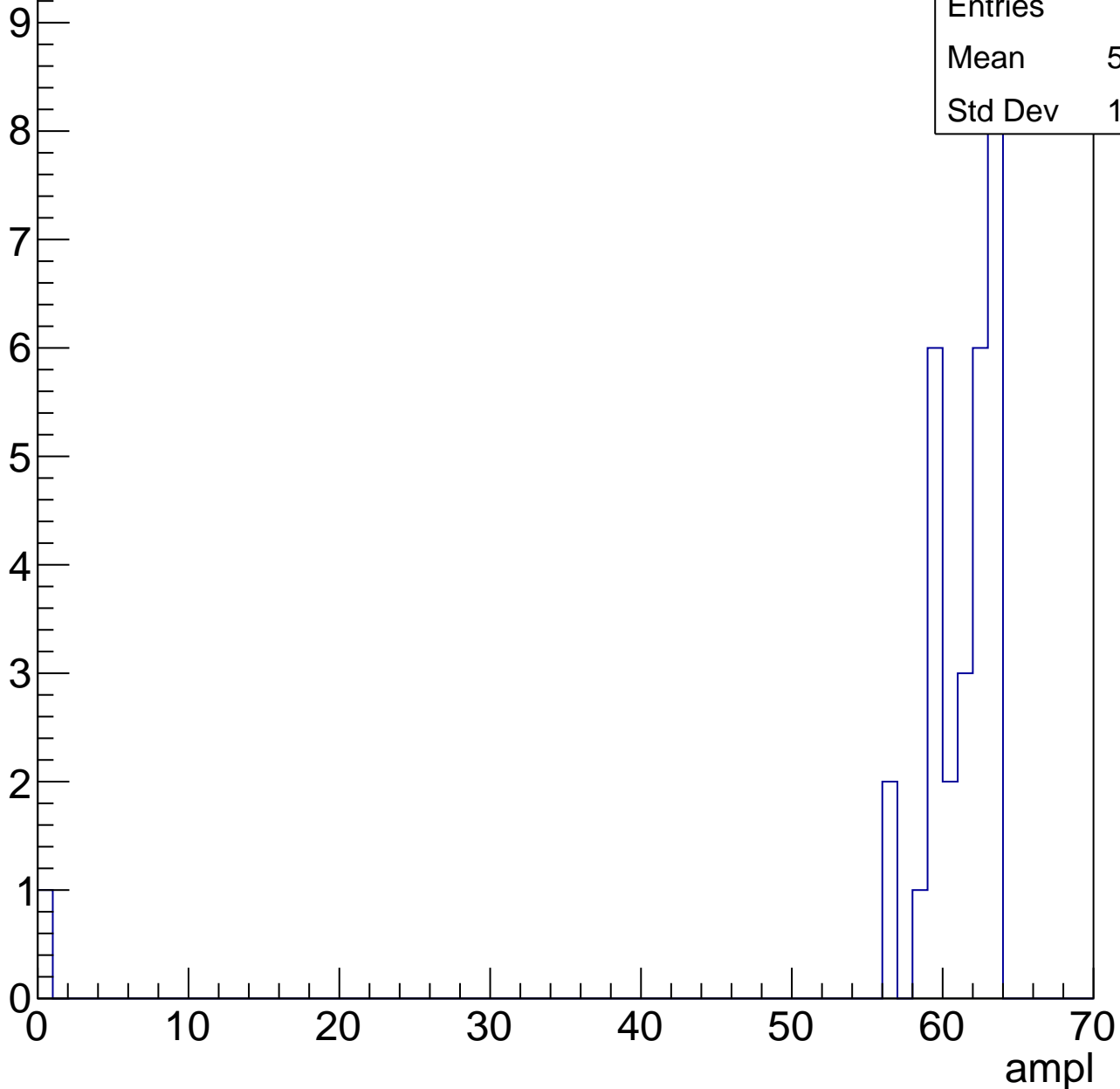
Entries	65
Mean	57.38
Std Dev	3.122

# B0L002S, U23-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.87
Std Dev	11.12



# B0L002S, U23-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch18, adc0

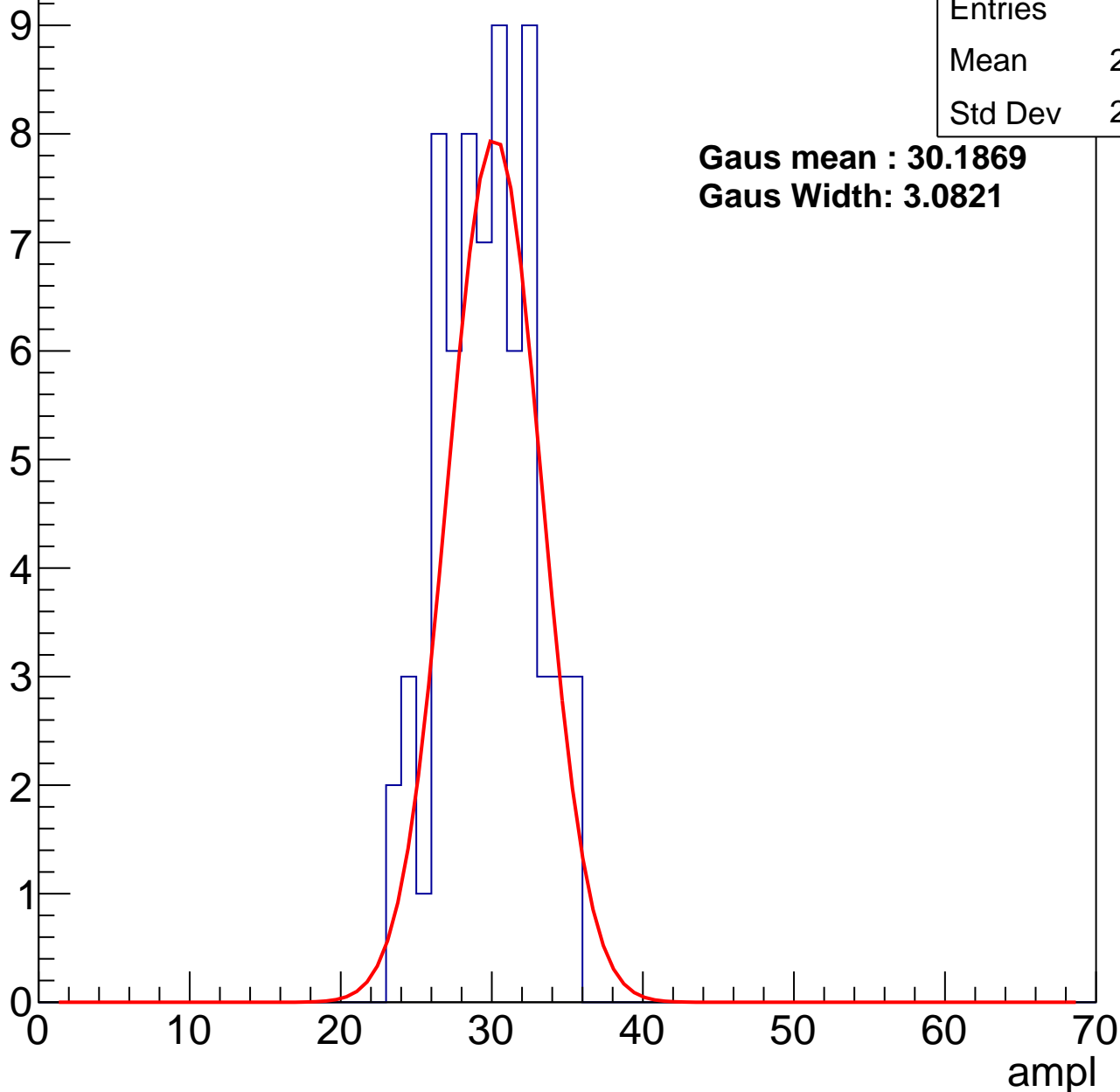
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.26
Std Dev	2.988

**Gaus mean : 30.1869**

**Gaus Width: 3.0821**

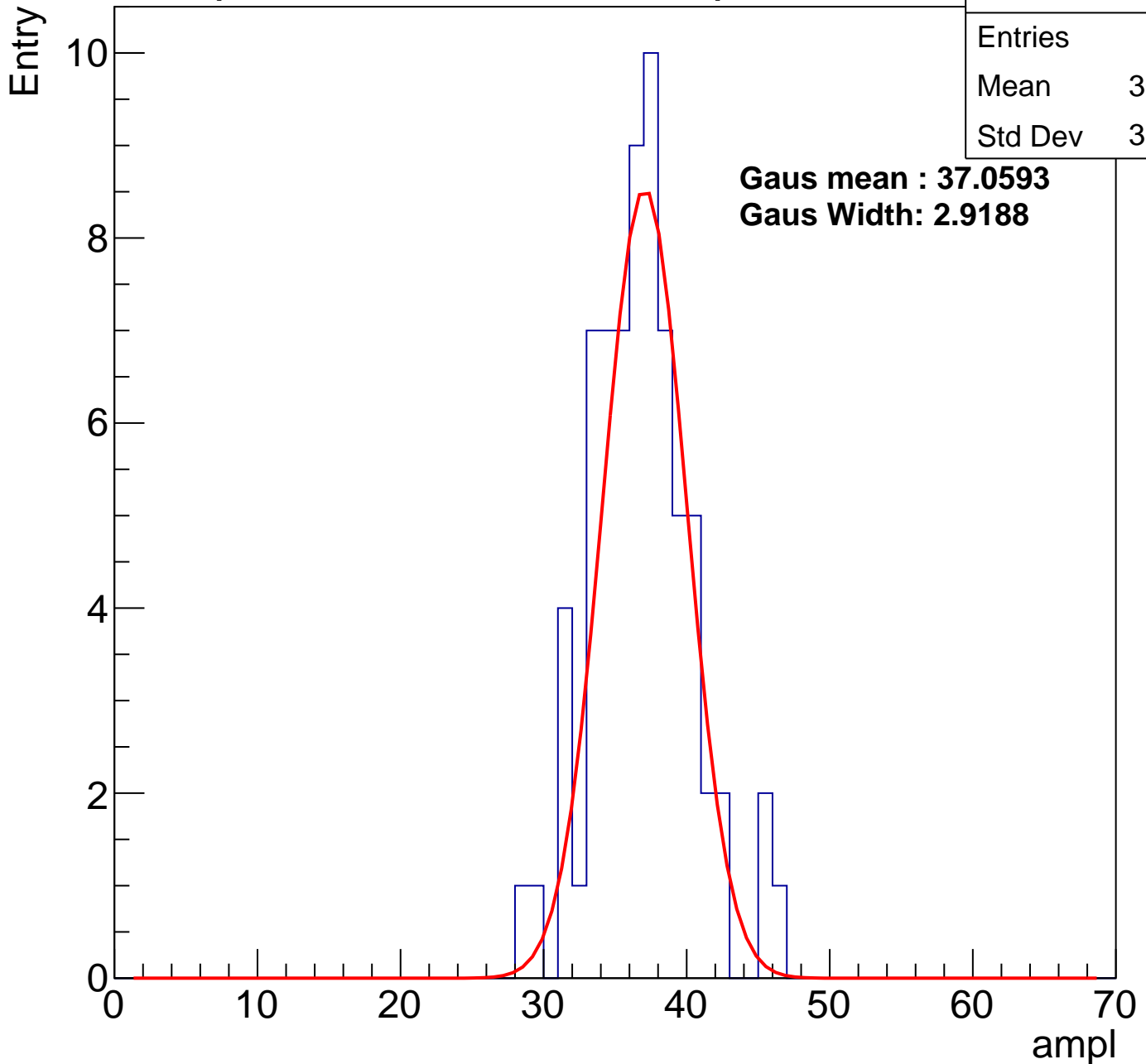


# B0L002S, U23-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	71
Mean	36.39
Std Dev	3.486

**Gaus mean : 37.0593**  
**Gaus Width: 2.9188**



# B0L002S, U23-ch18, adc2

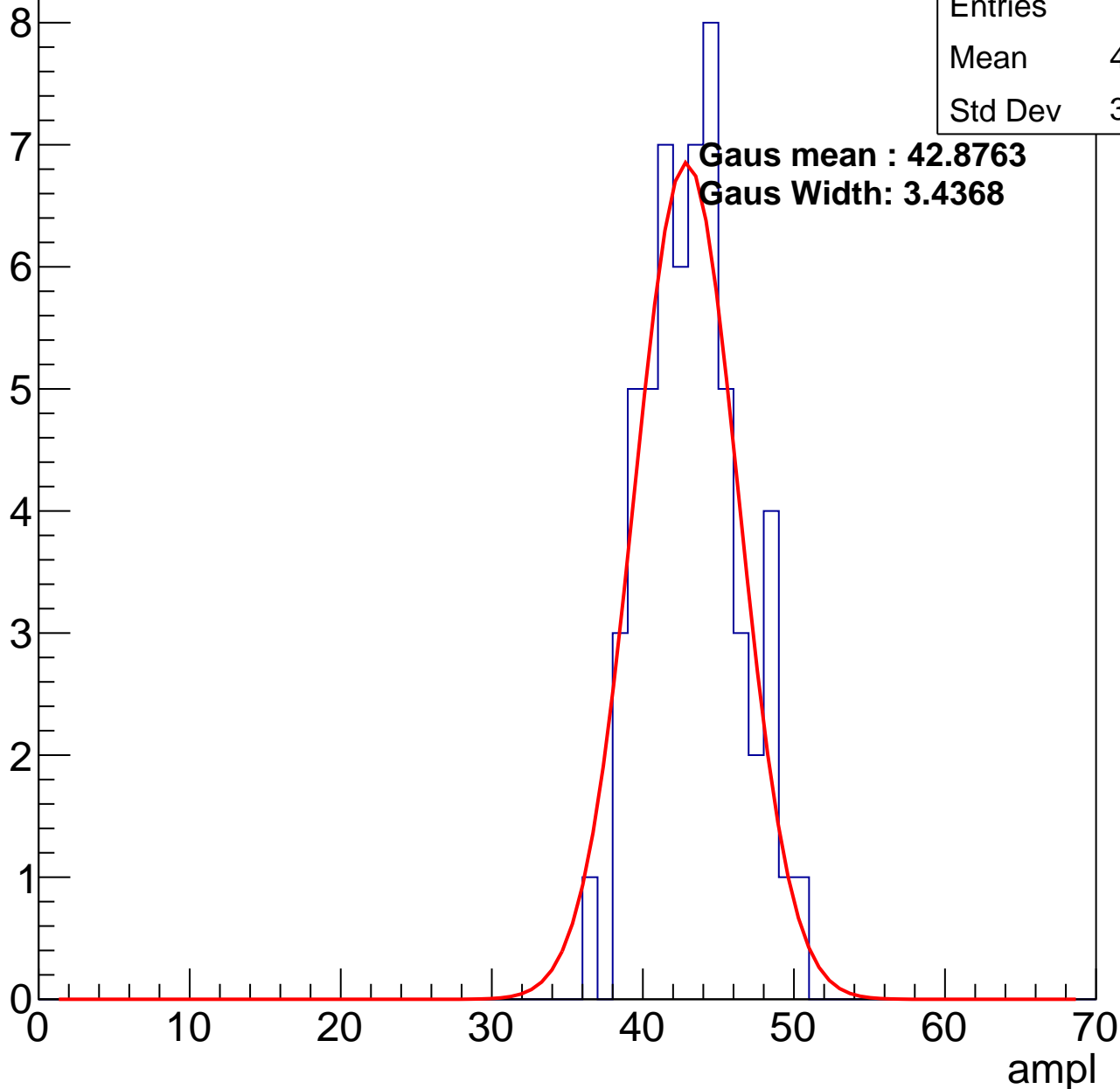
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.84
Std Dev	3.089

**Gaus mean : 42.8763**

**Gaus Width: 3.4368**

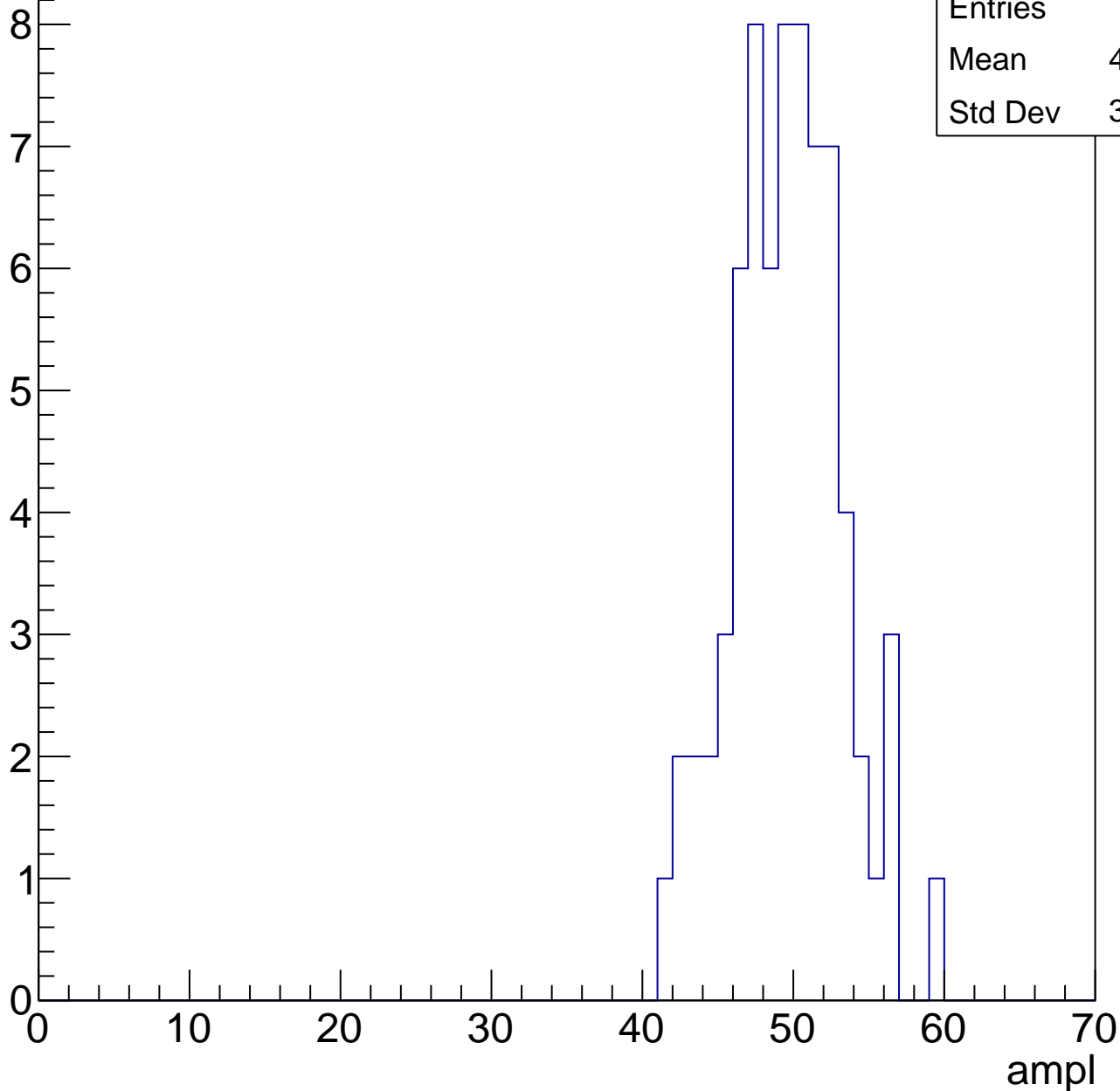


# B0L002S, U23-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	49.14
Std Dev	3.597

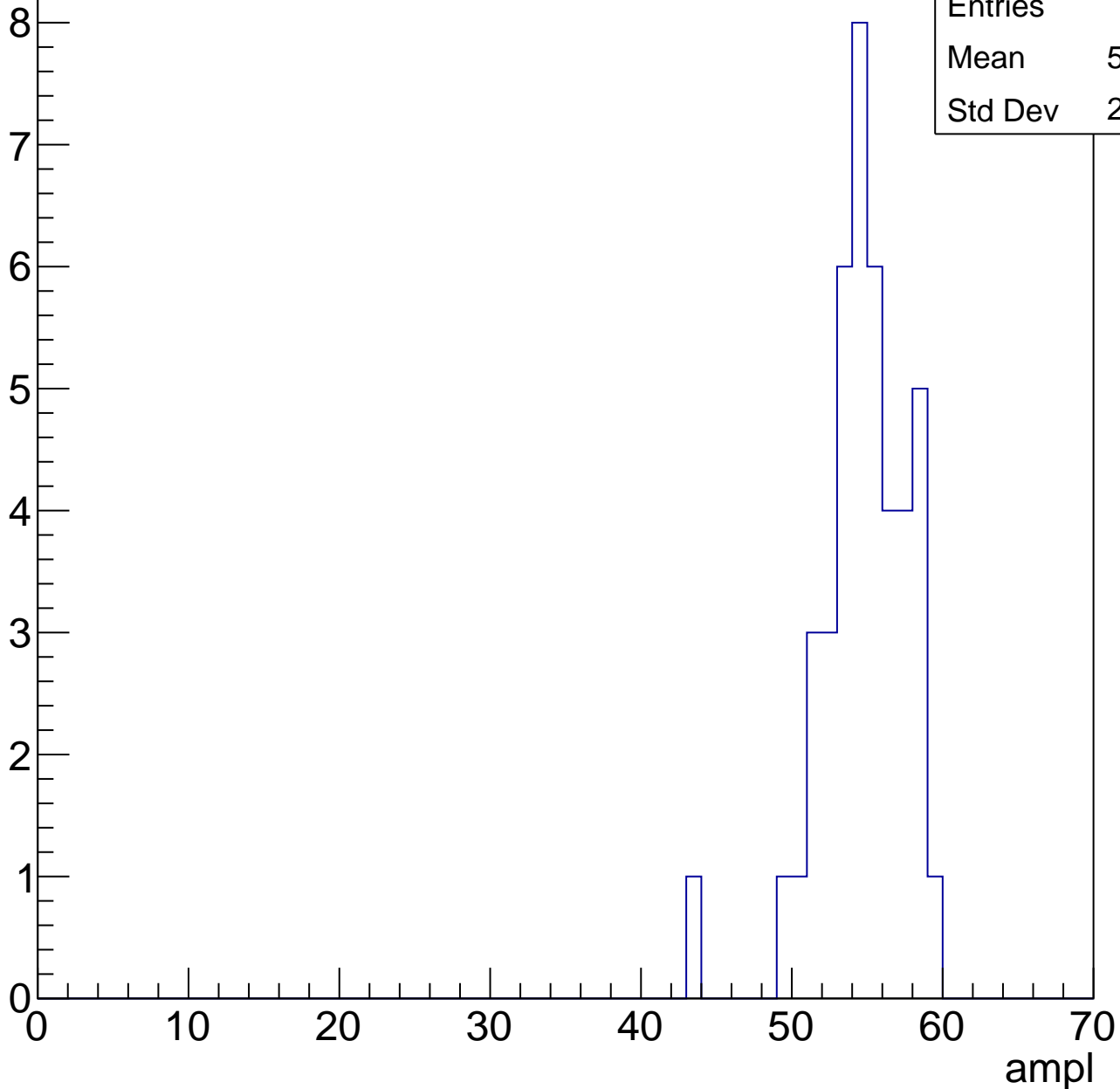


# B0L002S, U23-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	54.23
Std Dev	2.924



# B0L002S, U23-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	59
Mean	59.75
Std Dev	2.304

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

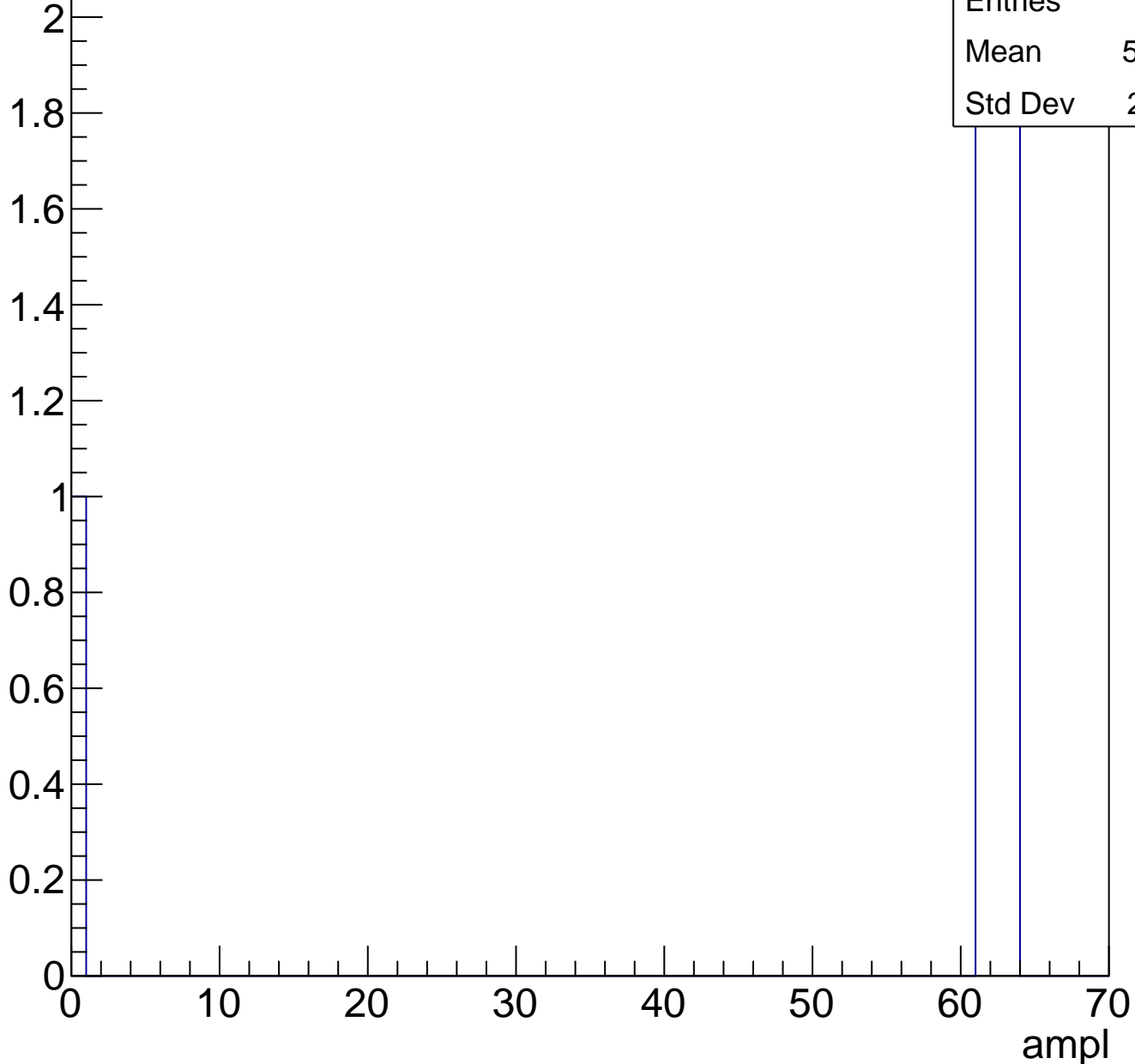
8

10

# B0L002S, U23-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L002S, U23-ch19, adc0

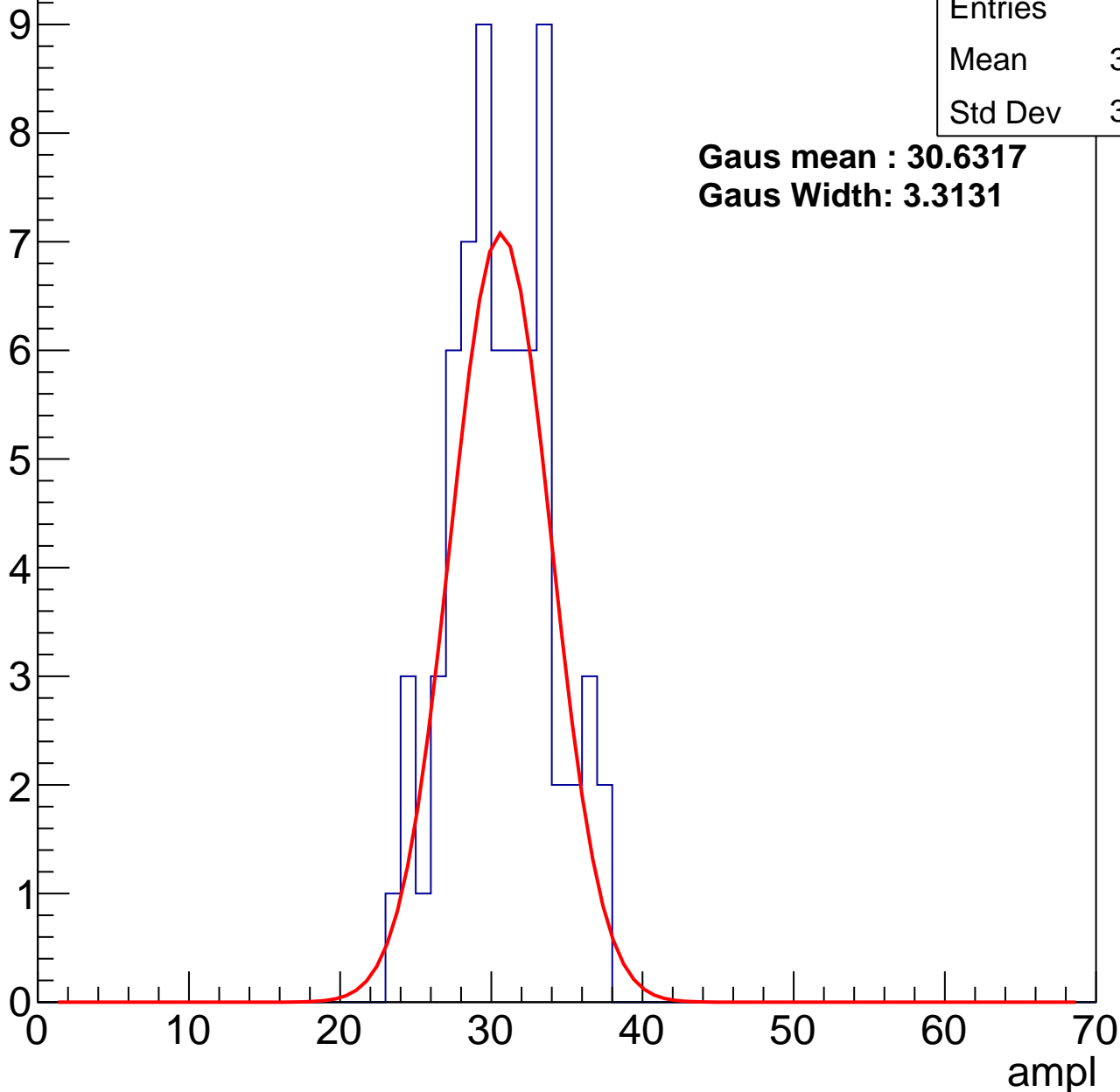
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.18
Std Dev	3.298

**Gaus mean : 30.6317**

**Gaus Width: 3.3131**



# B0L002S, U23-ch19, adc1

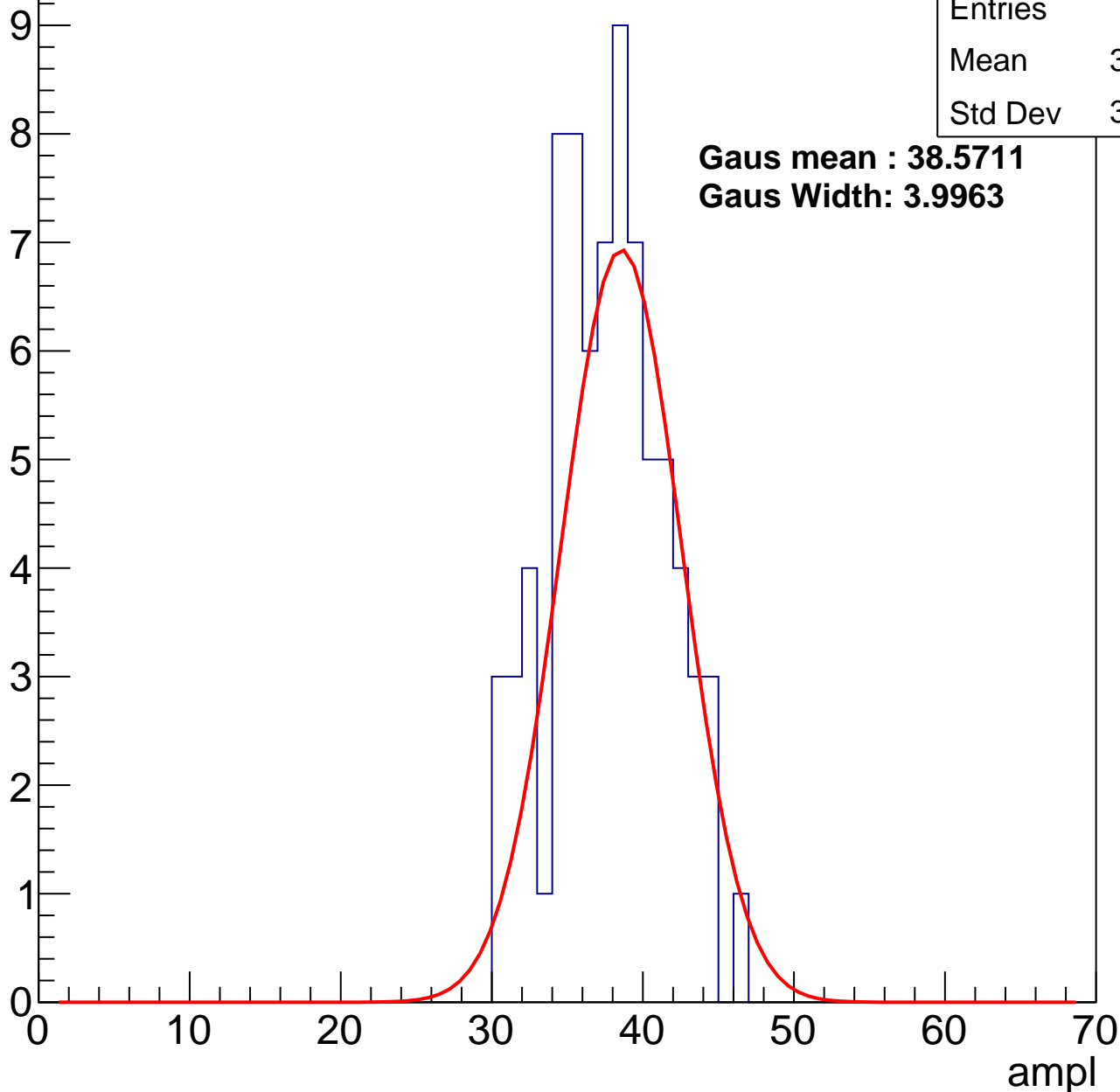
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	37.22
Std Dev	3.737

**Gaus mean : 38.5711**

**Gaus Width: 3.9963**



# B0L002S, U23-ch19, adc2

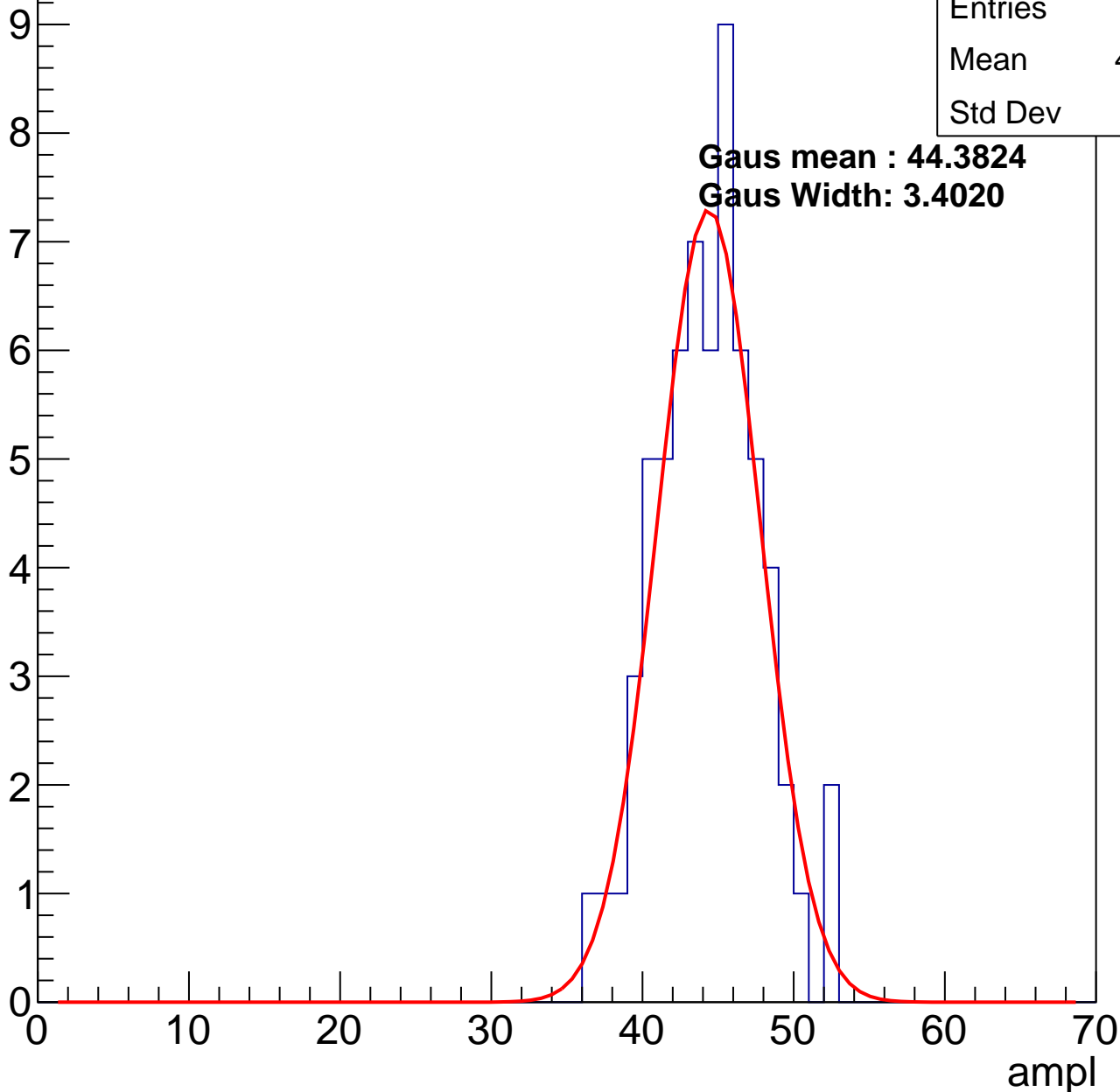
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.91
Std Dev	3.39

**Gaus mean : 44.3824**

**Gaus Width: 3.4020**



# B0L002S, U23-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

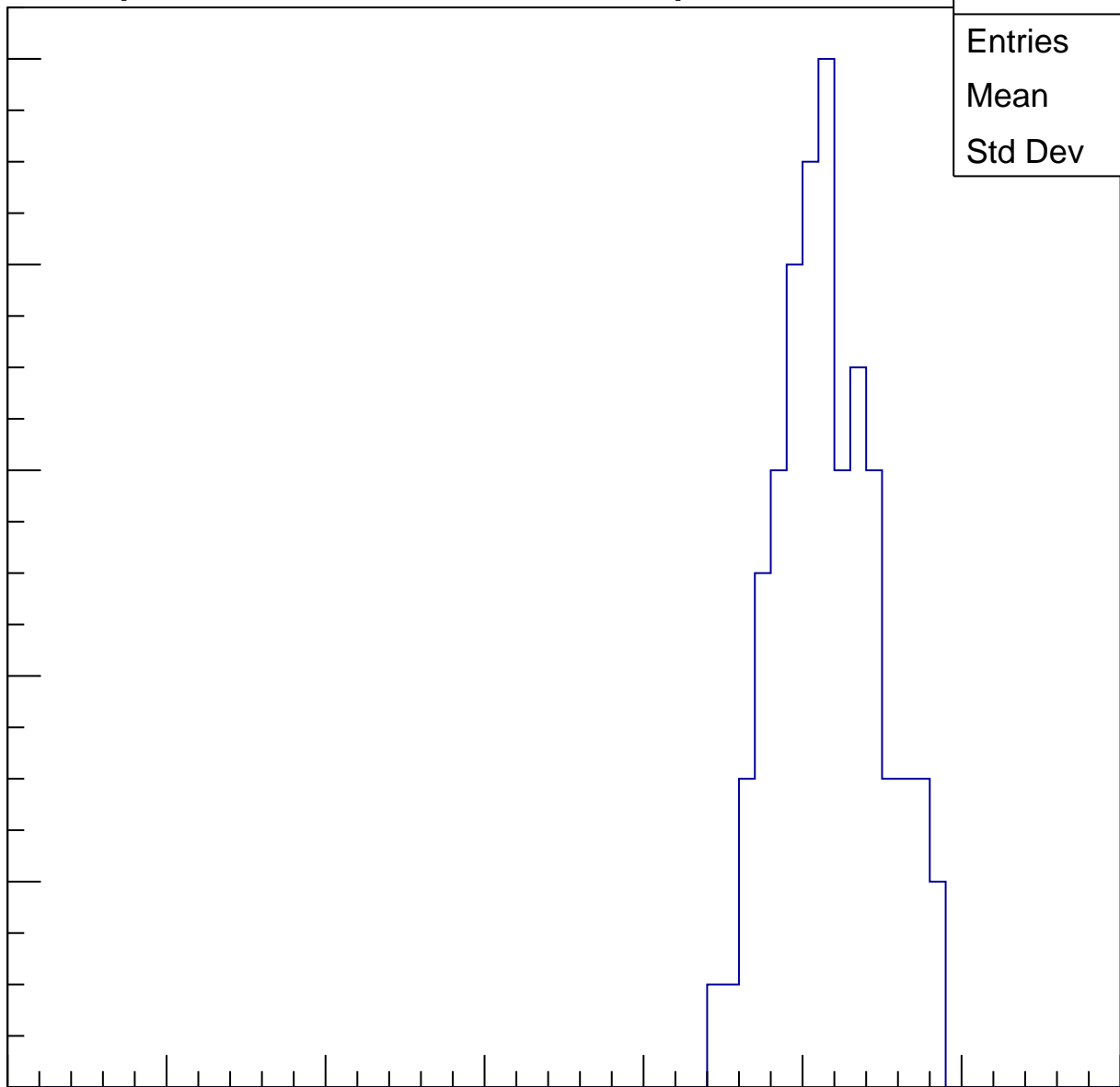
Entries	73
Mean	51.08
Std Dev	3.209

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

6

5

4

3

2

1

0

Entries	52
Mean	57.56
Std Dev	2.97

10

20

30

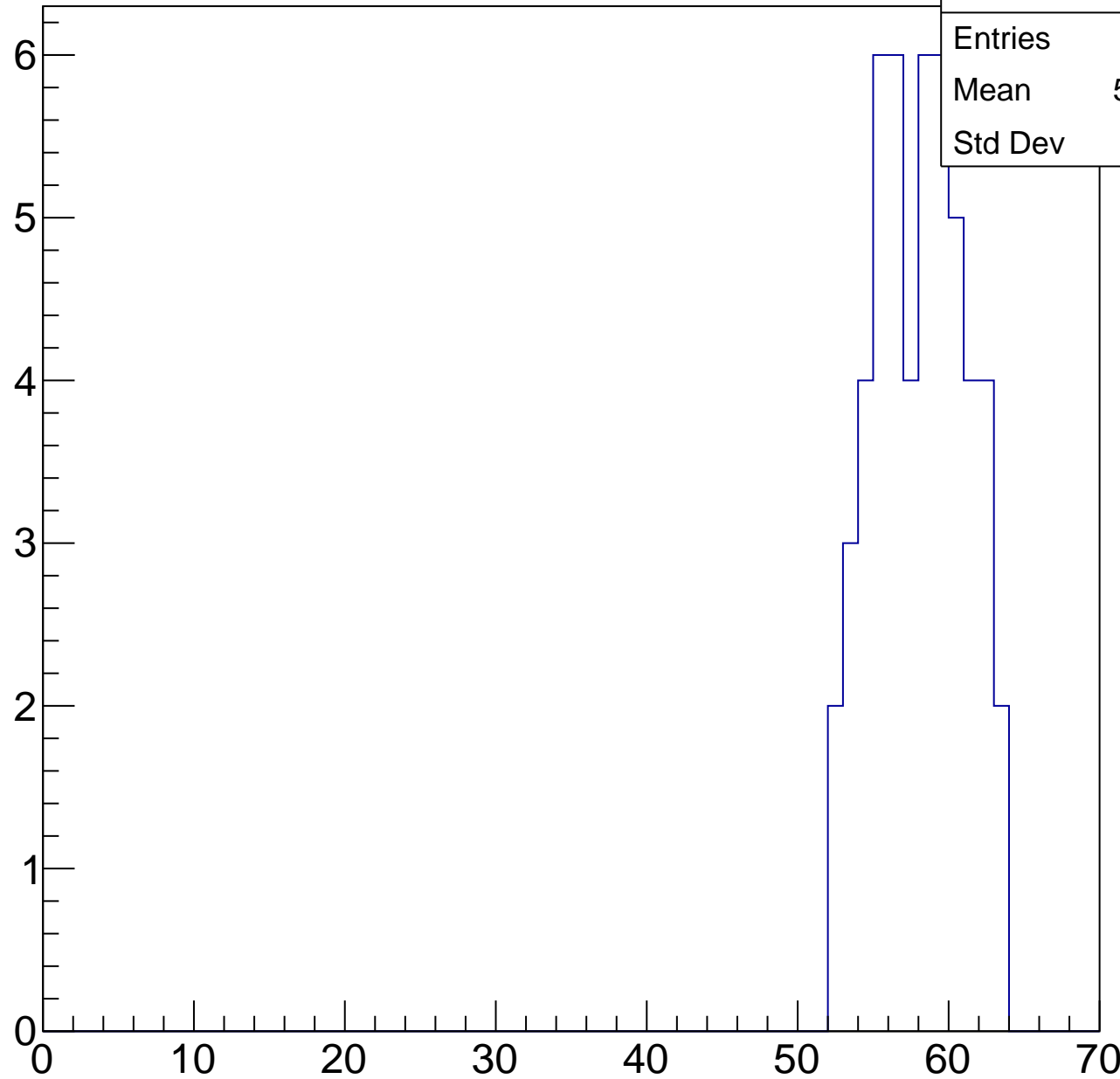
40

50

60

70

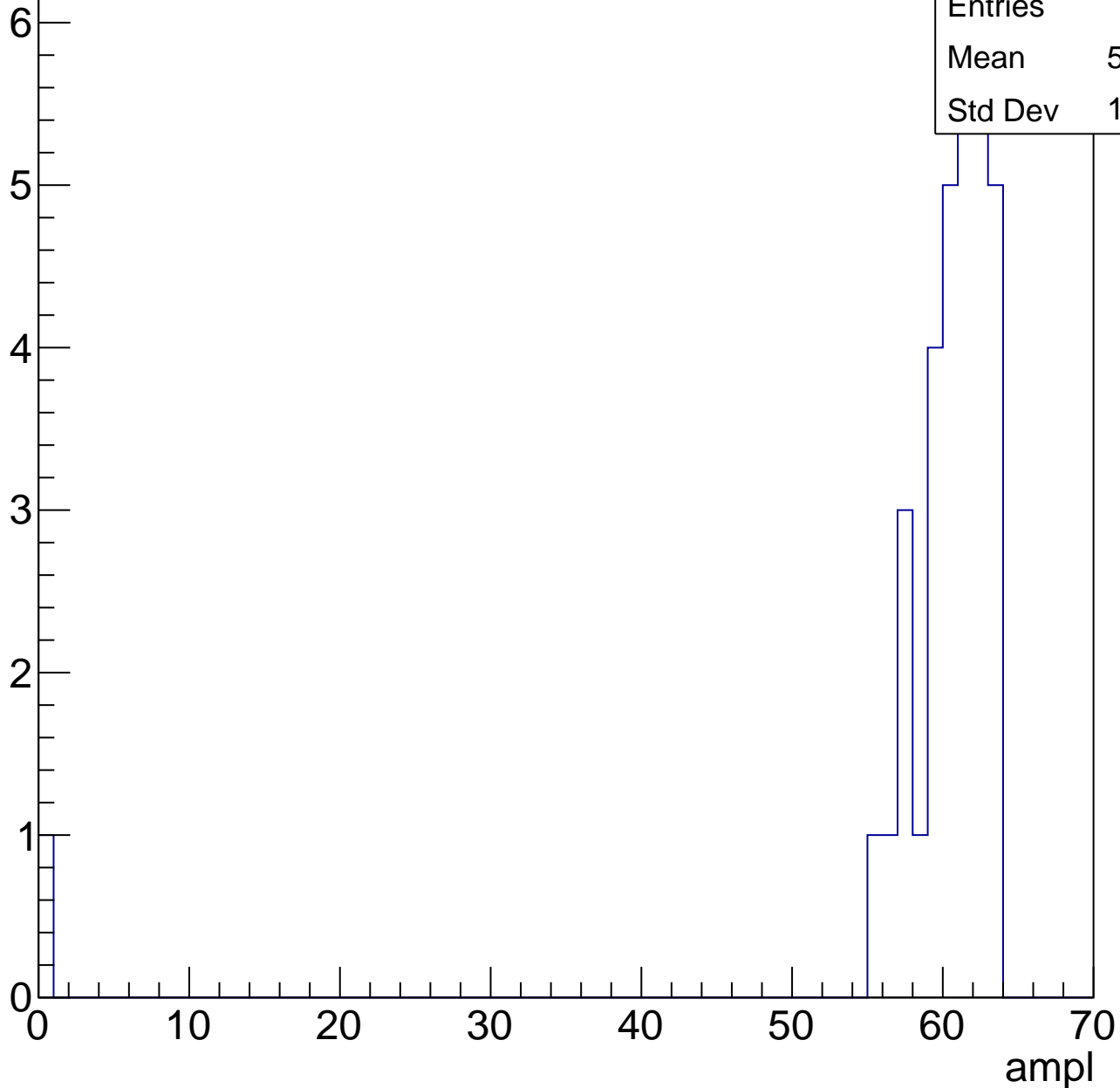
ampl



# B0L002S, U23-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

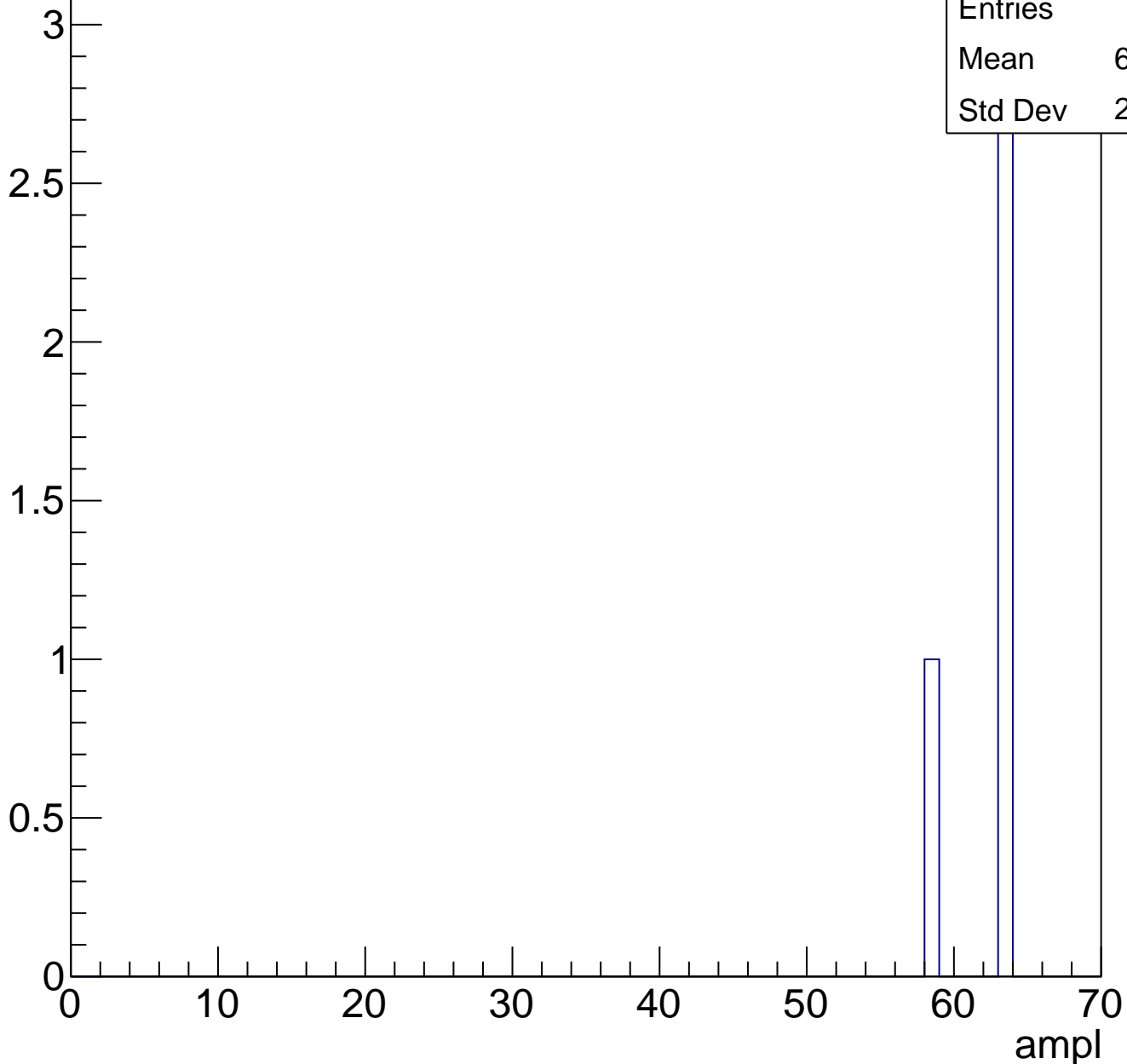
Entry



# B0L002S, U23-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	19
Std Dev	0

# B0L002S, U23-ch20, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	54
Mean	29.59
Std Dev	3.246

**Gaus mean : 30.2369**

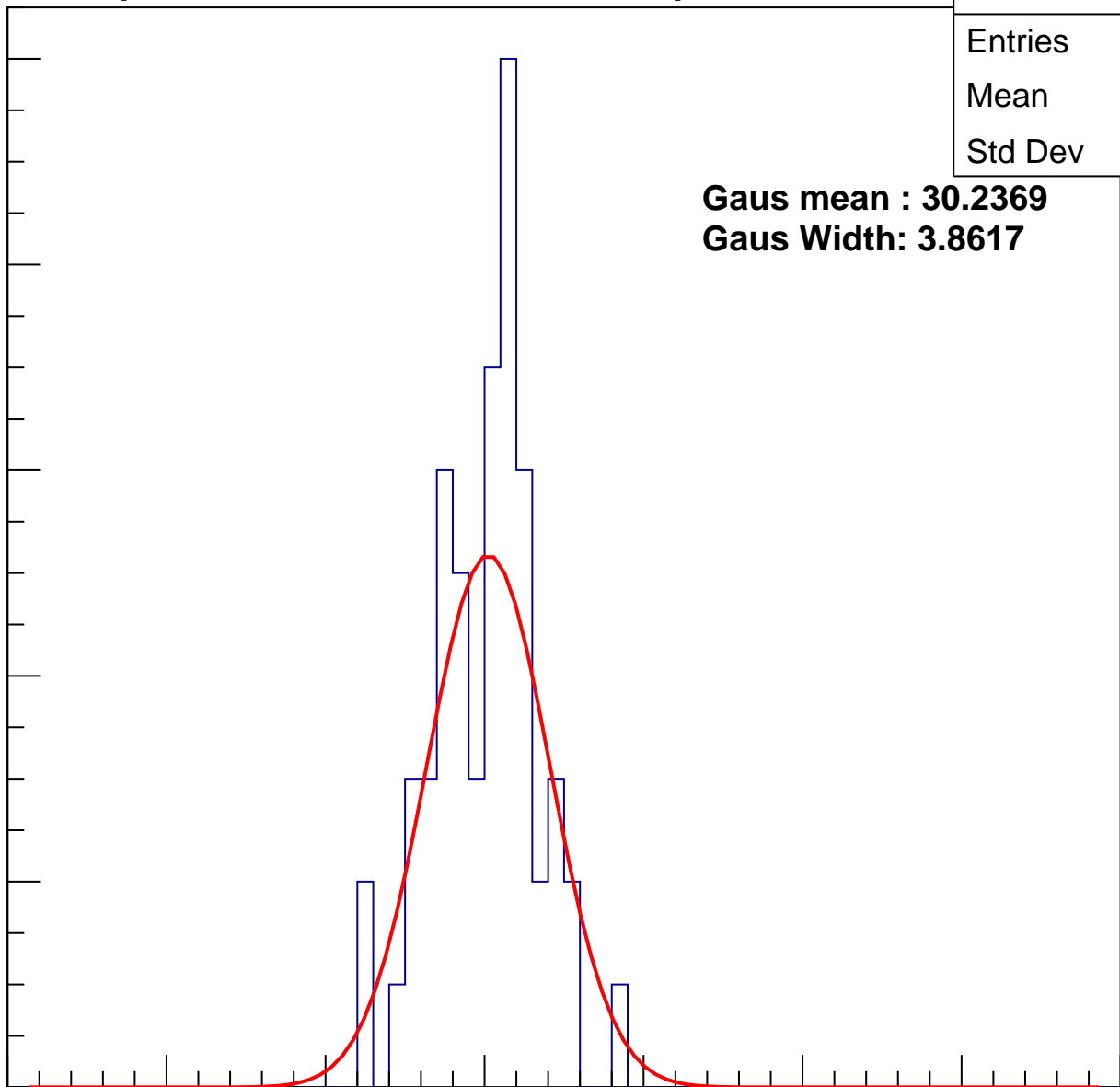
**Gaus Width: 3.8617**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch20, adc1

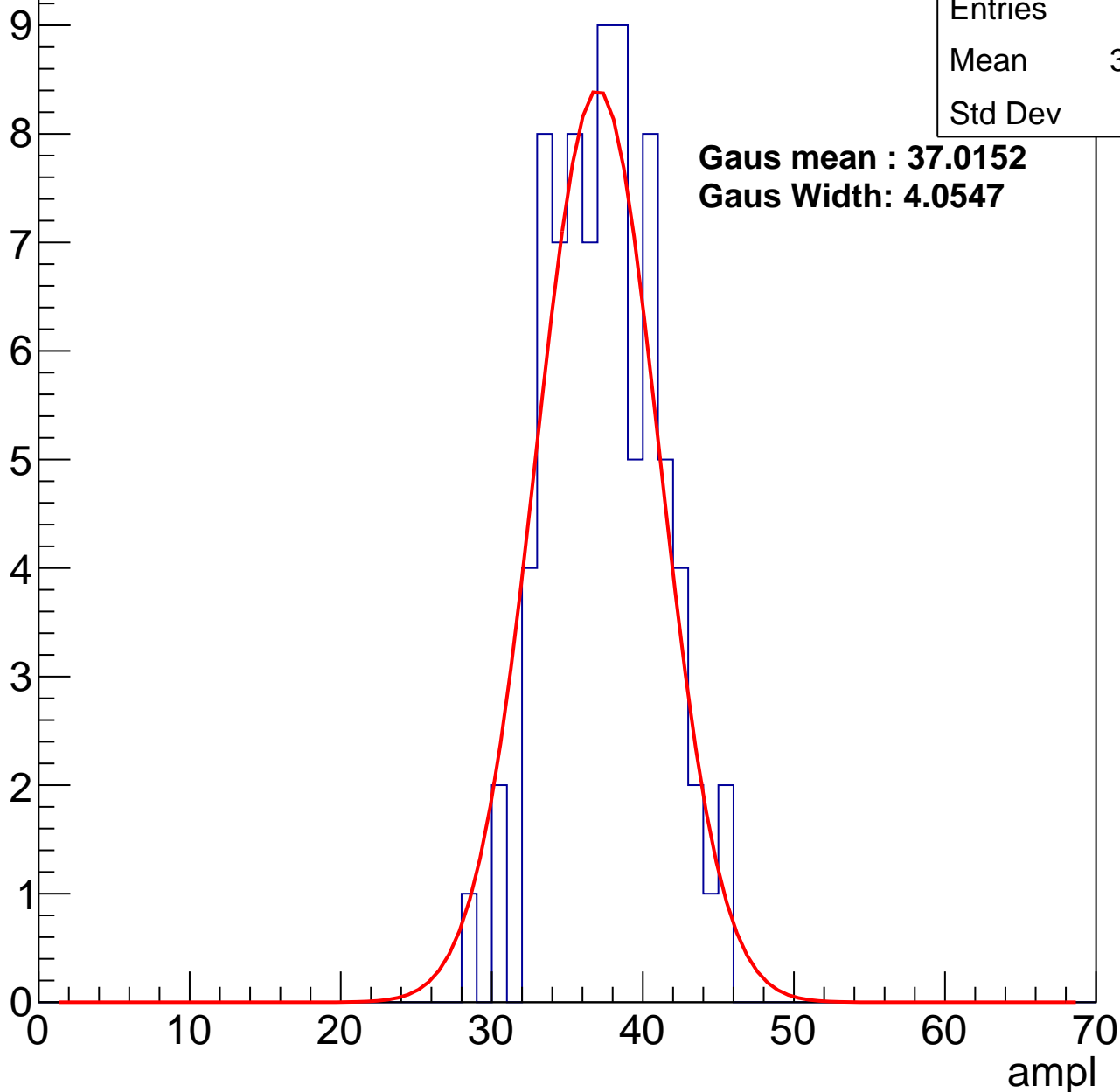
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	82
Mean	36.99
Std Dev	3.56

**Gaus mean : 37.0152**

**Gaus Width: 4.0547**



# B0L002S, U23-ch20, adc2

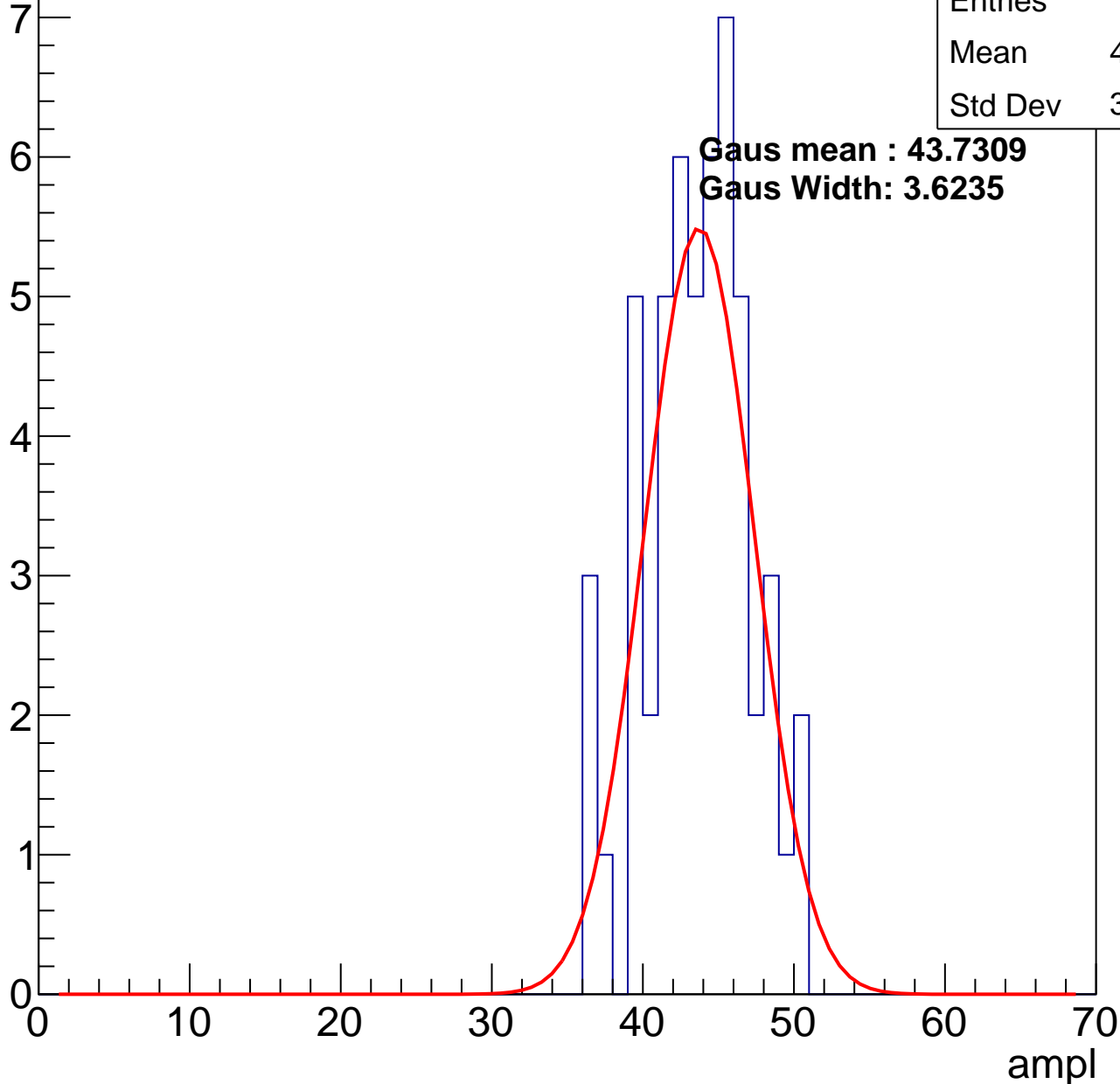
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.17
Std Dev	3.435

**Gaus mean : 43.7309**

**Gaus Width: 3.6235**



# B0L002S, U23-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

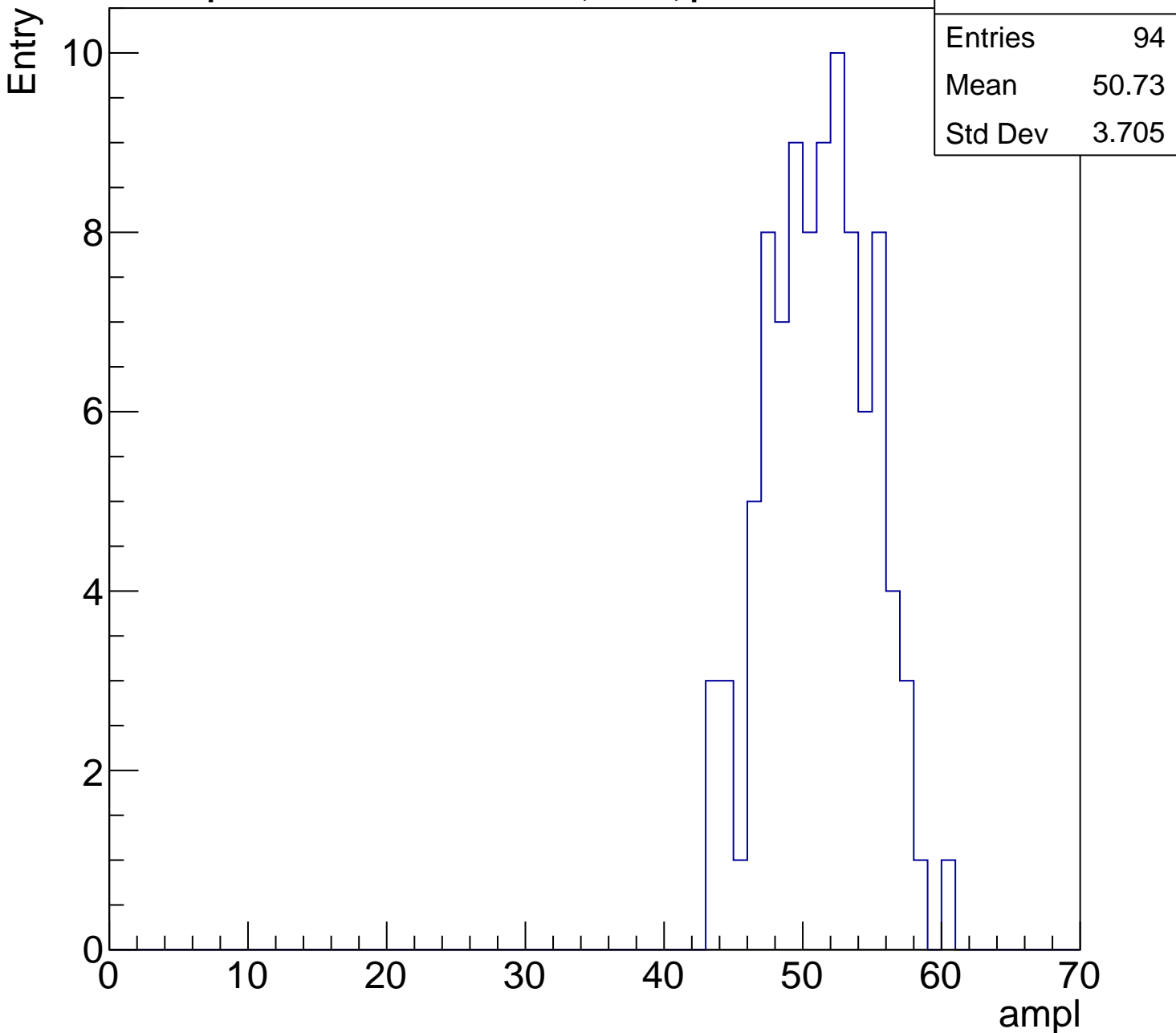
Entries	94
Mean	50.73
Std Dev	3.705

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

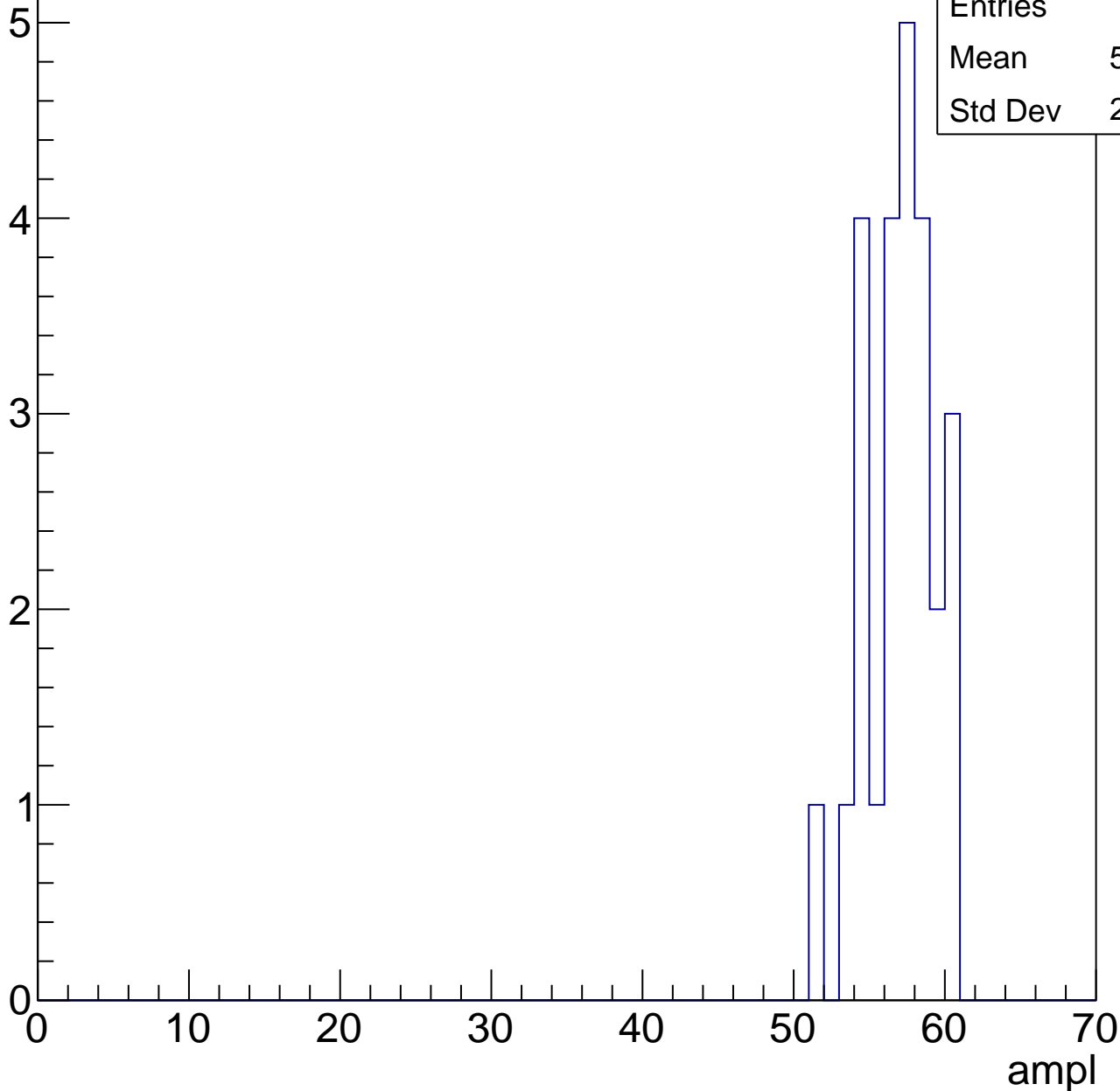


# B0L002S, U23-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	25
Mean	56.56
Std Dev	2.282



# B0L002S, U23-ch20, adc5

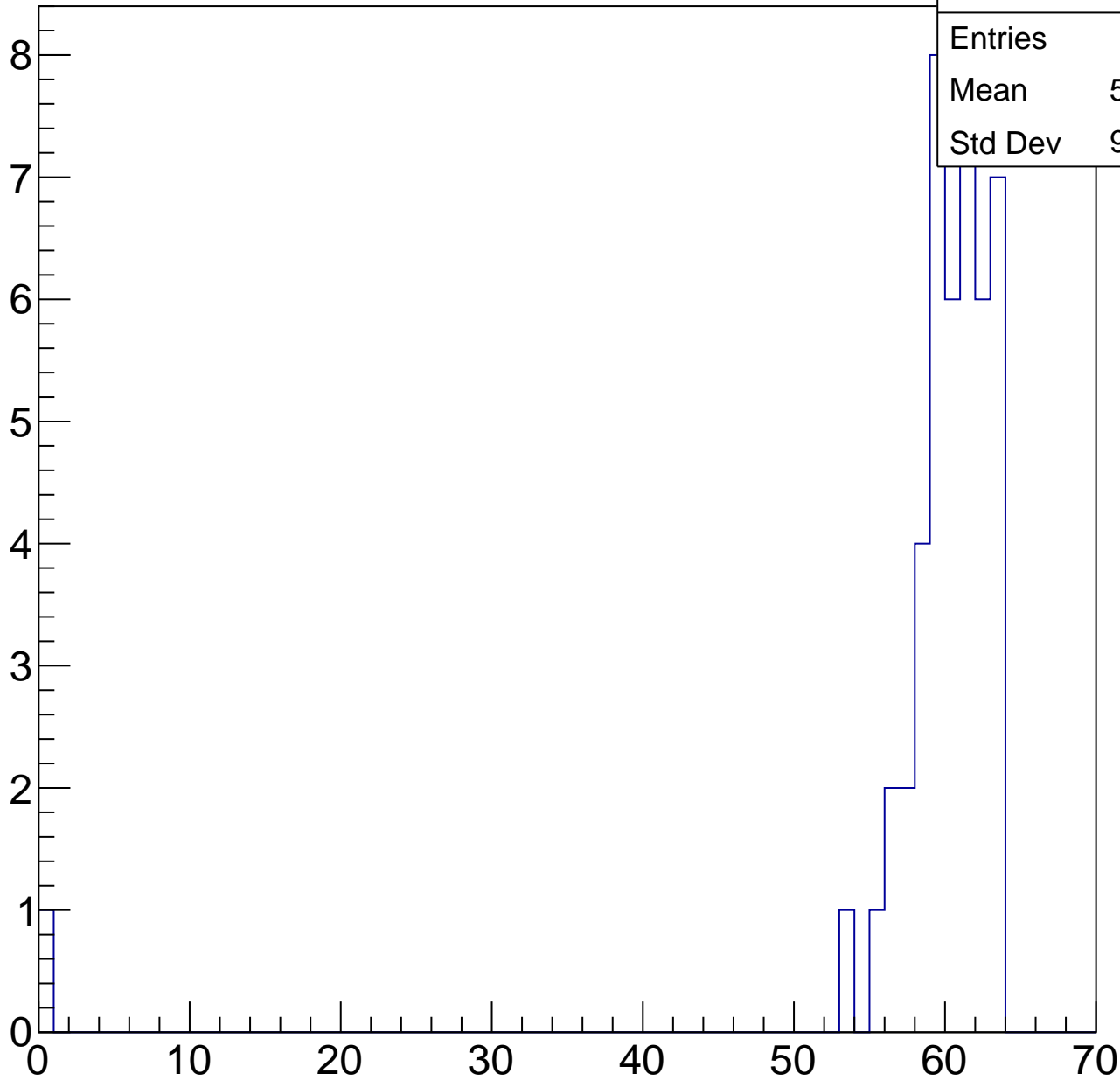
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	58.67
Std Dev	9.044

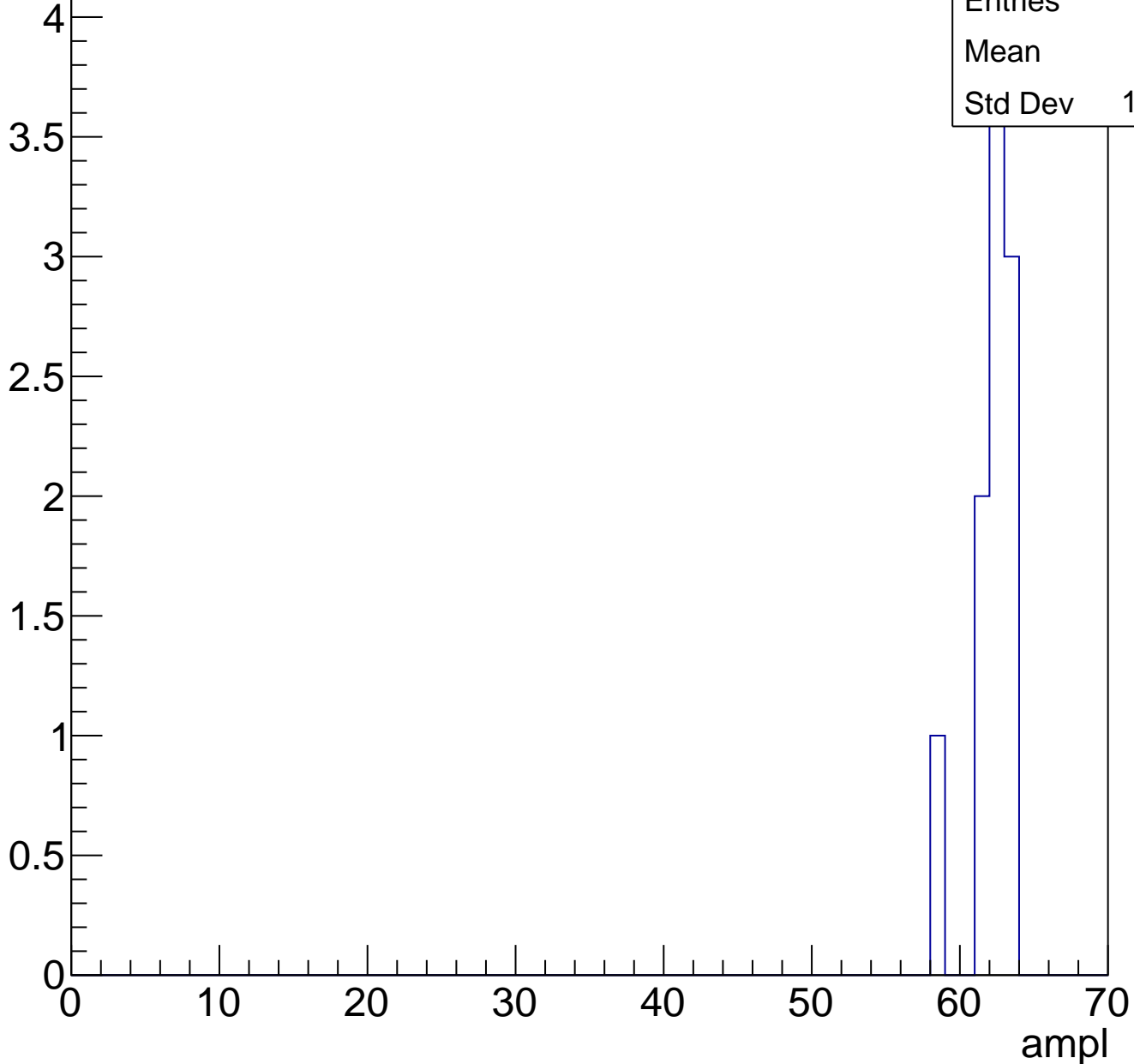
ampl



# B0L002S, U23-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch21, adc0

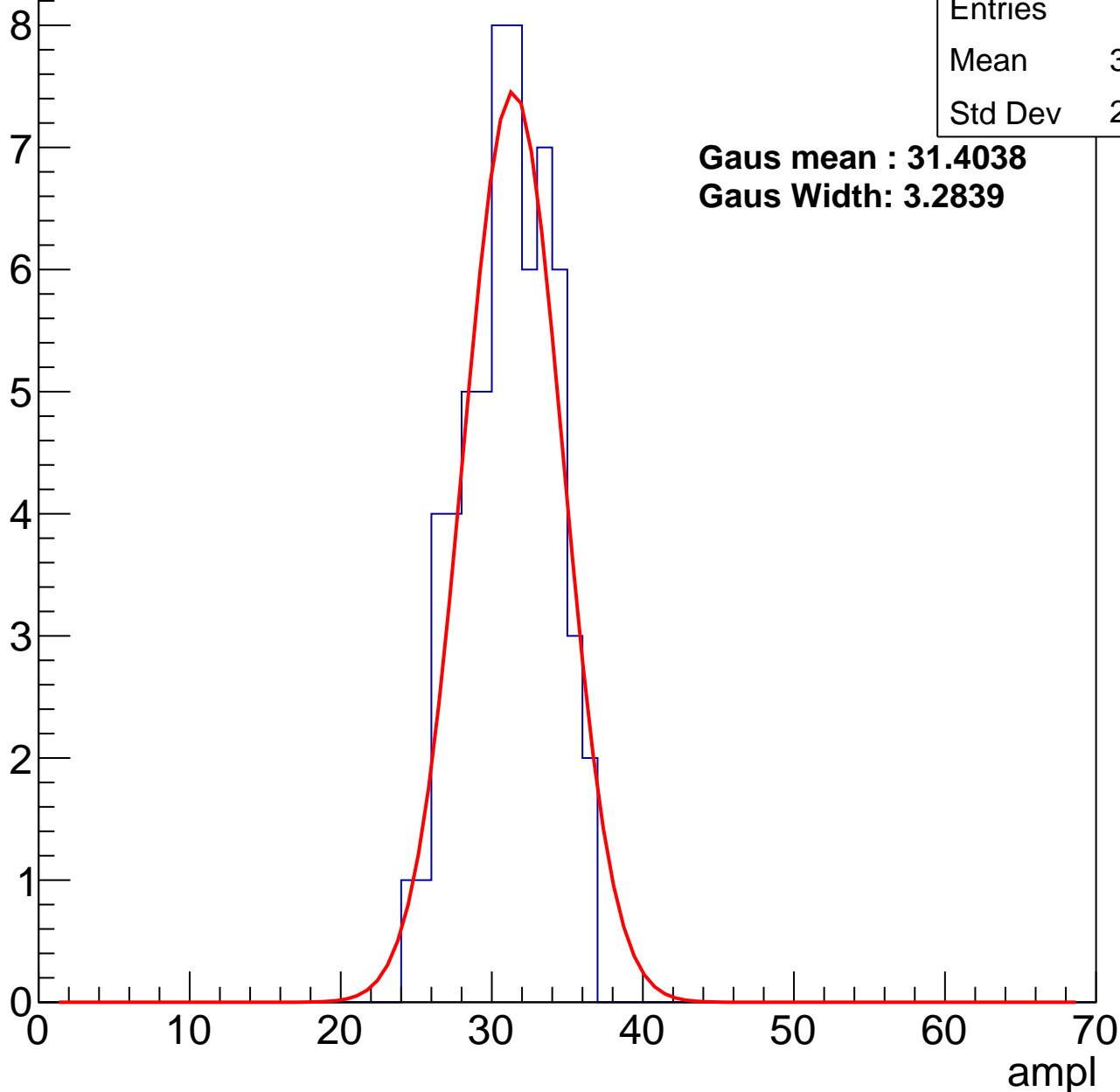
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	30.63
Std Dev	2.887

**Gaus mean : 31.4038**

**Gaus Width: 3.2839**



# B0L002S, U23-ch21, adc1

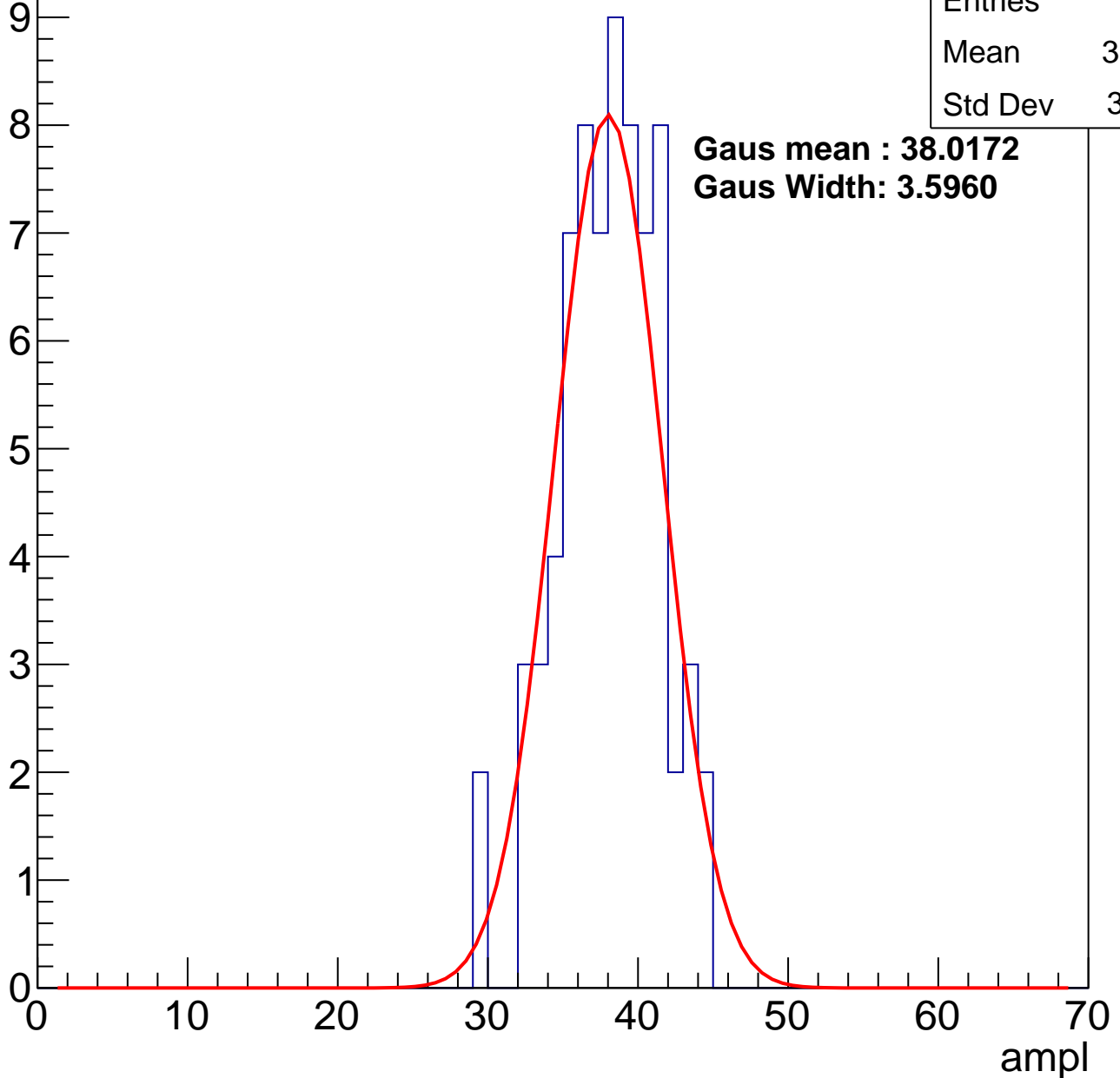
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.59
Std Dev	3.281

**Gaus mean : 38.0172**

**Gaus Width: 3.5960**



# B0L002S, U23-ch21, adc2

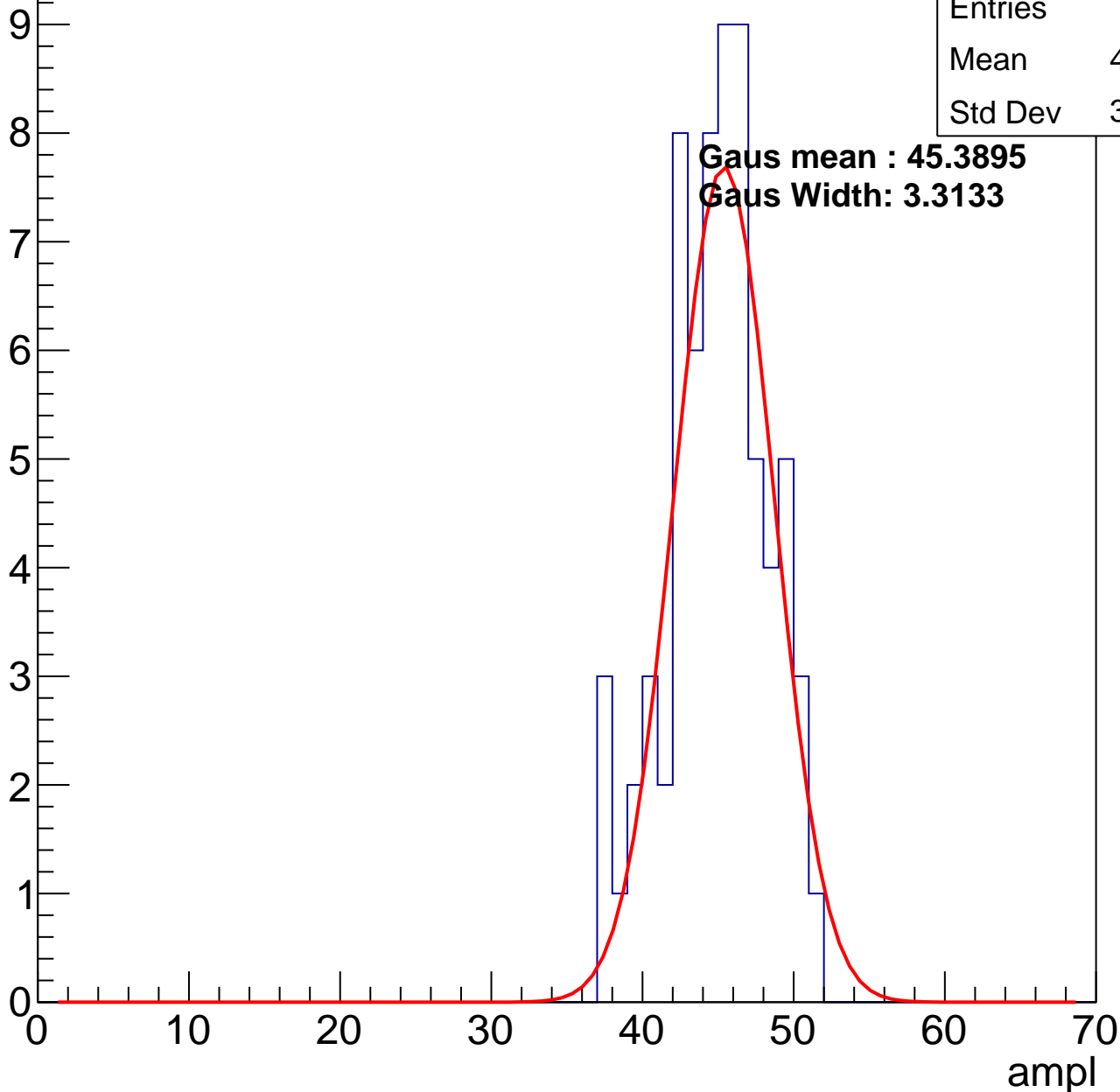
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.45
Std Dev	3.317

**Gaus mean : 45.3895**

**Gaus Width: 3.3133**

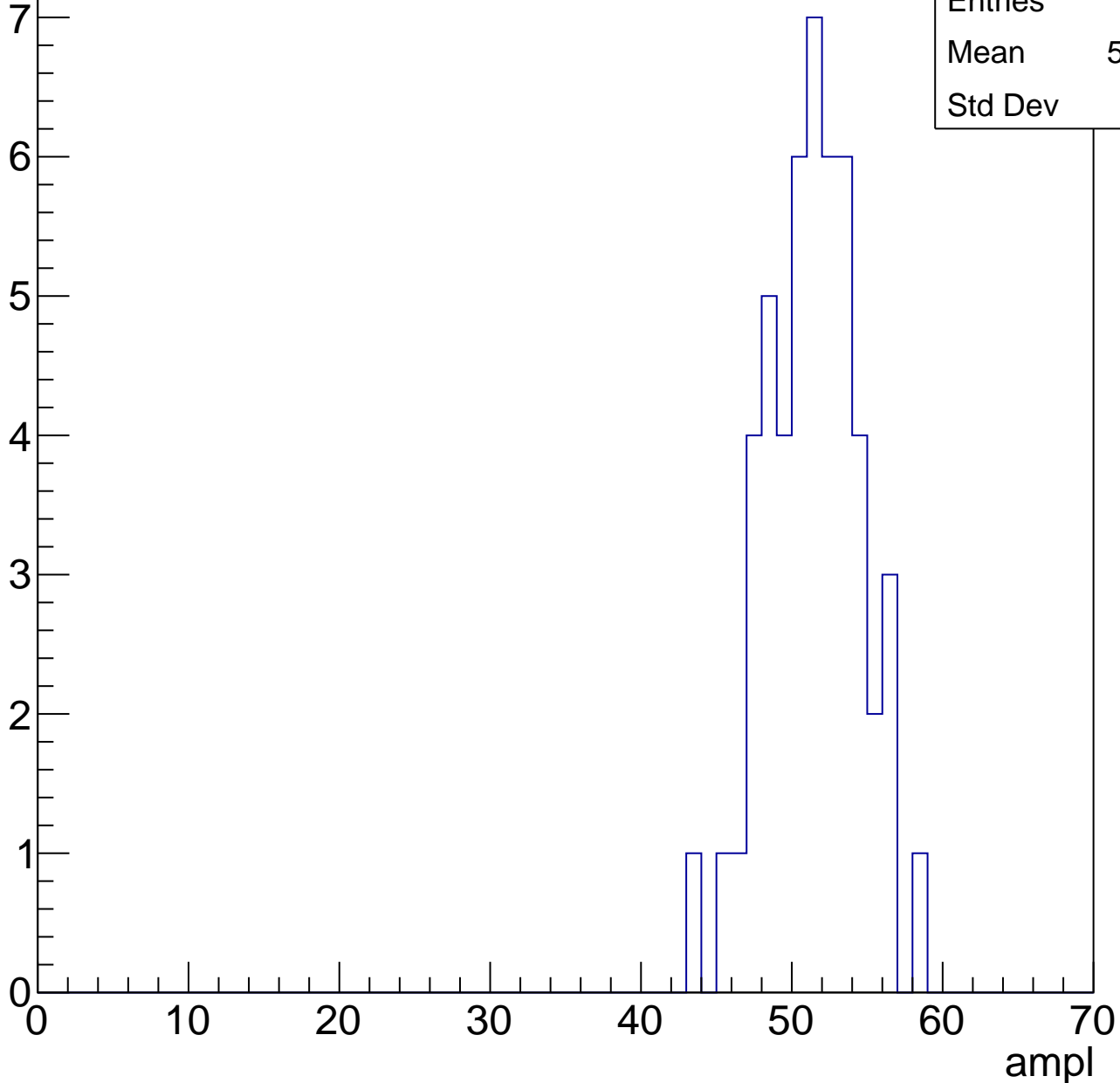


# B0L002S, U23-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

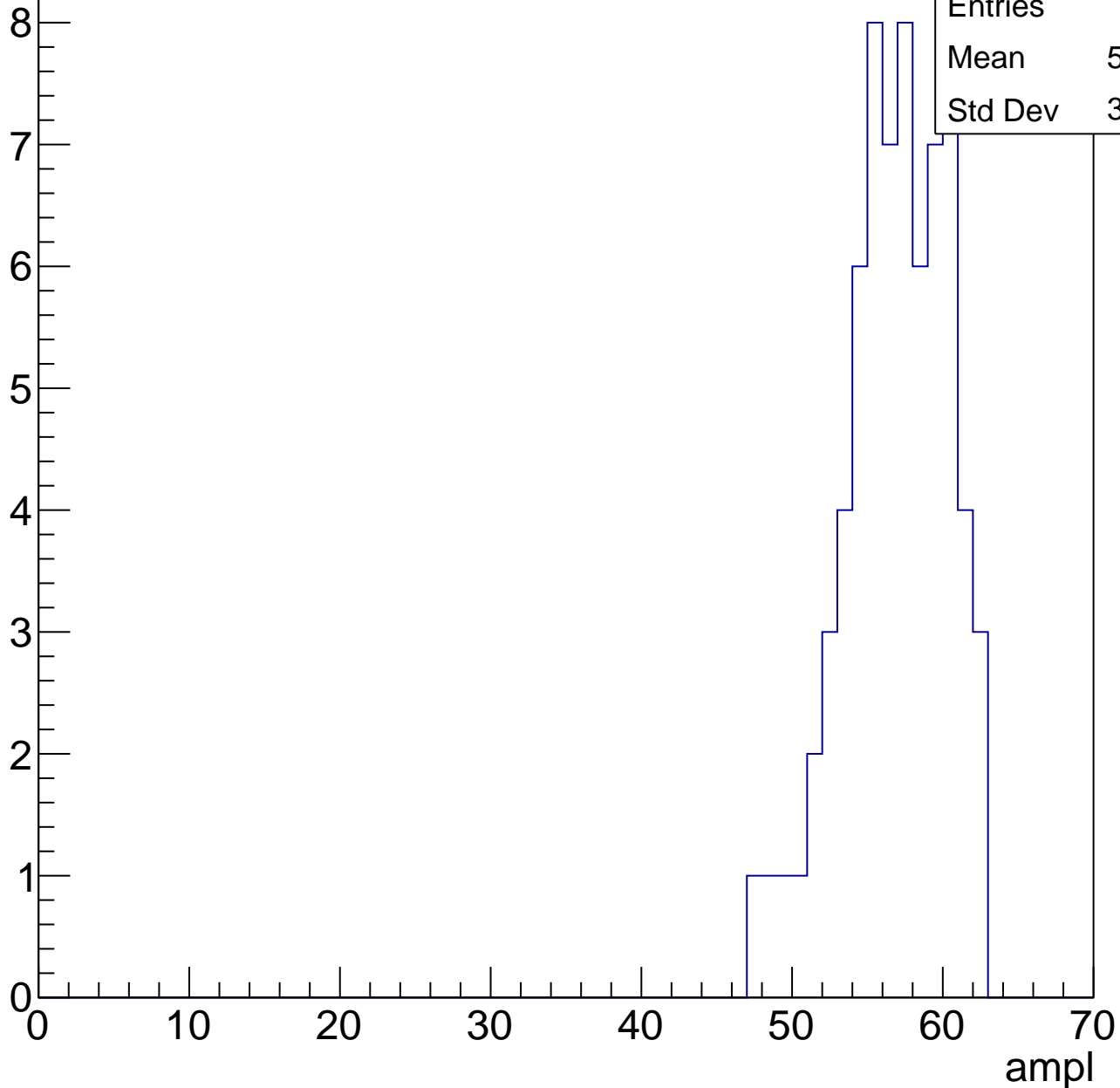
Entries	51
Mean	50.92
Std Dev	3.06



# B0L002S, U23-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



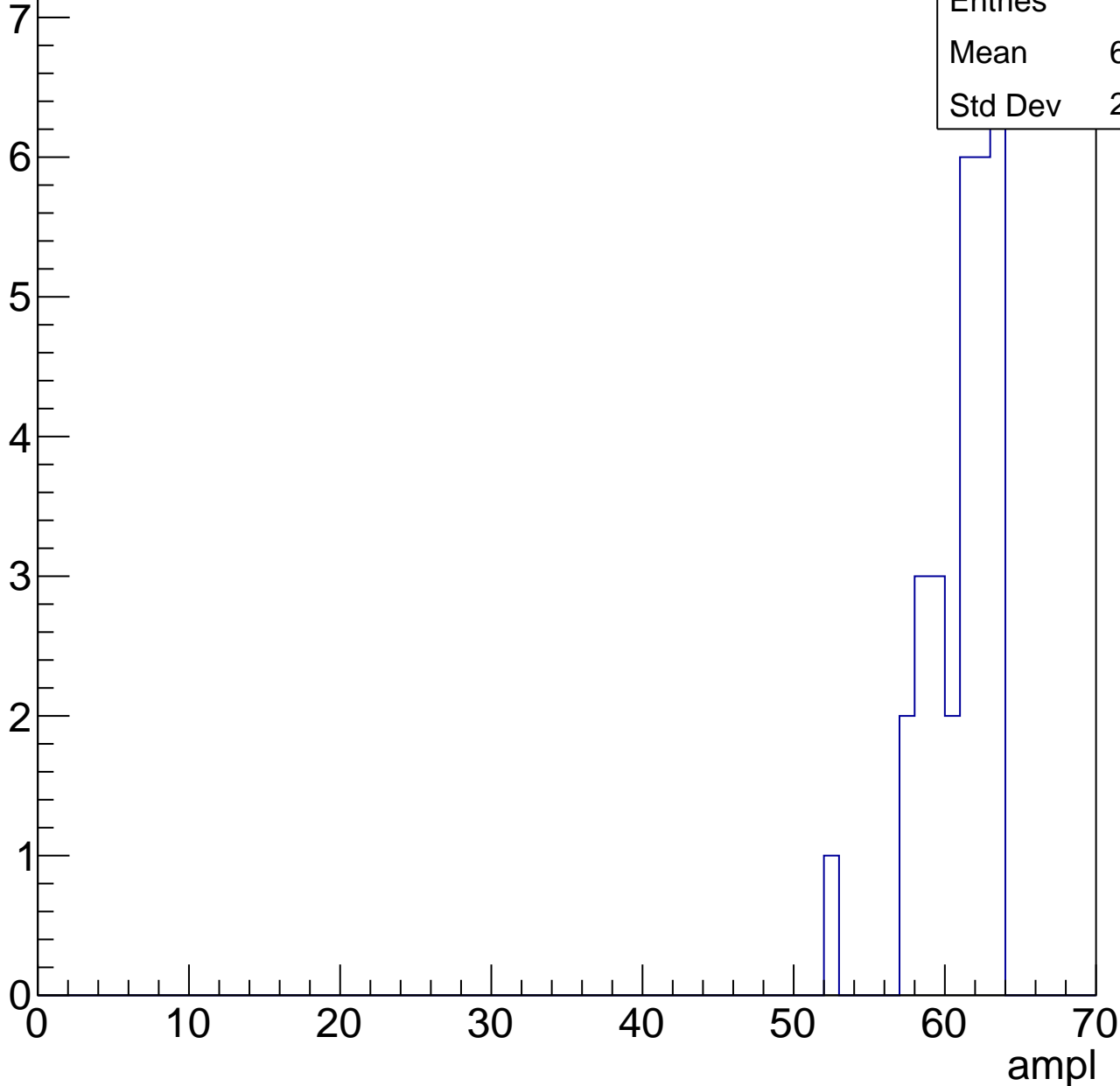
Entries	70
Mean	56.39
Std Dev	3.415

# B0L002S, U23-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	60.53
Std Dev	2.459



# B0L002S, U23-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch22, adc0

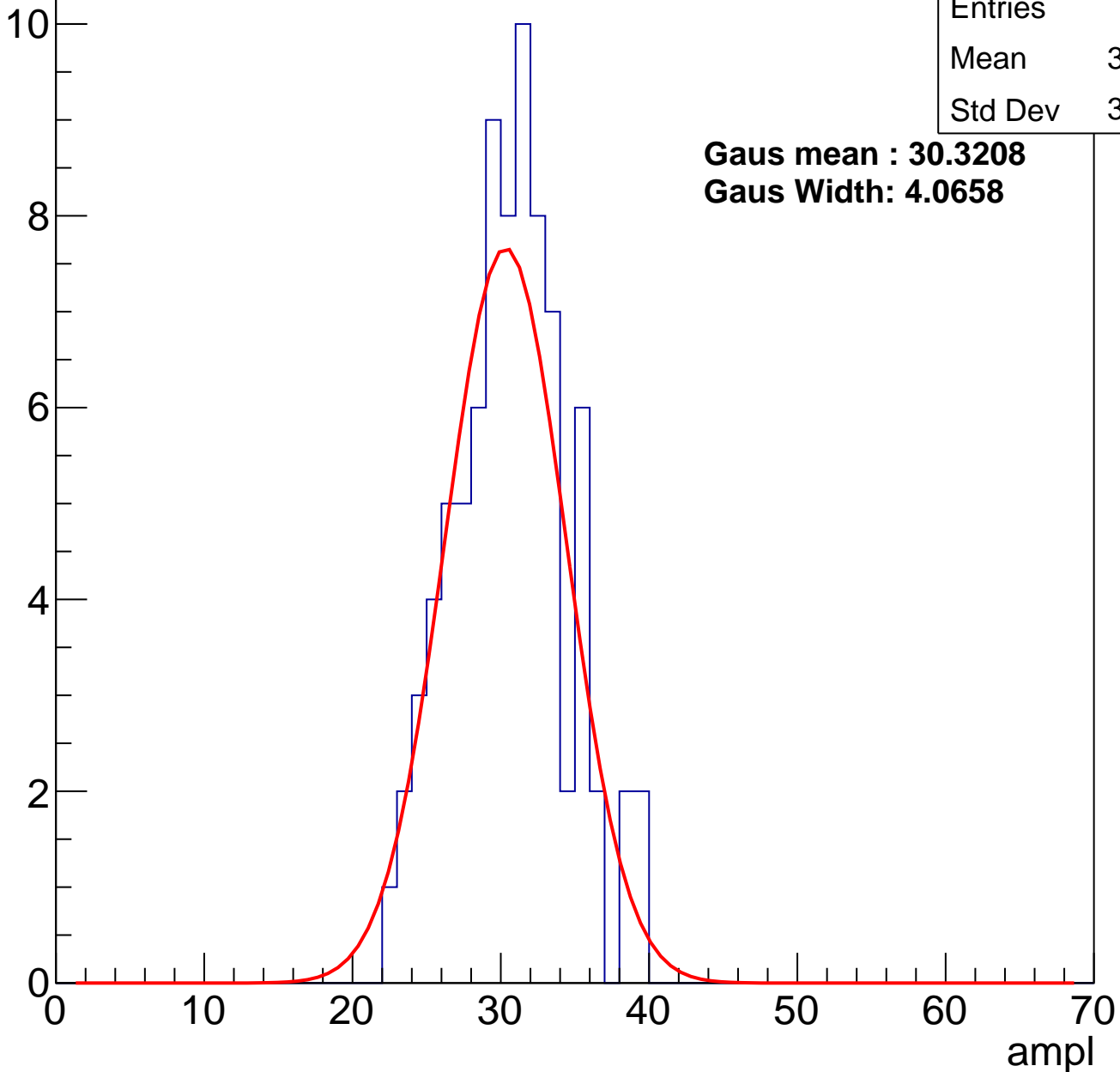
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	82
Mean	30.18
Std Dev	3.784

**Gaus mean : 30.3208**

**Gaus Width: 4.0658**

Entry



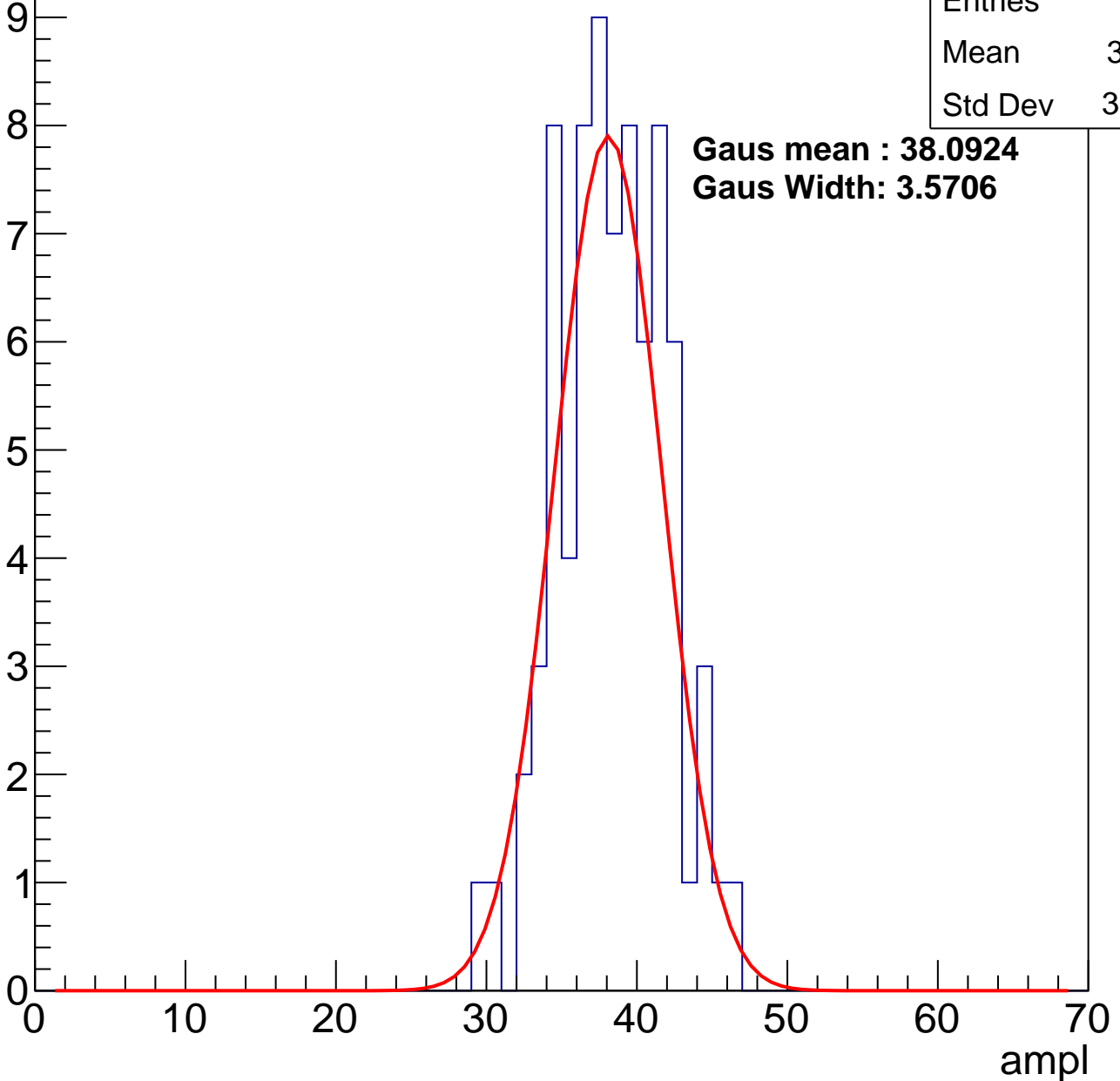
# B0L002S, U23-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	37.91
Std Dev	3.498

**Gaus mean : 38.0924**  
**Gaus Width: 3.5706**



# B0L002S, U23-ch22, adc2

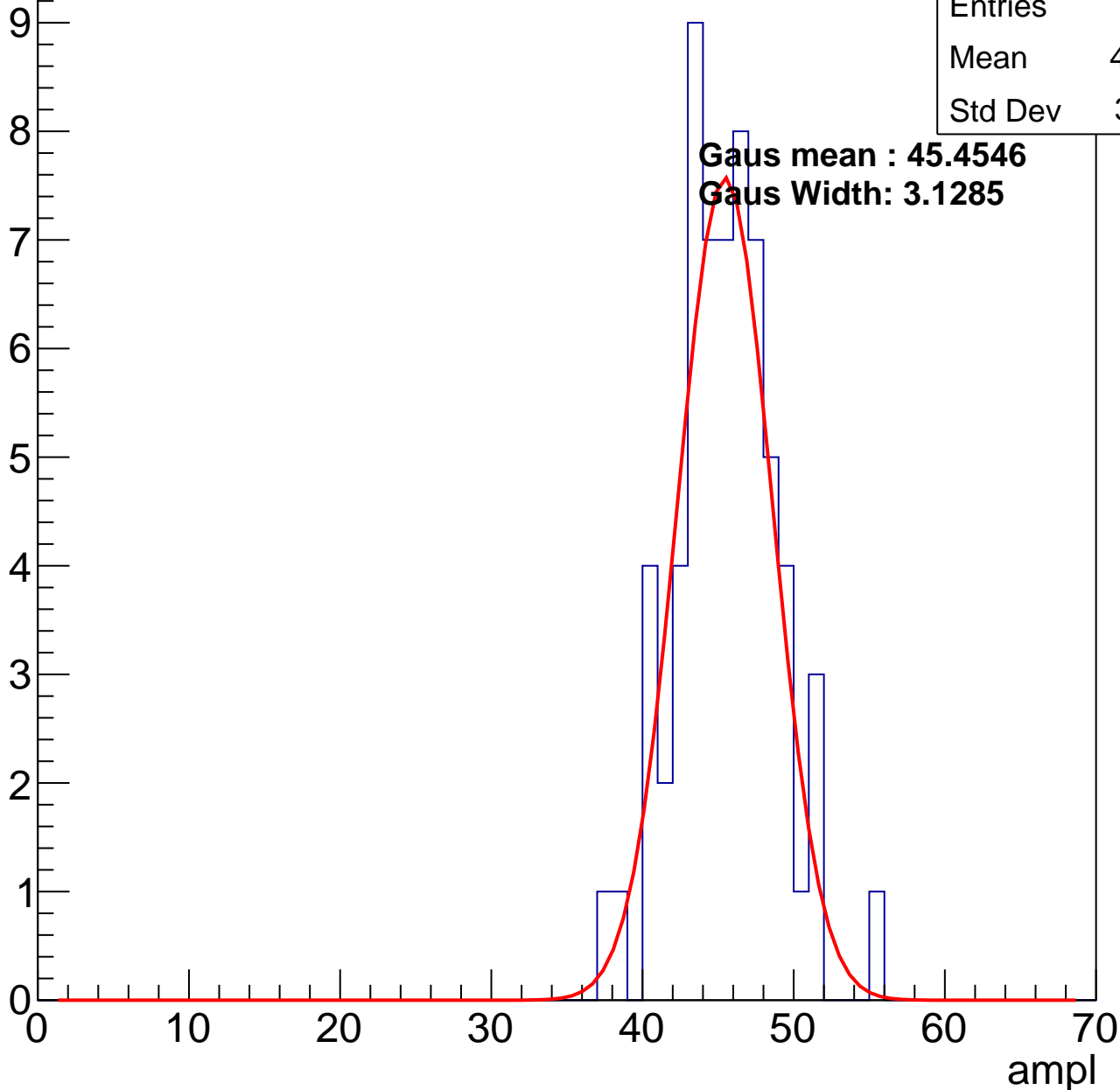
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	45.09
Std Dev	3.311

**Gaus mean : 45.4546**

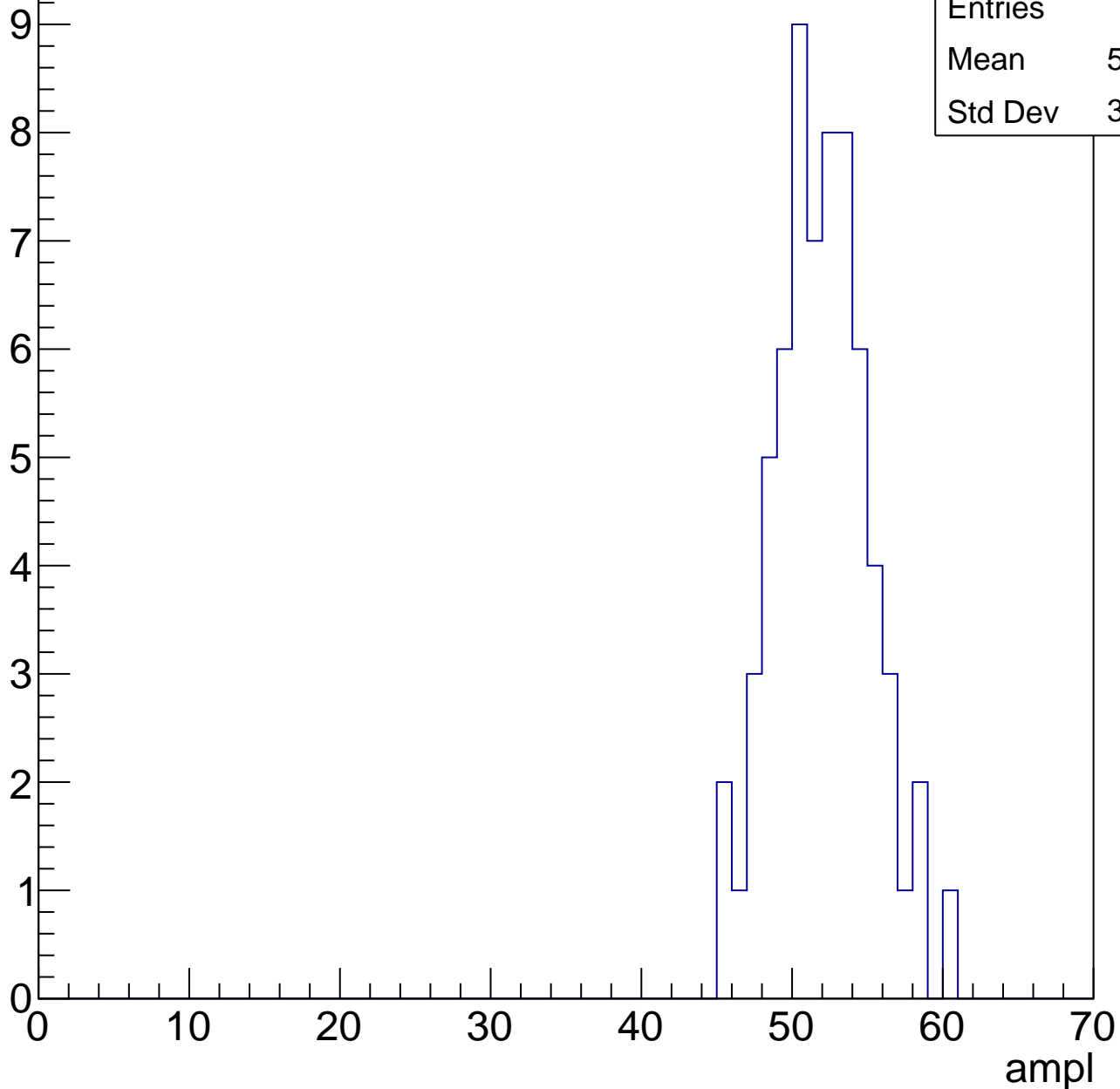
**Gaus Width: 3.1285**



# B0L002S, U23-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

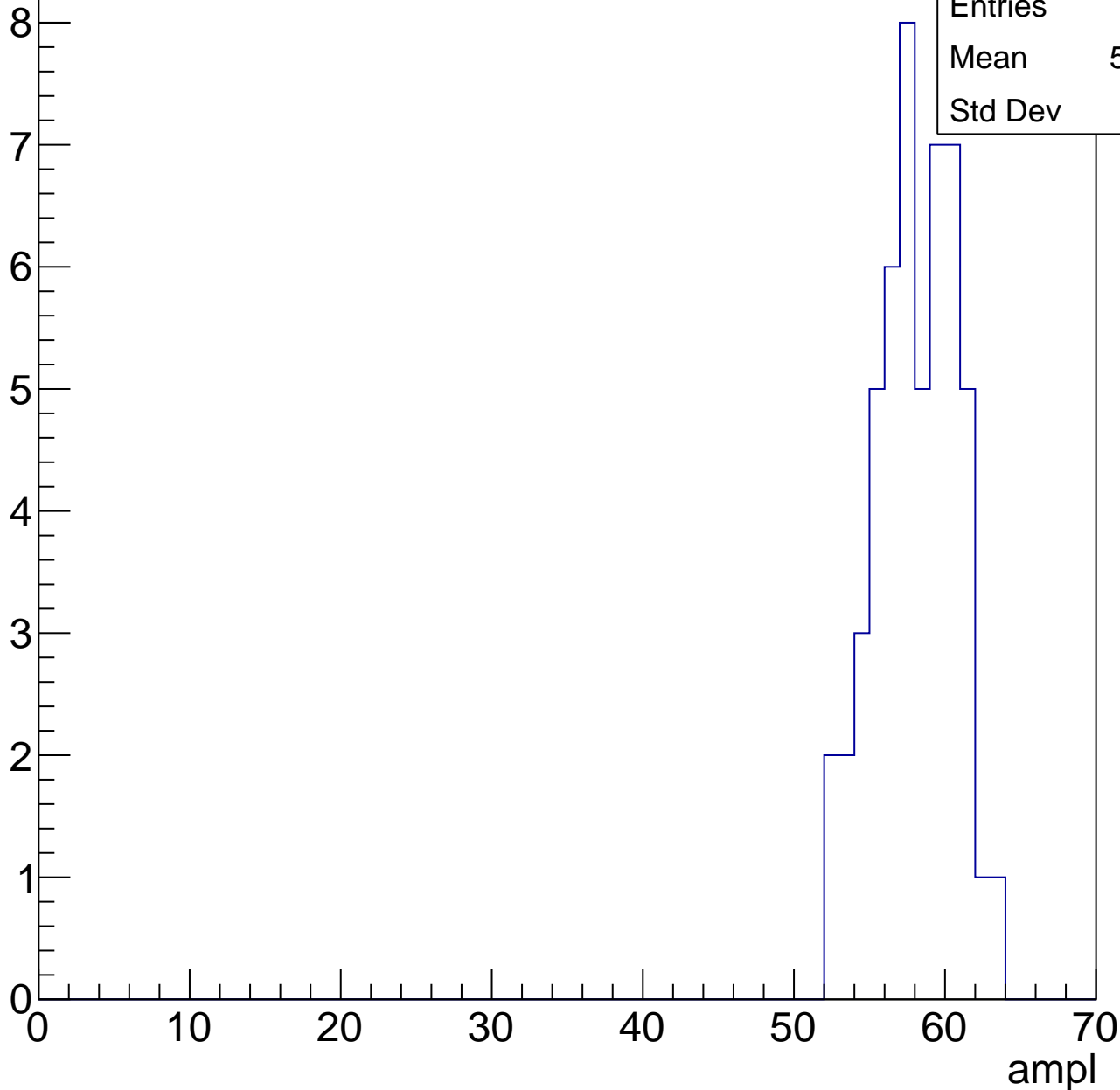


Entries	66
Mean	51.56
Std Dev	3.139

# B0L002S, U23-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



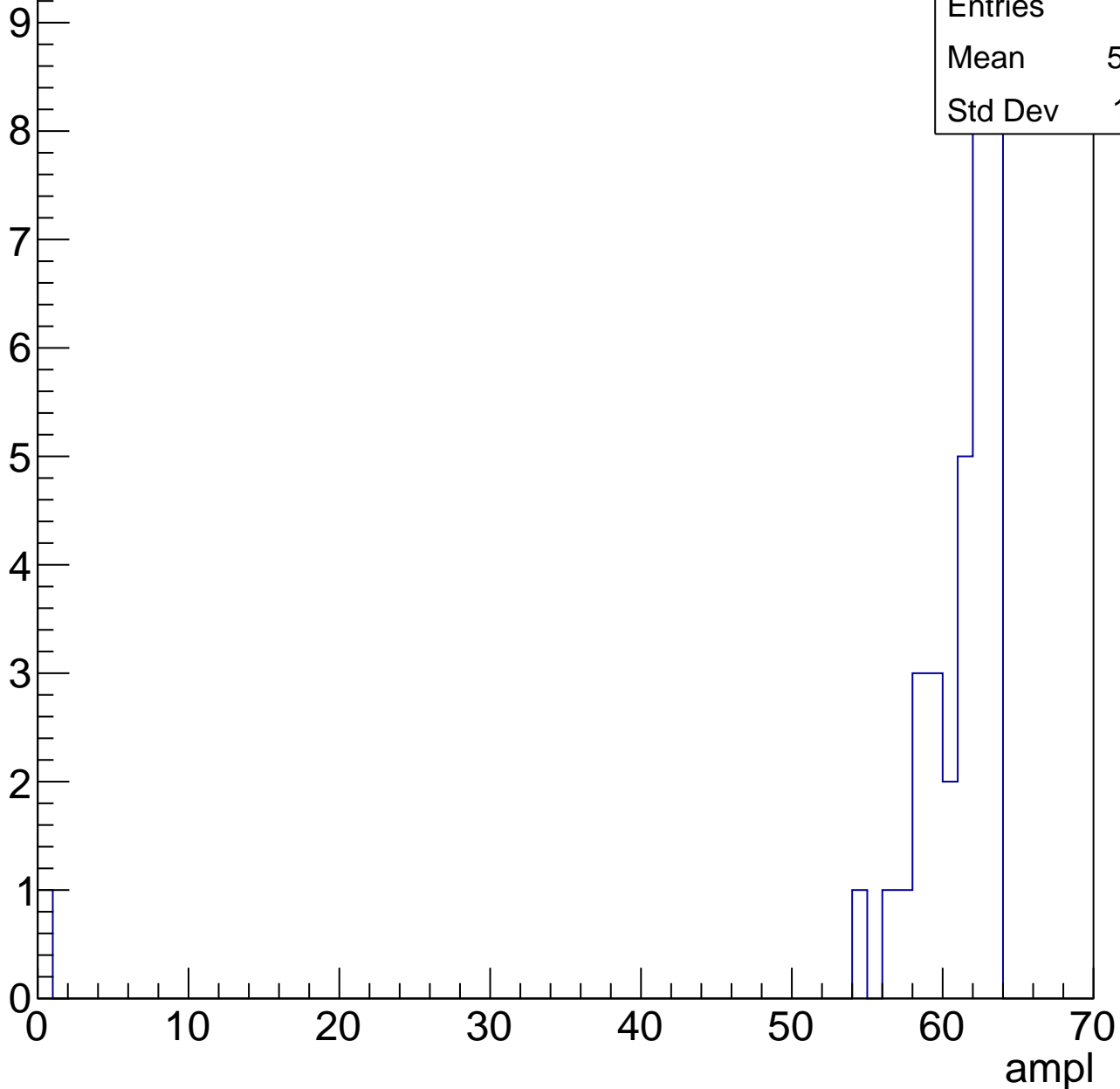
Entries	52
Mean	57.54
Std Dev	2.62

# B0L002S, U23-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.97
Std Dev	10.51



# B0L002S, U23-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch23, adc0

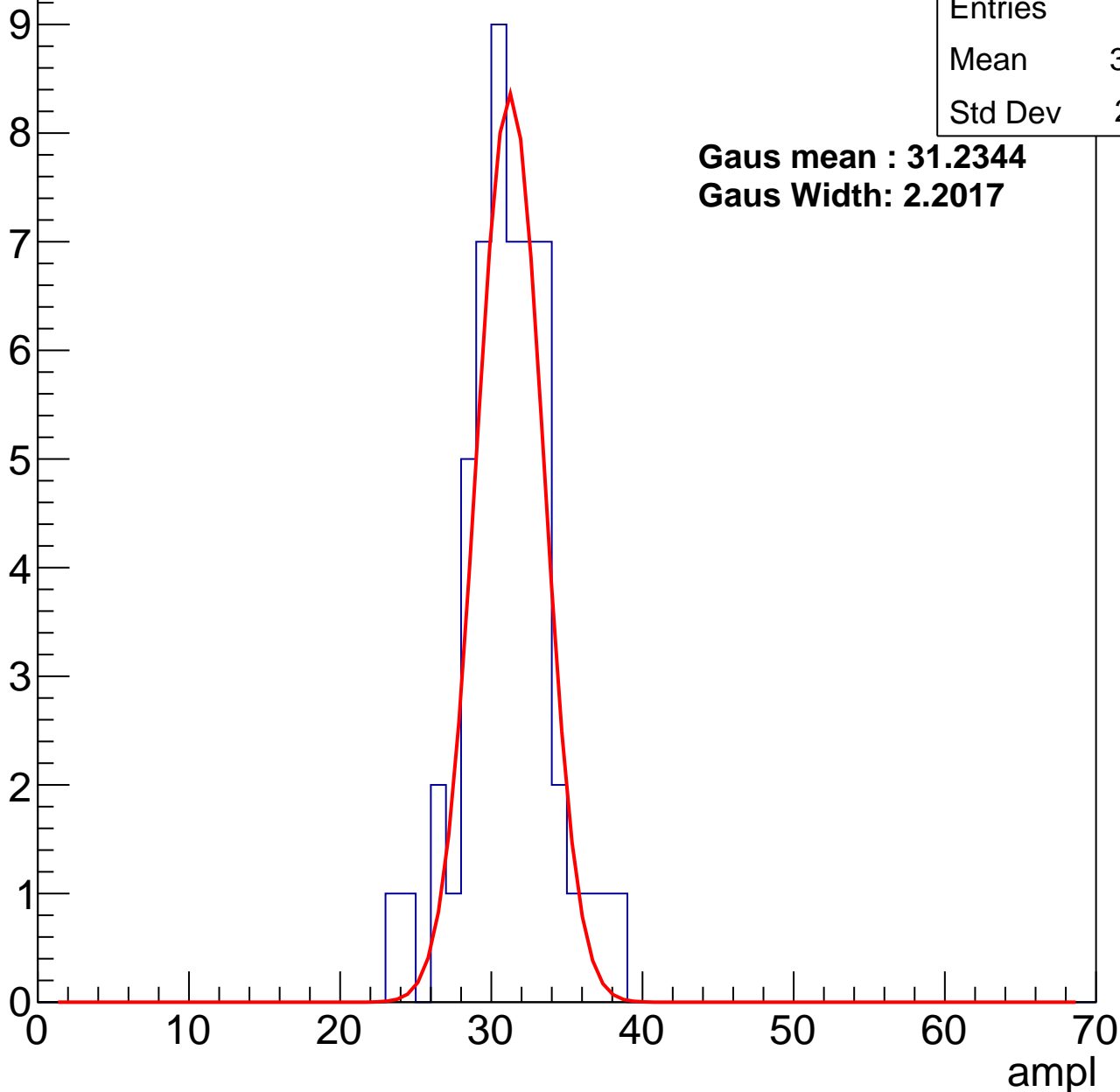
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	30.66
Std Dev	2.861

**Gaus mean : 31.2344**

**Gaus Width: 2.2017**



# B0L002S, U23-ch23, adc1

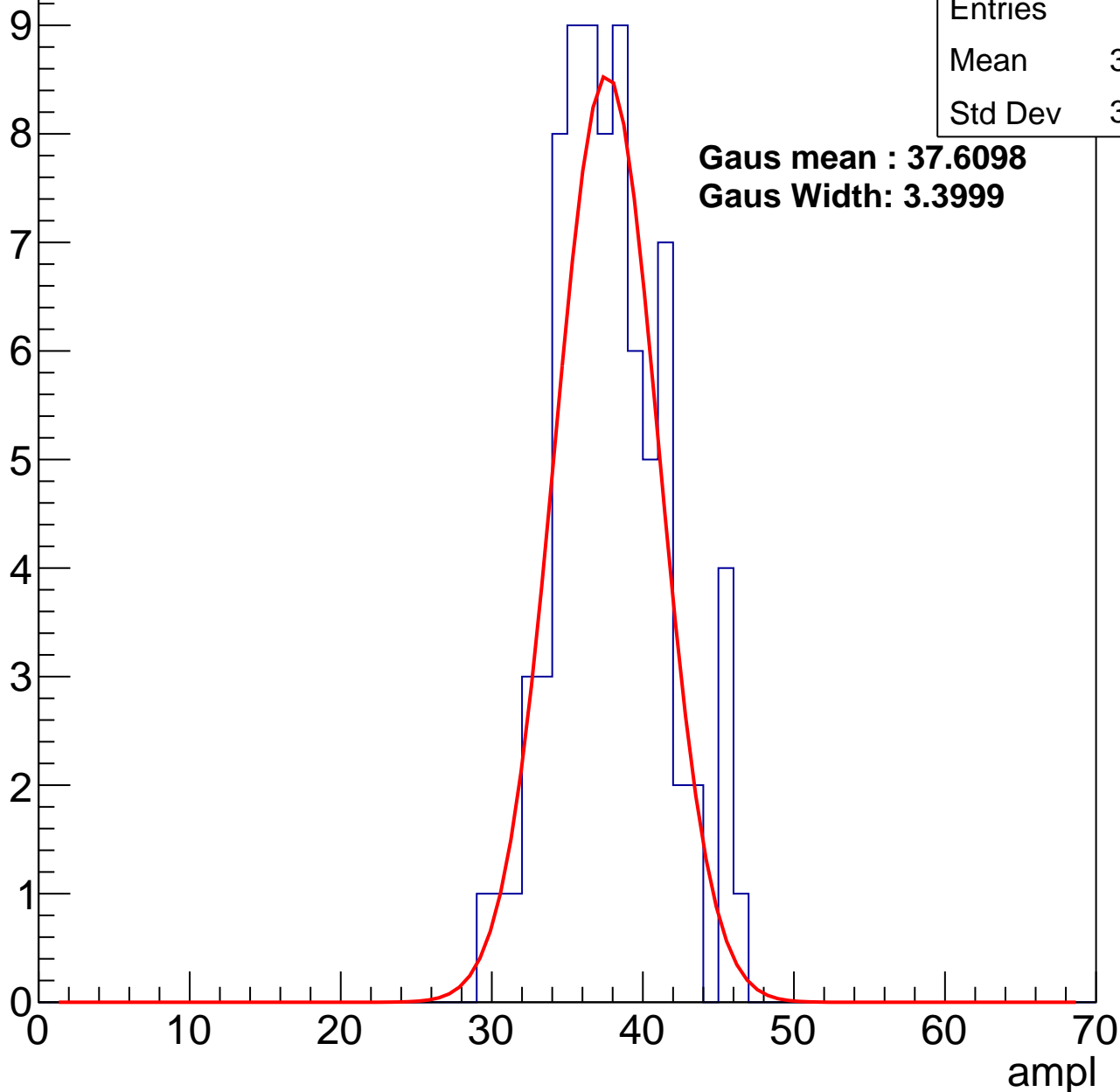
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	37.35
Std Dev	3.607

**Gaus mean : 37.6098**

**Gaus Width: 3.3999**



# B0L002S, U23-ch23, adc2

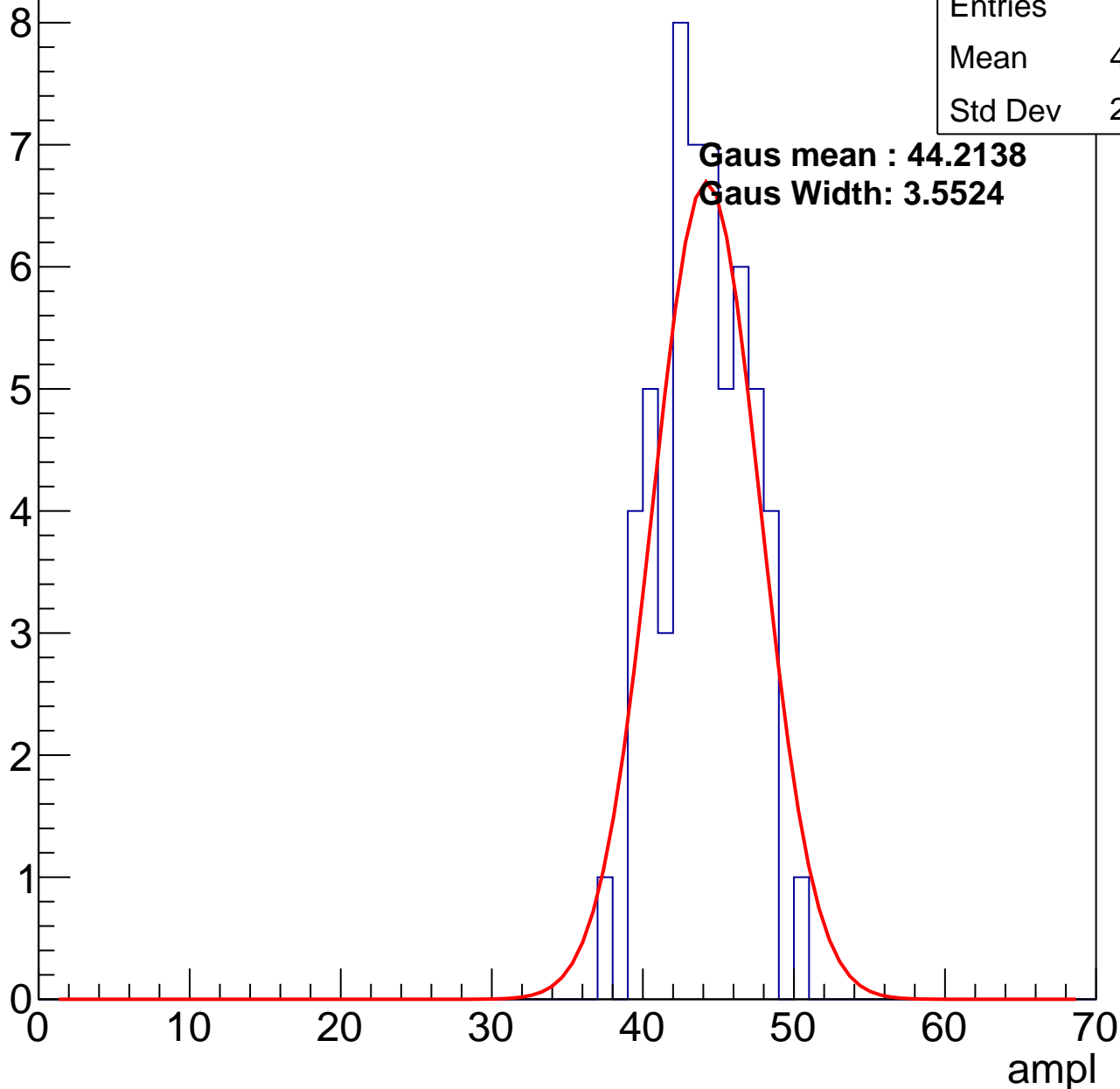
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.55
Std Dev	2.859

**Gaus mean : 44.2138**

**Gaus Width: 3.5524**

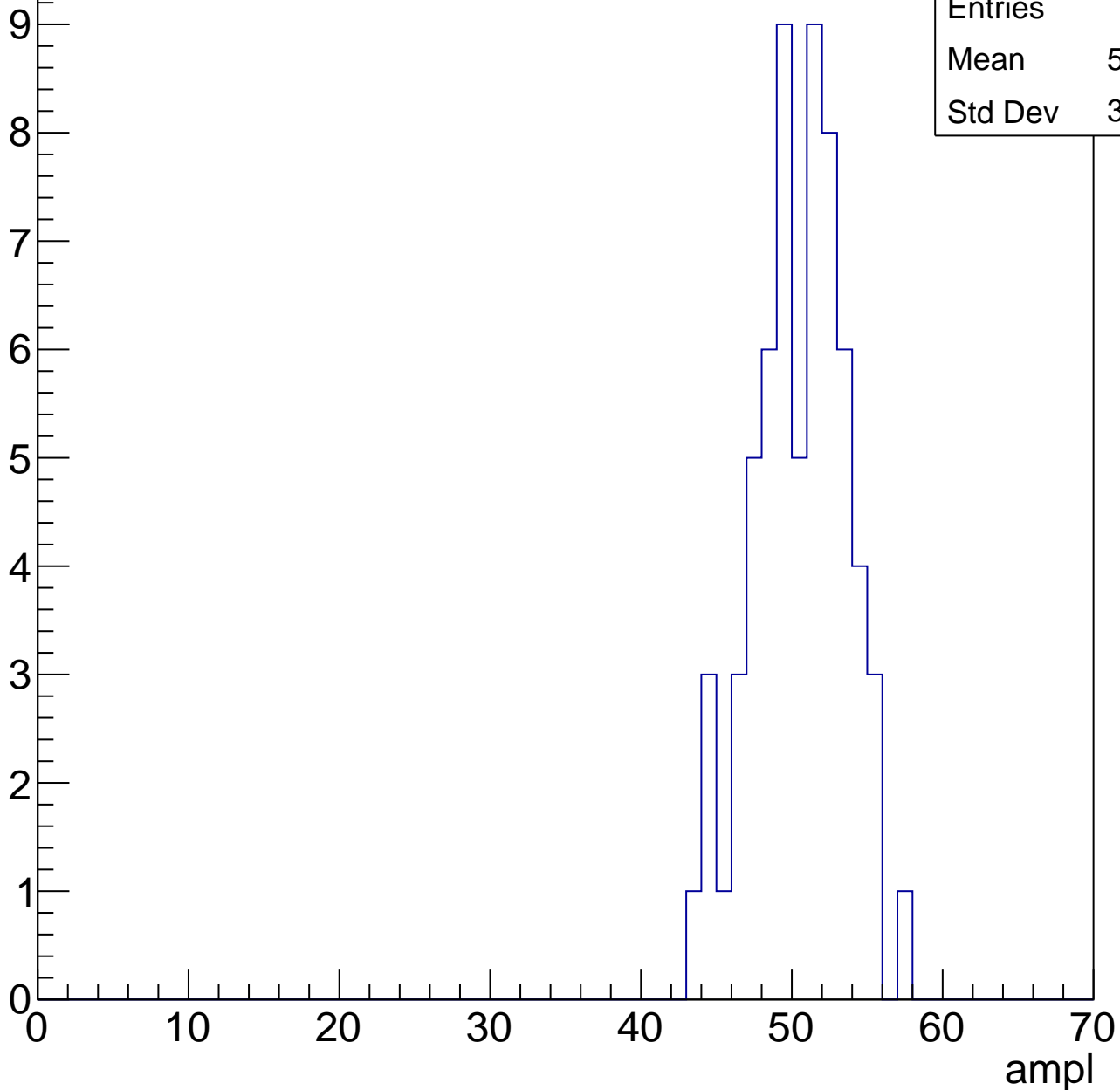


# B0L002S, U23-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

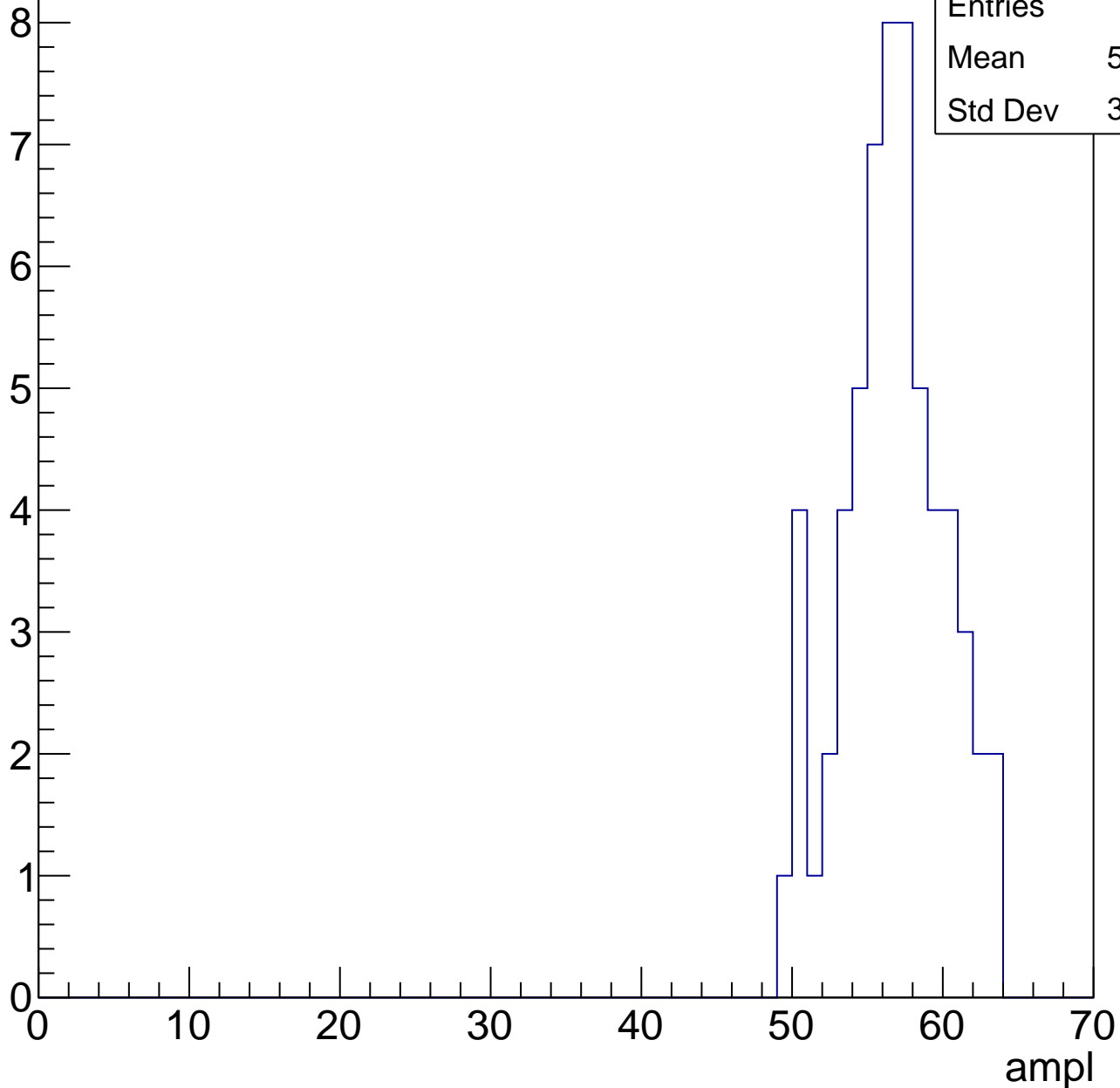
Entries	64
Mean	50.05
Std Dev	3.038



# B0L002S, U23-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

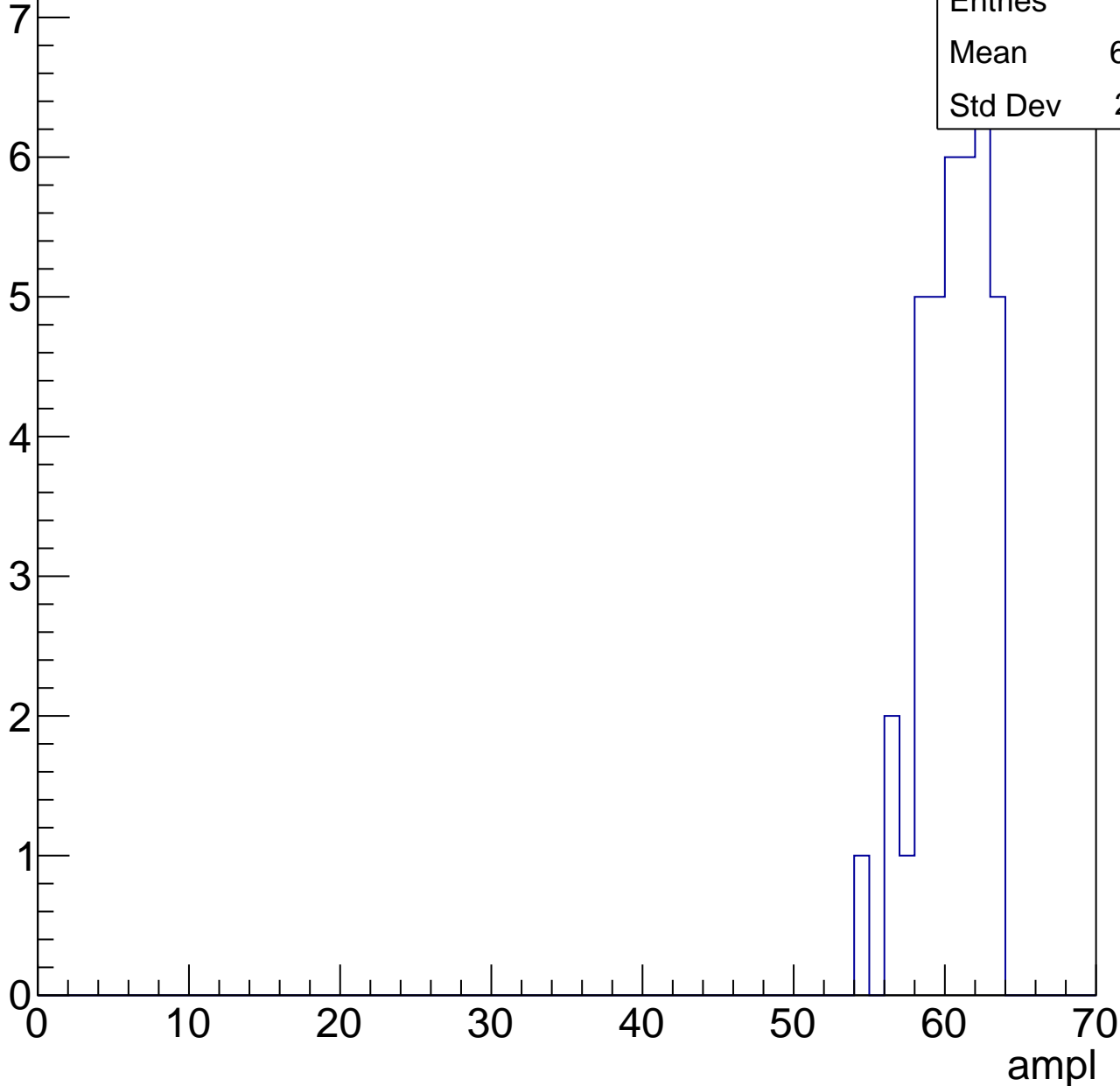


# B0L002S, U23-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

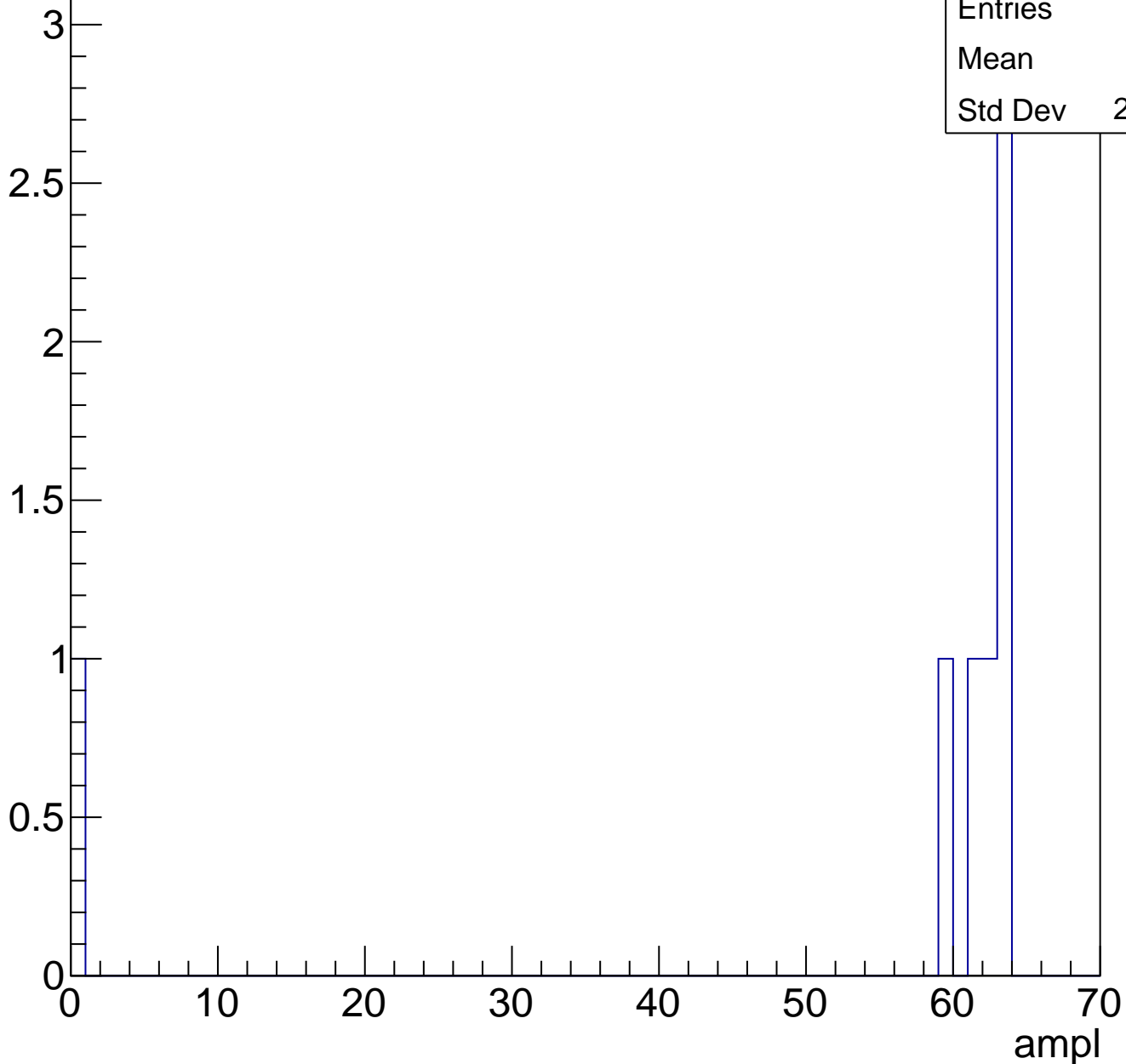
Entries	38
Mean	60.08
Std Dev	2.181



# B0L002S, U23-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch24, adc0

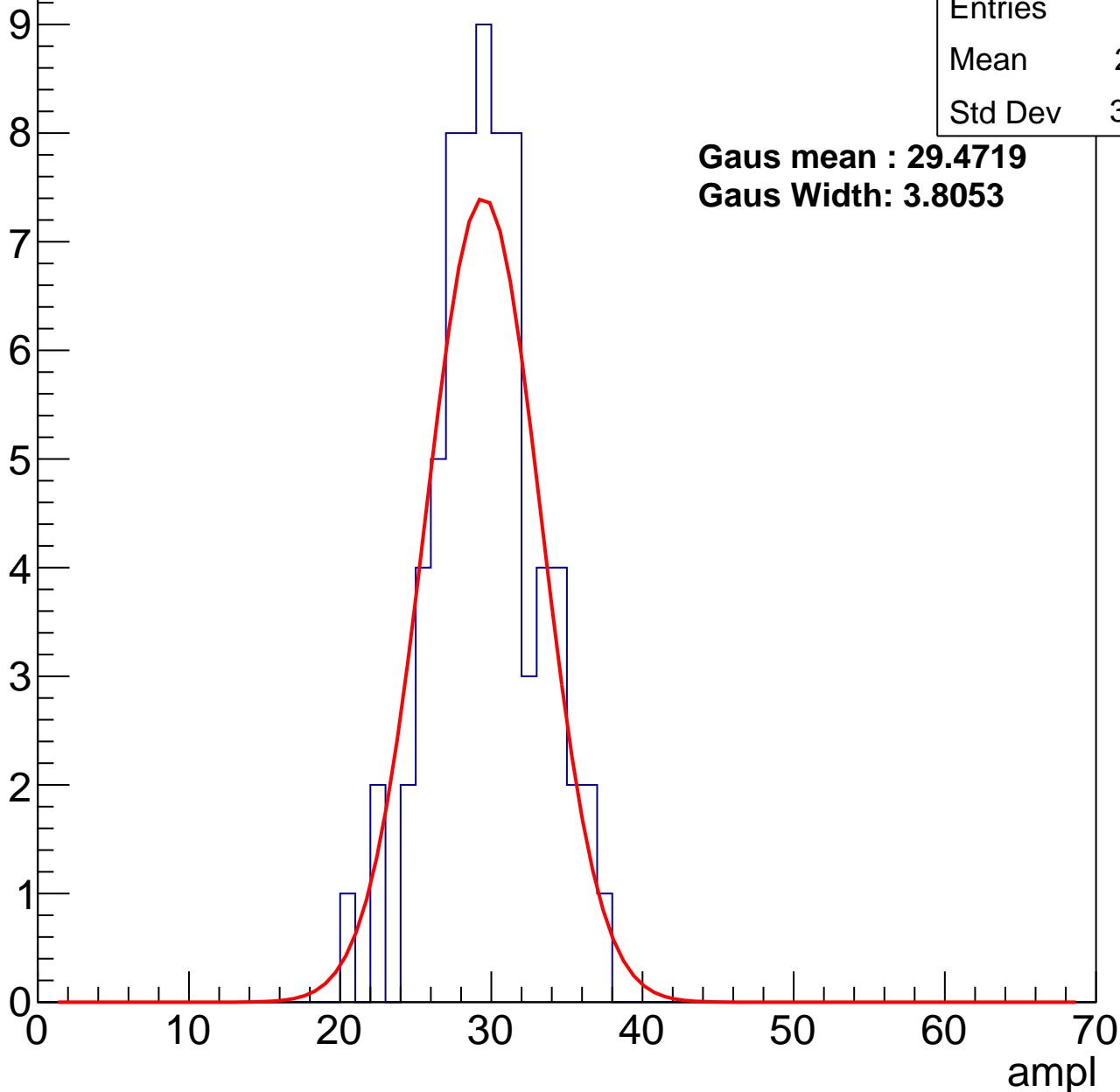
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.21
Std Dev	3.439

**Gaus mean : 29.4719**

**Gaus Width: 3.8053**



# B0L002S, U23-ch24, adc1

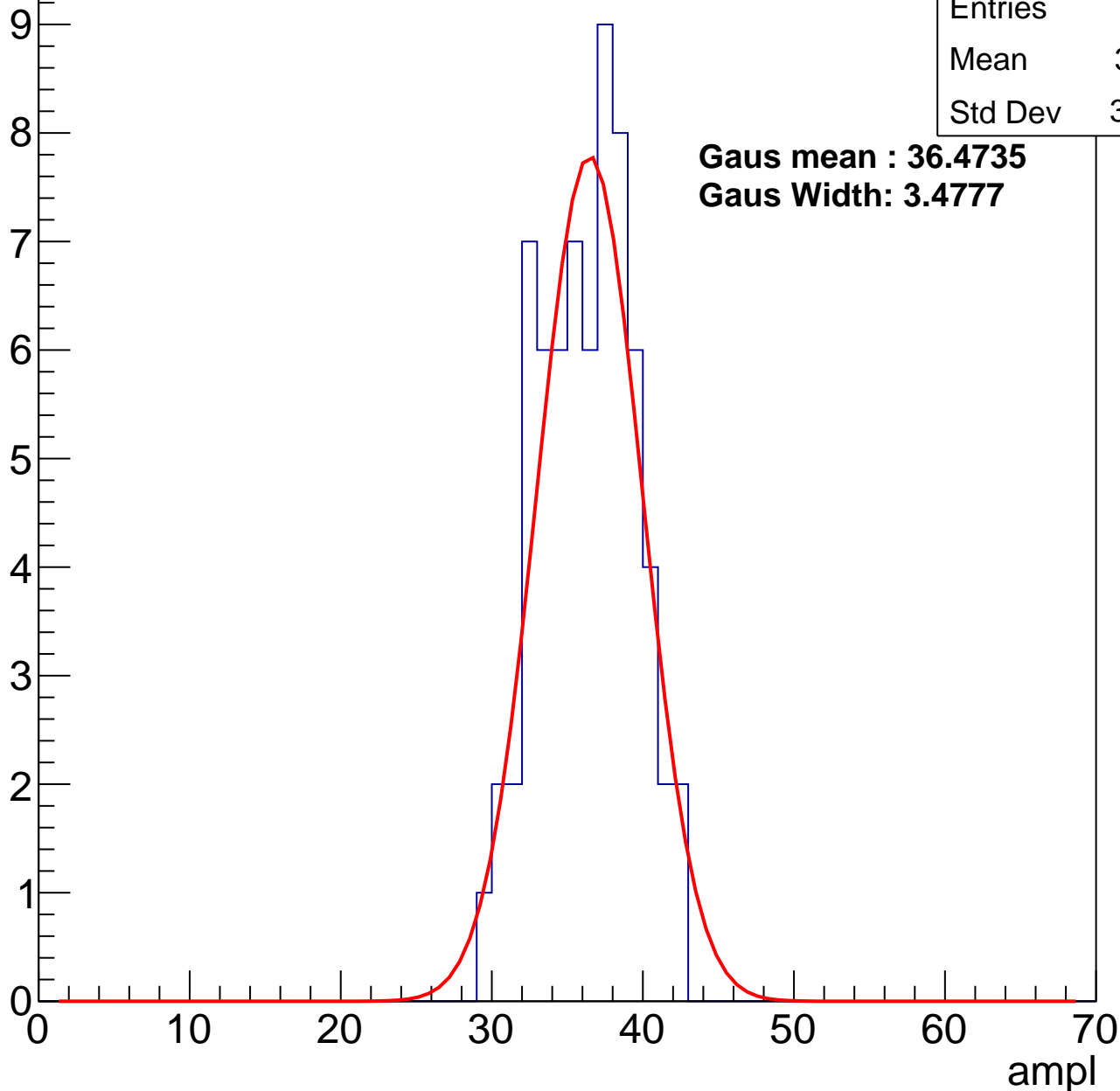
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.81
Std Dev	3.083

**Gaus mean : 36.4735**

**Gaus Width: 3.4777**



# B0L002S, U23-ch24, adc2

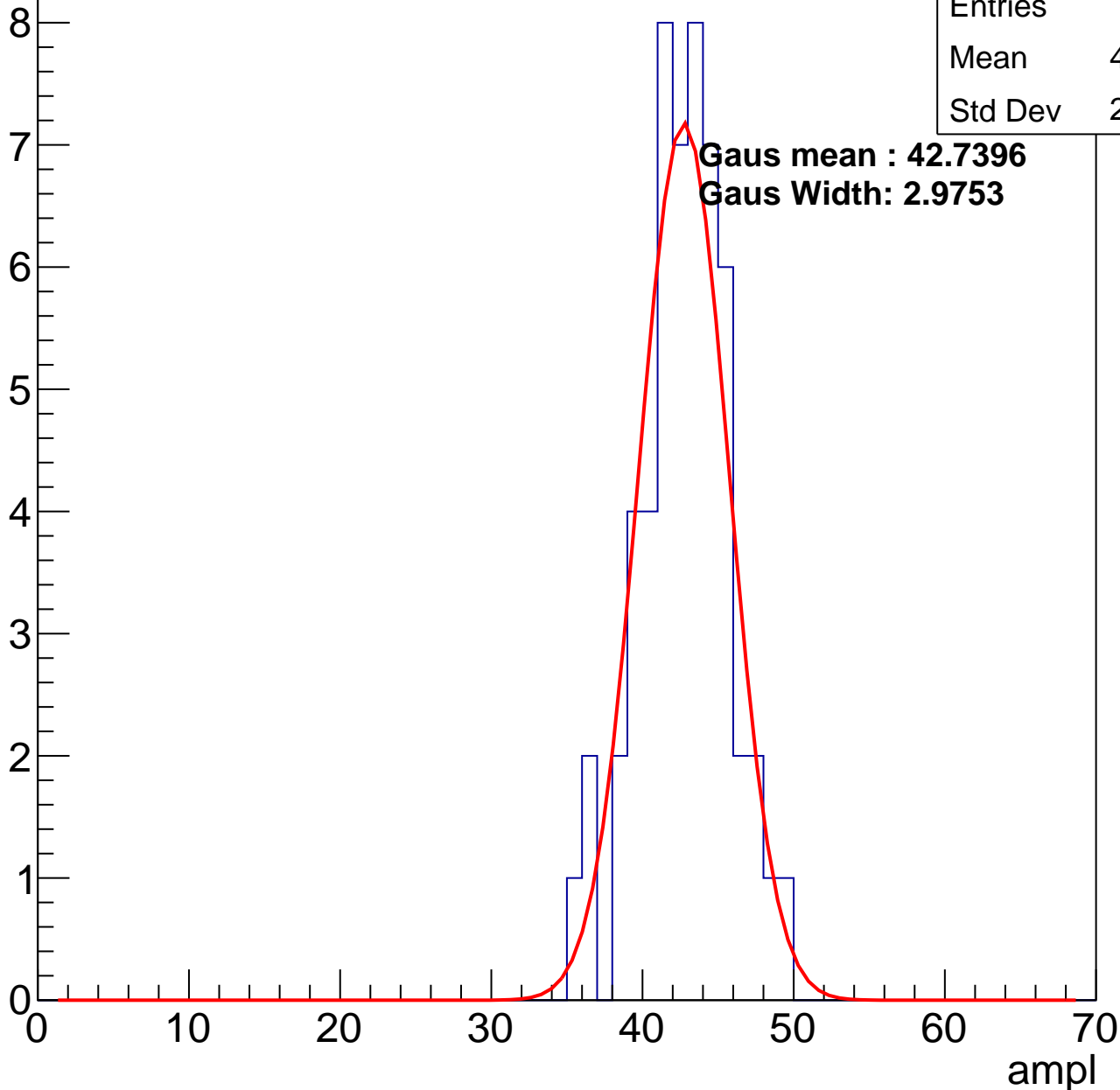
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	42.29
Std Dev	2.909

**Gaus mean : 42.7396**

**Gaus Width: 2.9753**

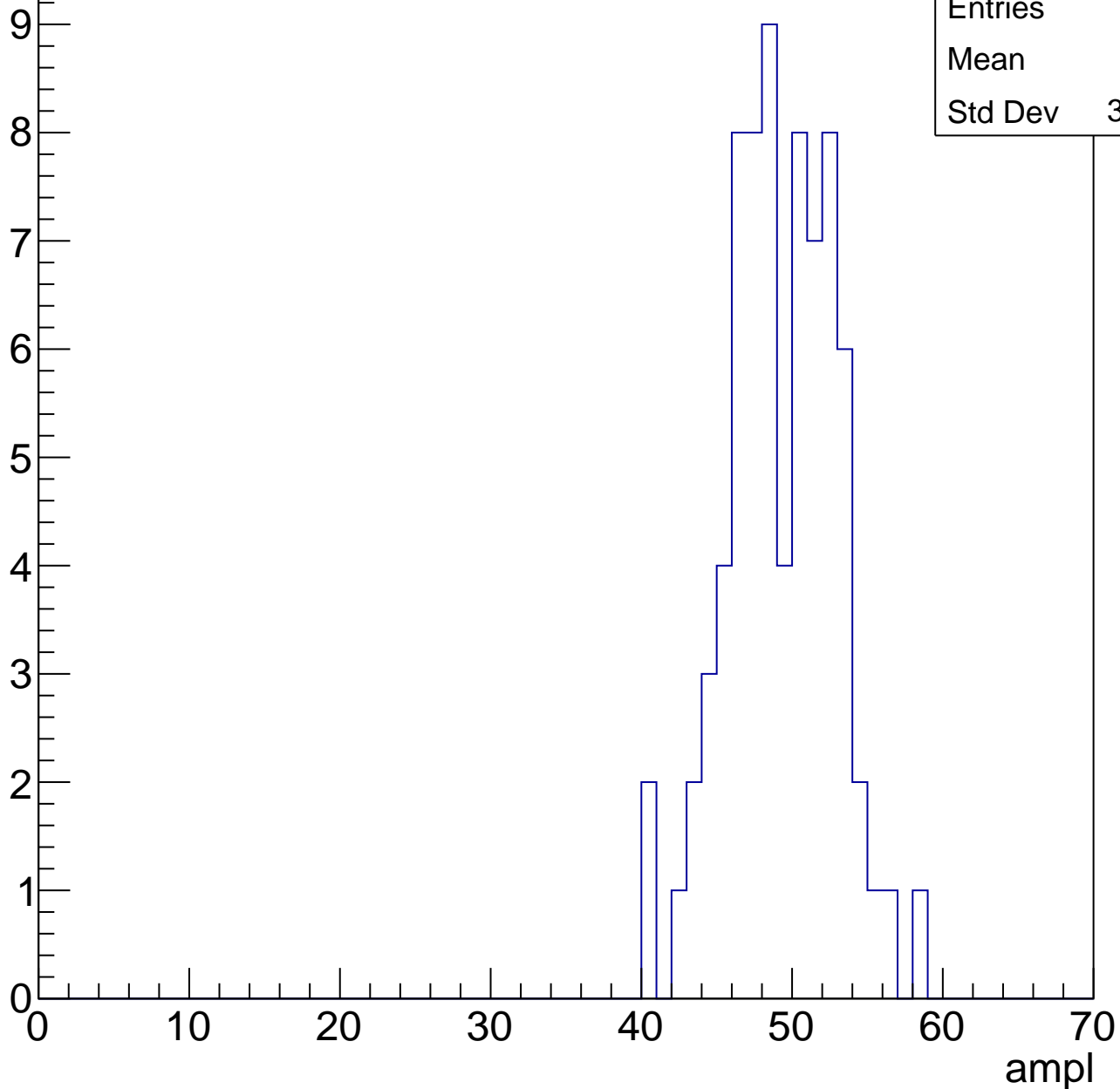


# B0L002S, U23-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	48.8
Std Dev	3.563

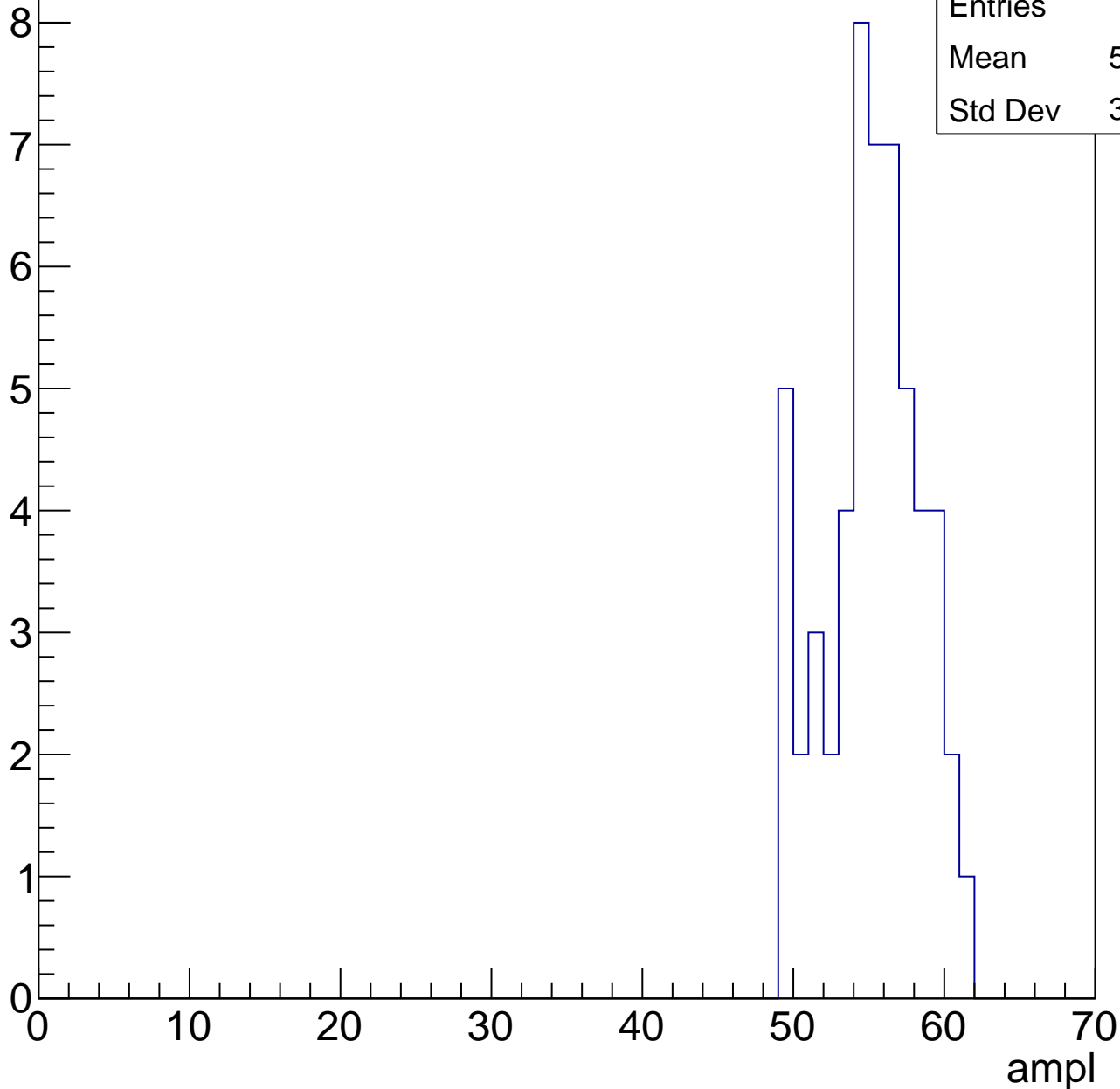


# B0L002S, U23-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	54.76
Std Dev	3.132



# B0L002S, U23-ch24, adc5

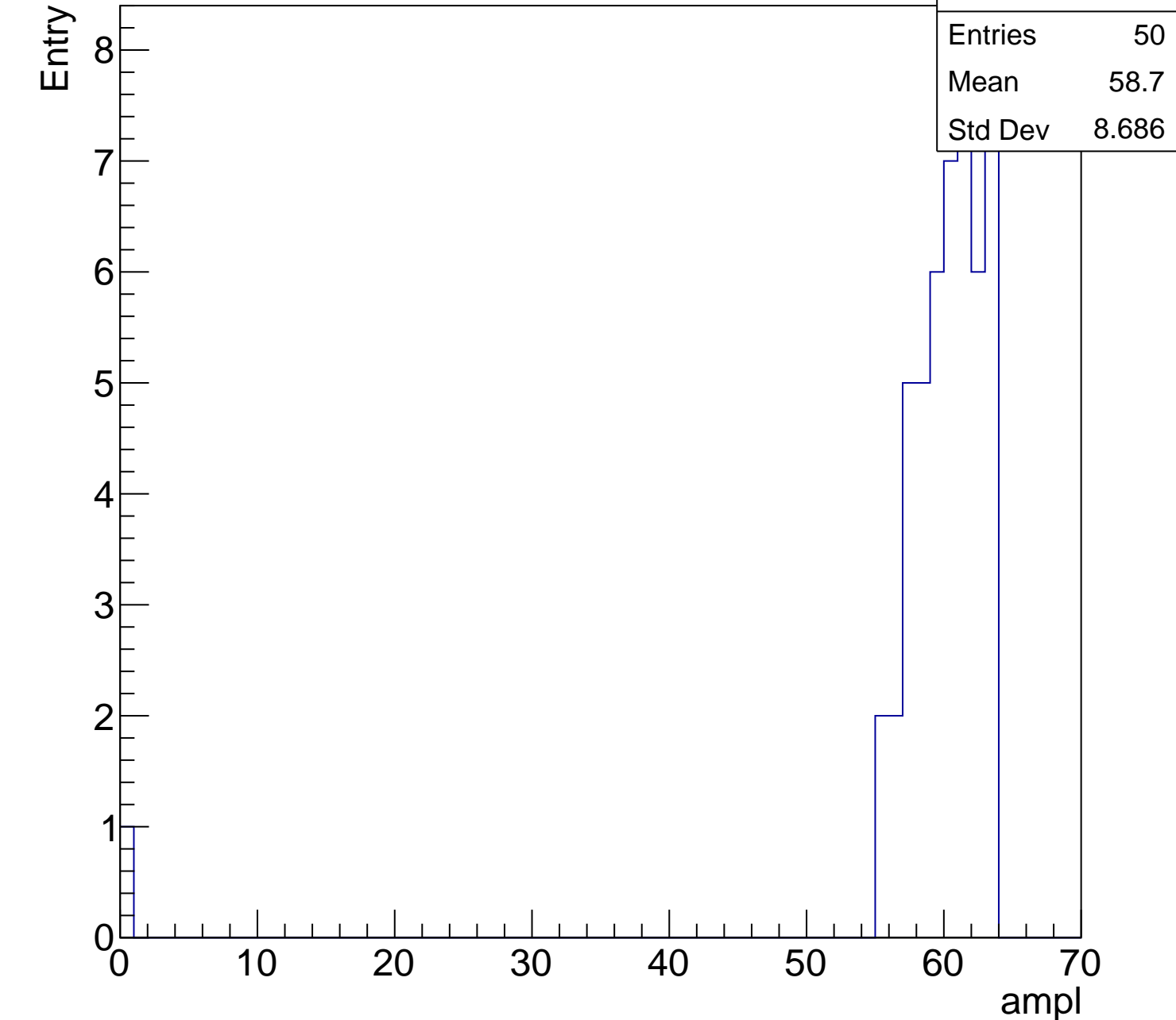
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	58.7
Std Dev	8.686

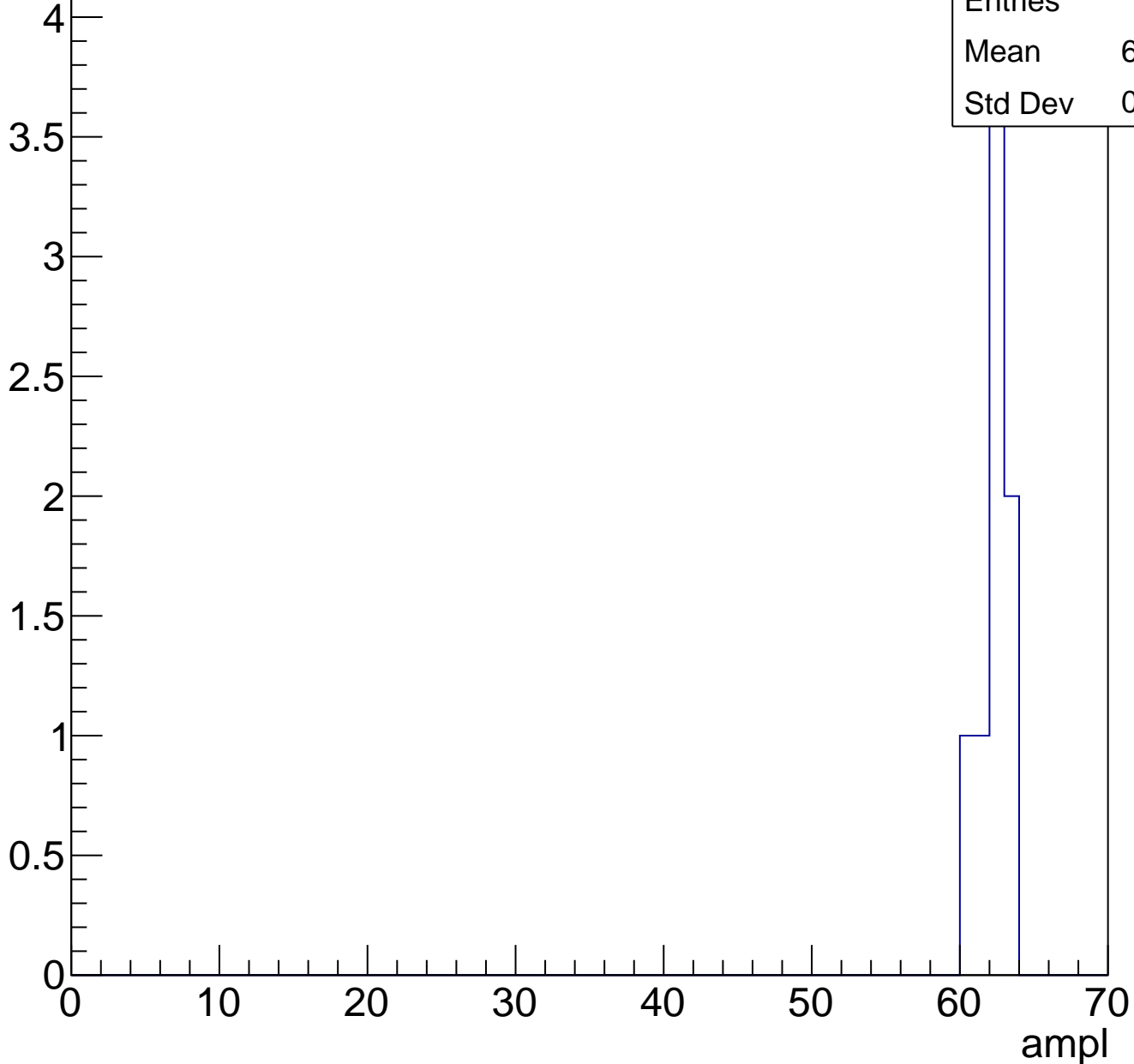
ampl



# B0L002S, U23-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch25, adc0

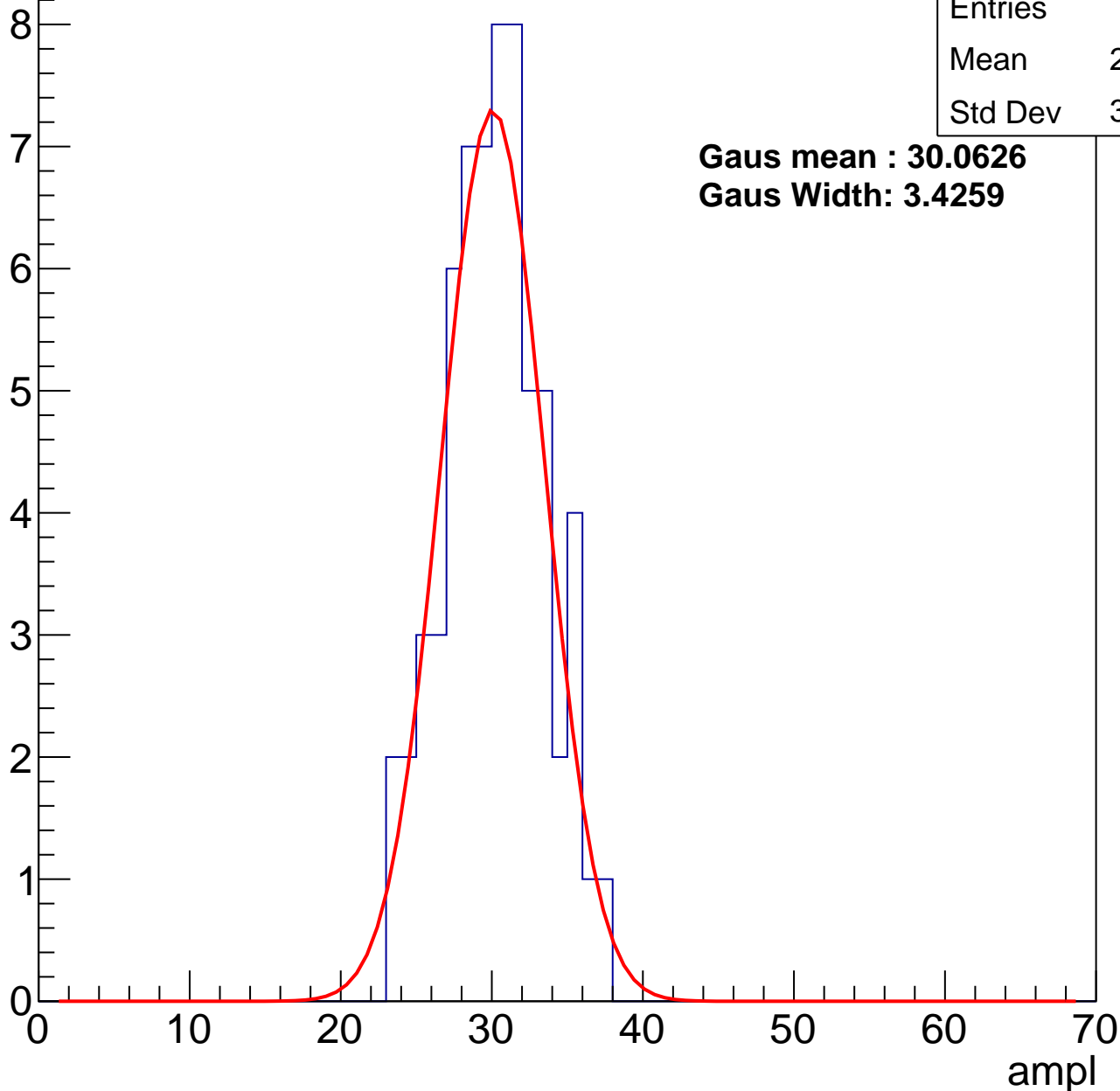
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.72
Std Dev	3.228

**Gaus mean : 30.0626**

**Gaus Width: 3.4259**



# B0L002S, U23-ch25, adc1

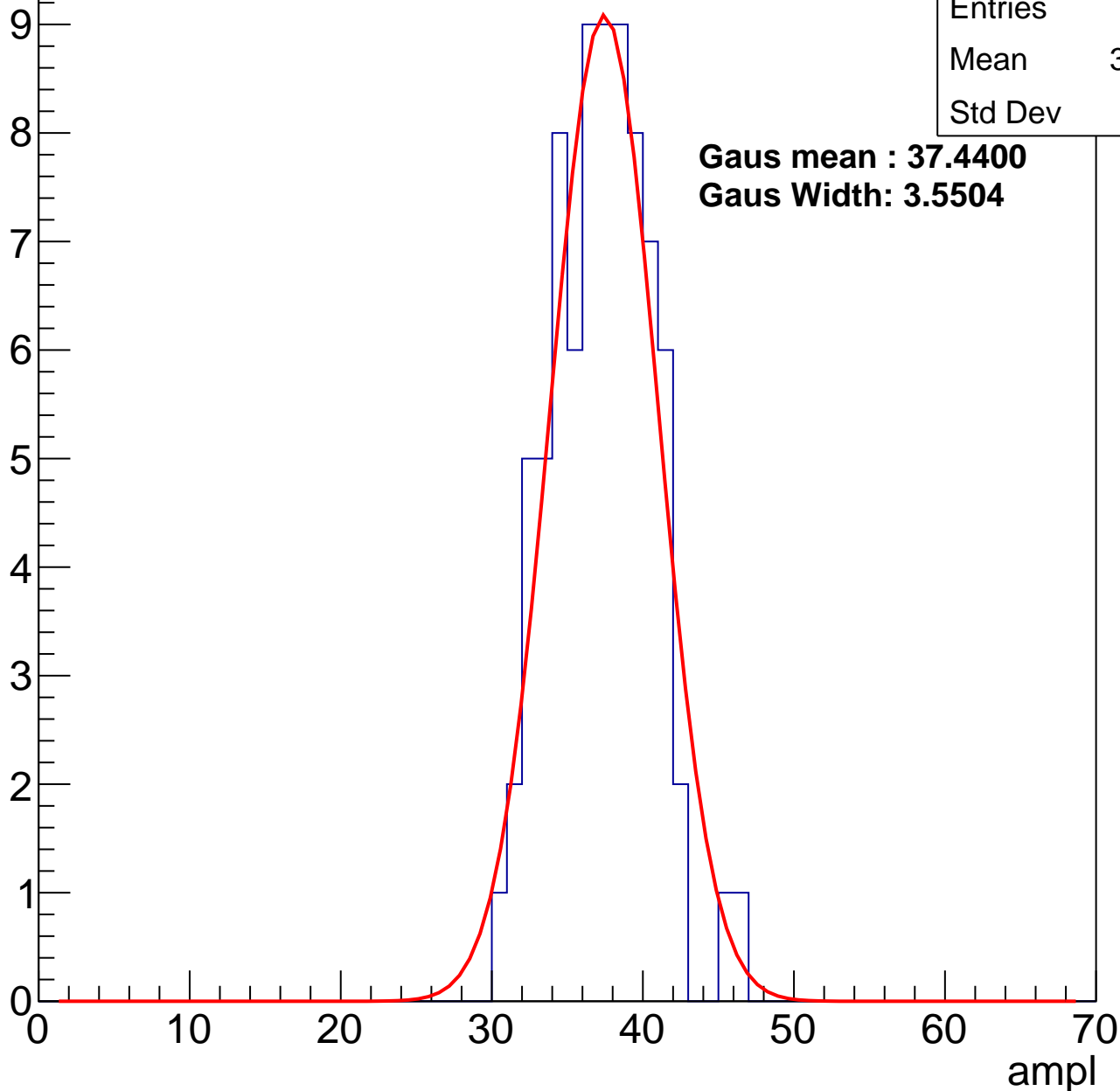
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	36.85
Std Dev	3.23

**Gaus mean : 37.4400**

**Gaus Width: 3.5504**



# B0L002S, U23-ch25, adc2

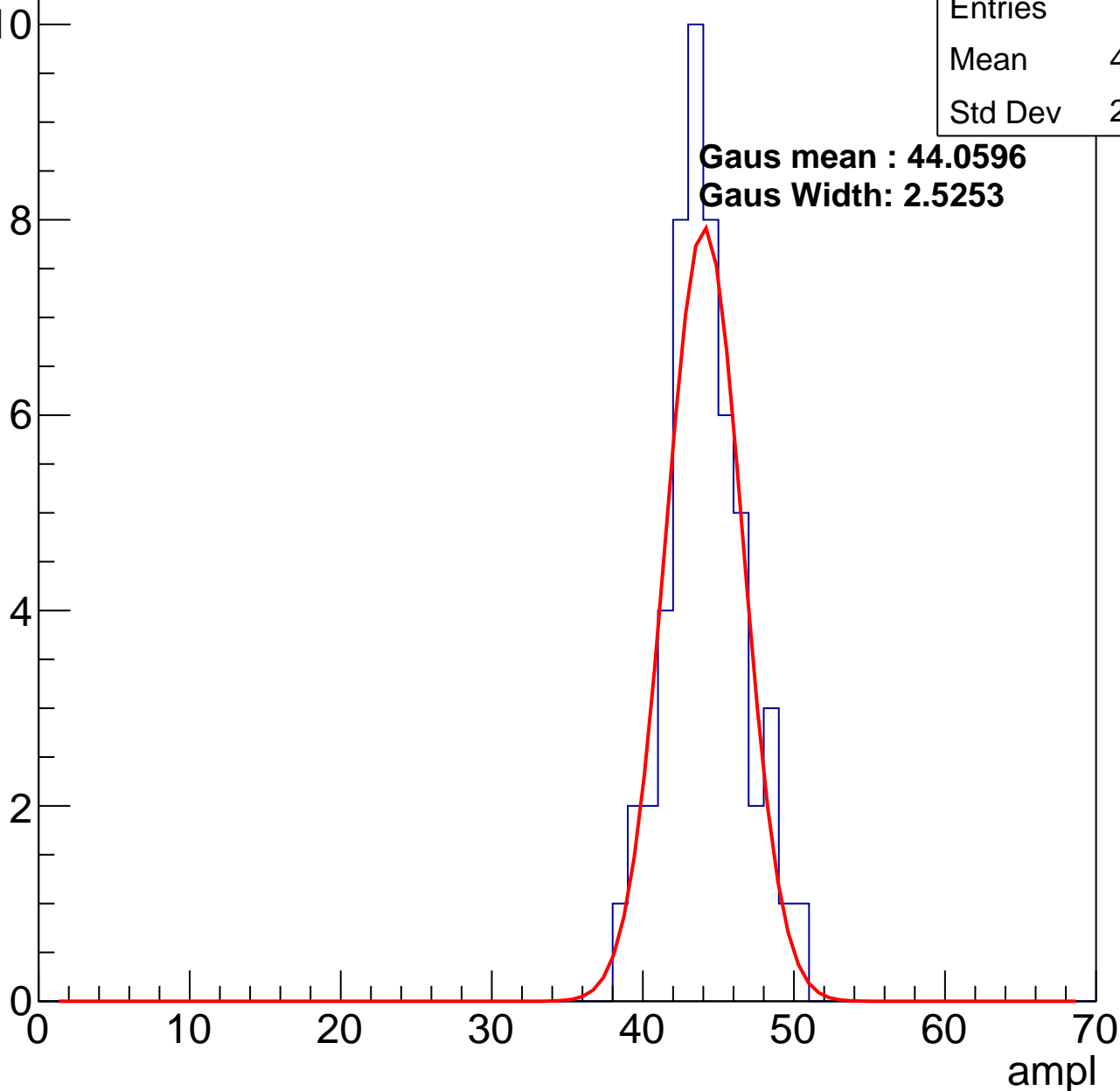
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.68
Std Dev	2.546

**Gaus mean : 44.0596**

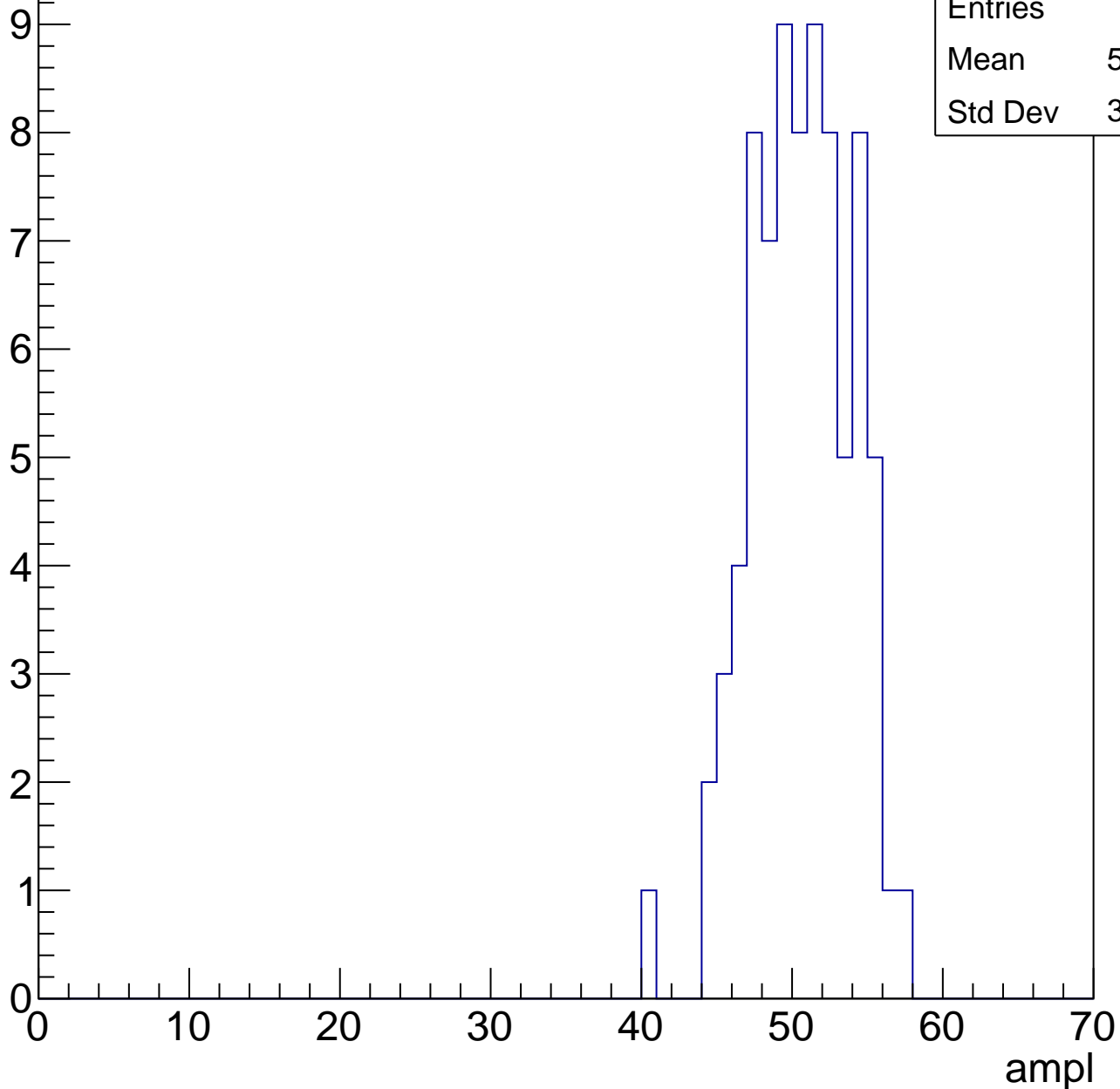
**Gaus Width: 2.5253**



# B0L002S, U23-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

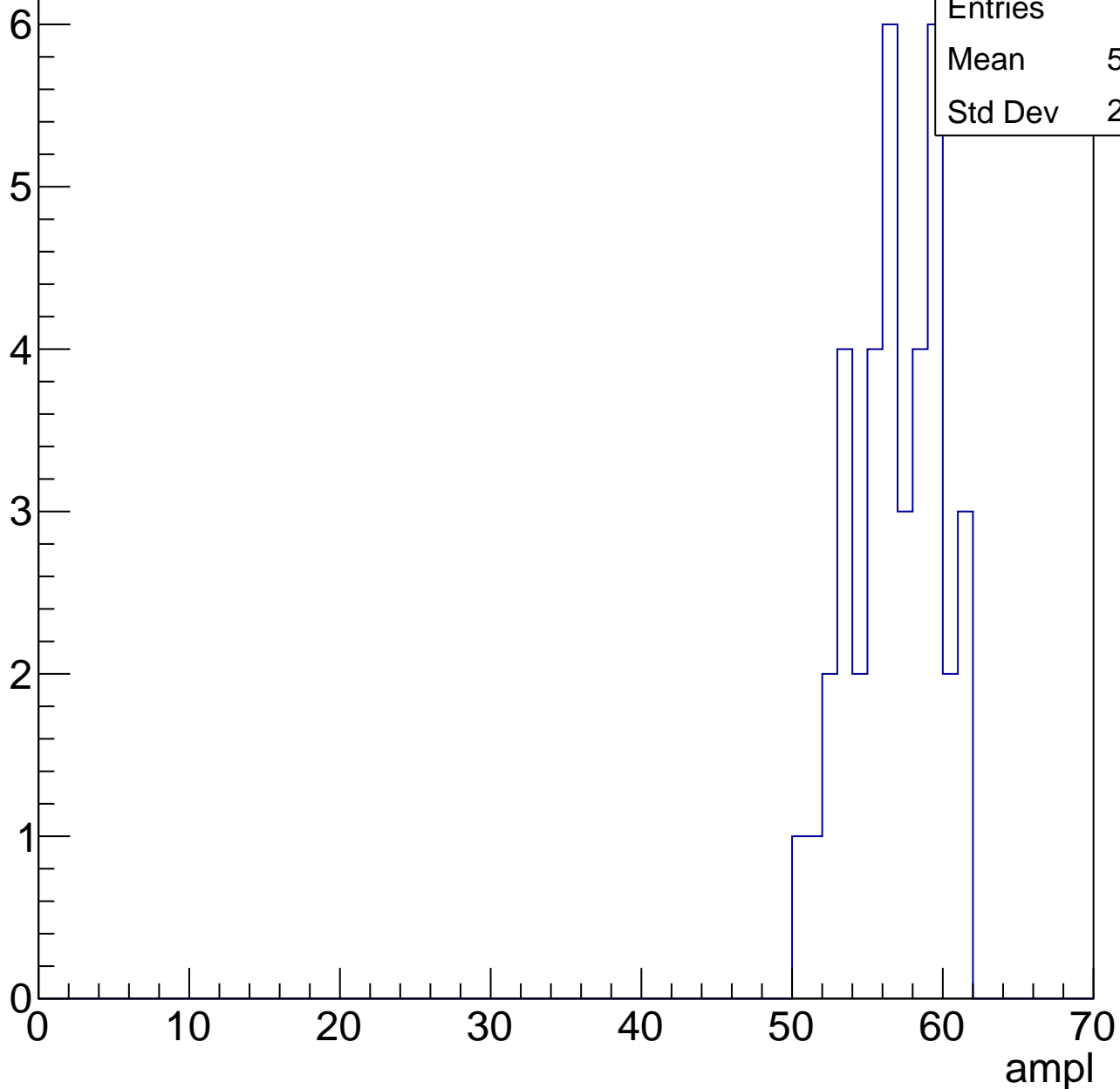


# B0L002S, U23-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	56.34
Std Dev	2.886

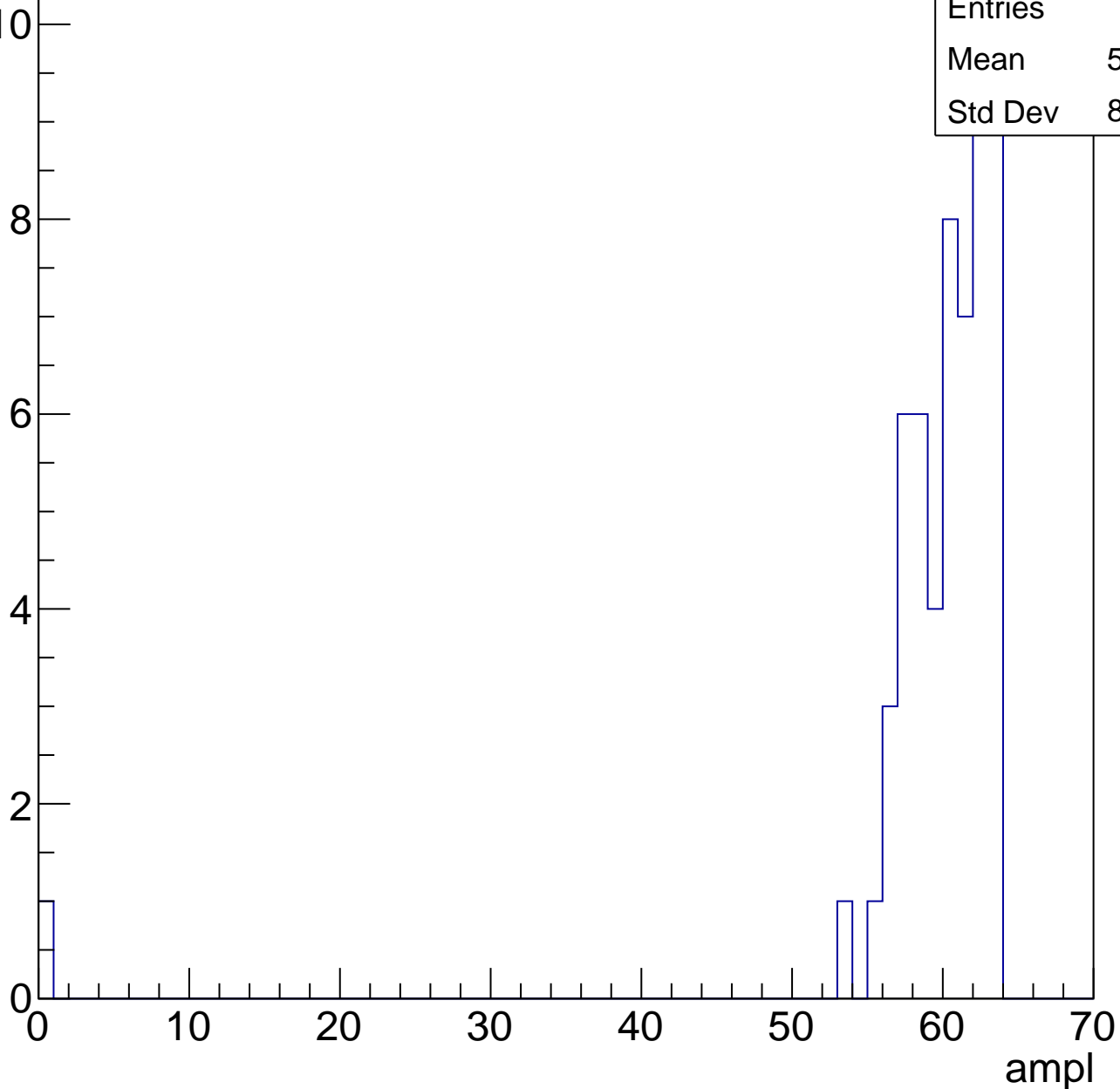


# B0L002S, U23-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	58.93
Std Dev	8.246



# B0L002S, U23-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L002S, U23-ch26, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	76
Mean	29.82
Std Dev	4.801

**Gaus mean : 31.0176**

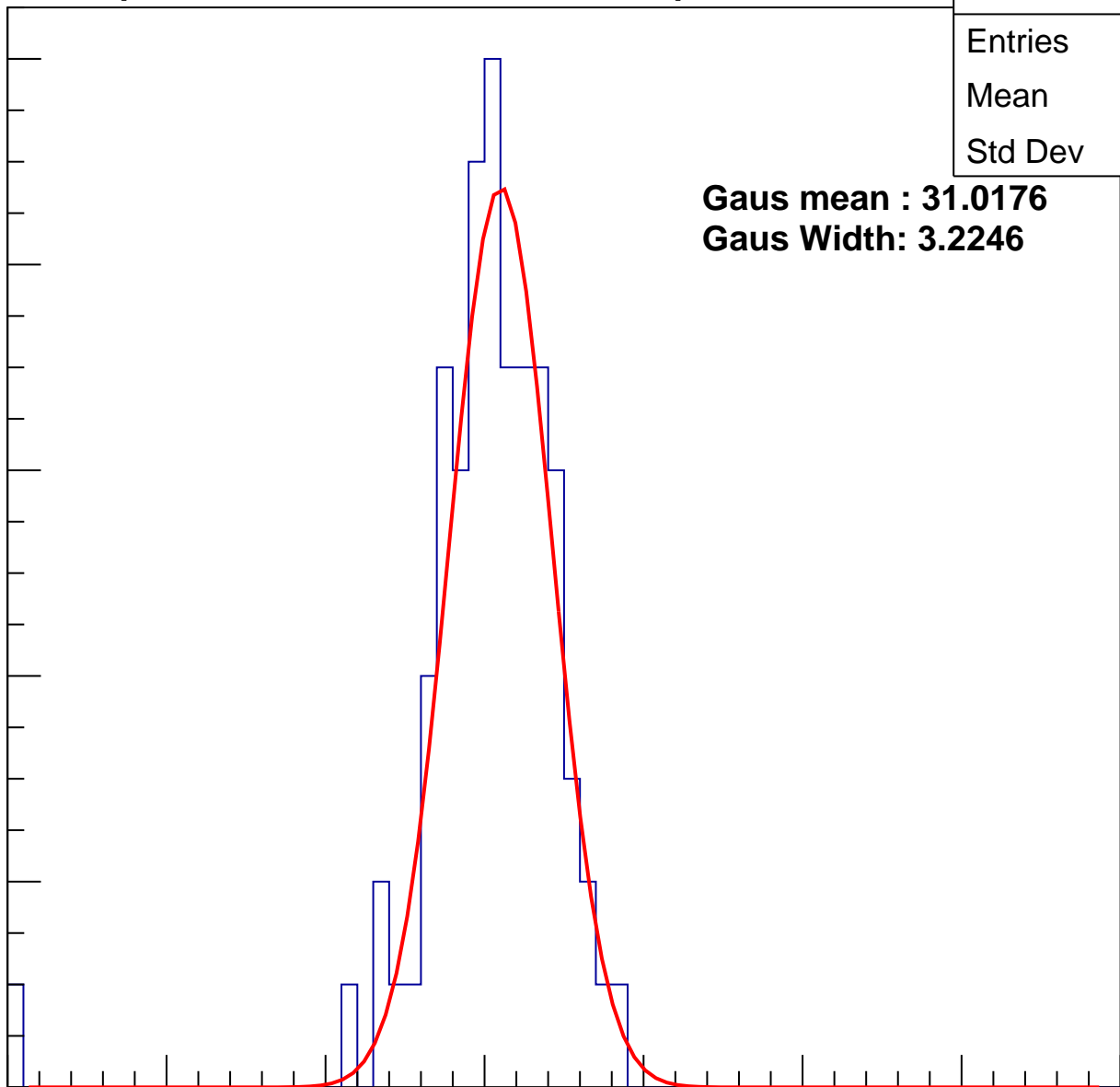
**Gaus Width: 3.2246**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch26, adc1

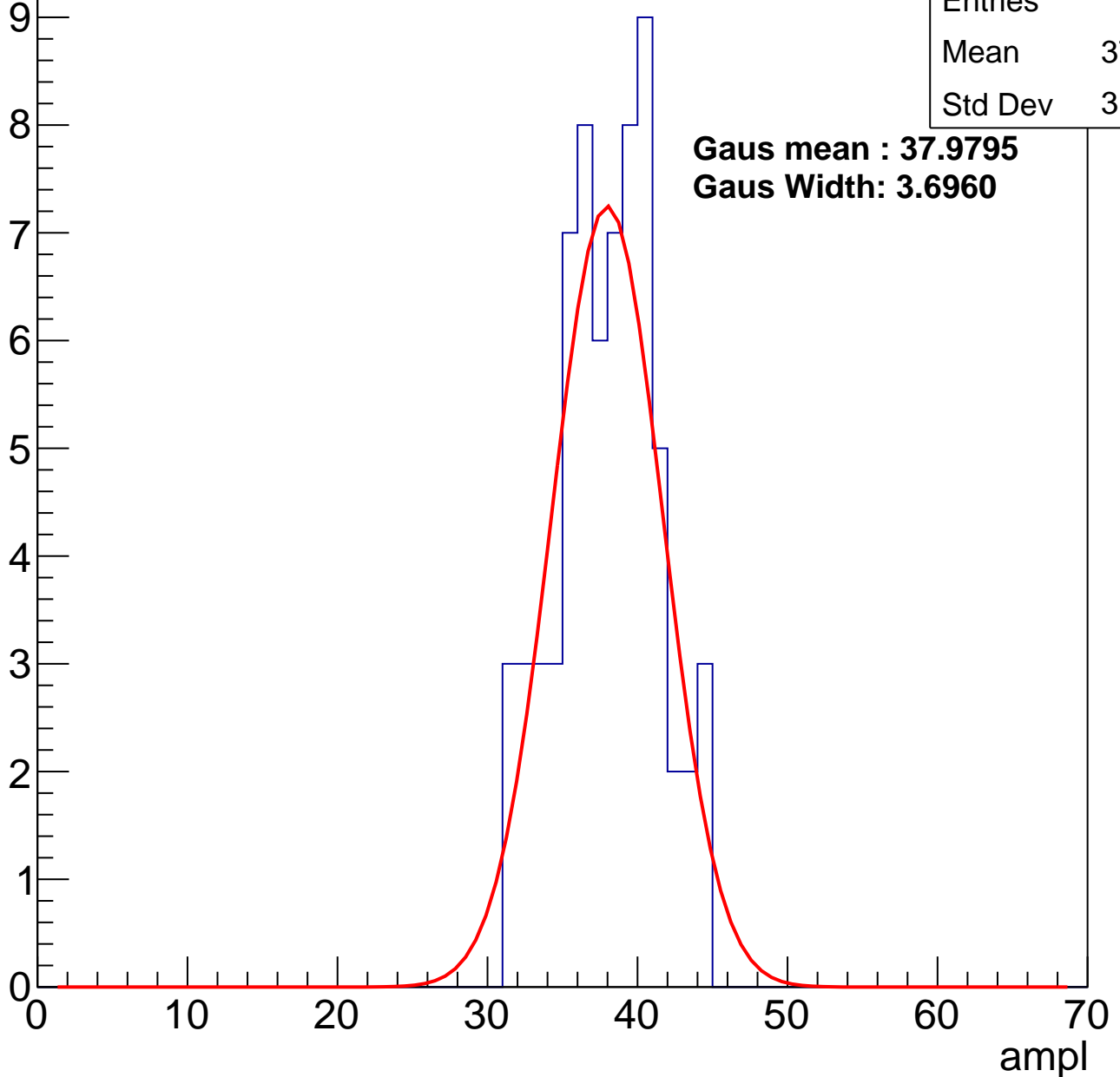
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.54
Std Dev	3.282

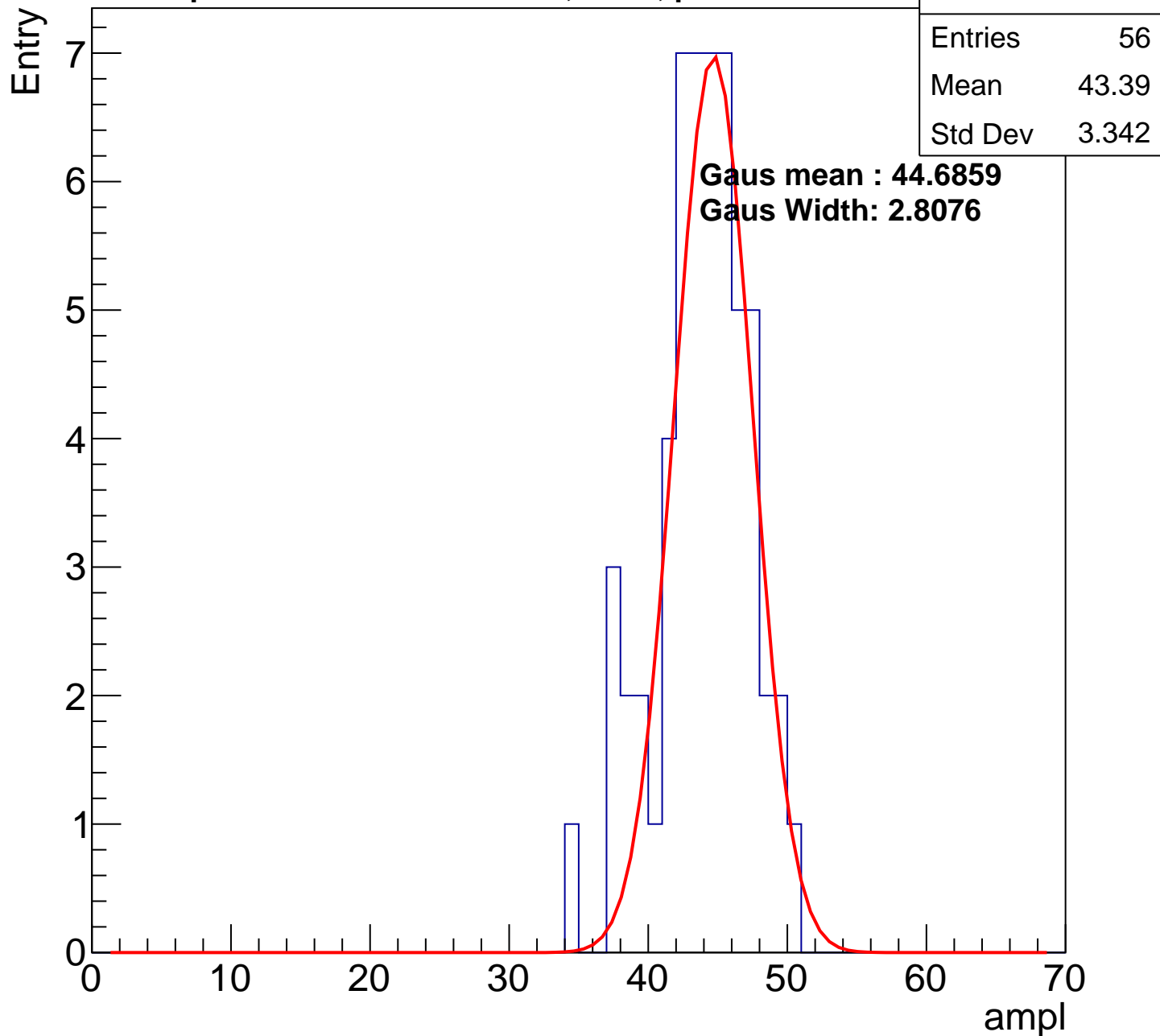
**Gaus mean : 37.9795**

**Gaus Width: 3.6960**



# B0L002S, U23-ch26, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

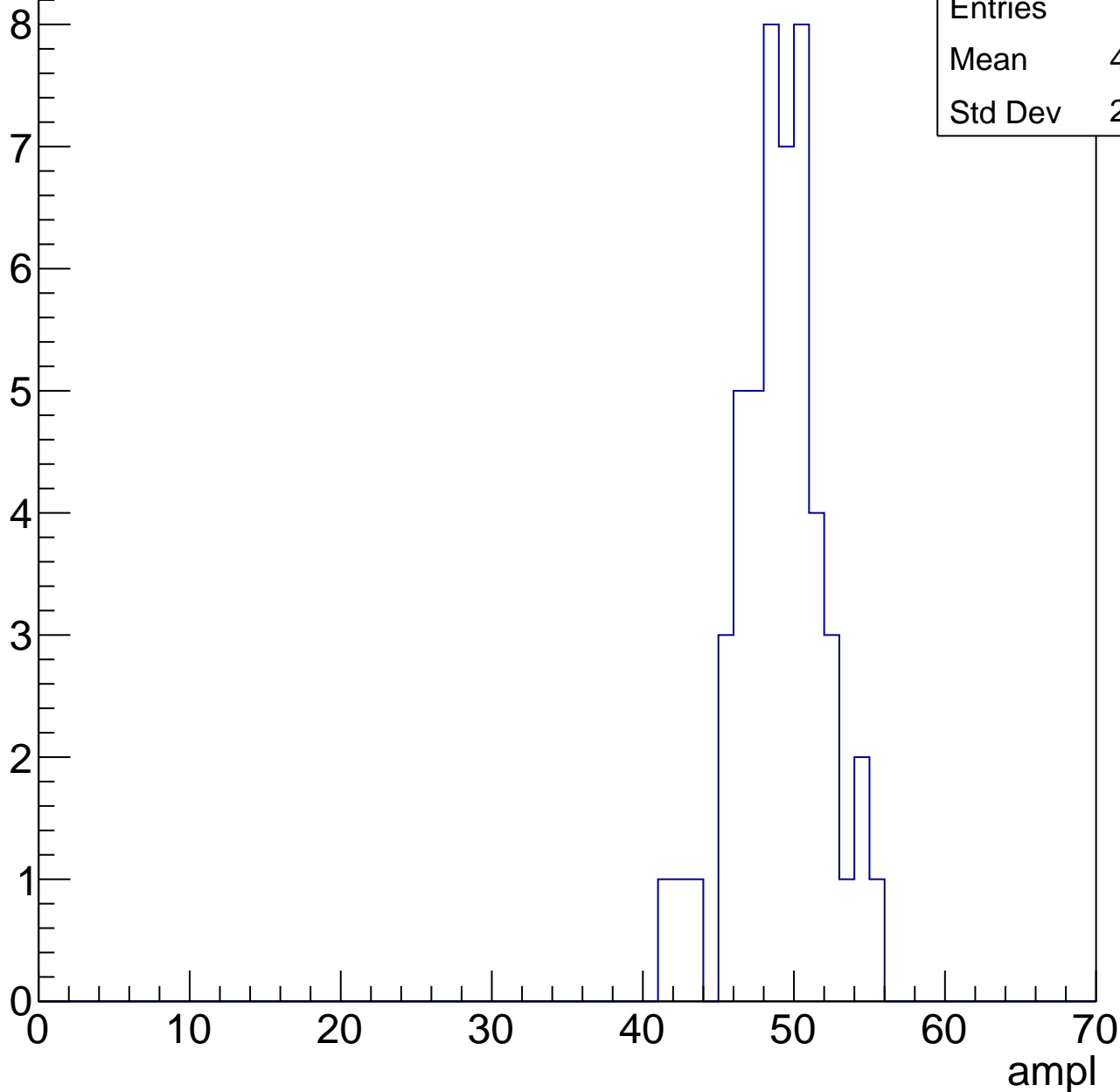


# B0L002S, U23-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	48.58
Std Dev	2.878

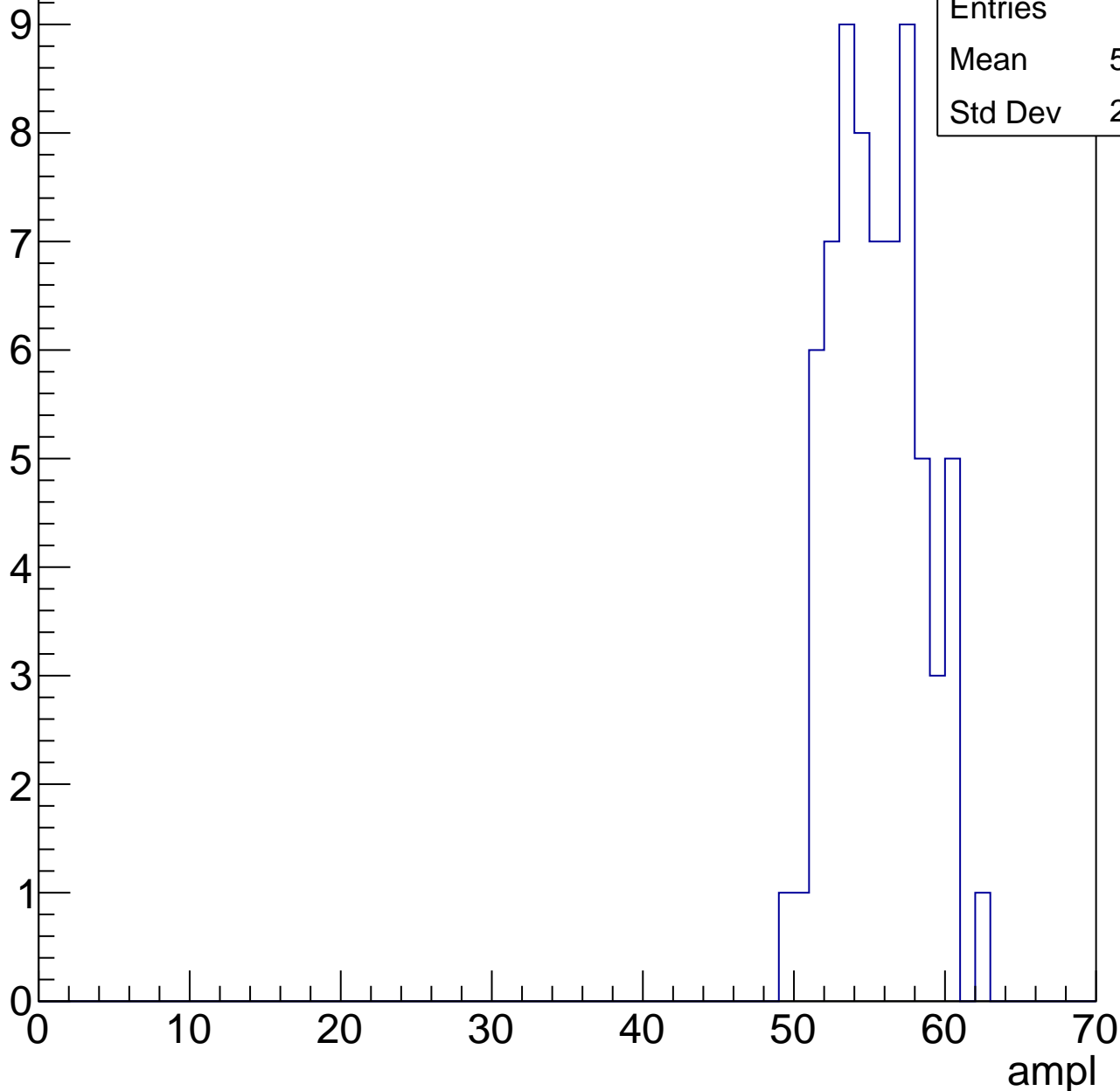


# B0L002S, U23-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

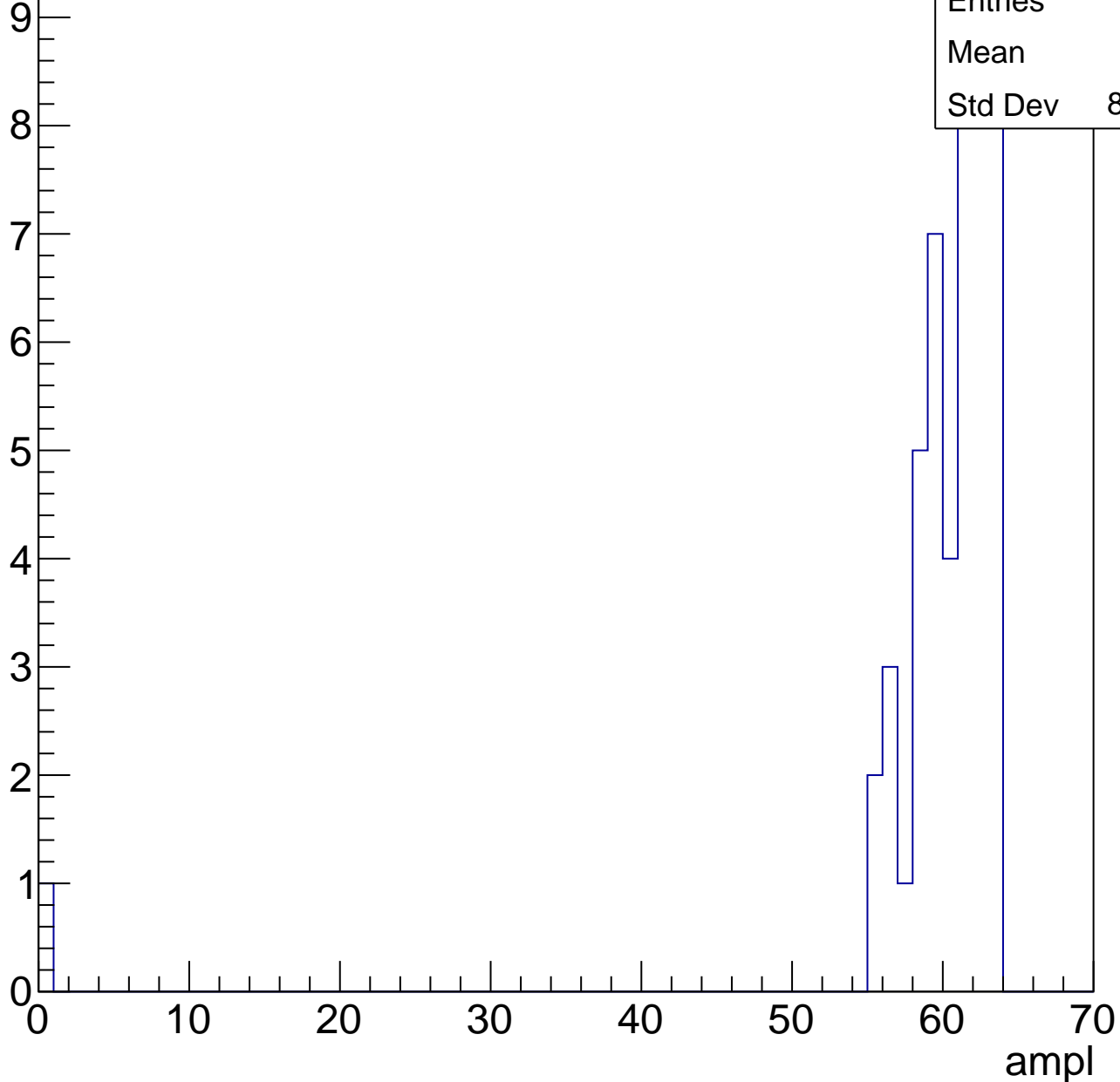
Entries	69
Mean	55.03
Std Dev	2.884



# B0L002S, U23-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

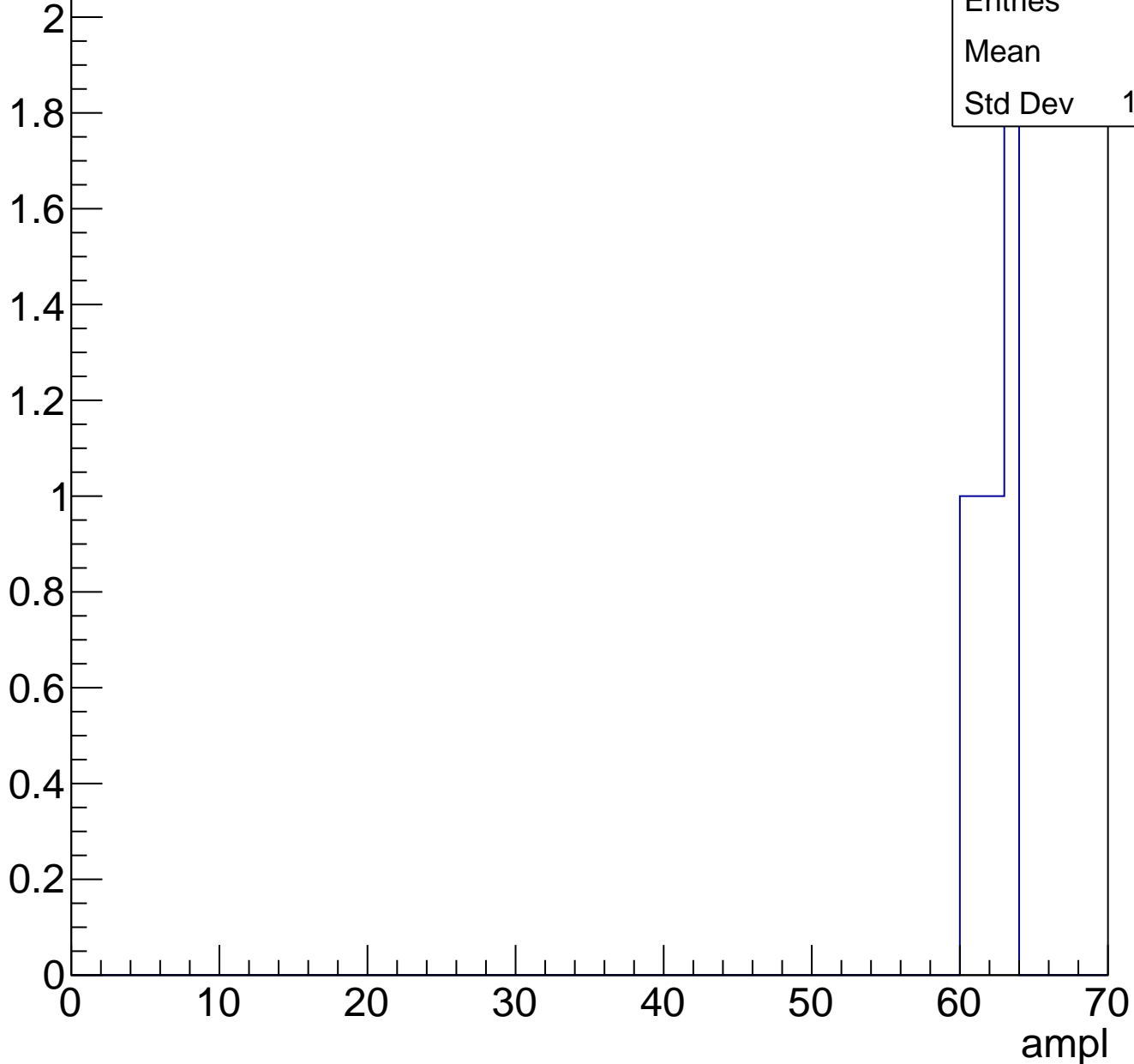
Entry



# B0L002S, U23-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	68
Mean	29.06
Std Dev	4.684

**Gaus mean : 29.7463**

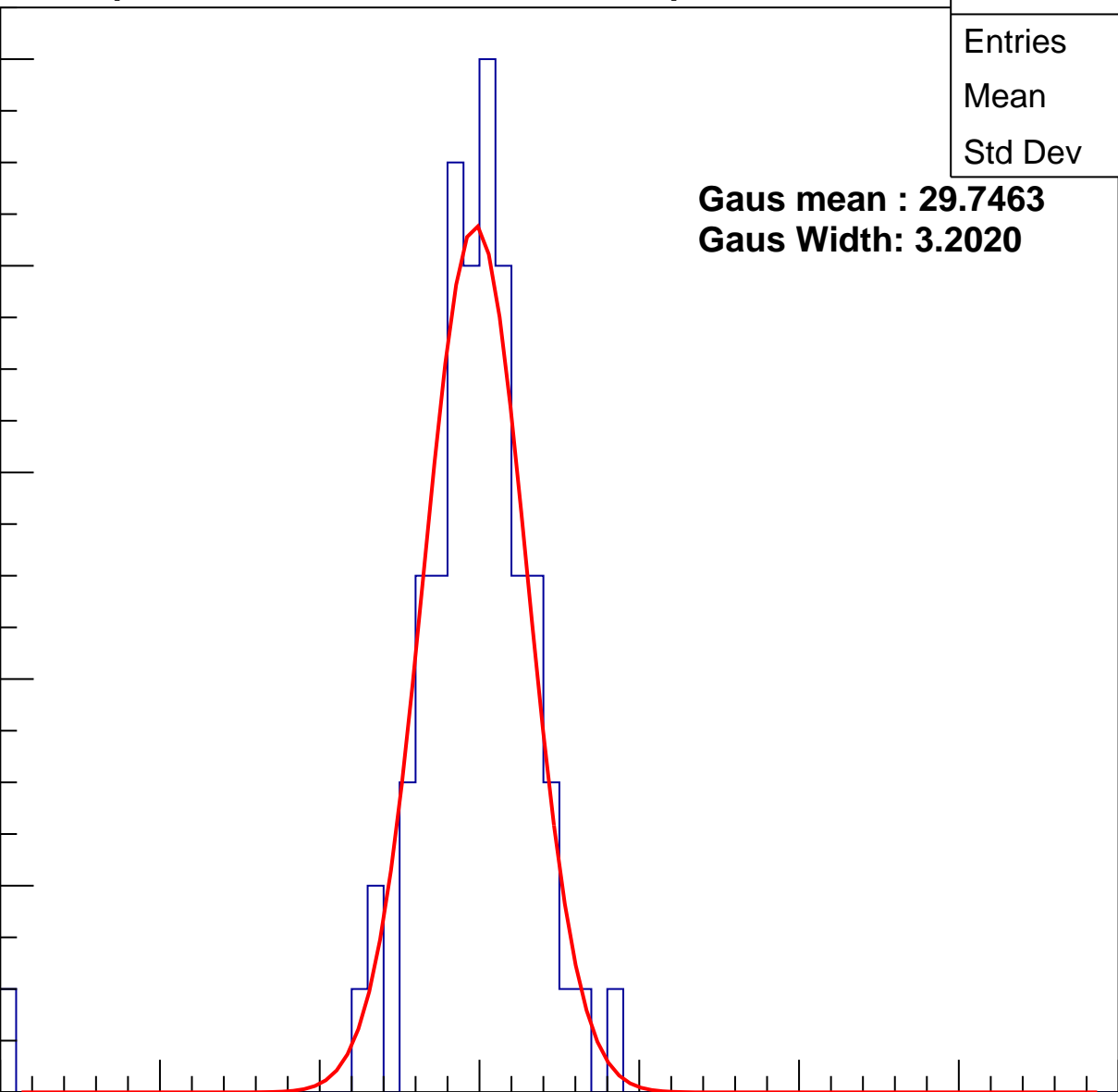
**Gaus Width: 3.2020**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch27, adc1

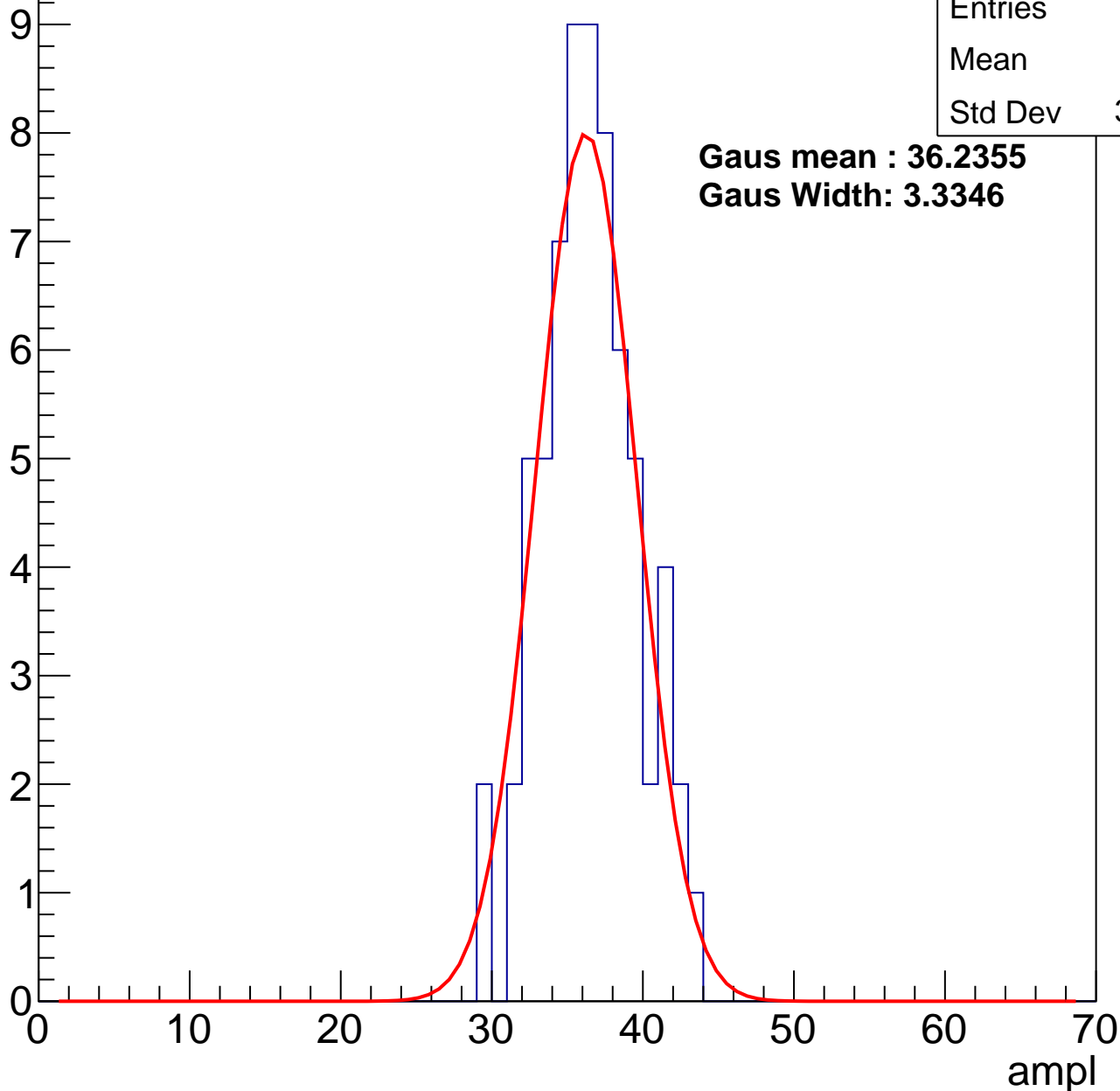
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36
Std Dev	3.091

**Gaus mean : 36.2355**

**Gaus Width: 3.3346**



# B0L002S, U23-ch27, adc2

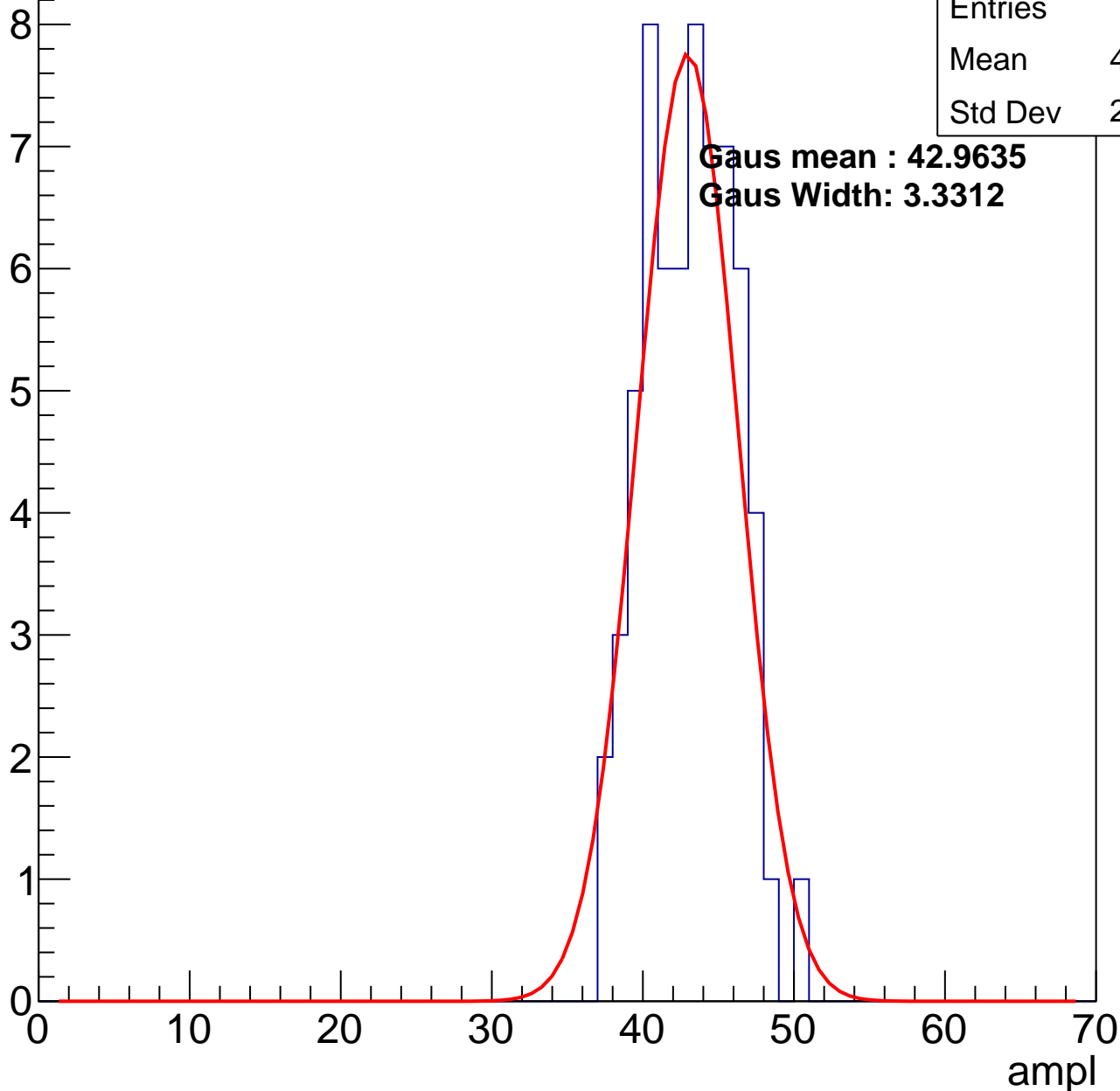
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.66
Std Dev	2.927

**Gaus mean : 42.9635**

**Gaus Width: 3.3312**



# B0L002S, U23-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

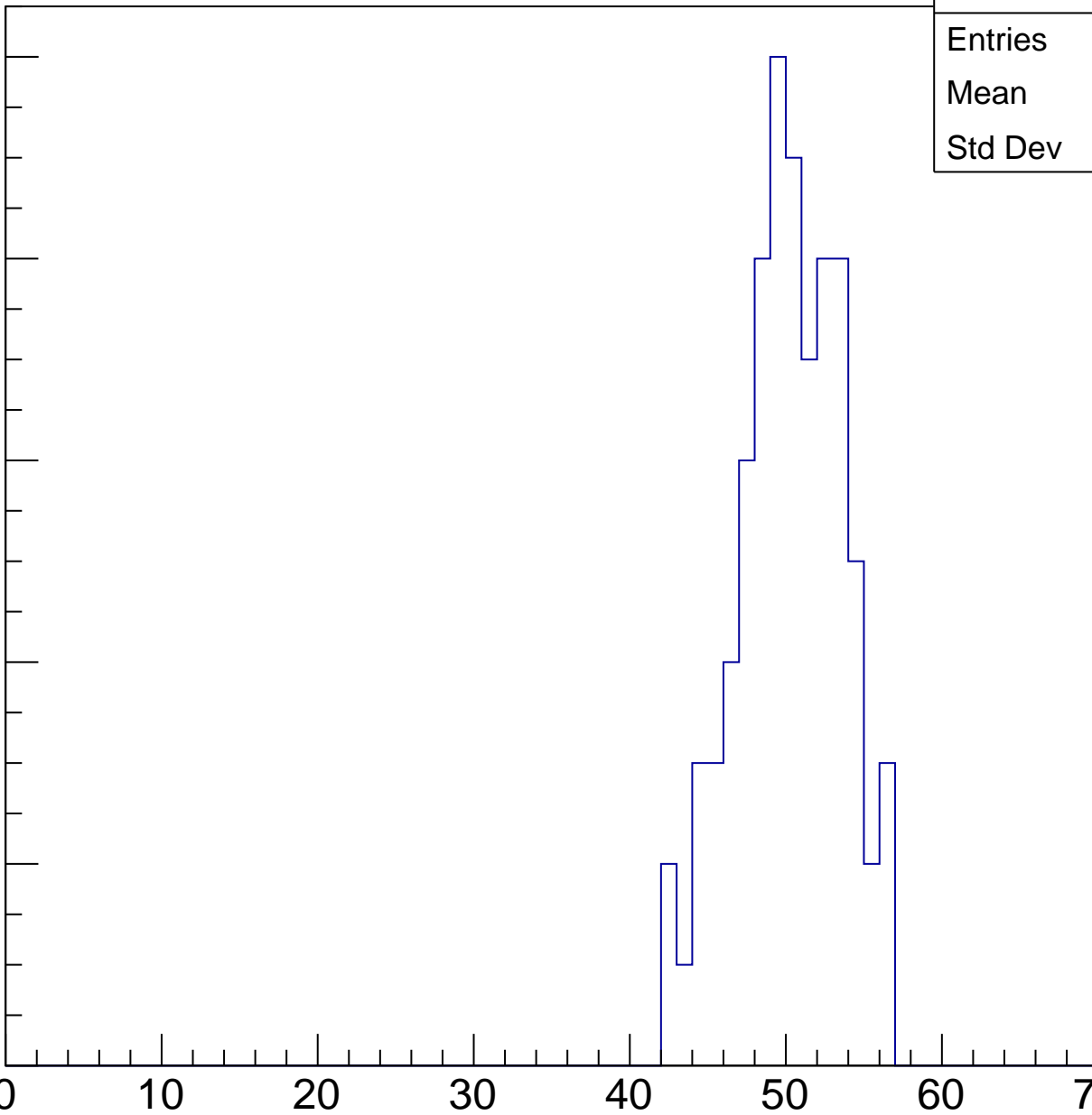
Entries	79
Mean	49.73
Std Dev	3.306

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

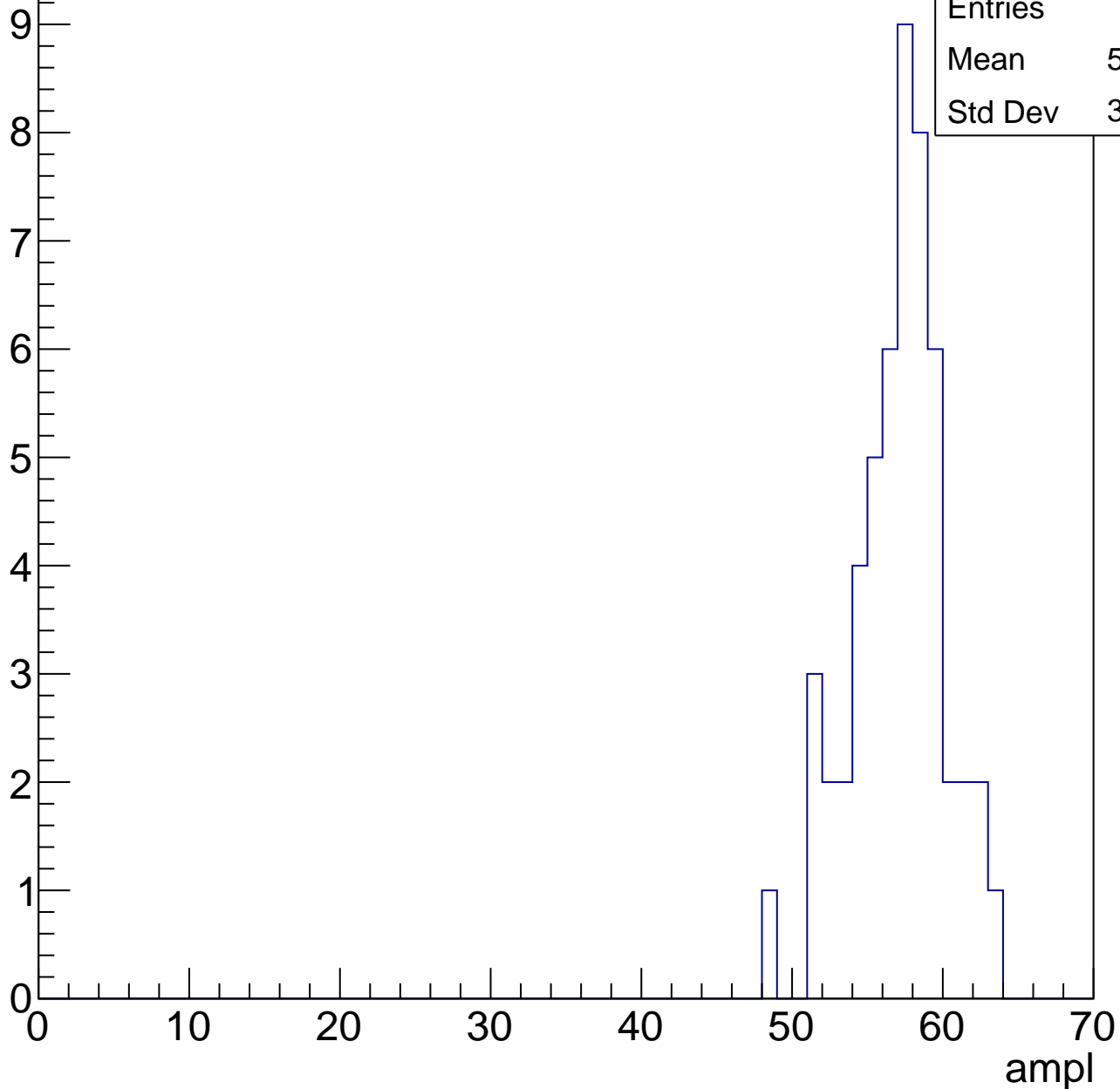
ampl



# B0L002S, U23-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

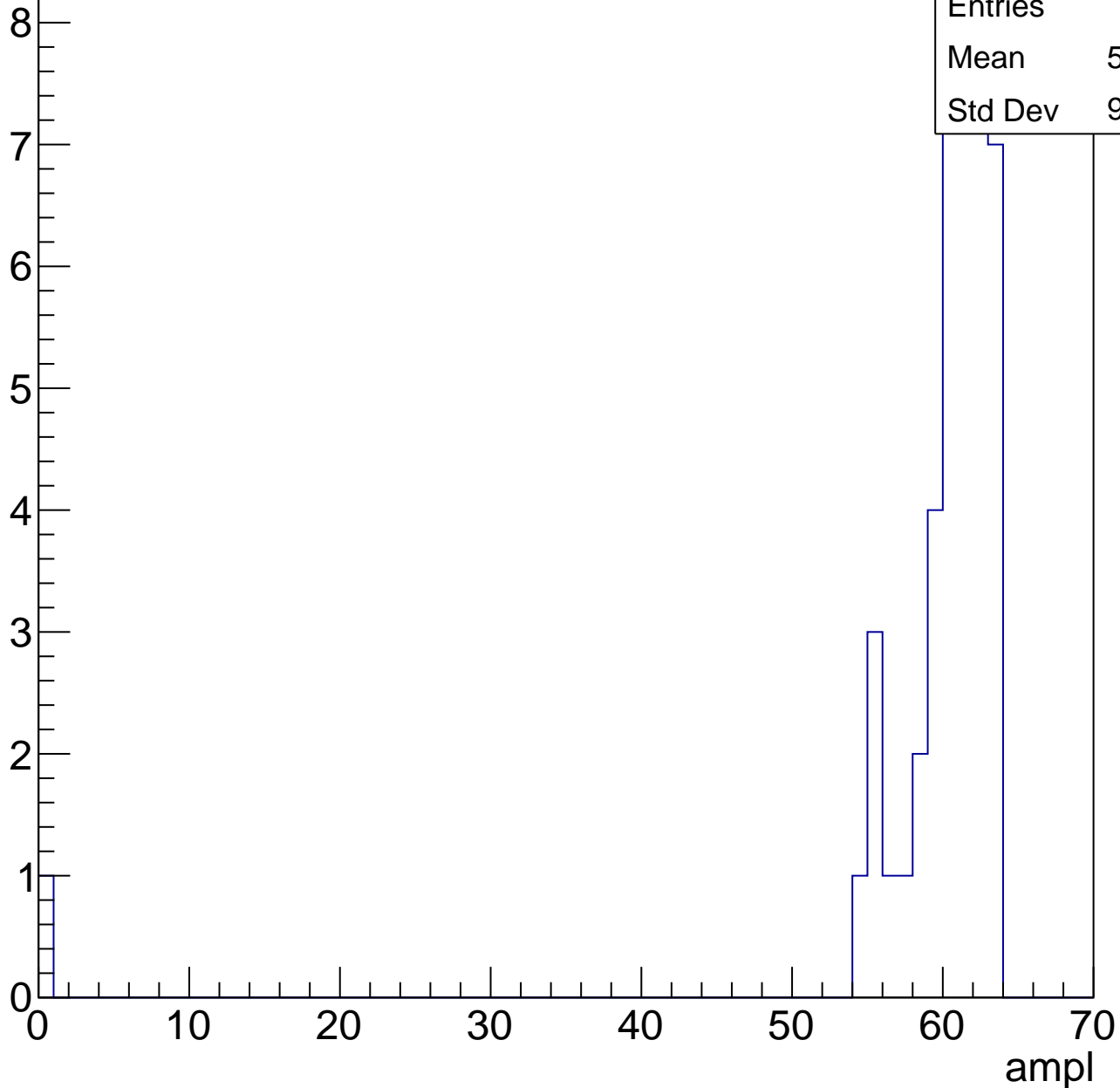
Entry



# B0L002S, U23-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	44
Mean	58.84
Std Dev	9.283

# B0L002S, U23-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch28, adc0

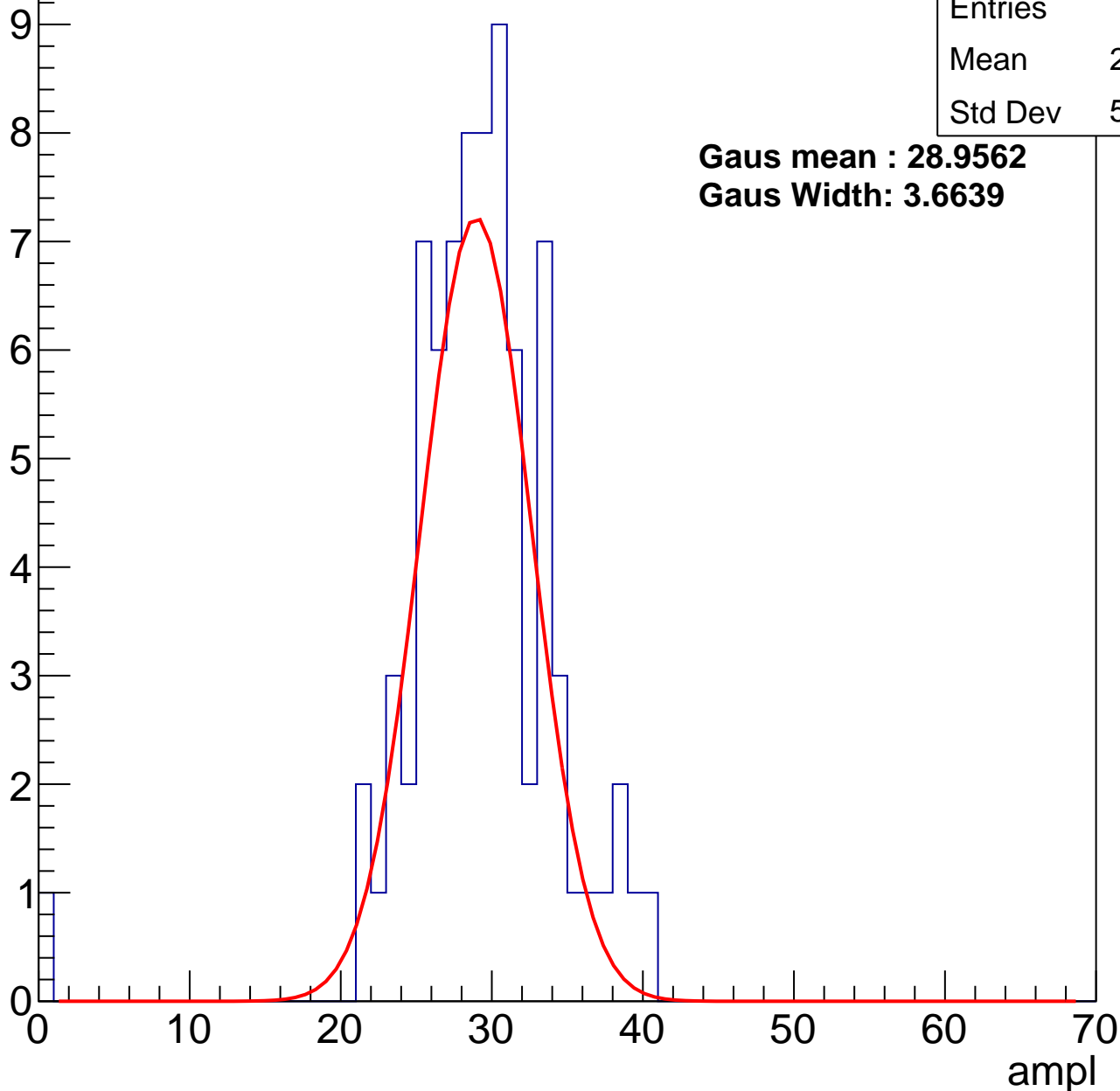
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	28.77
Std Dev	5.209

**Gaus mean : 28.9562**

**Gaus Width: 3.6639**



# B0L002S, U23-ch28, adc1

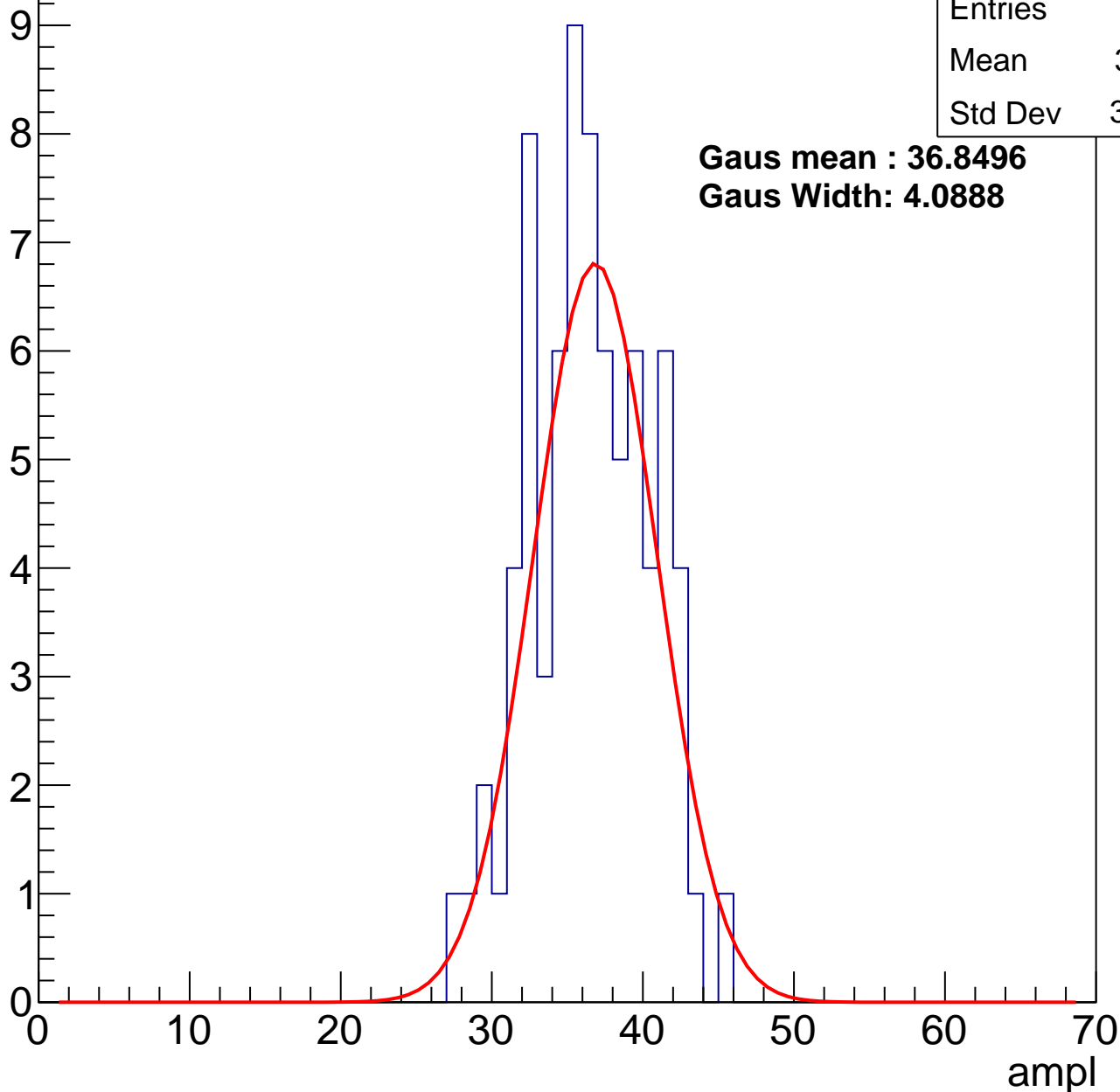
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.01
Std Dev	3.864

**Gaus mean : 36.8496**

**Gaus Width: 4.0888**



# B0L002S, U23-ch28, adc2

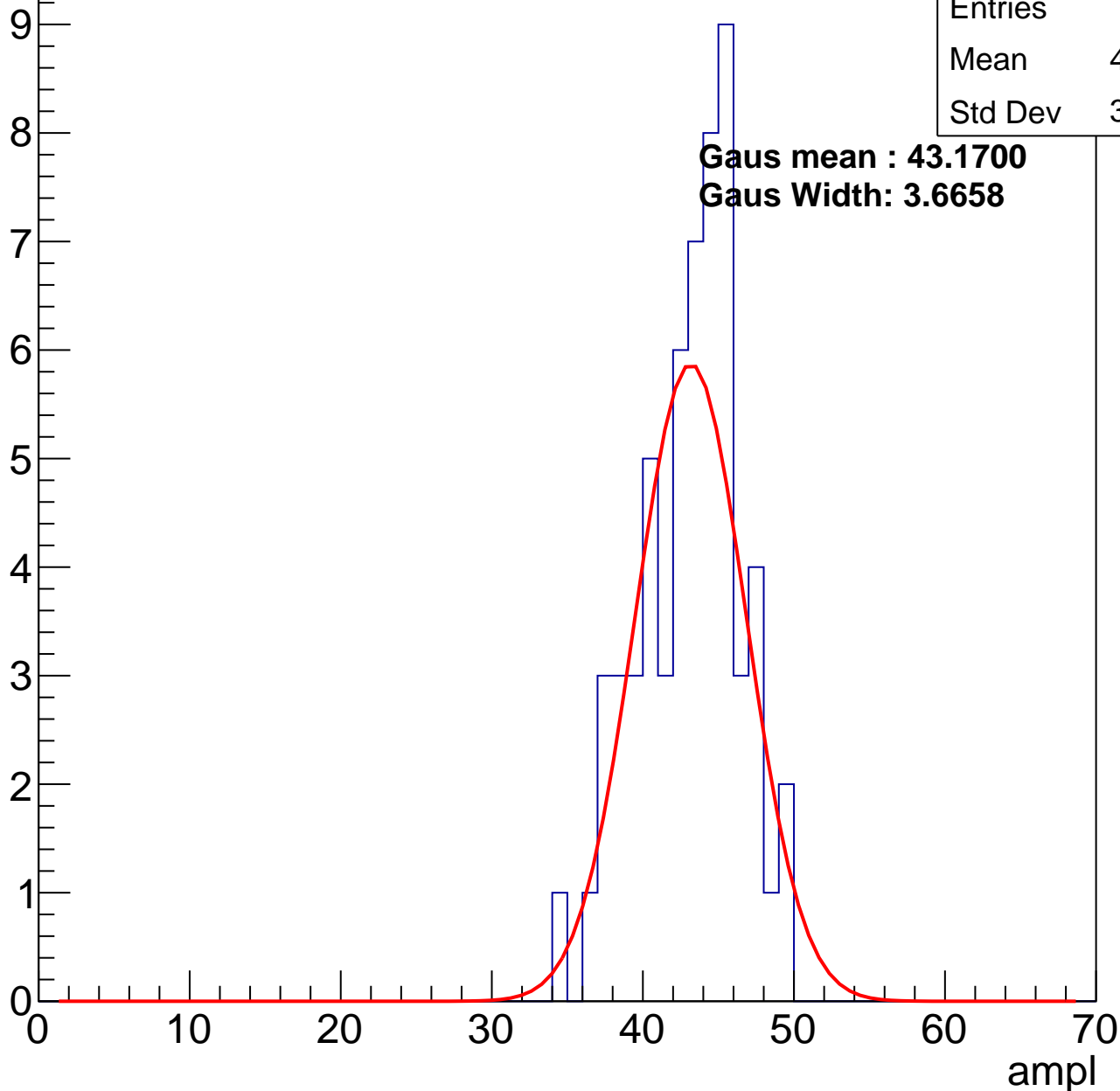
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	42.66
Std Dev	3.317

**Gaus mean : 43.1700**

**Gaus Width: 3.6658**

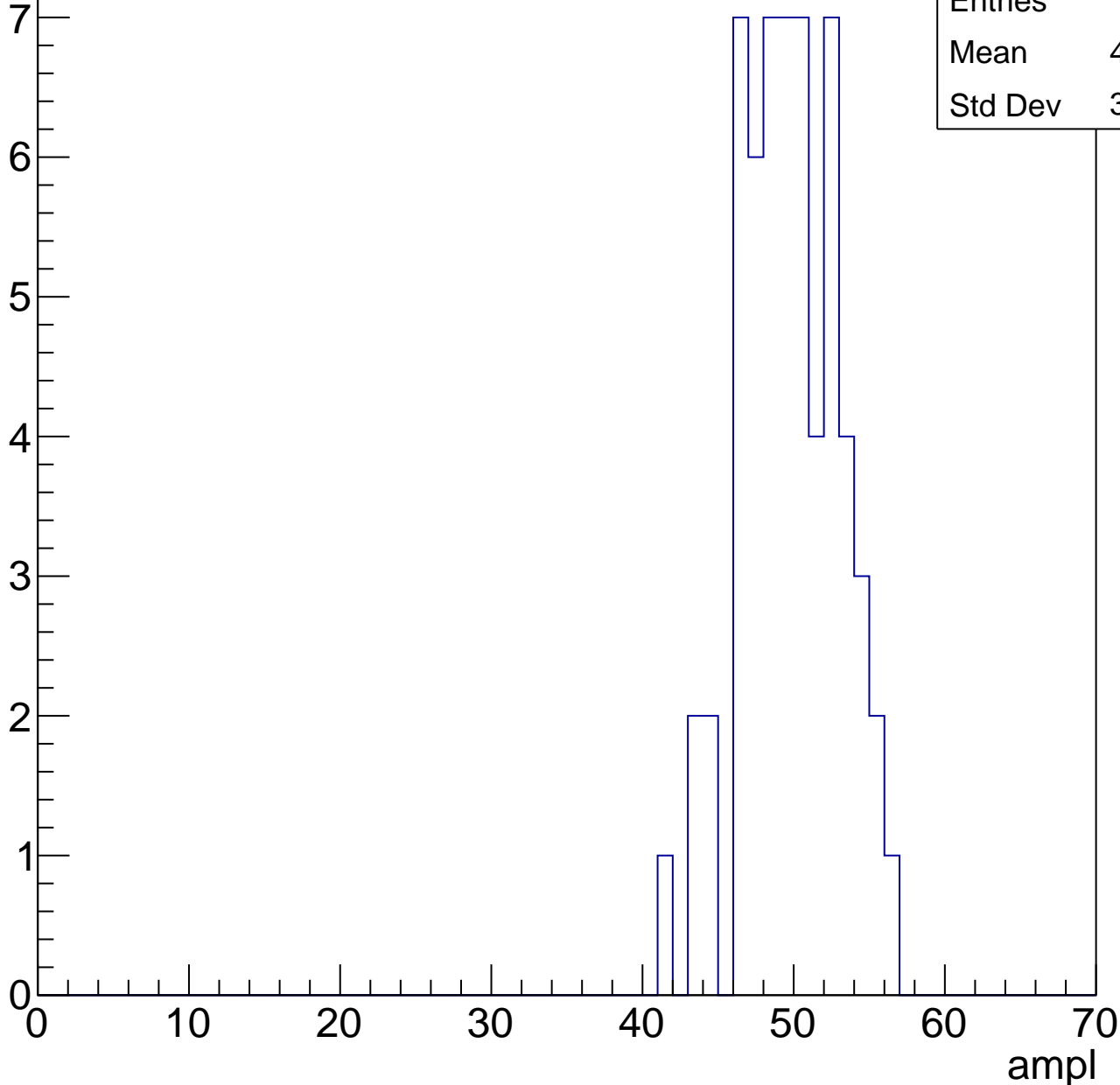


# B0L002S, U23-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

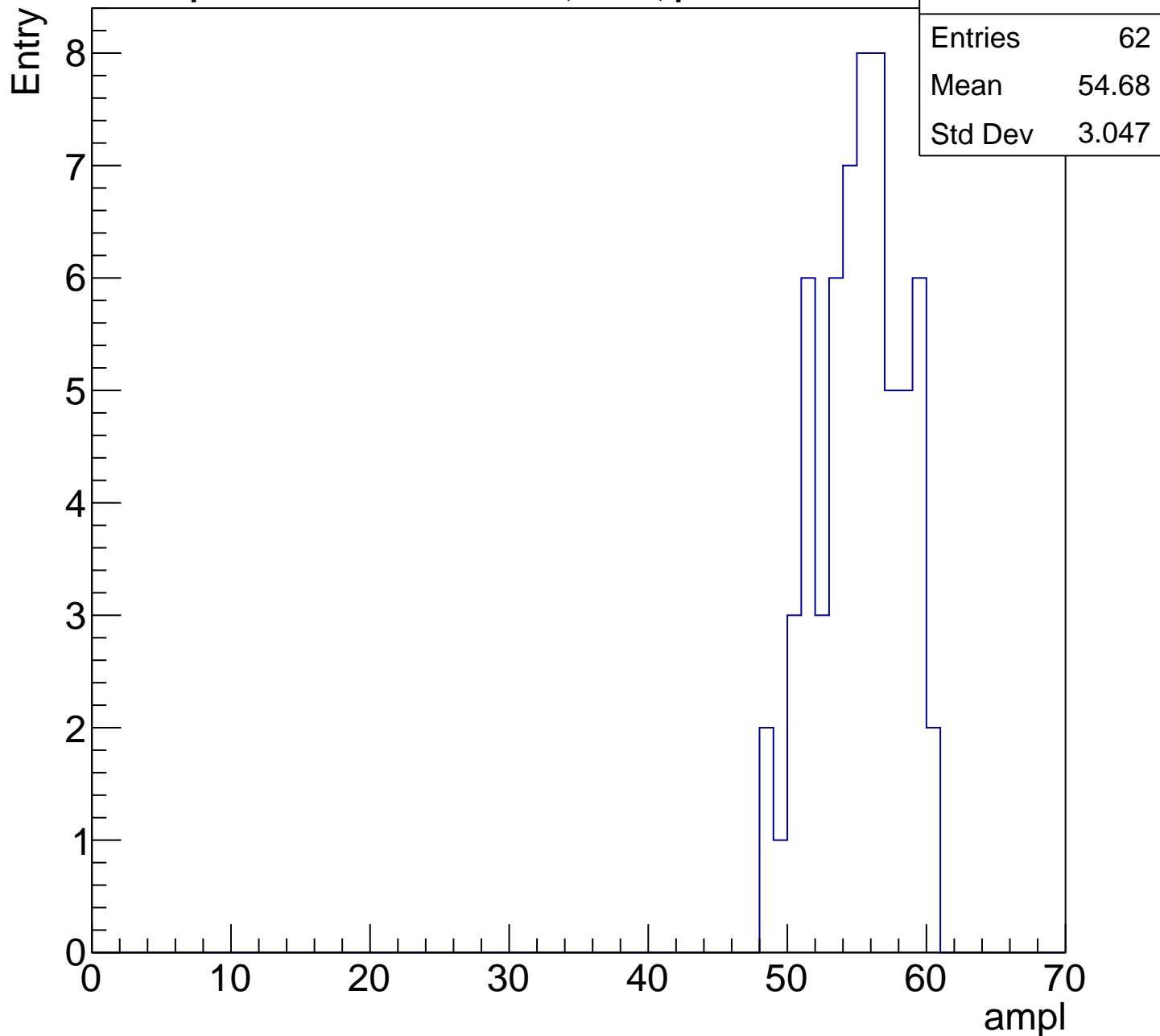
Entry

Entries	60
Mean	49.27
Std Dev	3.219



# B0L002S, U23-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

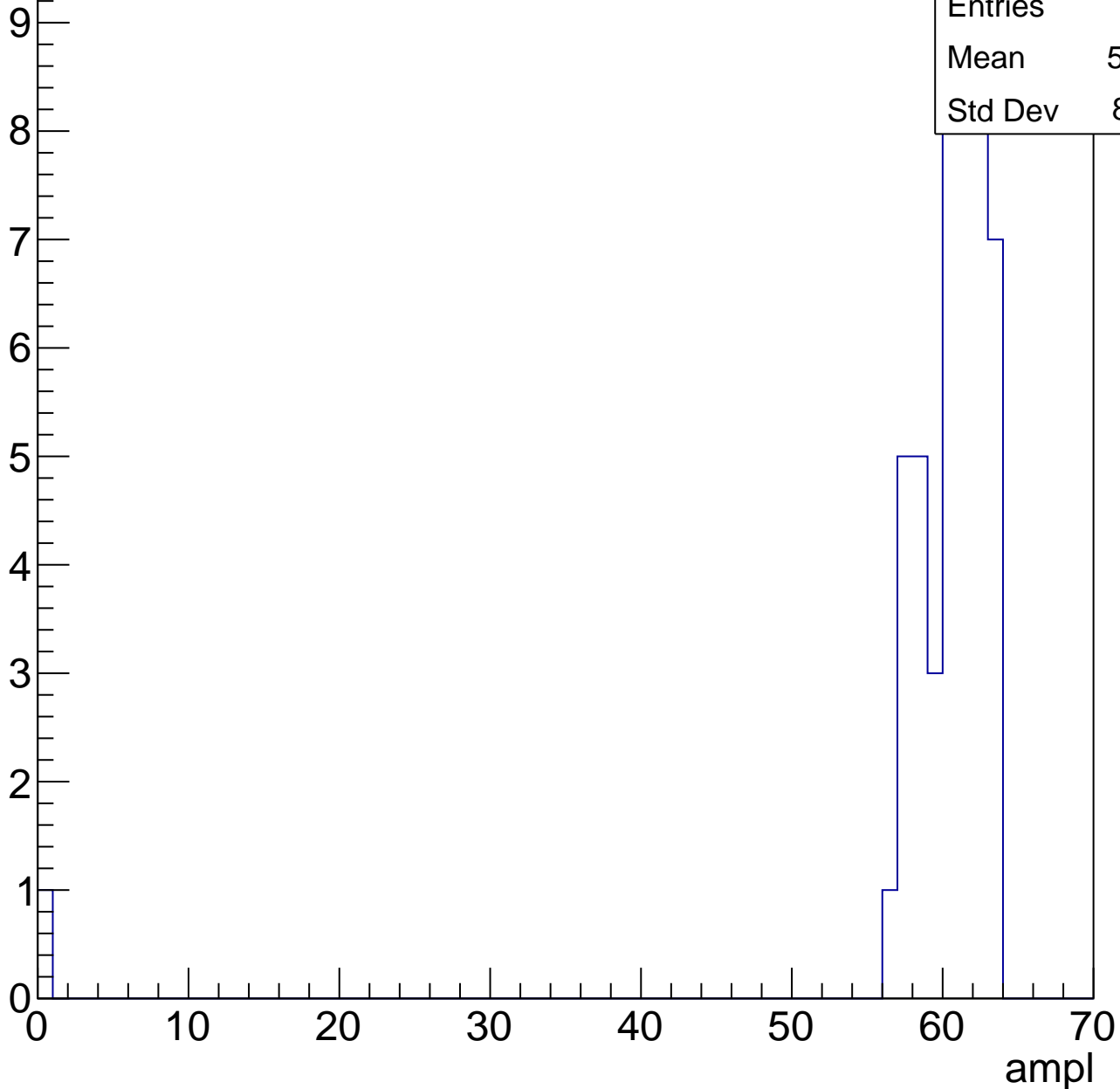


# B0L002S, U23-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

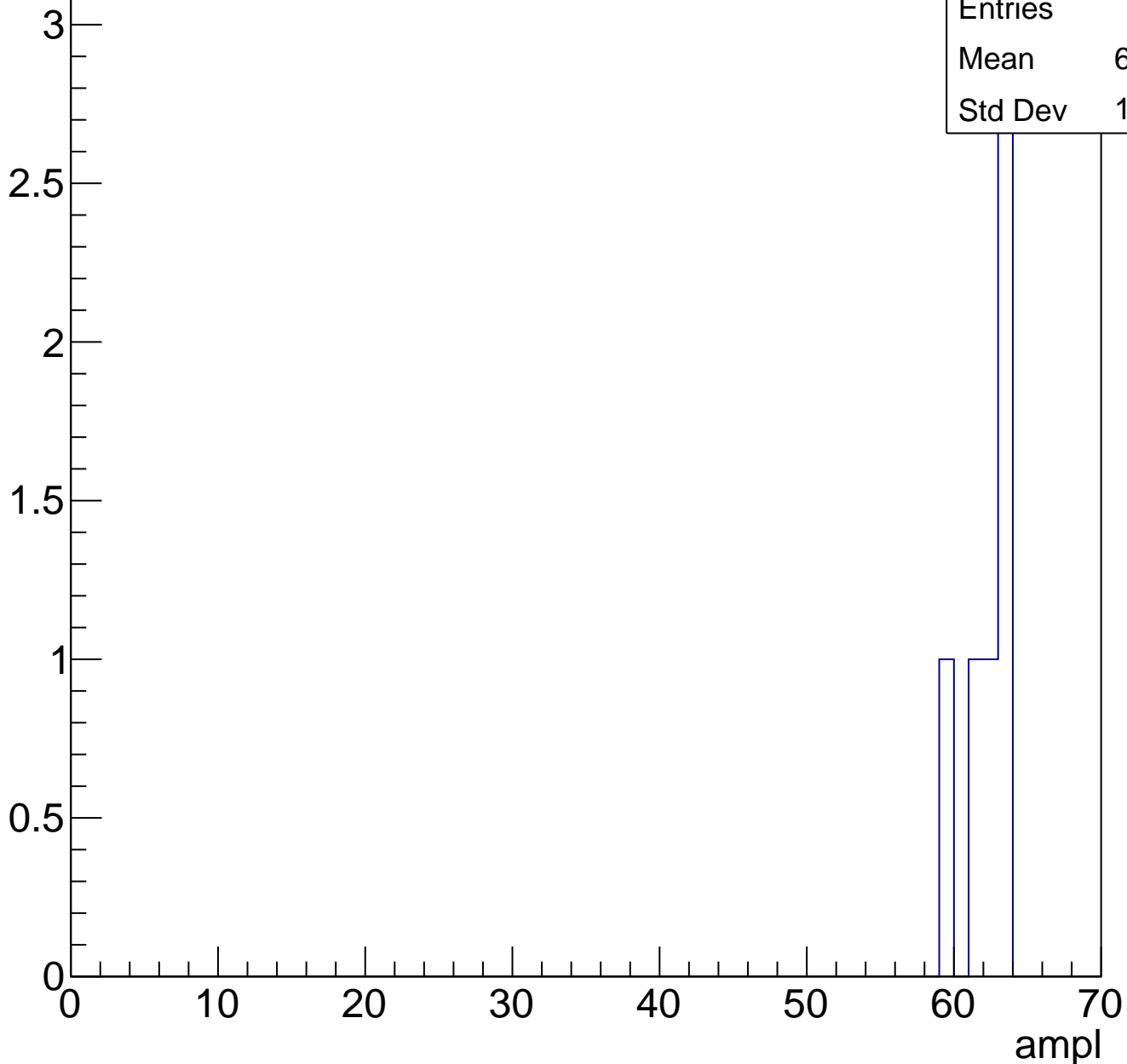
Entries	48
Mean	59.08
Std Dev	8.841



# B0L002S, U23-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch29, adc0

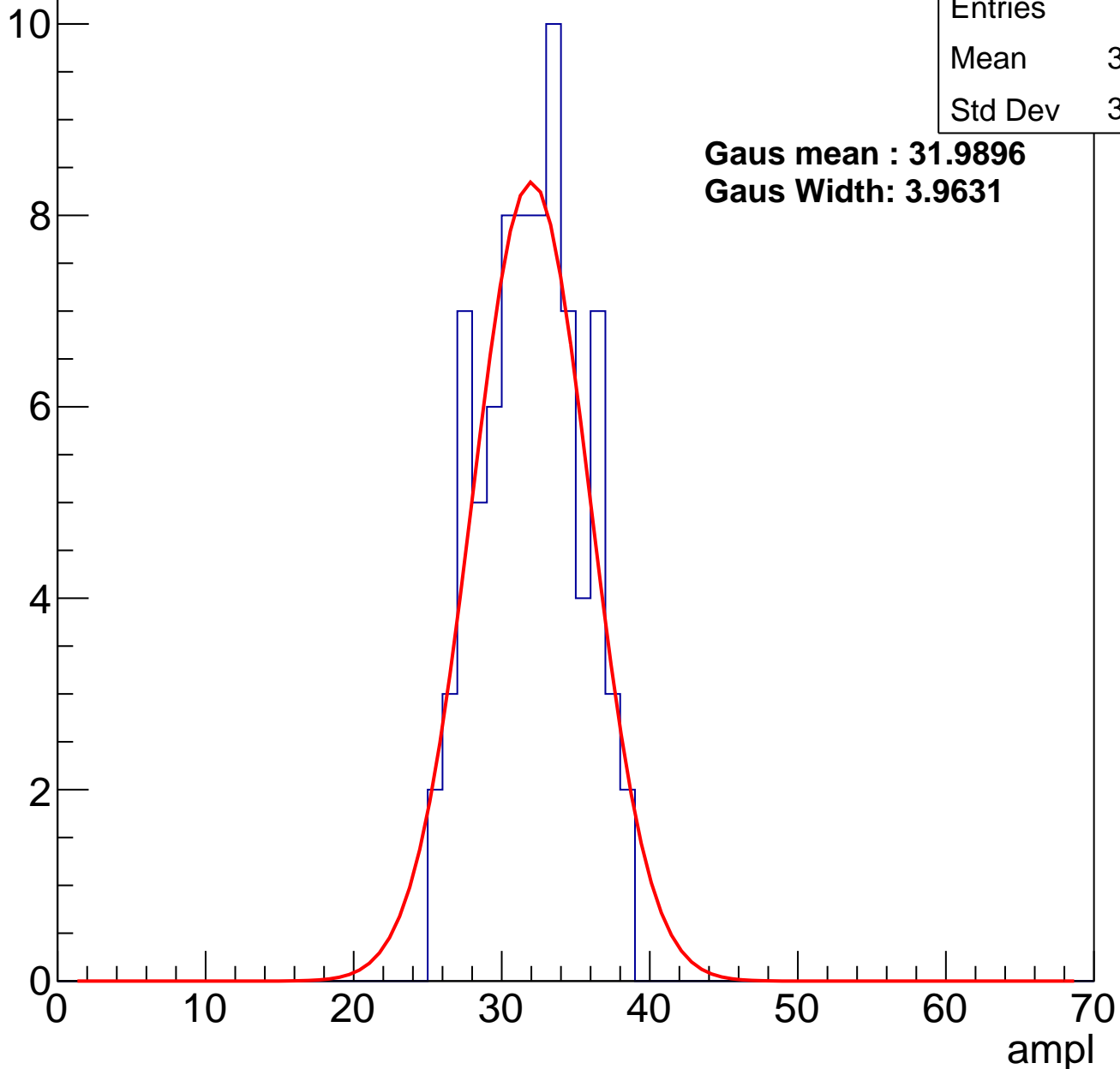
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	80
Mean	31.52
Std Dev	3.298

**Gaus mean : 31.9896**

**Gaus Width: 3.9631**

Entry



# B0L002S, U23-ch29, adc1

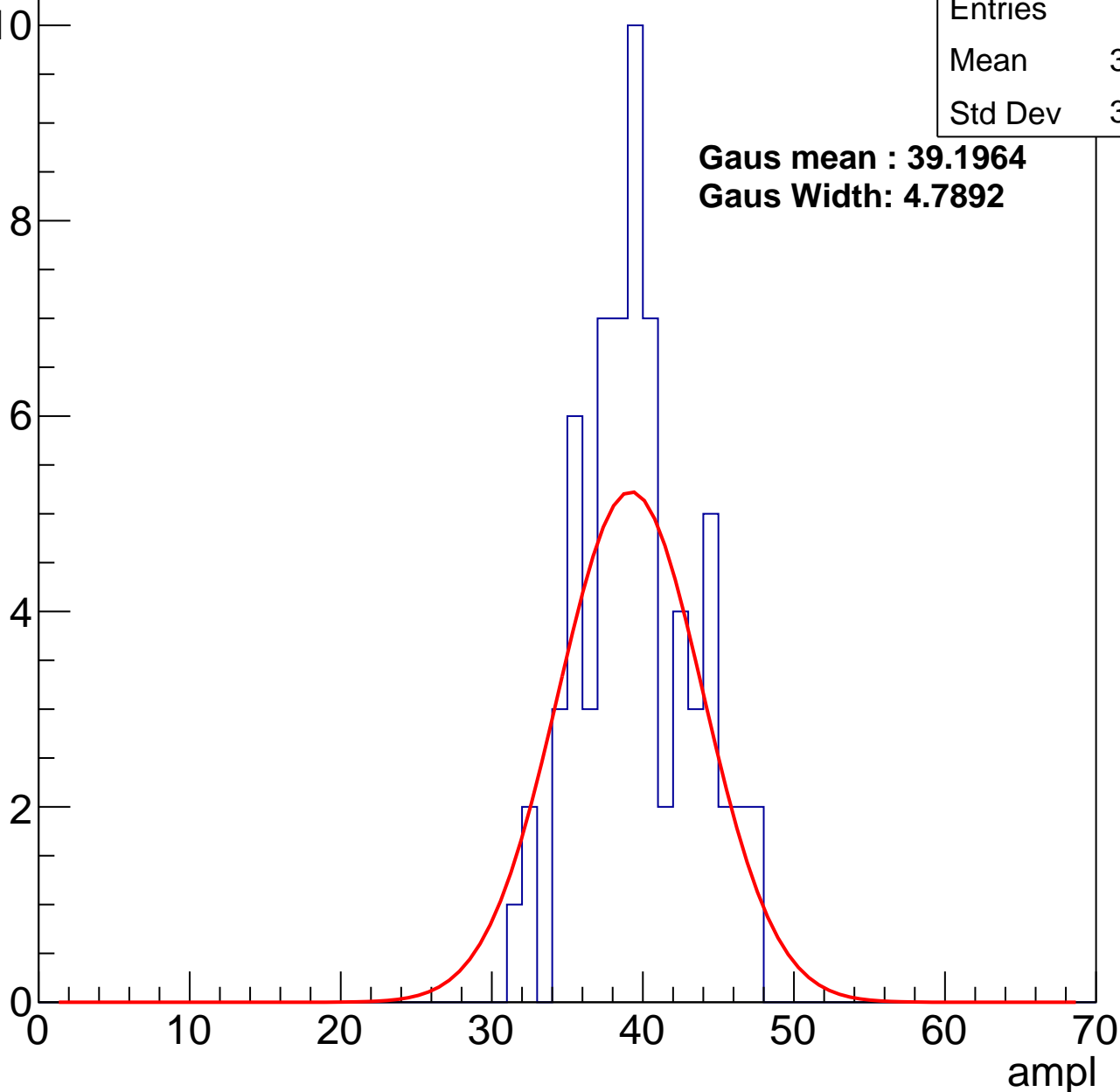
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	39.17
Std Dev	3.724

**Gaus mean : 39.164**

**Gaus Width: 4.7892**



# B0L002S, U23-ch29, adc2

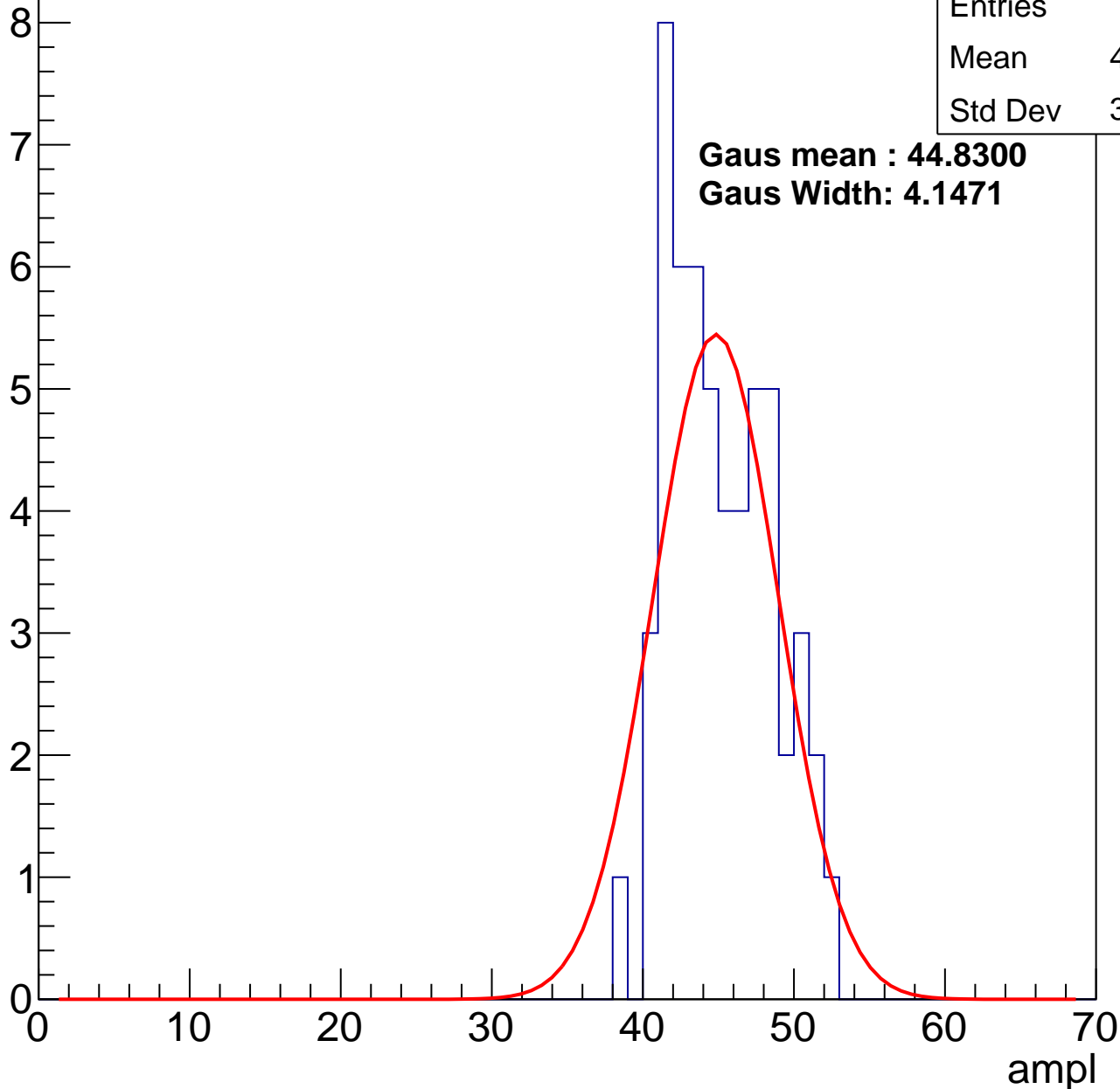
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.67
Std Dev	3.374

**Gaus mean : 44.8300**

**Gaus Width: 4.1471**

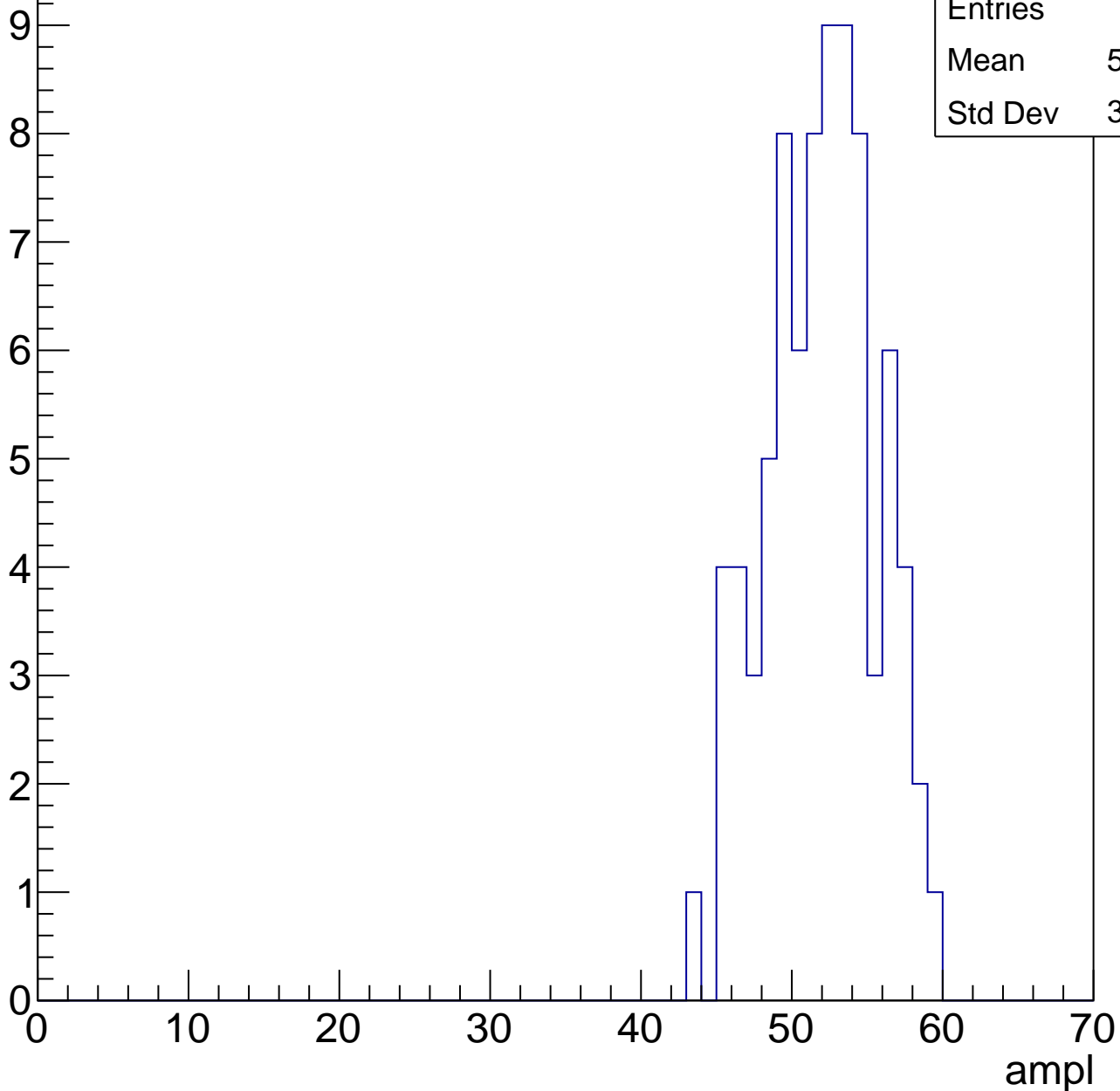


# B0L002S, U23-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	51.47
Std Dev	3.587

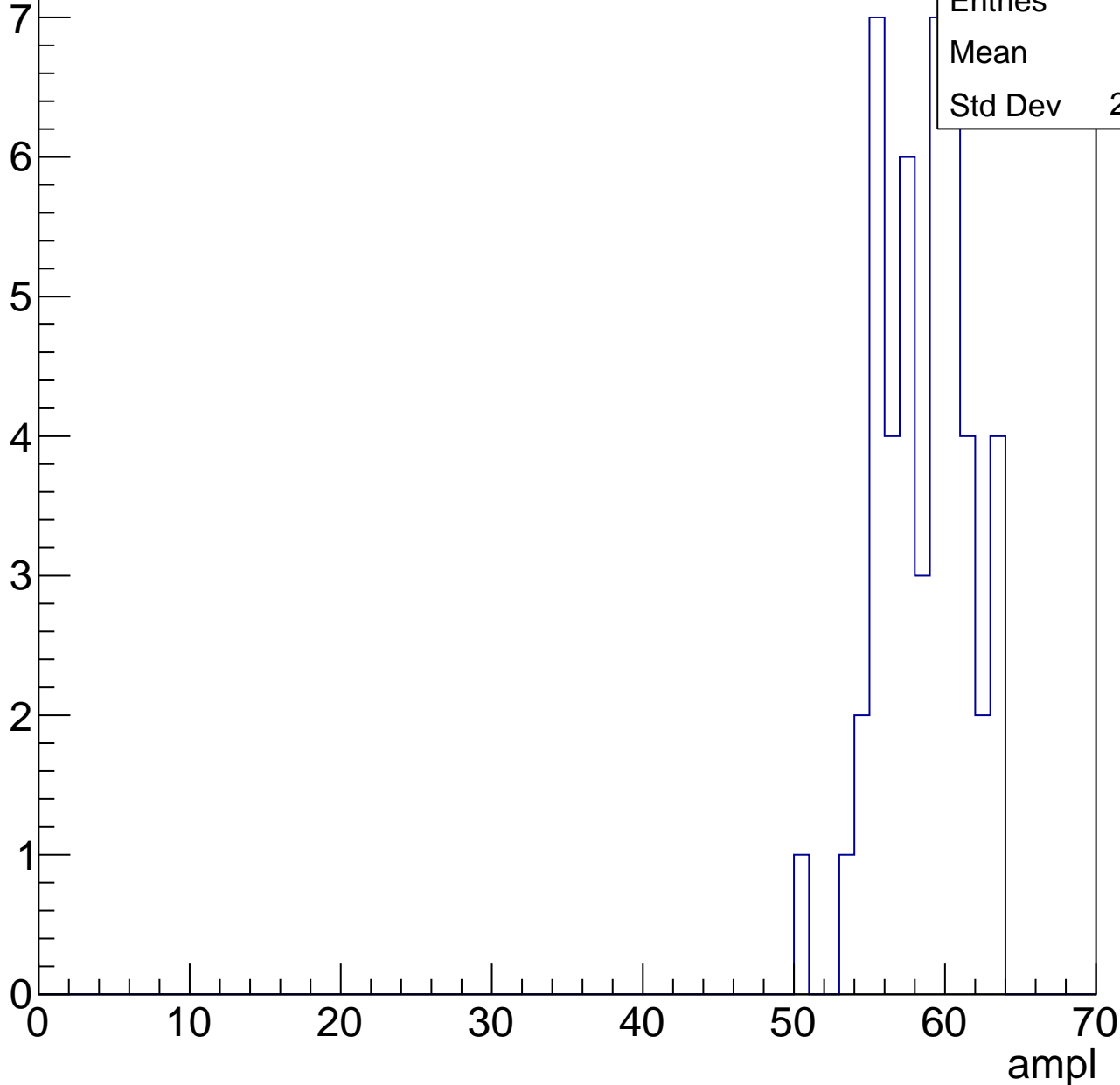


# B0L002S, U23-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	58.1
Std Dev	2.917

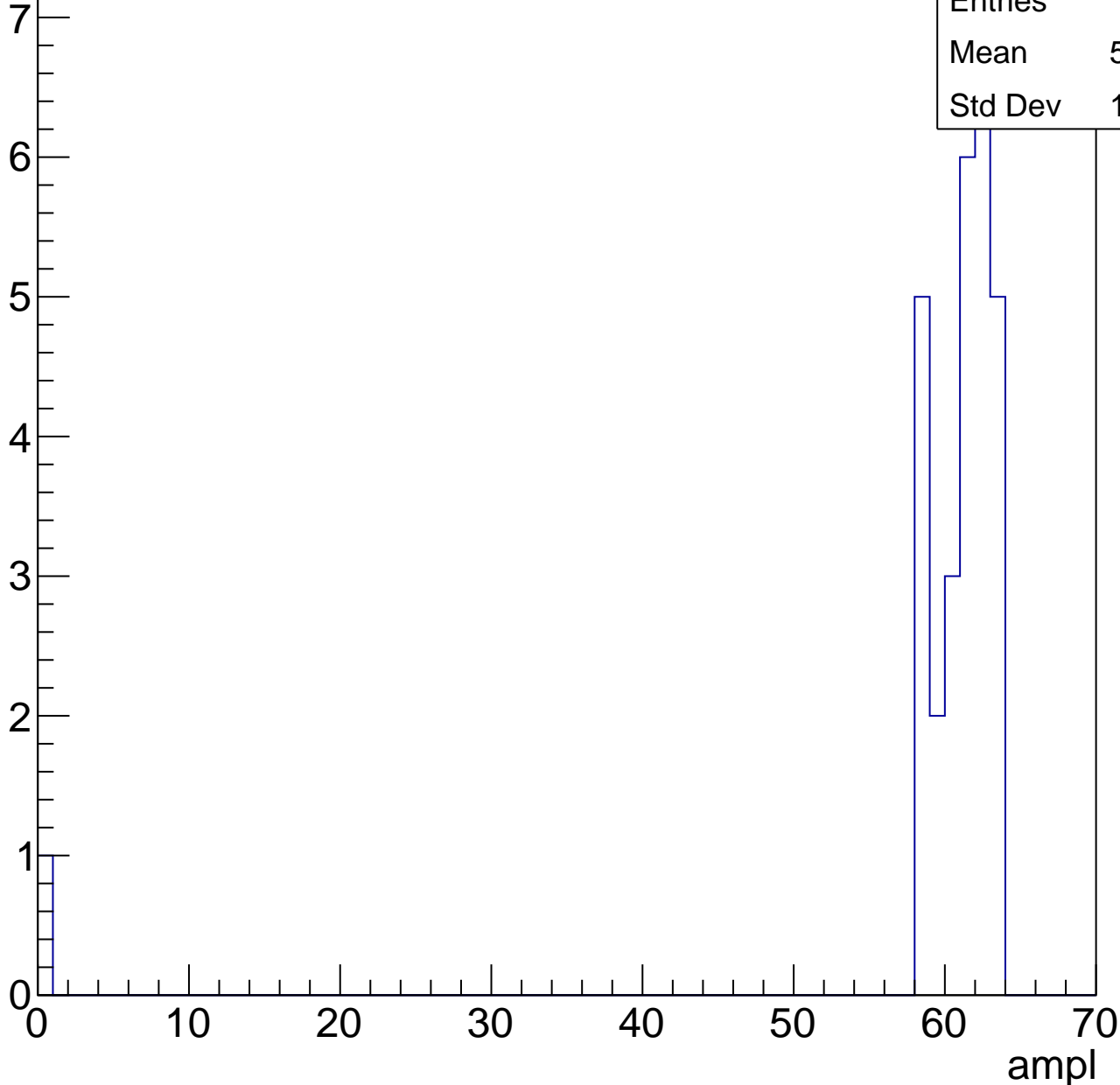


# B0L002S, U23-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.72
Std Dev	11.22



# B0L002S, U23-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch30, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	71
Mean	29.41
Std Dev	2.861

**Gaus mean : 29.6996**

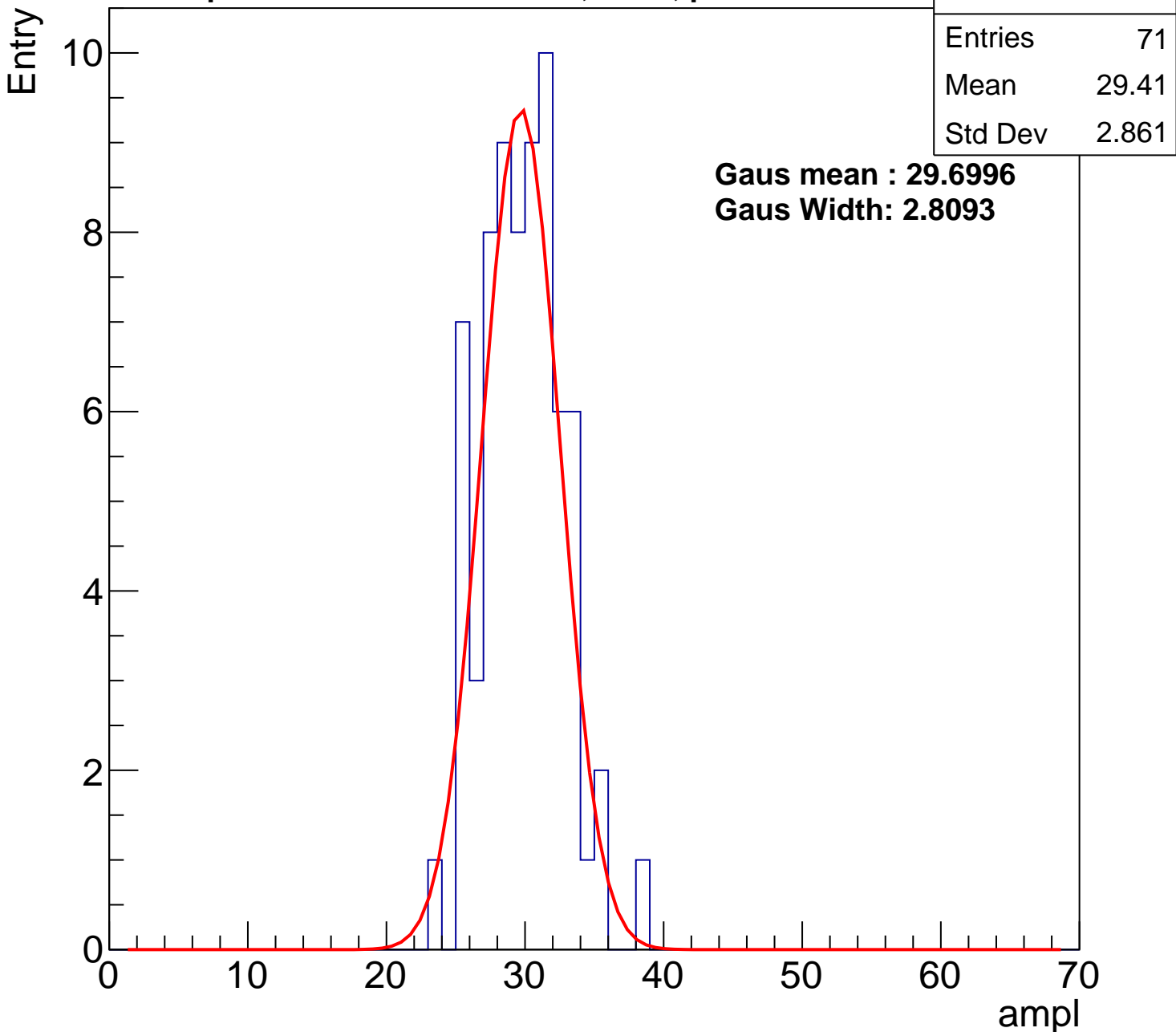
**Gaus Width: 2.8093**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	74
Mean	36.69
Std Dev	3.316

**Gaus mean : 37.0683**

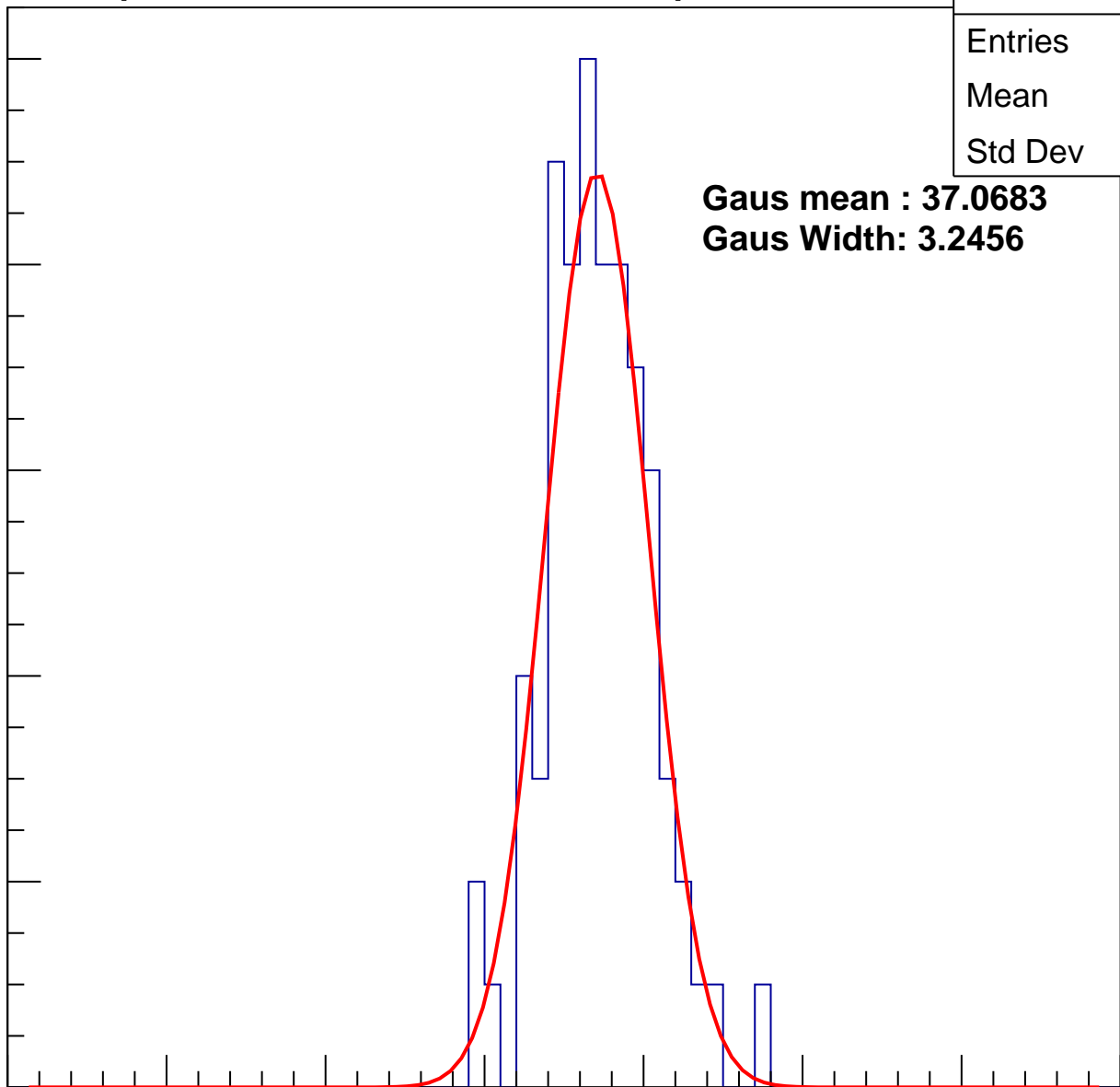
**Gaus Width: 3.2456**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch30, adc2

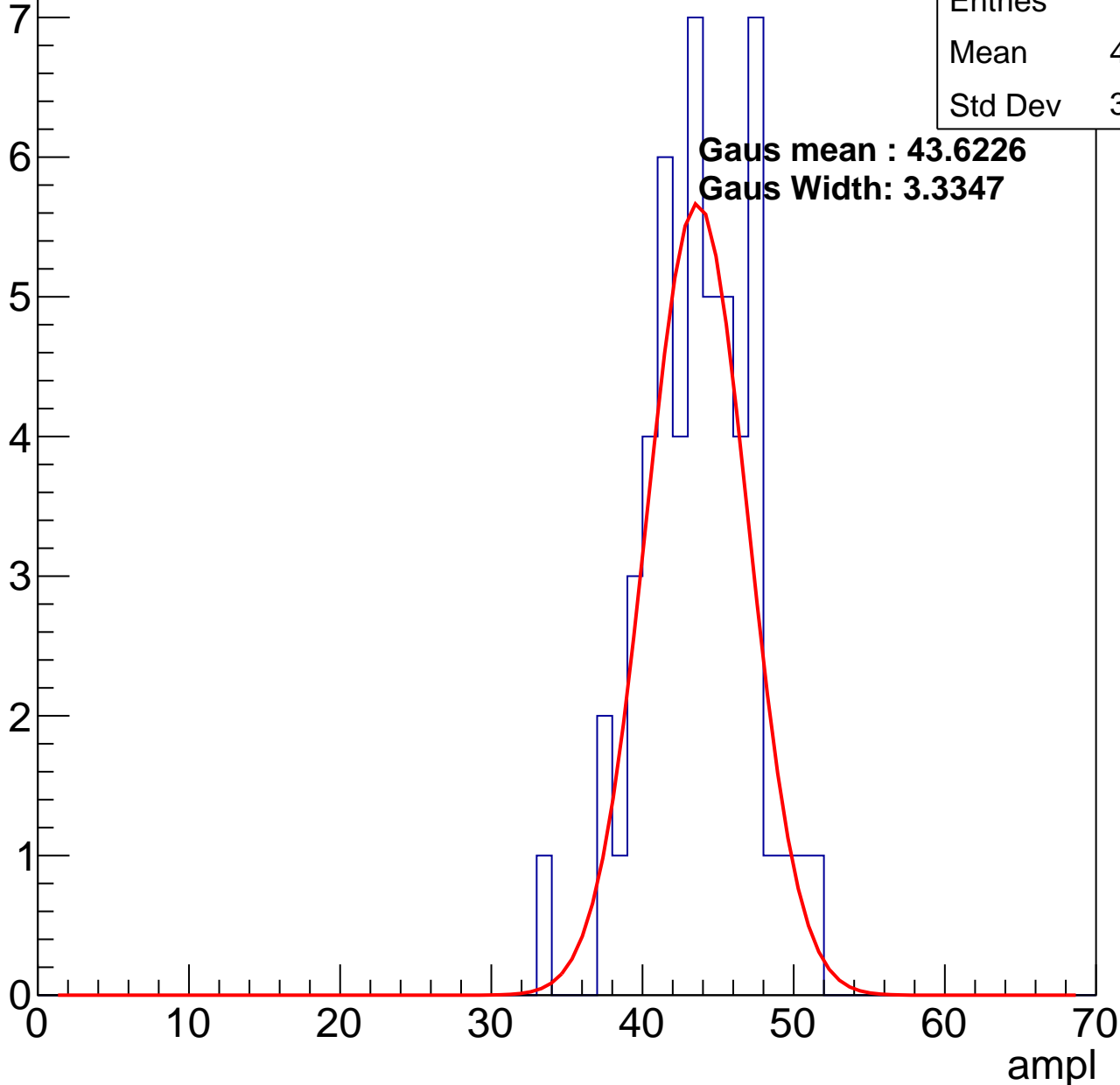
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.26
Std Dev	3.508

**Gaus mean : 43.6226**

**Gaus Width: 3.3347**

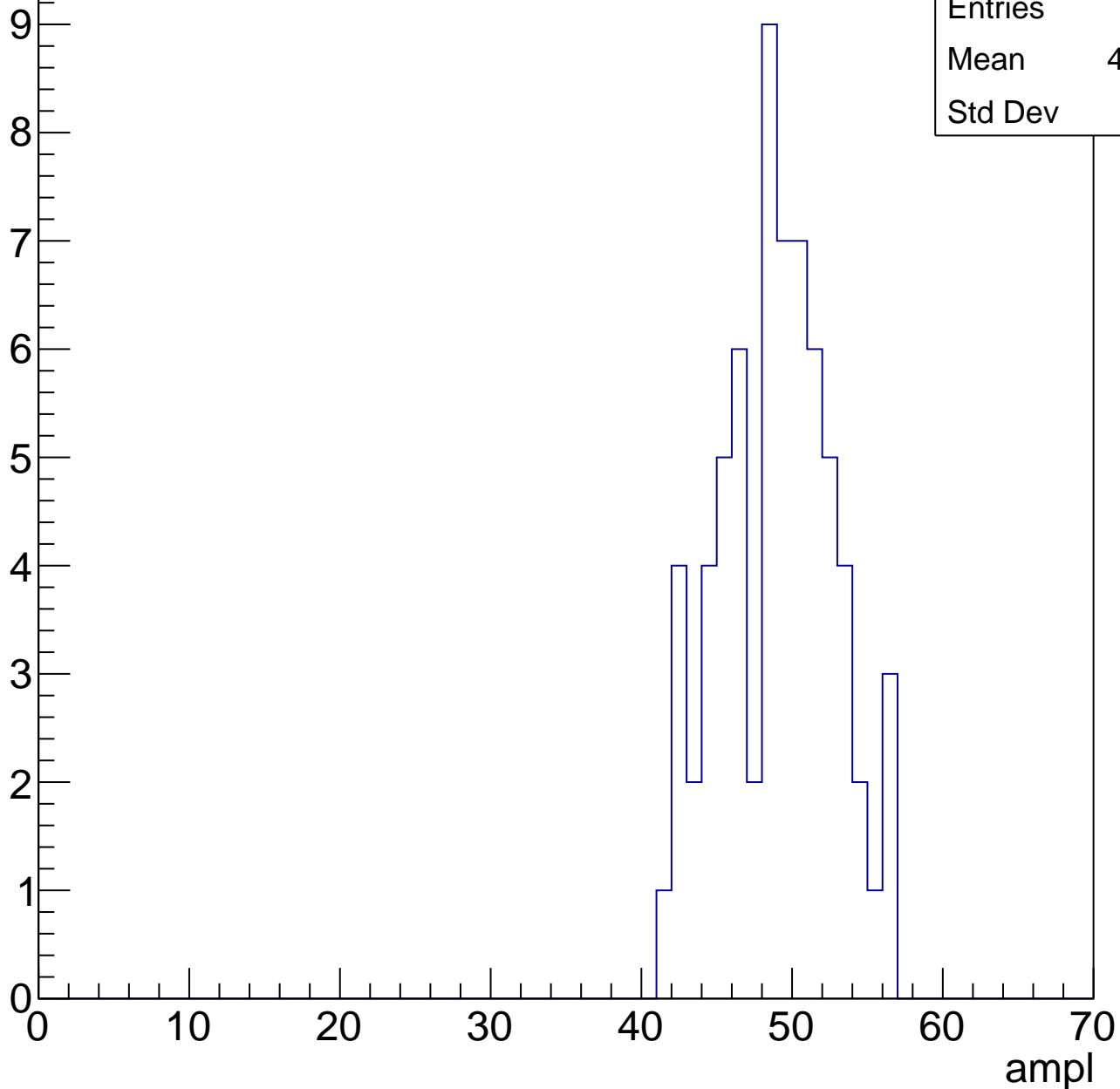


# B0L002S, U23-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

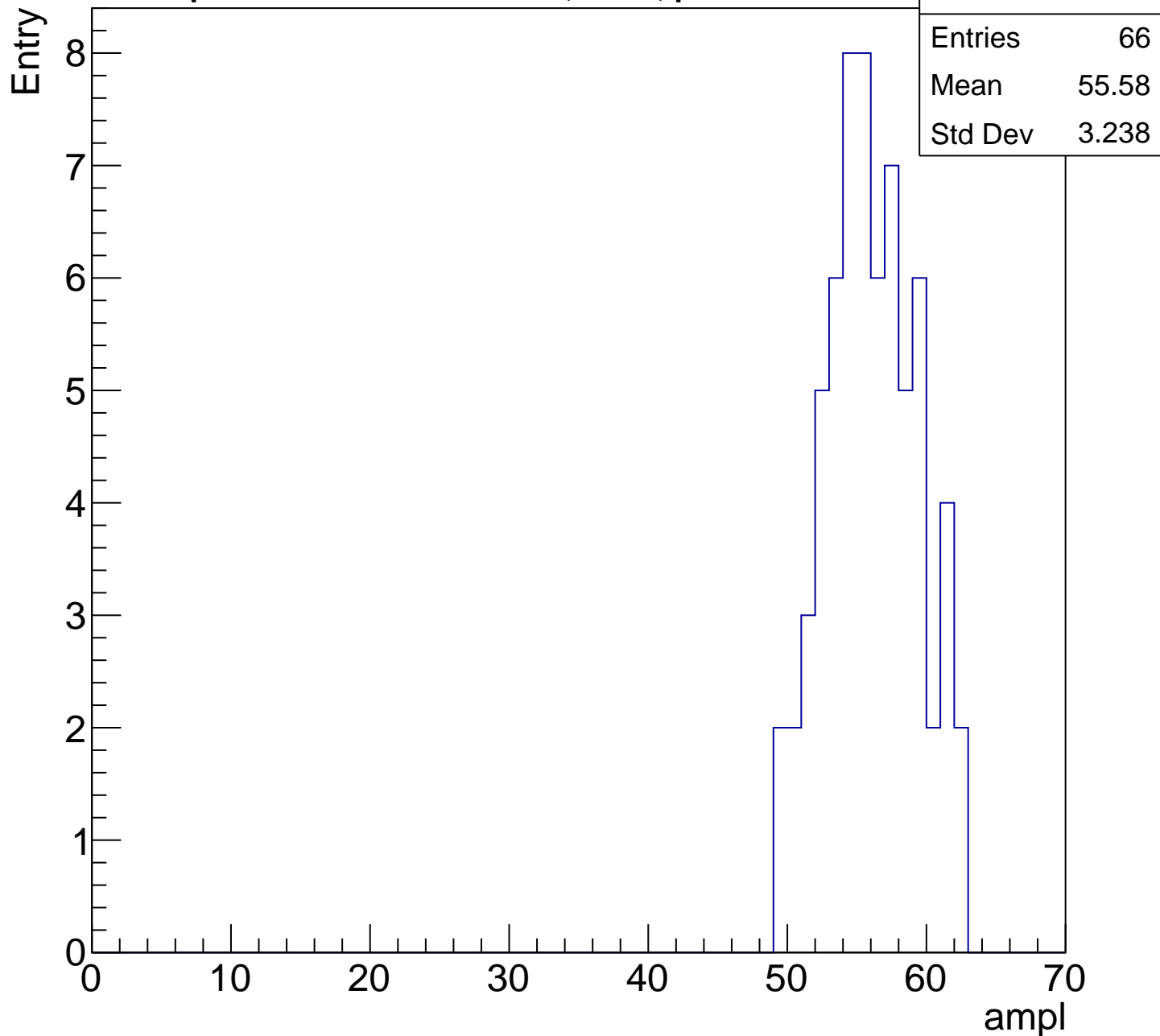
Entry

Entries	68
Mean	48.53
Std Dev	3.72



# B0L002S, U23-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U23-ch30, adc5

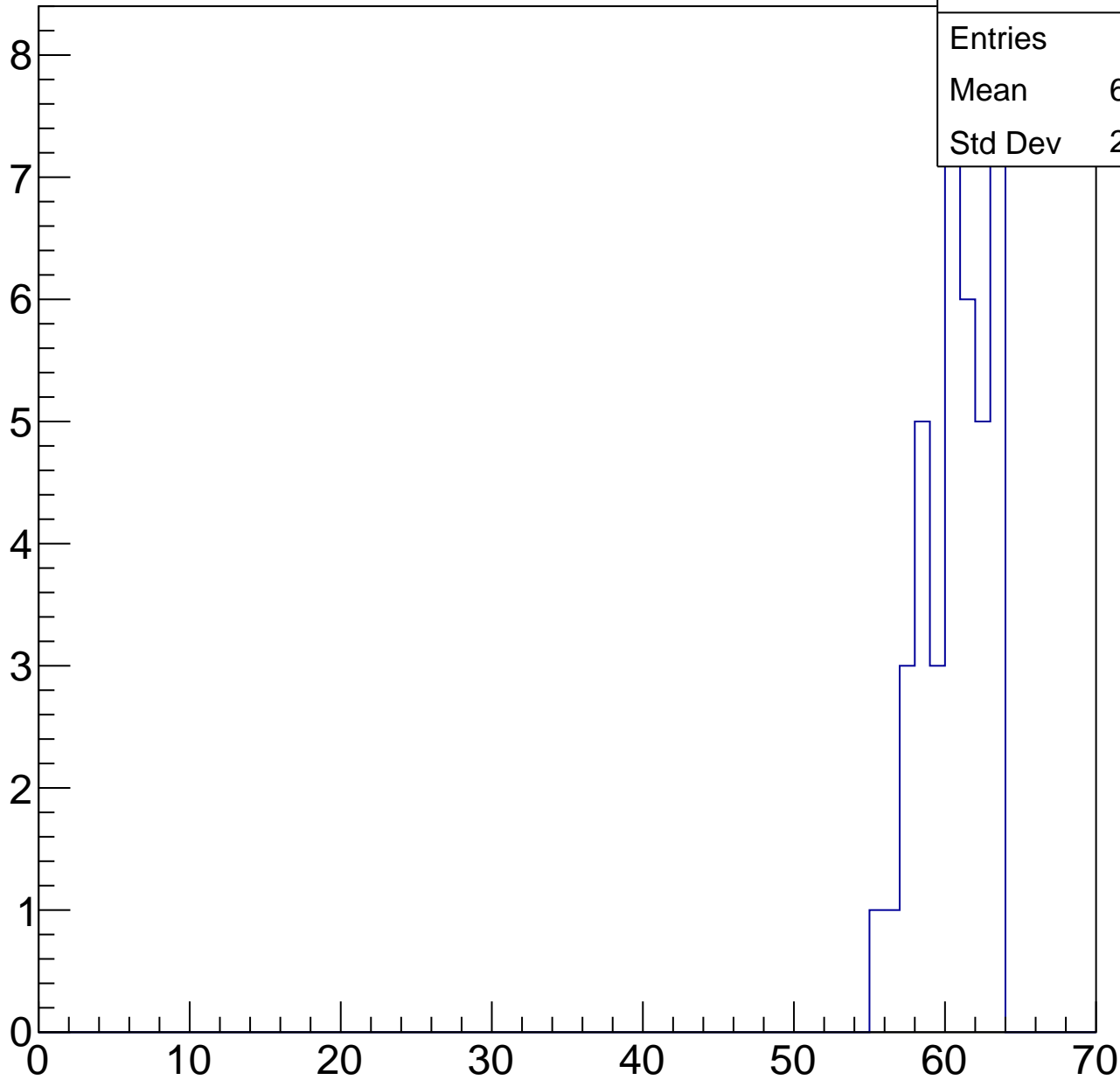
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	60.23
Std Dev	2.162

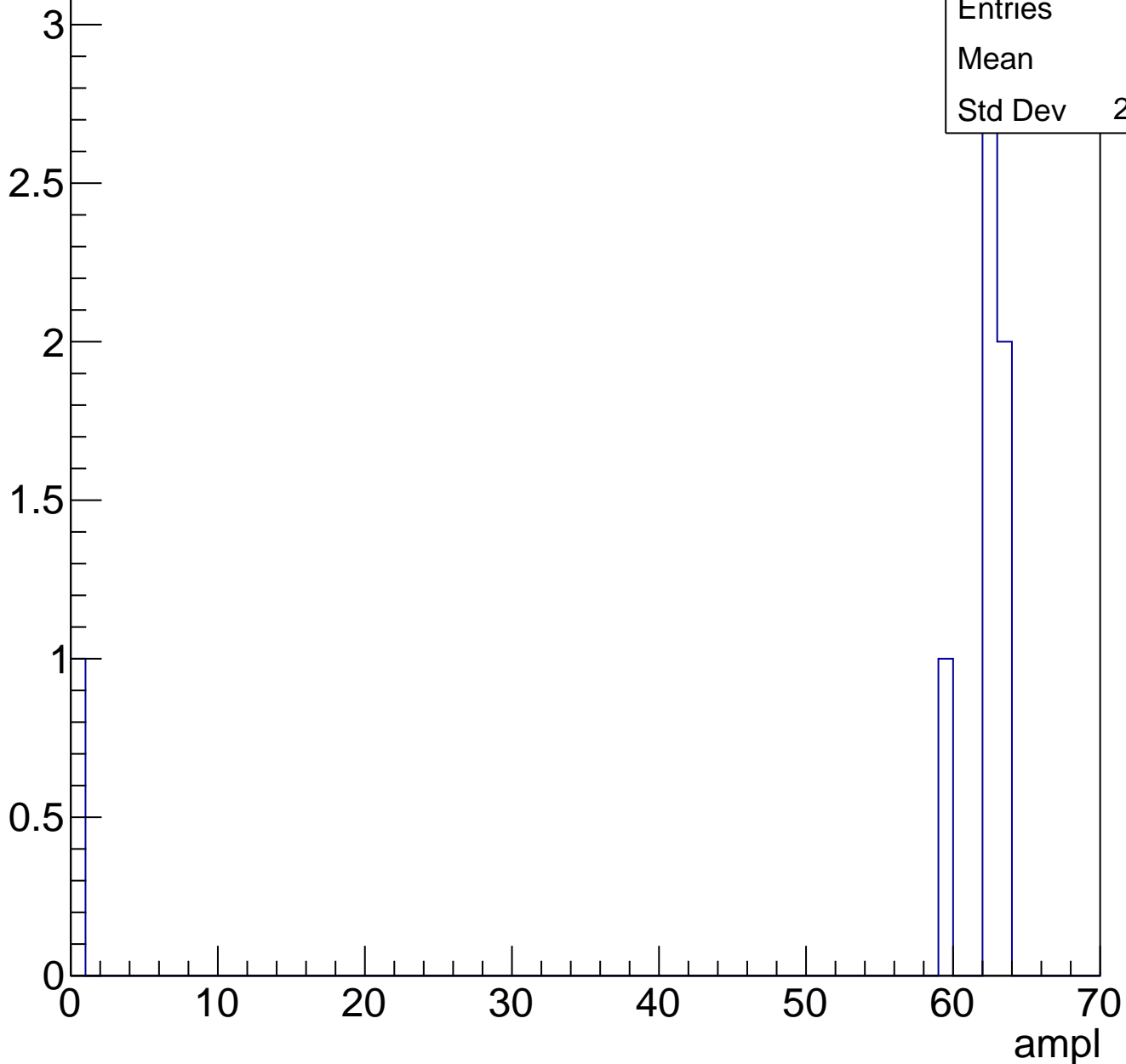
ampl



# B0L002S, U23-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L002S, U23-ch31, adc0

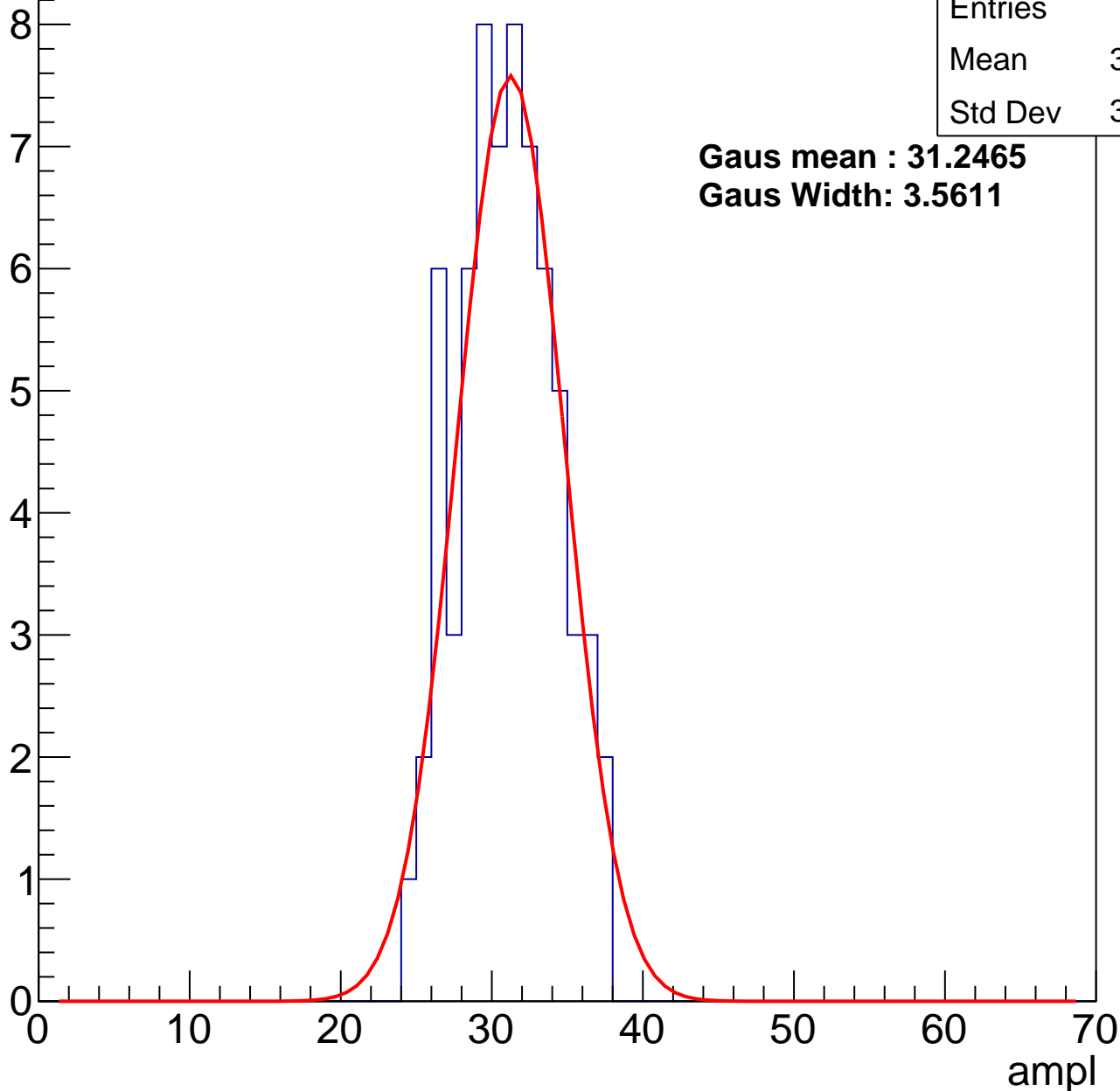
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.57
Std Dev	3.163

**Gaus mean : 31.2465**

**Gaus Width: 3.5611**



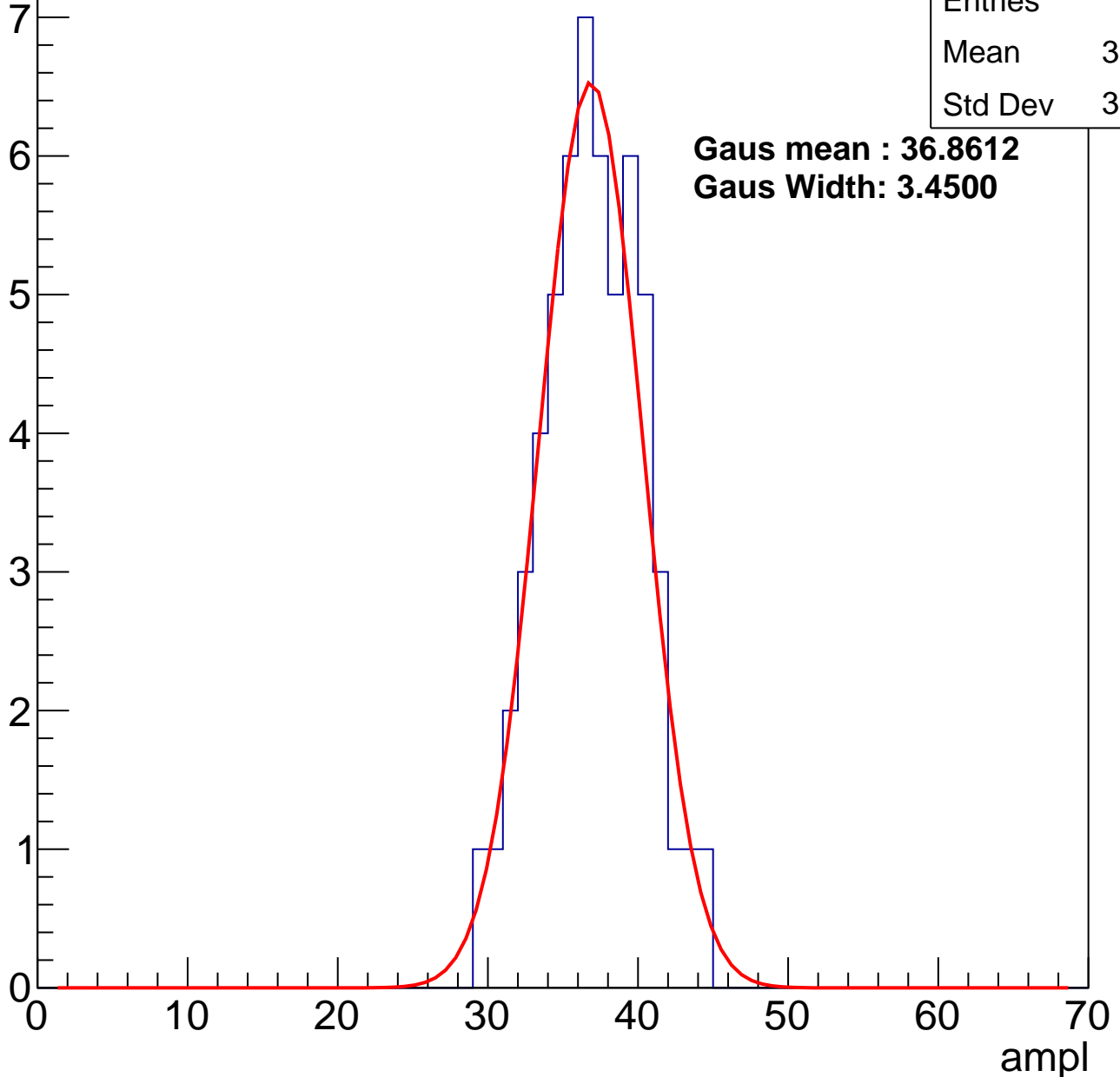
# B0L002S, U23-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.47
Std Dev	3.288

**Gaus mean : 36.8612**  
**Gaus Width: 3.4500**



# B0L002S, U23-ch31, adc2

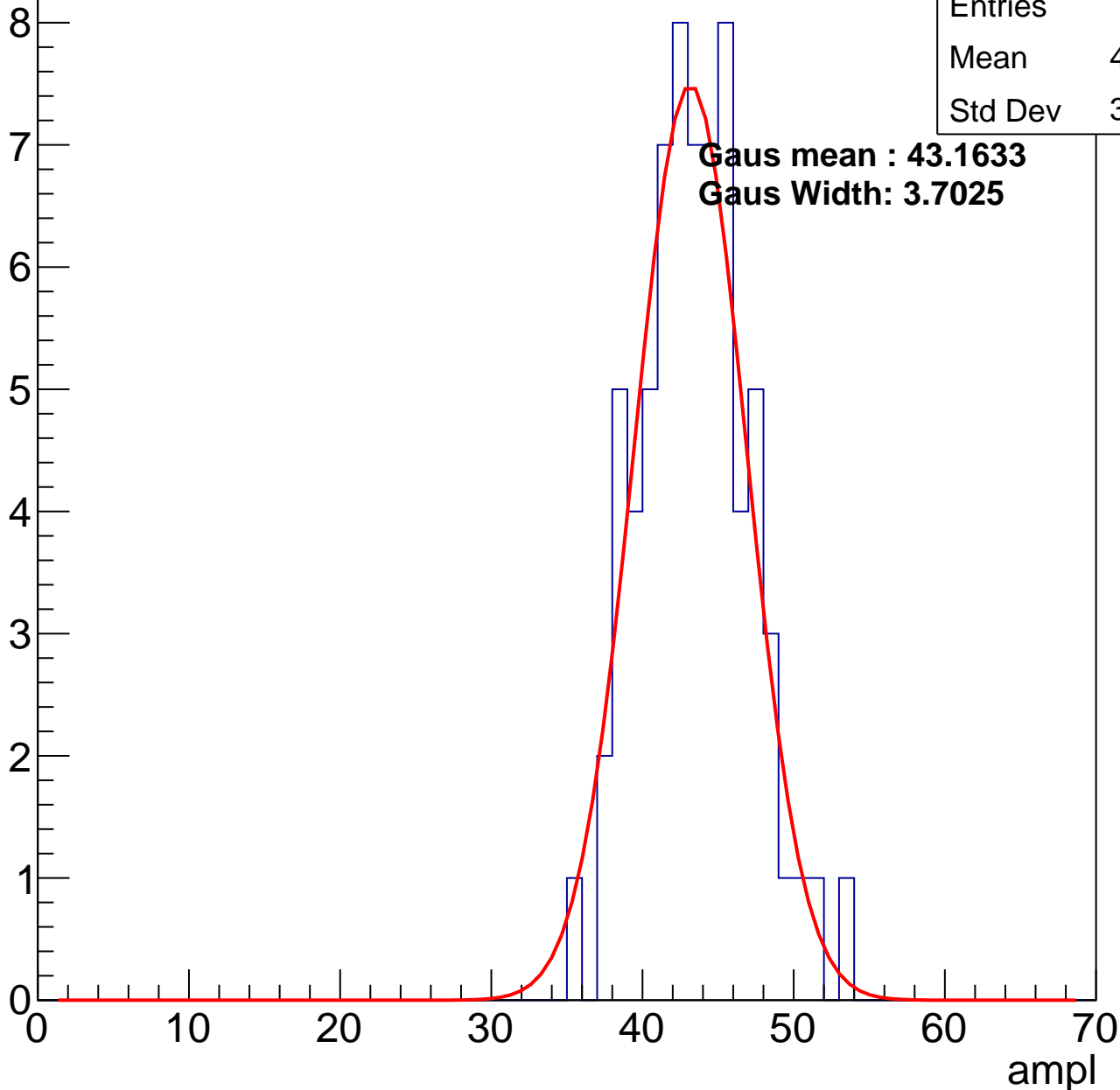
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.04
Std Dev	3.555

**Gaus mean : 43.1633**

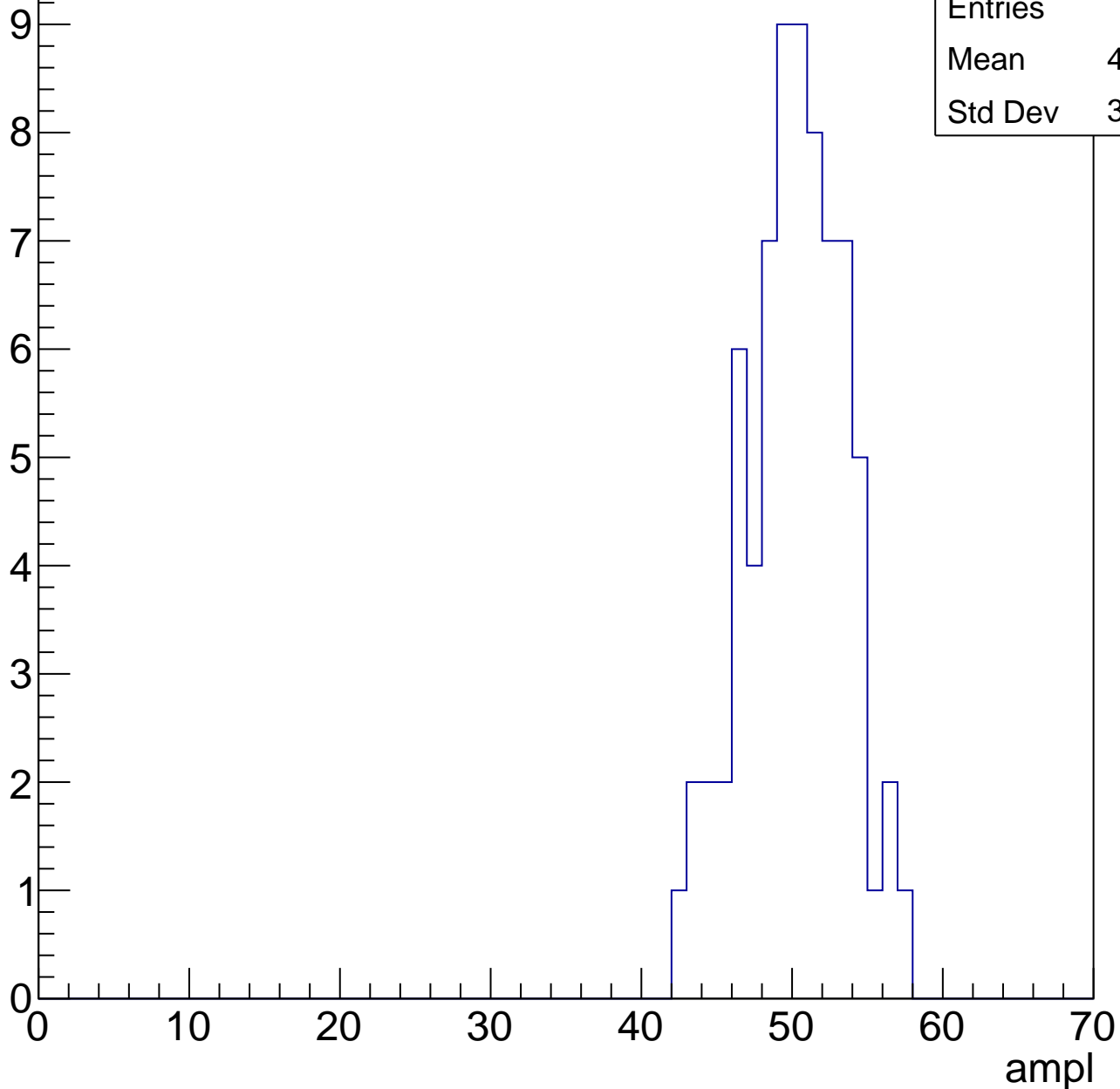
**Gaus Width: 3.7025**



# B0L002S, U23-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

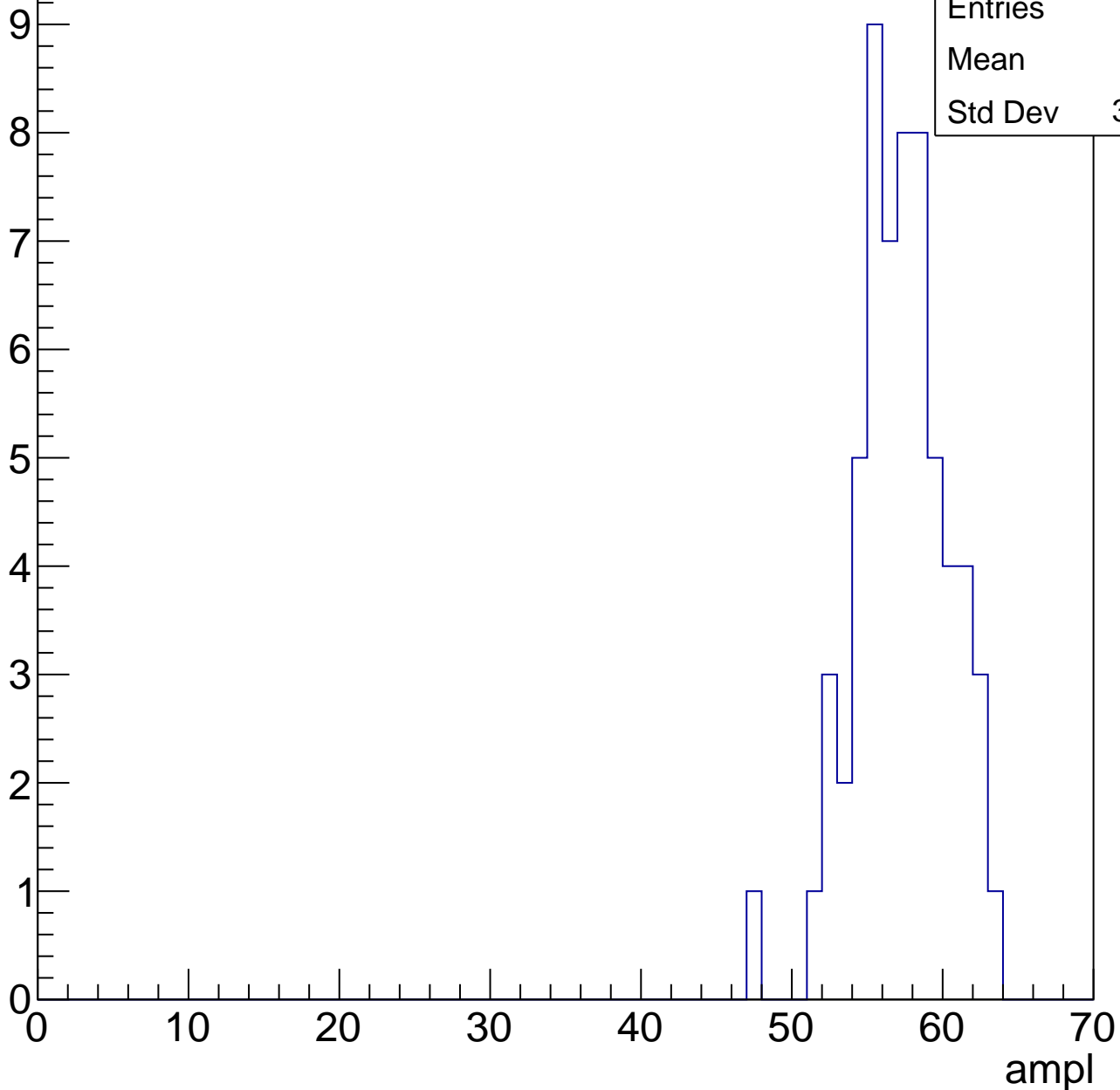


# B0L002S, U23-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

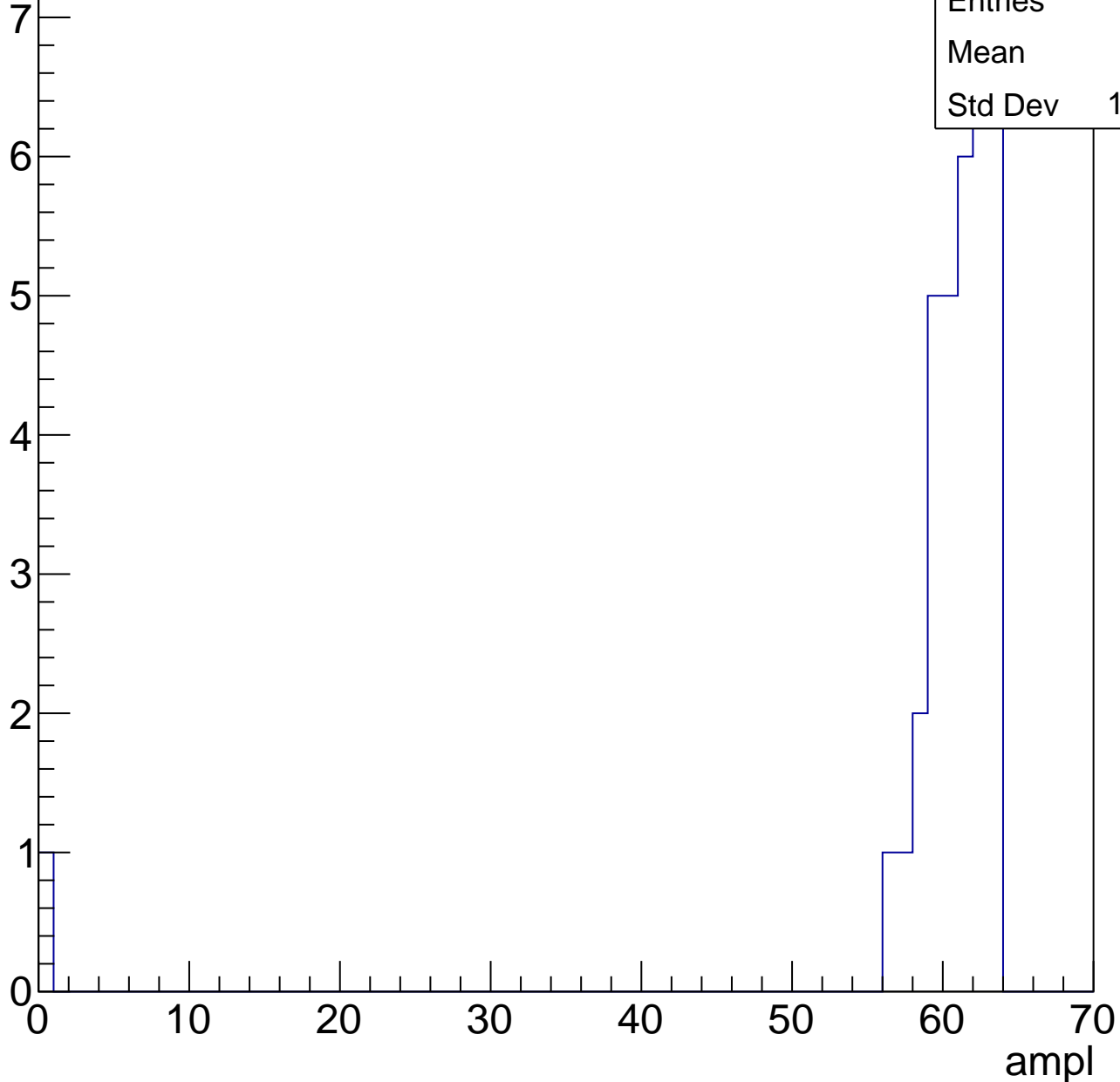
Entries	61
Mean	56.8
Std Dev	3.051



# B0L002S, U23-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	62
Std Dev	1.414



# B0L002S, U23-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L002S, U23-ch32, adc0

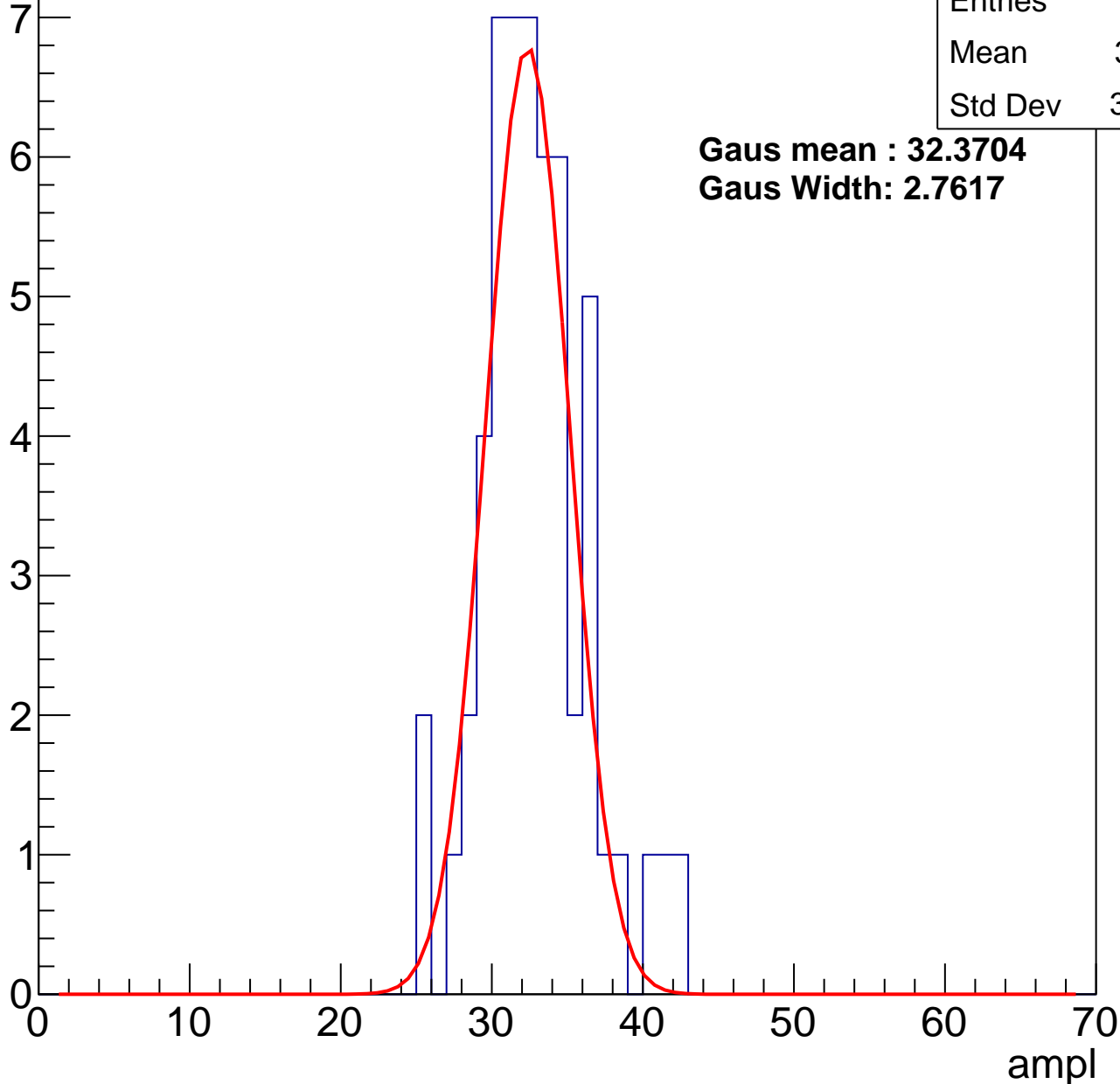
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	32.41
Std Dev	3.472

**Gaus mean : 32.3704**

**Gaus Width: 2.7617**



# B0L002S, U23-ch32, adc1

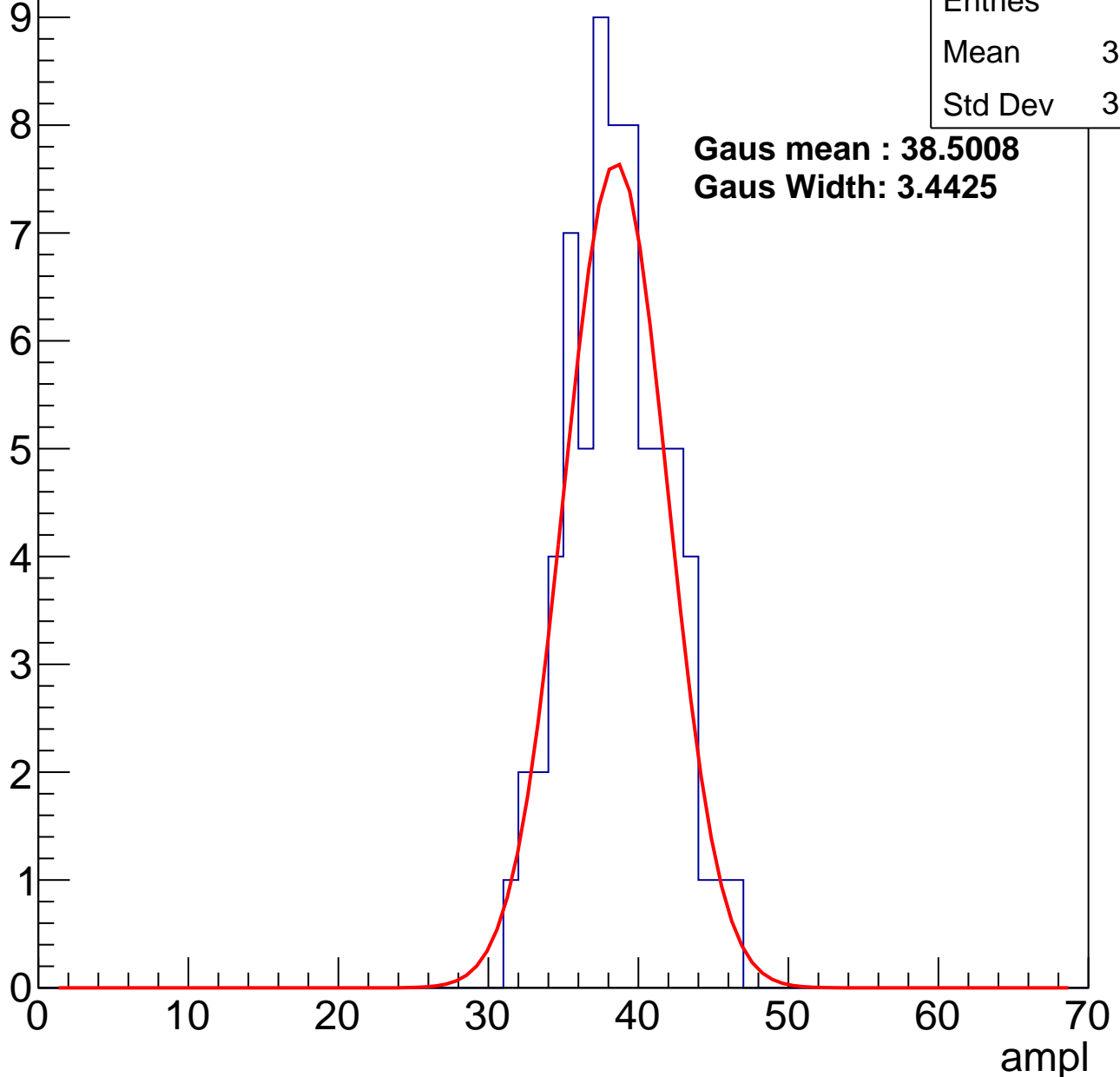
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38.13
Std Dev	3.272

**Gaus mean : 38.5008**

**Gaus Width: 3.4425**



# B0L002S, U23-ch32, adc2

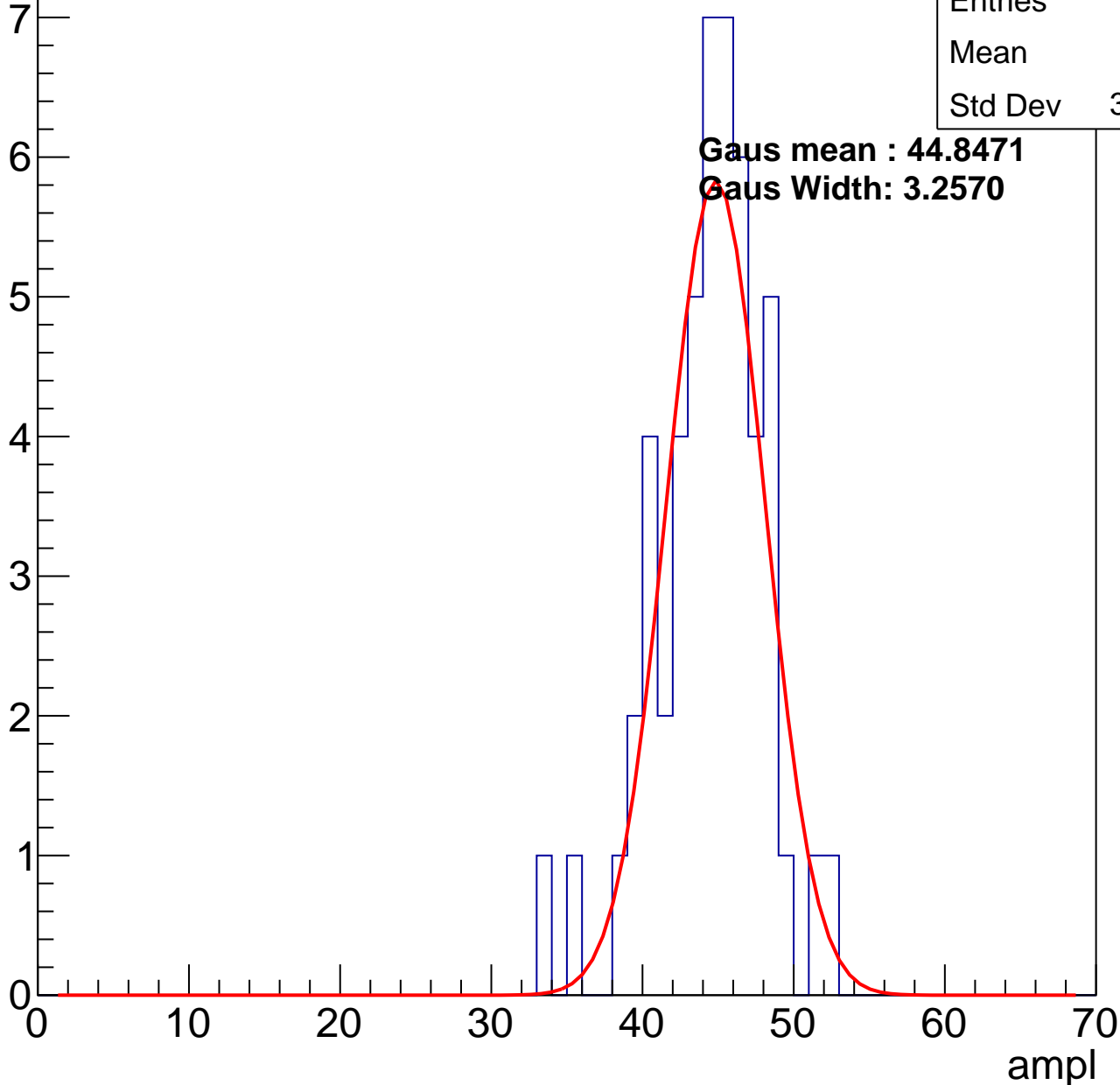
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	44
Std Dev	3.606

Gaus mean : 44.8471

Gaus Width: 3.2570

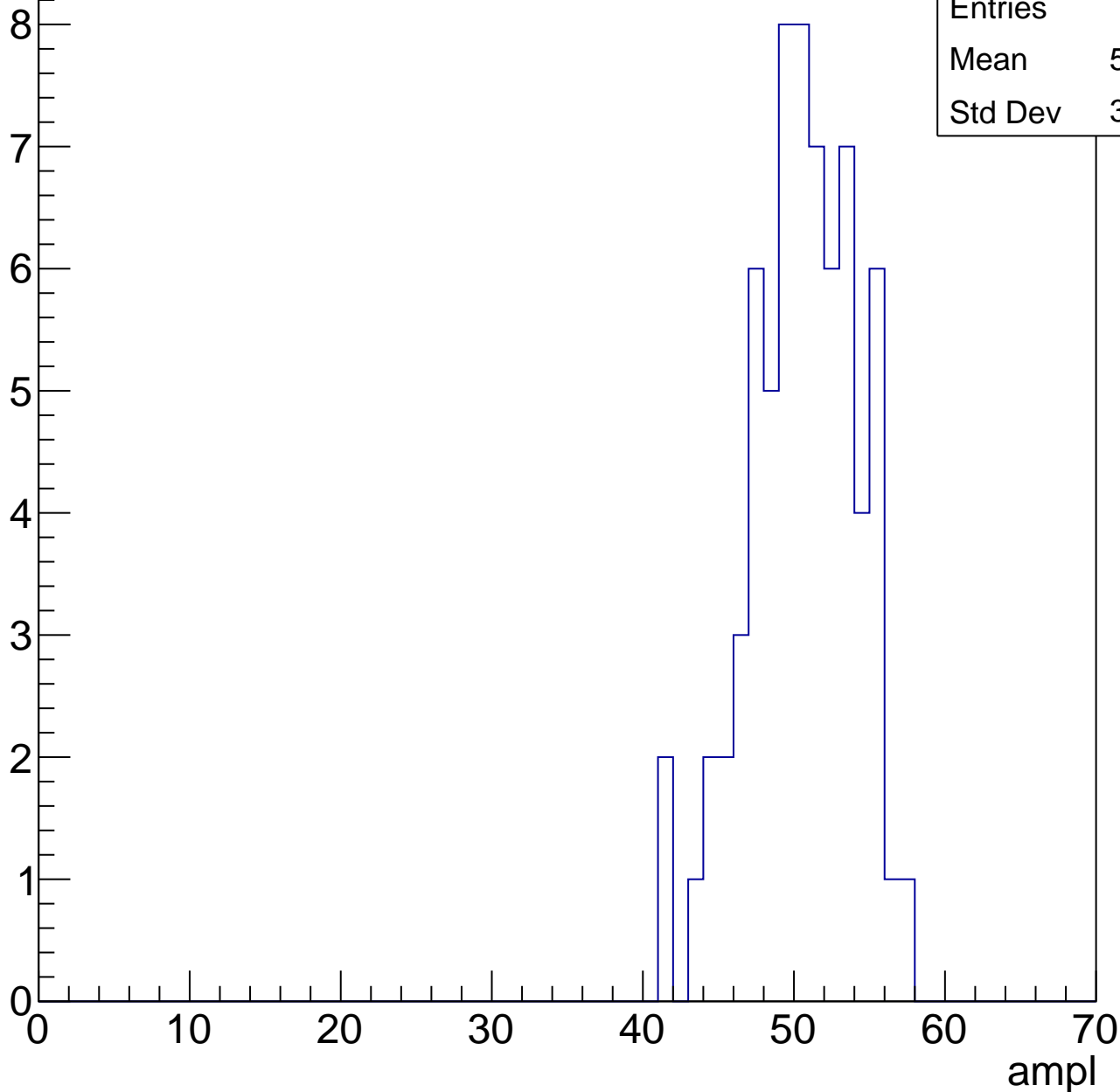


# B0L002S, U23-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	50.06
Std Dev	3.522

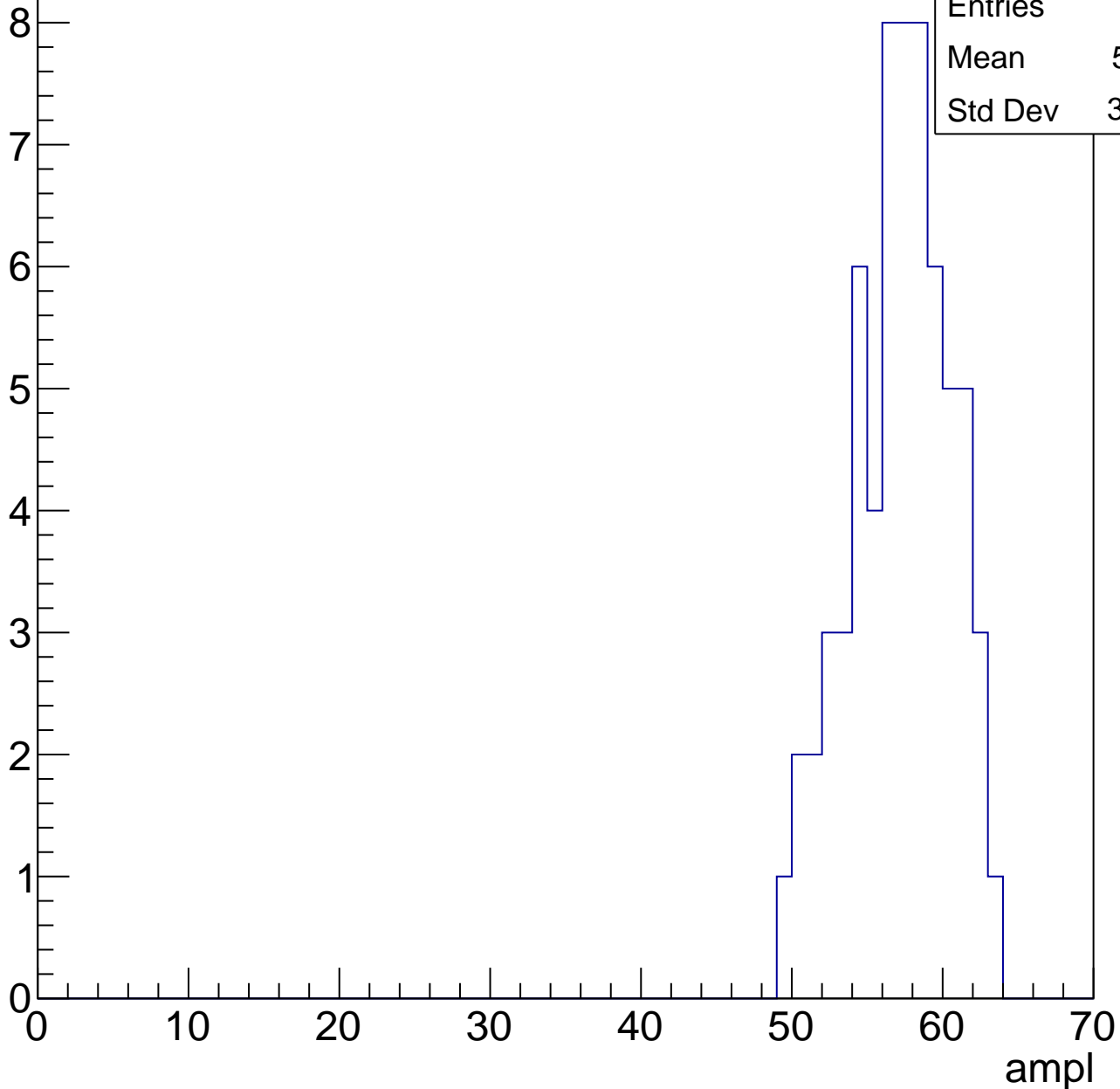


# B0L002S, U23-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	56.71
Std Dev	3.276

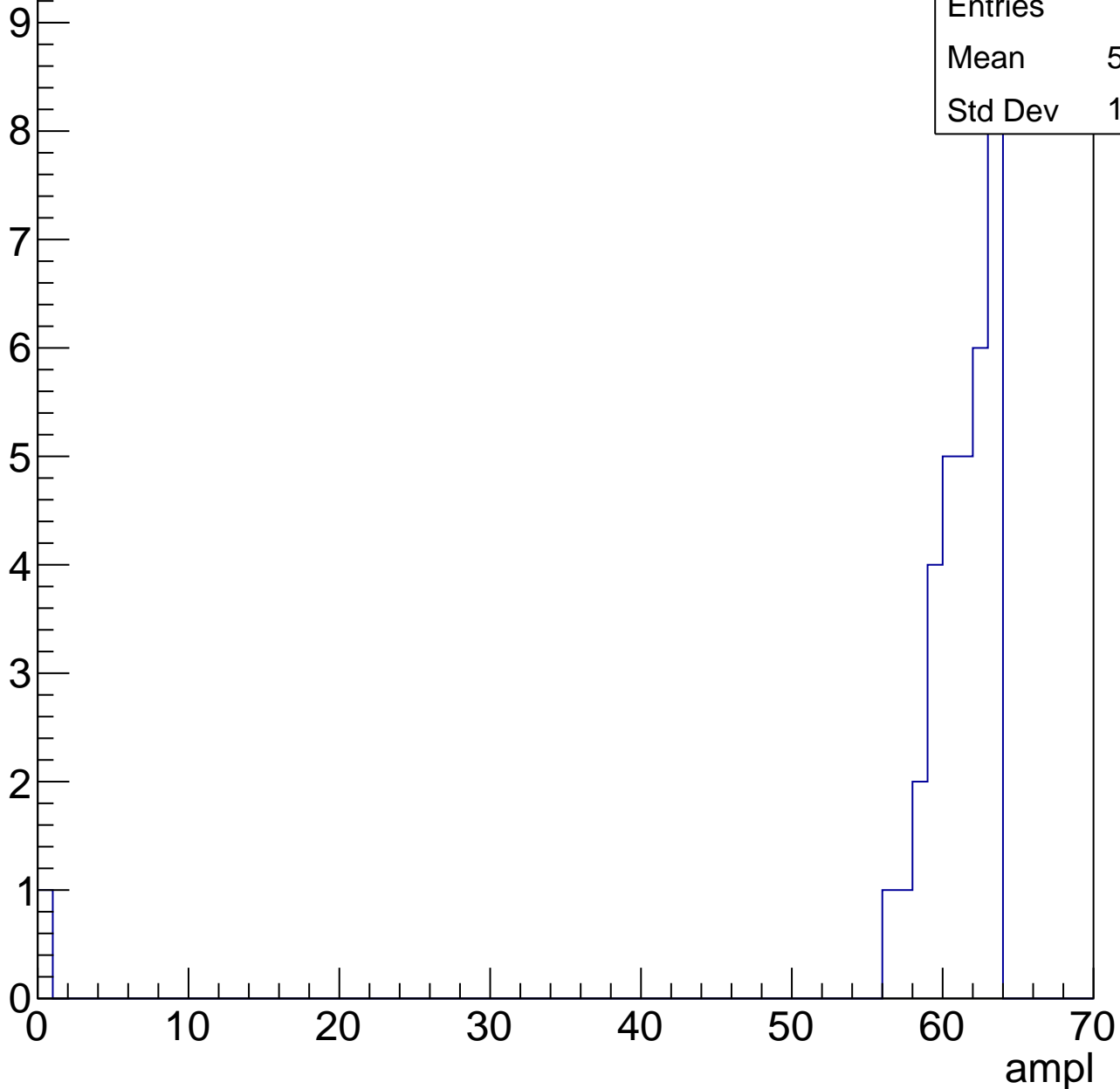


# B0L002S, U23-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.09
Std Dev	10.46



# B0L002S, U23-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch33, adc0

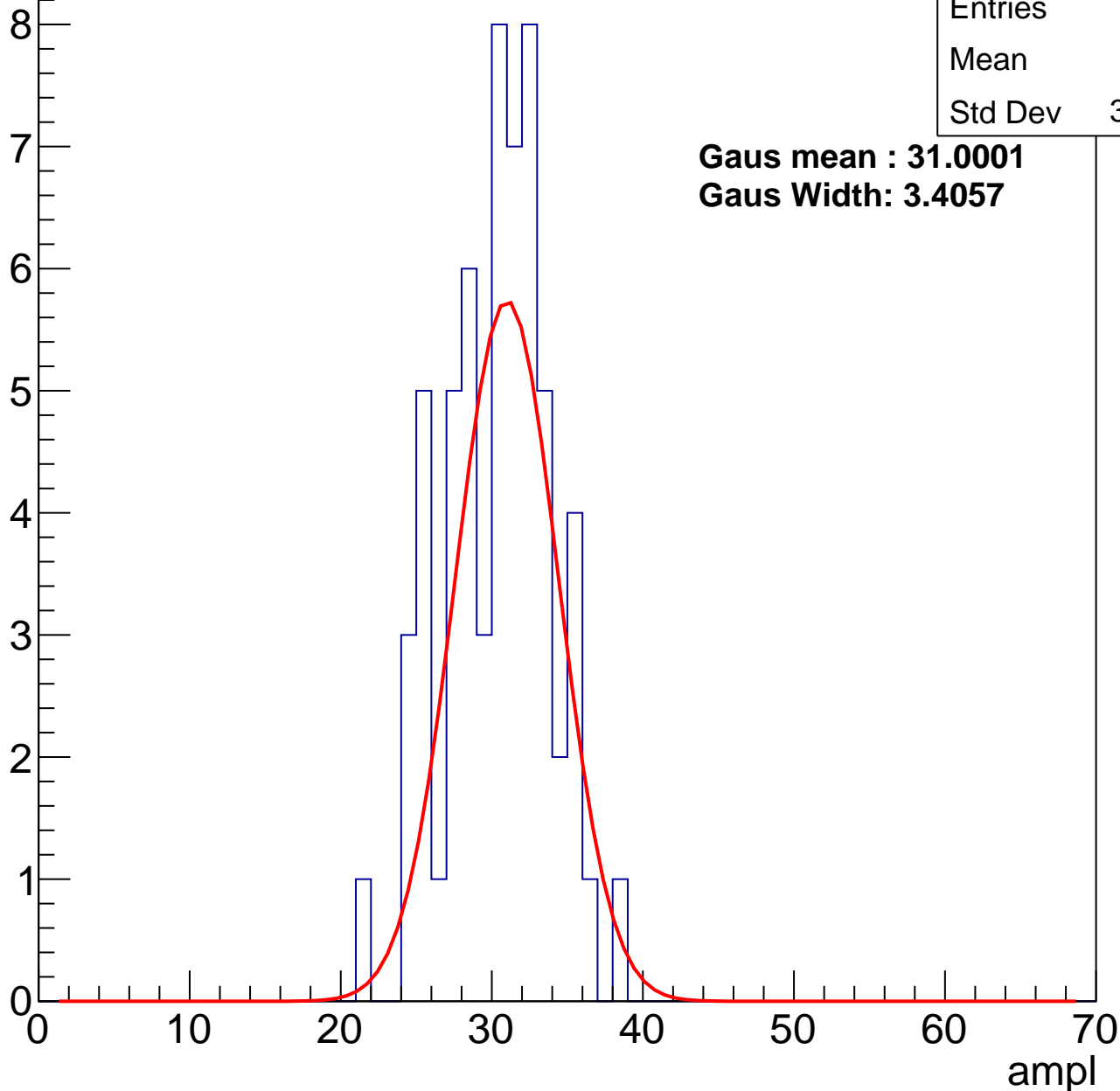
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.9
Std Dev	3.458

**Gaus mean : 31.0001**

**Gaus Width: 3.4057**



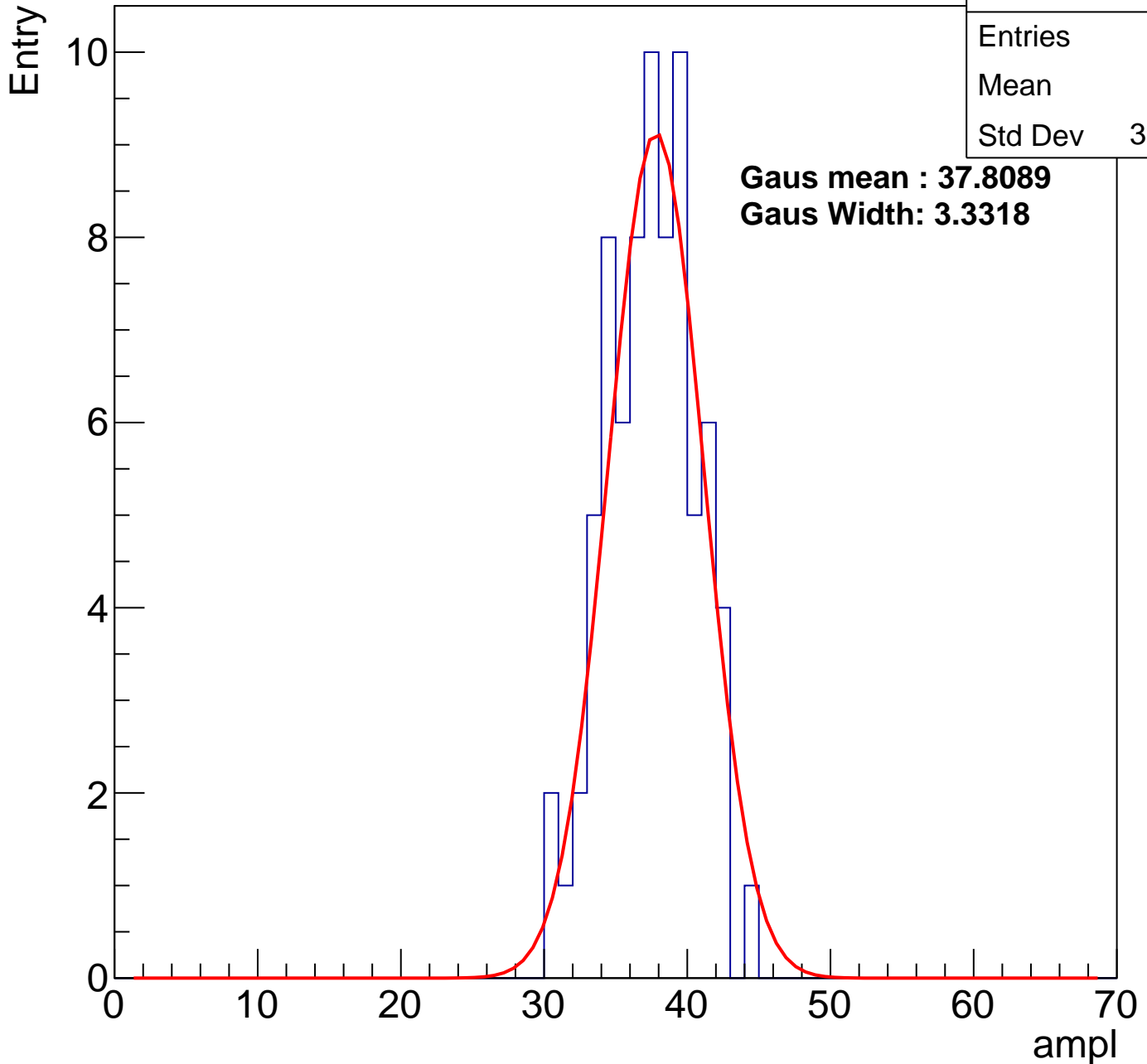
# B0L002S, U23-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	76
Mean	37
Std Dev	3.048

**Gaus mean : 37.8089**

**Gaus Width: 3.3318**



# B0L002S, U23-ch33, adc2

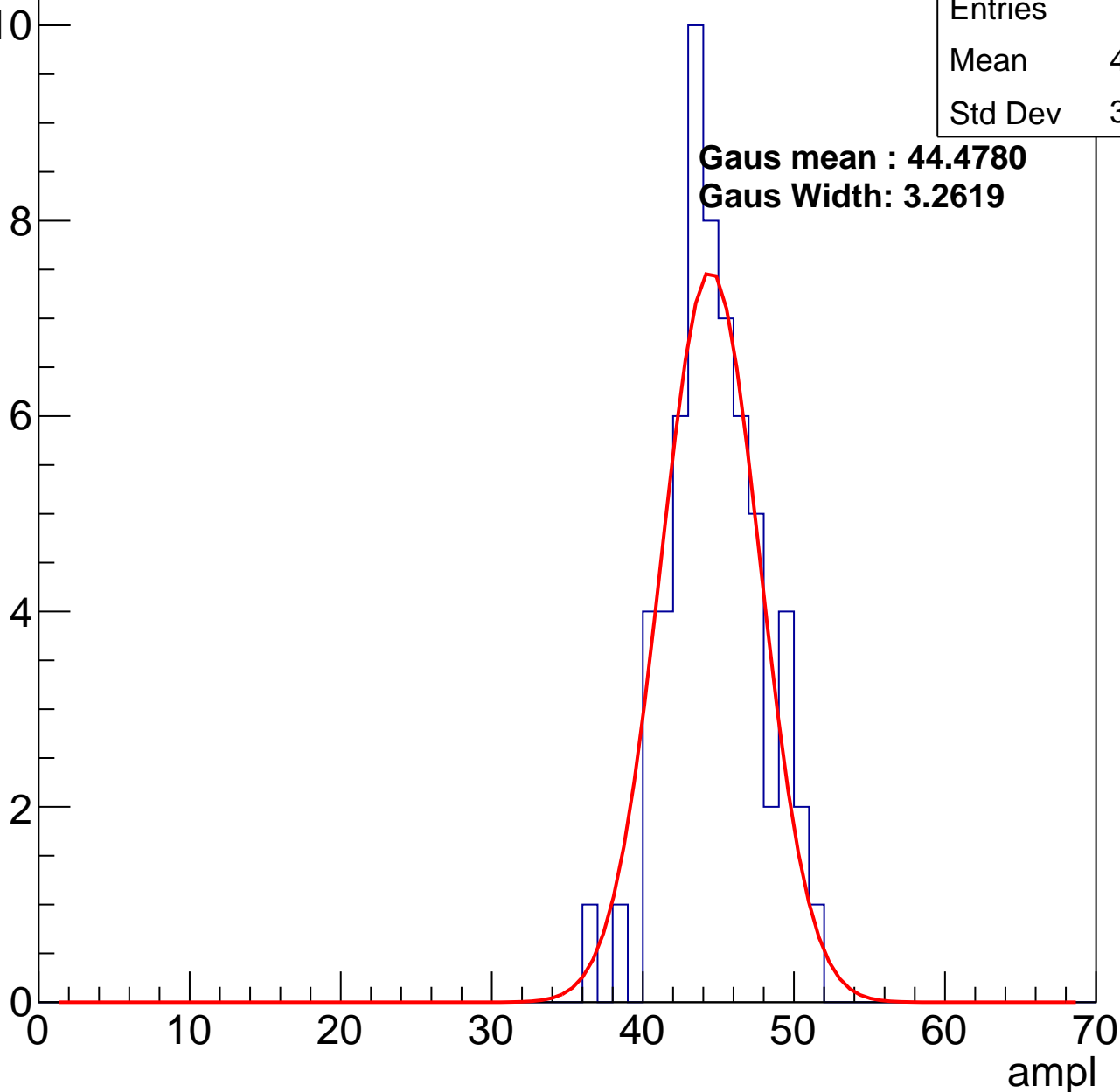
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.28
Std Dev	3.025

**Gaus mean : 44.4780**

**Gaus Width: 3.2619**

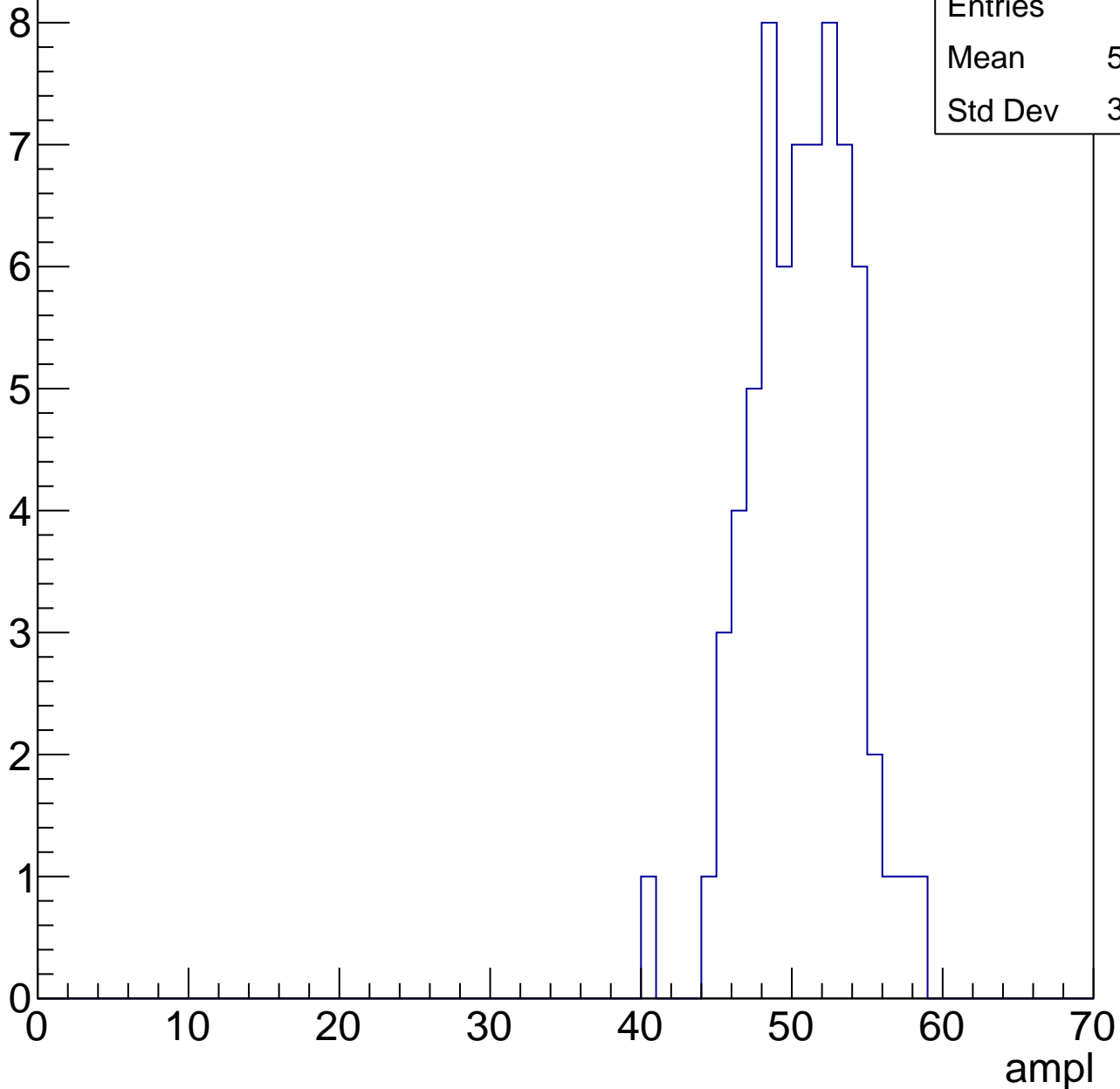


# B0L002S, U23-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.22
Std Dev	3.325

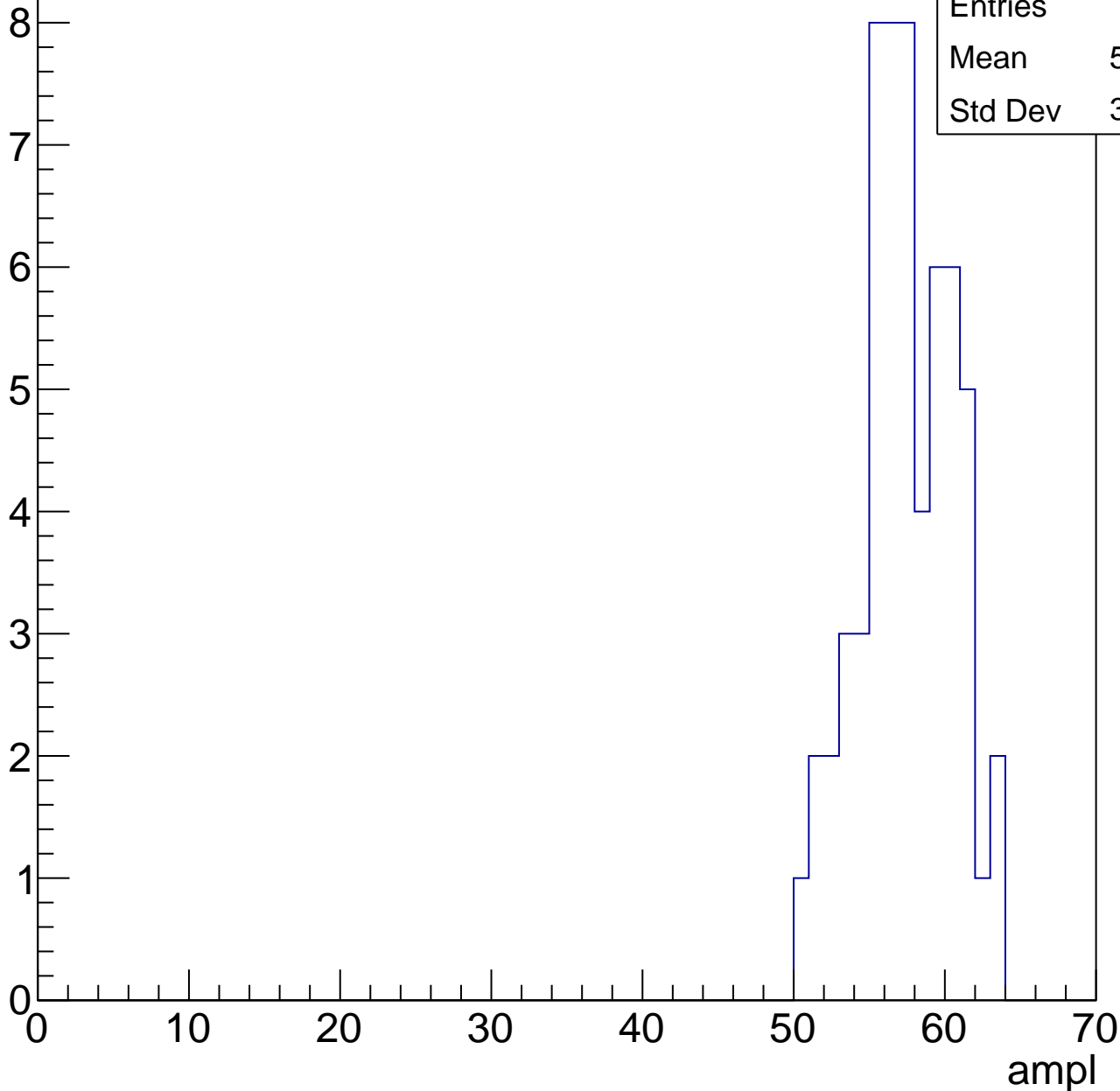


# B0L002S, U23-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.95
Std Dev	3.039

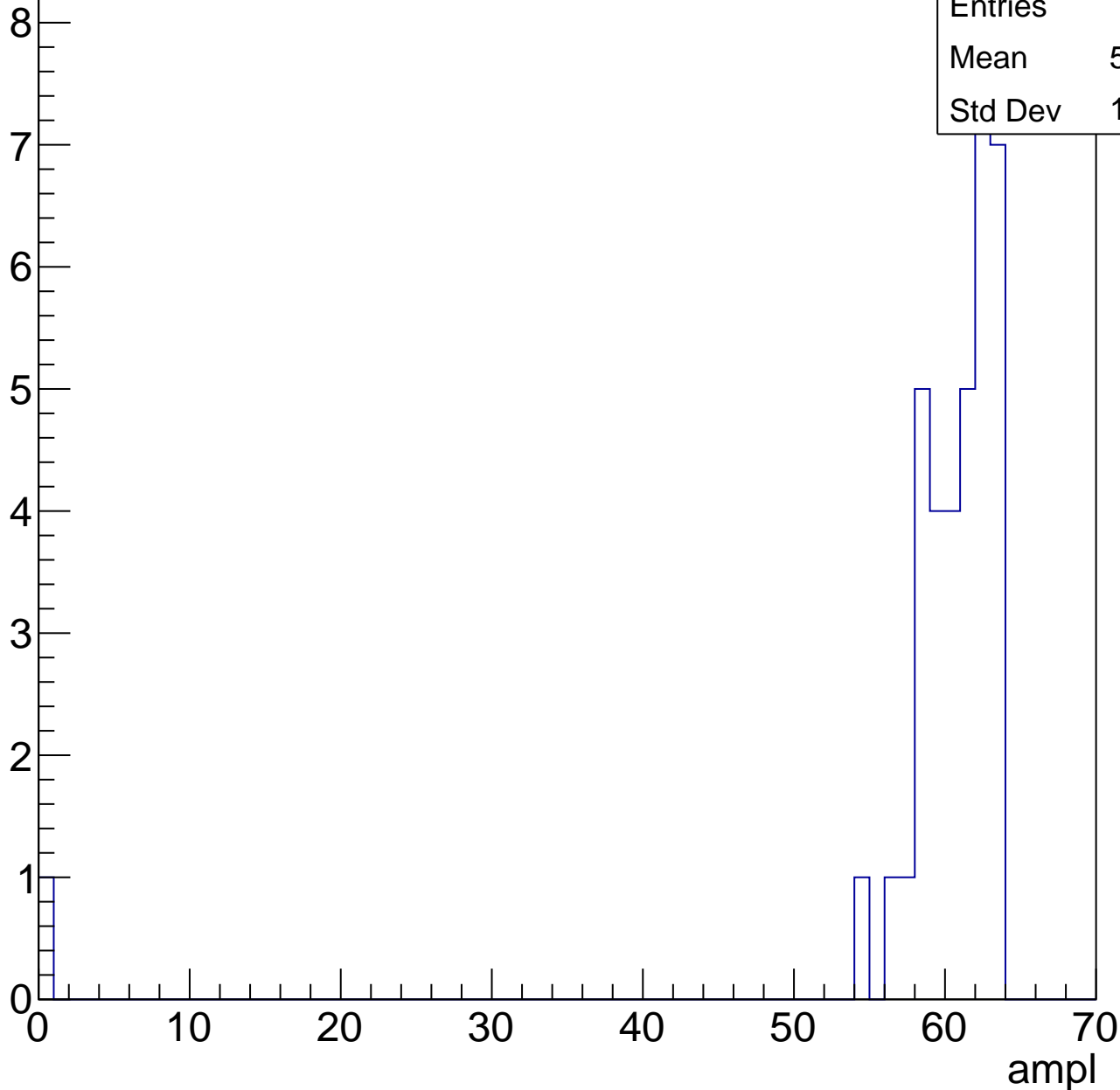


# B0L002S, U23-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.78
Std Dev	10.04



# B0L002S, U23-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch34, adc0

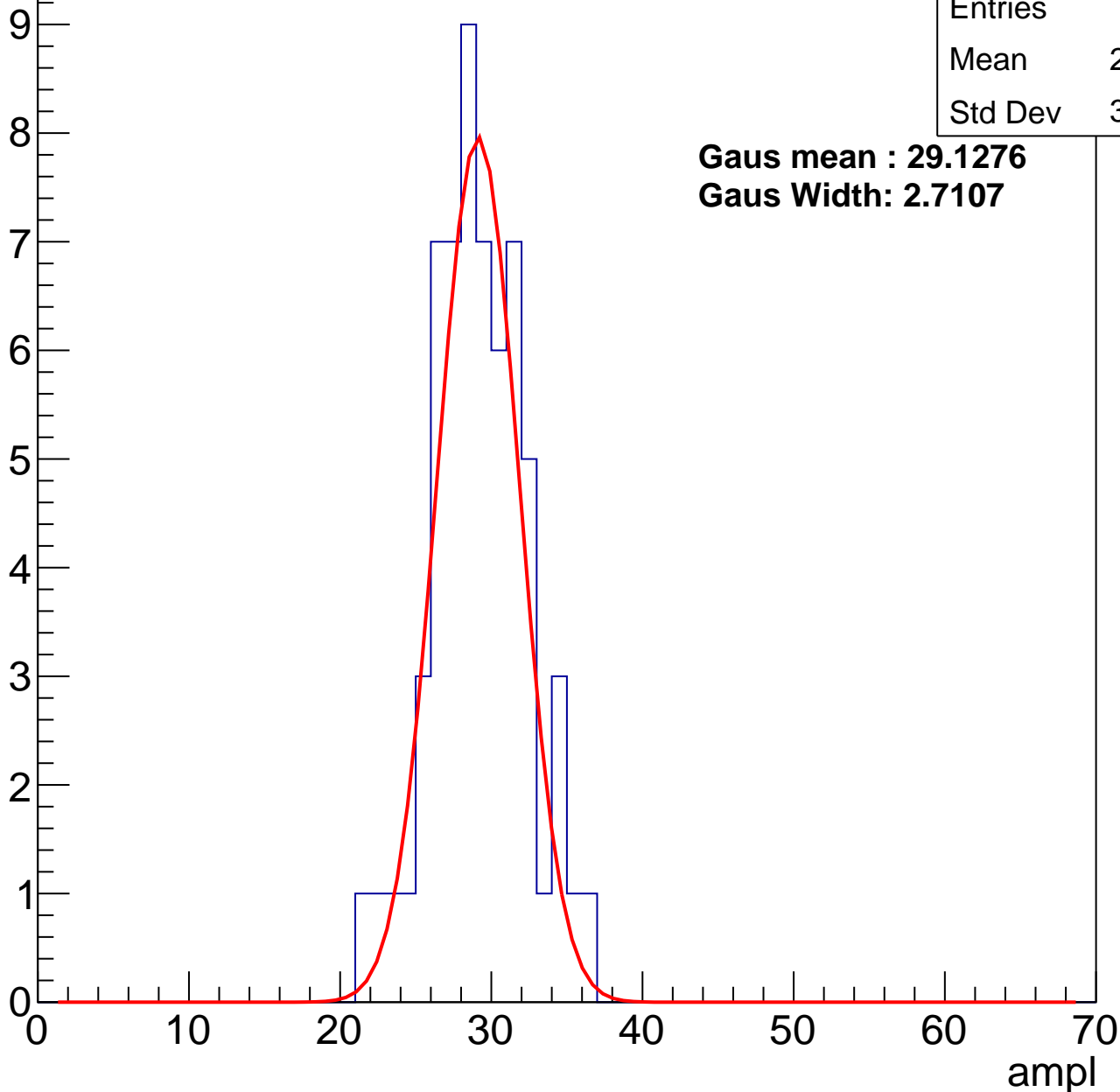
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	28.75
Std Dev	3.066

**Gaus mean : 29.1276**

**Gaus Width: 2.7107**



# B0L002S, U23-ch34, adc1

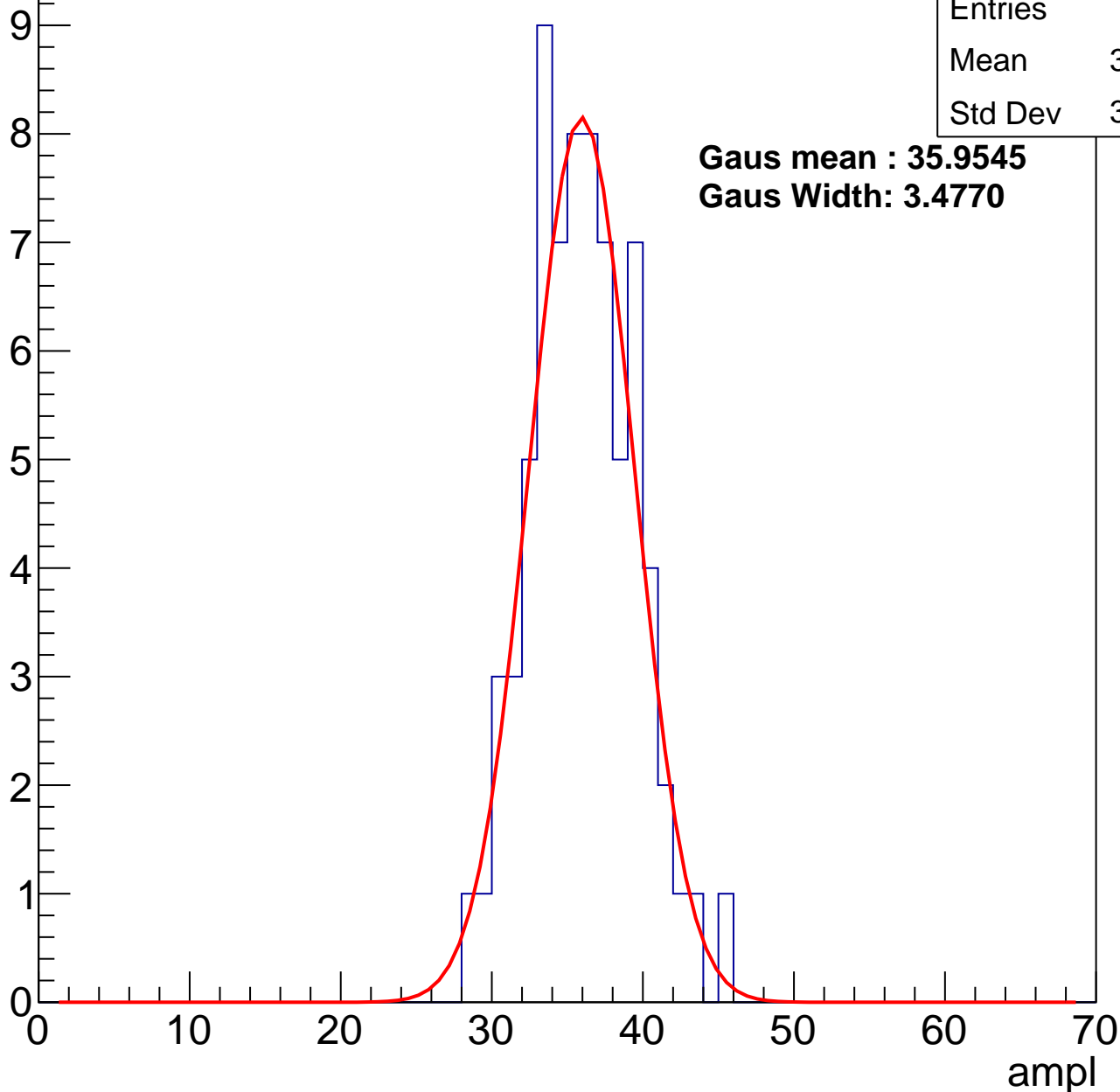
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	35.58
Std Dev	3.412

**Gaus mean : 35.9545**

**Gaus Width: 3.4770**



# B0L002S, U23-ch34, adc2

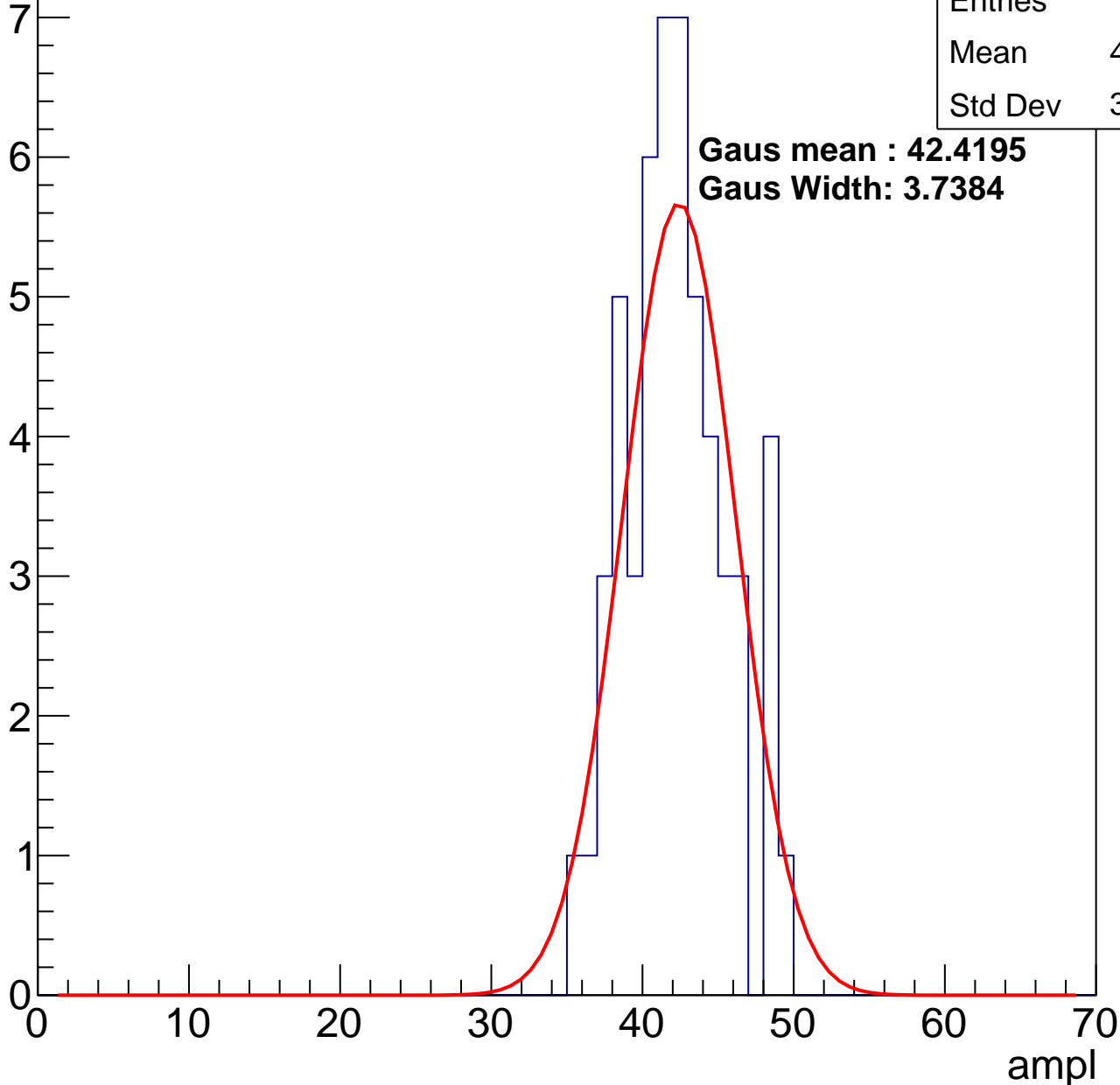
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	41.79
Std Dev	3.322

**Gaus mean : 42.4195**

**Gaus Width: 3.7384**



# B0L002S, U23-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	82
Mean	48.5
Std Dev	3.768

Entry

10

8

6

4

2

0

0

10

20

30

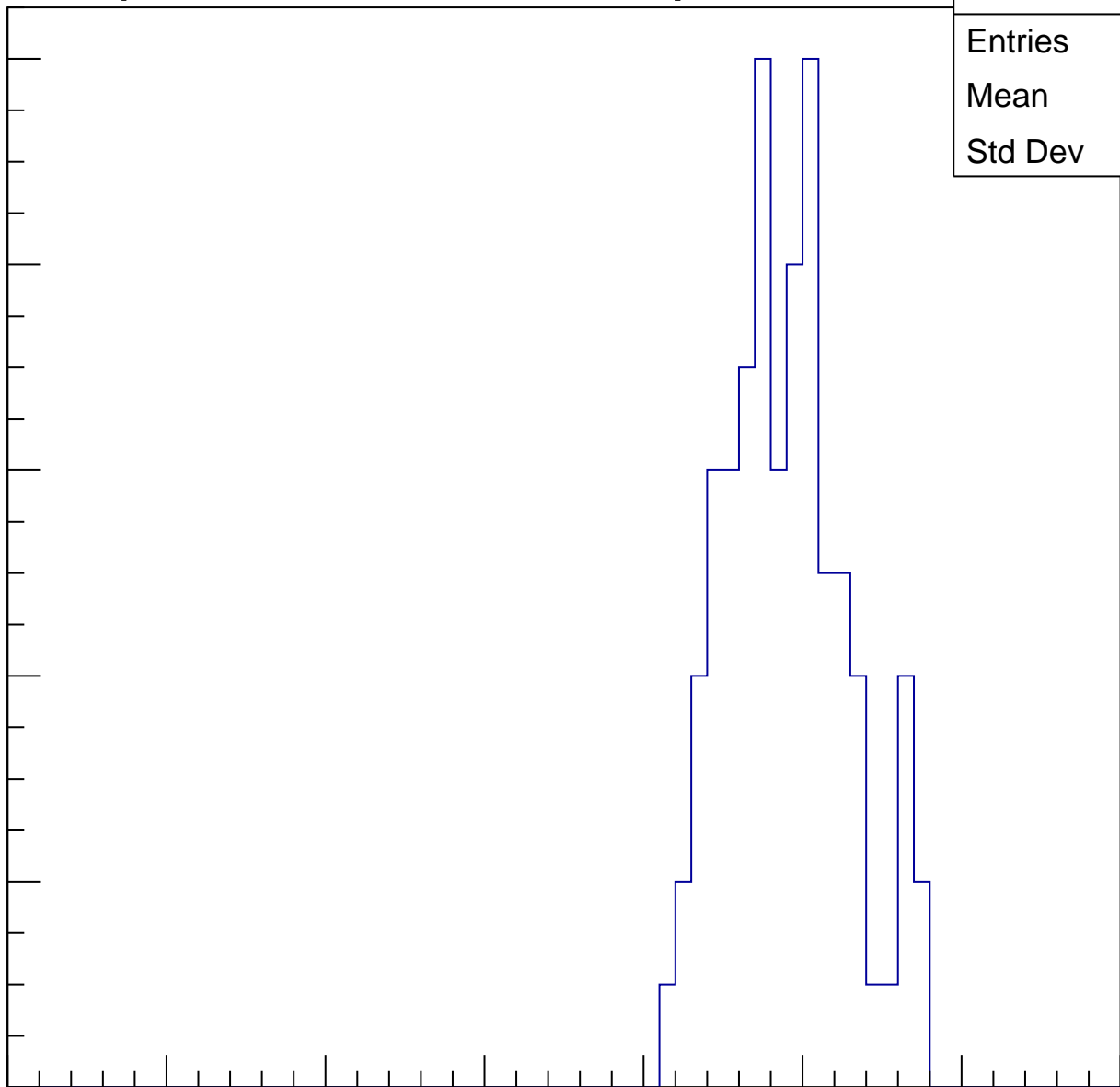
40

50

60

70

ampl

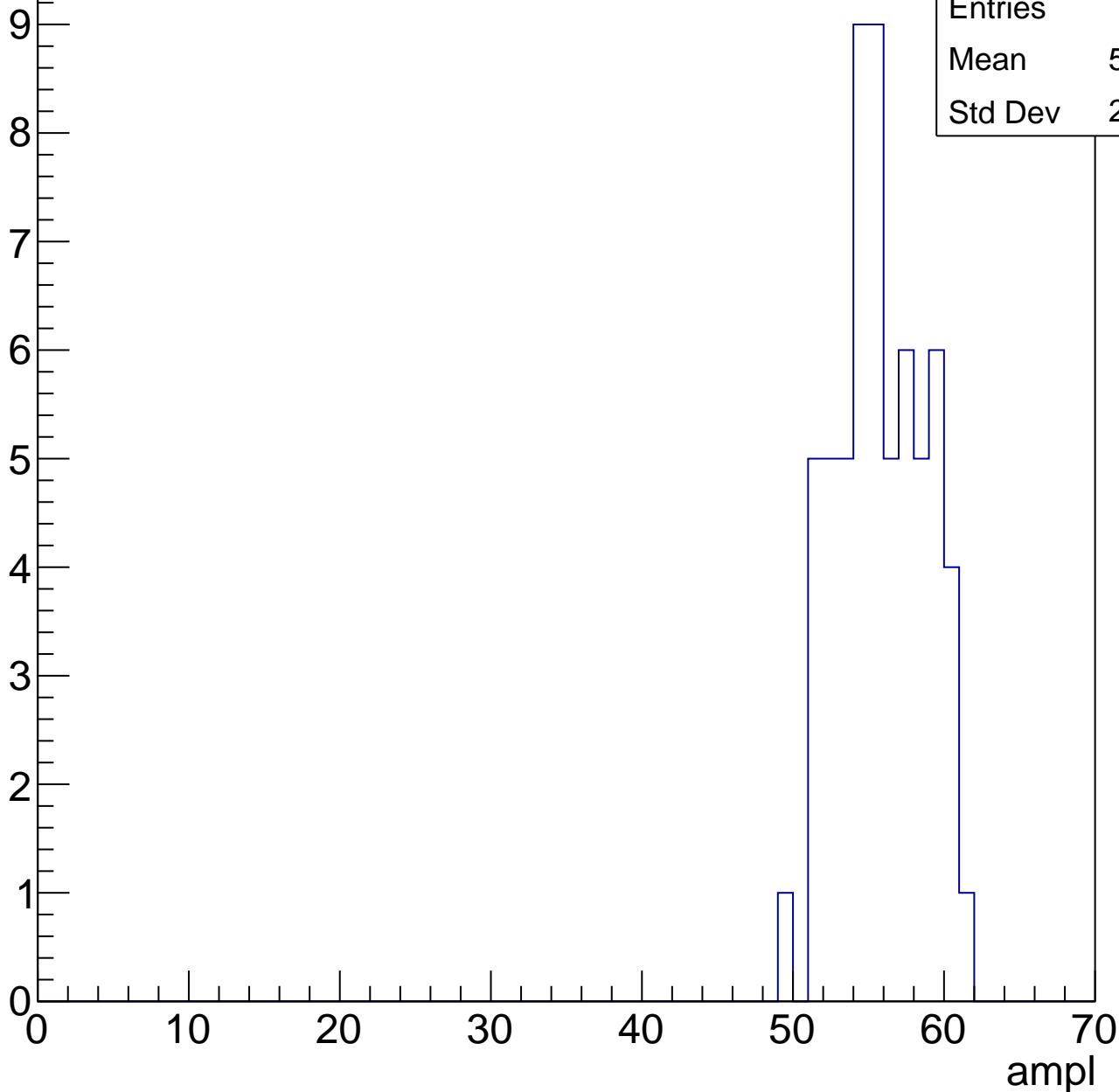


# B0L002S, U23-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	55.36
Std Dev	2.829

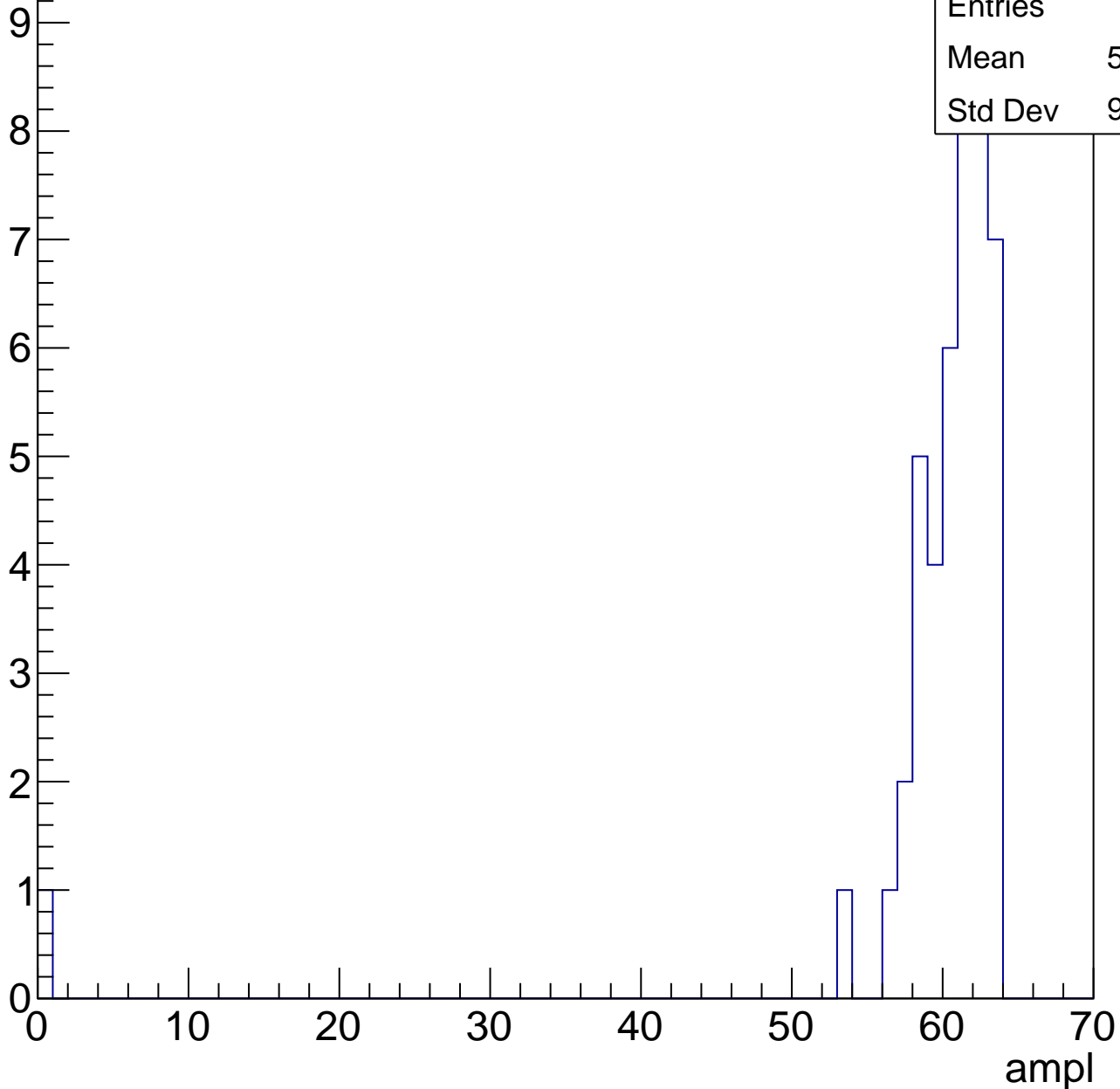


# B0L002S, U23-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.04
Std Dev	9.158



# B0L002S, U23-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch35, adc0

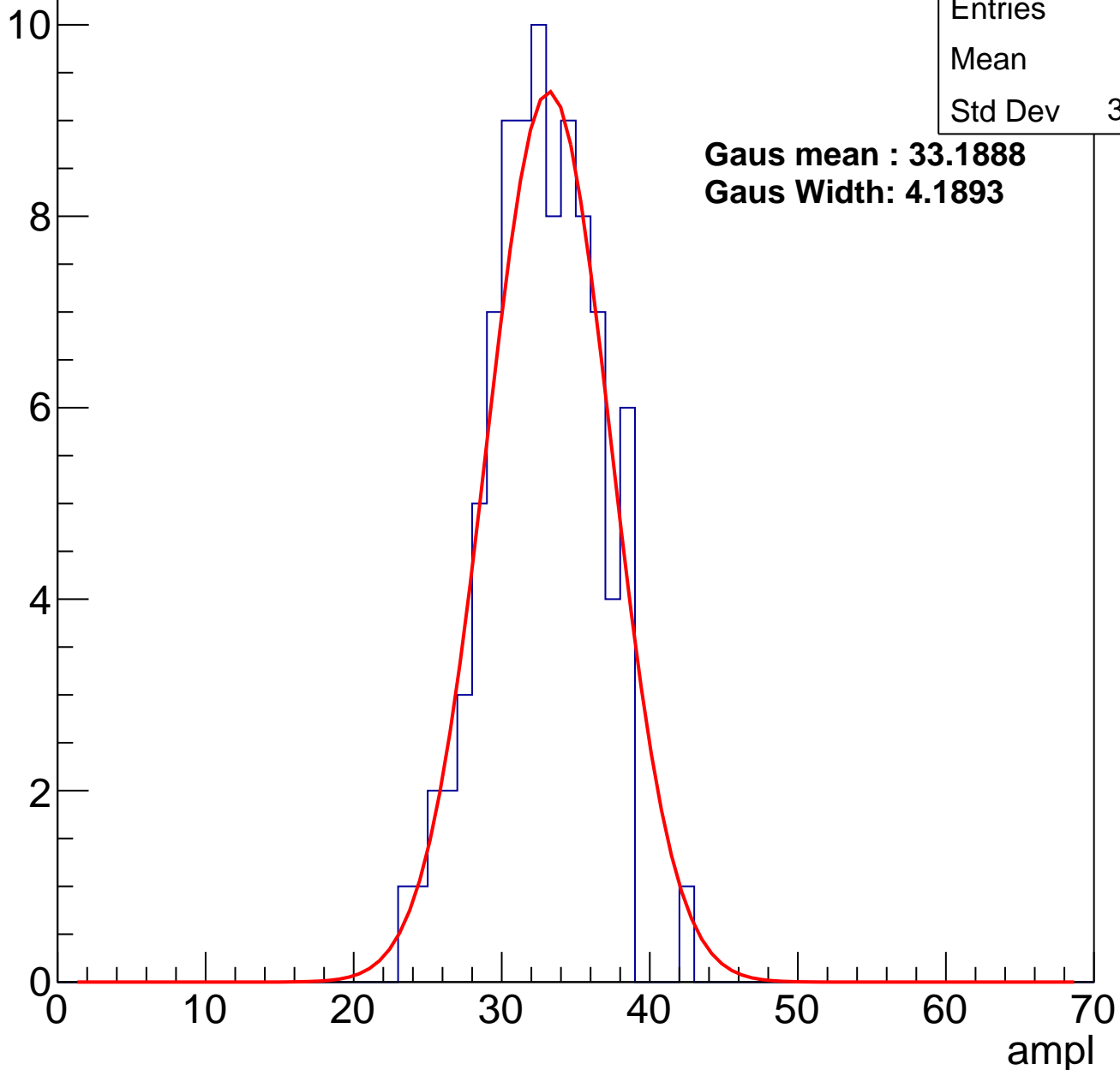
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	92
Mean	32.2
Std Dev	3.639

**Gaus mean : 33.1888**

**Gaus Width: 4.1893**

Entry



# B0L002S, U23-ch35, adc1

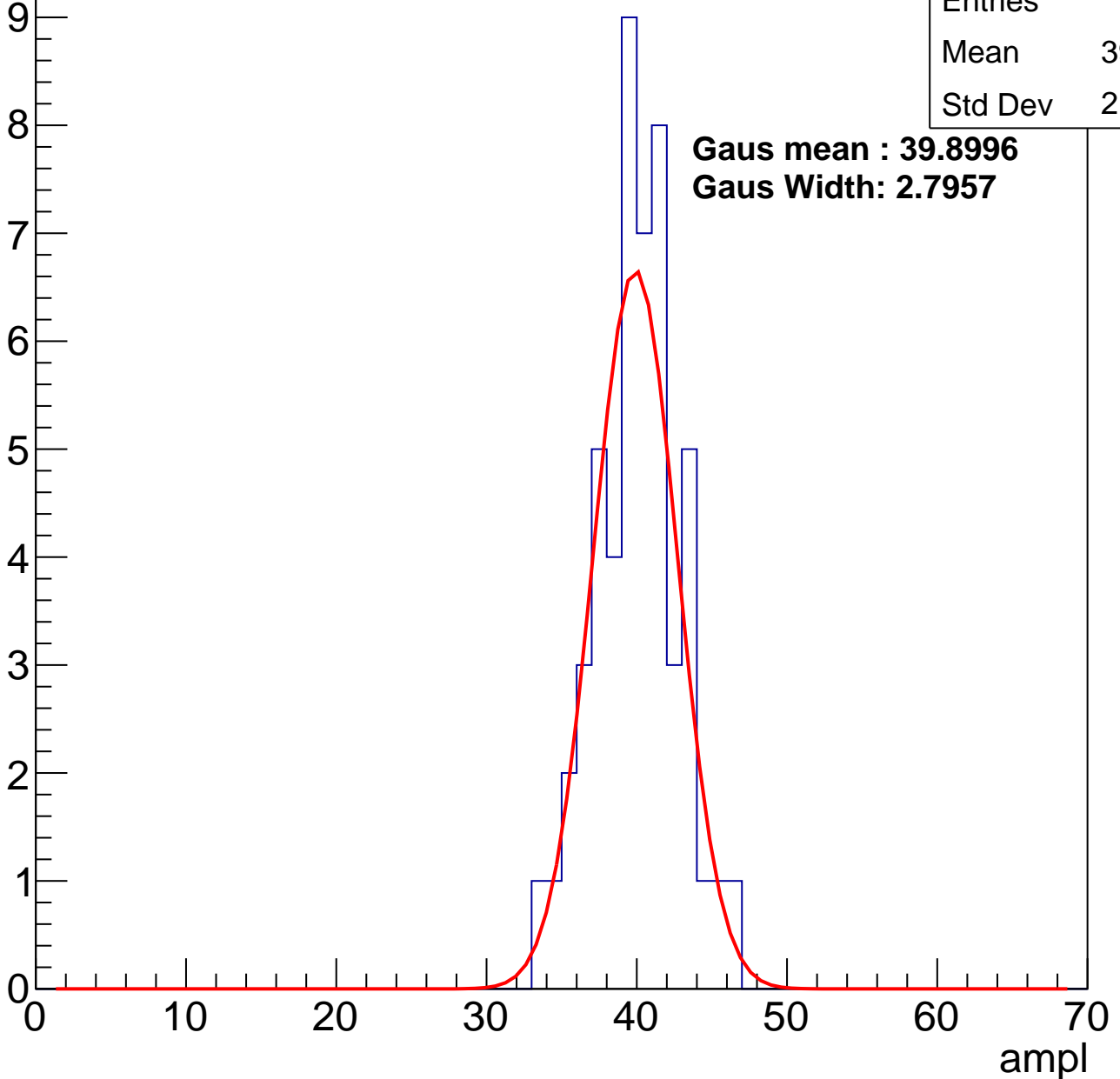
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	39.55
Std Dev	2.746

**Gaus mean : 39.8996**

**Gaus Width: 2.7957**



# B0L002S, U23-ch35, adc2

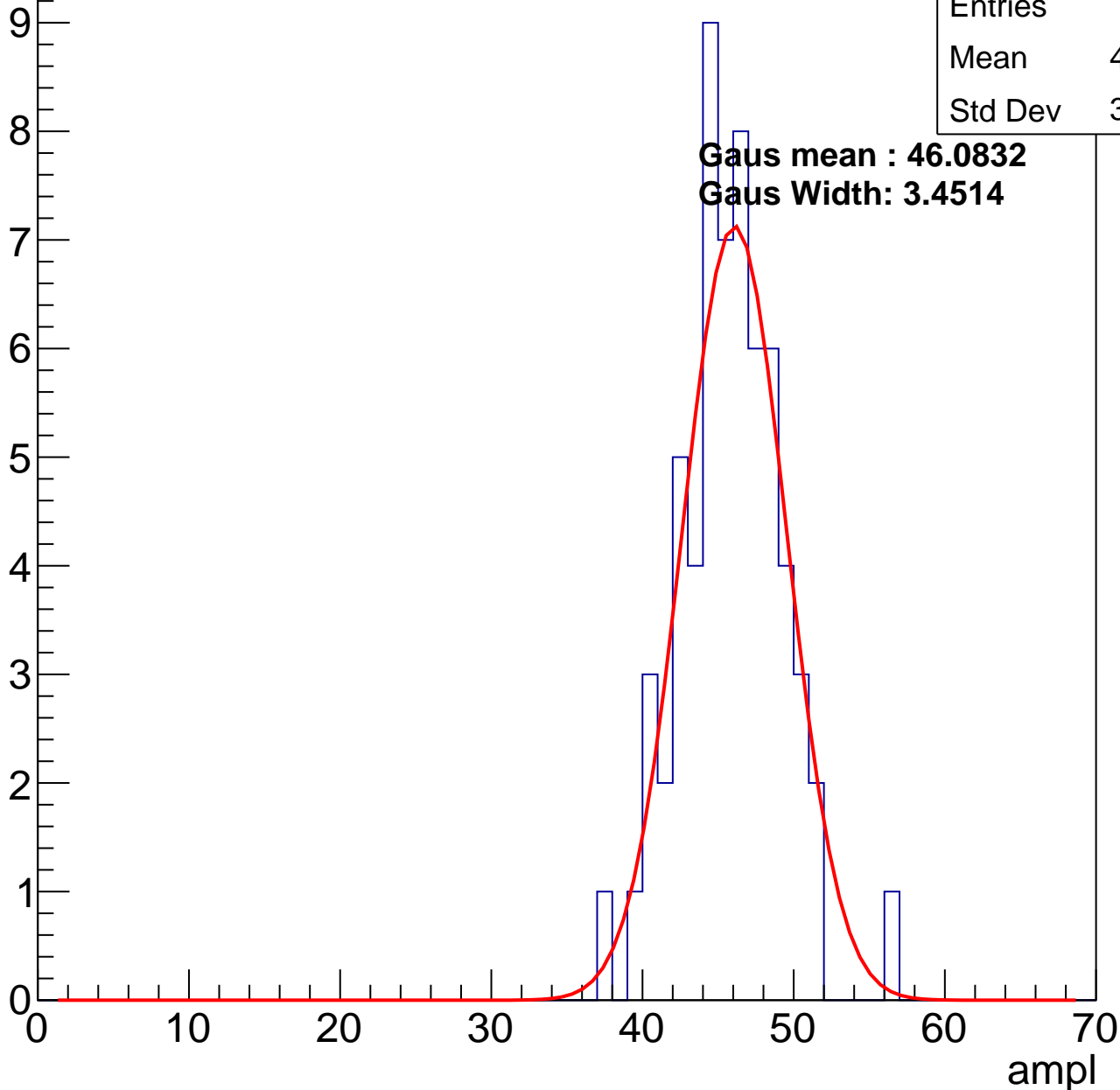
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.37
Std Dev	3.327

**Gaus mean : 46.0832**

**Gaus Width: 3.4514**

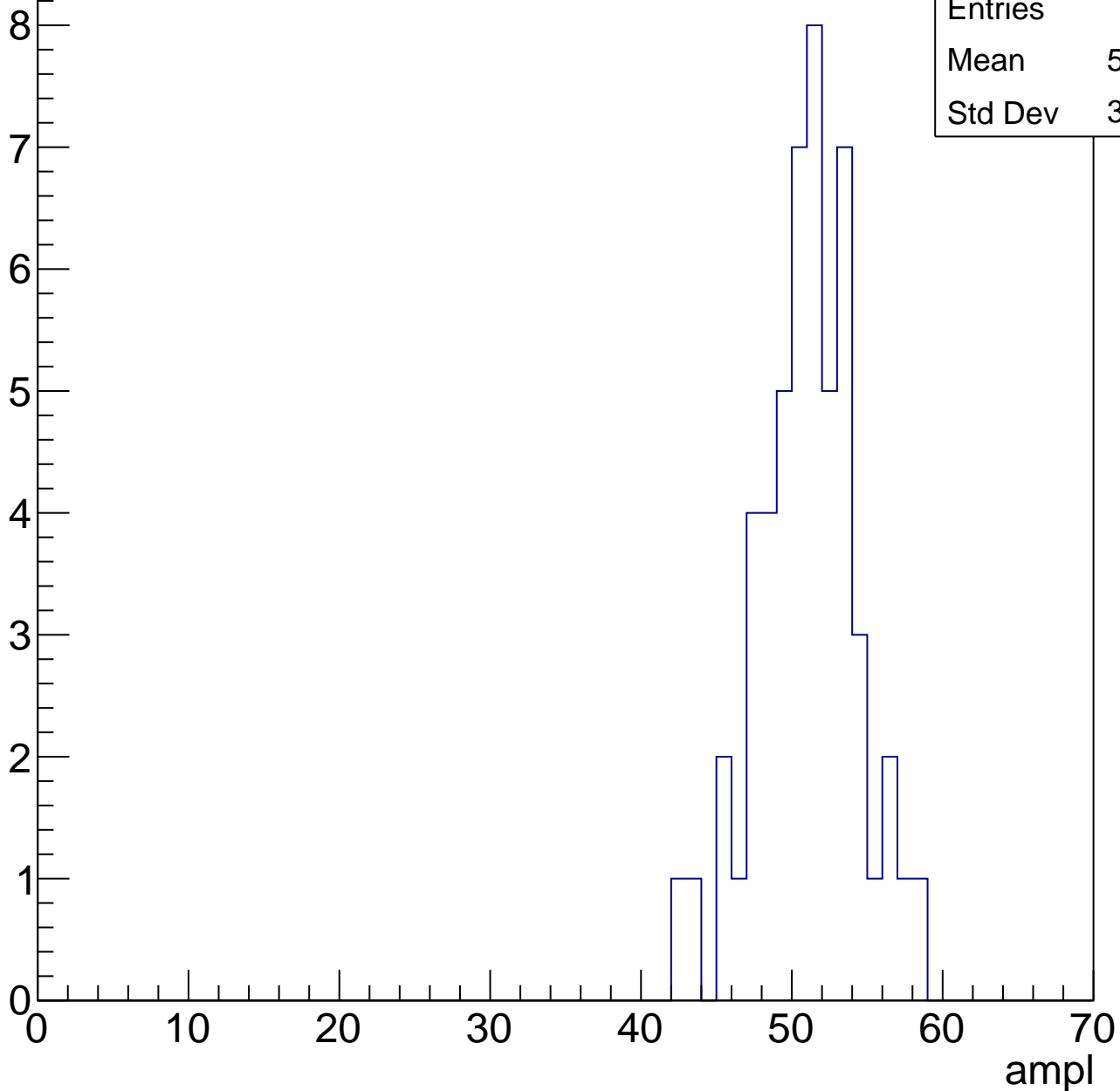


# B0L002S, U23-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	50.55
Std Dev	3.277

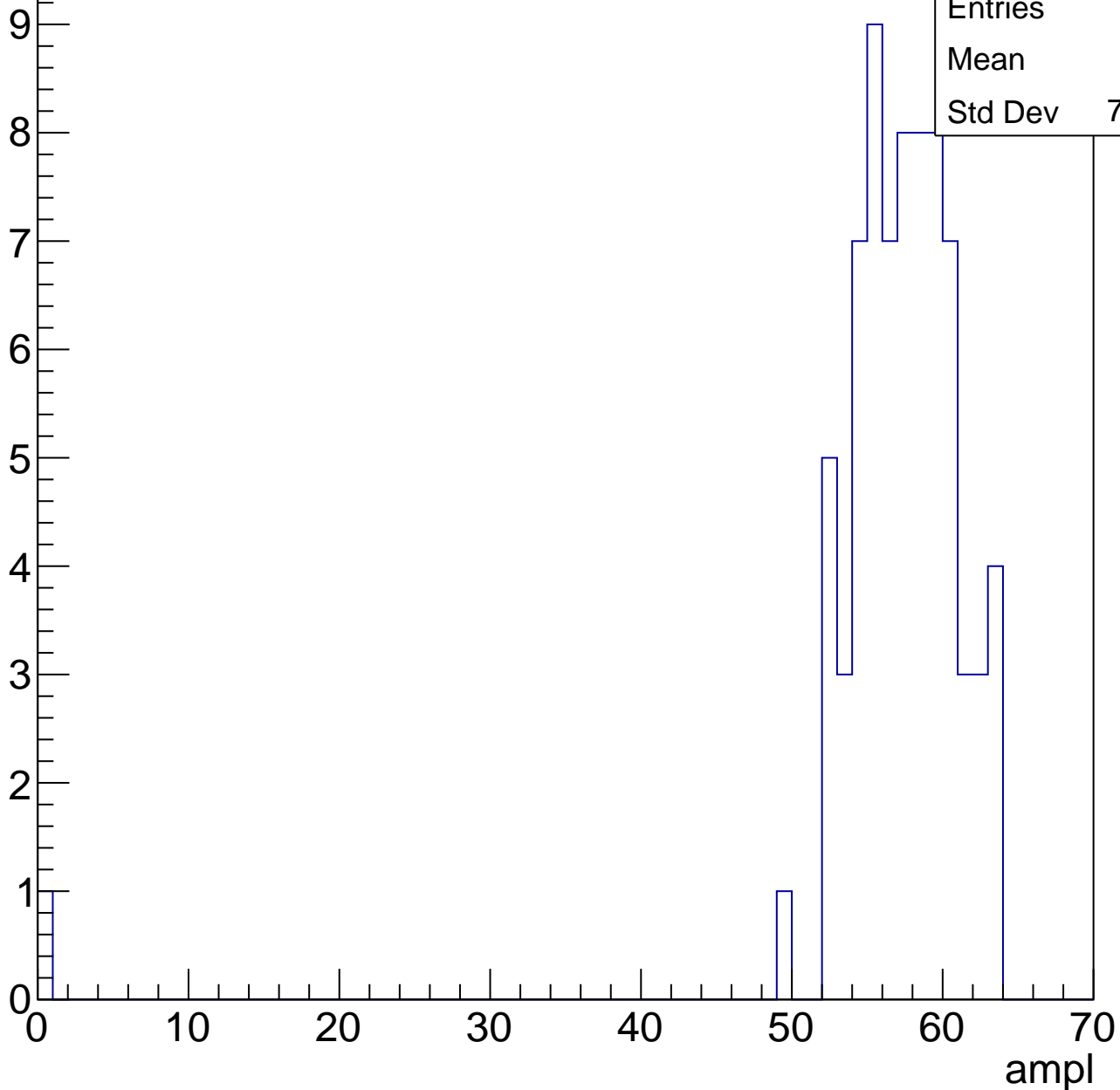


# B0L002S, U23-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	56.3
Std Dev	7.283

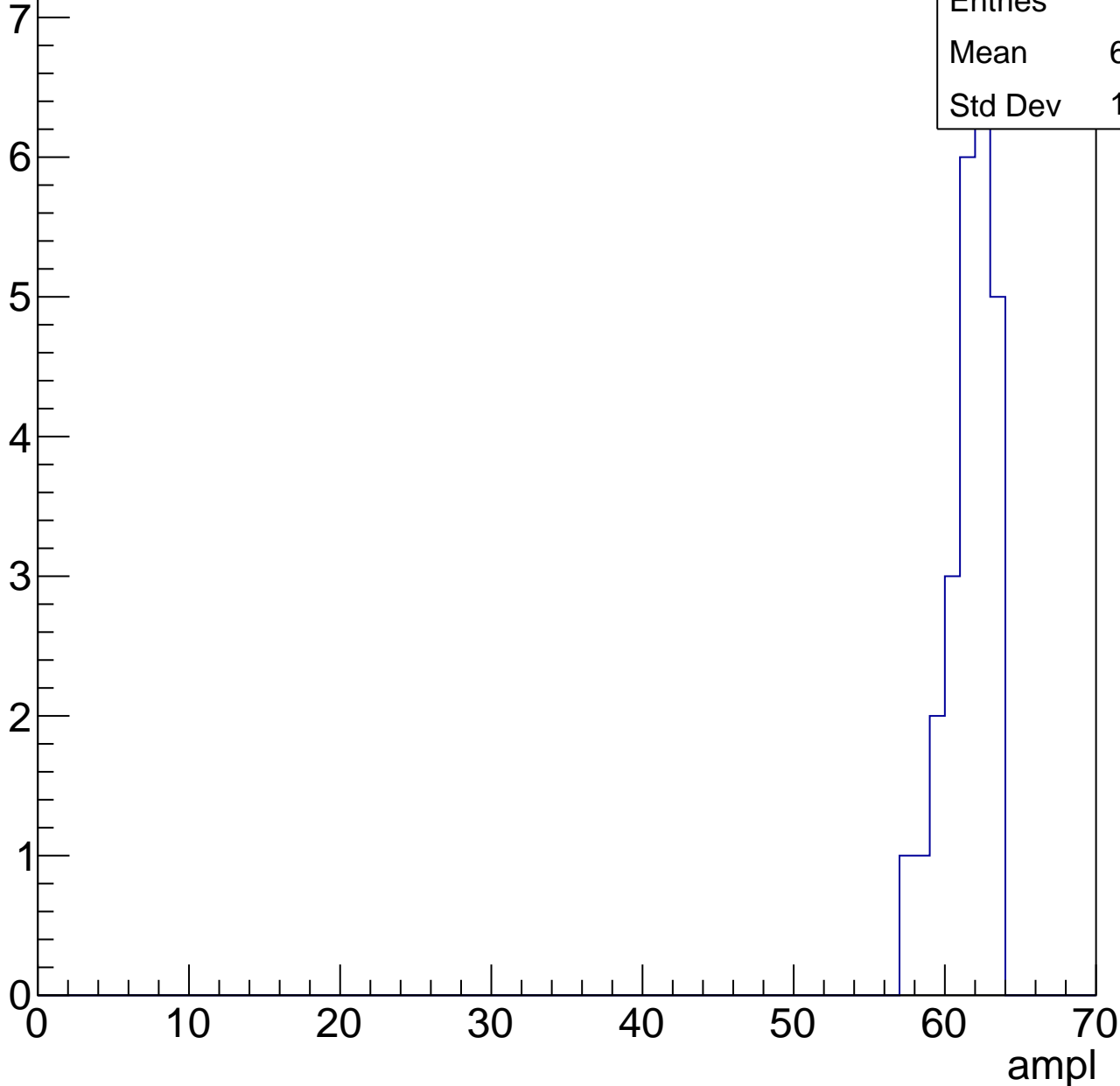


# B0L002S, U23-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	25
Mean	61.12
Std Dev	1.583



# B0L002S, U23-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L002S, U23-ch36, adc0

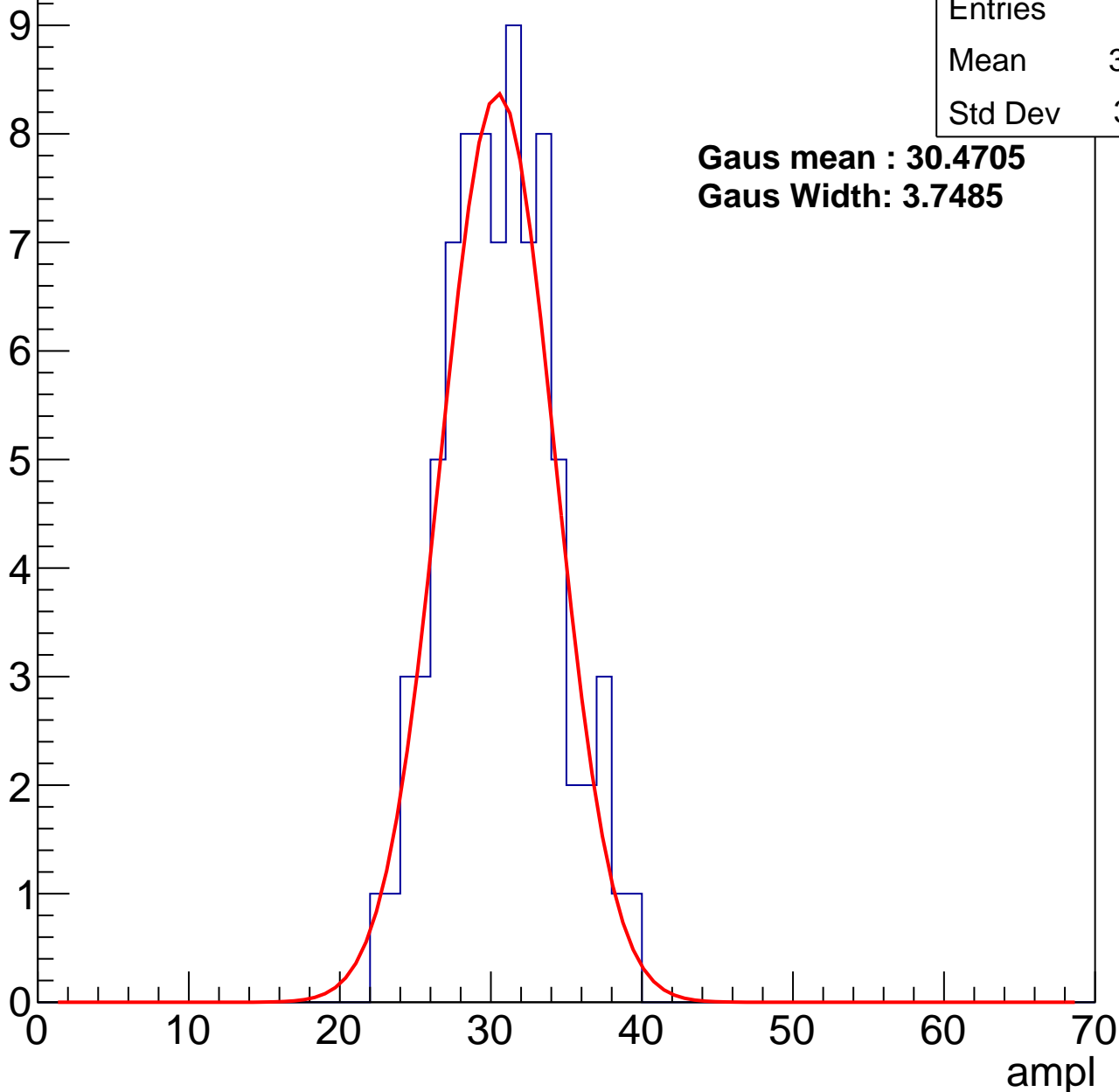
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	30.17
Std Dev	3.651

**Gaus mean : 30.4705**

**Gaus Width: 3.7485**



# B0L002S, U23-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	71
Mean	38
Std Dev	3.403

**Gaus mean : 38.0066**

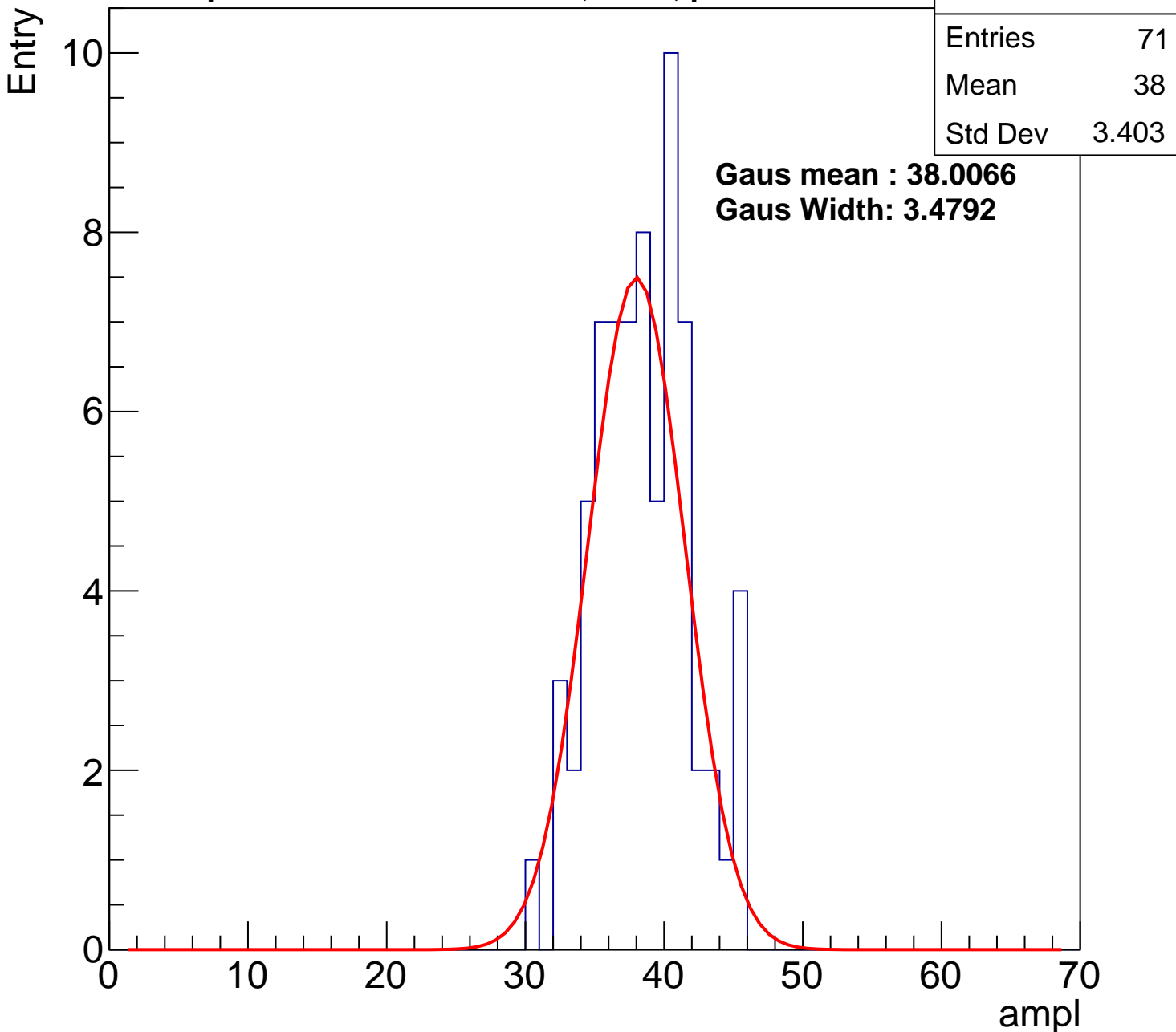
**Gaus Width: 3.4792**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch36, adc2

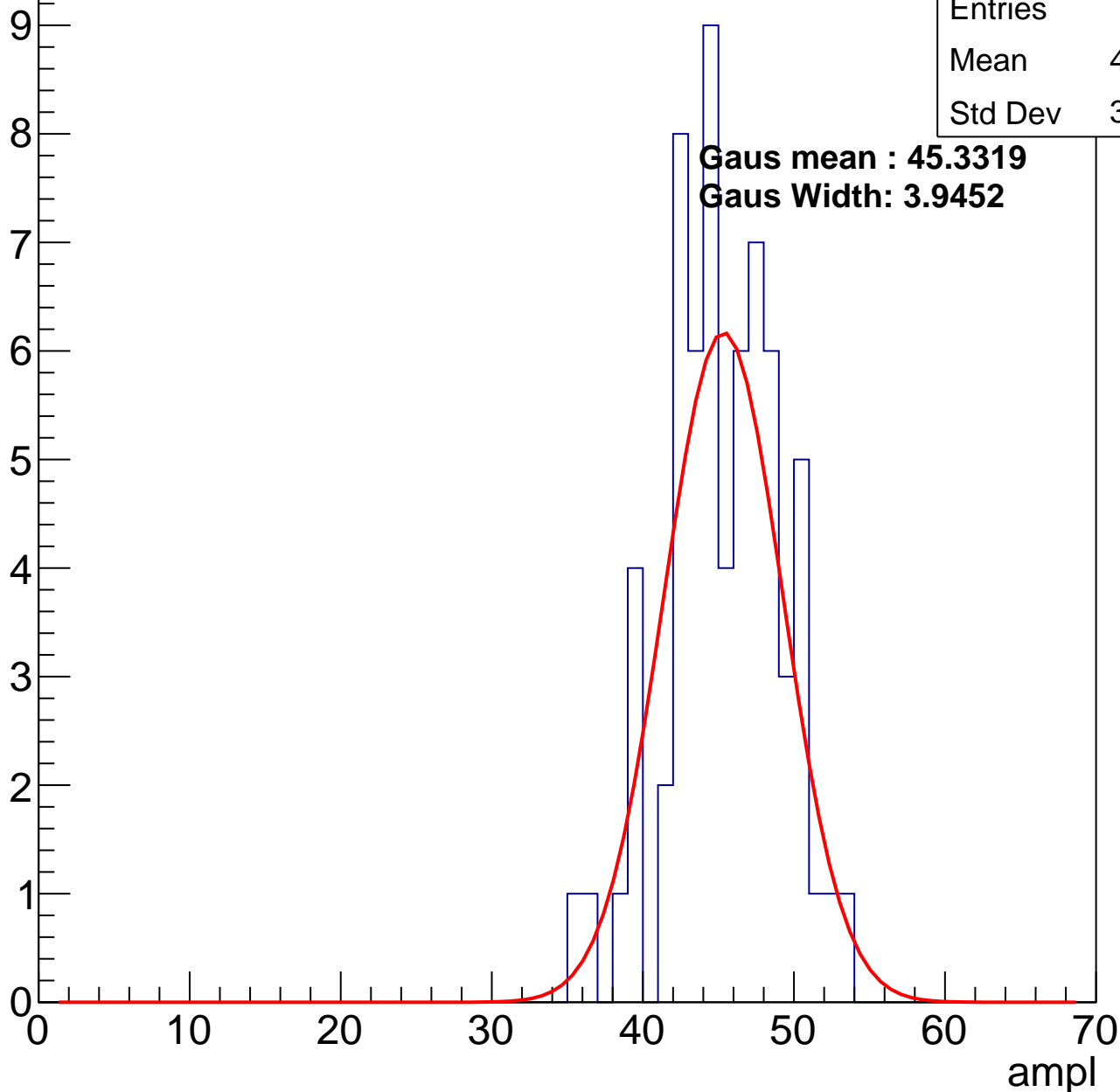
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.89
Std Dev	3.734

**Gaus mean : 45.3319**

**Gaus Width: 3.9452**

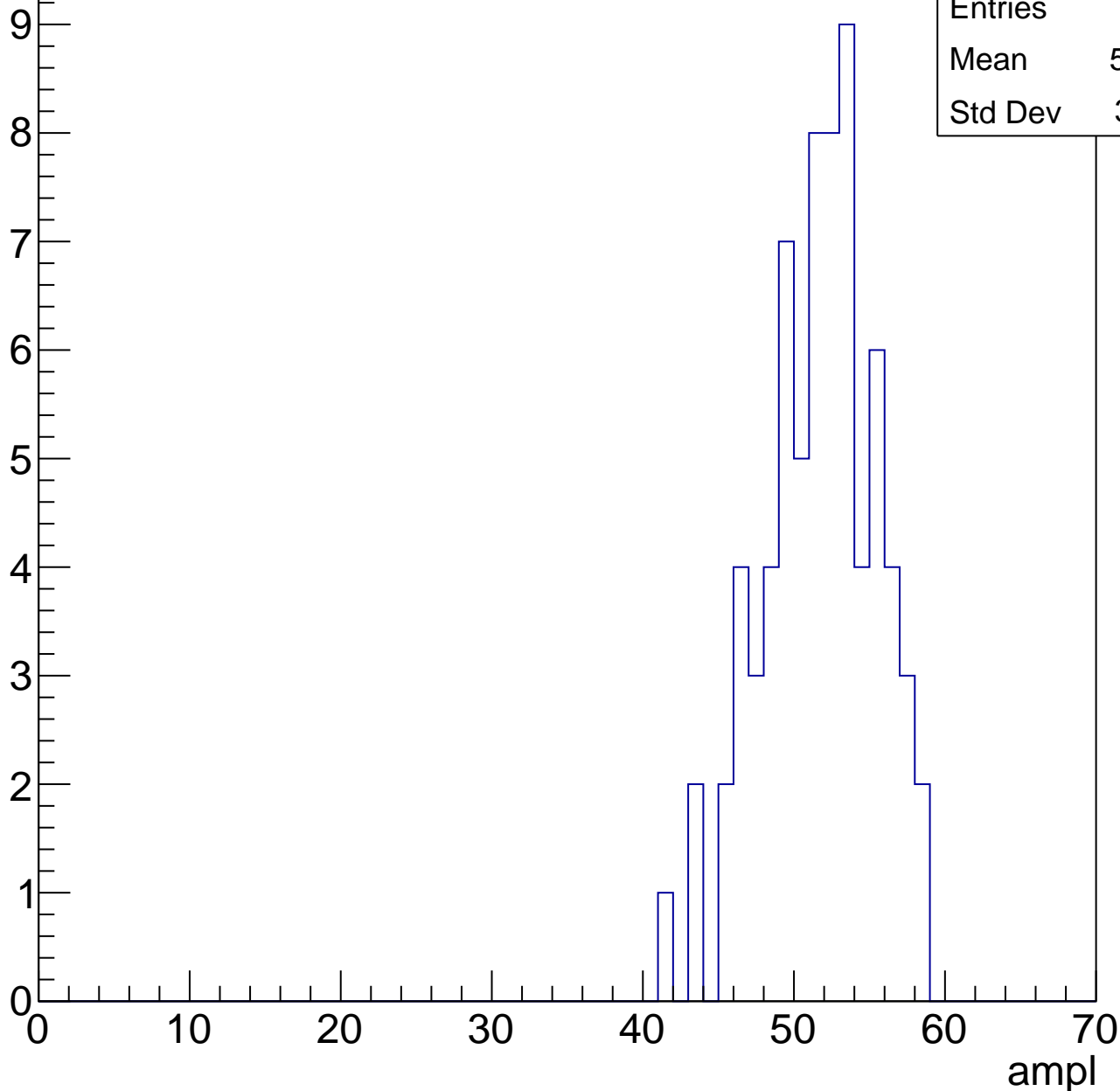


# B0L002S, U23-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	51.18
Std Dev	3.721

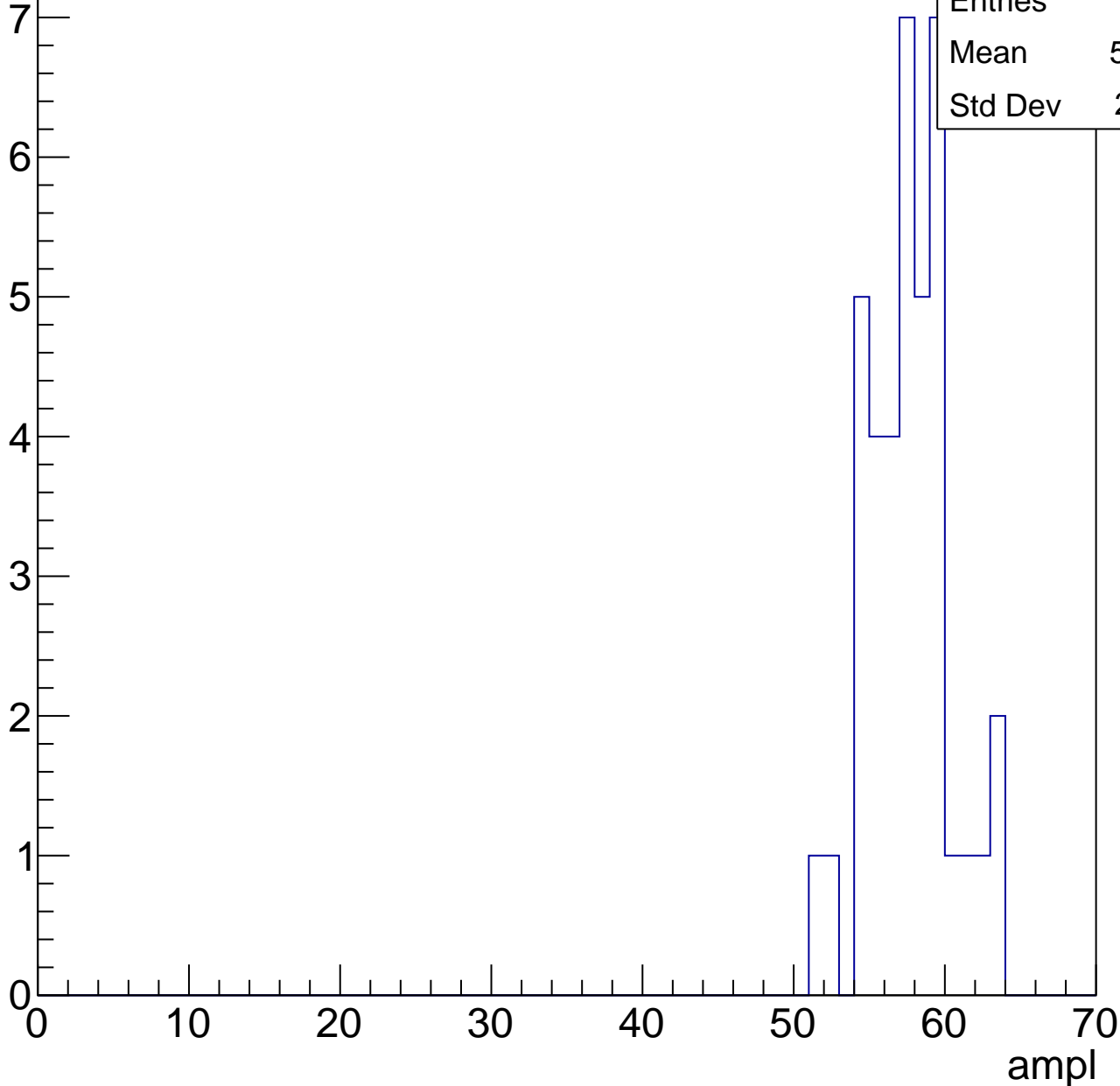


# B0L002S, U23-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	57.13
Std Dev	2.681

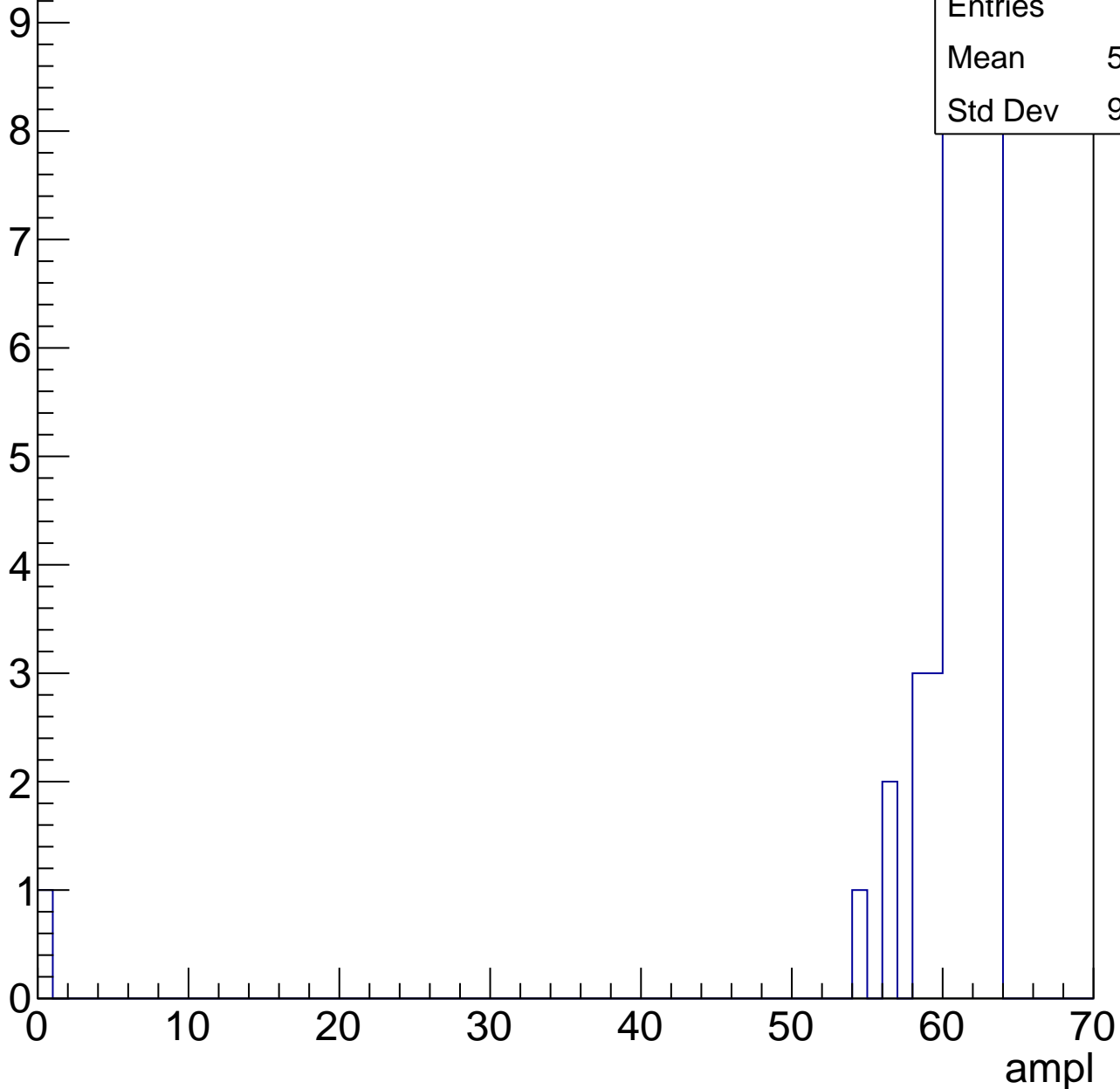


# B0L002S, U23-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	44
Mean	59.25
Std Dev	9.262



# B0L002S, U23-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch37, adc0

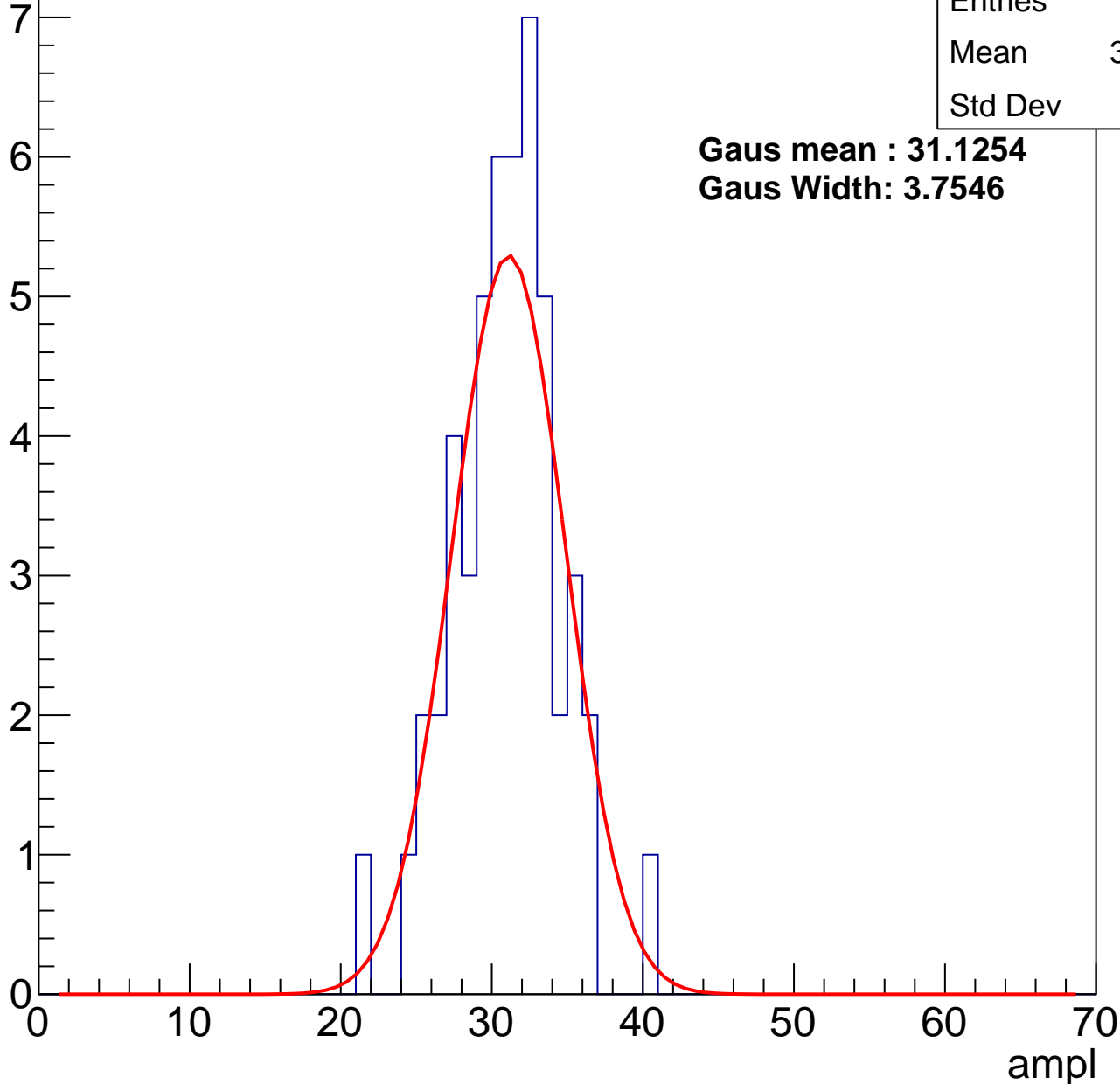
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	30.48
Std Dev	3.46

**Gaus mean : 31.1254**

**Gaus Width: 3.7546**

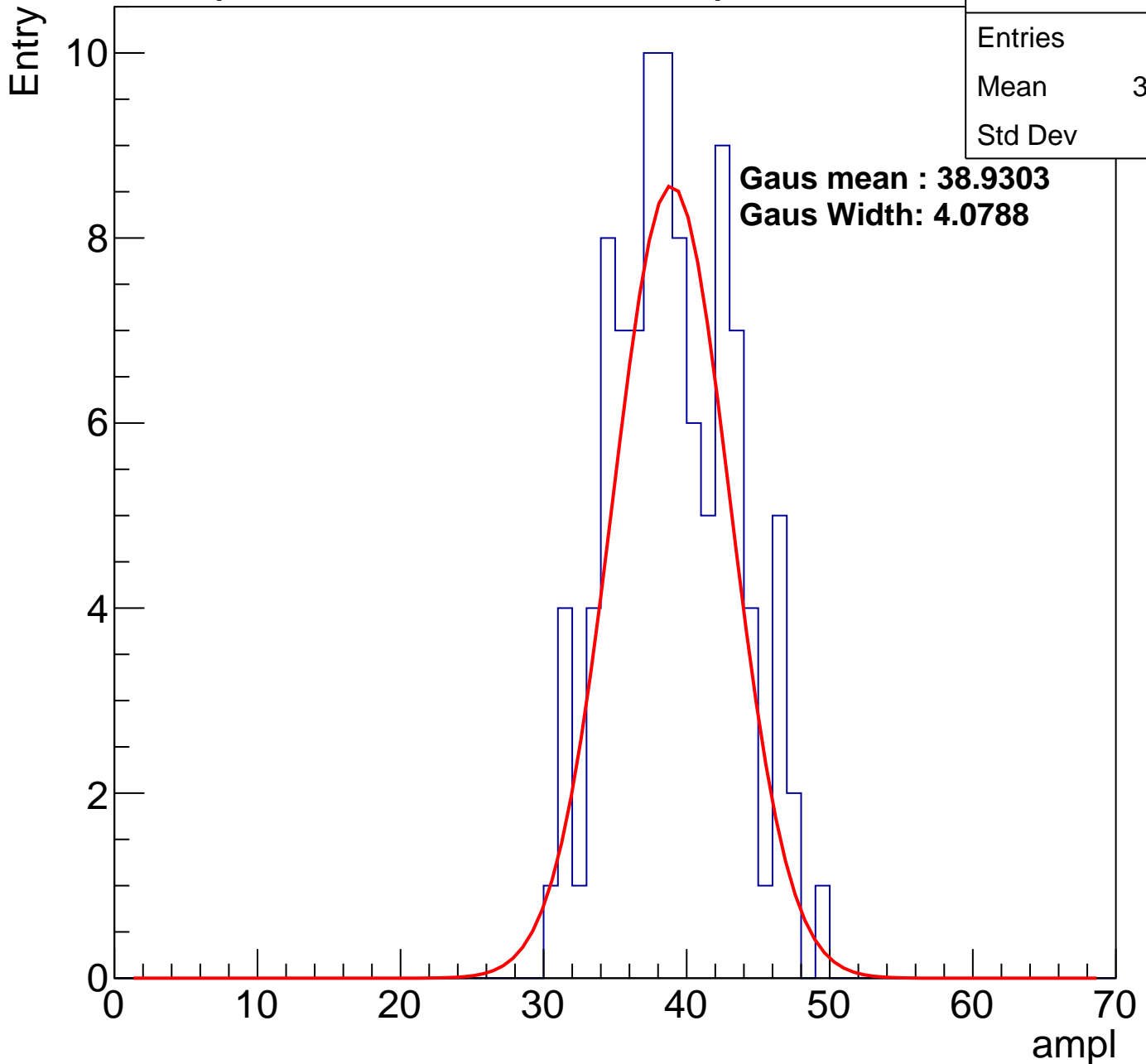


# B0L002S, U23-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	100
Mean	38.67
Std Dev	4.21

**Gaus mean : 38.9303**  
**Gaus Width: 4.0788**



# B0L002S, U23-ch37, adc2

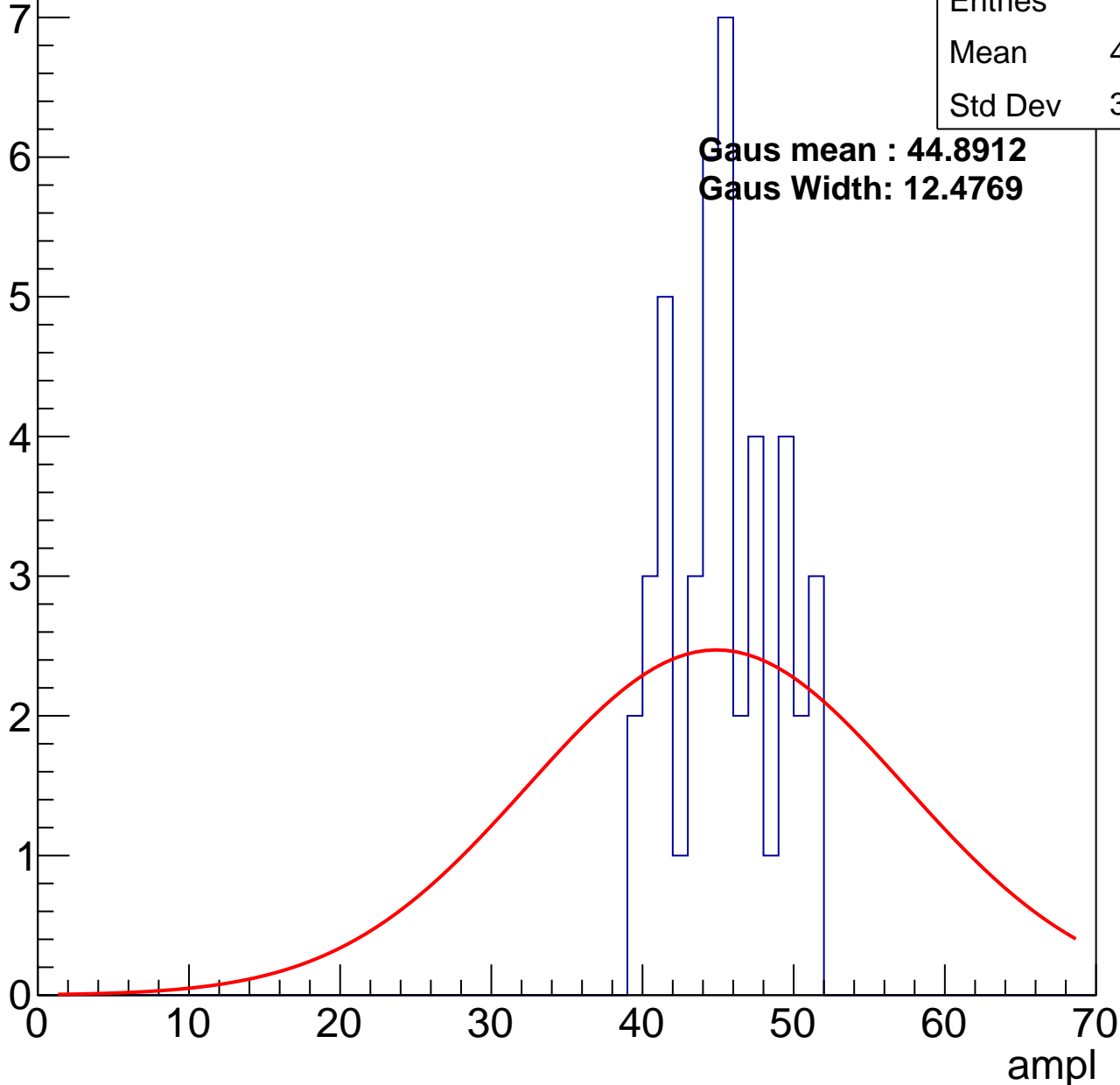
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	44.88
Std Dev	3.418

**Gaus mean : 44.8912**

**Gaus Width: 12.4769**

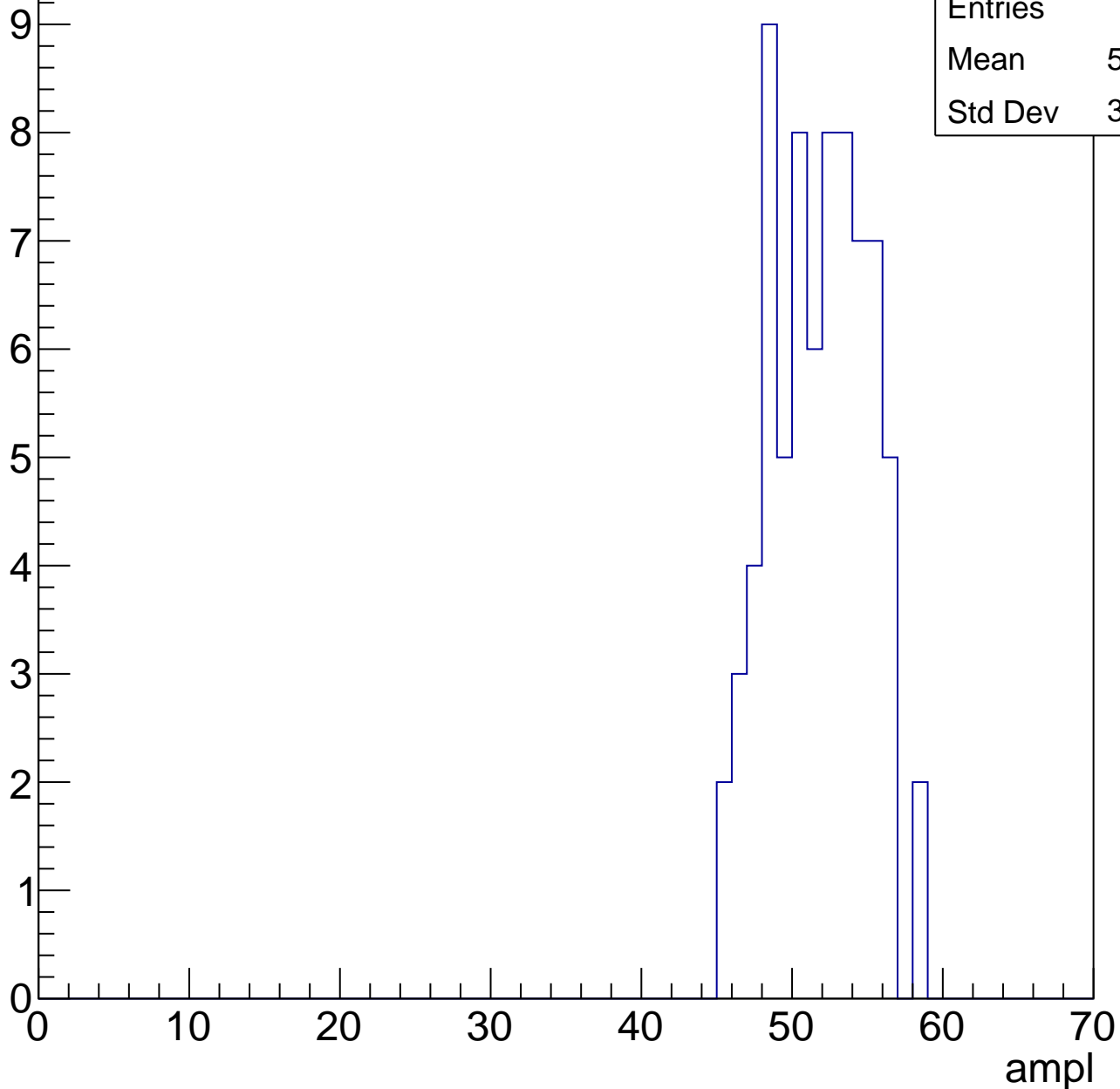


# B0L002S, U23-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

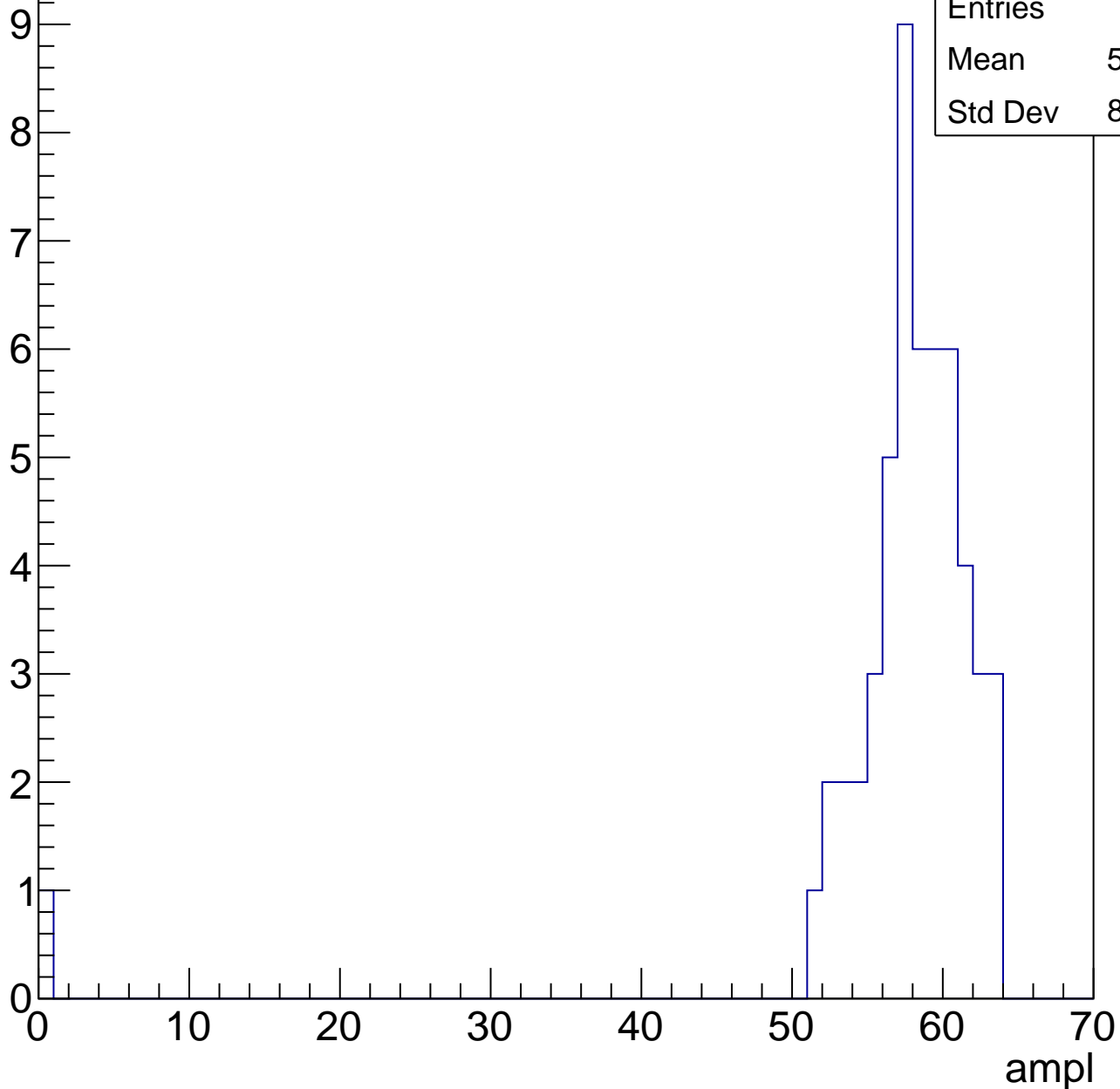
Entries	74
Mean	51.32
Std Dev	3.184



# B0L002S, U23-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



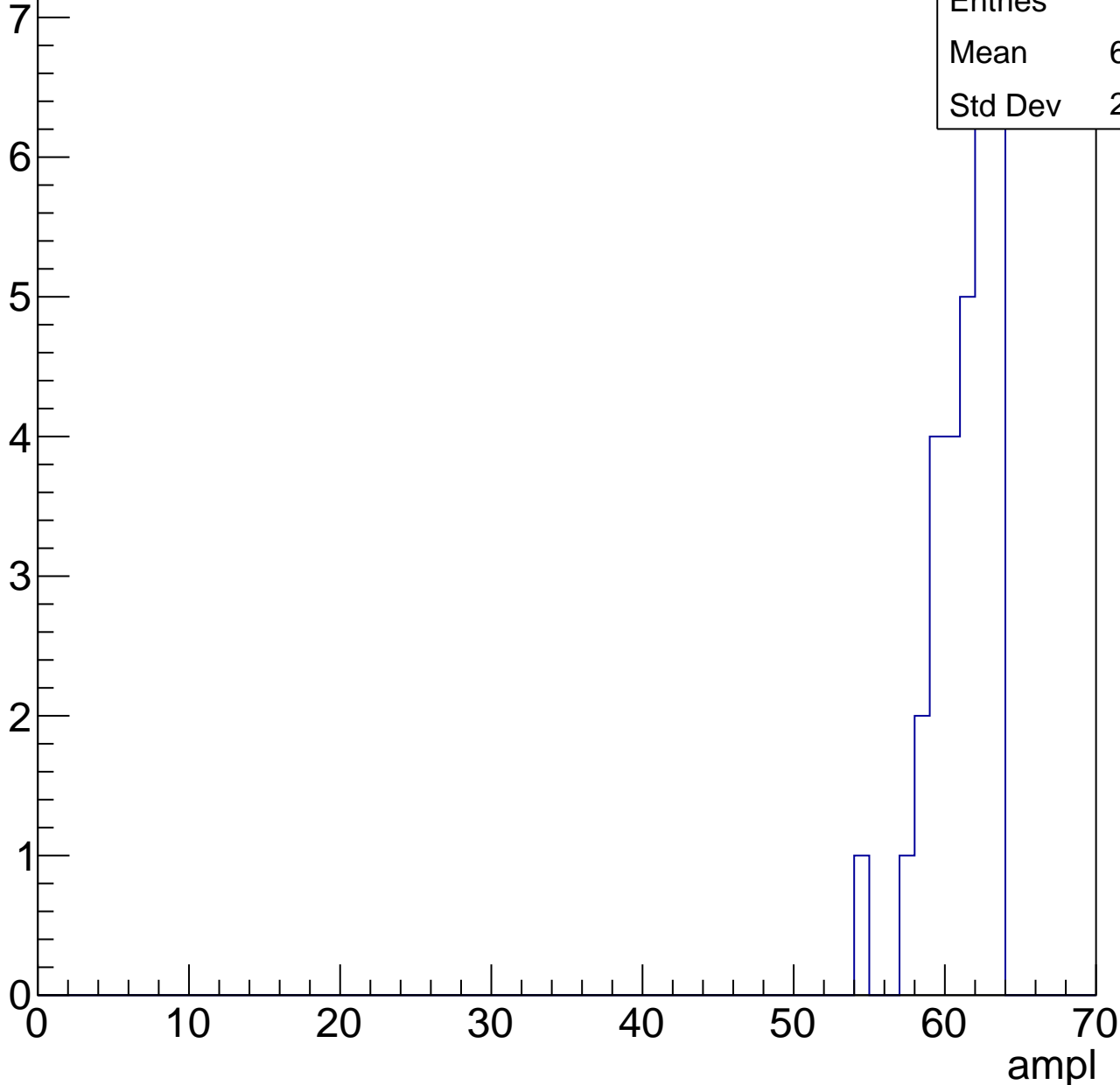
Entries	53
Mean	56.75
Std Dev	8.389

# B0L002S, U23-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	31
Mean	60.74
Std Dev	2.094



# B0L002S, U23-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch38, adc0

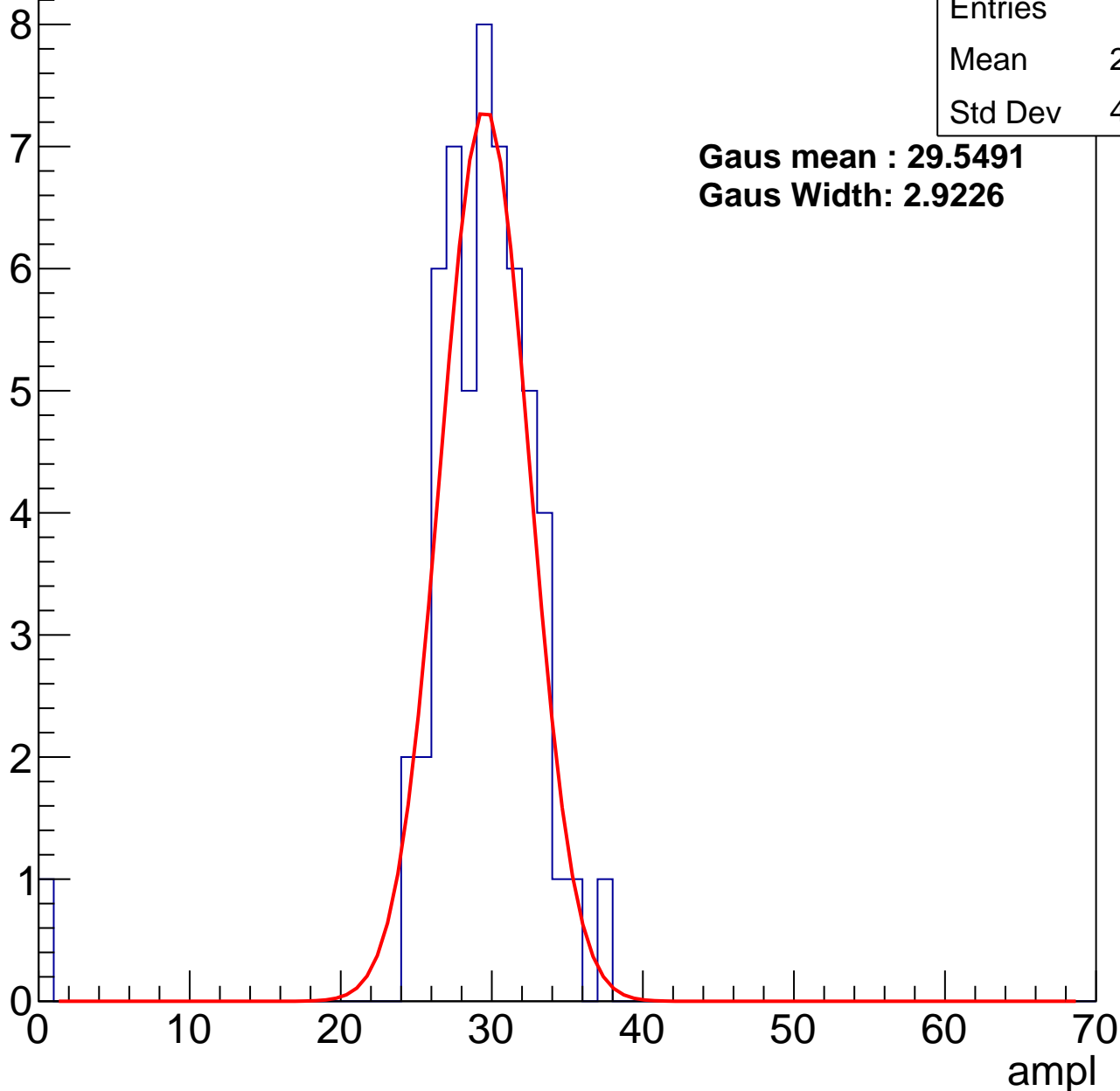
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	28.73
Std Dev	4.764

**Gaus mean : 29.5491**

**Gaus Width: 2.9226**



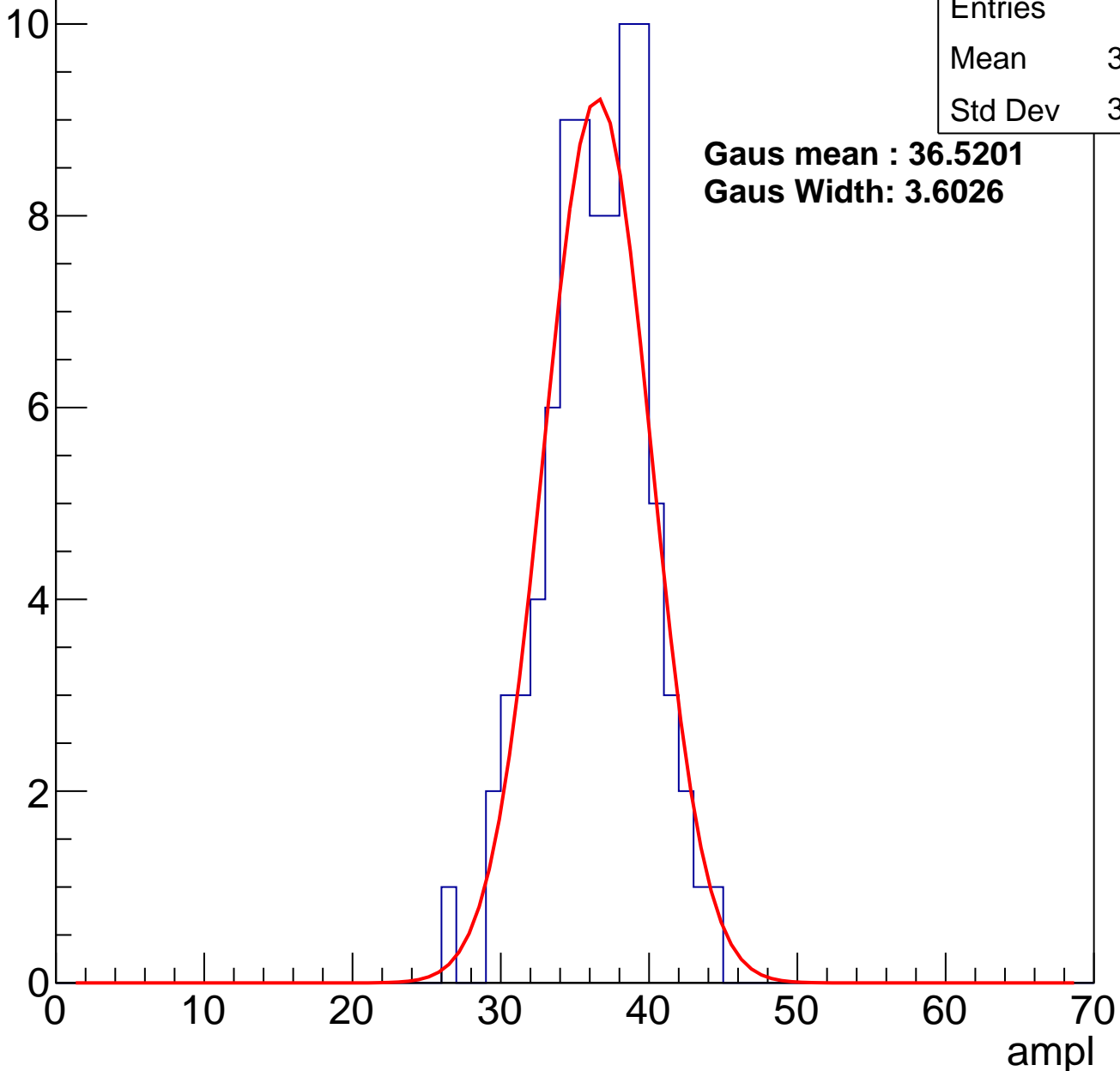
# B0L002S, U23-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	85
Mean	36.02
Std Dev	3.467

**Gaus mean : 36.5201**  
**Gaus Width: 3.6026**

Entry



# B0L002S, U23-ch38, adc2

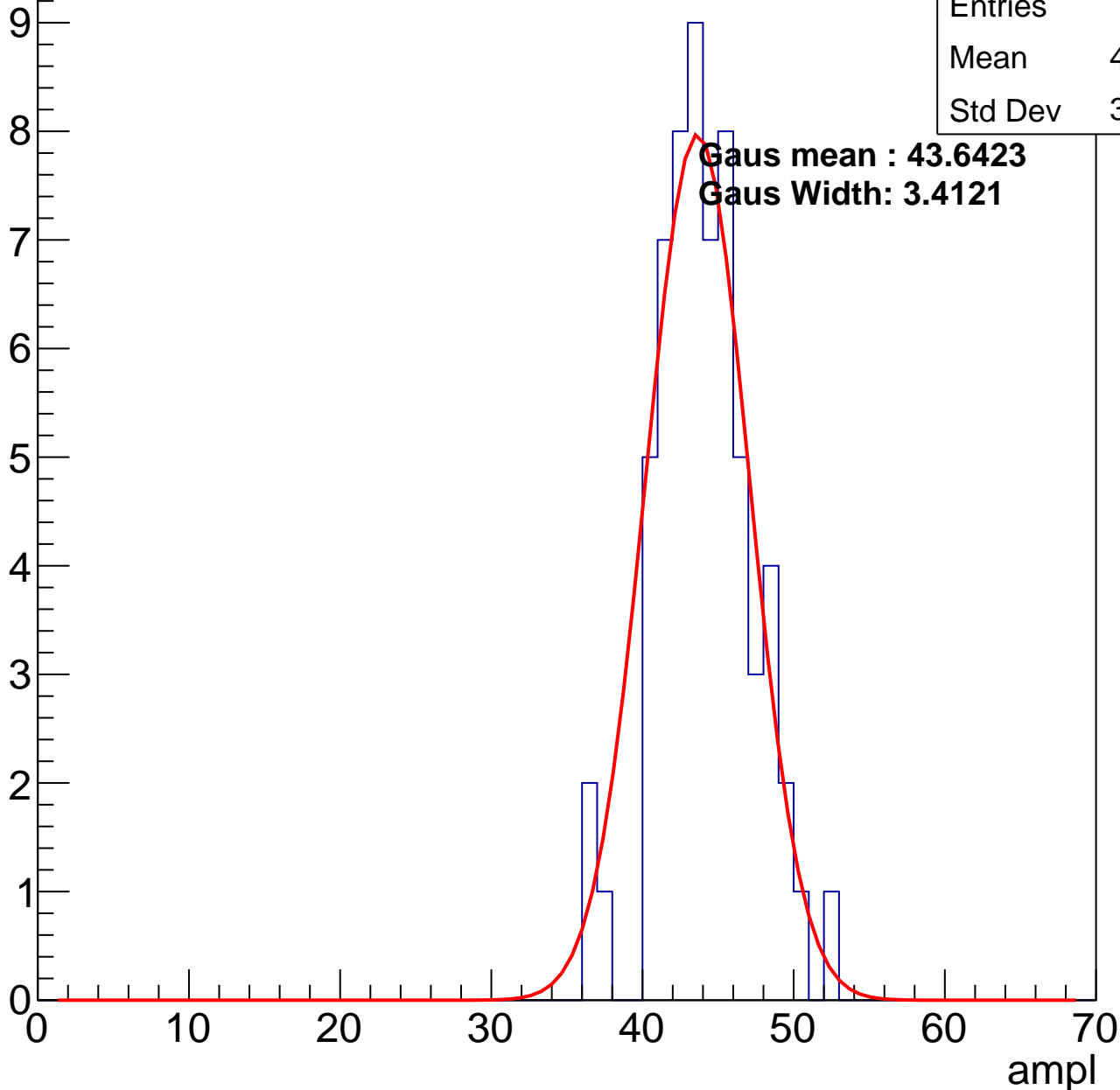
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.65
Std Dev	3.143

**Gaus mean : 43.6423**

**Gaus Width: 3.4121**

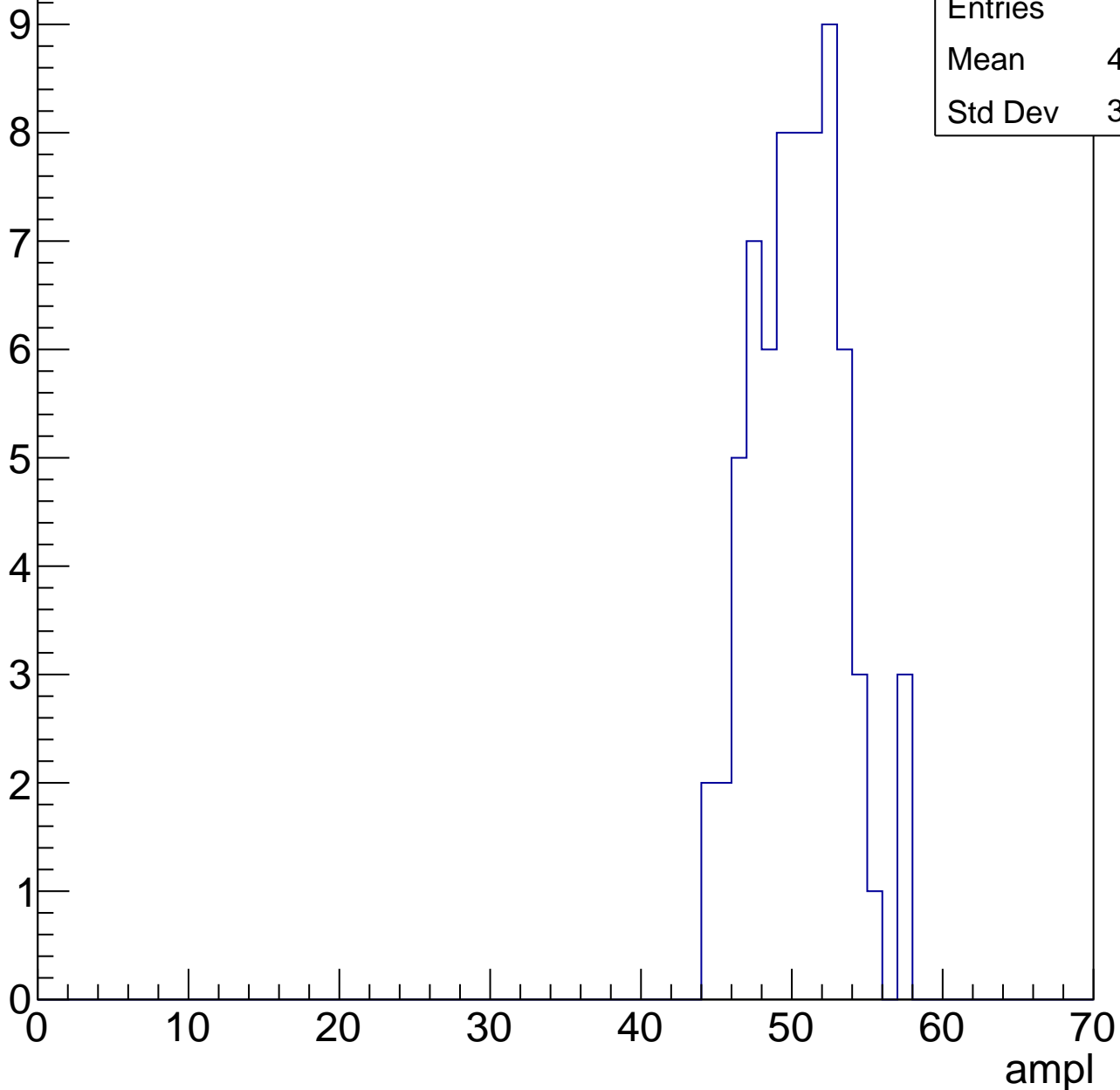


# B0L002S, U23-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	49.99
Std Dev	3.007

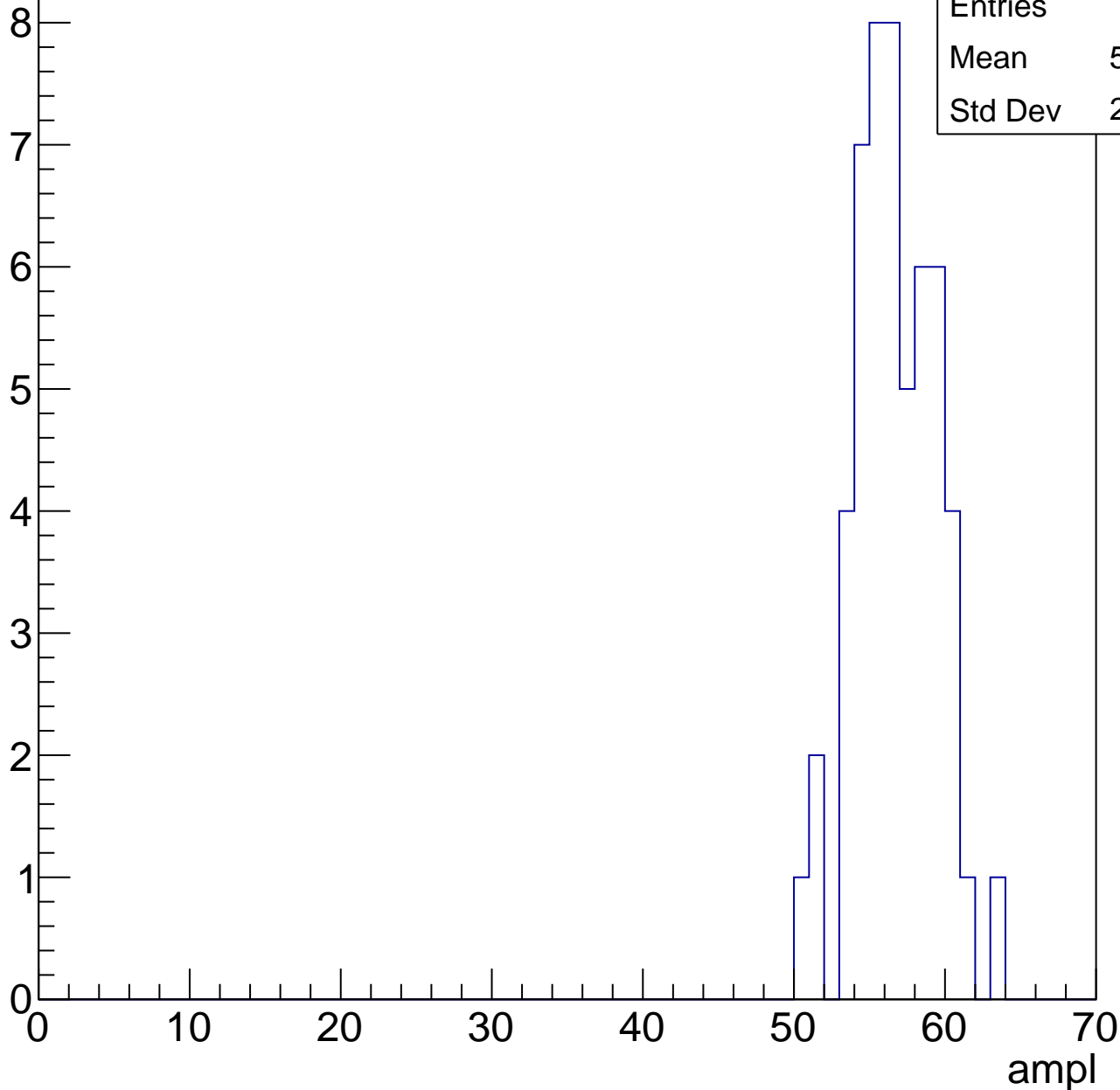


# B0L002S, U23-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.25
Std Dev	2.663

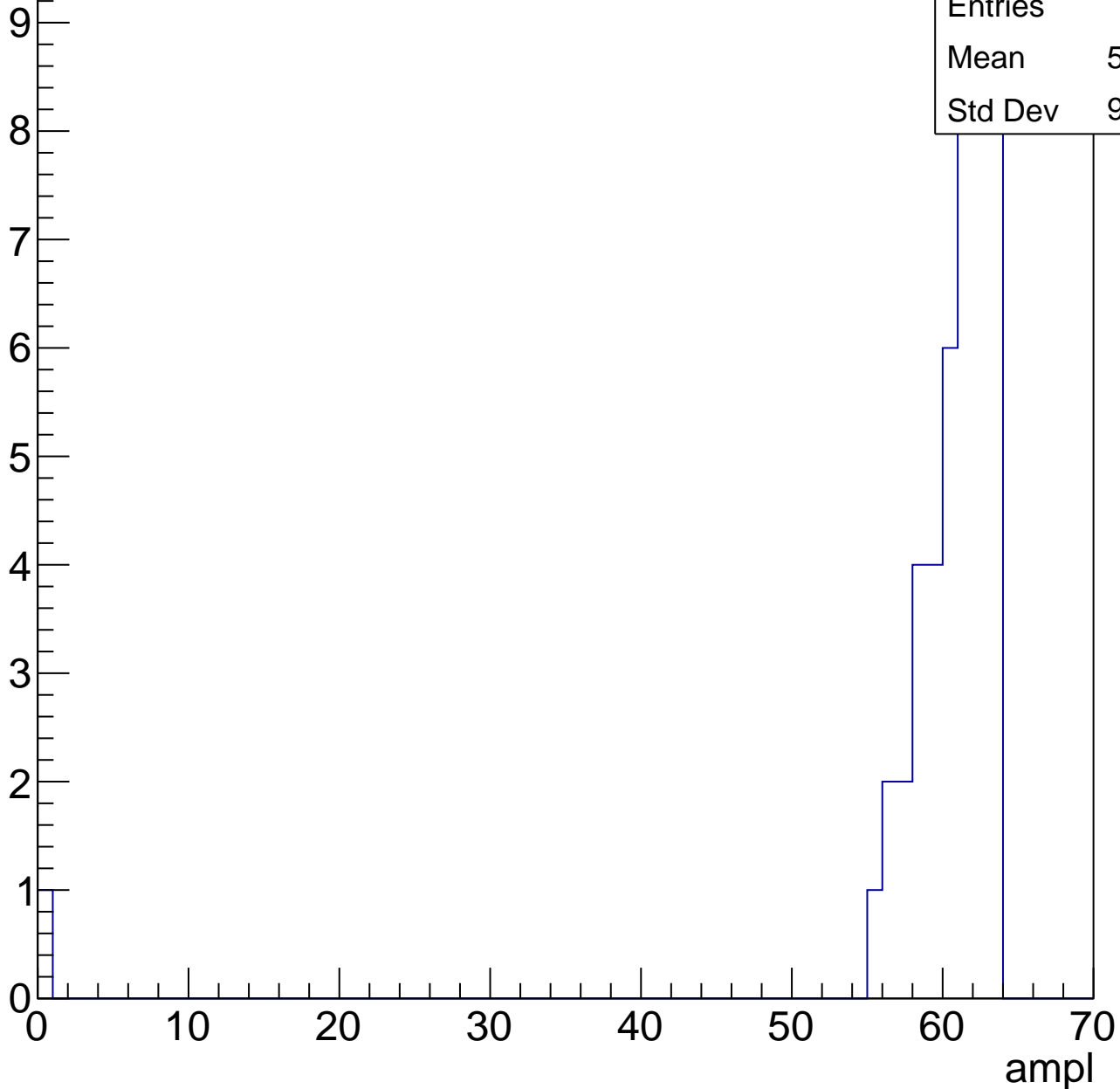


# B0L002S, U23-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.09
Std Dev	9.157



# B0L002S, U23-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch39, adc0

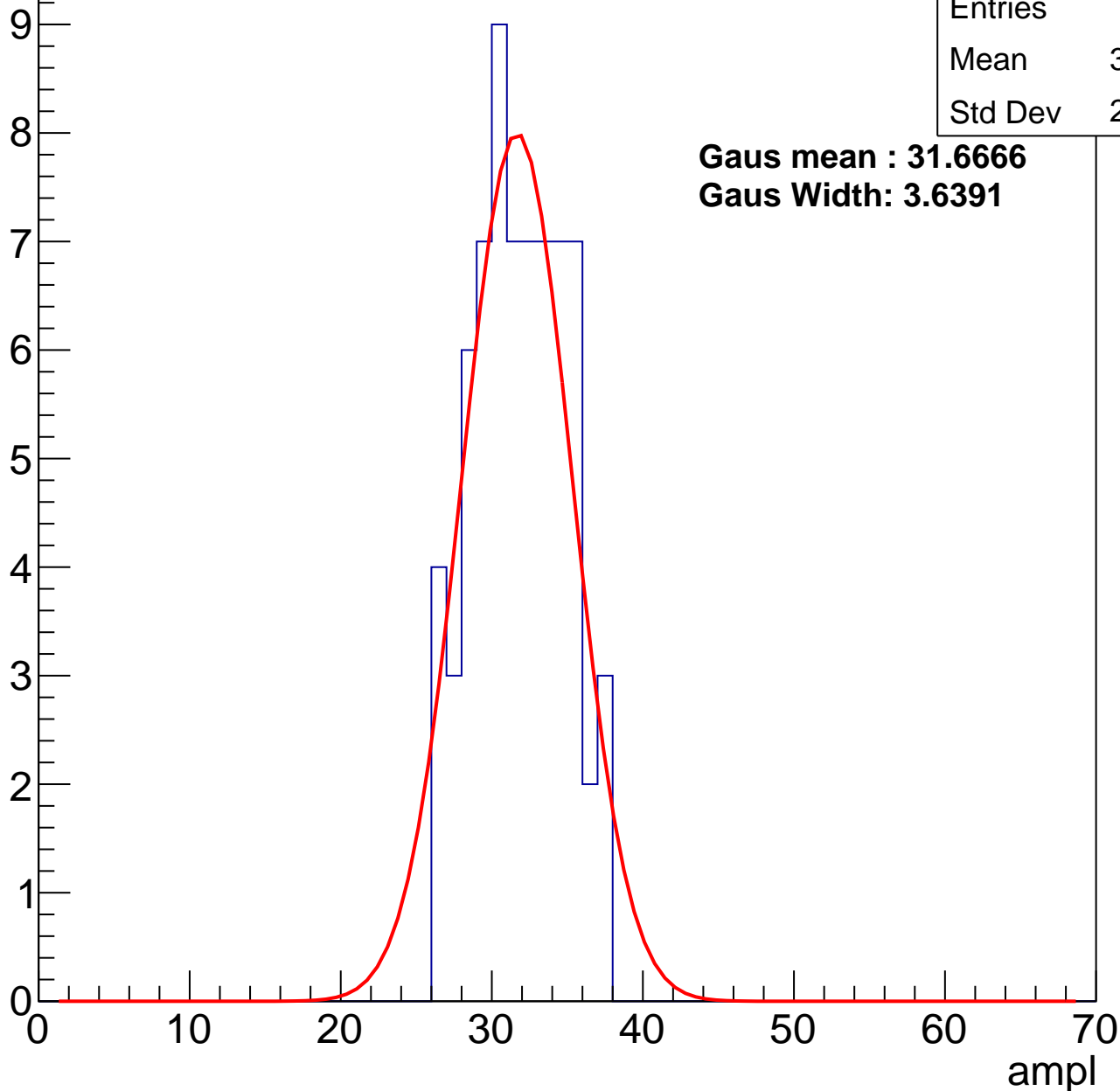
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	31.36
Std Dev	2.944

**Gaus mean : 31.6666**

**Gaus Width: 3.6391**



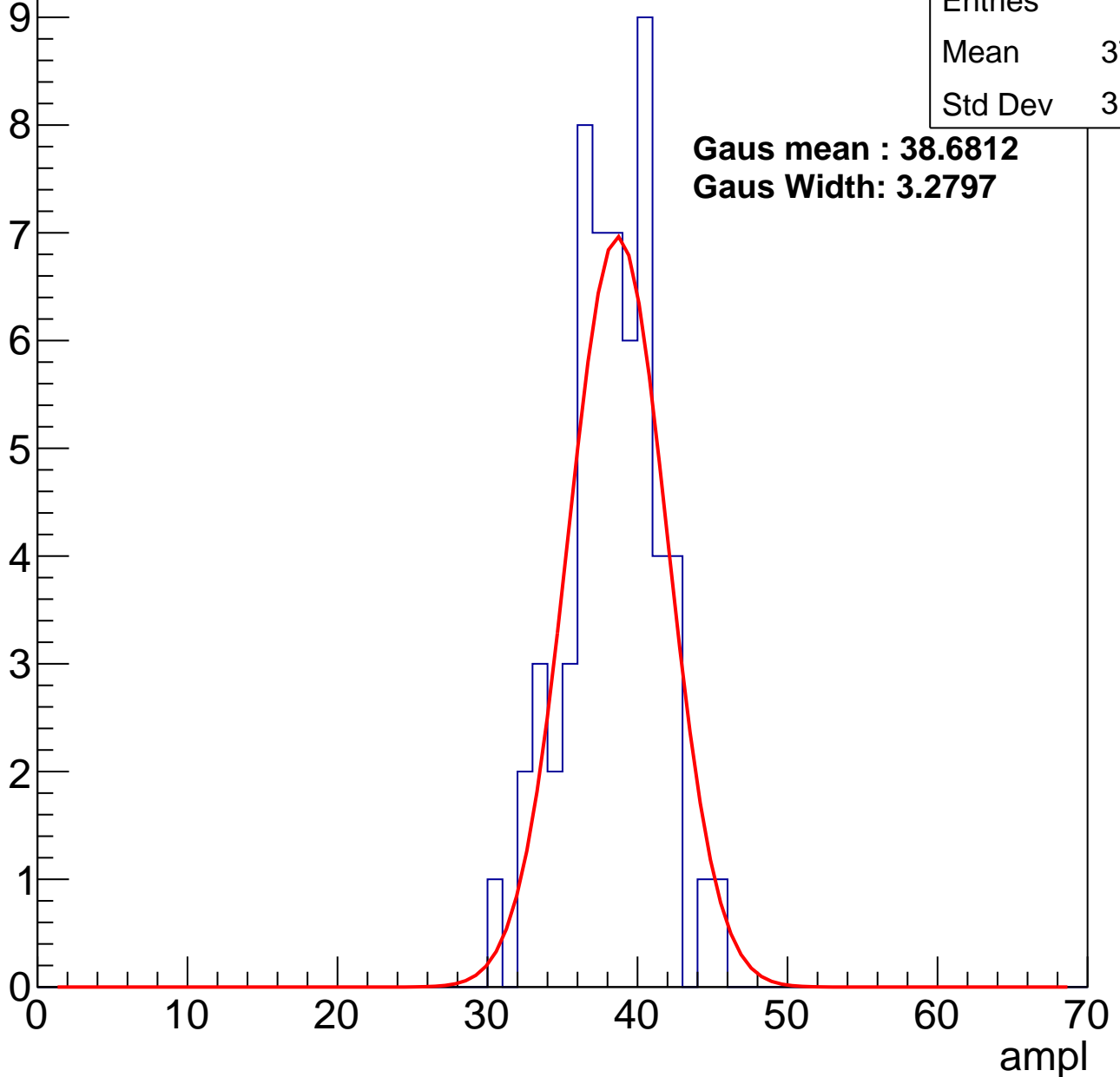
# B0L002S, U23-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.83
Std Dev	3.035

**Gaus mean : 38.6812**  
**Gaus Width: 3.2797**



# B0L002S, U23-ch39, adc2

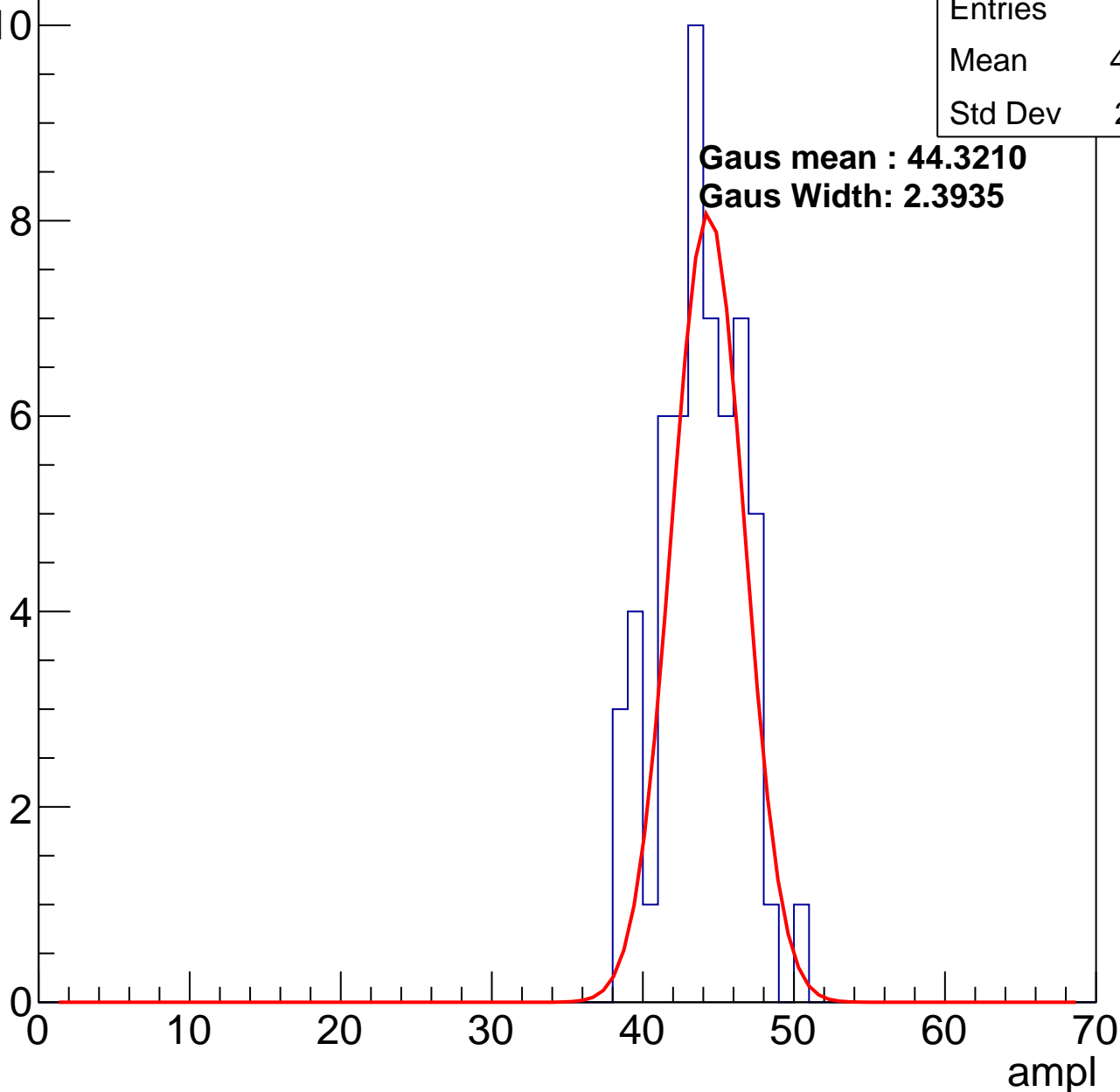
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.35
Std Dev	2.711

**Gaus mean : 44.3210**

**Gaus Width: 2.3935**

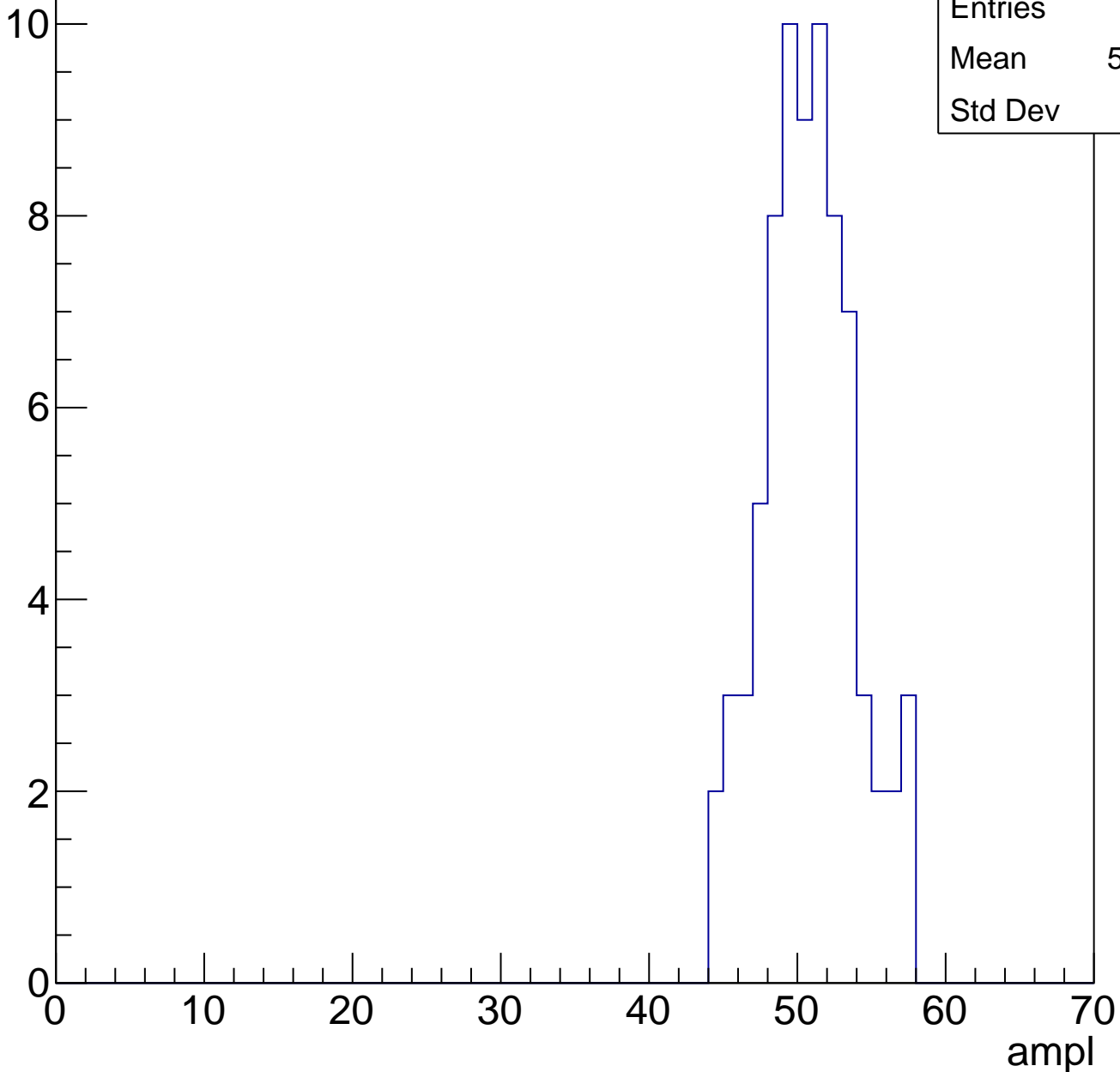


# B0L002S, U23-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	50.29
Std Dev	3.05

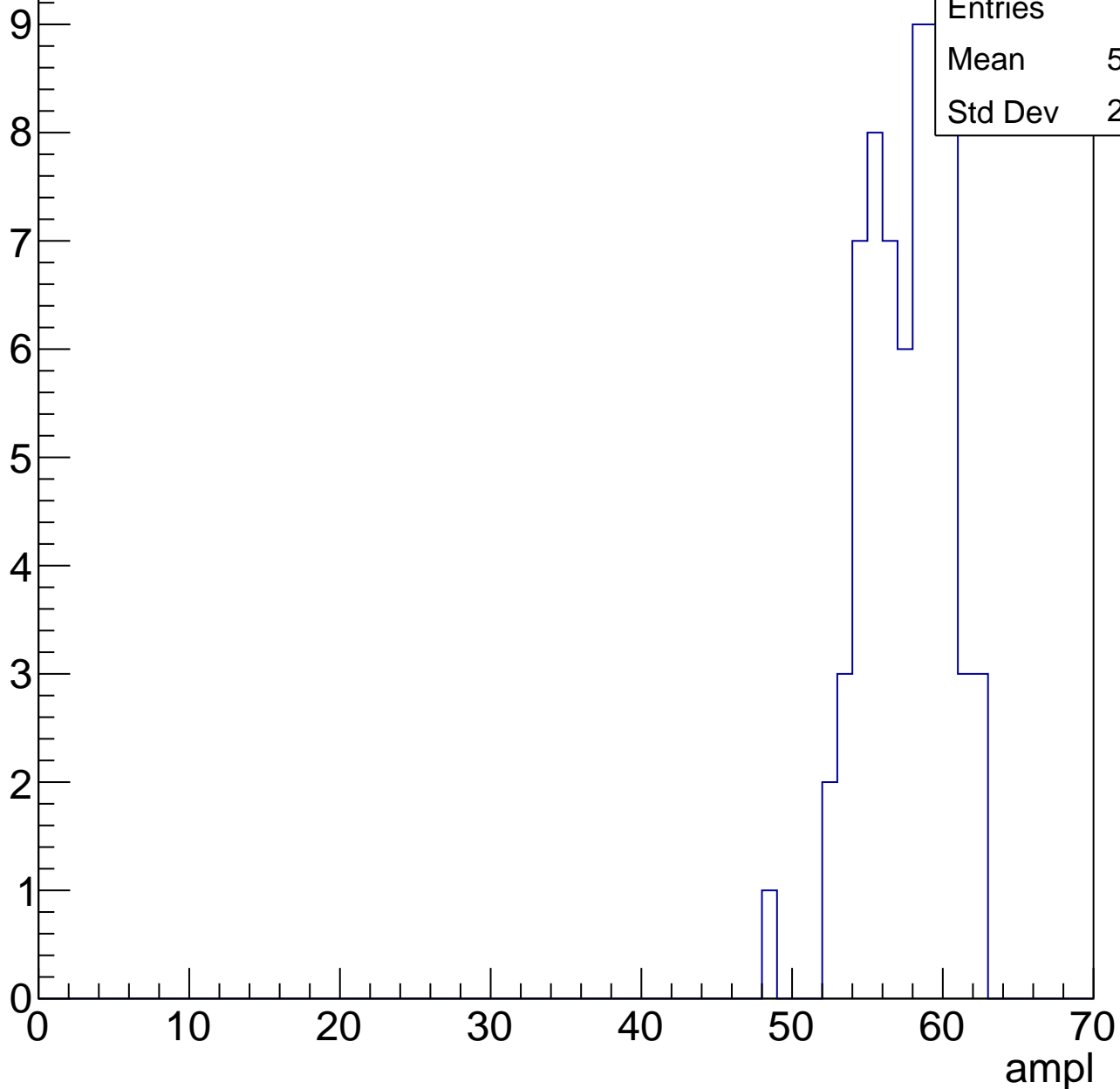
Entry



# B0L002S, U23-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



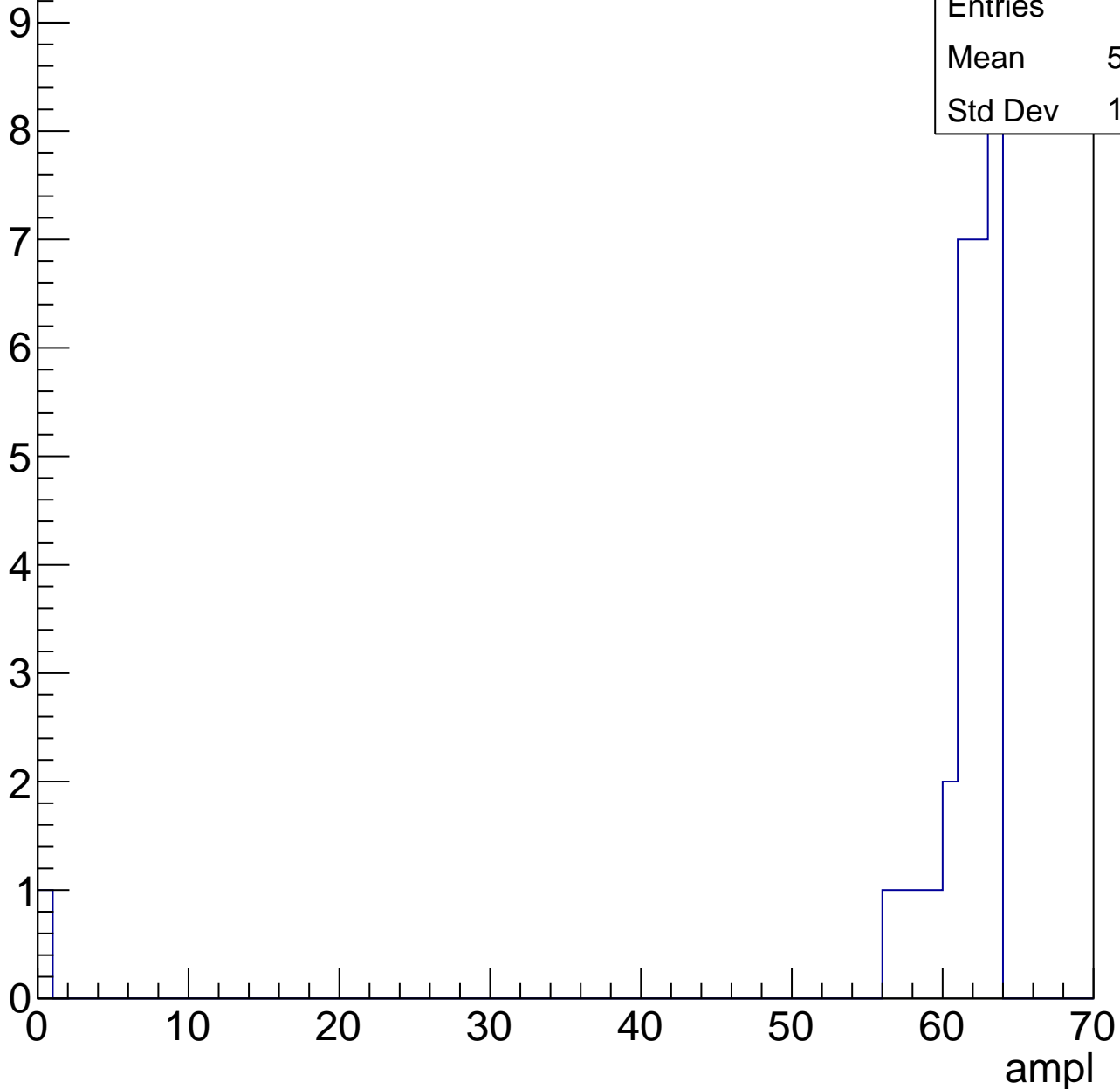
Entries	66
Mean	57.05
Std Dev	2.809

# B0L002S, U23-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.27
Std Dev	11.15



# B0L002S, U23-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch39, adc7

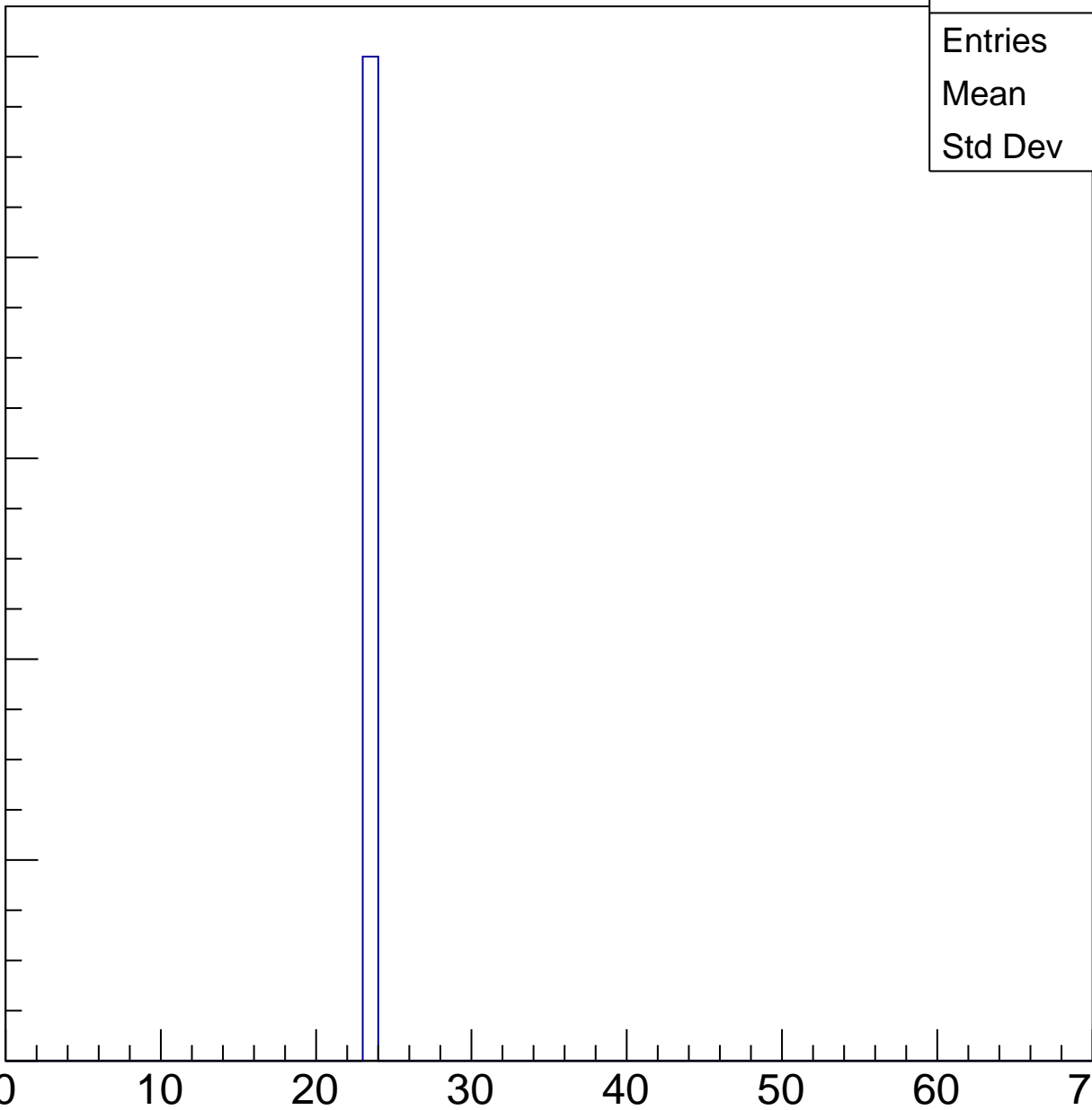
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl



# B0L002S, U23-ch40, adc0

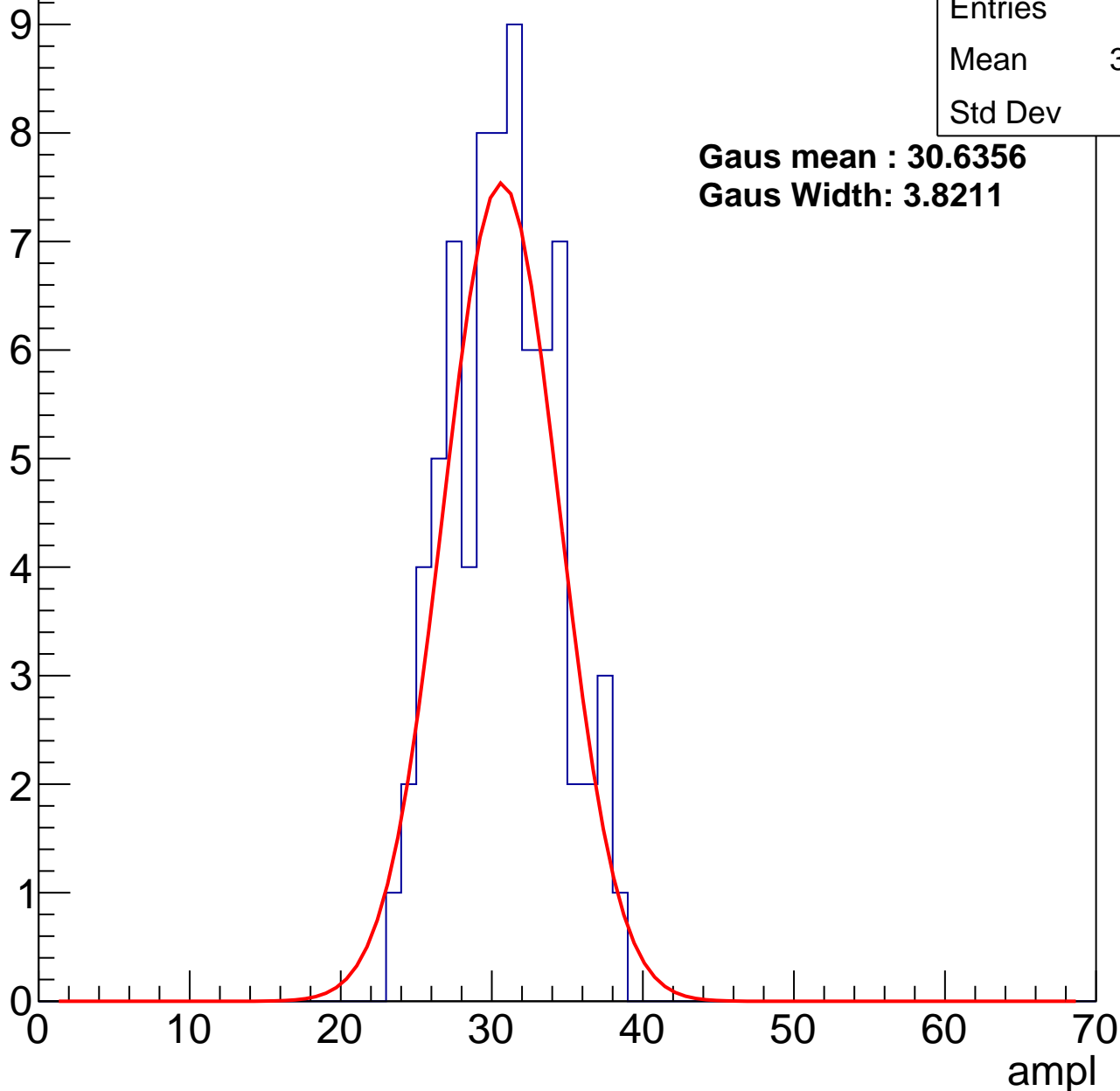
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.29
Std Dev	3.49

**Gaus mean : 30.6356**

**Gaus Width: 3.8211**



# B0L002S, U23-ch40, adc1

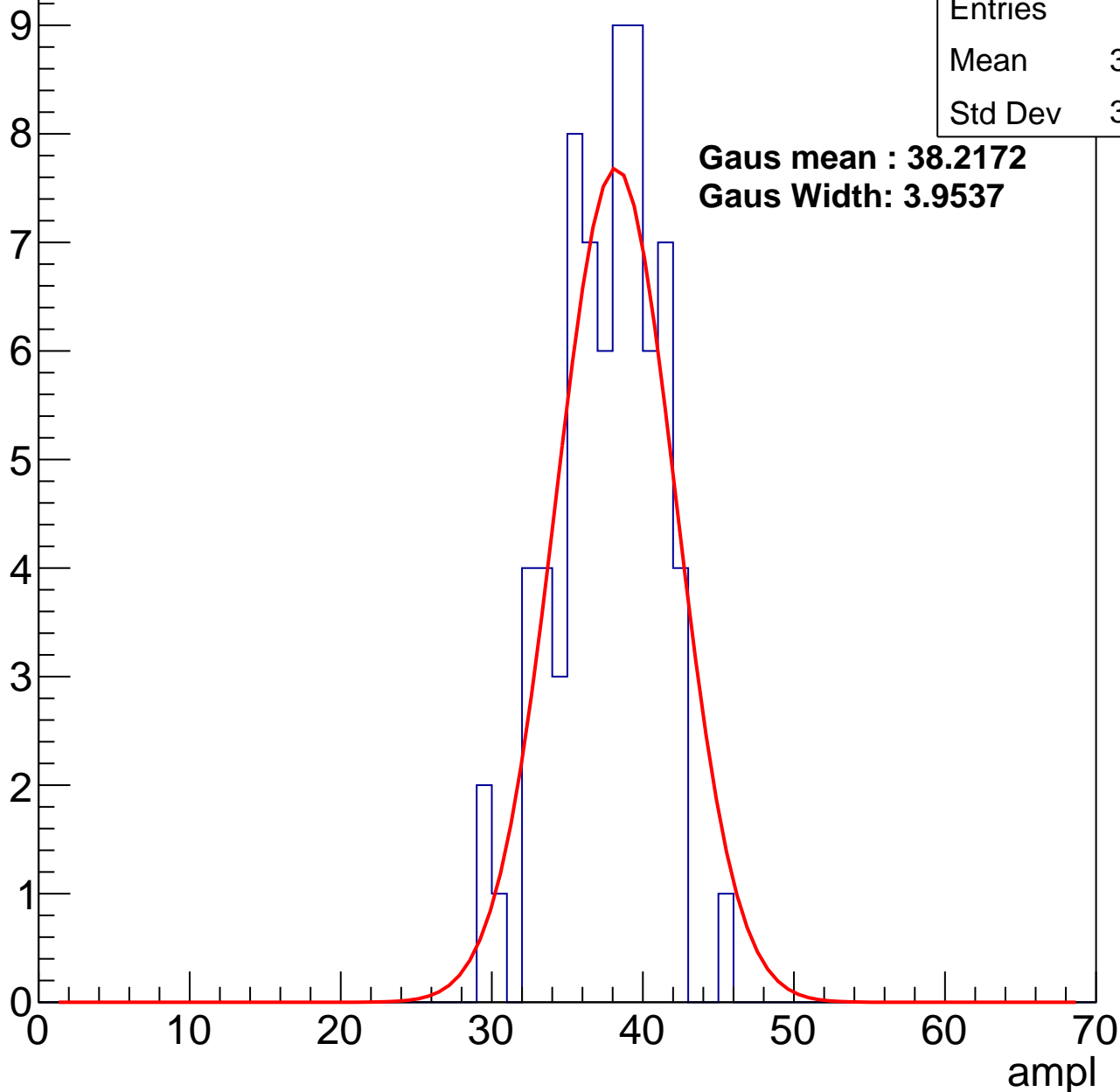
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.14
Std Dev	3.316

**Gaus mean : 38.2172**

**Gaus Width: 3.9537**



# B0L002S, U23-ch40, adc2

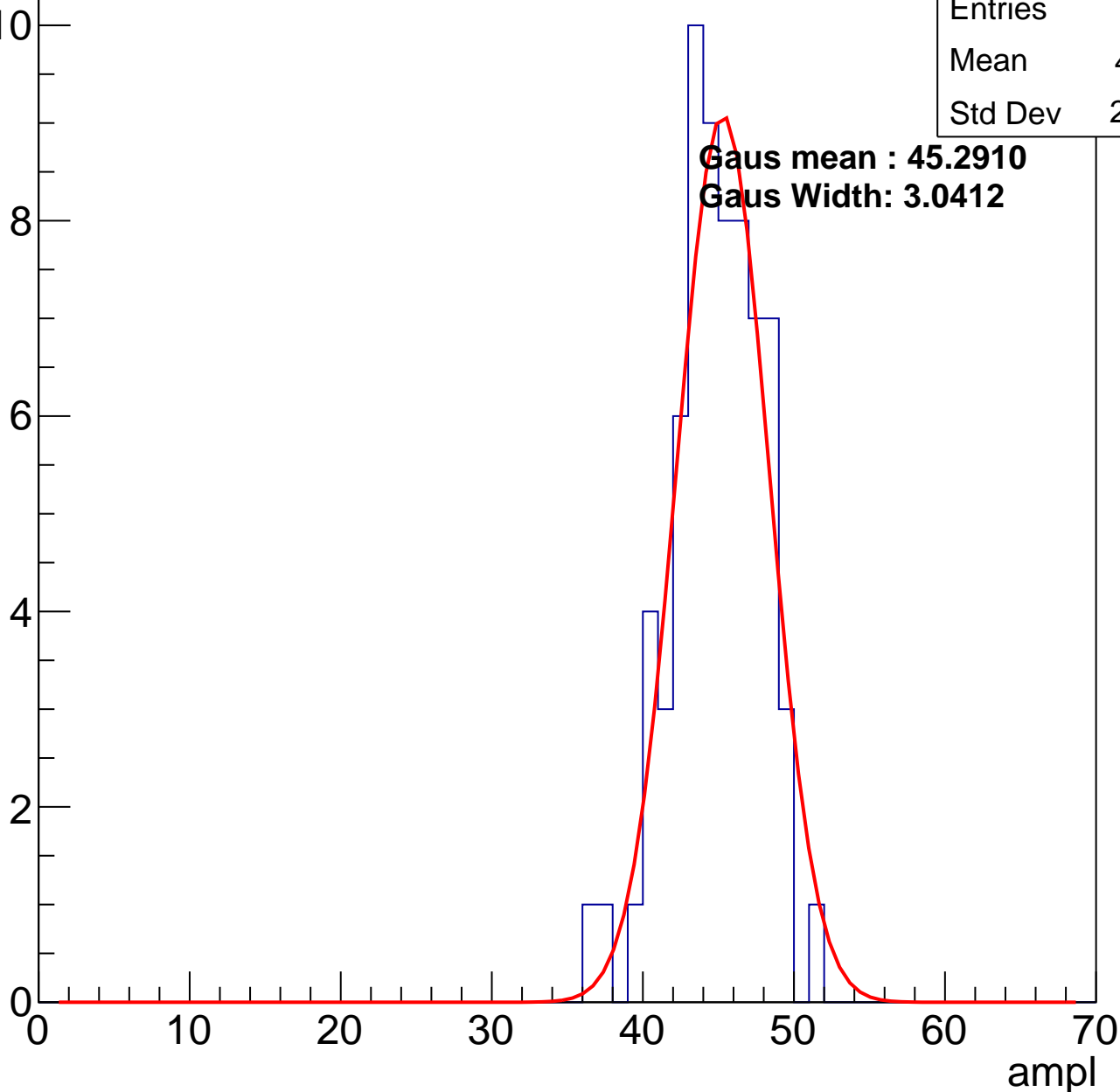
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.41
Std Dev	2.926

**Gaus mean : 45.2910**

**Gaus Width: 3.0412**

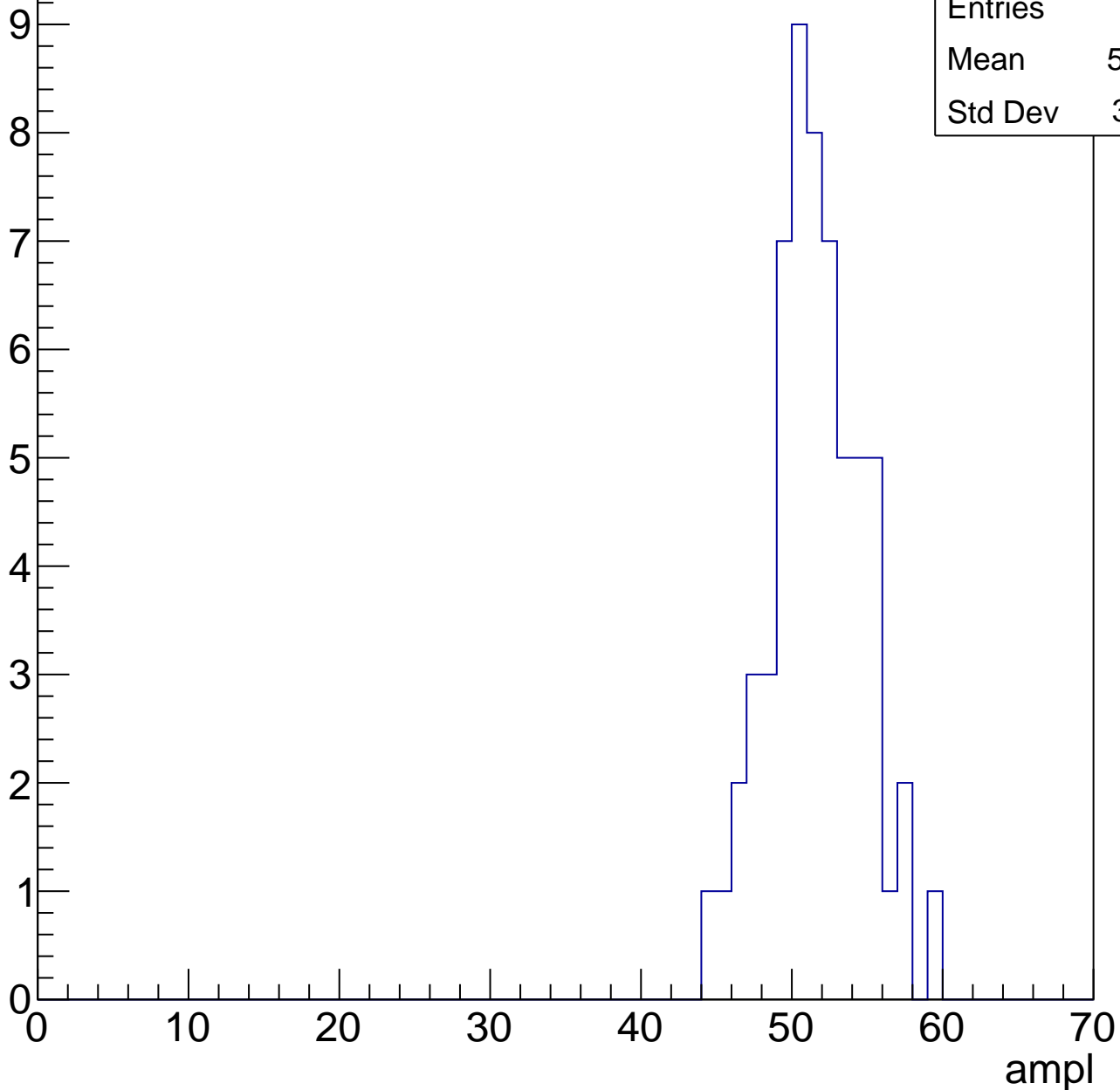


# B0L002S, U23-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	51.17
Std Dev	3.051

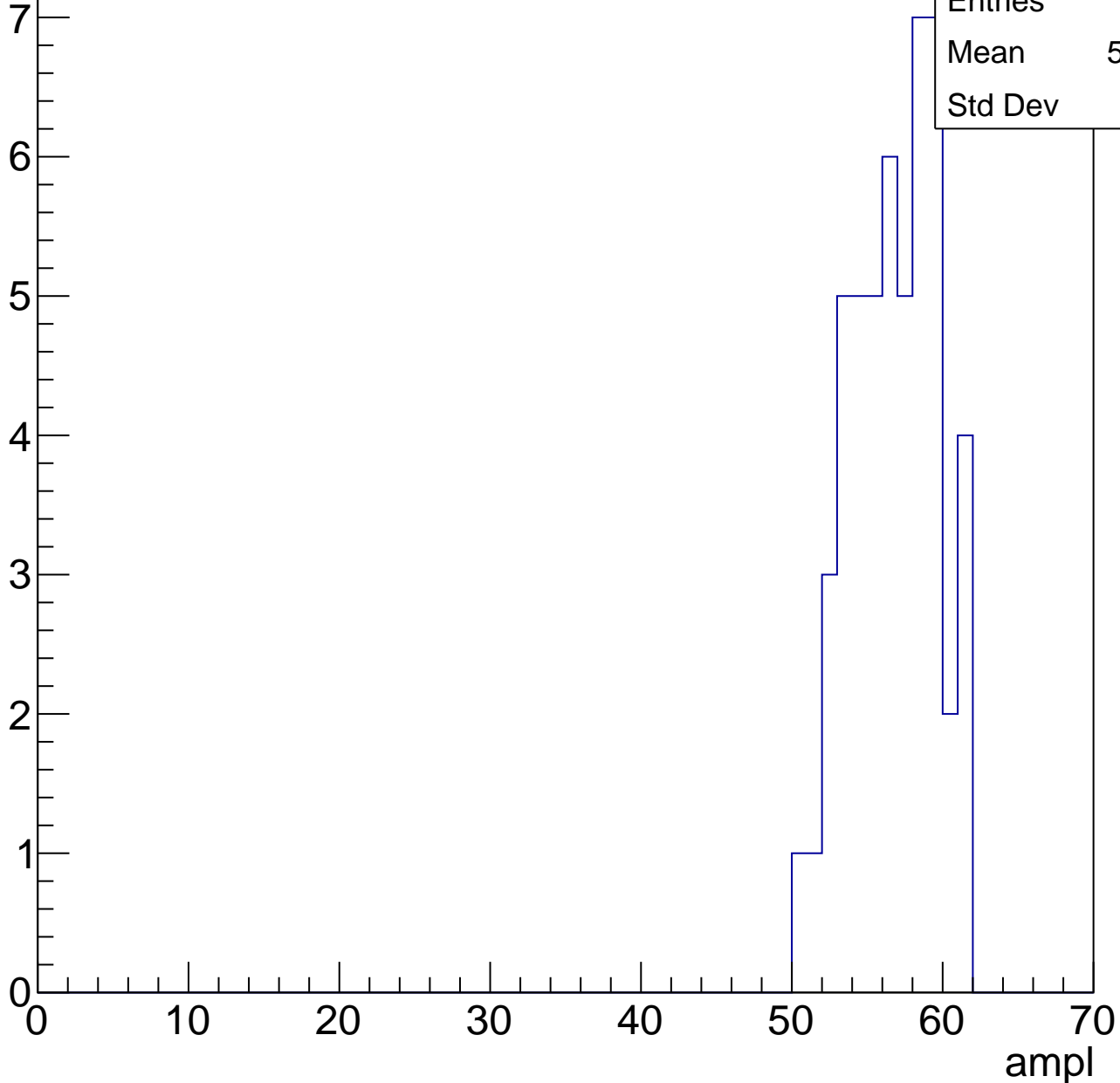


# B0L002S, U23-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.29
Std Dev	2.81

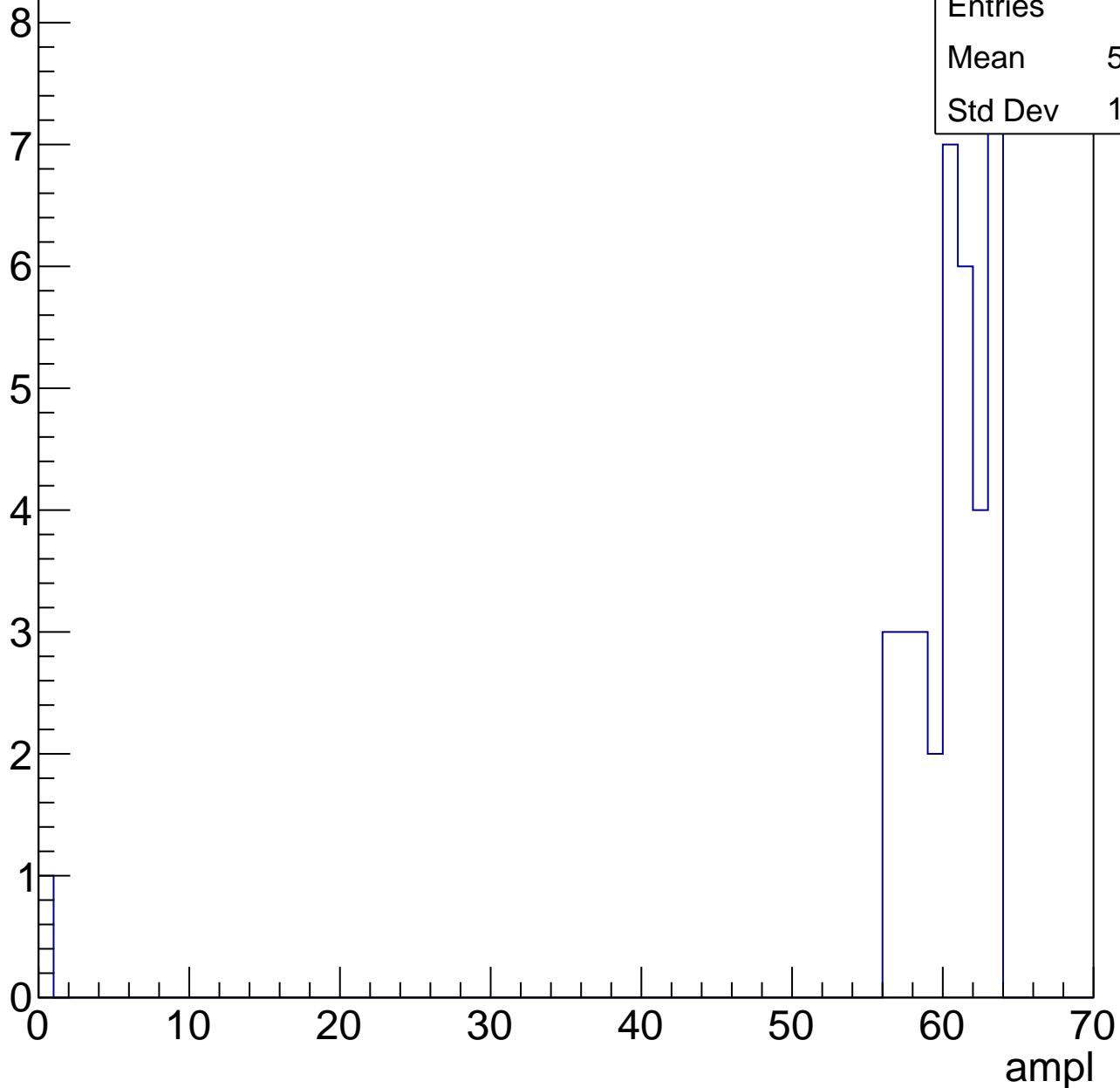


# B0L002S, U23-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.62
Std Dev	10.02

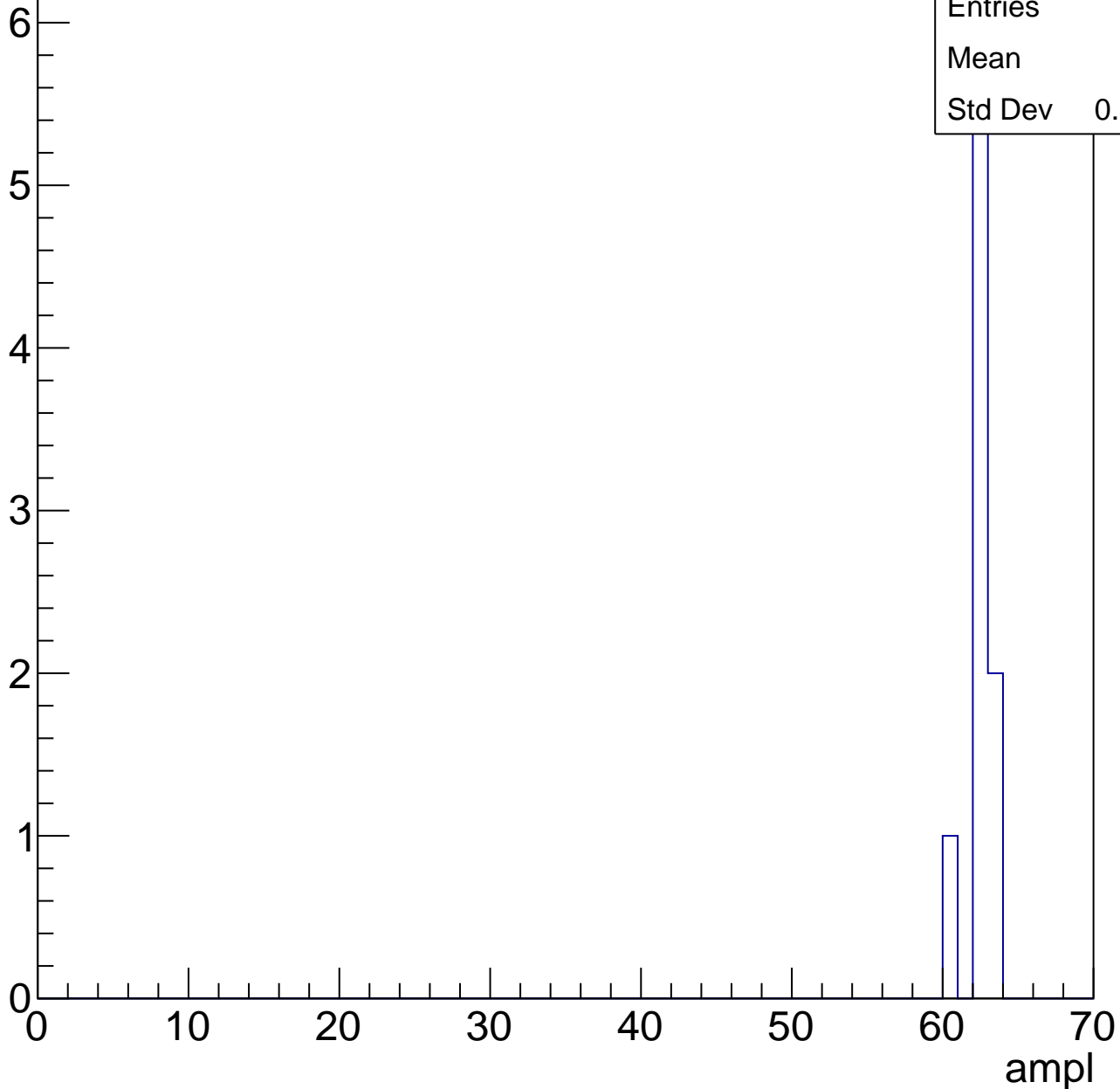


# B0L002S, U23-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	9
Mean	62
Std Dev	0.8165





# B0L002S, U23-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch41, adc0

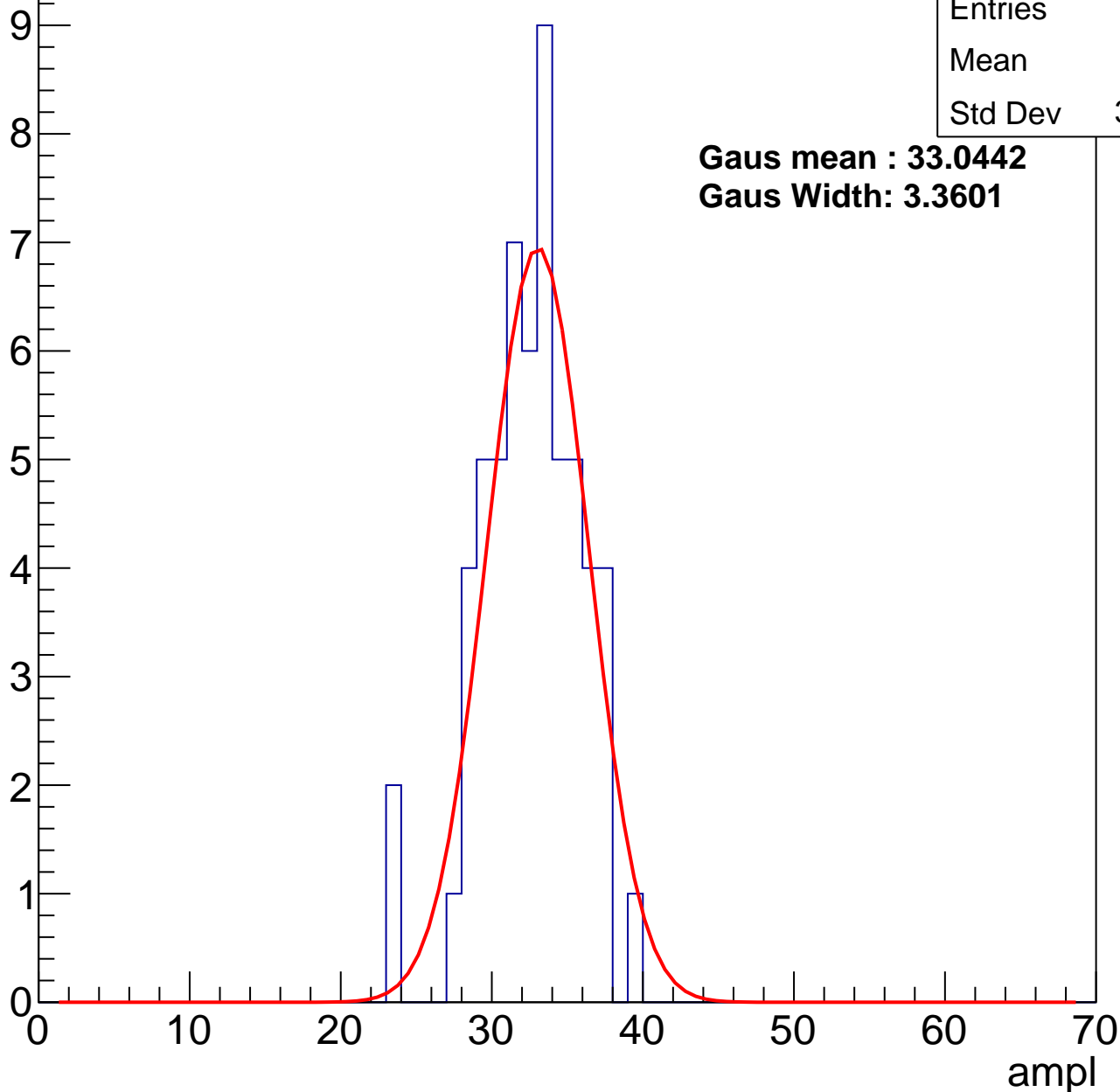
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	32.1
Std Dev	3.241

**Gaus mean : 33.0442**

**Gaus Width: 3.3601**



# B0L002S, U23-ch41, adc1

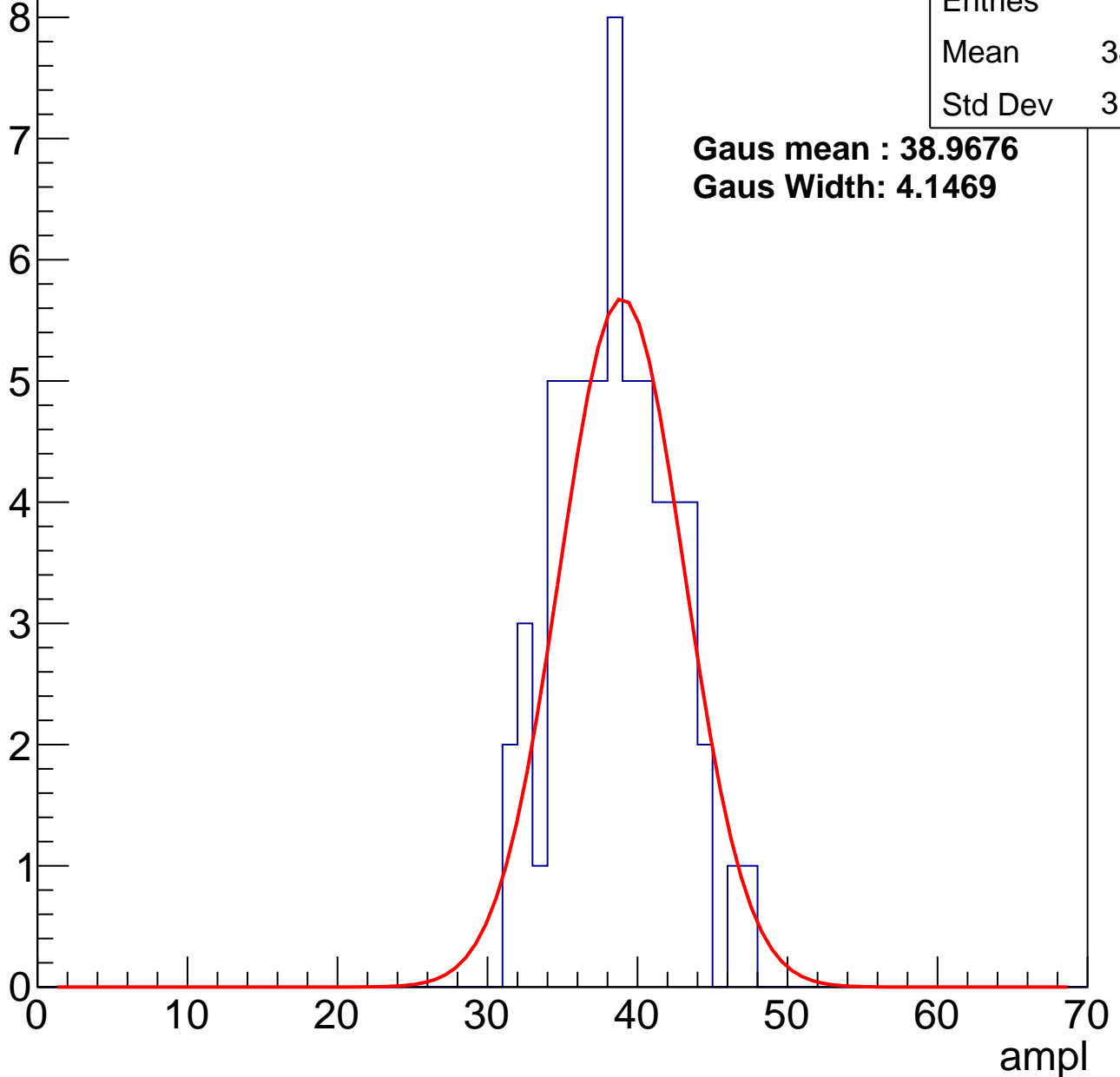
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	38.08
Std Dev	3.703

**Gaus mean : 38.9676**

**Gaus Width: 4.1469**



# B0L002S, U23-ch41, adc2

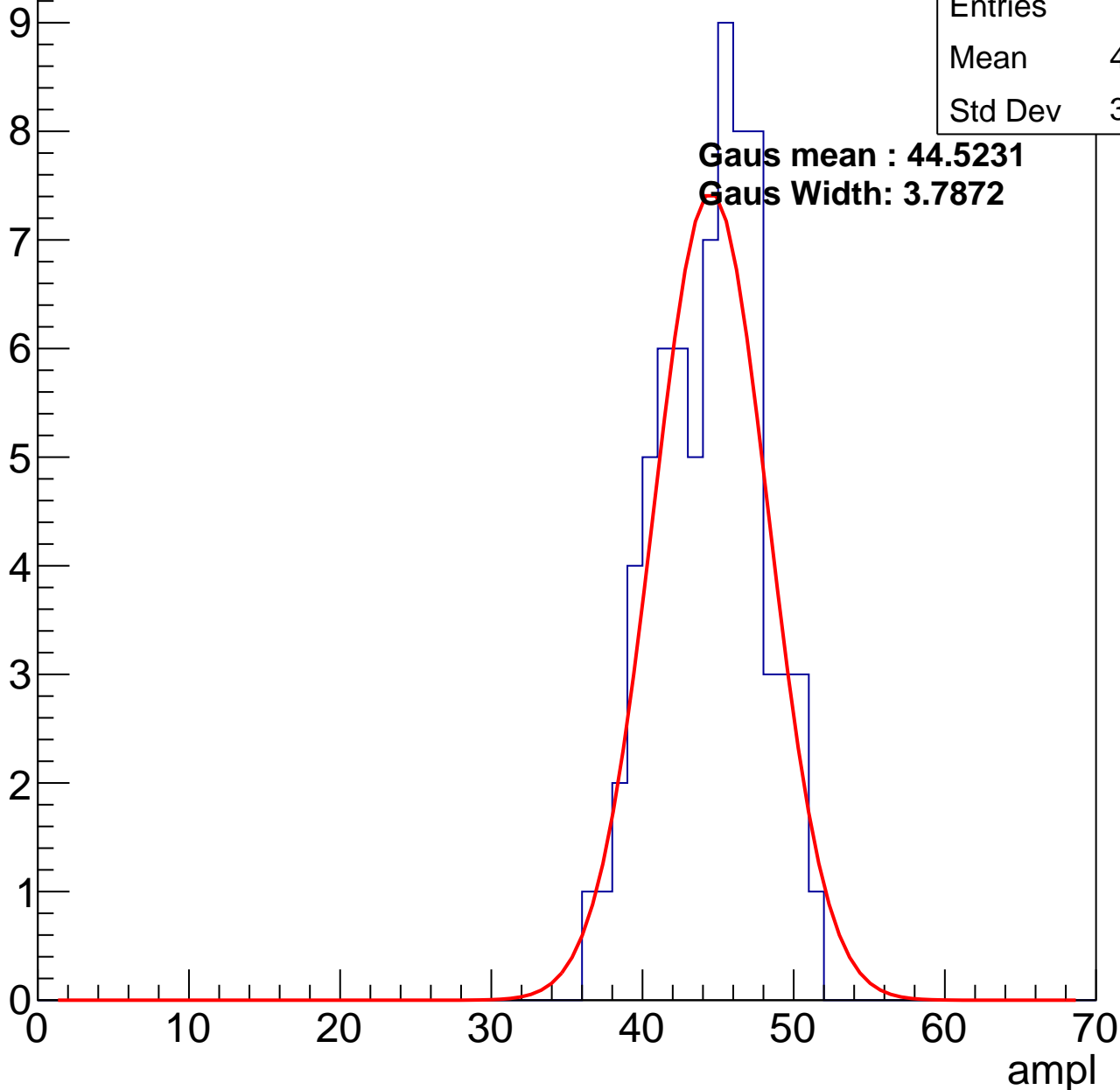
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.99
Std Dev	3.418

**Gaus mean : 44.5231**

**Gaus Width: 3.7872**

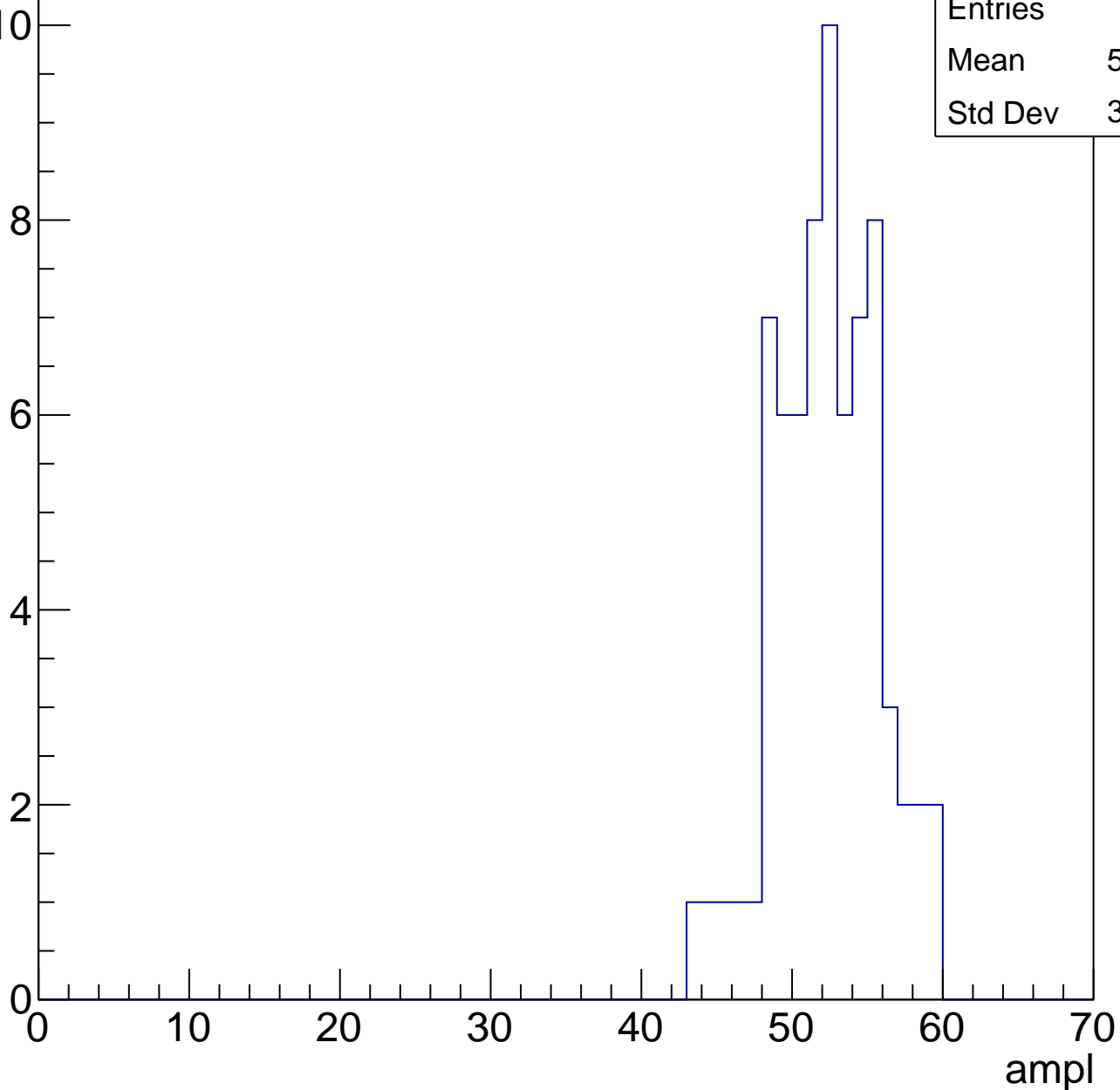


# B0L002S, U23-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	51.88
Std Dev	3.387

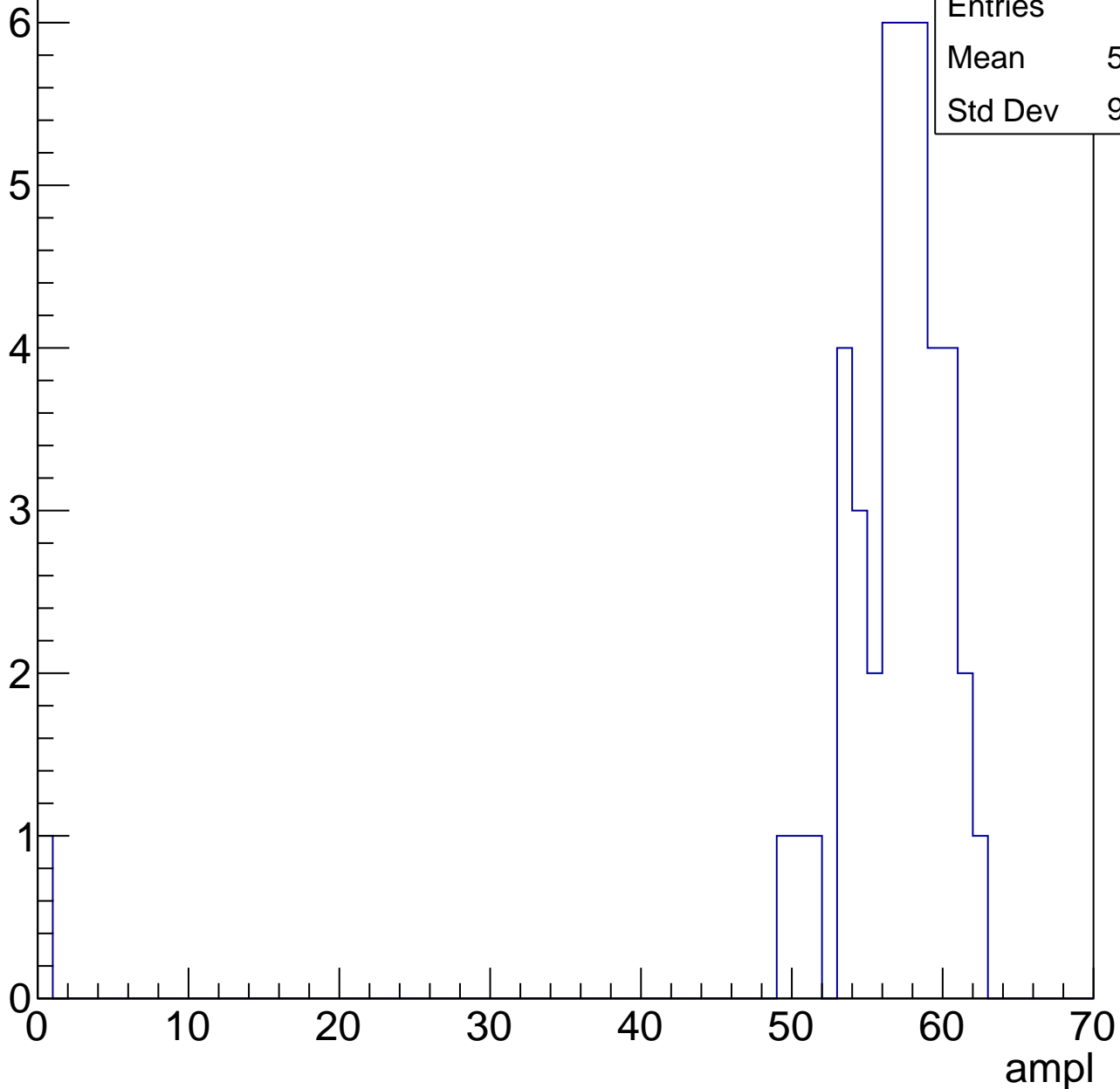


# B0L002S, U23-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	42
Mean	55.24
Std Dev	9.113



# B0L002S, U23-ch41, adc5

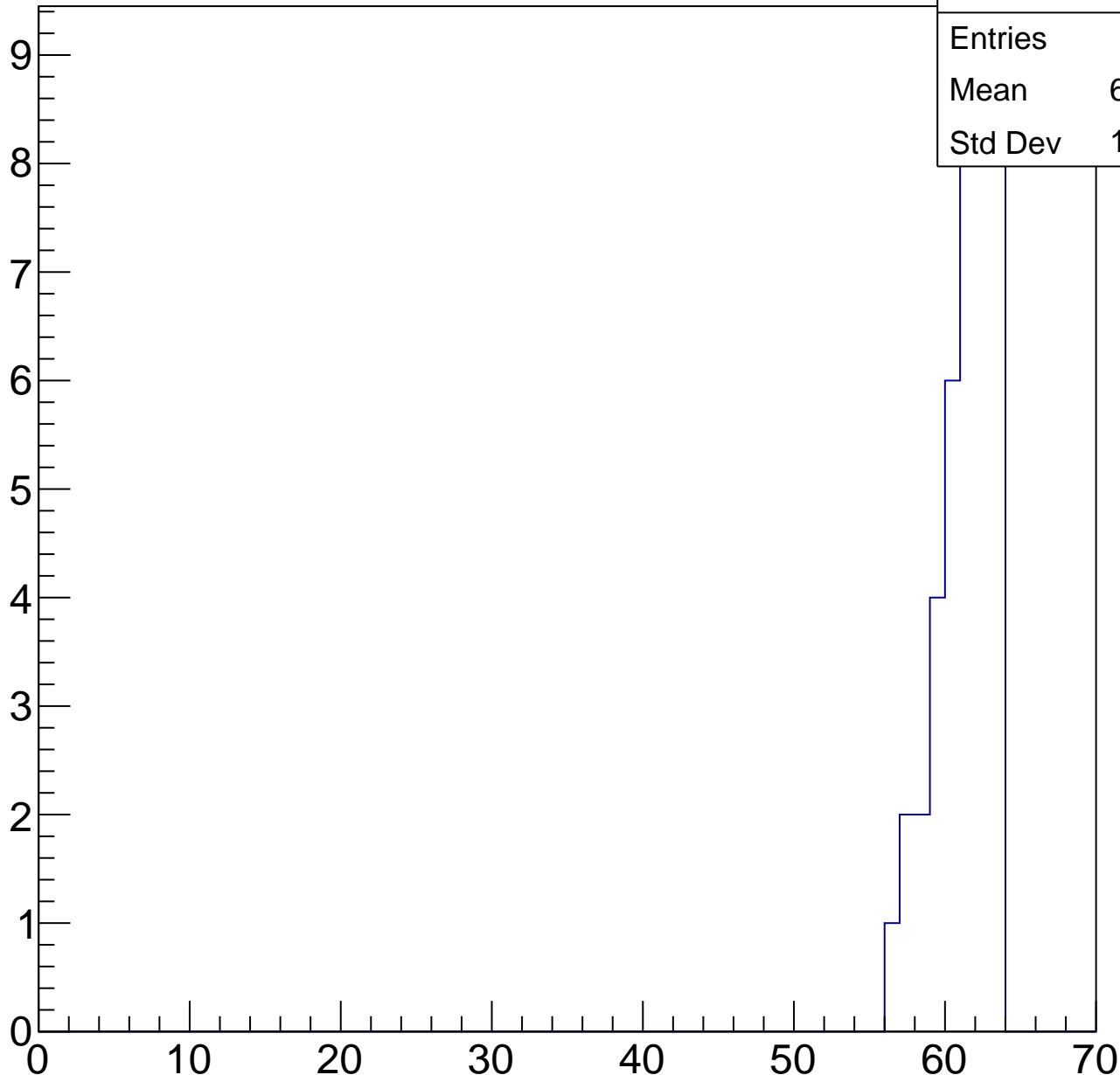
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	60.83
Std Dev	1.869

ampl



# B0L002S, U23-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch42, adc0

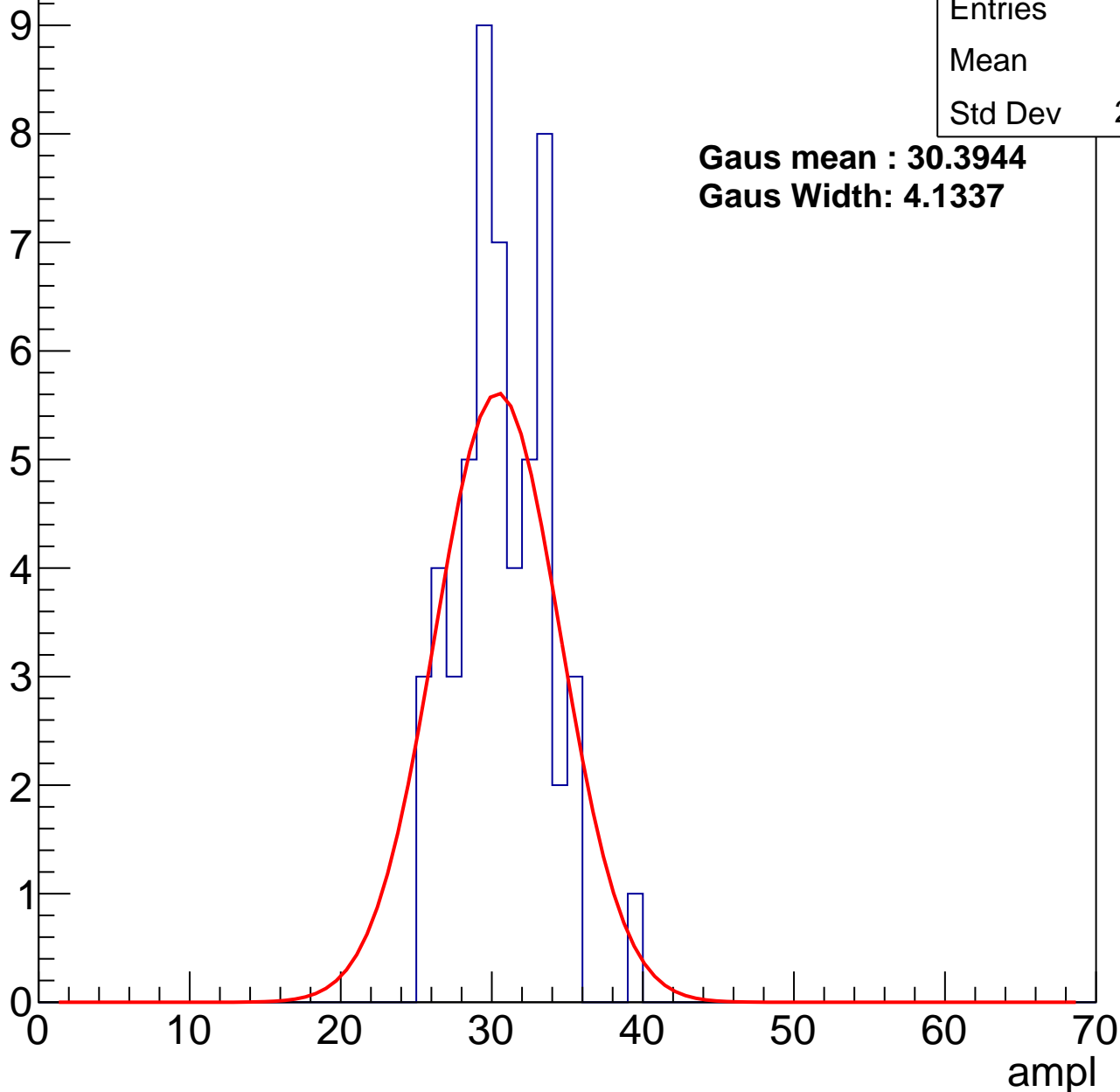
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	30.2
Std Dev	2.971

**Gaus mean : 30.3944**

**Gaus Width: 4.1337**



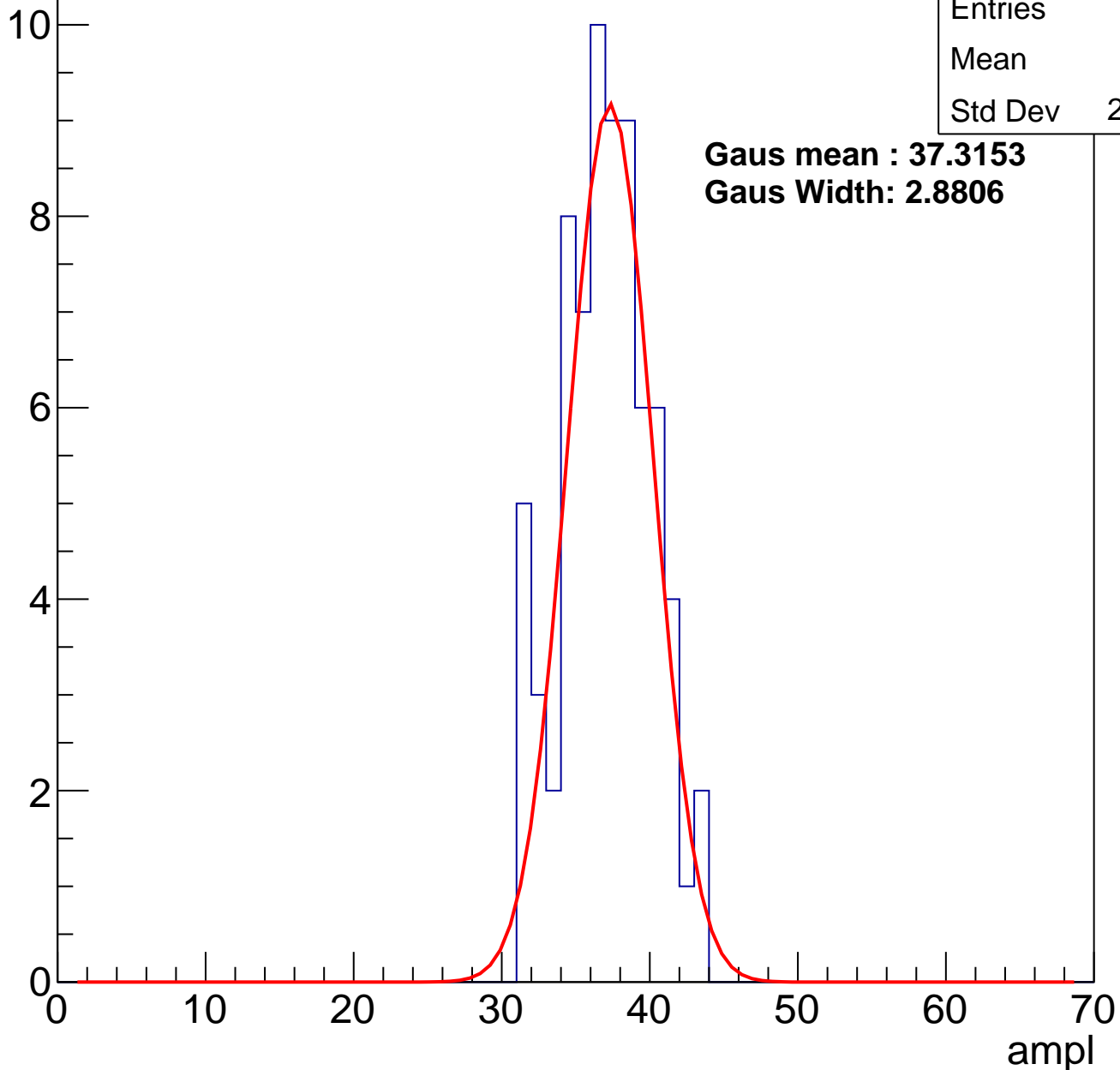
# B0L002S, U23-ch42, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	72
Mean	36.6
Std Dev	2.966

**Gaus mean : 37.3153**  
**Gaus Width: 2.8806**

Entry



# B0L002S, U23-ch42, adc2

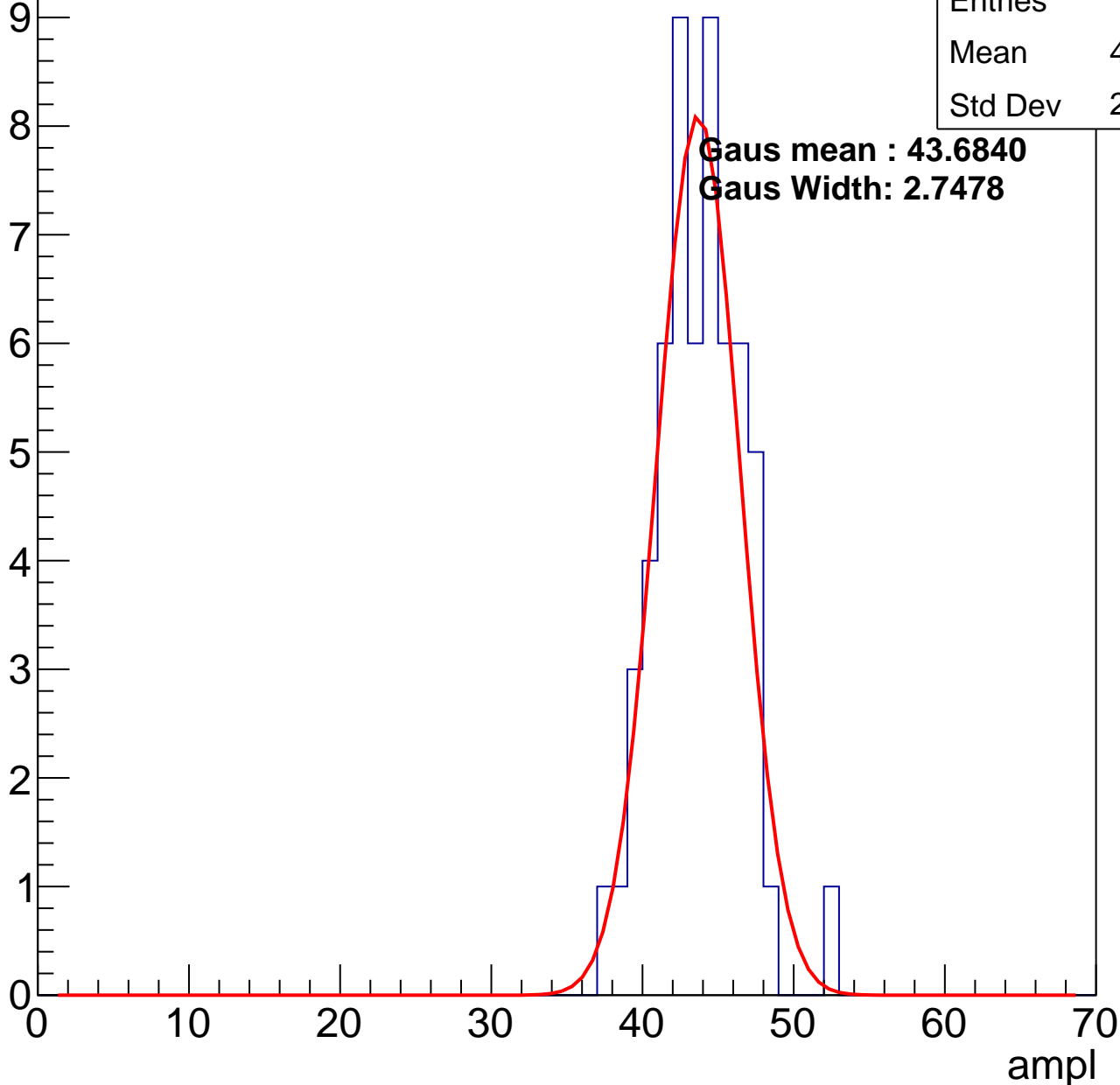
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.29
Std Dev	2.773

**Gaus mean : 43.6840**

**Gaus Width: 2.7478**

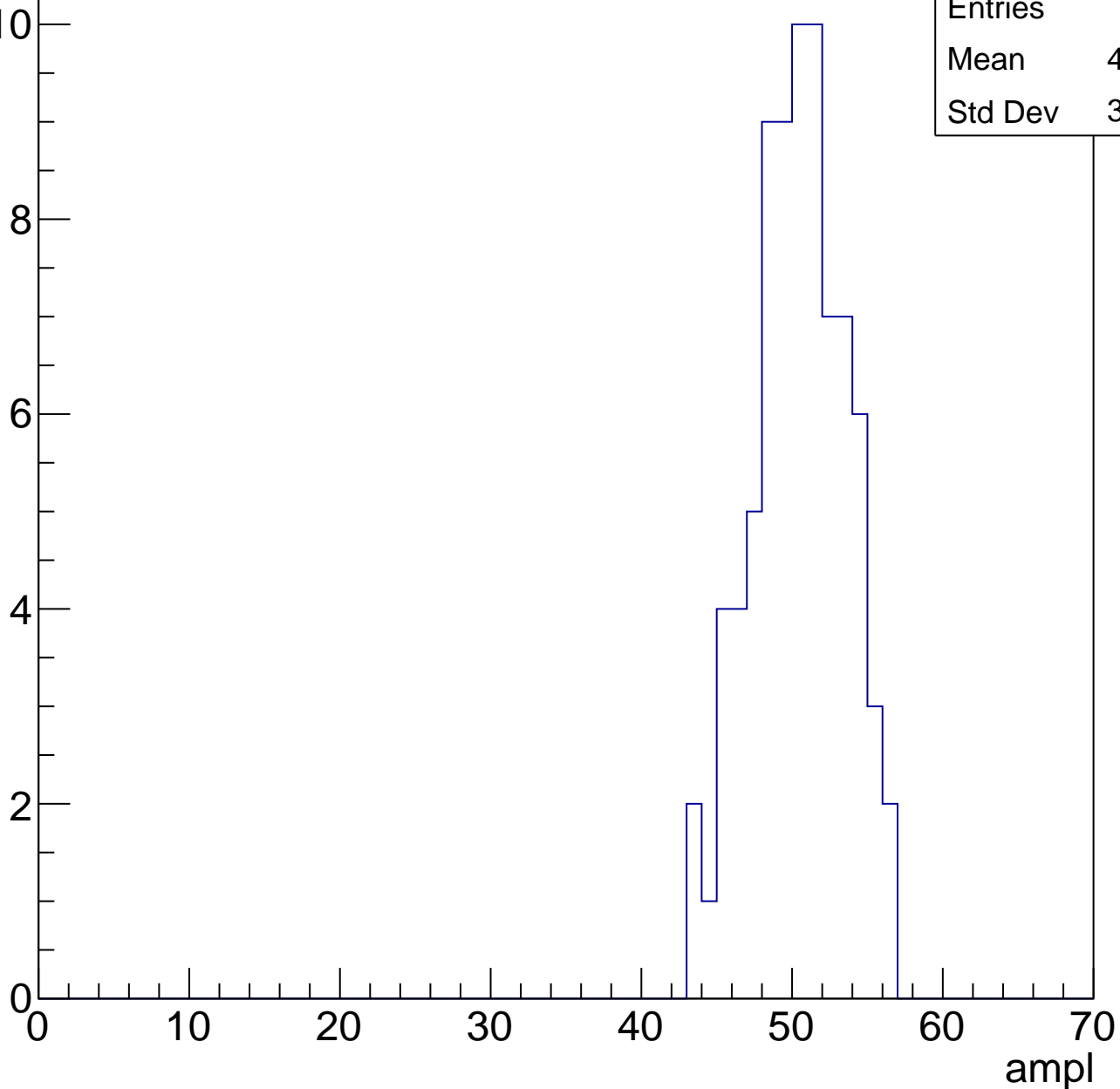


# B0L002S, U23-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

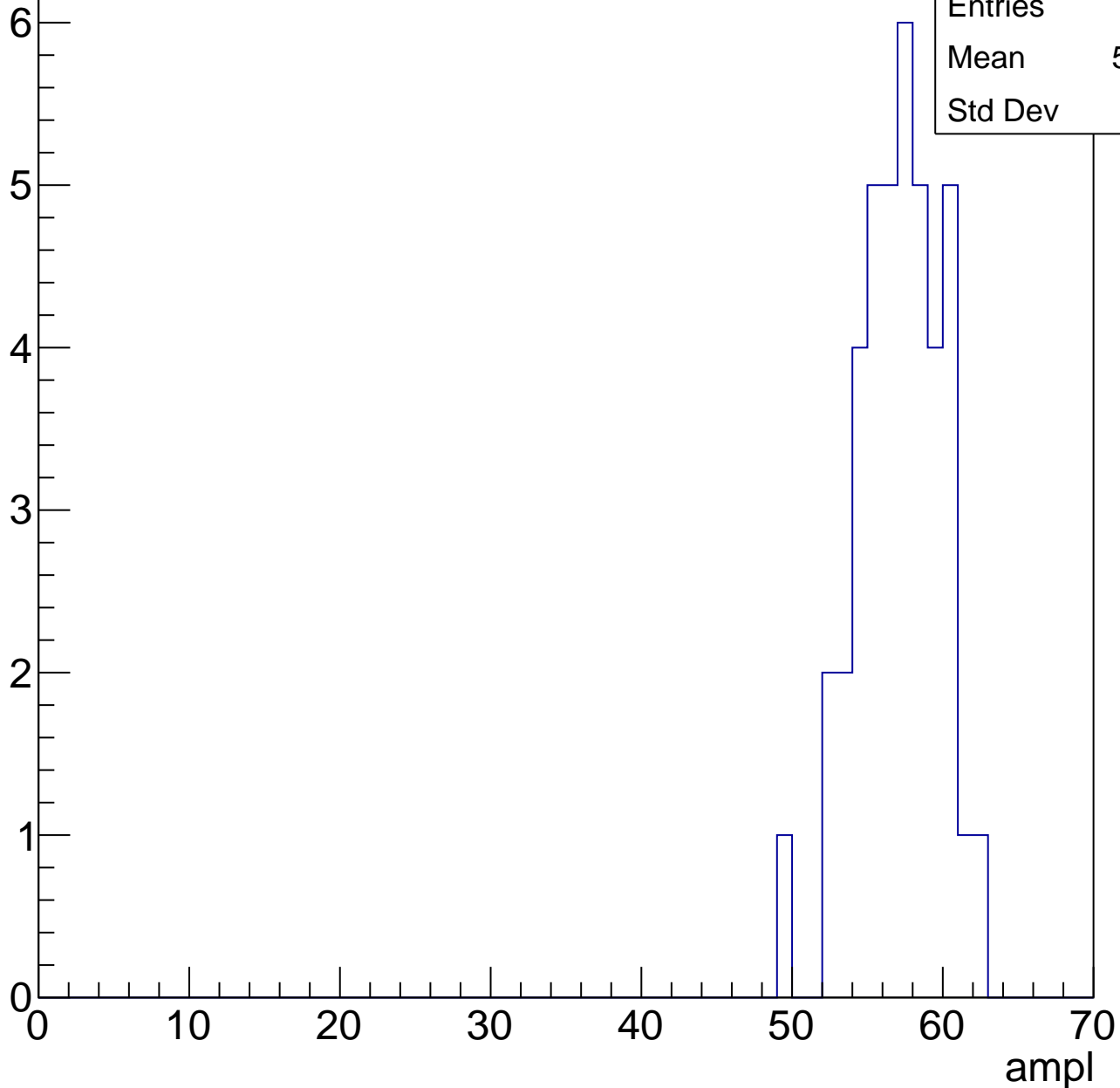
Entries	79
Mean	49.97
Std Dev	3.044



# B0L002S, U23-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



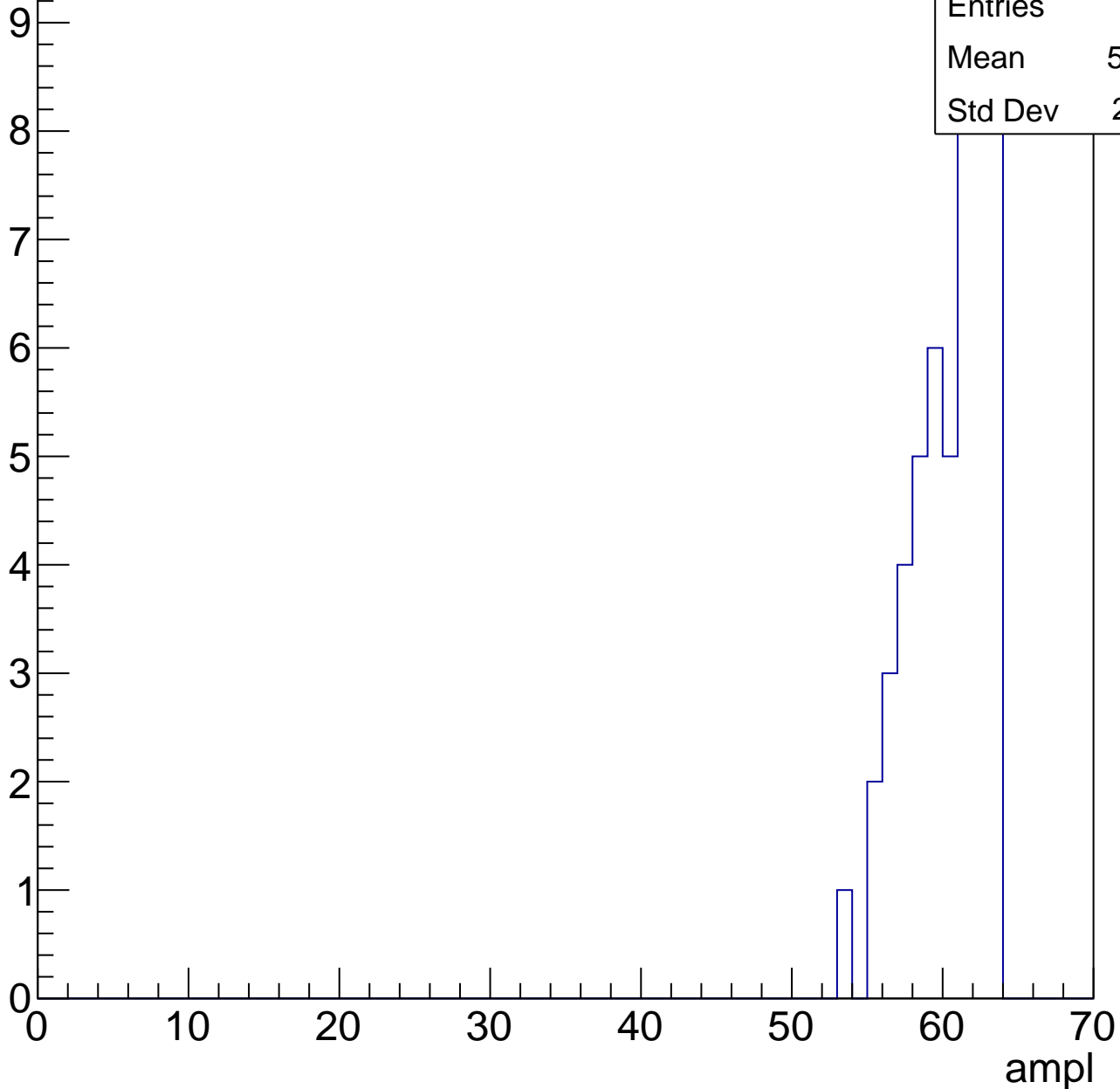
Entries	41
Mean	56.61
Std Dev	2.74

# B0L002S, U23-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	59.88
Std Dev	2.501



# B0L002S, U23-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12
Std Dev	12

# B0L002S, U23-ch43, adc0

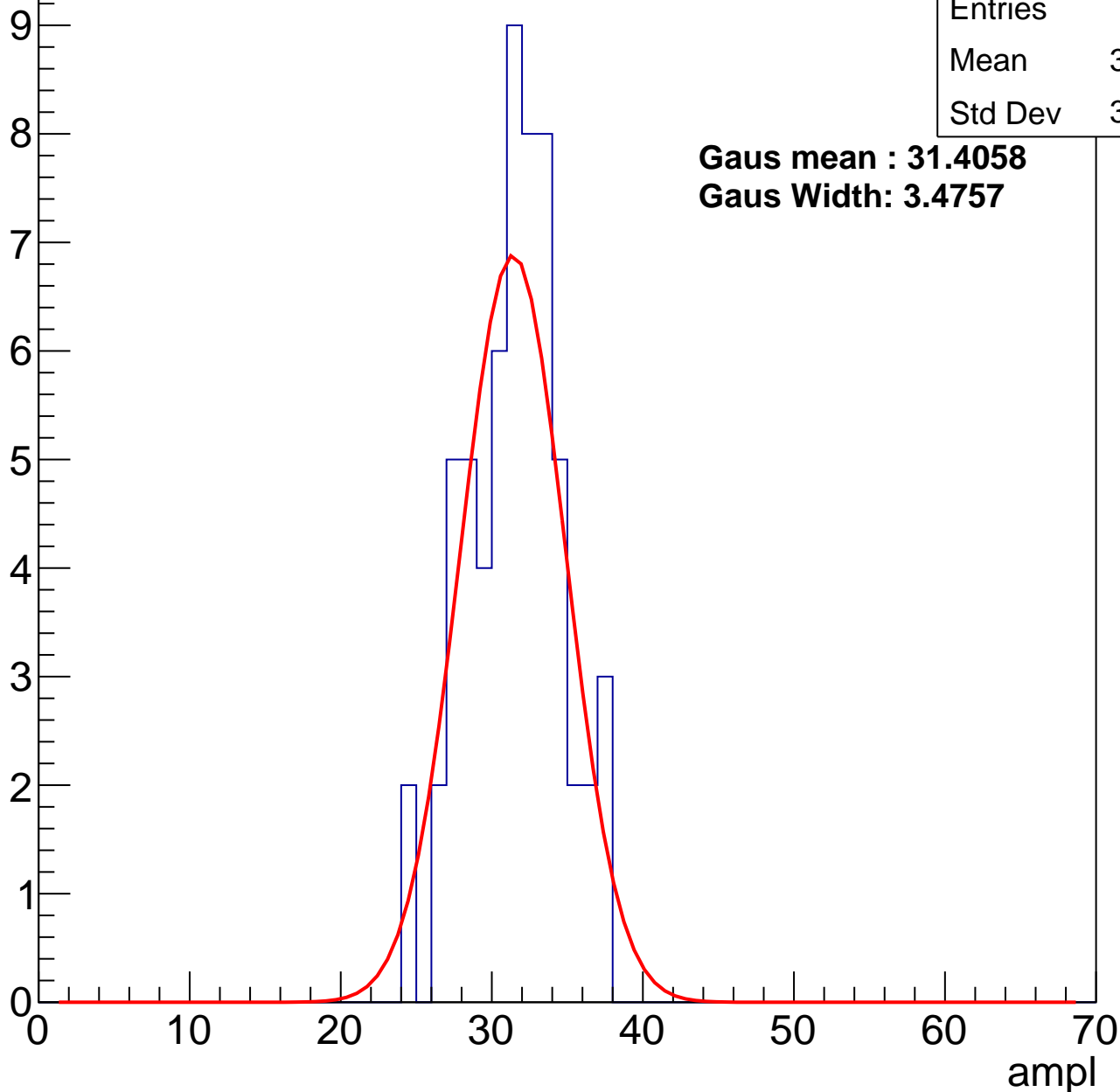
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	31.03
Std Dev	3.057

**Gaus mean : 31.4058**

**Gaus Width: 3.4757**



# B0L002S, U23-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	68
Mean	37.15
Std Dev	3.427

**Gaus mean : 38.1690**

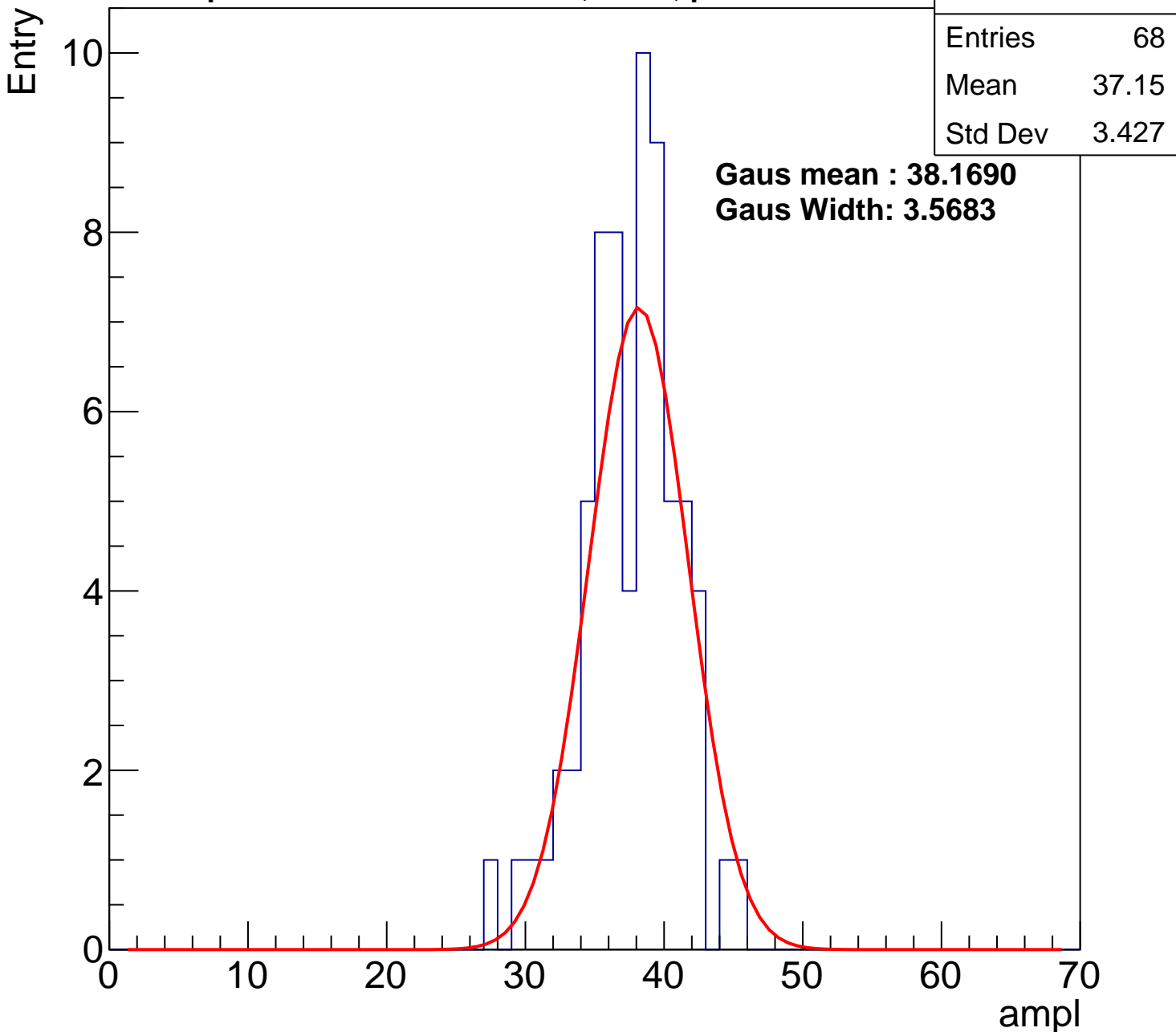
**Gaus Width: 3.5683**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch43, adc2

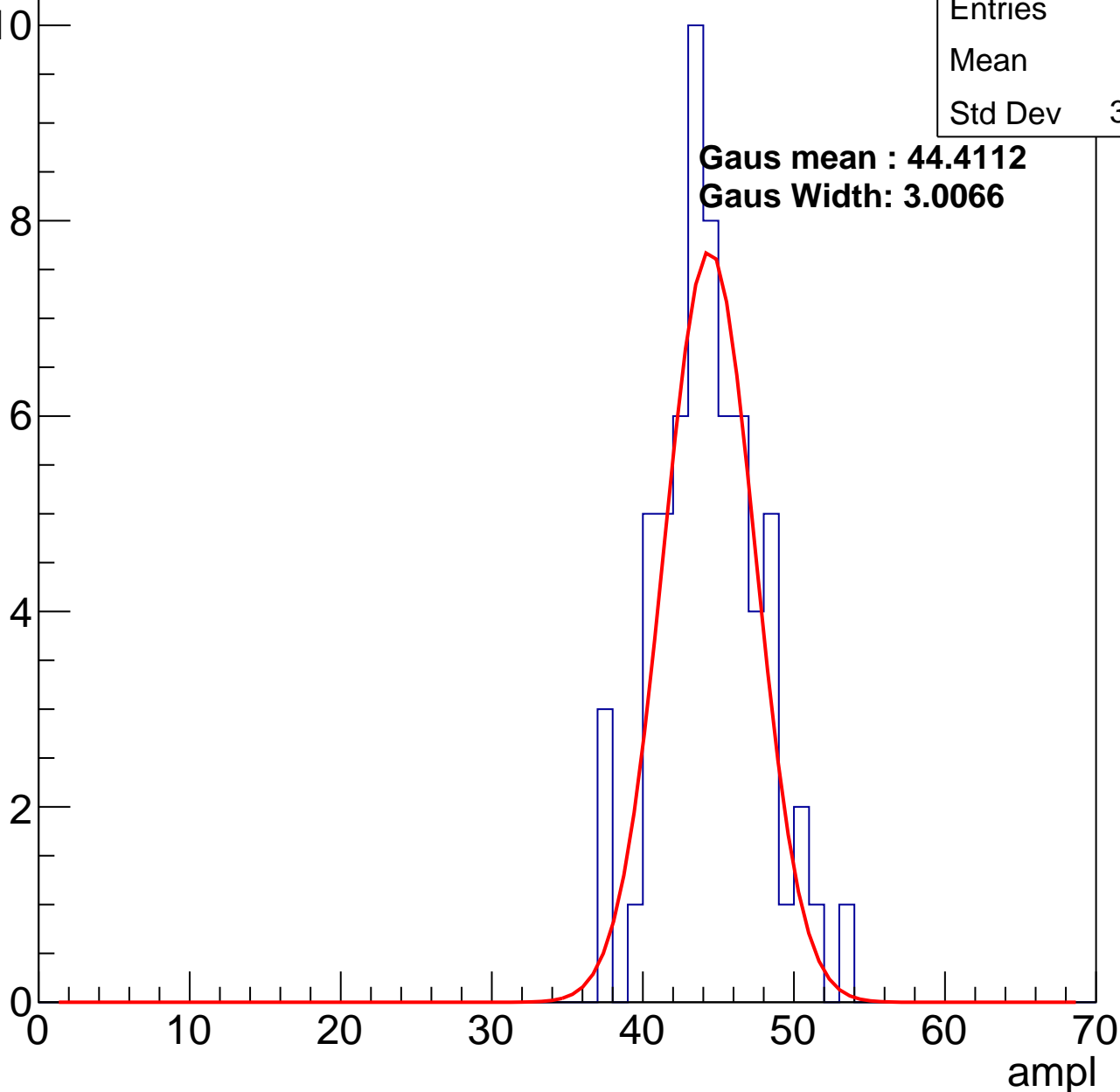
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44
Std Dev	3.317

**Gaus mean : 44.4112**

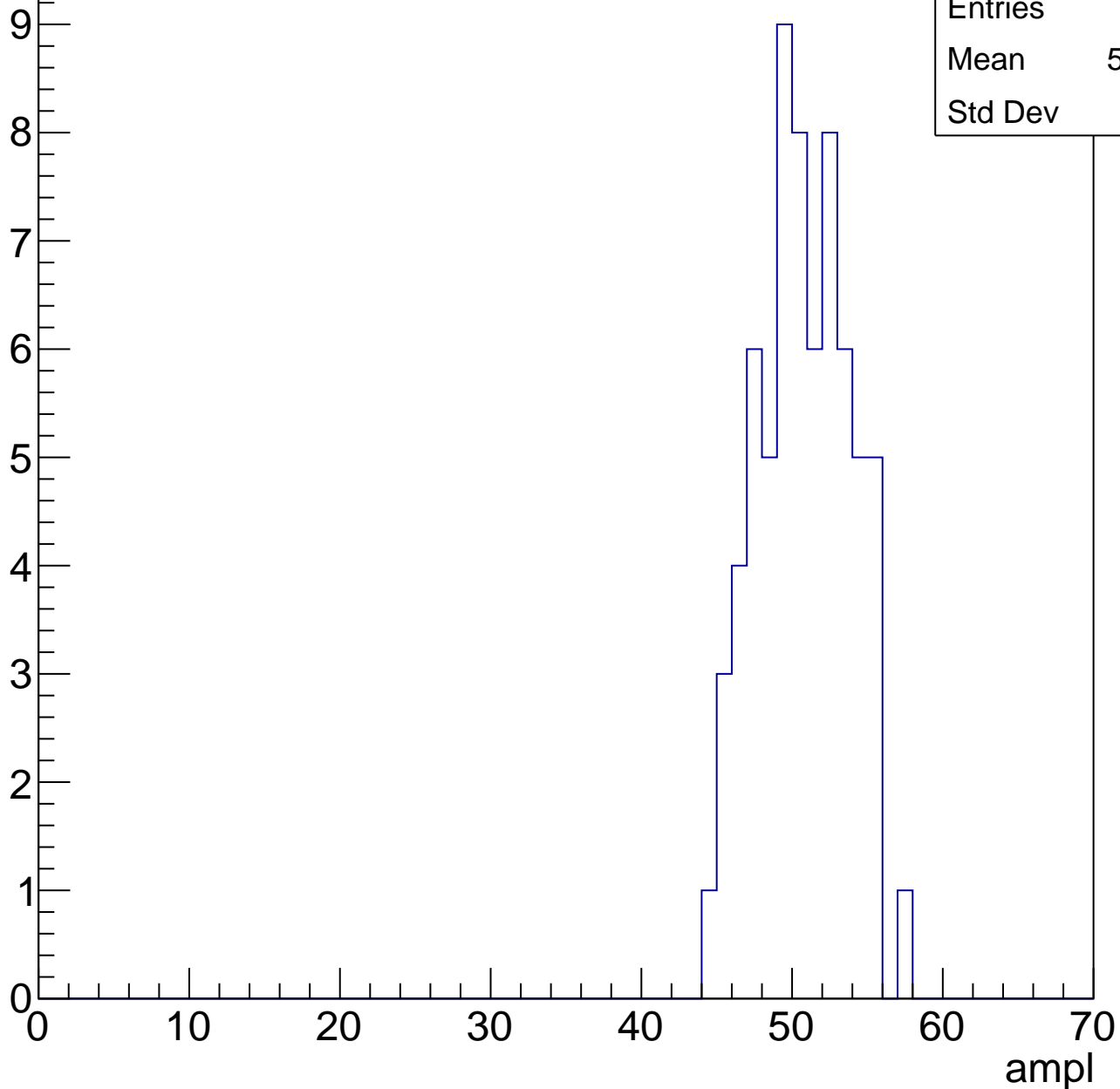
**Gaus Width: 3.0066**



# B0L002S, U23-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

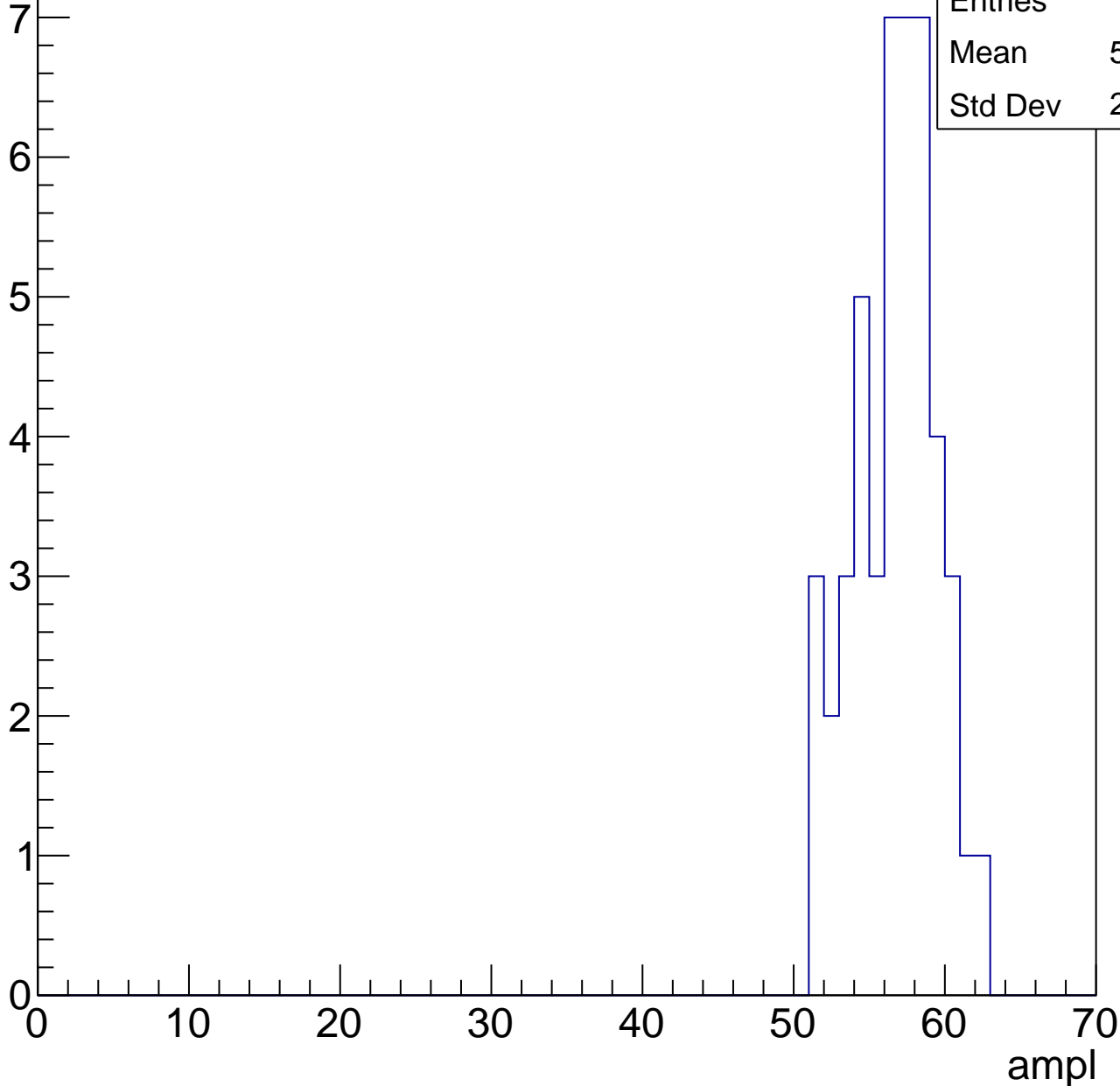


# B0L002S, U23-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	56.24
Std Dev	2.696

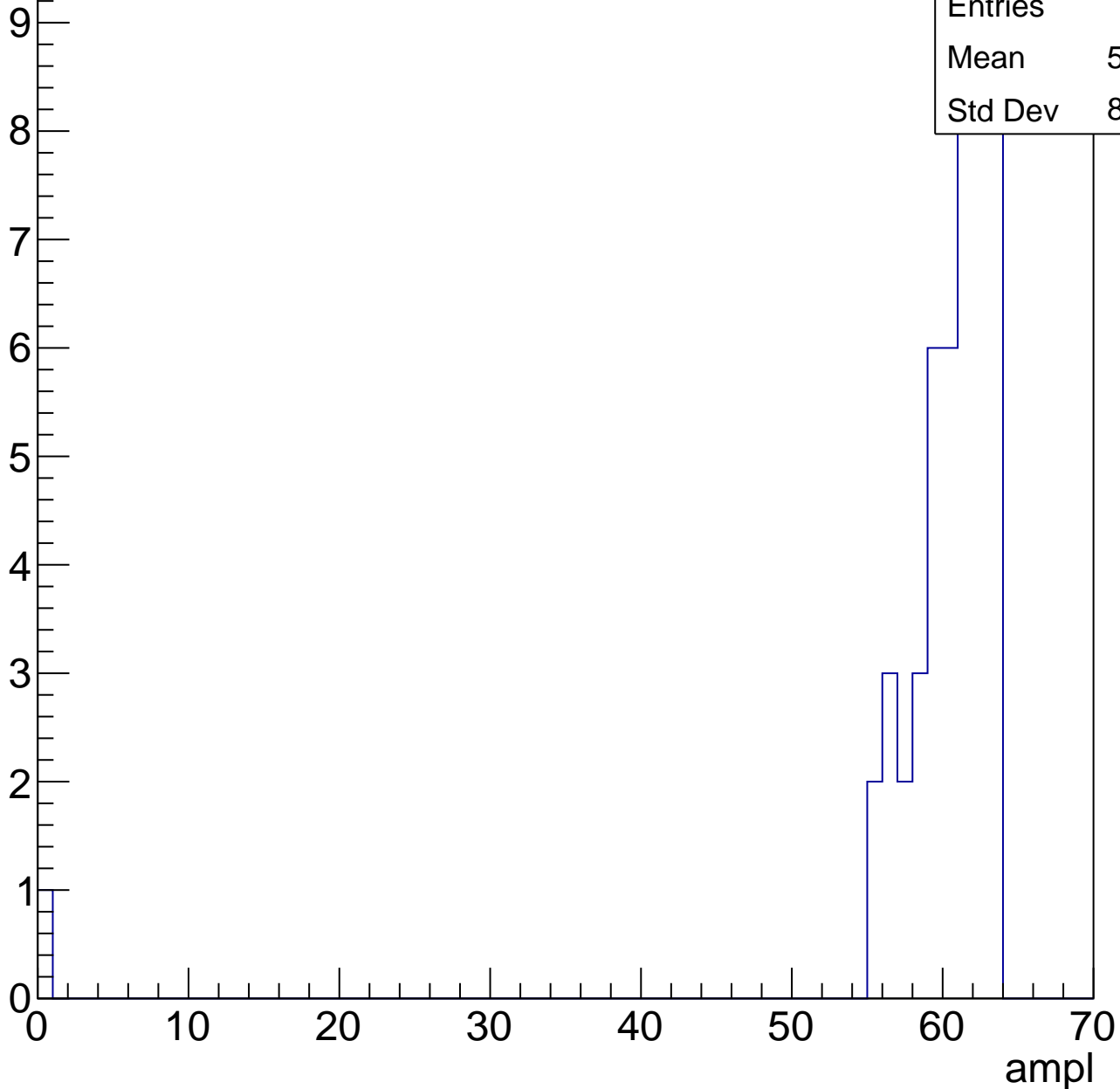


# B0L002S, U23-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	59.08
Std Dev	8.738



# B0L002S, U23-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch44, adc0

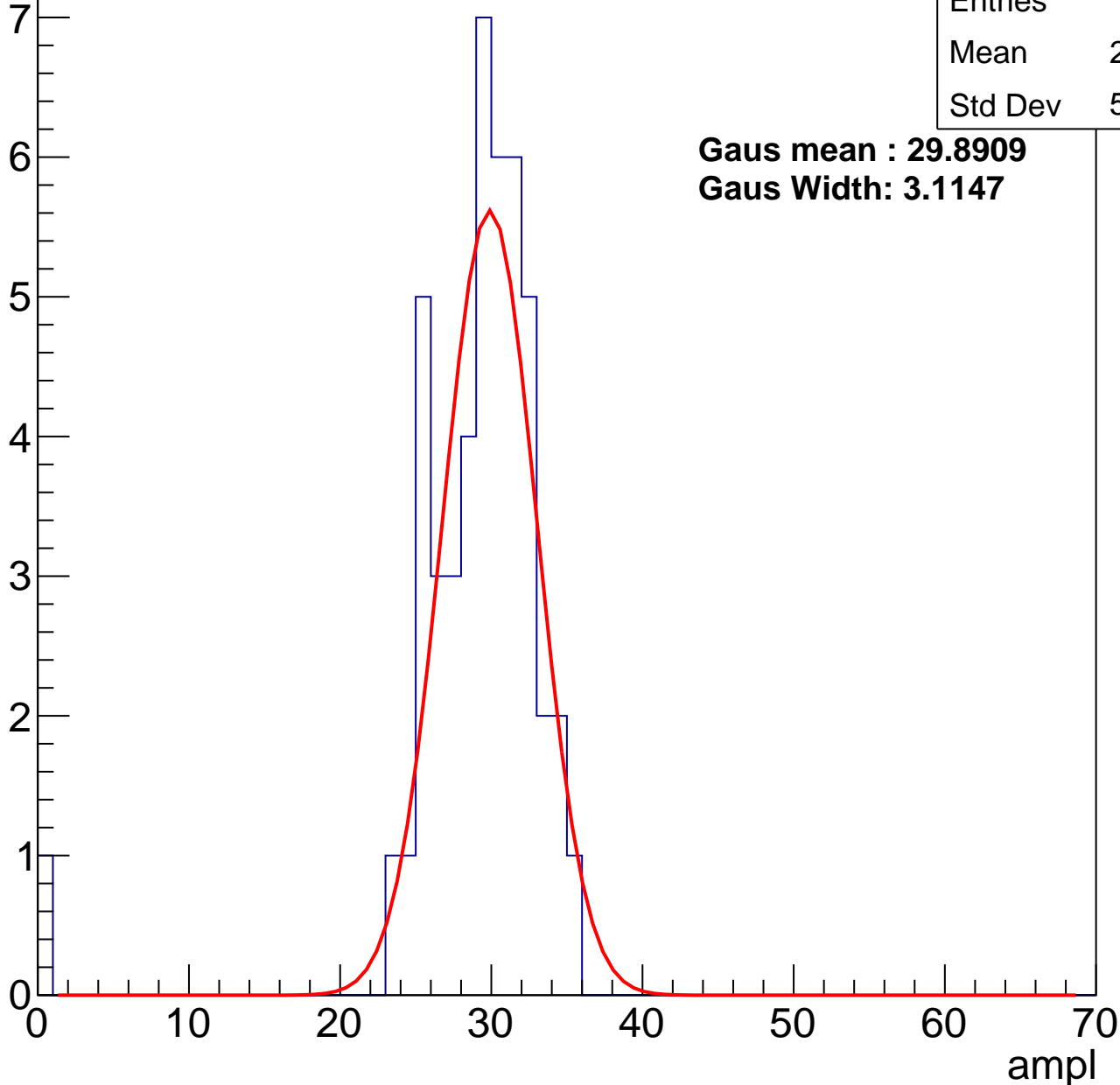
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	28.53
Std Dev	5.069

**Gaus mean : 29.8909**

**Gaus Width: 3.1147**



# B0L002S, U23-ch44, adc1

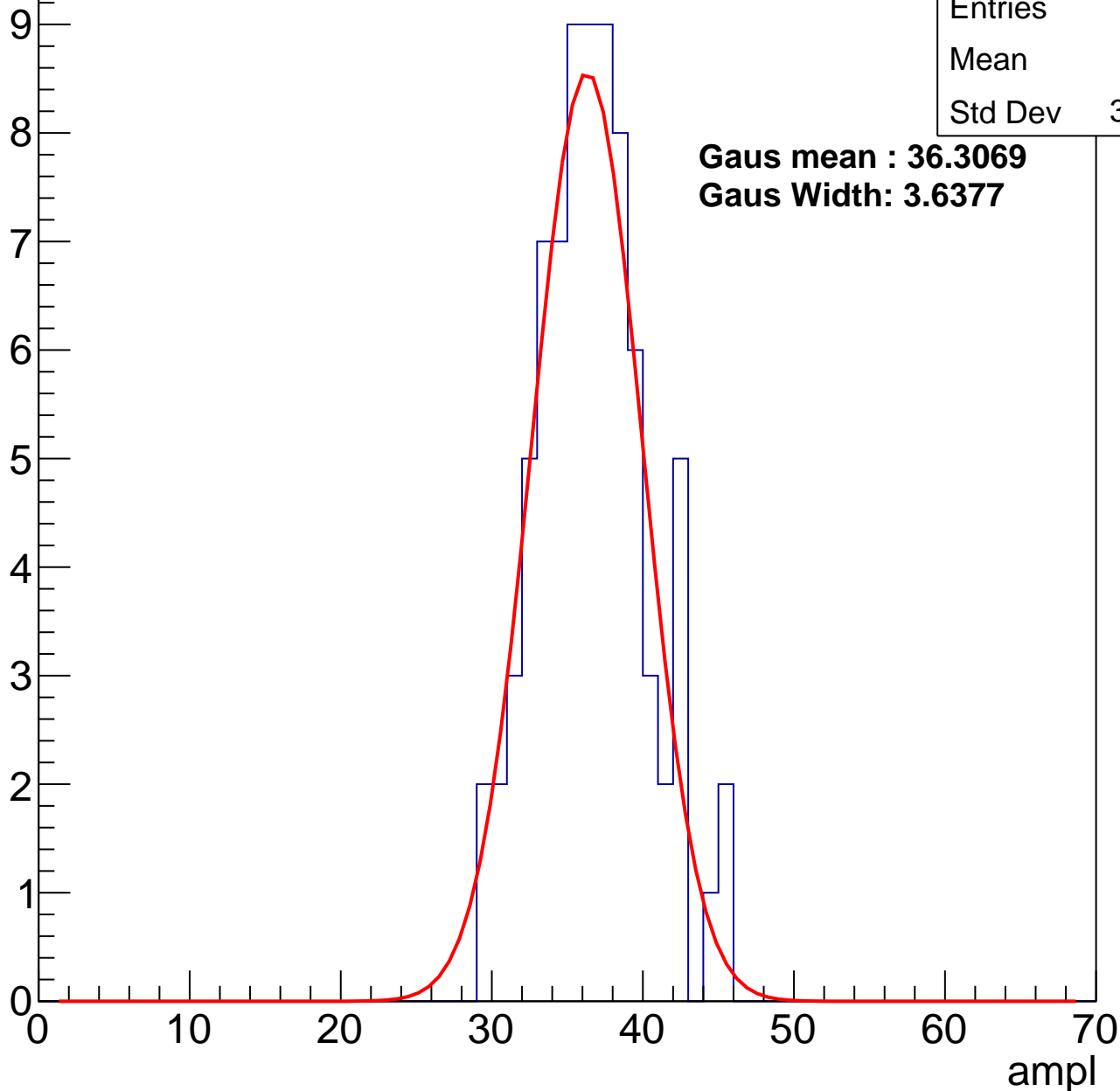
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	80
Mean	36.2
Std Dev	3.572

**Gaus mean : 36.3069**

**Gaus Width: 3.6377**



# B0L002S, U23-ch44, adc2

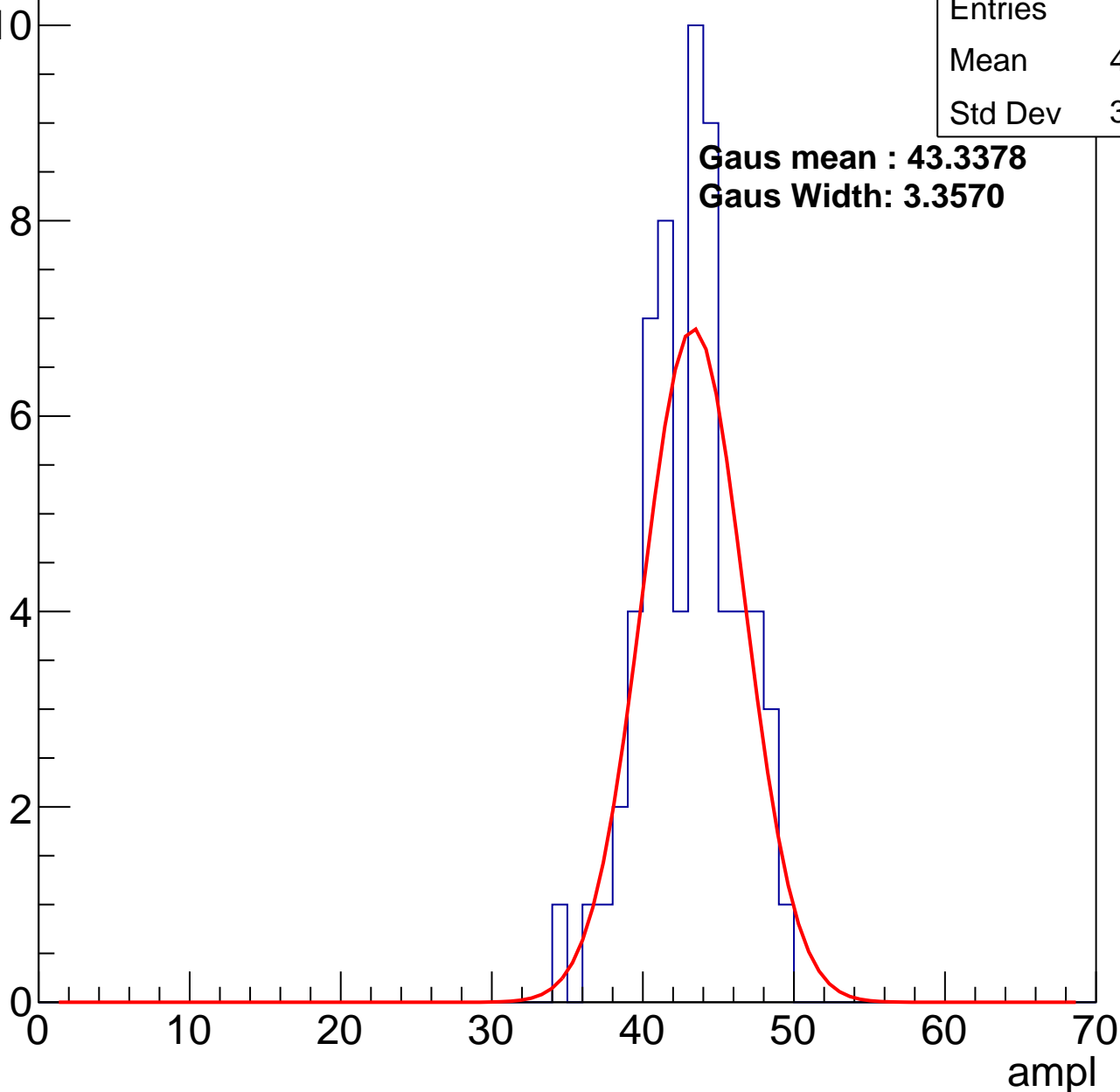
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.63
Std Dev	3.103

**Gaus mean : 43.3378**

**Gaus Width: 3.3570**

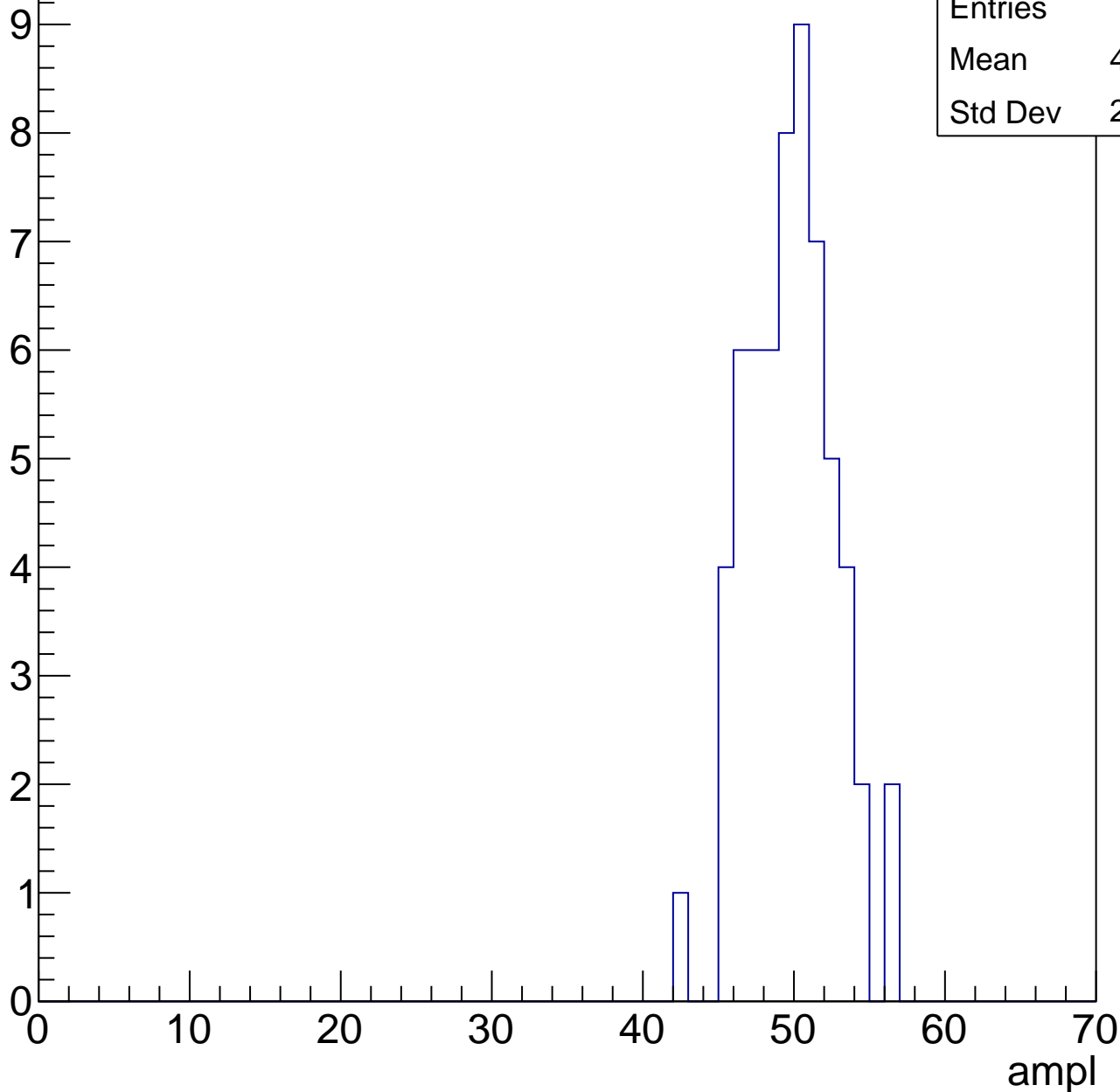


# B0L002S, U23-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

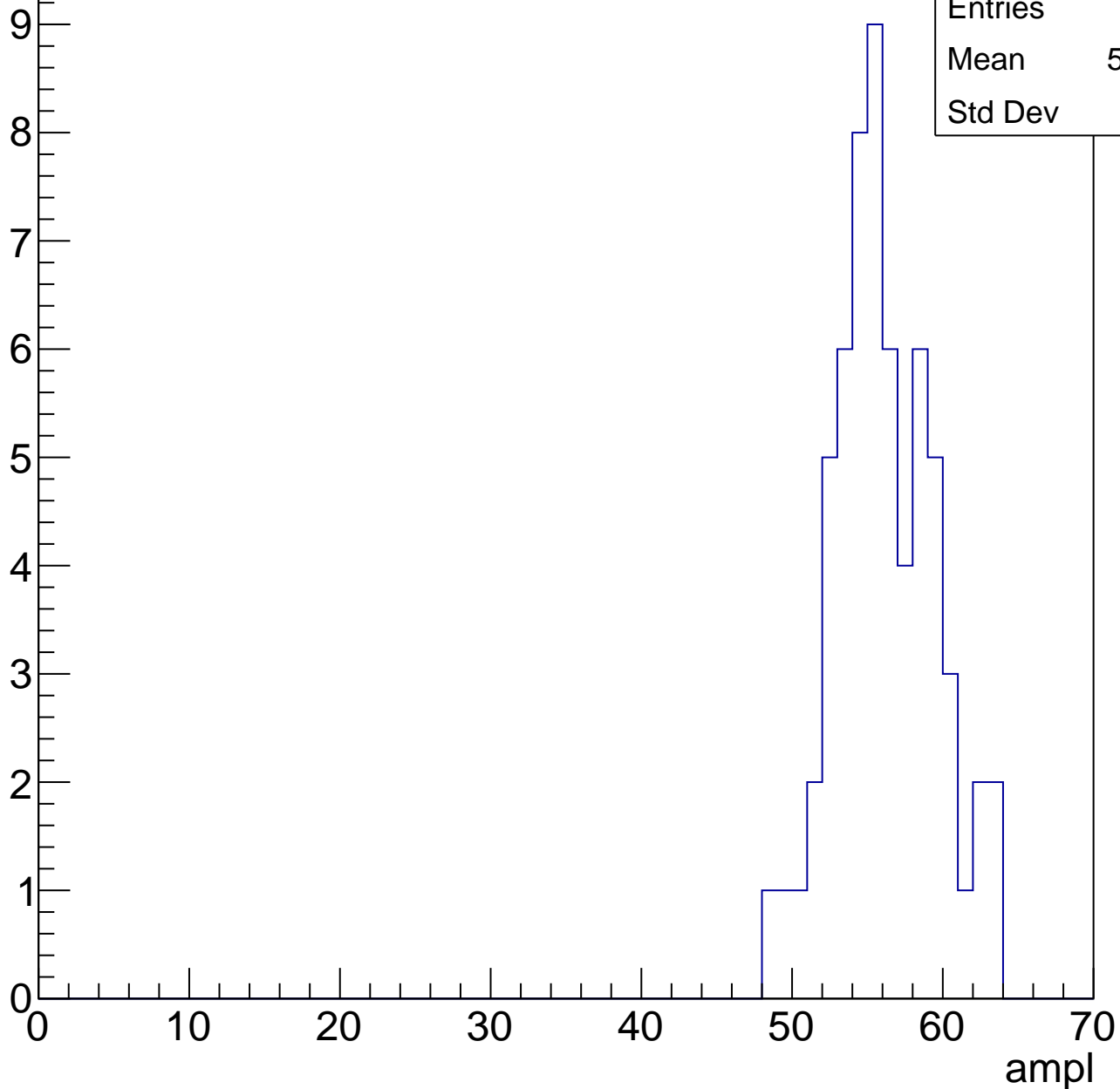
Entries	60
Mean	49.32
Std Dev	2.843



# B0L002S, U23-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



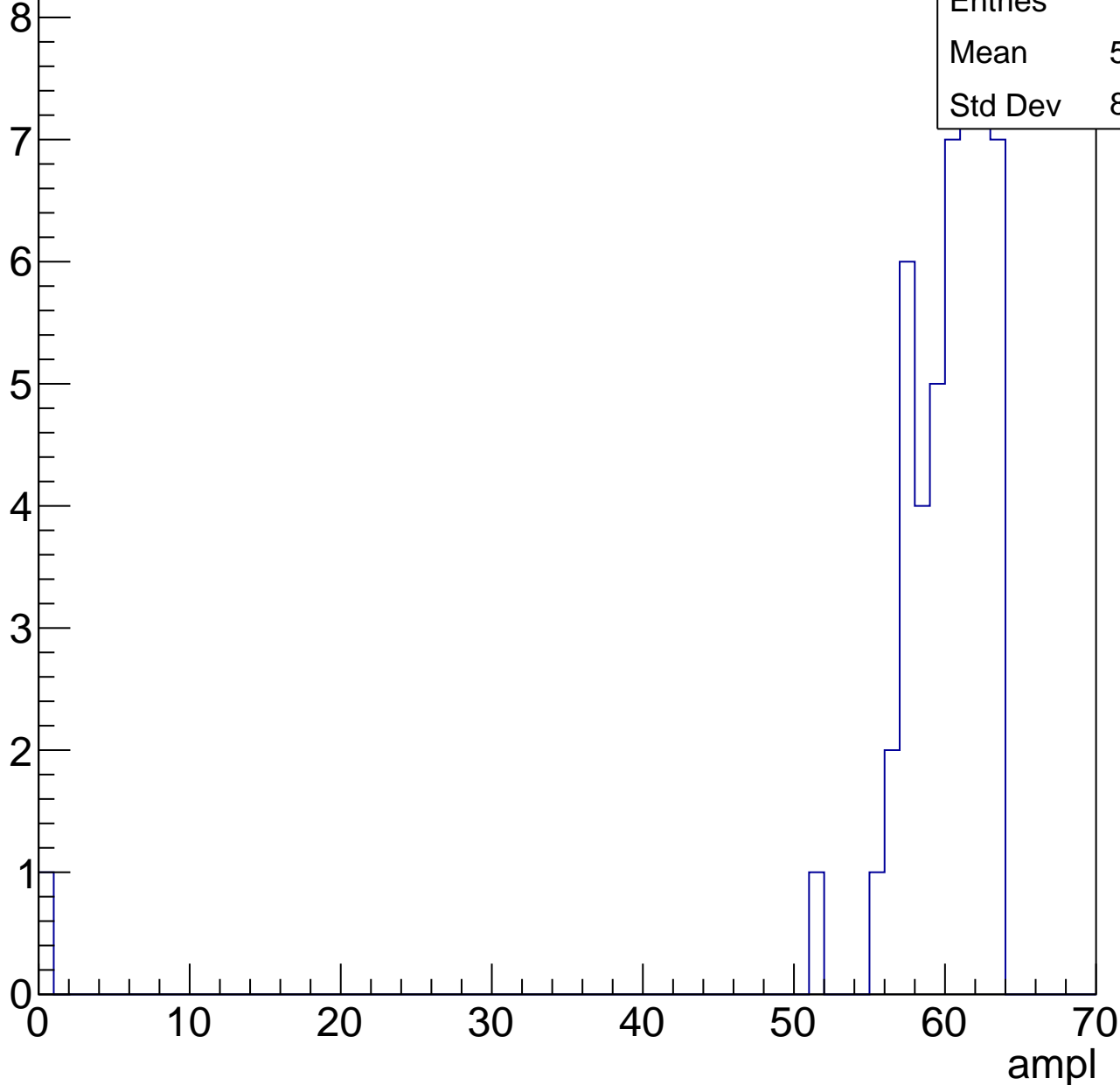
Entries	62
Mean	55.68
Std Dev	3.32

# B0L002S, U23-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	58.64
Std Dev	8.745



# B0L002S, U23-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch45, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	28.26
Std Dev	3.29

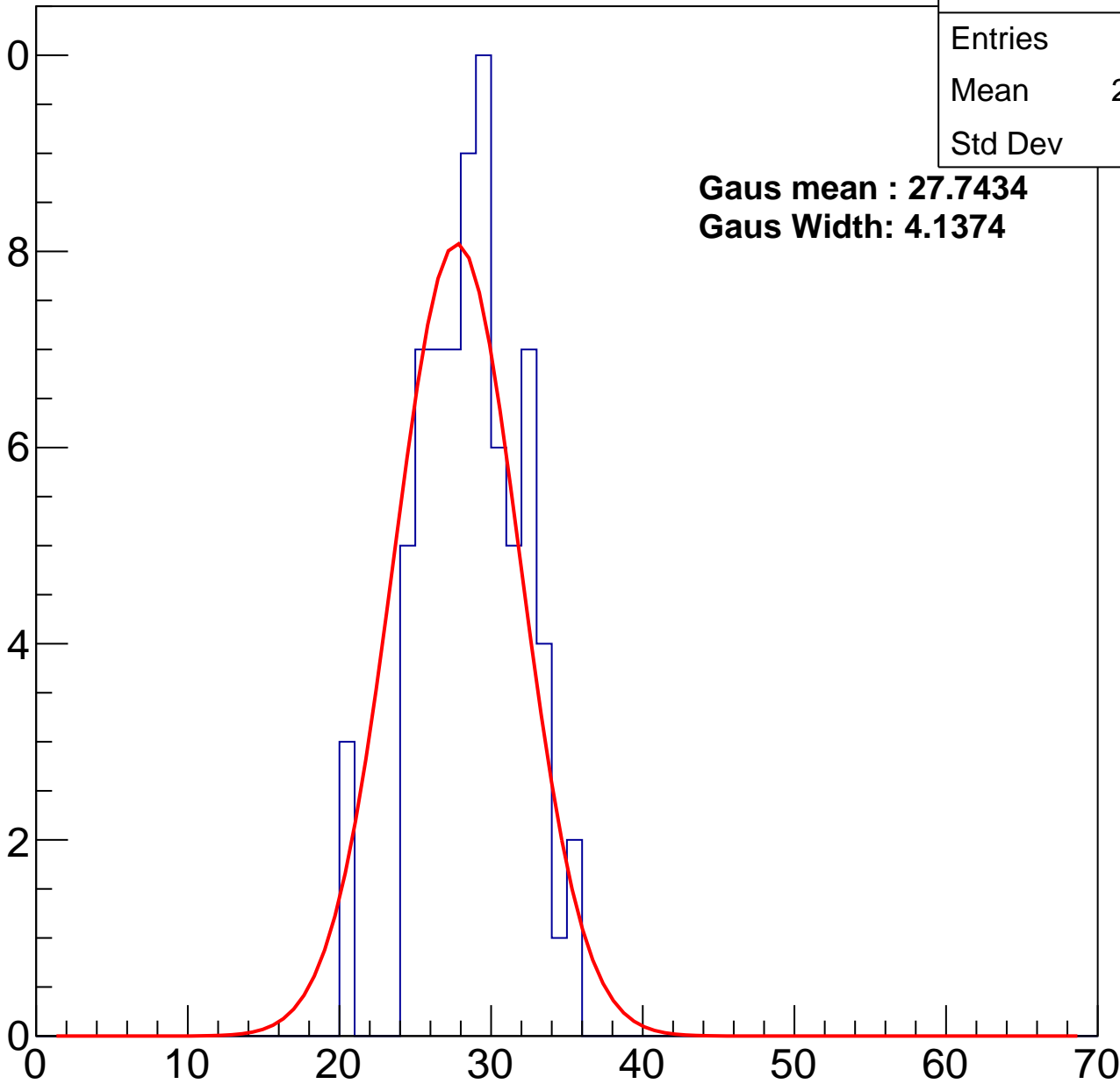
**Gaus mean : 27.7434**

**Gaus Width: 4.1374**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch45, adc1

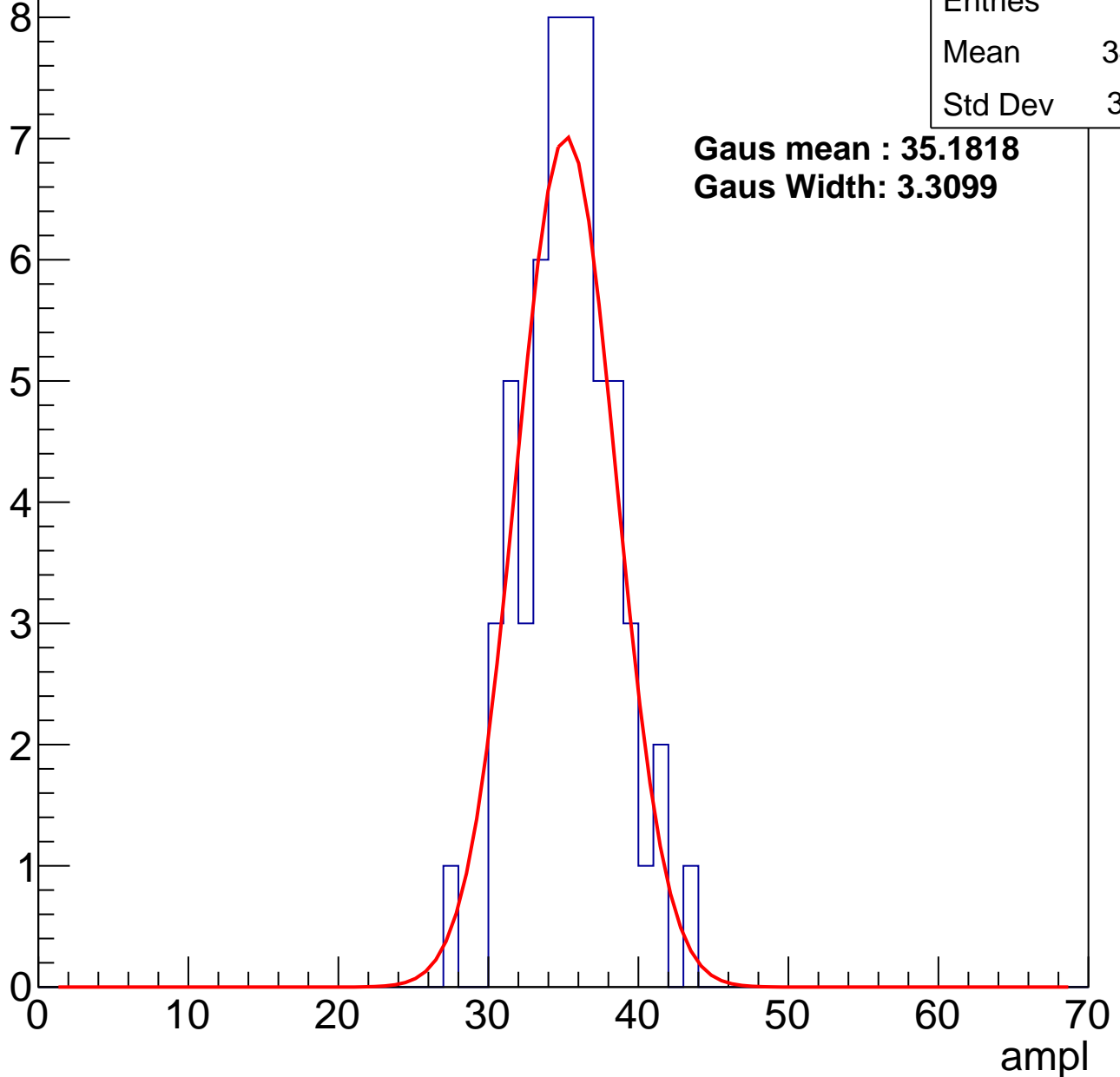
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	34.97
Std Dev	3.081

**Gaus mean : 35.1818**

**Gaus Width: 3.3099**



# B0L002S, U23-ch45, adc2

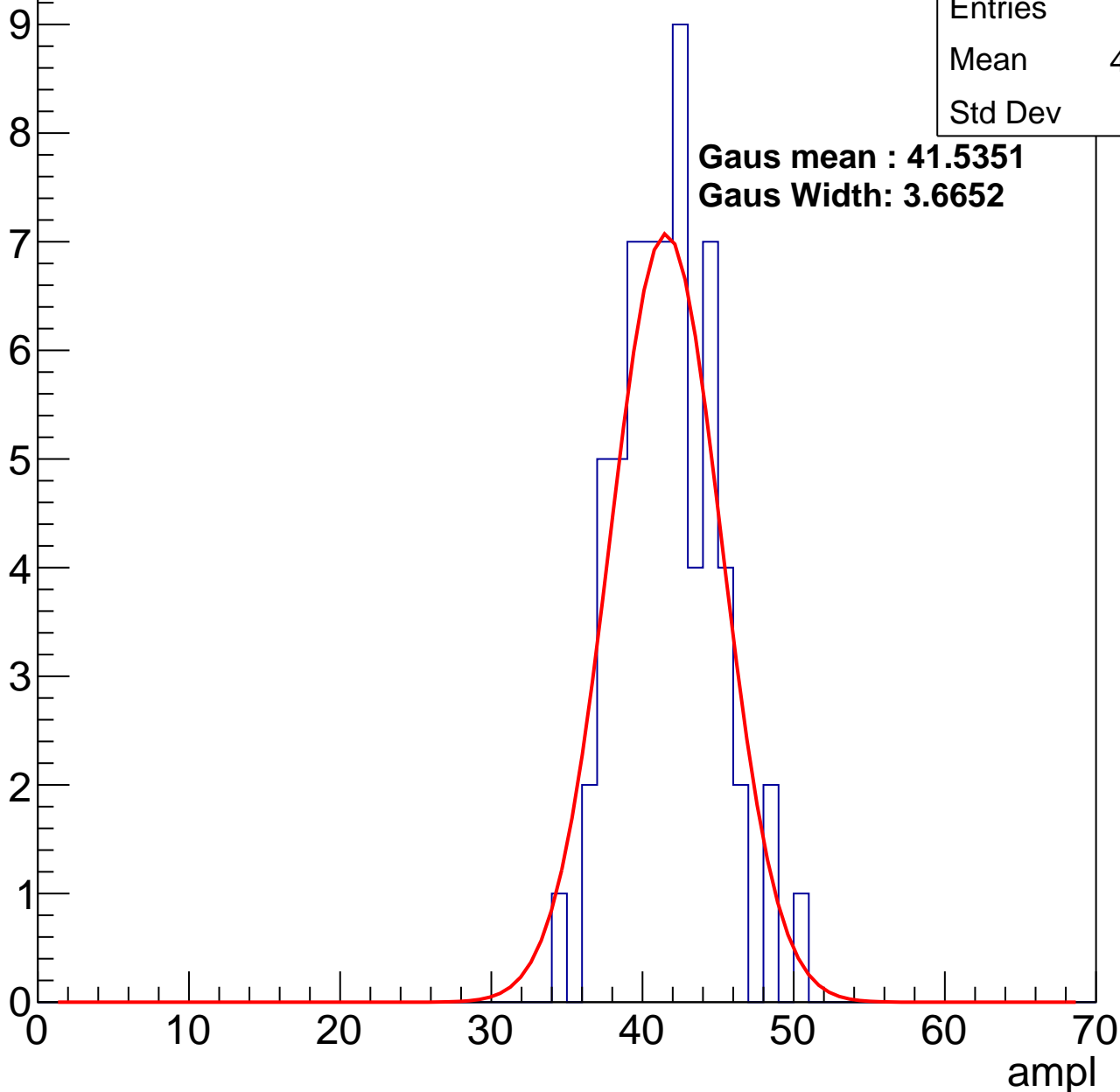
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	41.22
Std Dev	3.18

**Gaus mean : 41.5351**

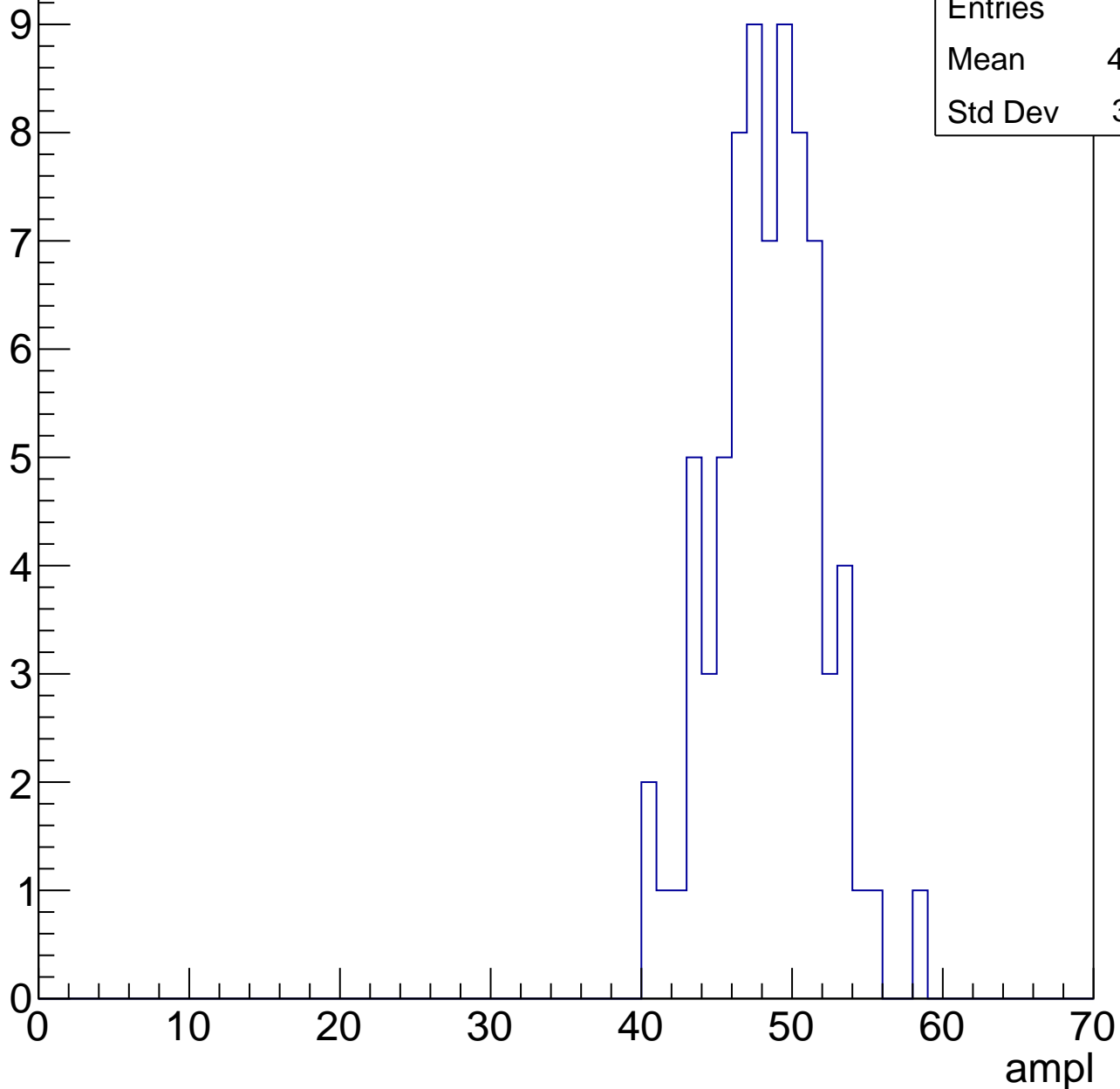
**Gaus Width: 3.6652**



# B0L002S, U23-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



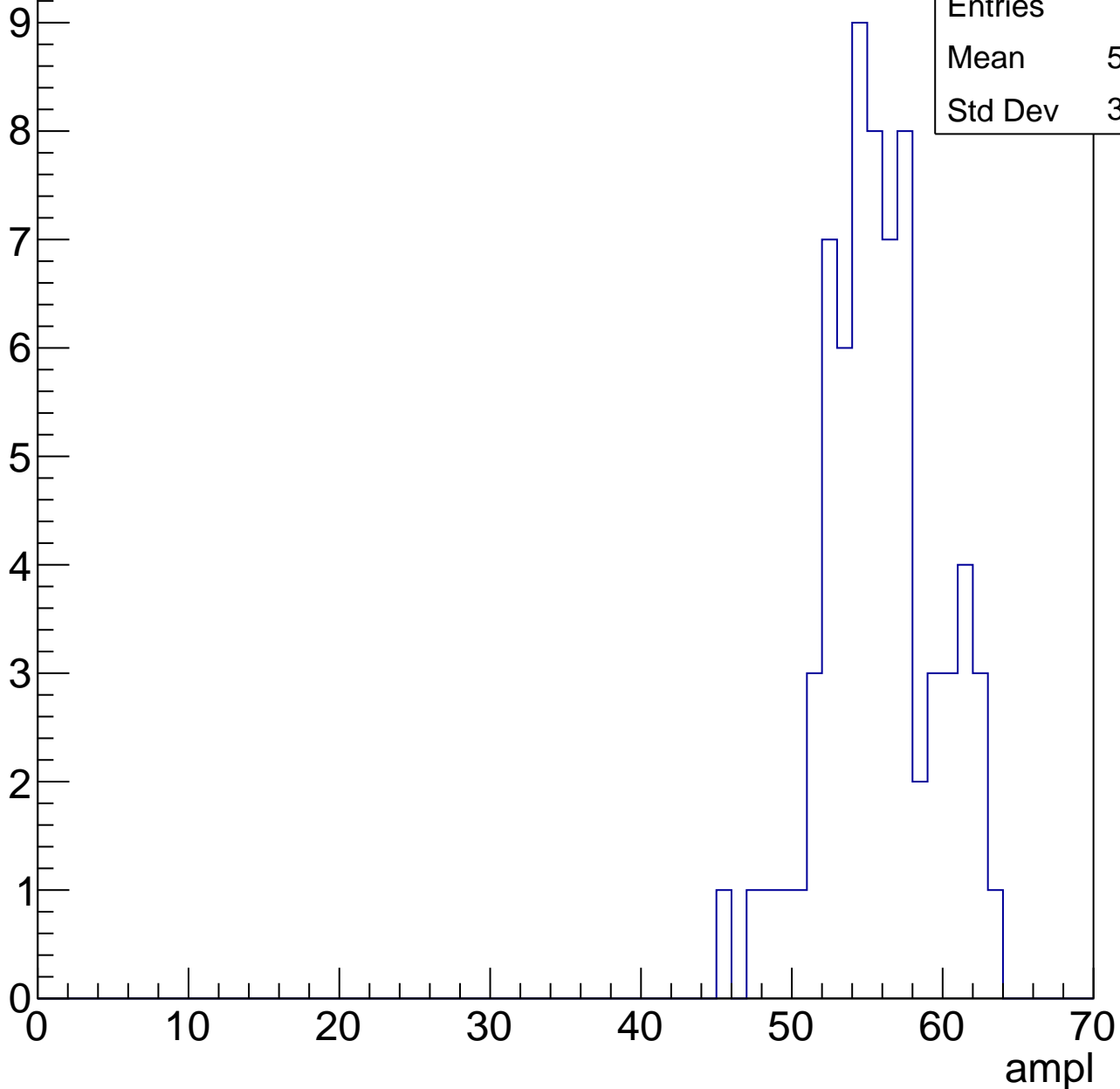
Entries	75
Mean	47.93
Std Dev	3.481

# B0L002S, U23-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	55.28
Std Dev	3.706

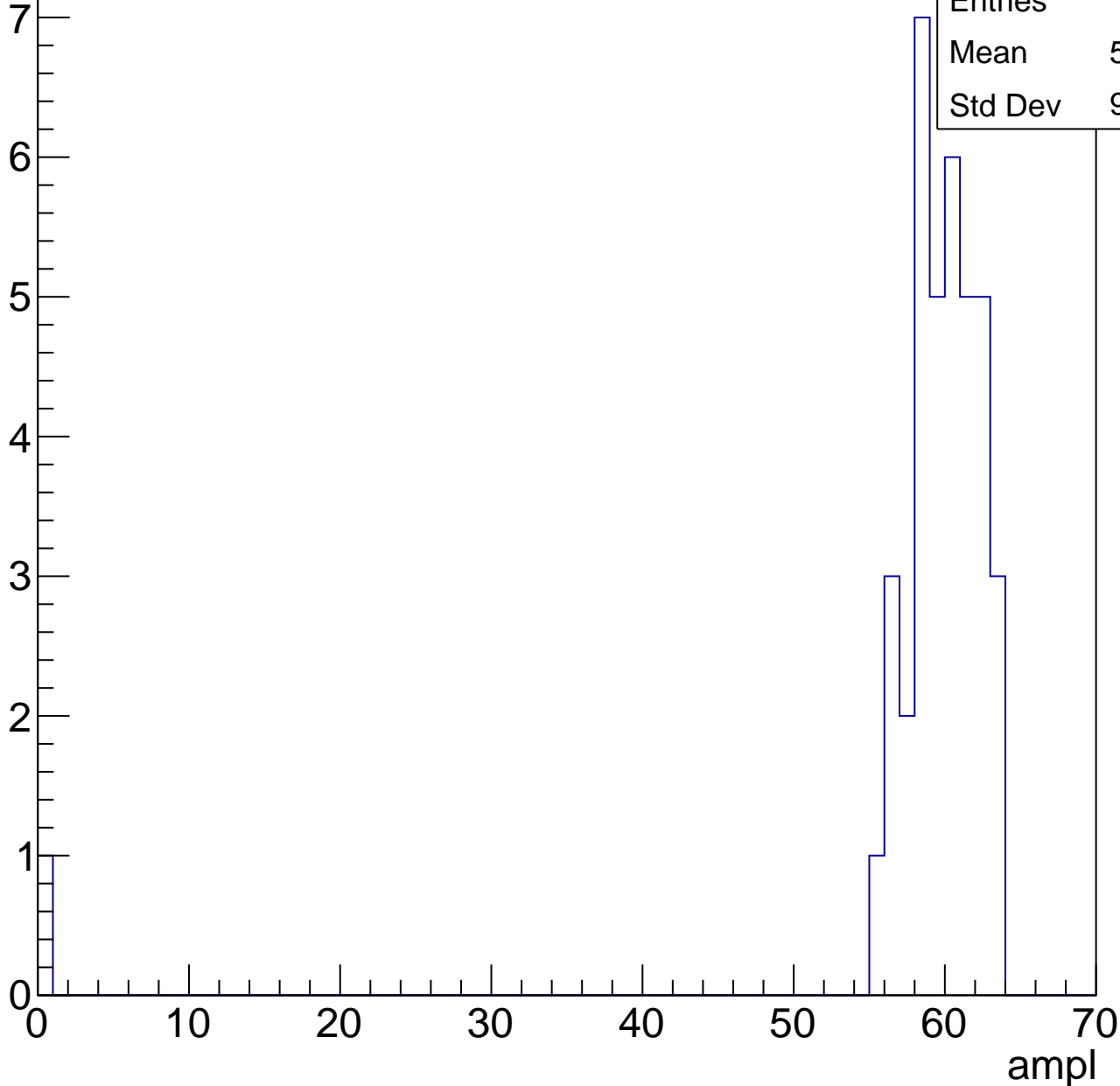


# B0L002S, U23-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

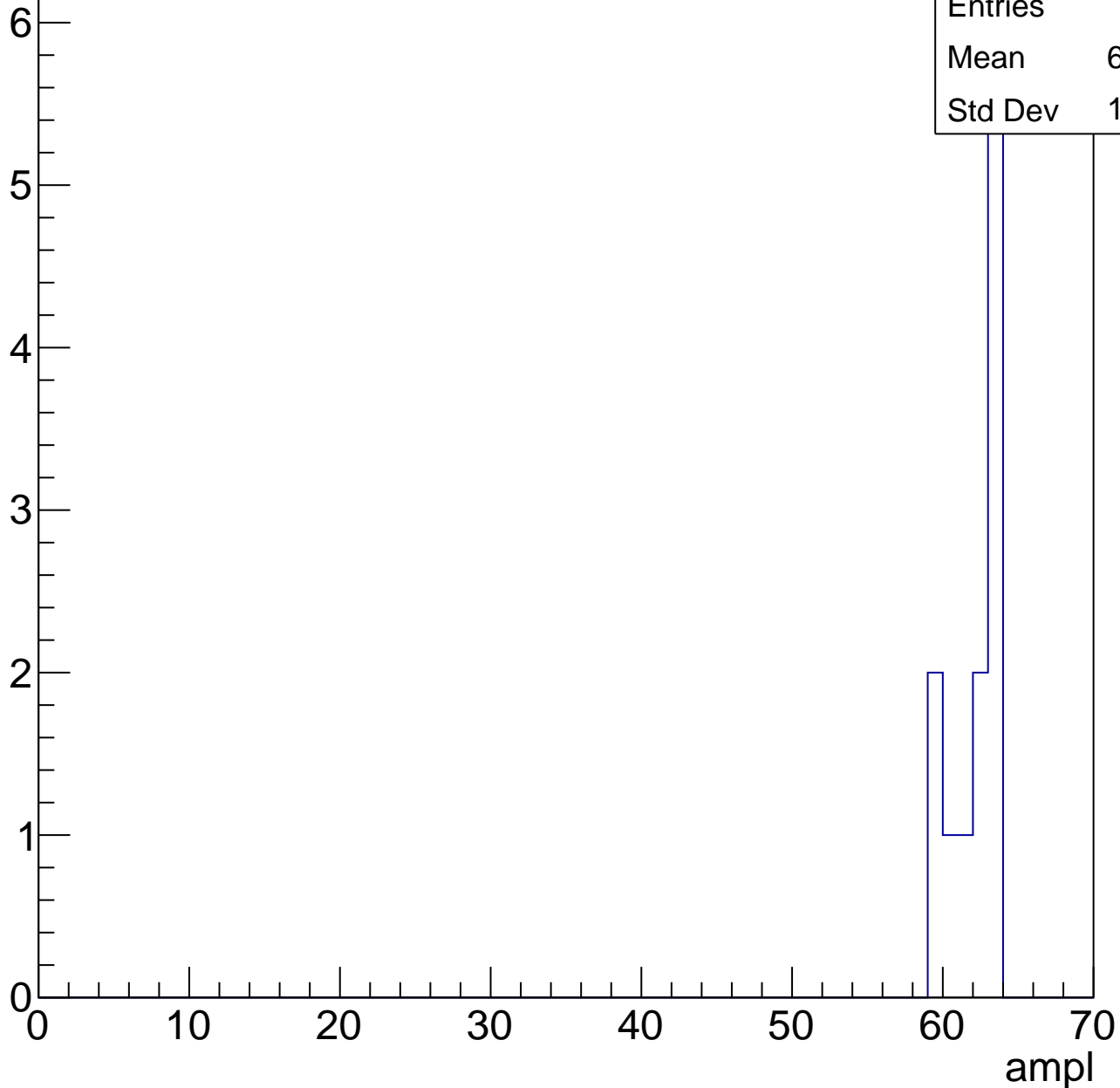
Entries	38
Mean	57.95
Std Dev	9.755



# B0L002S, U23-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch46, adc0

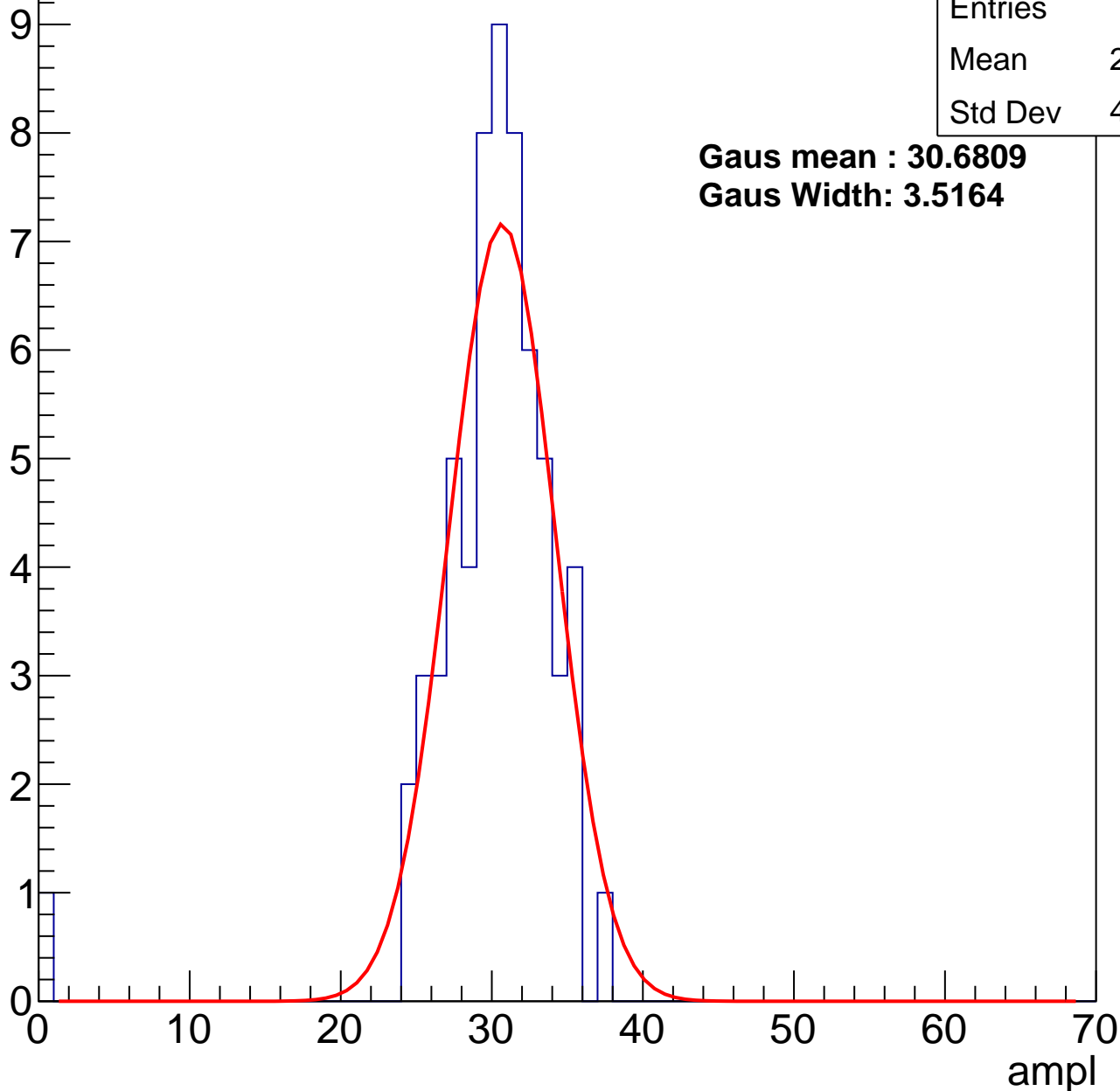
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.58
Std Dev	4.798

**Gaus mean : 30.6809**

**Gaus Width: 3.5164**



# B0L002S, U23-ch46, adc1

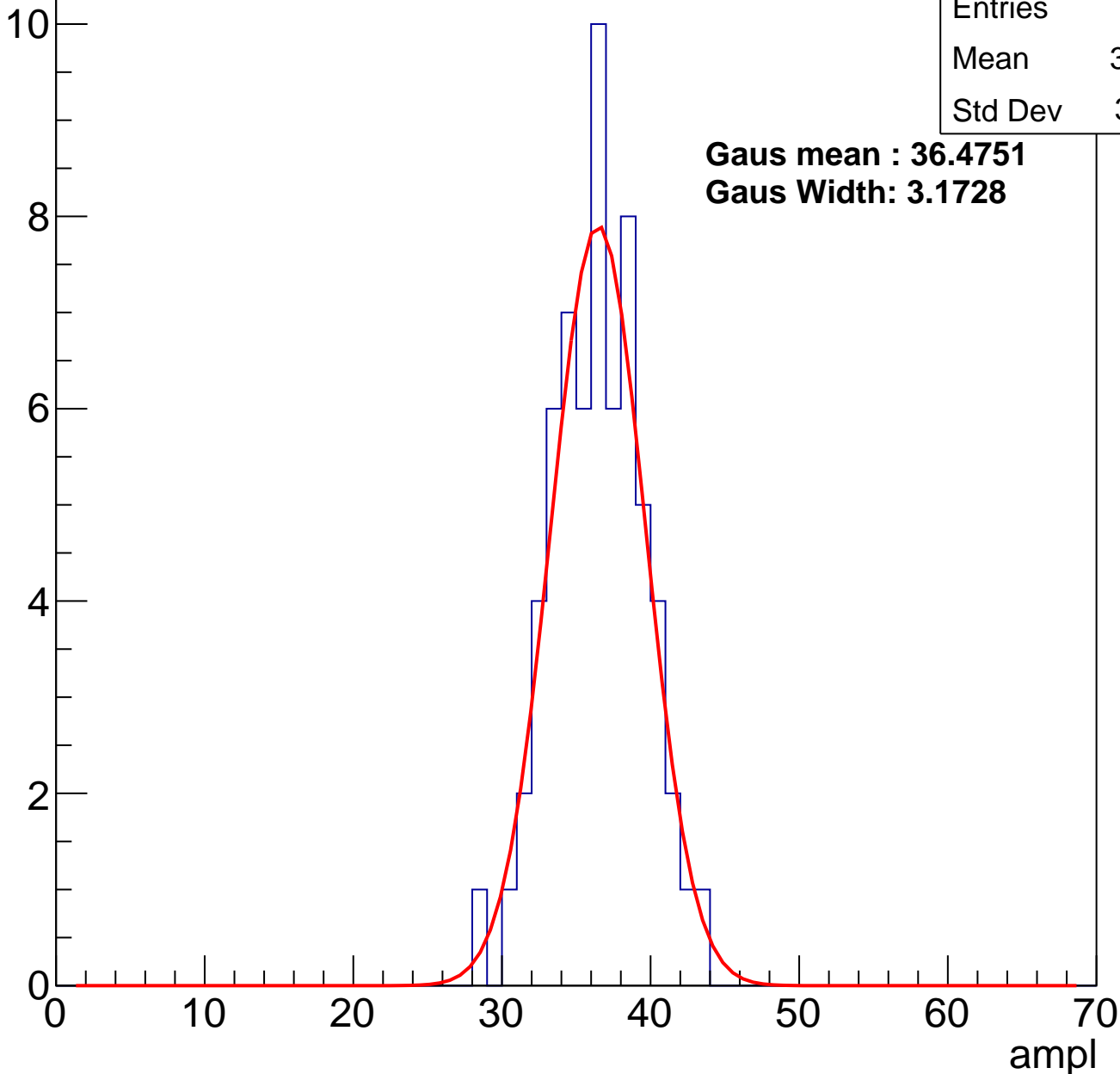
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	64
Mean	35.97
Std Dev	3.021

**Gaus mean : 36.4751**

**Gaus Width: 3.1728**

Entry



# B0L002S, U23-ch46, adc2

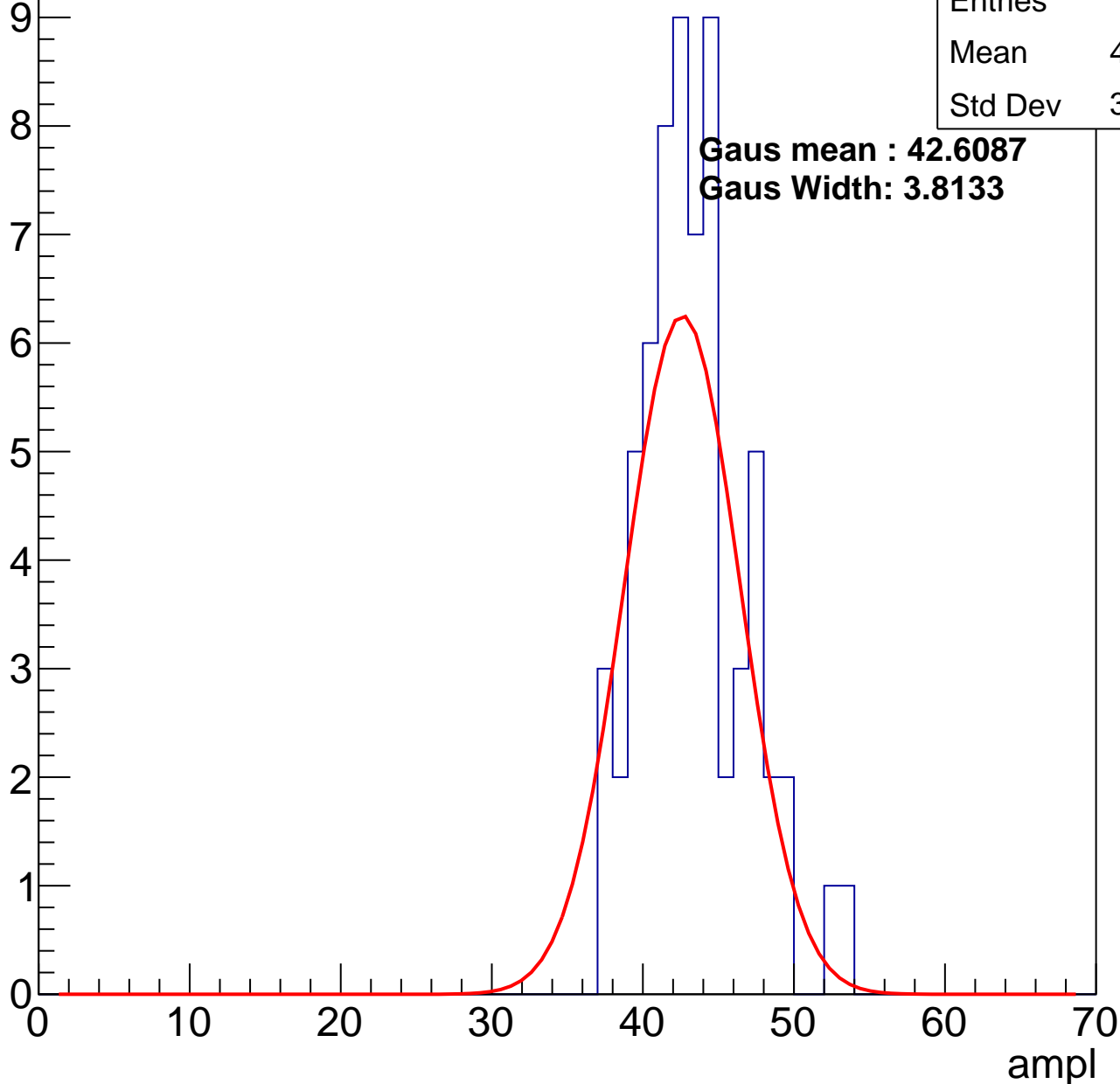
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	42.88
Std Dev	3.417

**Gaus mean : 42.6087**

**Gaus Width: 3.8133**

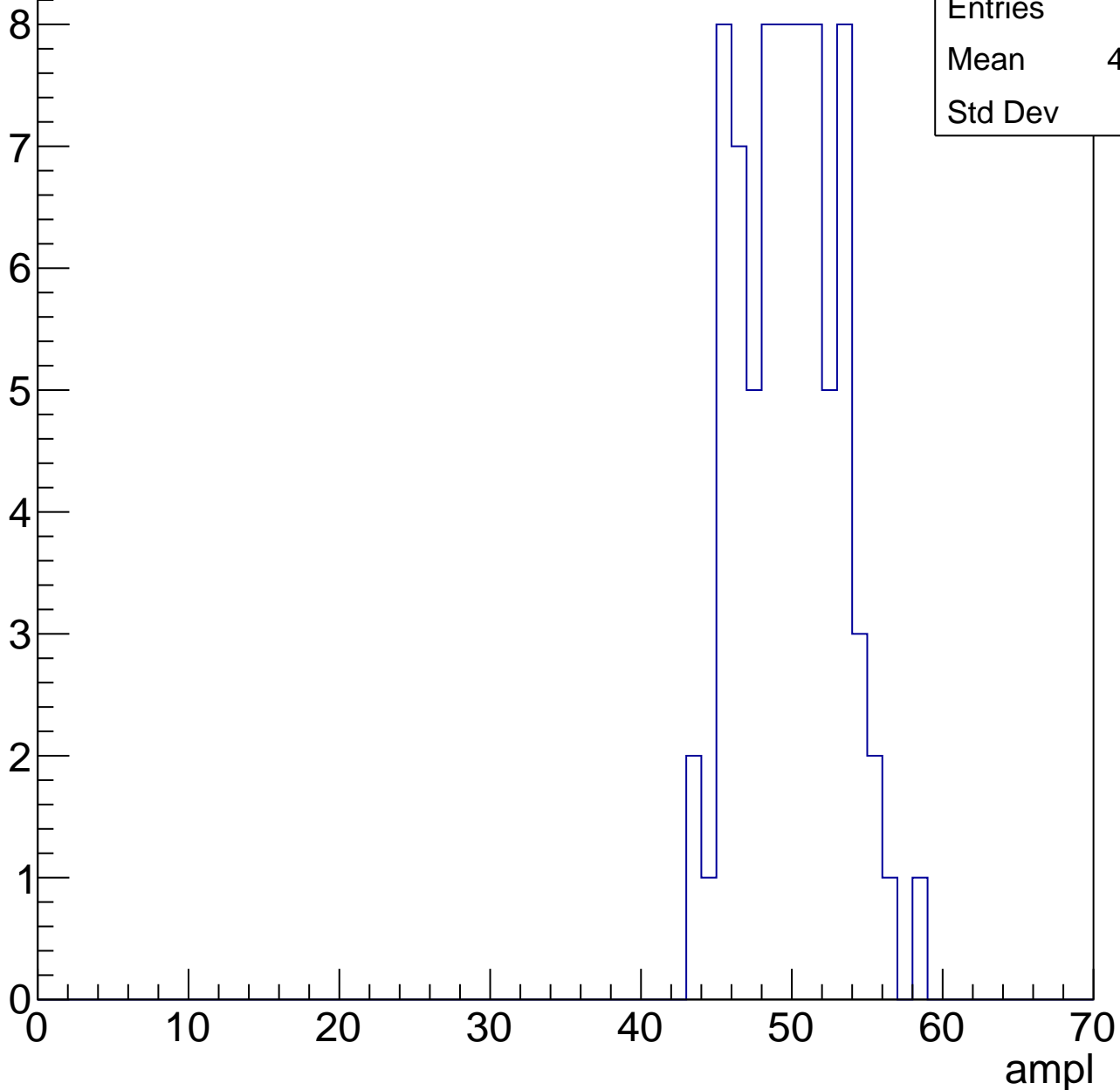


# B0L002S, U23-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	49.35
Std Dev	3.26

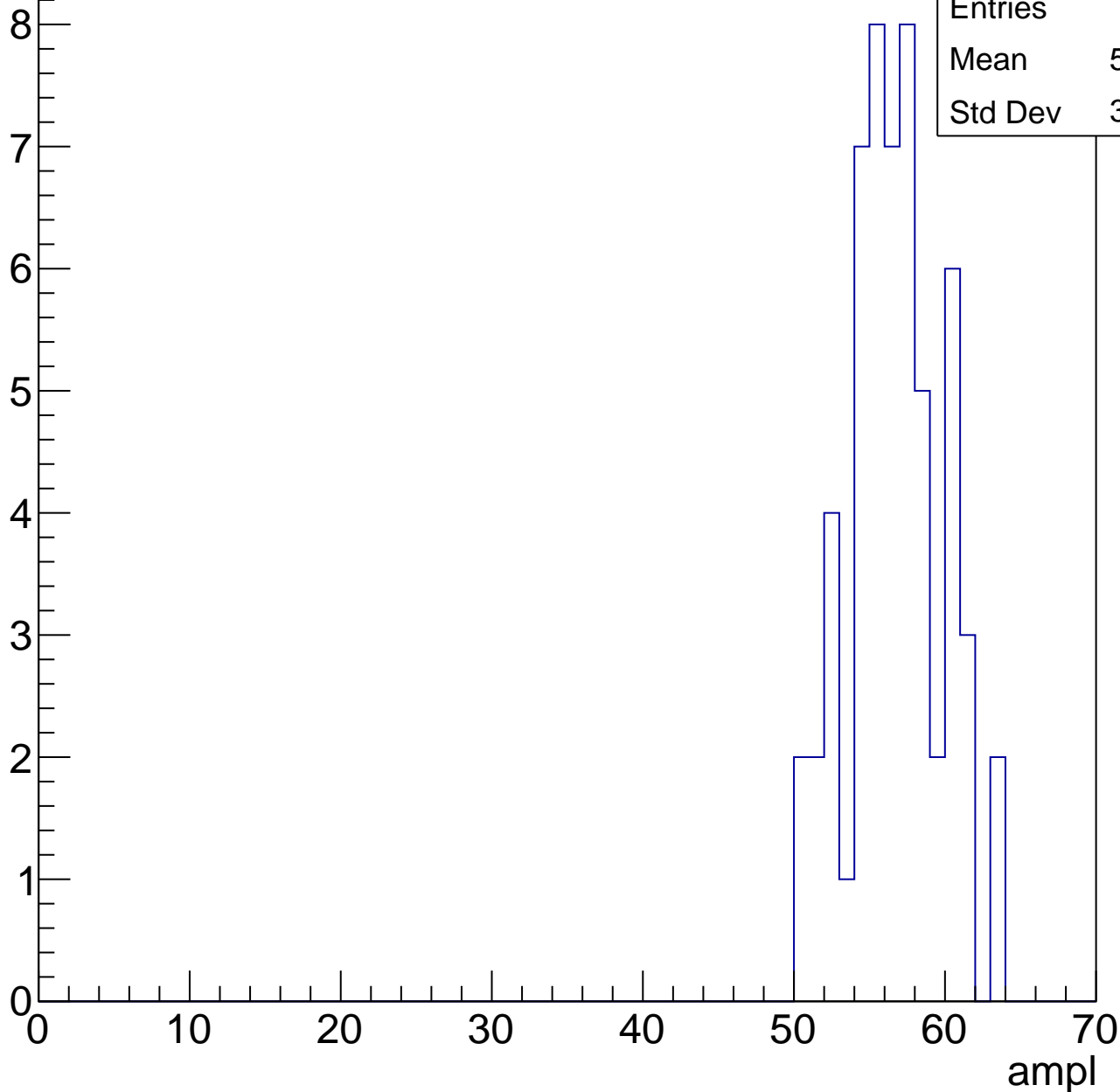


# B0L002S, U23-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

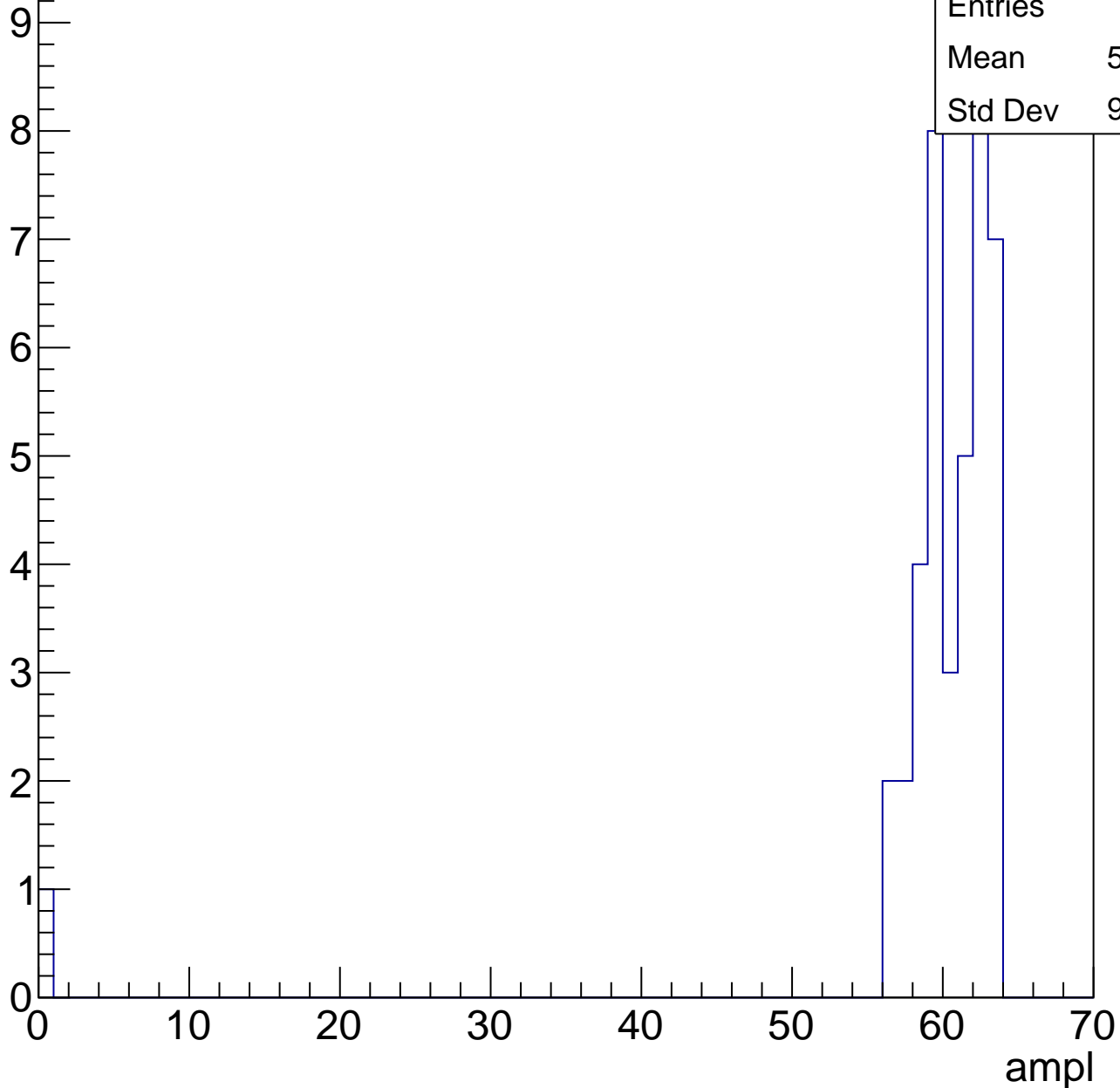
Entries	57
Mean	56.25
Std Dev	3.085



# B0L002S, U23-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

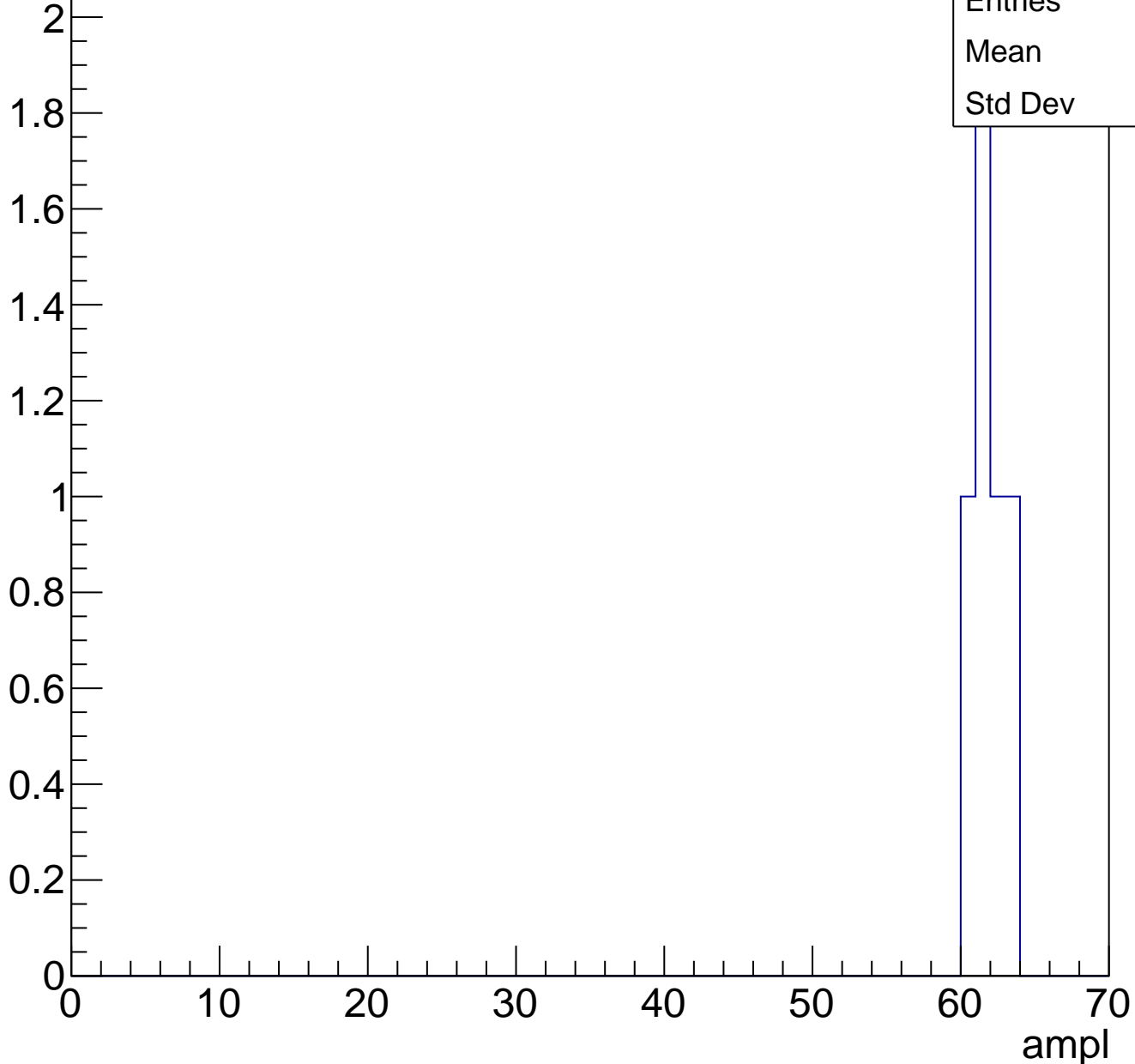
Entry



# B0L002S, U23-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L002S, U23-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	62
Mean	30.42
Std Dev	3.046

**Gaus mean : 30.4704**

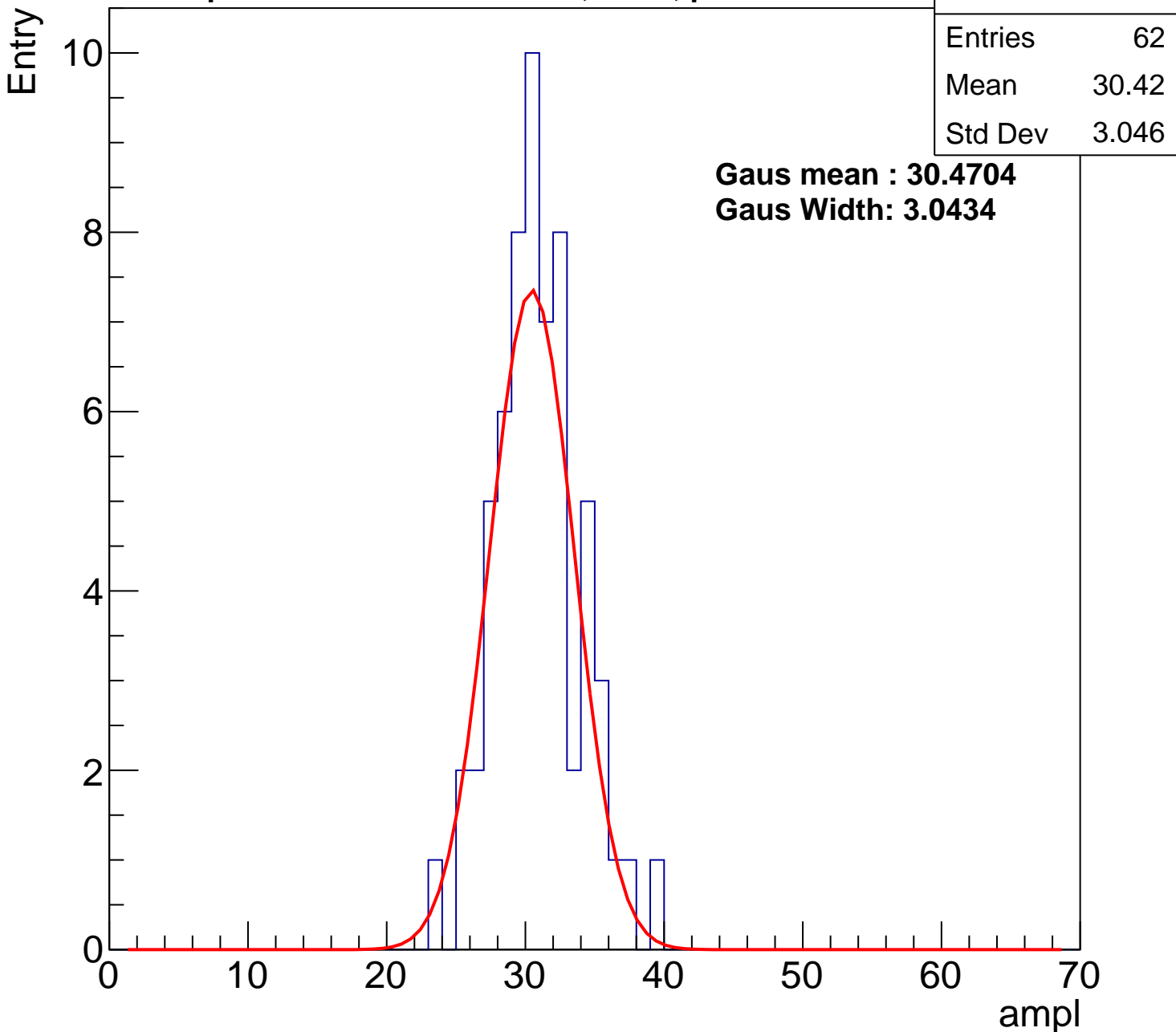
**Gaus Width: 3.0434**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch47, adc1

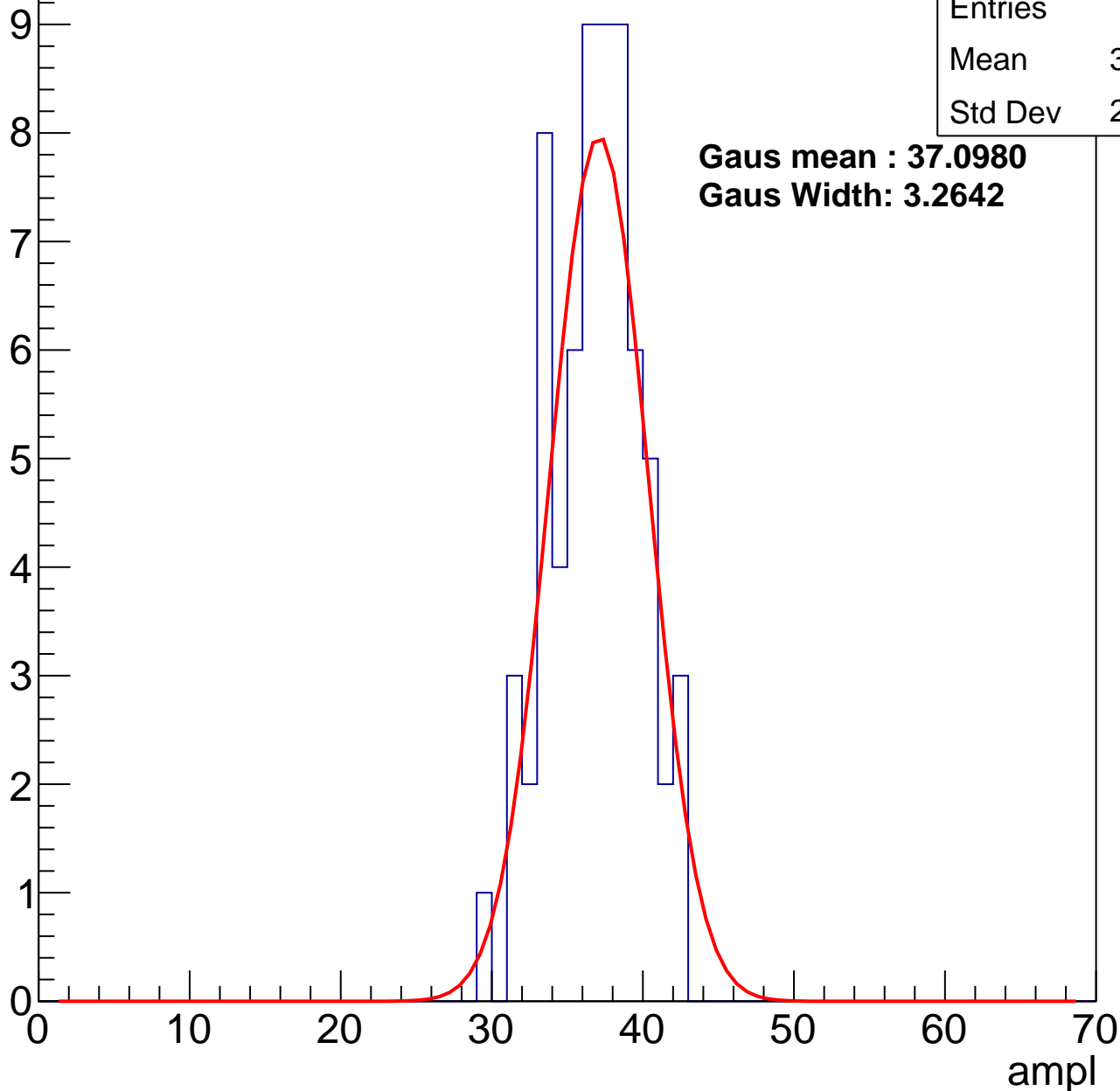
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.37
Std Dev	2.936

**Gaus mean : 37.0980**

**Gaus Width: 3.2642**



# B0L002S, U23-ch47, adc2

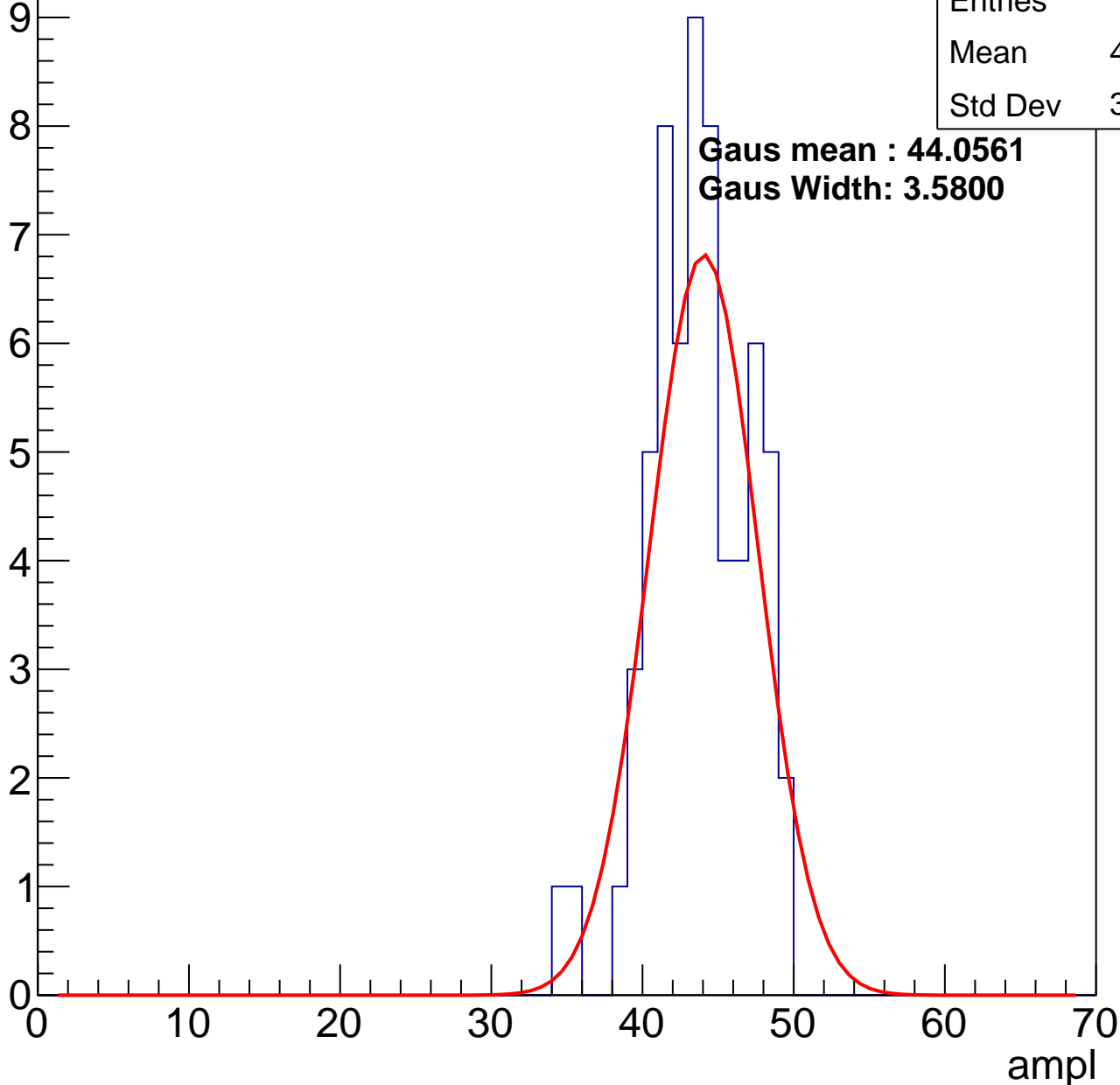
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.29
Std Dev	3.209

**Gaus mean : 44.0561**

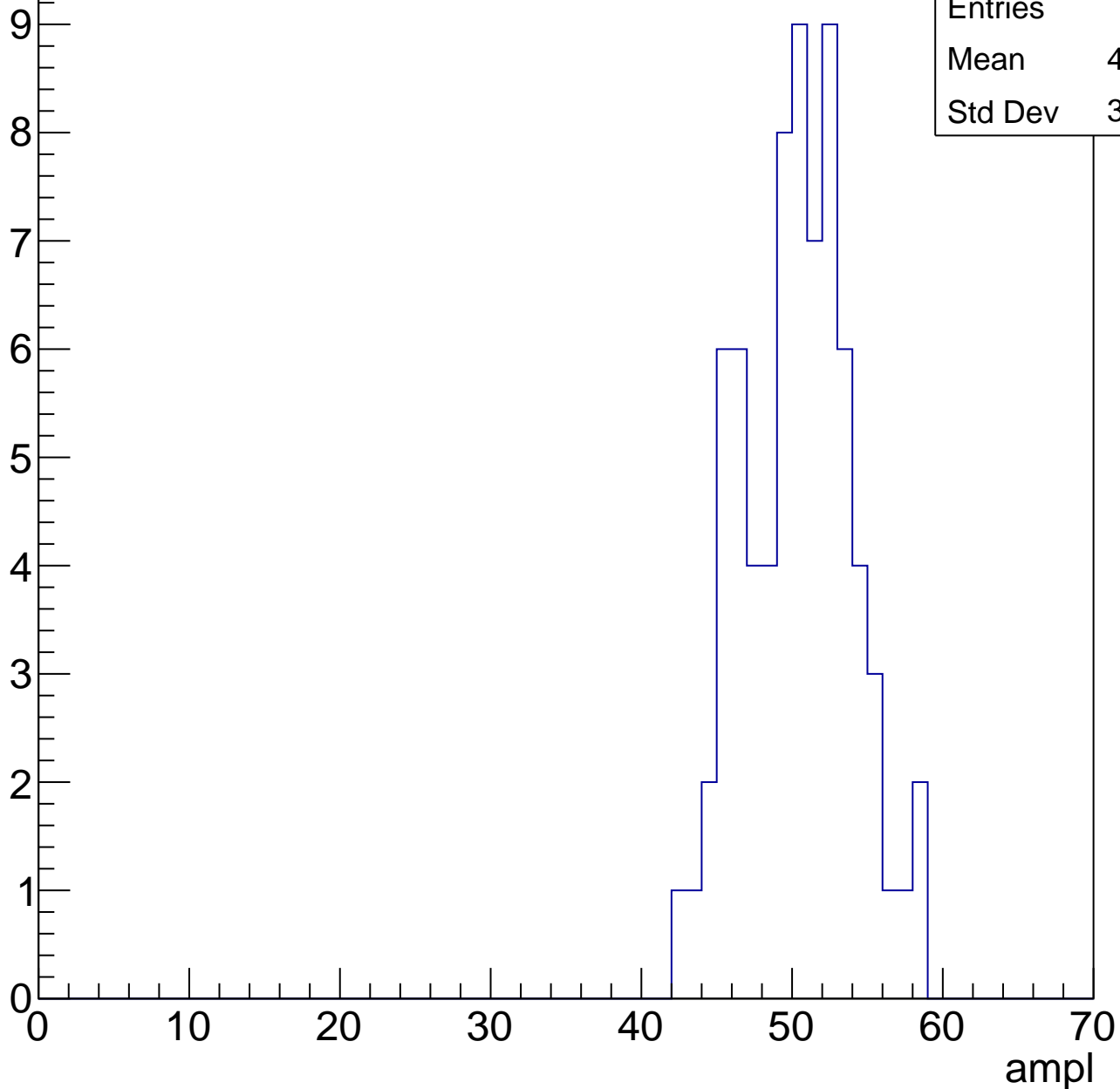
**Gaus Width: 3.5800**



# B0L002S, U23-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

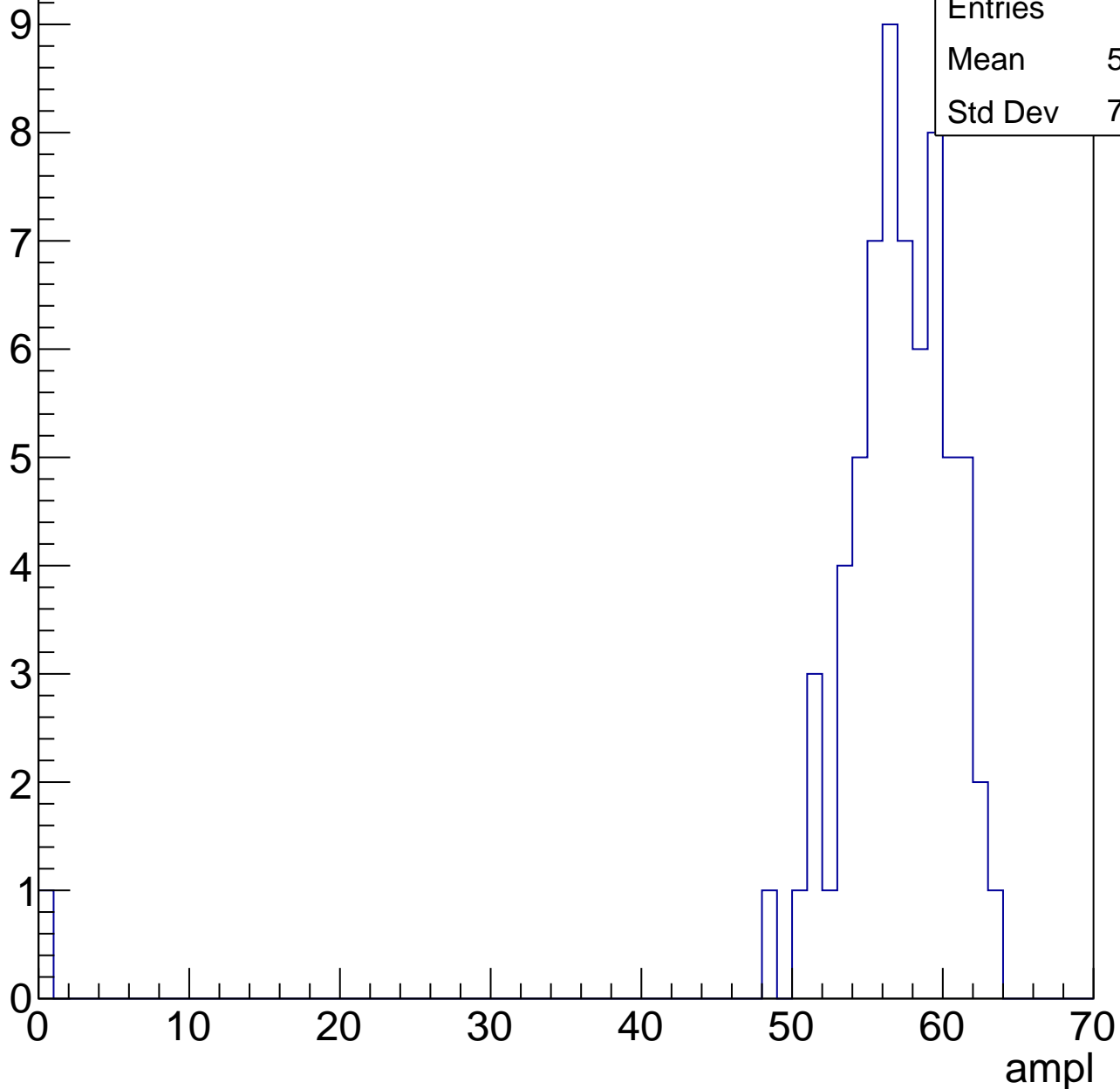
Entry



# B0L002S, U23-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



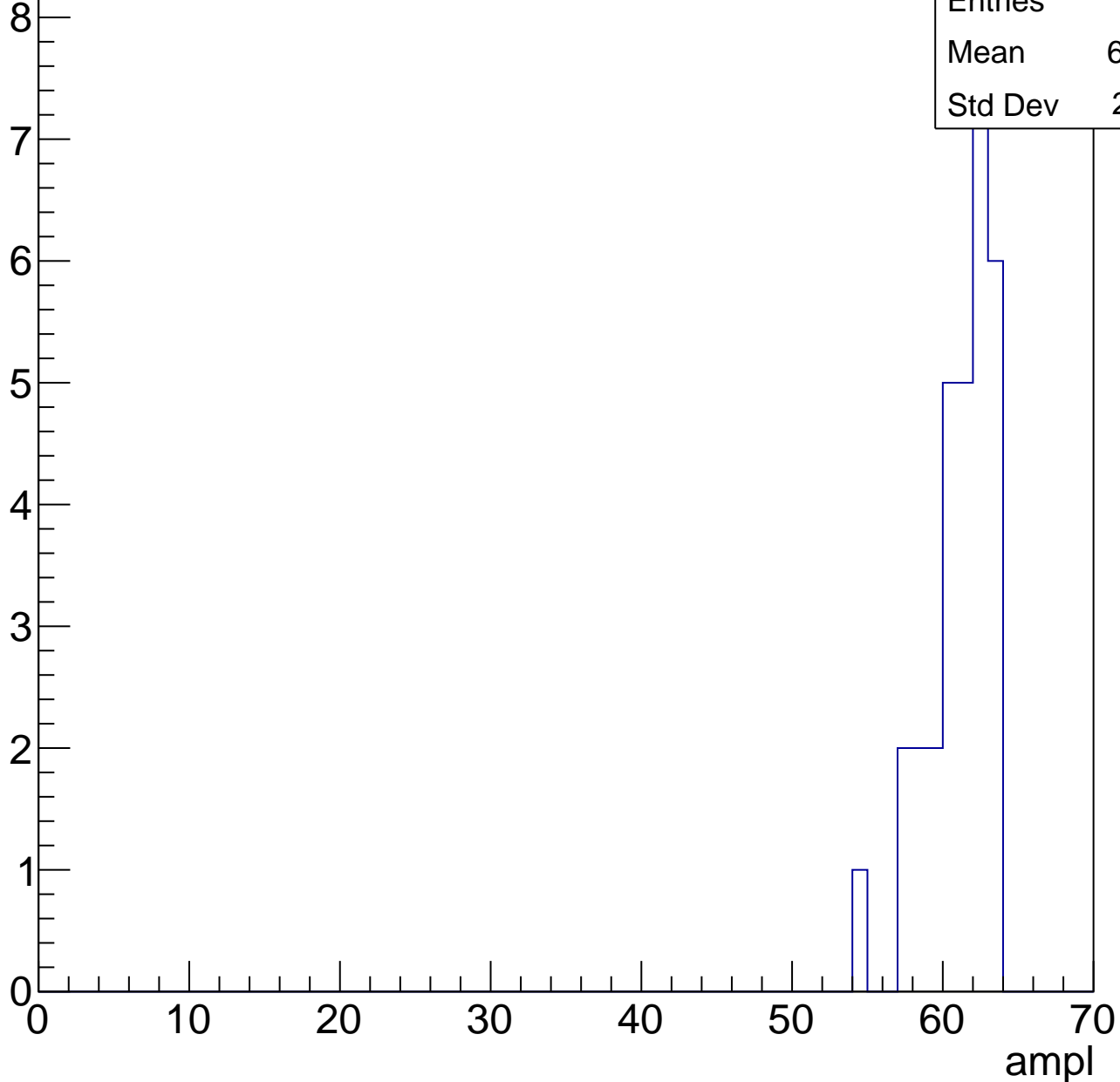
Entries	66
Mean	55.83
Std Dev	7.605

# B0L002S, U23-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	31
Mean	60.68
Std Dev	2.131



# B0L002S, U23-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L002S, U23-ch48, adc0

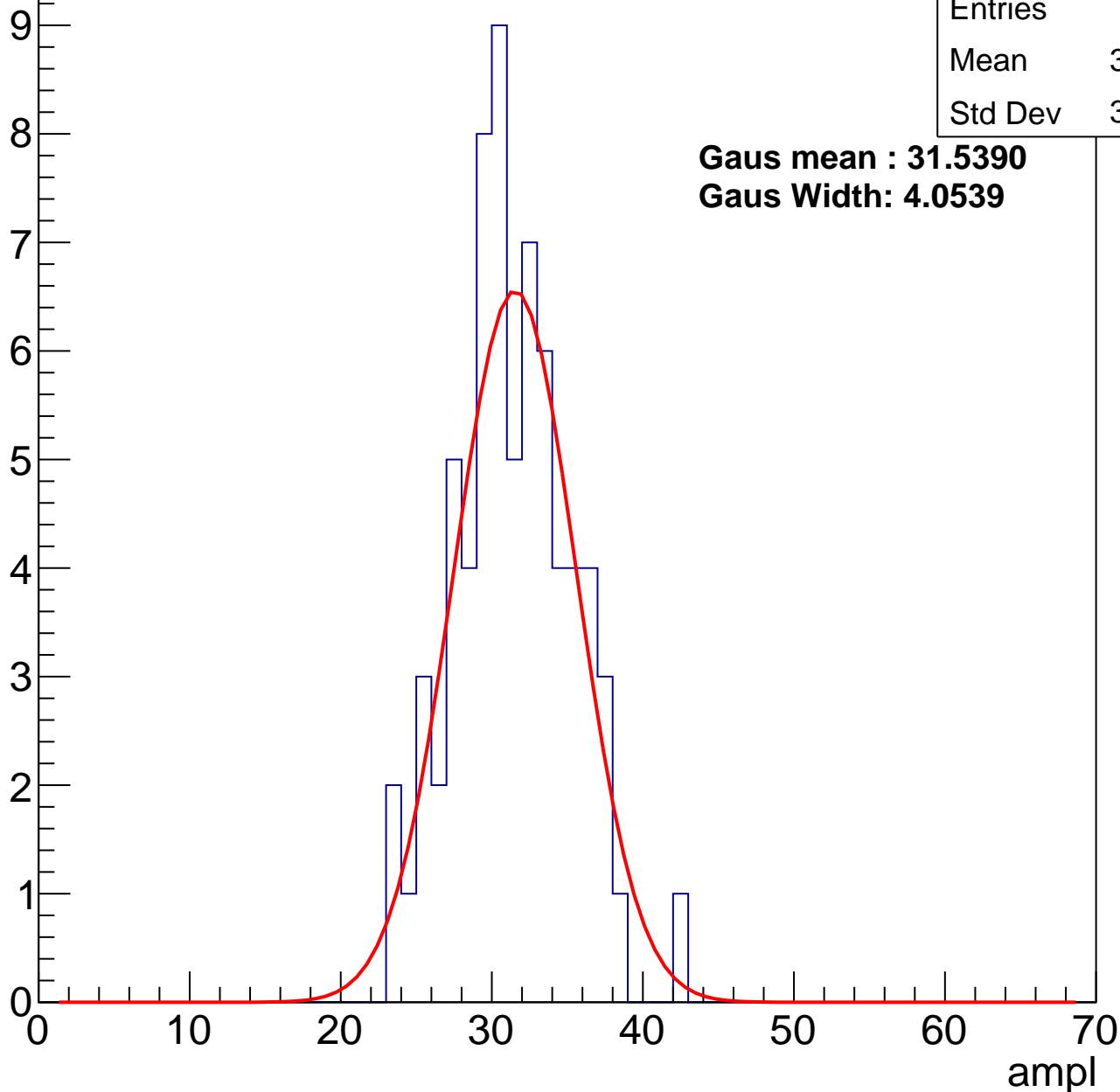
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.93
Std Dev	3.808

**Gaus mean : 31.5390**

**Gaus Width: 4.0539**



# B0L002S, U23-ch48, adc1

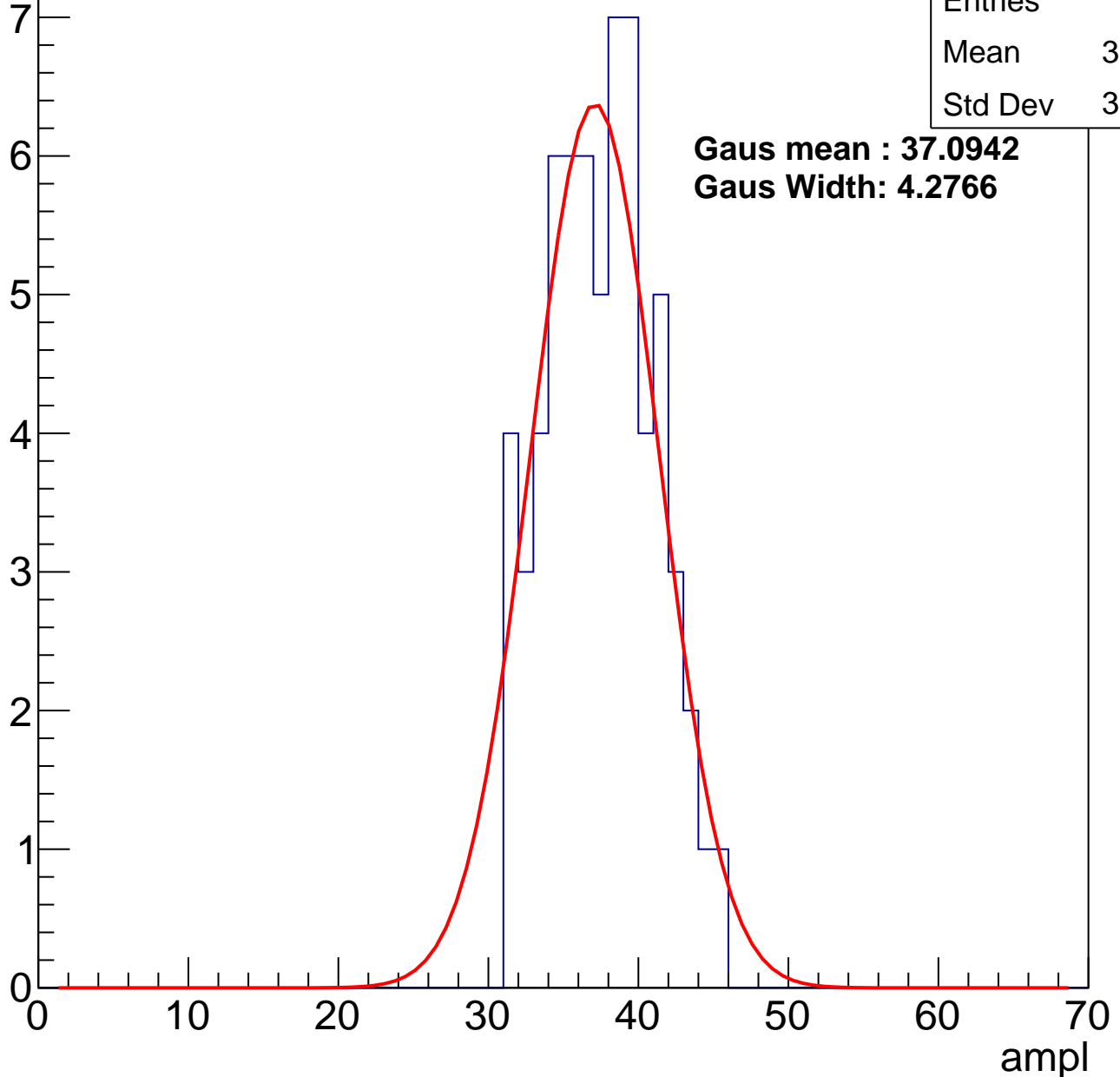
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.06
Std Dev	3.486

**Gaus mean : 37.0942**

**Gaus Width: 4.2766**



# B0L002S, U23-ch48, adc2

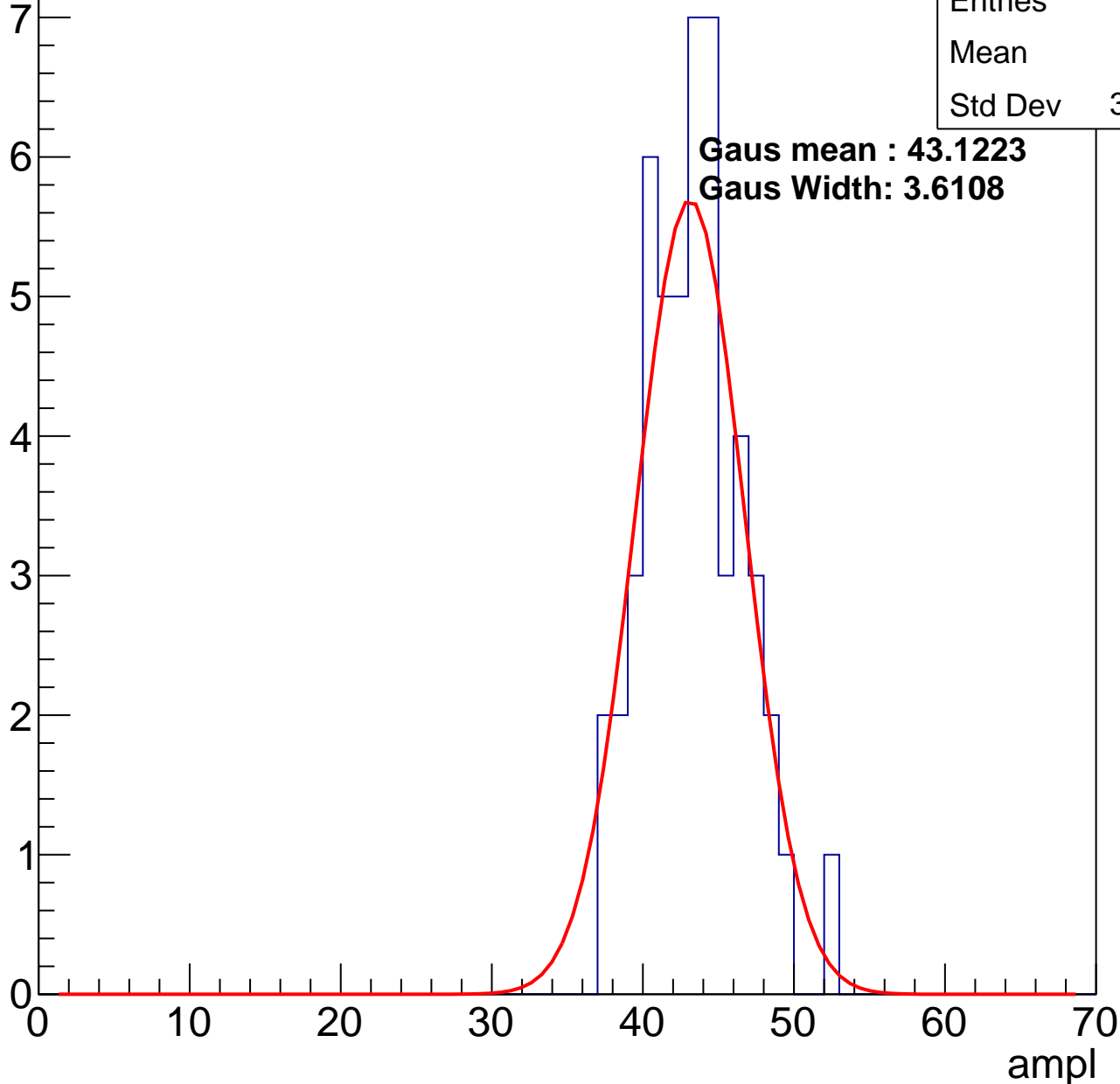
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	42.9
Std Dev	3.189

**Gaus mean : 43.1223**

**Gaus Width: 3.6108**



# B0L002S, U23-ch48, adc3

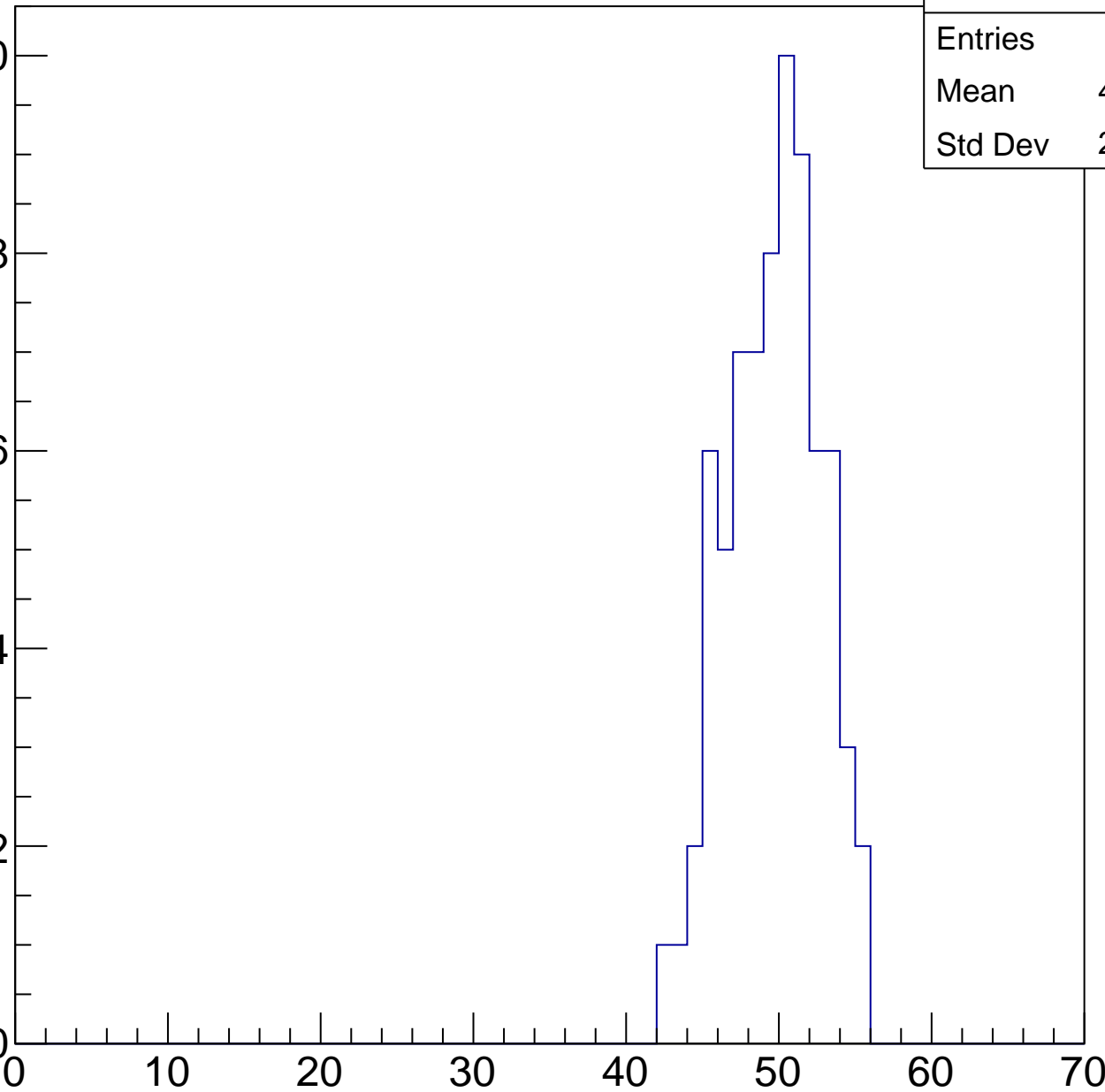
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	49.19
Std Dev	2.987

Entry

10  
8  
6  
4  
2  
0

ampl

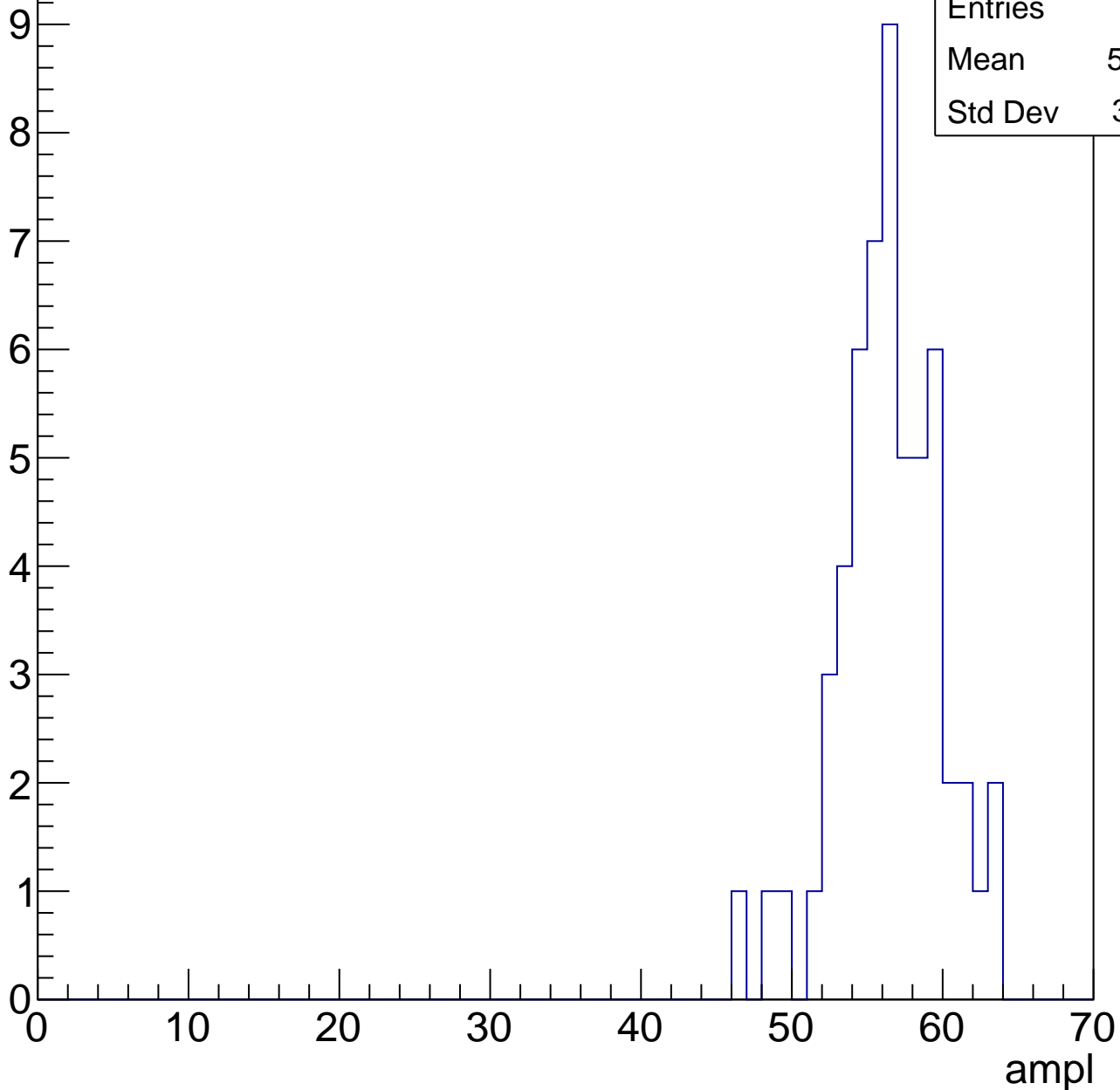


# B0L002S, U23-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

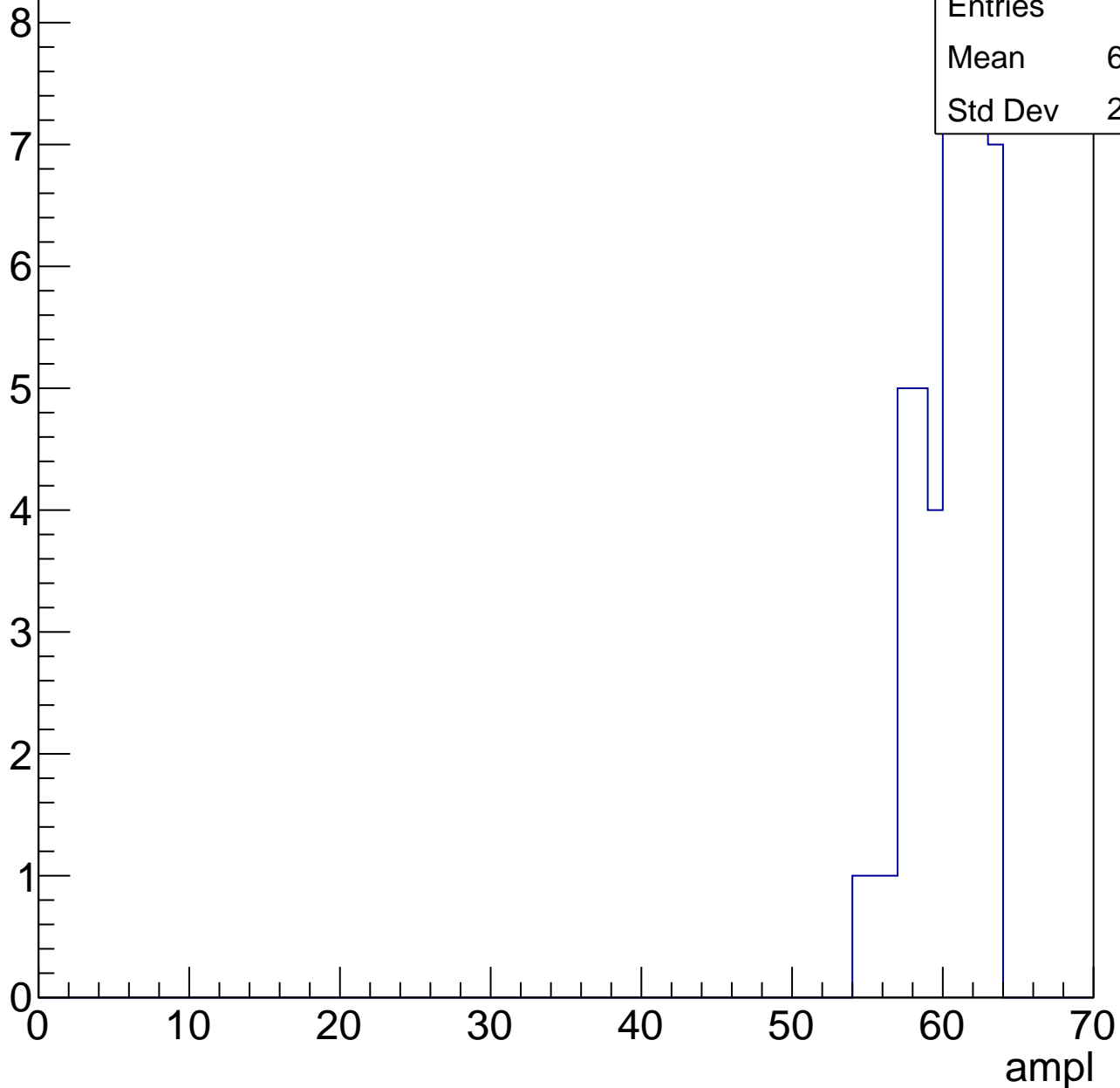
Entries	56
Mean	55.96
Std Dev	3.401



# B0L002S, U23-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

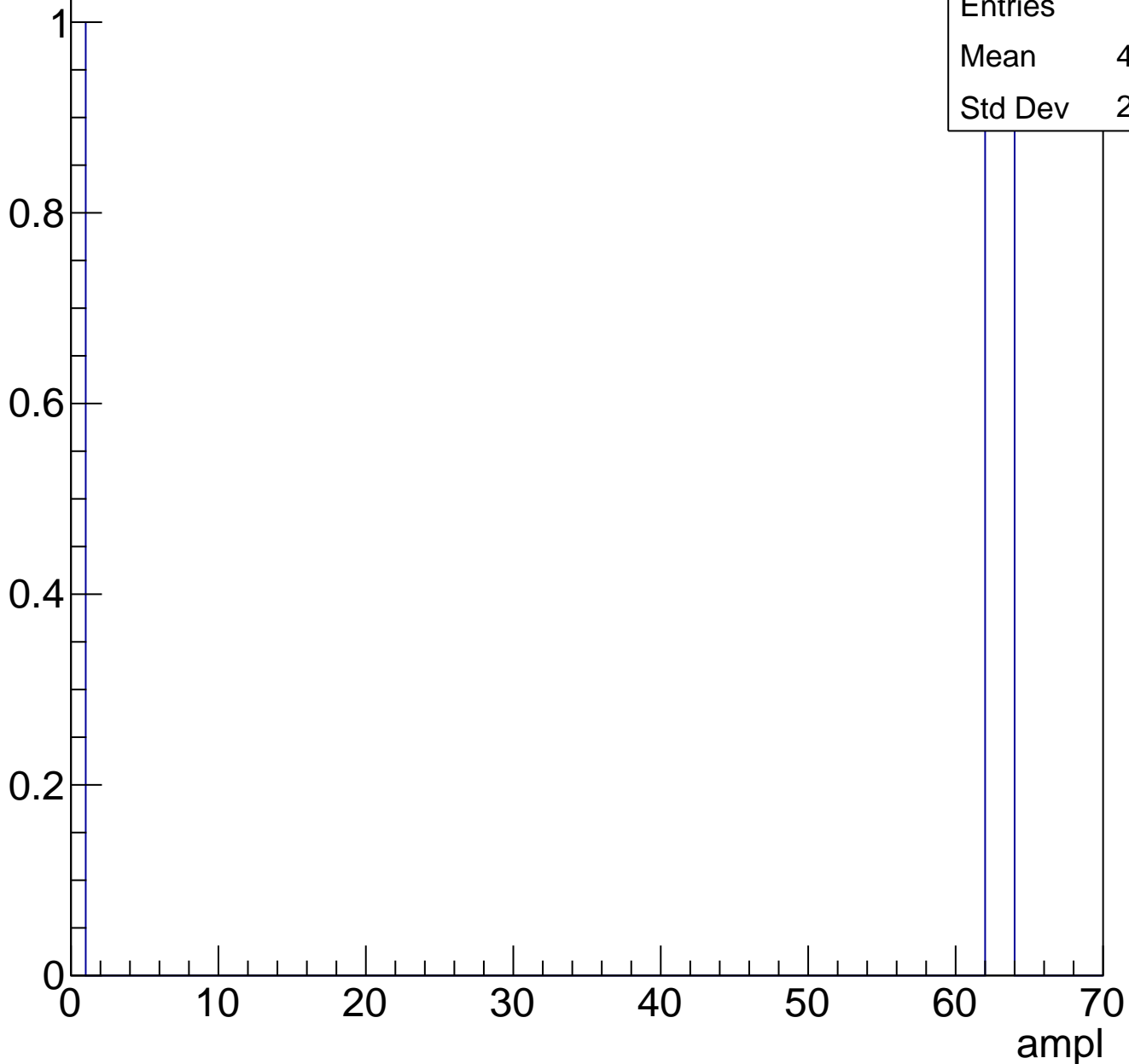
Entry



# B0L002S, U23-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch49, adc0

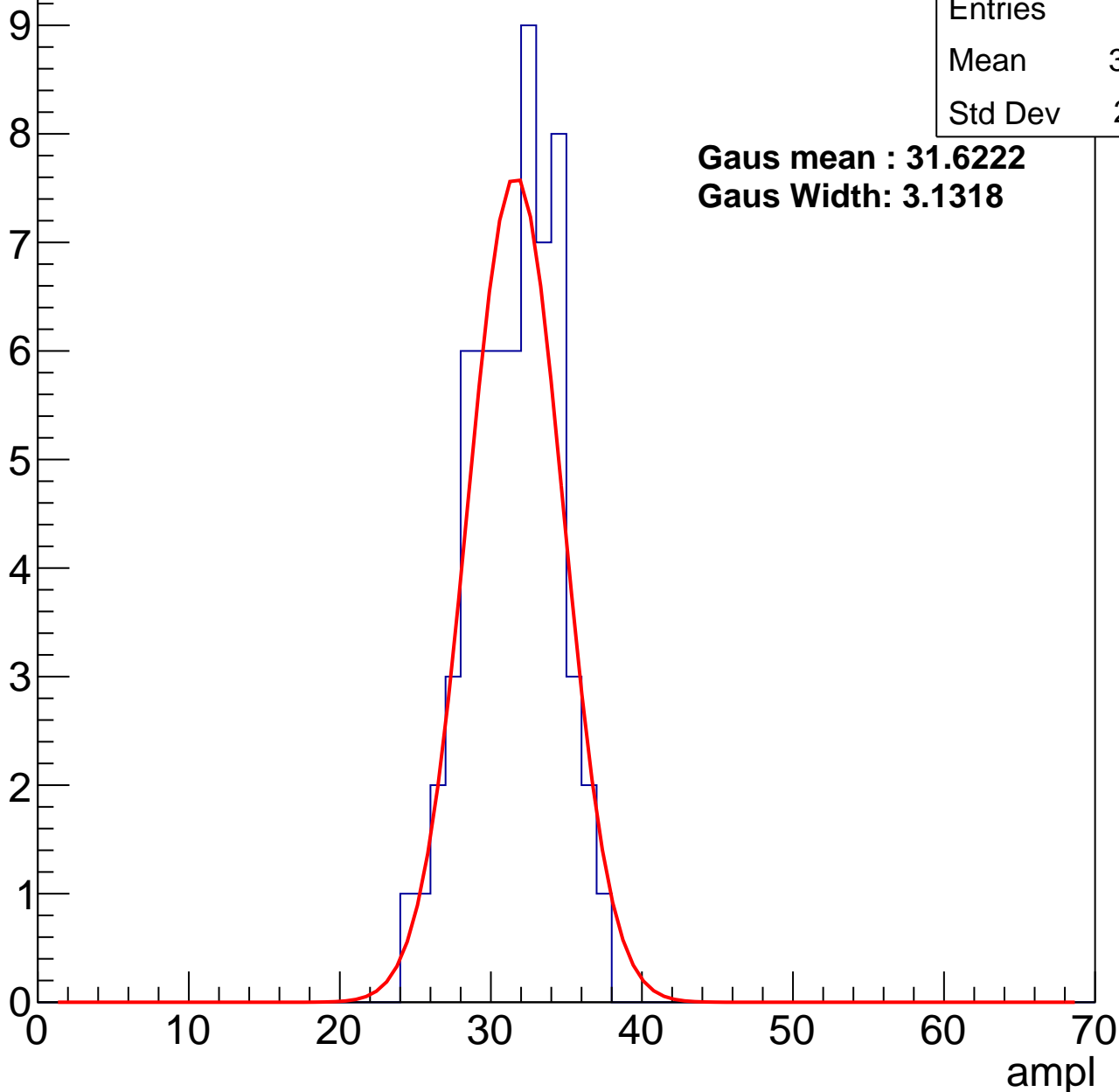
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	31.07
Std Dev	2.891

**Gaus mean : 31.6222**

**Gaus Width: 3.1318**



# B0L002S, U23-ch49, adc1

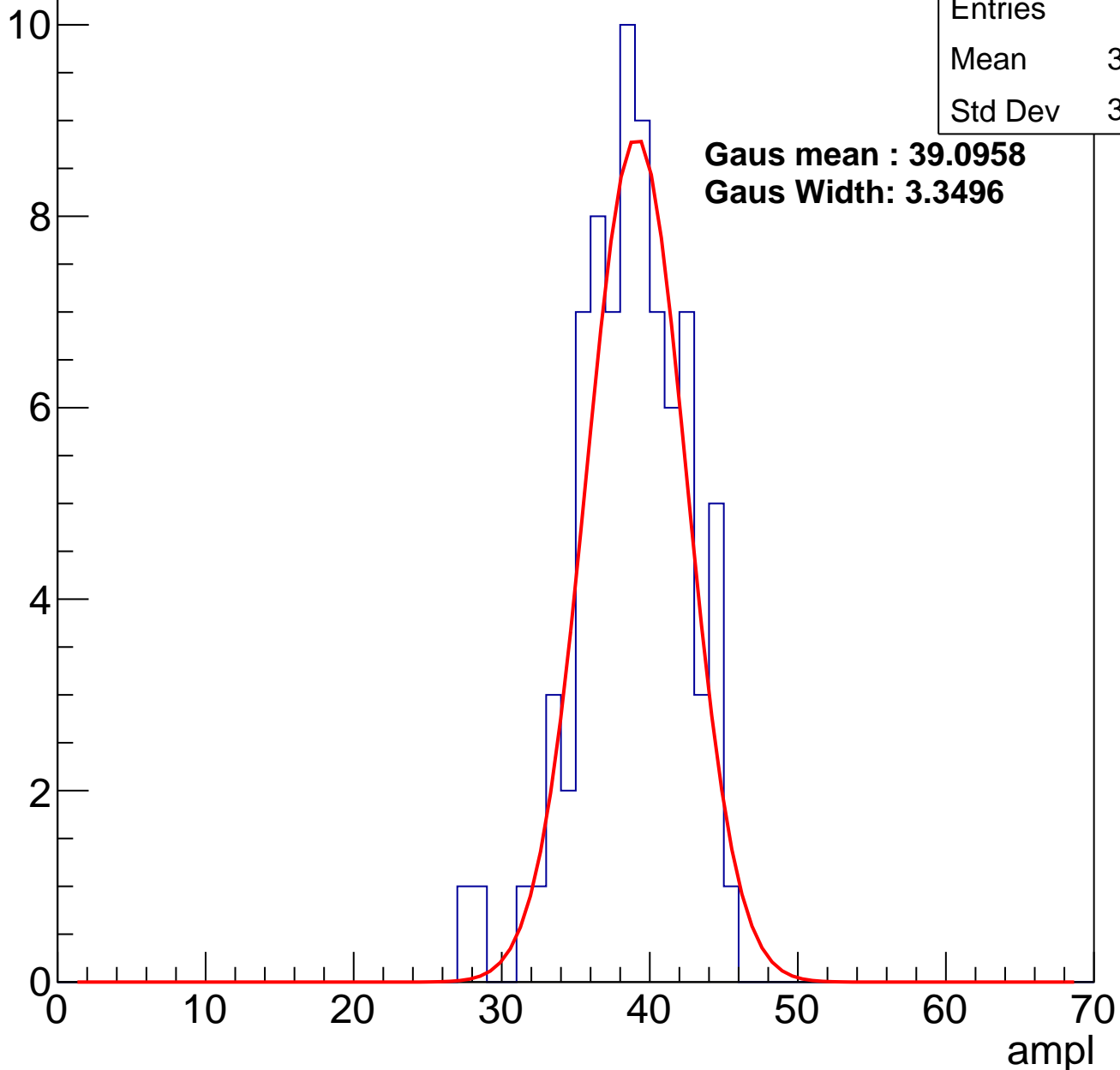
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	79
Mean	38.25
Std Dev	3.588

**Gaus mean : 39.0958**

**Gaus Width: 3.3496**

Entry



# B0L002S, U23-ch49, adc2

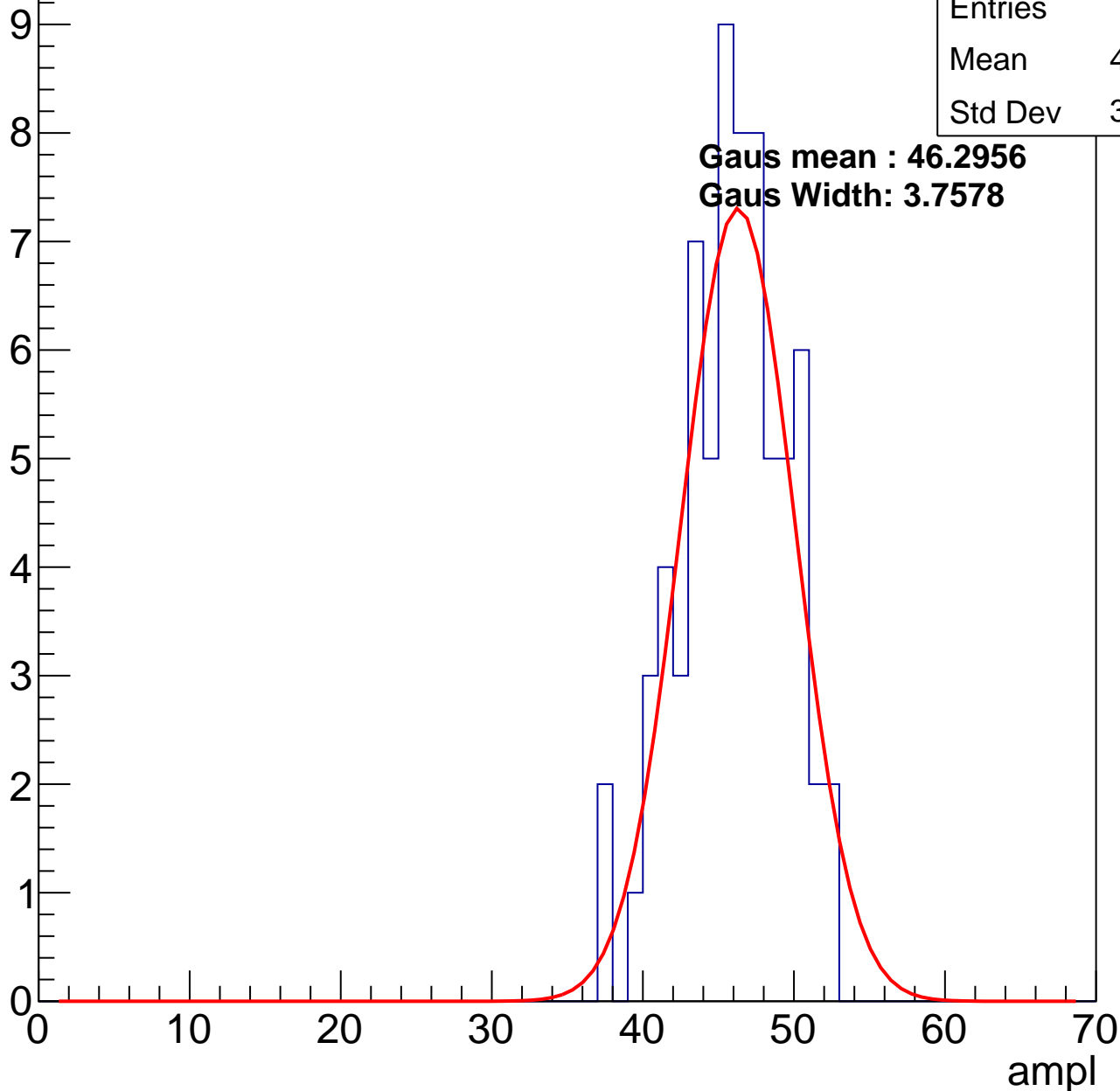
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	45.49
Std Dev	3.447

**Gaus mean : 46.2956**

**Gaus Width: 3.7578**

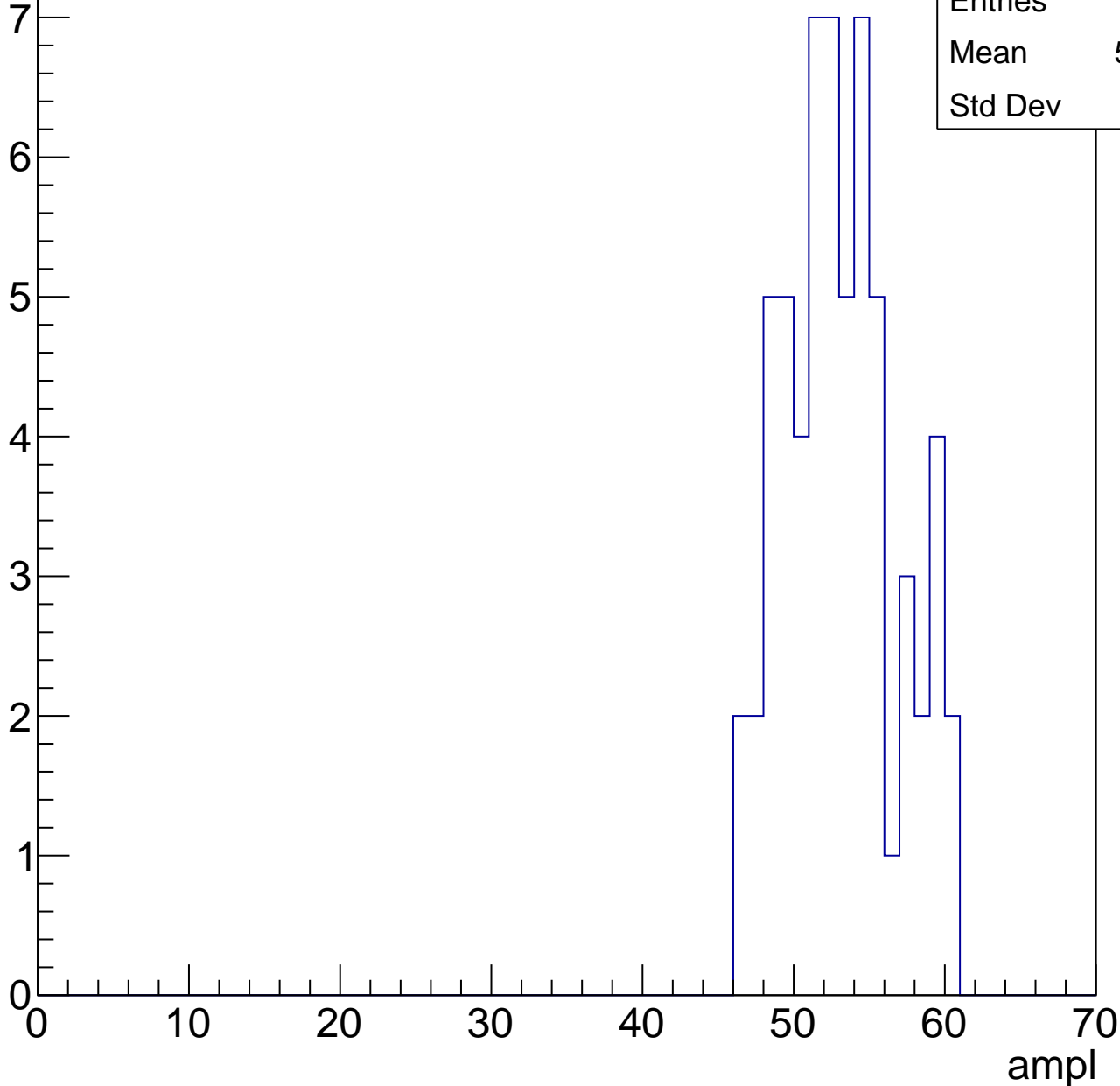


# B0L002S, U23-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

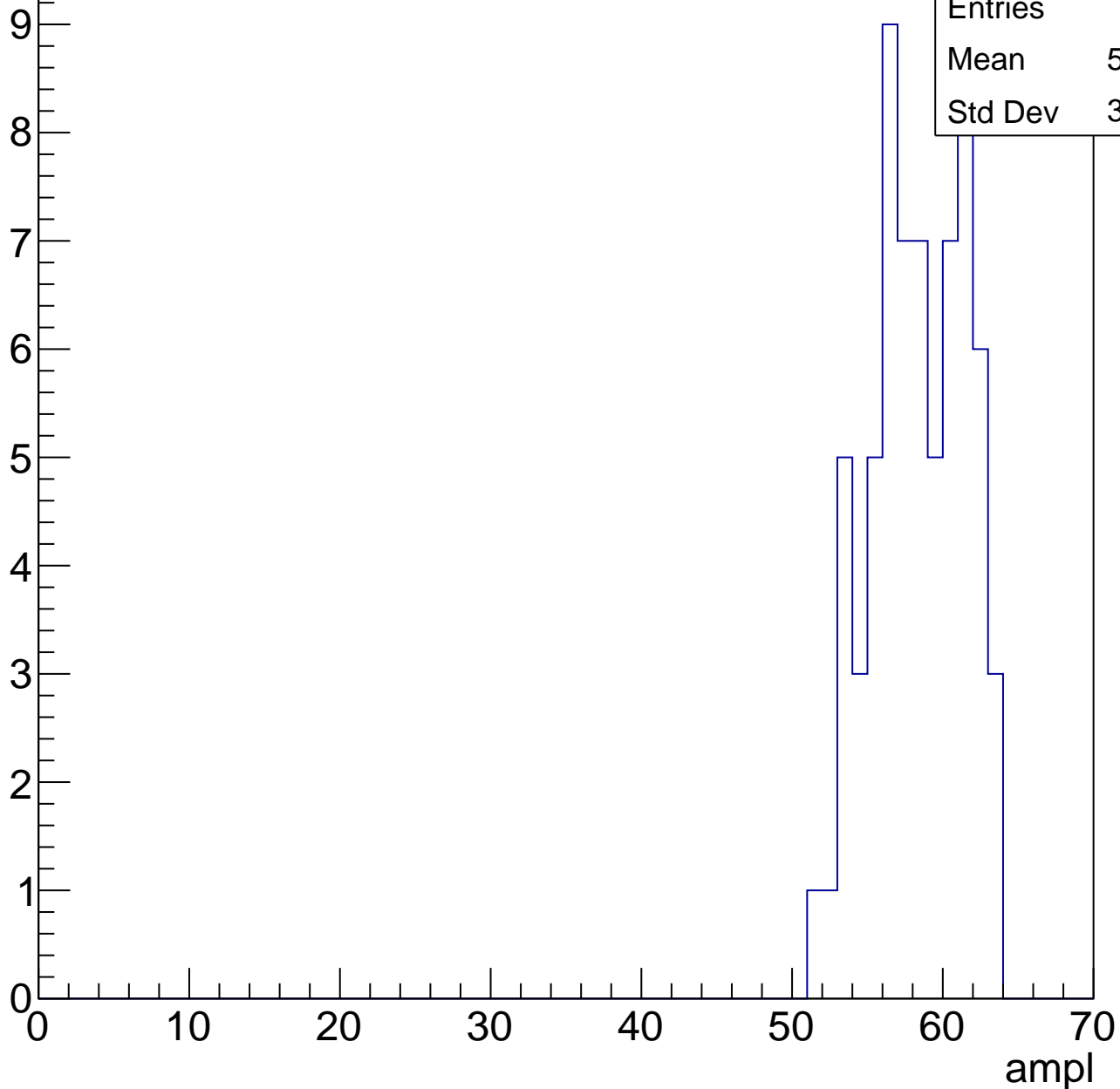
Entries	61
Mean	52.61
Std Dev	3.65



# B0L002S, U23-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



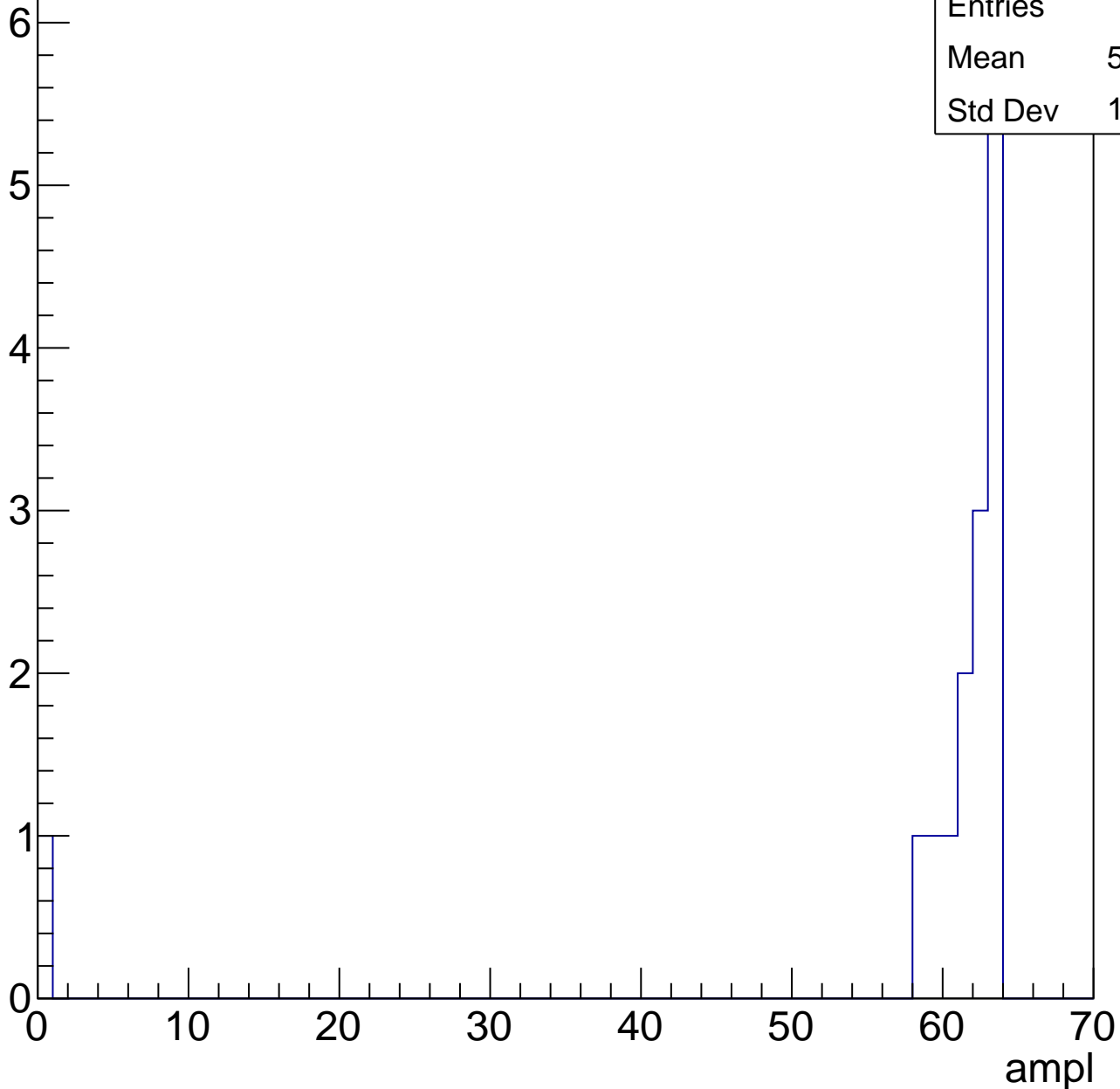
Entries	67
Mean	57.88
Std Dev	3.045

# B0L002S, U23-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	15
Mean	57.53
Std Dev	15.45



# B0L002S, U23-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch50, adc0

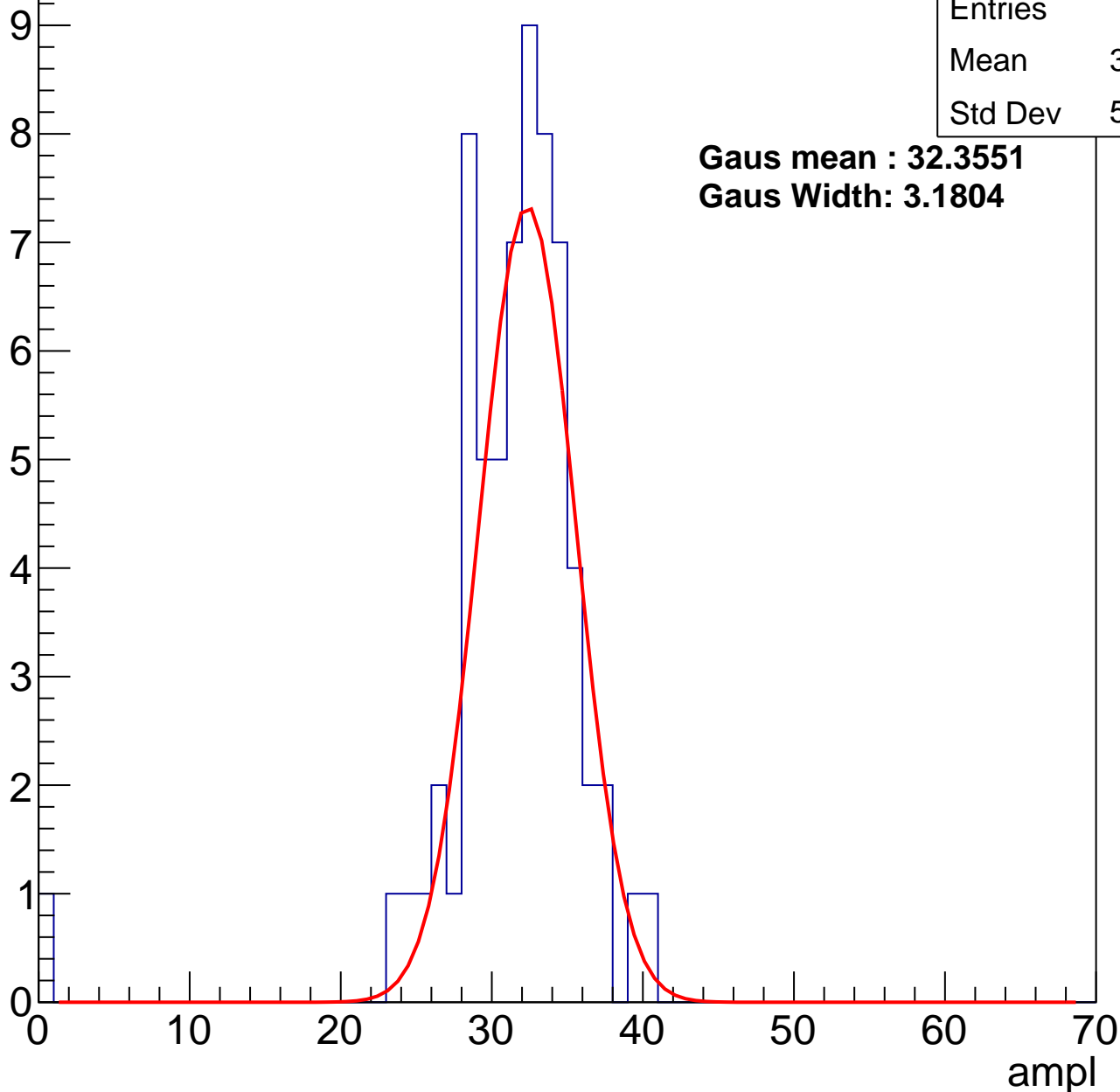
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.94
Std Dev	5.087

**Gaus mean : 32.3551**

**Gaus Width: 3.1804**



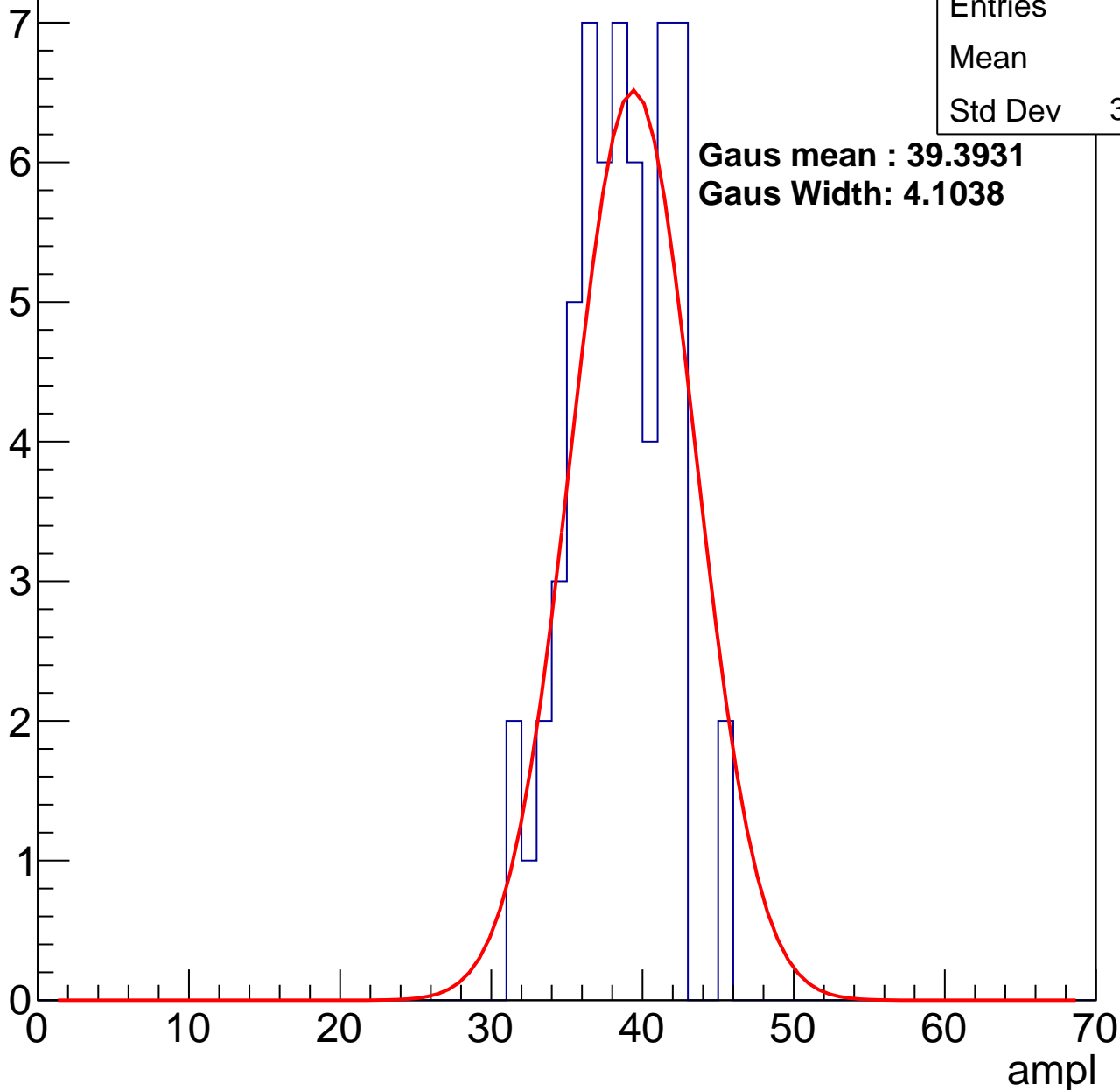
# B0L002S, U23-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38
Std Dev	3.205

**Gaus mean : 39.3931**  
**Gaus Width: 4.1038**



# B0L002S, U23-ch50, adc2

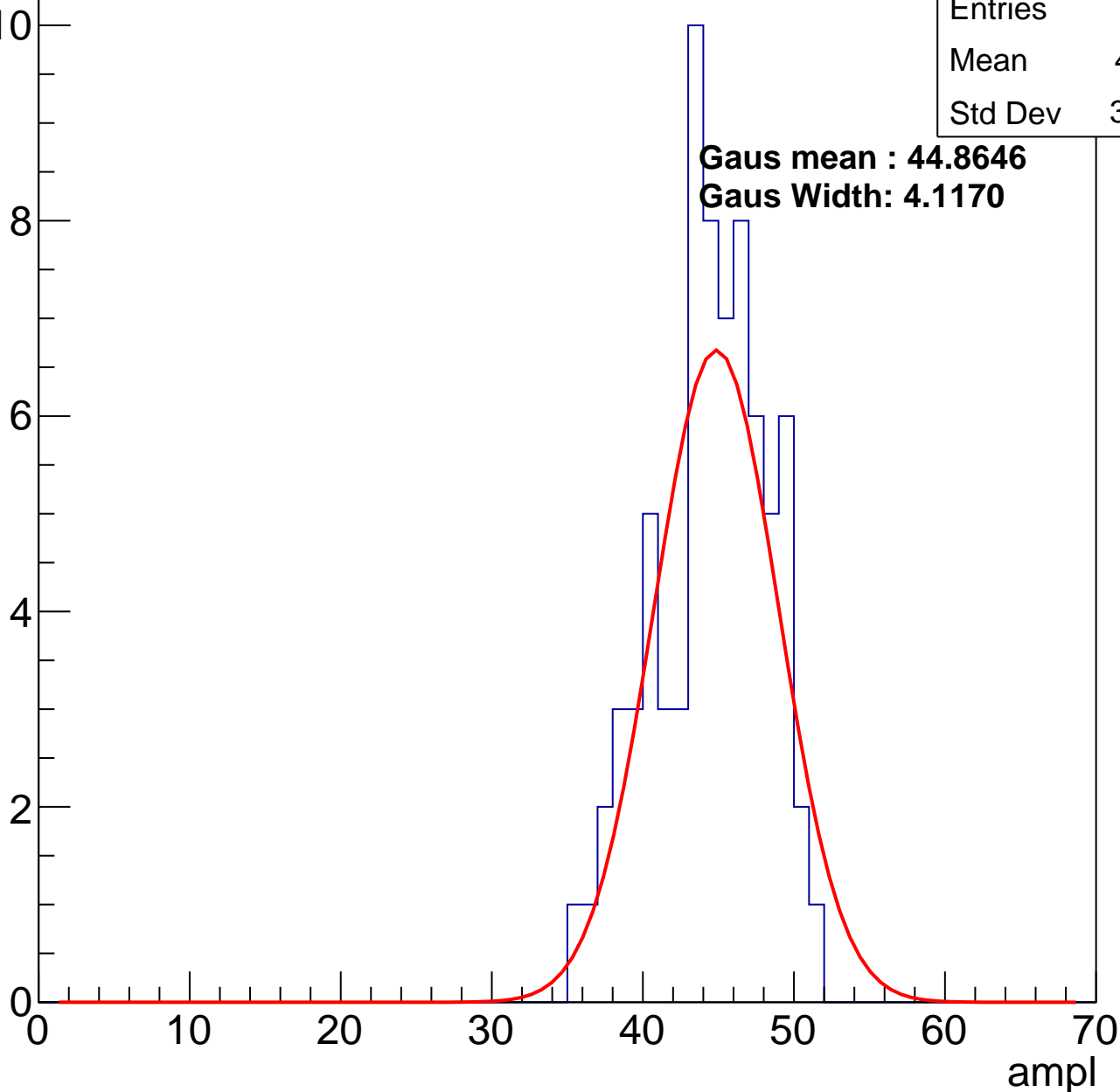
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	44.01
Std Dev	3.674

**Gaus mean : 44.8646**

**Gaus Width: 4.1170**

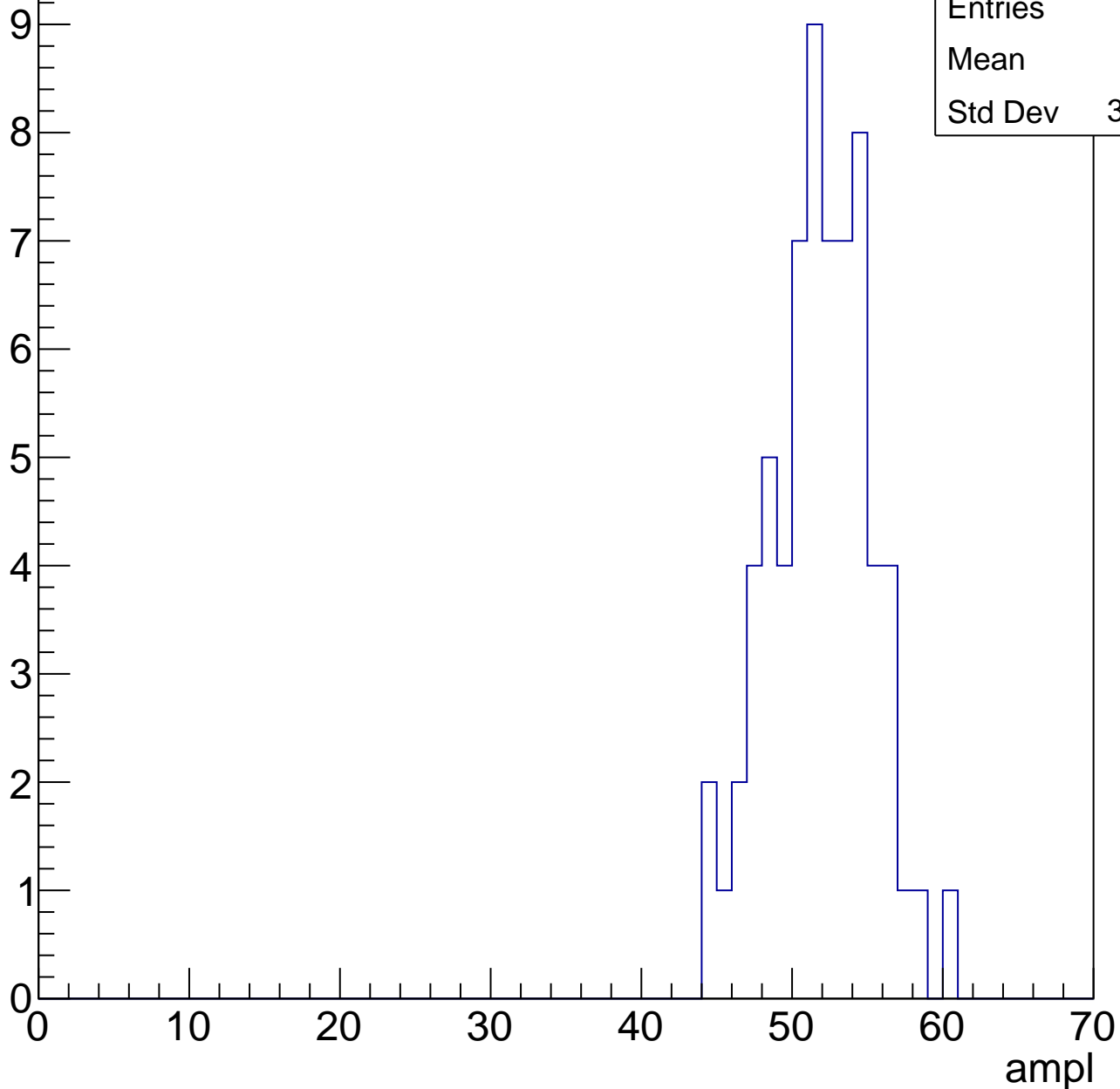


# B0L002S, U23-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	51.4
Std Dev	3.328

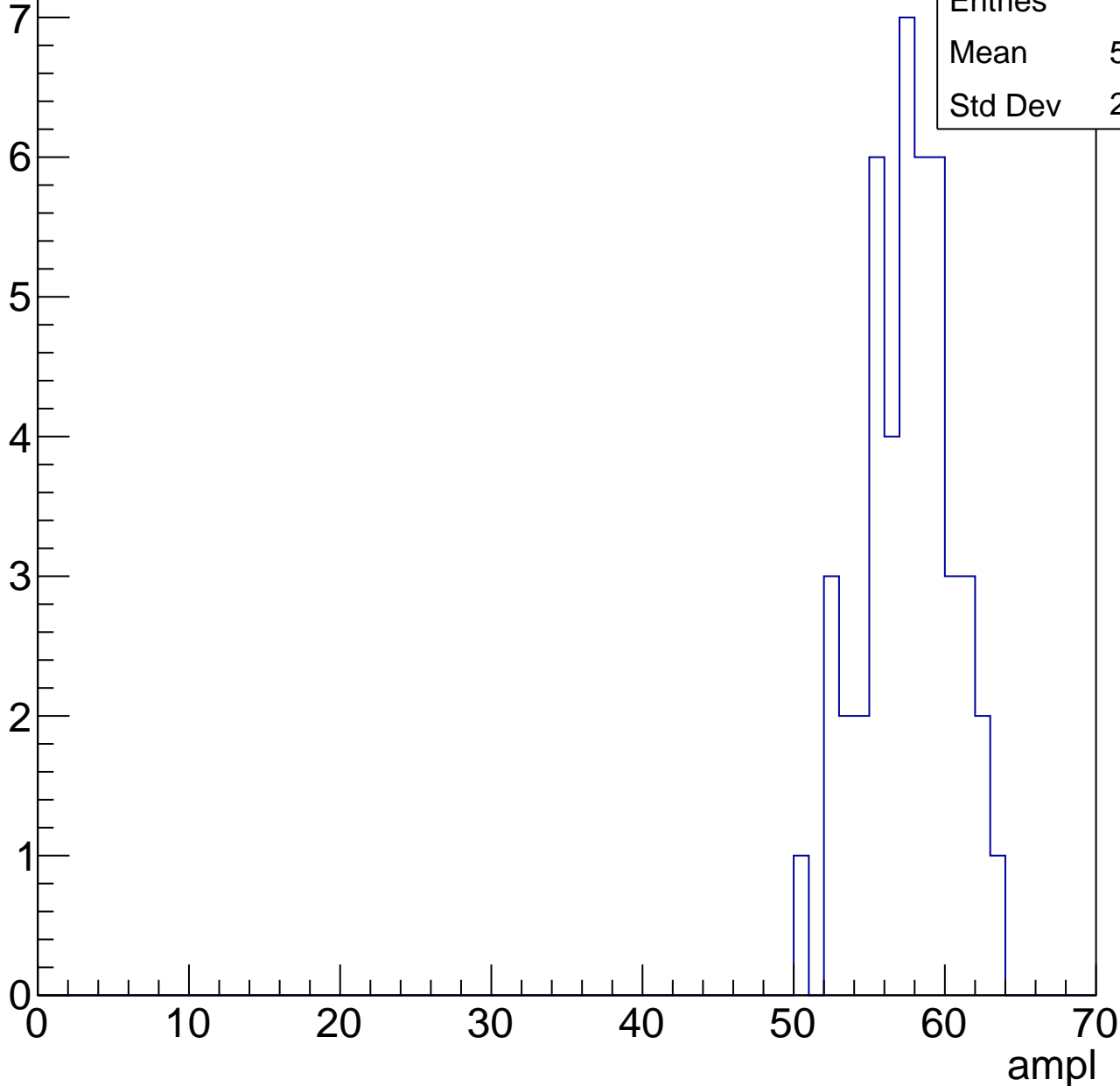


# B0L002S, U23-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	57.07
Std Dev	2.922

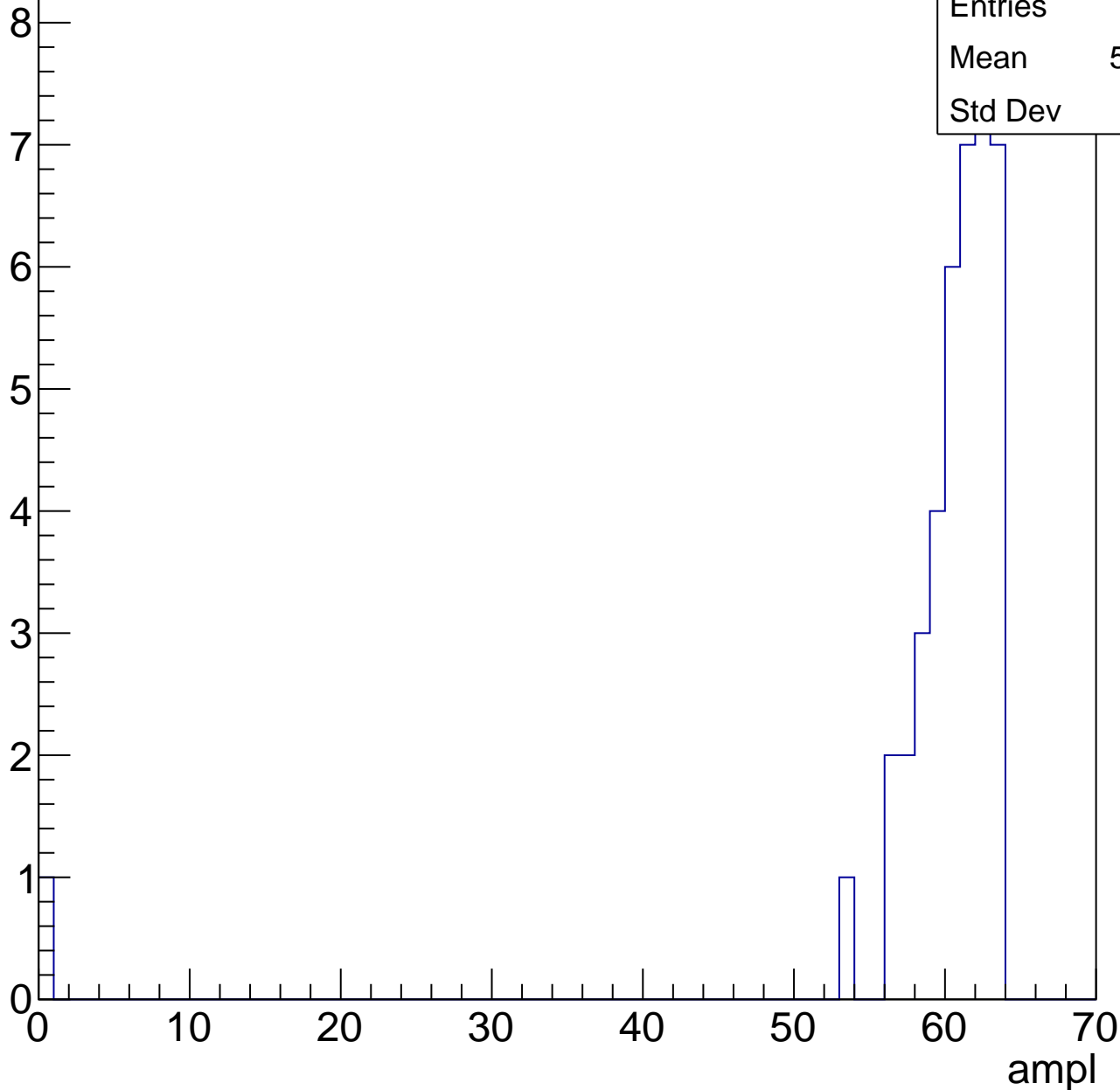


# B0L002S, U23-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.85
Std Dev	9.58



# B0L002S, U23-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

ampl



# B0L002S, U23-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch51, adc0

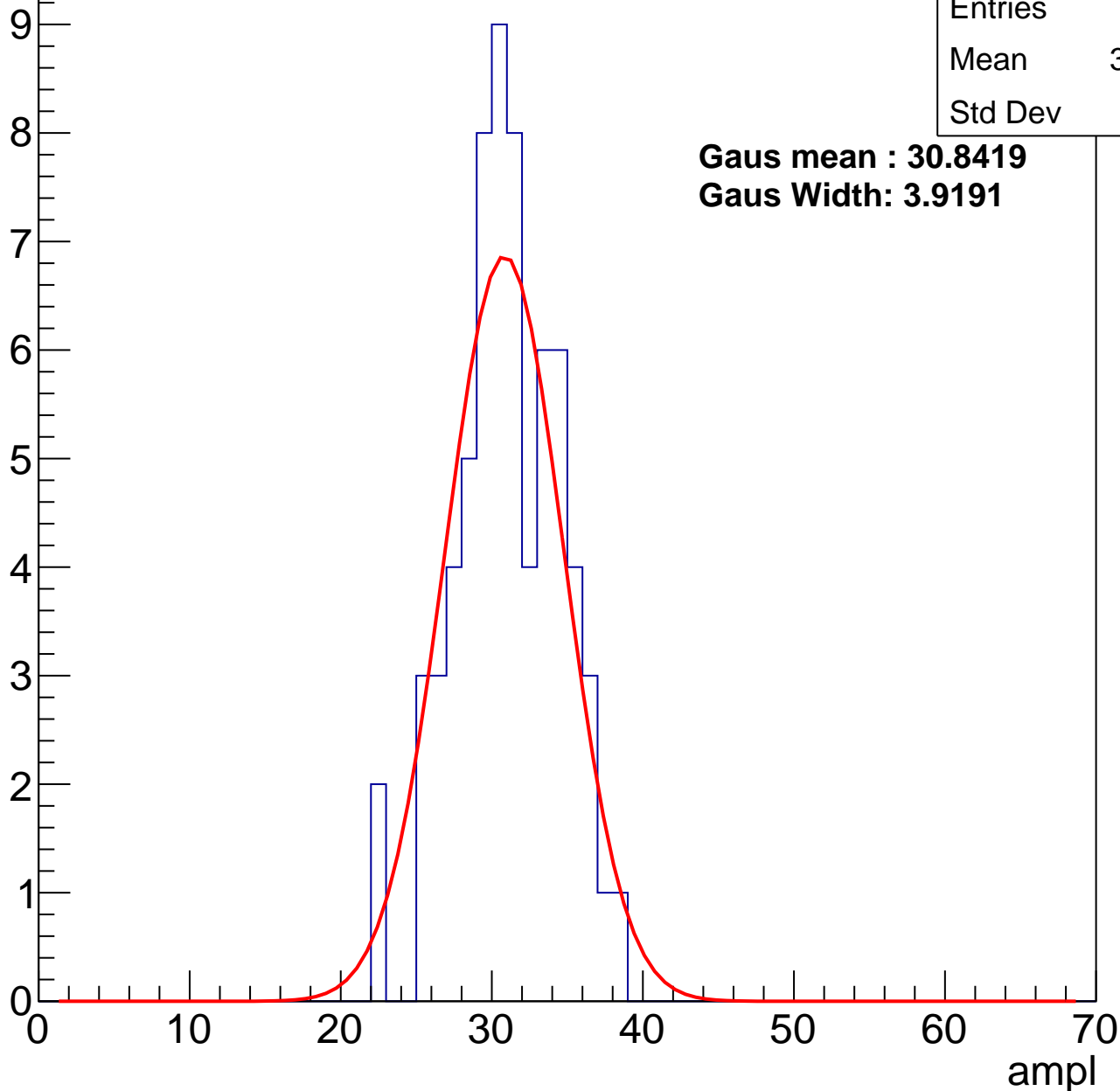
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.57
Std Dev	3.43

**Gaus mean : 30.8419**

**Gaus Width: 3.9191**



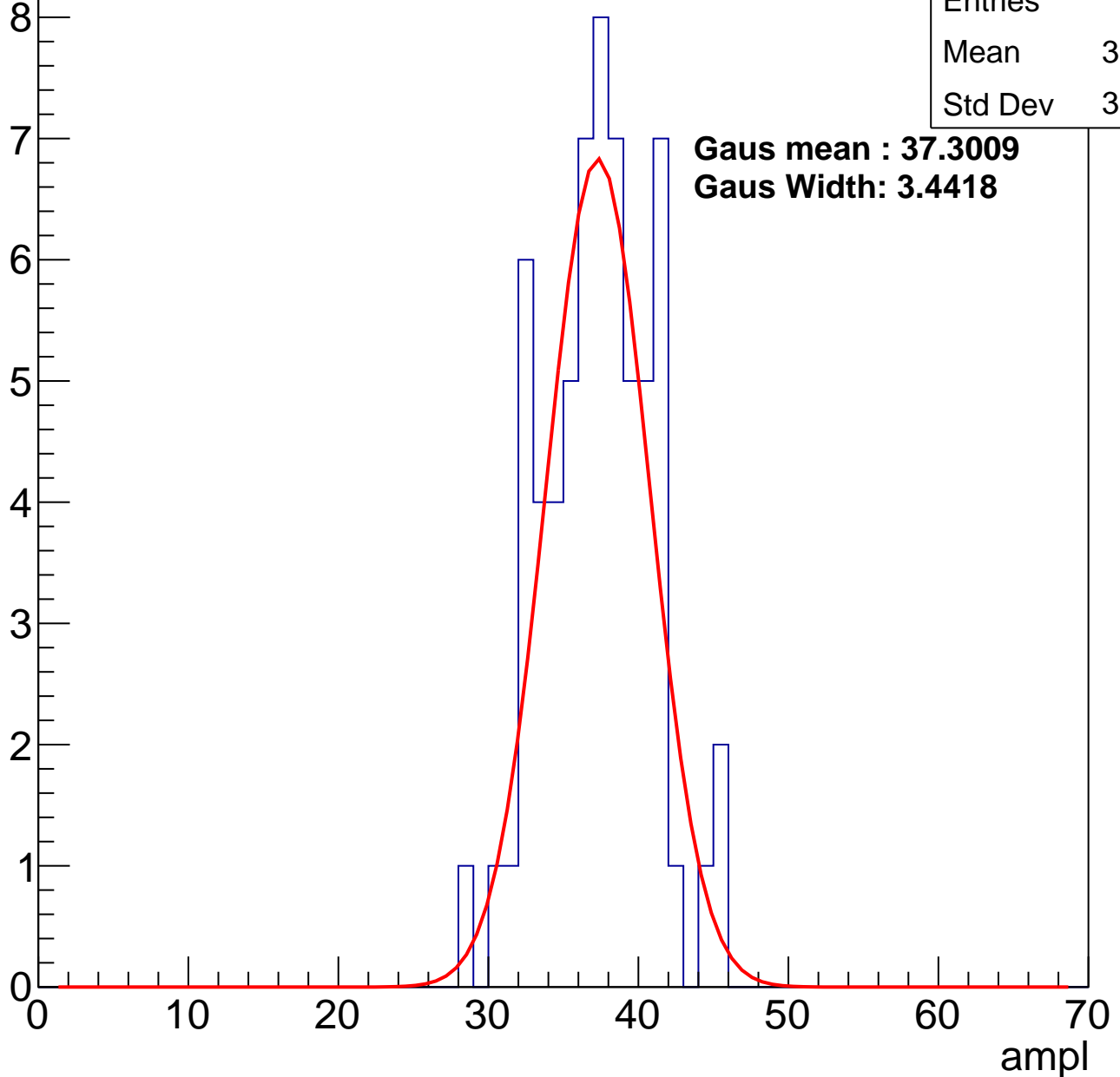
# B0L002S, U23-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.86
Std Dev	3.569

**Gaus mean : 37.3009**  
**Gaus Width: 3.4418**



# B0L002S, U23-ch51, adc2

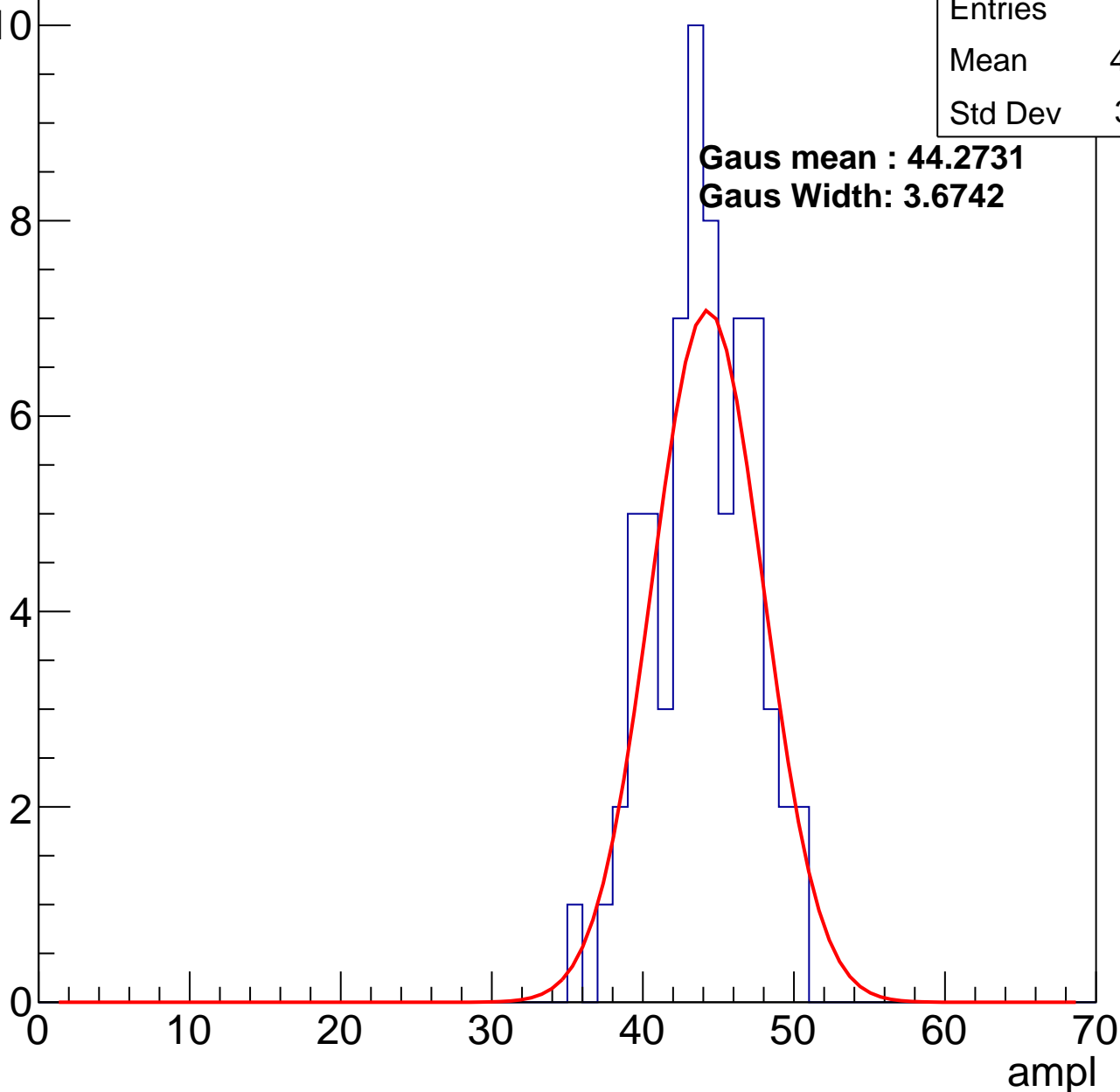
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.53
Std Dev	3.261

**Gaus mean : 44.2731**

**Gaus Width: 3.6742**

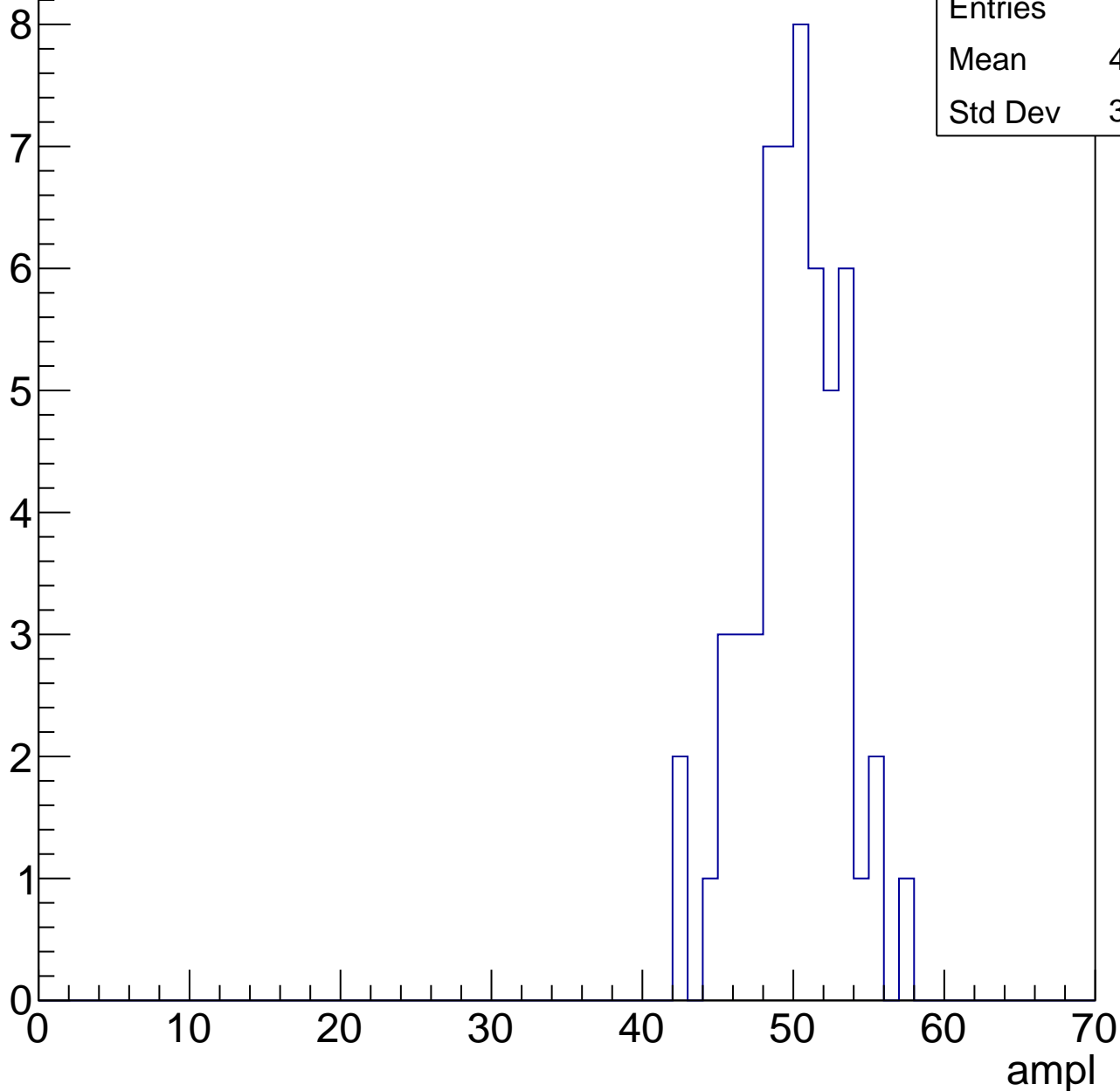


# B0L002S, U23-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	49.56
Std Dev	3.115

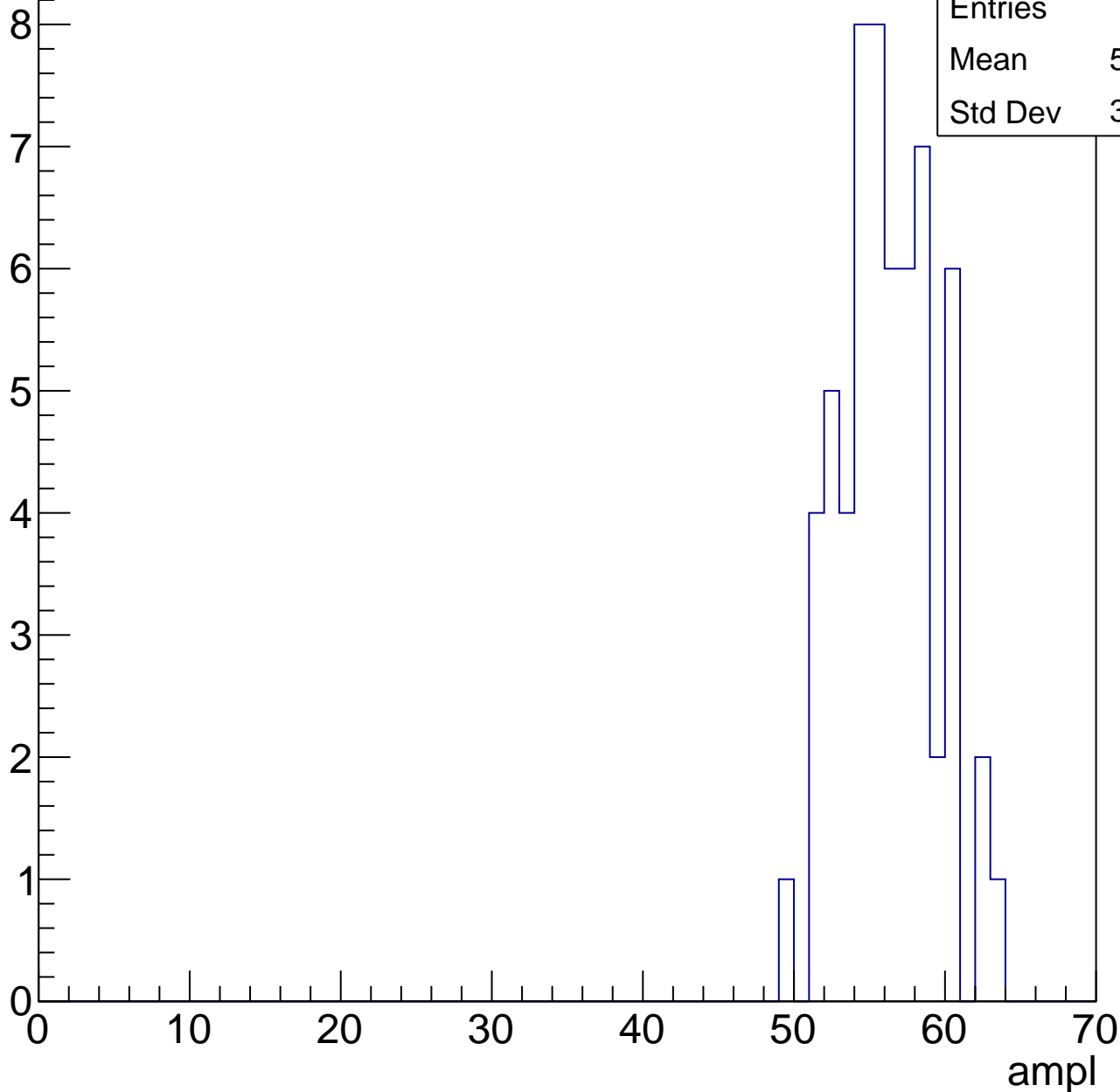


# B0L002S, U23-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	55.77
Std Dev	3.084

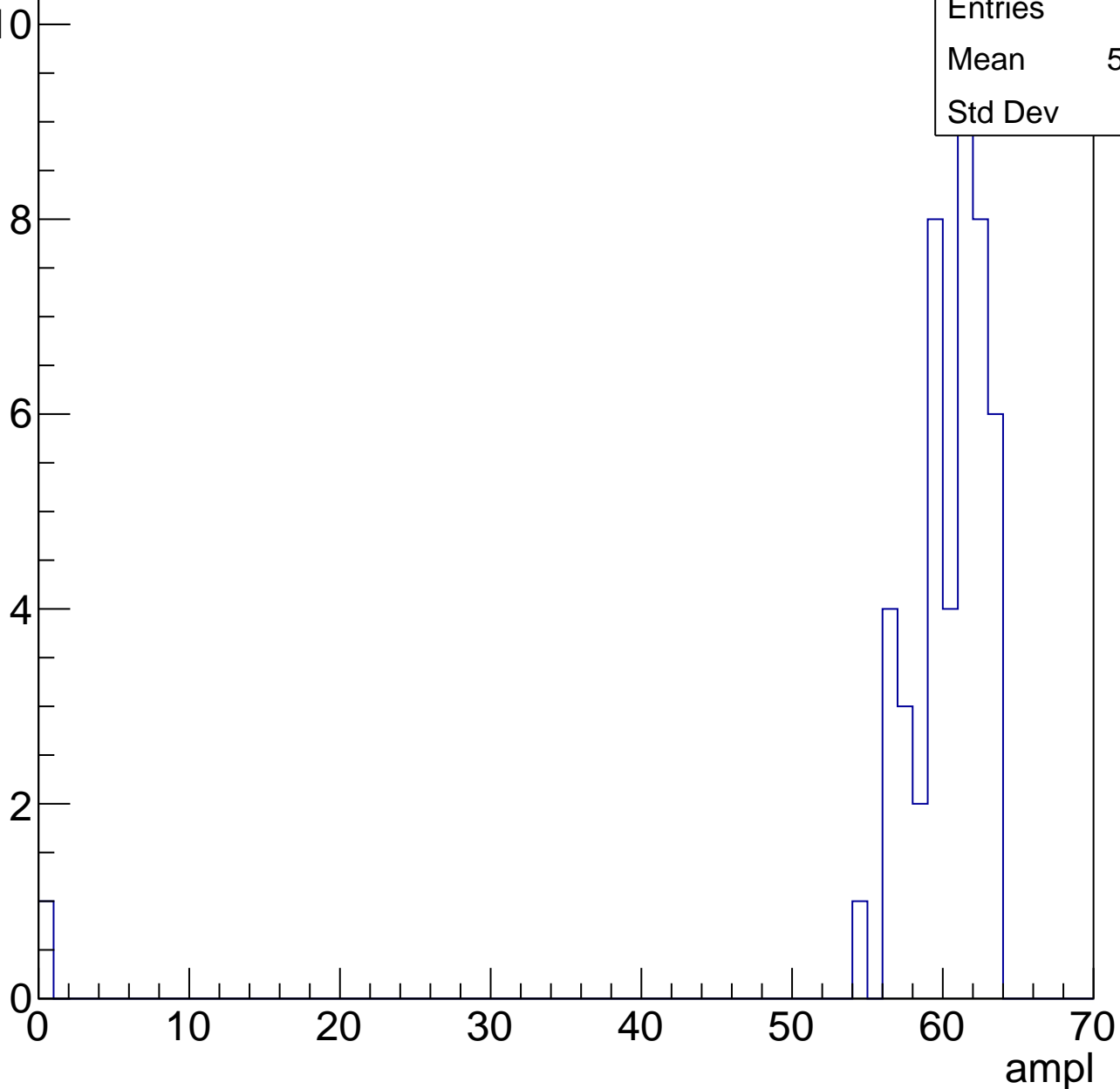


# B0L002S, U23-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

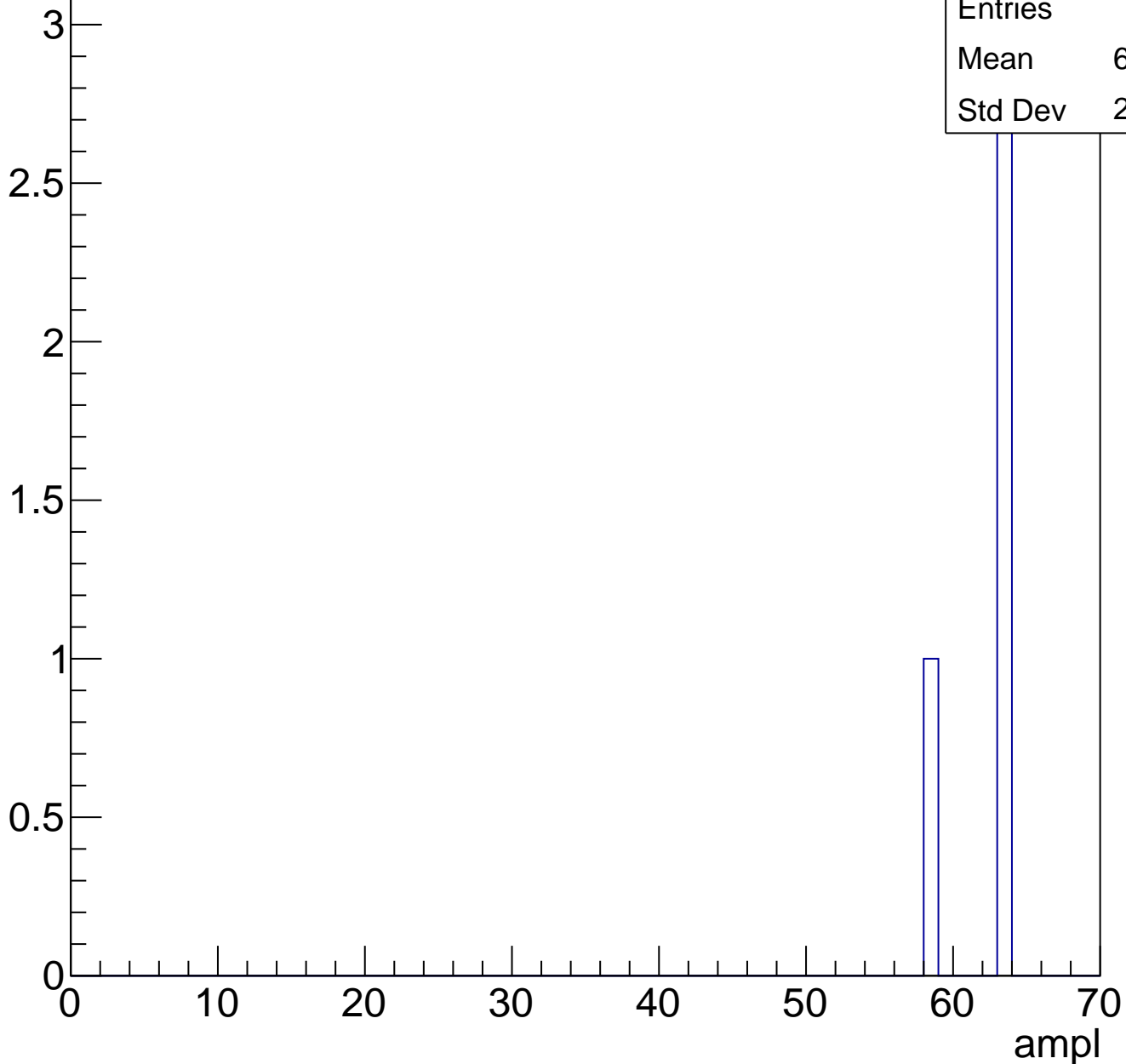
Entries	47
Mean	58.74
Std Dev	8.95



# B0L002S, U23-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch52, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	66
Mean	29.68
Std Dev	4.623

**Gaus mean : 30.5843**

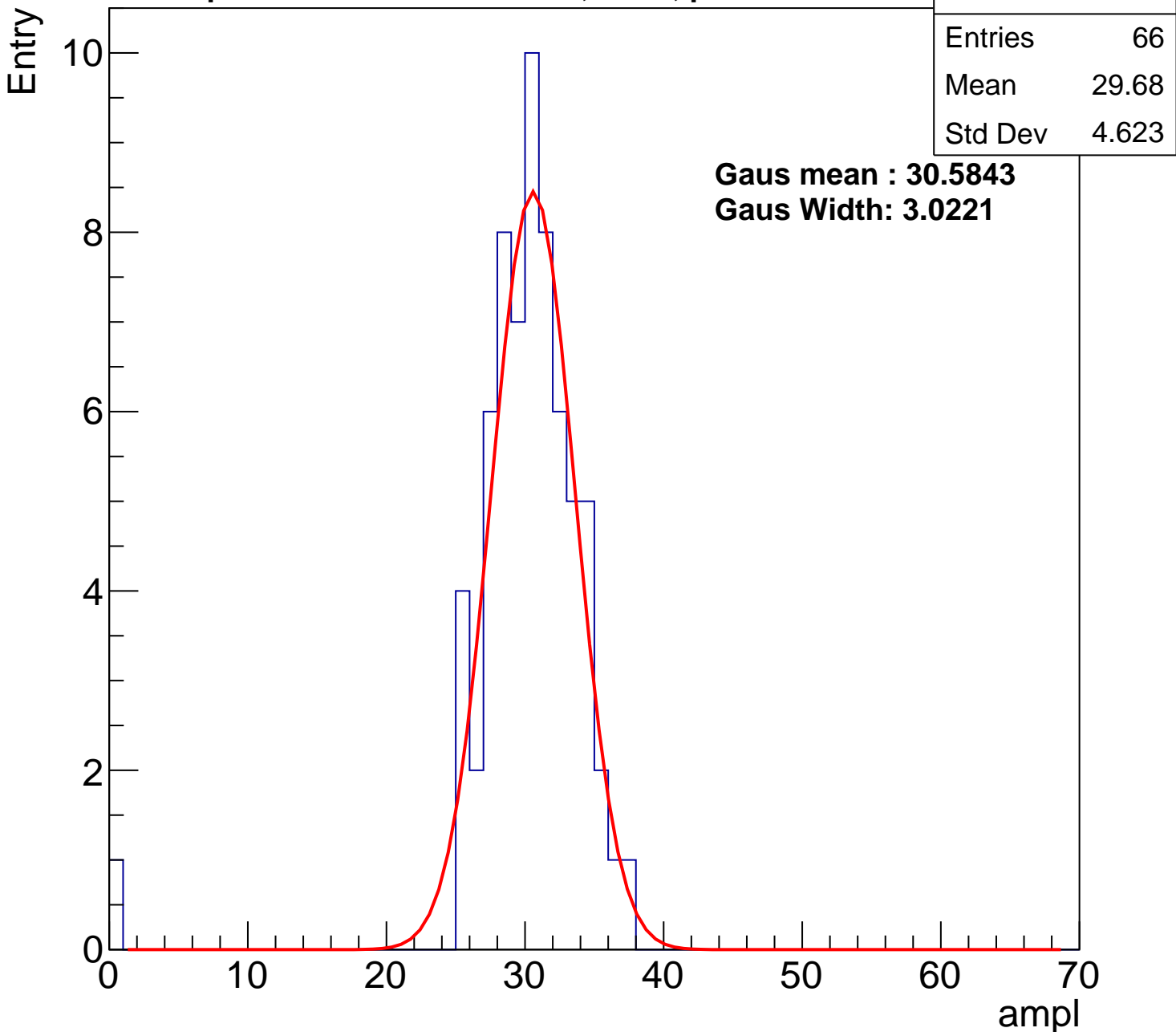
**Gaus Width: 3.0221**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch52, adc1

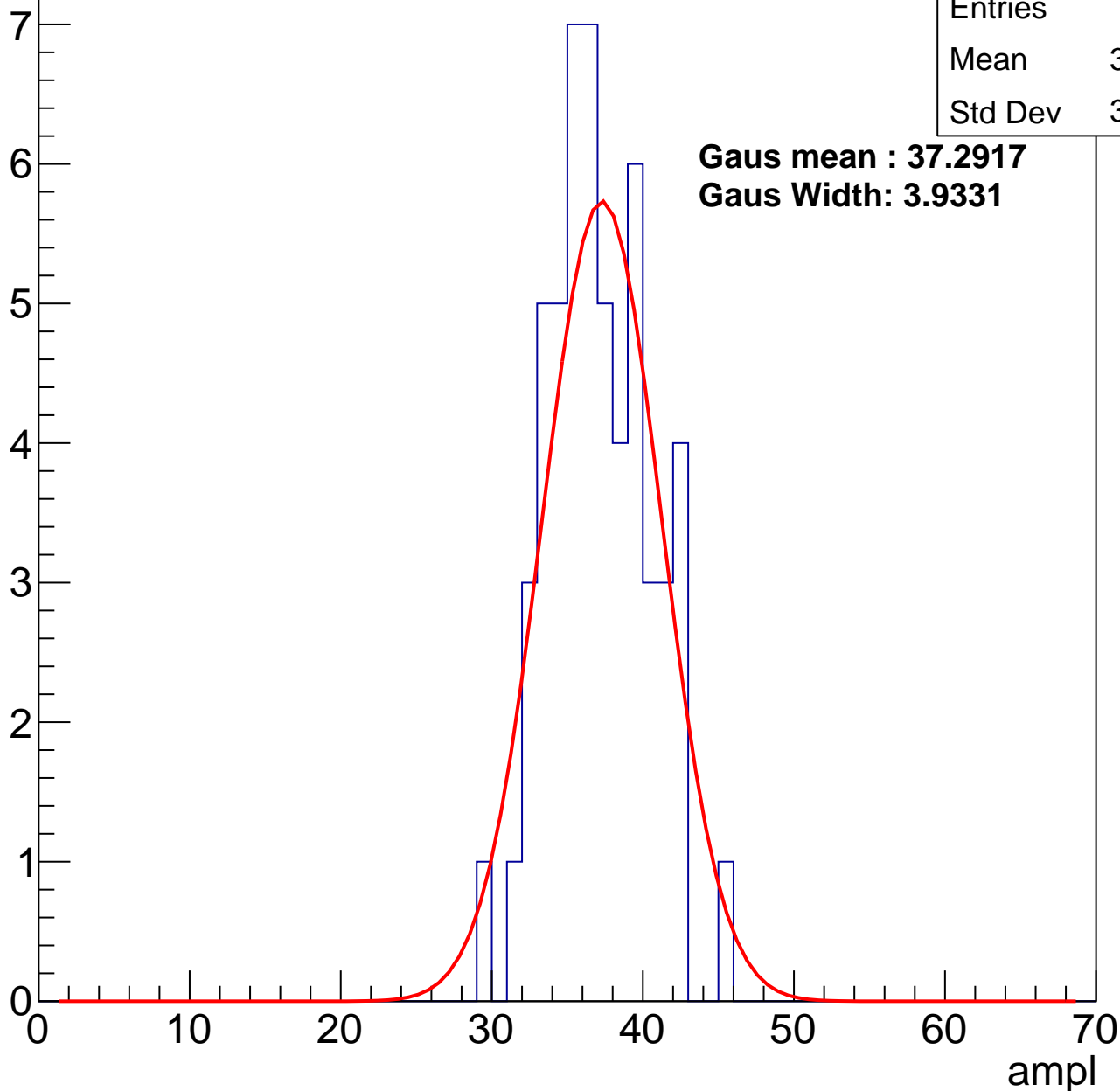
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.64
Std Dev	3.288

**Gaus mean : 37.2917**

**Gaus Width: 3.9331**



# B0L002S, U23-ch52, adc2

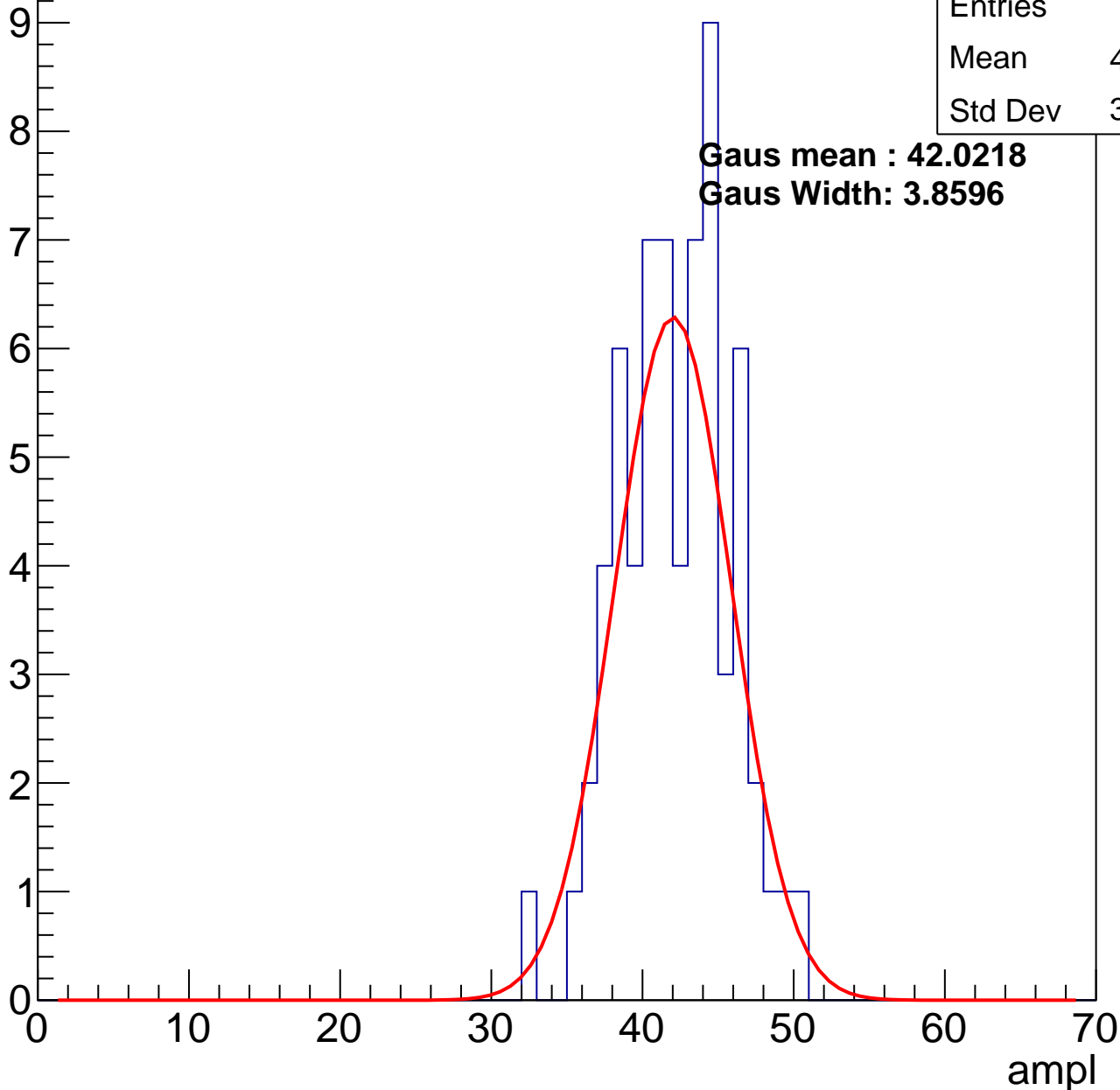
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	41.74
Std Dev	3.586

**Gaus mean : 42.0218**

**Gaus Width: 3.8596**



# B0L002S, U23-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

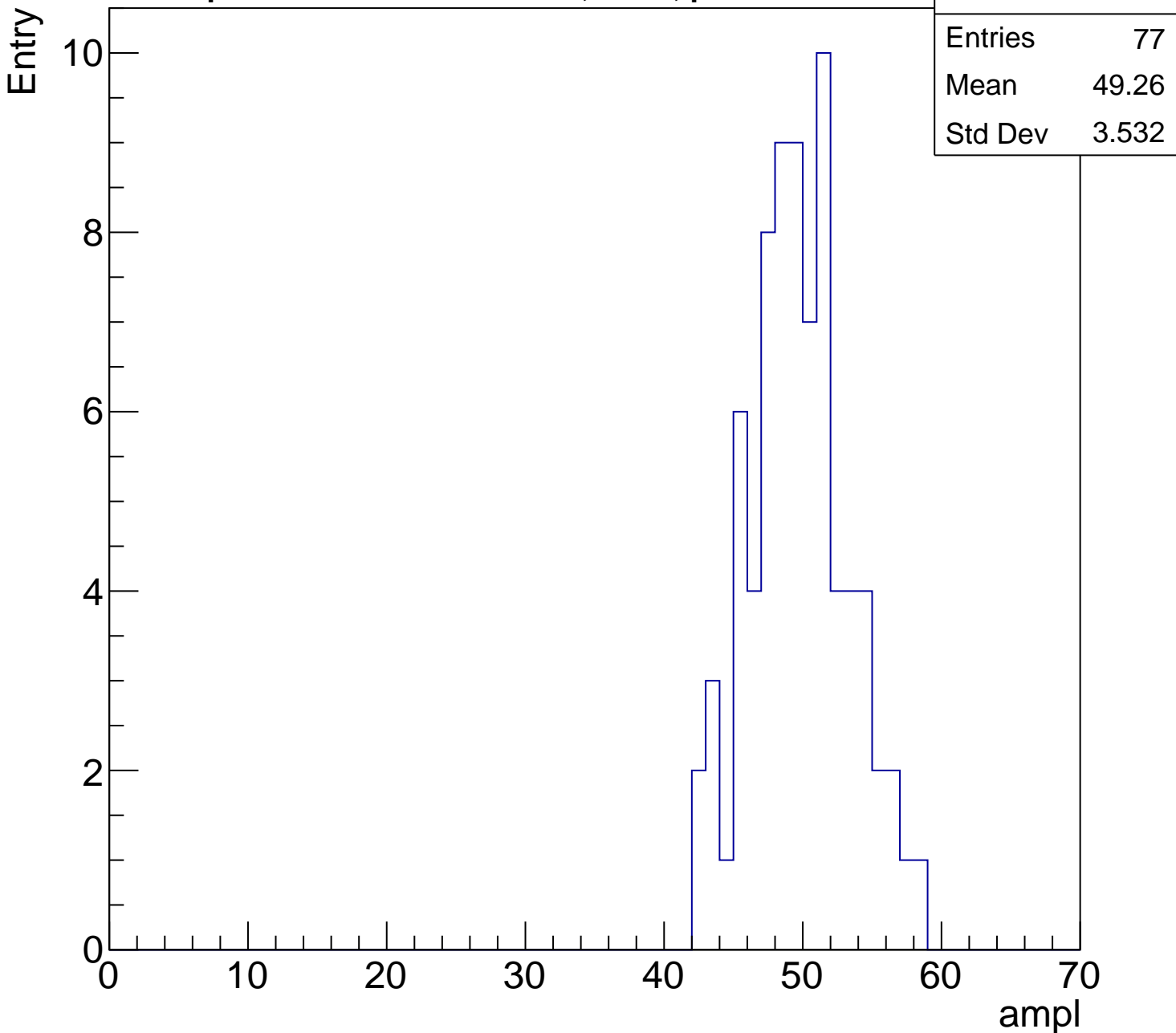
Entries	77
Mean	49.26
Std Dev	3.532

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

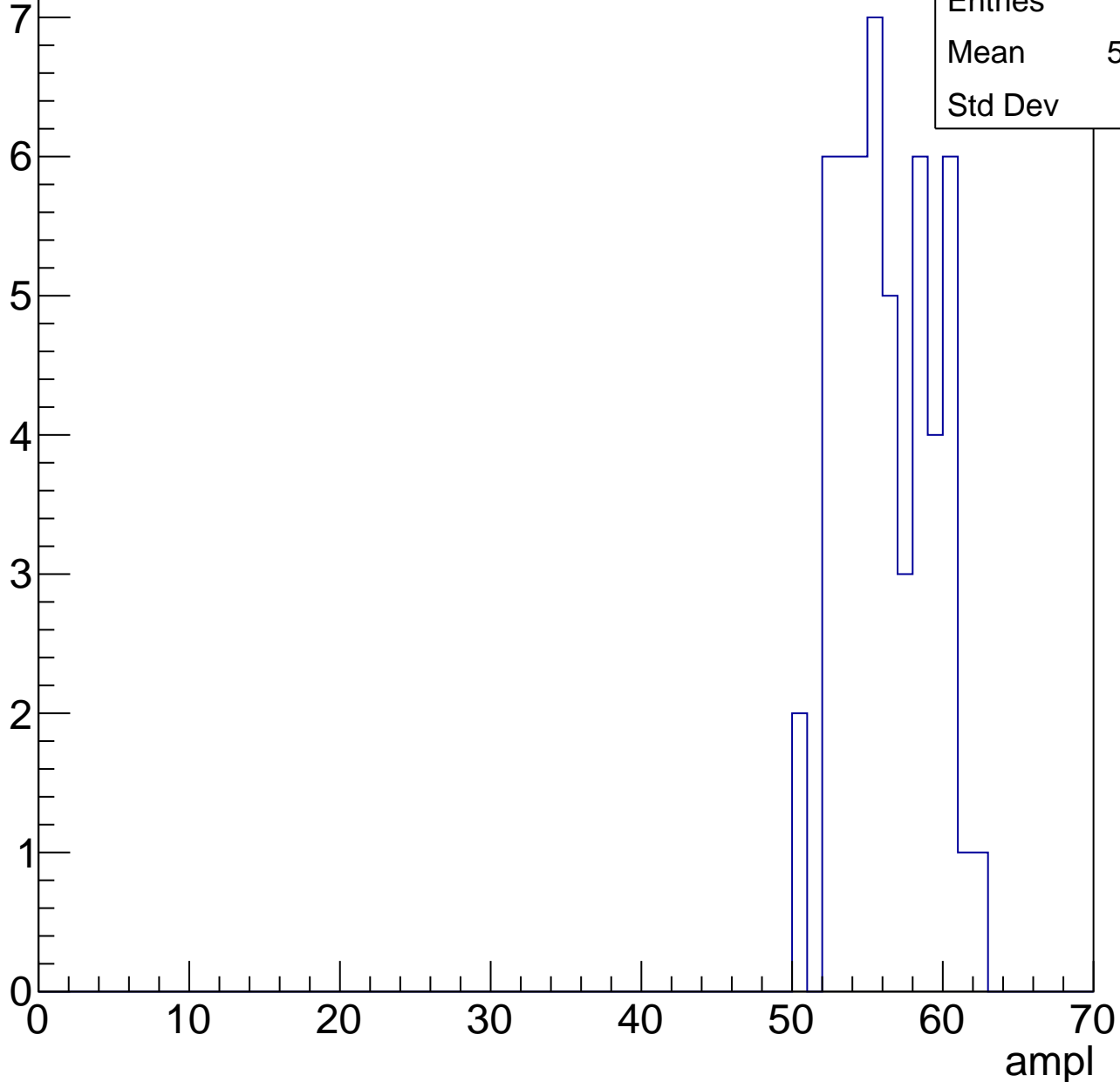


# B0L002S, U23-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	55.79
Std Dev	2.98

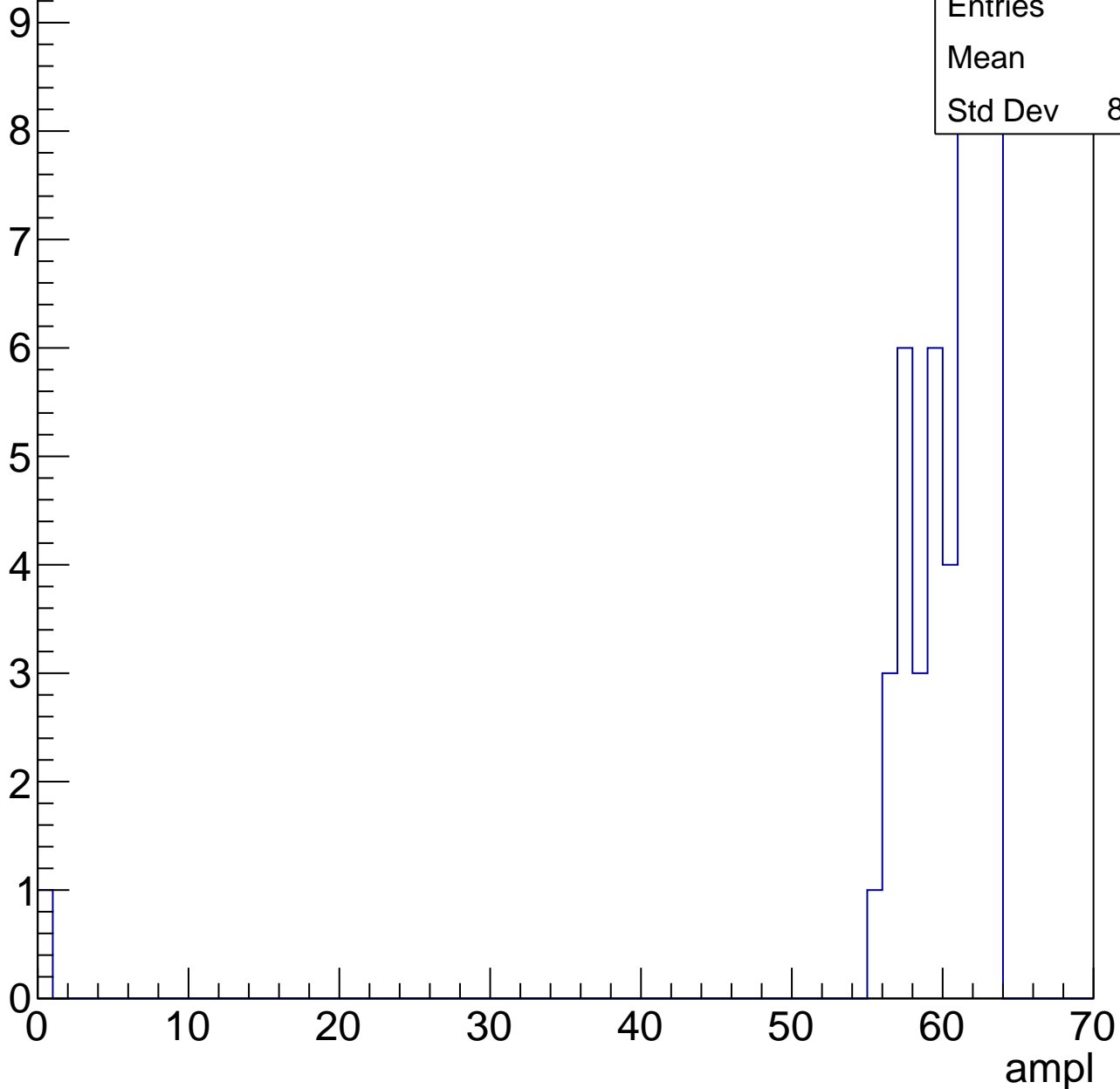


# B0L002S, U23-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	58.9
Std Dev	8.725



# B0L002S, U23-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L002S, U23-ch53, adc0

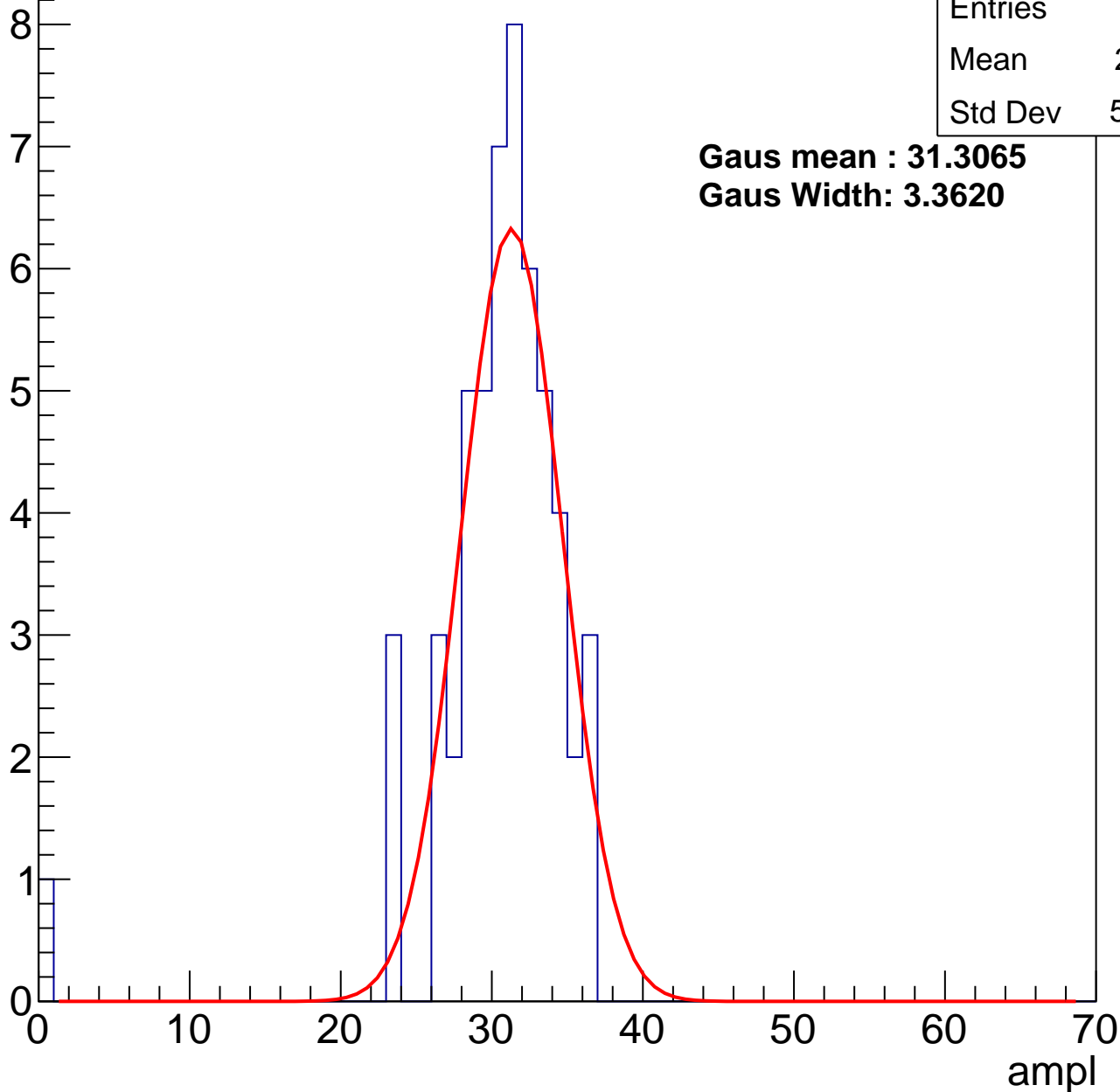
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	29.91
Std Dev	5.158

**Gaus mean : 31.3065**

**Gaus Width: 3.3620**



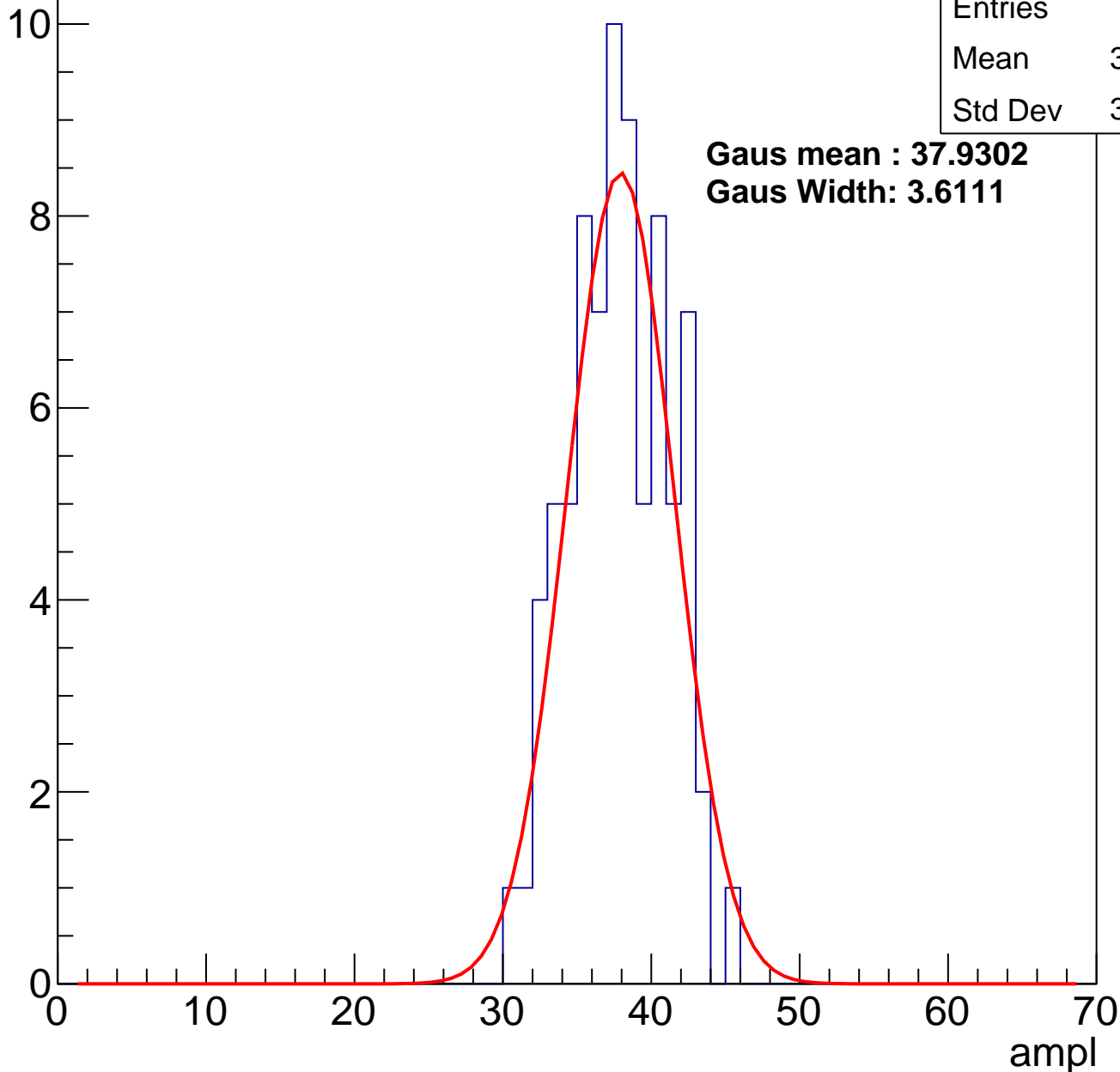
# B0L002S, U23-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	37.35
Std Dev	3.265

**Gaus mean : 37.9302**  
**Gaus Width: 3.6111**

Entry



# B0L002S, U23-ch53, adc2

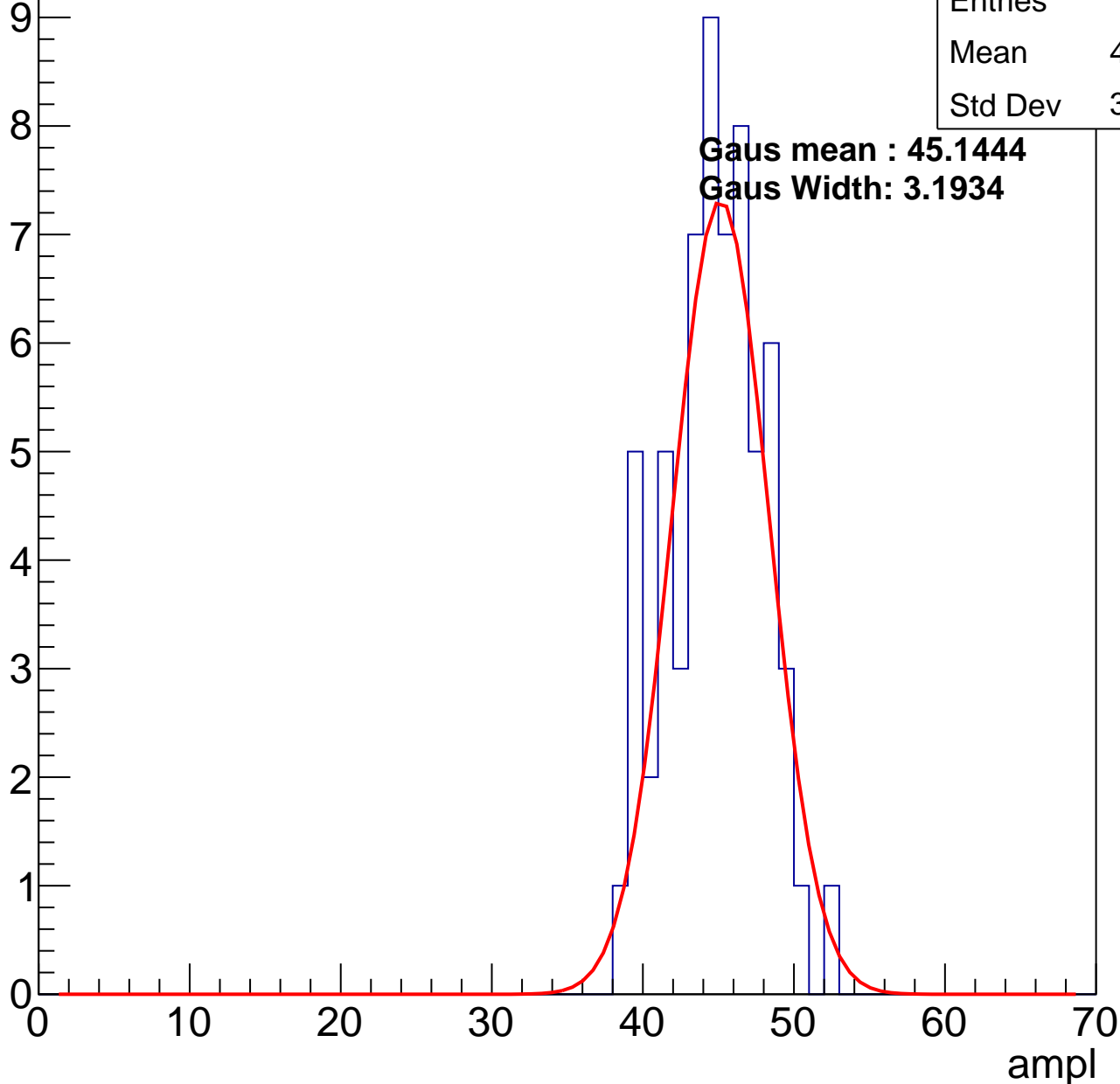
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.38
Std Dev	3.093

**Gaus mean : 45.1444**

**Gaus Width: 3.1934**

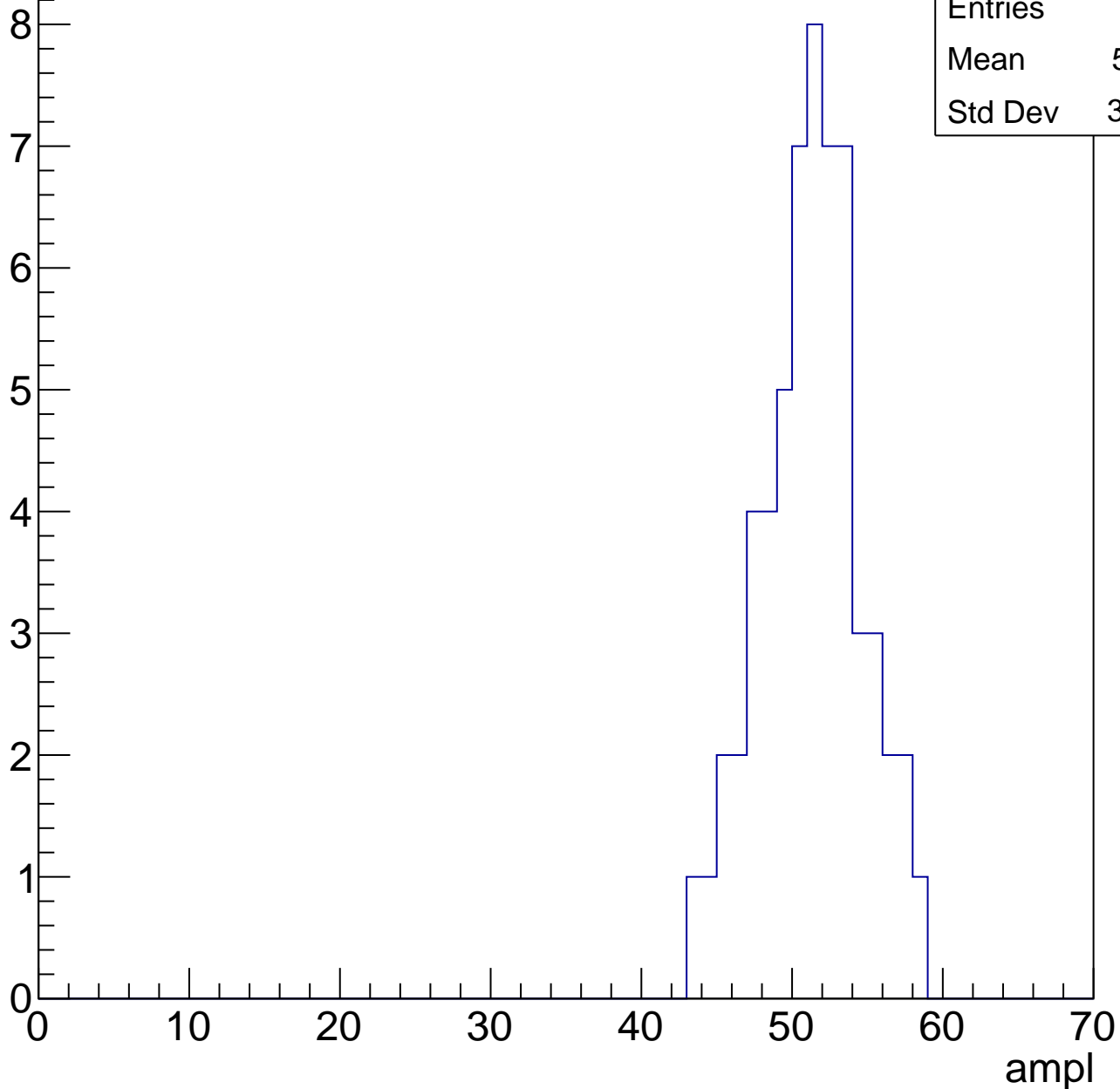


# B0L002S, U23-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.81
Std Dev	3.296

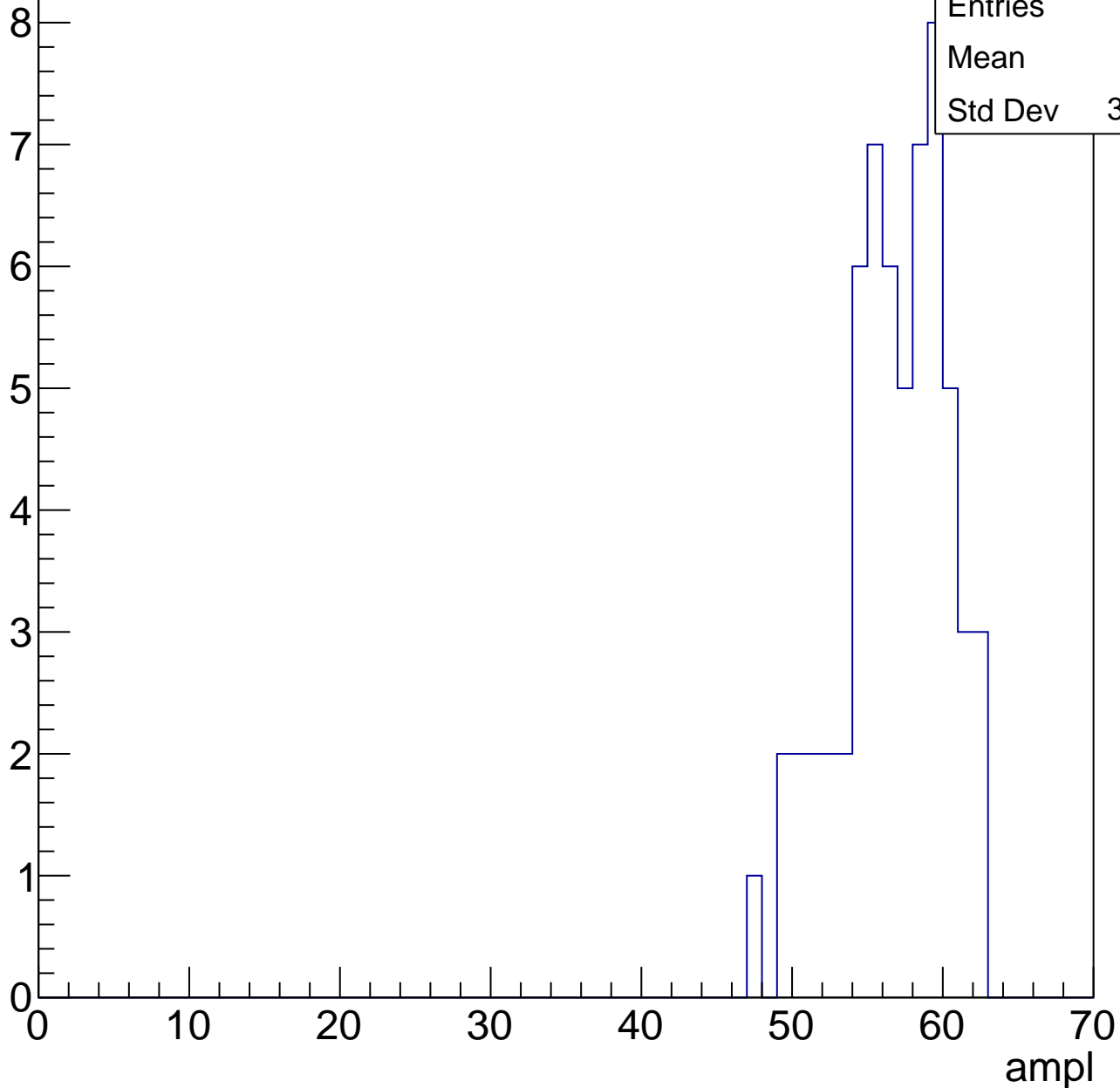


# B0L002S, U23-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.3
Std Dev	3.494

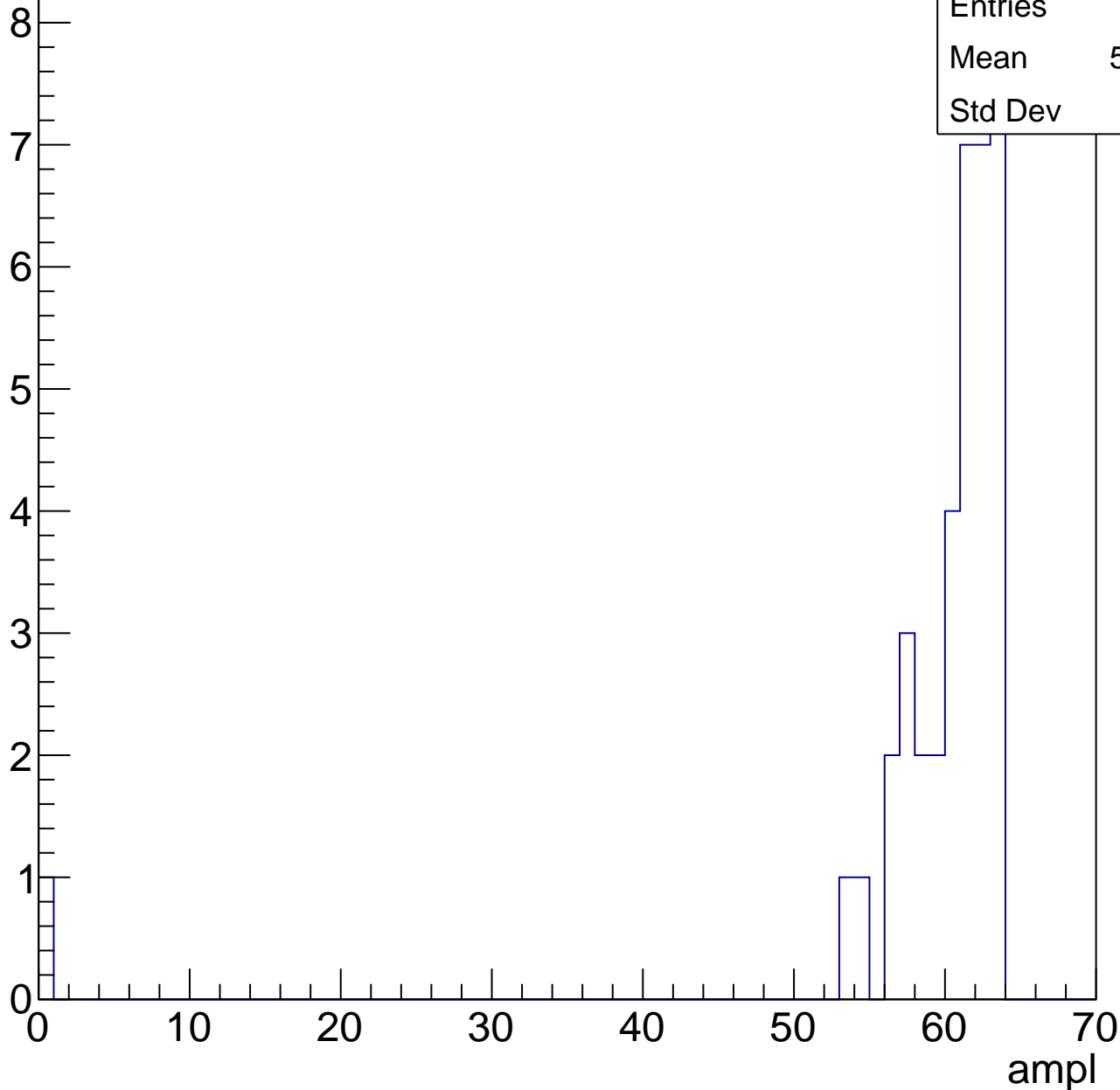


# B0L002S, U23-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.66
Std Dev	9.99



# B0L002S, U23-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch54, adc0

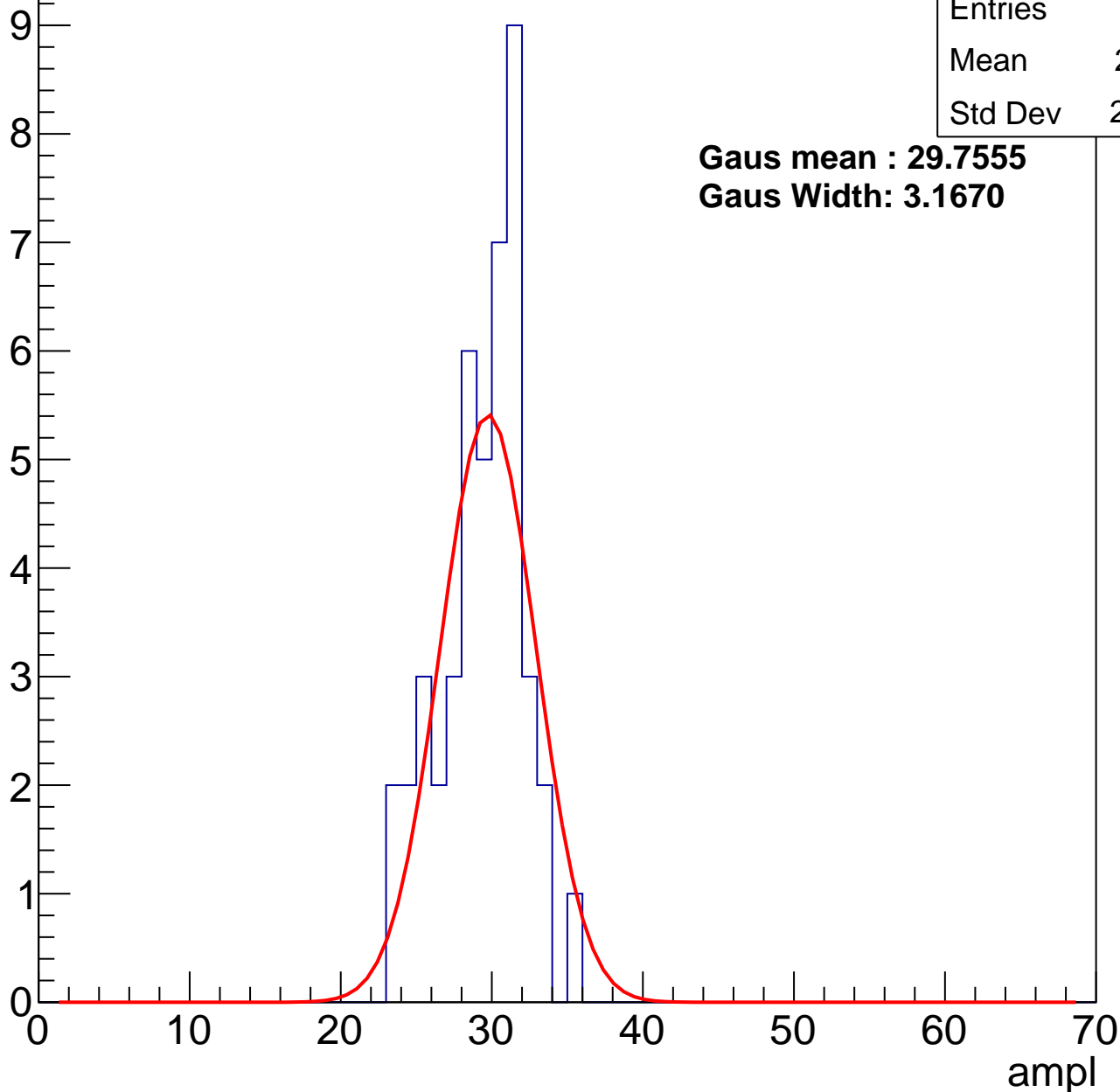
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	28.91
Std Dev	2.763

**Gaus mean : 29.7555**

**Gaus Width: 3.1670**



# B0L002S, U23-ch54, adc1

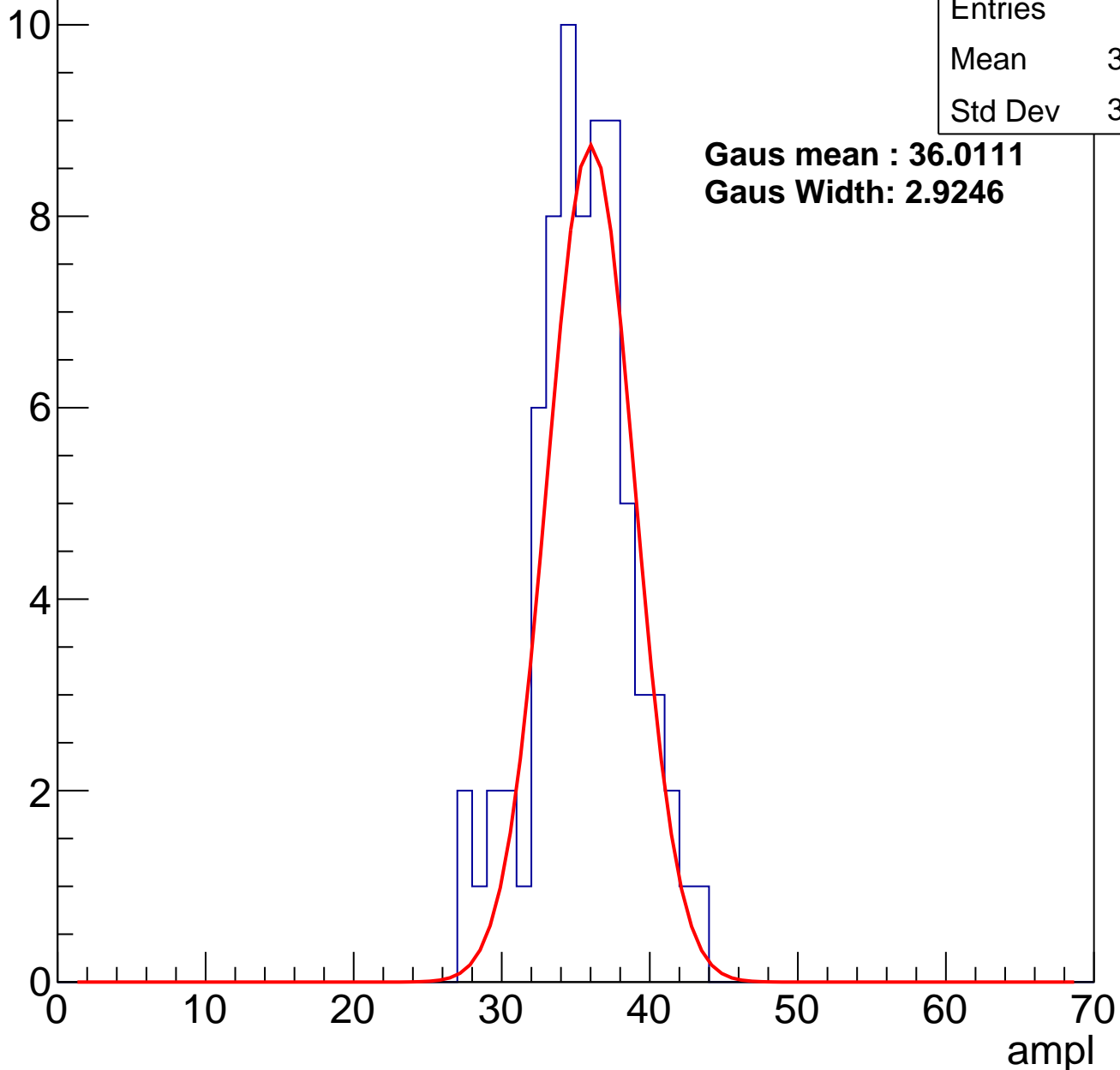
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	35.04
Std Dev	3.329

**Gaus mean : 36.0111**

**Gaus Width: 2.9246**

Entry



# B0L002S, U23-ch54, adc2

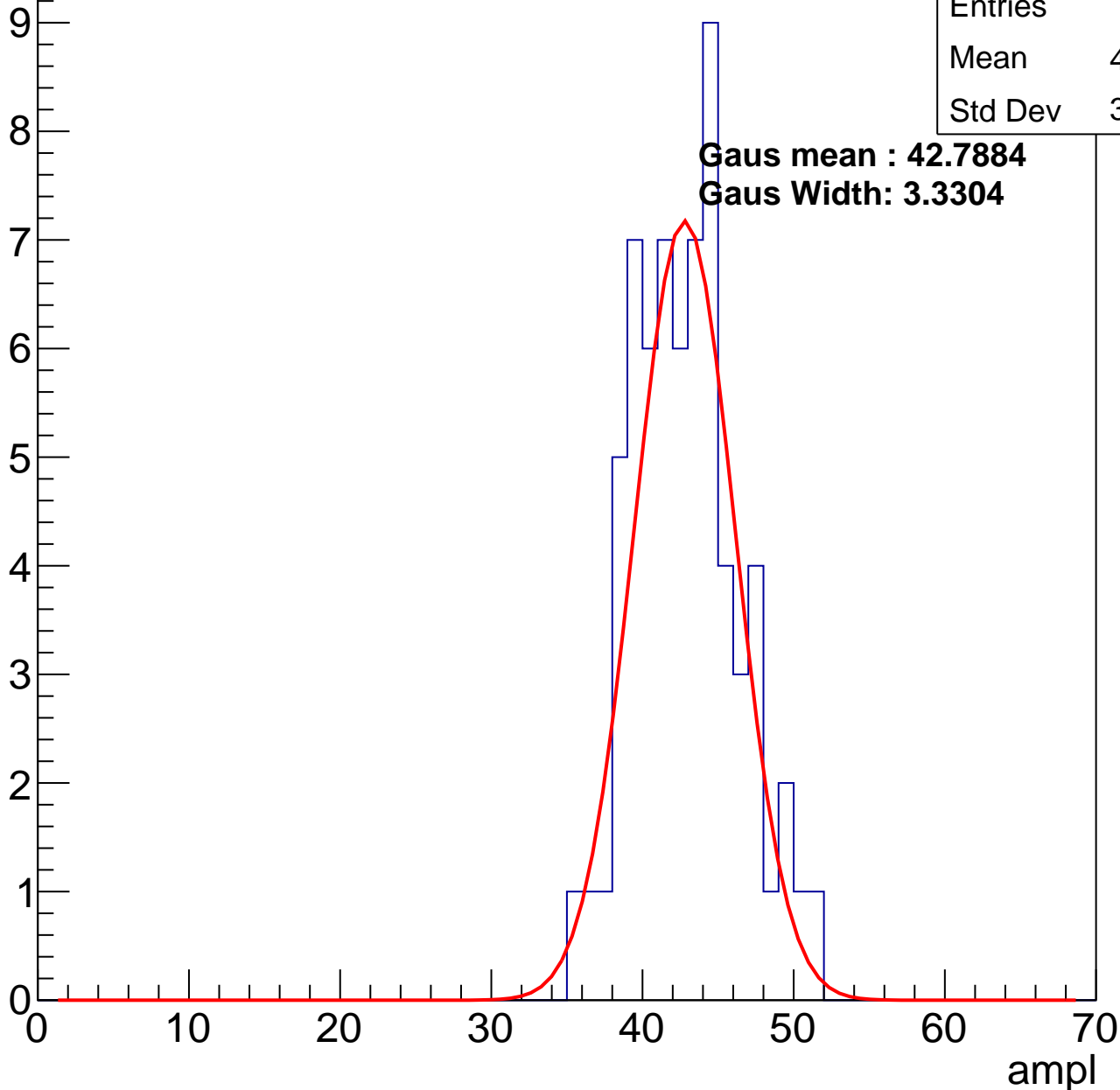
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.42
Std Dev	3.434

**Gaus mean : 42.7884**

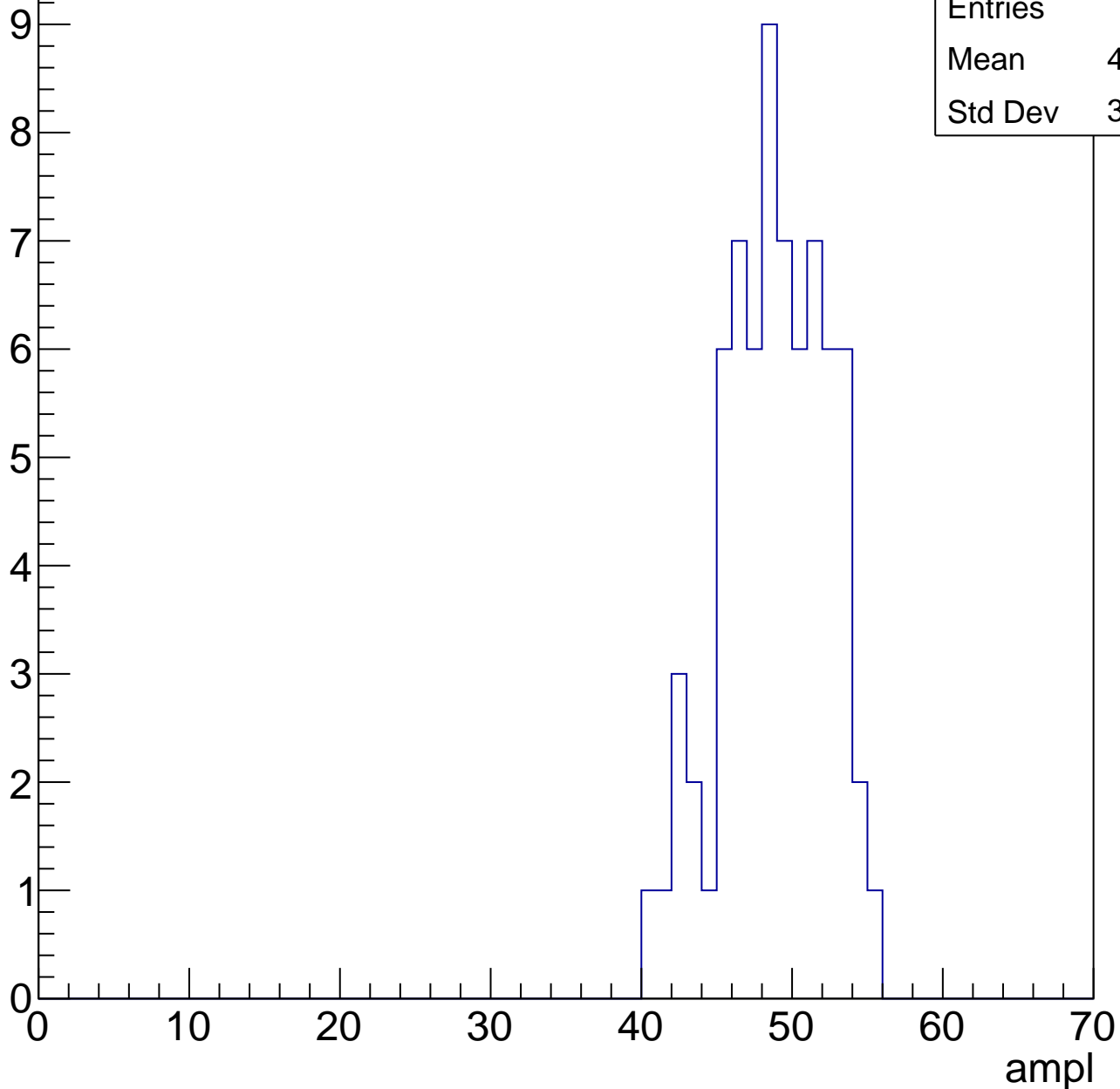
**Gaus Width: 3.3304**



# B0L002S, U23-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



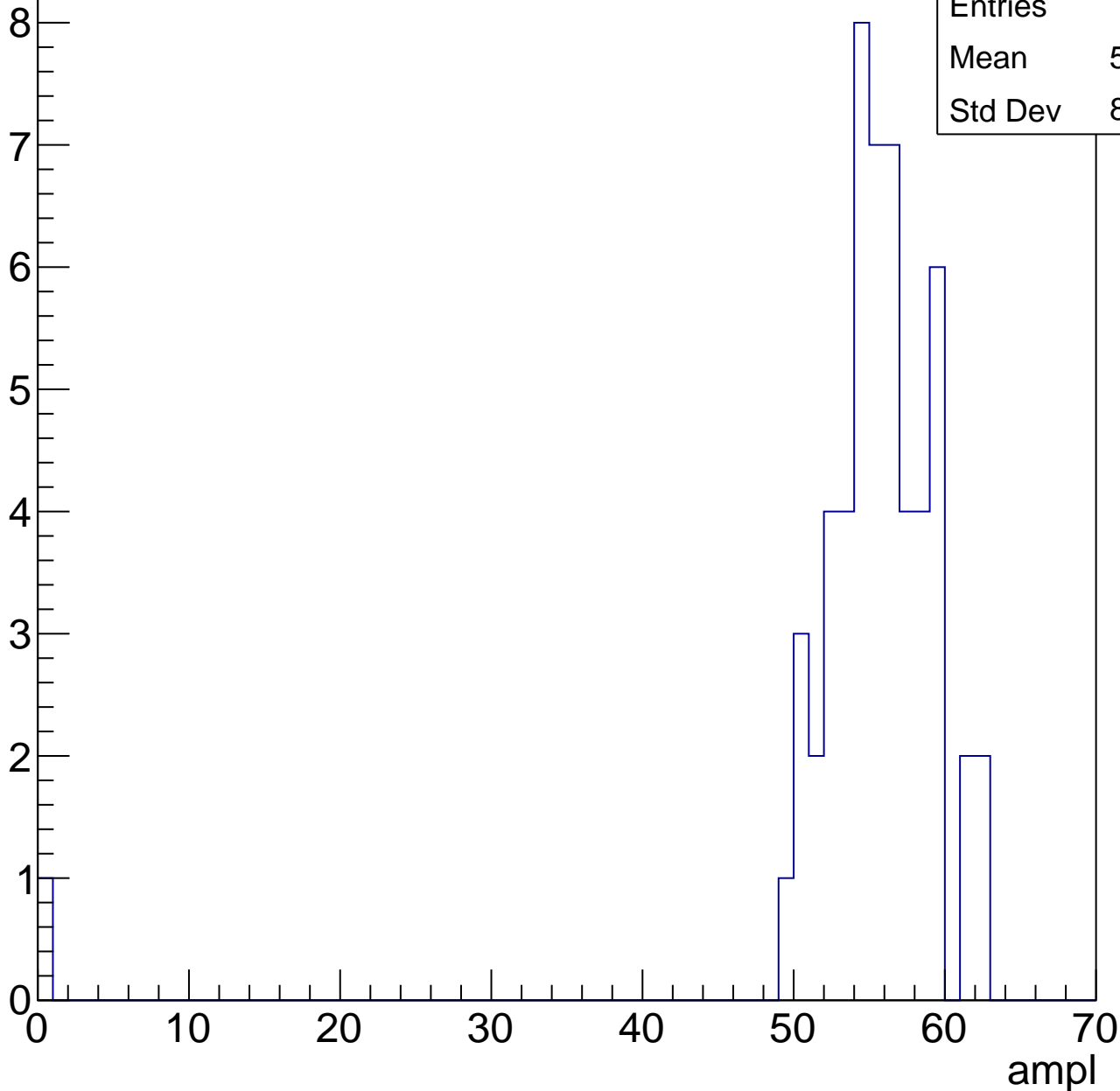
Entries	71
Mean	48.39
Std Dev	3.409

# B0L002S, U23-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	54.36
Std Dev	8.012



# B0L002S, U23-ch54, adc5

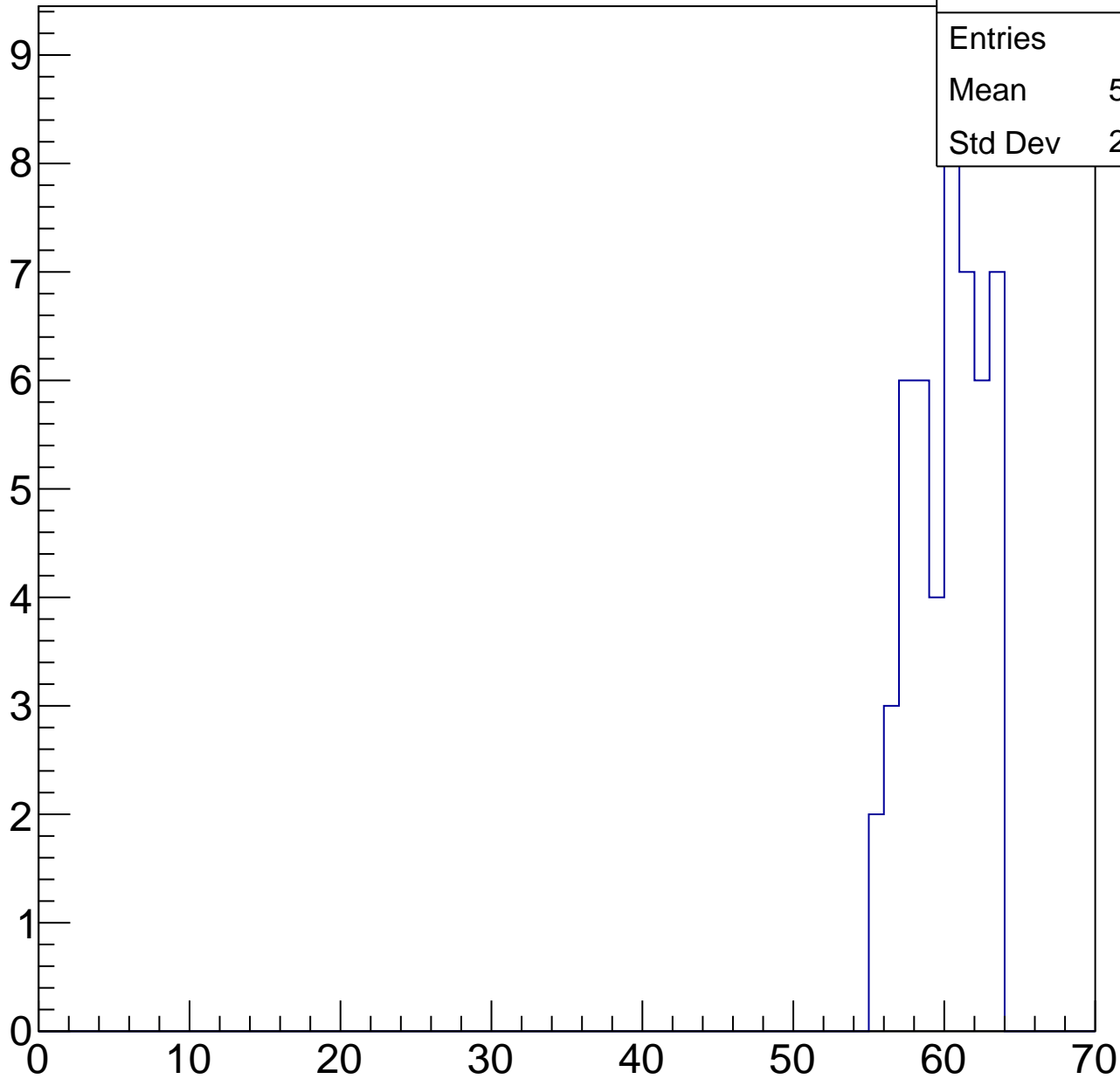
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	59.68
Std Dev	2.319

ampl



# B0L002S, U23-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch55, adc0

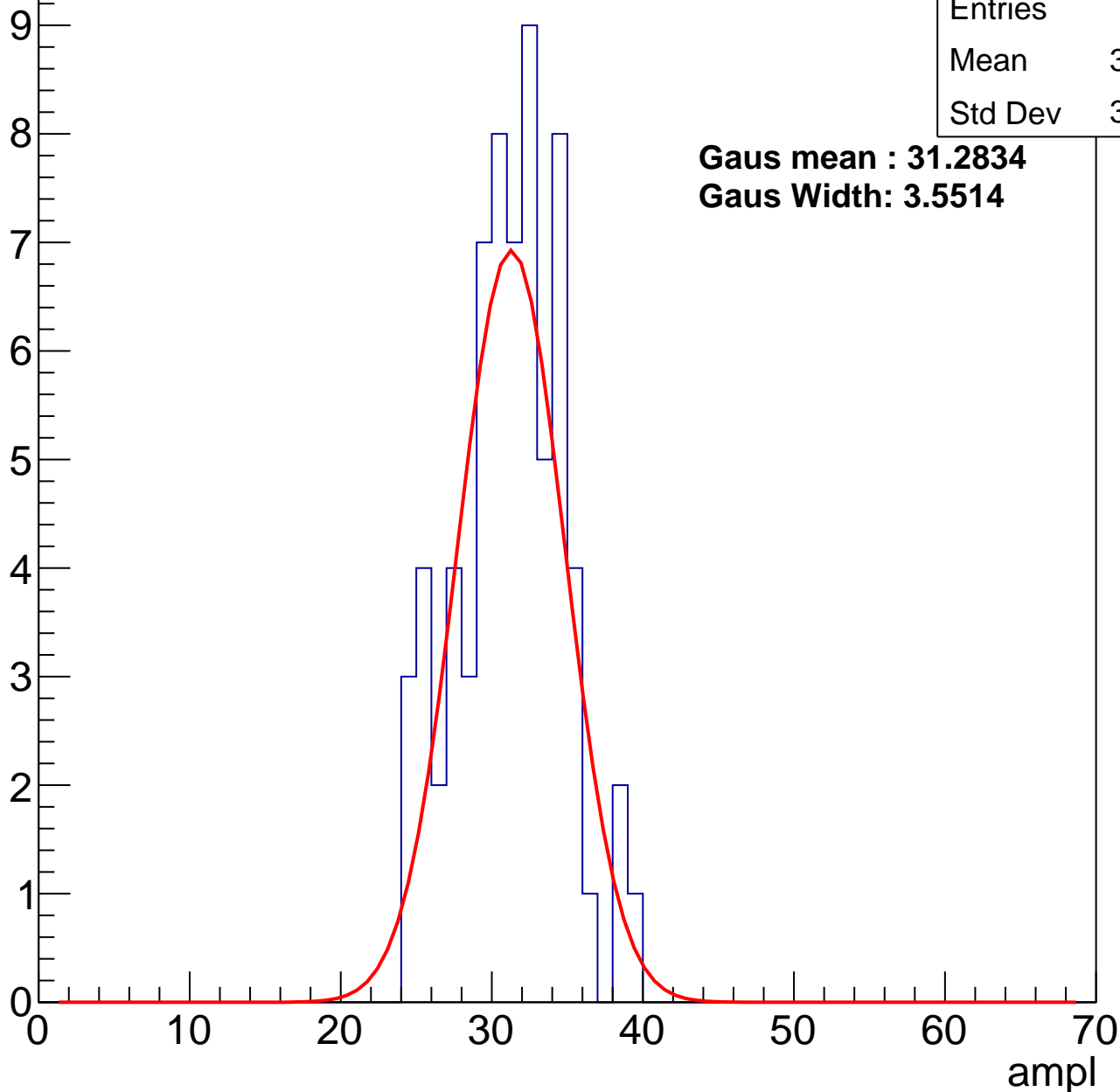
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.76
Std Dev	3.465

**Gaus mean : 31.2834**

**Gaus Width: 3.5514**



# B0L002S, U23-ch55, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	65
Mean	37.66
Std Dev	2.978

**Gaus mean : 38.2760**

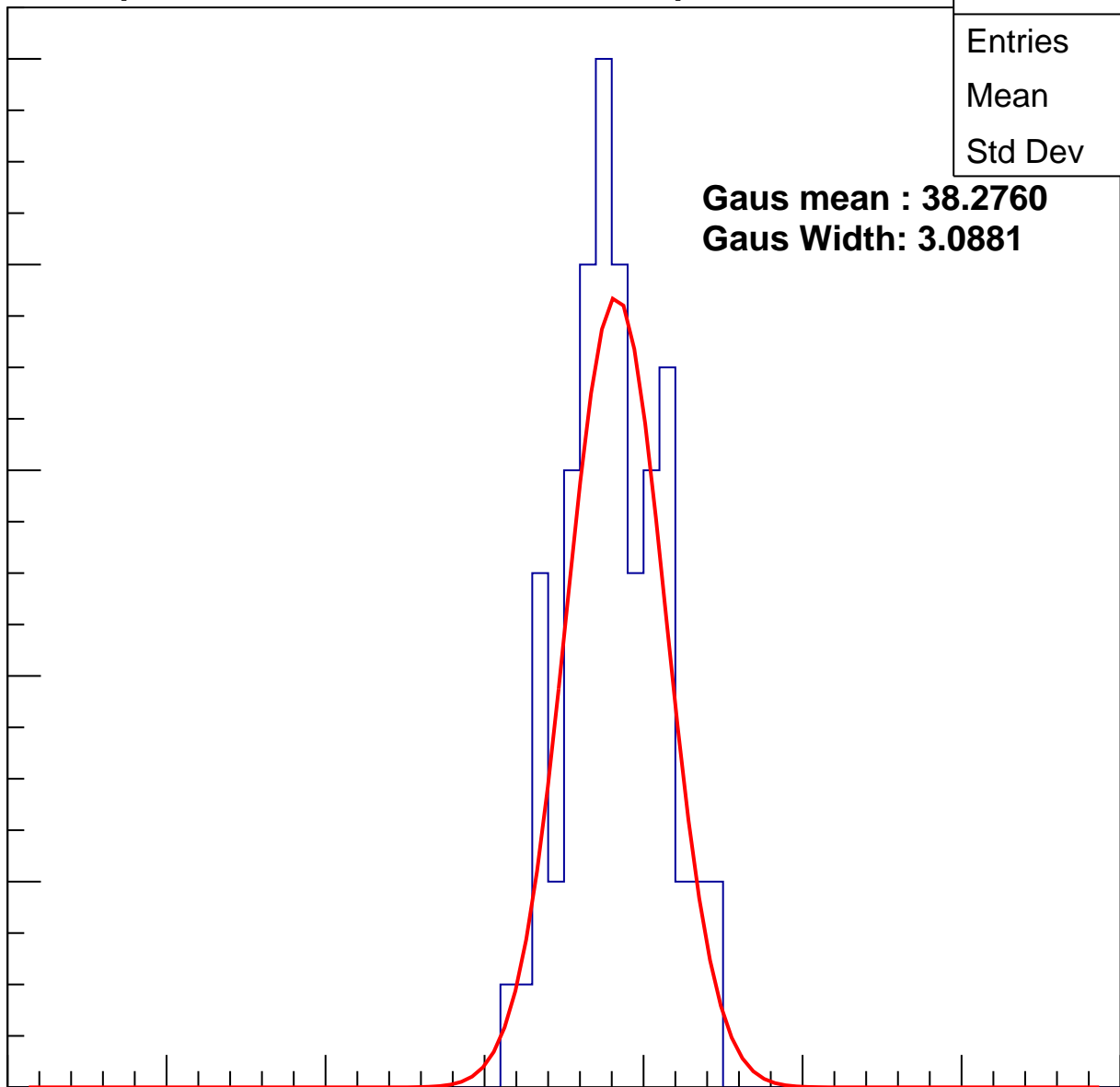
**Gaus Width: 3.0881**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch55, adc2

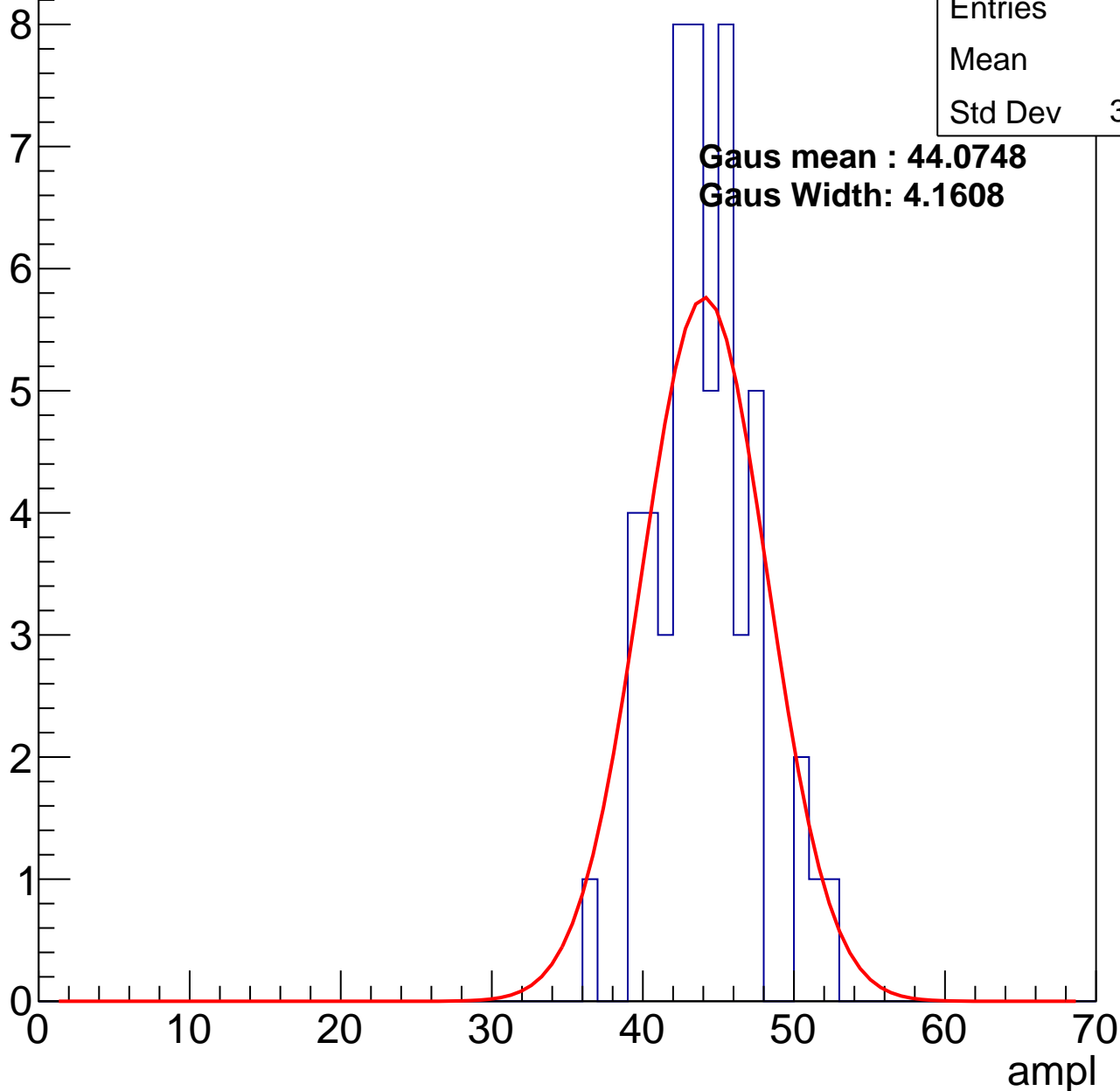
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.6
Std Dev	3.182

**Gaus mean : 44.0748**

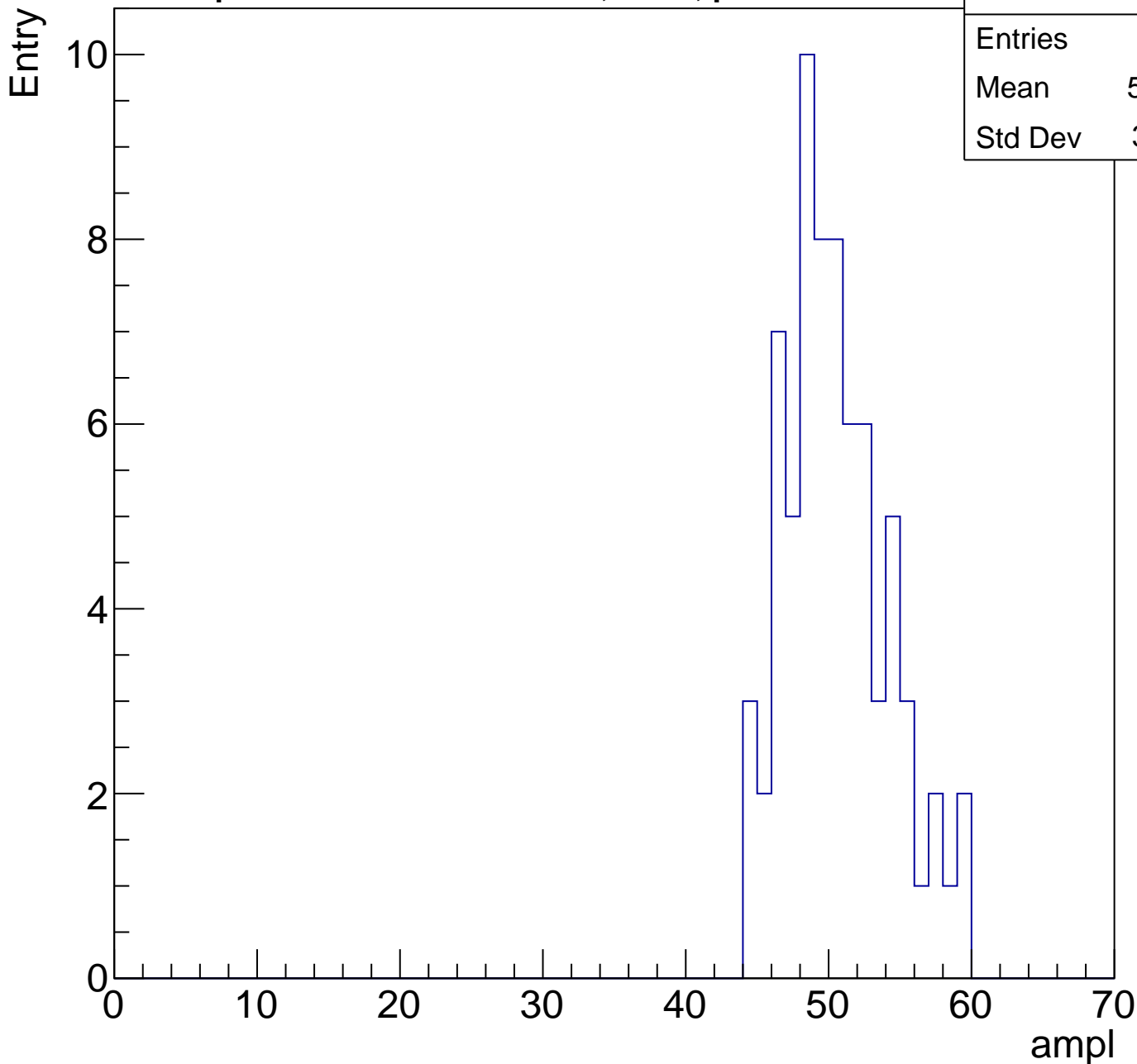
**Gaus Width: 4.1608**



# B0L002S, U23-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	72
Mean	50.12
Std Dev	3.601



# B0L002S, U23-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 44

Mean 55.39

Std Dev 3.256

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

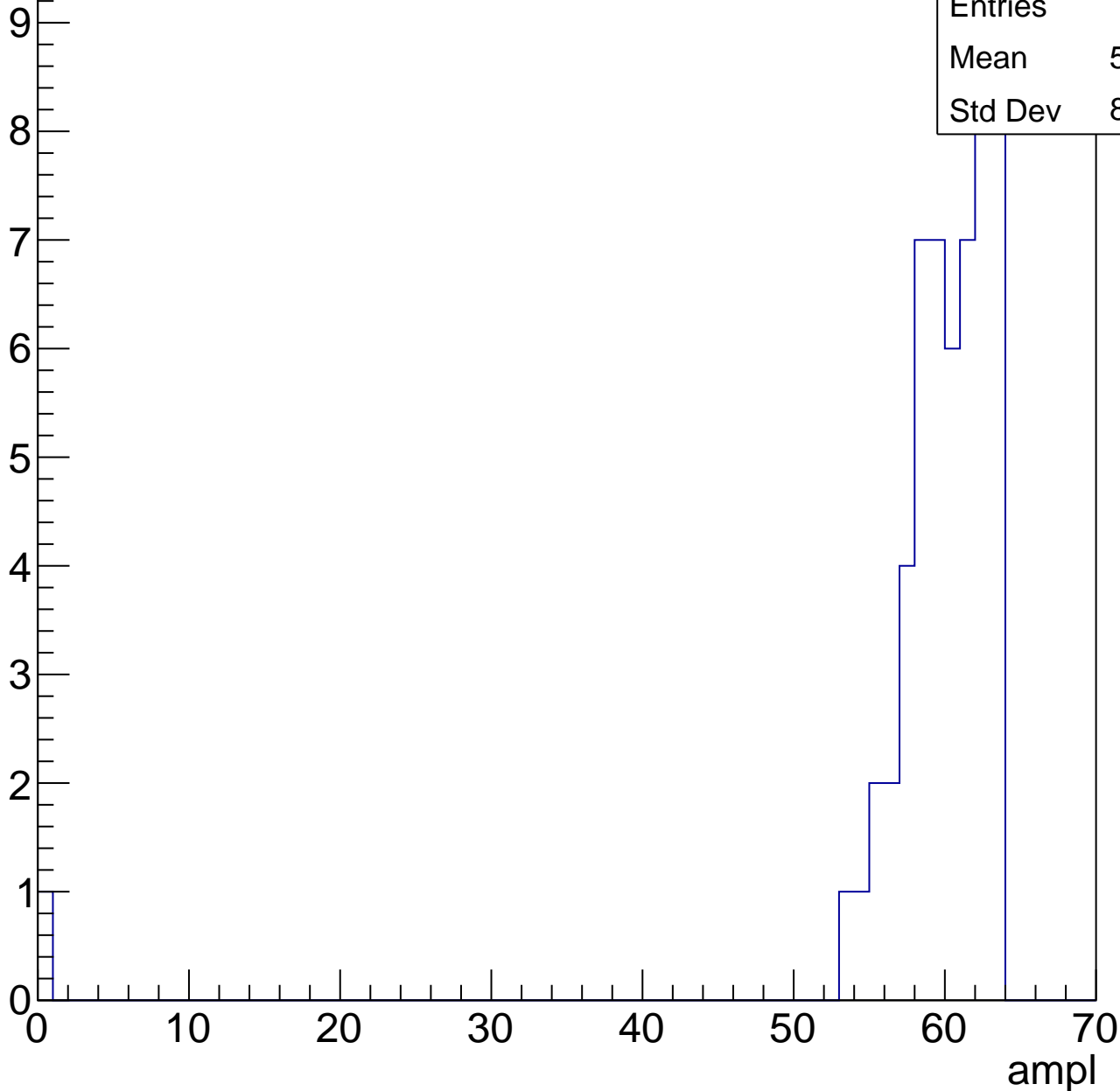
ampl

# B0L002S, U23-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	58.64
Std Dev	8.367



# B0L002S, U23-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

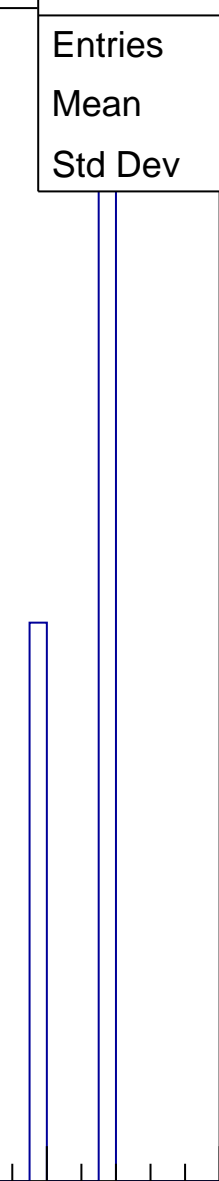
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.67
Std Dev	1.886

0 10 20 30 40 50 60 70

ampl





# B0L002S, U23-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch56, adc0

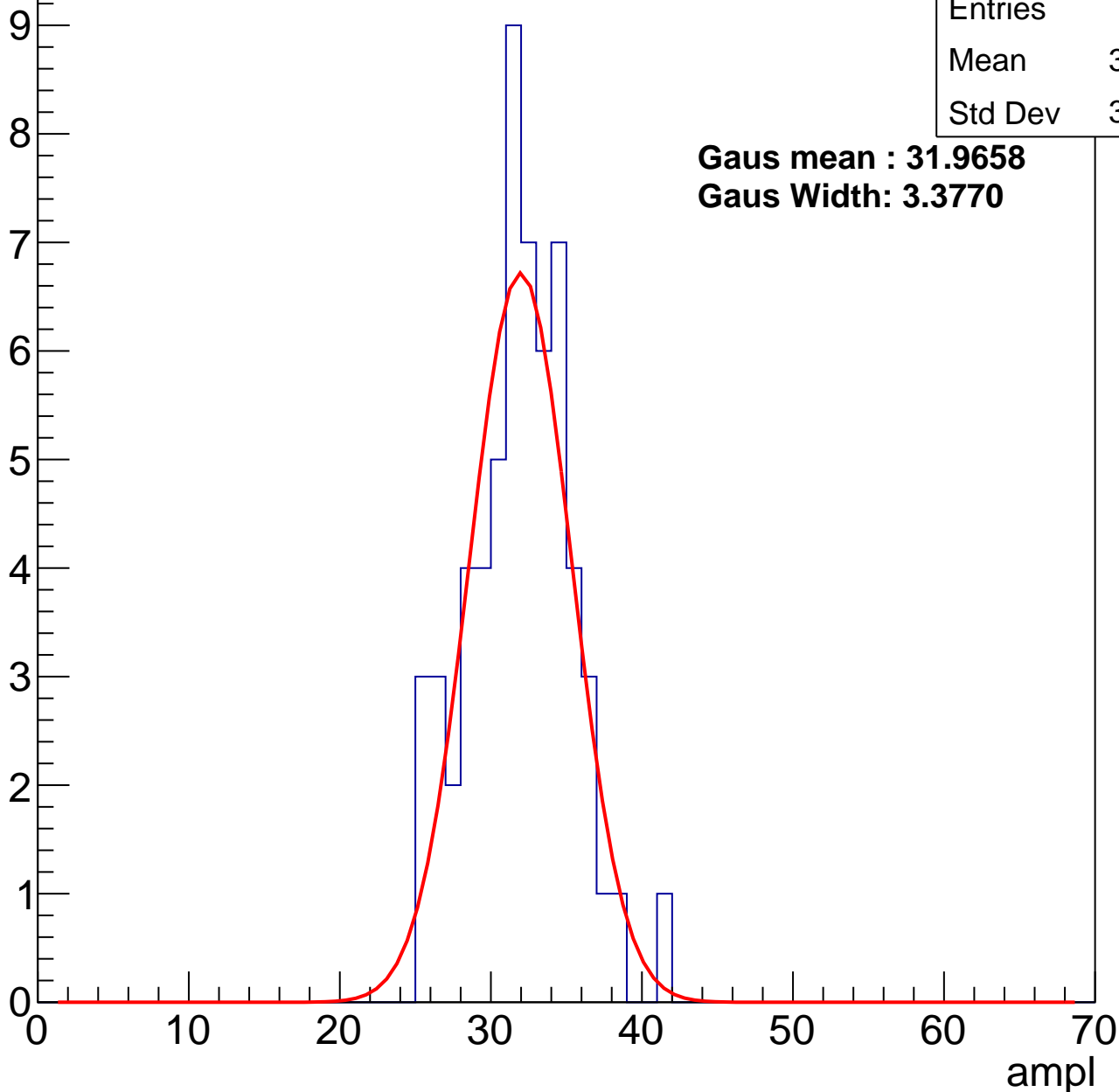
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	31.47
Std Dev	3.359

**Gaus mean : 31.9658**

**Gaus Width: 3.3770**



# B0L002S, U23-ch56, adc1

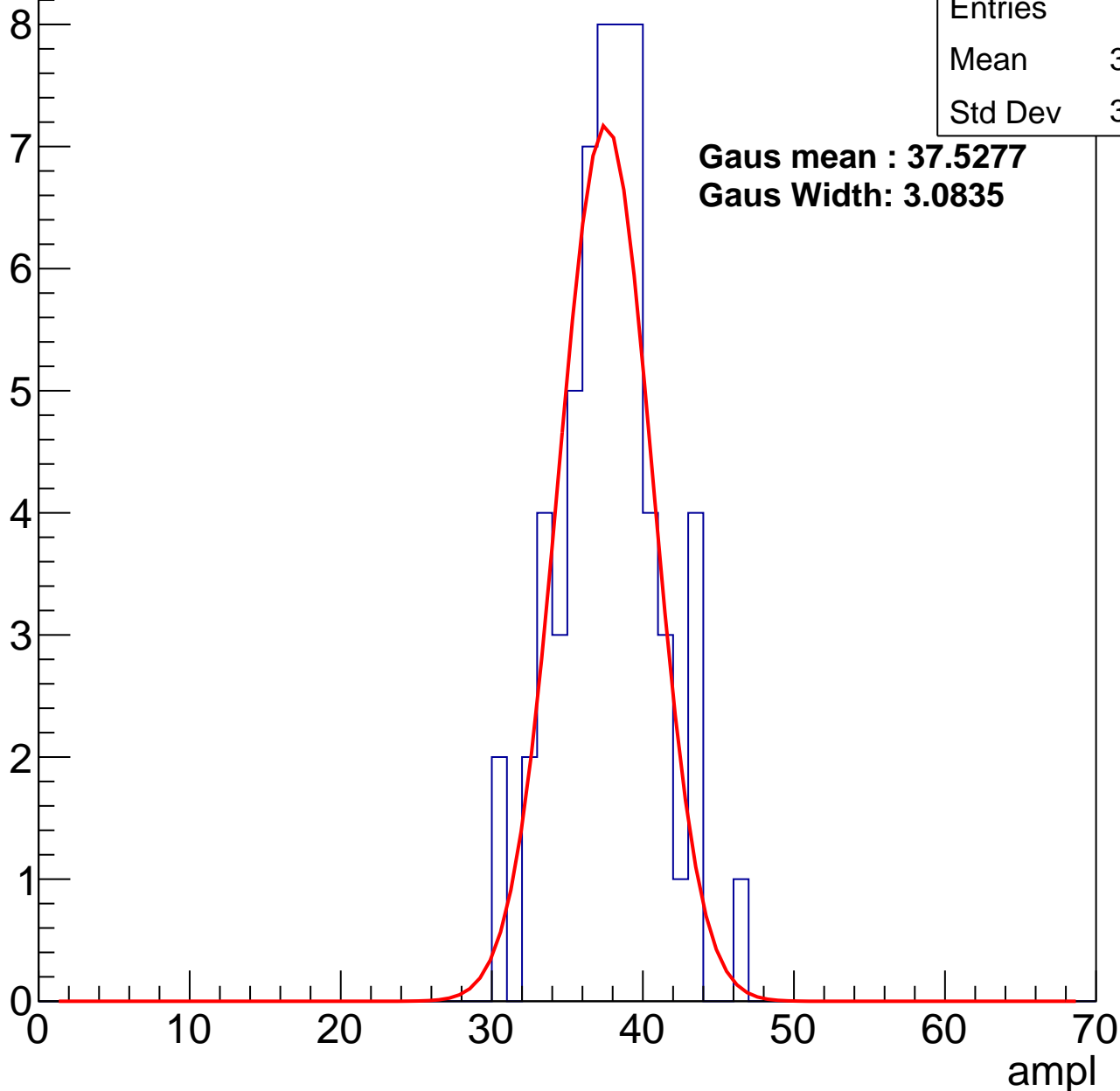
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.33
Std Dev	3.249

**Gaus mean : 37.5277**

**Gaus Width: 3.0835**



# B0L002S, U23-ch56, adc2

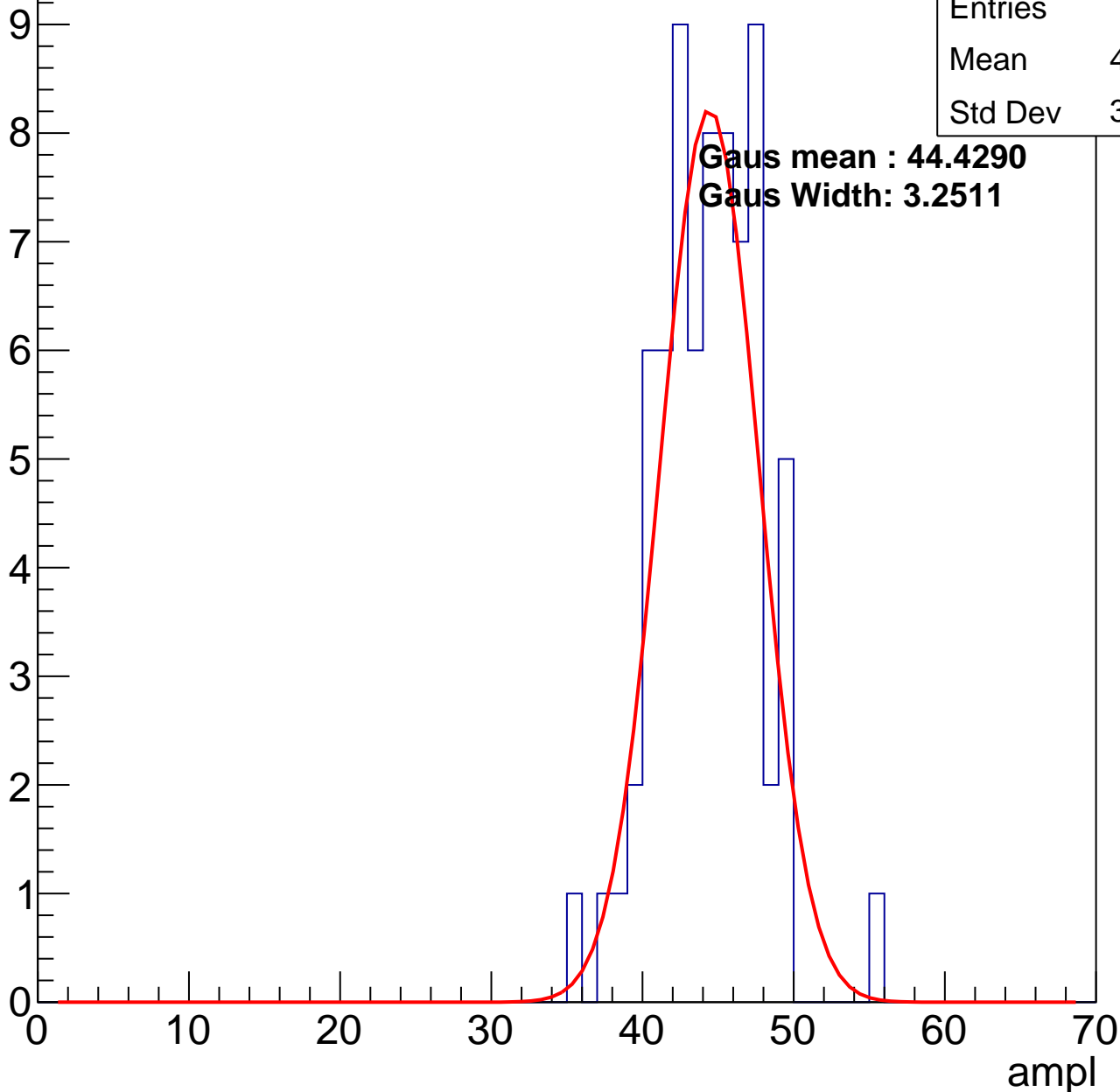
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.93
Std Dev	3.339

**Gaus mean : 44.4290**

**Gaus Width: 3.2511**



# B0L002S, U23-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	79
Mean	51.62
Std Dev	3.304

Entry

10

8

6

4

2

0

0

10

20

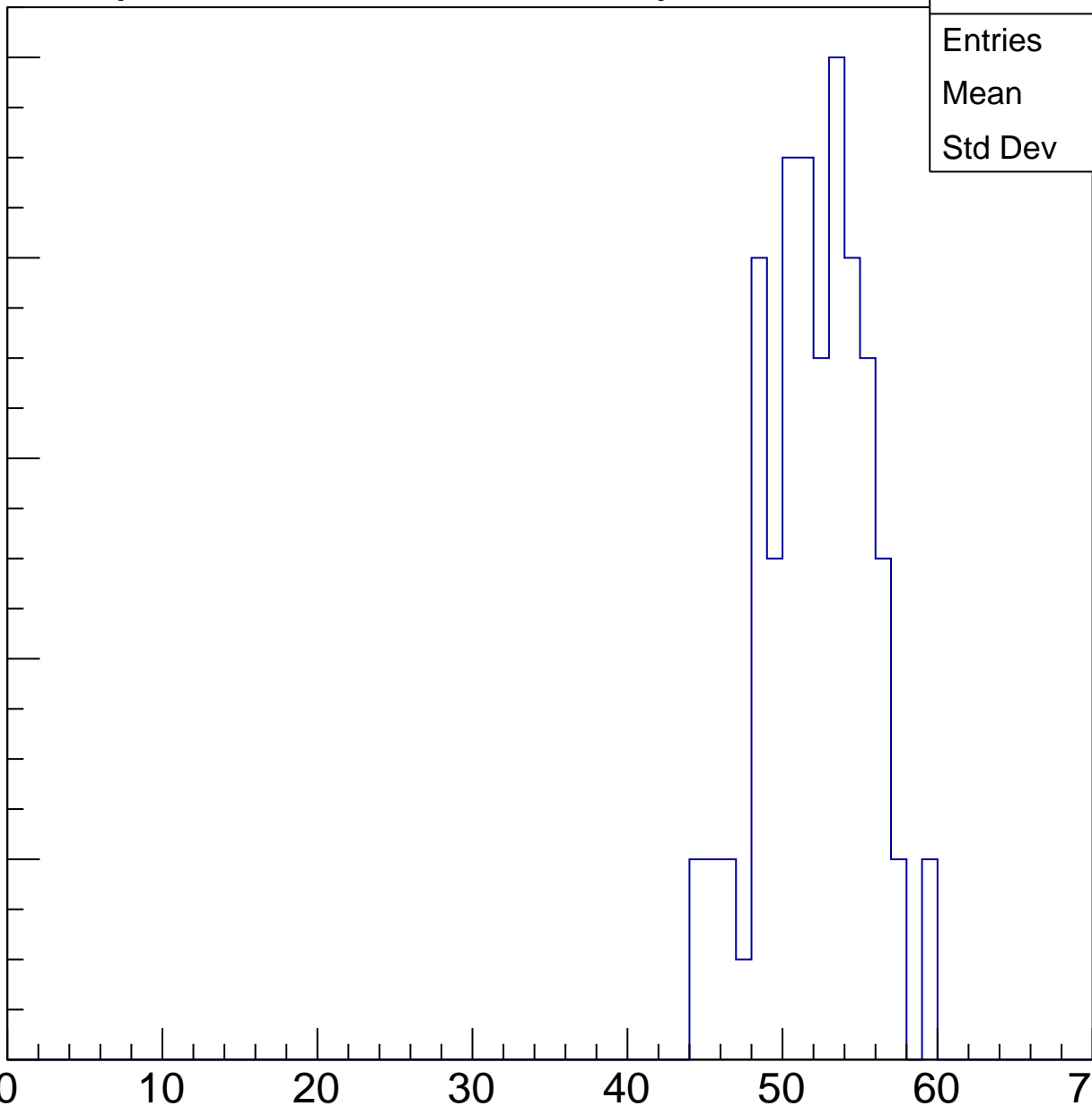
30

40

50

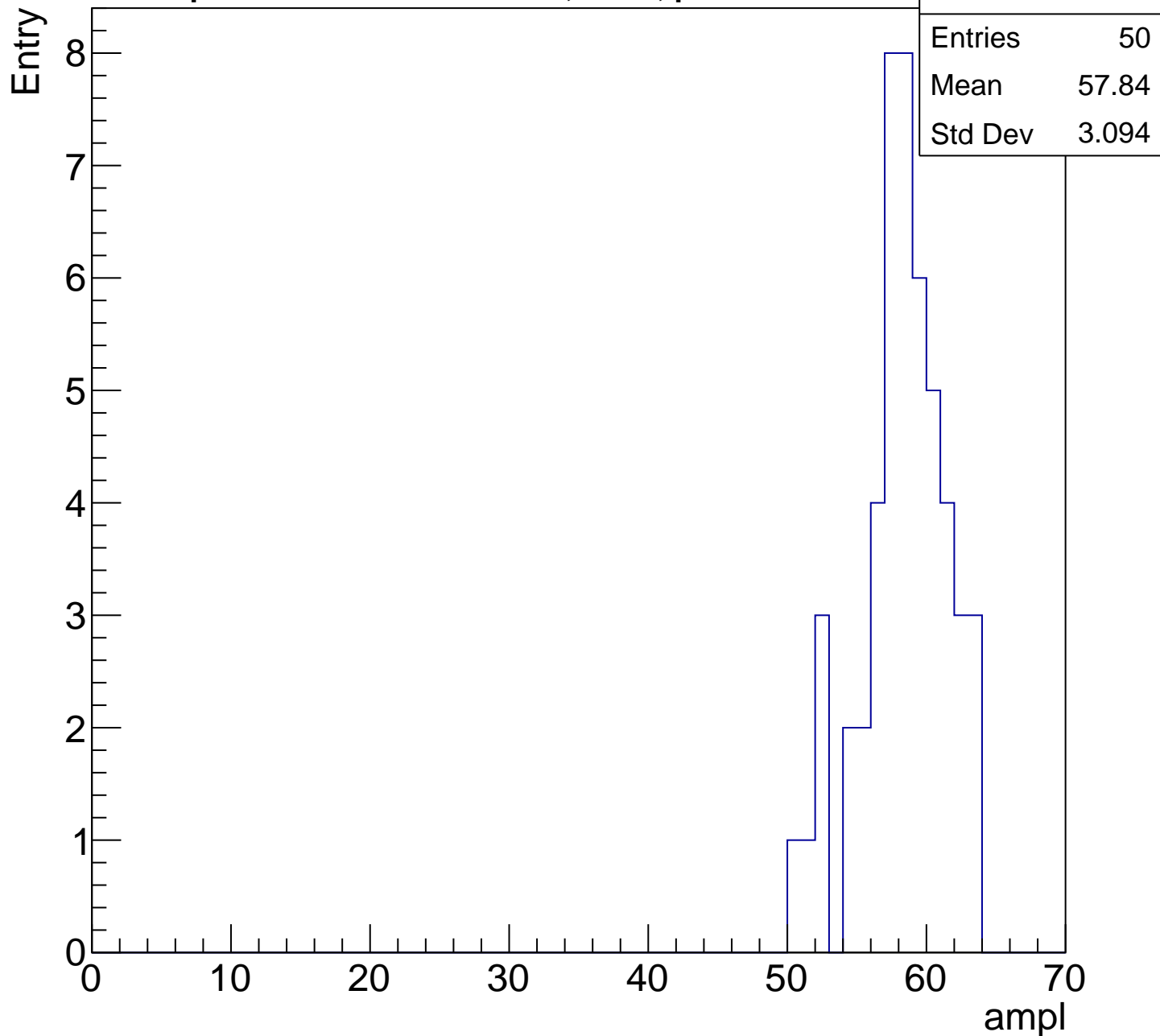
60

ampl



# B0L002S, U23-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

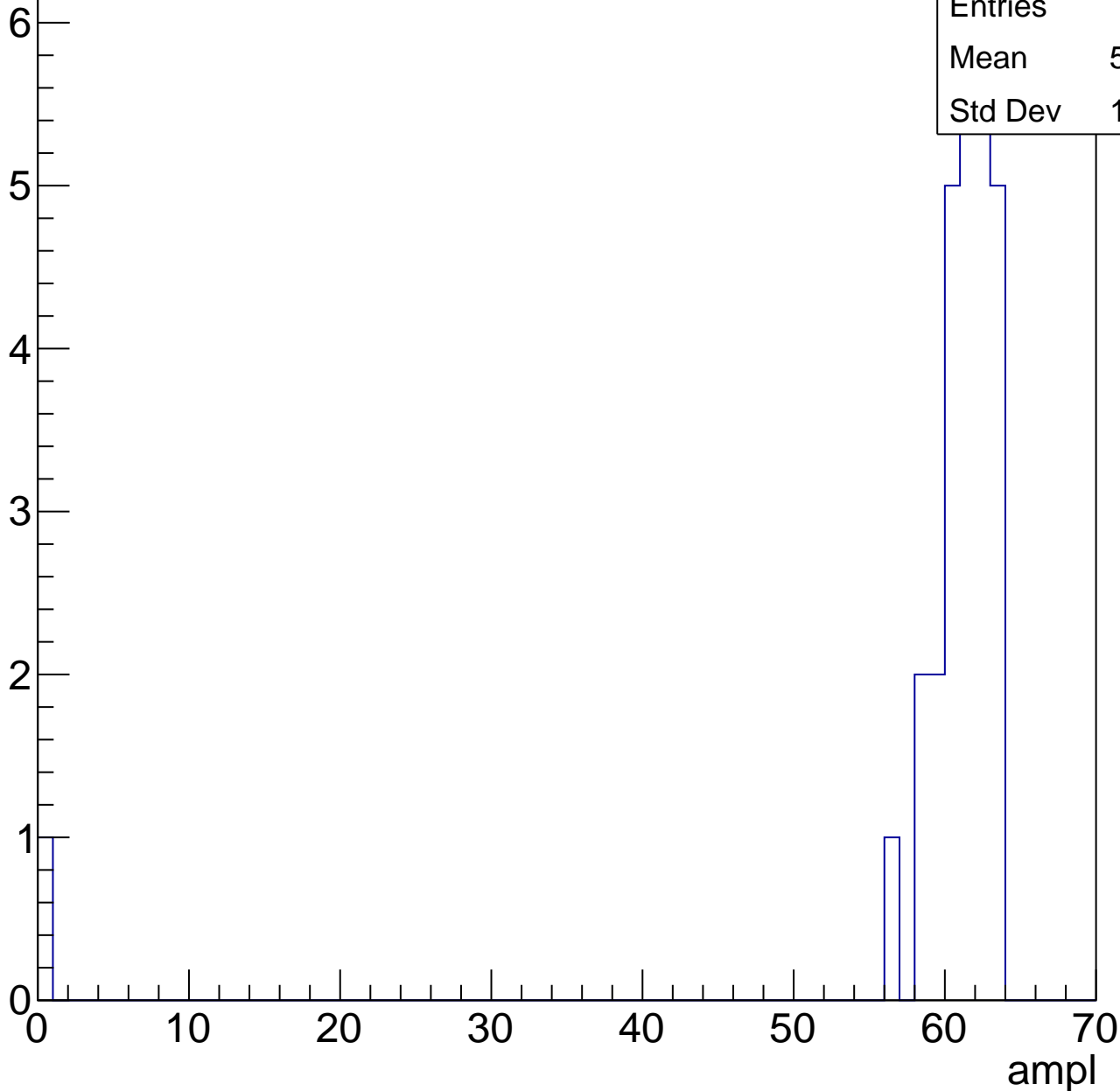


# B0L002S, U23-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.68
Std Dev	11.42



# B0L002S, U23-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

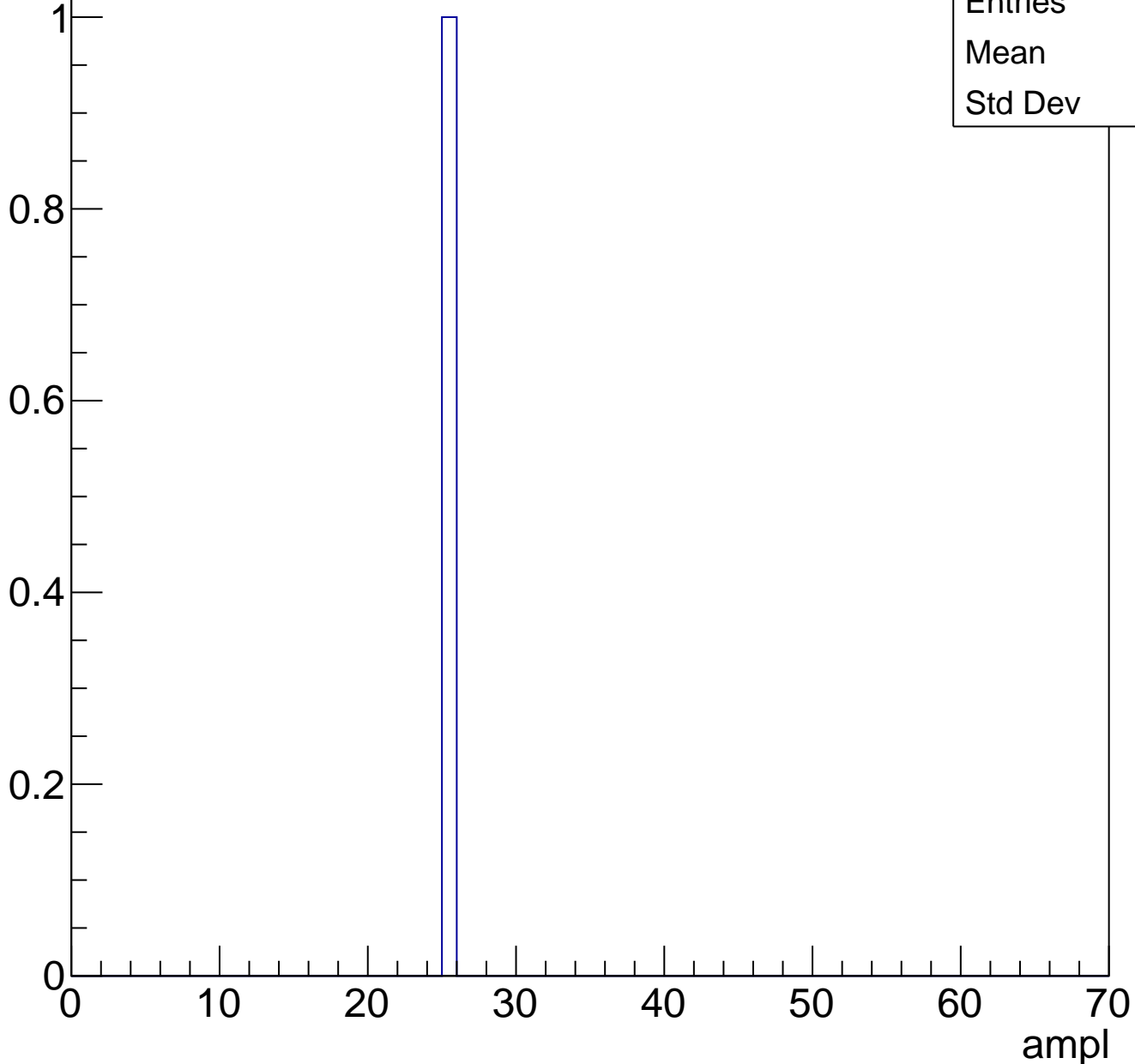
0 10 20 30 40 50 60 70



# B0L002S, U23-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L002S, U23-ch57, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	70
Mean	30.1
Std Dev	3.419

**Gaus mean : 30.5887**

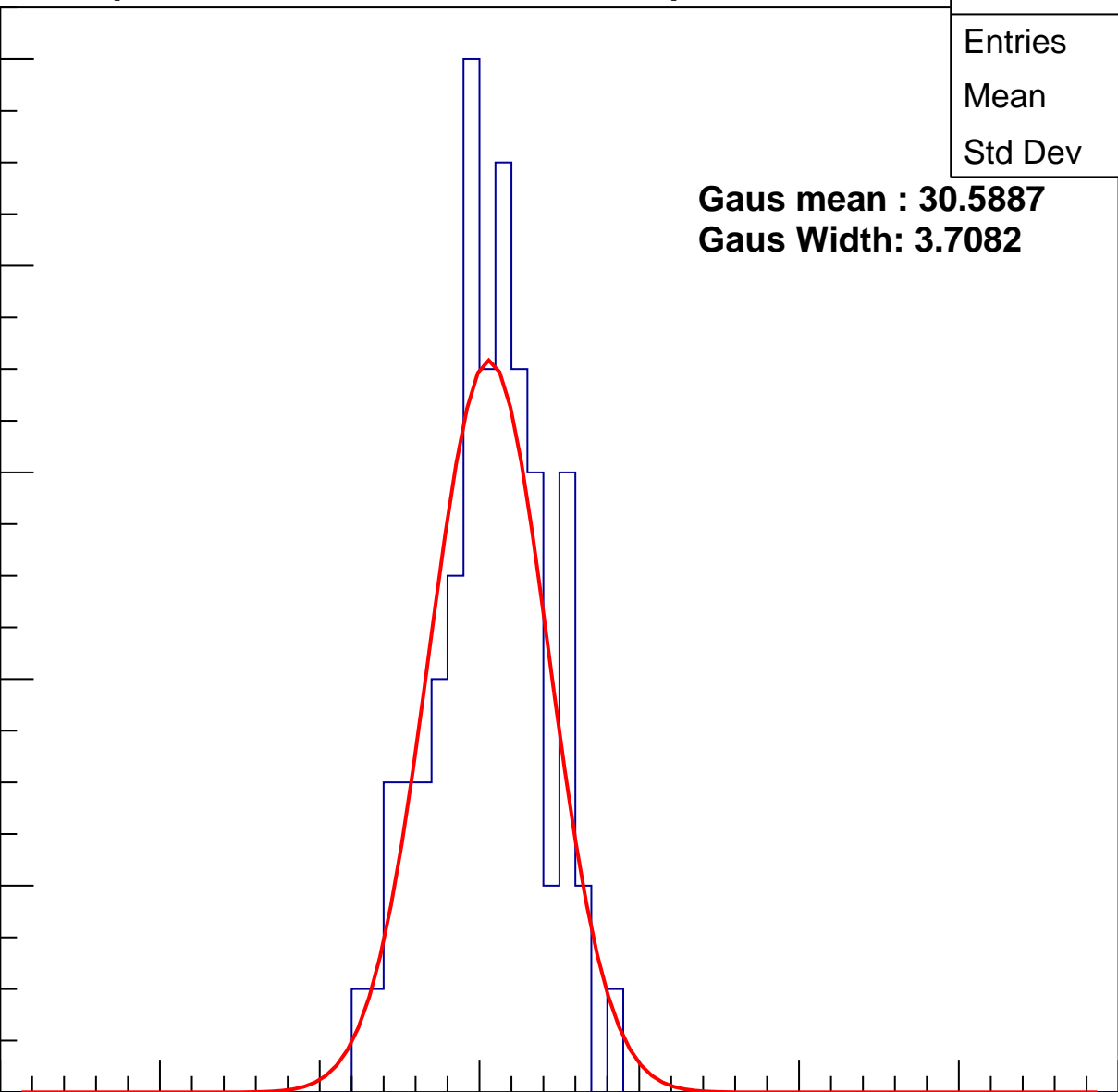
**Gaus Width: 3.7082**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch57, adc1

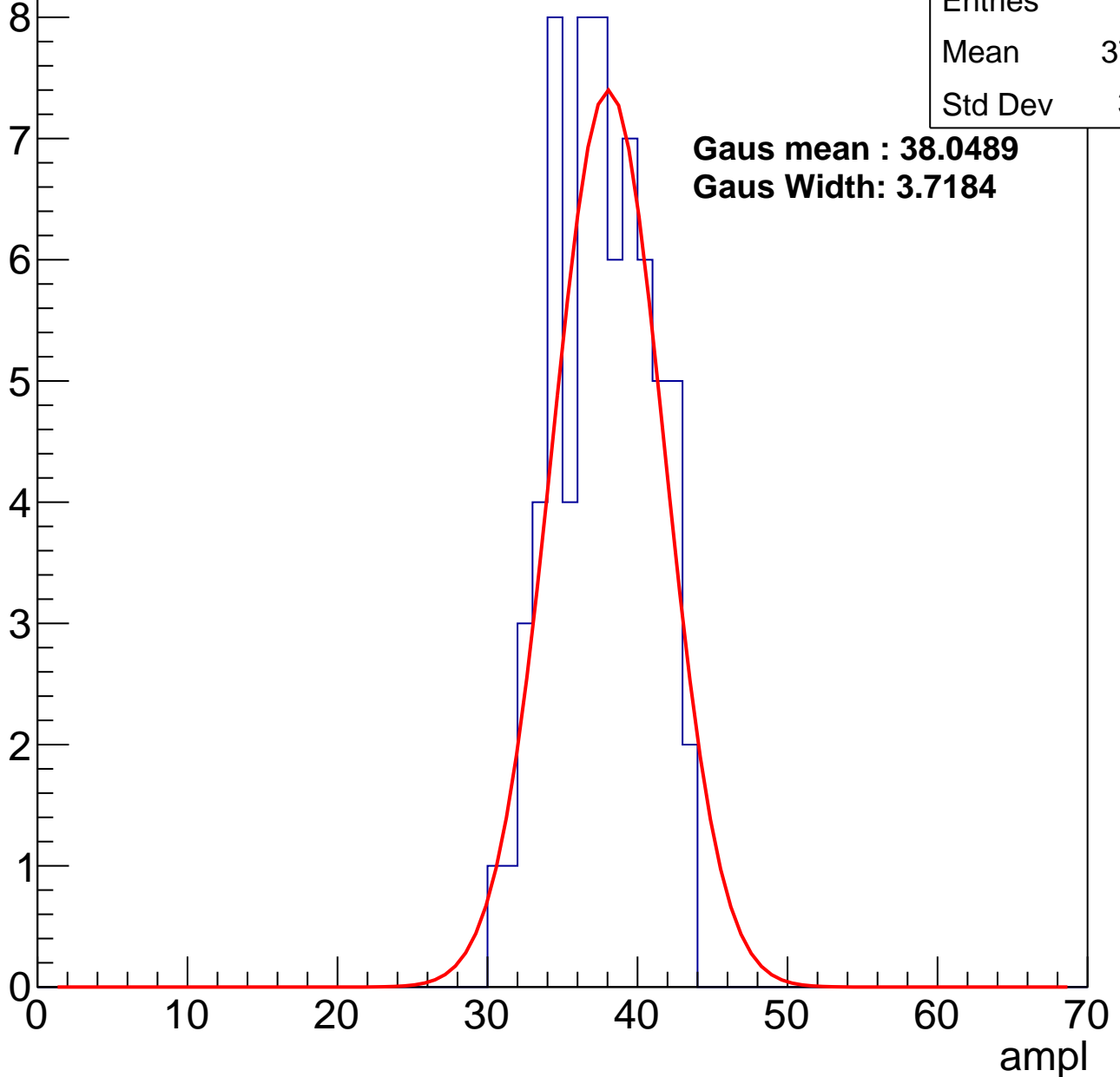
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.16
Std Dev	3.17

**Gaus mean : 38.0489**

**Gaus Width: 3.7184**



# B0L002S, U23-ch57, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	72
Mean	44.15
Std Dev	3.398

**Gaus mean : 44.8450**

**Gaus Width: 3.8775**

Entry

10

8

6

4

2

0

0

10

20

30

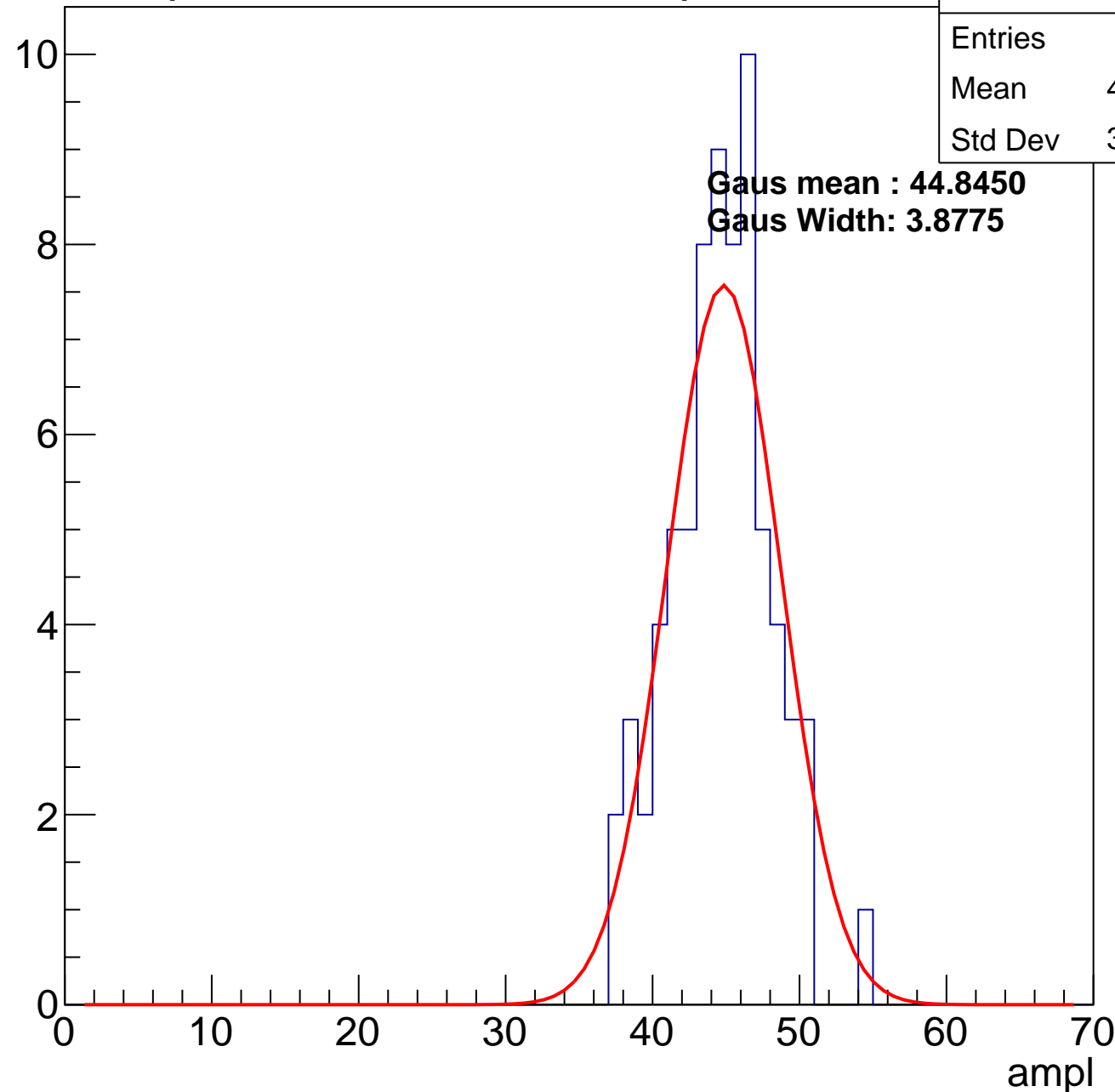
40

50

60

70

ampl

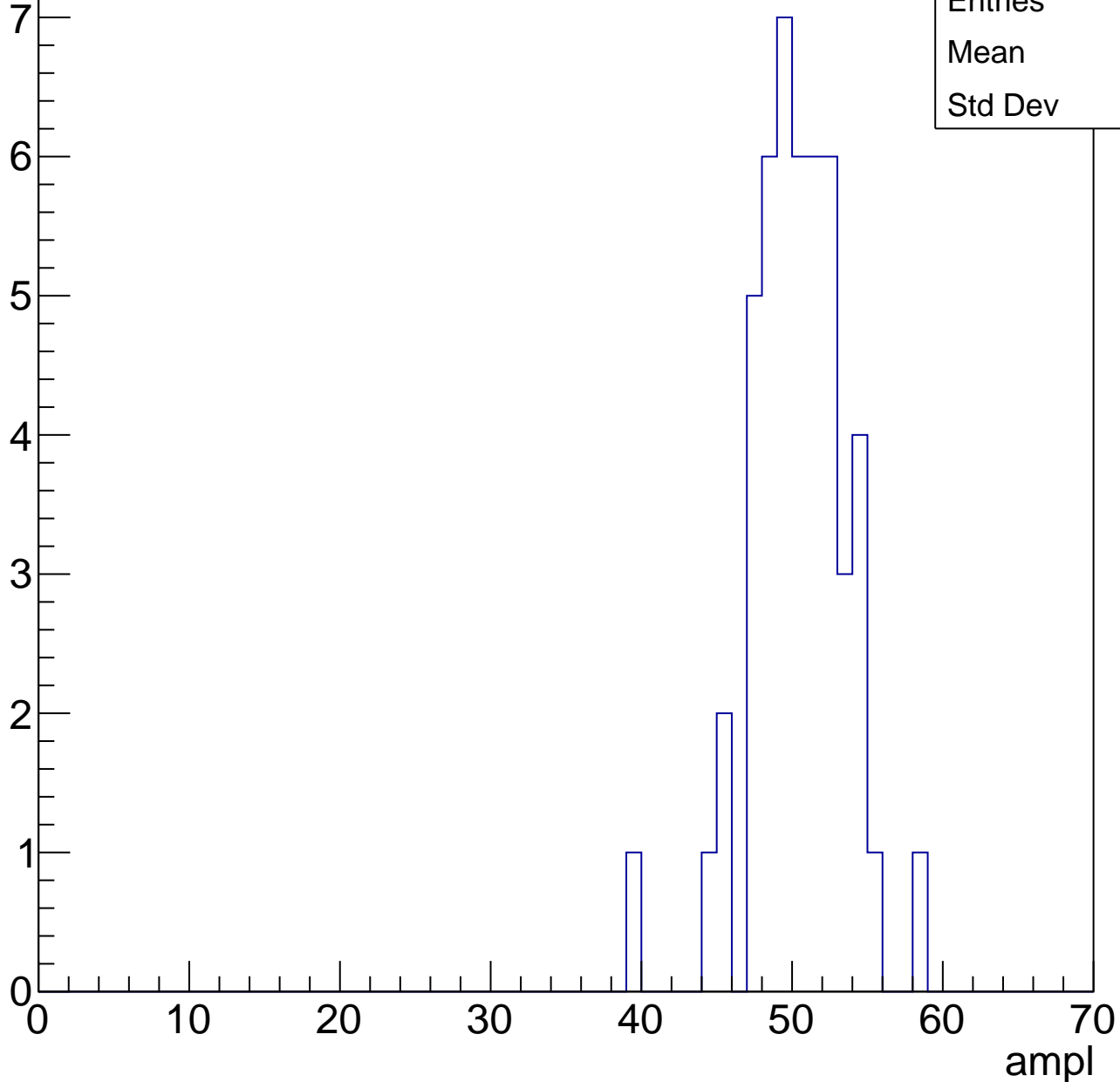


# B0L002S, U23-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

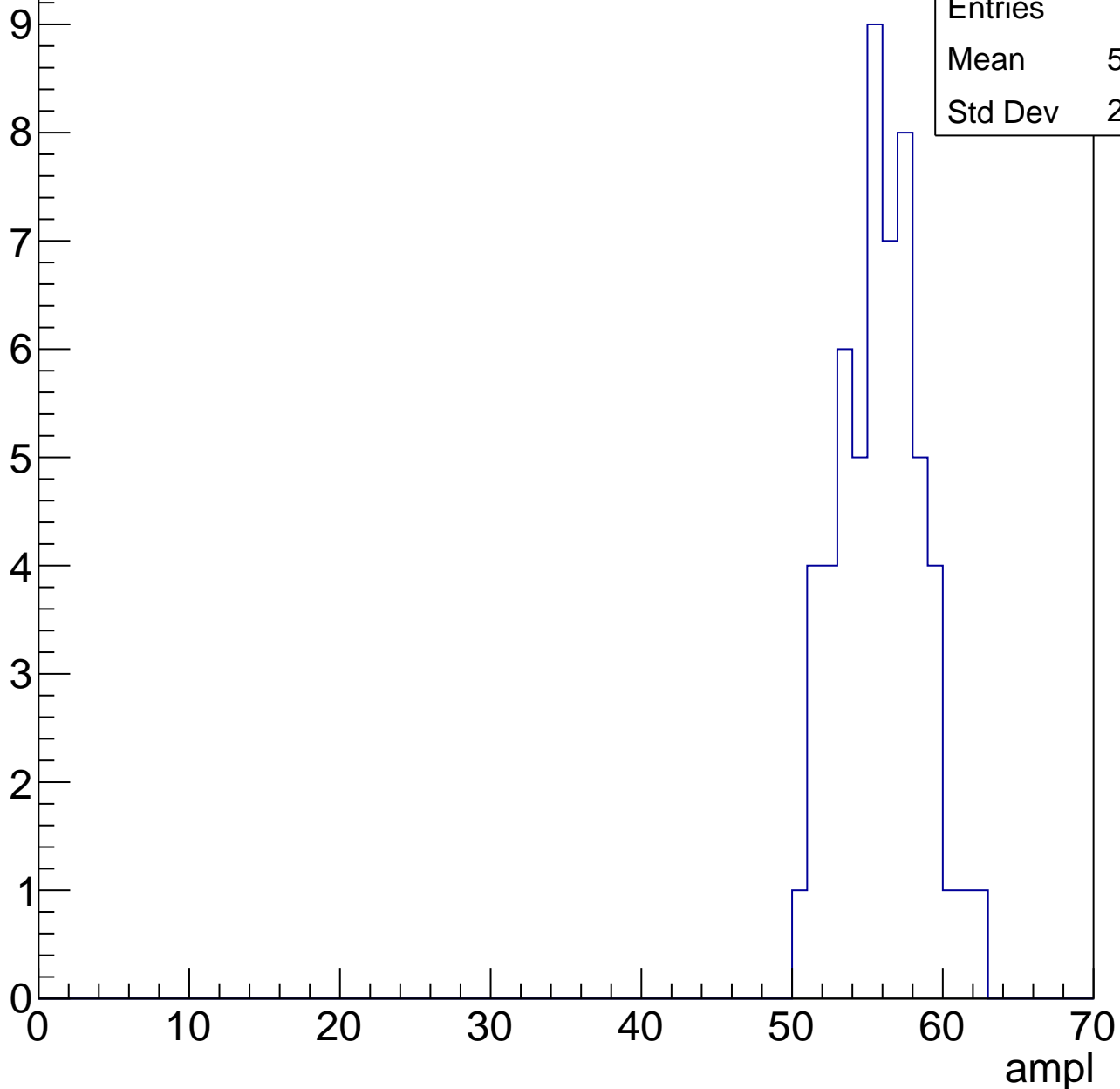
Entries	49
Mean	49.9
Std Dev	3.17



# B0L002S, U23-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	59.08
Std Dev	8.559

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

# B0L002S, U23-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch58, adc0

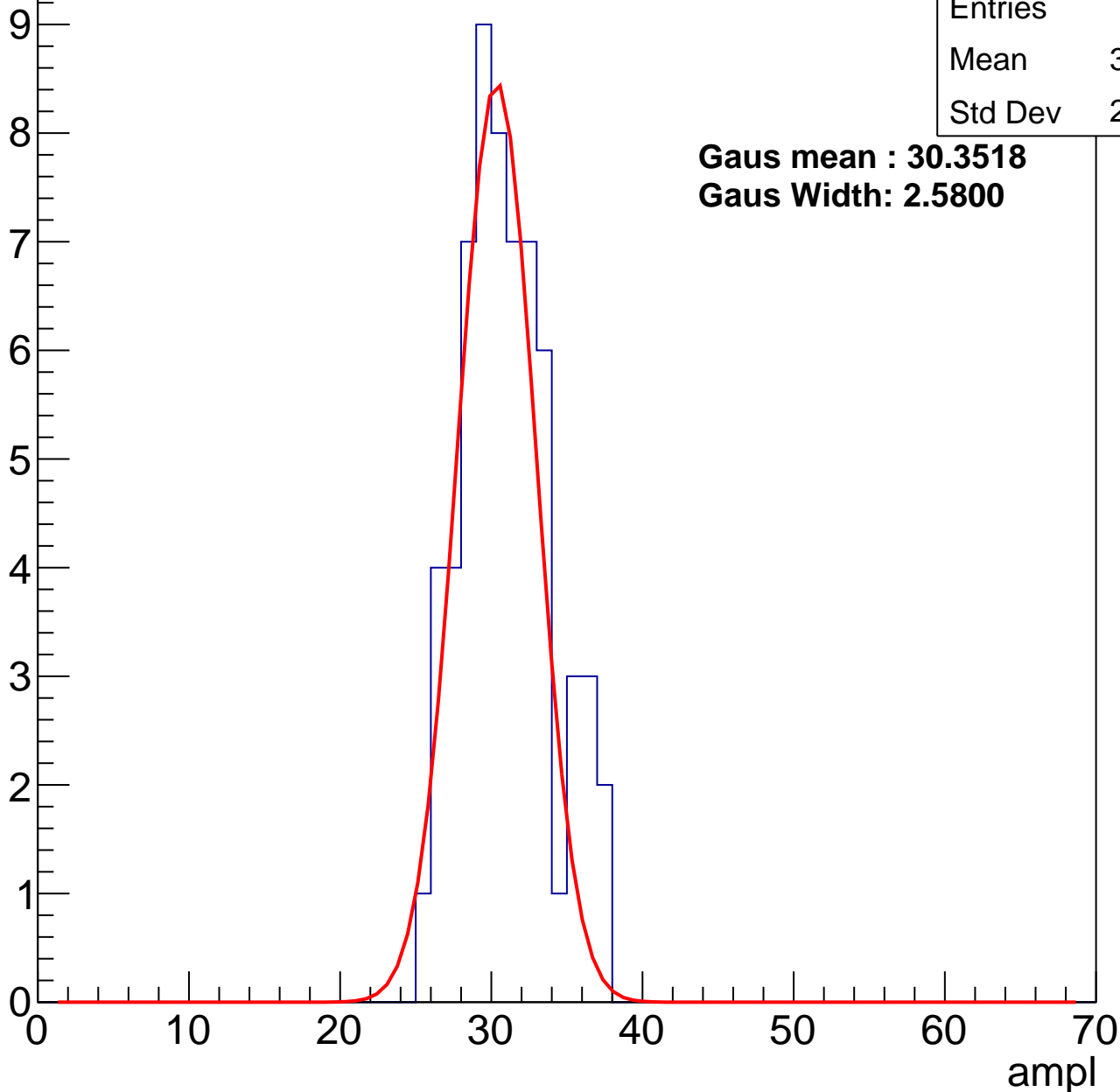
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.55
Std Dev	2.922

**Gaus mean : 30.3518**

**Gaus Width: 2.5800**



# B0L002S, U23-ch58, adc1

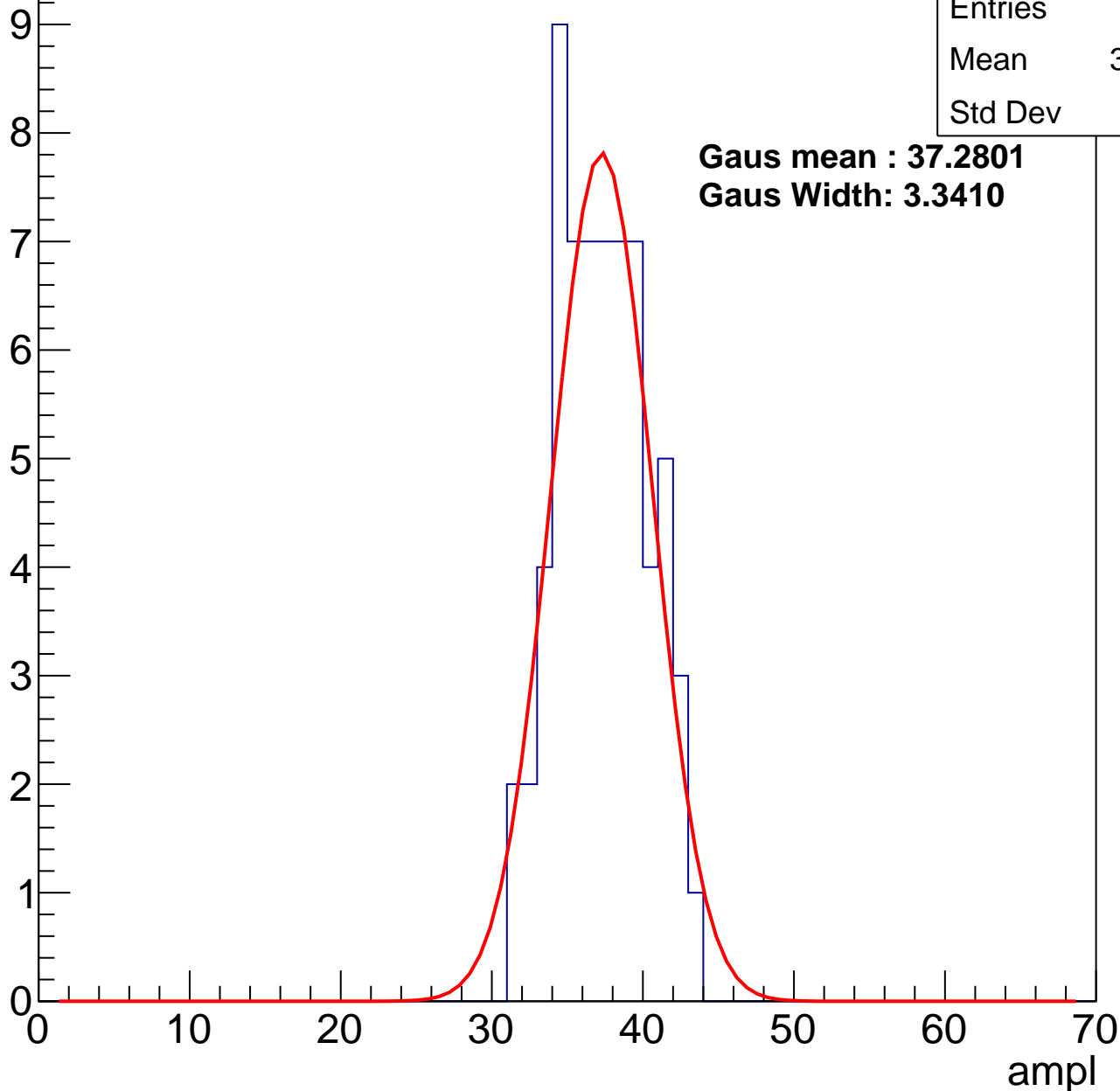
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.82
Std Dev	2.94

**Gaus mean : 37.2801**

**Gaus Width: 3.3410**



# B0L002S, U23-ch58, adc2

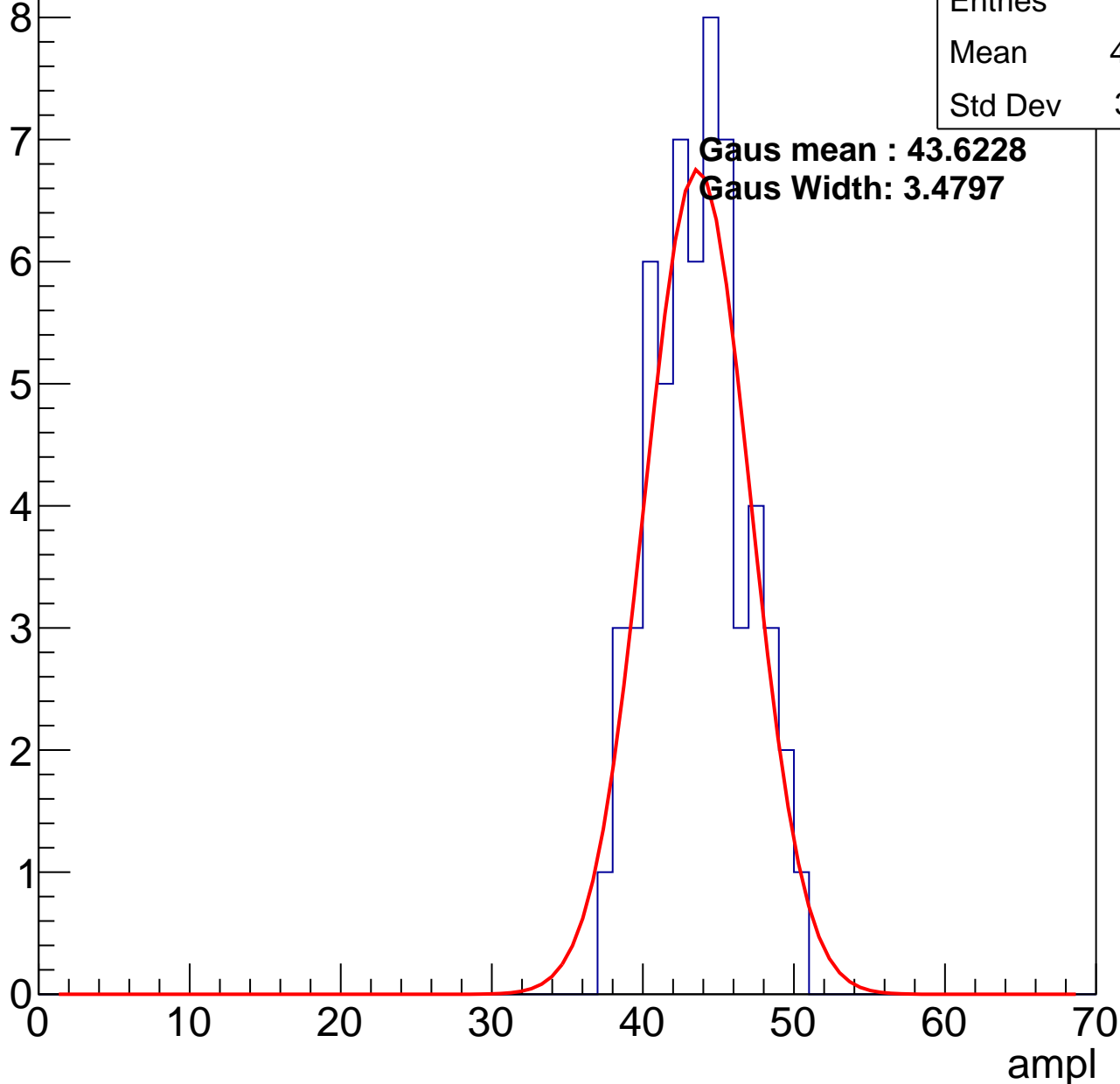
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.22
Std Dev	3.081

**Gaus mean : 43.6228**

**Gaus Width: 3.4797**

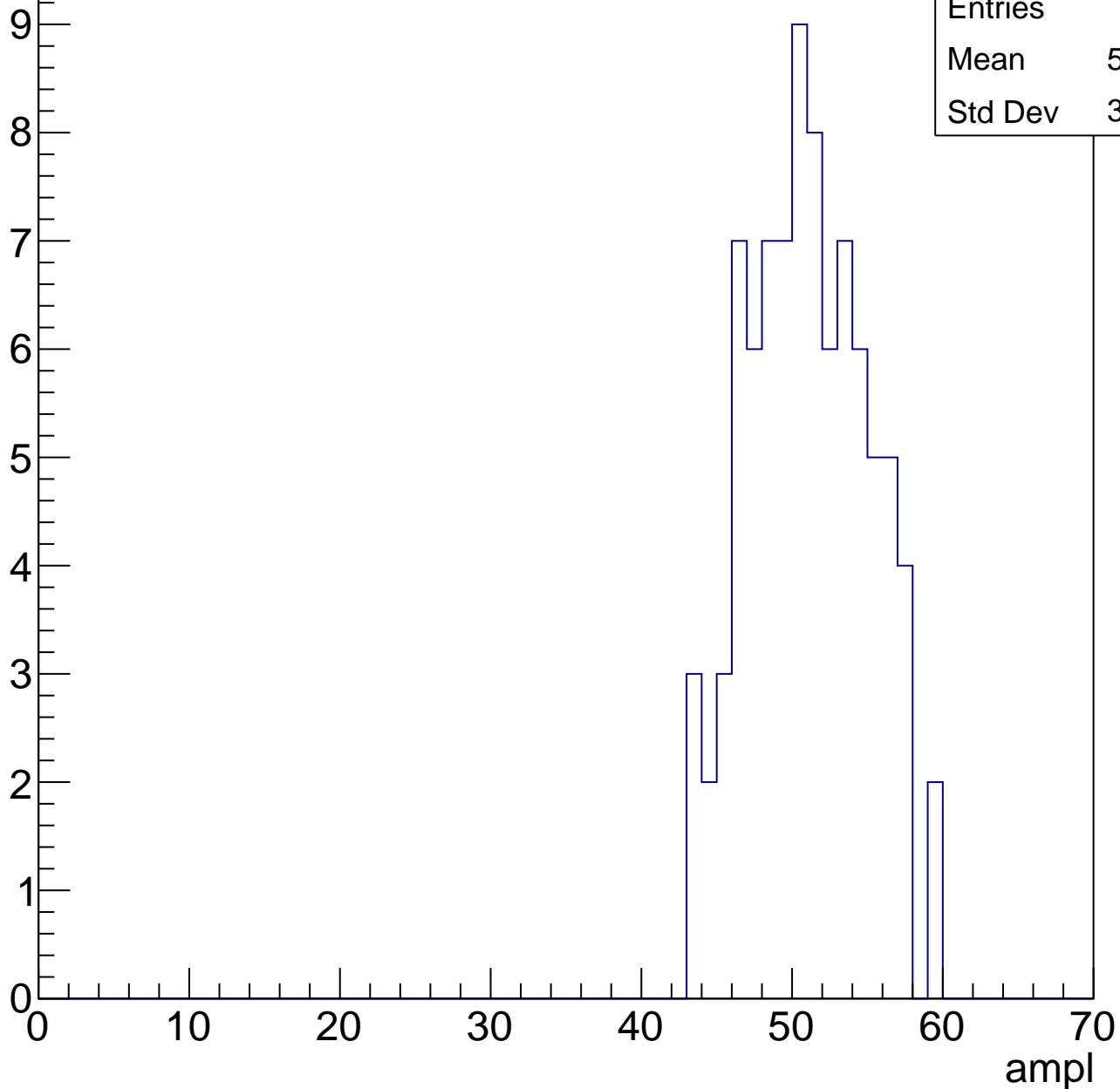


# B0L002S, U23-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	87
Mean	50.59
Std Dev	3.894

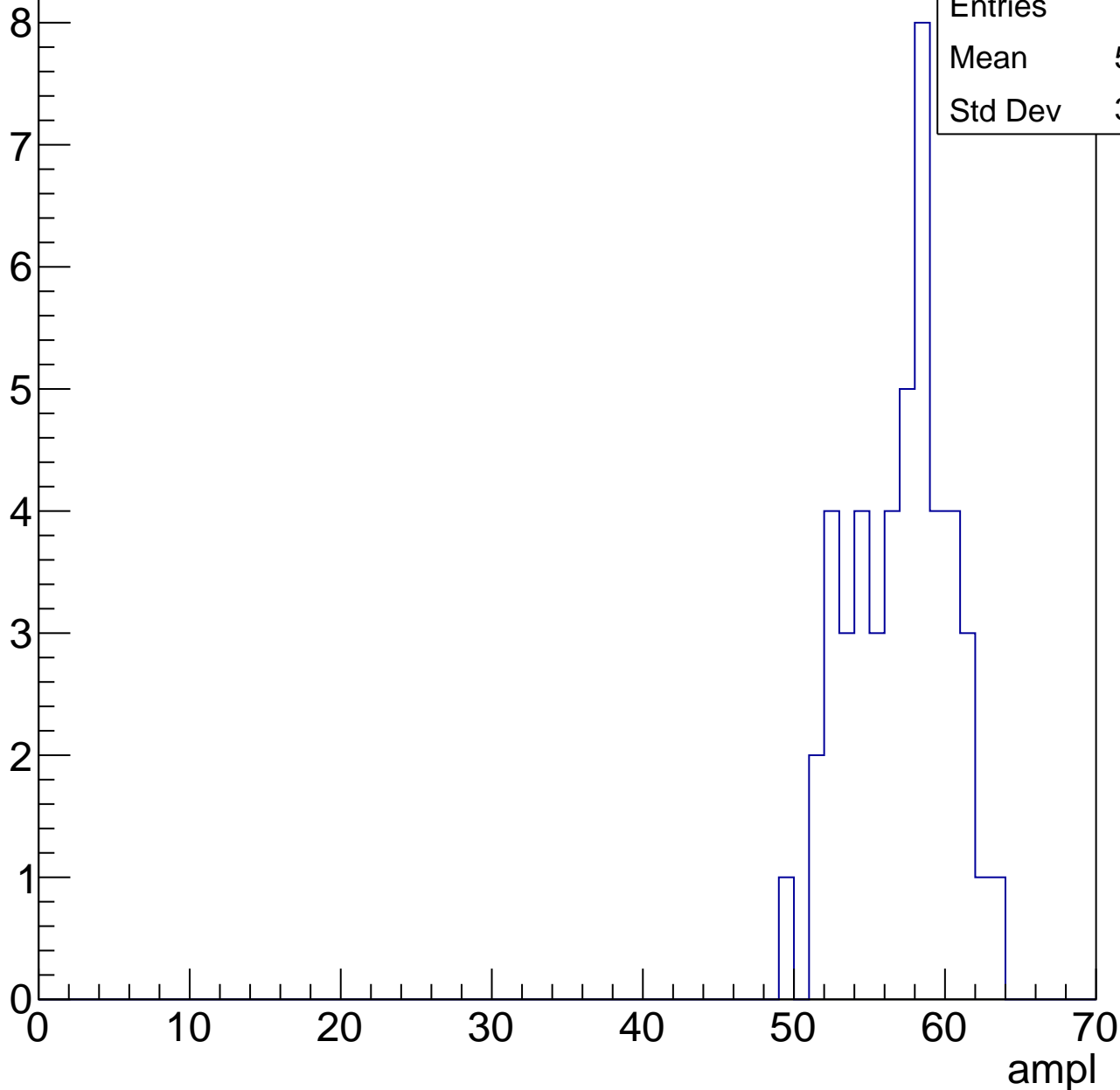


# B0L002S, U23-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	56.51
Std Dev	3.241

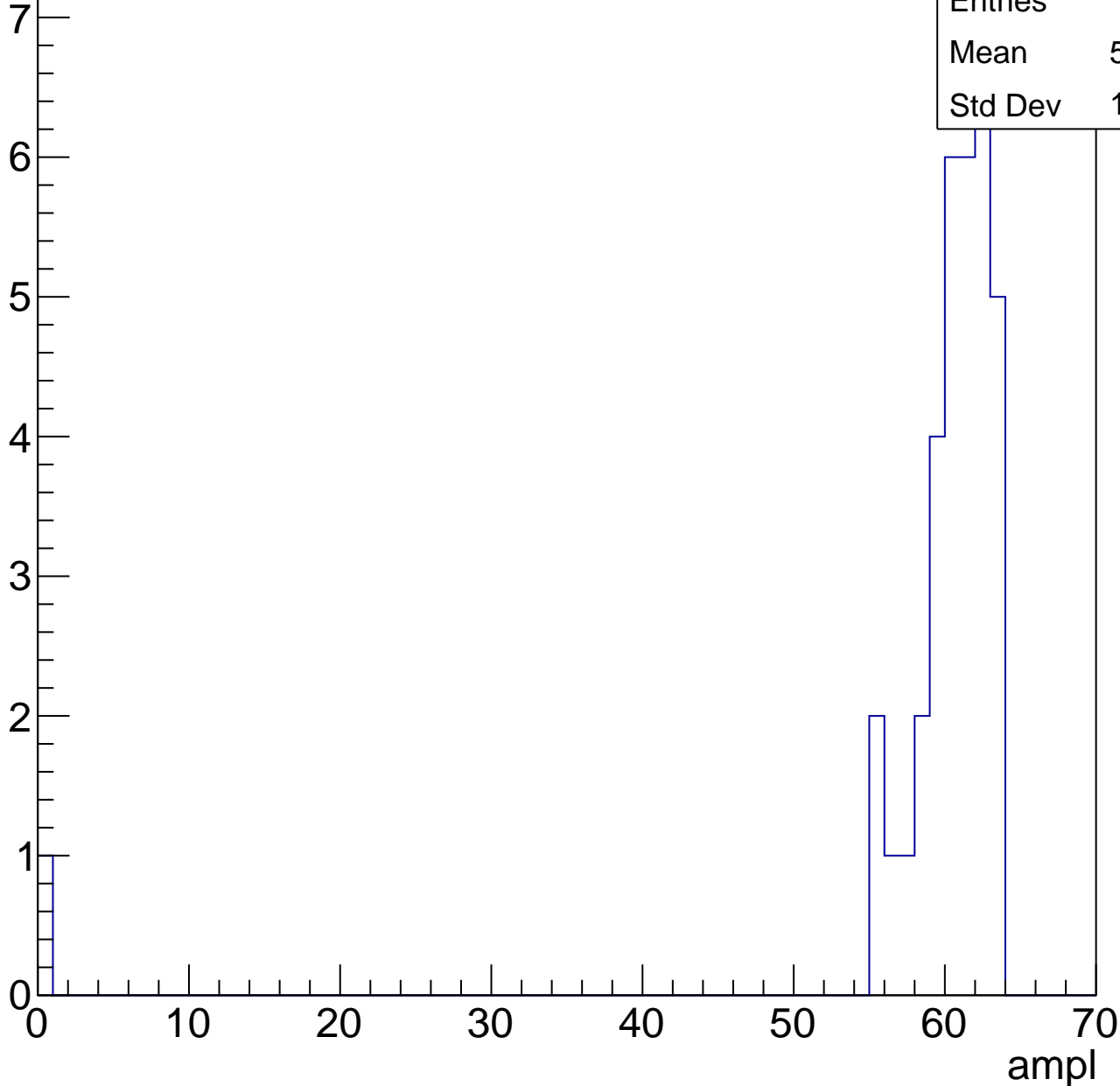


# B0L002S, U23-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.57
Std Dev	10.27



# B0L002S, U23-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

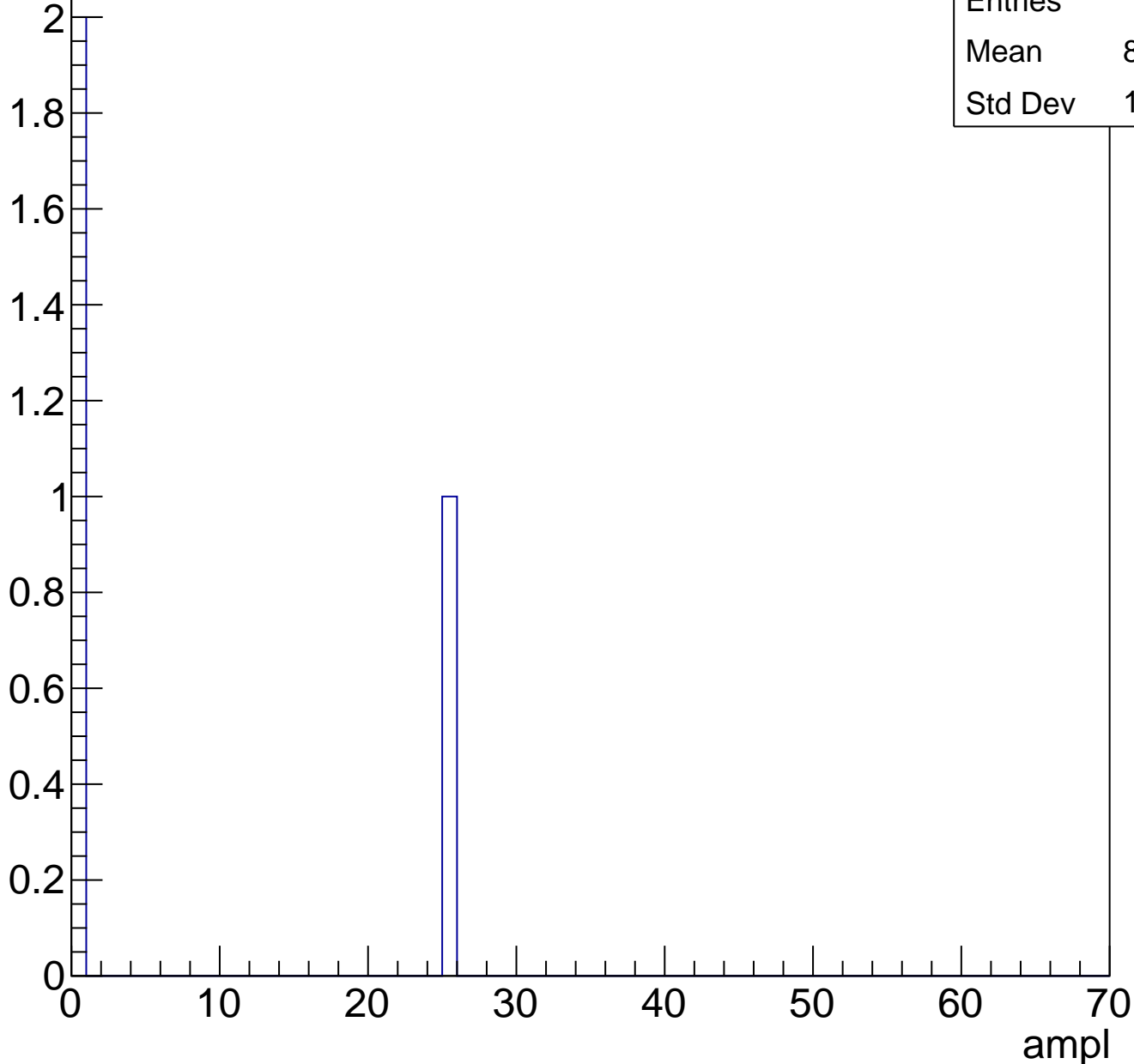




# B0L002S, U23-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

# B0L002S, U23-ch59, adc0

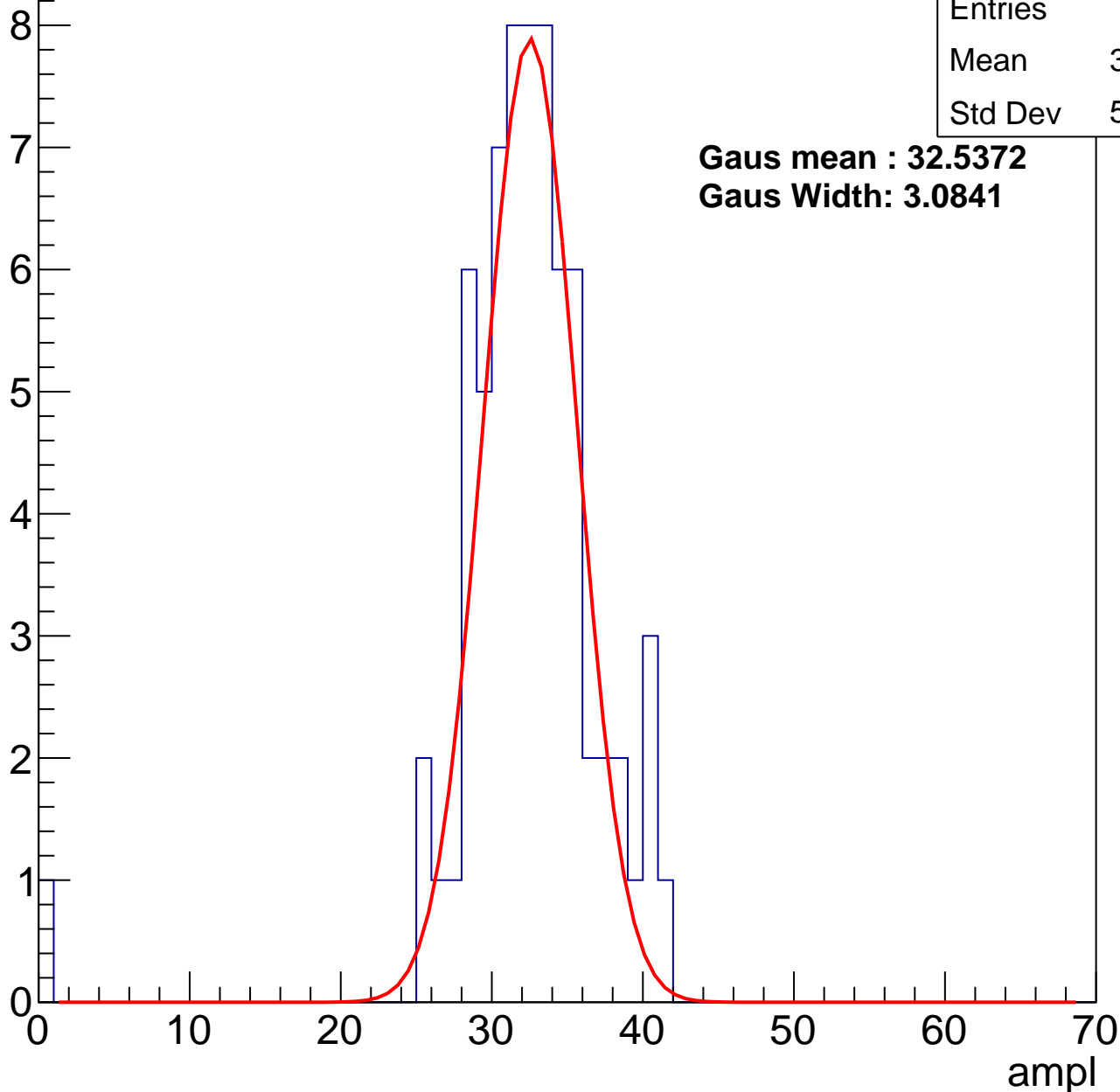
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	31.86
Std Dev	5.238

**Gaus mean : 32.5372**

**Gaus Width: 3.0841**



# B0L002S, U23-ch59, adc1

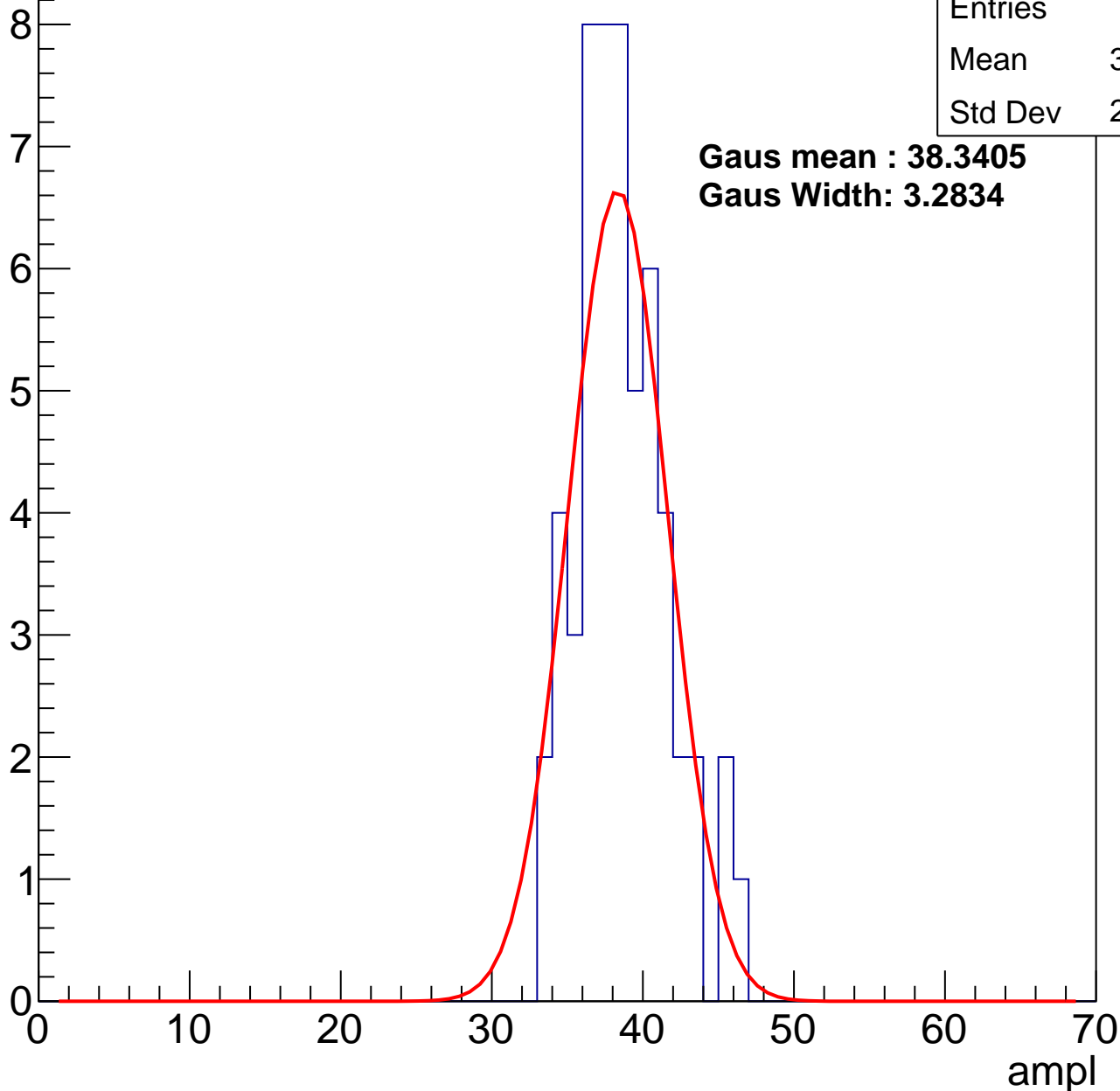
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	38.18
Std Dev	2.979

**Gaus mean : 38.3405**

**Gaus Width: 3.2834**



# B0L002S, U23-ch59, adc2

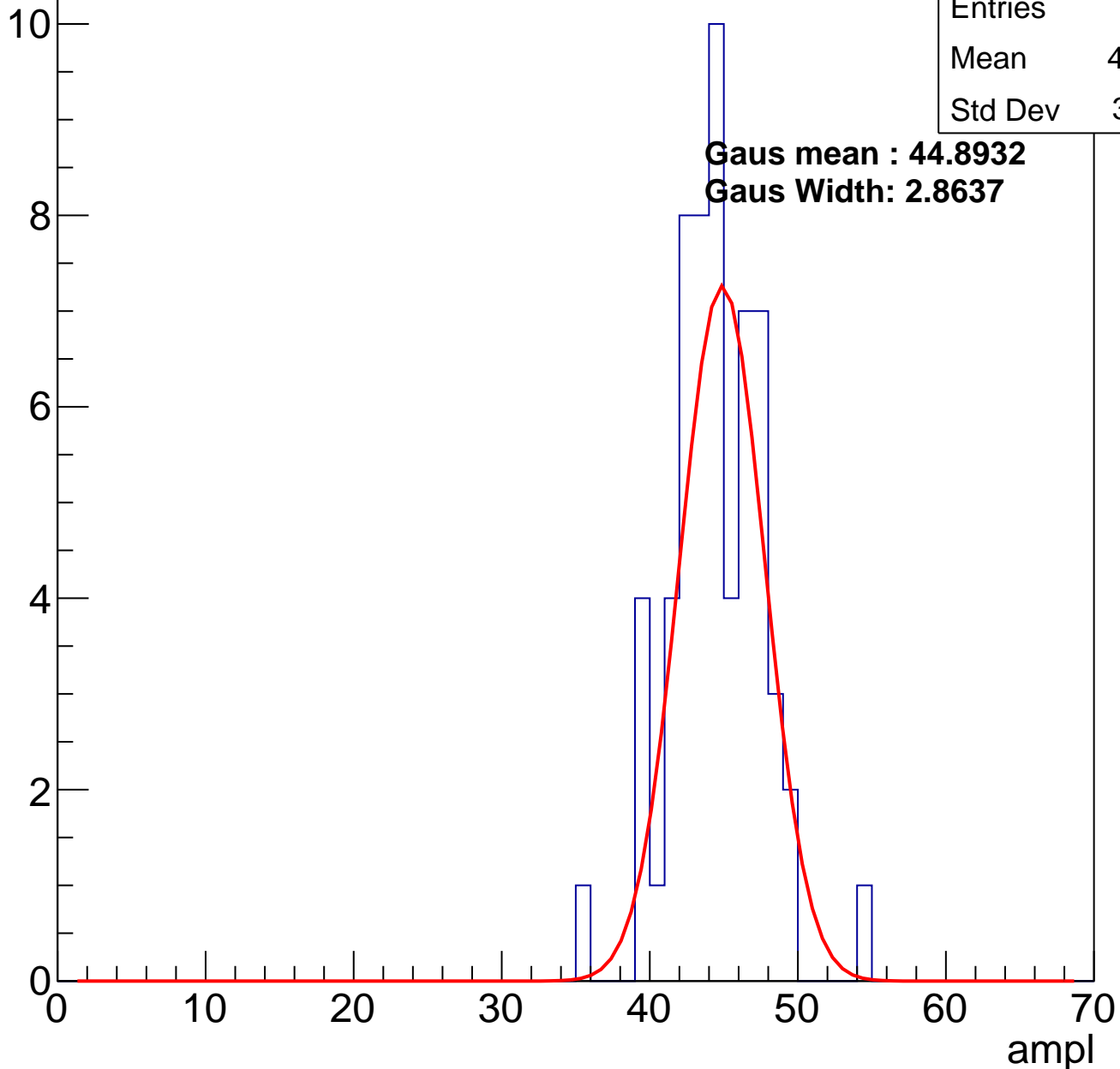
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	60
Mean	44.03
Std Dev	3.071

**Gaus mean : 44.8932**

**Gaus Width: 2.8637**

Entry



# B0L002S, U23-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	67
Mean	50.6
Std Dev	3.274

Entry

10

8

6

4

2

0

0

10

20

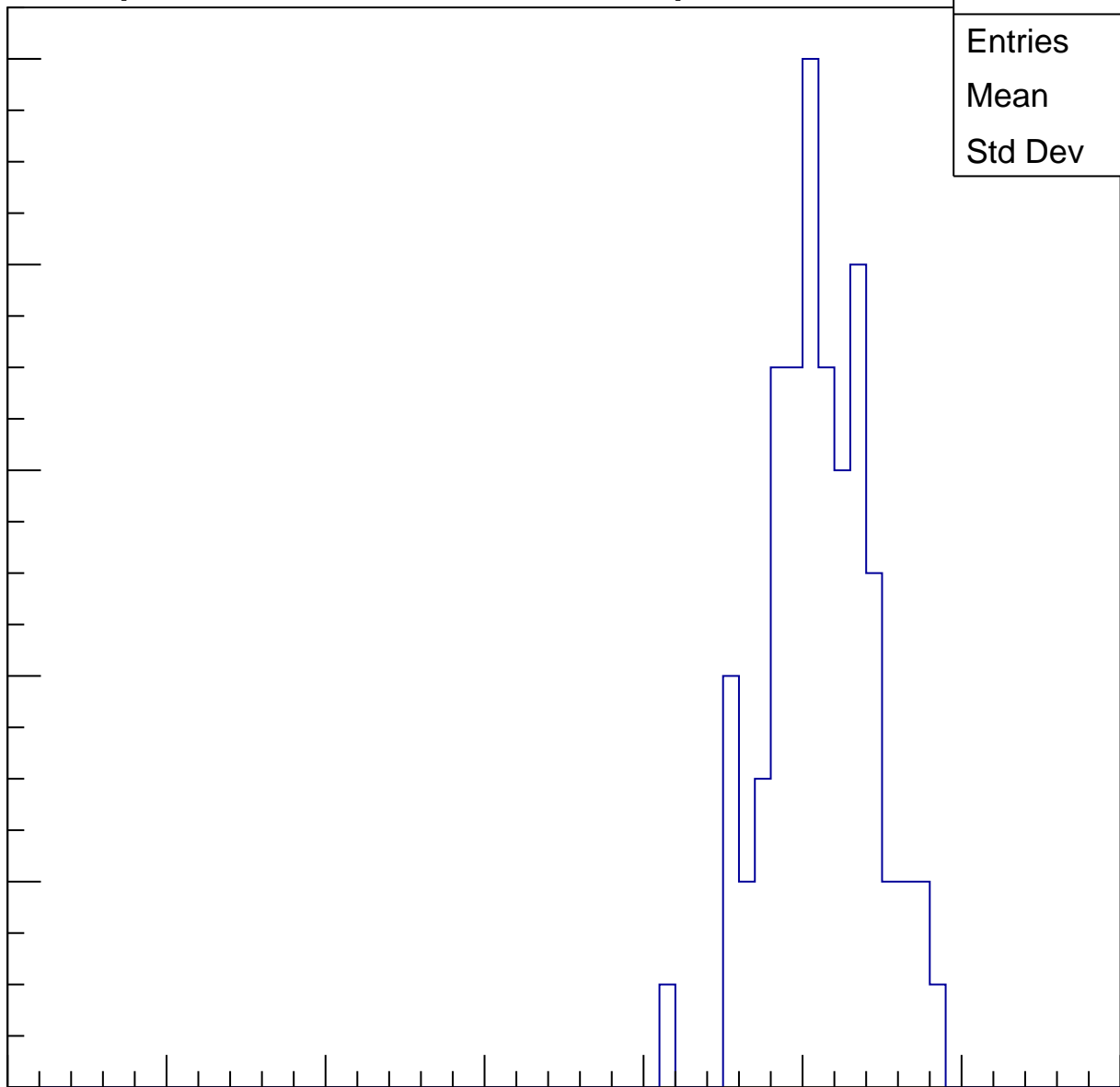
30

40

50

60

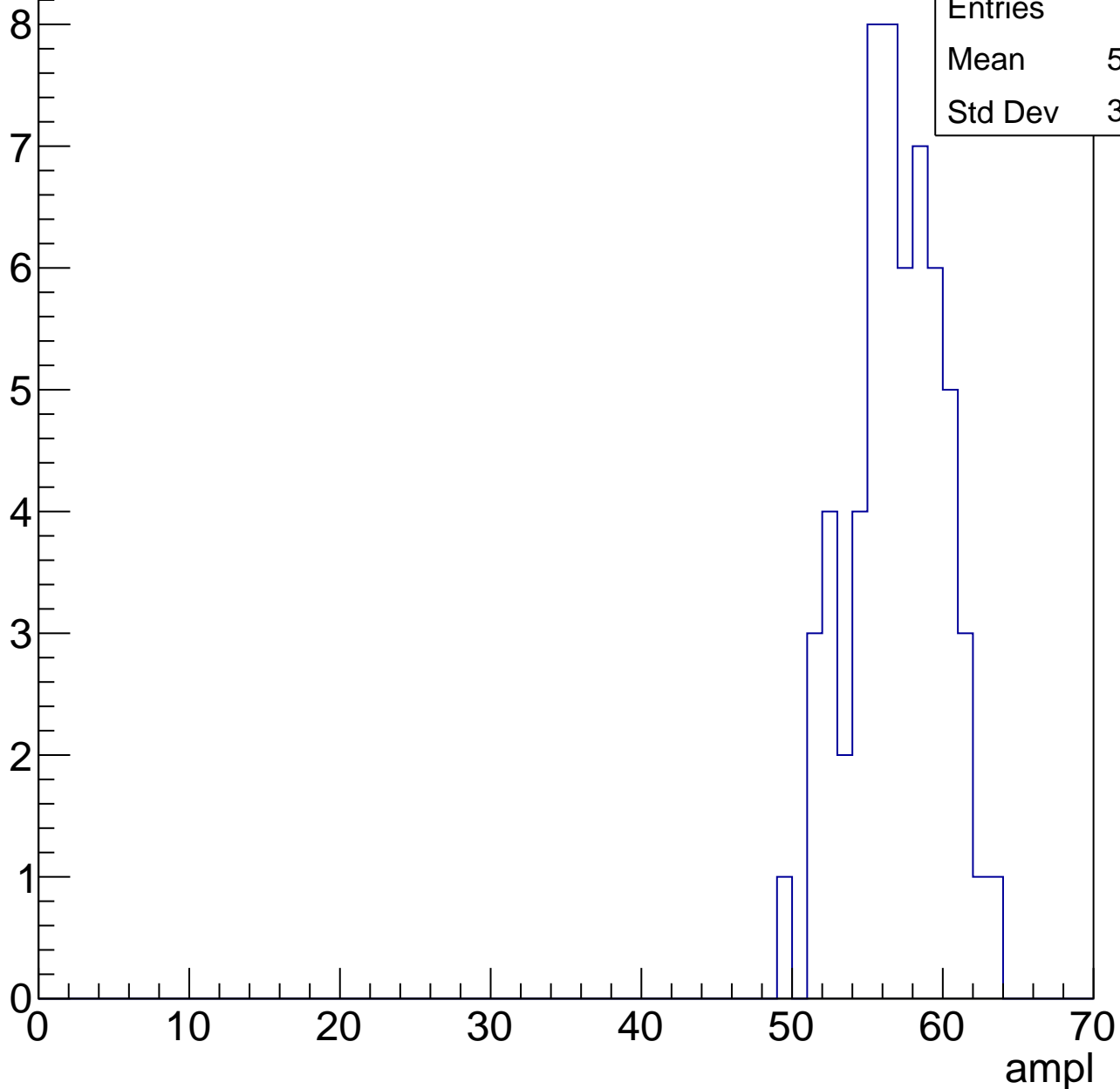
ampl



# B0L002S, U23-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	59
Mean	56.44
Std Dev	3.044

# B0L002S, U23-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

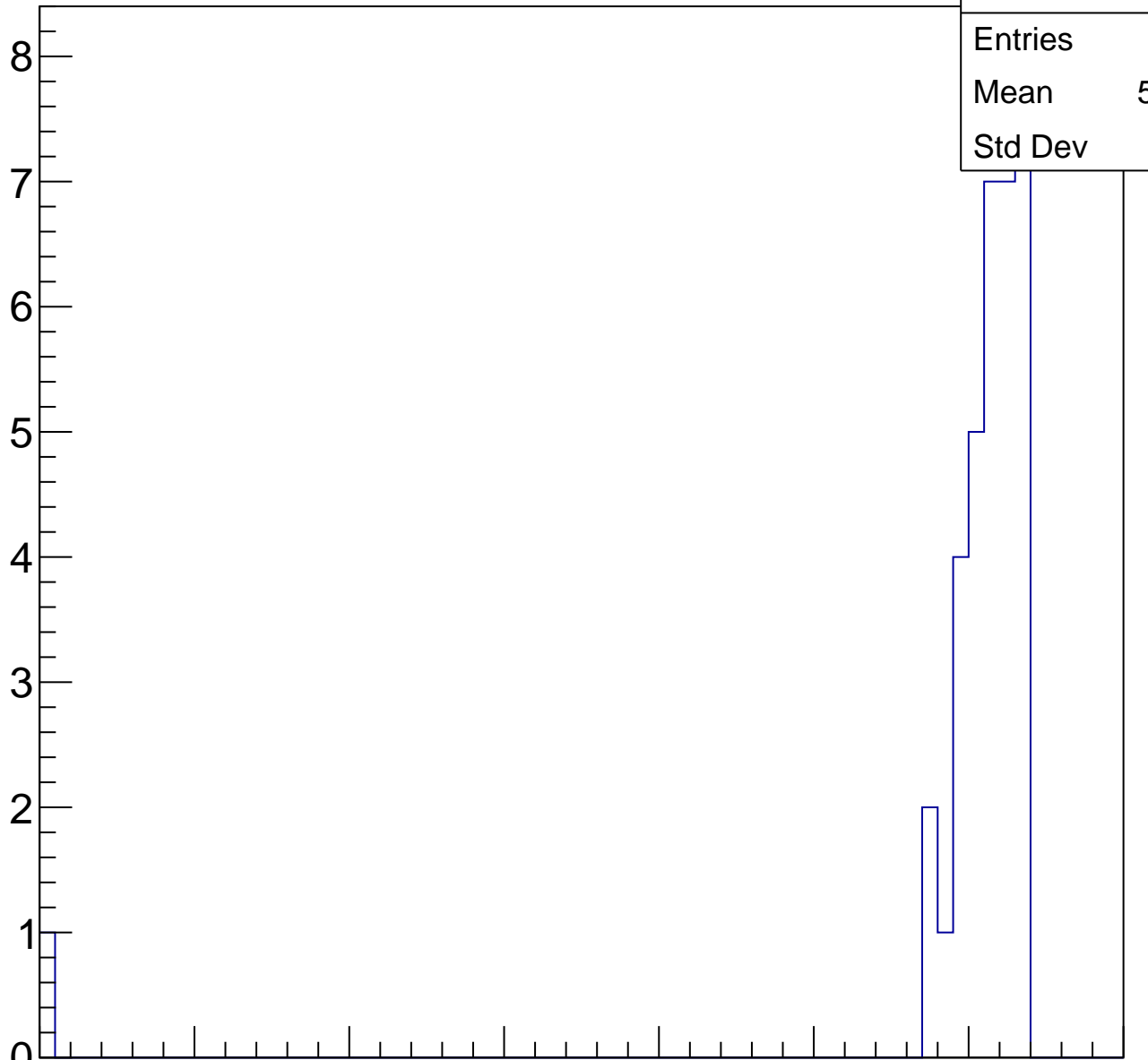
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	35
Mean	59.23
Std Dev	10.3

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

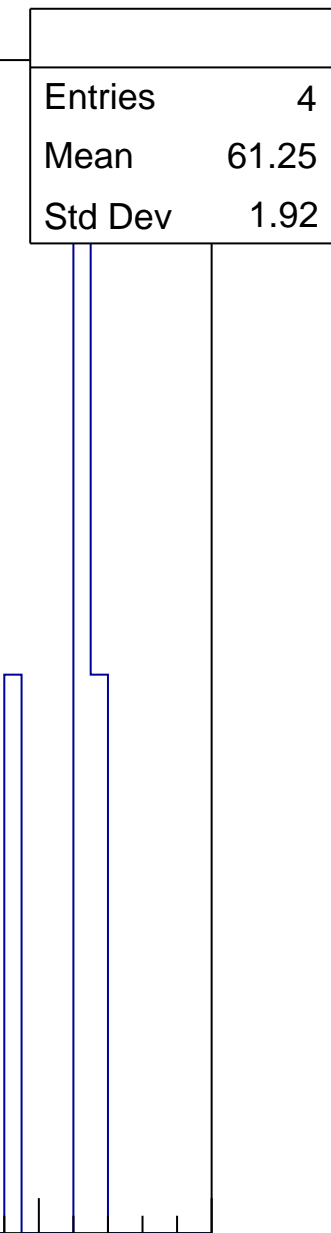
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.25
Std Dev	1.92

0 10 20 30 40 50 60 70

ampl





# B0L002S, U23-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch60, adc0

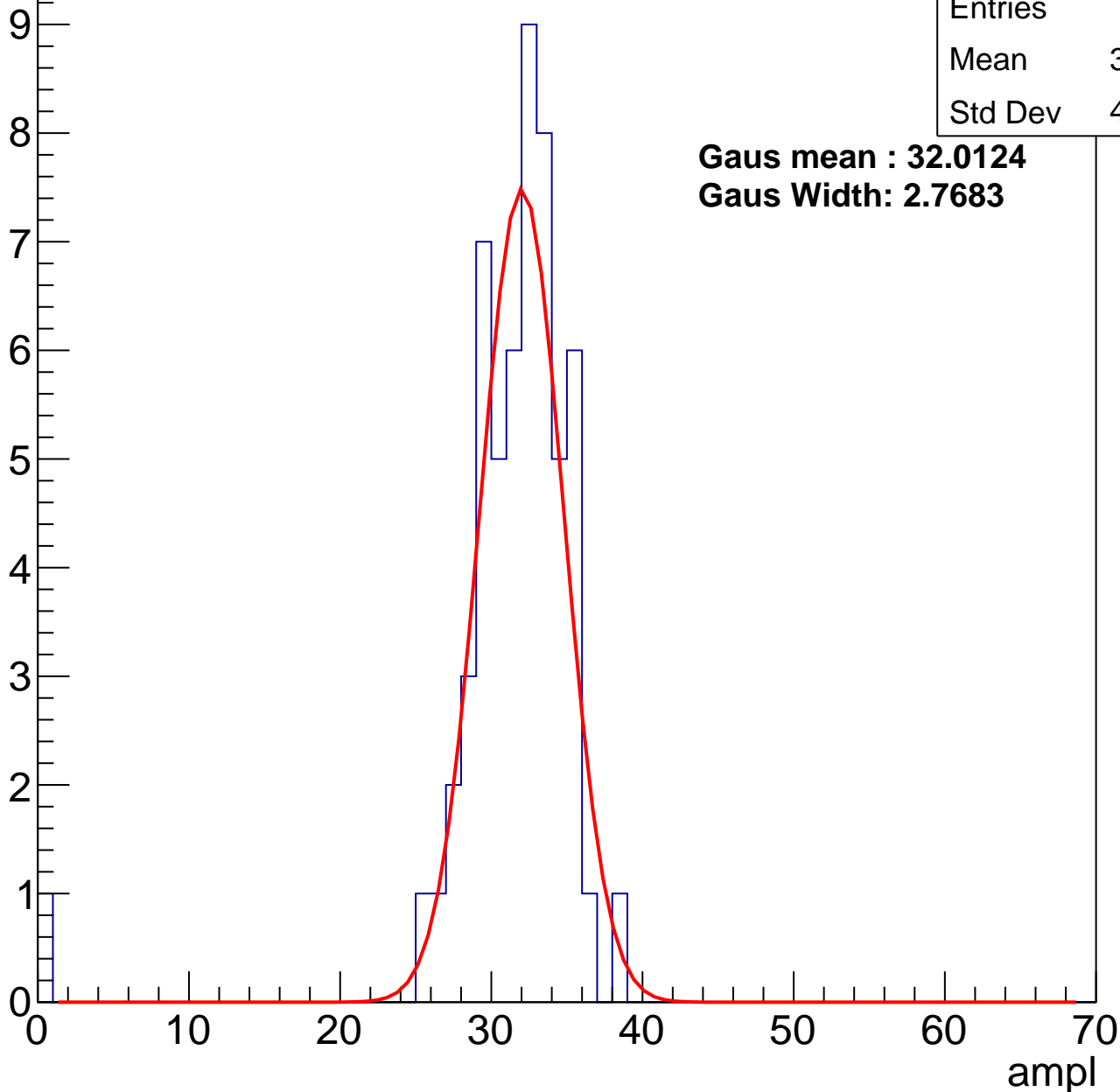
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	30.96
Std Dev	4.946

**Gaus mean : 32.0124**

**Gaus Width: 2.7683**

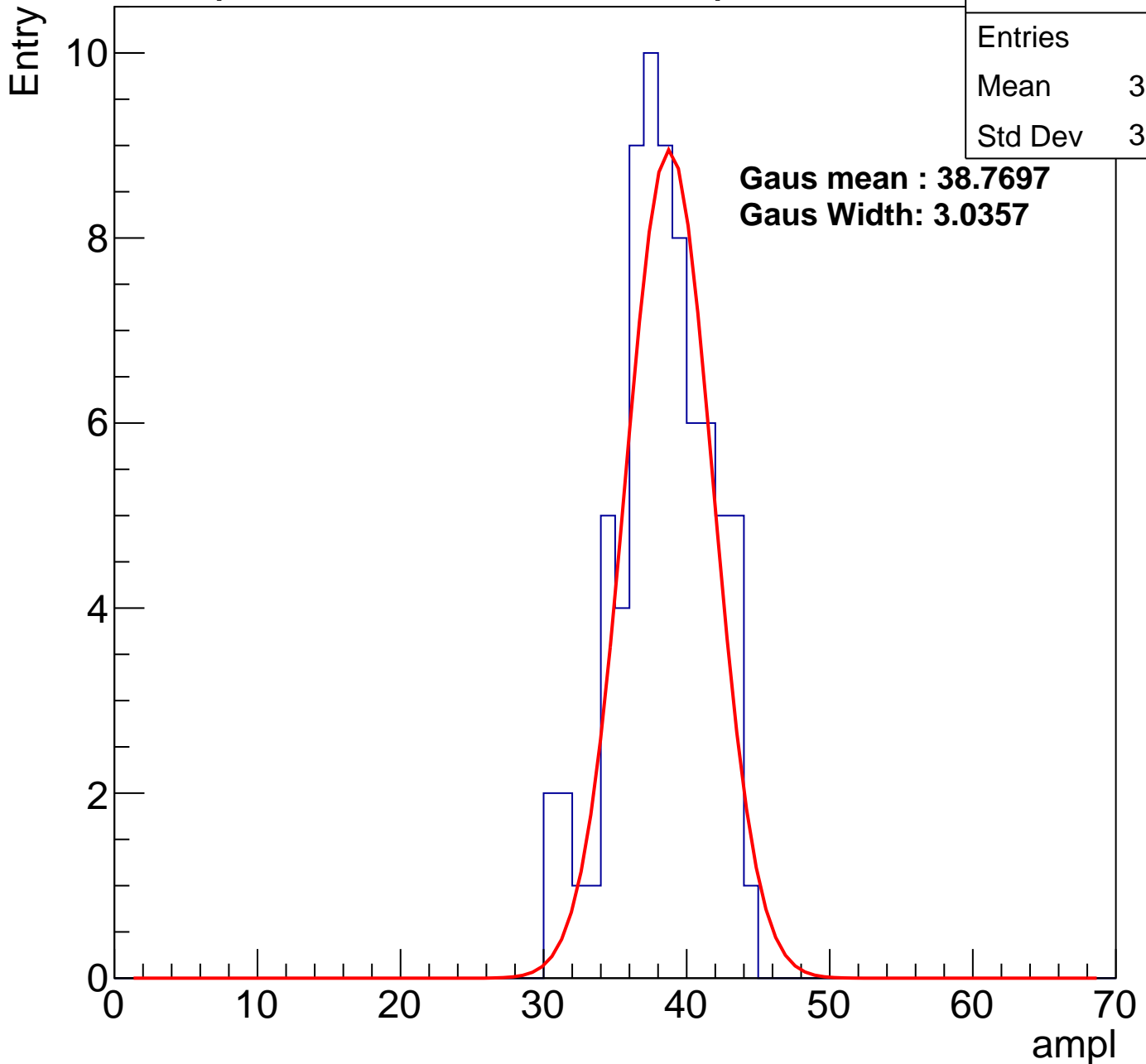


# B0L002S, U23-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	74
Mean	37.84
Std Dev	3.234

**Gaus mean : 38.7697**  
**Gaus Width: 3.0357**



# B0L002S, U23-ch60, adc2

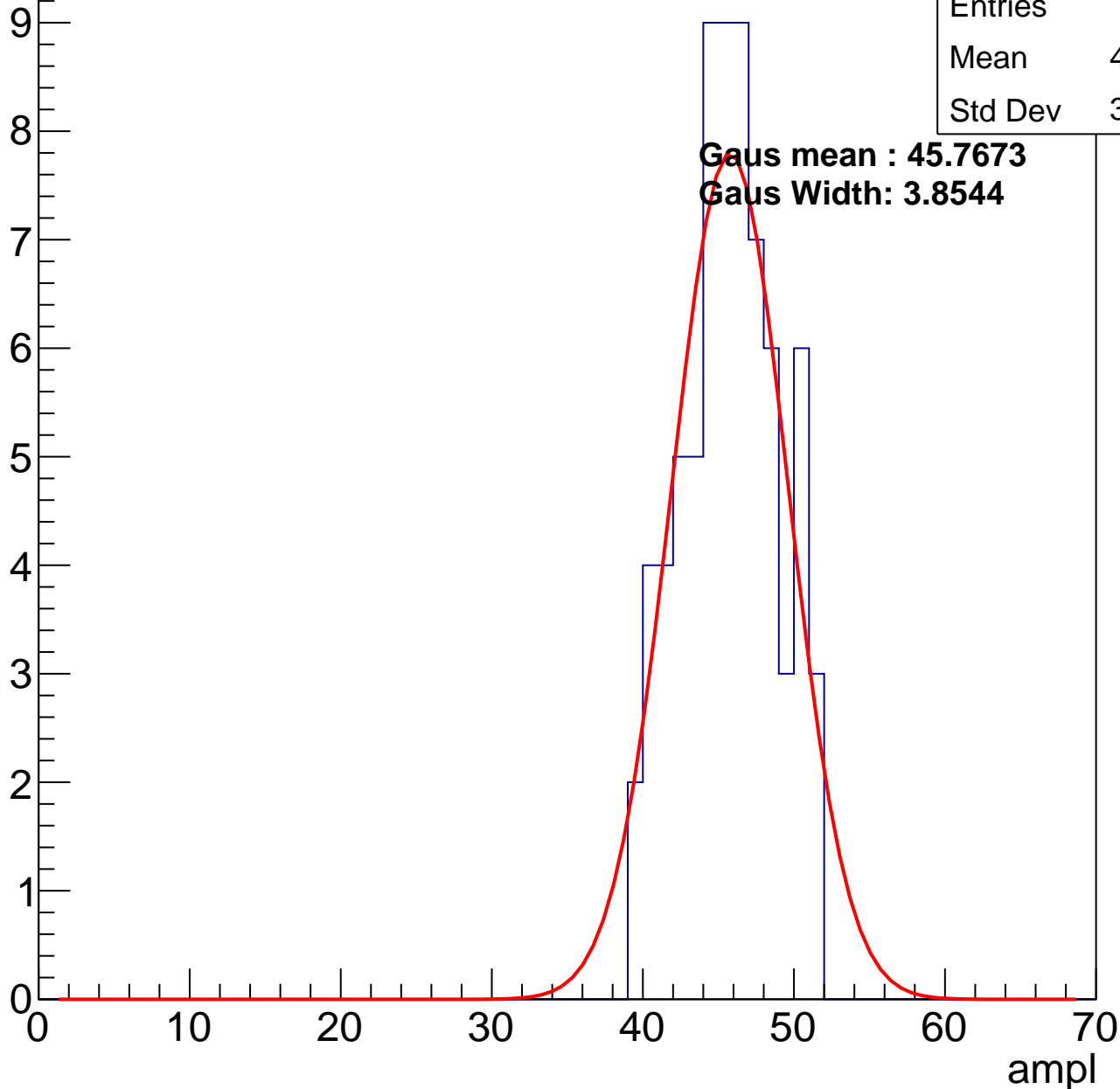
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45.26
Std Dev	3.122

**Gaus mean : 45.7673**

**Gaus Width: 3.8544**

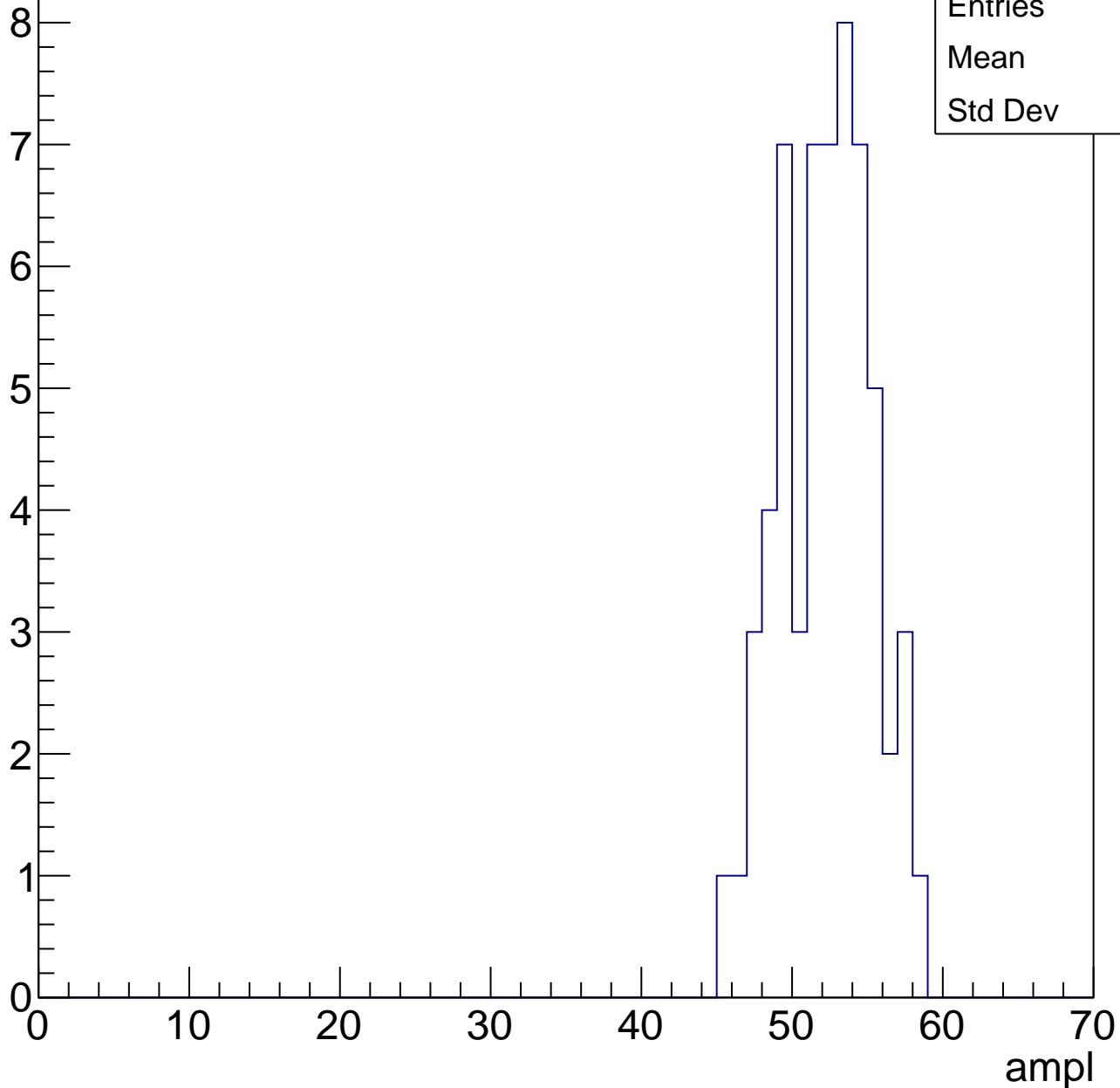


# B0L002S, U23-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	51.8
Std Dev	2.99

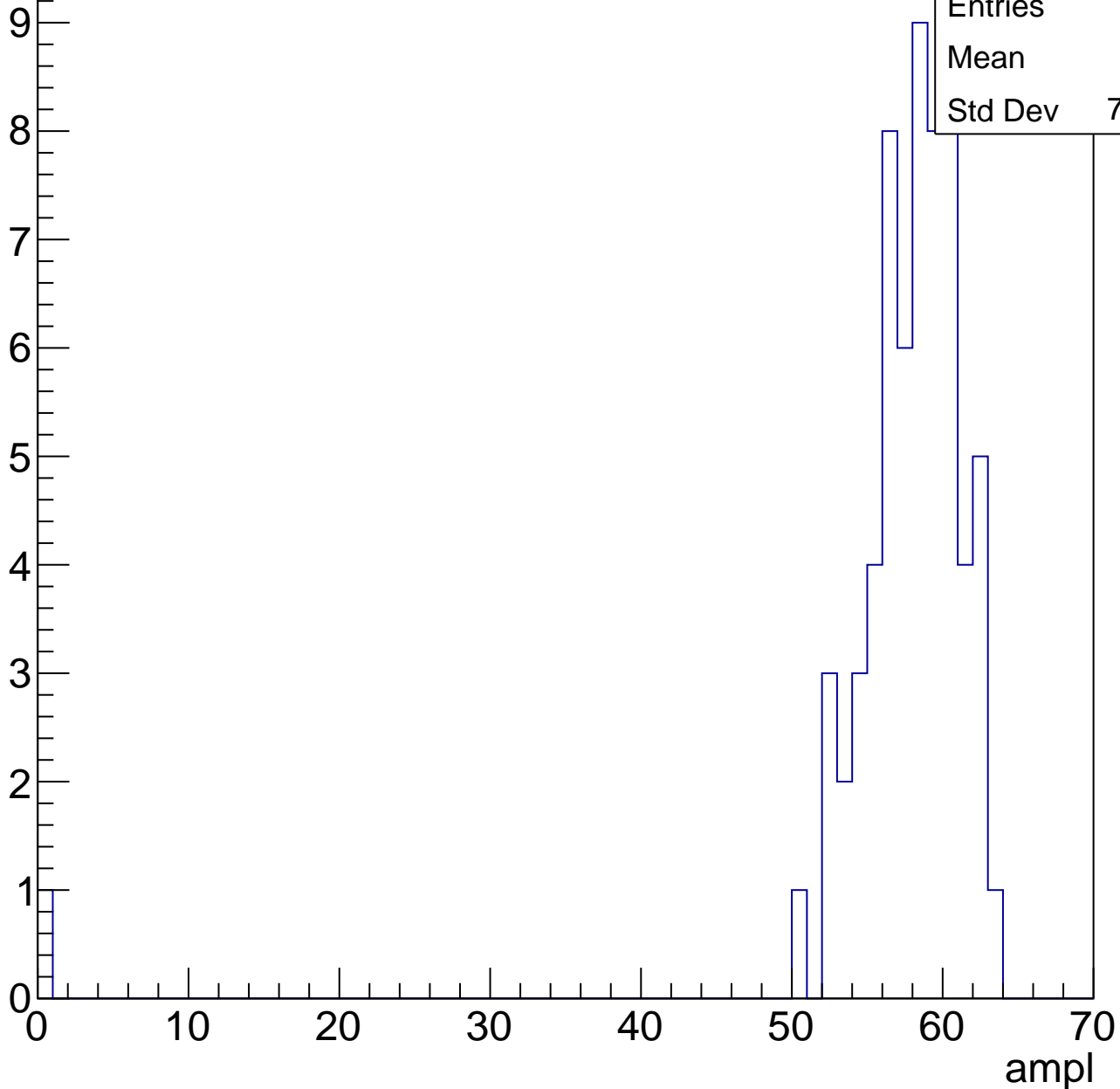


# B0L002S, U23-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	56.8
Std Dev	7.706

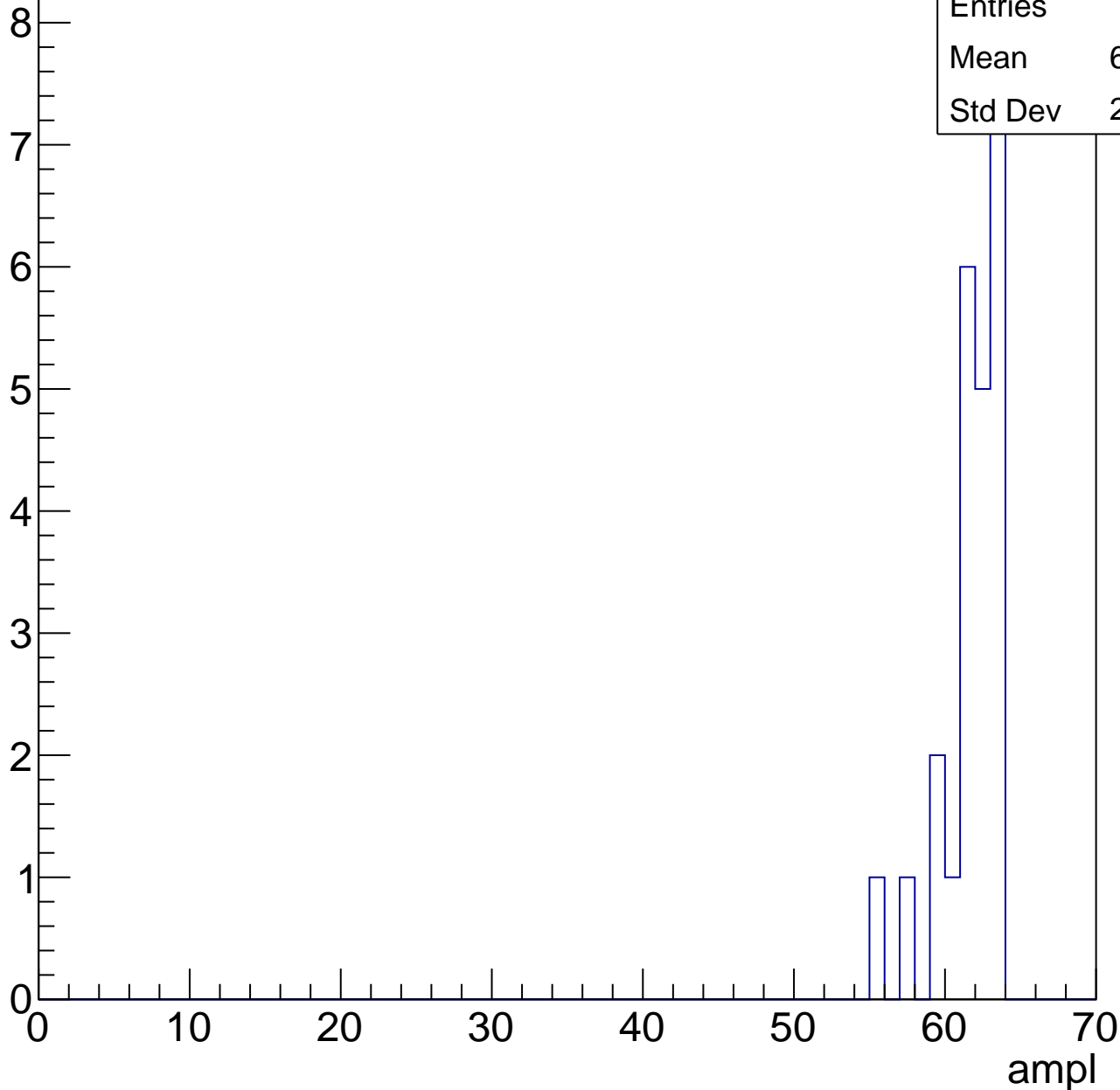


# B0L002S, U23-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	24
Mean	61.25
Std Dev	2.005



# B0L002S, U23-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch61, adc0

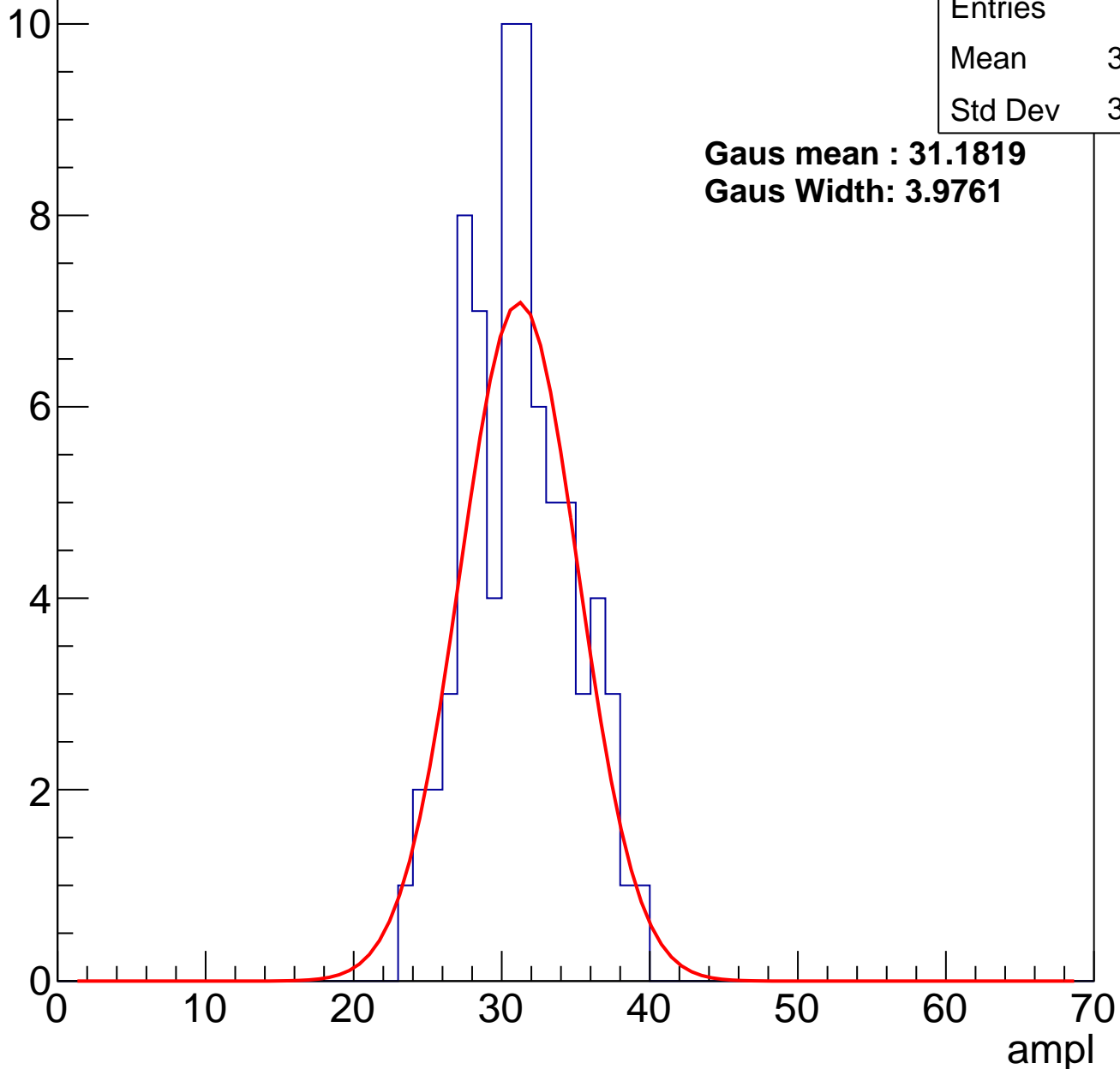
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	30.68
Std Dev	3.575

**Gaus mean : 31.1819**

**Gaus Width: 3.9761**

Entry



# B0L002S, U23-ch61, adc1

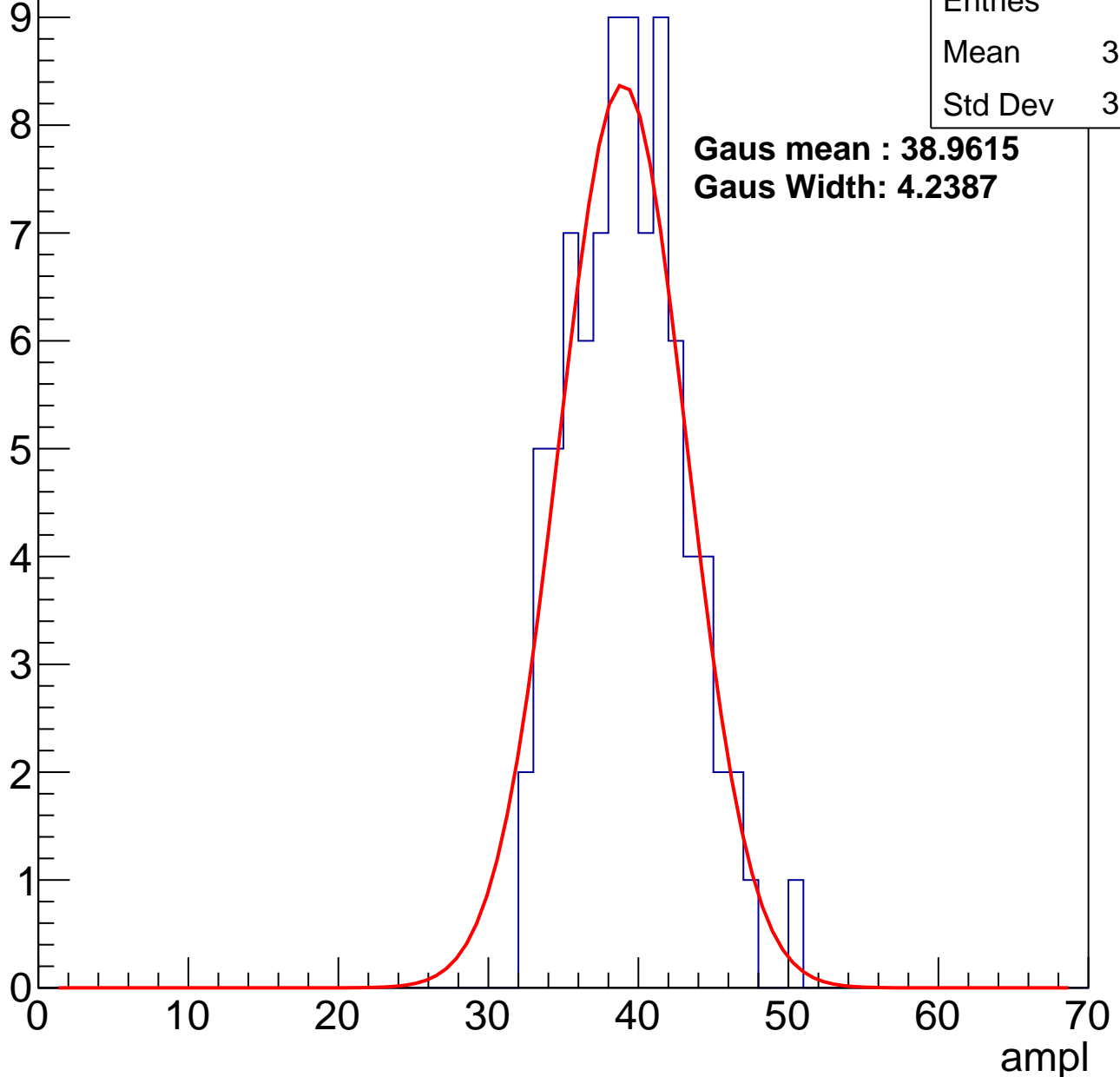
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	86
Mean	38.84
Std Dev	3.766

**Gaus mean : 38.9615**

**Gaus Width: 4.2387**



# B0L002S, U23-ch61, adc2

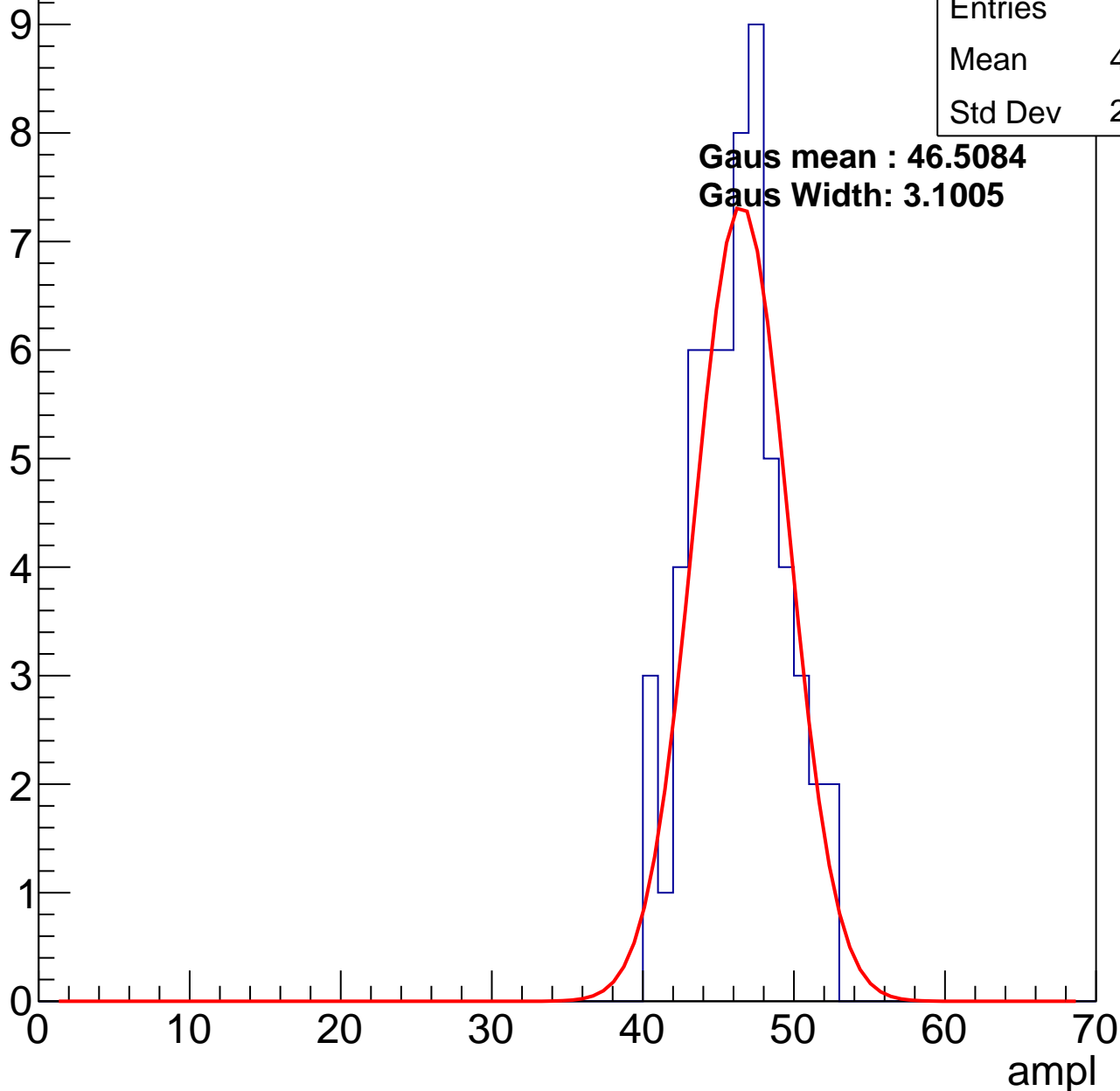
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	45.83
Std Dev	2.952

**Gaus mean : 46.5084**

**Gaus Width: 3.1005**

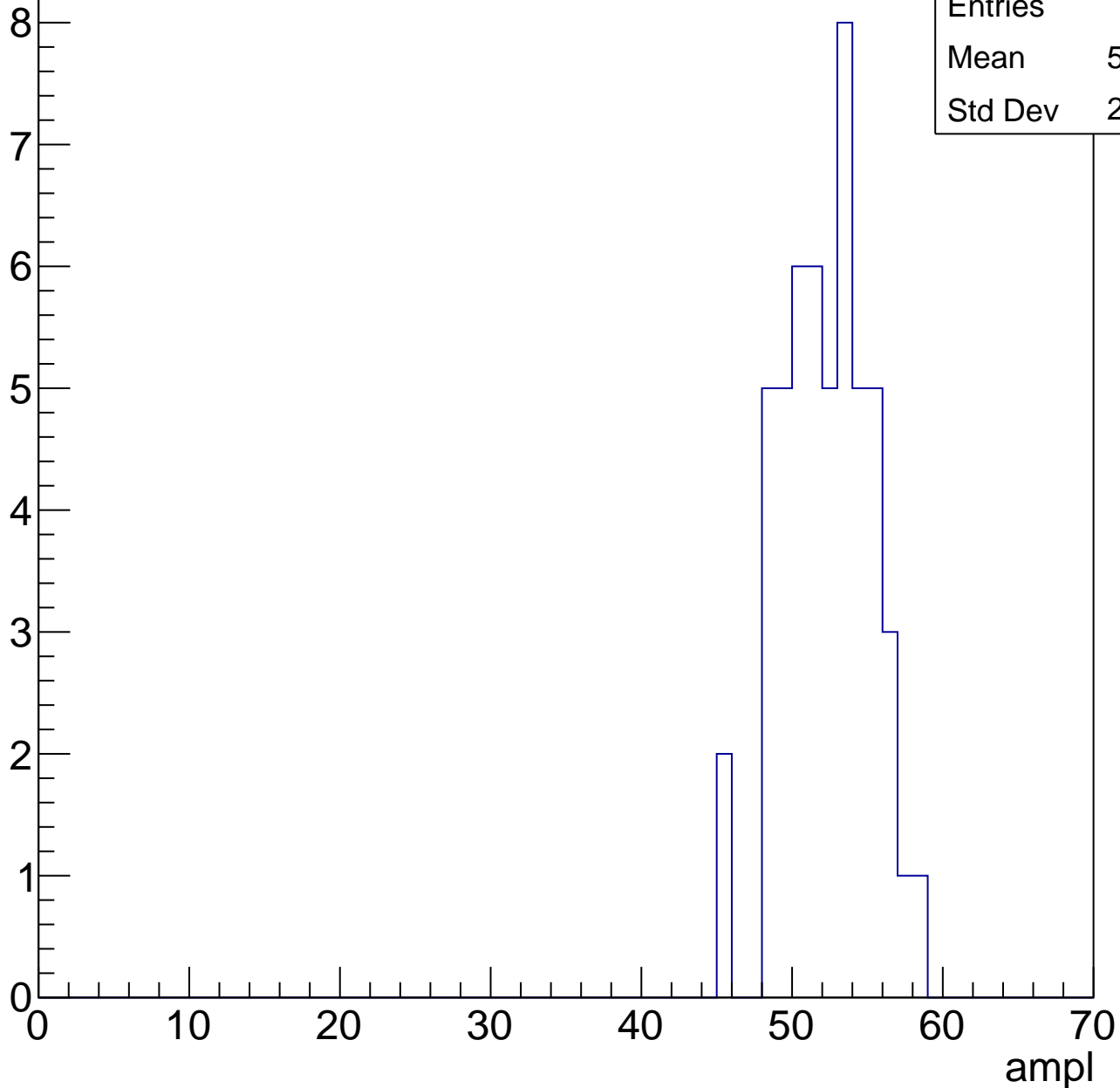


# B0L002S, U23-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

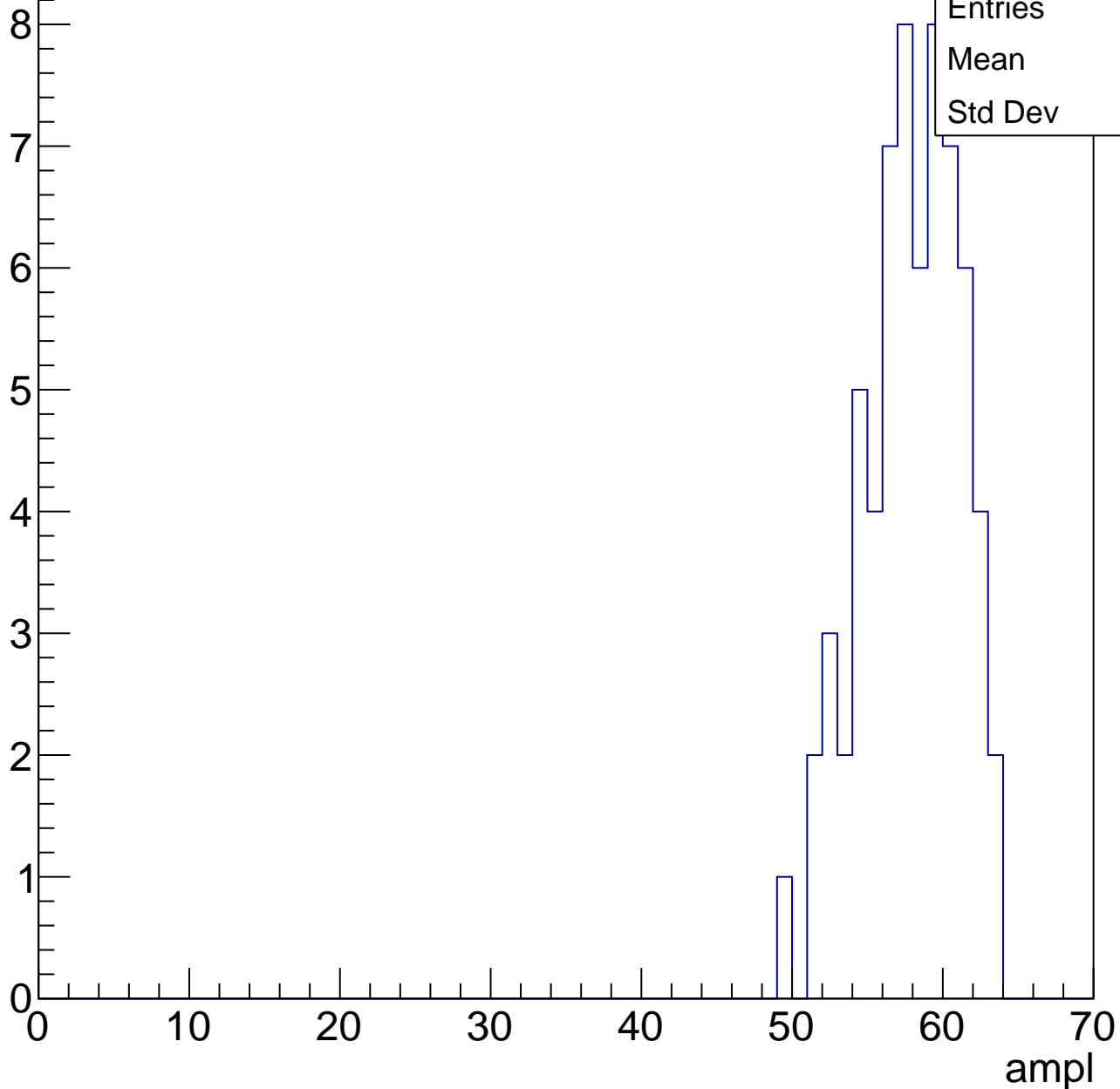
Entries	52
Mean	51.79
Std Dev	2.885



# B0L002S, U23-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

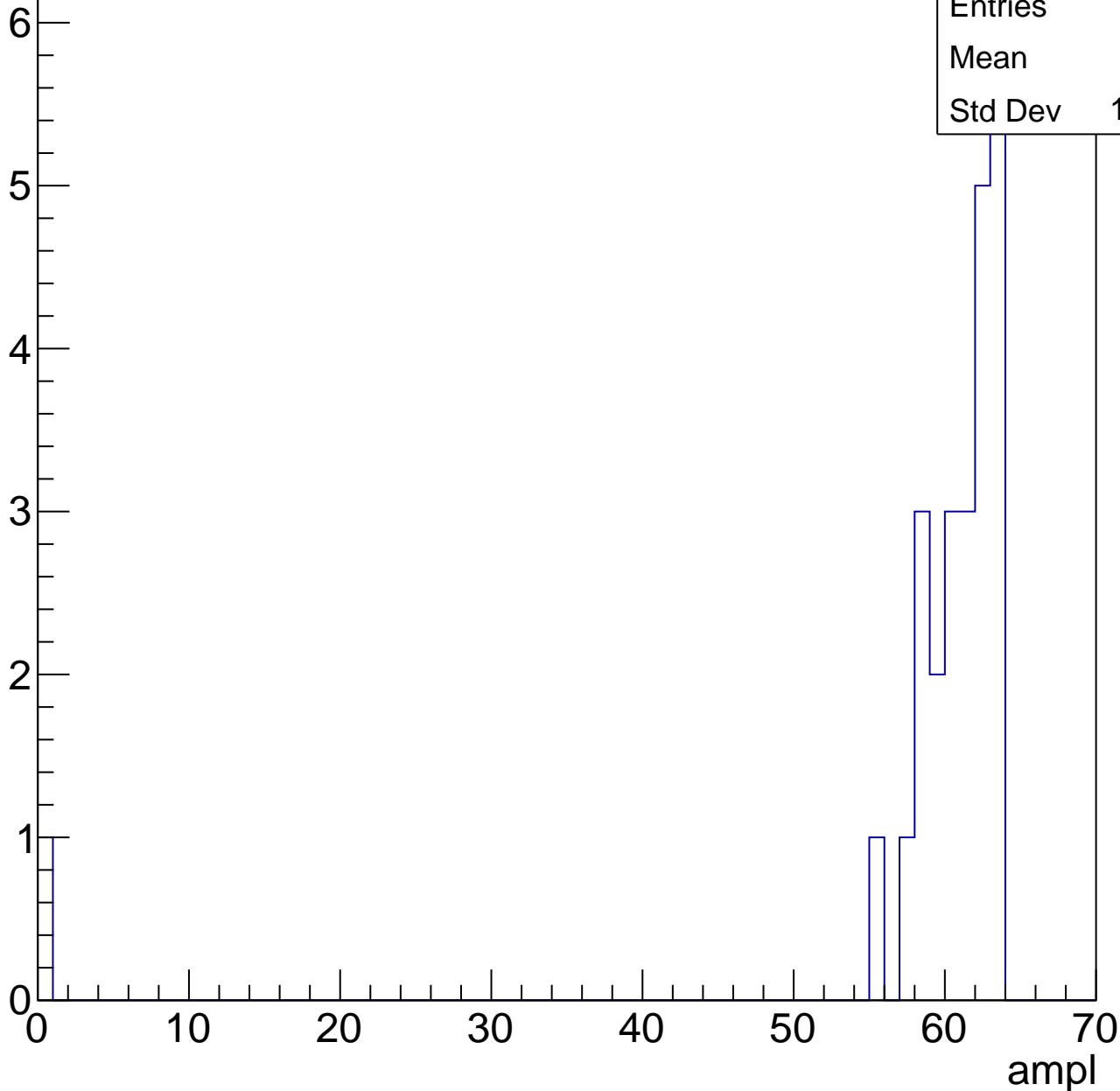


# B0L002S, U23-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.2
Std Dev	12.07



# B0L002S, U23-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch62, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	83
Mean	29.89
Std Dev	3.584

**Gaus mean : 30.6938**

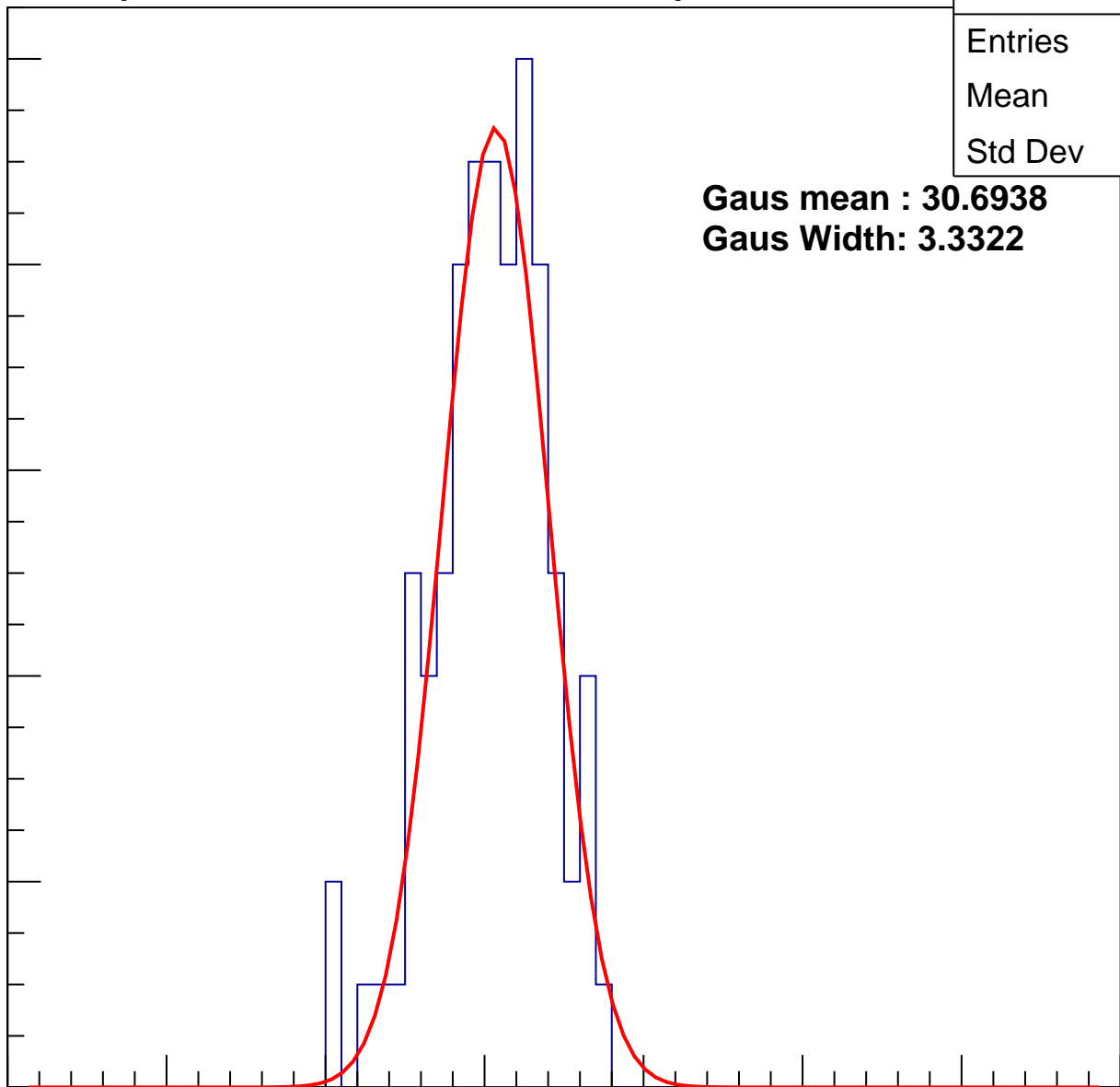
**Gaus Width: 3.3322**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch62, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	80
Mean	38.38
Std Dev	3.803

**Gaus mean : 38.8548**

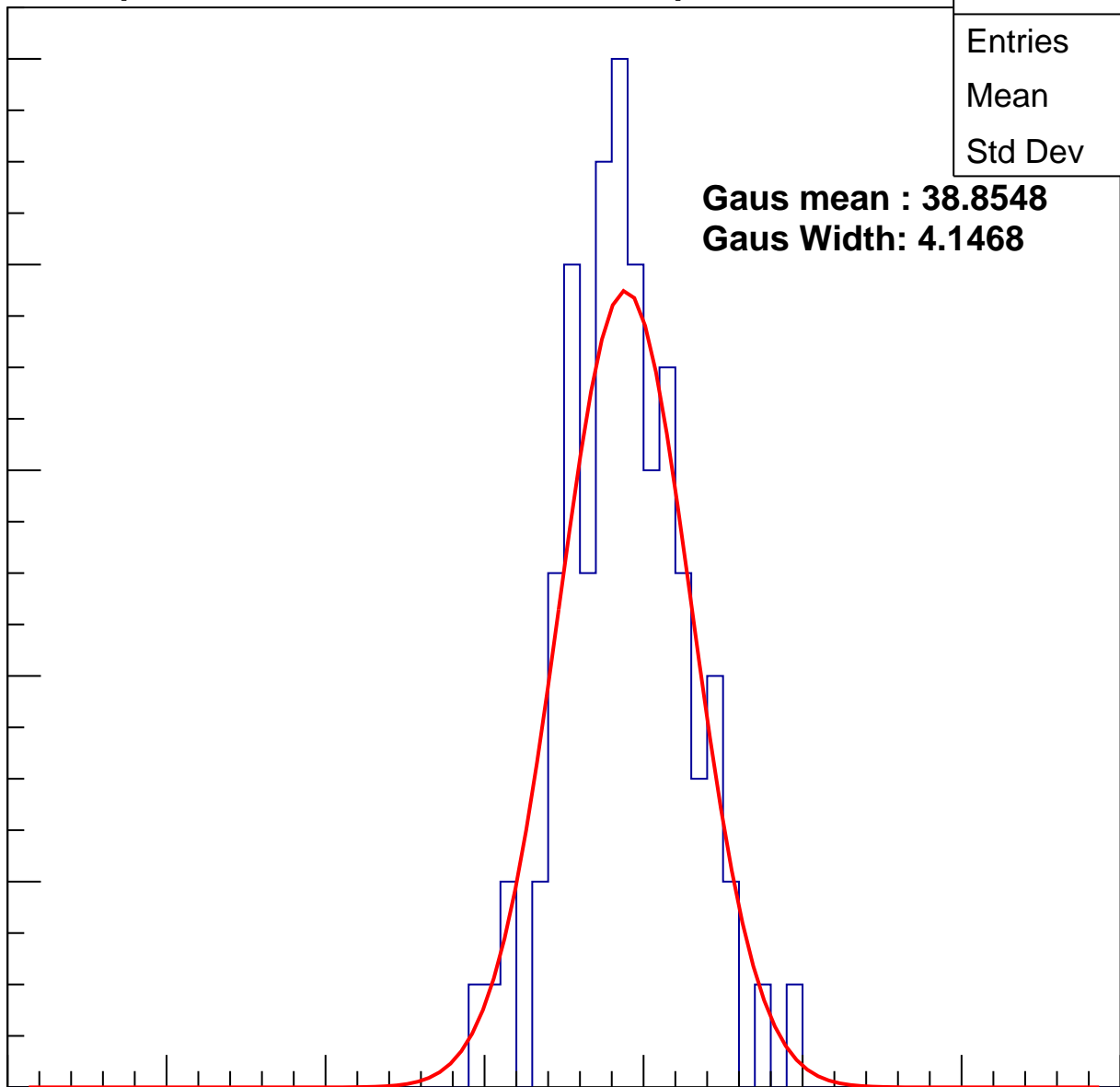
**Gaus Width: 4.1468**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch62, adc2

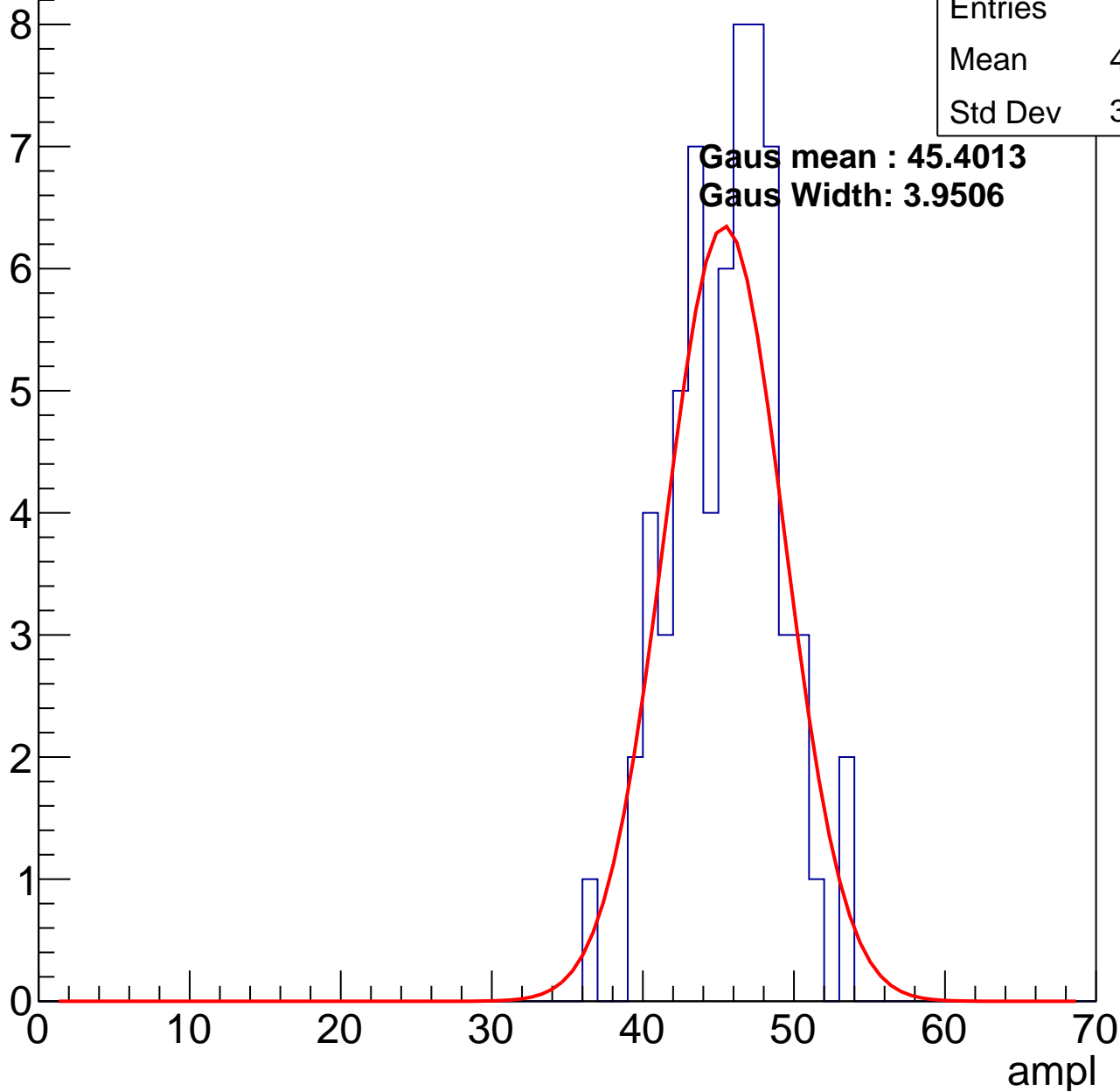
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	45.12
Std Dev	3.462

**Gaus mean : 45.4013**

**Gaus Width: 3.9506**

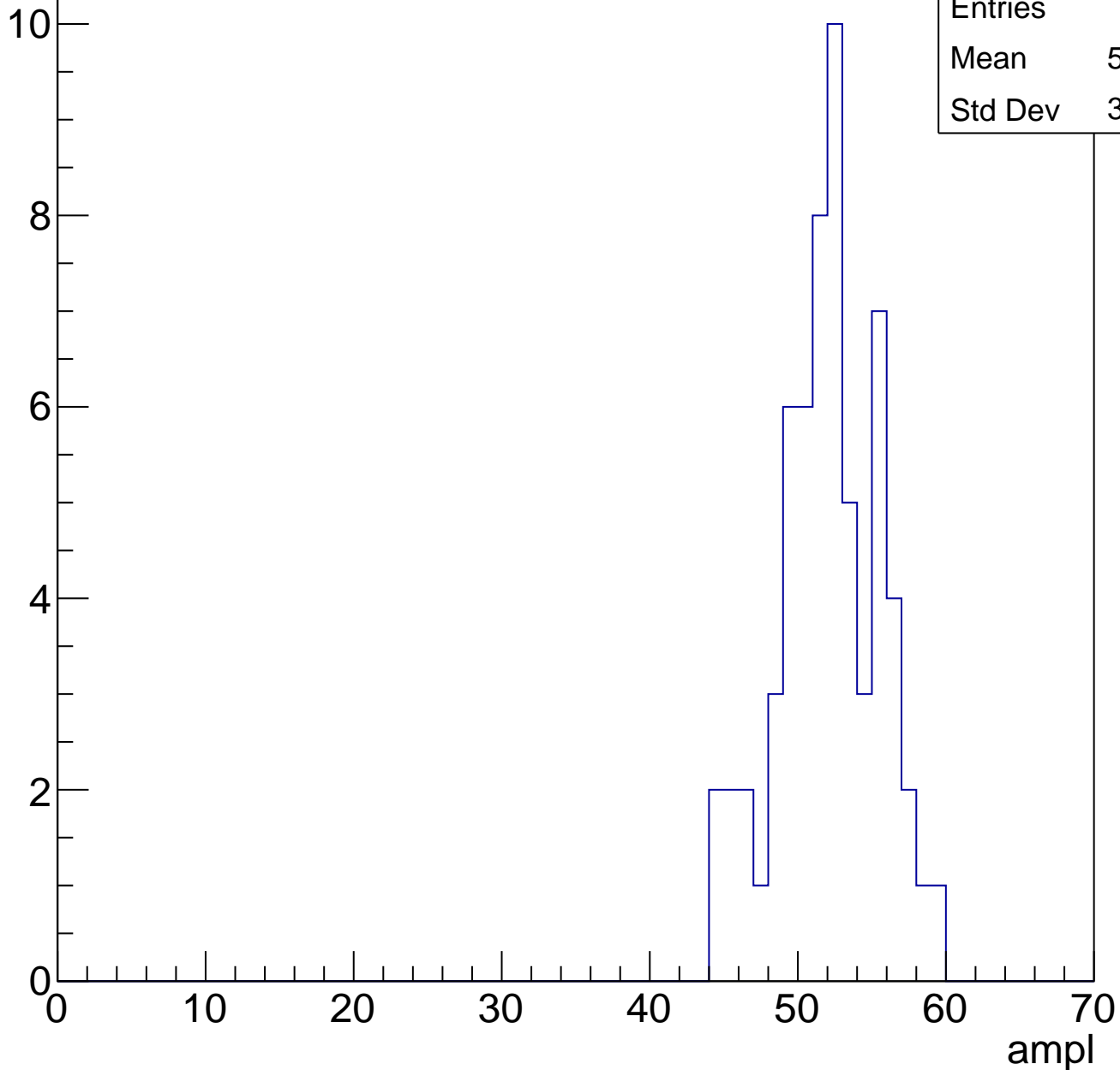


# B0L002S, U23-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	63
Mean	51.59
Std Dev	3.393

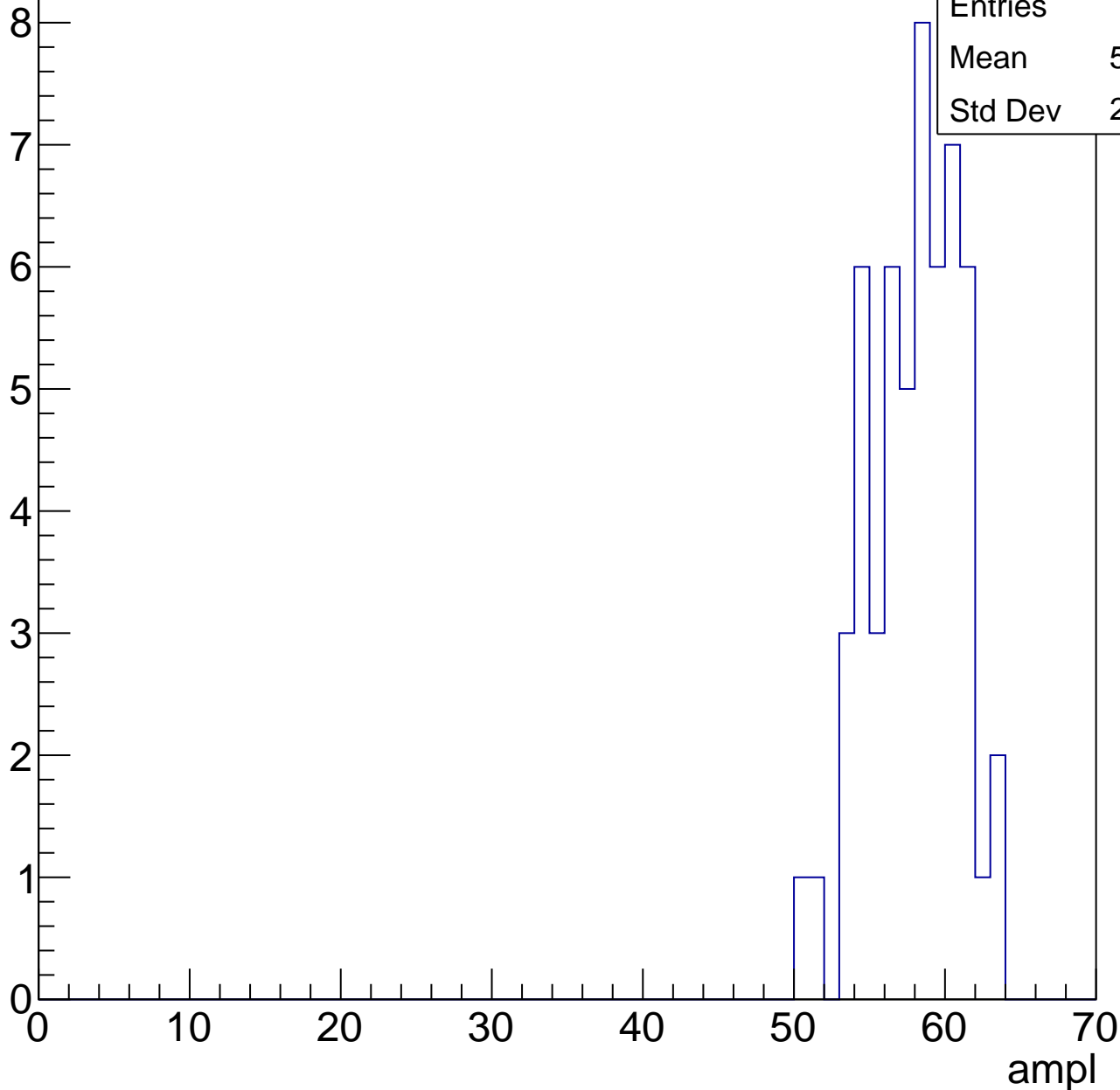
Entry



# B0L002S, U23-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

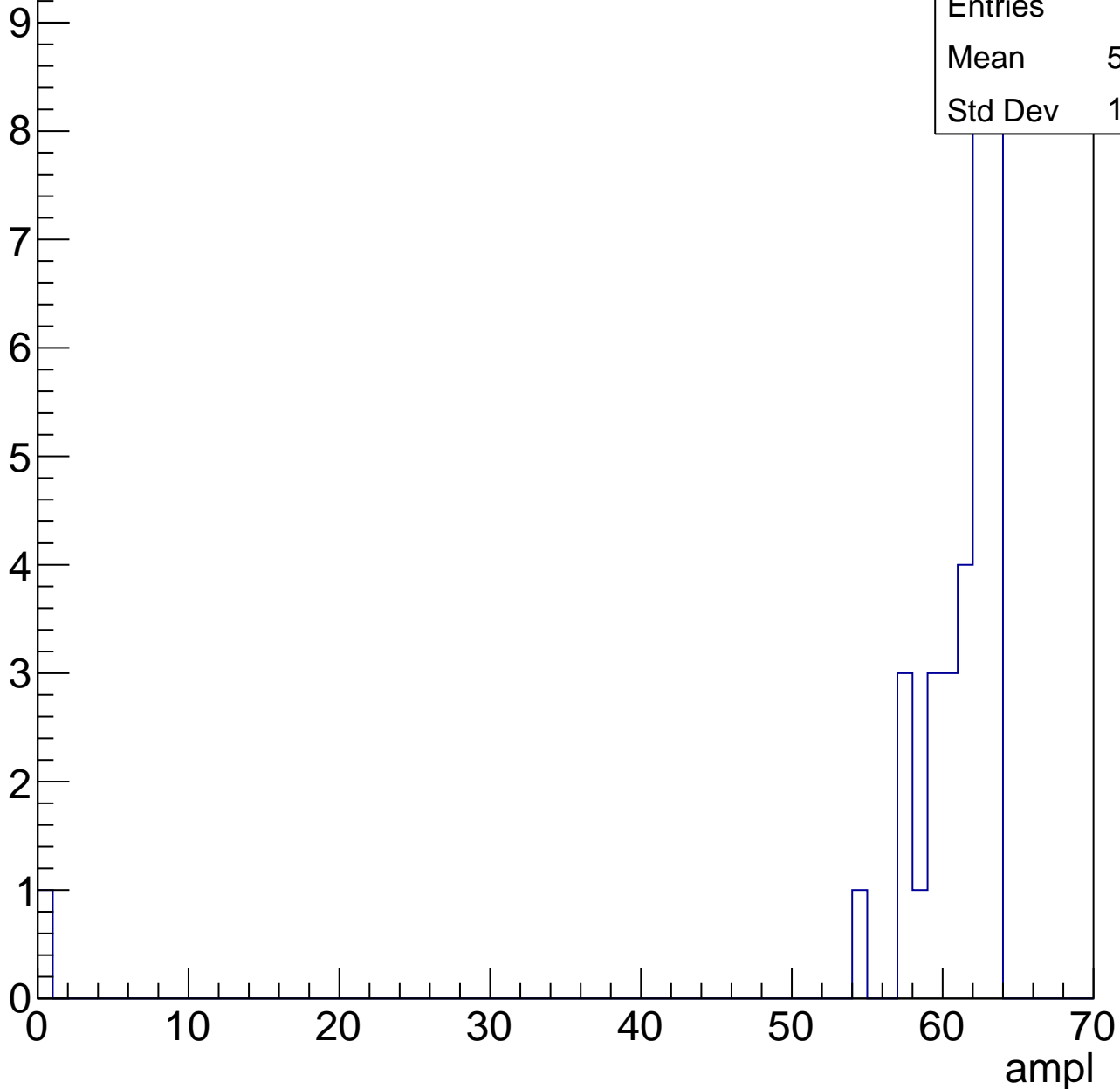


# B0L002S, U23-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.97
Std Dev	10.66



# B0L002S, U23-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch63, adc0

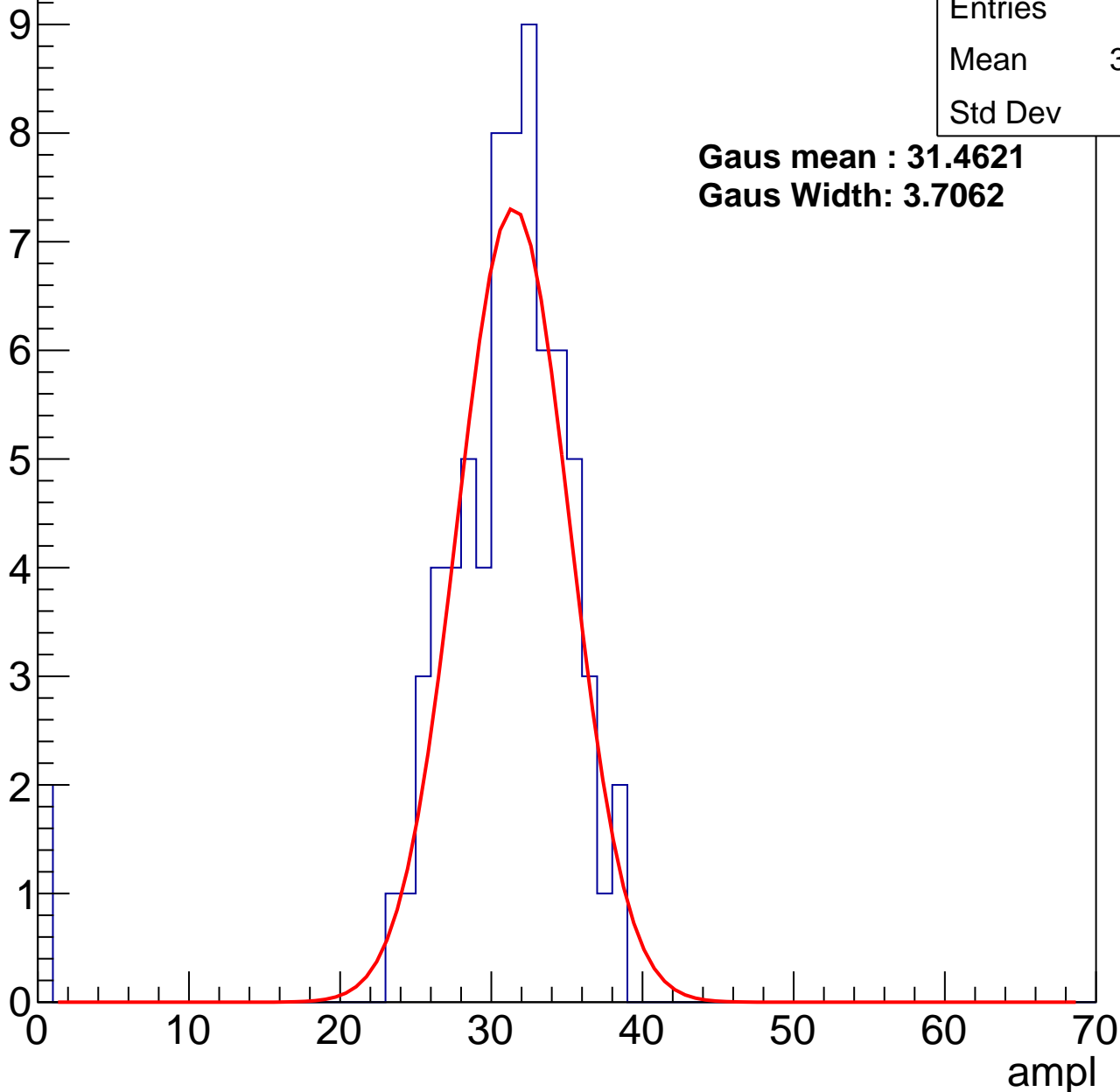
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.06
Std Dev	6.11

**Gaus mean : 31.4621**

**Gaus Width: 3.7062**



# B0L002S, U23-ch63, adc1

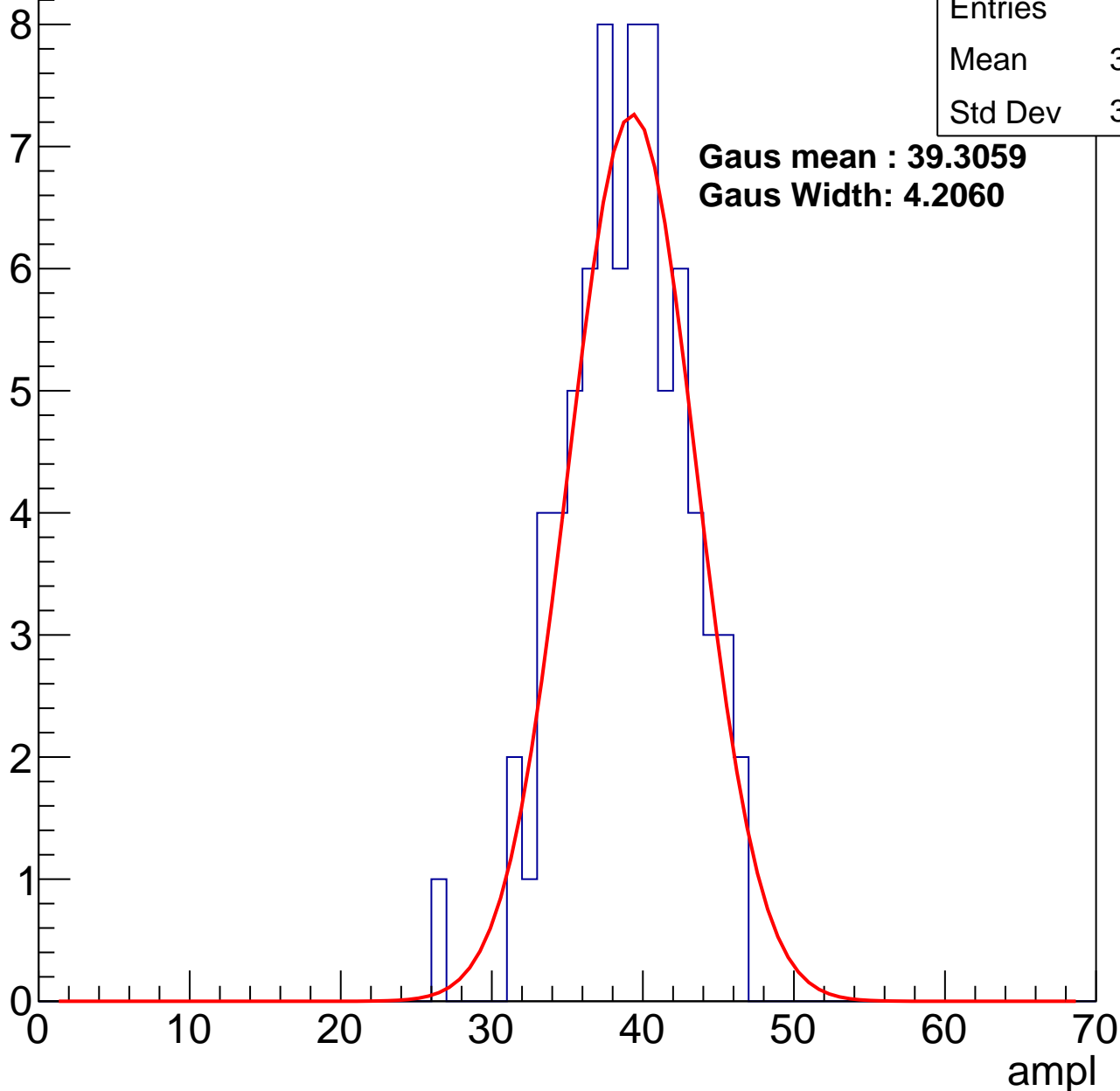
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	38.46
Std Dev	3.918

**Gaus mean : 39.3059**

**Gaus Width: 4.2060**



# B0L002S, U23-ch63, adc2

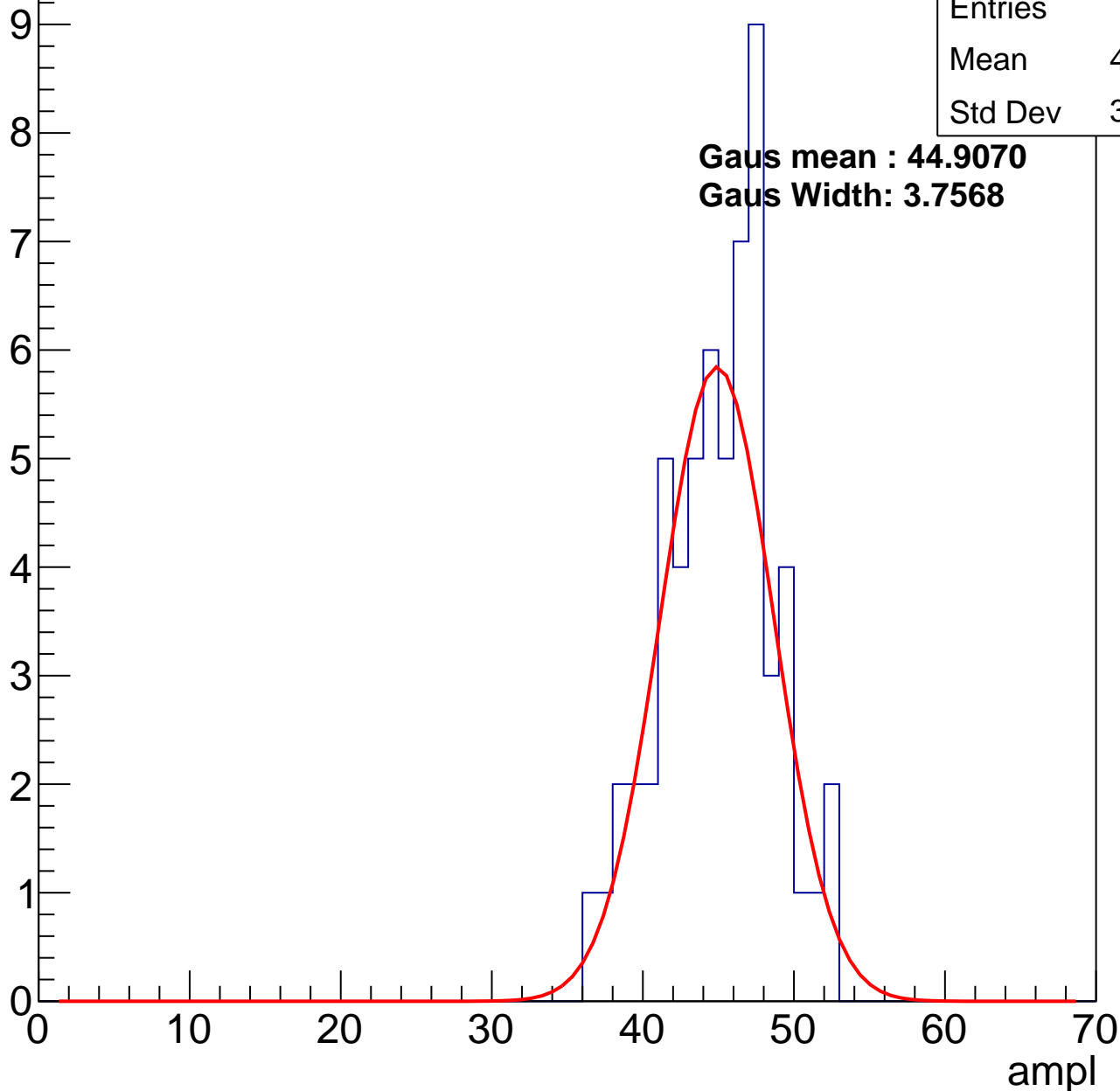
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.57
Std Dev	3.626

**Gaus mean : 44.9070**

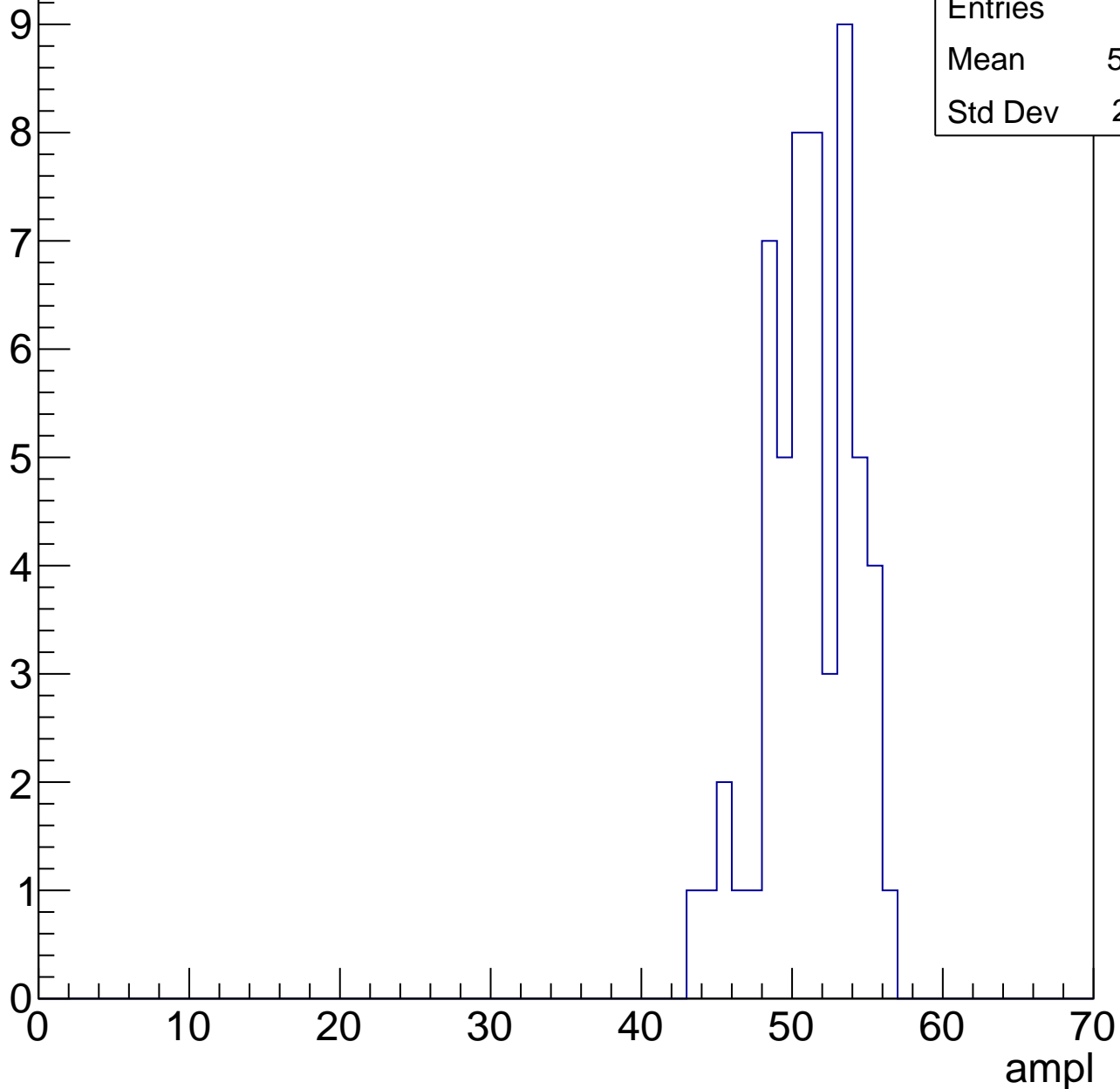
**Gaus Width: 3.7568**



# B0L002S, U23-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

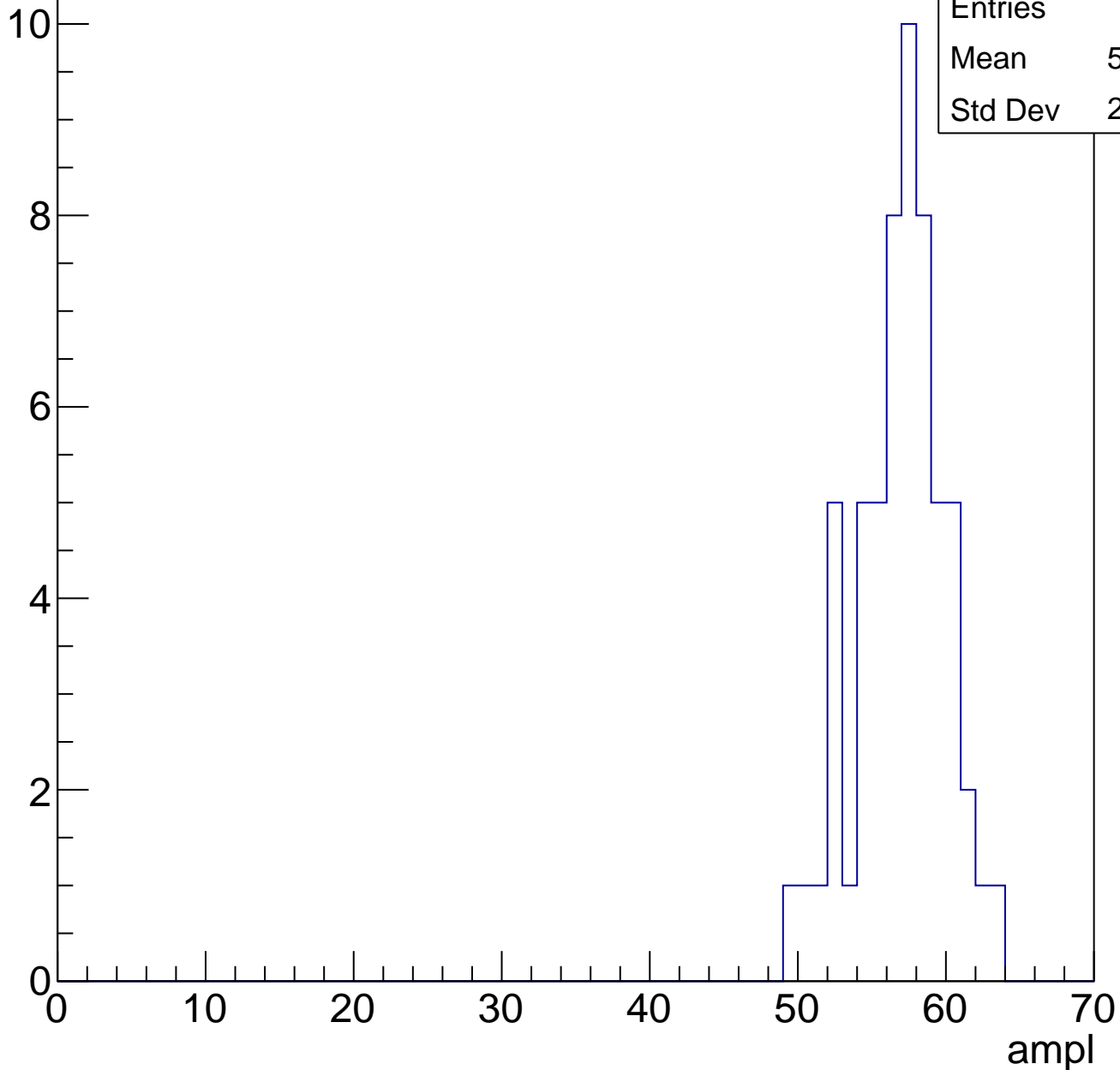


# B0L002S, U23-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	59
Mean	56.47
Std Dev	2.948

Entry

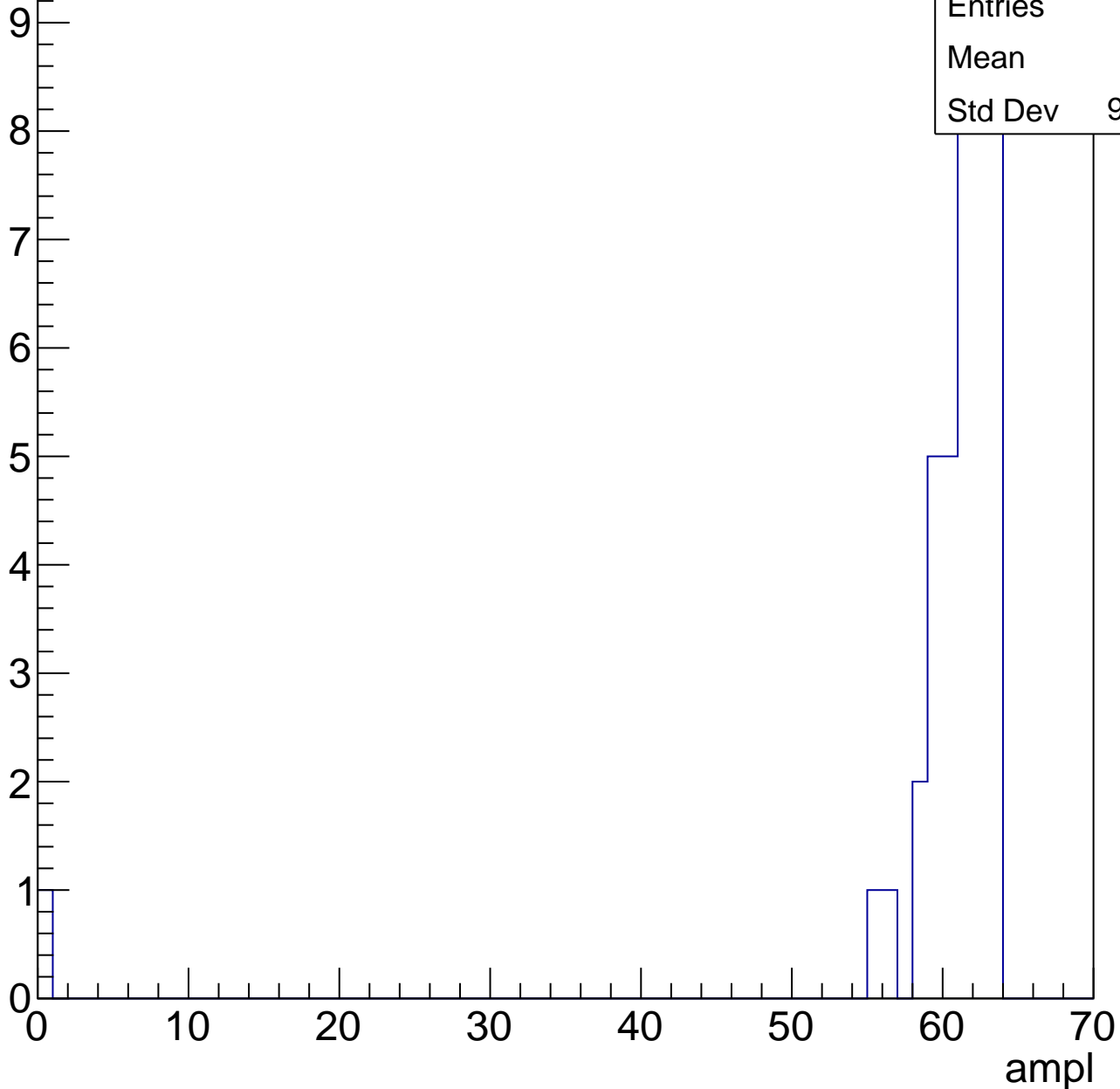


# B0L002S, U23-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.3
Std Dev	9.683



# B0L002S, U23-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch64, adc0

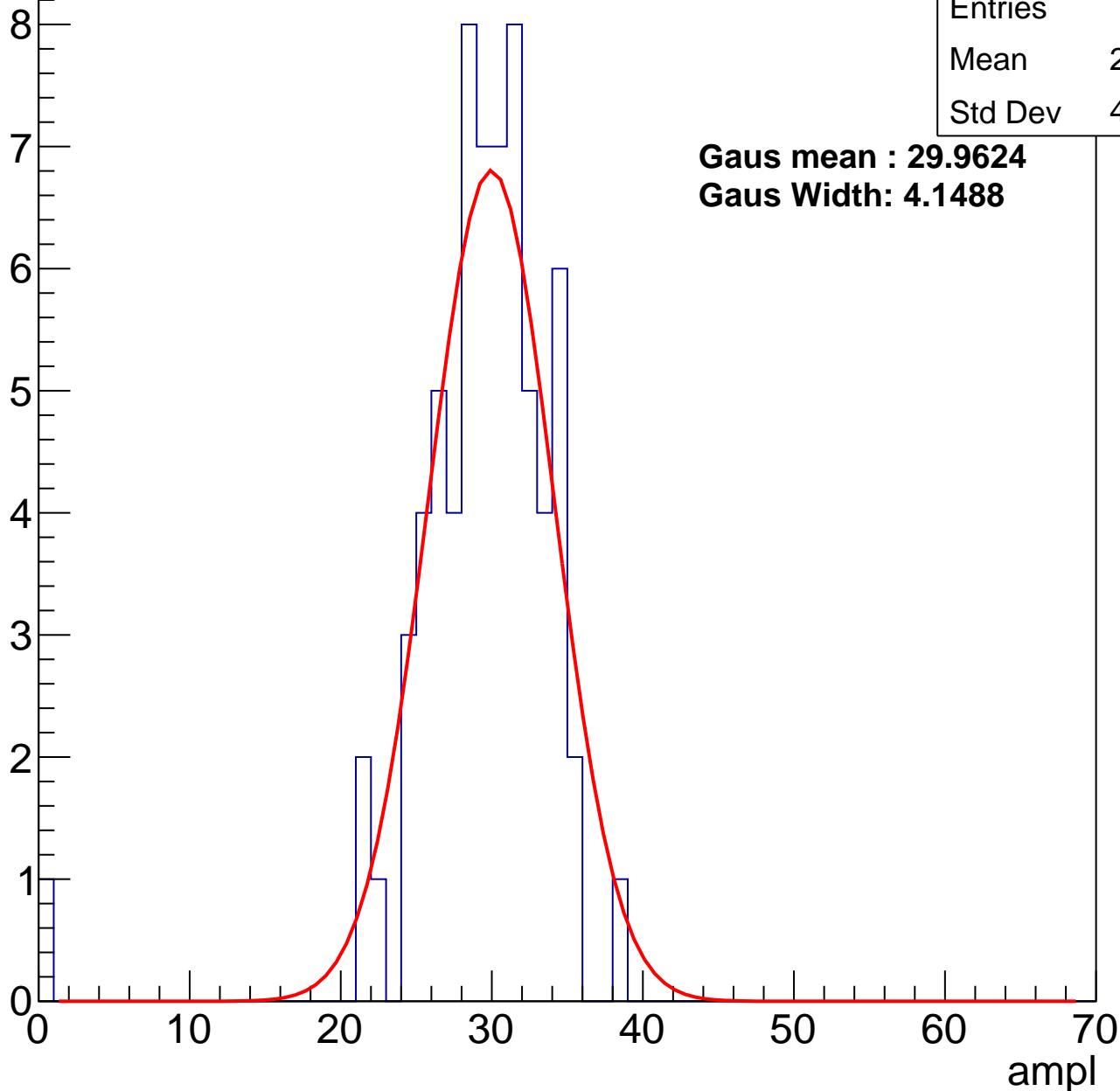
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.87
Std Dev	4.964

**Gaus mean : 29.9624**

**Gaus Width: 4.1488**



# B0L002S, U23-ch64, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	72
Mean	36.71
Std Dev	3.368

**Gaus mean : 36.9522**

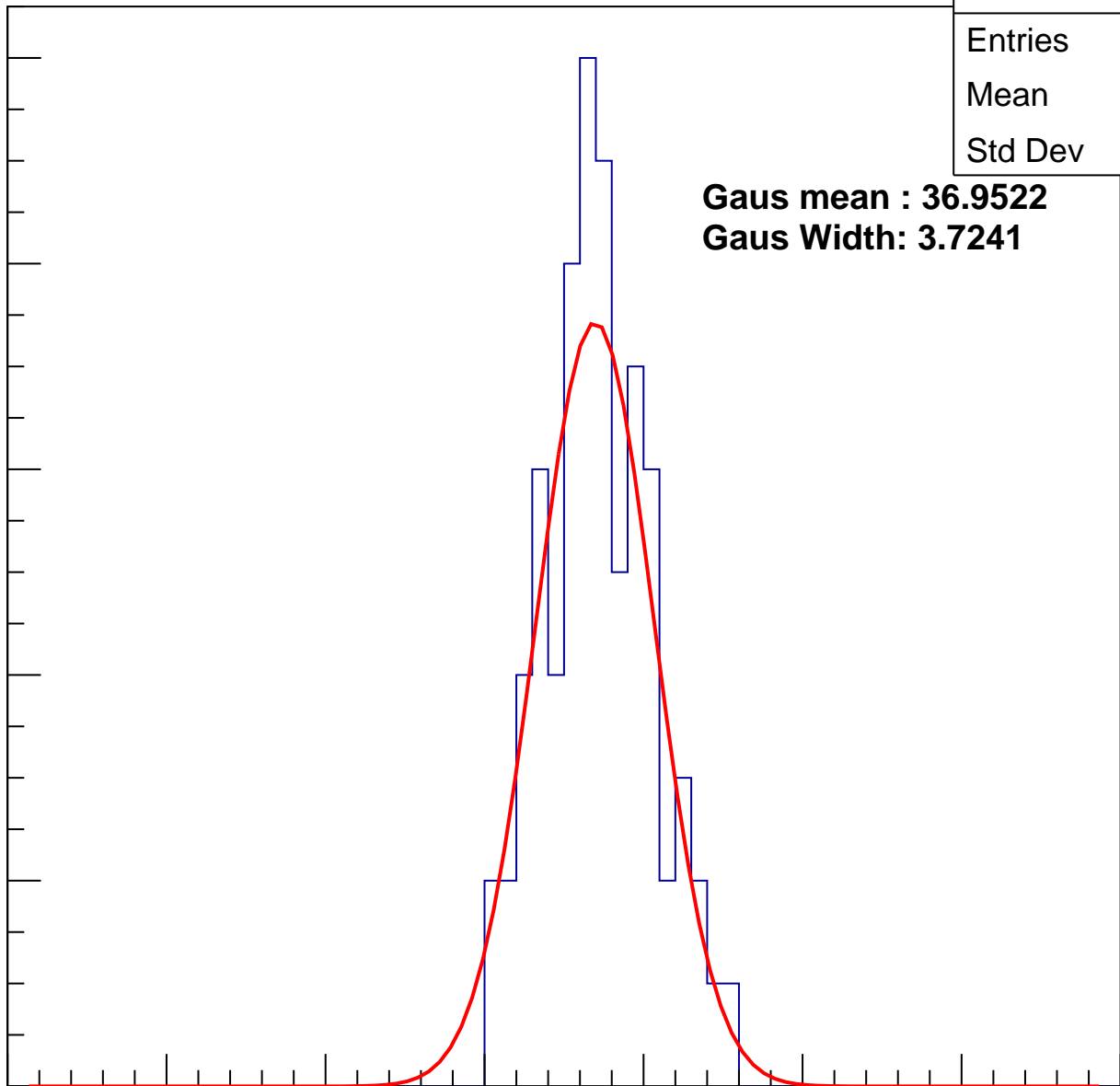
**Gaus Width: 3.7241**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch64, adc2

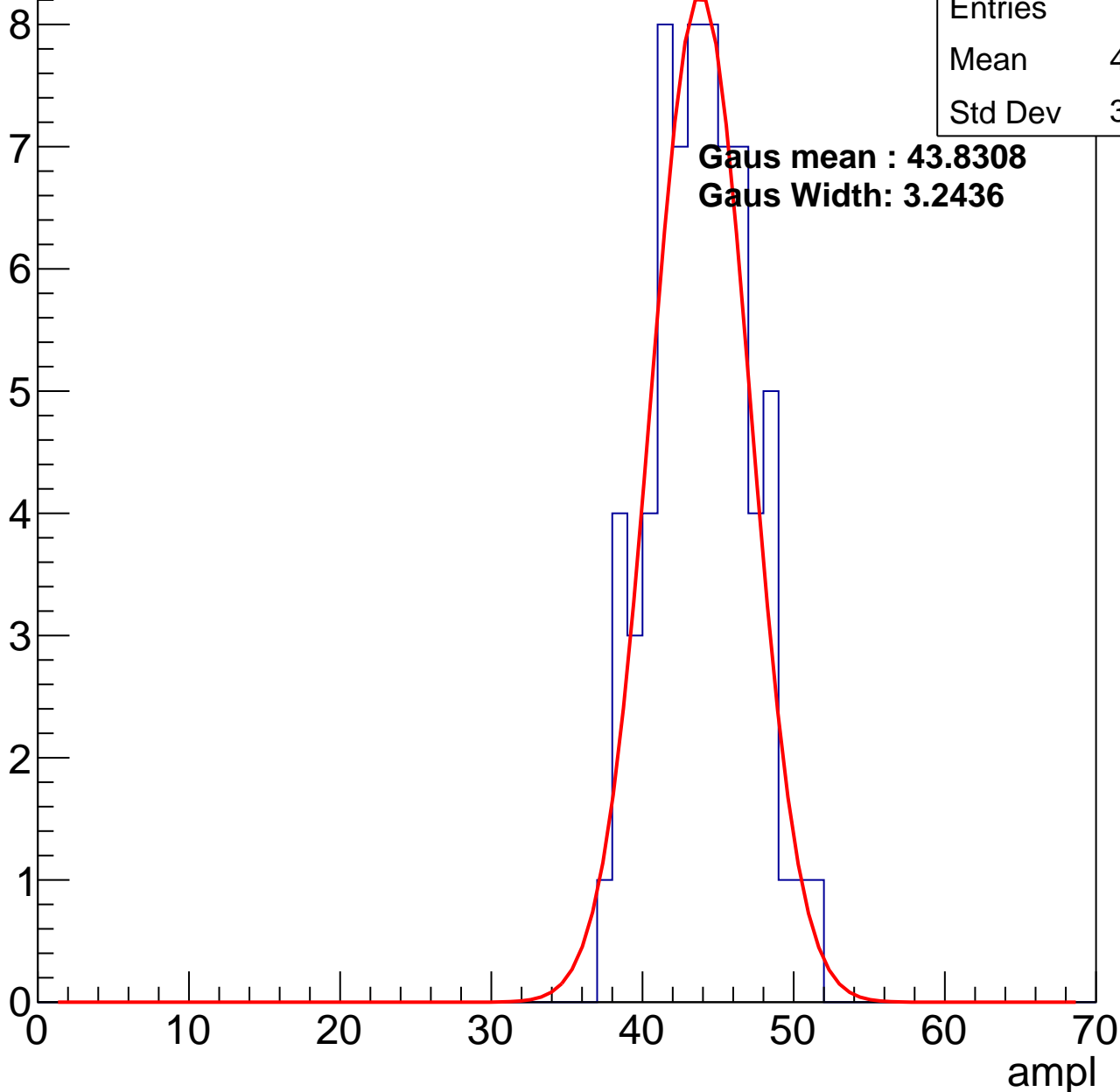
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.46
Std Dev	3.142

**Gaus mean : 43.8308**

**Gaus Width: 3.2436**

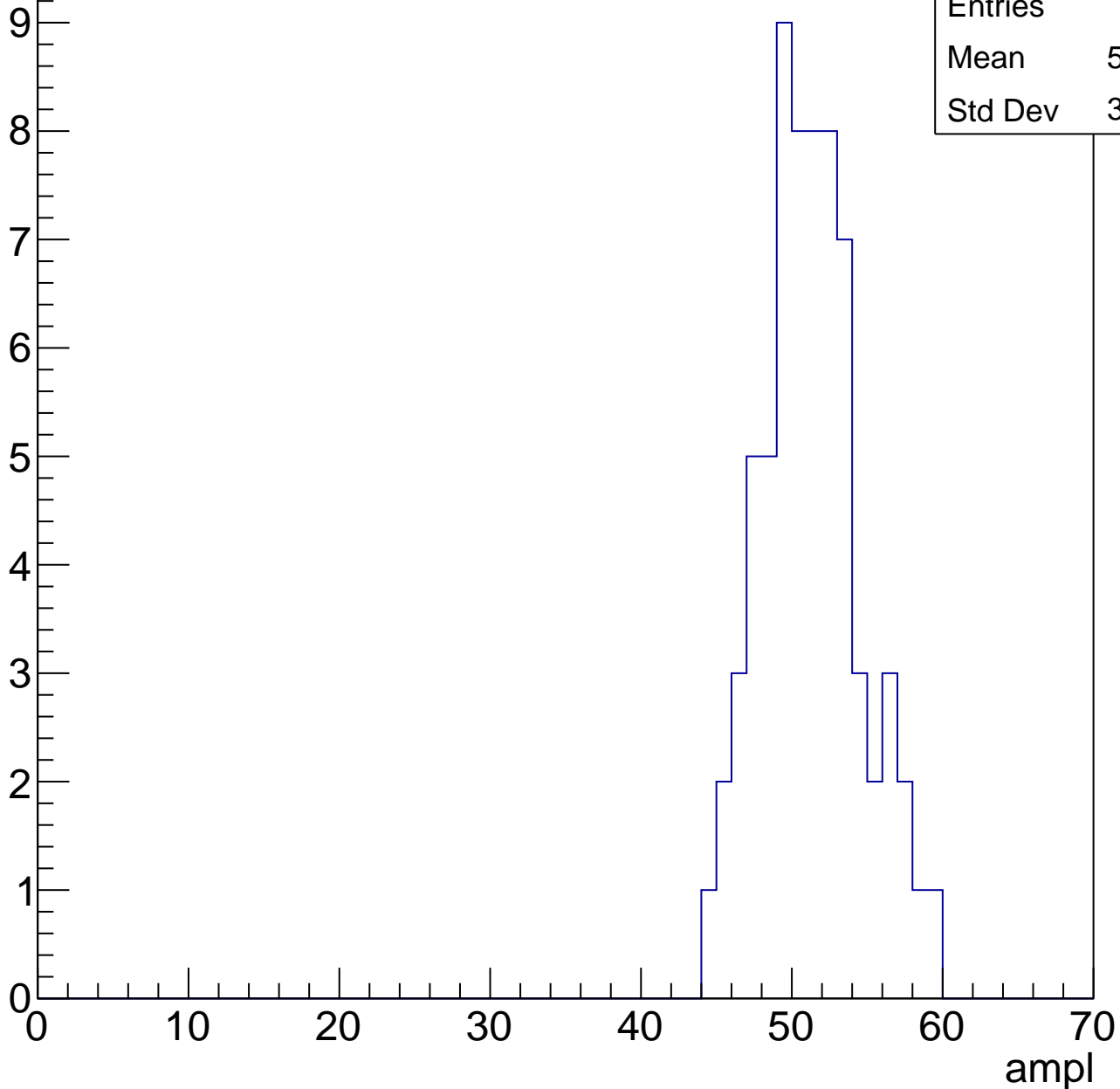


# B0L002S, U23-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.79
Std Dev	3.247

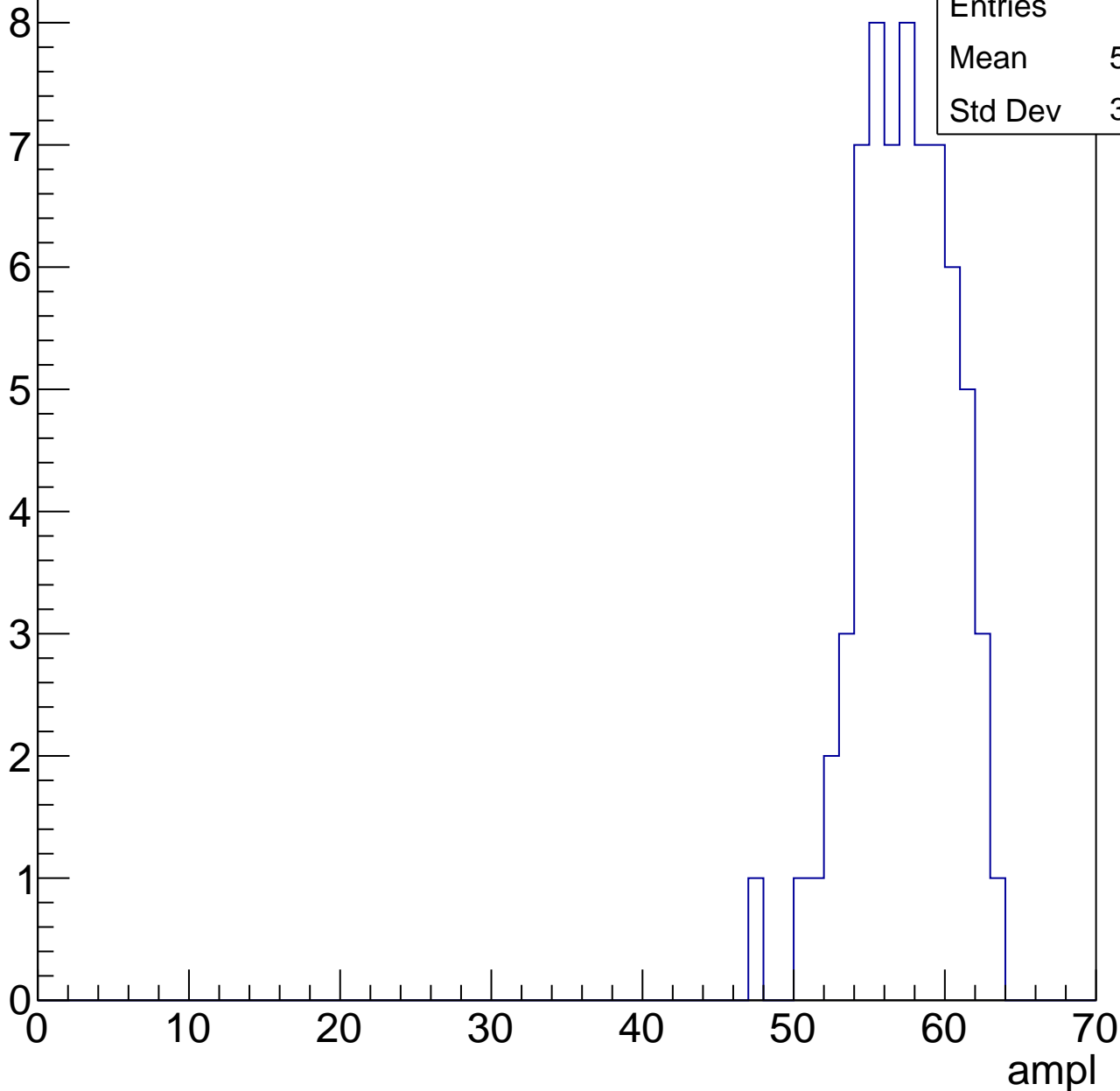


# B0L002S, U23-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	56.87
Std Dev	3.148

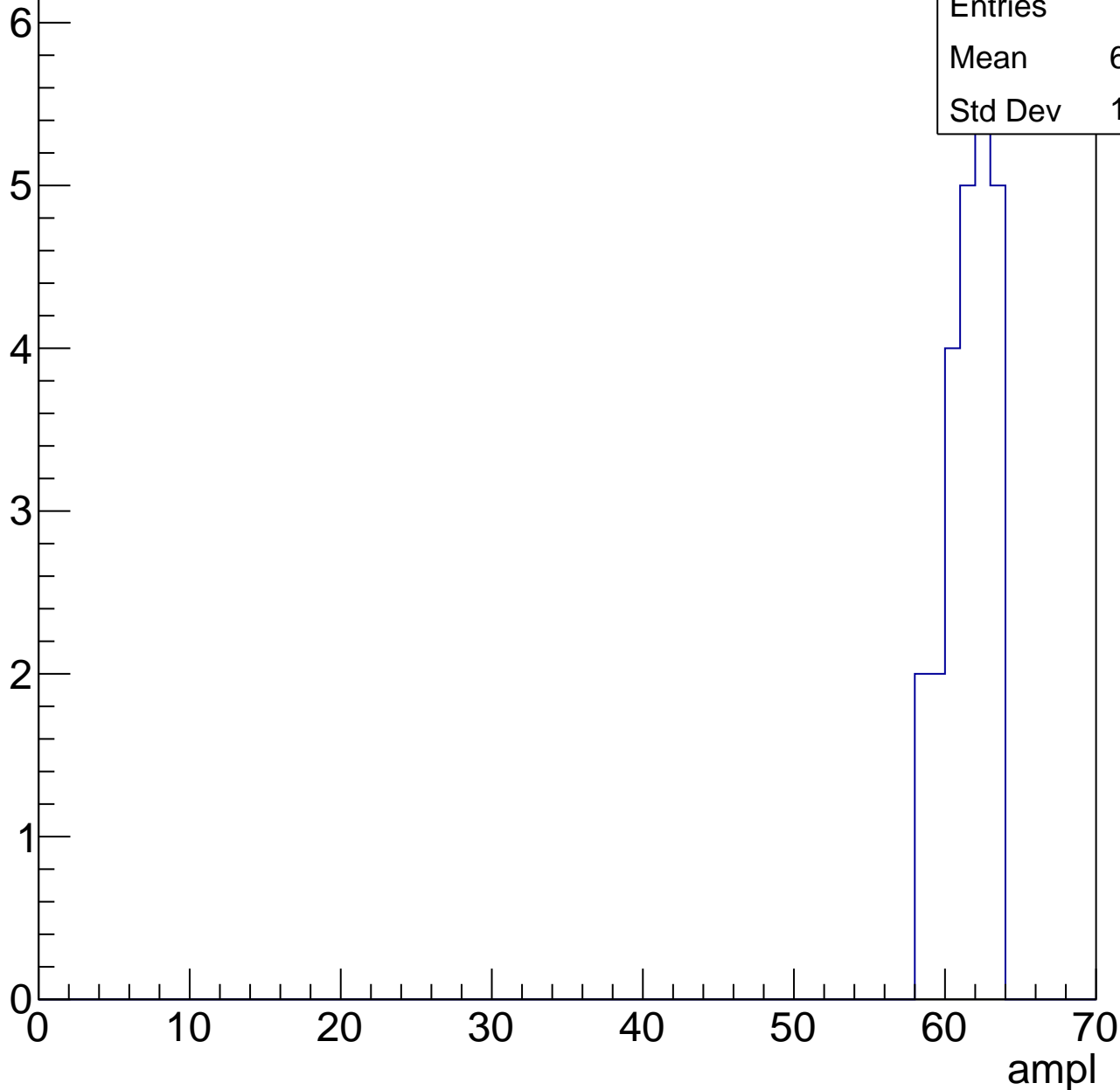


# B0L002S, U23-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

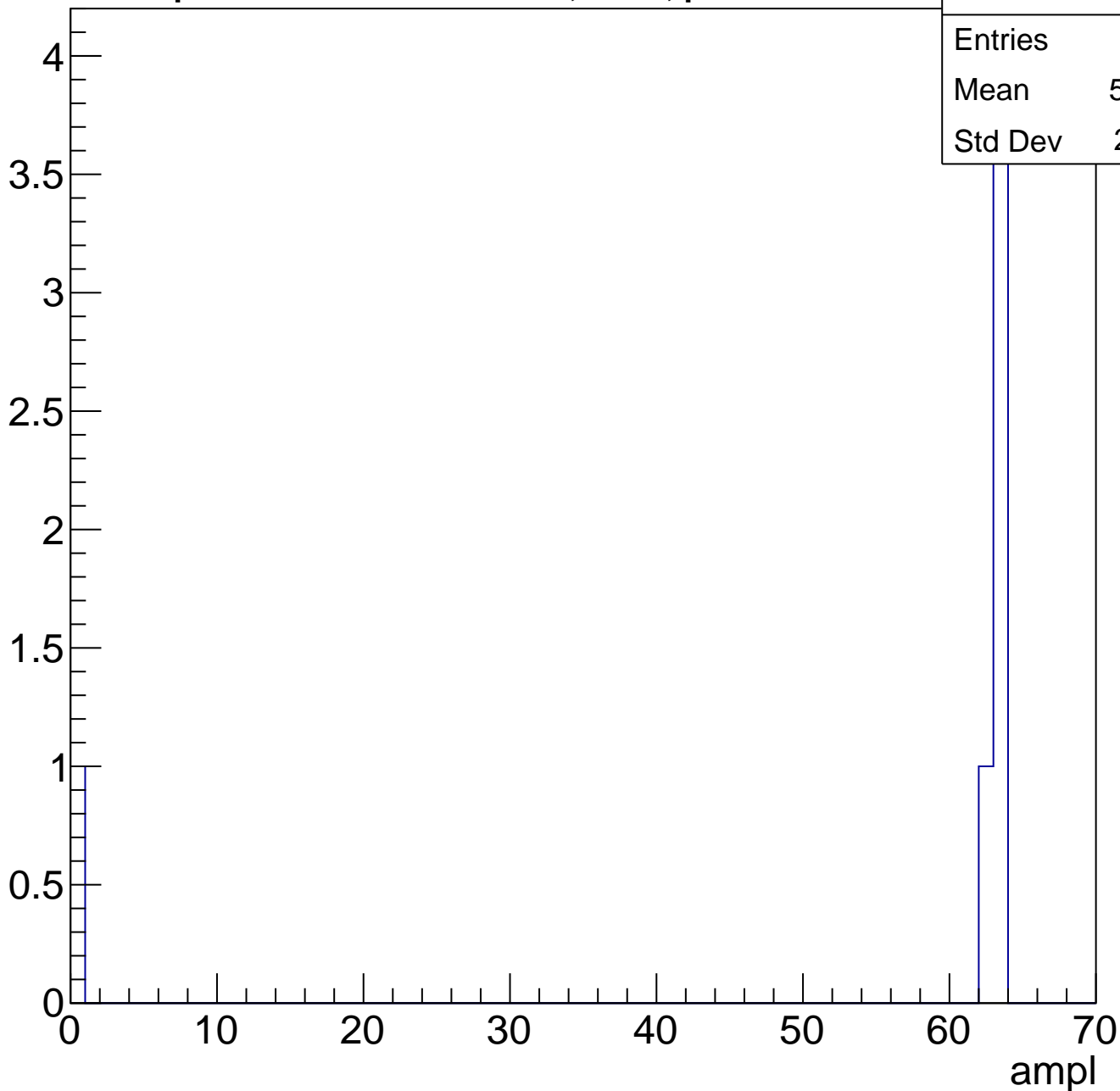
Entries	24
Mean	61.08
Std Dev	1.525



# B0L002S, U23-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B0L002S, U23-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	94
Mean	28.03
Std Dev	4.63

**Gaus mean : 28.4981**

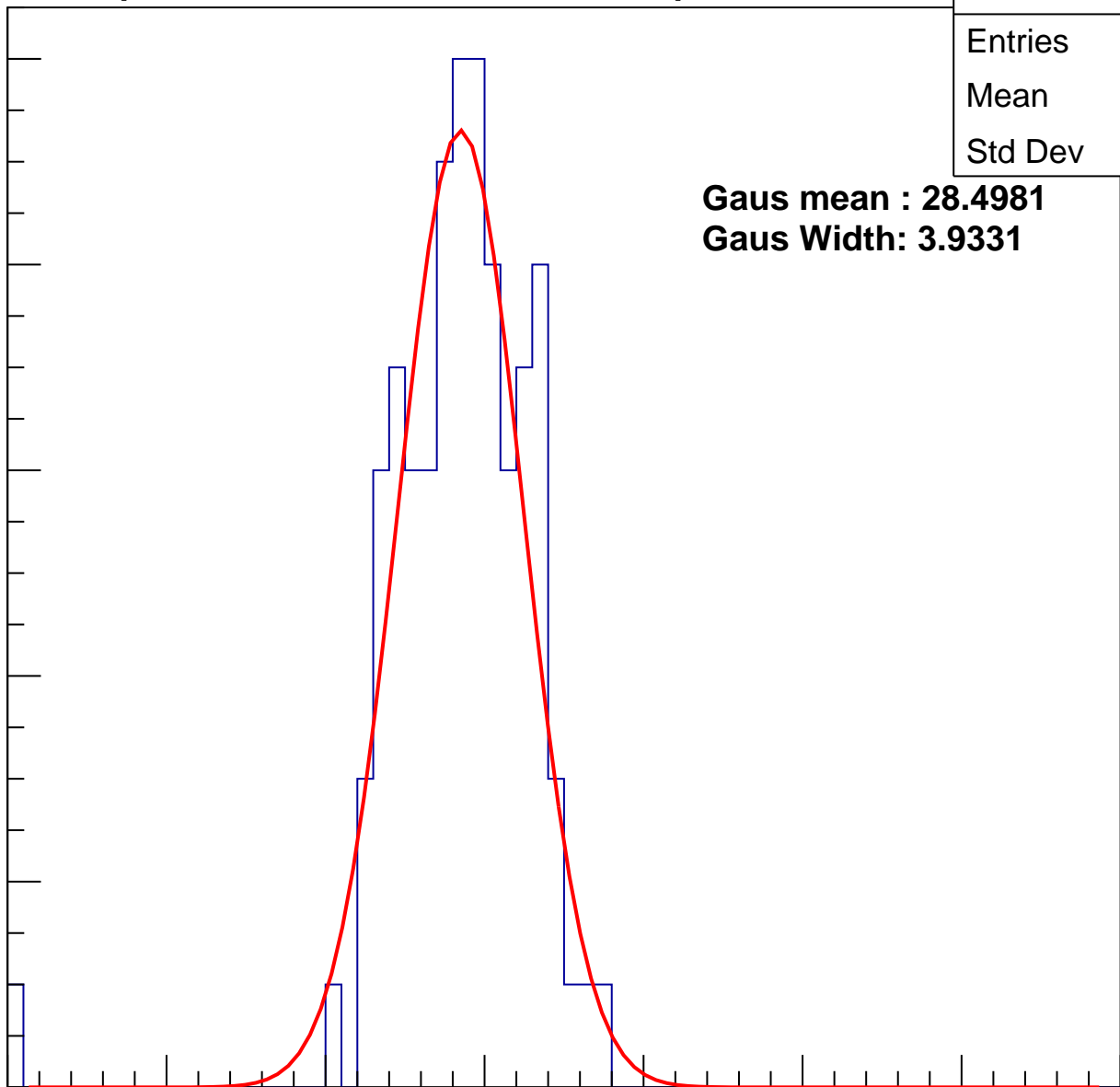
**Gaus Width: 3.9331**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	77
Mean	37.14
Std Dev	3.418

**Gaus mean : 37.7449**

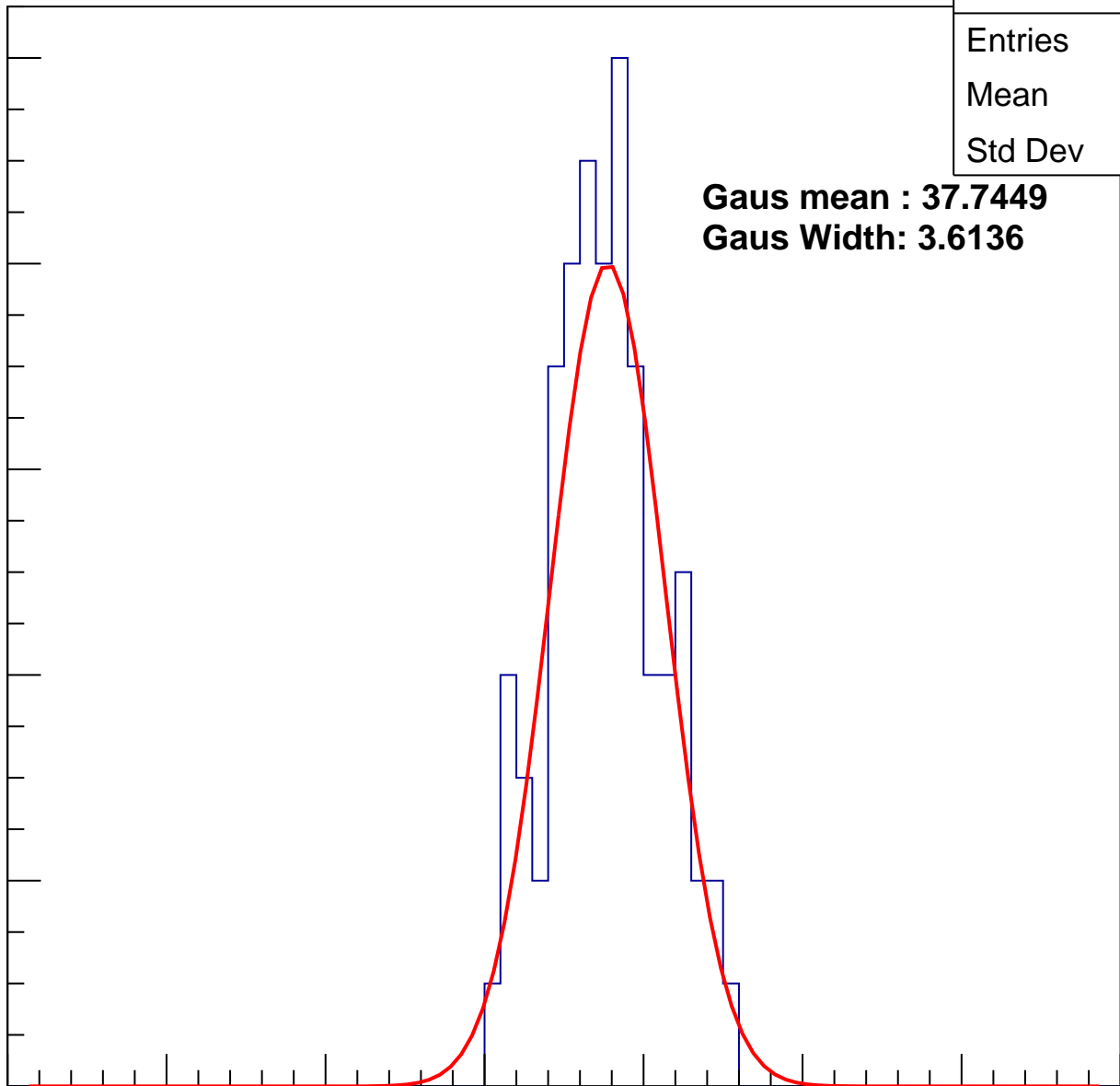
**Gaus Width: 3.6136**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch65, adc2

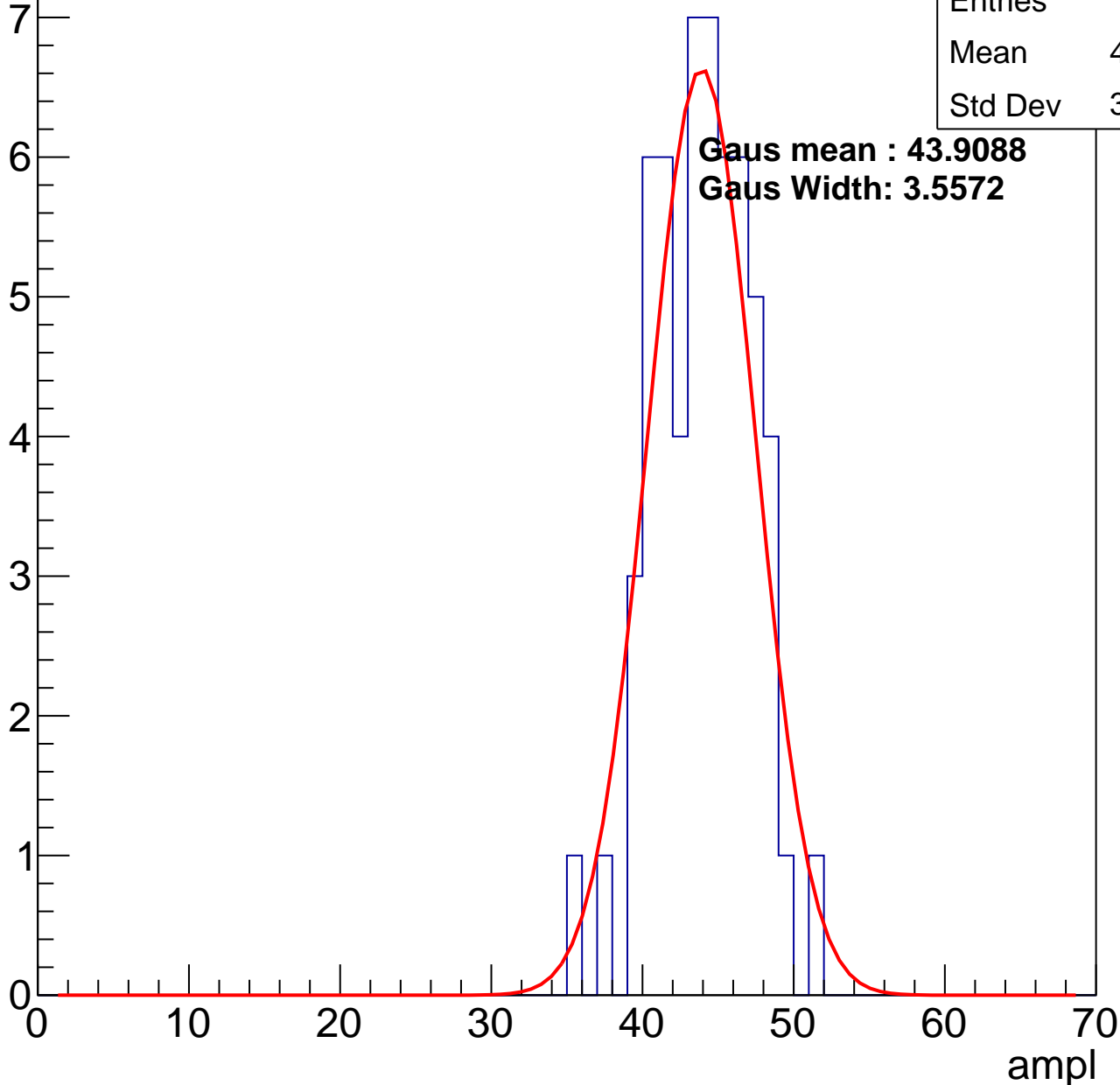
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.53
Std Dev	3.158

**Gaus mean : 43.9088**

**Gaus Width: 3.5572**



# B0L002S, U23-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

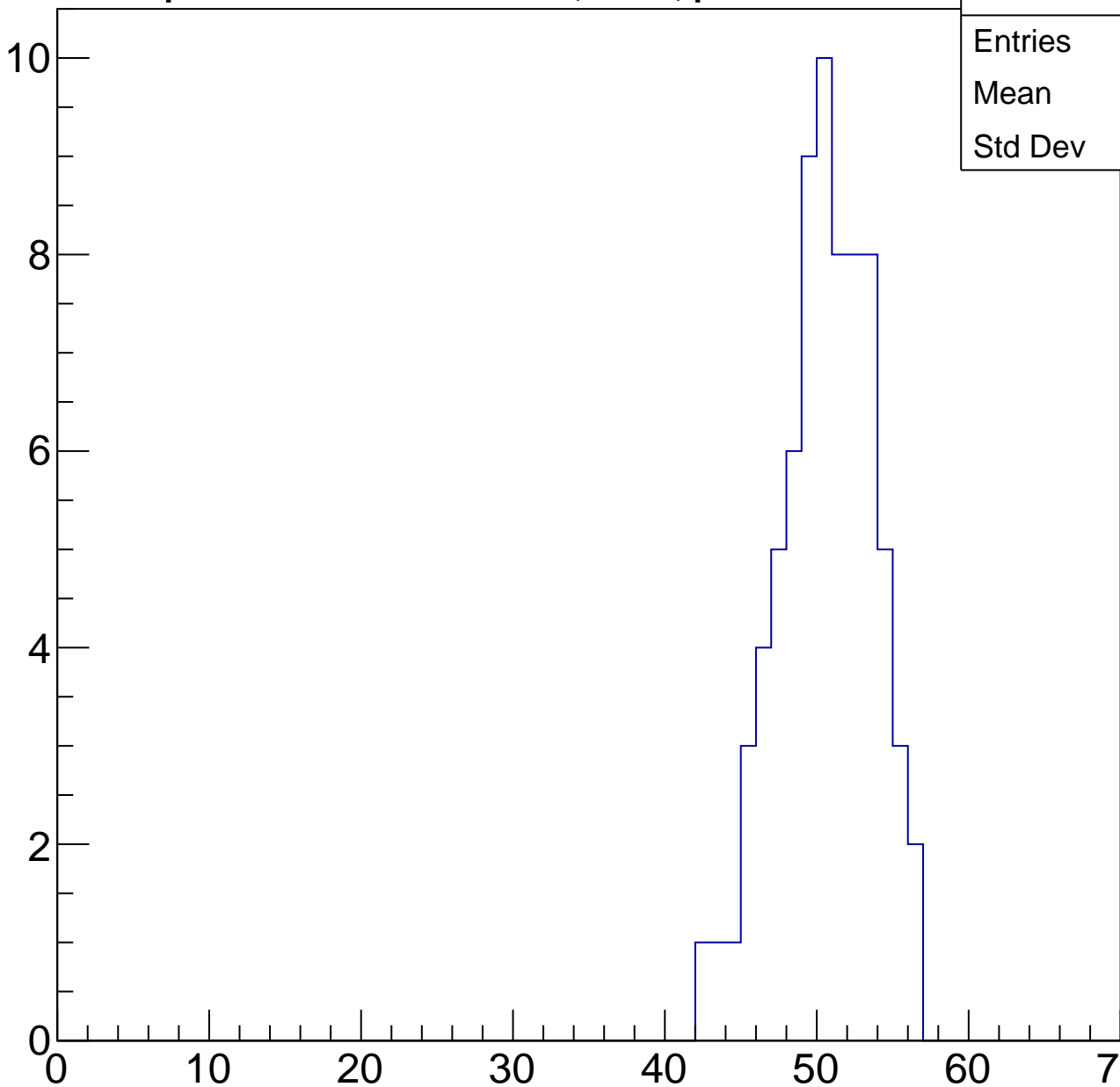
Entries	74
Mean	50.09
Std Dev	3.085

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

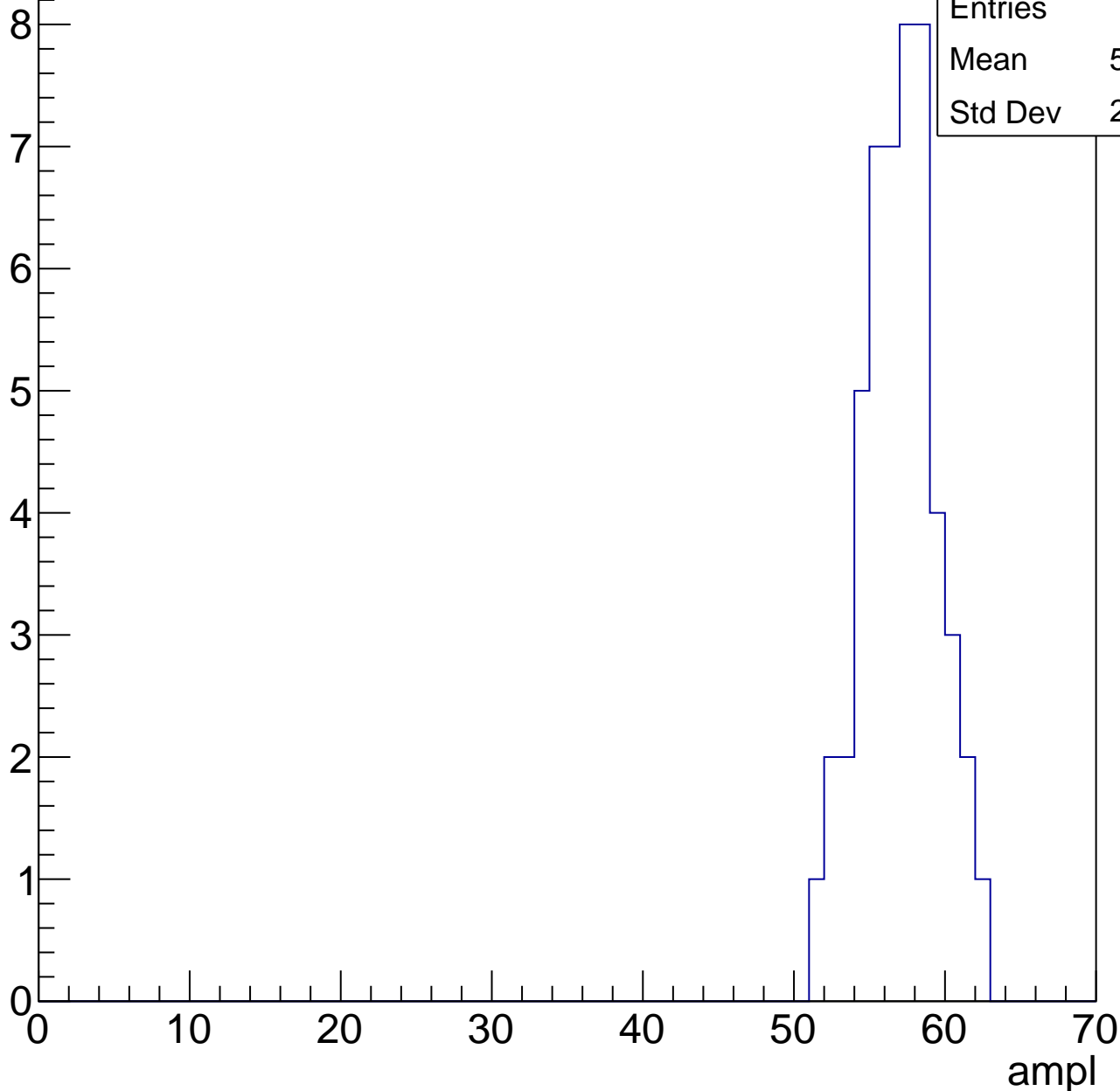


# B0L002S, U23-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.56
Std Dev	2.434

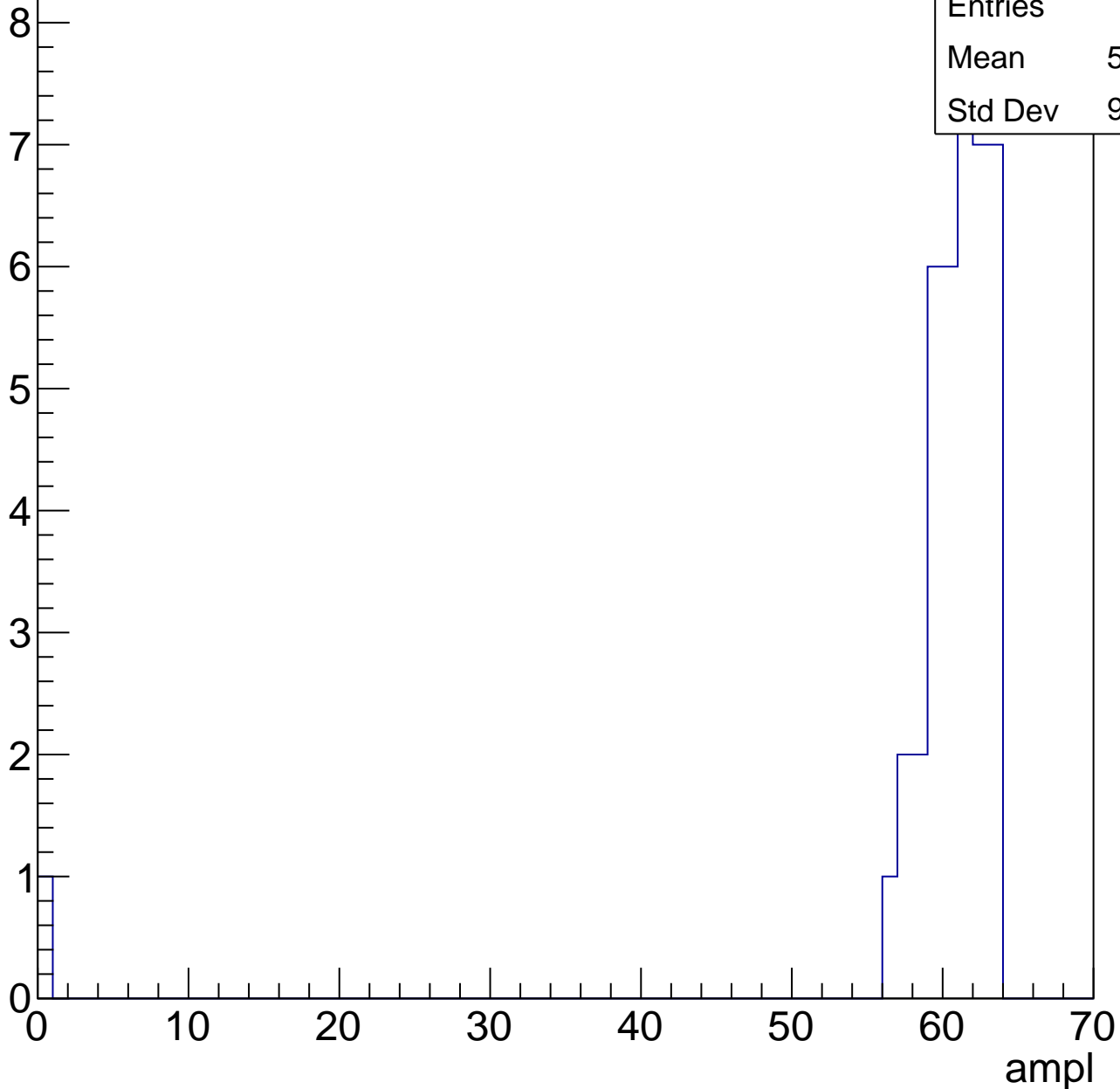


# B0L002S, U23-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.08
Std Dev	9.634



# B0L002S, U23-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch66, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	54
Mean	29.15
Std Dev	4.817

**Gaus mean : 30.6556**

**Gaus Width: 2.7715**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L002S, U23-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	57
Mean	35.16
Std Dev	2.667

**Gaus mean : 35.9508**

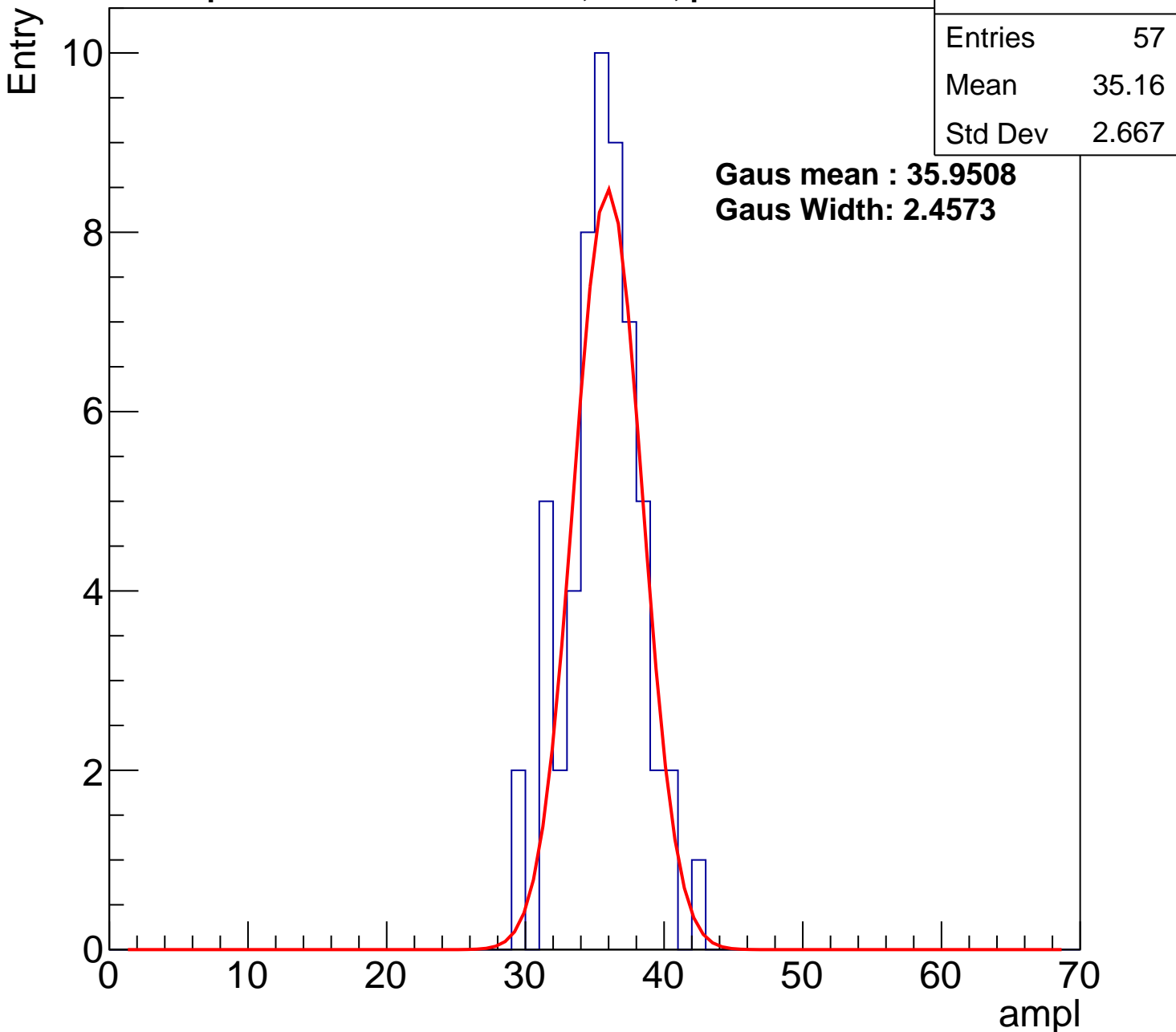
**Gaus Width: 2.4573**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	85
Mean	42.6
Std Dev	3.344

**Gaus mean : 42.8012**

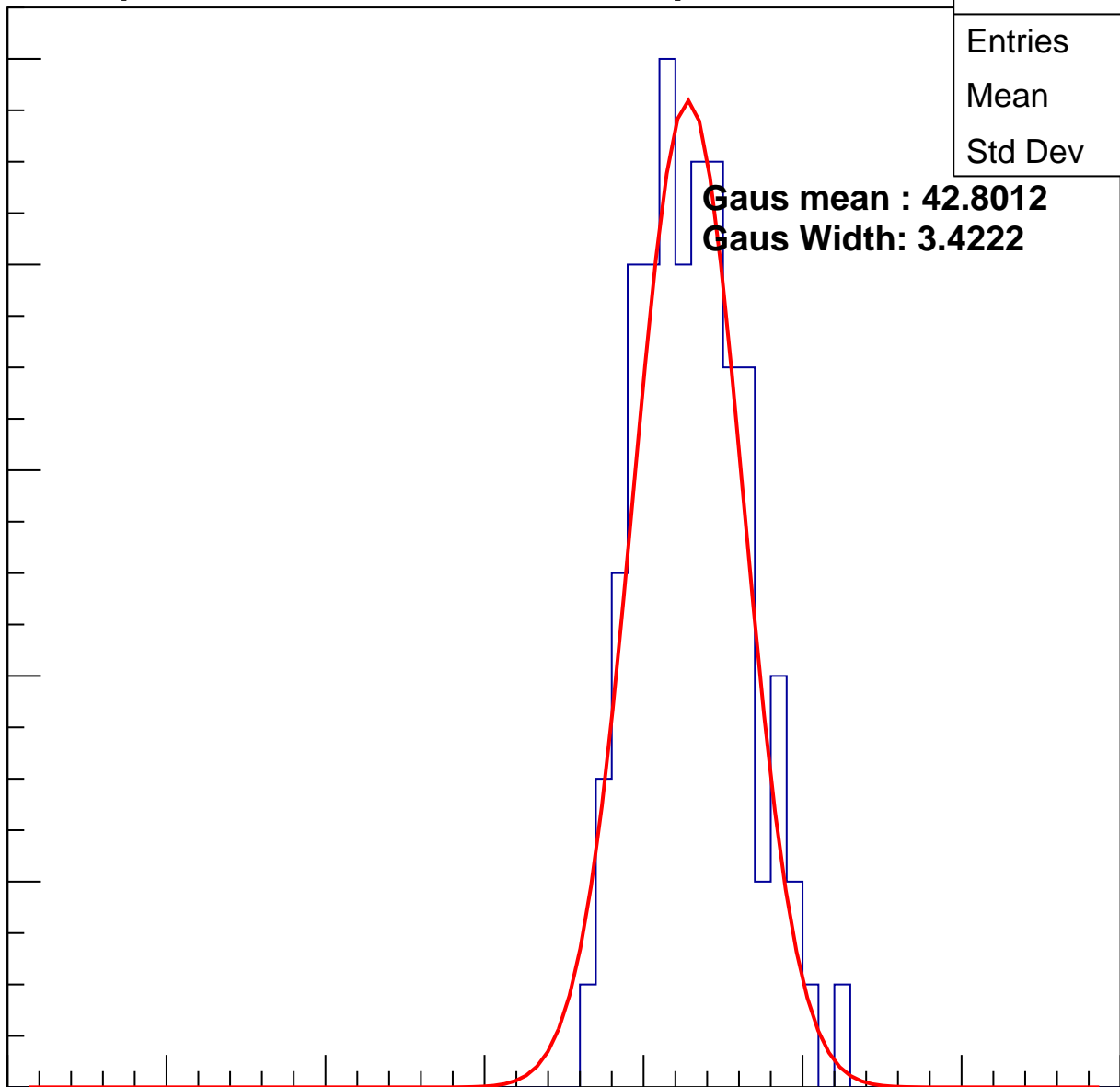
**Gaus Width: 3.4222**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

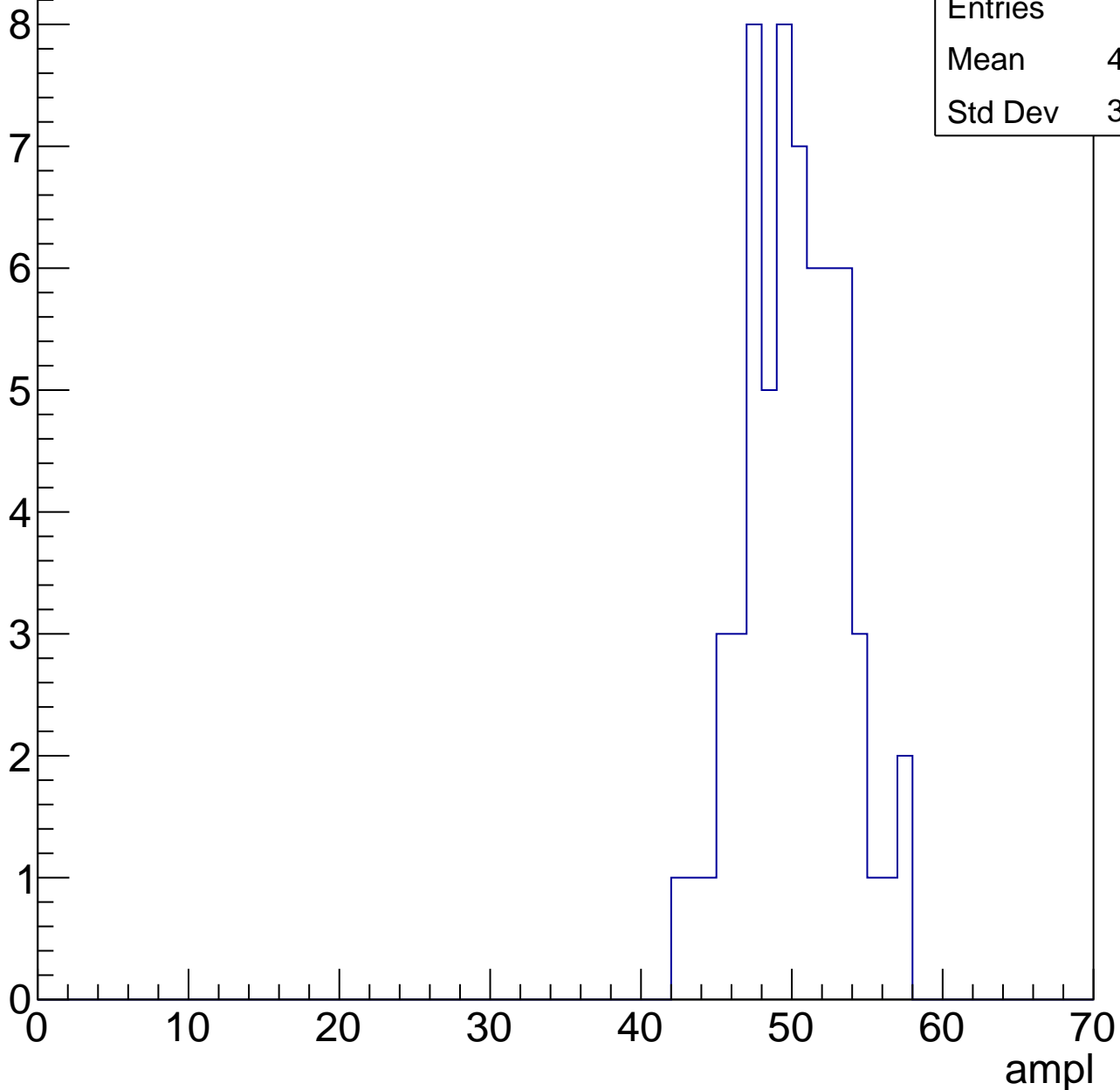


# B0L002S, U23-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	49.73
Std Dev	3.259

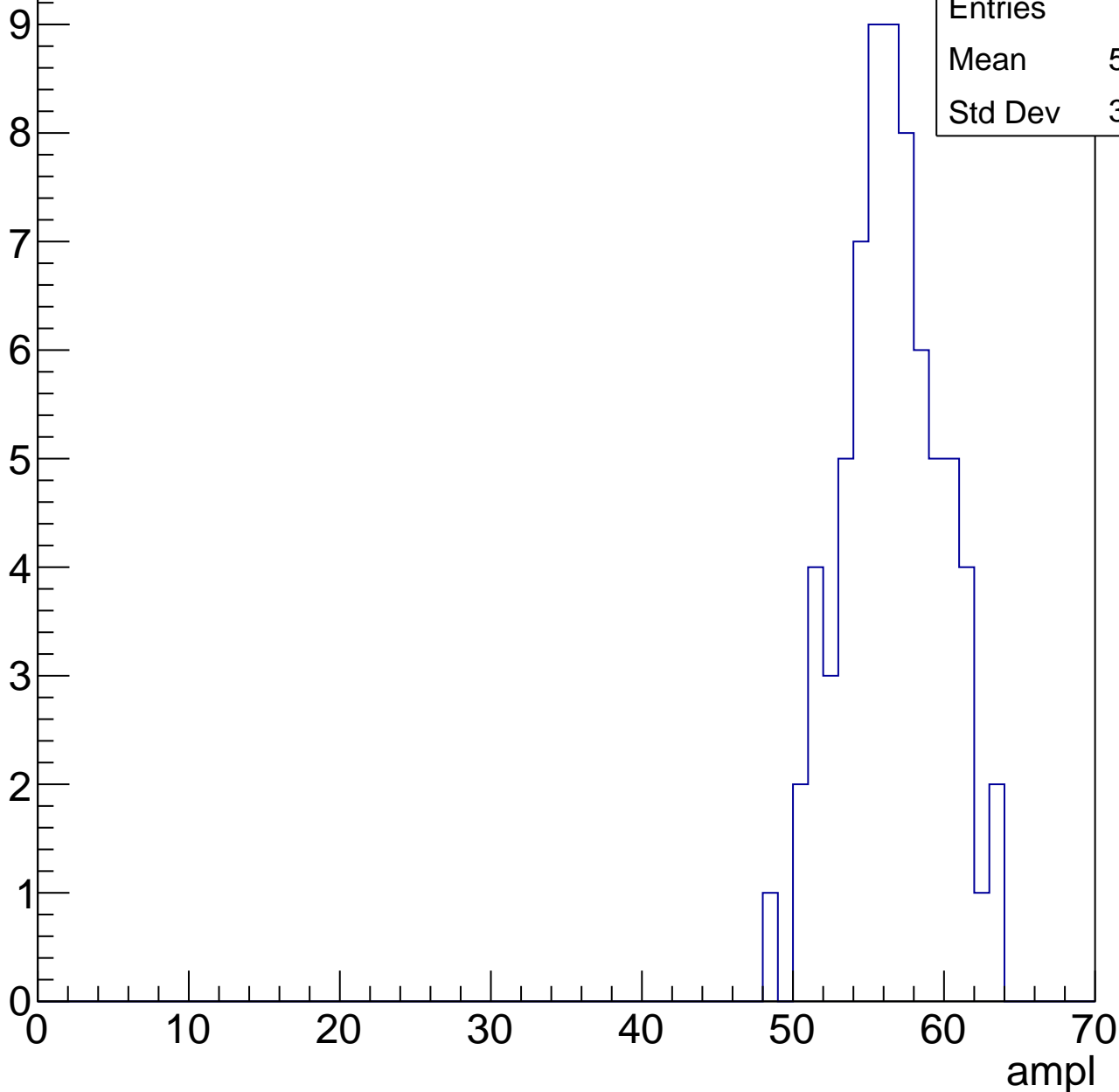


# B0L002S, U23-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

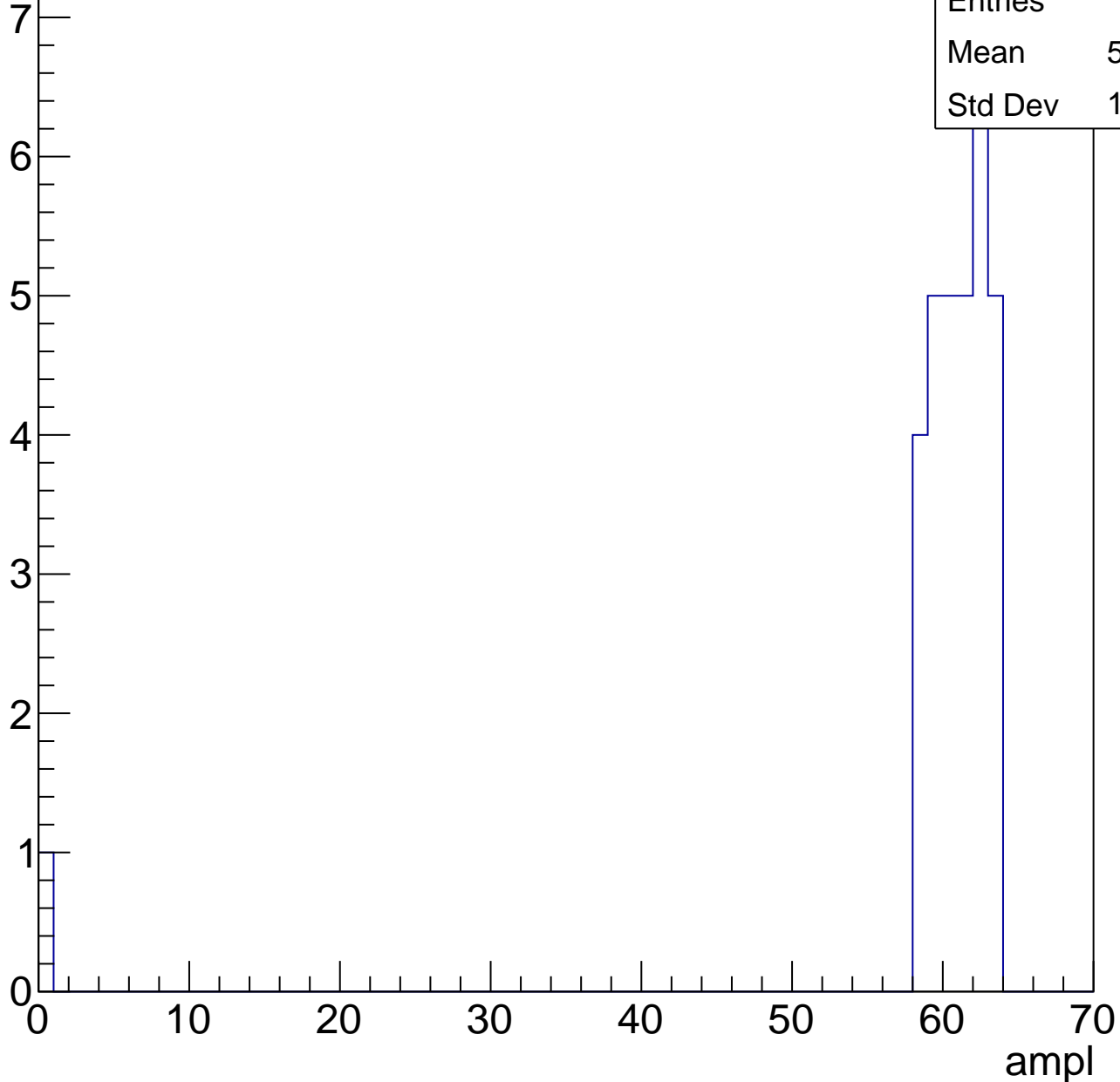
Entries	71
Mean	56.07
Std Dev	3.265



# B0L002S, U23-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch67, adc0

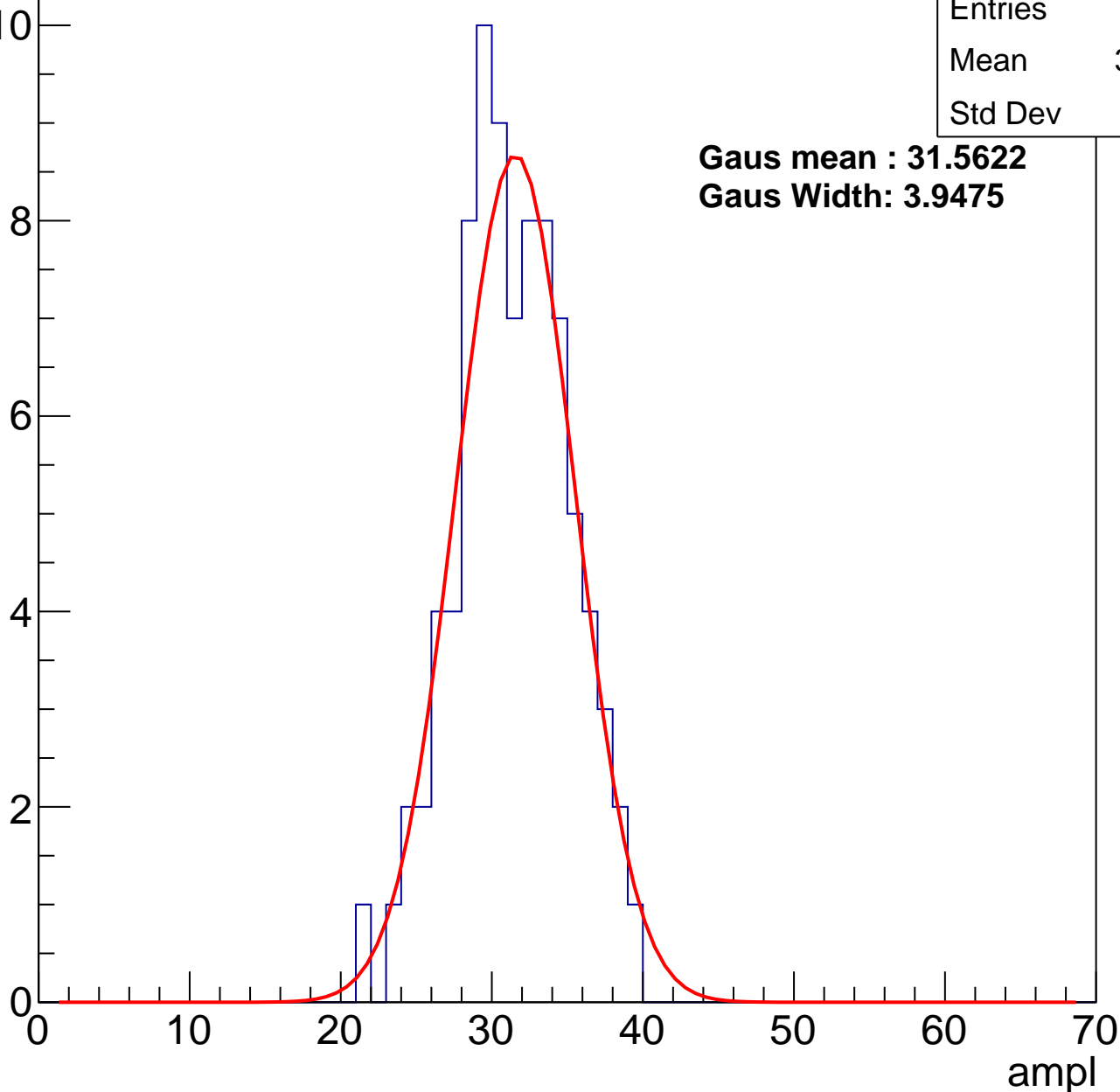
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	86
Mean	30.91
Std Dev	3.69

**Gaus mean : 31.5622**

**Gaus Width: 3.9475**



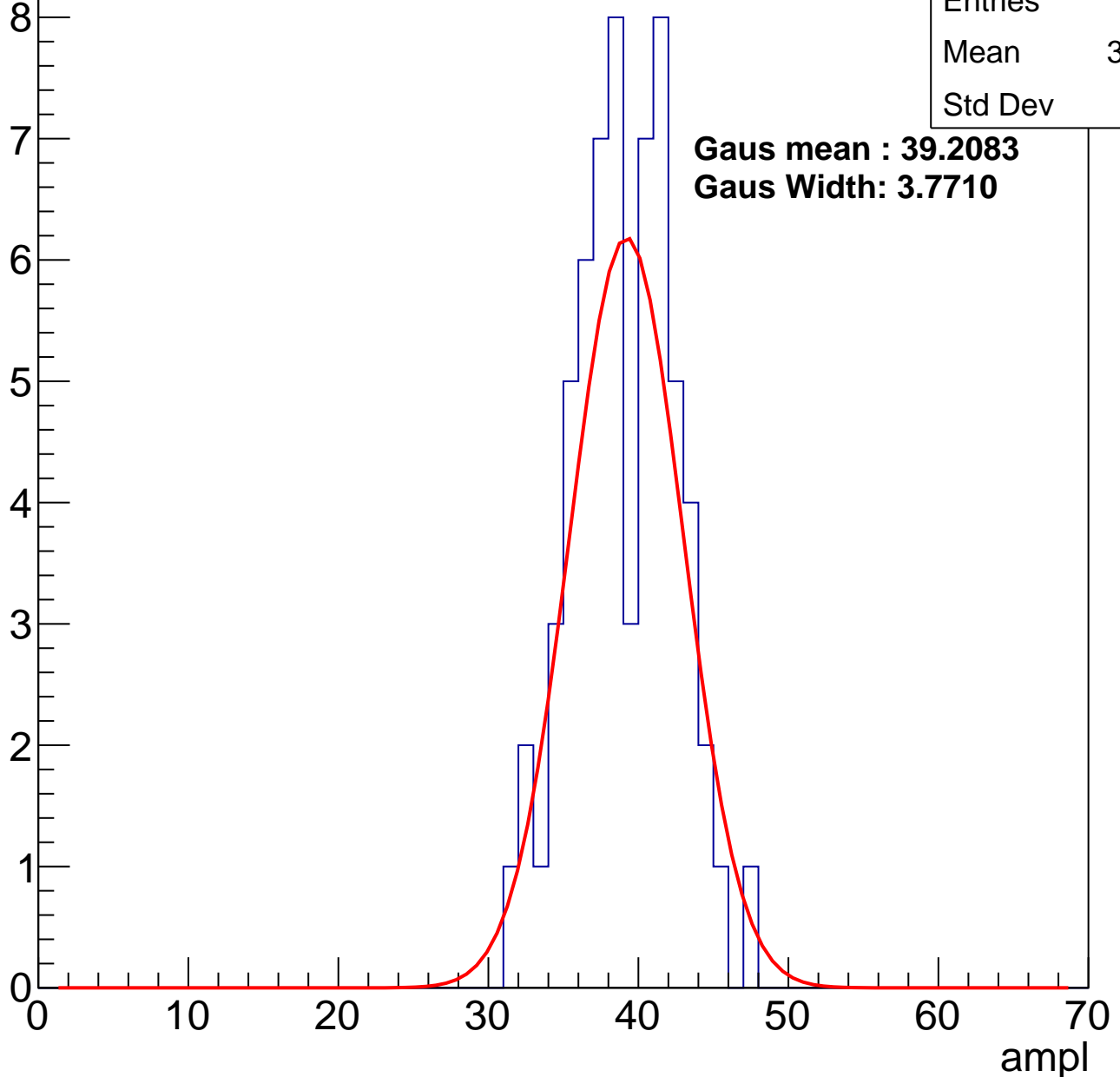
# B0L002S, U23-ch67, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.61
Std Dev	3.38

**Gaus mean : 39.2083**  
**Gaus Width: 3.7710**



# B0L002S, U23-ch67, adc2

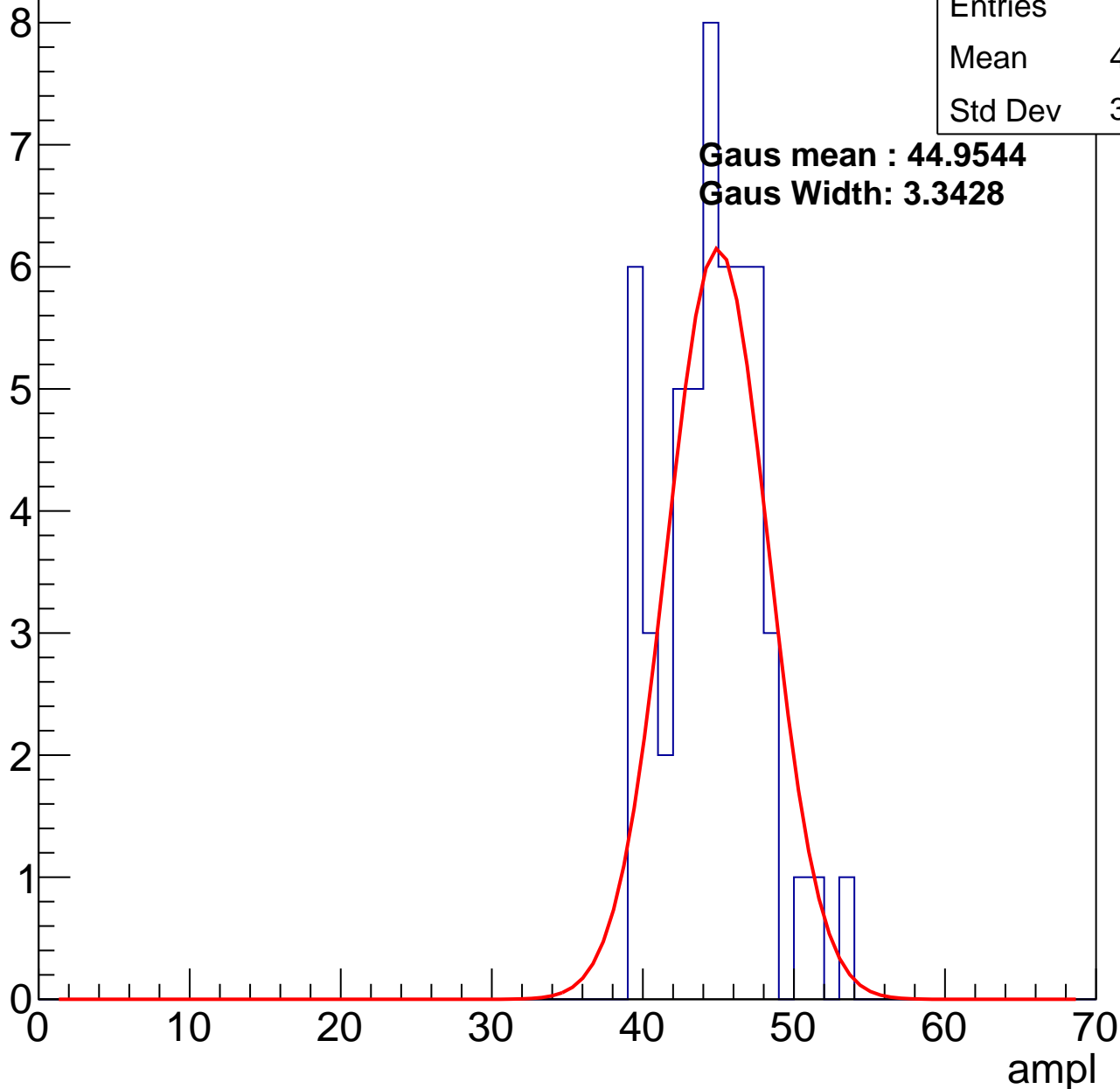
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.13
Std Dev	3.186

**Gaus mean : 44.9544**

**Gaus Width: 3.3428**



# B0L002S, U23-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

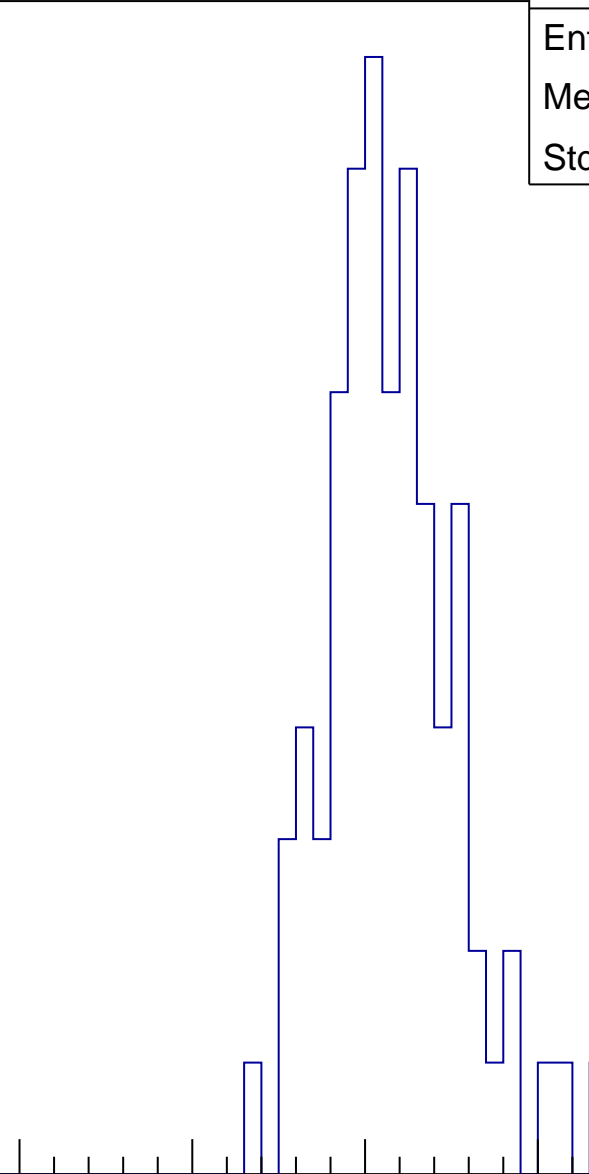
Entries	77
Mean	51.14
Std Dev	3.782

Entry

10  
8  
6  
4  
2  
0

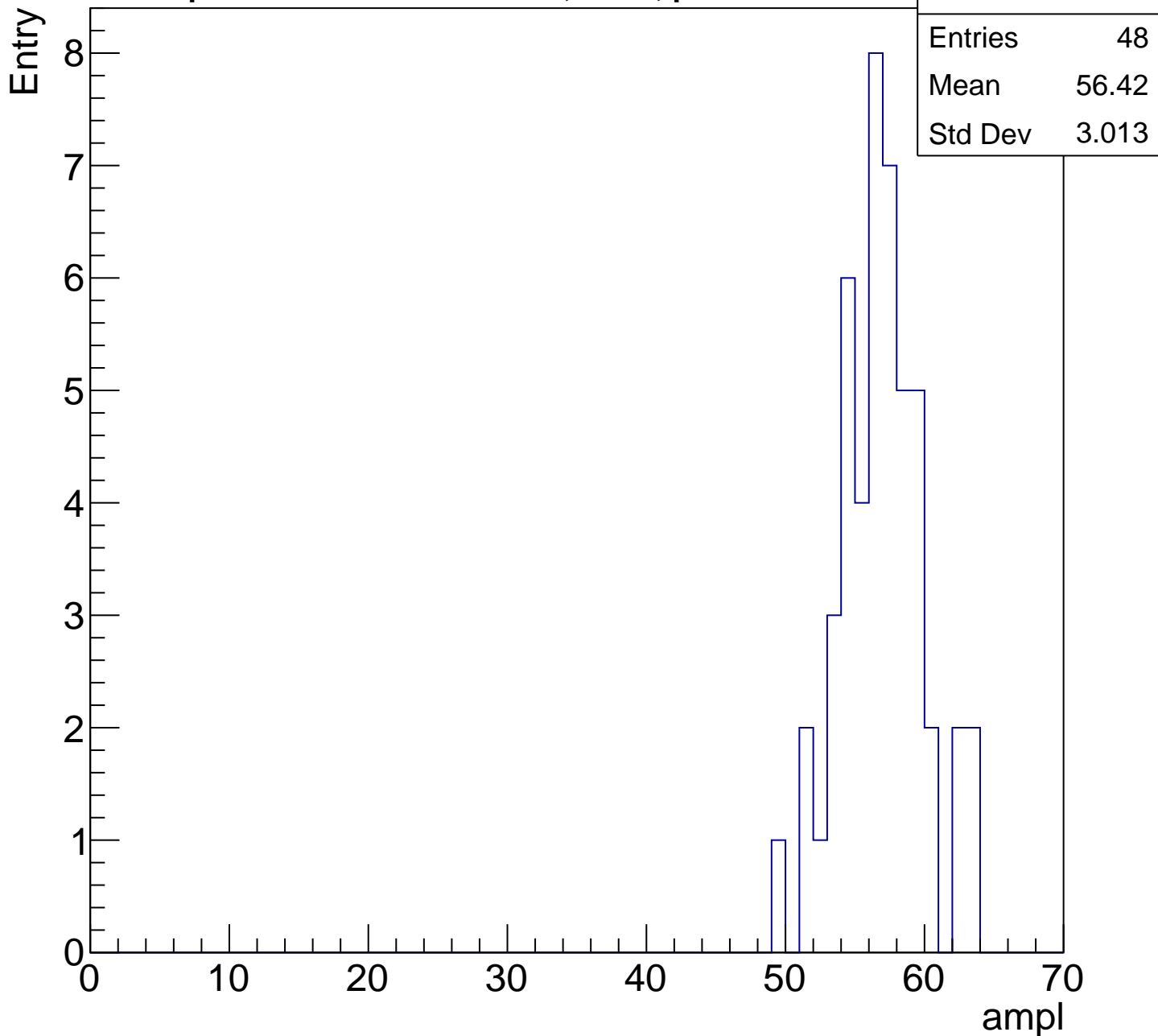
0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

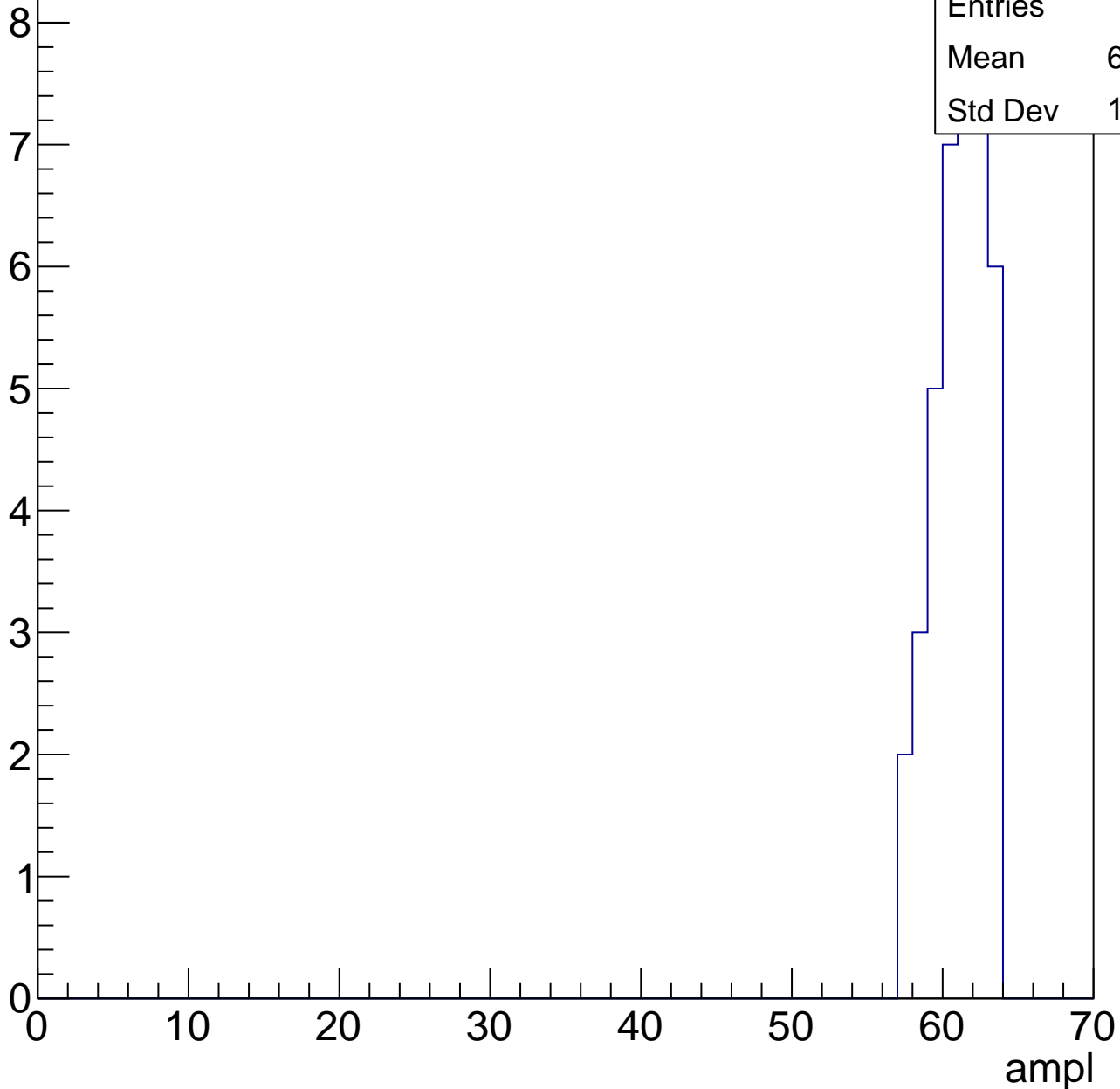


# B0L002S, U23-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

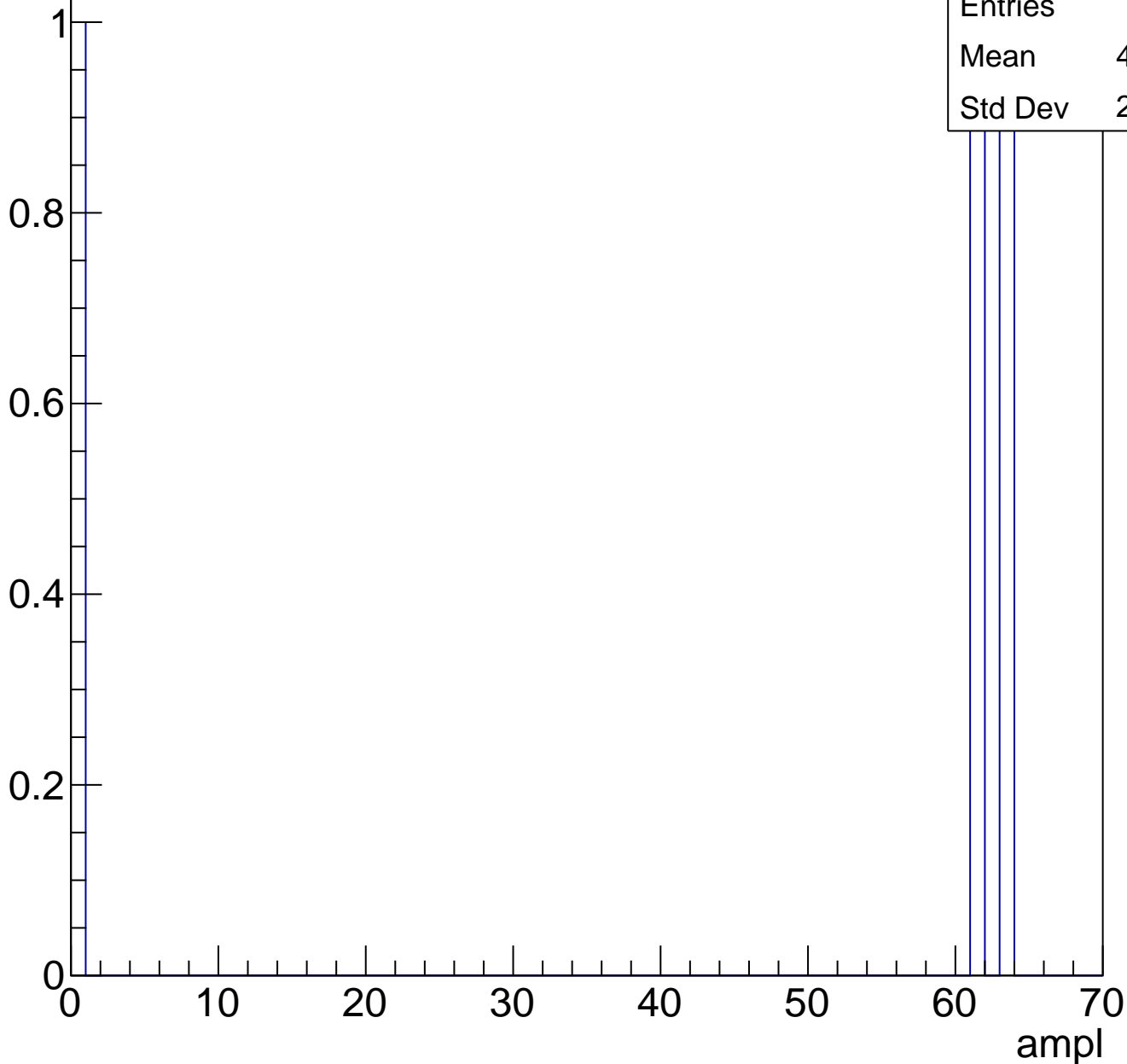
Entries	39
Mean	60.64
Std Dev	1.702



# B0L002S, U23-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch68, adc0

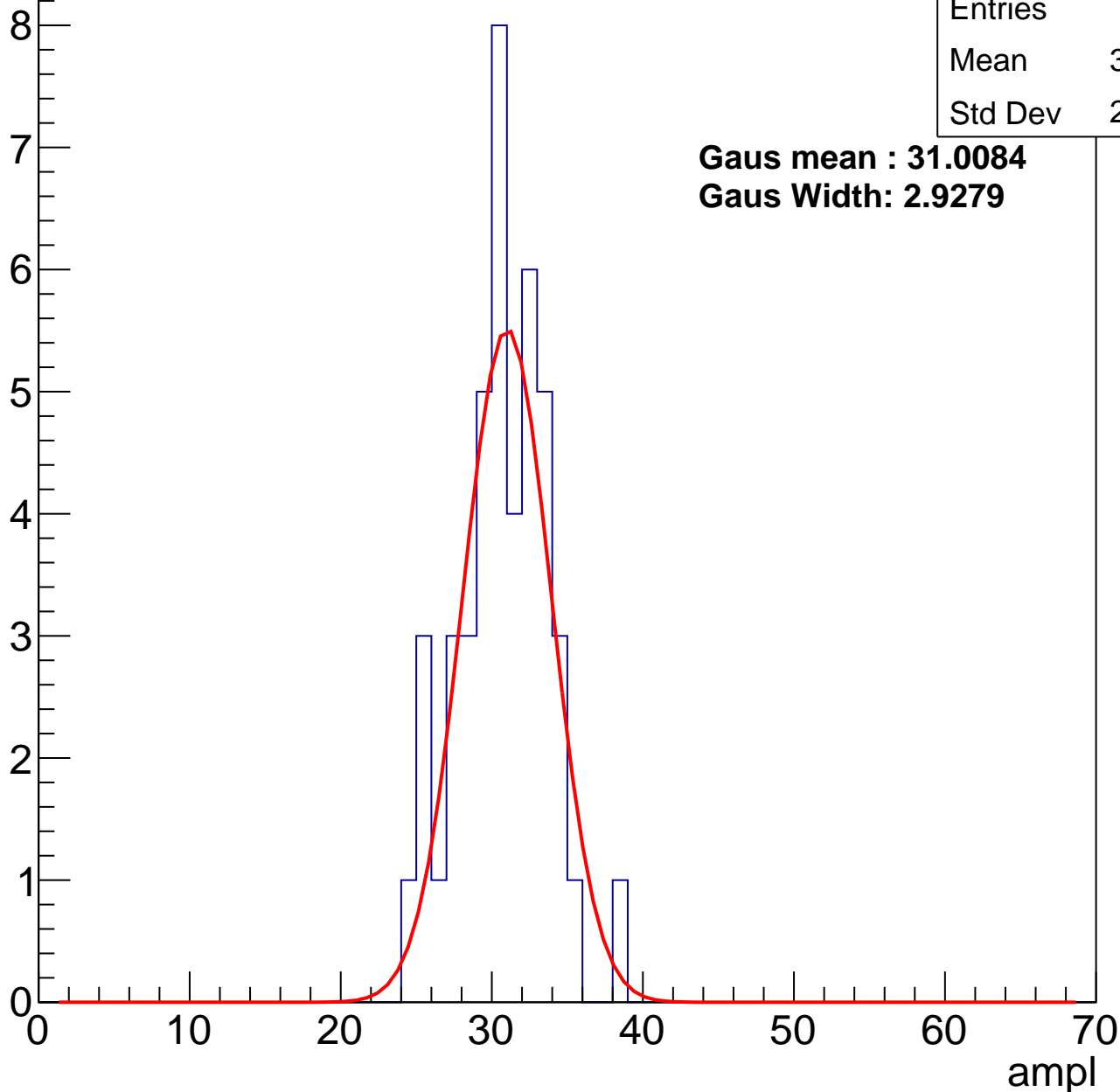
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	44
Mean	30.25
Std Dev	2.932

**Gaus mean : 31.0084**

**Gaus Width: 2.9279**



# B0L002S, U23-ch68, adc1

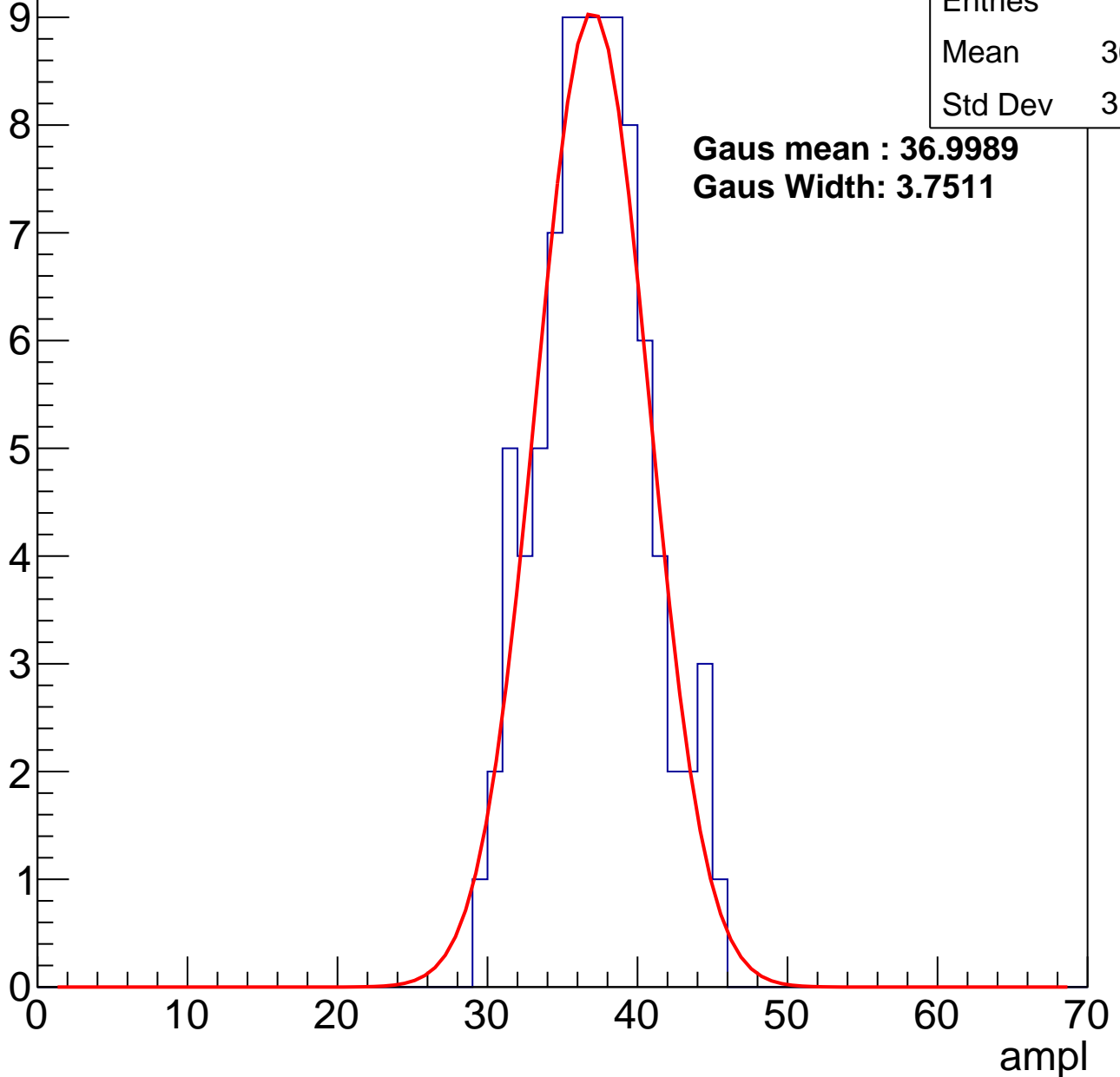
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	86
Mean	36.65
Std Dev	3.595

**Gaus mean : 36.9989**

**Gaus Width: 3.7511**



# B0L002S, U23-ch68, adc2

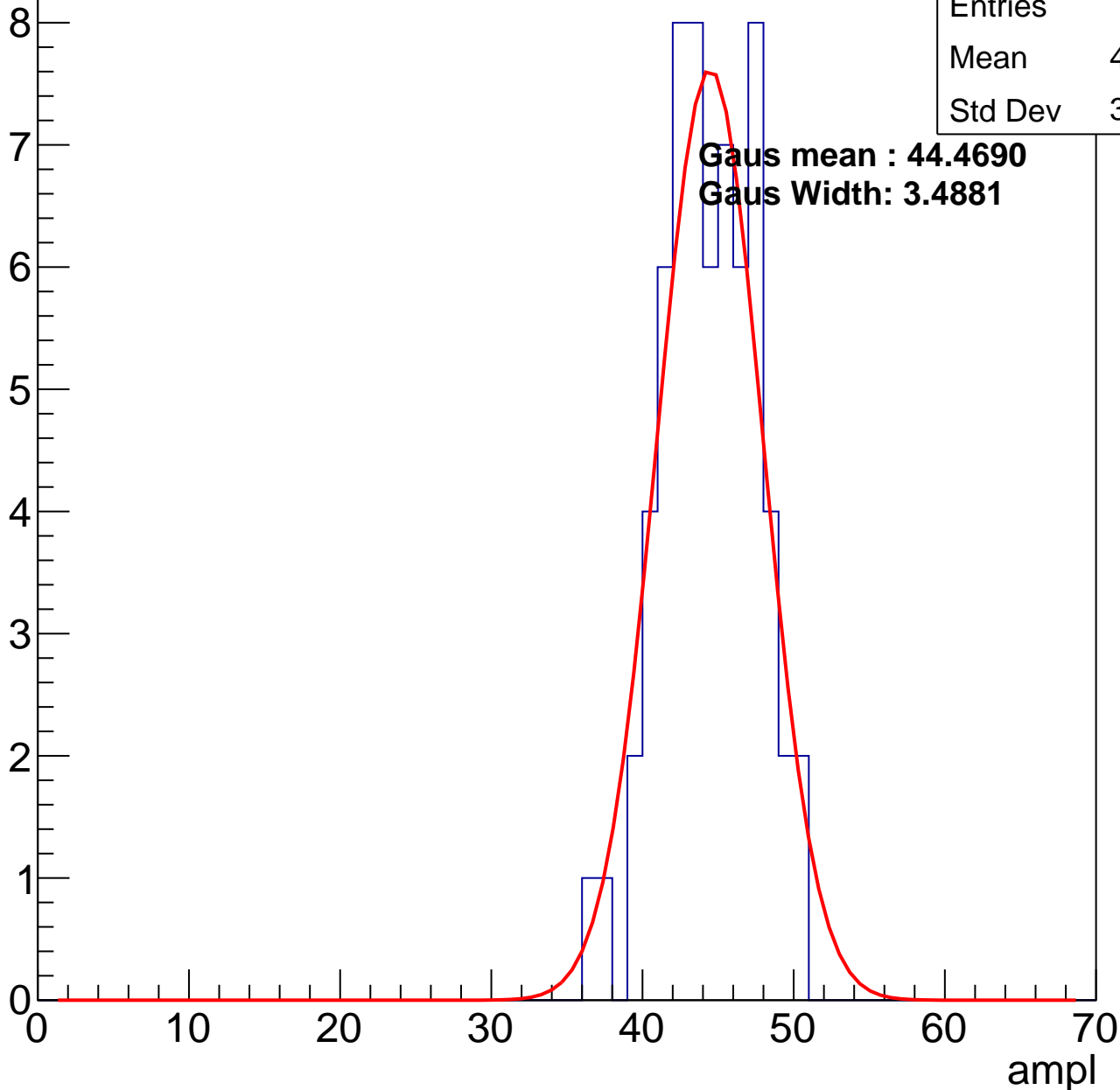
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.97
Std Dev	3.063

**Gaus mean : 44.4690**

**Gaus Width: 3.4881**

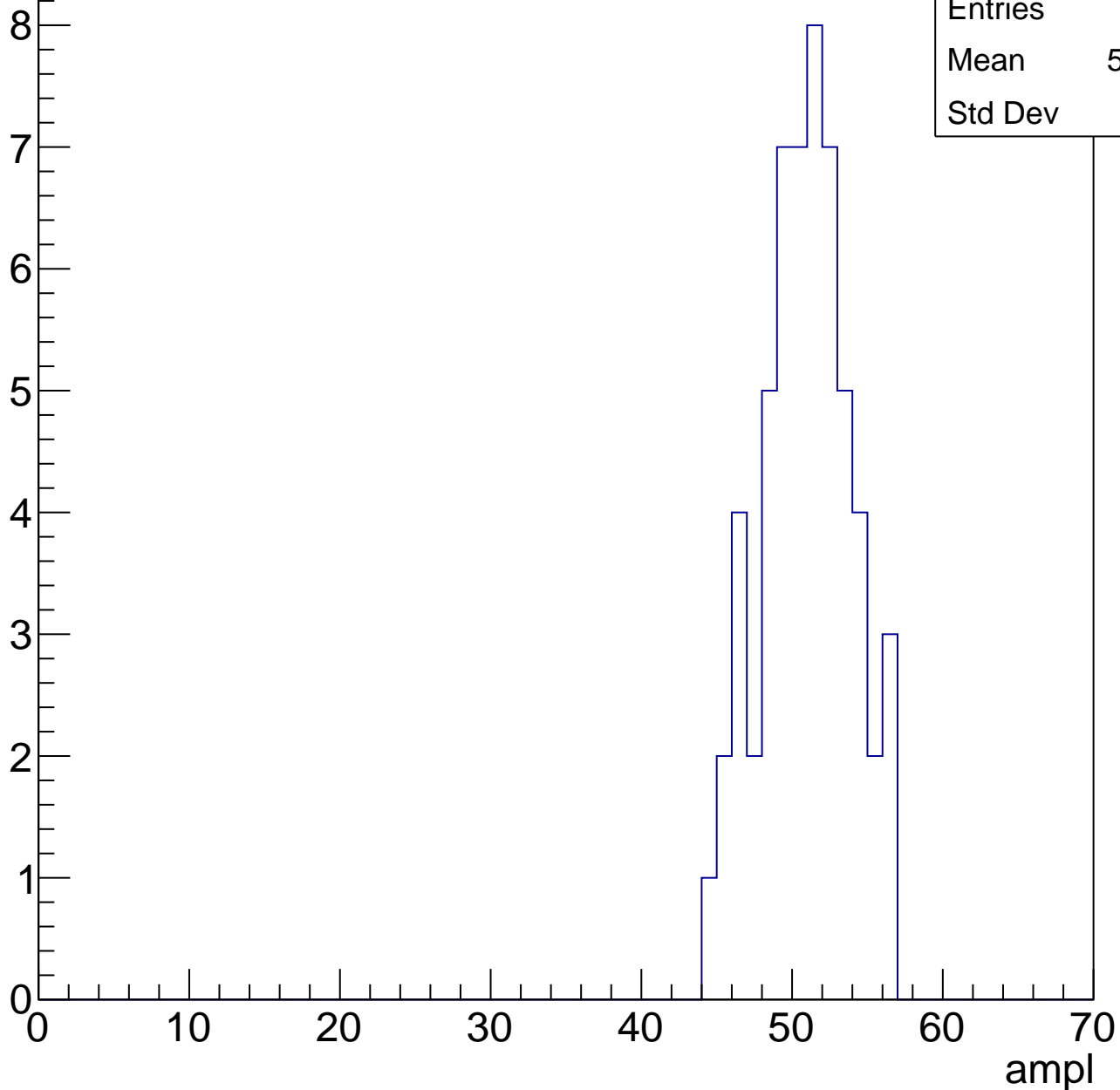


# B0L002S, U23-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	50.46
Std Dev	2.92

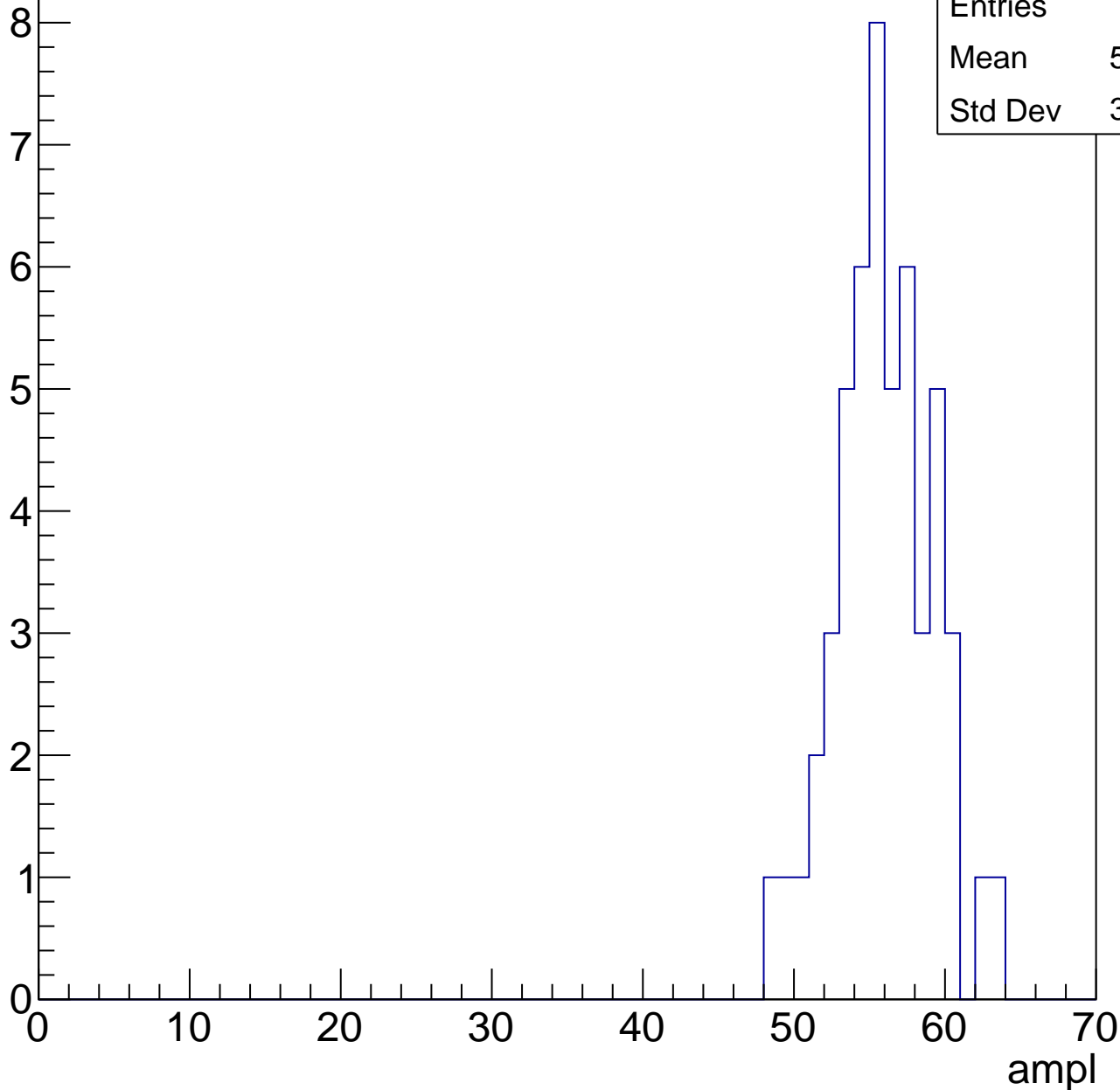


# B0L002S, U23-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.49
Std Dev	3.152

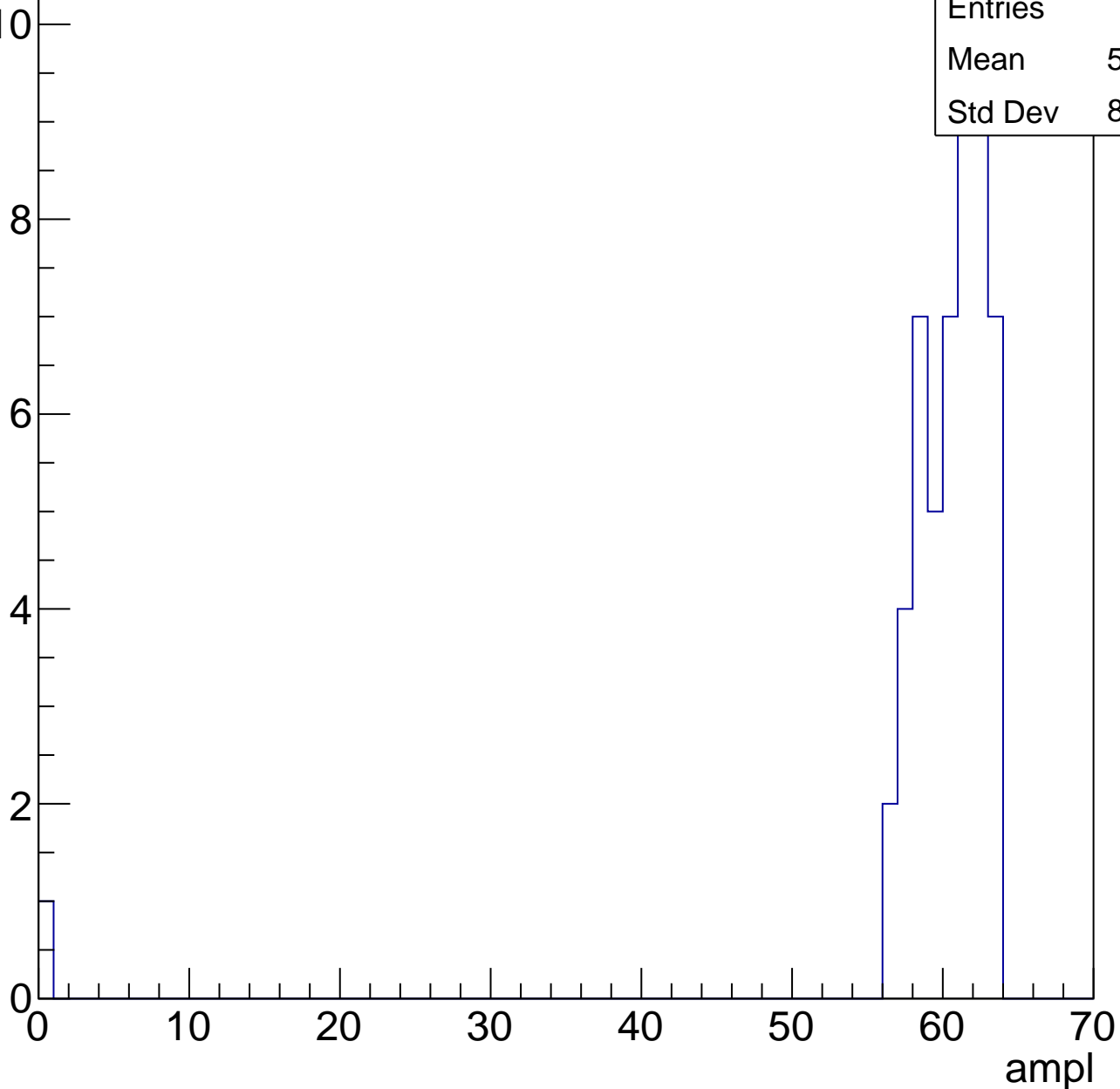


# B0L002S, U23-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	59.04
Std Dev	8.506



# B0L002S, U23-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch69, adc0

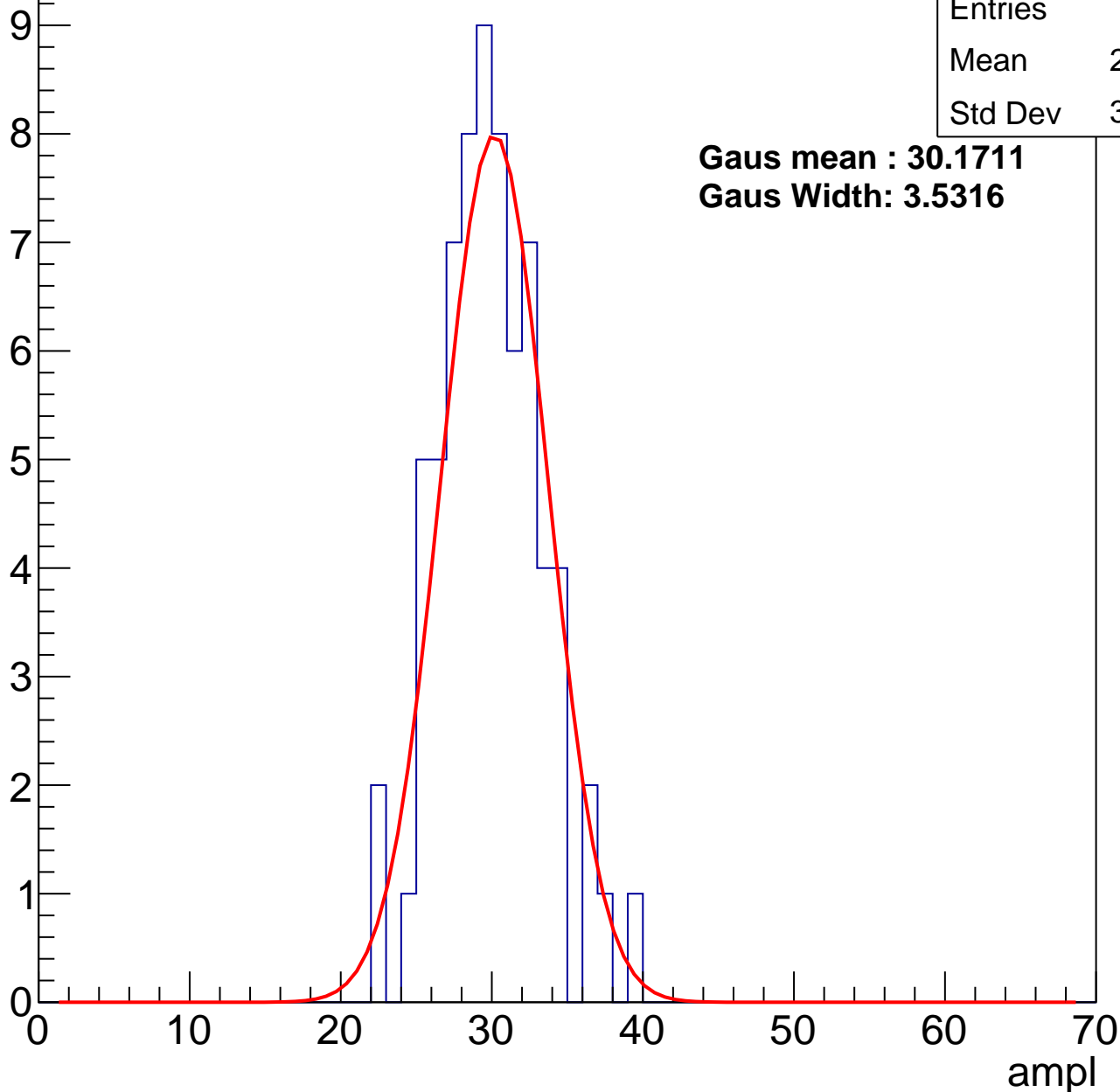
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.47
Std Dev	3.358

**Gaus mean : 30.1711**

**Gaus Width: 3.5316**



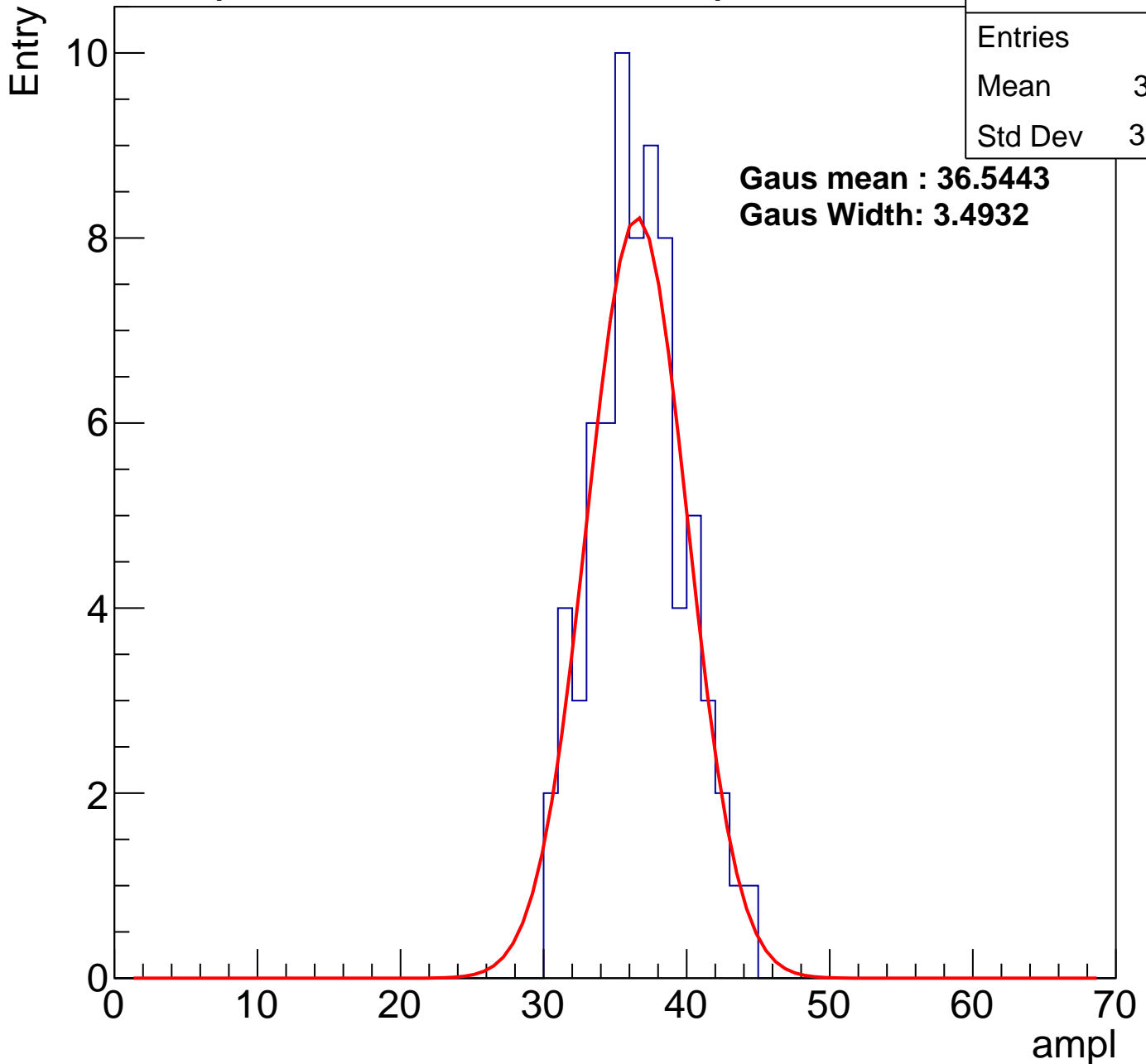
# B0L002S, U23-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	72
Mean	36.21
Std Dev	3.166

**Gaus mean : 36.5443**

**Gaus Width: 3.4932**



# B0L002S, U23-ch69, adc2

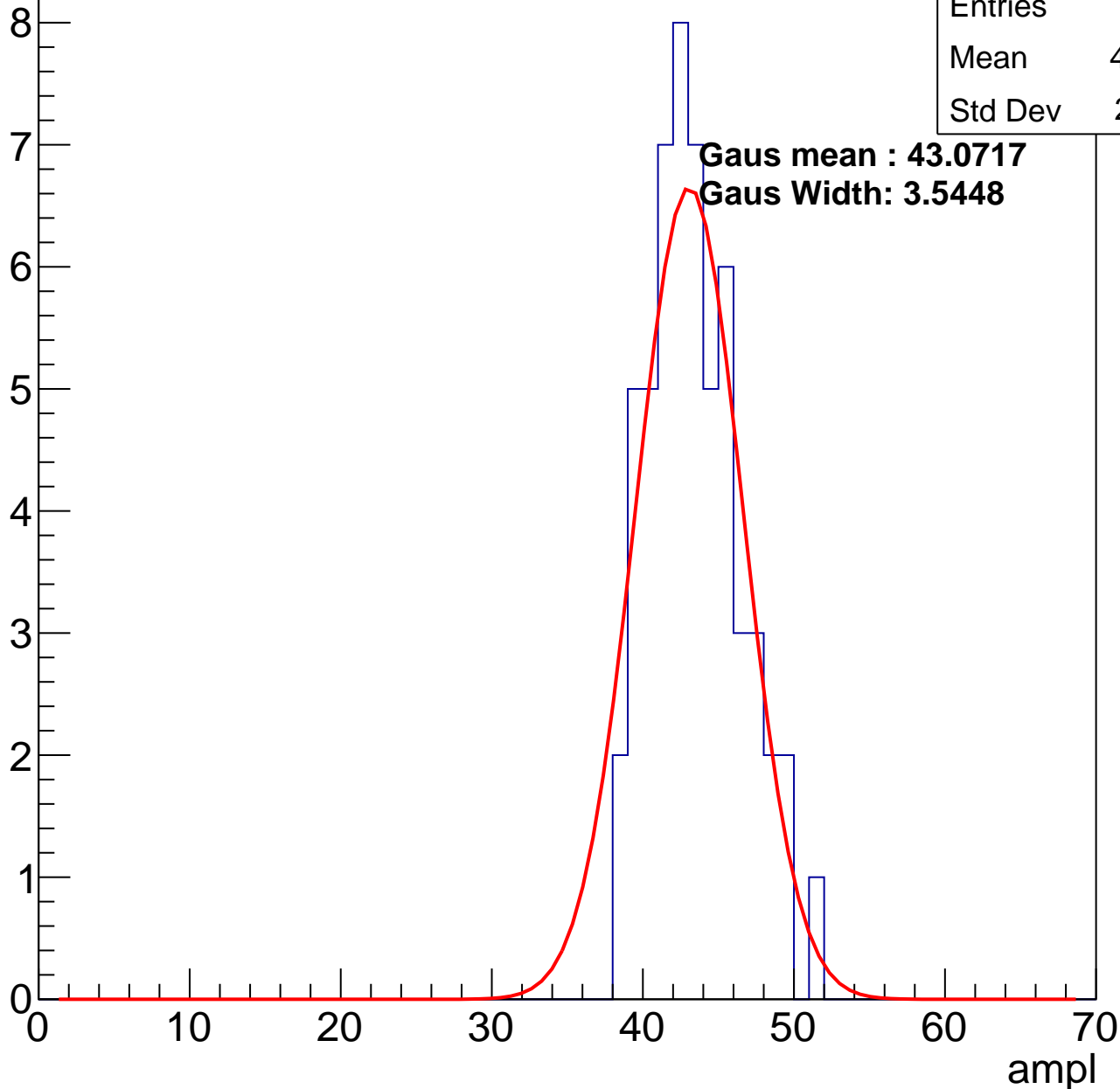
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.02
Std Dev	2.991

**Gaus mean : 43.0717**

**Gaus Width: 3.5448**

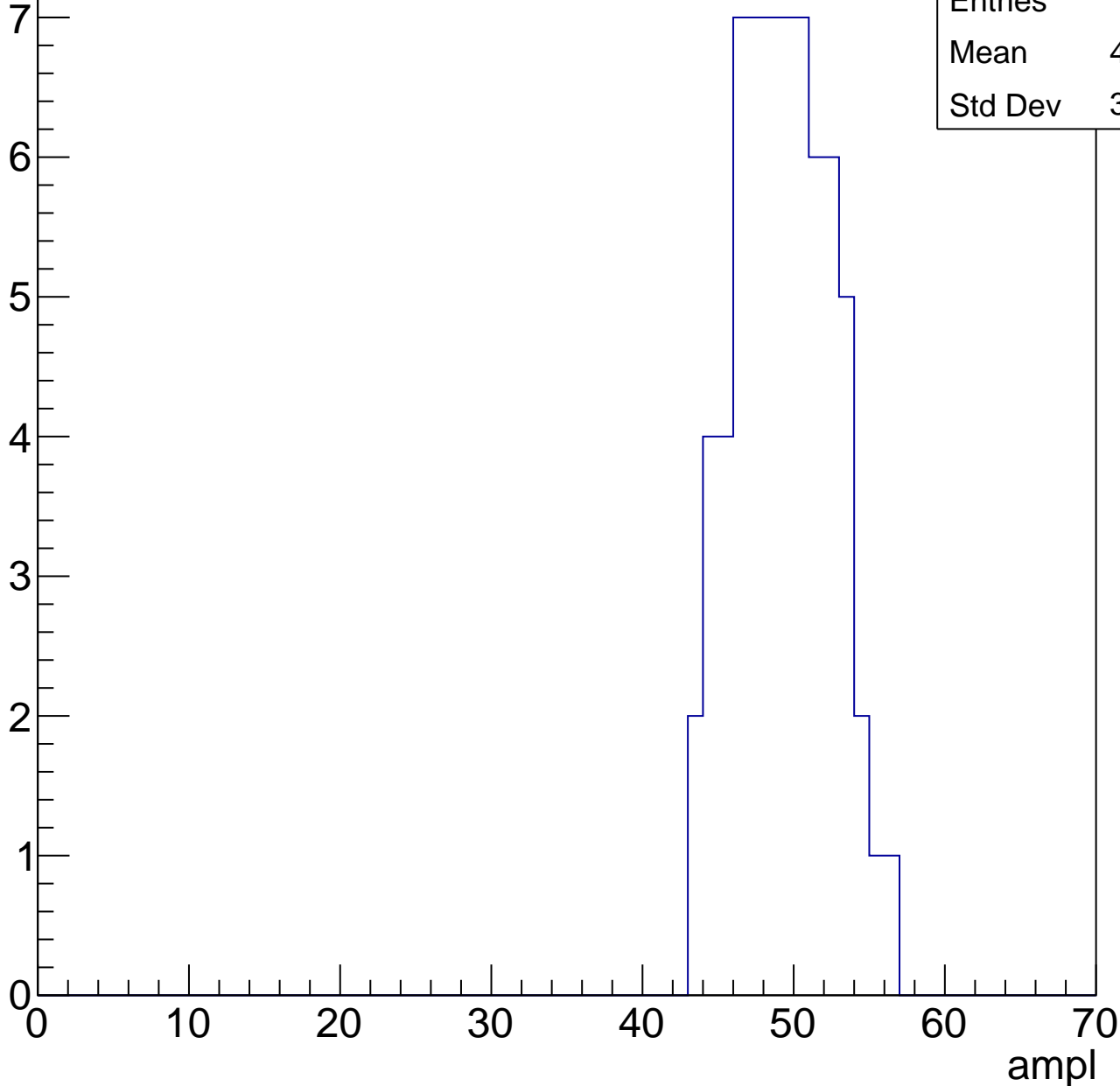


# B0L002S, U23-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

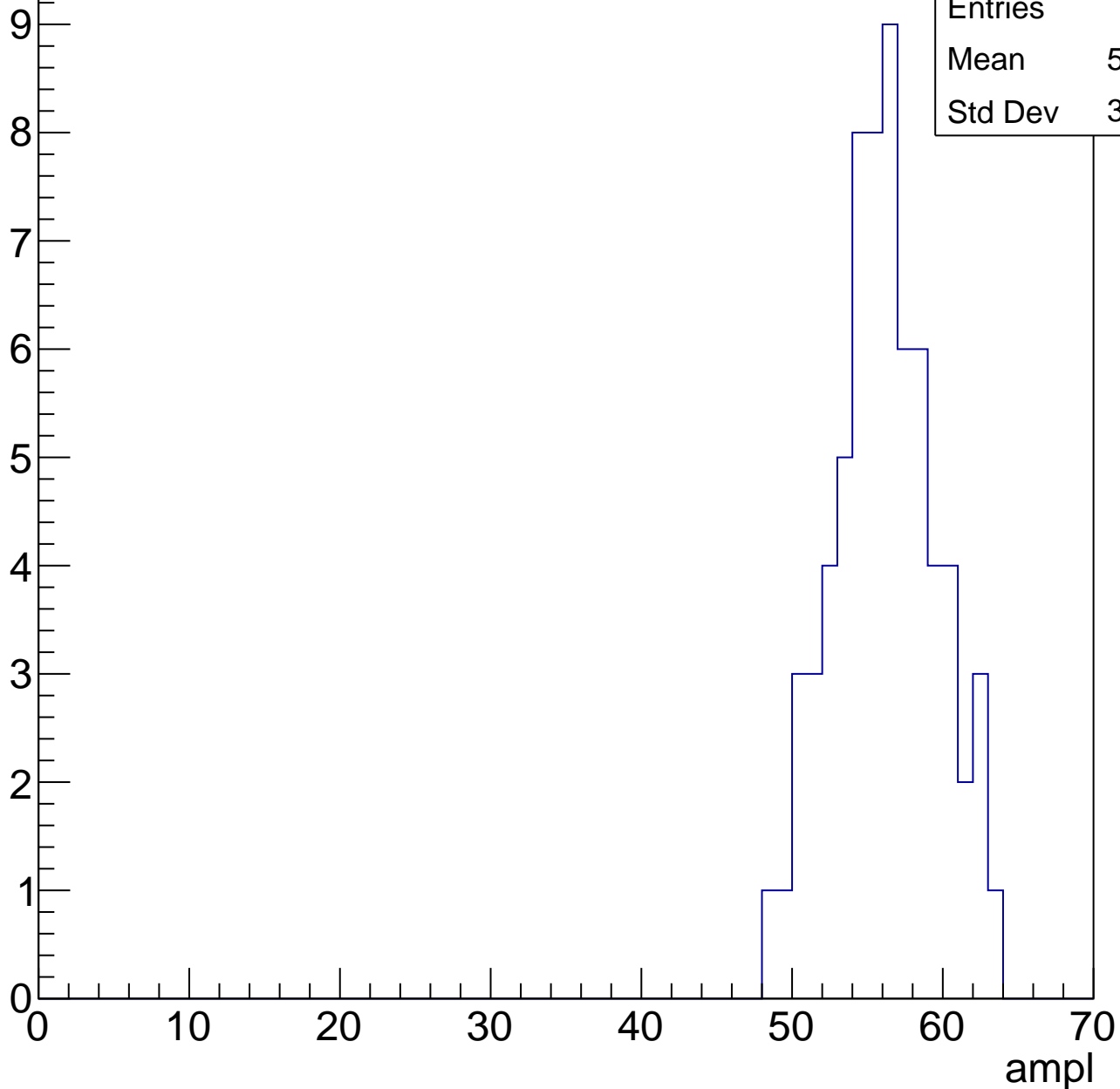
Entries	66
Mean	48.85
Std Dev	3.096



# B0L002S, U23-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

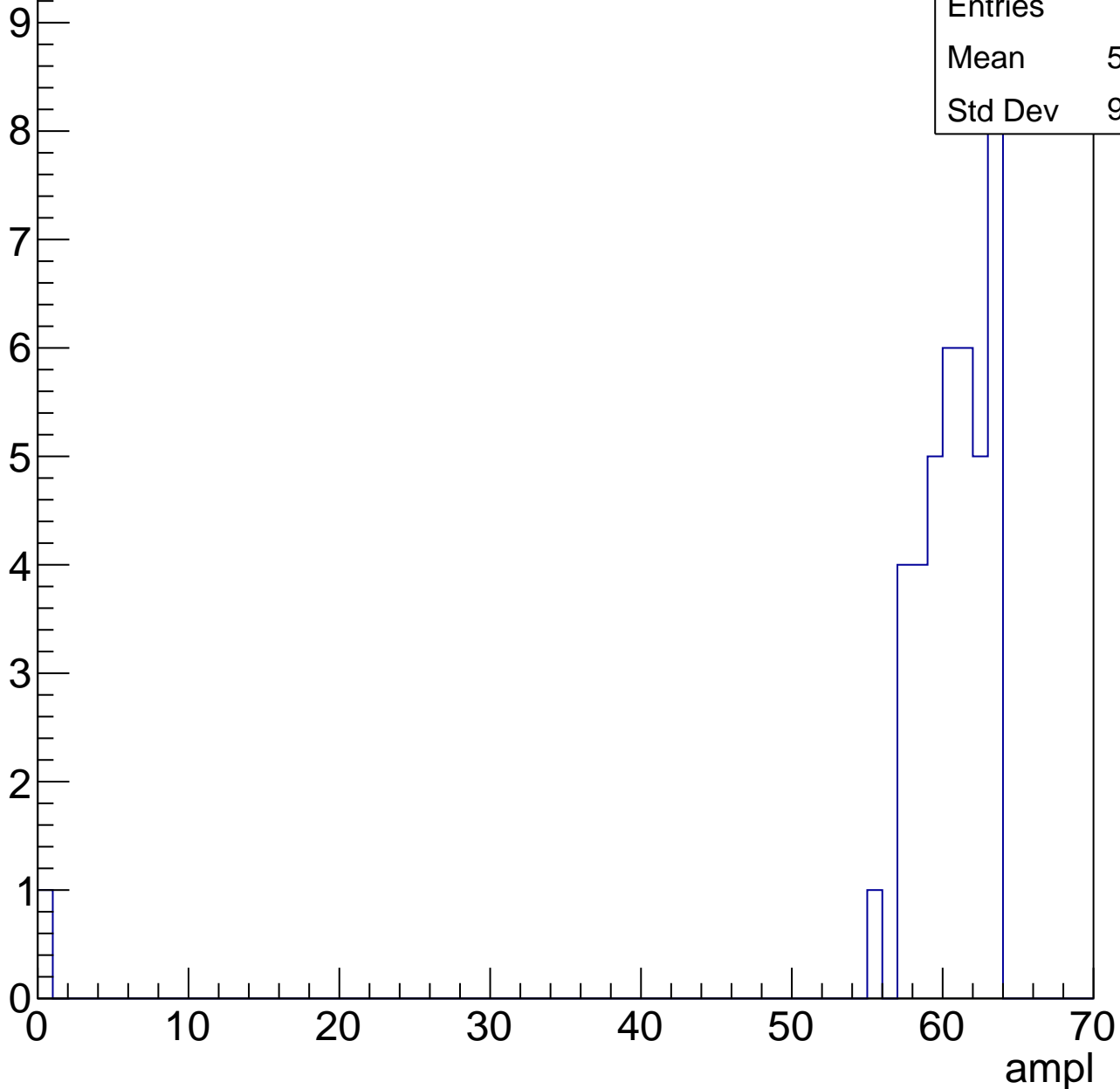


# B0L002S, U23-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

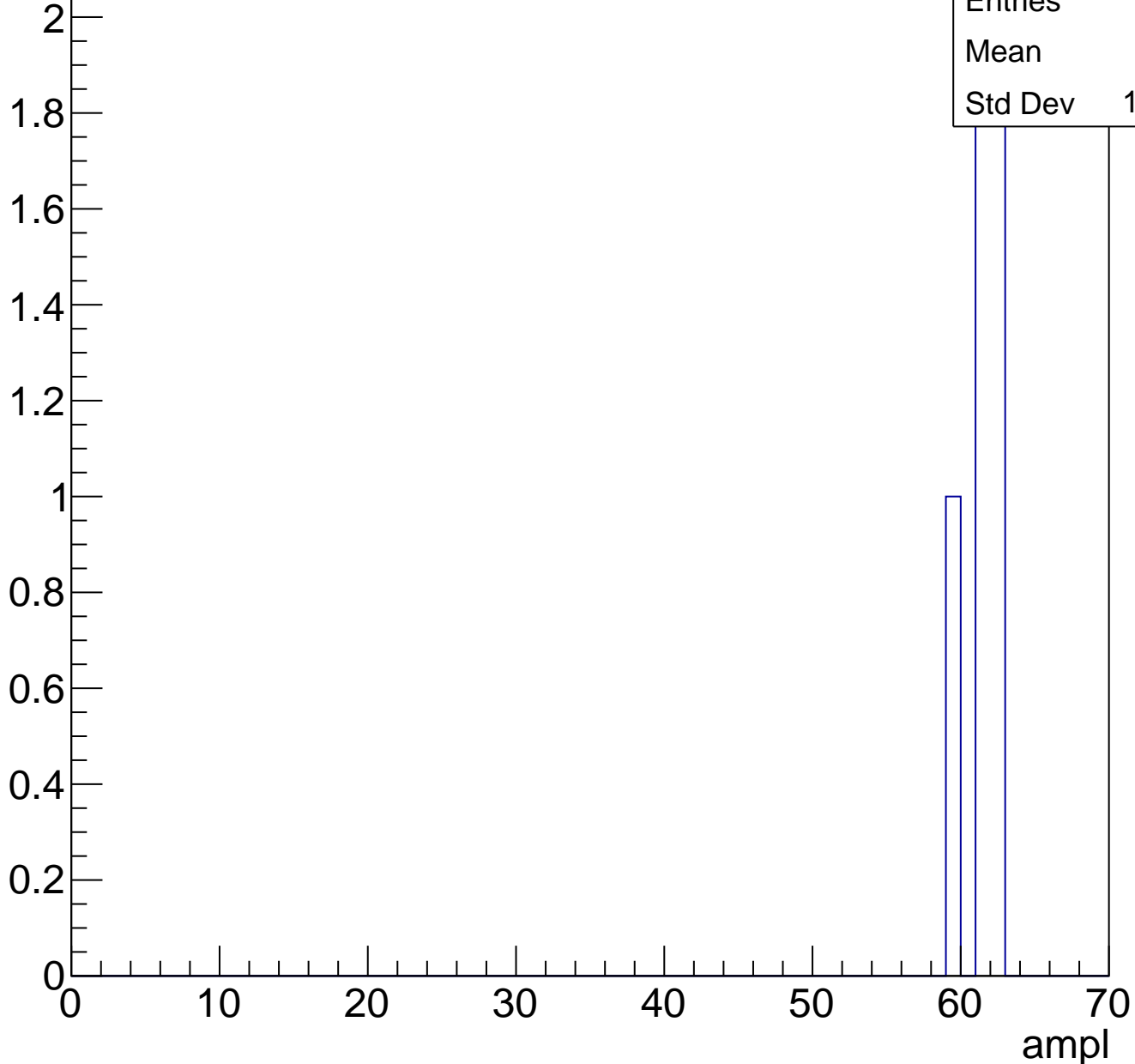
Entries	41
Mean	58.85
Std Dev	9.545



# B0L002S, U23-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch70, adc0

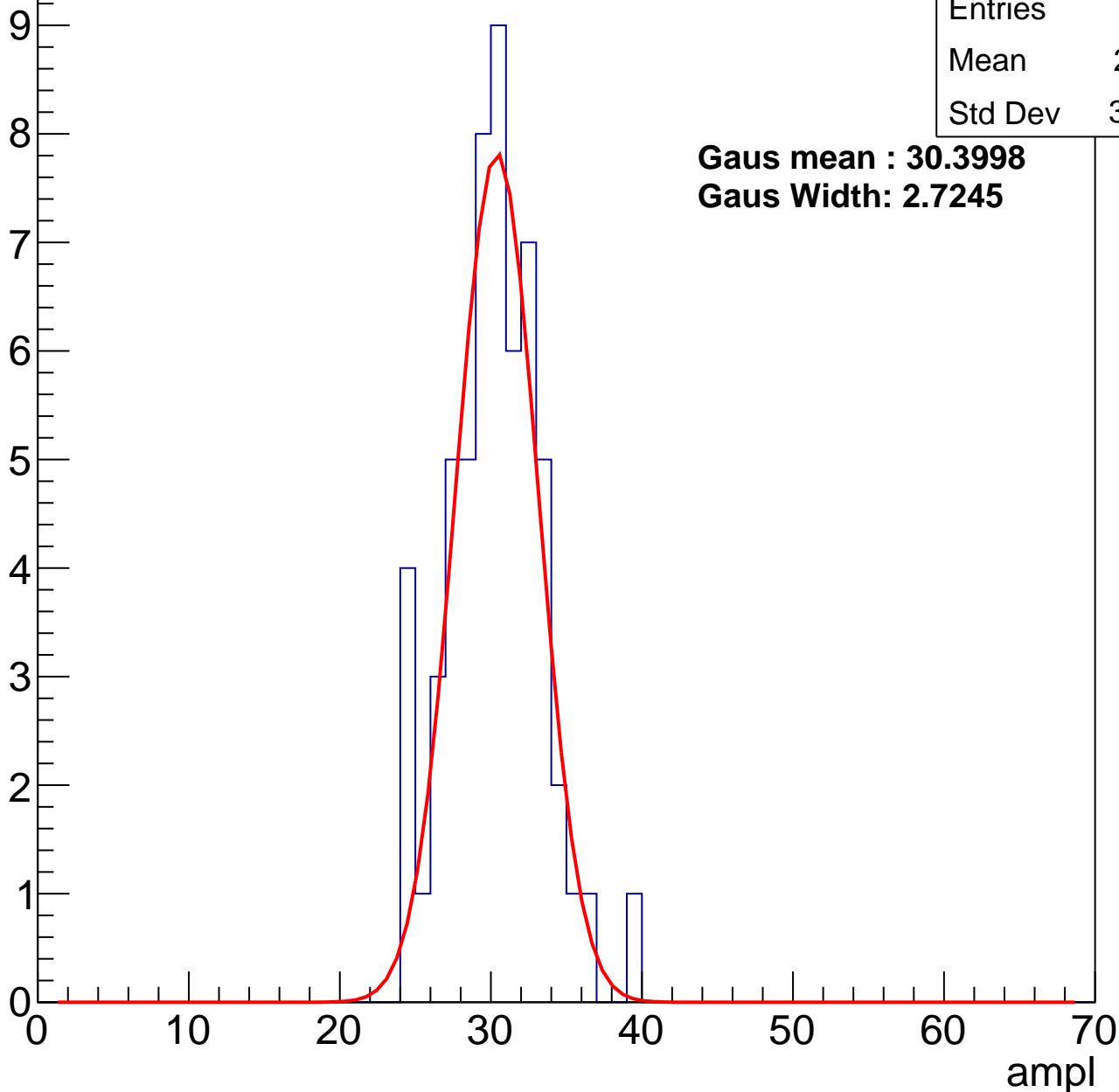
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.81
Std Dev	3.054

**Gaus mean : 30.3998**

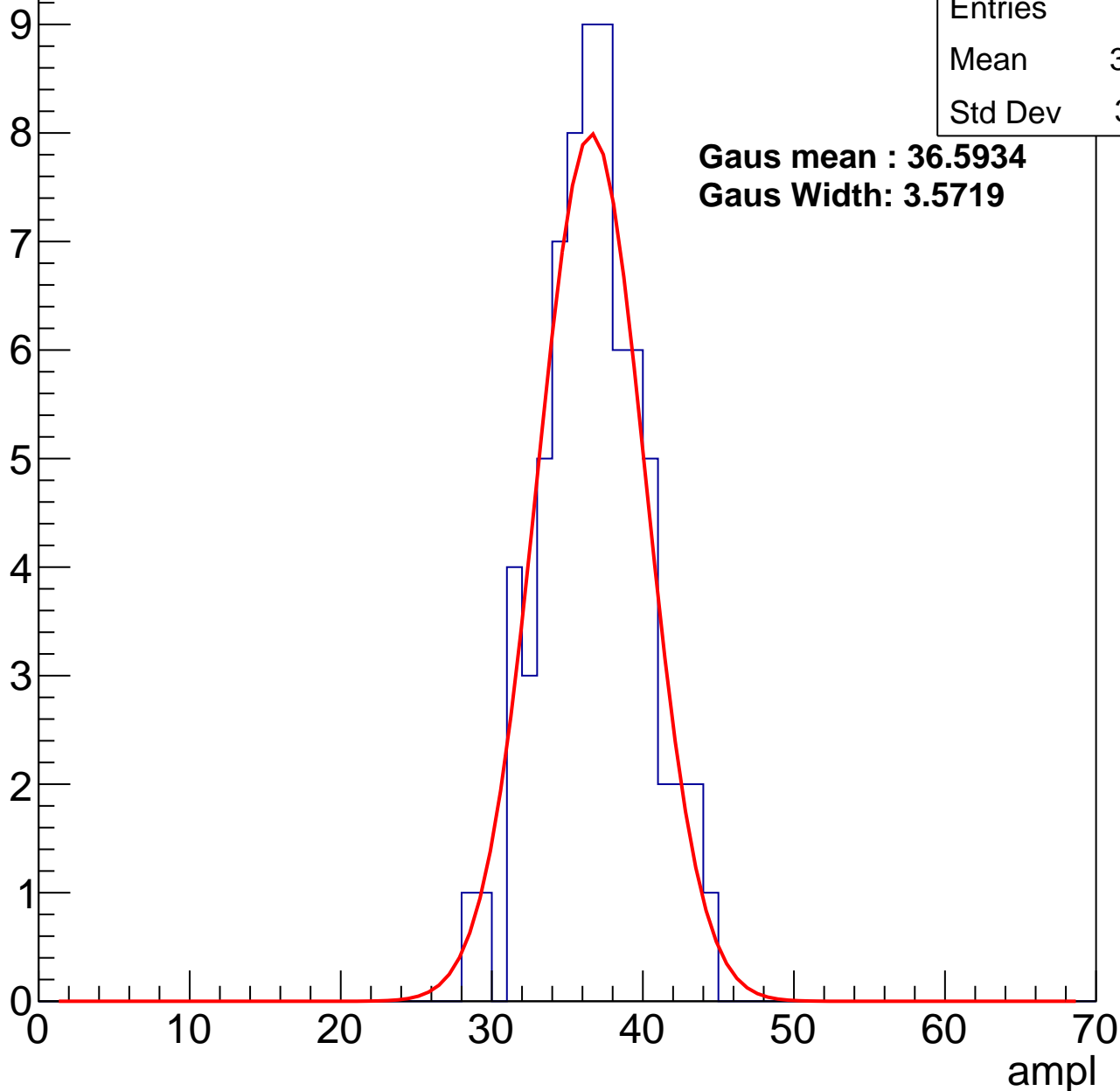
**Gaus Width: 2.7245**



# B0L002S, U23-ch70, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch70, adc2

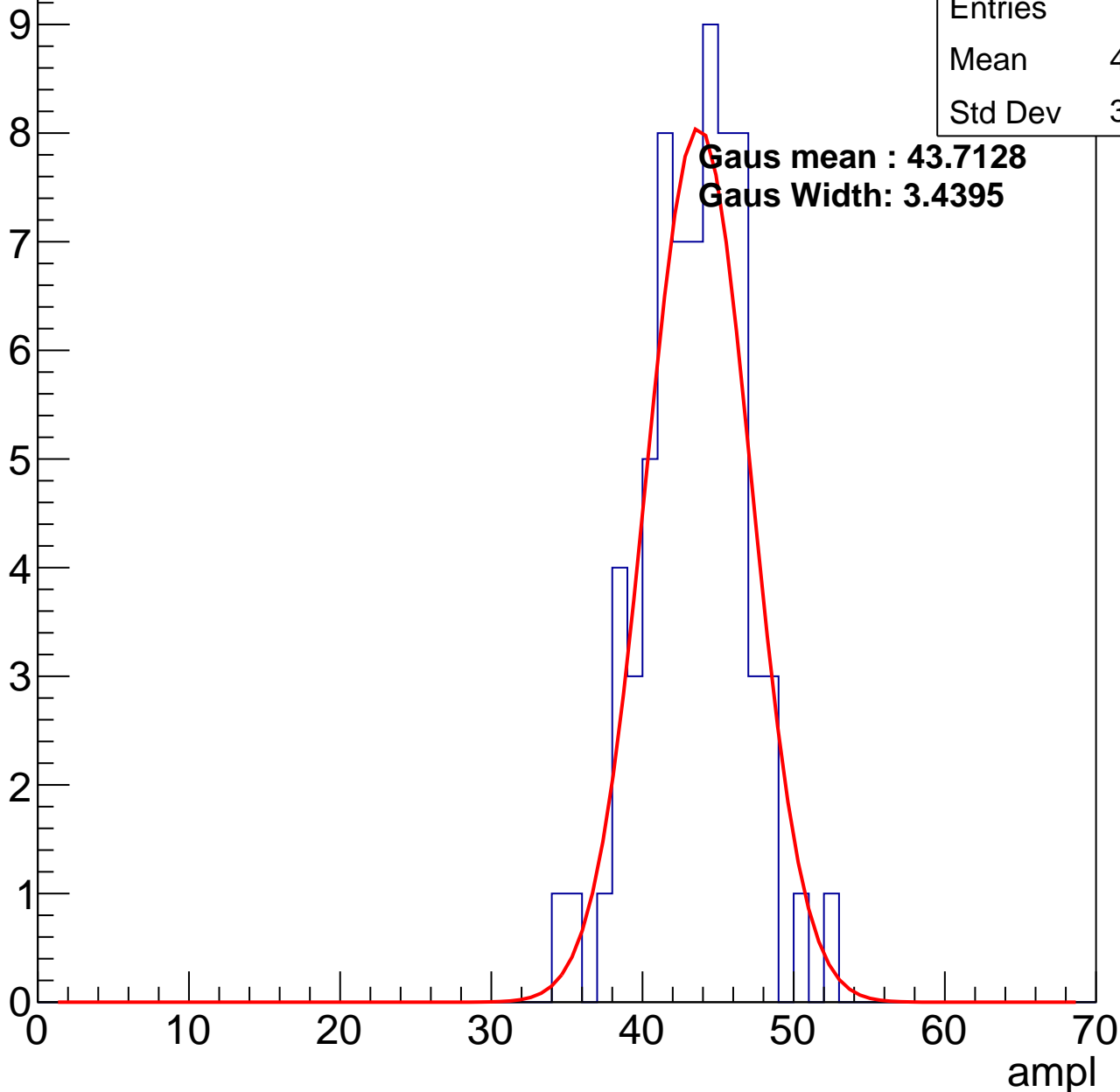
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.99
Std Dev	3.336

**Gaus mean : 43.7128**

**Gaus Width: 3.4395**

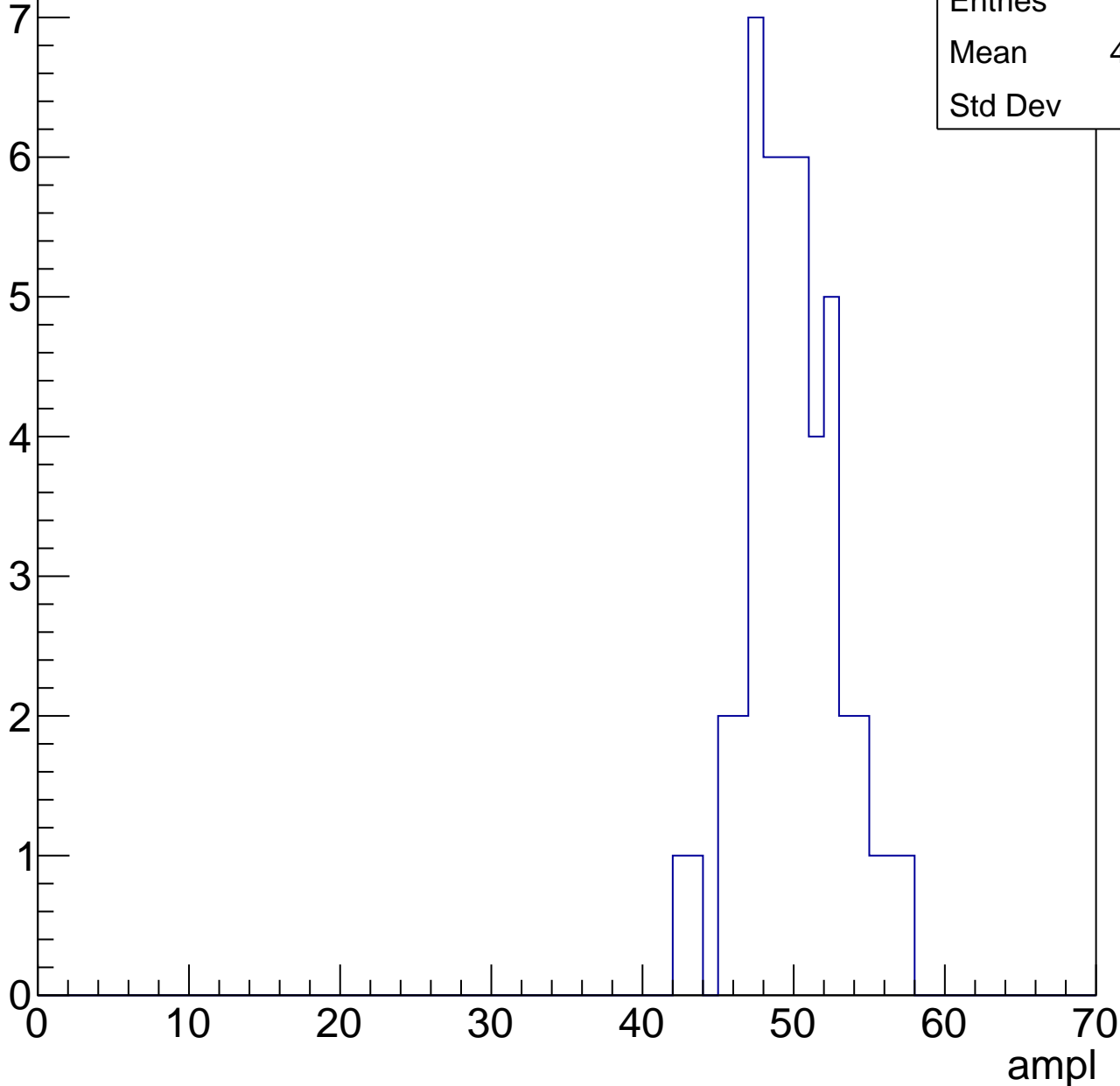


# B0L002S, U23-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	49.45
Std Dev	3.12

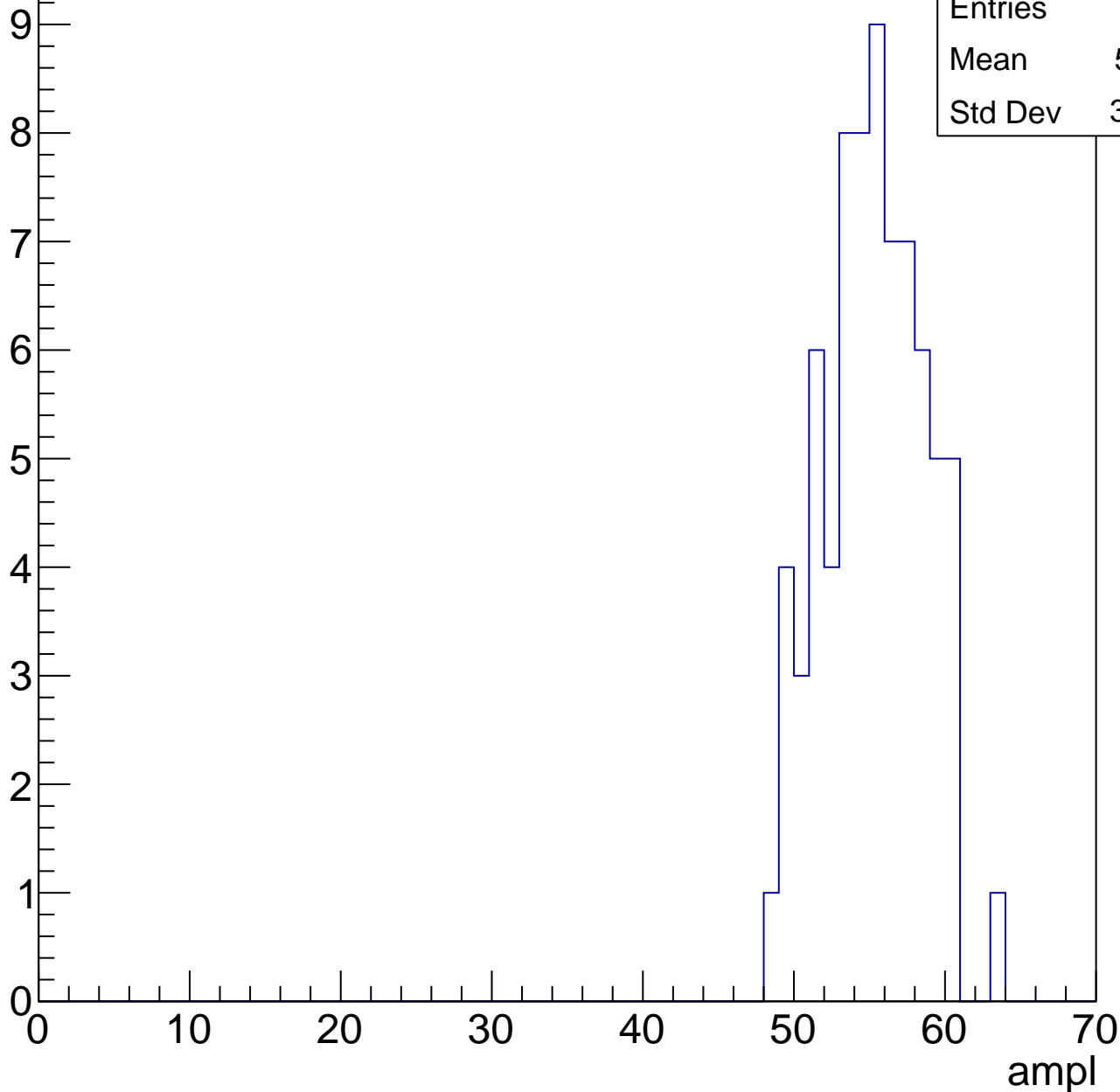


# B0L002S, U23-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

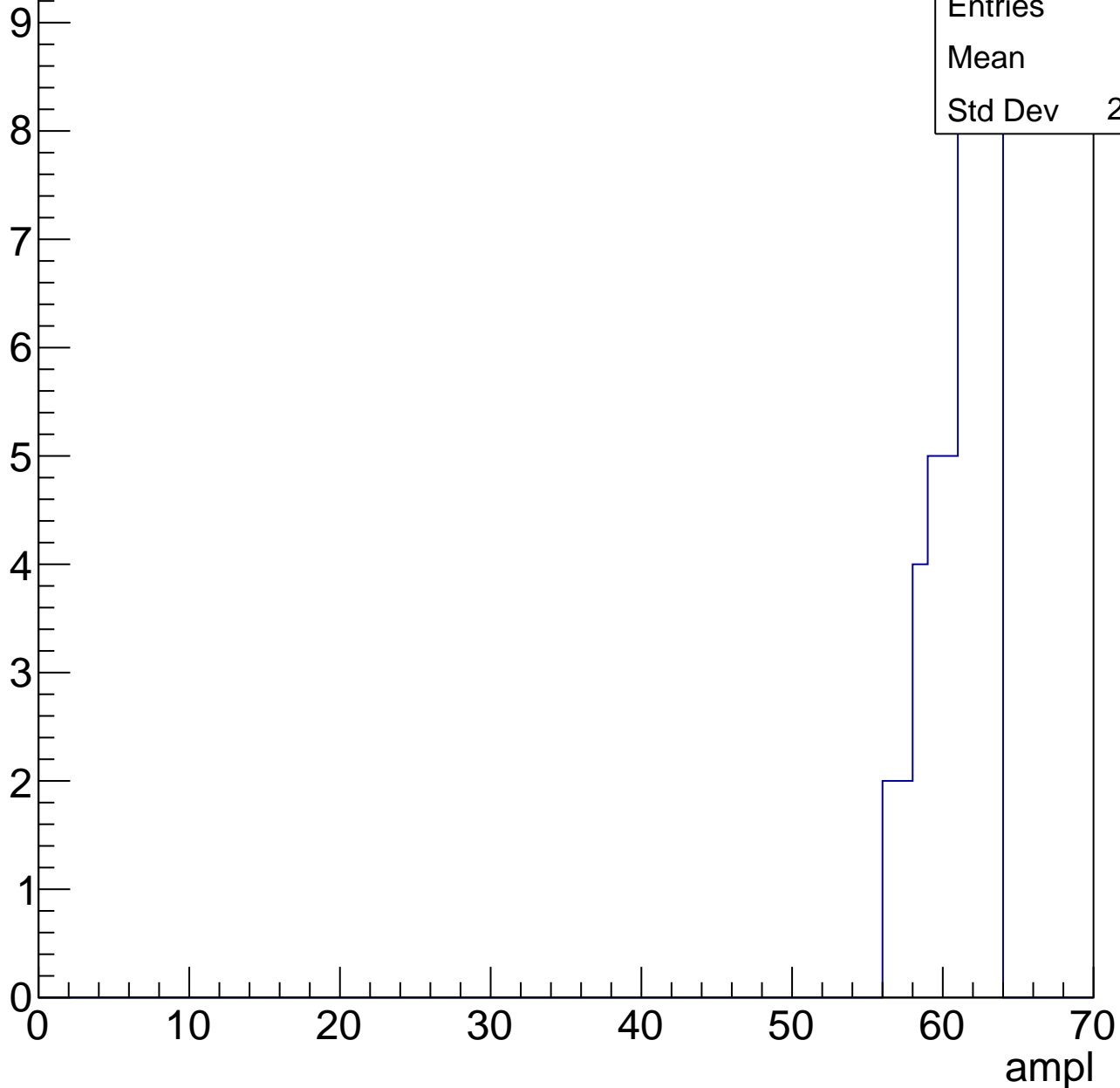
Entries	74
Mean	54.81
Std Dev	3.278



# B0L002S, U23-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch71, adc0

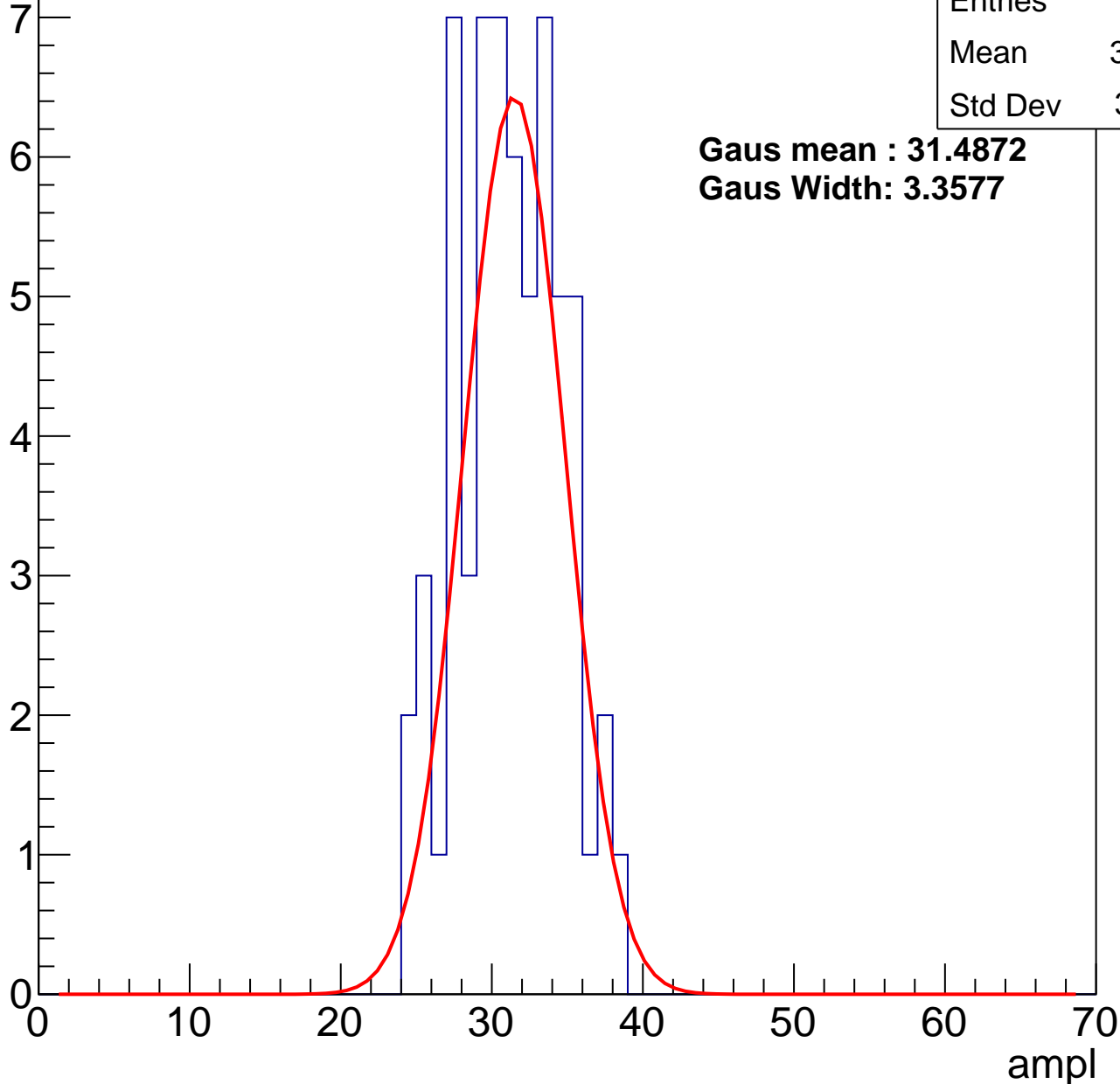
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.73
Std Dev	3.371

**Gaus mean : 31.4872**

**Gaus Width: 3.3577**



# B0L002S, U23-ch71, adc1

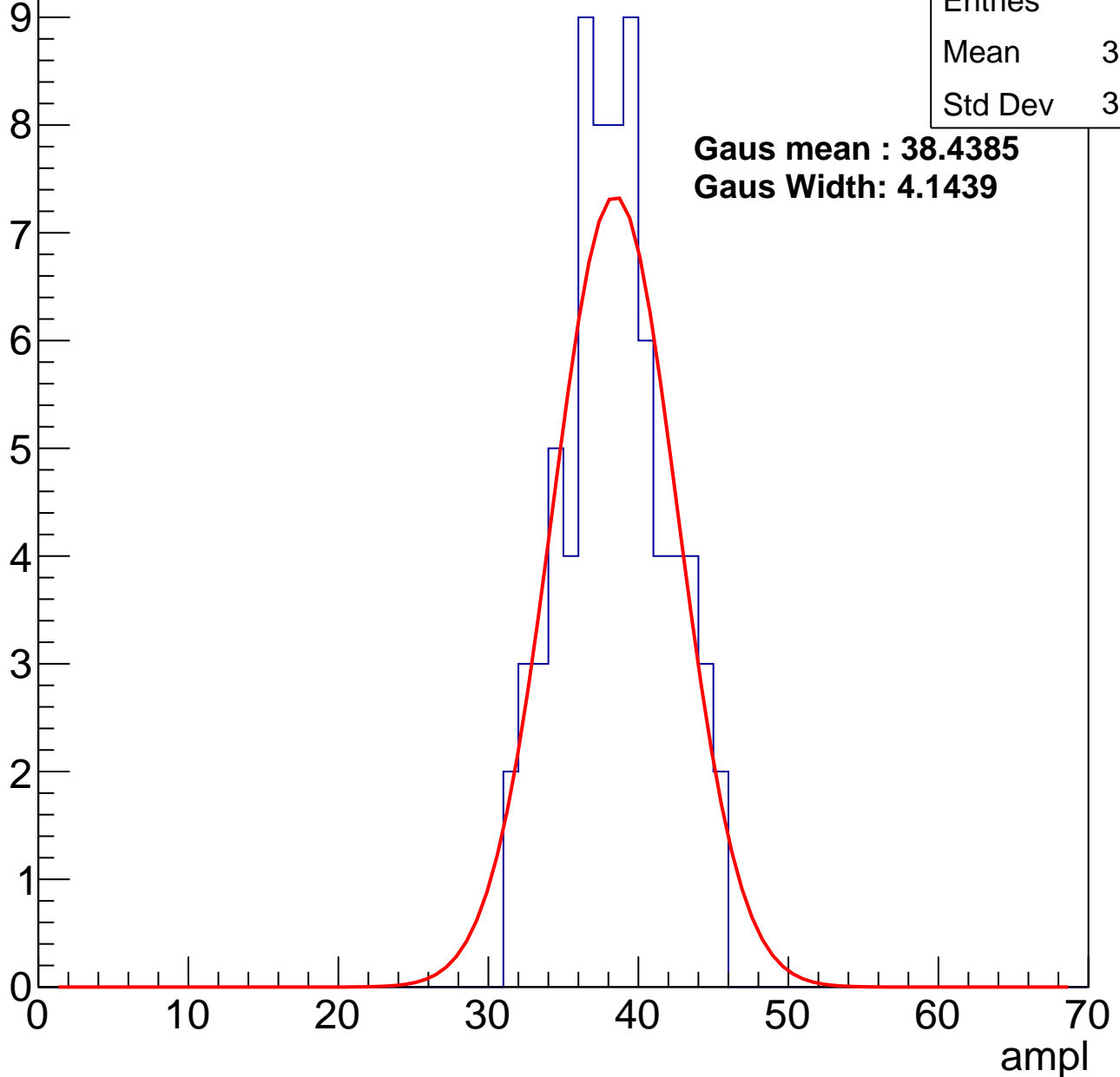
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.95
Std Dev	3.448

**Gaus mean : 38.4385**

**Gaus Width: 4.1439**



# B0L002S, U23-ch71, adc2

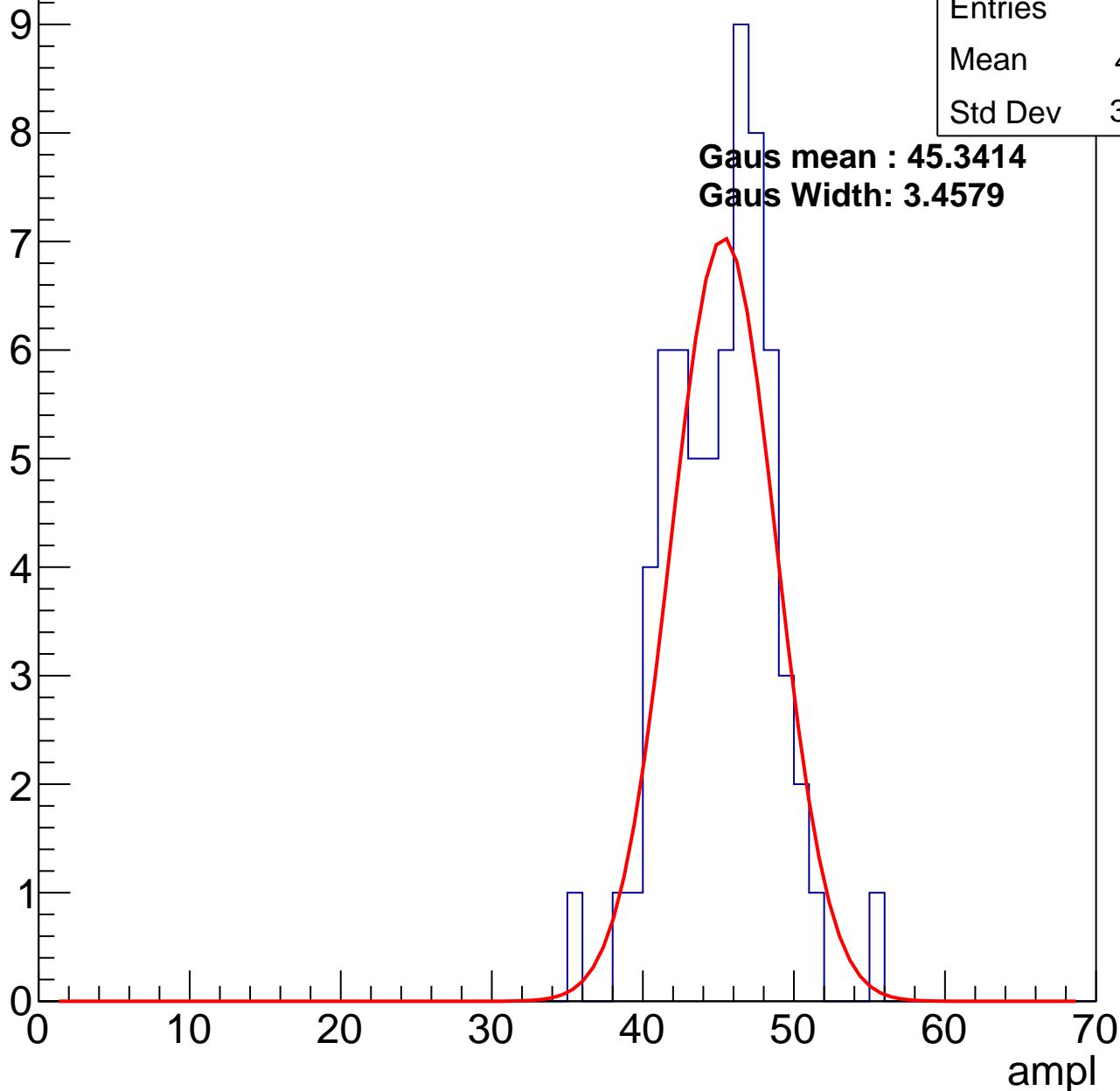
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.71
Std Dev	3.472

**Gaus mean : 45.3414**

**Gaus Width: 3.4579**

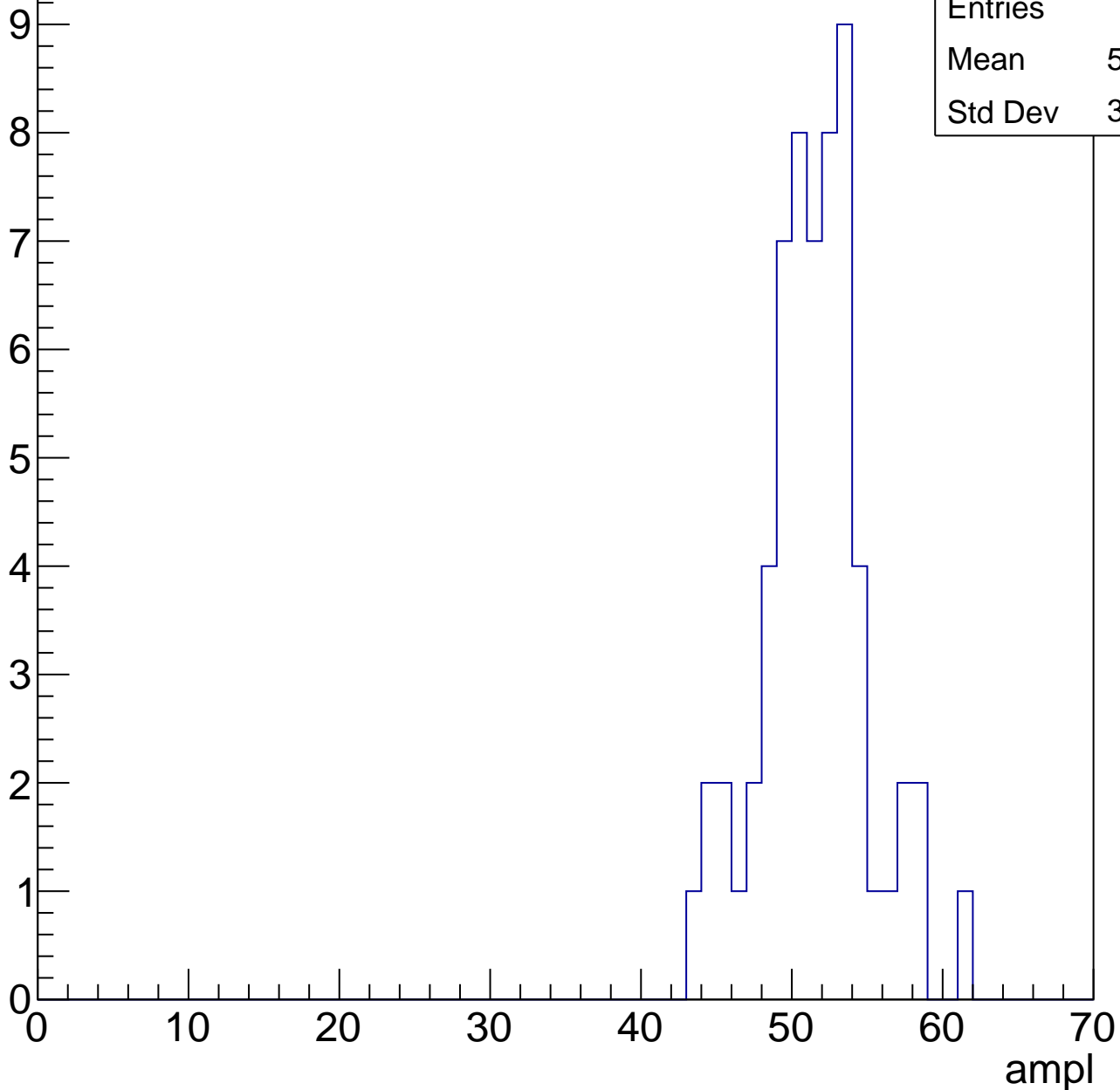


# B0L002S, U23-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.03
Std Dev	3.487

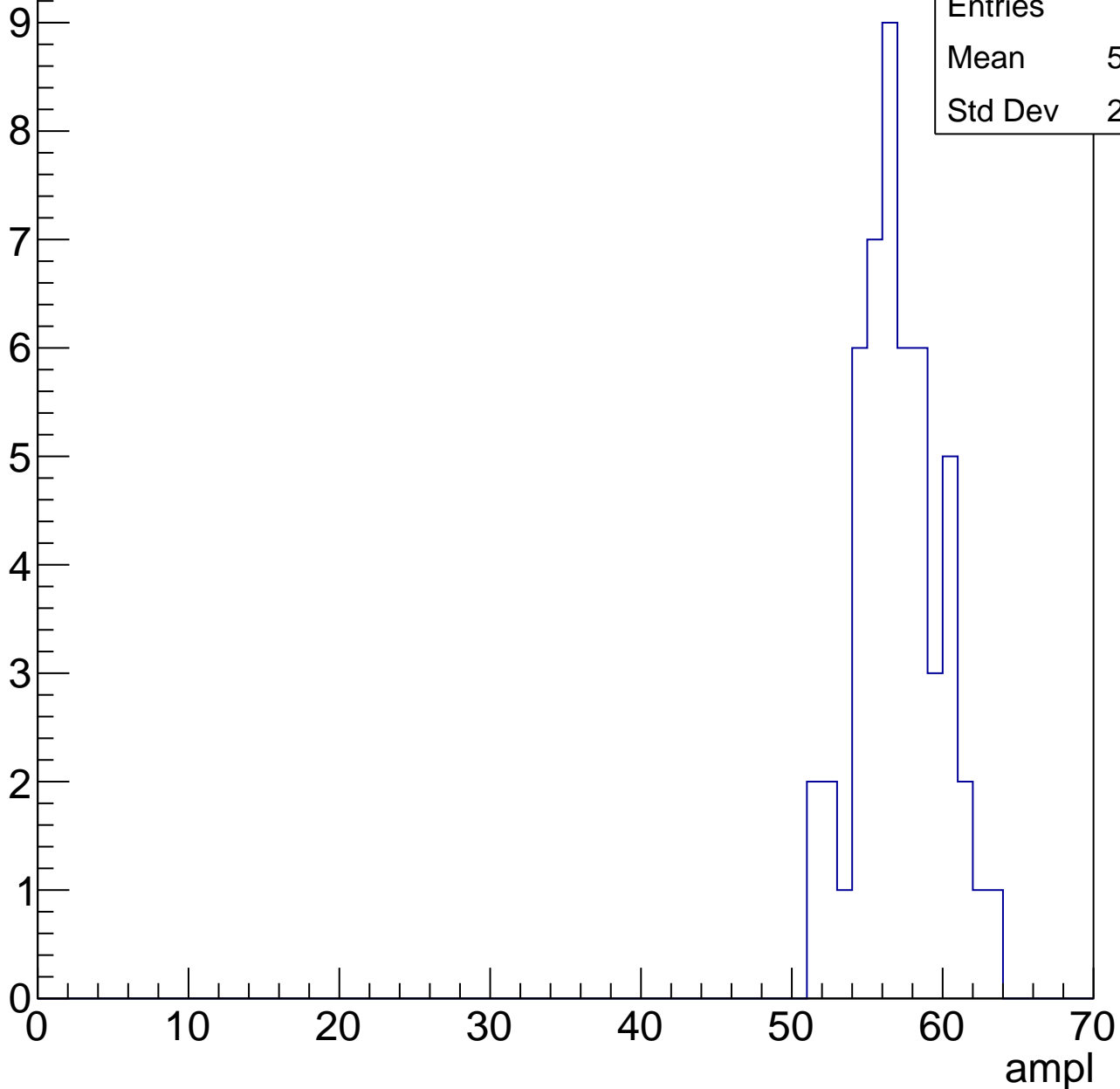


# B0L002S, U23-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.59
Std Dev	2.717



# B0L002S, U23-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	59.15
Std Dev	9.55

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

# B0L002S, U23-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch72, adc0

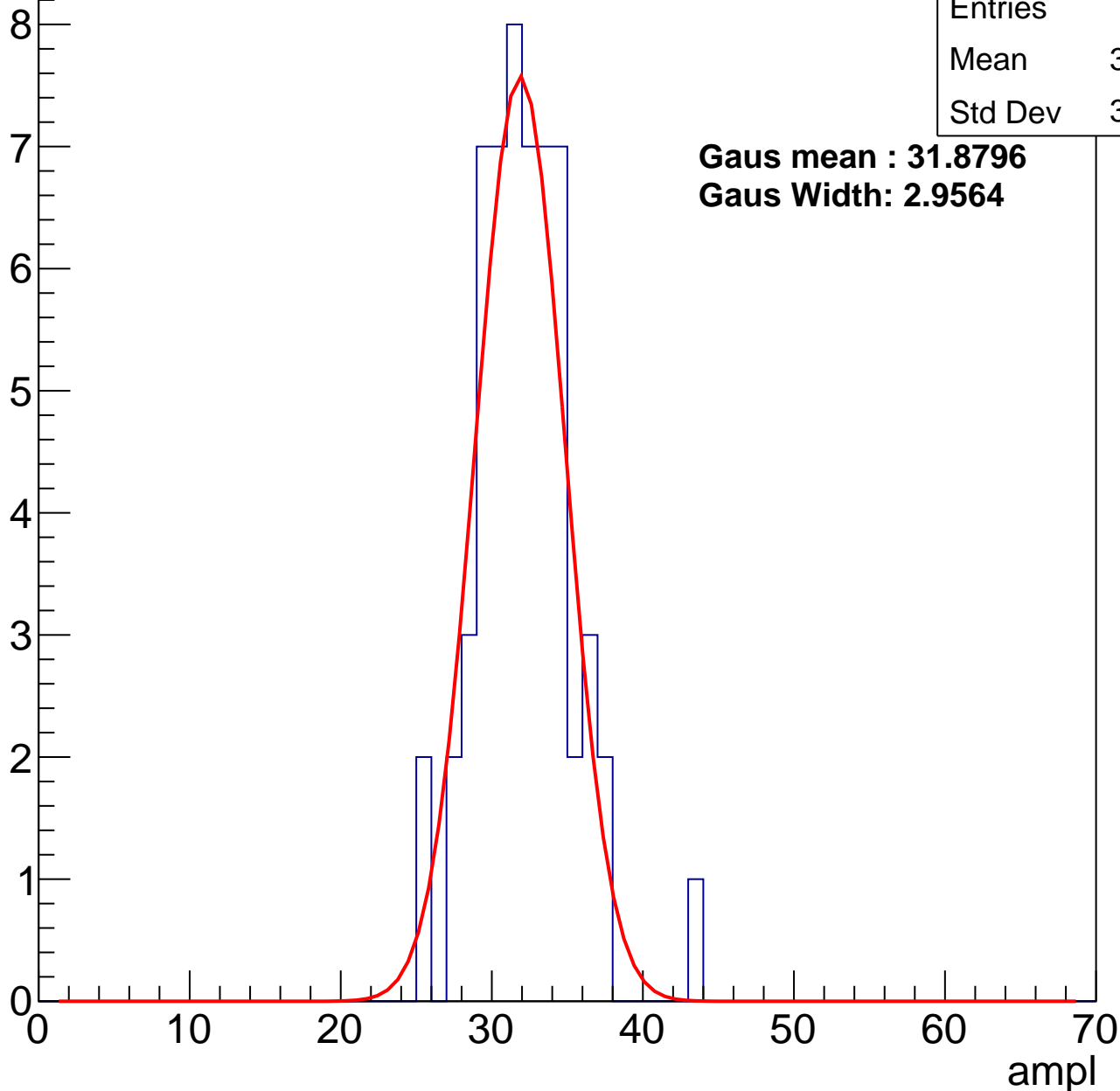
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.67
Std Dev	3.109

**Gaus mean : 31.8796**

**Gaus Width: 2.9564**



# B0L002S, U23-ch72, adc1

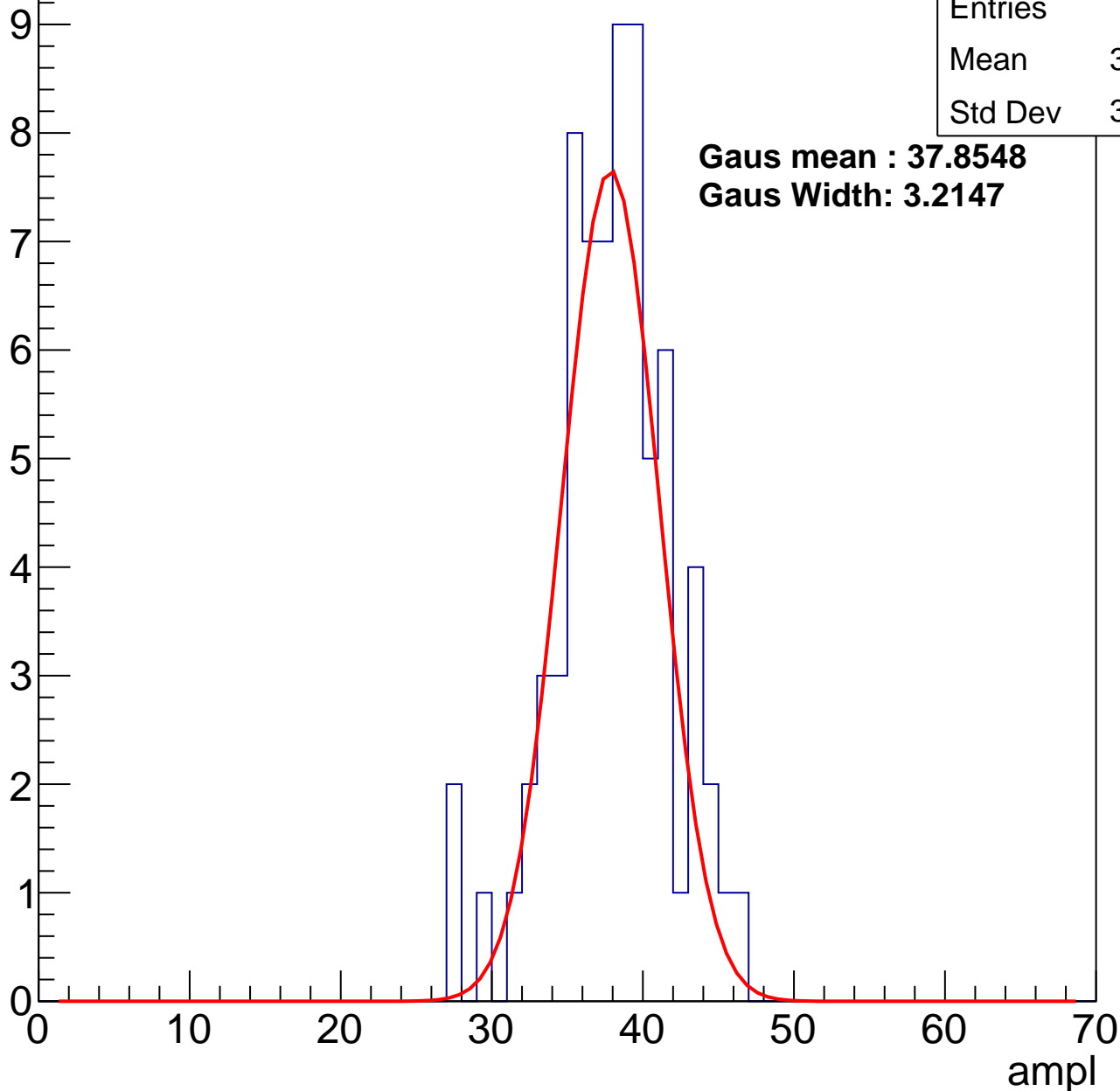
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	37.53
Std Dev	3.804

**Gaus mean : 37.8548**

**Gaus Width: 3.2147**



# B0L002S, U23-ch72, adc2

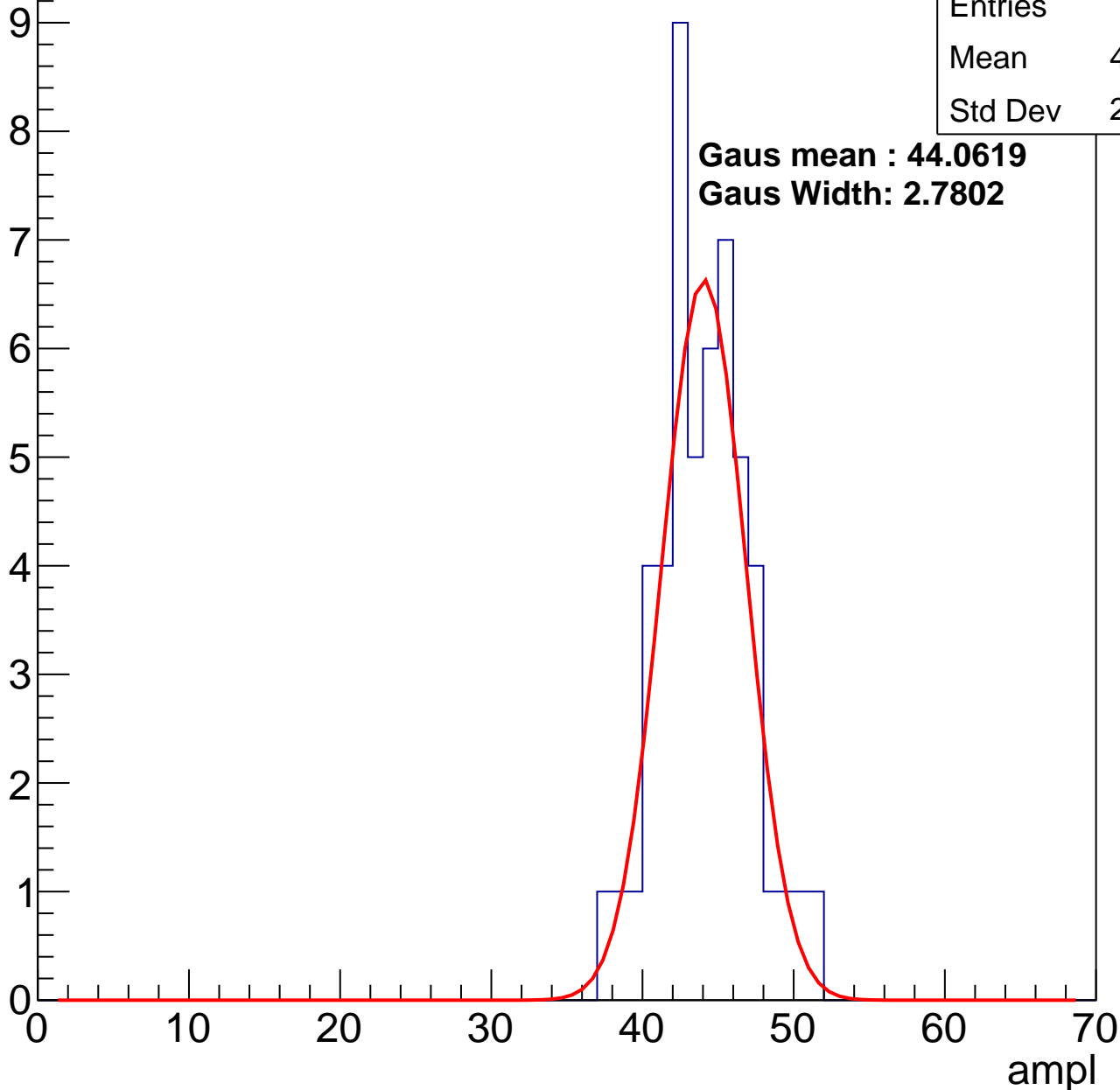
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	43.65
Std Dev	2.916

**Gaus mean : 44.0619**

**Gaus Width: 2.7802**

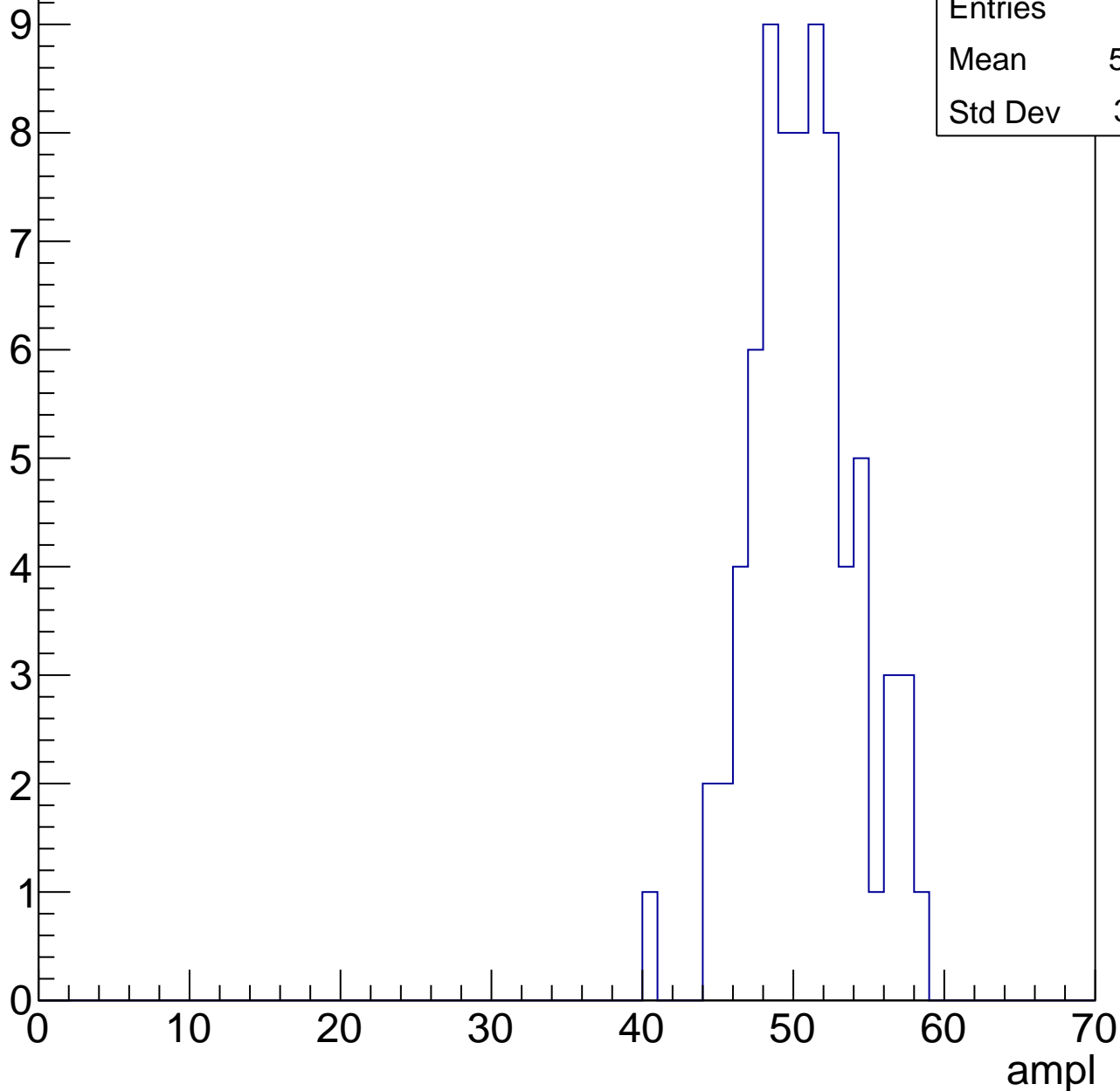


# B0L002S, U23-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	50.23
Std Dev	3.451

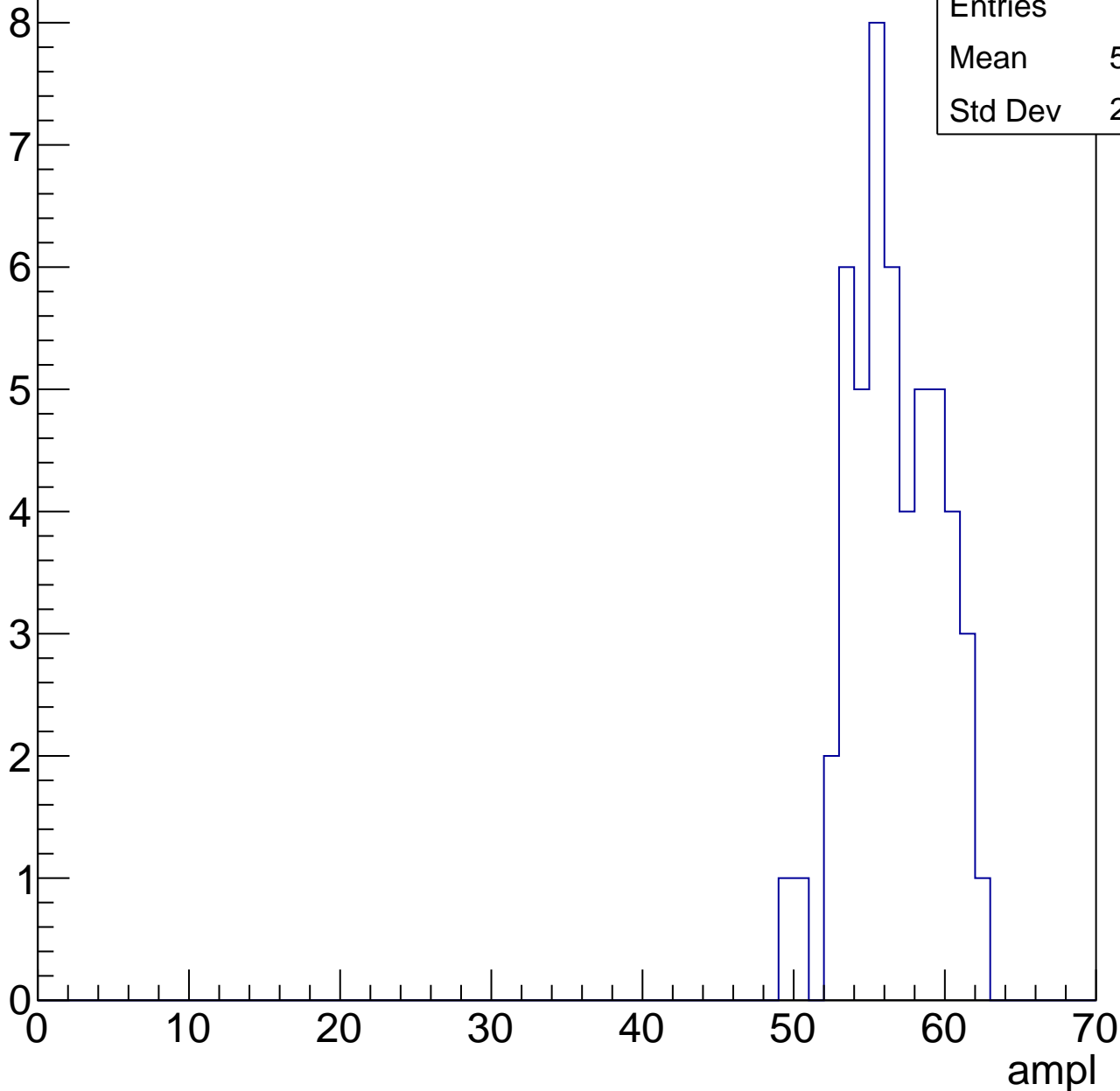


# B0L002S, U23-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.18
Std Dev	2.942

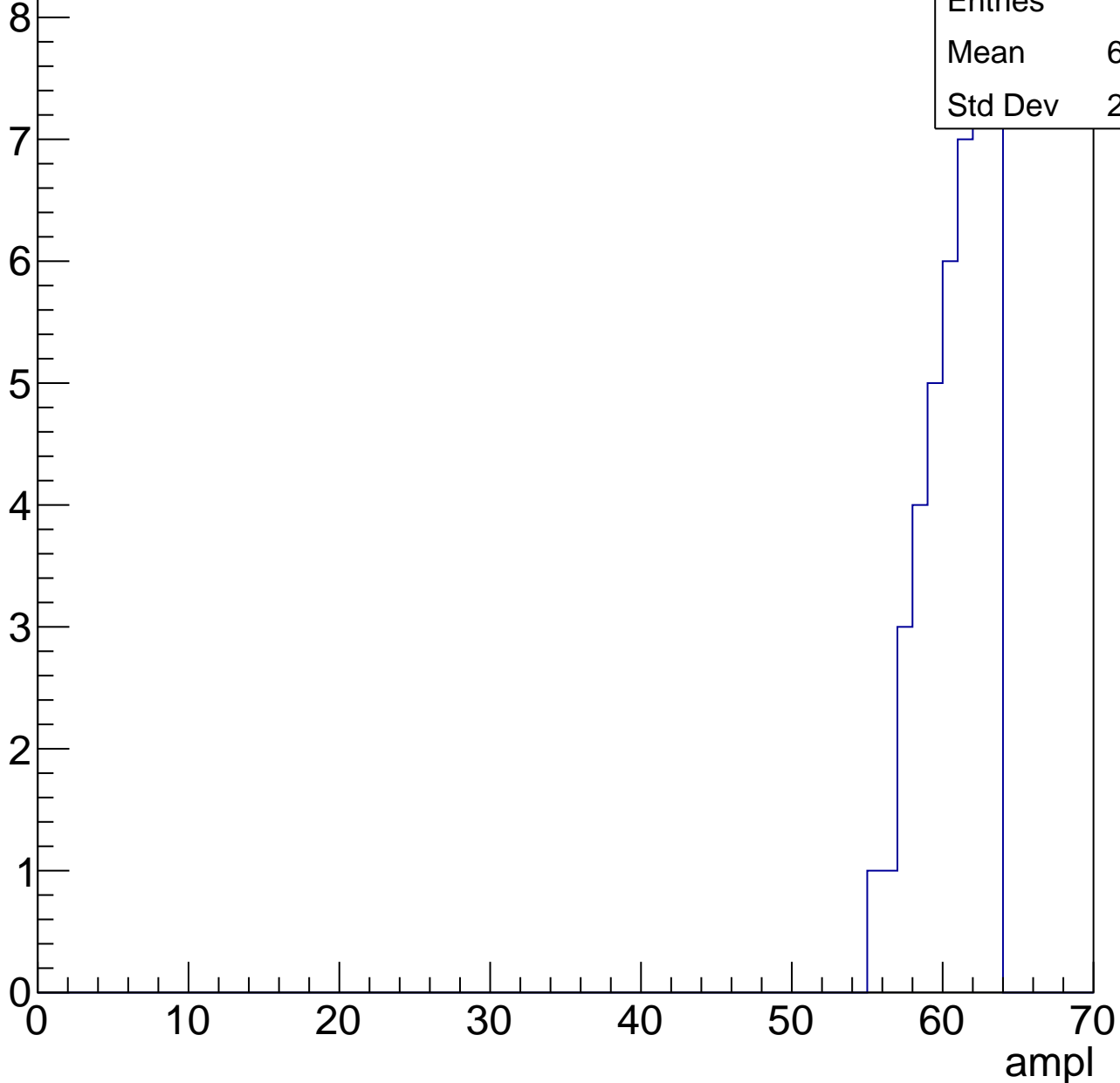


# B0L002S, U23-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	60.37
Std Dev	2.124



# B0L002S, U23-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	47
Std Dev	27.14

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch73, adc0

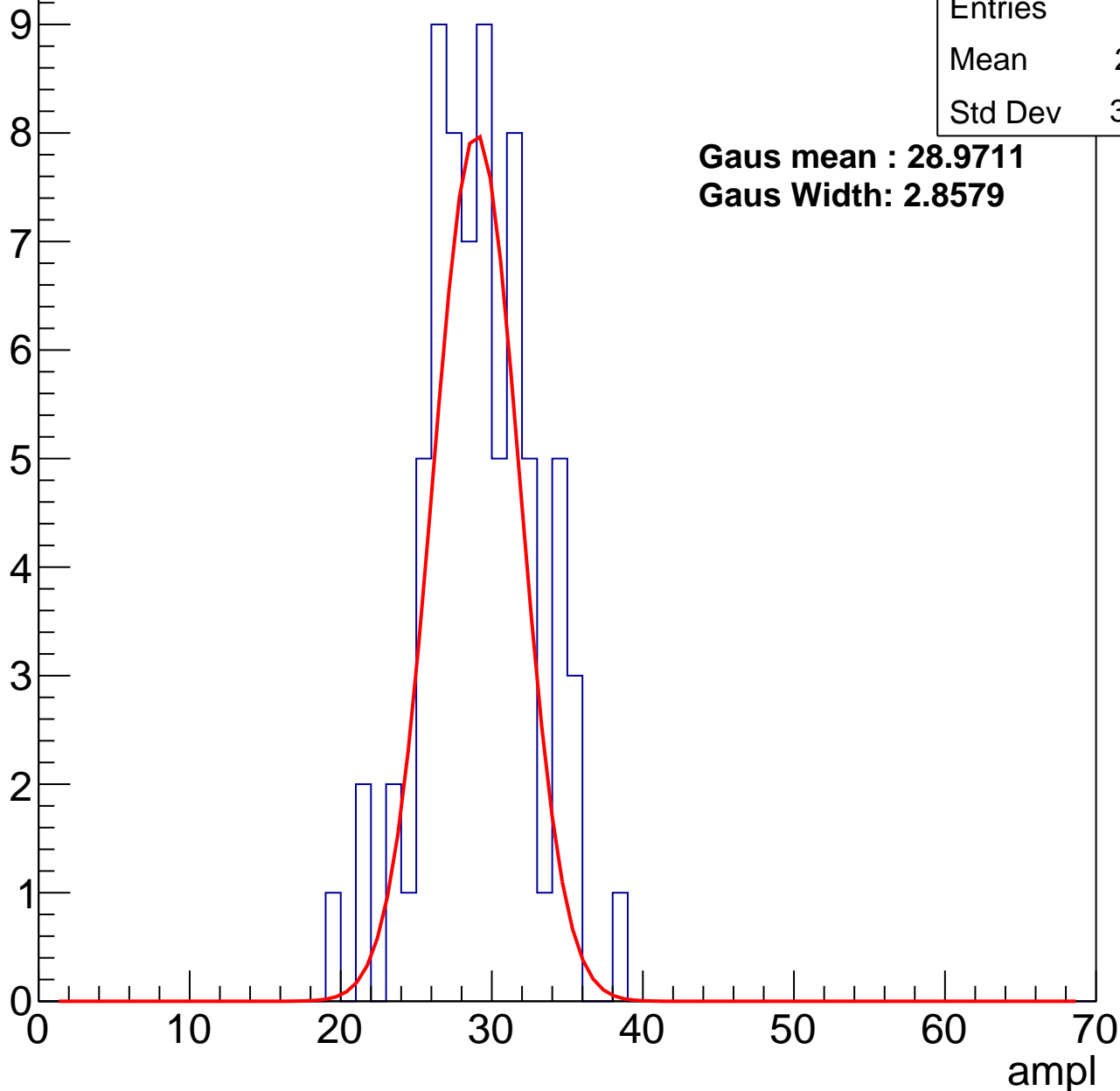
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	28.71
Std Dev	3.596

**Gaus mean : 28.9711**

**Gaus Width: 2.8579**



# B0L002S, U23-ch73, adc1

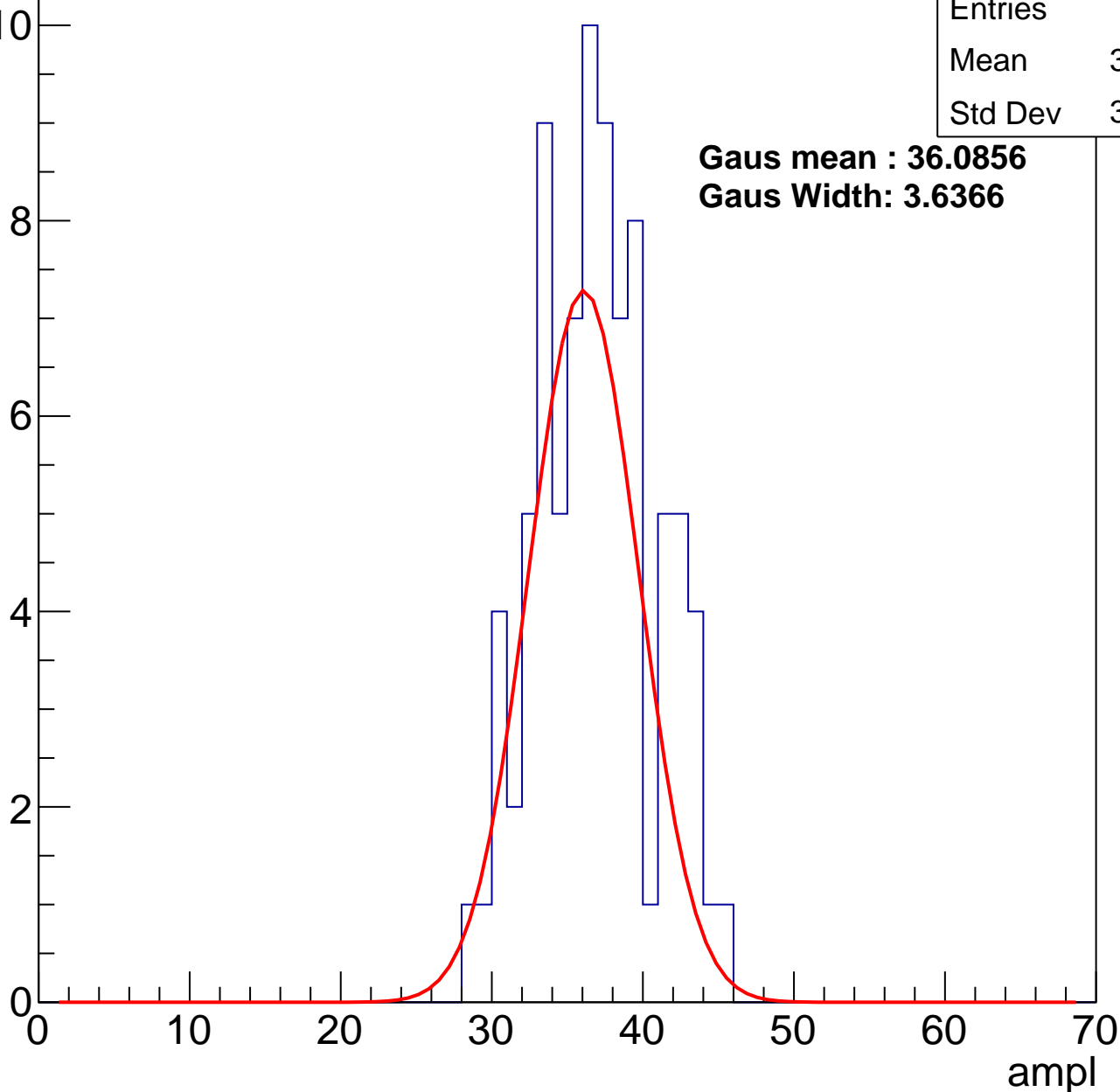
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	85
Mean	36.45
Std Dev	3.833

**Gaus mean : 36.0856**

**Gaus Width: 3.6366**



# B0L002S, U23-ch73, adc2

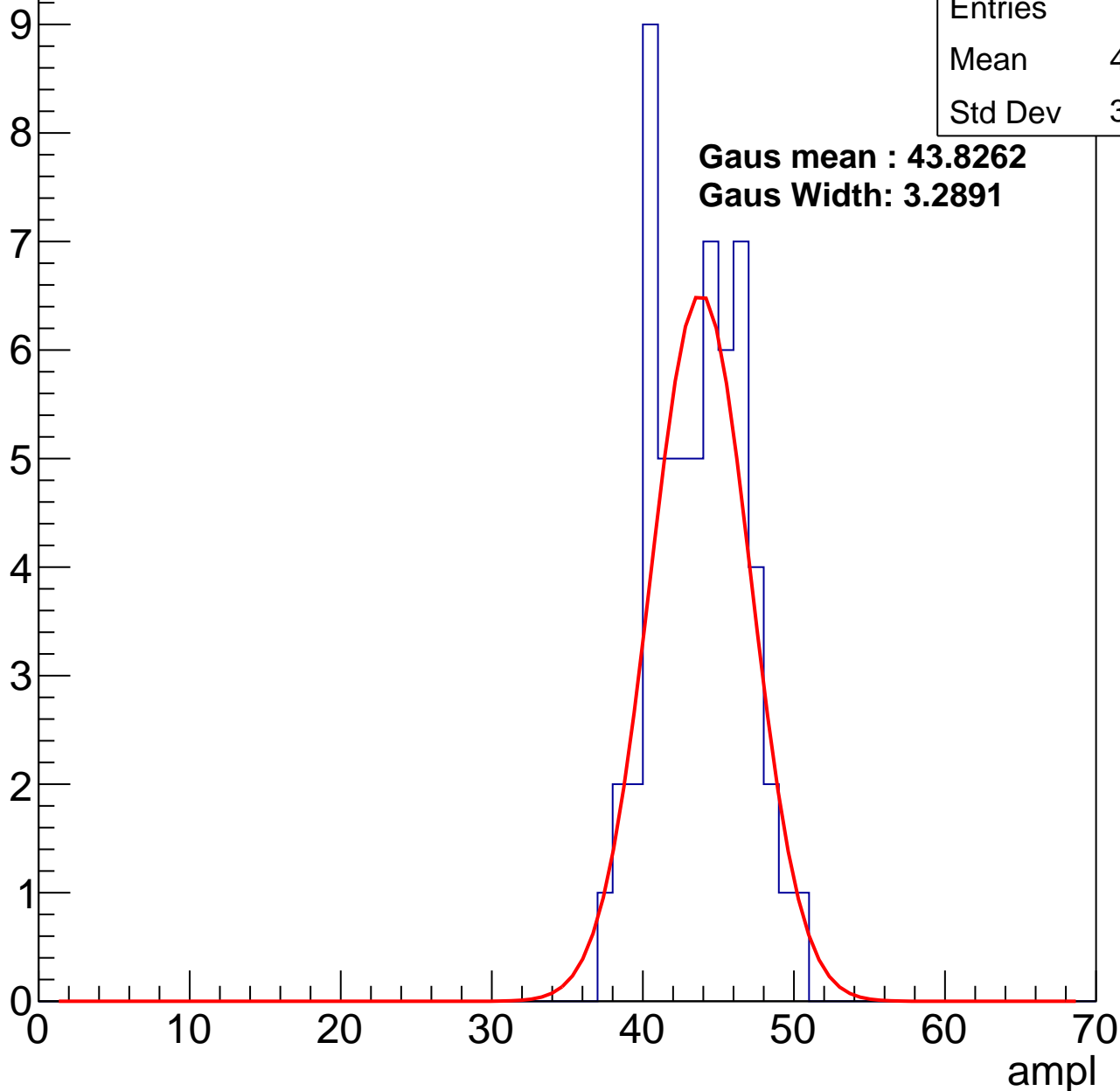
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.23
Std Dev	3.003

**Gaus mean : 43.8262**

**Gaus Width: 3.2891**

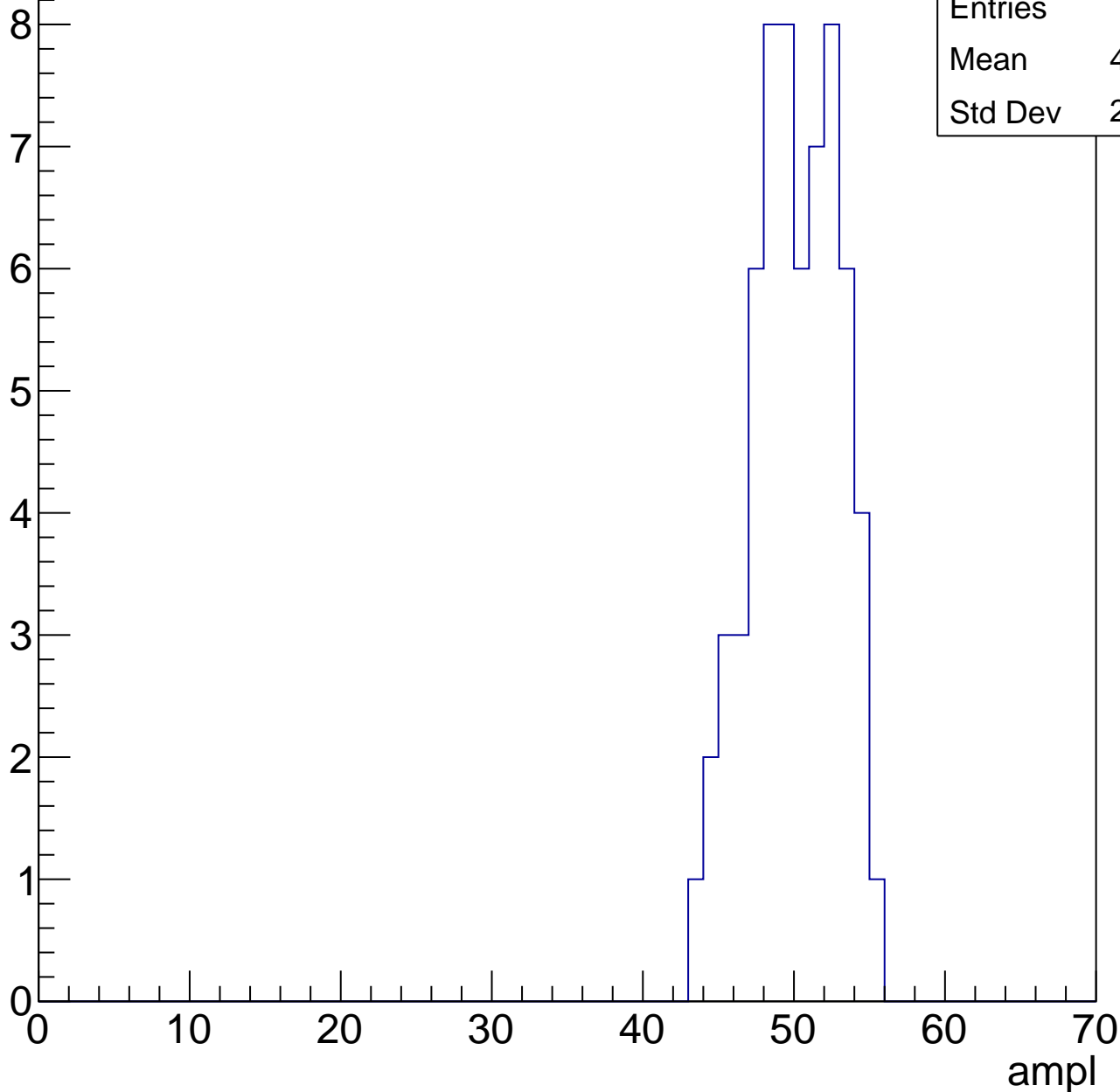


# B0L002S, U23-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	49.59
Std Dev	2.843

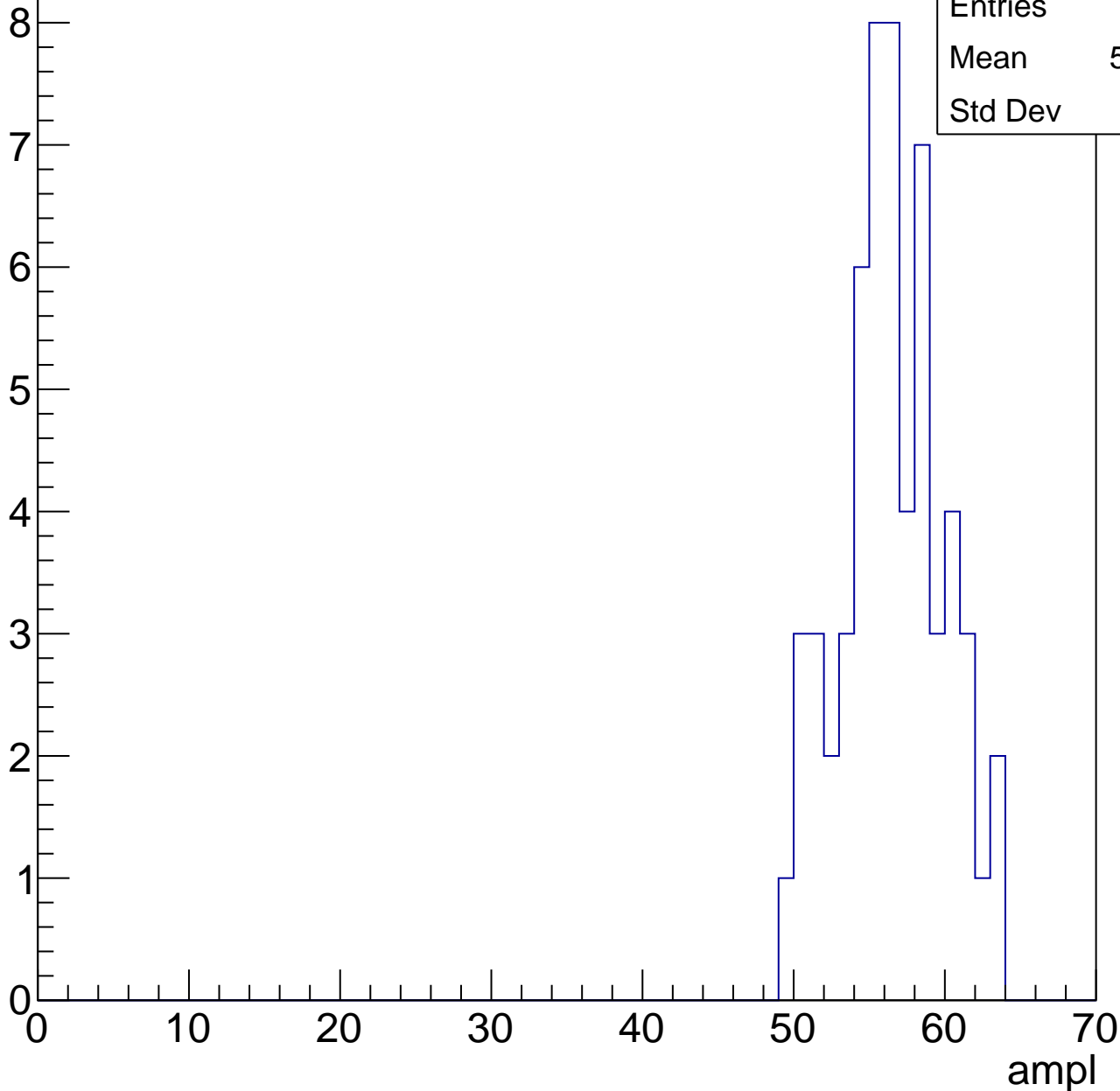


# B0L002S, U23-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.02
Std Dev	3.36

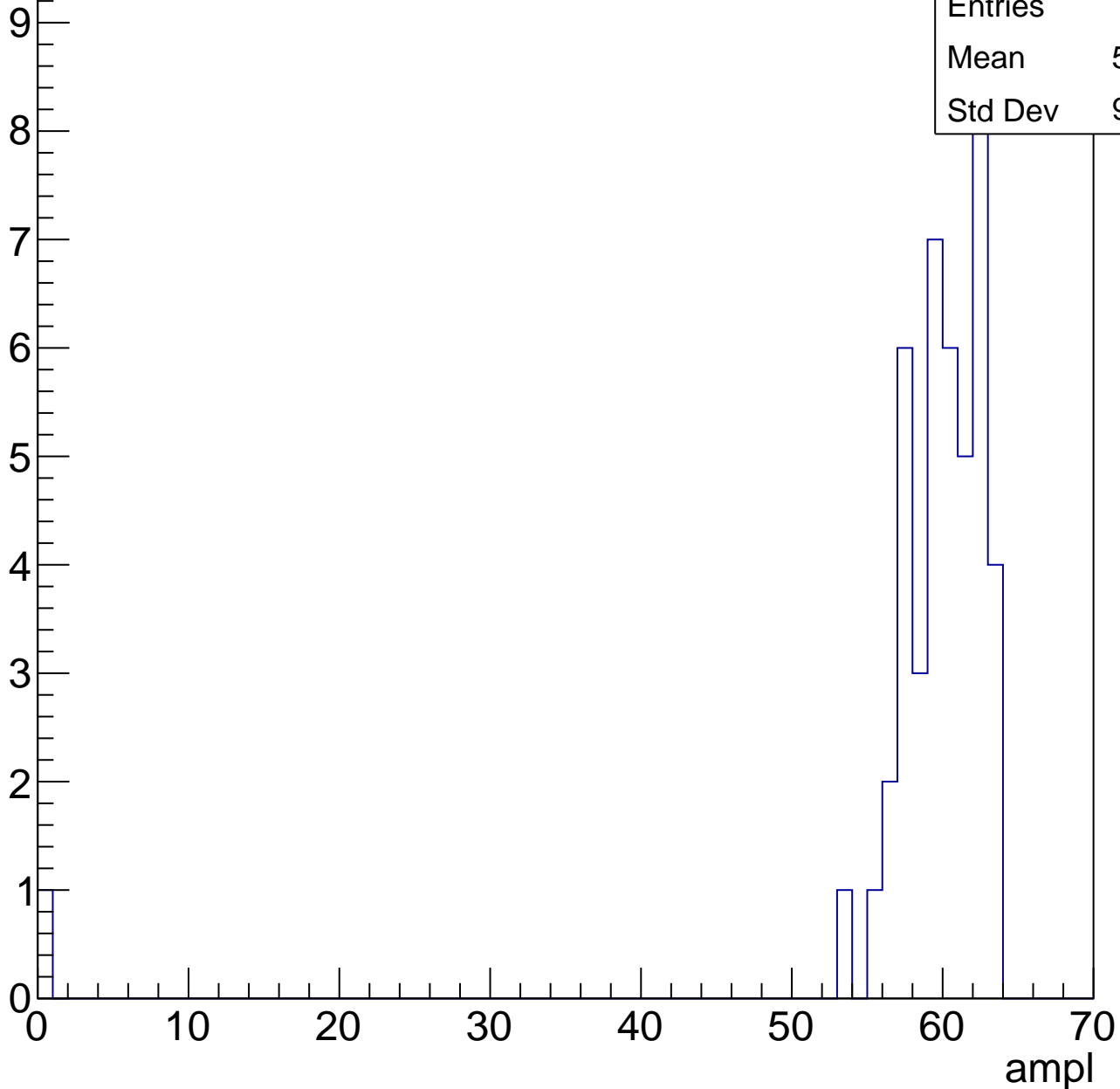


# B0L002S, U23-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

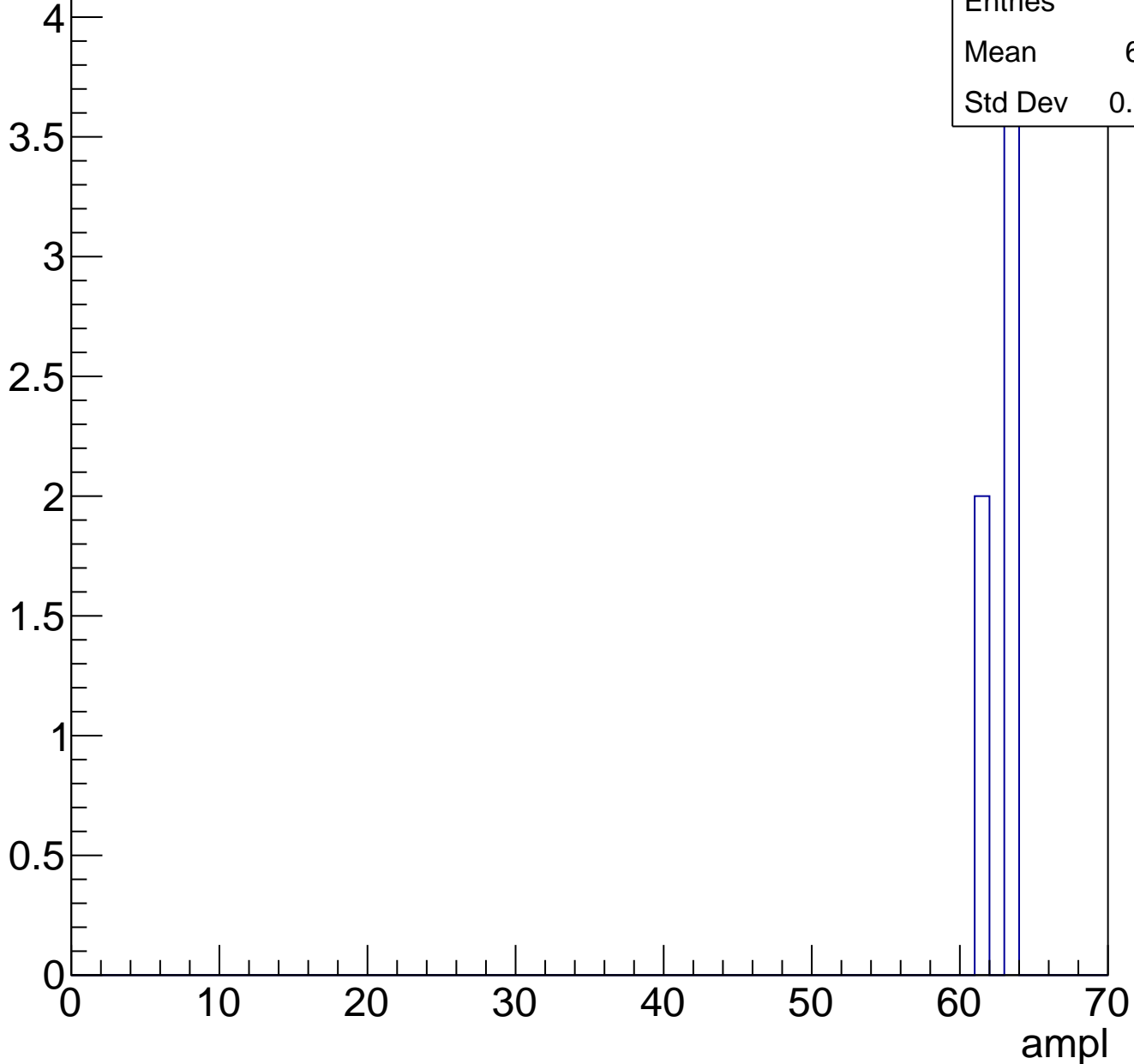
Entries	45
Mean	58.31
Std Dev	9.101



# B0L002S, U23-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch74, adc0

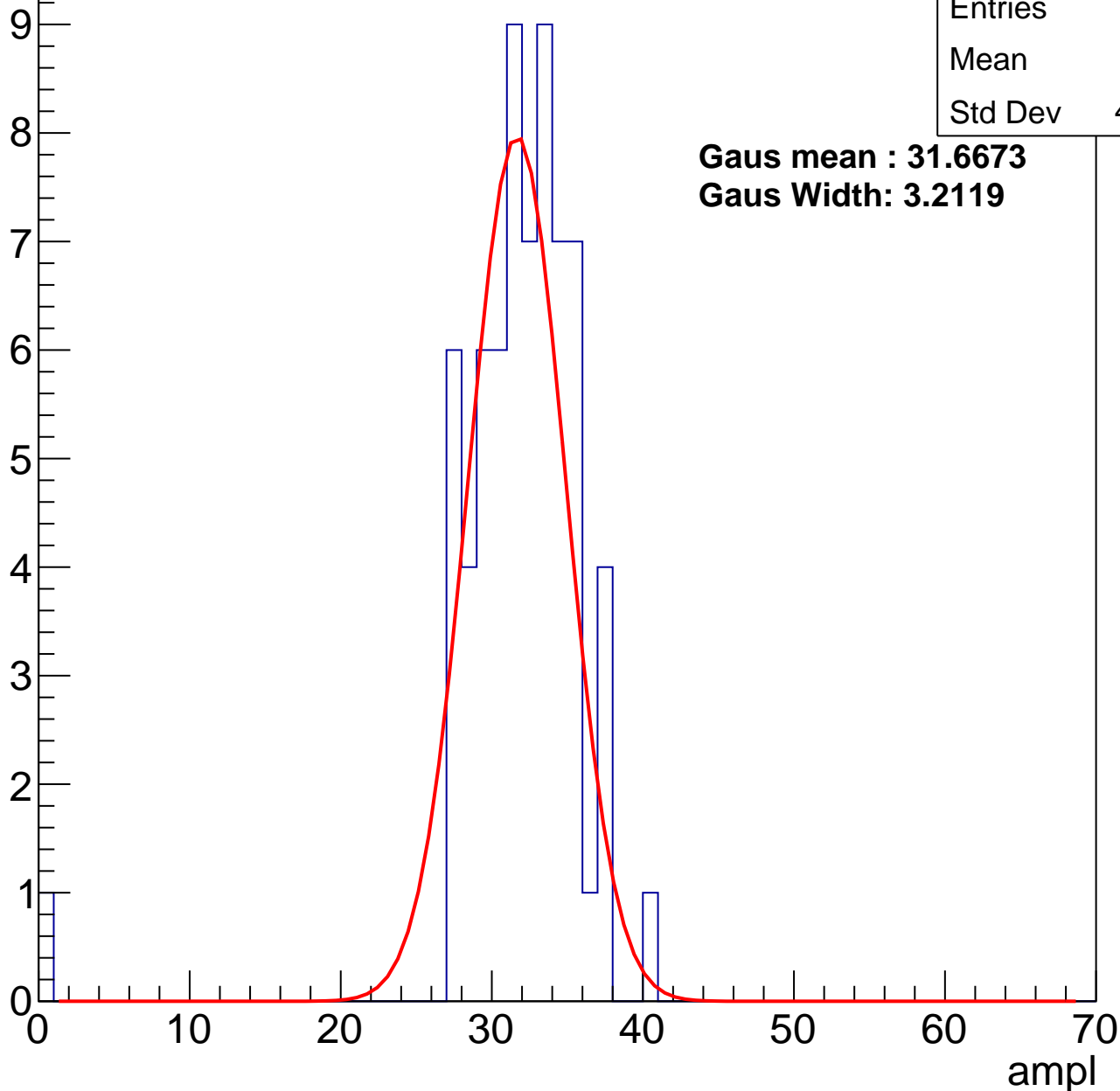
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.4
Std Dev	4.821

**Gaus mean : 31.6673**

**Gaus Width: 3.2119**



# B0L002S, U23-ch74, adc1

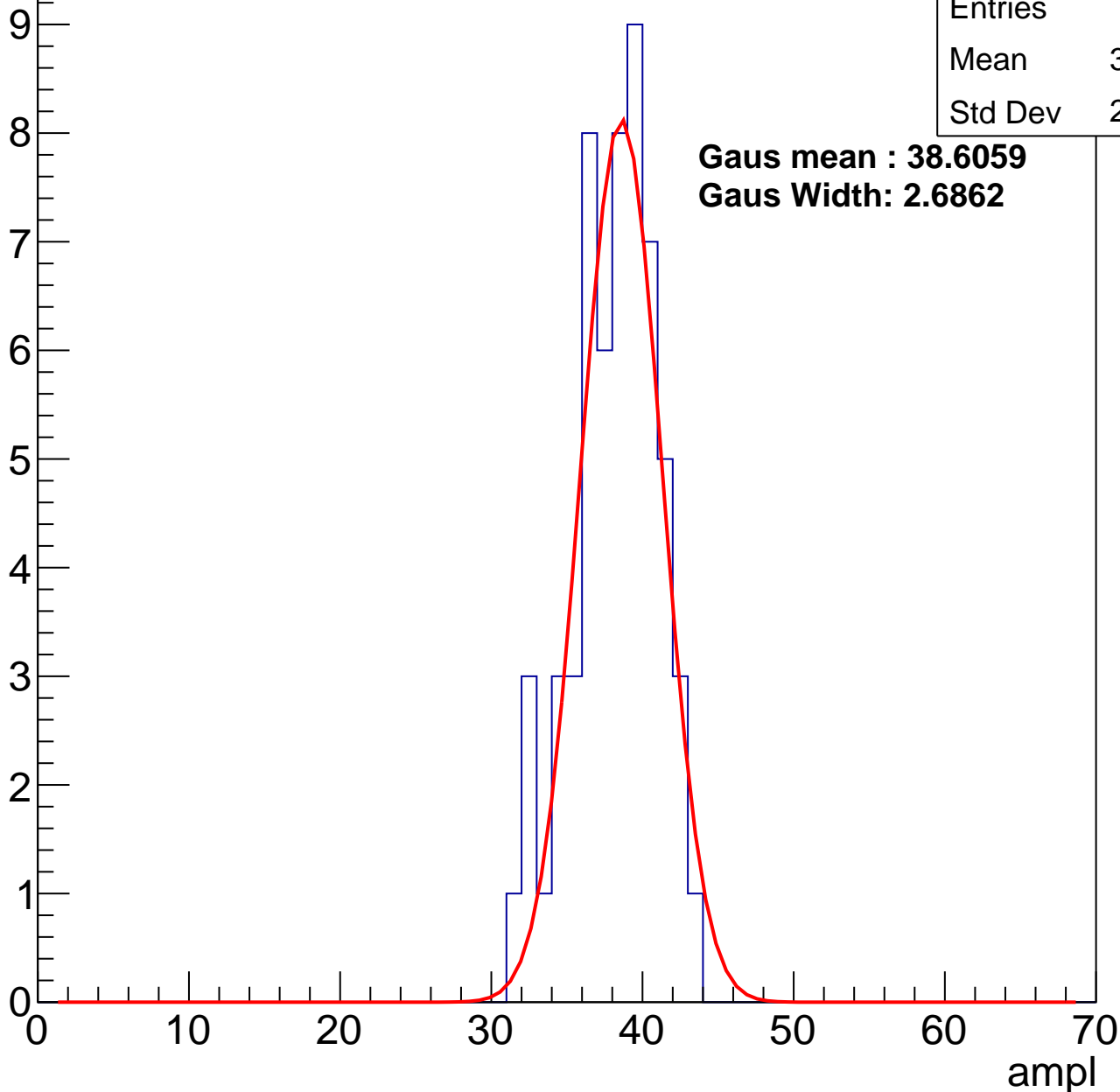
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.69
Std Dev	2.768

**Gaus mean : 38.6059**

**Gaus Width: 2.6862**



# B0L002S, U23-ch74, adc2

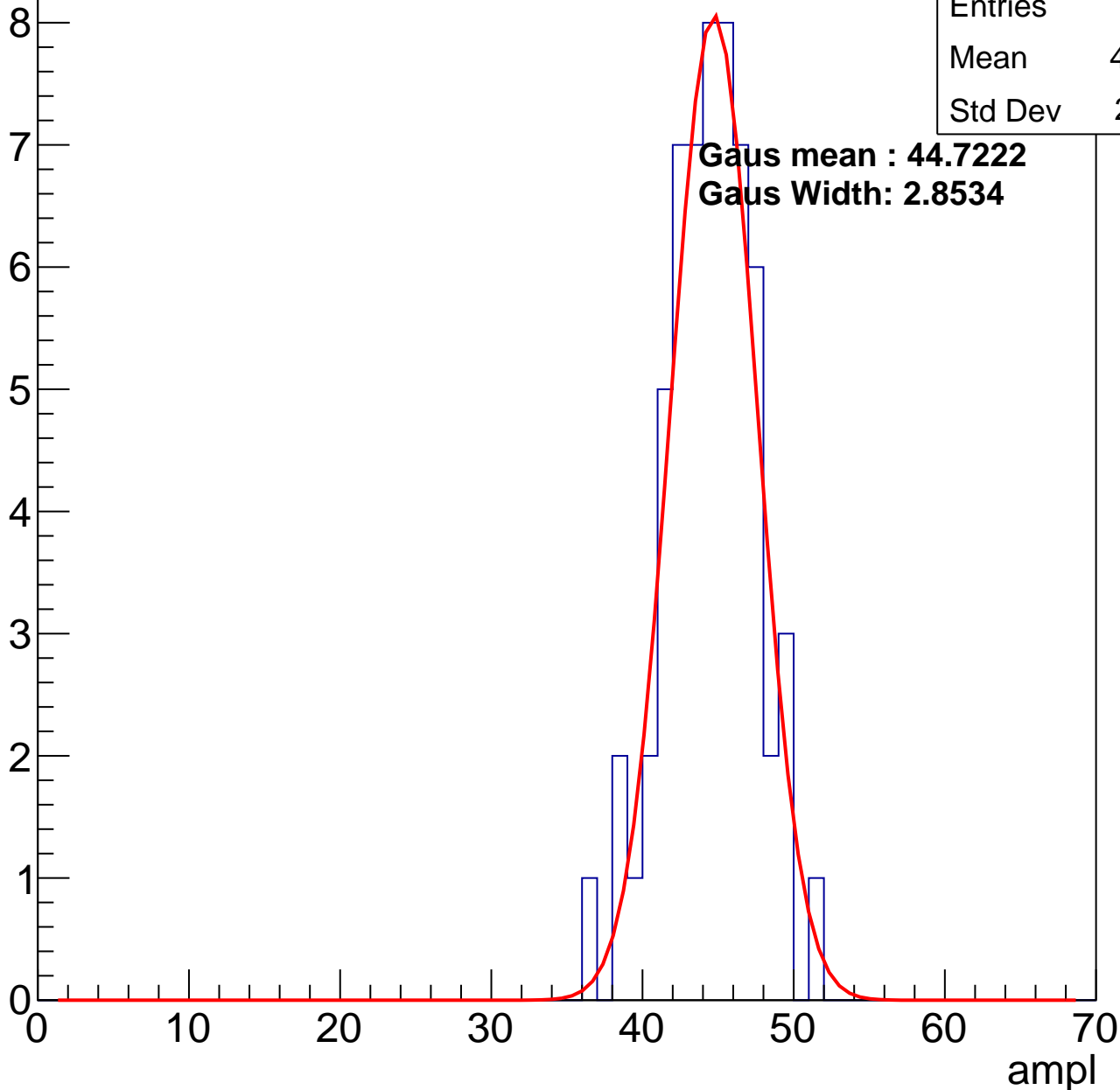
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.02
Std Dev	2.941

**Gaus mean : 44.7222**

**Gaus Width: 2.8534**

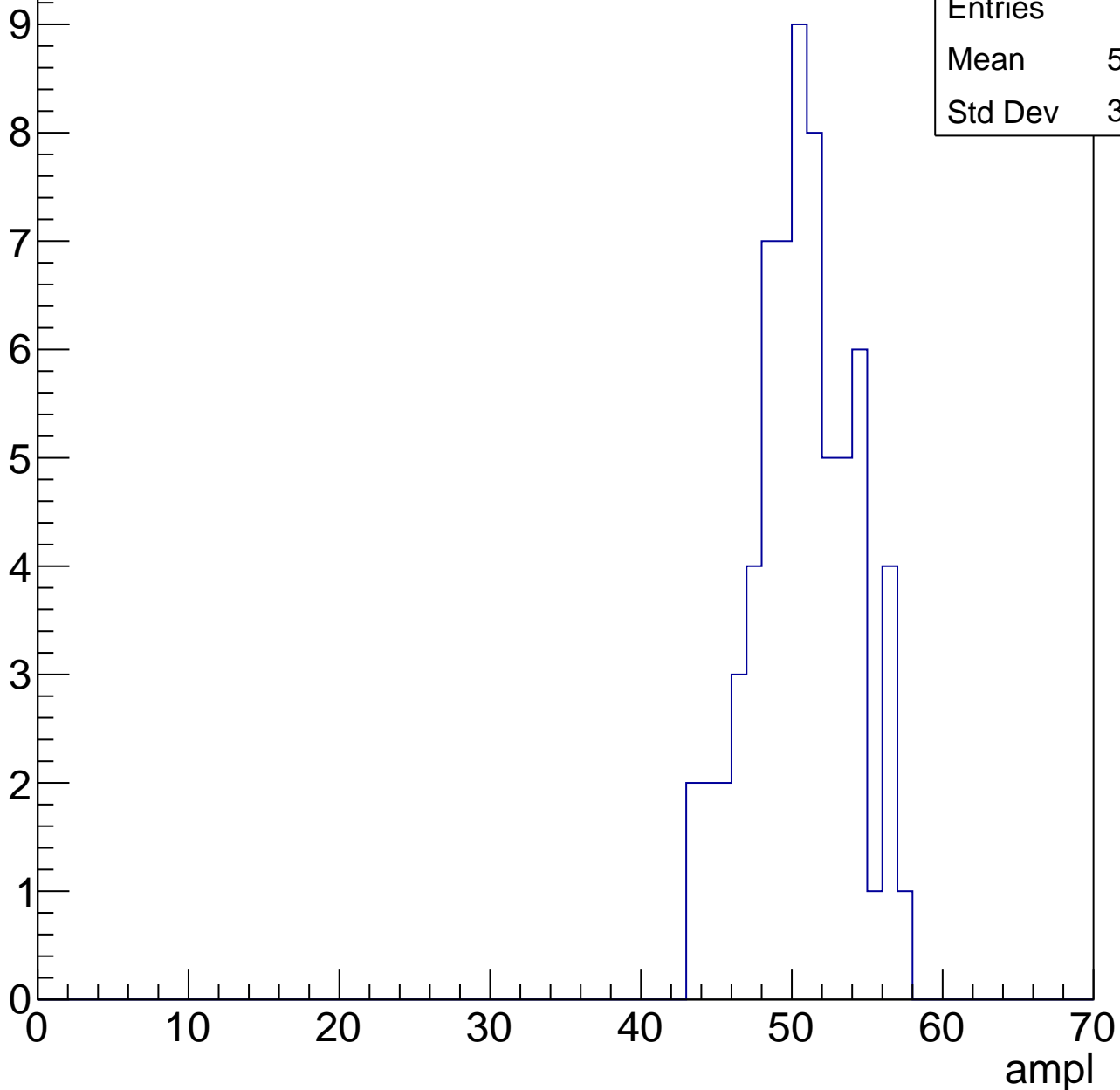


# B0L002S, U23-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.18
Std Dev	3.312



# B0L002S, U23-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	67
Mean	56.45
Std Dev	3.024

Entry

10

8

6

4

2

0

0

10

20

30

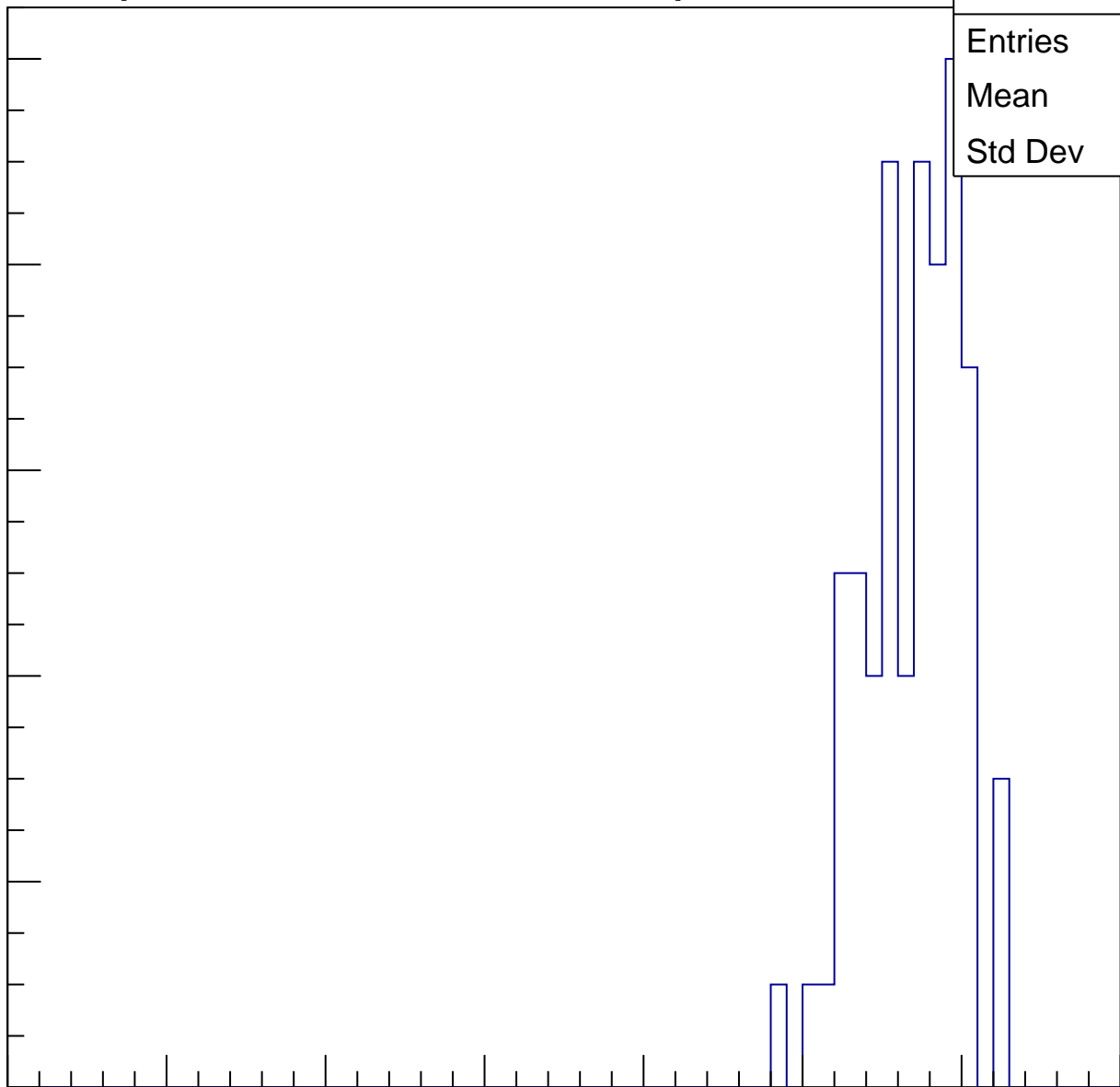
40

50

60

70

ampl

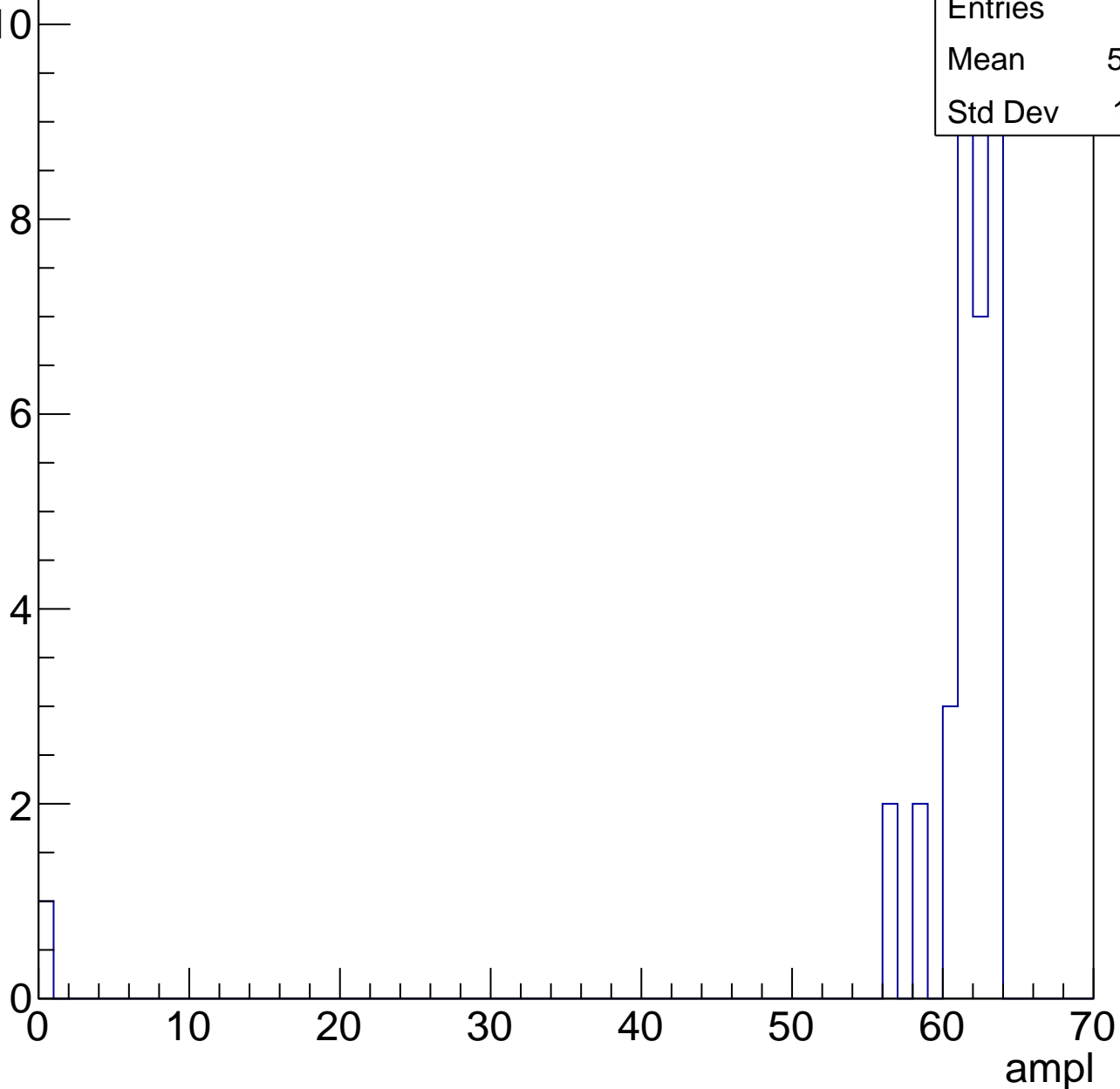


# B0L002S, U23-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.44
Std Dev	10.51



# B0L002S, U23-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch74, adc7

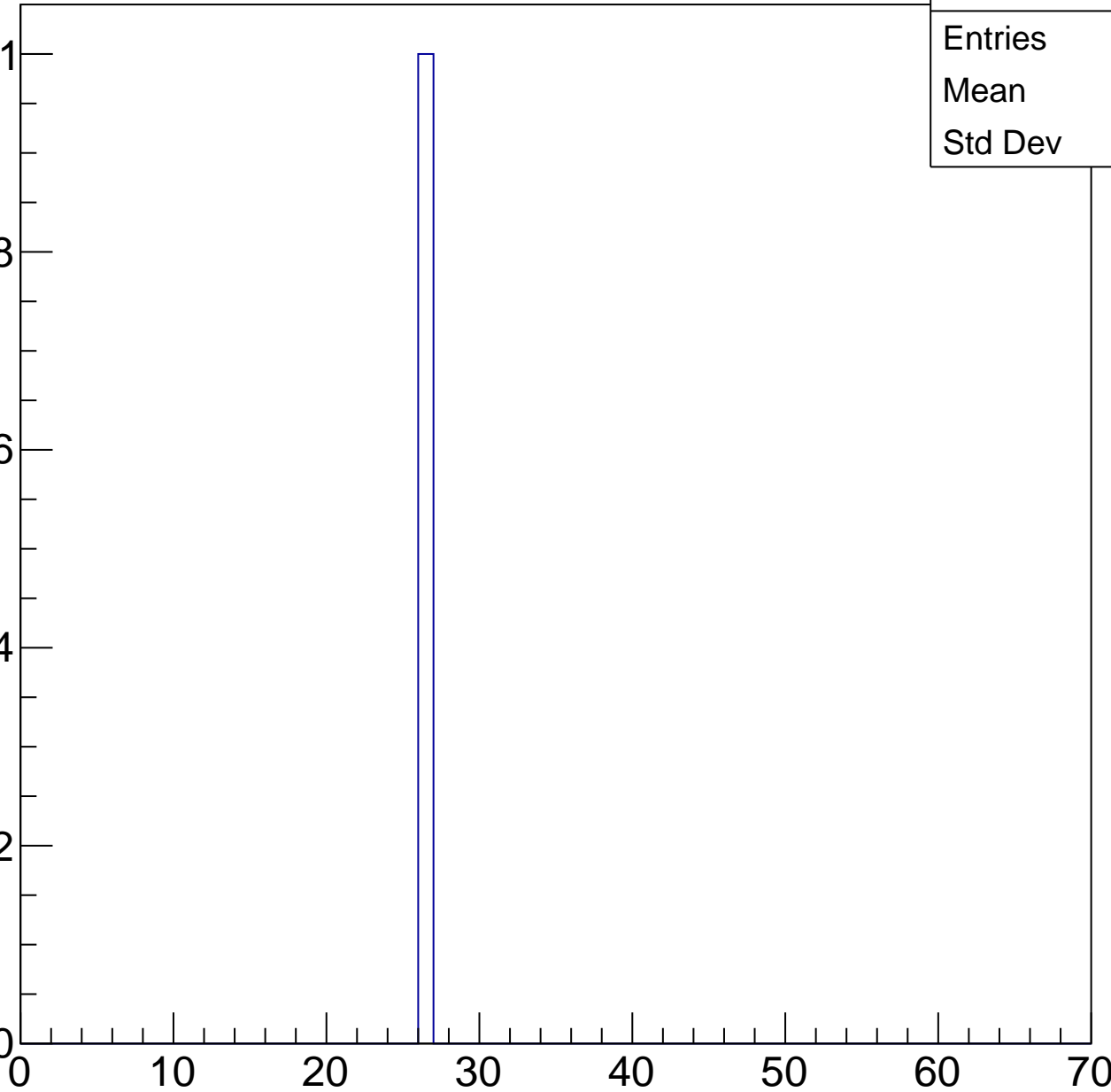
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	26
Std Dev	0

ampl



# B0L002S, U23-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	88
Mean	30.07
Std Dev	3.729

**Gaus mean : 30.4423**

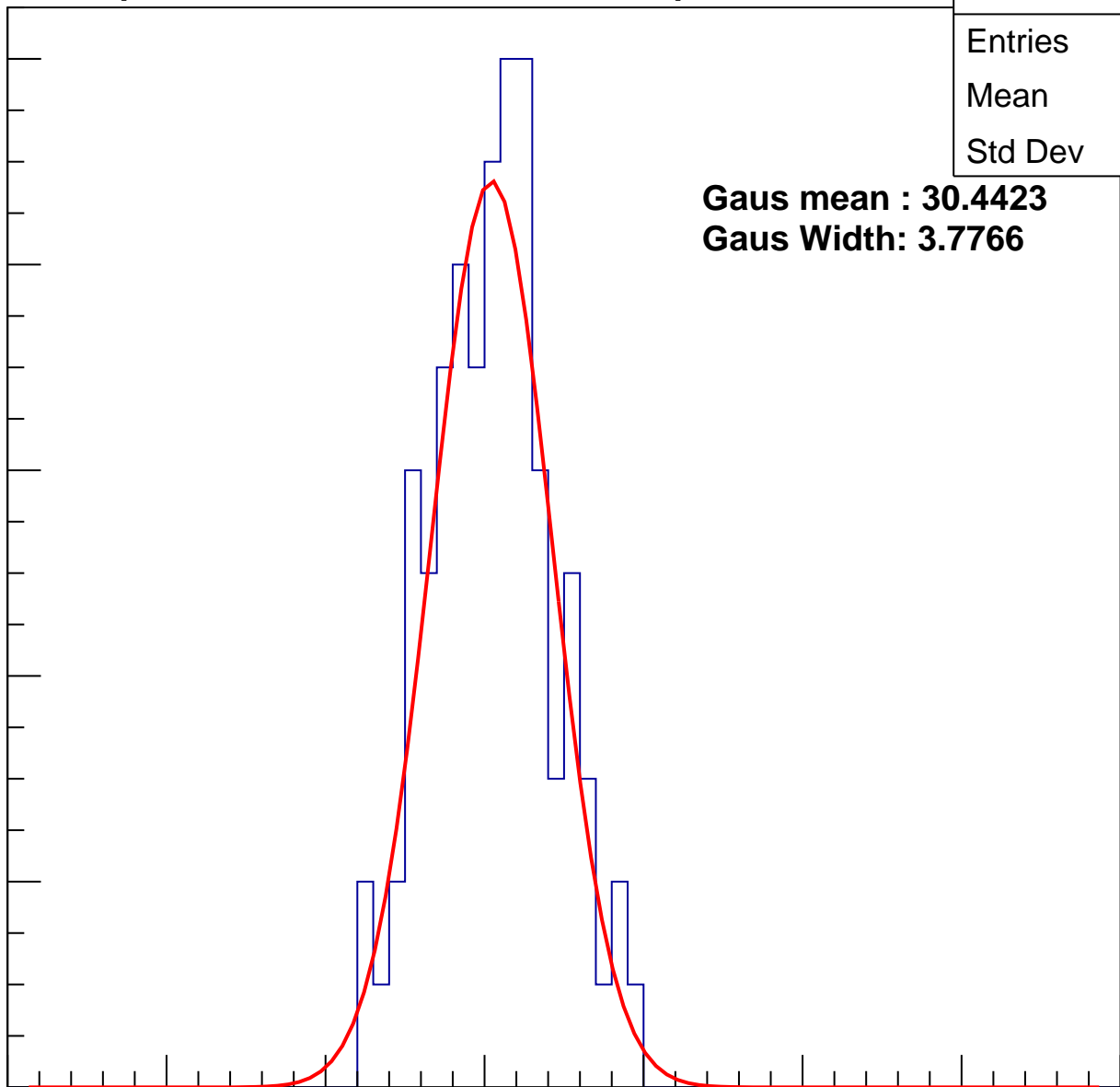
**Gaus Width: 3.7766**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch75, adc1

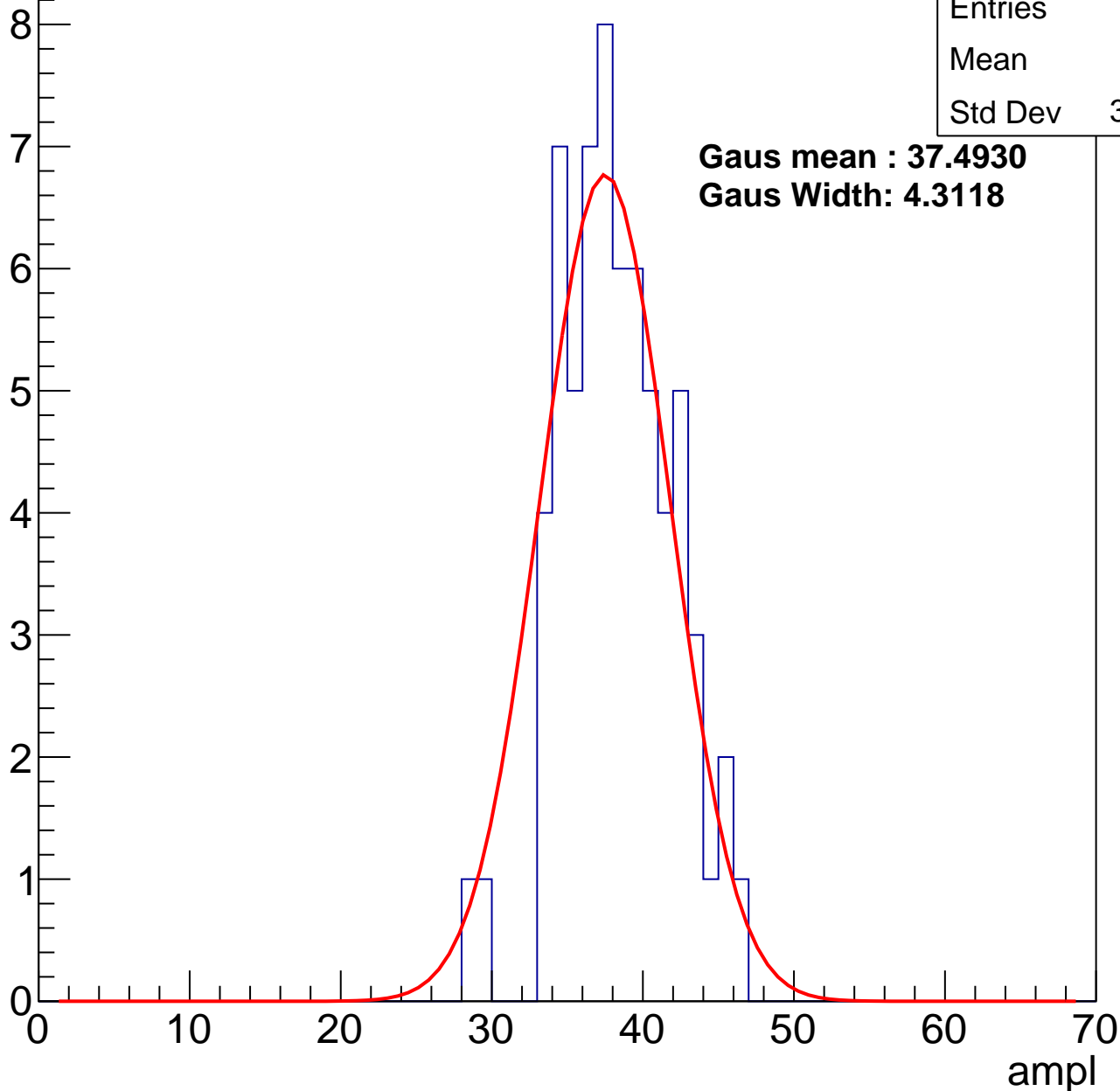
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.8
Std Dev	3.665

**Gaus mean : 37.4930**

**Gaus Width: 4.3118**



# B0L002S, U23-ch75, adc2

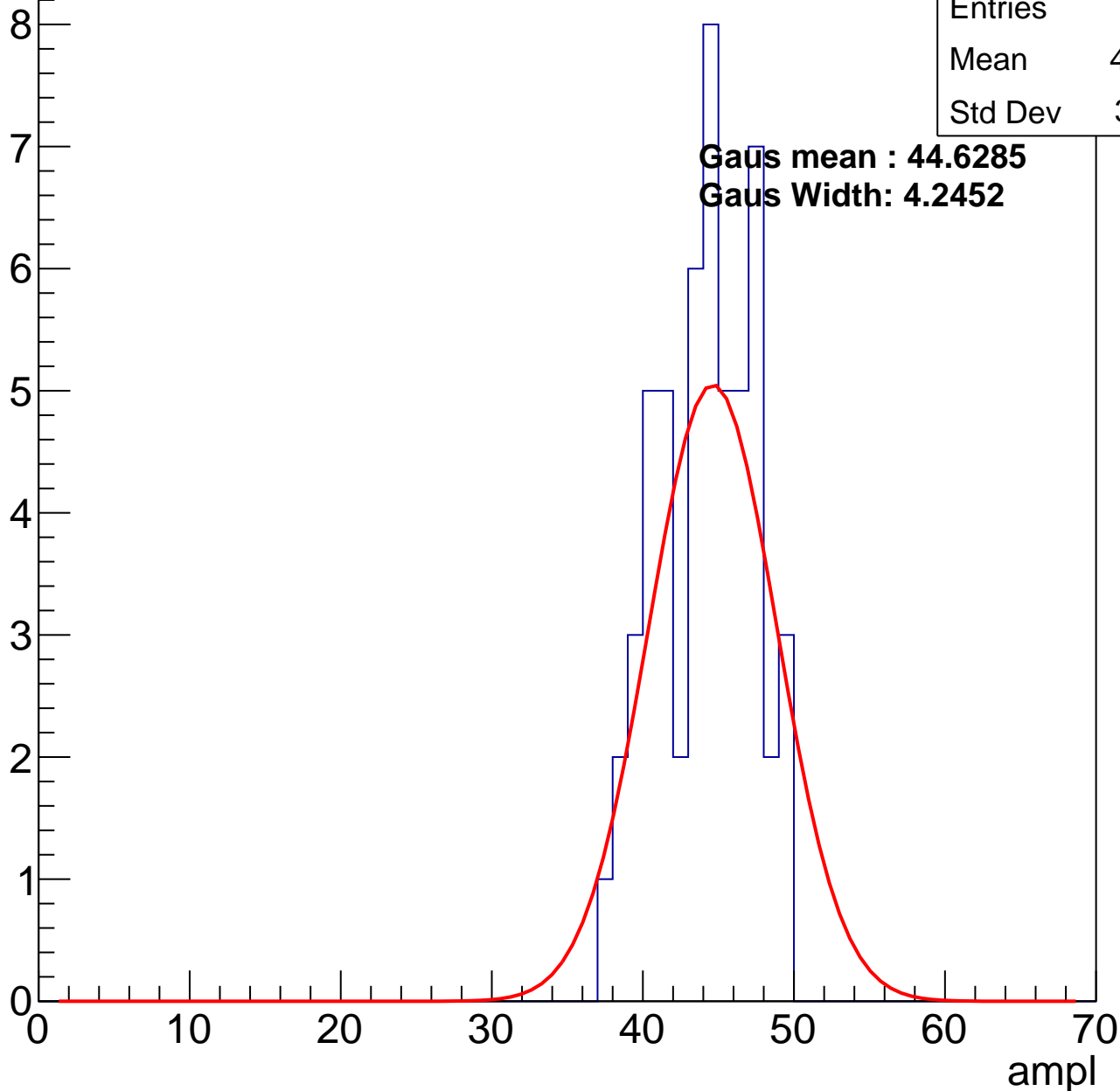
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.63
Std Dev	3.111

**Gaus mean : 44.6285**

**Gaus Width: 4.2452**

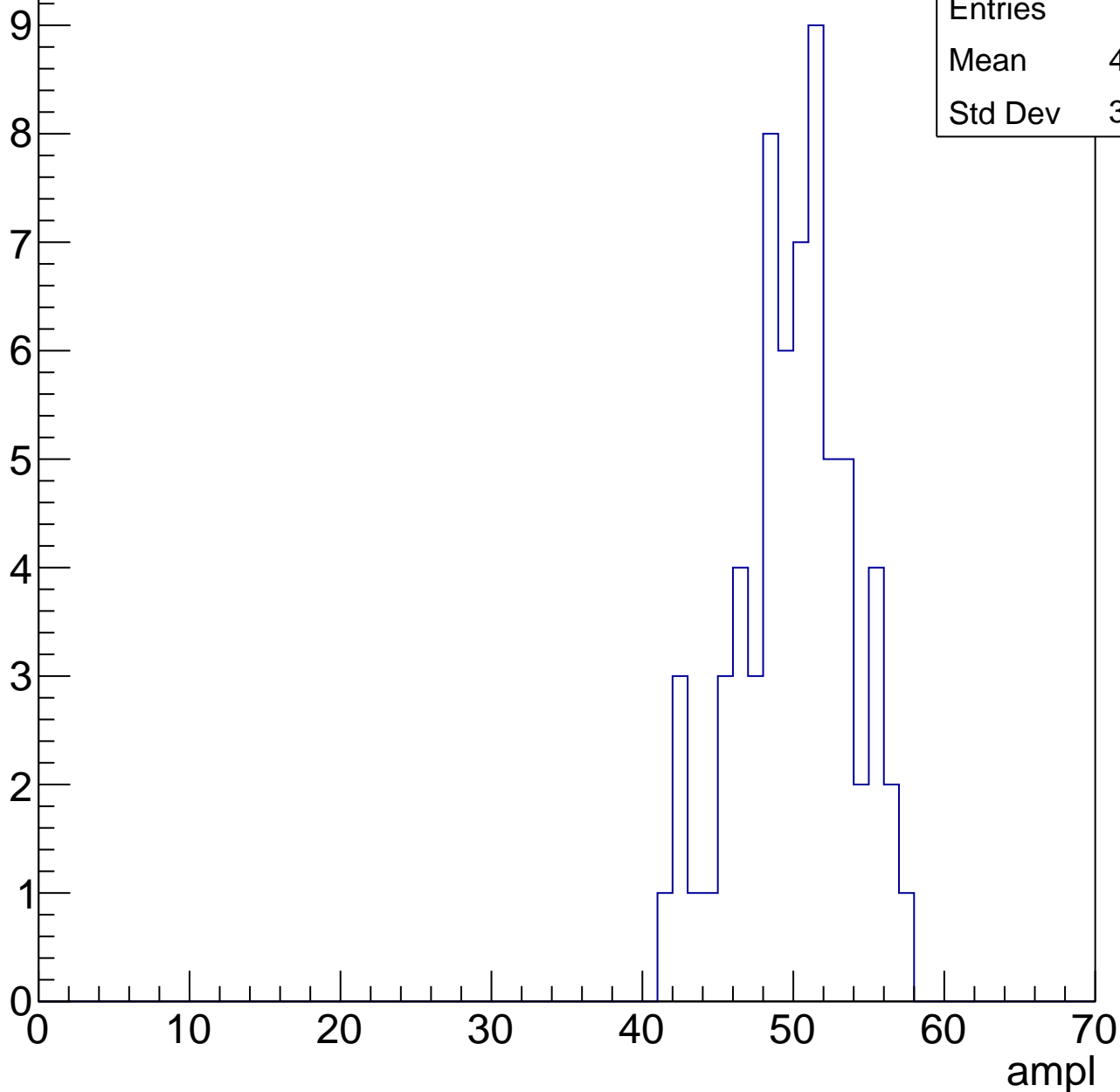


# B0L002S, U23-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

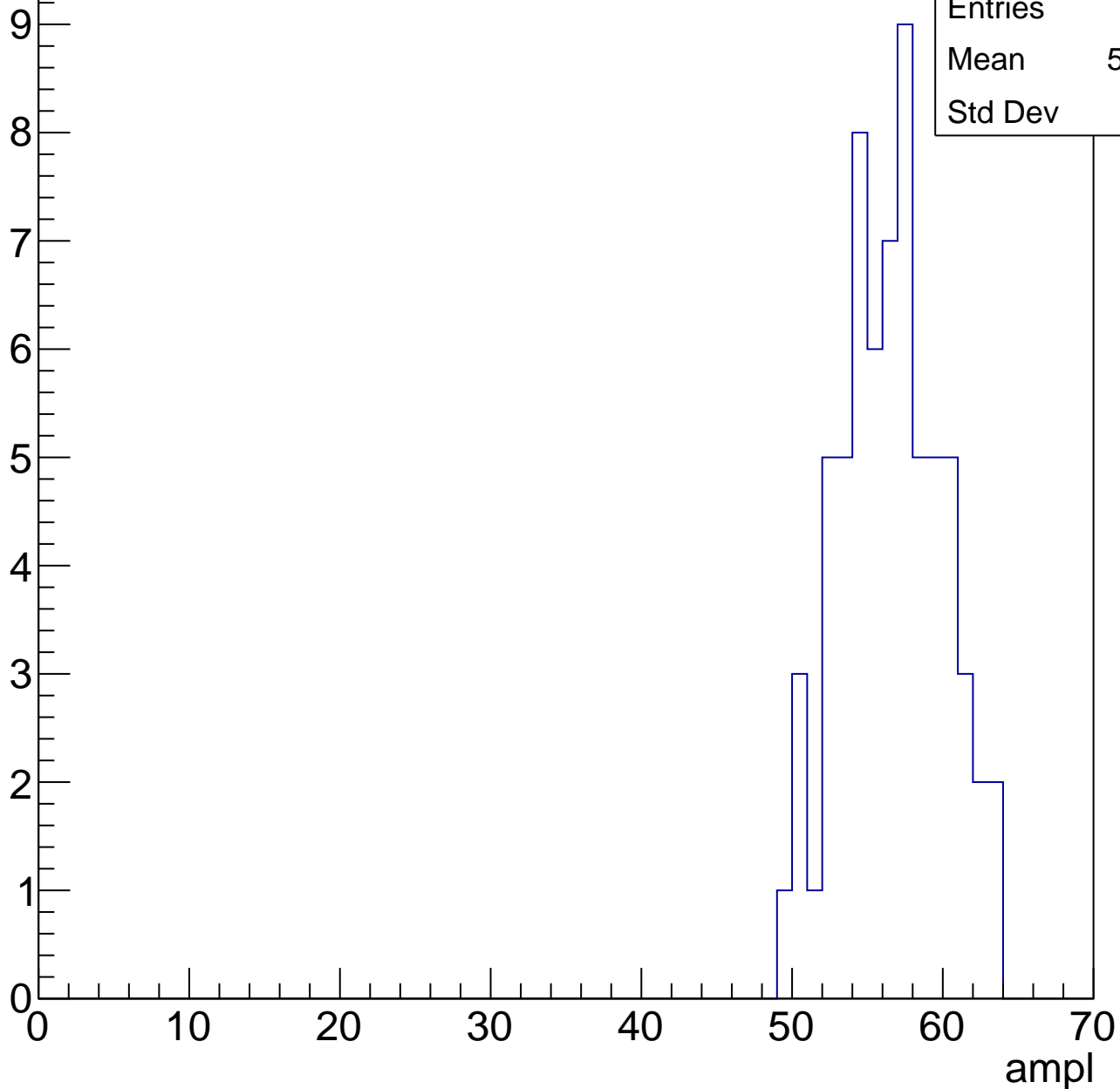
Entries	65
Mean	49.58
Std Dev	3.675



# B0L002S, U23-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



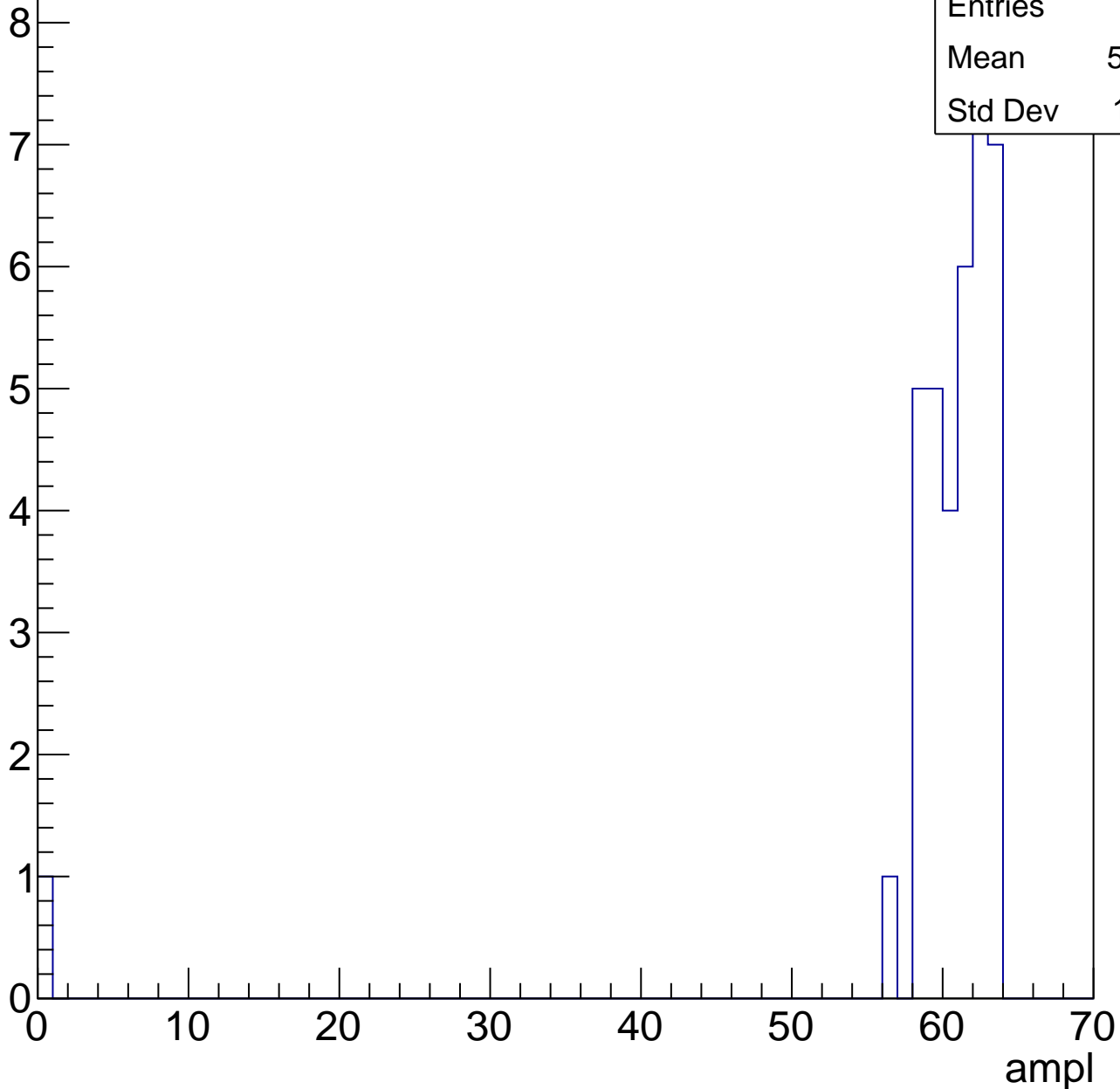
Entries	67
Mean	56.12
Std Dev	3.33

# B0L002S, U23-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.03
Std Dev	10.01



# B0L002S, U23-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

# B0L002S, U23-ch76, adc0

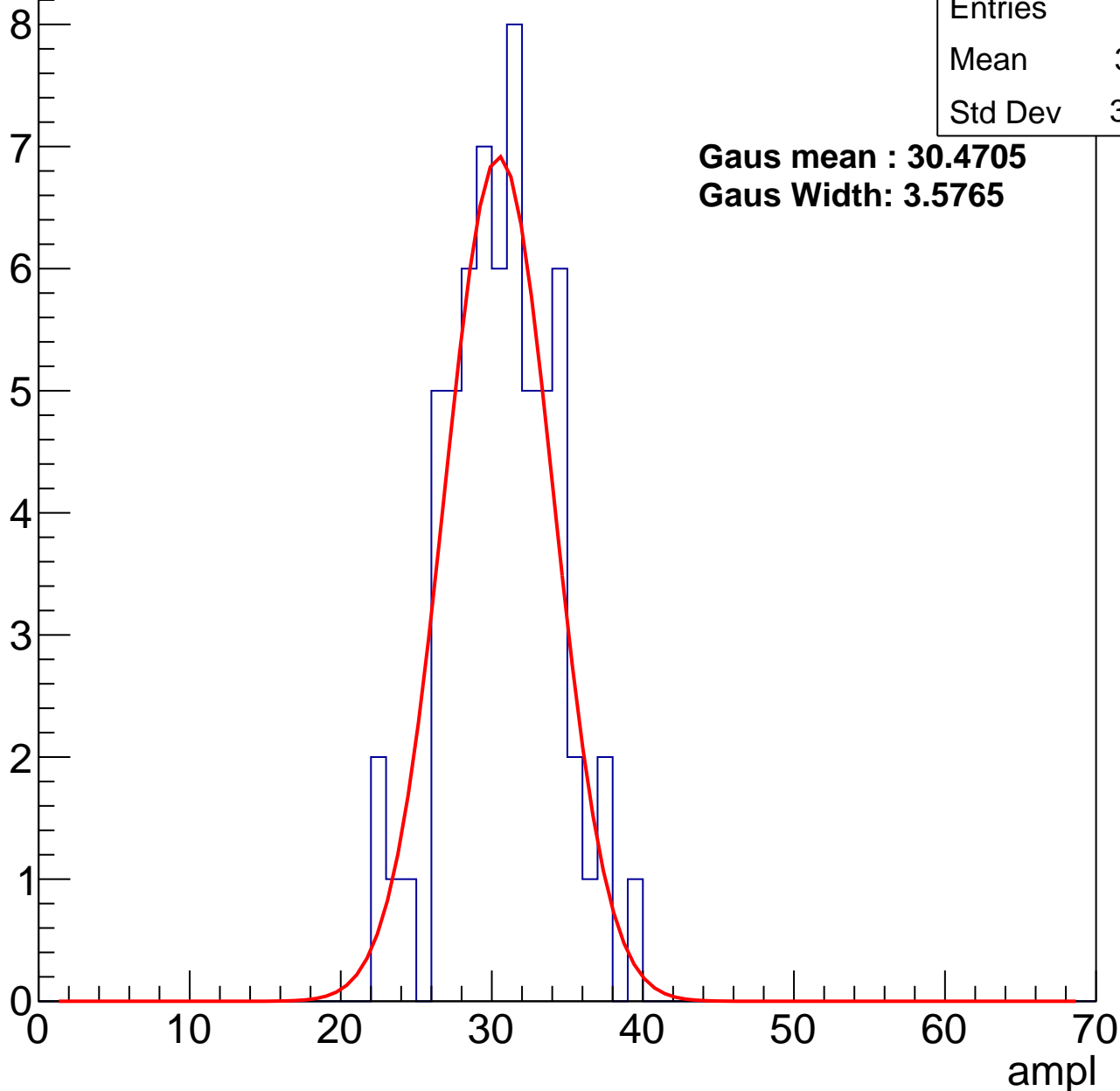
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.21
Std Dev	3.564

**Gaus mean : 30.4705**

**Gaus Width: 3.5765**



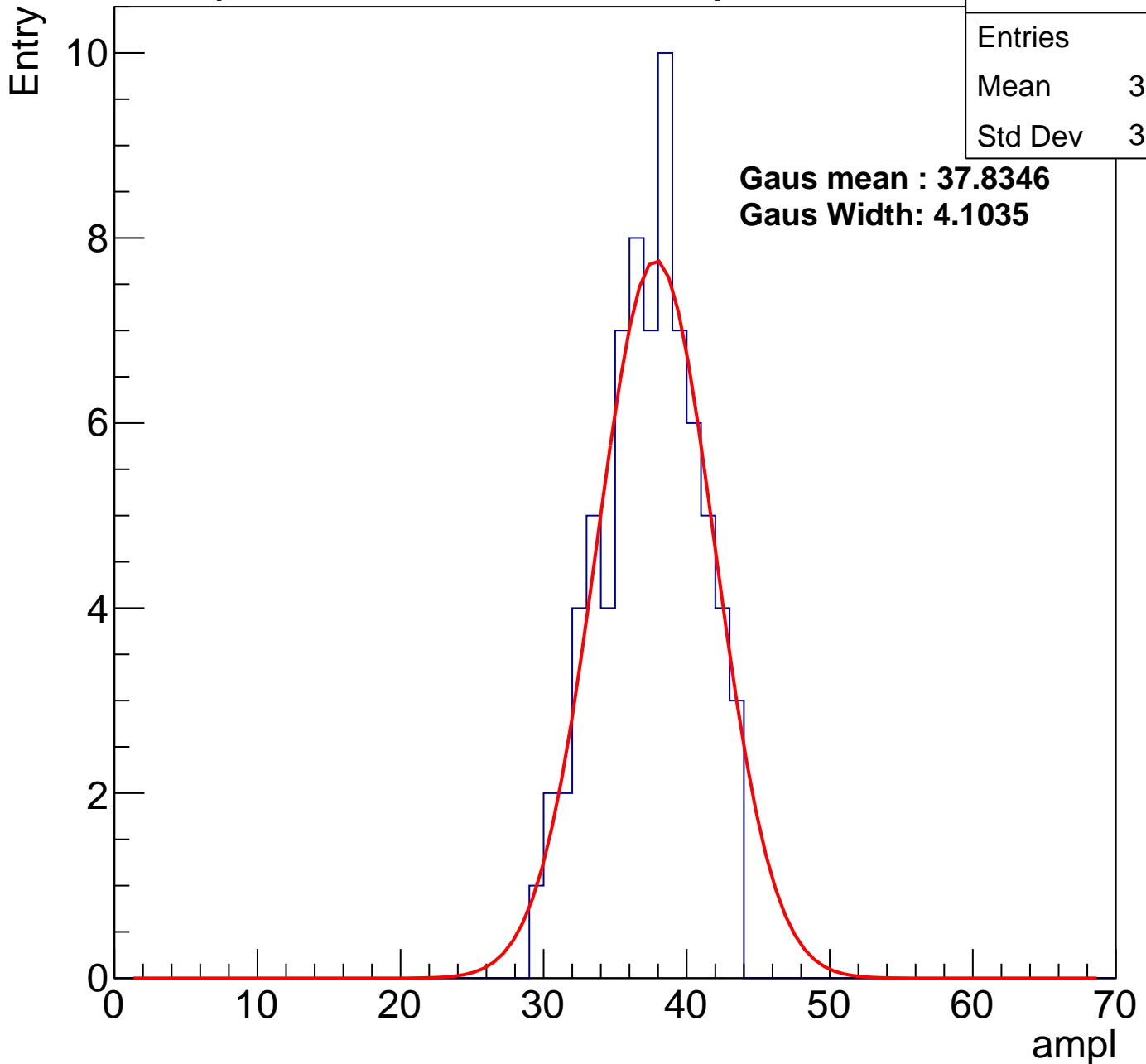
# B0L002S, U23-ch76, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	36.89
Std Dev	3.396

**Gaus mean : 37.8346**

**Gaus Width: 4.1035**



# B0L002S, U23-ch76, adc2

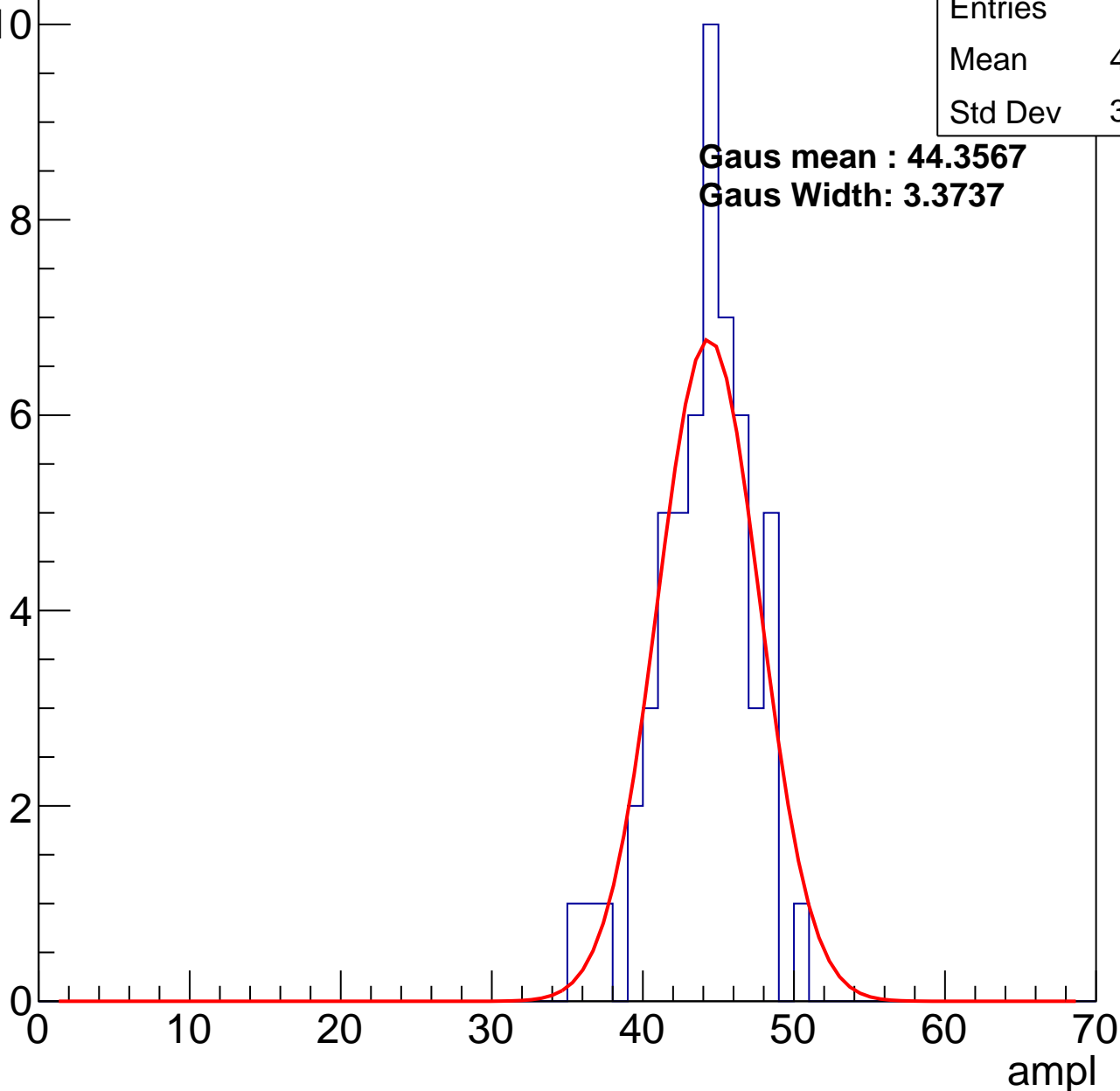
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.59
Std Dev	3.069

**Gaus mean : 44.3567**

**Gaus Width: 3.3737**



# B0L002S, U23-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

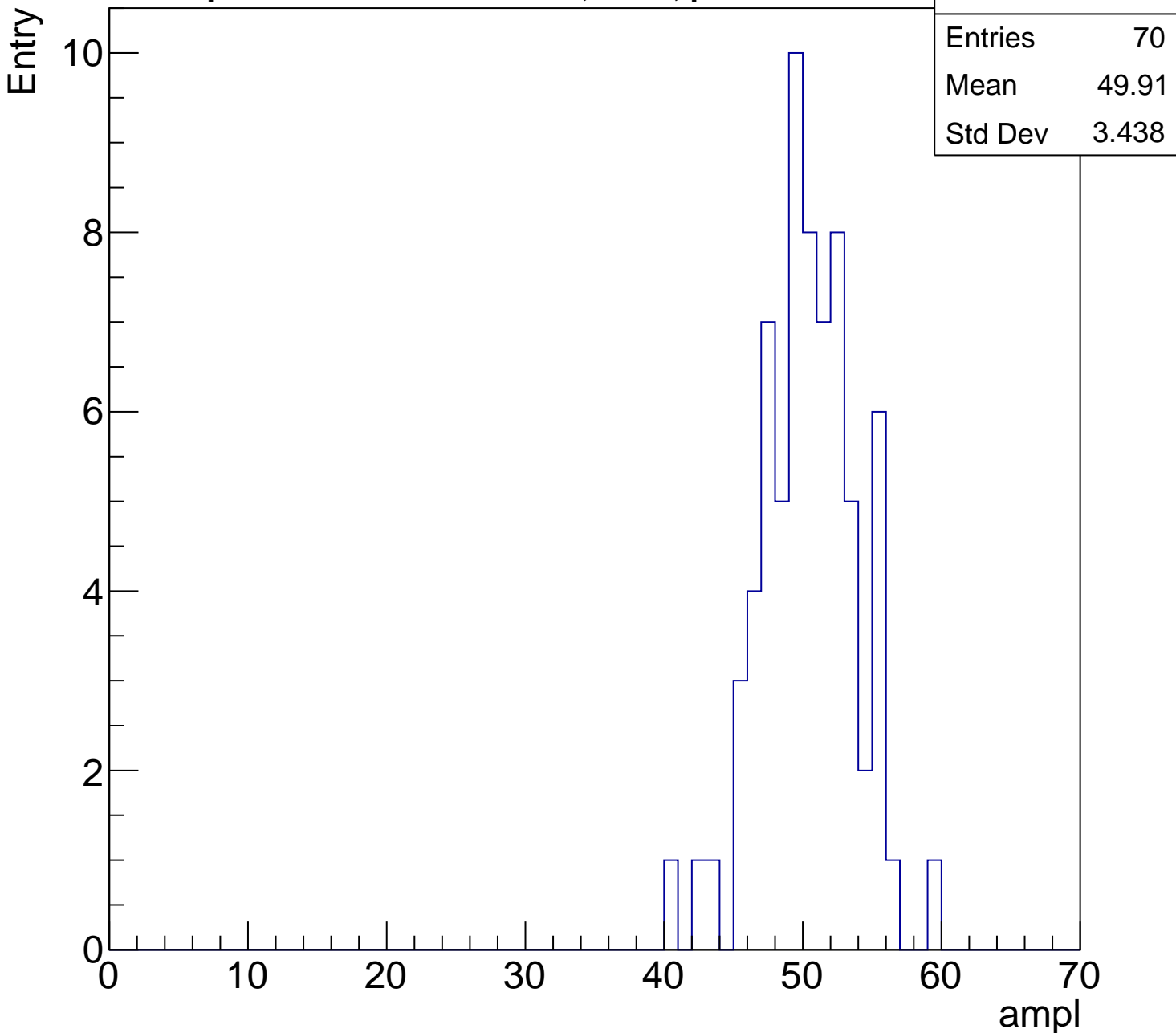
Entries	70
Mean	49.91
Std Dev	3.438

Entry

10  
8  
6  
4  
2  
0

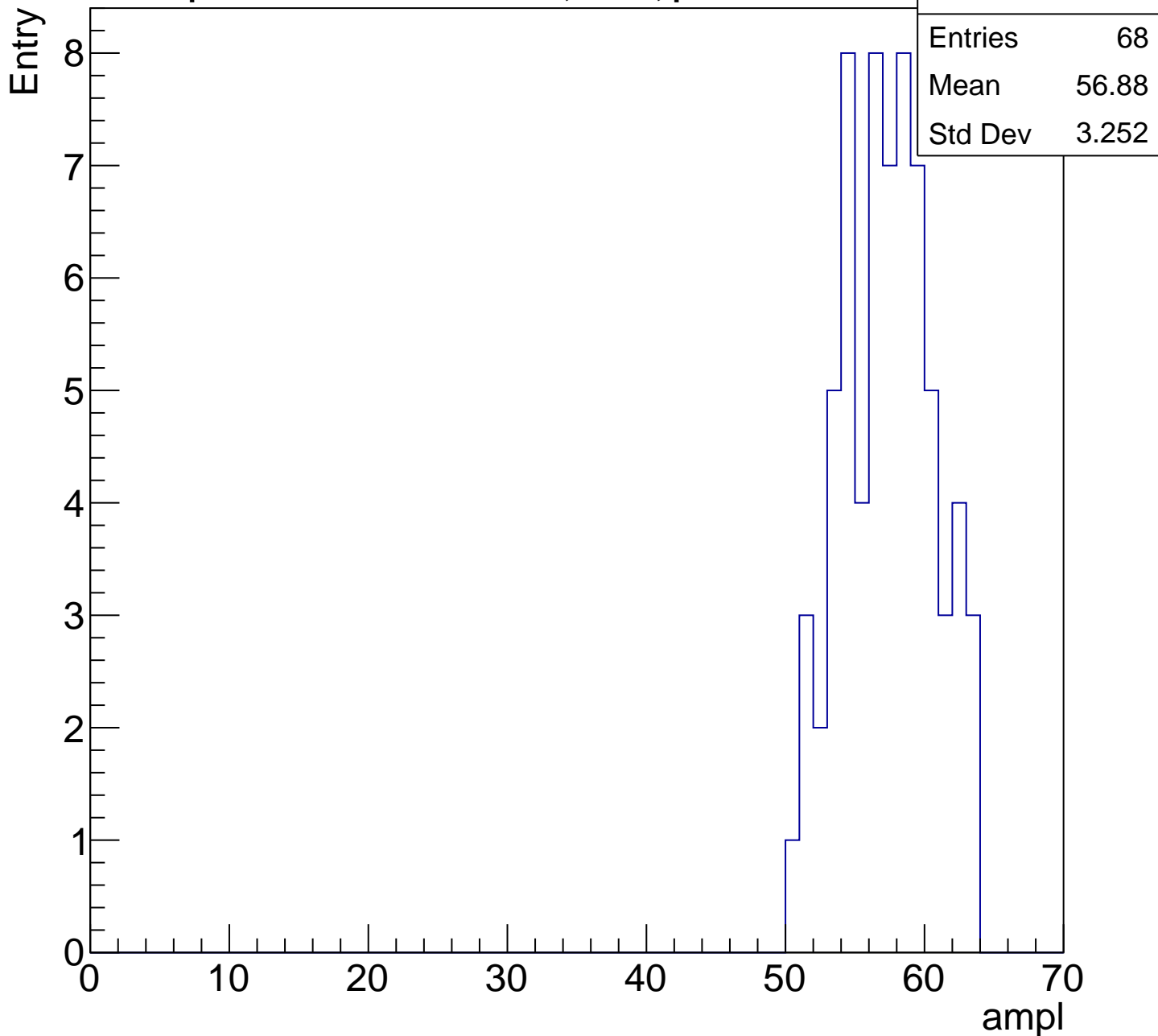
ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

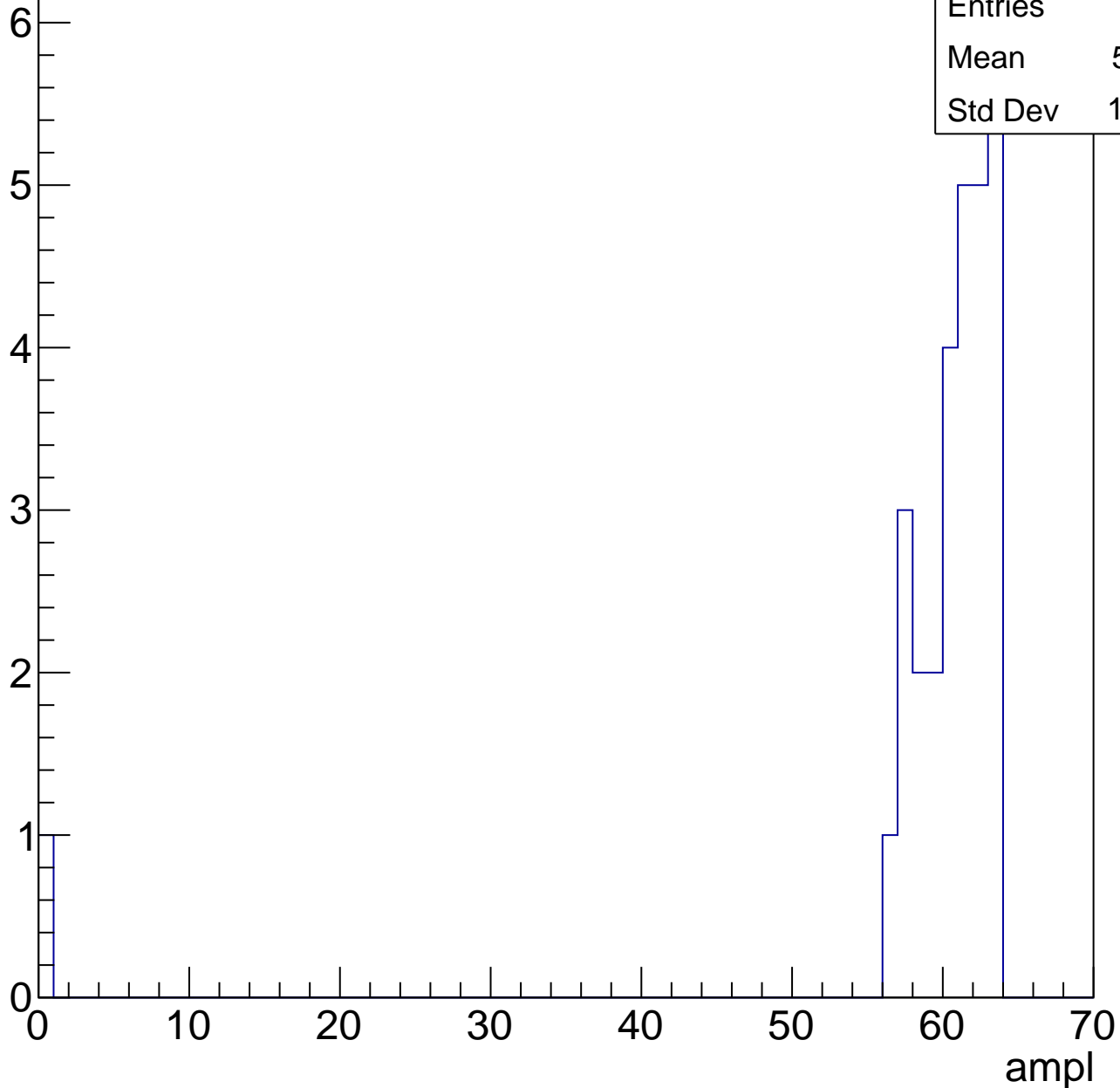


# B0L002S, U23-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

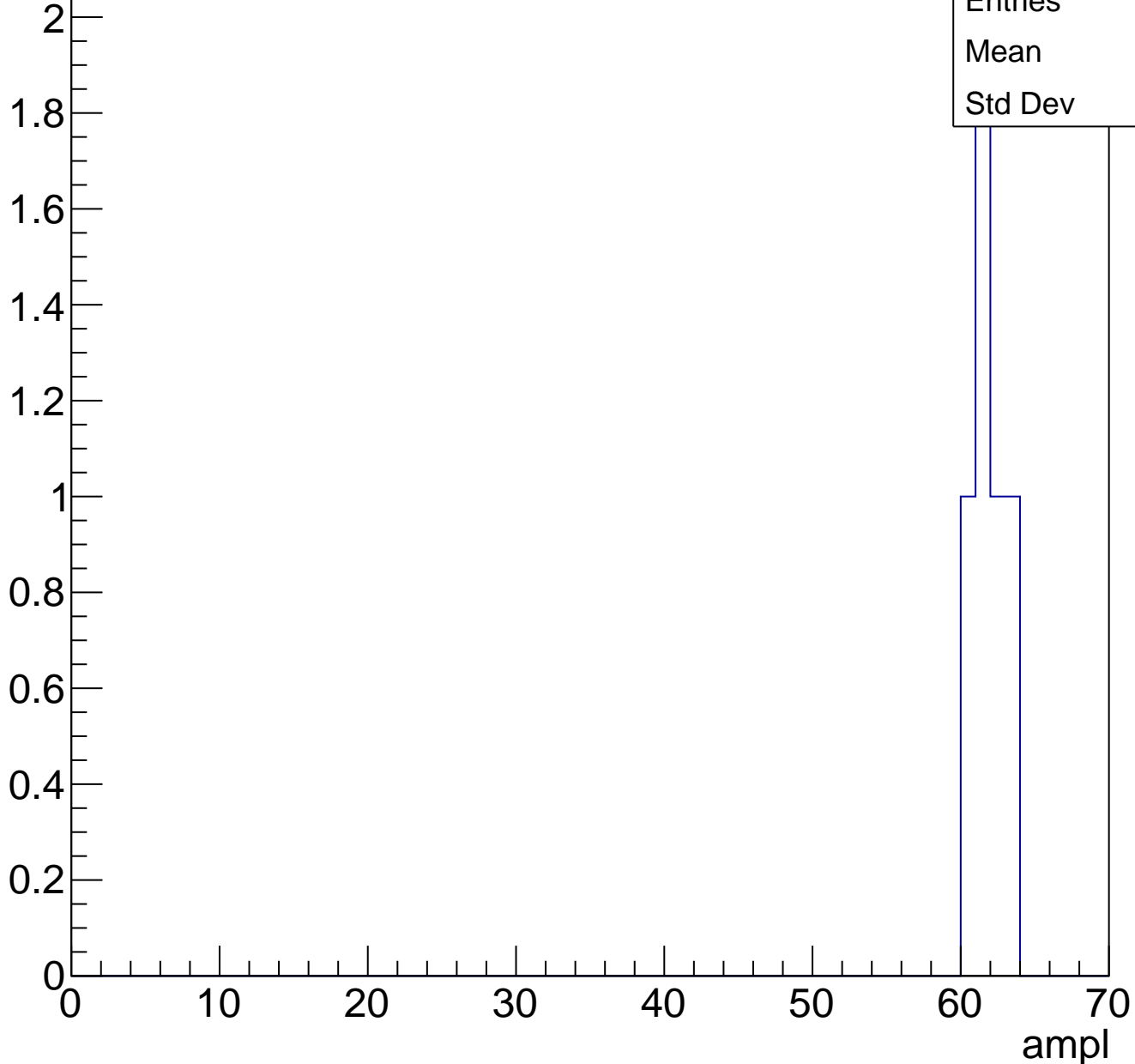
Entries	29
Mean	58.41
Std Dev	11.23



# B0L002S, U23-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	5
Mean	61.4
Std Dev	1.02



# B0L002S, U23-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch77, adc0

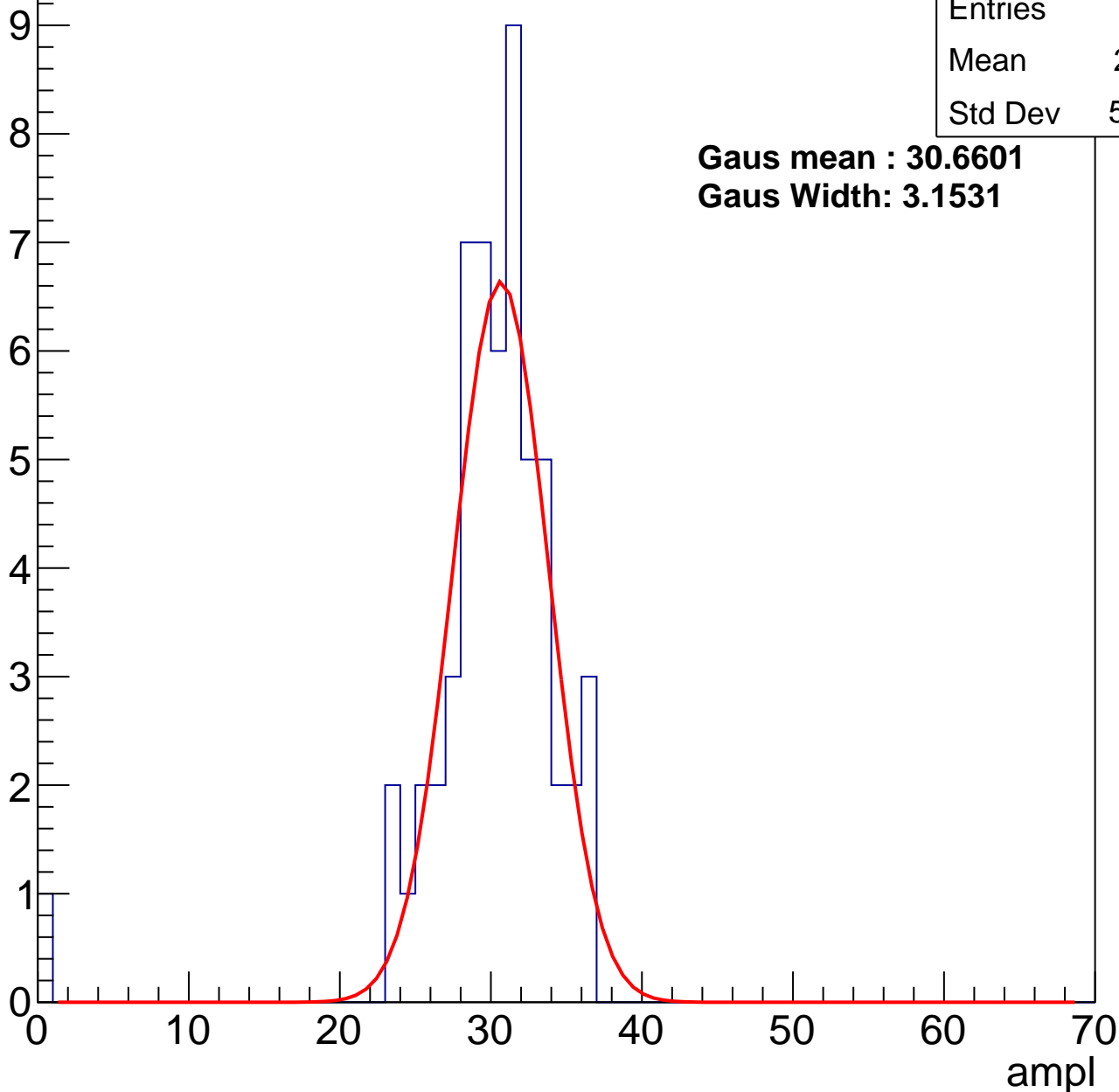
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.51
Std Dev	5.006

**Gaus mean : 30.6601**

**Gaus Width: 3.1531**



# B0L002S, U23-ch77, adc1

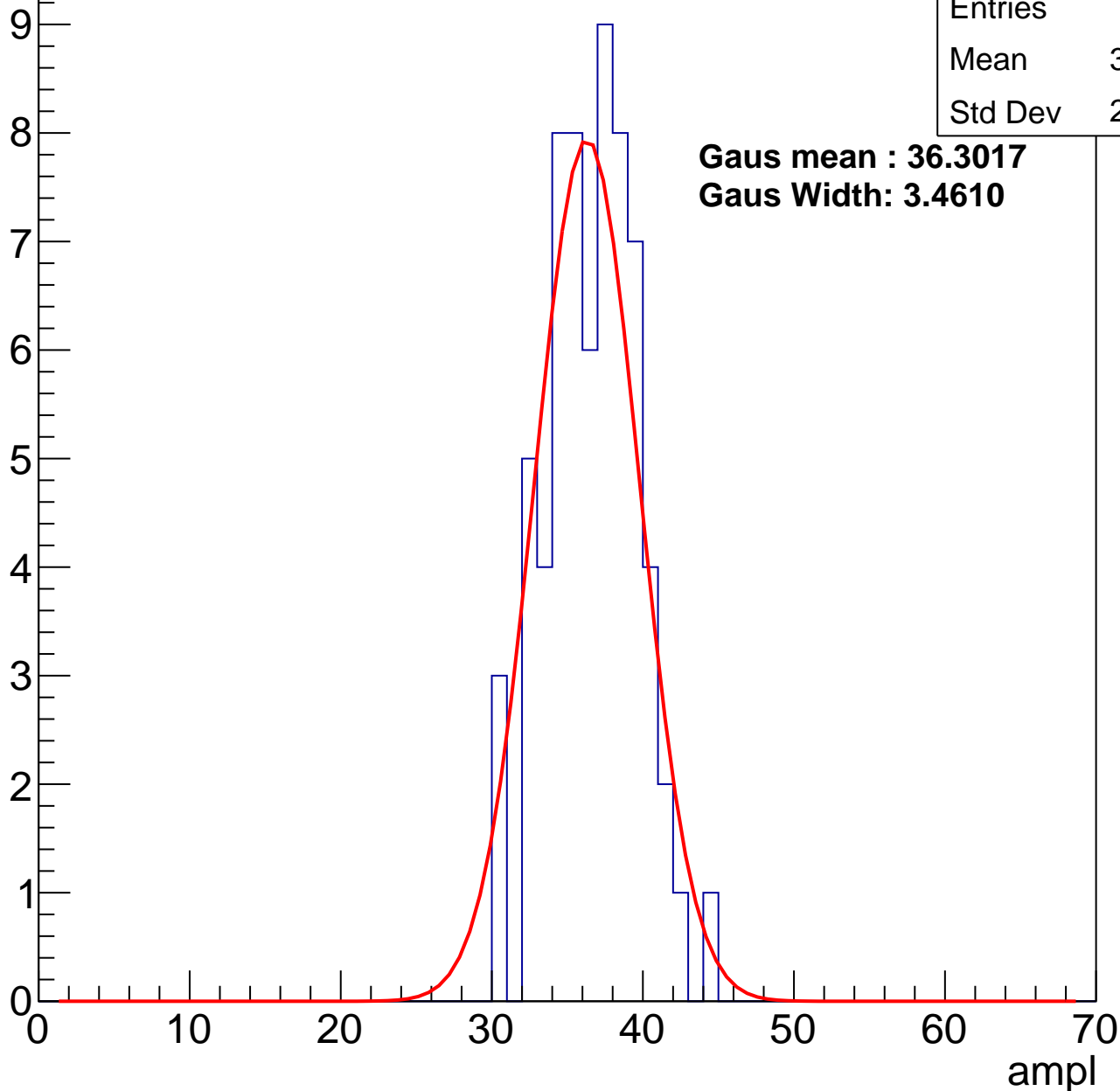
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.18
Std Dev	2.964

**Gaus mean : 36.3017**

**Gaus Width: 3.4610**



# B0L002S, U23-ch77, adc2

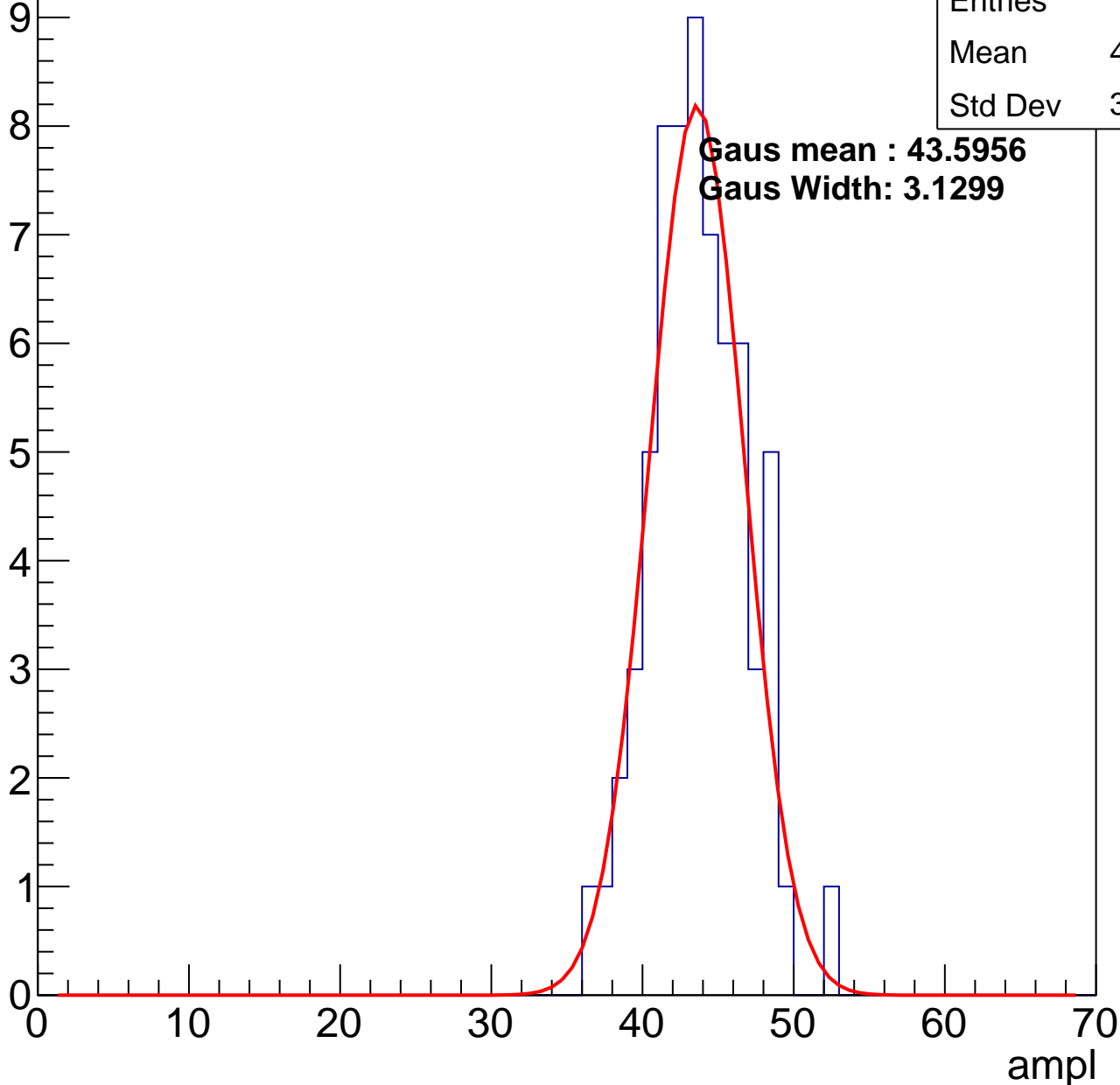
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.23
Std Dev	3.113

**Gaus mean : 43.5956**

**Gaus Width: 3.1299**

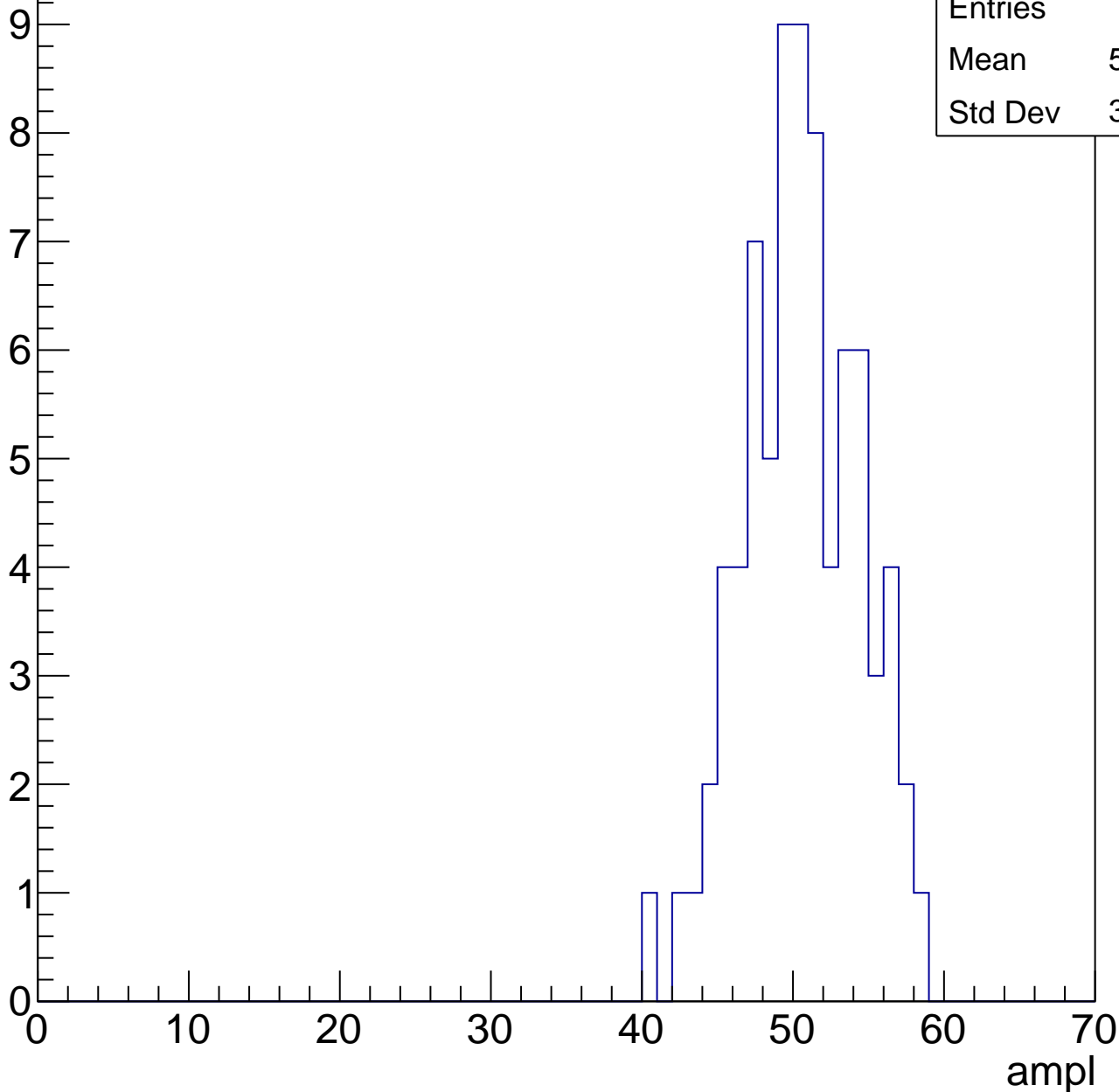


# B0L002S, U23-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	50.08
Std Dev	3.772

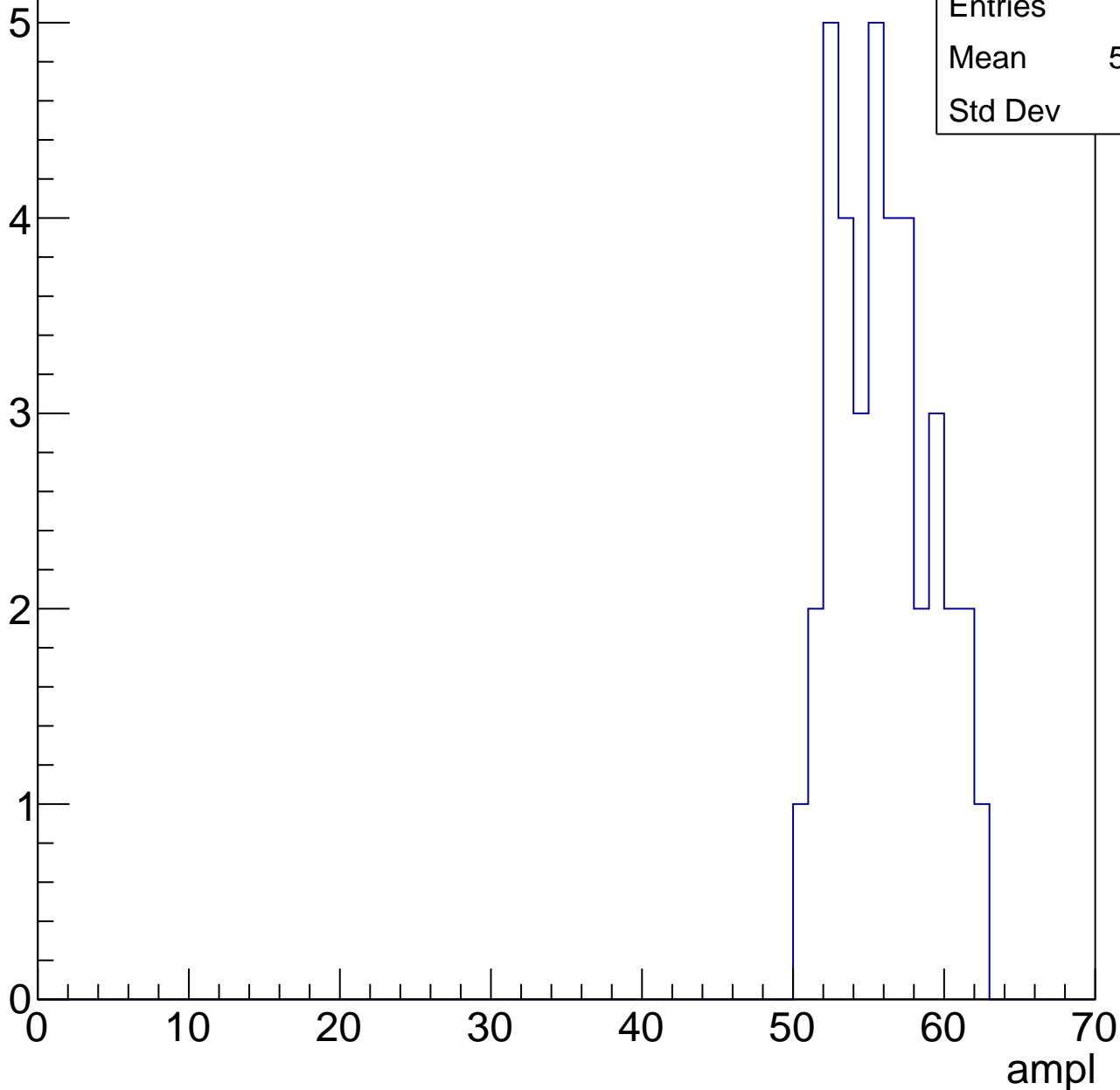


# B0L002S, U23-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	55.53
Std Dev	3.11

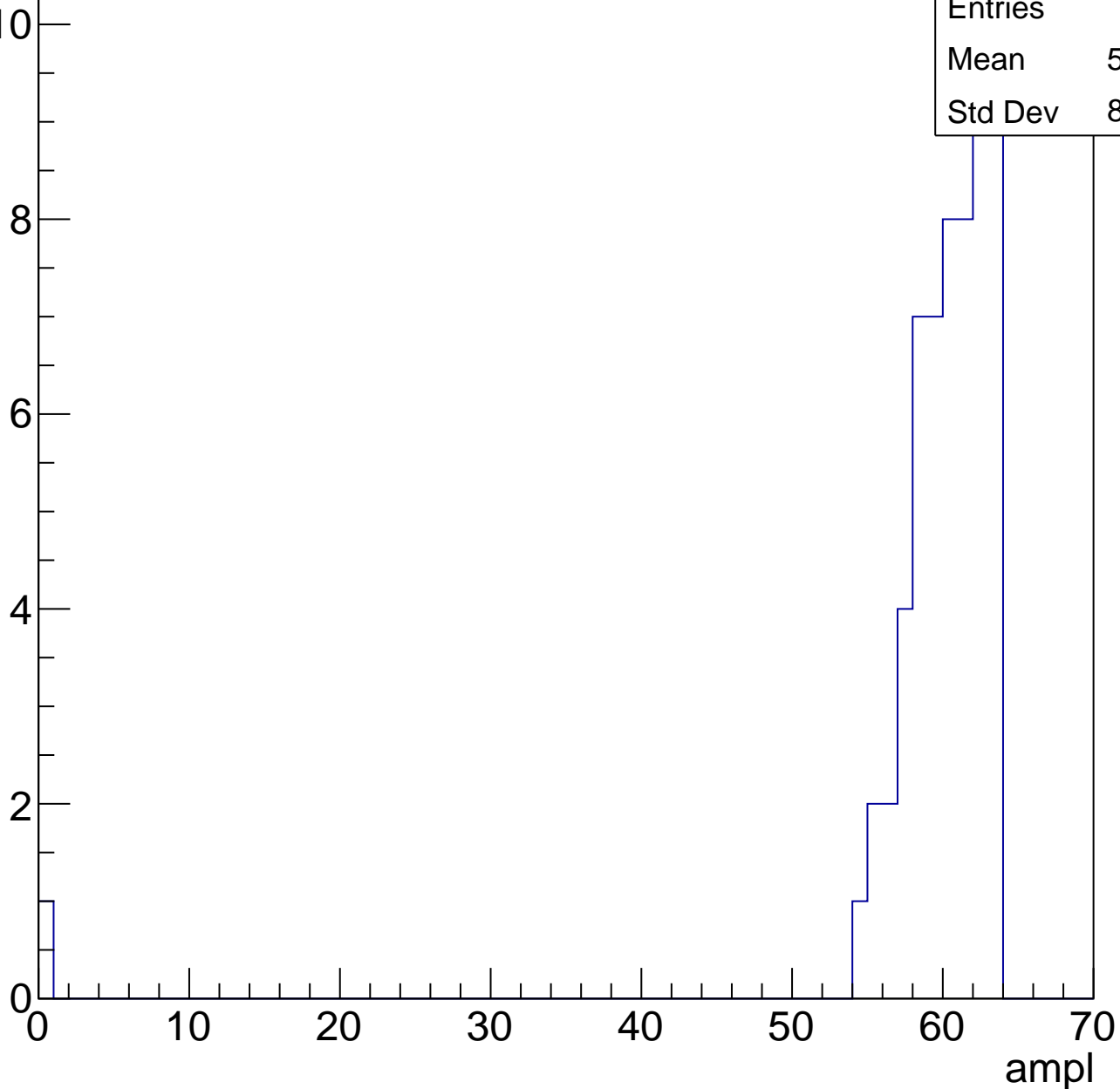


# B0L002S, U23-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	58.97
Std Dev	8.088



# B0L002S, U23-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch78, adc0

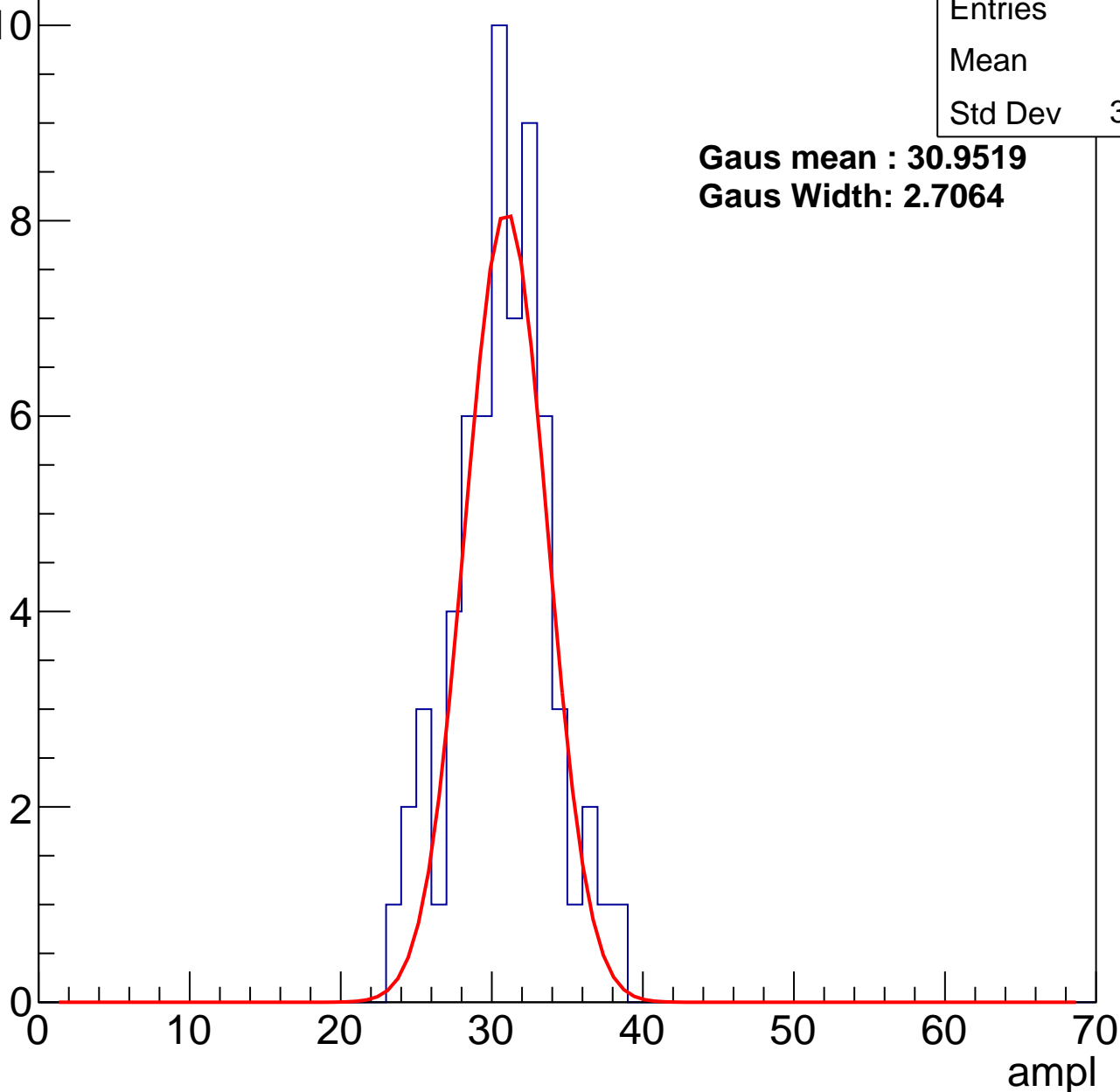
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.3
Std Dev	3.155

**Gaus mean : 30.9519**

**Gaus Width: 2.7064**



# B0L002S, U23-ch78, adc1

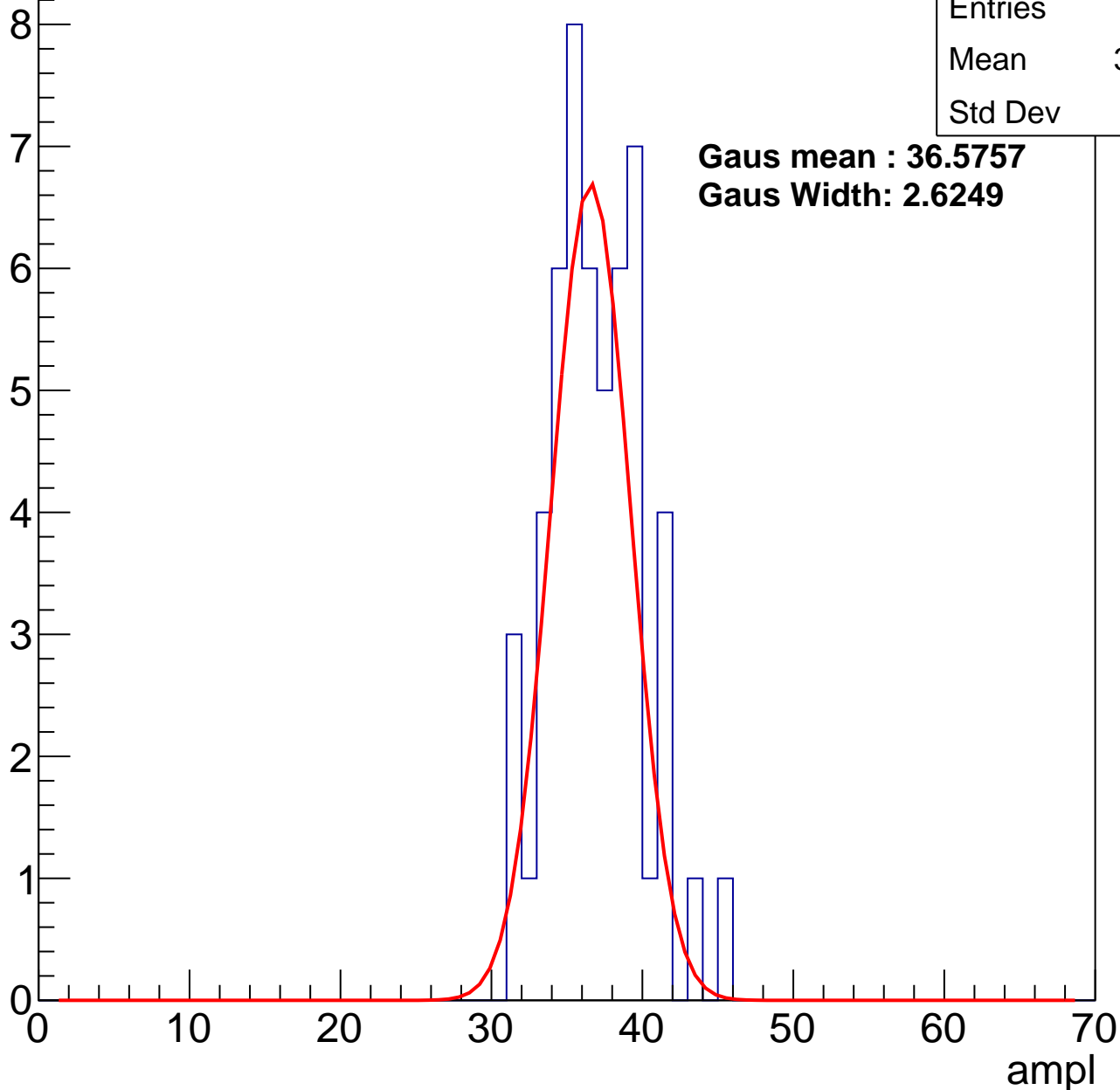
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	36.51
Std Dev	3.02

**Gaus mean : 36.5757**

**Gaus Width: 2.6249**



# B0L002S, U23-ch78, adc2

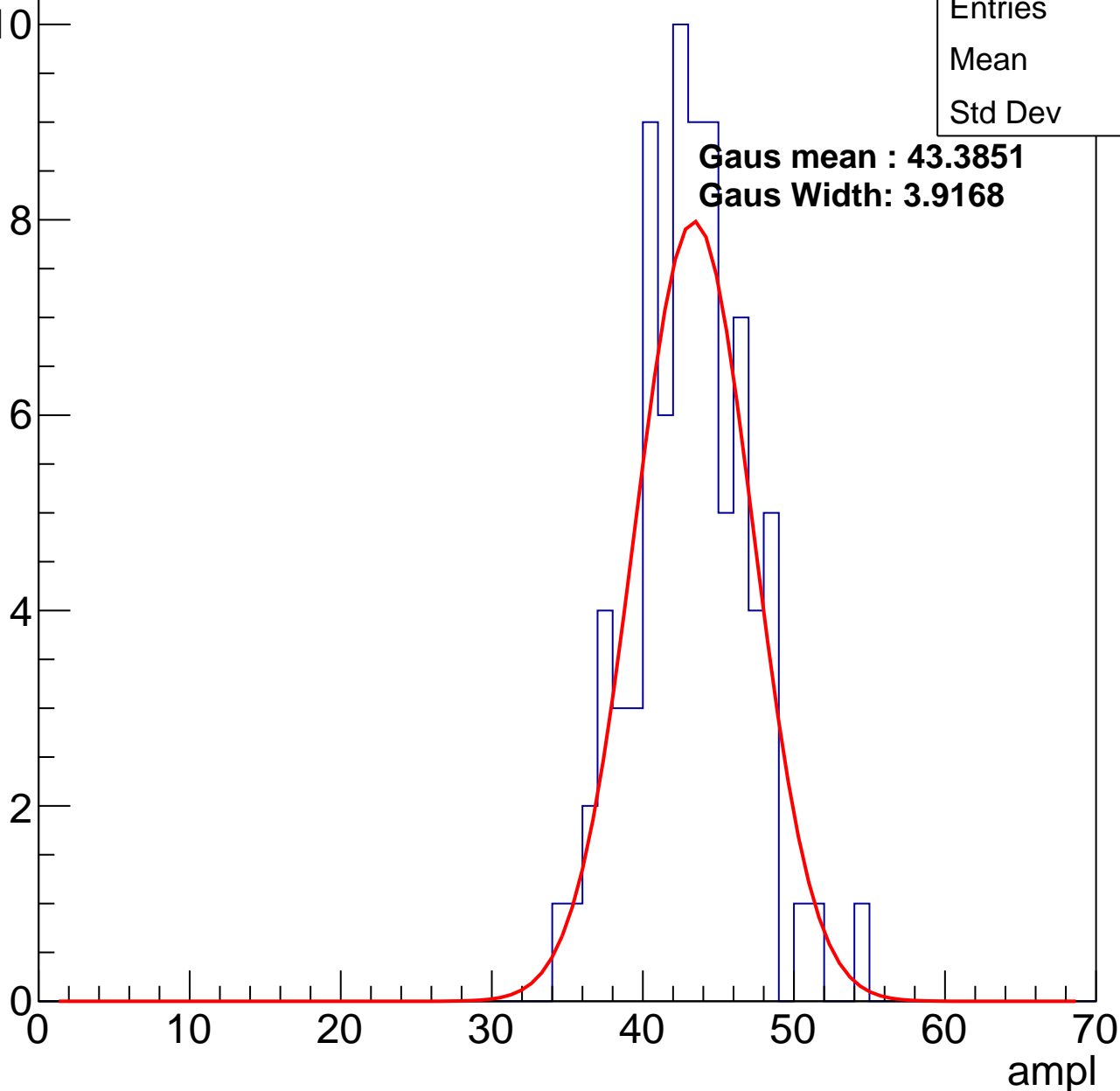
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	42.7
Std Dev	3.74

**Gaus mean : 43.3851**

**Gaus Width: 3.9168**

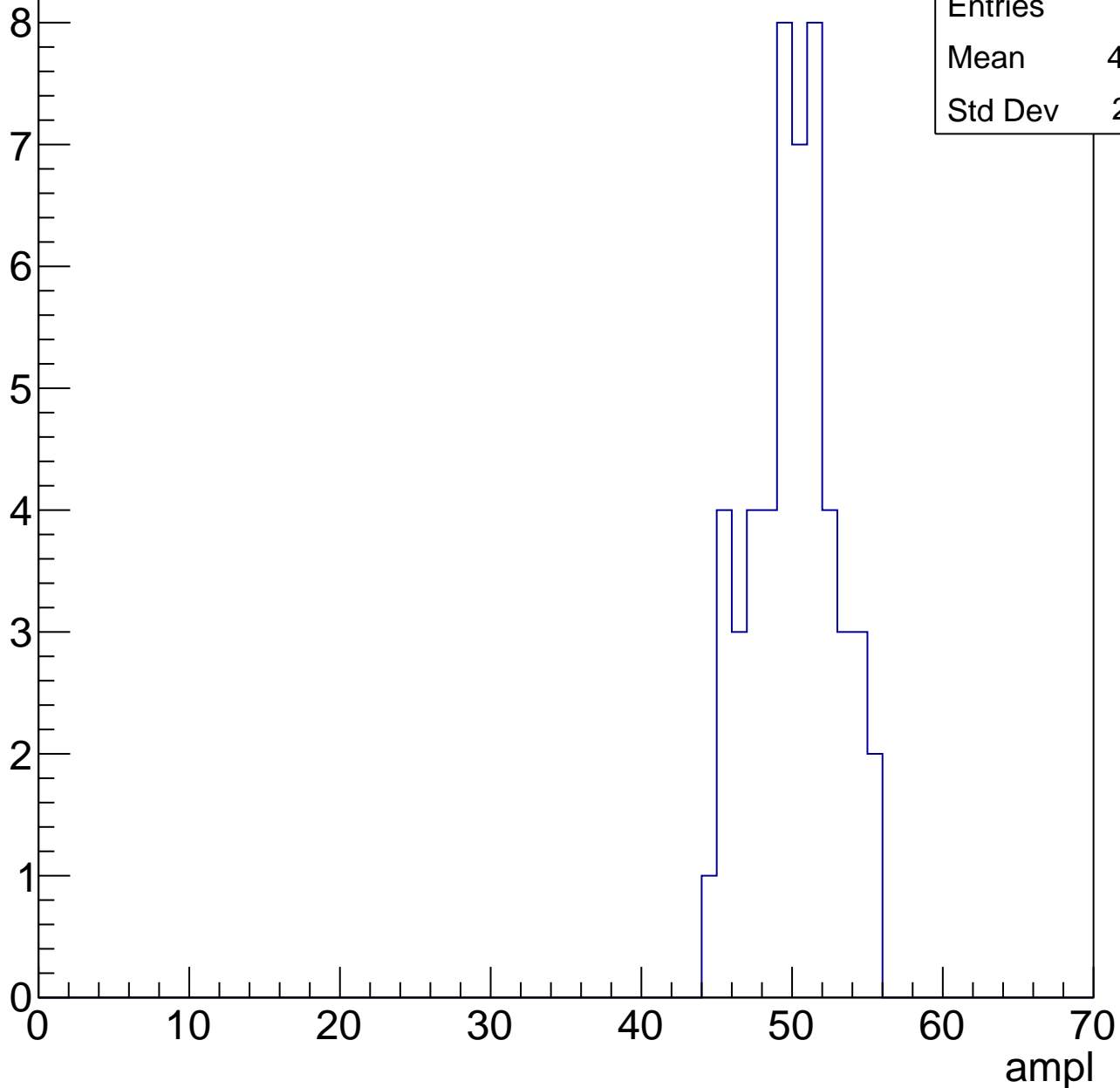


# B0L002S, U23-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	49.63
Std Dev	2.751

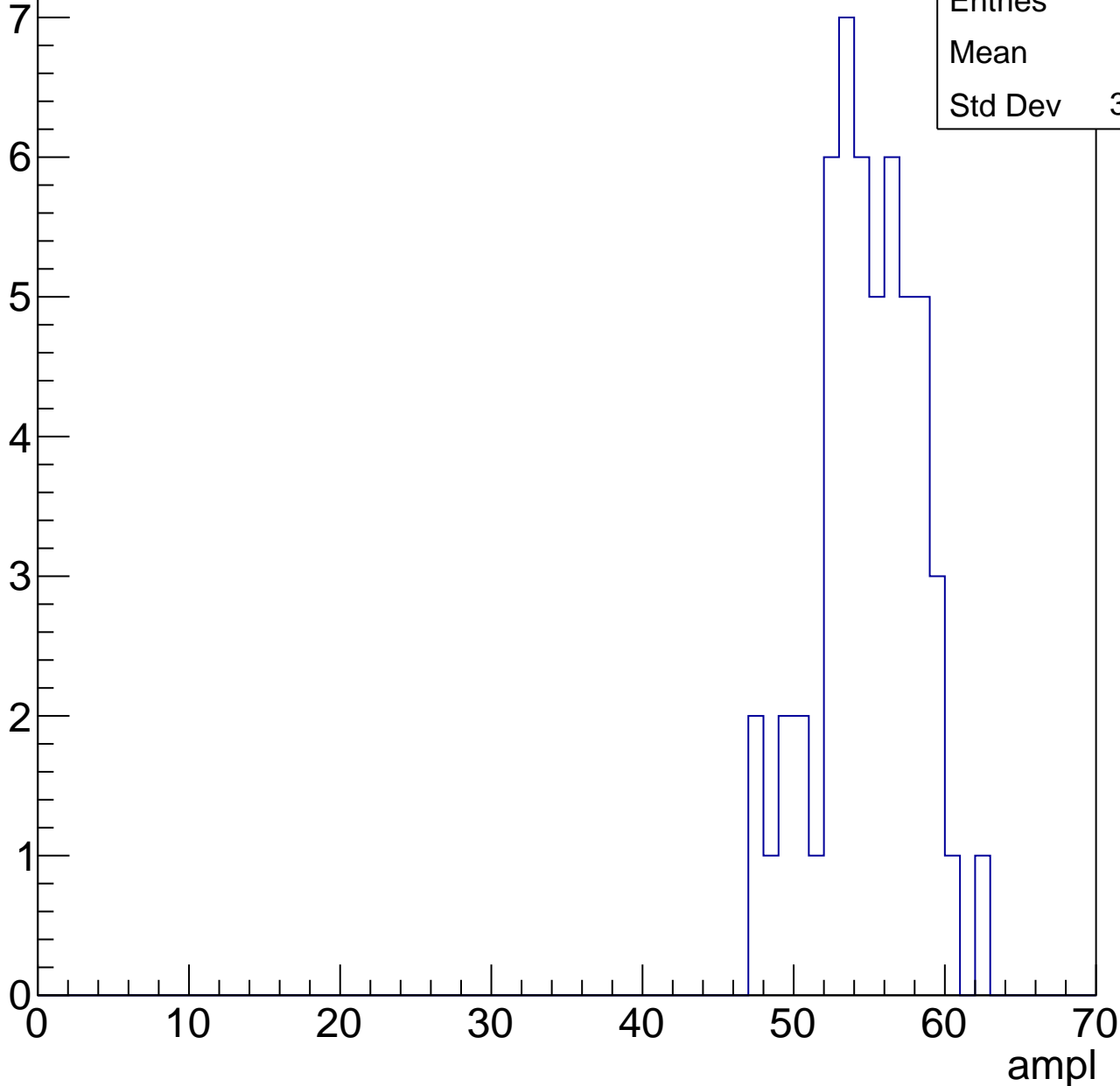


# B0L002S, U23-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	54.4
Std Dev	3.299

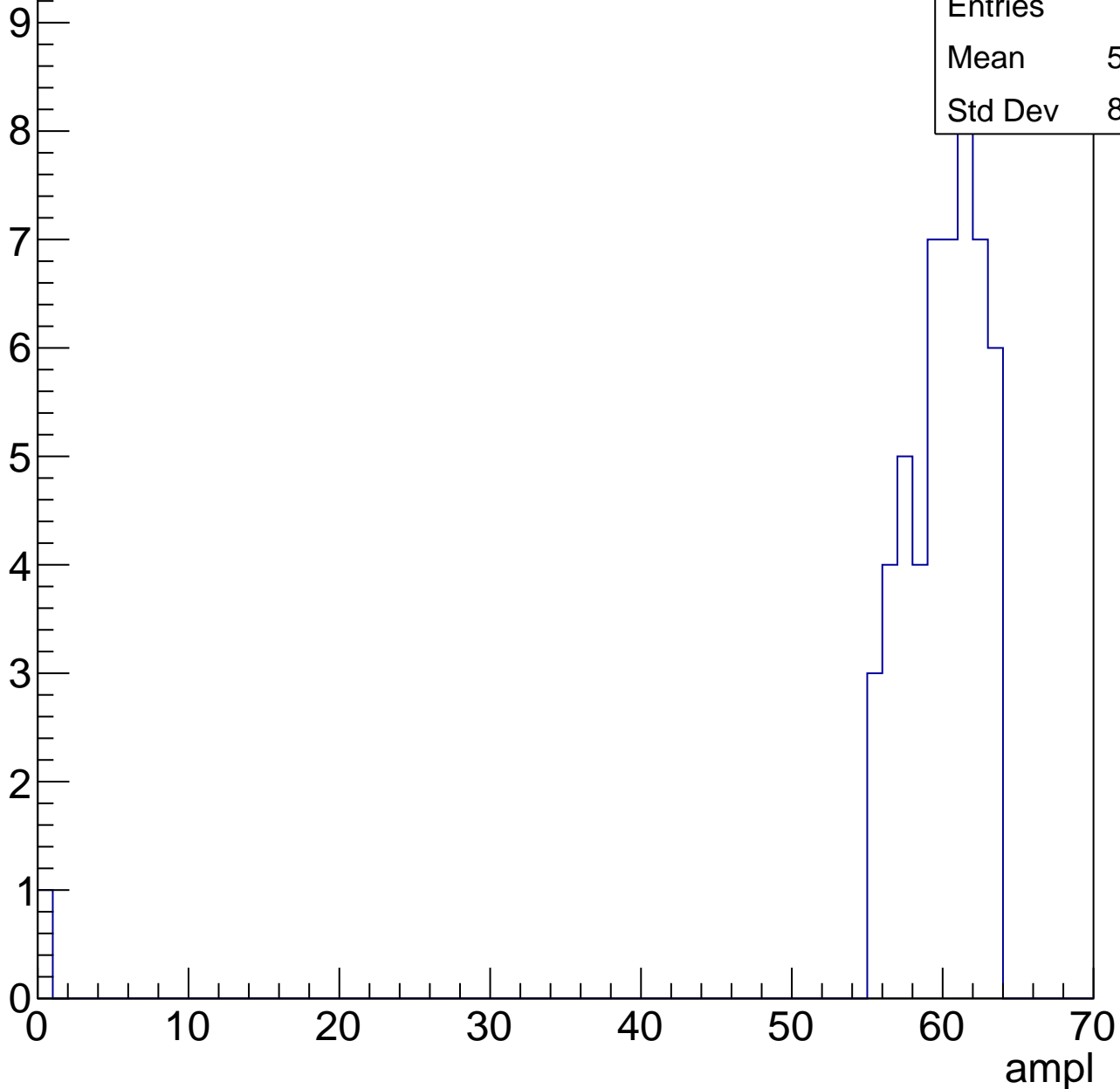


# B0L002S, U23-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

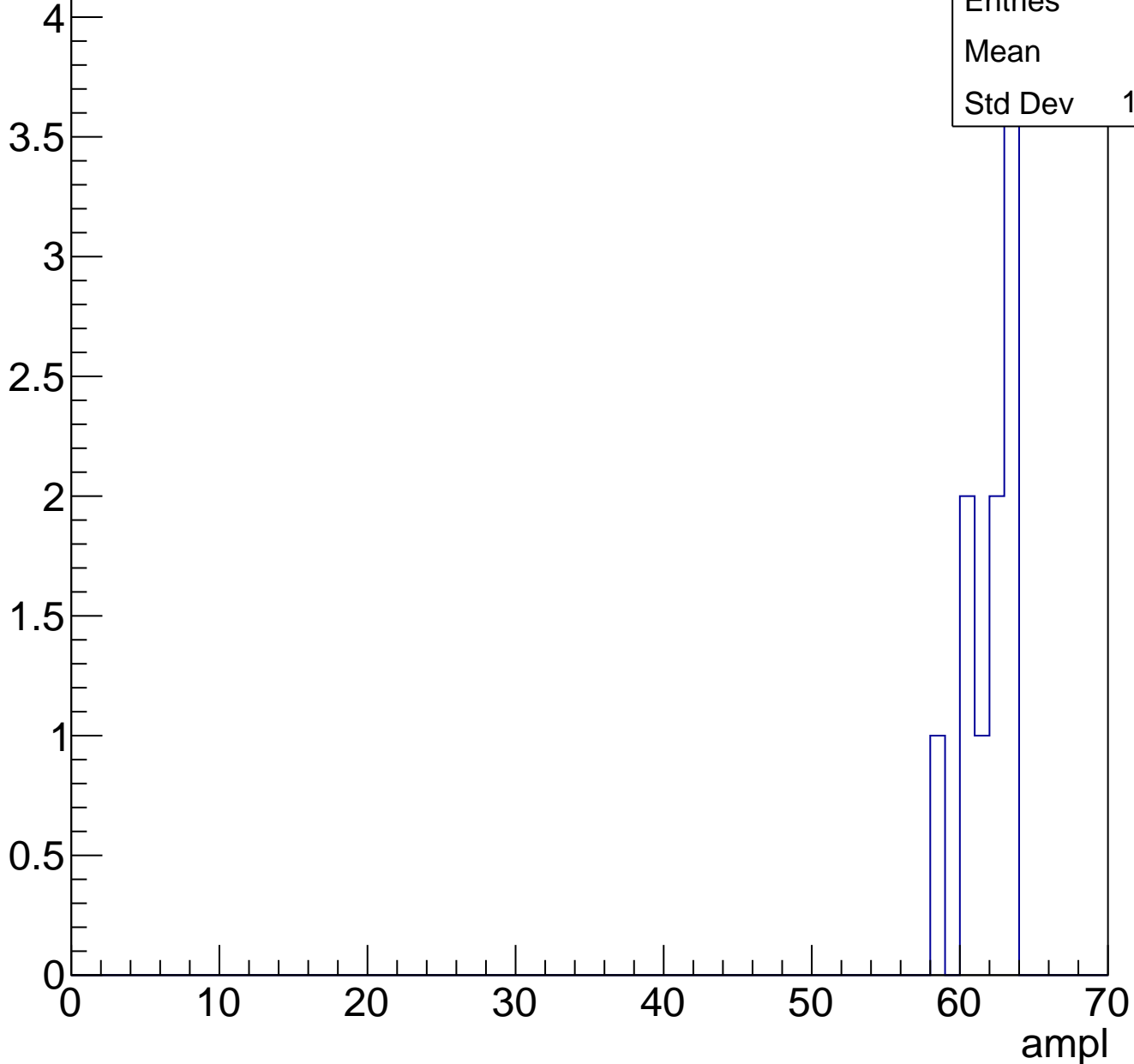
Entries	53
Mean	58.49
Std Dev	8.442



# B0L002S, U23-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L002S, U23-ch79, adc0

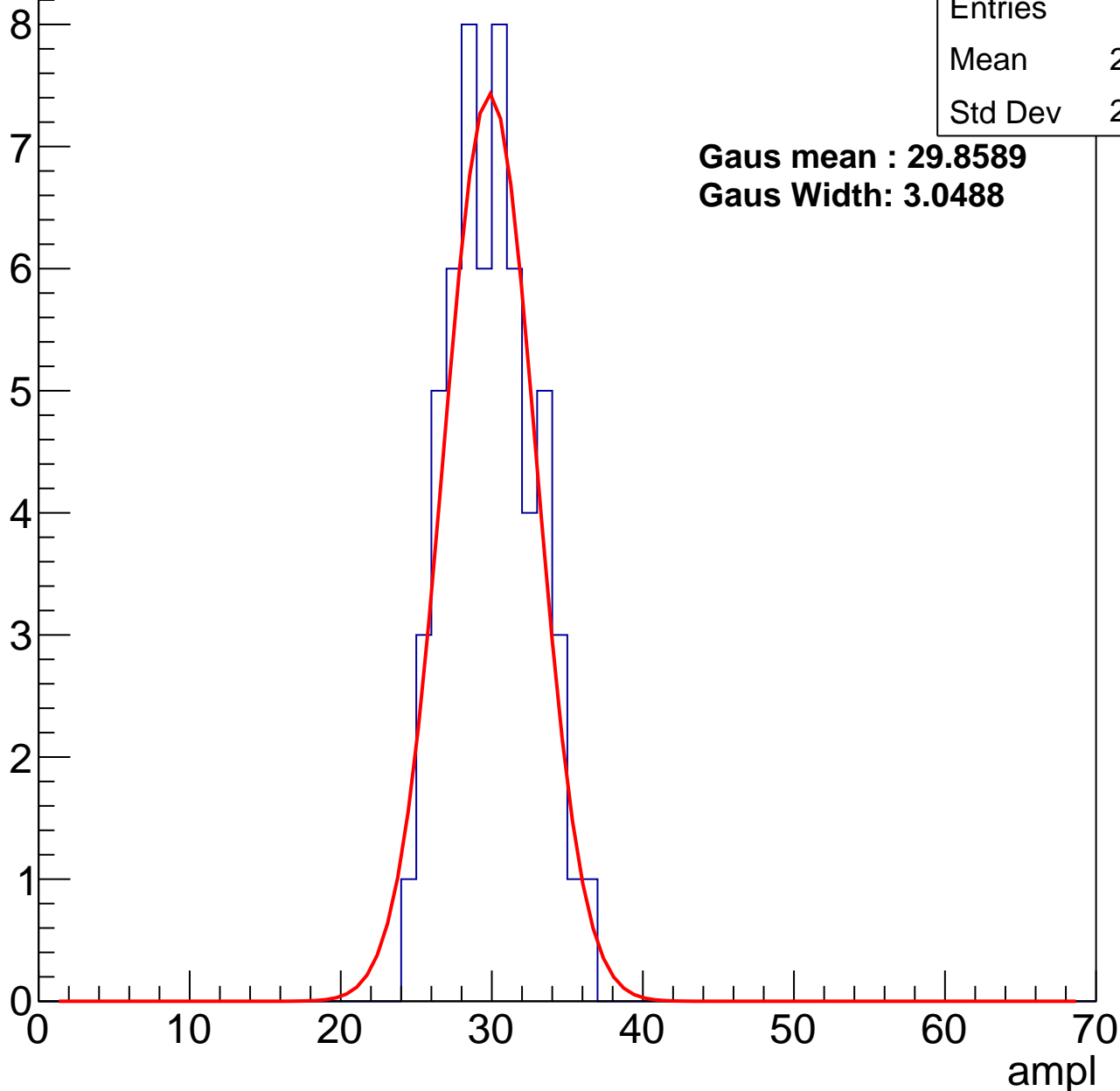
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.49
Std Dev	2.792

**Gaus mean : 29.8589**

**Gaus Width: 3.0488**



# B0L002S, U23-ch79, adc1

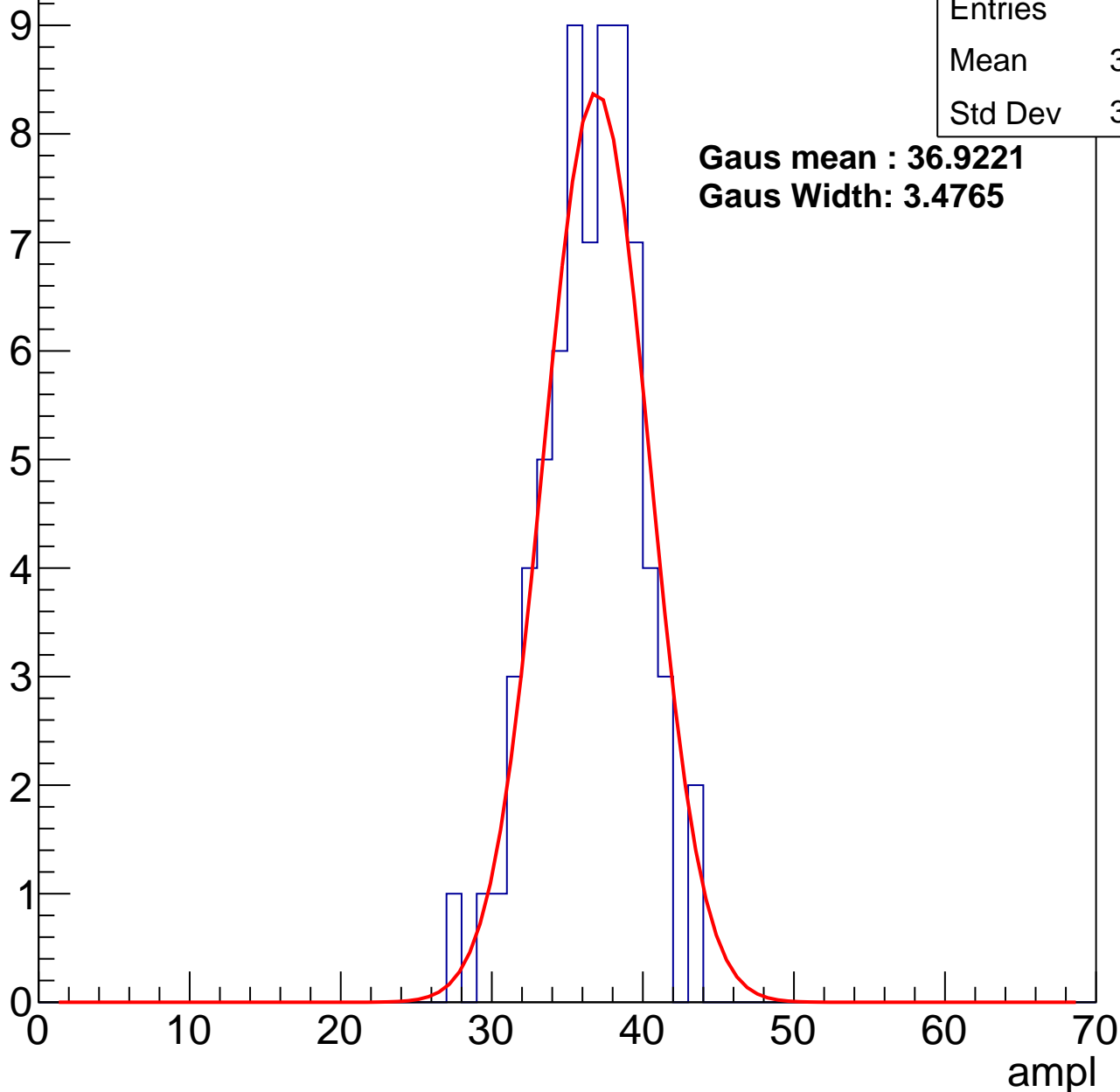
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.06
Std Dev	3.202

**Gaus mean : 36.9221**

**Gaus Width: 3.4765**



# B0L002S, U23-ch79, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	43.6
Std Dev	3.406

**Gaus mean : 44.2554**

**Gaus Width: 3.3231**

Entry

10

8

6

4

2

0

0

10

20

30

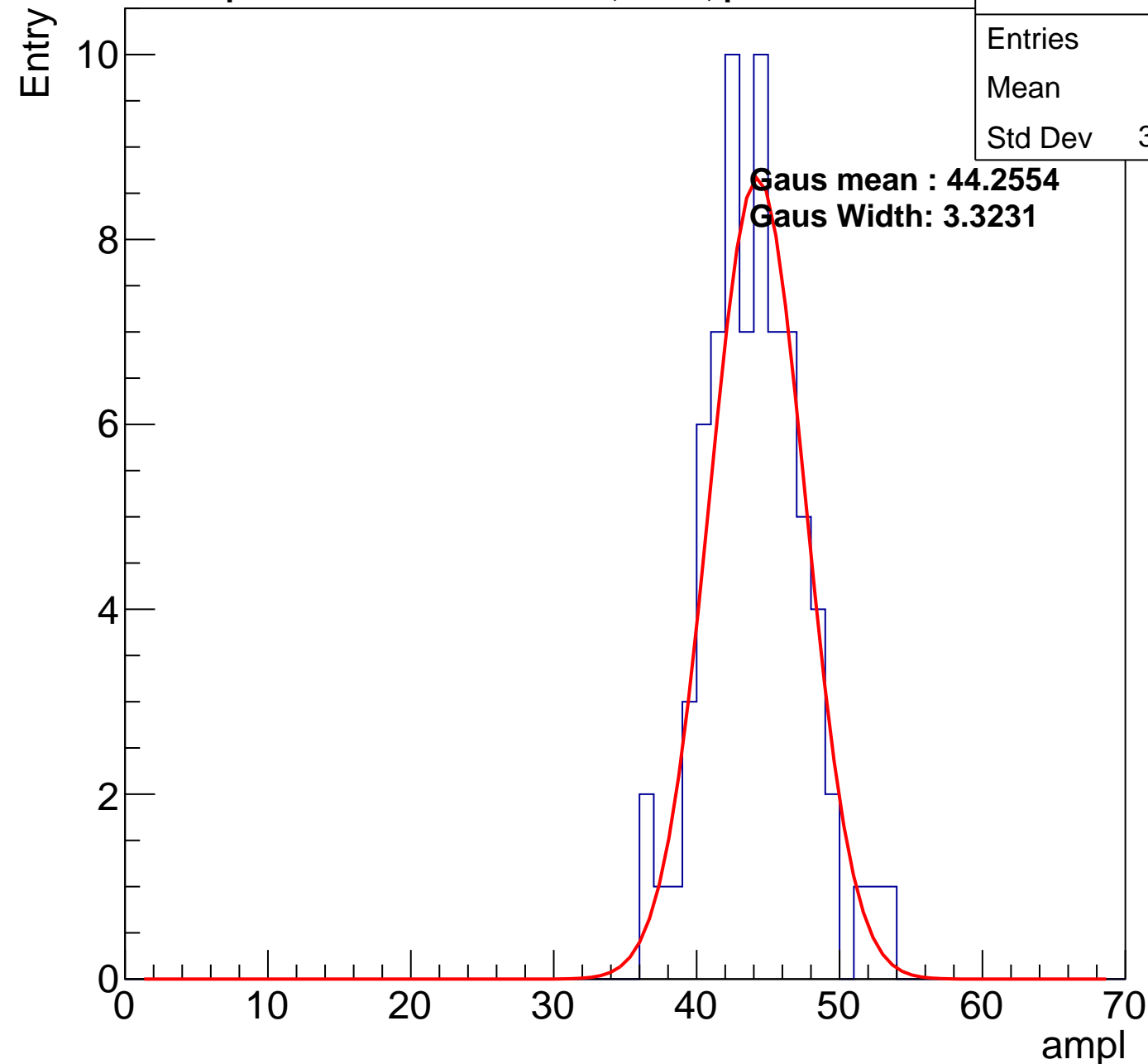
40

50

60

70

ampl

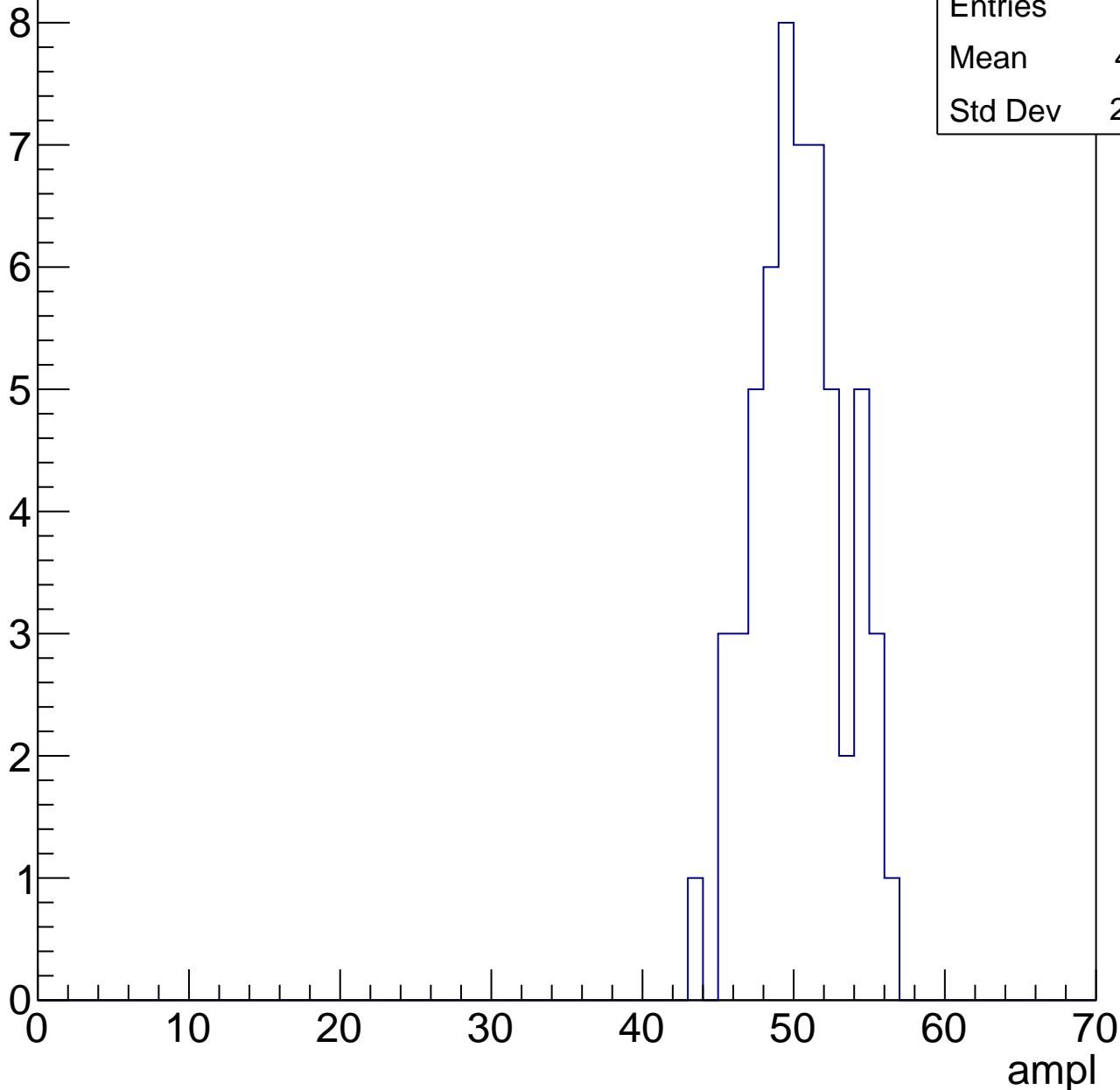


# B0L002S, U23-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

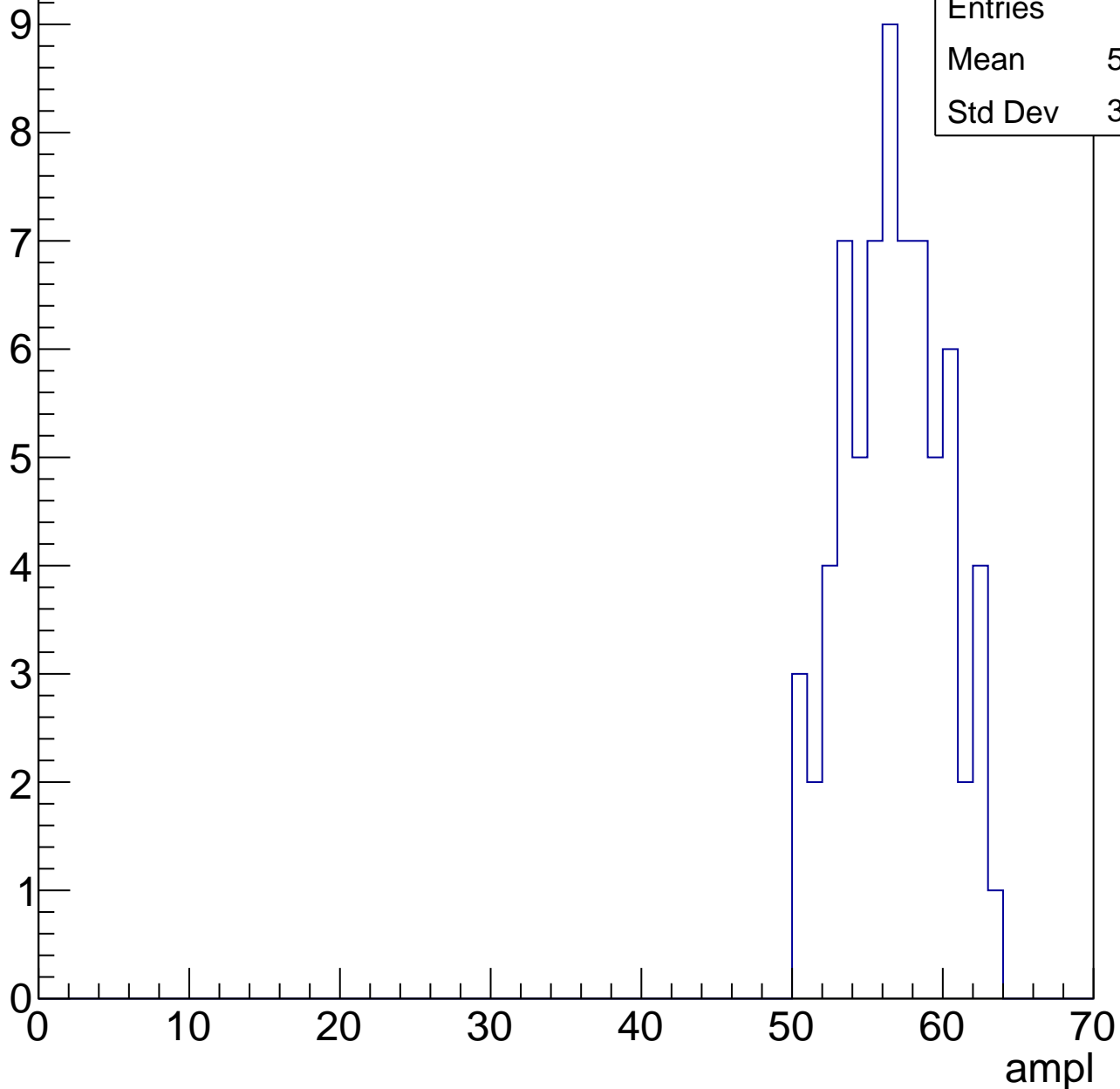
Entries	56
Mean	49.91
Std Dev	2.942



# B0L002S, U23-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

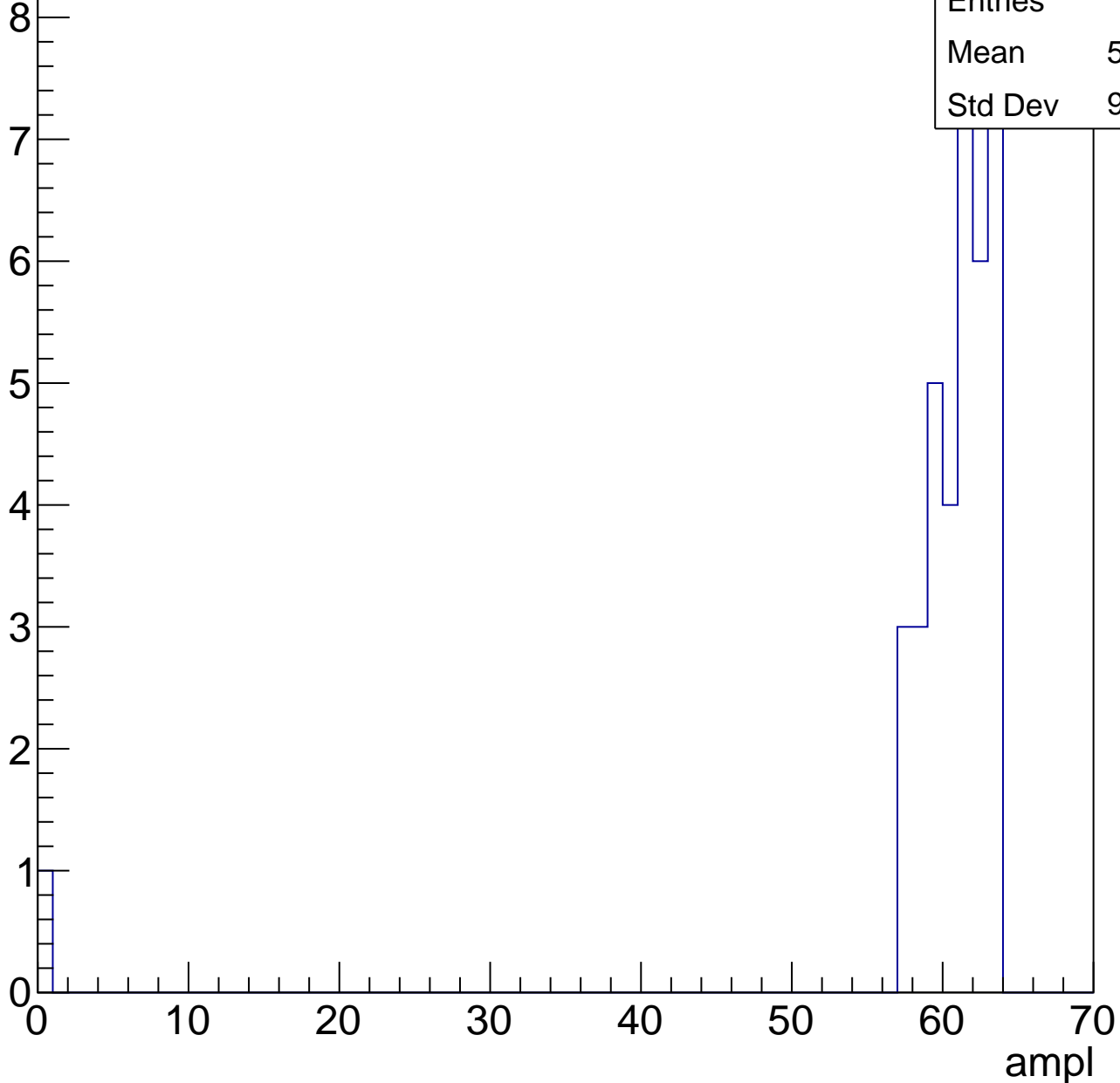


# B0L002S, U23-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.05
Std Dev	9.886



# B0L002S, U23-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

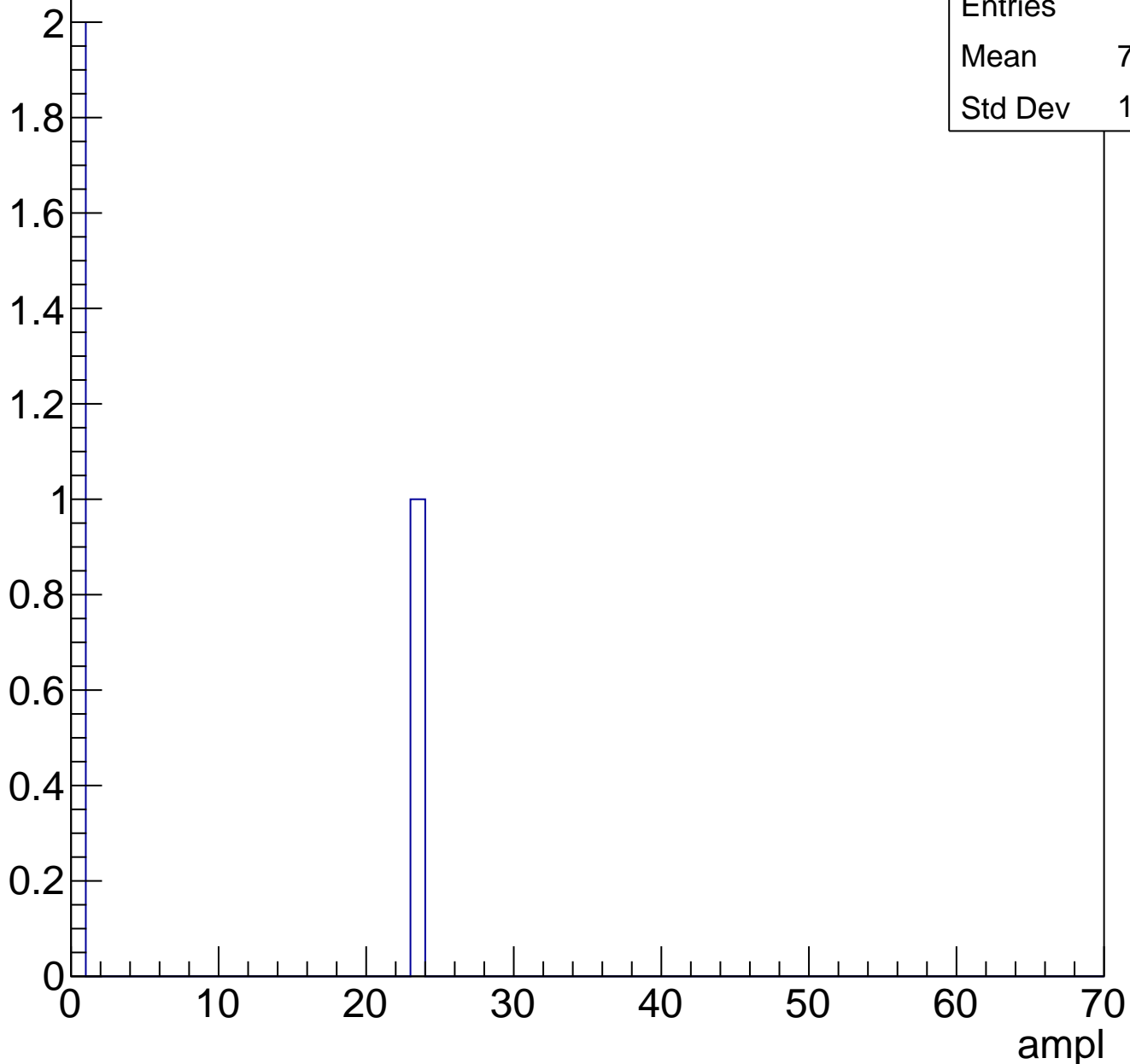




# B0L002S, U23-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L002S, U23-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	68
Mean	28.06
Std Dev	6.629

**Gaus mean : 30.5012**

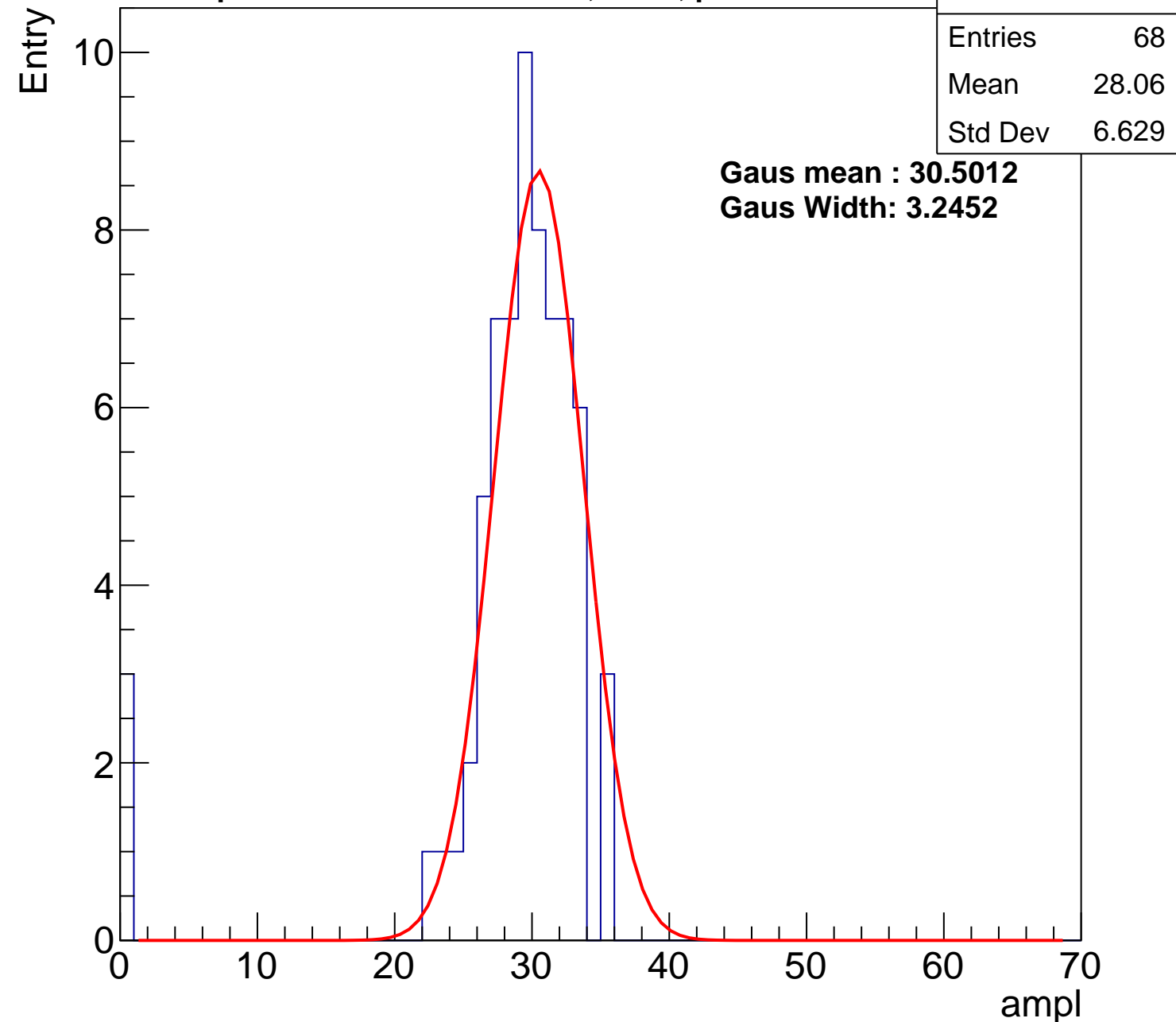
**Gaus Width: 3.2452**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	61
Mean	35.54
Std Dev	2.94

**Gaus mean : 36.0972**

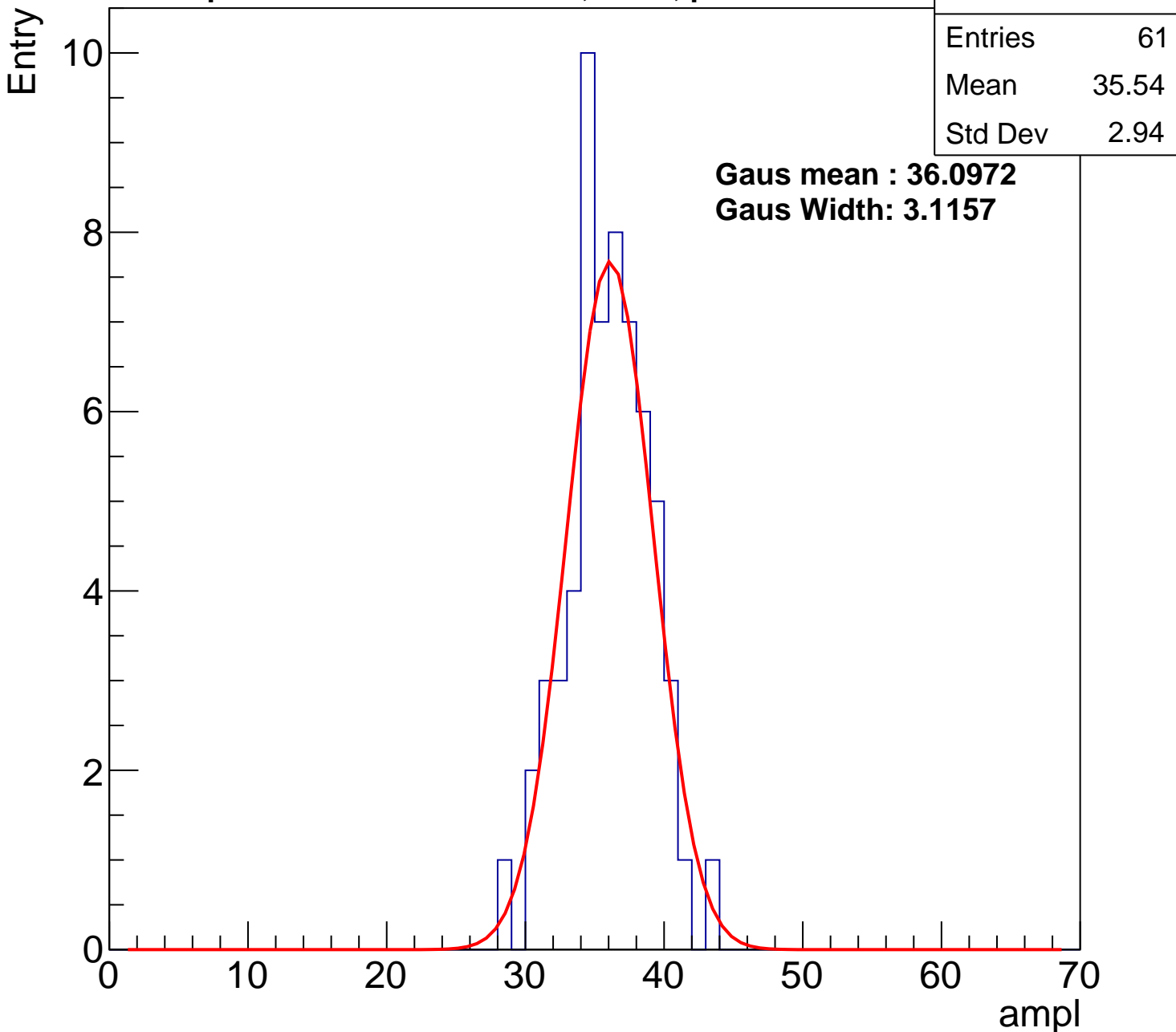
**Gaus Width: 3.1157**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch80, adc2

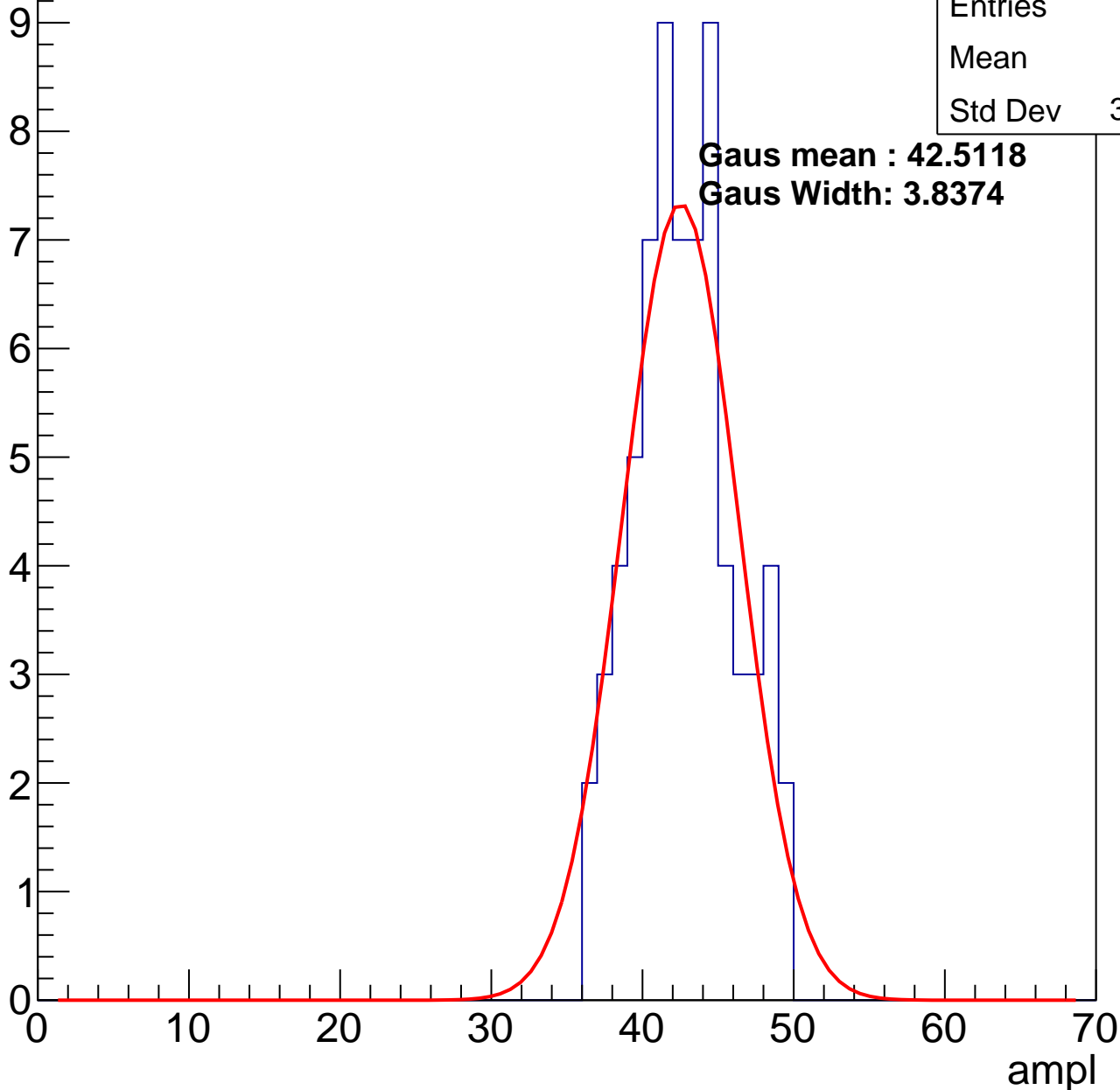
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	42.3
Std Dev	3.254

**Gaus mean : 42.5118**

**Gaus Width: 3.8374**

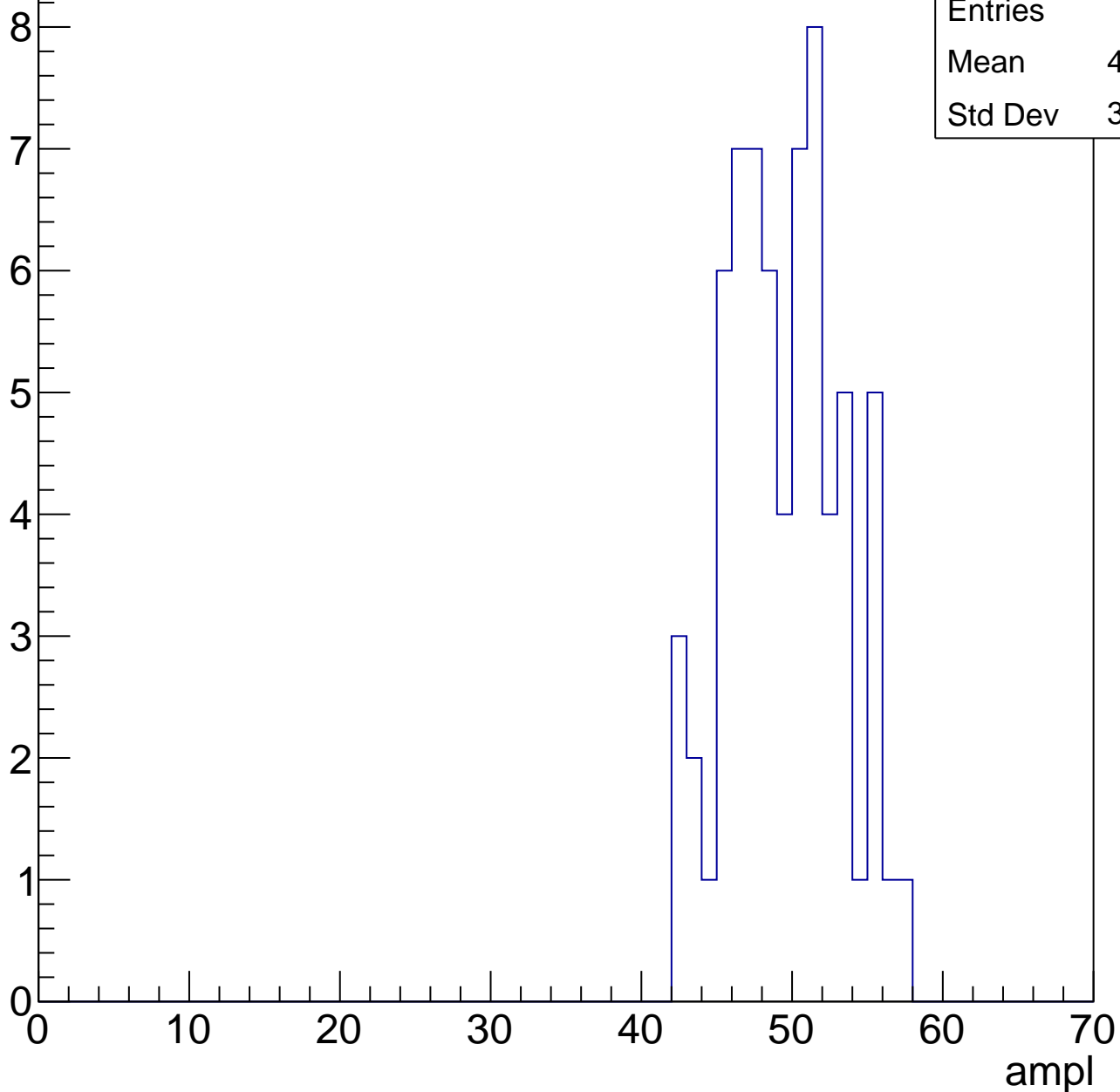


# B0L002S, U23-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	49.03
Std Dev	3.658

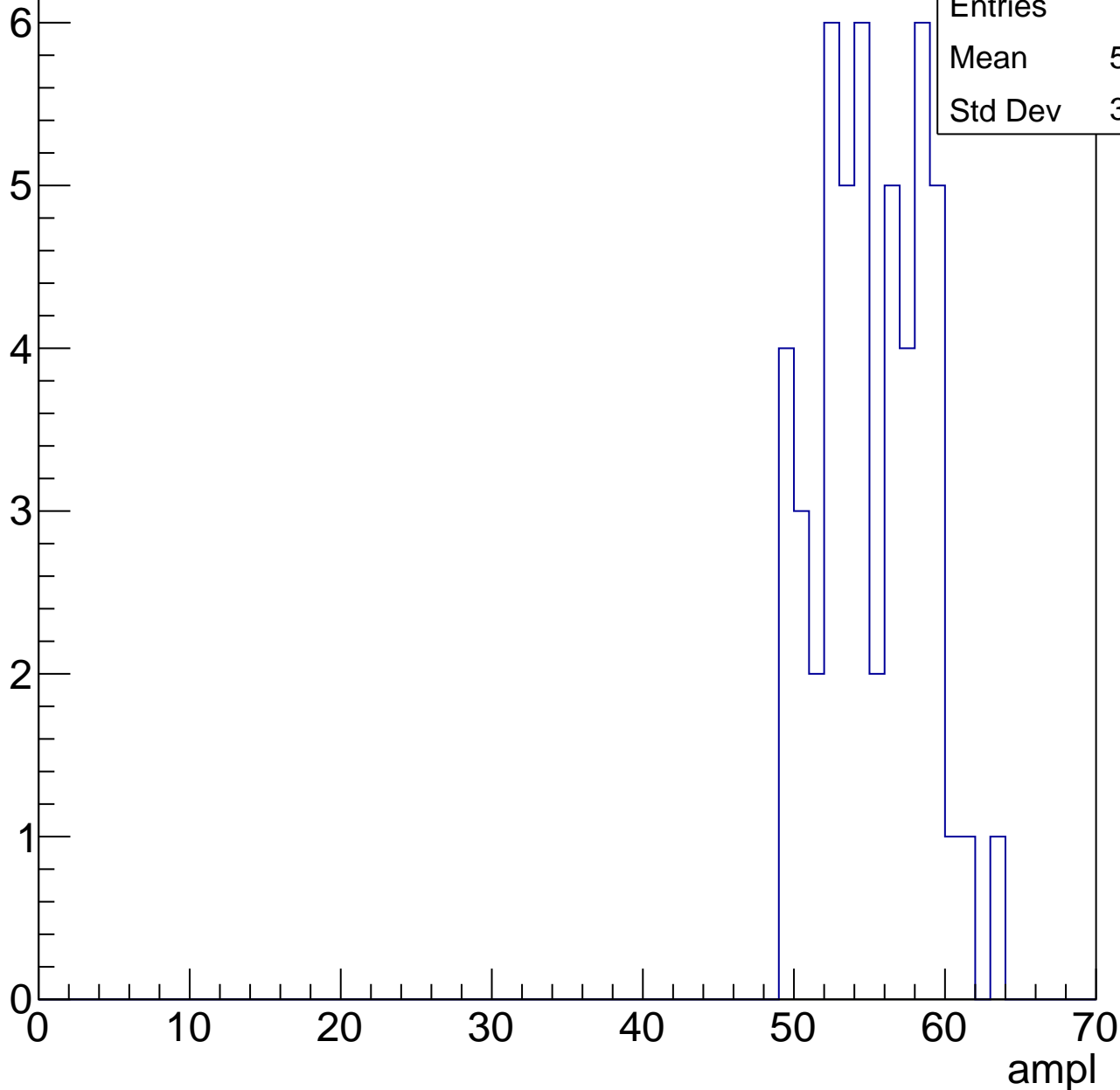


# B0L002S, U23-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	54.78
Std Dev	3.455

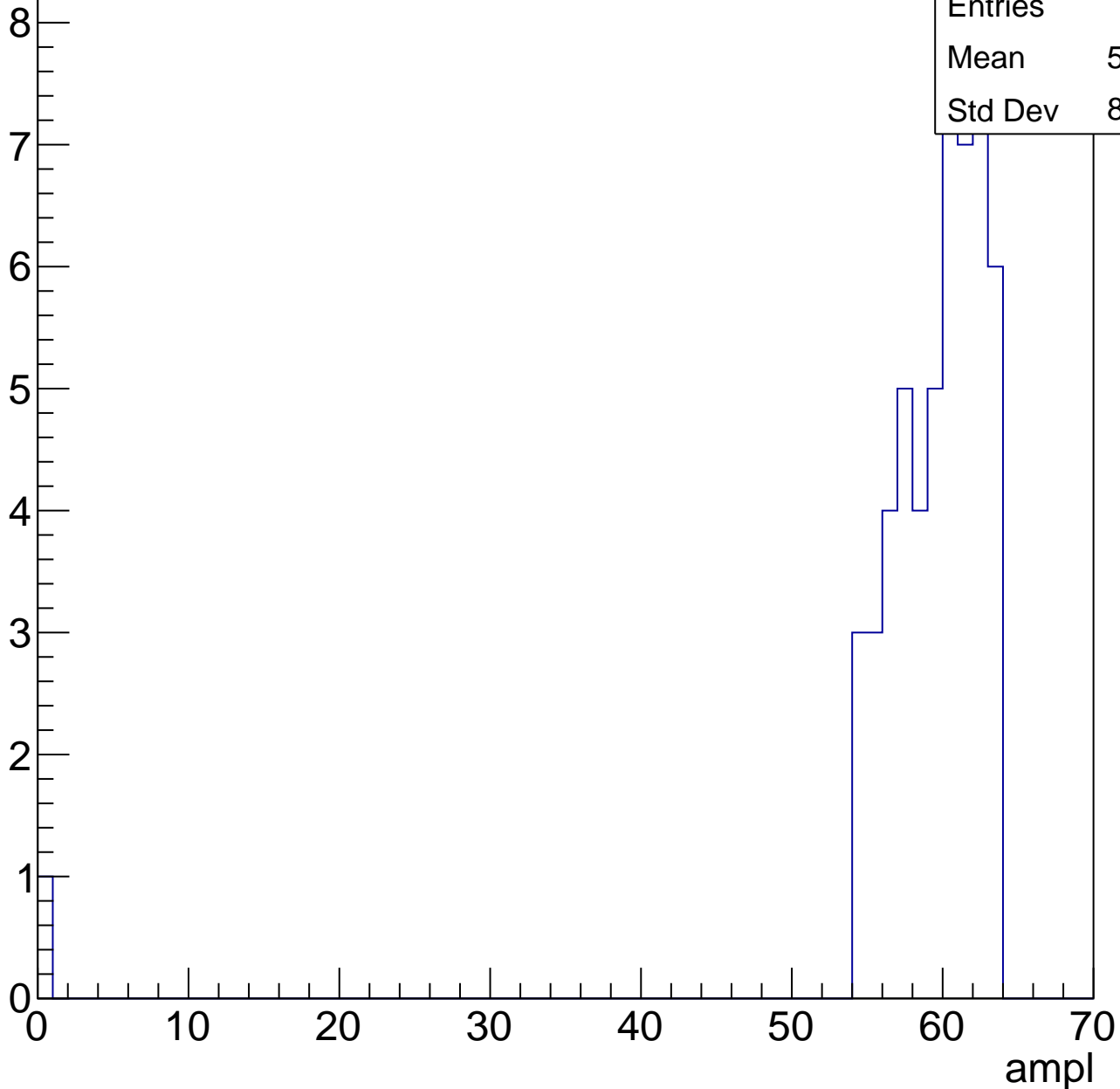


# B0L002S, U23-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.22
Std Dev	8.428



# B0L002S, U23-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

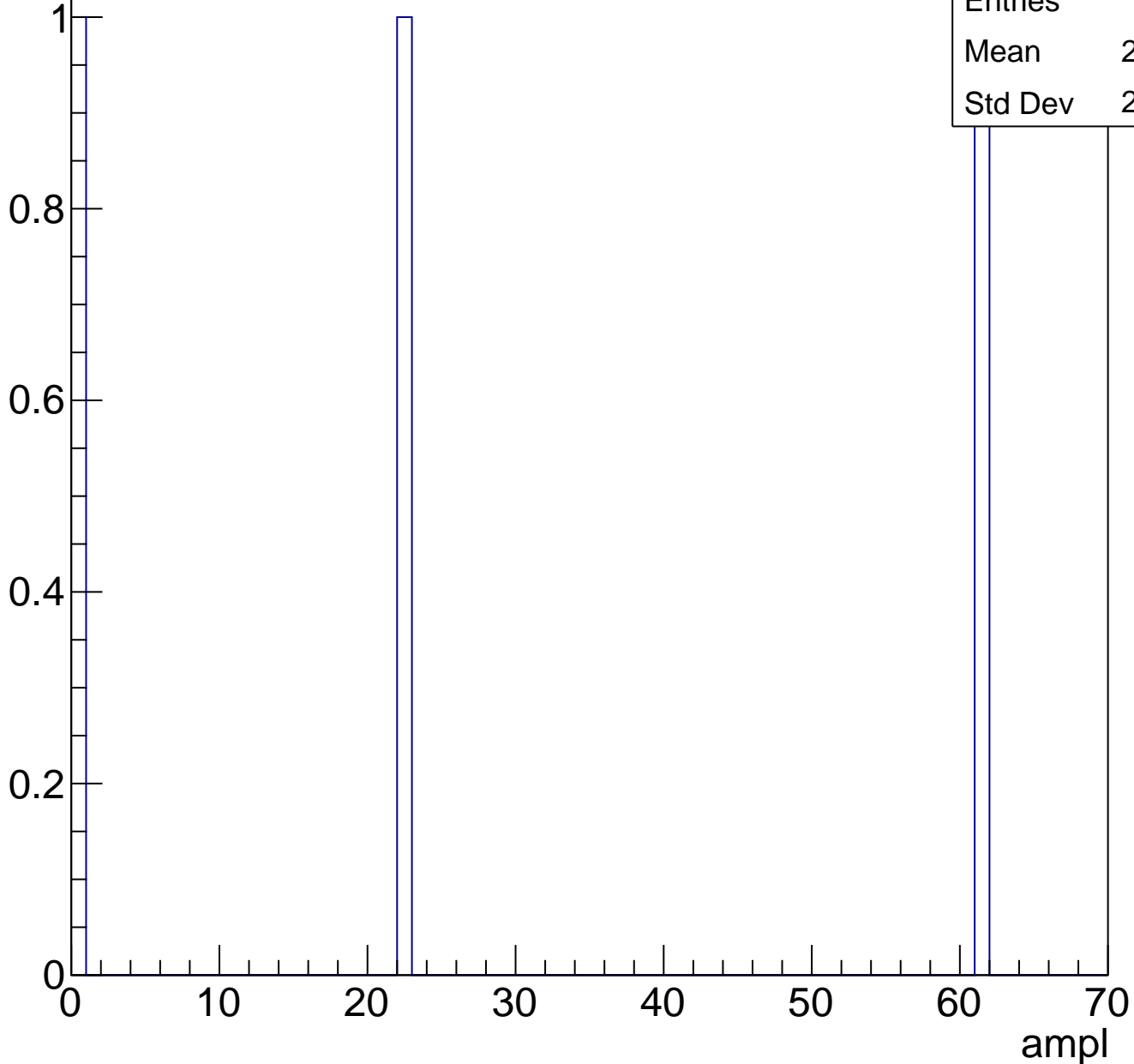




# B0L002S, U23-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	27.67
Std Dev	25.22

# B0L002S, U23-ch81, adc0

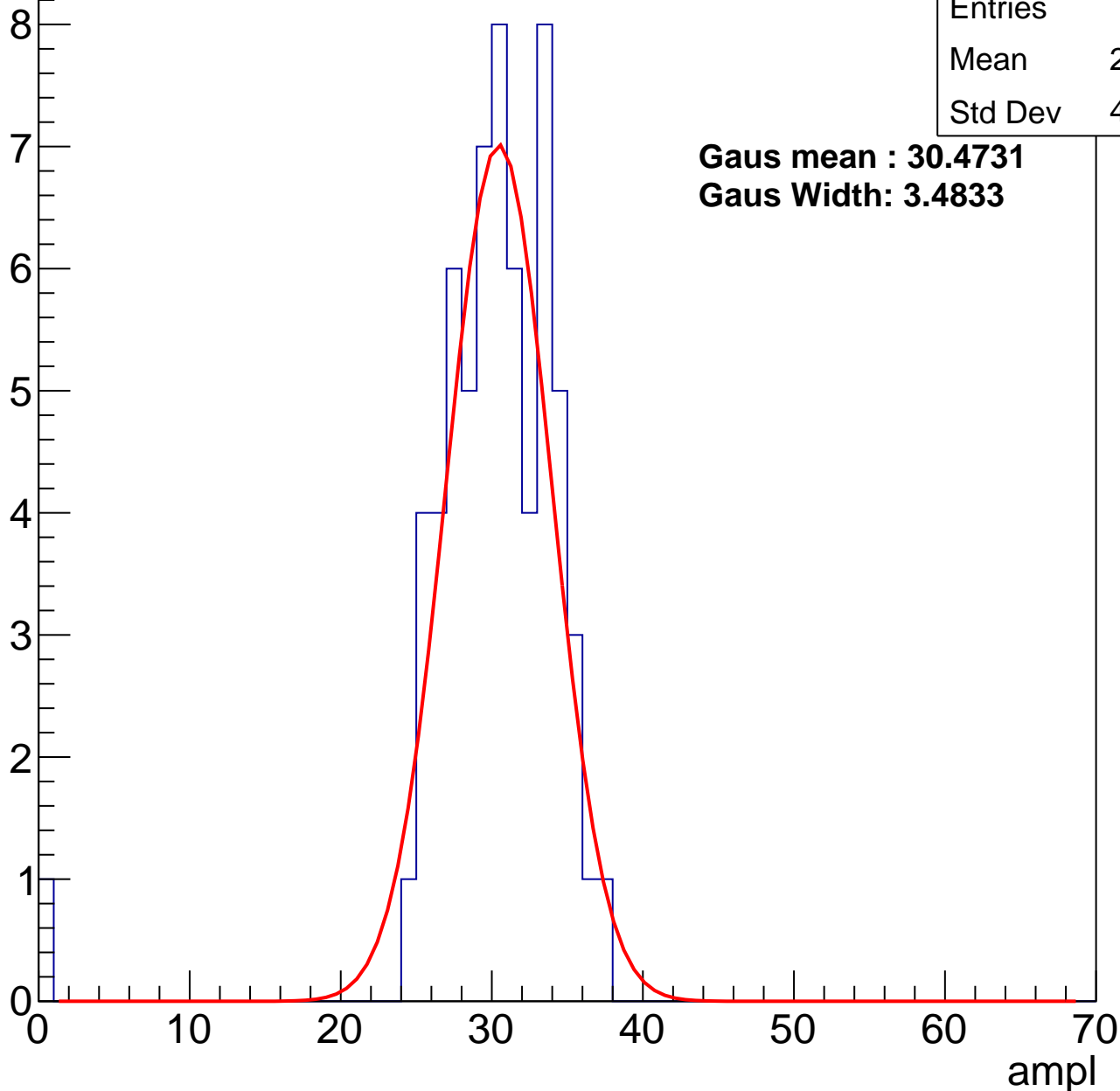
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.67
Std Dev	4.854

**Gaus mean : 30.4731**

**Gaus Width: 3.4833**



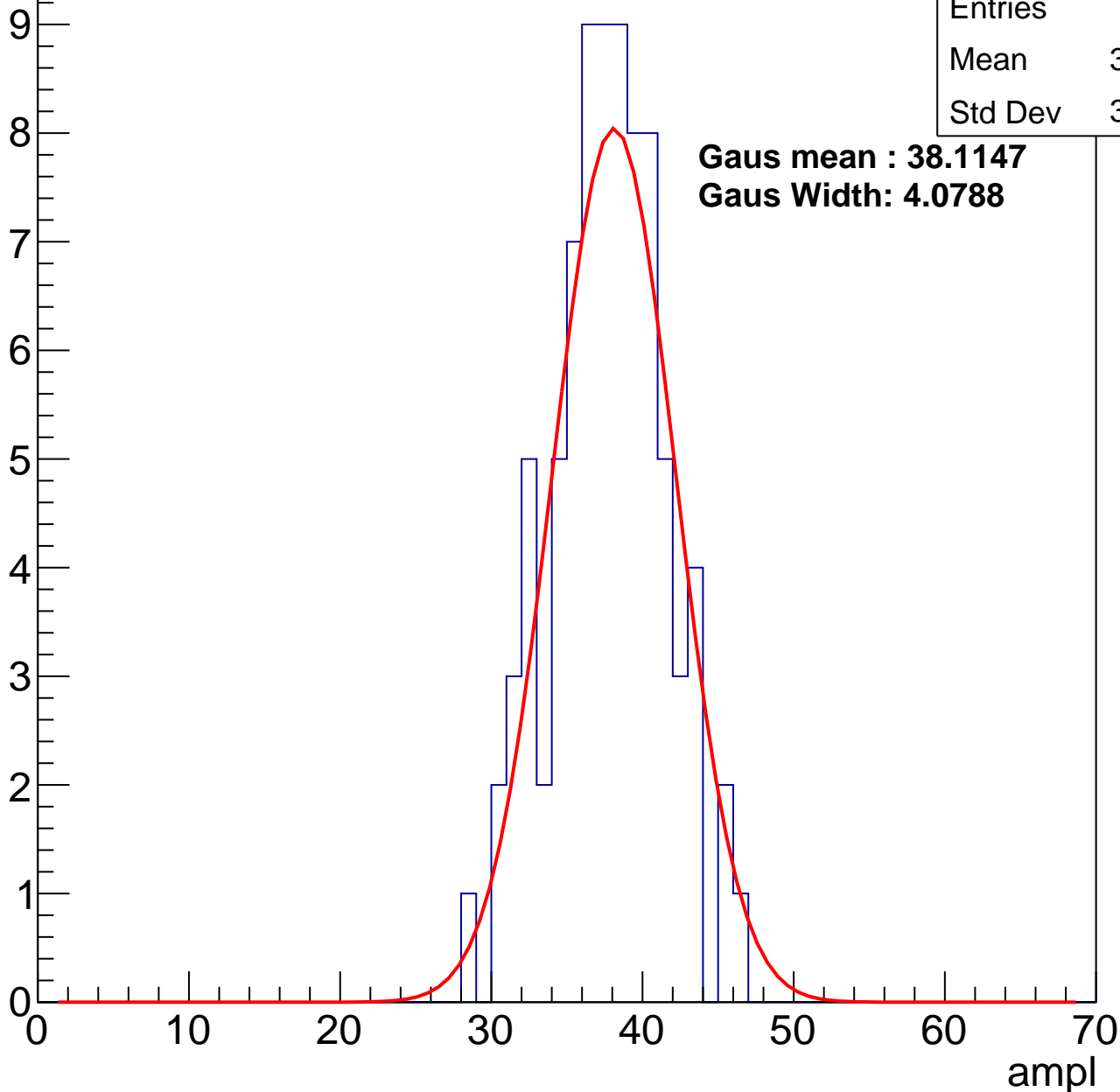
# B0L002S, U23-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	83
Mean	37.25
Std Dev	3.709

**Gaus mean : 38.1147**  
**Gaus Width: 4.0788**



# B0L002S, U23-ch81, adc2

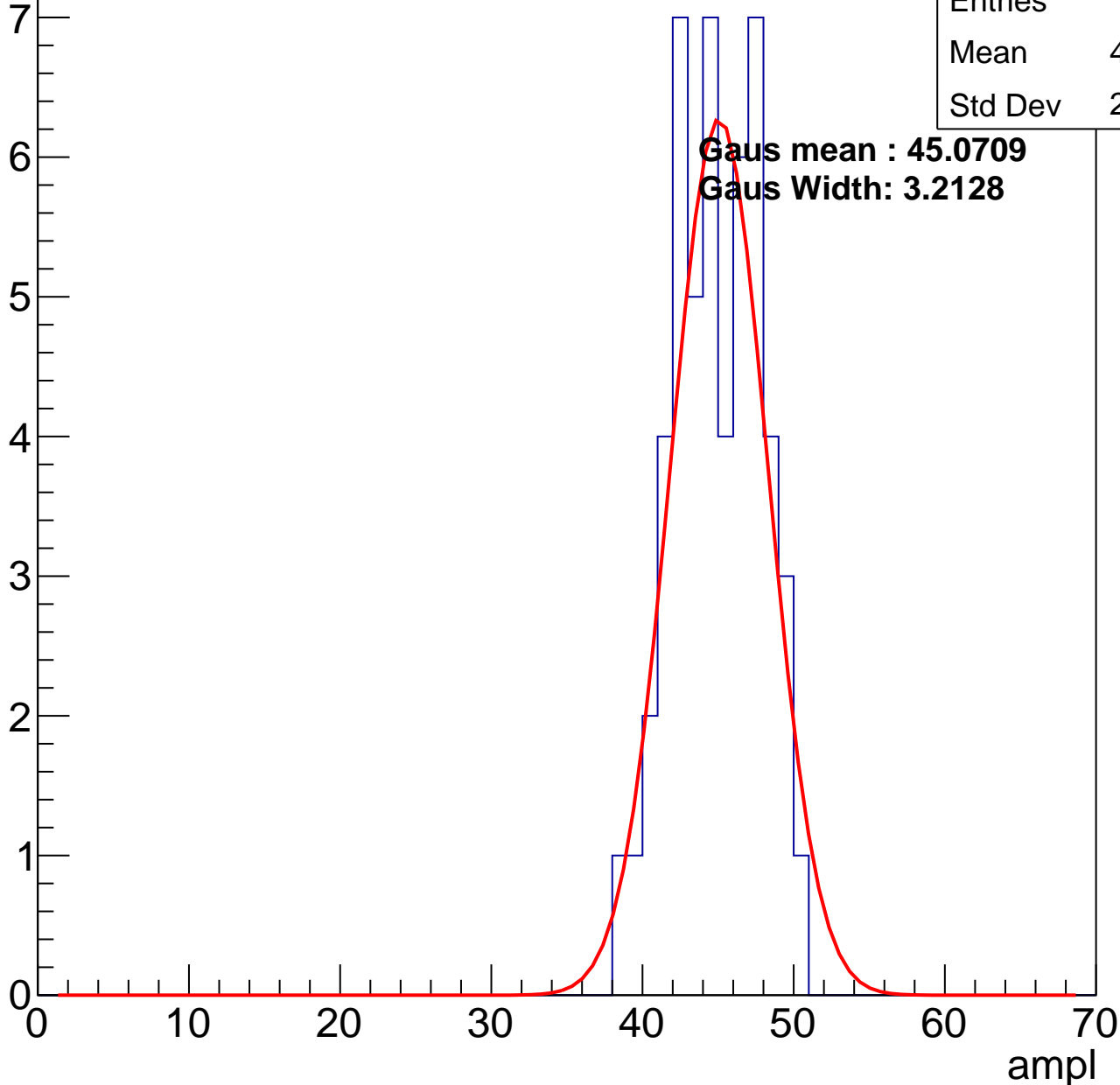
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	44.46
Std Dev	2.832

**Gaus mean : 45.0709**

**Gaus Width: 3.2128**

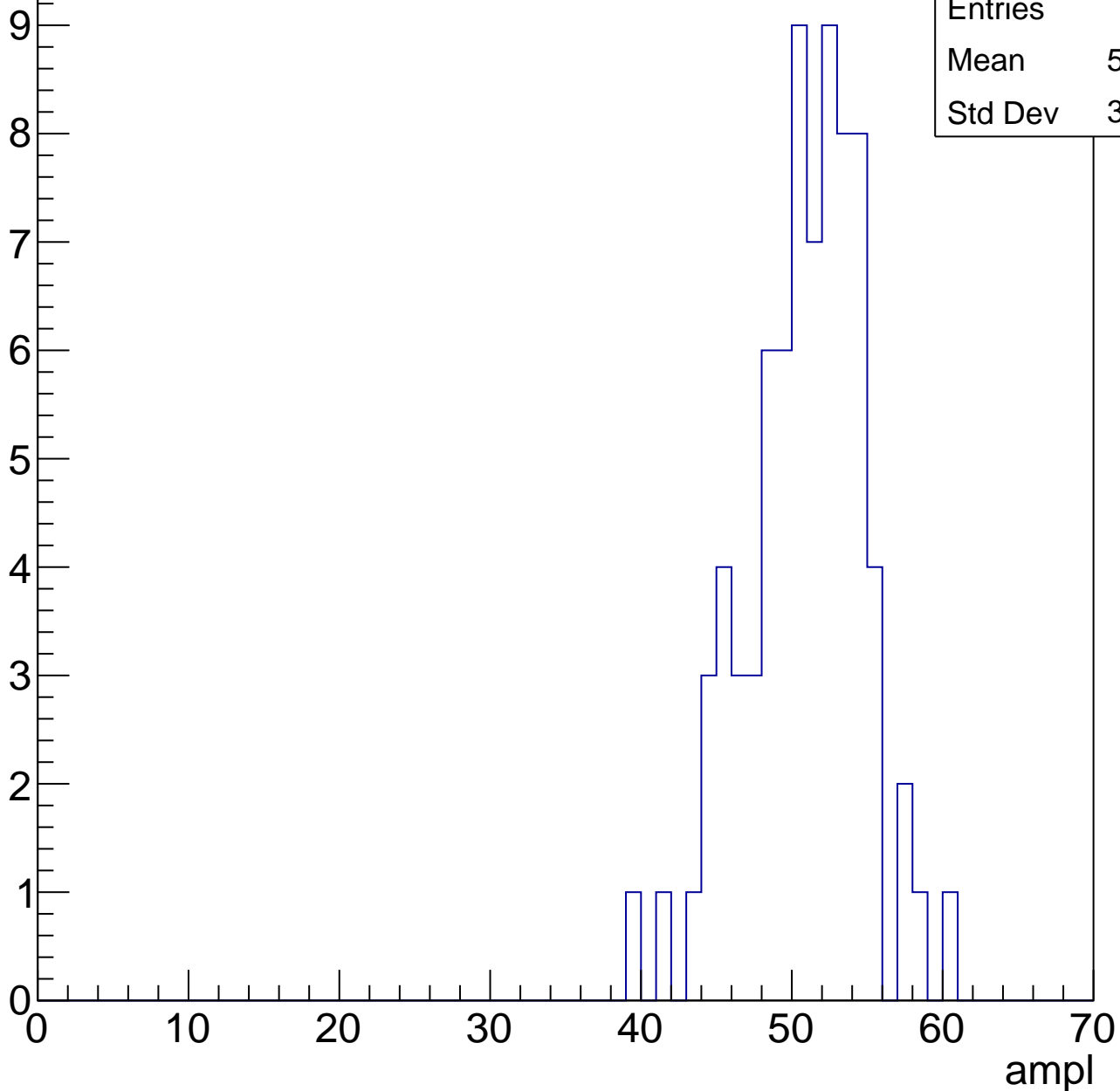


# B0L002S, U23-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	50.38
Std Dev	3.878

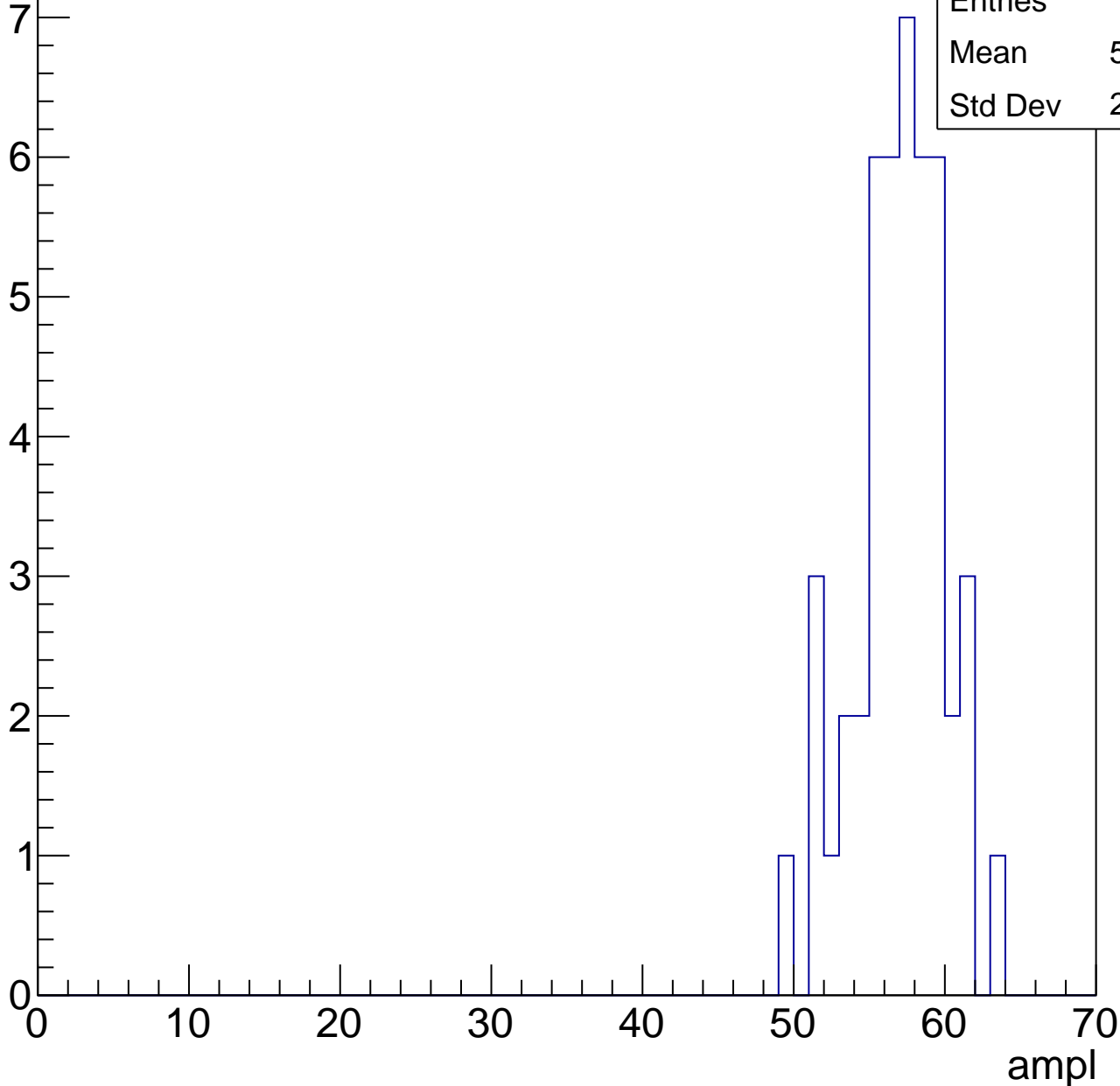


# B0L002S, U23-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	56.54
Std Dev	2.947

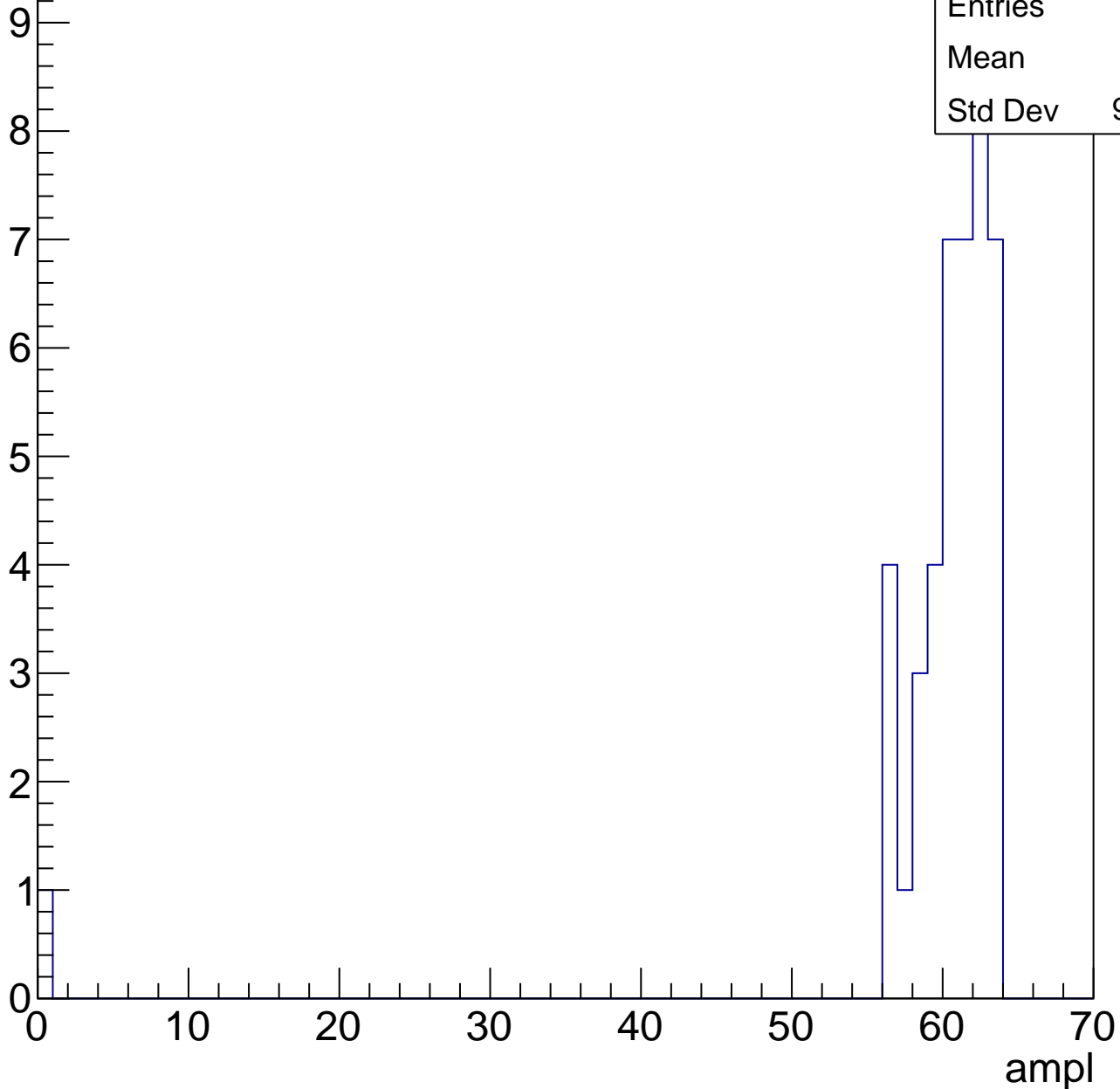


# B0L002S, U23-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	59
Std Dev	9.341



# B0L002S, U23-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch82, adc0

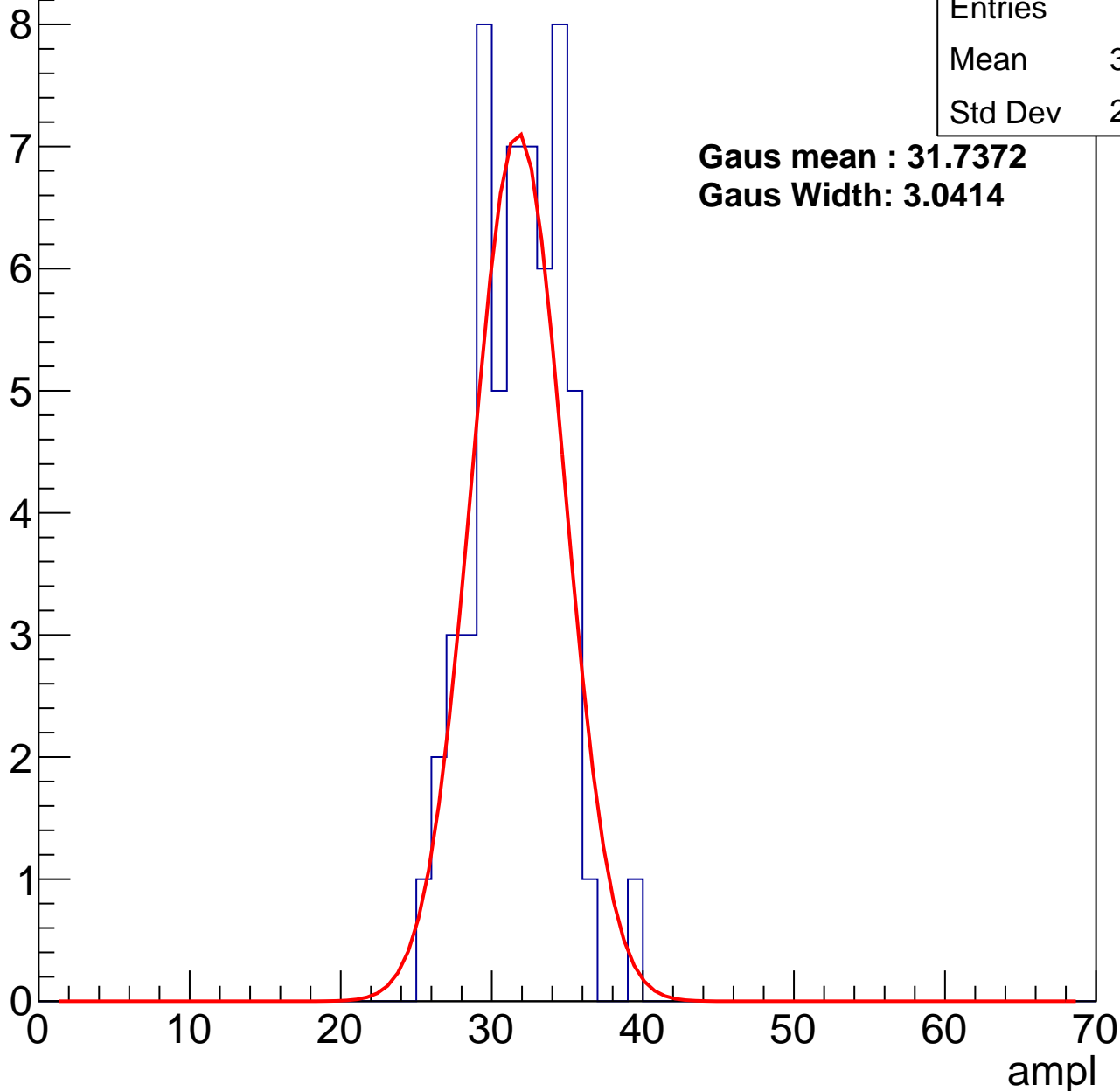
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	31.32
Std Dev	2.854

**Gaus mean : 31.7372**

**Gaus Width: 3.0414**



# B0L002S, U23-ch82, adc1

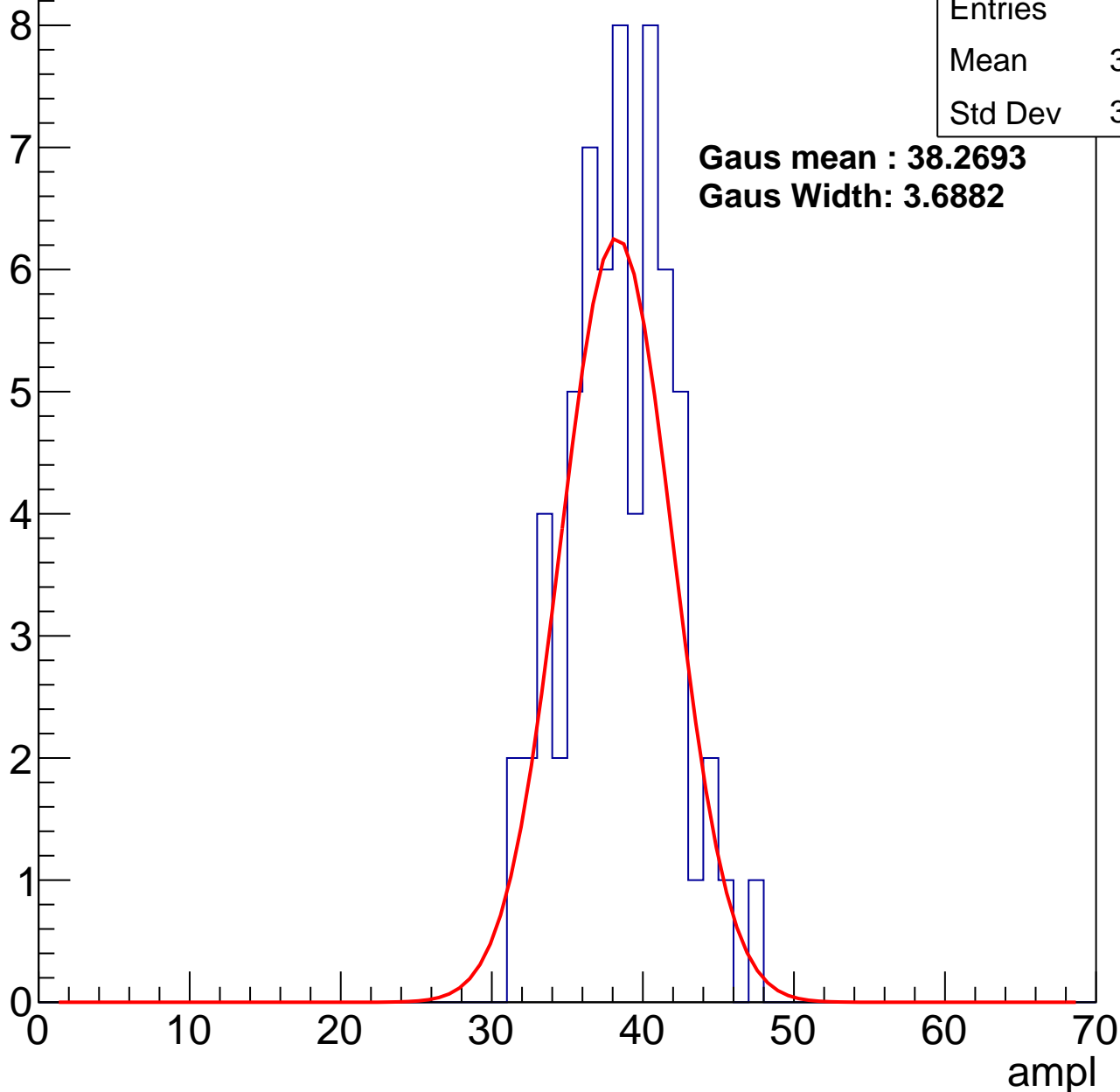
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.03
Std Dev	3.486

**Gaus mean : 38.2693**

**Gaus Width: 3.6882**



# B0L002S, U23-ch82, adc2

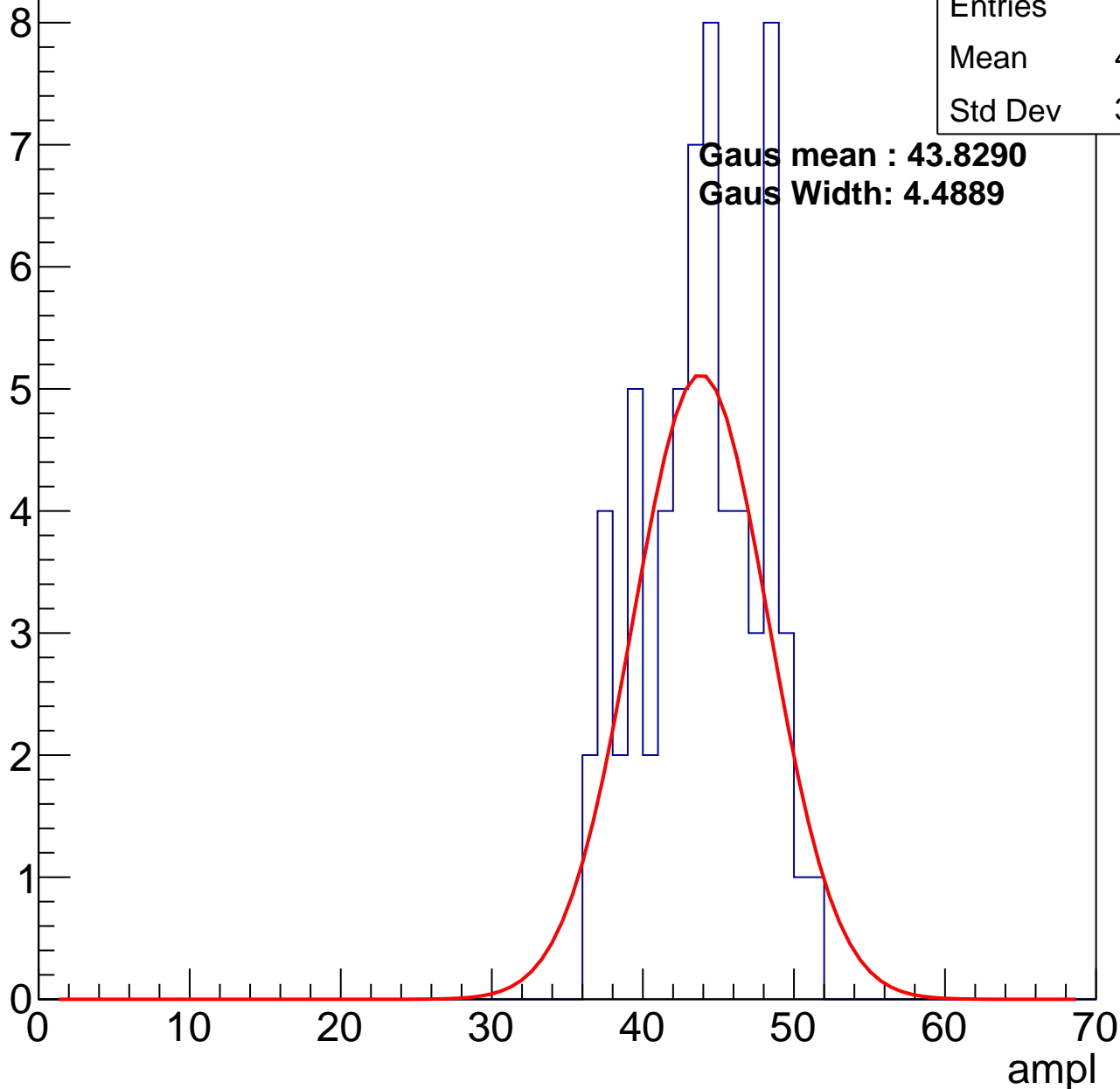
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.41
Std Dev	3.841

**Gaus mean : 43.8290**

**Gaus Width: 4.4889**

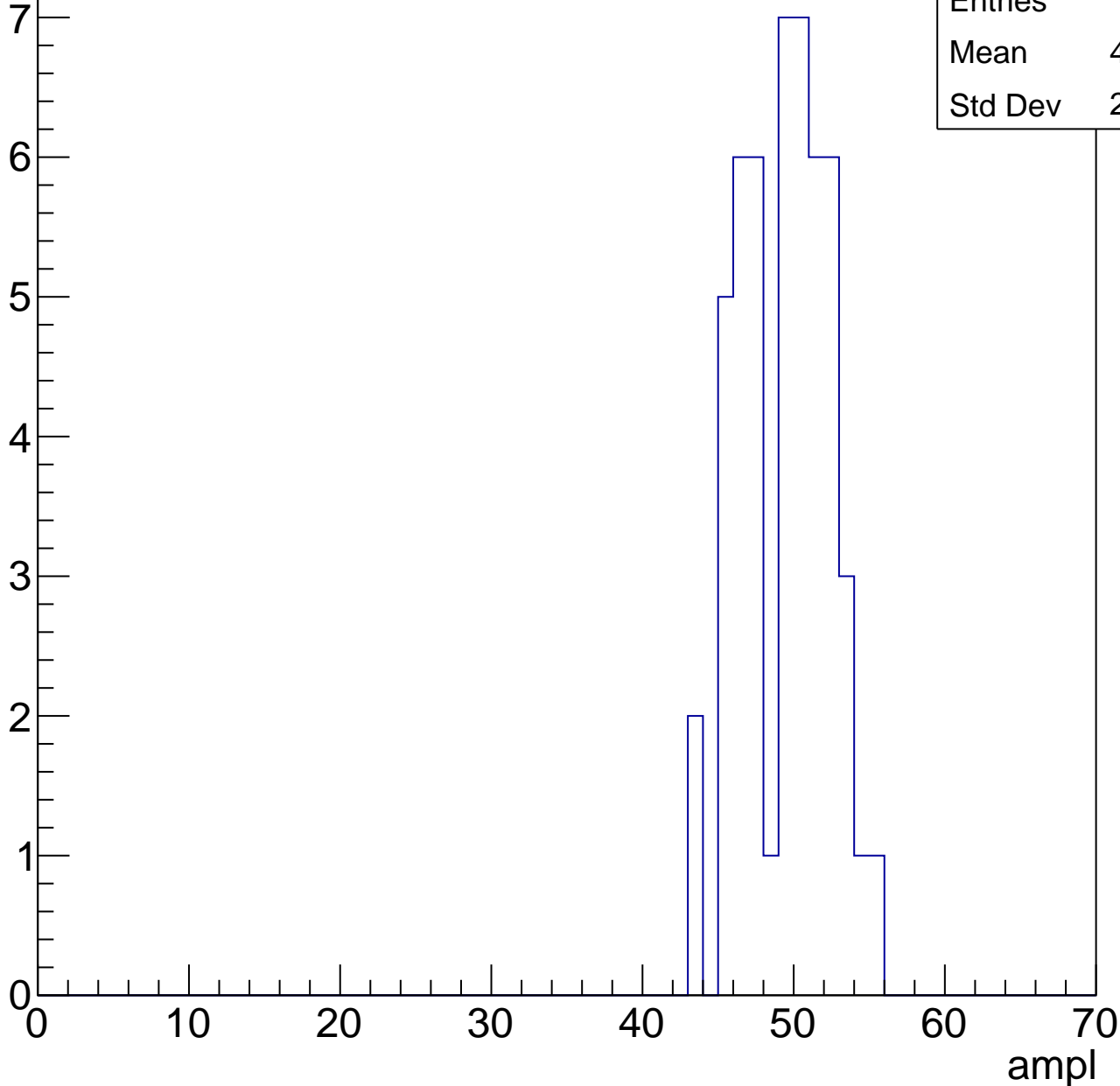


# B0L002S, U23-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	48.94
Std Dev	2.886



# B0L002S, U23-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

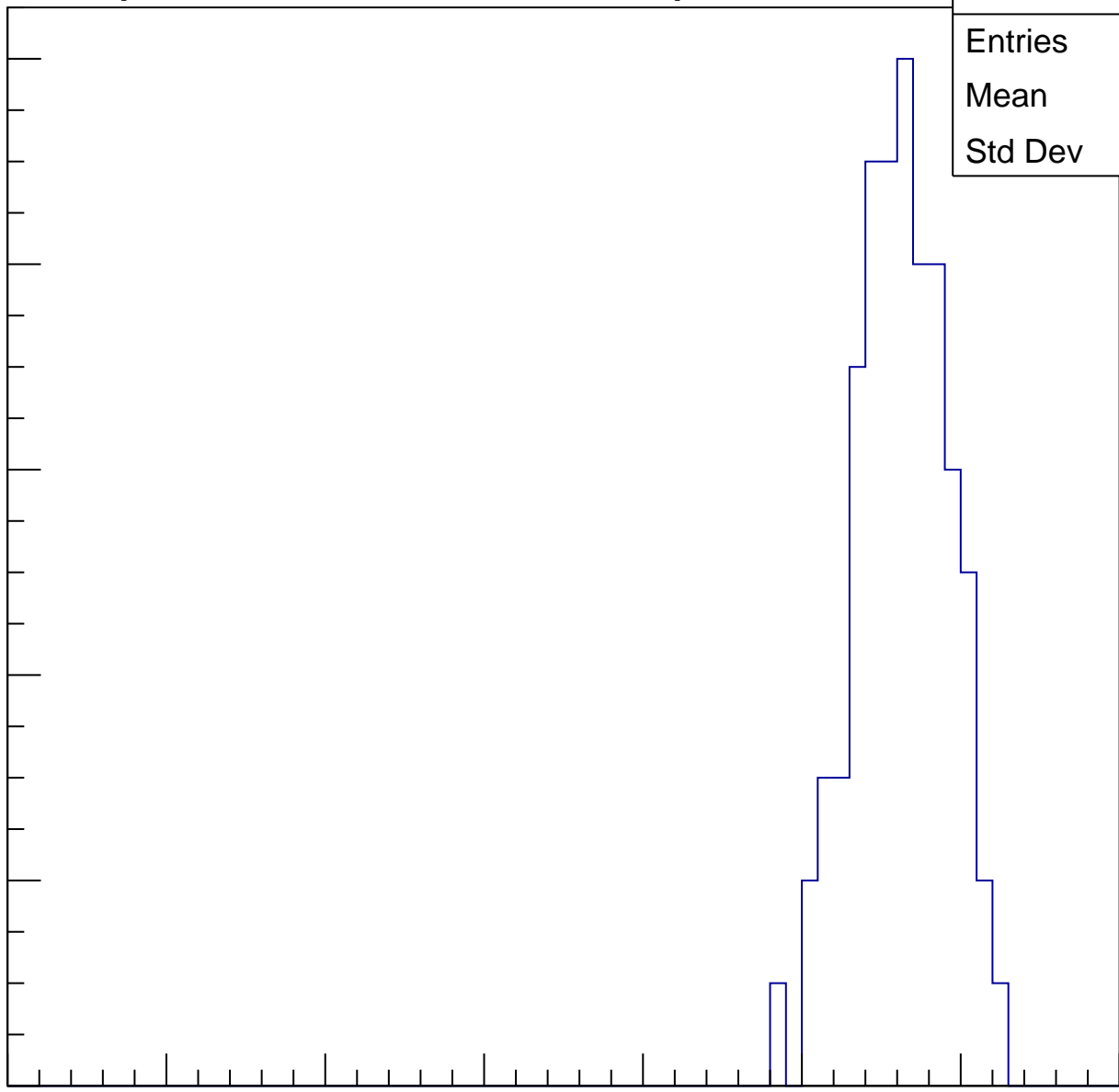
50

60

70

ampl

Entries	74
Mean	55.77
Std Dev	2.902

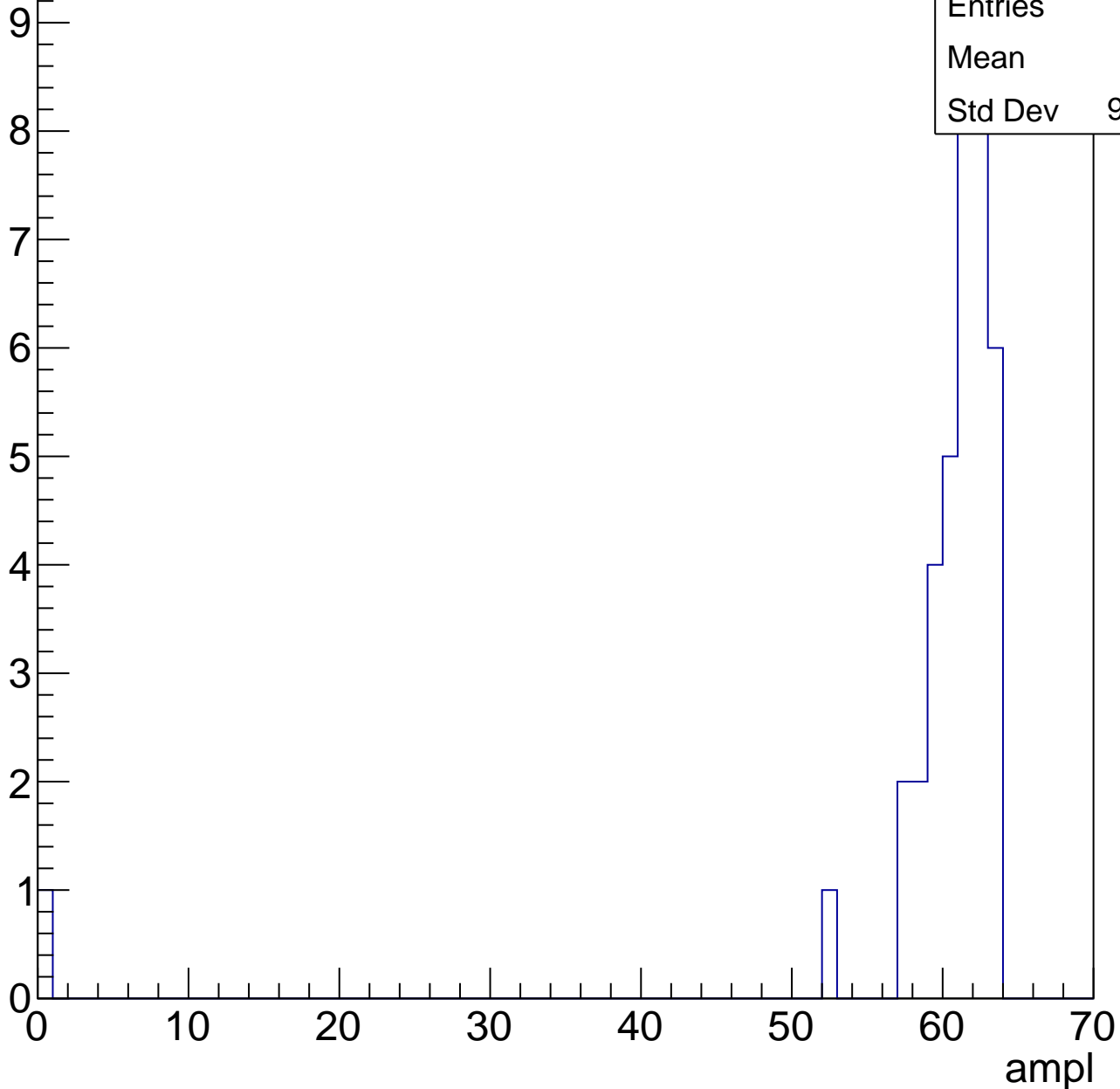


# B0L002S, U23-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59
Std Dev	9.939



# B0L002S, U23-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch83, adc0

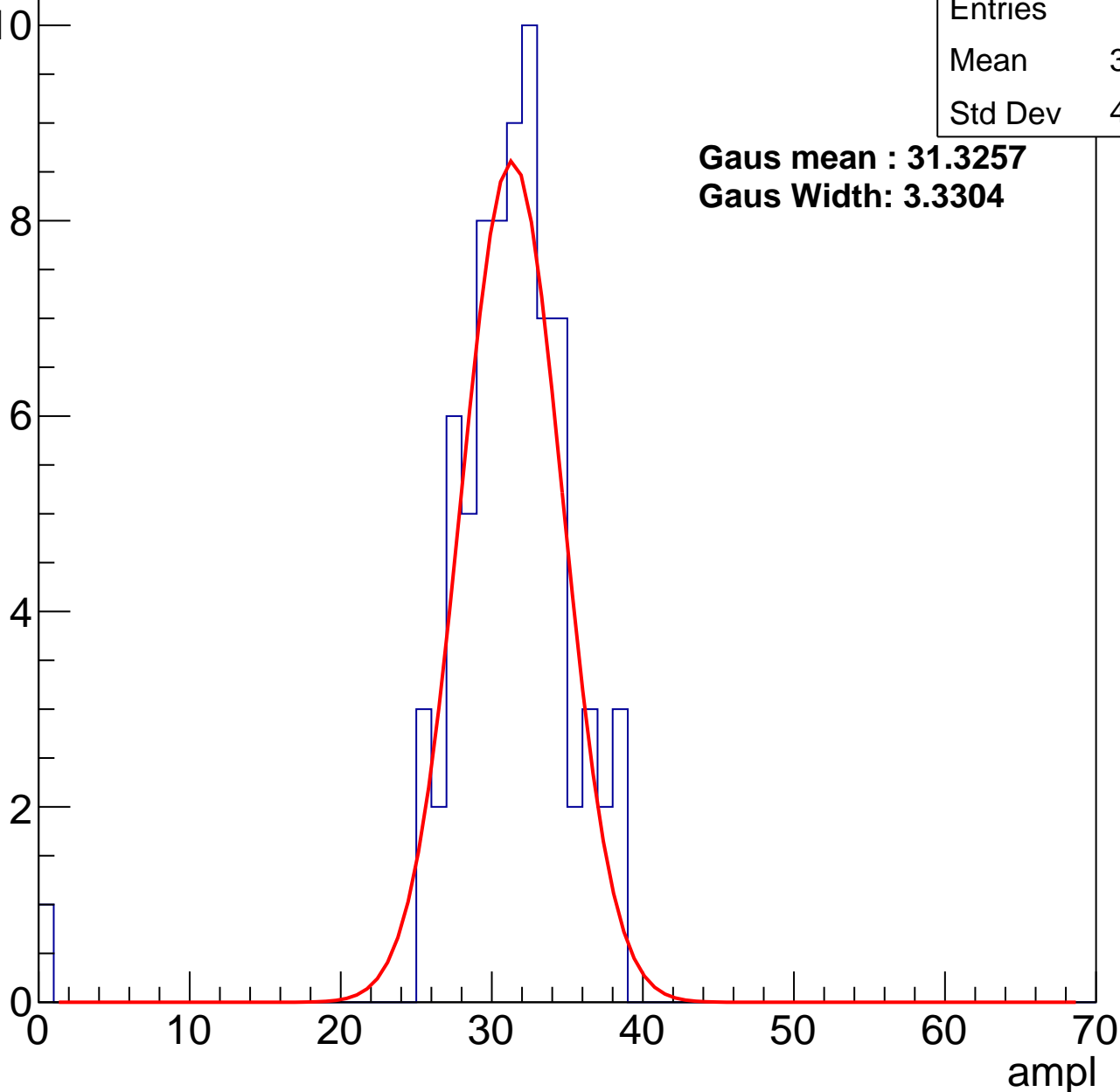
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	30.72
Std Dev	4.759

**Gaus mean : 31.3257**

**Gaus Width: 3.3304**



# B0L002S, U23-ch83, adc1

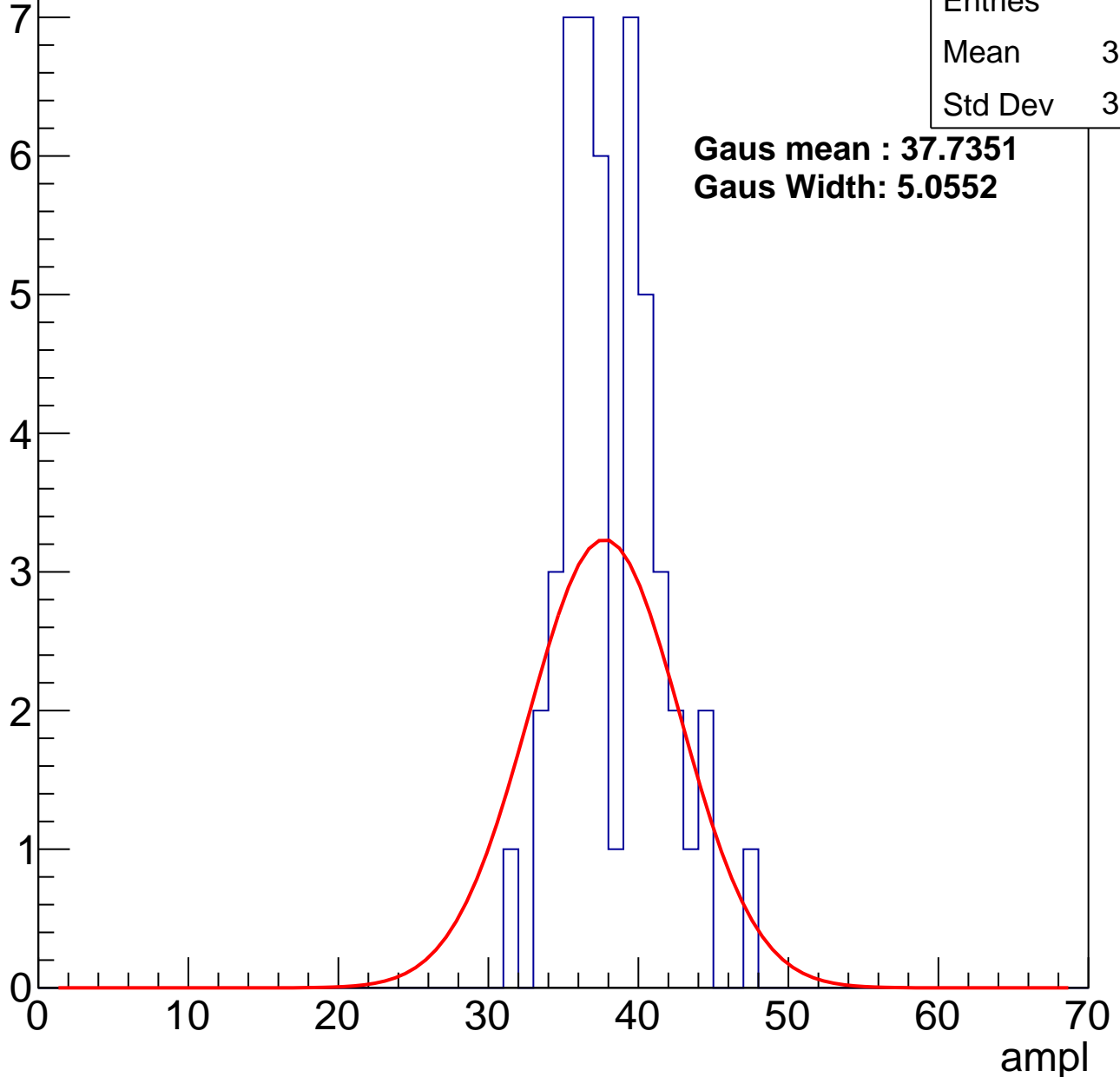
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	37.79
Std Dev	3.247

**Gaus mean : 37.7351**

**Gaus Width: 5.0552**



# B0L002S, U23-ch83, adc2

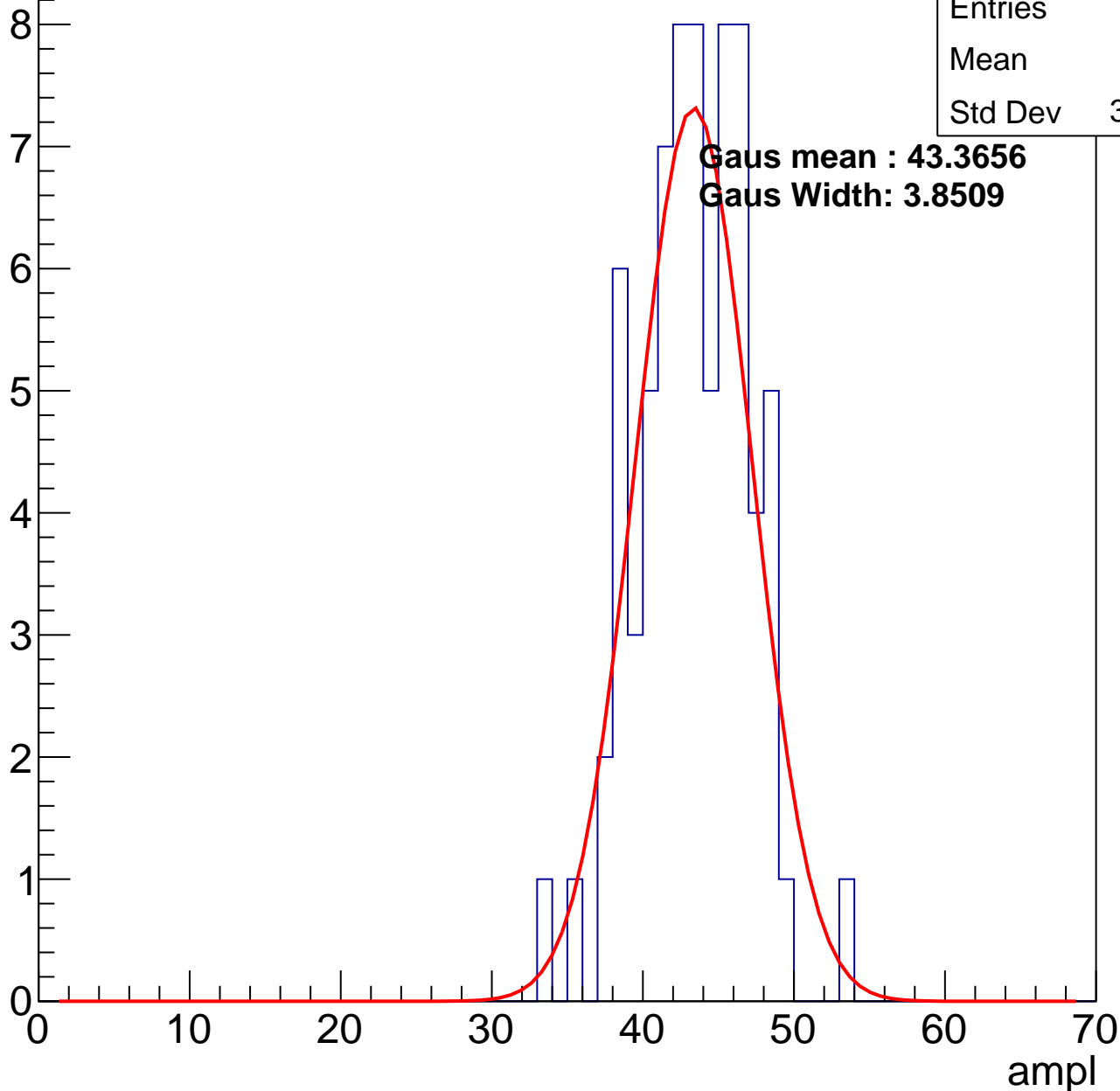
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.9
Std Dev	3.604

**Gaus mean : 43.3656**

**Gaus Width: 3.8509**

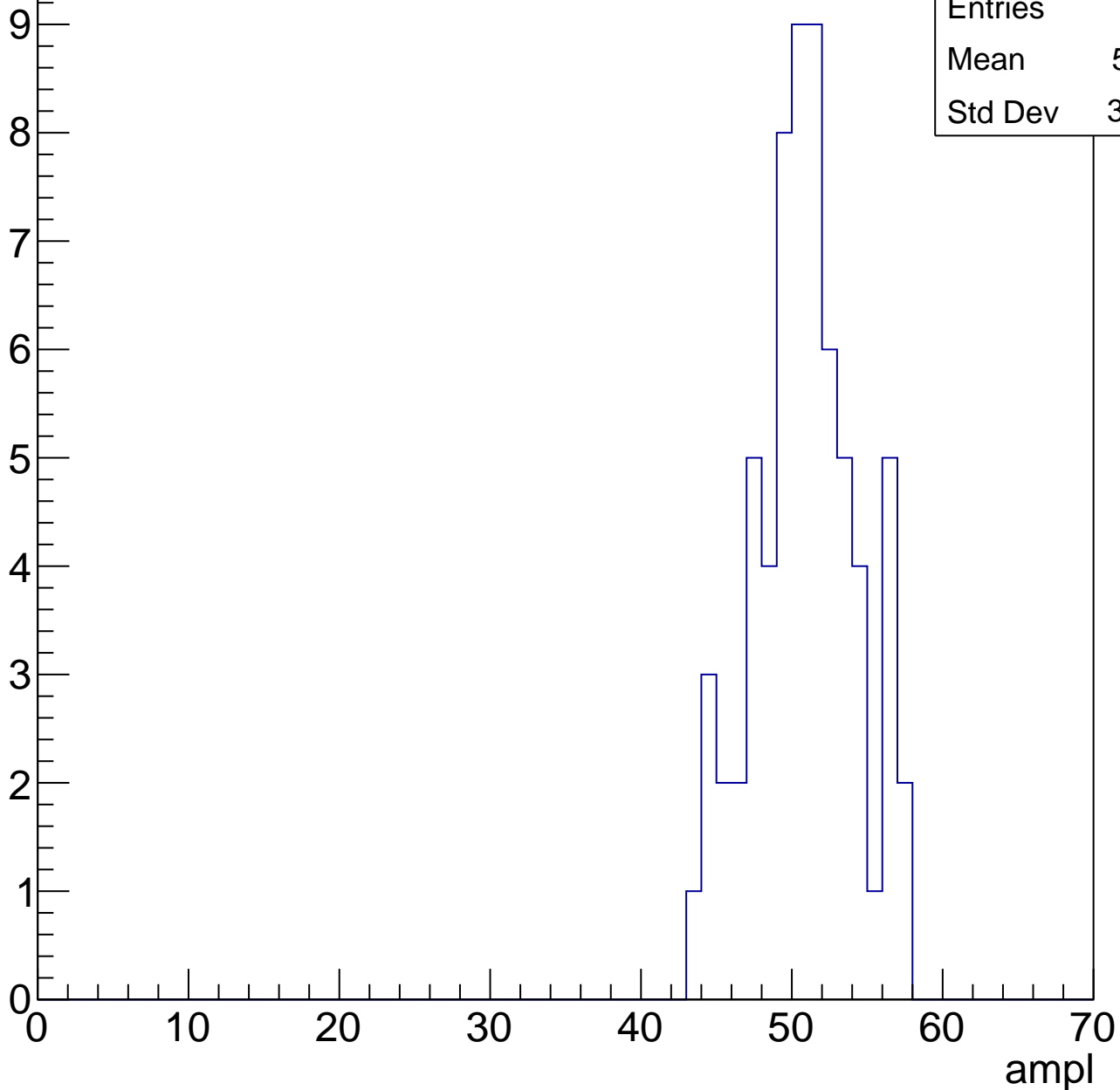


# B0L002S, U23-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

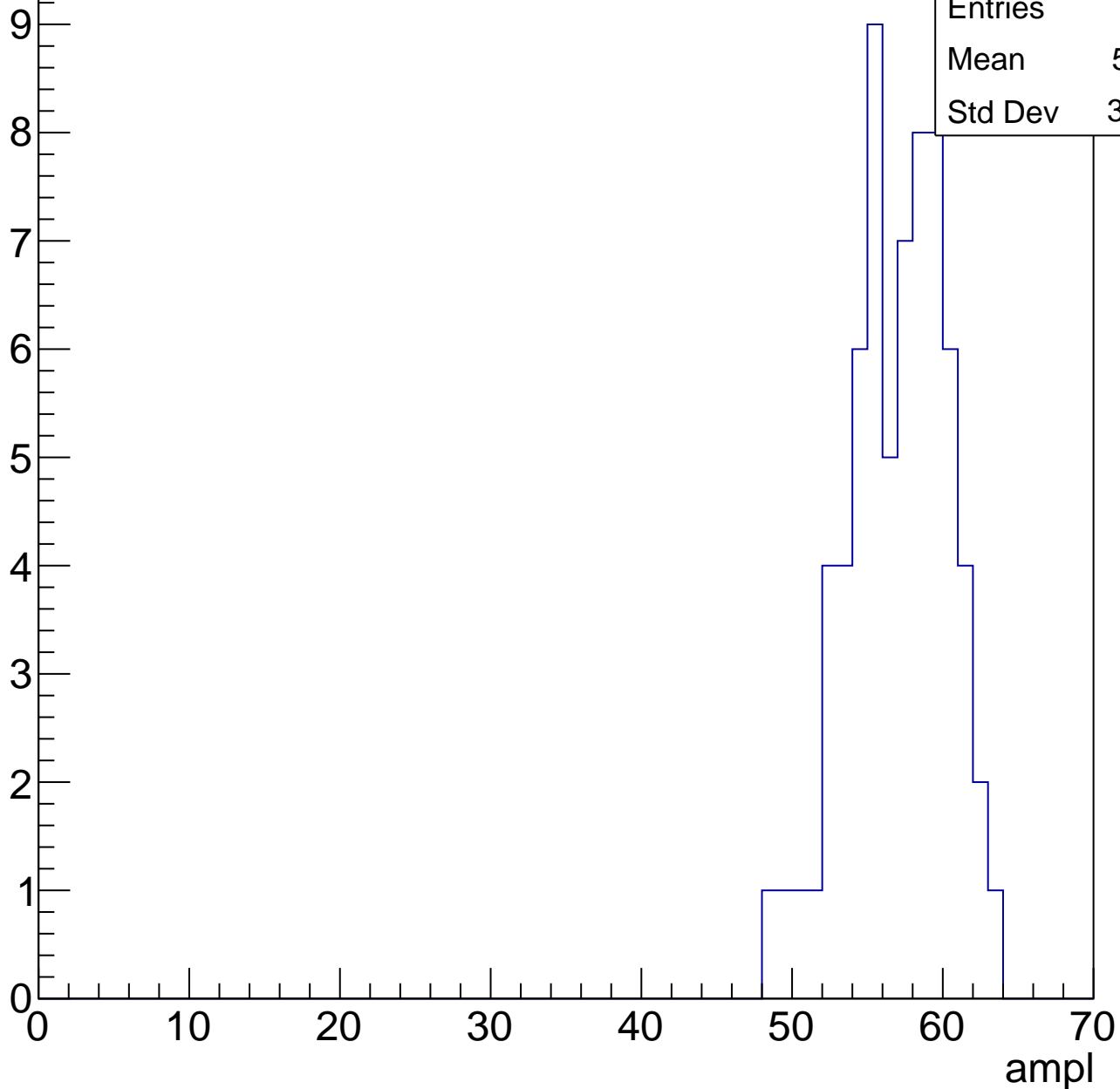
Entries	66
Mean	50.41
Std Dev	3.353



# B0L002S, U23-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



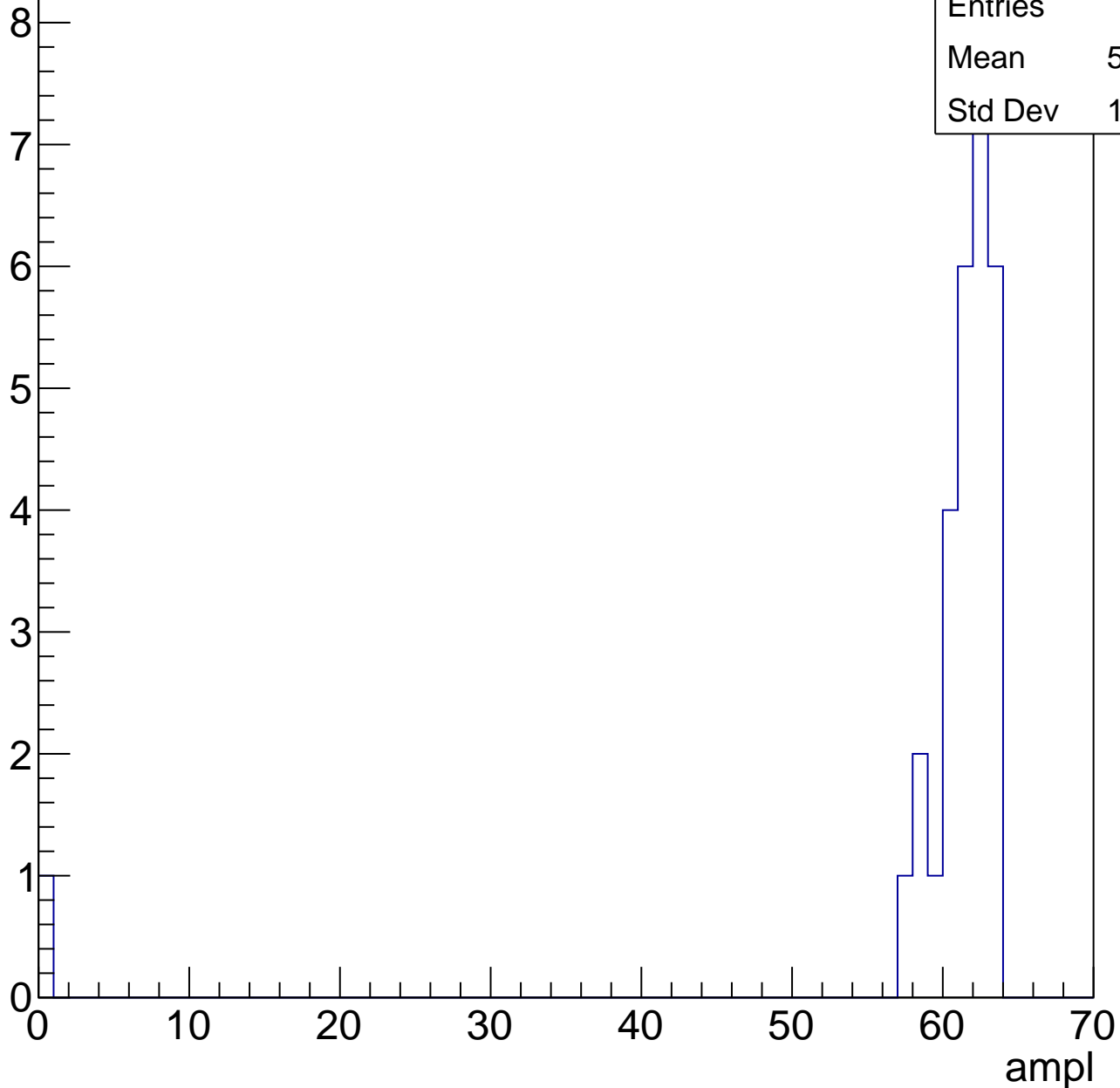
Entries	68
Mean	56.51
Std Dev	3.247

# B0L002S, U23-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

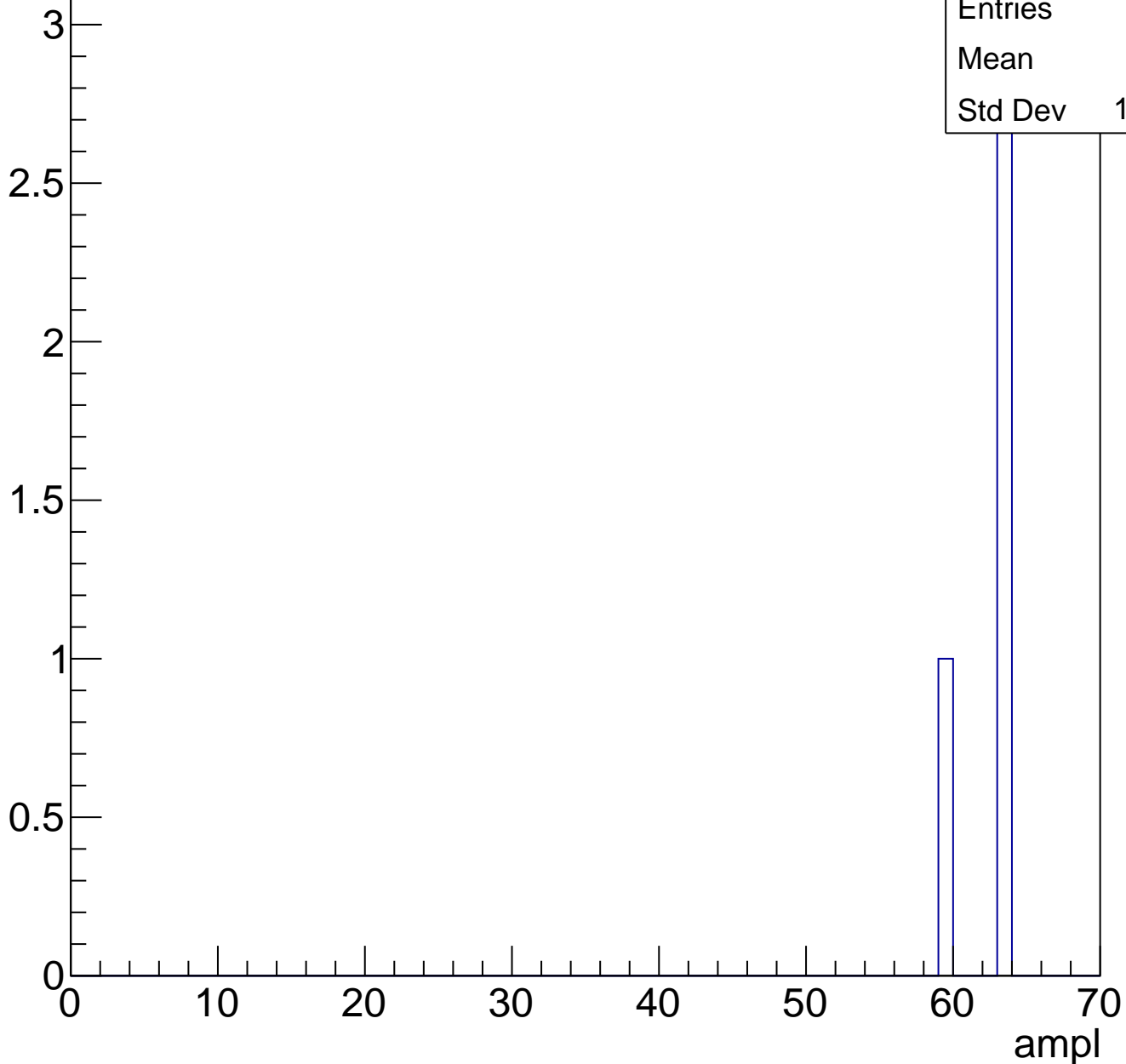
Entries	29
Mean	59.03
Std Dev	11.27



# B0L002S, U23-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B0L002S, U23-ch84, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	69
Mean	30.78
Std Dev	3.189

**Gaus mean : 30.6971**

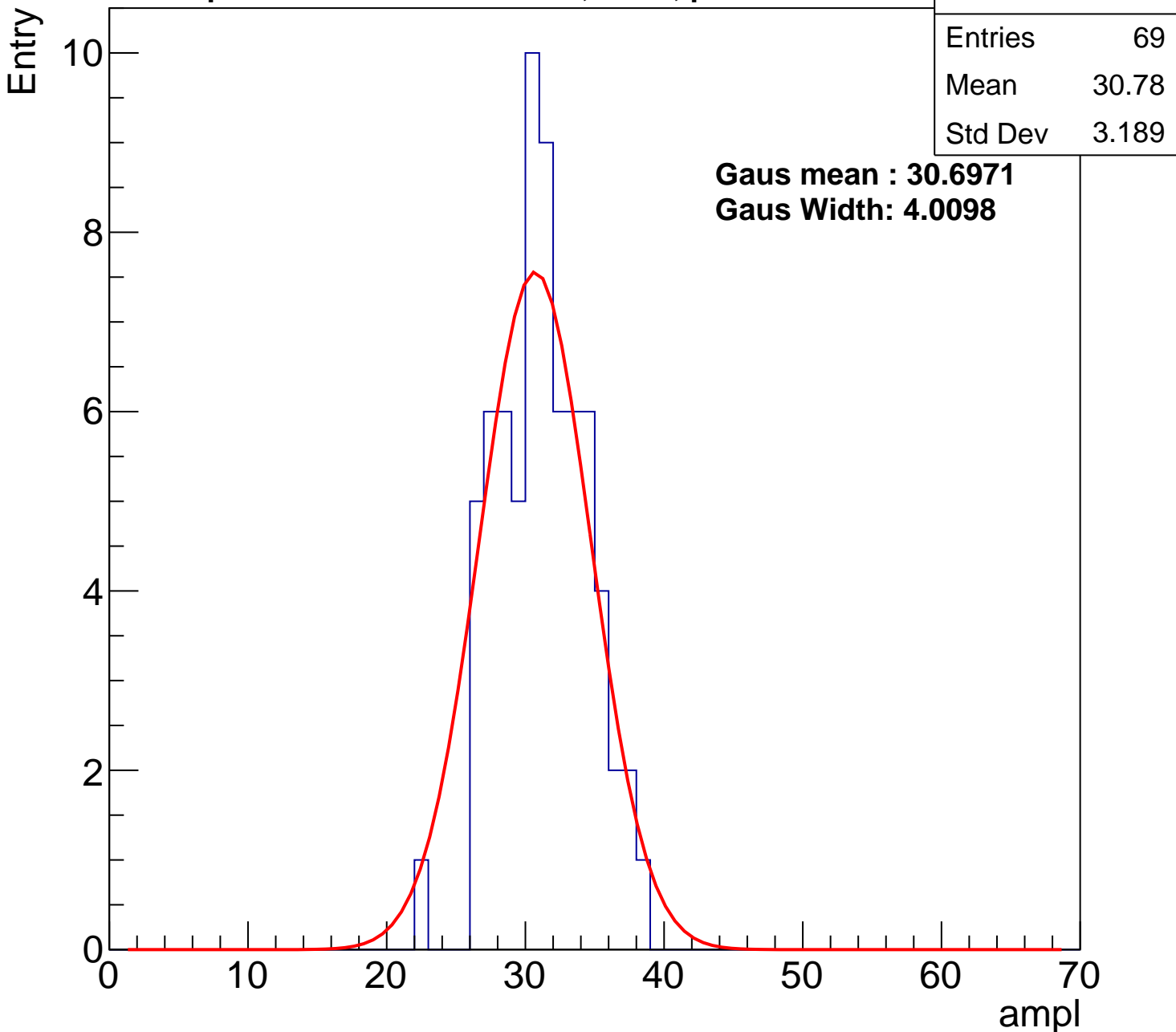
**Gaus Width: 4.0098**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch84, adc1

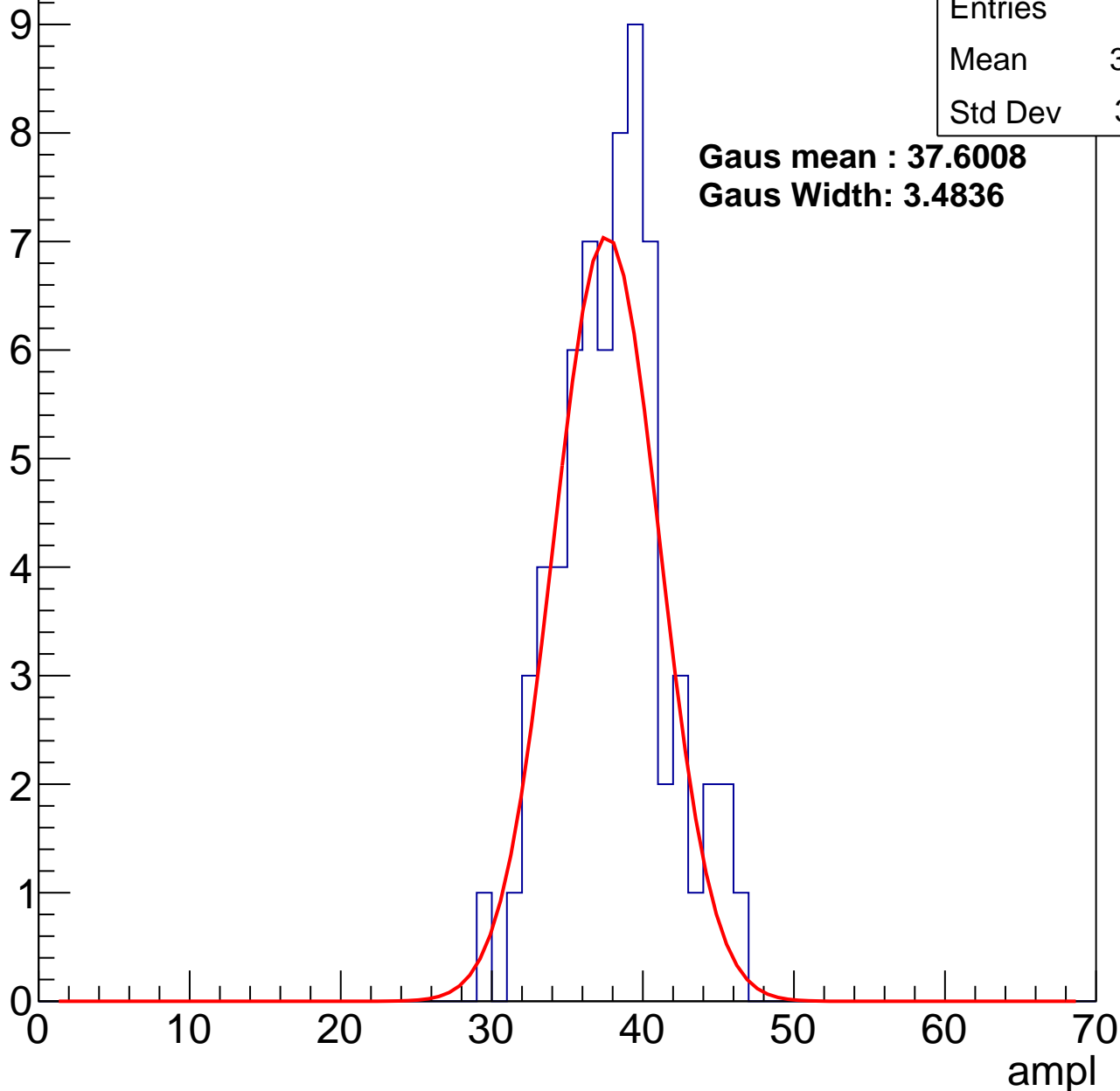
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.58
Std Dev	3.541

**Gaus mean : 37.6008**

**Gaus Width: 3.4836**



# B0L002S, U23-ch84, adc2

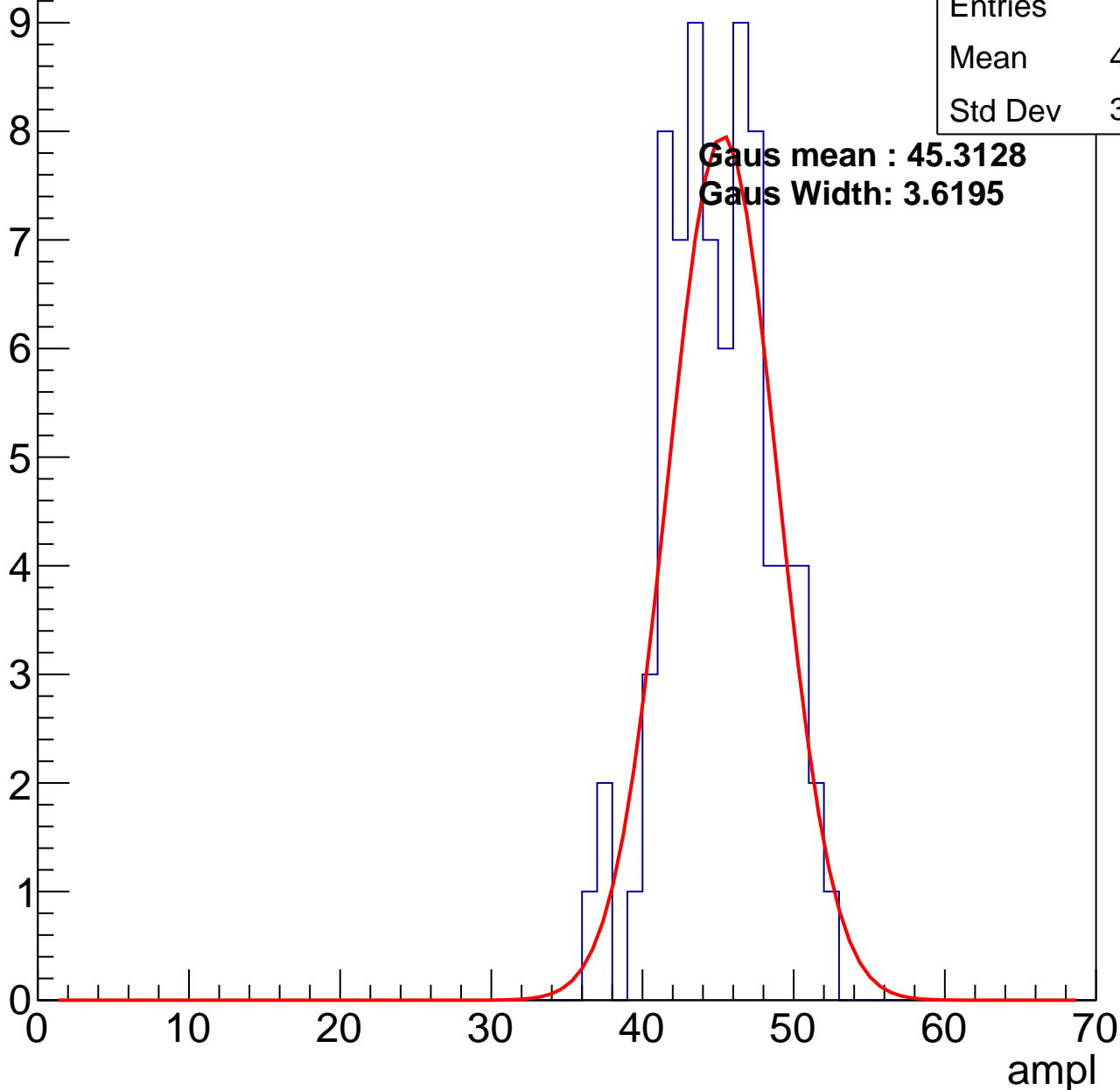
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	44.58
Std Dev	3.446

**Gaus mean : 45.3128**

**Gaus Width: 3.6195**

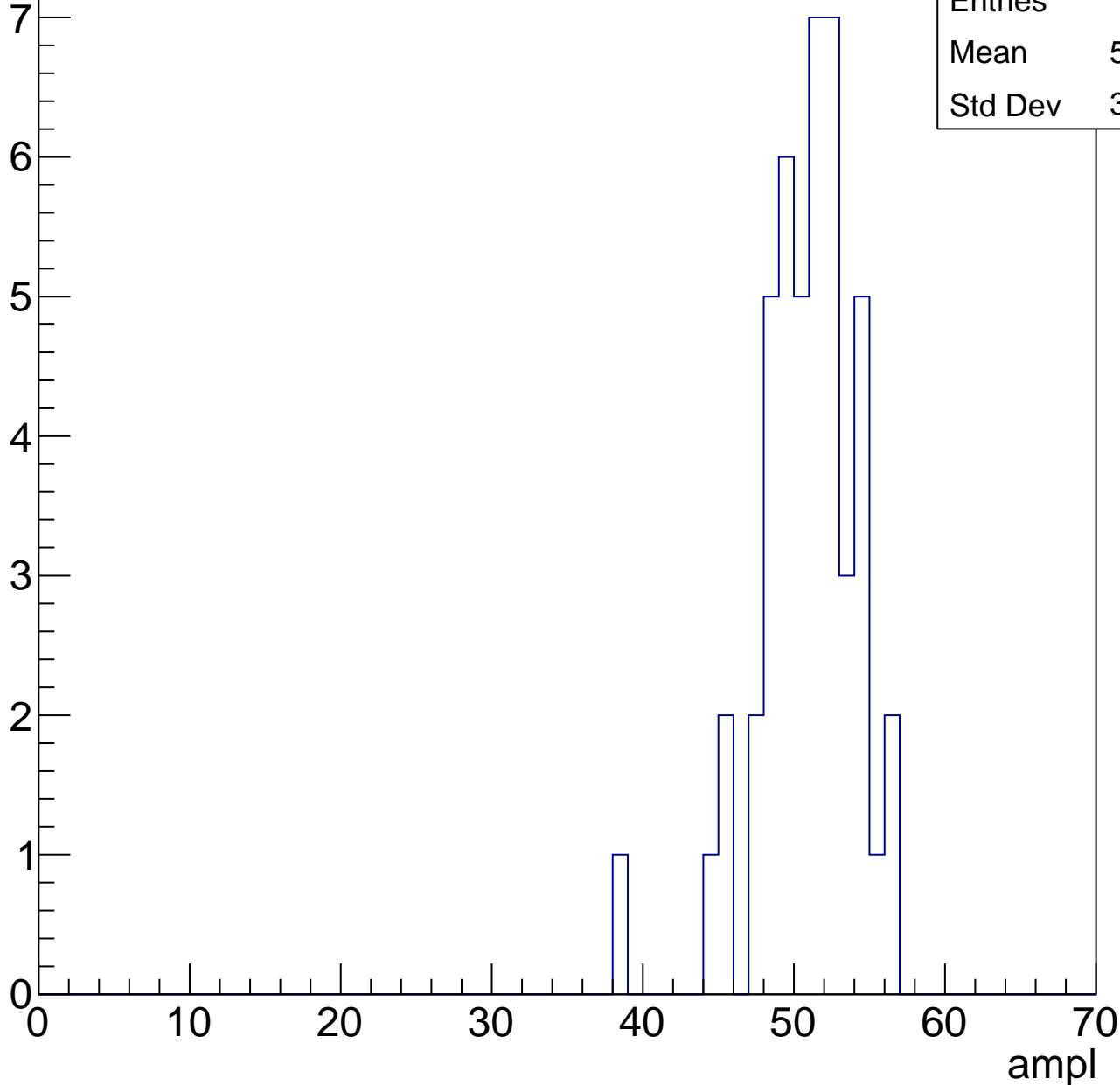


# B0L002S, U23-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	50.36
Std Dev	3.284

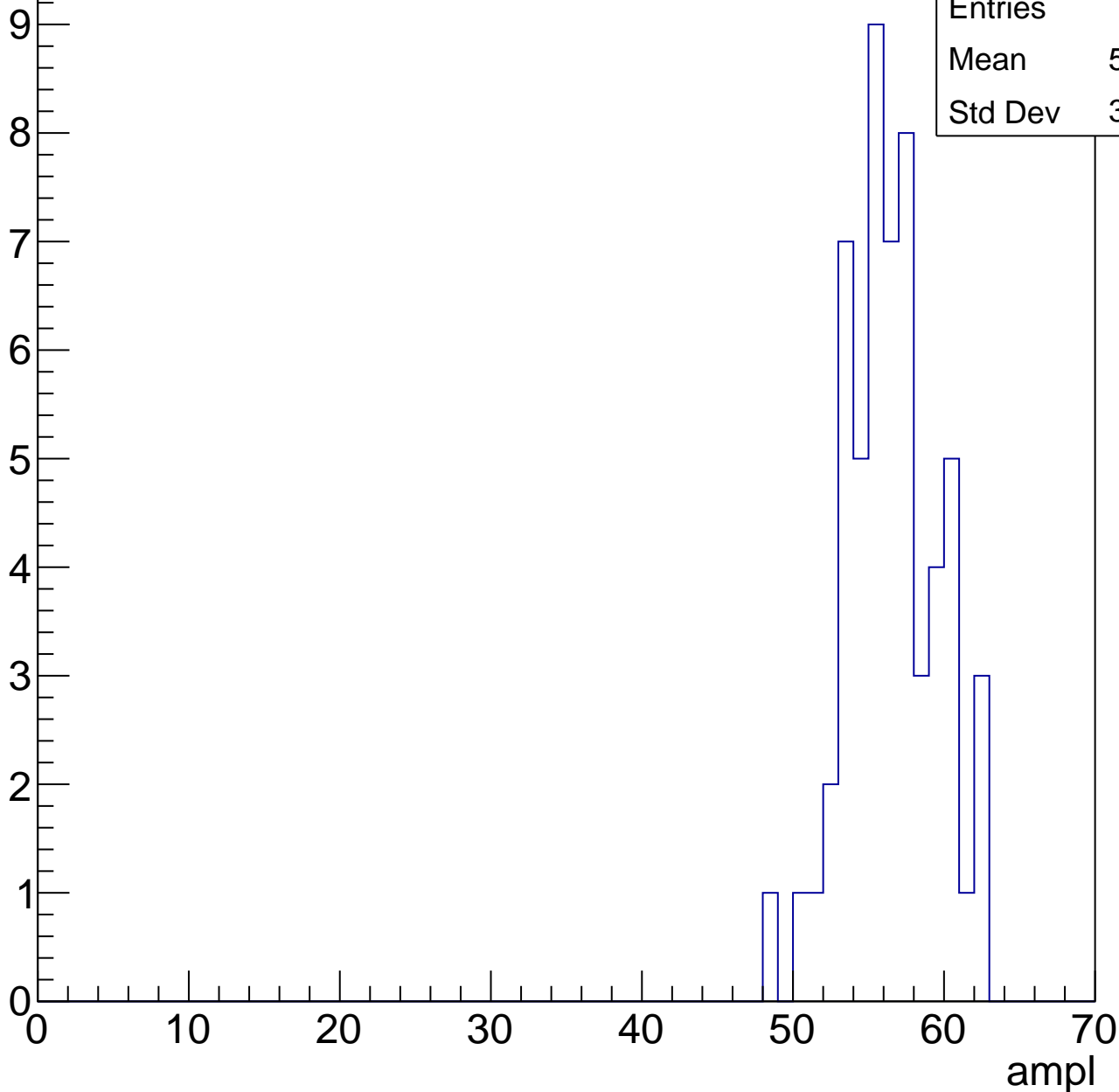


# B0L002S, U23-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.04
Std Dev	3.014

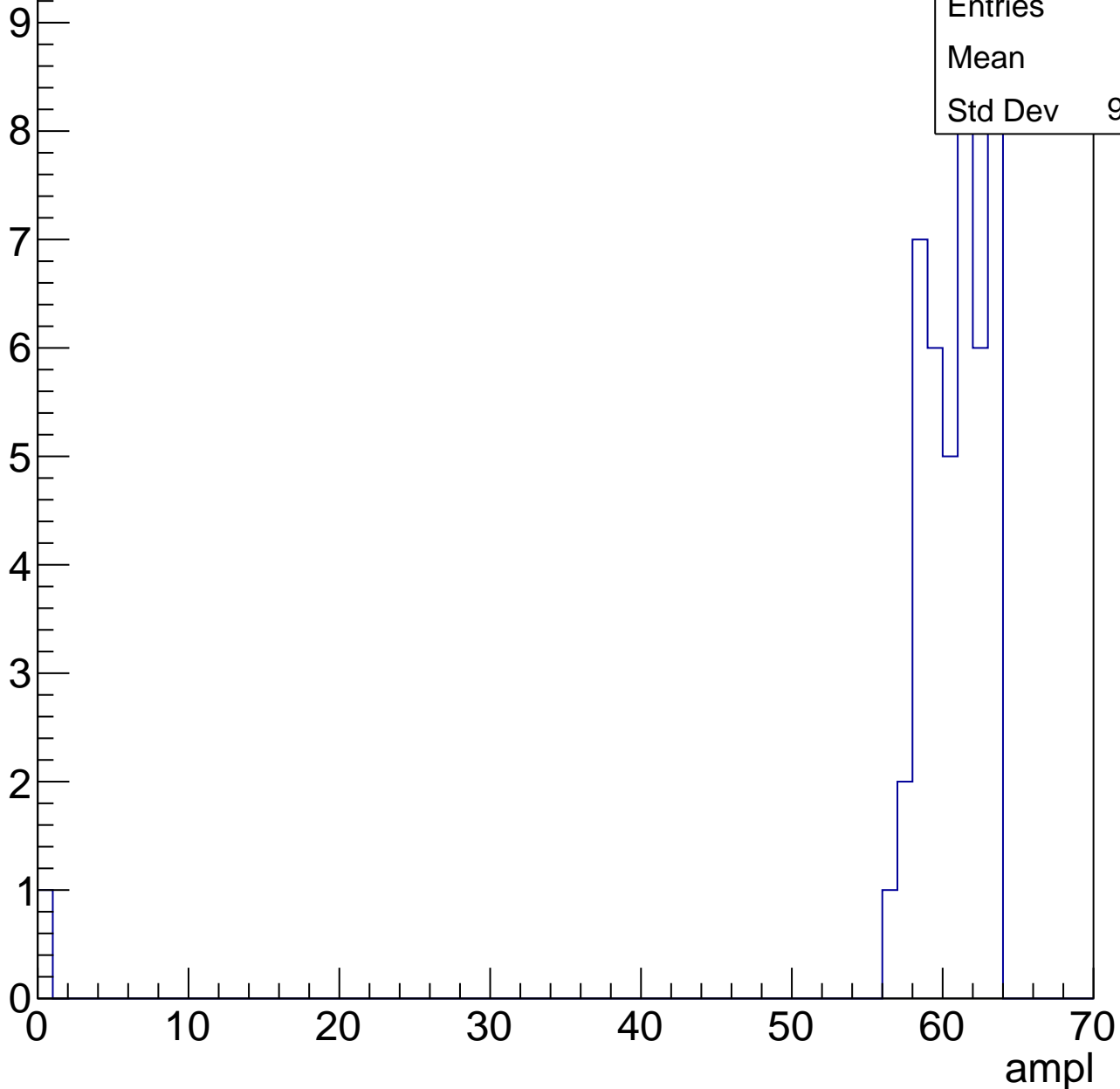


# B0L002S, U23-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59
Std Dev	9.102



# B0L002S, U23-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

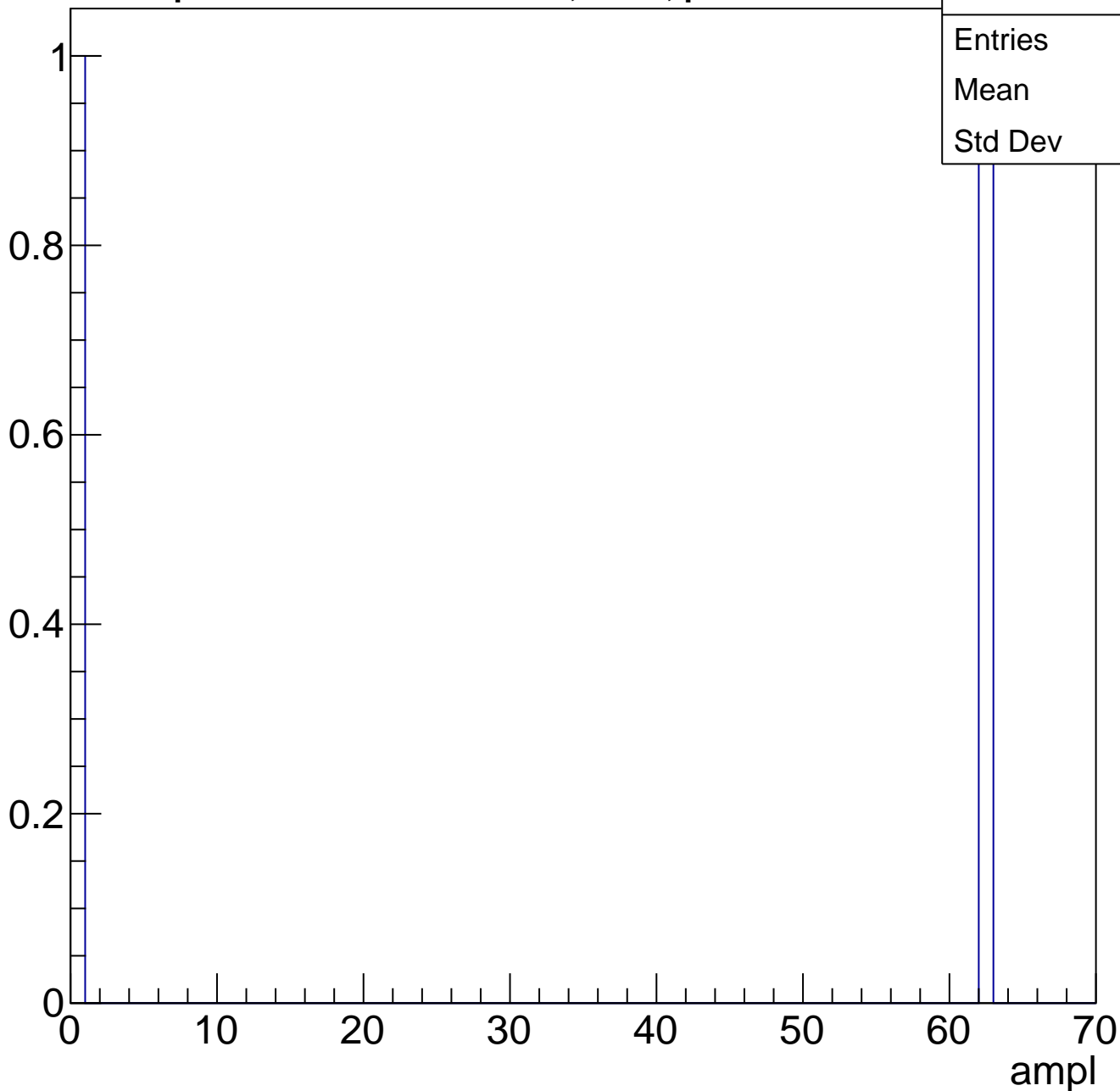




# B0L002S, U23-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch85, adc0

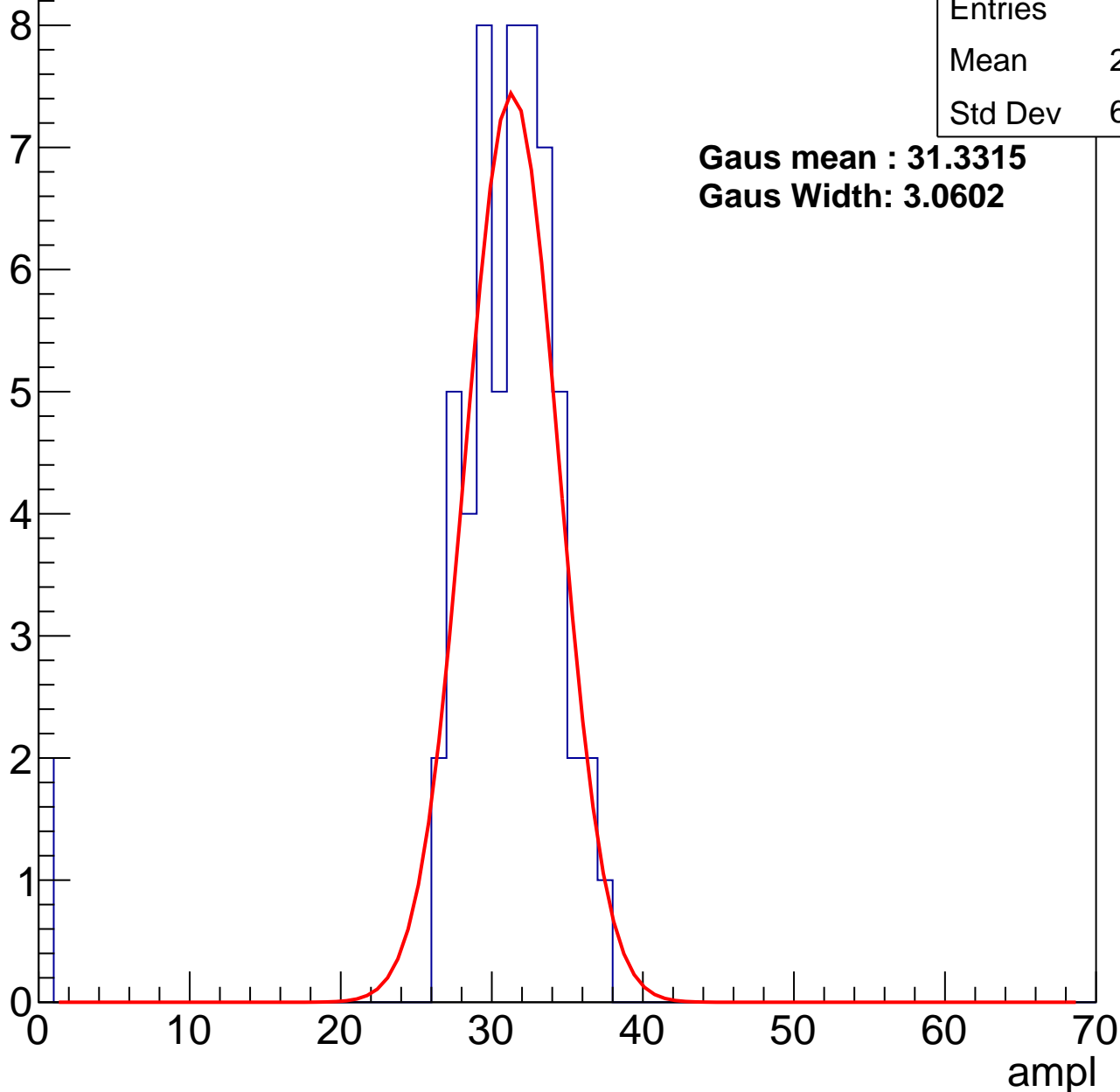
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.92
Std Dev	6.182

**Gaus mean : 31.3315**

**Gaus Width: 3.0602**



# B0L002S, U23-ch85, adc1

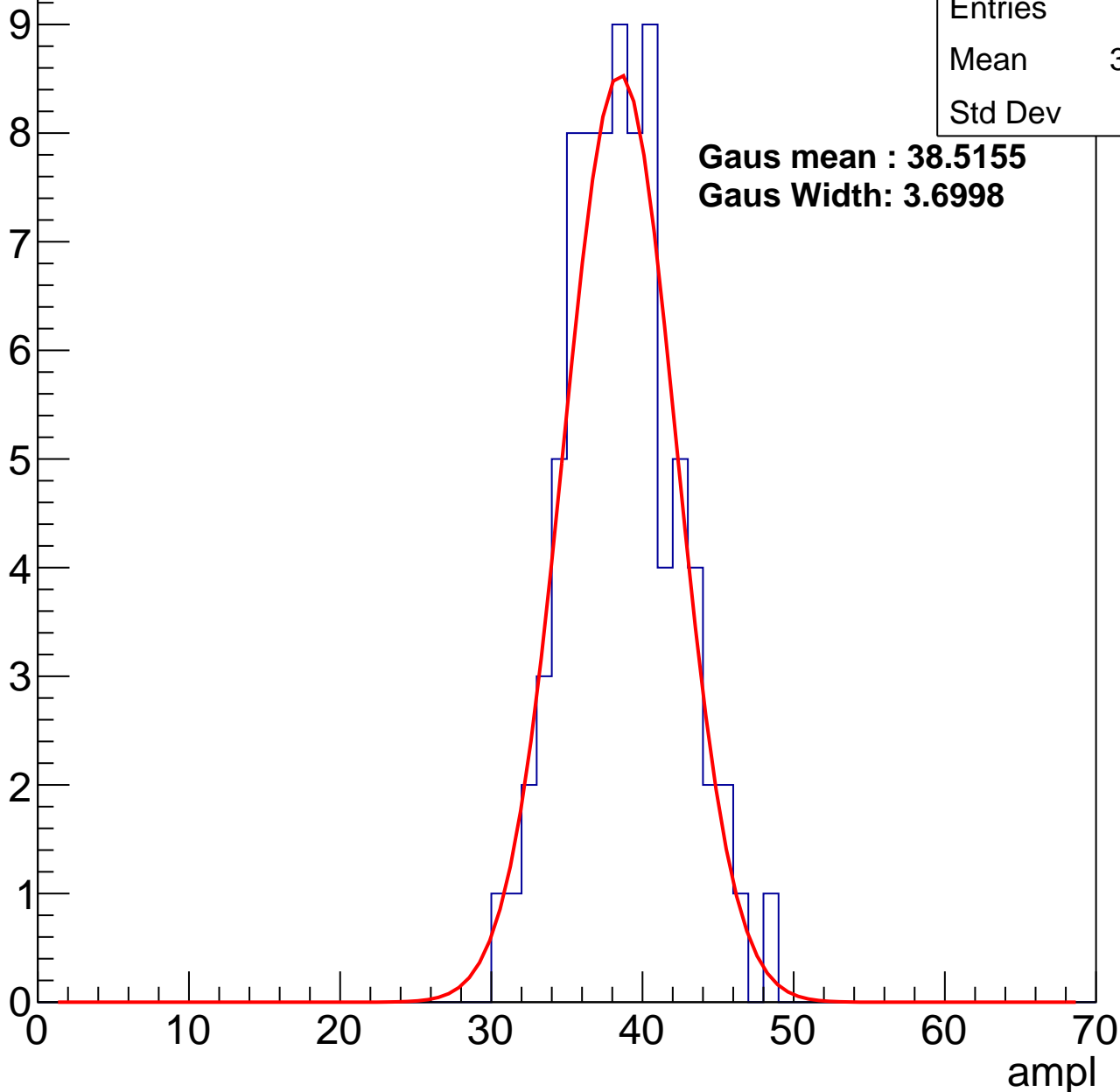
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	38.15
Std Dev	3.59

**Gaus mean : 38.5155**

**Gaus Width: 3.6998**



# B0L002S, U23-ch85, adc2

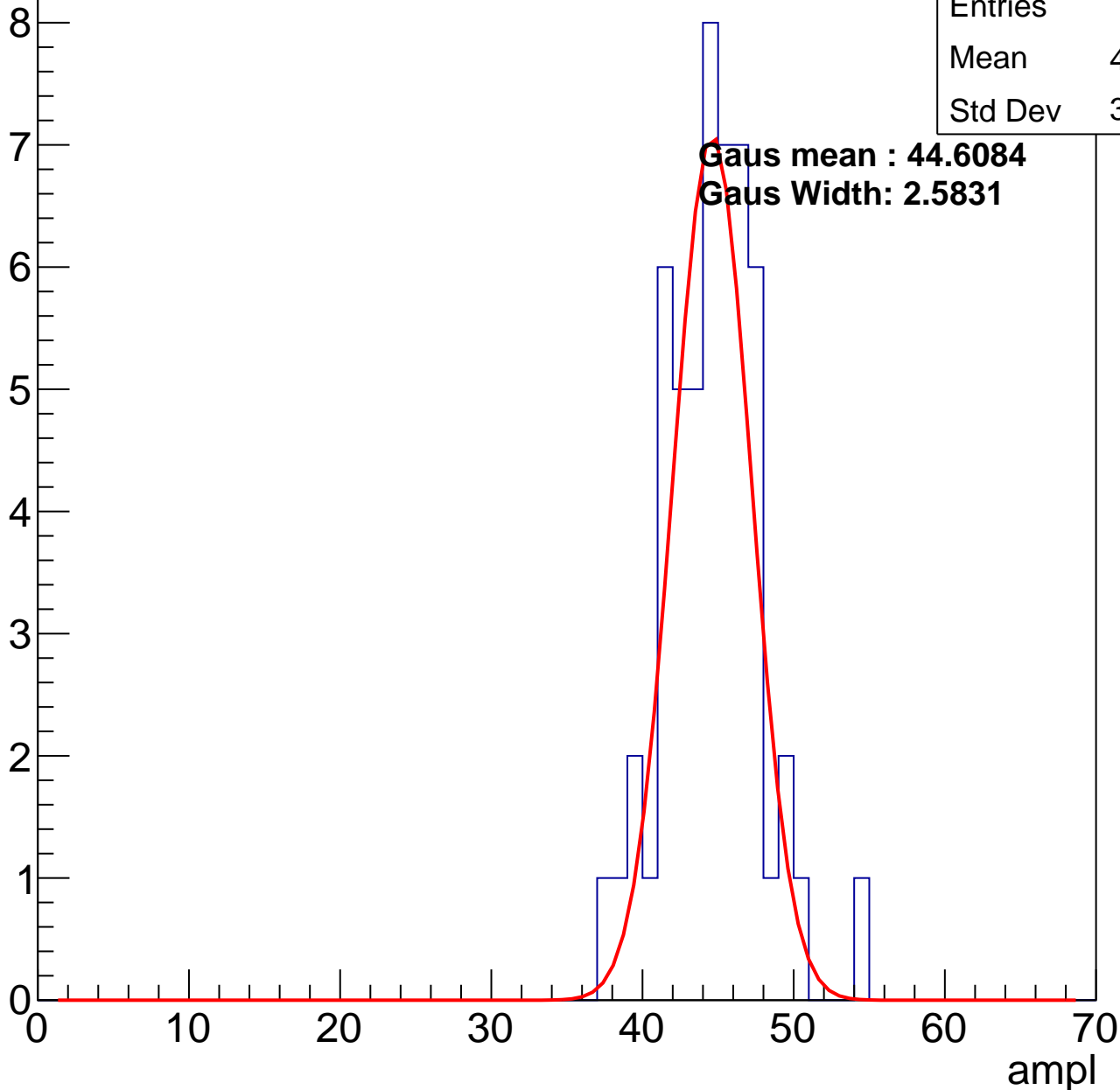
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	44.17
Std Dev	3.102

**Gaus mean : 44.6084**

**Gaus Width: 2.5831**



# B0L002S, U23-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

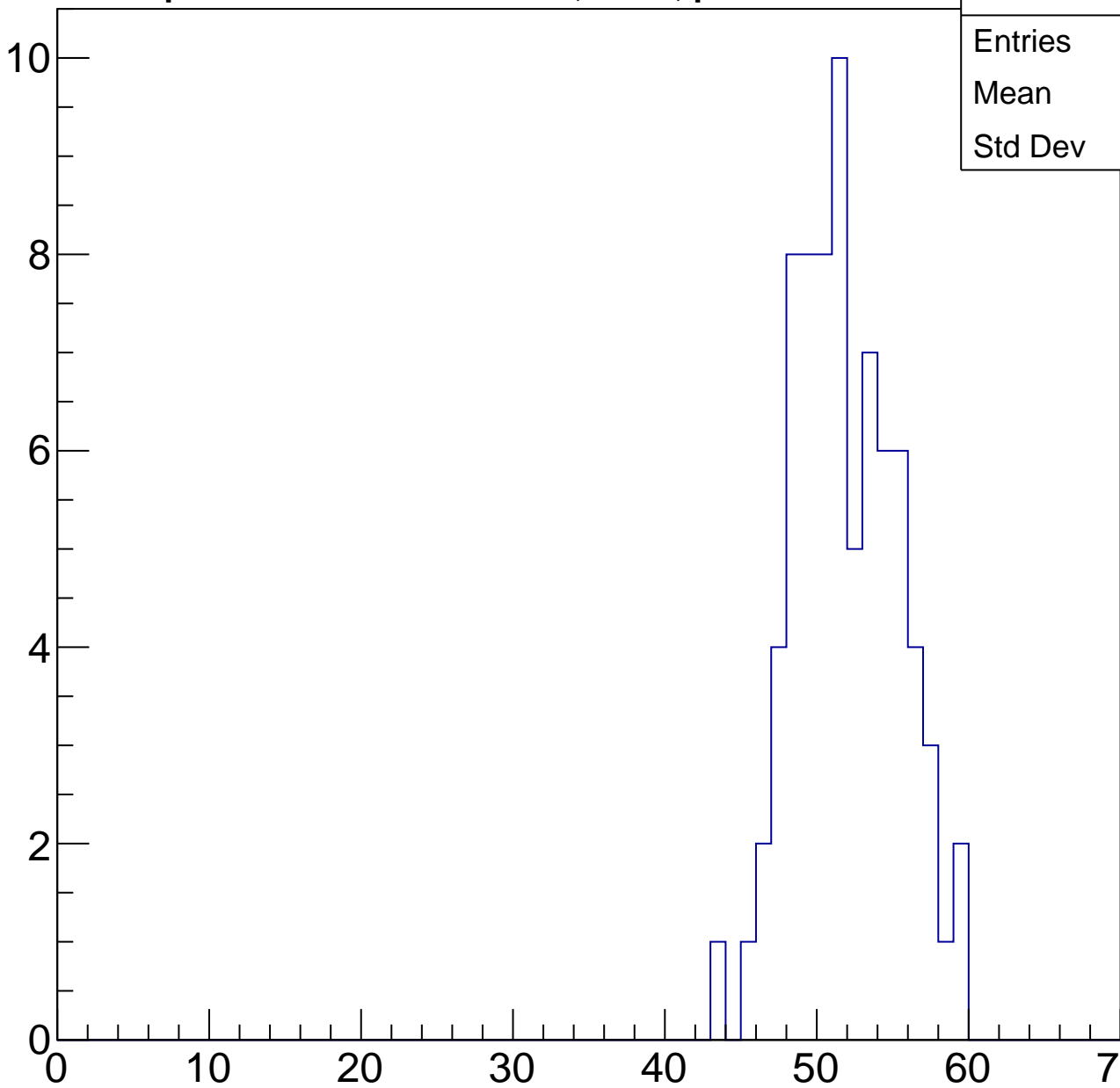
Entries	76
Mean	51.45
Std Dev	3.4

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

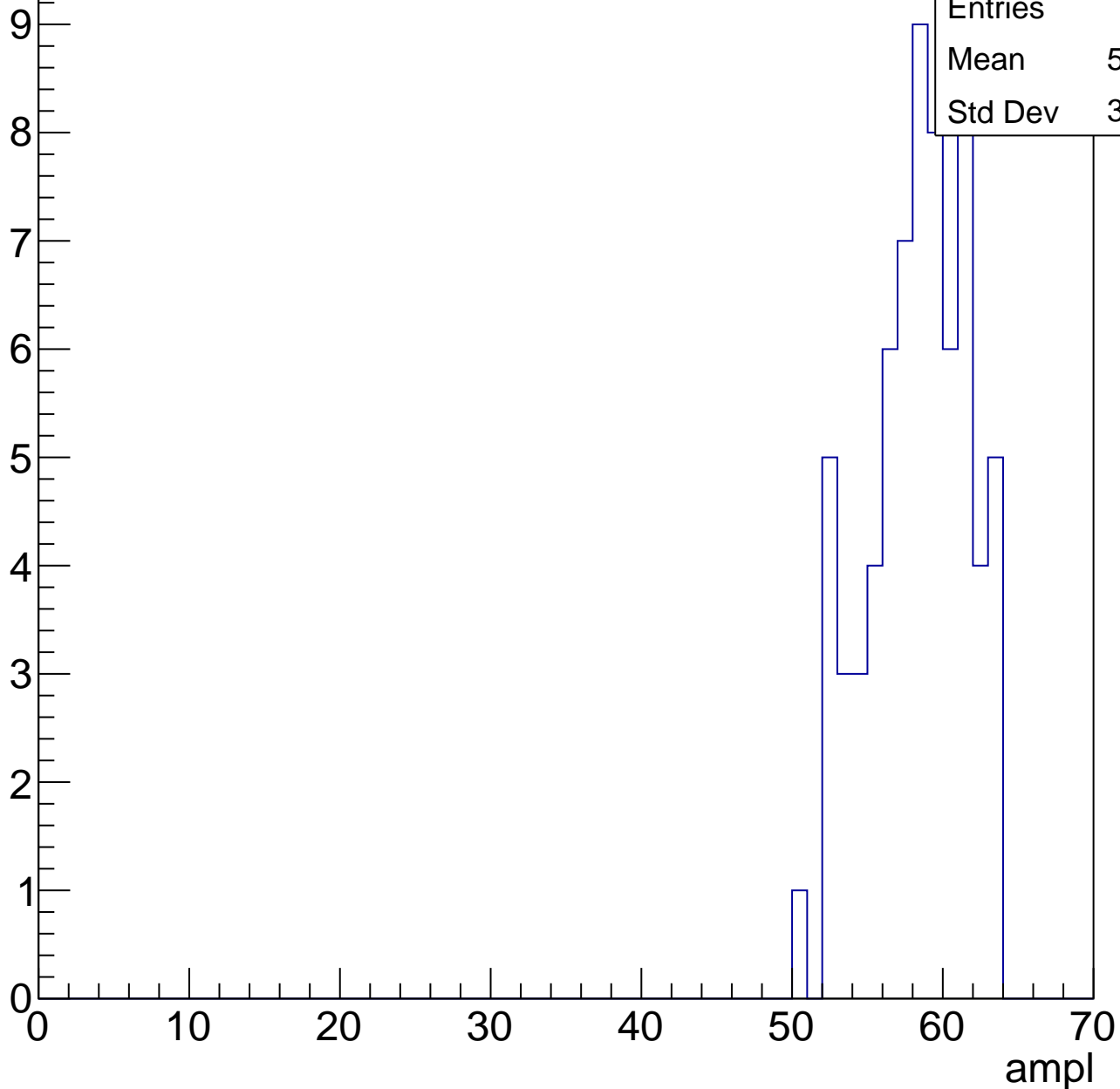
ampl



# B0L002S, U23-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

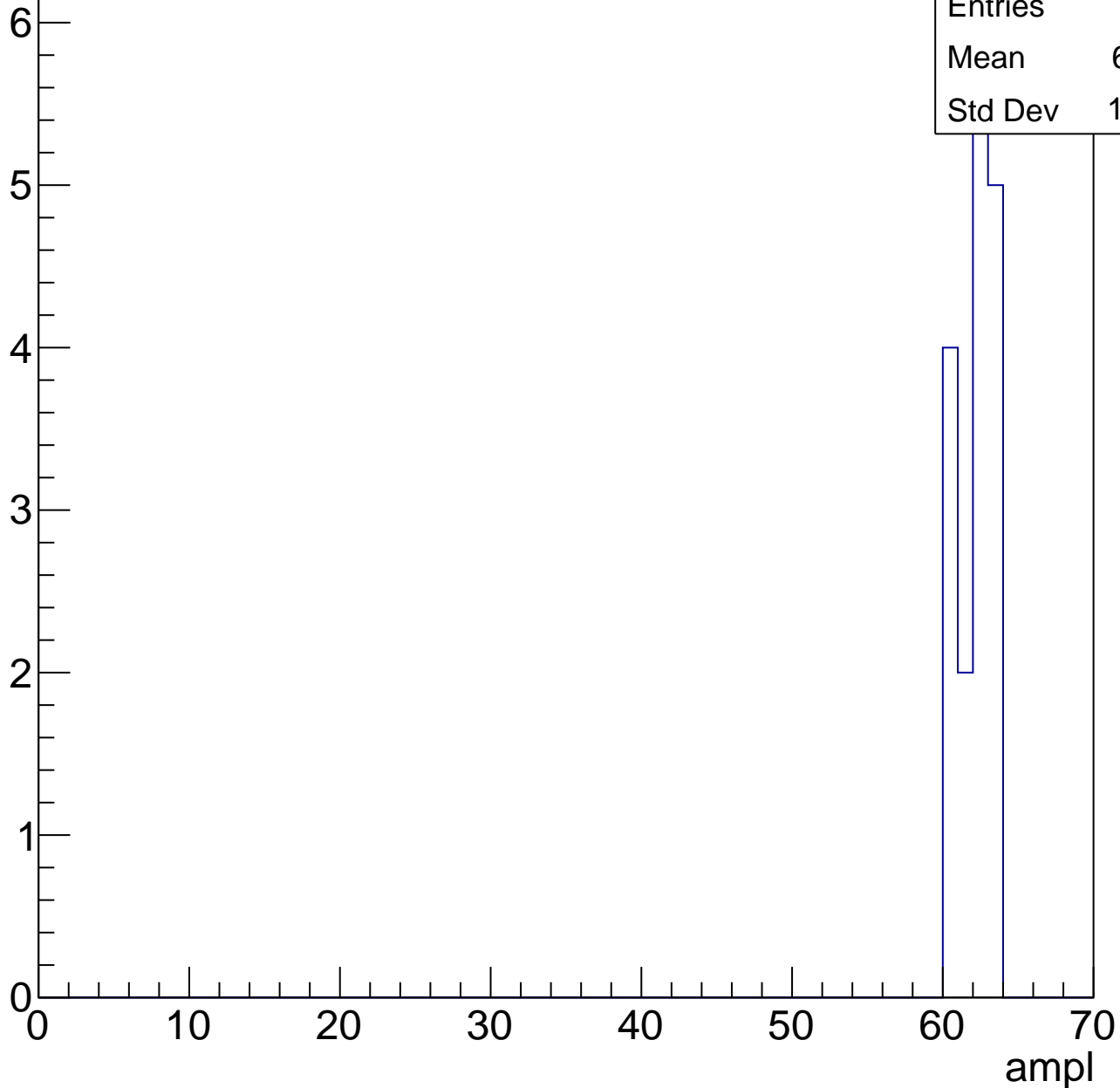
Entry



# B0L002S, U23-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

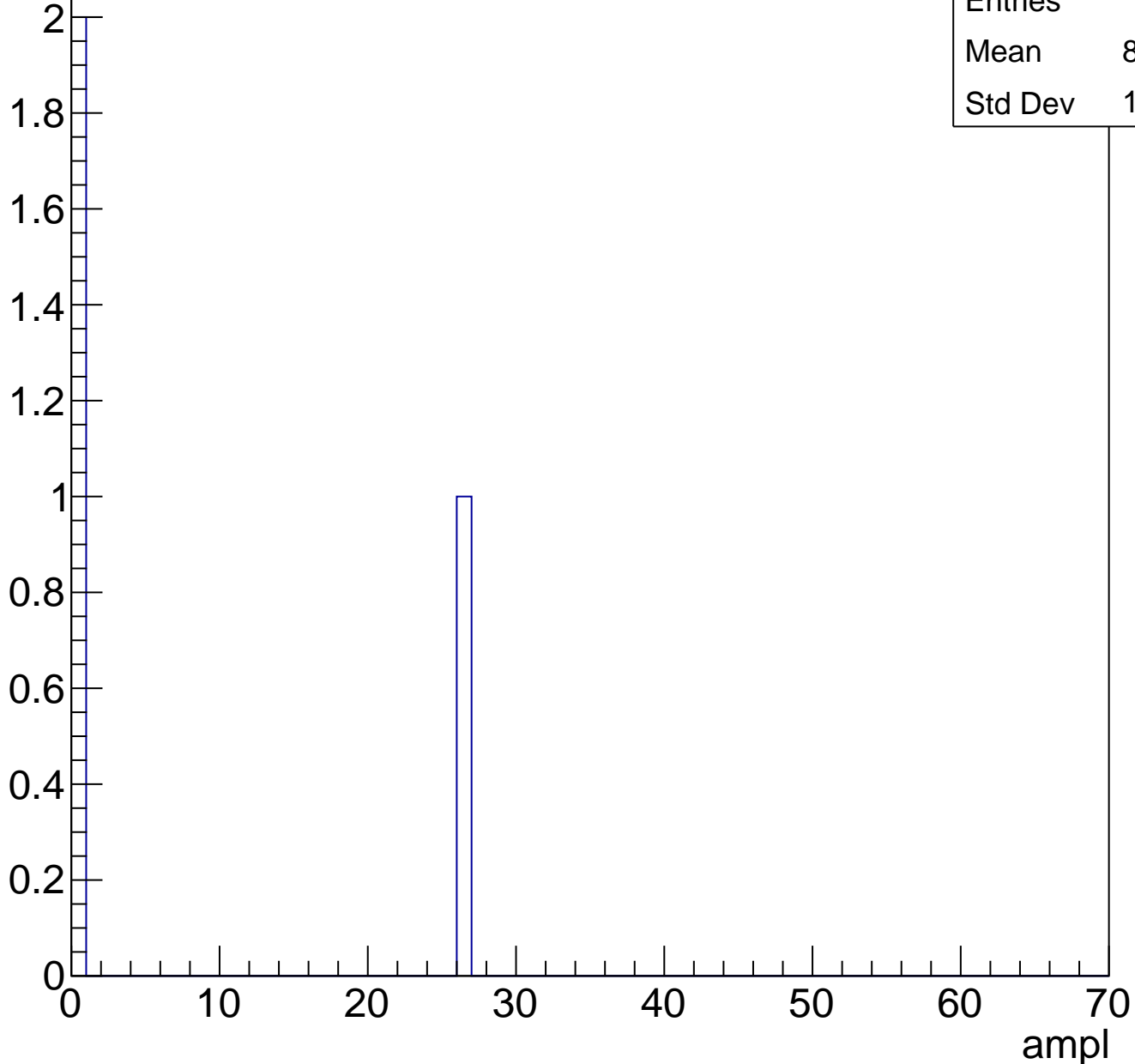




# B0L002S, U23-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8.667
Std Dev	12.26

# B0L002S, U23-ch86, adc0

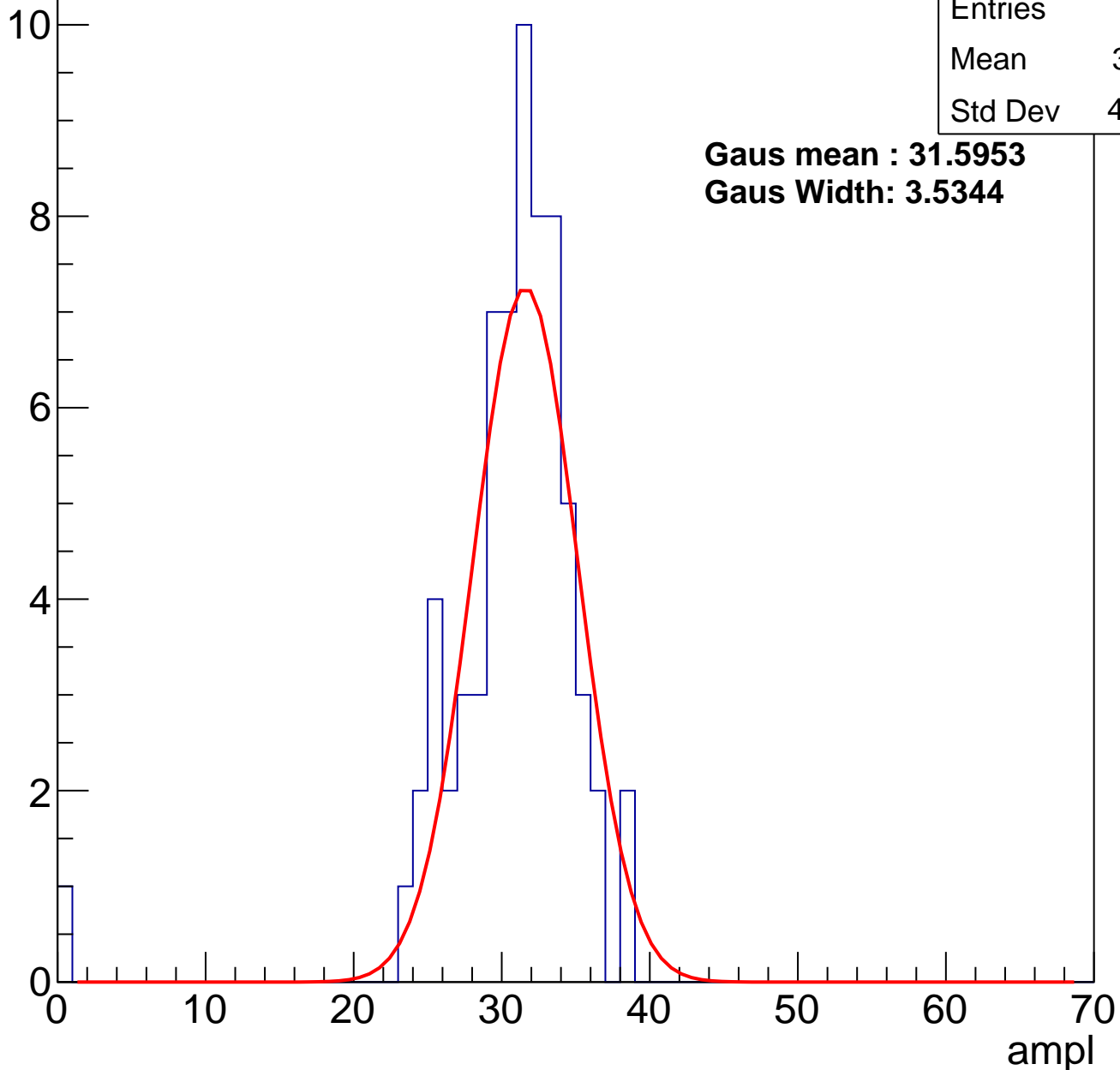
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	68
Mean	30.21
Std Dev	4.948

**Gaus mean : 31.5953**

**Gaus Width: 3.5344**

Entry



# B0L002S, U23-ch86, adc1

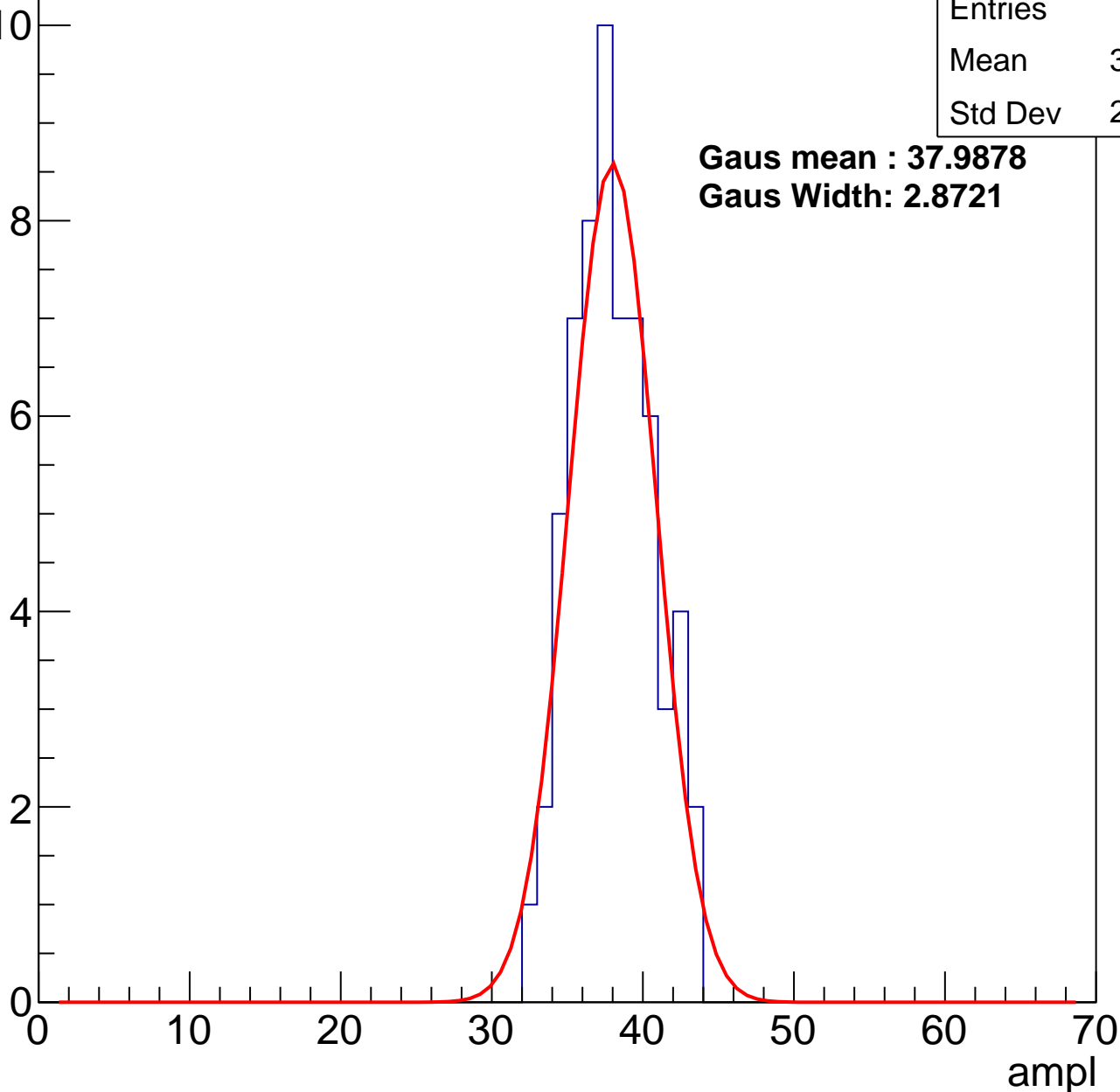
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.53
Std Dev	2.632

**Gaus mean : 37.9878**

**Gaus Width: 2.8721**



# B0L002S, U23-ch86, adc2

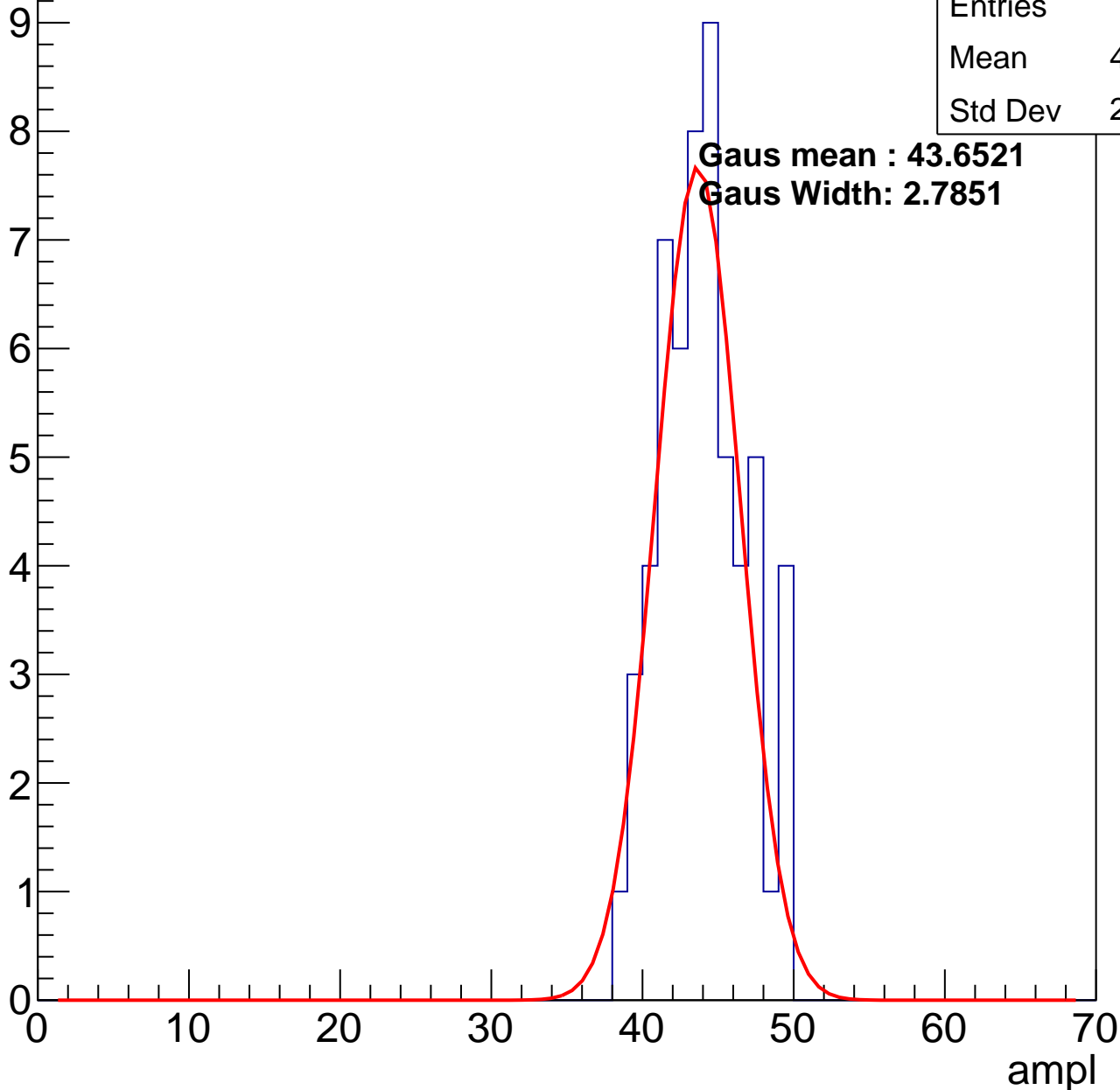
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.54
Std Dev	2.779

**Gaus mean : 43.6521**

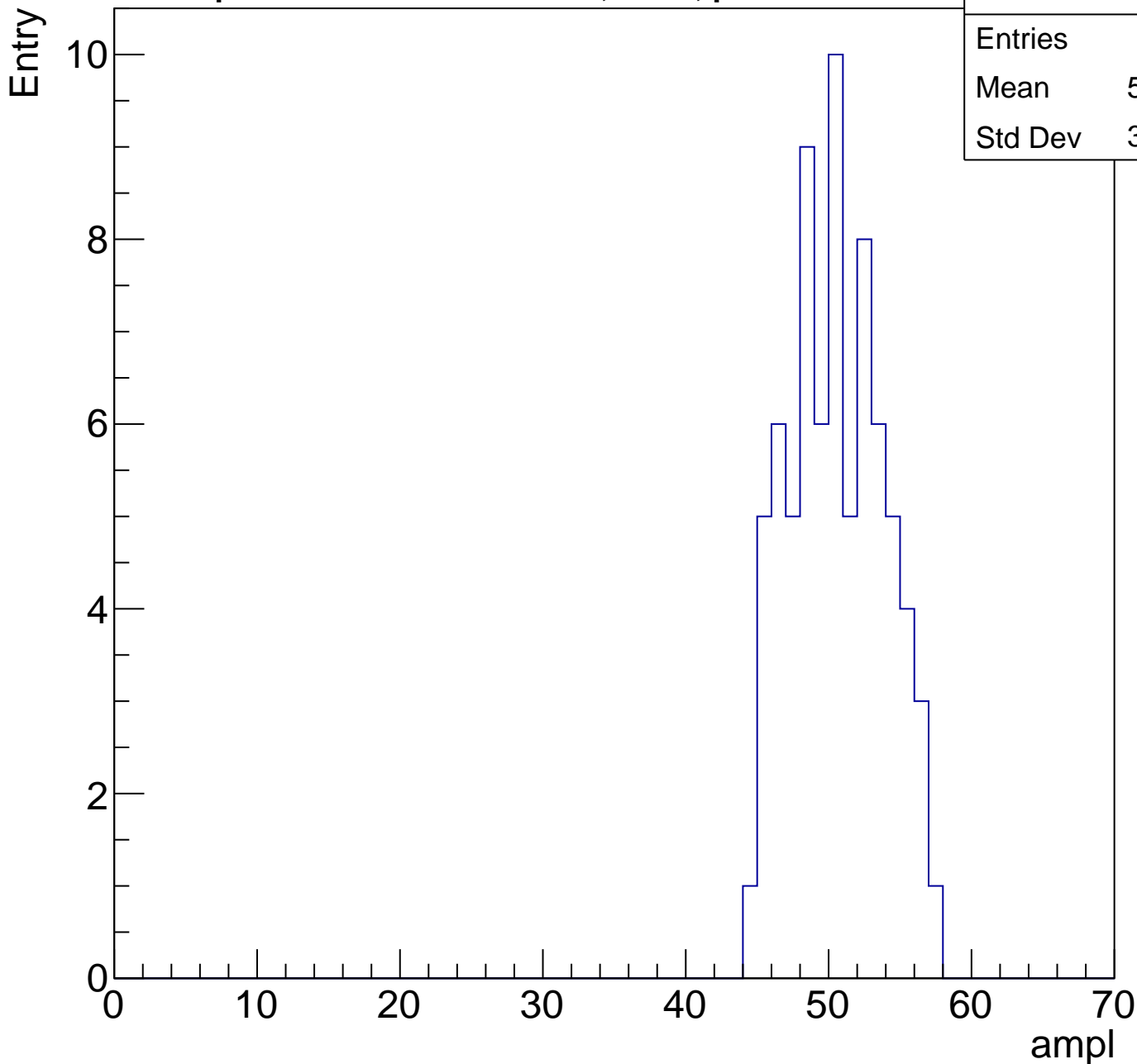
**Gaus Width: 2.7851**



# B0L002S, U23-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

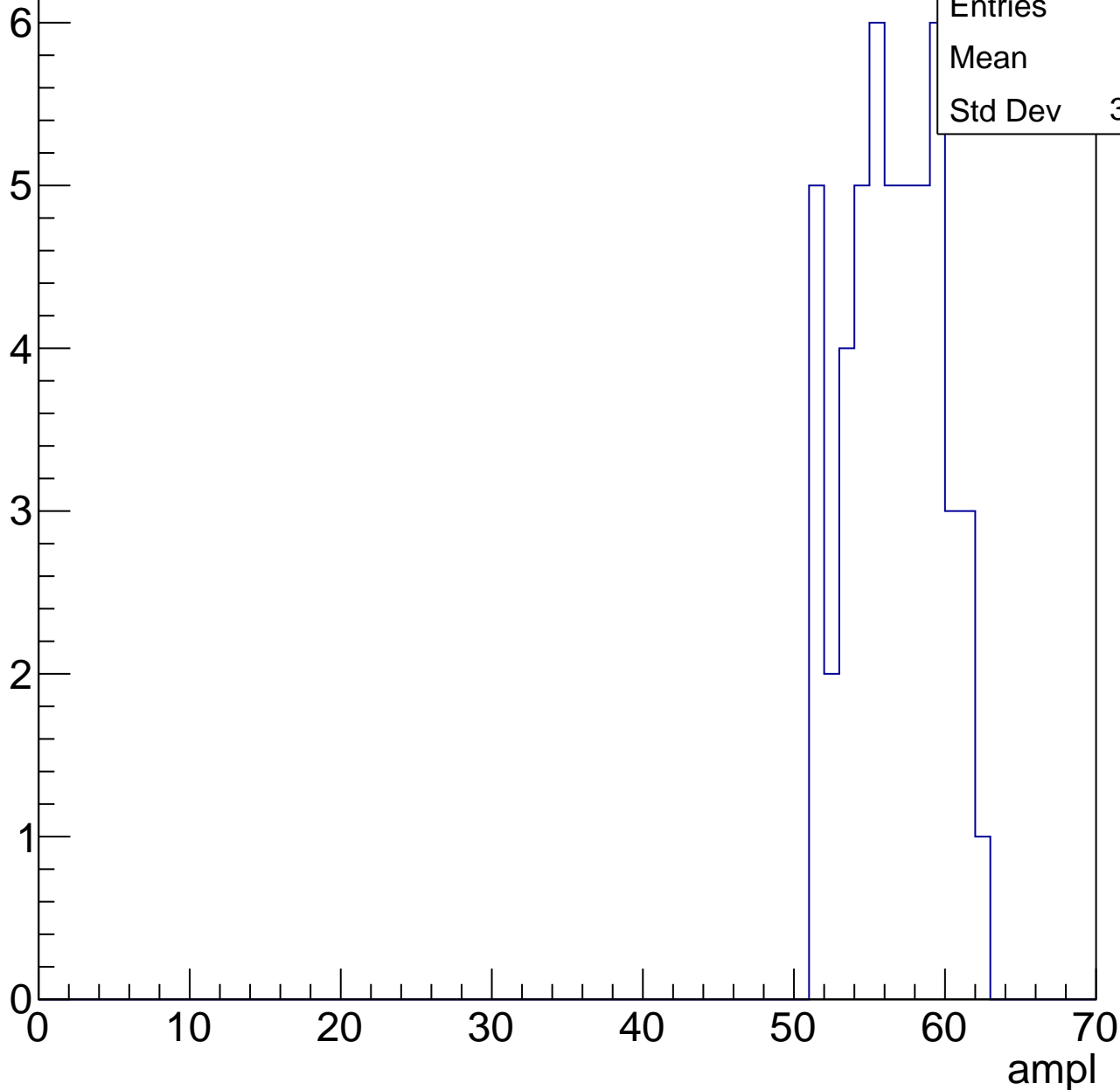
Entries	74
Mean	50.14
Std Dev	3.227



# B0L002S, U23-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



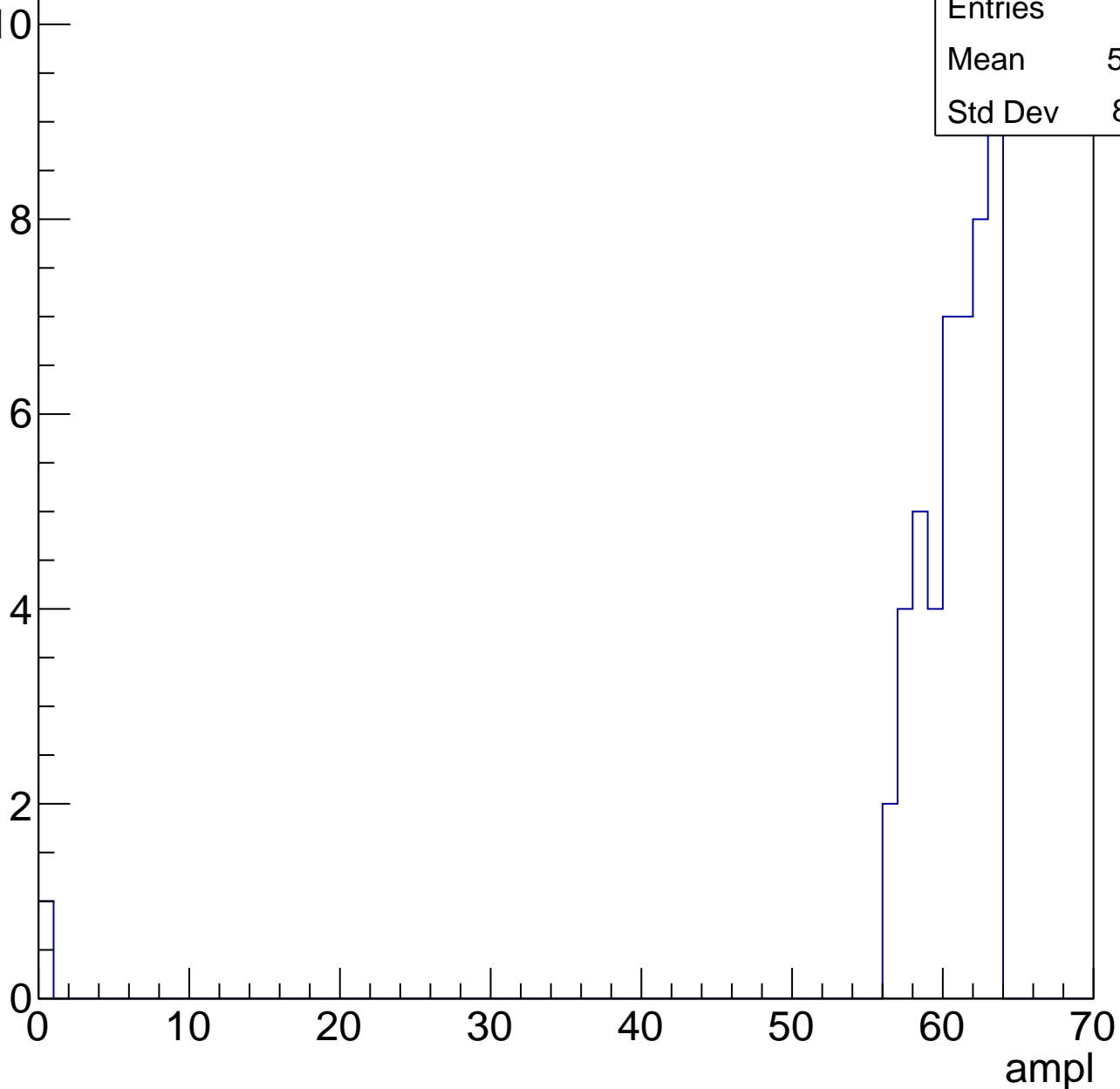
Entries	50
Mean	56.1
Std Dev	3.022

# B0L002S, U23-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	59.15
Std Dev	8.881



# B0L002S, U23-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch87, adc0

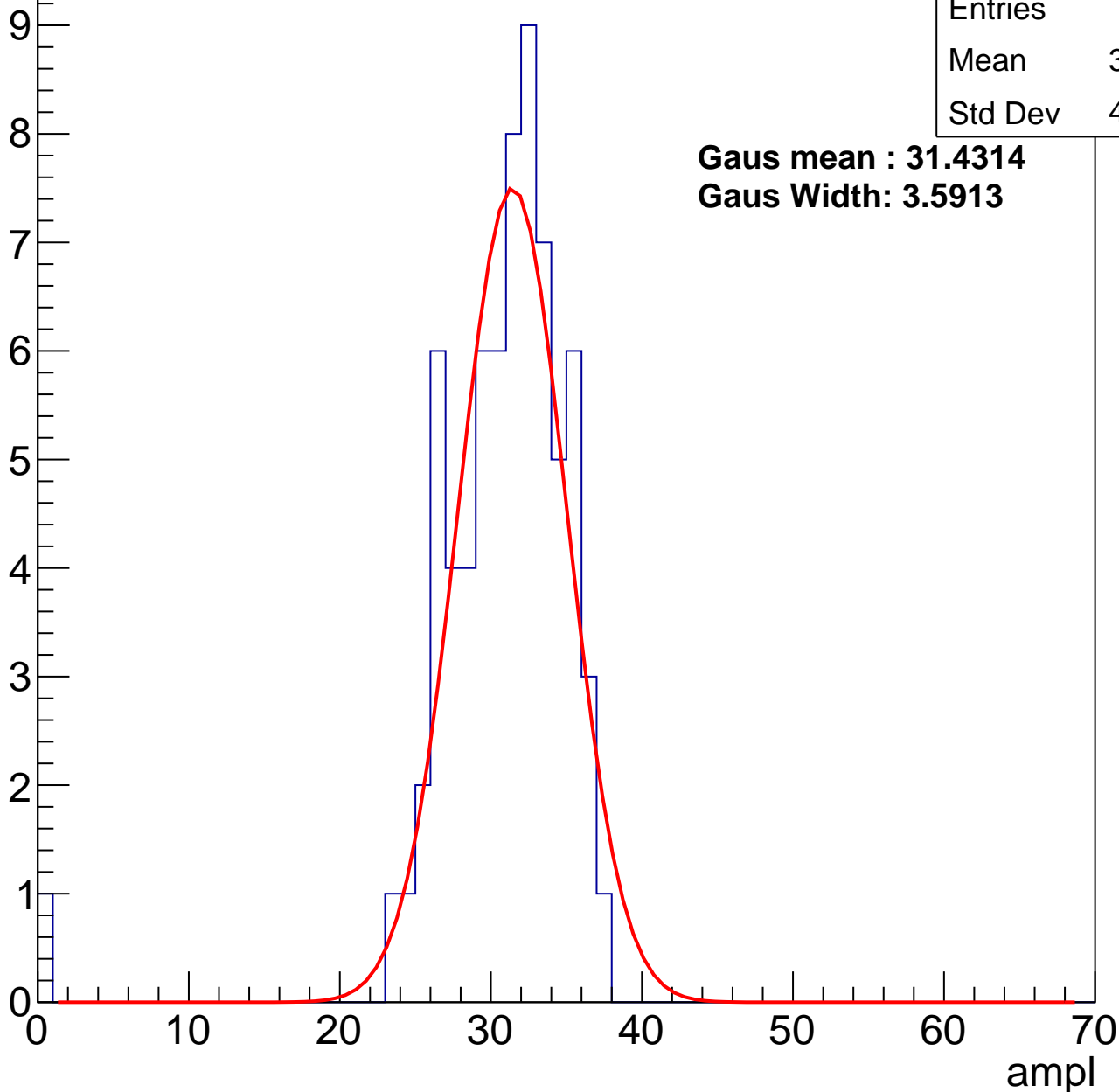
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.27
Std Dev	4.896

**Gaus mean : 31.4314**

**Gaus Width: 3.5913**



# B0L002S, U23-ch87, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.24
Std Dev	3.262

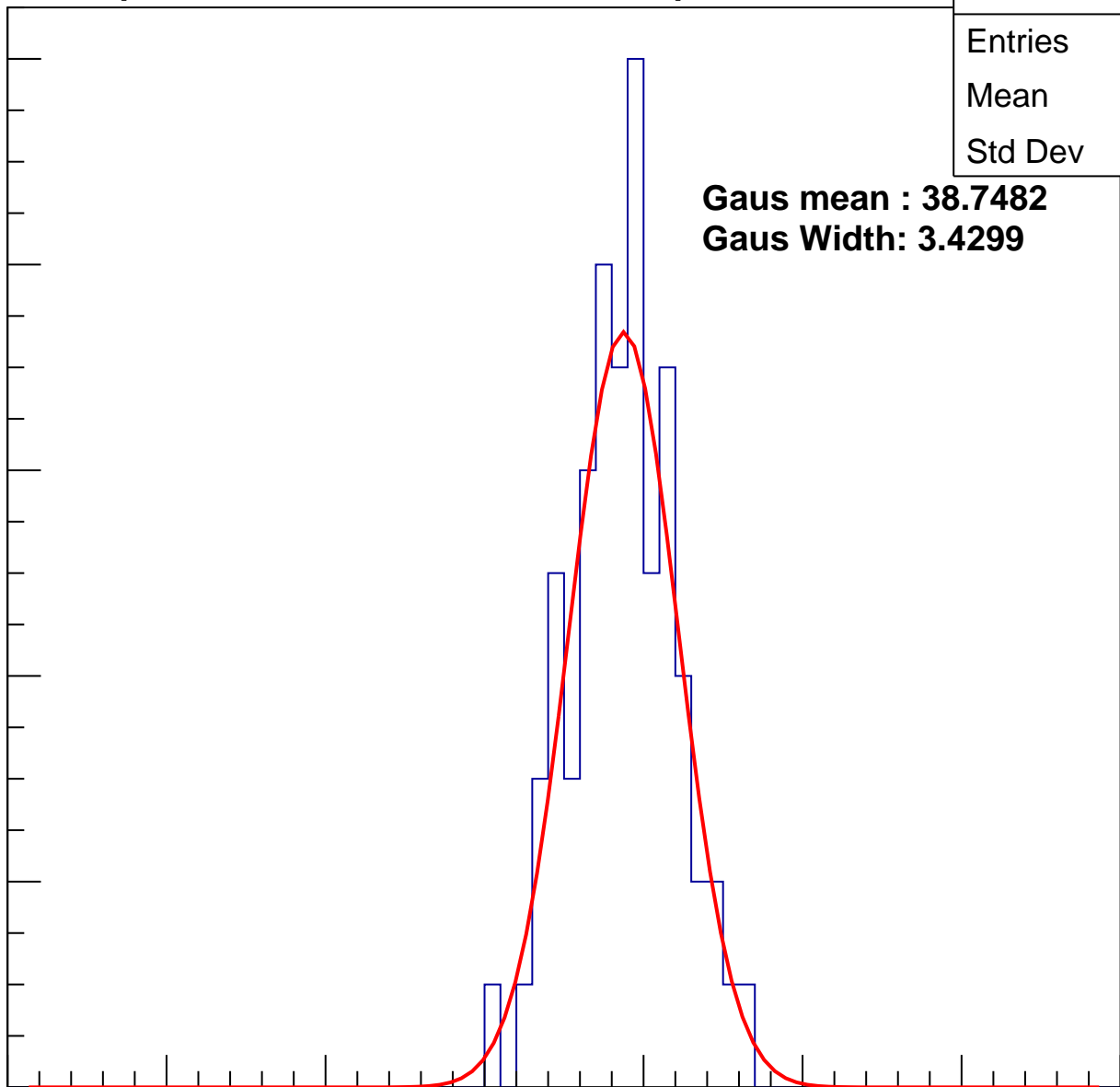
**Gaus mean : 38.7482**

**Gaus Width: 3.4299**

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch87, adc2

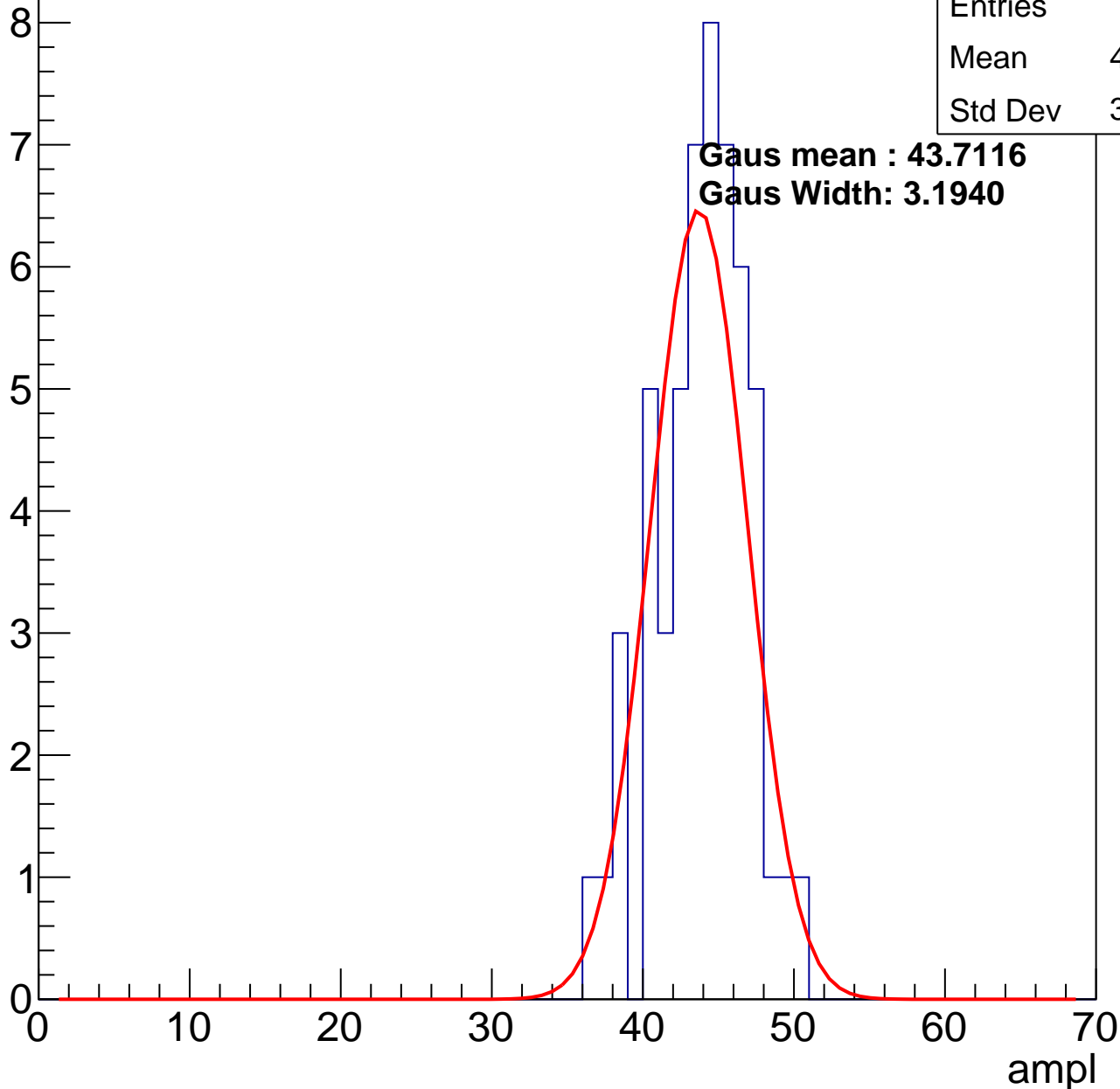
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.44
Std Dev	3.016

**Gaus mean : 43.7116**

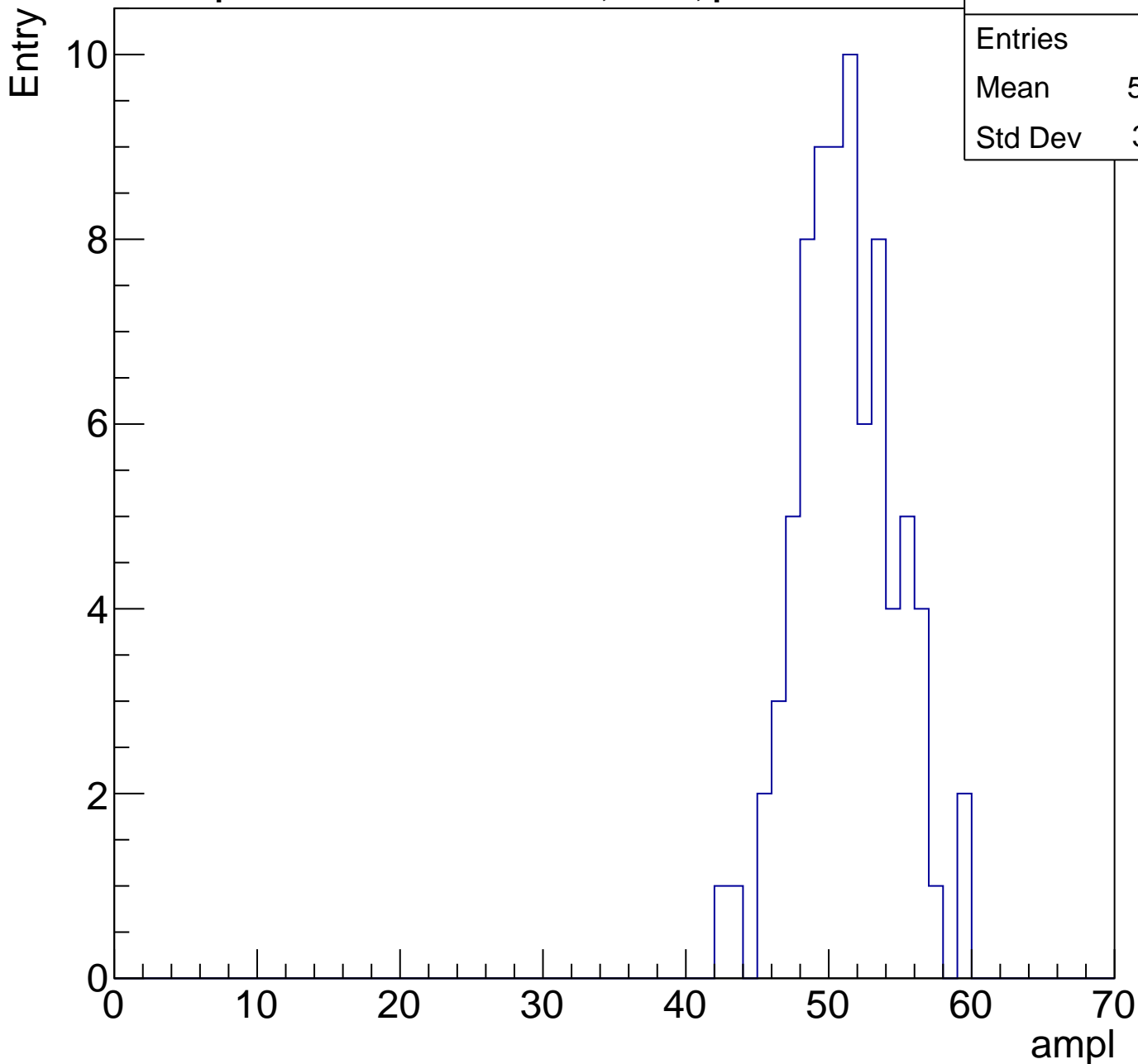
**Gaus Width: 3.1940**



# B0L002S, U23-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	78
Mean	50.76
Std Dev	3.401

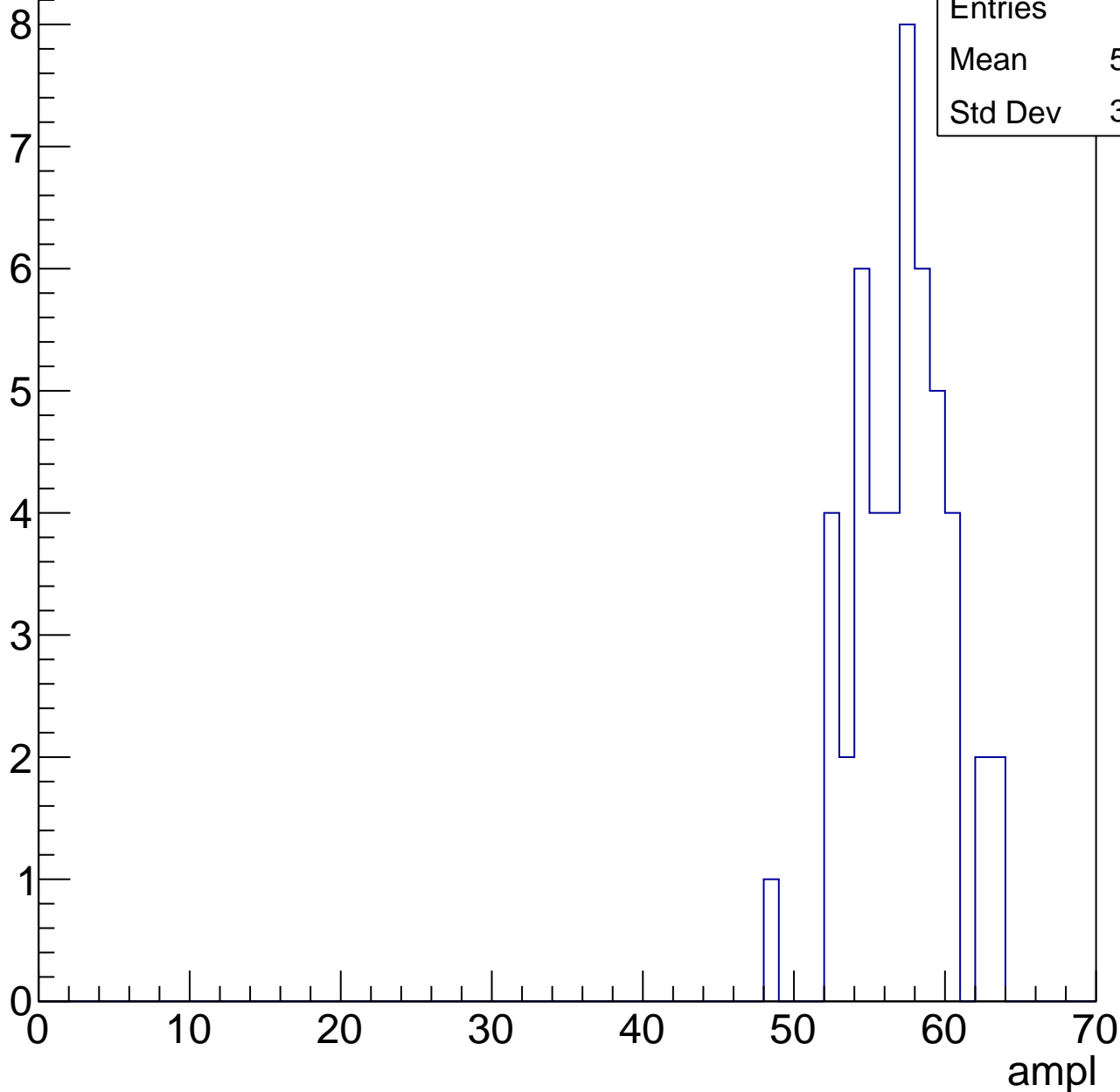


# B0L002S, U23-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	56.65
Std Dev	3.112

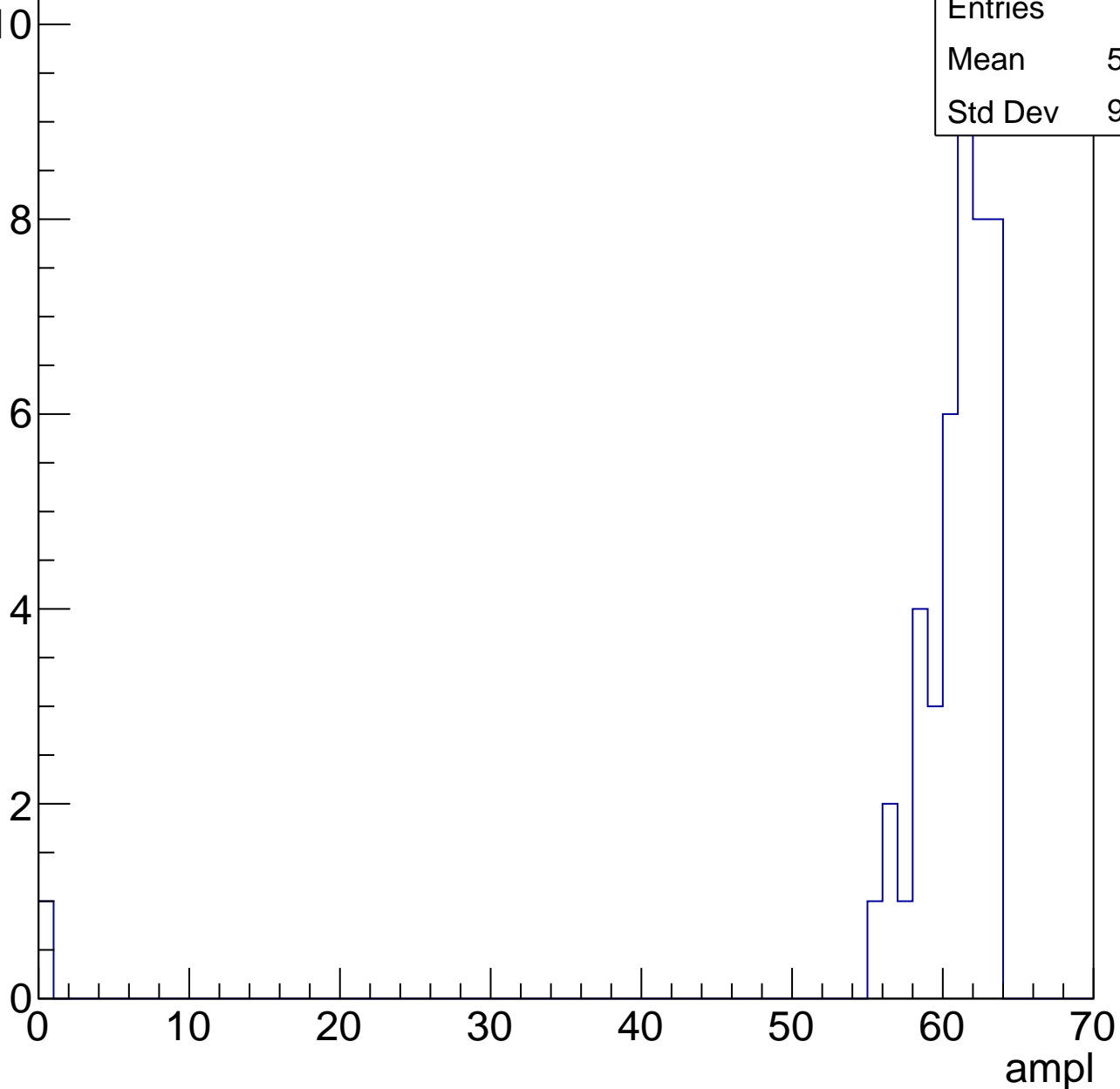


# B0L002S, U23-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	44
Mean	59.16
Std Dev	9.254



# B0L002S, U23-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch88, adc0

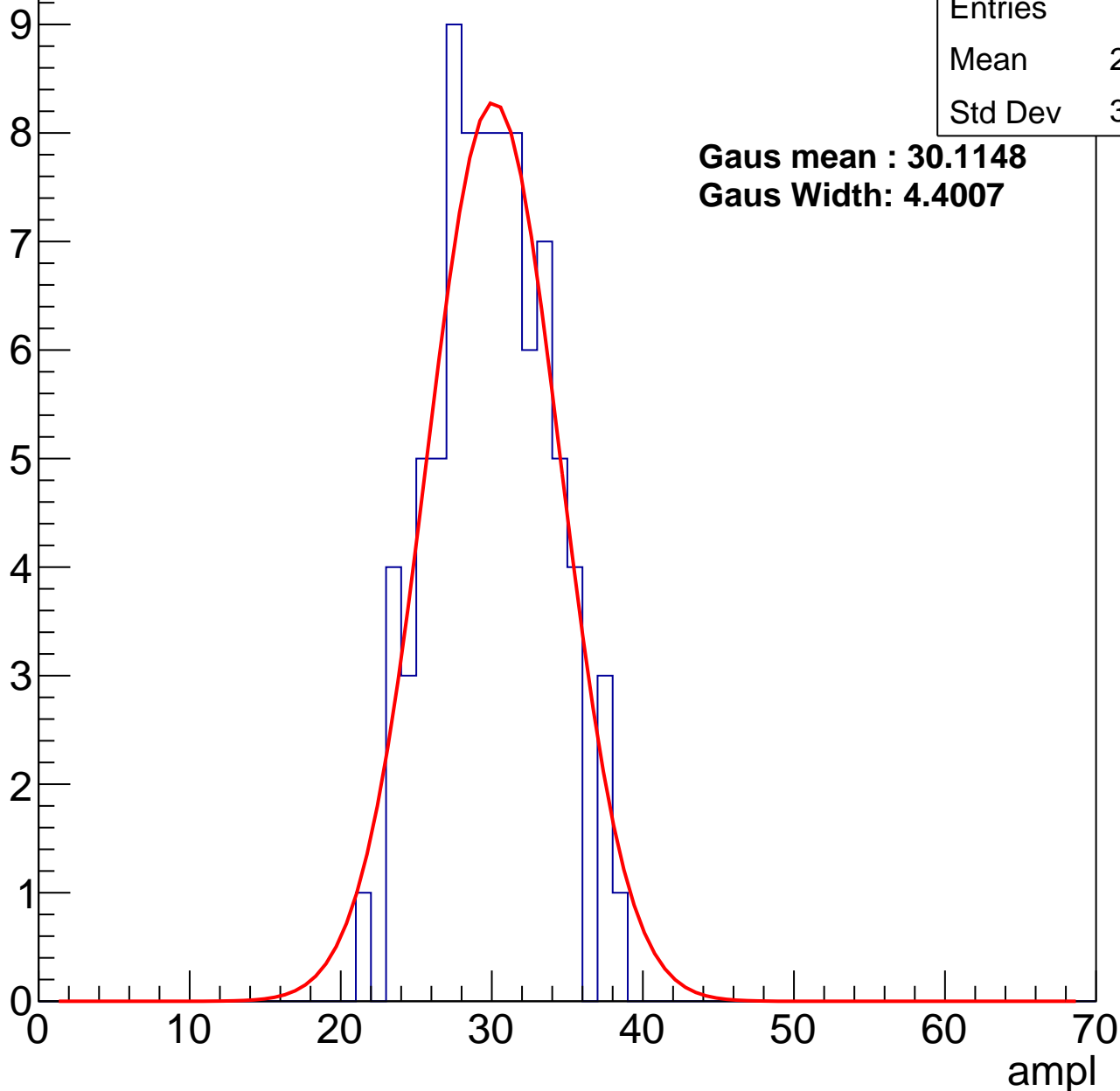
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	85
Mean	29.52
Std Dev	3.722

**Gaus mean : 30.1148**

**Gaus Width: 4.4007**



# B0L002S, U23-ch88, adc1

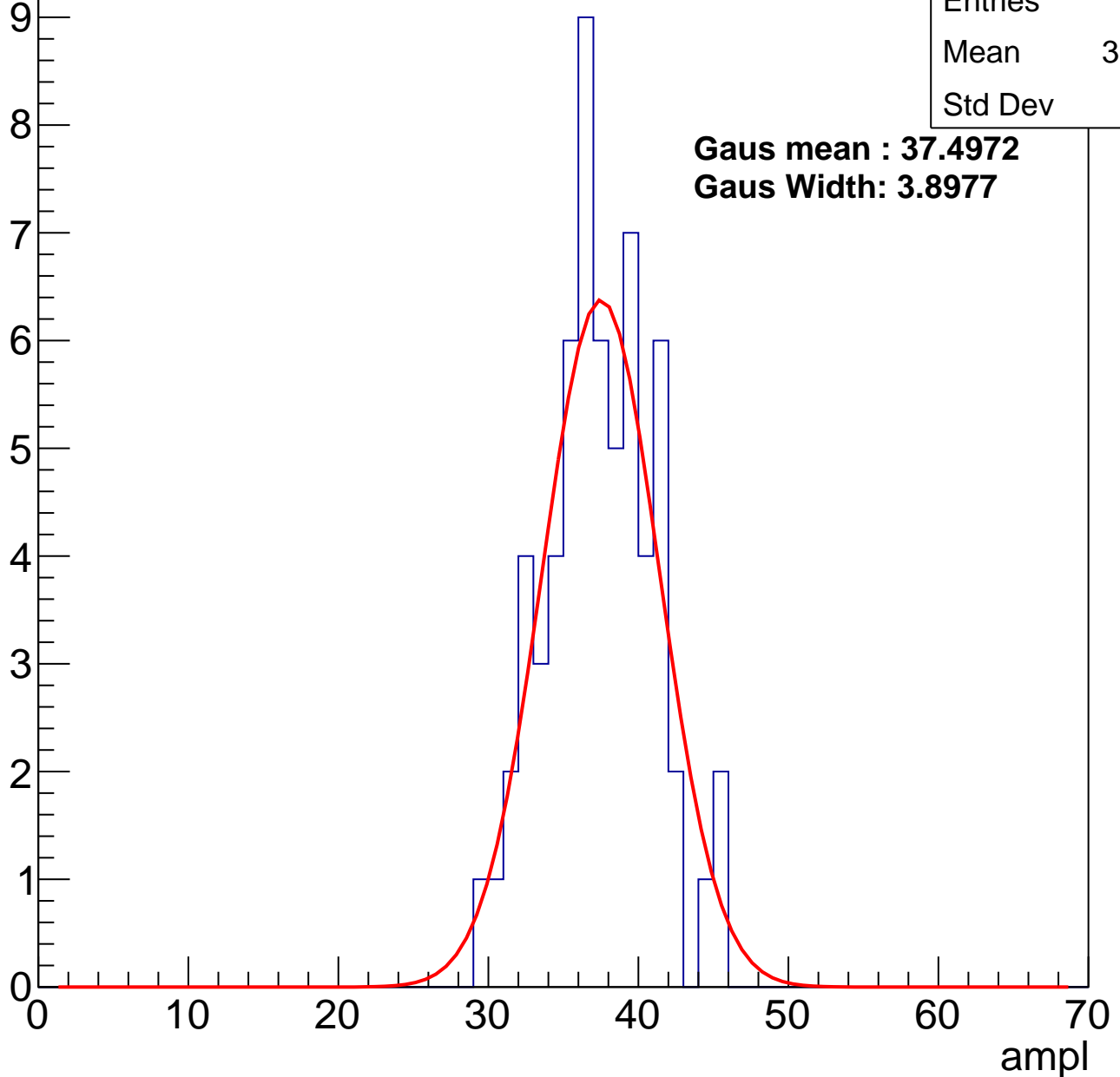
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.94
Std Dev	3.55

**Gaus mean : 37.4972**

**Gaus Width: 3.8977**



# B0L002S, U23-ch88, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	62
Mean	42.85
Std Dev	3.182

**Gaus mean : 43.5980**

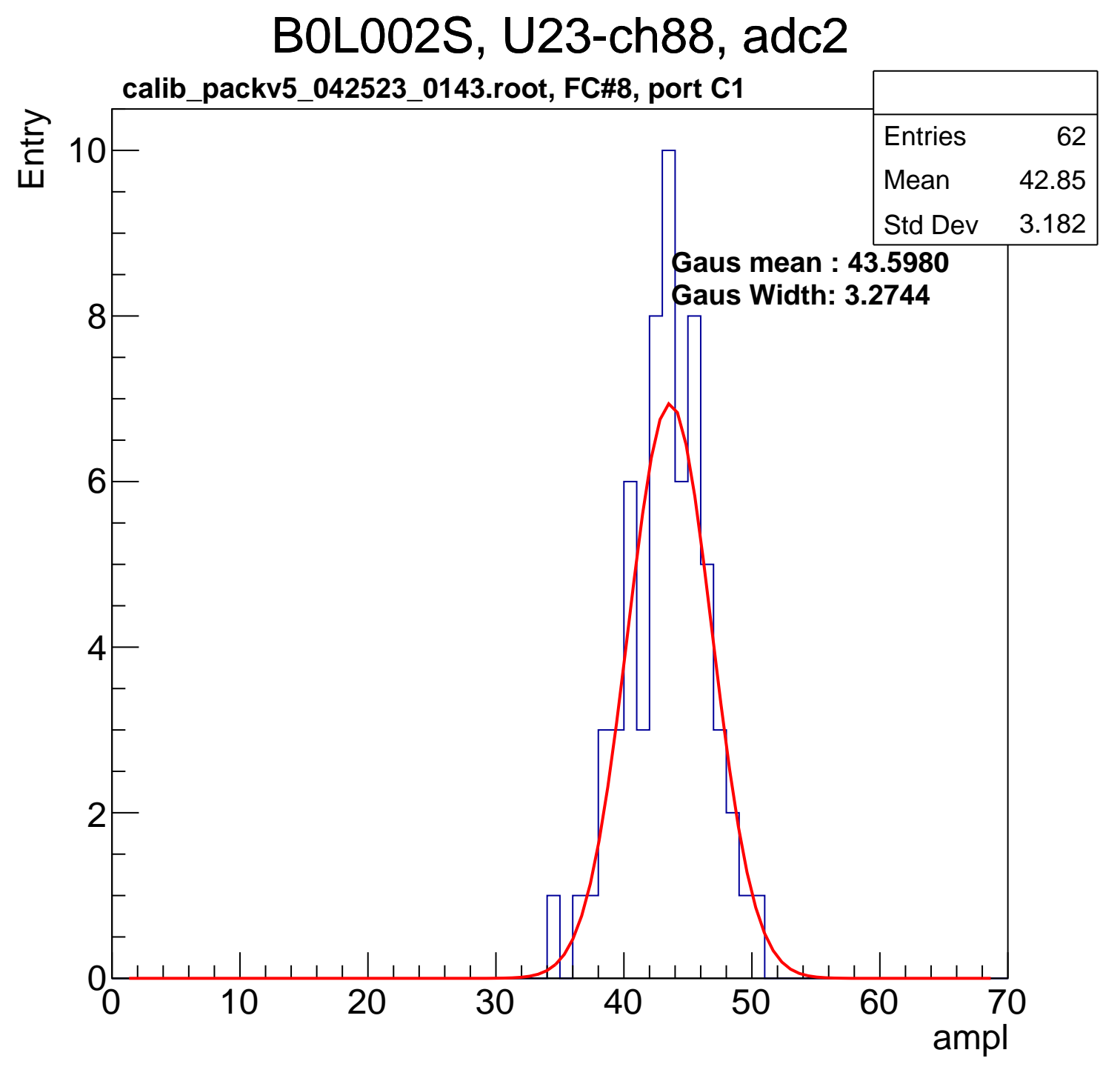
**Gaus Width: 3.2744**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

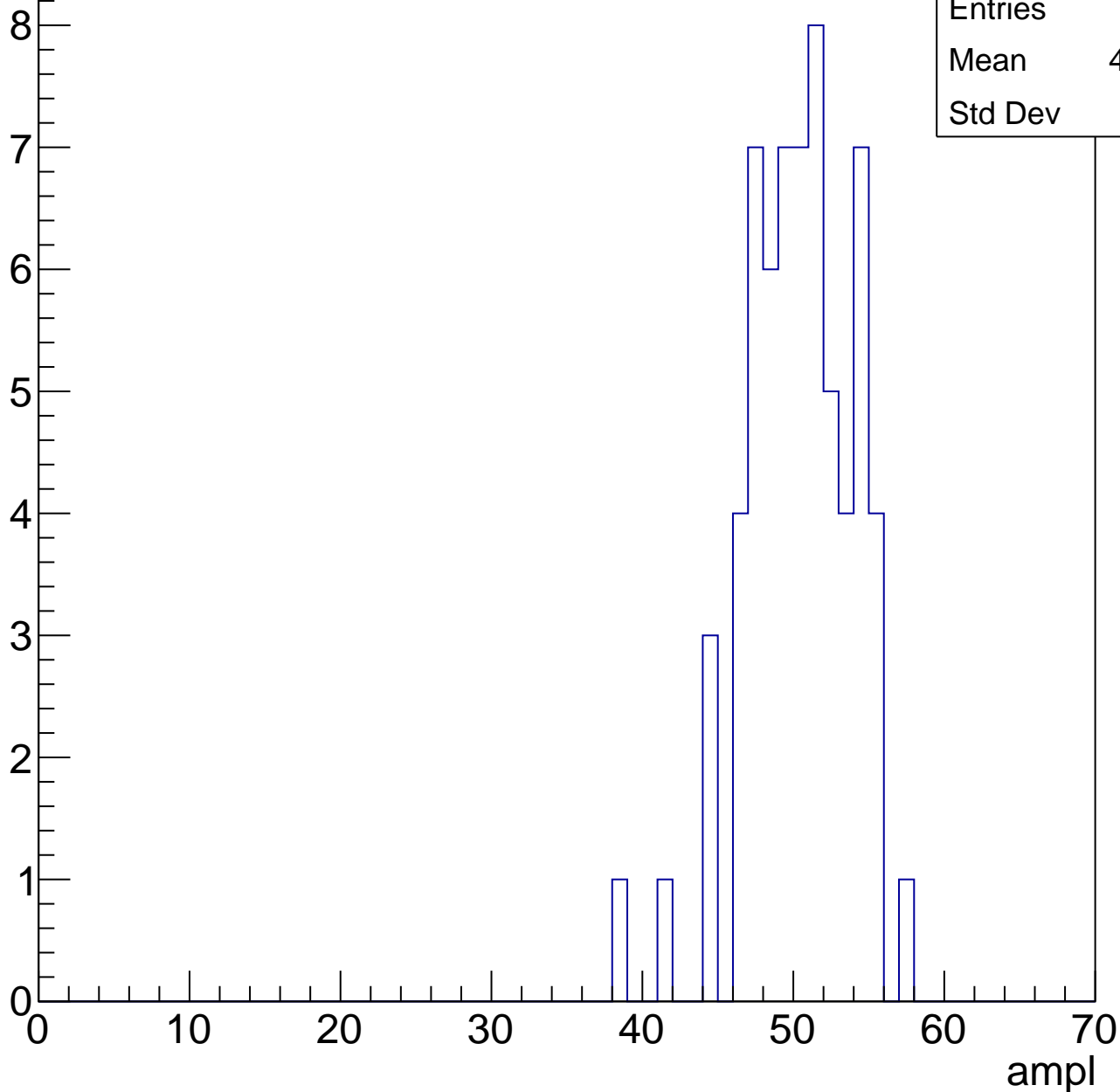


# B0L002S, U23-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.85
Std Dev	3.54

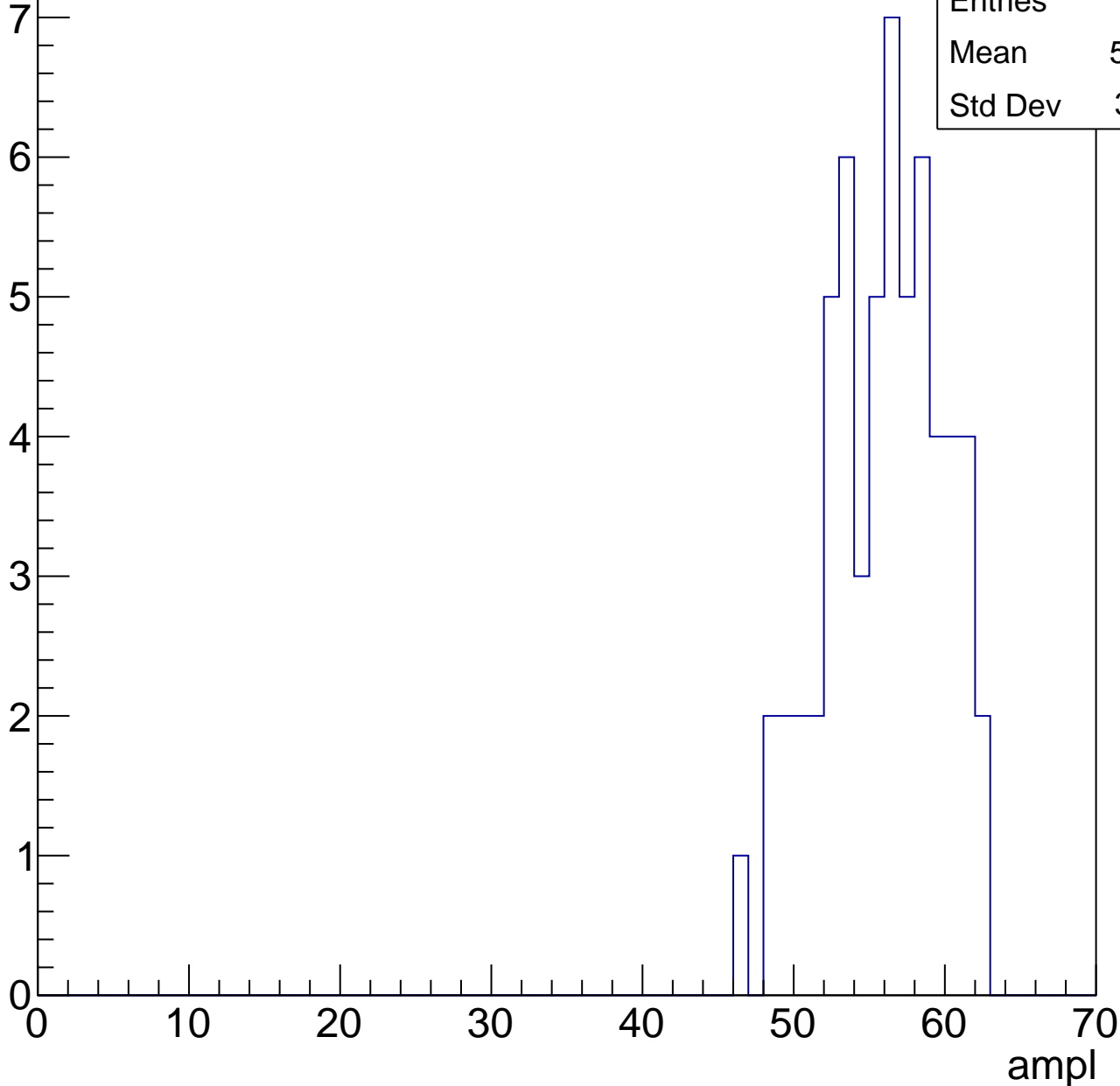


# B0L002S, U23-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	55.43
Std Dev	3.831

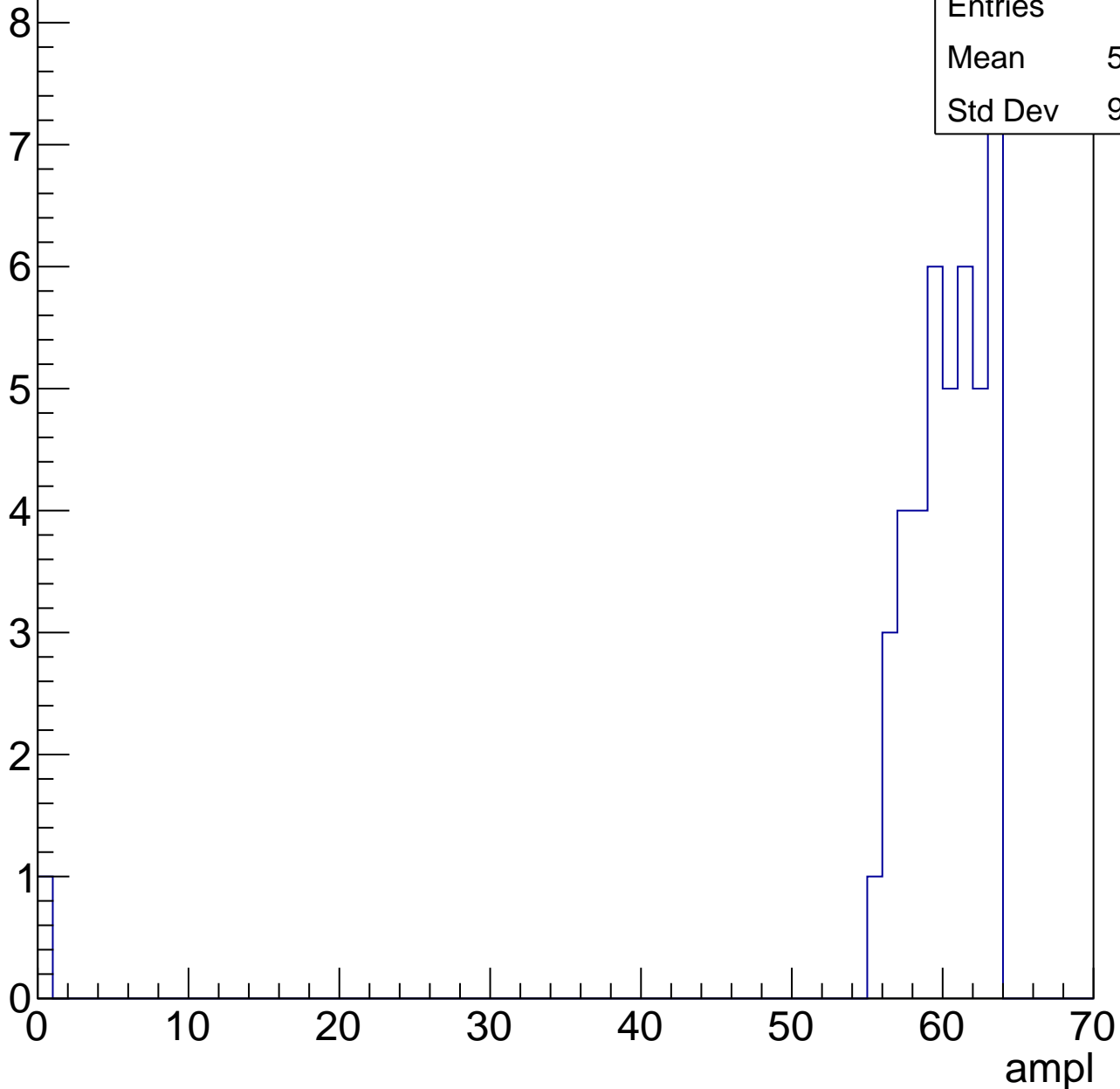


# B0L002S, U23-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

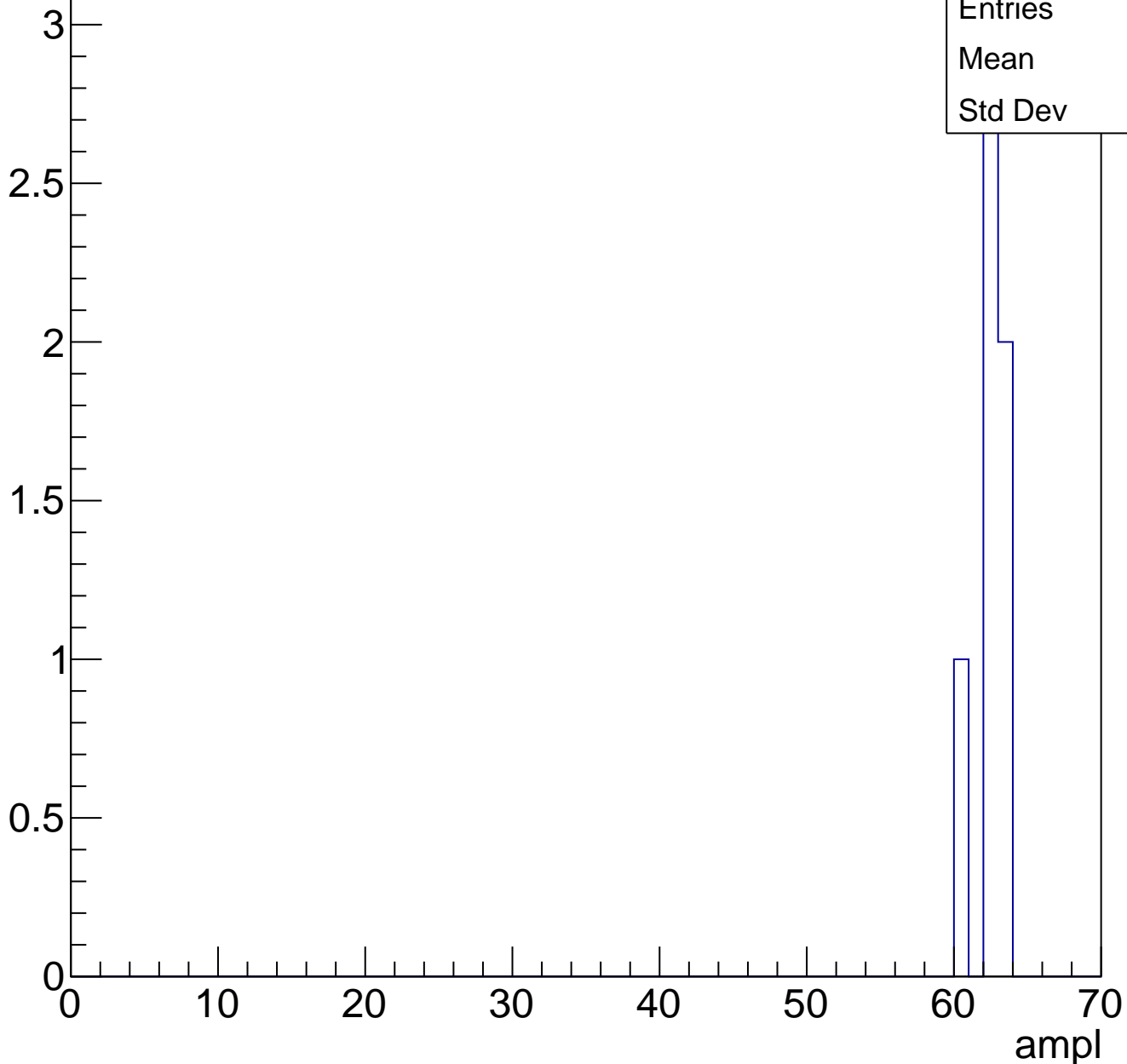
Entries	43
Mean	58.53
Std Dev	9.322



# B0L002S, U23-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch89, adc0

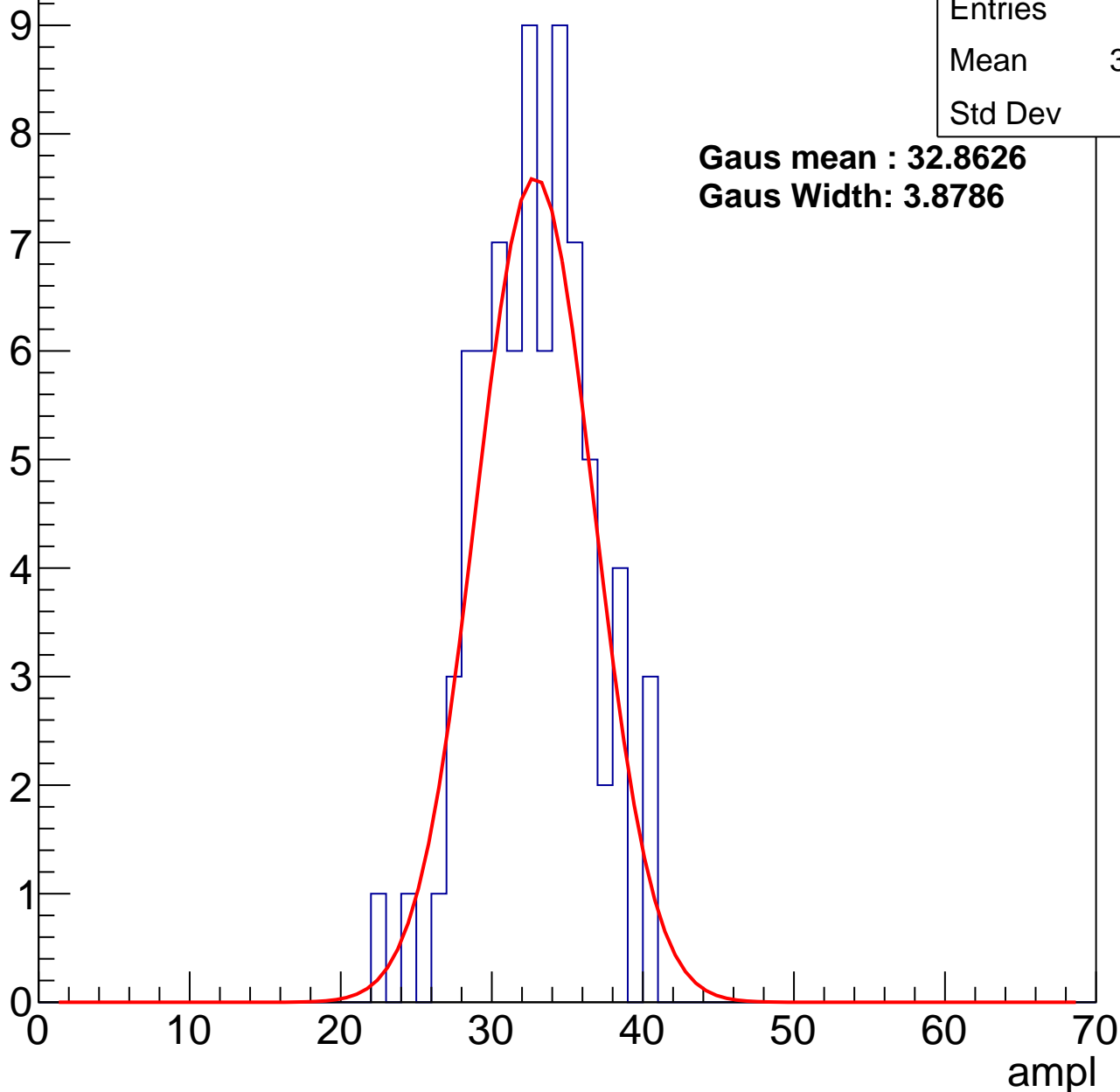
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	32.29
Std Dev	3.67

**Gaus mean : 32.8626**

**Gaus Width: 3.8786**



# B0L002S, U23-ch89, adc1

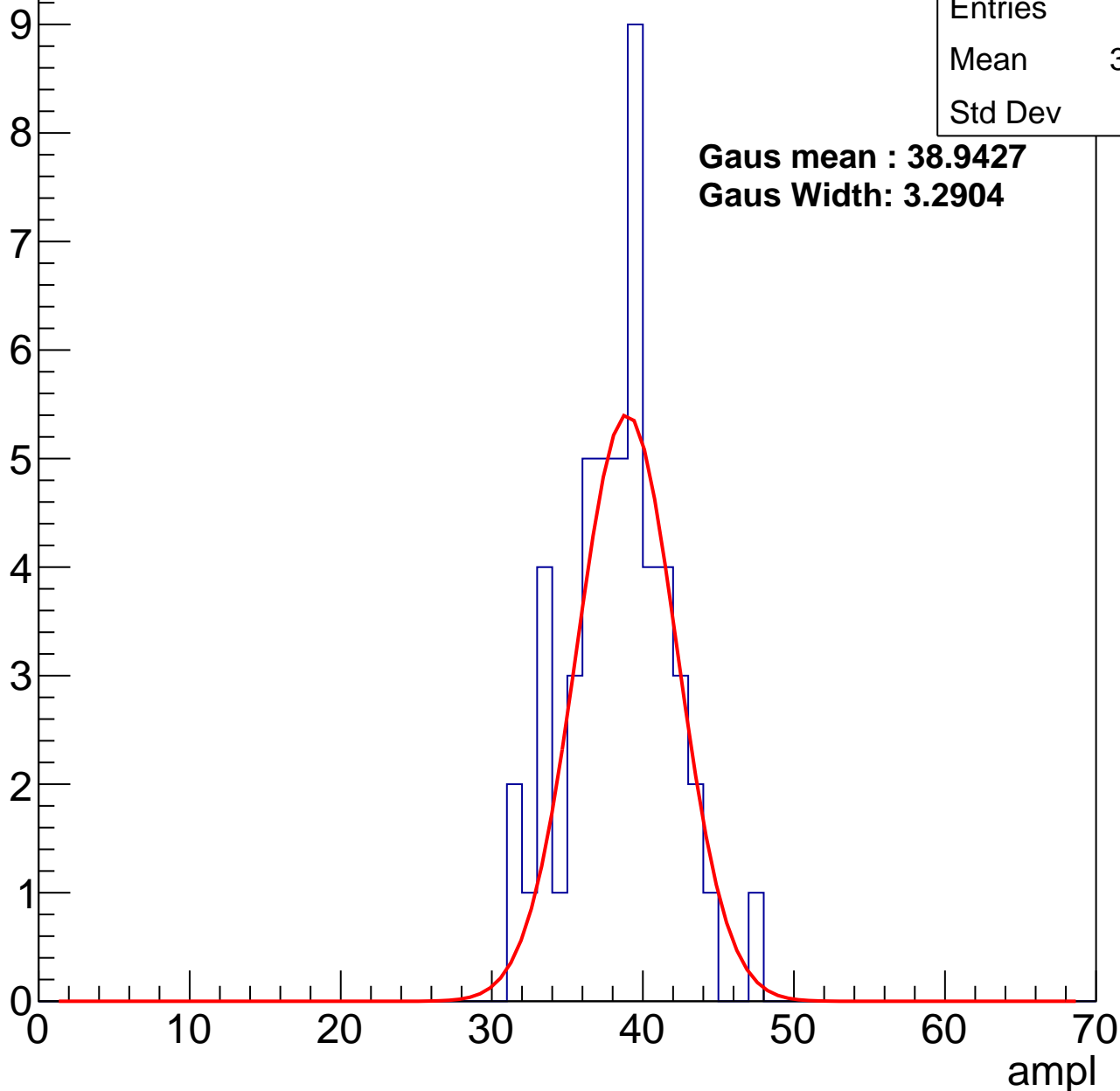
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	37.96
Std Dev	3.4

**Gaus mean : 38.9427**

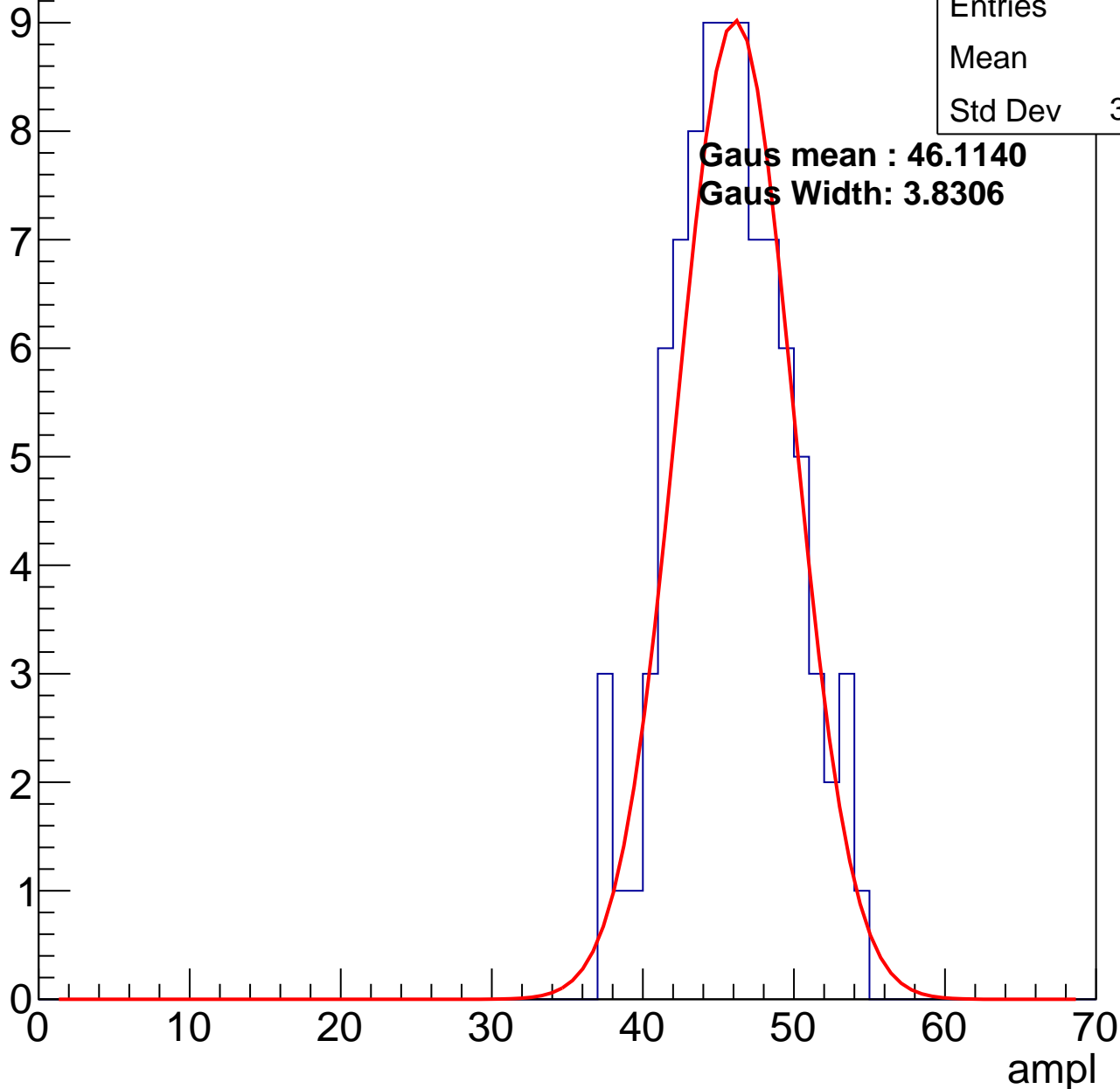
**Gaus Width: 3.2904**



# B0L002S, U23-ch89, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

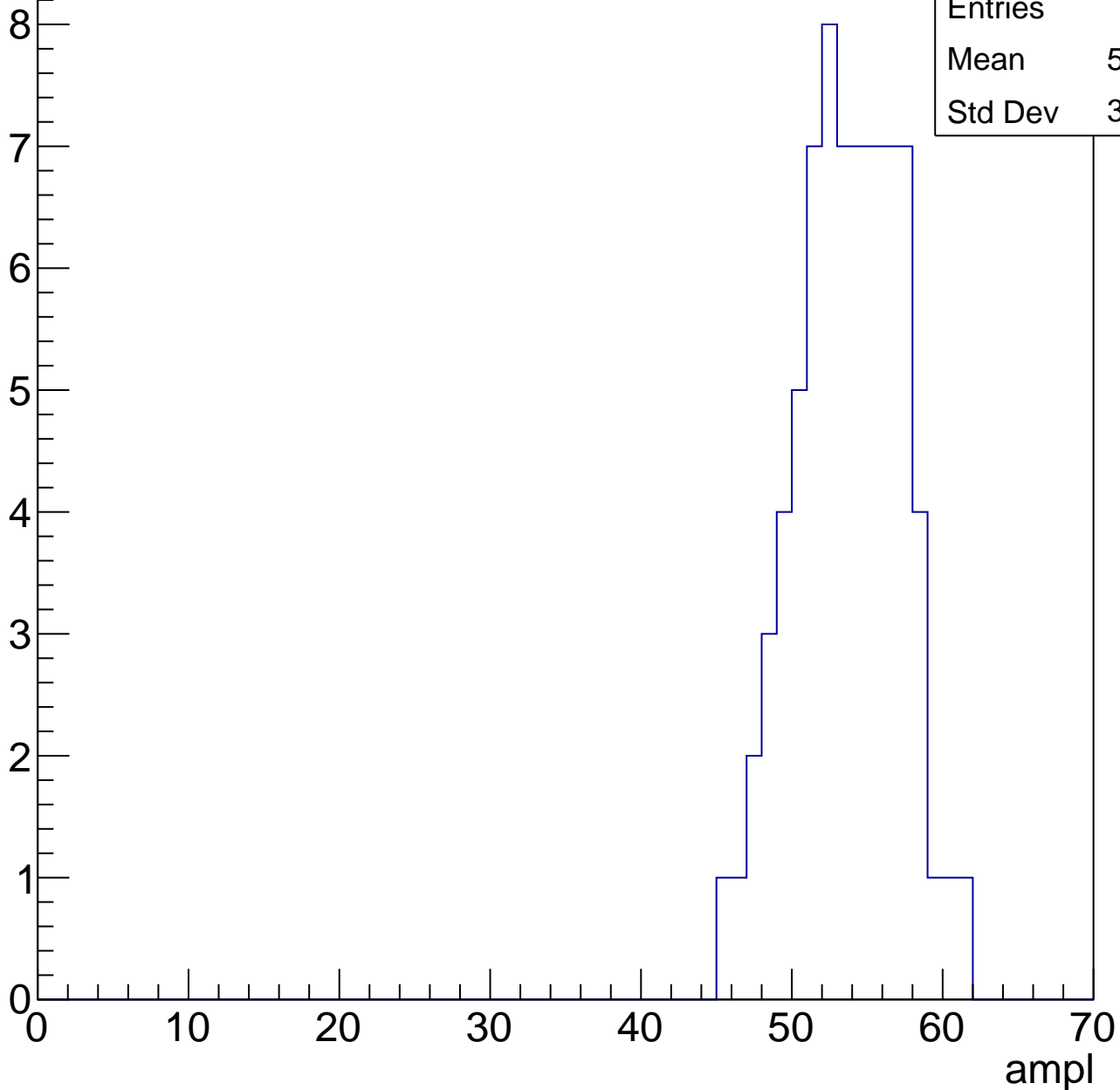


# B0L002S, U23-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

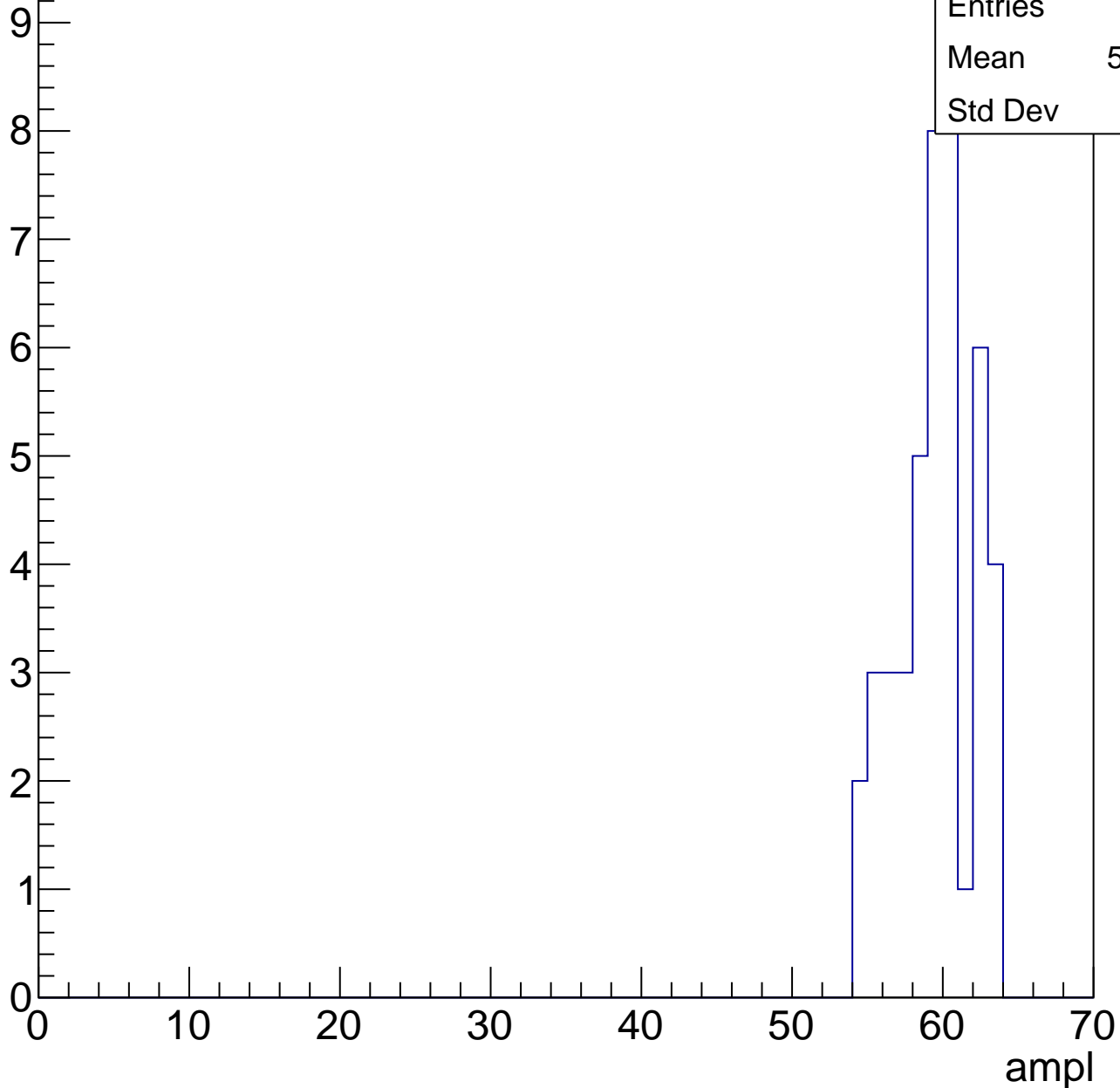
Entries	73
Mean	53.22
Std Dev	3.433



# B0L002S, U23-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

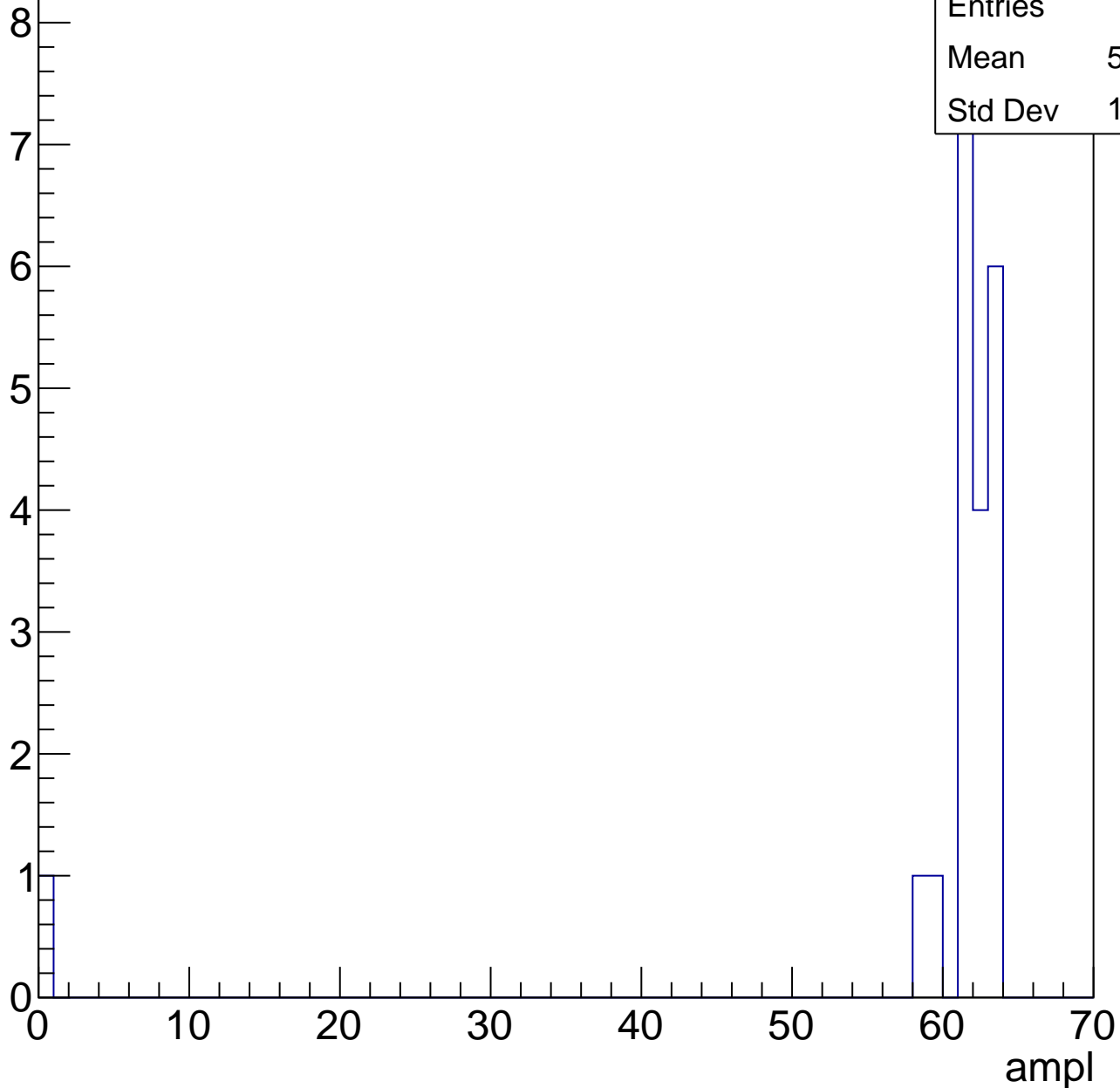


# B0L002S, U23-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.62
Std Dev	13.17



# B0L002S, U23-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch90, adc0

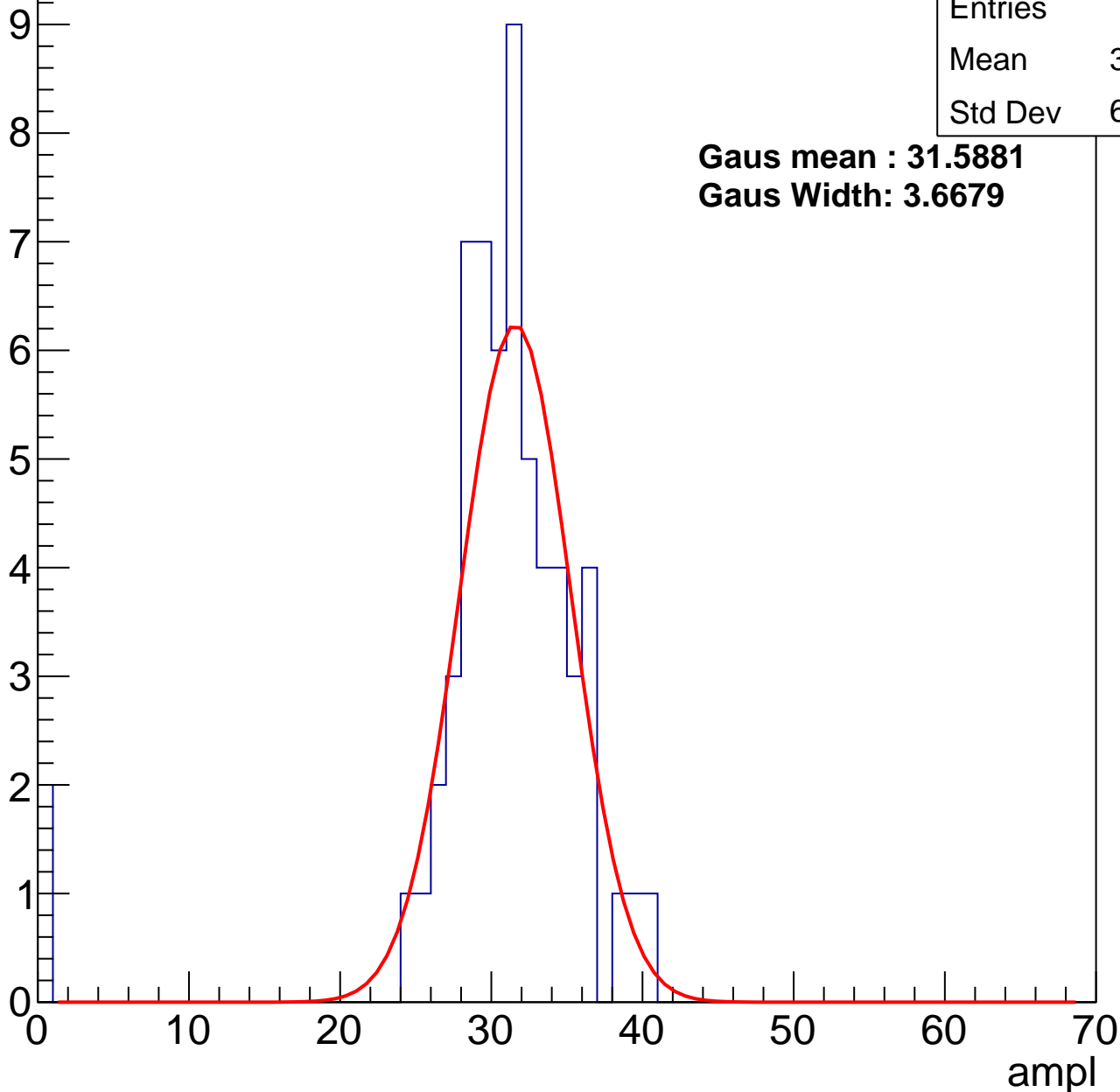
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	30.07
Std Dev	6.463

**Gaus mean : 31.5881**

**Gaus Width: 3.6679**



# B0L002S, U23-ch90, adc1

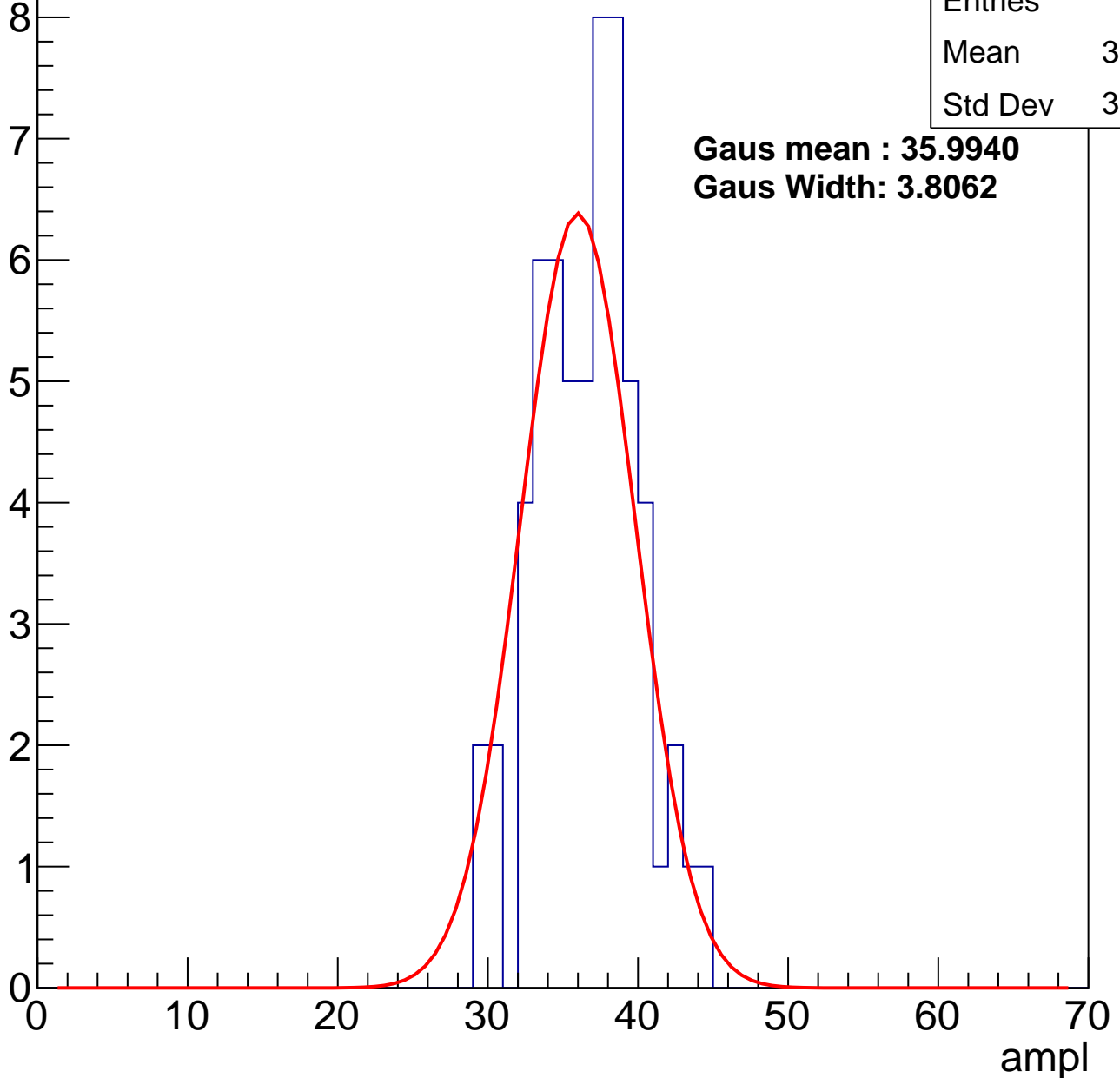
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.17
Std Dev	3.352

**Gaus mean : 35.9940**

**Gaus Width: 3.8062**



# B0L002S, U23-ch90, adc2

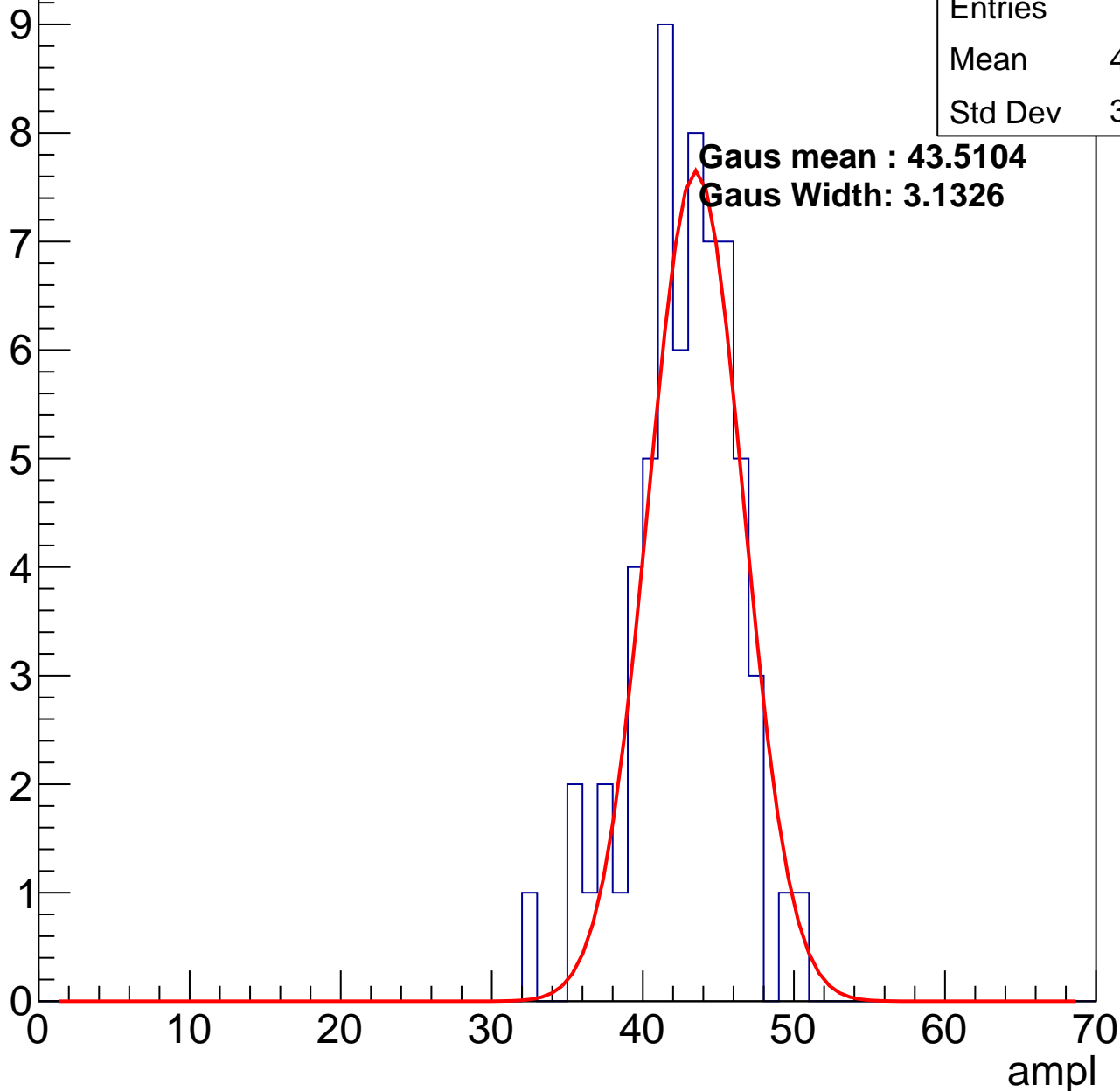
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.29
Std Dev	3.397

**Gaus mean : 43.5104**

**Gaus Width: 3.1326**



# B0L002S, U23-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

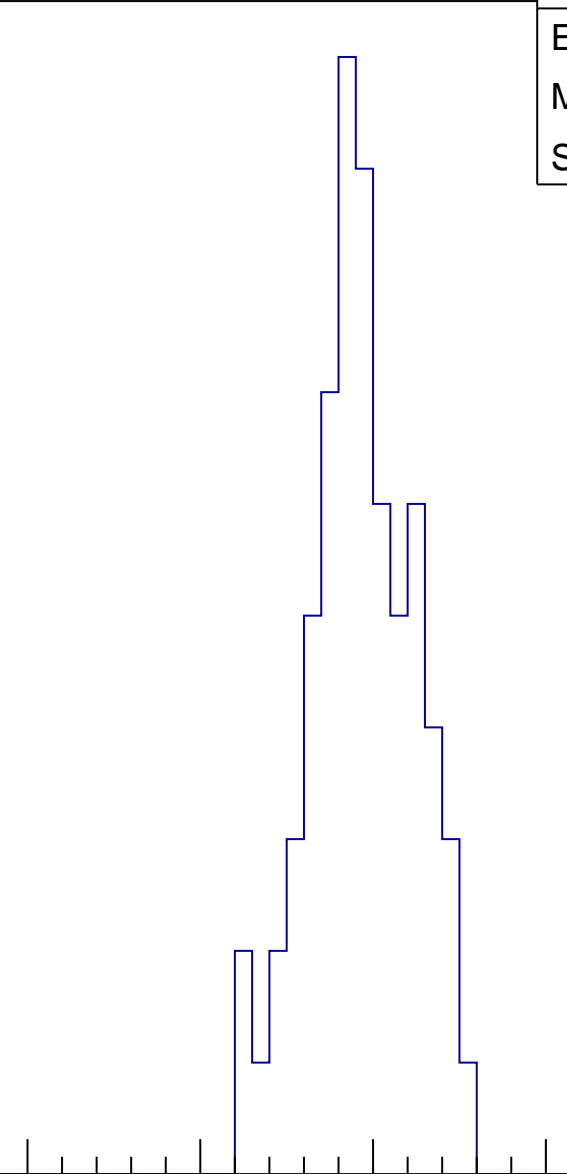
Entries	64
Mean	48.84
Std Dev	2.98

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

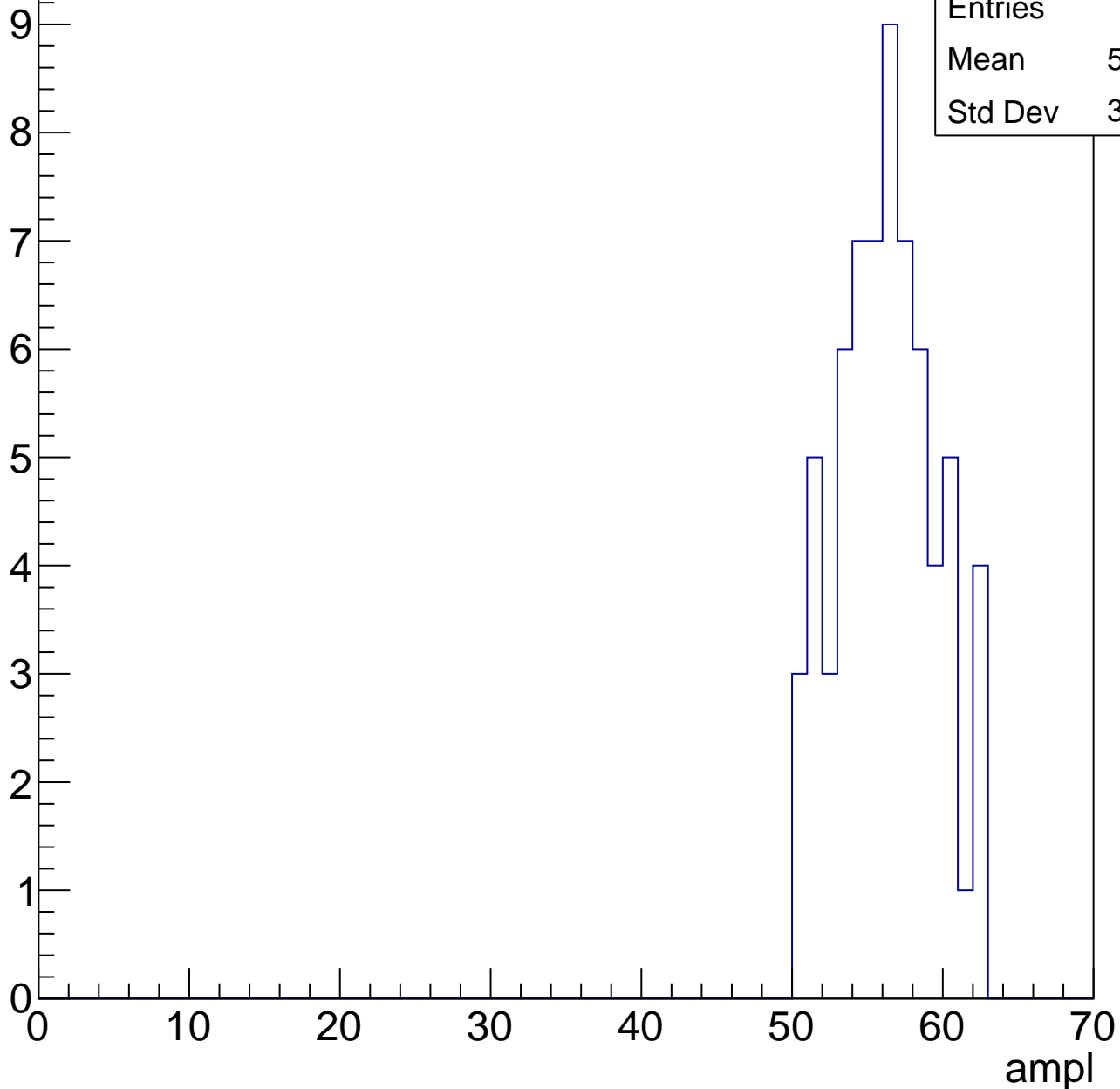
ampl



# B0L002S, U23-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



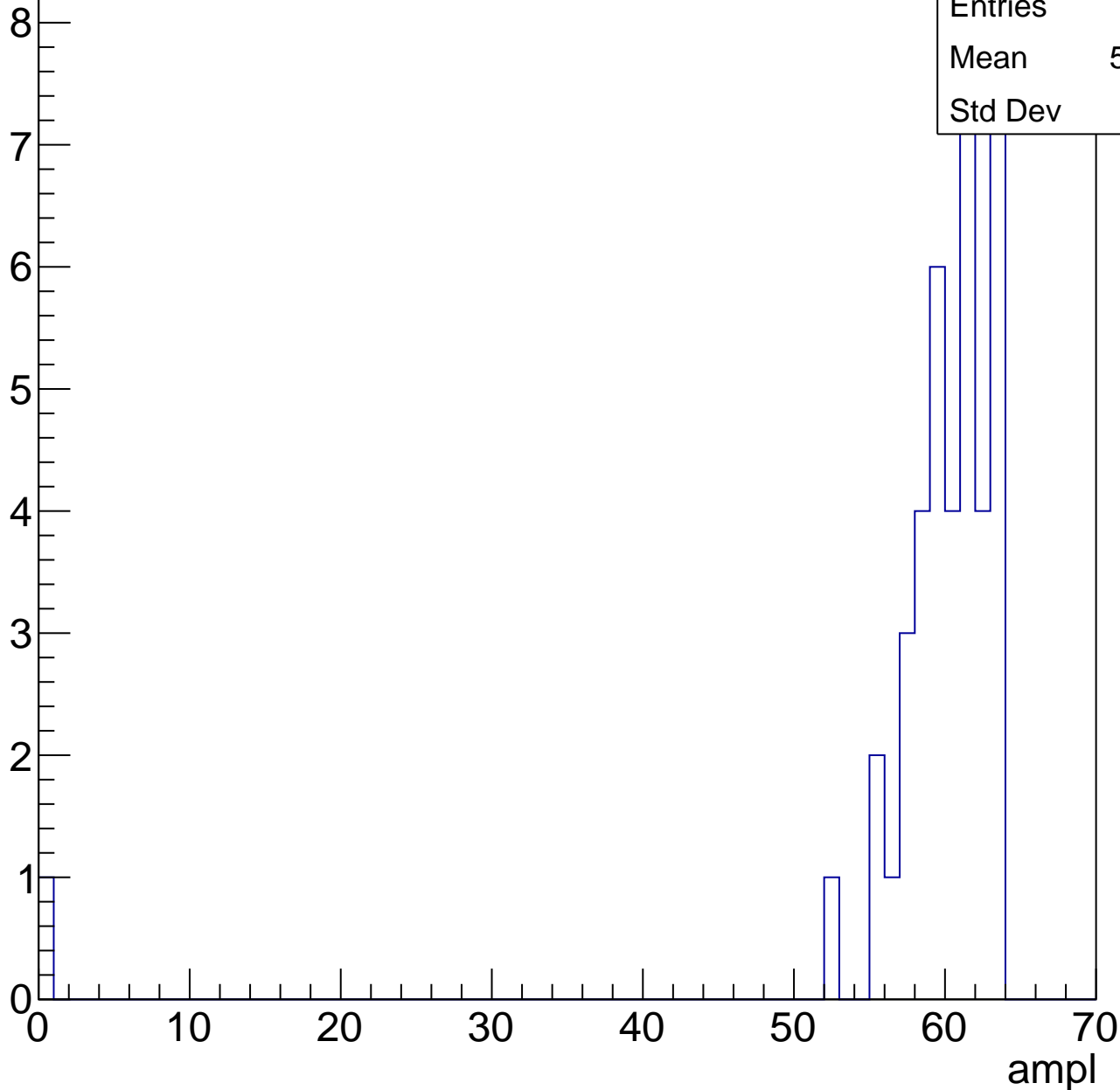
Entries	67
Mean	55.79
Std Dev	3.193

# B0L002S, U23-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

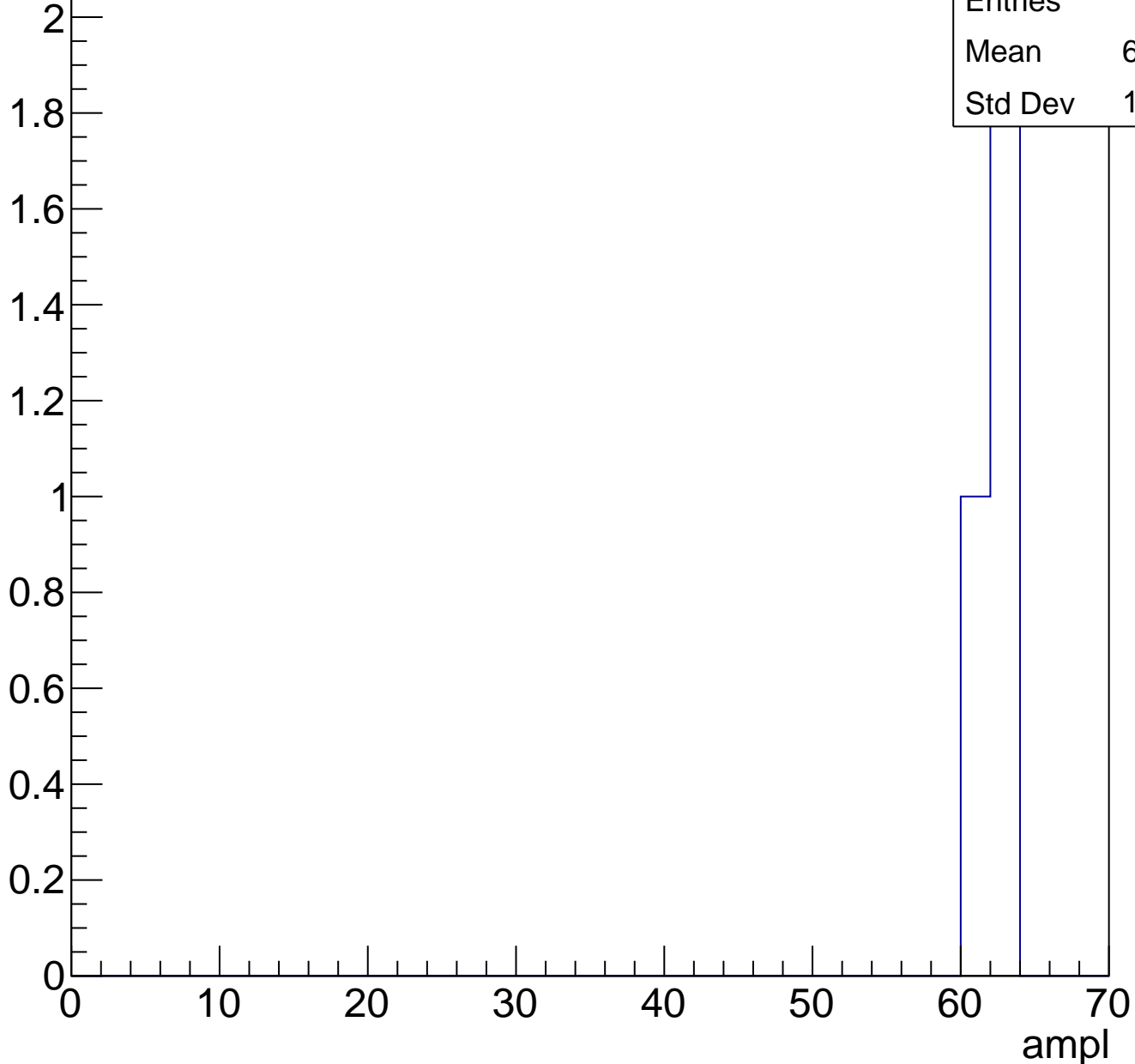
Entries	42
Mean	58.45
Std Dev	9.48



# B0L002S, U23-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch91, adc0

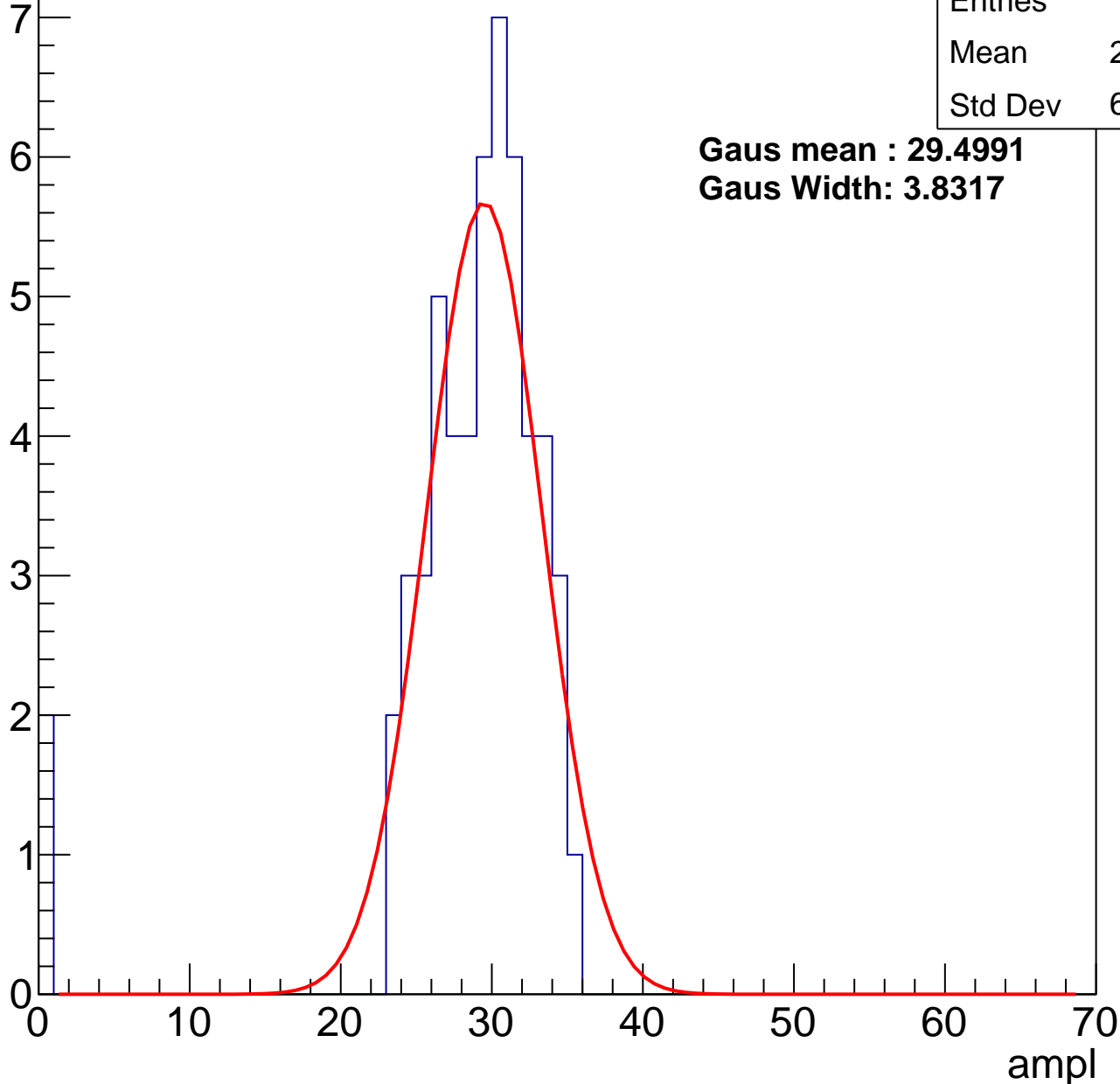
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	54
Mean	27.96
Std Dev	6.274

**Gaus mean : 29.4991**

**Gaus Width: 3.8317**



# B0L002S, U23-ch91, adc1

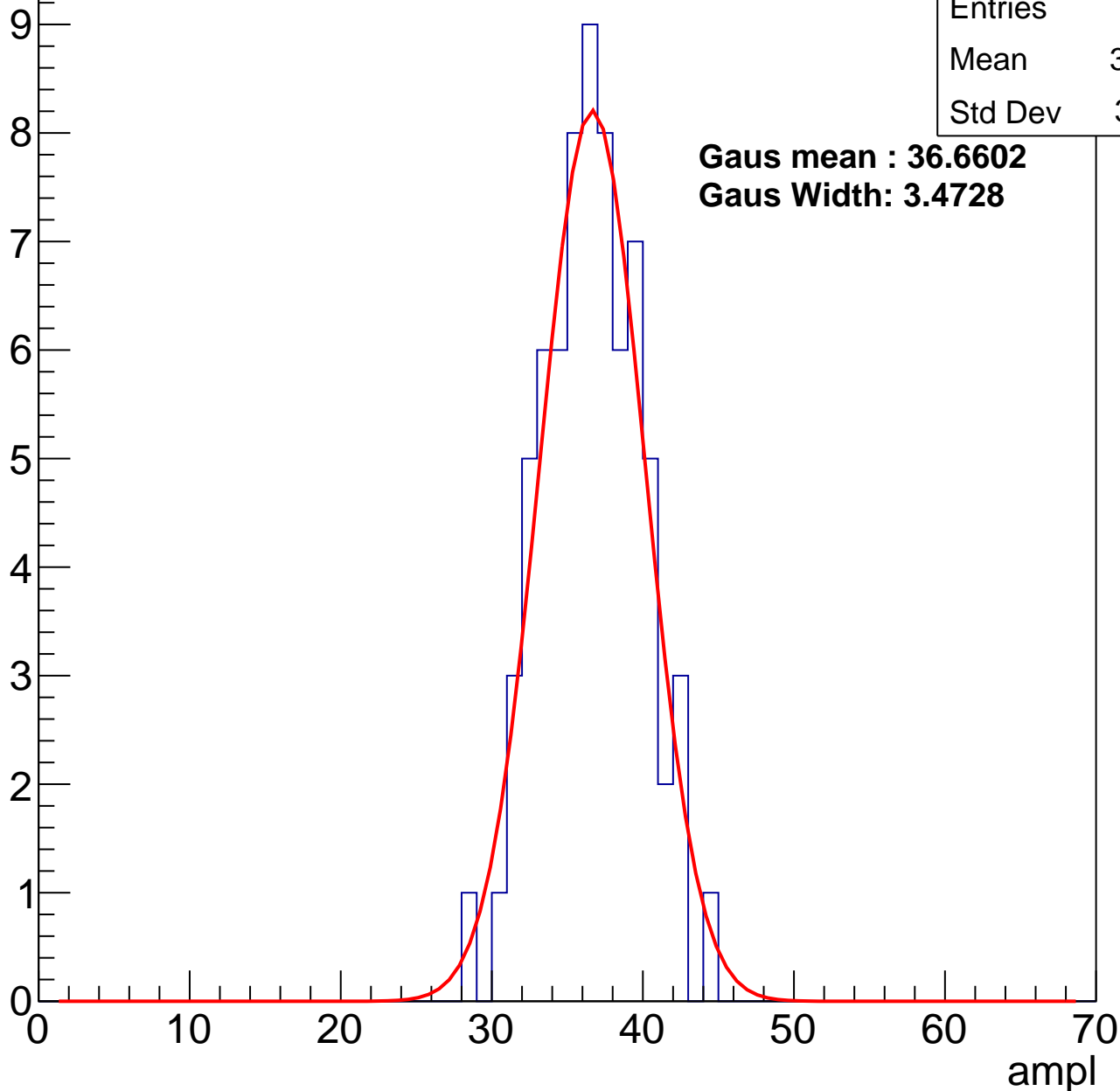
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.14
Std Dev	3.221

**Gaus mean : 36.6602**

**Gaus Width: 3.4728**



# B0L002S, U23-ch91, adc2

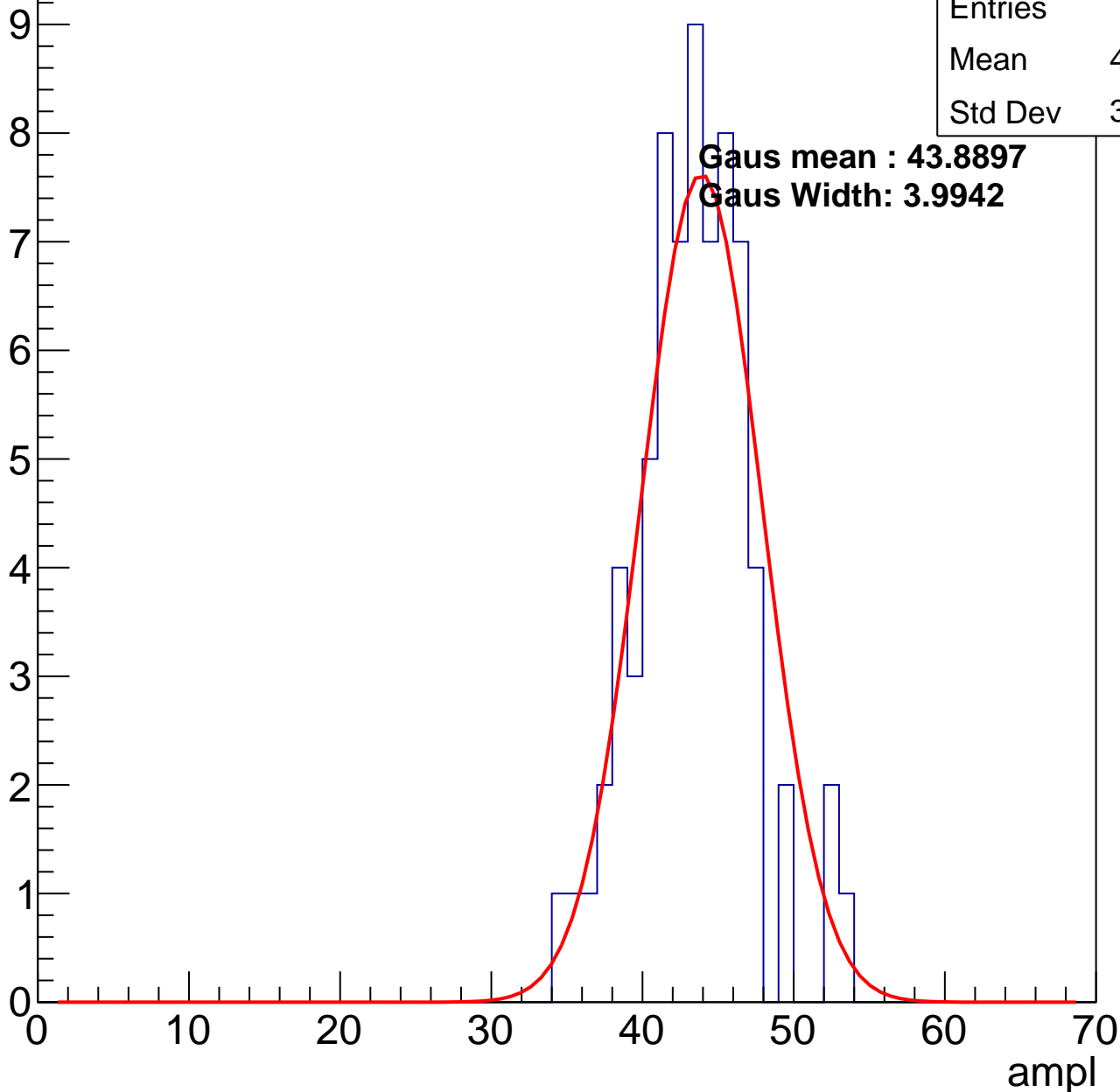
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	42.92
Std Dev	3.722

**Gaus mean : 43.8897**

**Gaus Width: 3.9942**



# B0L002S, U23-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

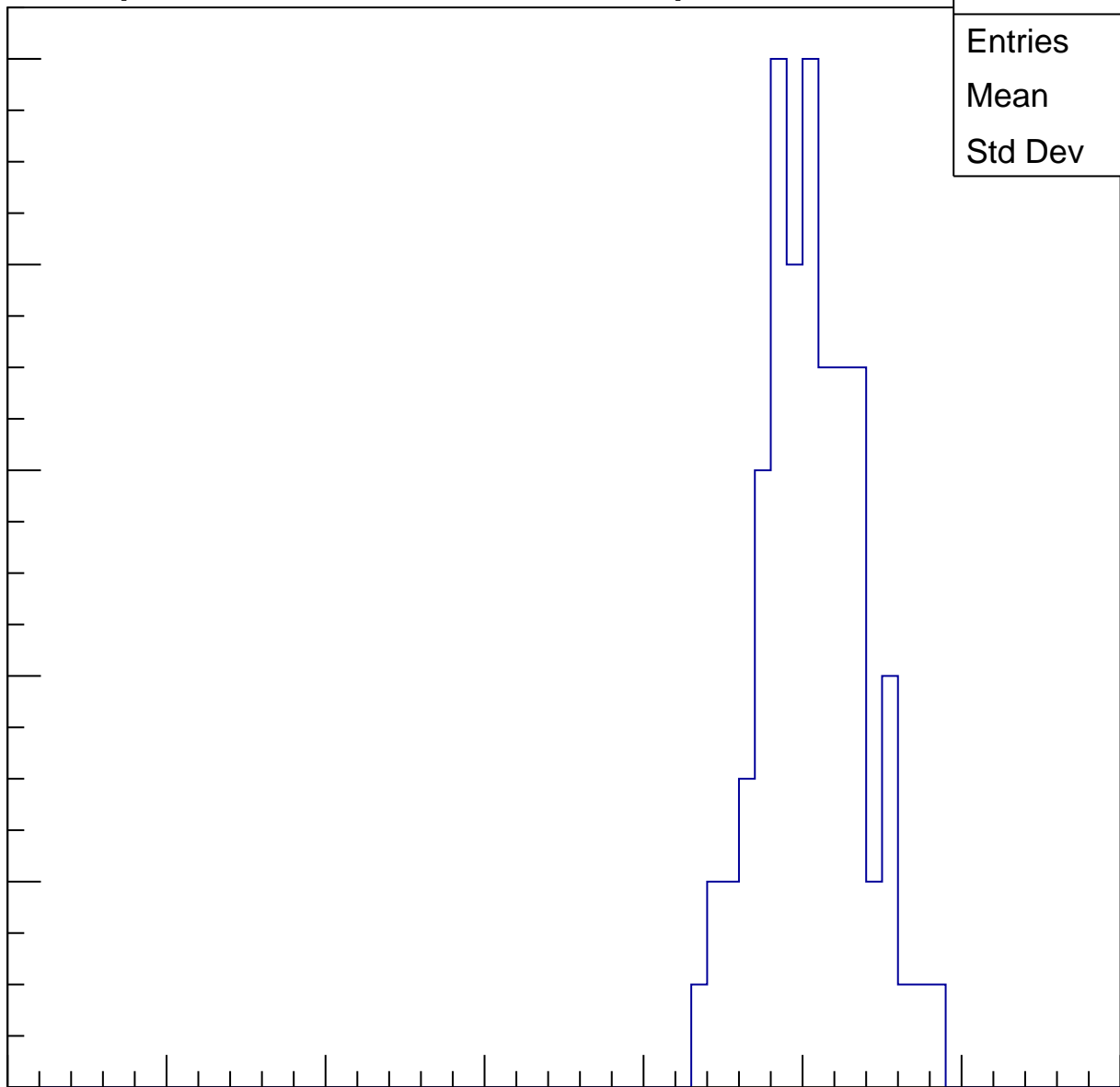
Entries	72
Mean	50.06
Std Dev	3.118

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

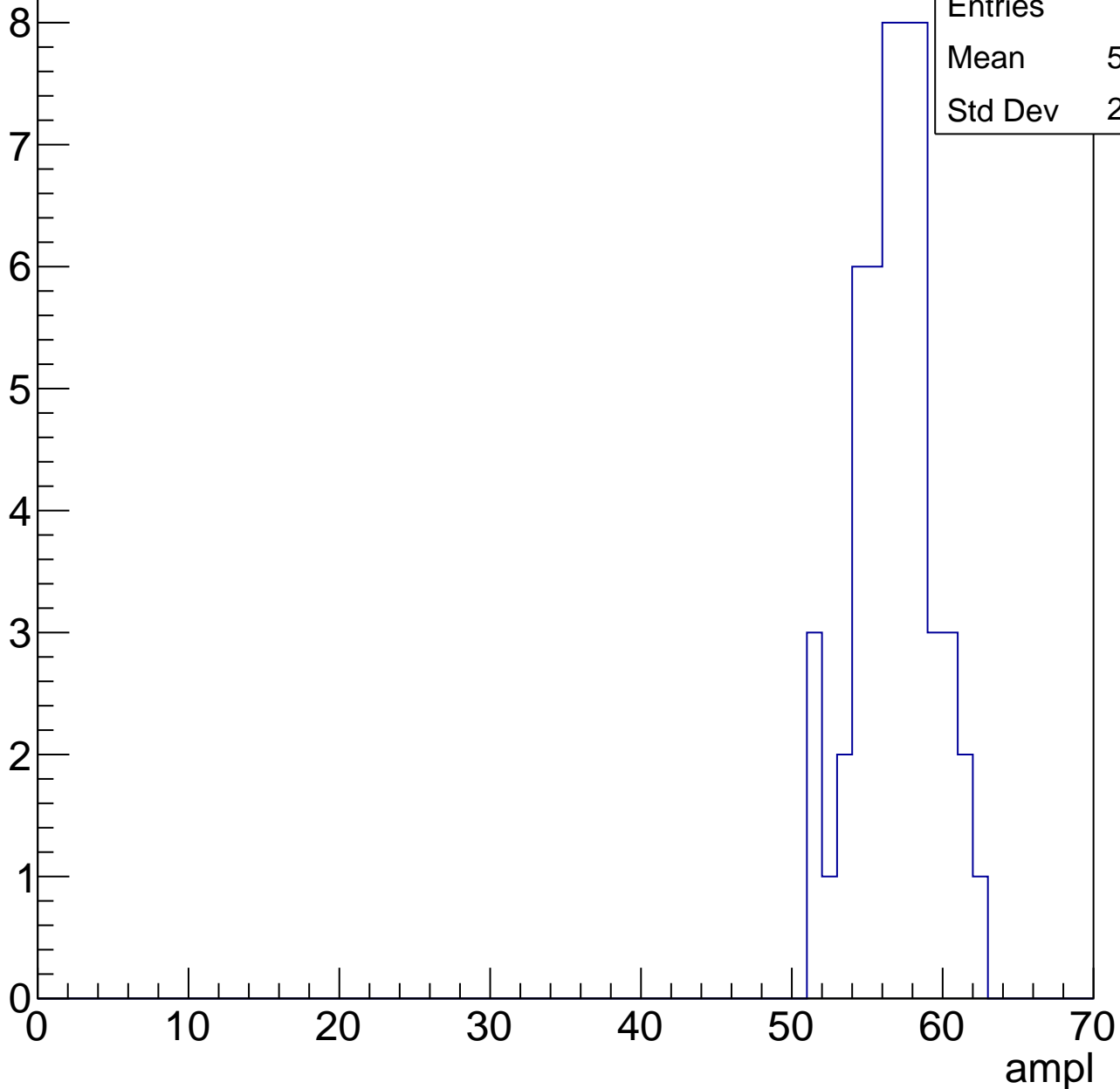


# B0L002S, U23-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.35
Std Dev	2.558

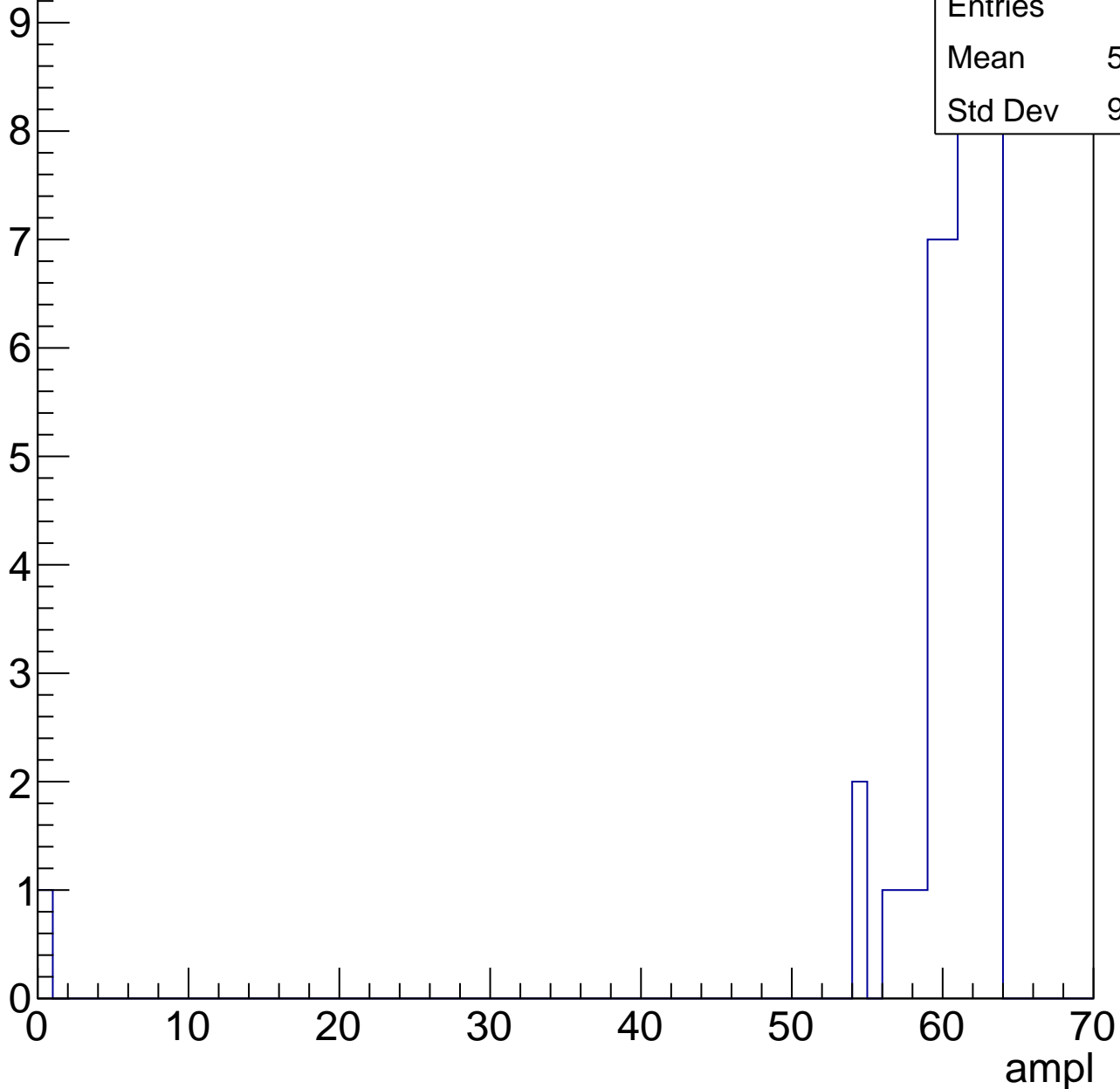


# B0L002S, U23-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.18
Std Dev	9.188



# B0L002S, U23-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch92, adc0

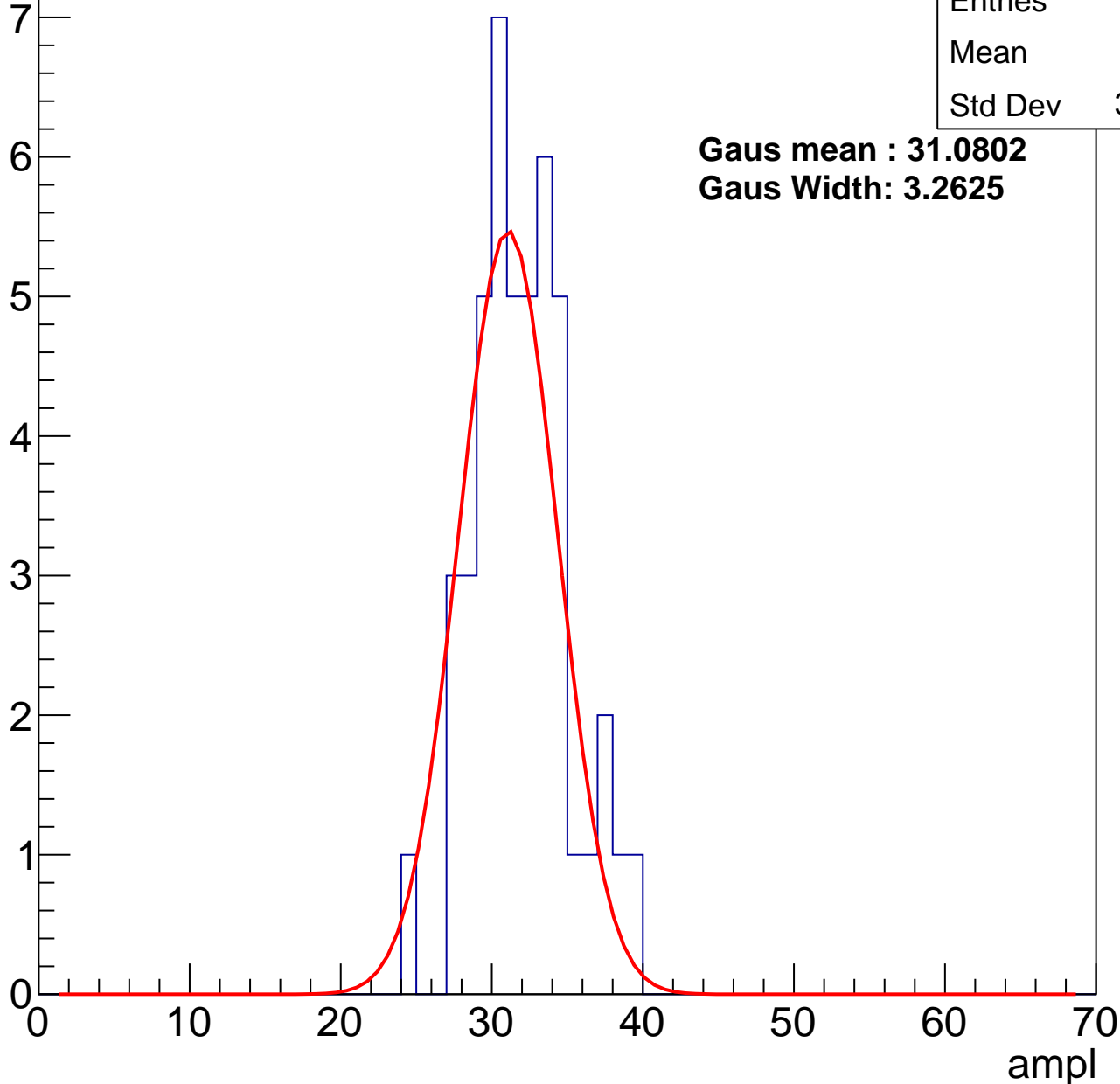
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	31.5
Std Dev	3.091

**Gaus mean : 31.0802**

**Gaus Width: 3.2625**



# B0L002S, U23-ch92, adc1

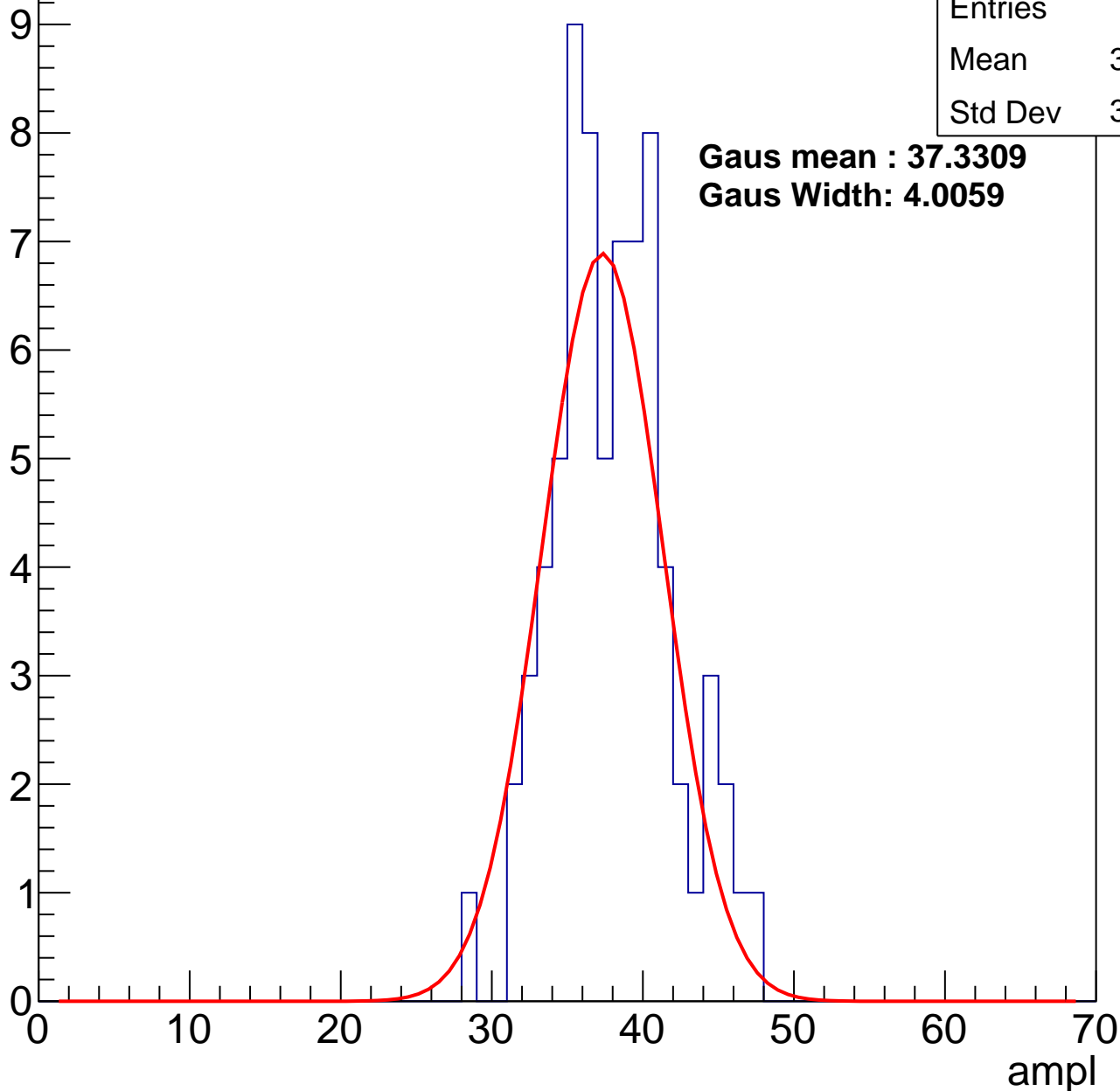
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.55
Std Dev	3.832

**Gaus mean : 37.3309**

**Gaus Width: 4.0059**



# B0L002S, U23-ch92, adc2

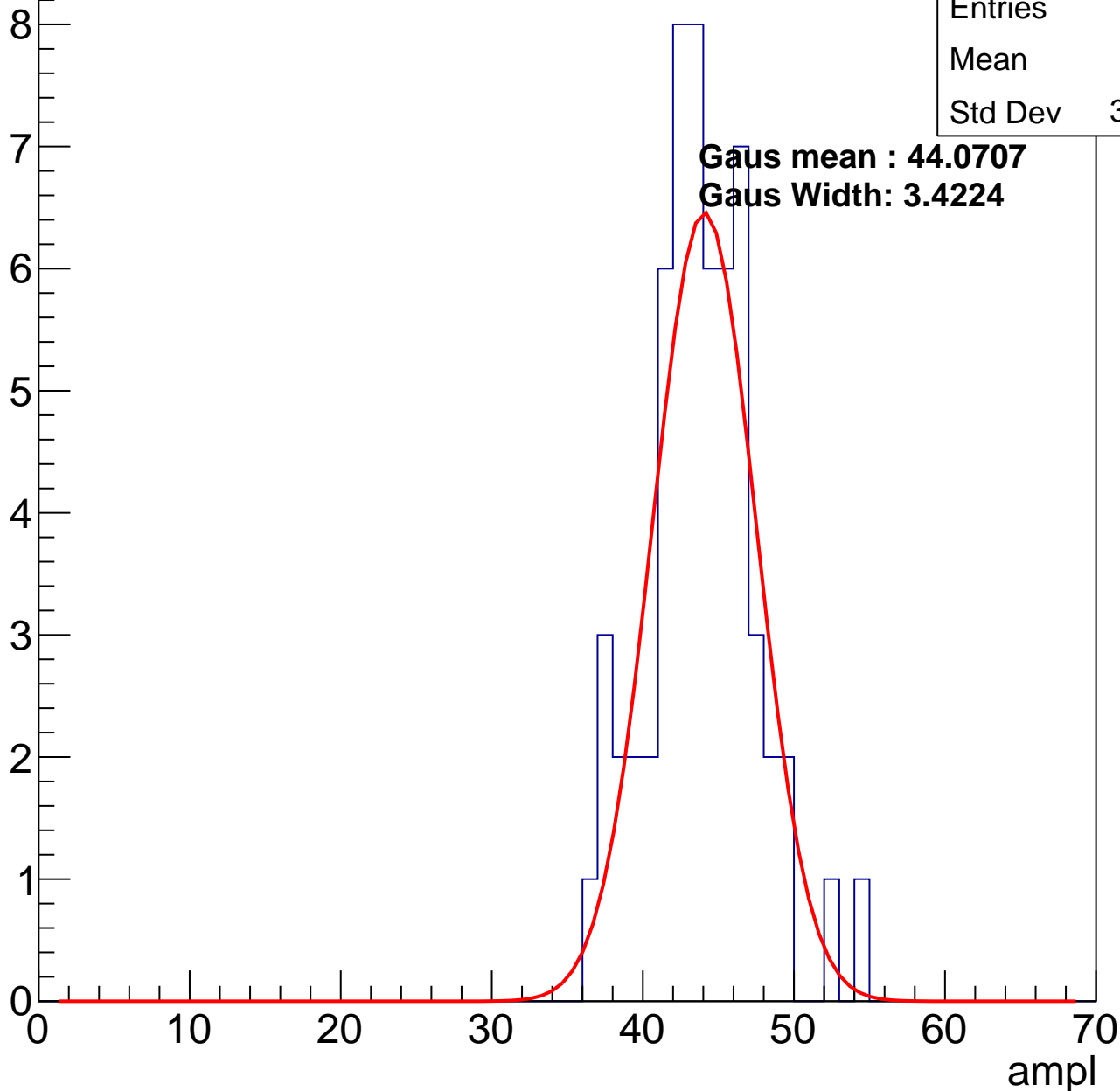
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.4
Std Dev	3.522

**Gaus mean : 44.0707**

**Gaus Width: 3.4224**

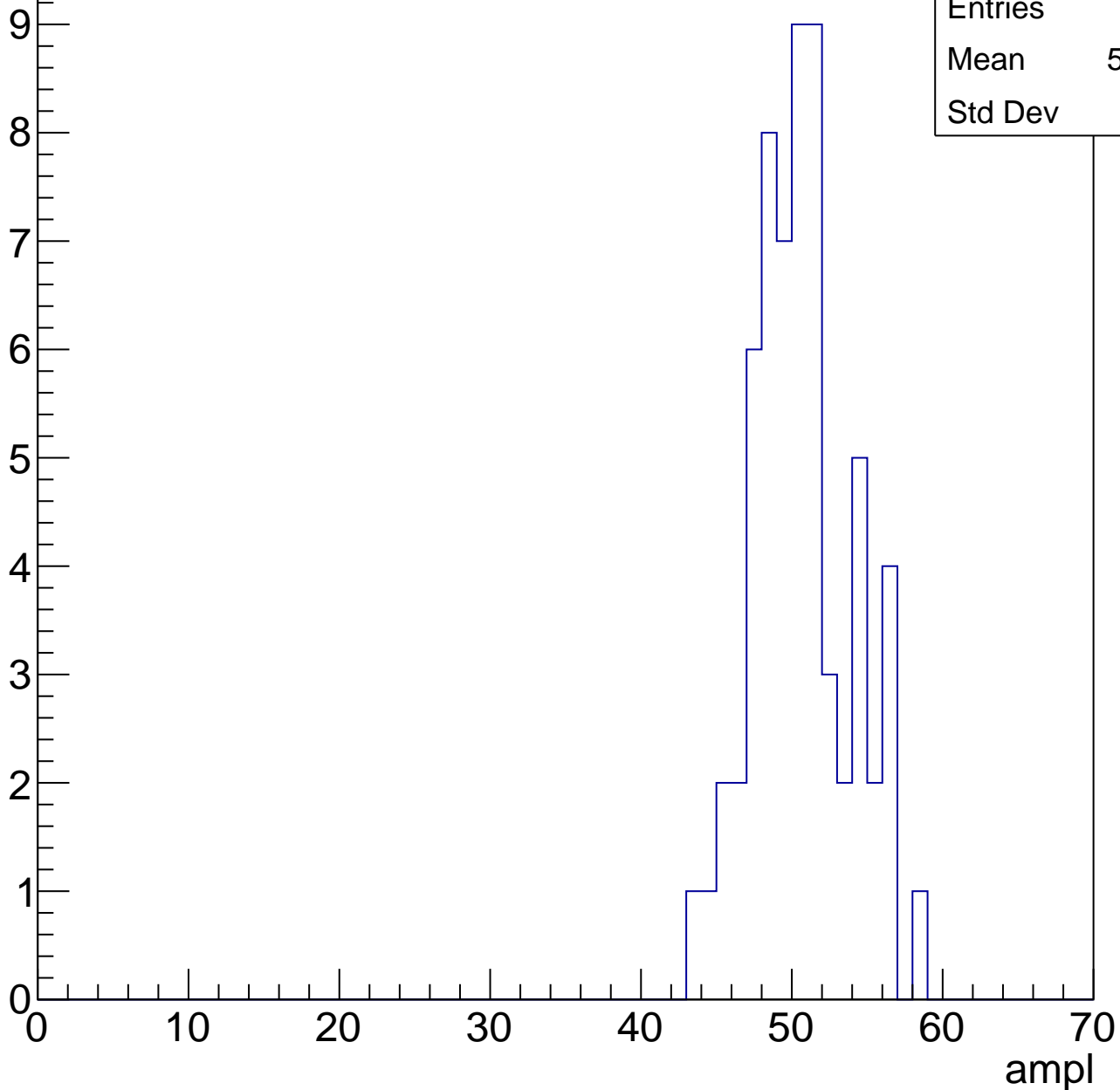


# B0L002S, U23-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

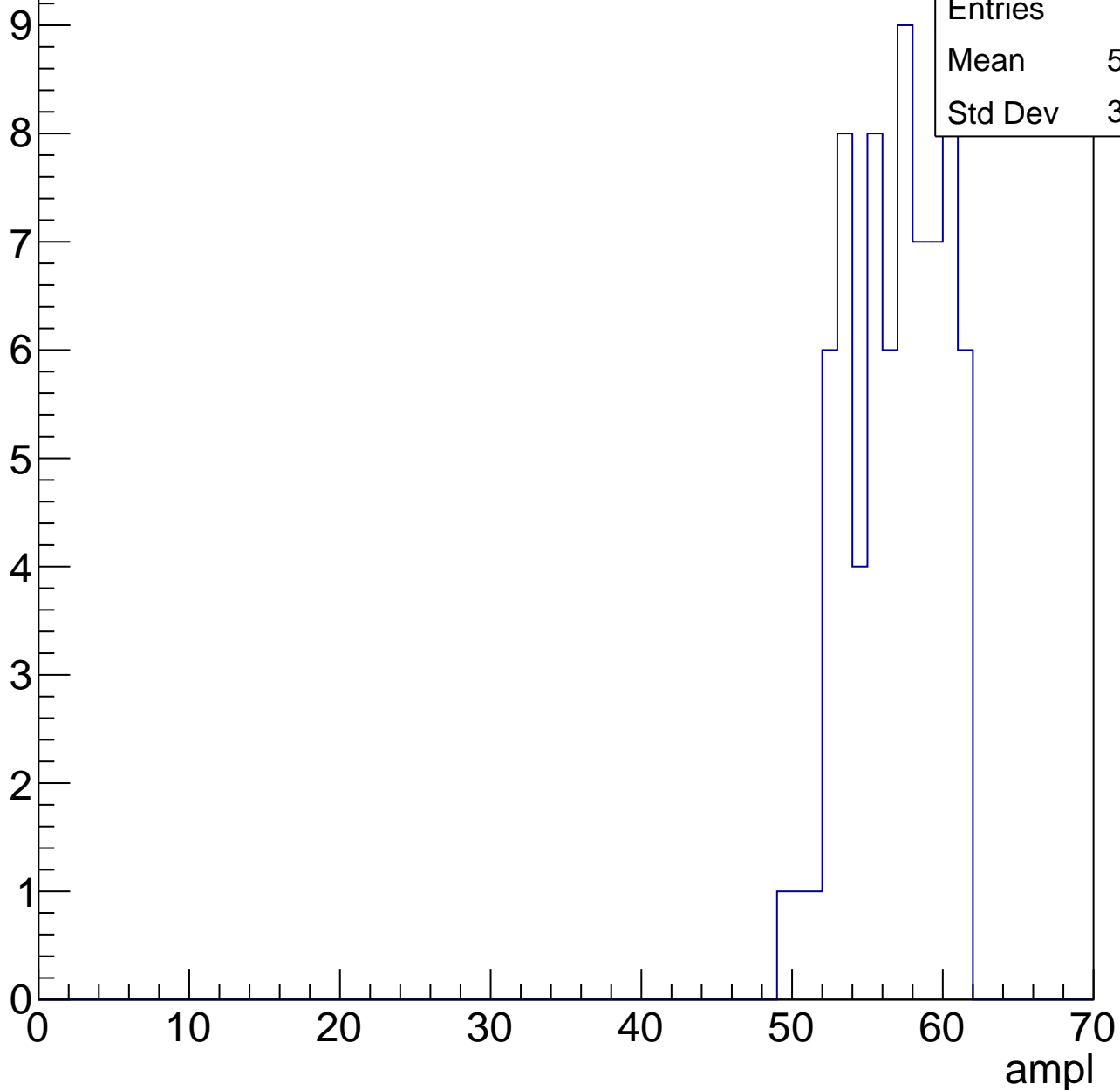
Entries	62
Mean	50.18
Std Dev	3.2



# B0L002S, U23-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



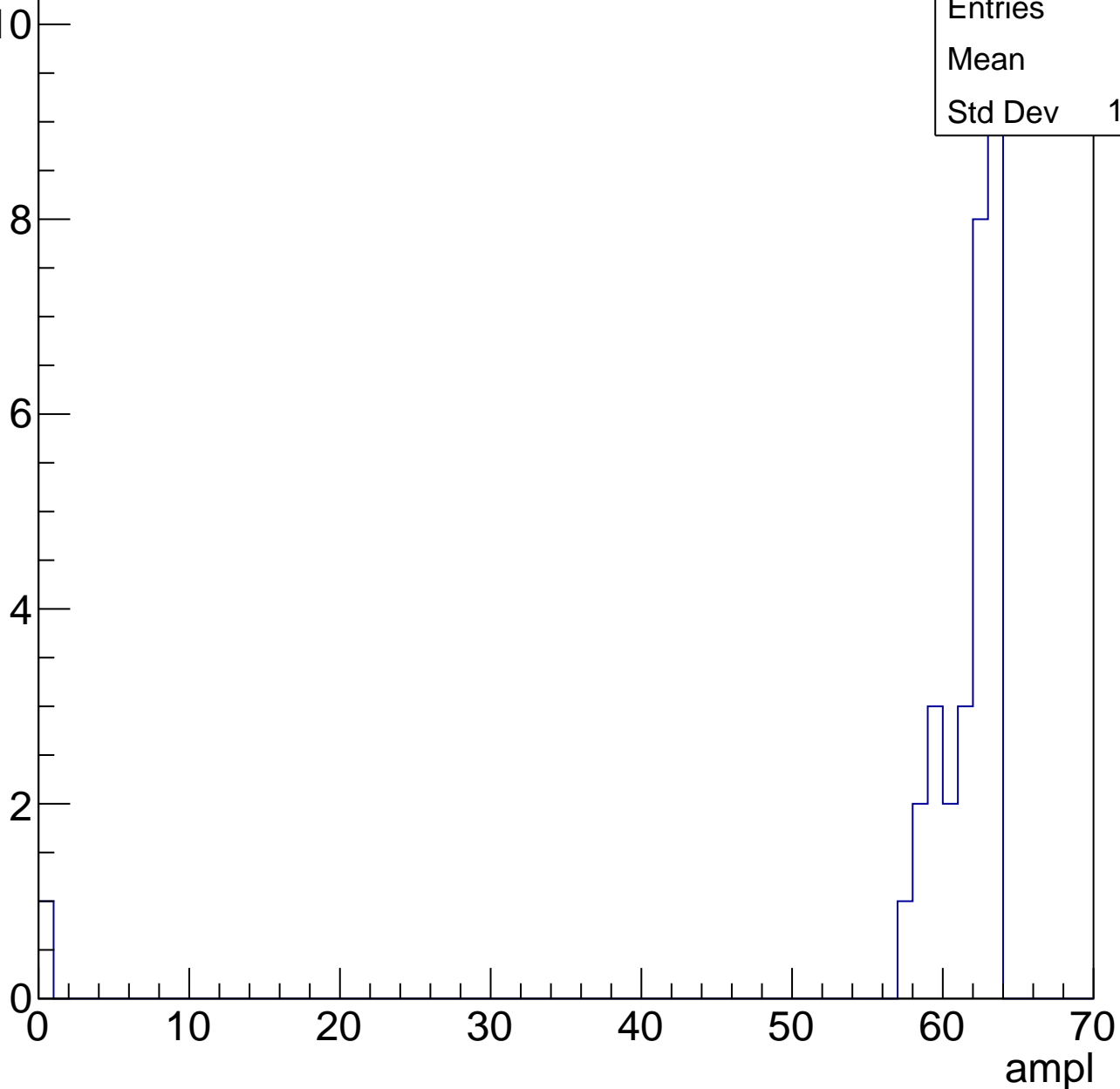
Entries	72
Mean	56.33
Std Dev	3.055

# B0L002S, U23-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.3
Std Dev	11.15



# B0L002S, U23-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch93, adc0

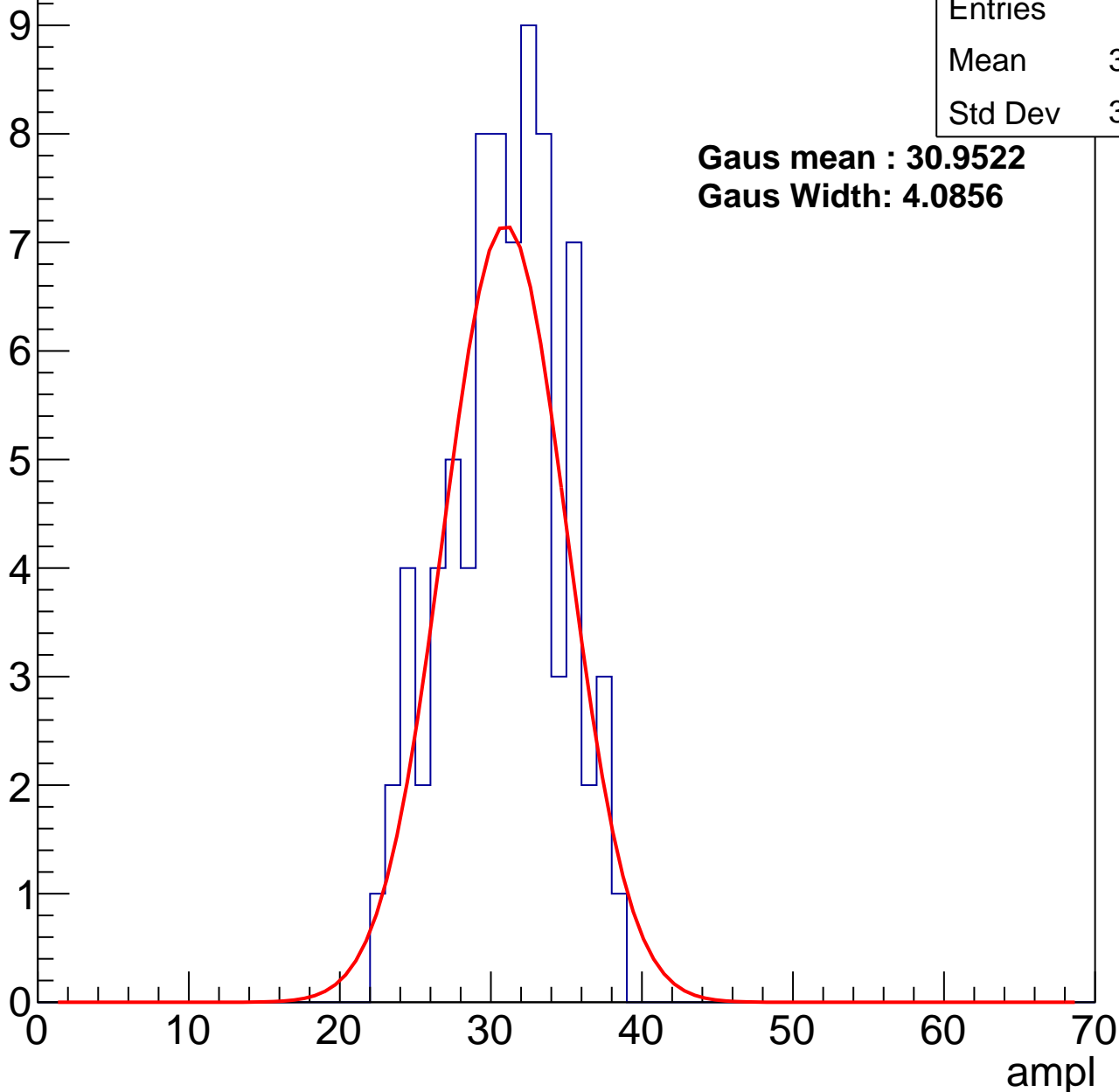
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	30.44
Std Dev	3.747

**Gaus mean : 30.9522**

**Gaus Width: 4.0856**



# B0L002S, U23-ch93, adc1

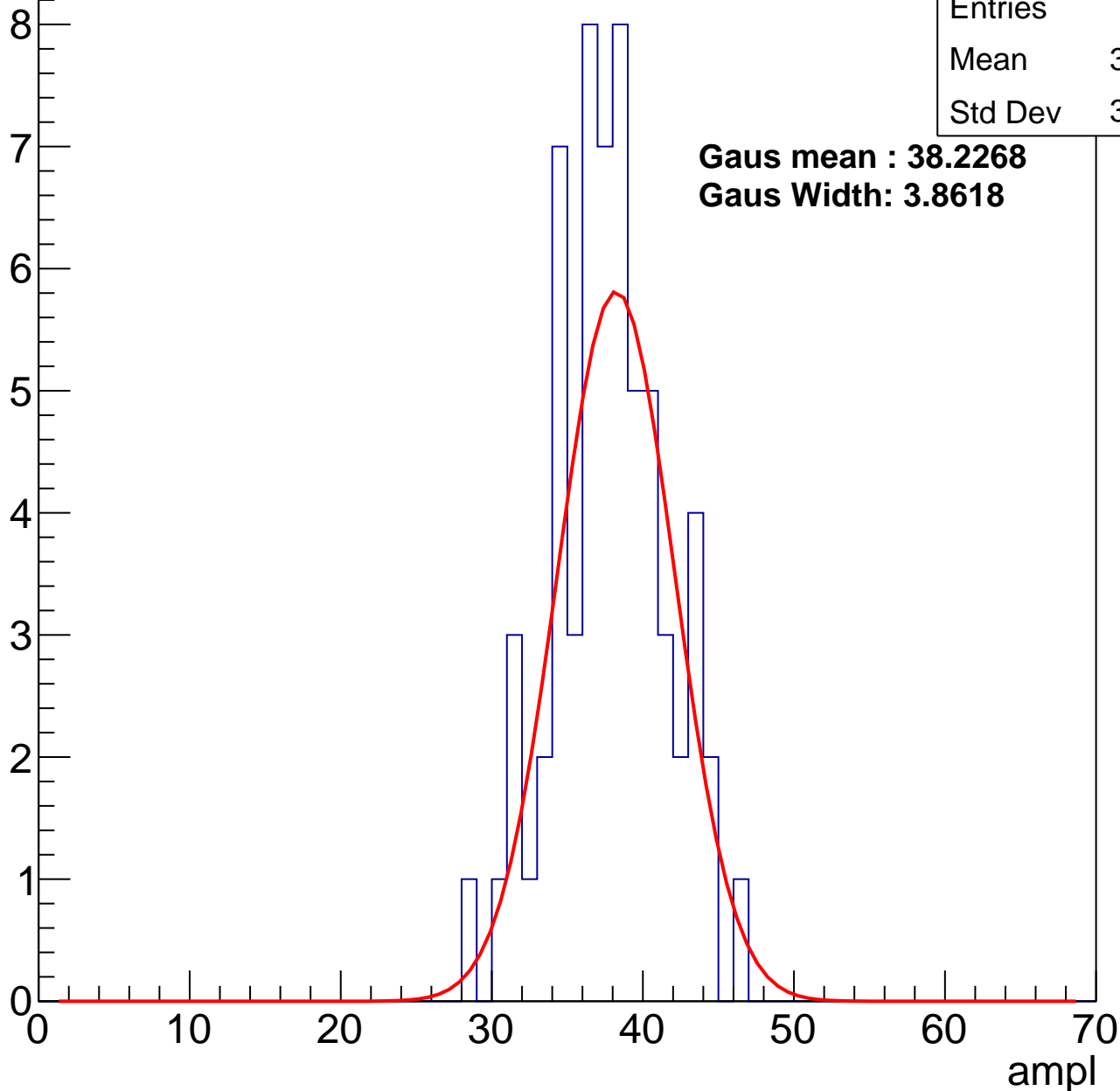
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.32
Std Dev	3.707

**Gaus mean : 38.2268**

**Gaus Width: 3.8618**



# B0L002S, U23-ch93, adc2

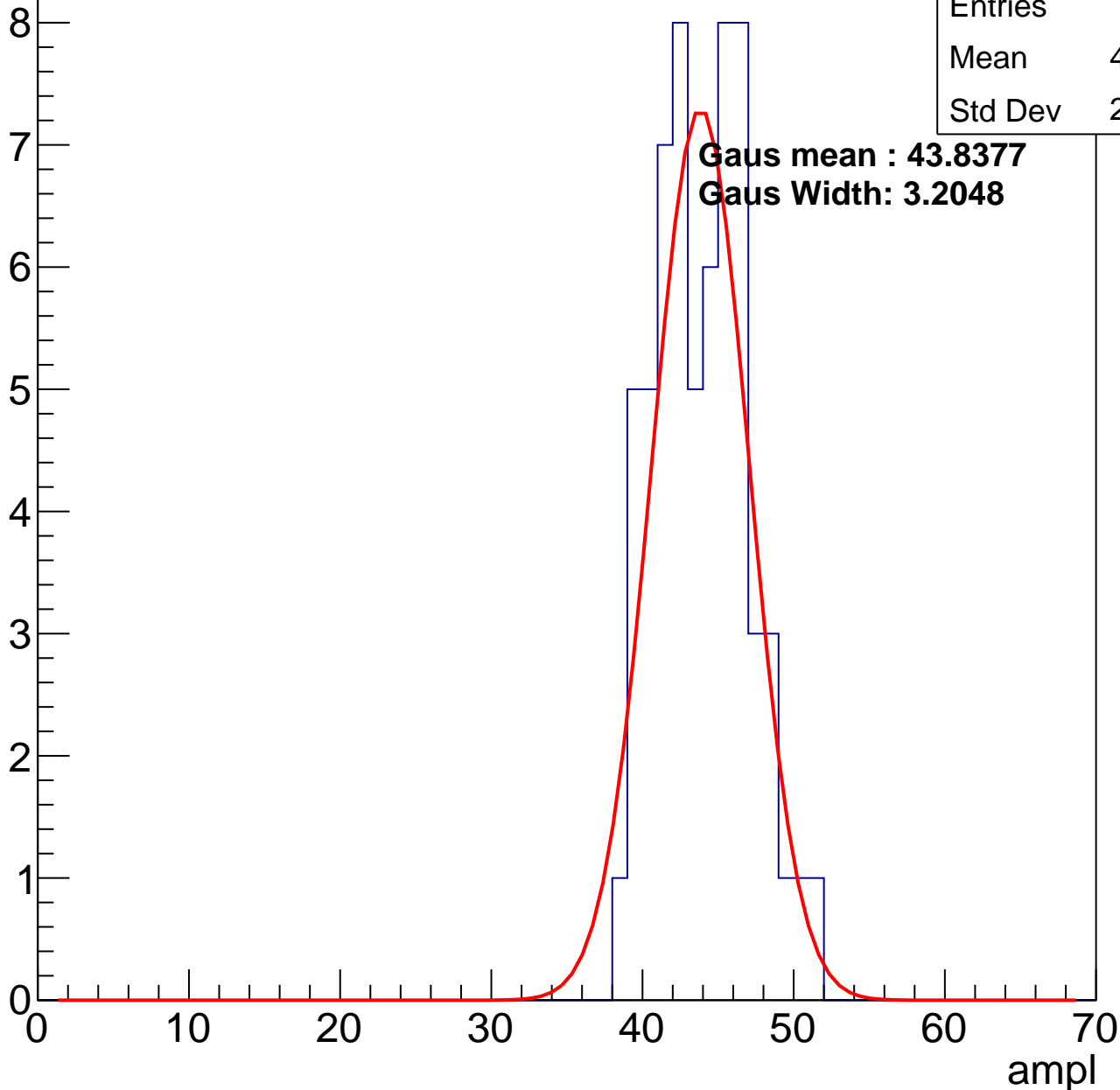
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.52
Std Dev	2.982

**Gaus mean : 43.8377**

**Gaus Width: 3.2048**

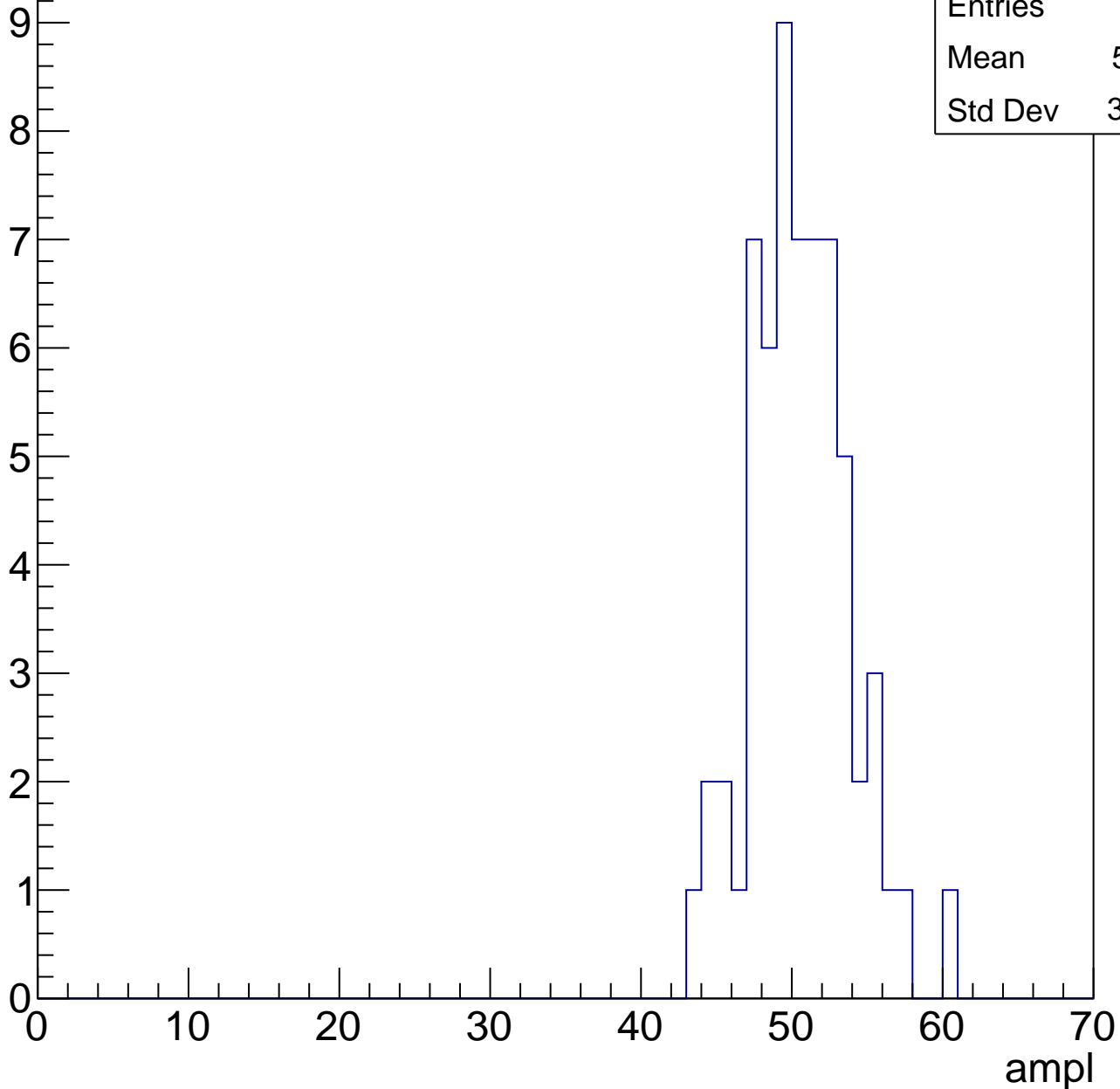


# B0L002S, U23-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	50.11
Std Dev	3.248

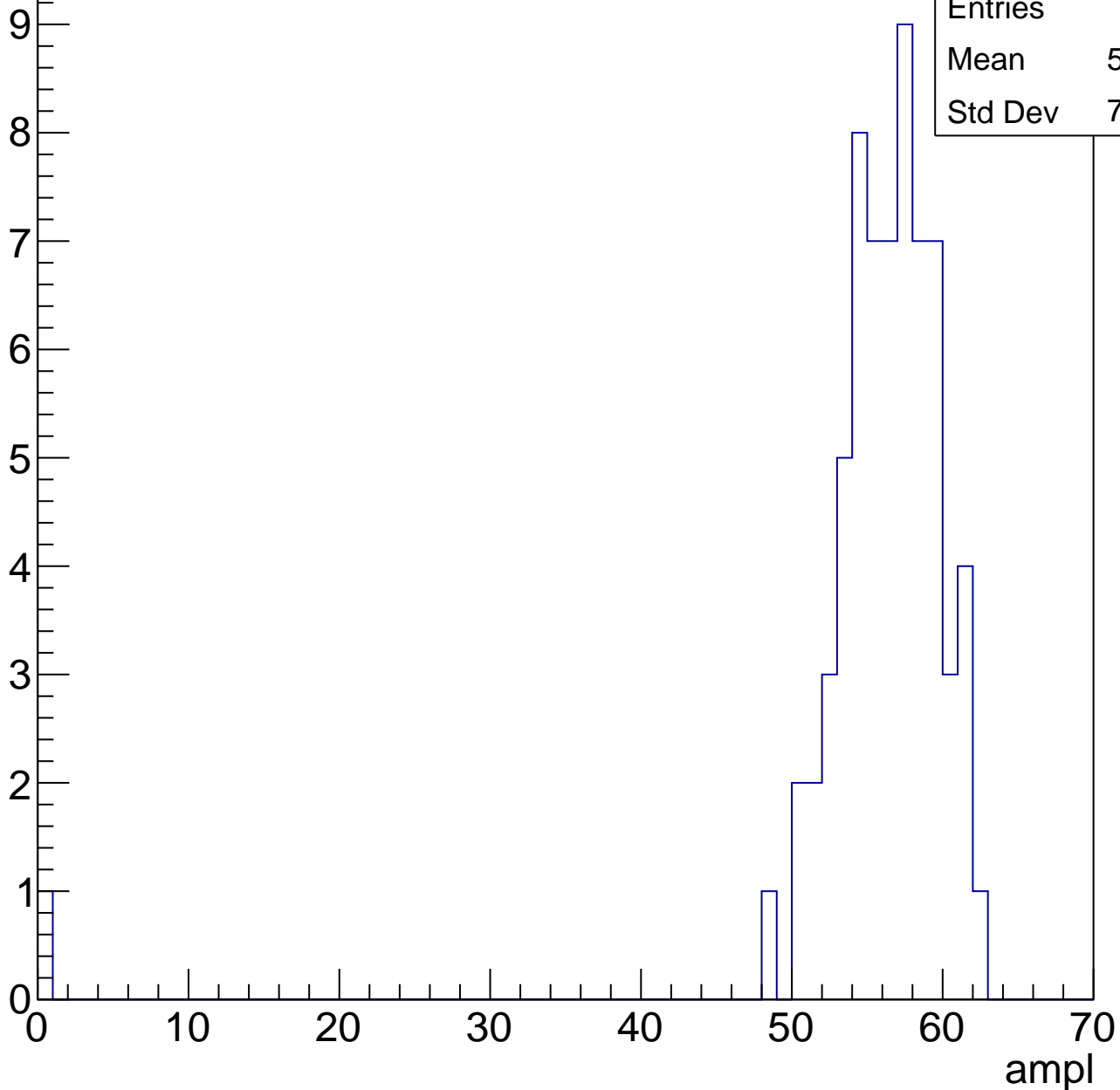


# B0L002S, U23-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	55.19
Std Dev	7.426

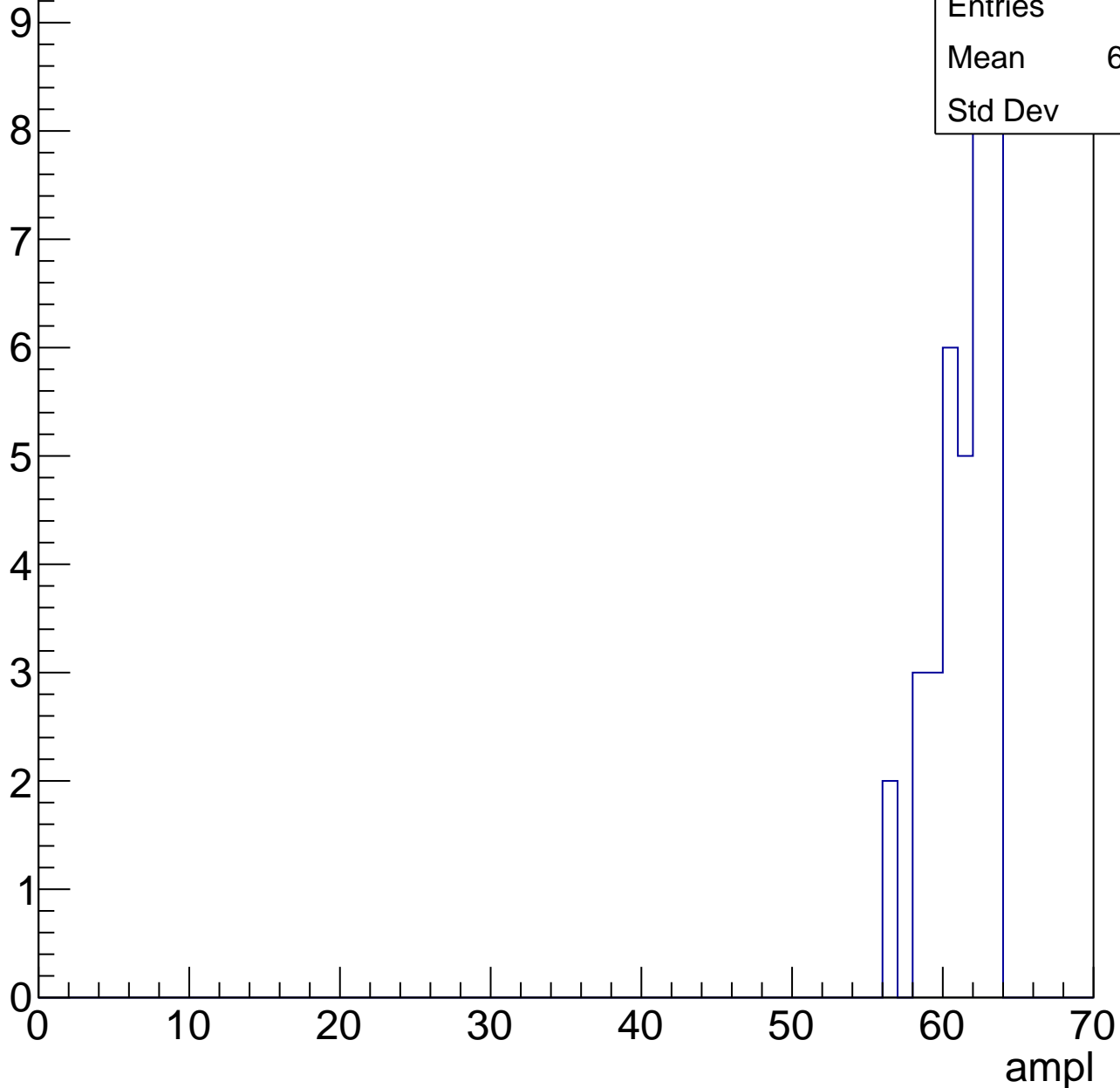


# B0L002S, U23-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	36
Mean	60.86
Std Dev	1.96



# B0L002S, U23-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch94, adc0

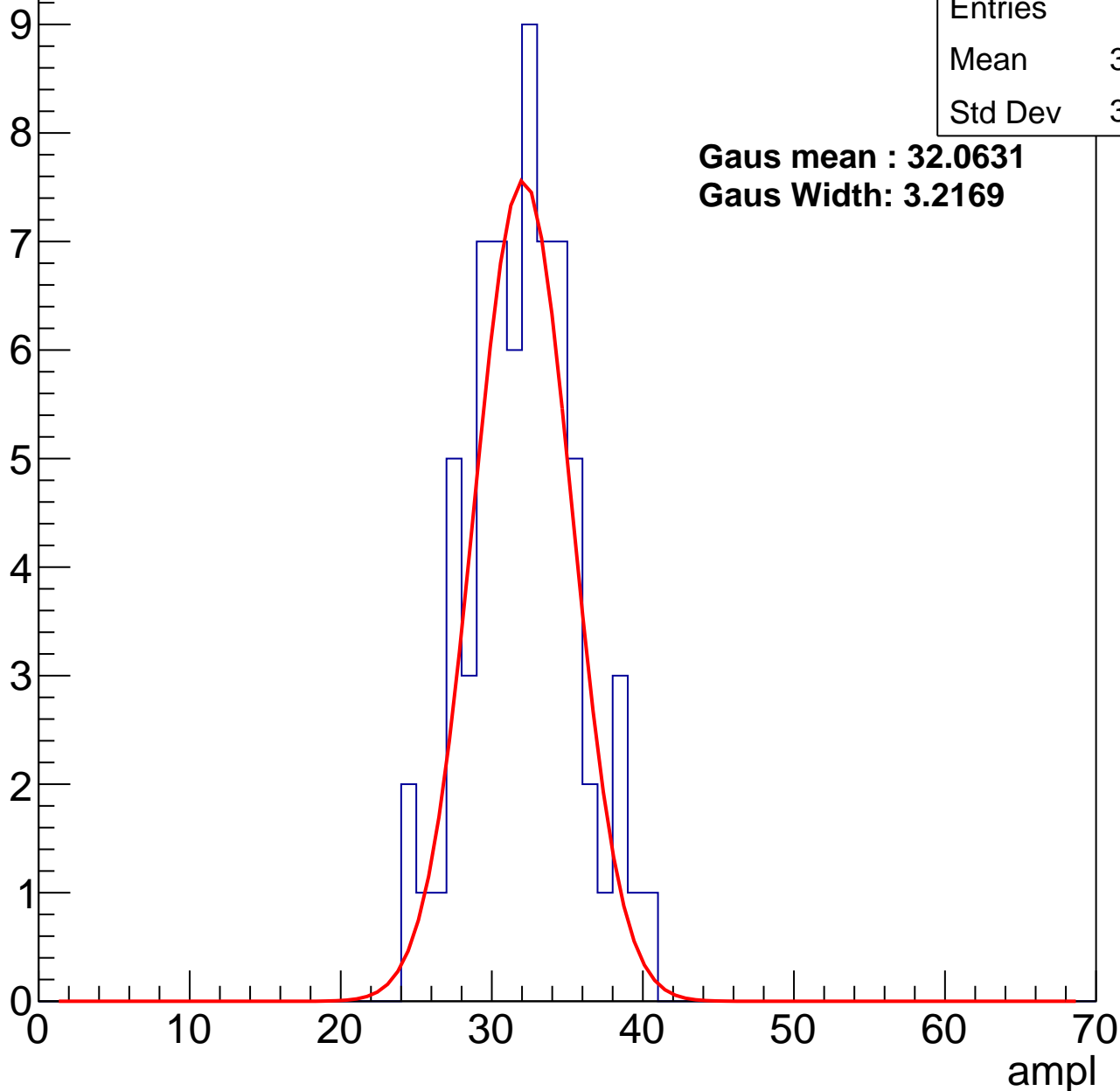
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.63
Std Dev	3.497

**Gaus mean : 32.0631**

**Gaus Width: 3.2169**



# B0L002S, U23-ch94, adc1

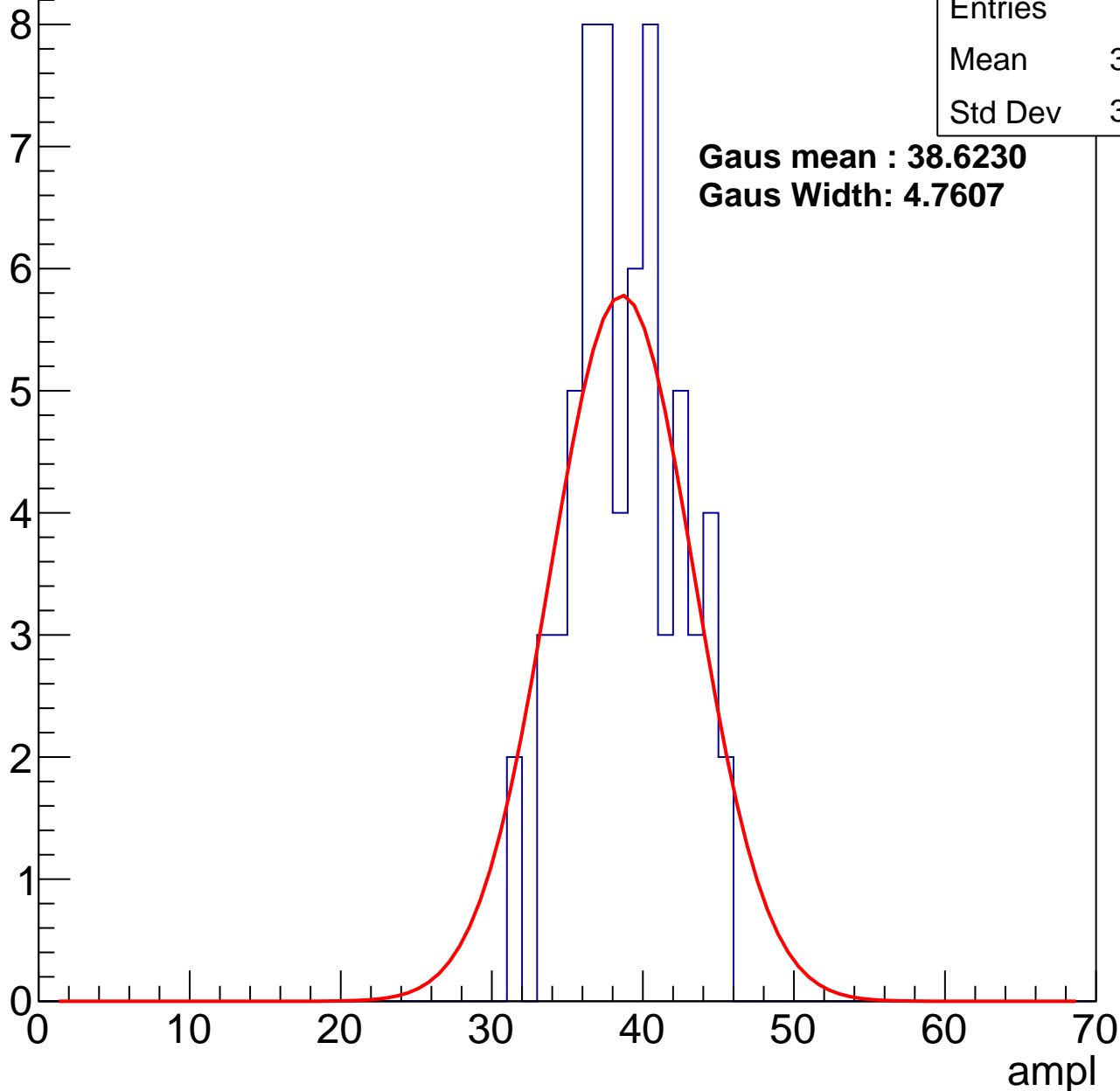
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.38
Std Dev	3.444

**Gaus mean : 38.6230**

**Gaus Width: 4.7607**



# B0L002S, U23-ch94, adc2

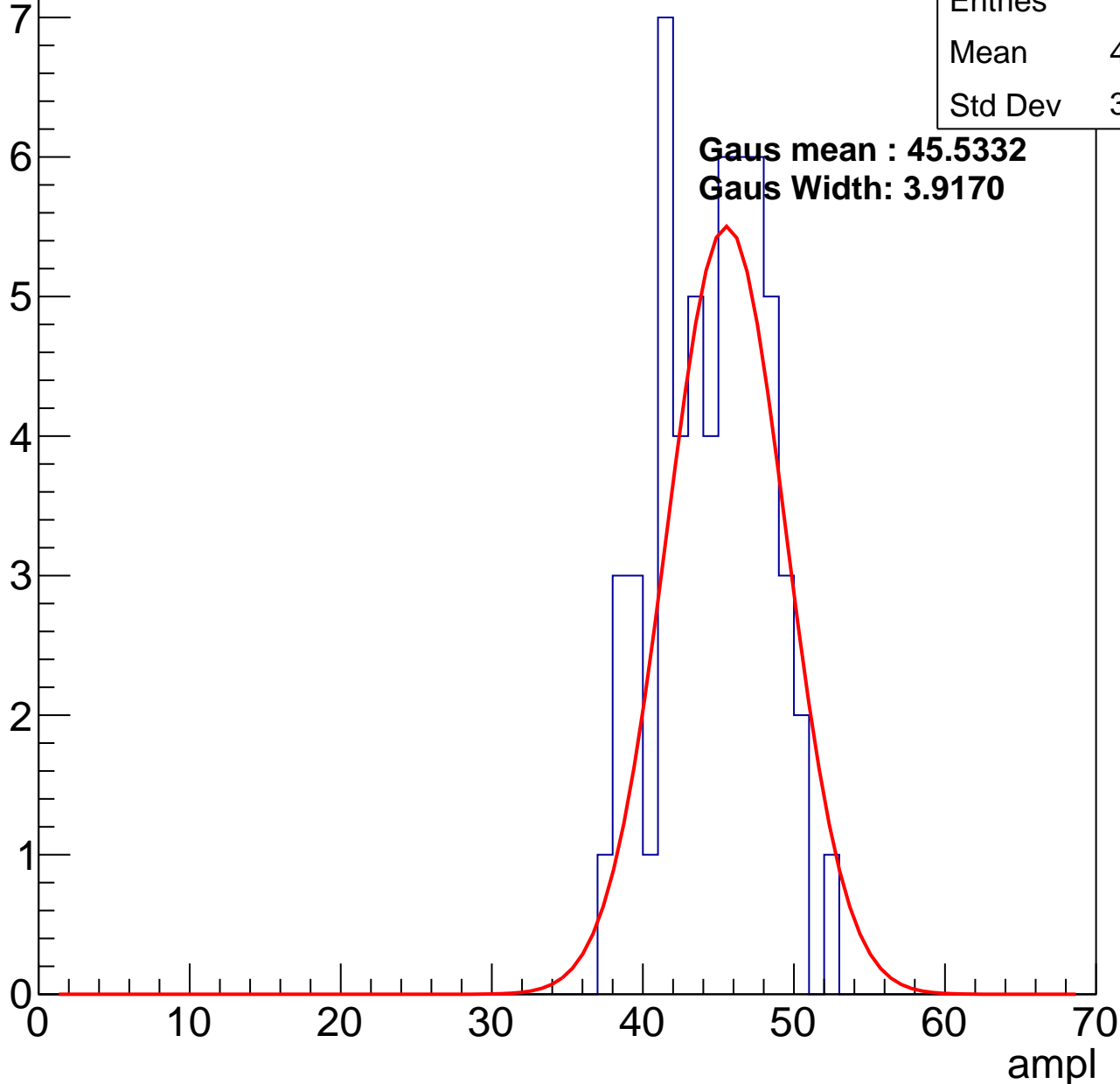
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.23
Std Dev	3.514

**Gaus mean : 45.5332**

**Gaus Width: 3.9170**

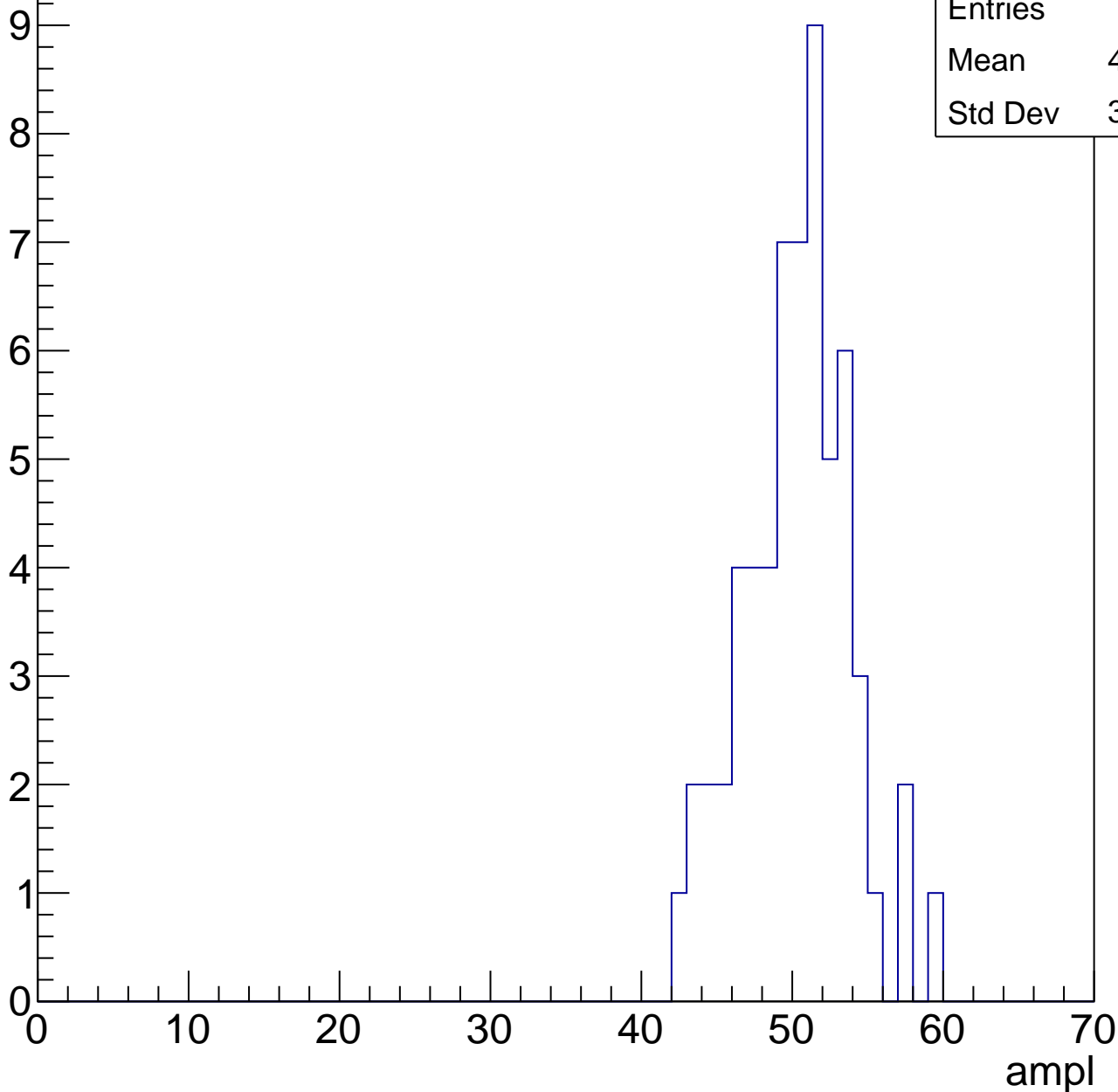


# B0L002S, U23-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	49.83
Std Dev	3.513

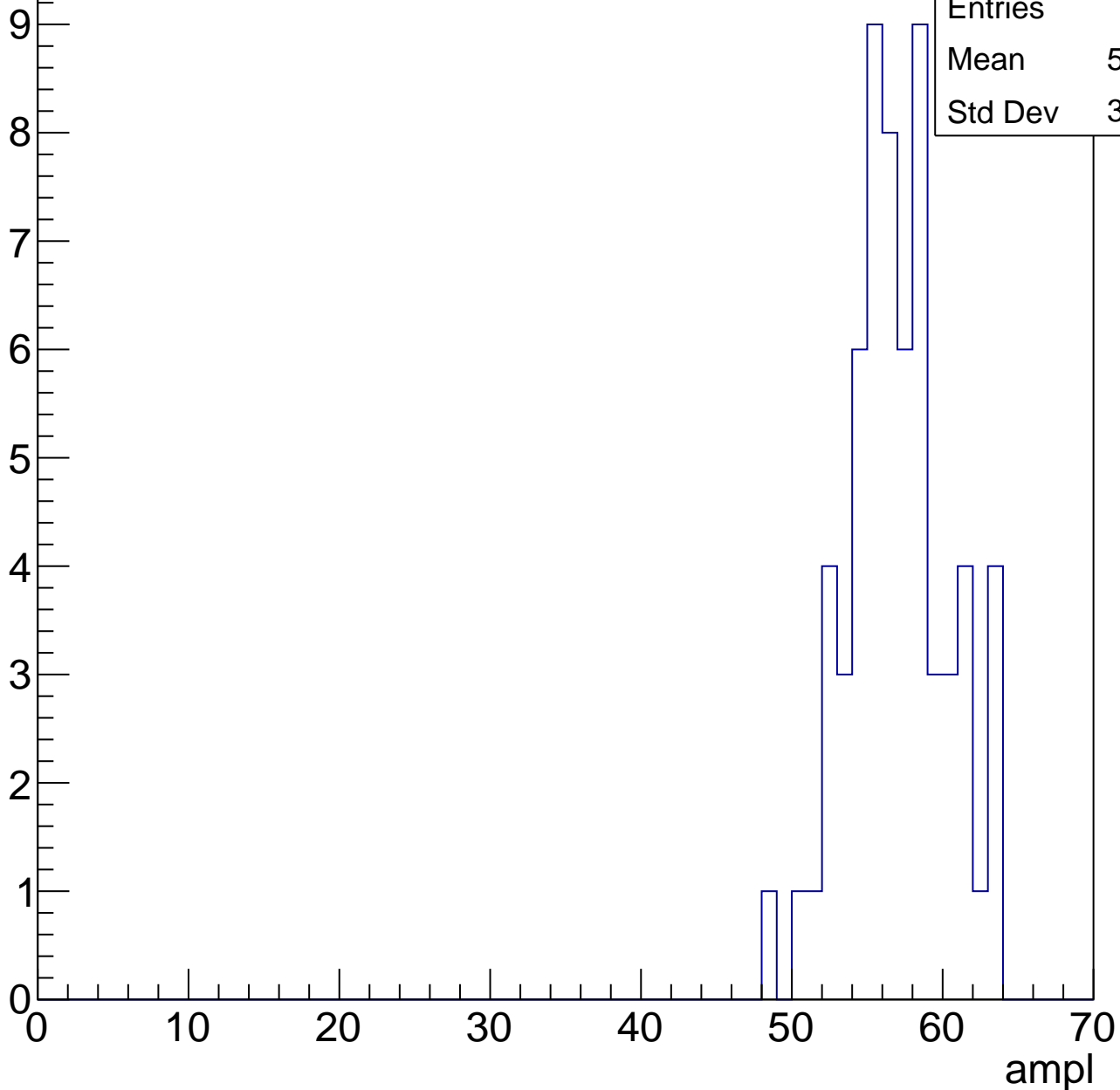


# B0L002S, U23-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.54
Std Dev	3.285

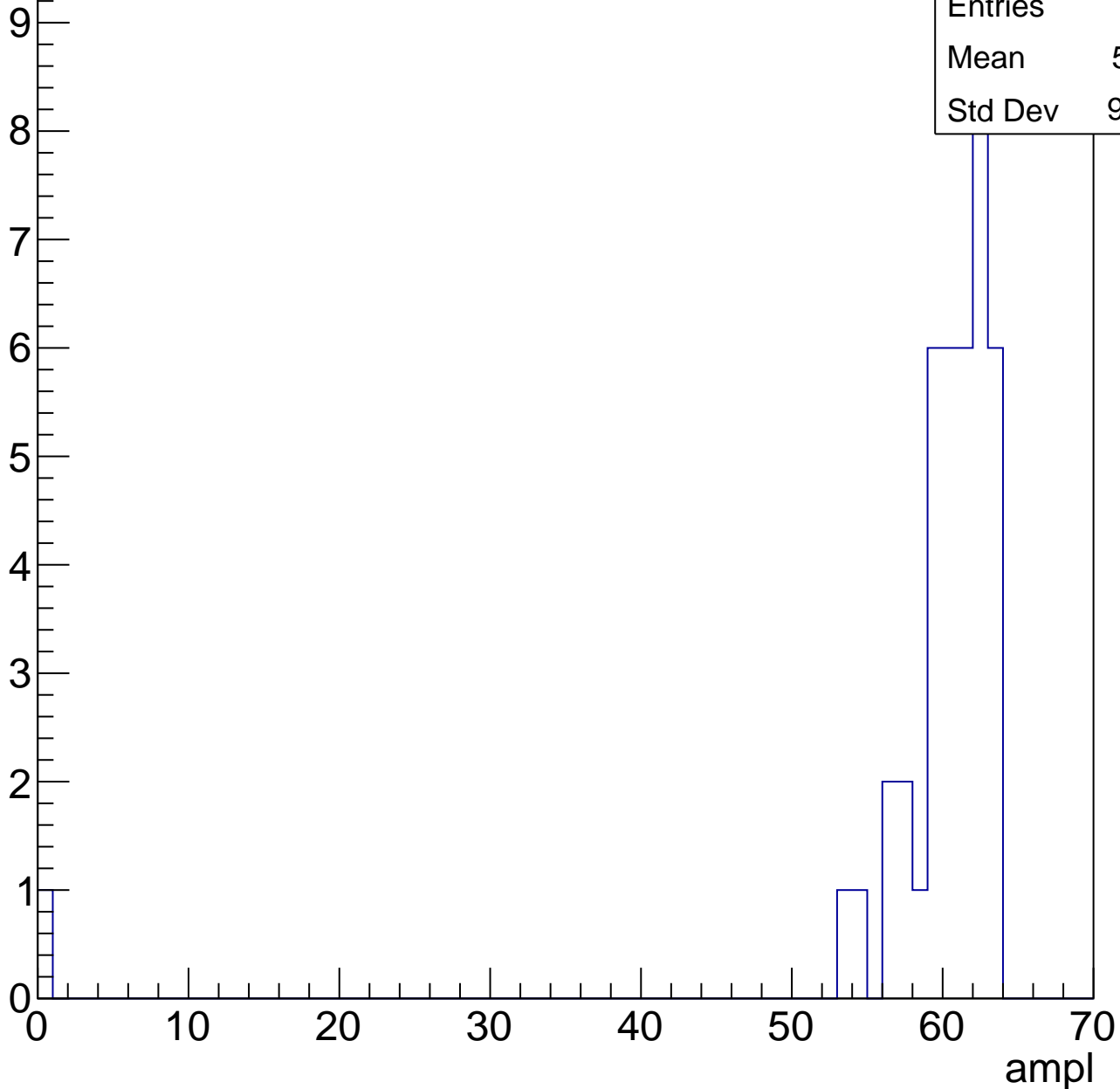


# B0L002S, U23-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.71
Std Dev	9.592



# B0L002S, U23-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

60

Std Dev

0



# B0L002S, U23-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch95, adc0

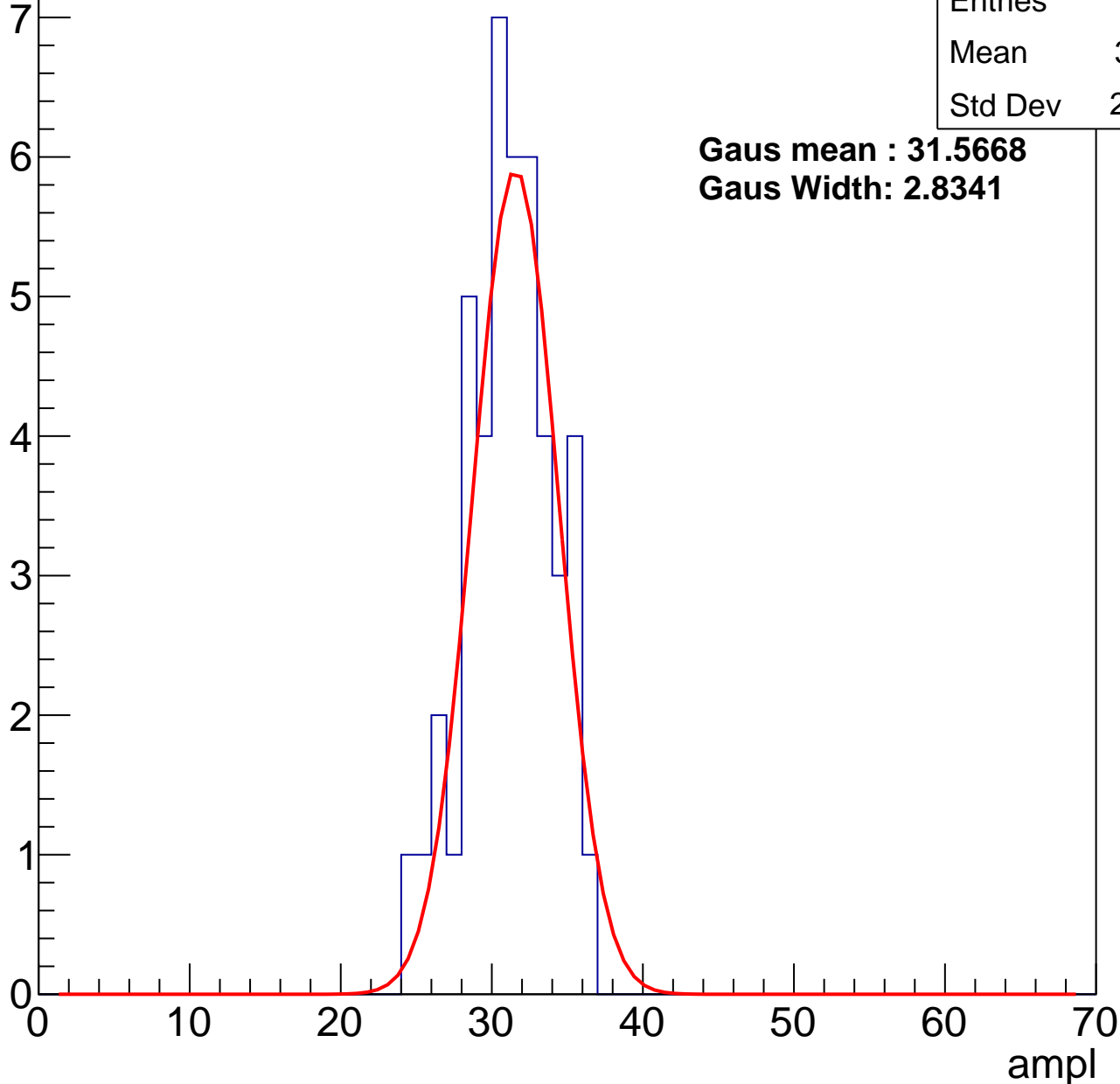
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	45
Mean	30.71
Std Dev	2.802

**Gaus mean : 31.5668**

**Gaus Width: 2.8341**

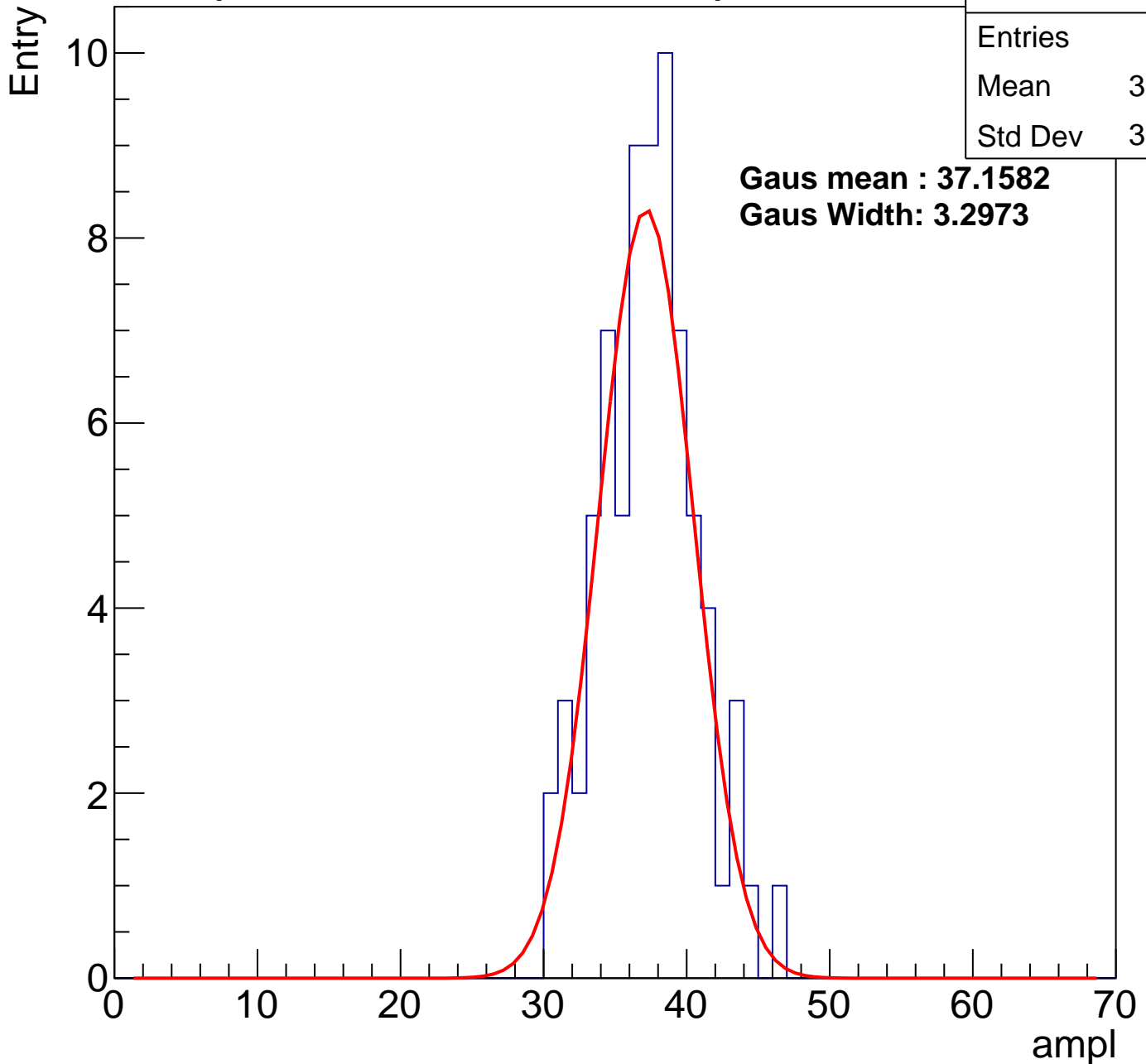


# B0L002S, U23-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	74
Mean	36.89
Std Dev	3.363

**Gaus mean : 37.1582**  
**Gaus Width: 3.2973**



# B0L002S, U23-ch95, adc2

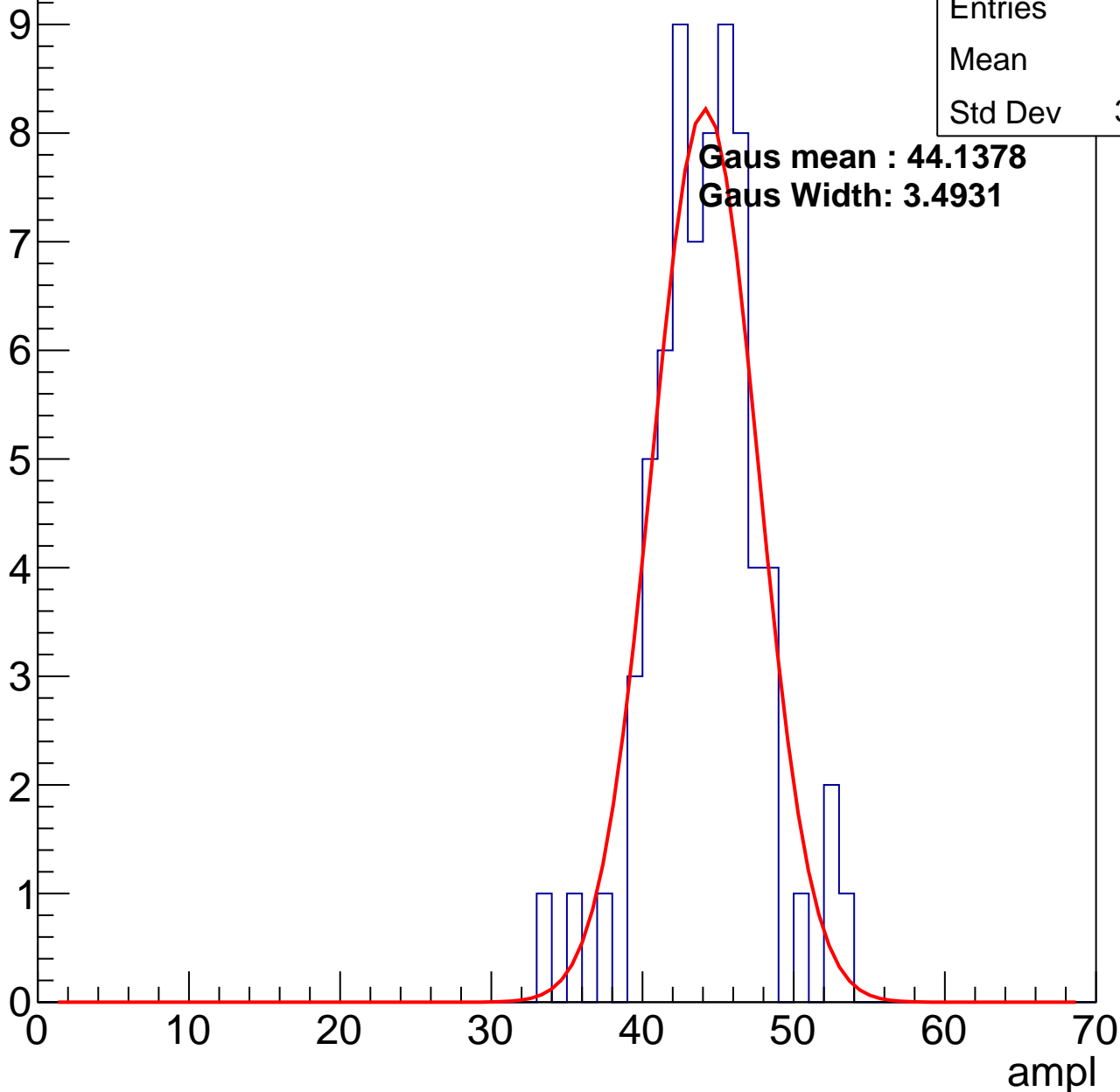
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.7
Std Dev	3.551

**Gaus mean : 44.1378**

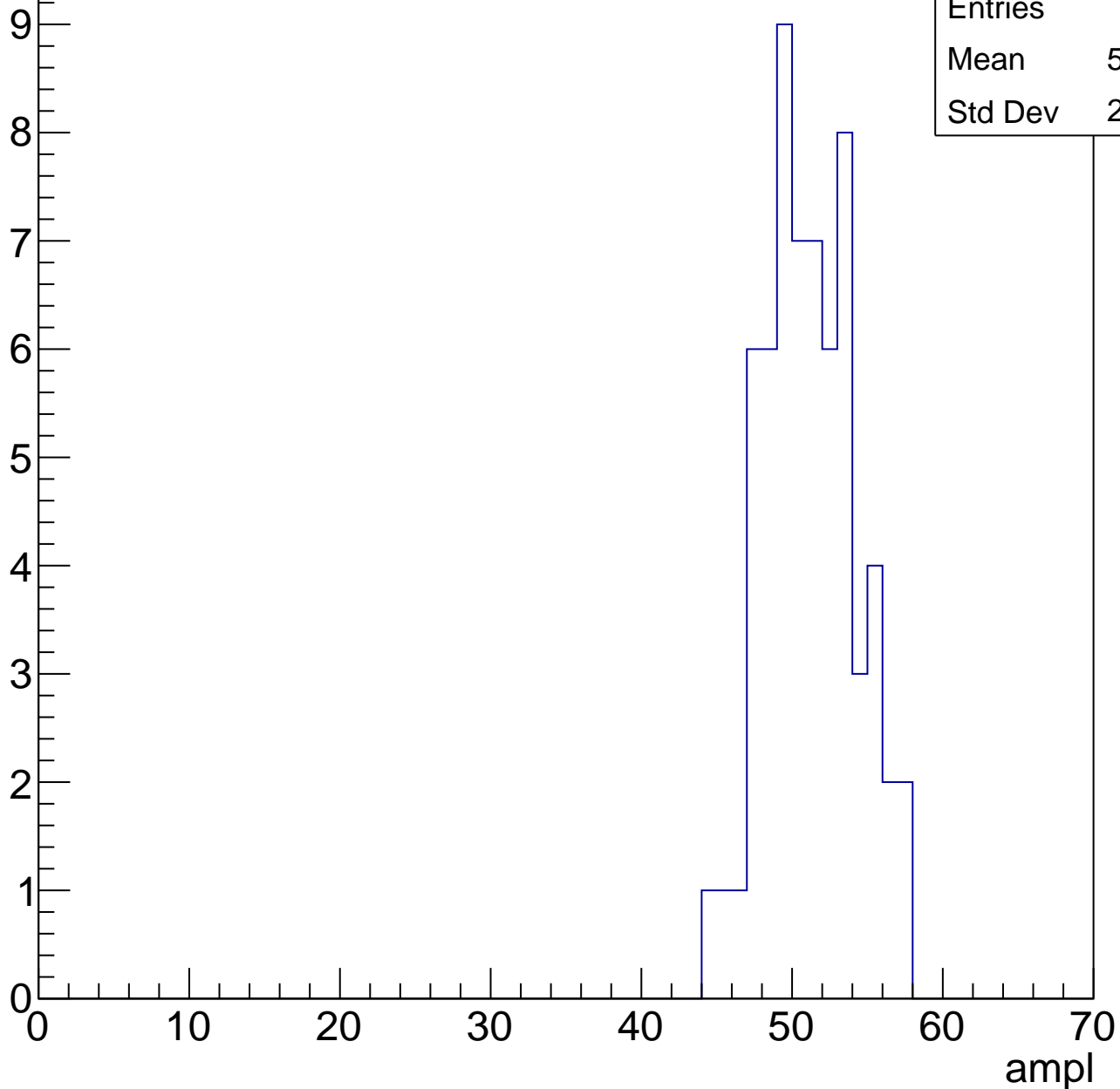
**Gaus Width: 3.4931**



# B0L002S, U23-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

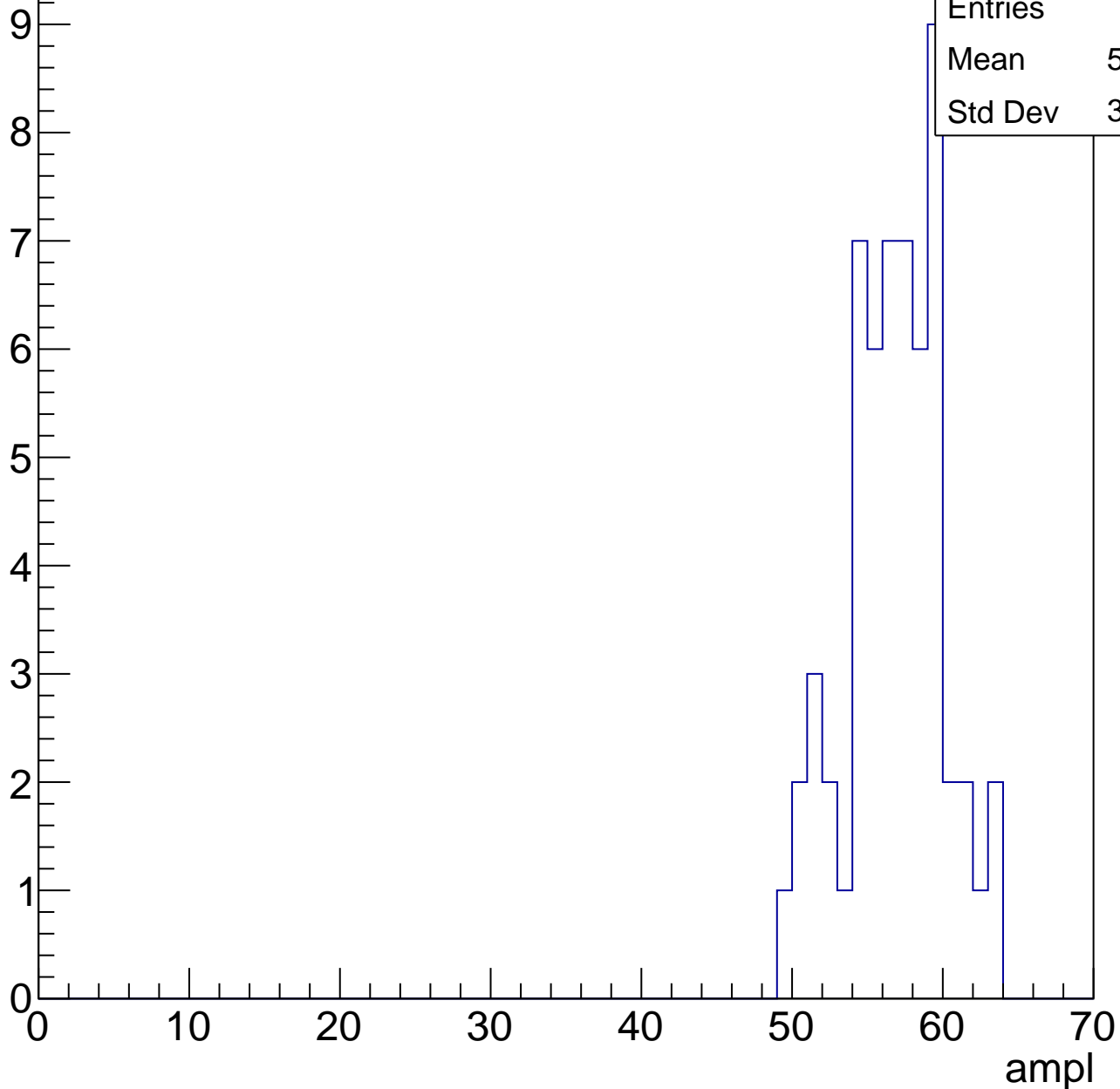


Entries	63
Mean	50.75
Std Dev	2.955

# B0L002S, U23-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

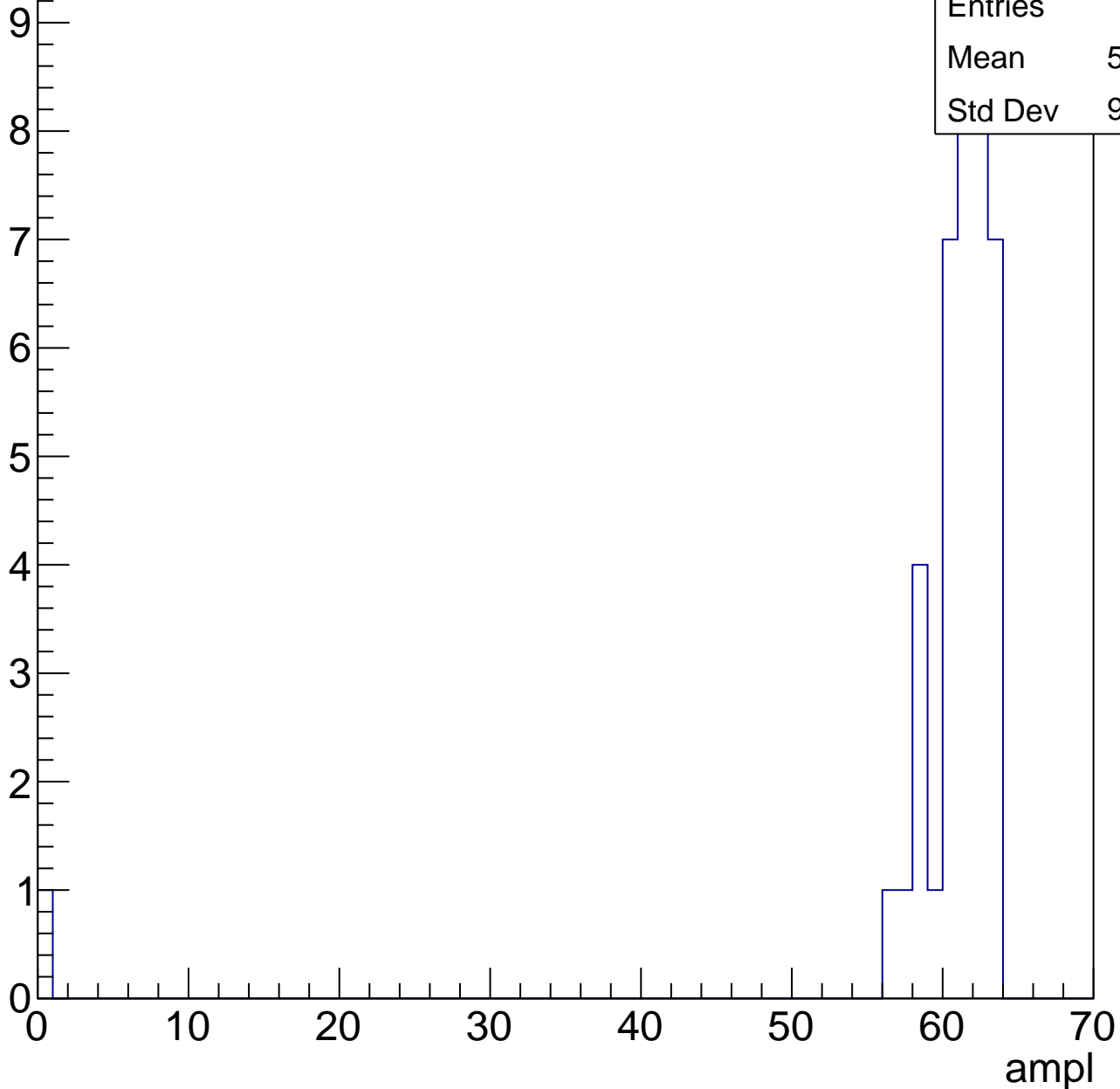


# B0L002S, U23-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.26
Std Dev	9.776



# B0L002S, U23-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch96, adc0

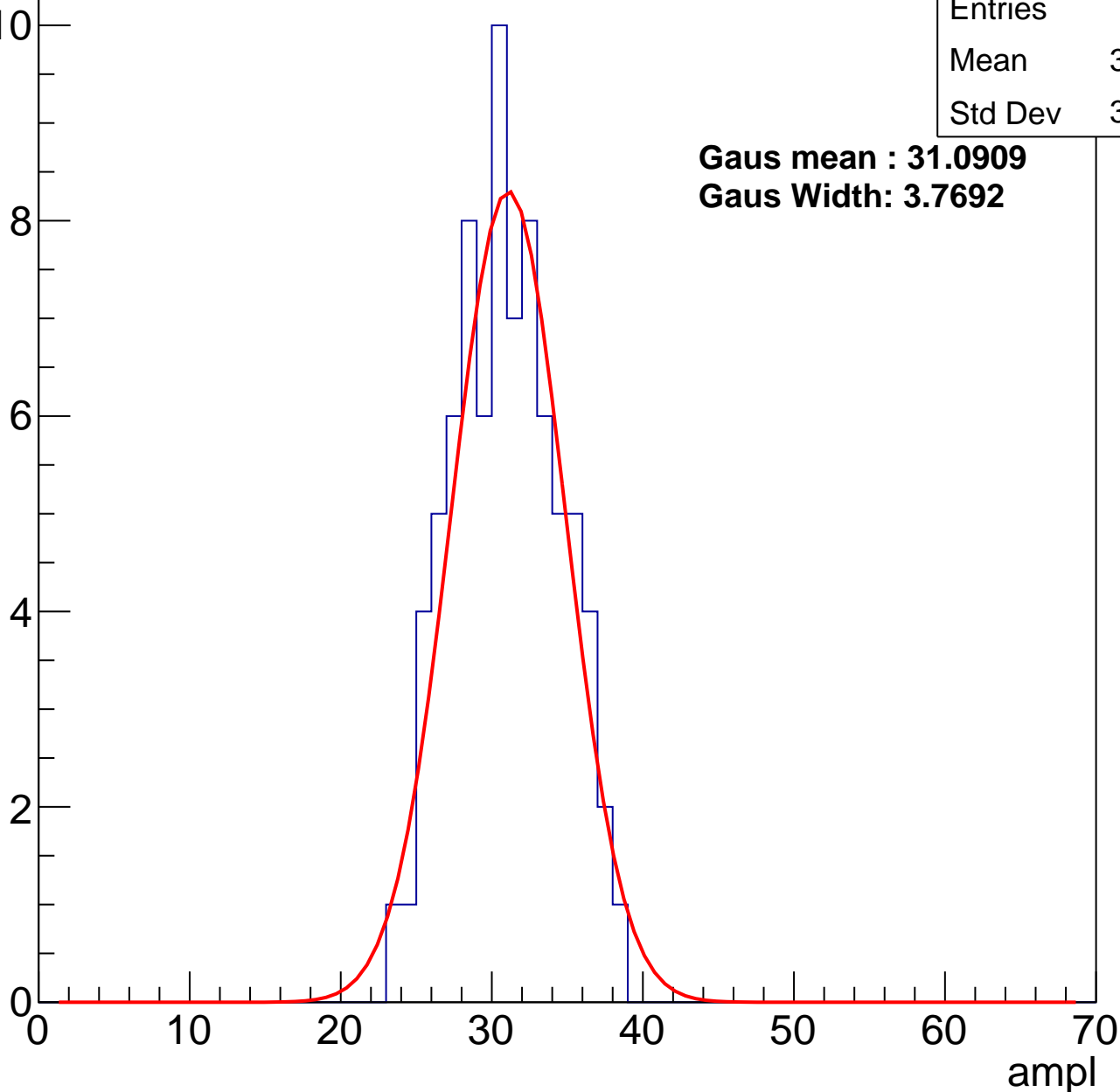
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.49
Std Dev	3.453

**Gaus mean : 31.0909**

**Gaus Width: 3.7692**



# B0L002S, U23-ch96, adc1

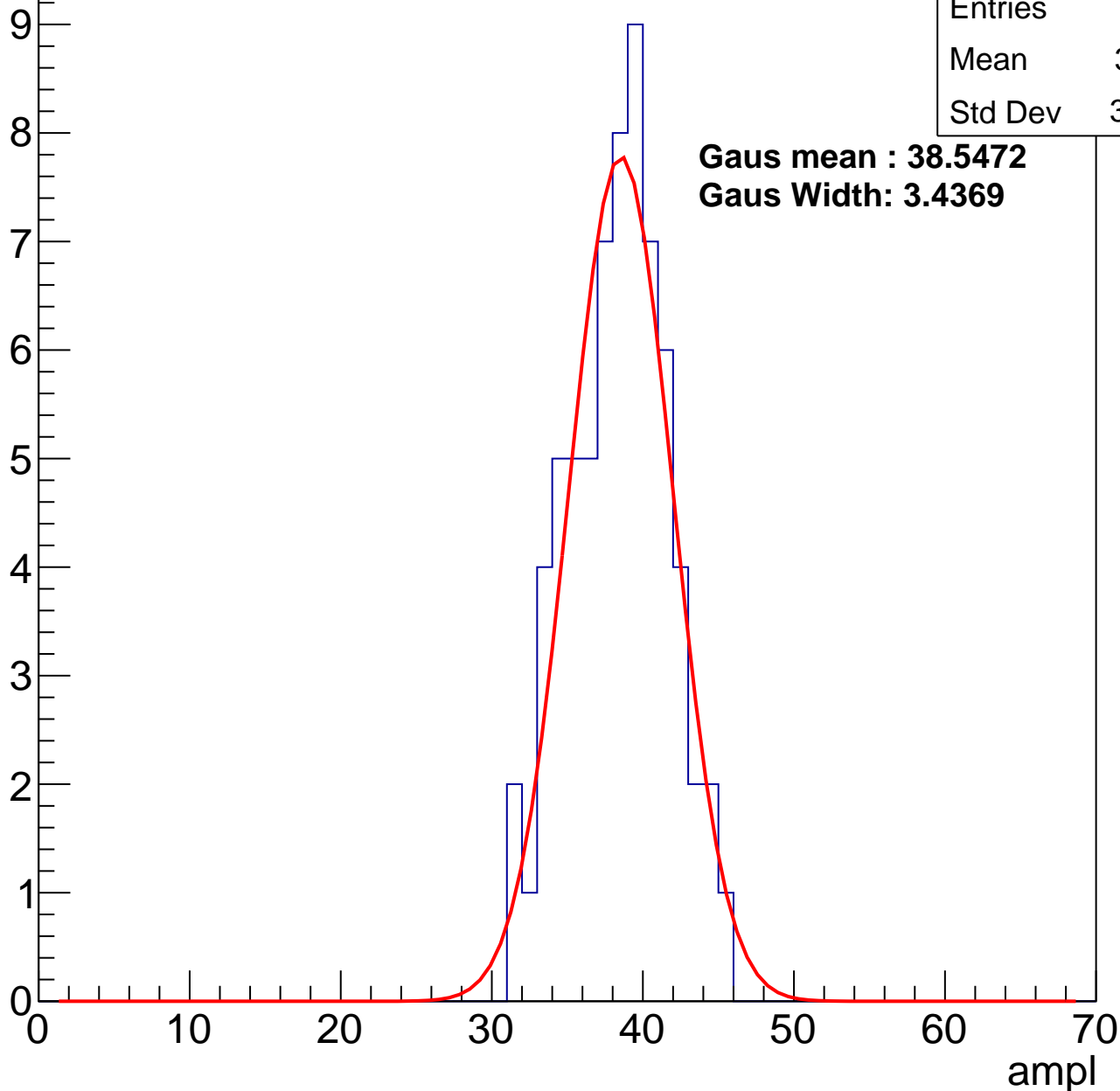
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.91
Std Dev	3.235

**Gaus mean : 38.5472**

**Gaus Width: 3.4369**



# B0L002S, U23-ch96, adc2

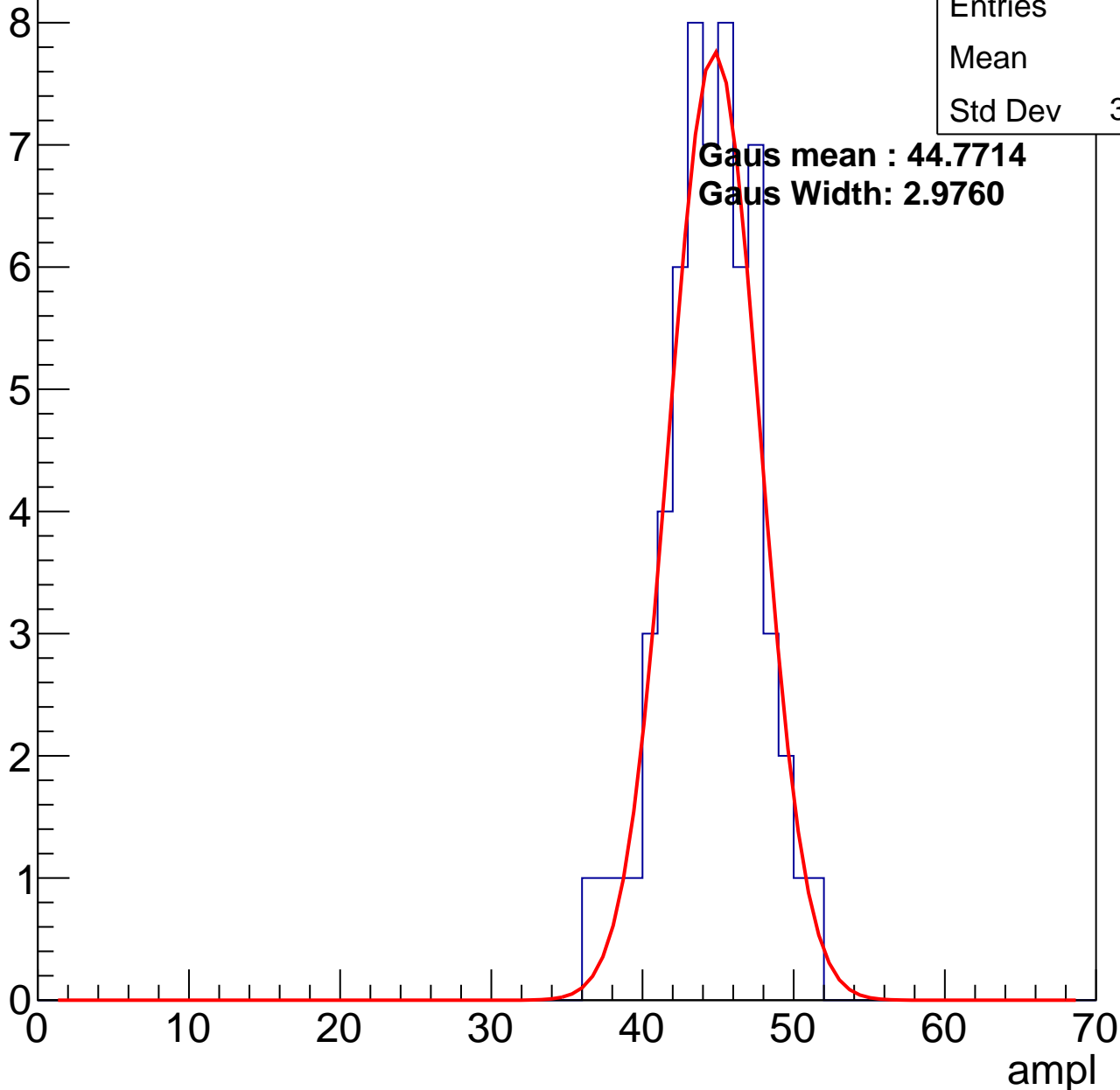
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.1
Std Dev	3.075

**Gaus mean : 44.7714**

**Gaus Width: 2.9760**

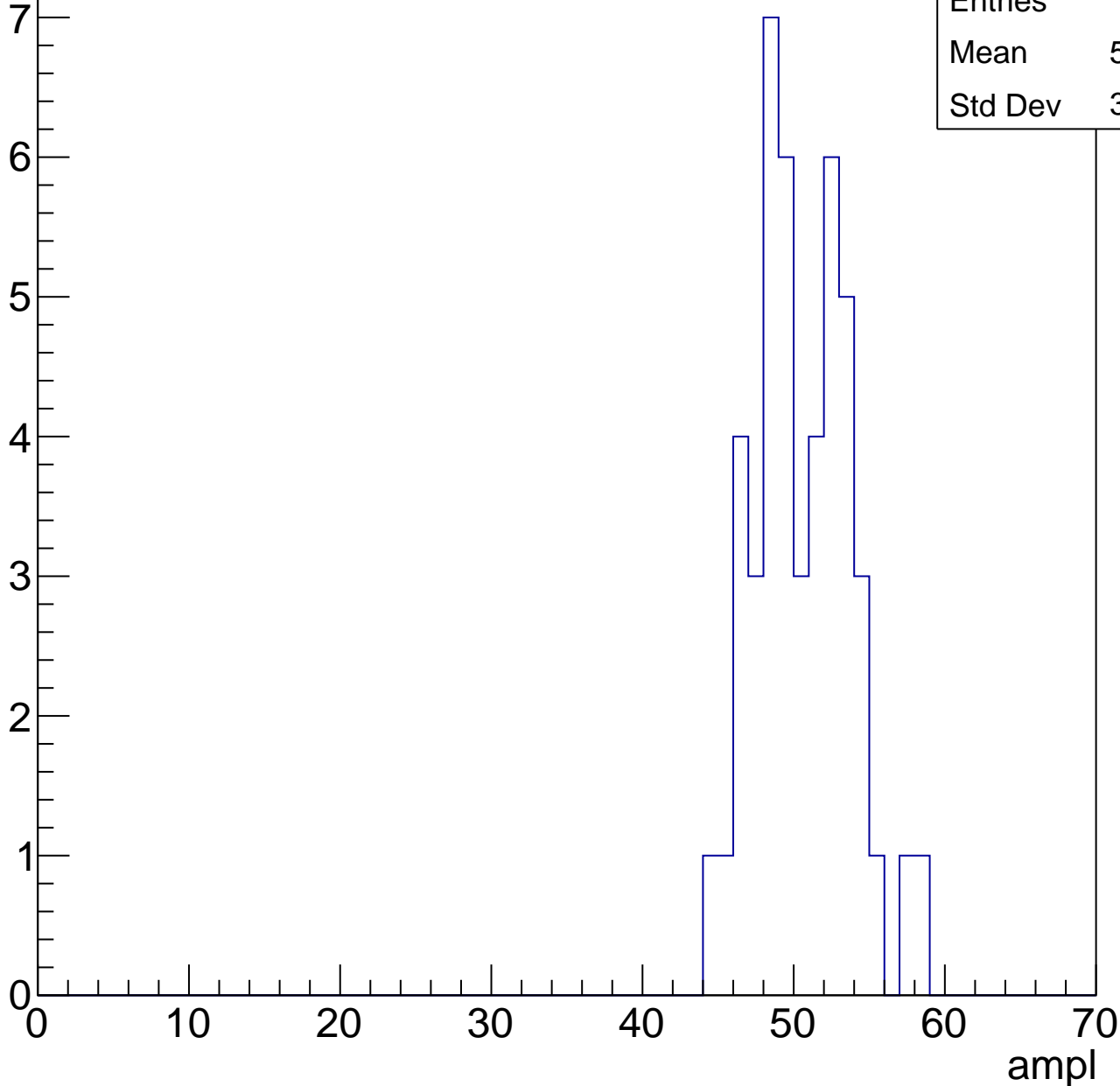


# B0L002S, U23-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

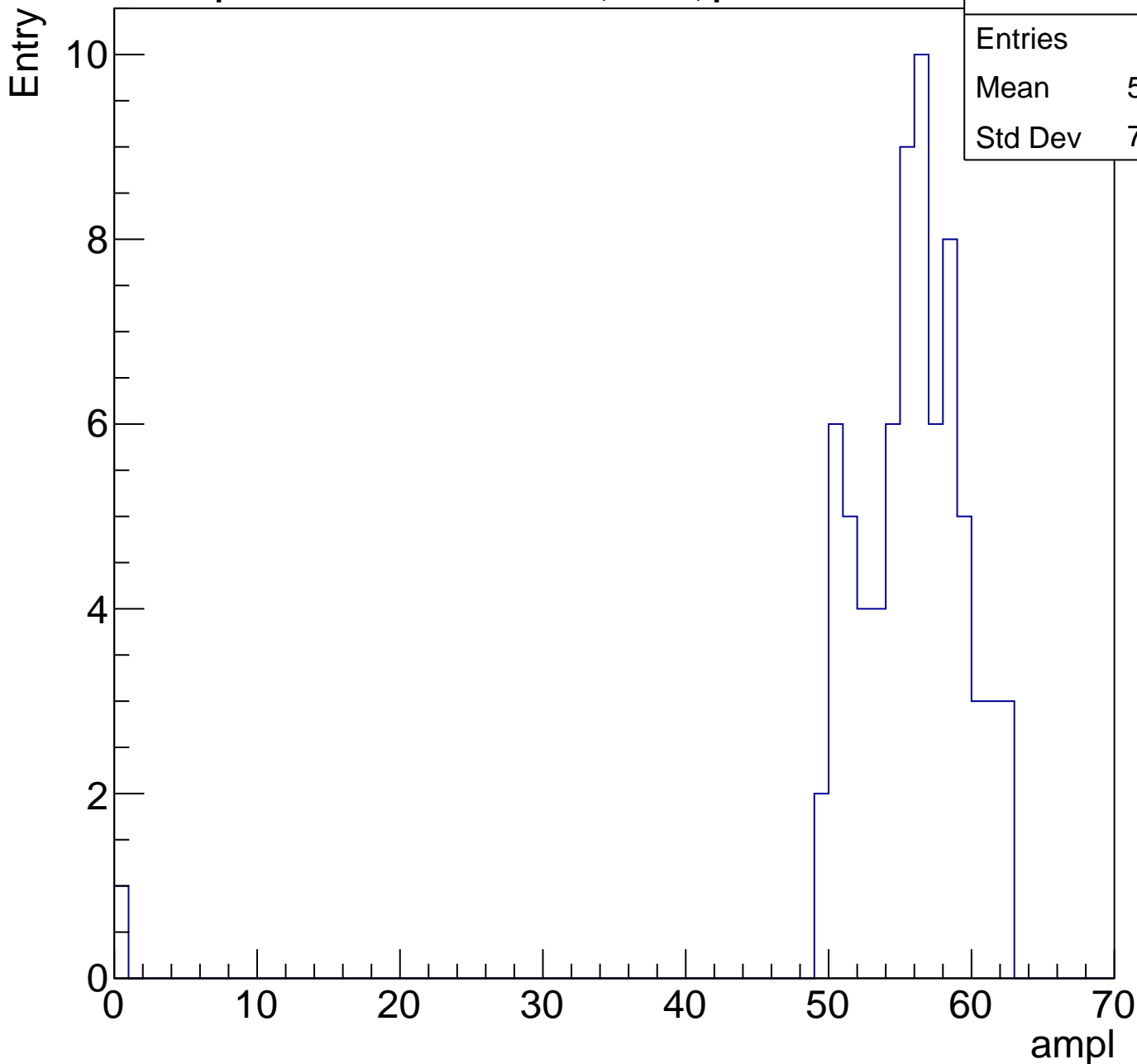
Entries	46
Mean	50.15
Std Dev	3.107



# B0L002S, U23-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	75
Mean	54.69
Std Dev	7.206

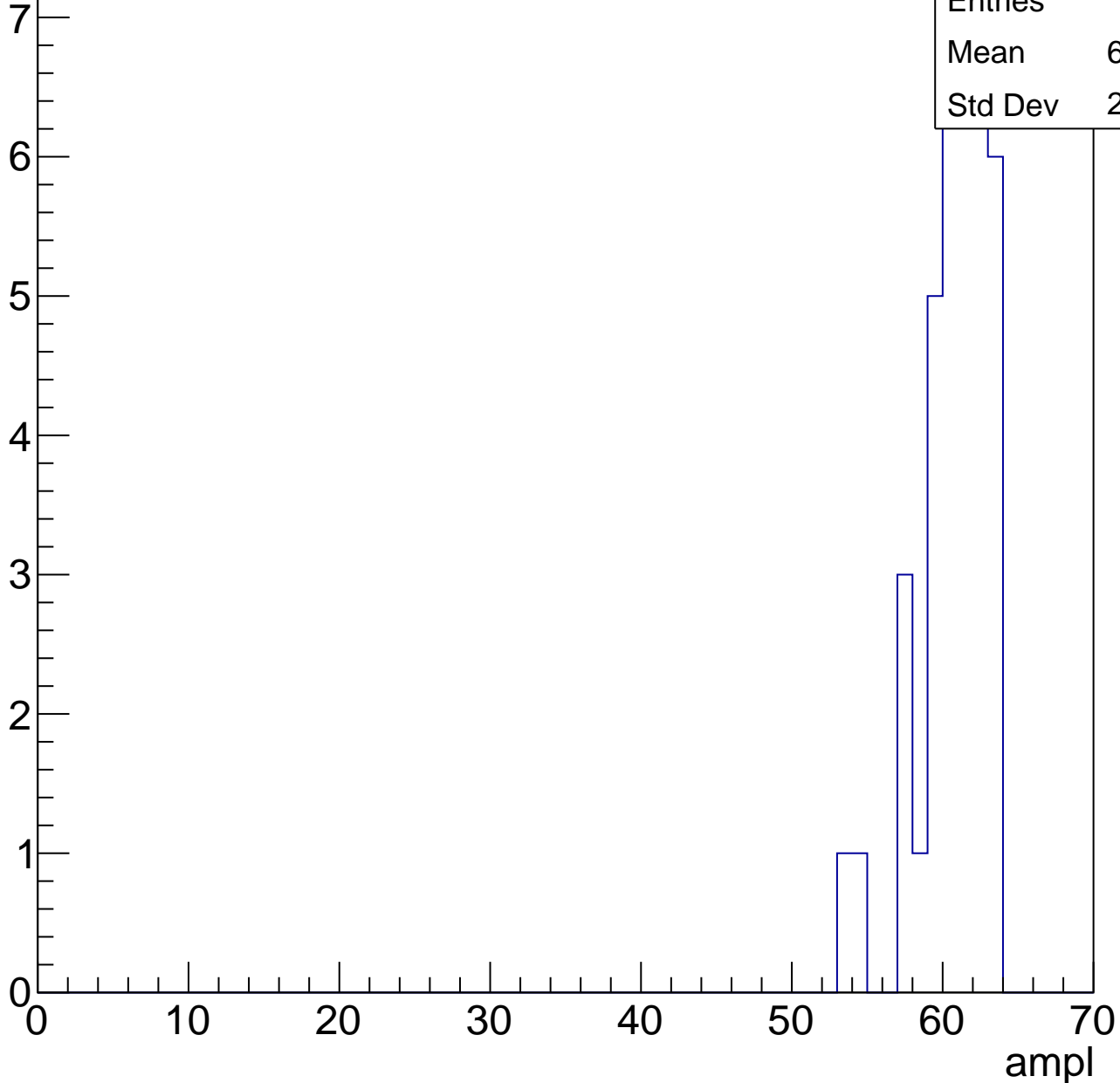


# B0L002S, U23-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	60.26
Std Dev	2.336



# B0L002S, U23-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	4
Mean	63
Std Dev	0



# B0L002S, U23-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L002S, U23-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	83
Mean	32.06
Std Dev	3.507

**Gaus mean : 32.2645**

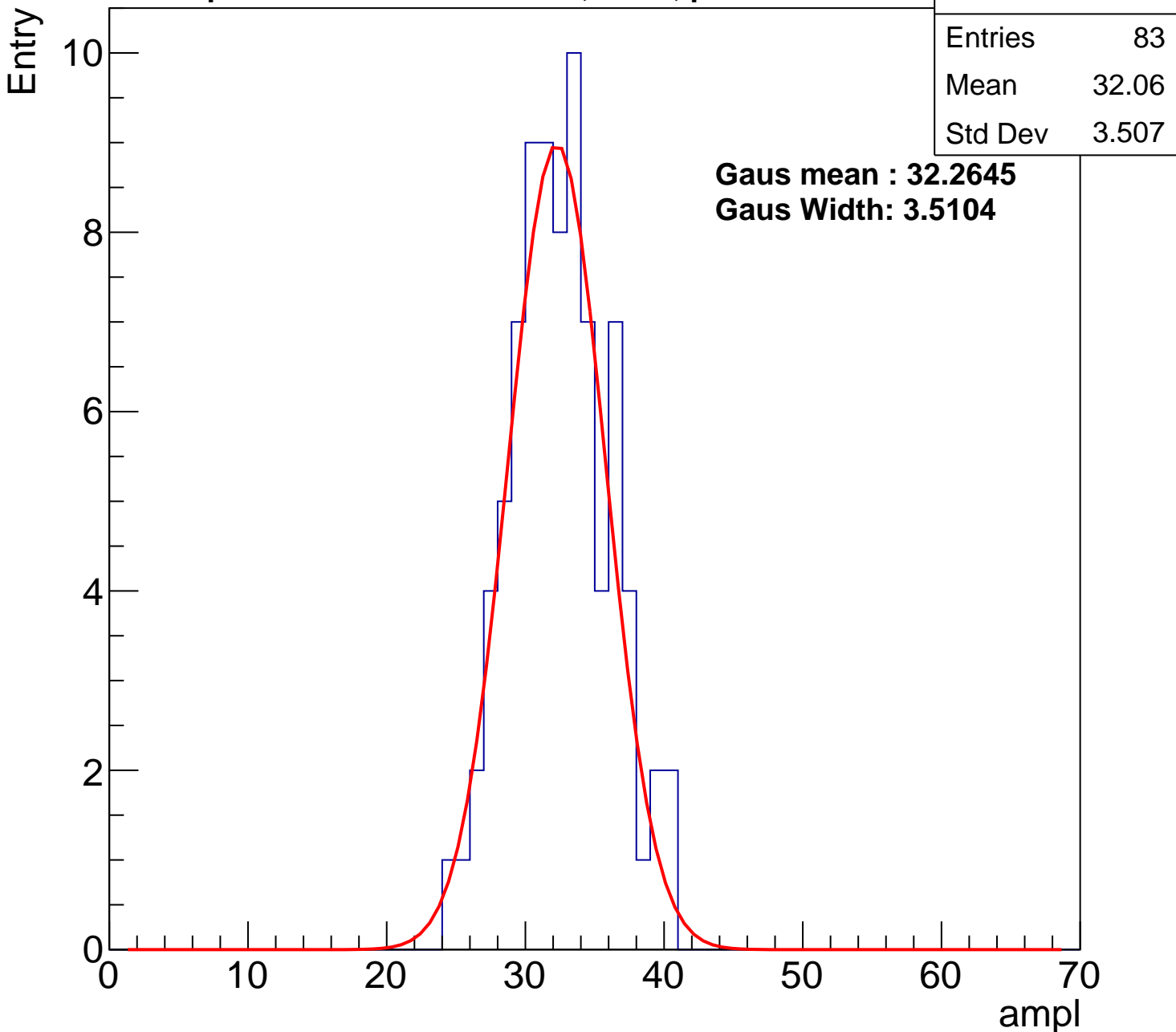
**Gaus Width: 3.5104**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch97, adc1

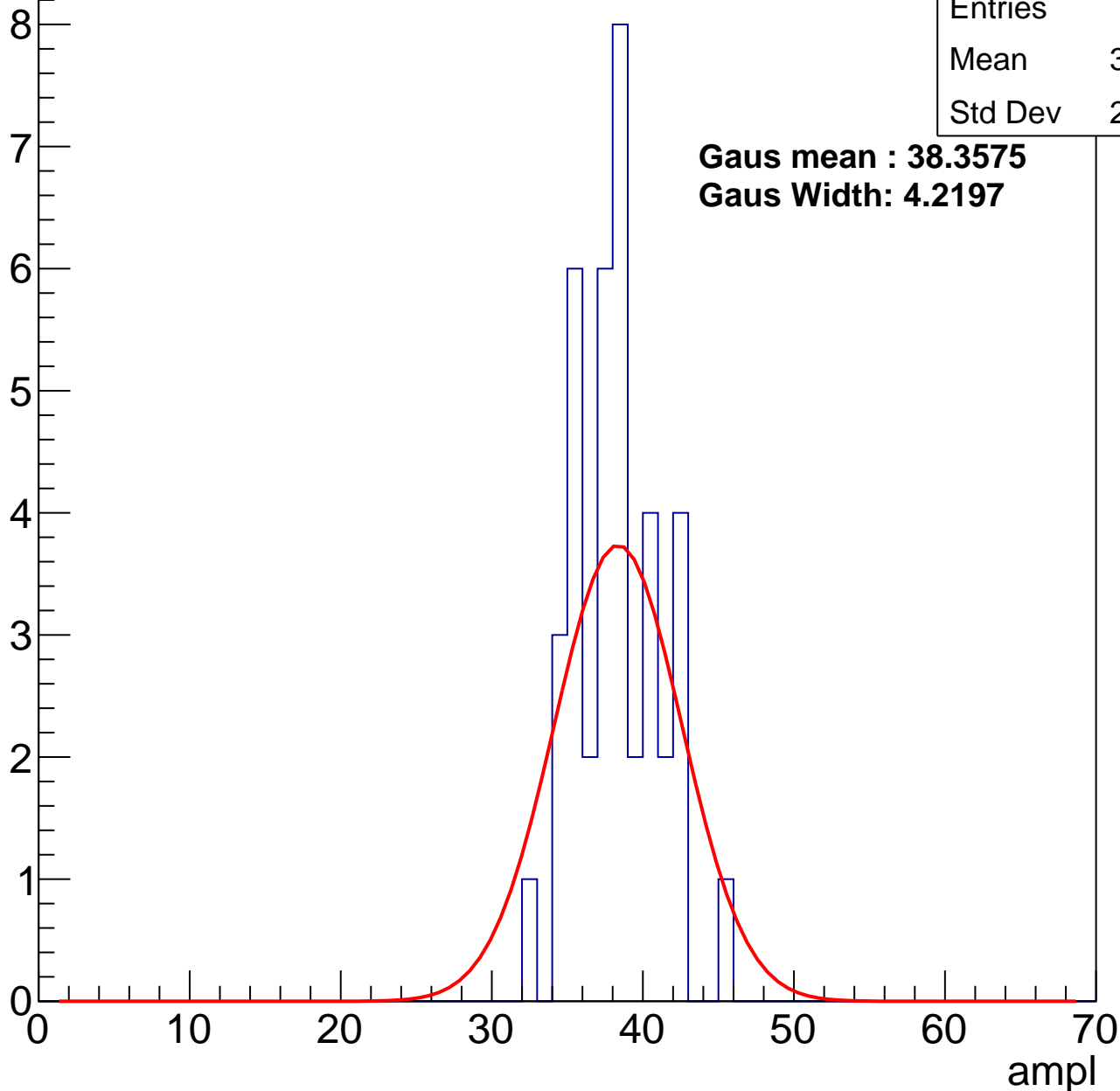
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	39
Mean	37.82
Std Dev	2.772

**Gaus mean : 38.3575**

**Gaus Width: 4.2197**



# B0L002S, U23-ch97, adc2

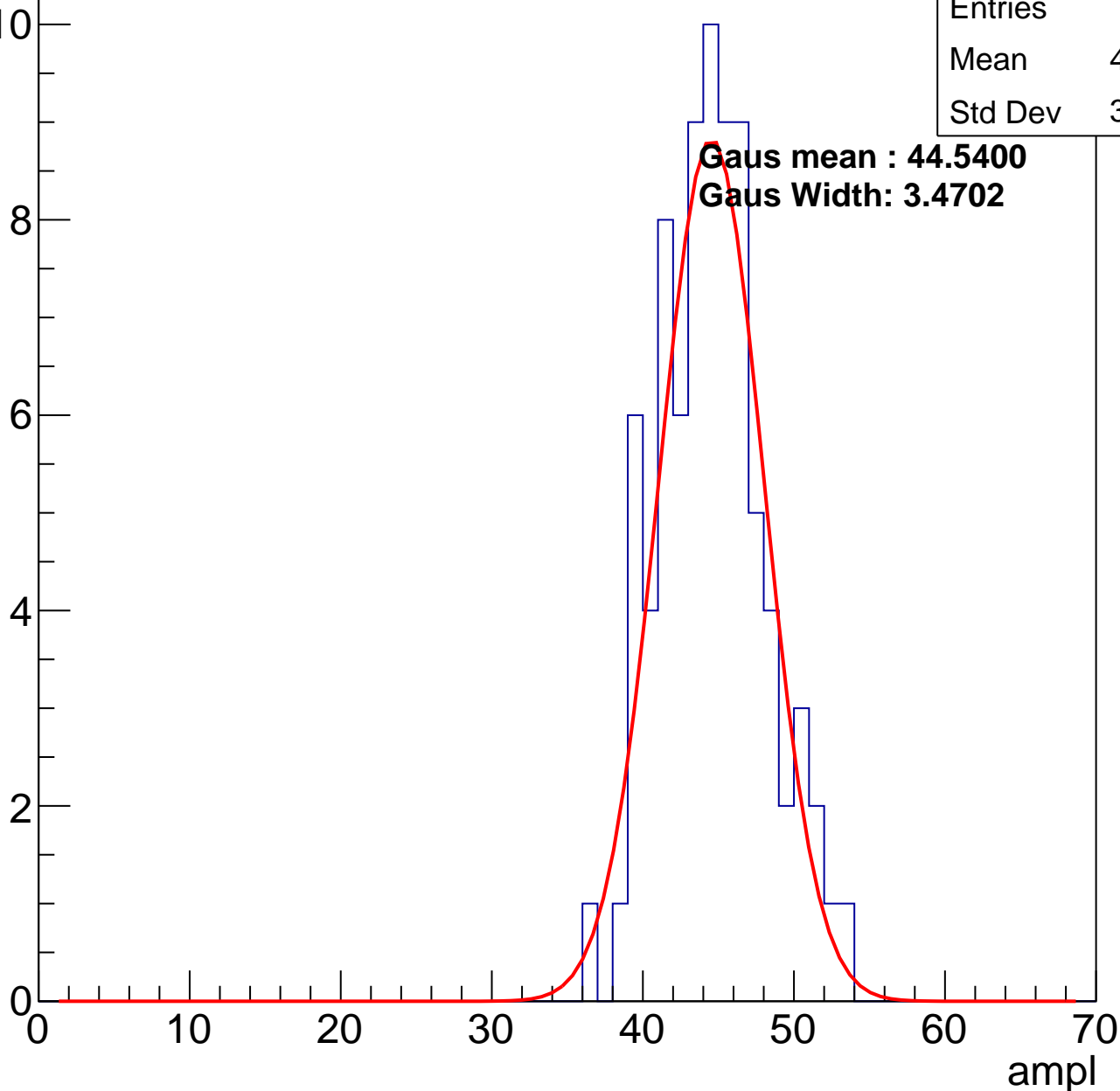
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	44.15
Std Dev	3.464

**Gaus mean : 44.5400**

**Gaus Width: 3.4702**

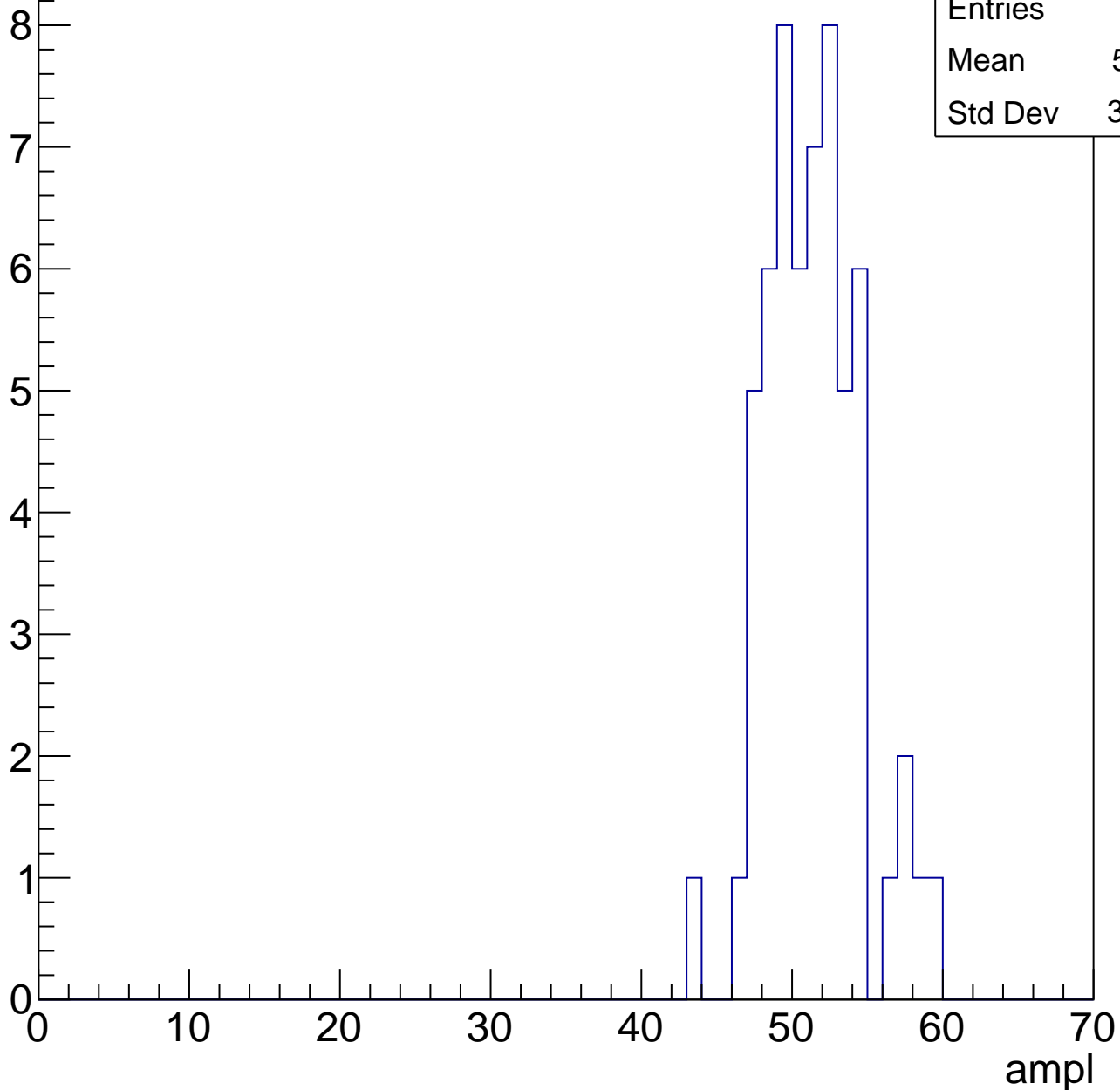


# B0L002S, U23-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	50.91
Std Dev	3.087



# B0L002S, U23-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

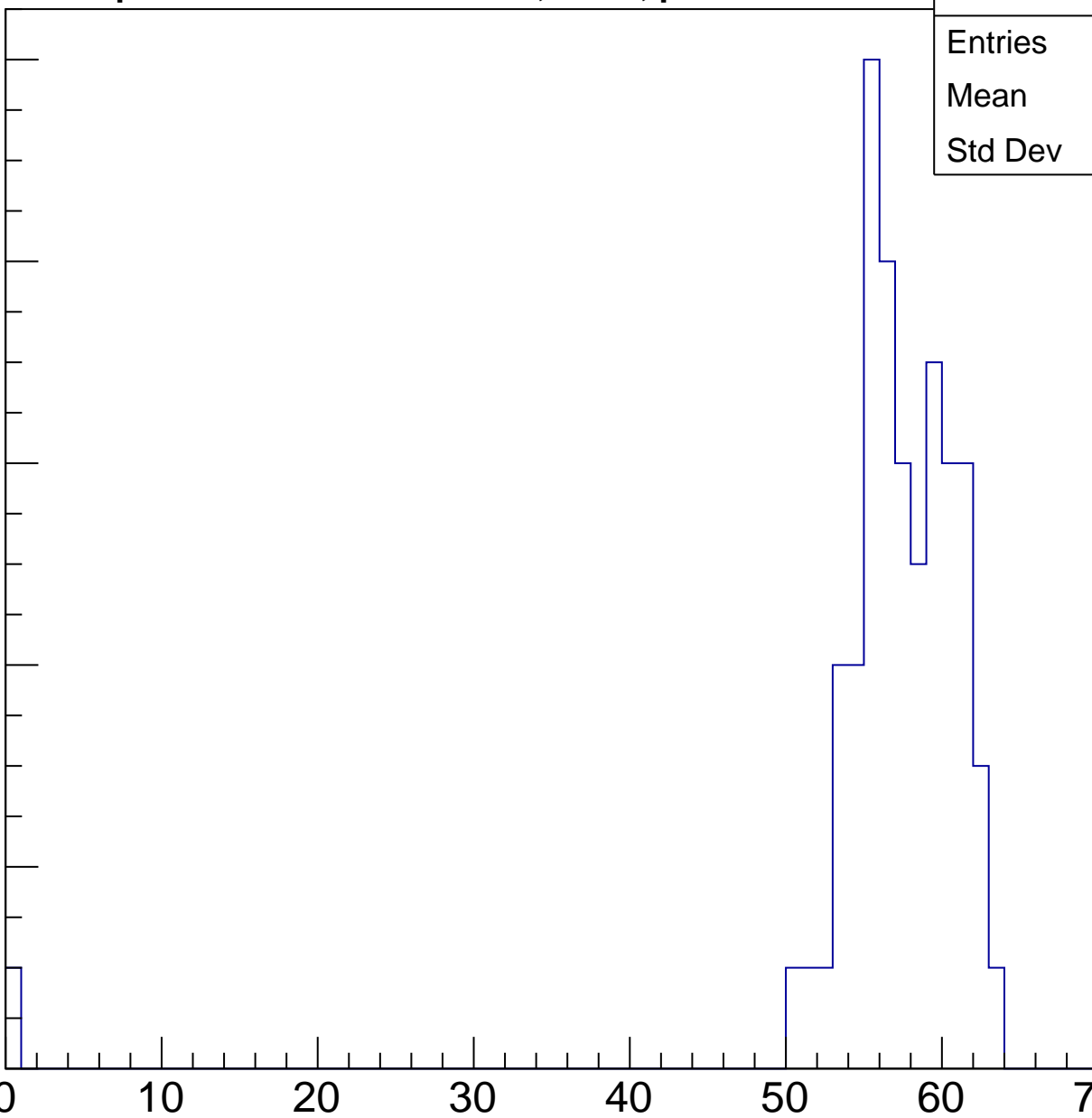
Entries	64
Mean	56.23
Std Dev	7.668

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

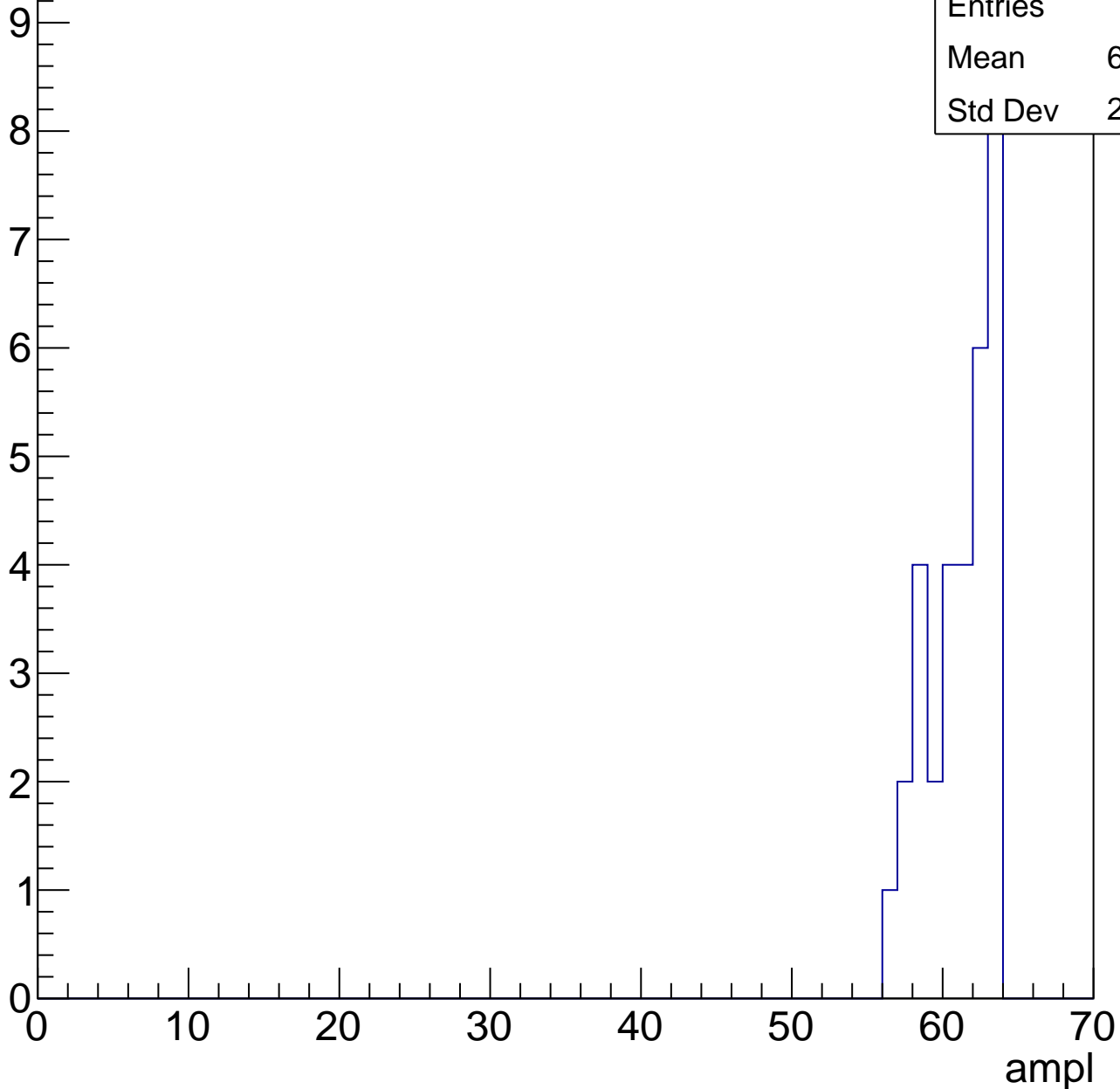


# B0L002S, U23-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	32
Mean	60.72
Std Dev	2.125



# B0L002S, U23-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



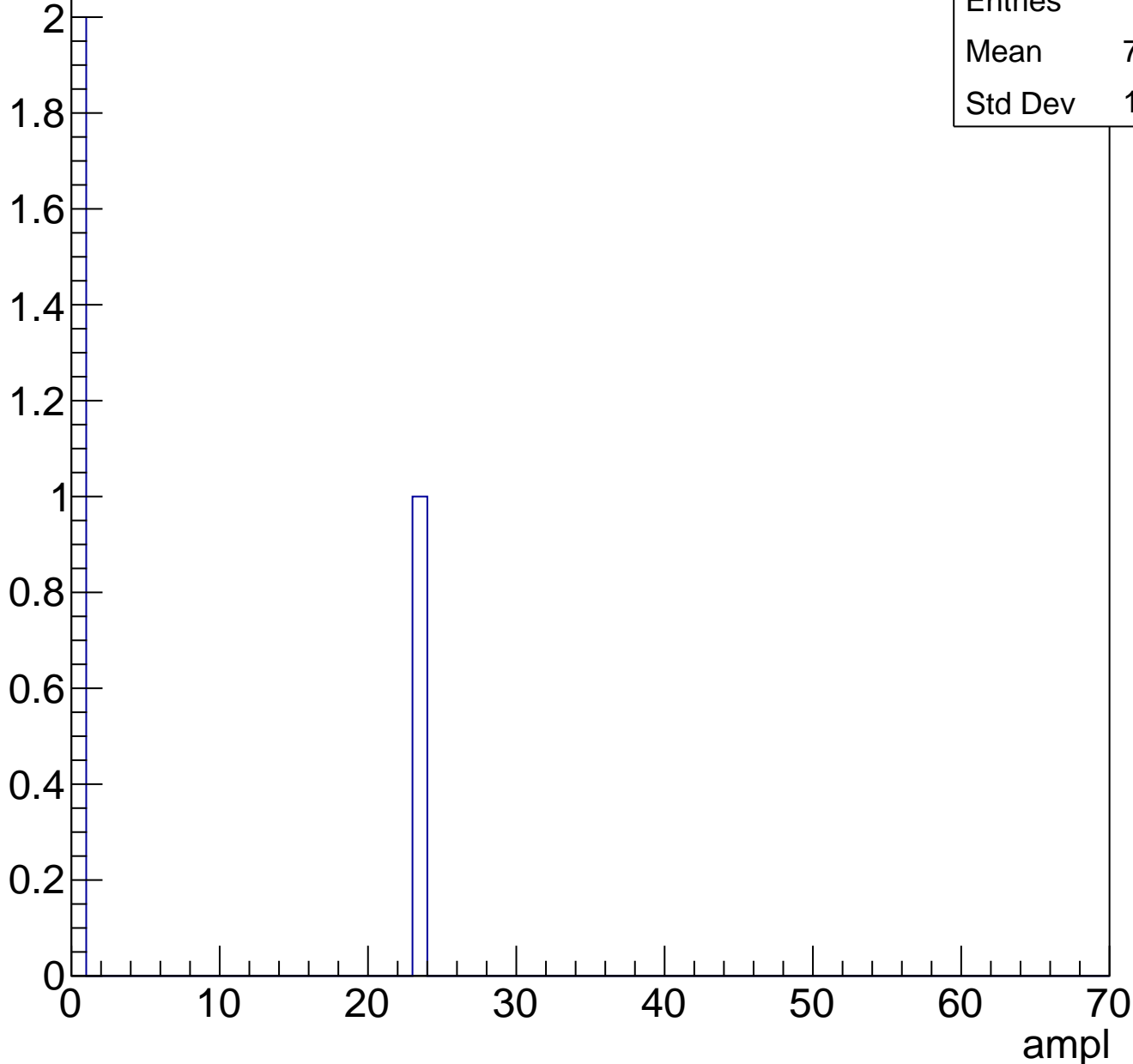
Entries	1
Mean	62
Std Dev	0



# B0L002S, U23-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L002S, U23-ch98, adc0

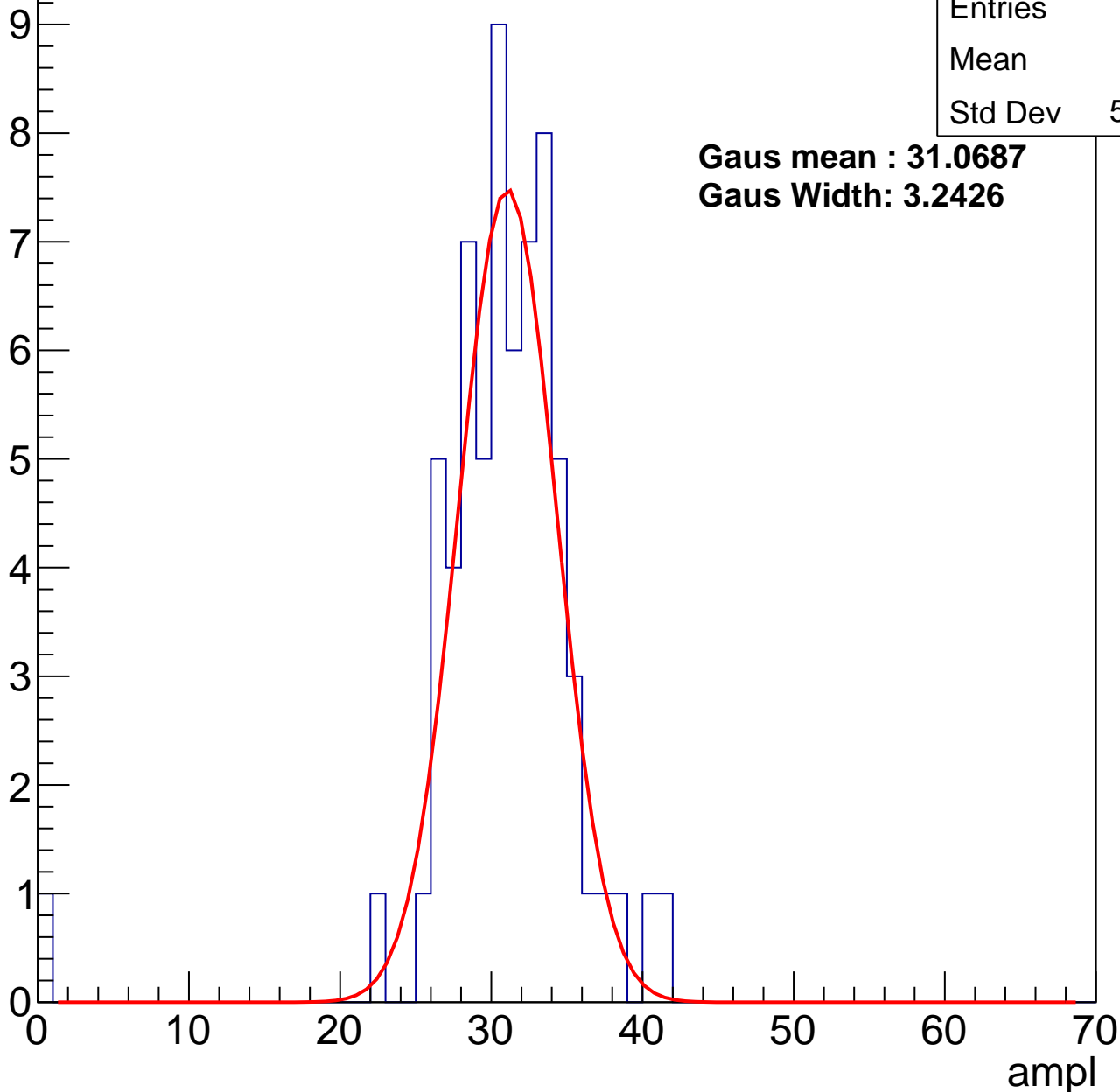
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.4
Std Dev	5.123

**Gaus mean : 31.0687**

**Gaus Width: 3.2426**



# B0L002S, U23-ch98, adc1

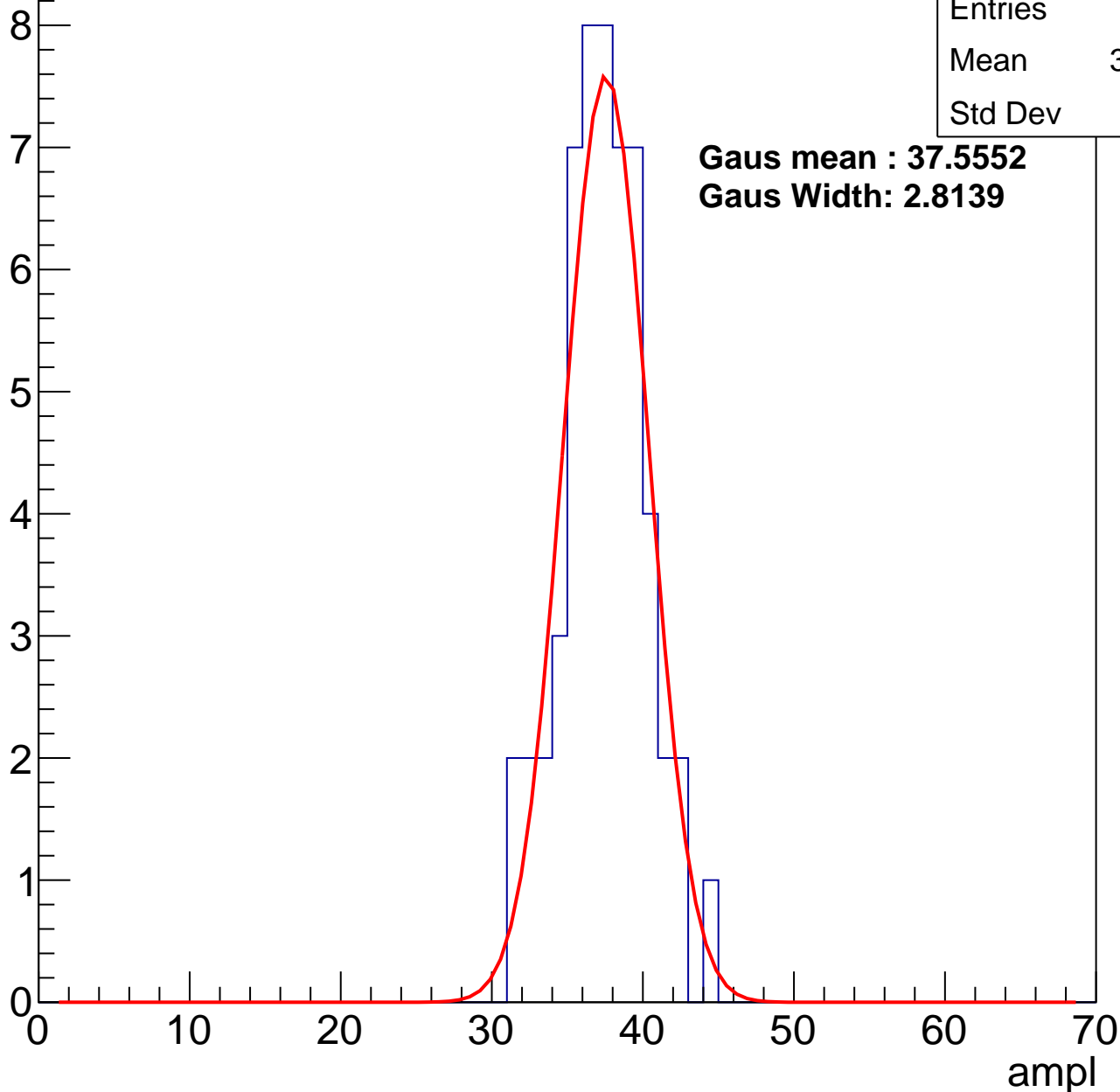
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.95
Std Dev	2.76

**Gaus mean : 37.5552**

**Gaus Width: 2.8139**



# B0L002S, U23-ch98, adc2

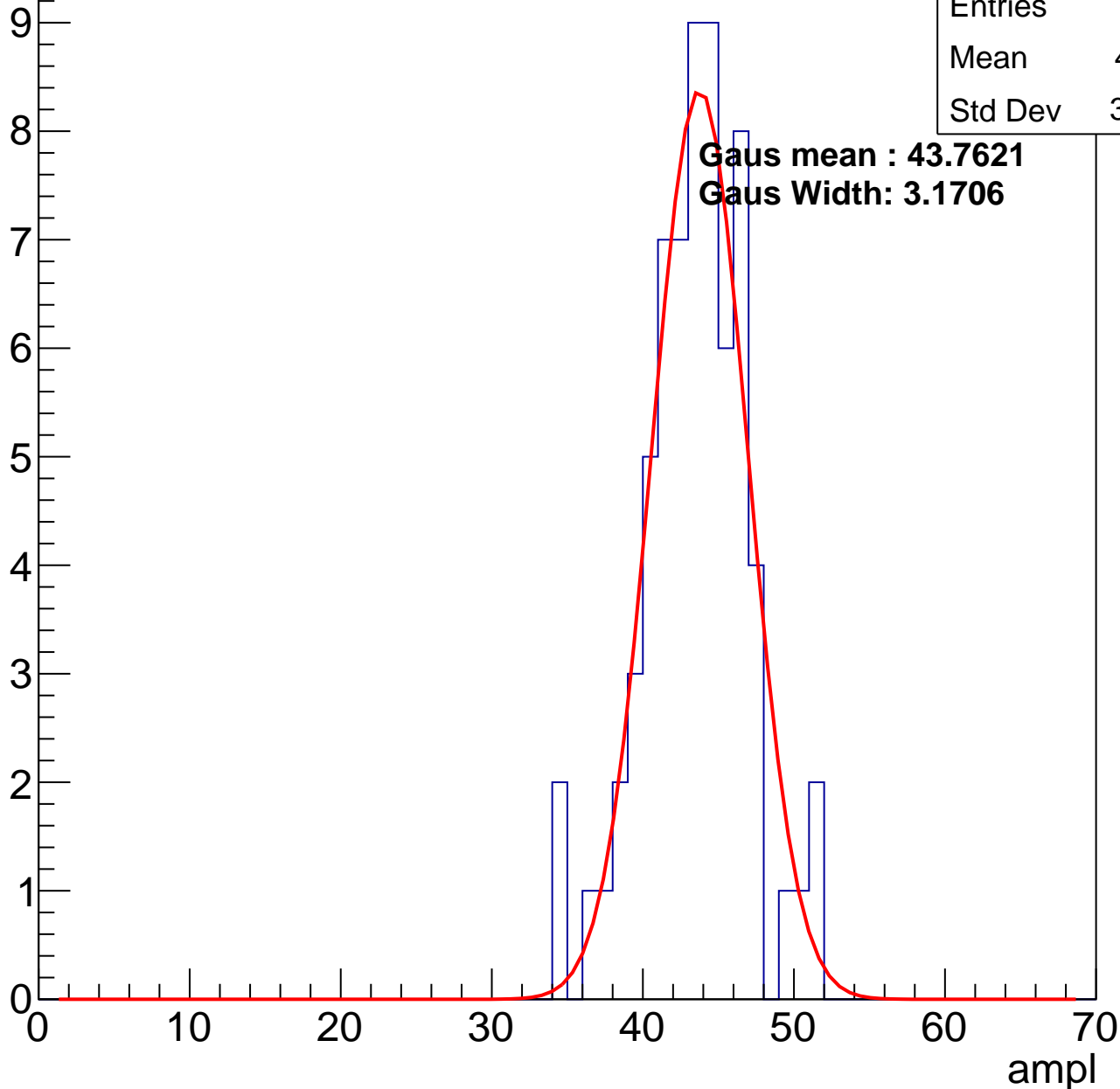
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.01
Std Dev	3.445

**Gaus mean : 43.7621**

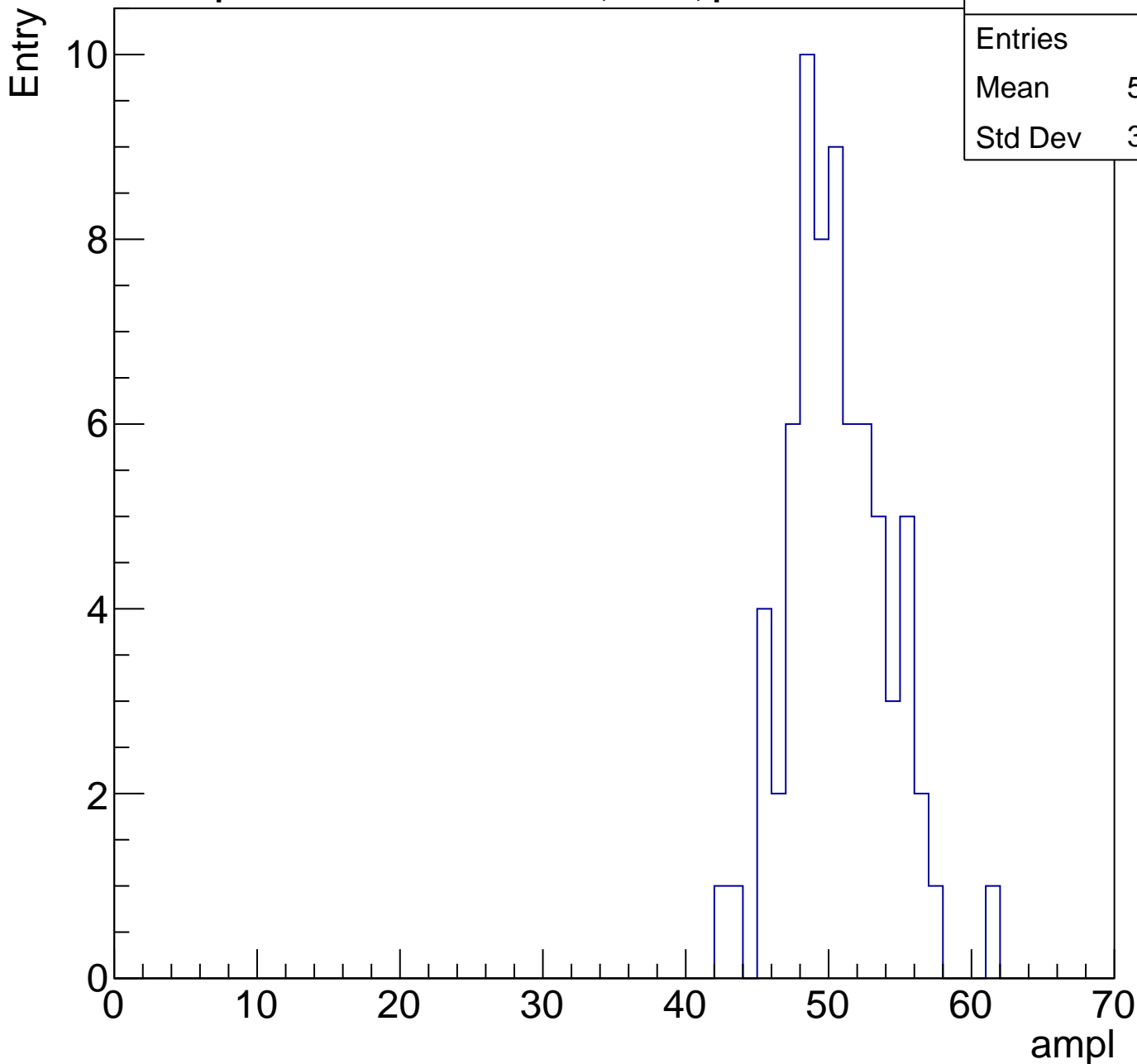
**Gaus Width: 3.1706**



# B0L002S, U23-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	70
Mean	50.16
Std Dev	3.454

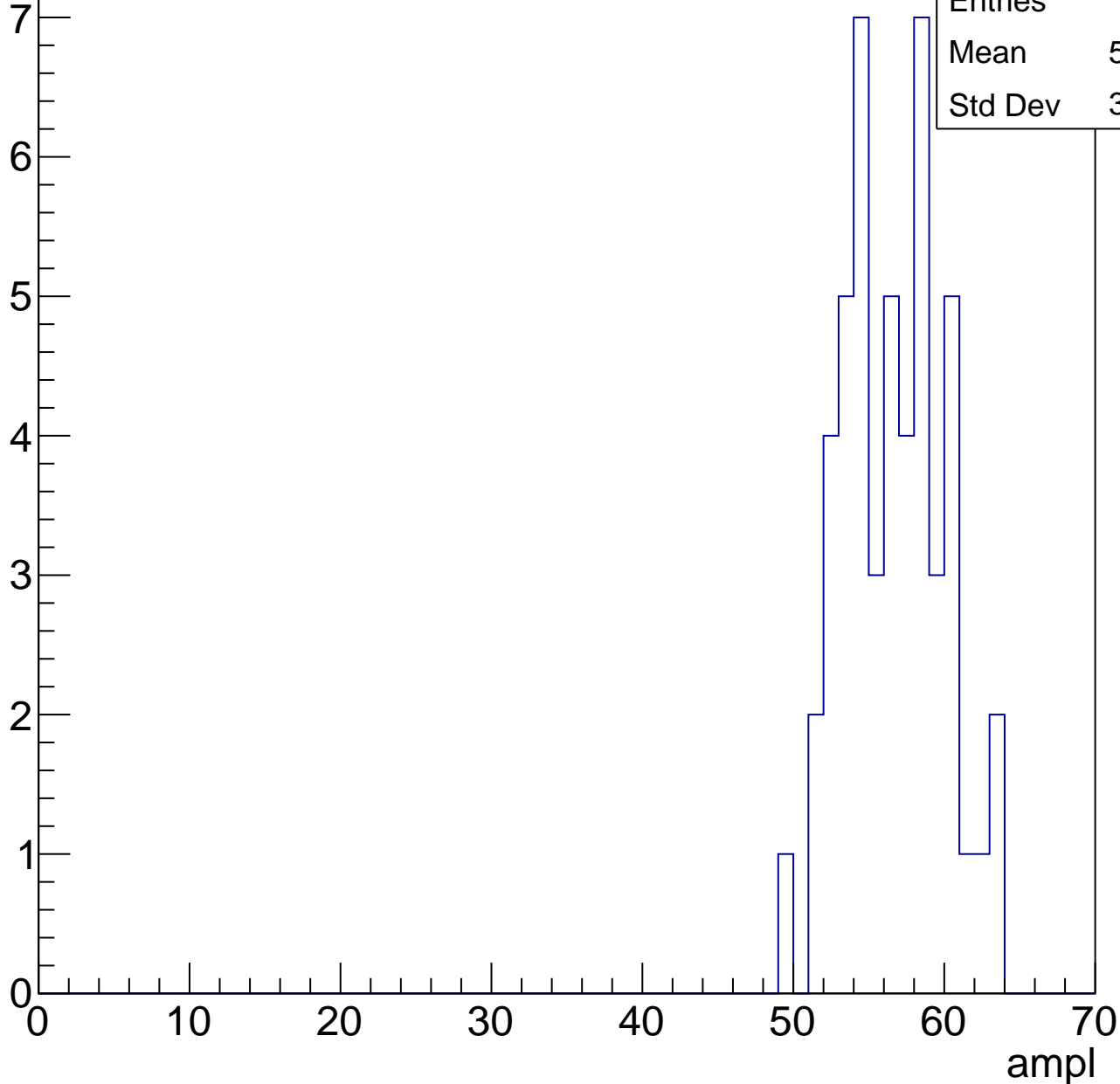


# B0L002S, U23-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.14
Std Dev	3.274

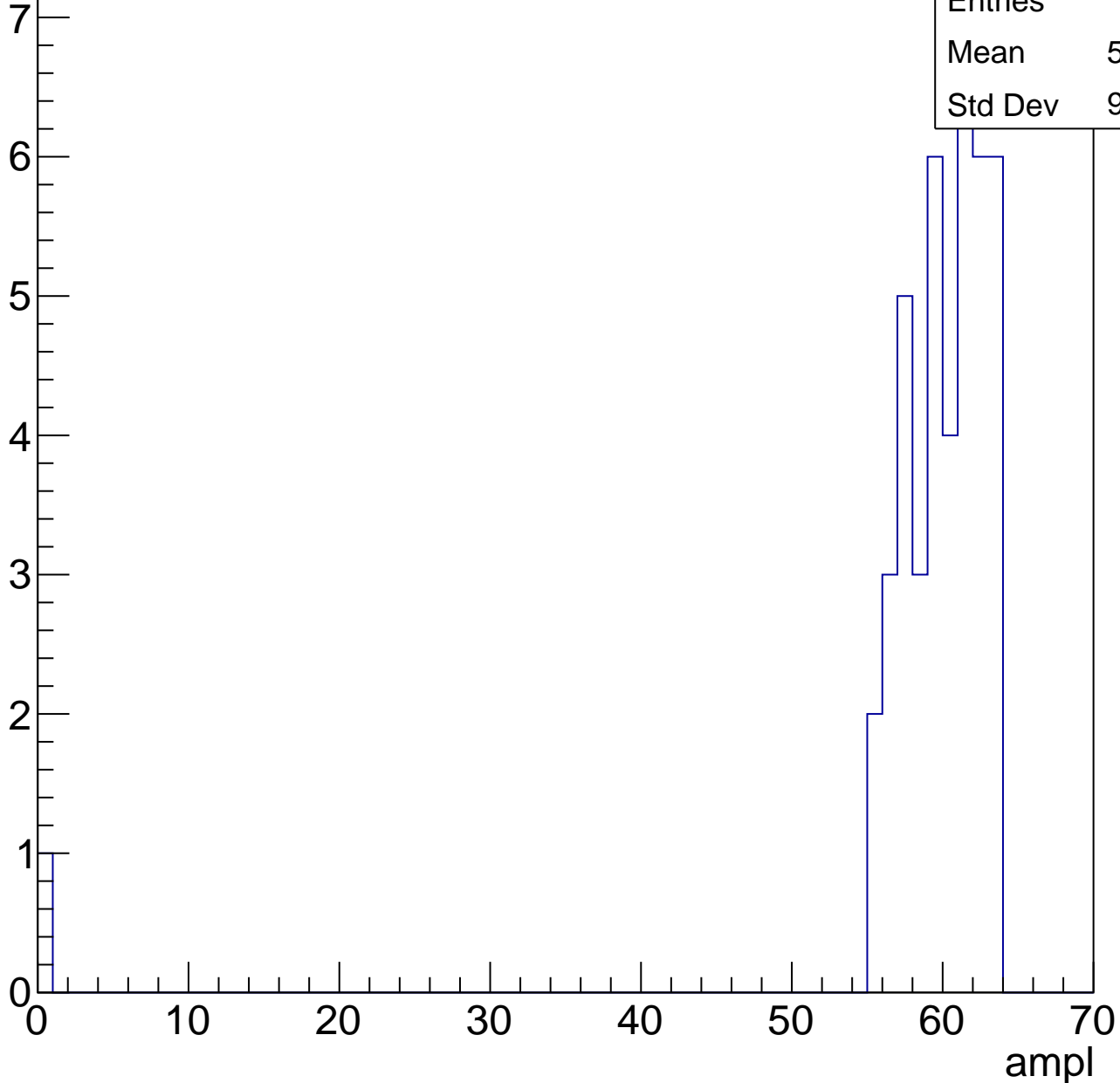


# B0L002S, U23-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

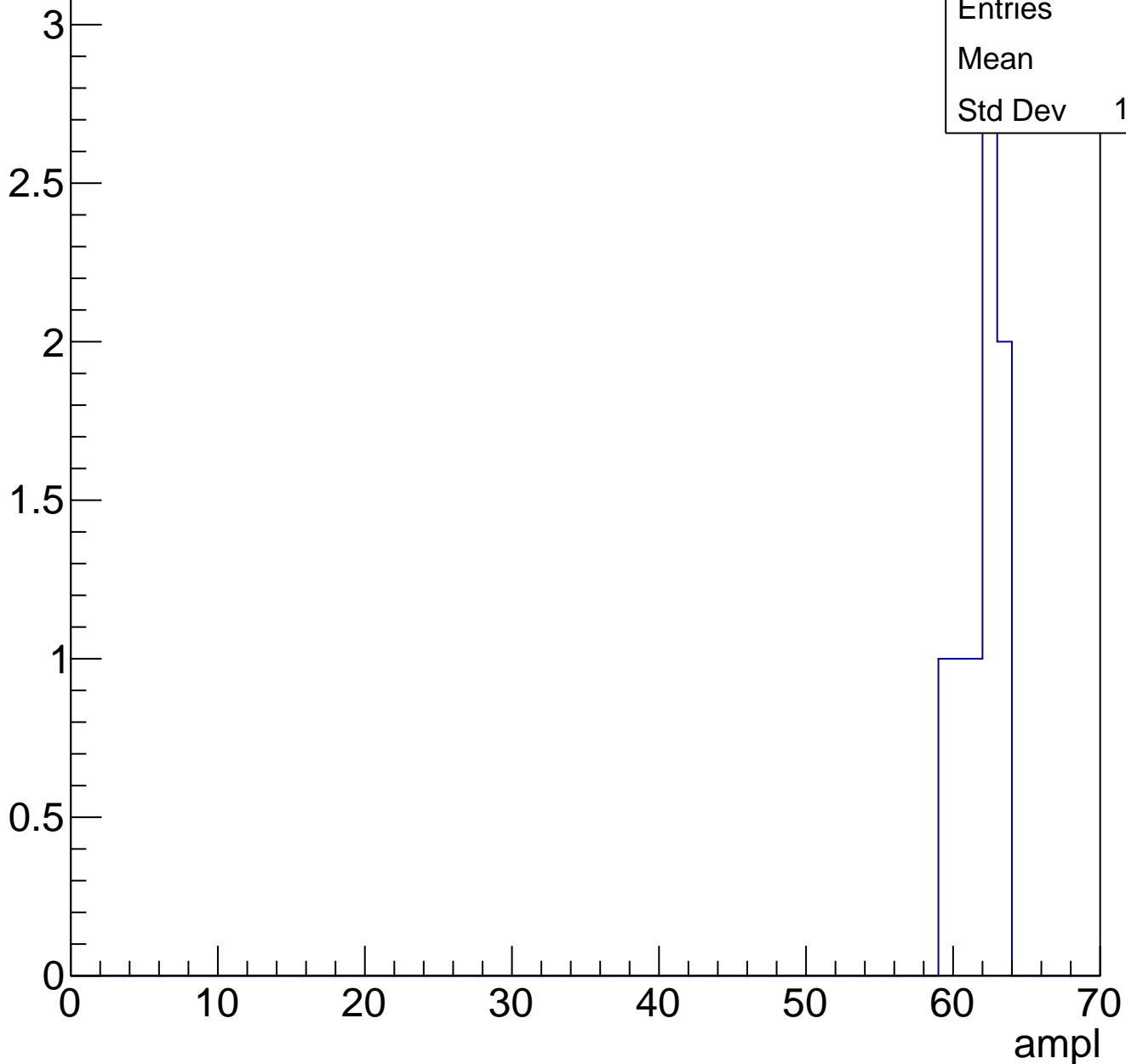
Entries	43
Mean	58.33
Std Dev	9.308



# B0L002S, U23-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch99, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	100
Mean	30.48
Std Dev	5.999

**Gaus mean : 32.0939**

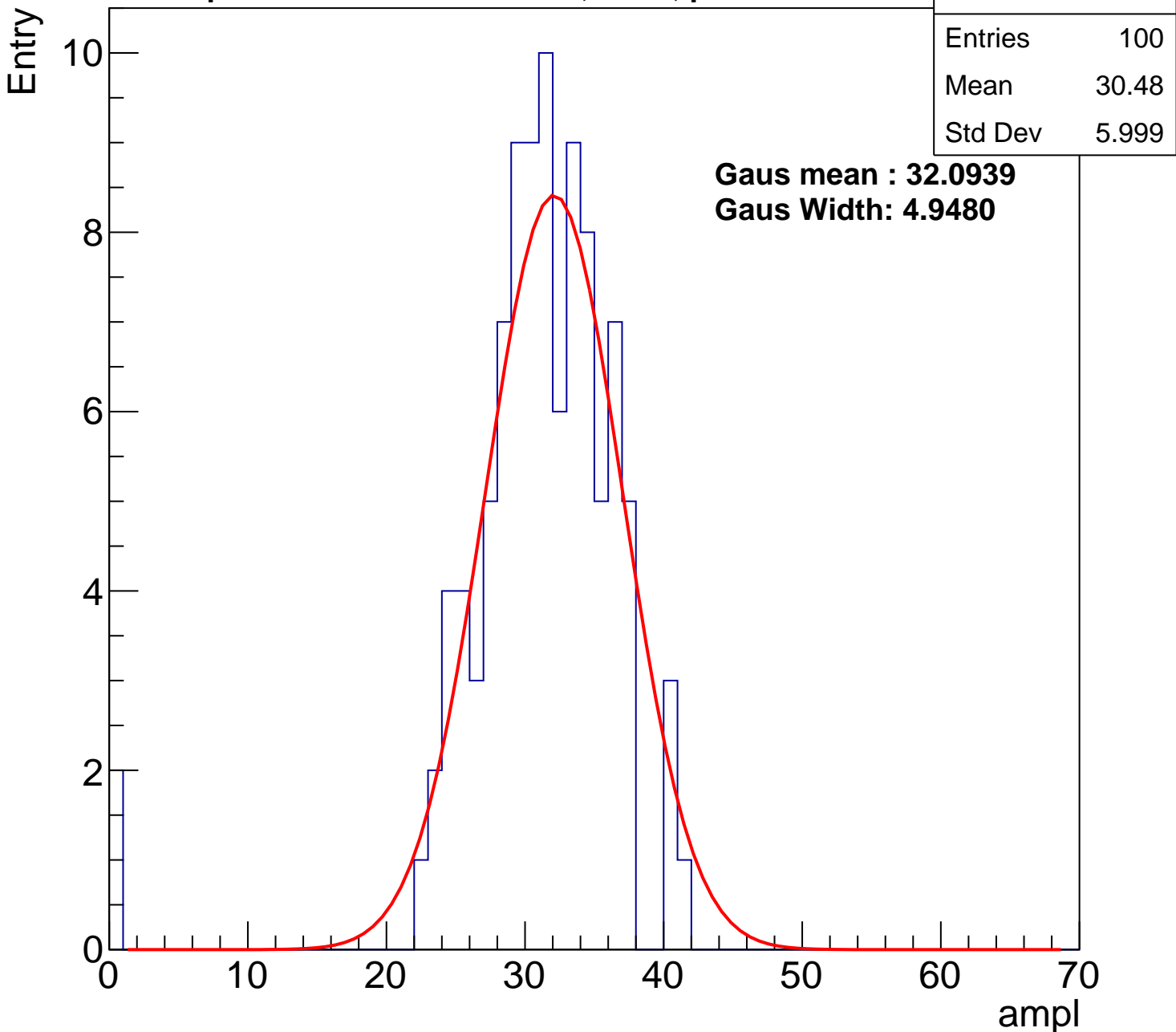
**Gaus Width: 4.9480**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch99, adc1

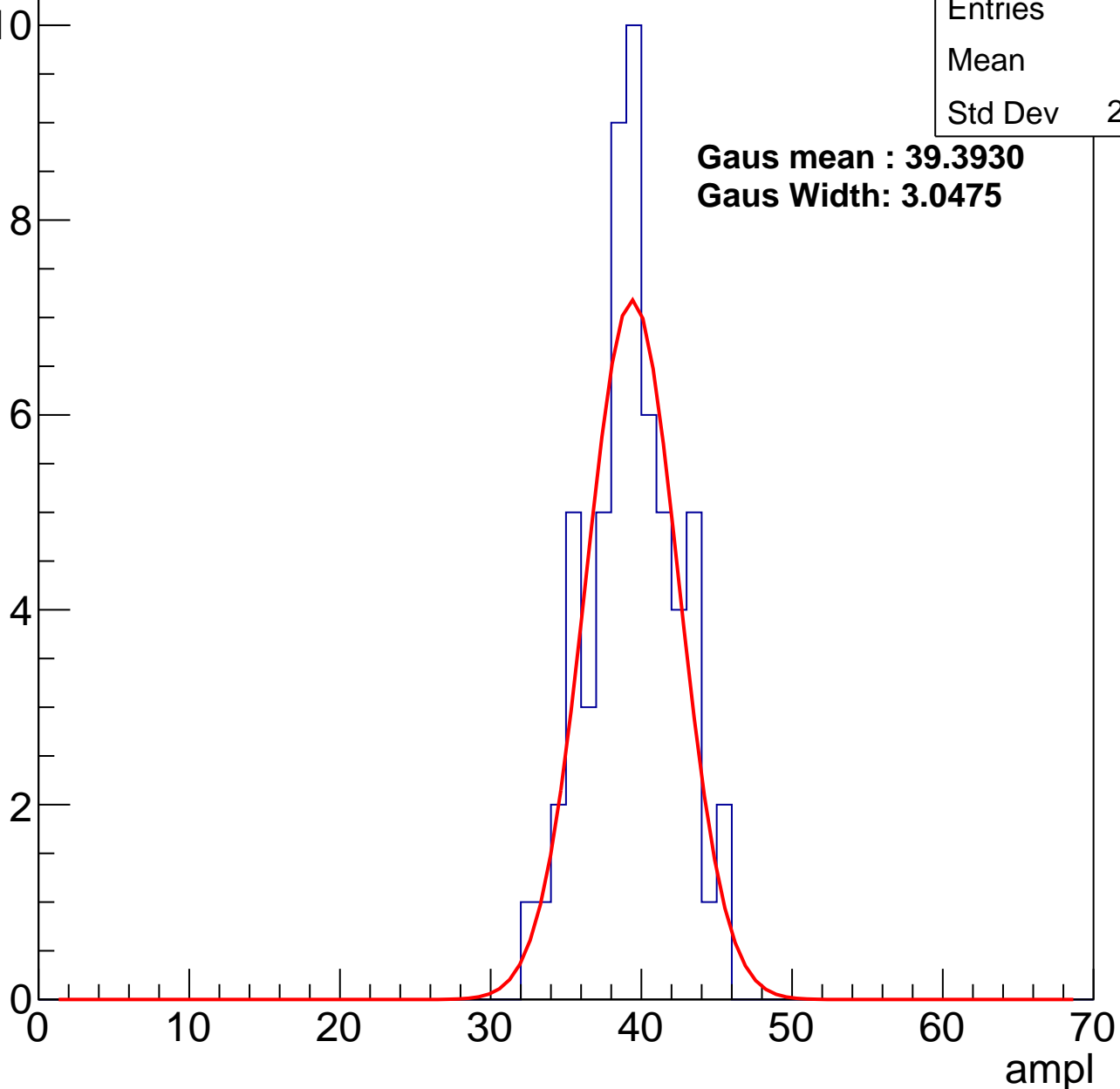
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38.9
Std Dev	2.938

**Gaus mean : 39.3930**

**Gaus Width: 3.0475**



# B0L002S, U23-ch99, adc2

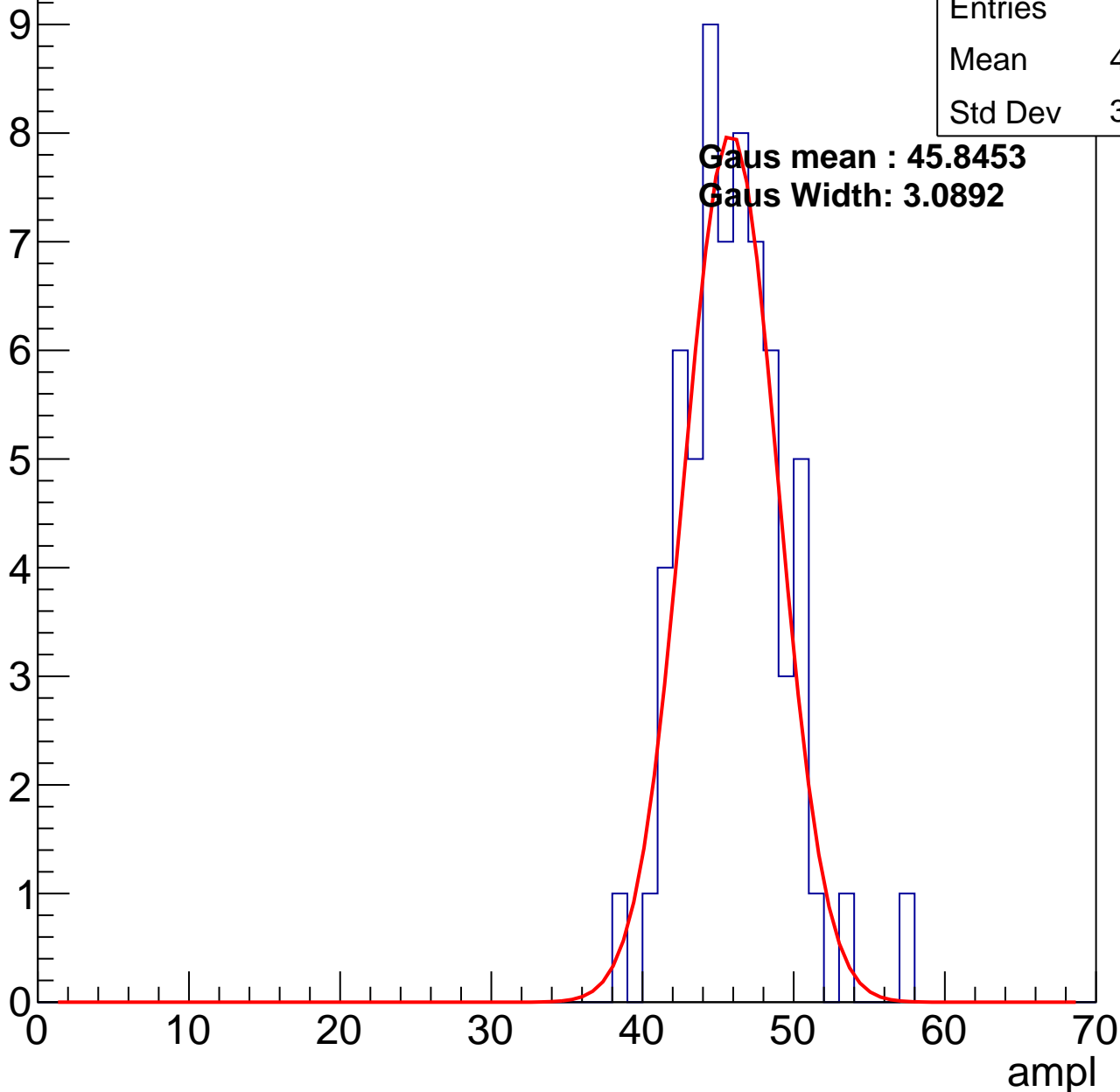
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	45.58
Std Dev	3.295

**Gaus mean : 45.8453**

**Gaus Width: 3.0892**



# B0L002S, U23-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

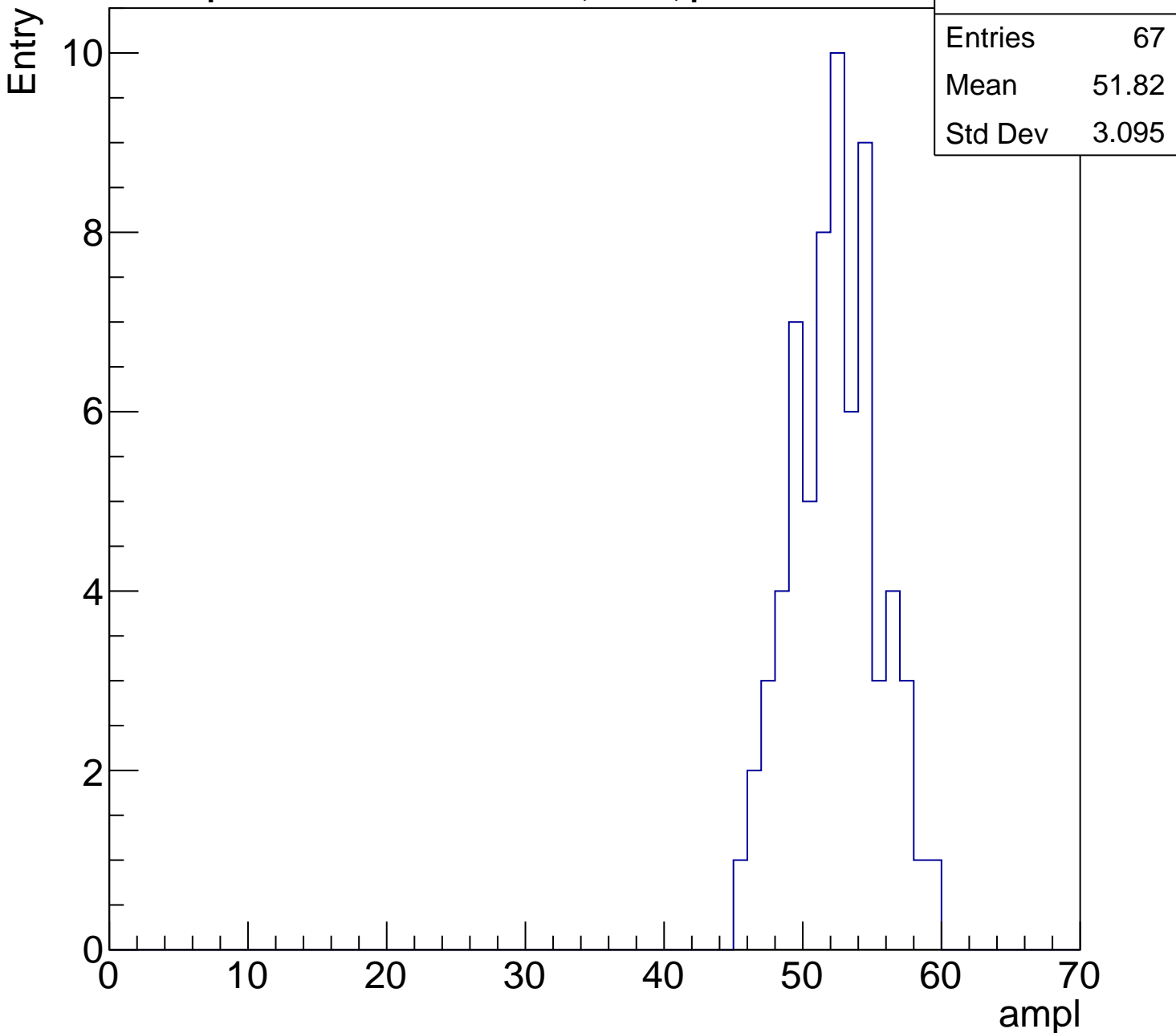
Entries	67
Mean	51.82
Std Dev	3.095

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

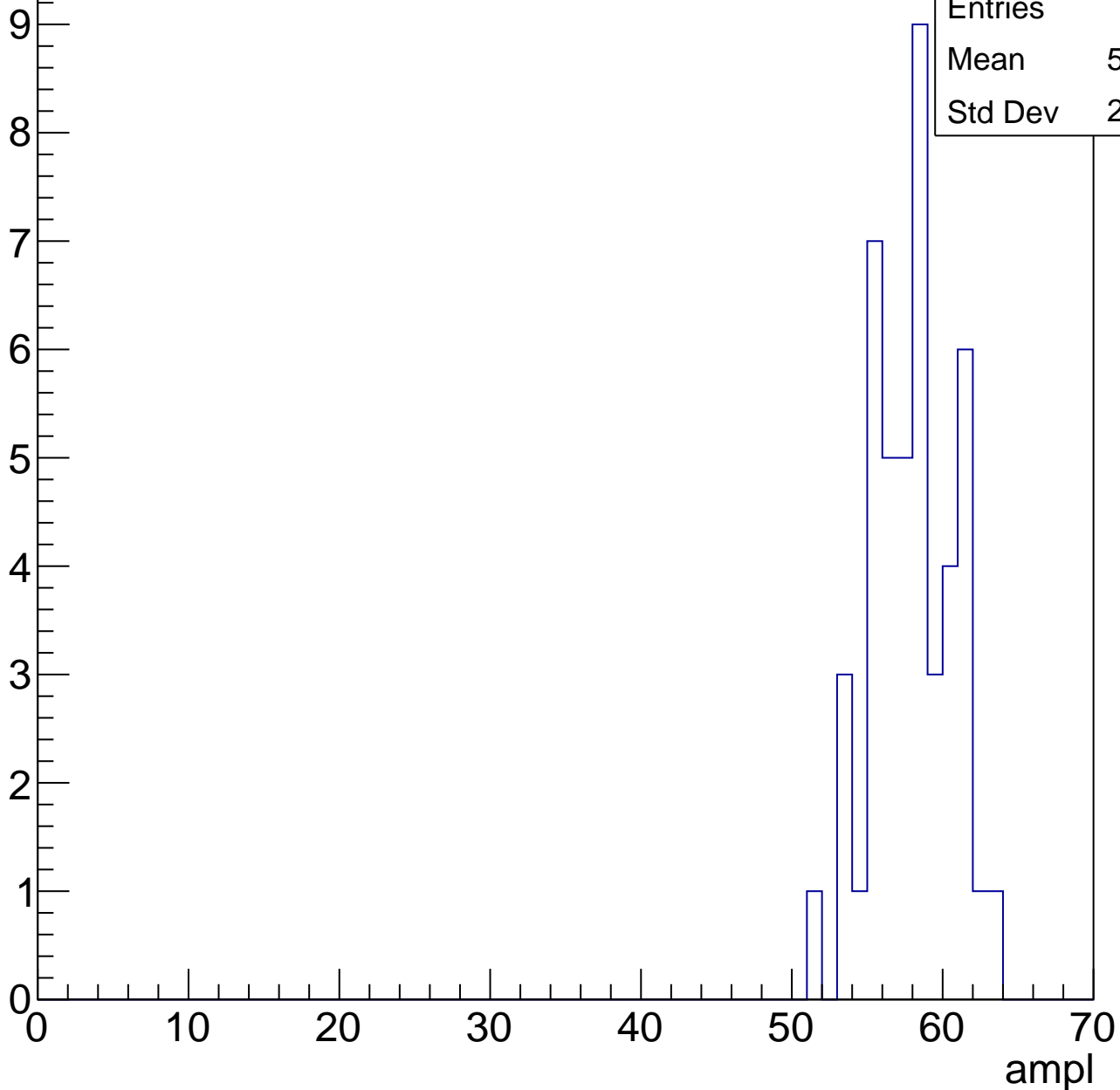


# B0L002S, U23-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	46
Mean	57.48
Std Dev	2.676

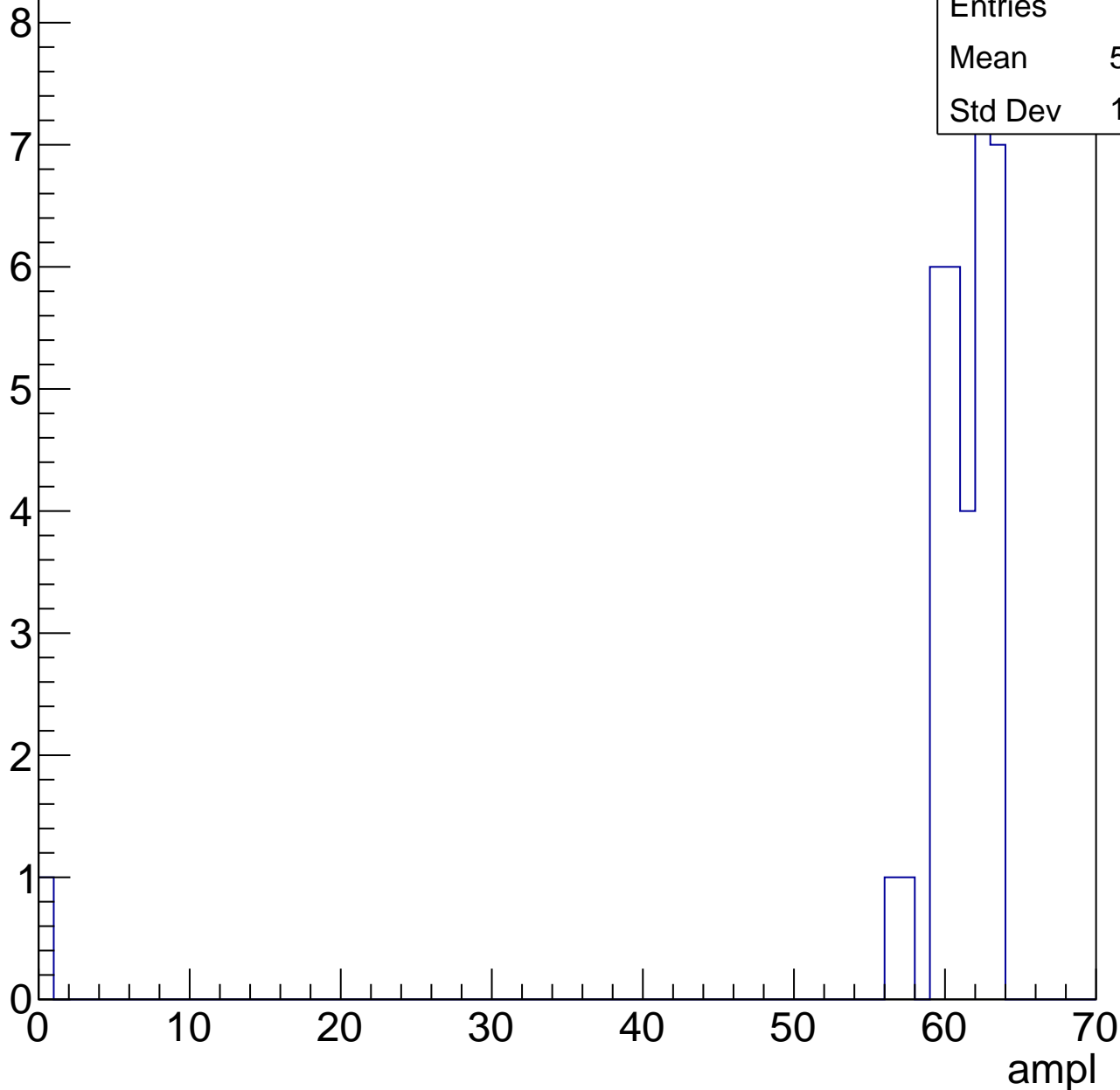


# B0L002S, U23-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.06
Std Dev	10.43



# B0L002S, U23-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

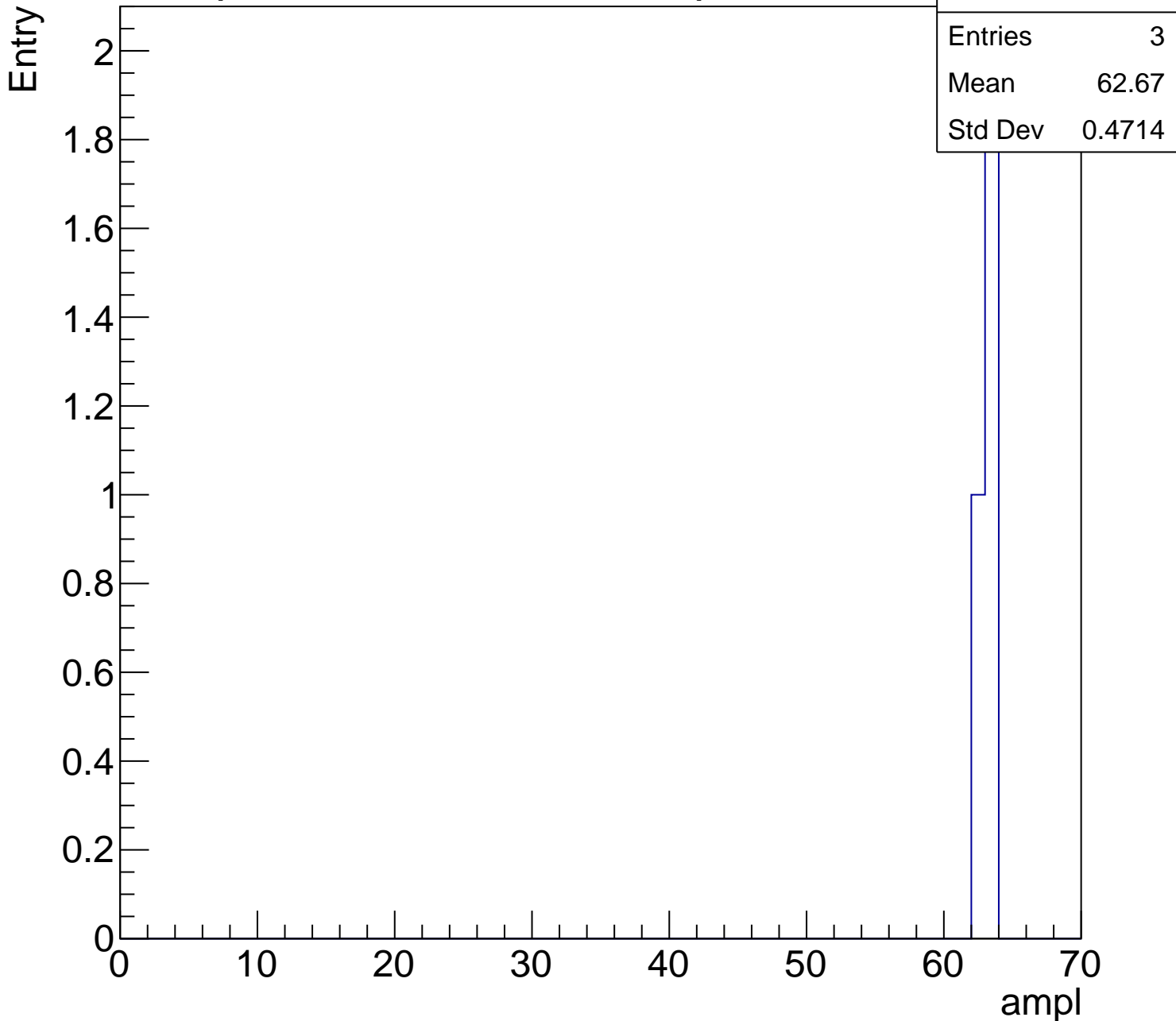
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70





# B0L002S, U23-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch100, adc0

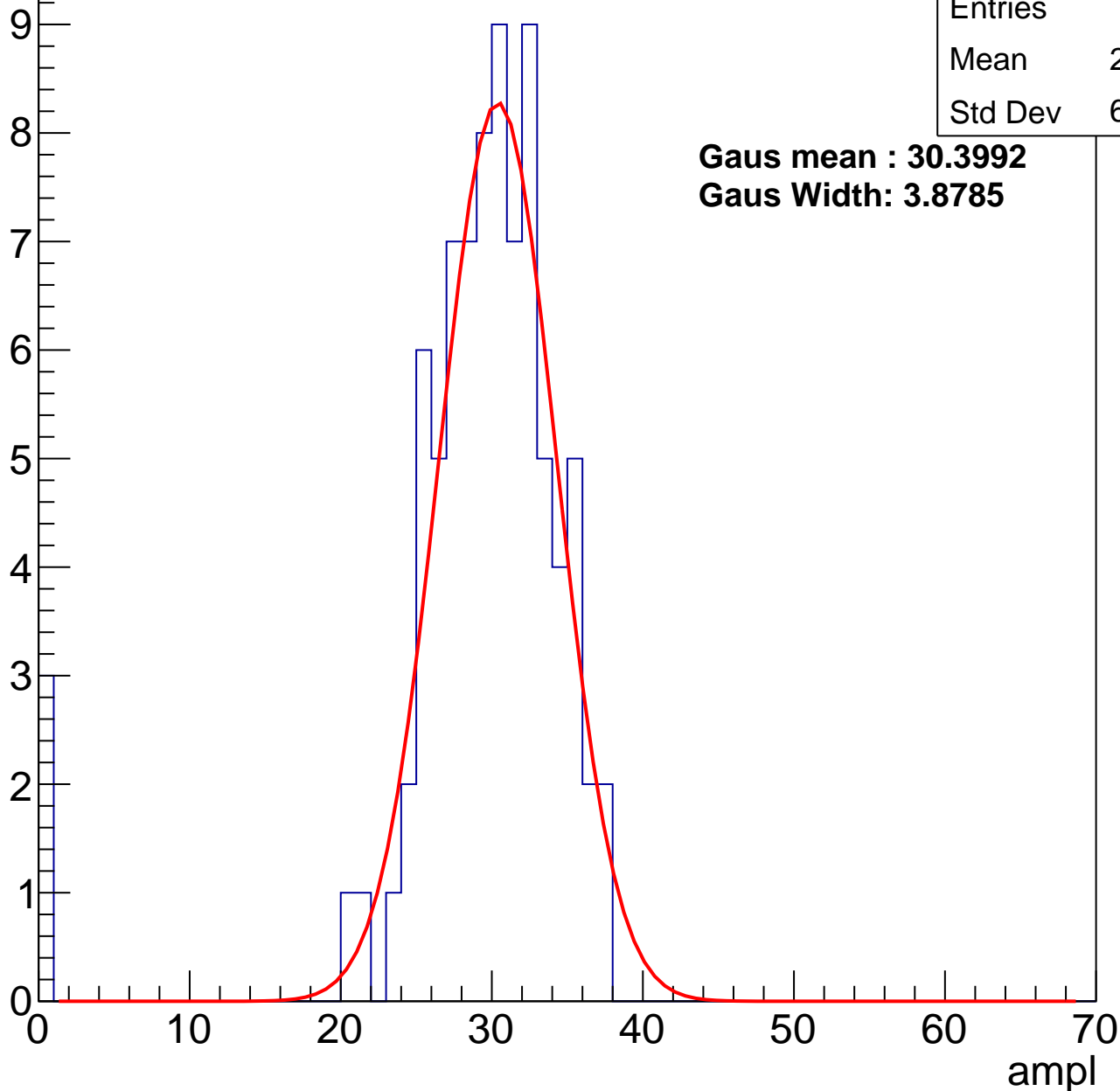
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	84
Mean	28.64
Std Dev	6.568

**Gaus mean : 30.3992**

**Gaus Width: 3.8785**



# B0L002S, U23-ch100, adc1

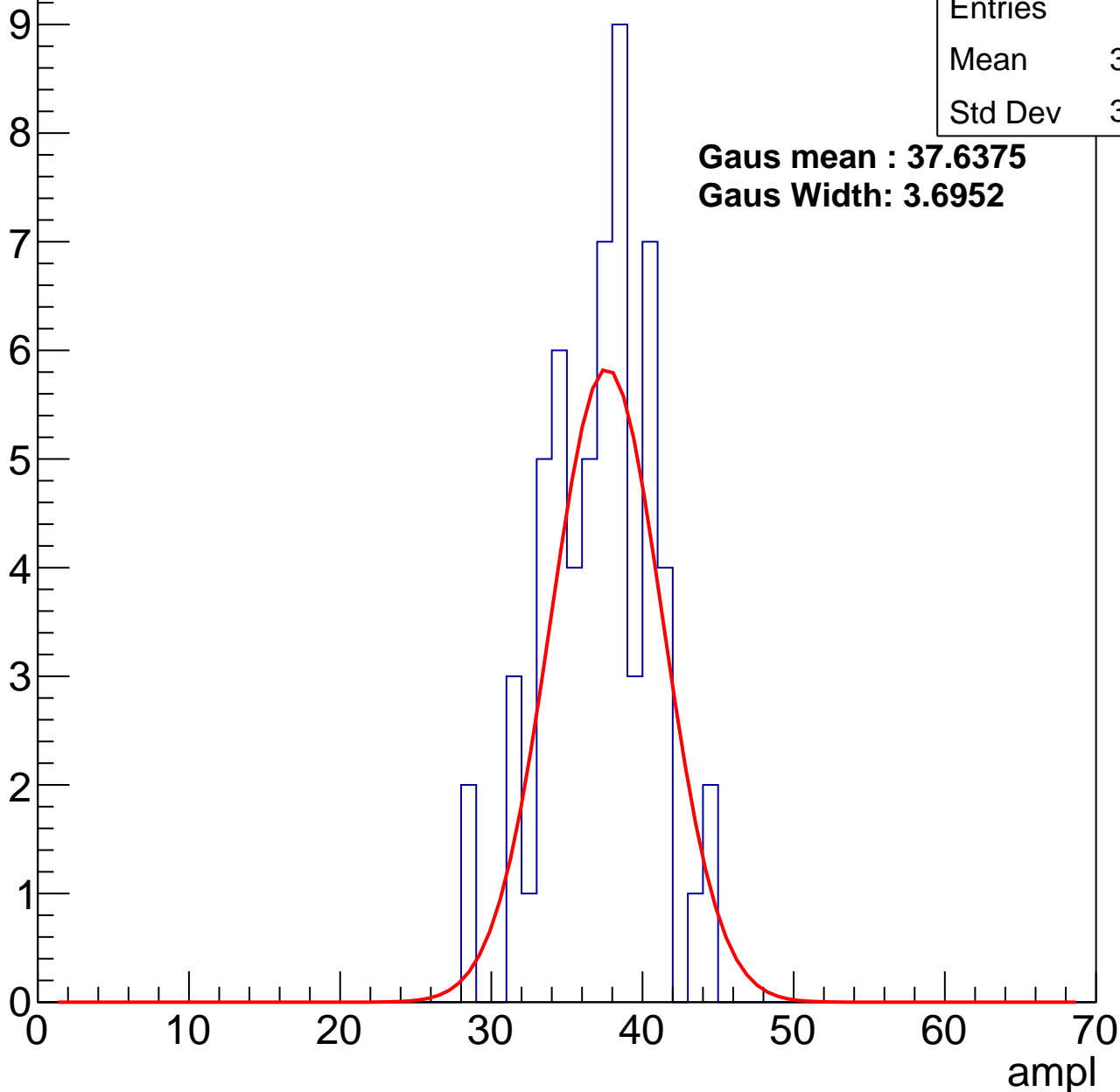
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.66
Std Dev	3.506

**Gaus mean : 37.6375**

**Gaus Width: 3.6952**



# B0L002S, U23-ch100, adc2

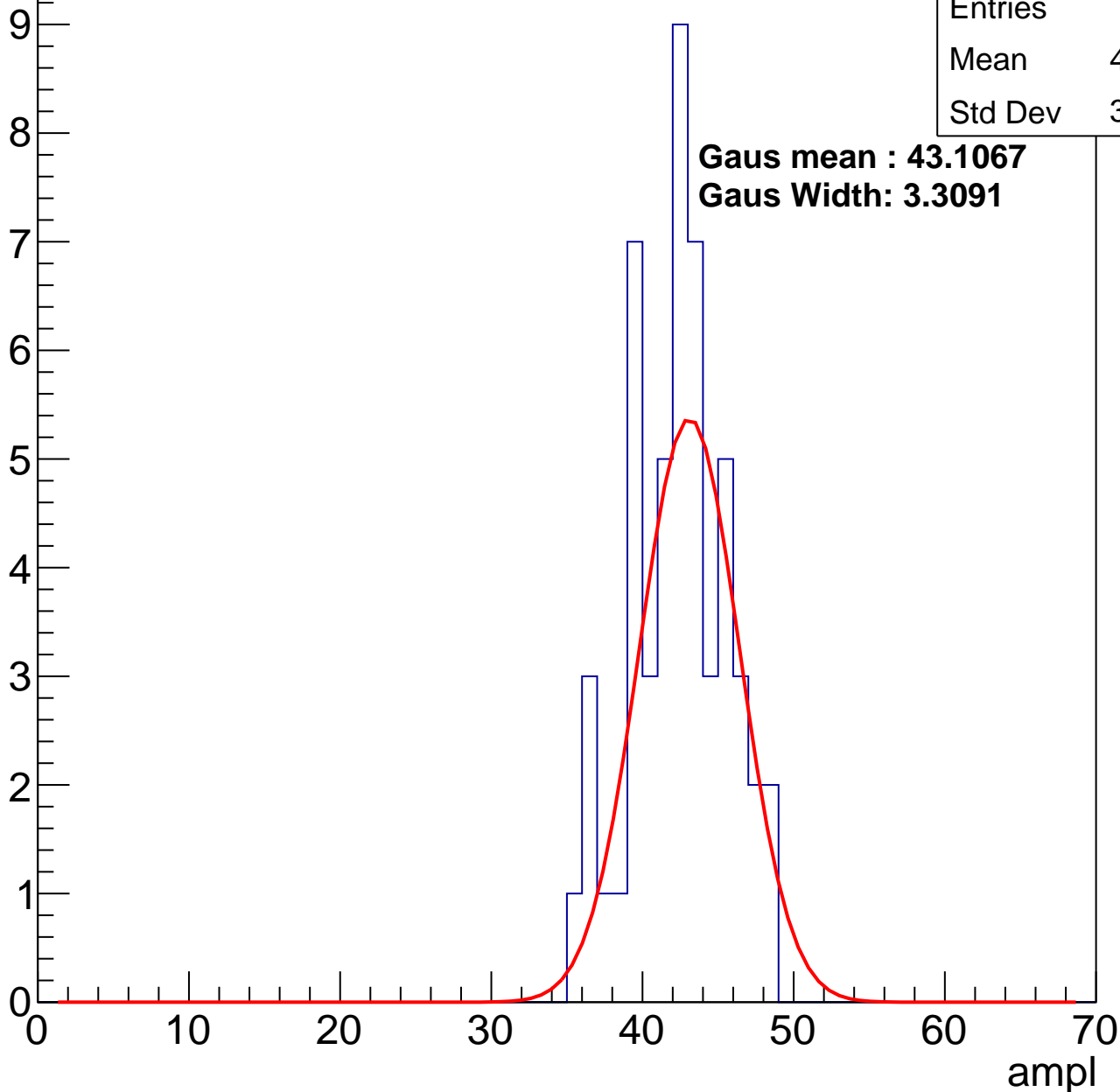
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	41.92
Std Dev	3.137

**Gaus mean : 43.1067**

**Gaus Width: 3.3091**



# B0L002S, U23-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	77
Mean	48.62
Std Dev	3.26

Entry

10

8

6

4

2

0

0

10

20

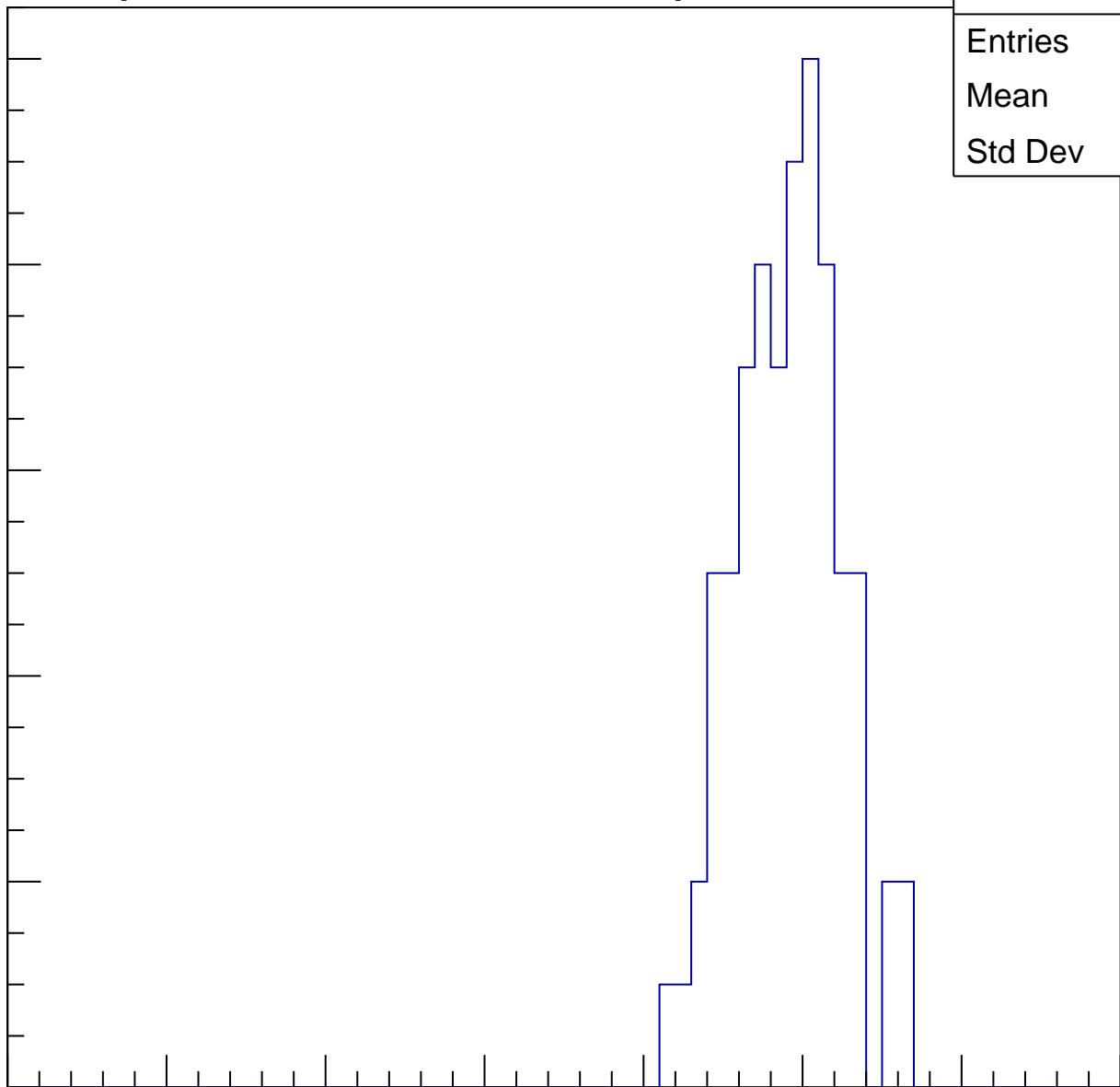
30

40

50

60

ampl

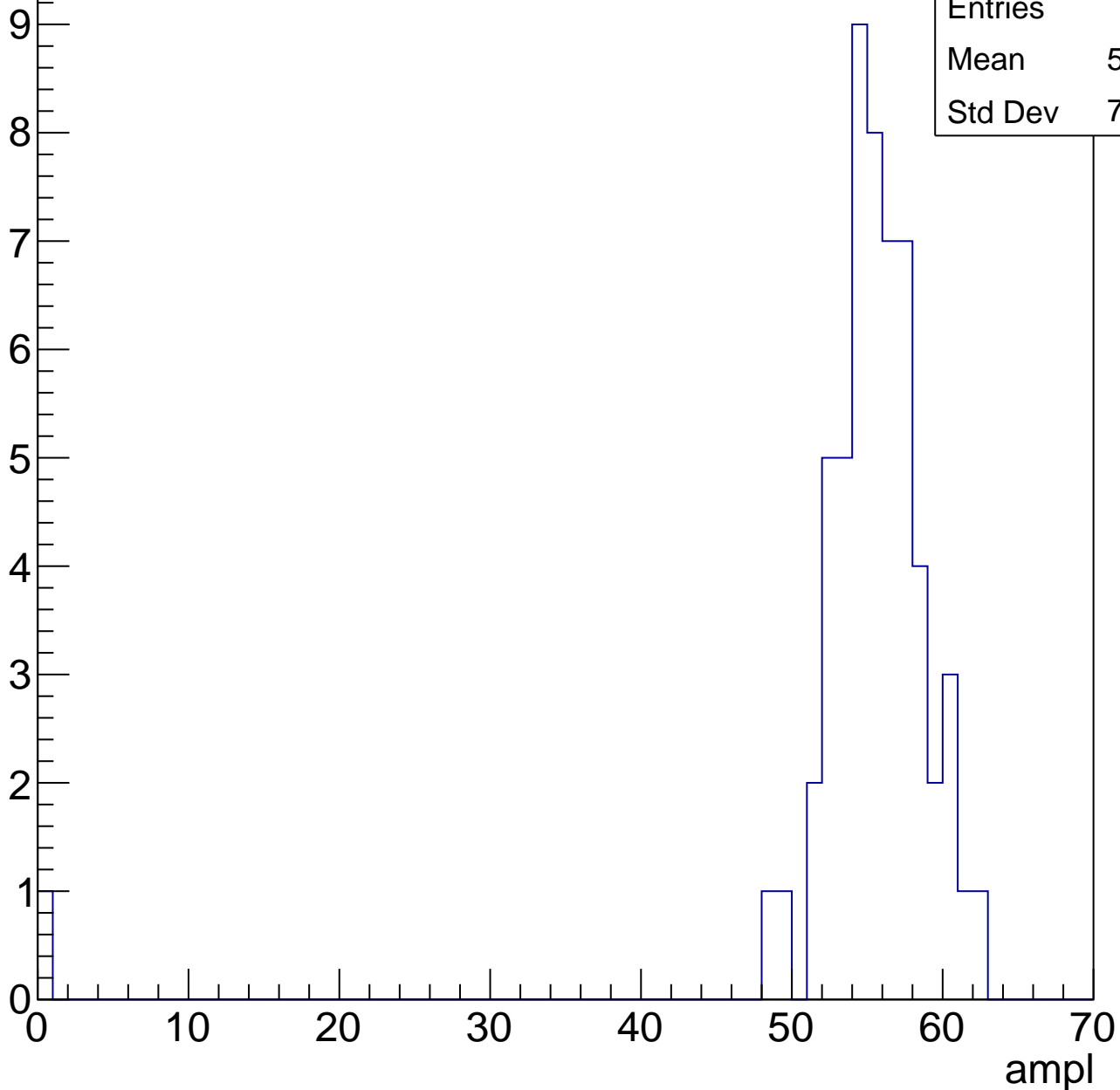


# B0L002S, U23-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	54.28
Std Dev	7.779

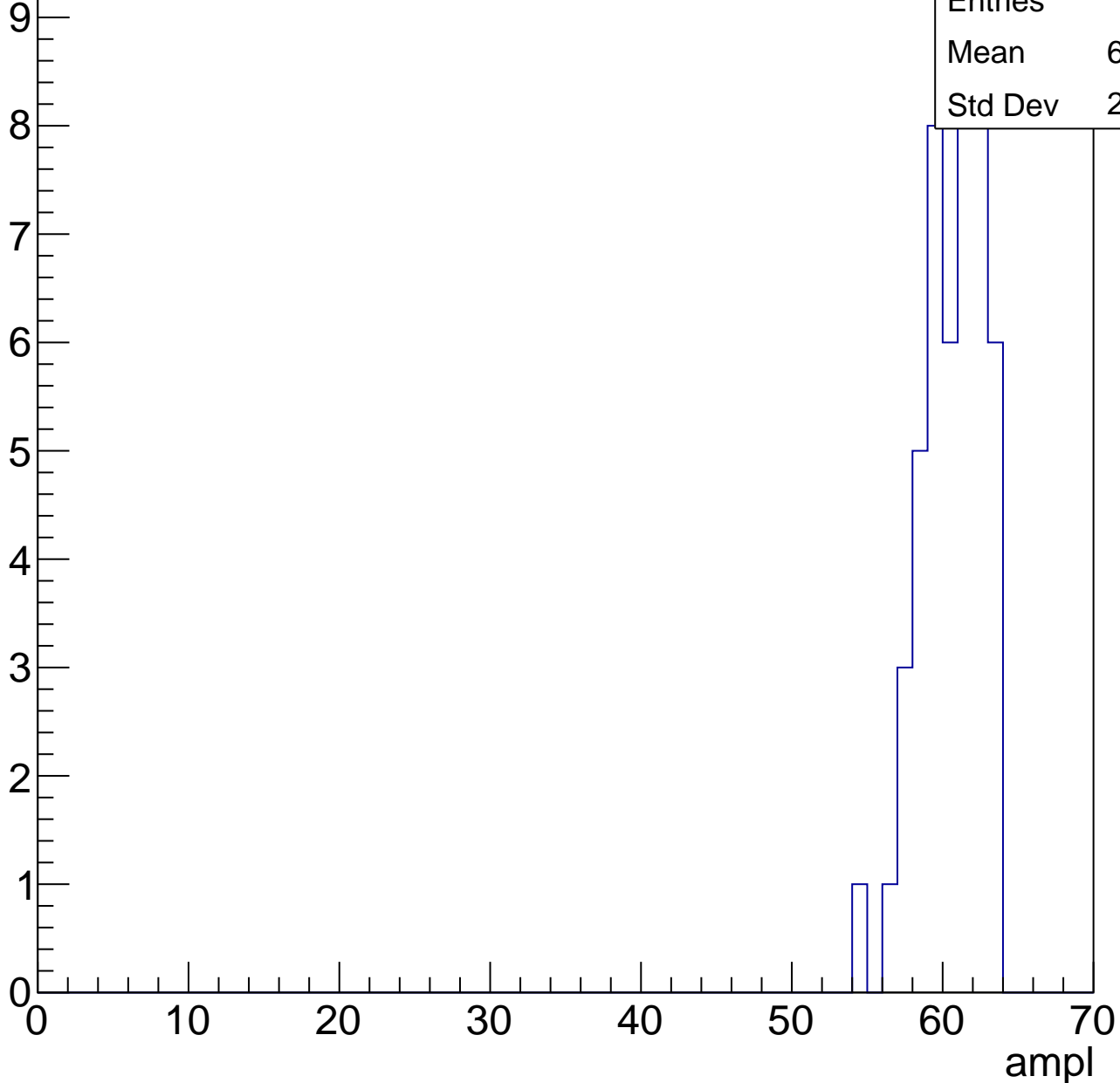


# B0L002S, U23-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

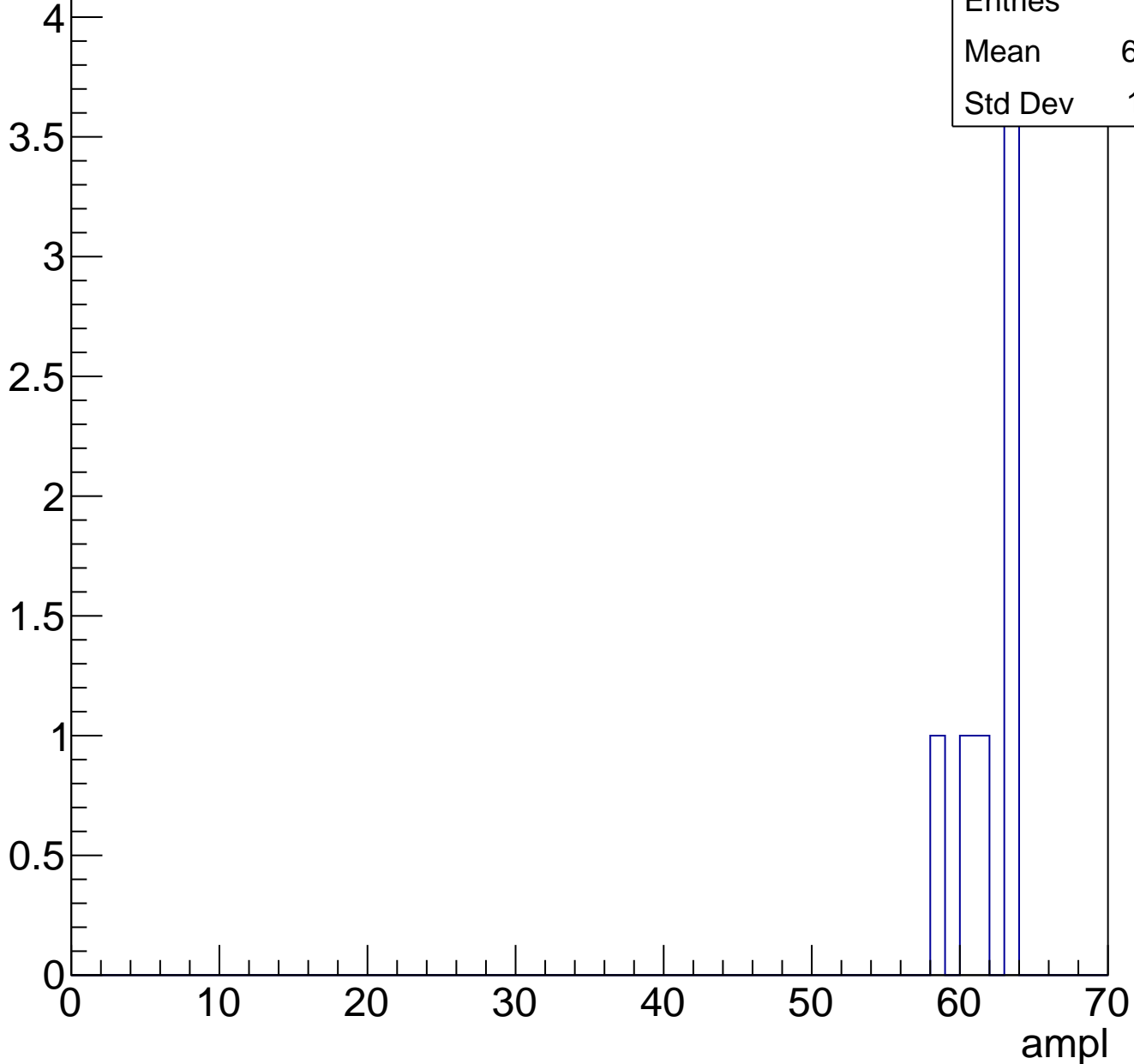
Entries	47
Mean	60.15
Std Dev	2.083



# B0L002S, U23-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	7
Mean	61.57
Std Dev	1.841



# B0L002S, U23-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch101, adc0

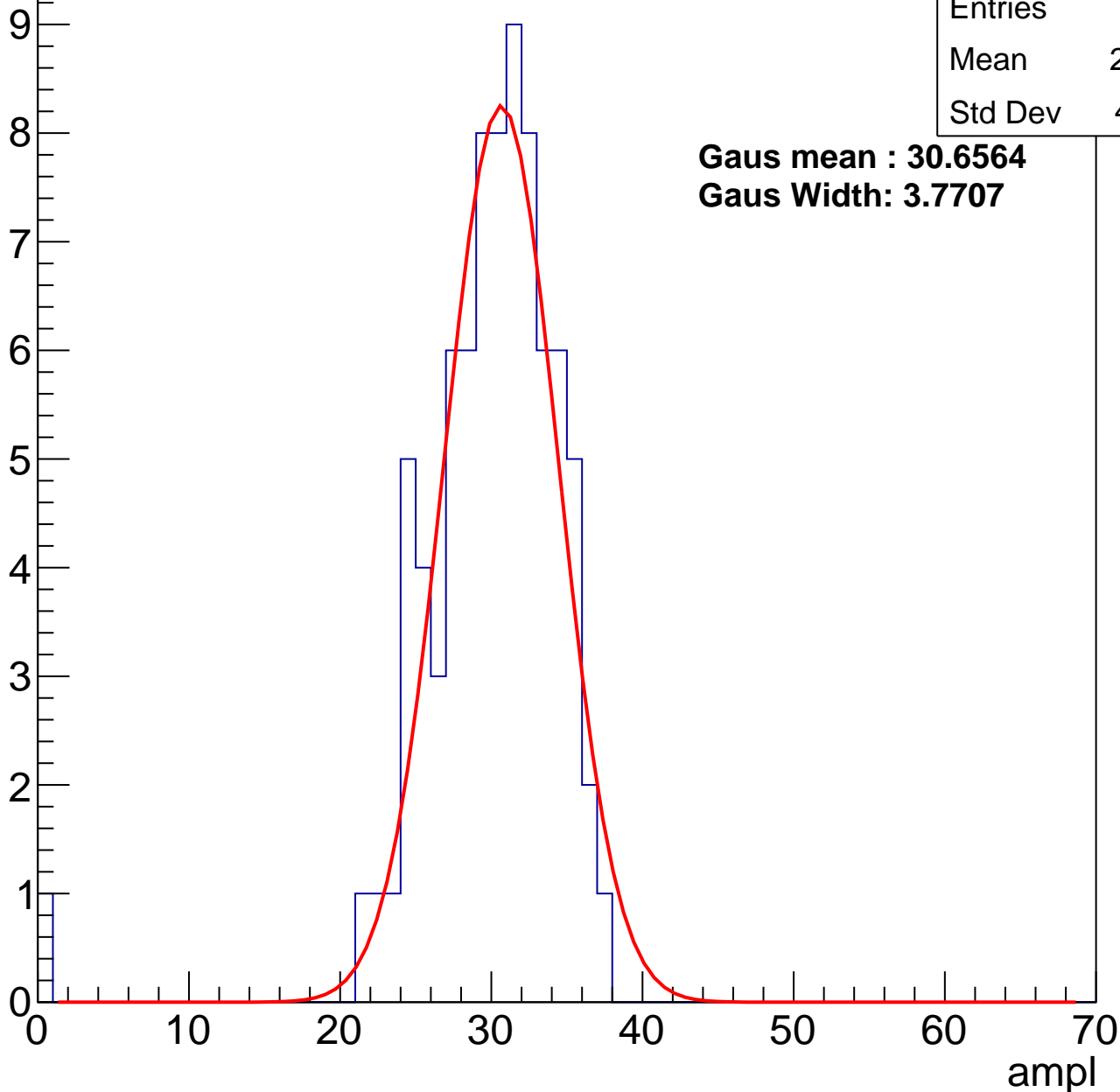
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	29.47
Std Dev	4.871

**Gaus mean : 30.6564**

**Gaus Width: 3.7707**



# B0L002S, U23-ch101, adc1

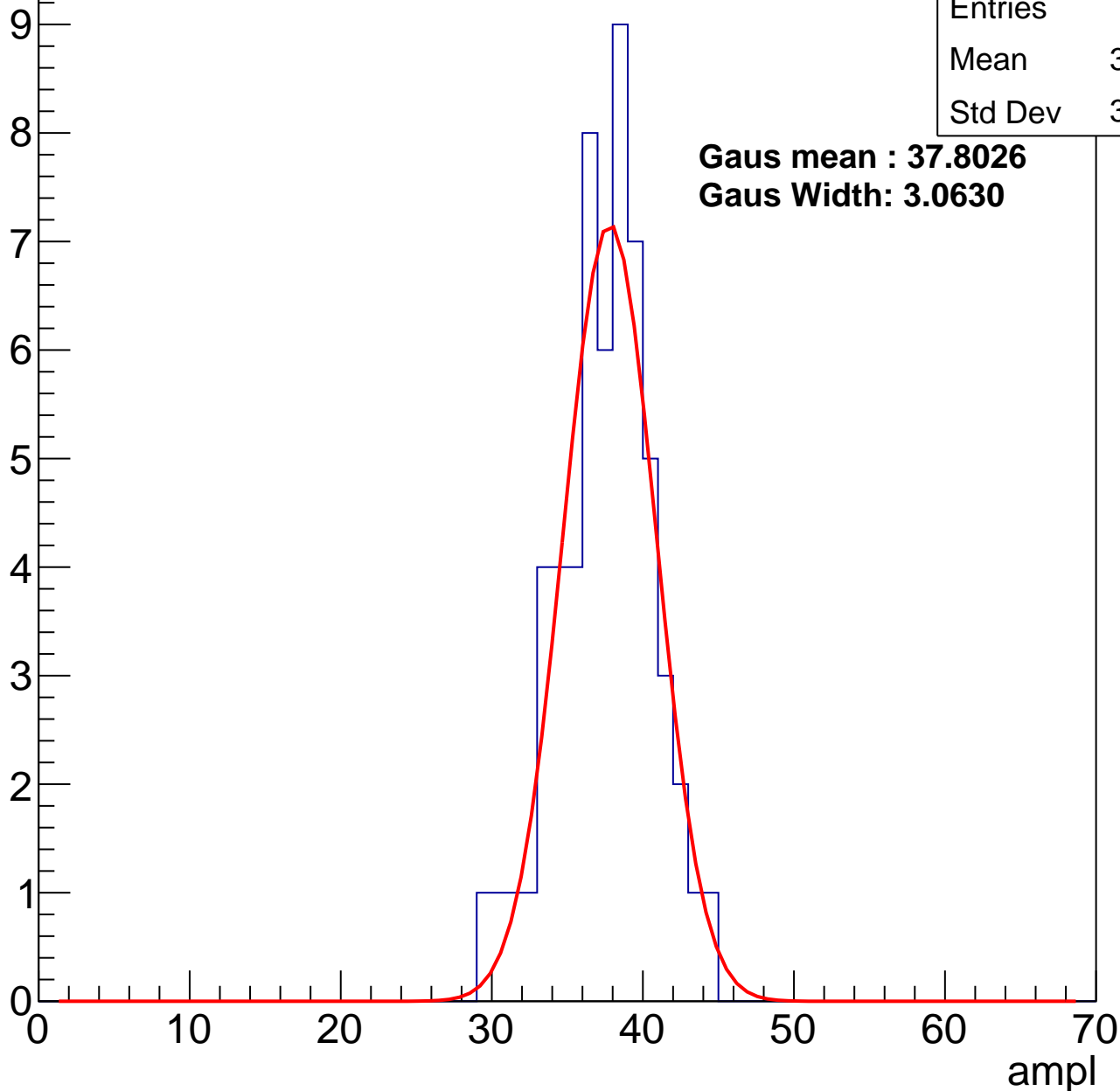
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.05
Std Dev	3.115

**Gaus mean : 37.8026**

**Gaus Width: 3.0630**



# B0L002S, U23-ch101, adc2

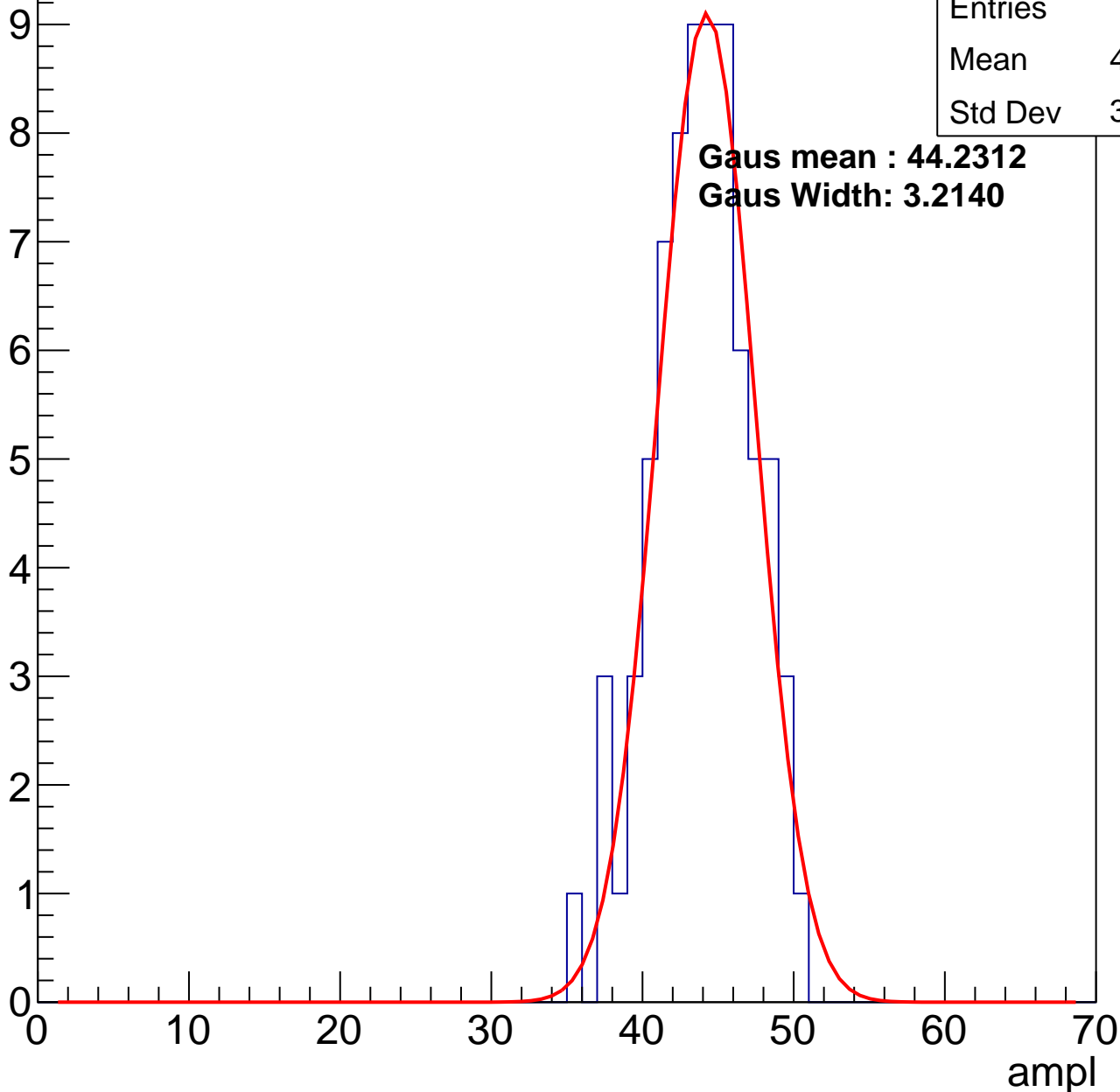
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	43.47
Std Dev	3.206

**Gaus mean : 44.2312**

**Gaus Width: 3.2140**

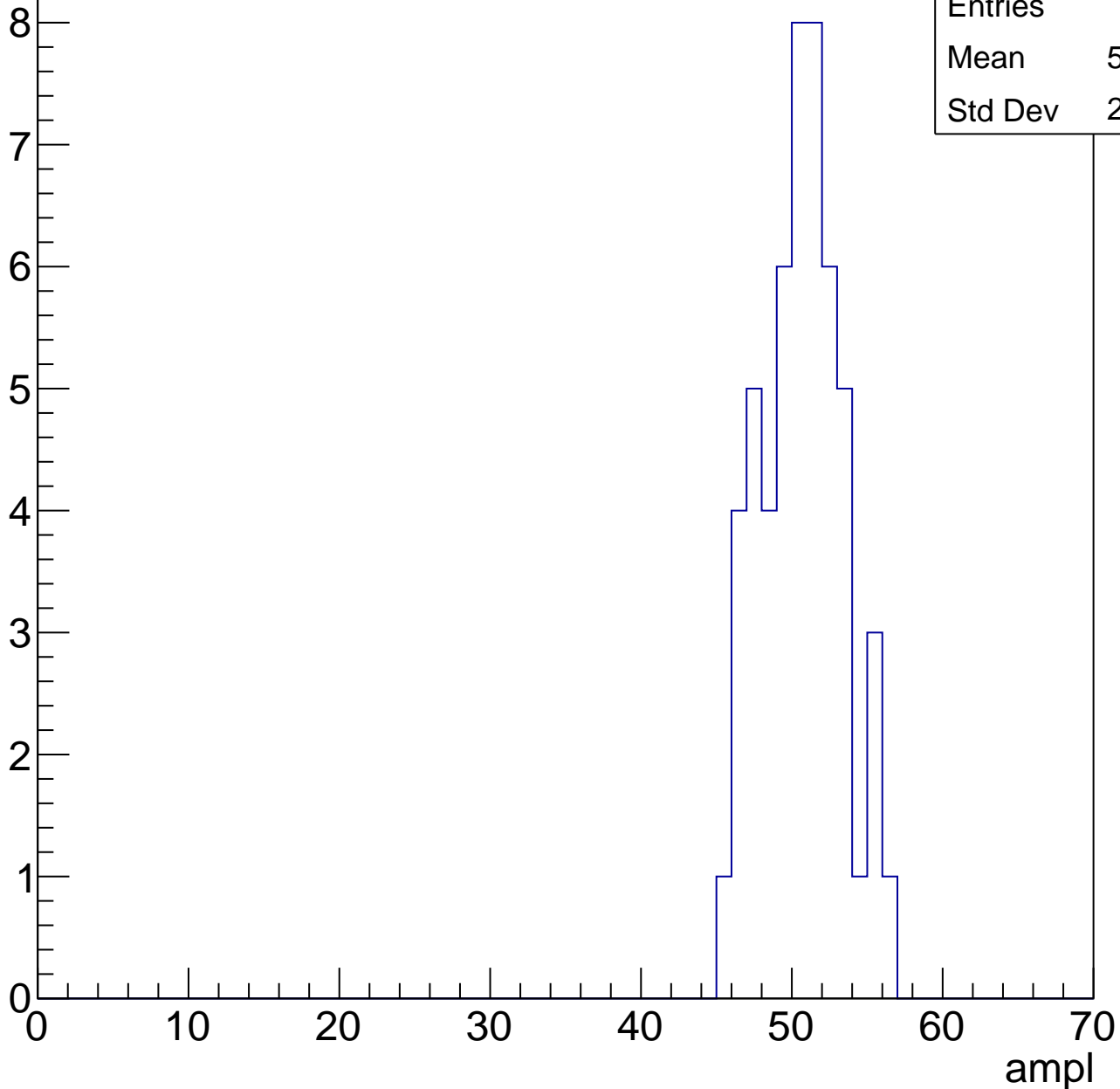


# B0L002S, U23-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

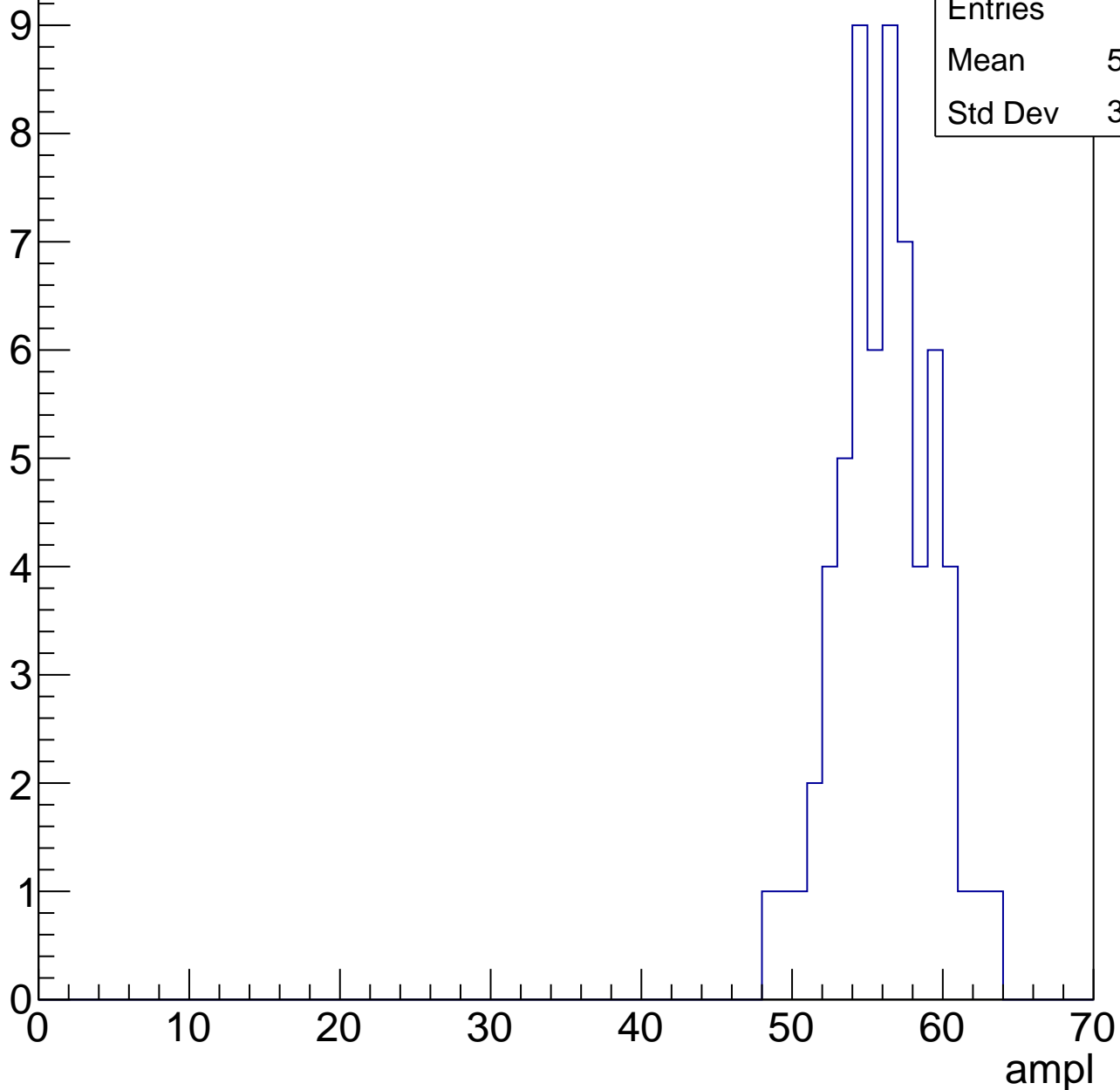
Entries	52
Mean	50.19
Std Dev	2.624



# B0L002S, U23-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



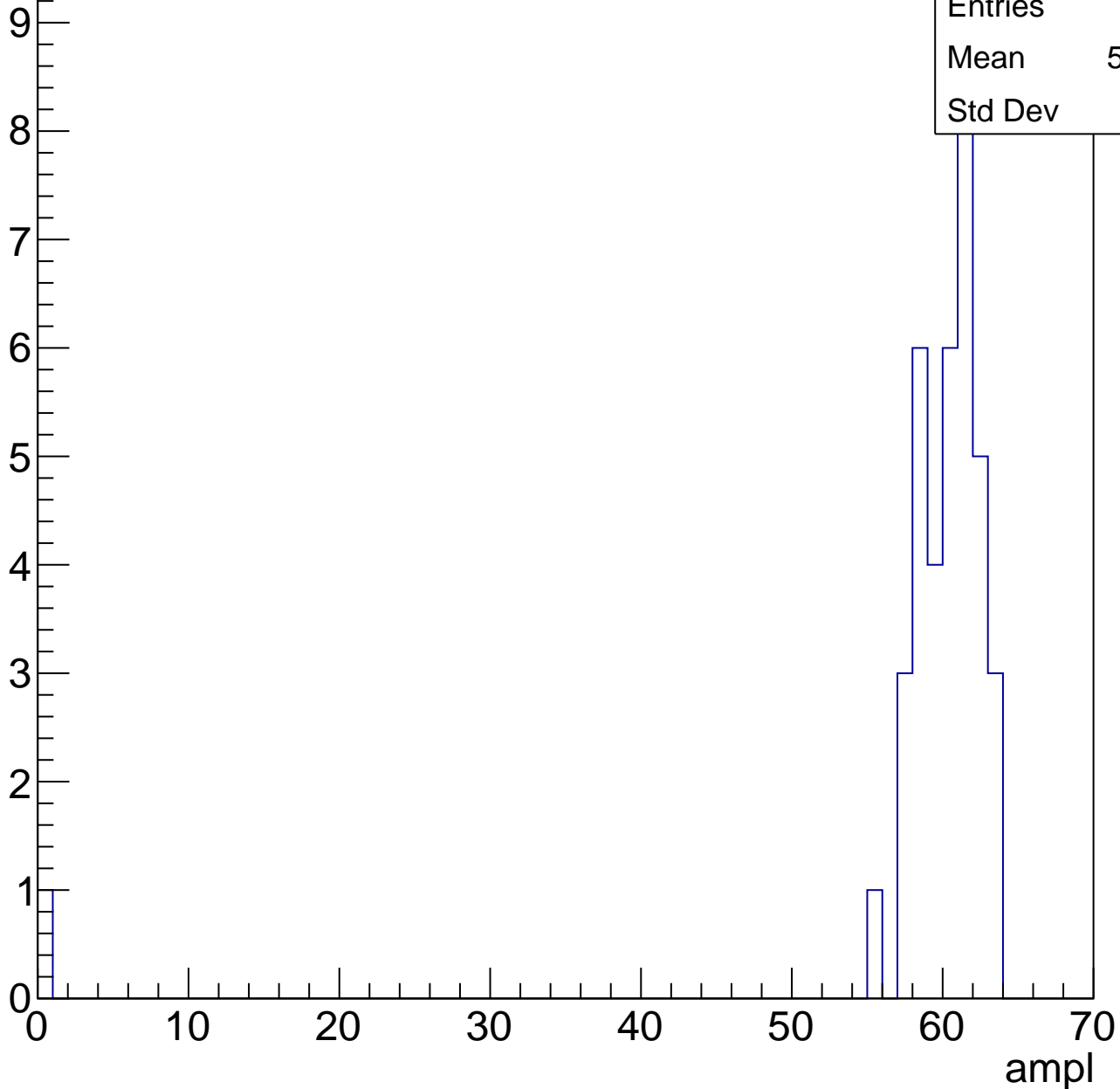
Entries	62
Mean	55.69
Std Dev	3.098

# B0L002S, U23-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.37
Std Dev	9.78

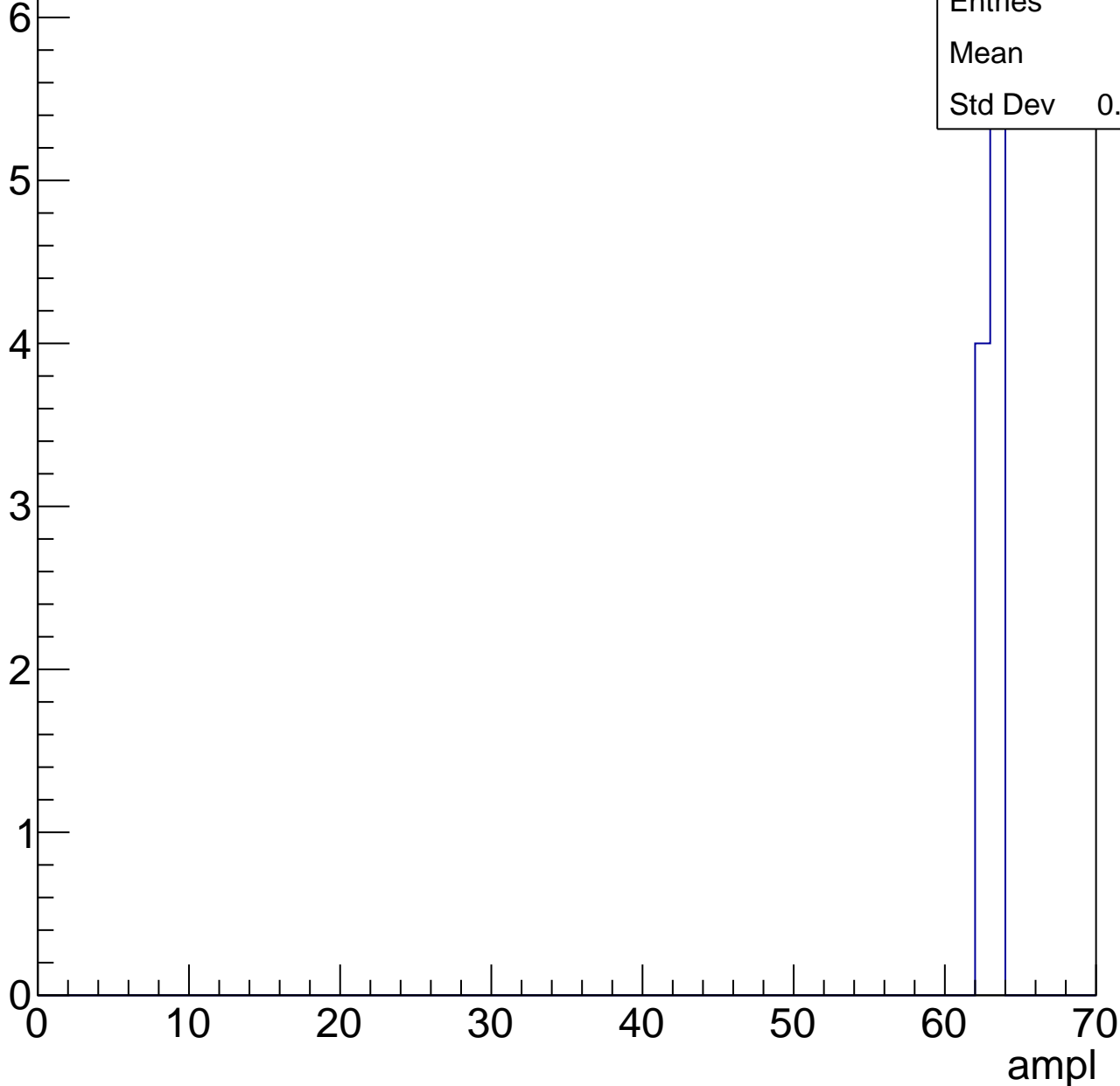


# B0L002S, U23-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	10
Mean	62.6
Std Dev	0.4899





# B0L002S, U23-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch102, adc0

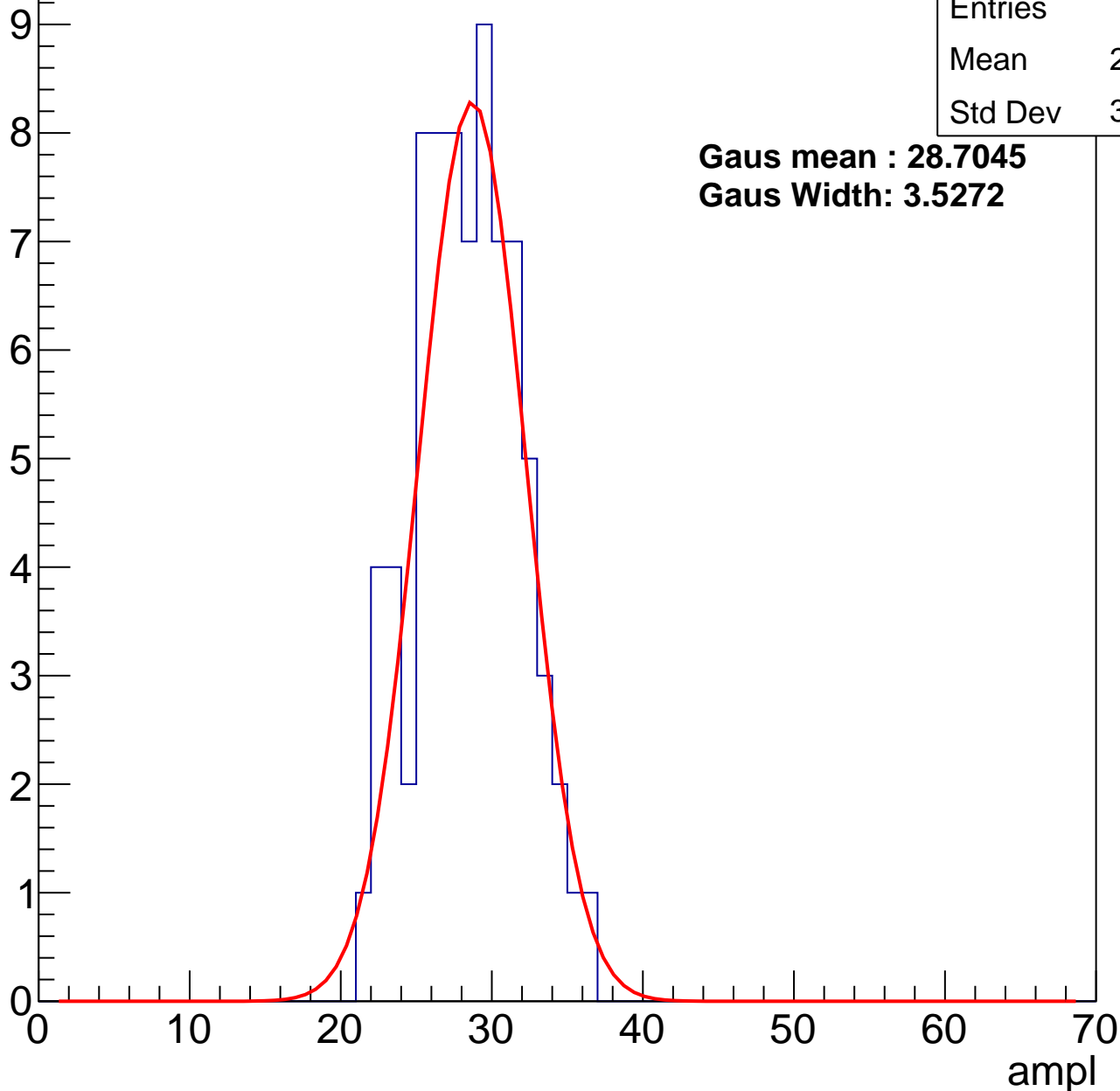
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	27.99
Std Dev	3.375

**Gaus mean : 28.7045**

**Gaus Width: 3.5272**



# B0L002S, U23-ch102, adc1

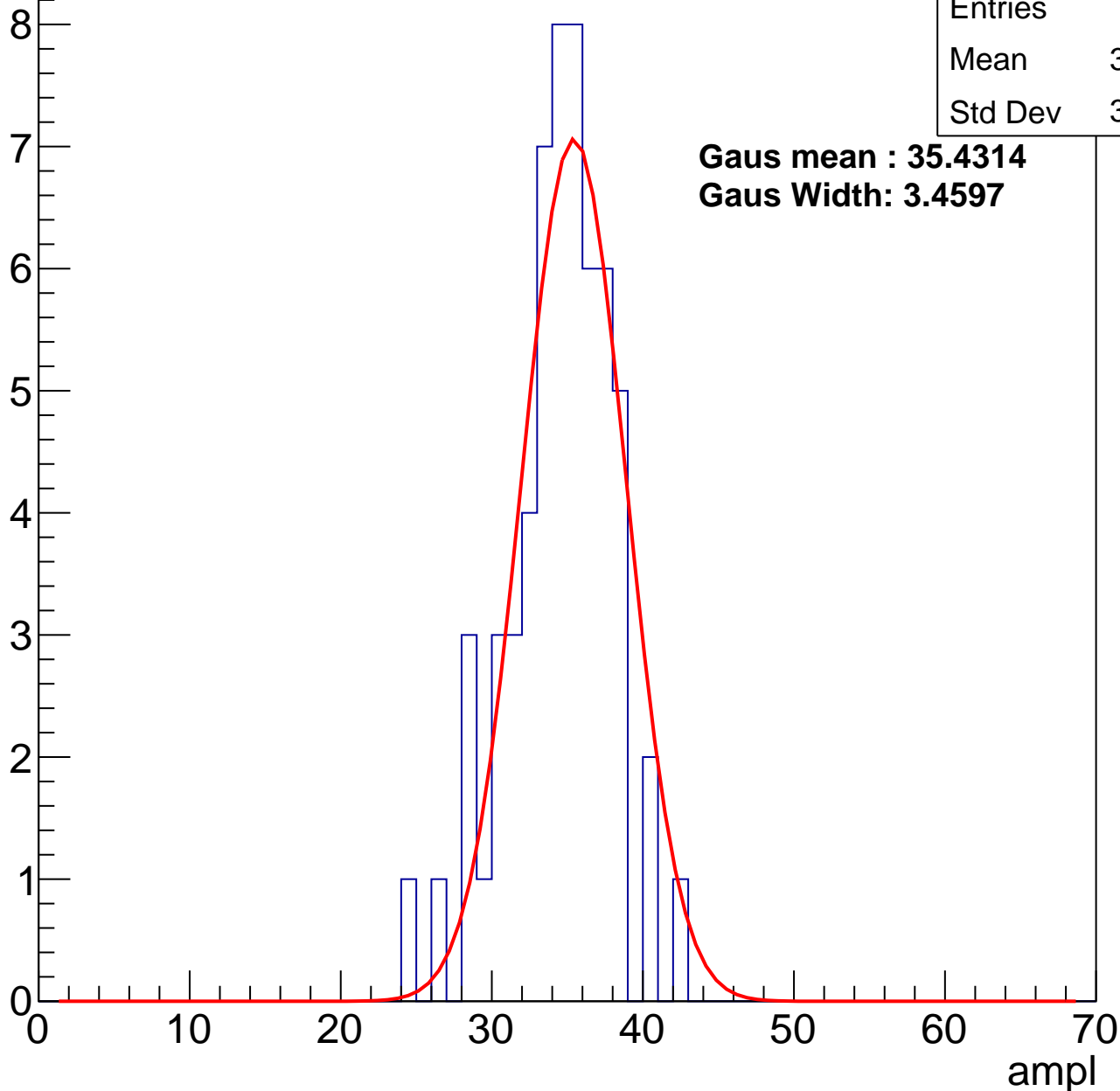
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	34.02
Std Dev	3.437

**Gaus mean : 35.4314**

**Gaus Width: 3.4597**



# B0L002S, U23-ch102, adc2

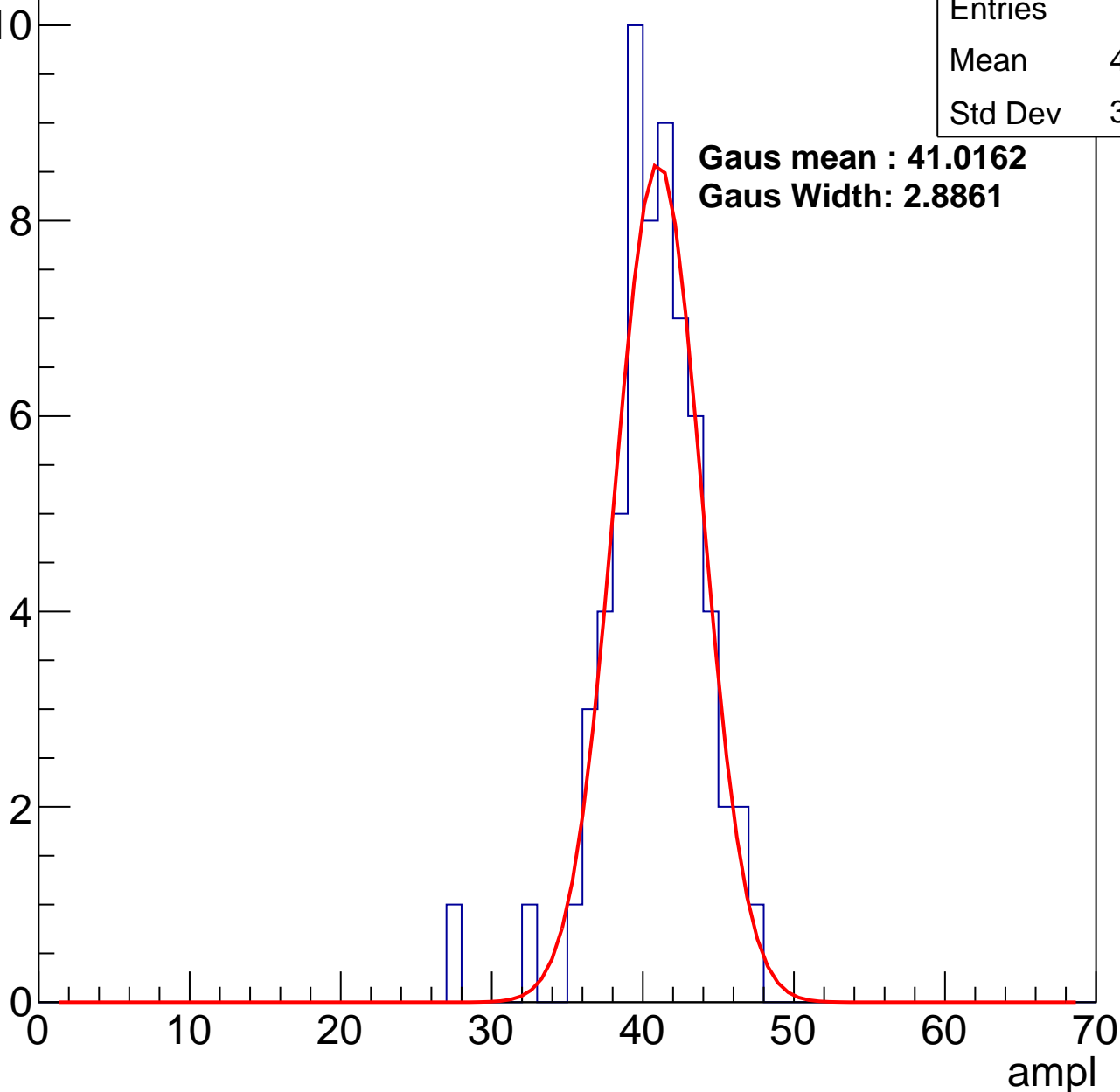
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	40.25
Std Dev	3.293

**Gaus mean : 41.0162**

**Gaus Width: 2.8861**

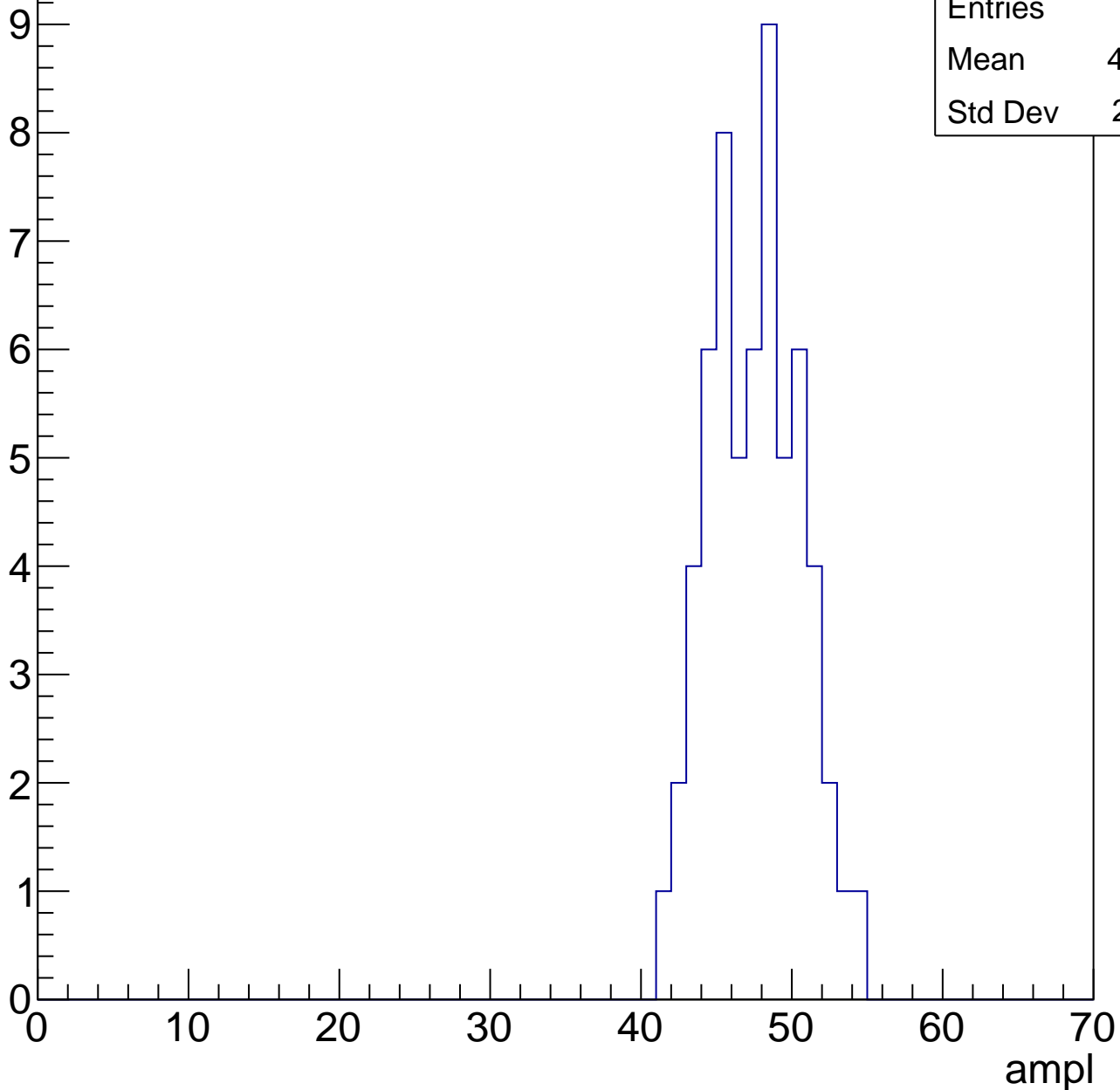


# B0L002S, U23-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	47.08
Std Dev	2.951

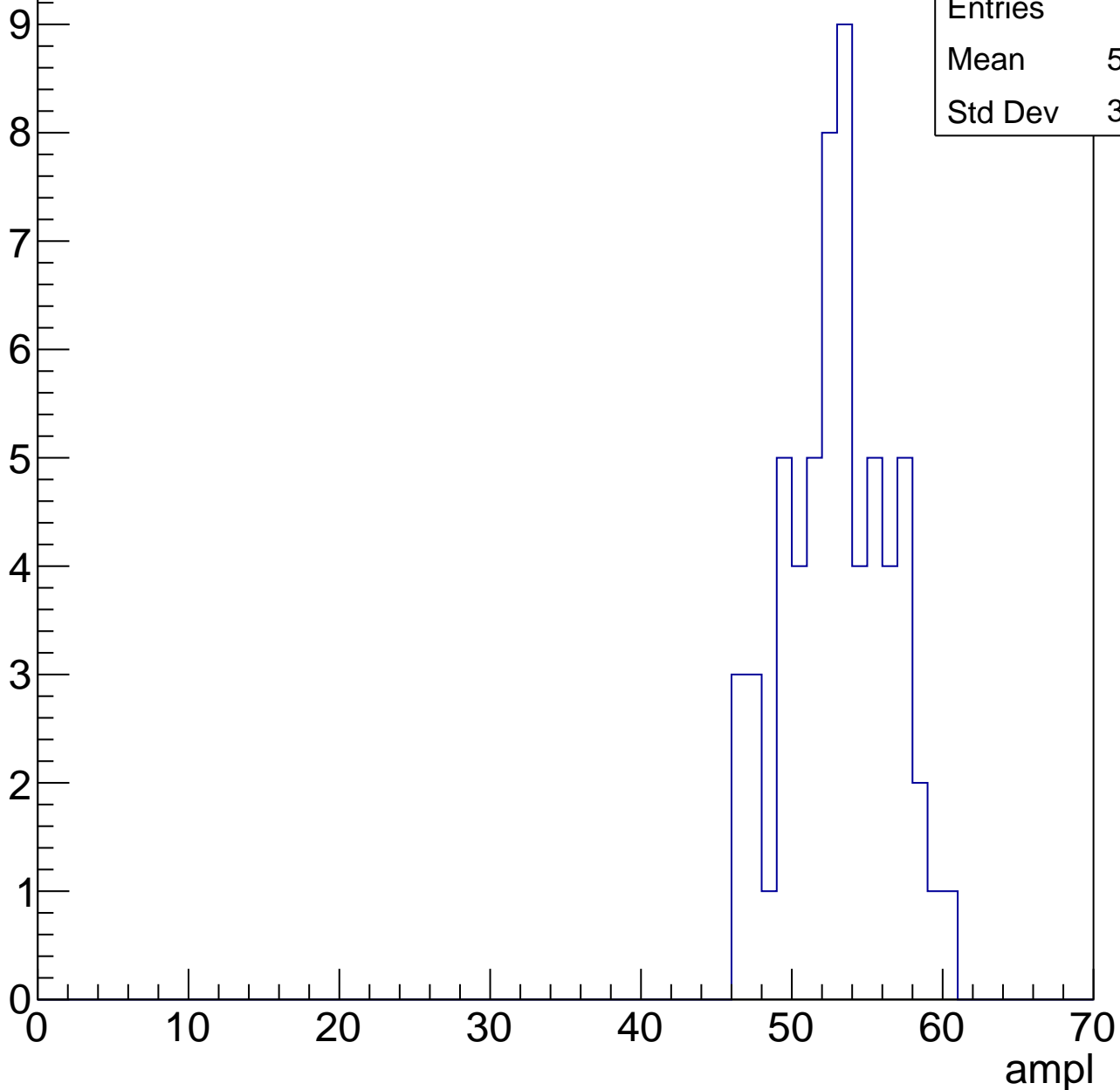


# B0L002S, U23-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	52.58
Std Dev	3.388

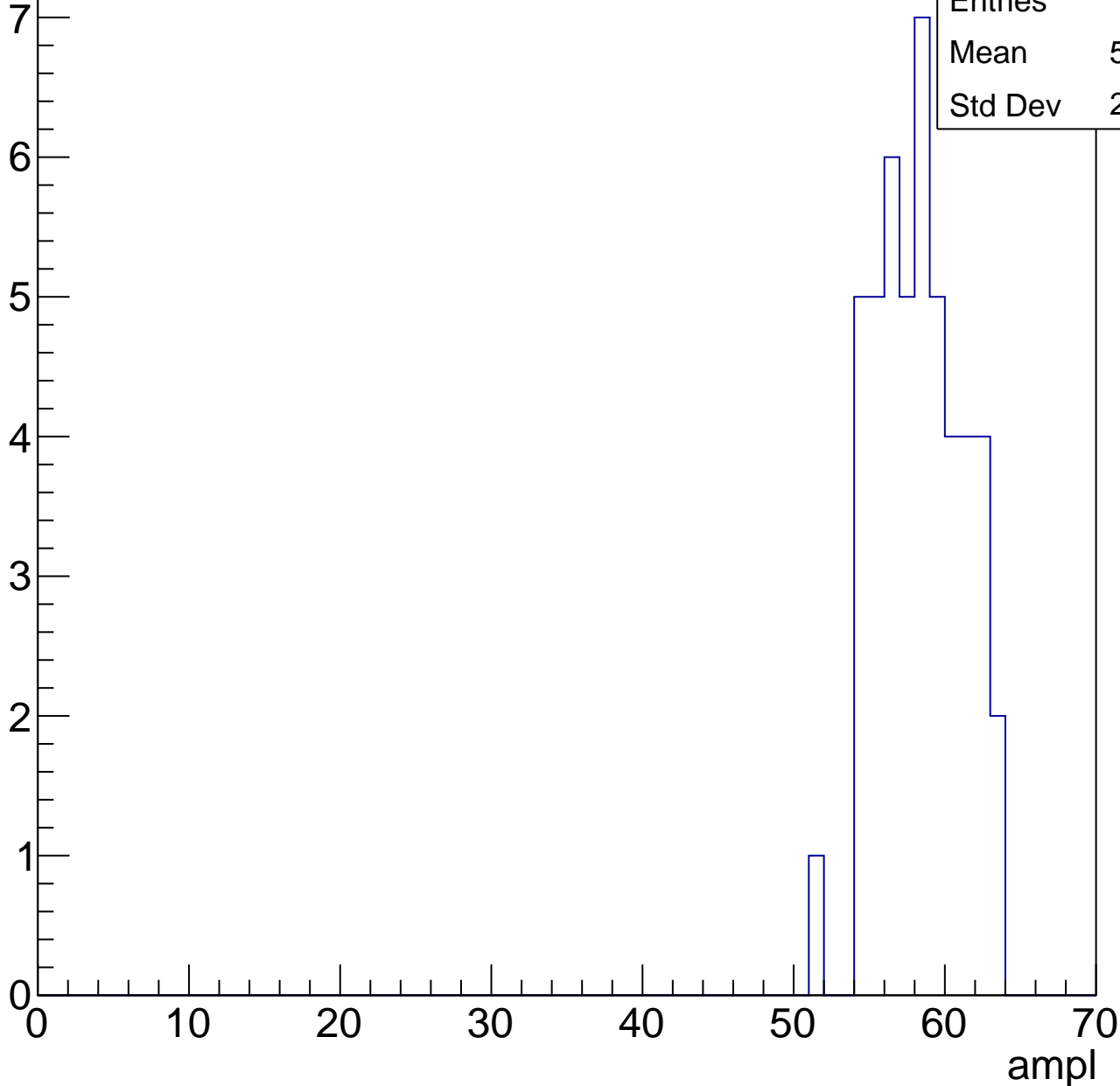


# B0L002S, U23-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	57.83
Std Dev	2.786

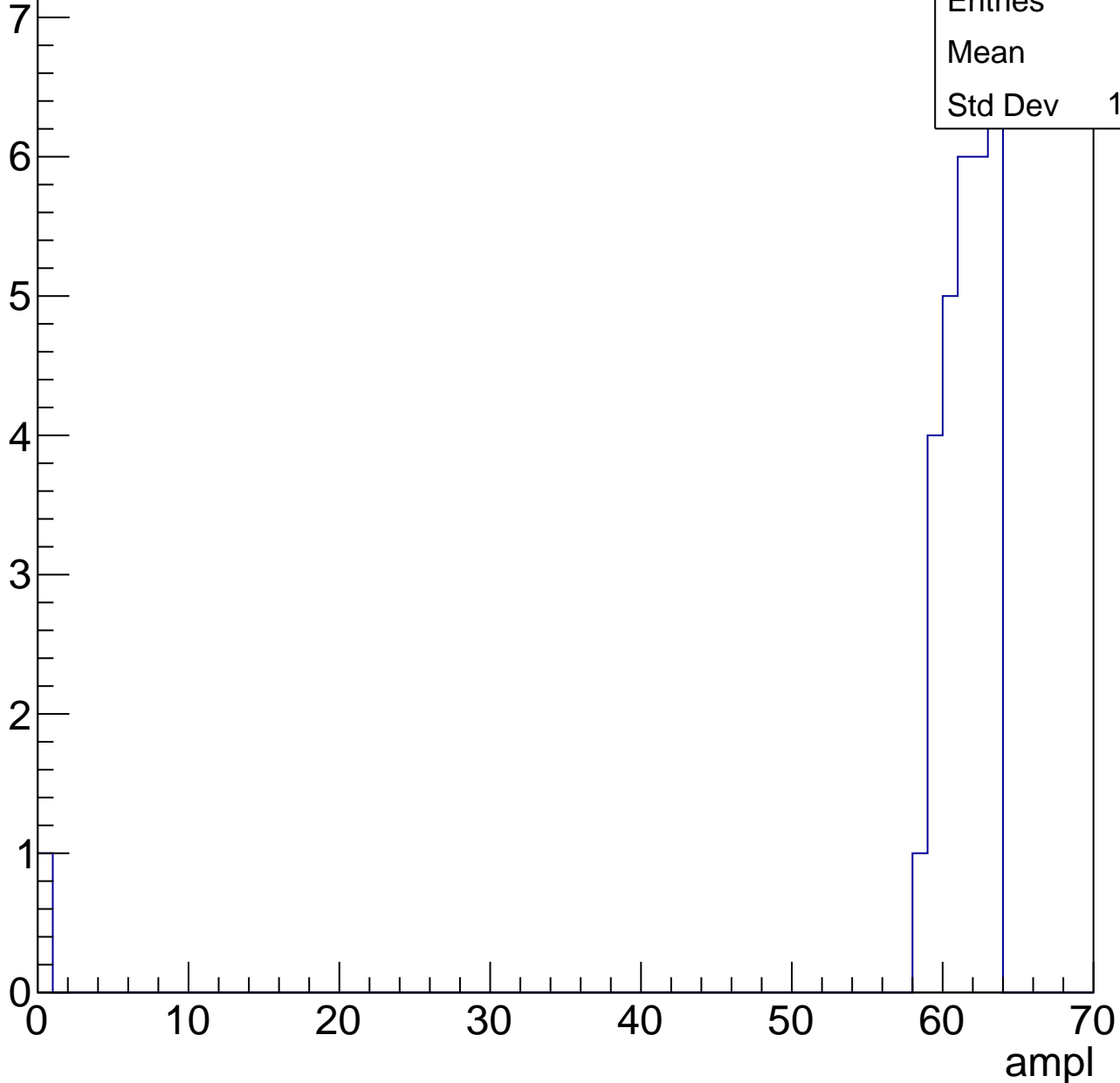


# B0L002S, U23-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.1
Std Dev	11.07





# B0L002S, U23-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch103, adc0

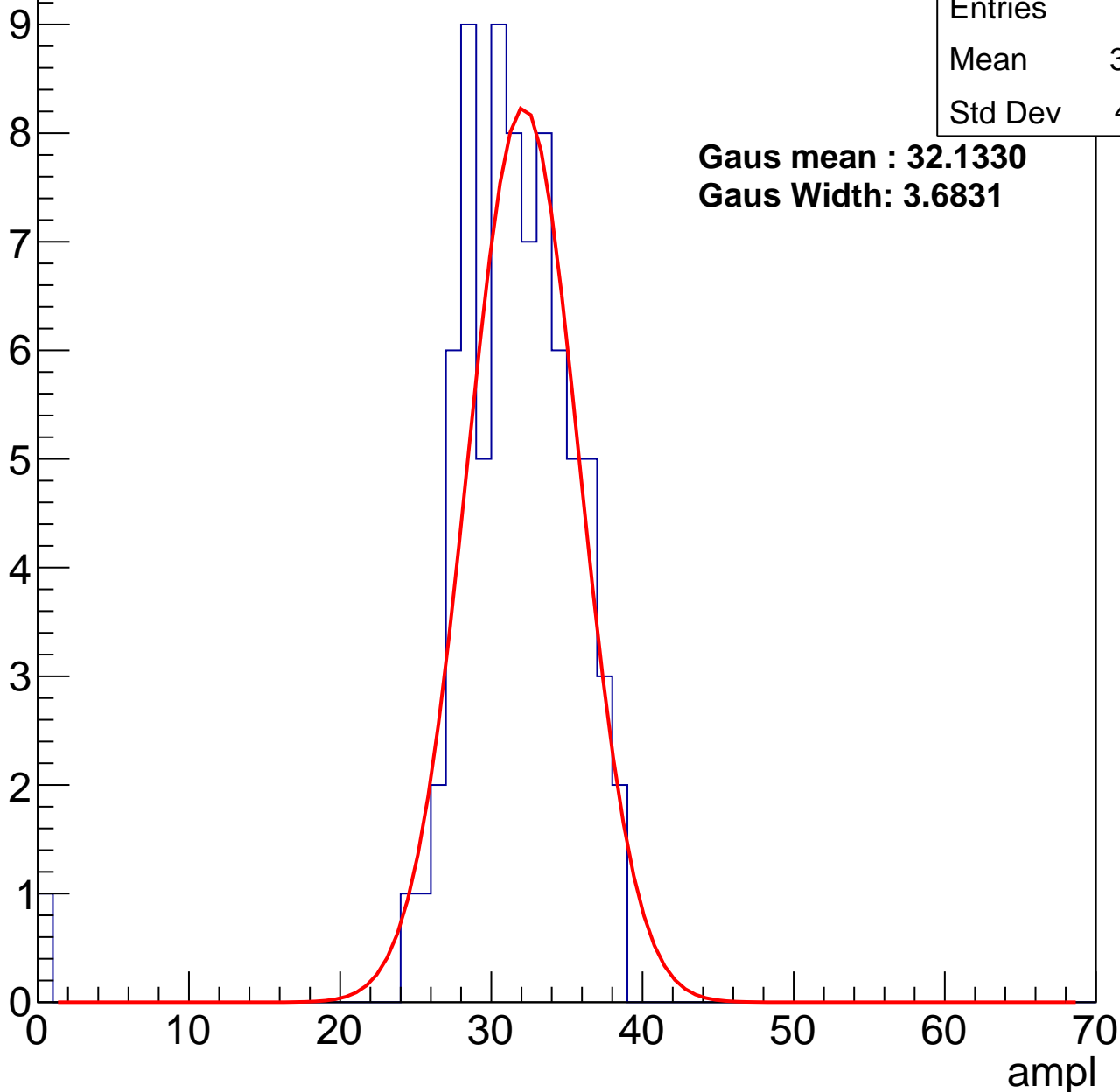
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	30.92
Std Dev	4.811

**Gaus mean : 32.1330**

**Gaus Width: 3.6831**



# B0L002S, U23-ch103, adc1

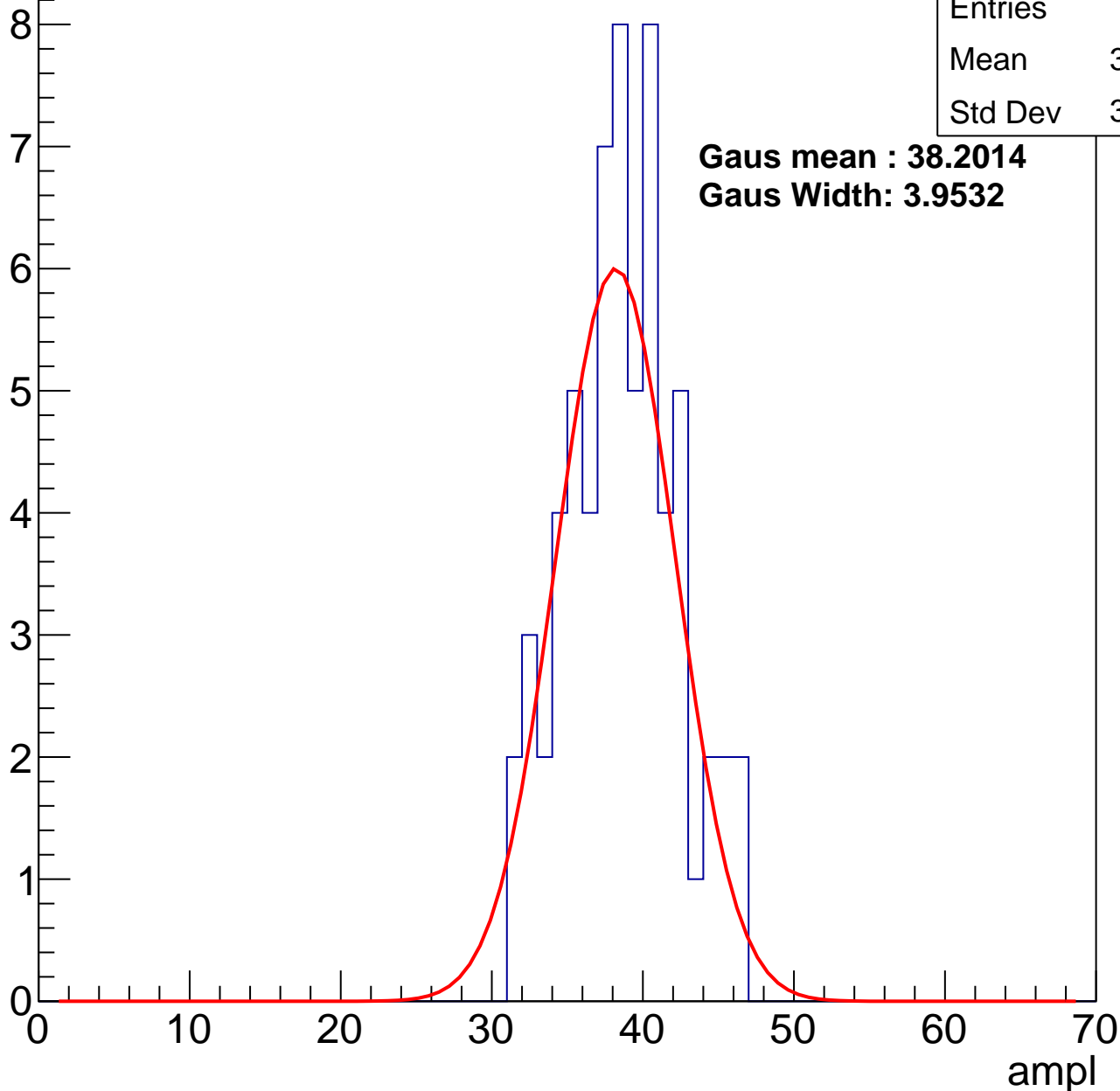
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.19
Std Dev	3.669

**Gaus mean : 38.2014**

**Gaus Width: 3.9532**



# B0L002S, U23-ch103, adc2

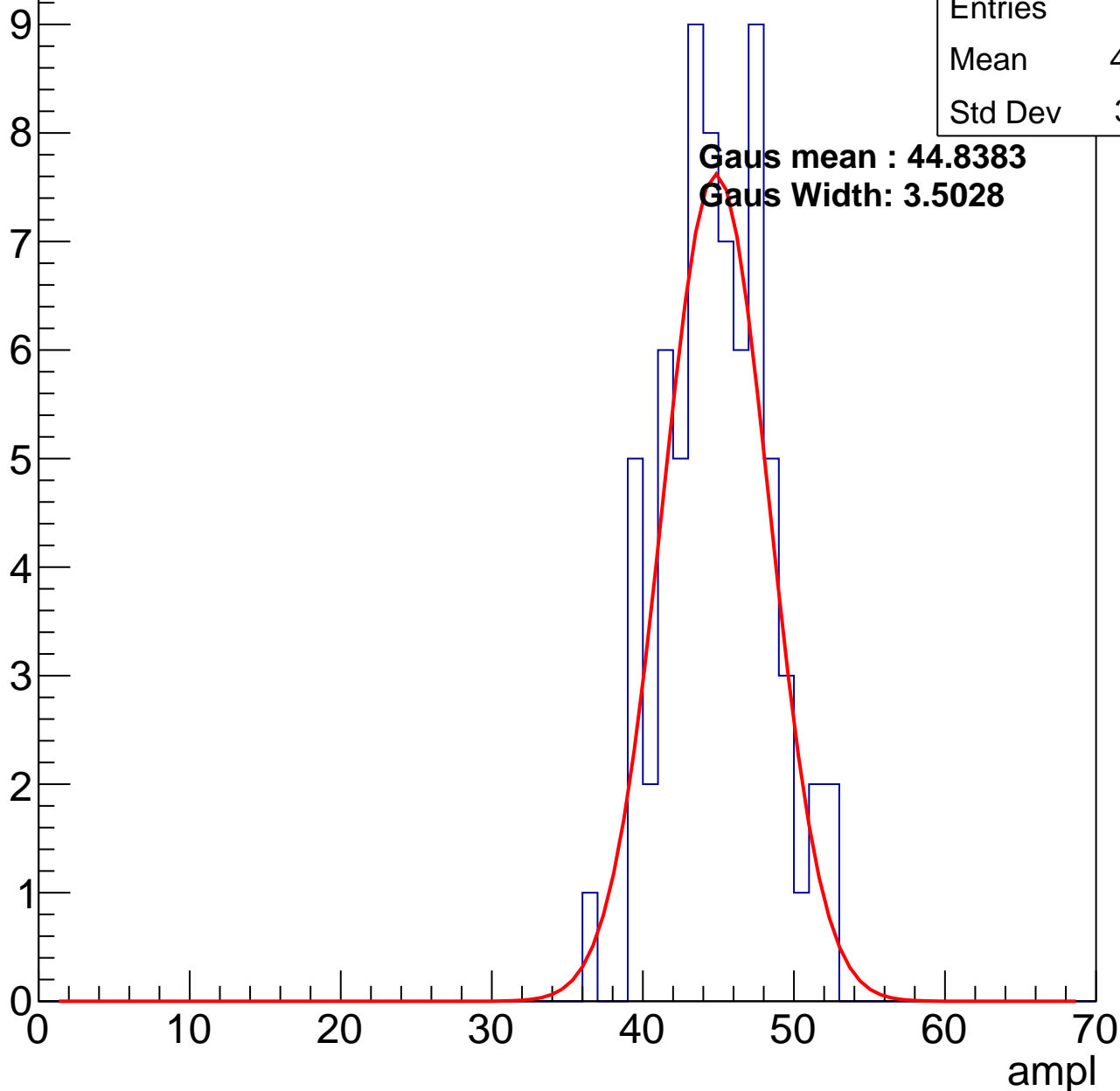
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.55
Std Dev	3.381

**Gaus mean : 44.8383**

**Gaus Width: 3.5028**

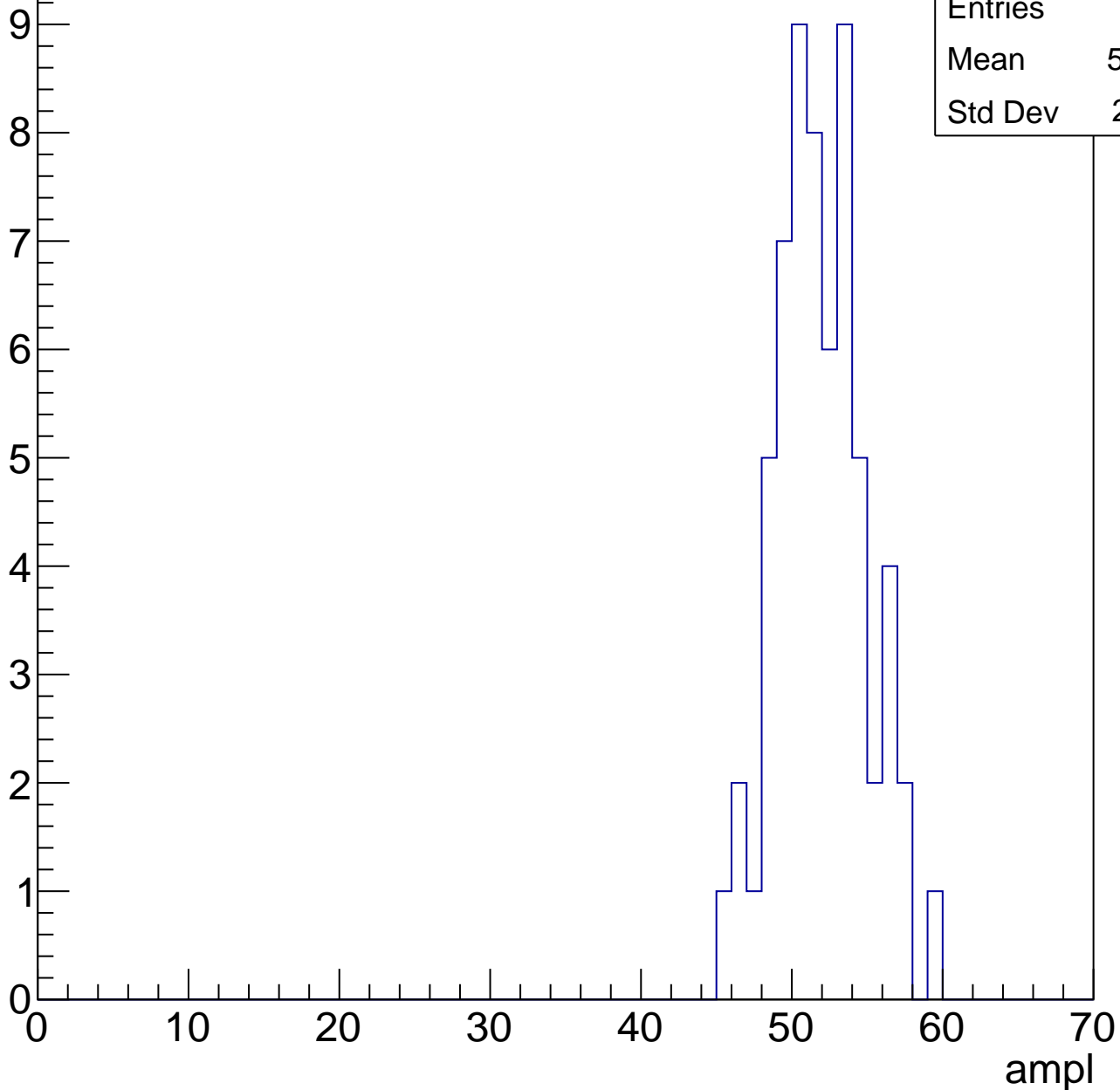


# B0L002S, U23-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

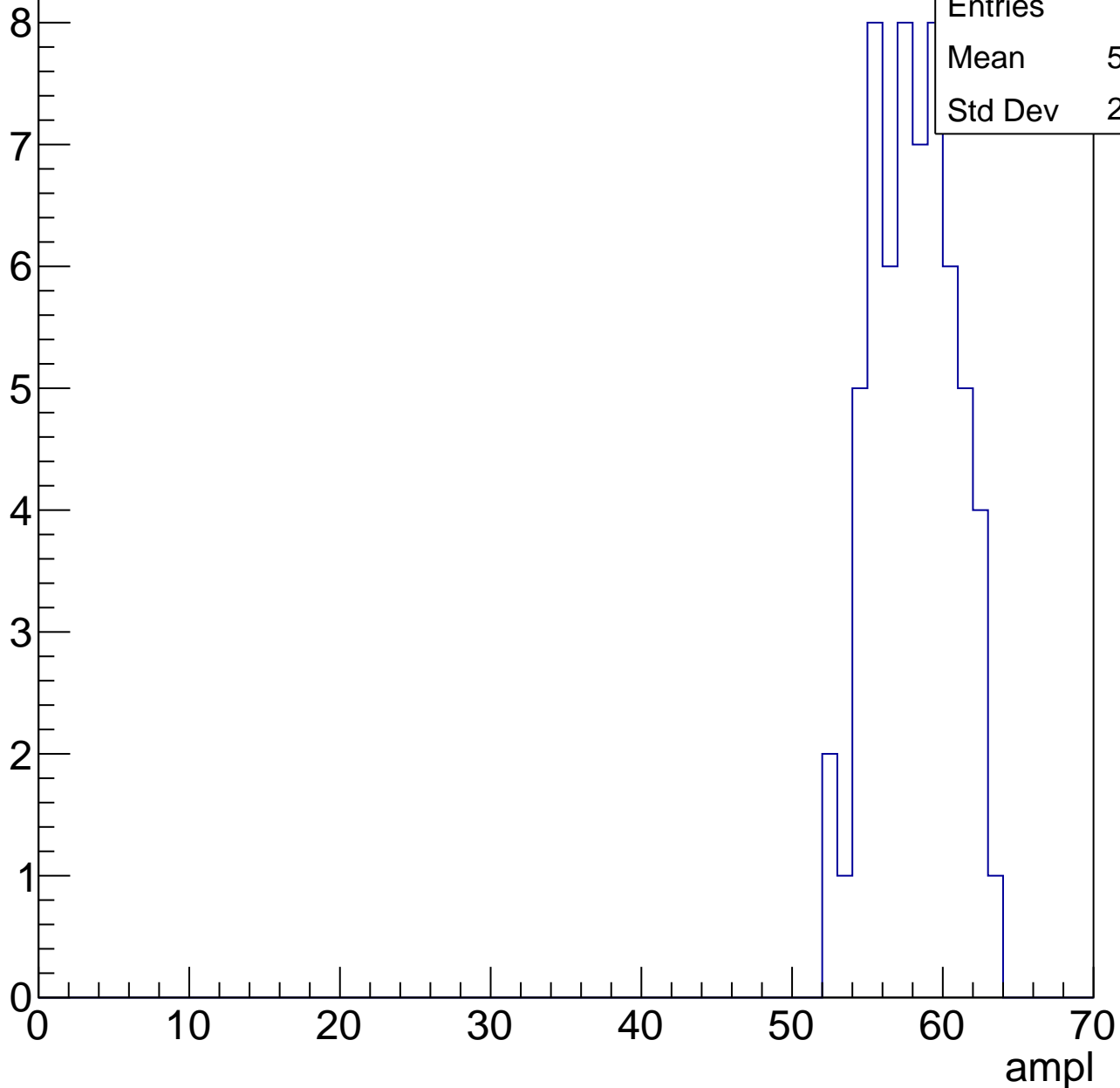
Entries	62
Mean	51.47
Std Dev	2.911



# B0L002S, U23-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



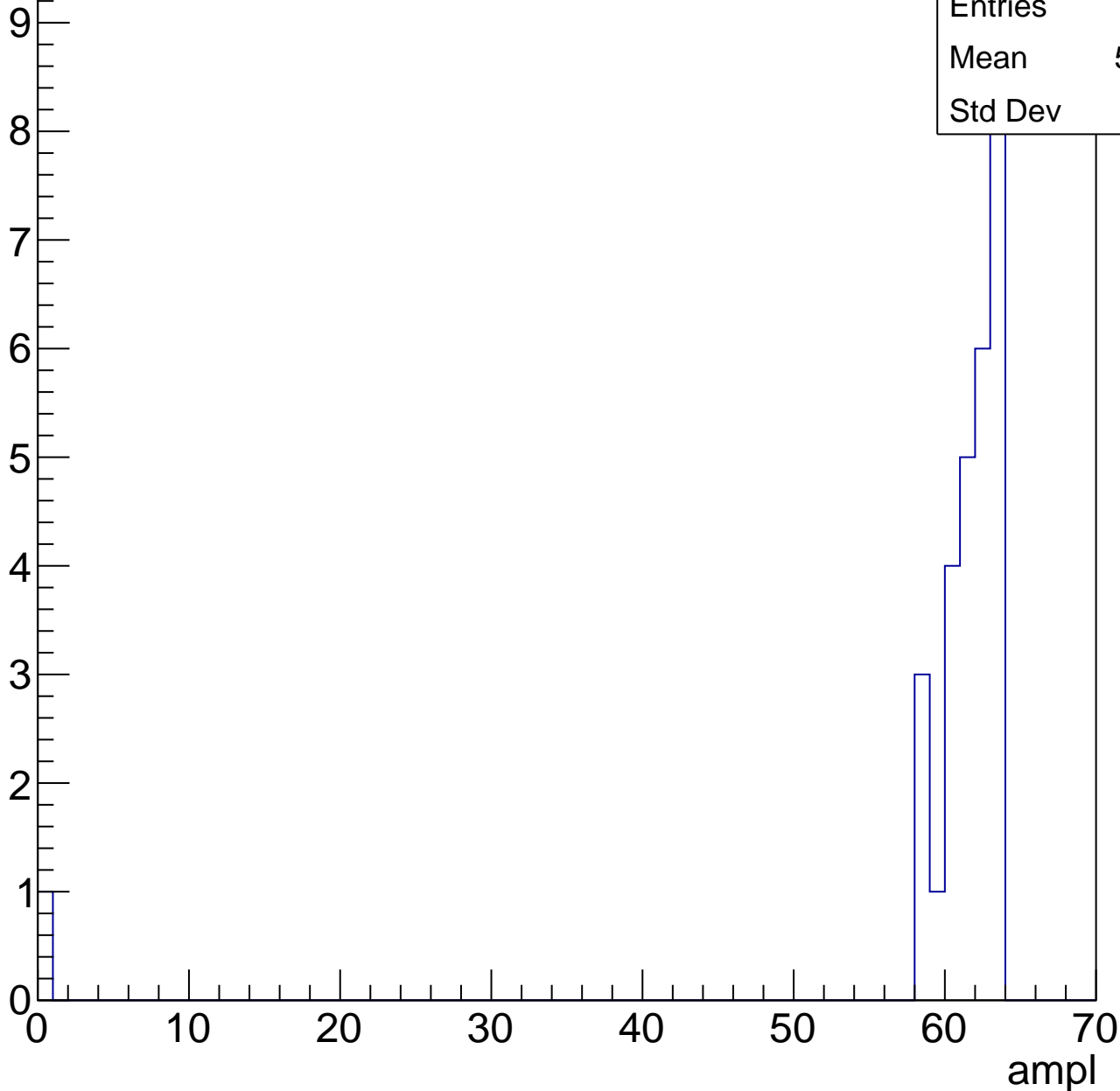
Entries	61
Mean	57.59
Std Dev	2.676

# B0L002S, U23-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59.21
Std Dev	11.3



# B0L002S, U23-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch104, adc0

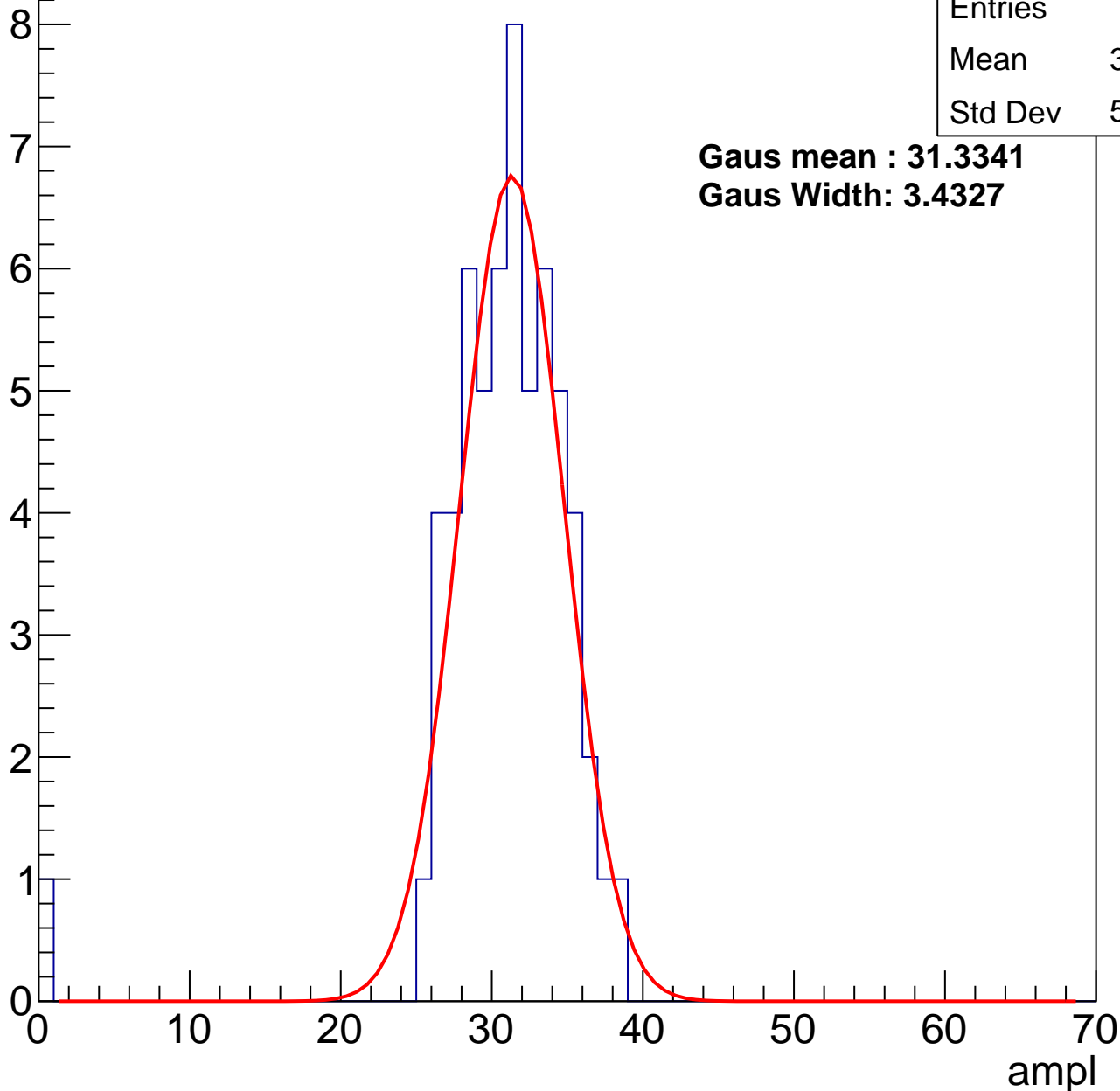
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.39
Std Dev	5.029

**Gaus mean : 31.3341**

**Gaus Width: 3.4327**



# B0L002S, U23-ch104, adc1

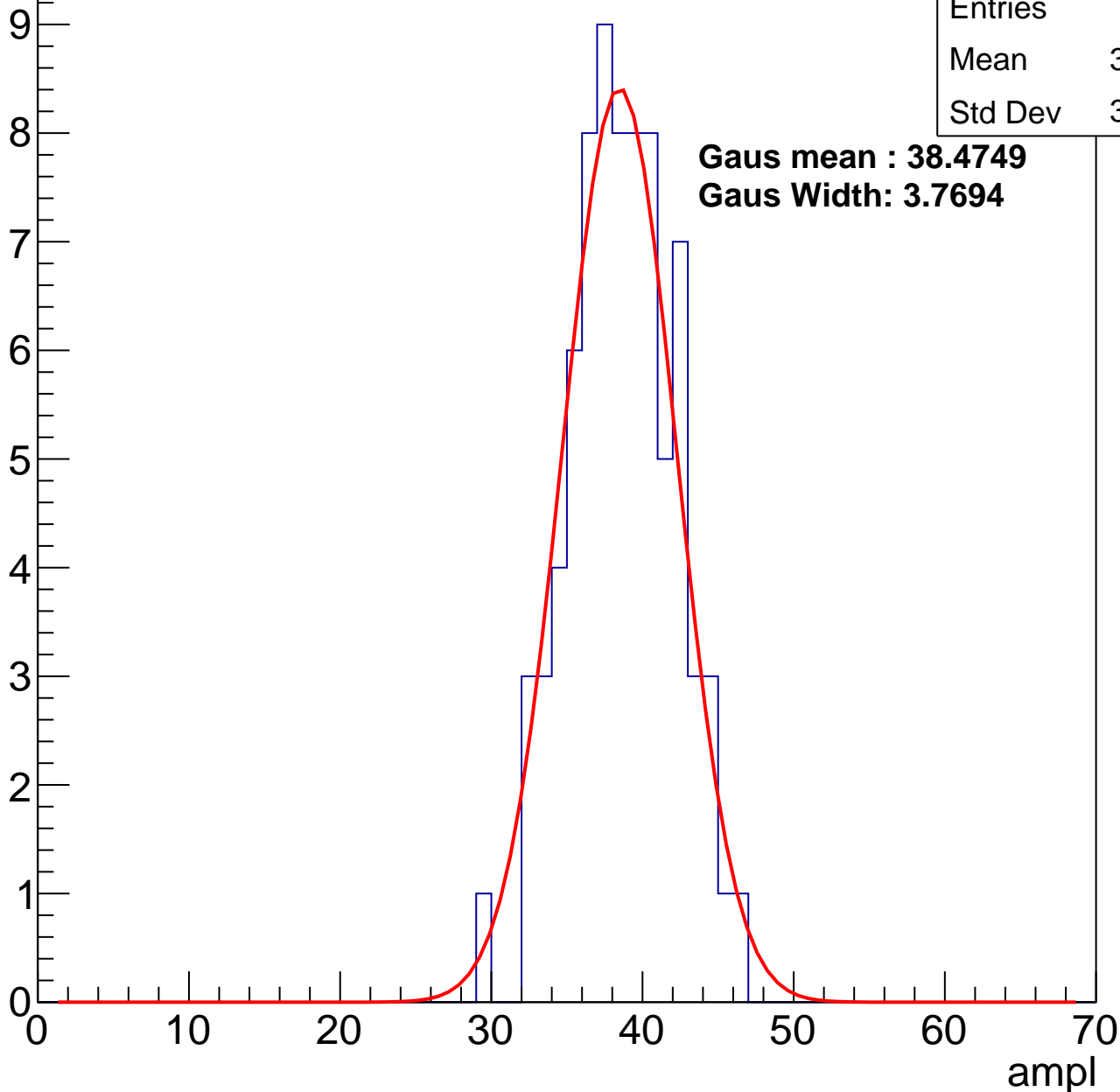
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	78
Mean	38.18
Std Dev	3.422

**Gaus mean : 38.4749**

**Gaus Width: 3.7694**



# B0L002S, U23-ch104, adc2

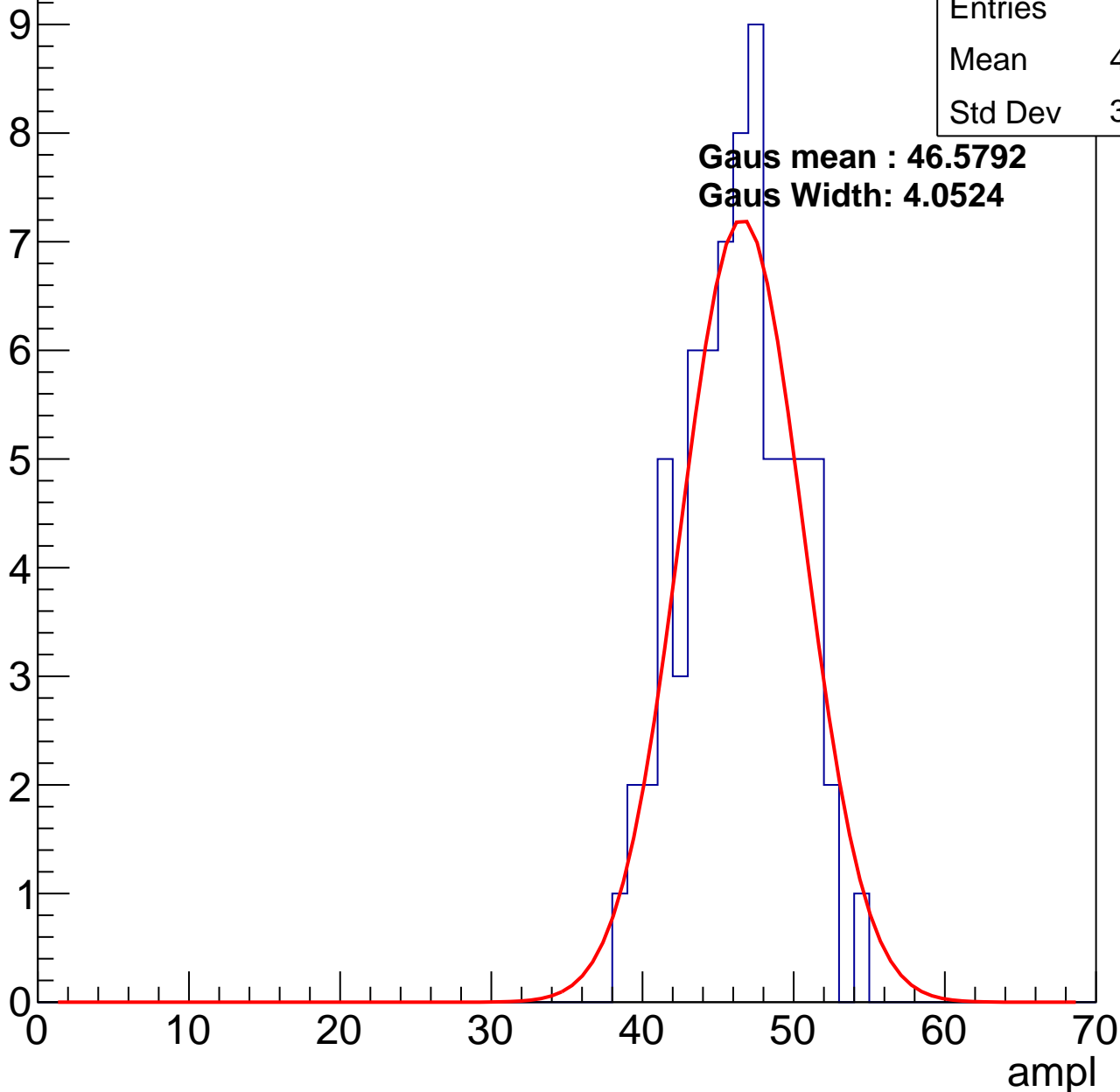
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45.88
Std Dev	3.547

**Gaus mean : 46.5792**

**Gaus Width: 4.0524**

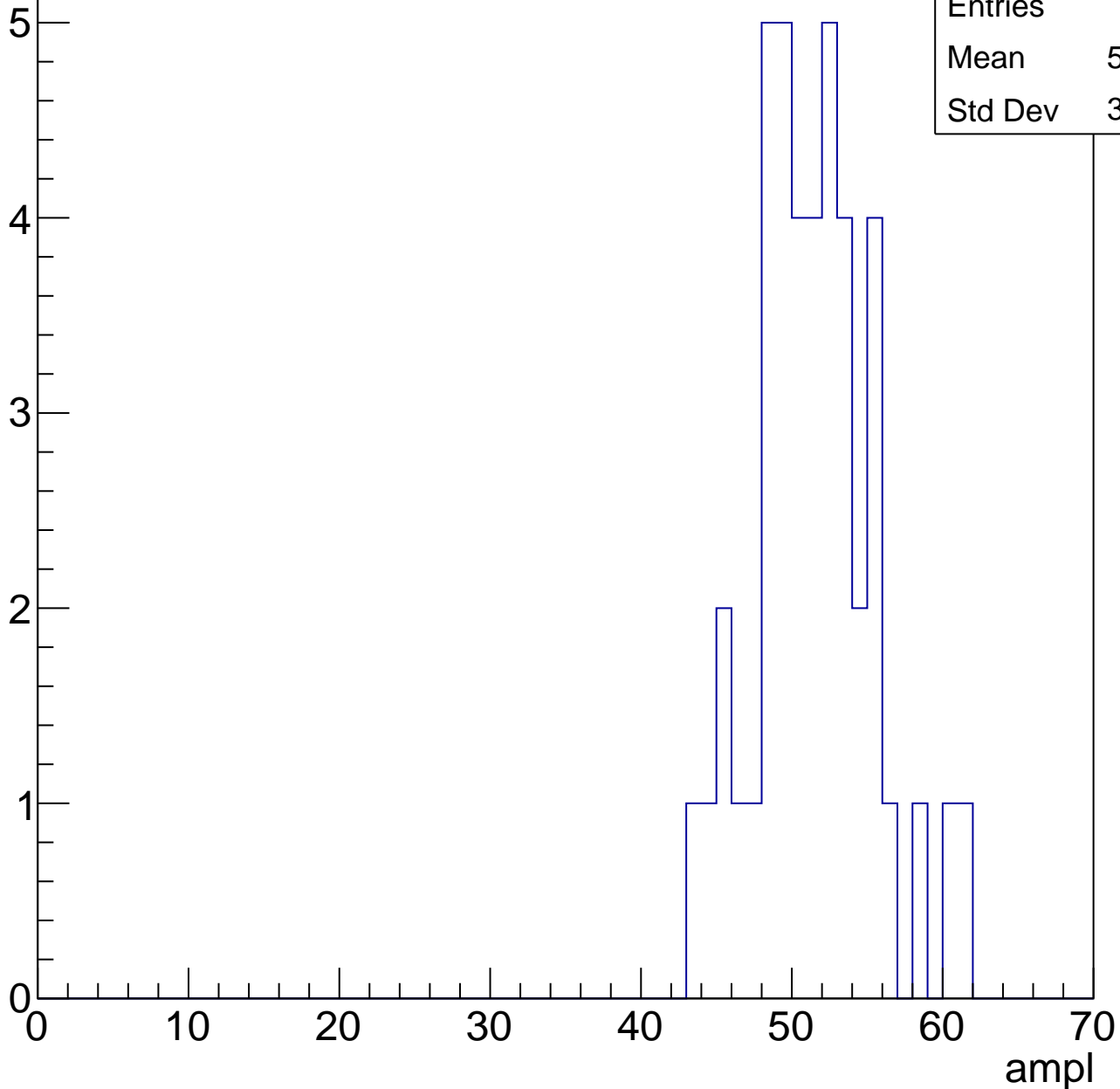


# B0L002S, U23-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	43
Mean	51.02
Std Dev	3.897

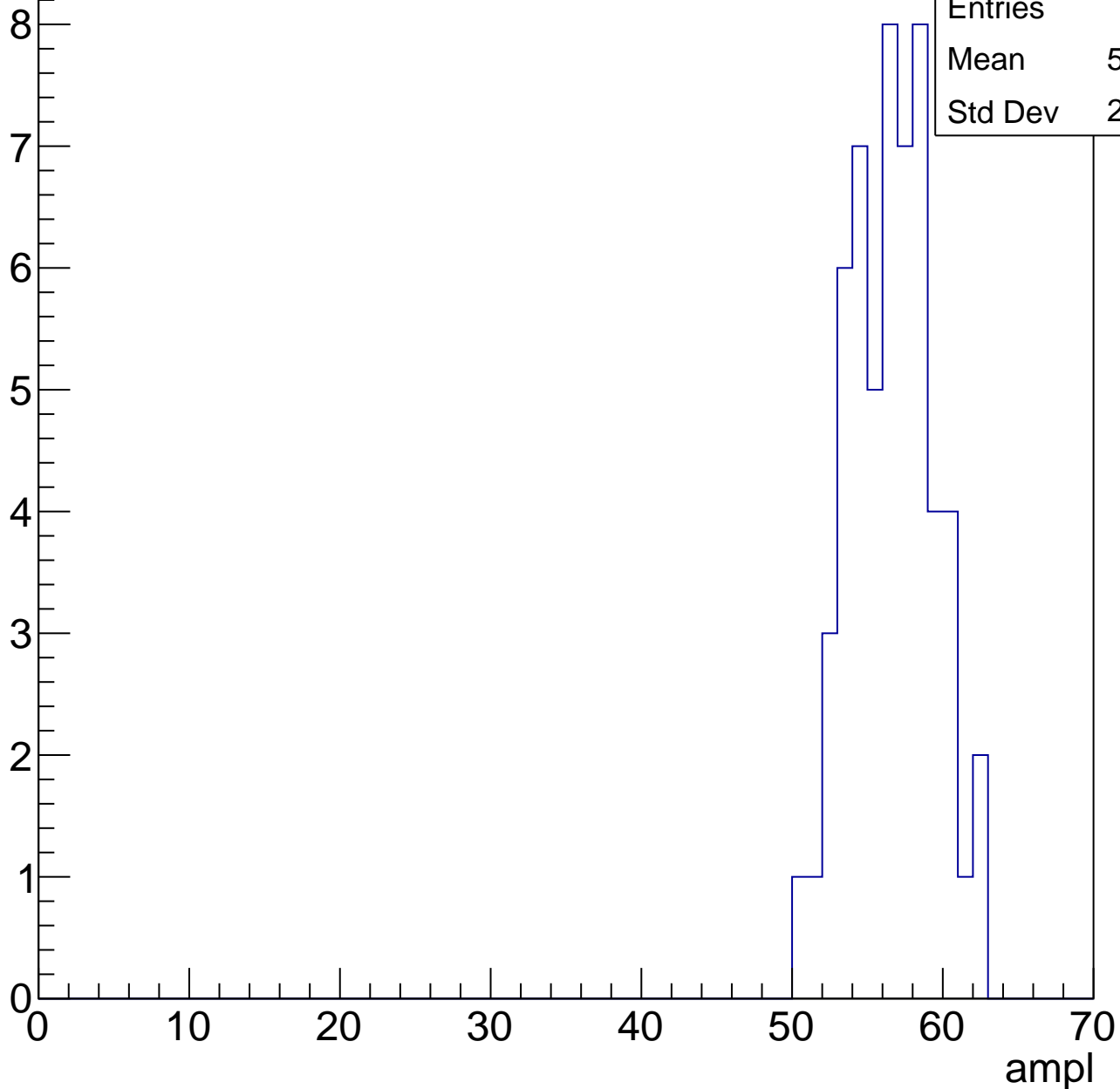


# B0L002S, U23-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.14
Std Dev	2.749

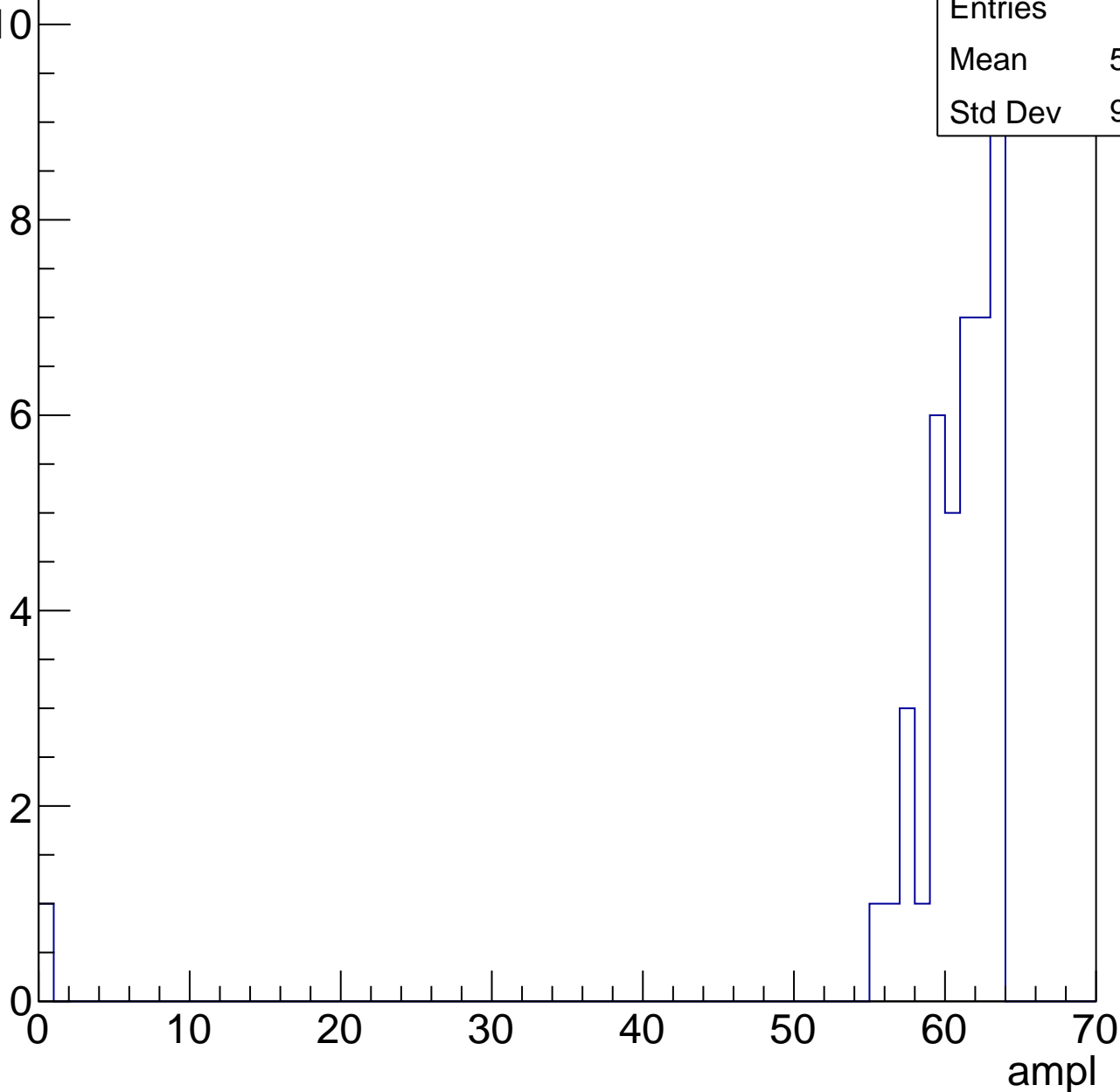


# B0L002S, U23-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	42
Mean	59.17
Std Dev	9.479



# B0L002S, U23-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L002S, U23-ch105, adc0

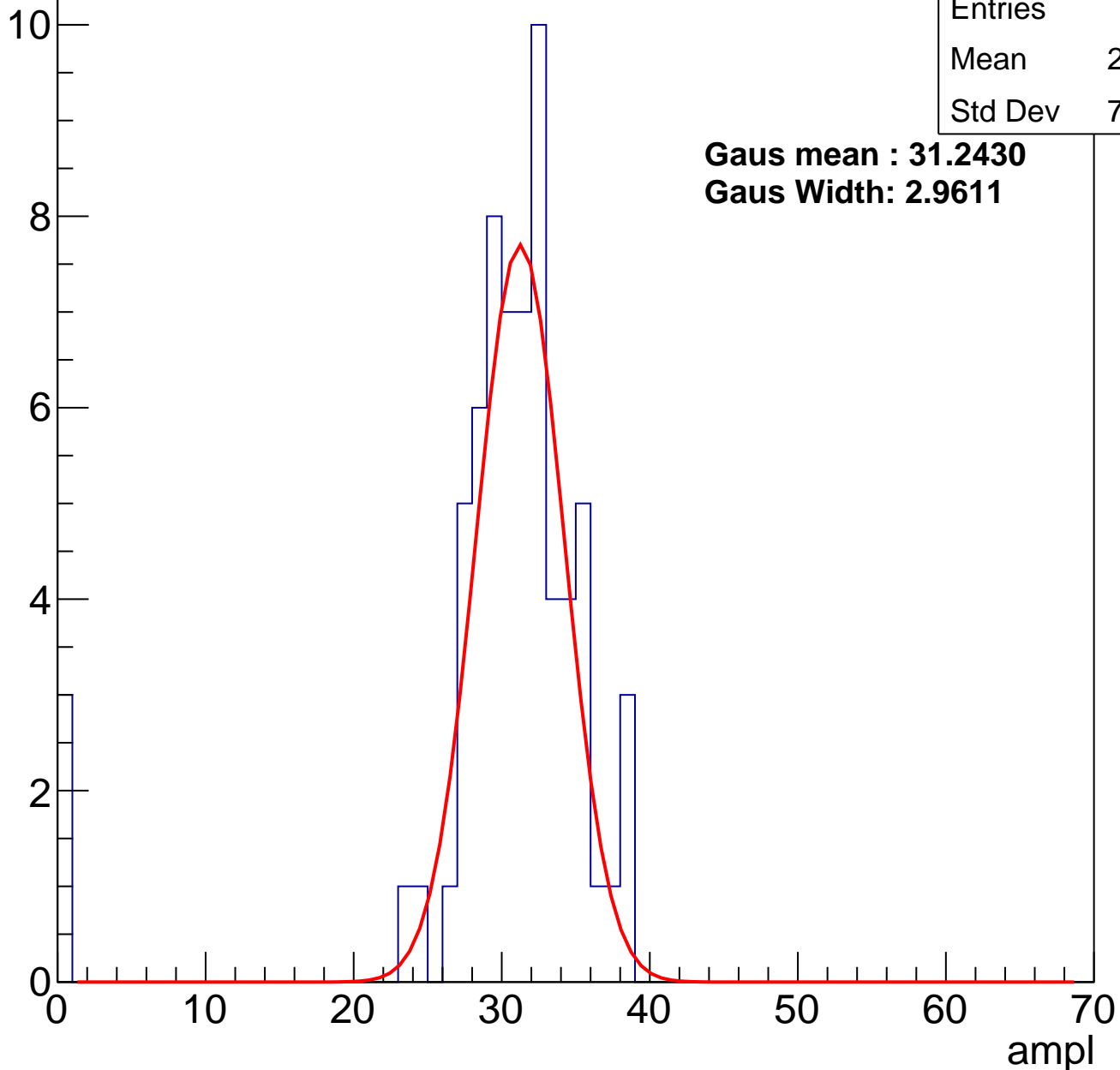
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	67
Mean	29.63
Std Dev	7.142

**Gaus mean : 31.2430**

**Gaus Width: 2.9611**

Entry



# B0L002S, U23-ch105, adc1

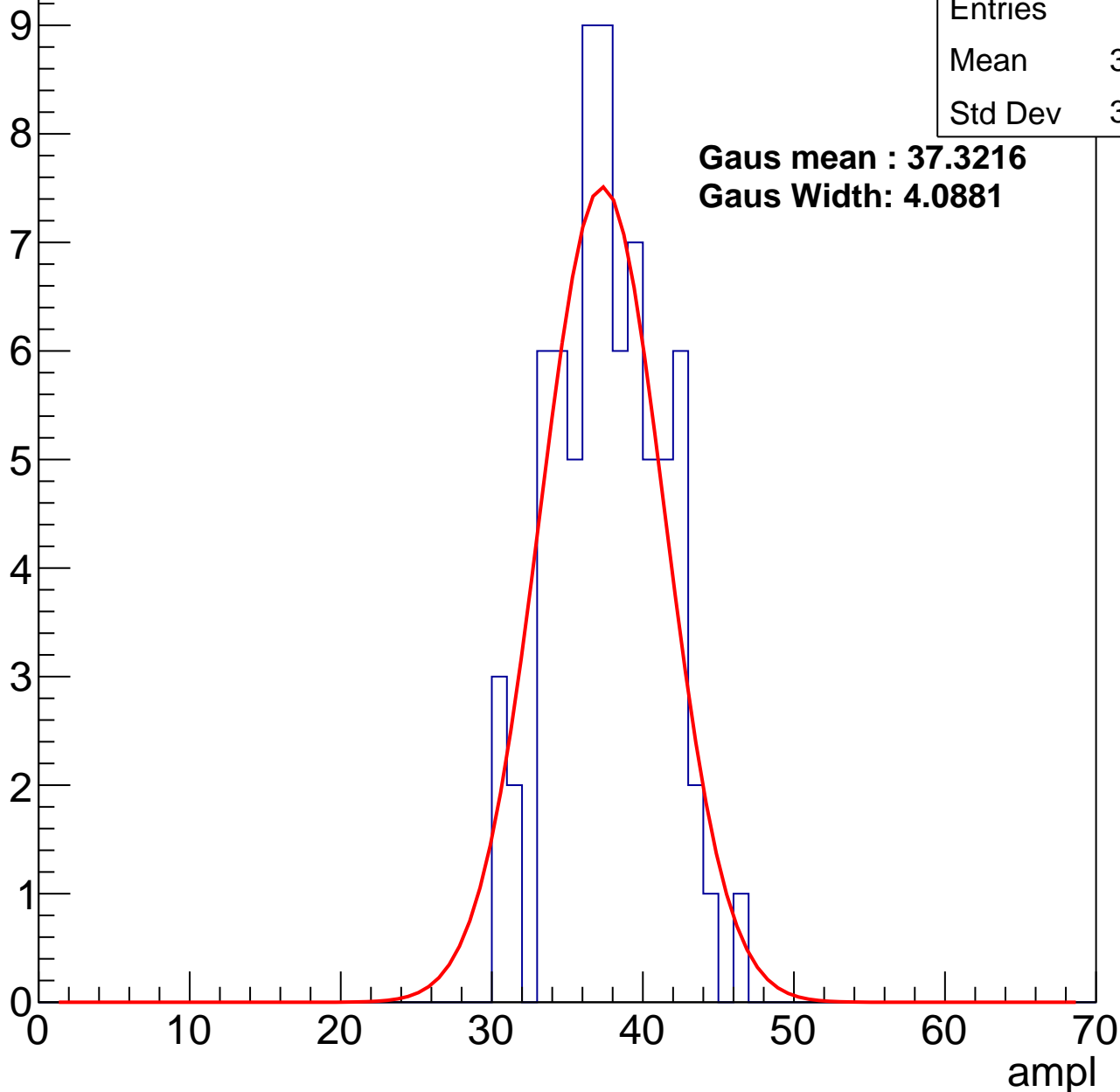
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.26
Std Dev	3.519

**Gaus mean : 37.3216**

**Gaus Width: 4.0881**



# B0L002S, U23-ch105, adc2

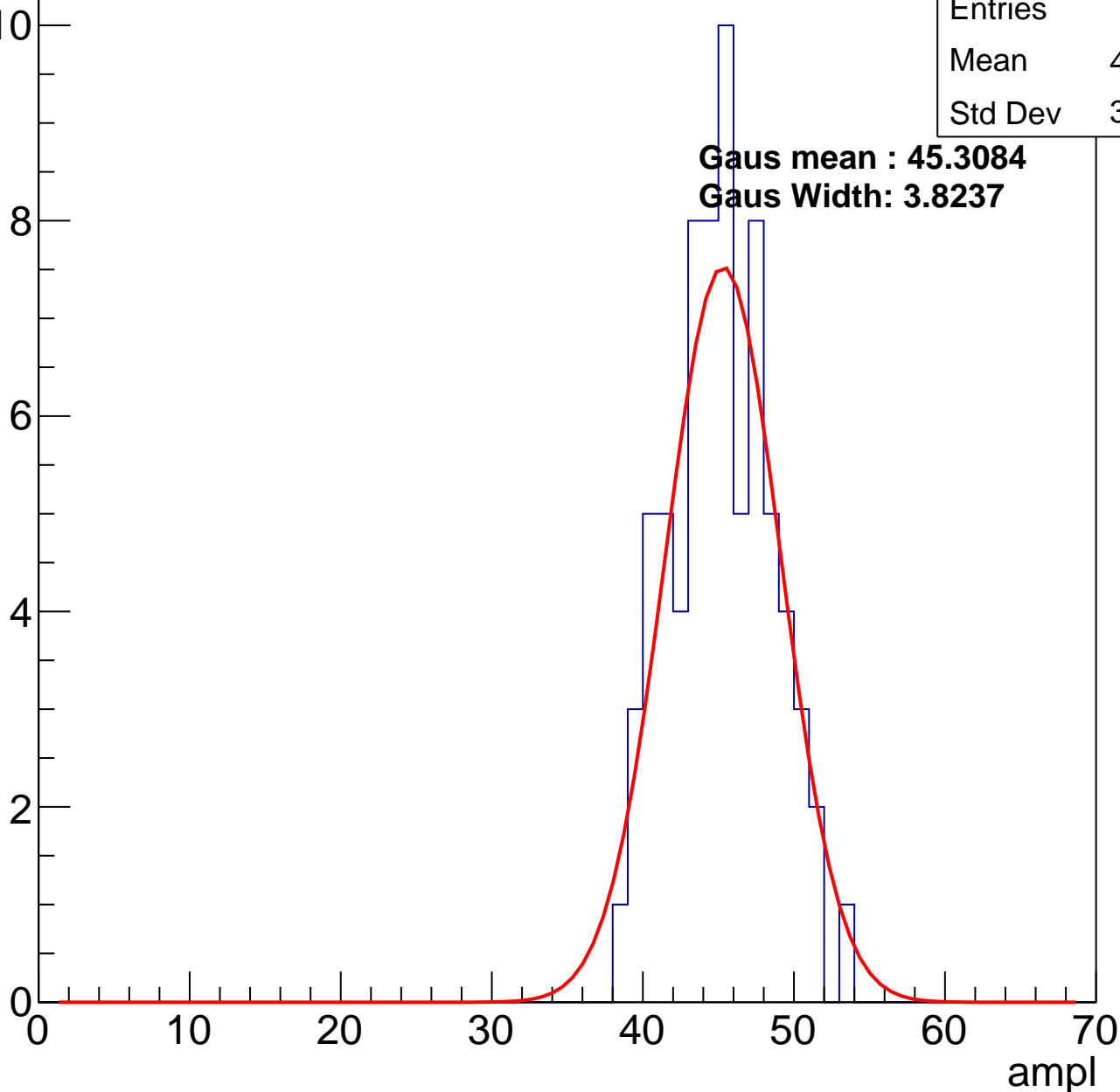
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.74
Std Dev	3.312

**Gaus mean : 45.3084**

**Gaus Width: 3.8237**

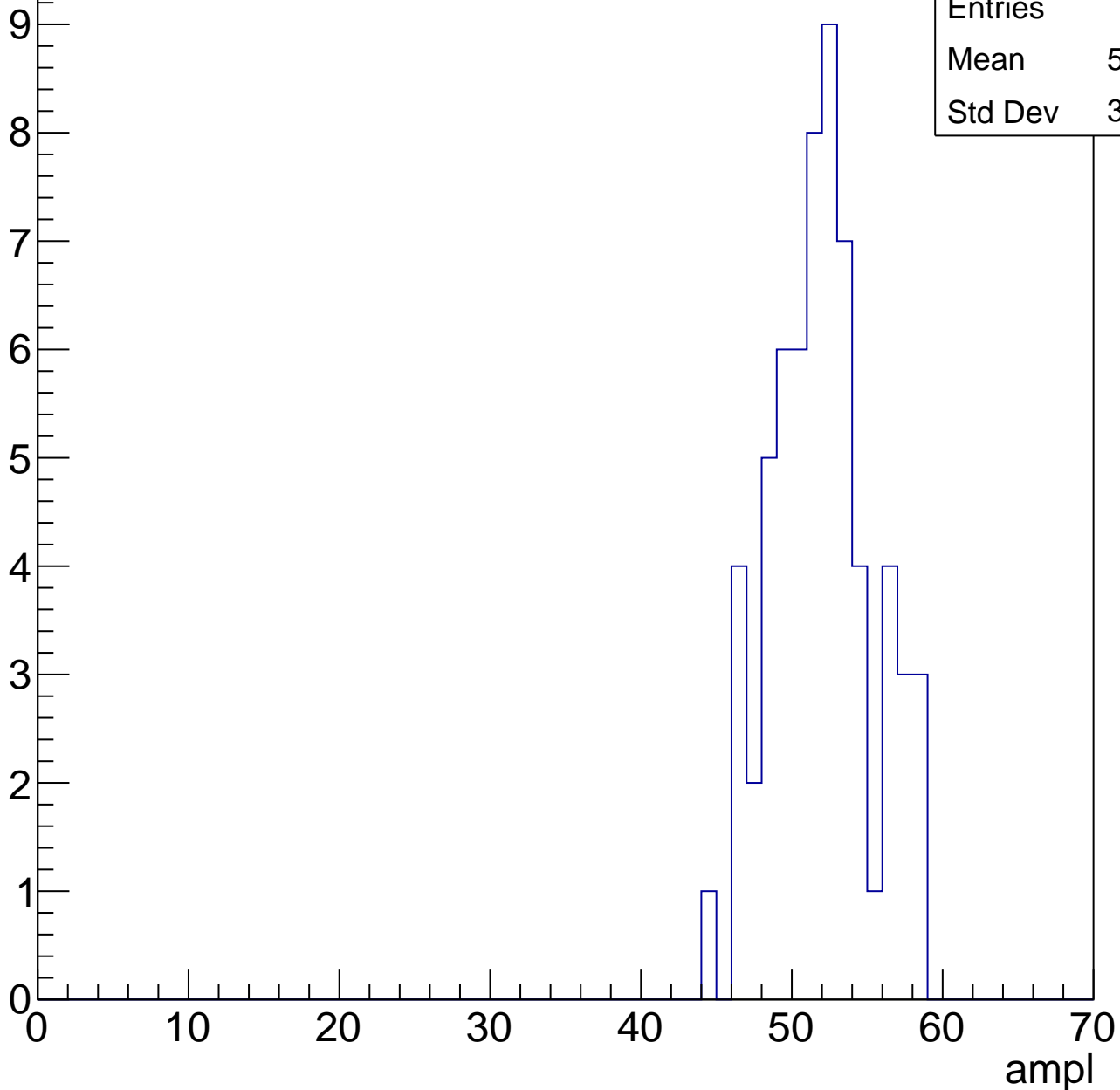


# B0L002S, U23-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	51.48
Std Dev	3.299

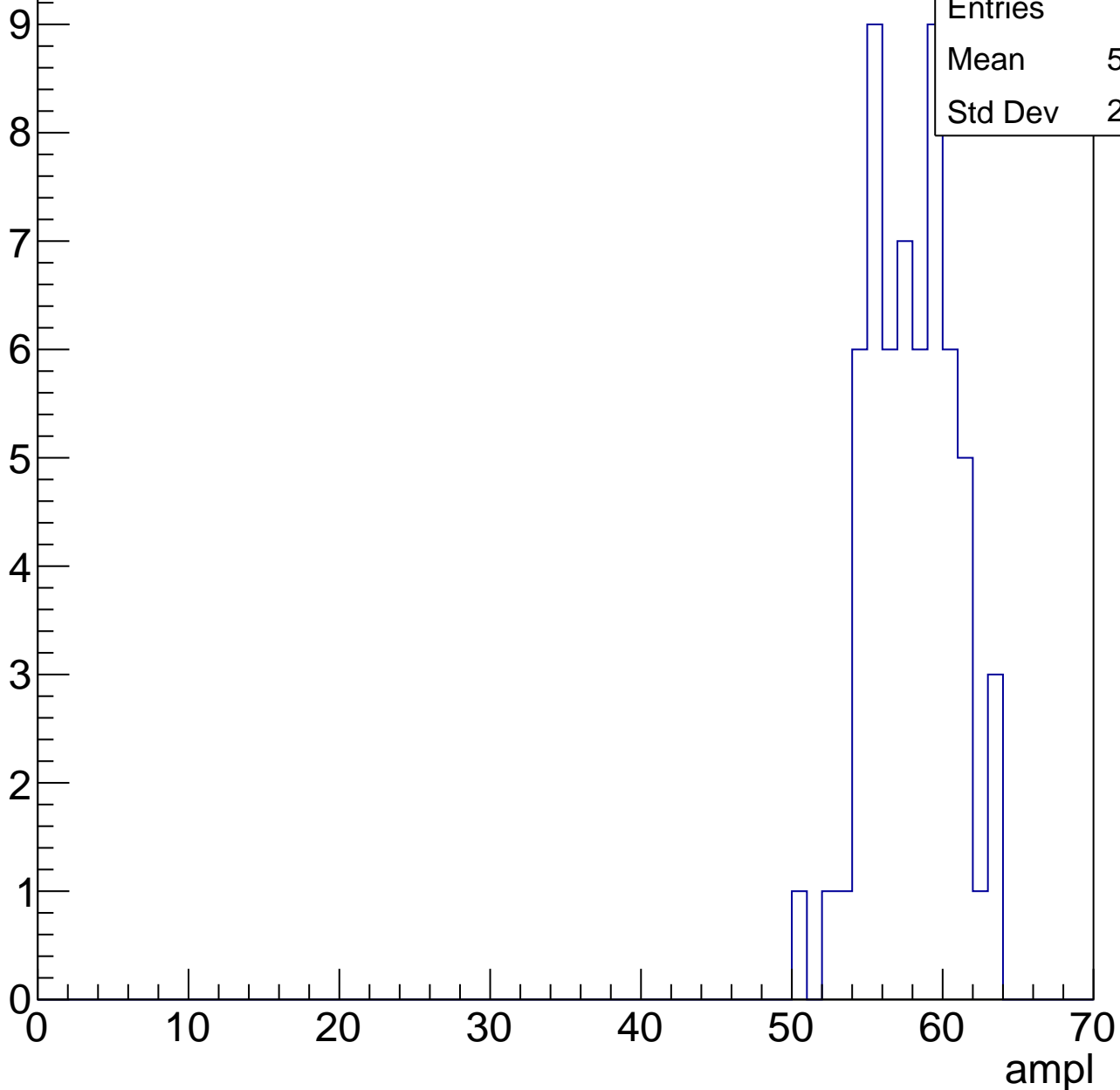


# B0L002S, U23-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	57.44
Std Dev	2.814

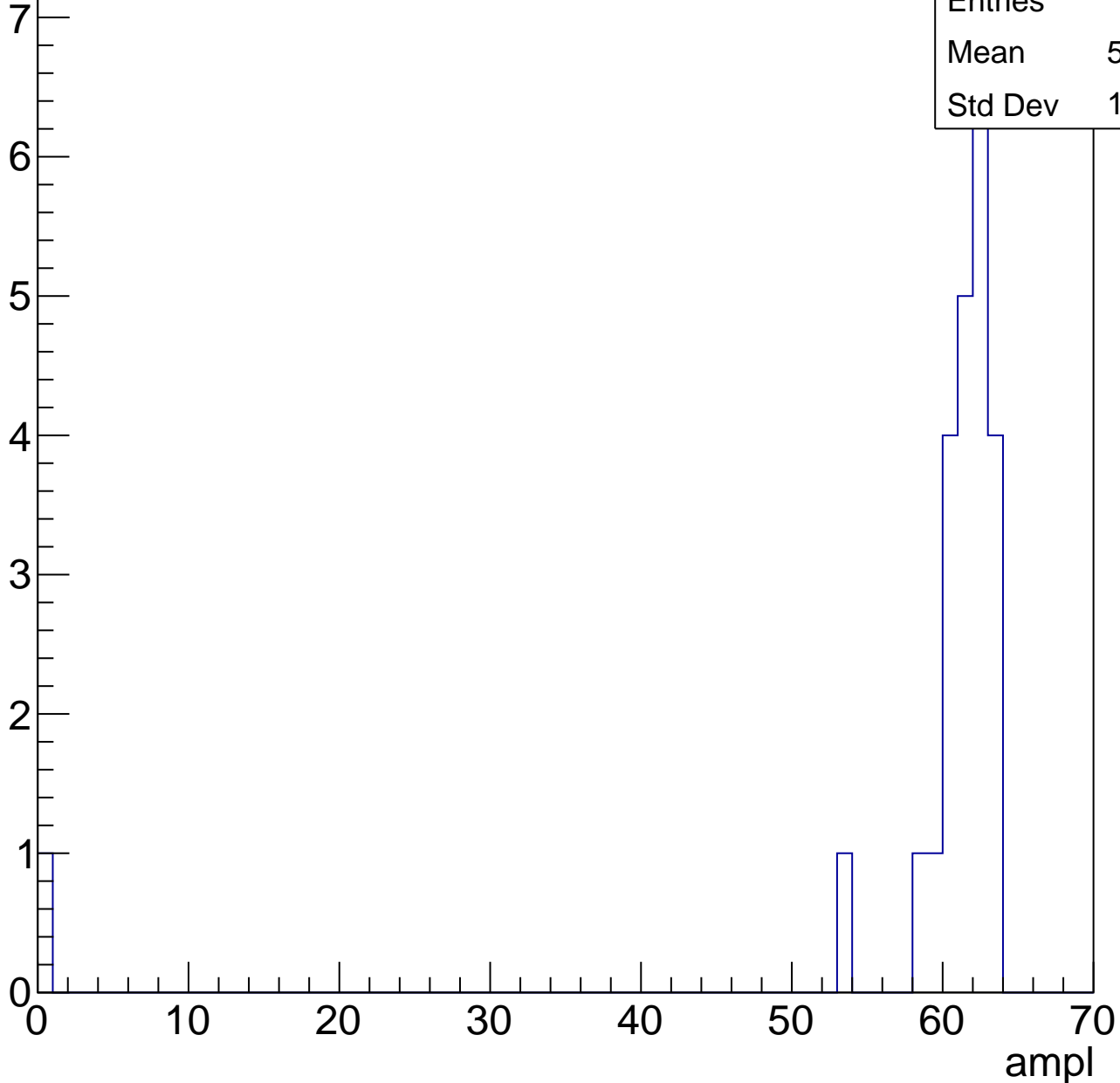


# B0L002S, U23-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.38
Std Dev	12.35



# B0L002S, U23-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch106, adc0

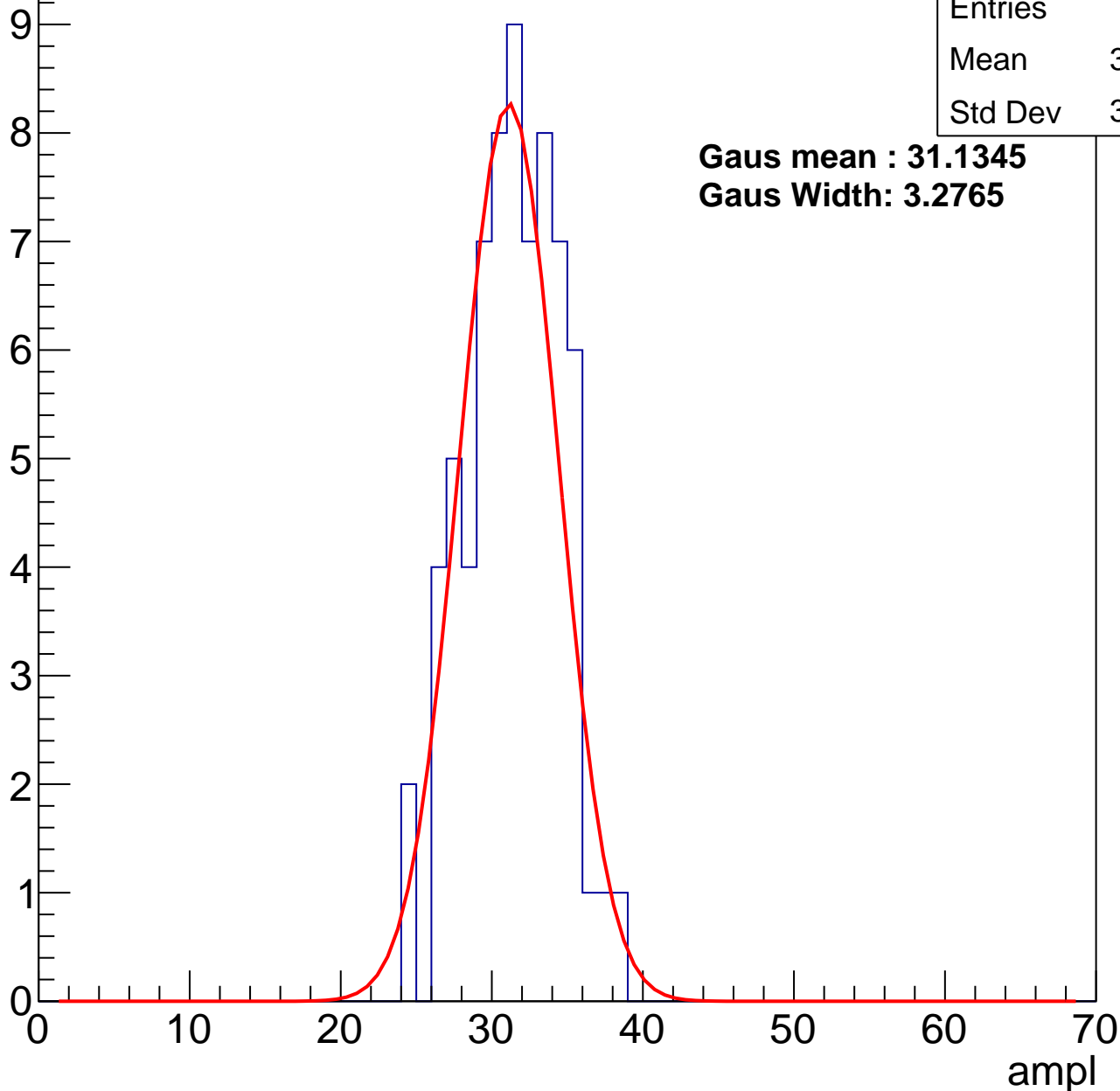
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.97
Std Dev	3.066

**Gaus mean : 31.1345**

**Gaus Width: 3.2765**



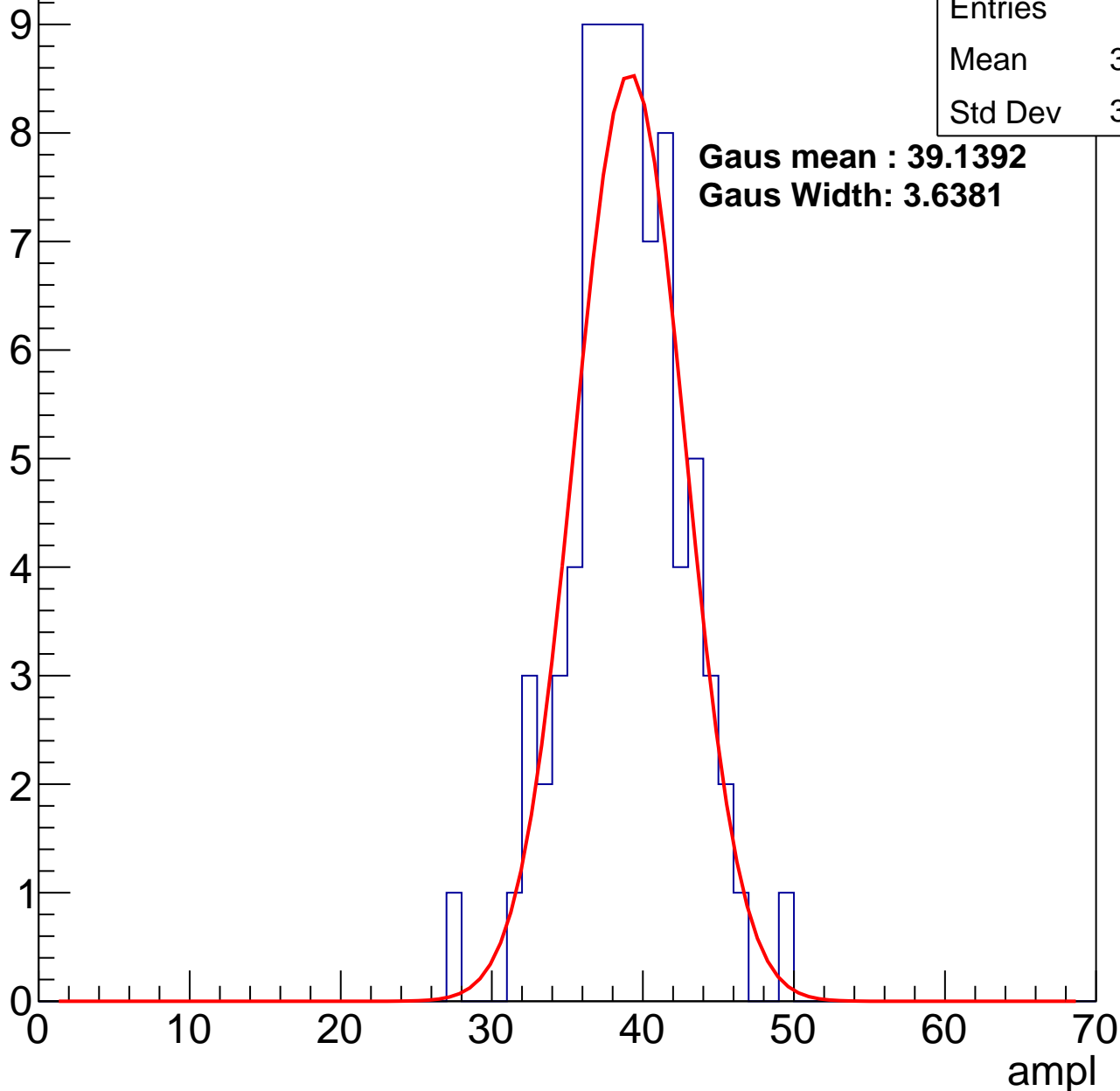
# B0L002S, U23-ch106, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	38.52
Std Dev	3.725

**Gaus mean : 39.1392**  
**Gaus Width: 3.6381**



# B0L002S, U23-ch106, adc2

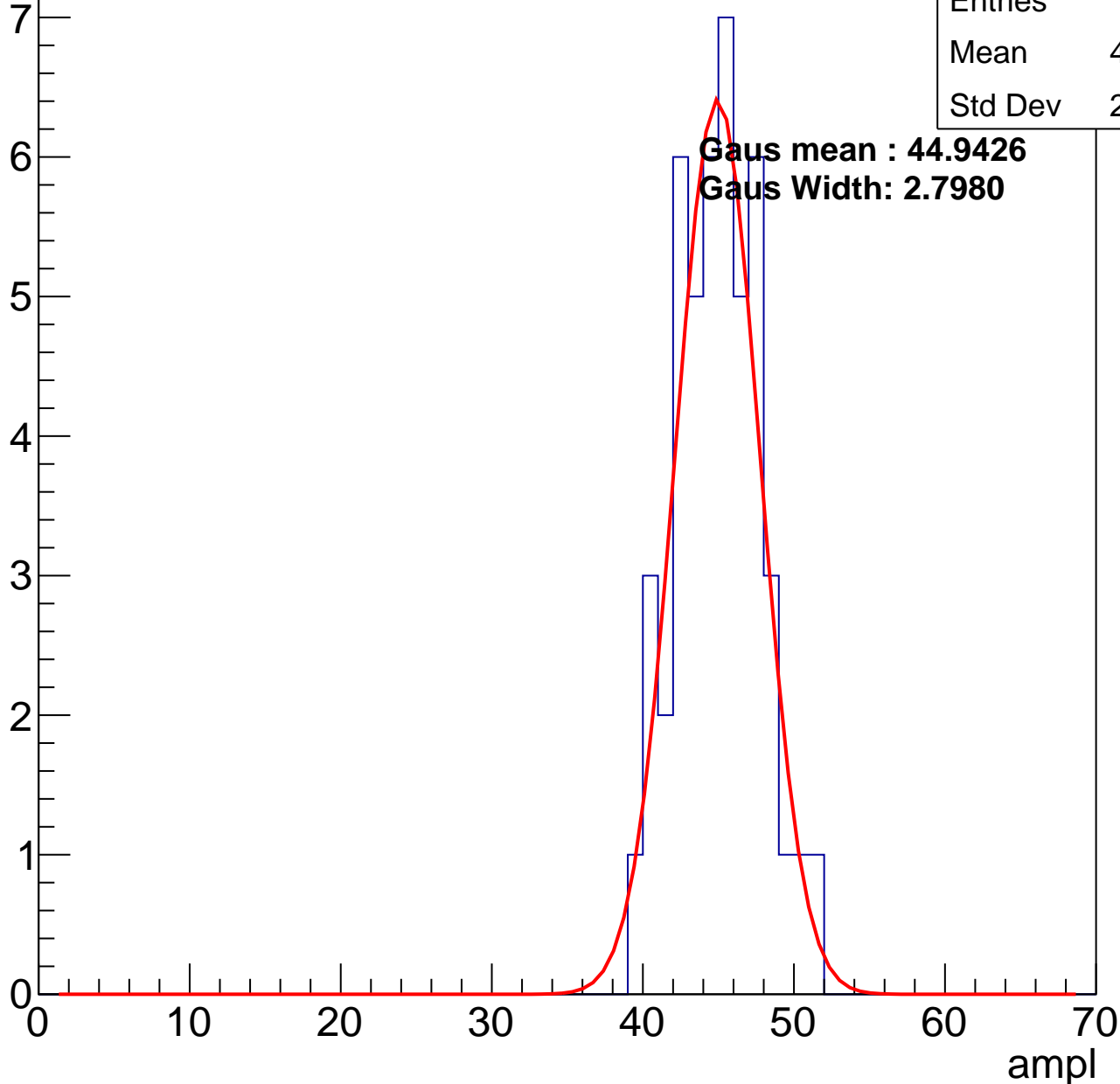
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	44.53
Std Dev	2.712

**Gaus mean : 44.9426**

**Gaus Width: 2.7980**

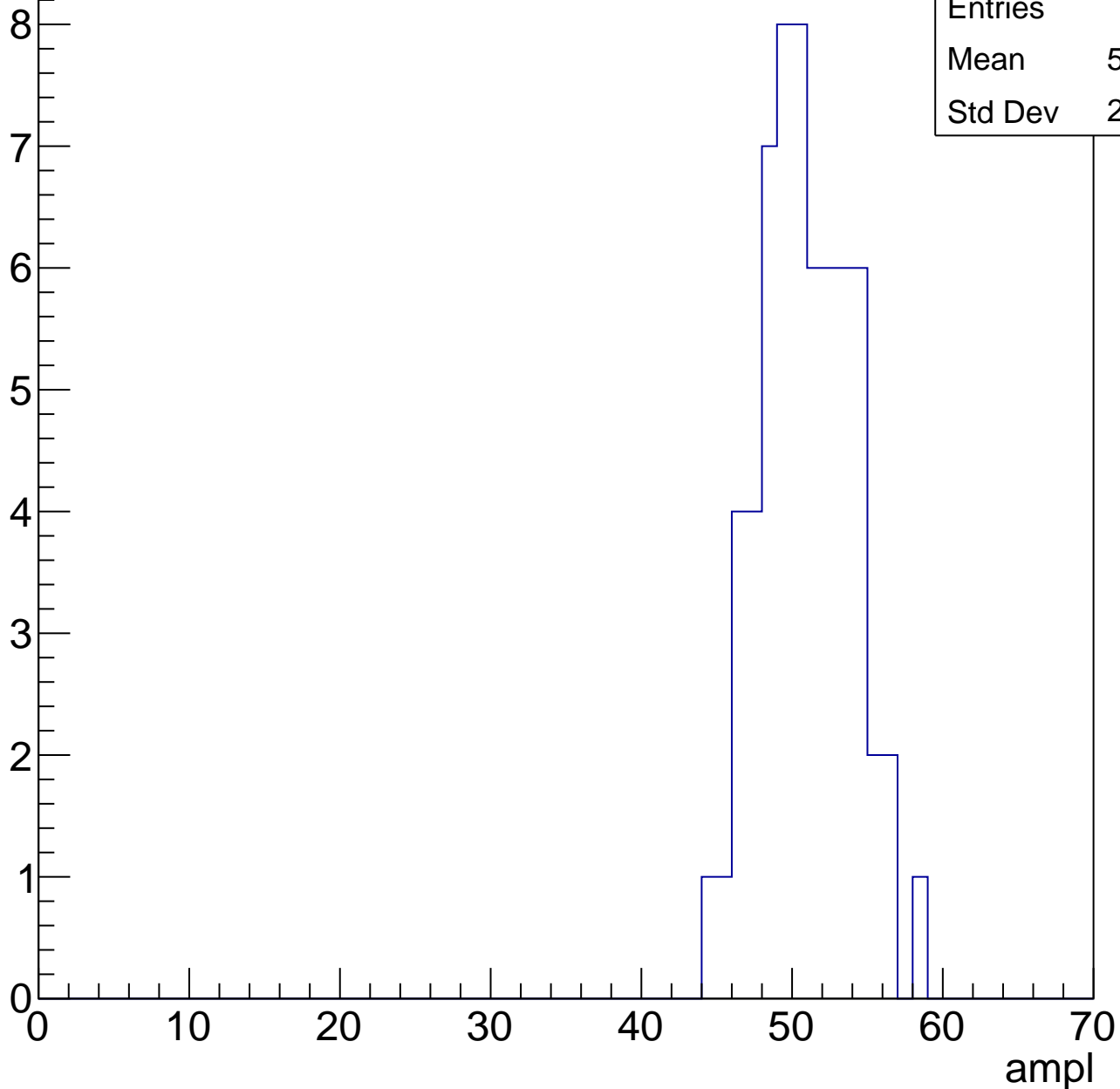


# B0L002S, U23-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	50.47
Std Dev	2.977

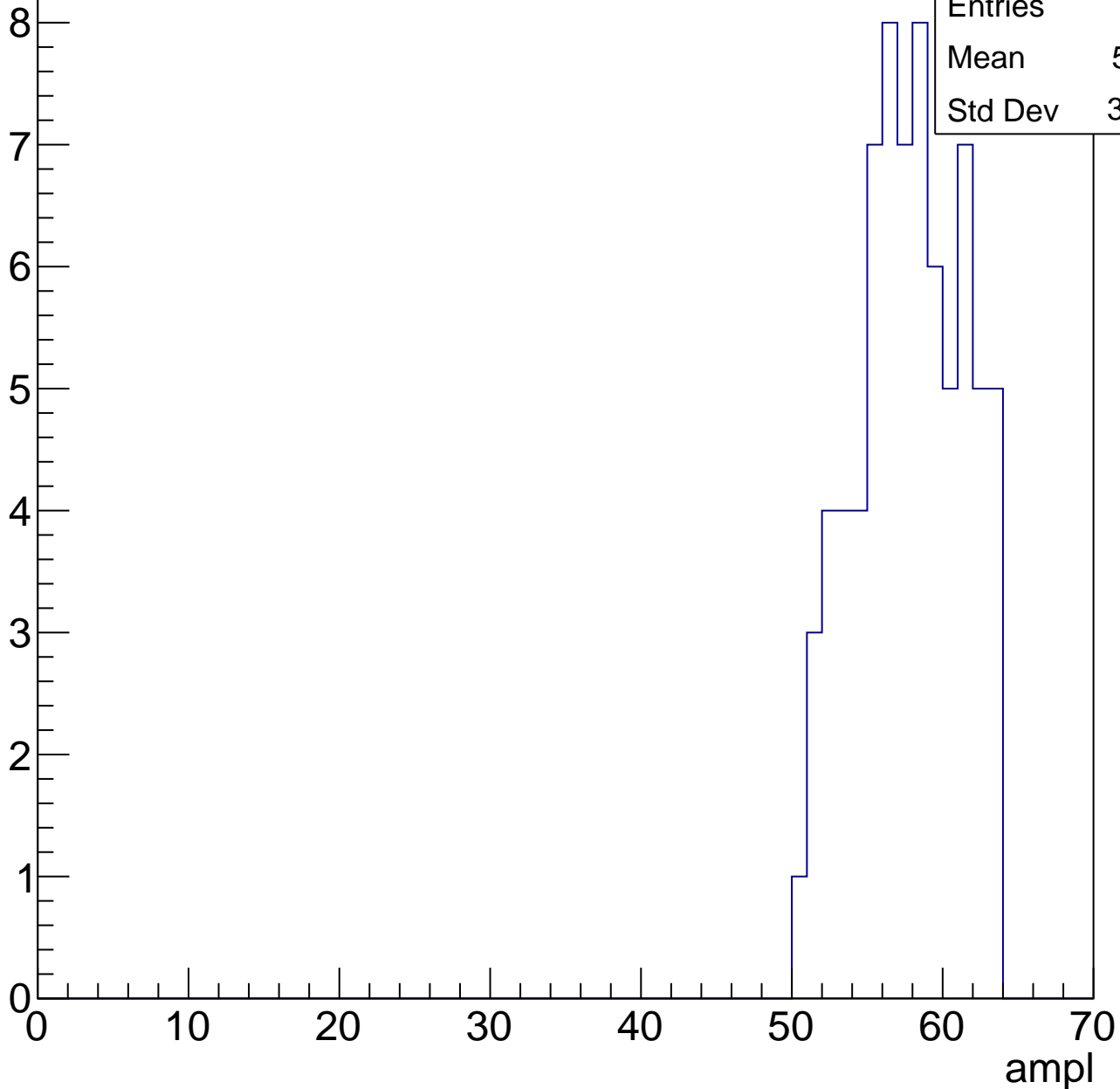


# B0L002S, U23-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	57.31
Std Dev	3.448

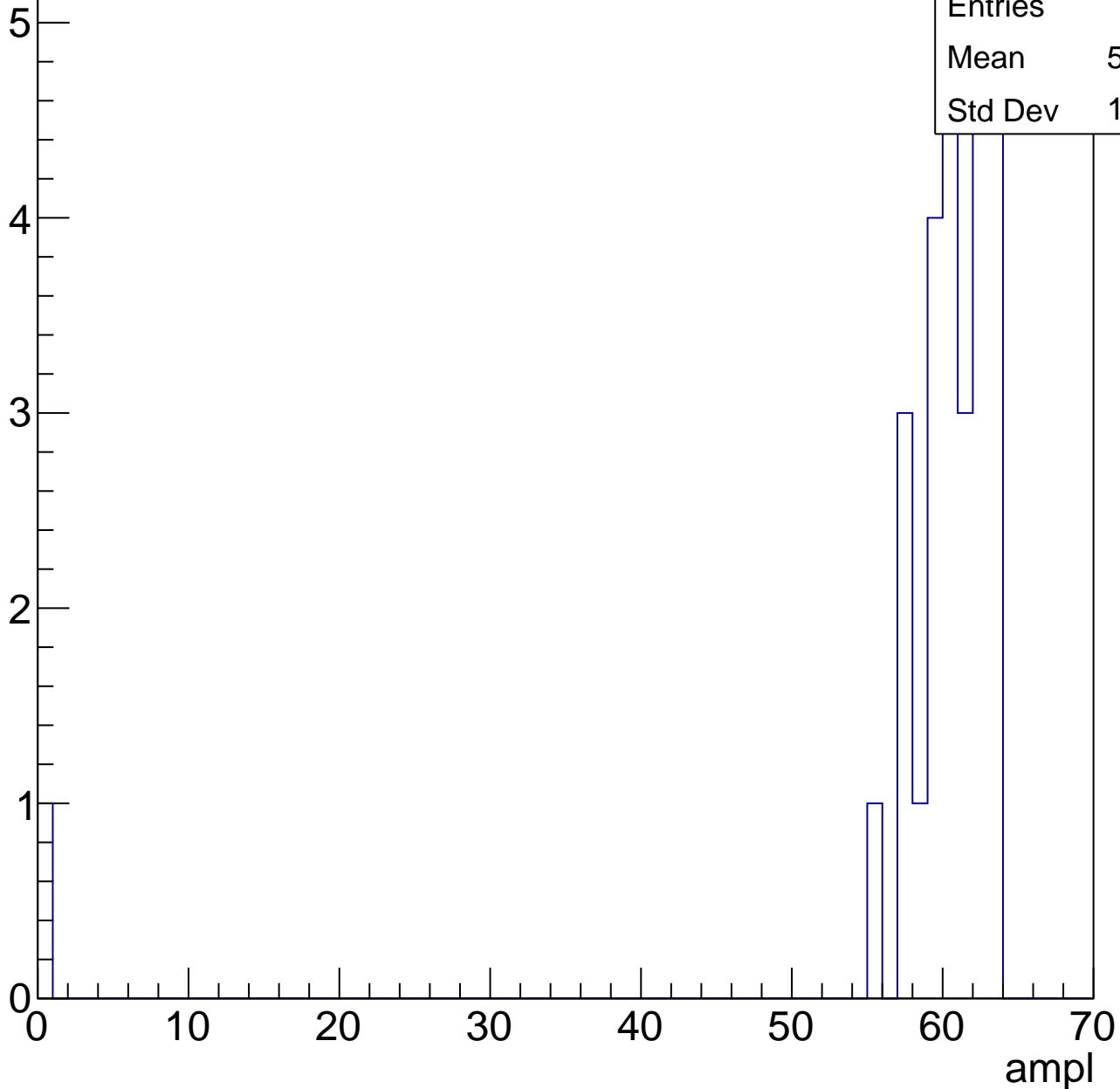


# B0L002S, U23-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.14
Std Dev	11.39



# B0L002S, U23-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch107, adc0

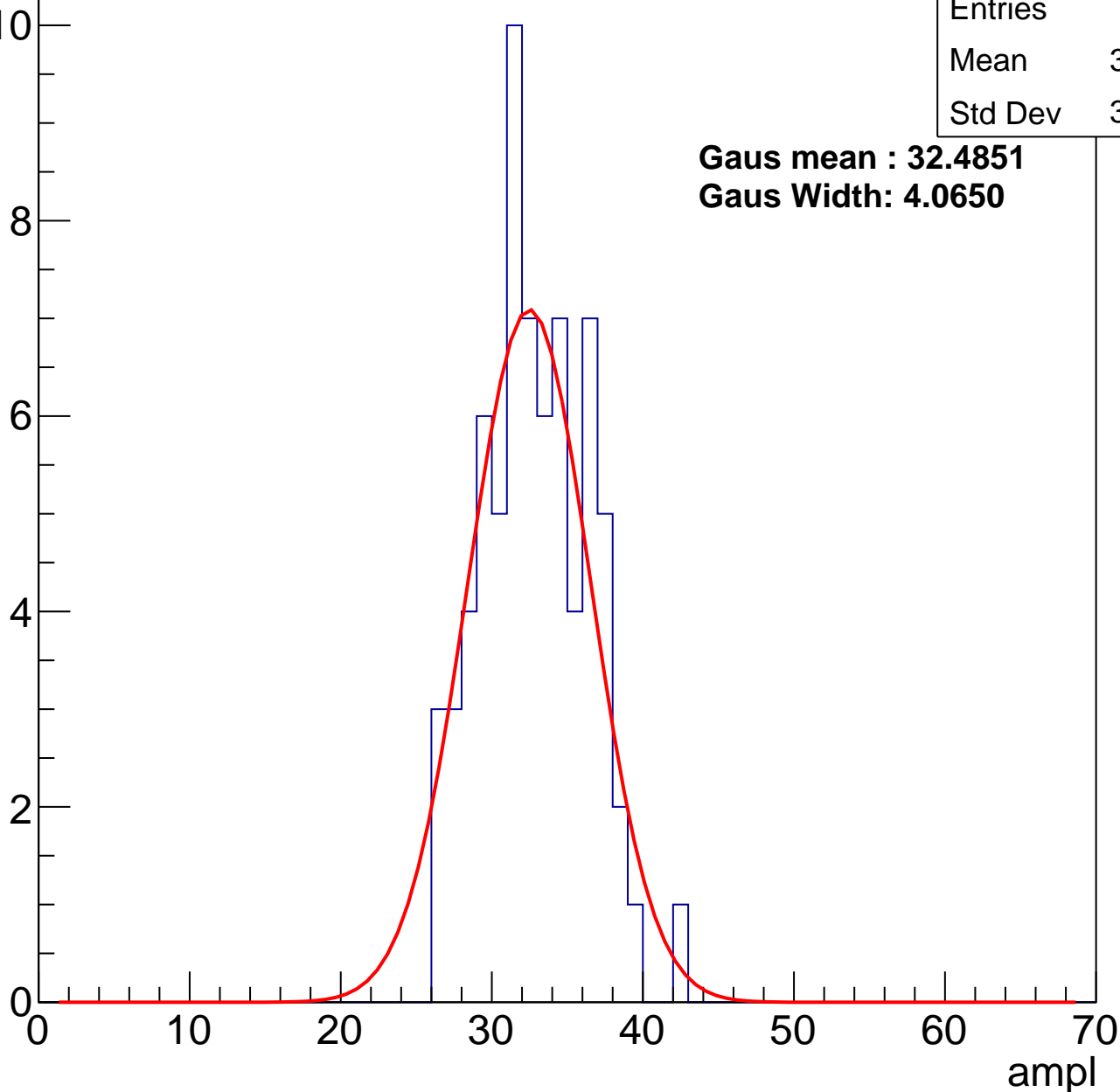
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	32.38
Std Dev	3.457

**Gaus mean : 32.4851**

**Gaus Width: 4.0650**



# B0L002S, U23-ch107, adc1

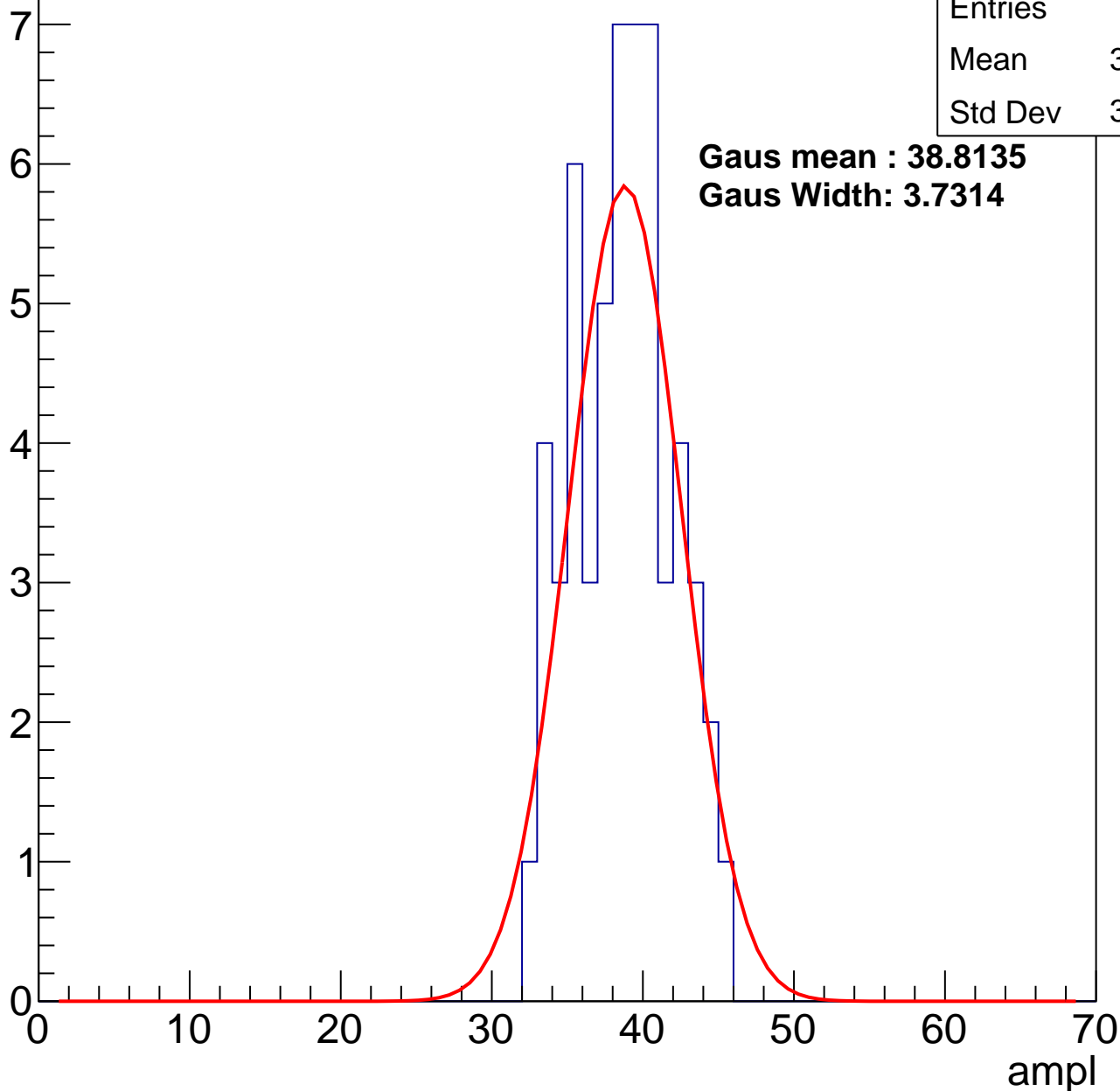
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	38.23
Std Dev	3.202

**Gaus mean : 38.8135**

**Gaus Width: 3.7314**



# B0L002S, U23-ch107, adc2

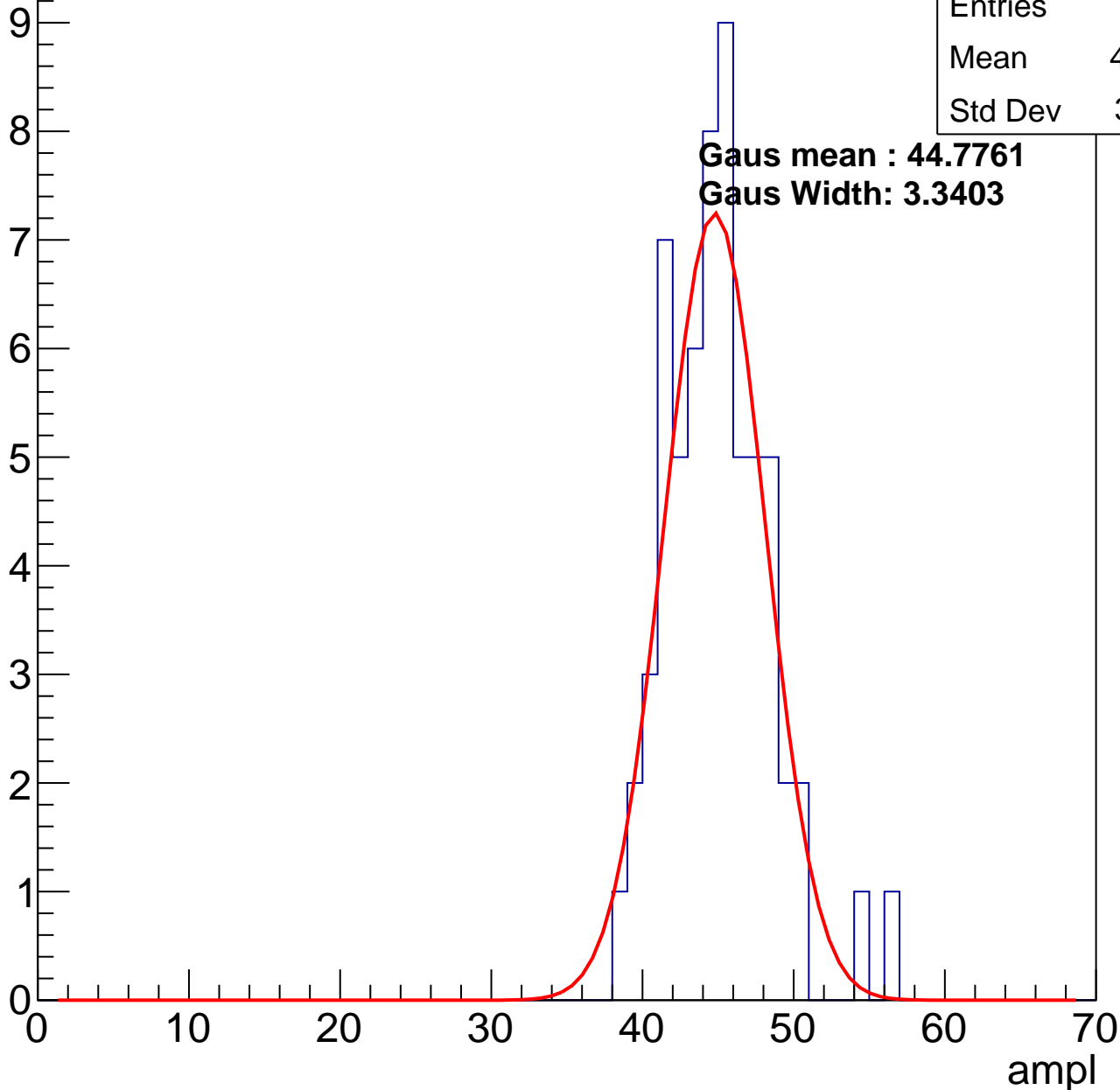
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.53
Std Dev	3.421

**Gaus mean : 44.7761**

**Gaus Width: 3.3403**

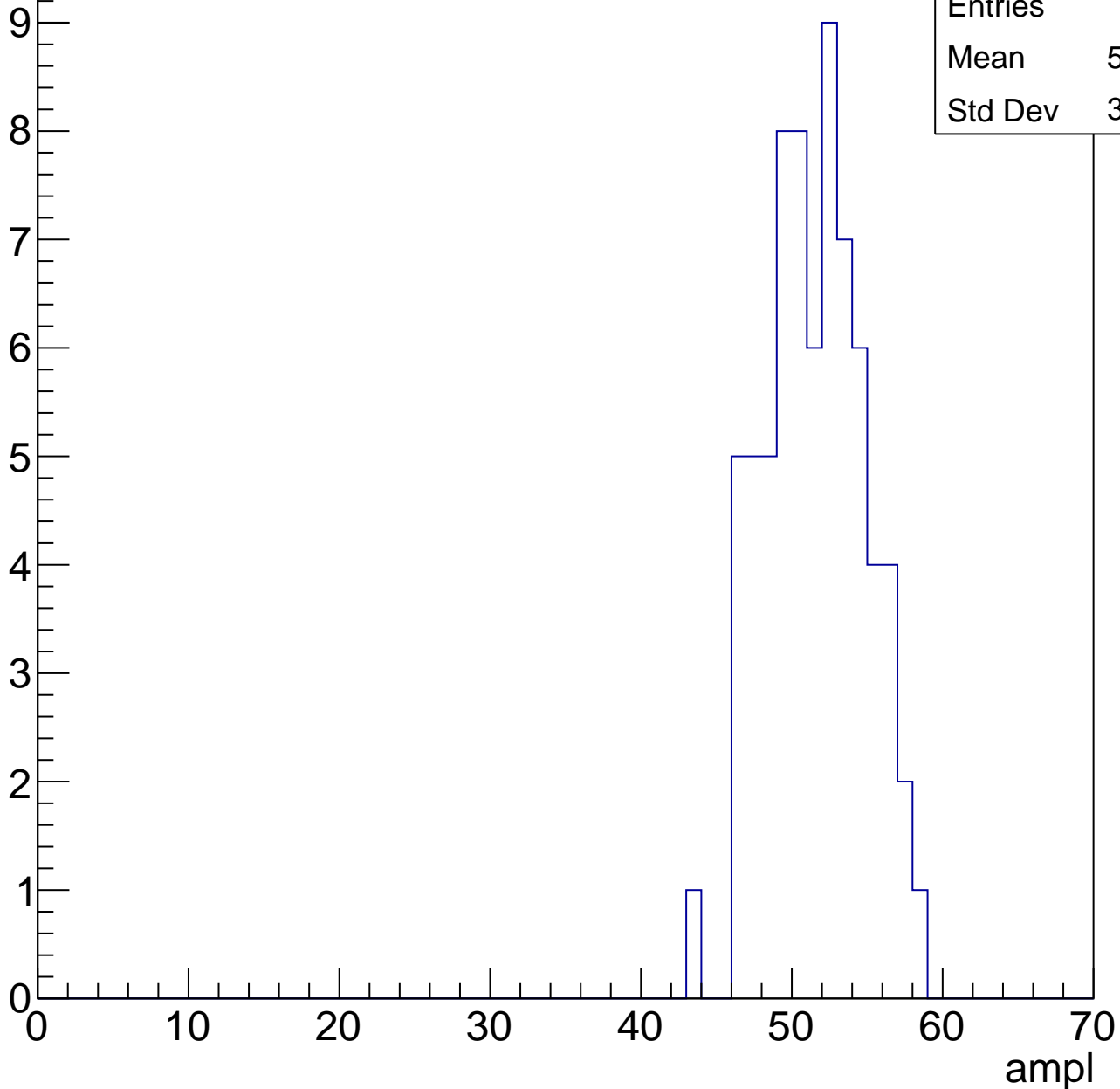


# B0L002S, U23-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	51.06
Std Dev	3.206

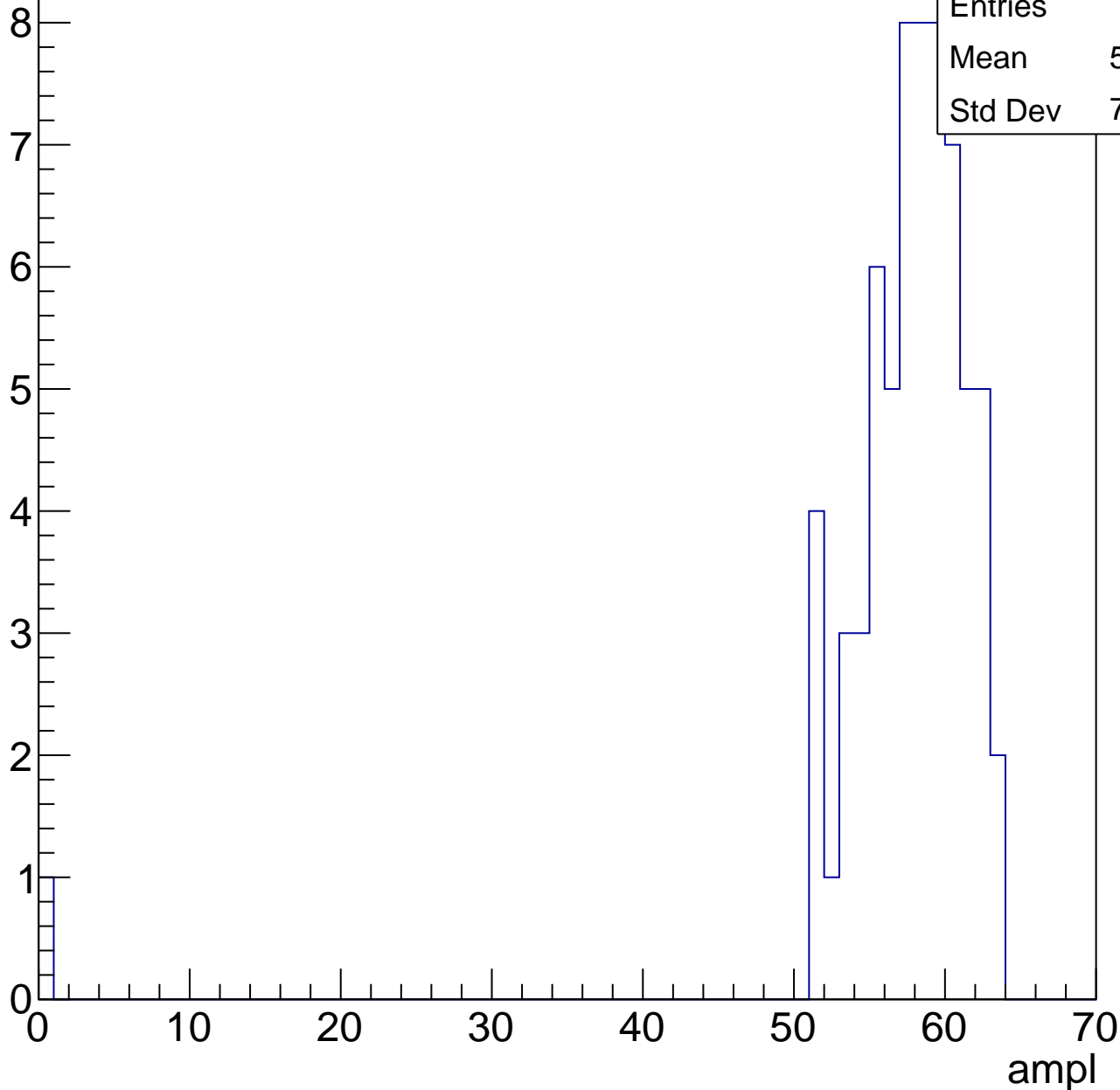


# B0L002S, U23-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

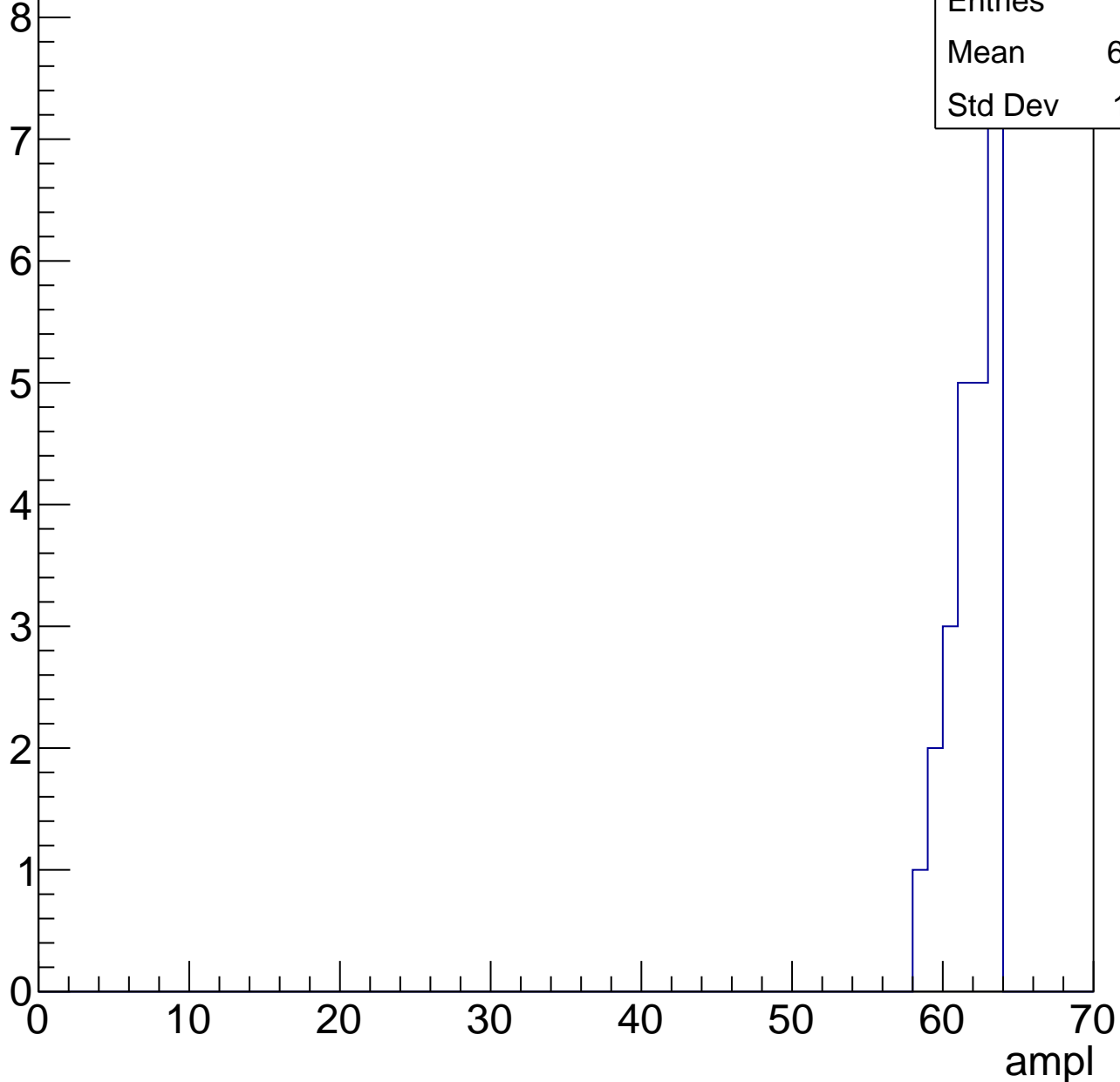
Entries	66
Mean	56.67
Std Dev	7.682



# B0L002S, U23-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	24
Mean	61.46
Std Dev	1.471

# B0L002S, U23-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch108, adc0

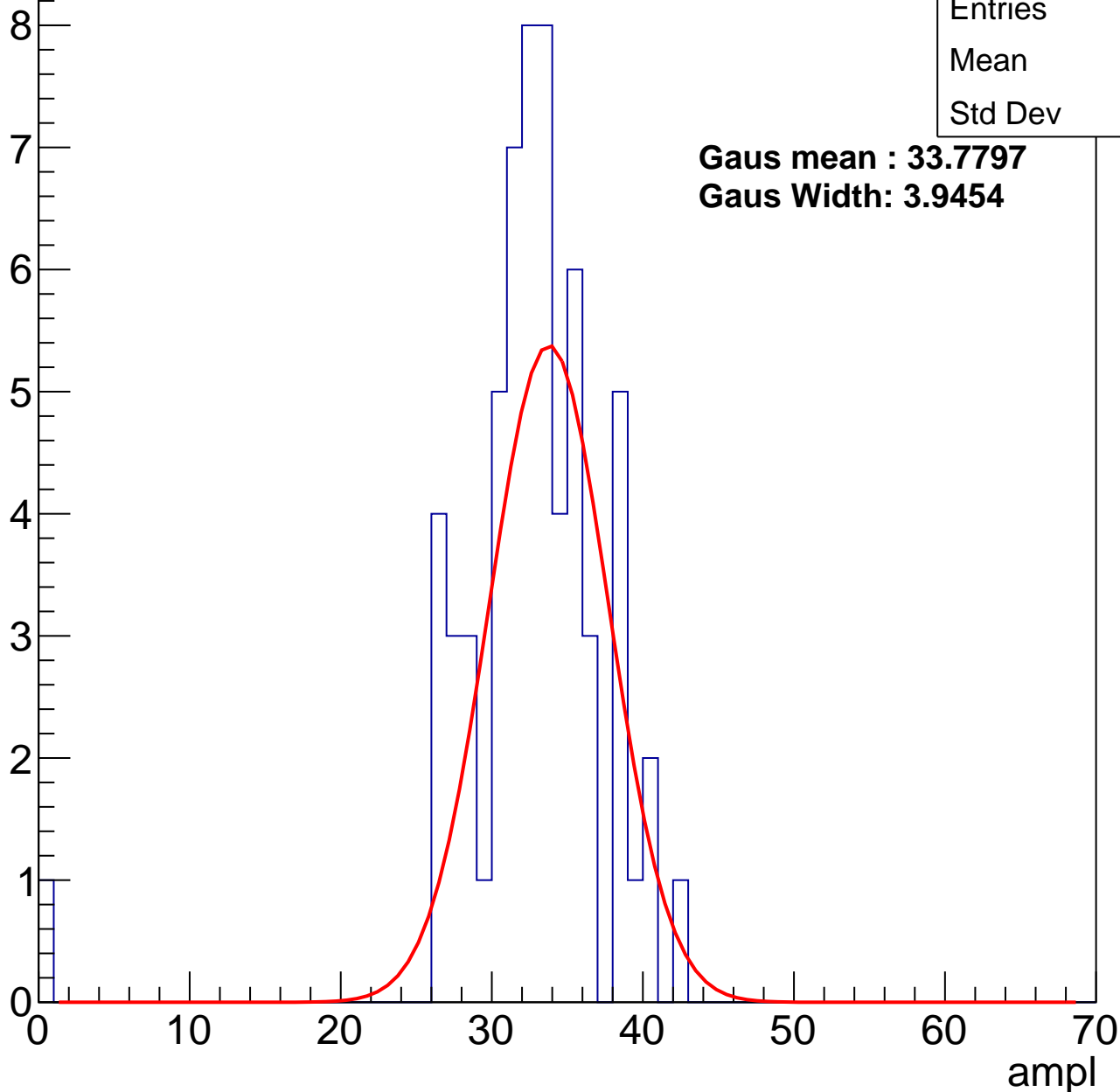
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	32.1
Std Dev	5.55

**Gaus mean : 33.7797**

**Gaus Width: 3.9454**



# B0L002S, U23-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	52
Mean	37.6
Std Dev	2.877

**Gaus mean : 37.8296**

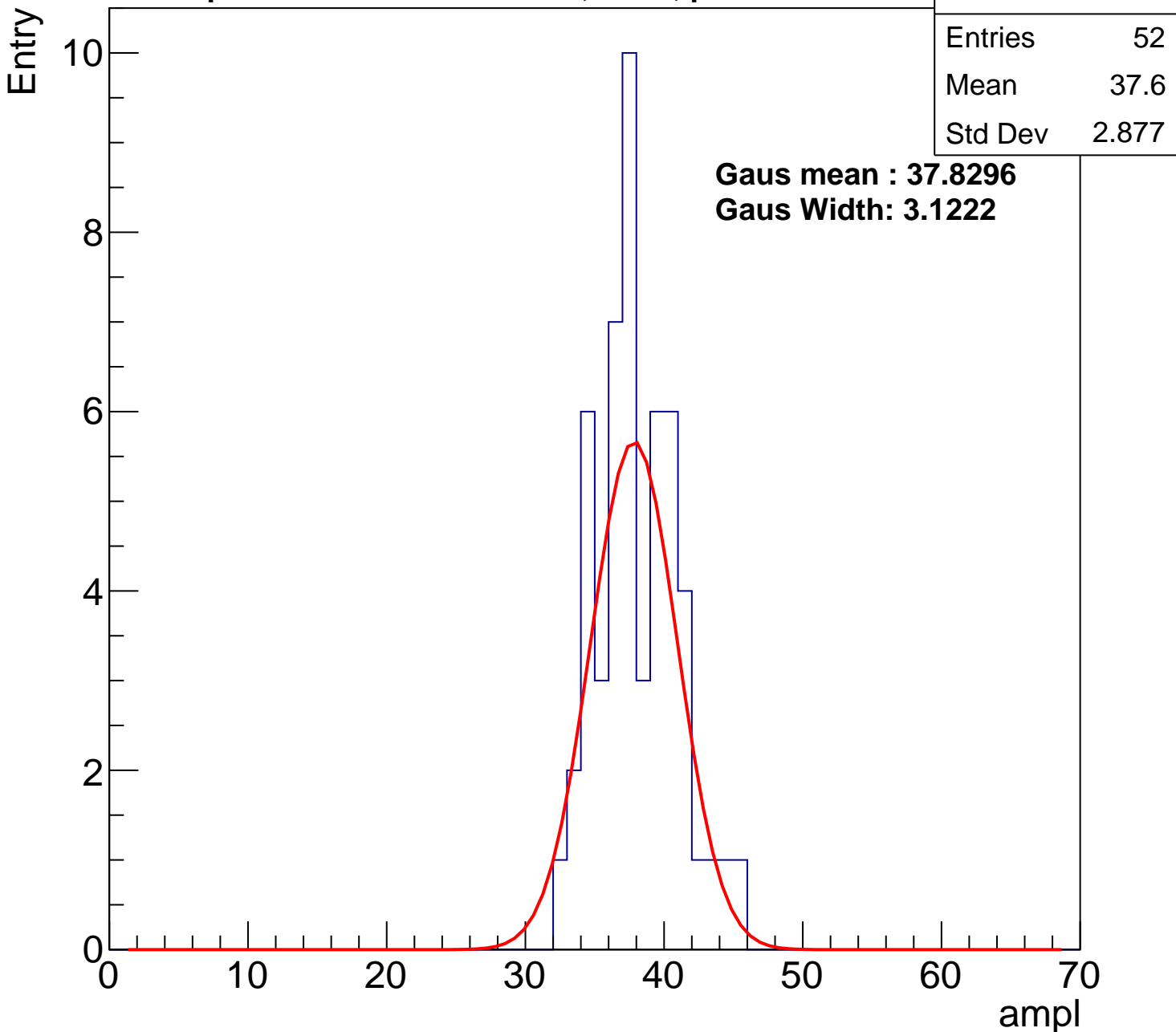
**Gaus Width: 3.1222**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch108, adc2

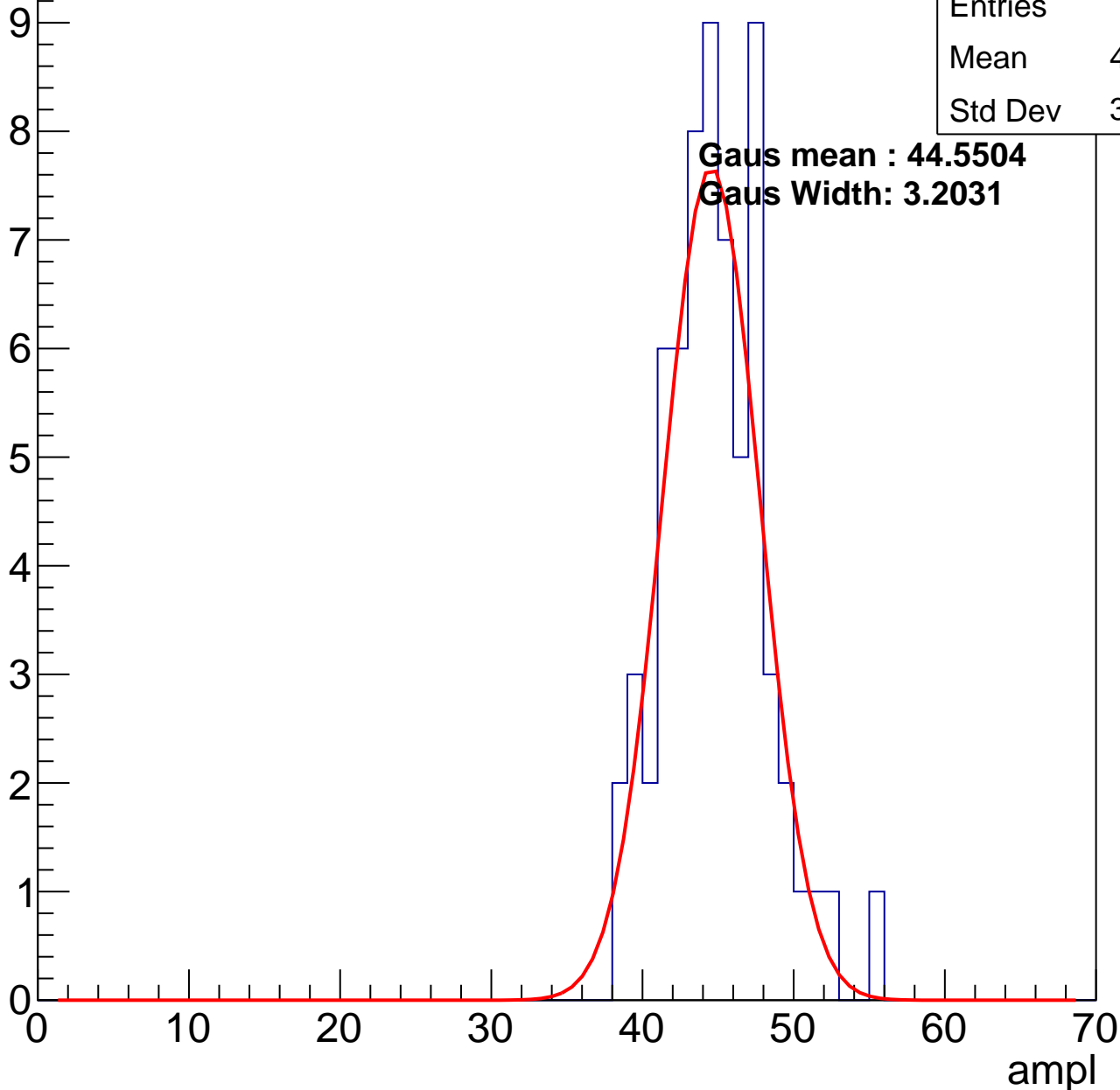
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.38
Std Dev	3.329

**Gaus mean : 44.5504**

**Gaus Width: 3.2031**

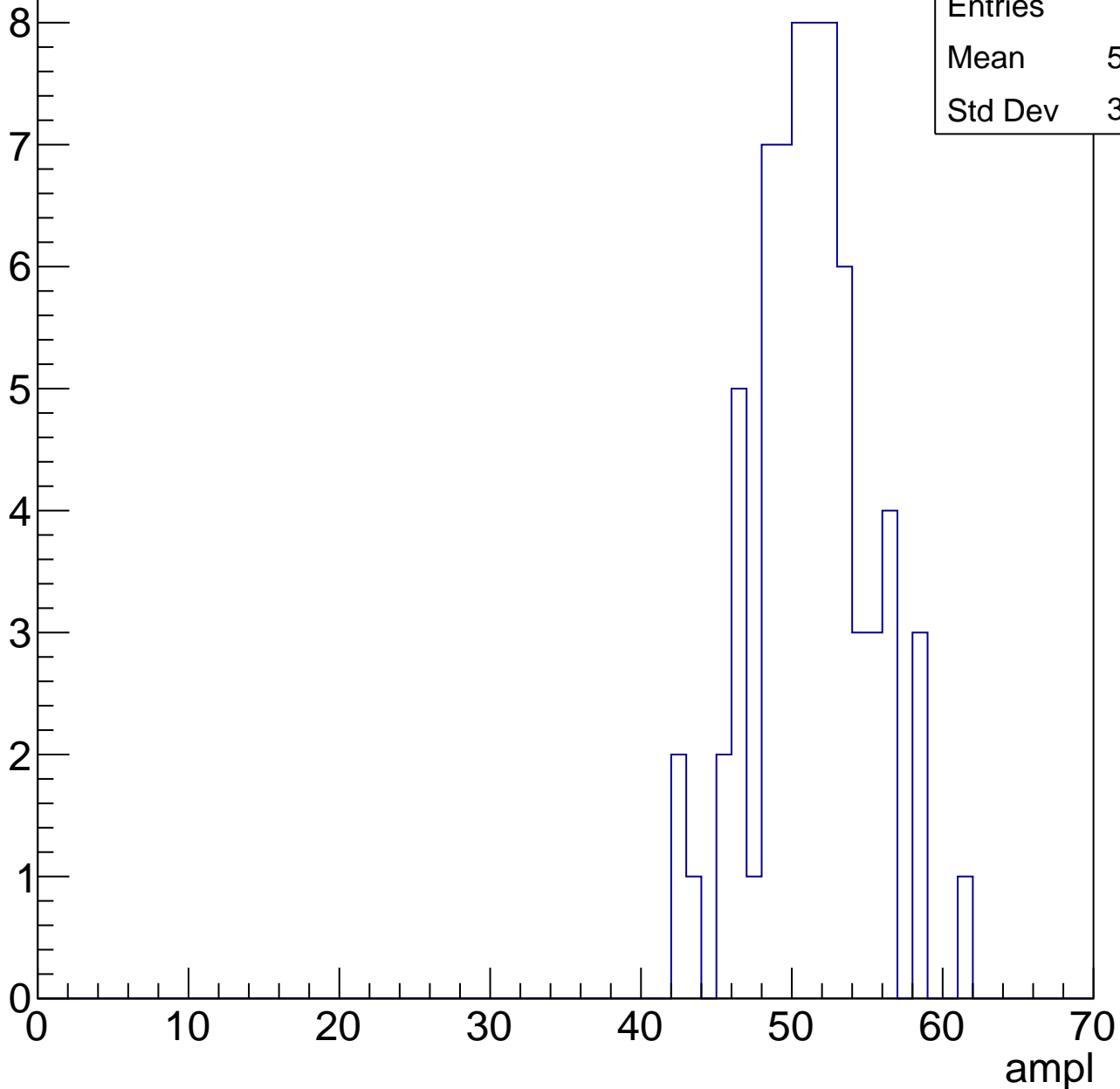


# B0L002S, U23-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

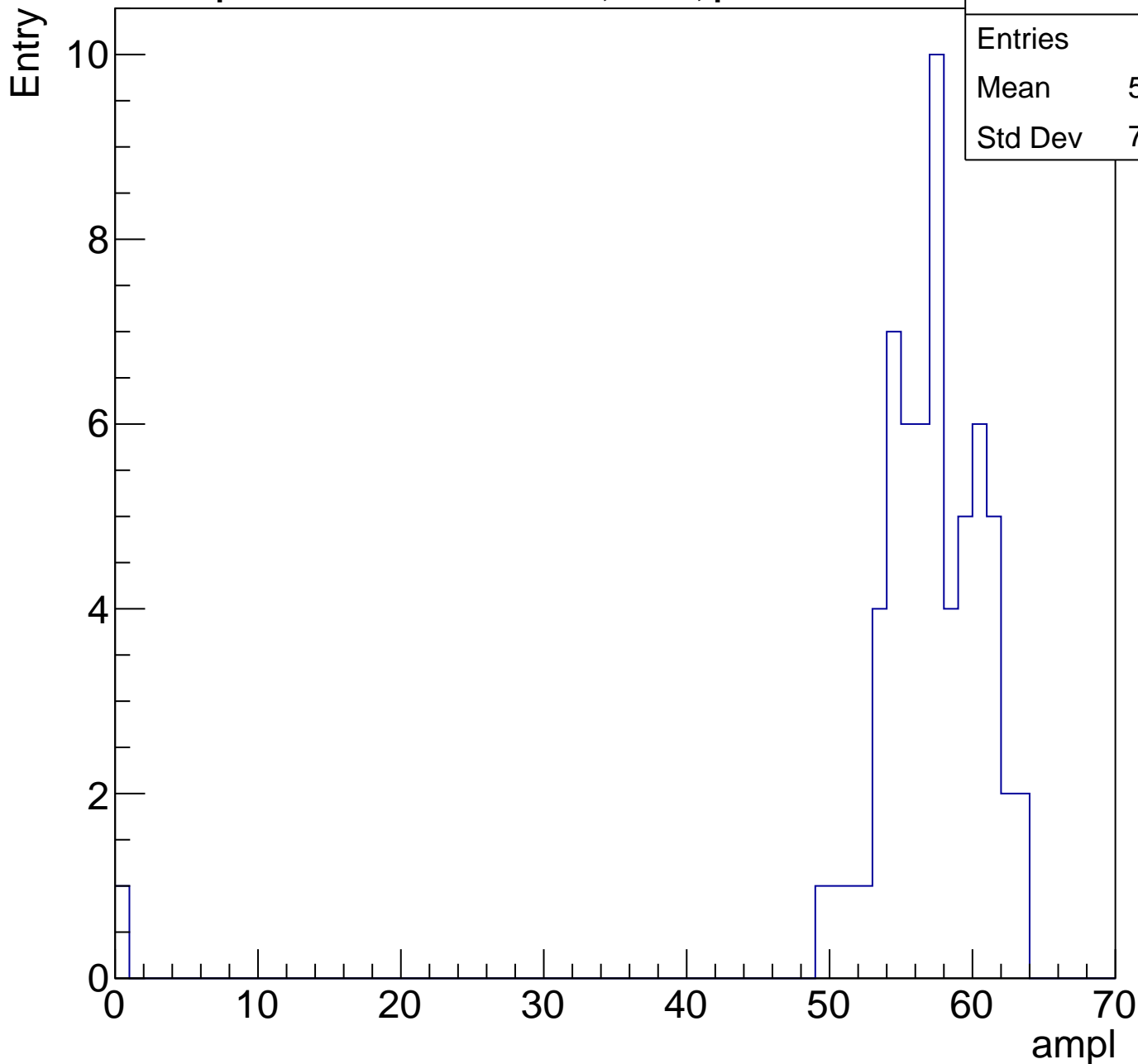
Entries	69
Mean	50.74
Std Dev	3.783



# B0L002S, U23-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	62
Mean	55.97
Std Dev	7.823



# B0L002S, U23-ch108, adc5

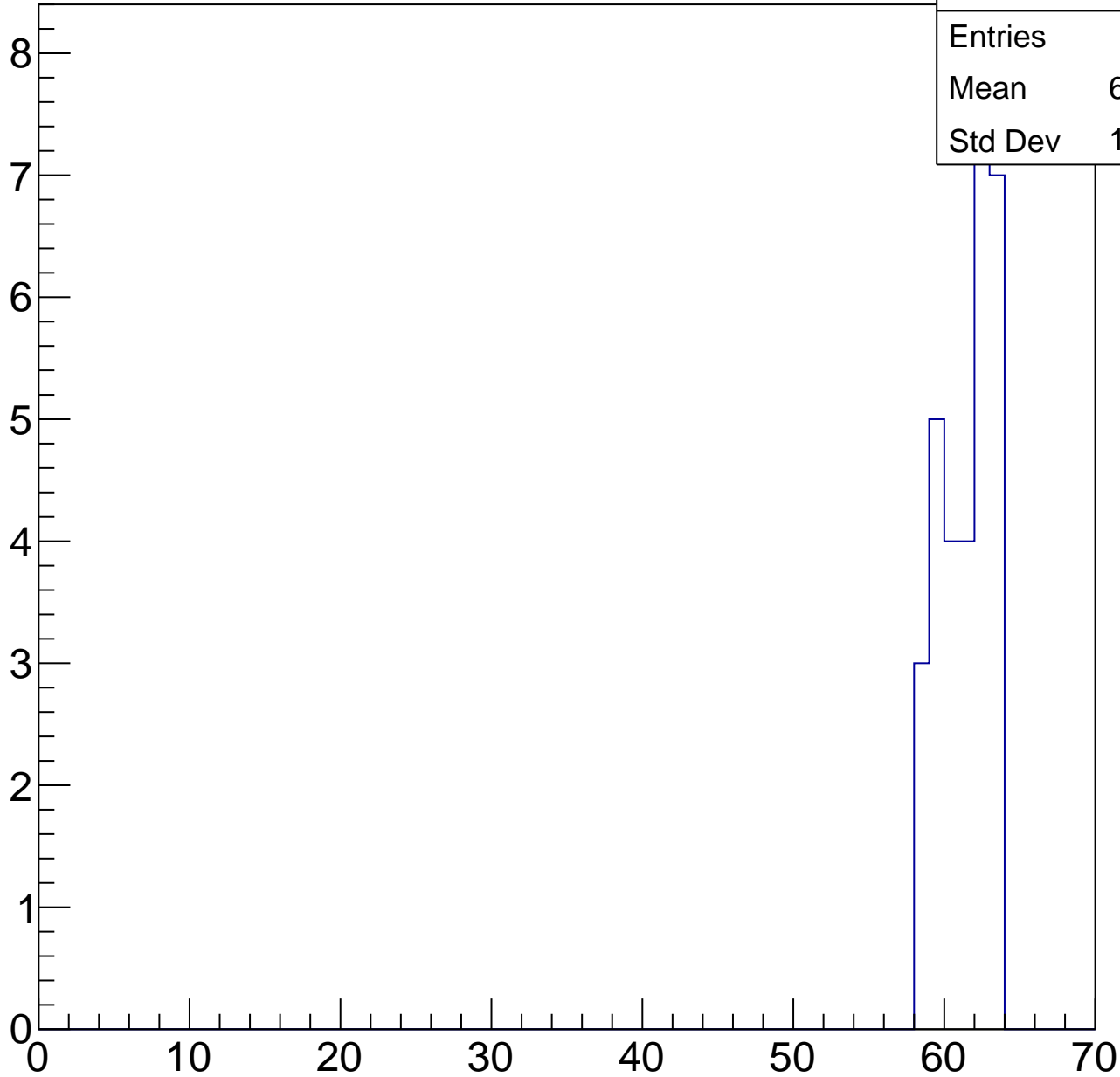
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	31
Mean	60.97
Std Dev	1.675

ampl



# B0L002S, U23-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L002S, U23-ch109, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	70
Mean	31.26
Std Dev	4.801

**Gaus mean : 32.1670**

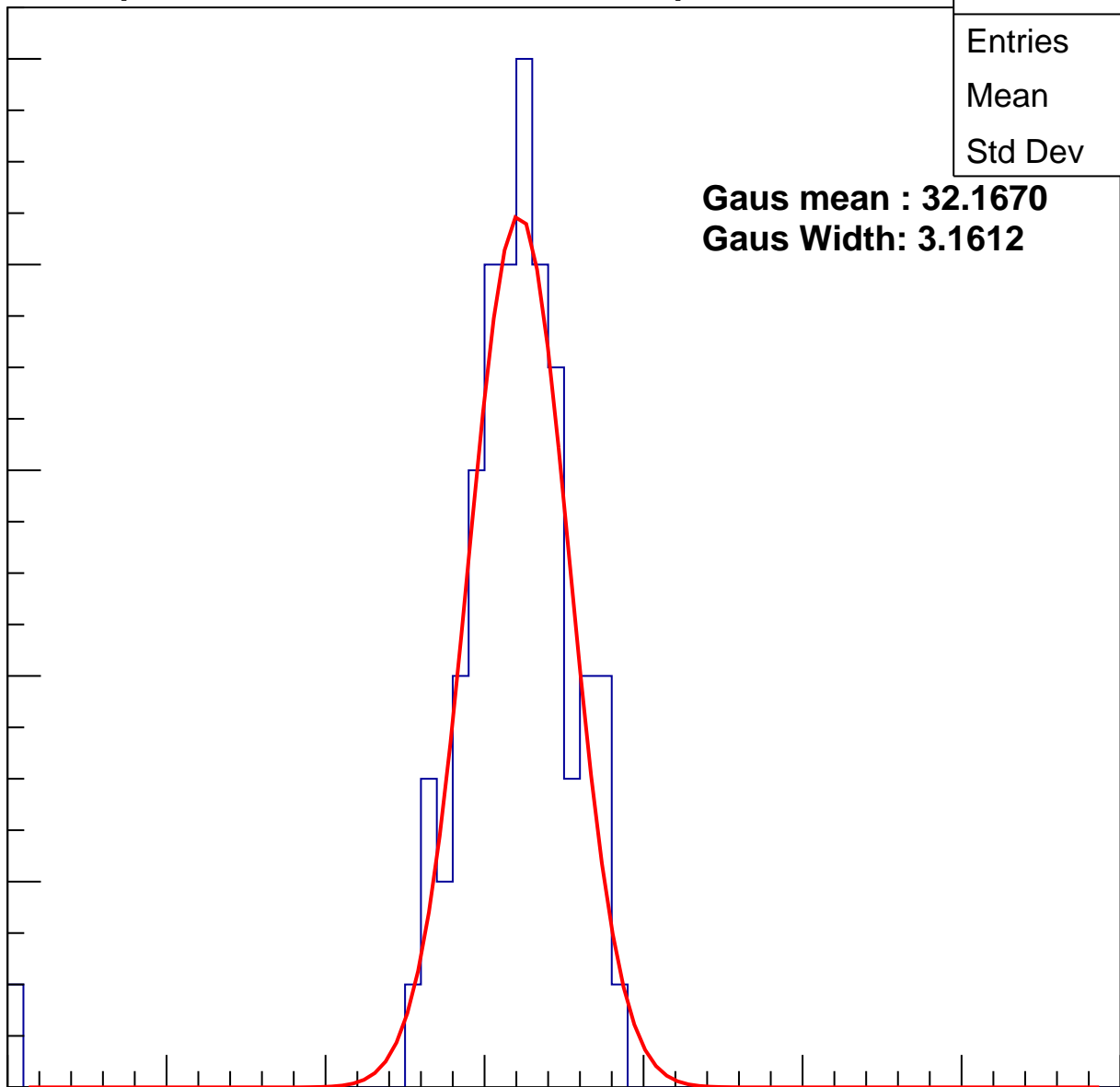
**Gaus Width: 3.1612**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch109, adc1

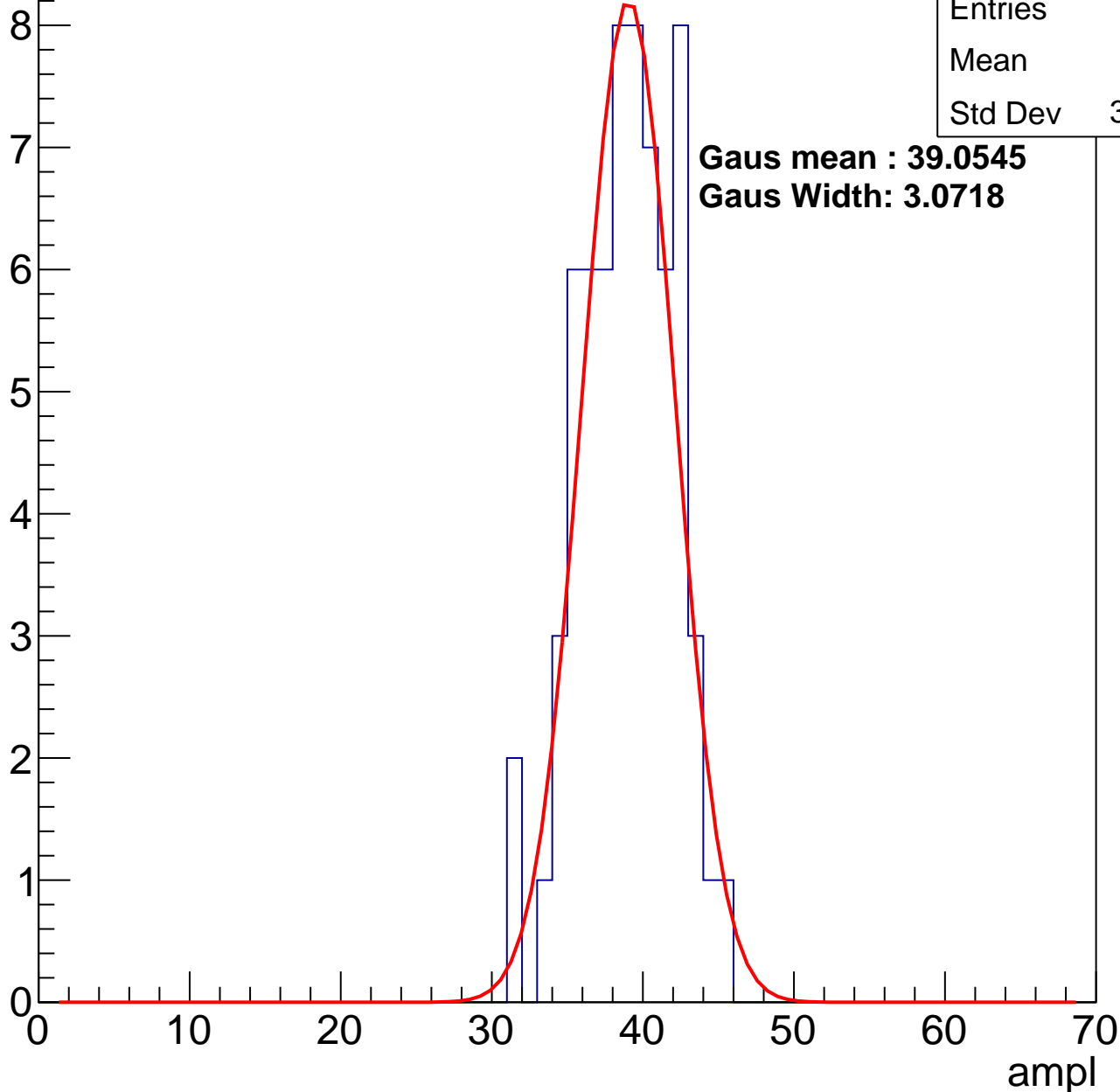
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.5
Std Dev	3.056

**Gaus mean : 39.0545**

**Gaus Width: 3.0718**



# B0L002S, U23-ch109, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	45.35
Std Dev	3.561

**Gaus mean : 46.1928**

**Gaus Width: 3.7029**

10

8

6

4

2

0

ampl

0

10

20

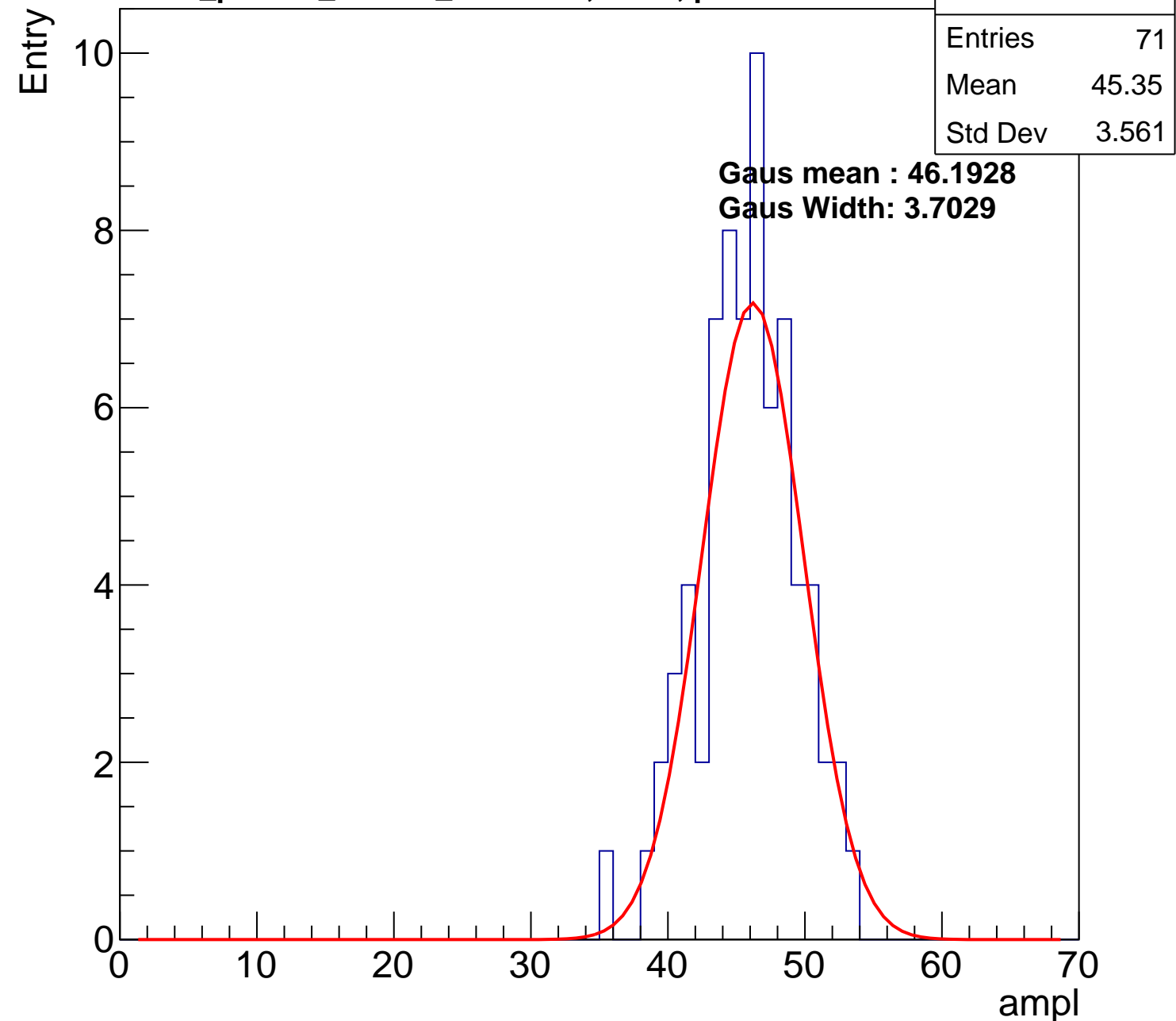
30

40

50

60

70

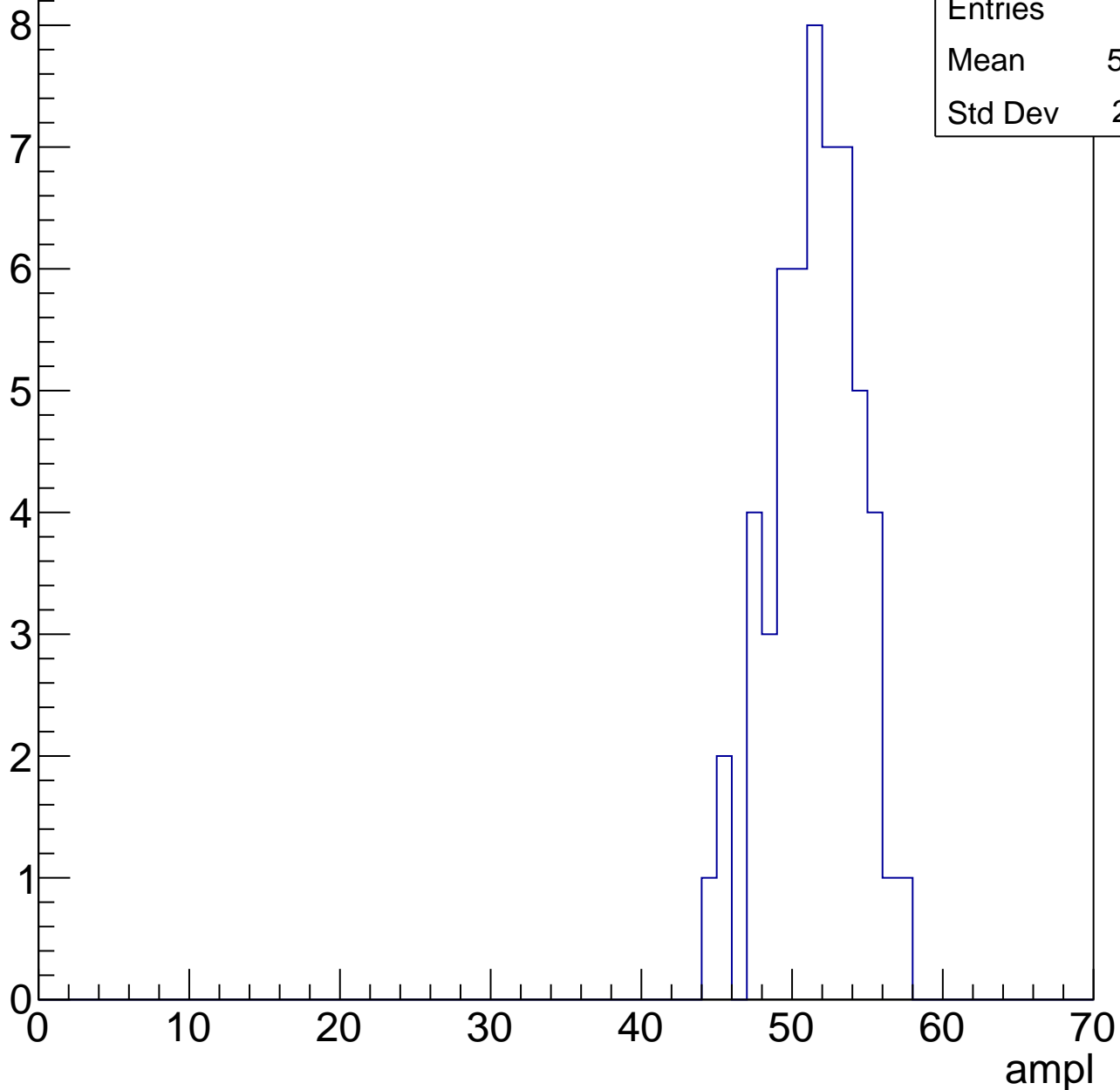


# B0L002S, U23-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

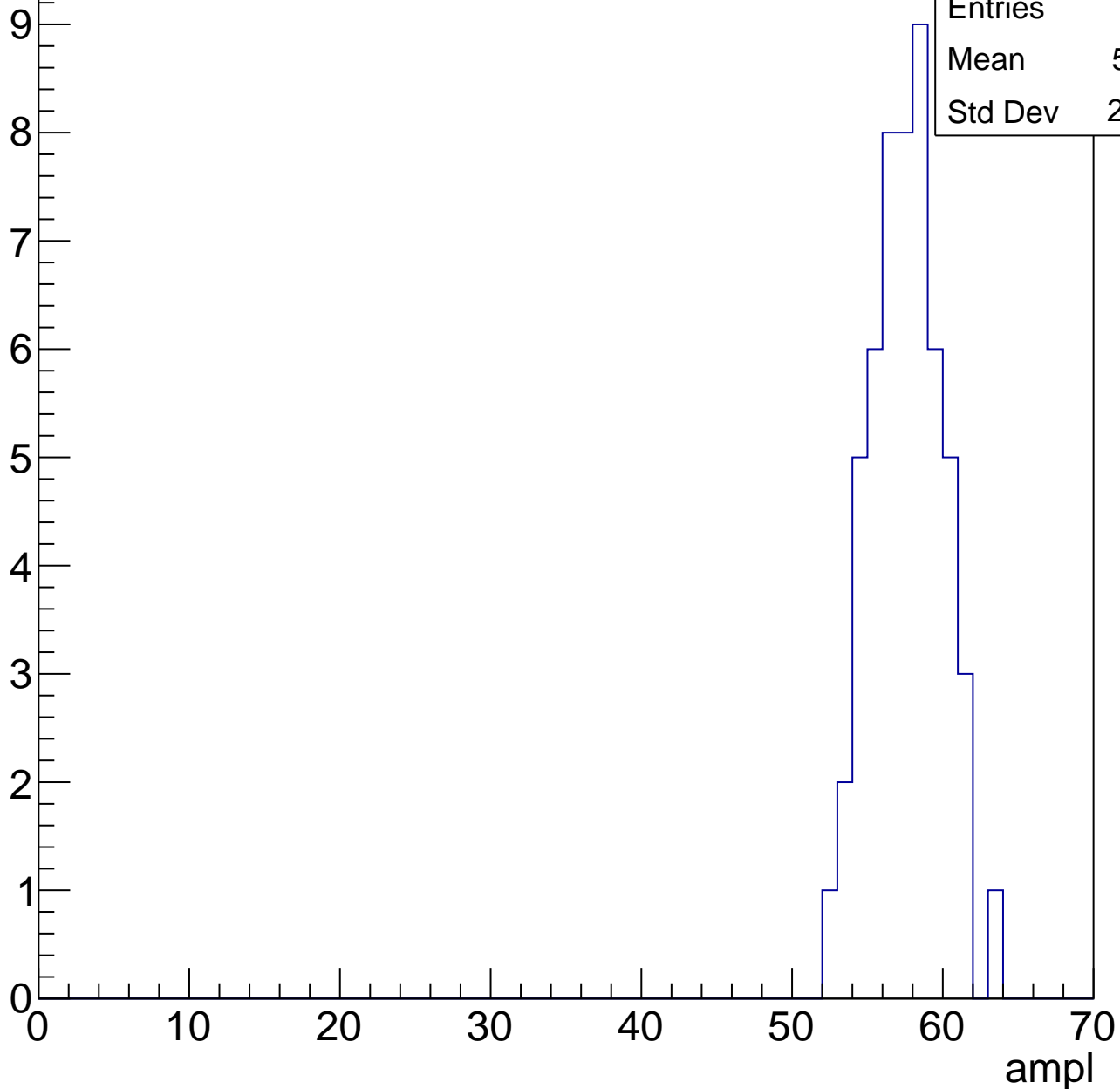
Entries	55
Mean	51.02
Std Dev	2.851



# B0L002S, U23-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



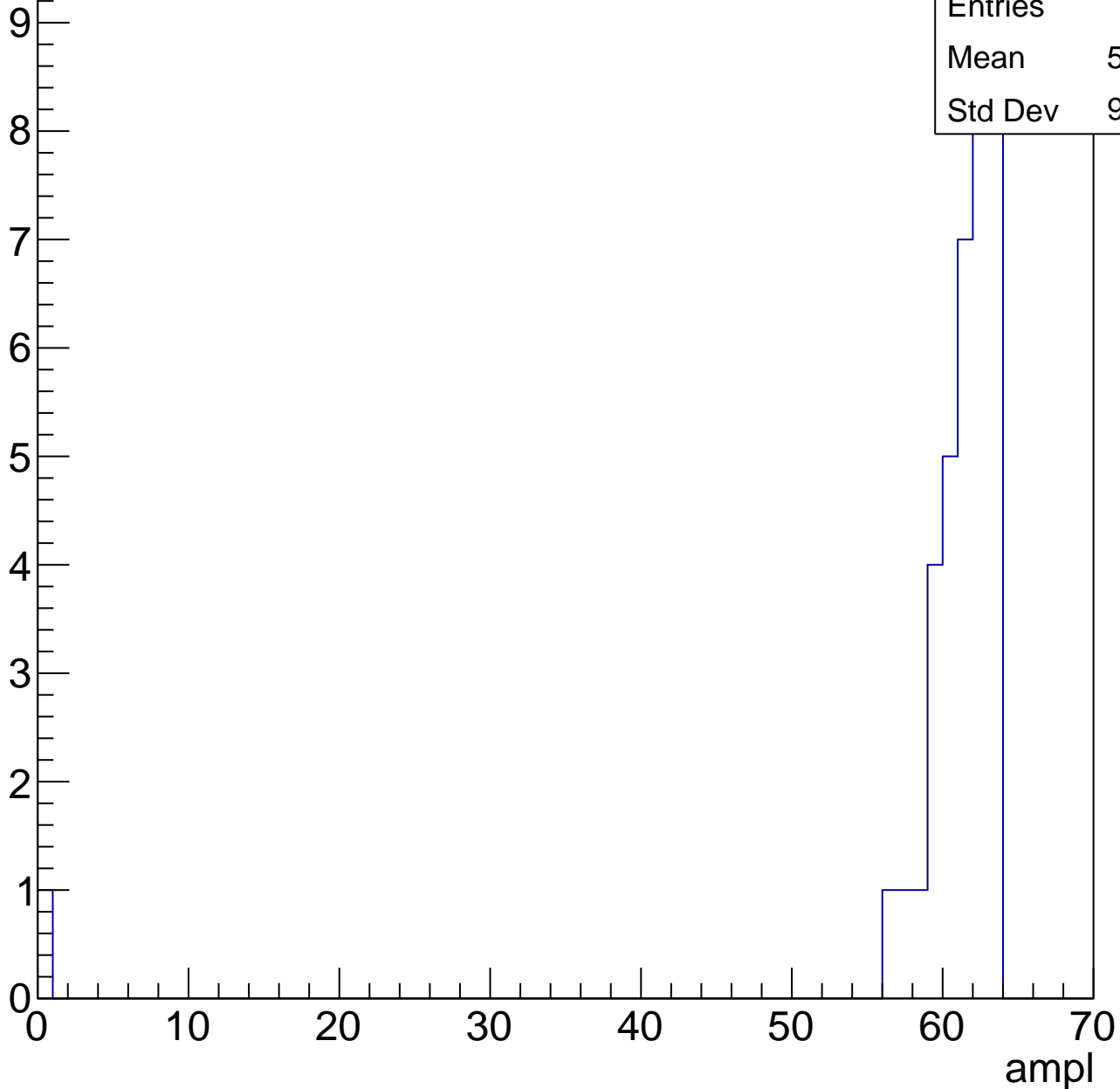
Entries	54
Mean	57.11
Std Dev	2.339

# B0L002S, U23-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.45
Std Dev	9.928



# B0L002S, U23-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

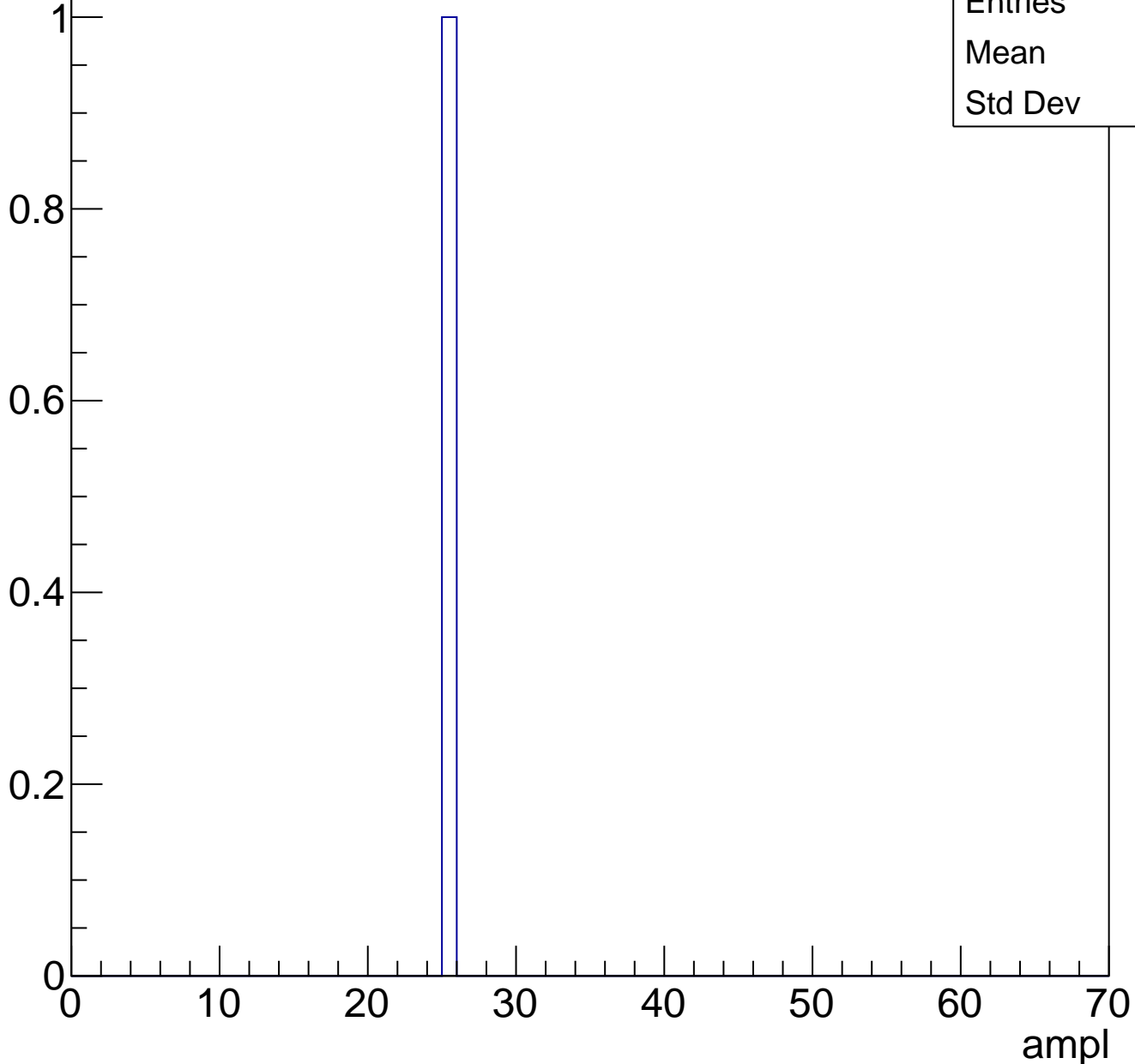




# B0L002S, U23-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L002S, U23-ch110, adc0

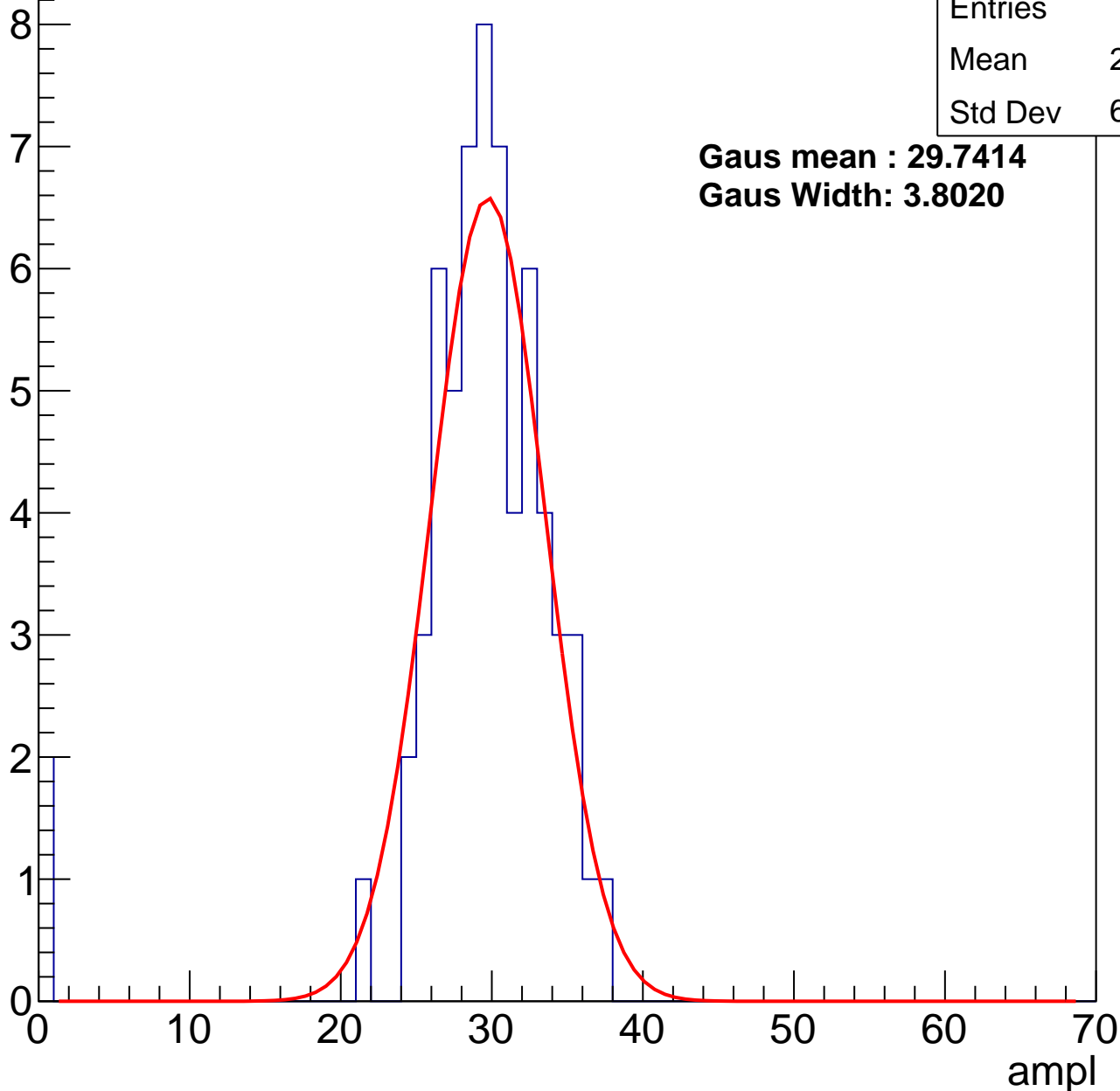
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	28.59
Std Dev	6.109

**Gaus mean : 29.7414**

**Gaus Width: 3.8020**



# B0L002S, U23-ch110, adc1

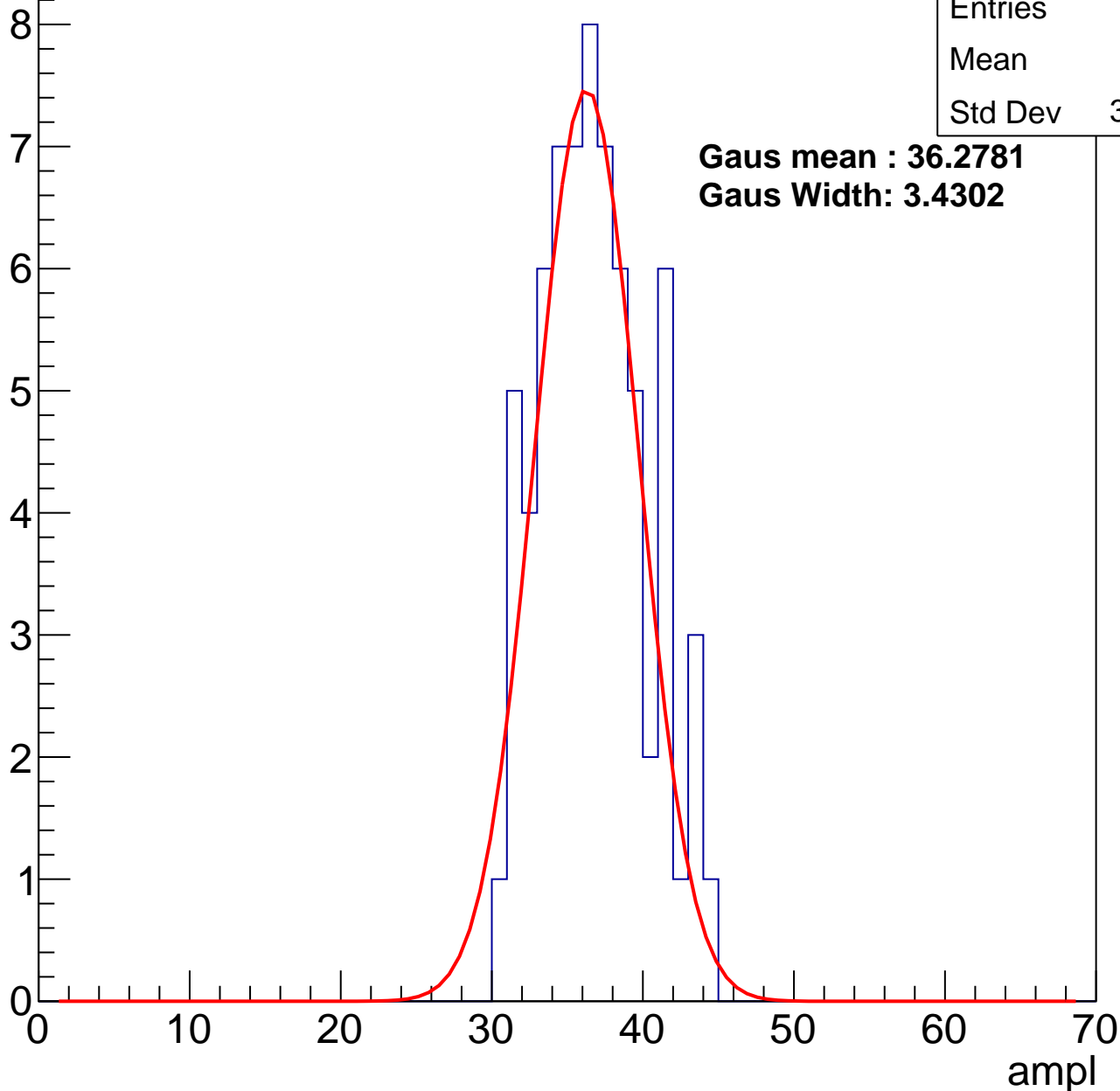
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.3
Std Dev	3.432

**Gaus mean : 36.2781**

**Gaus Width: 3.4302**



# B0L002S, U23-ch110, adc2

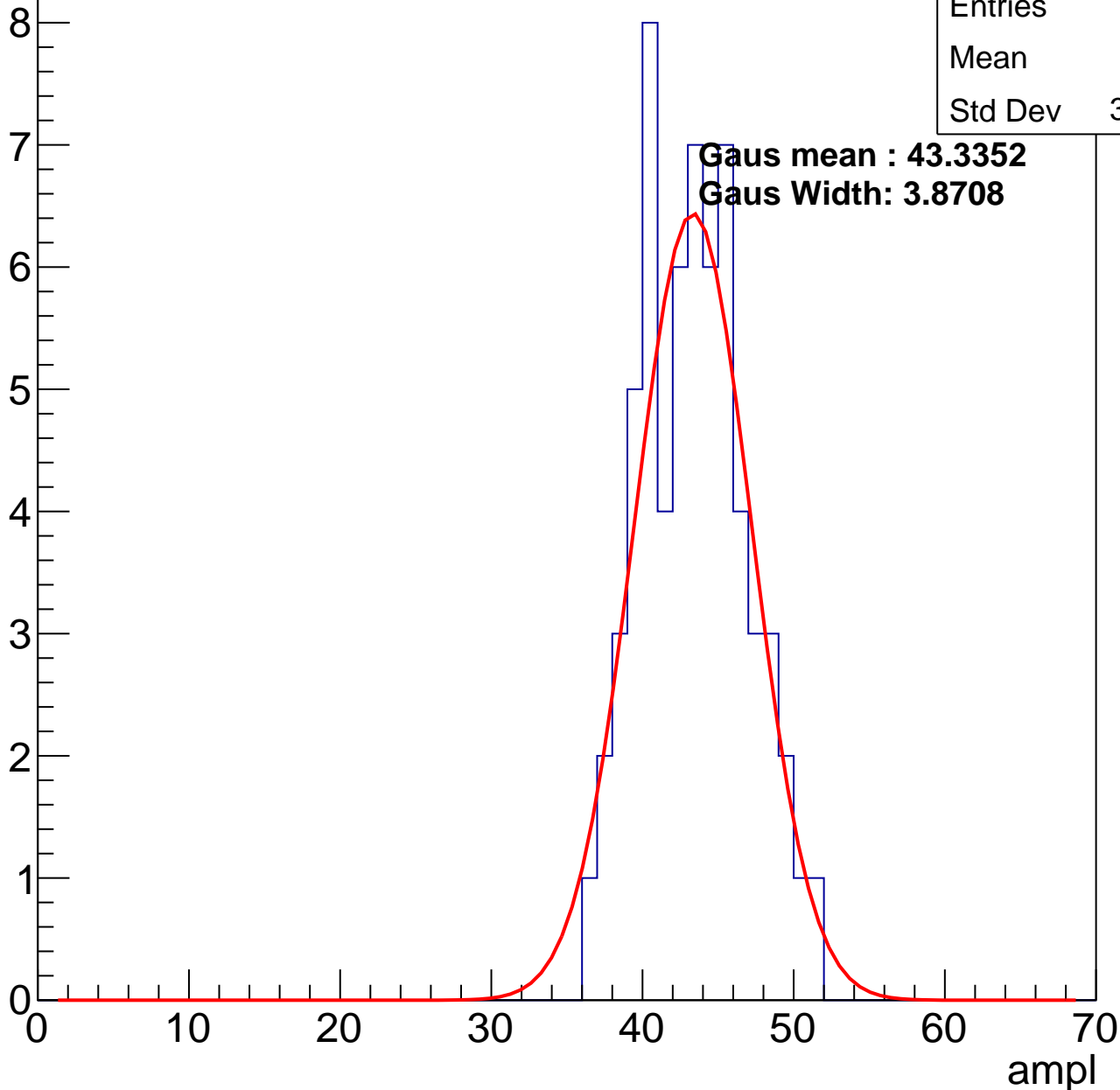
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.9
Std Dev	3.444

**Gaus mean : 43.3352**

**Gaus Width: 3.8708**

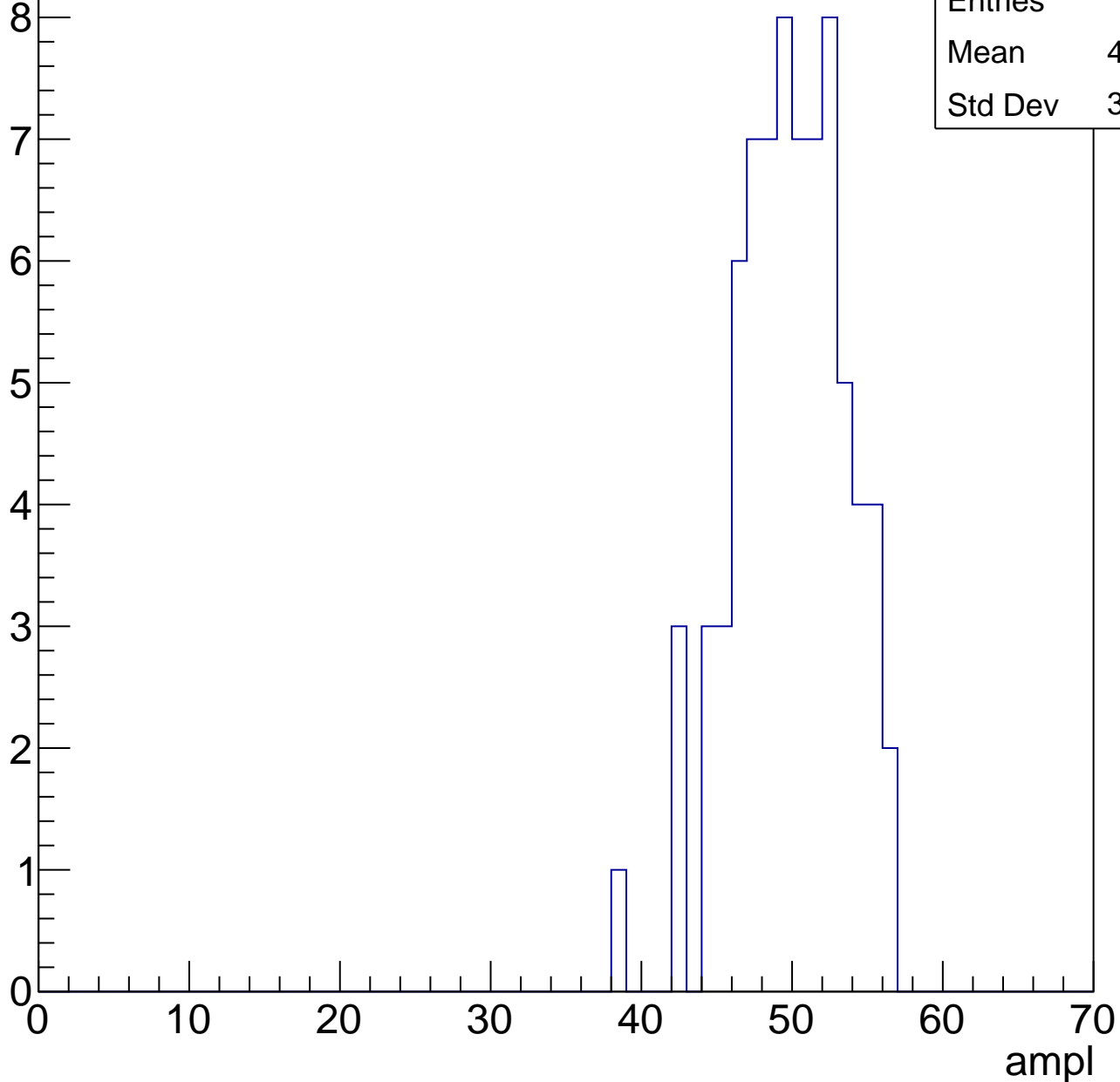


# B0L002S, U23-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	49.33
Std Dev	3.653



# B0L002S, U23-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	47
Mean	55.94
Std Dev	3.021

Entry

10

8

6

4

2

0

0

10

20

30

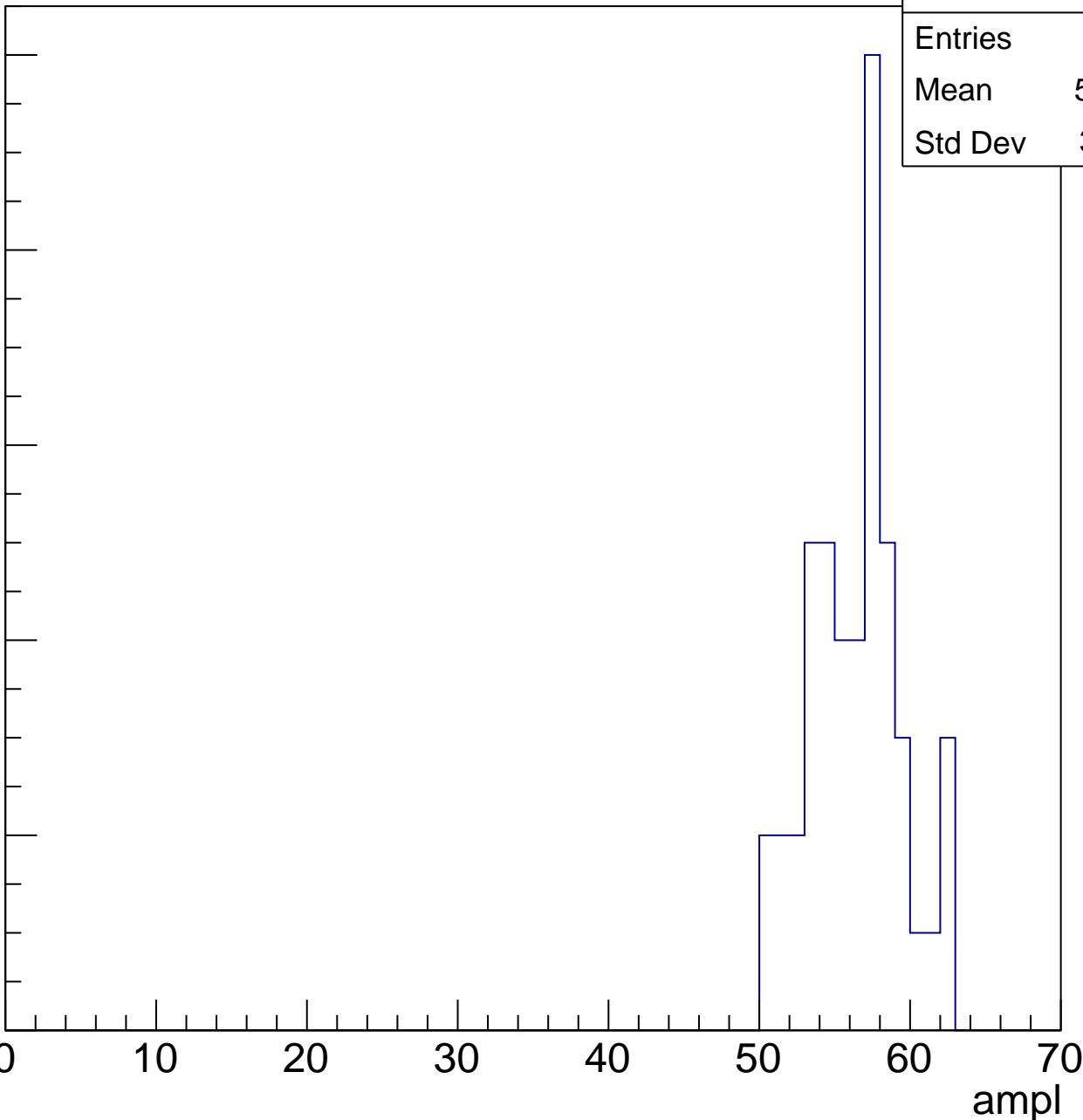
40

50

60

ampl

70

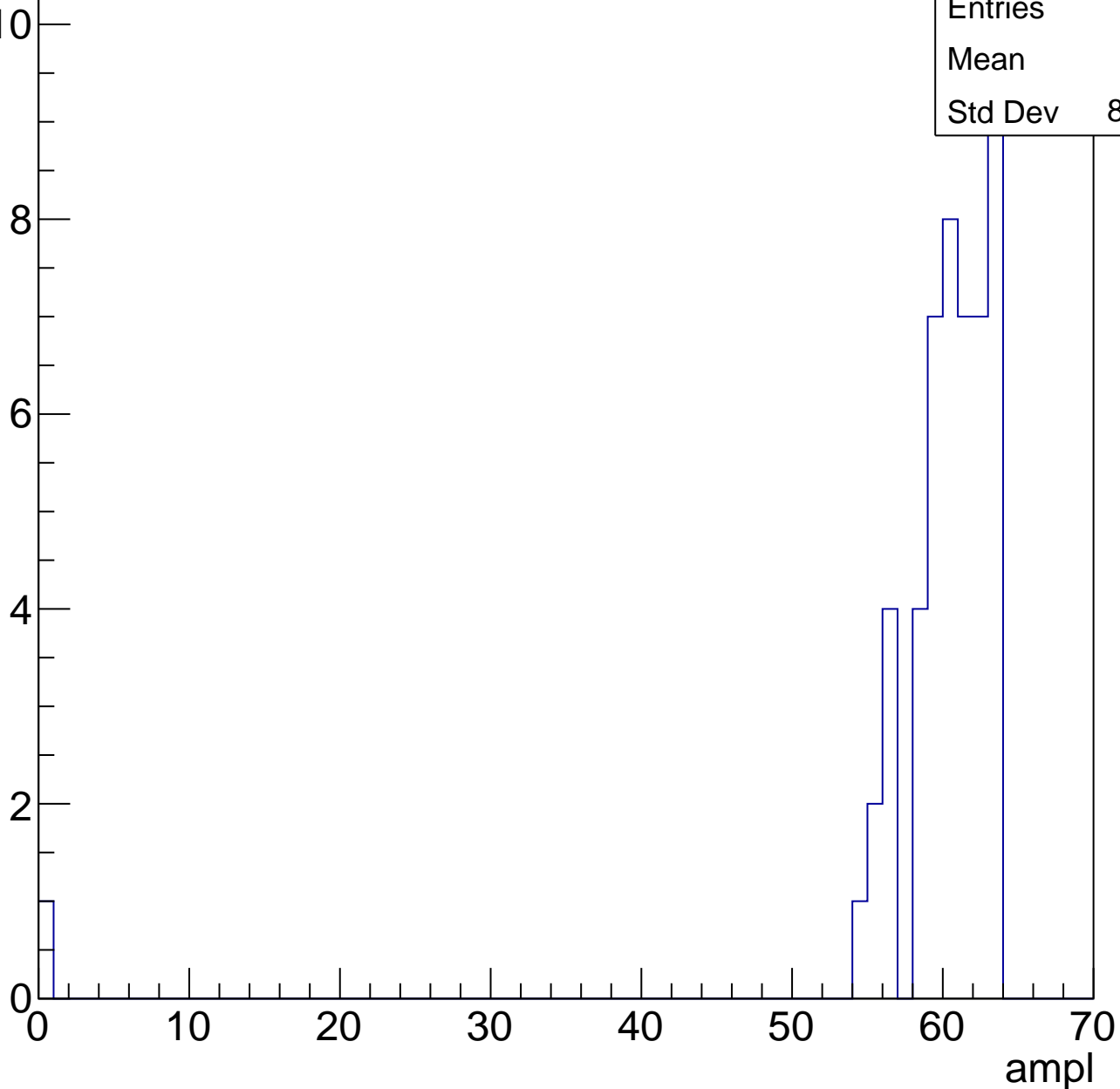


# B0L002S, U23-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

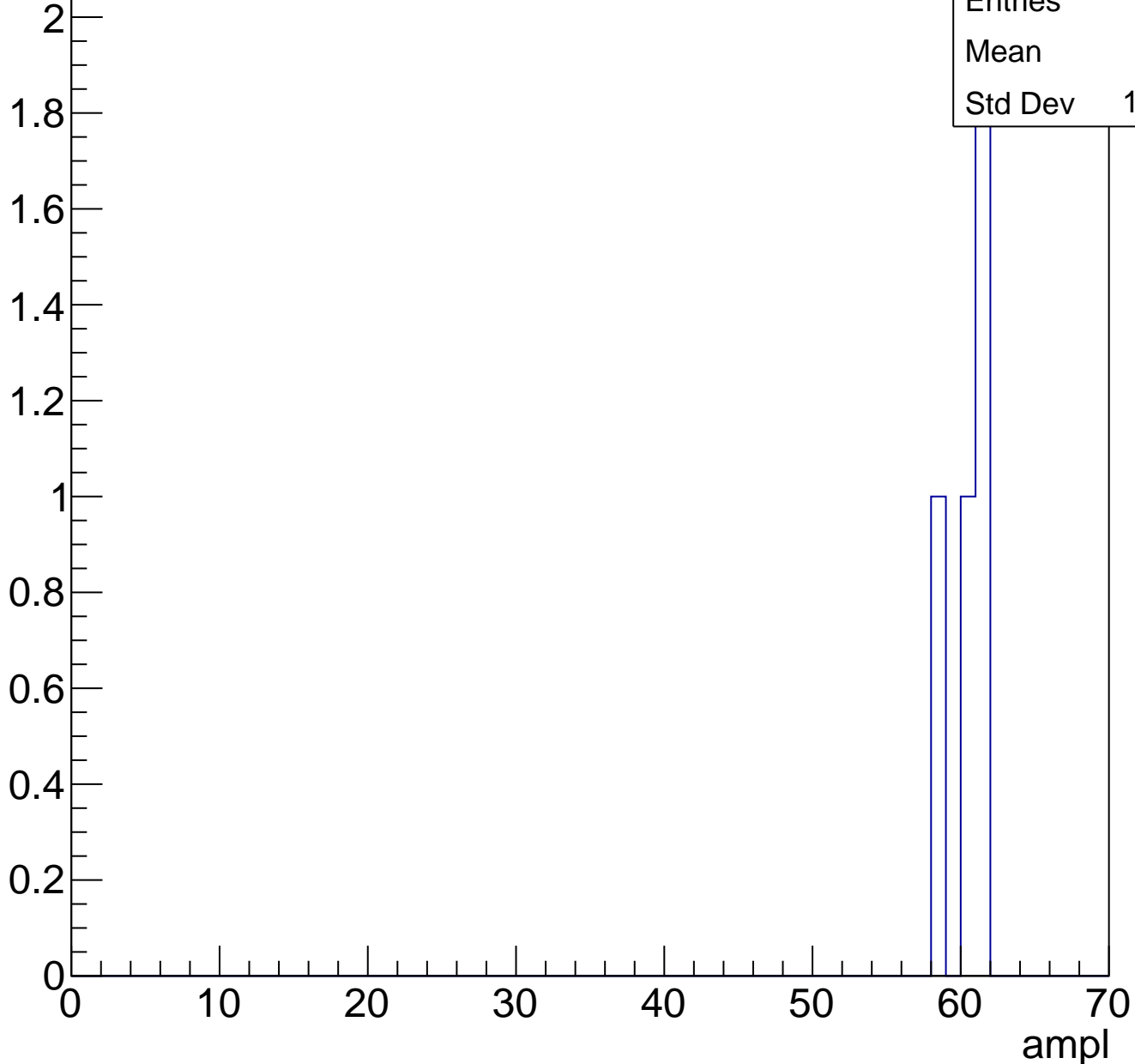
Entries	51
Mean	58.9
Std Dev	8.673



# B0L002S, U23-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	4
Mean	60
Std Dev	1.225



# B0L002S, U23-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch111, adc0

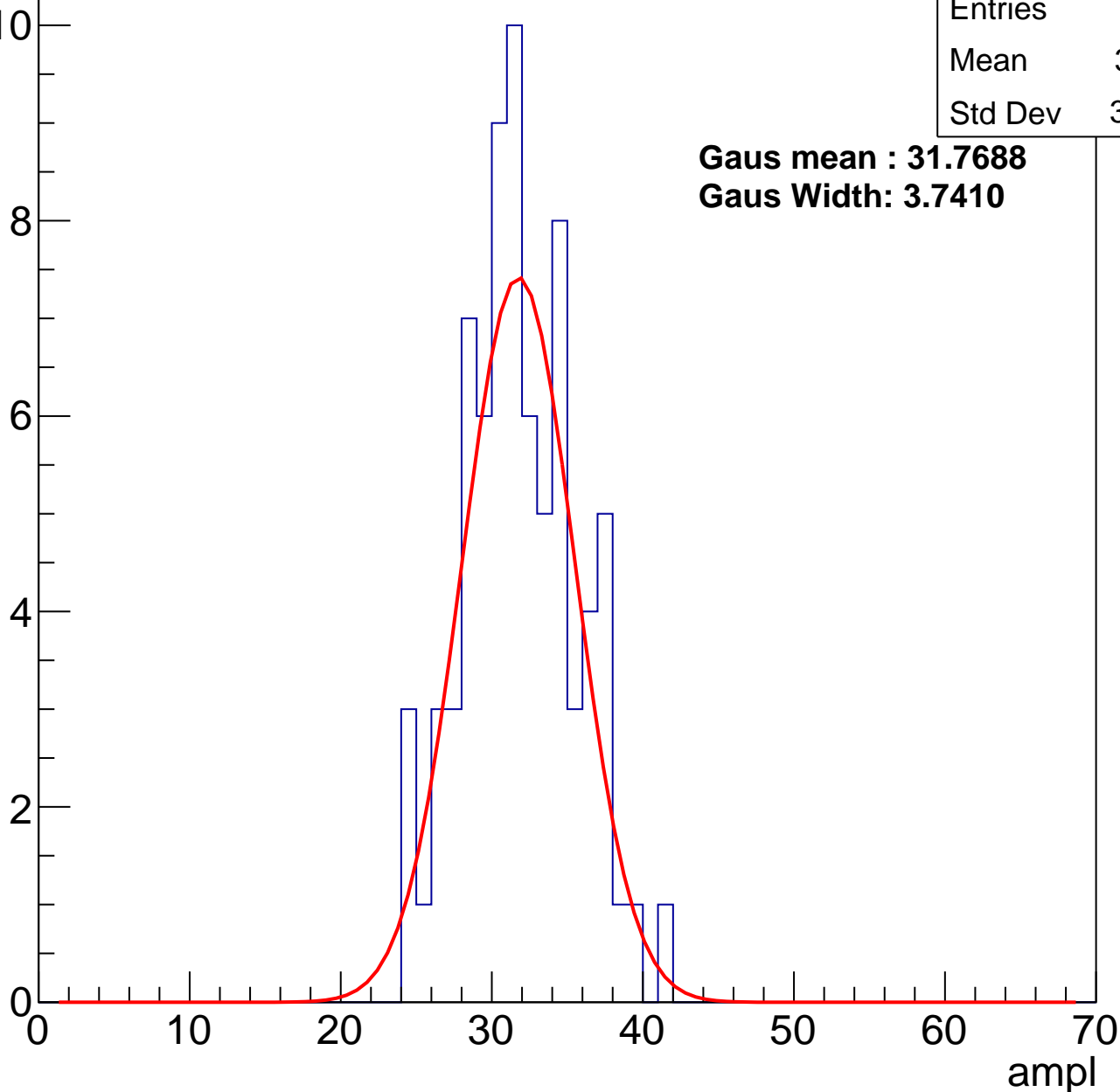
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.41
Std Dev	3.678

**Gaus mean : 31.7688**

**Gaus Width: 3.7410**



# B0L002S, U23-ch111, adc1

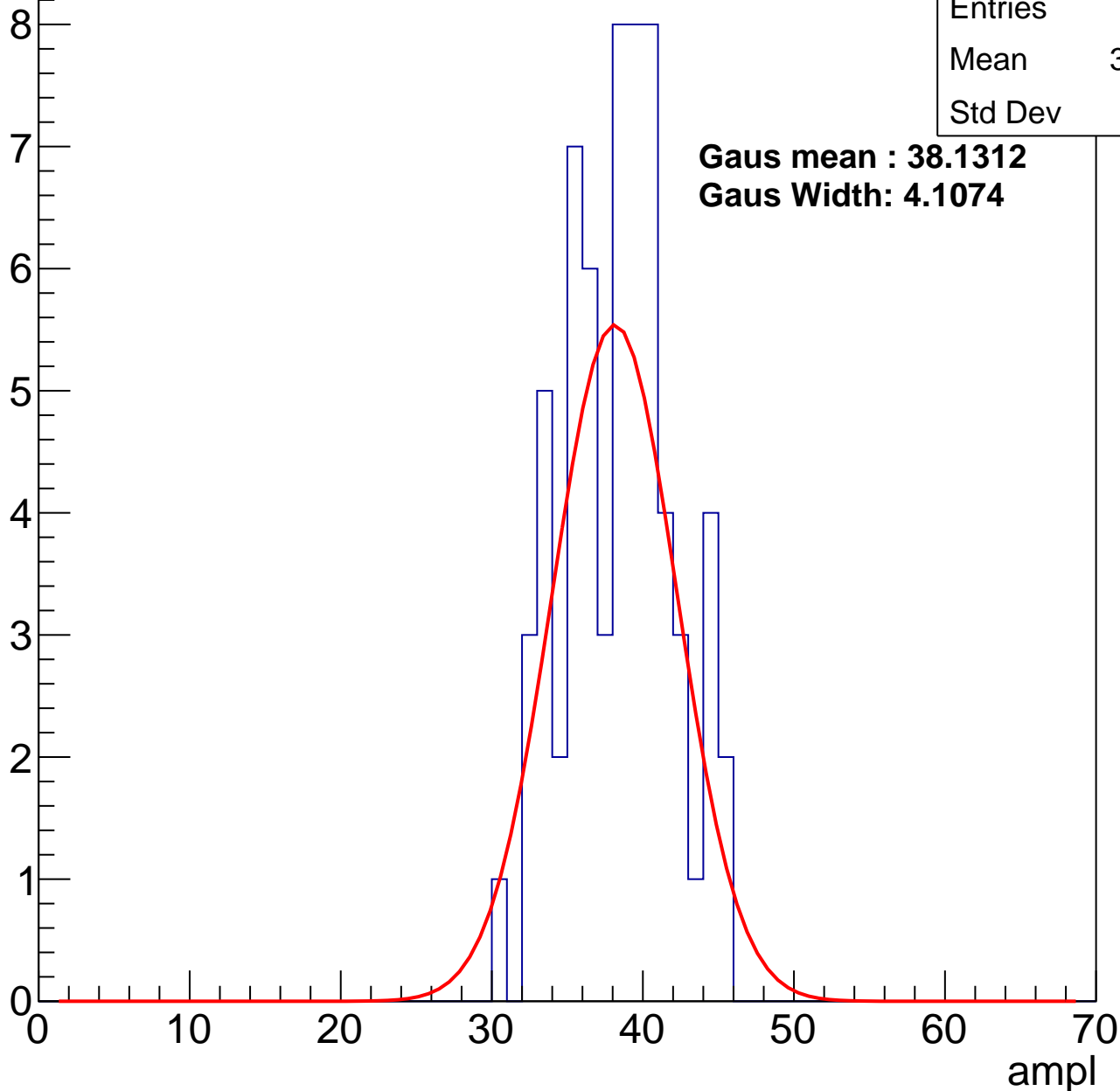
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.94
Std Dev	3.53

**Gaus mean : 38.1312**

**Gaus Width: 4.1074**



# B0L002S, U23-ch111, adc2

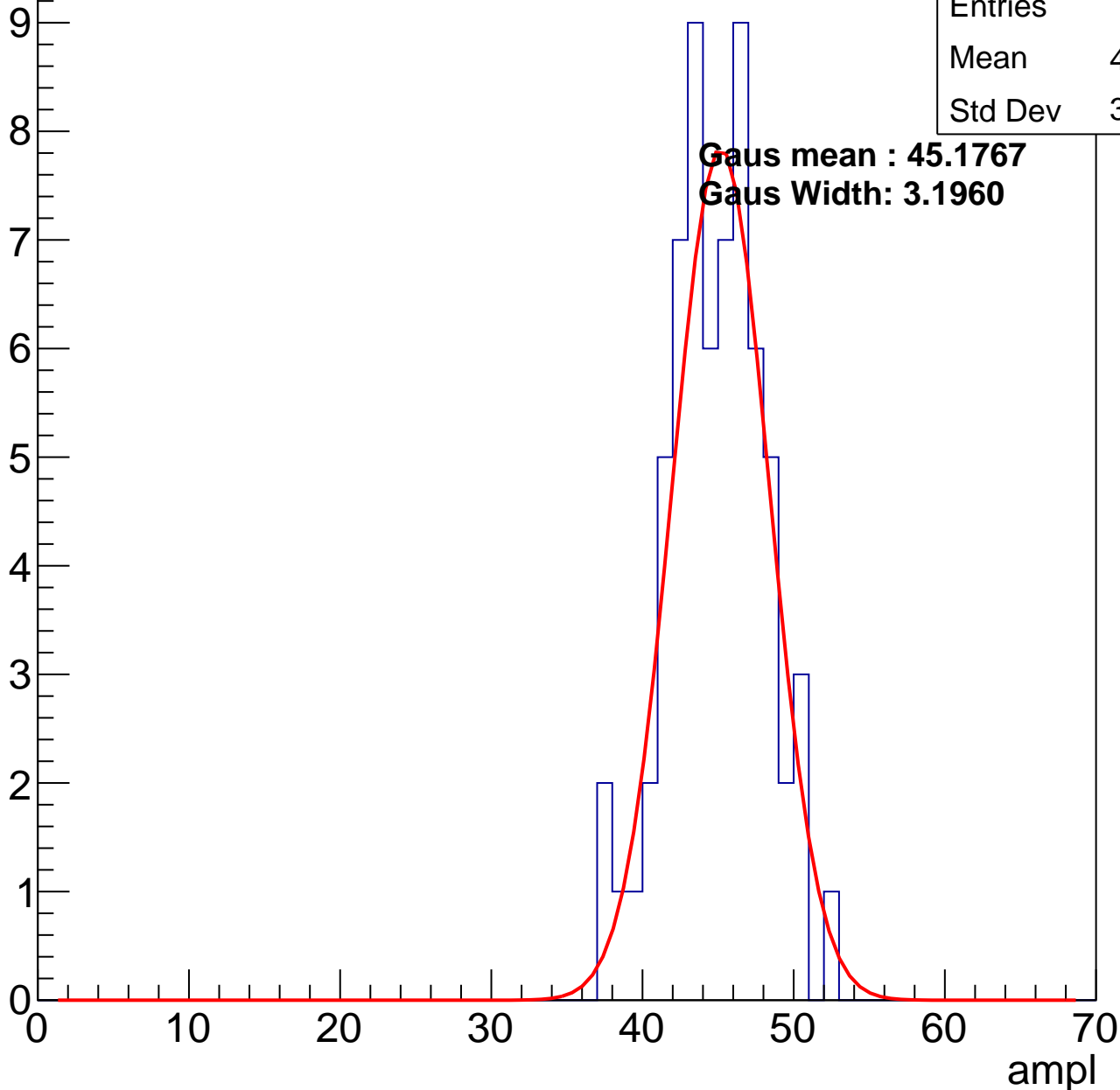
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.42
Std Dev	3.163

**Gaus mean : 45.1767**

**Gaus Width: 3.1960**

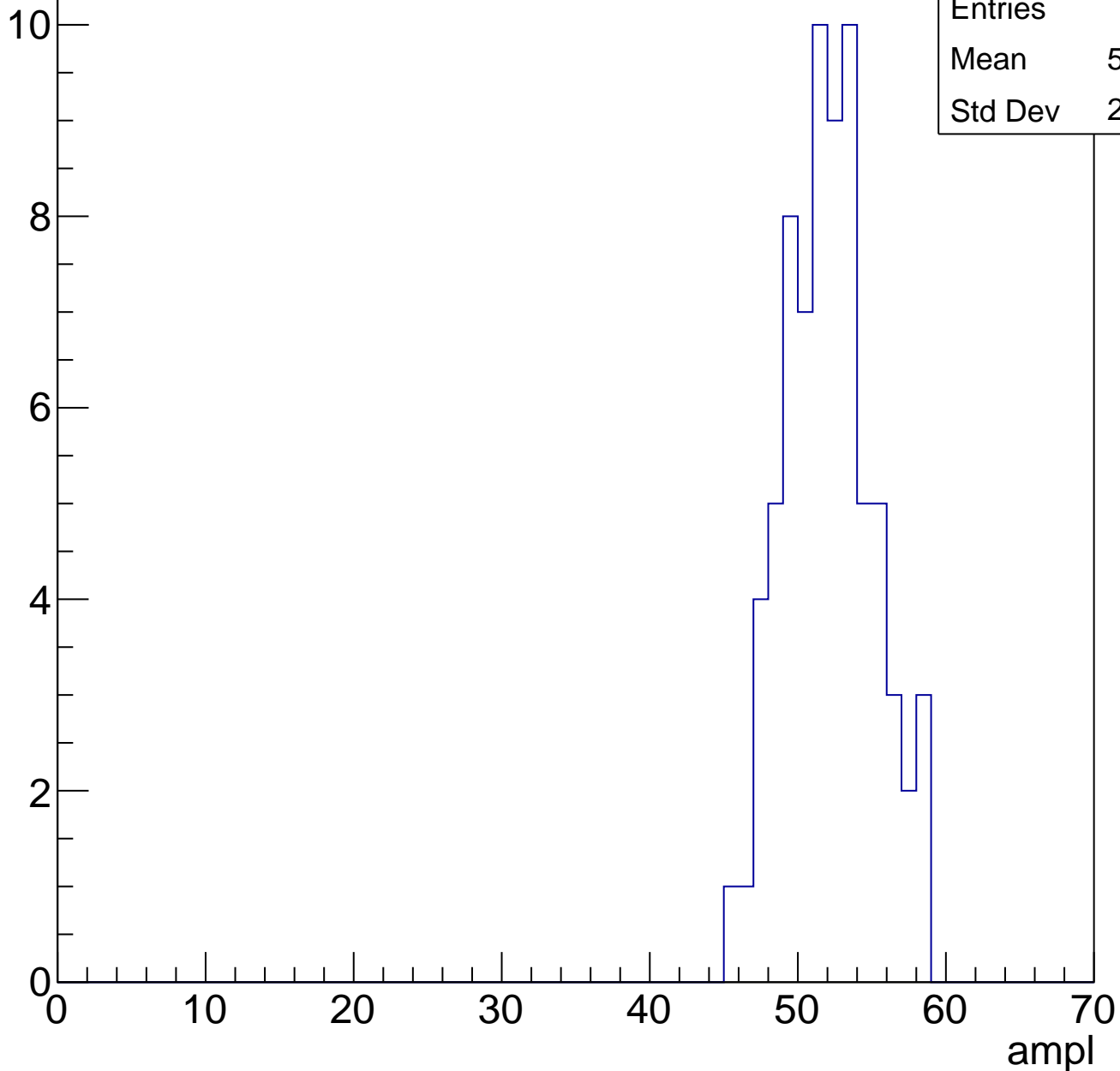


# B0L002S, U23-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	51.64
Std Dev	2.976

Entry

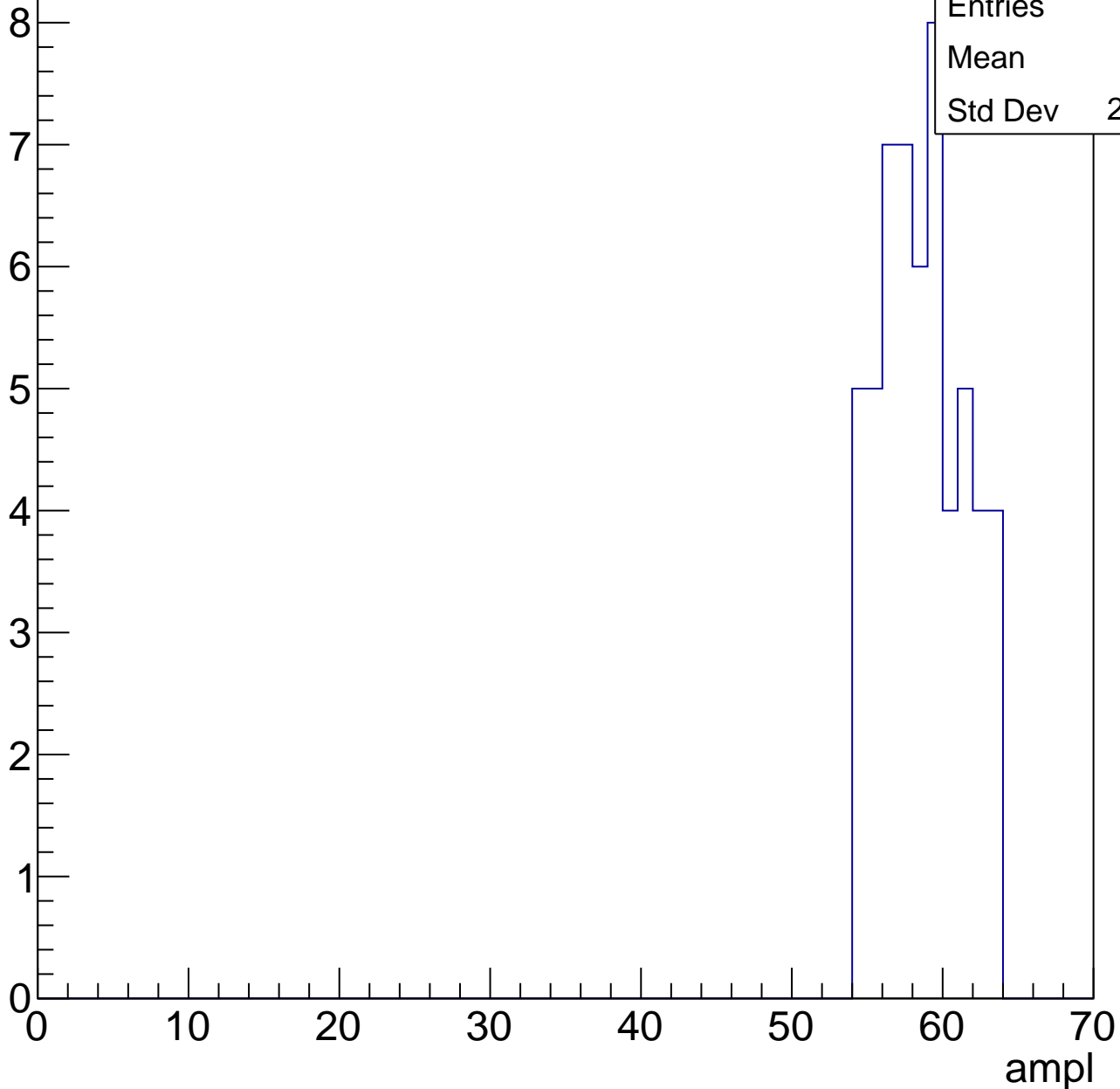


# B0L002S, U23-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

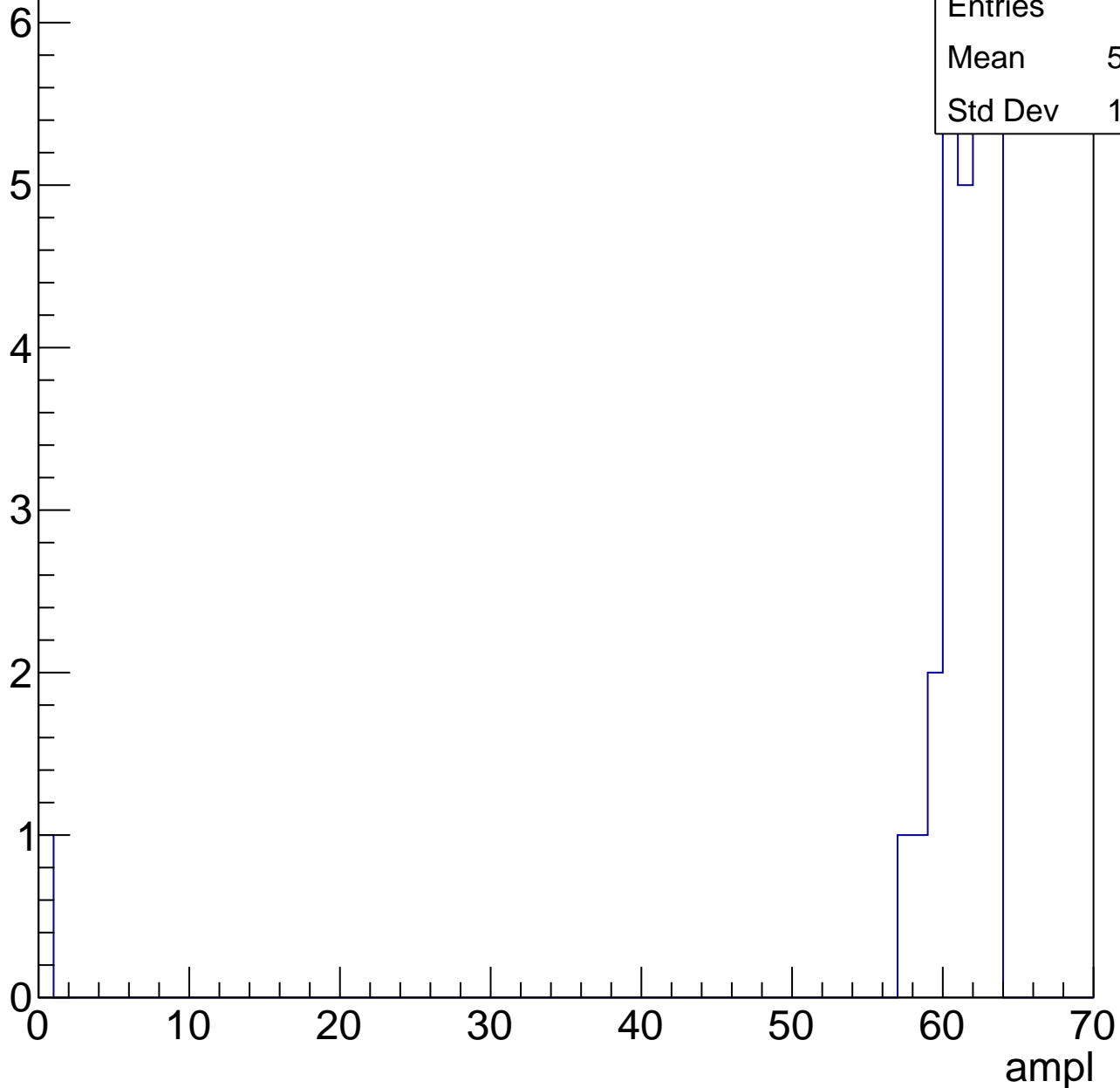
Entries	55
Mean	58.2
Std Dev	2.666



# B0L002S, U23-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L002S, U23-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L002S, U23-ch112, adc0

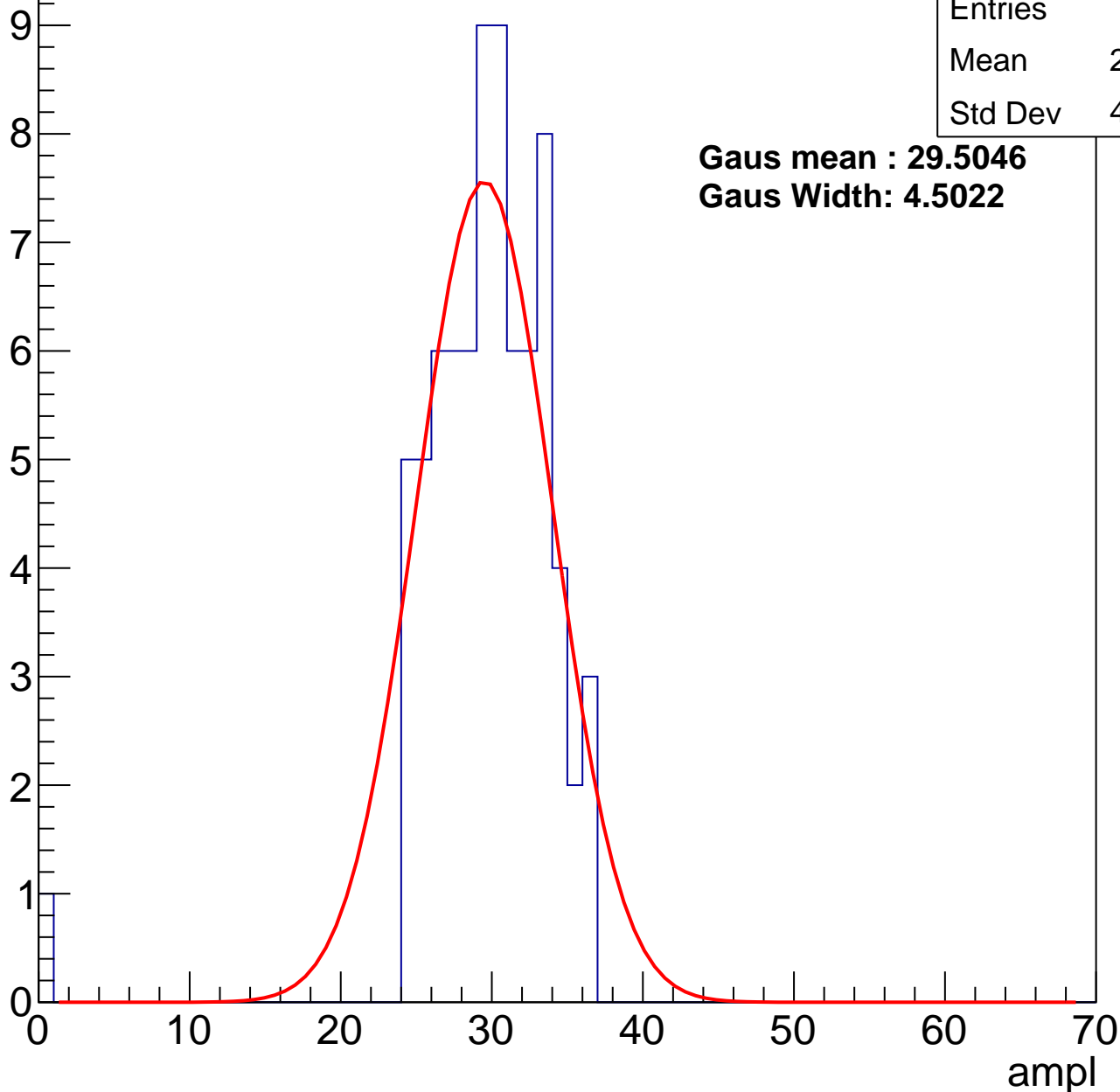
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	29.18
Std Dev	4.676

**Gaus mean : 29.5046**

**Gaus Width: 4.5022**



# B0L002S, U23-ch112, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	73
Mean	37.29
Std Dev	3.536

**Gaus mean : 37.7945**

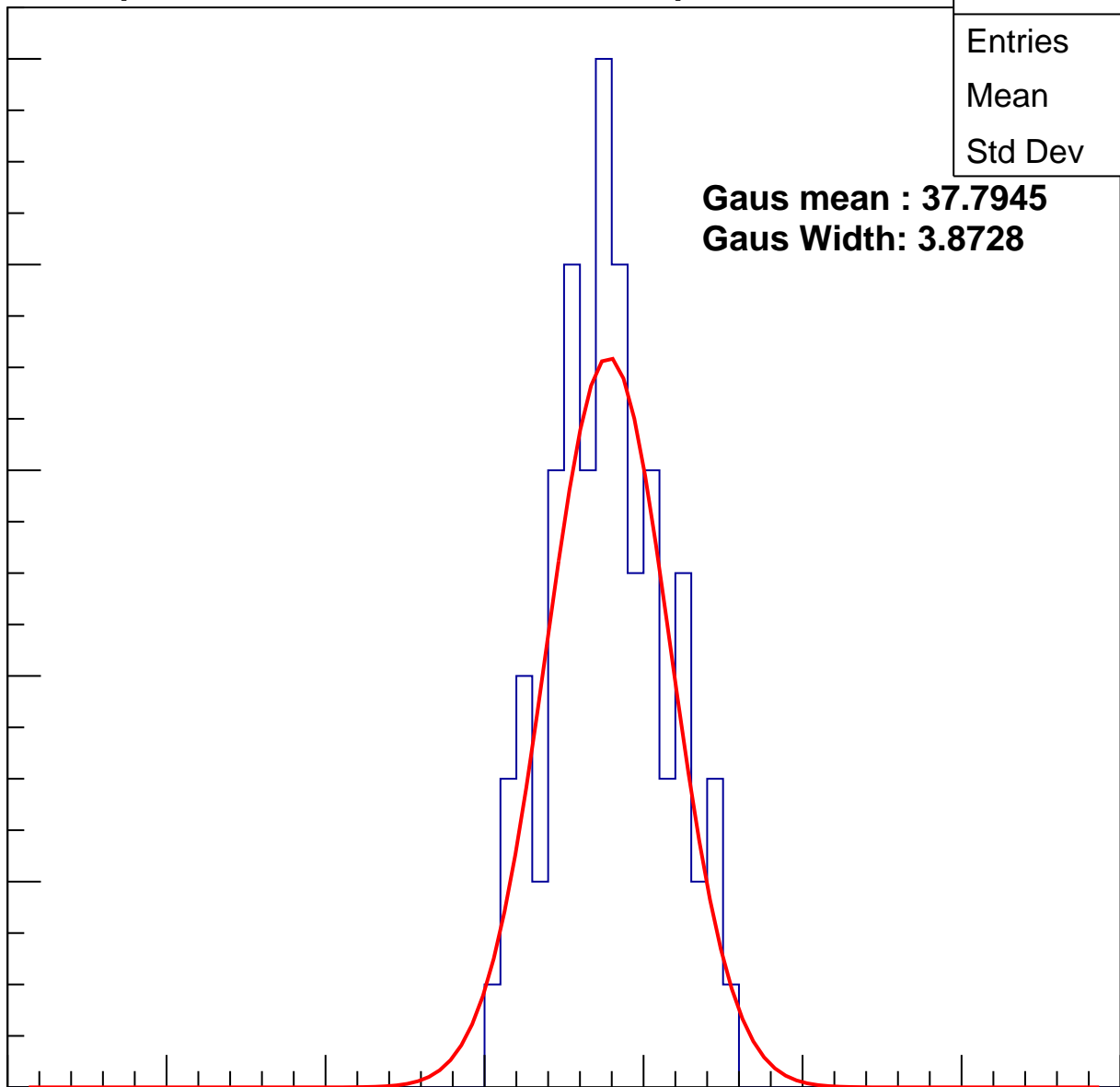
**Gaus Width: 3.8728**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch112, adc2

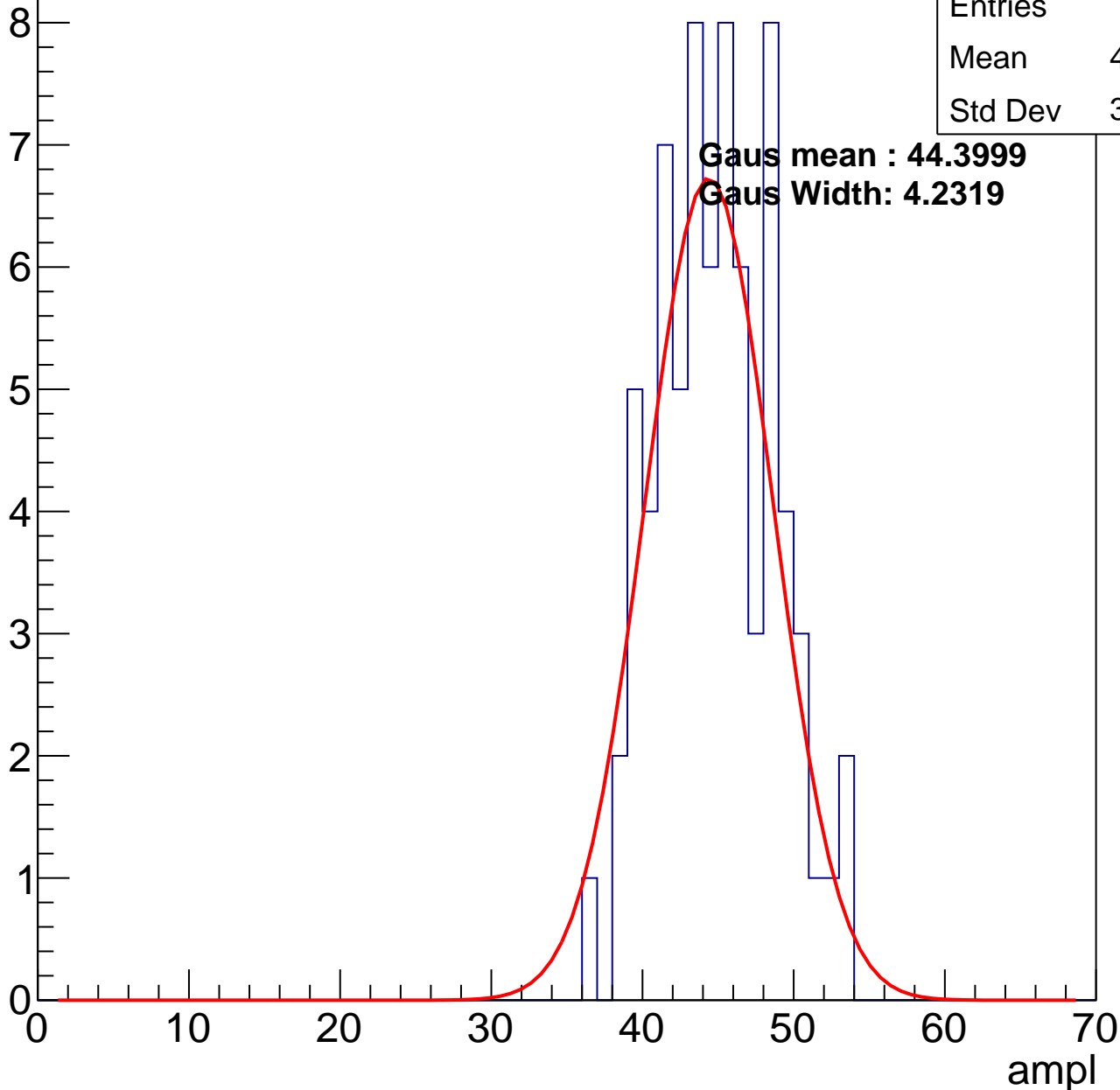
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	44.43
Std Dev	3.806

**Gaus mean : 44.3999**

**Gaus Width: 4.2319**

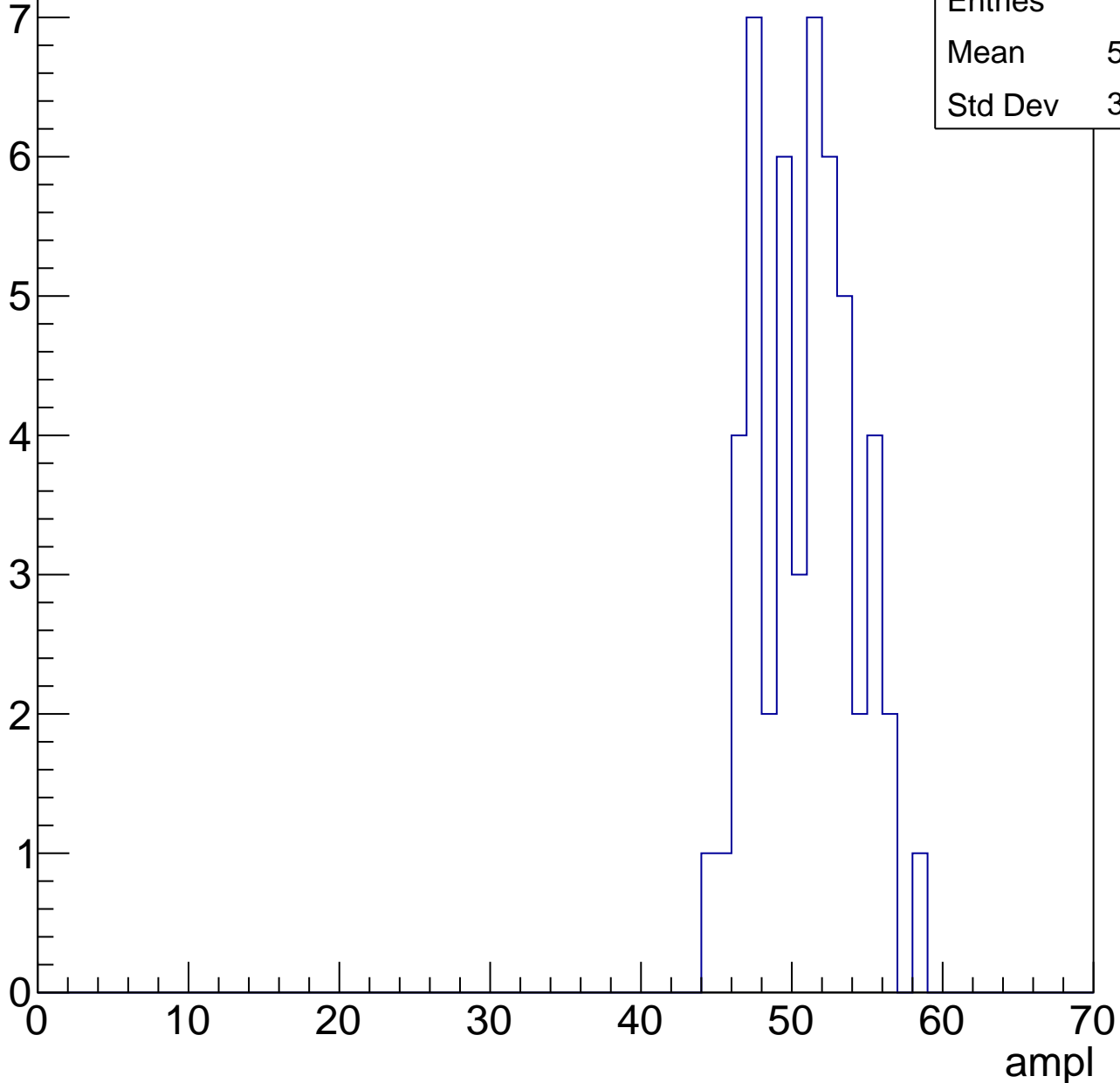


# B0L002S, U23-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	50.47
Std Dev	3.238

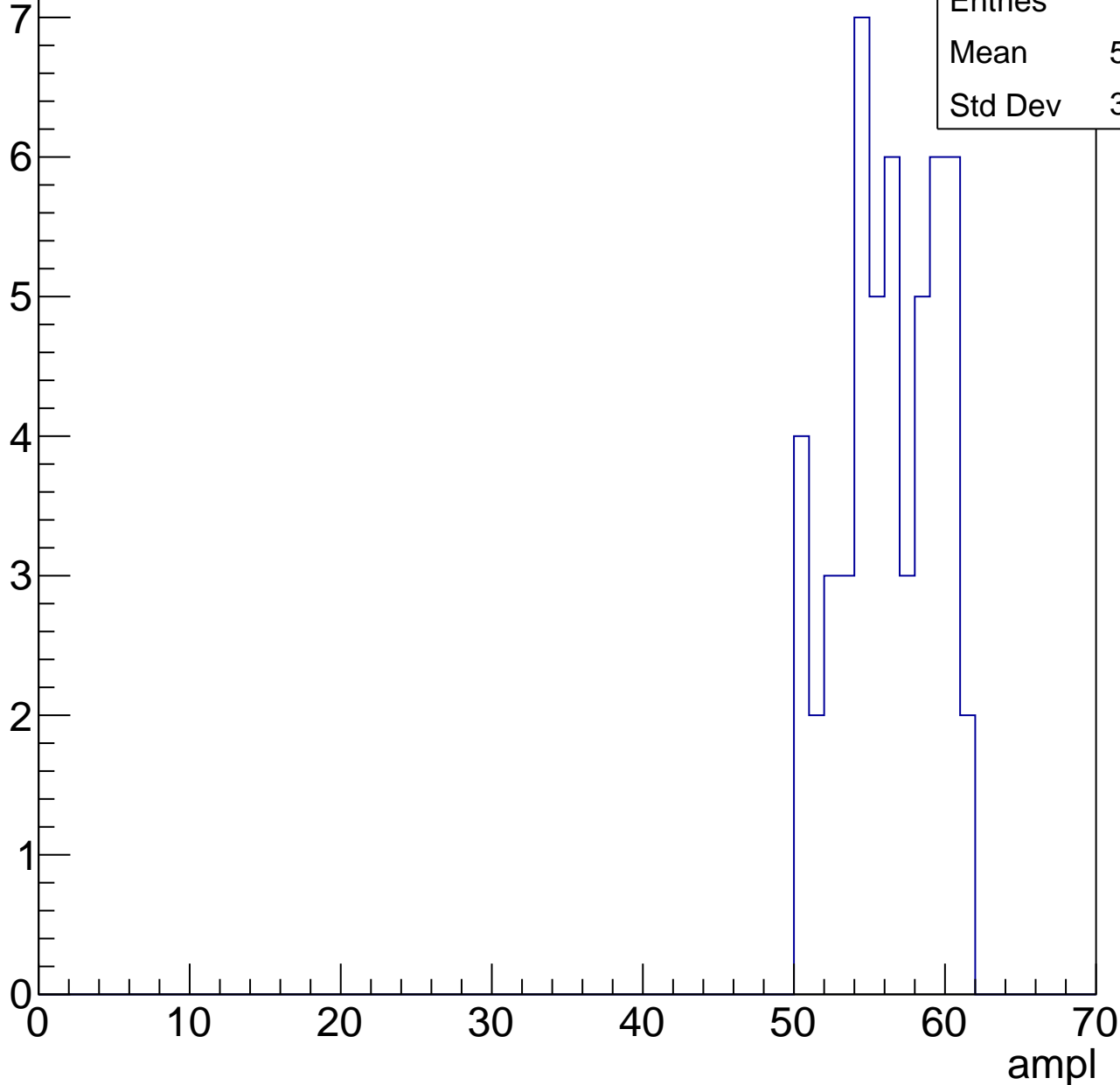


# B0L002S, U23-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

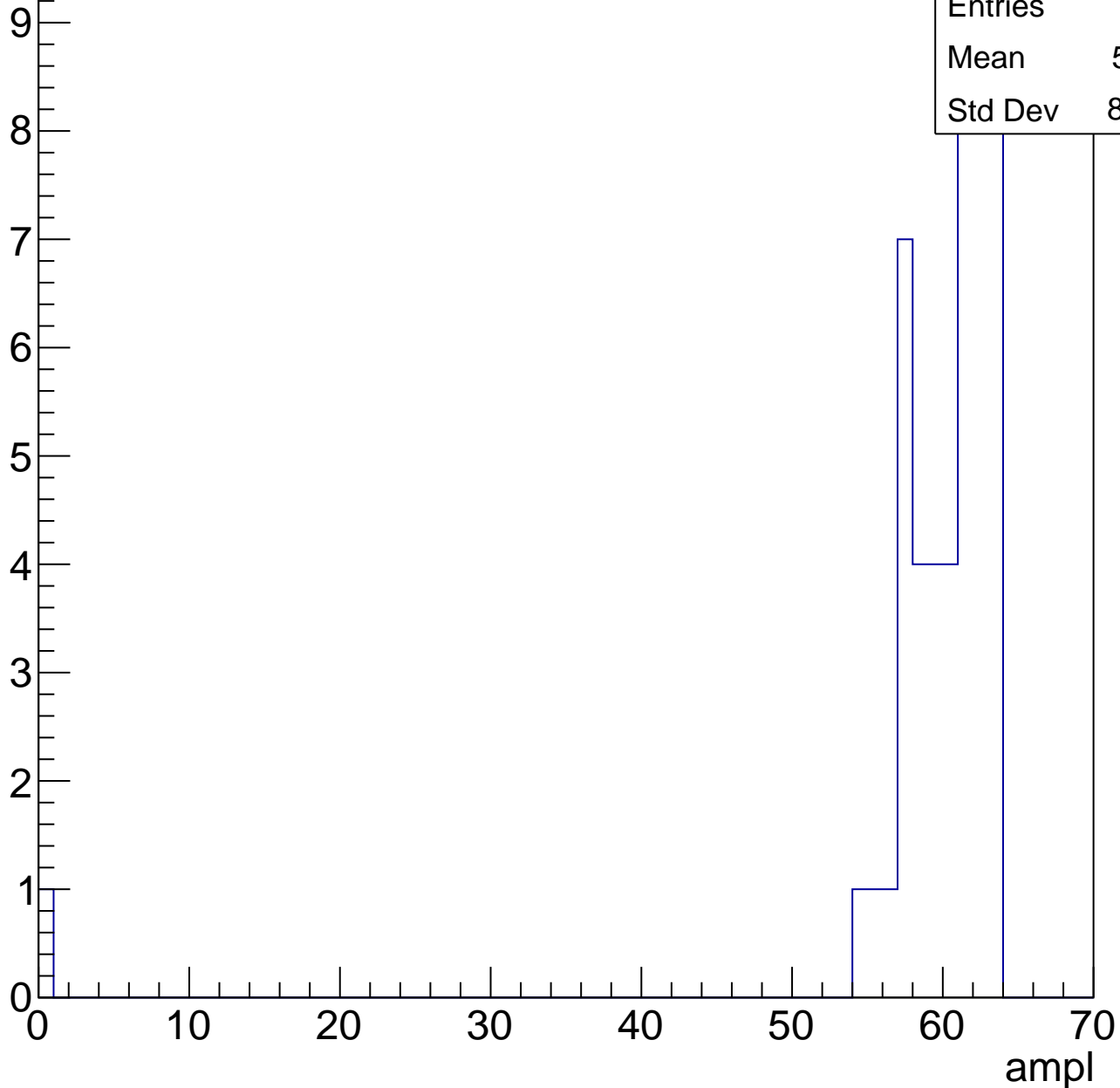
Entries	52
Mean	55.83
Std Dev	3.173



# B0L002S, U23-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

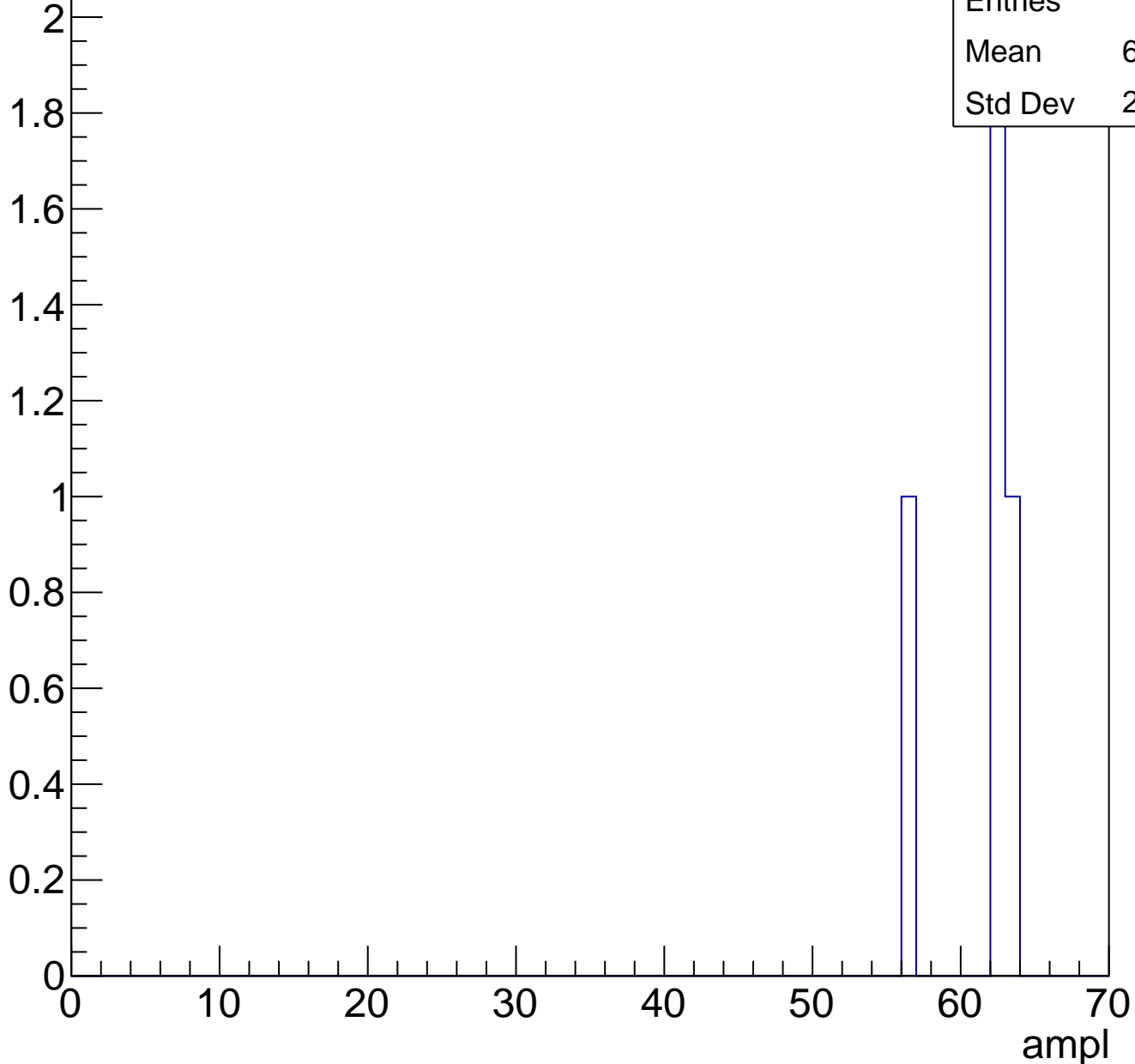
Entry



# B0L002S, U23-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L002S, U23-ch113, adc0

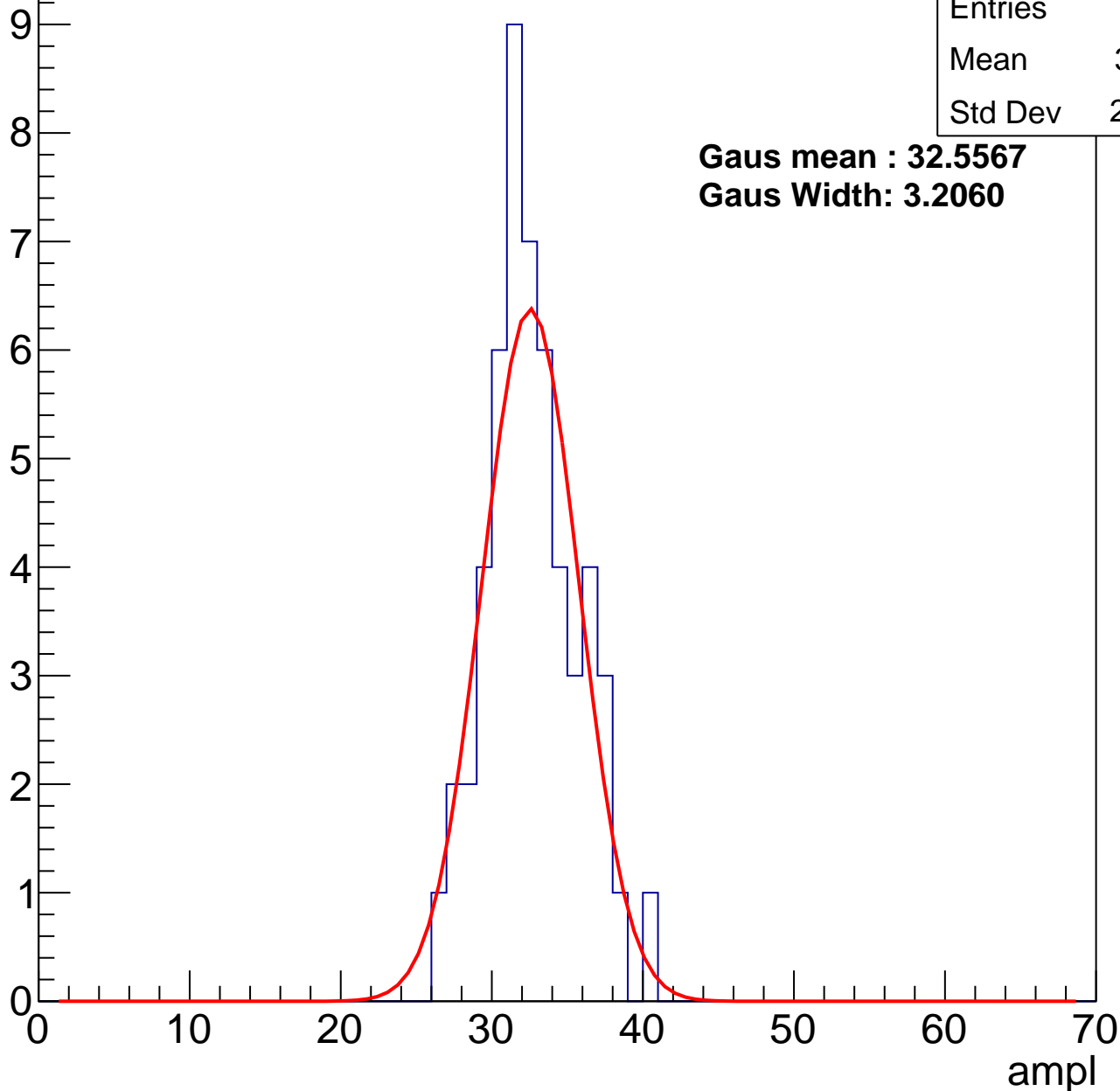
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	32.21
Std Dev	2.987

**Gaus mean : 32.5567**

**Gaus Width: 3.2060**



# B0L002S, U23-ch113, adc1

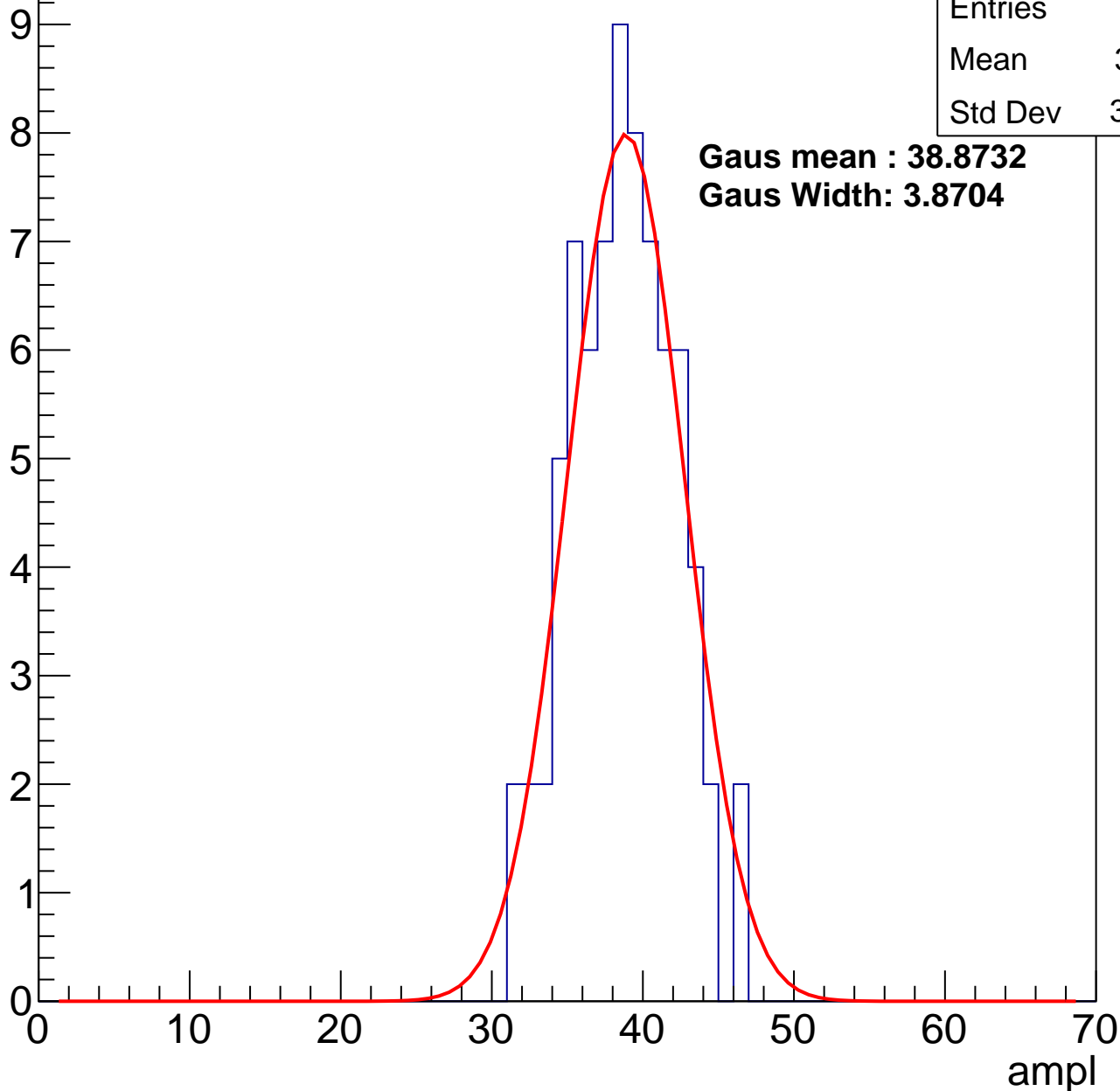
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.21
Std Dev	3.419

**Gaus mean : 38.8732**

**Gaus Width: 3.8704**



# B0L002S, U23-ch113, adc2

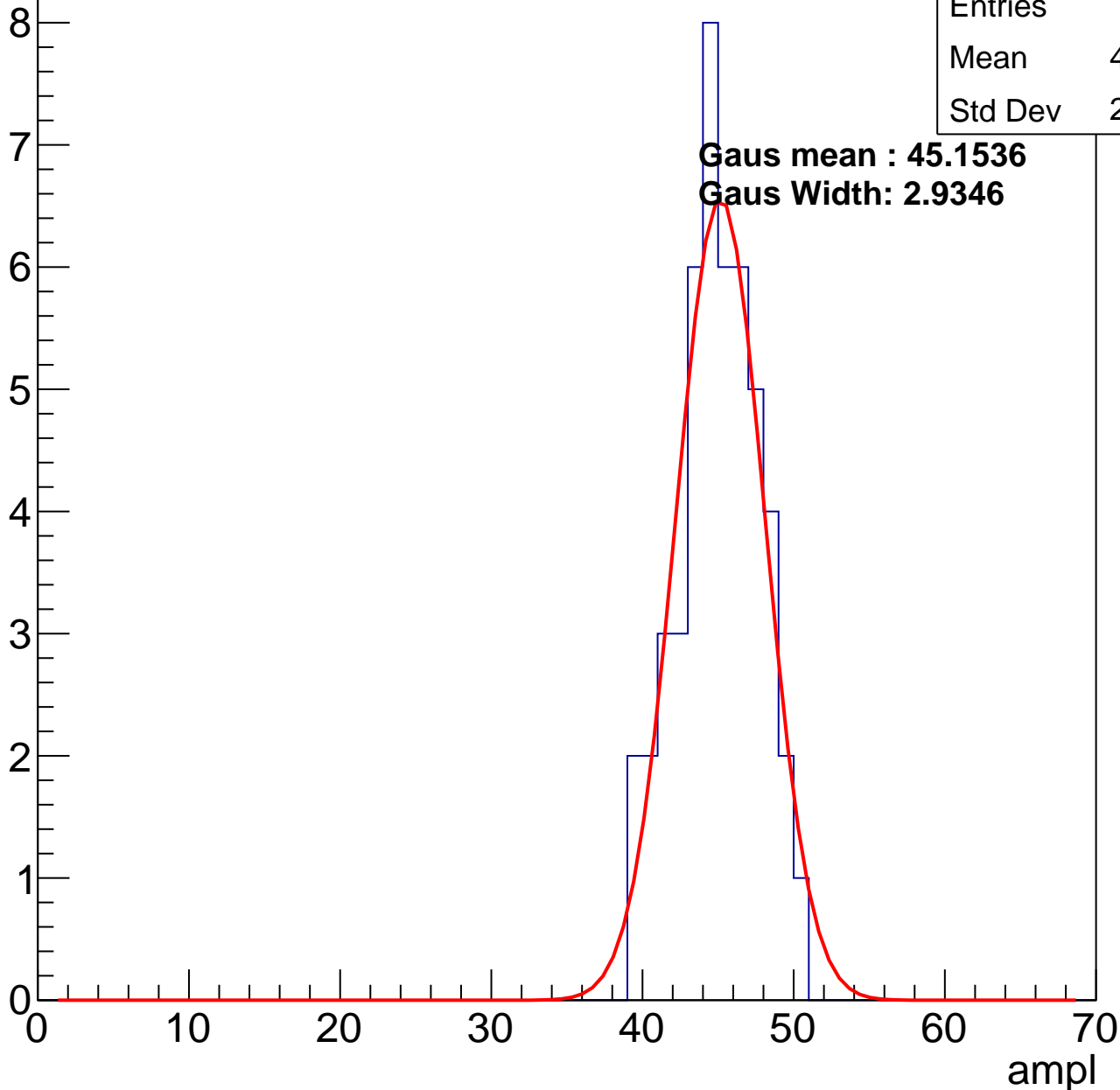
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	44.54
Std Dev	2.653

**Gaus mean : 45.1536**

**Gaus Width: 2.9346**

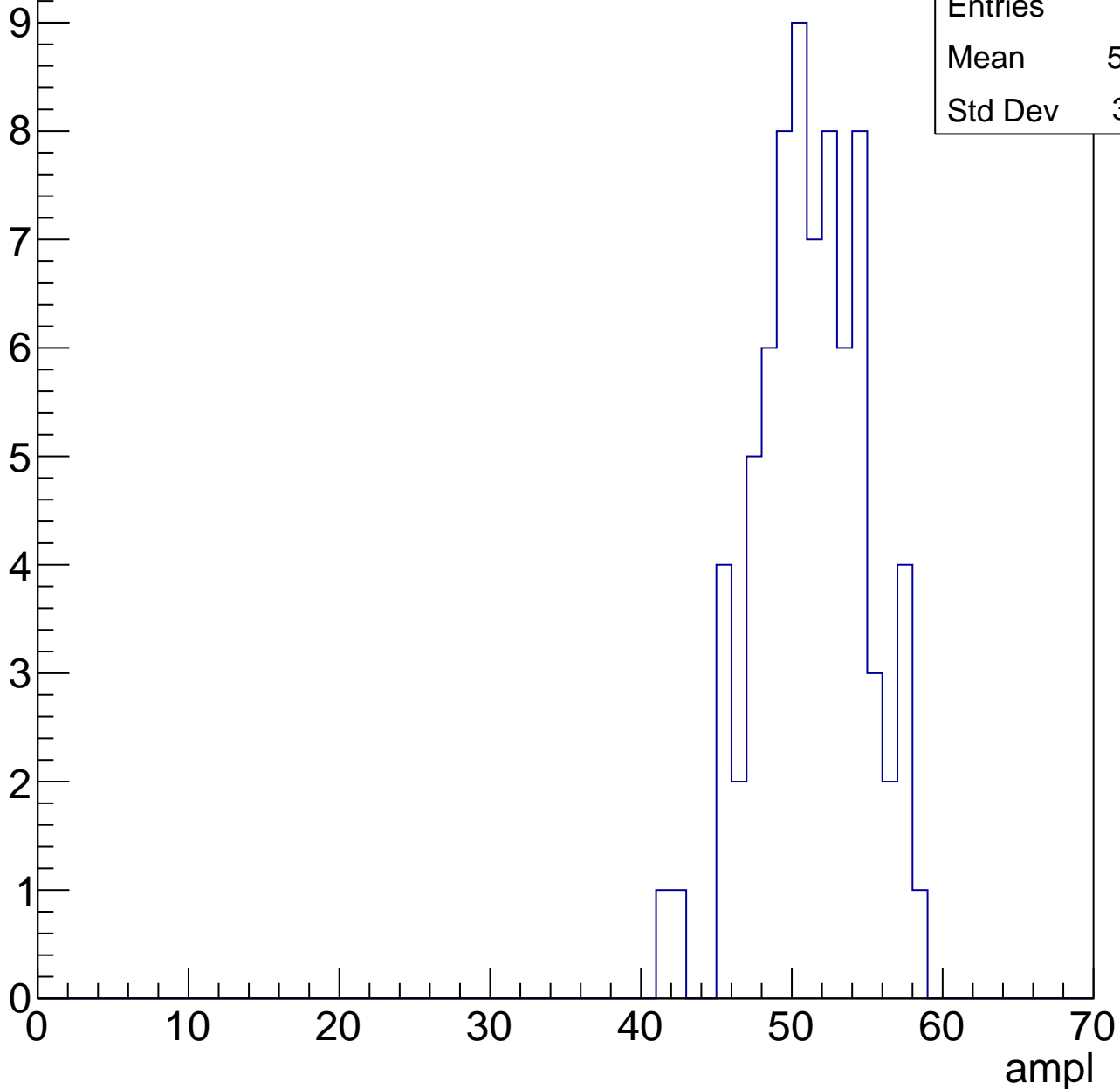


# B0L002S, U23-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	75
Mean	50.75
Std Dev	3.541

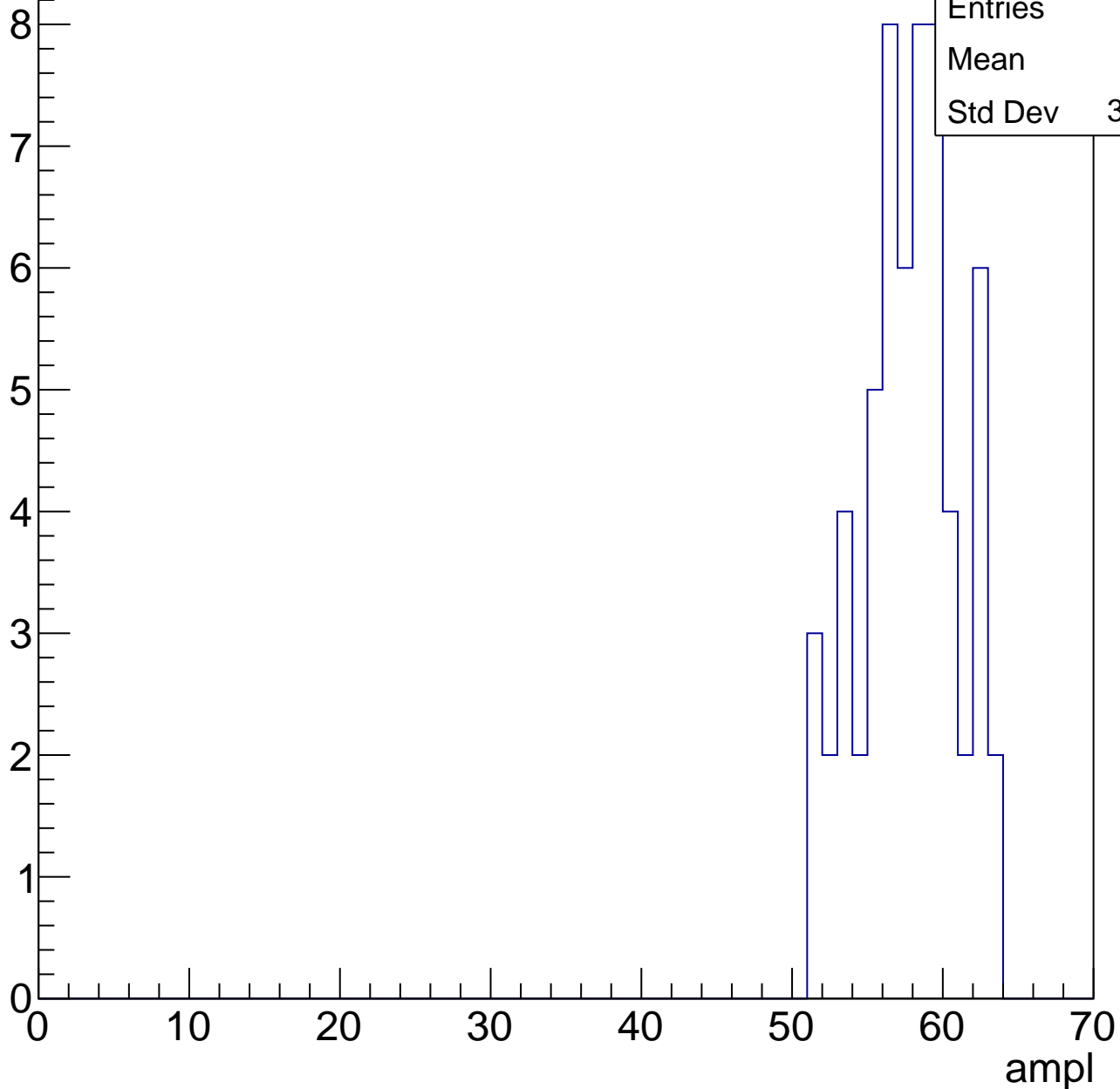


# B0L002S, U23-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	57.3
Std Dev	3.143

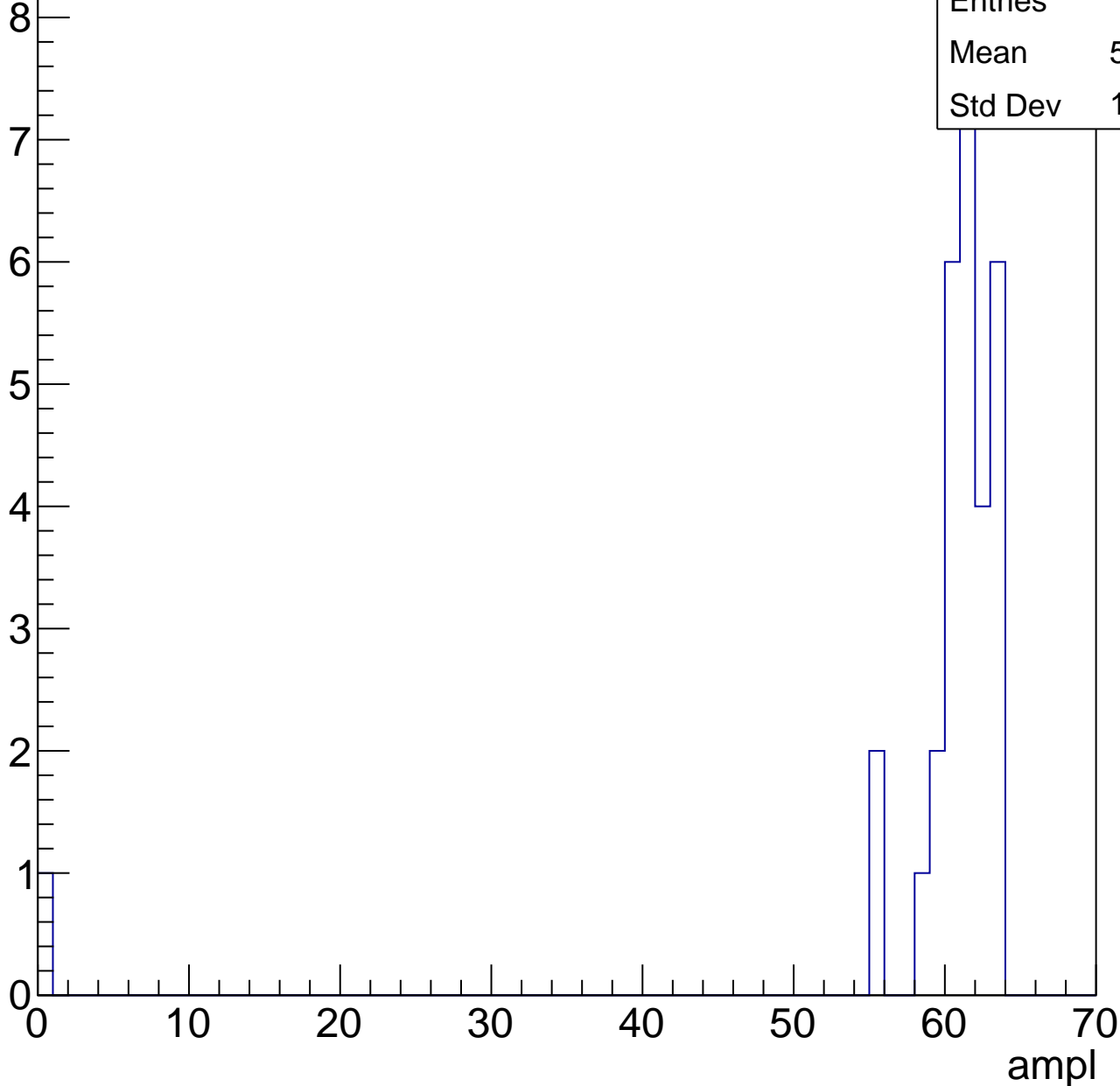


# B0L002S, U23-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.67
Std Dev	11.08



# B0L002S, U23-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

# B0L002S, U23-ch114, adc0

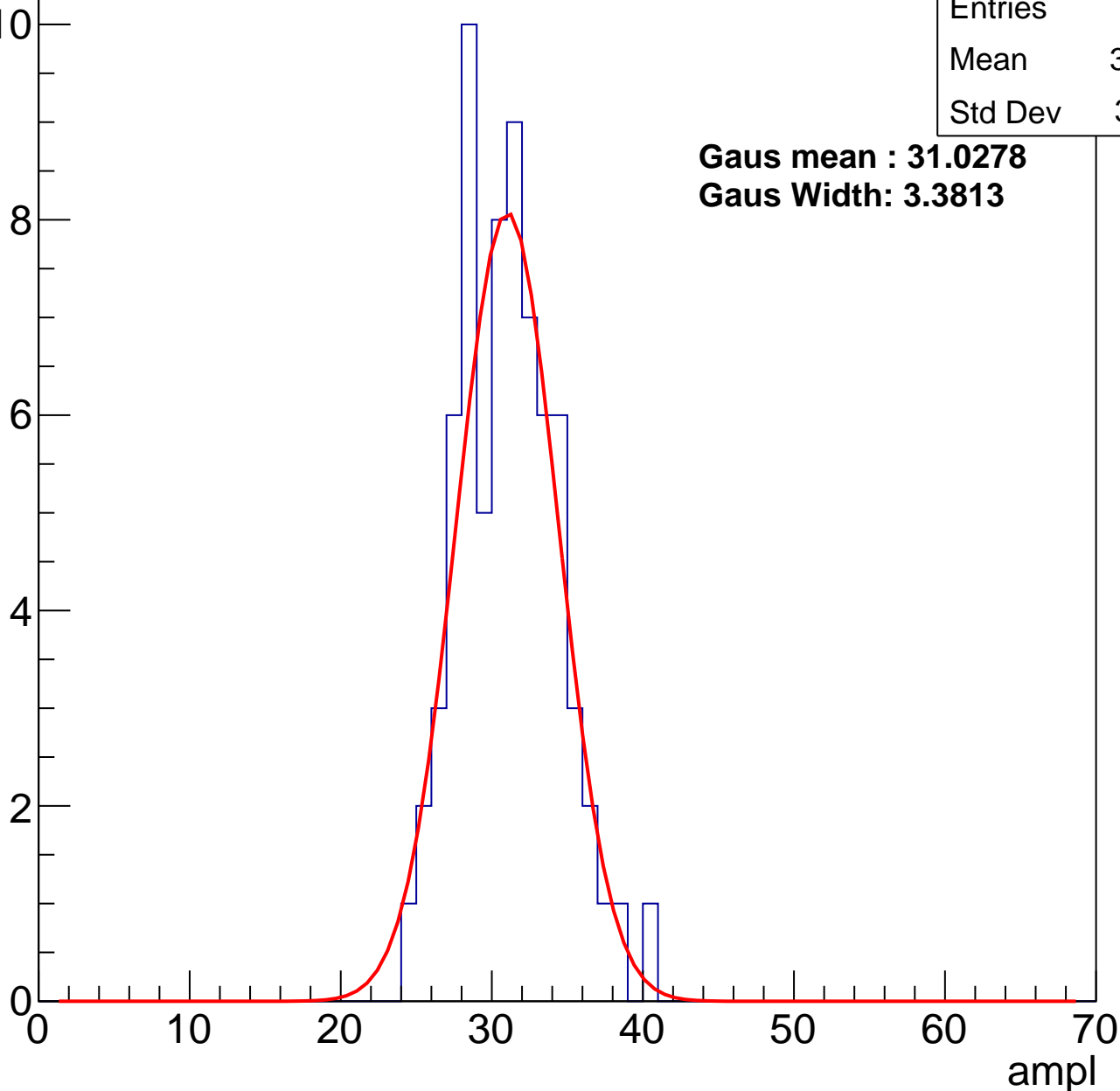
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.65
Std Dev	3.251

**Gaus mean : 31.0278**

**Gaus Width: 3.3813**



# B0L002S, U23-ch114, adc1

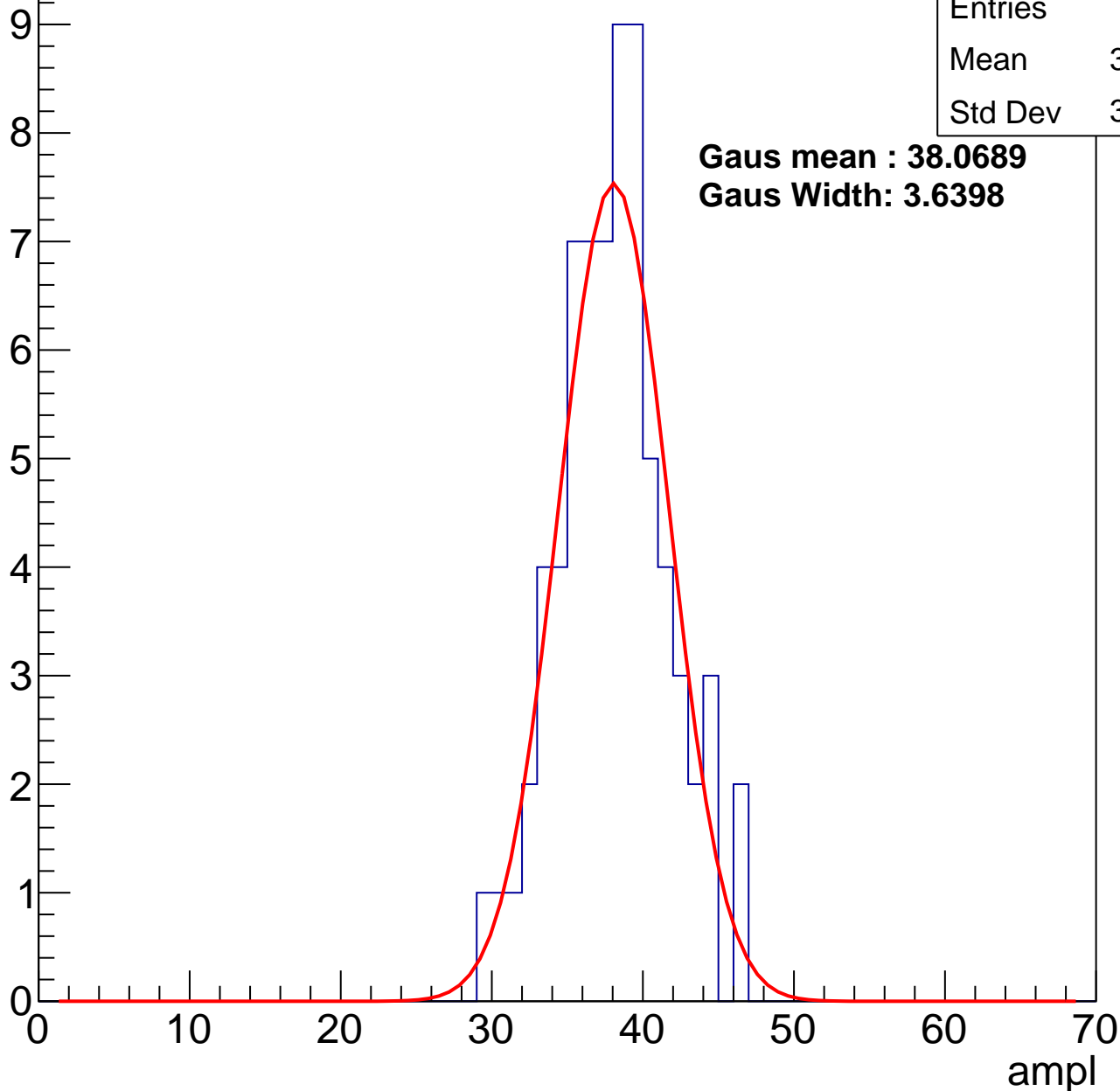
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.62
Std Dev	3.593

**Gaus mean : 38.0689**

**Gaus Width: 3.6398**



# B0L002S, U23-ch114, adc2

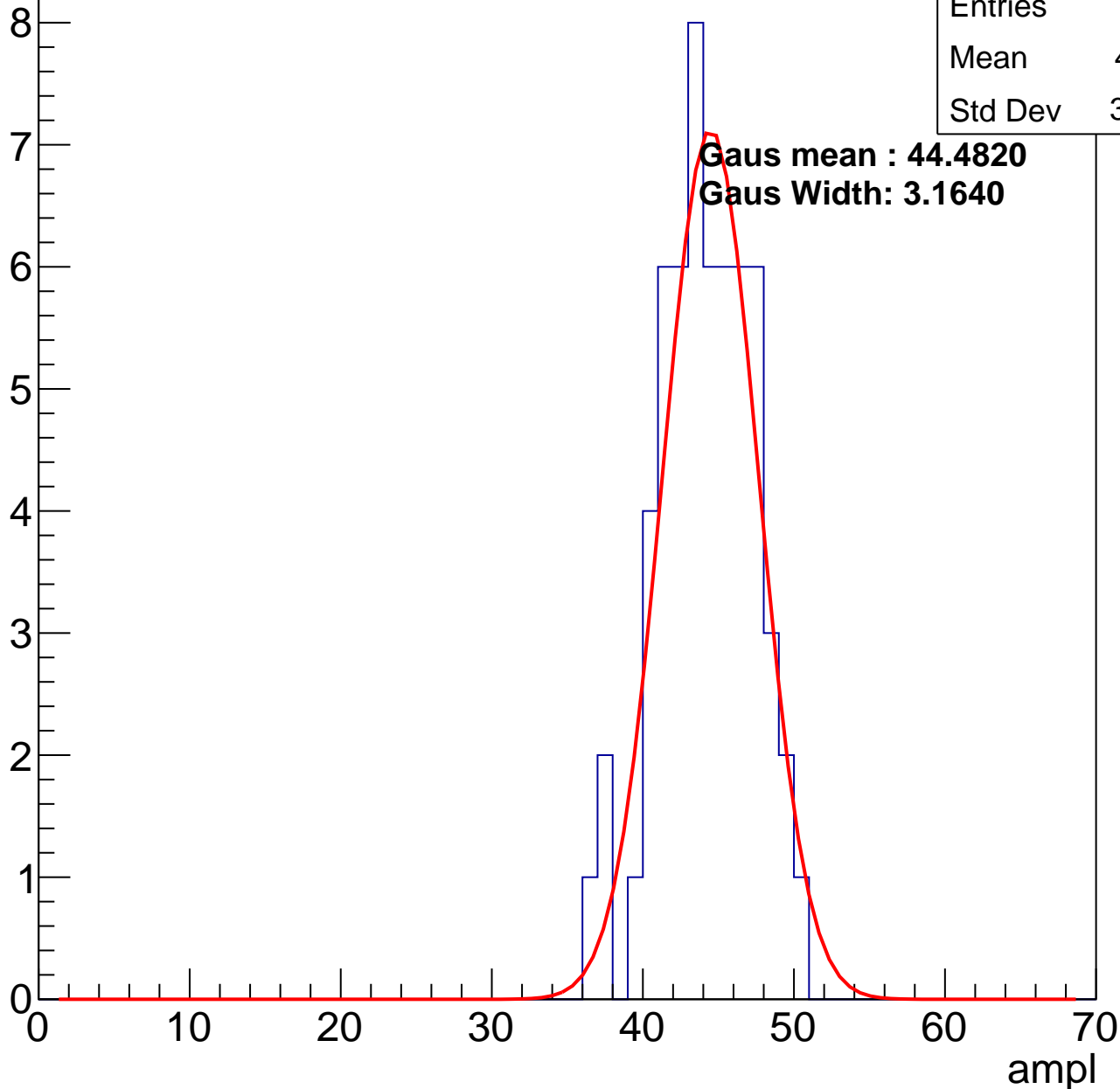
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.71
Std Dev	3.079

**Gaus mean : 44.4820**

**Gaus Width: 3.1640**

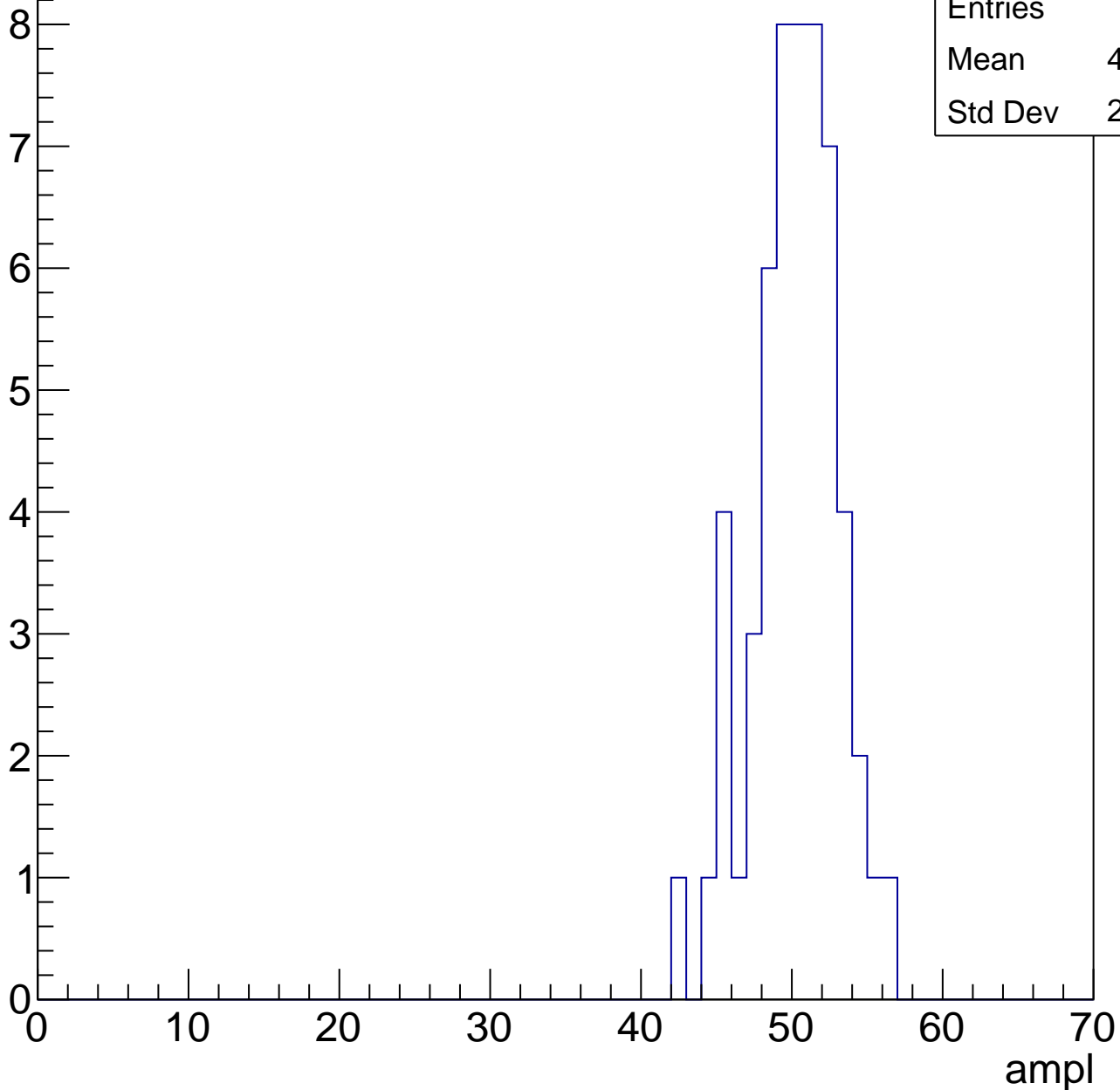


# B0L002S, U23-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	49.75
Std Dev	2.817

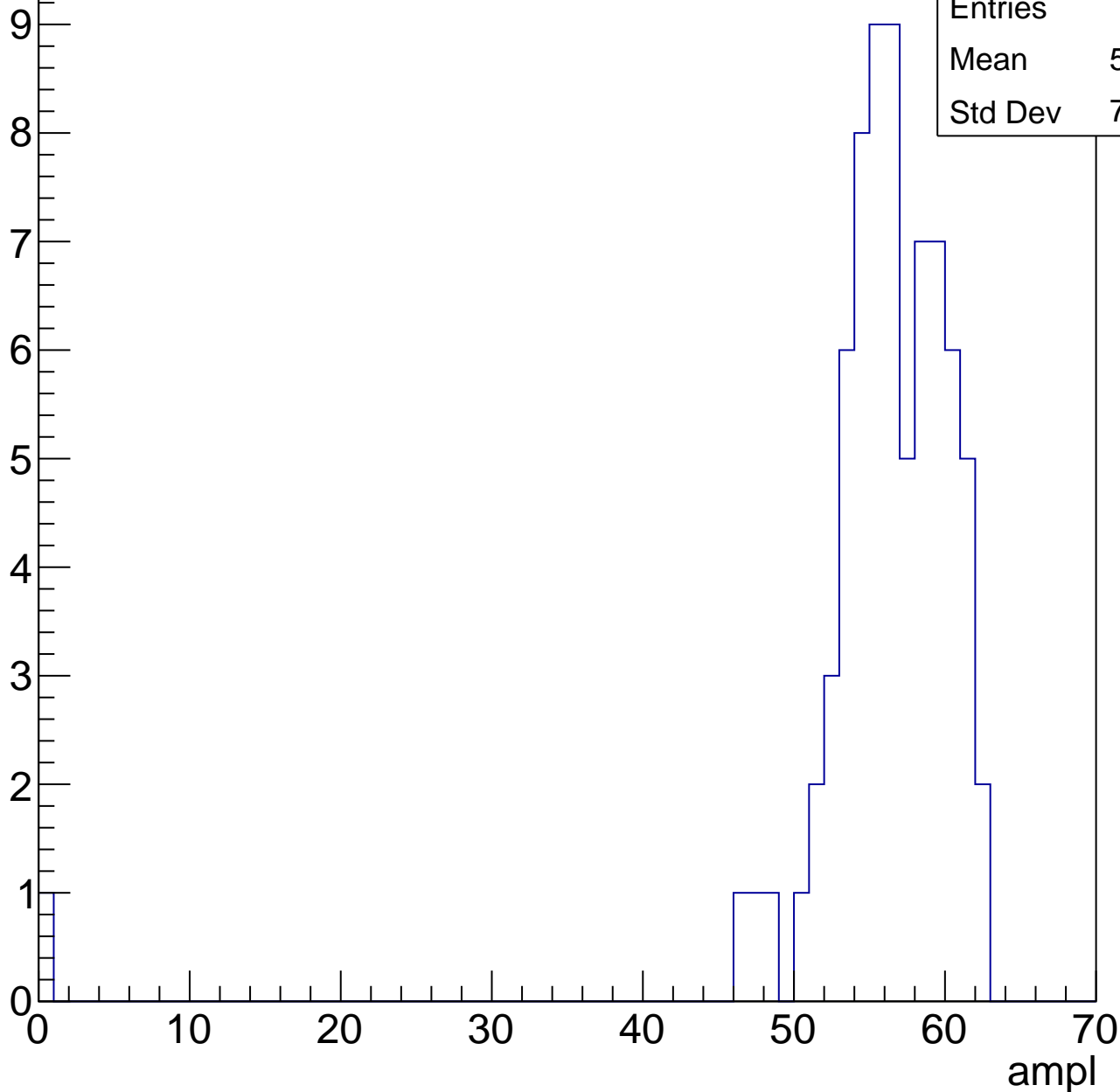


# B0L002S, U23-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	74
Mean	55.28
Std Dev	7.316

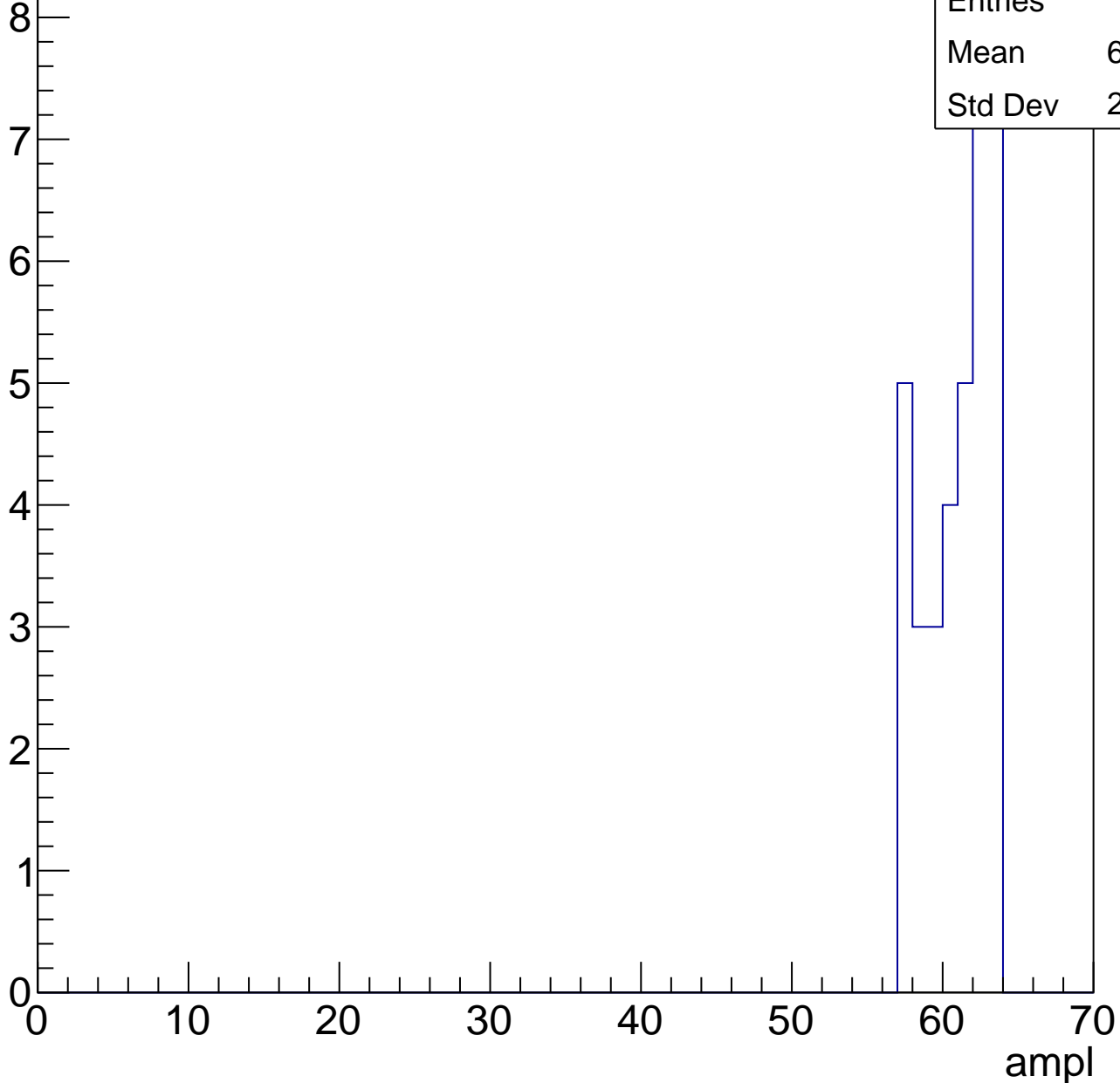


# B0L002S, U23-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	36
Mean	60.58
Std Dev	2.087



# B0L002S, U23-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	63
Std Dev	0



# B0L002S, U23-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L002S, U23-ch115, adc0

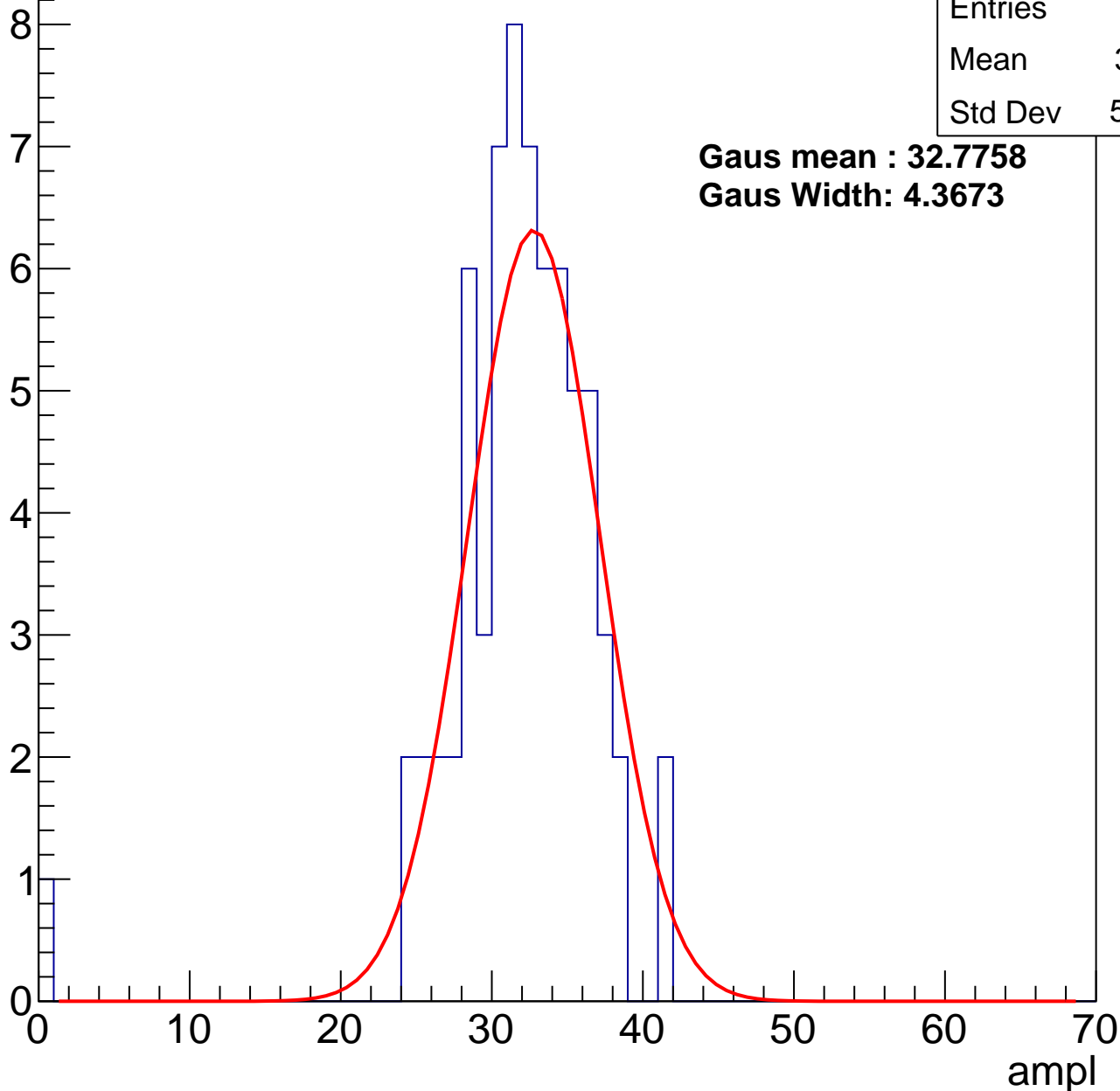
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	69
Mean	31.41
Std Dev	5.344

**Gaus mean : 32.7758**

**Gaus Width: 4.3673**



# B0L002S, U23-ch115, adc1

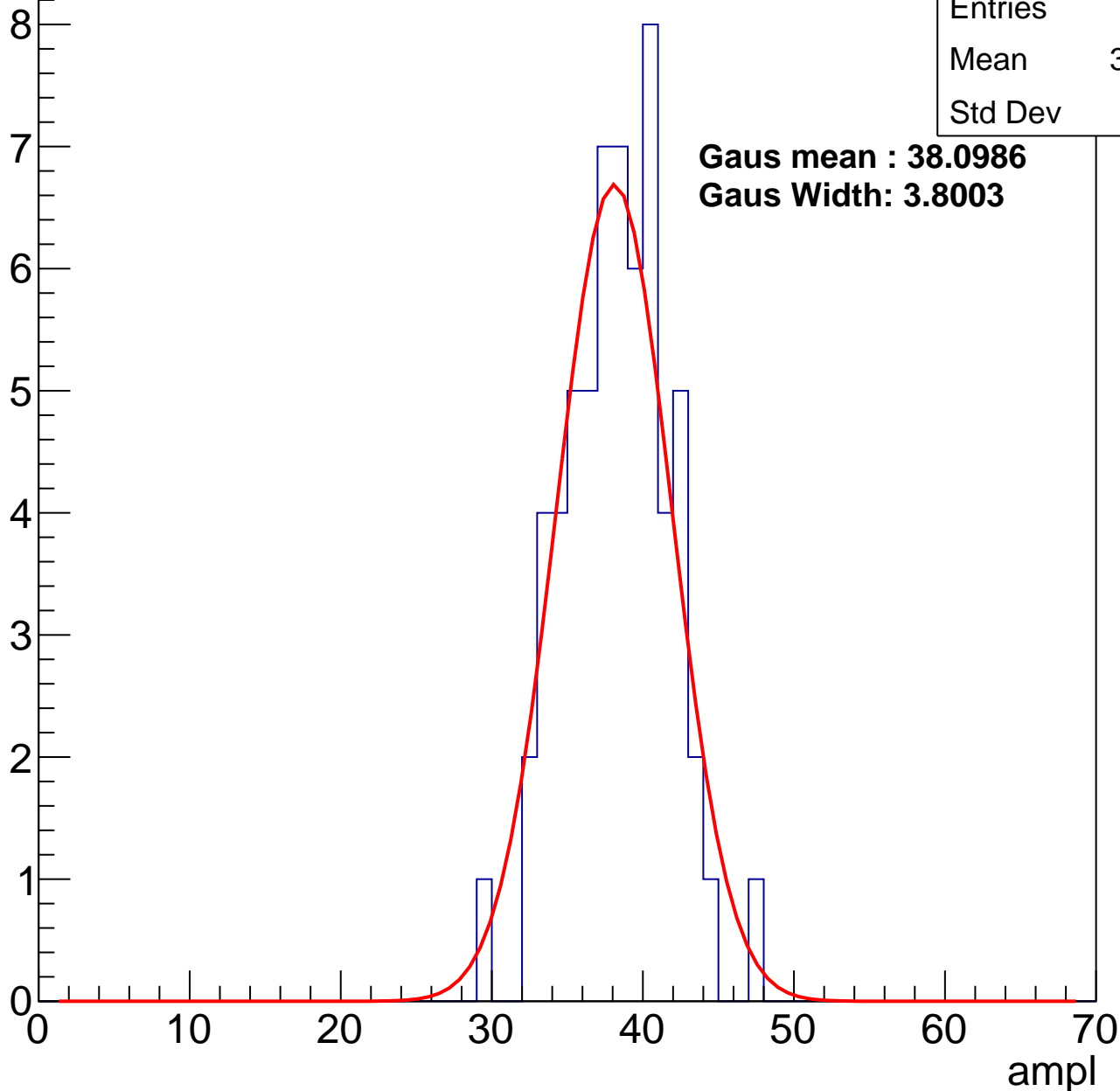
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.84
Std Dev	3.38

**Gaus mean : 38.0986**

**Gaus Width: 3.8003**



# B0L002S, U23-ch115, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.86
Std Dev	3.177

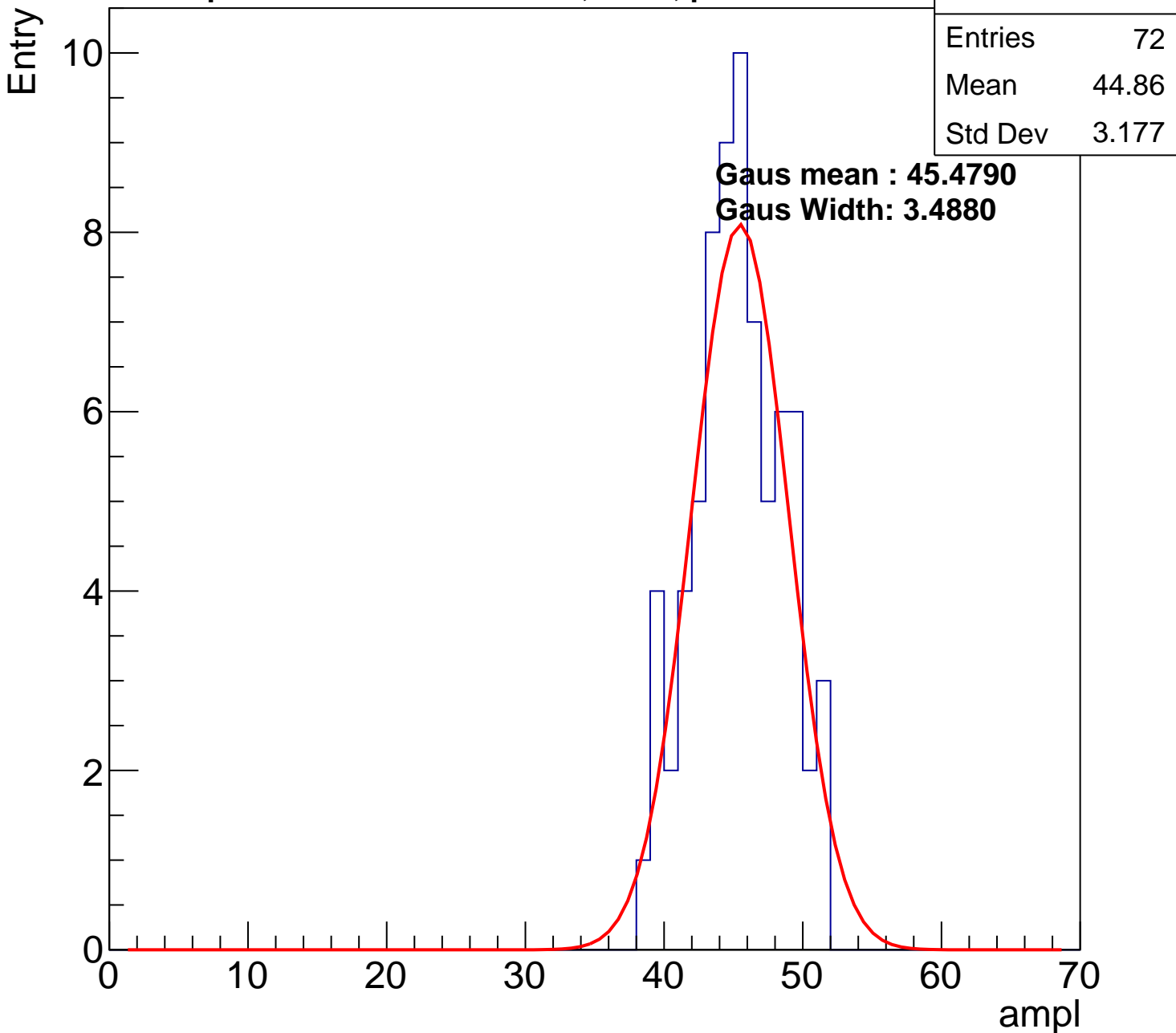
**Gaus mean : 45.4790**

**Gaus Width: 3.4880**

10  
8  
6  
4  
2  
0

ampl

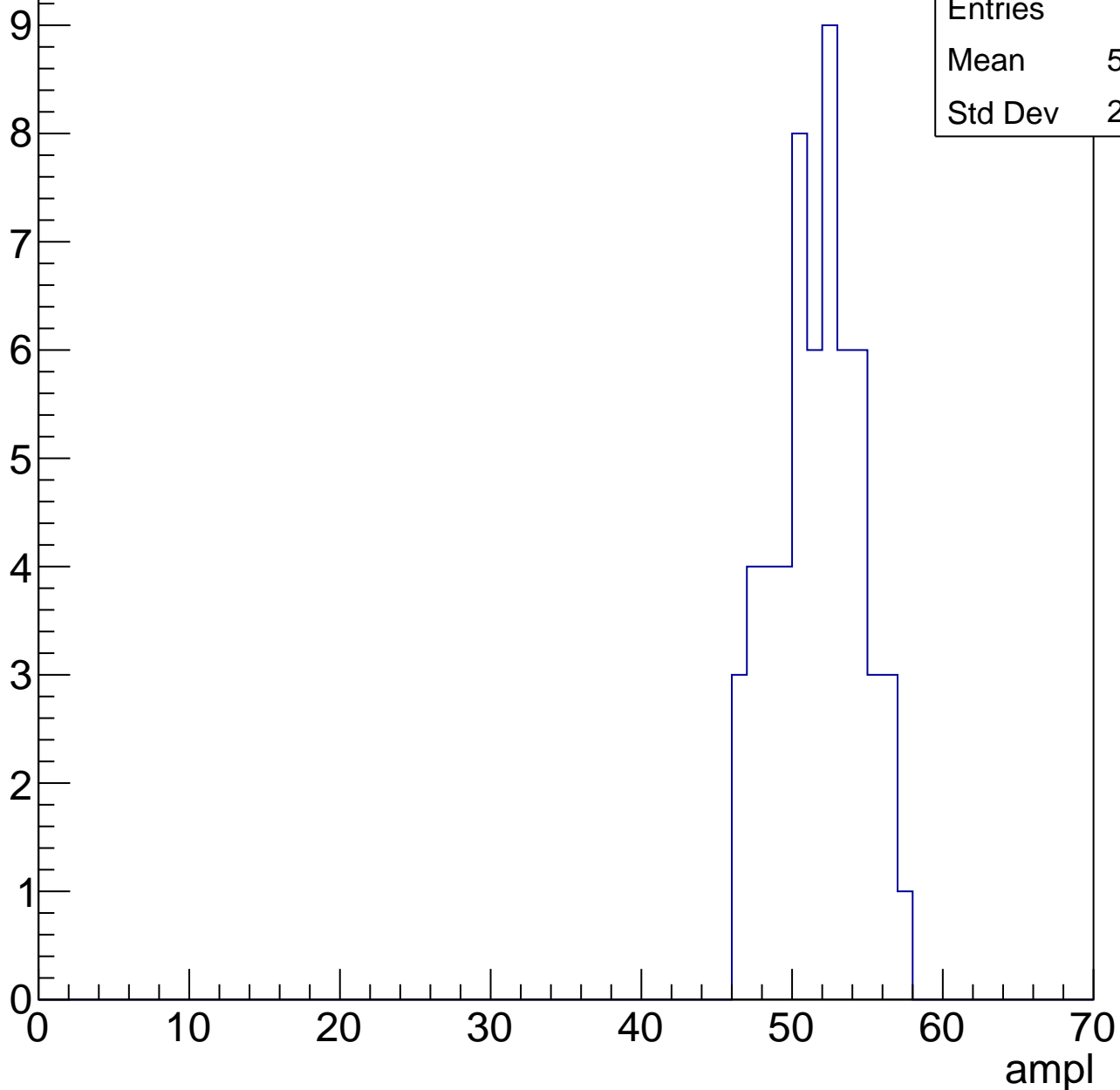
0 10 20 30 40 50 60 70



# B0L002S, U23-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



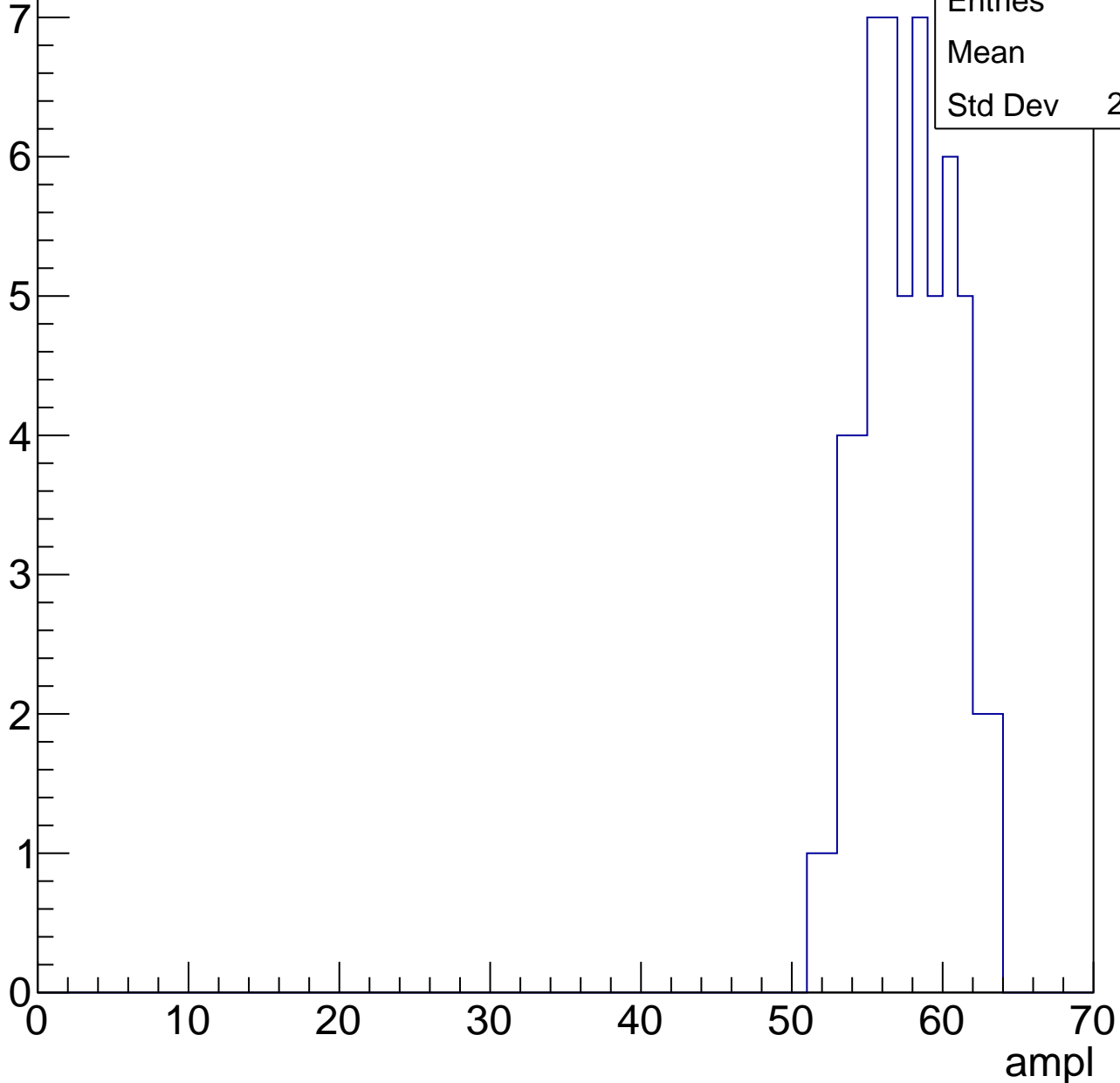
Entries	57
Mean	51.23
Std Dev	2.785

# B0L002S, U23-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	57.3
Std Dev	2.909

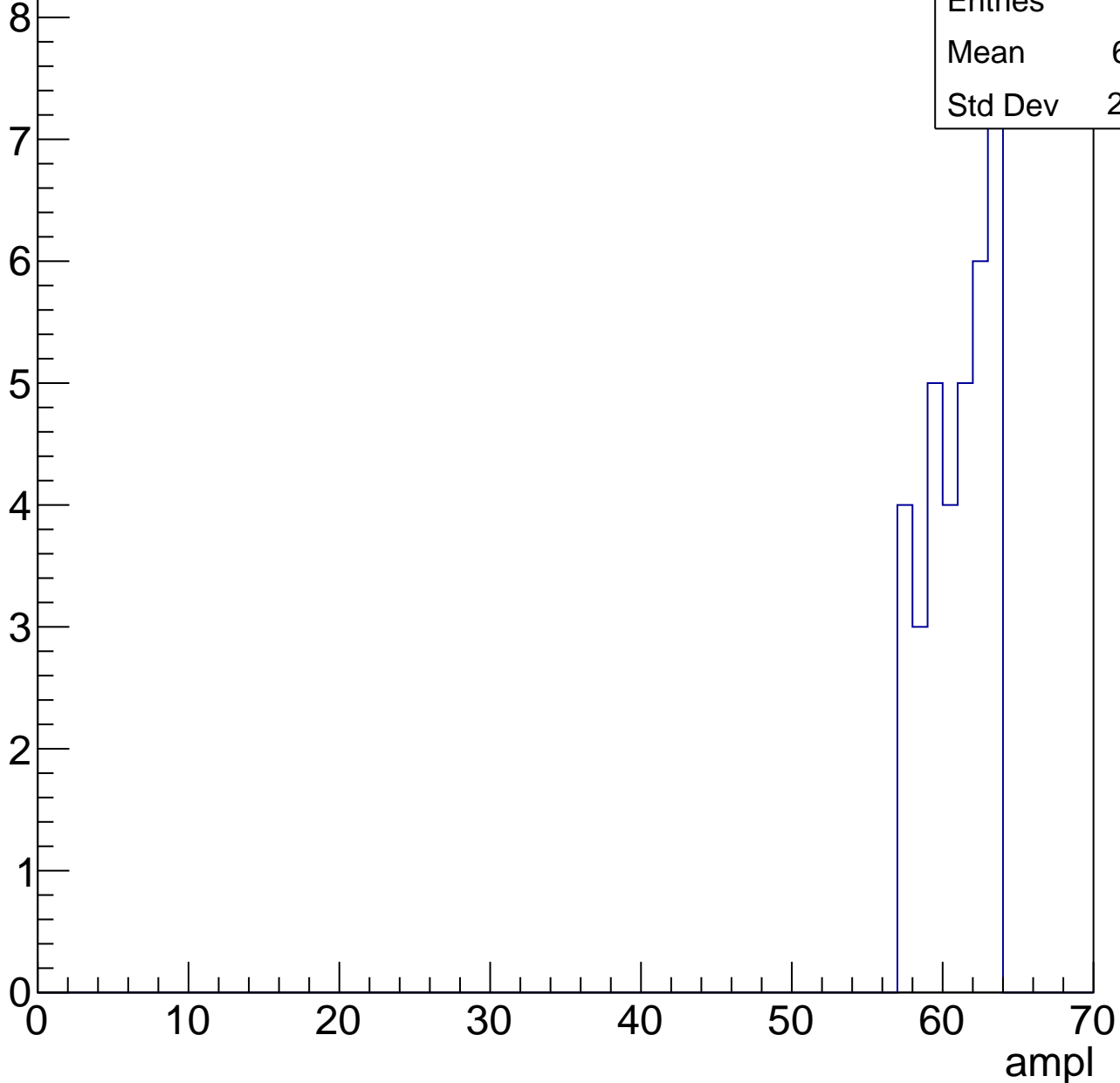


# B0L002S, U23-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

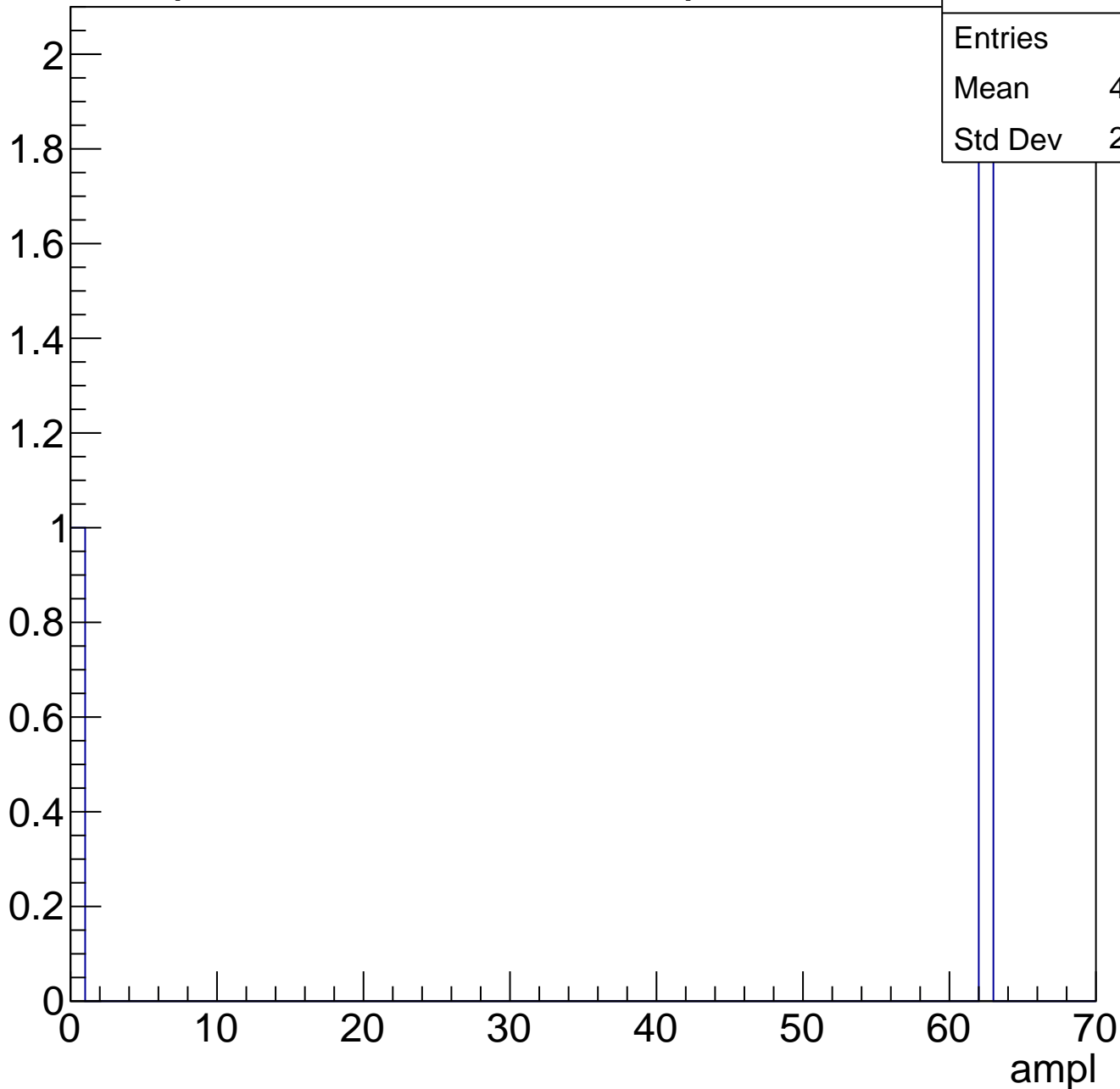
Entries	35
Mean	60.51
Std Dev	2.034



# B0L002S, U23-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch116, adc0

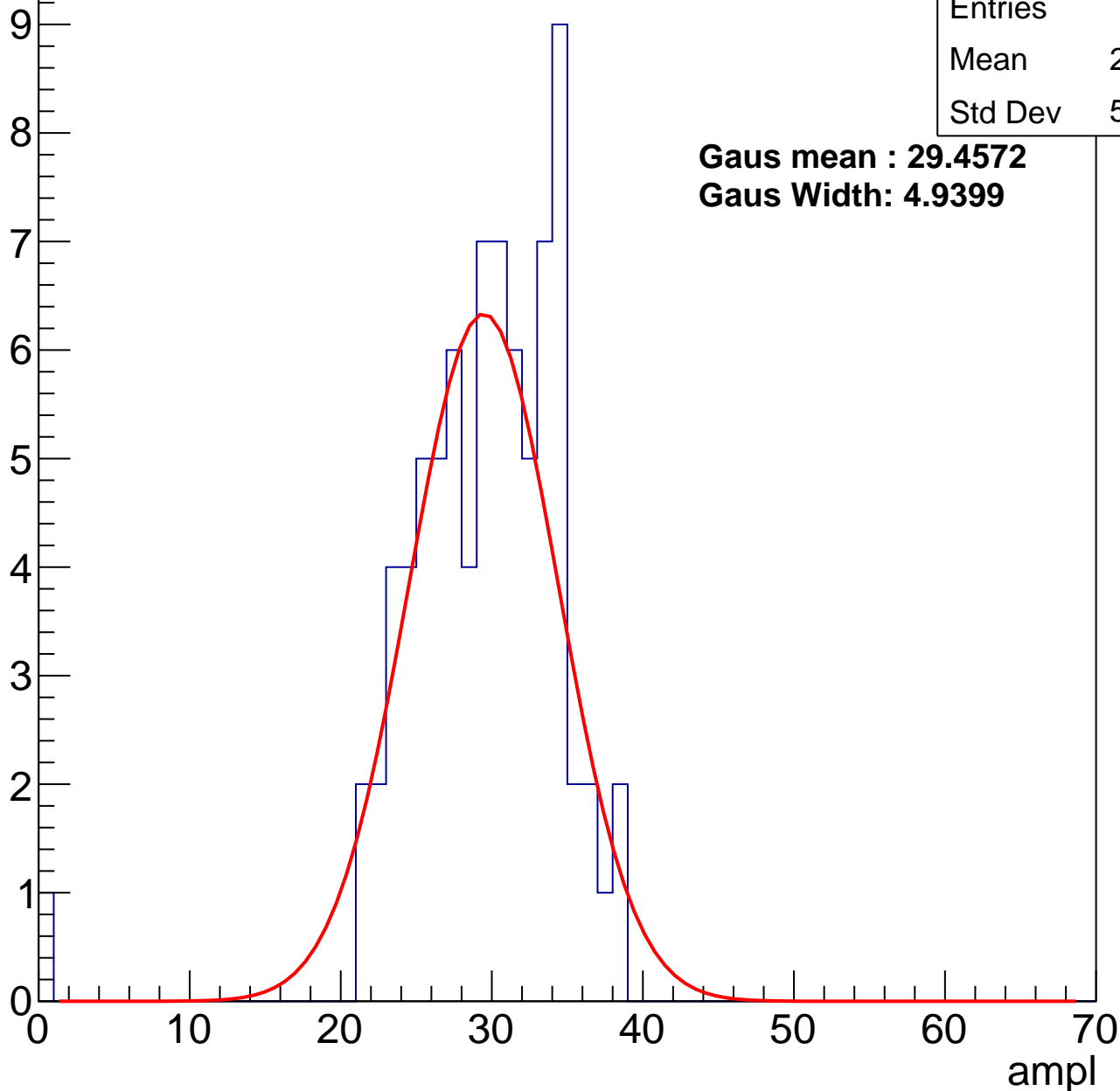
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	81
Mean	29.06
Std Dev	5.297

**Gaus mean : 29.4572**

**Gaus Width: 4.9399**



# B0L002S, U23-ch116, adc1

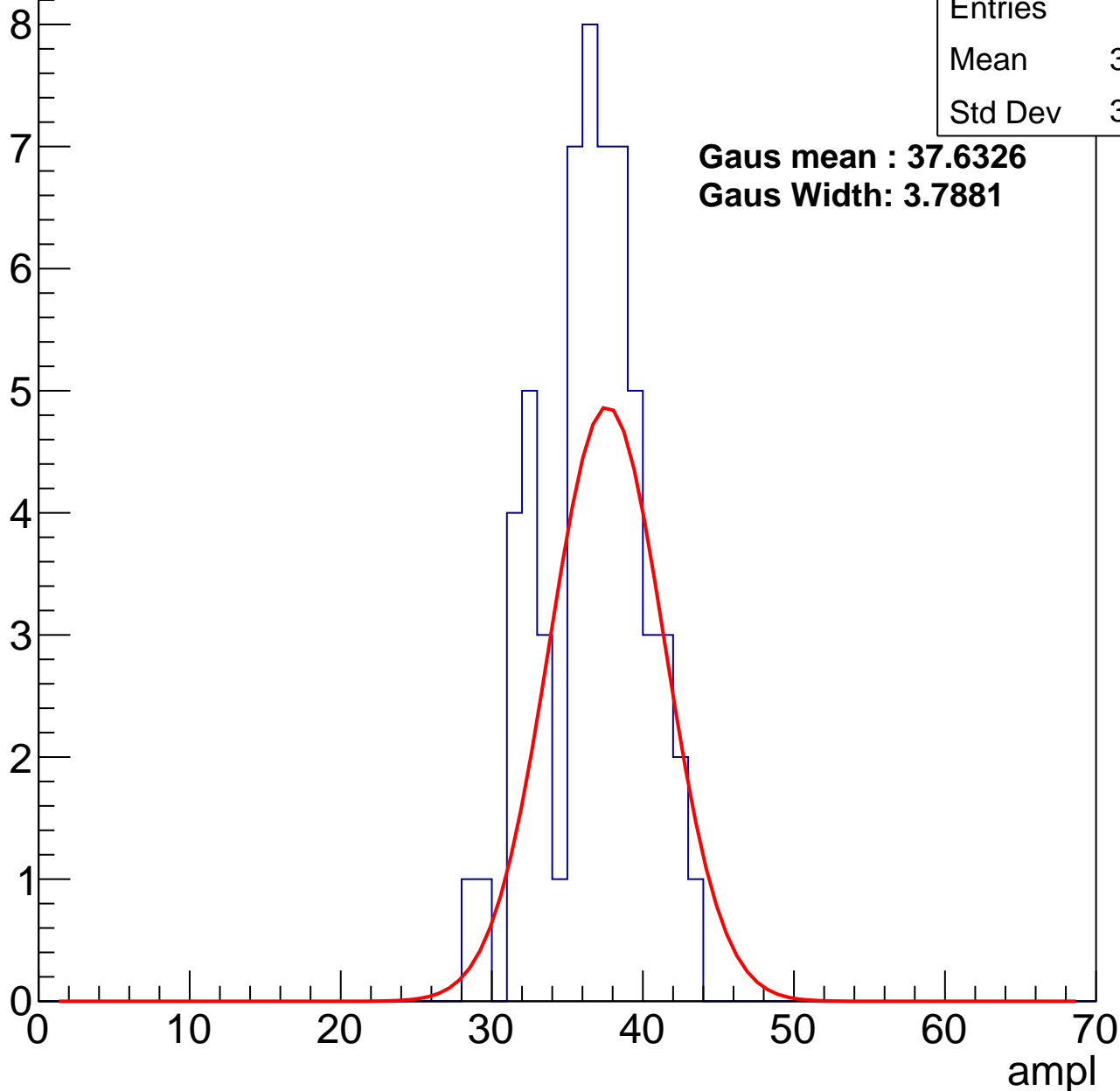
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	36.16
Std Dev	3.357

**Gaus mean : 37.6326**

**Gaus Width: 3.7881**



# B0L002S, U23-ch116, adc2

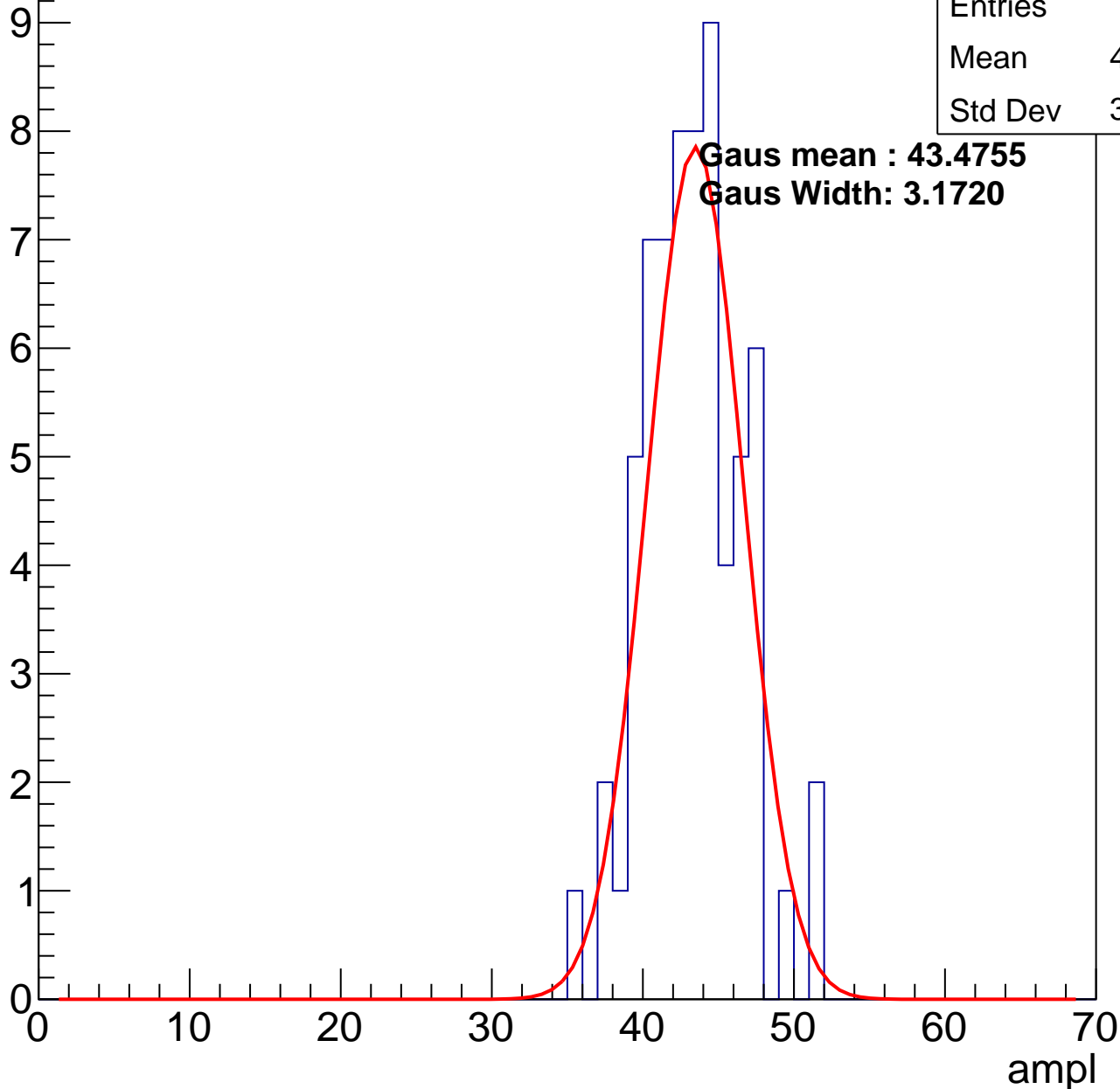
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.85
Std Dev	3.183

**Gaus mean : 43.4755**

**Gaus Width: 3.1720**

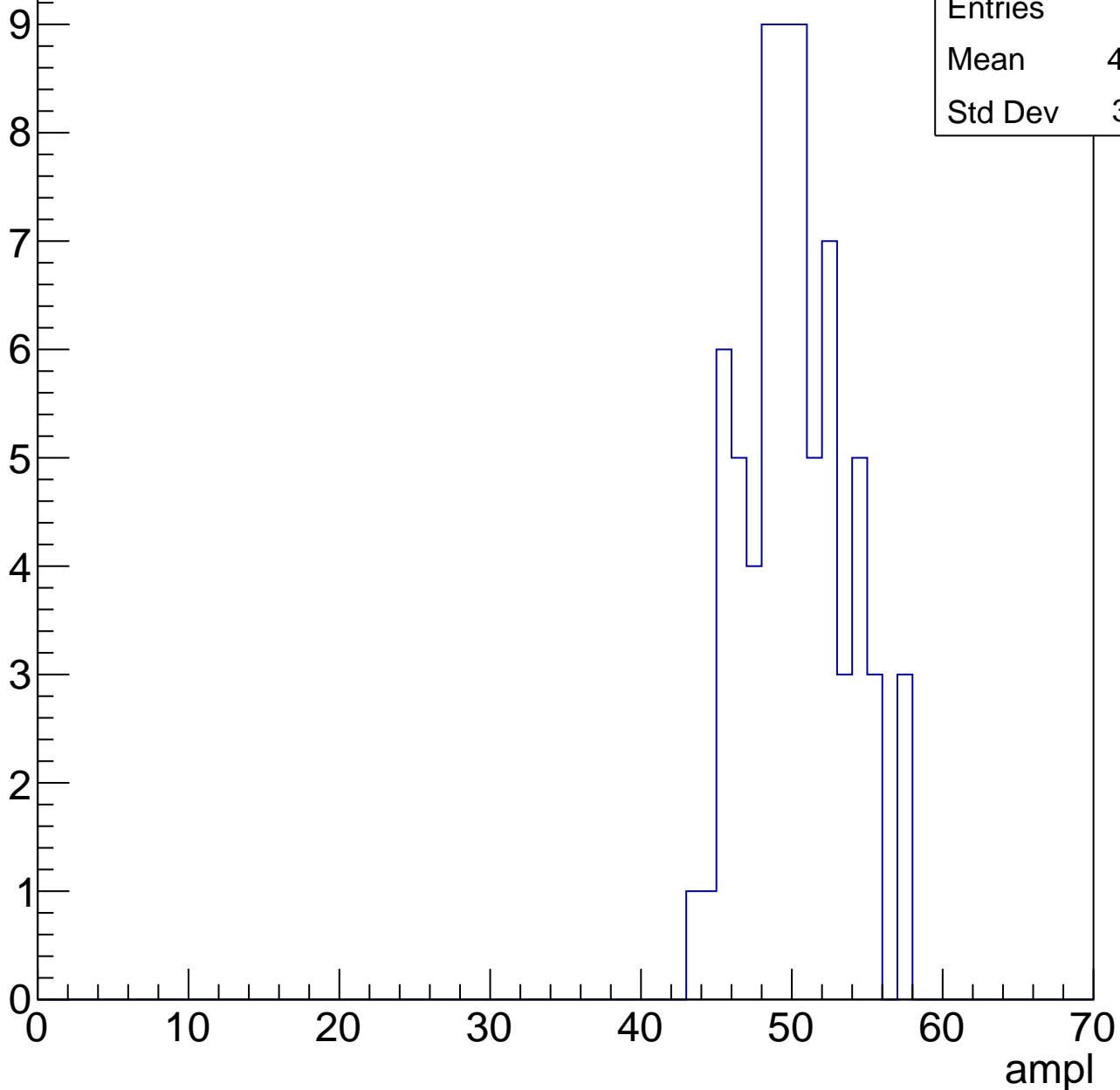


# B0L002S, U23-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	49.74
Std Dev	3.281



# B0L002S, U23-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

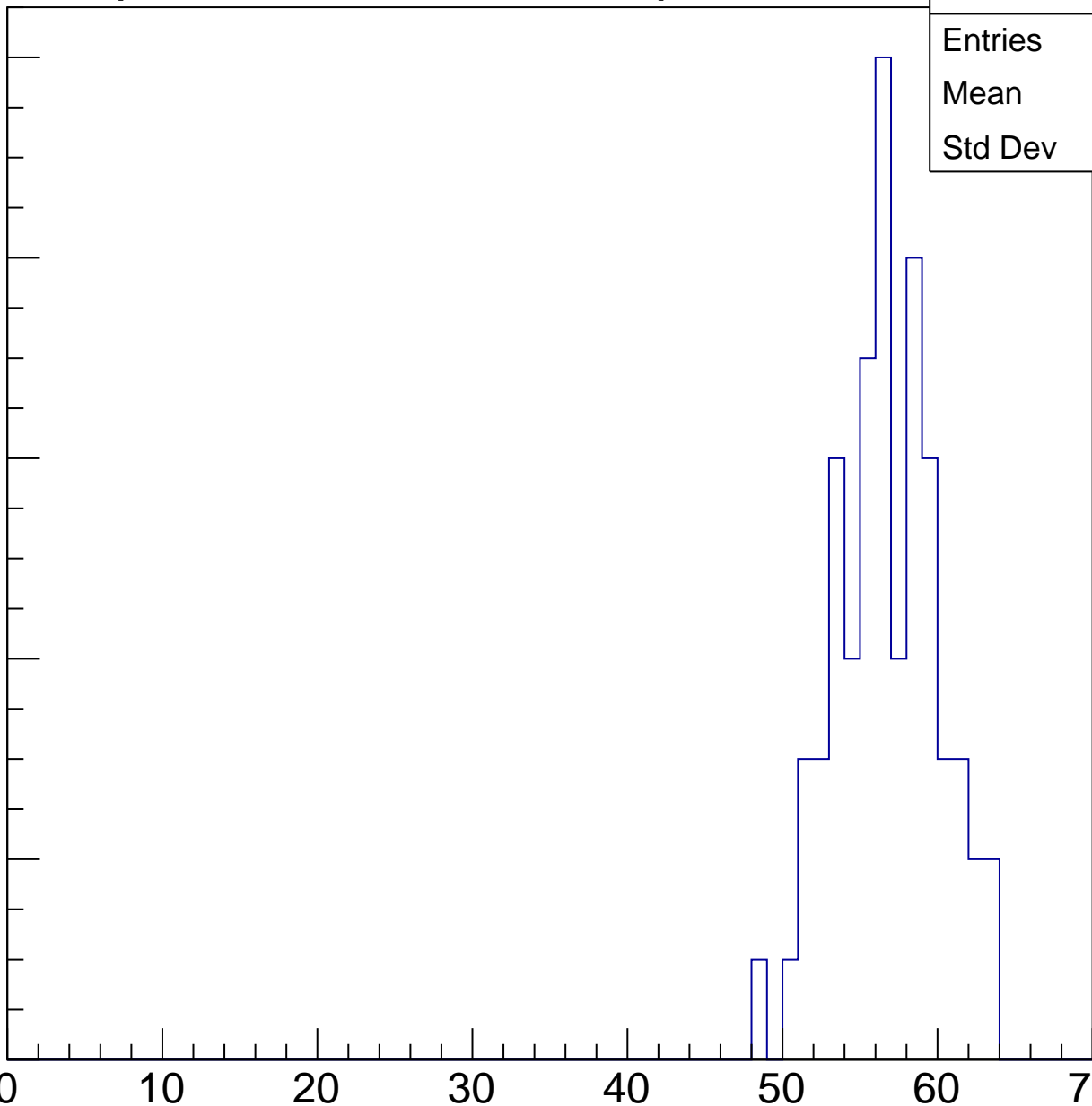
Entries	63
Mean	56.27
Std Dev	3.282

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

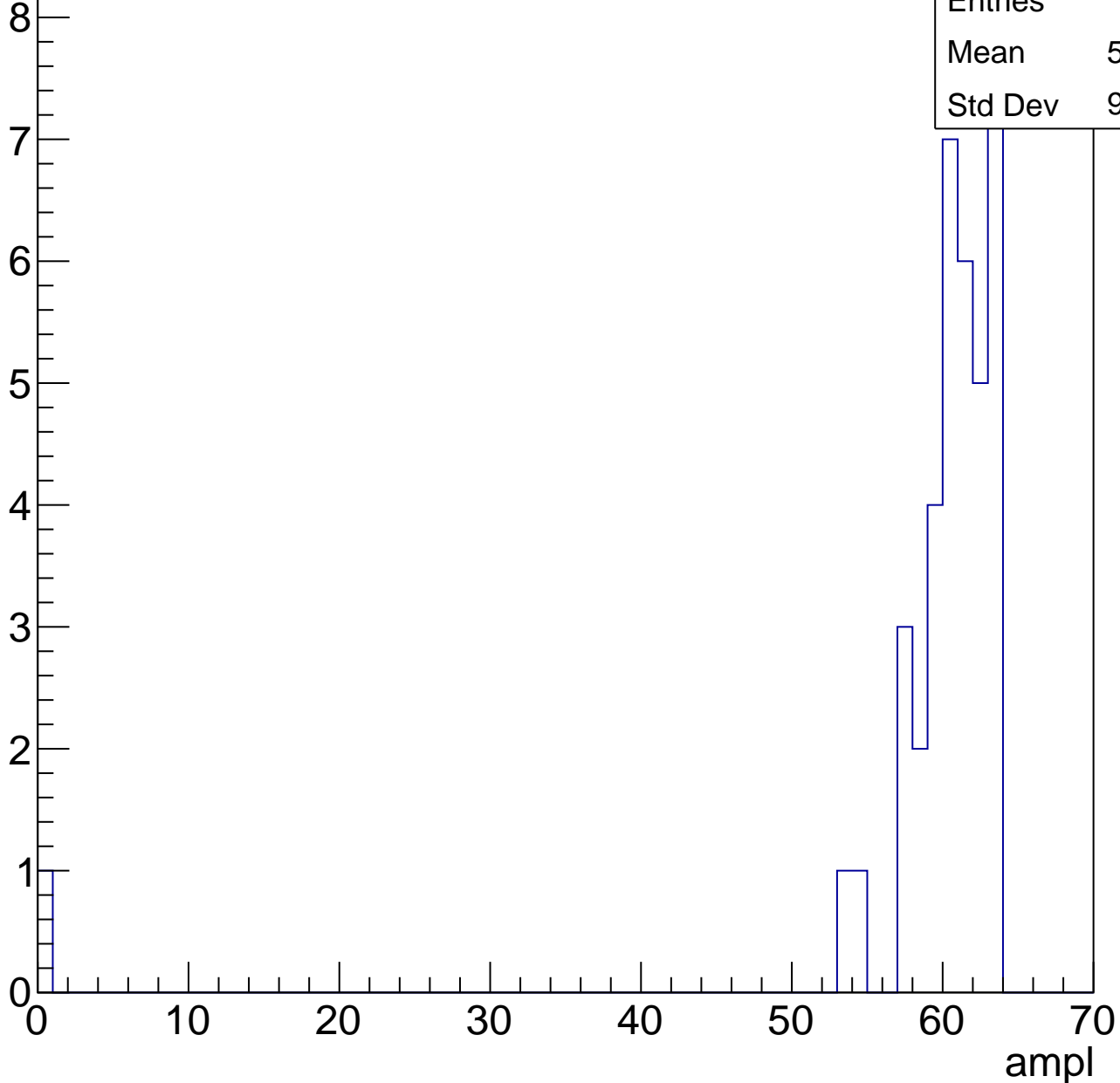


# B0L002S, U23-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.68
Std Dev	9.942



# B0L002S, U23-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch117, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	84
Mean	30.89
Std Dev	5.12

**Gaus mean : 31.5781**

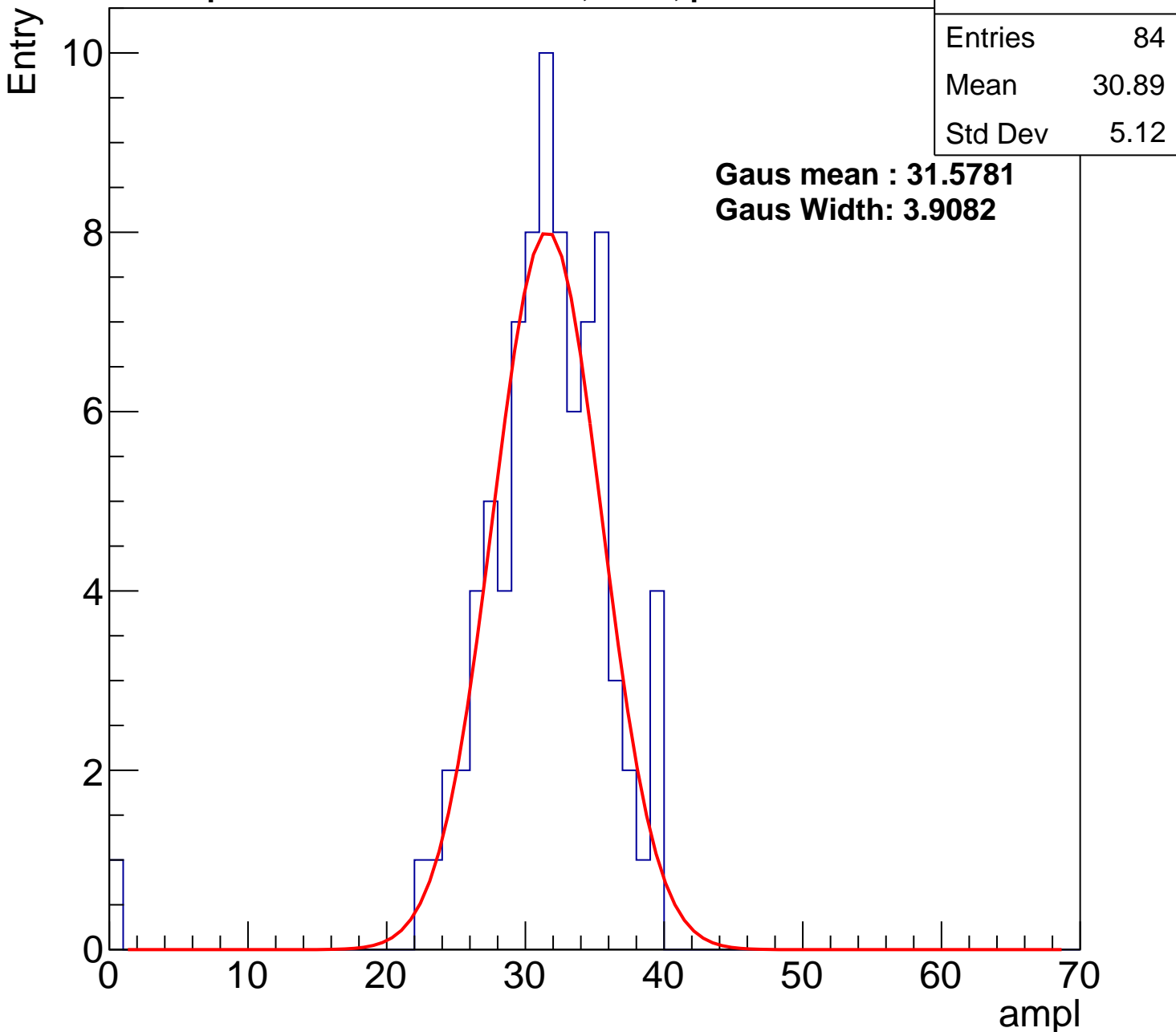
**Gaus Width: 3.9082**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch117, adc1

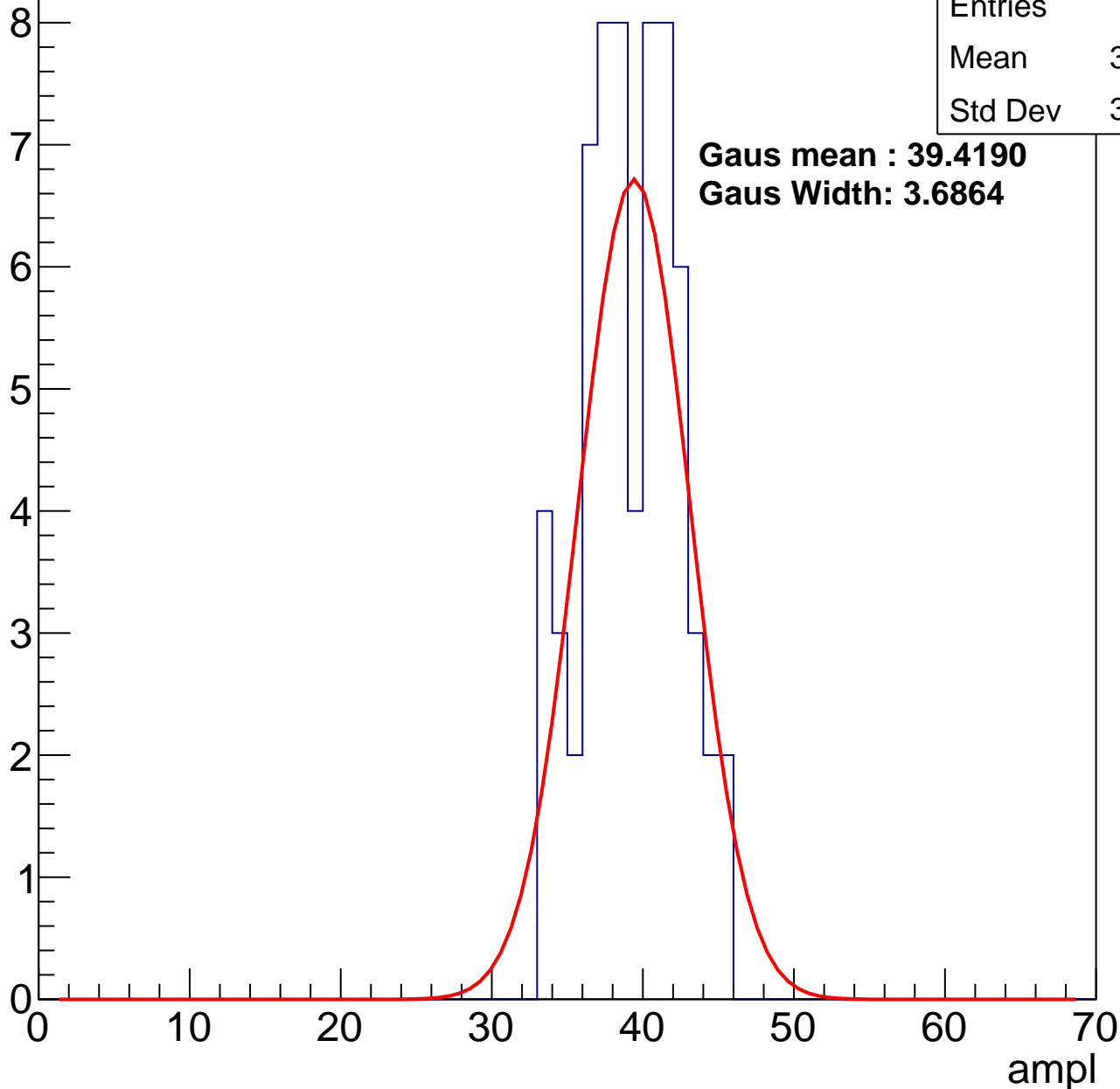
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	38.75
Std Dev	3.074

**Gaus mean : 39.4190**

**Gaus Width: 3.6864**



# B0L002S, U23-ch117, adc2

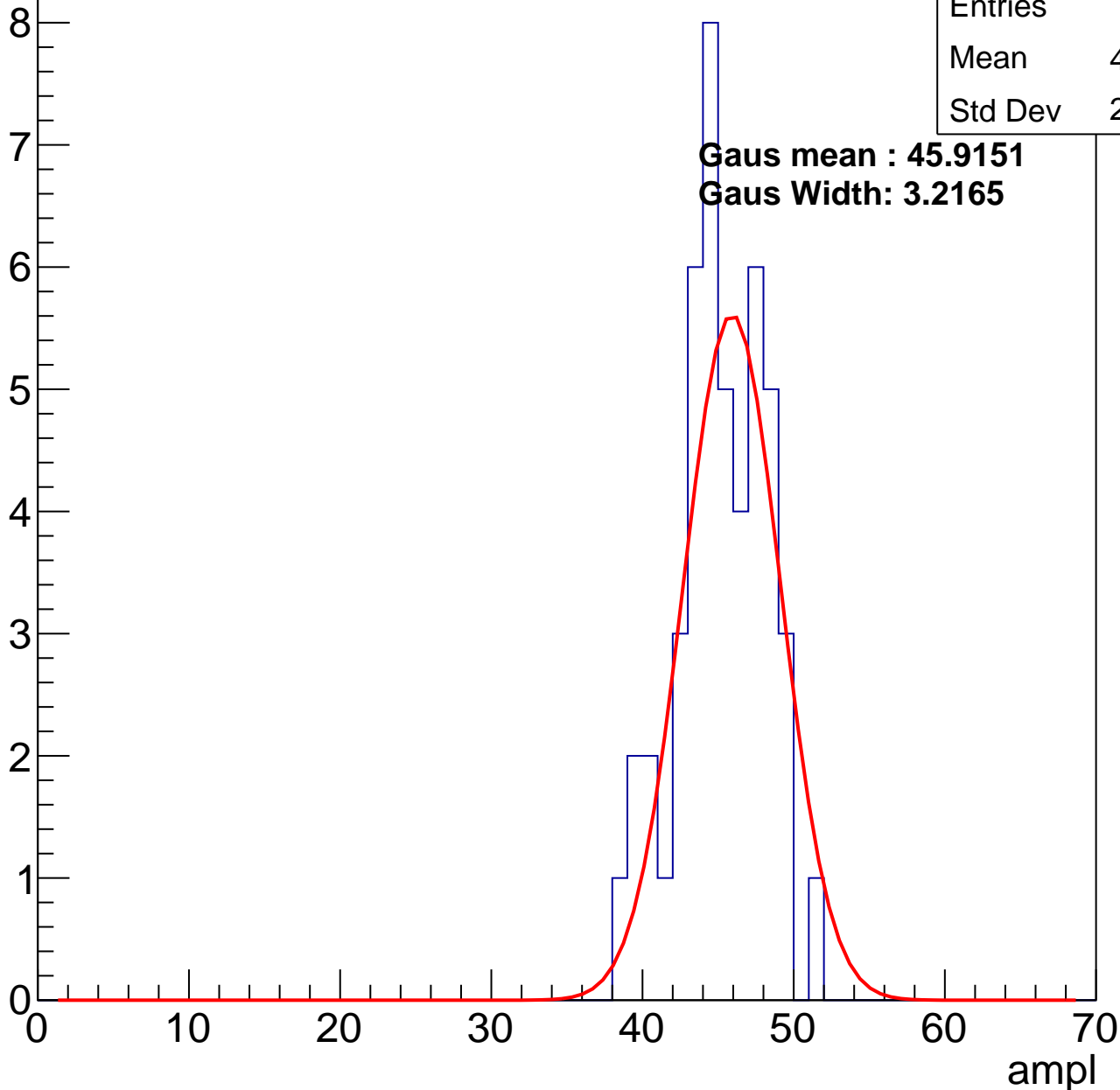
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	47
Mean	44.72
Std Dev	2.915

**Gaus mean : 45.9151**

**Gaus Width: 3.2165**

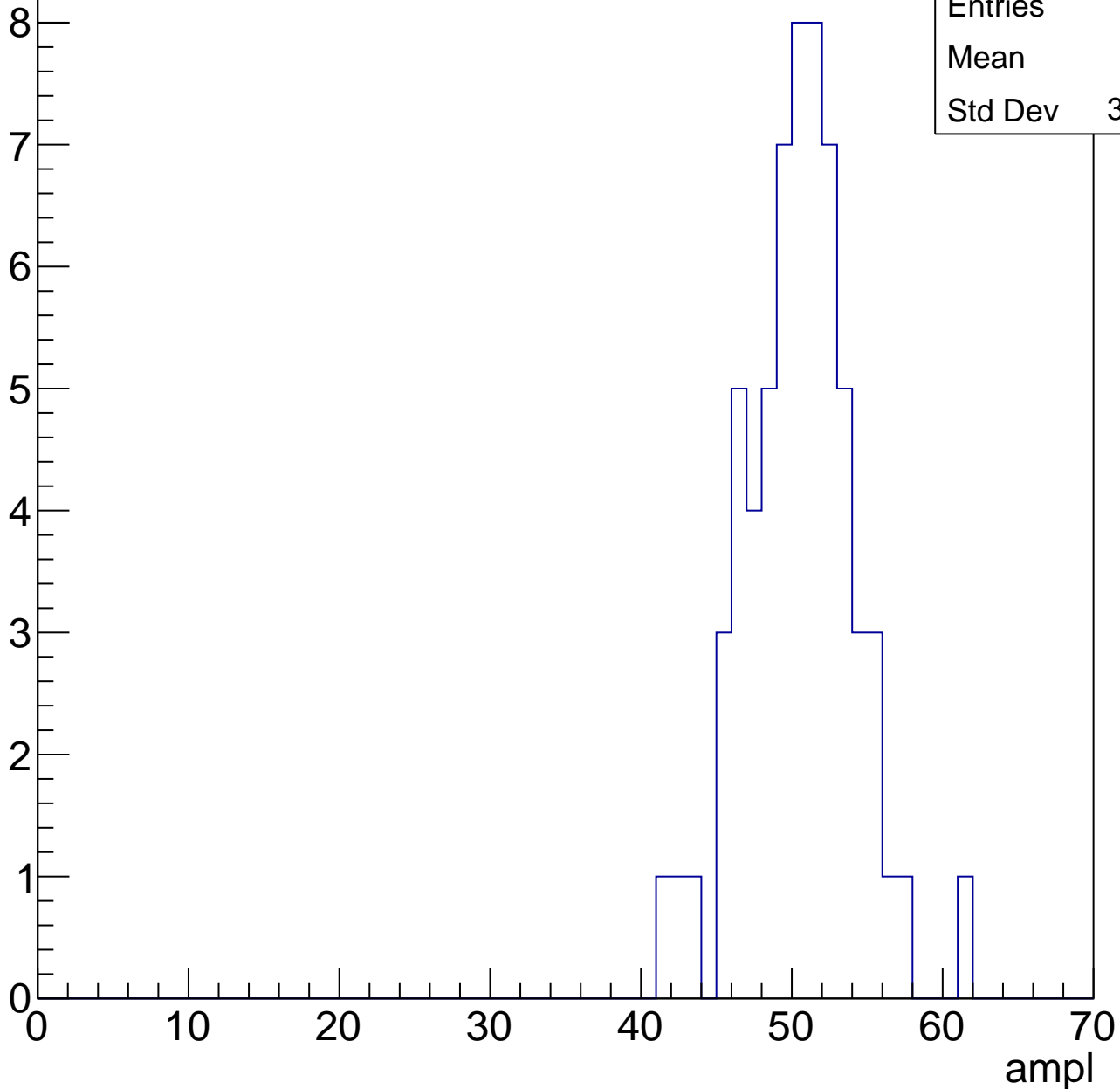


# B0L002S, U23-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	50
Std Dev	3.584

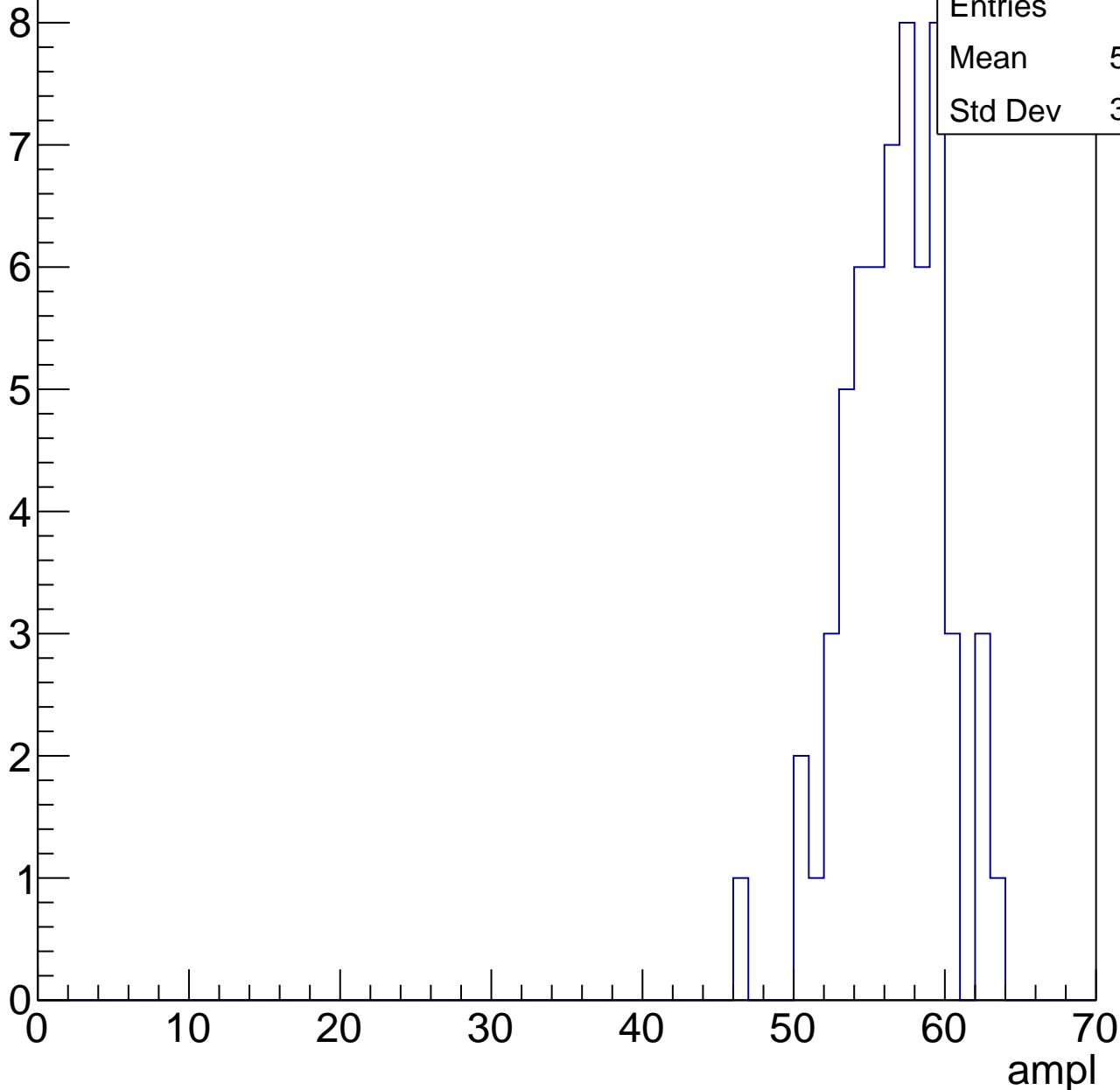


# B0L002S, U23-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.15
Std Dev	3.234

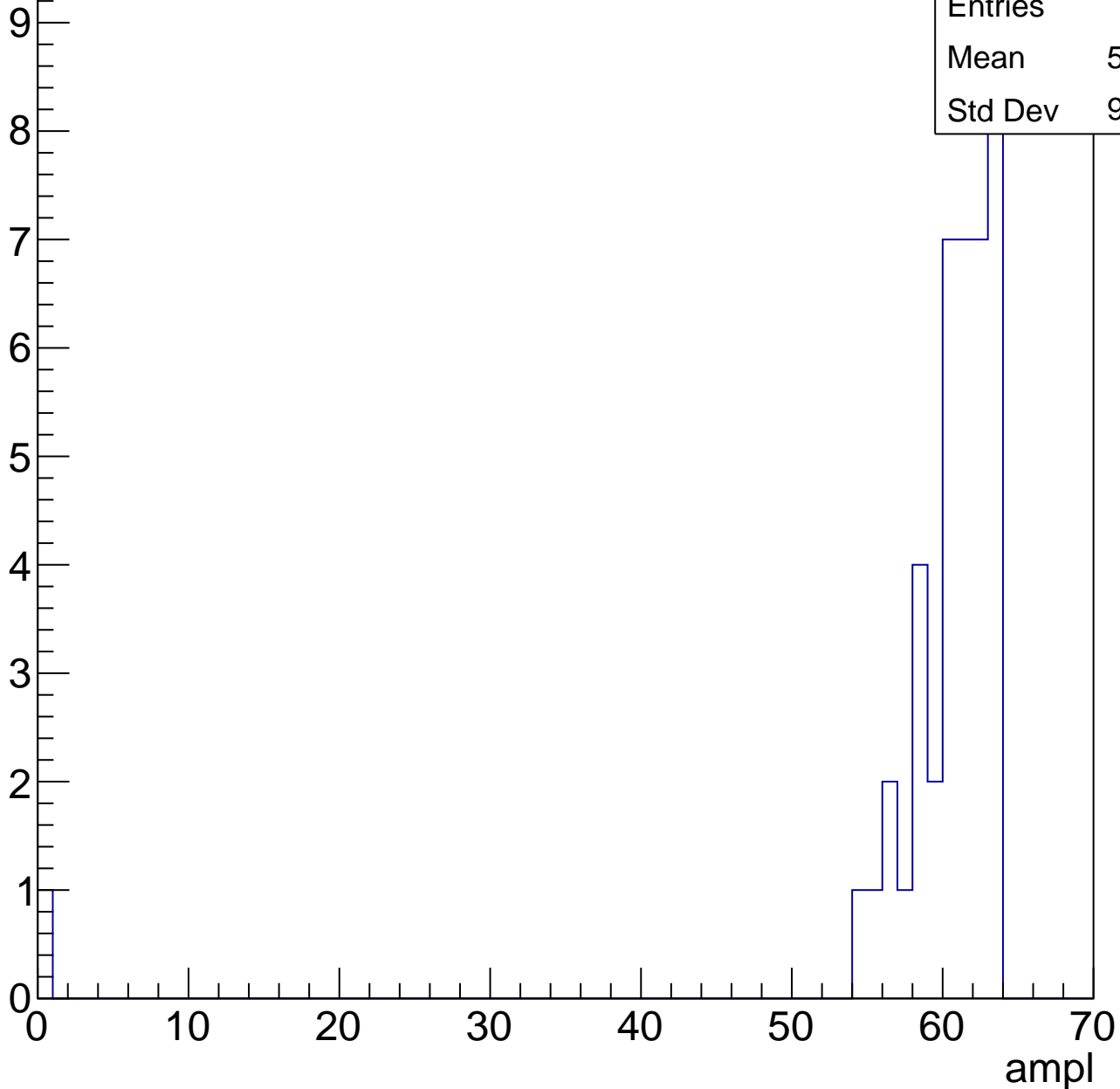


# B0L002S, U23-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

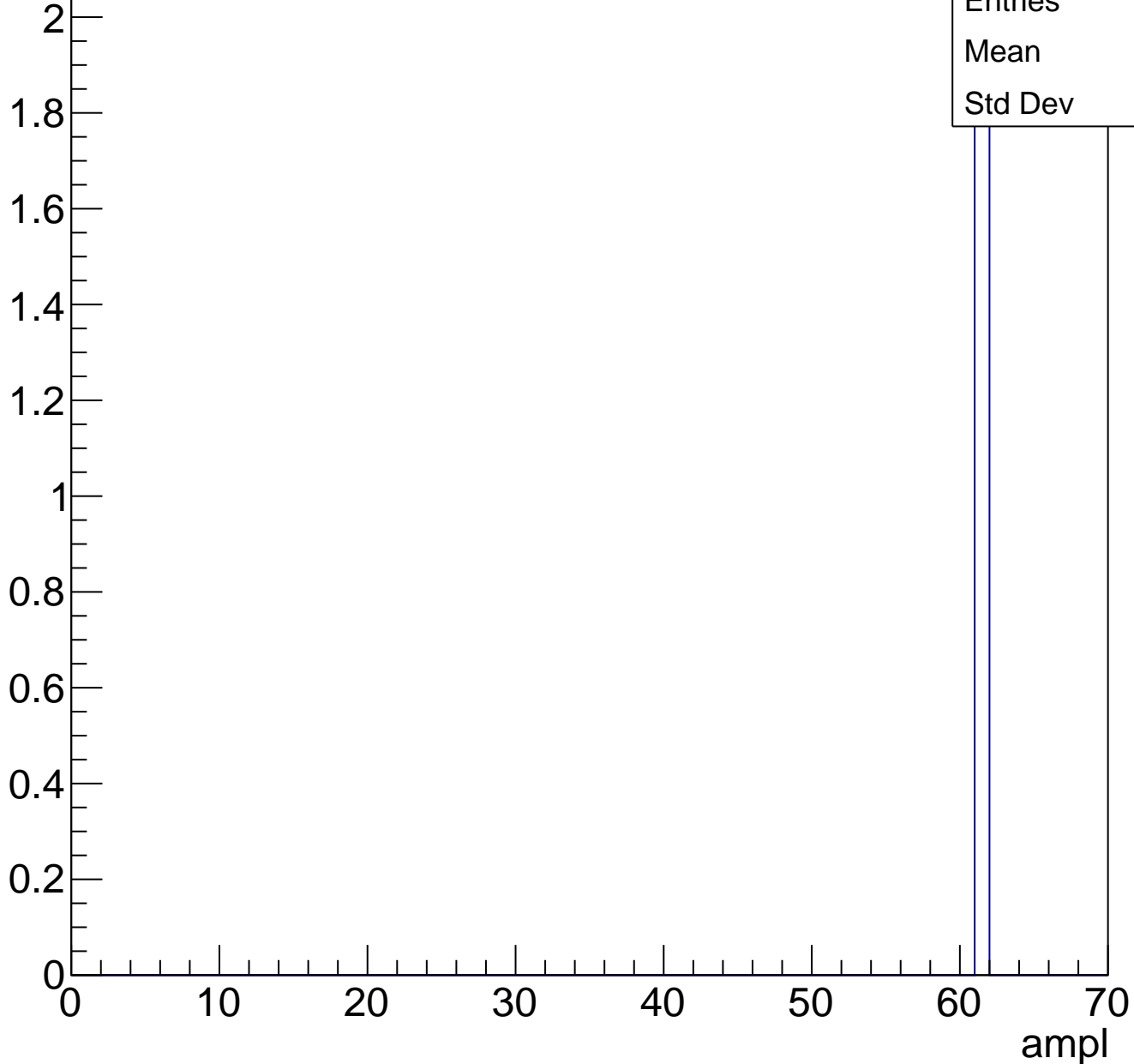
Entries	42
Mean	58.95
Std Dev	9.499



# B0L002S, U23-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch118, adc0

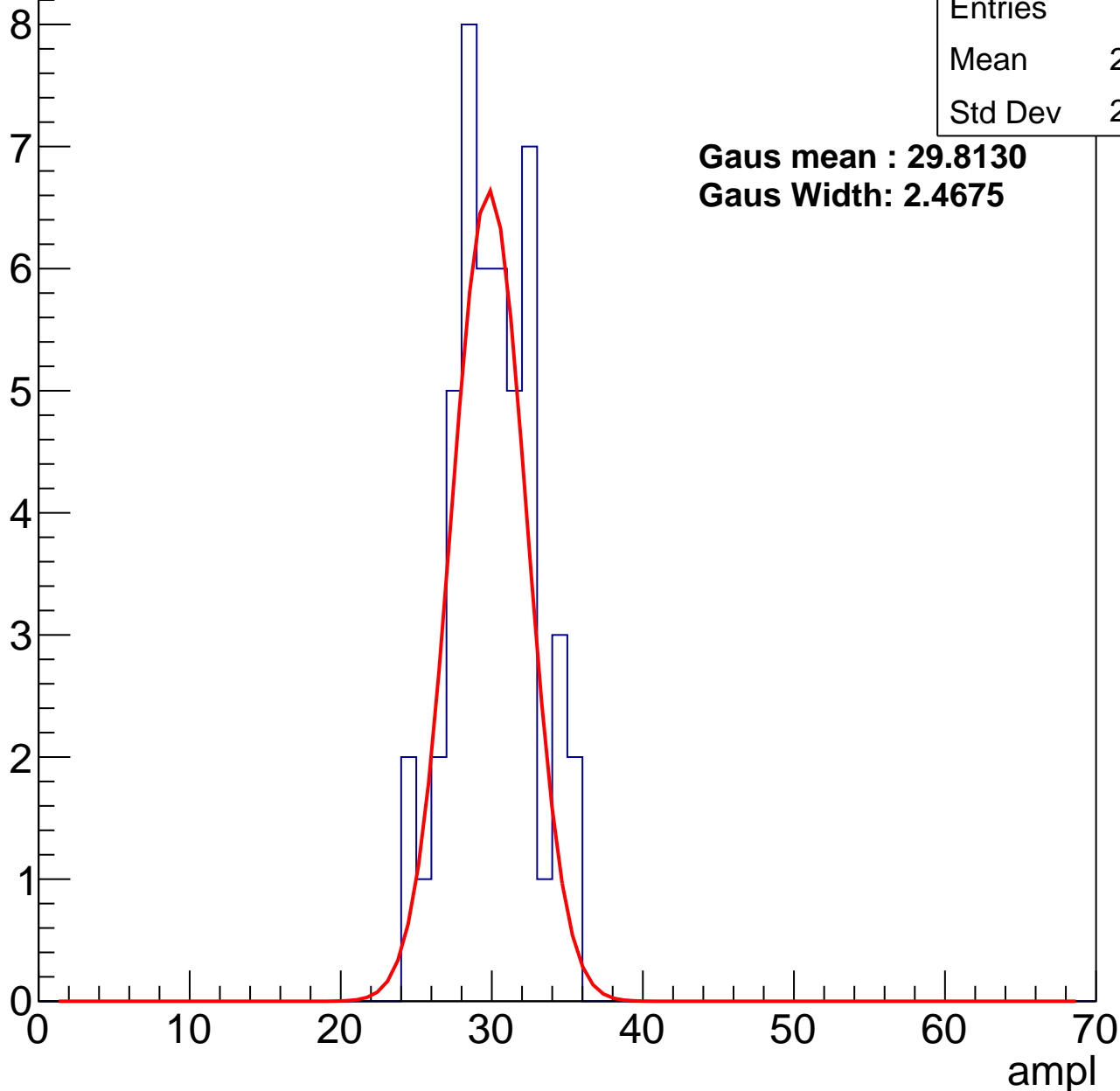
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	48
Mean	29.62
Std Dev	2.682

**Gaus mean : 29.8130**

**Gaus Width: 2.4675**



# B0L002S, U23-ch118, adc1

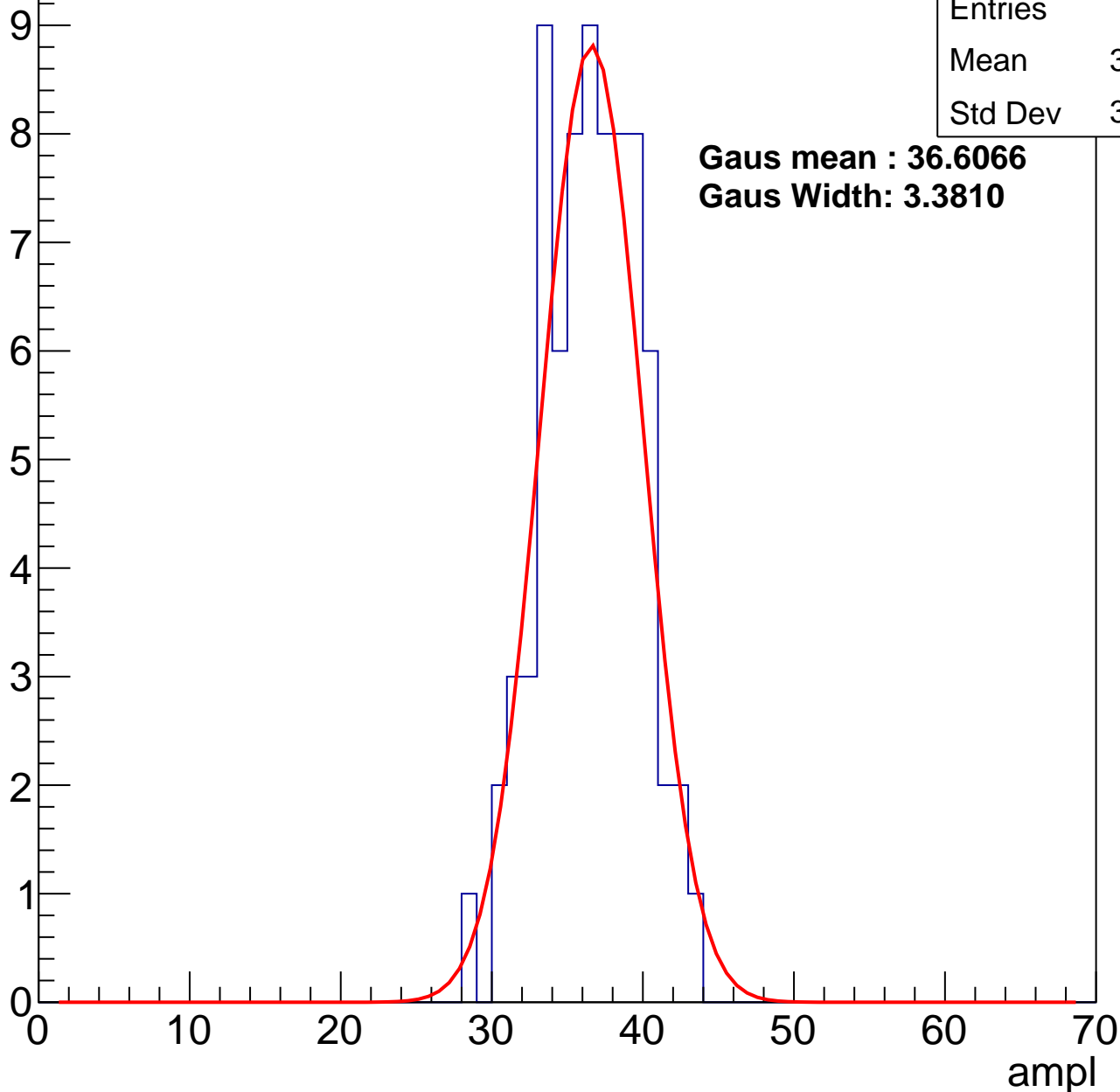
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.09
Std Dev	3.142

**Gaus mean : 36.6066**

**Gaus Width: 3.3810**



# B0L002S, U23-ch118, adc2

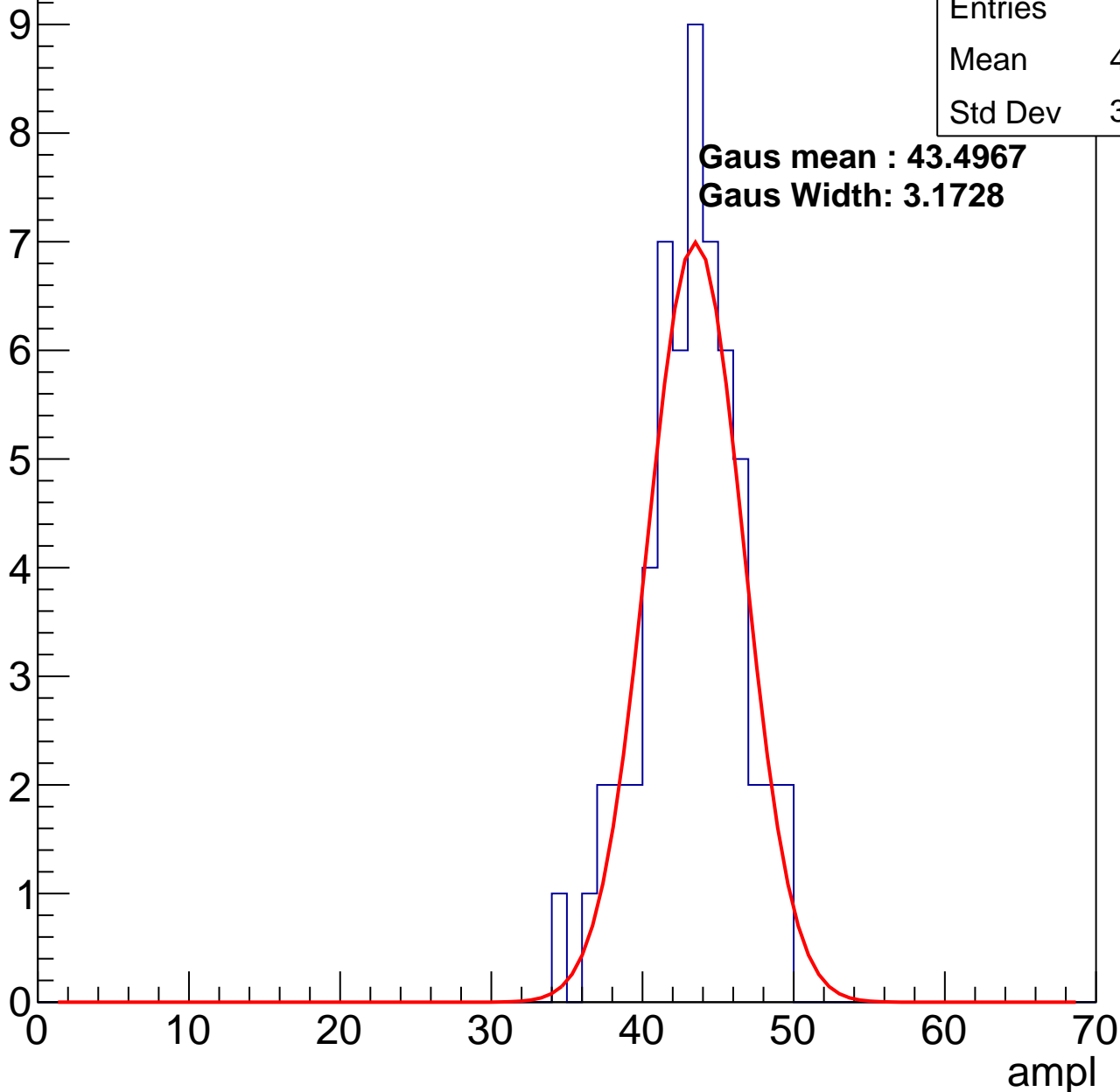
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.76
Std Dev	3.164

**Gaus mean : 43.4967**

**Gaus Width: 3.1728**

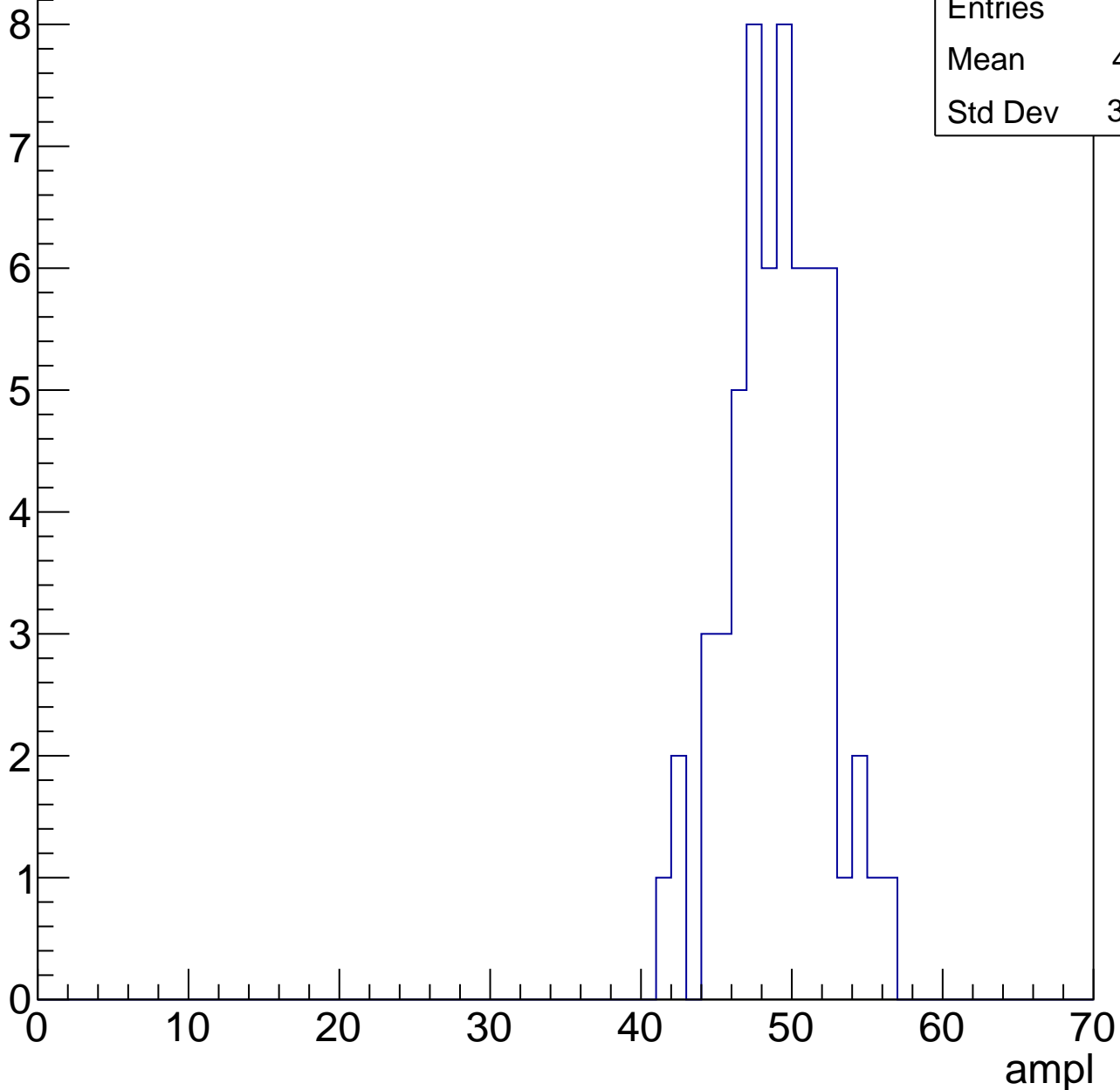


# B0L002S, U23-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	48.61
Std Dev	3.173

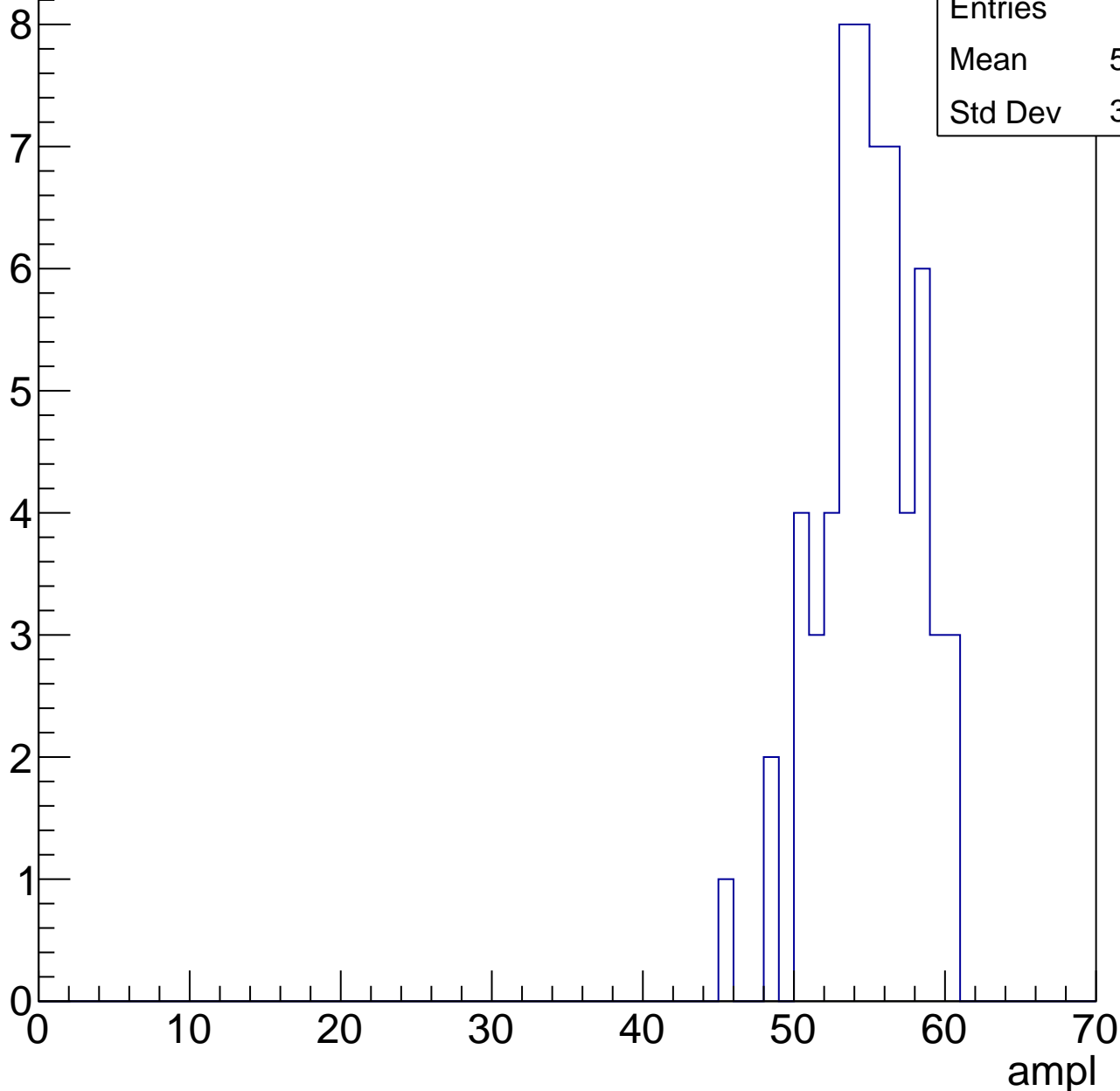


# B0L002S, U23-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	60
Mean	54.47
Std Dev	3.175

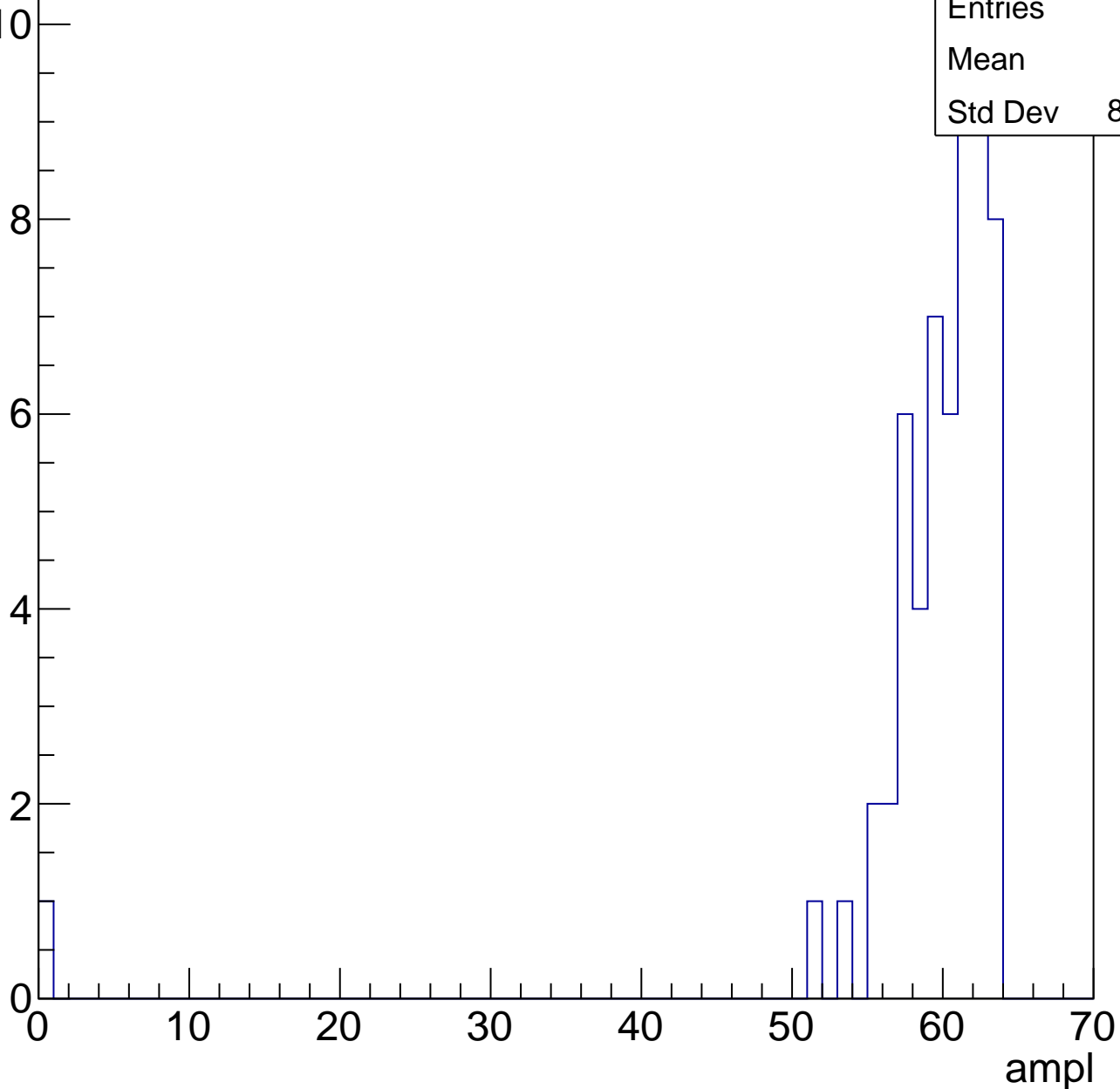


# B0L002S, U23-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	57
Mean	58.7
Std Dev	8.286



# B0L002S, U23-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch119, adc0

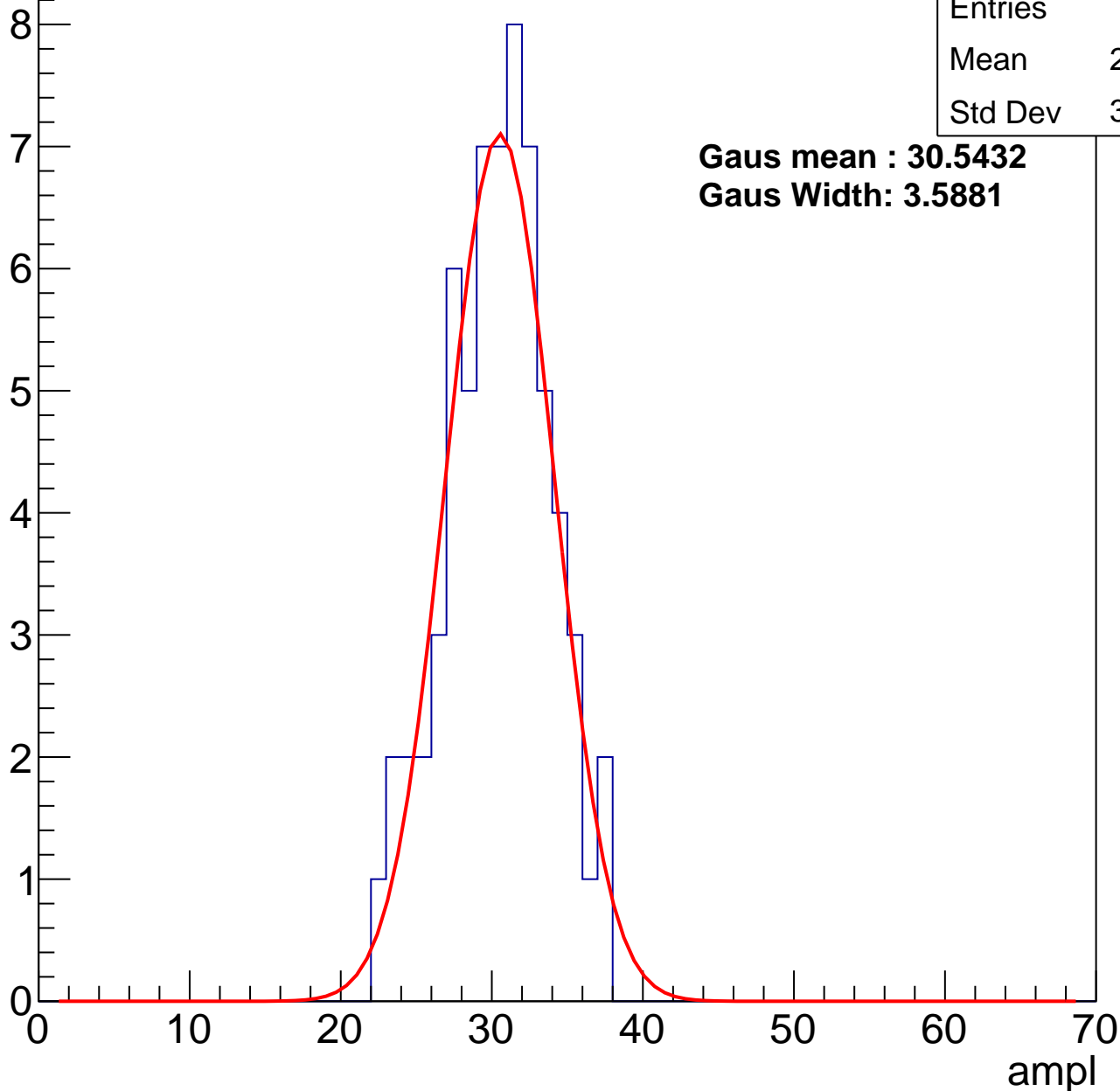
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.95
Std Dev	3.435

**Gaus mean : 30.5432**

**Gaus Width: 3.5881**



# B0L002S, U23-ch119, adc1

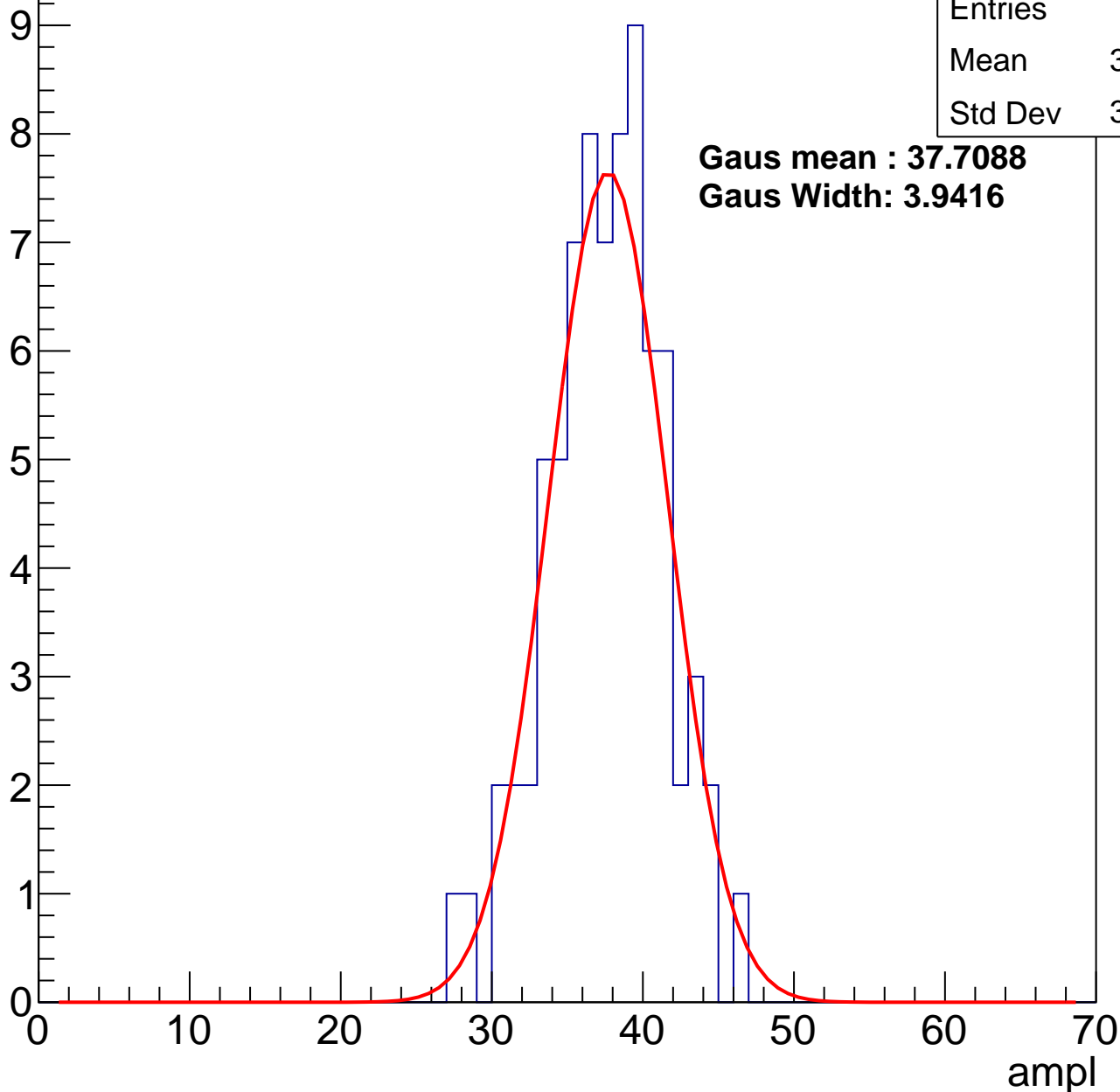
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	77
Mean	37.09
Std Dev	3.767

**Gaus mean : 37.7088**

**Gaus Width: 3.9416**



# B0L002S, U23-ch119, adc2

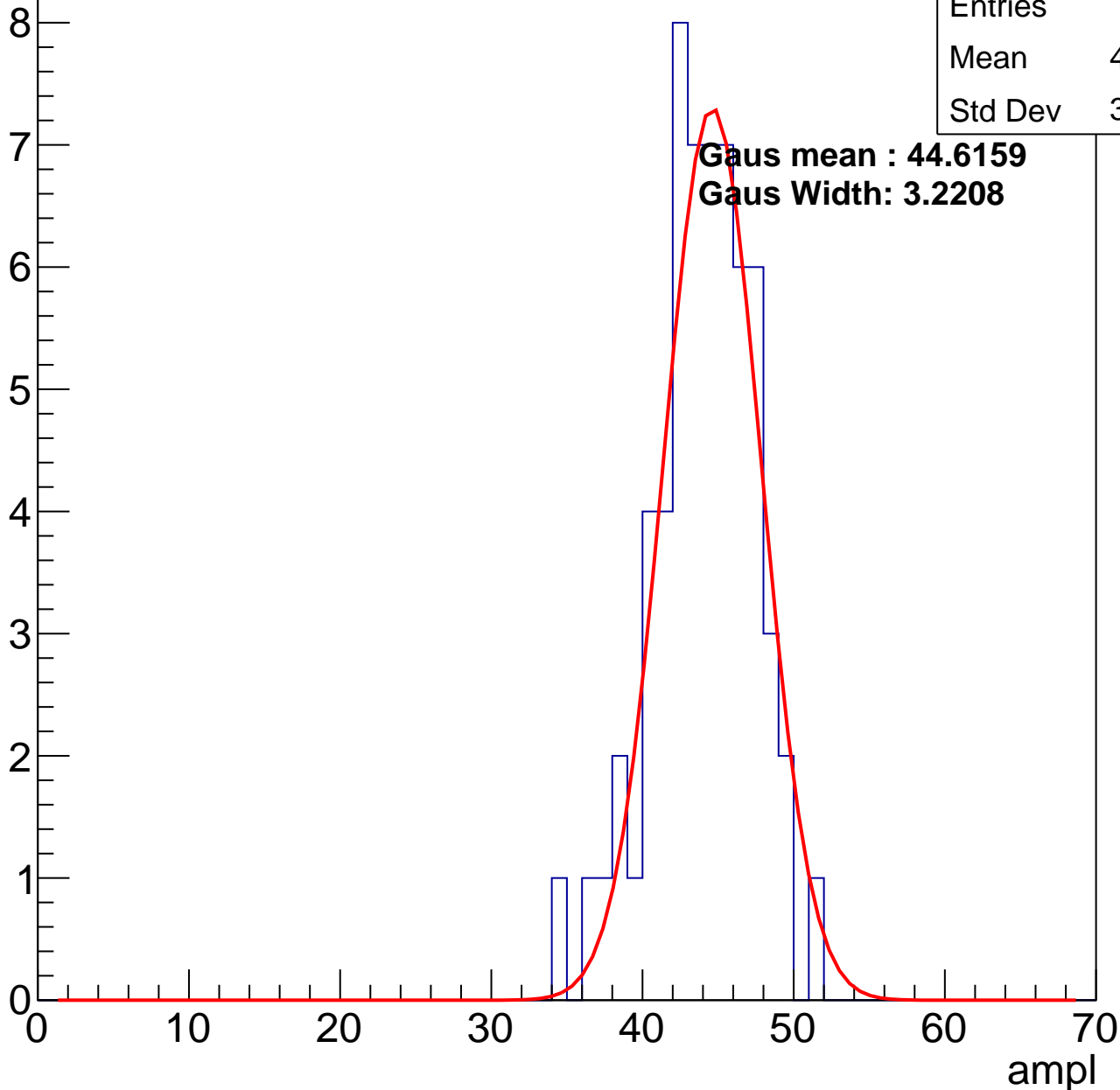
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.56
Std Dev	3.312

**Gaus mean : 44.6159**

**Gaus Width: 3.2208**

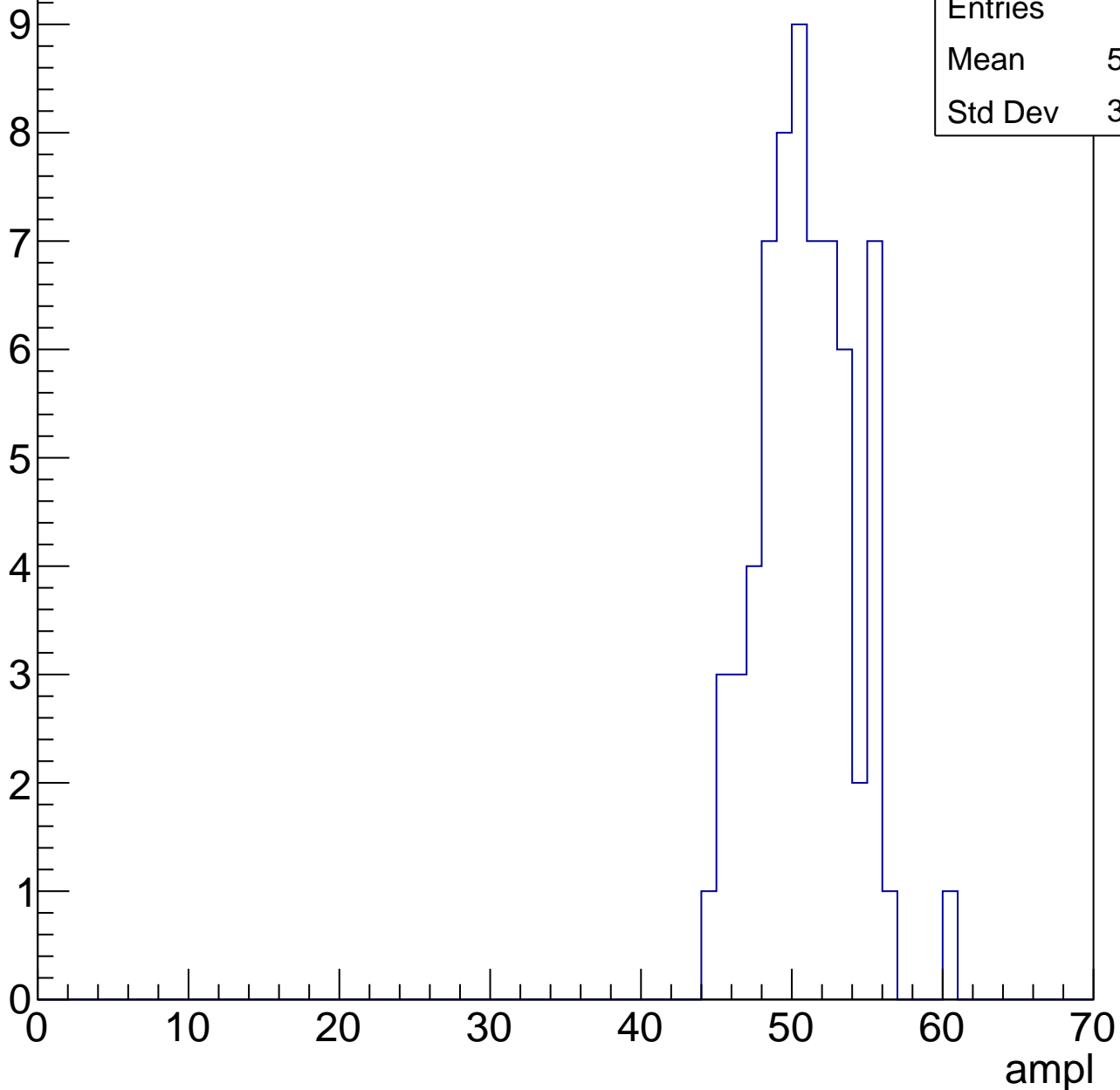


# B0L002S, U23-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

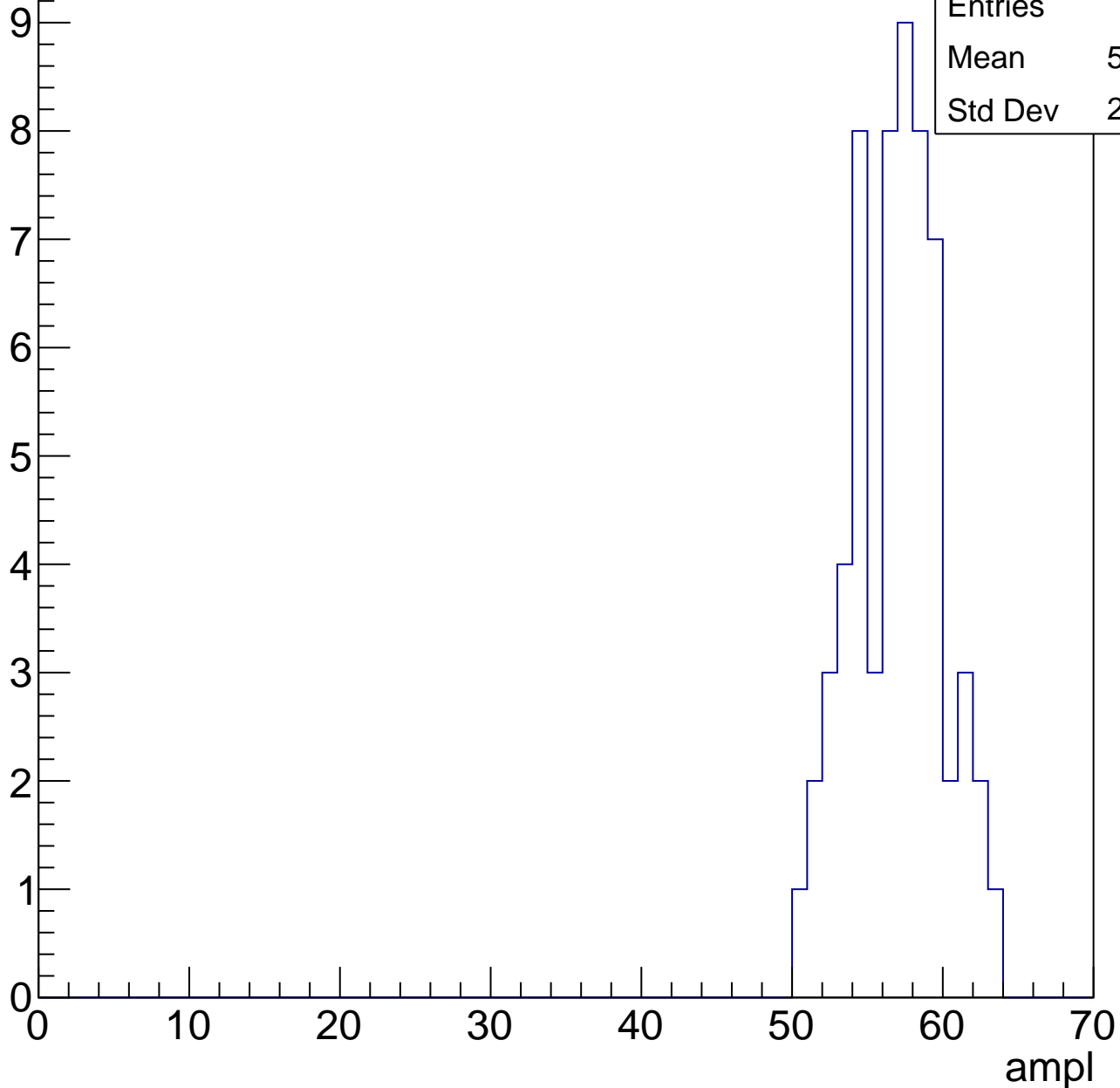
Entries	66
Mean	50.47
Std Dev	3.134



# B0L002S, U23-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

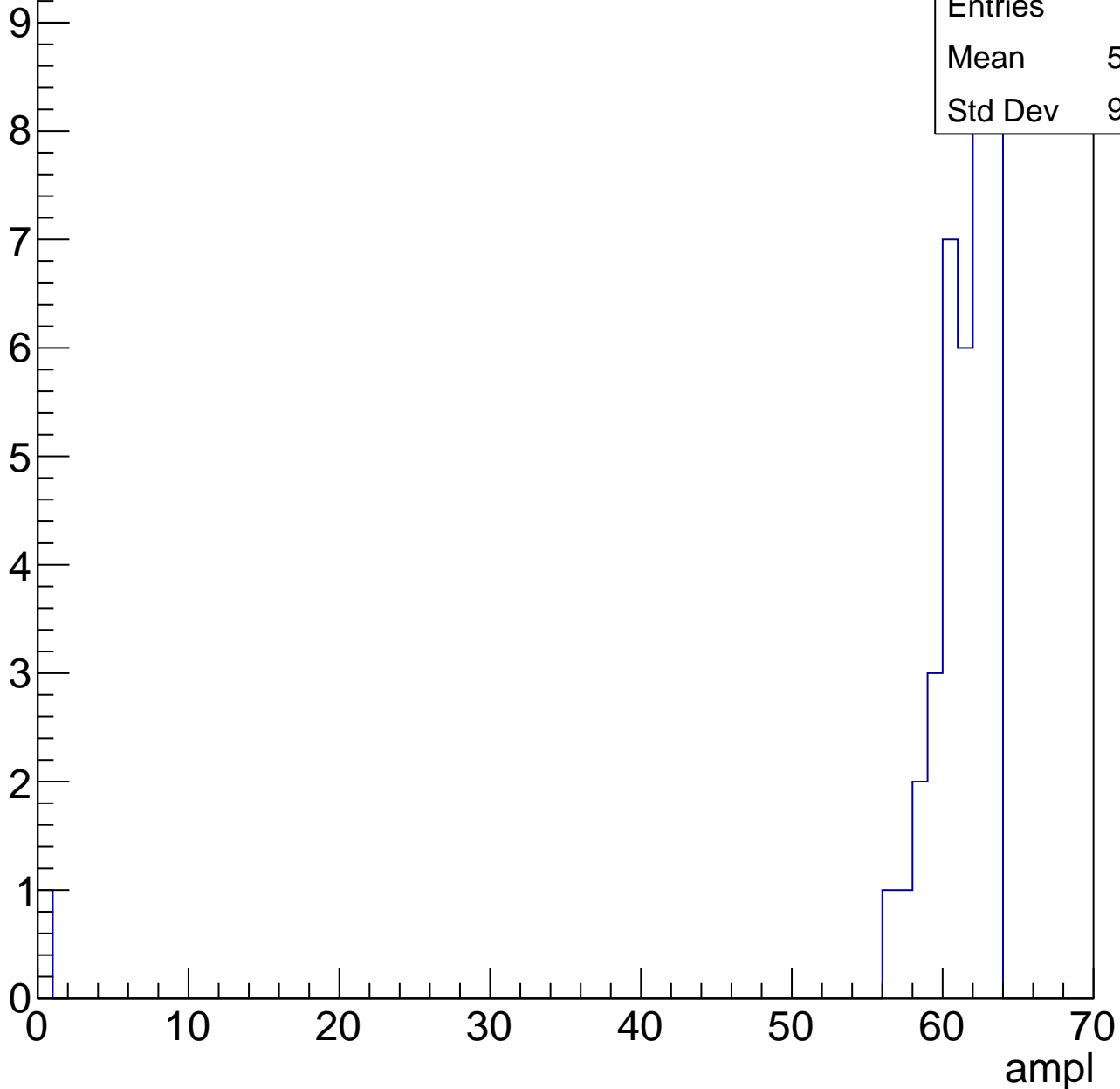


# B0L002S, U23-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.34
Std Dev	9.919



# B0L002S, U23-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch120, adc0

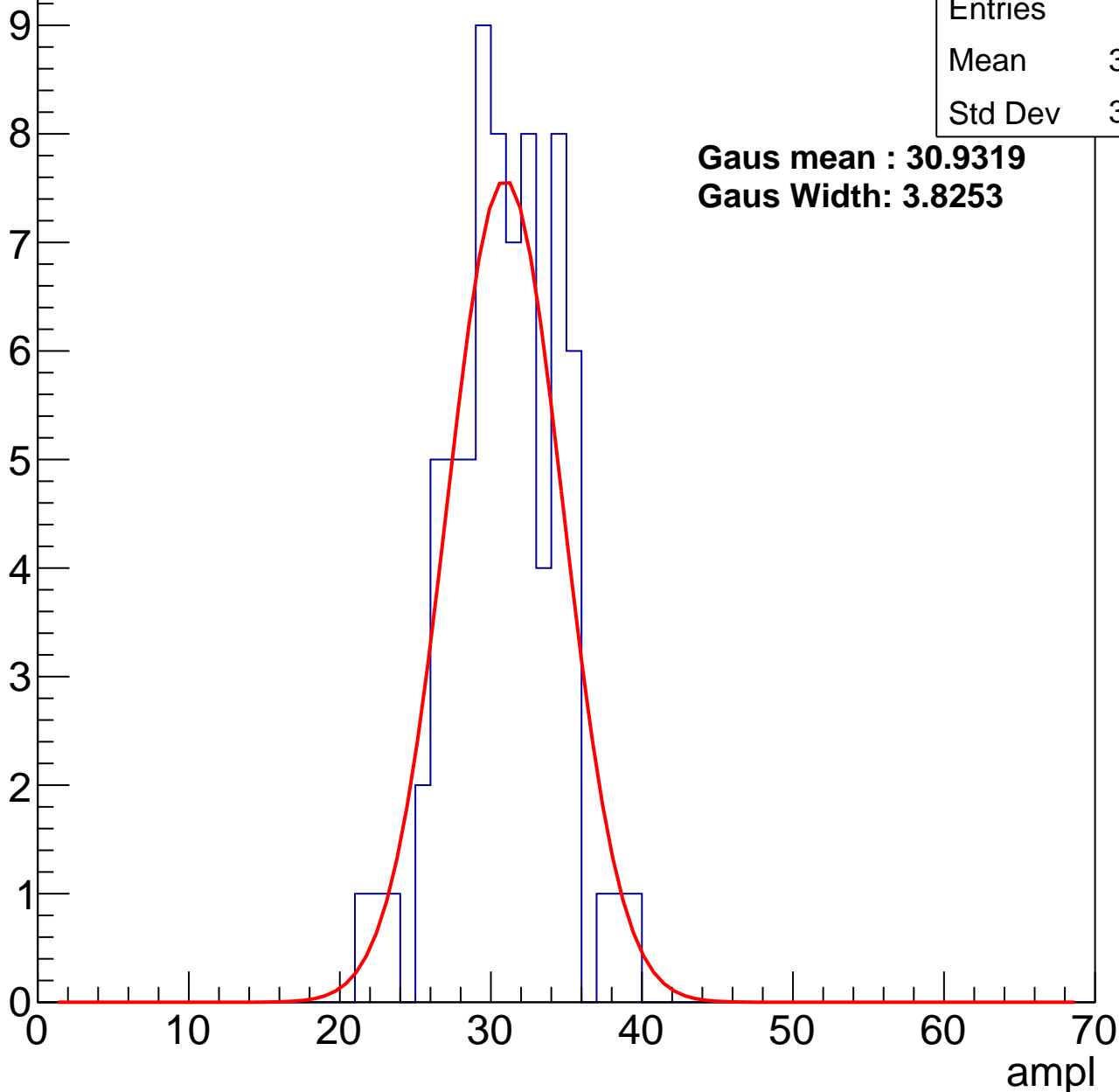
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.45
Std Dev	3.569

**Gaus mean : 30.9319**

**Gaus Width: 3.8253**



# B0L002S, U23-ch120, adc1

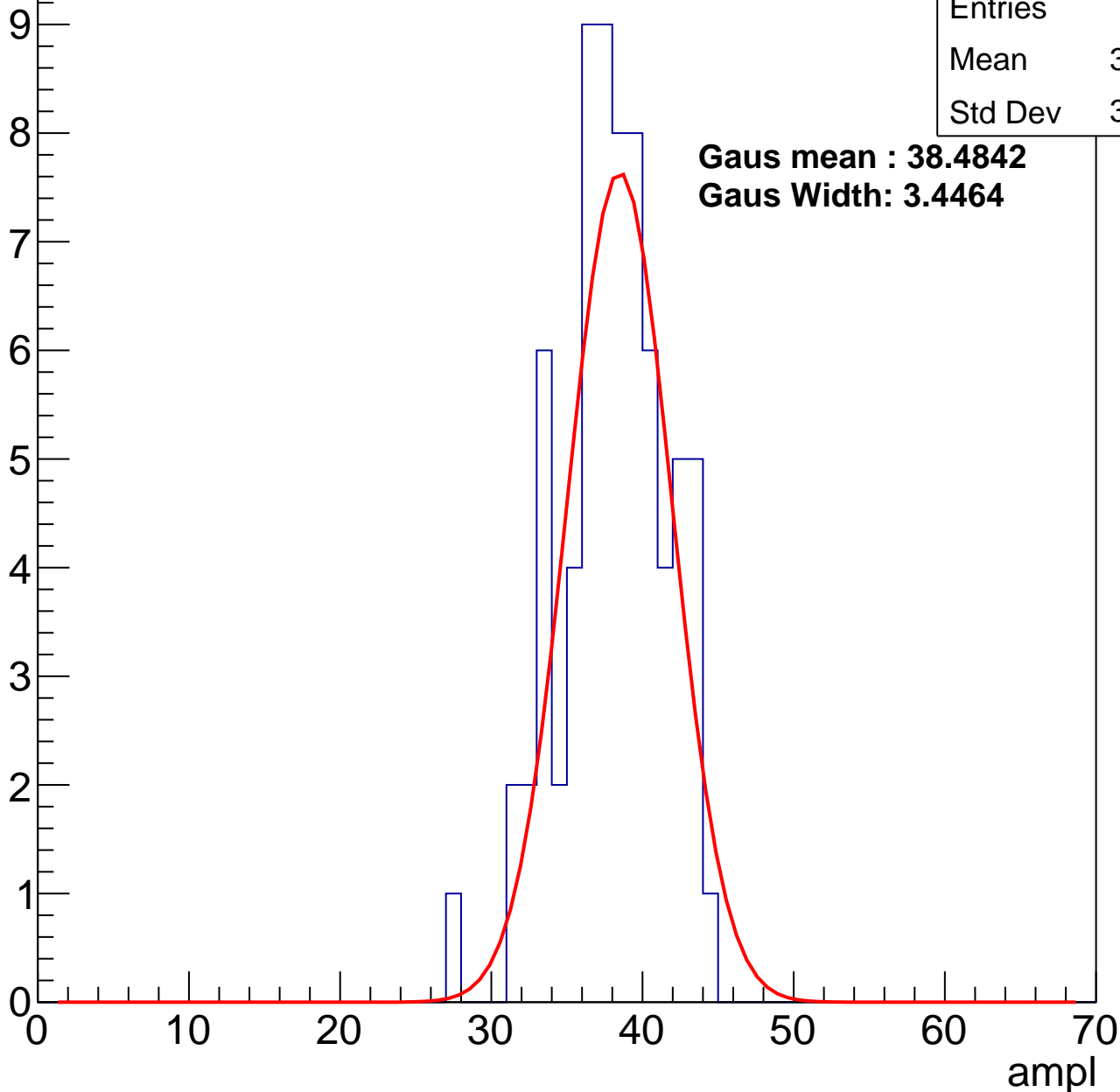
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	37.57
Std Dev	3.435

**Gaus mean : 38.4842**

**Gaus Width: 3.4464**



# B0L002S, U23-ch120, adc2

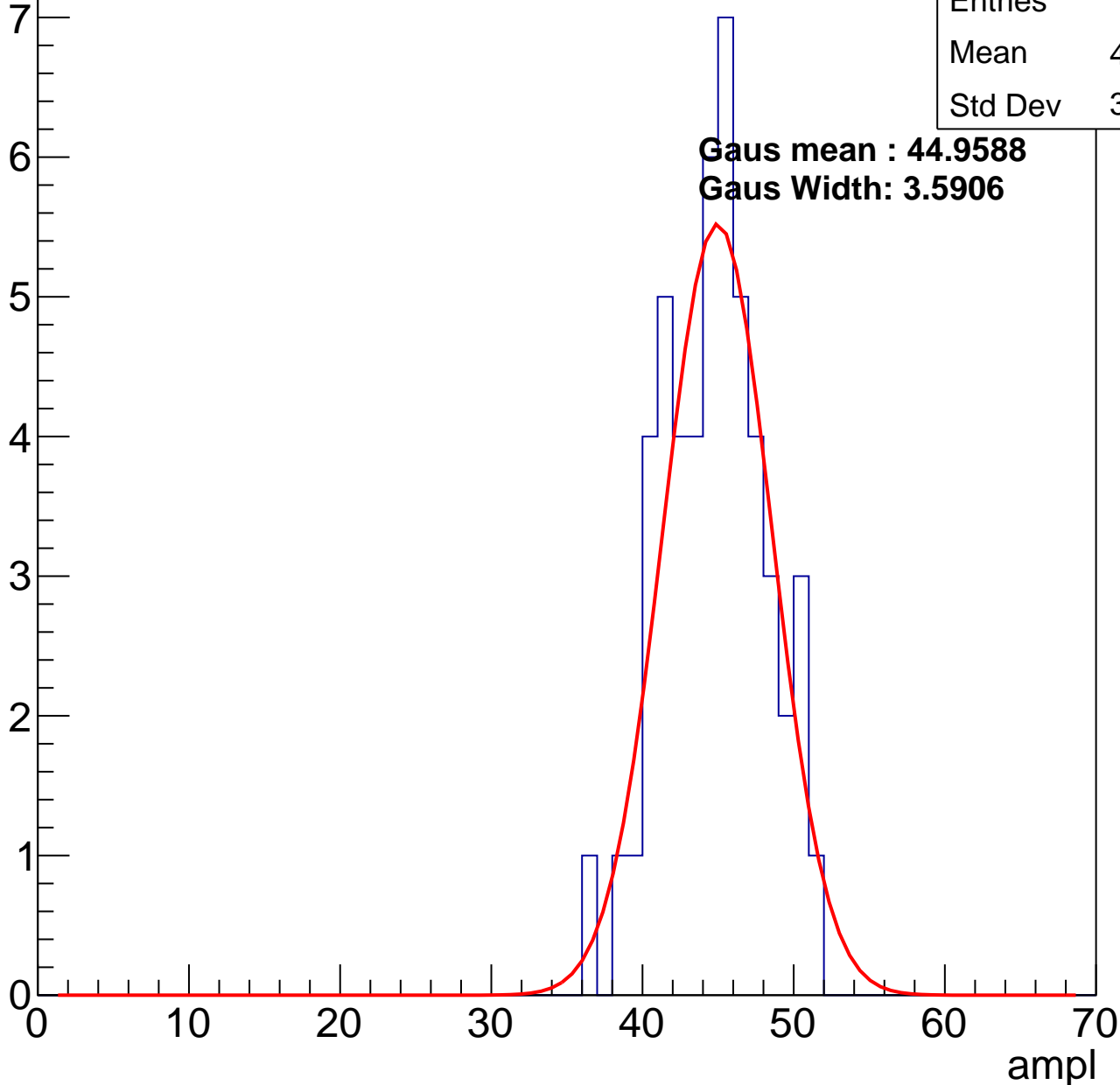
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	44.27
Std Dev	3.338

**Gaus mean : 44.9588**

**Gaus Width: 3.5906**

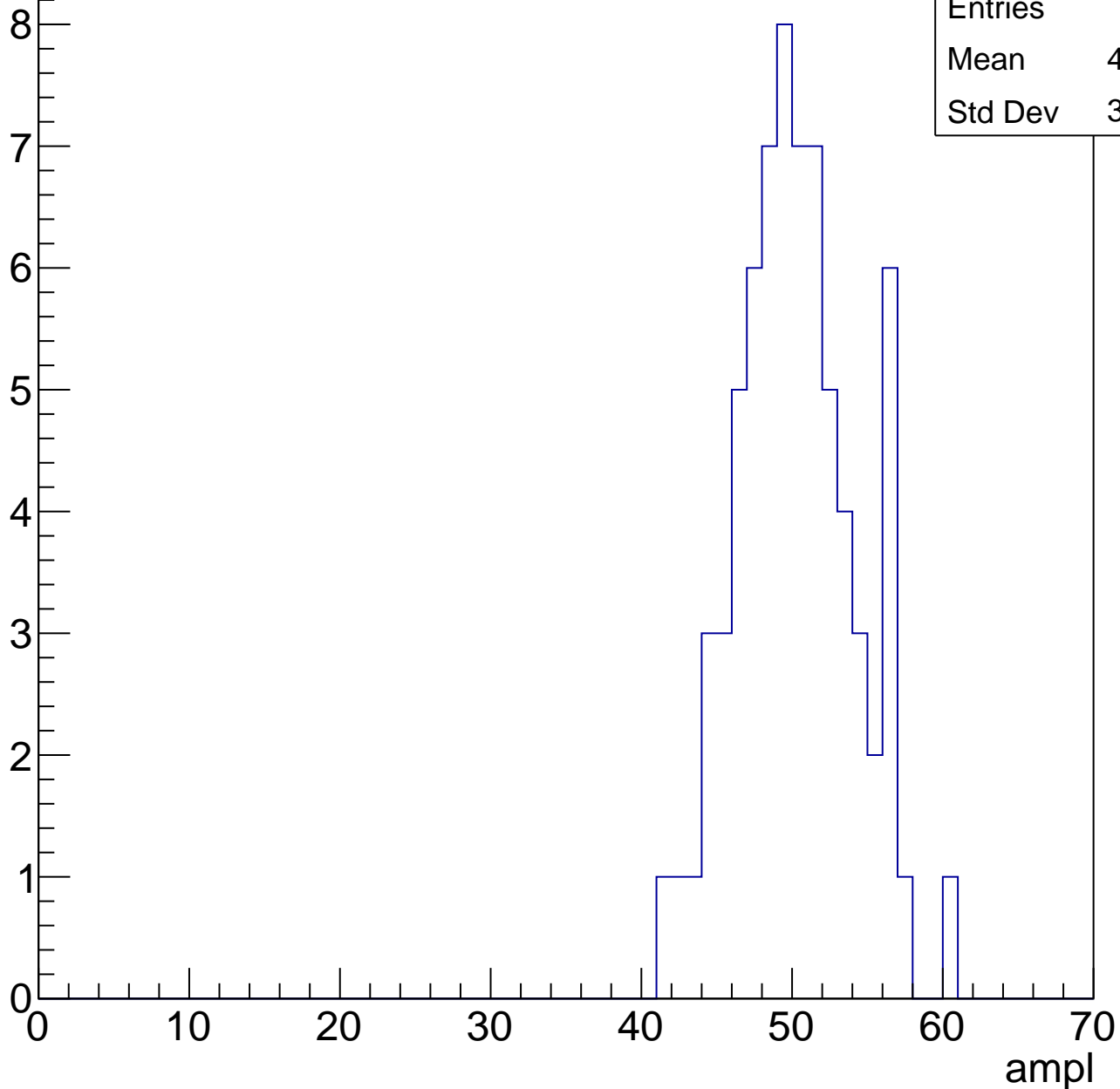


# B0L002S, U23-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	49.82
Std Dev	3.883

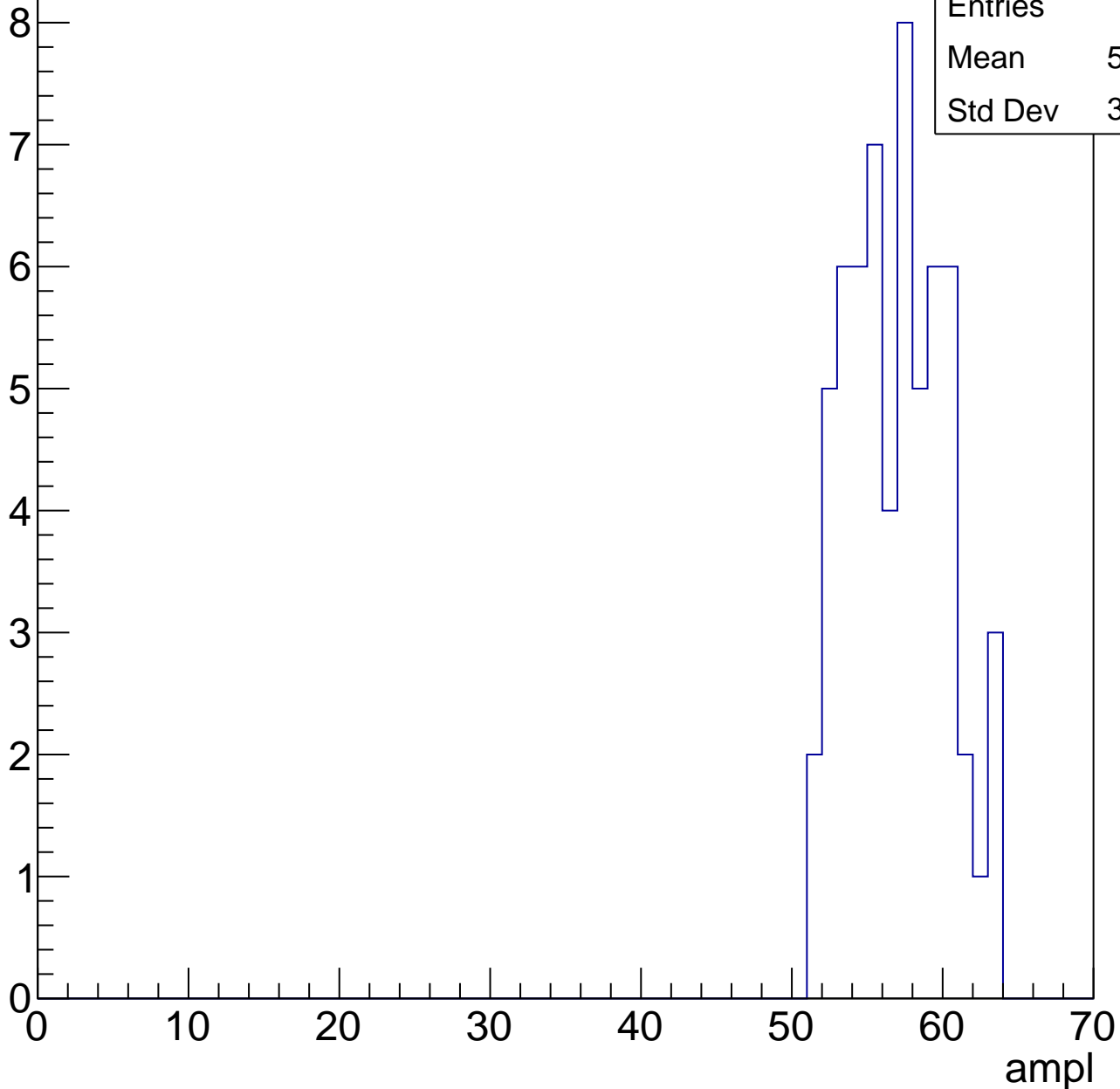


# B0L002S, U23-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.49
Std Dev	3.166

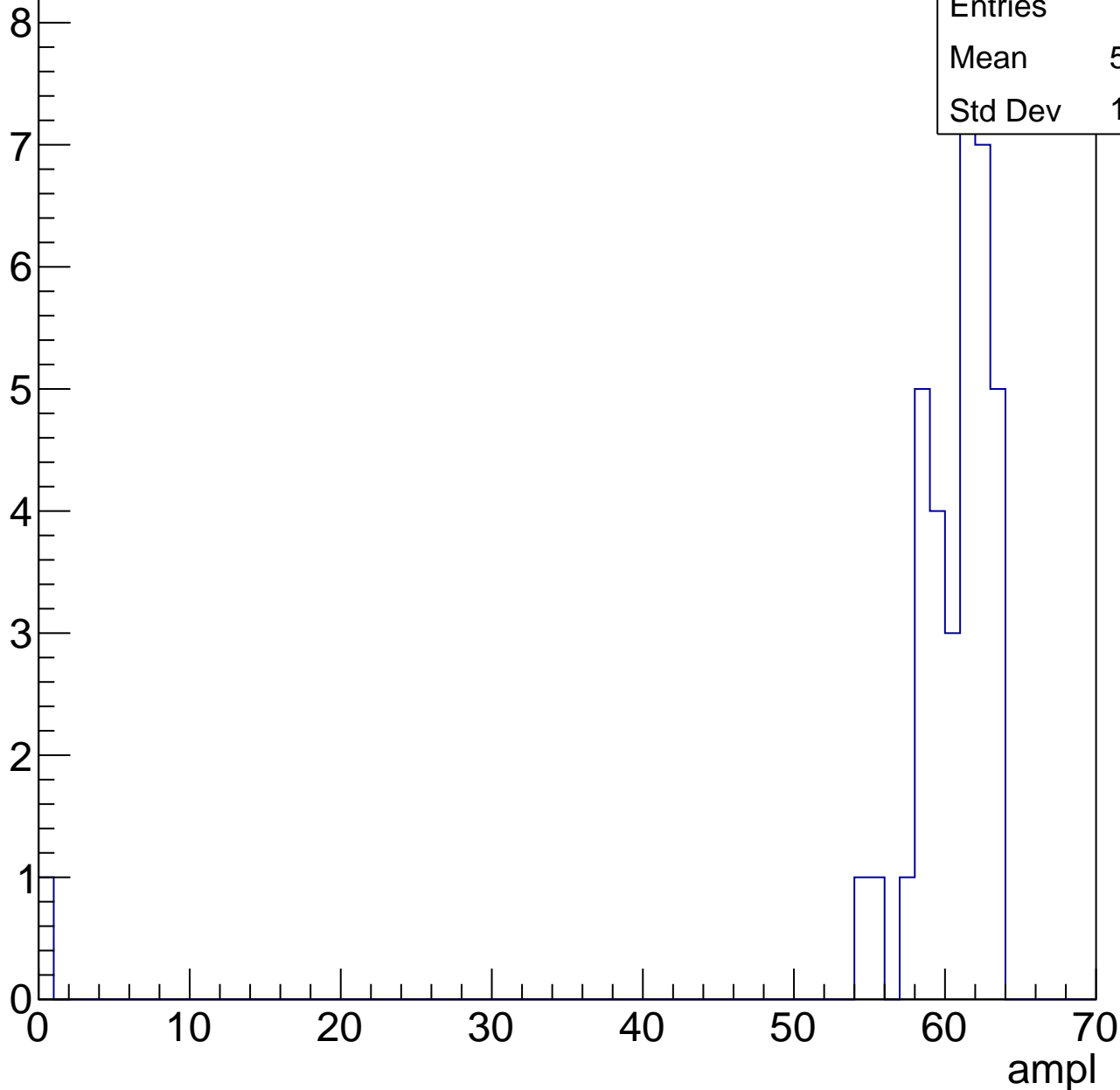


# B0L002S, U23-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	36
Mean	58.58
Std Dev	10.14



# B0L002S, U23-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl



# B0L002S, U23-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch121, adc0

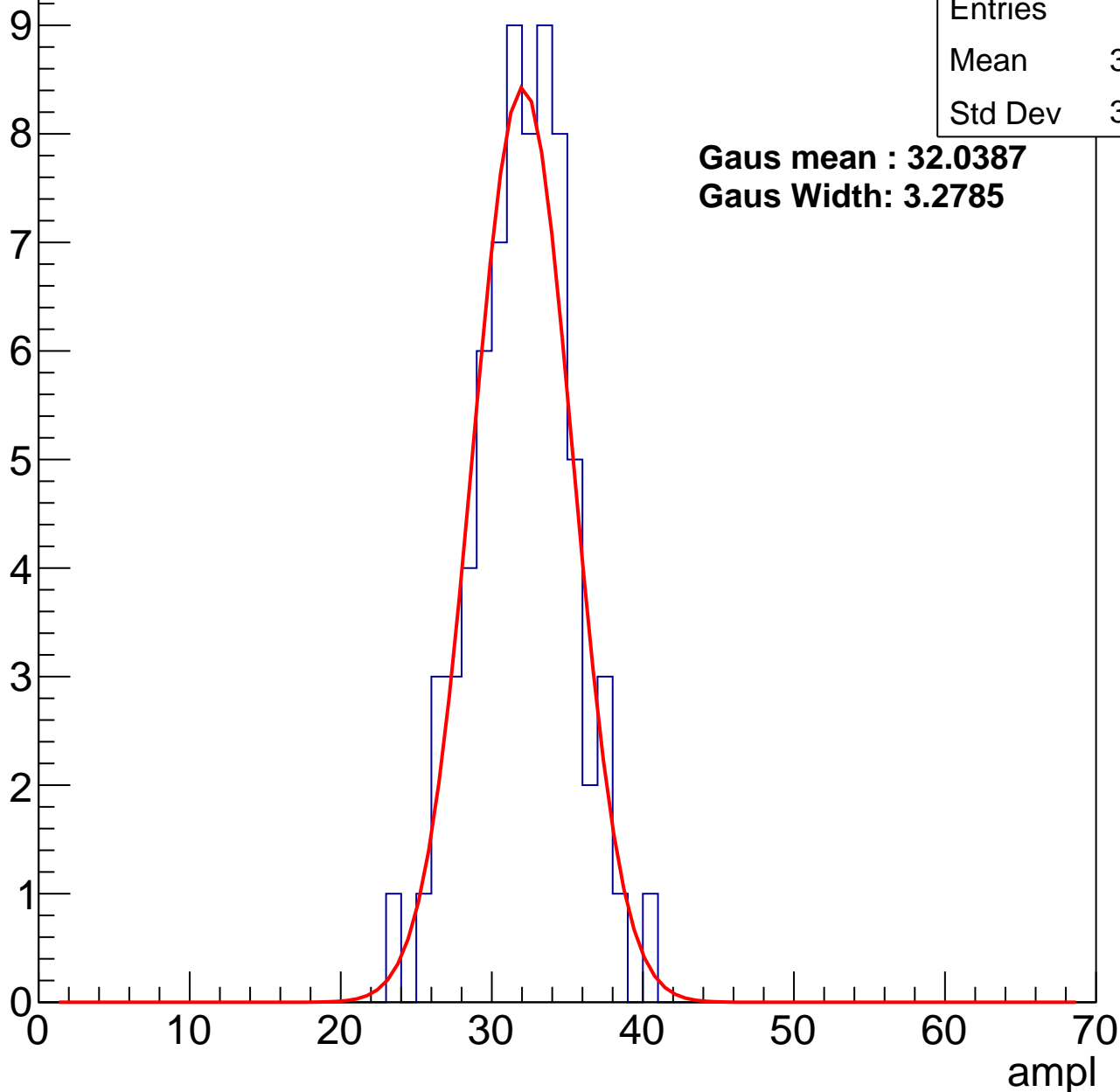
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.59
Std Dev	3.248

**Gaus mean : 32.0387**

**Gaus Width: 3.2785**



# B0L002S, U23-ch121, adc1

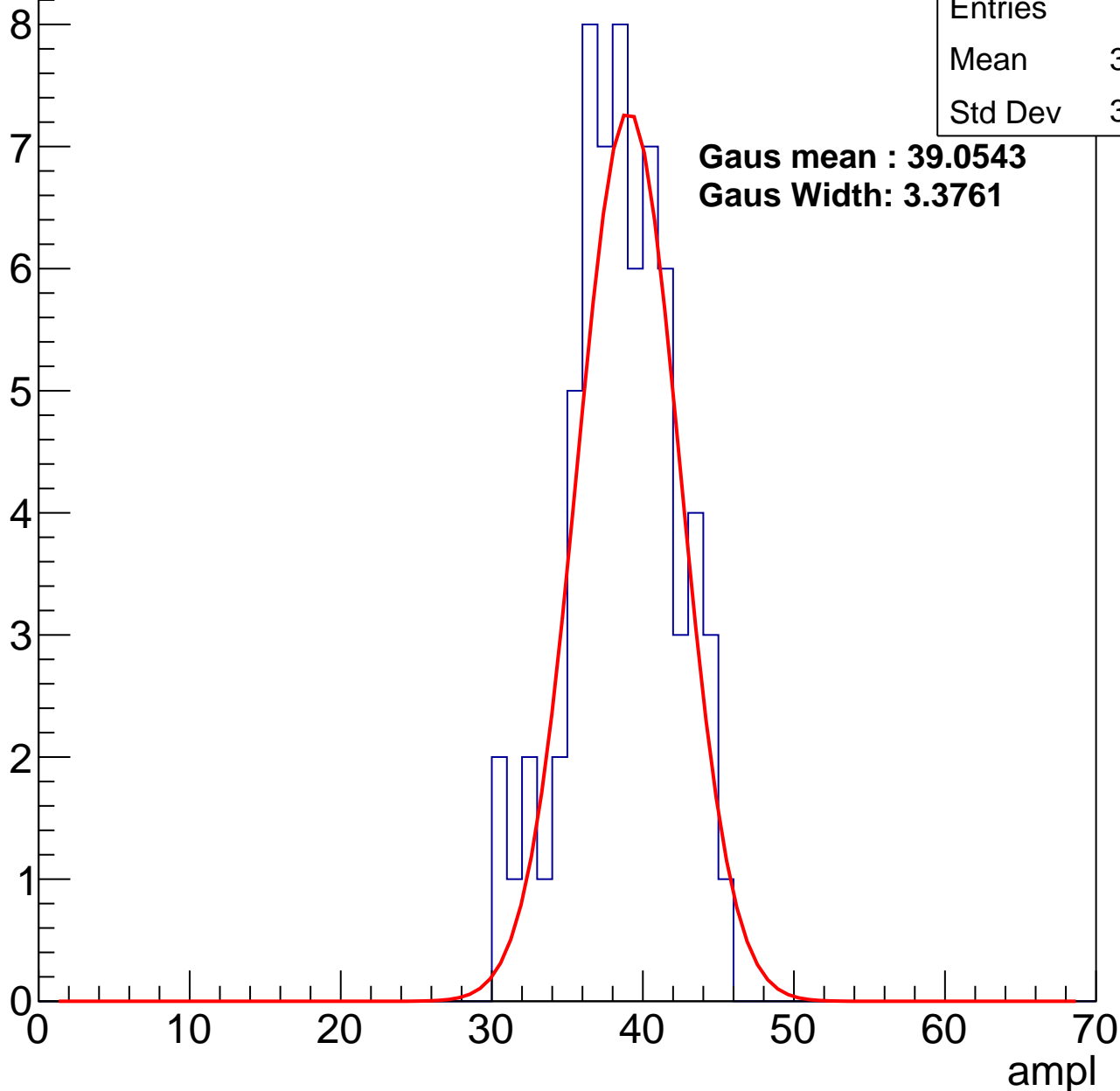
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.14
Std Dev	3.442

**Gaus mean : 39.0543**

**Gaus Width: 3.3761**



# B0L002S, U23-ch121, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	80
Mean	45.48
Std Dev	3.457

**Gaus mean : 46.0548**

**Gaus Width: 3.7170**

10

8

6

4

2

0

0

10

20

30

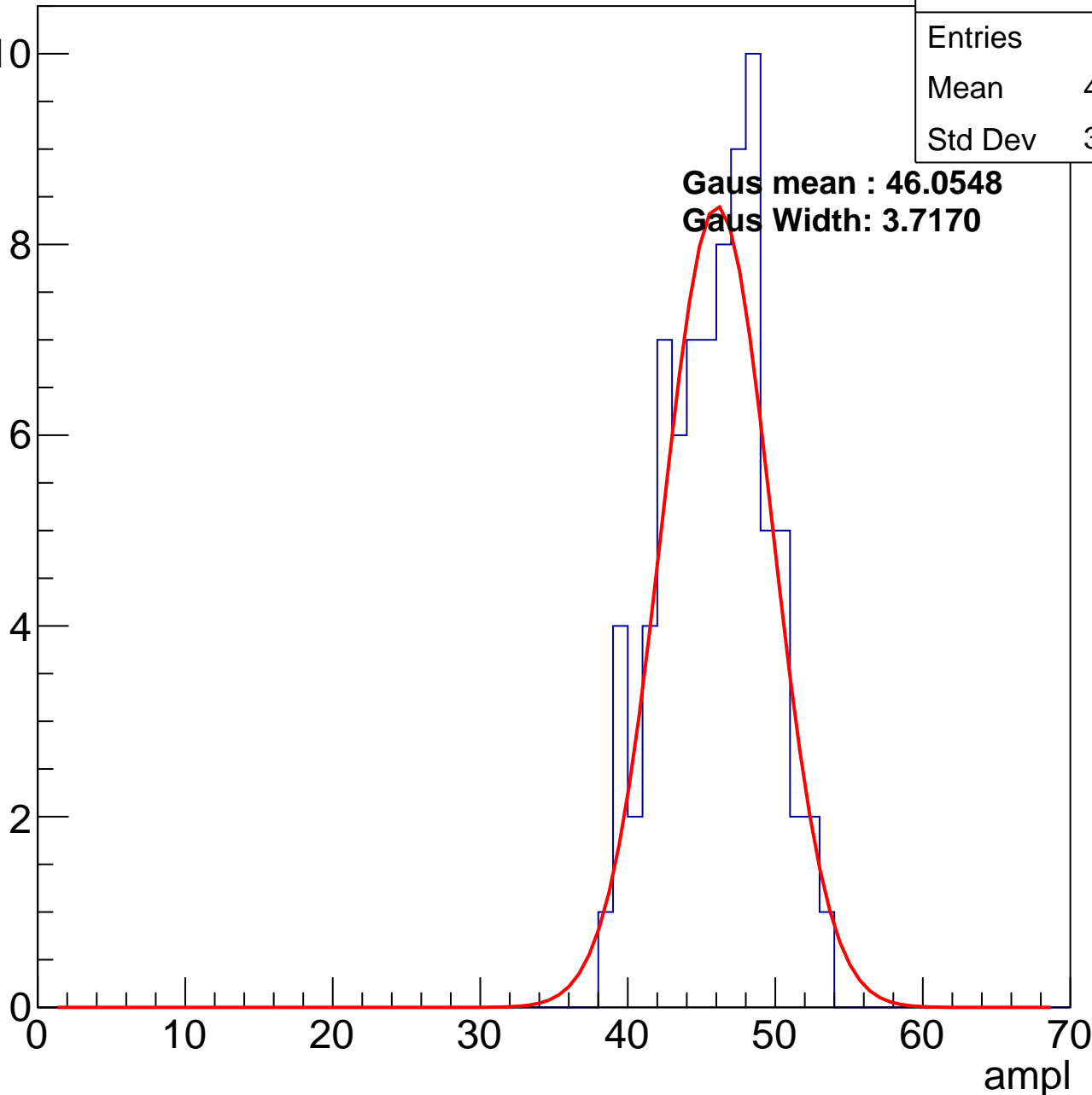
40

50

60

70

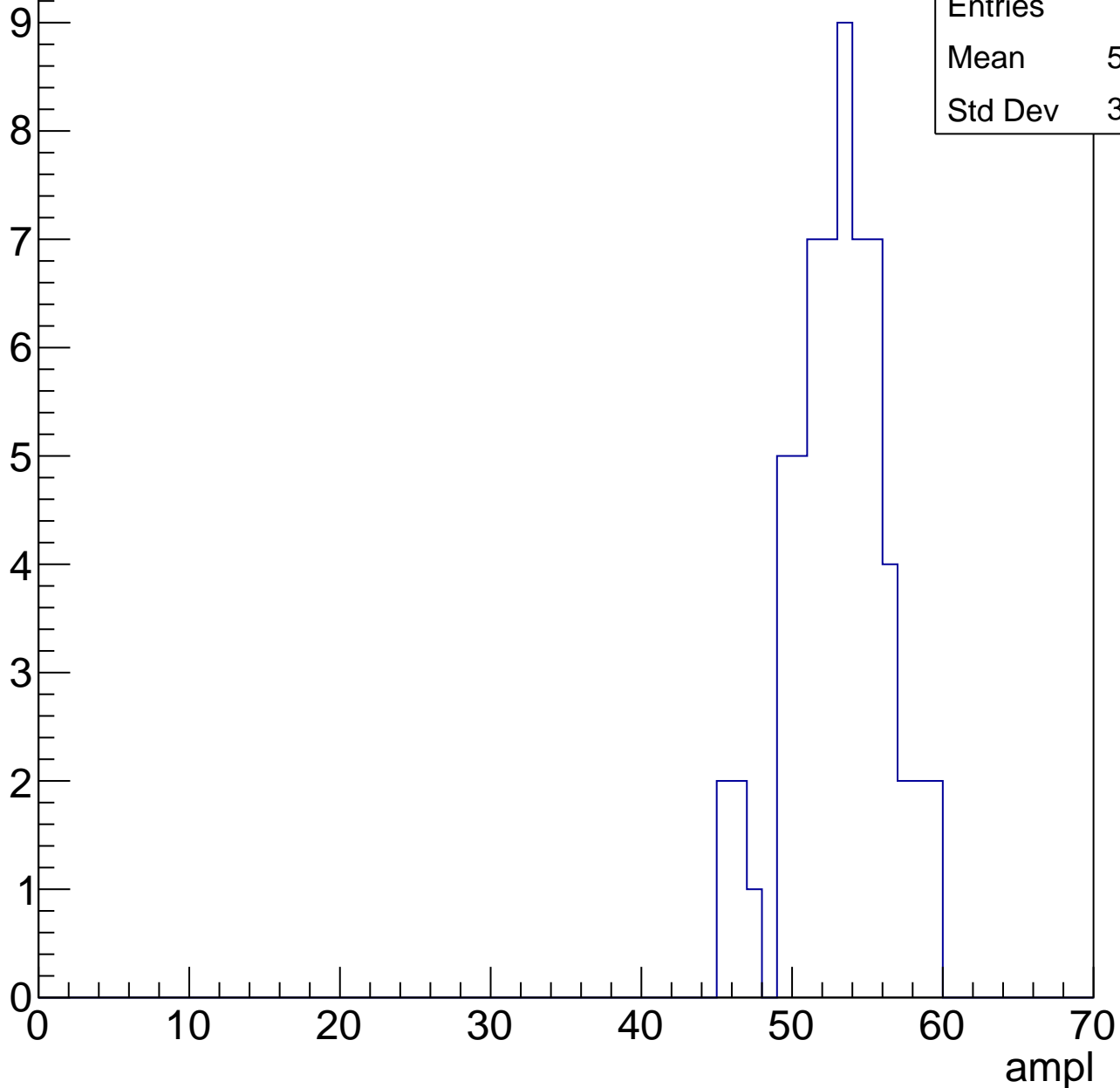
ampl



# B0L002S, U23-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



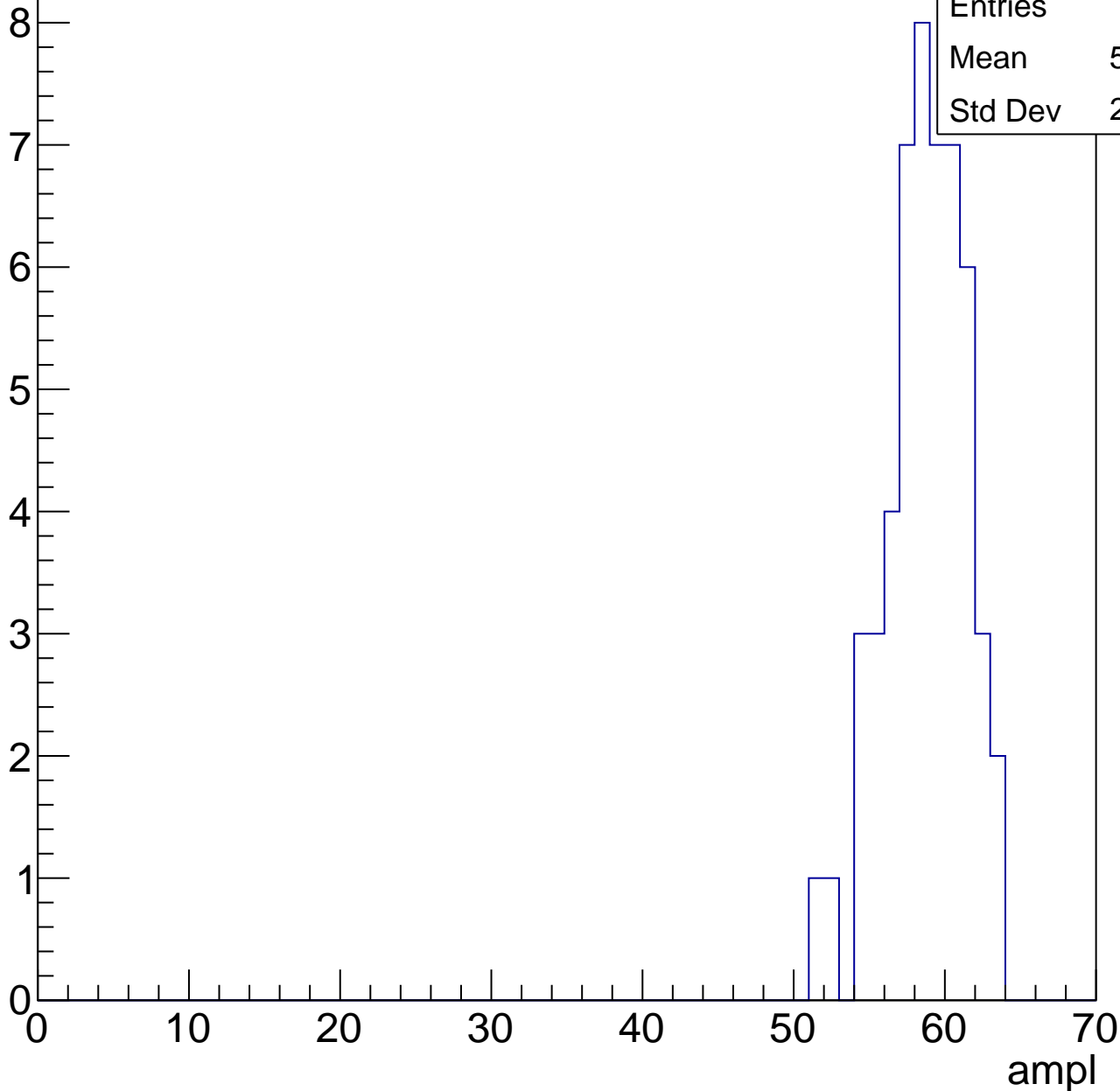
Entries	62
Mean	52.53
Std Dev	3.196

# B0L002S, U23-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	58.23
Std Dev	2.657

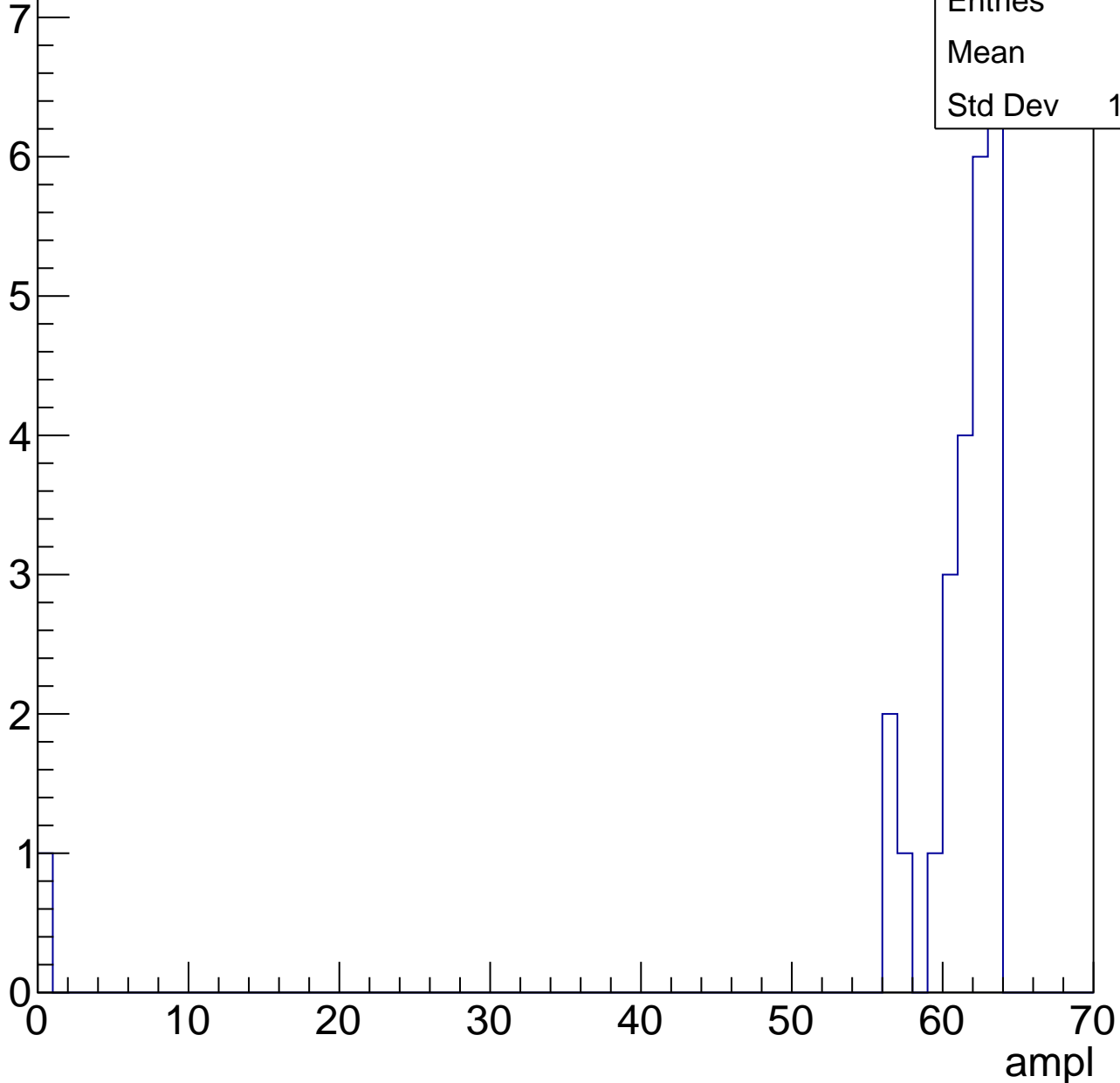


# B0L002S, U23-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.6
Std Dev	12.14



# B0L002S, U23-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L002S, U23-ch122, adc0

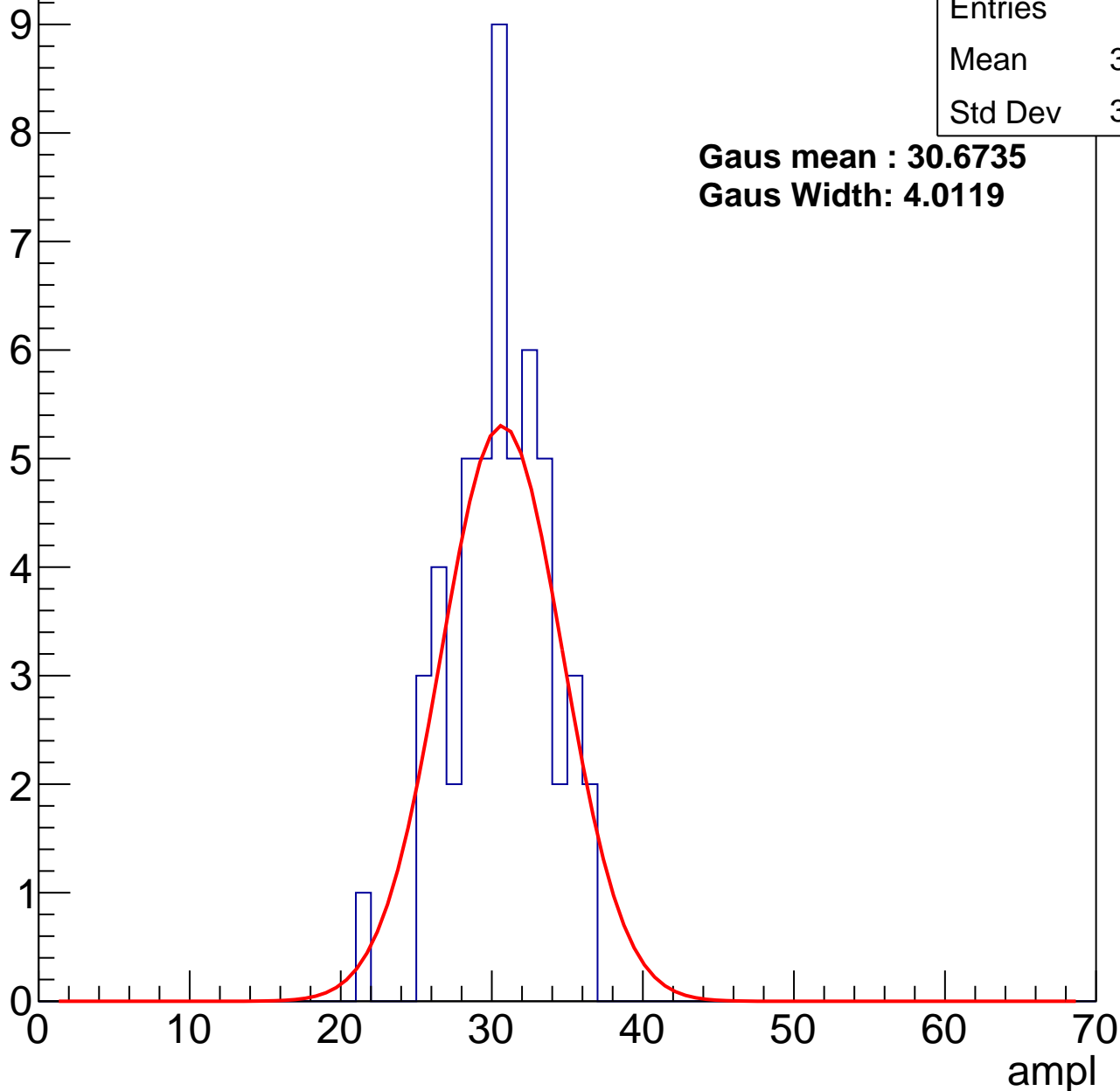
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	30.12
Std Dev	3.148

**Gaus mean : 30.6735**

**Gaus Width: 4.0119**



# B0L002S, U23-ch122, adc1

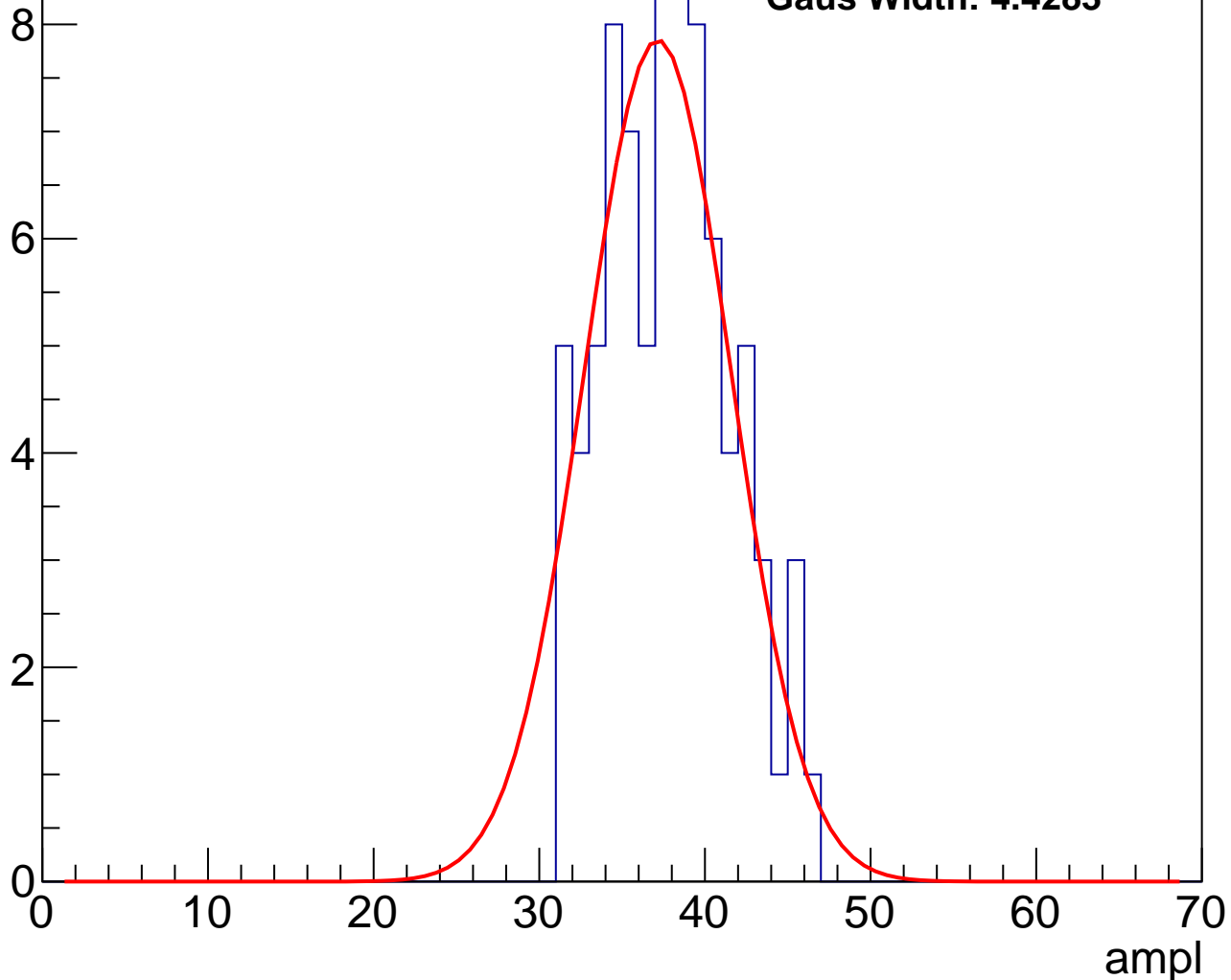
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	85
Mean	37.35
Std Dev	3.709

**Gaus mean : 37.1493**

**Gaus Width: 4.4283**



# B0L002S, U23-ch122, adc2

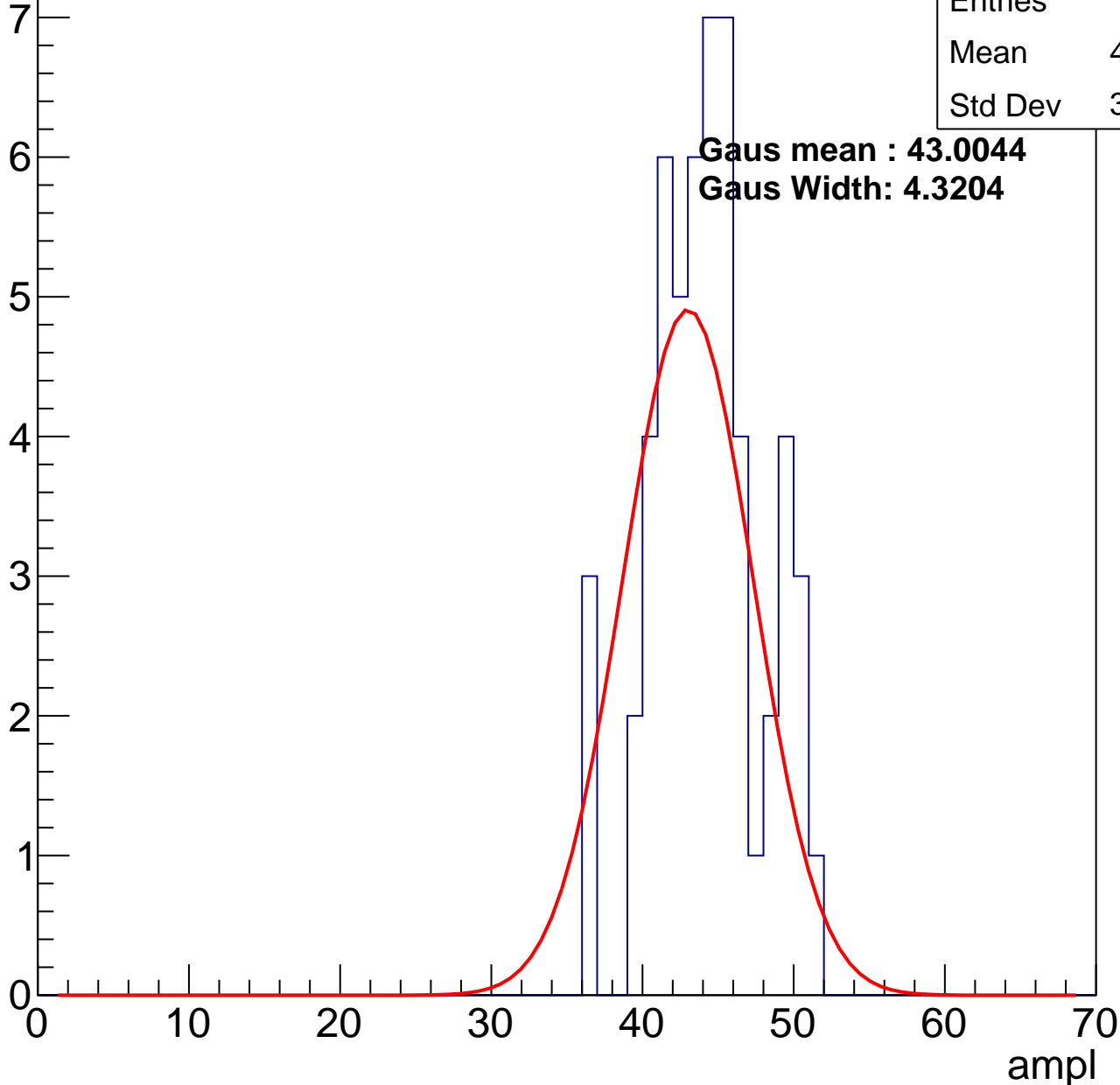
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	55
Mean	43.76
Std Dev	3.578

**Gaus mean : 43.0044**

**Gaus Width: 4.3204**

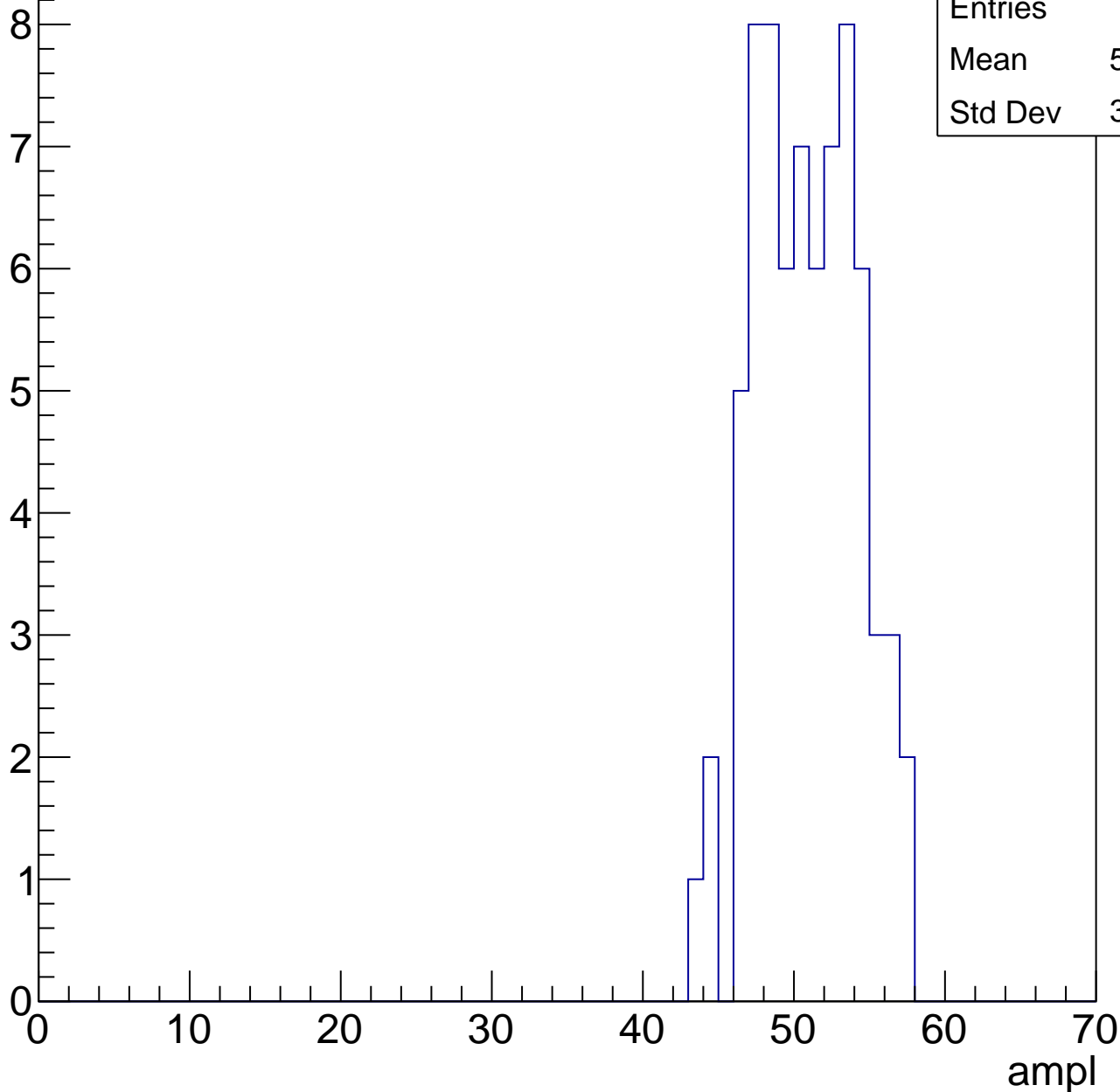


# B0L002S, U23-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	72
Mean	50.42
Std Dev	3.295

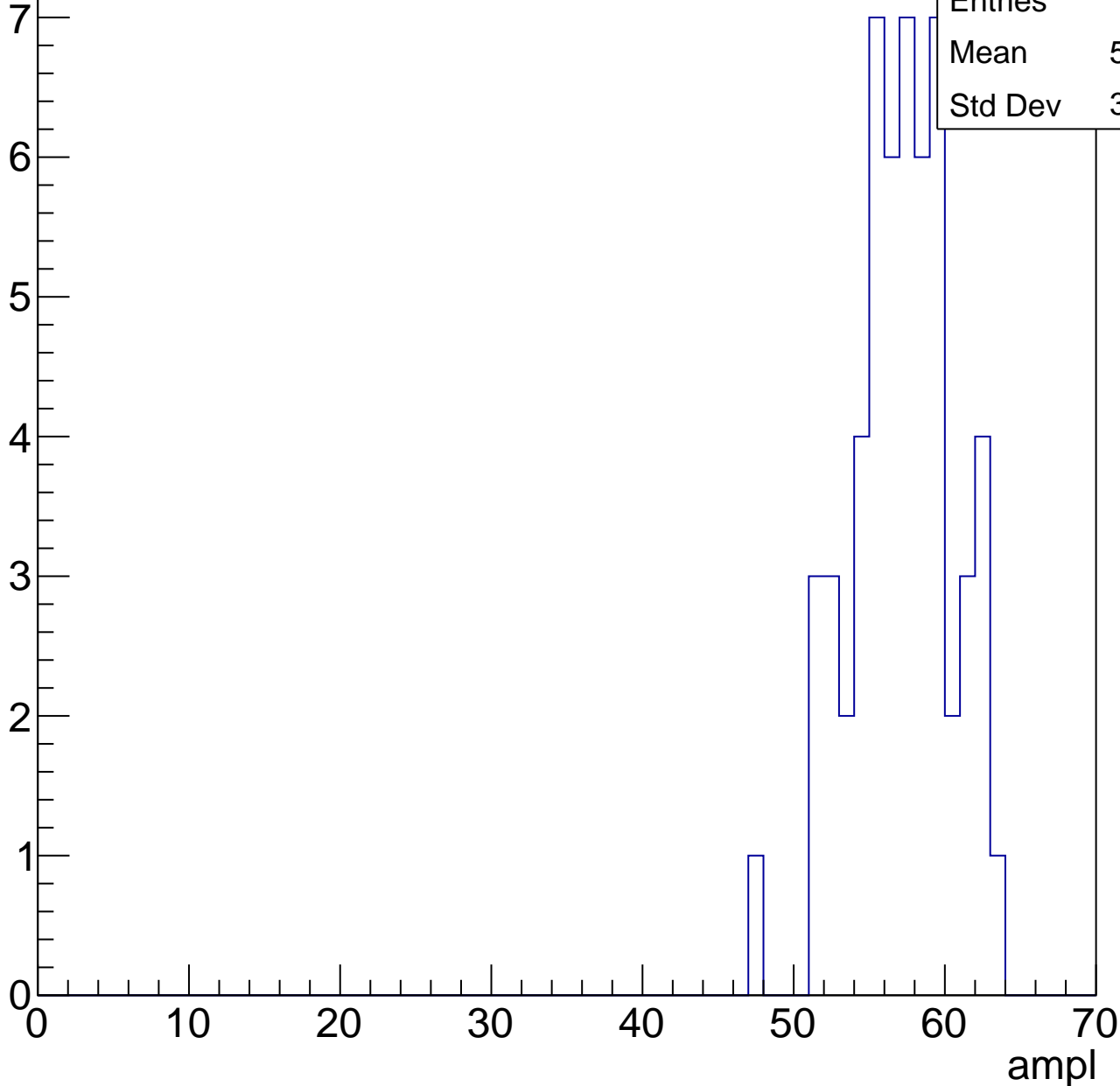


# B0L002S, U23-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.66
Std Dev	3.313

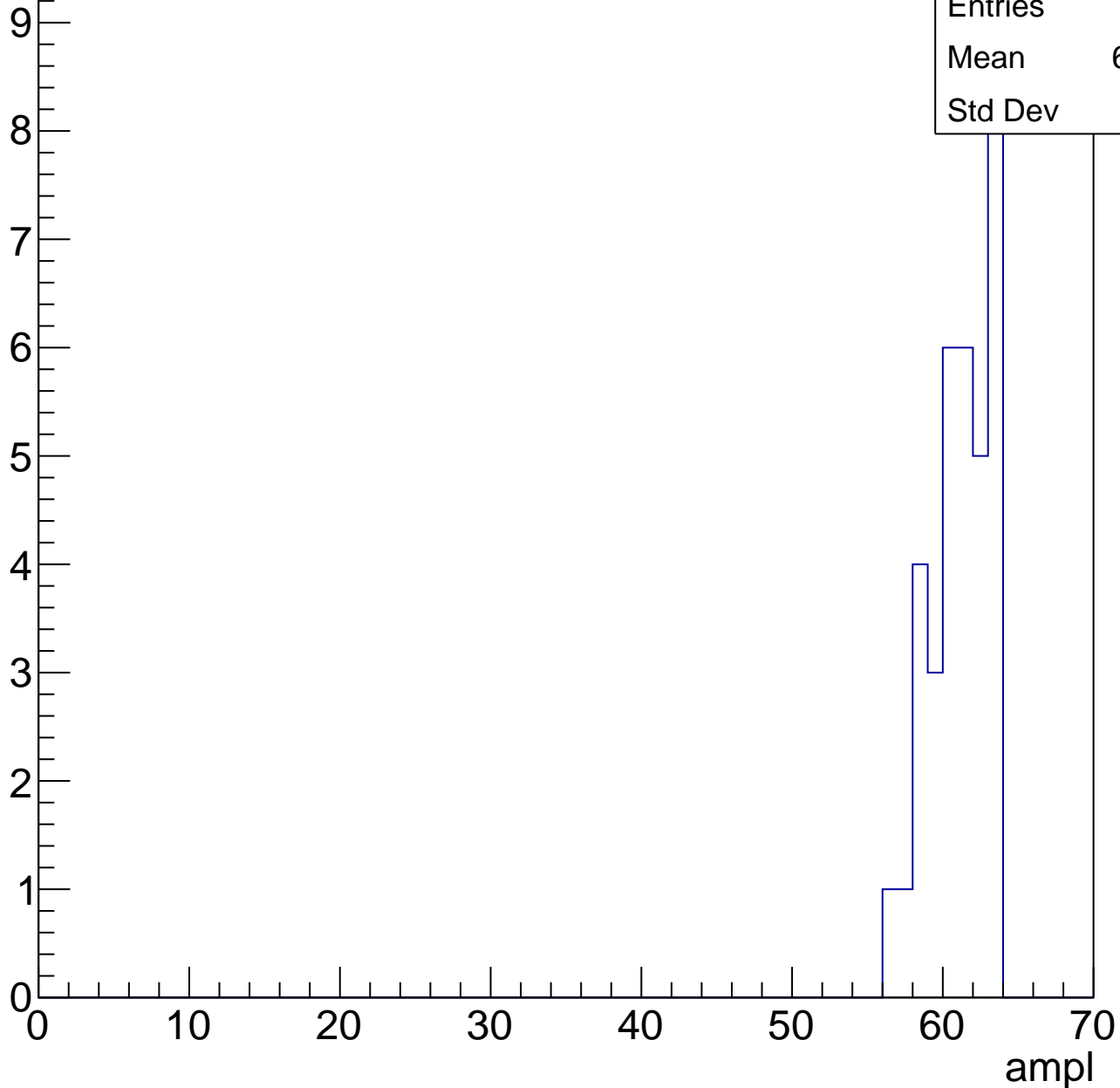


# B0L002S, U23-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

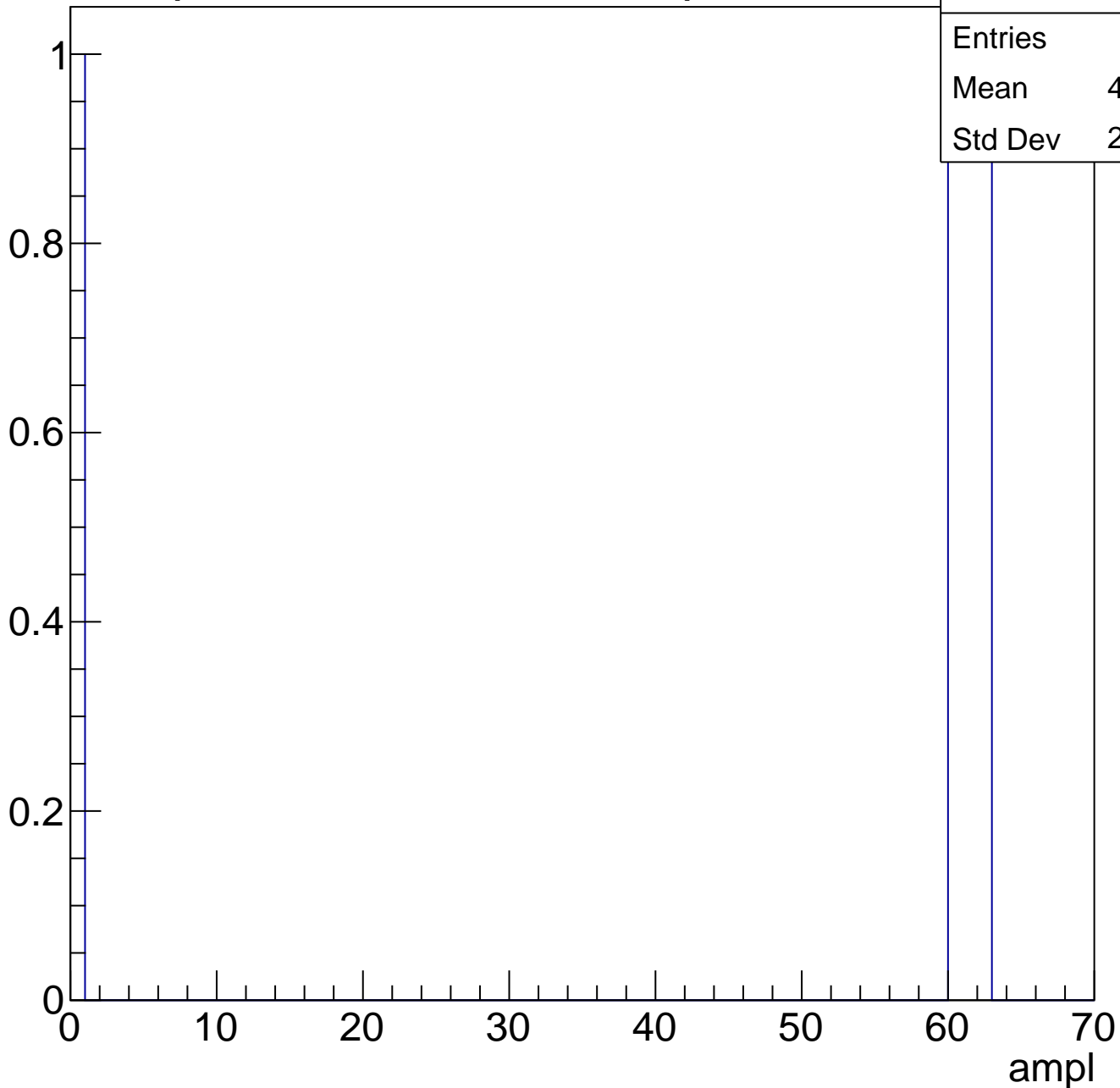
Entries	35
Mean	60.71
Std Dev	1.95



# B0L002S, U23-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

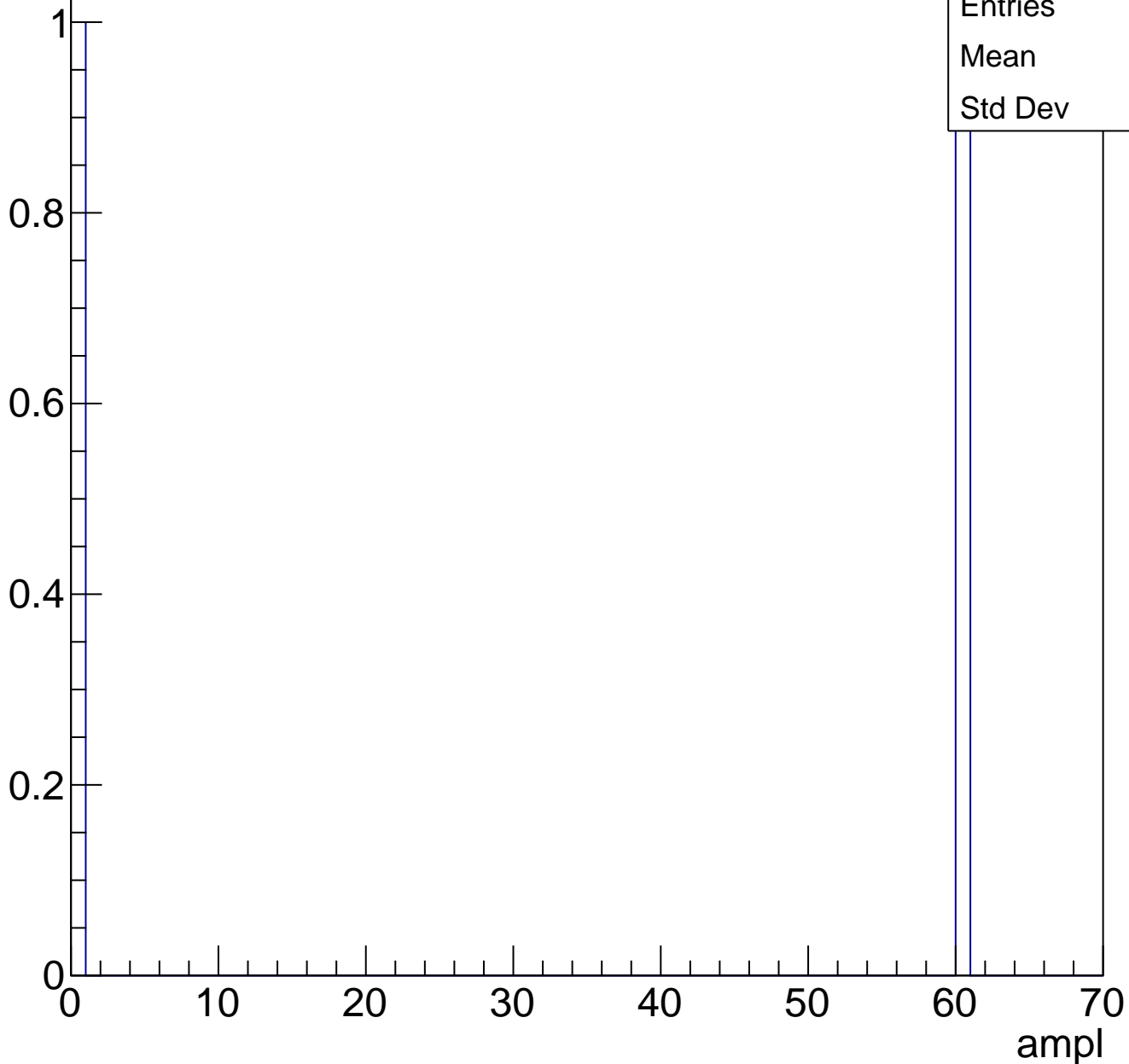




# B0L002S, U23-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch123, adc0

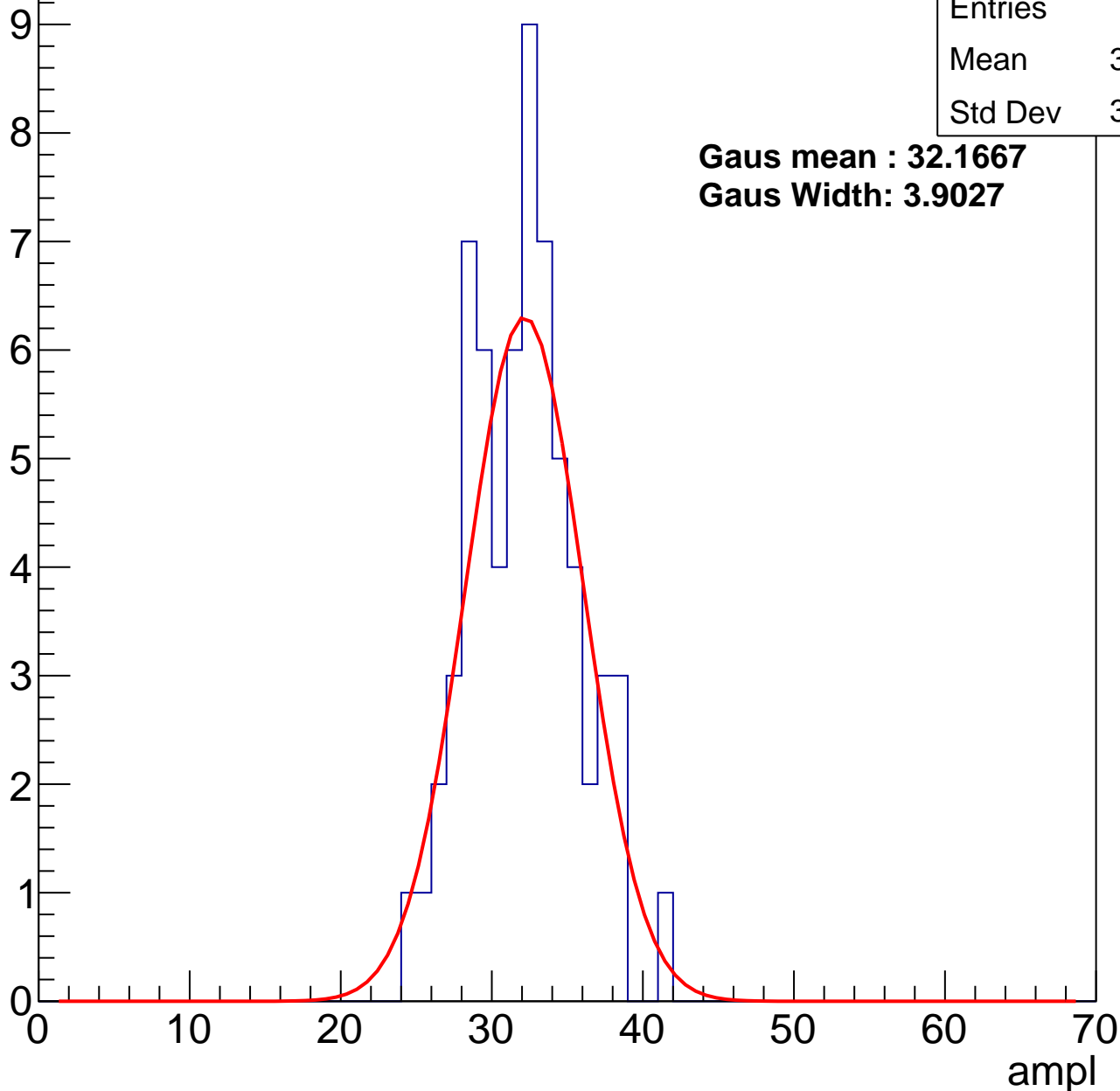
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.64
Std Dev	3.533

**Gaus mean : 32.1667**

**Gaus Width: 3.9027**



# B0L002S, U23-ch123, adc1

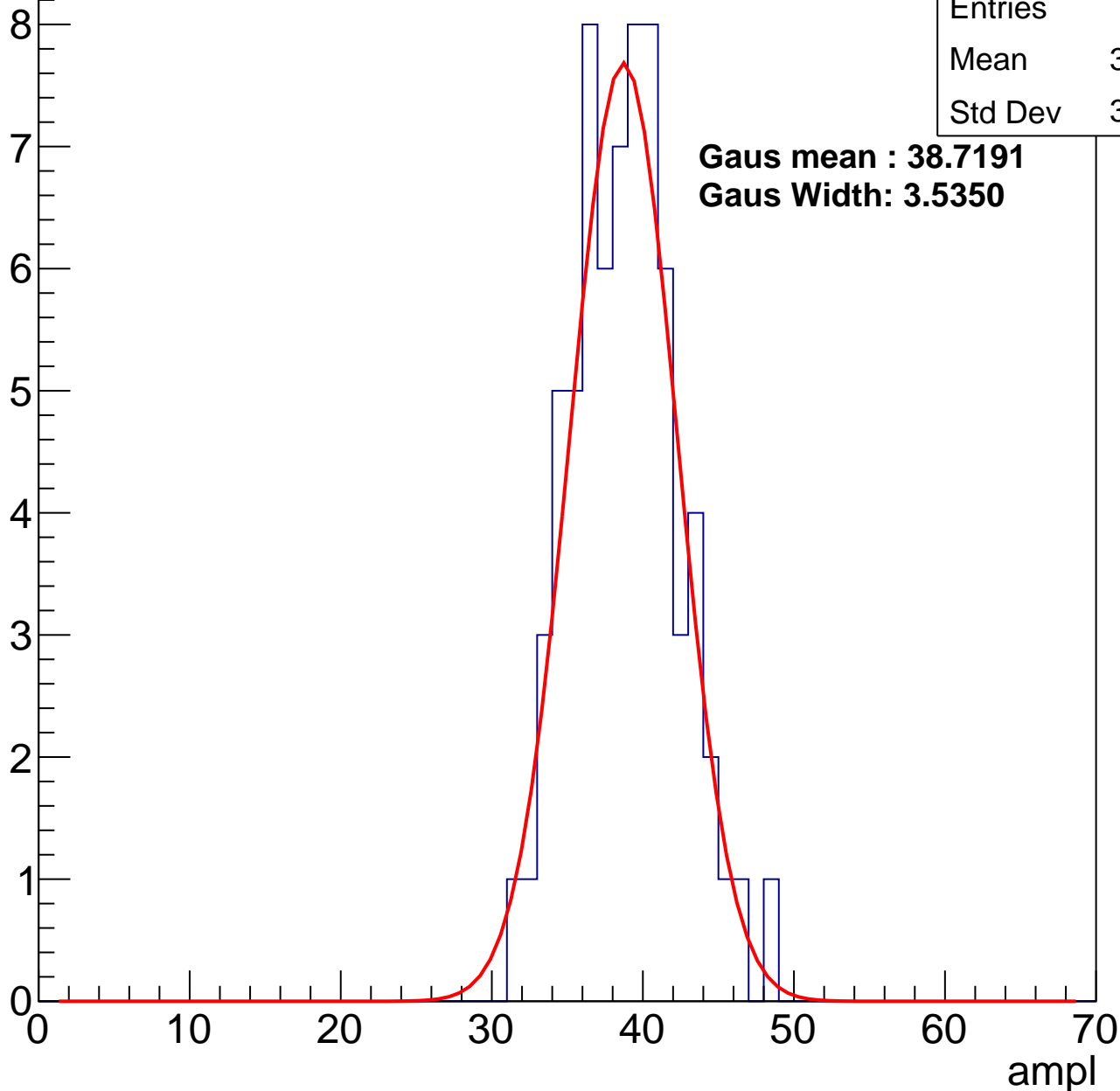
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	38.37
Std Dev	3.465

**Gaus mean : 38.7191**

**Gaus Width: 3.5350**



# B0L002S, U23-ch123, adc2

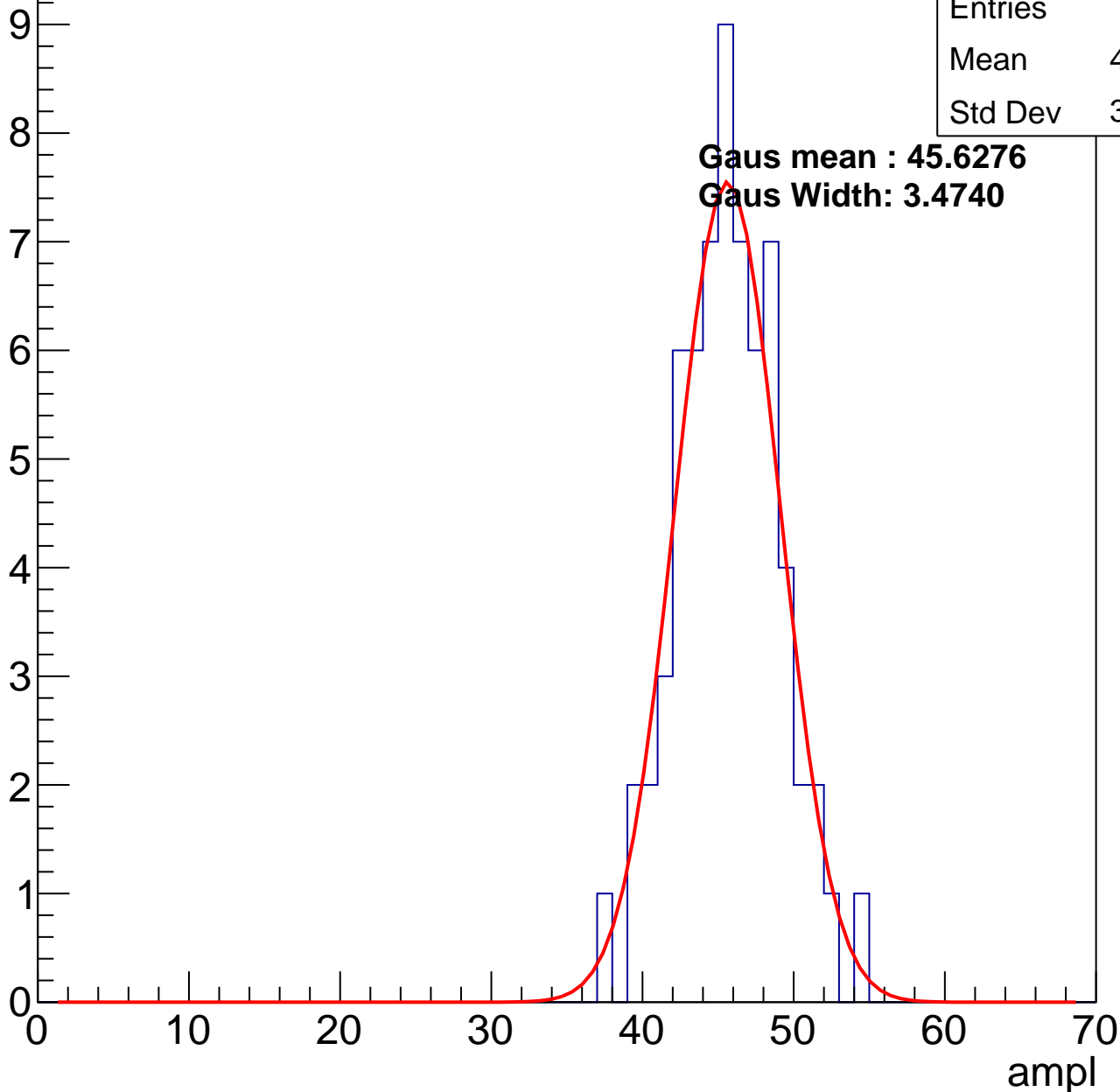
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	66
Mean	45.23
Std Dev	3.316

**Gaus mean : 45.6276**

**Gaus Width: 3.4740**

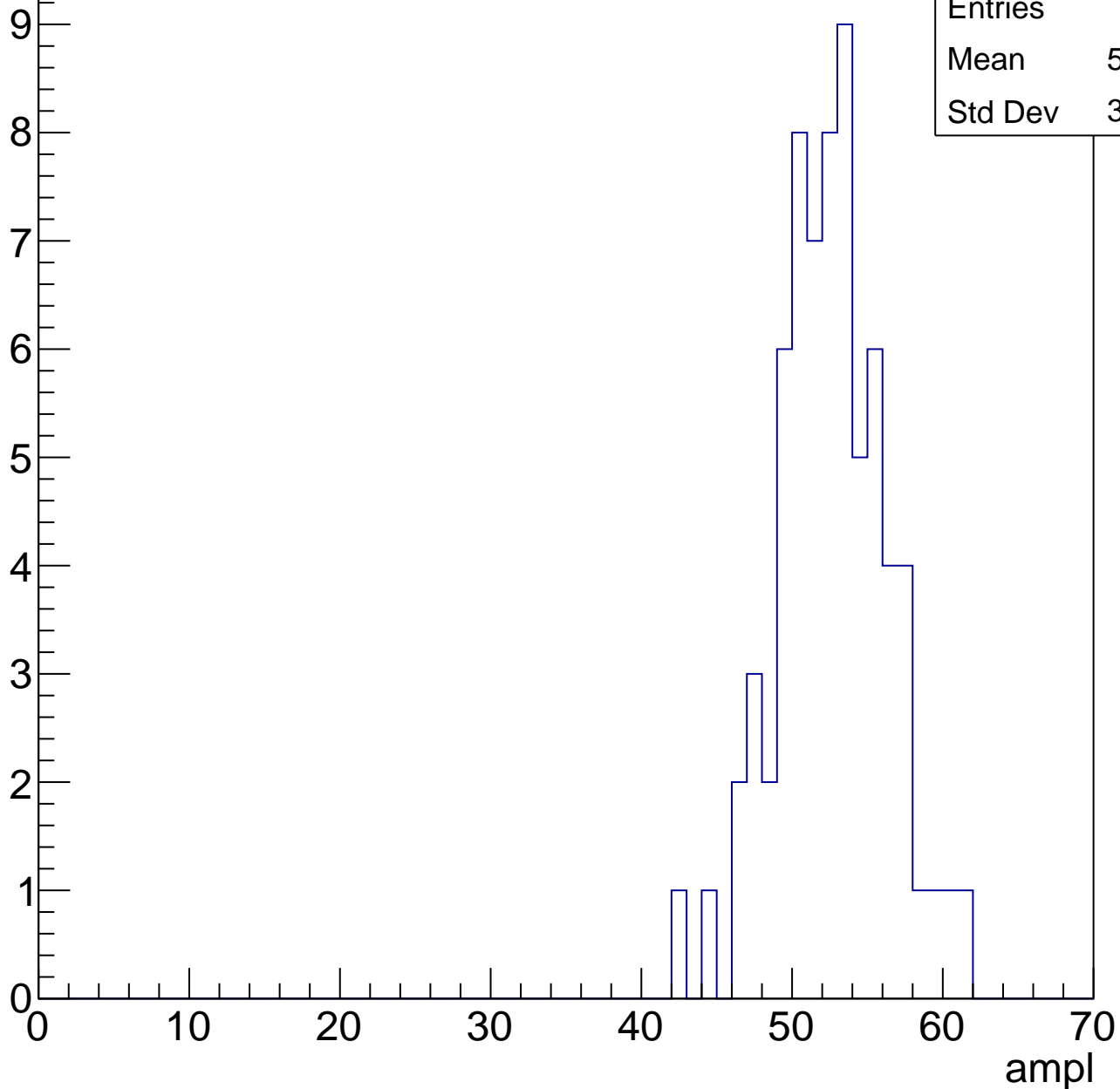


# B0L002S, U23-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	70
Mean	52.13
Std Dev	3.609

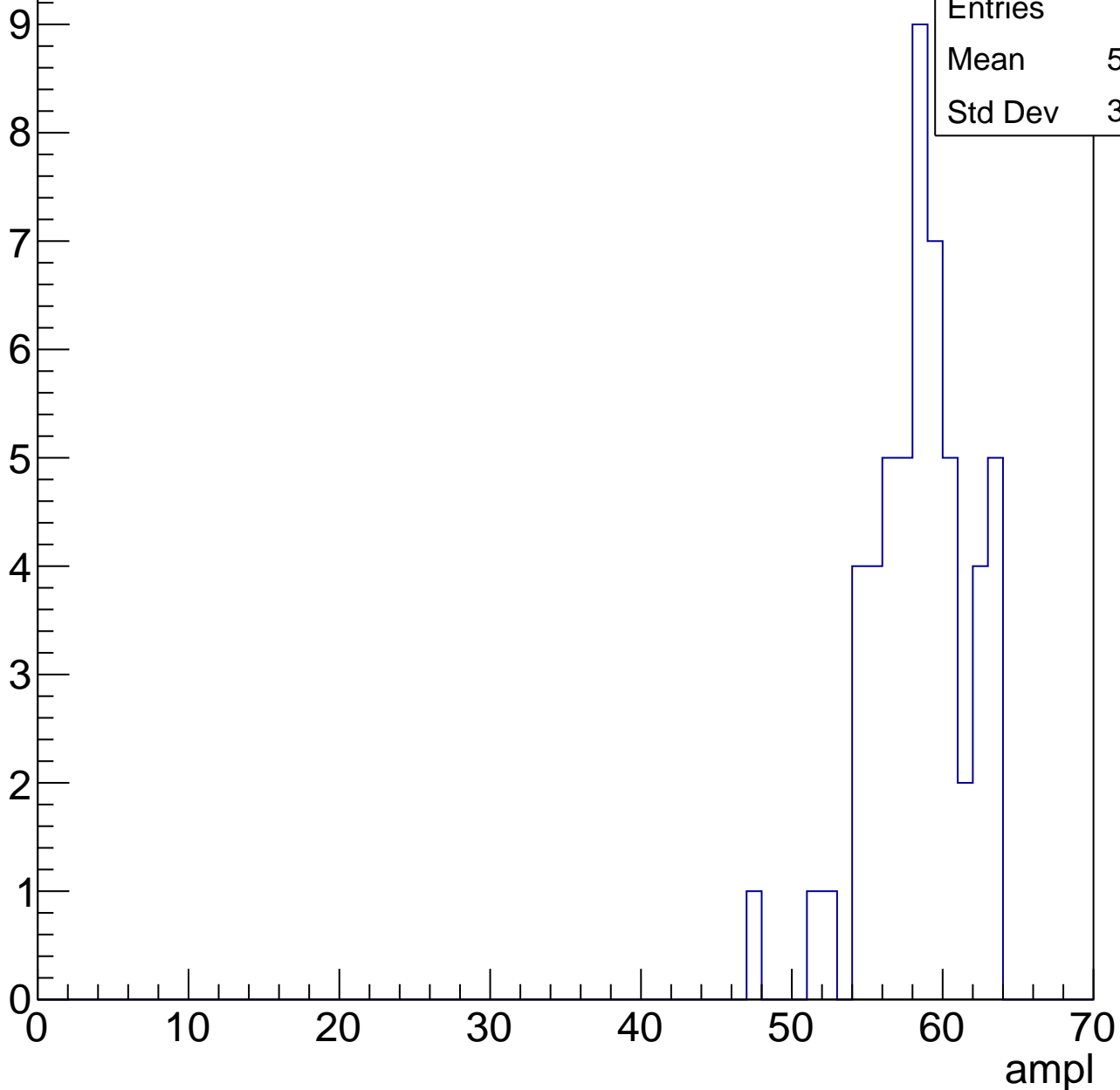


# B0L002S, U23-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	53
Mean	57.94
Std Dev	3.265

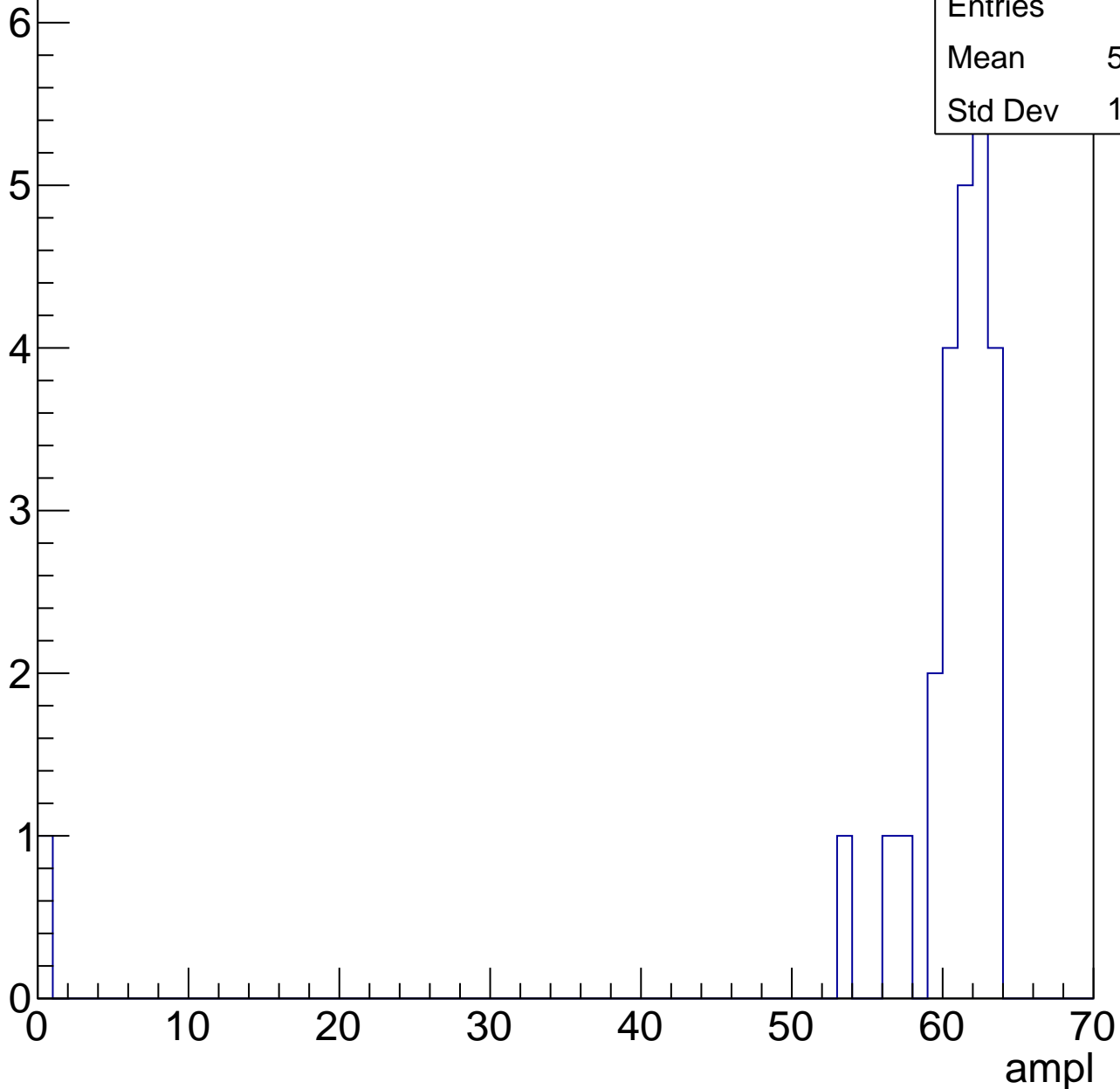


# B0L002S, U23-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

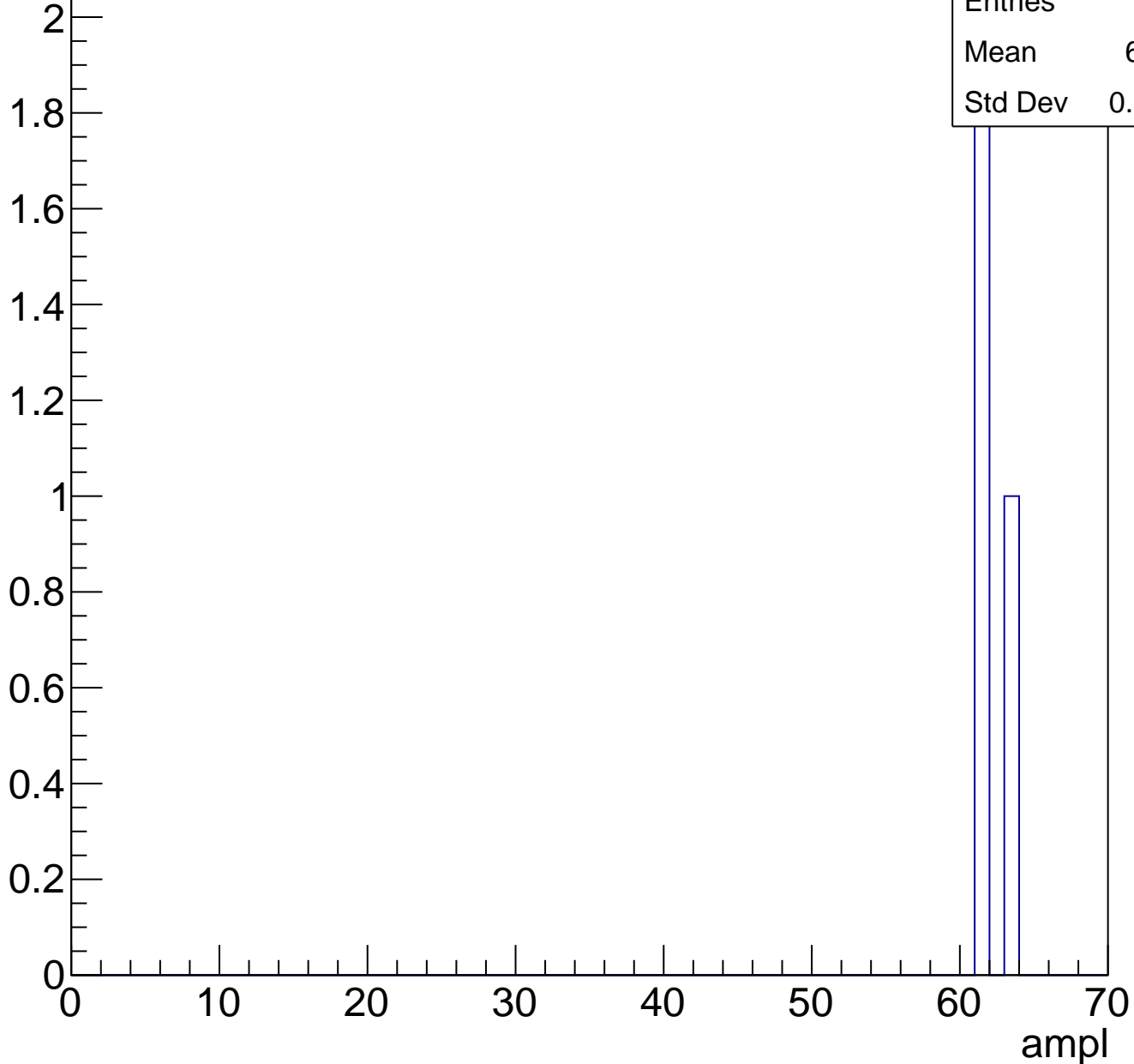
Entries	25
Mean	58.12
Std Dev	12.09



# B0L002S, U23-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch124, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	74
Mean	30.64
Std Dev	4.789

**Gaus mean : 31.9884**

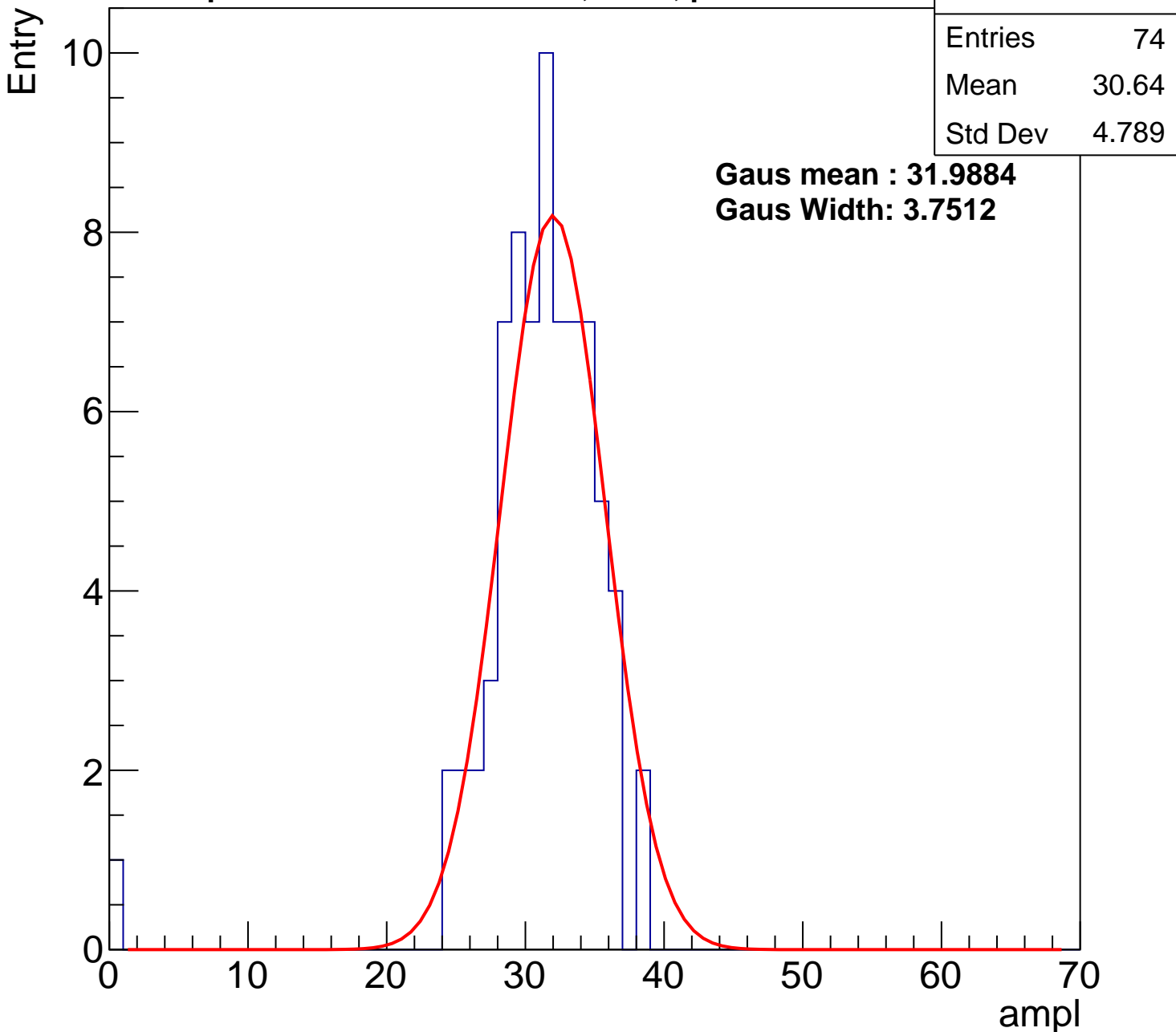
**Gaus Width: 3.7512**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch124, adc1

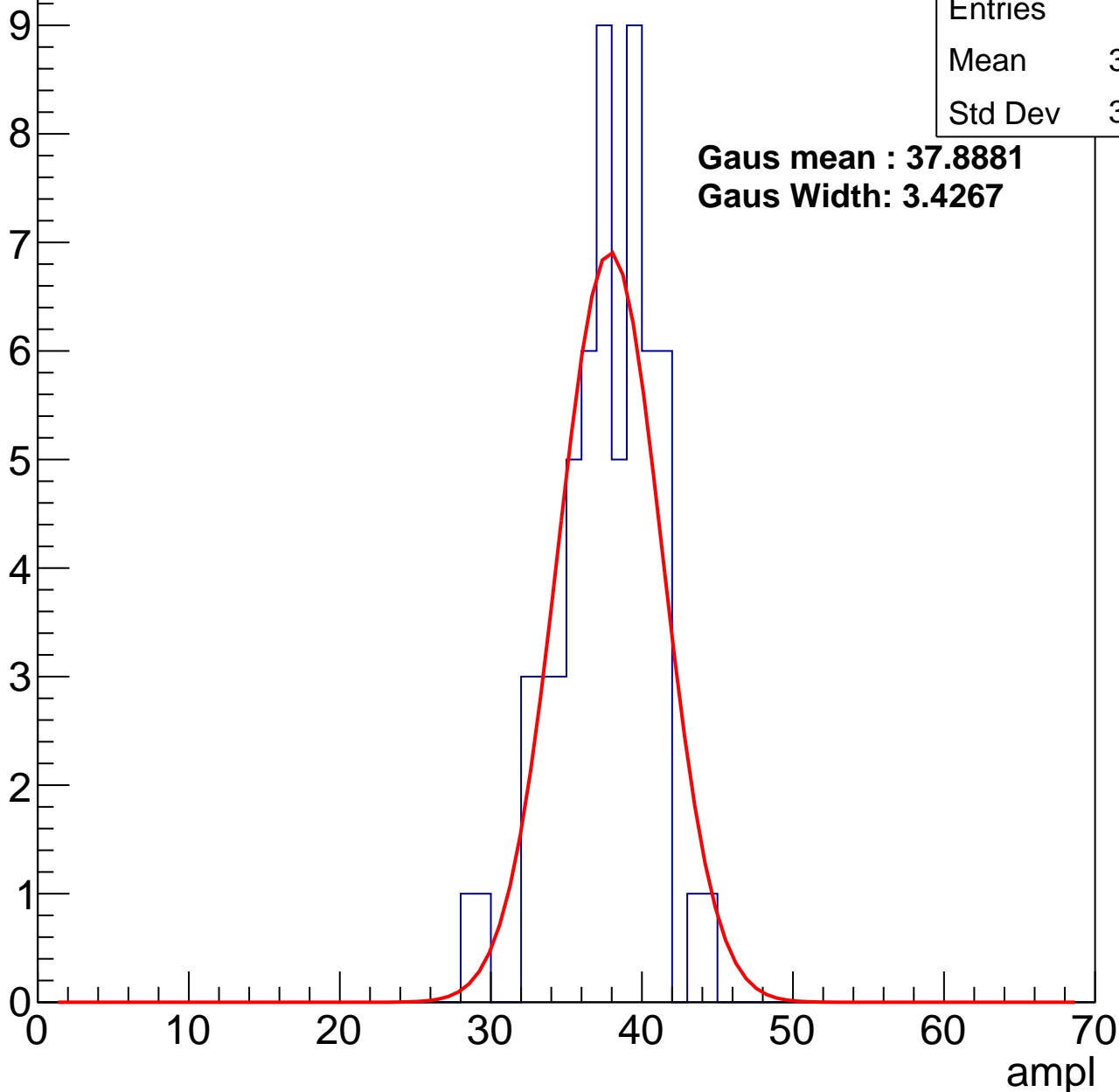
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	59
Mean	37.15
Std Dev	3.177

**Gaus mean : 37.8881**

**Gaus Width: 3.4267**



# B0L002S, U23-ch124, adc2

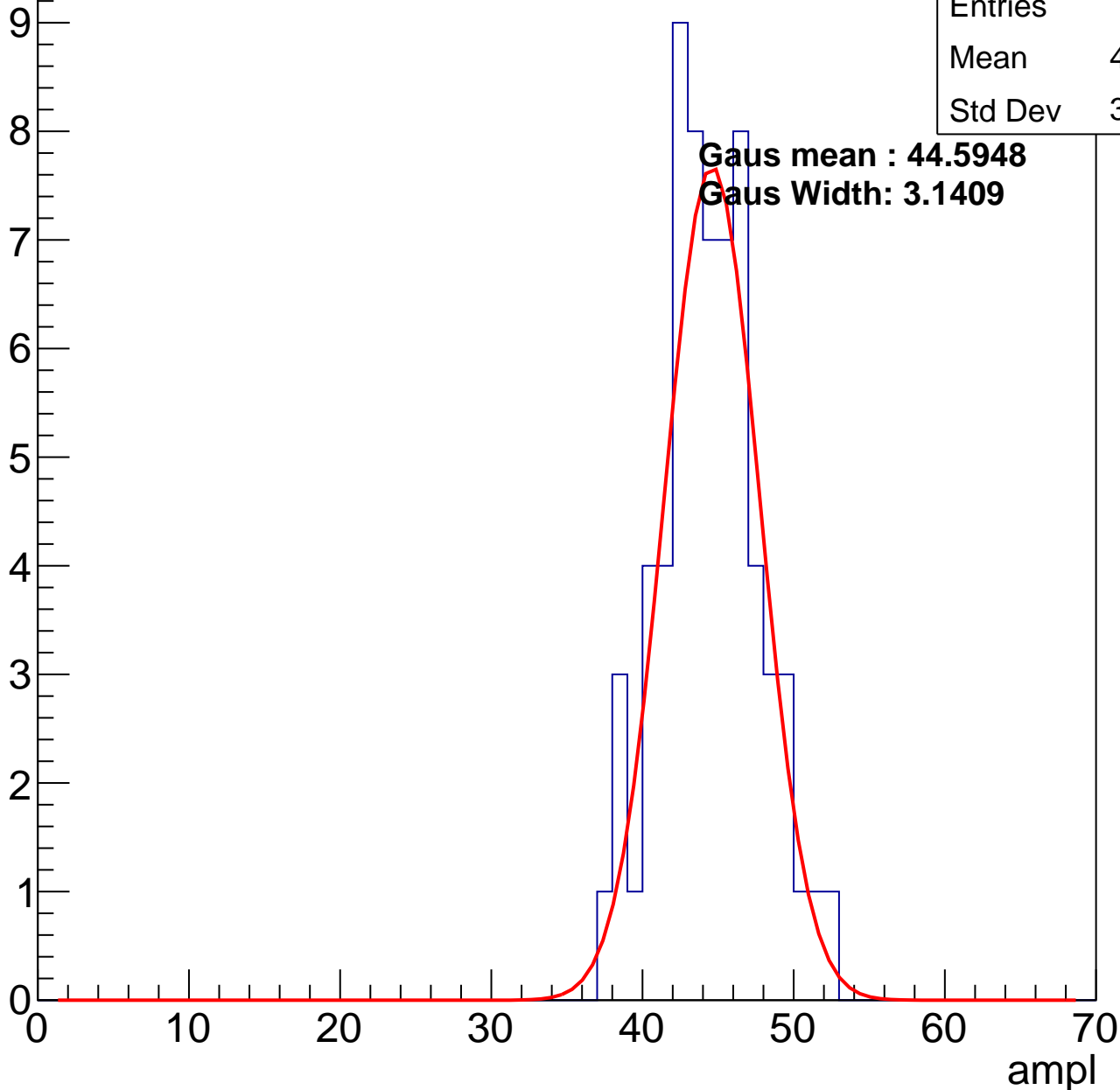
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.98
Std Dev	3.218

**Gaus mean : 44.5948**

**Gaus Width: 3.1409**

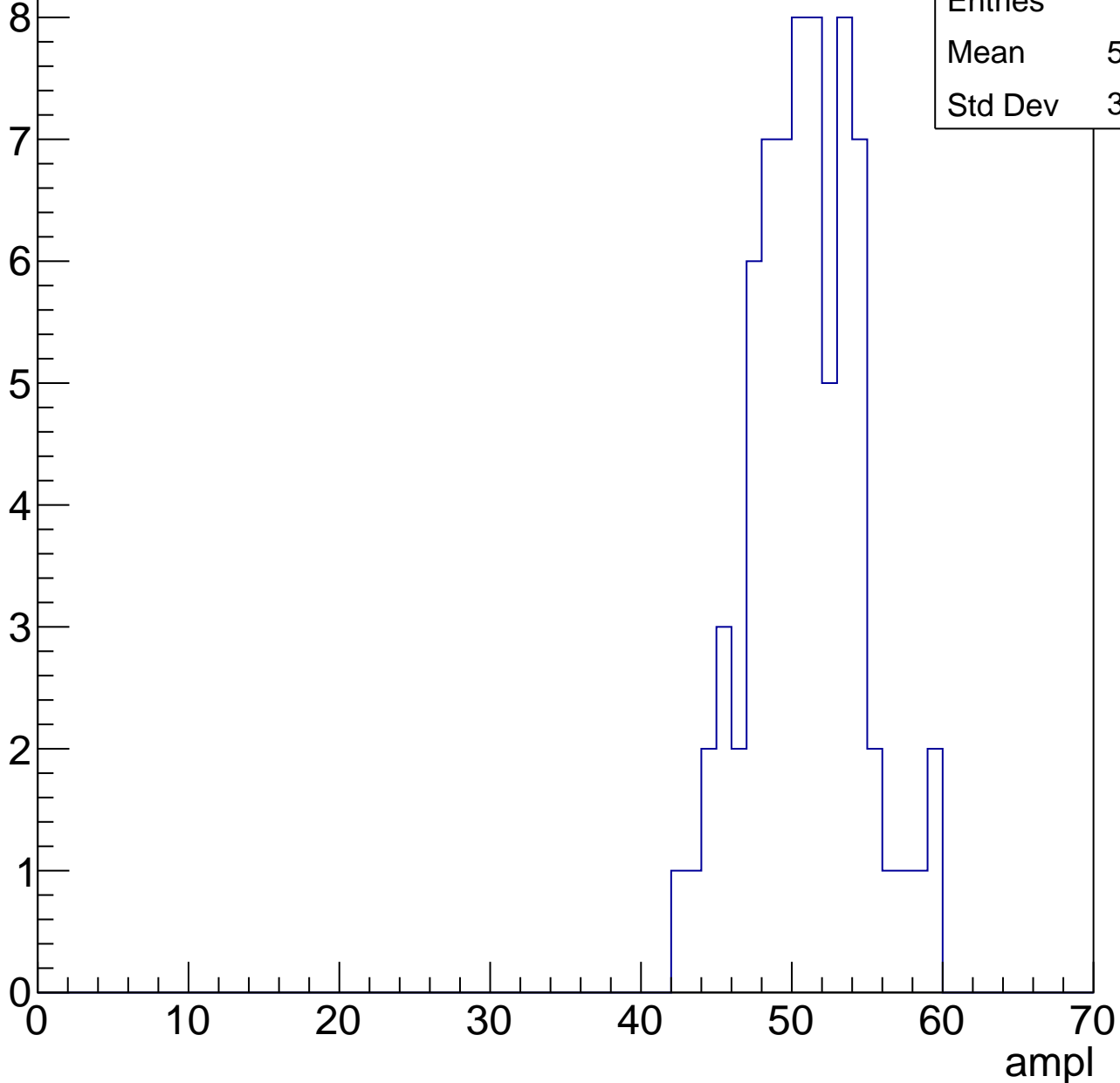


# B0L002S, U23-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

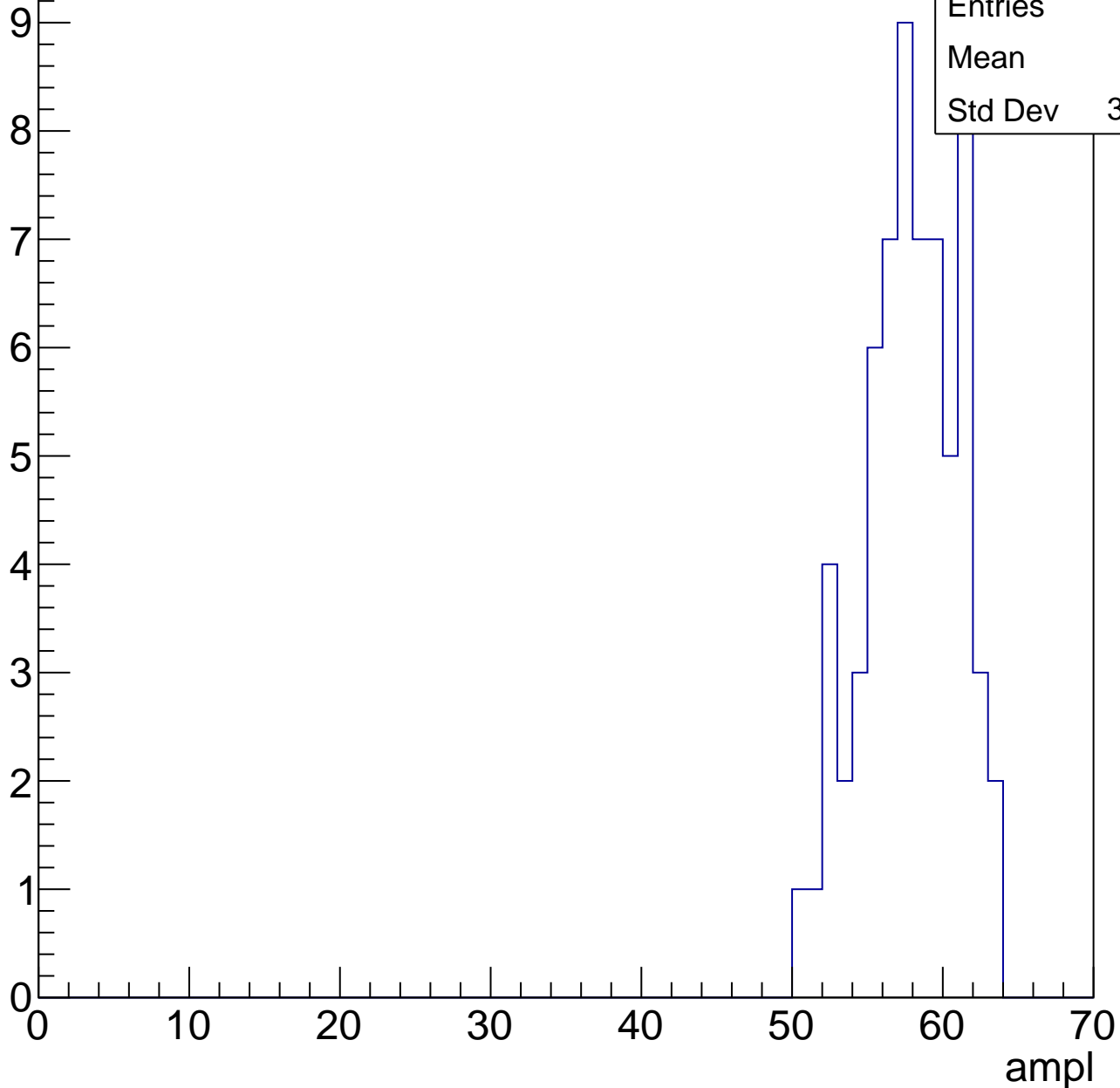
Entries	72
Mean	50.42
Std Dev	3.616



# B0L002S, U23-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

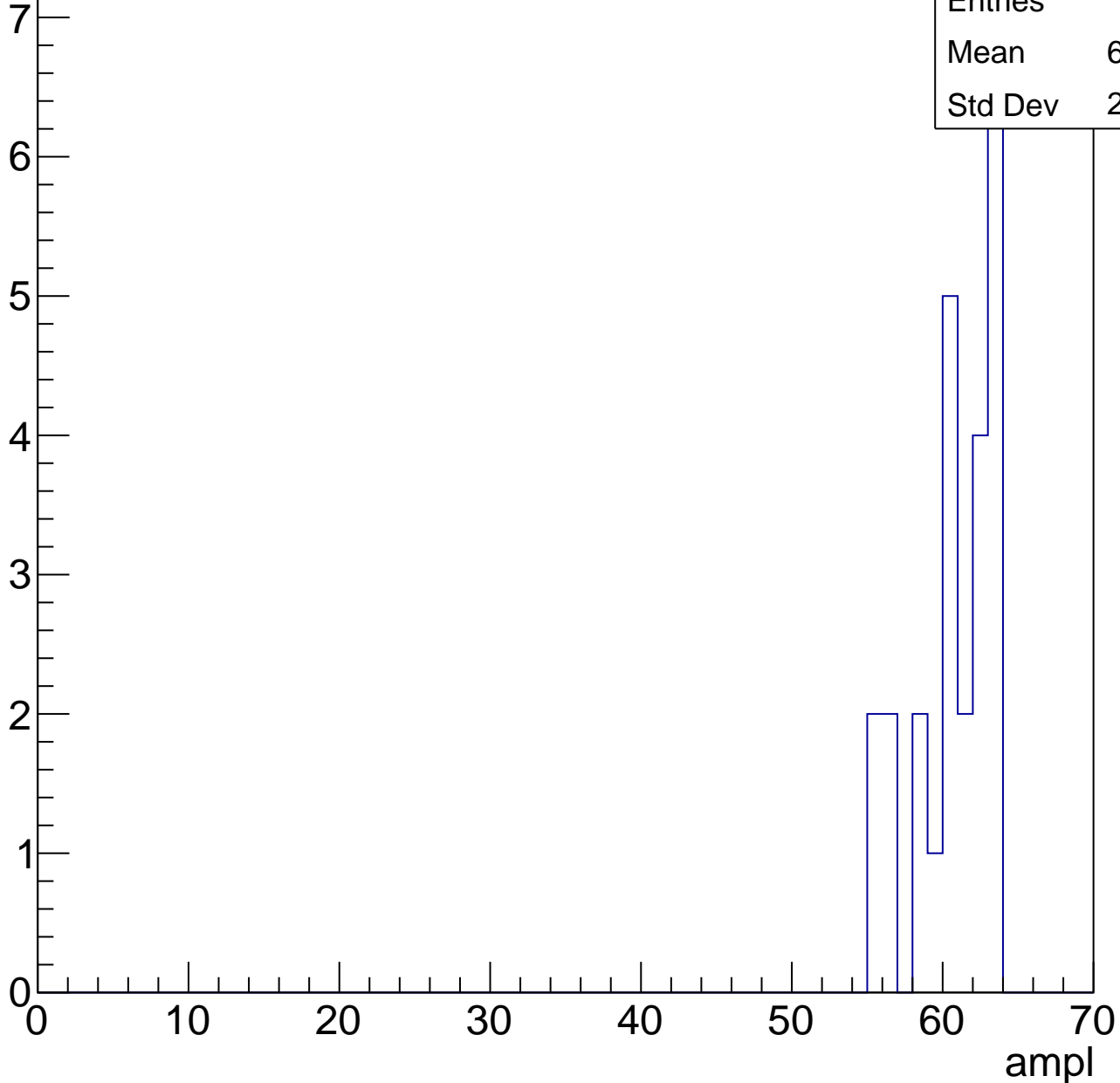


# B0L002S, U23-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

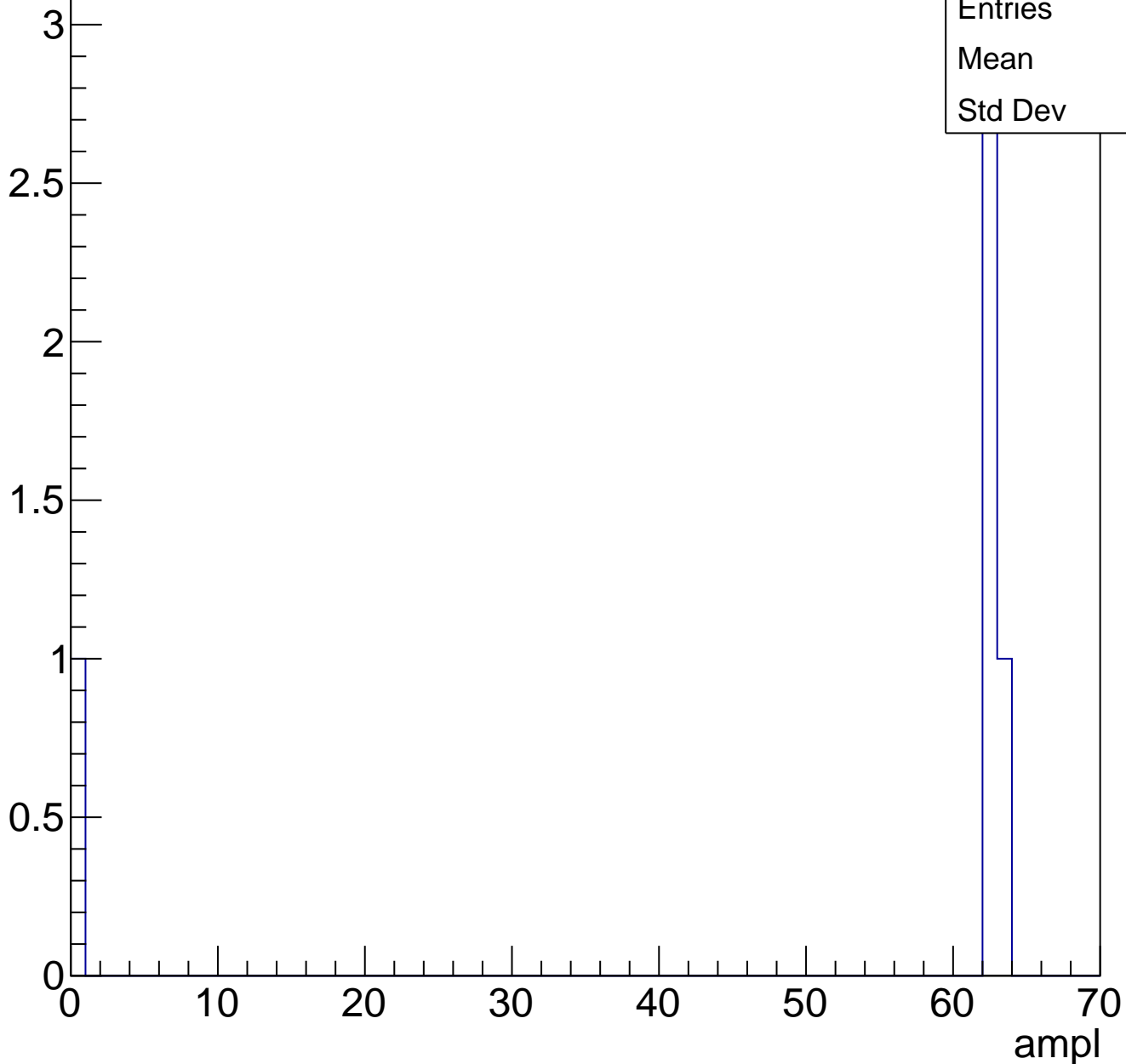
Entries	25
Mean	60.32
Std Dev	2.603



# B0L002S, U23-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L002S, U23-ch125, adc0

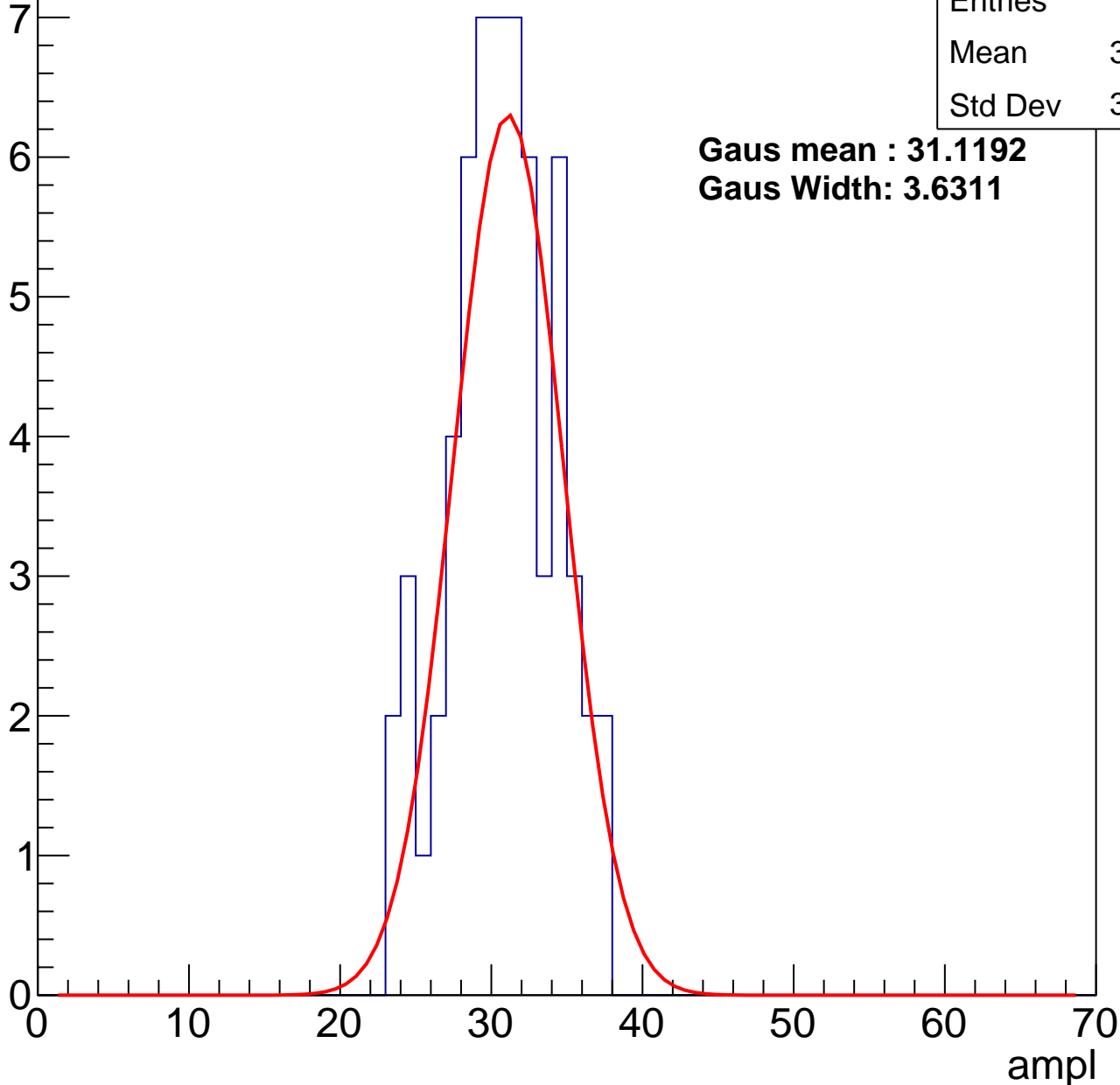
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	61
Mean	30.28
Std Dev	3.446

**Gaus mean : 31.1192**

**Gaus Width: 3.6311**



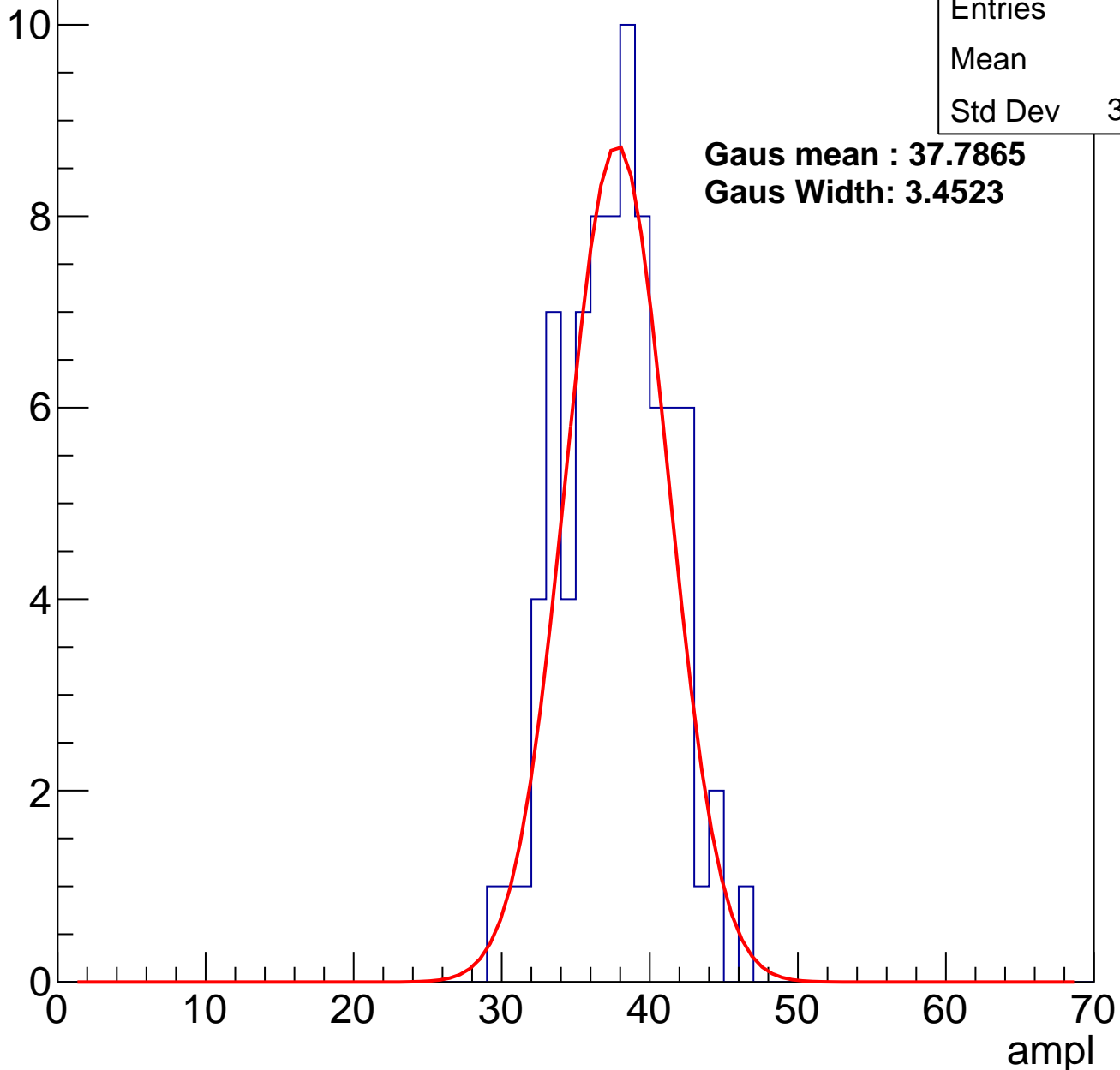
# B0L002S, U23-ch125, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	81
Mean	37.3
Std Dev	3.483

**Gaus mean : 37.7865**  
**Gaus Width: 3.4523**

Entry



# B0L002S, U23-ch125, adc2

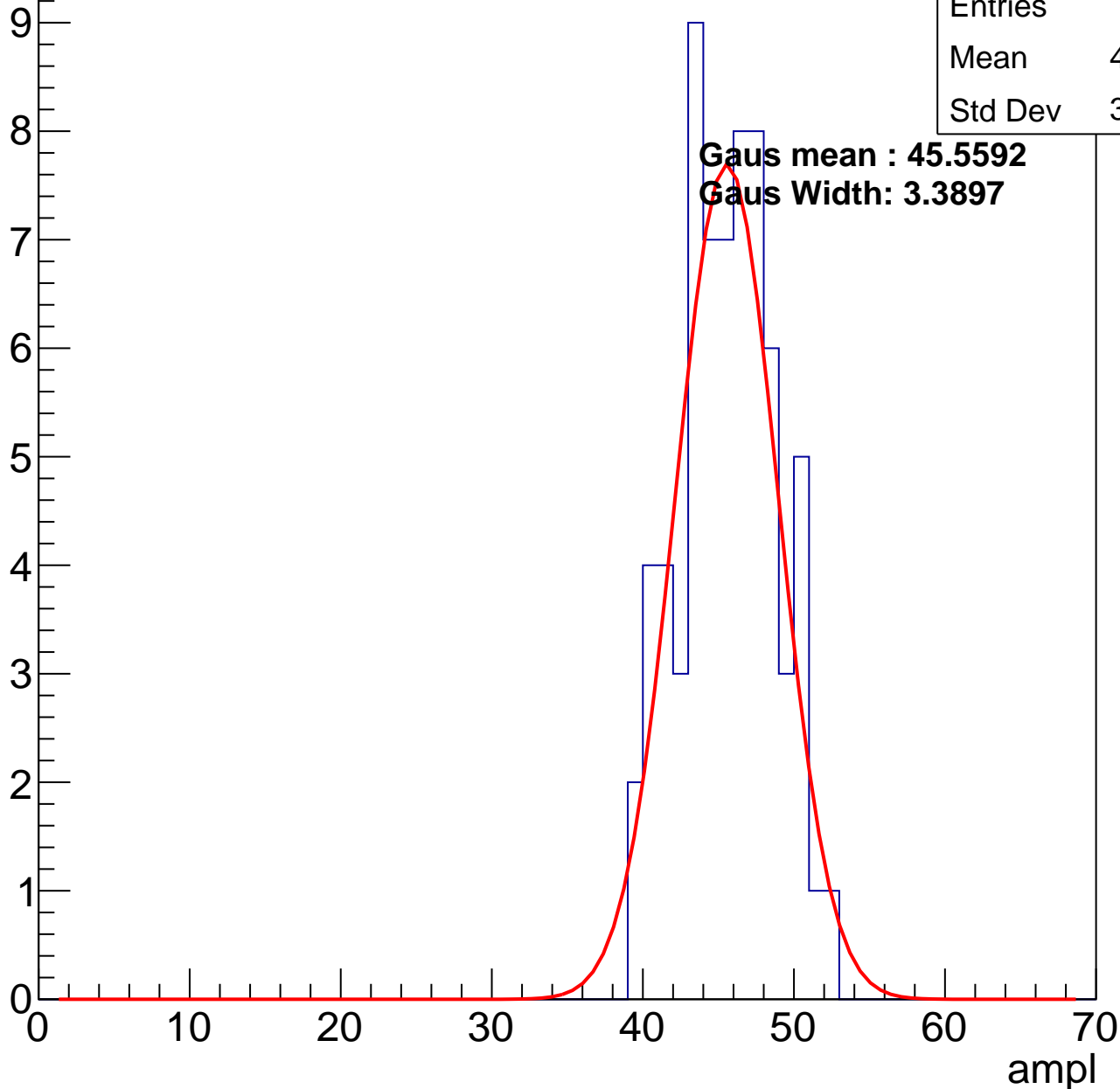
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.15
Std Dev	3.107

**Gaus mean : 45.5592**

**Gaus Width: 3.3897**

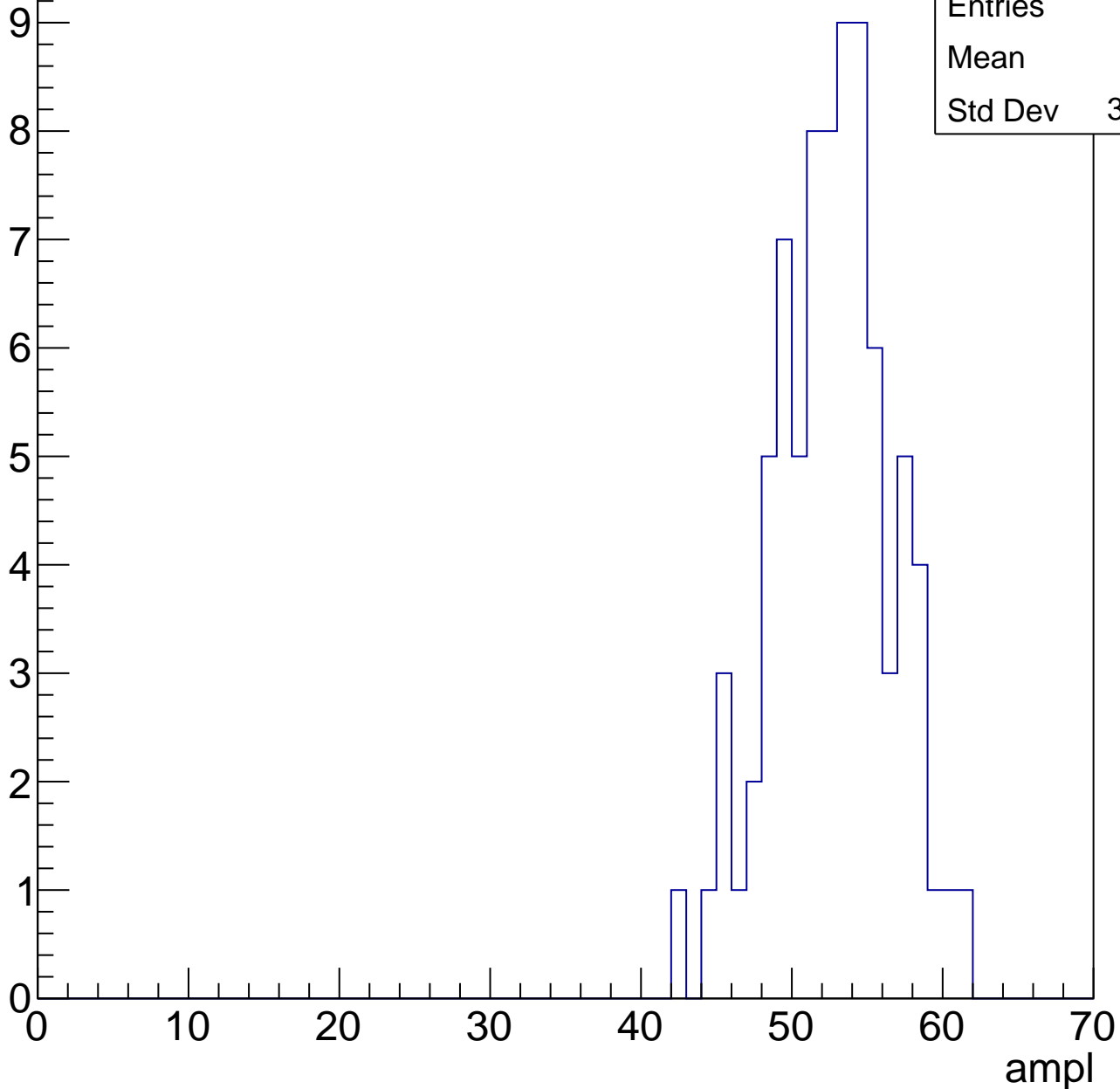


# B0L002S, U23-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

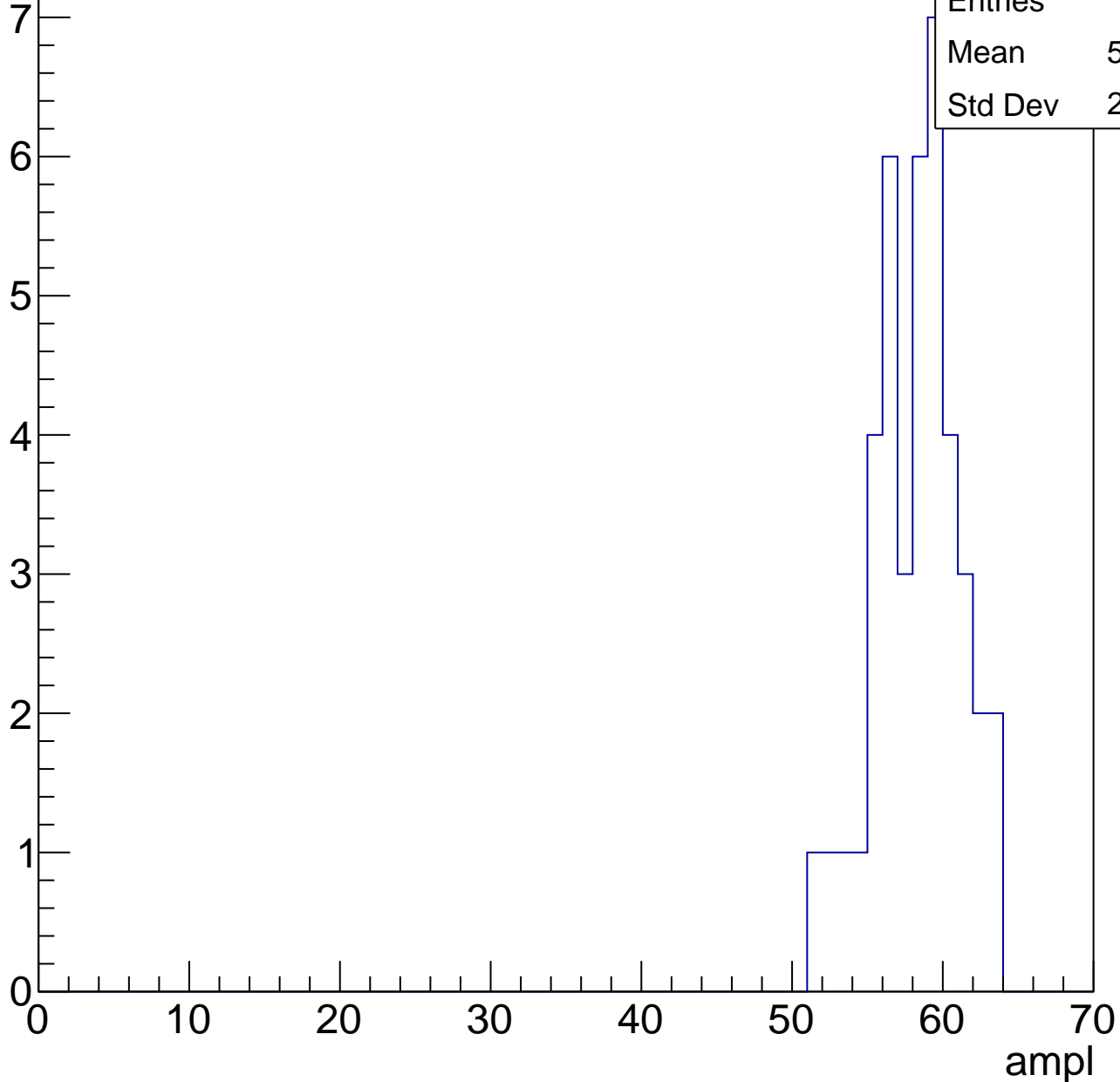
Entries	80
Mean	52.2
Std Dev	3.832



# B0L002S, U23-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

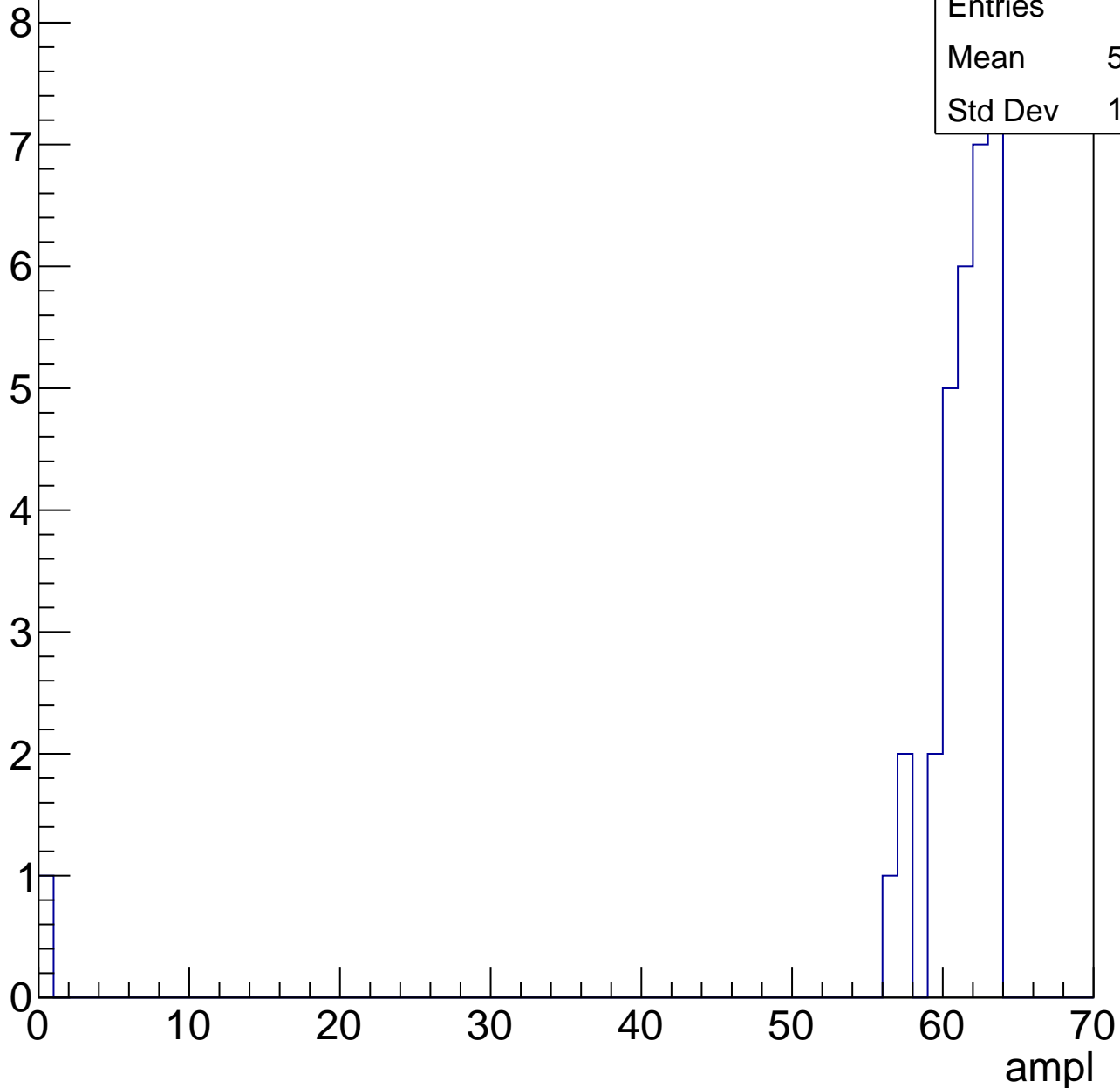


# B0L002S, U23-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59.12
Std Dev	10.78



# B0L002S, U23-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch126, adc0

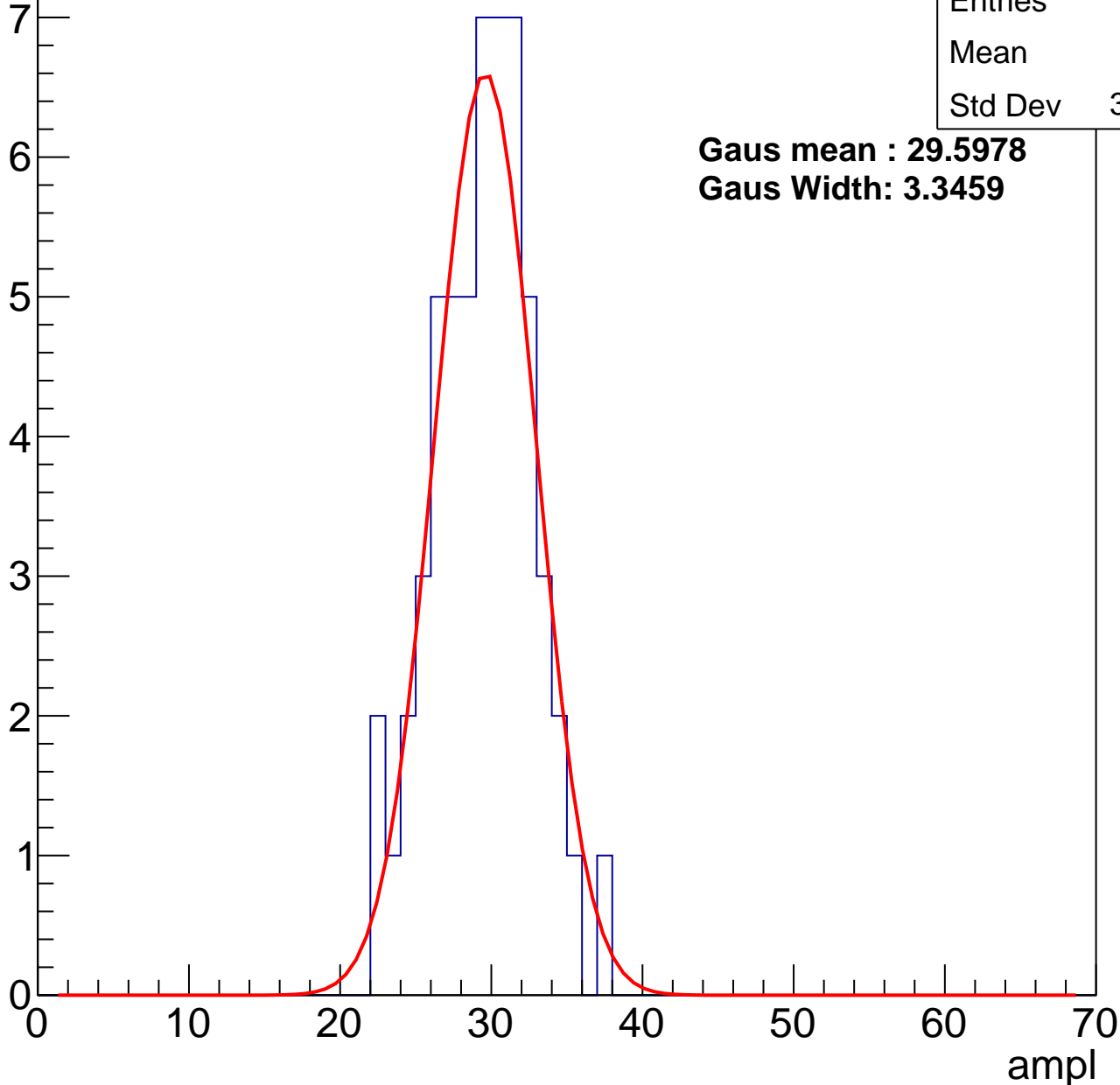
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	56
Mean	29
Std Dev	3.218

**Gaus mean : 29.5978**

**Gaus Width: 3.3459**



# B0L002S, U23-ch126, adc1

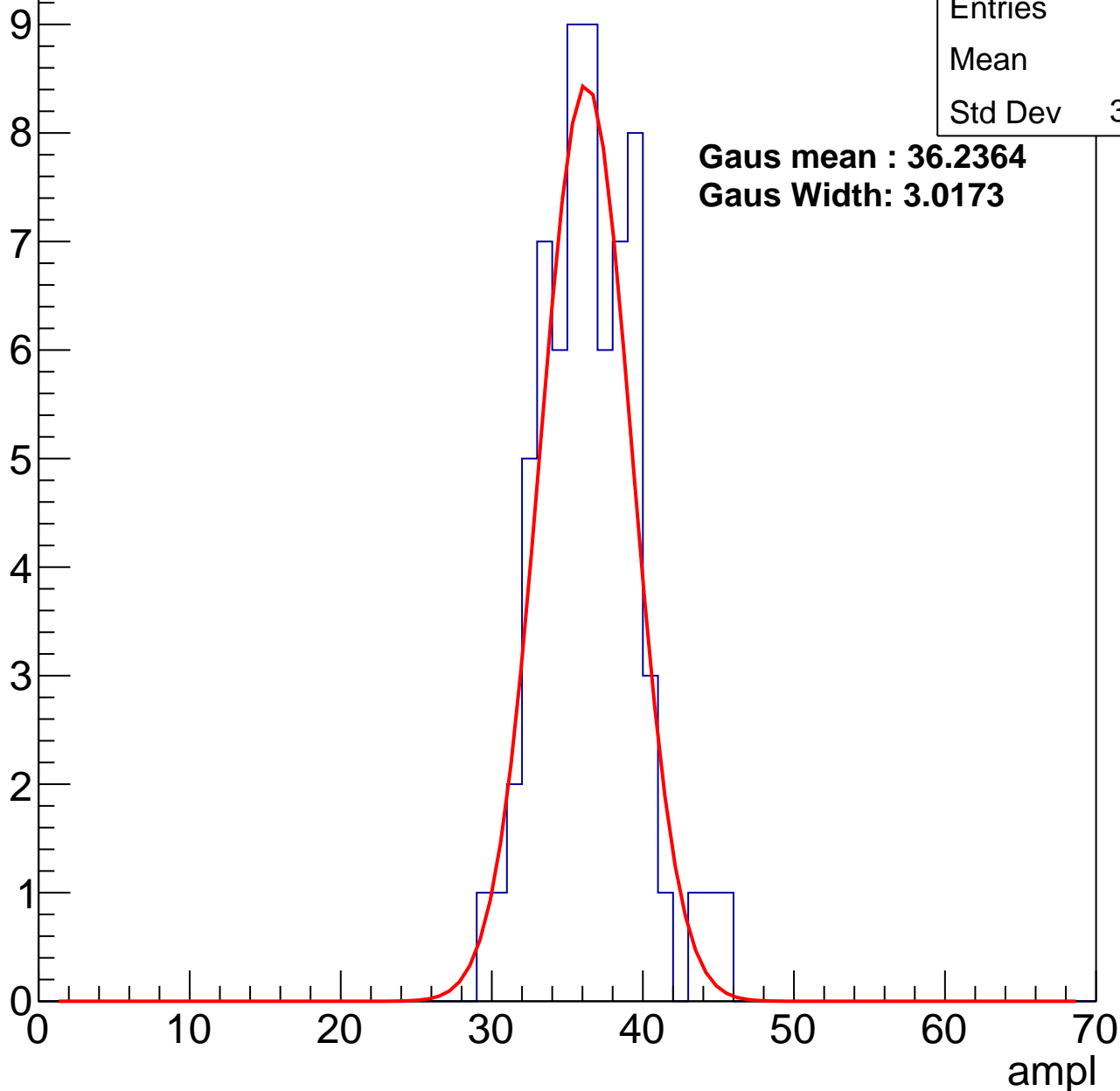
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	68
Mean	36
Std Dev	3.172

**Gaus mean : 36.2364**

**Gaus Width: 3.0173**



# B0L002S, U23-ch126, adc2

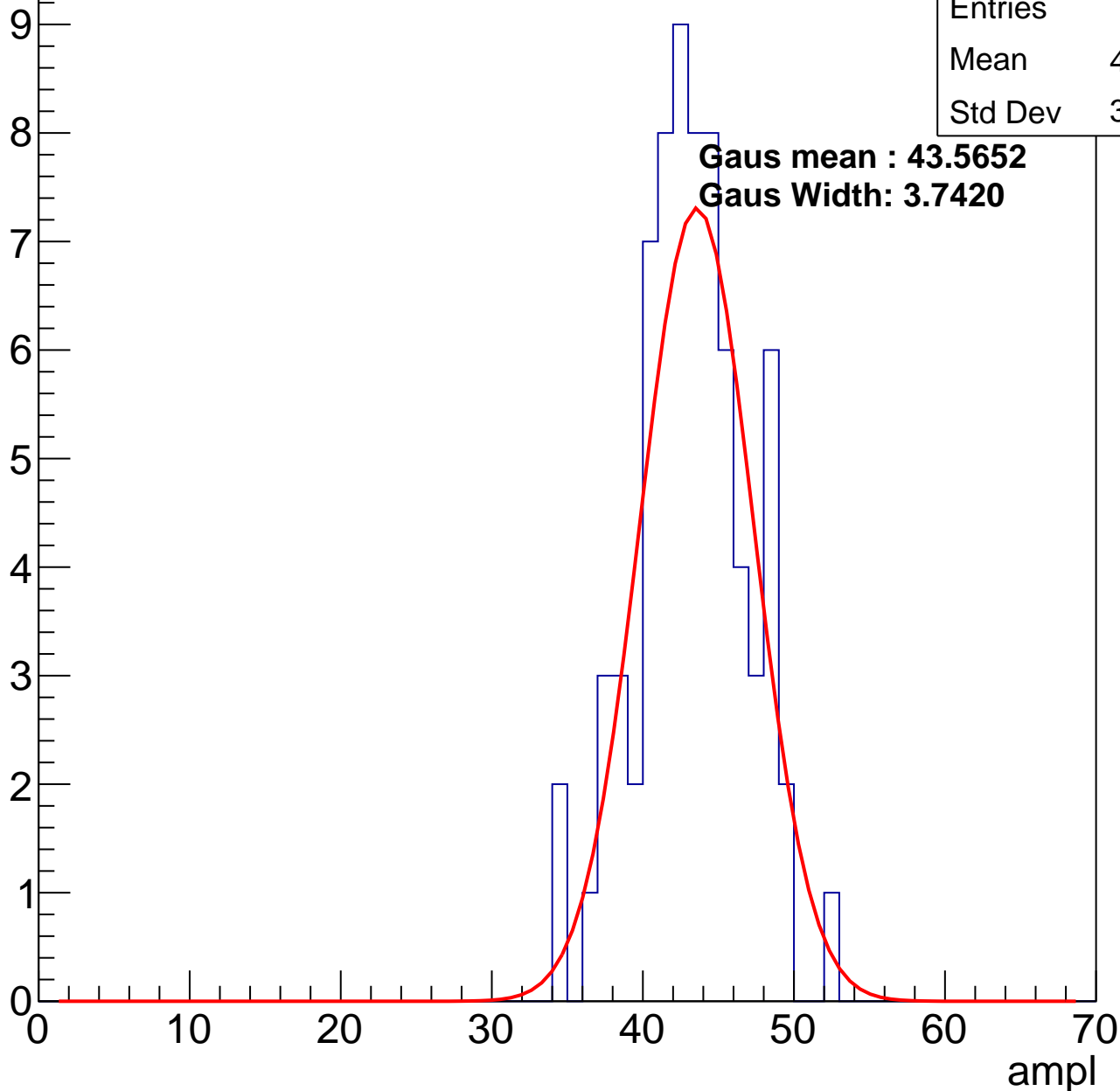
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.77
Std Dev	3.598

**Gaus mean : 43.5652**

**Gaus Width: 3.7420**

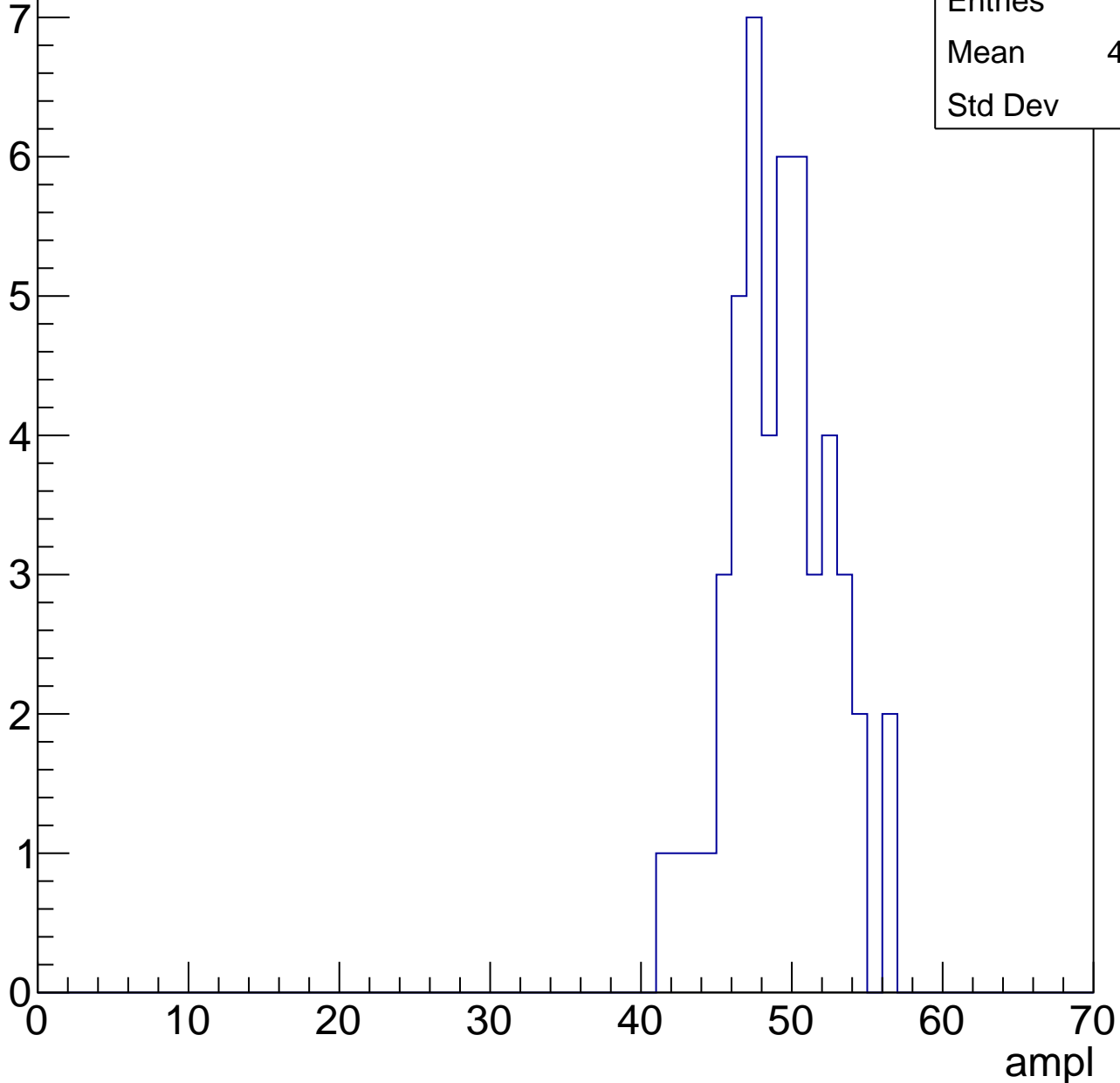


# B0L002S, U23-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	49
Mean	48.78
Std Dev	3.34

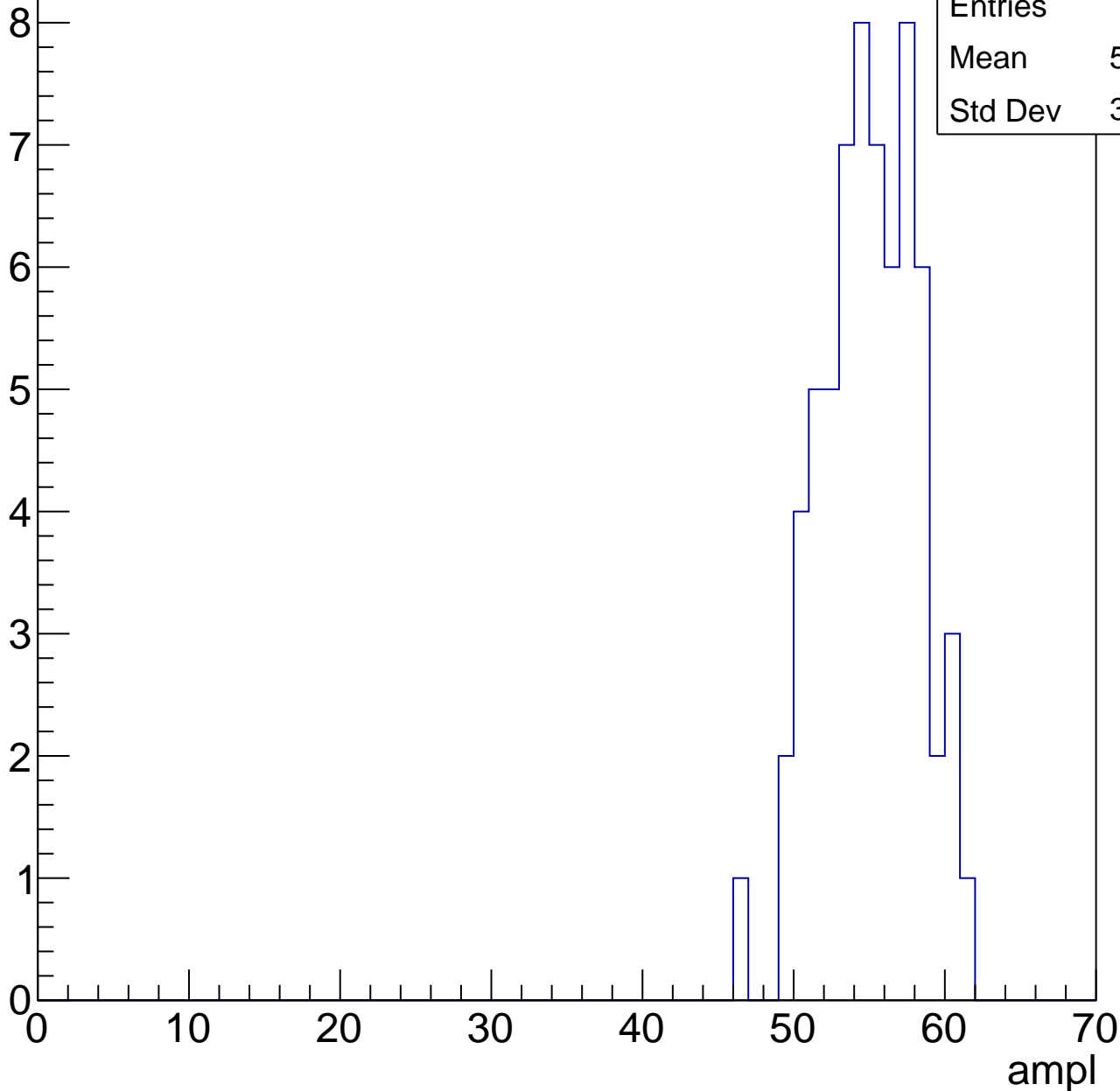


# B0L002S, U23-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	65
Mean	54.55
Std Dev	3.128

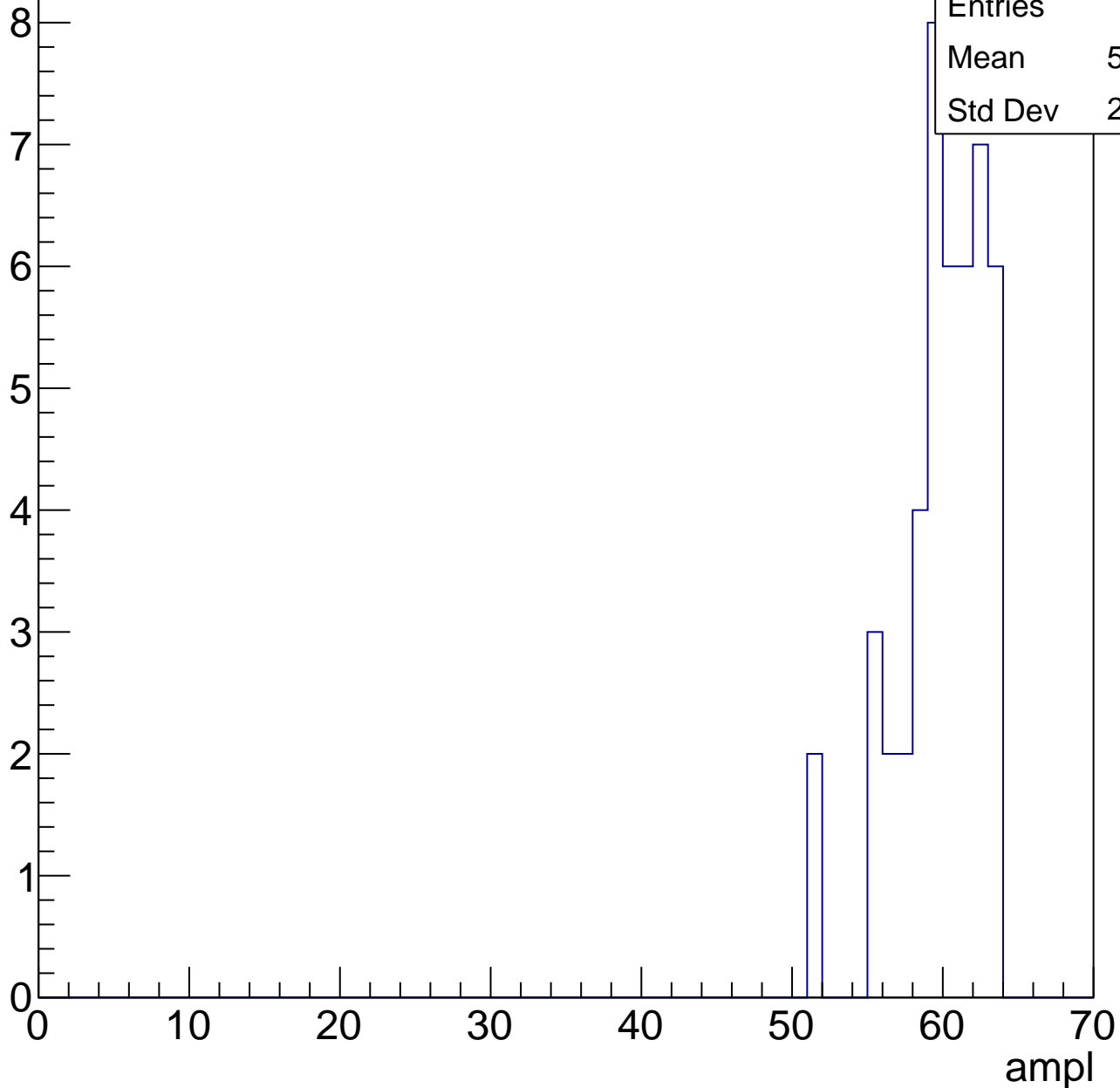


# B0L002S, U23-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

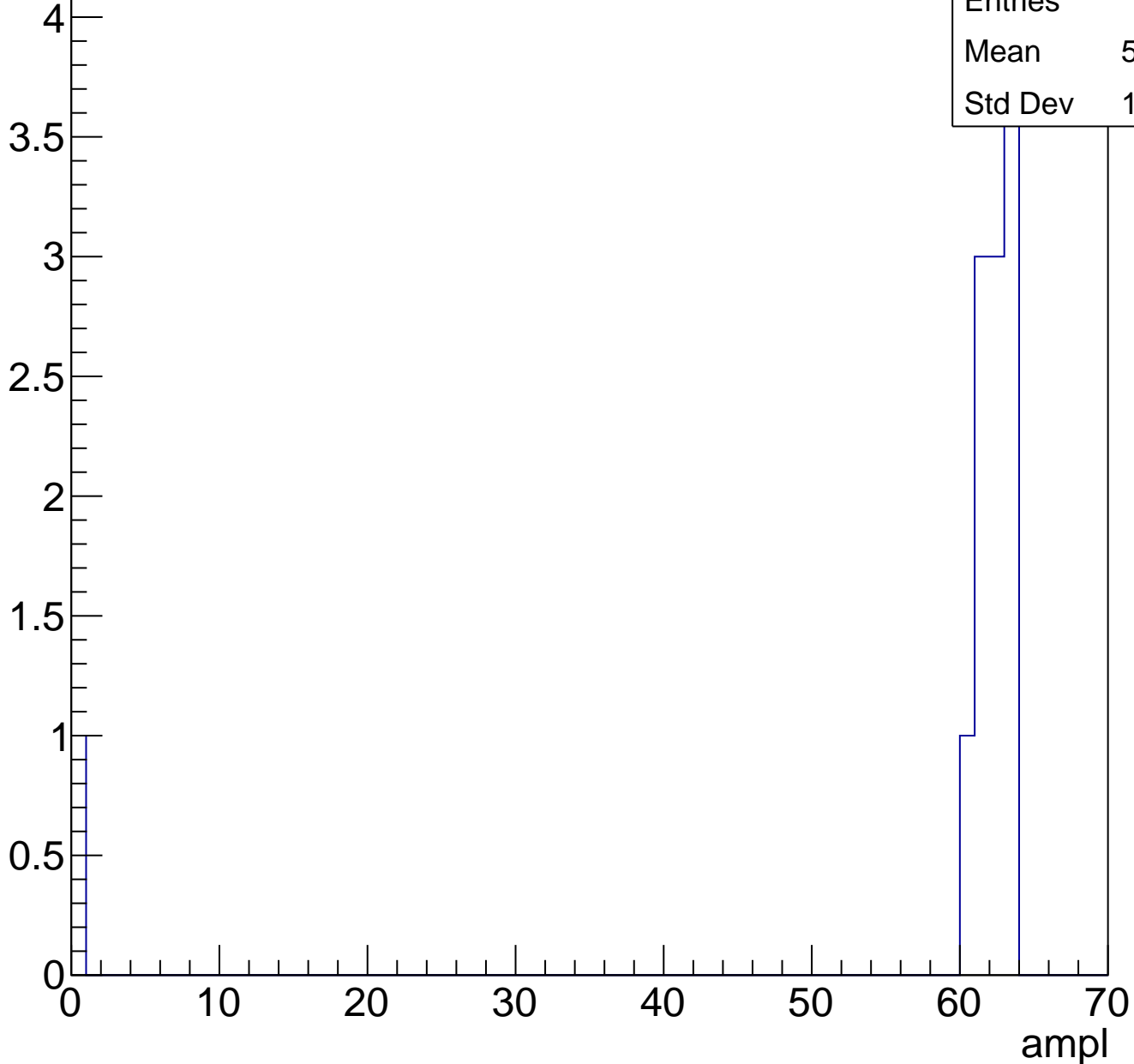
Entries	46
Mean	59.46
Std Dev	2.895



# B0L002S, U23-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U23-ch127, adc0

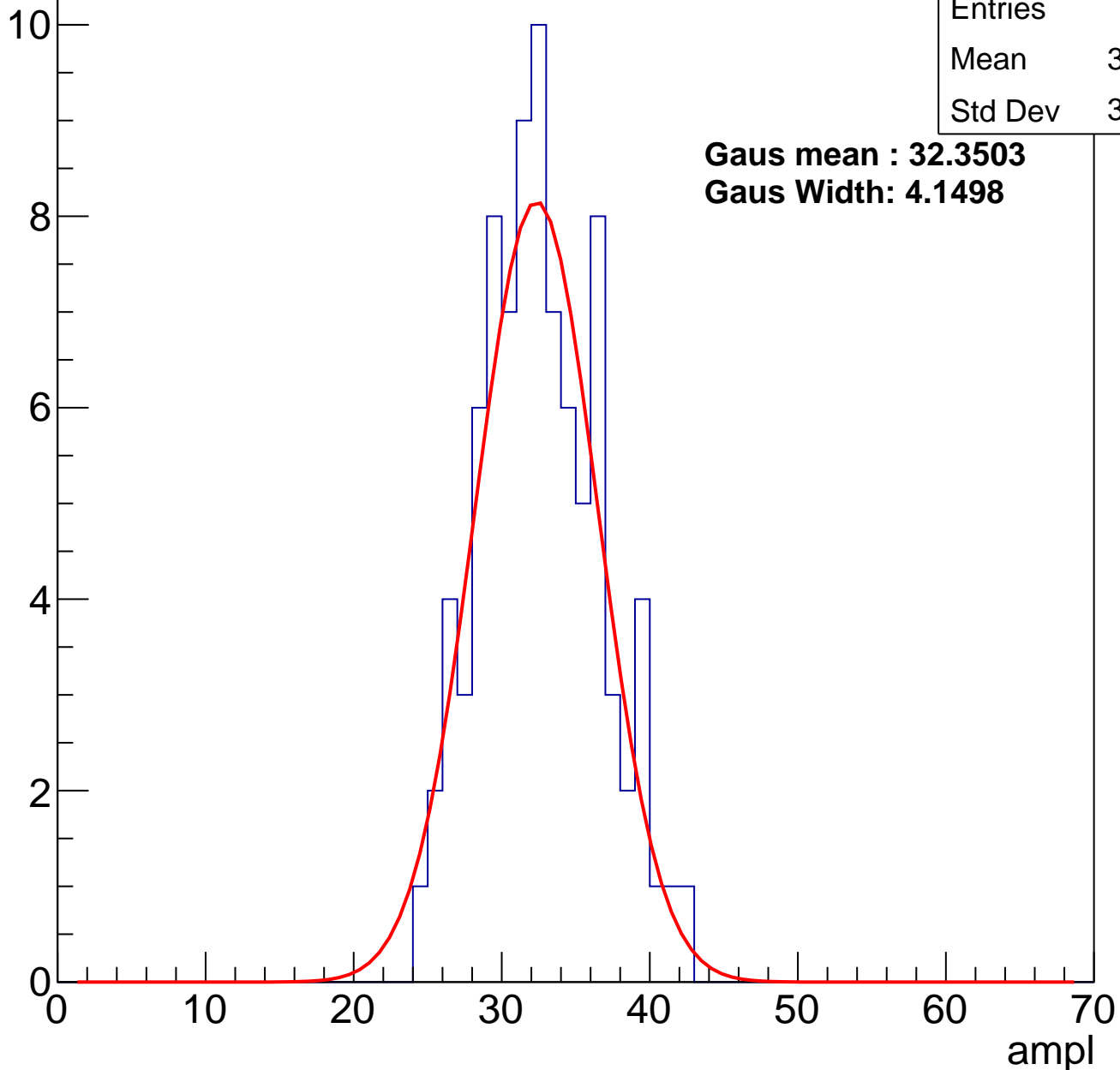
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	88
Mean	32.18
Std Dev	3.953

**Gaus mean : 32.3503**

**Gaus Width: 4.1498**

Entry



# B0L002S, U23-ch127, adc1

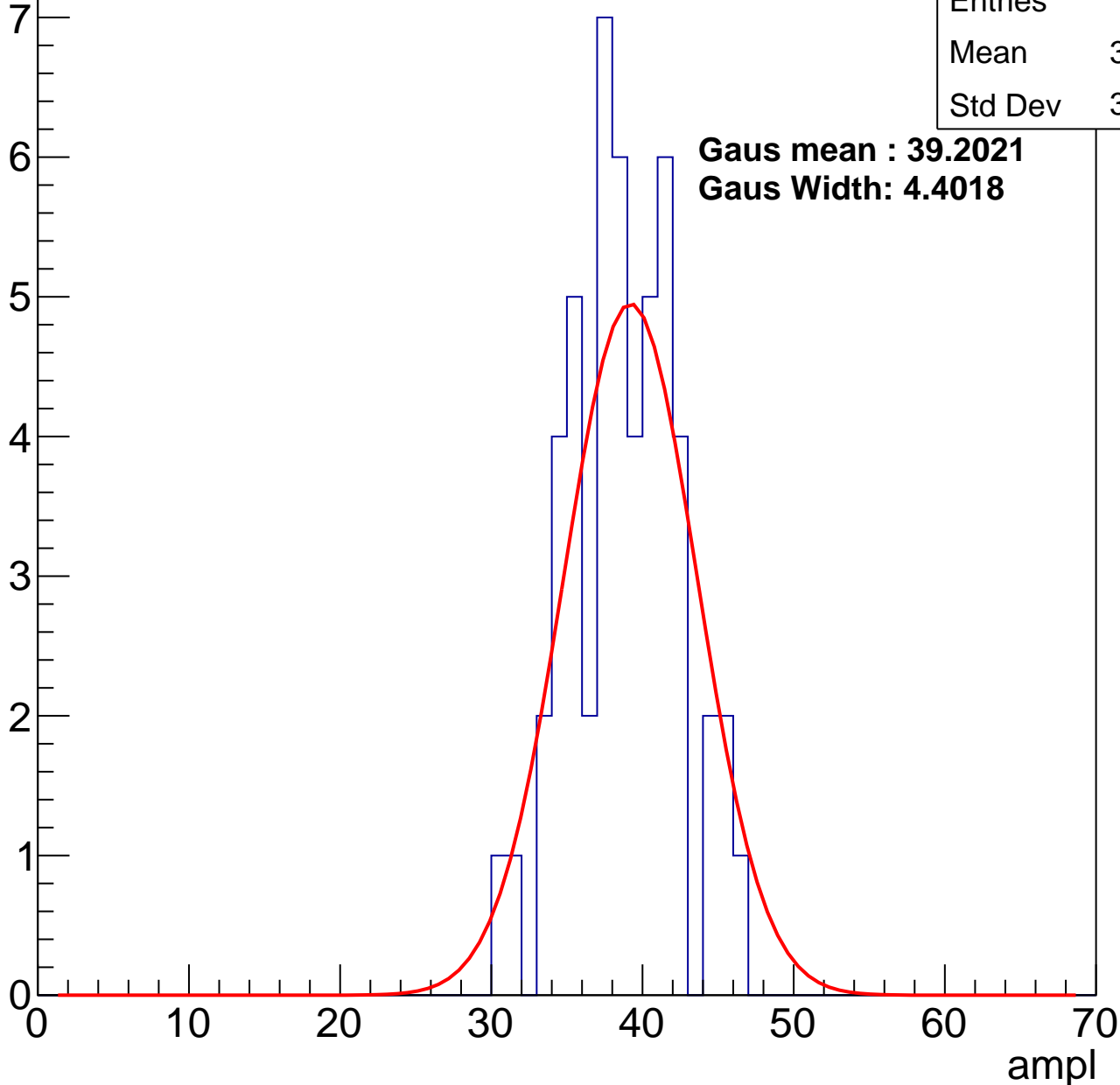
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	52
Mean	38.29
Std Dev	3.559

**Gaus mean : 39.2021**

**Gaus Width: 4.4018**



# B0L002S, U23-ch127, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	64
Mean	44.7
Std Dev	3.353

**Gaus mean : 45.3651**

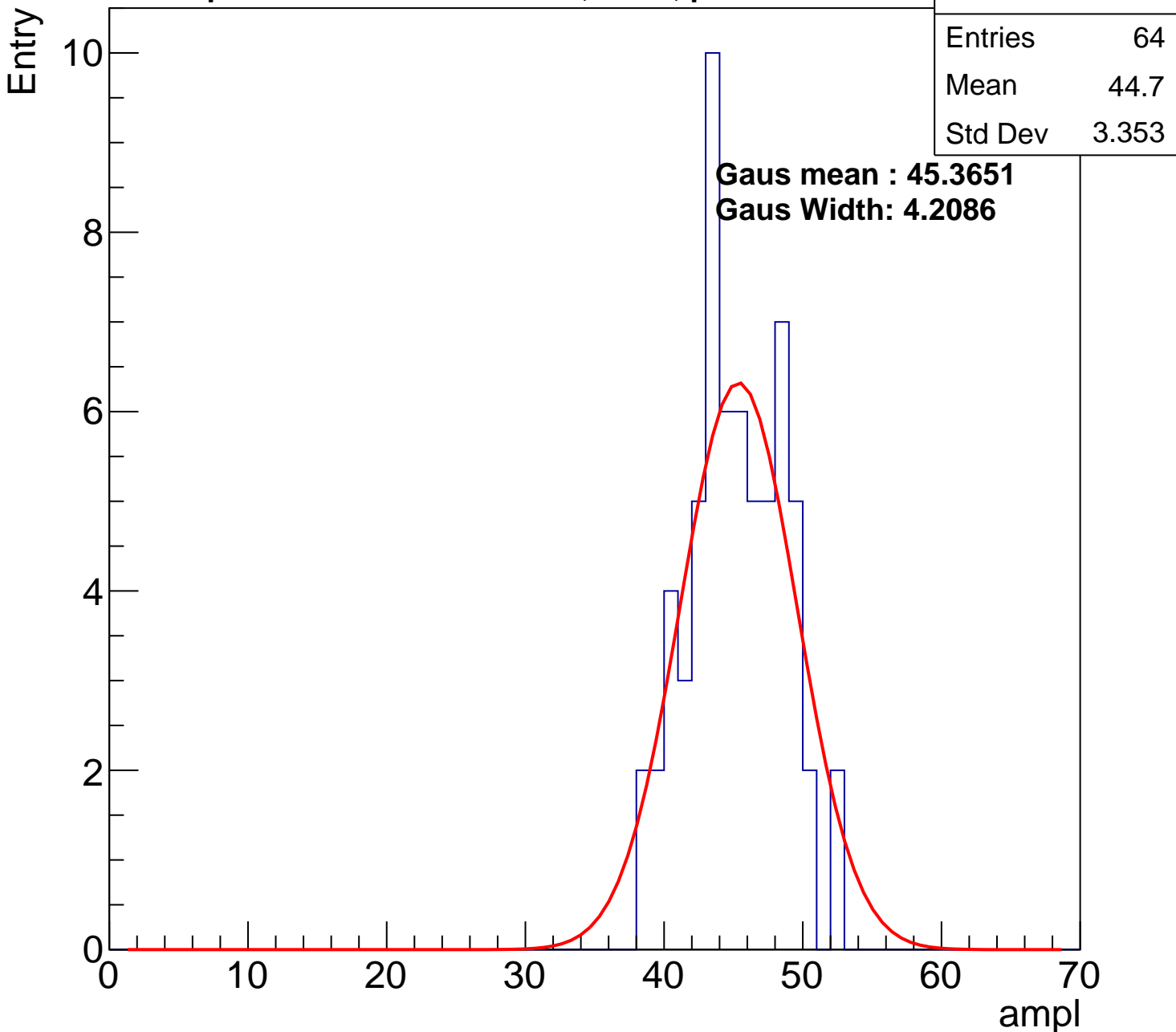
**Gaus Width: 4.2086**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

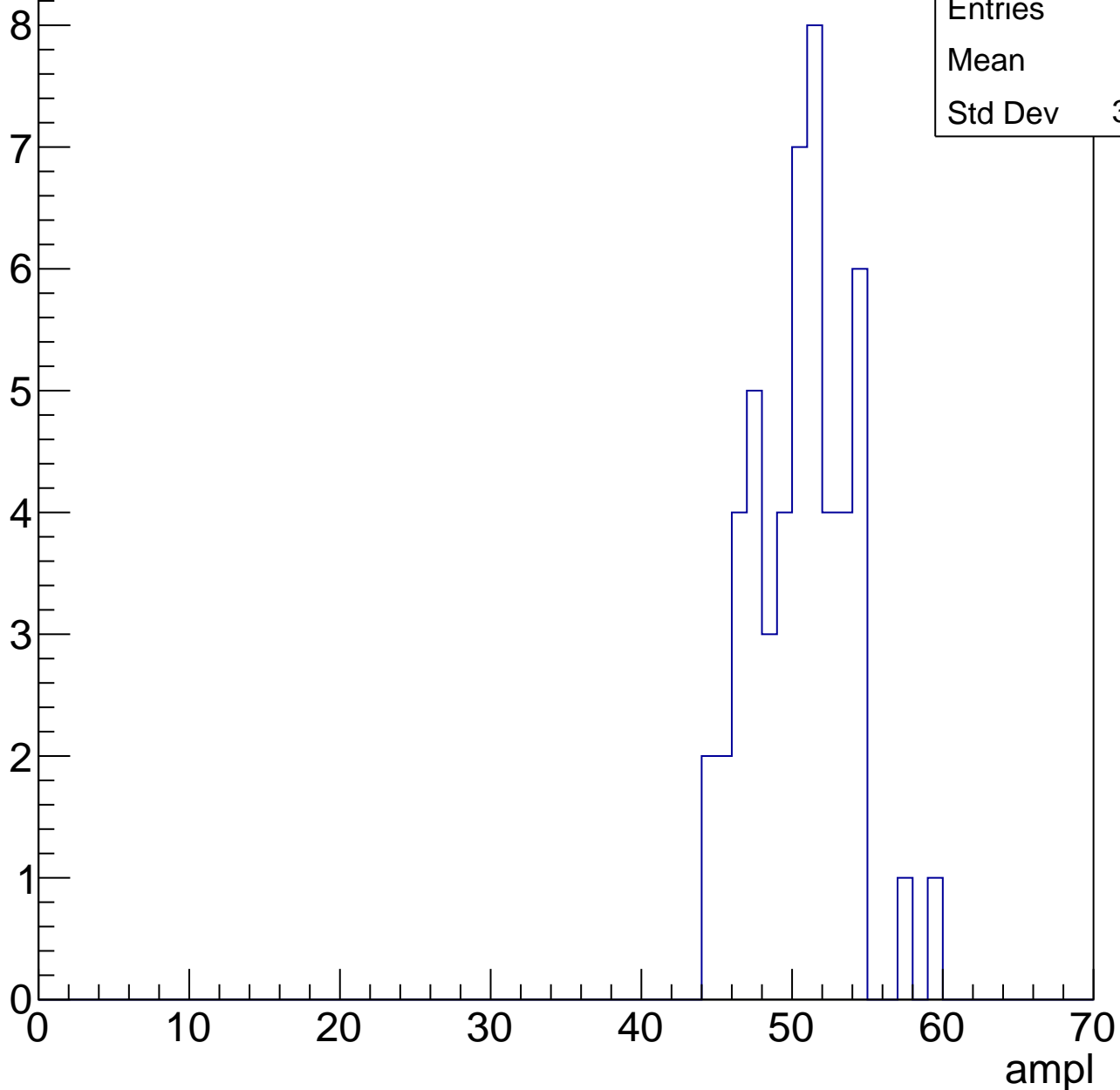


# B0L002S, U23-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	51
Mean	50.1
Std Dev	3.231



# B0L002S, U23-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

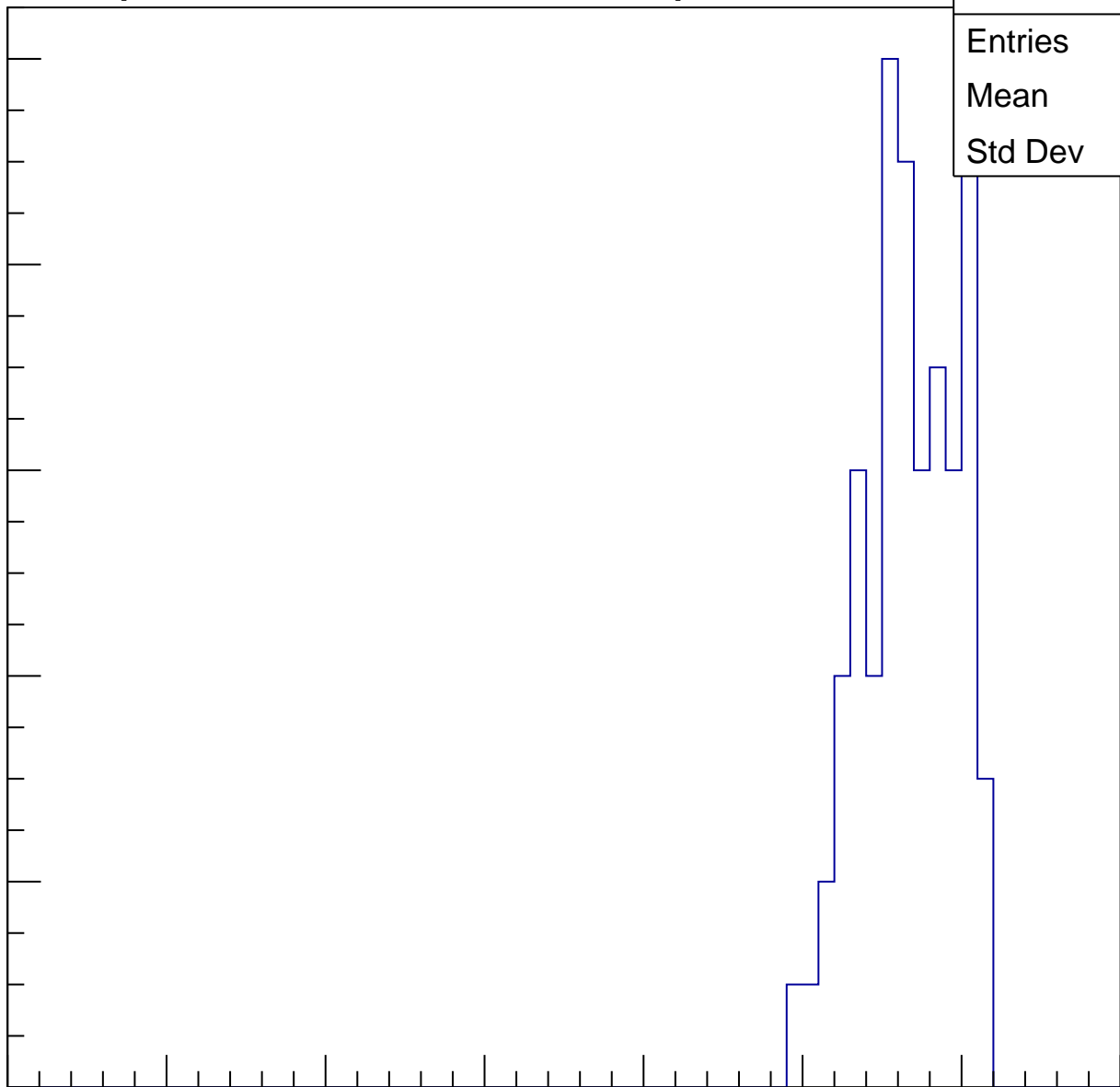
Entries	68
Mean	56.21
Std Dev	2.928

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

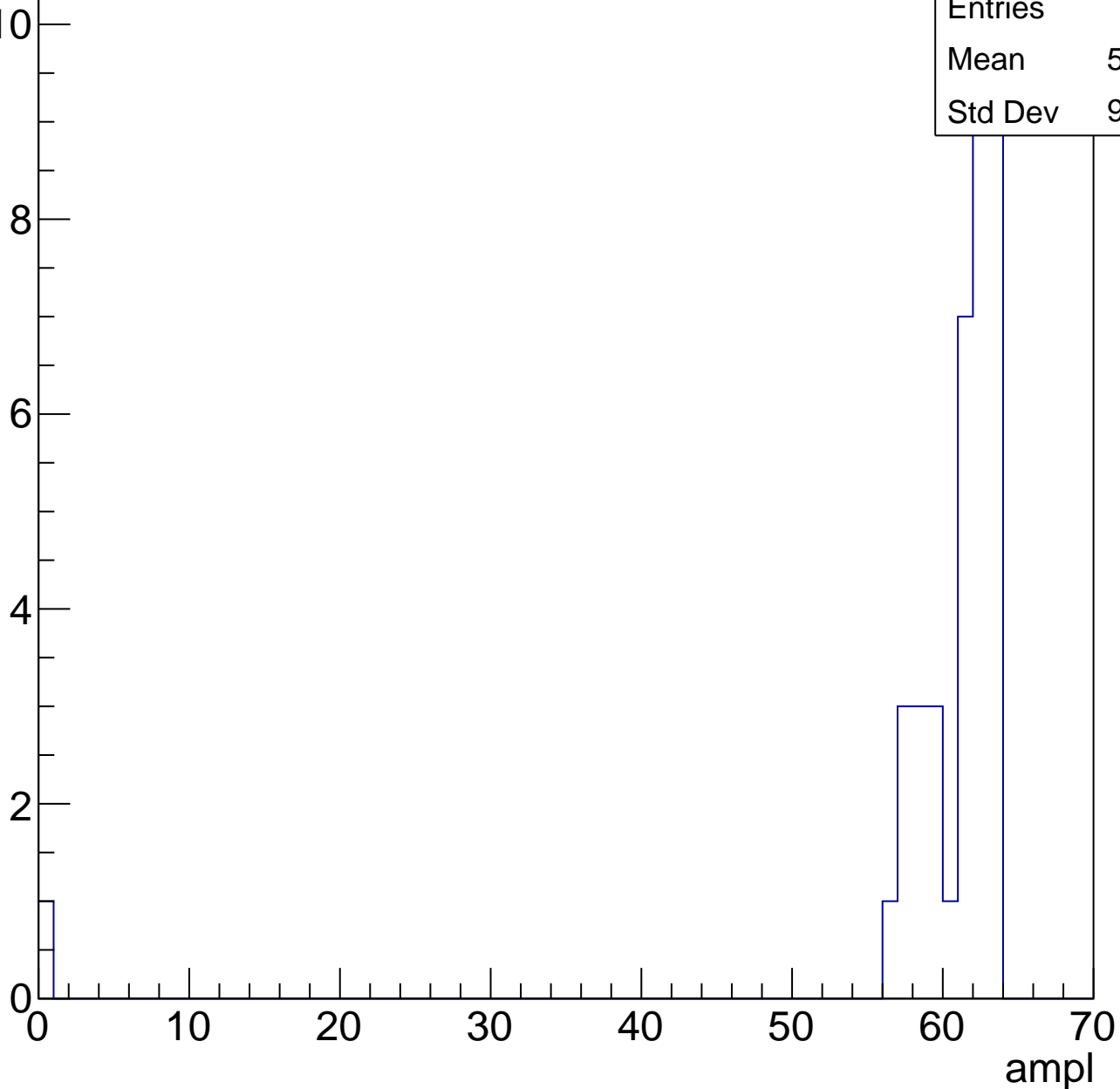


# B0L002S, U23-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.26
Std Dev	9.954



# B0L002S, U23-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L002S, U23-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0