

B0L002S, U17-ch0, adc0

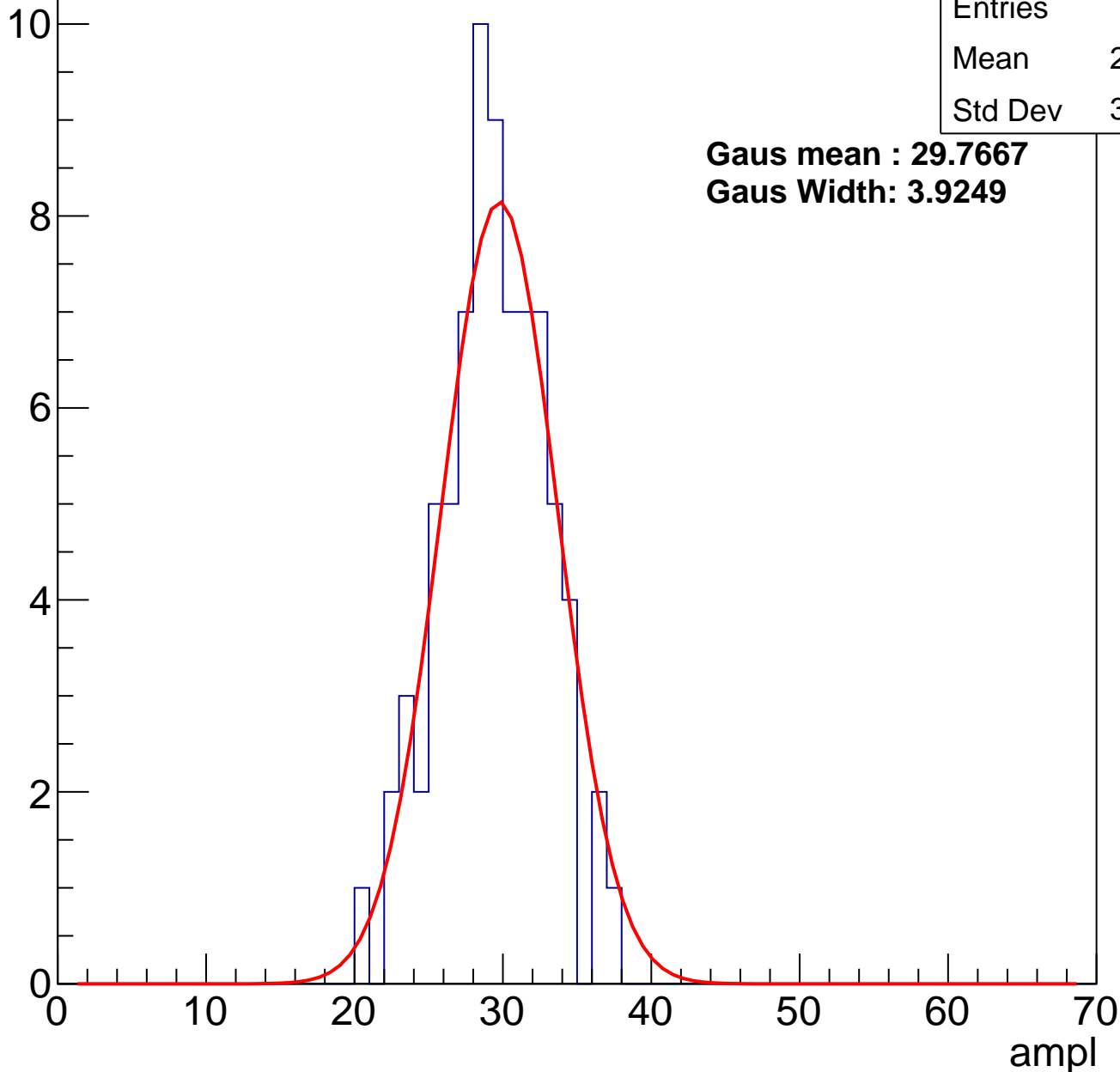
calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	28.92
Std Dev	3.493

Gaus mean : 29.7667

Gaus Width: 3.9249

Entry



B0L002S, U17-ch0, adc1

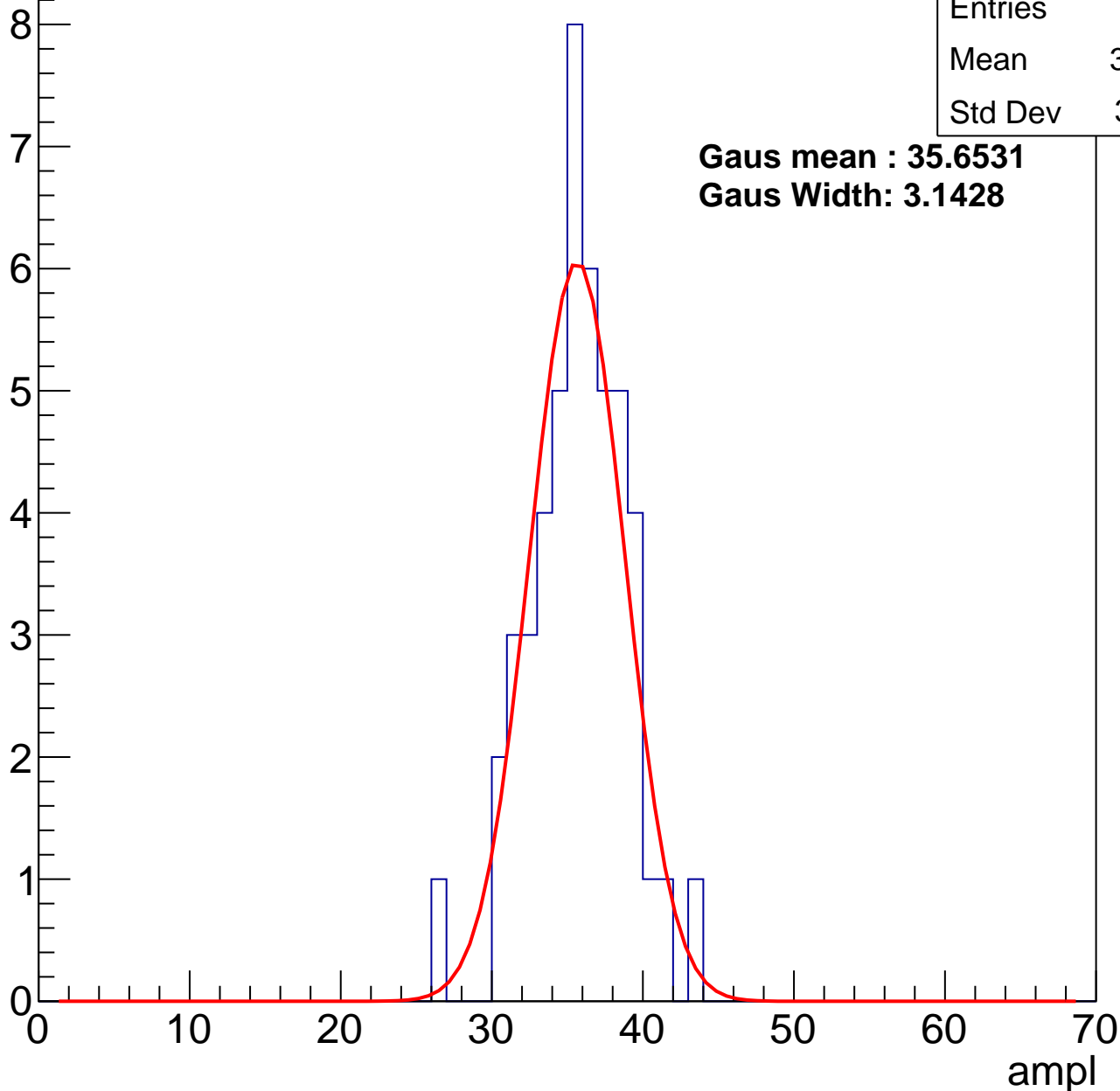
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	35.27
Std Dev	3.141

Gaus mean : 35.6531

Gaus Width: 3.1428



B0L002S, U17-ch0, adc2

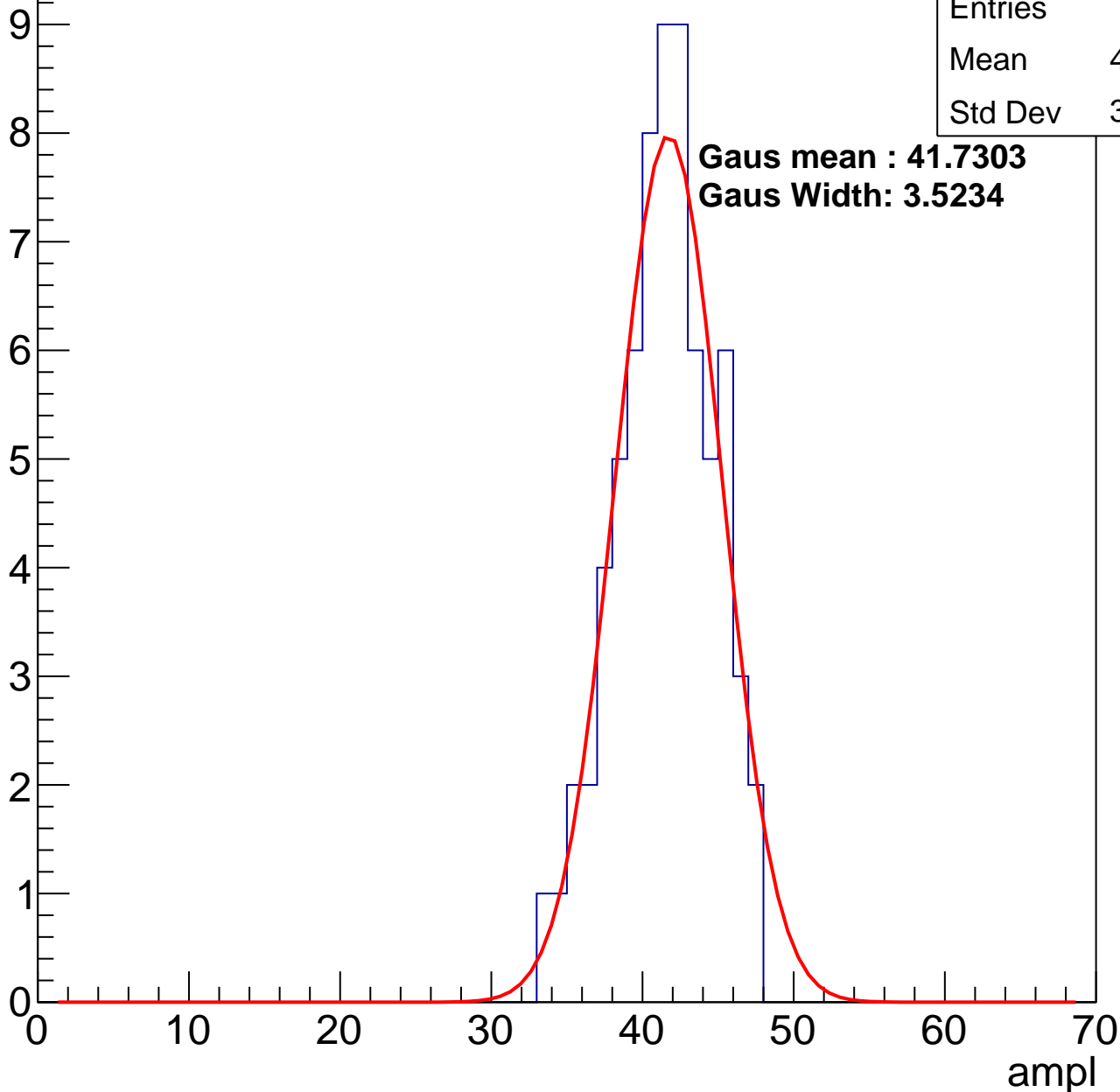
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	40.99
Std Dev	3.178

Gaus mean : 41.7303

Gaus Width: 3.5234

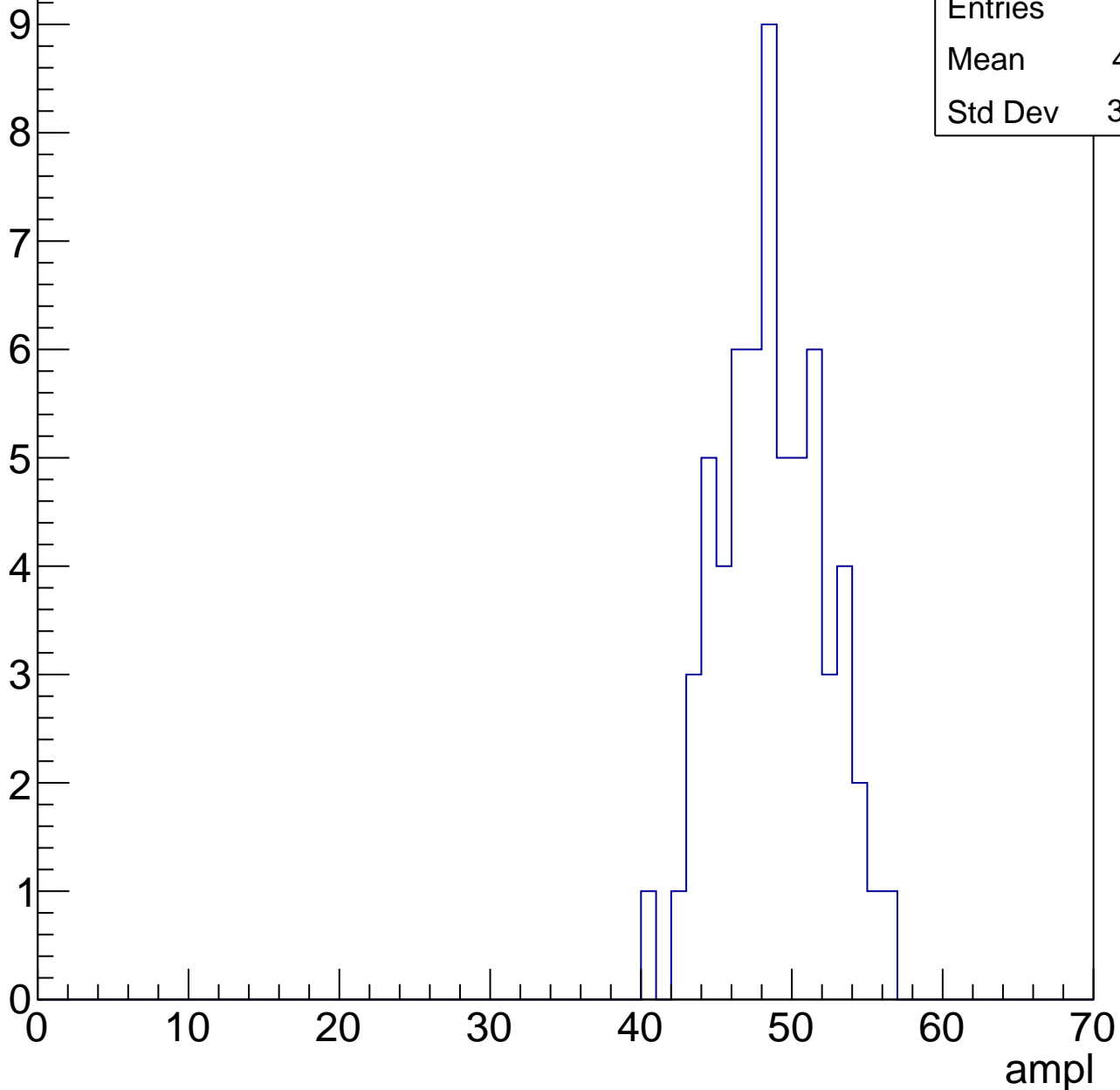


B0L002S, U17-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

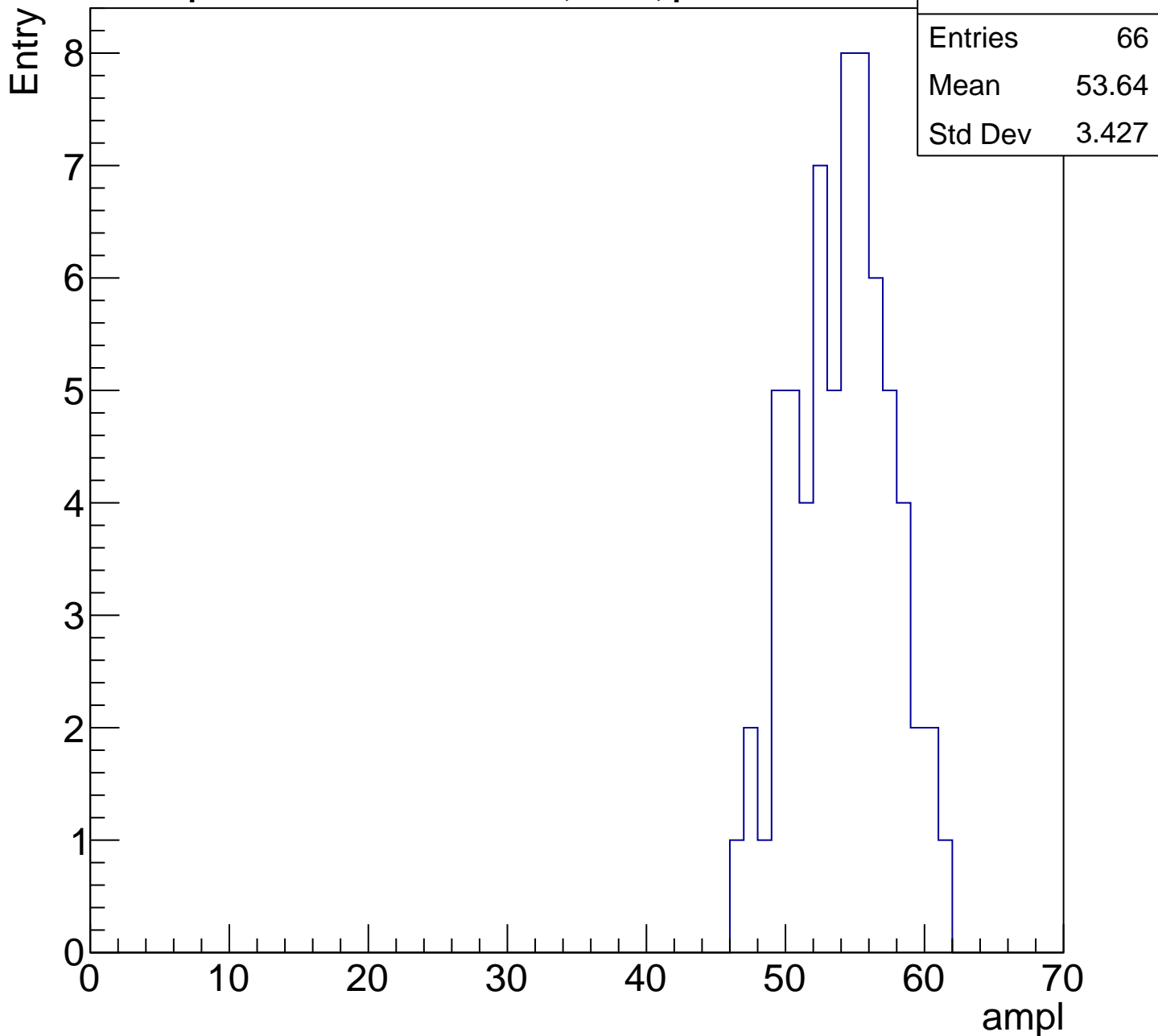
Entry

Entries	62
Mean	48.21
Std Dev	3.432



B0L002S, U17-ch0, adc4

calib_packv5_042523_0143.root, FC#8, port C1

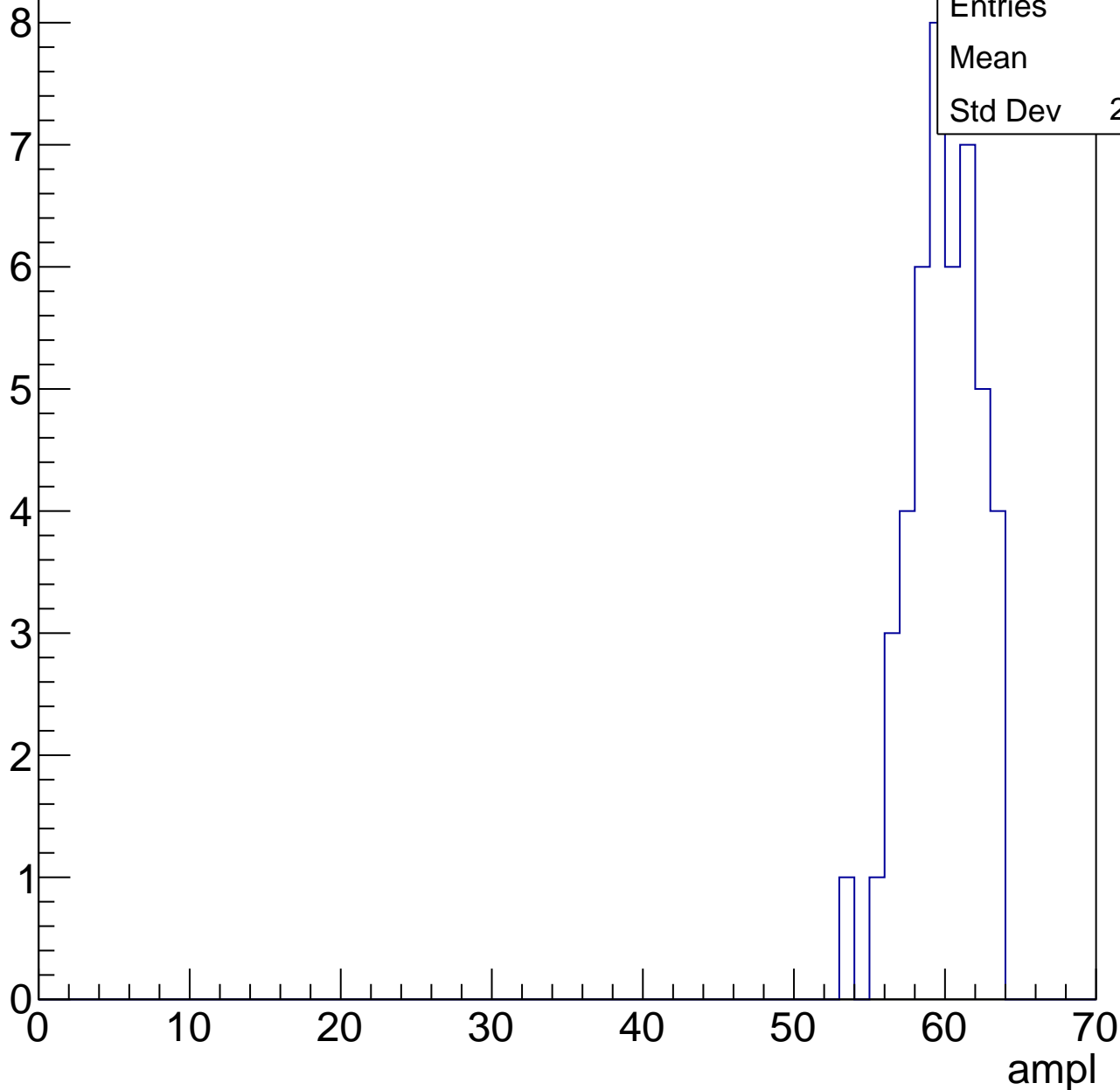


B0L002S, U17-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.4
Std Dev	2.294

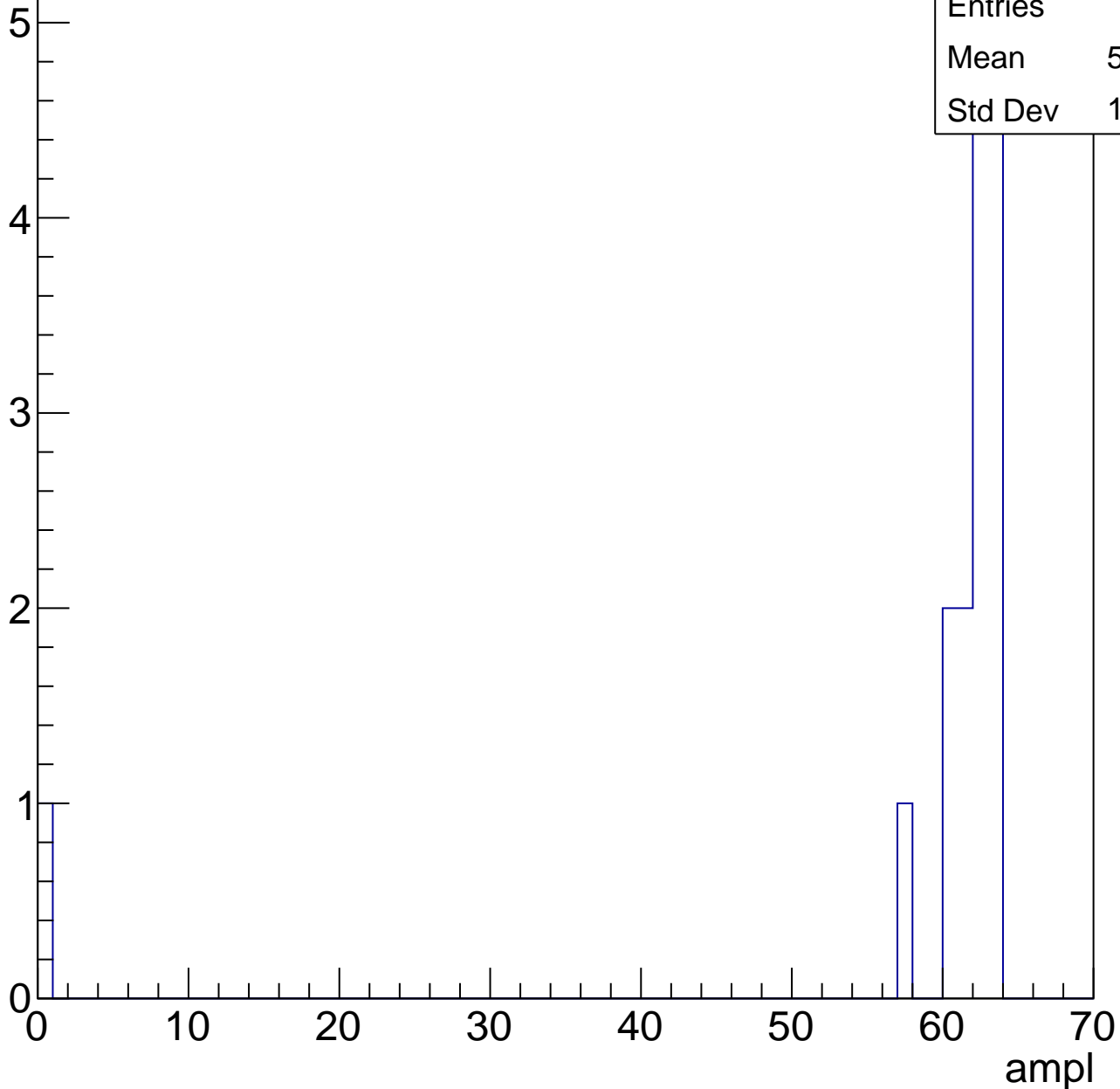


B0L002S, U17-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

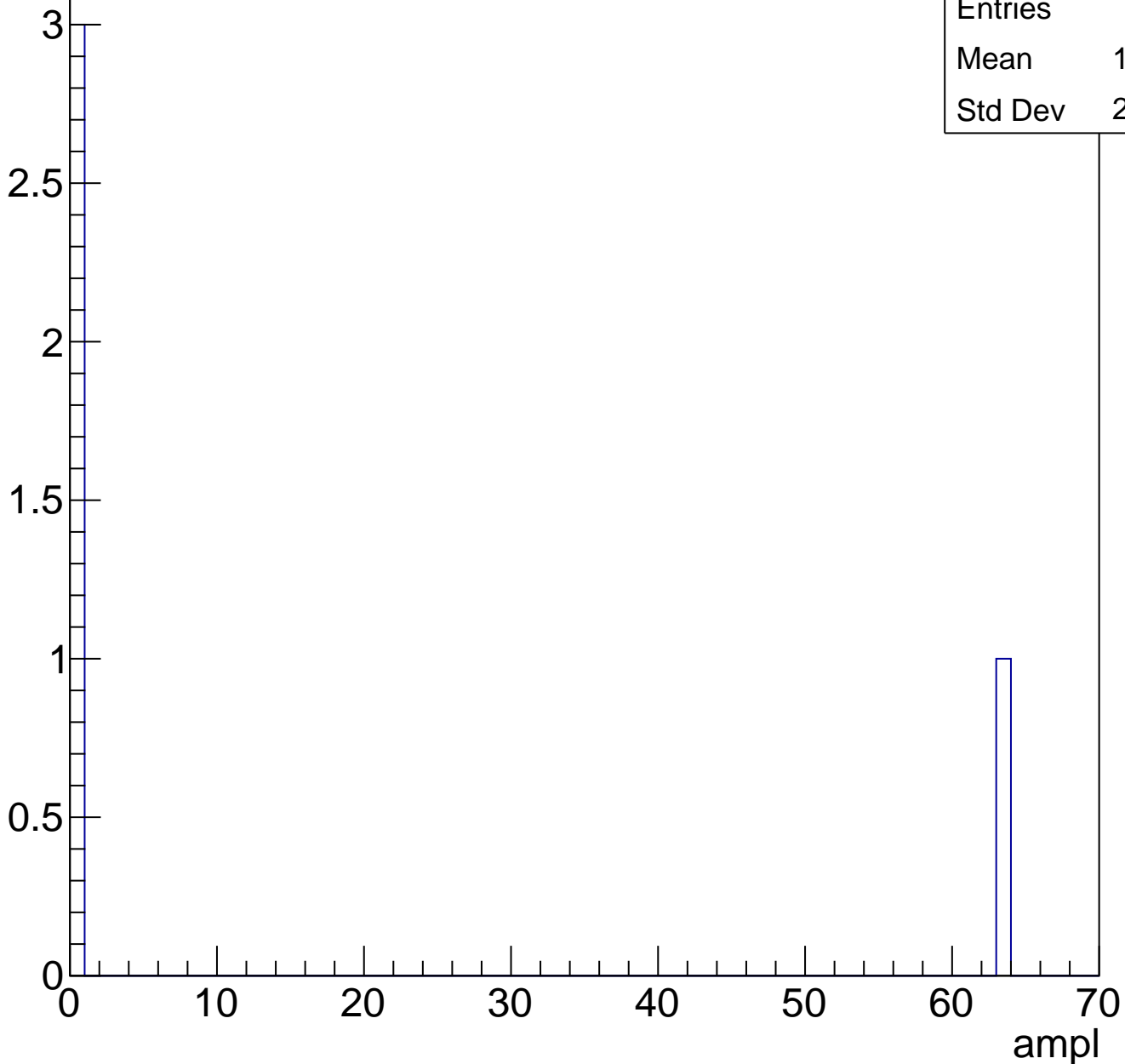
Entries	16
Mean	57.75
Std Dev	14.99



B0L002S, U17-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch1, adc0

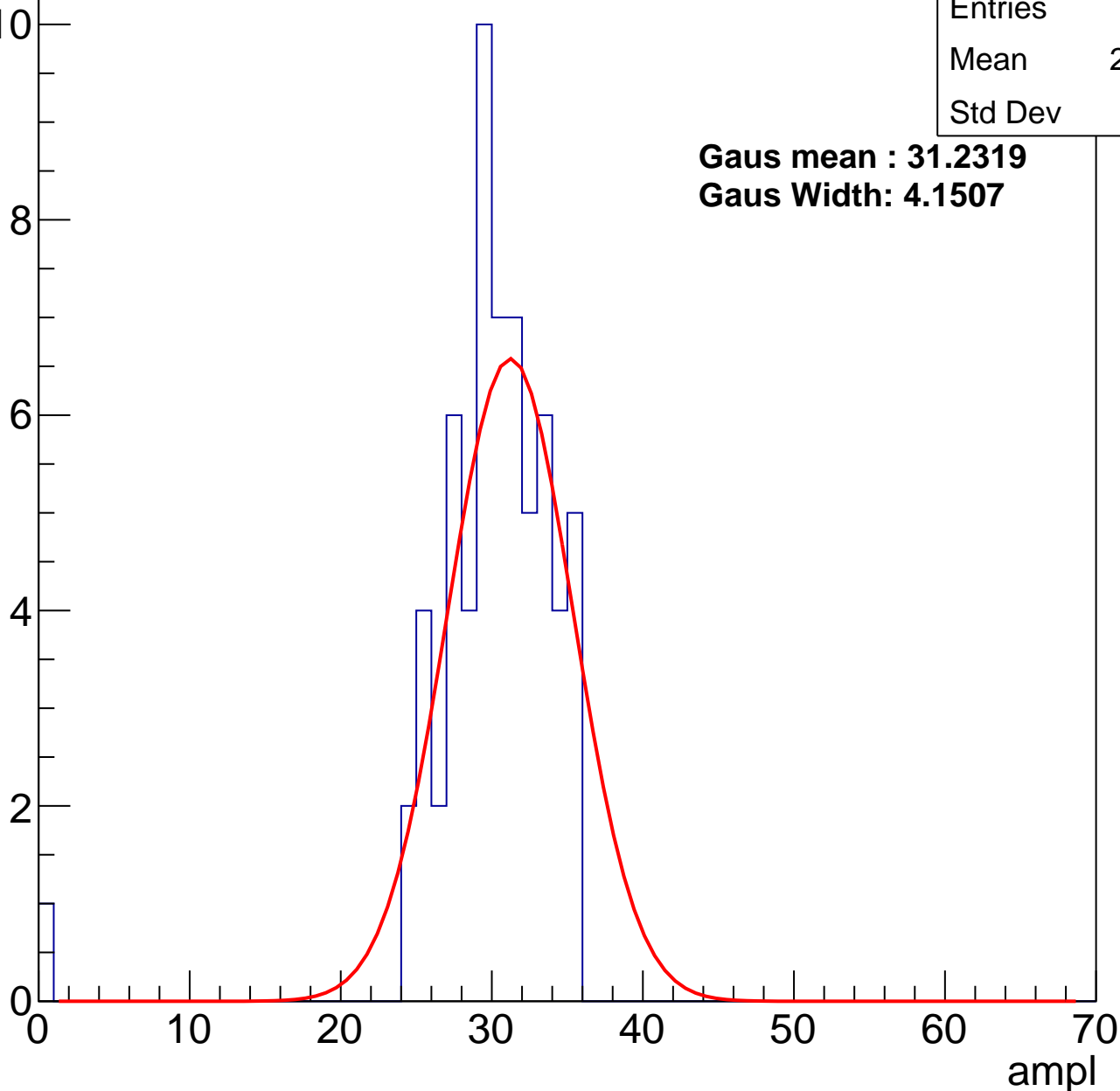
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.52
Std Dev	4.78

Gaus mean : 31.2319

Gaus Width: 4.1507



B0L002S, U17-ch1, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	36.86
Std Dev	3.707

Gaus mean : 36.8699

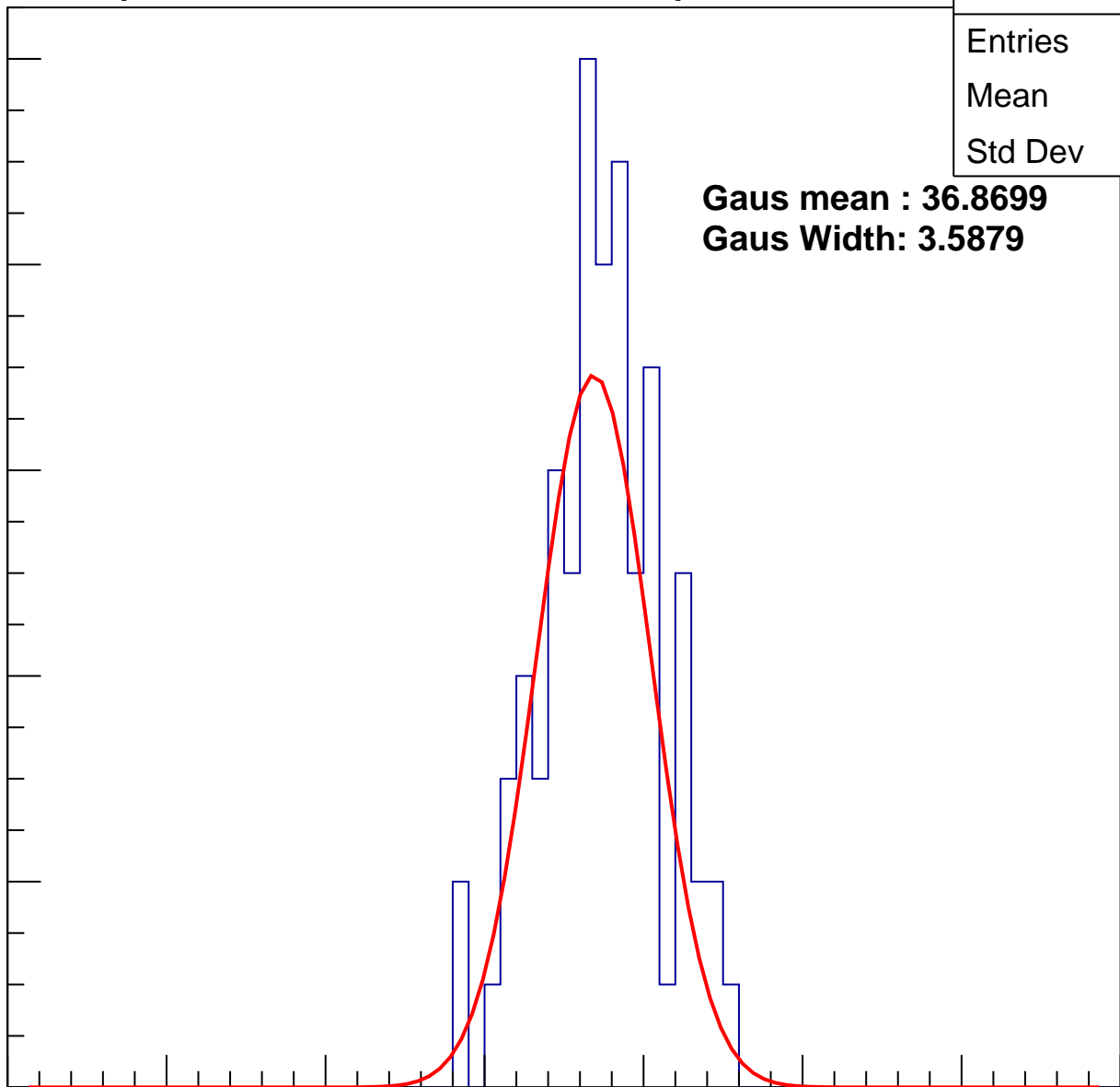
Gaus Width: 3.5879

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U17-ch1, adc2

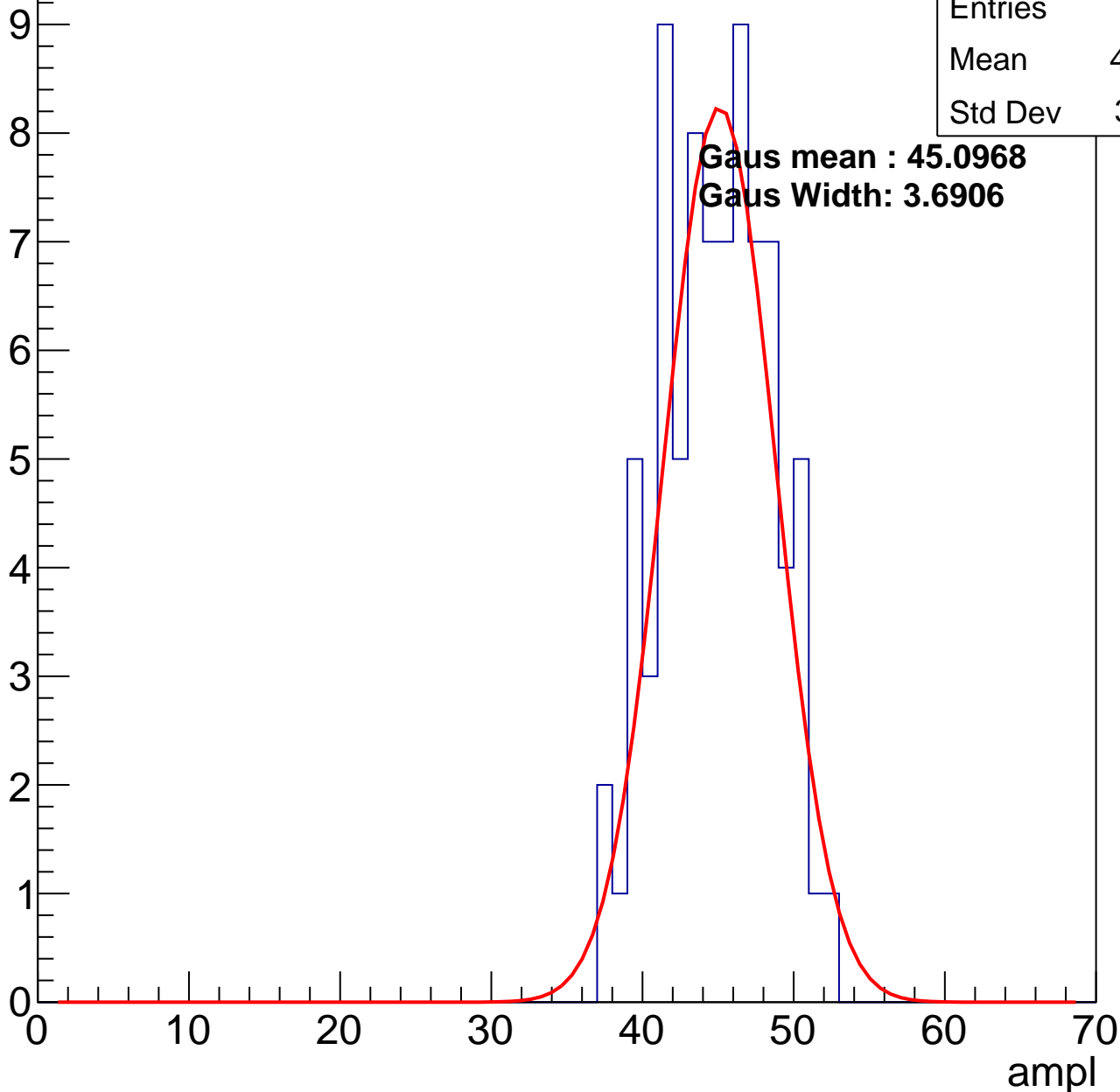
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	44.46
Std Dev	3.531

Gaus mean : 45.0968

Gaus Width: 3.6906

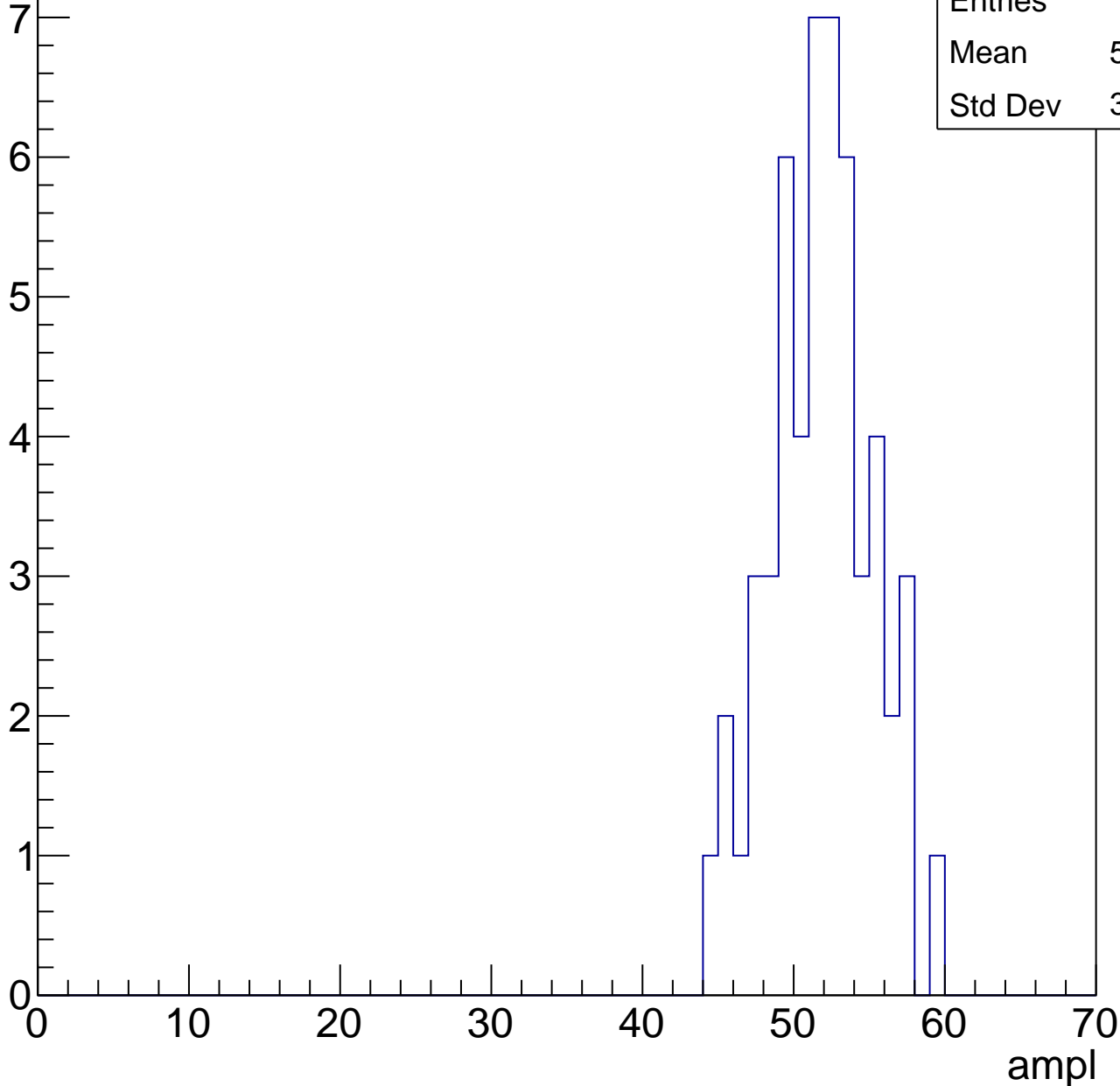


B0L002S, U17-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	51.36
Std Dev	3.326

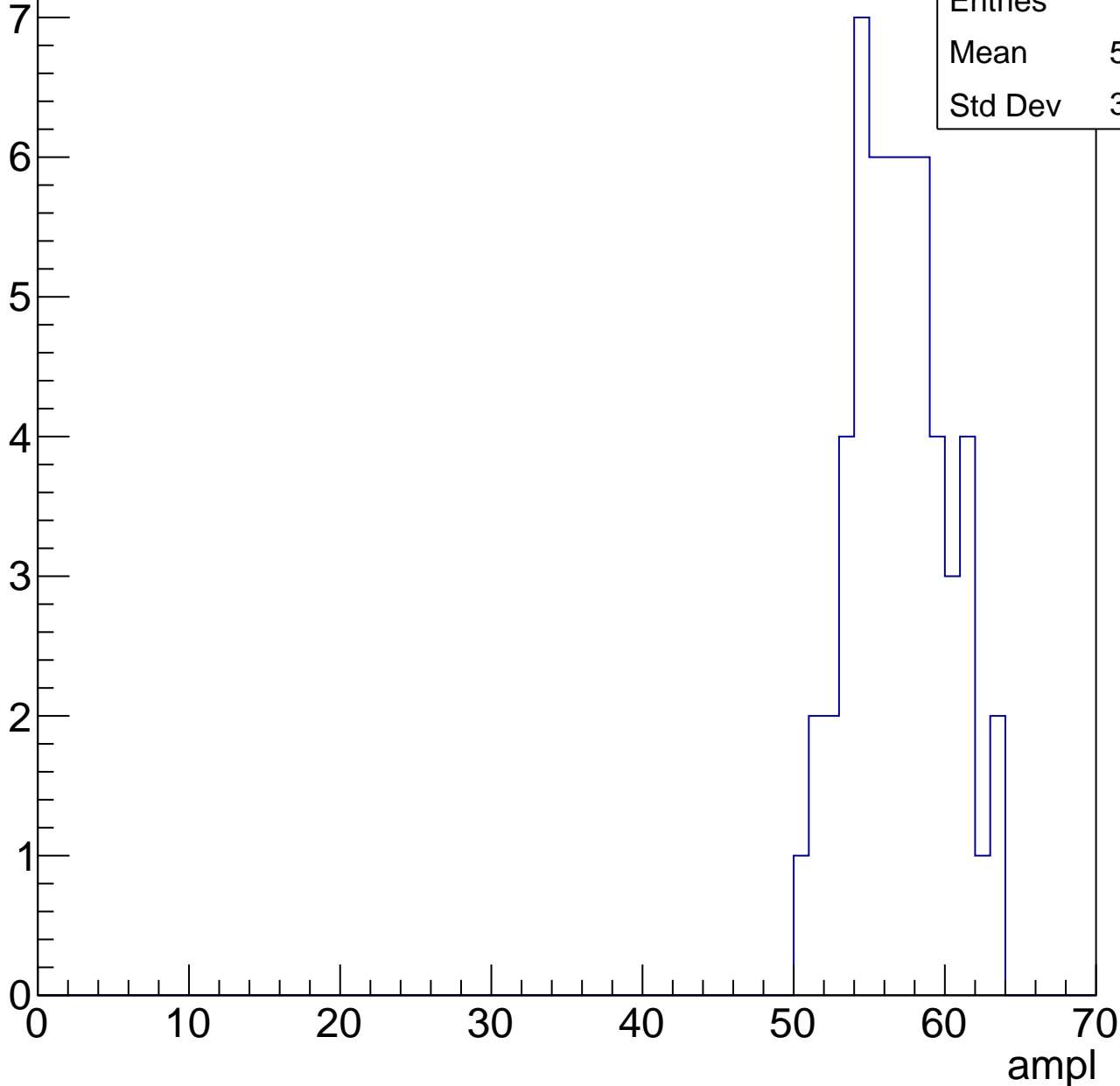


B0L002S, U17-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	56.48
Std Dev	3.114

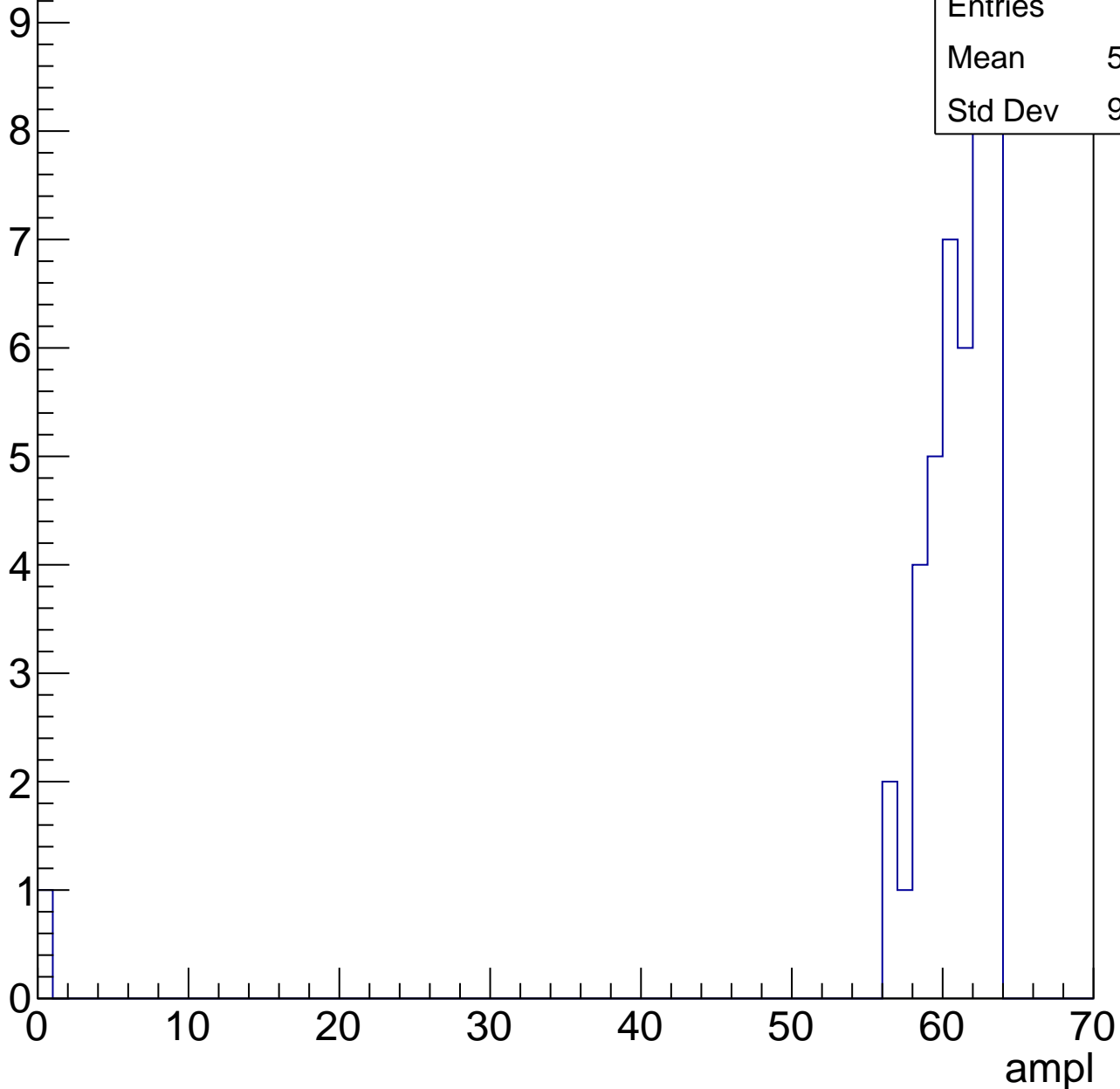


B0L002S, U17-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	59.16
Std Dev	9.333



B0L002S, U17-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

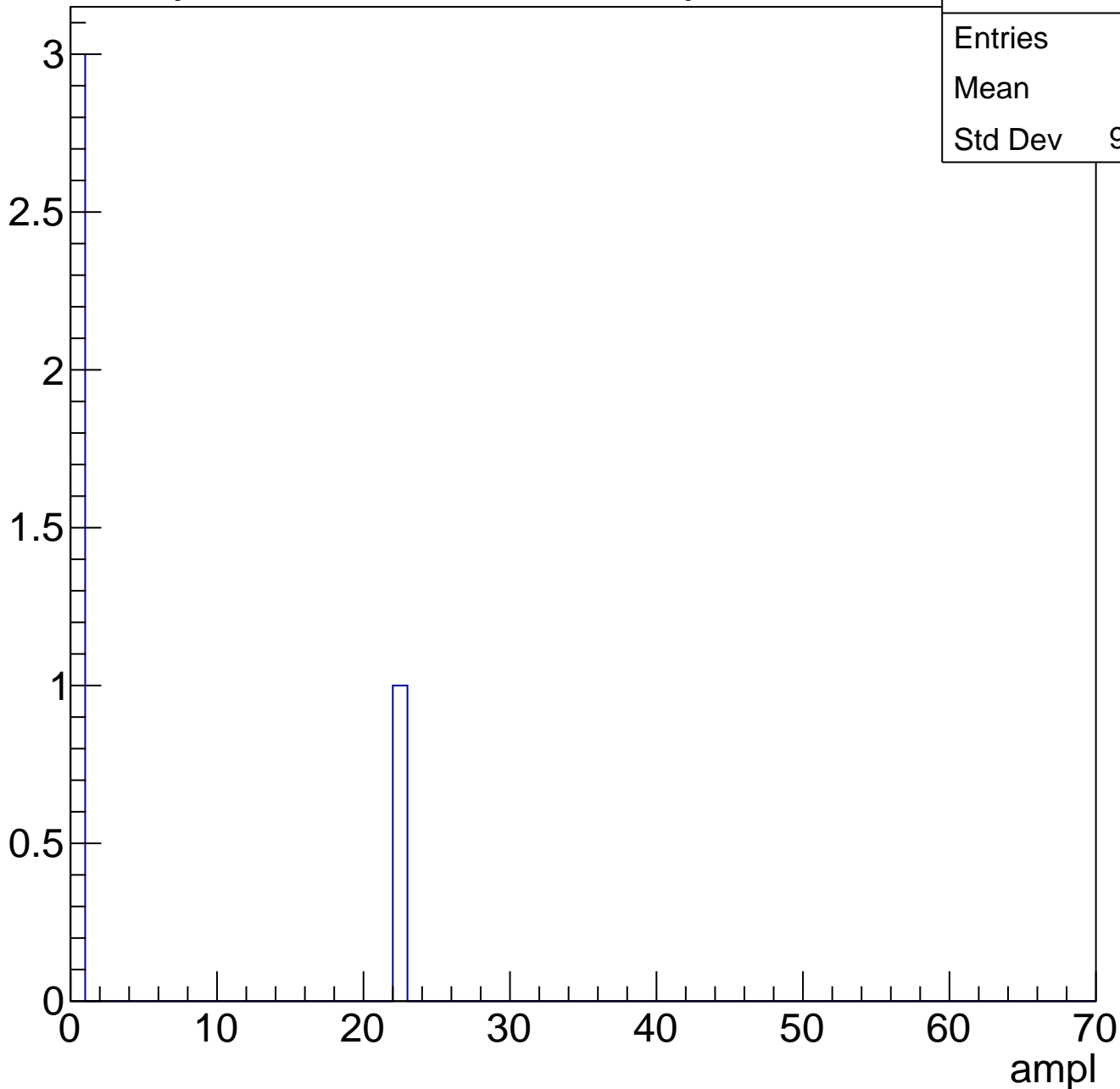


Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	5.5
Std Dev	9.526

B0L002S, U17-ch2, adc0

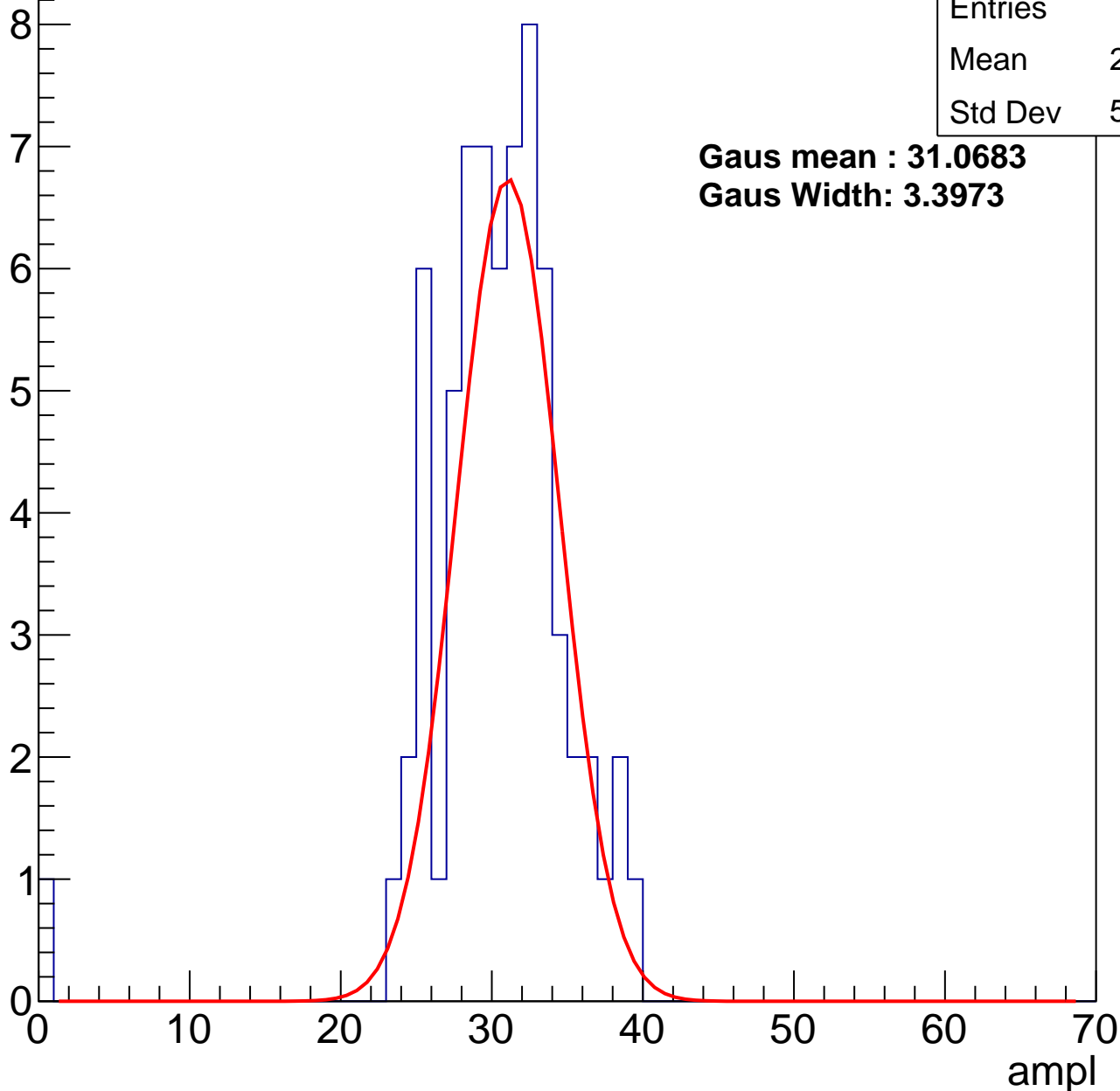
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.82
Std Dev	5.125

Gaus mean : 31.0683

Gaus Width: 3.3973



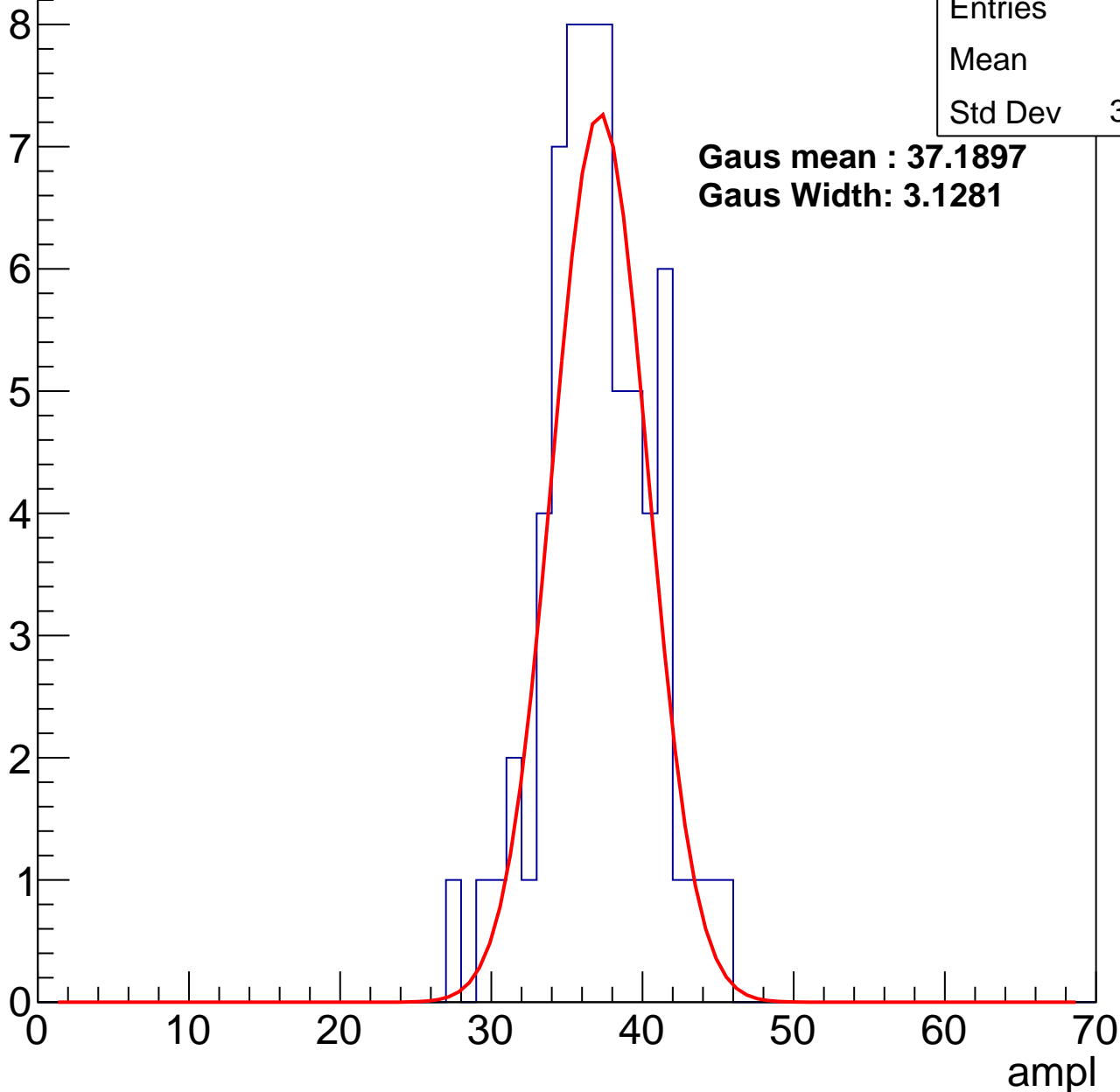
B0L002S, U17-ch2, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.6
Std Dev	3.503

Gaus mean : 37.1897
Gaus Width: 3.1281



B0L002S, U17-ch2, adc2

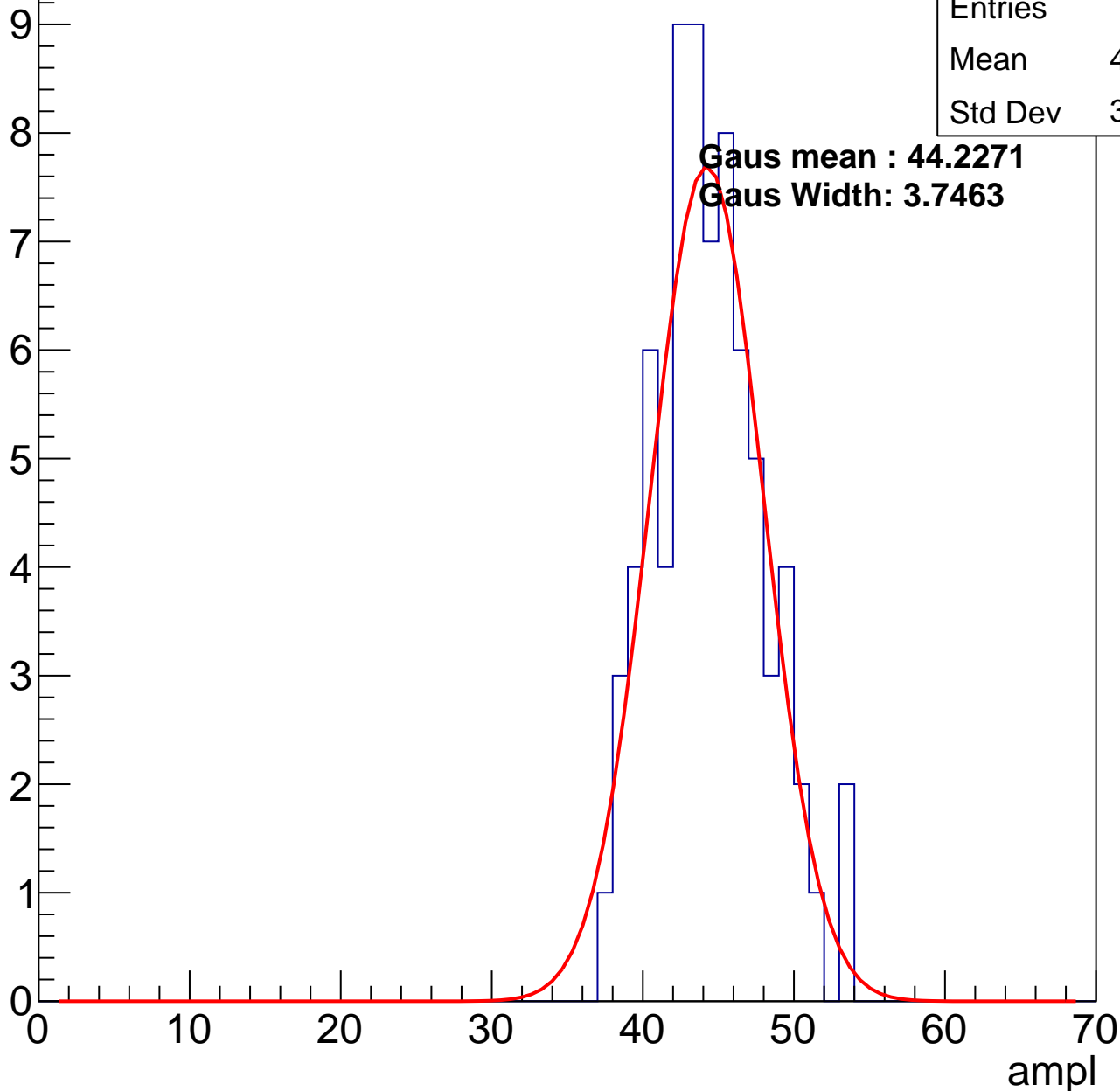
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.95
Std Dev	3.567

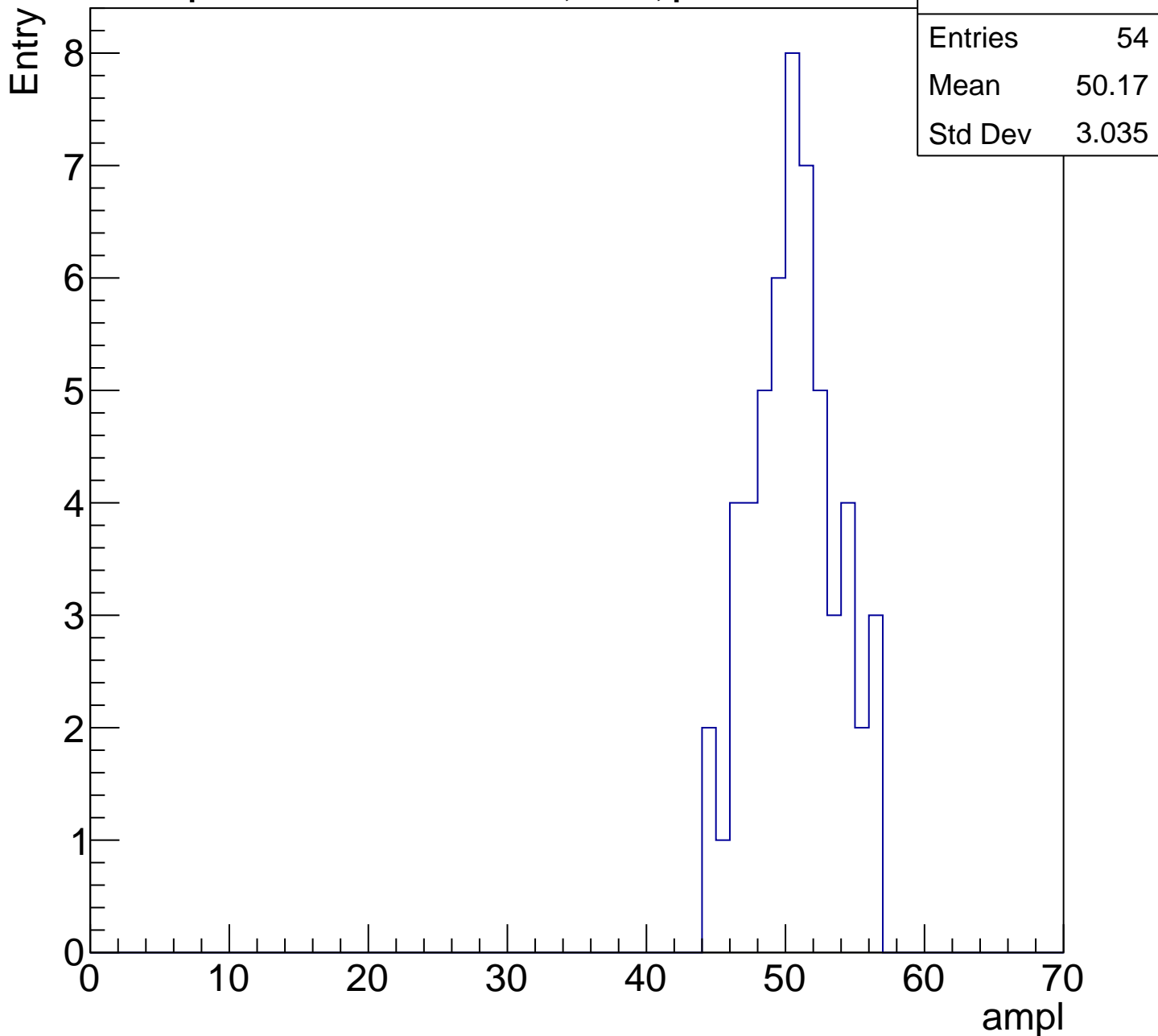
Gaus mean : 44.2271

Gaus Width: 3.7463



B0L002S, U17-ch2, adc3

calib_packv5_042523_0143.root, FC#8, port C1

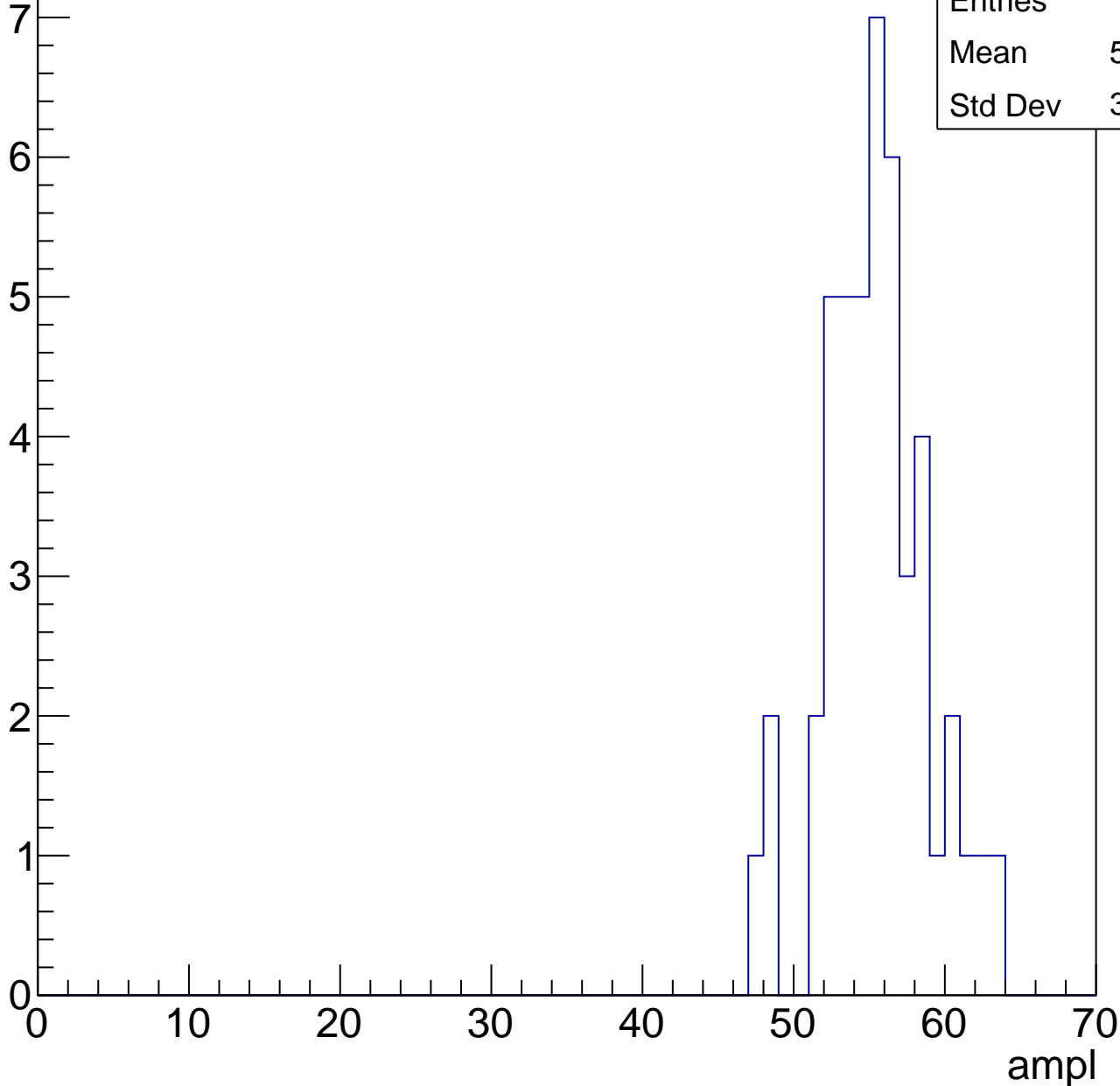


B0L002S, U17-ch2, adc4

calib_packv5_042523_0143.root, FC#8, port C1

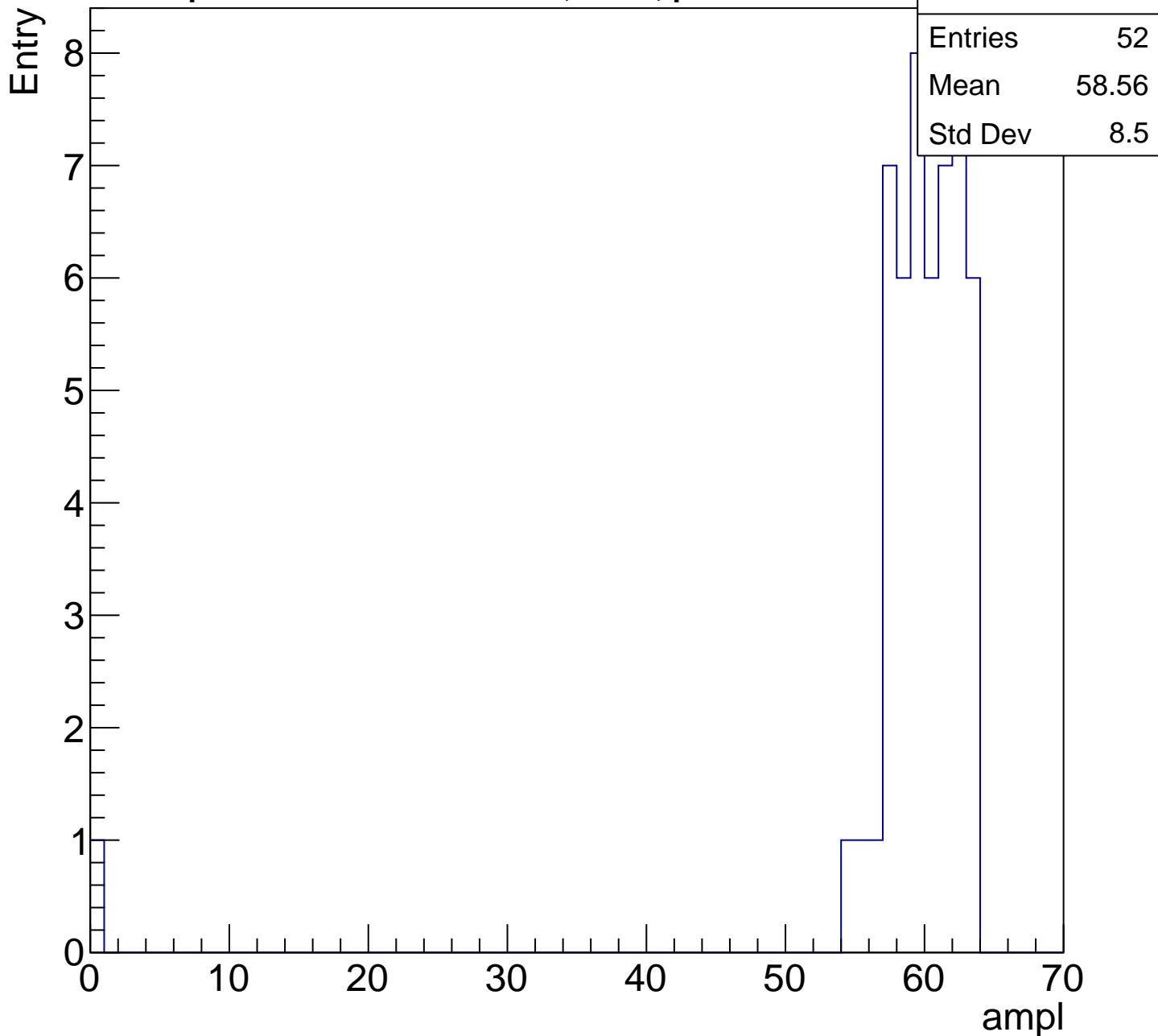
Entry

Entries	46
Mean	54.98
Std Dev	3.404



B0L002S, U17-ch2, adc5

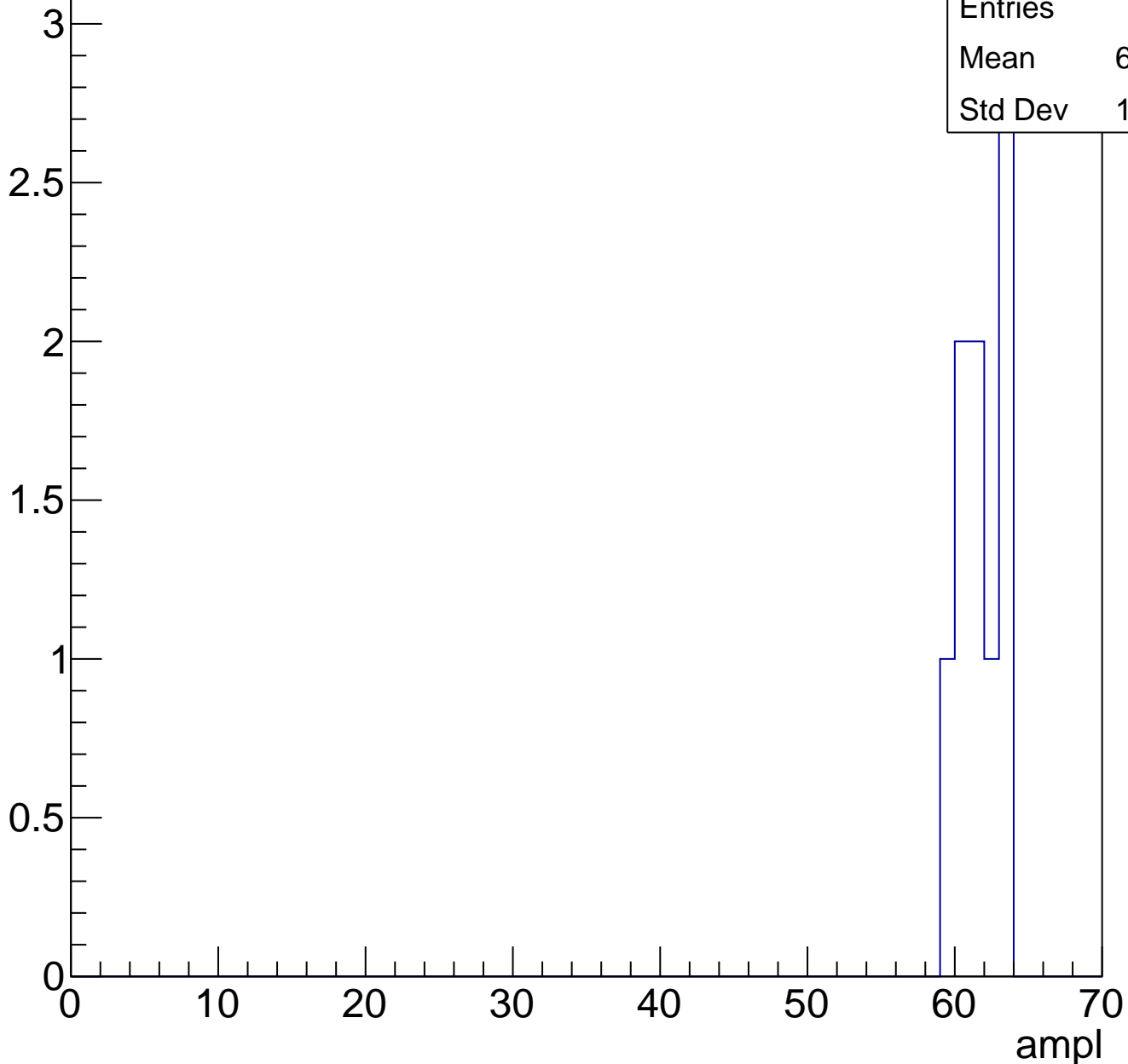
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U17-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch3, adc0

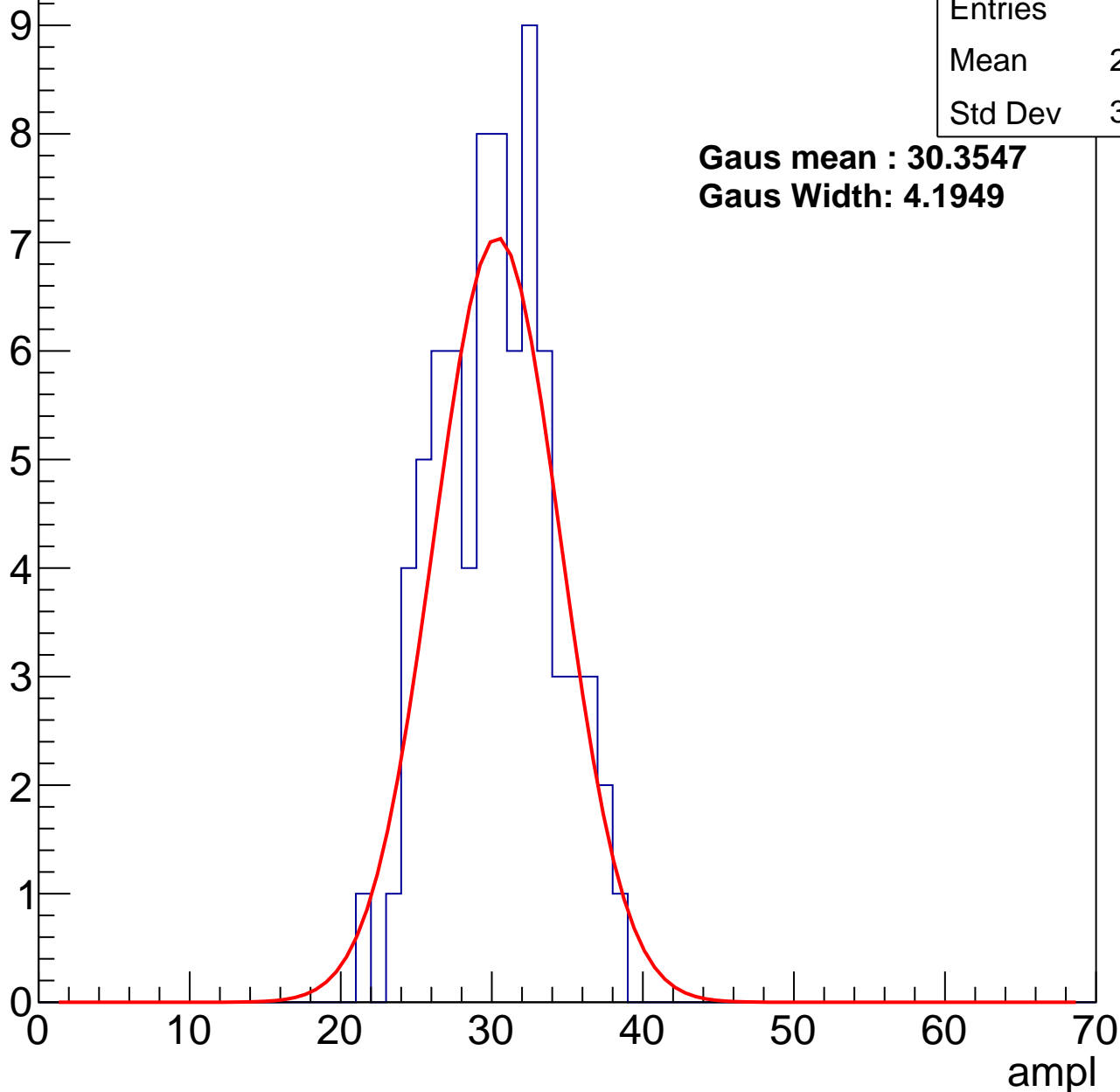
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	29.82
Std Dev	3.734

Gaus mean : 30.3547

Gaus Width: 4.1949



B0L002S, U17-ch3, adc1

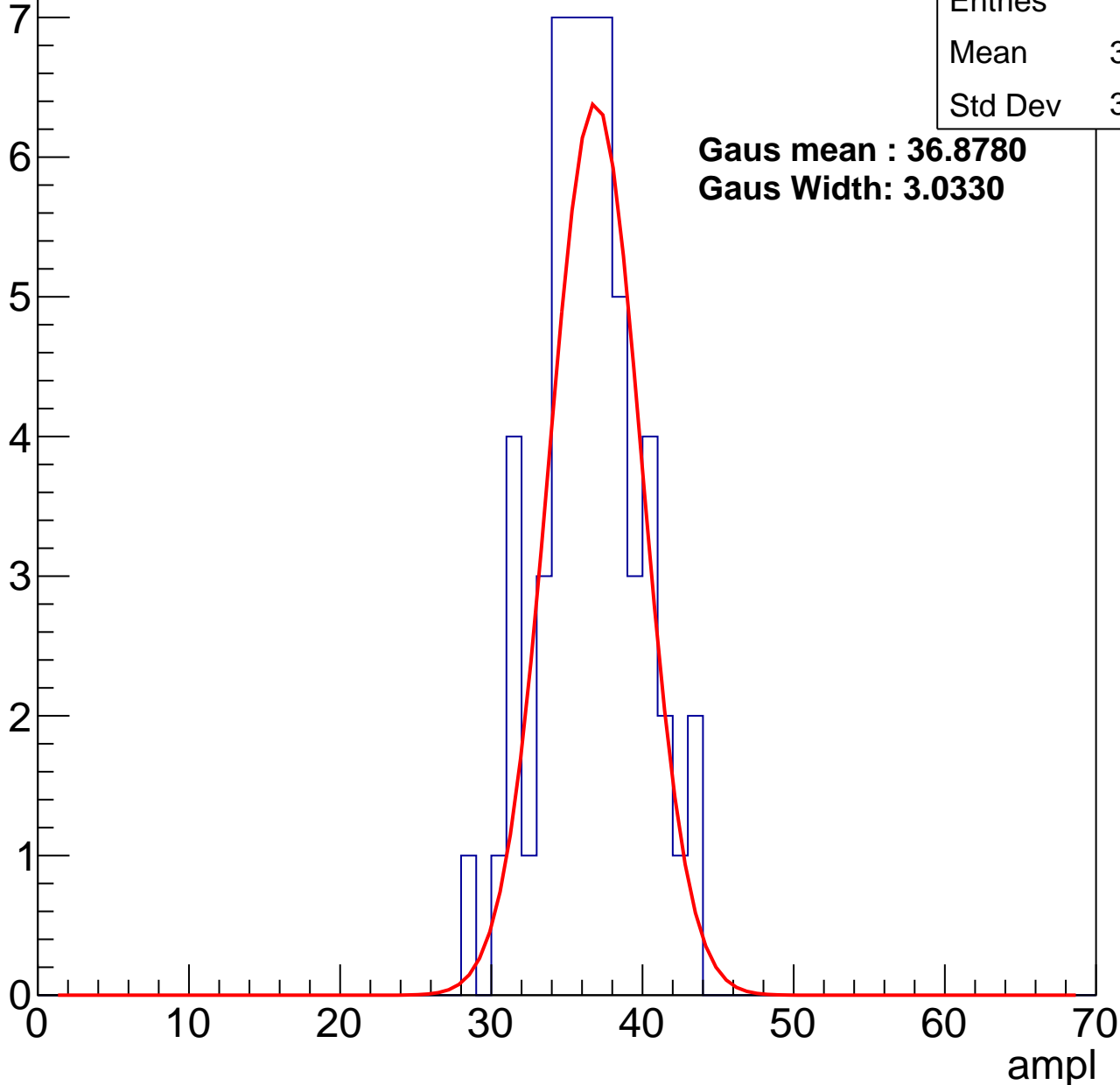
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.07
Std Dev	3.247

Gaus mean : 36.8780

Gaus Width: 3.0330



B0L002S, U17-ch3, adc2

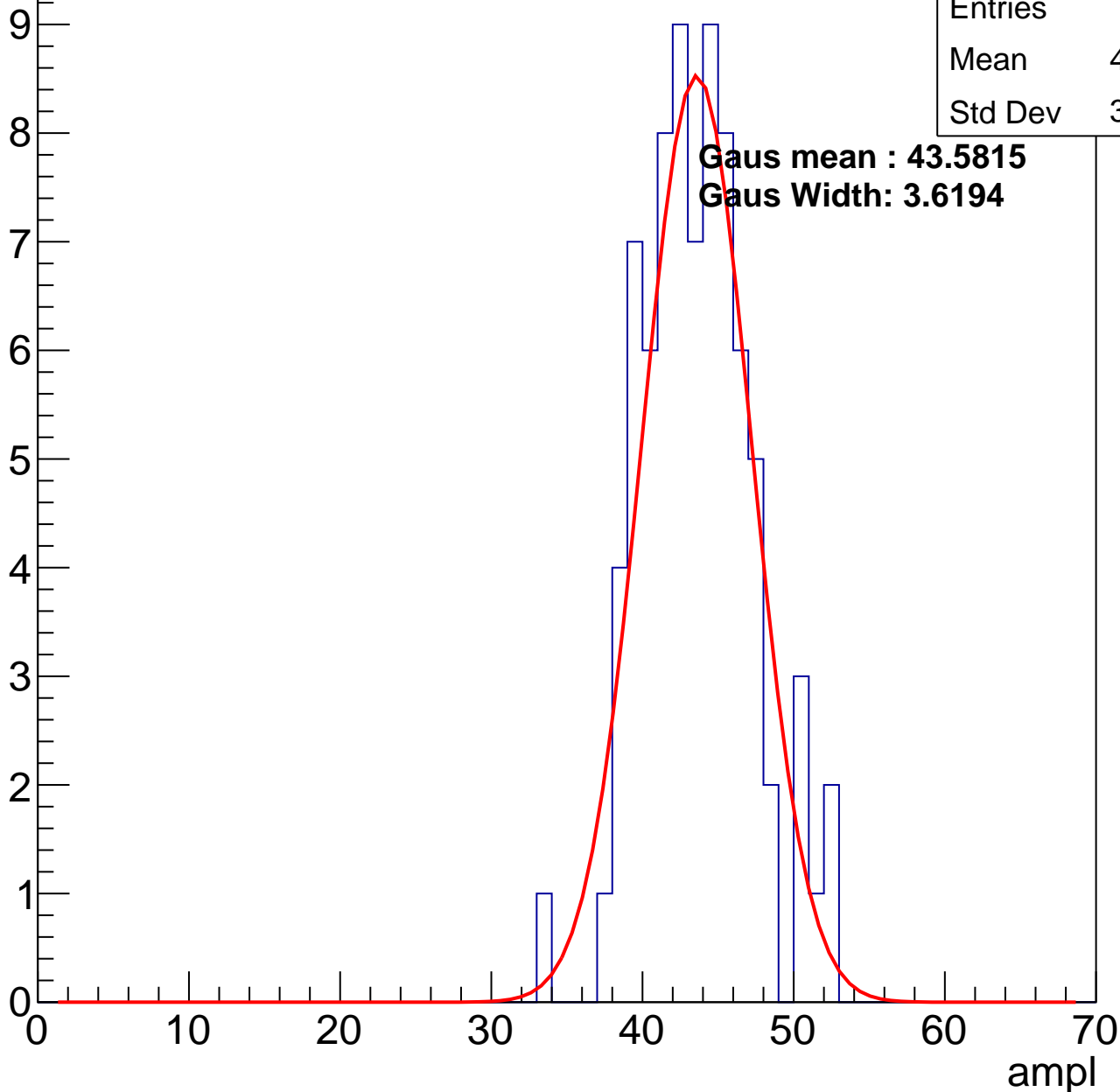
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	43.16
Std Dev	3.626

Gaus mean : 43.5815

Gaus Width: 3.6194

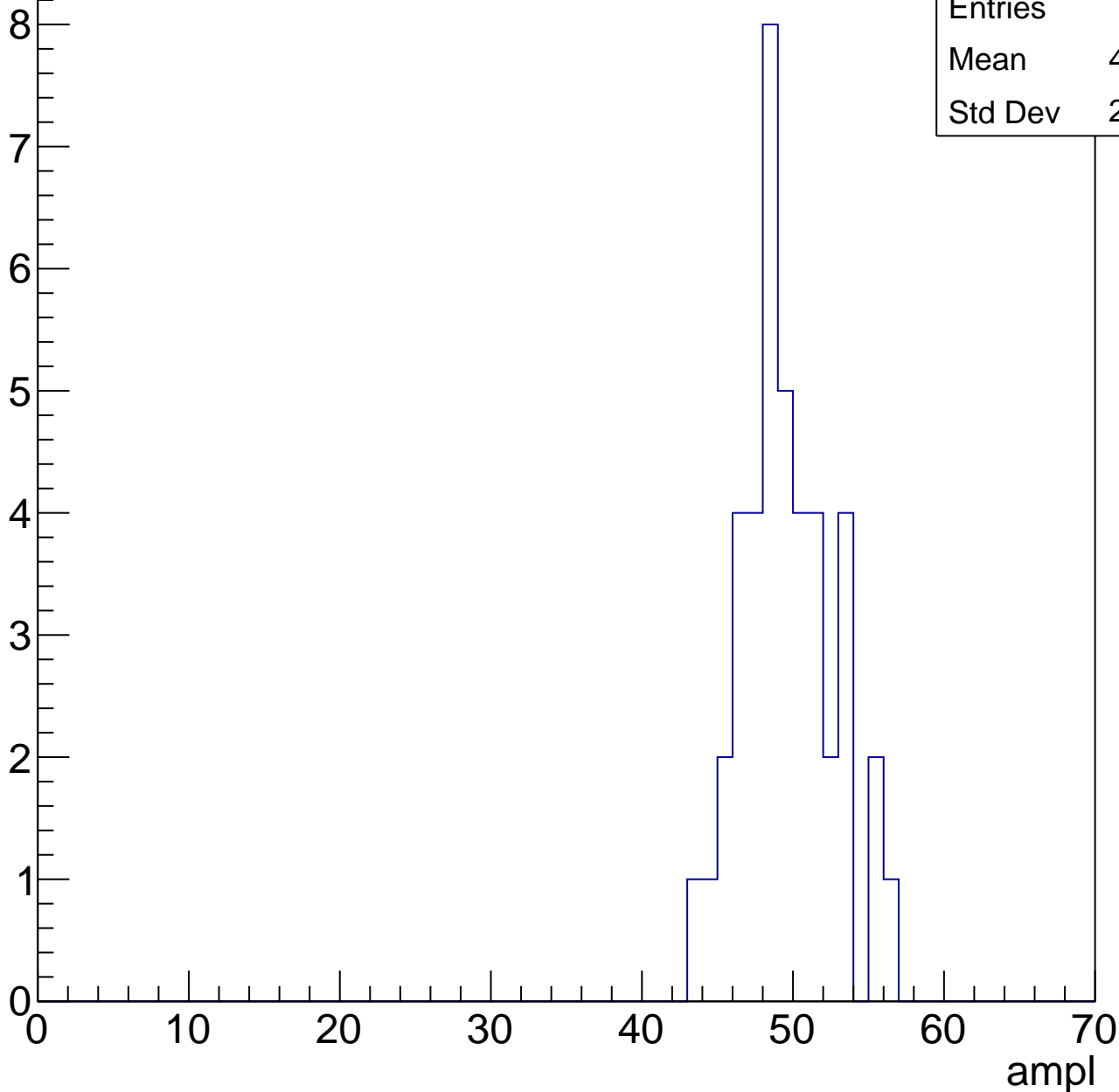


B0L002S, U17-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

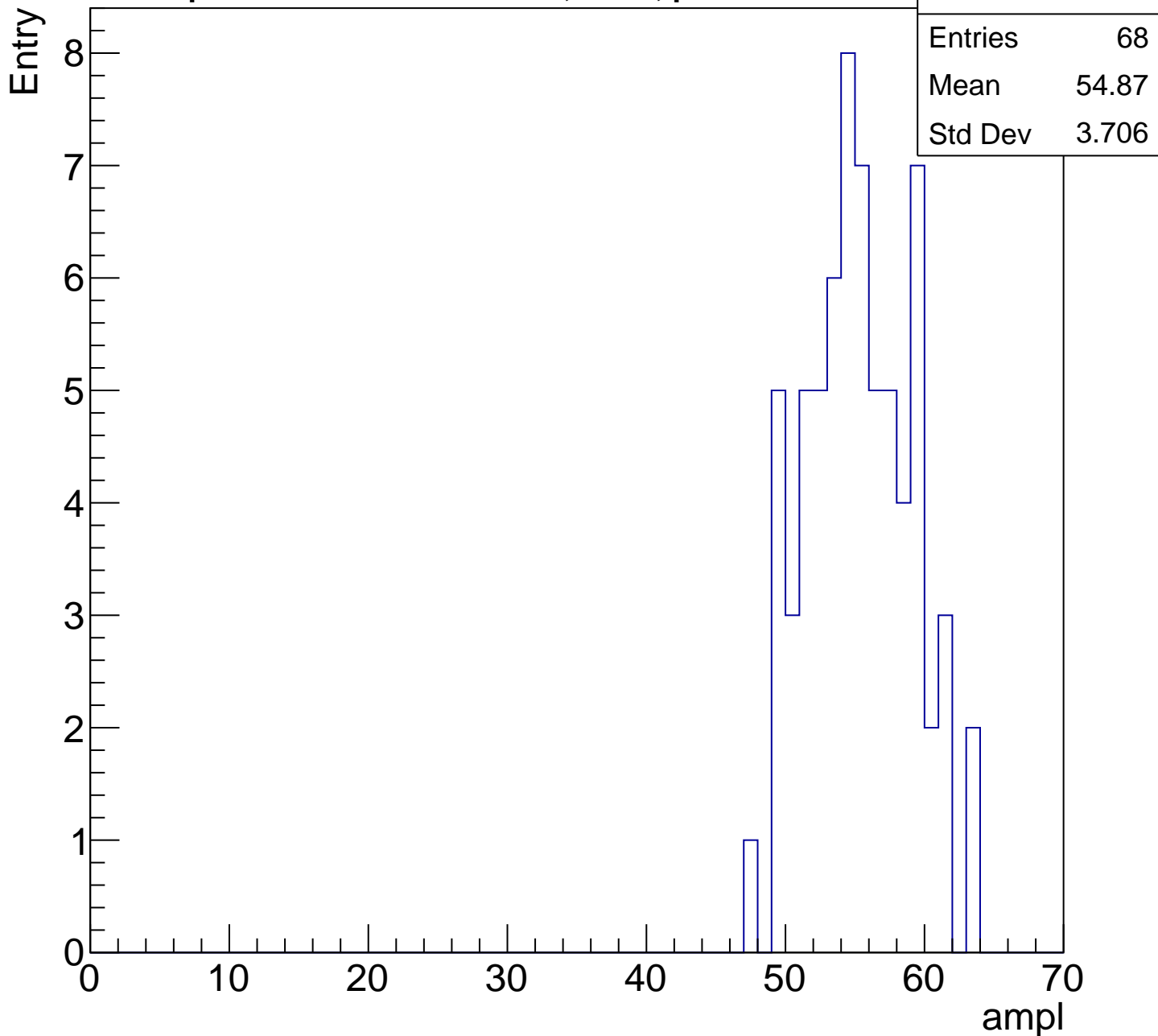
Entry

Entries	42
Mean	49.14
Std Dev	2.989



B0L002S, U17-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

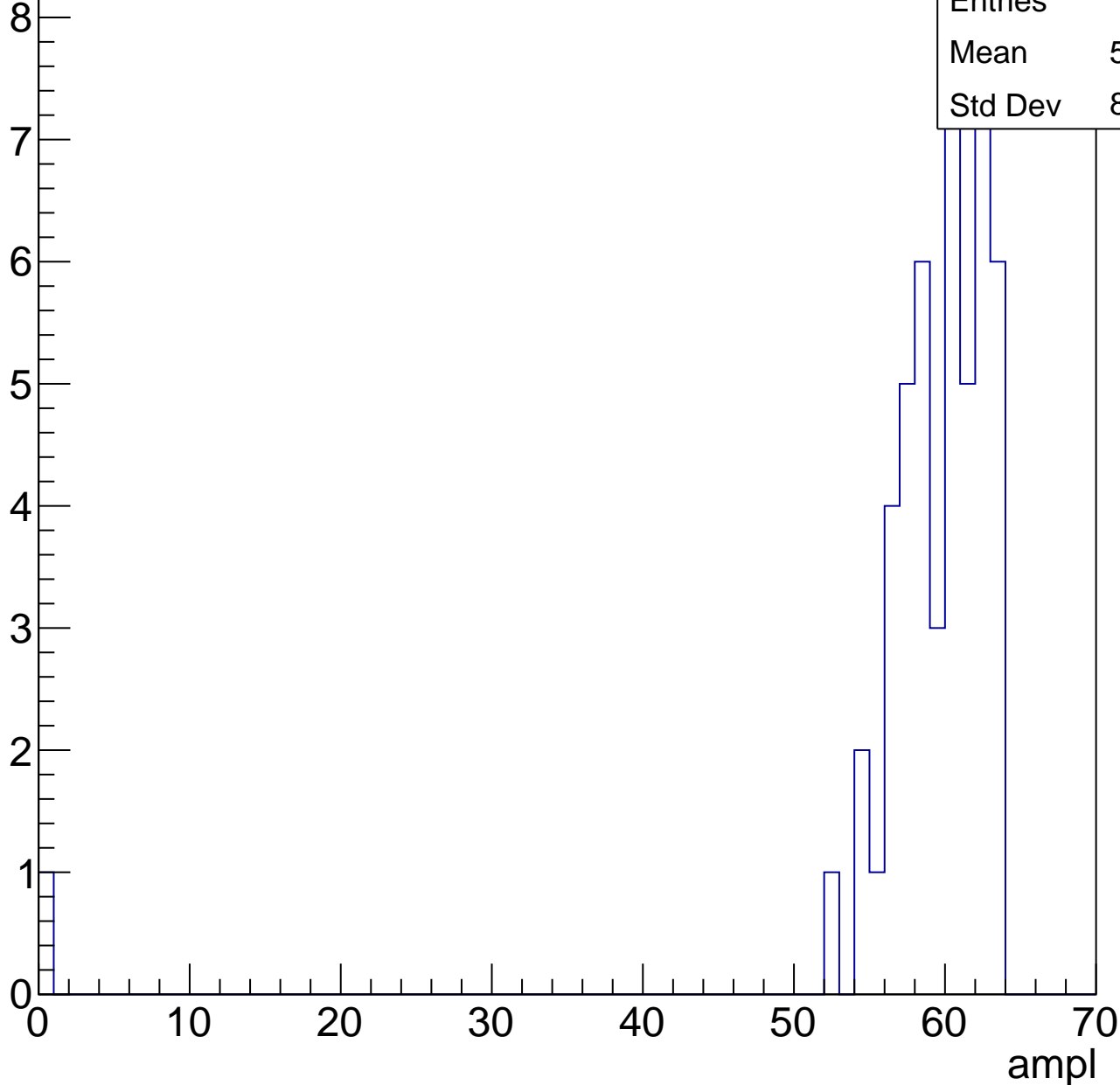


B0L002S, U17-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	58.16
Std Dev	8.739



B0L002S, U17-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	62
Std Dev	0.8165

0 10 20 30 40 50 60 70

ampl

B0L002S, U17-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch4, adc0

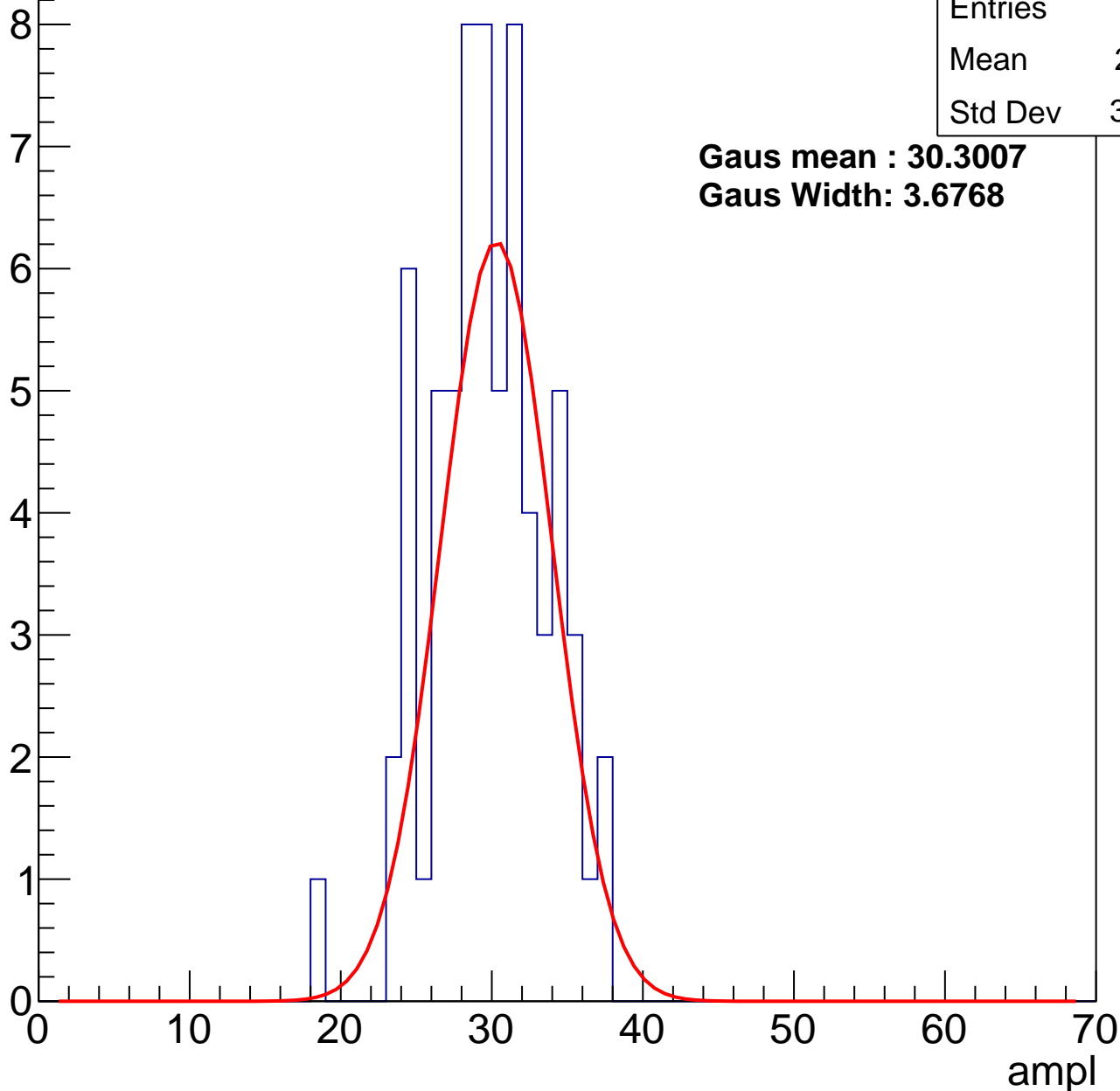
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.31
Std Dev	3.778

Gaus mean : 30.3007

Gaus Width: 3.6768



B0L002S, U17-ch4, adc1

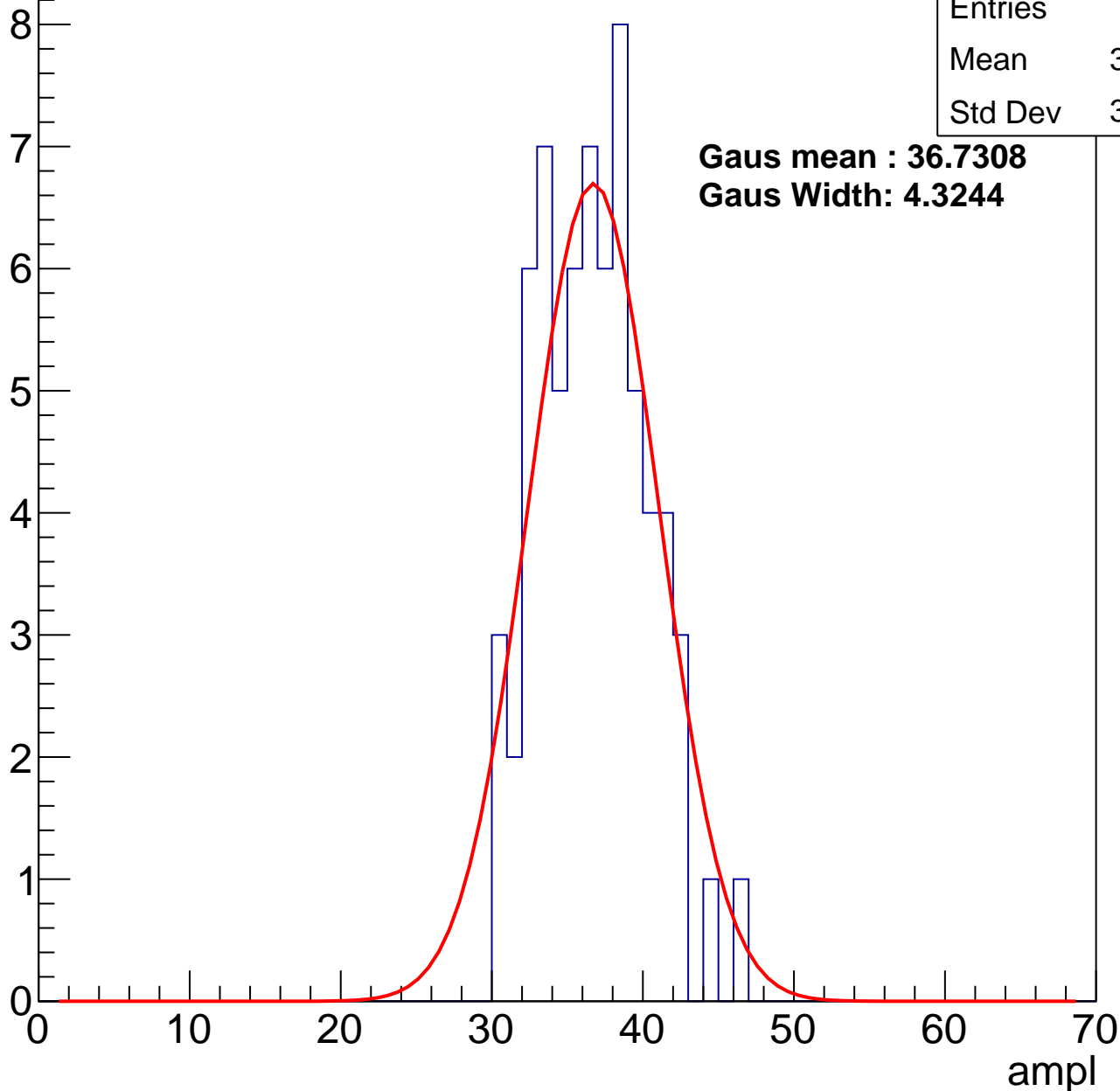
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	36.29
Std Dev	3.548

Gaus mean : 36.7308

Gaus Width: 4.3244



B0L002S, U17-ch4, adc2

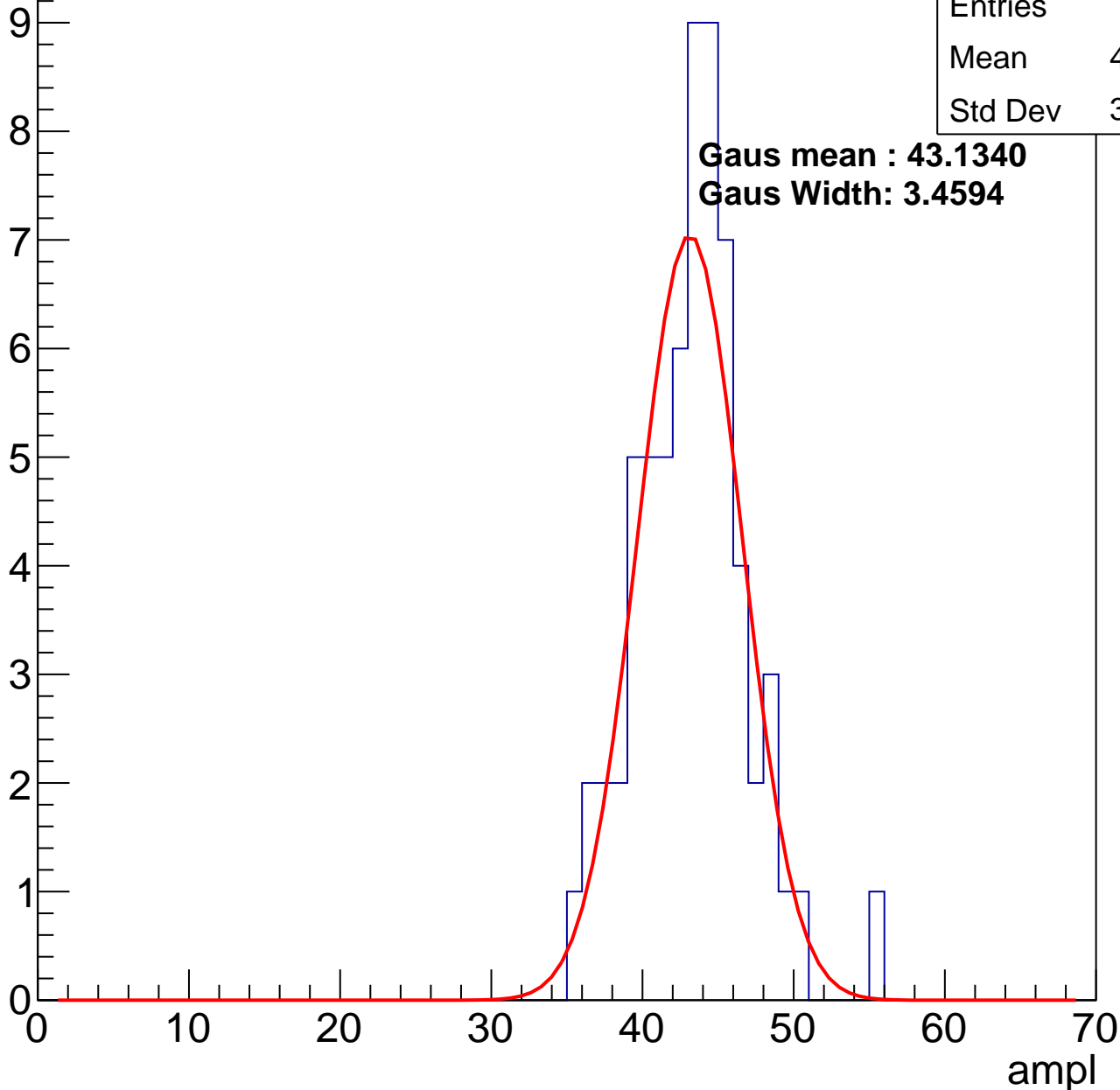
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	42.82
Std Dev	3.599

Gaus mean : 43.1340

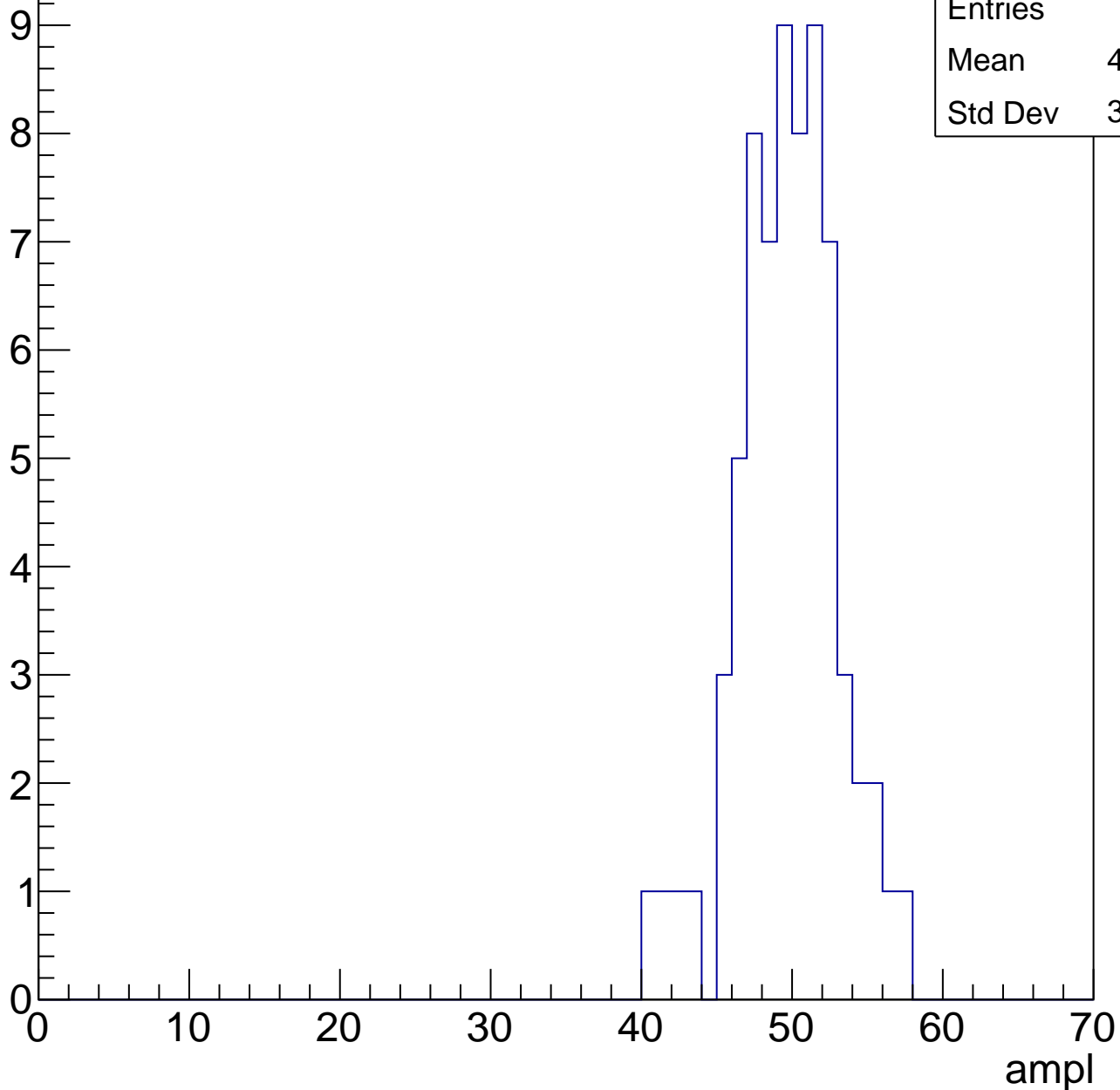
Gaus Width: 3.4594



B0L002S, U17-ch4, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



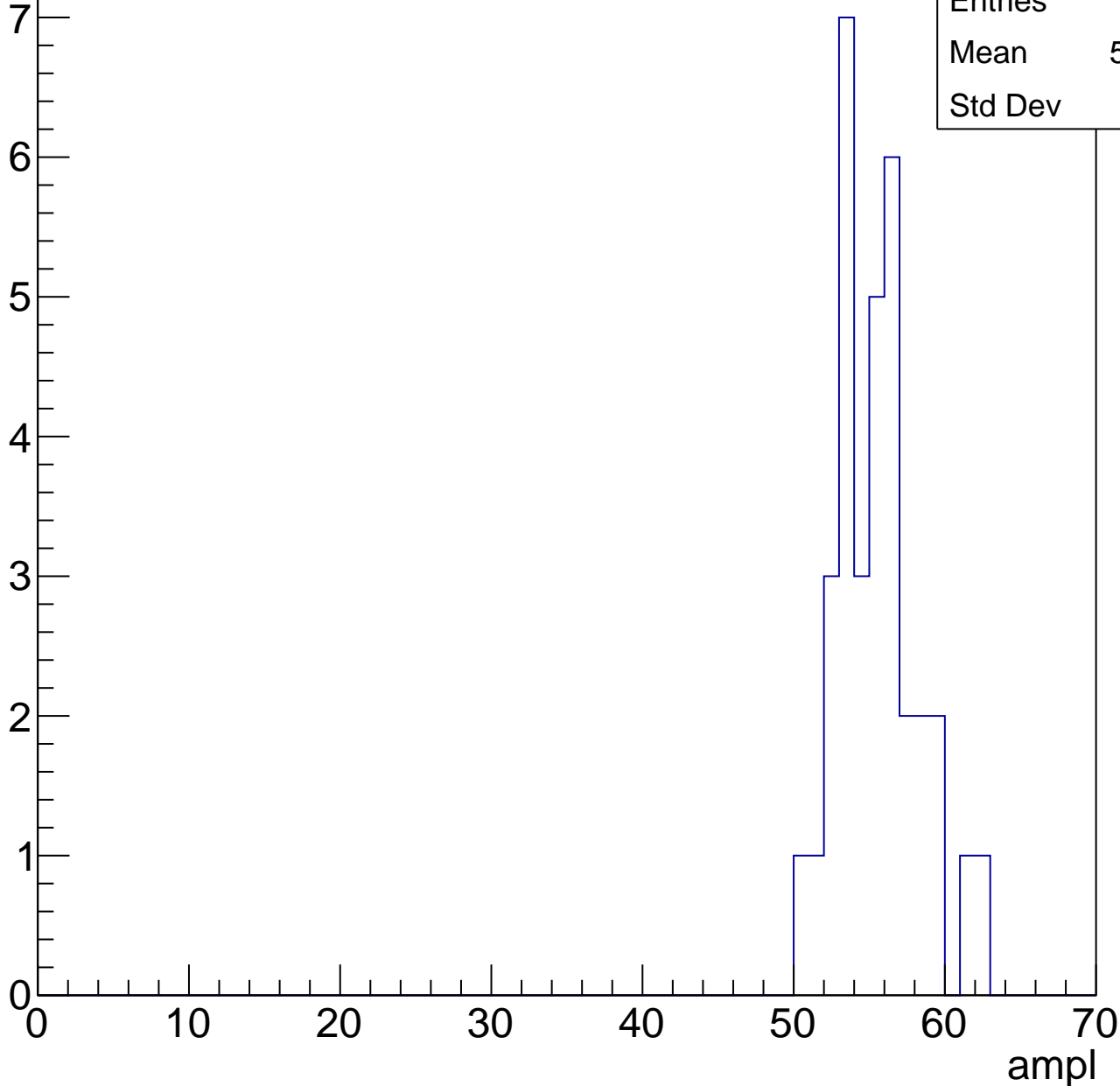
Entries	69
Mean	49.23
Std Dev	3.293

B0L002S, U17-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	55.06
Std Dev	2.7

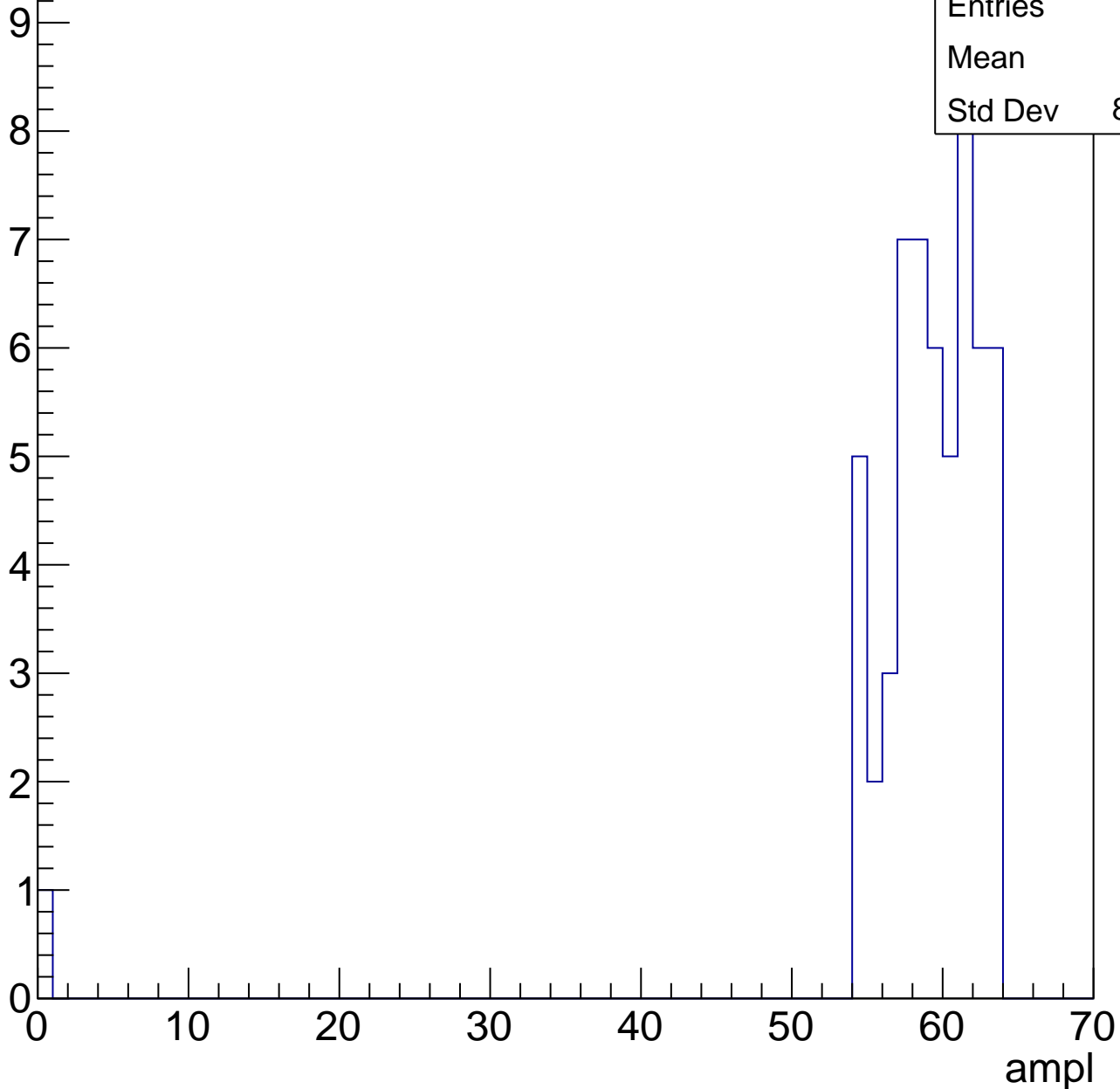


B0L002S, U17-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	58
Std Dev	8.201

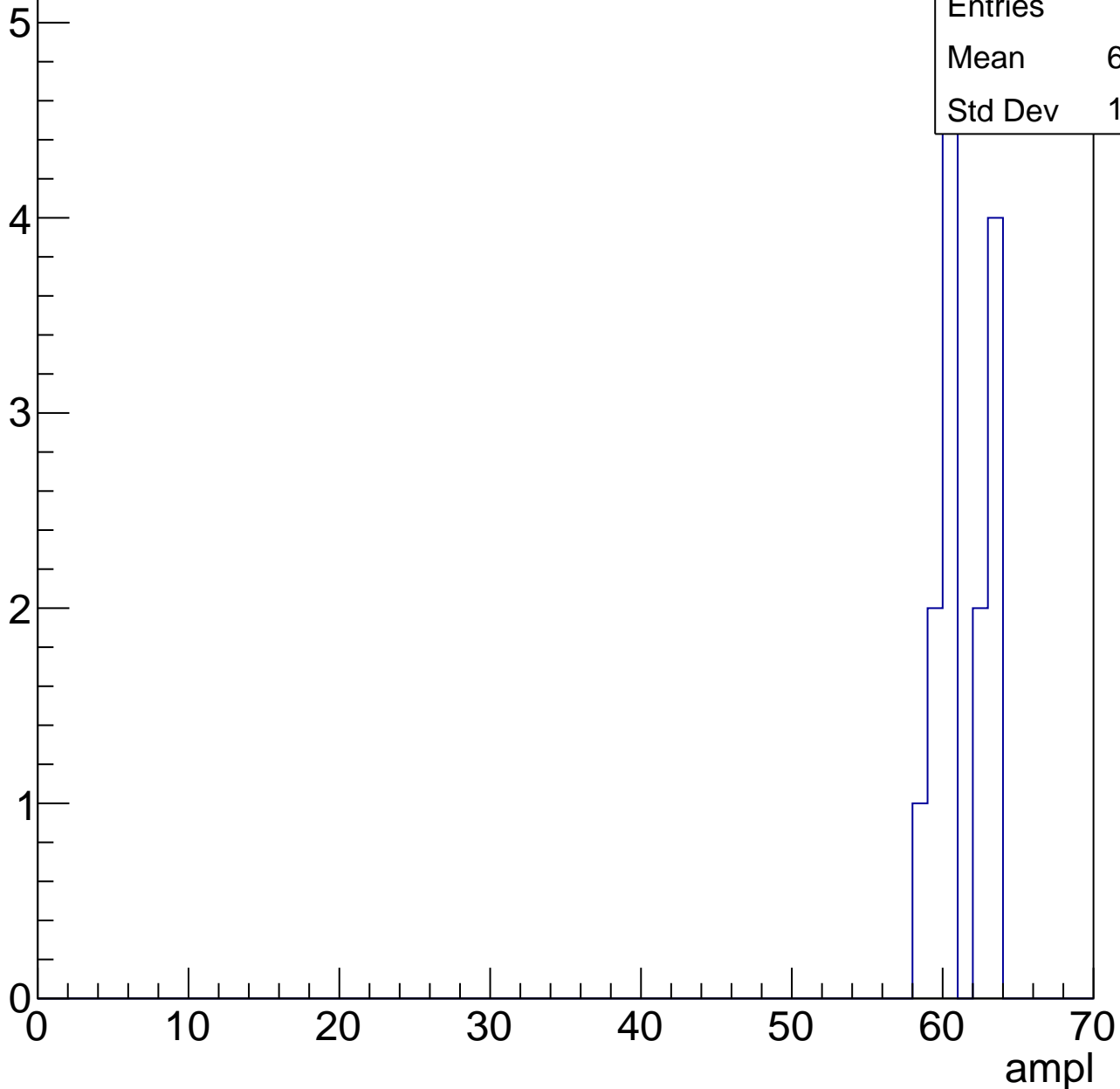


B0L002S, U17-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	14
Mean	60.86
Std Dev	1.684



B0L002S, U17-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch5, adc0

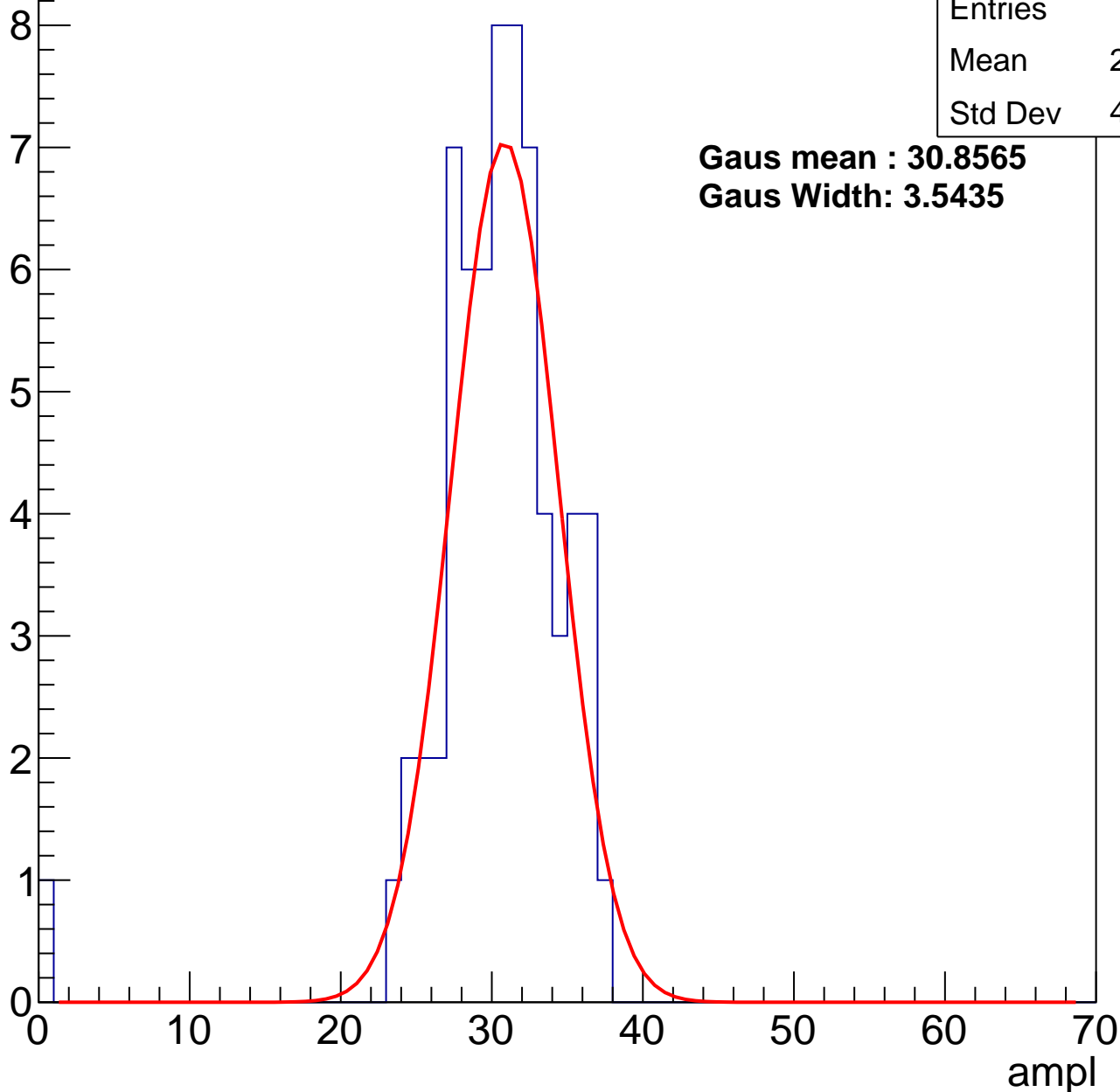
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.86
Std Dev	4.936

Gaus mean : 30.8565

Gaus Width: 3.5435



B0L002S, U17-ch5, adc1

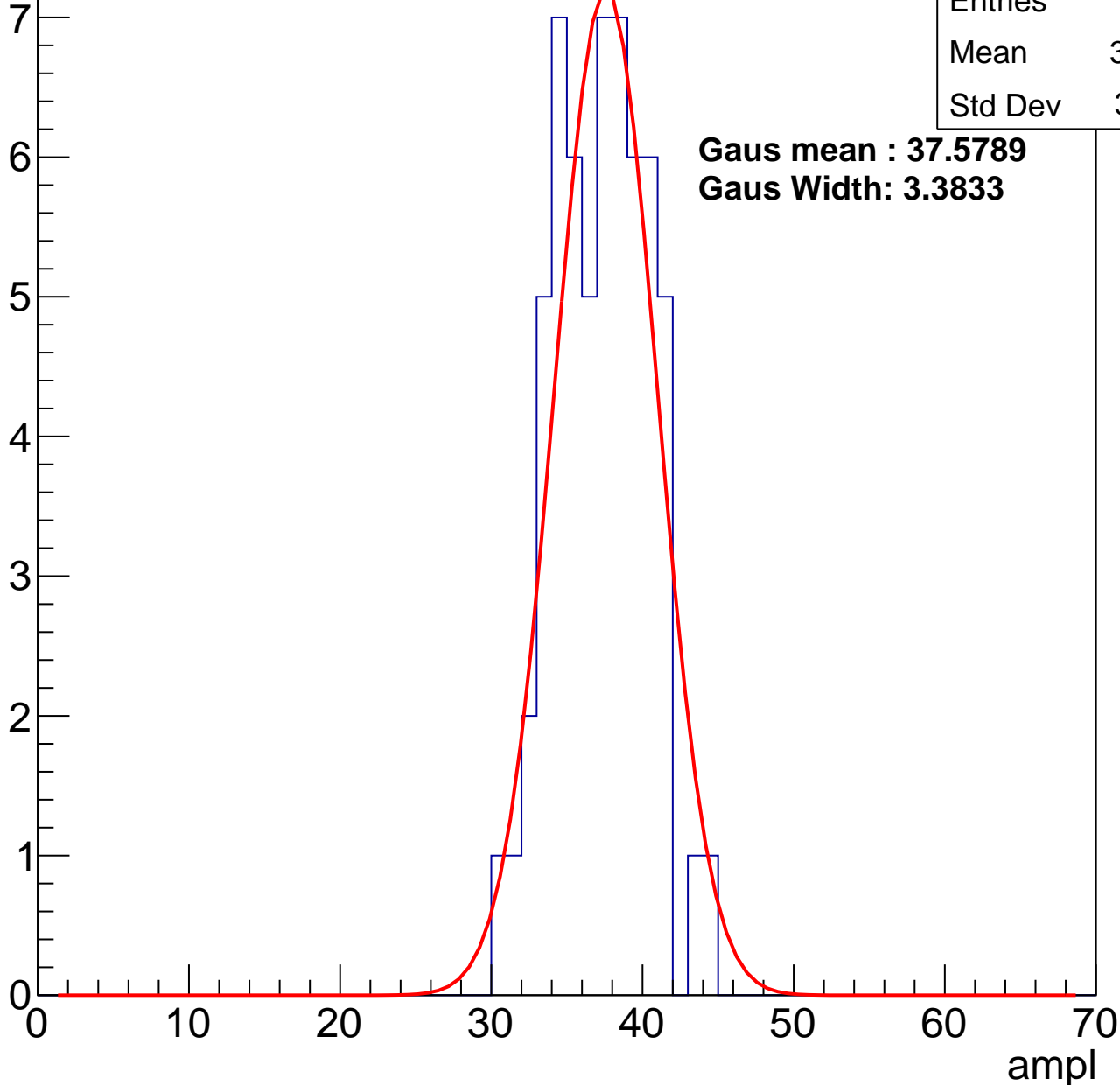
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.82
Std Dev	3.041

Gaus mean : 37.5789

Gaus Width: 3.3833



B0L002S, U17-ch5, adc2

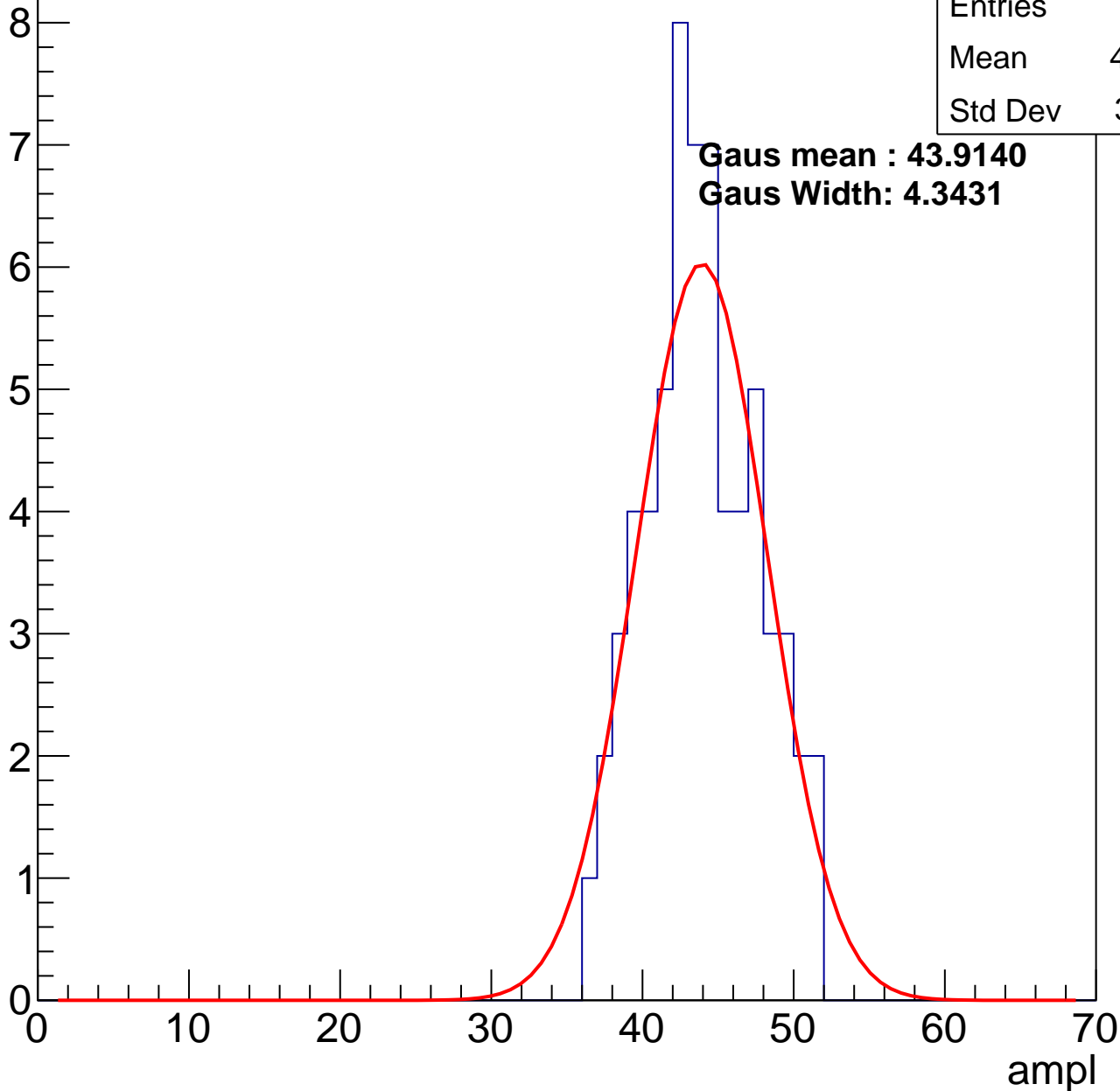
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.47
Std Dev	3.661

Gaus mean : 43.9140

Gaus Width: 4.3431

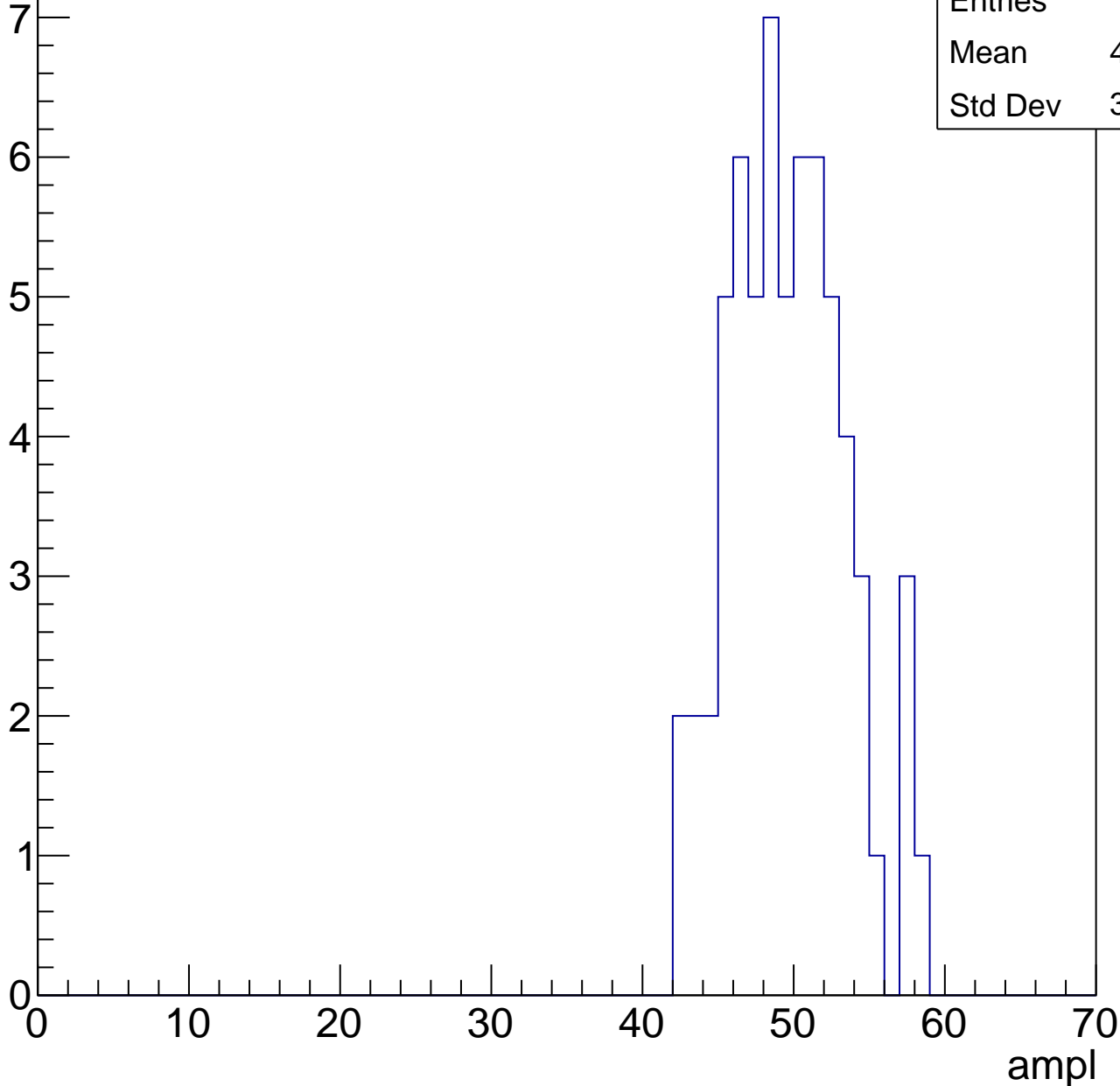


B0L002S, U17-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

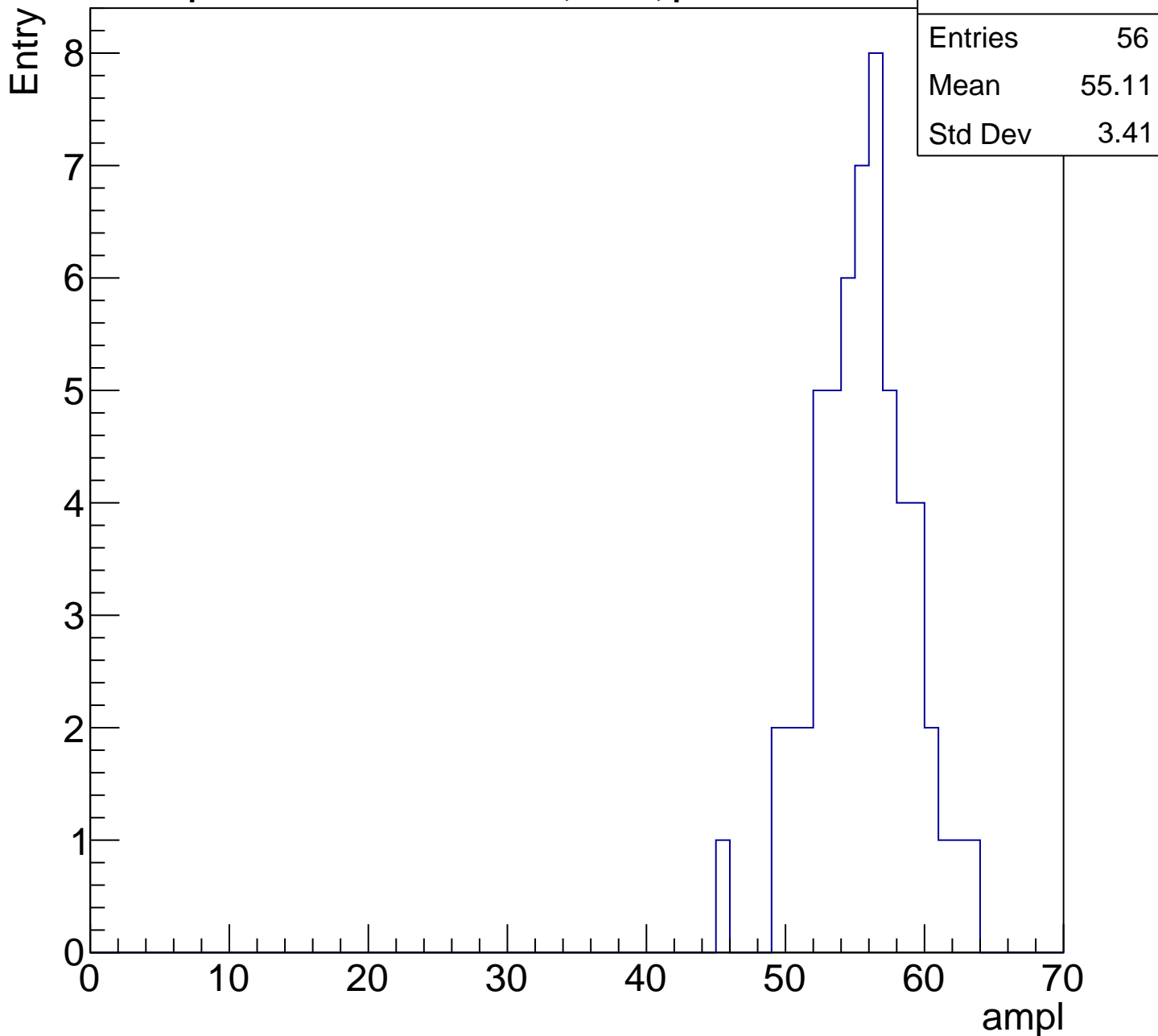
Entry

Entries	63
Mean	49.19
Std Dev	3.783



B0L002S, U17-ch5, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U17-ch5, adc5

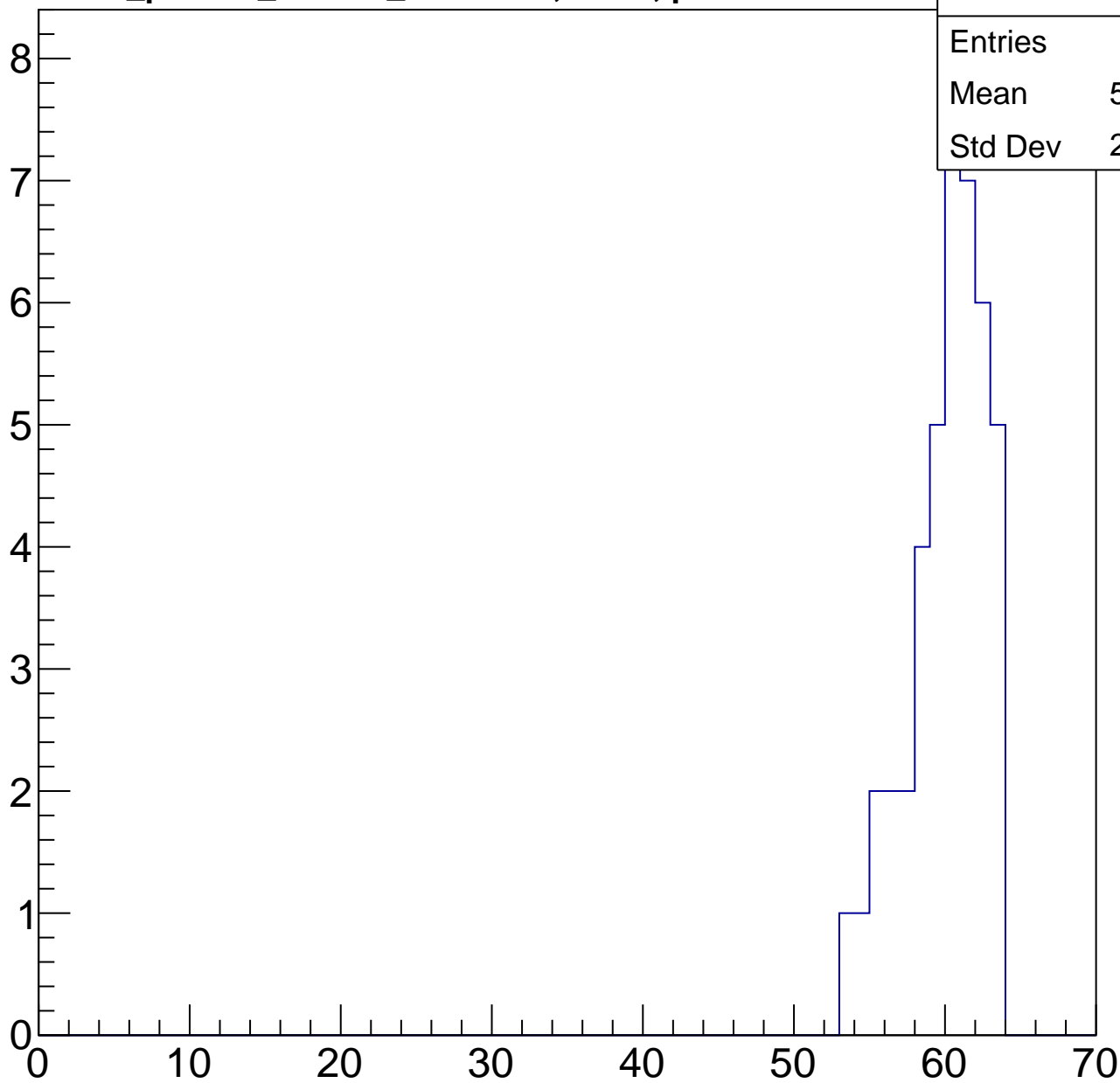
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.63
Std Dev	2.534

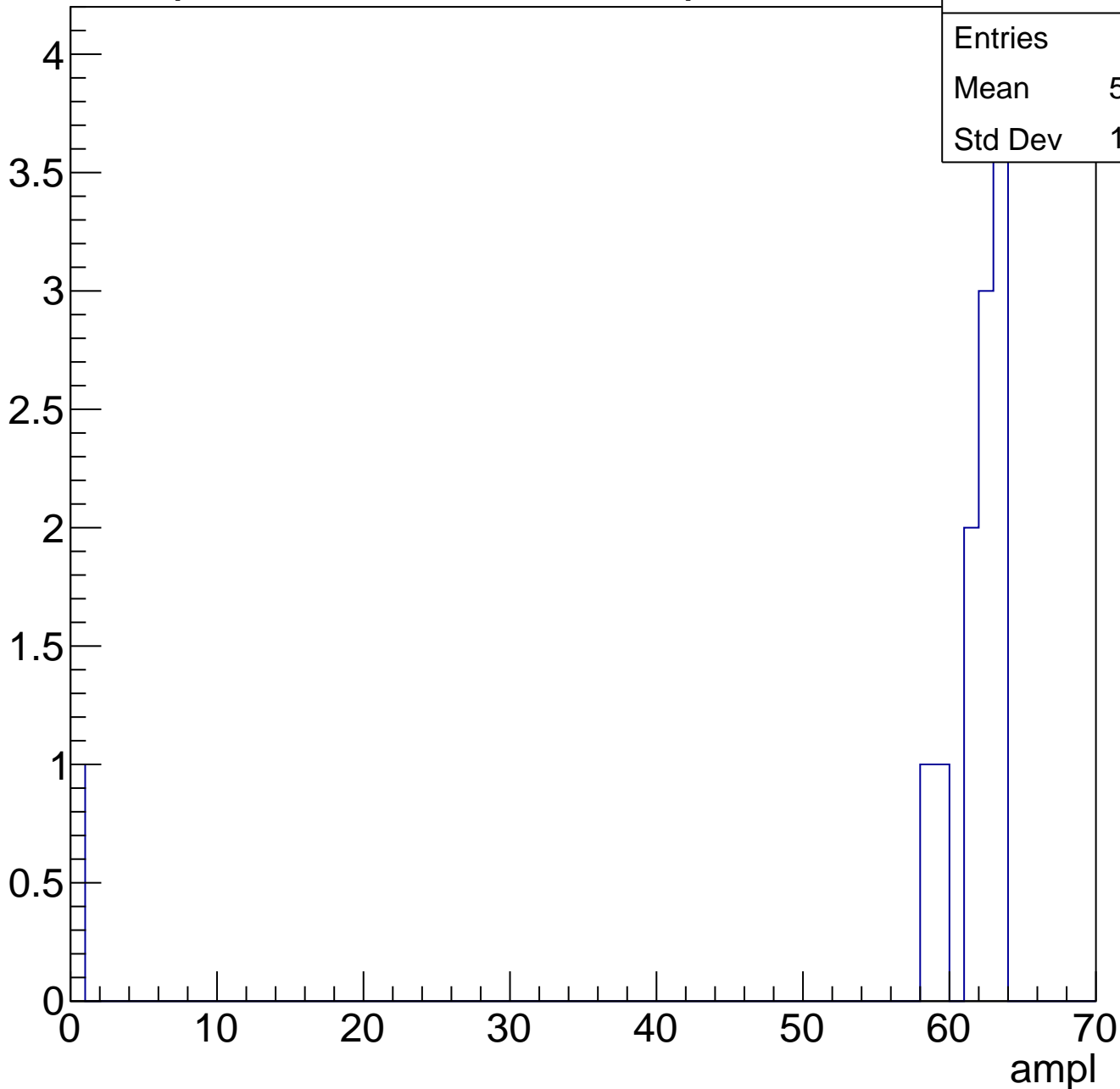
ampl



B0L002S, U17-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch6, adc0

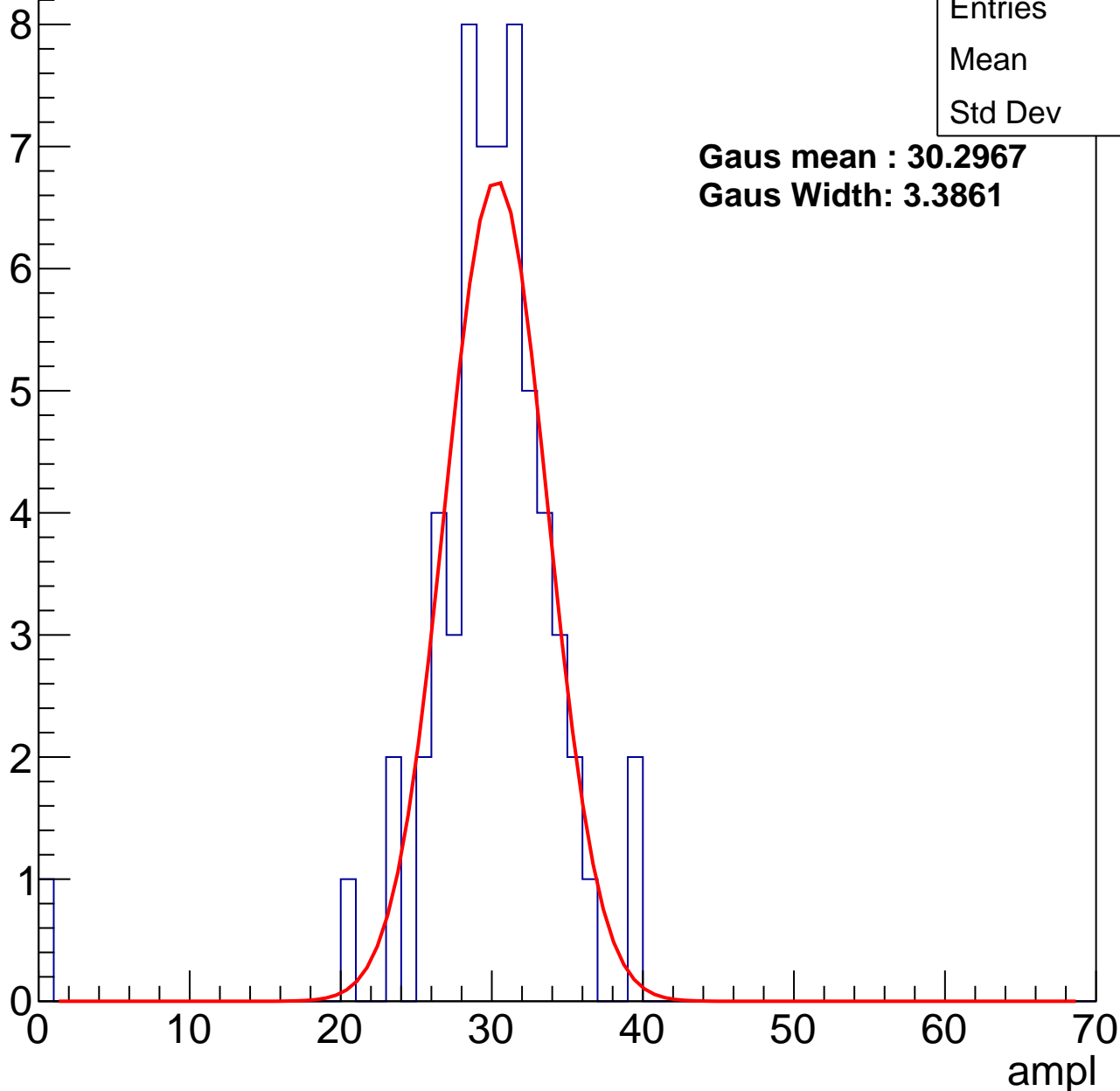
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.4
Std Dev	5.19

Gaus mean : 30.2967

Gaus Width: 3.3861



B0L002S, U17-ch6, adc1

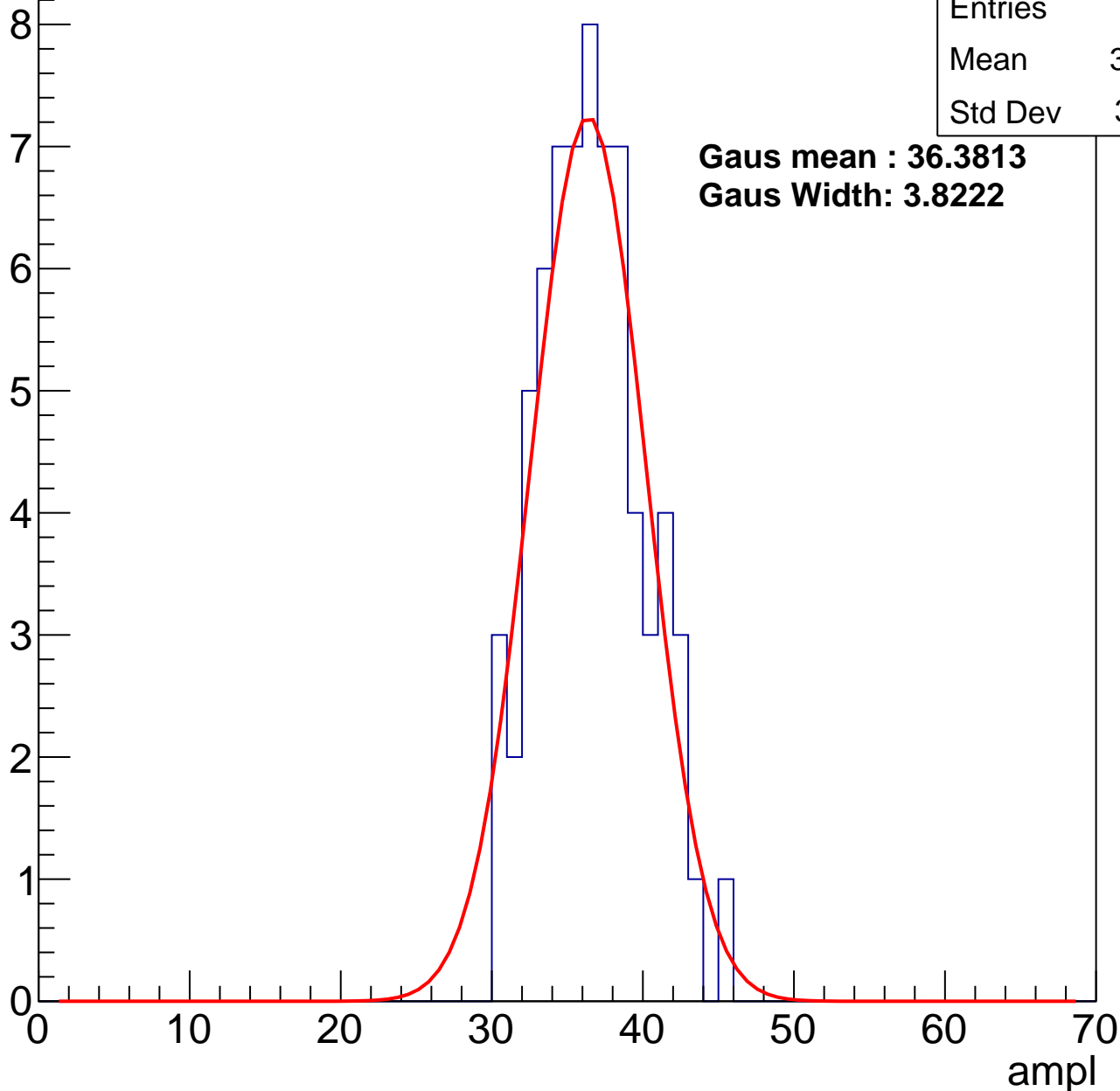
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	36.18
Std Dev	3.391

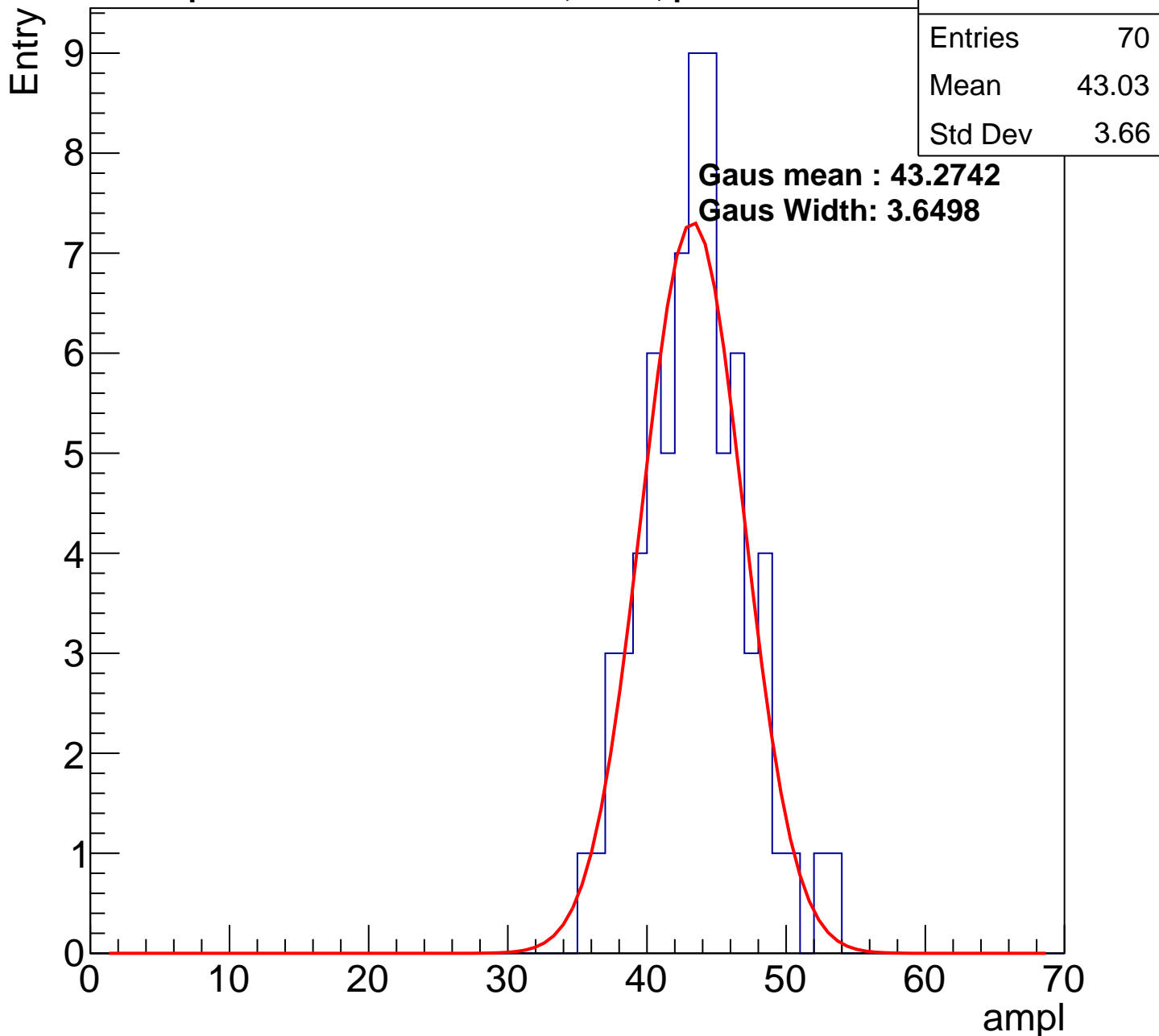
Gaus mean : 36.3813

Gaus Width: 3.8222



B0L002S, U17-ch6, adc2

calib_packv5_042523_0143.root, FC#8, port C1

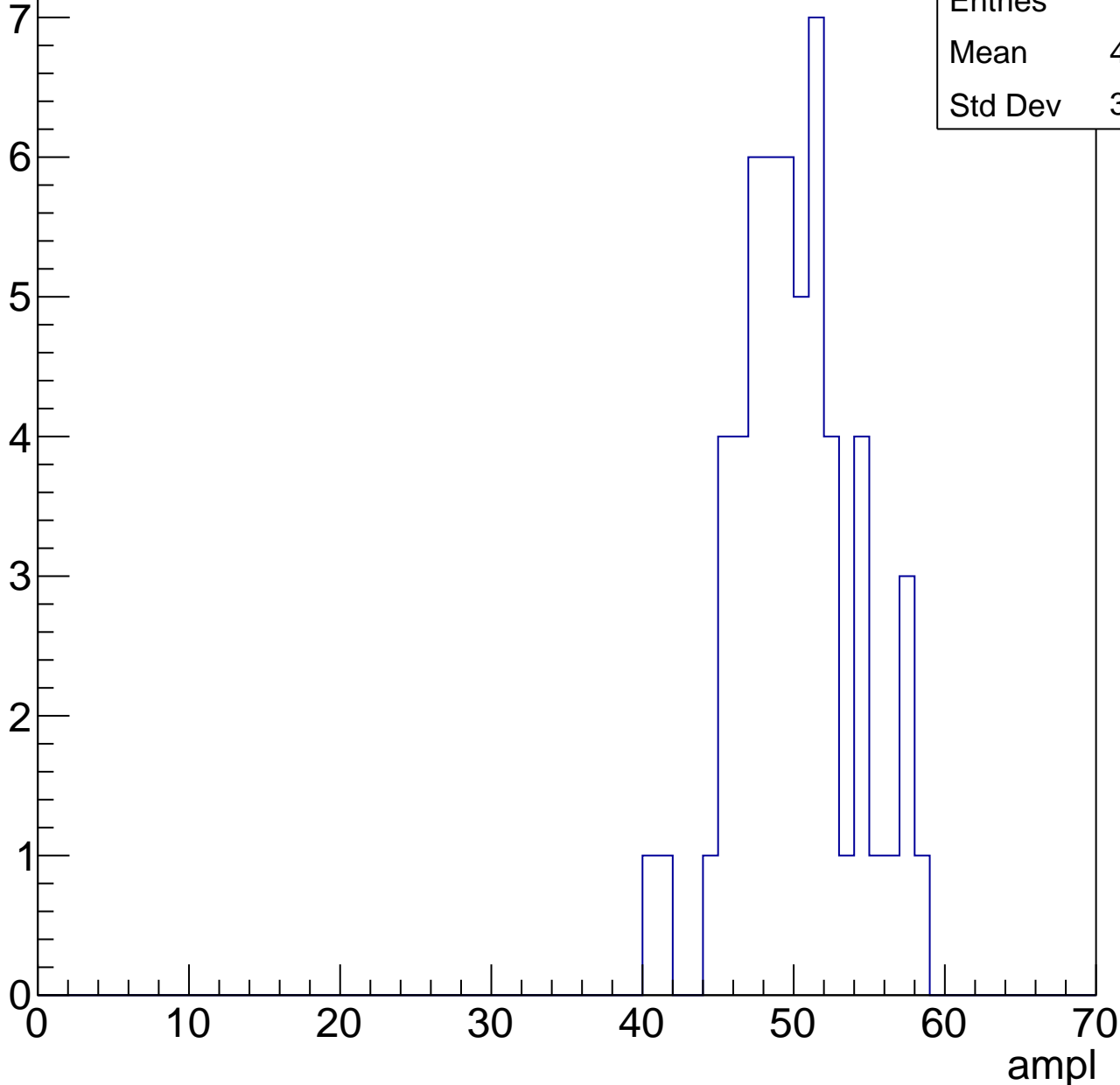


B0L002S, U17-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	49.59
Std Dev	3.835

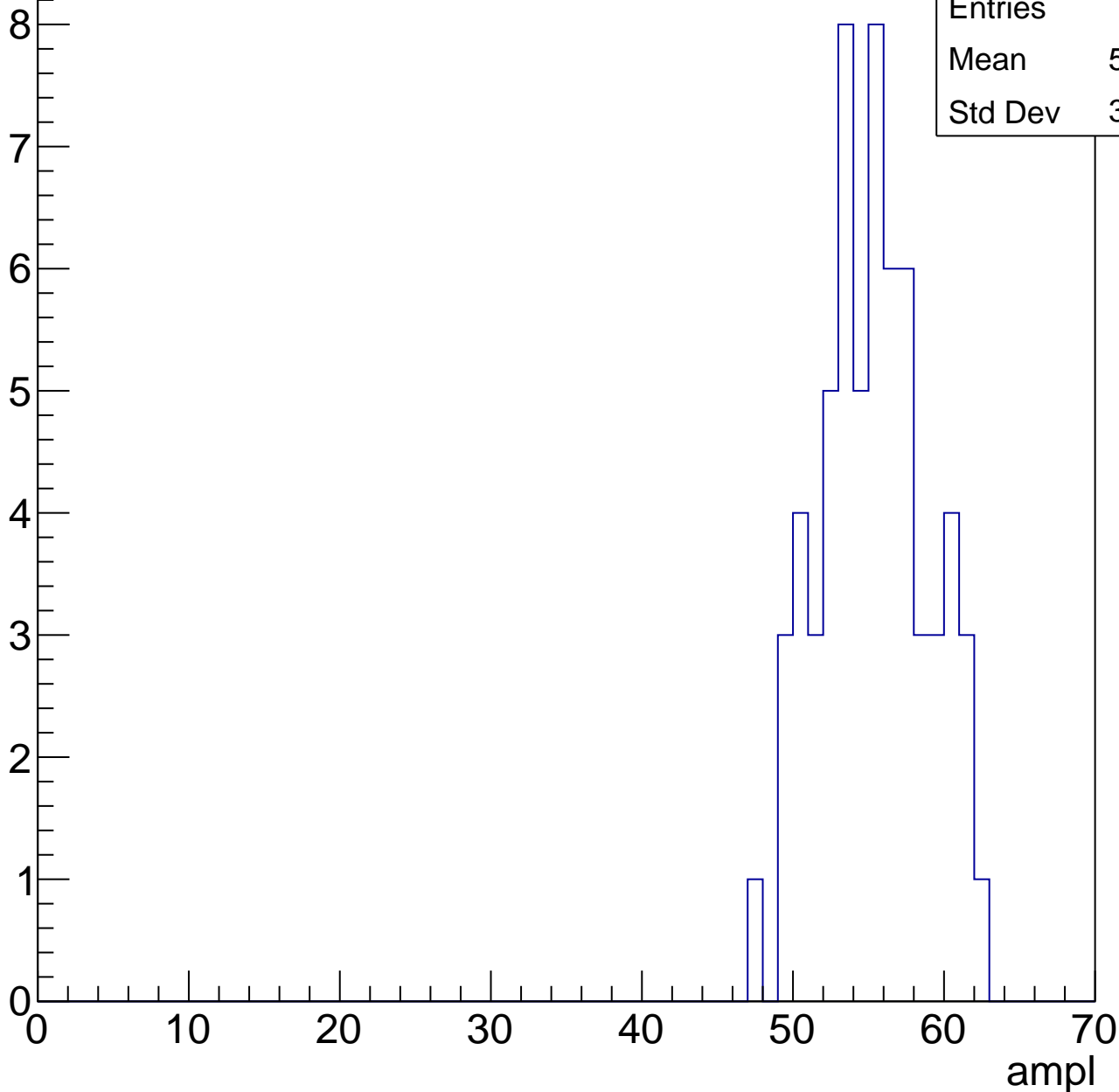


B0L002S, U17-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	54.84
Std Dev	3.479

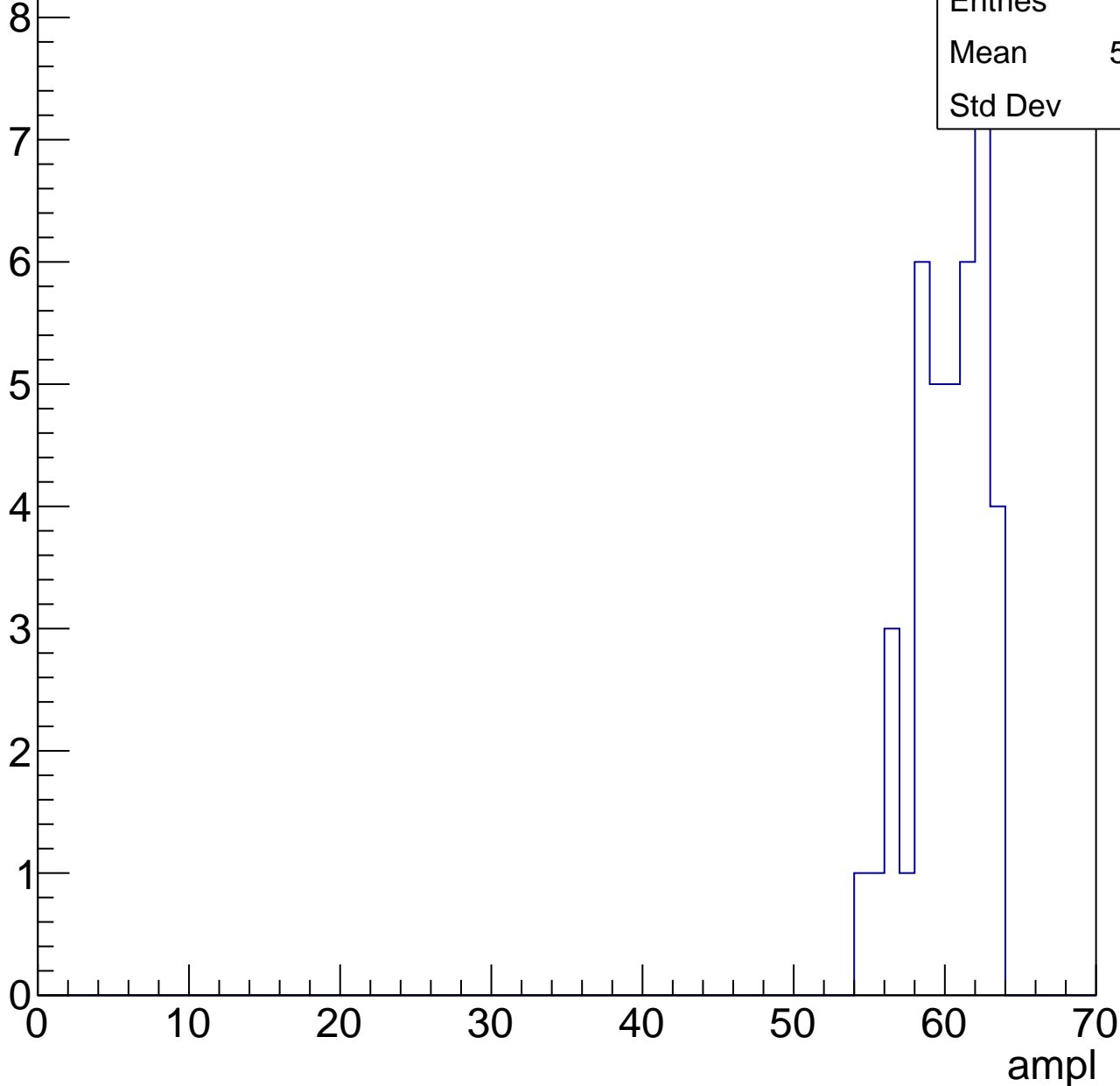


B0L002S, U17-ch6, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

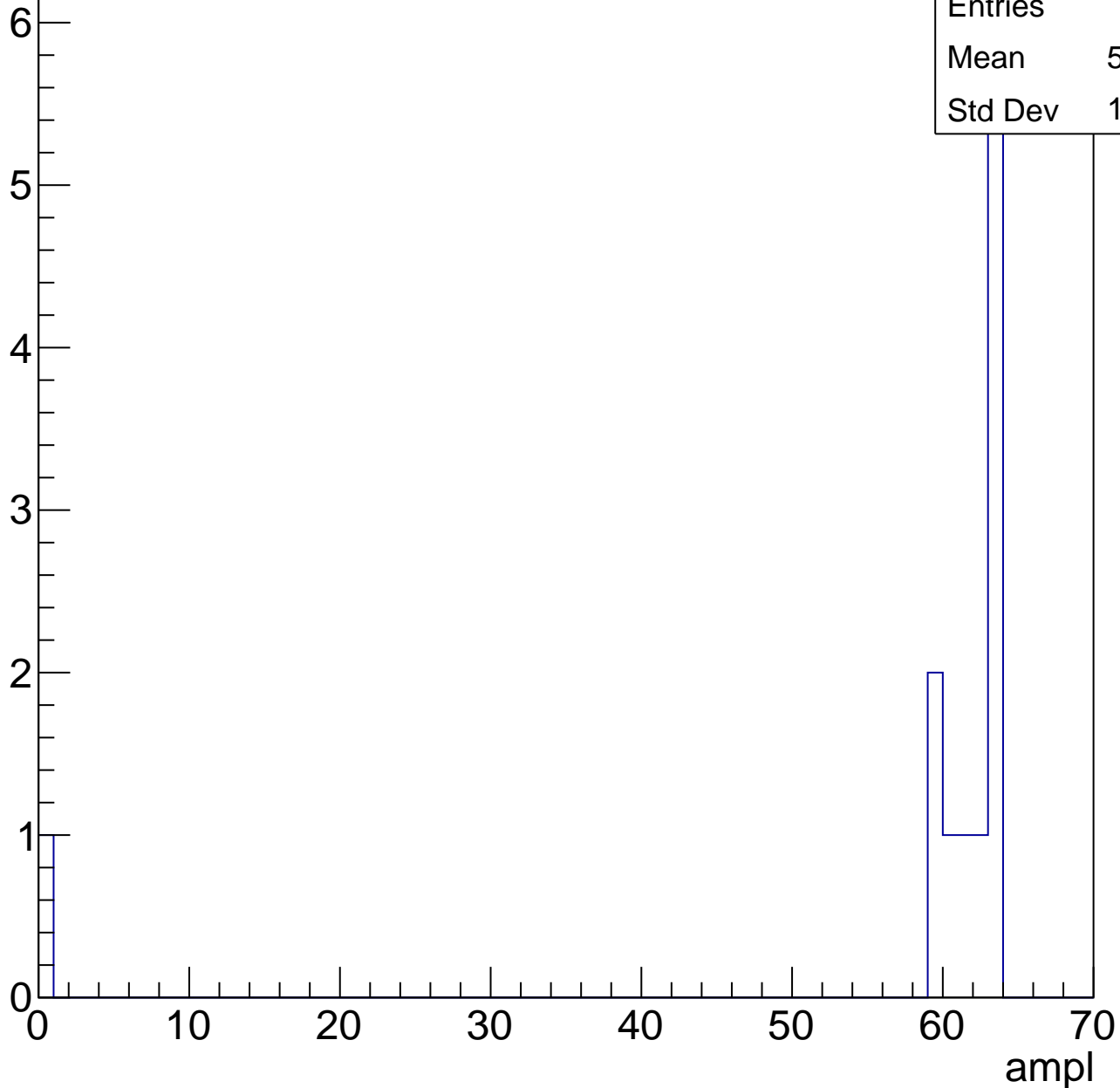
Entries	40
Mean	59.77
Std Dev	2.34



B0L002S, U17-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

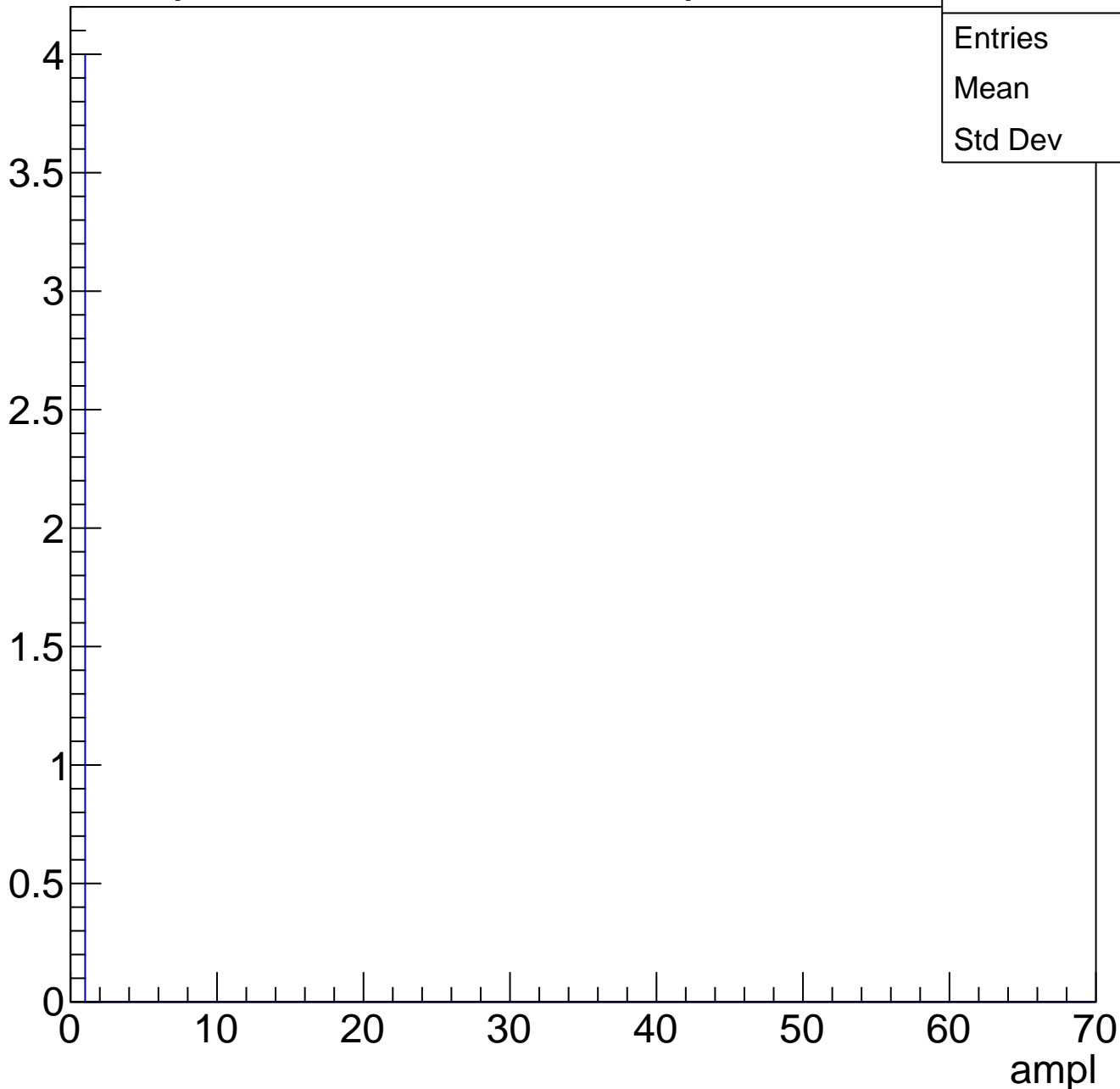


Entries	12
Mean	56.58
Std Dev	17.13

B0L002S, U17-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	0
Std Dev	0

B0L002S, U17-ch7, adc0

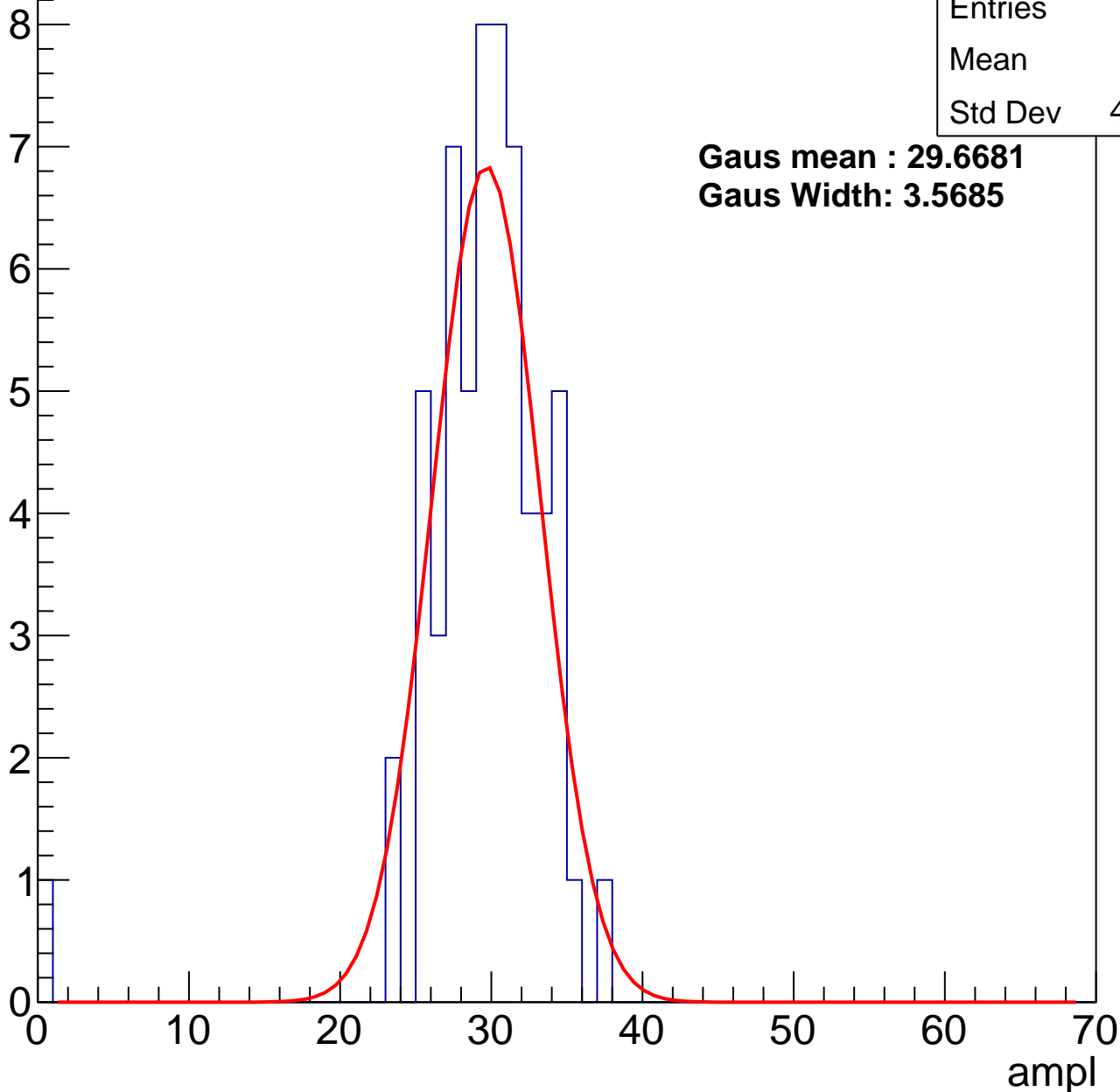
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29
Std Dev	4.815

Gaus mean : 29.6681

Gaus Width: 3.5685



B0L002S, U17-ch7, adc1

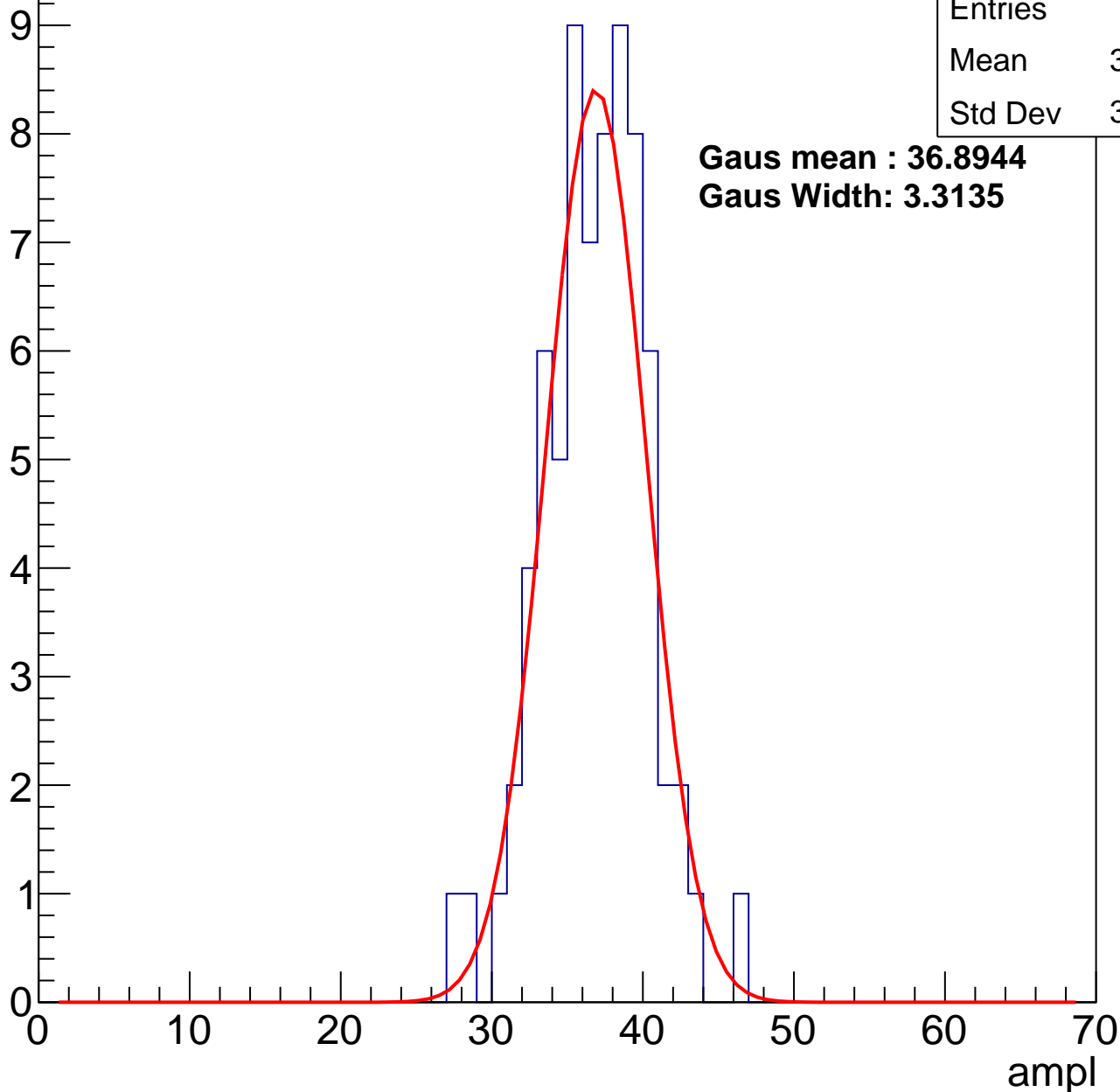
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	36.37
Std Dev	3.414

Gaus mean : 36.8944

Gaus Width: 3.3135



B0L002S, U17-ch7, adc2

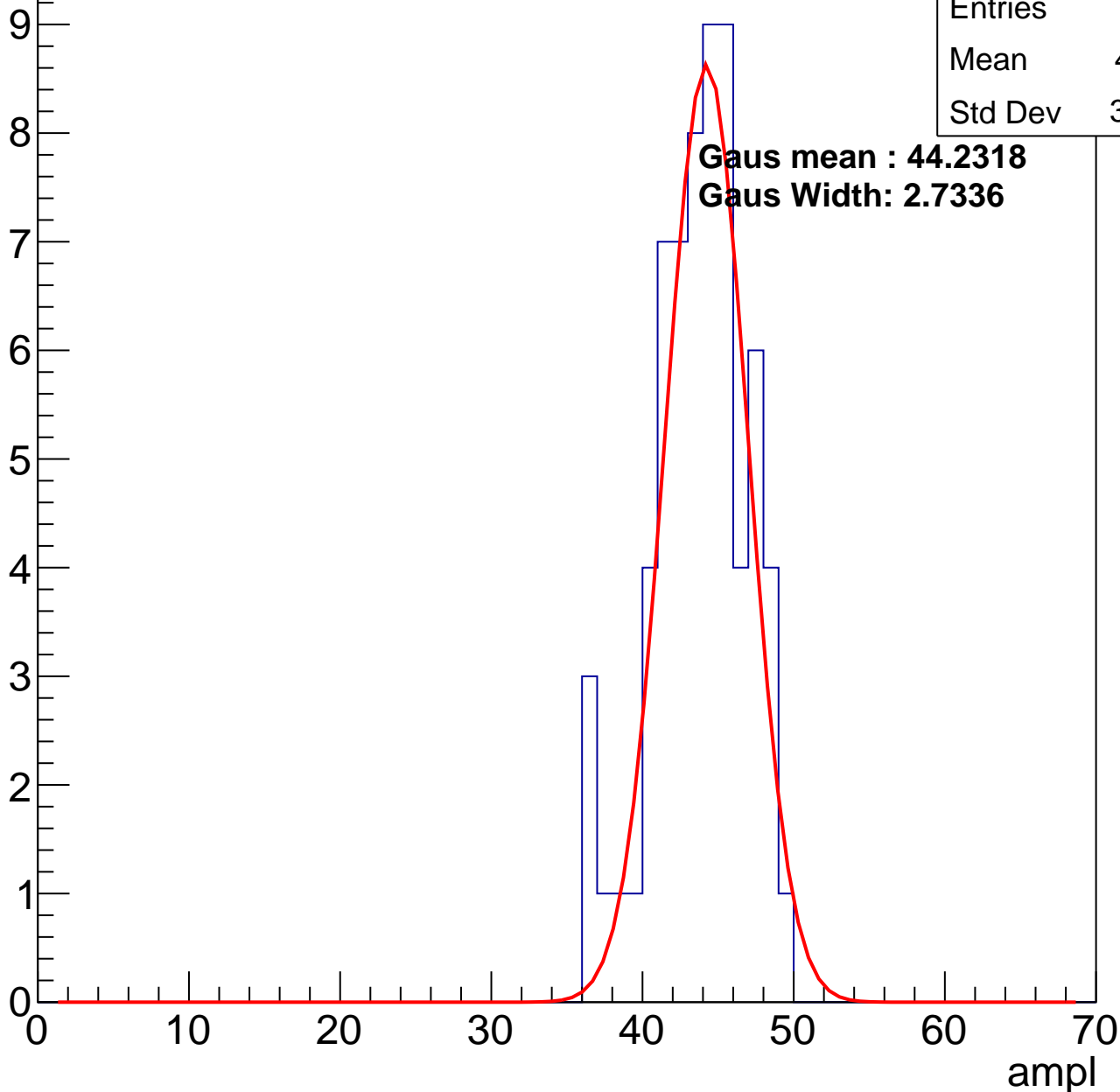
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.31
Std Dev	3.043

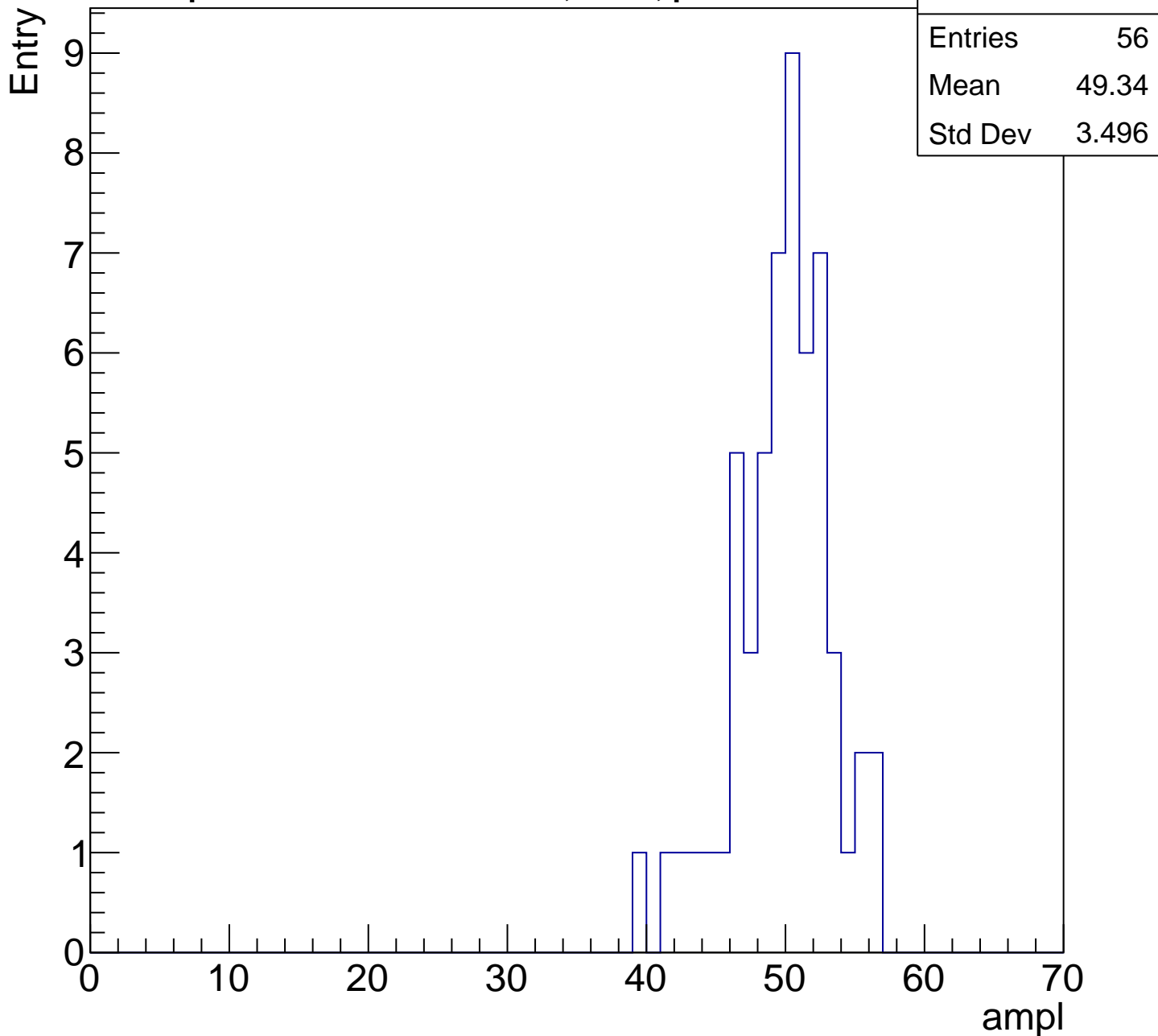
Gaus mean : 44.2318

Gaus Width: 2.7336



B0L002S, U17-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

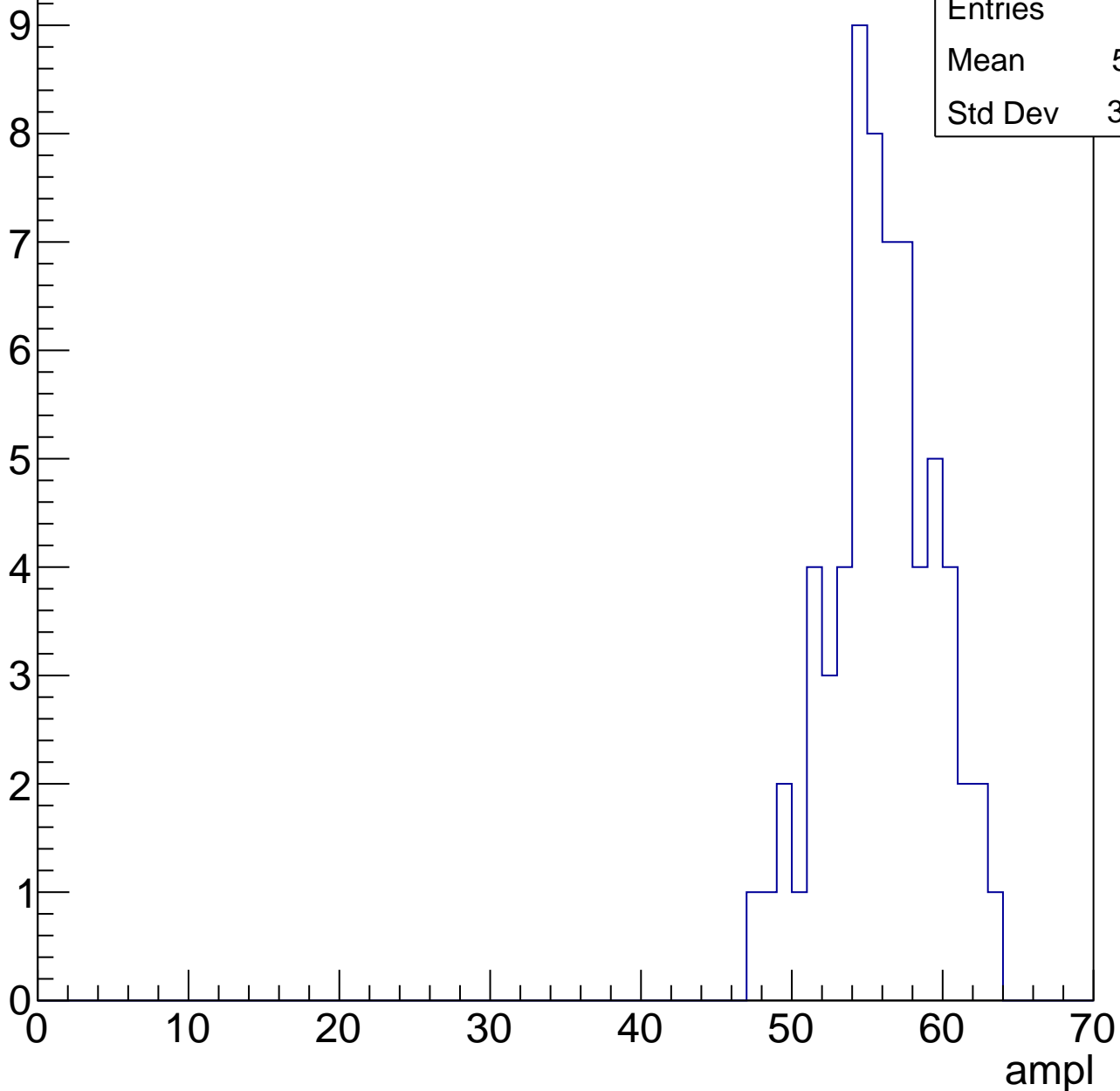


B0L002S, U17-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	55.51
Std Dev	3.496

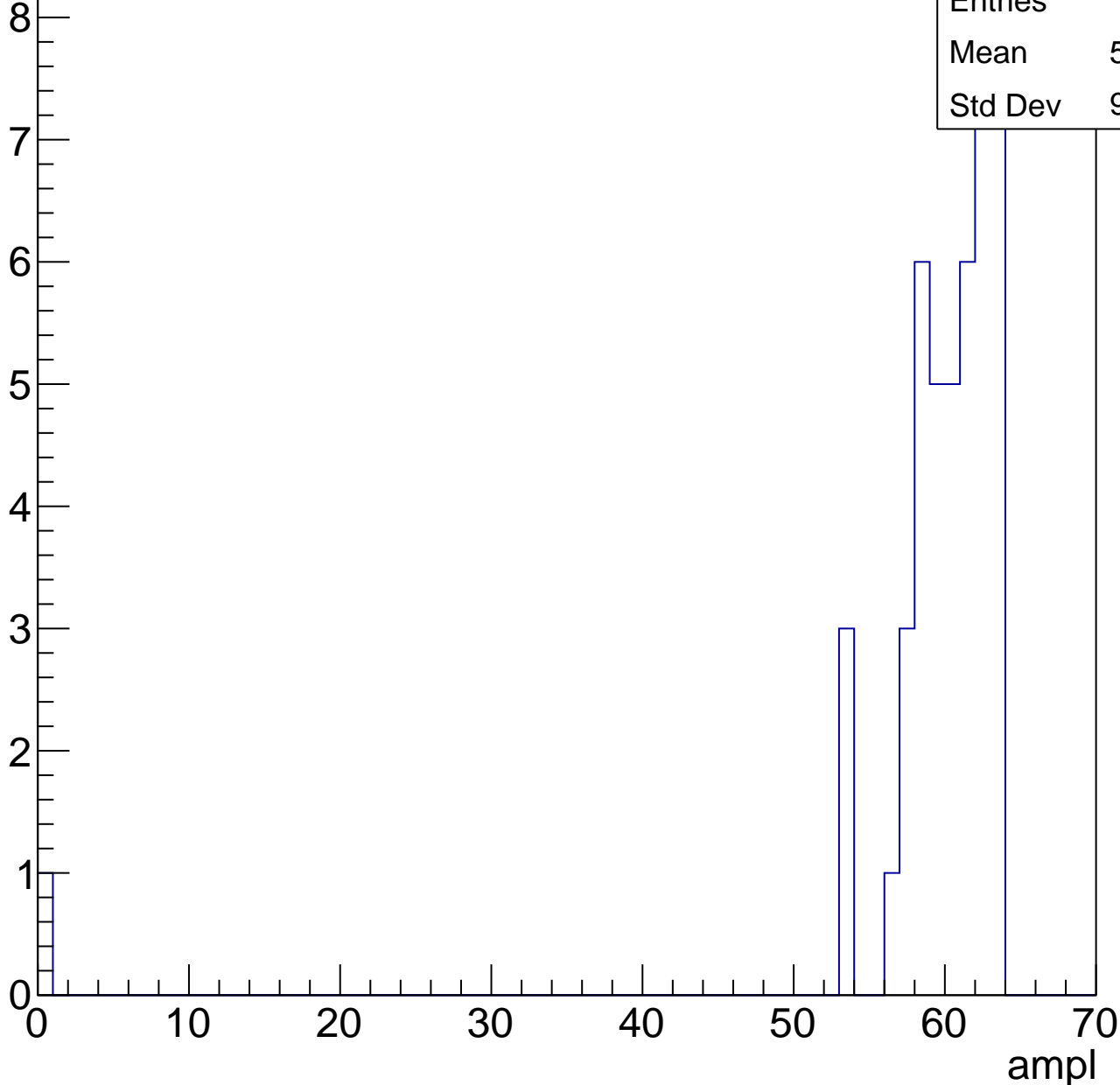


B0L002S, U17-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	58.59
Std Dev	9.133



B0L002S, U17-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

B0L002S, U17-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch8, adc0

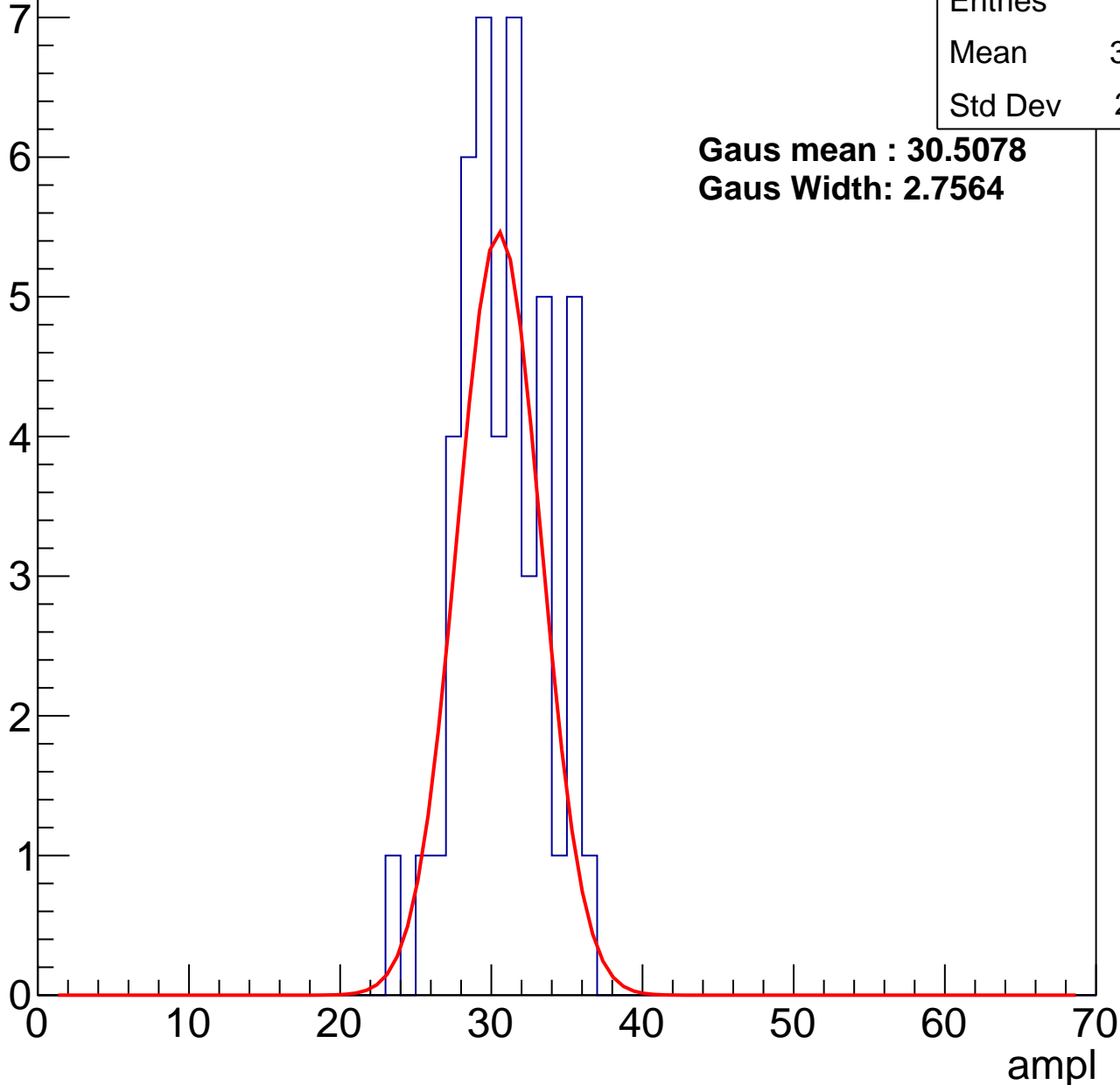
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	30.35
Std Dev	2.921

Gaus mean : 30.5078

Gaus Width: 2.7564



B0L002S, U17-ch8, adc1

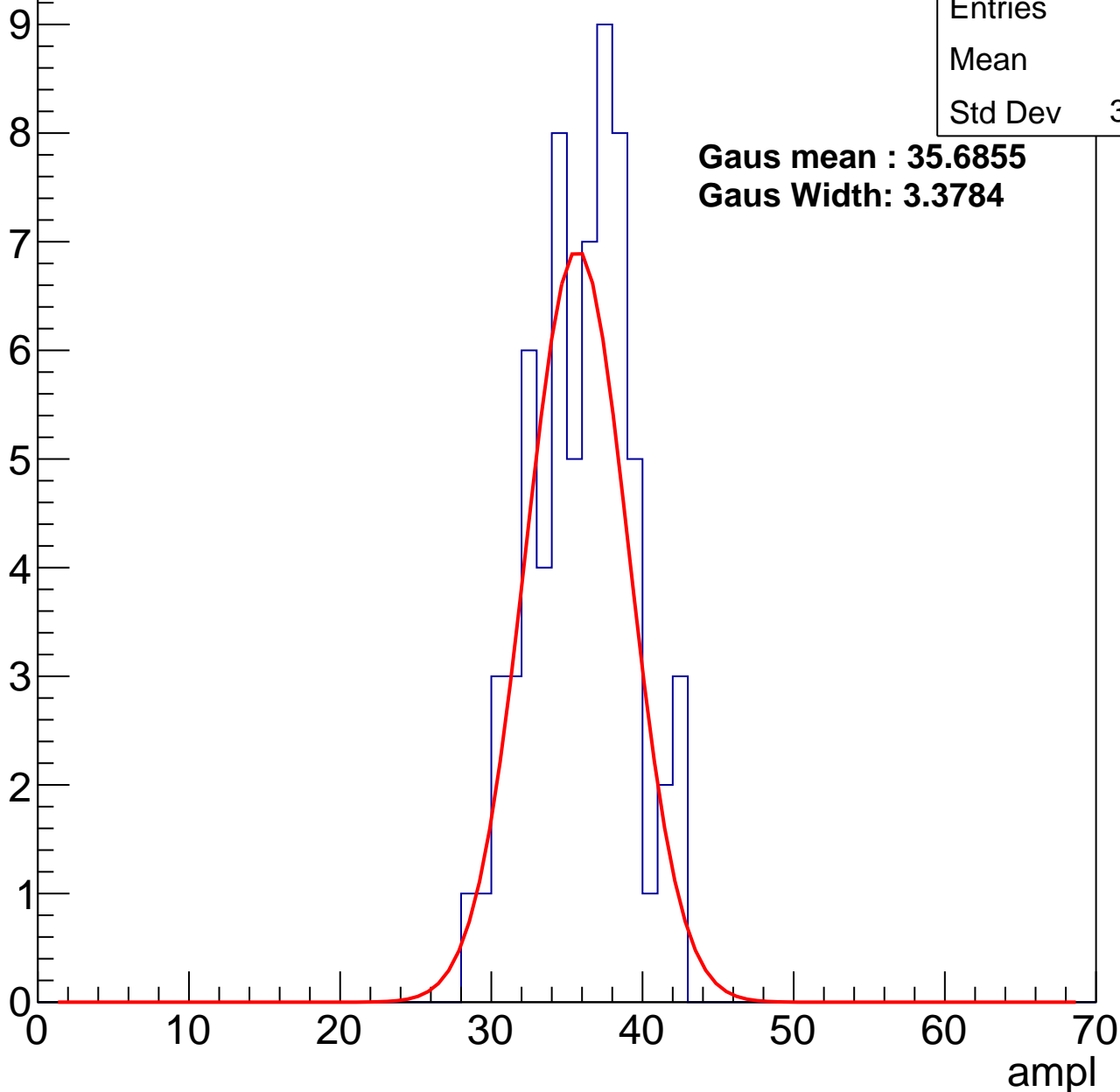
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	35.5
Std Dev	3.276

Gaus mean : 35.6855

Gaus Width: 3.3784



B0L002S, U17-ch8, adc2

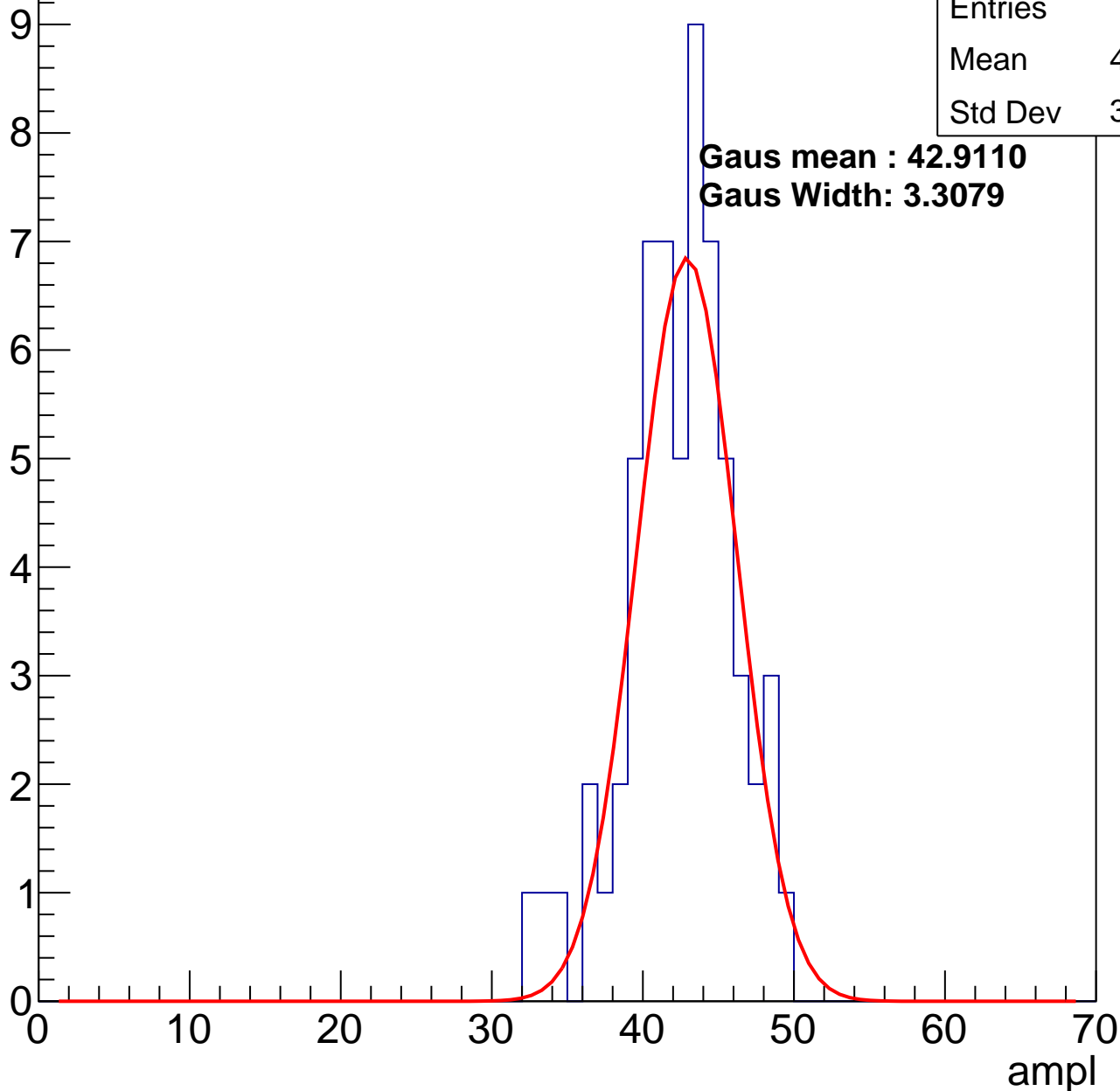
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	41.95
Std Dev	3.572

Gaus mean : 42.9110

Gaus Width: 3.3079

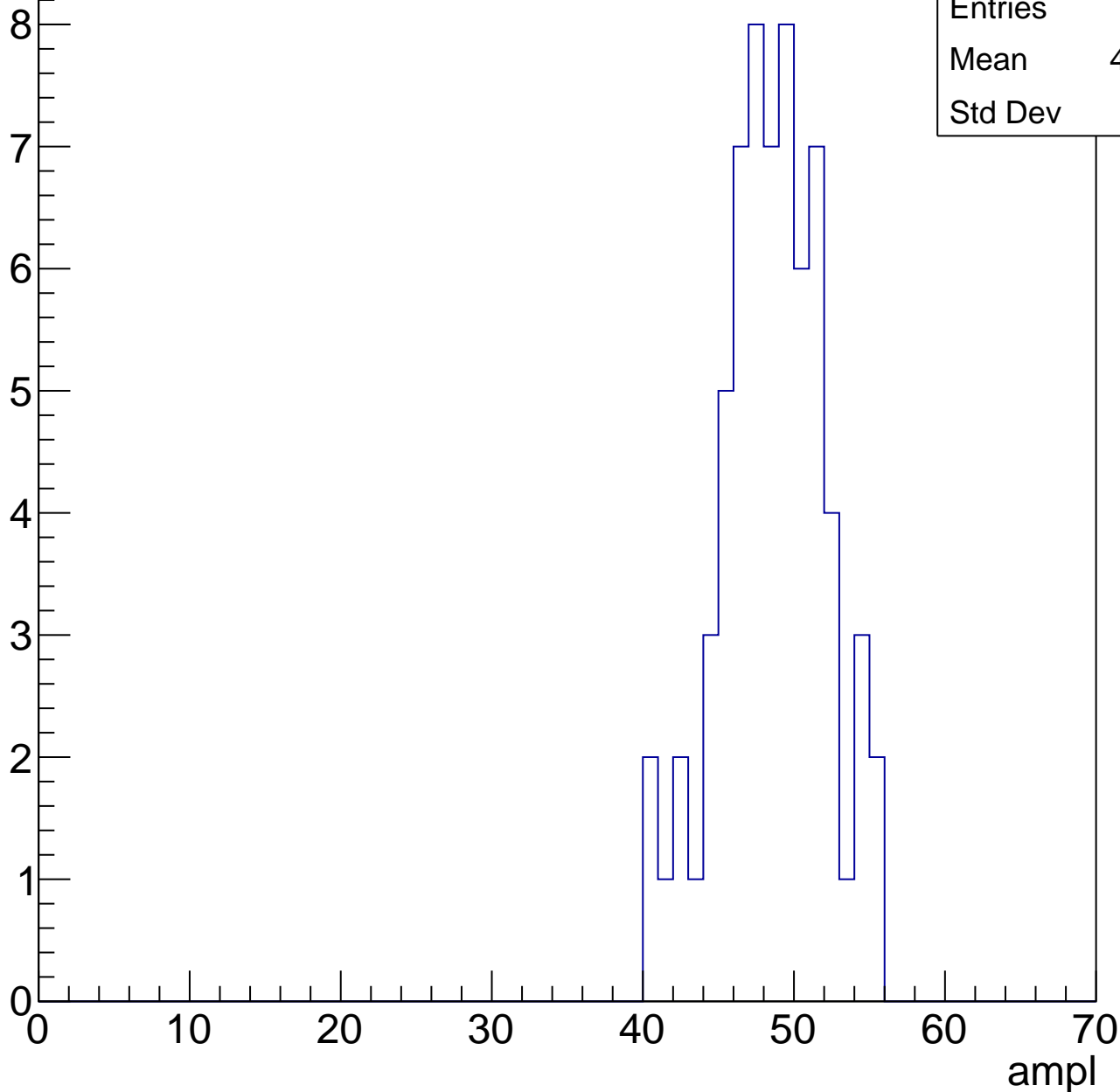


B0L002S, U17-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	48.07
Std Dev	3.44

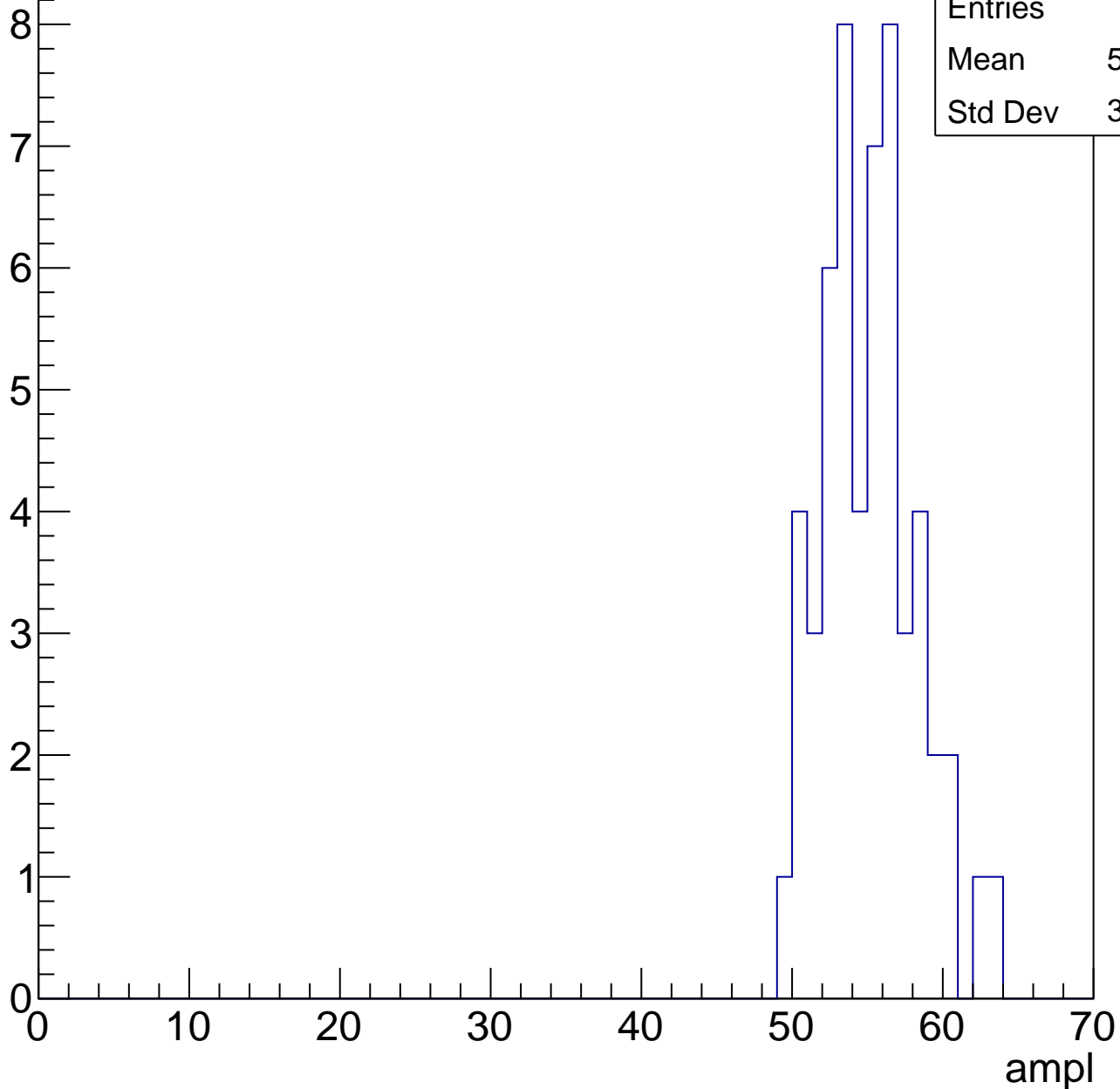


B0L002S, U17-ch8, adc4

calib_packv5_042523_0143.root, FC#8, port C1

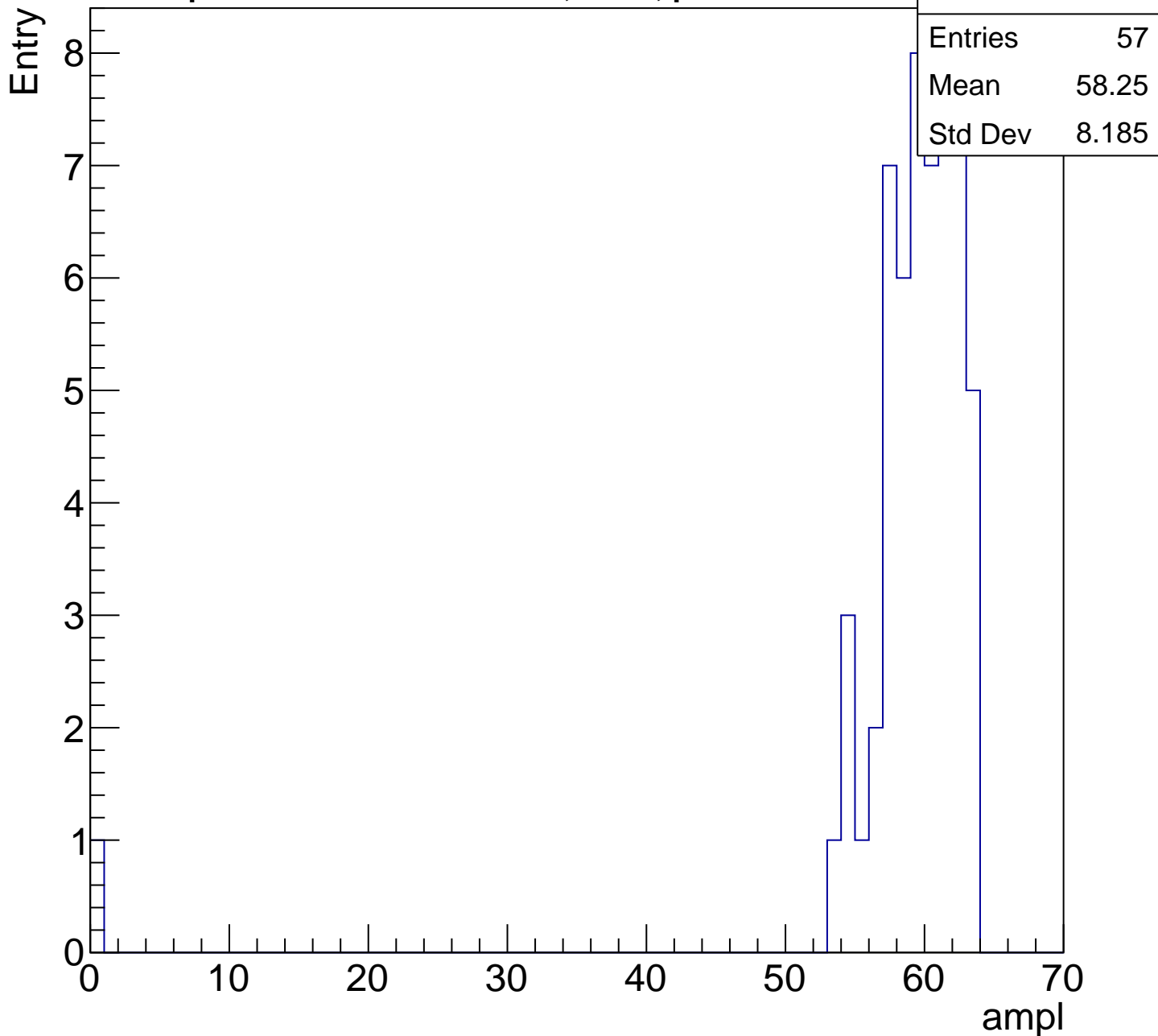
Entry

Entries	54
Mean	54.69
Std Dev	3.102



B0L002S, U17-ch8, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U17-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

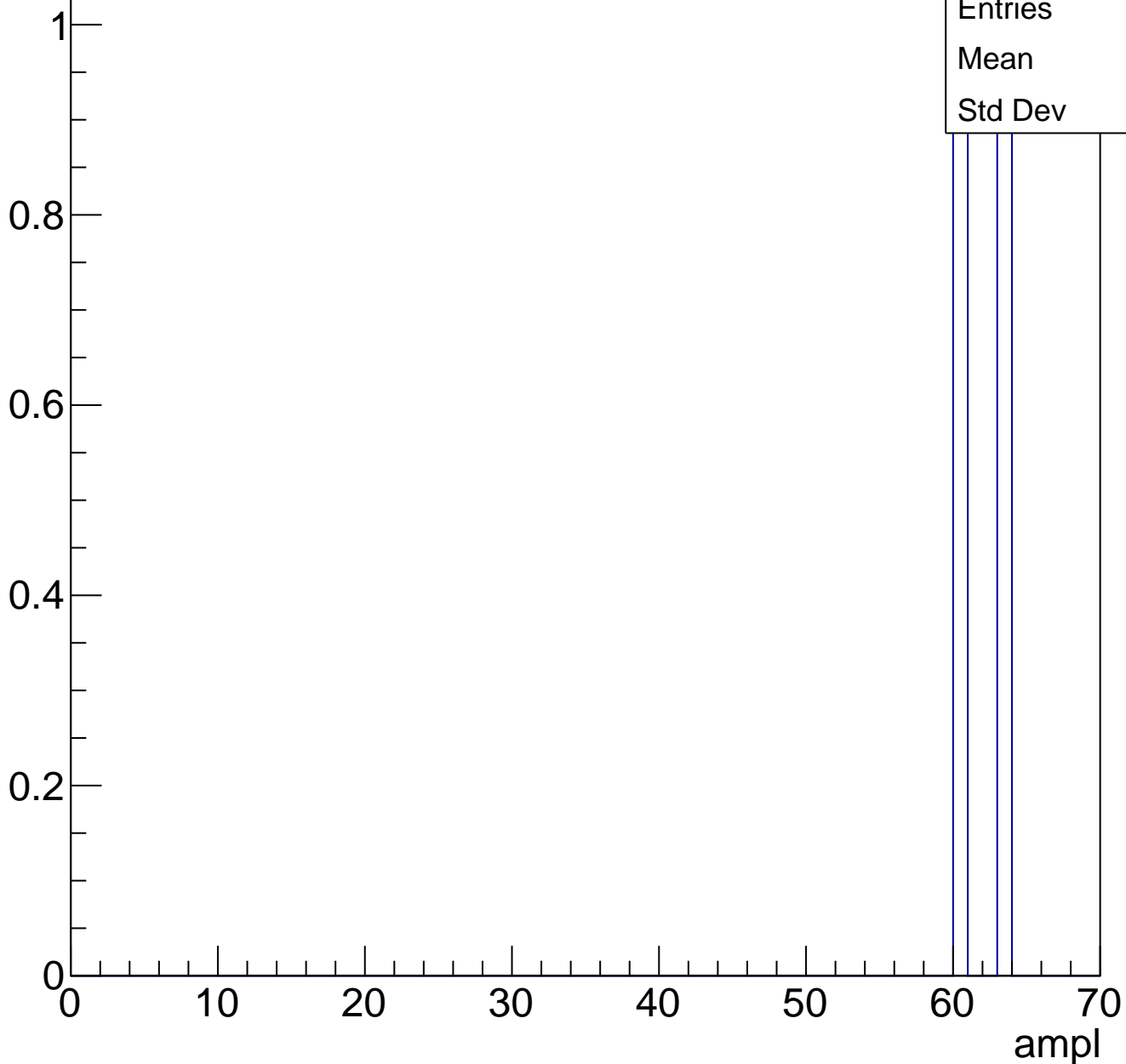
Entry



B0L002S, U17-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch9, adc0

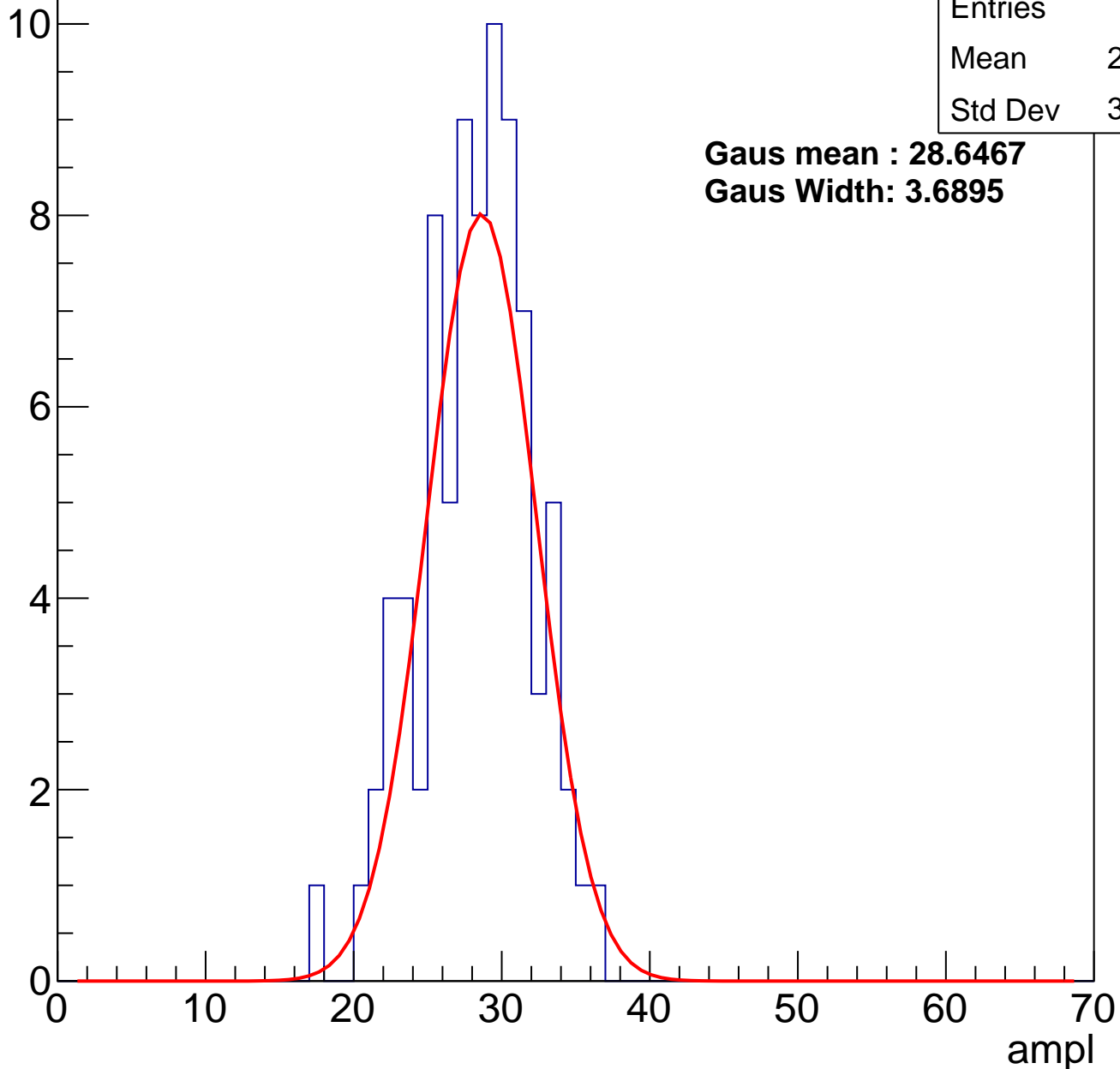
calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	27.82
Std Dev	3.703

Gaus mean : 28.6467

Gaus Width: 3.6895

Entry



B0L002S, U17-ch9, adc1

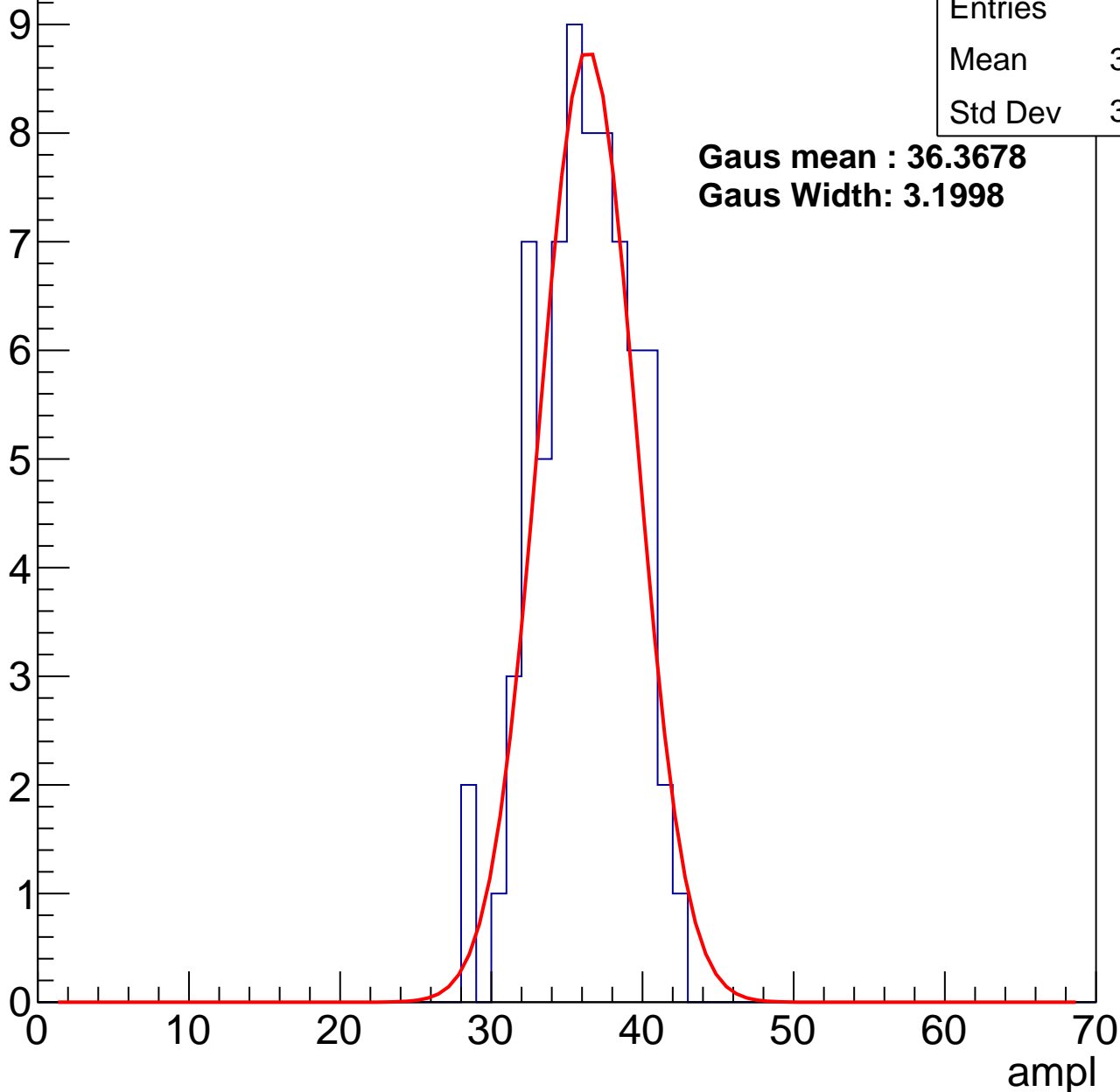
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	35.68
Std Dev	3.113

Gaus mean : 36.3678

Gaus Width: 3.1998



B0L002S, U17-ch9, adc2

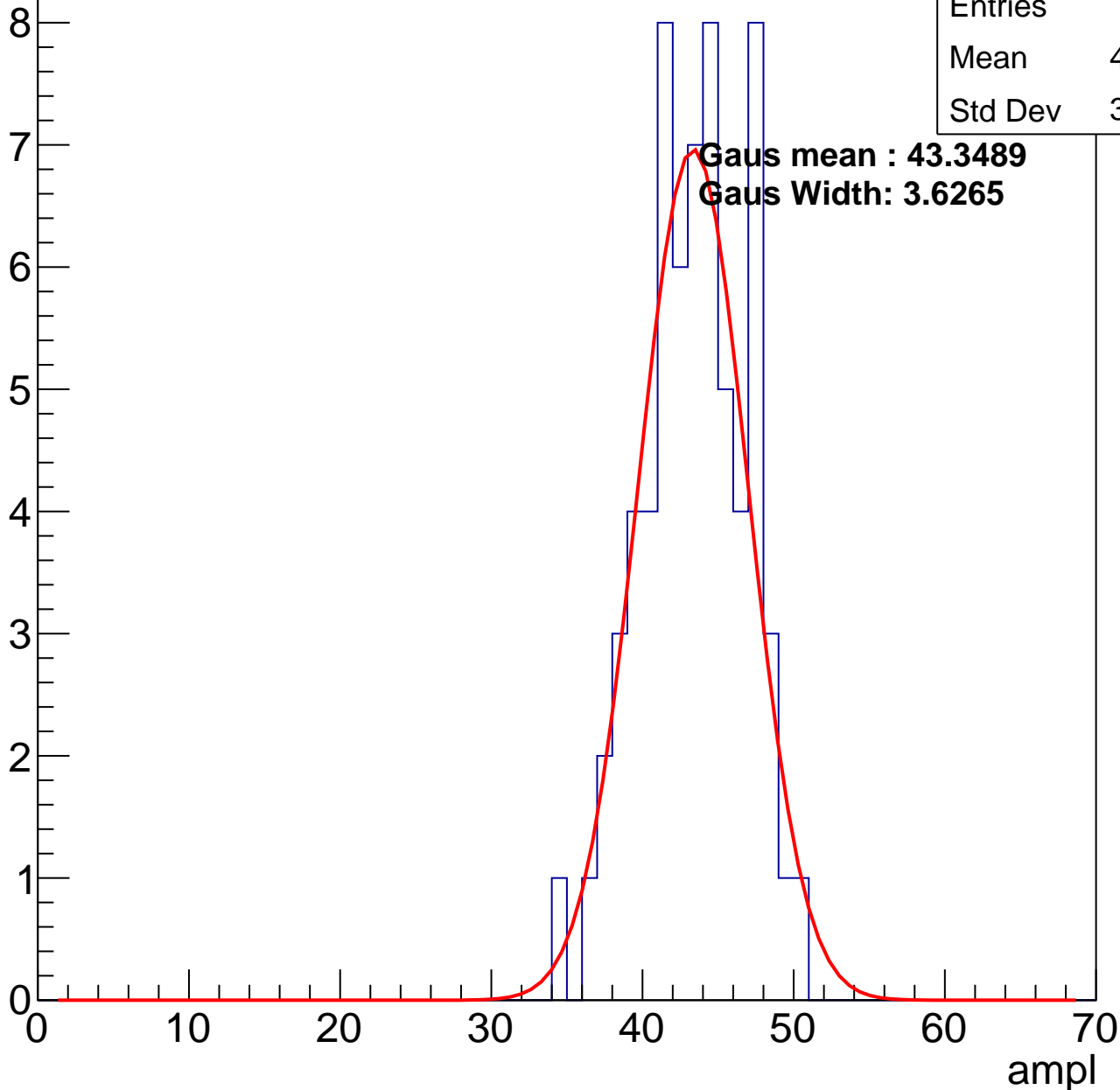
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.95
Std Dev	3.409

Gaus mean : 43.3489

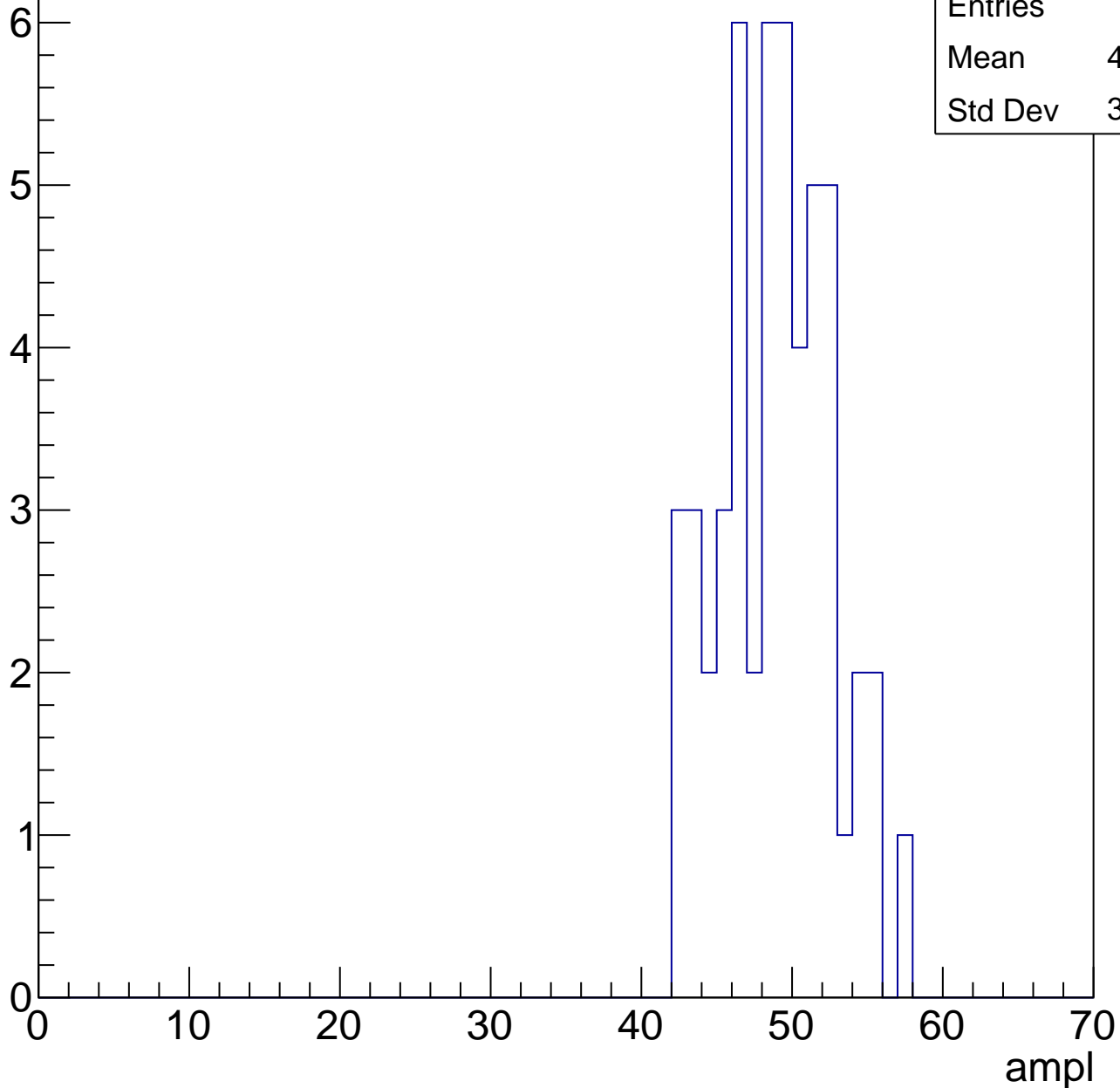
Gaus Width: 3.6265



B0L002S, U17-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



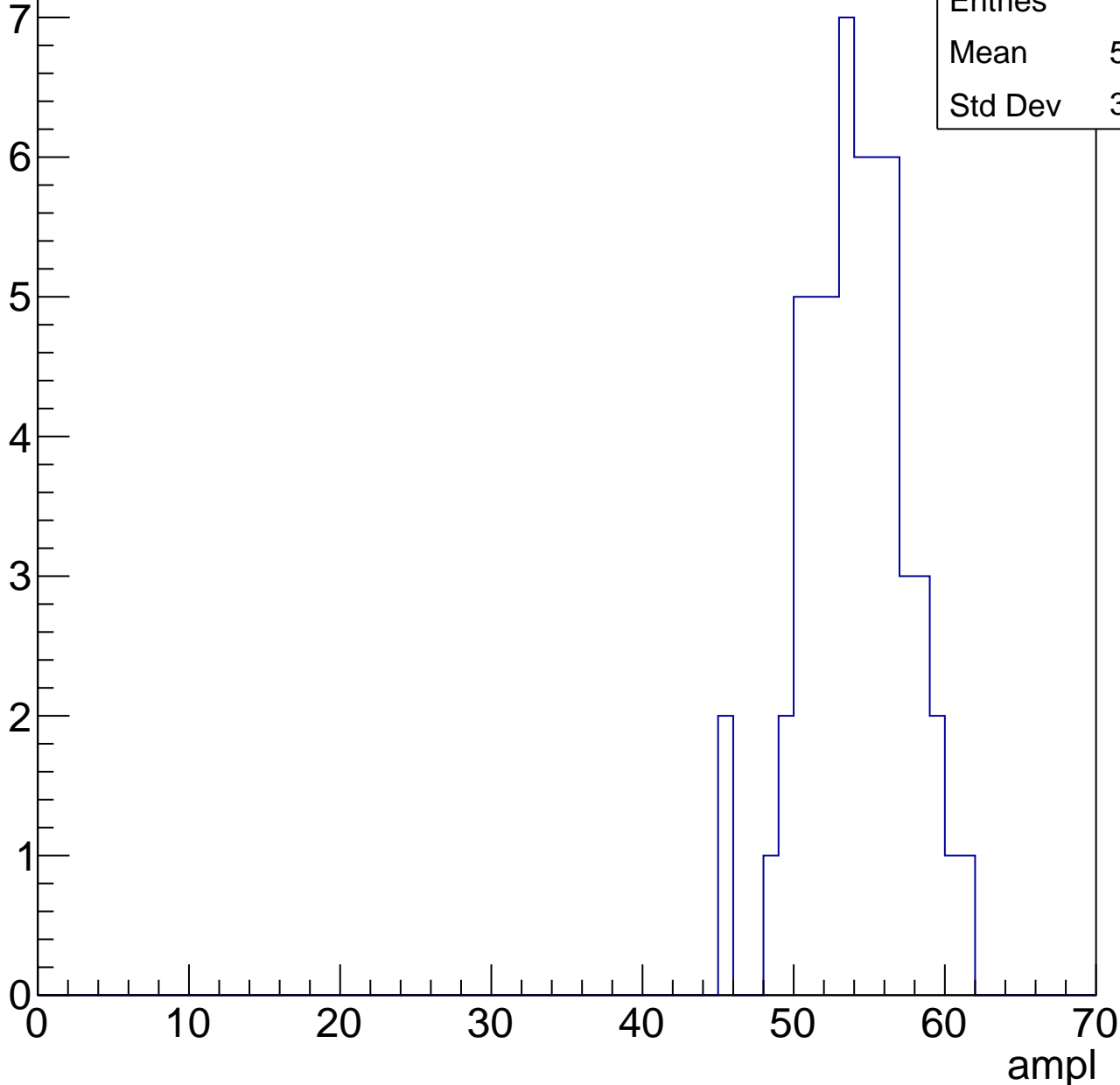
Entries	51
Mean	48.49
Std Dev	3.648

B0L002S, U17-ch9, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

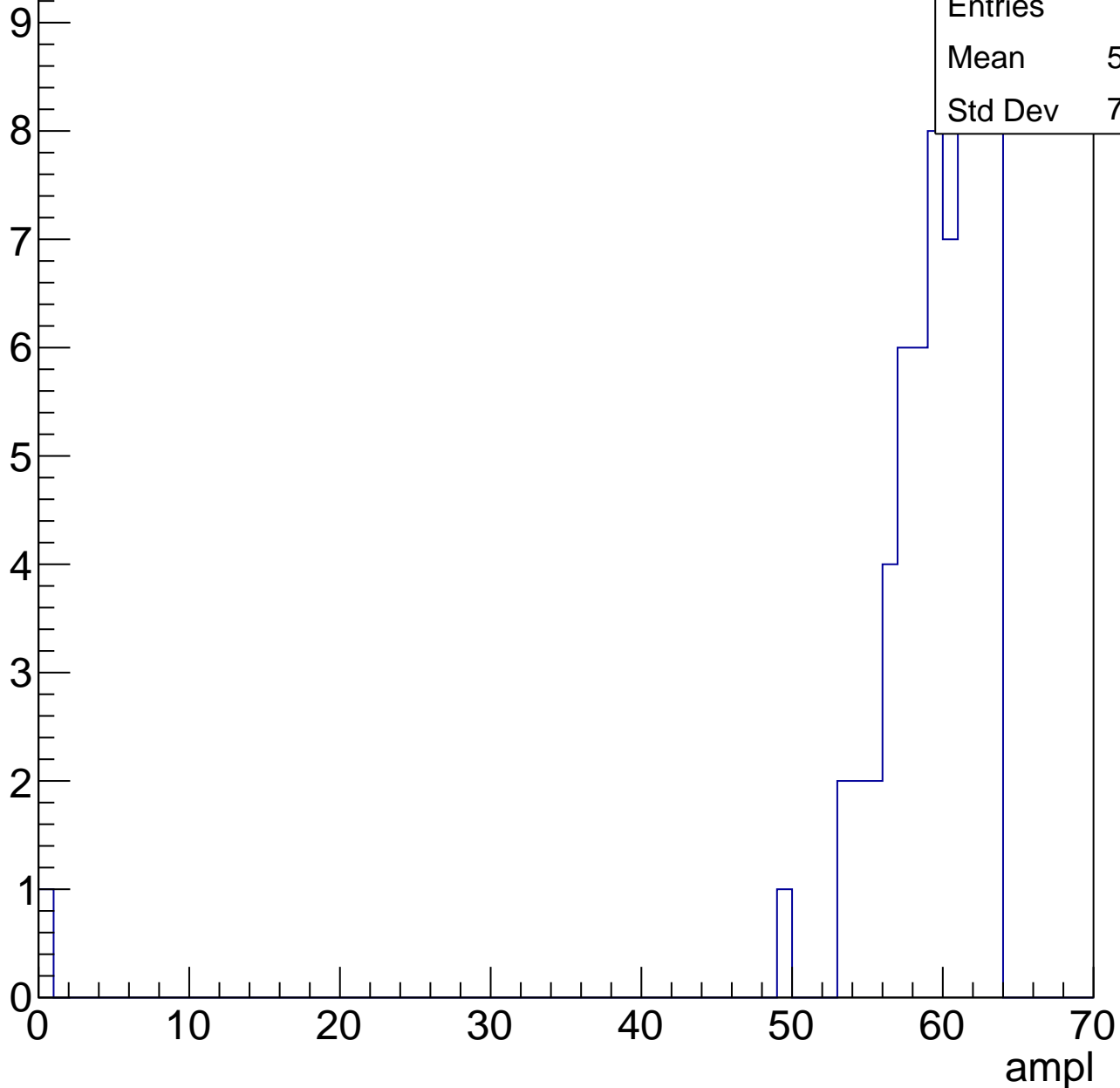
Entries	55
Mean	53.56
Std Dev	3.383



B0L002S, U17-ch9, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

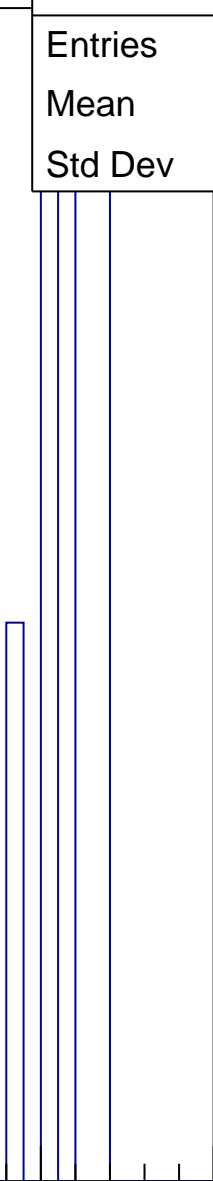
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.14
Std Dev	1.726

0 10 20 30 40 50 60 70

ampl



B0L002S, U17-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch10, adc0

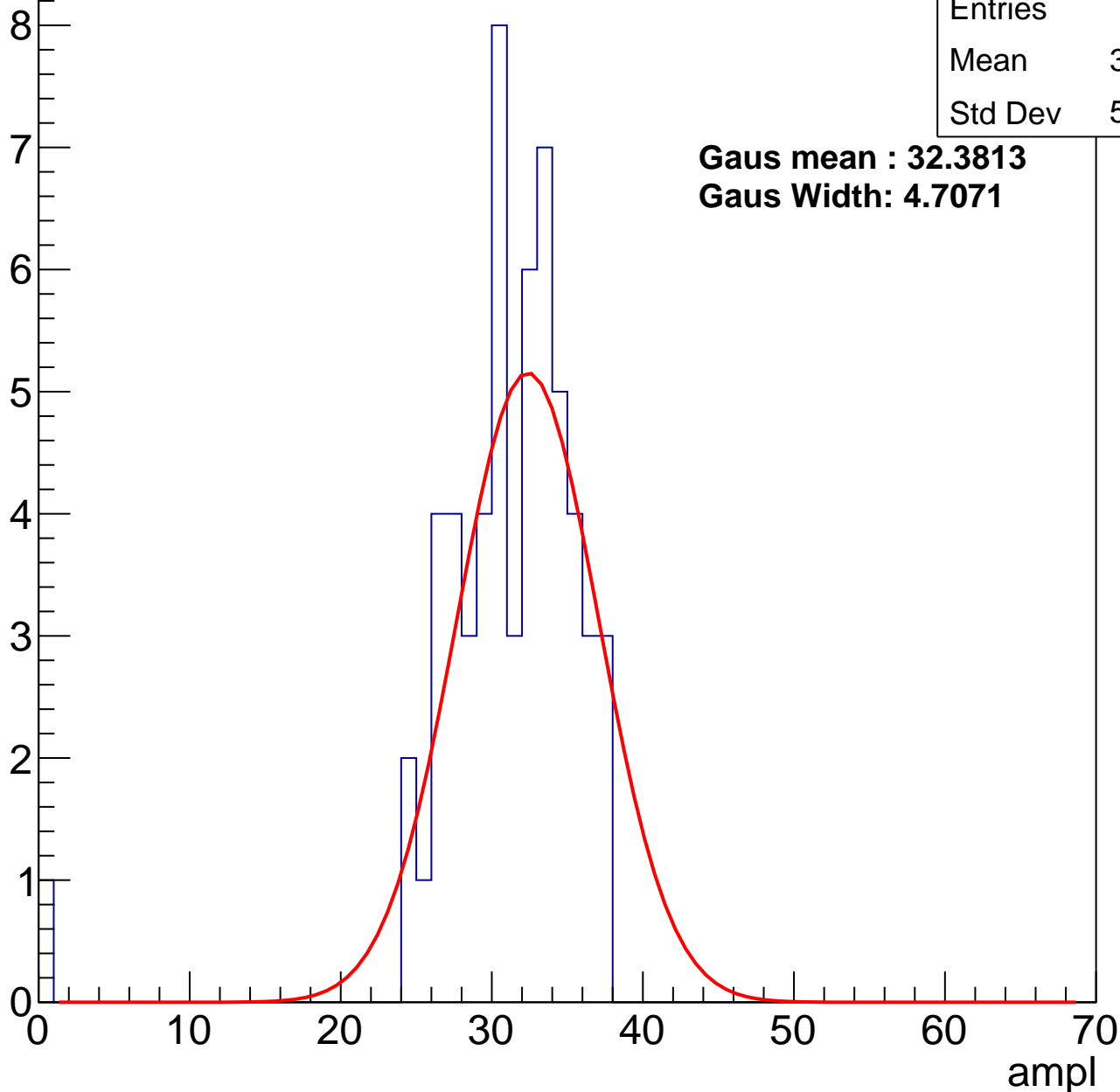
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	30.52
Std Dev	5.289

Gaus mean : 32.3813

Gaus Width: 4.7071



B0L002S, U17-ch10, adc1

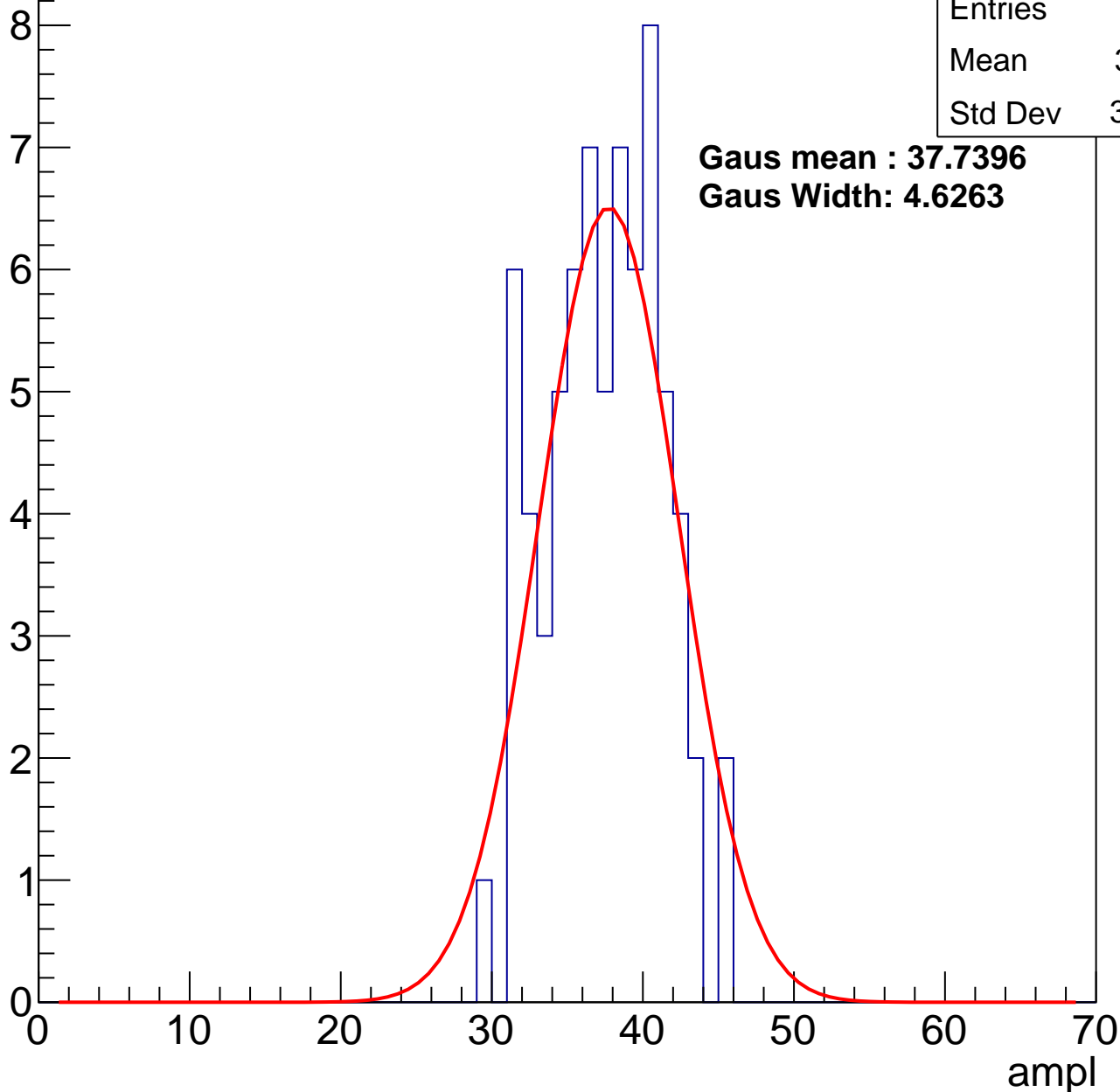
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.01
Std Dev	3.728

Gaus mean : 37.7396

Gaus Width: 4.6263



B0L002S, U17-ch10, adc2

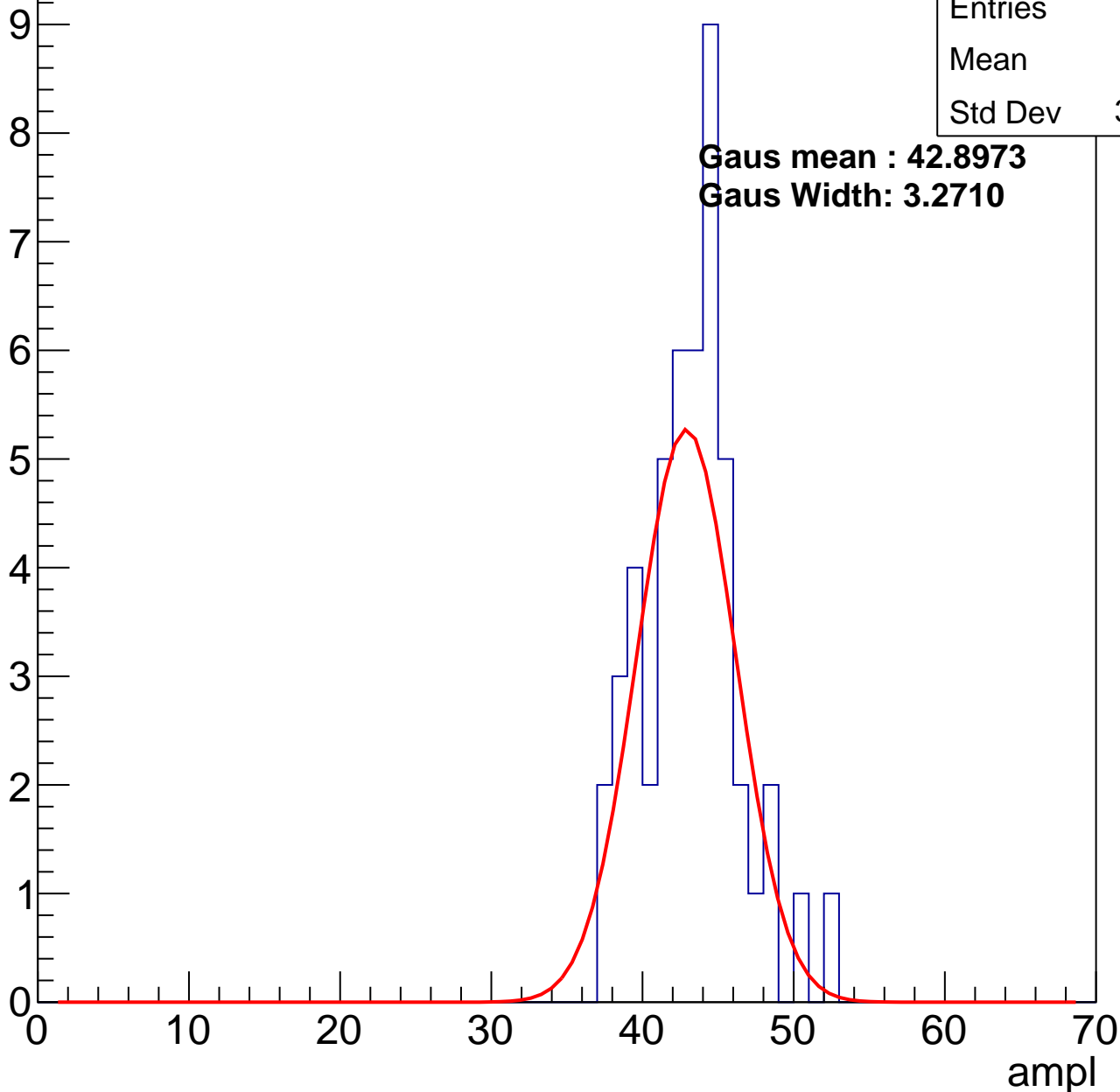
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	42.8
Std Dev	3.181

Gaus mean : 42.8973

Gaus Width: 3.2710

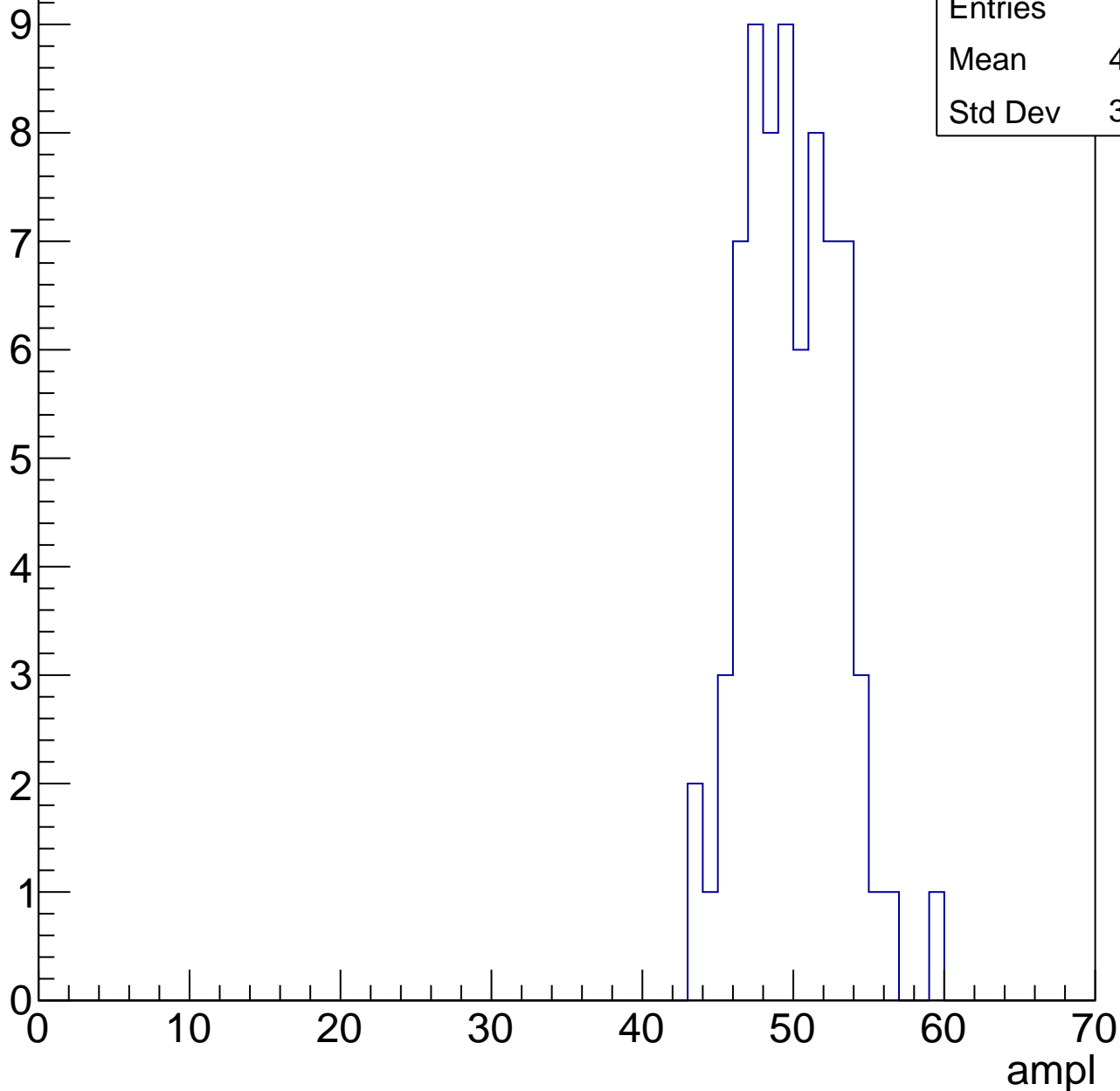


B0L002S, U17-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	49.45
Std Dev	3.119

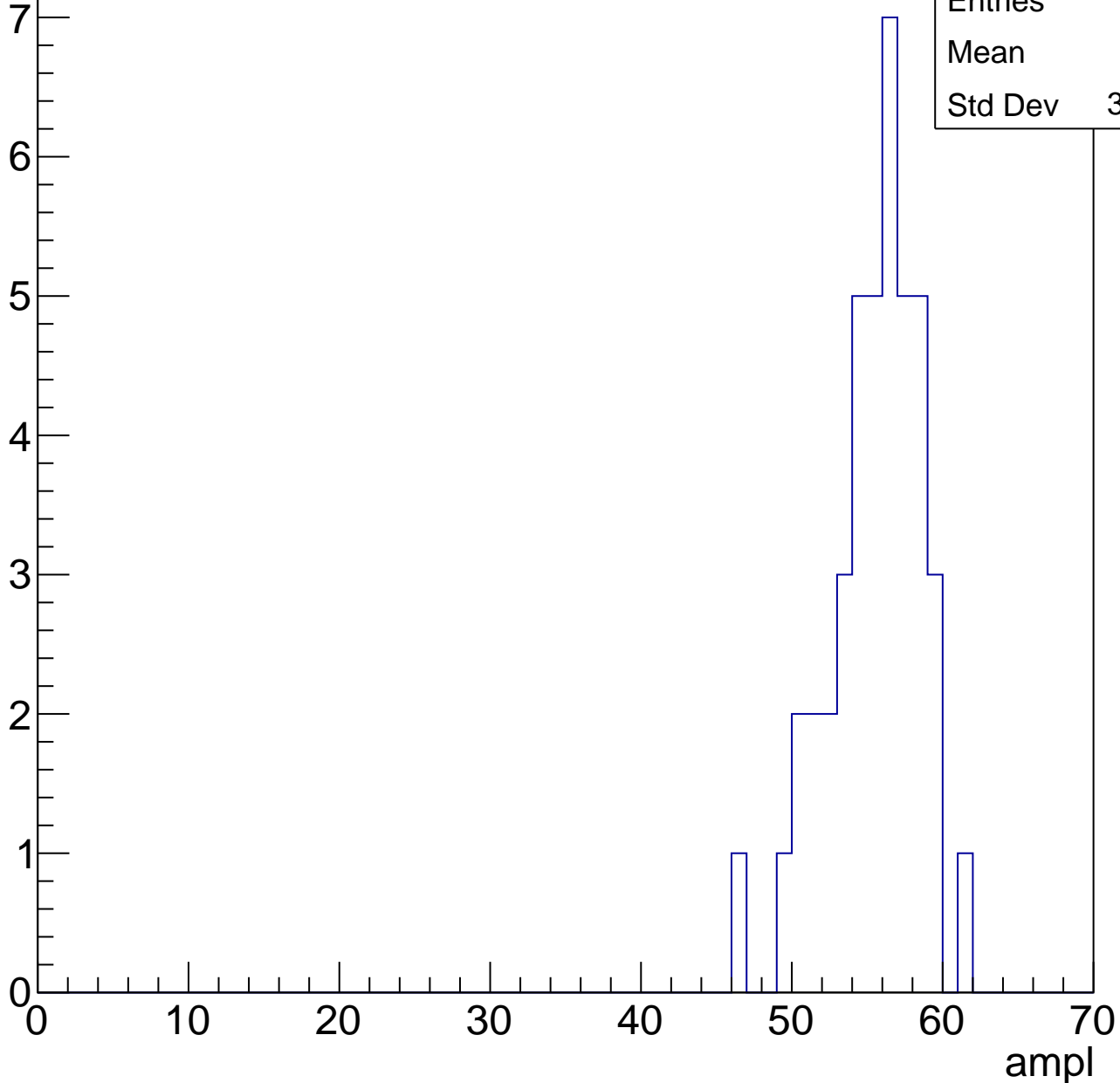


B0L002S, U17-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

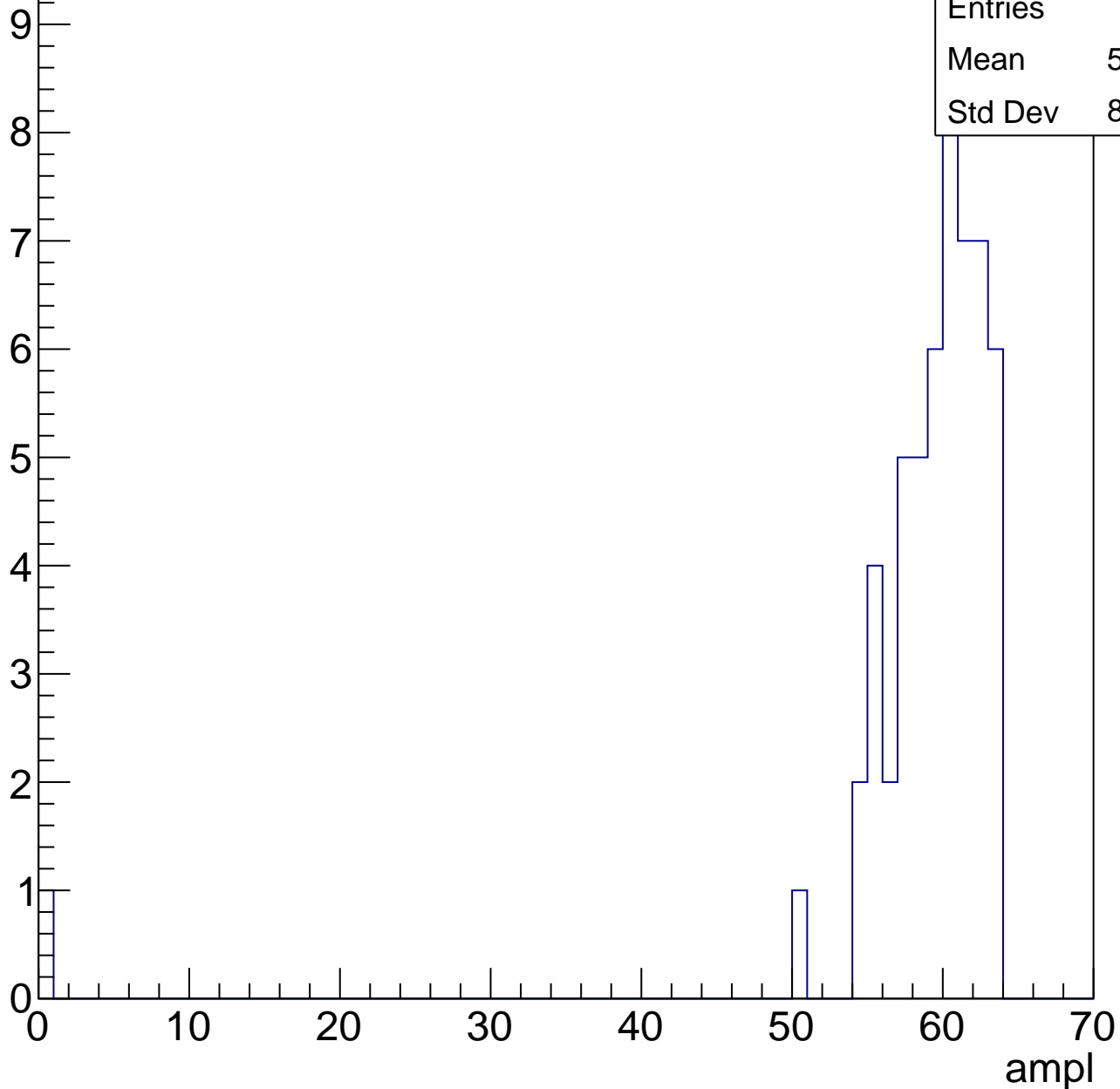
Entries	42
Mean	55
Std Dev	3.047



B0L002S, U17-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

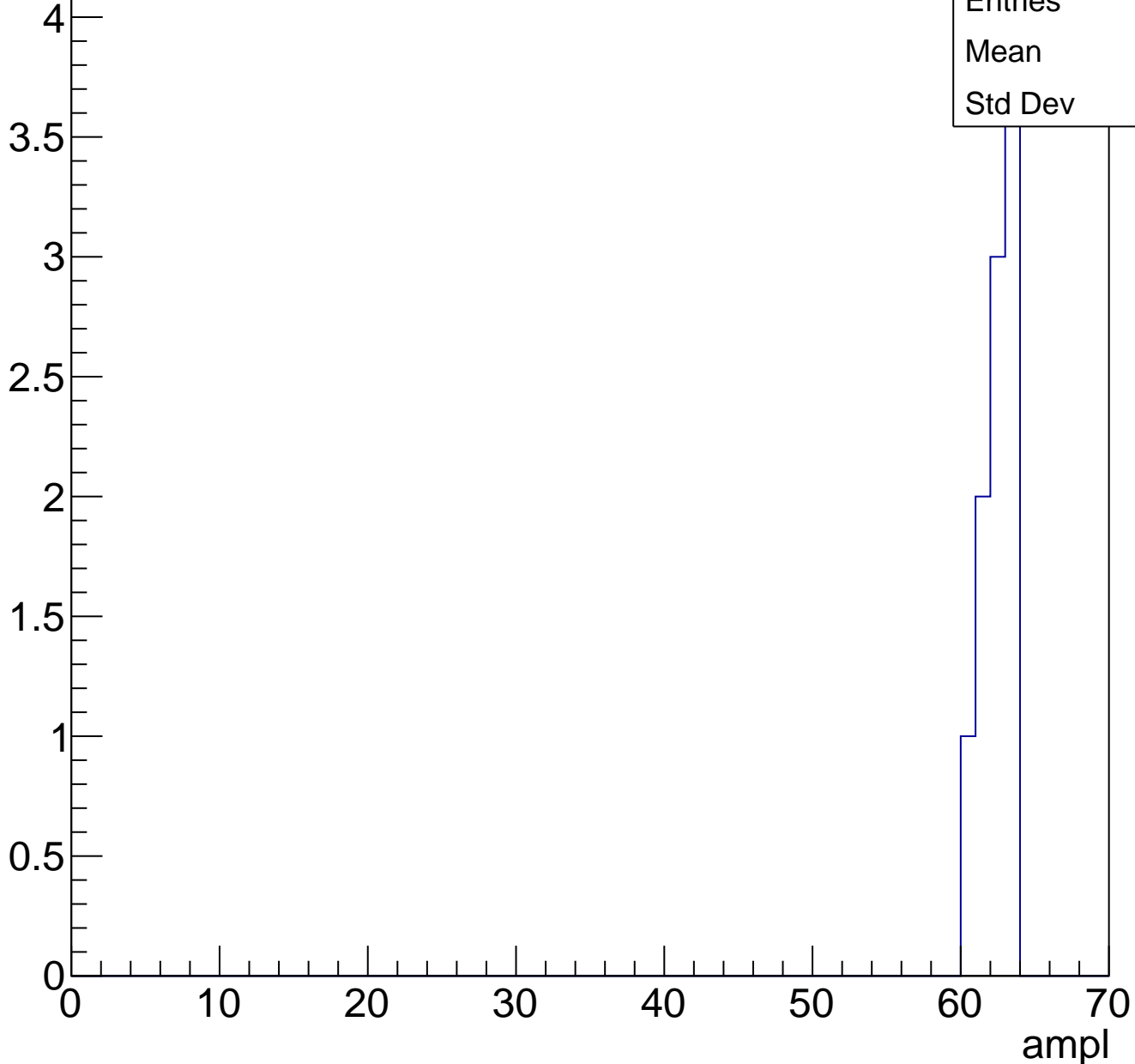
Entry



B0L002S, U17-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch11, adc0

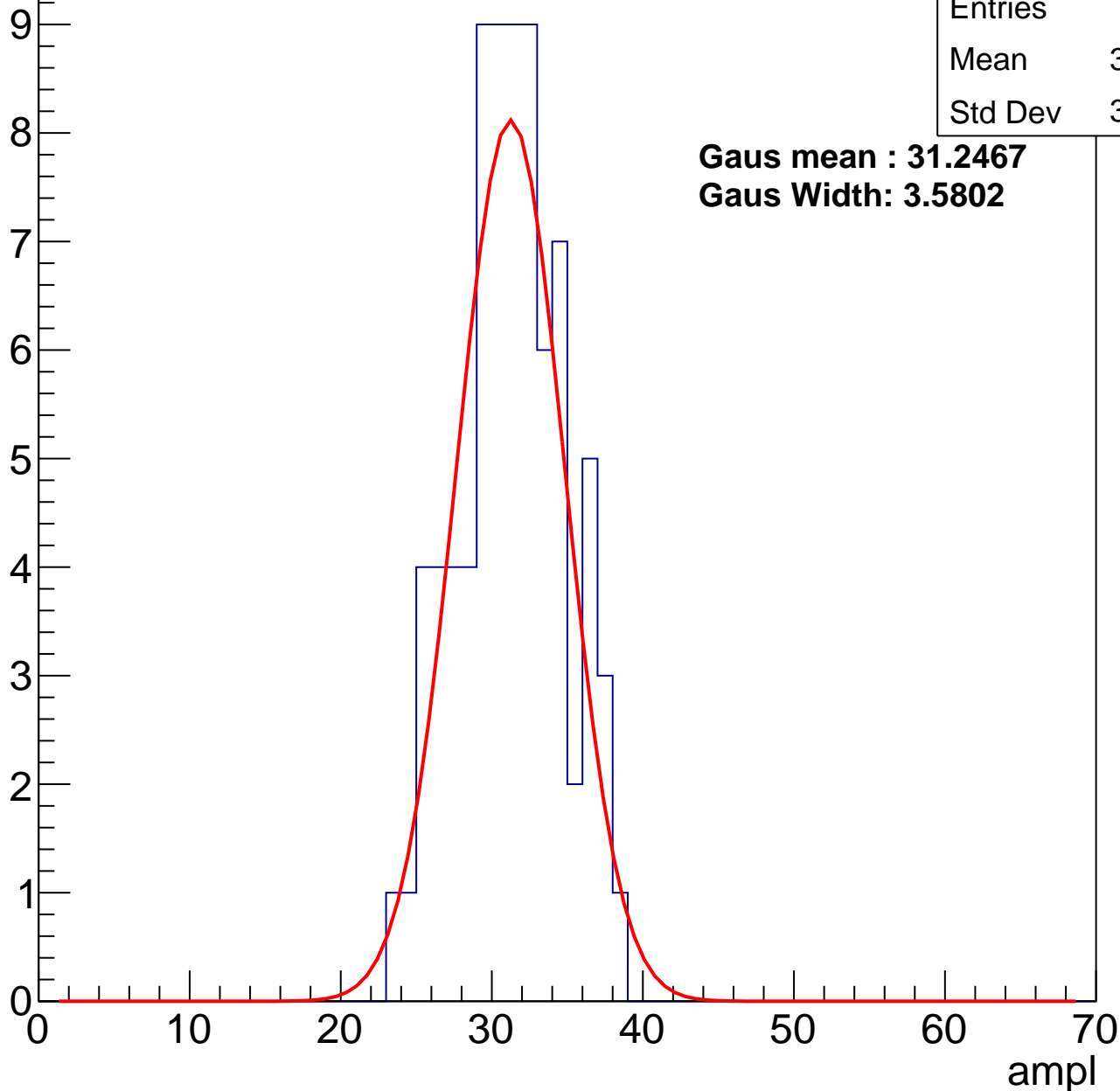
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	30.82
Std Dev	3.415

Gaus mean : 31.2467

Gaus Width: 3.5802



B0L002S, U17-ch11, adc1

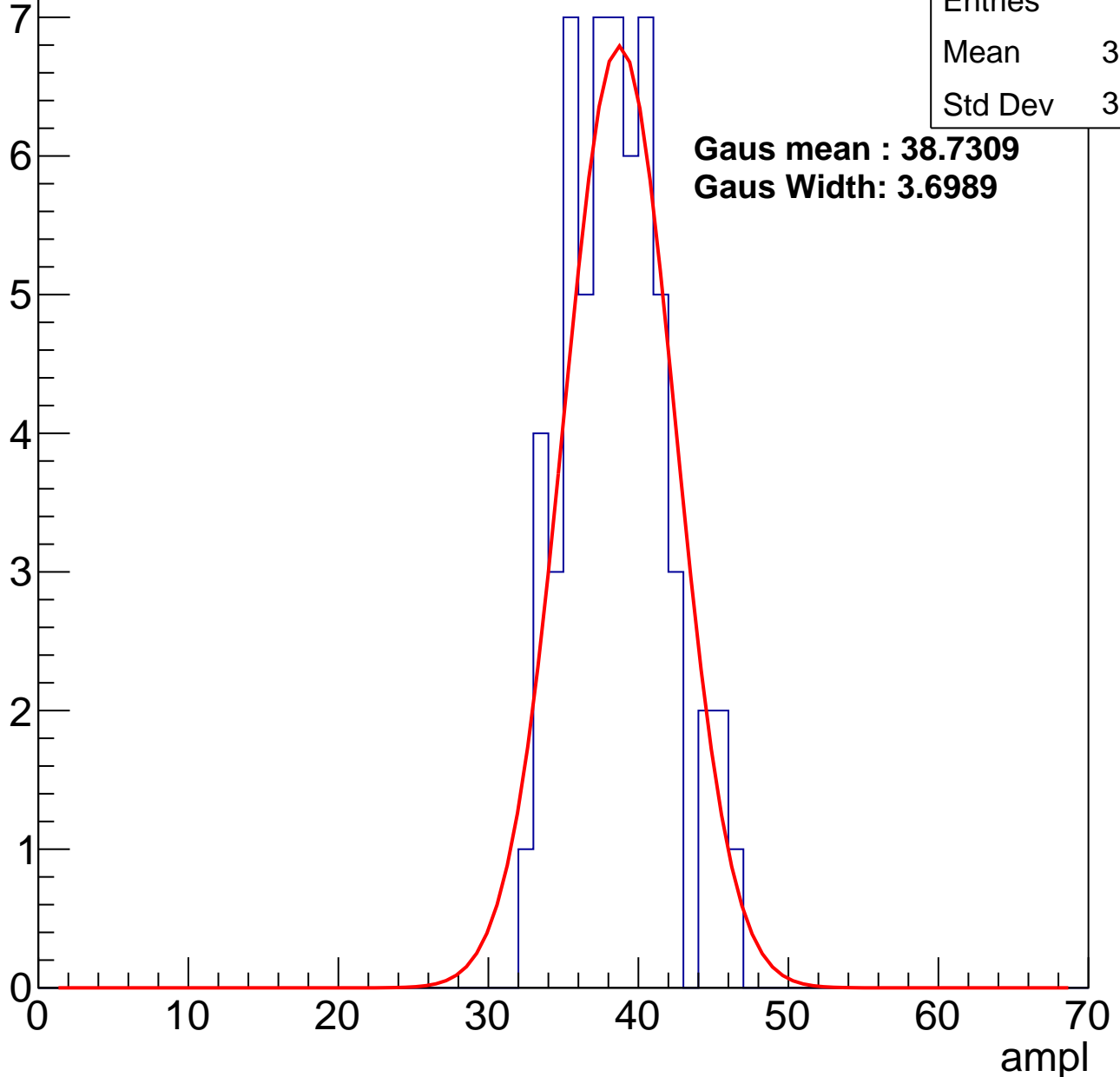
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	38.08
Std Dev	3.252

Gaus mean : 38.7309

Gaus Width: 3.6989



B0L002S, U17-ch11, adc2

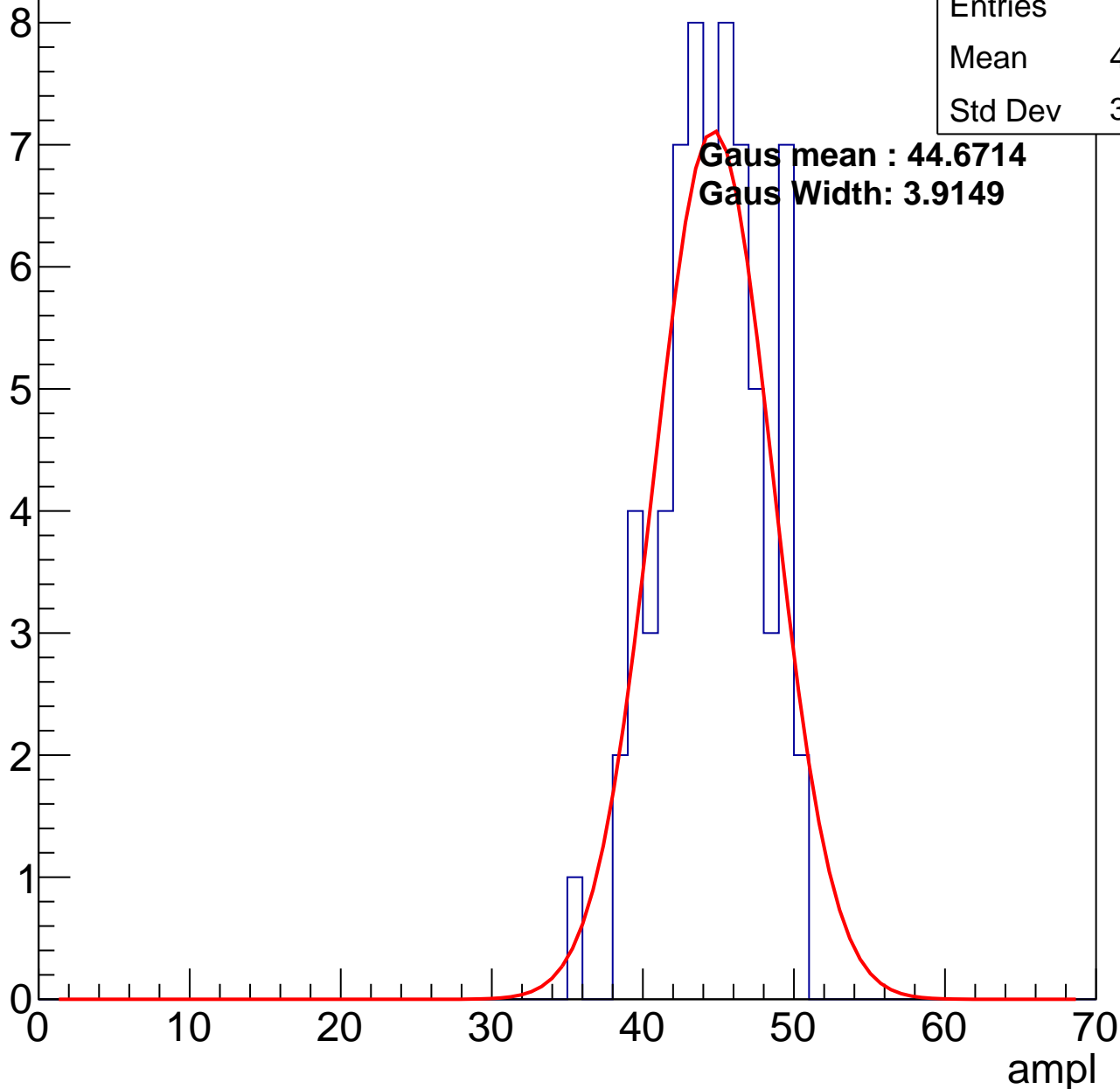
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	44.13
Std Dev	3.316

Gaus mean : 44.6714

Gaus Width: 3.9149

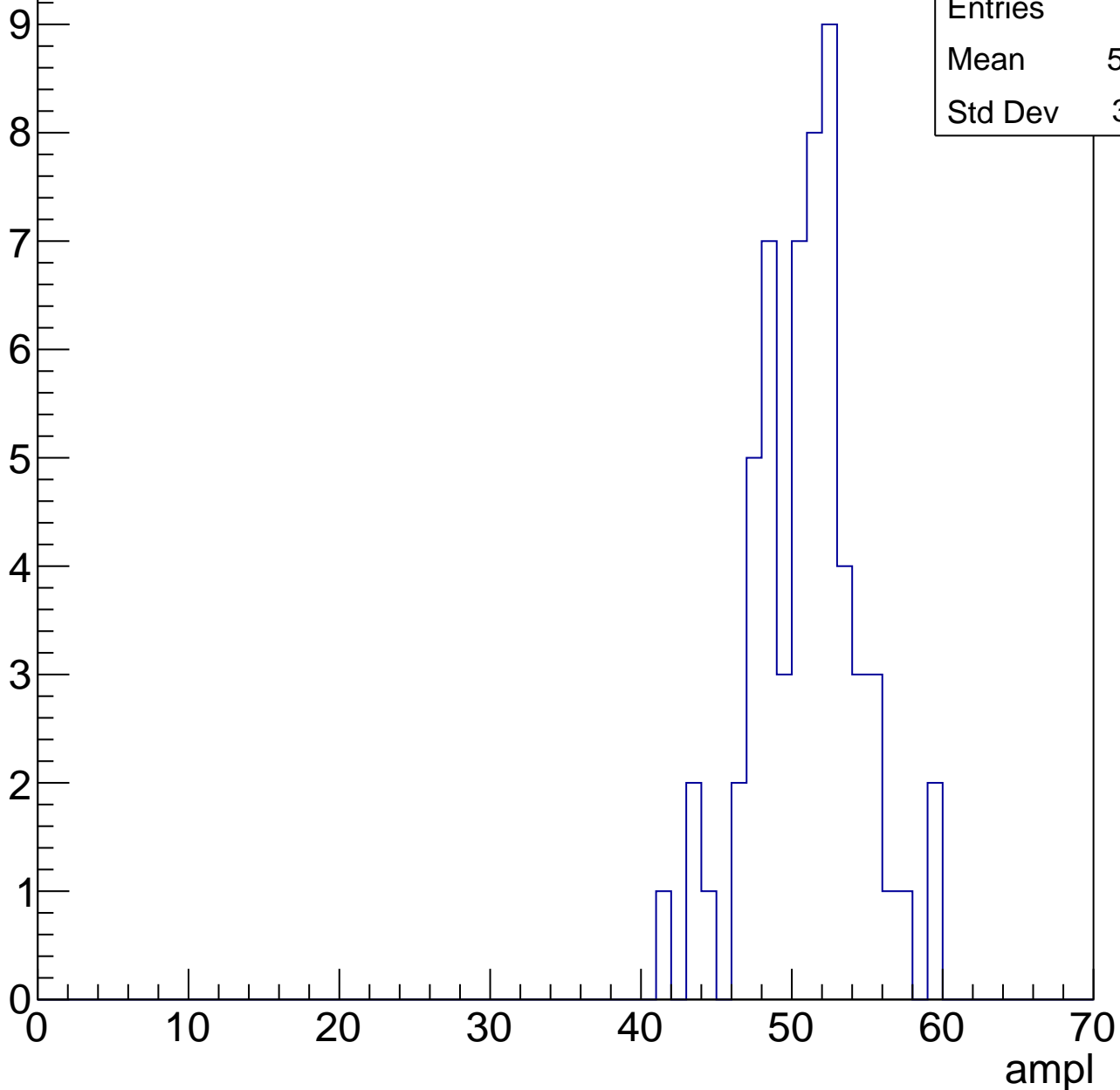


B0L002S, U17-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

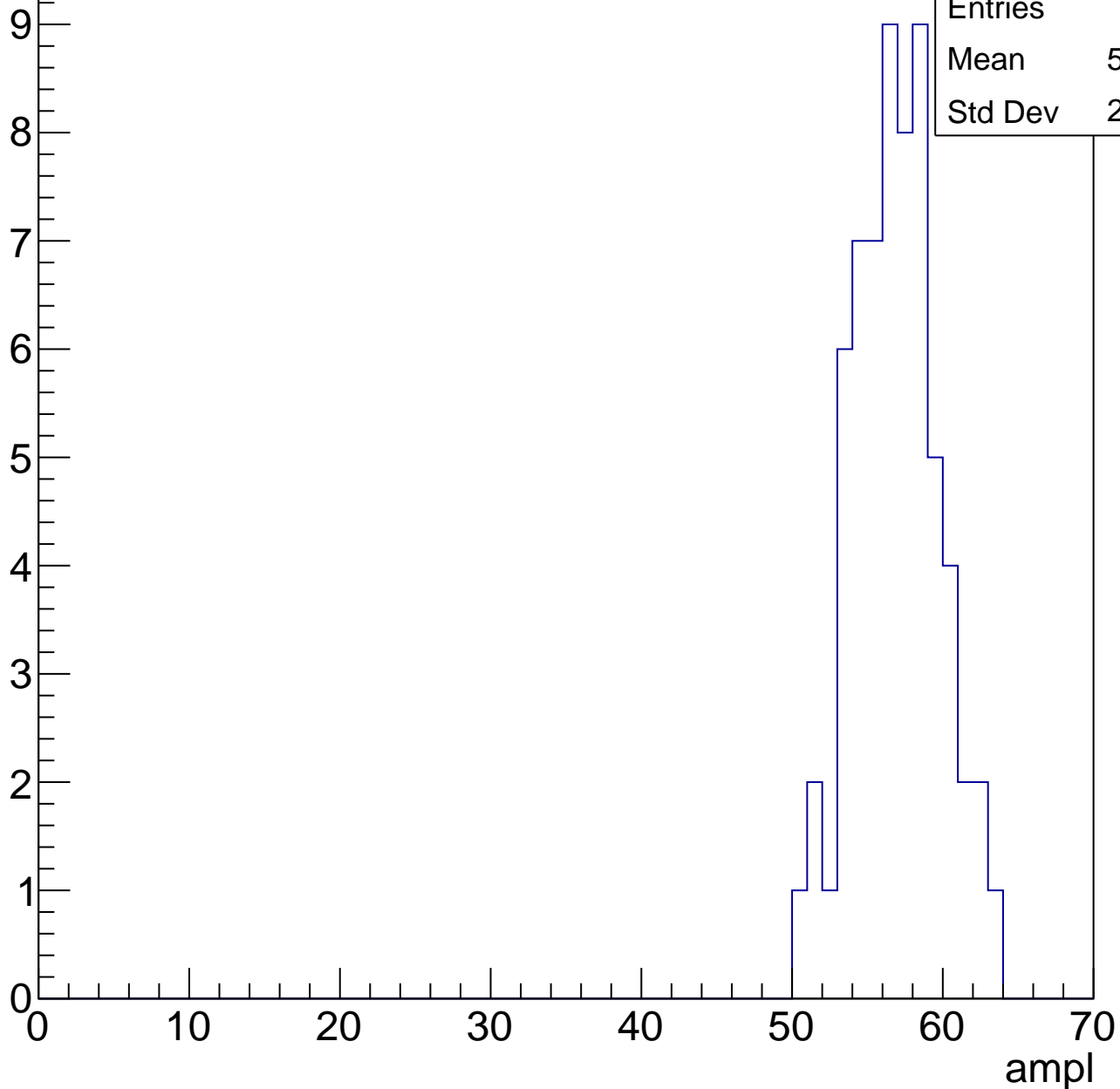
Entries	59
Mean	50.46
Std Dev	3.591



B0L002S, U17-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



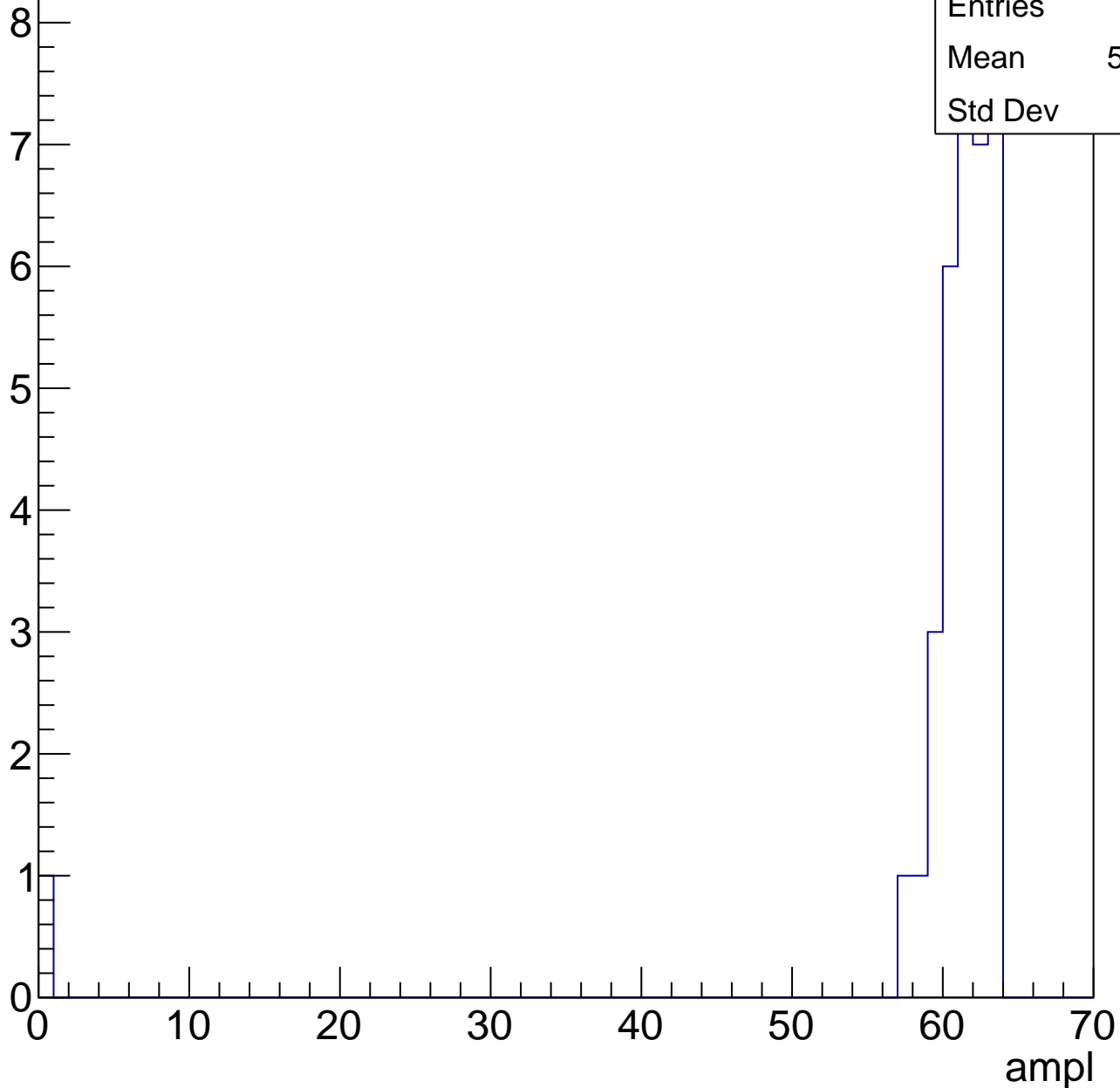
Entries	64
Mean	56.42
Std Dev	2.805

B0L002S, U17-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.37
Std Dev	10.3



B0L002S, U17-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch12, adc0

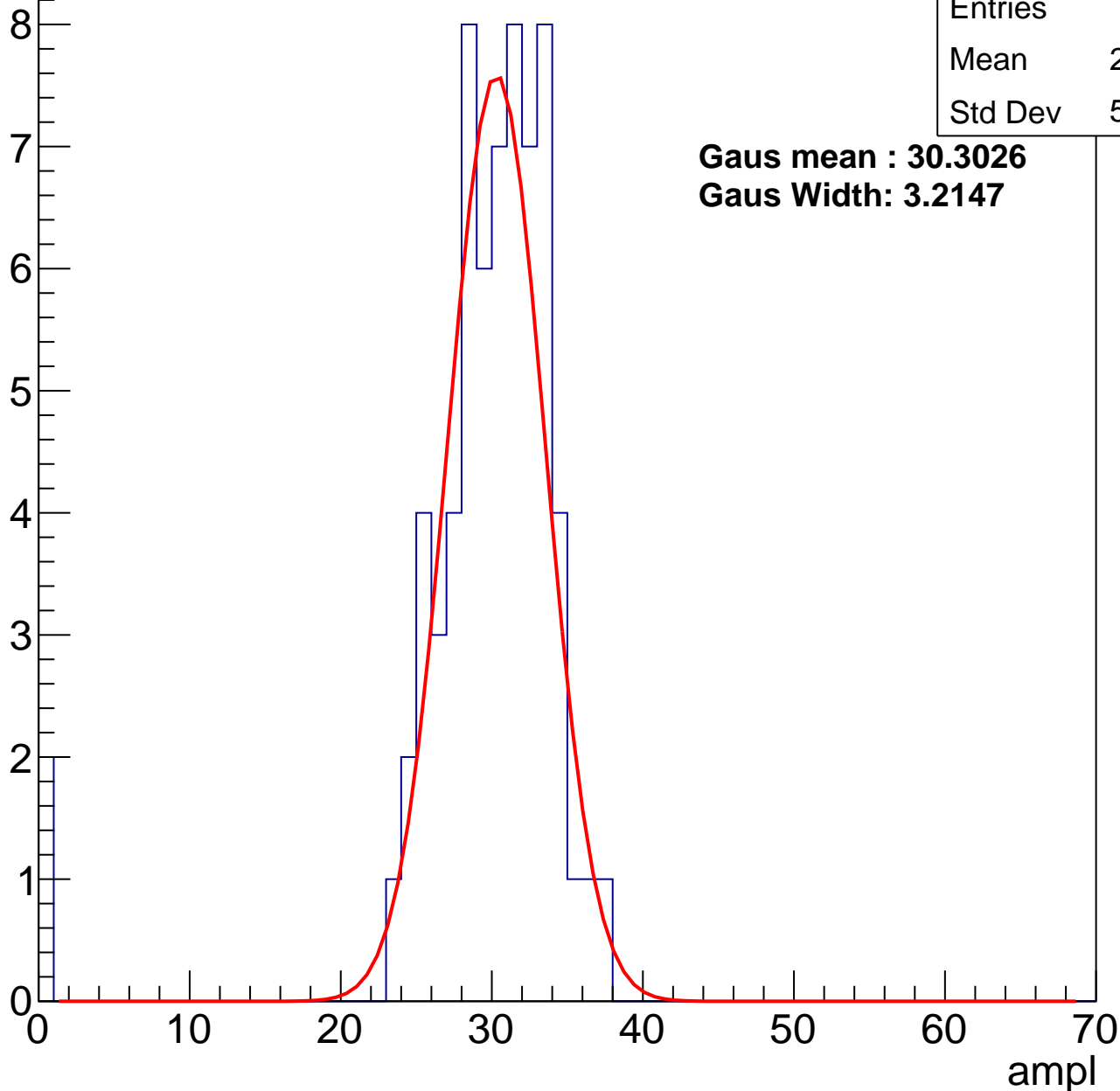
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.03
Std Dev	5.937

Gaus mean : 30.3026

Gaus Width: 3.2147



B0L002S, U17-ch12, adc1

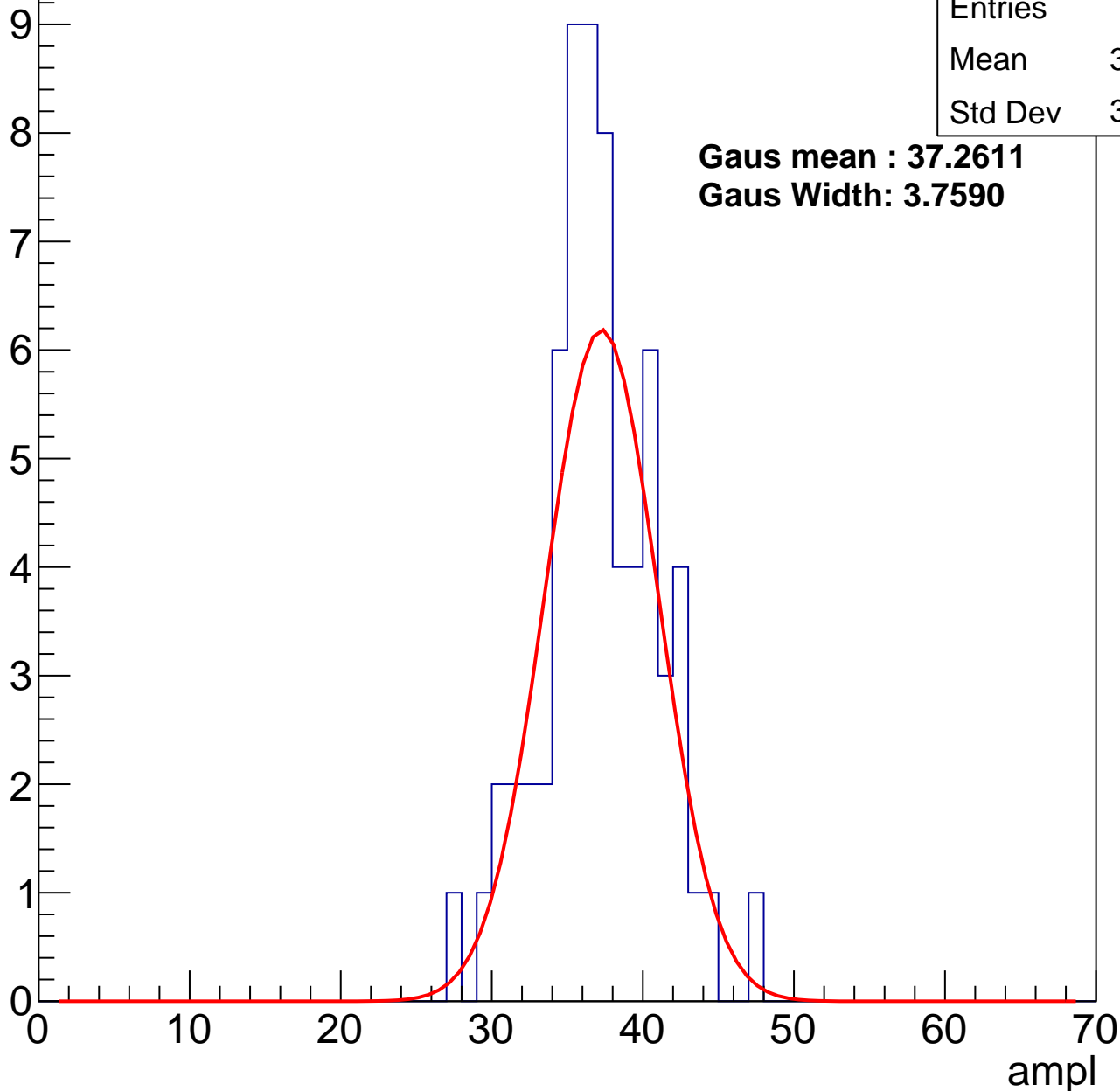
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.67
Std Dev	3.723

Gaus mean : 37.2611

Gaus Width: 3.7590



B0L002S, U17-ch12, adc2

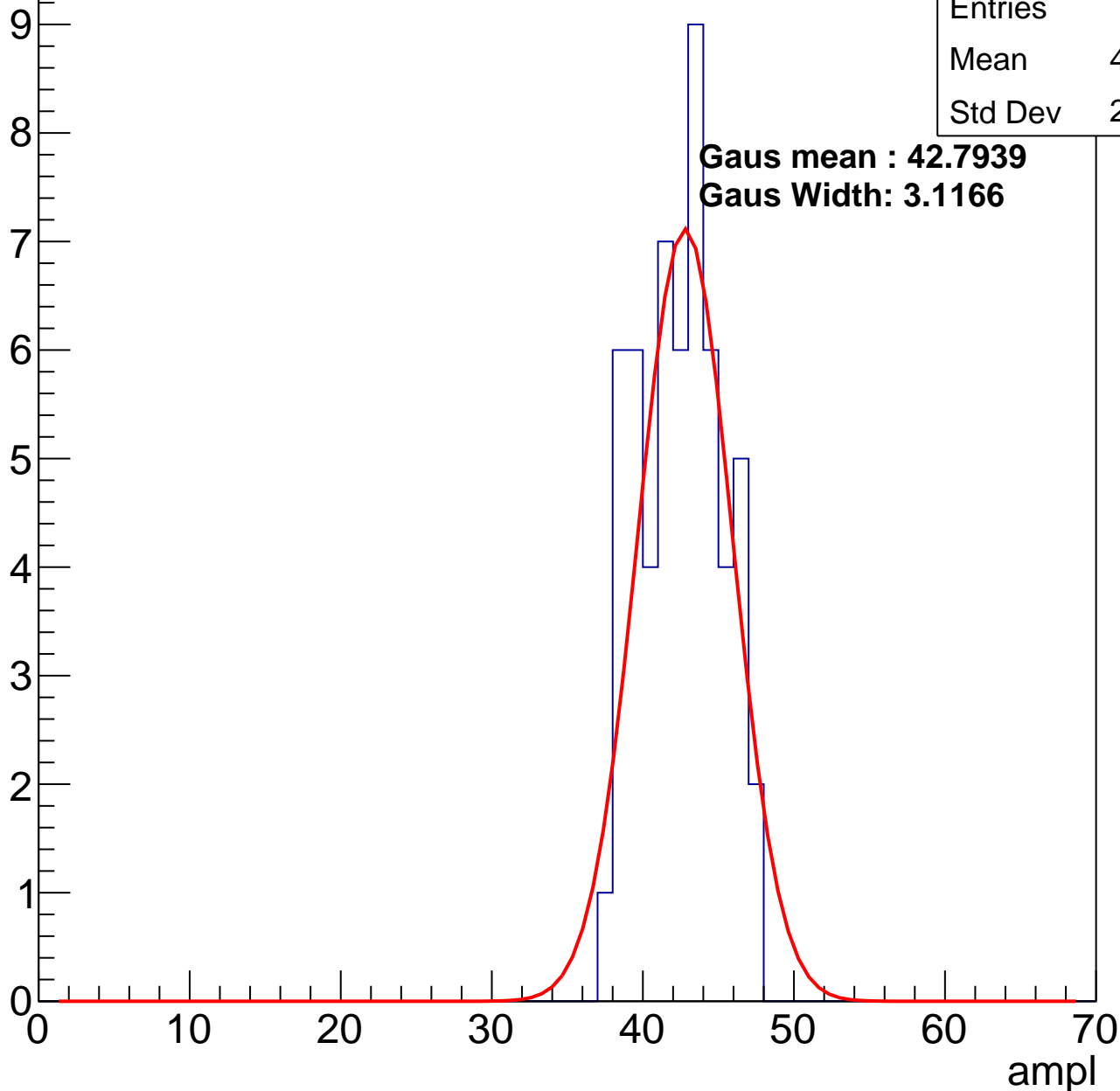
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	42.02
Std Dev	2.663

Gaus mean : 42.7939

Gaus Width: 3.1166



B0L002S, U17-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	49.28
Std Dev	3.073

Entry

10

8

6

4

2

0

0

10

20

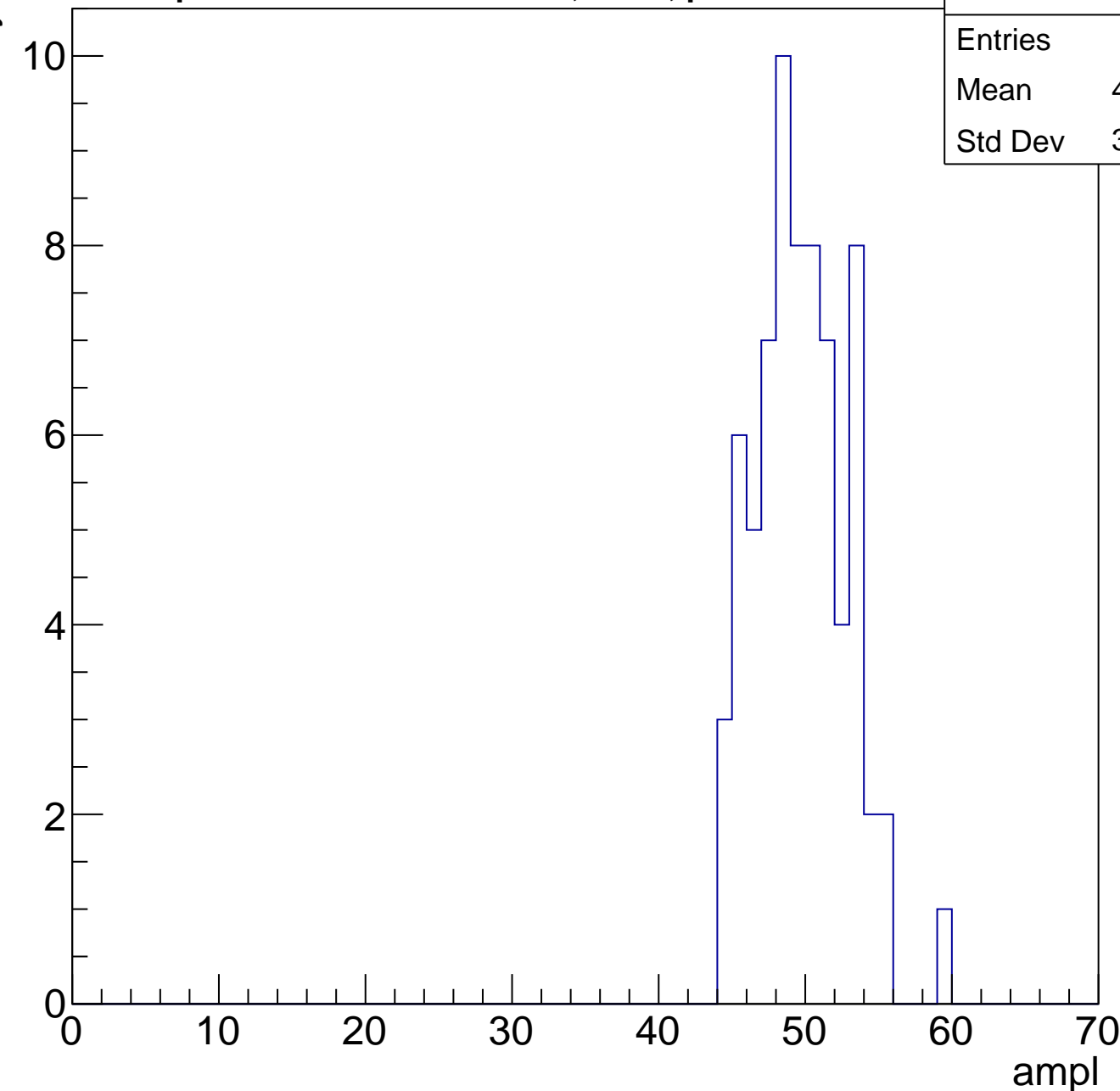
30

40

50

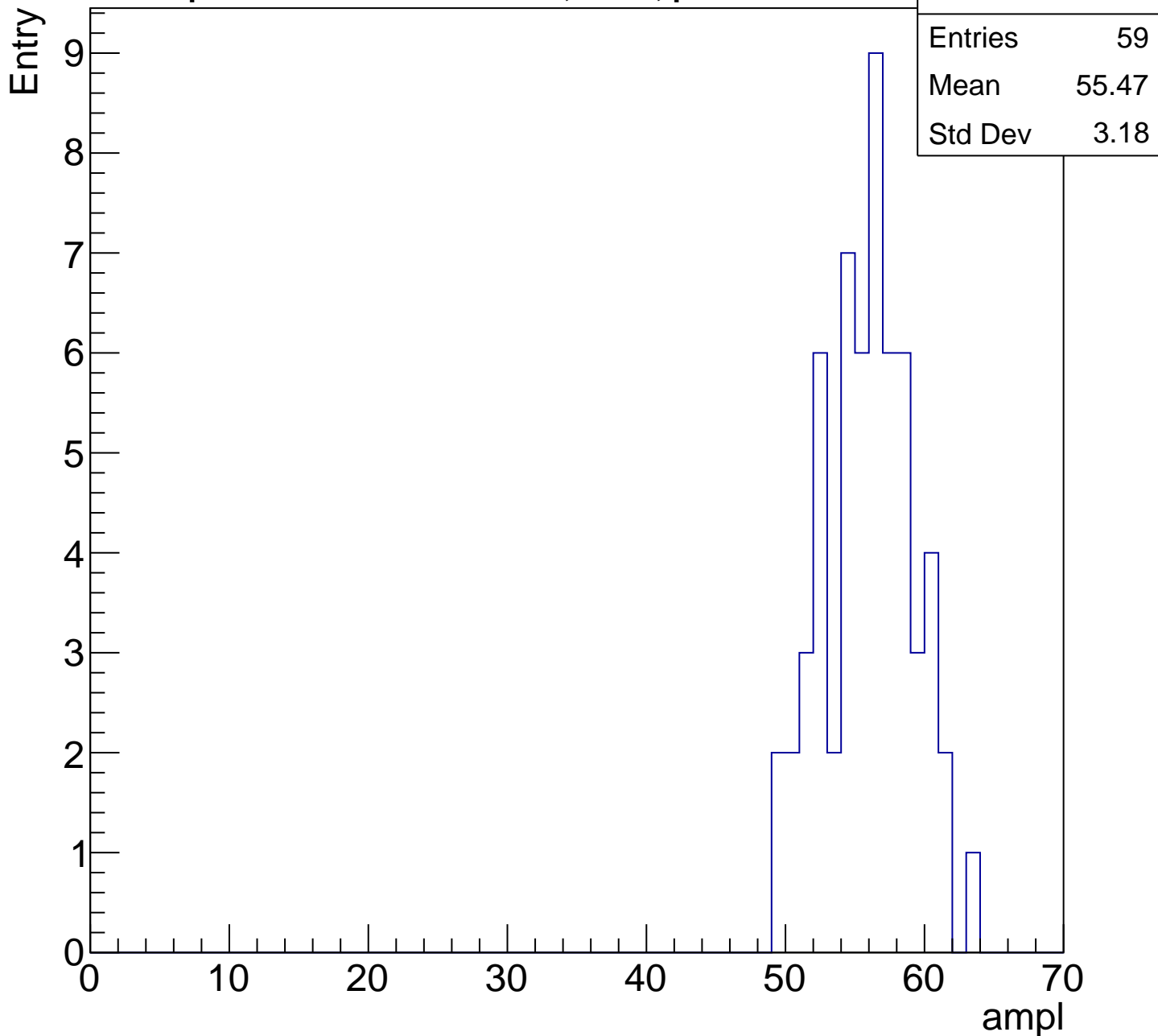
60

ampl



B0L002S, U17-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

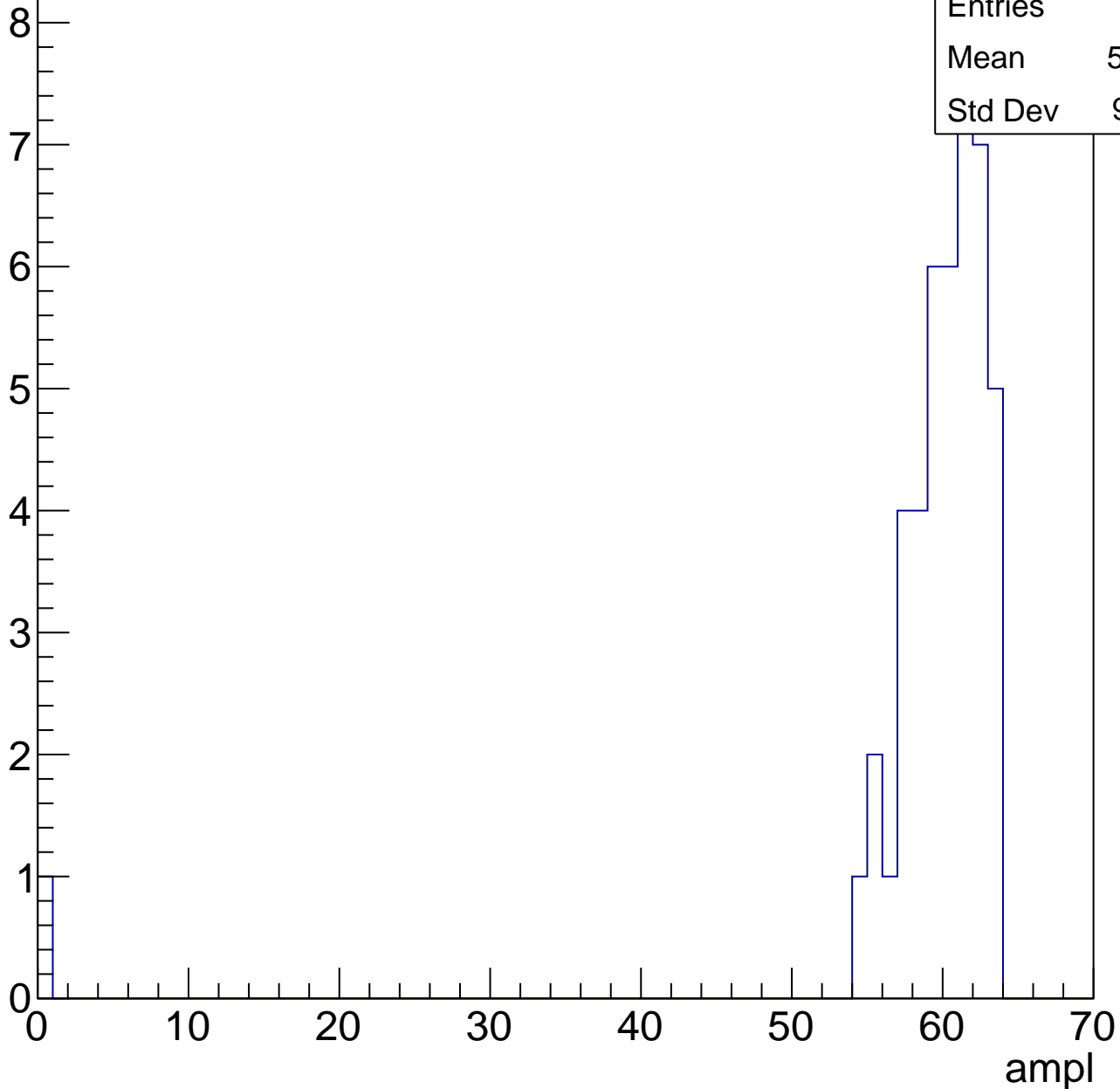


B0L002S, U17-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	58.47
Std Dev	9.111



B0L002S, U17-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch13, adc0

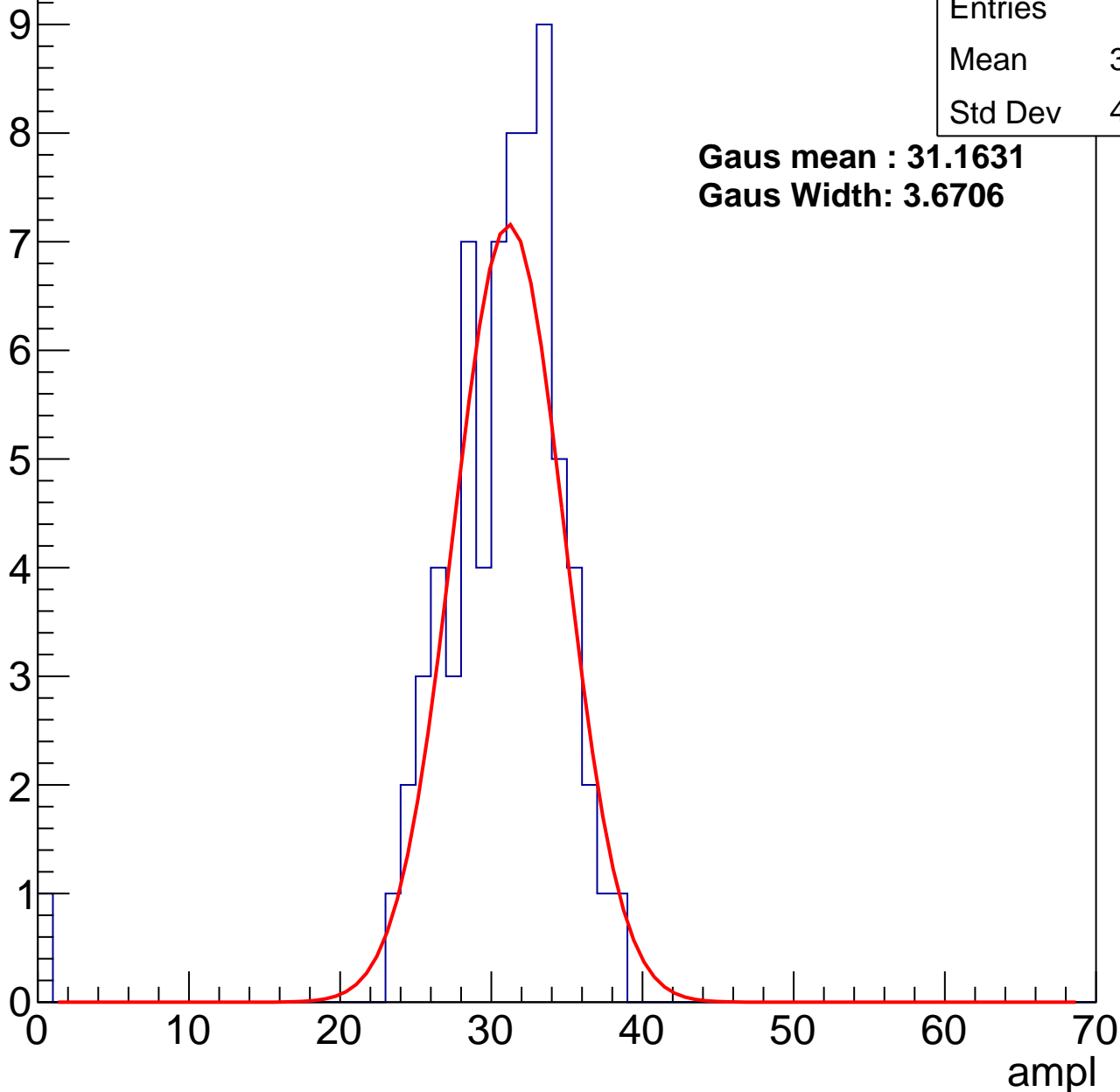
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.16
Std Dev	4.933

Gaus mean : 31.1631

Gaus Width: 3.6706



B0L002S, U17-ch13, adc1

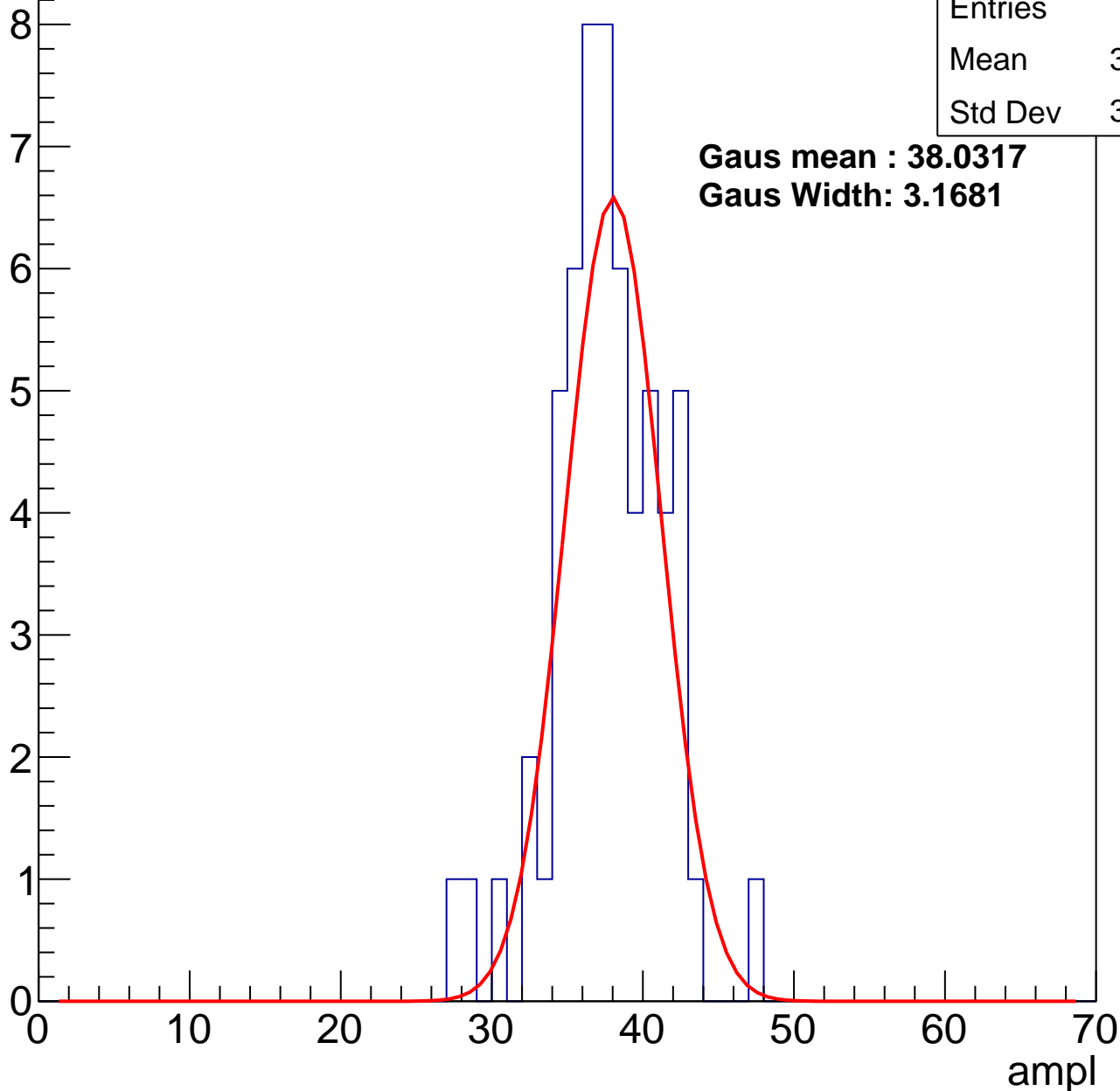
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	37.19
Std Dev	3.596

Gaus mean : 38.0317

Gaus Width: 3.1681



B0L002S, U17-ch13, adc2

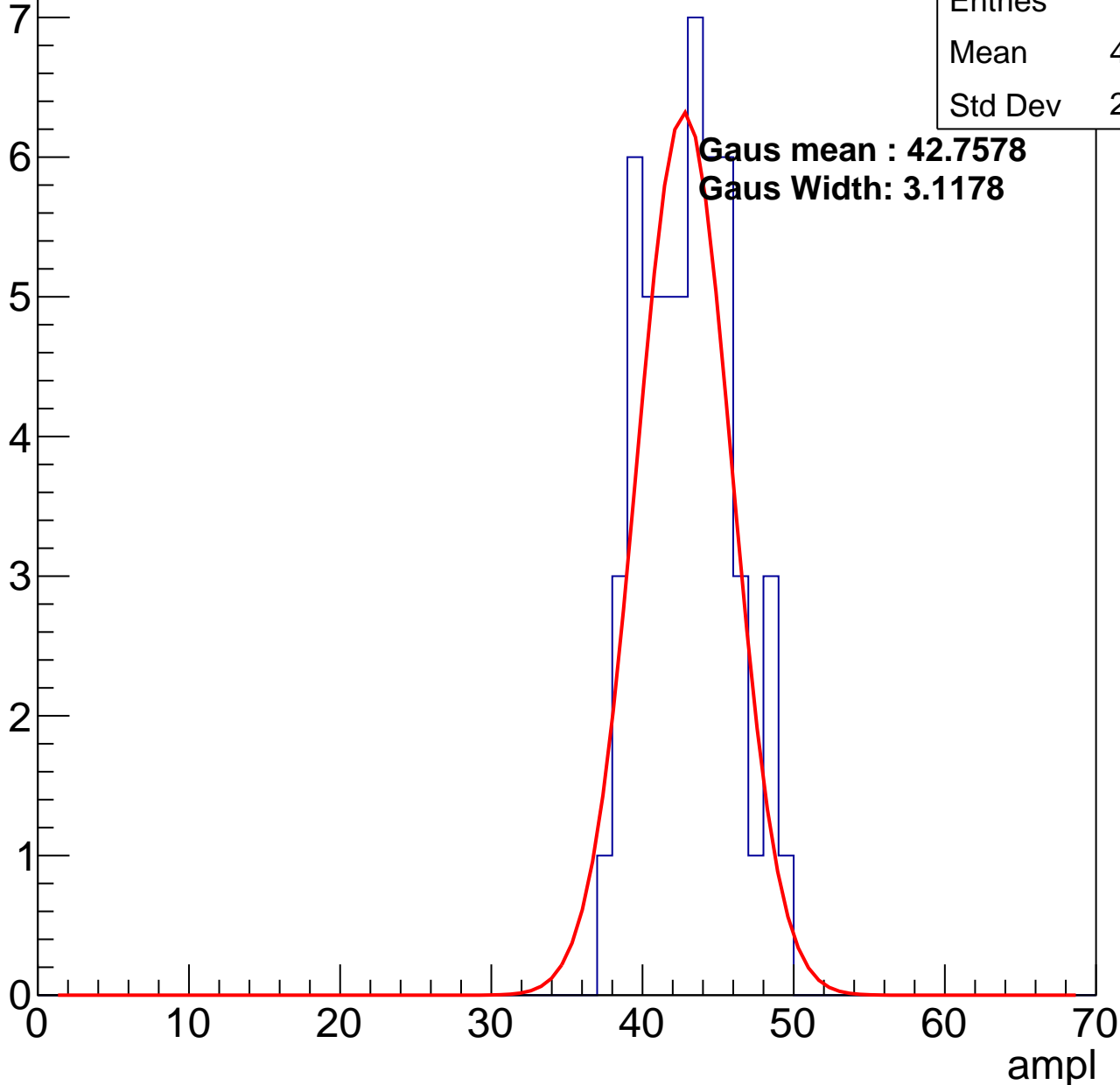
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	42.56
Std Dev	2.944

Gaus mean : 42.7578

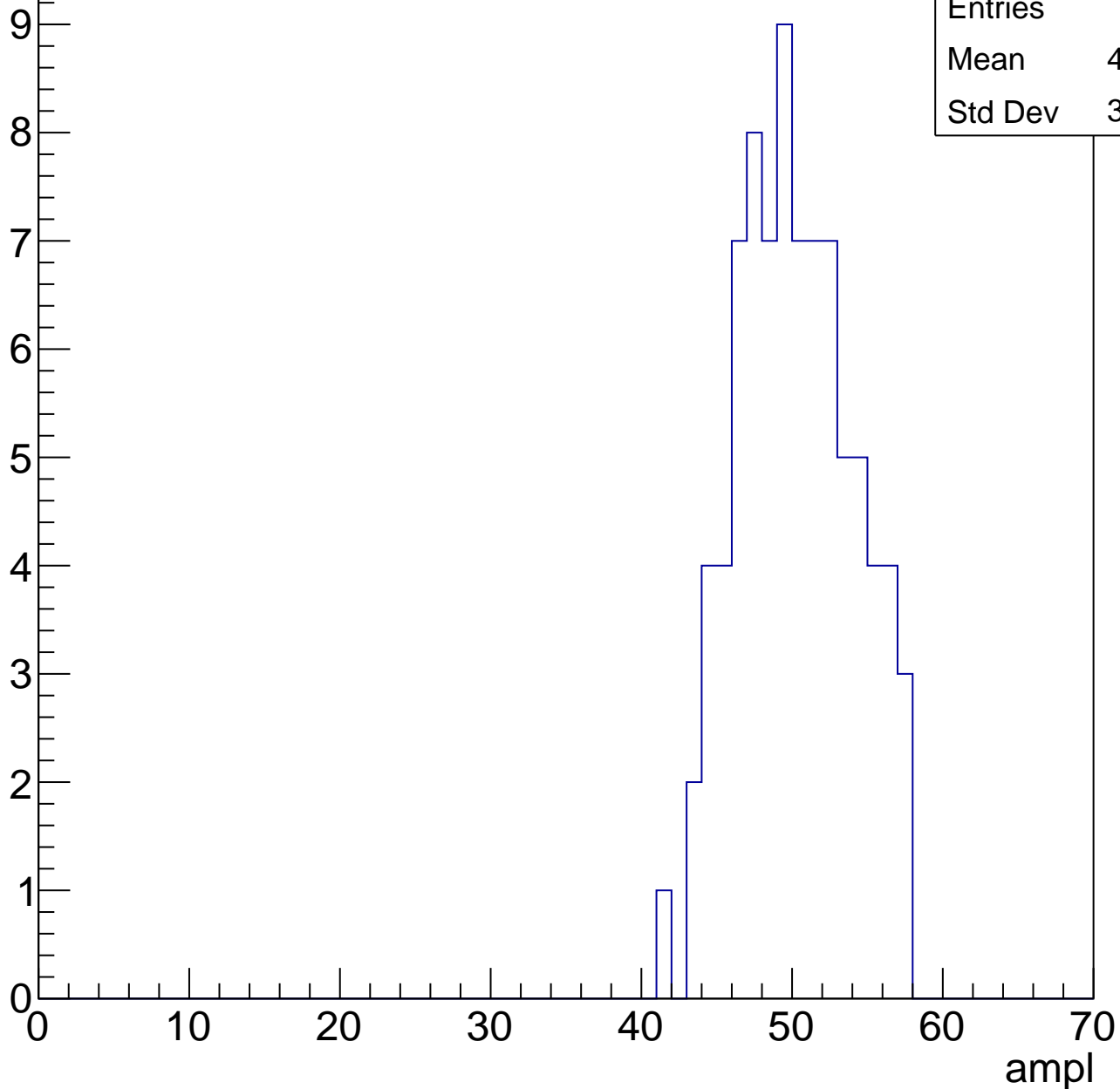
Gaus Width: 3.1178



B0L002S, U17-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

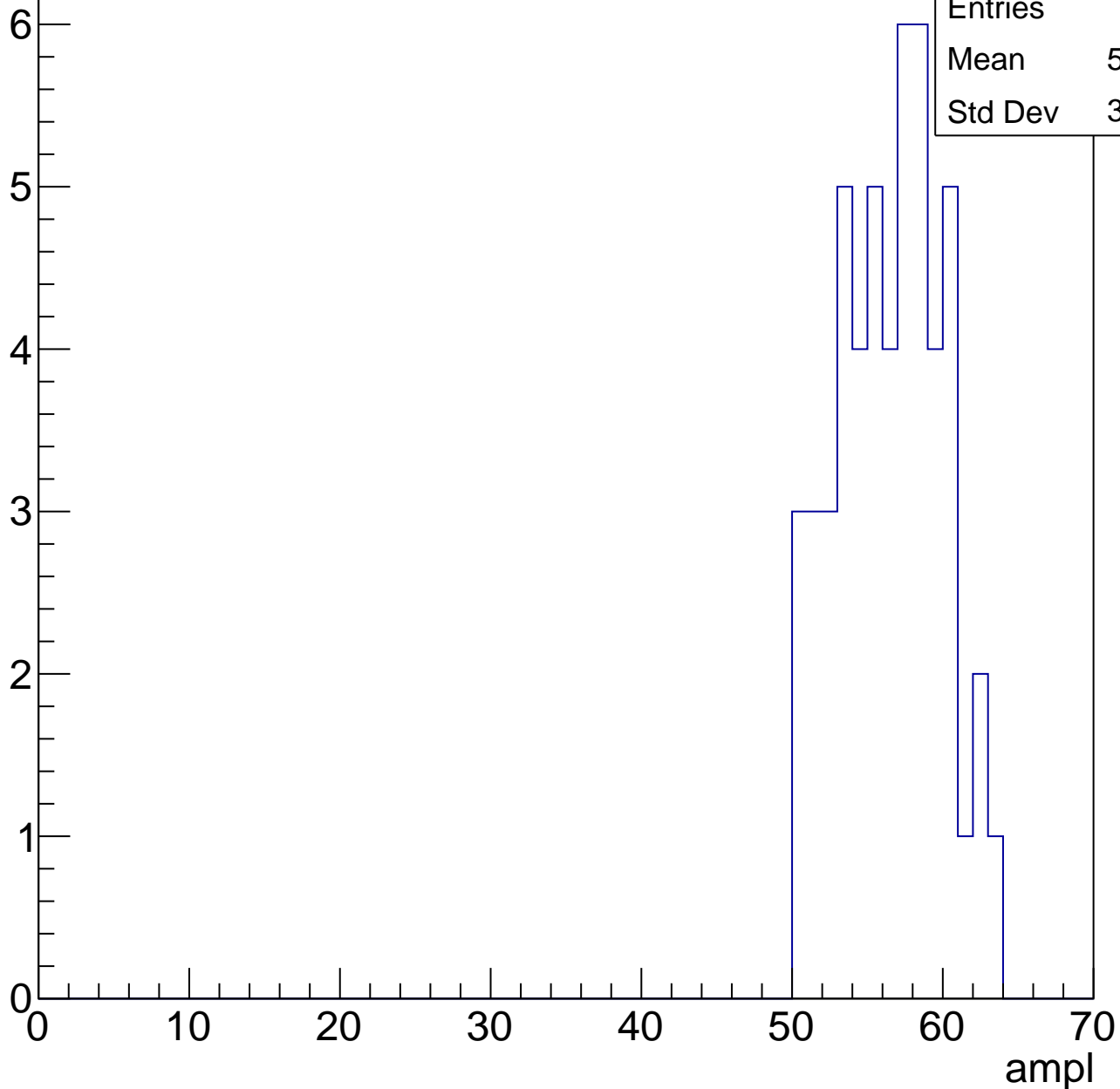


B0L002S, U17-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.02
Std Dev	3.365

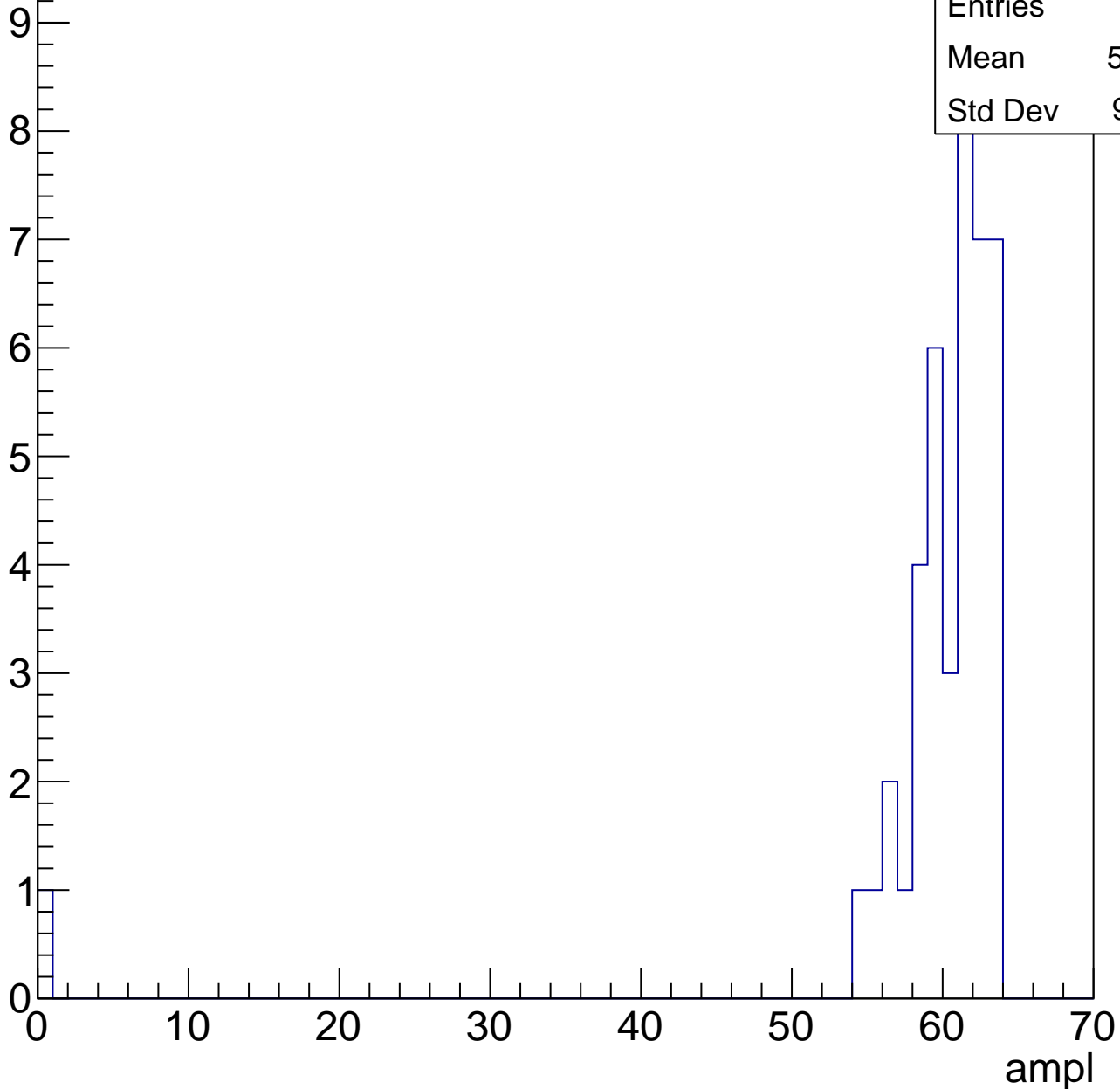


B0L002S, U17-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	58.76
Std Dev	9.461



B0L002S, U17-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.356

ampl

B0L002S, U17-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch14, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	28.58
Std Dev	3.471

Gaus mean : 28.9363

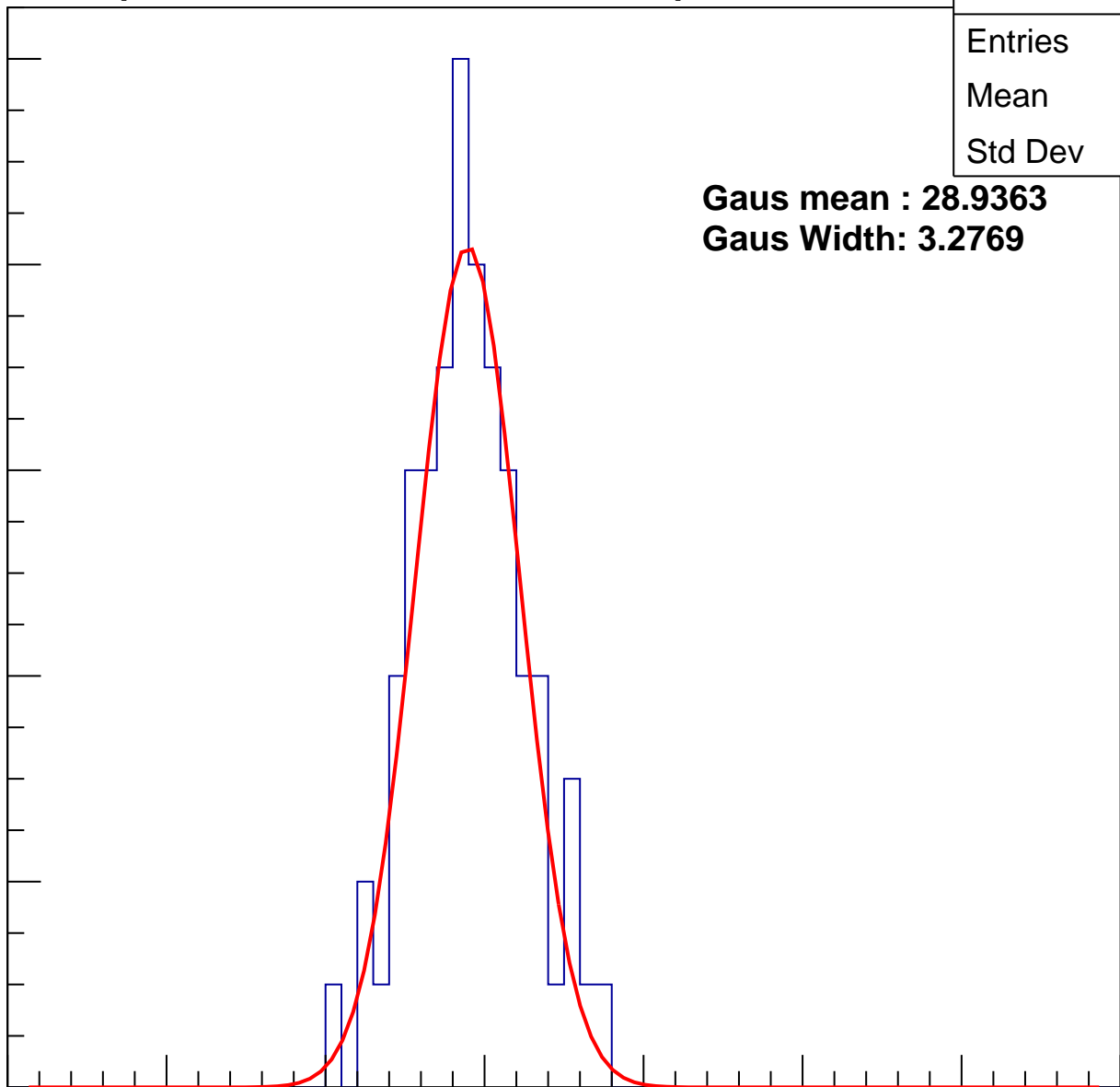
Gaus Width: 3.2769

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U17-ch14, adc1

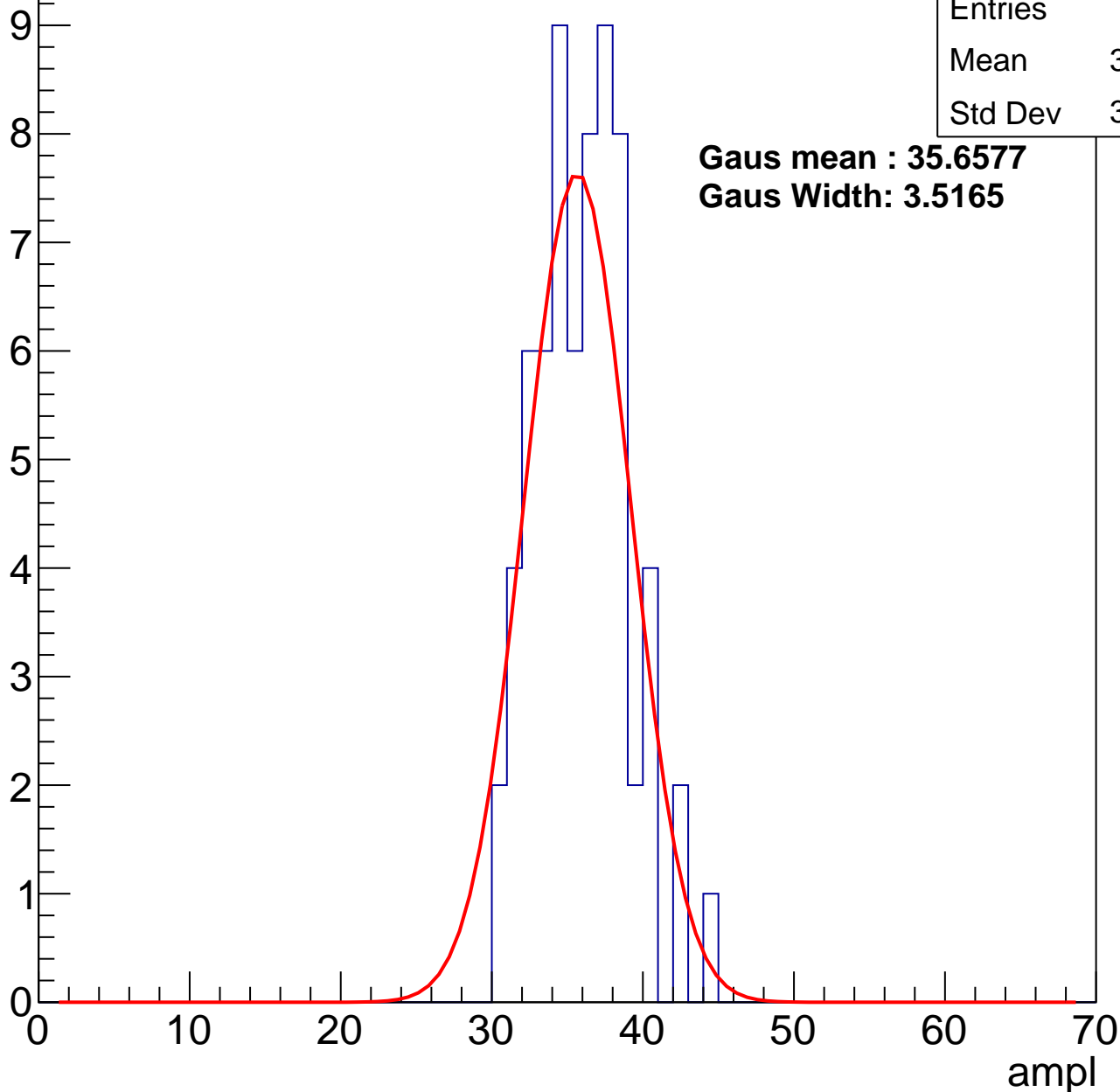
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	35.54
Std Dev	3.014

Gaus mean : 35.6577

Gaus Width: 3.5165



B0L002S, U17-ch14, adc2

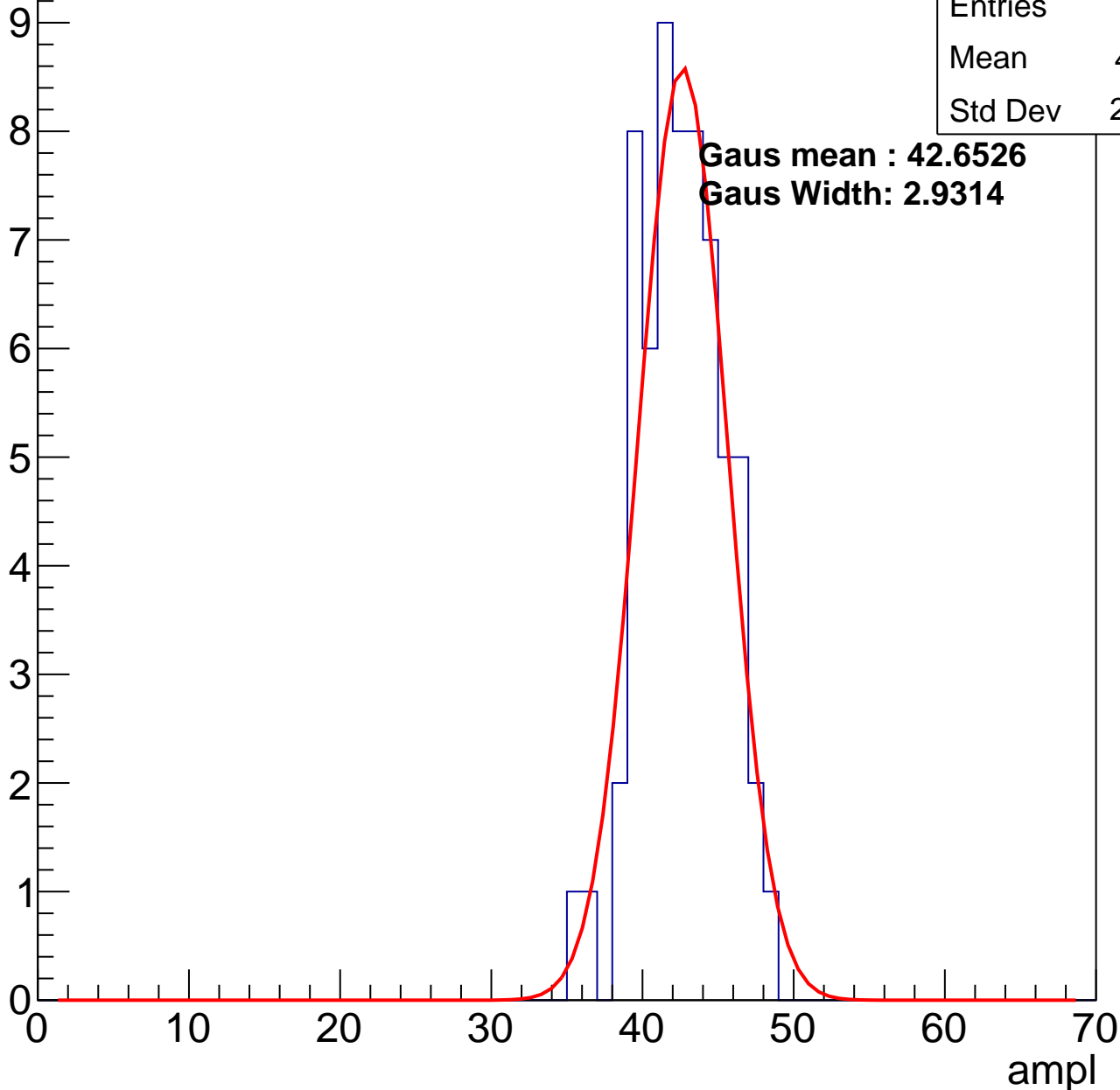
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.11
Std Dev	2.726

Gaus mean : 42.6526

Gaus Width: 2.9314

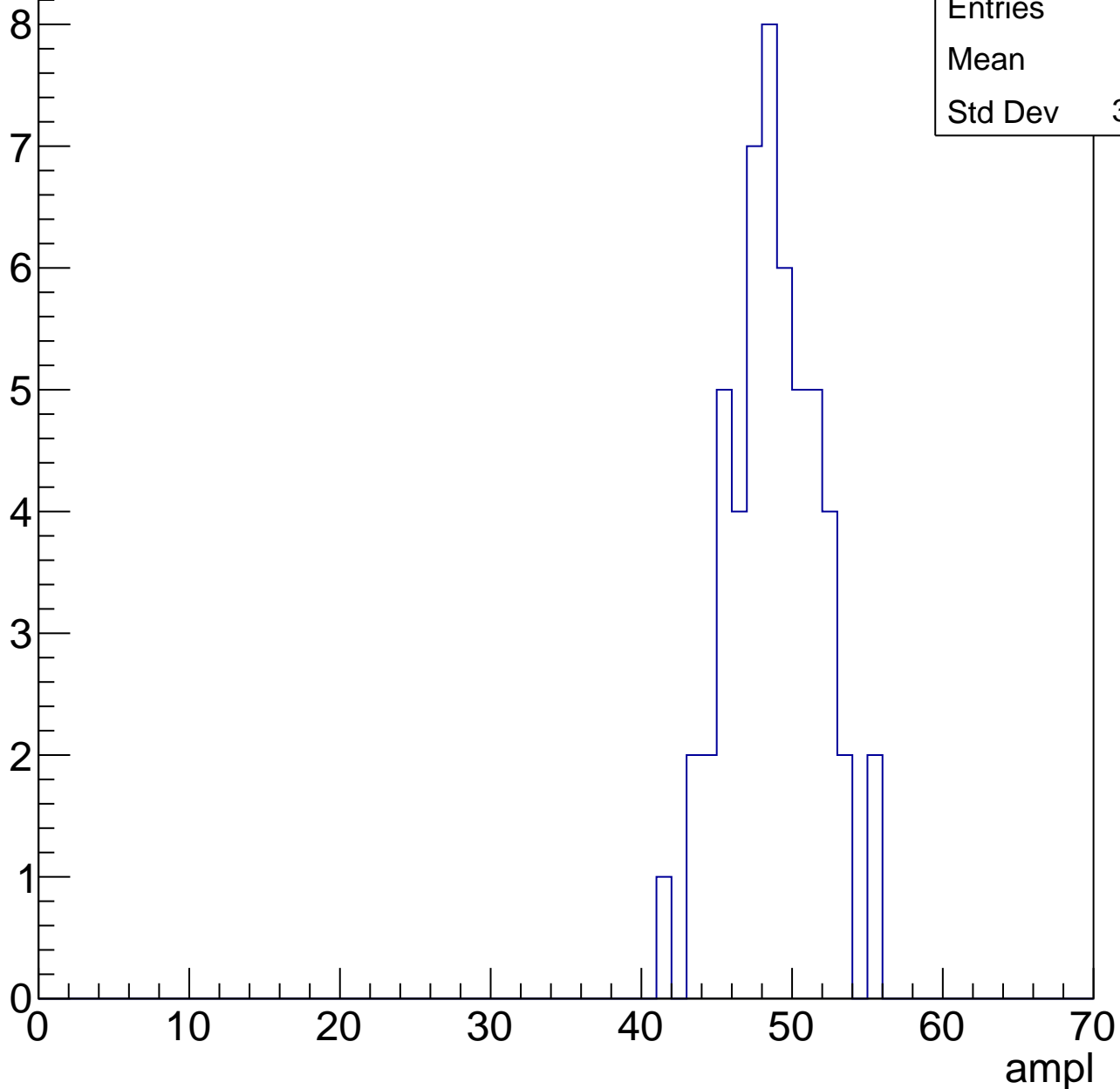


B0L002S, U17-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

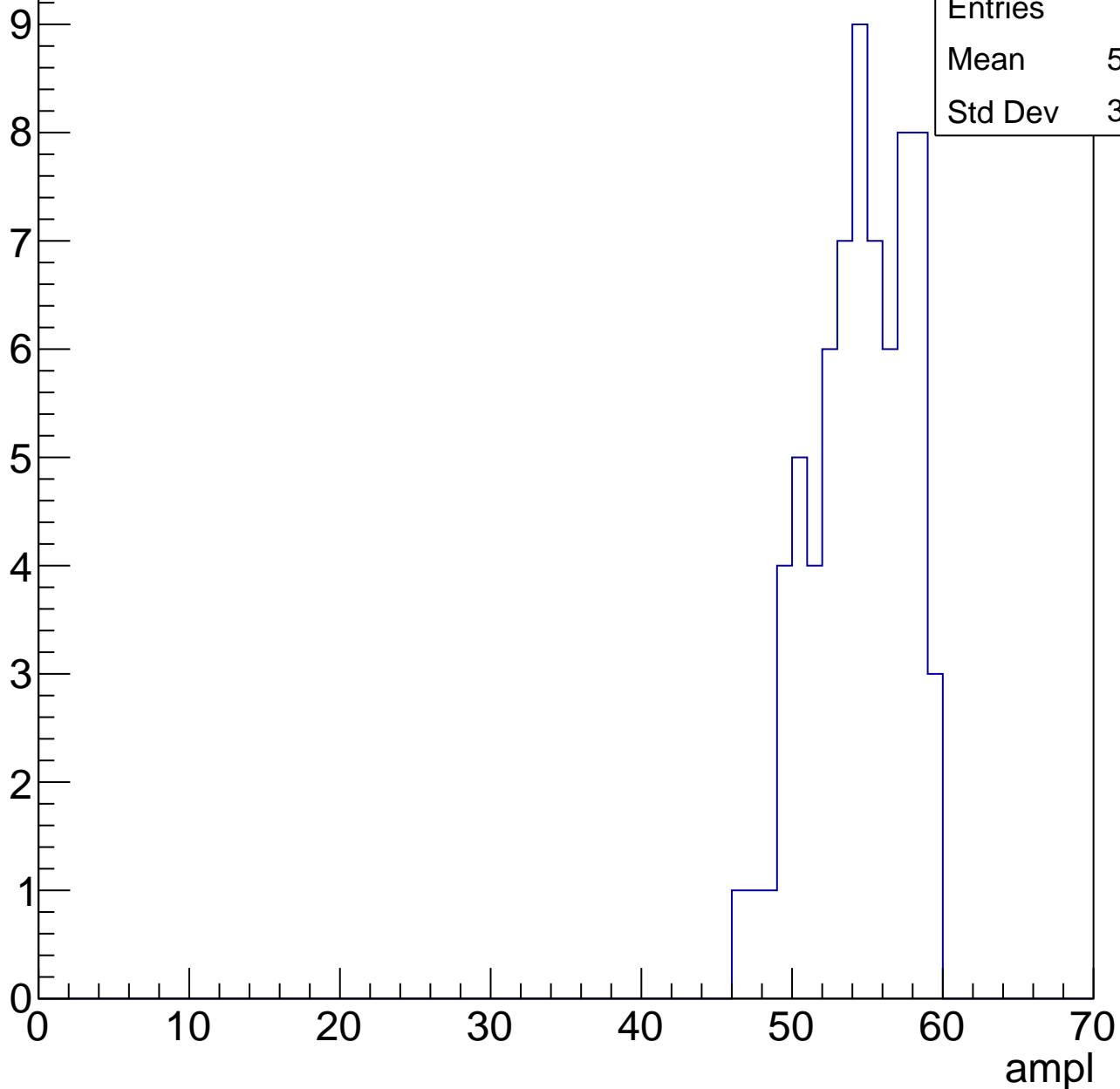
Entries	53
Mean	48.3
Std Dev	3.001



B0L002S, U17-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

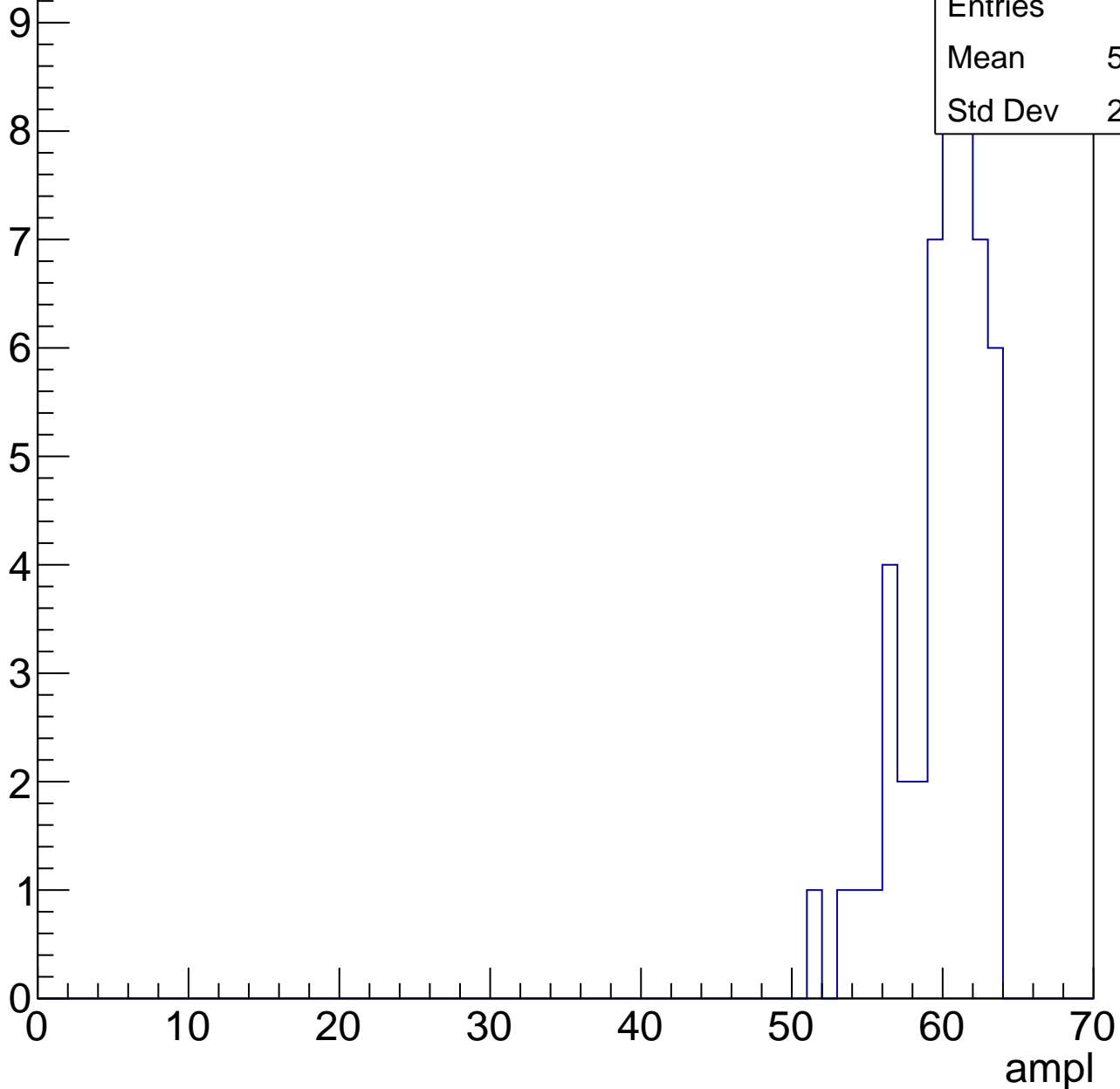


B0L002S, U17-ch14, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

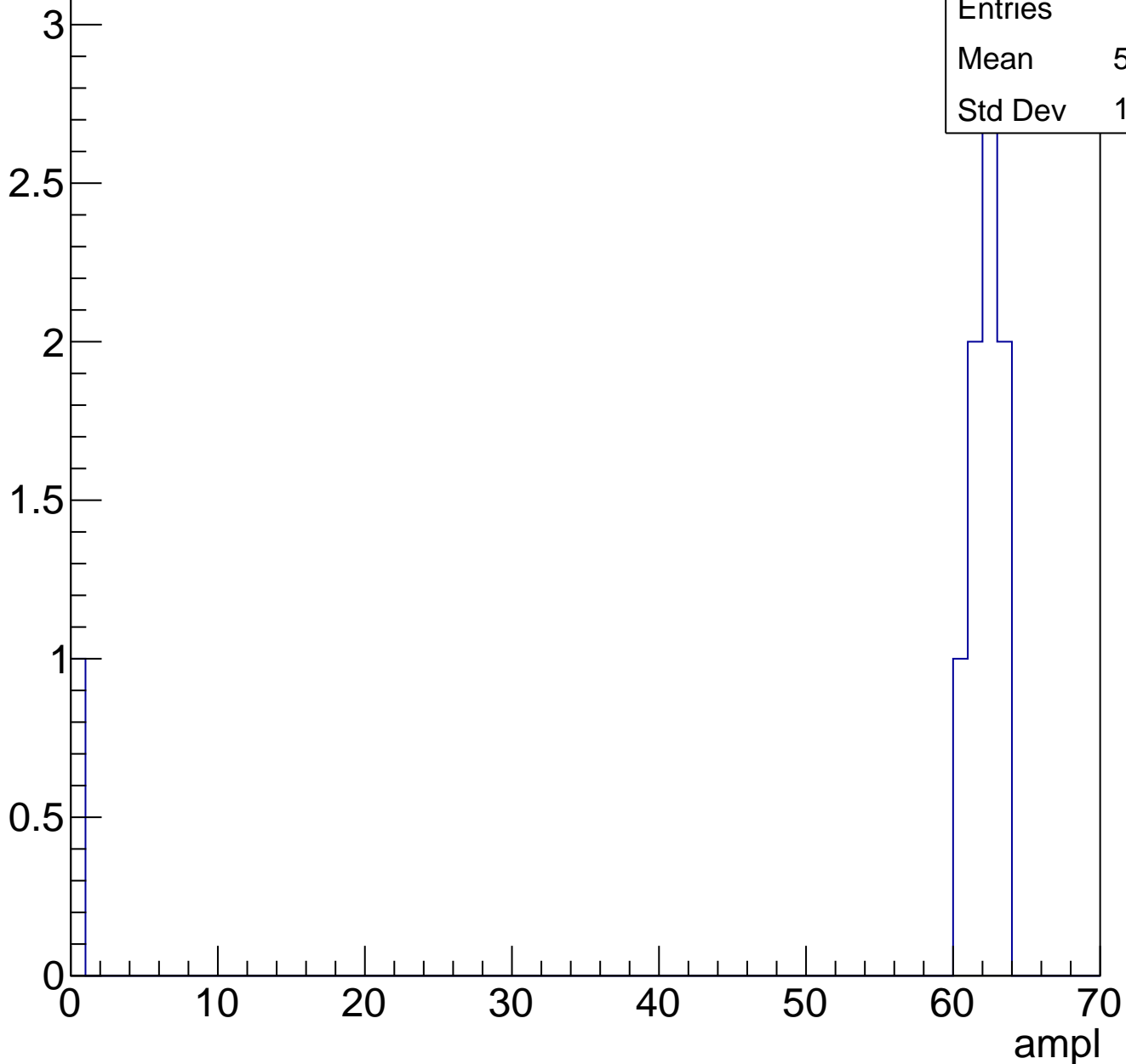
Entries	49
Mean	59.59
Std Dev	2.747



B0L002S, U17-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch15, adc0

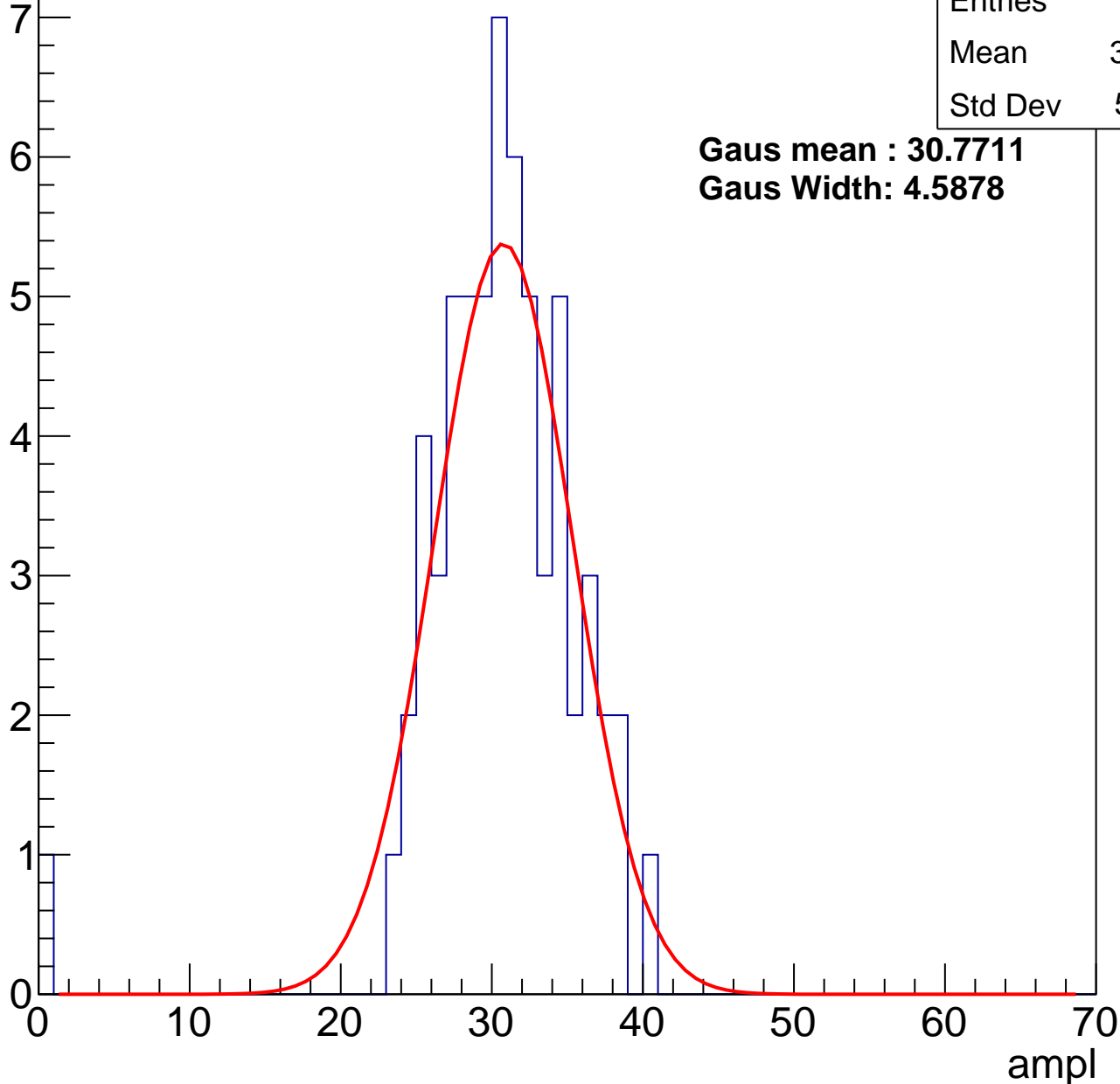
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.03
Std Dev	5.471

Gaus mean : 30.7711

Gaus Width: 4.5878



B0L002S, U17-ch15, adc1

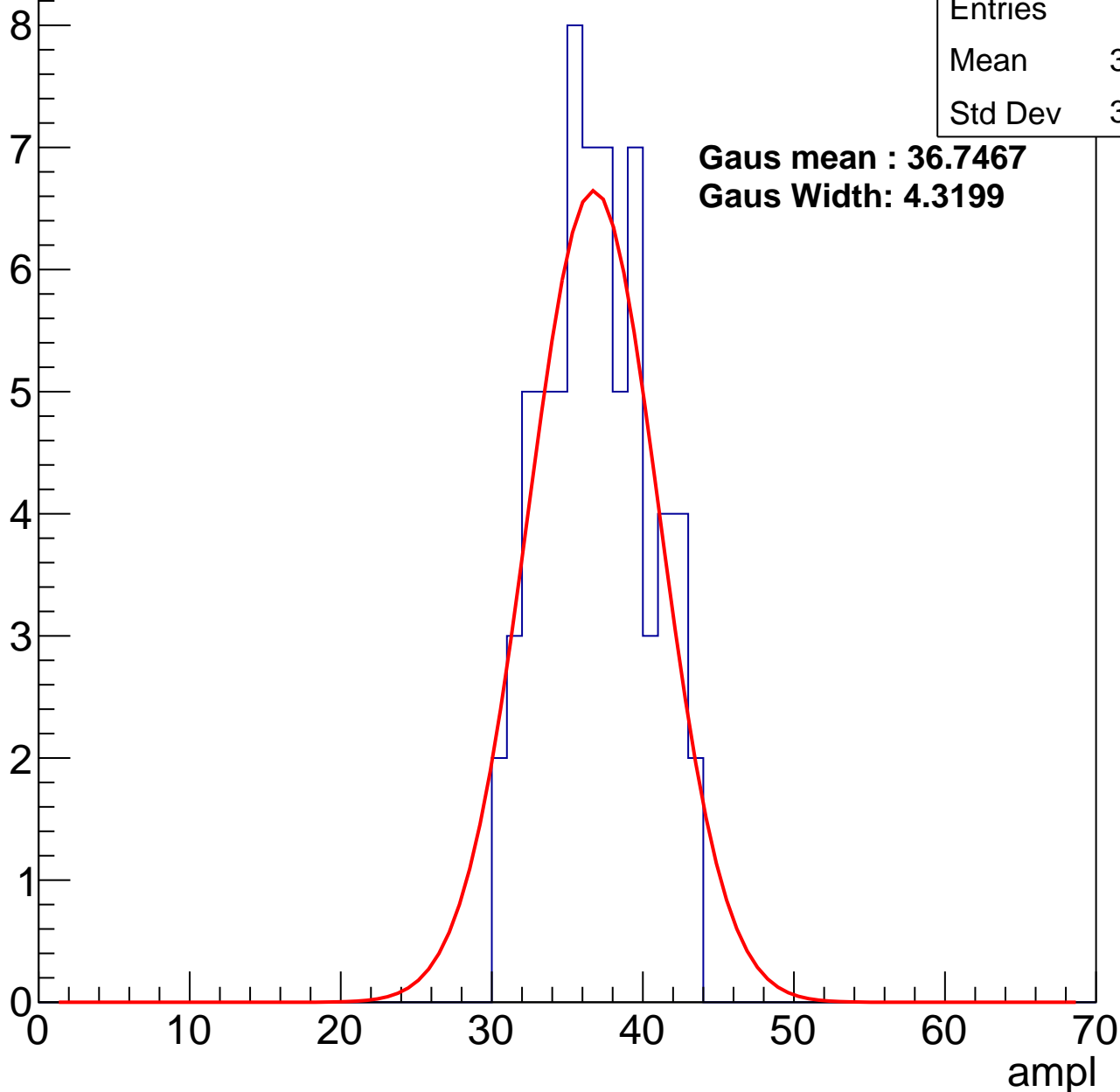
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.42
Std Dev	3.386

Gaus mean : 36.7467

Gaus Width: 4.3199



B0L002S, U17-ch15, adc2

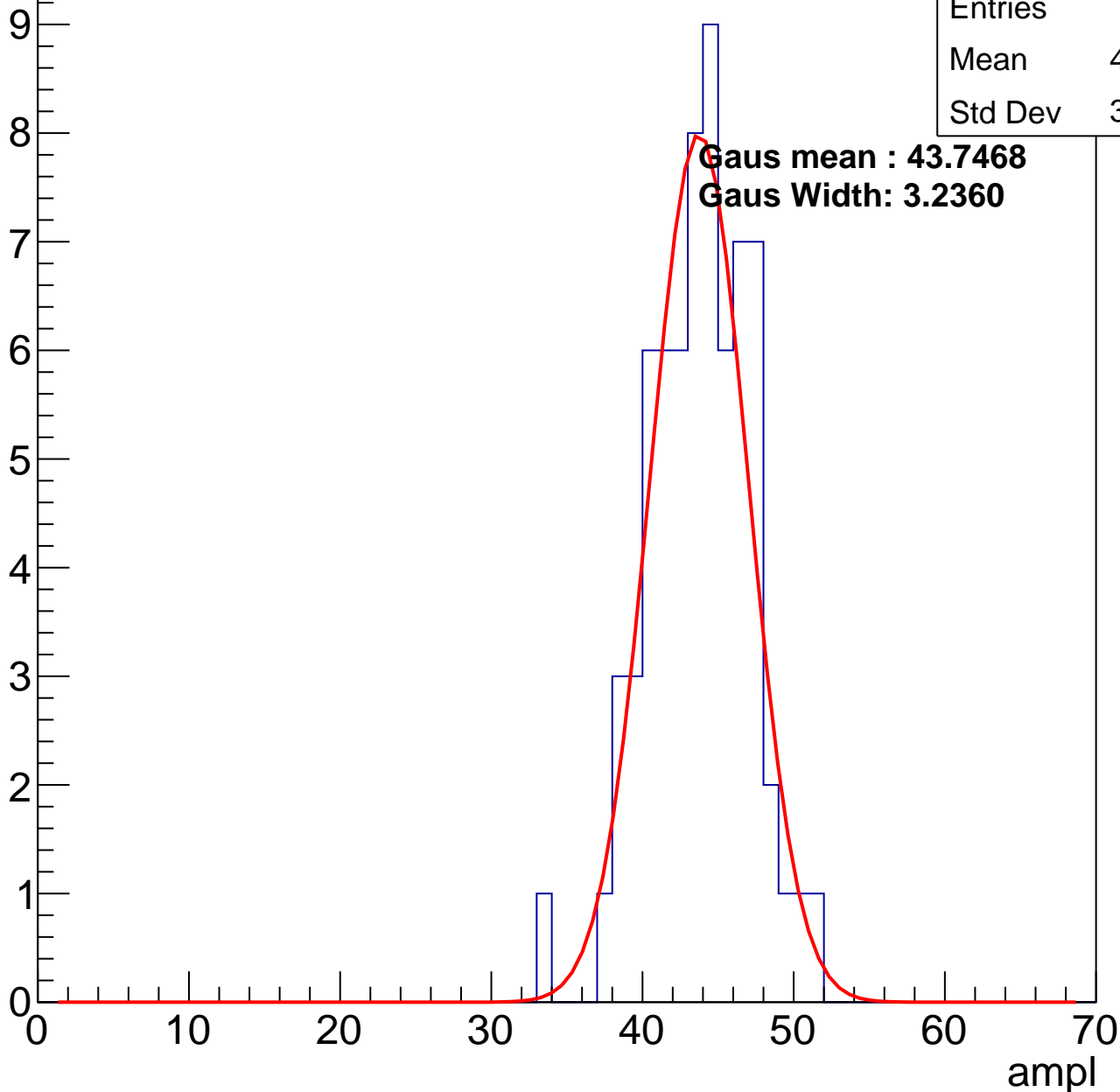
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.32
Std Dev	3.305

Gaus mean : 43.7468

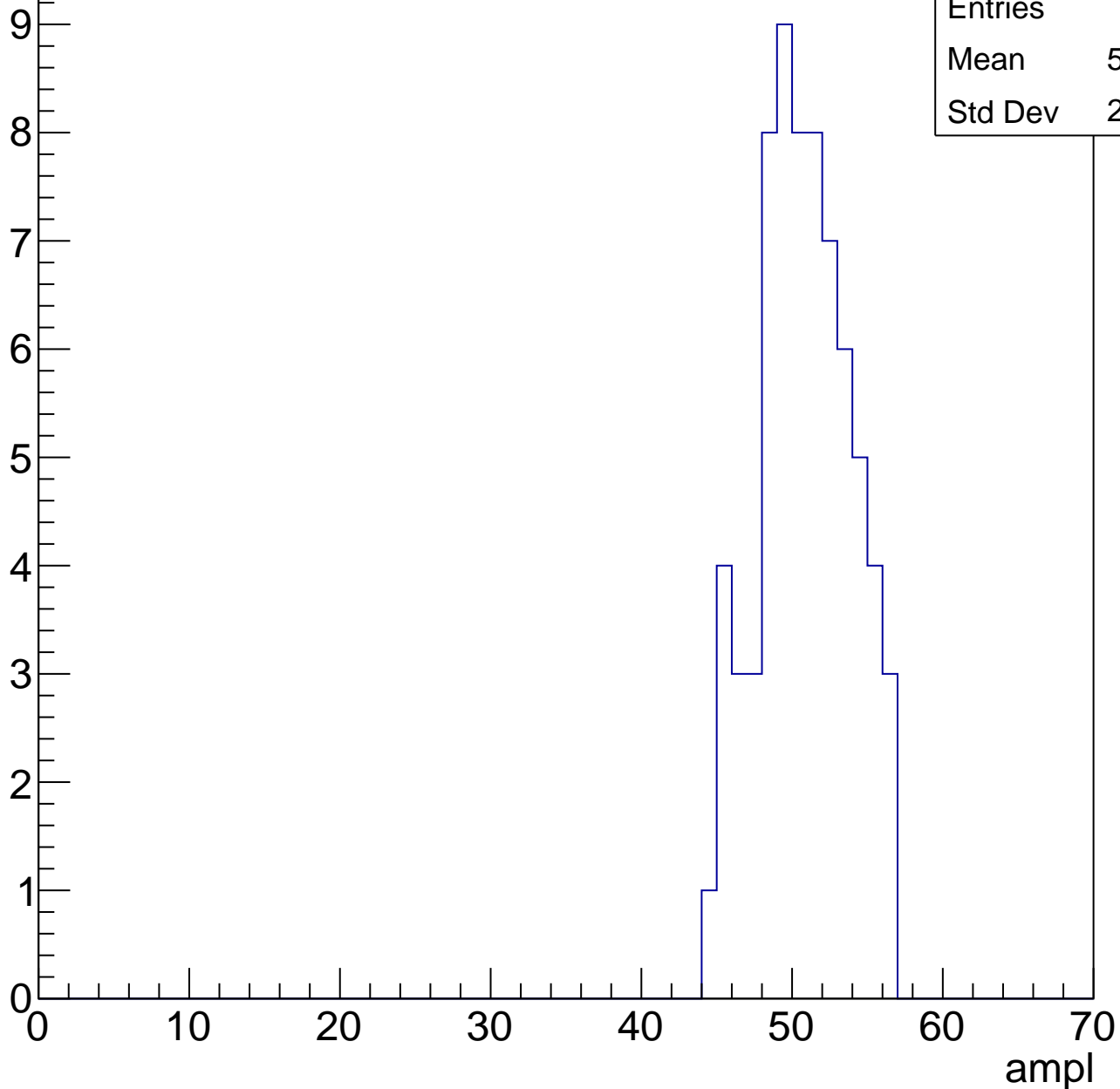
Gaus Width: 3.2360



B0L002S, U17-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

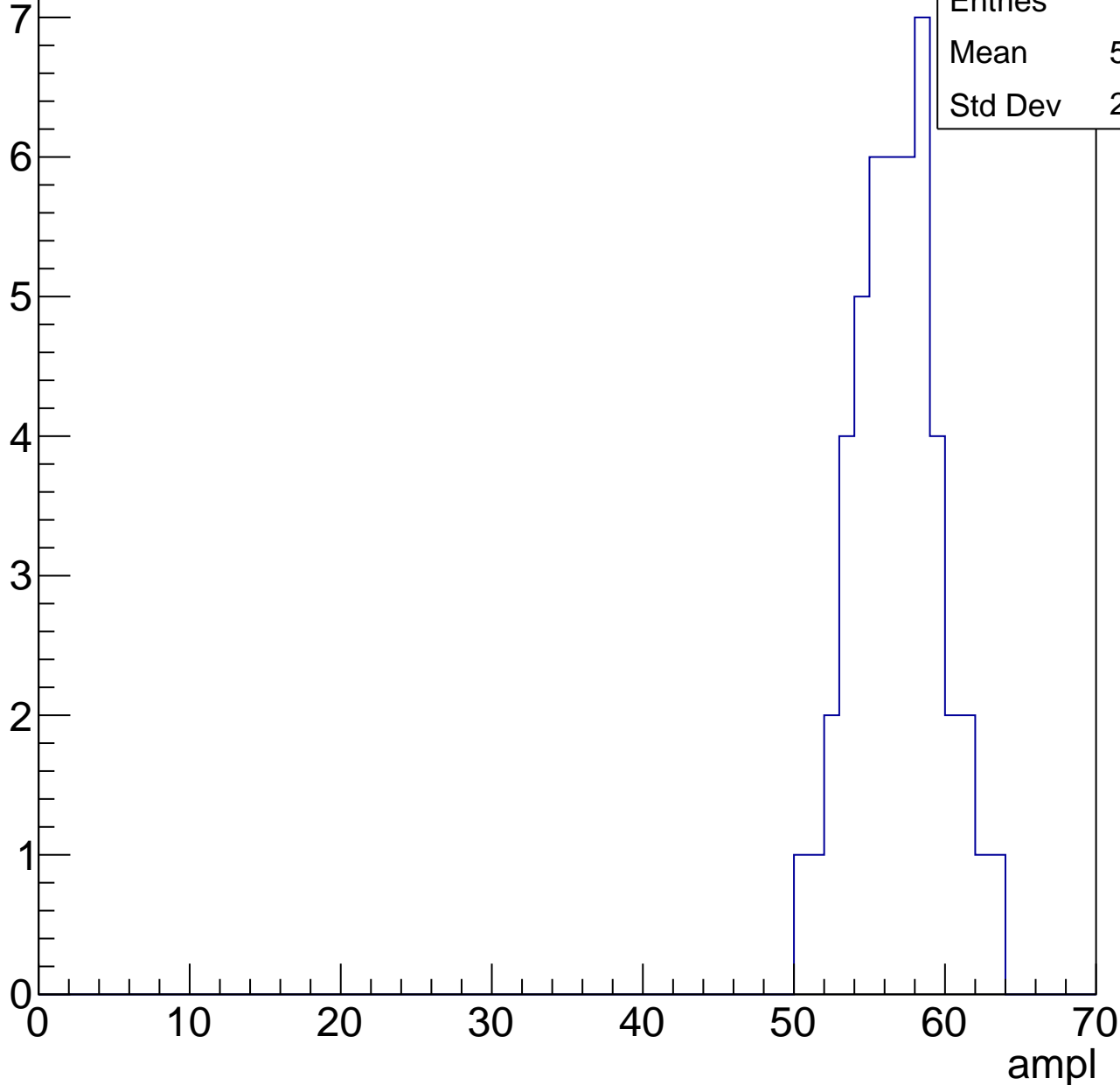


Entries	69
Mean	50.38
Std Dev	2.998

B0L002S, U17-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



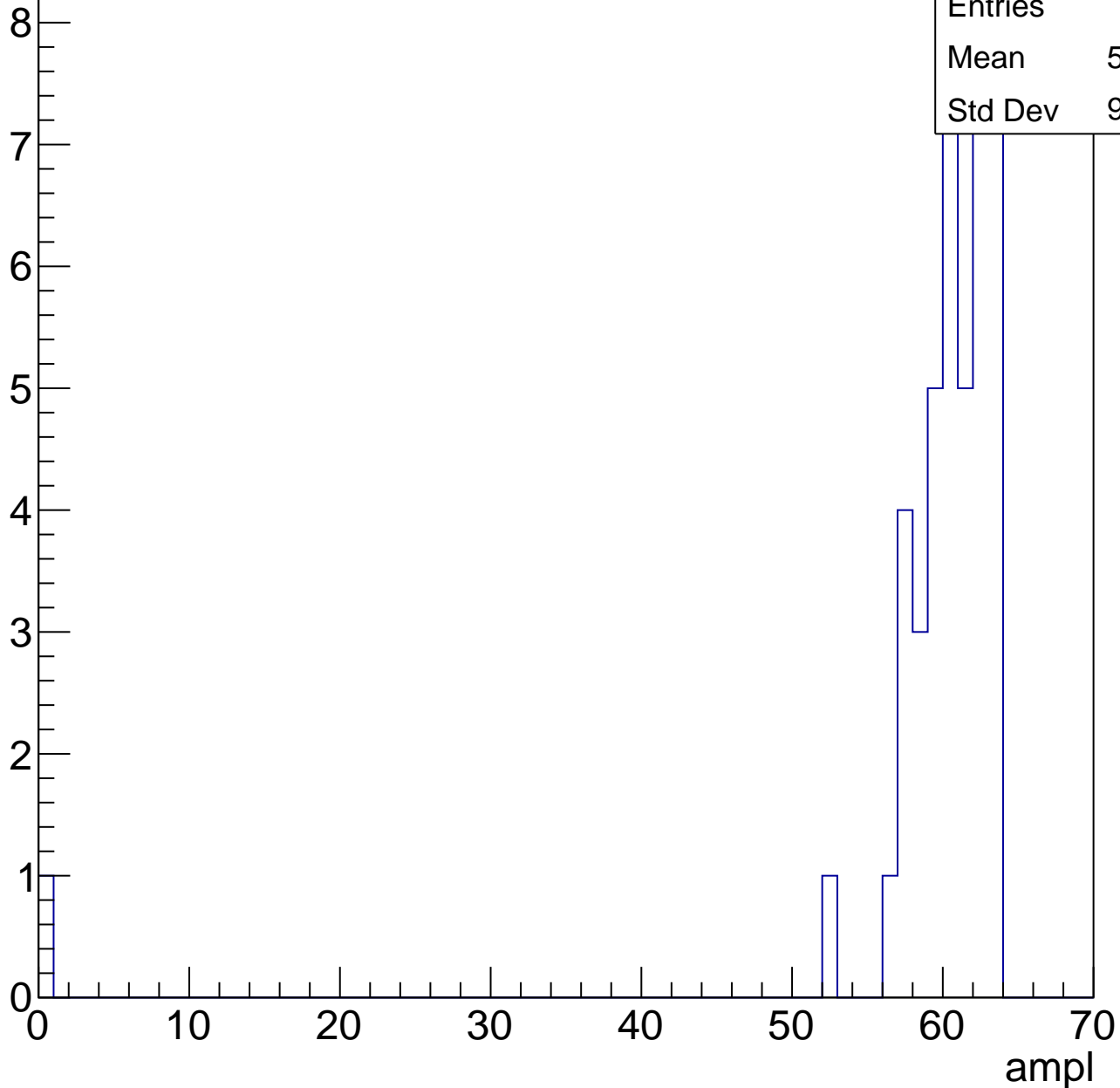
Entries	48
Mean	56.33
Std Dev	2.838

B0L002S, U17-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

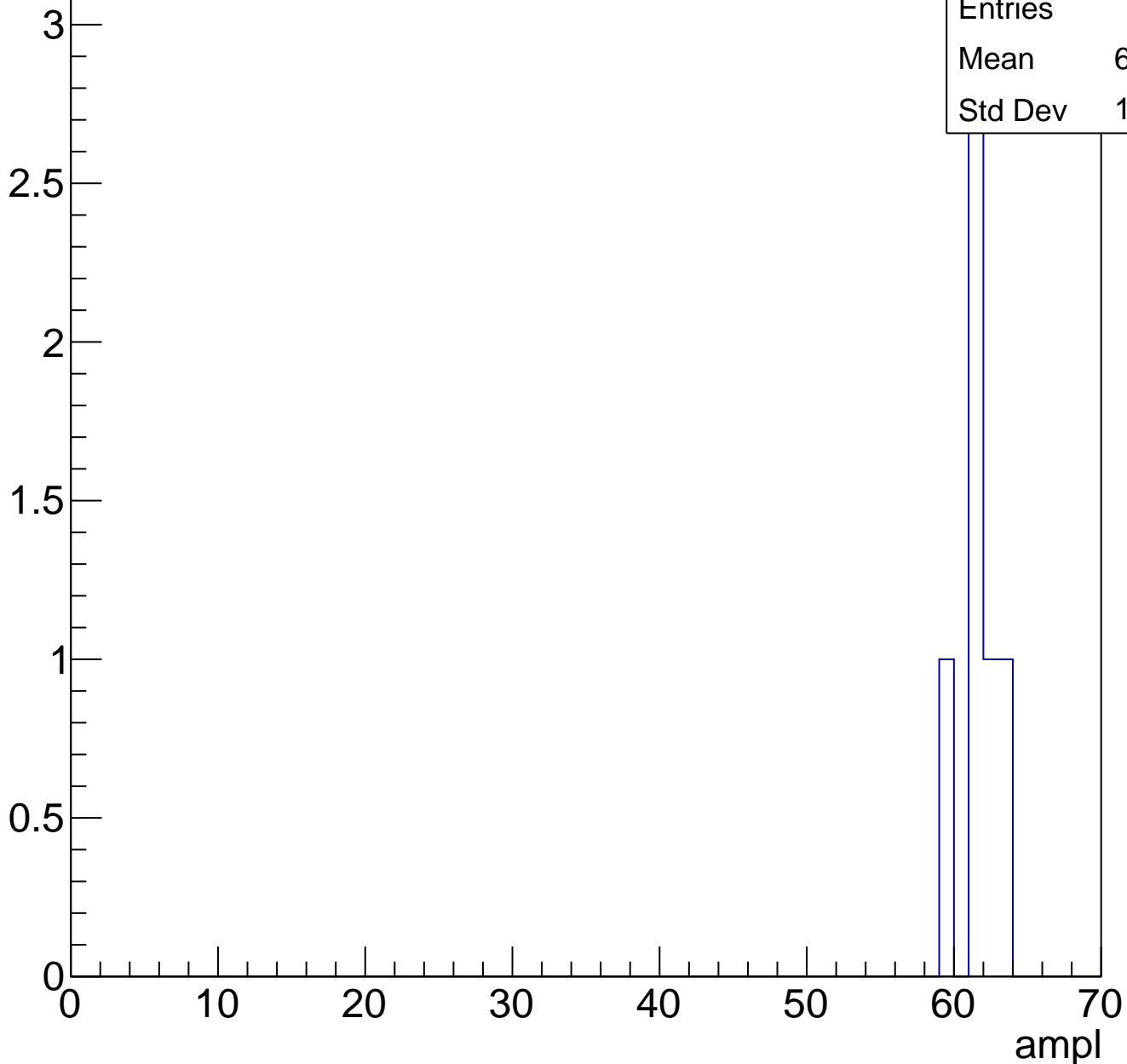
Entries	44
Mean	58.86
Std Dev	9.275



B0L002S, U17-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

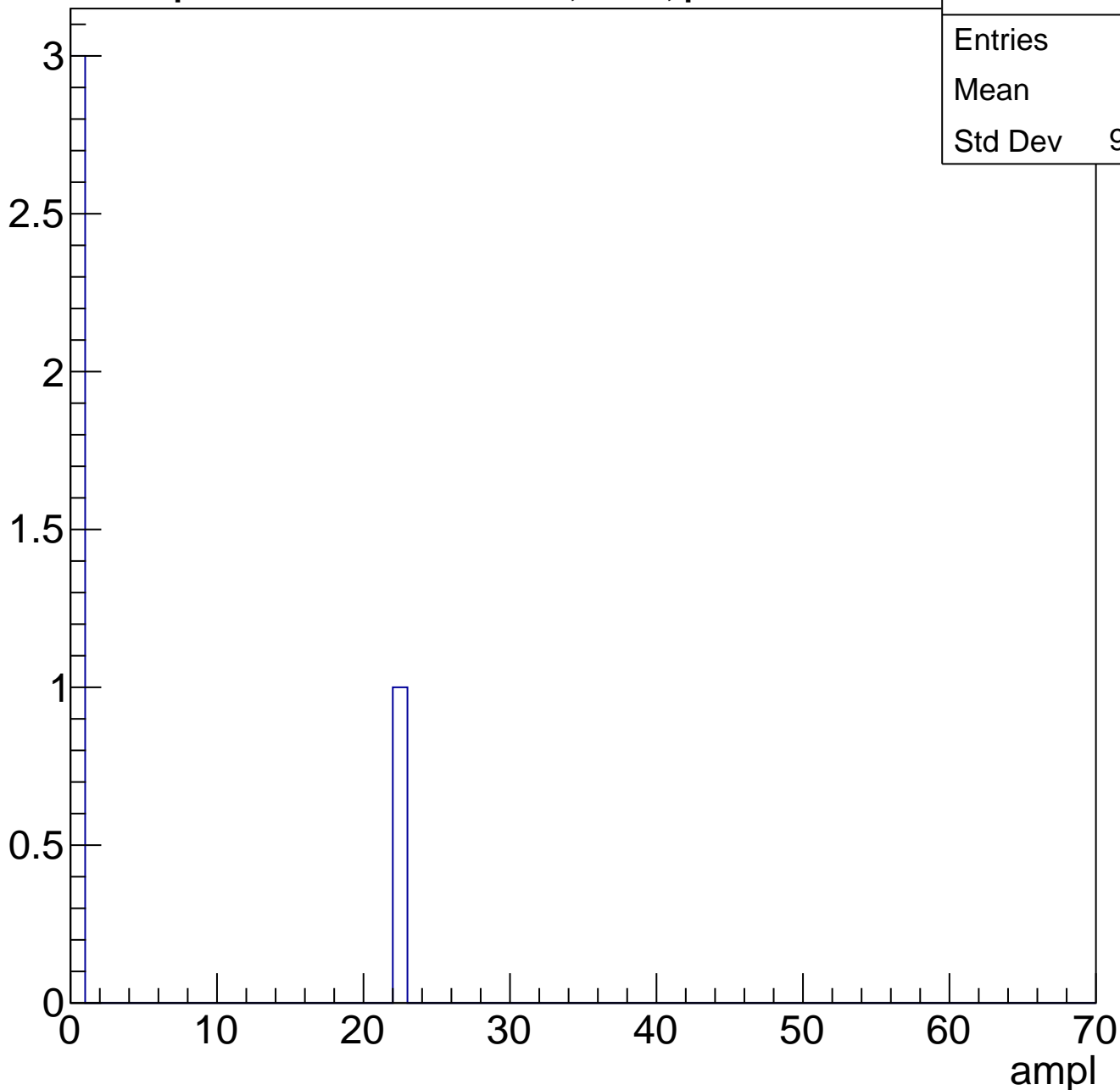
Entry



B0L002S, U17-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	5.5
Std Dev	9.526

B0L002S, U17-ch16, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	63
Mean	29.21
Std Dev	4.986

Gaus mean : 30.6377

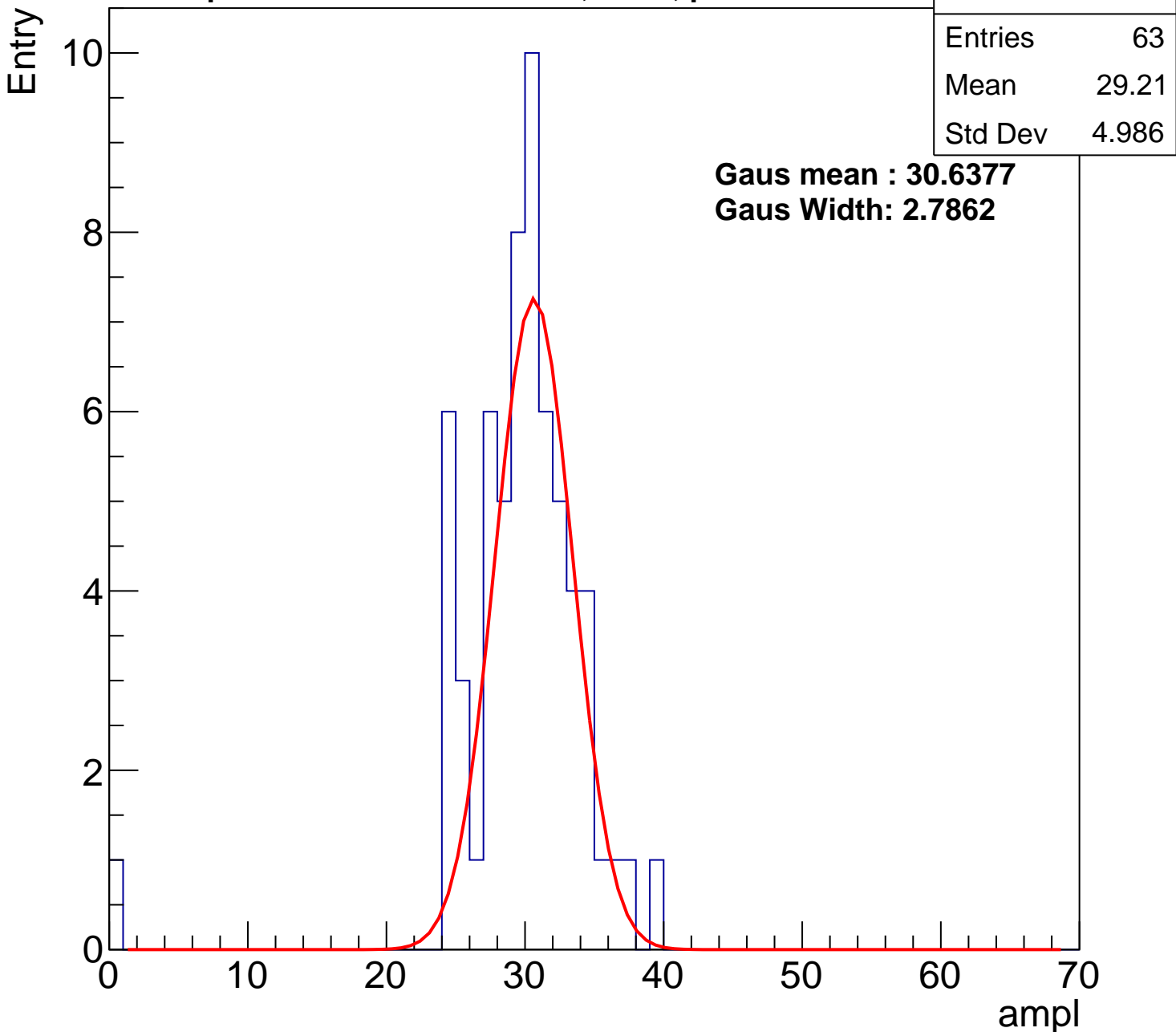
Gaus Width: 2.7862

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch16, adc1

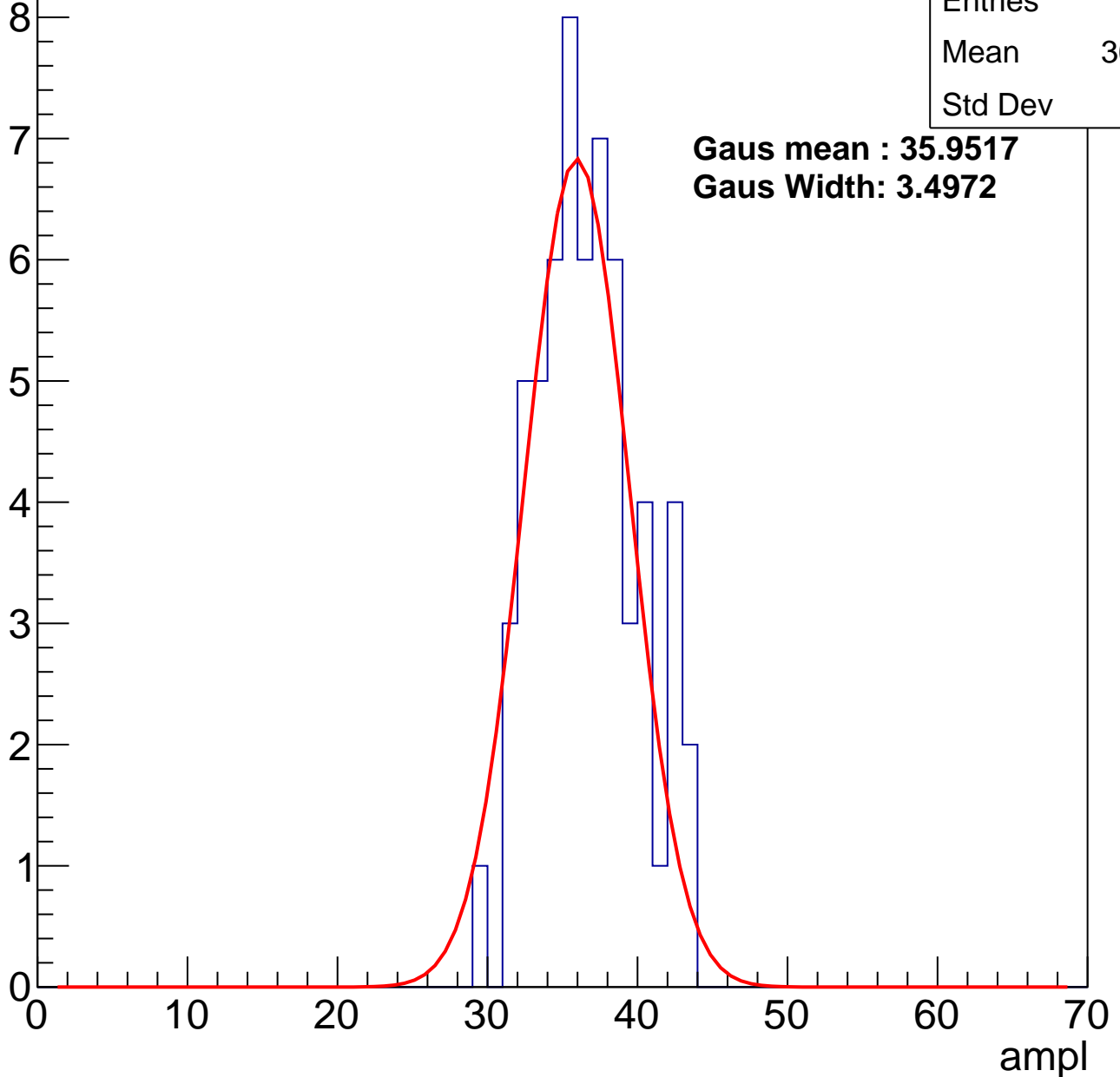
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.16
Std Dev	3.31

Gaus mean : 35.9517

Gaus Width: 3.4972



B0L002S, U17-ch16, adc2

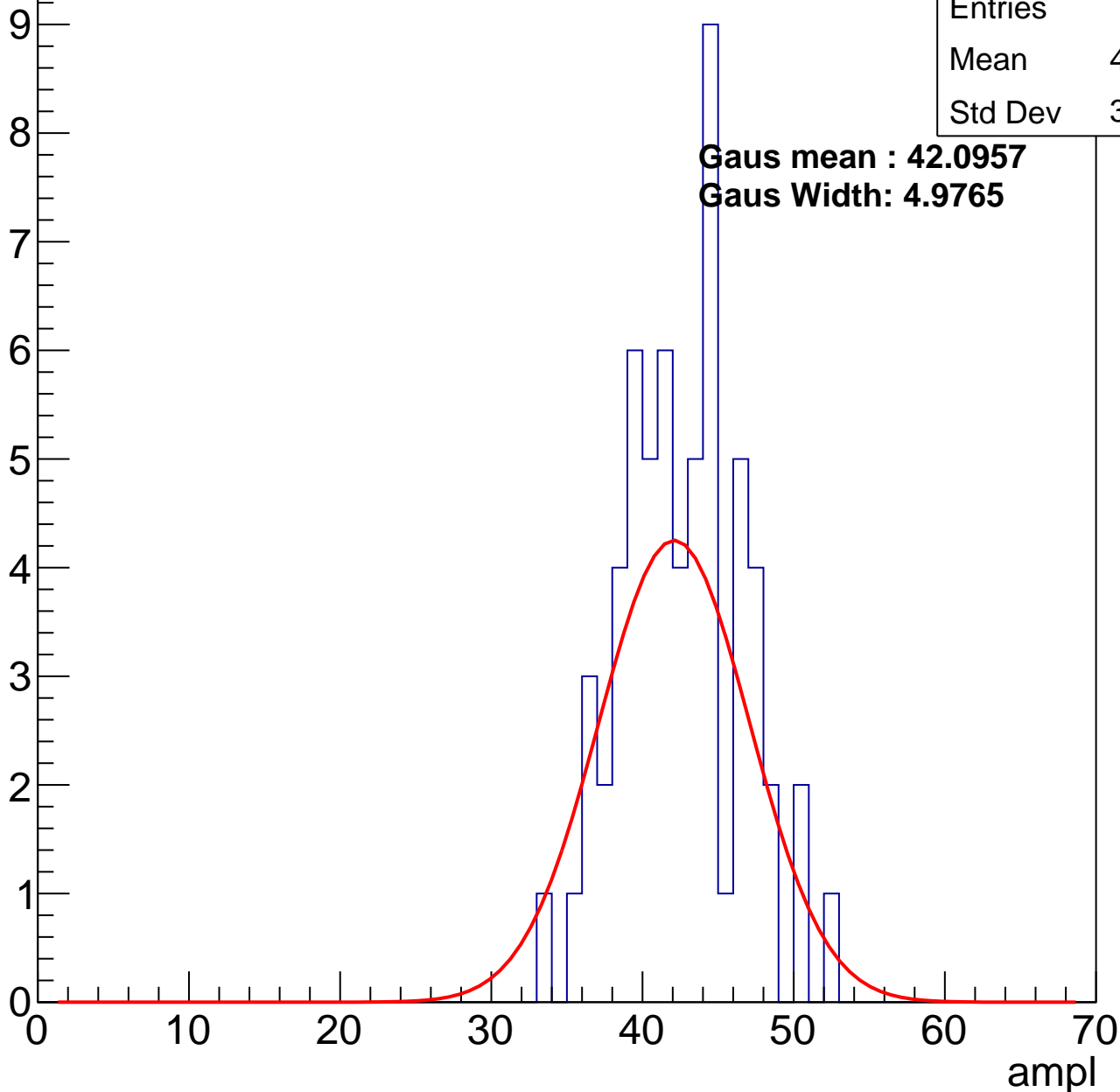
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.16
Std Dev	3.968

Gaus mean : 42.0957

Gaus Width: 4.9765

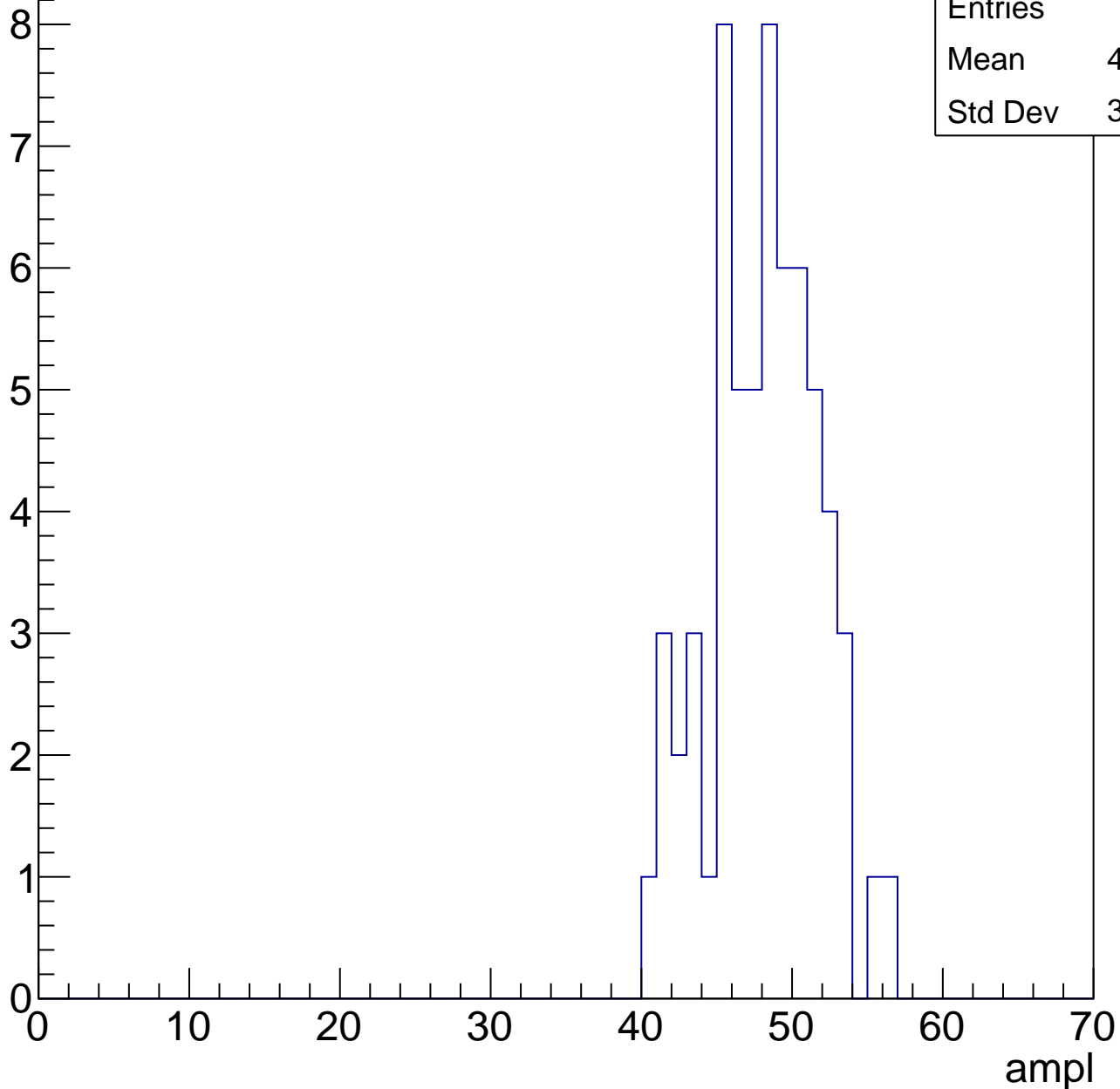


B0L002S, U17-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	47.68
Std Dev	3.573

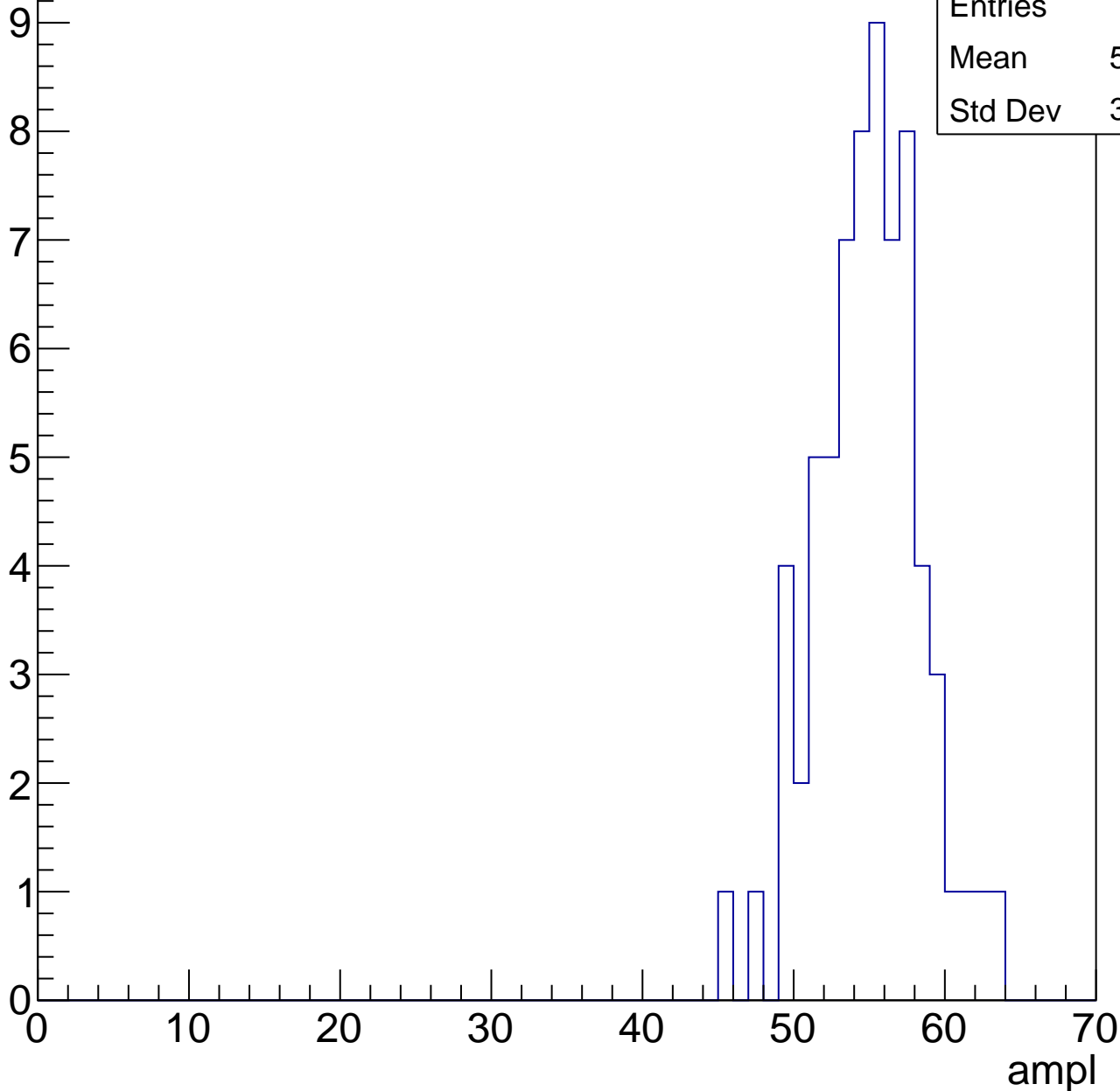


B0L002S, U17-ch16, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	54.47
Std Dev	3.423

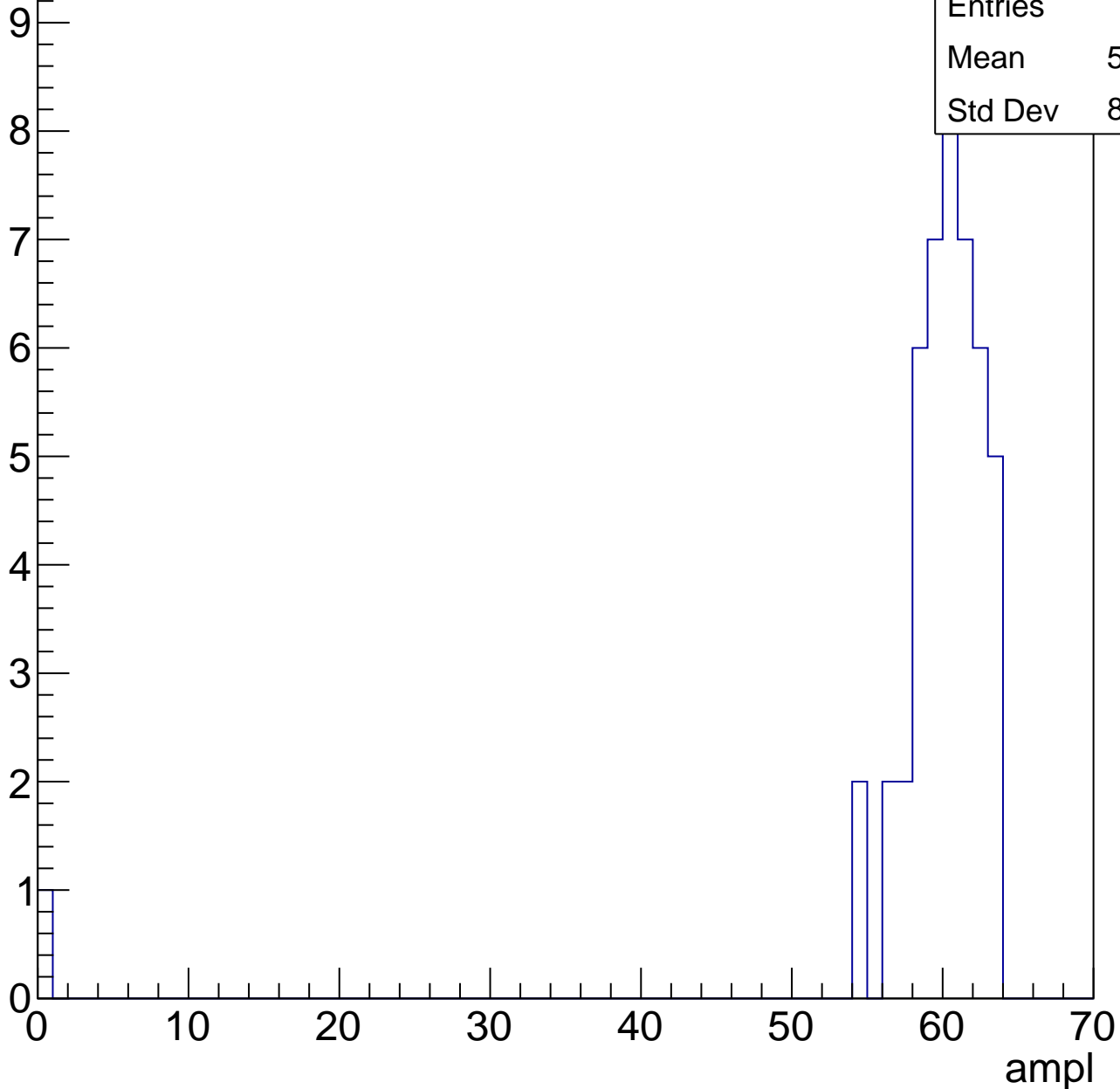


B0L002S, U17-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	58.49
Std Dev	8.899



B0L002S, U17-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch17, adc0

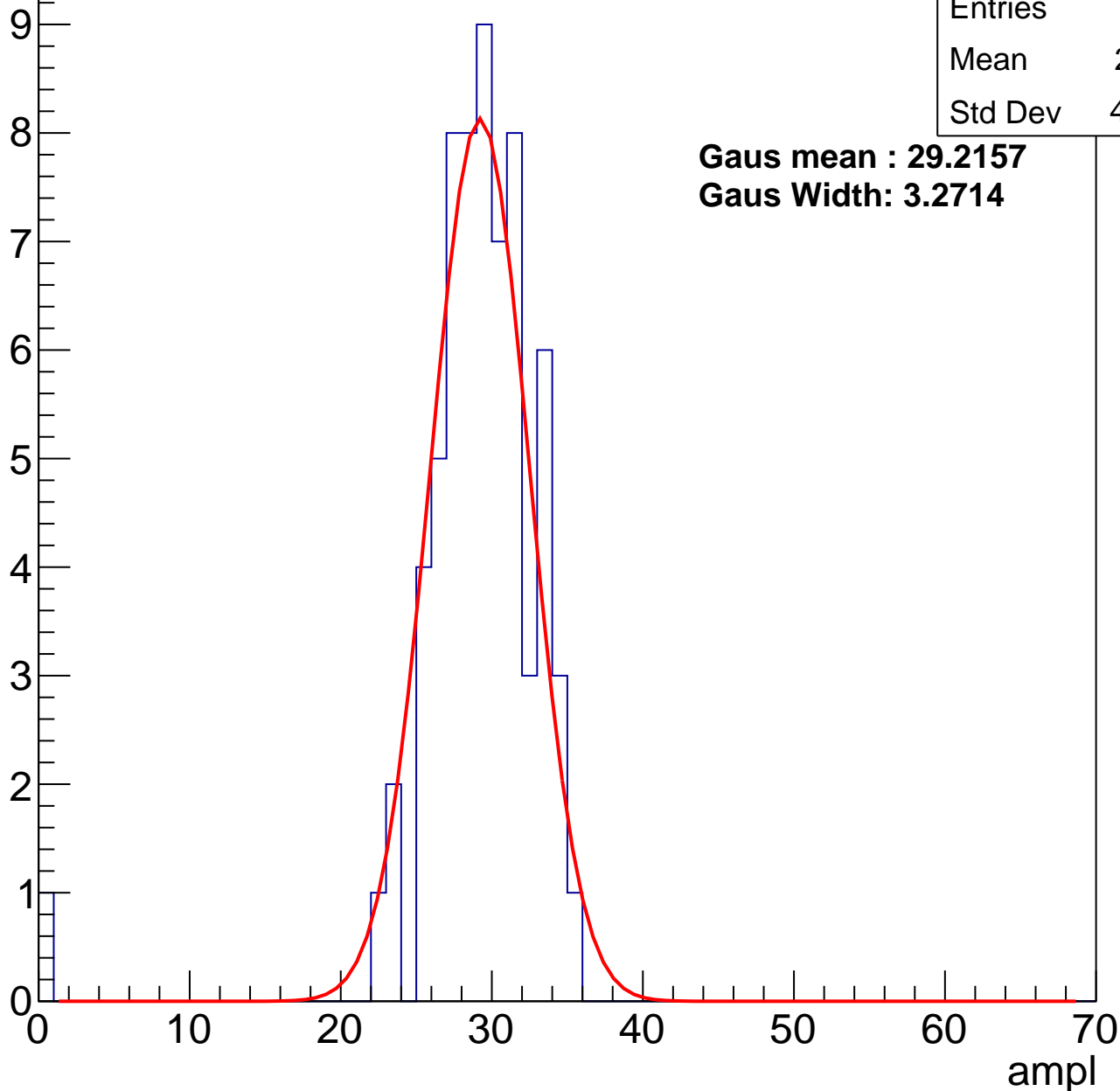
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	28.61
Std Dev	4.562

Gaus mean : 29.2157

Gaus Width: 3.2714



B0L002S, U17-ch17, adc1

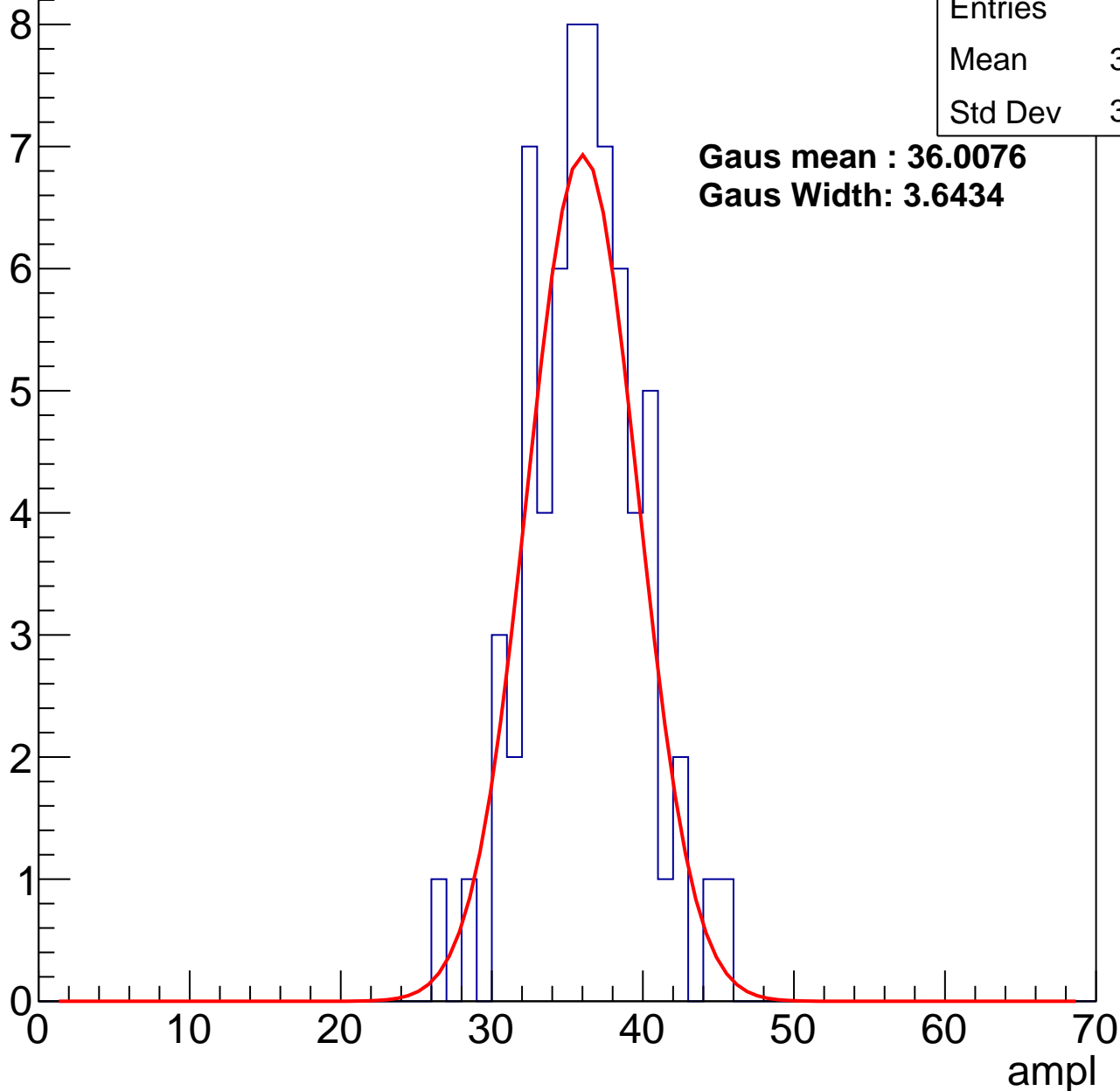
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	35.69
Std Dev	3.633

Gaus mean : 36.0076

Gaus Width: 3.6434



B0L002S, U17-ch17, adc2

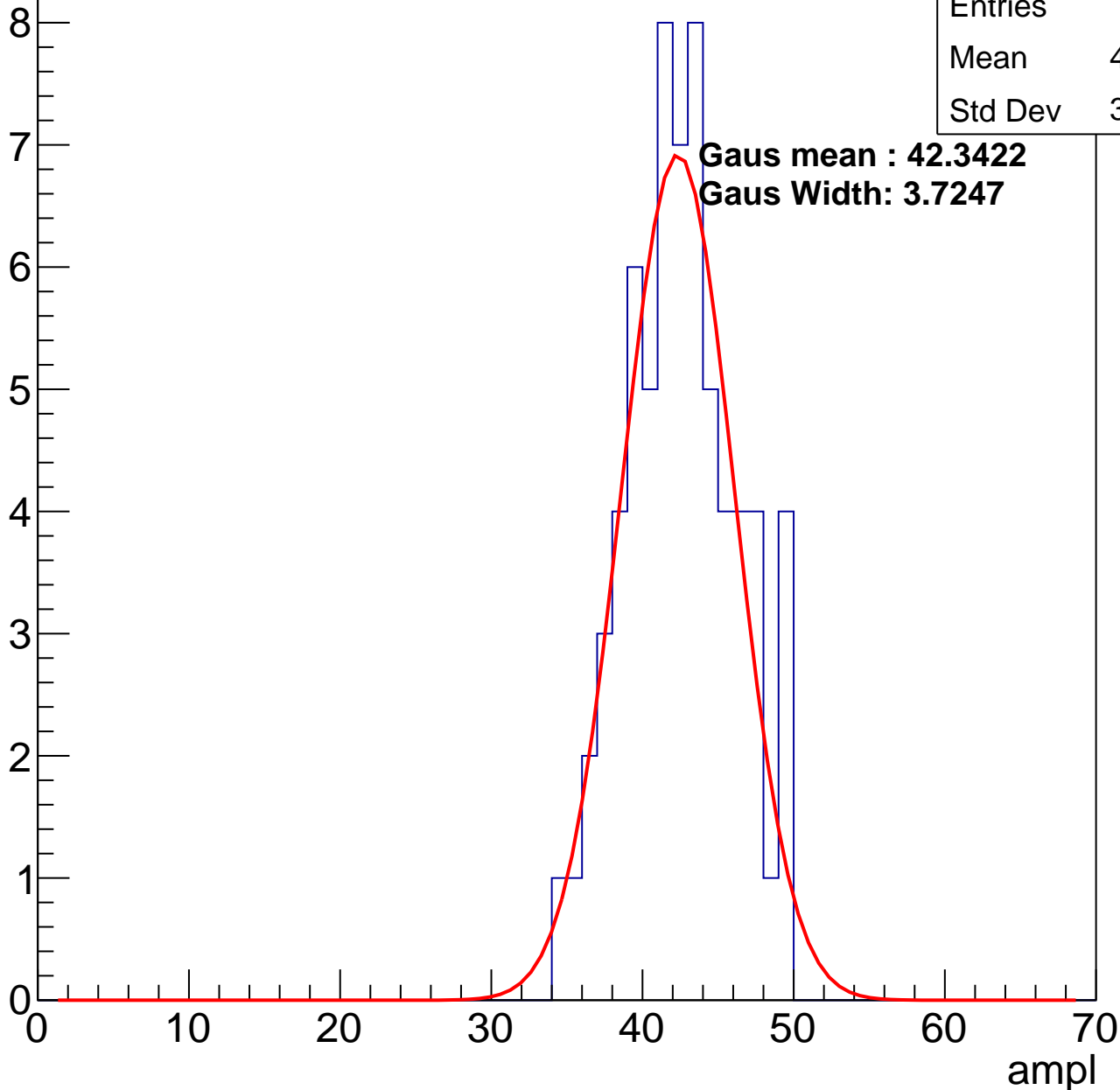
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.09
Std Dev	3.594

Gaus mean : 42.3422

Gaus Width: 3.7247

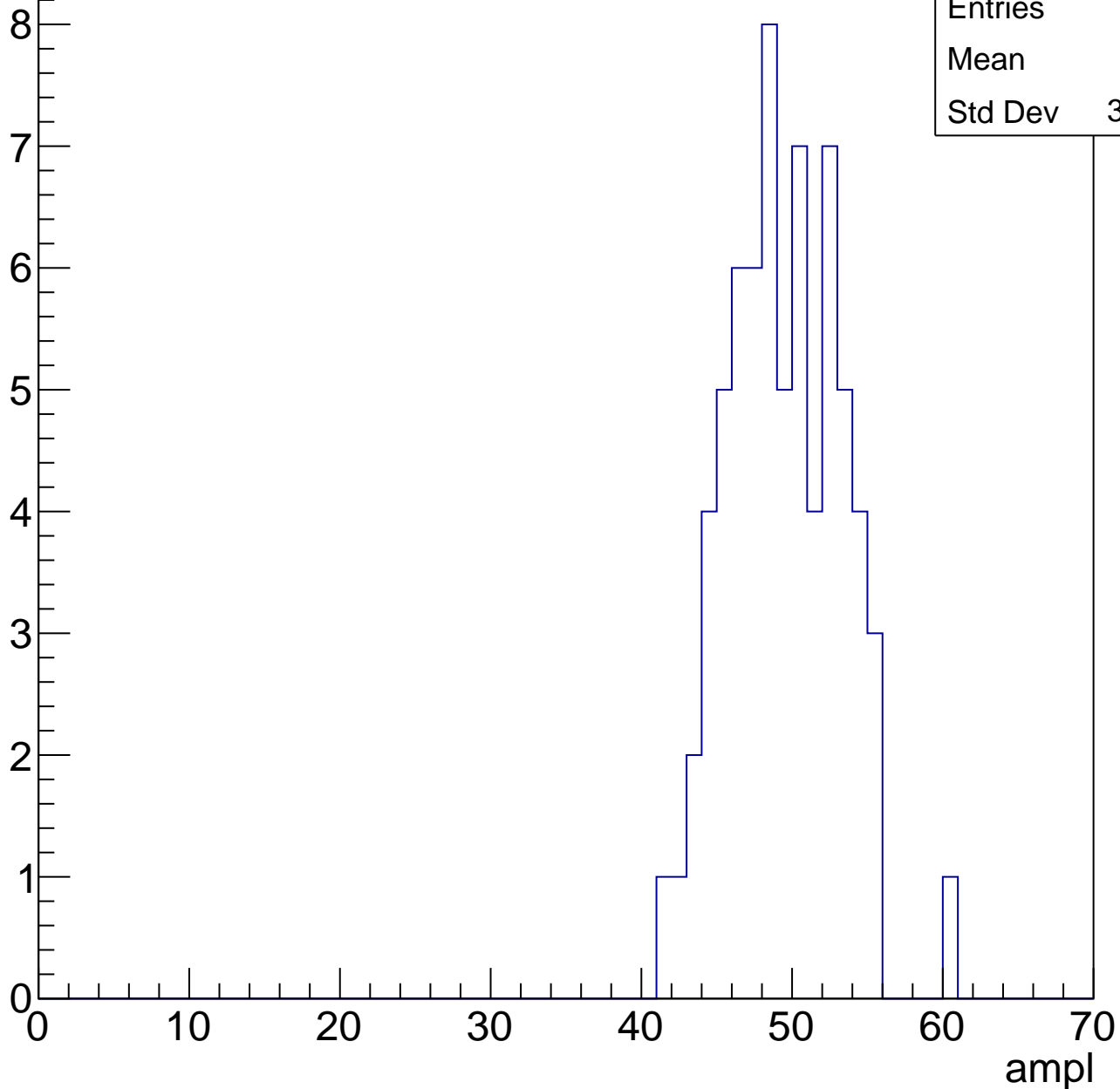


B0L002S, U17-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49
Std Dev	3.703

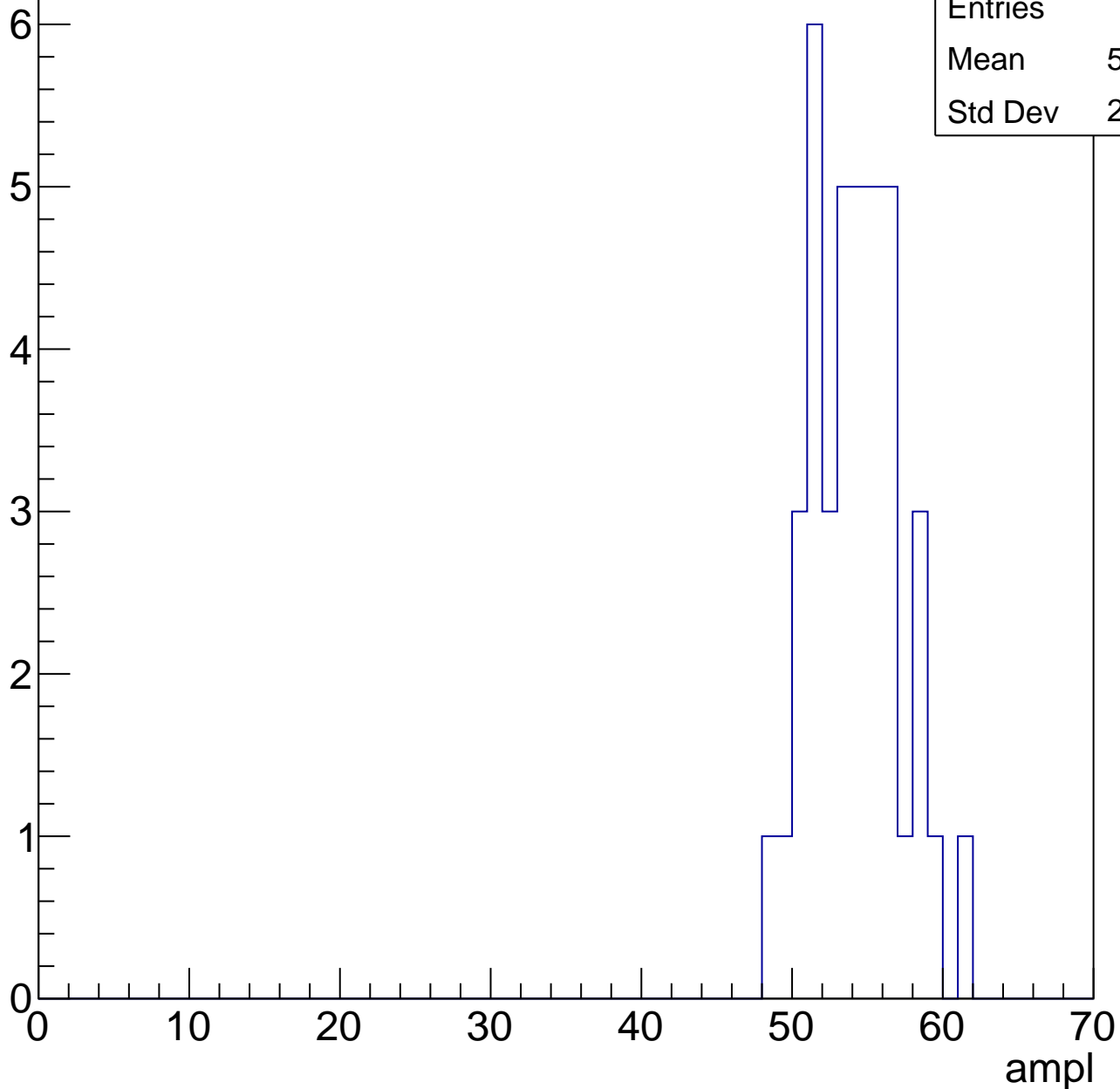


B0L002S, U17-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

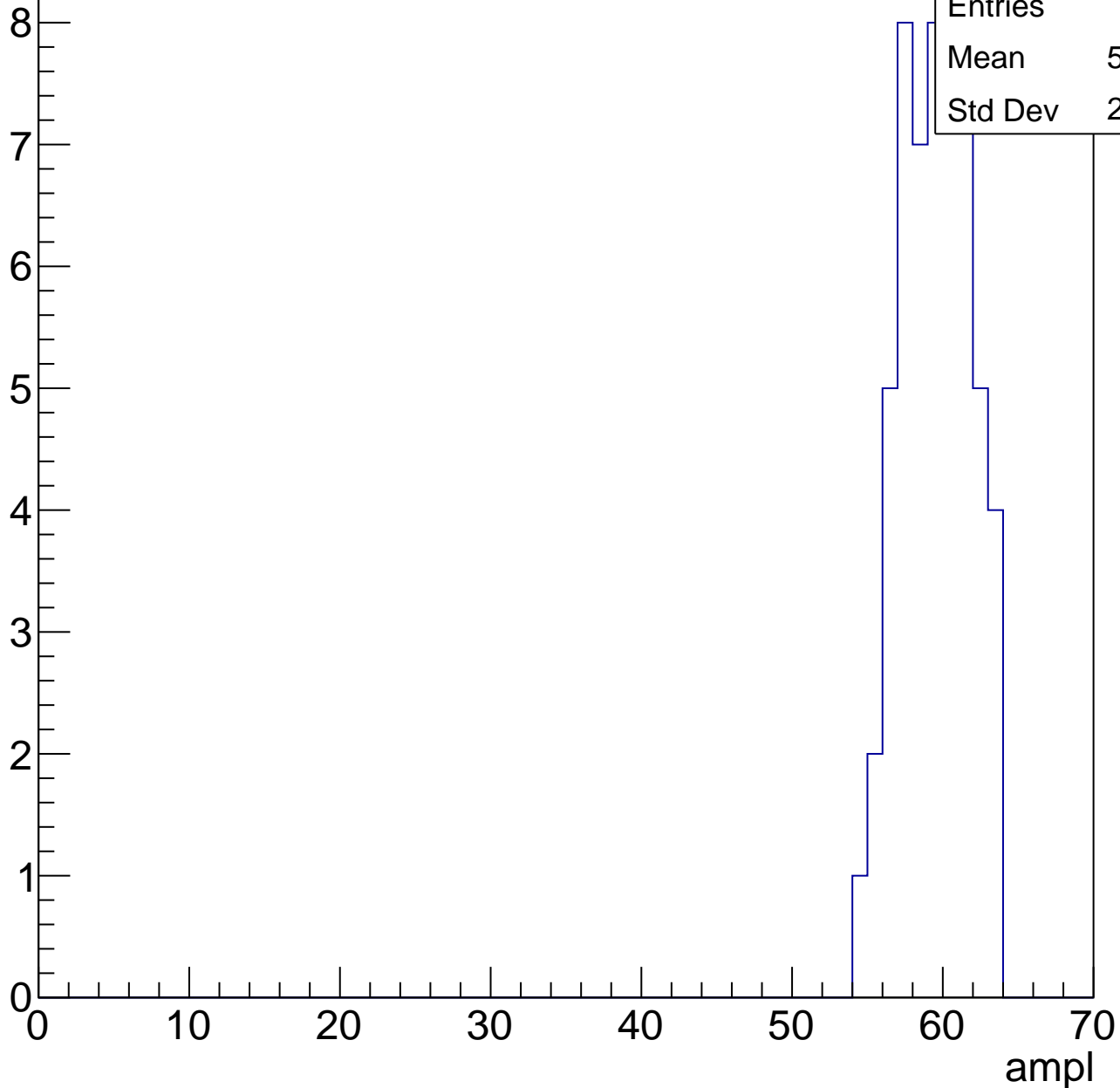
Entries	40
Mean	53.75
Std Dev	2.887



B0L002S, U17-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



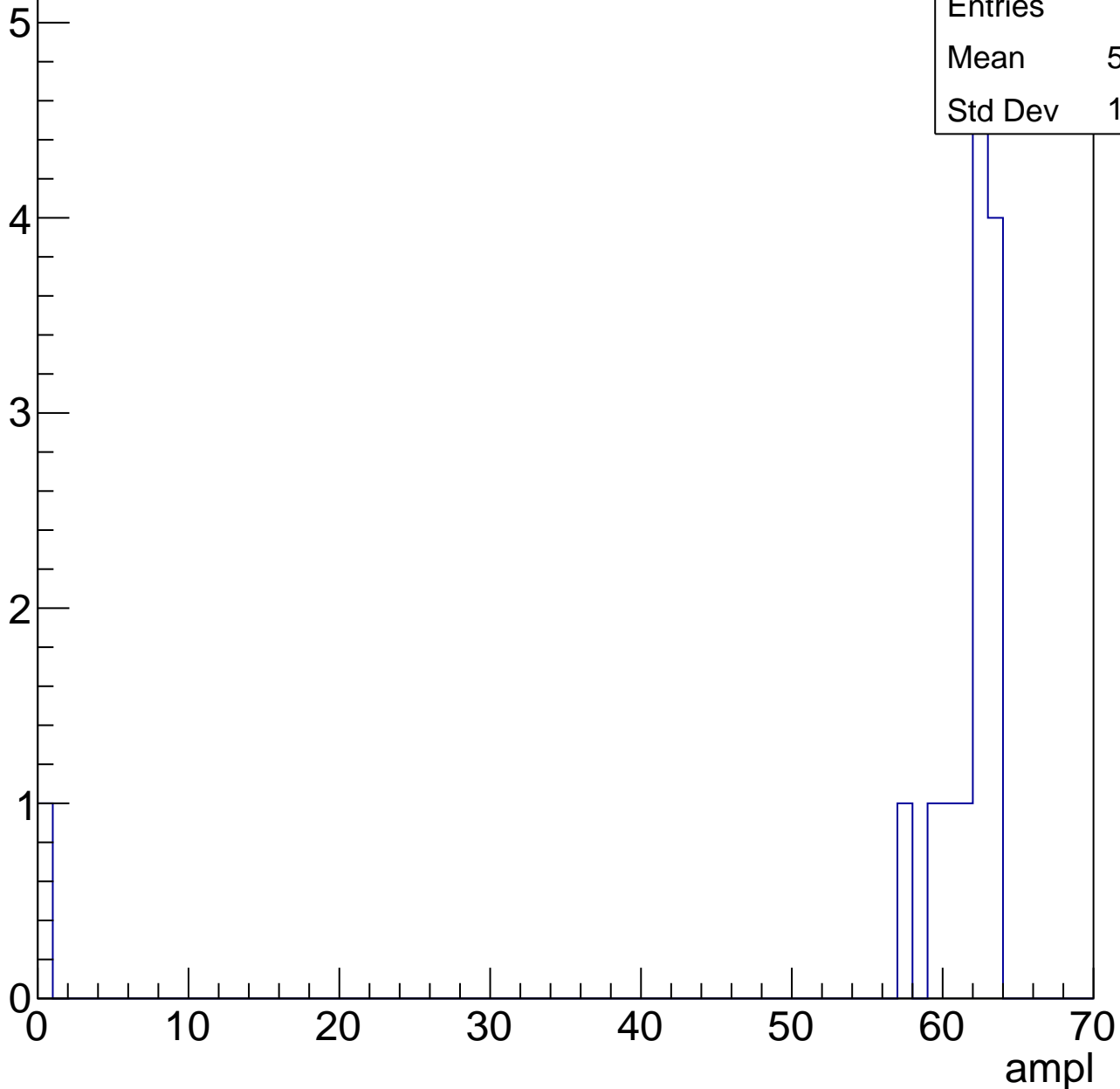
Entries	56
Mean	59.07
Std Dev	2.275

B0L002S, U17-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

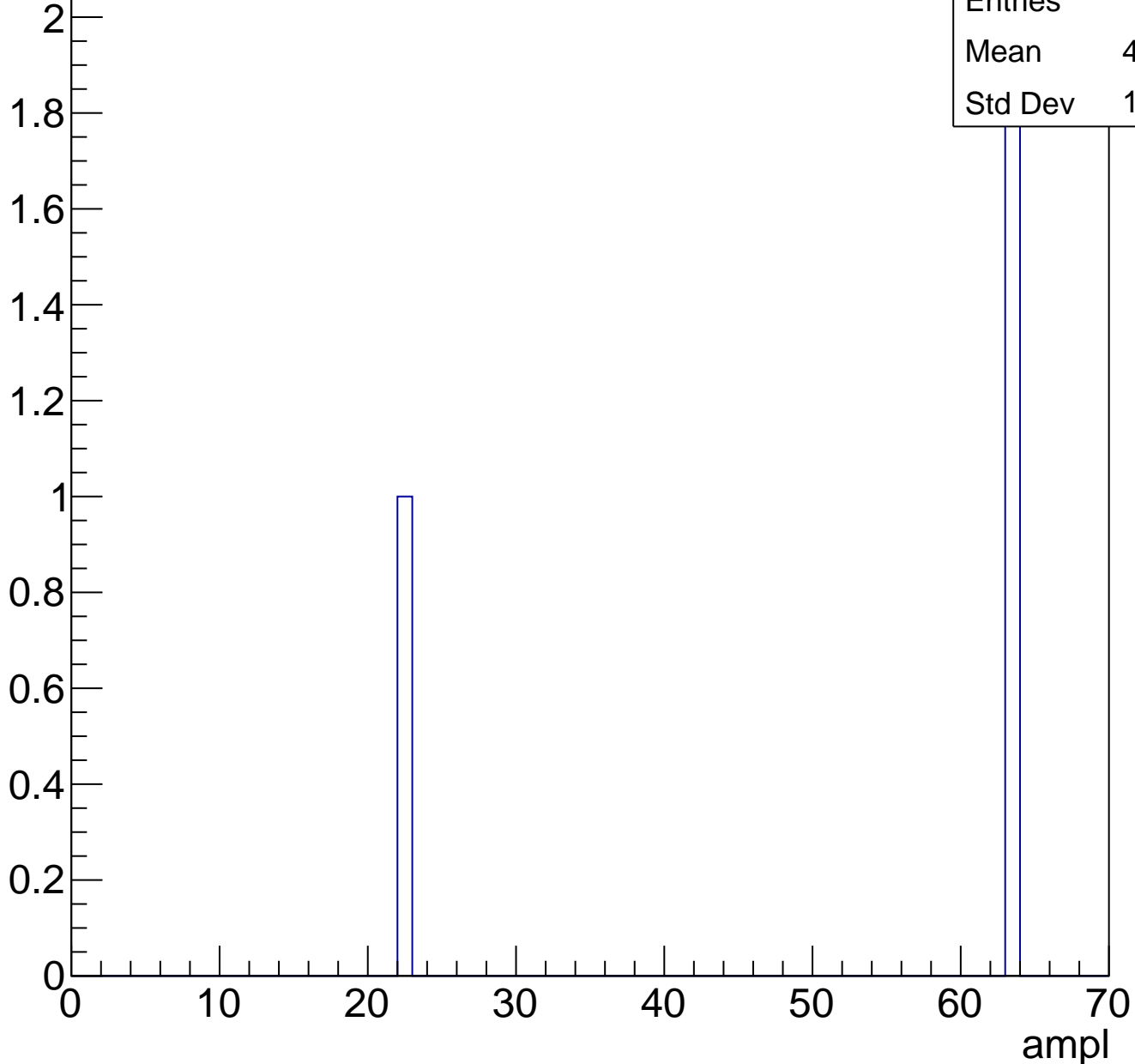
Entries	14
Mean	57.07
Std Dev	15.92



B0L002S, U17-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	49.33
Std Dev	19.33

B0L002S, U17-ch18, adc0

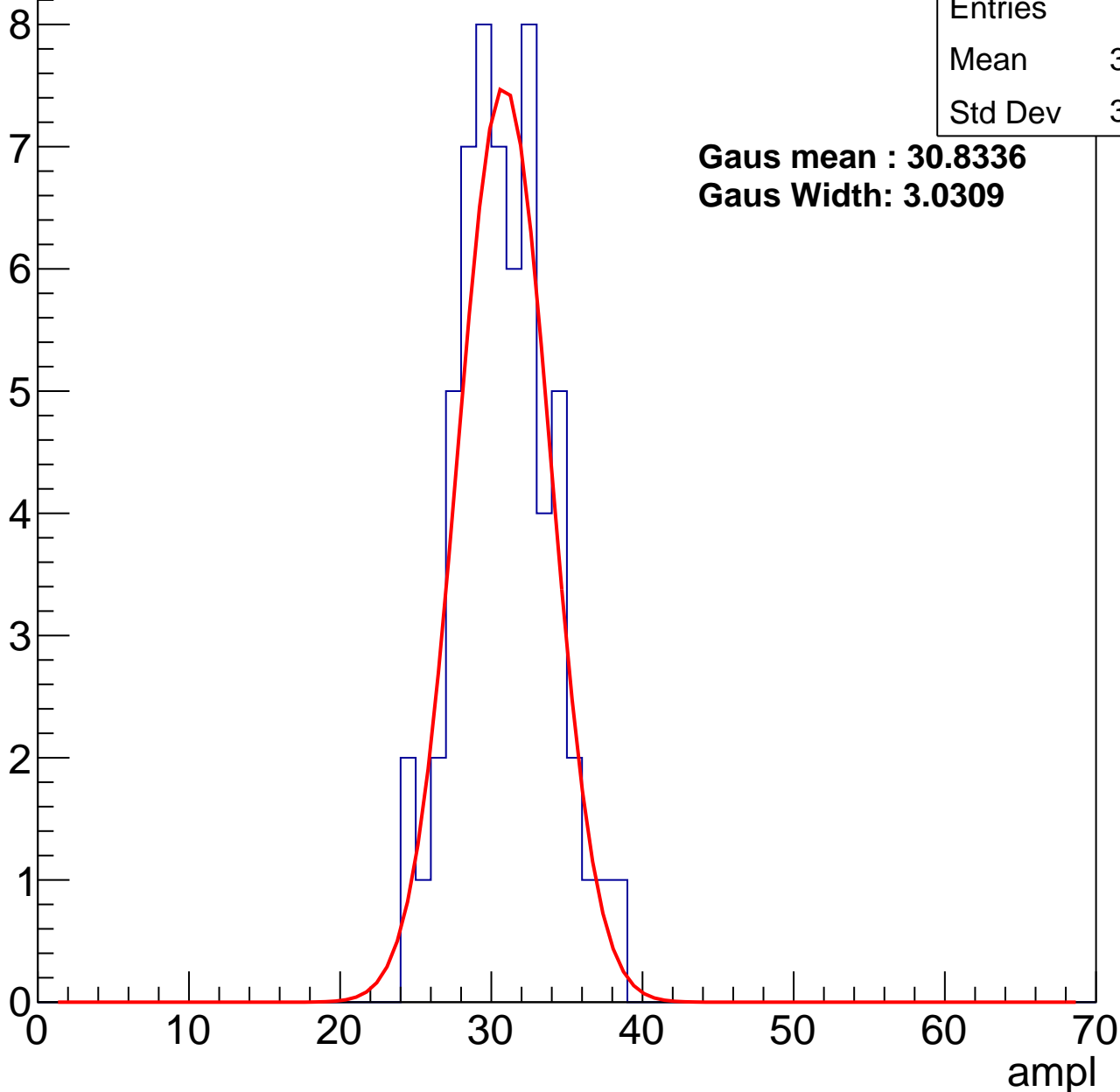
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	30.38
Std Dev	3.039

Gaus mean : 30.8336

Gaus Width: 3.0309



B0L002S, U17-ch18, adc1

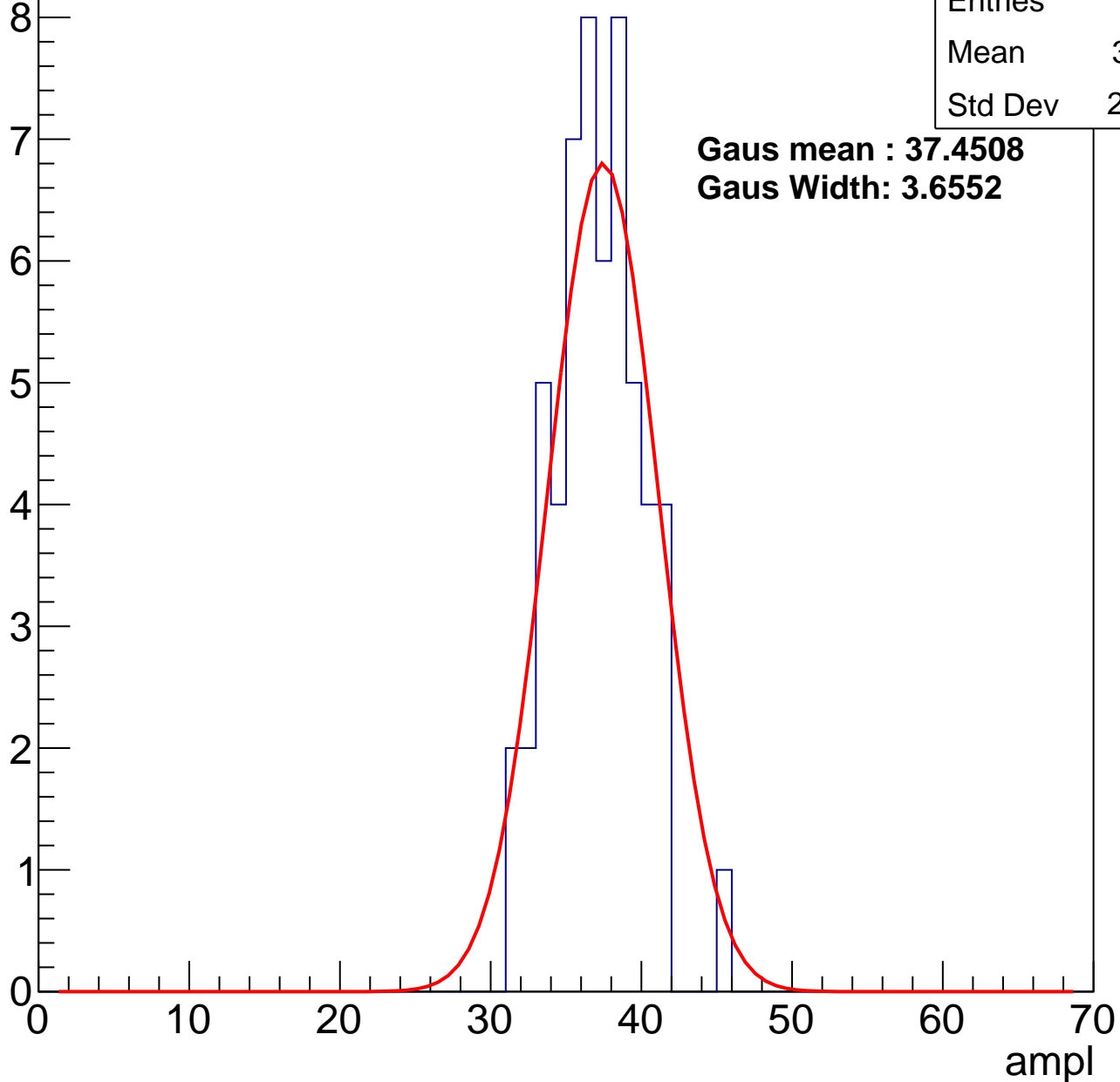
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.61
Std Dev	2.858

Gaus mean : 37.4508

Gaus Width: 3.6552



B0L002S, U17-ch18, adc2

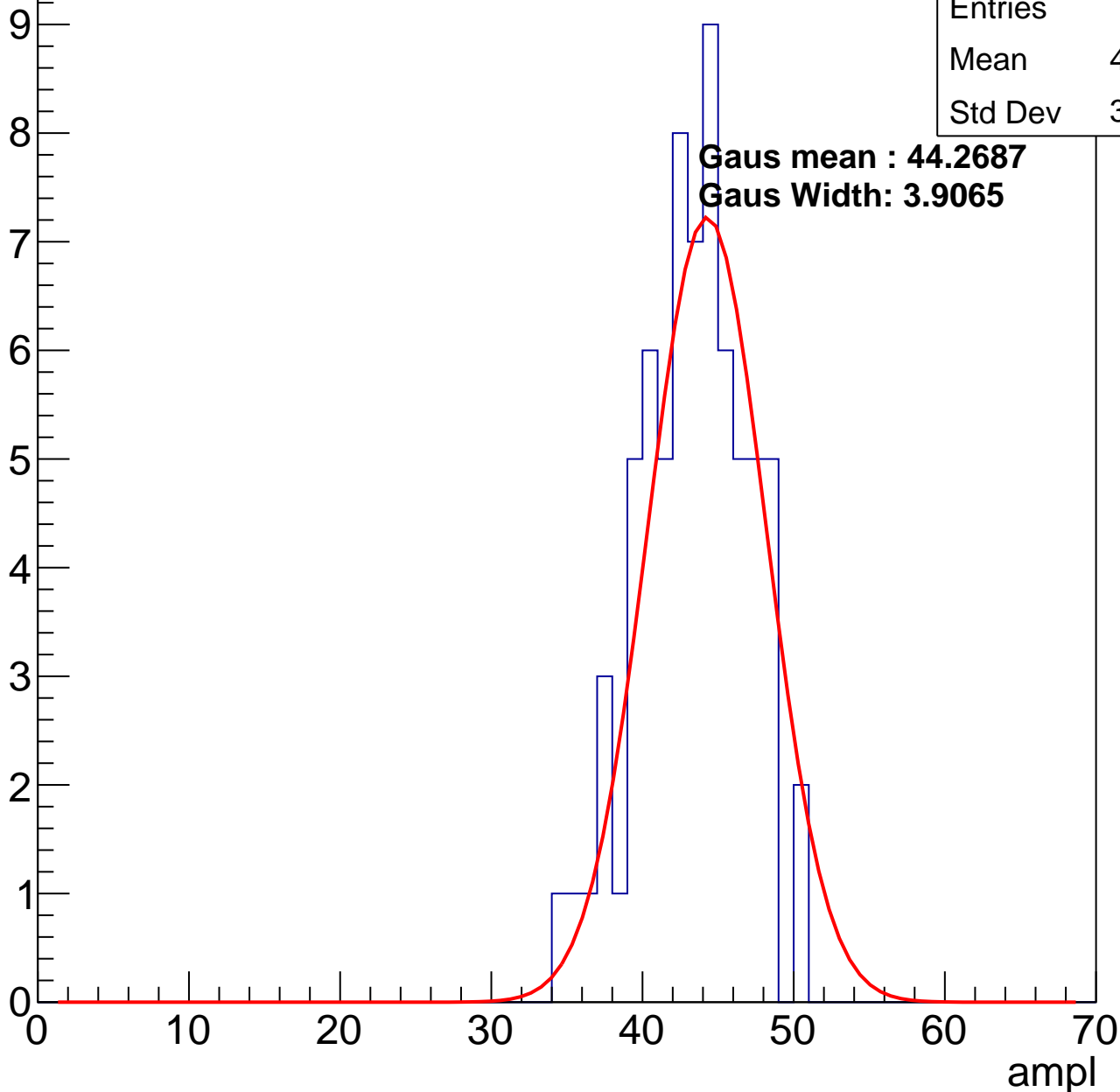
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.89
Std Dev	3.524

Gaus mean : 44.2687

Gaus Width: 3.9065

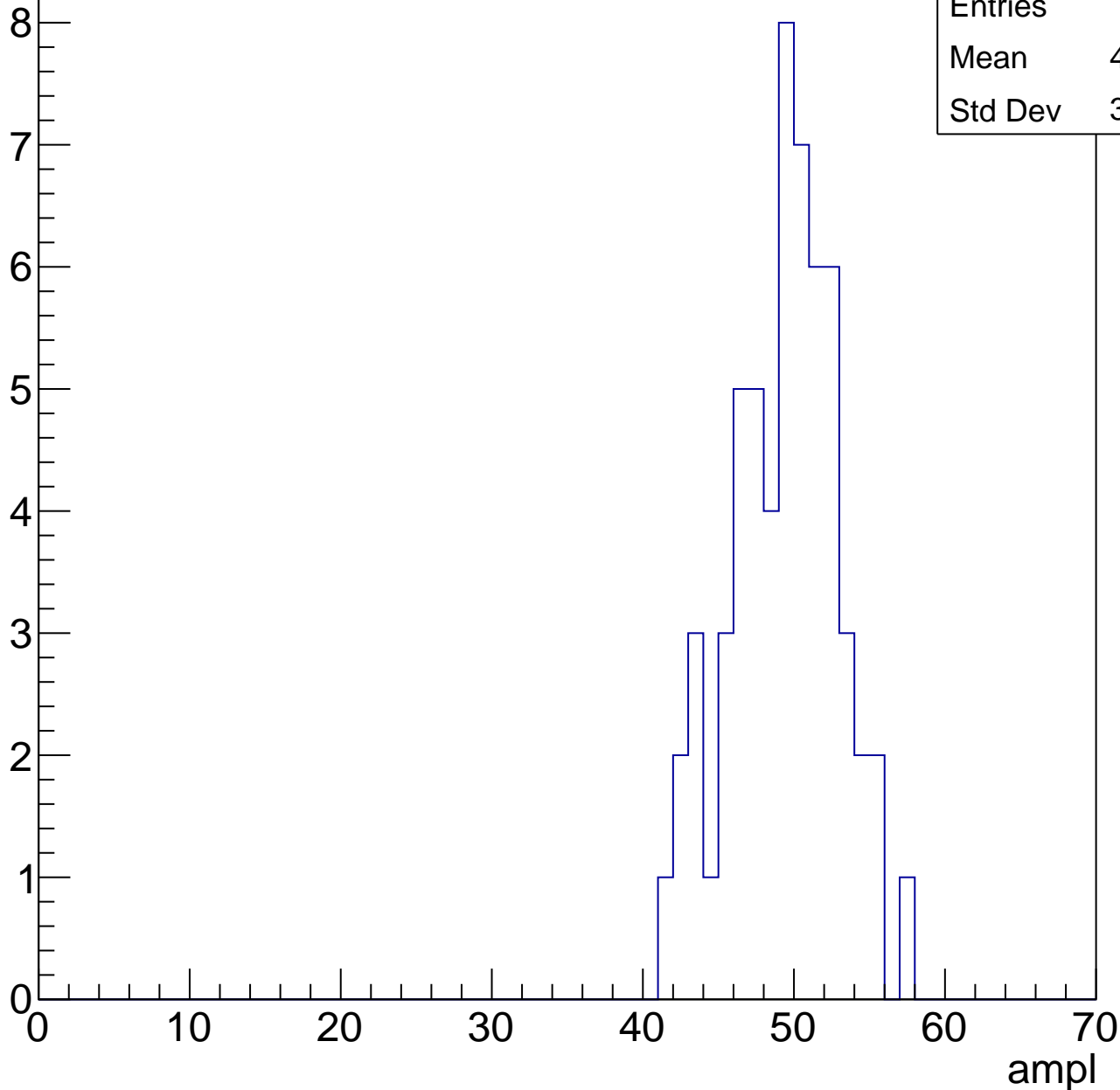


B0L002S, U17-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	48.88
Std Dev	3.518

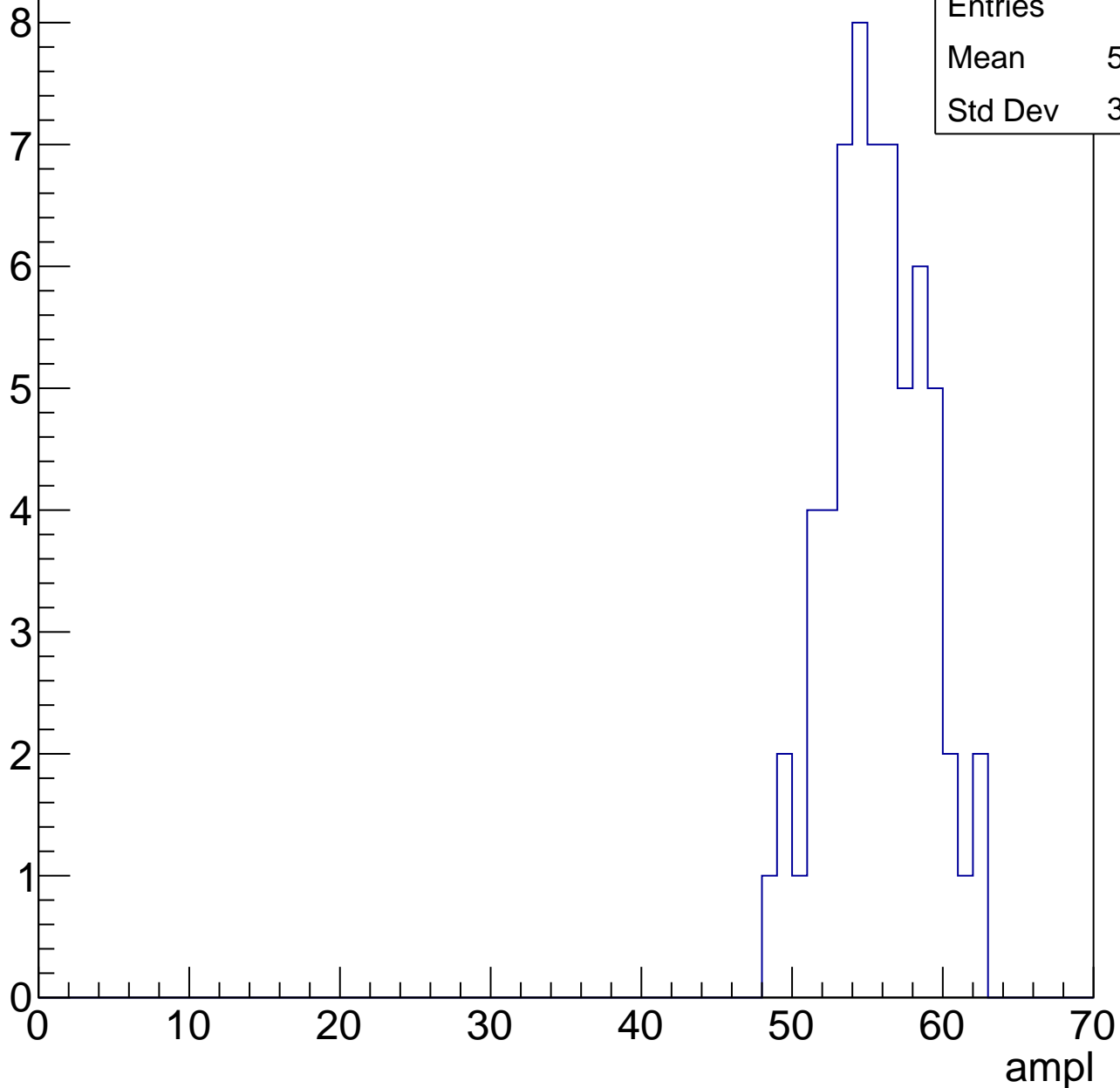


B0L002S, U17-ch18, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.18
Std Dev	3.175

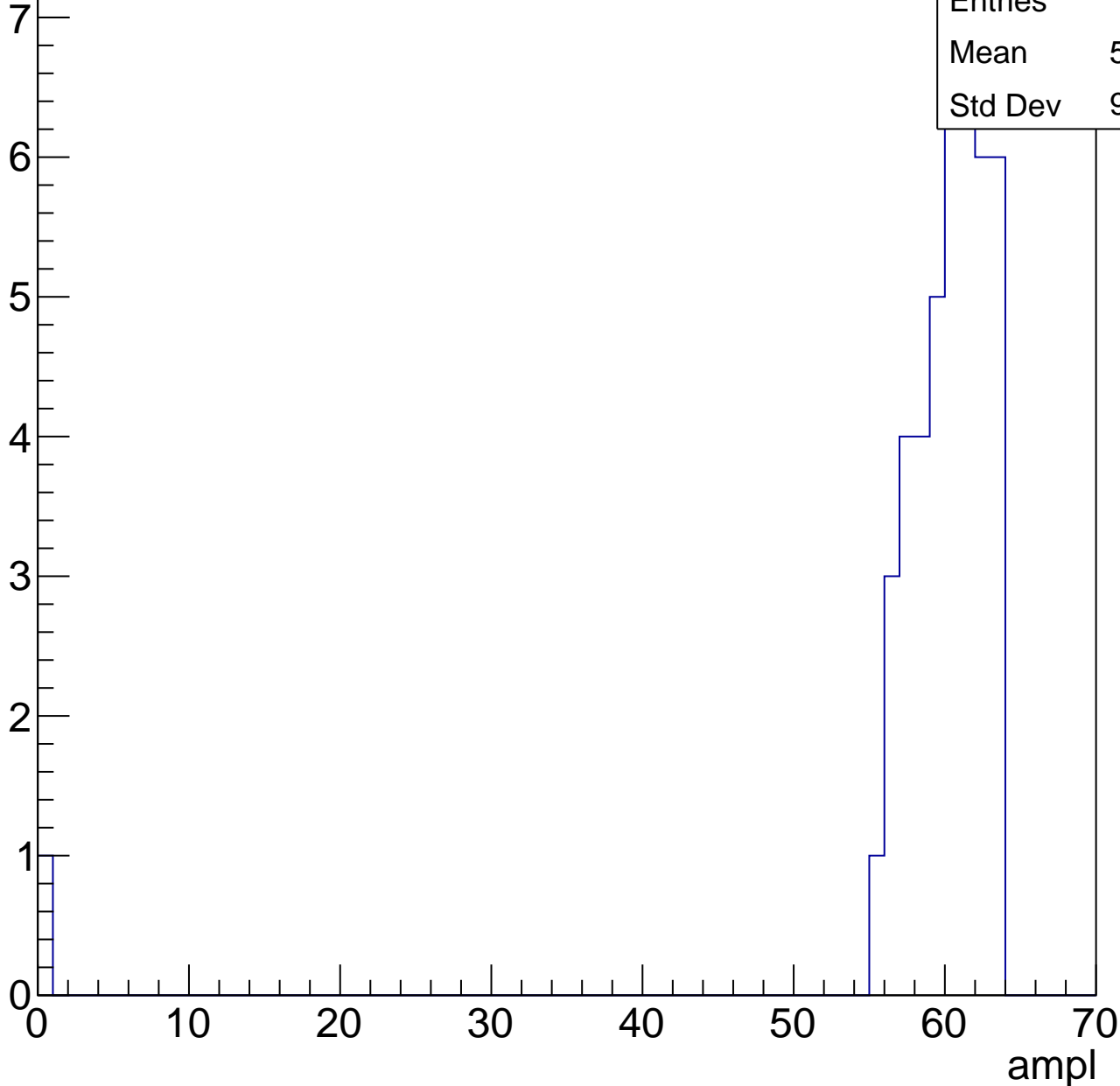


B0L002S, U17-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

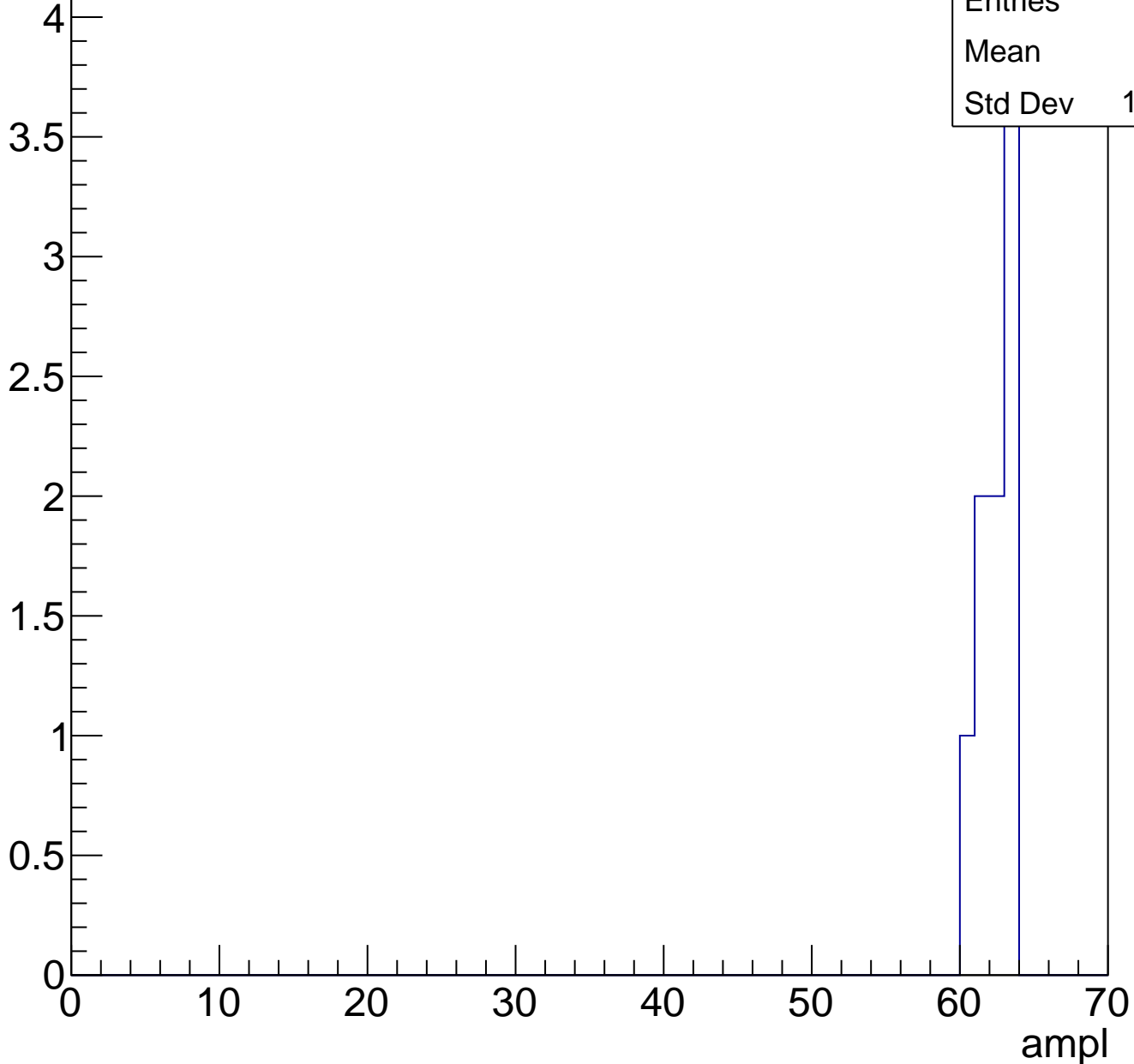
Entries	44
Mean	58.52
Std Dev	9.194



B0L002S, U17-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U17-ch19, adc0

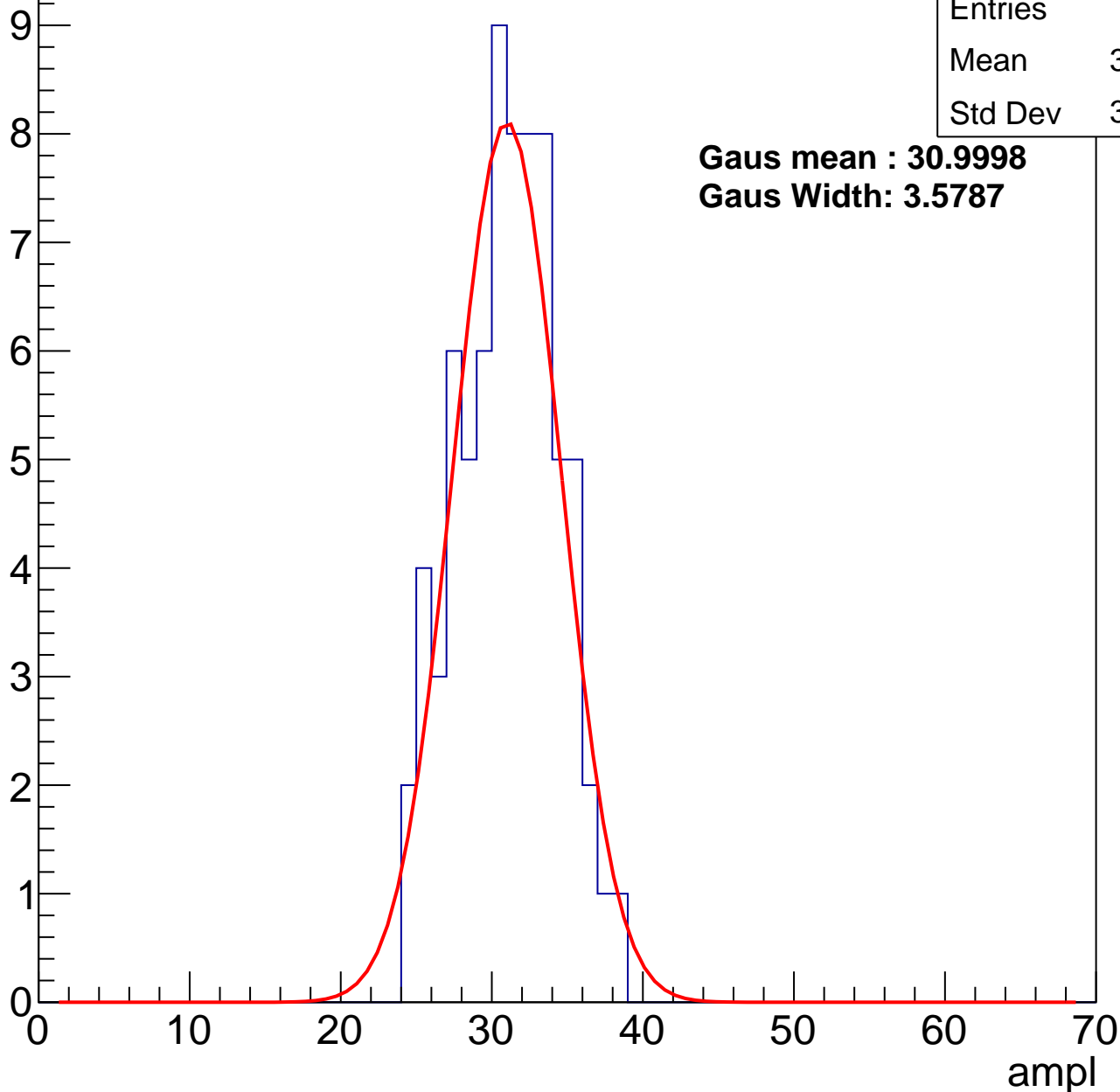
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.58
Std Dev	3.264

Gaus mean : 30.9998

Gaus Width: 3.5787



B0L002S, U17-ch19, adc1

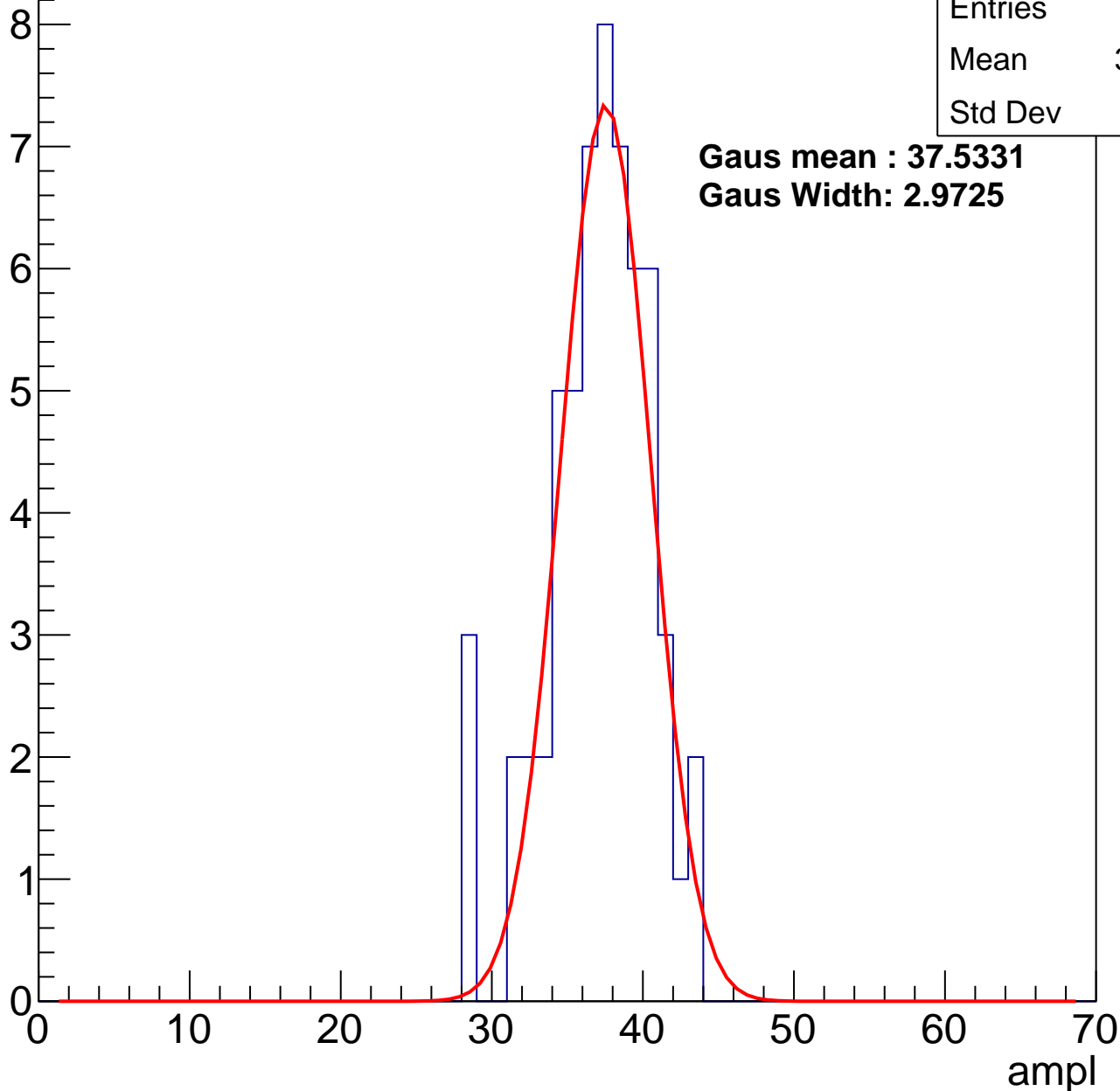
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.61
Std Dev	3.42

Gaus mean : 37.5331

Gaus Width: 2.9725



B0L002S, U17-ch19, adc2

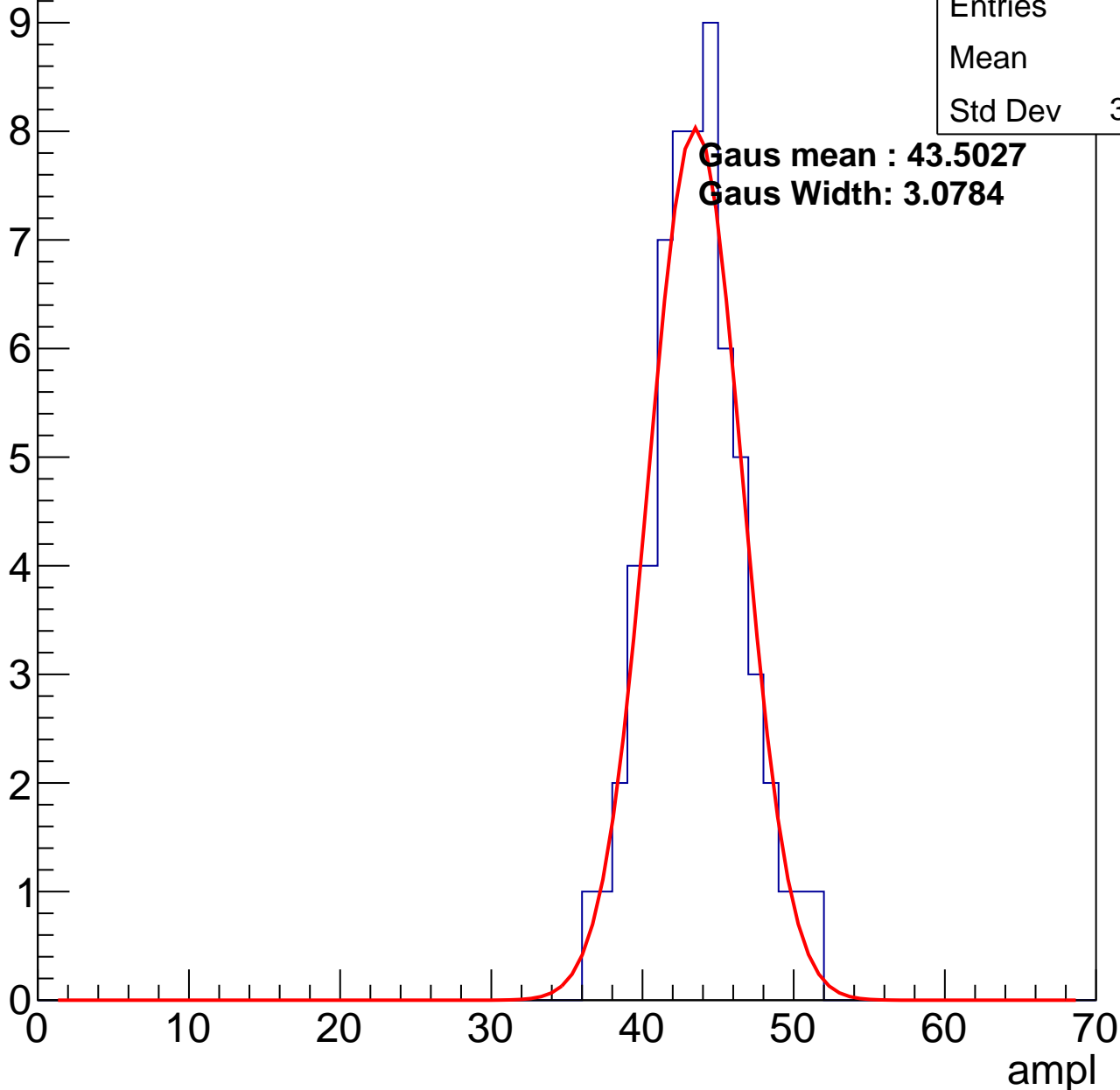
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.1
Std Dev	3.074

Gaus mean : 43.5027

Gaus Width: 3.0784

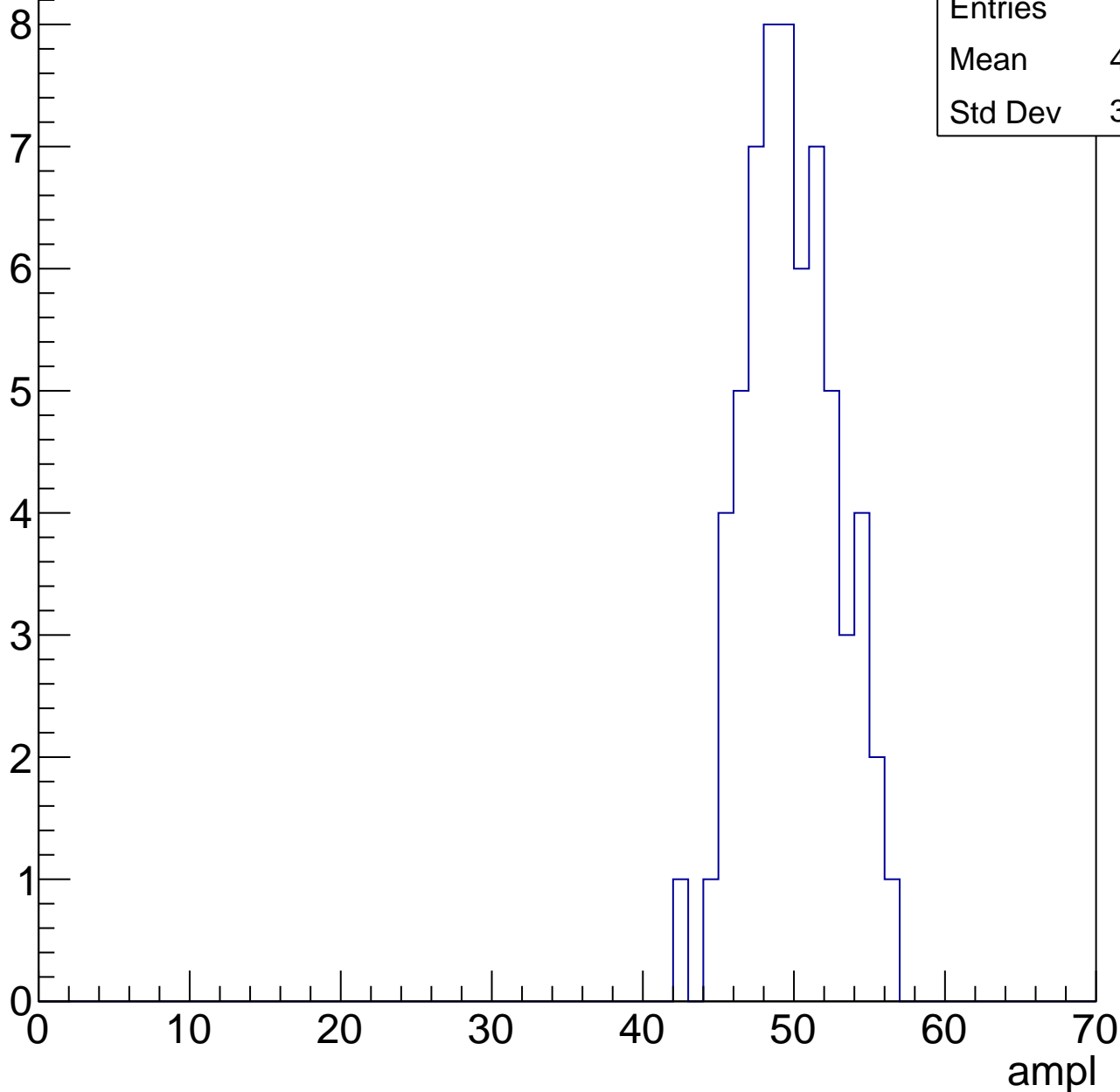


B0L002S, U17-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	49.34
Std Dev	3.005

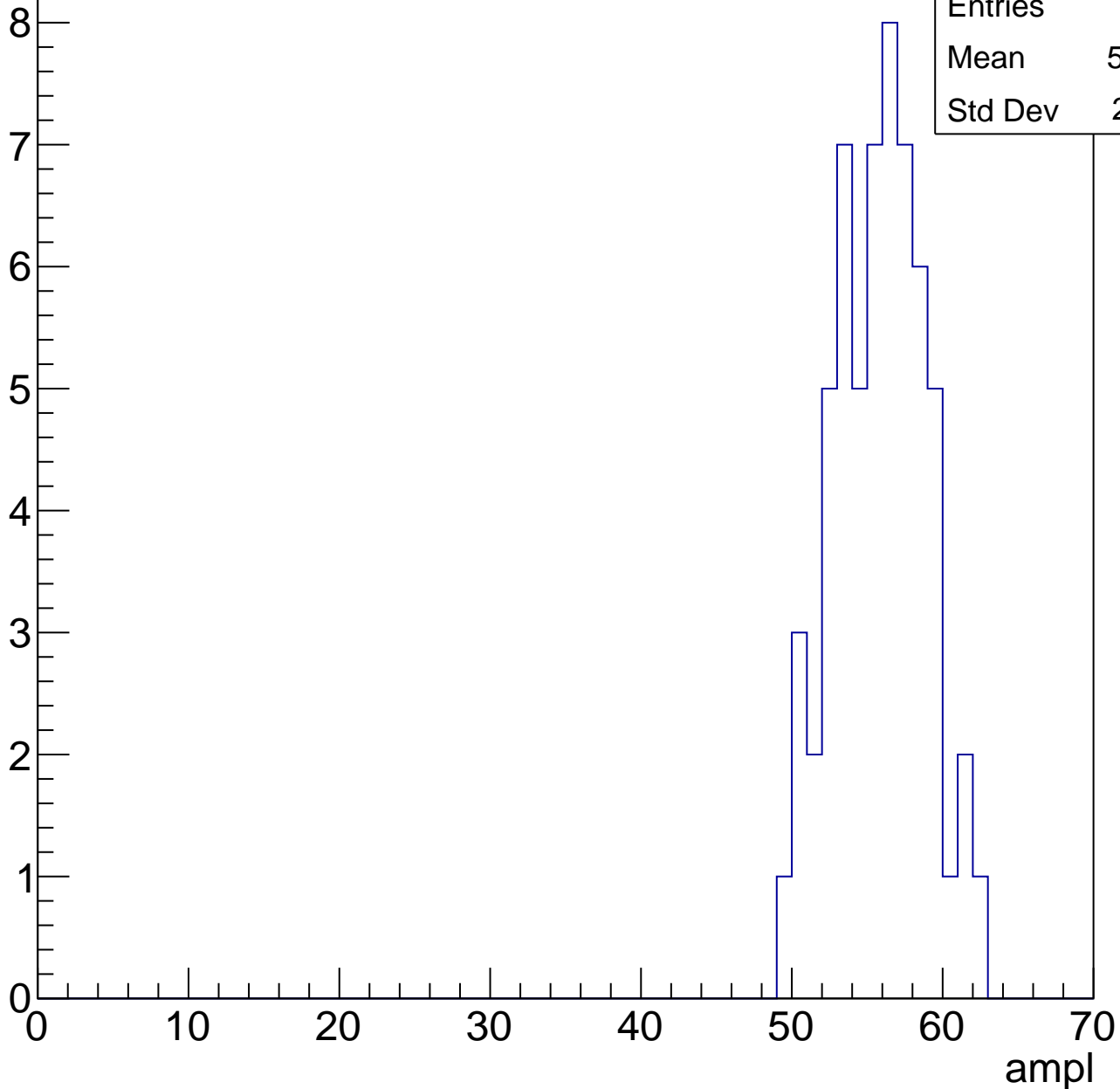


B0L002S, U17-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	55.35
Std Dev	2.971



B0L002S, U17-ch19, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 40

Mean 58.52

Std Dev 9.641

ampl

0

10

20

30

40

50

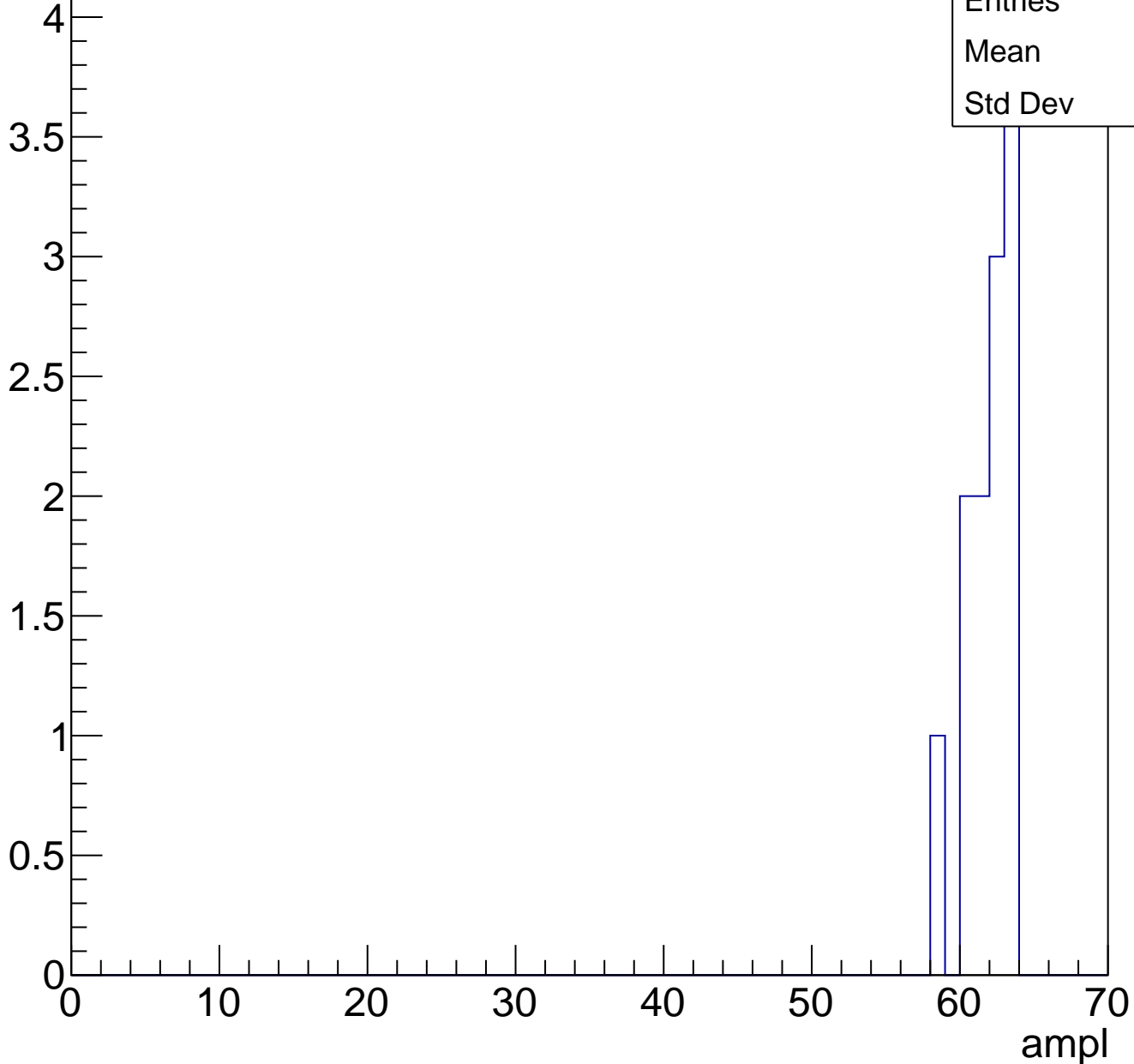
60

70

B0L002S, U17-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch20, adc0

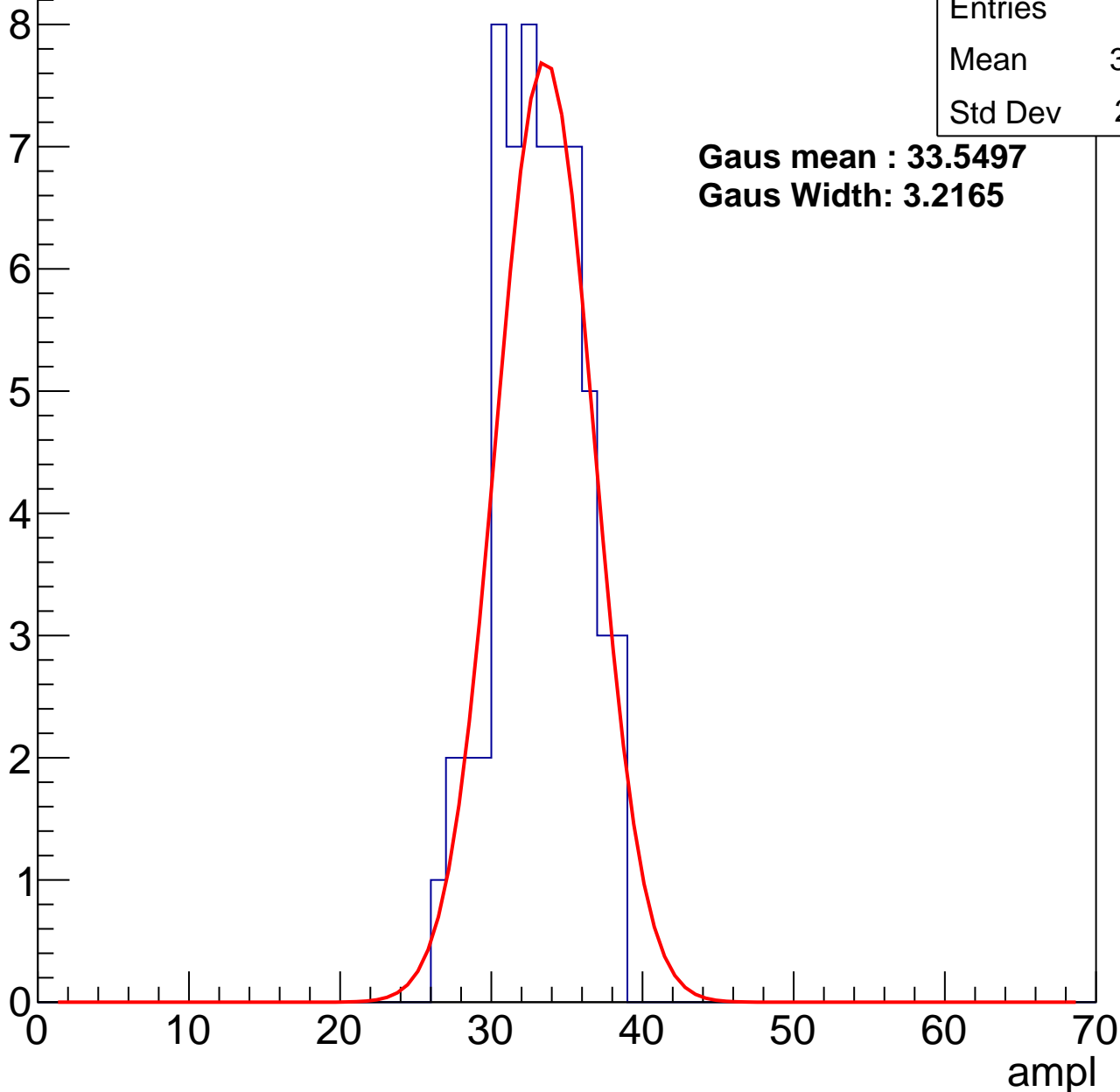
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	32.68
Std Dev	2.861

Gaus mean : 33.5497

Gaus Width: 3.2165



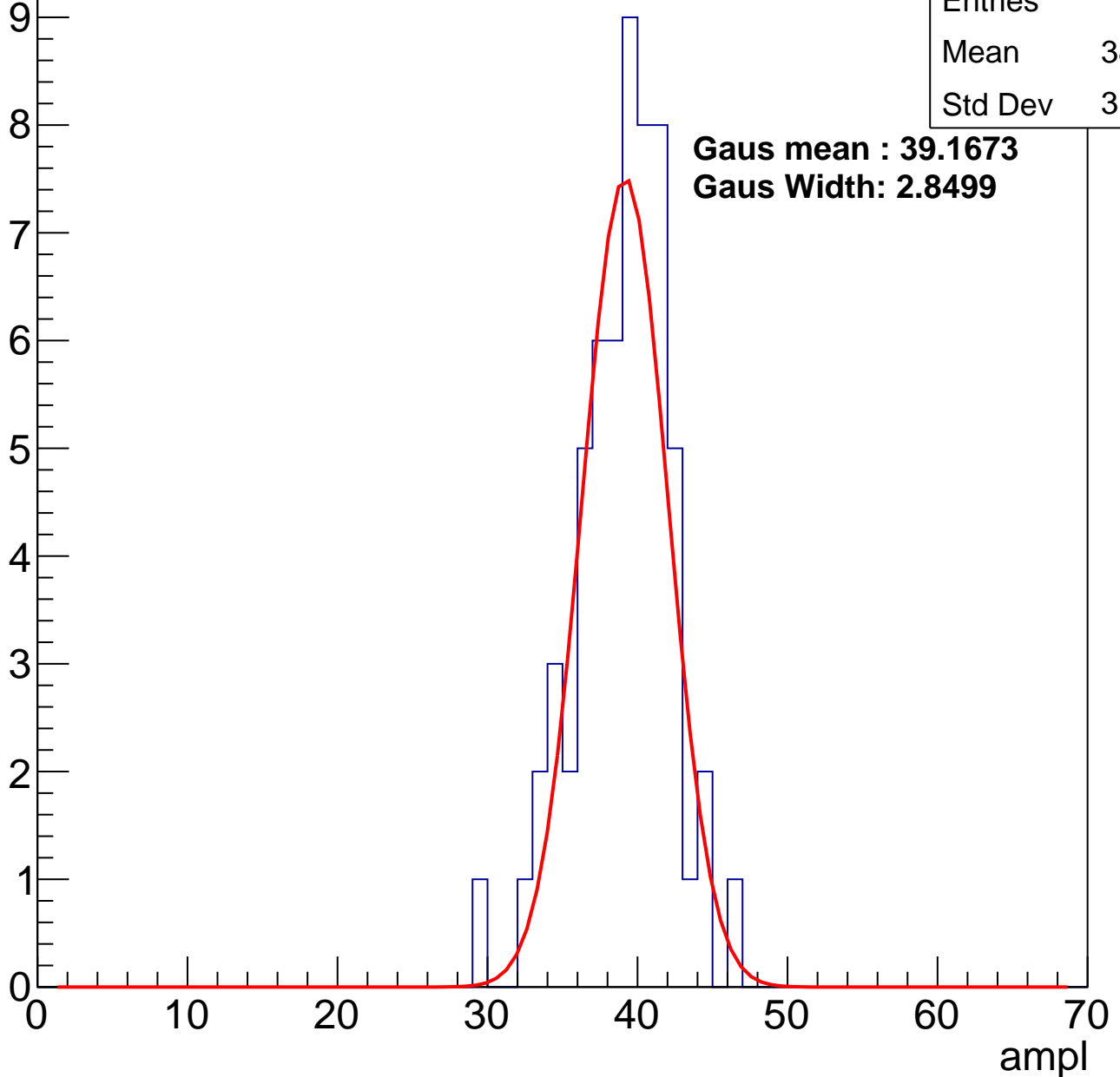
B0L002S, U17-ch20, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	38.58
Std Dev	3.143

Gaus mean : 39.1673
Gaus Width: 2.8499



B0L002S, U17-ch20, adc2

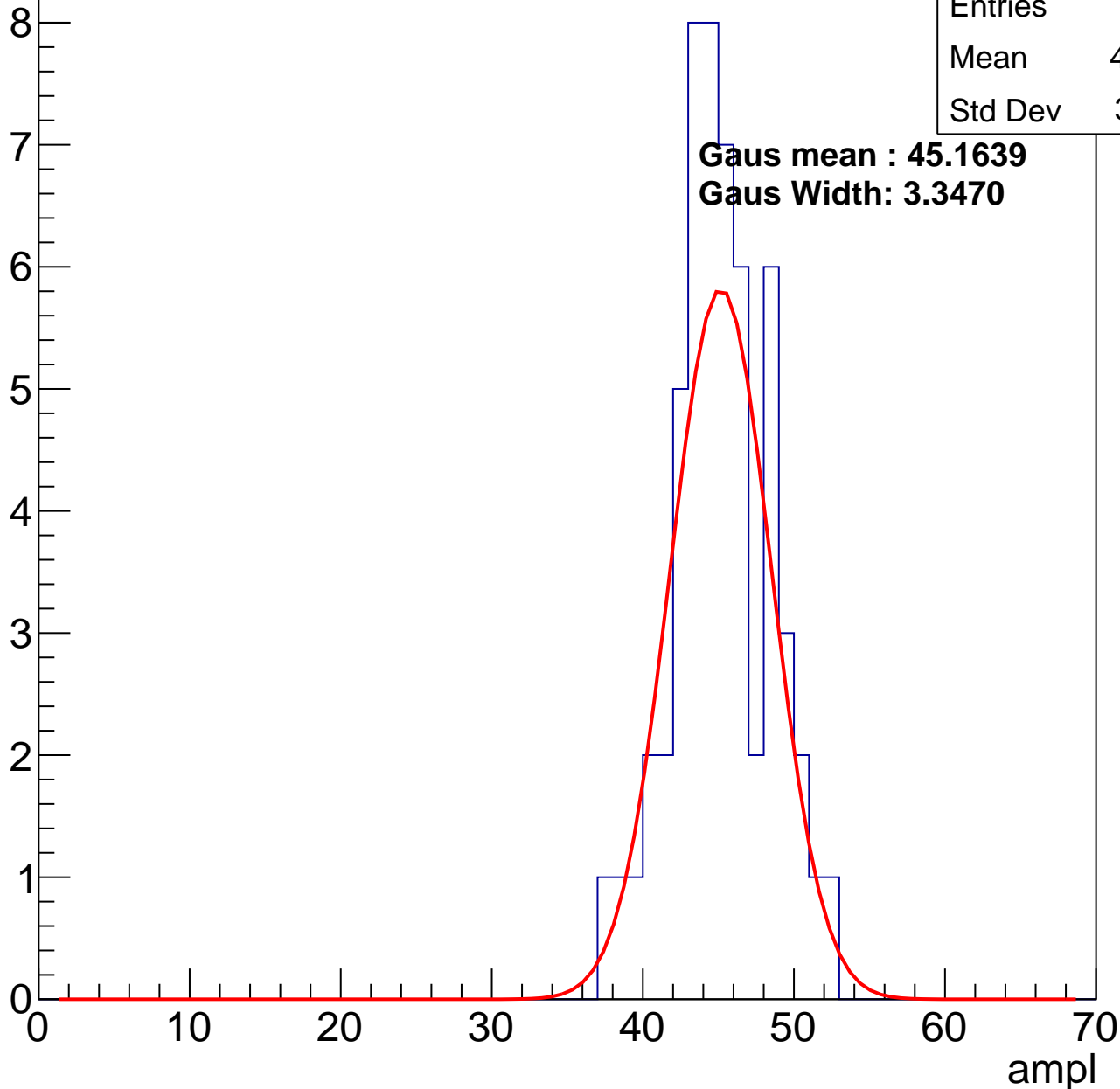
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	44.73
Std Dev	3.171

Gaus mean : 45.1639

Gaus Width: 3.3470

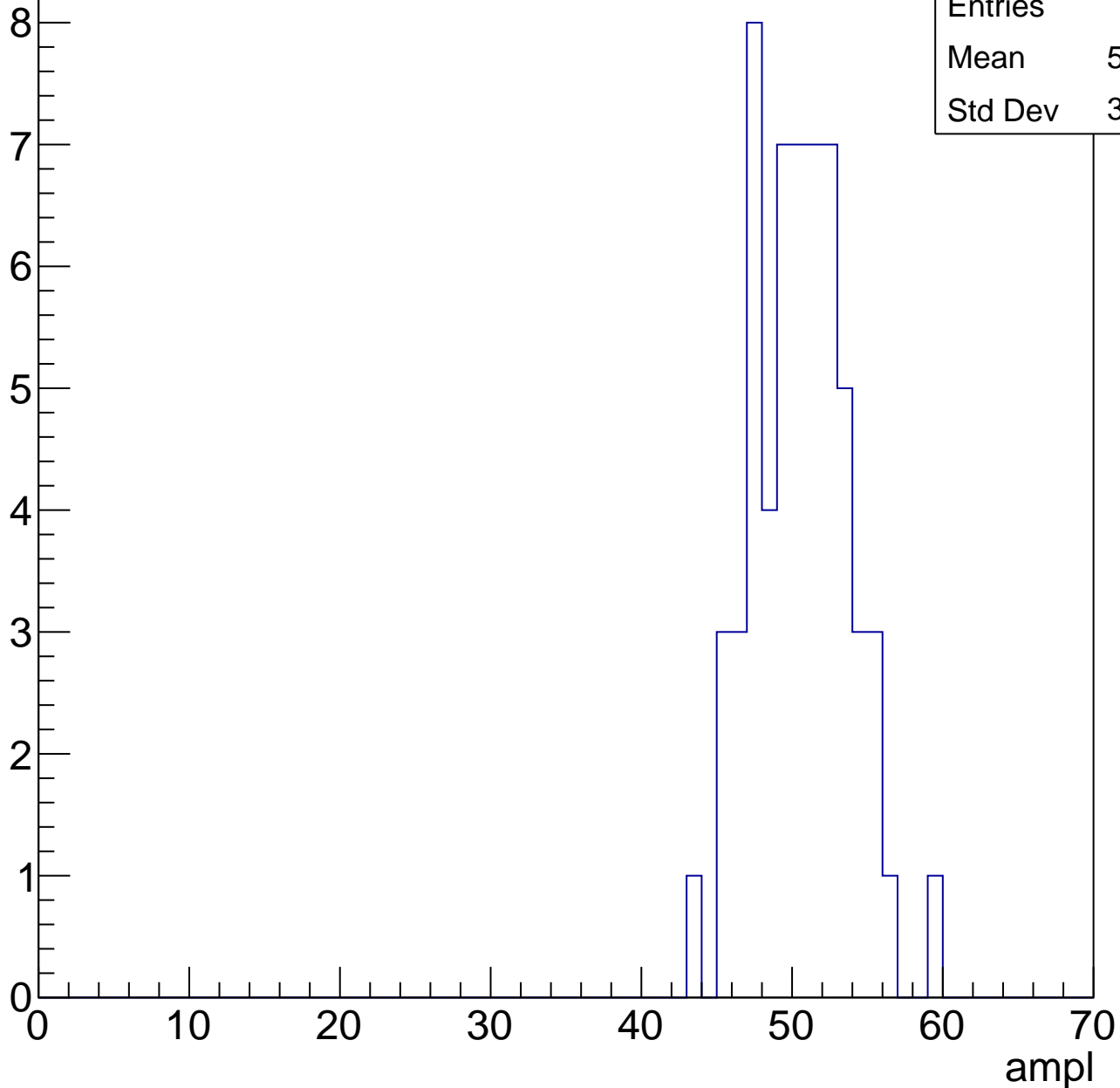


B0L002S, U17-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

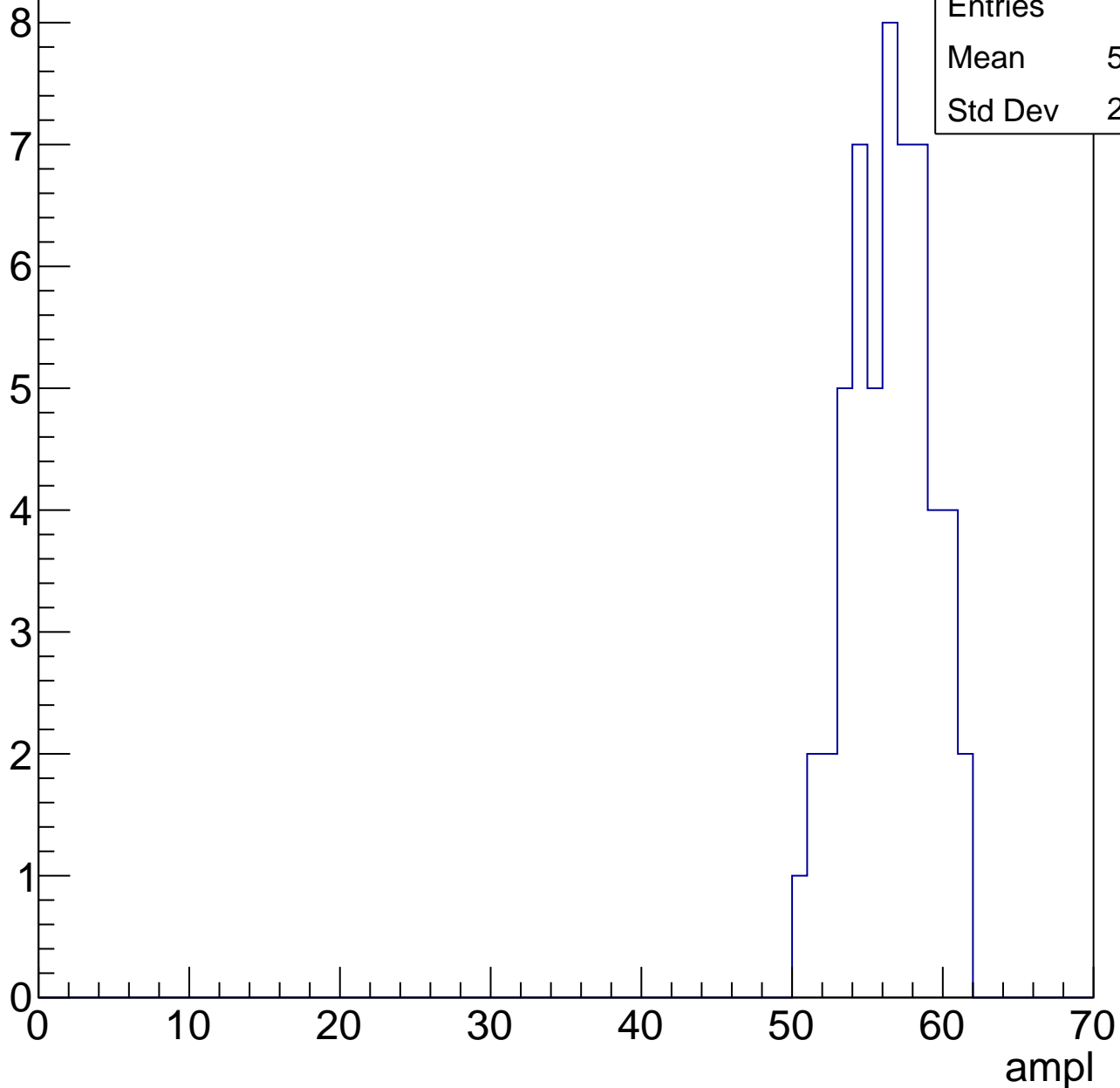
Entries	60
Mean	50.08
Std Dev	3.127



B0L002S, U17-ch20, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

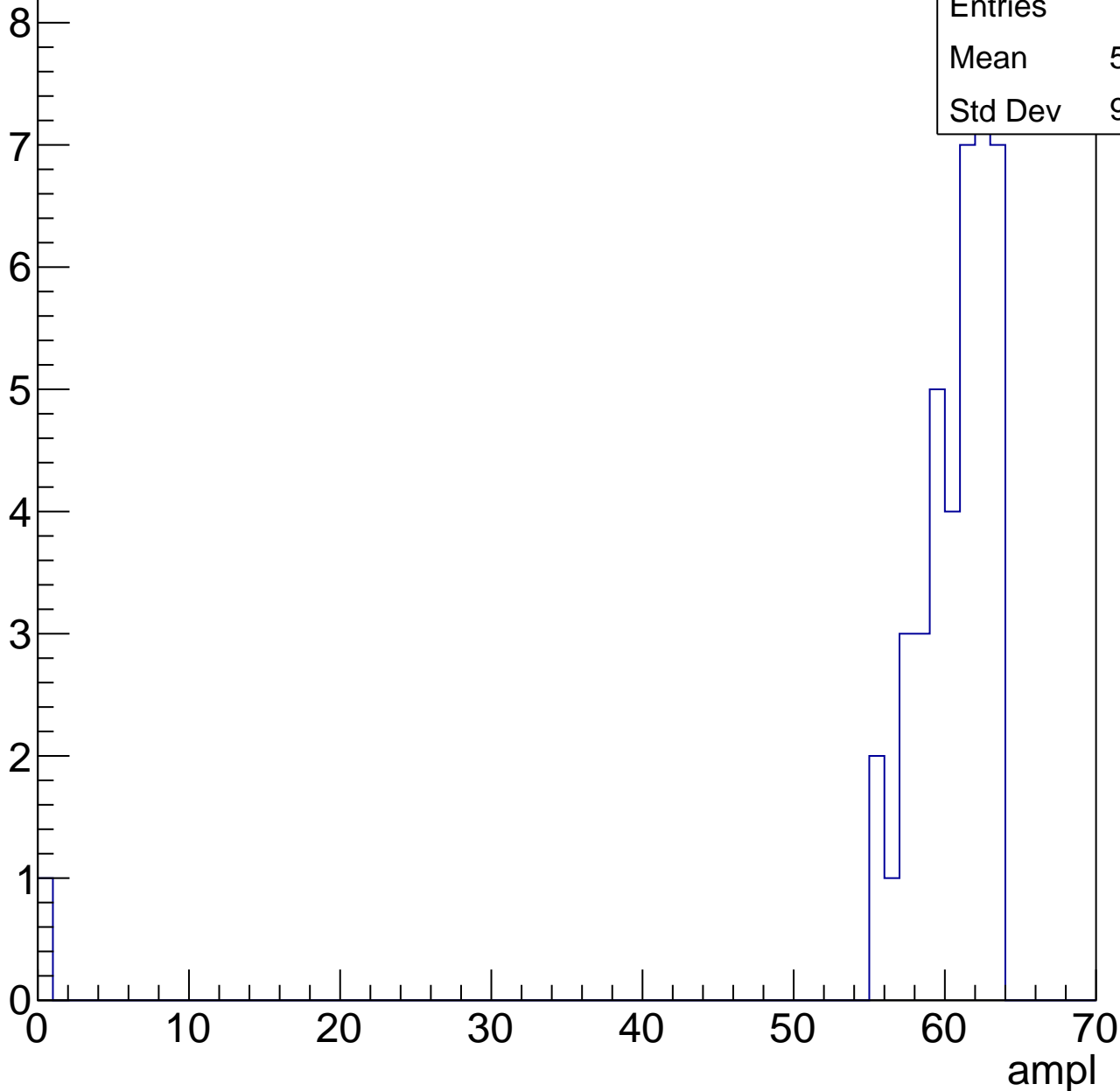


B0L002S, U17-ch20, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

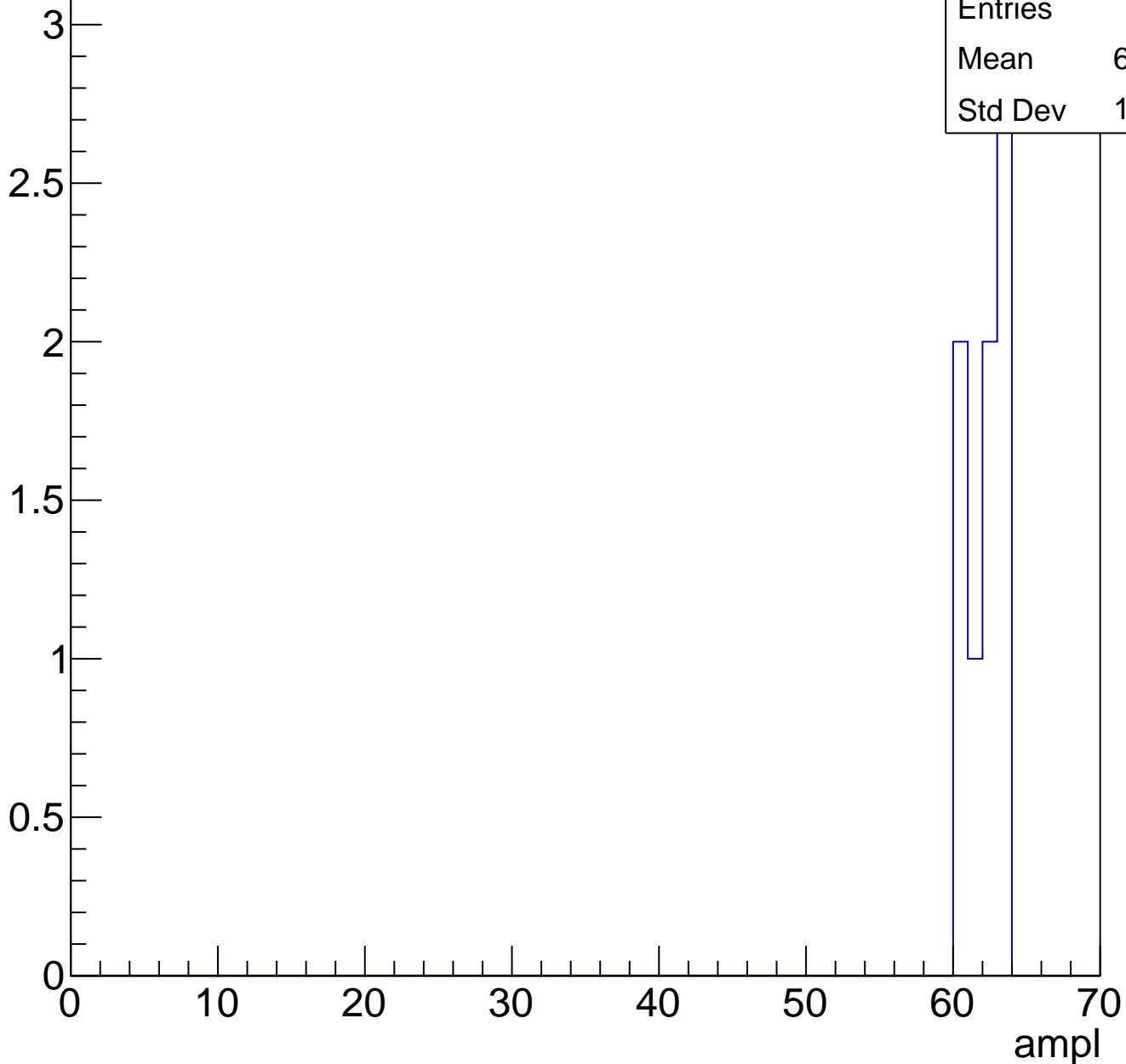
Entries	41
Mean	58.78
Std Dev	9.565



B0L002S, U17-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

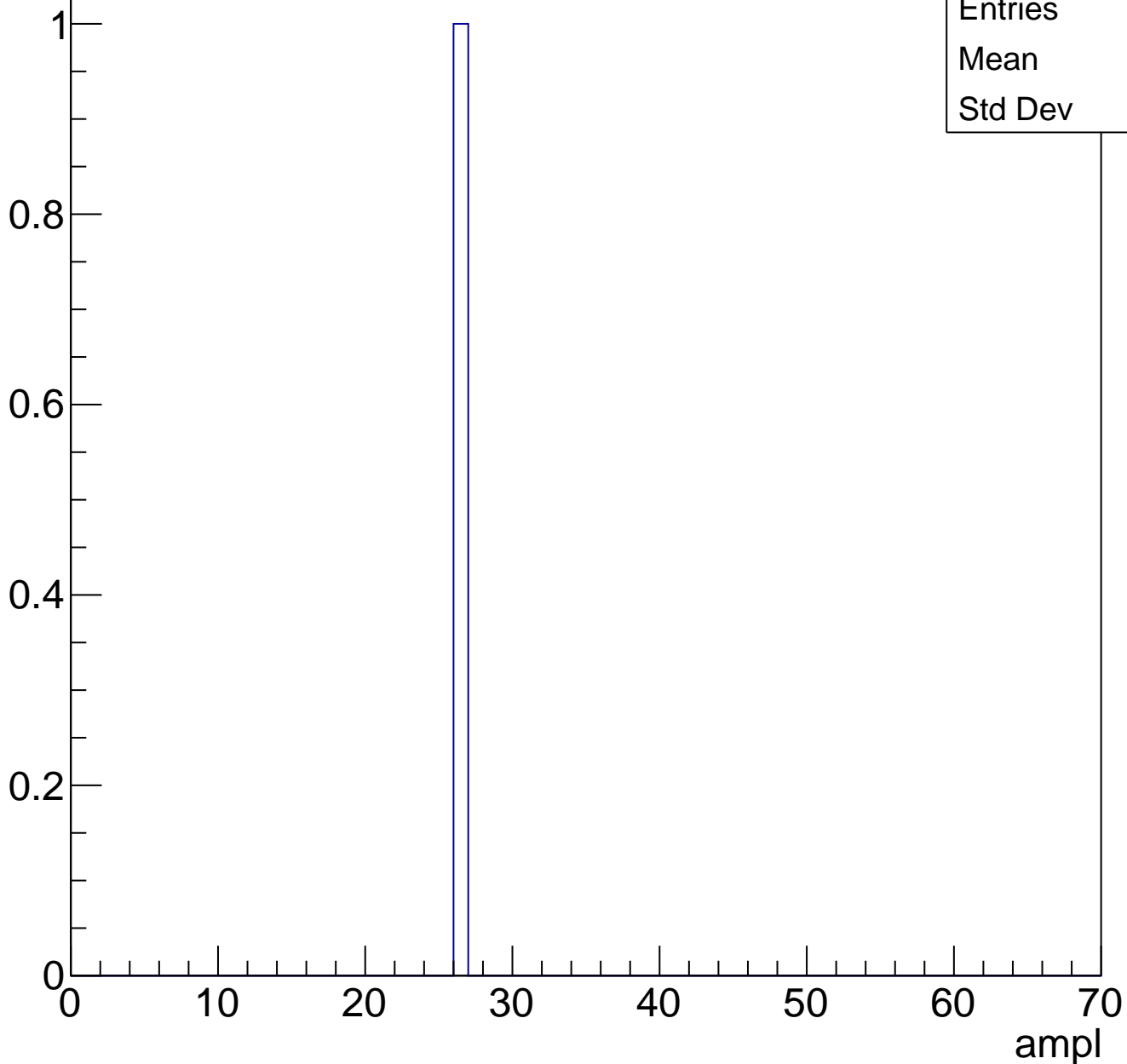
Entry



B0L002S, U17-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	26
Std Dev	0

B0L002S, U17-ch21, adc0

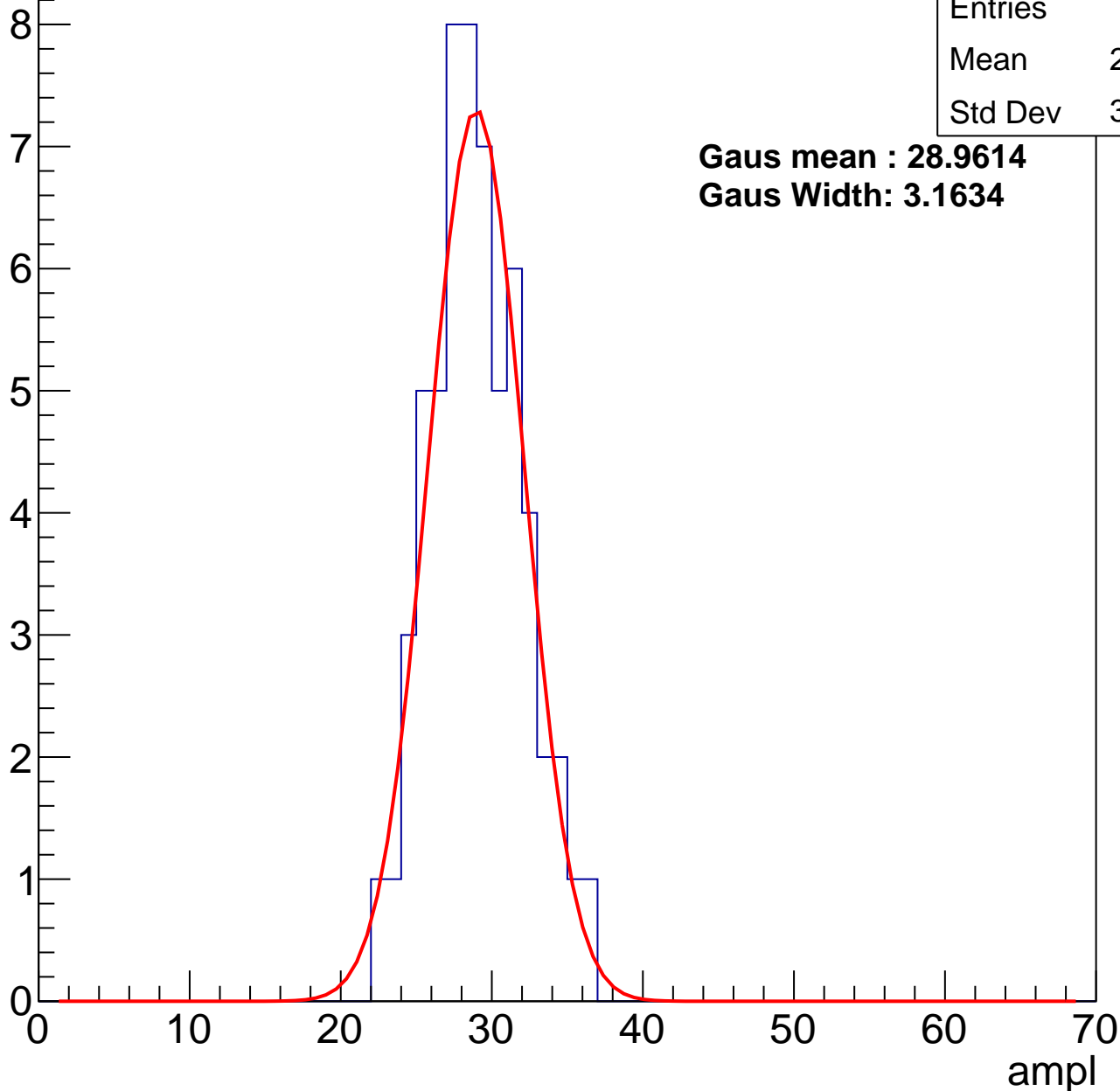
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	28.54
Std Dev	3.038

Gaus mean : 28.9614

Gaus Width: 3.1634



B0L002S, U17-ch21, adc1

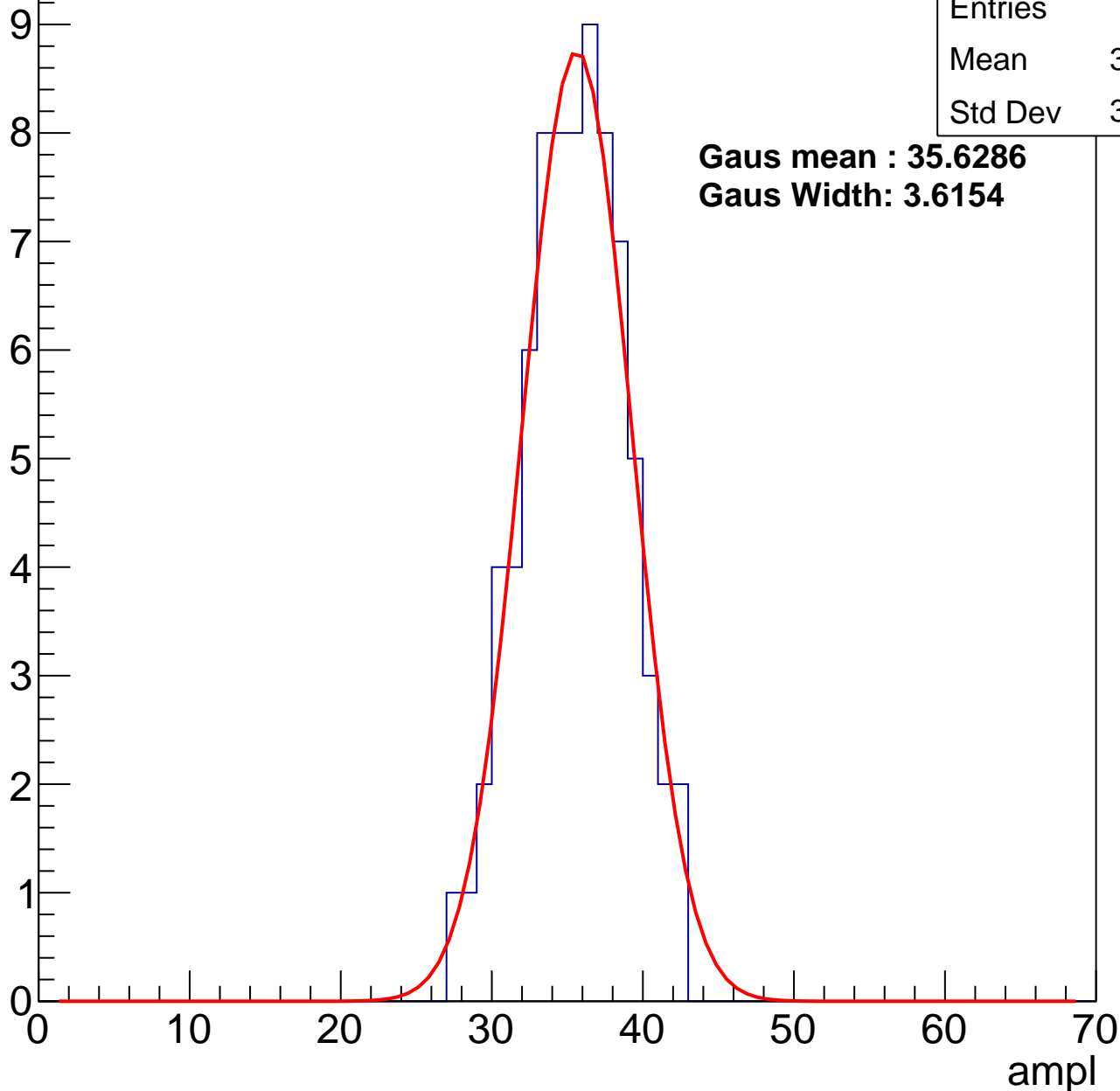
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	35.03
Std Dev	3.343

Gaus mean : 35.6286

Gaus Width: 3.6154



B0L002S, U17-ch21, adc2

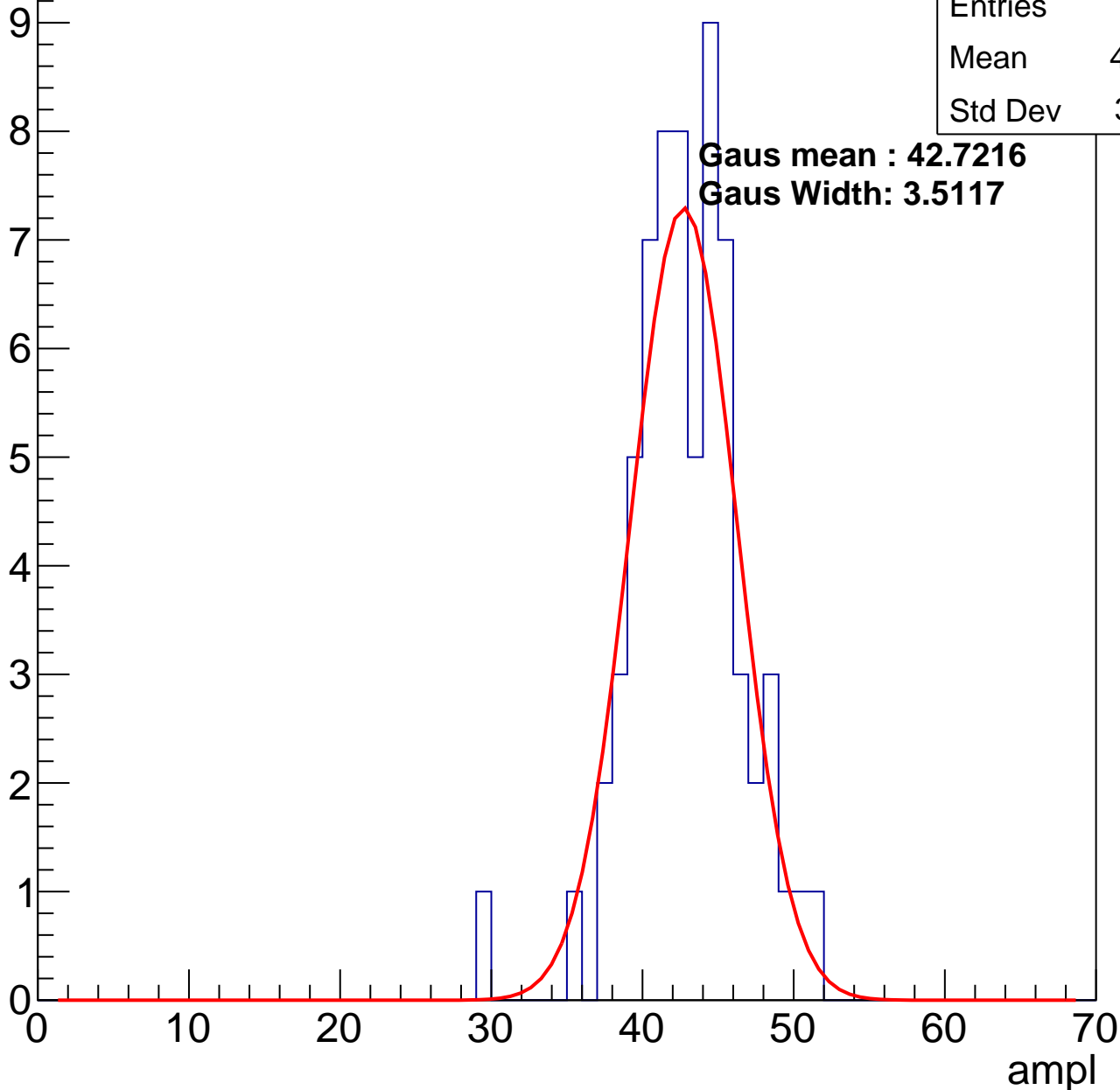
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.43
Std Dev	3.641

Gaus mean : 42.7216

Gaus Width: 3.5117

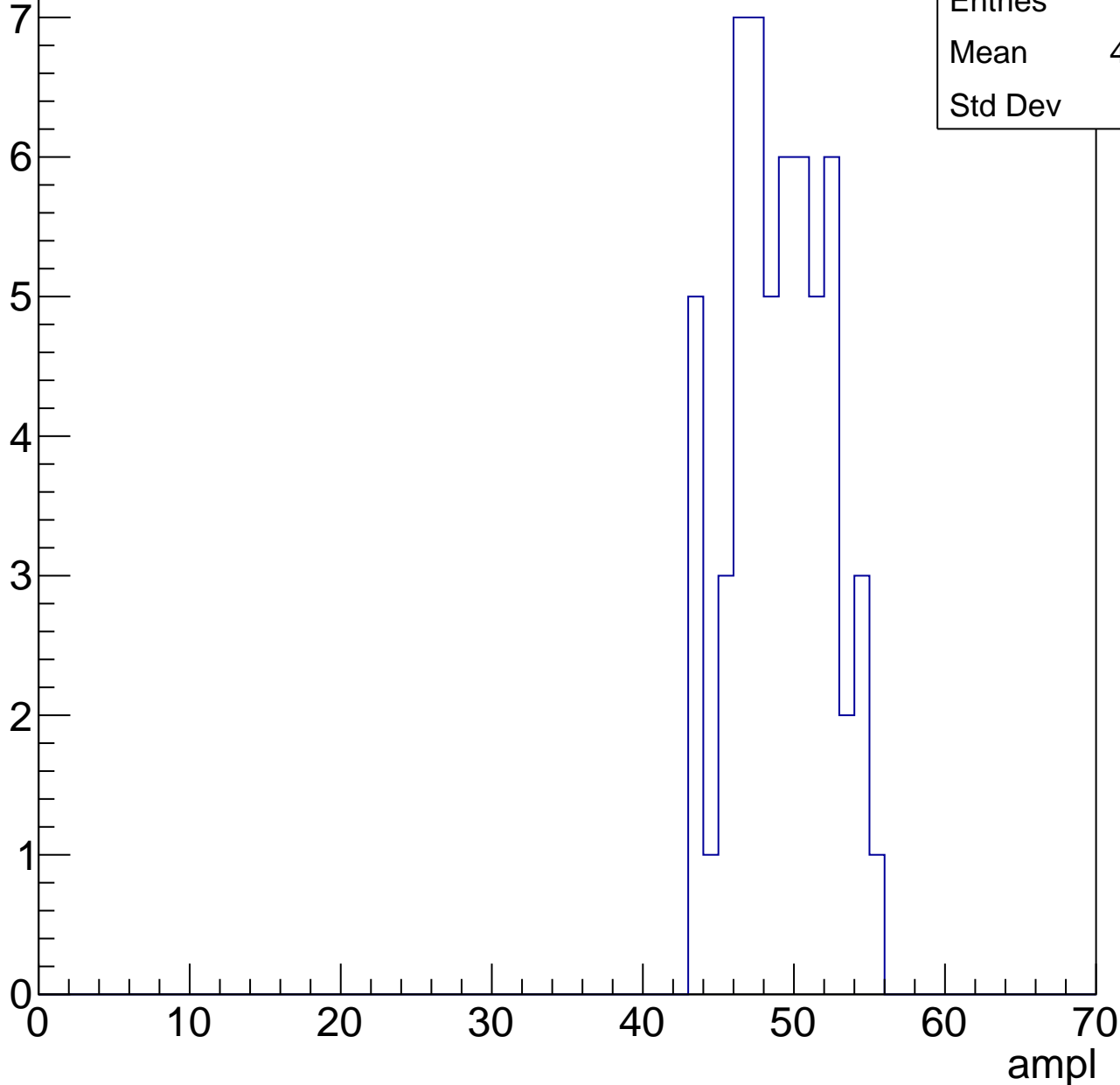


B0L002S, U17-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	48.58
Std Dev	3.14

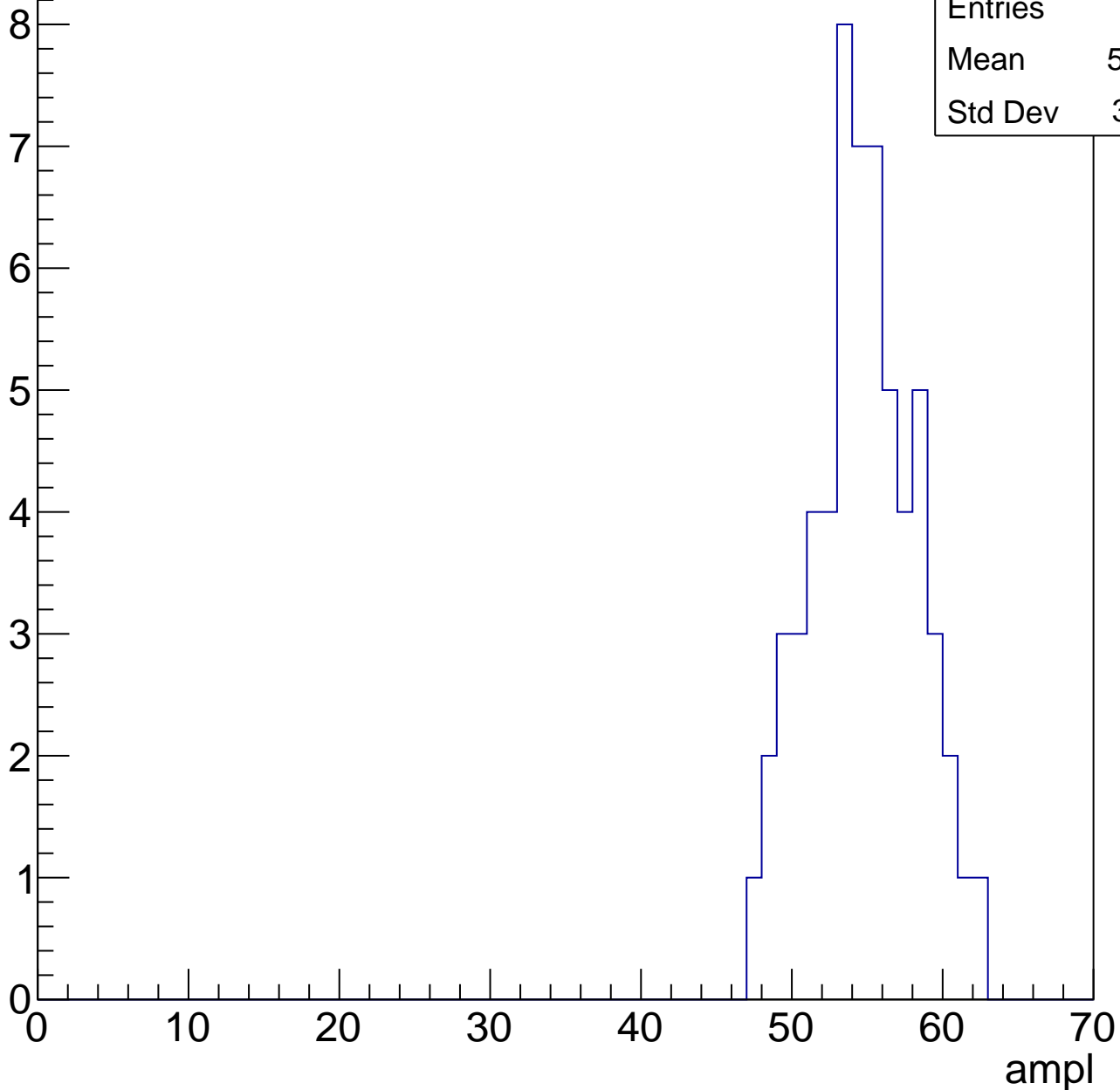


B0L002S, U17-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

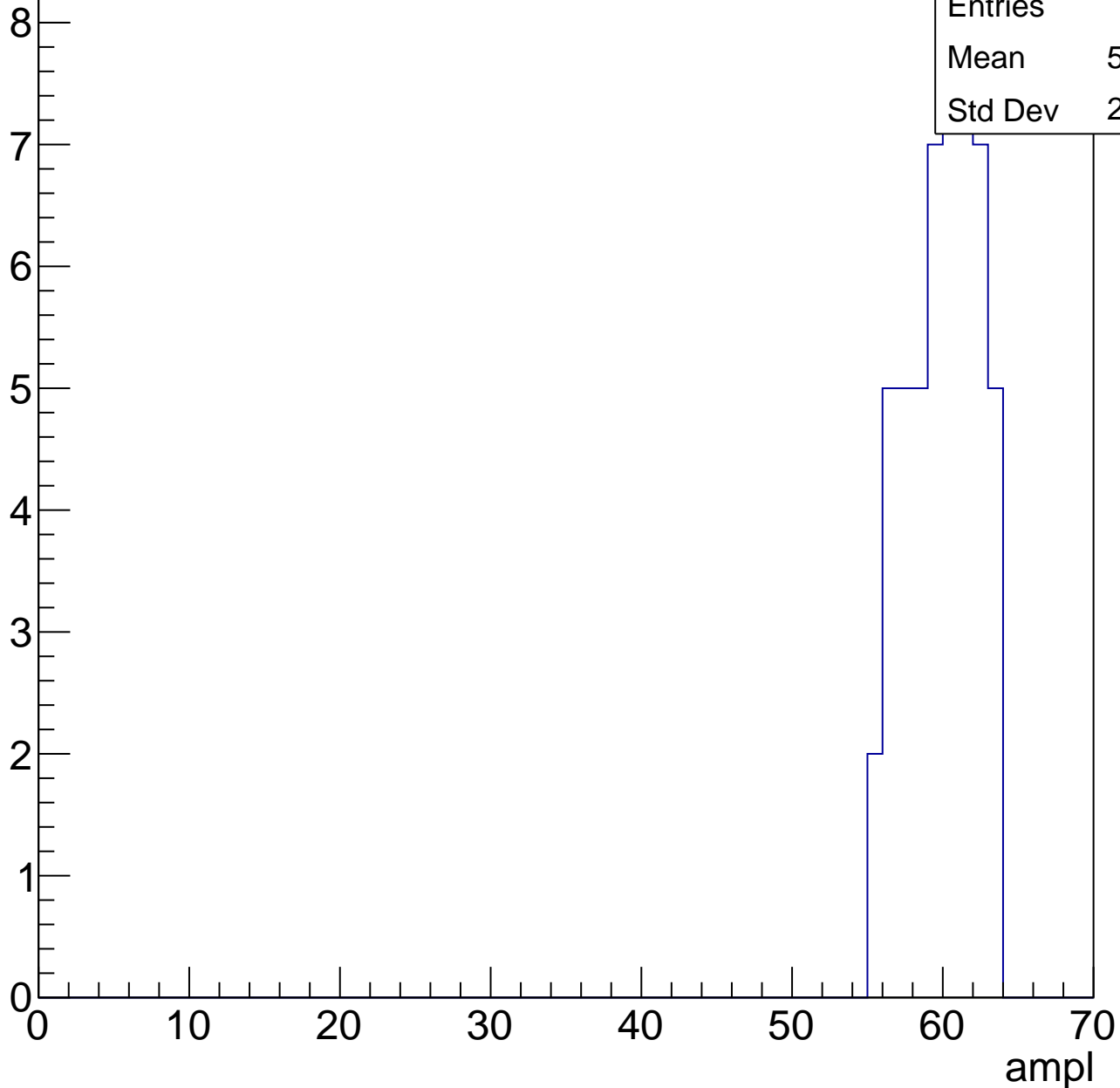
Entries	60
Mean	54.28
Std Dev	3.421



B0L002S, U17-ch21, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

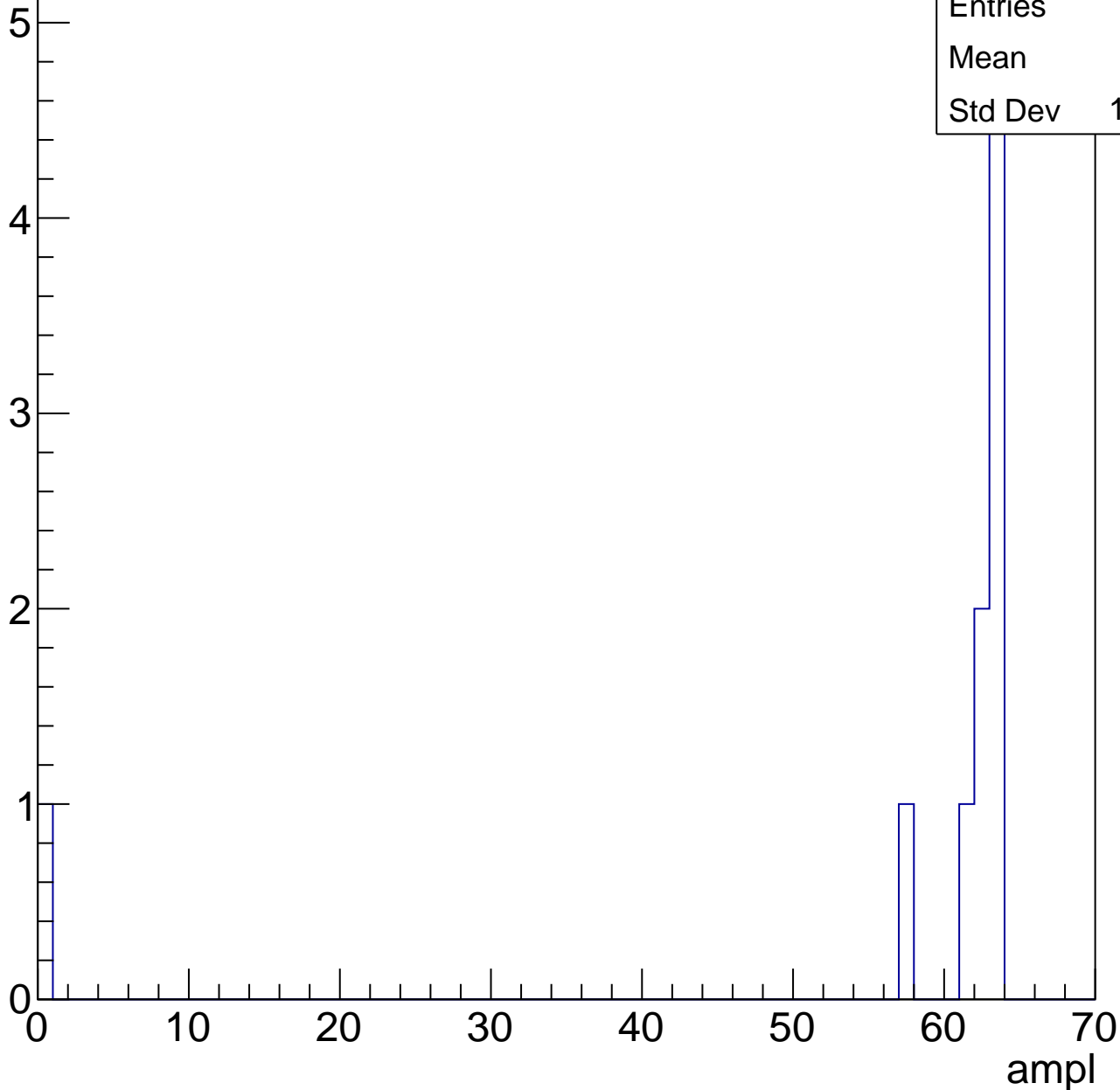


B0L002S, U17-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	55.7
Std Dev	18.65



B0L002S, U17-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch22, adc0

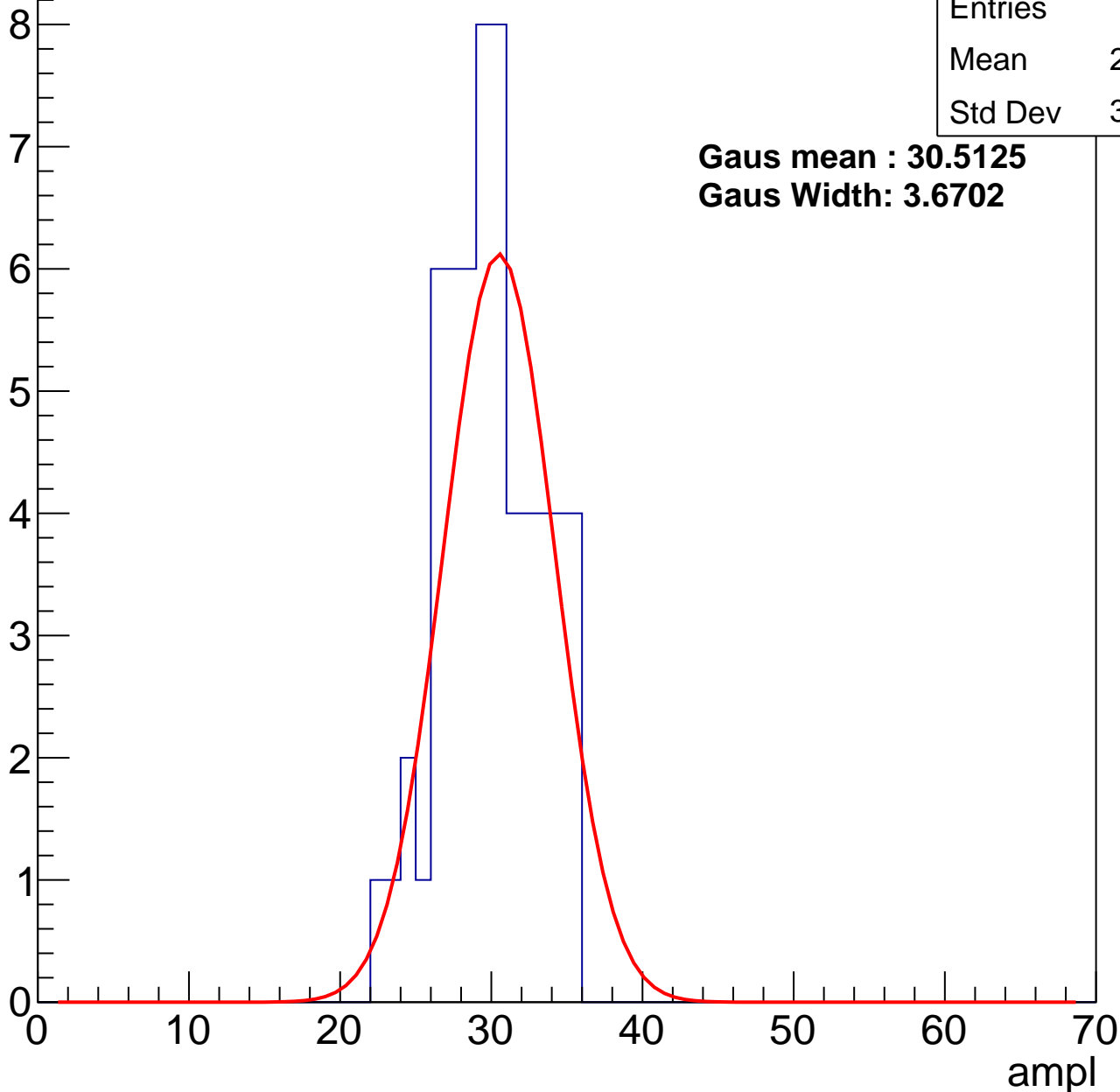
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.42
Std Dev	3.169

Gaus mean : 30.5125

Gaus Width: 3.6702



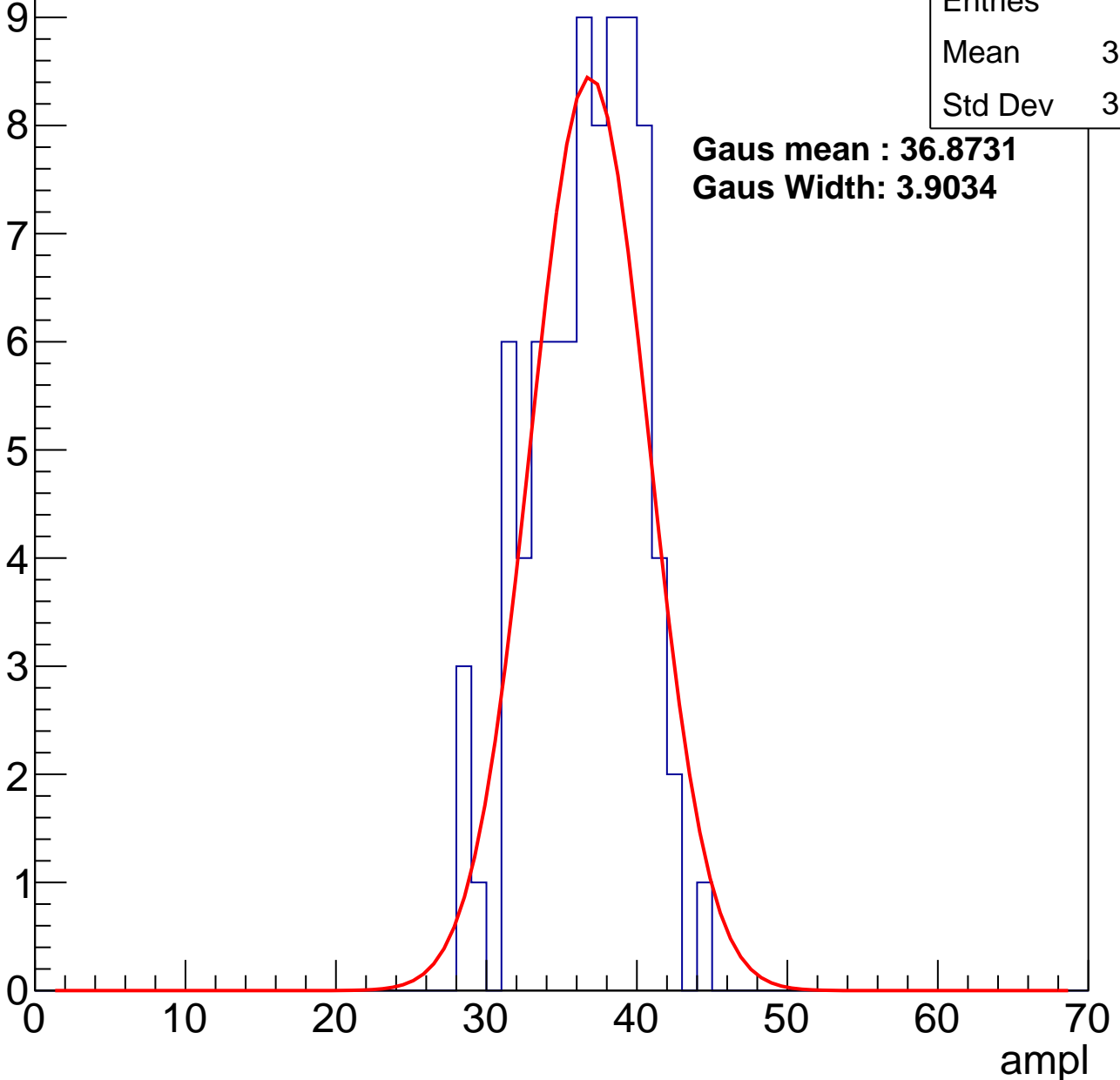
B0L002S, U17-ch22, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	36.15
Std Dev	3.538

Gaus mean : 36.8731
Gaus Width: 3.9034



B0L002S, U17-ch22, adc2

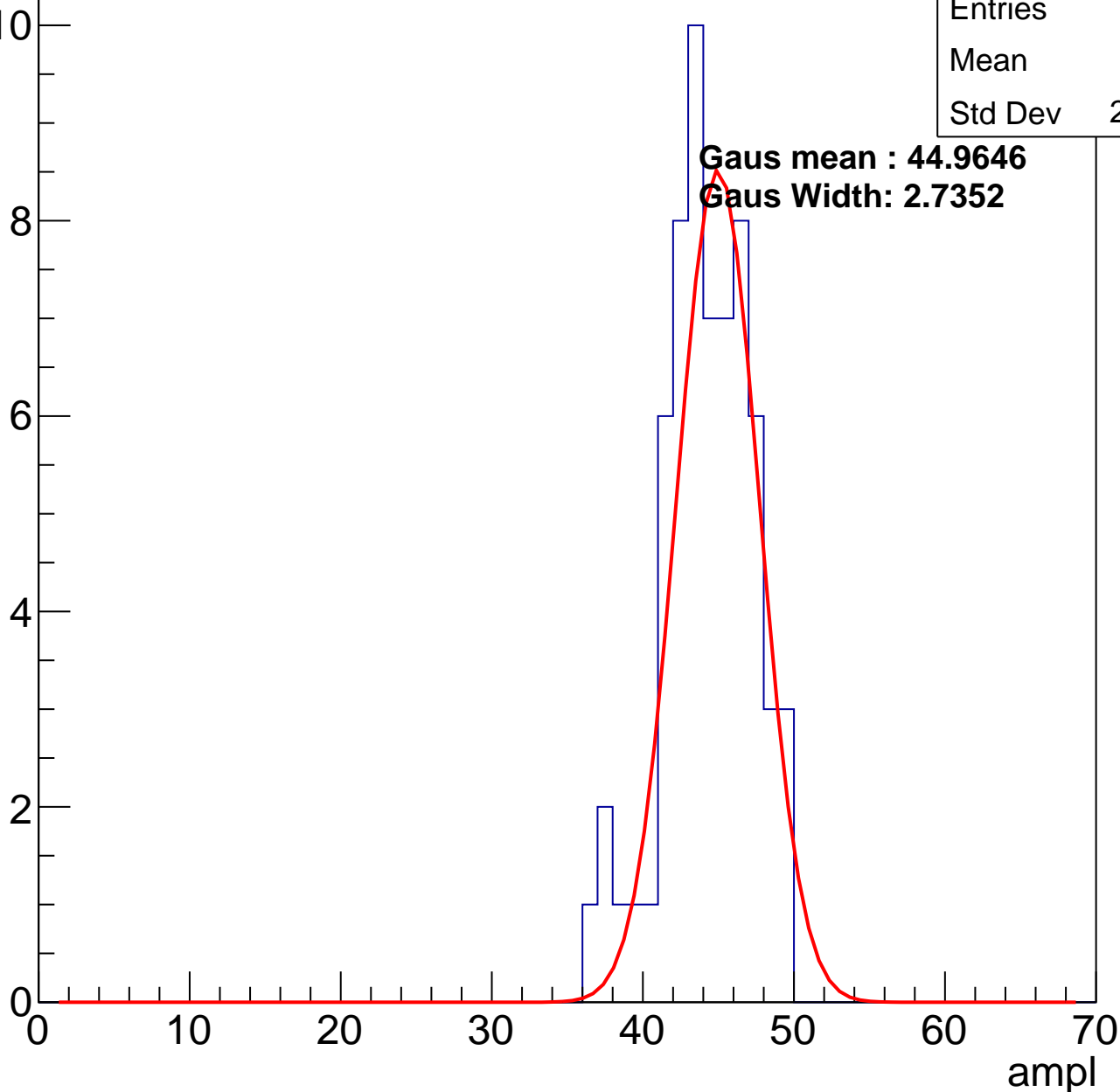
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.8
Std Dev	2.927

Gaus mean : 44.9646

Gaus Width: 2.7352

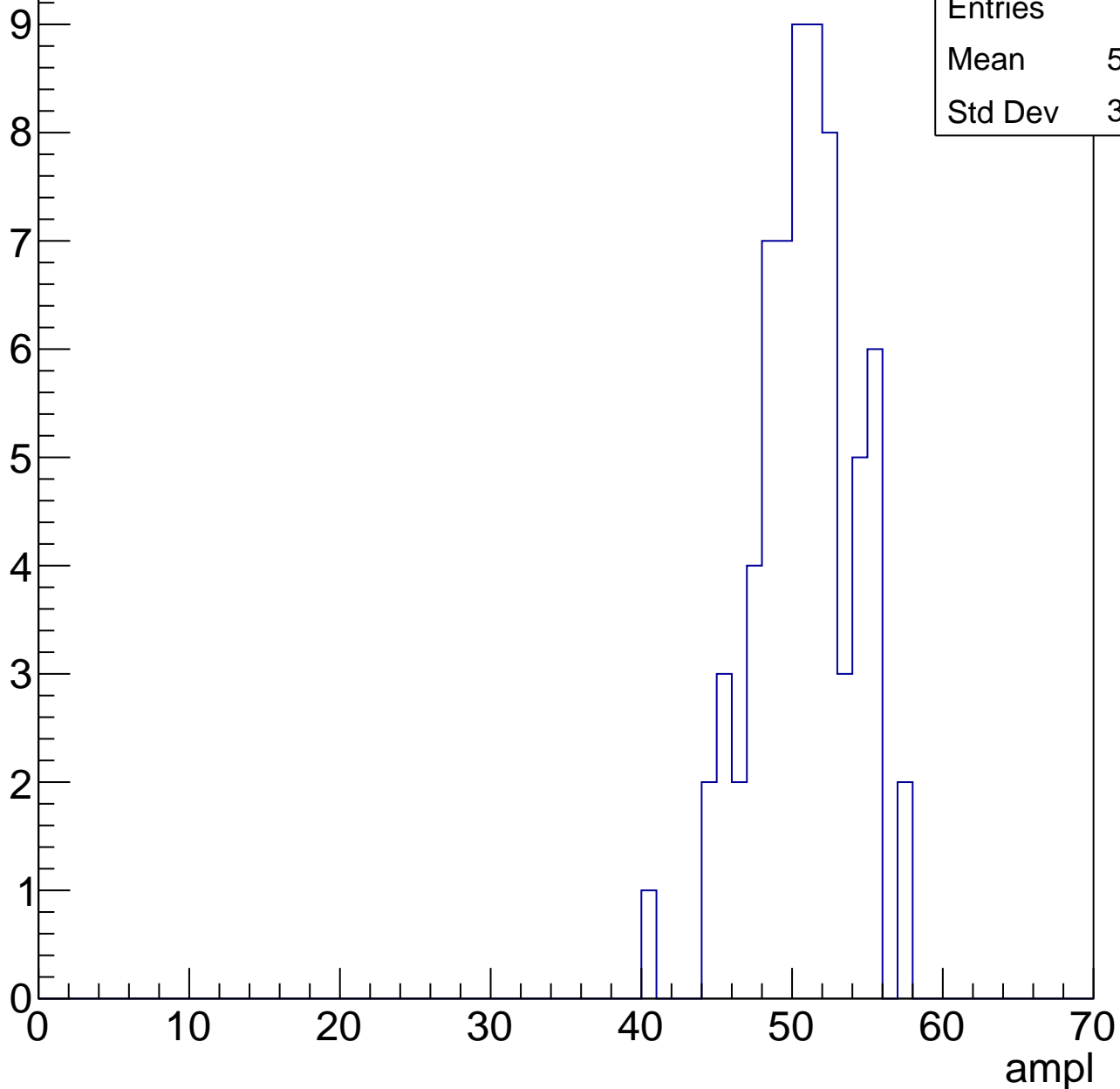


B0L002S, U17-ch22, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.29
Std Dev	3.299

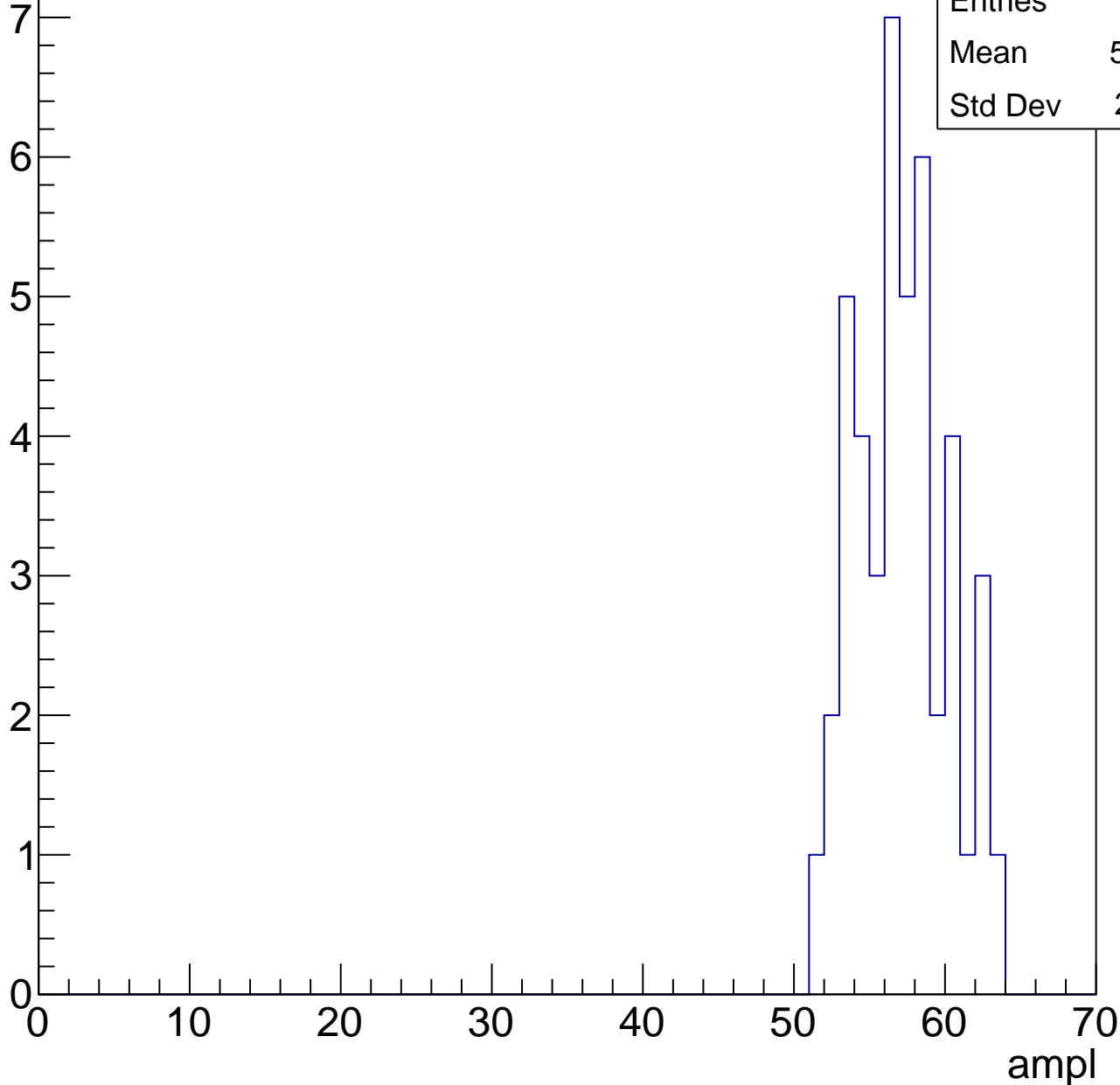


B0L002S, U17-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	56.68
Std Dev	2.991

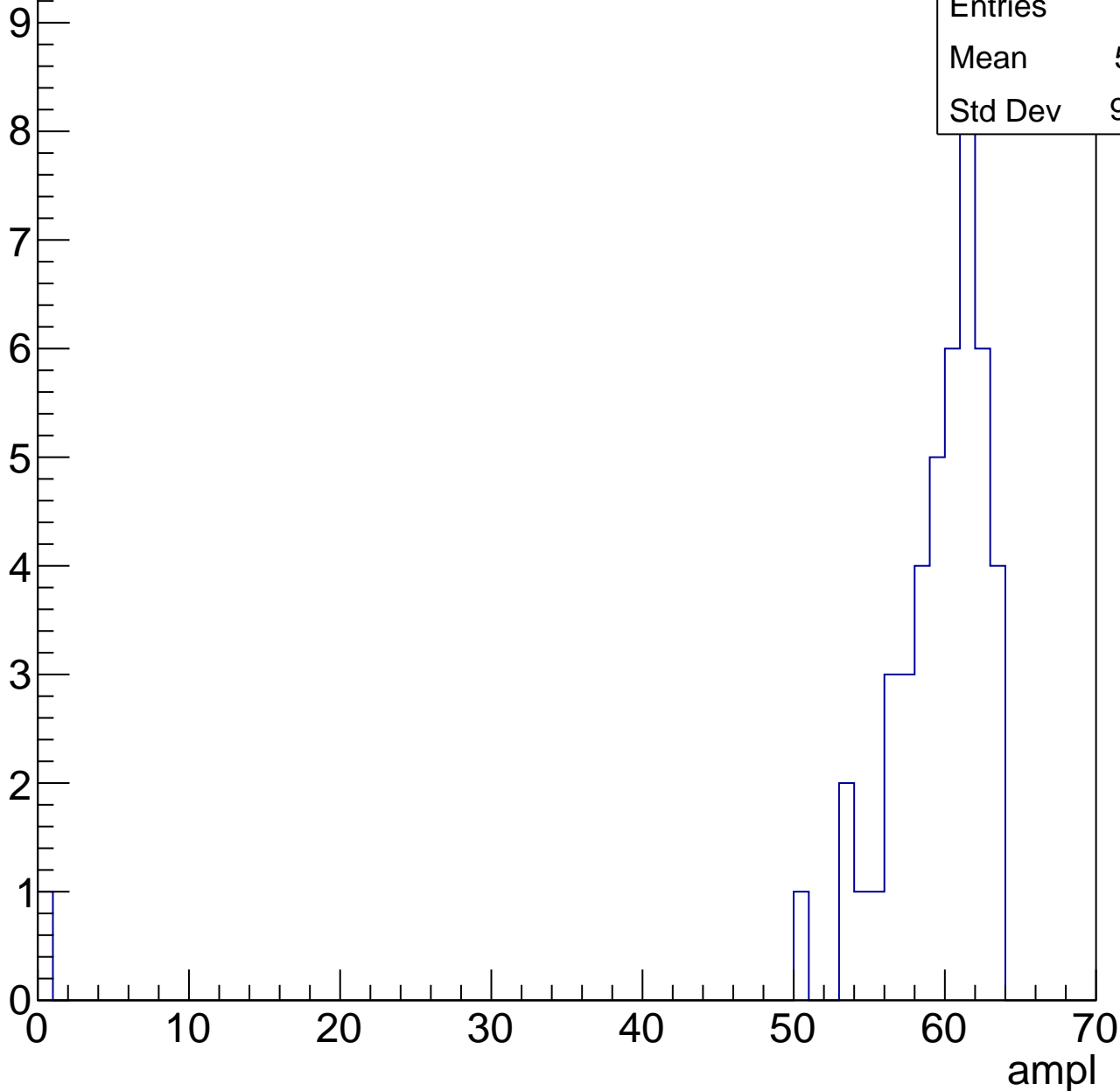


B0L002S, U17-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	57.91
Std Dev	9.117

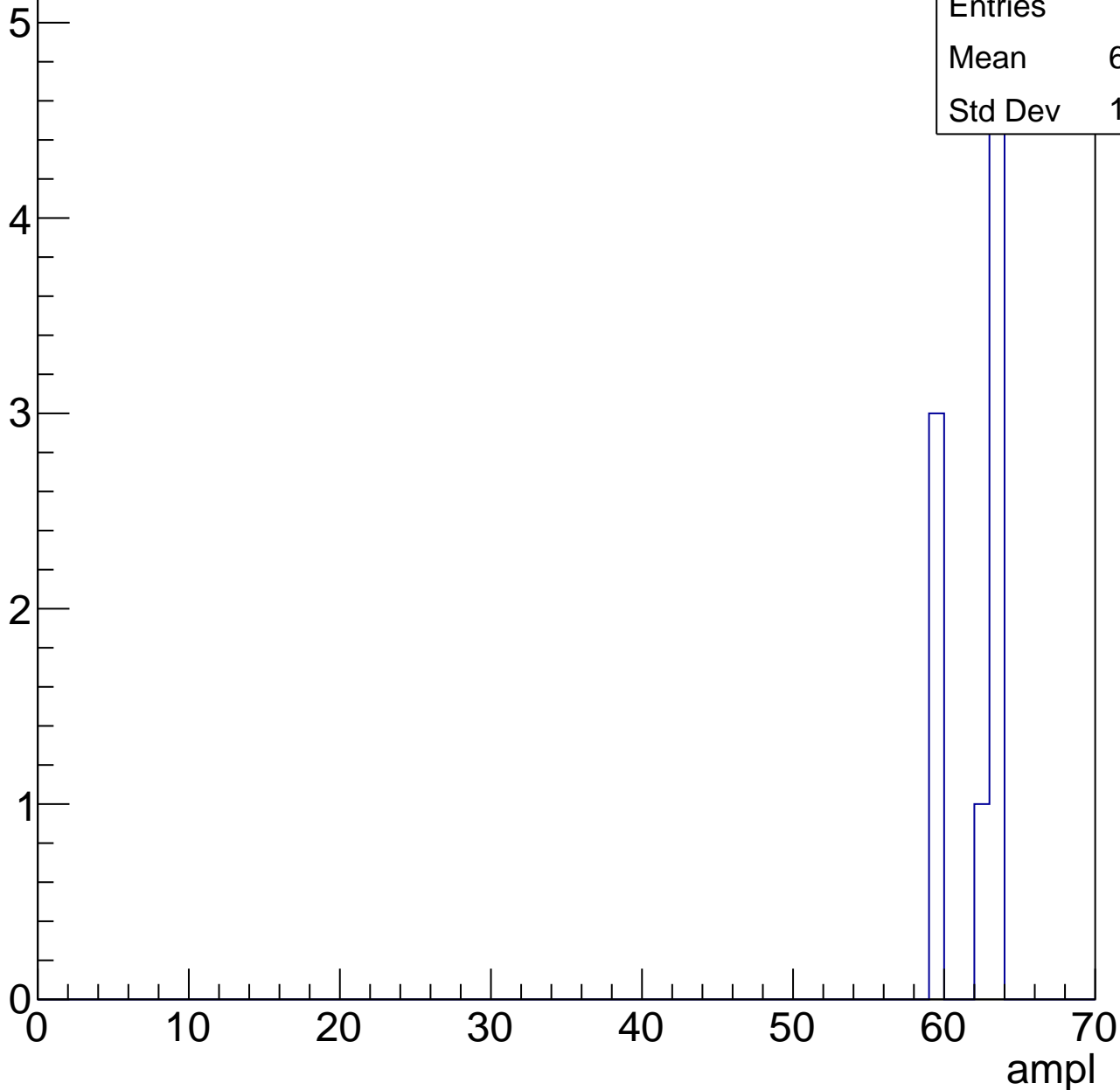


B0L002S, U17-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	61.56
Std Dev	1.832



B0L002S, U17-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U17-ch23, adc0

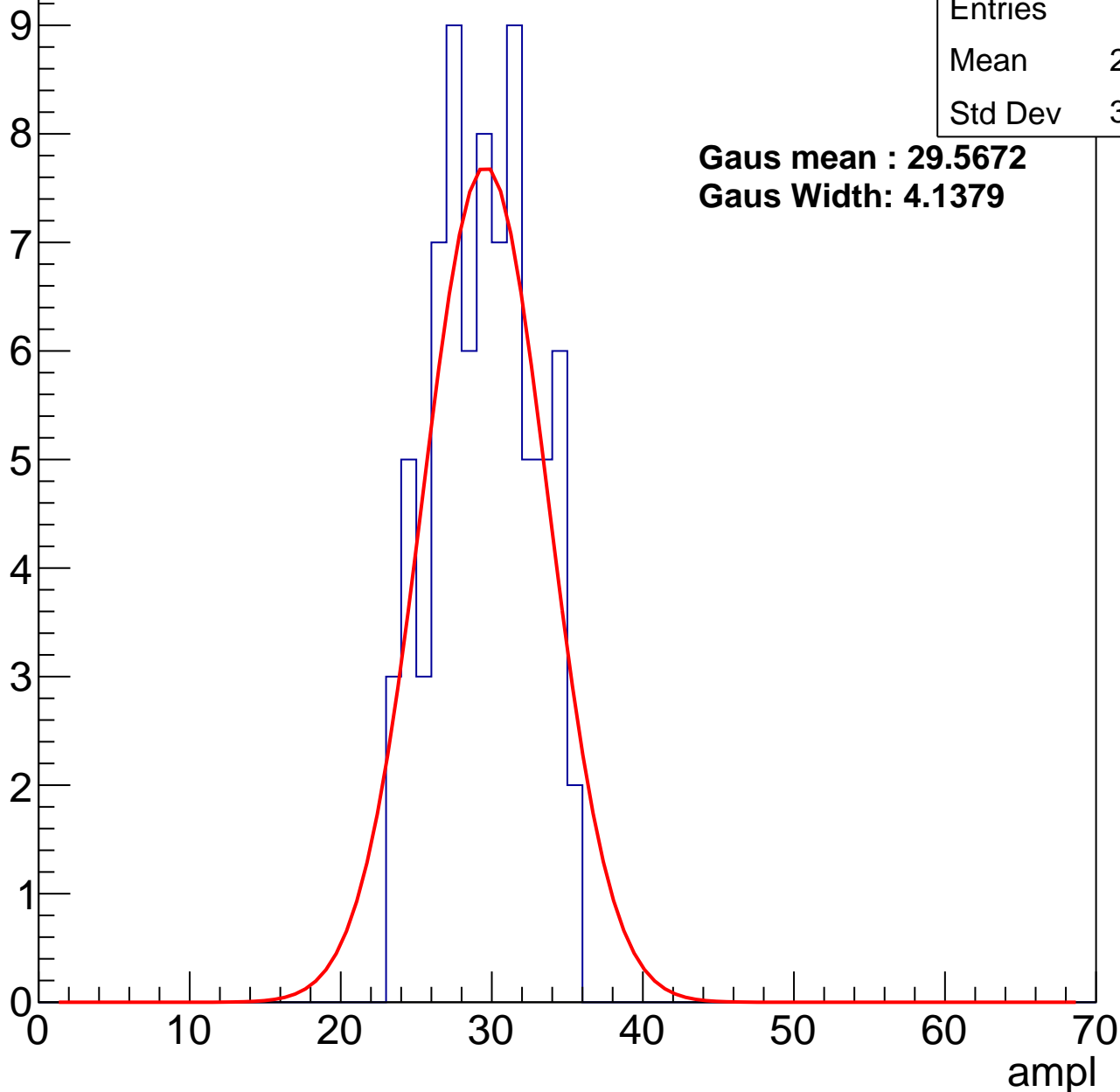
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	29.03
Std Dev	3.217

Gaus mean : 29.5672

Gaus Width: 4.1379



B0L002S, U17-ch23, adc1

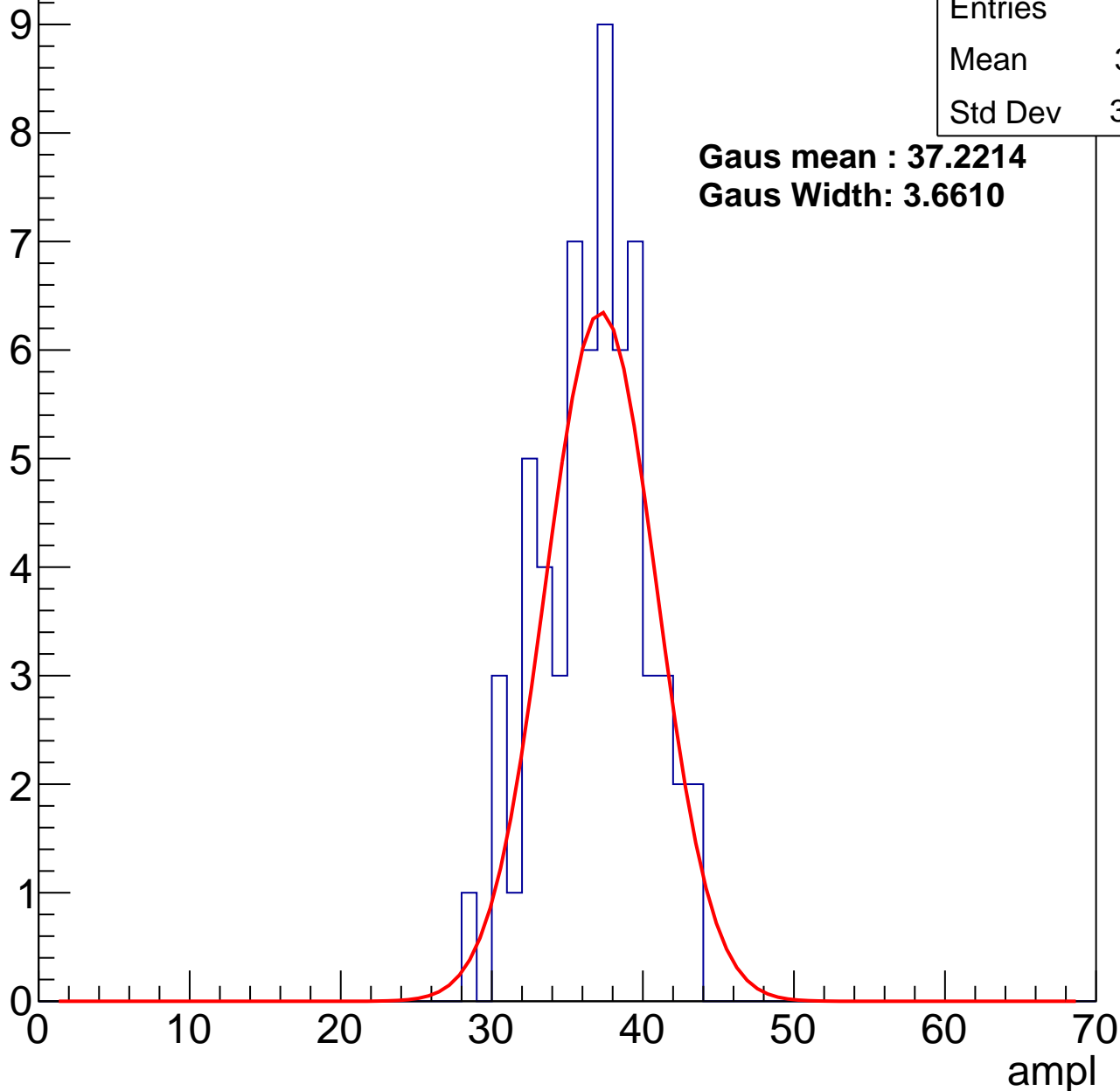
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.31
Std Dev	3.396

Gaus mean : 37.2214

Gaus Width: 3.6610



B0L002S, U17-ch23, adc2

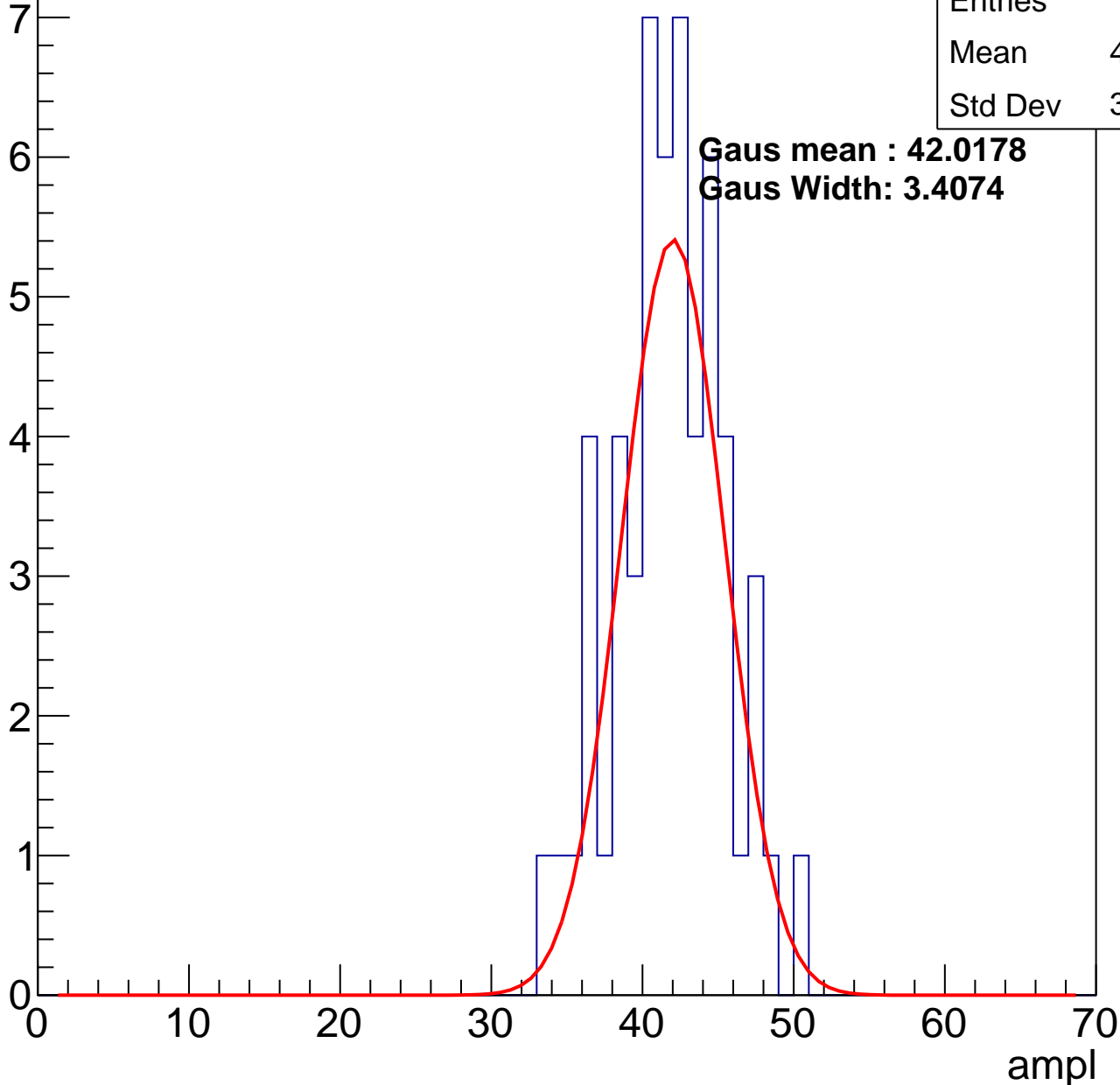
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	41.33
Std Dev	3.613

Gaus mean : 42.0178

Gaus Width: 3.4074

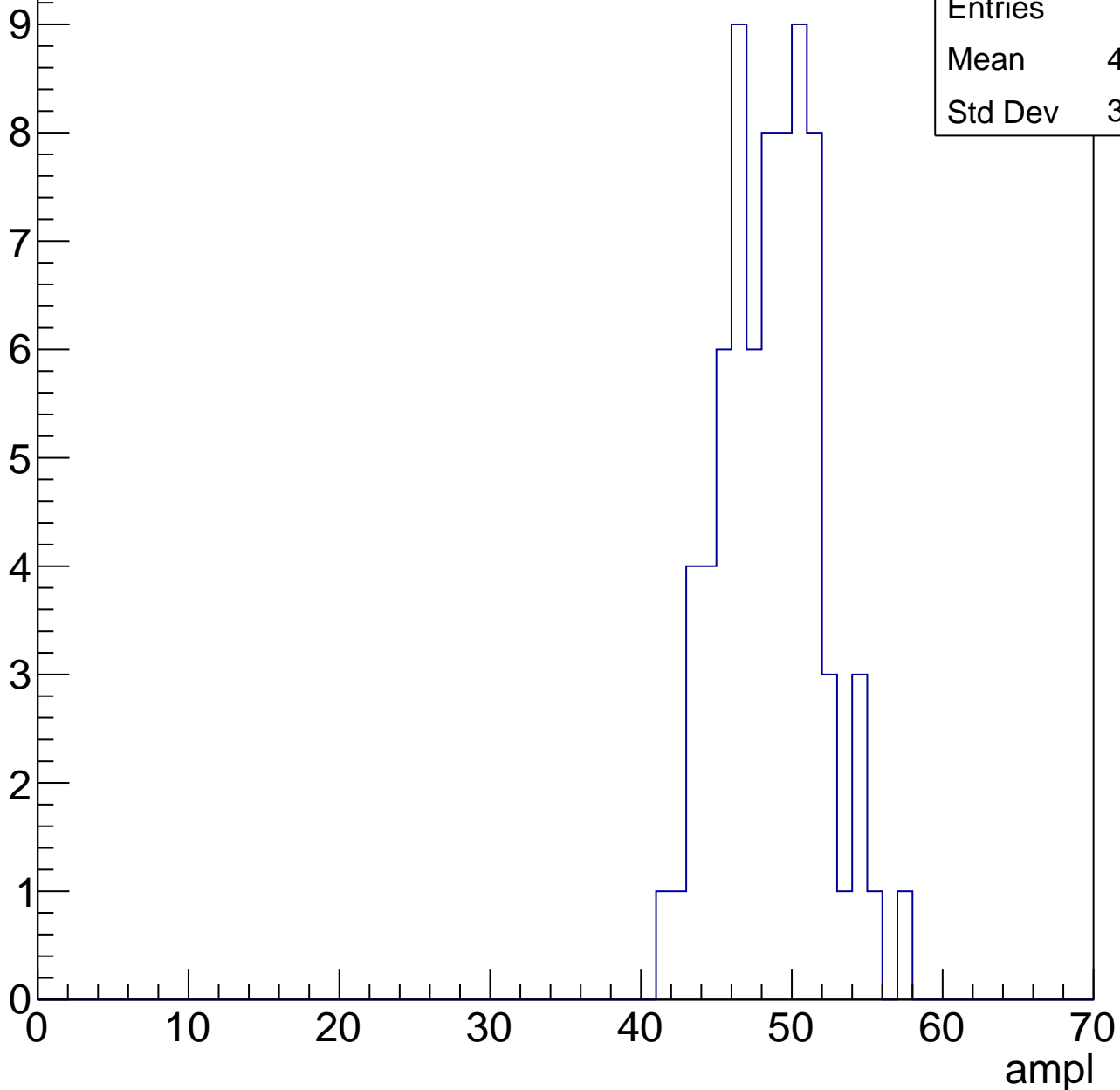


B0L002S, U17-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	48.14
Std Dev	3.249

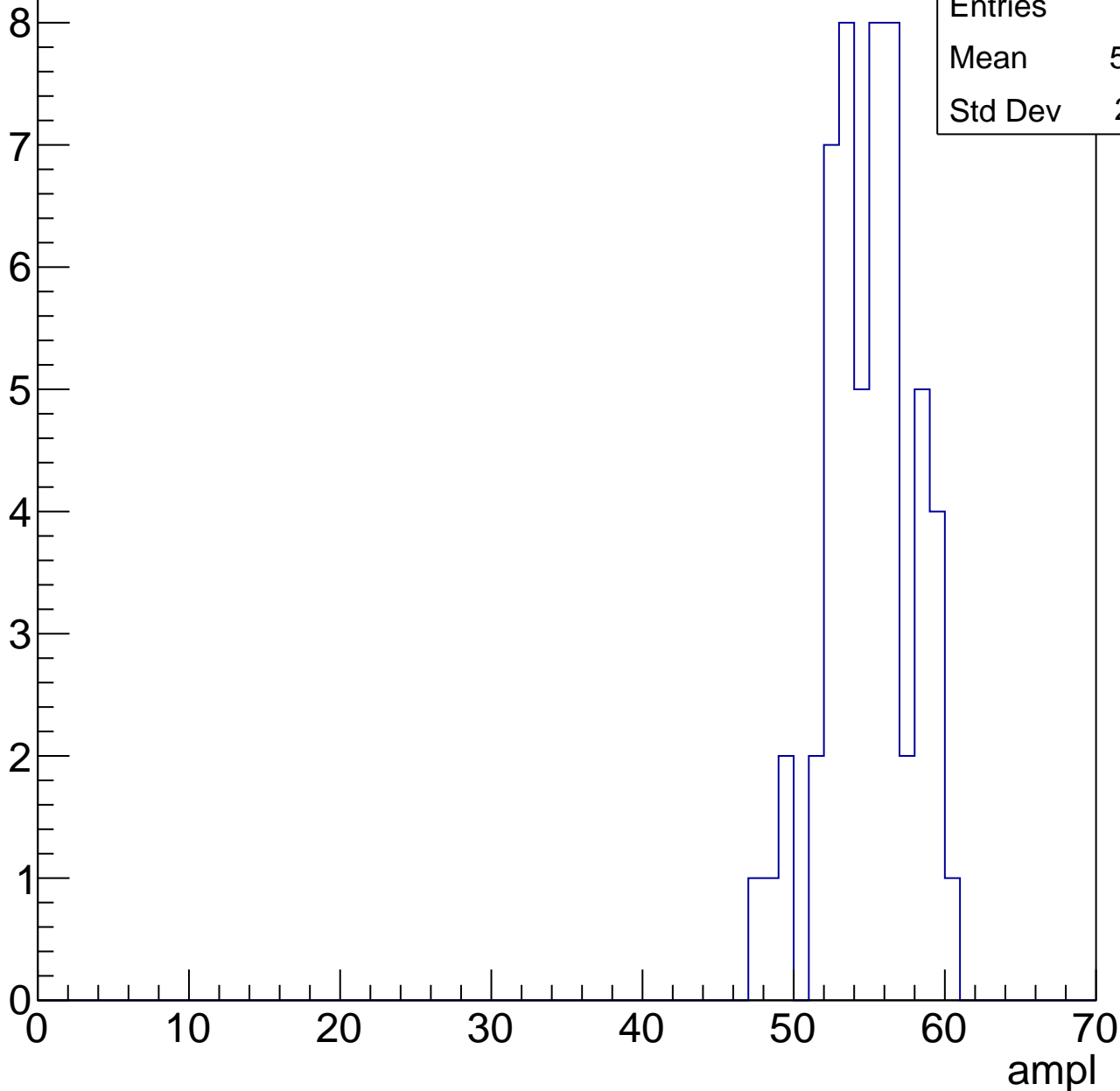


B0L002S, U17-ch23, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	54.46
Std Dev	2.891



B0L002S, U17-ch23, adc5

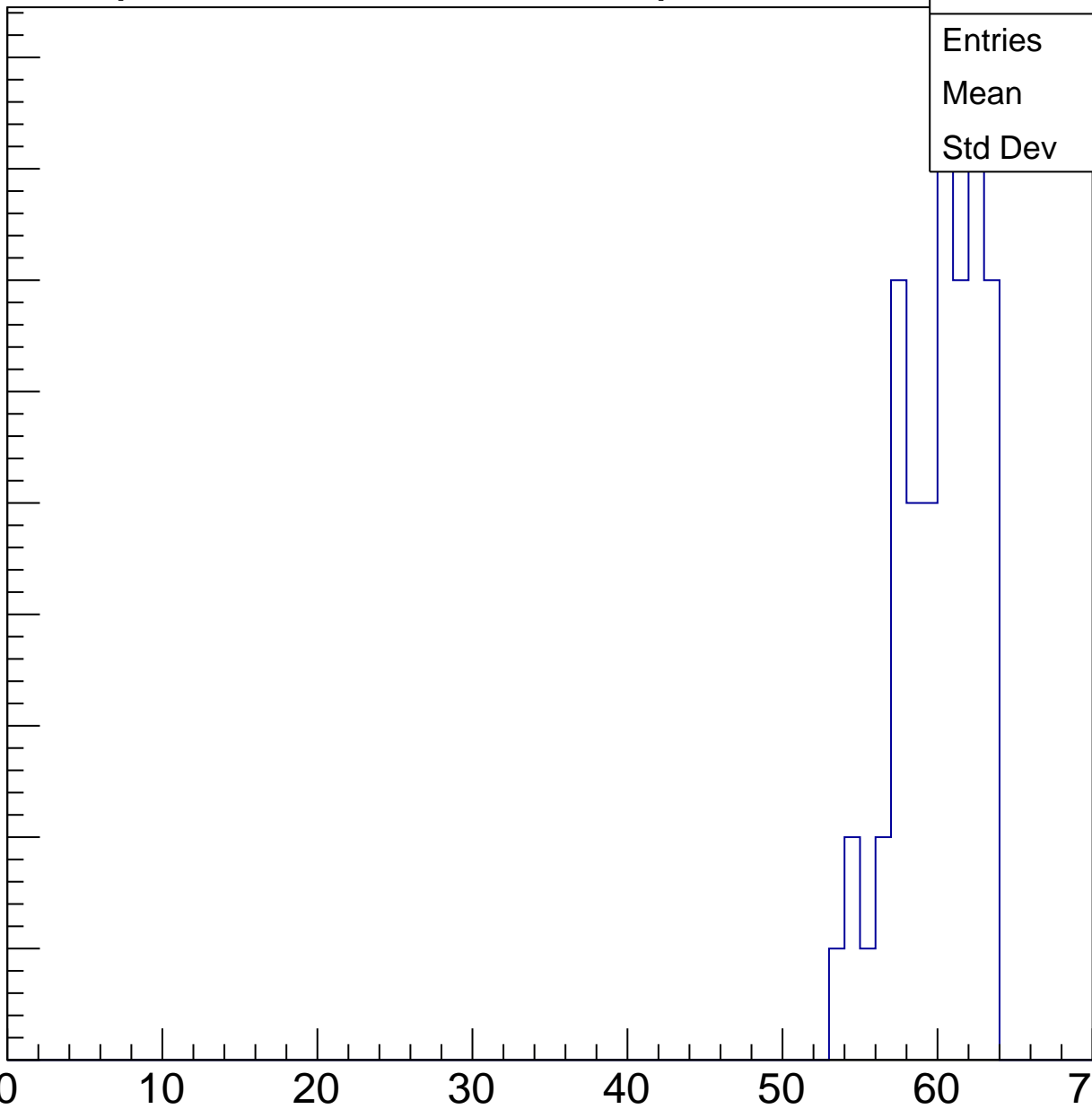
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	59.59
Std Dev	2.593

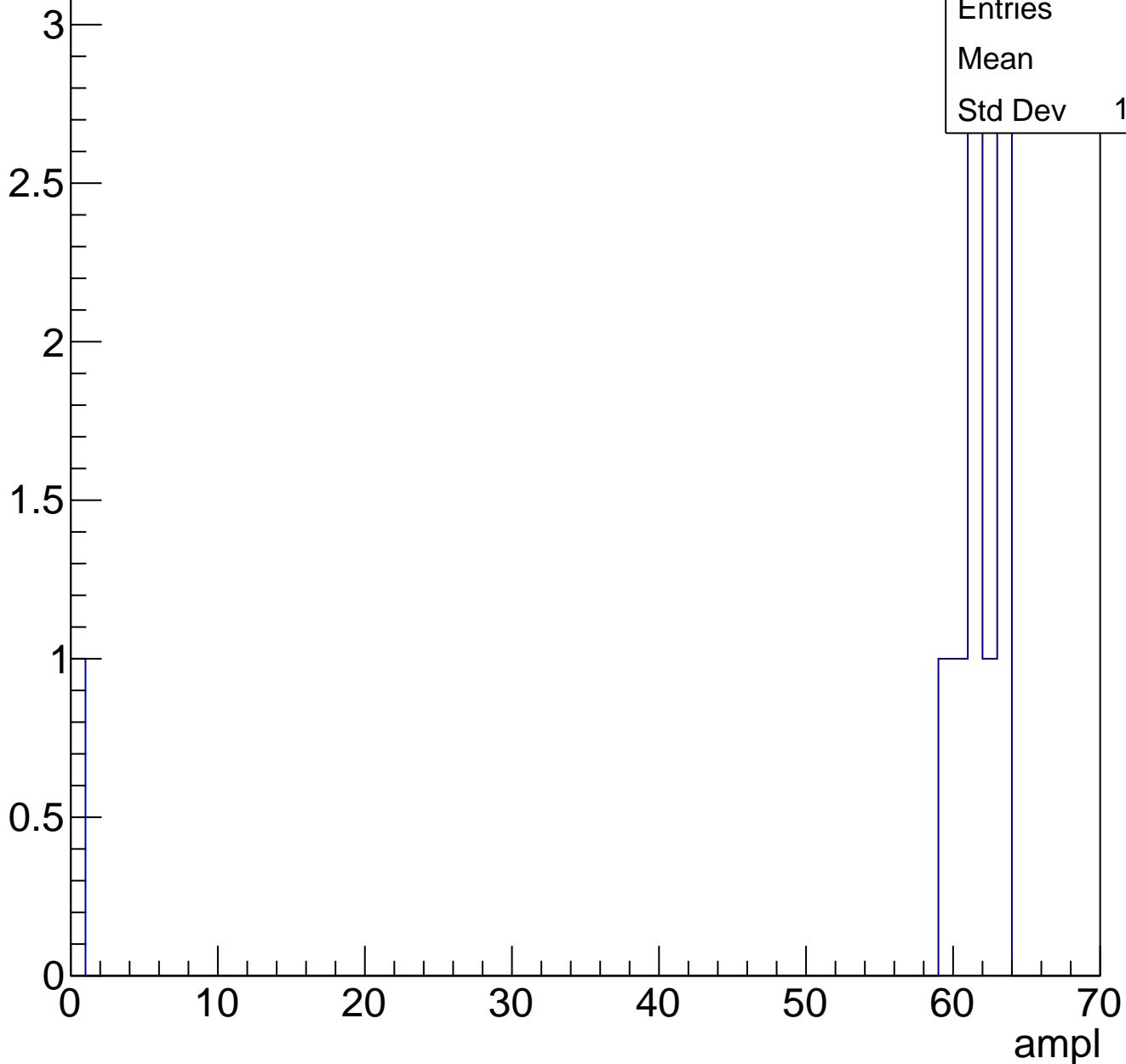
ampl



B0L002S, U17-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch24, adc0

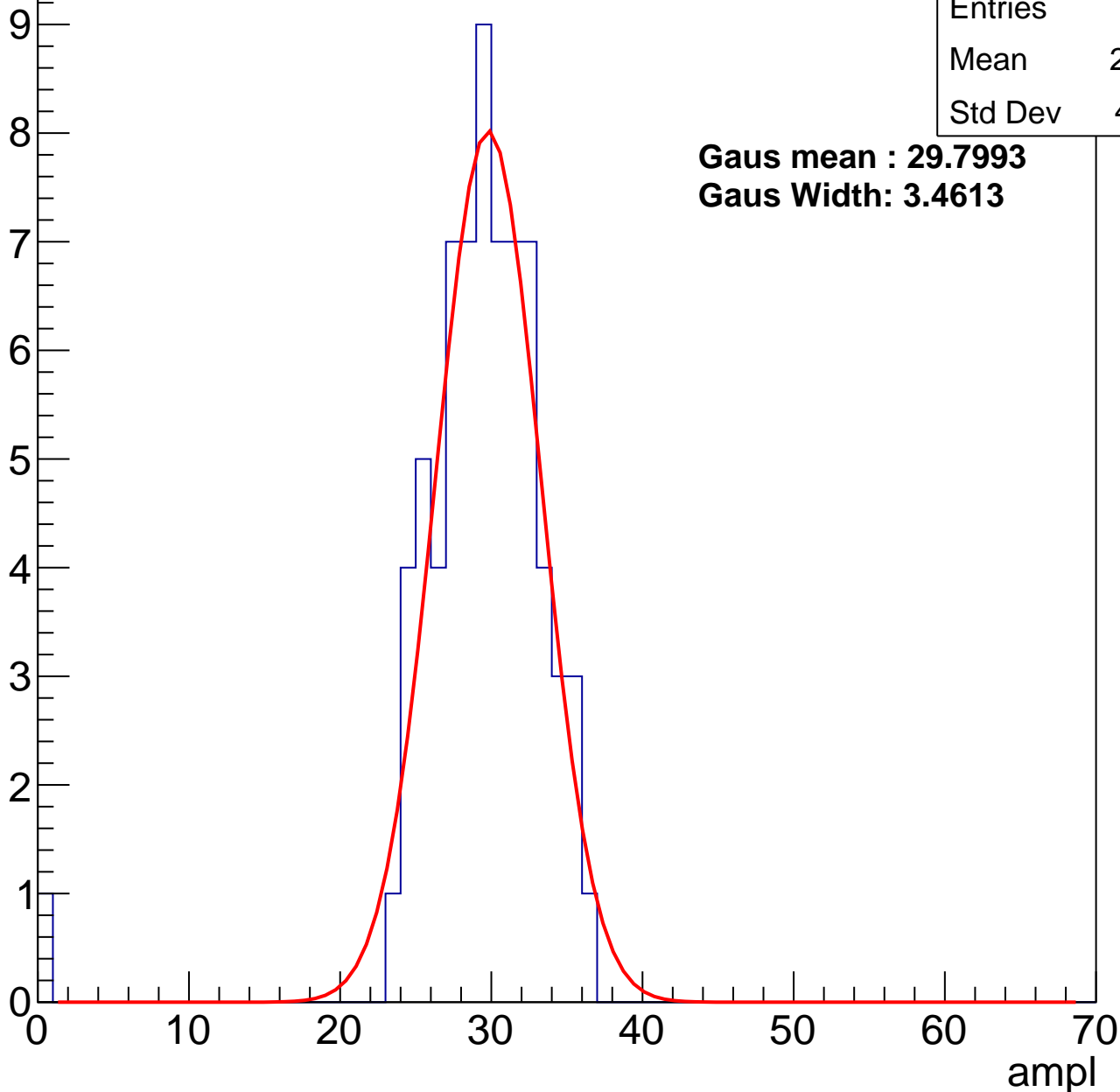
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	28.86
Std Dev	4.661

Gaus mean : 29.7993

Gaus Width: 3.4613



B0L002S, U17-ch24, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	36.05
Std Dev	3.13

Gaus mean : 36.3942

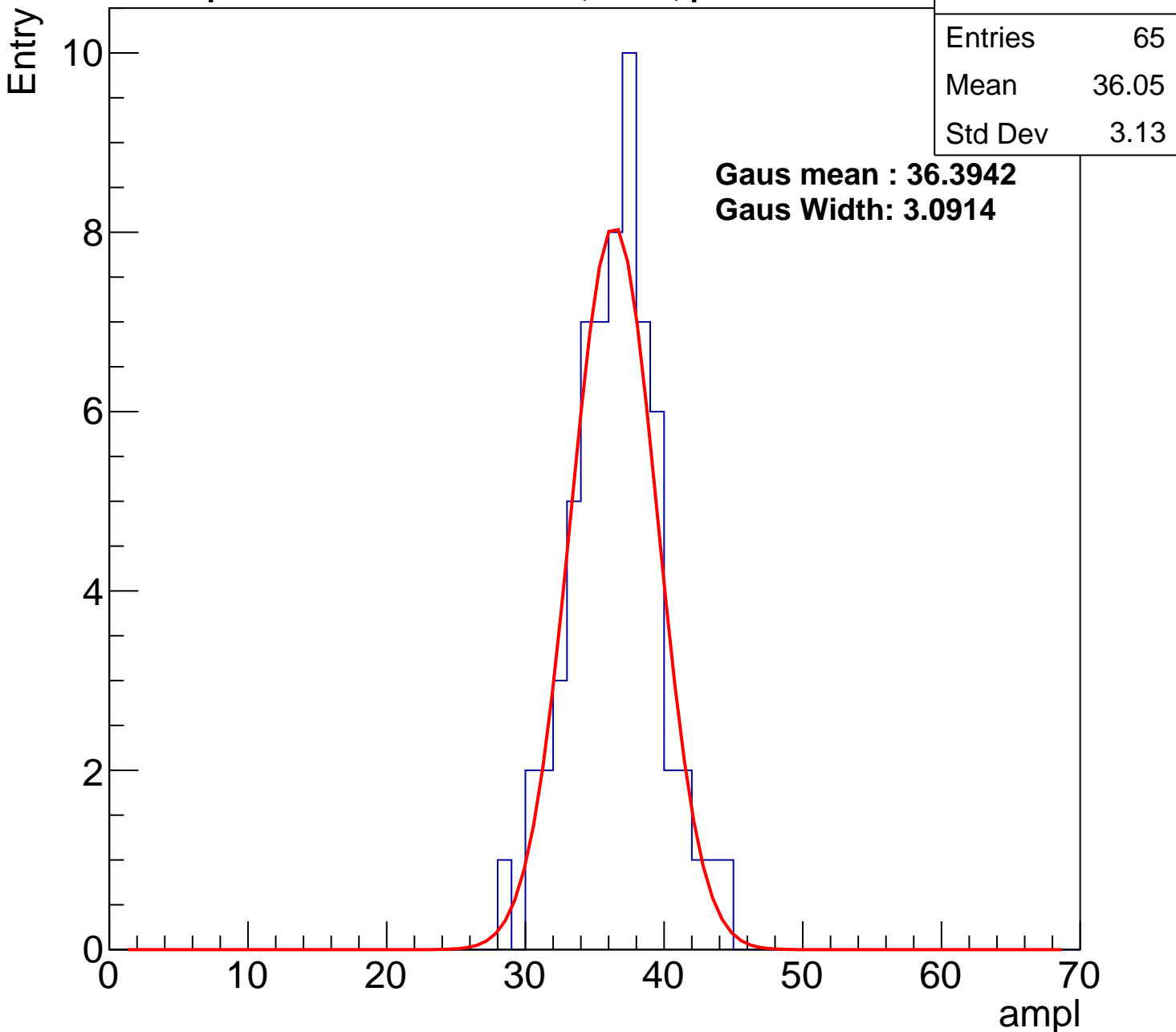
Gaus Width: 3.0914

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch24, adc2

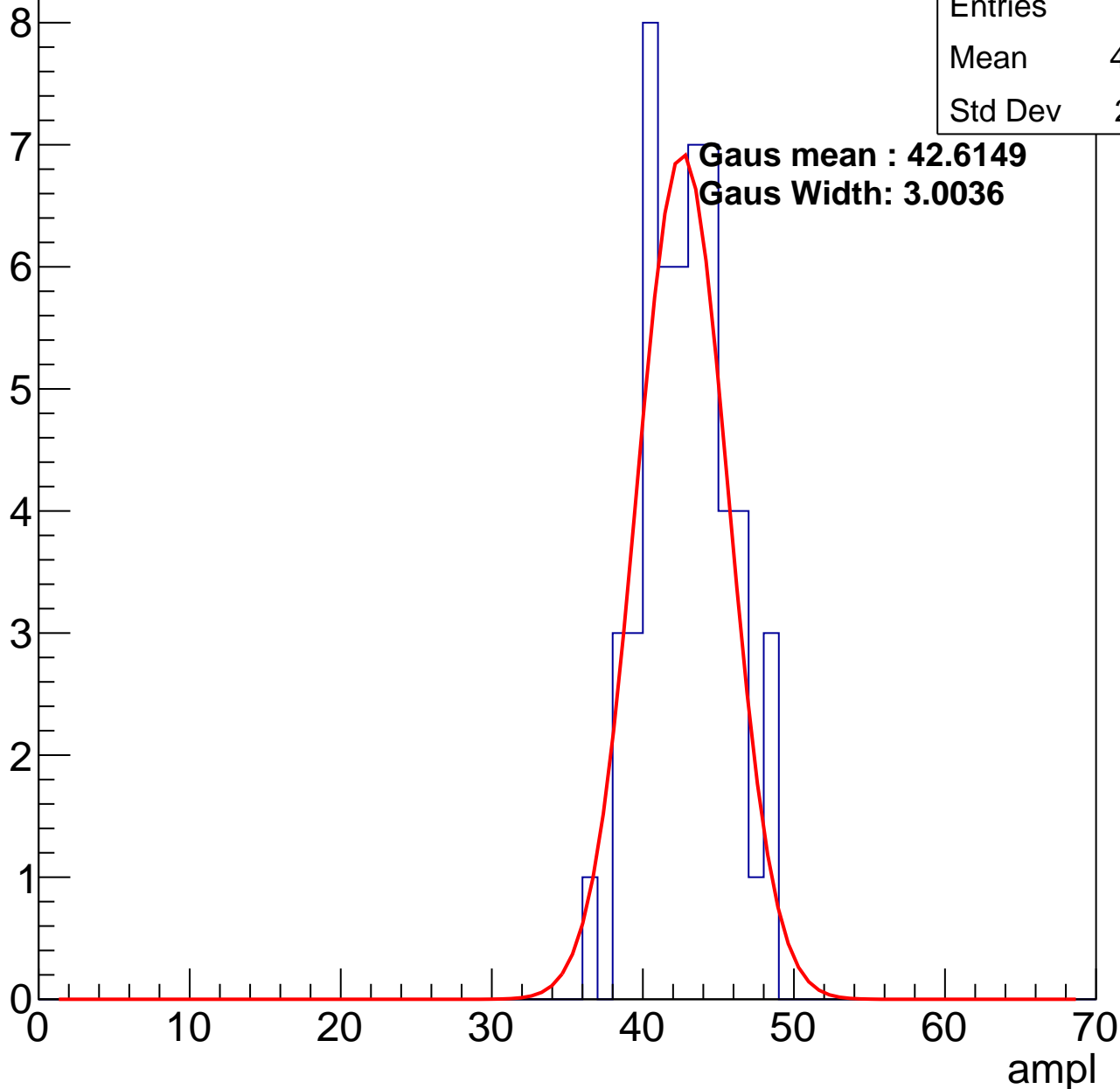
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	42.43
Std Dev	2.771

Gaus mean : 42.6149

Gaus Width: 3.0036

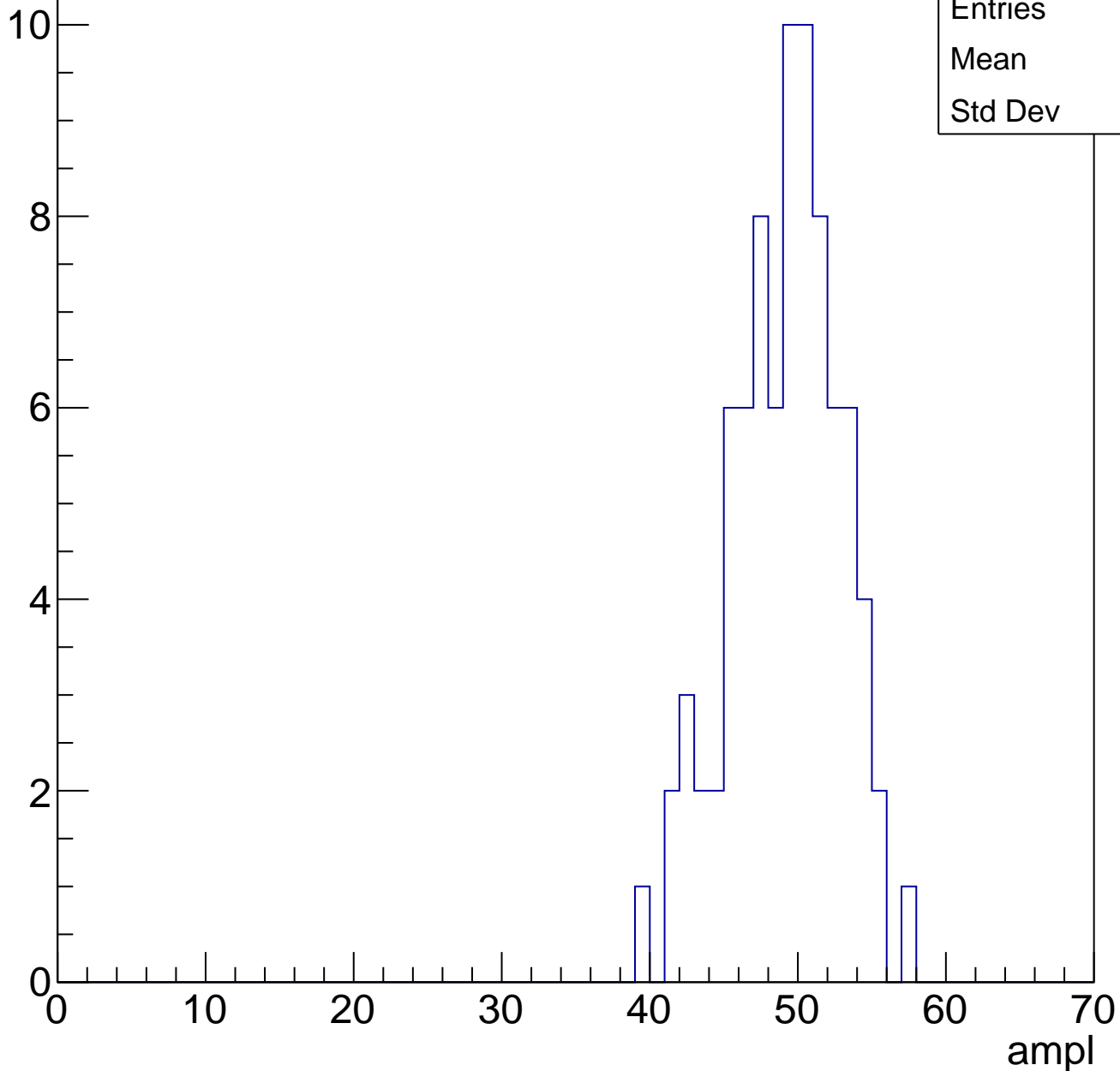


B0L002S, U17-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	48.7
Std Dev	3.65

Entry

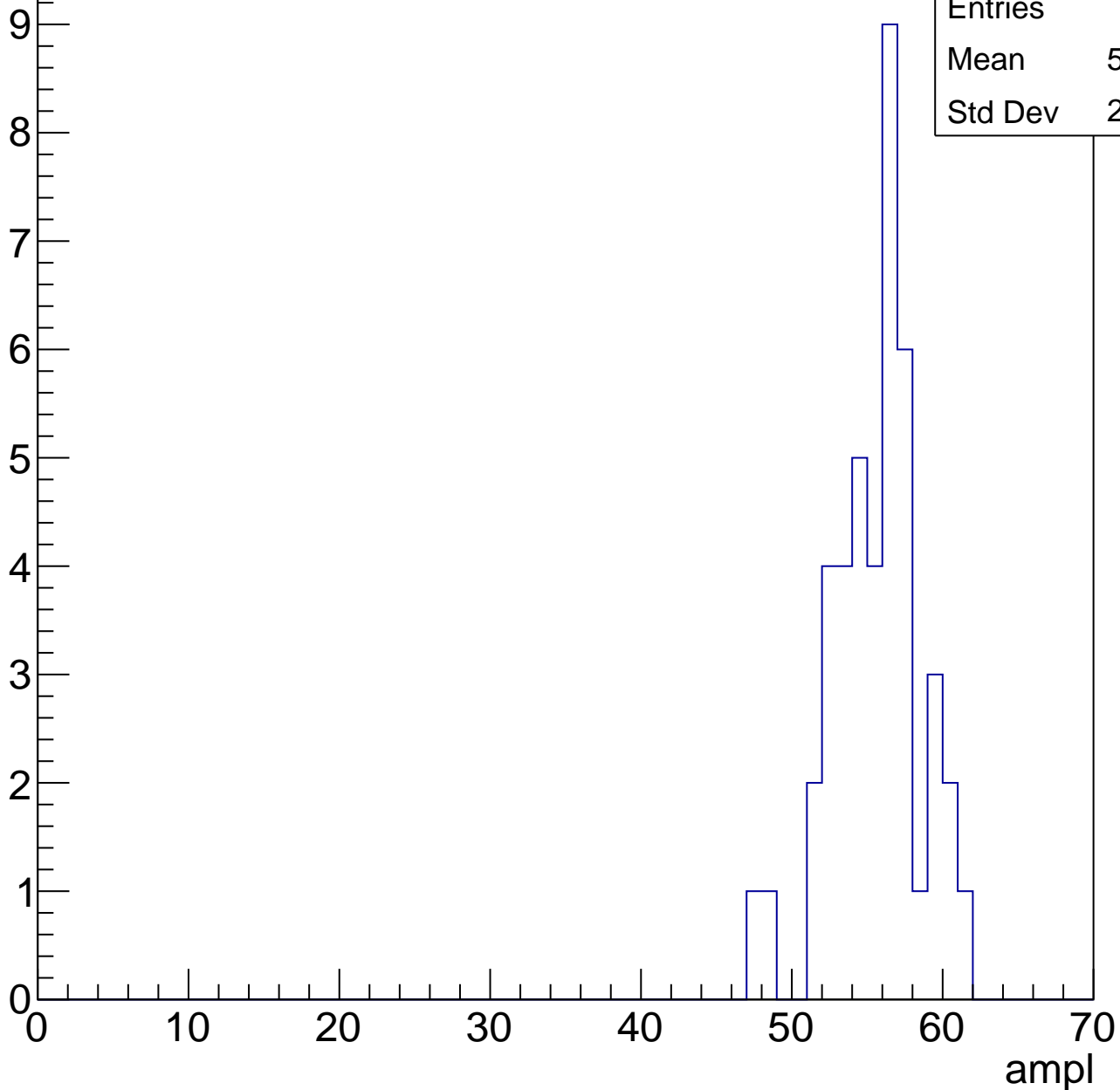


B0L002S, U17-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

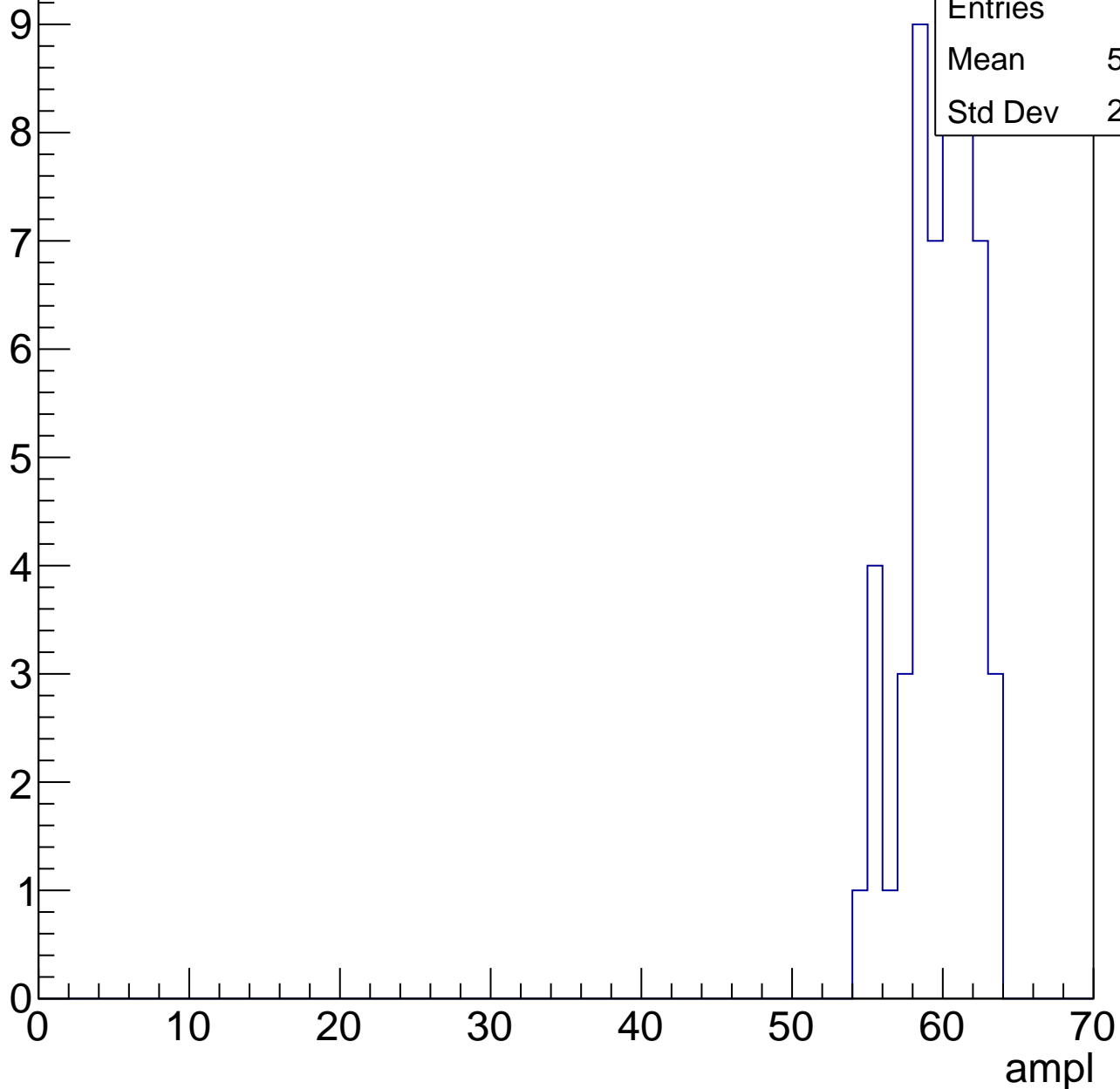
Entries	43
Mean	55.09
Std Dev	2.963



B0L002S, U17-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

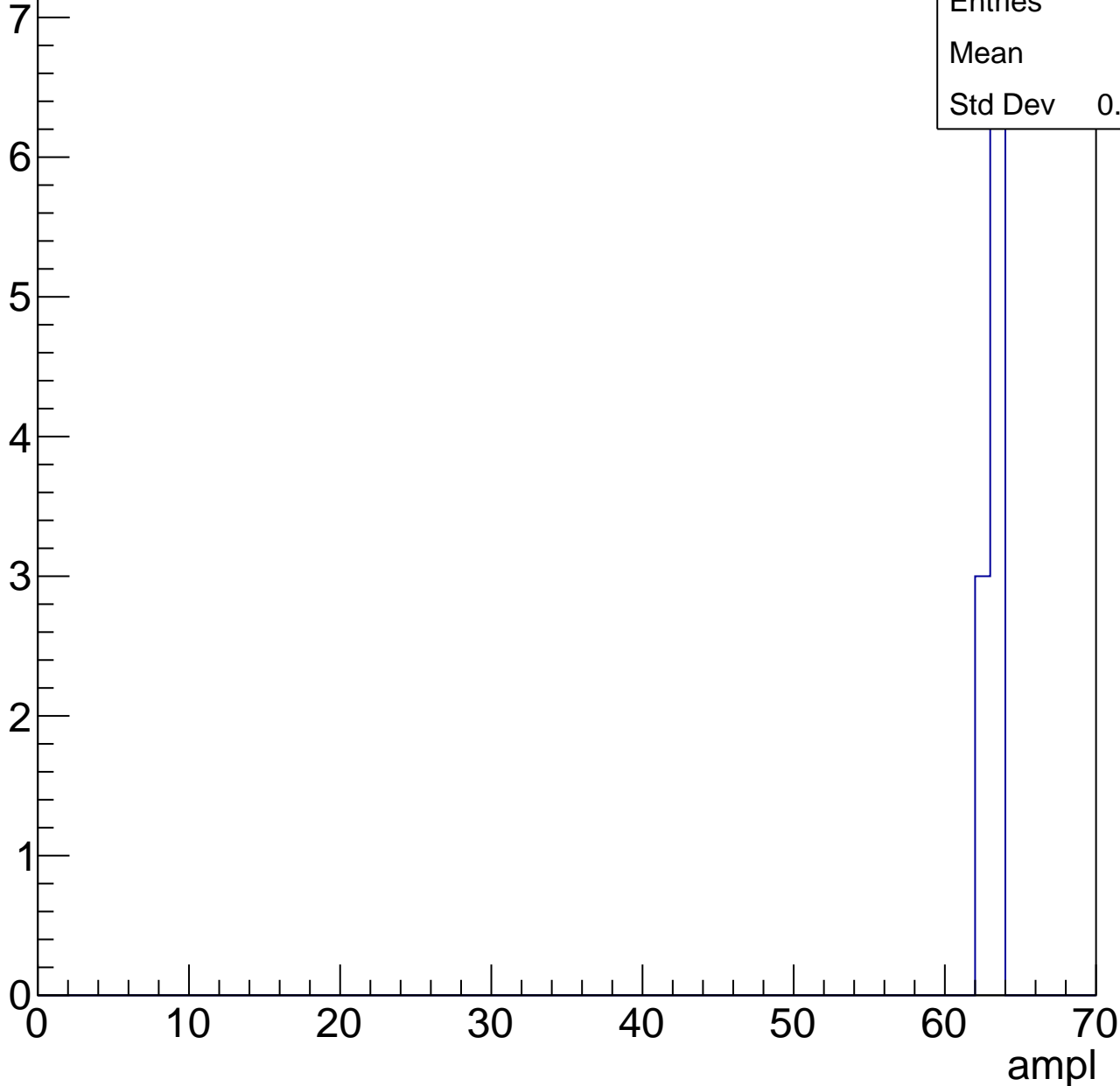


B0L002S, U17-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	62.7
Std Dev	0.4583



B0L002S, U17-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch25, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	30.91
Std Dev	4.93

Gaus mean : 31.9433

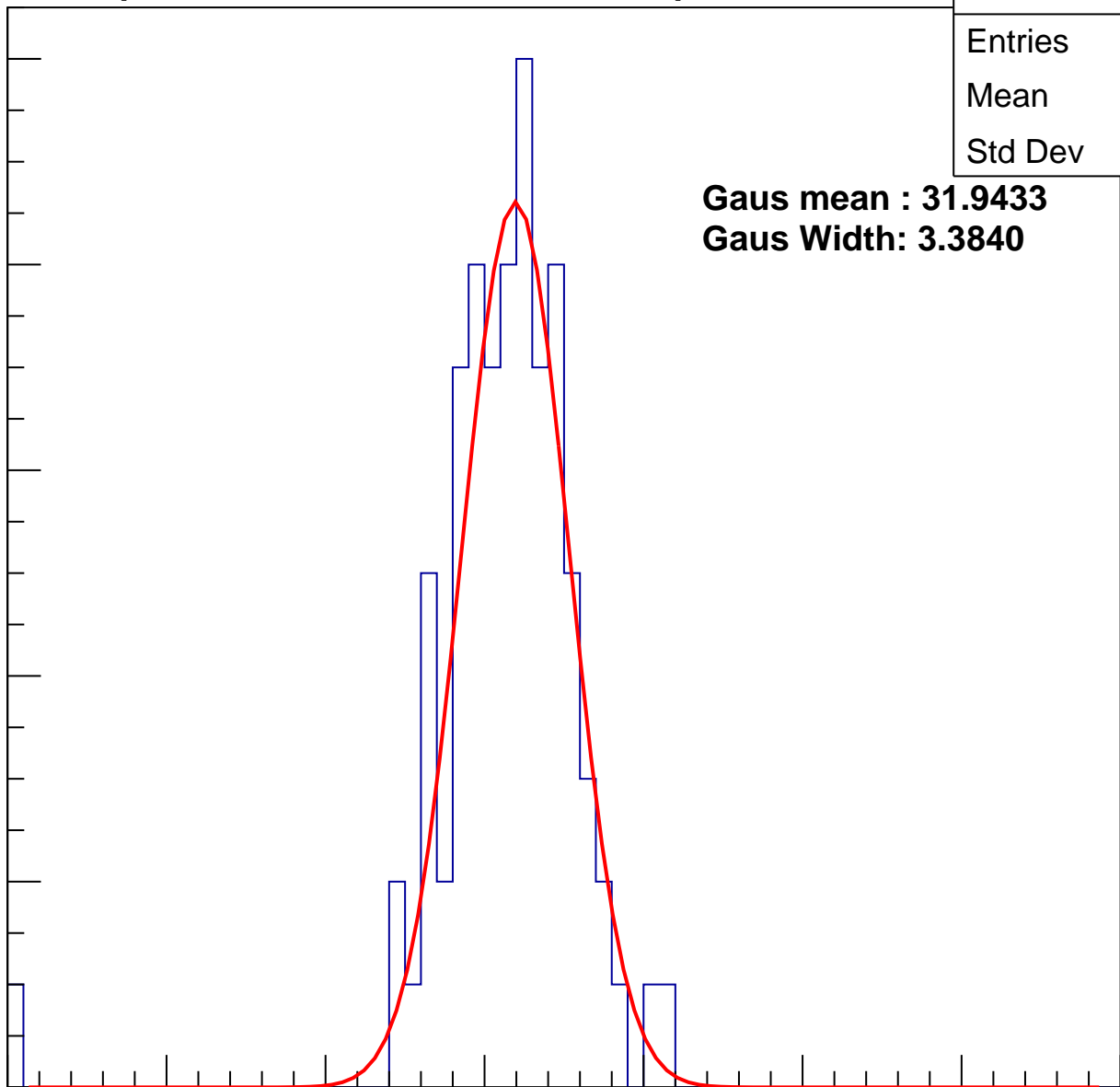
Gaus Width: 3.3840

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



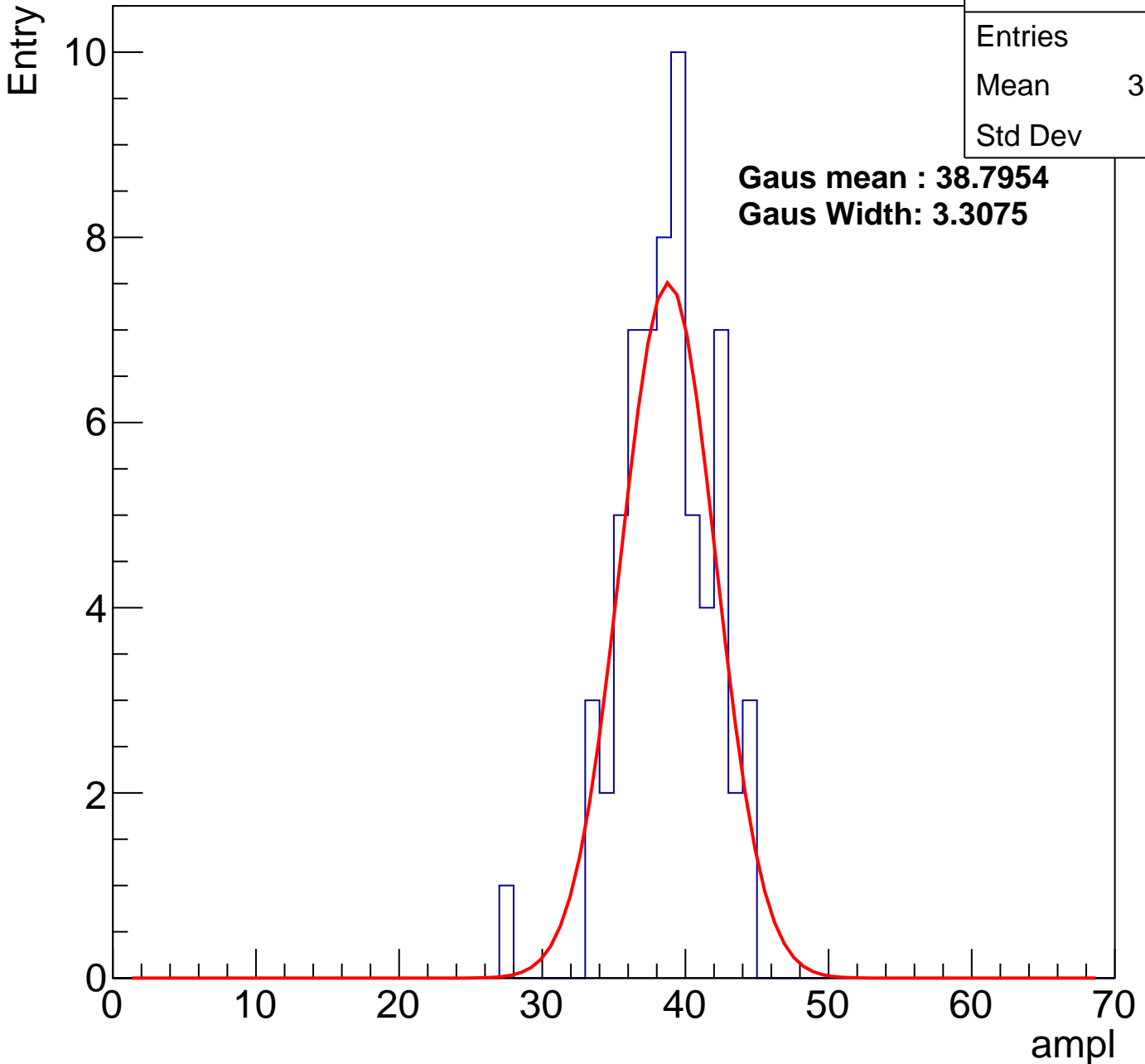
B0L002S, U17-ch25, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	38.28
Std Dev	3.16

Gaus mean : 38.7954

Gaus Width: 3.3075



B0L002S, U17-ch25, adc2

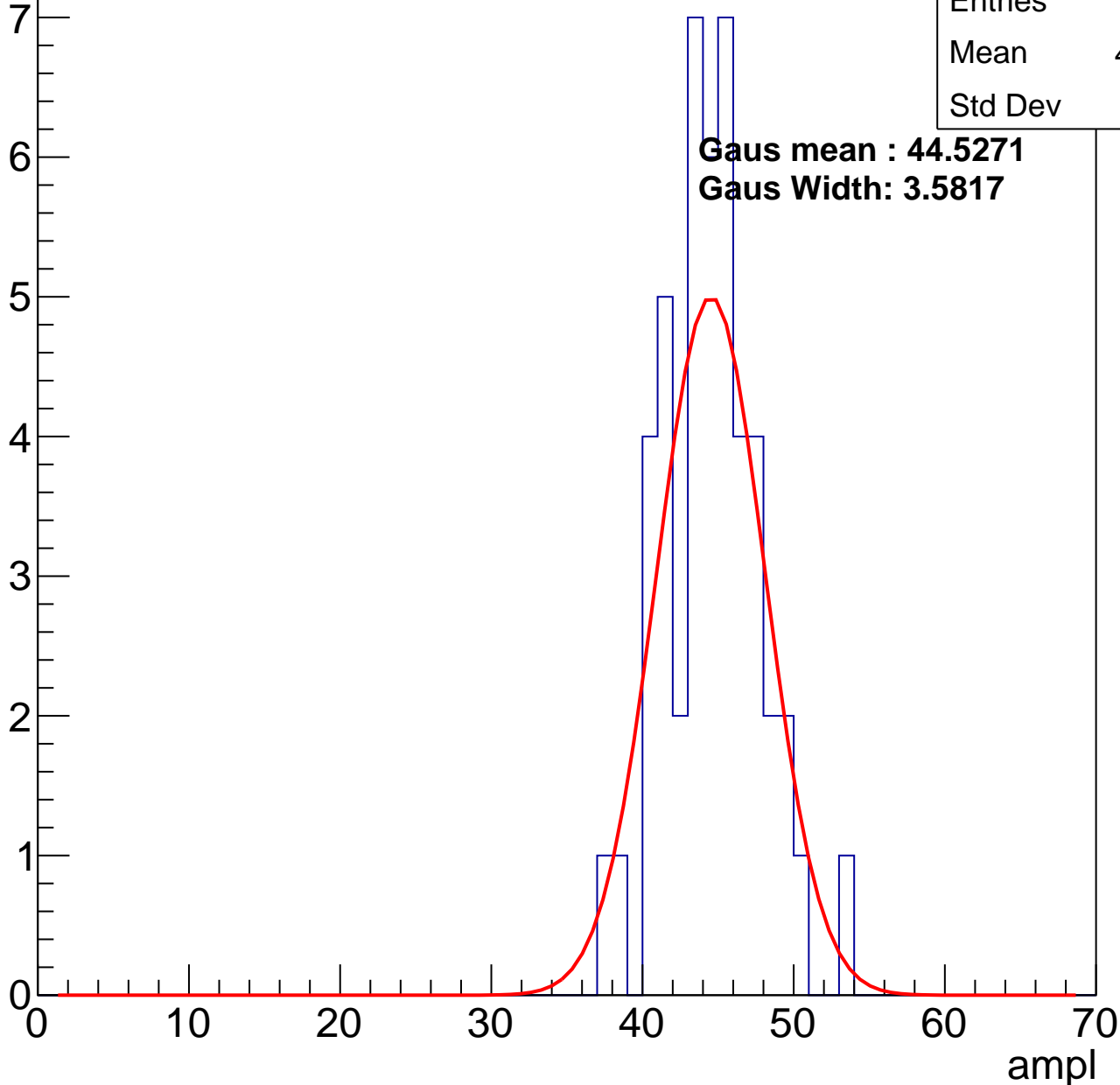
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	44.11
Std Dev	3.15

Gaus mean : 44.5271

Gaus Width: 3.5817

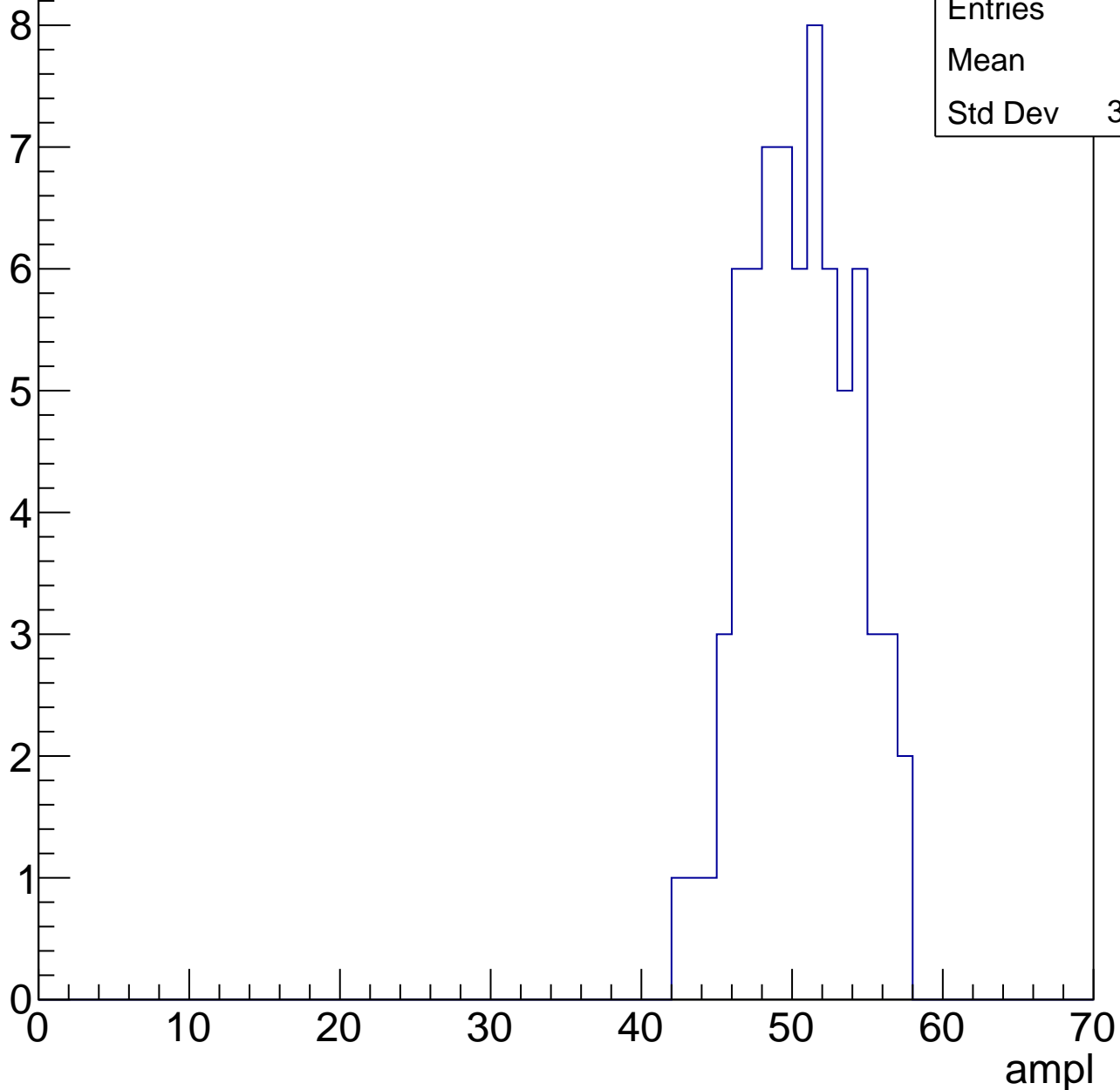


B0L002S, U17-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	50.1
Std Dev	3.485

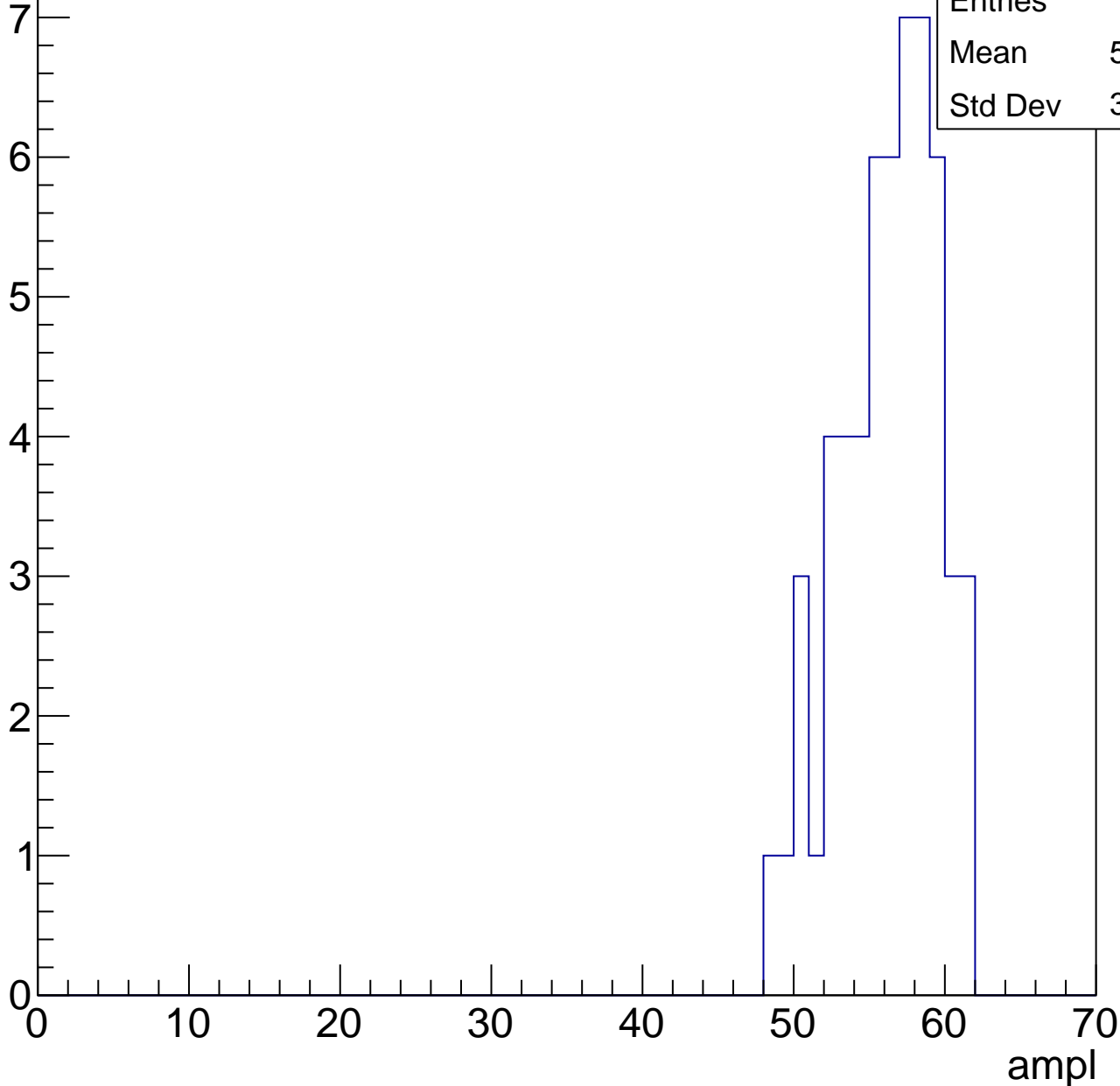


B0L002S, U17-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	55.75
Std Dev	3.209

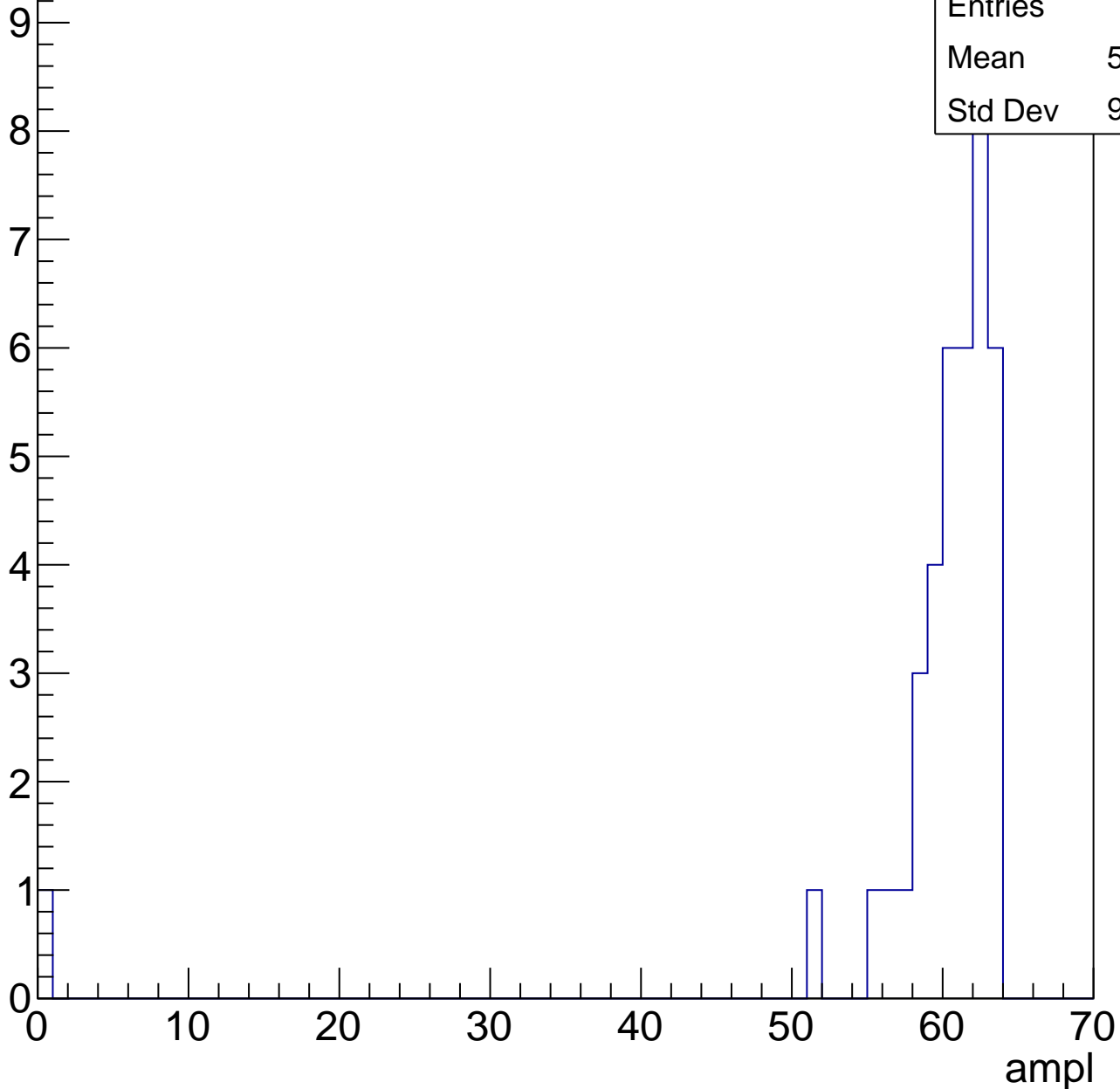


B0L002S, U17-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

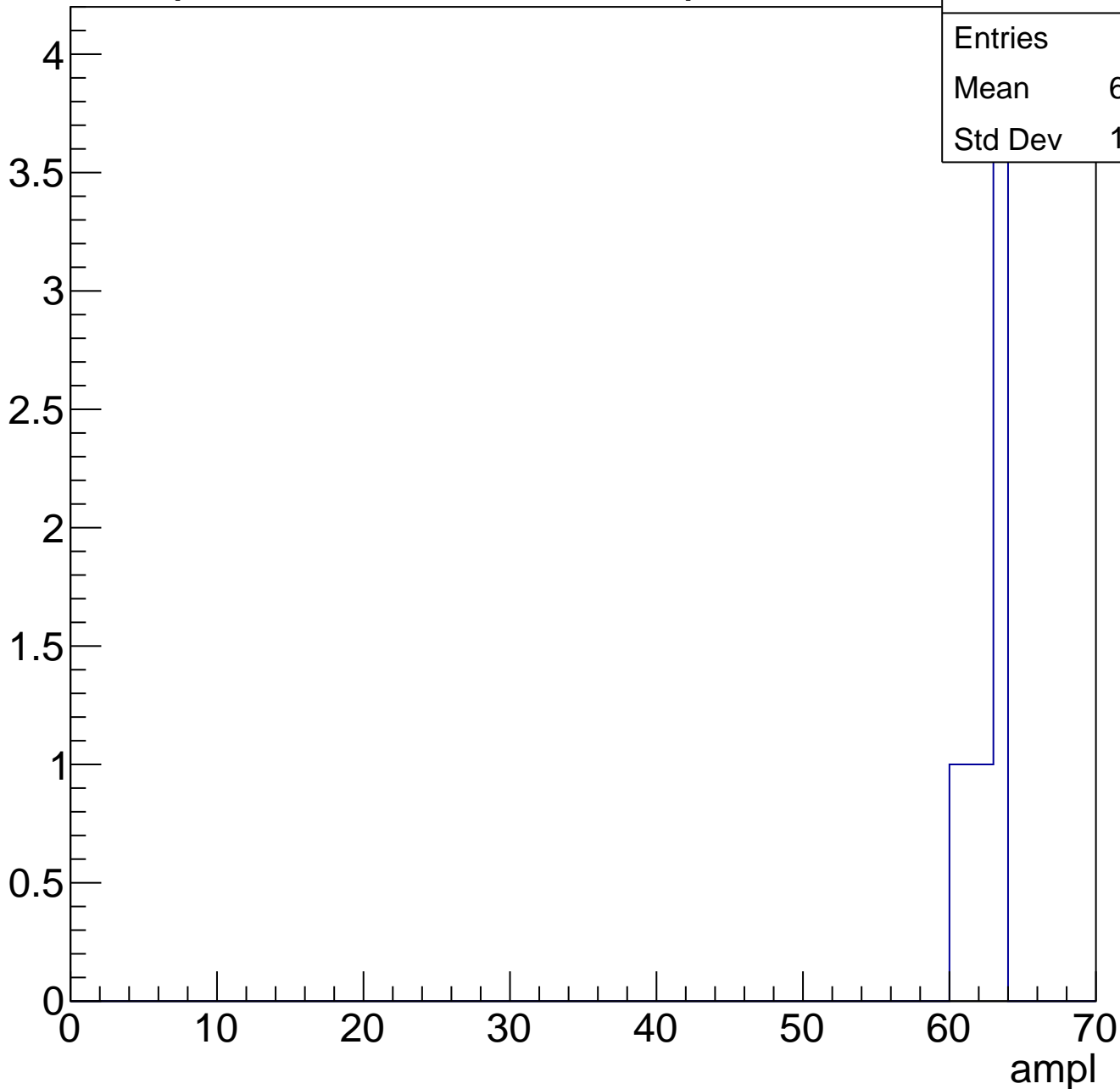
Entries	39
Mean	58.74
Std Dev	9.847



B0L002S, U17-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch25, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch26, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	29.16
Std Dev	4.572

Gaus mean : 30.1878

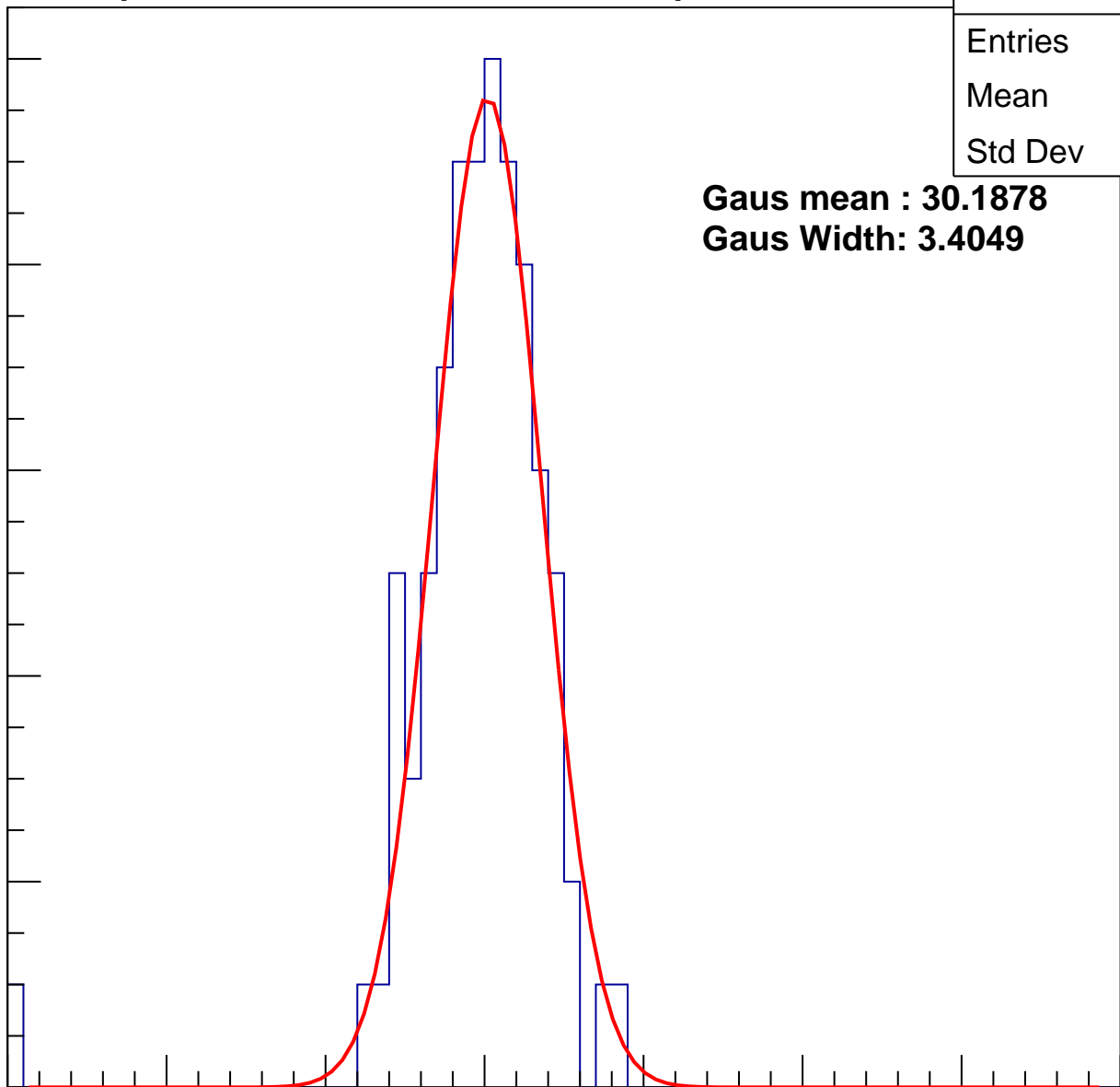
Gaus Width: 3.4049

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



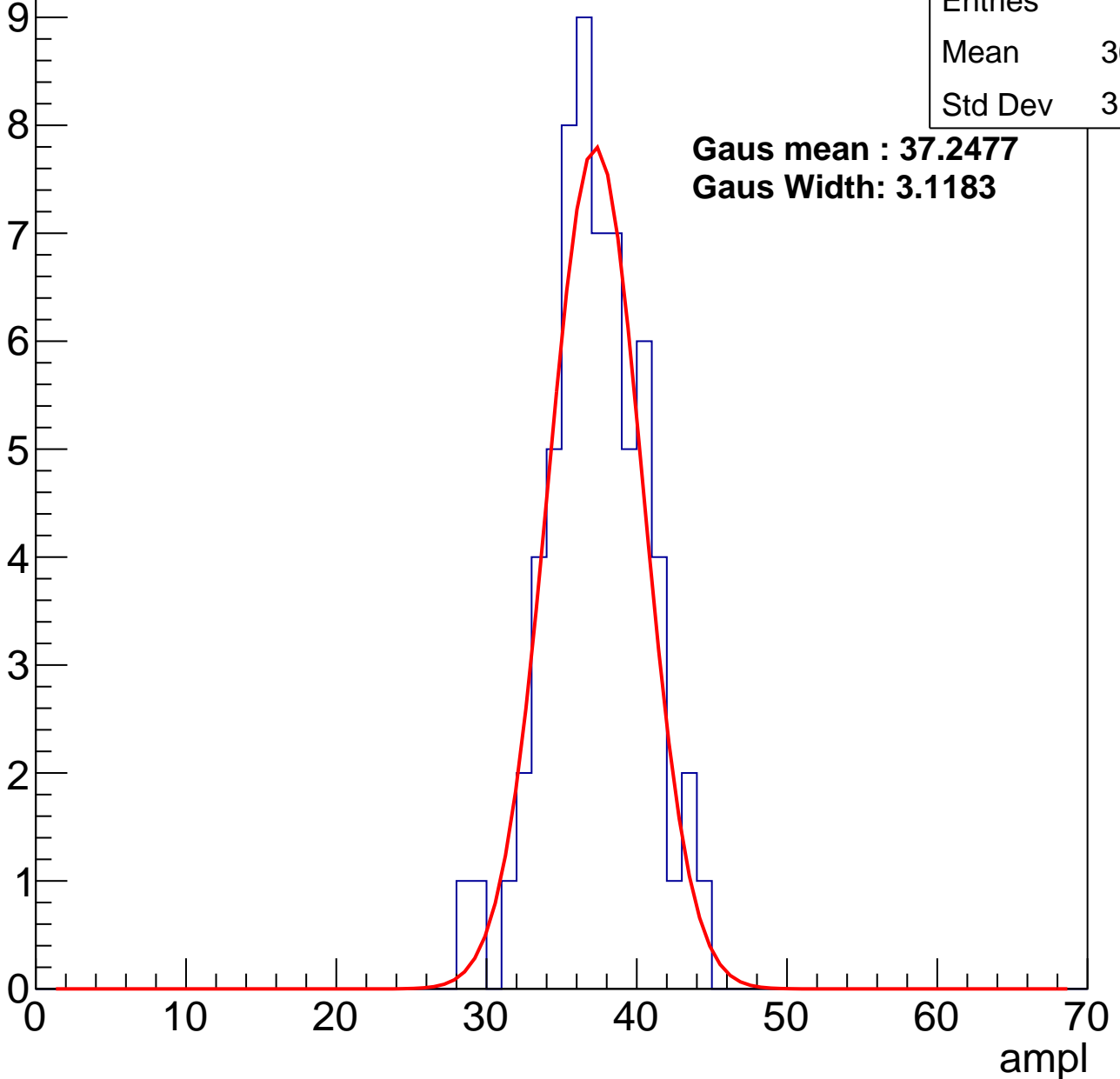
B0L002S, U17-ch26, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.78
Std Dev	3.233

Gaus mean : 37.2477
Gaus Width: 3.1183



B0L002S, U17-ch26, adc2

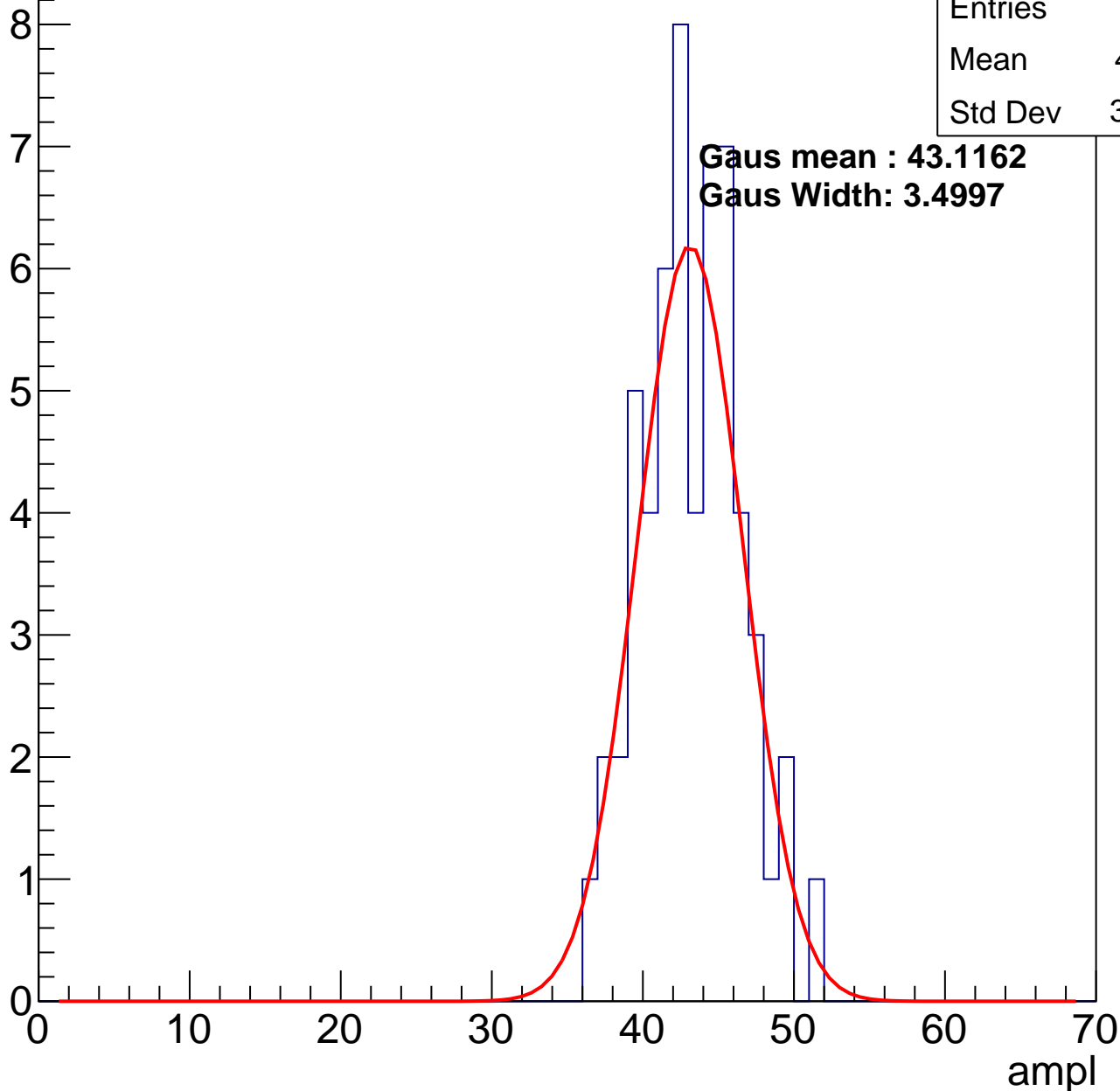
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.81
Std Dev	3.236

Gaus mean : 43.1162

Gaus Width: 3.4997

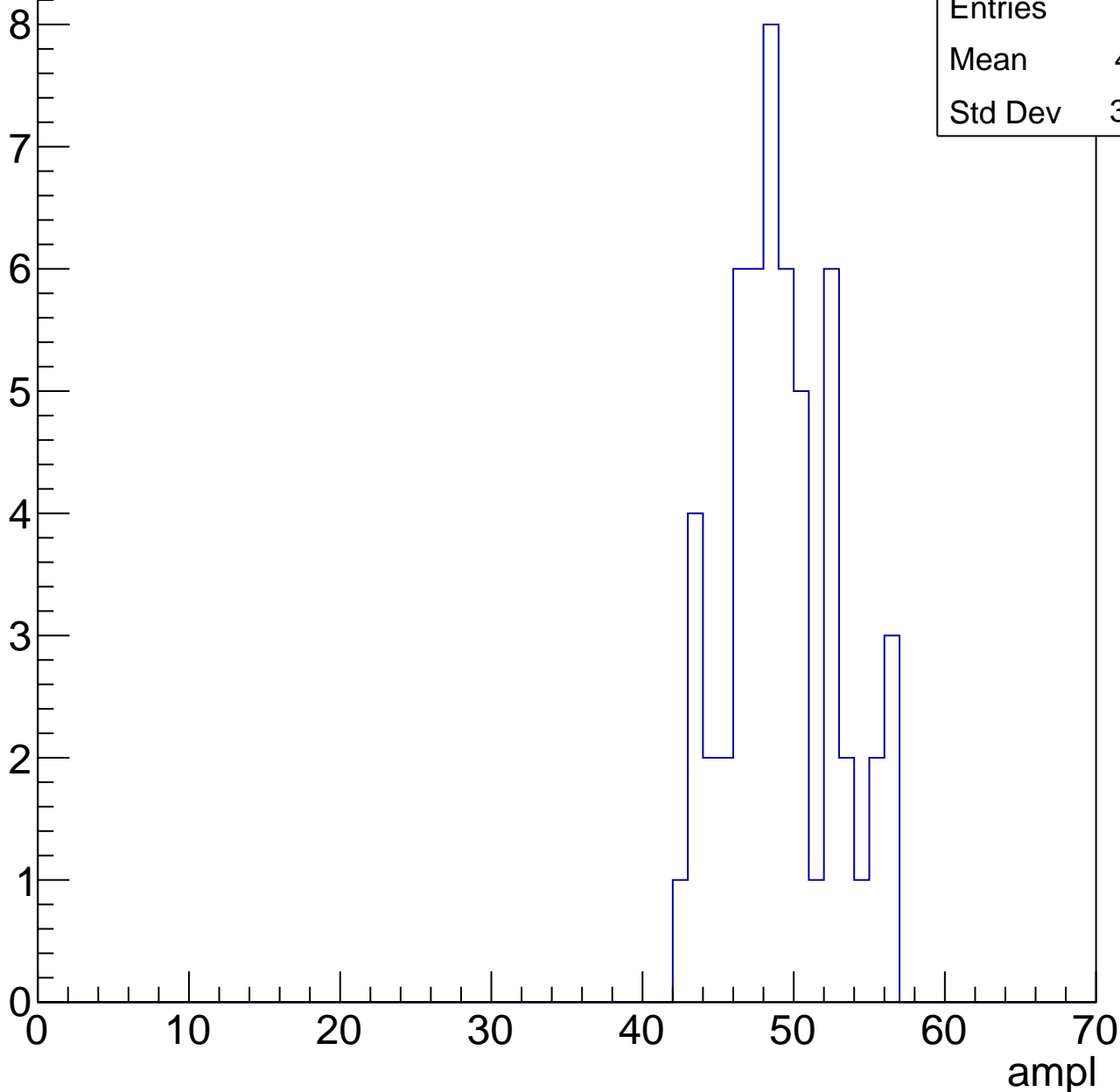


B0L002S, U17-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	48.71
Std Dev	3.556

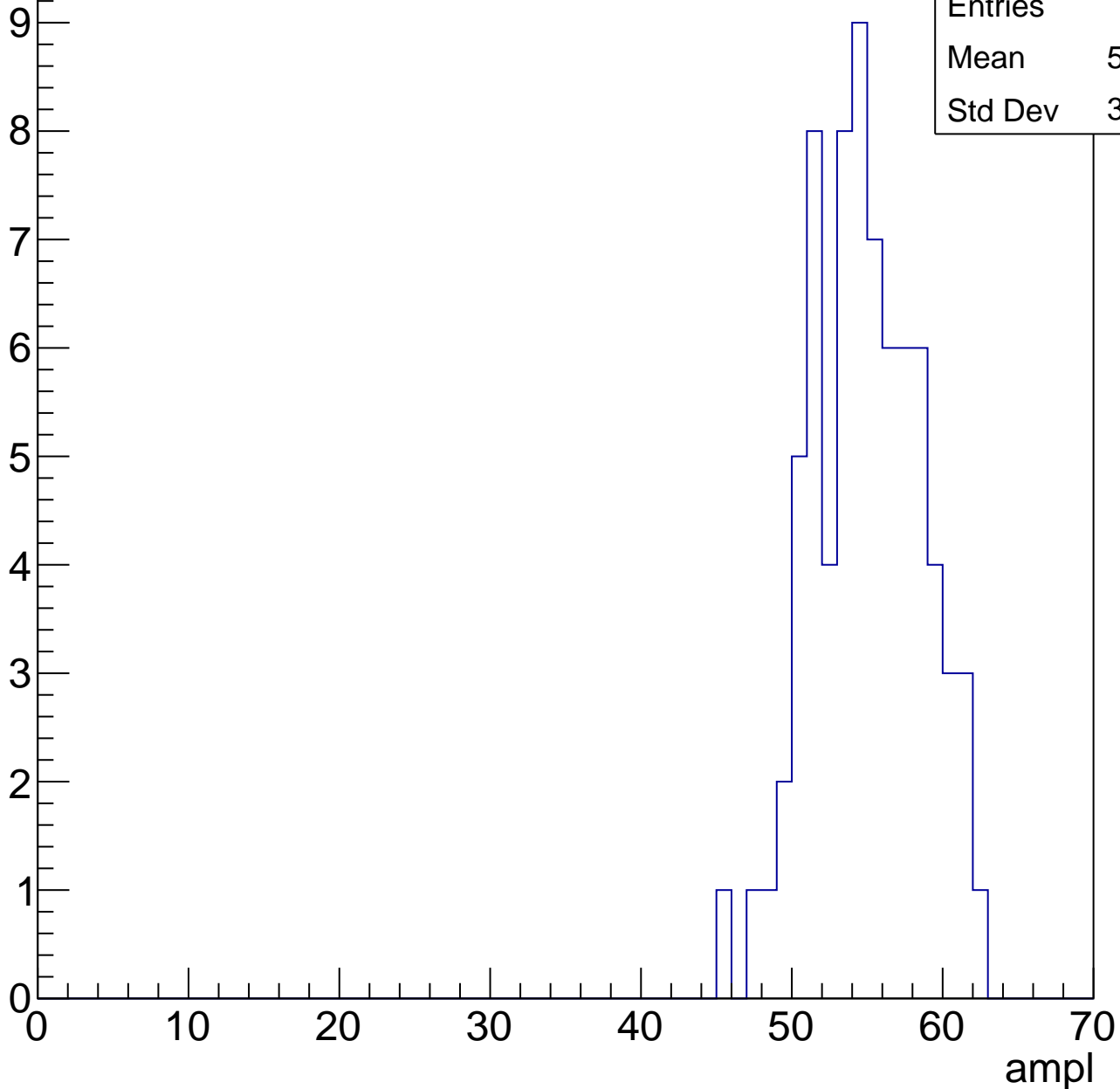


B0L002S, U17-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	54.48
Std Dev	3.605

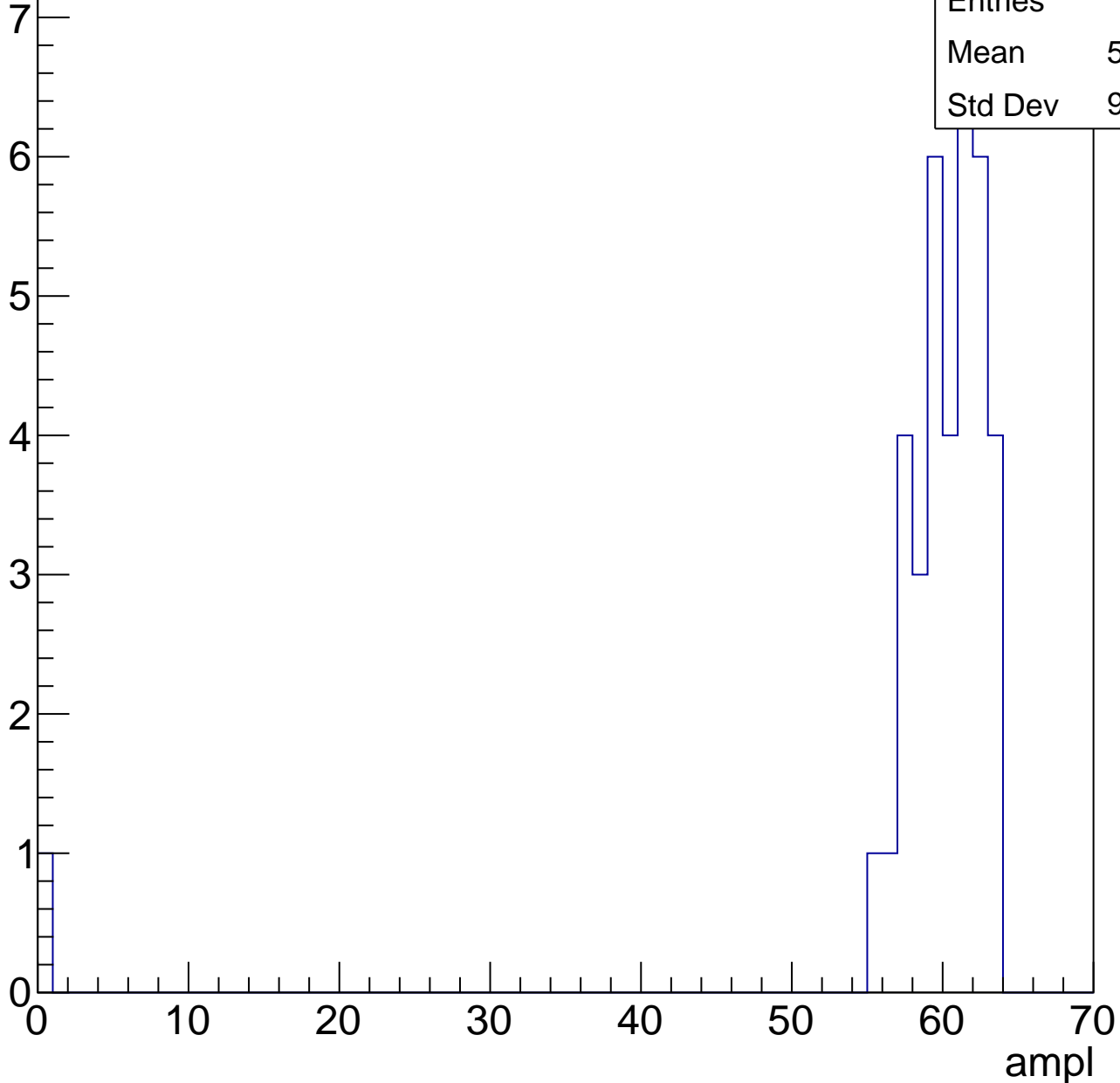


B0L002S, U17-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.32
Std Dev	9.943

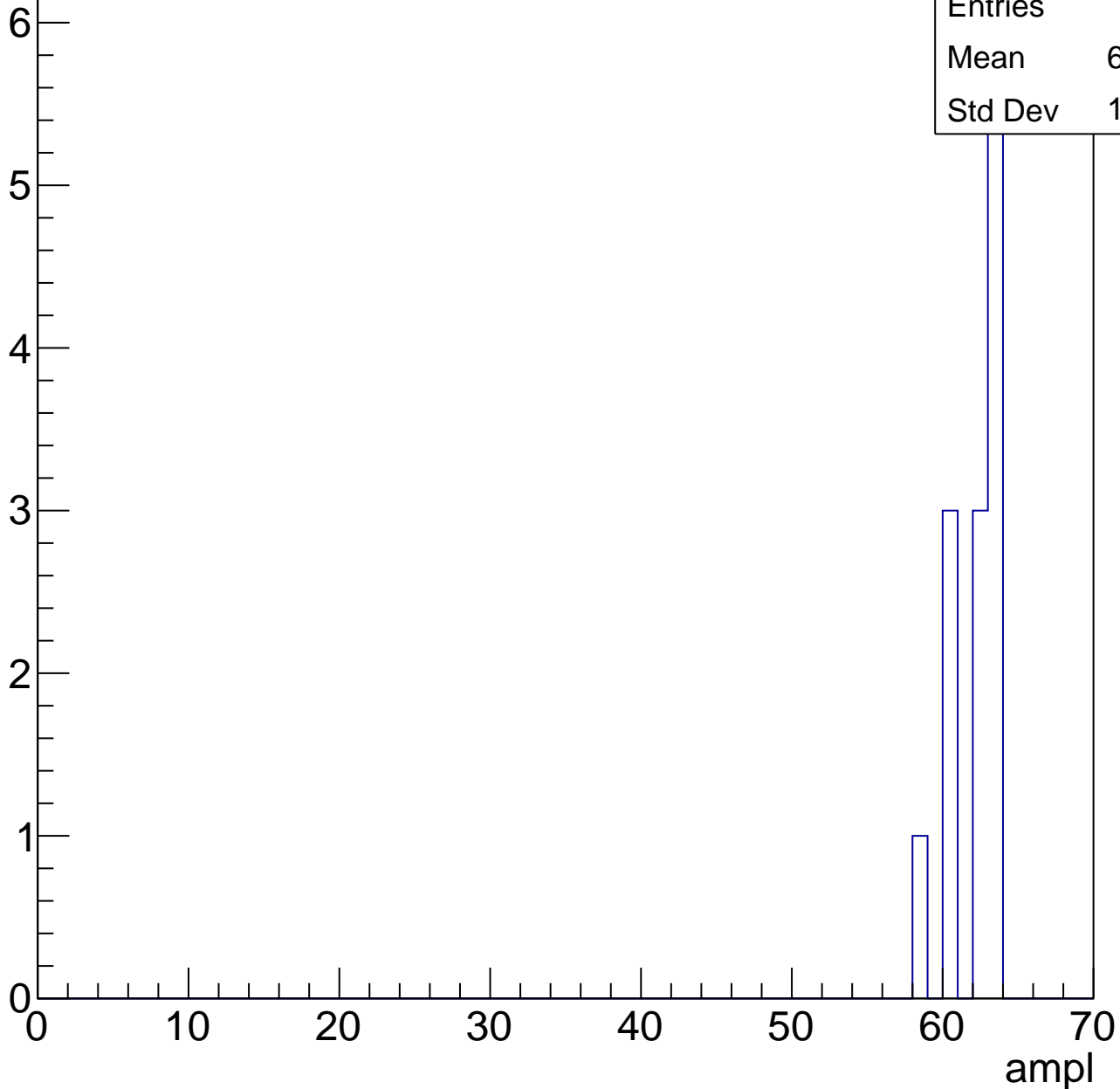


B0L002S, U17-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	13
Mean	61.69
Std Dev	1.588



B0L002S, U17-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U17-ch27, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	58
Mean	30.43
Std Dev	2.717

Gaus mean : 30.4059

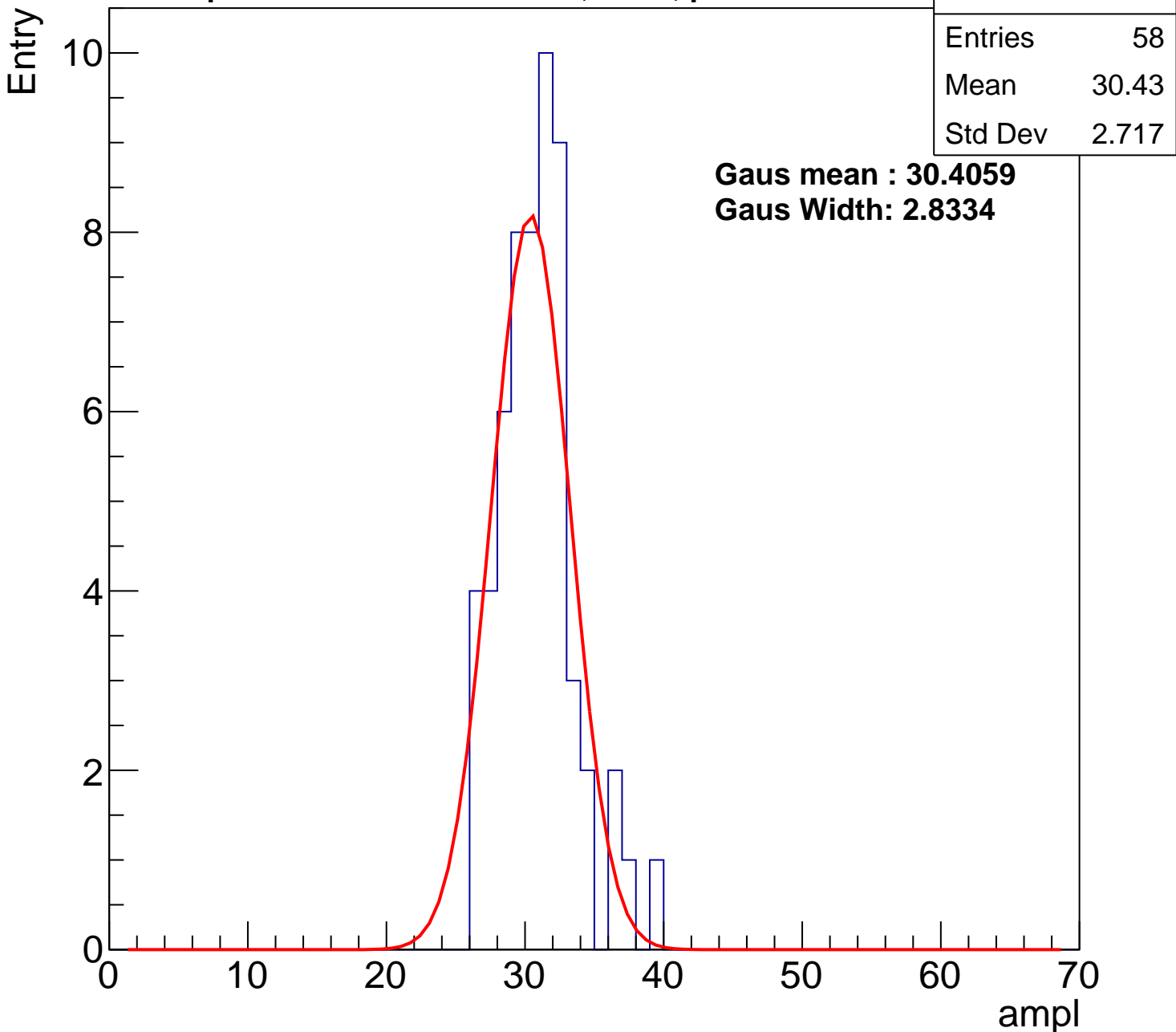
Gaus Width: 2.8334

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch27, adc1

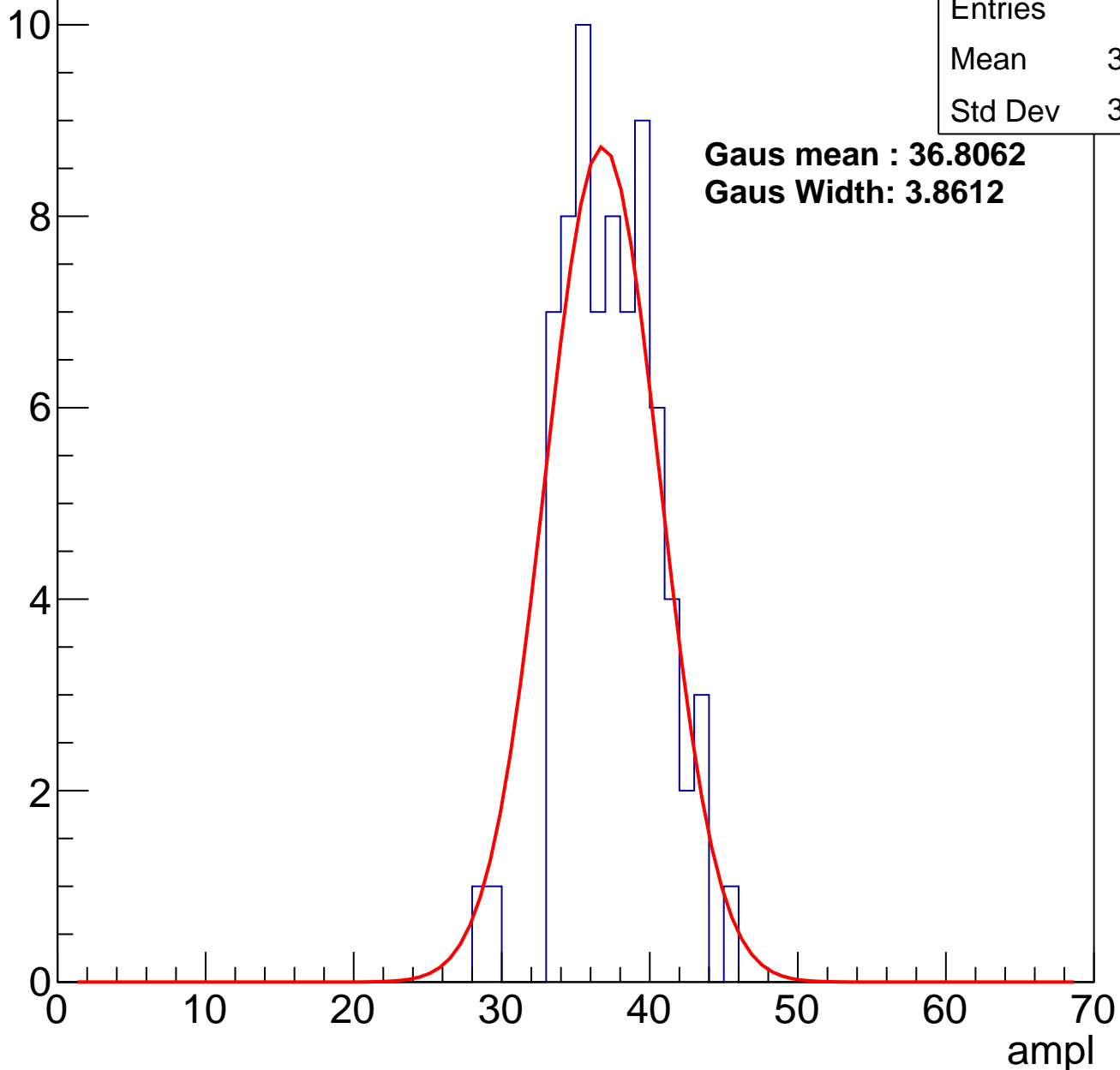
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	36.99
Std Dev	3.203

Gaus mean : 36.8062

Gaus Width: 3.8612

Entry



B0L002S, U17-ch27, adc2

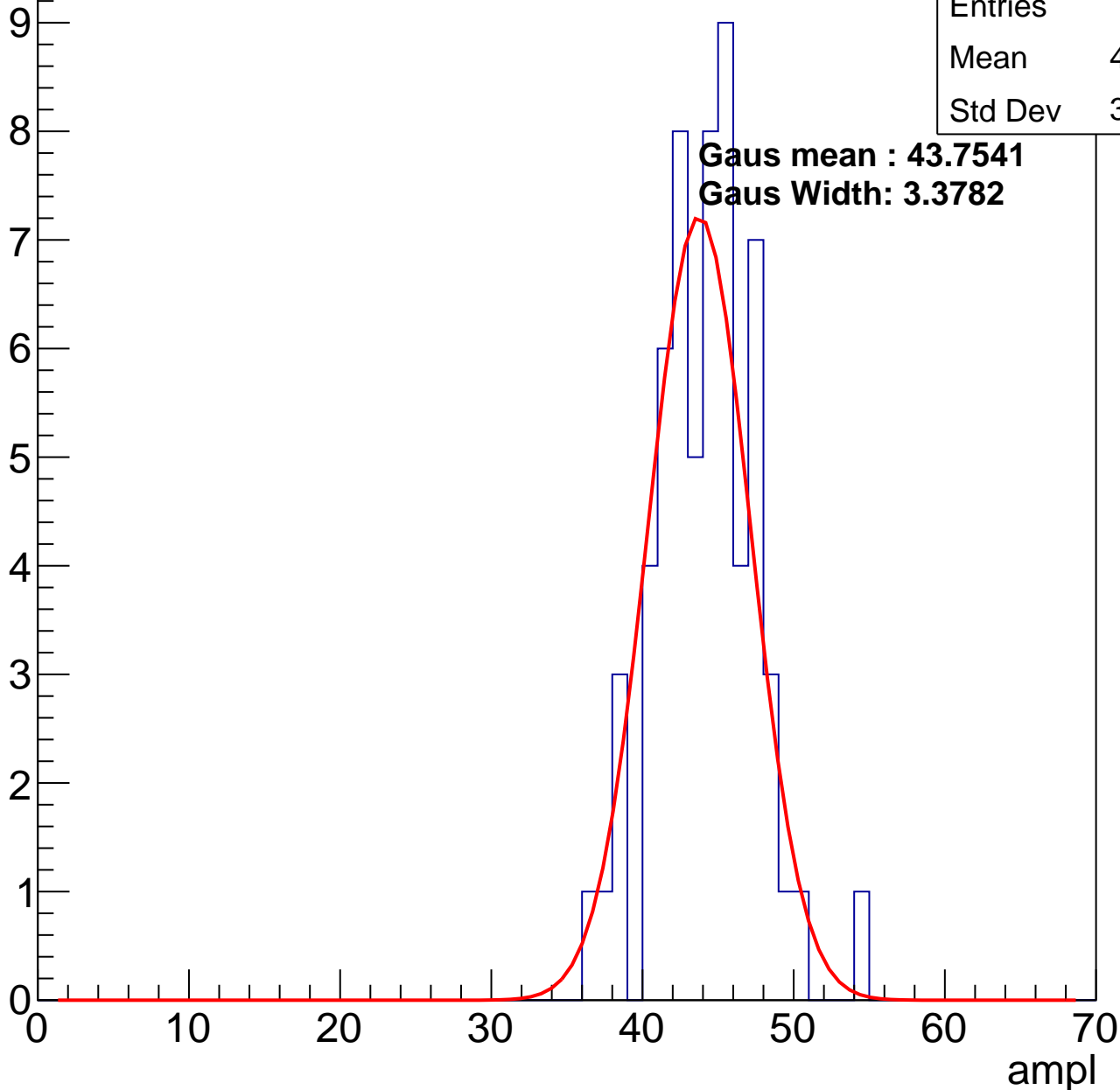
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.73
Std Dev	3.283

Gaus mean : 43.7541

Gaus Width: 3.3782

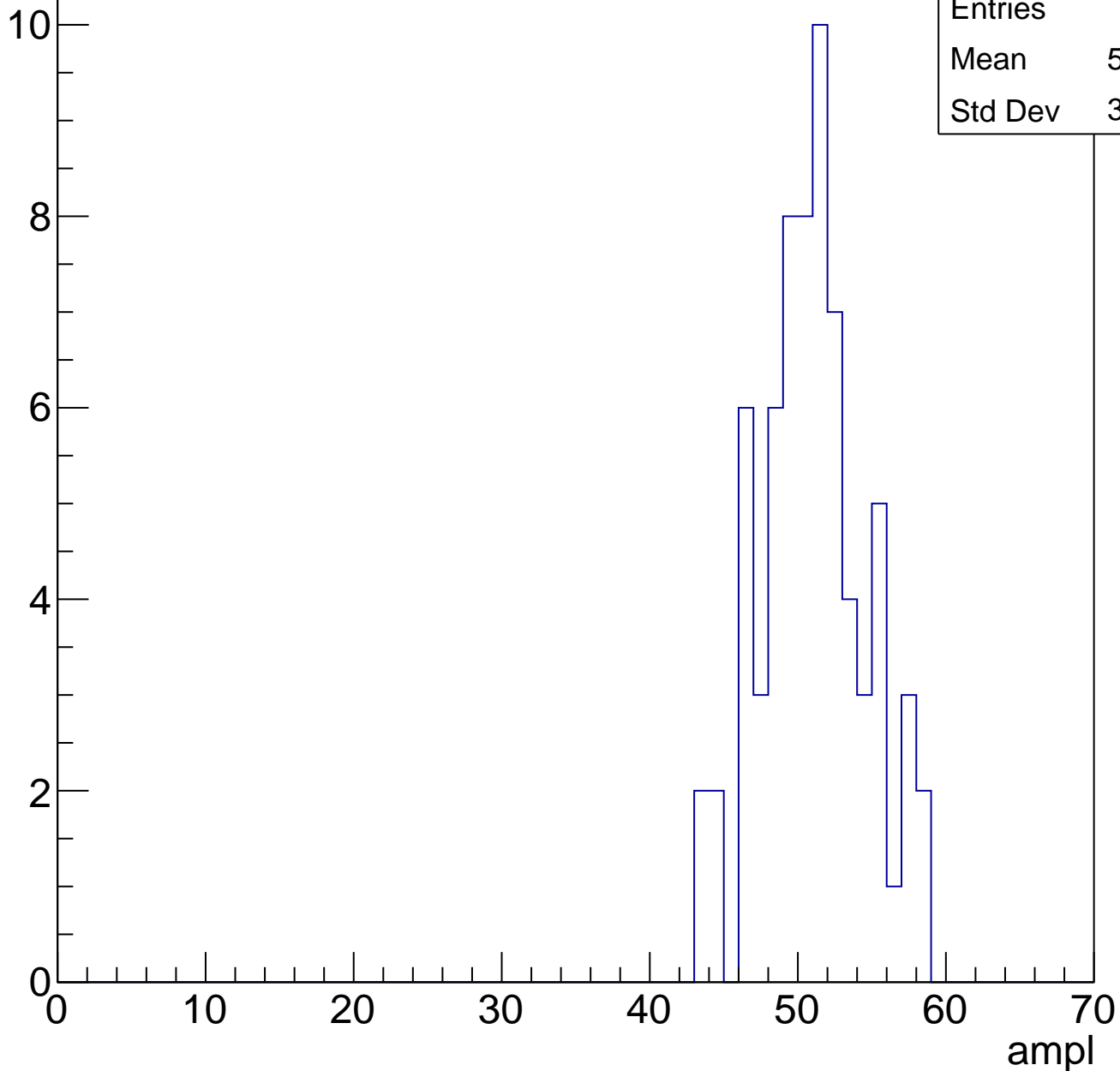


B0L002S, U17-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	50.53
Std Dev	3.512

Entry

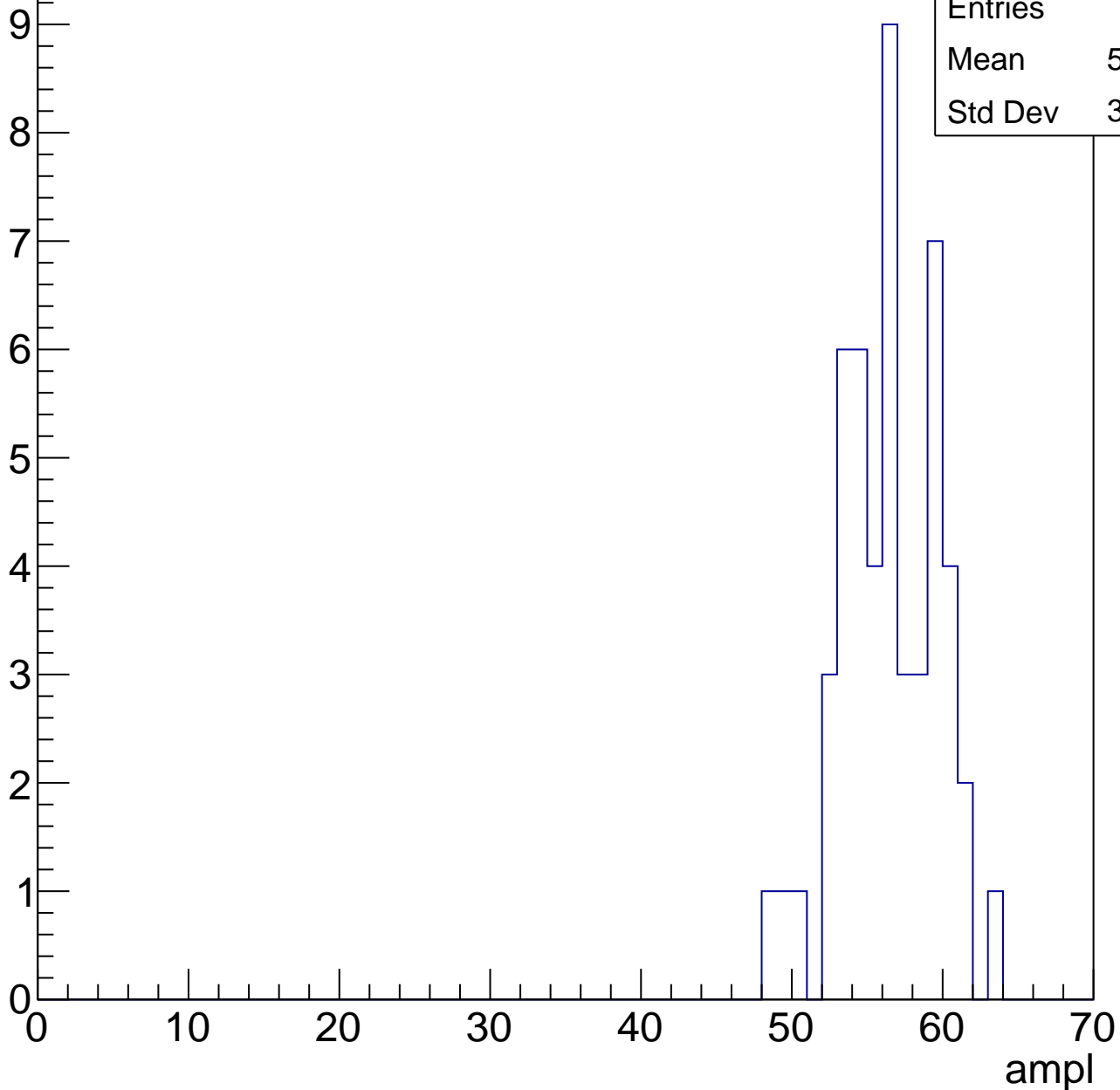


B0L002S, U17-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.92
Std Dev	3.192

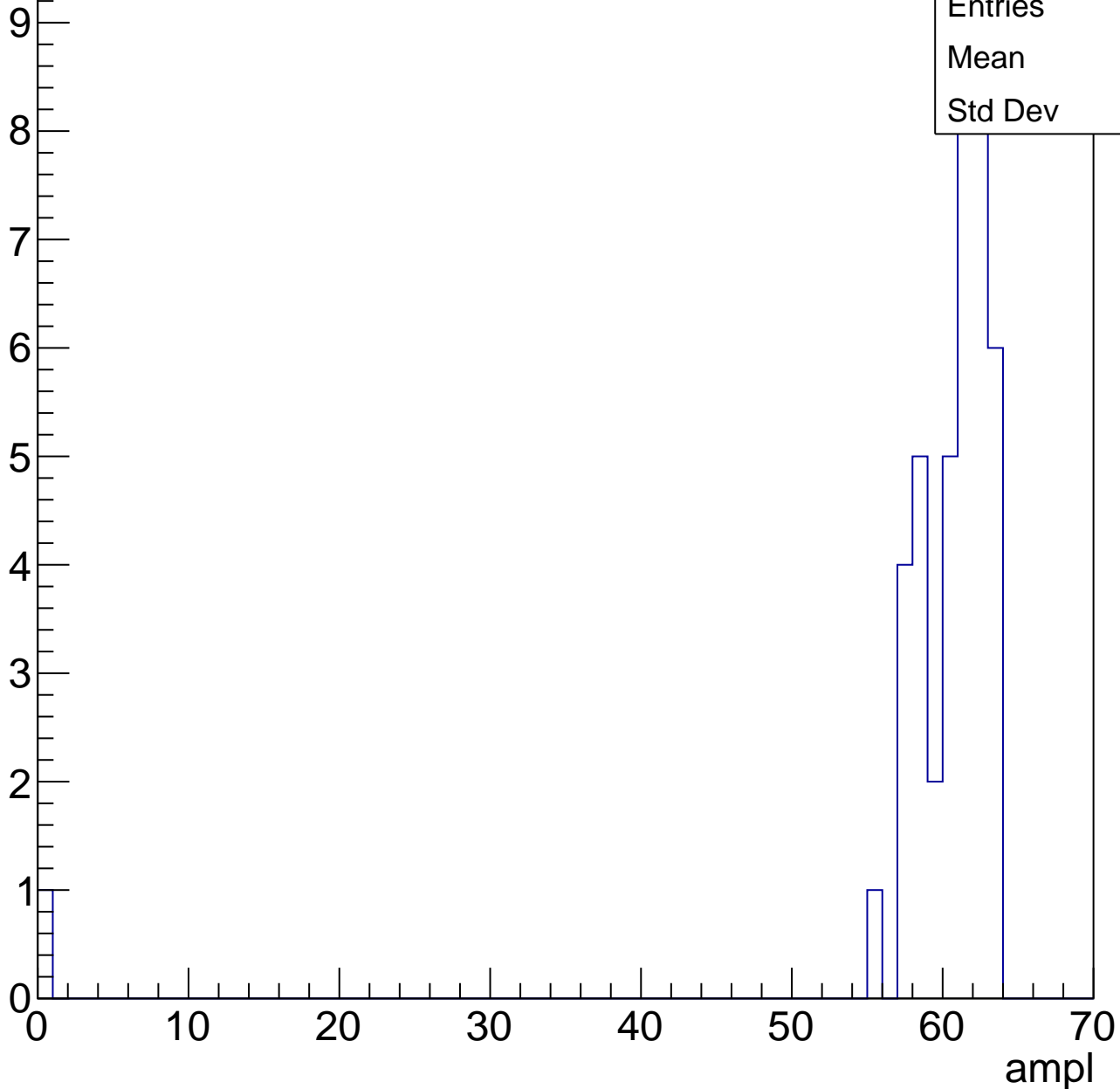


B0L002S, U17-ch27, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

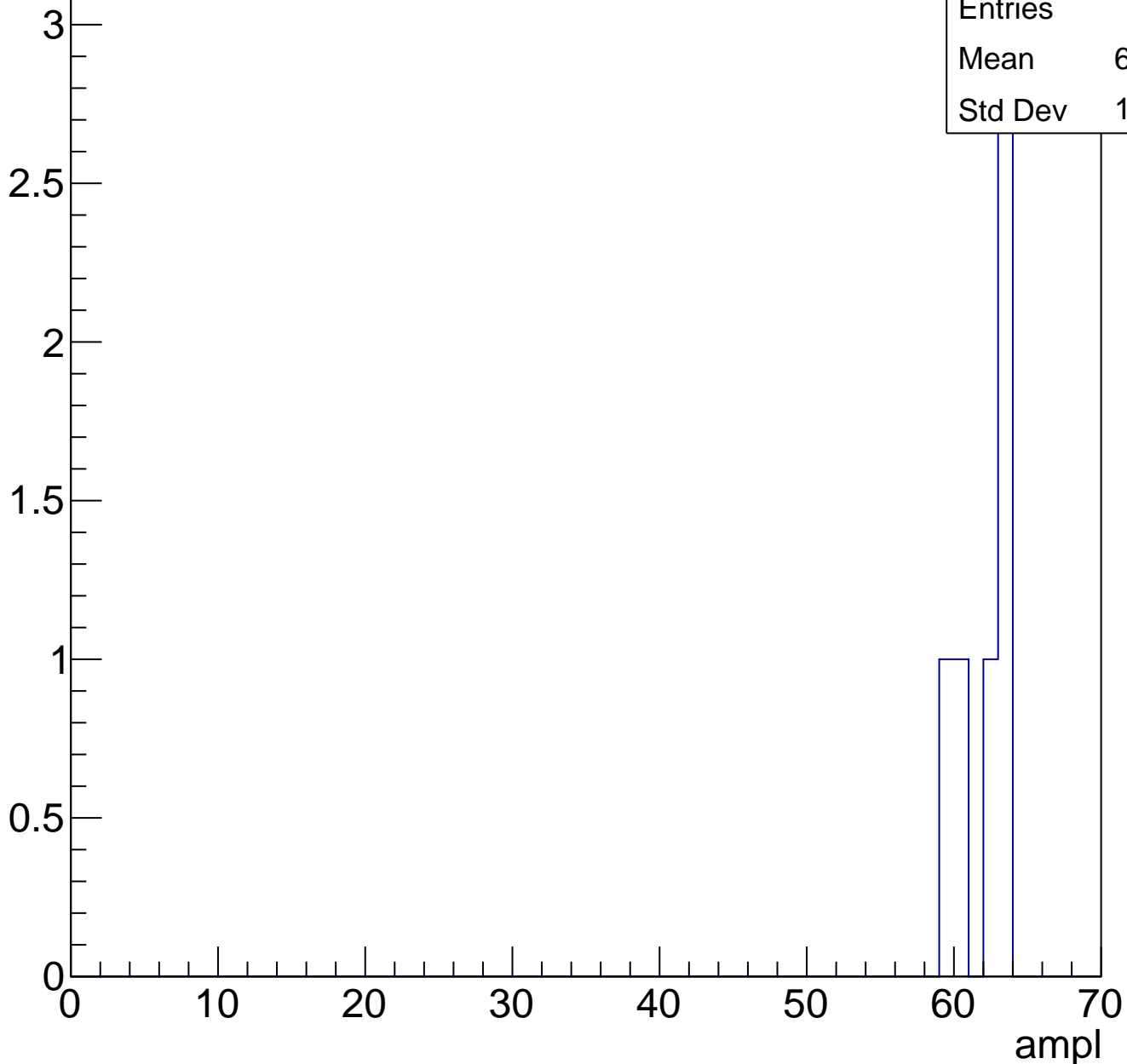
Entries	41
Mean	58.9
Std Dev	9.54



B0L002S, U17-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch28, adc0

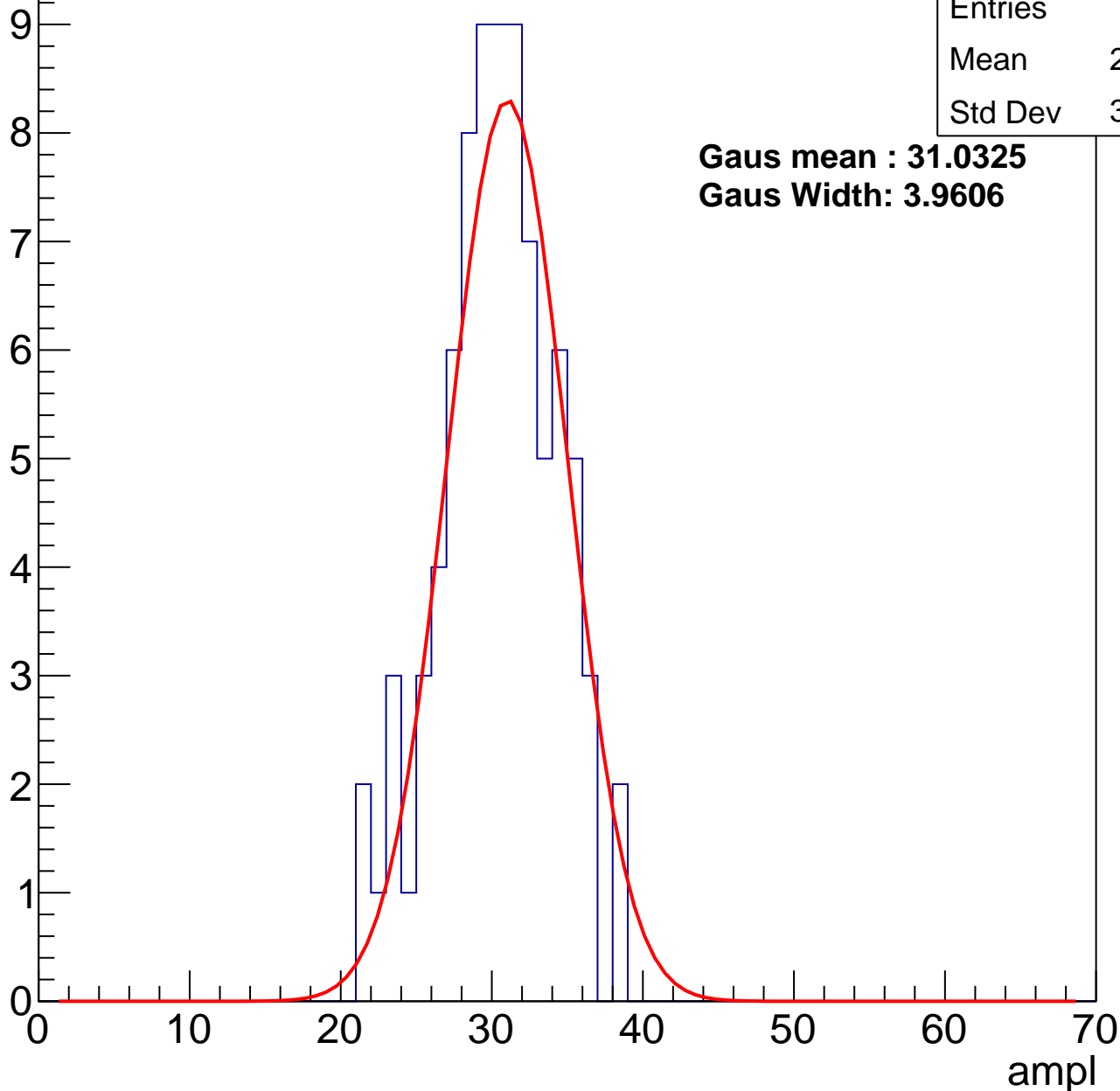
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	29.93
Std Dev	3.773

Gaus mean : 31.0325

Gaus Width: 3.9606



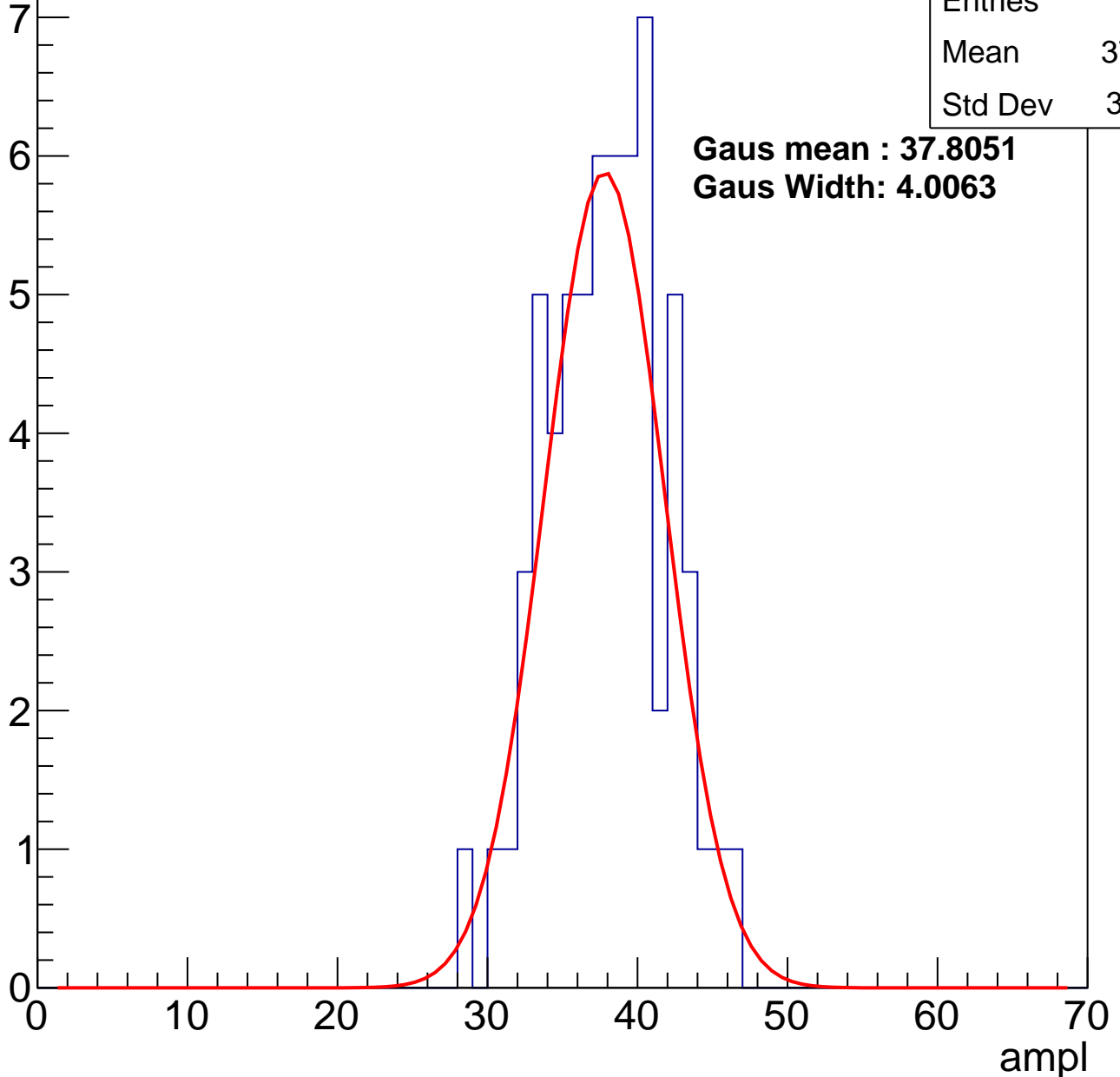
B0L002S, U17-ch28, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.48
Std Dev	3.821

Gaus mean : 37.8051
Gaus Width: 4.0063



B0L002S, U17-ch28, adc2

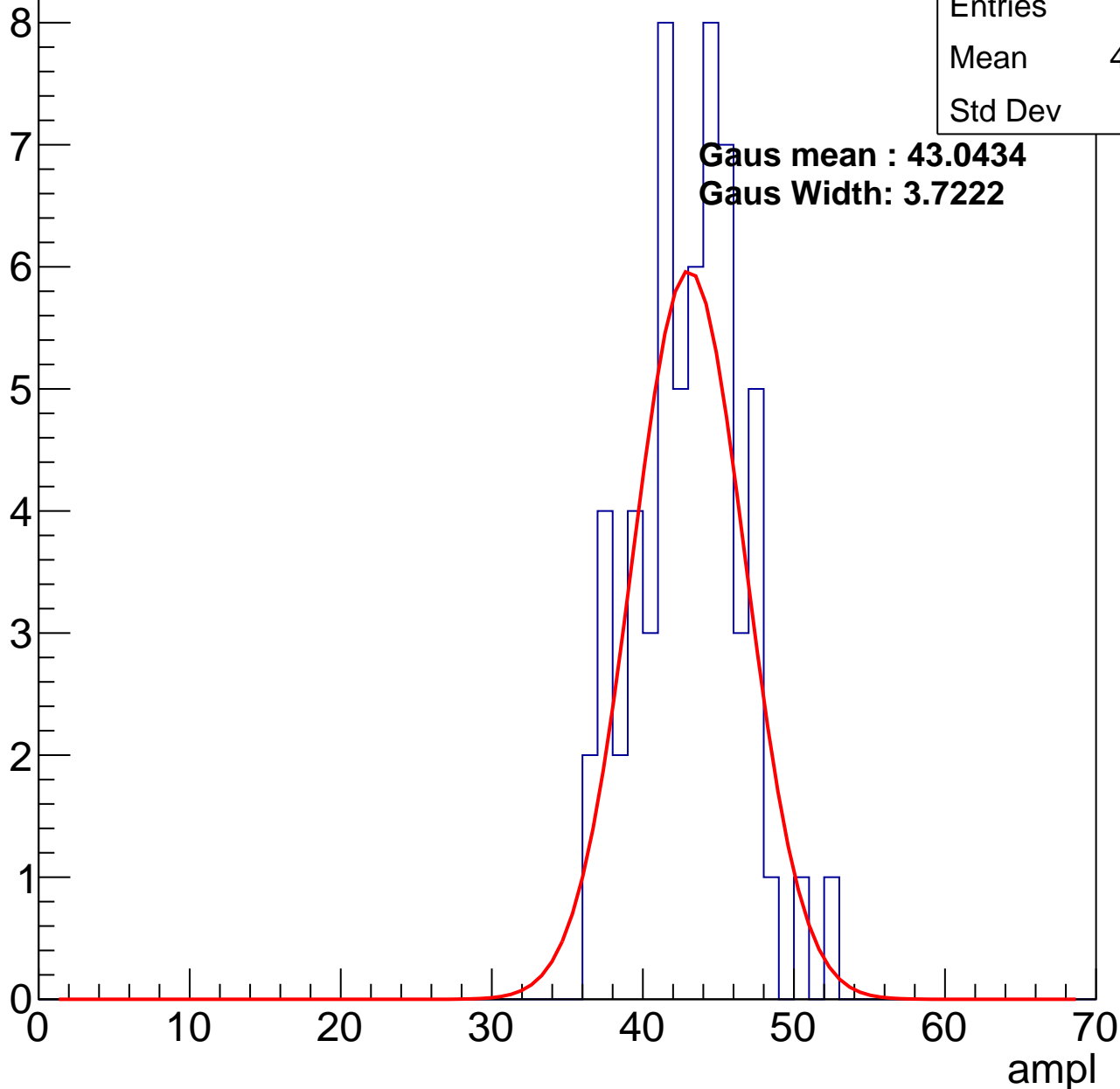
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	42.63
Std Dev	3.44

Gaus mean : 43.0434

Gaus Width: 3.7222

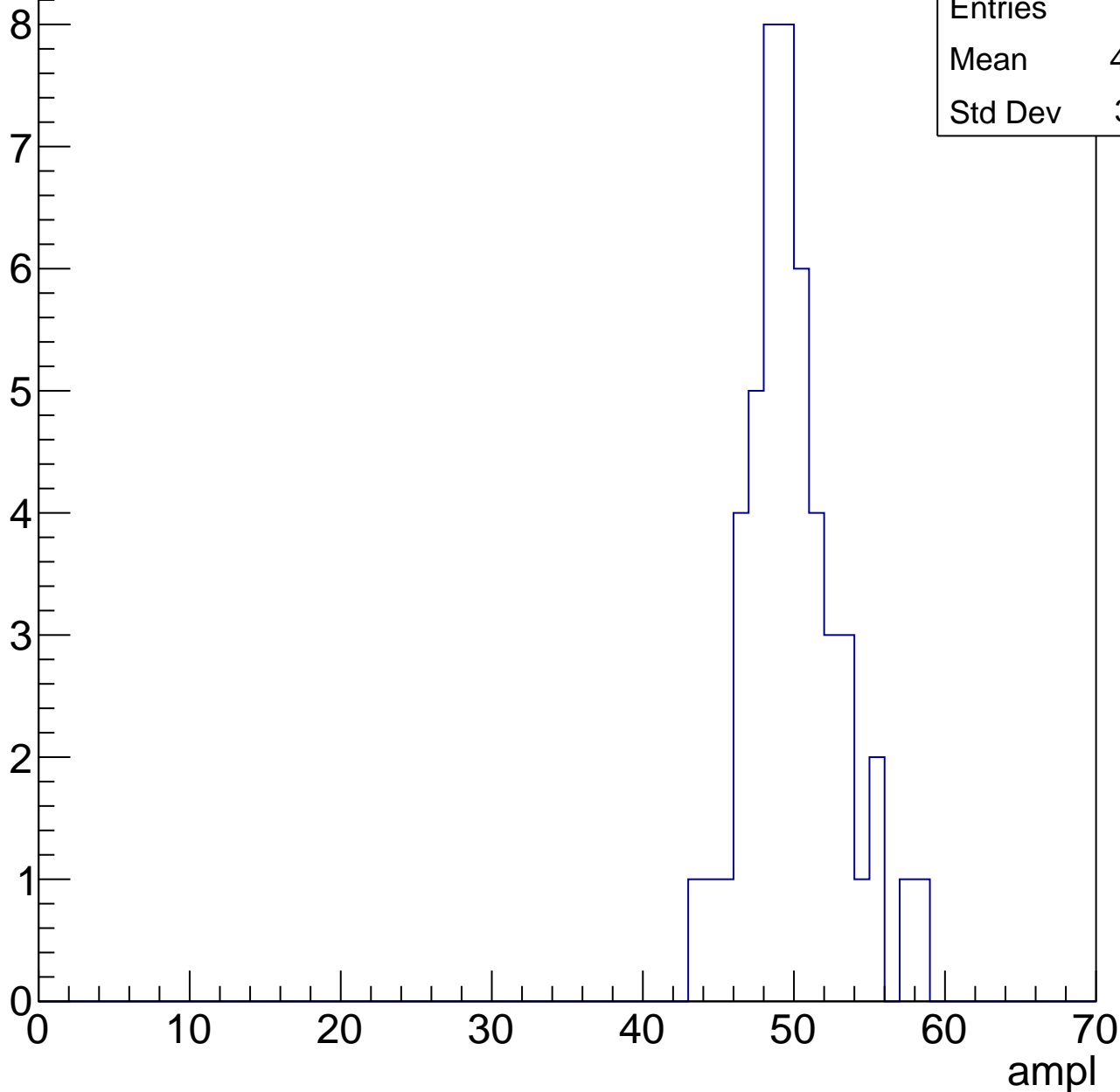


B0L002S, U17-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	49.49
Std Dev	3.091

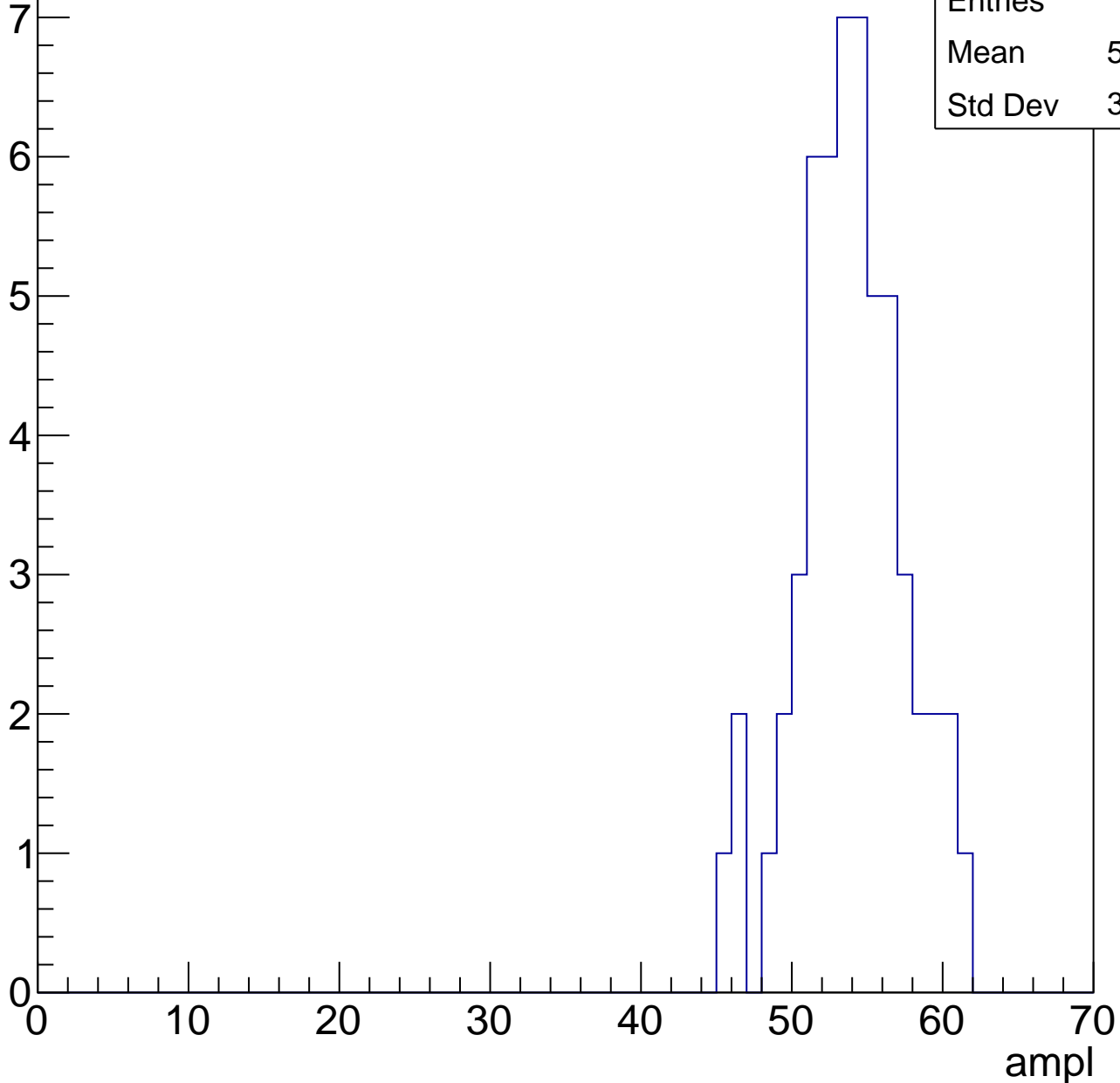


B0L002S, U17-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

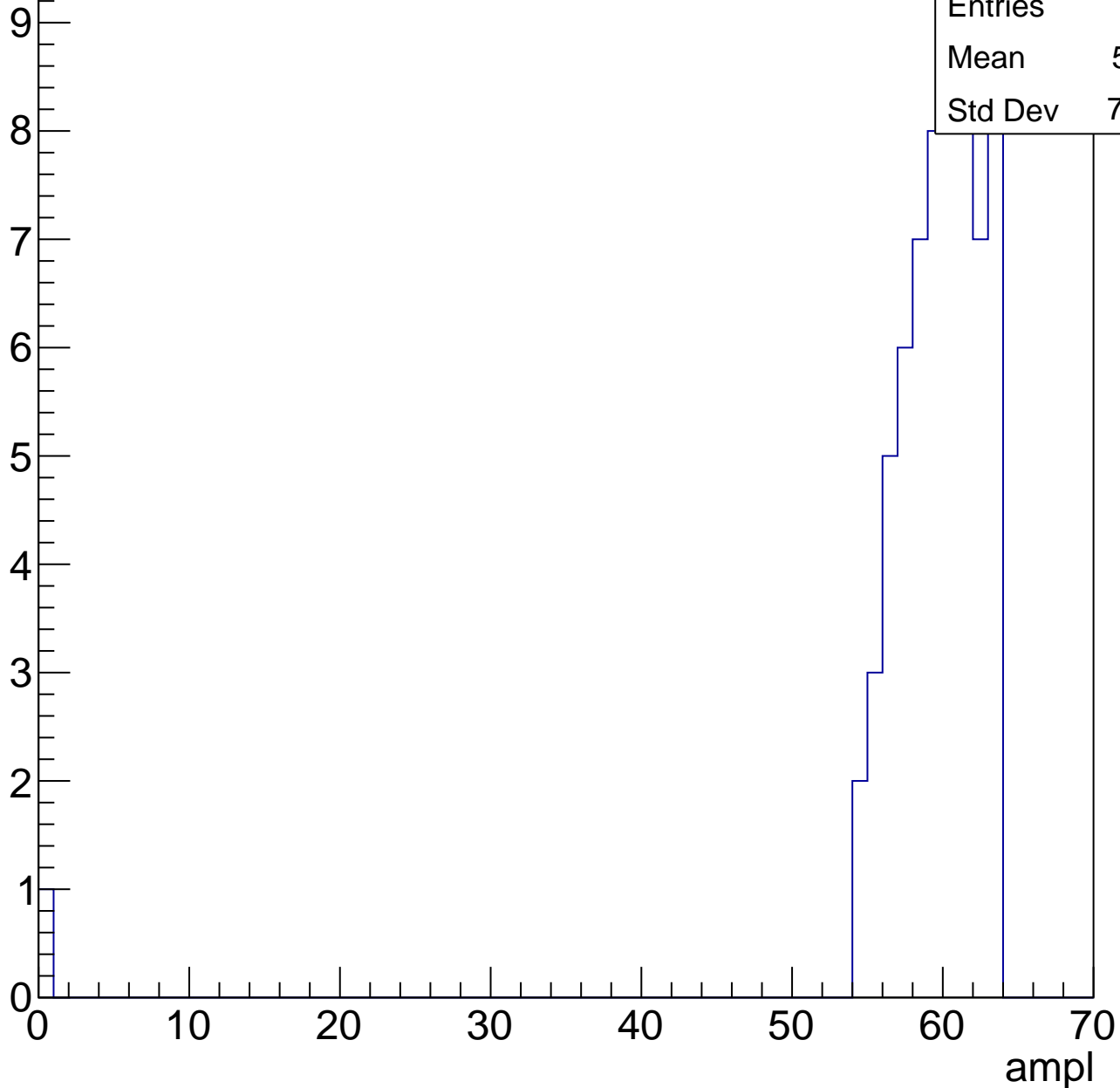
Entries	55
Mean	53.47
Std Dev	3.484



B0L002S, U17-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch29, adc0

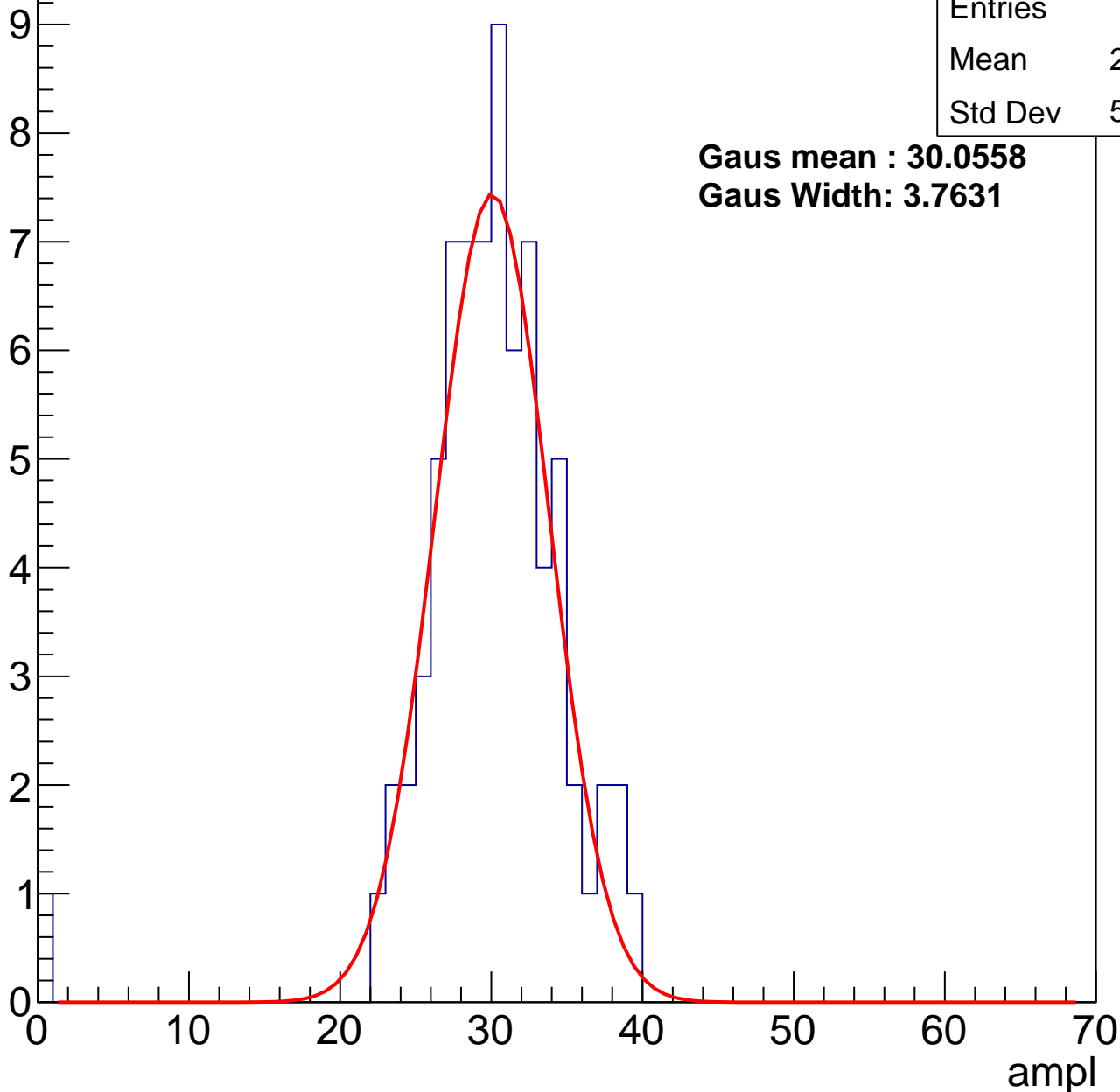
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	29.54
Std Dev	5.086

Gaus mean : 30.0558

Gaus Width: 3.7631



B0L002S, U17-ch29, adc1

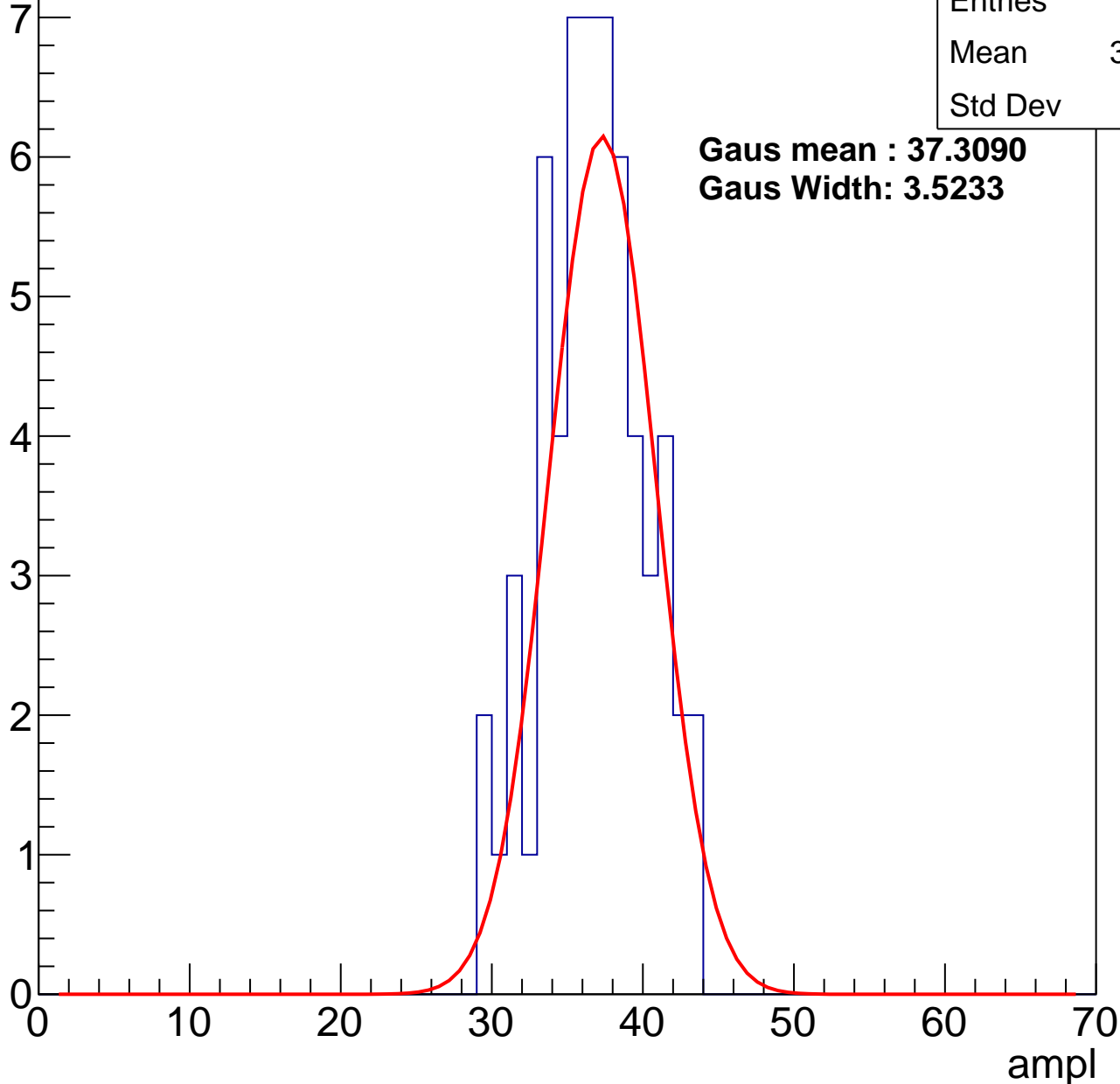
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.29
Std Dev	3.4

Gaus mean : 37.3090

Gaus Width: 3.5233



B0L002S, U17-ch29, adc2

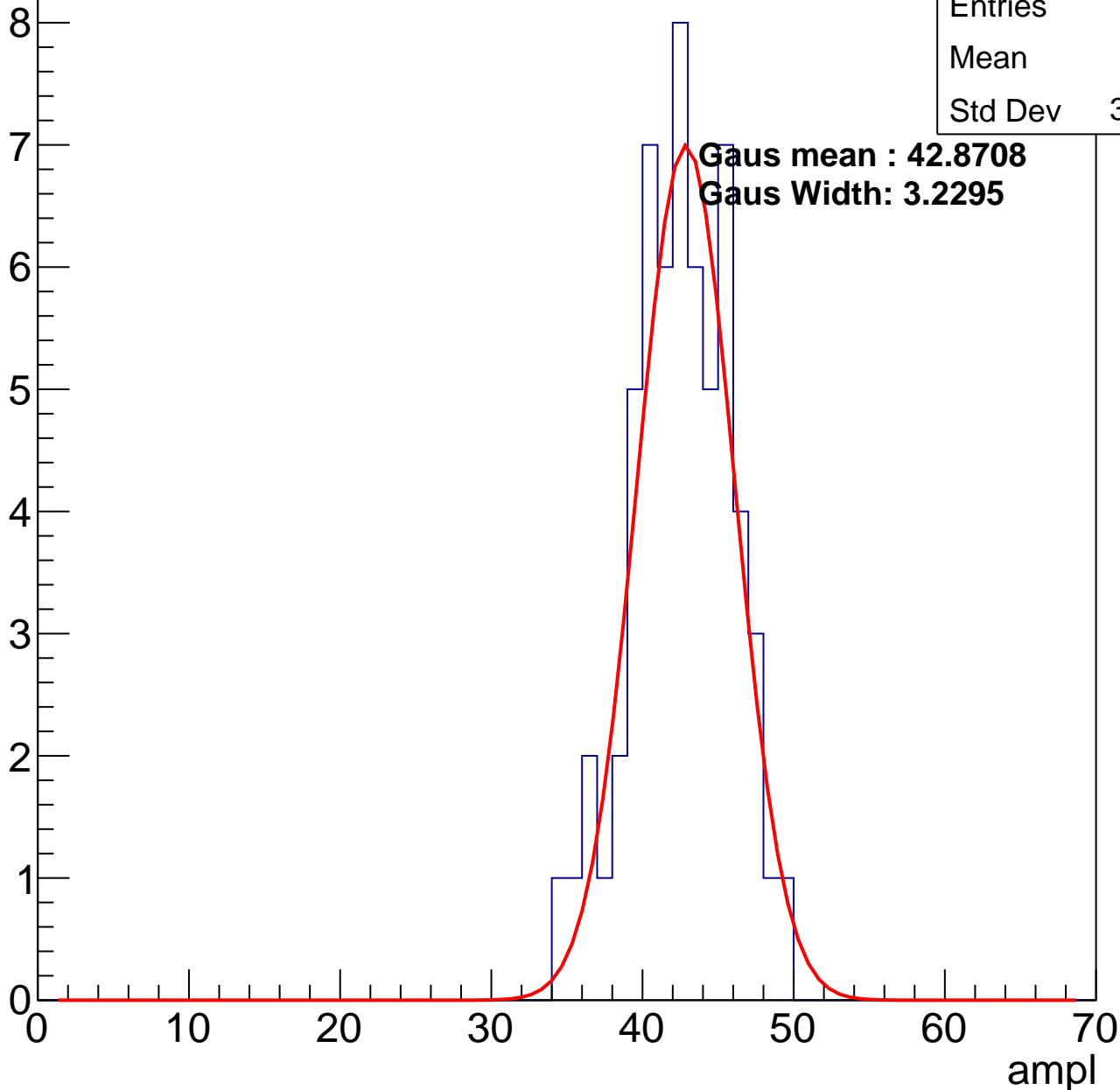
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	42.1
Std Dev	3.249

Gaus mean : 42.8708

Gaus Width: 3.2295

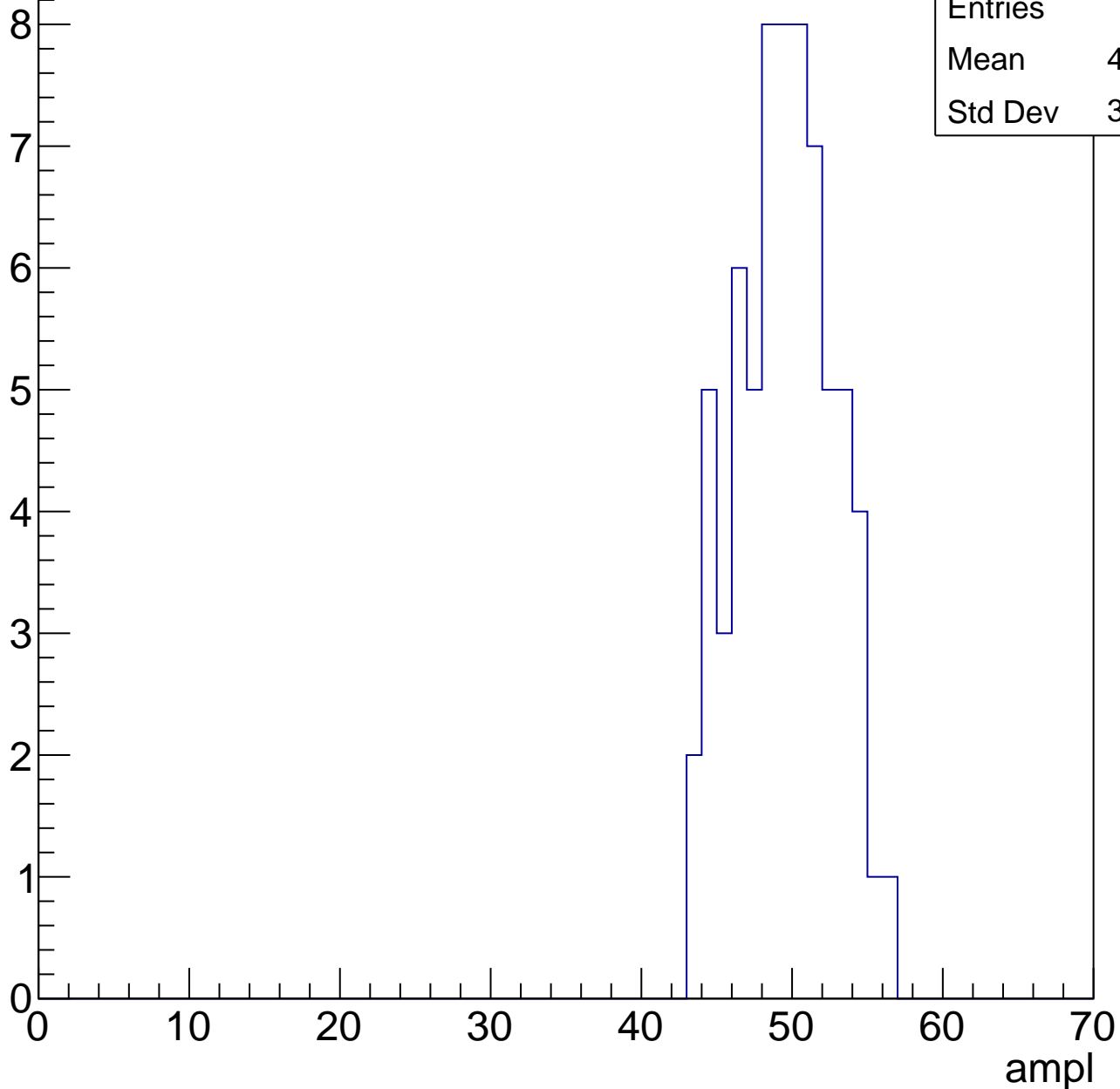


B0L002S, U17-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	49.07
Std Dev	3.145

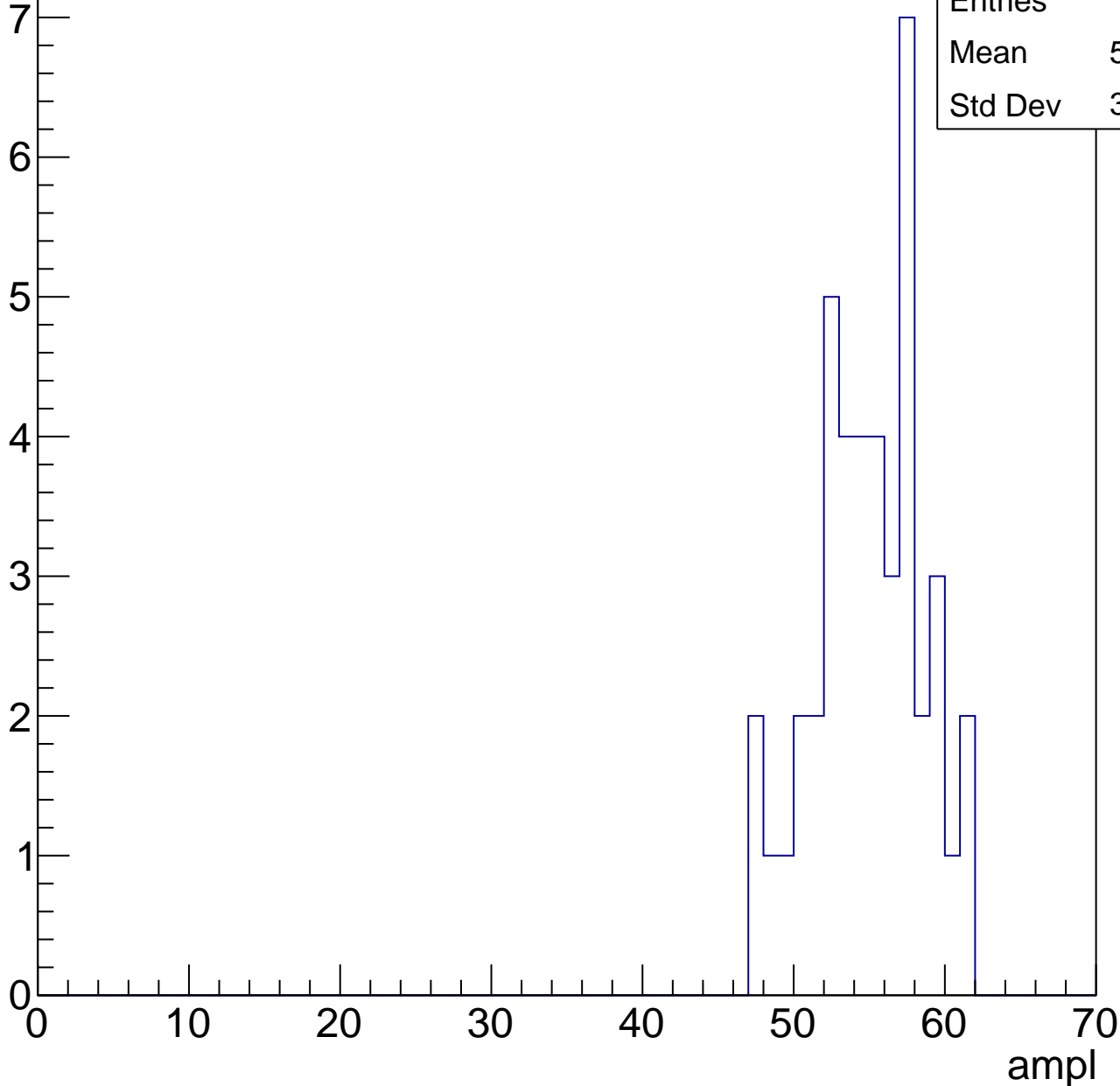


B0L002S, U17-ch29, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	54.49
Std Dev	3.553

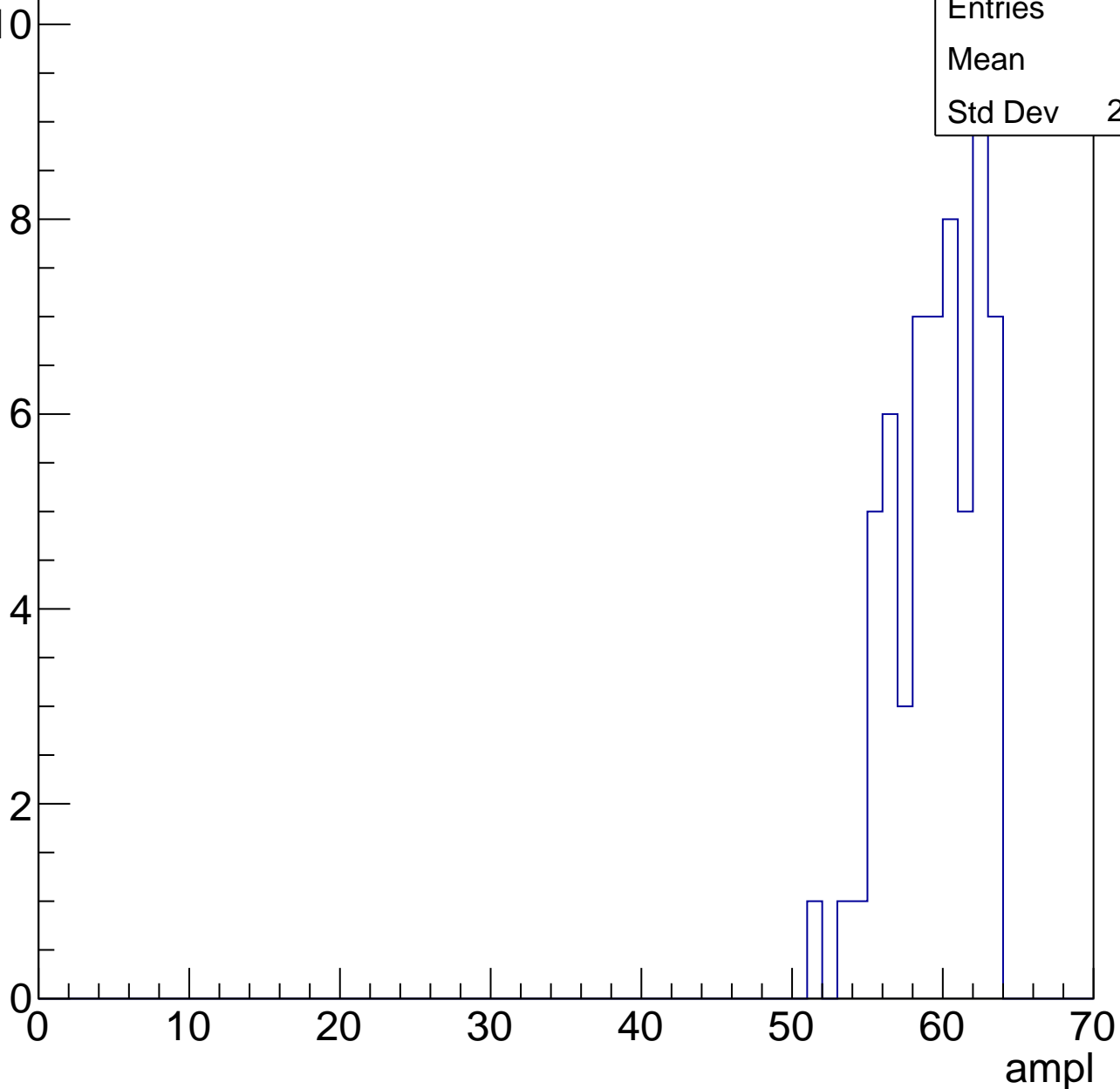


B0L002S, U17-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

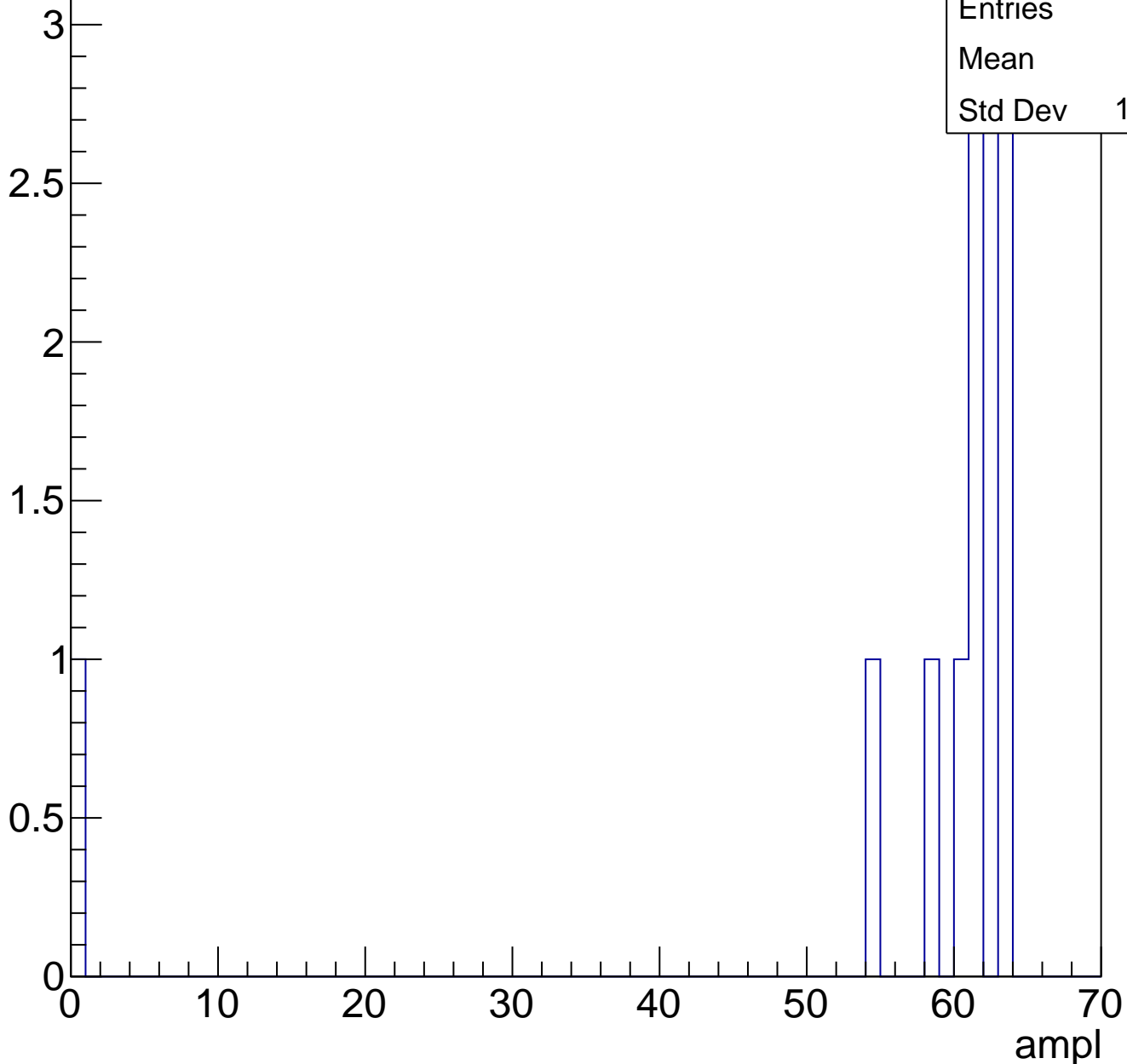
Entries	61
Mean	59.1
Std Dev	2.884



B0L002S, U17-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch30, adc0

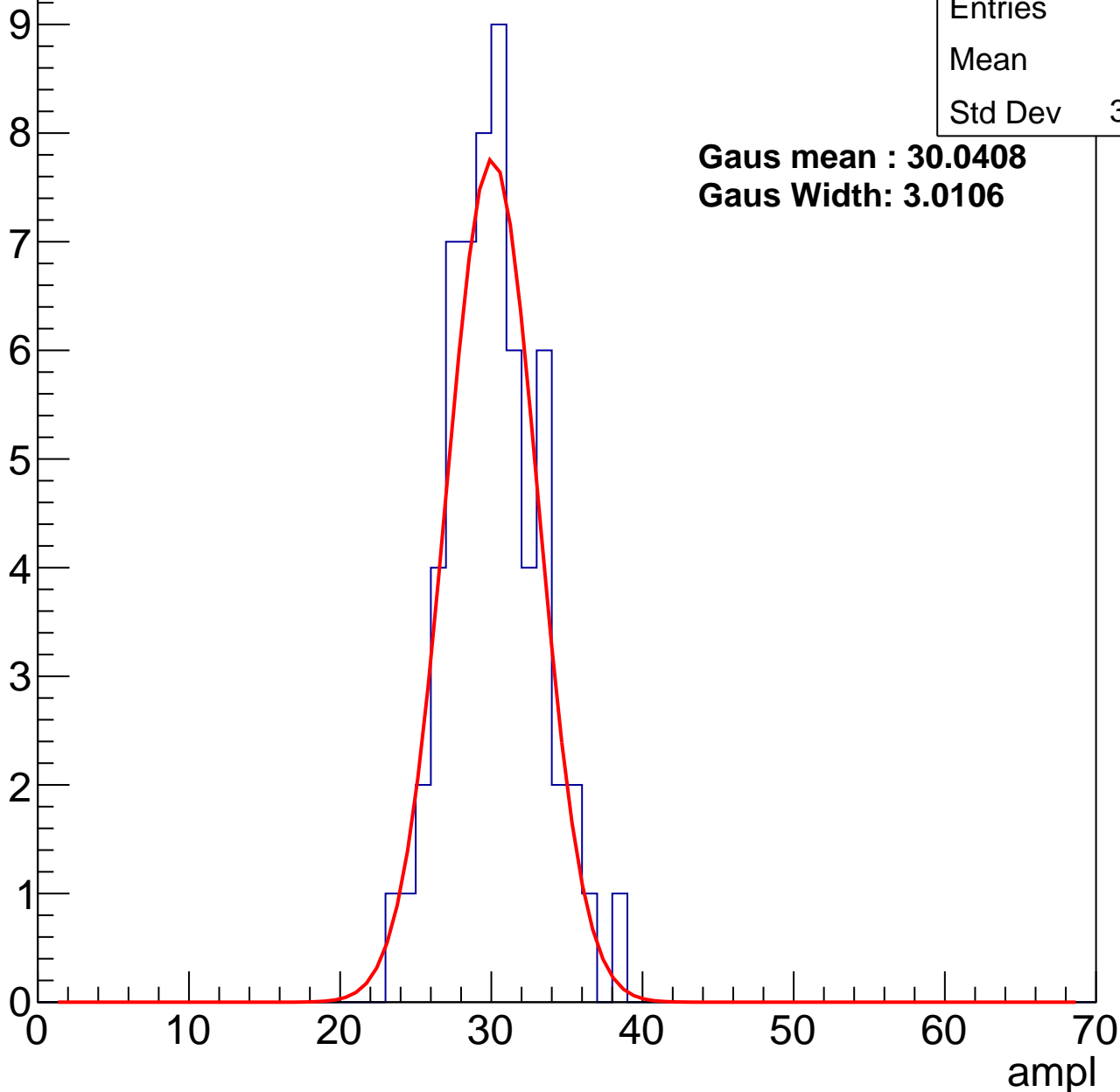
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.7
Std Dev	3.005

Gaus mean : 30.0408

Gaus Width: 3.0106



B0L002S, U17-ch30, adc1

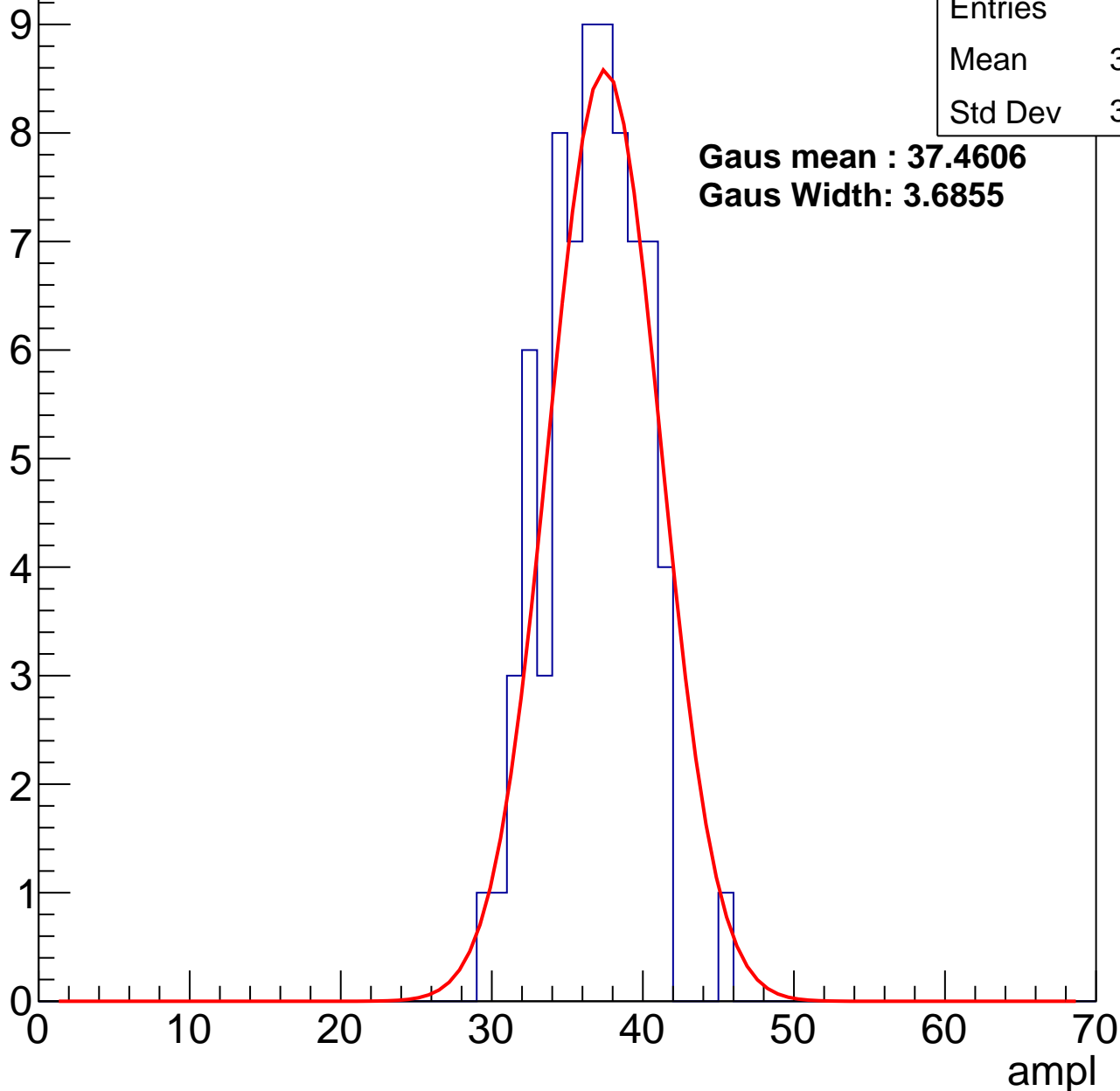
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	36.26
Std Dev	3.106

Gaus mean : 37.4606

Gaus Width: 3.6855



B0L002S, U17-ch30, adc2

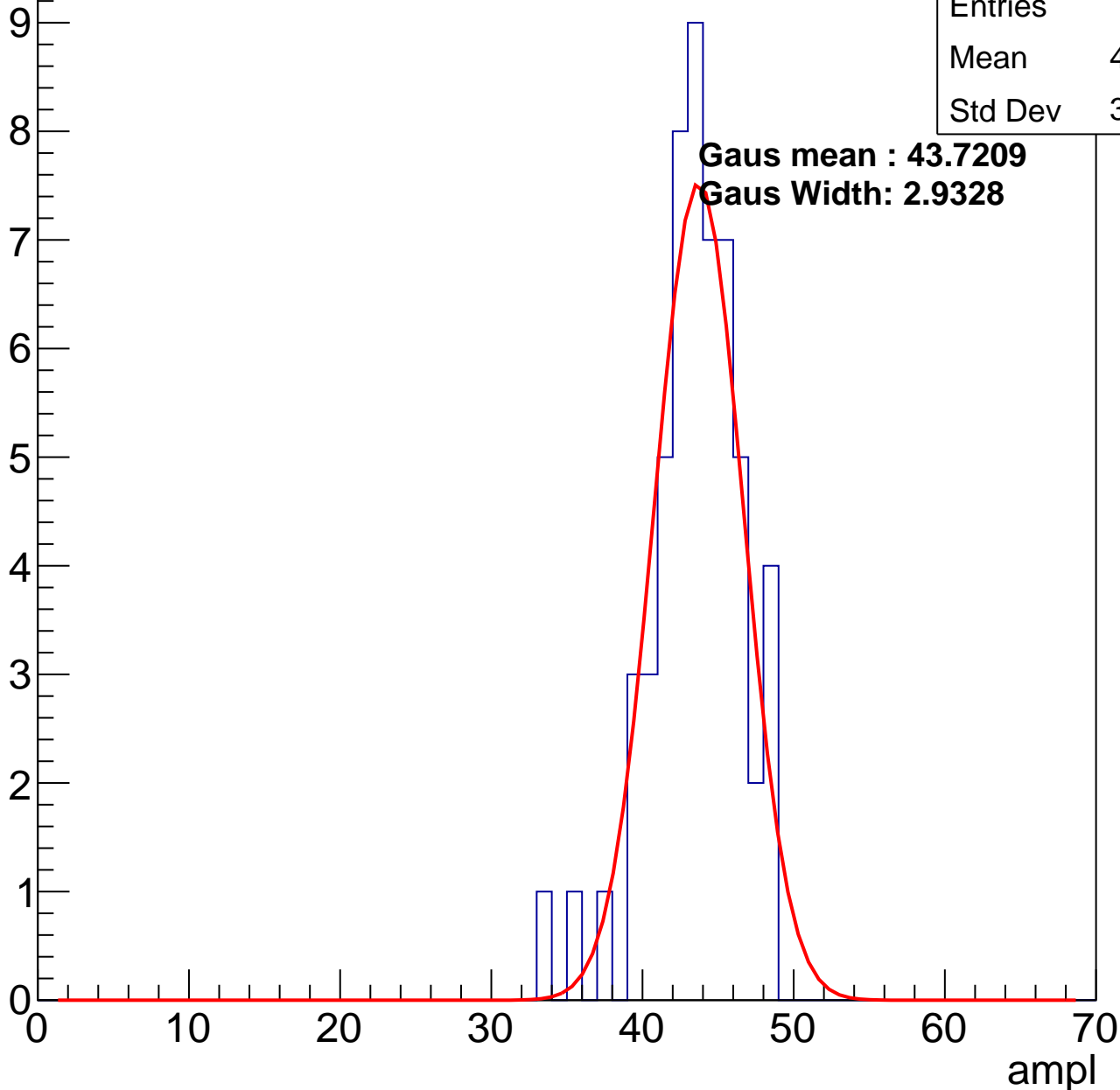
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.02
Std Dev	3.033

Gaus mean : 43.7209

Gaus Width: 2.9328

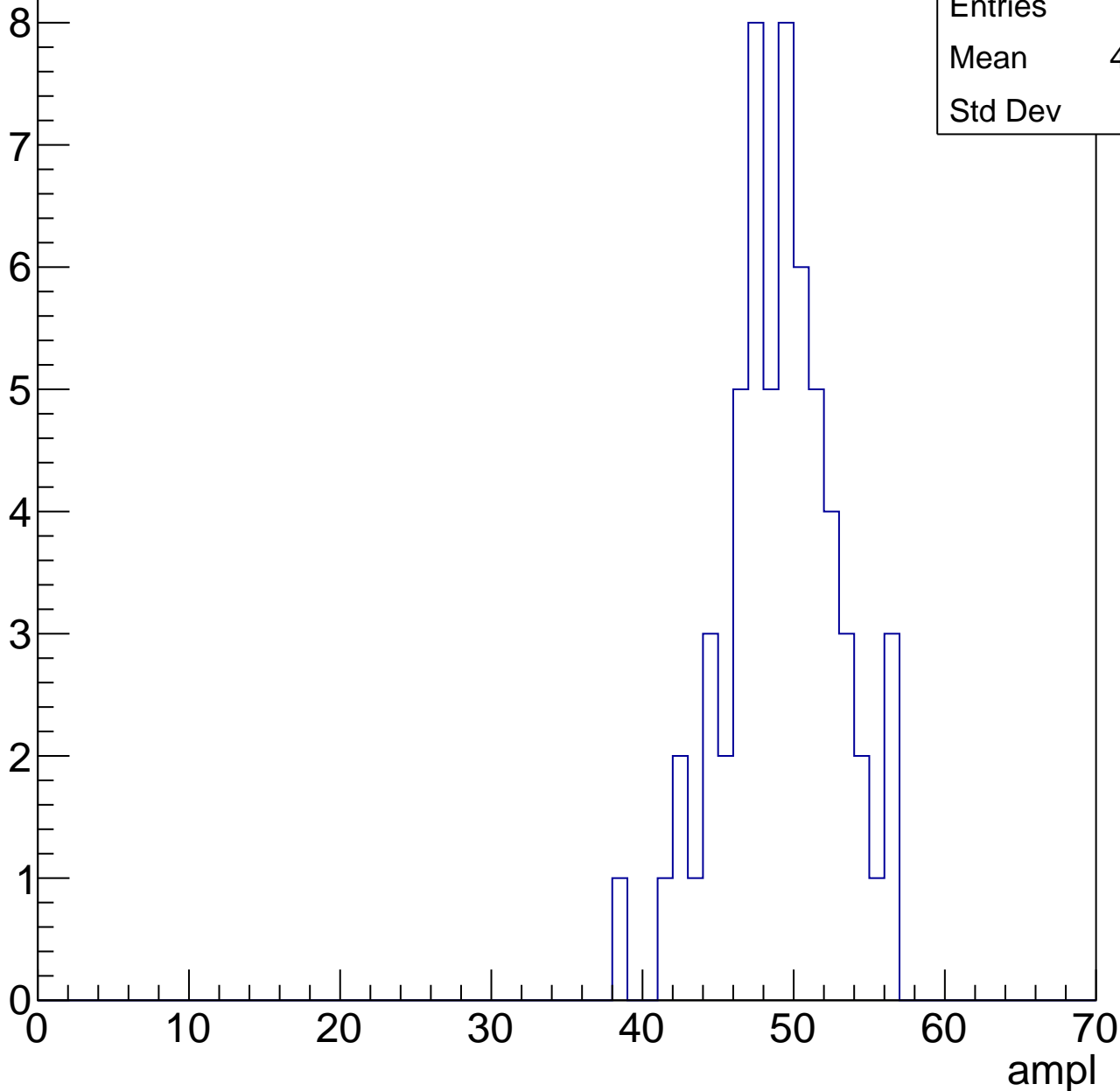


B0L002S, U17-ch30, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	48.65
Std Dev	3.75

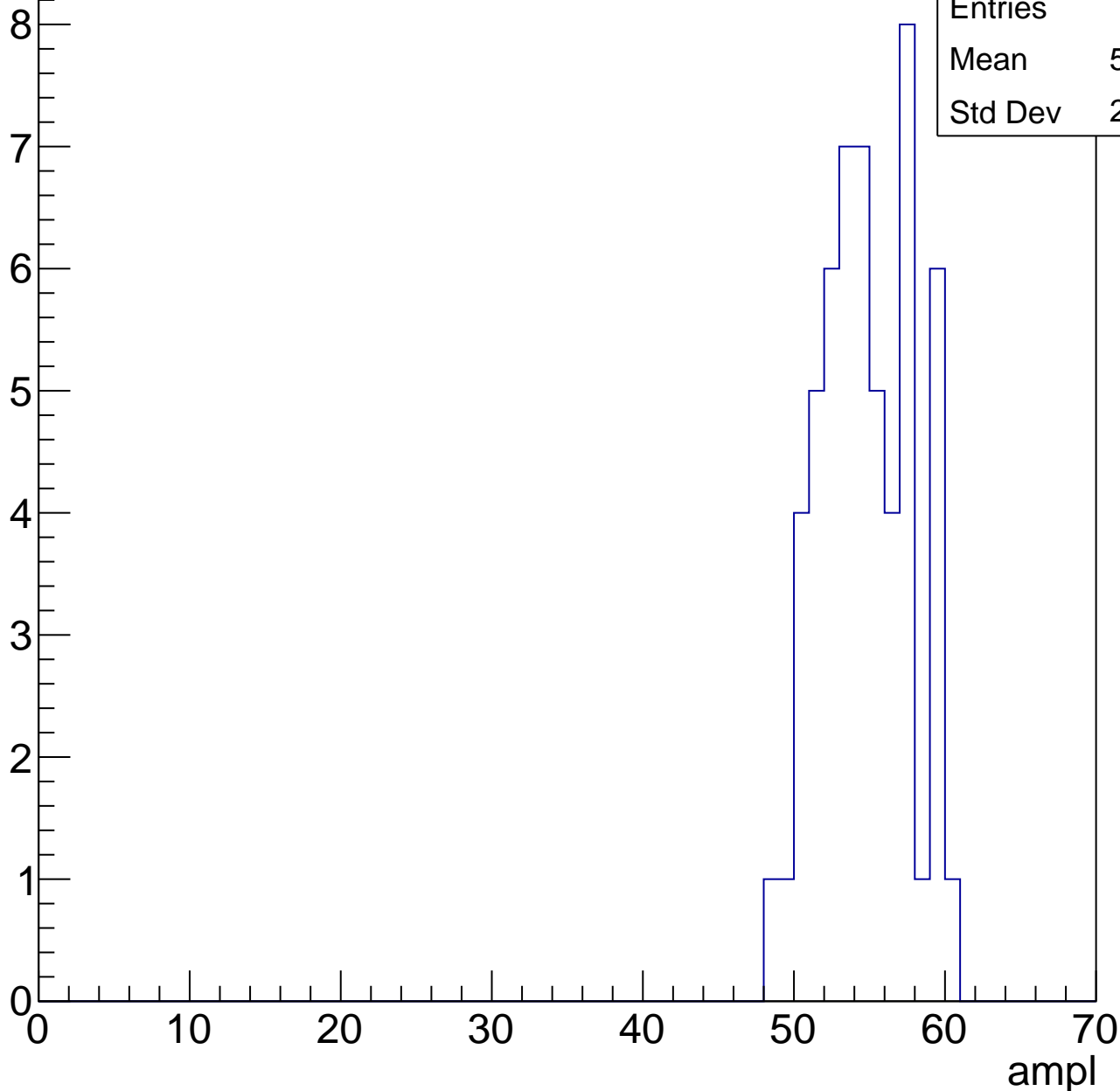


B0L002S, U17-ch30, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

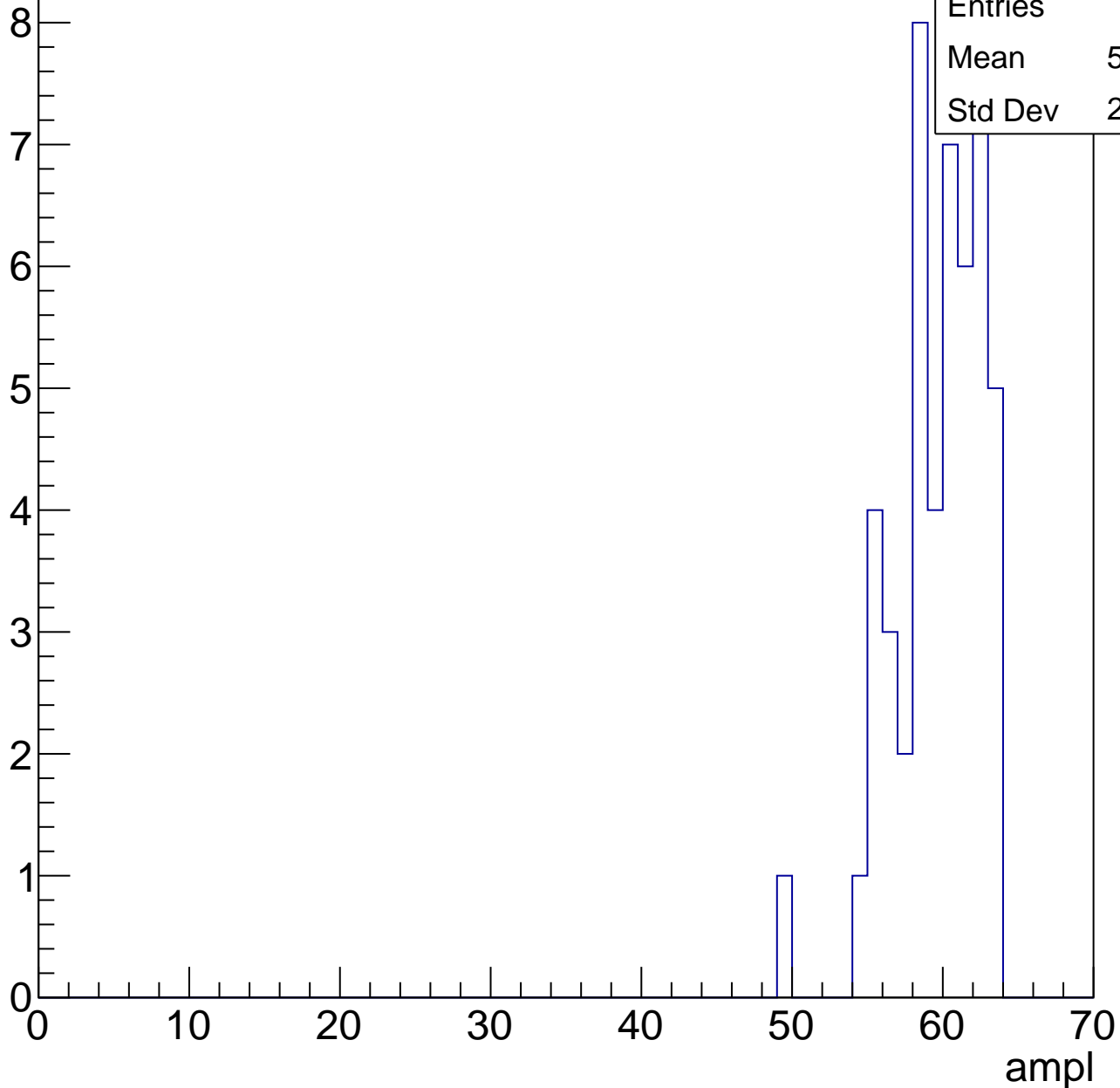
Entries	56
Mean	54.29
Std Dev	2.962



B0L002S, U17-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

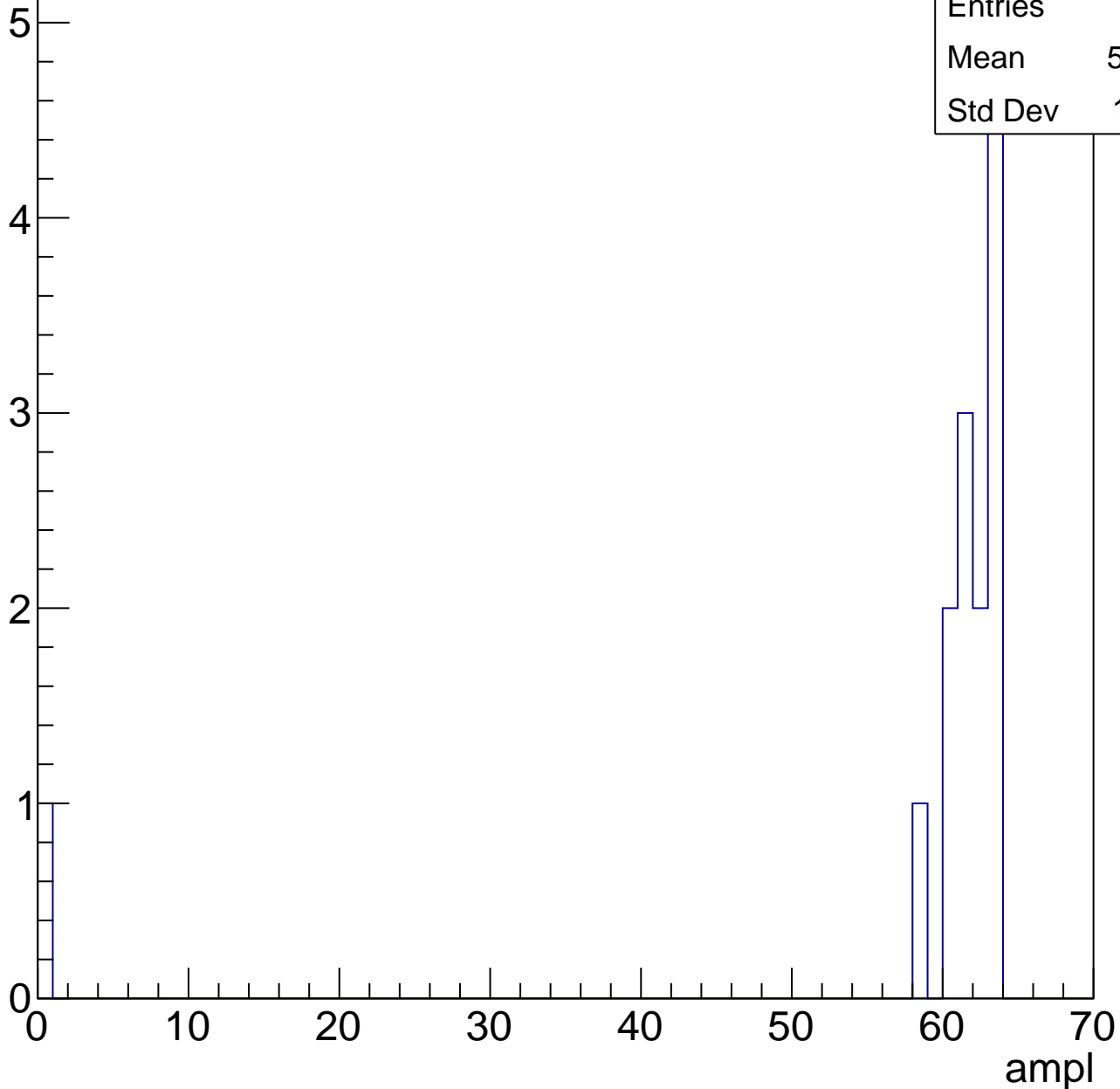


B0L002S, U17-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	14
Mean	57.14
Std Dev	15.91



B0L002S, U17-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch31, adc0

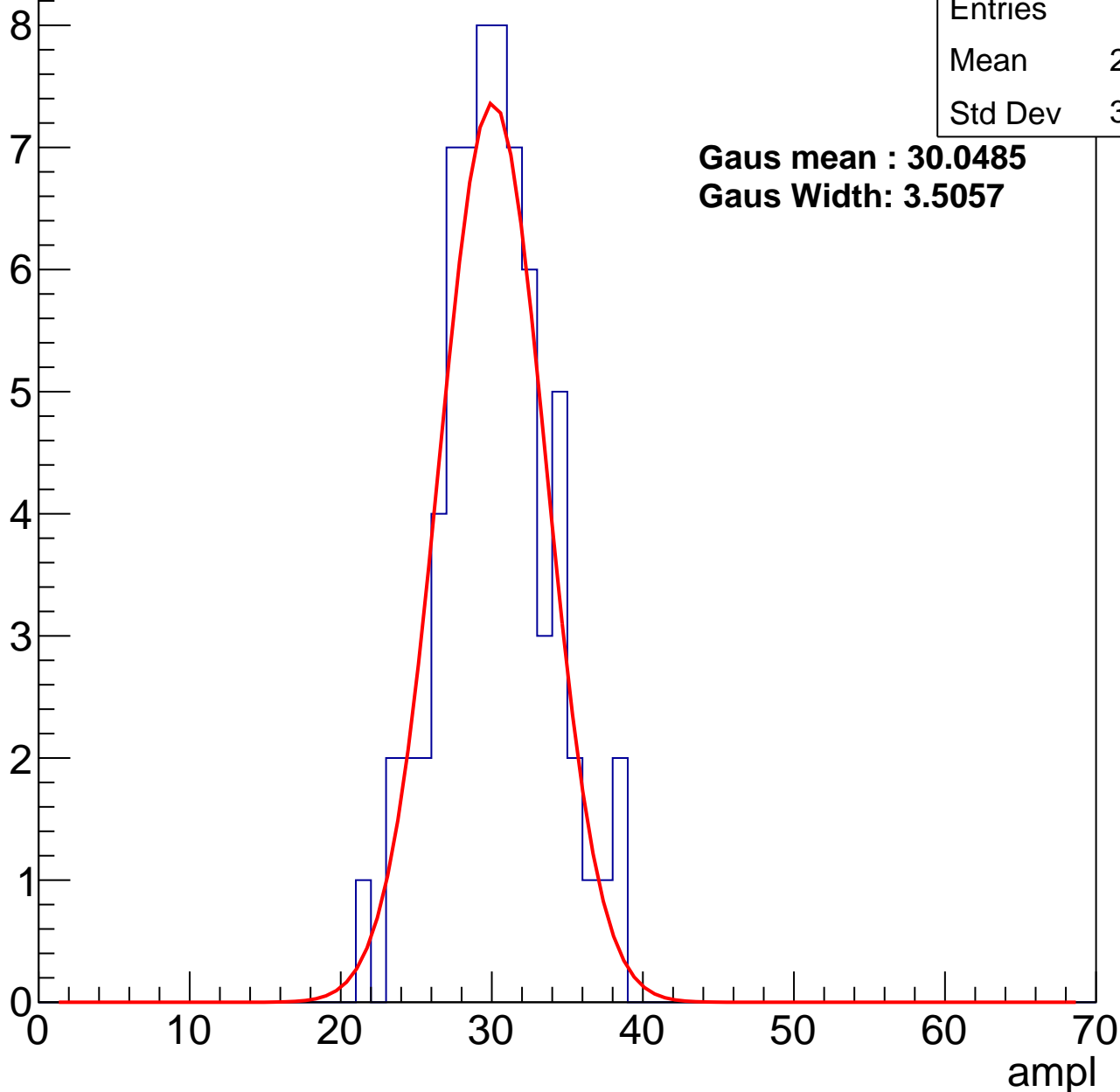
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.75
Std Dev	3.558

Gaus mean : 30.0485

Gaus Width: 3.5057



B0L002S, U17-ch31, adc1

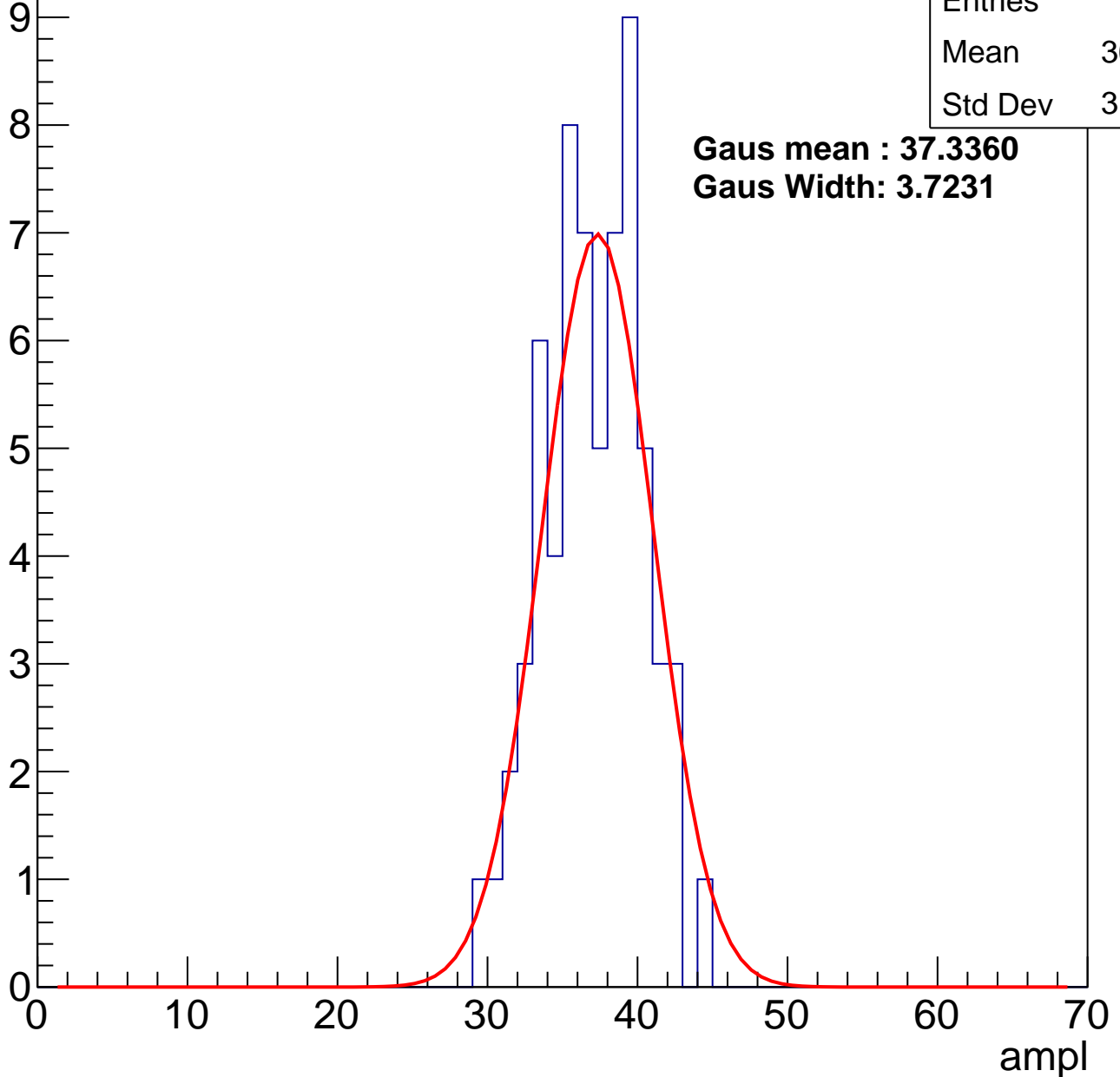
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.58
Std Dev	3.234

Gaus mean : 37.3360

Gaus Width: 3.7231



B0L002S, U17-ch31, adc2

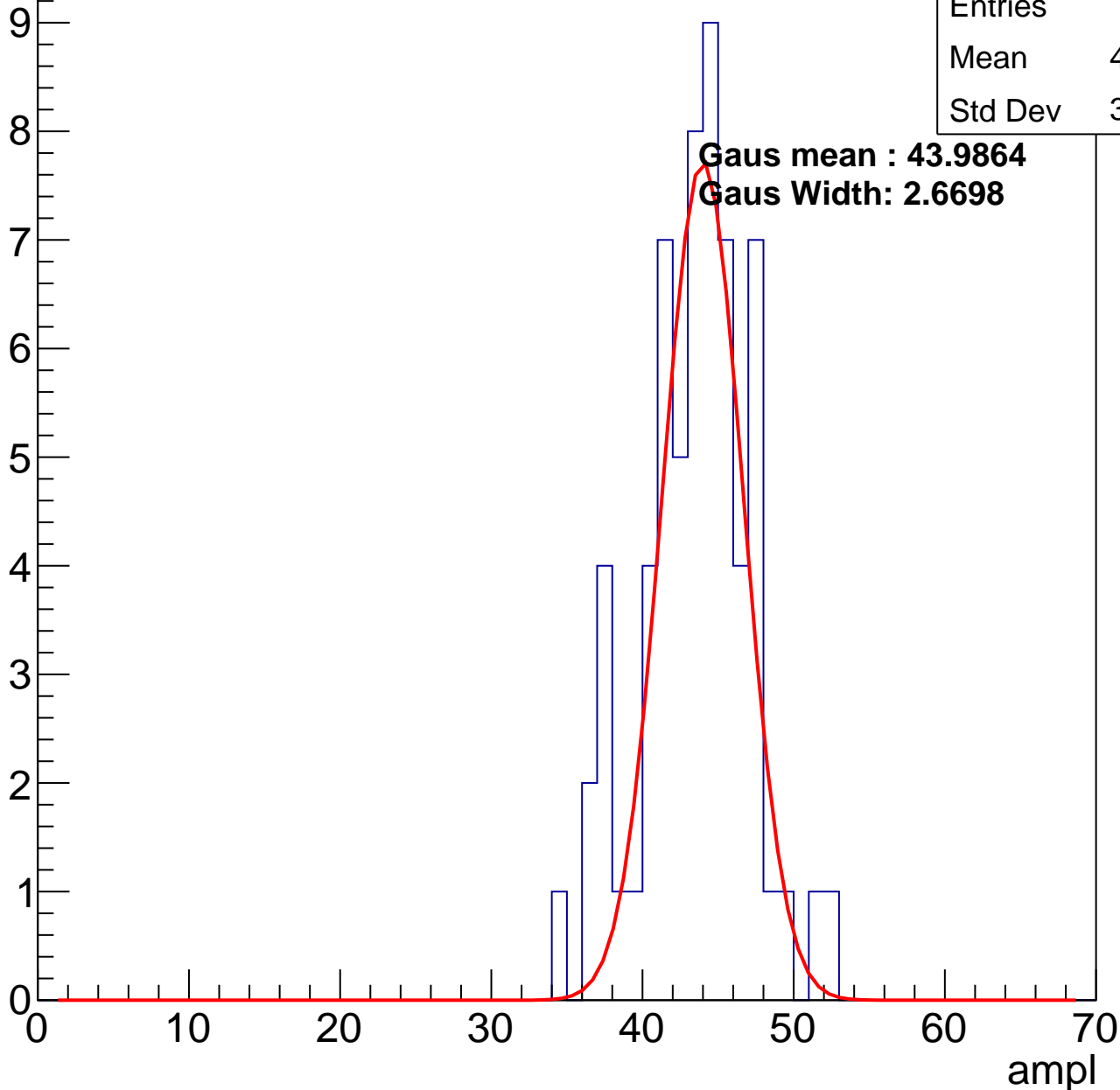
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.06
Std Dev	3.588

Gaus mean : 43.9864

Gaus Width: 2.6698

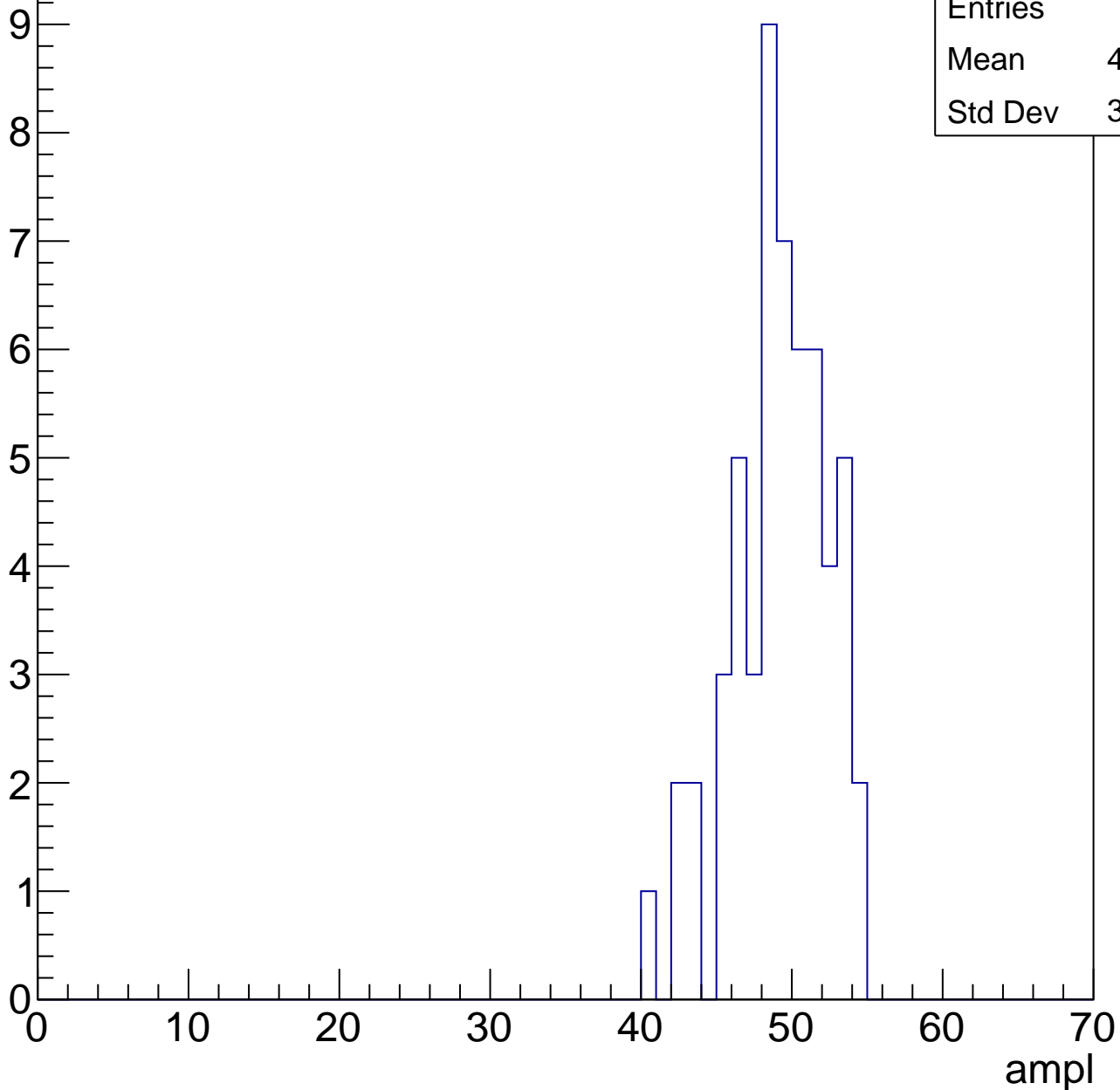


B0L002S, U17-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	48.69
Std Dev	3.179

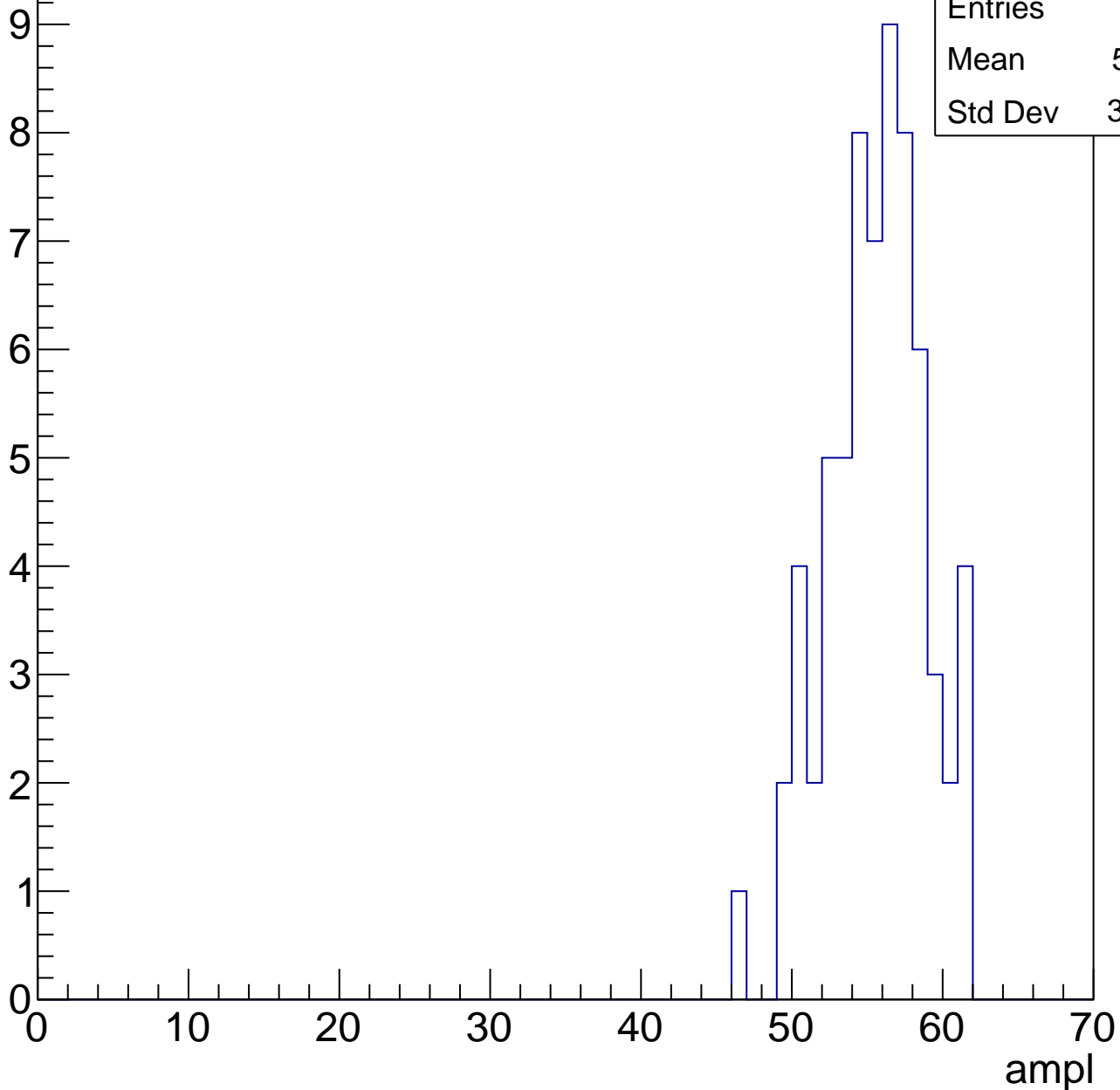


B0L002S, U17-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	55.11
Std Dev	3.243

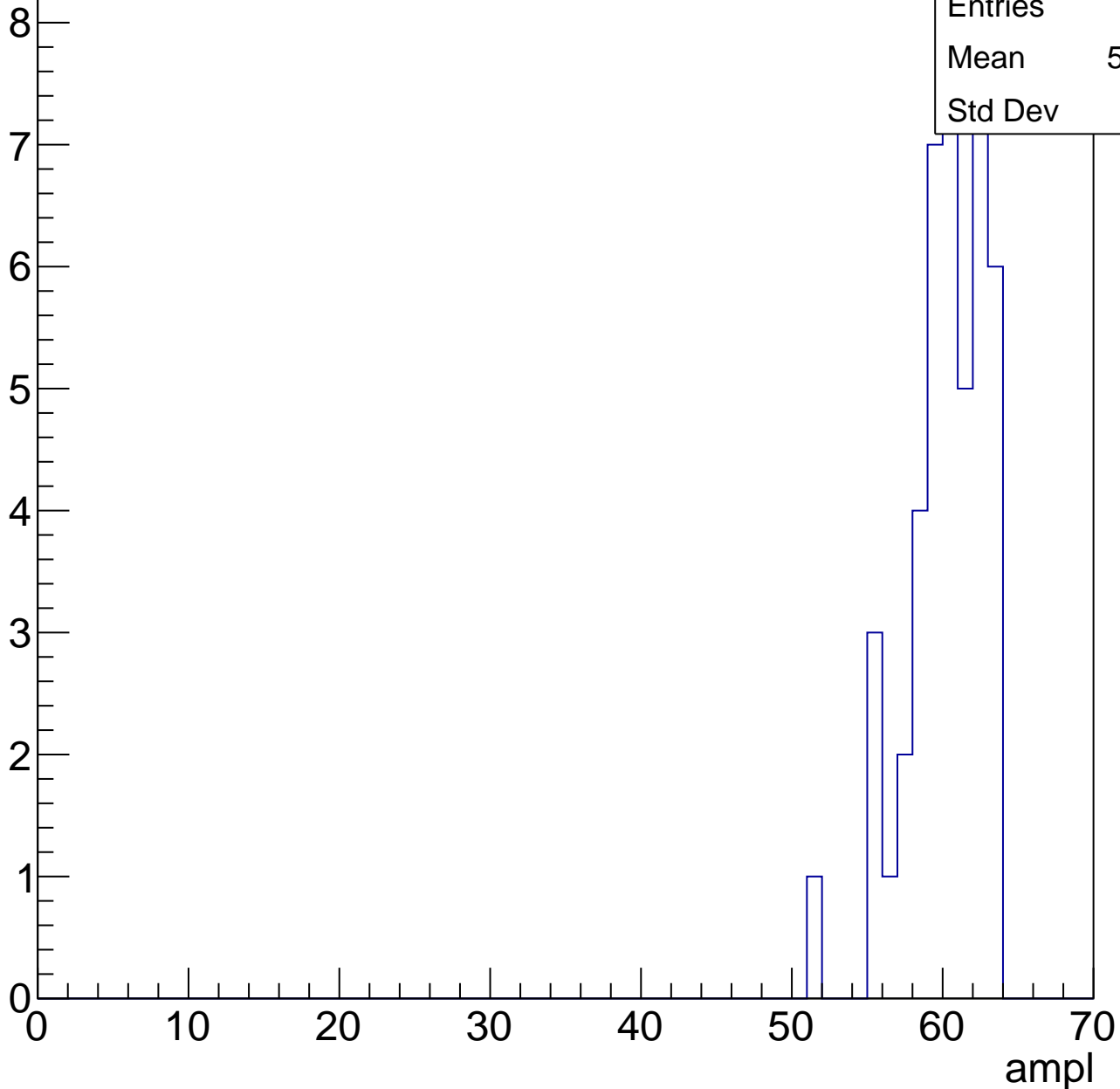


B0L002S, U17-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.78
Std Dev	2.59



B0L002S, U17-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch32, adc0

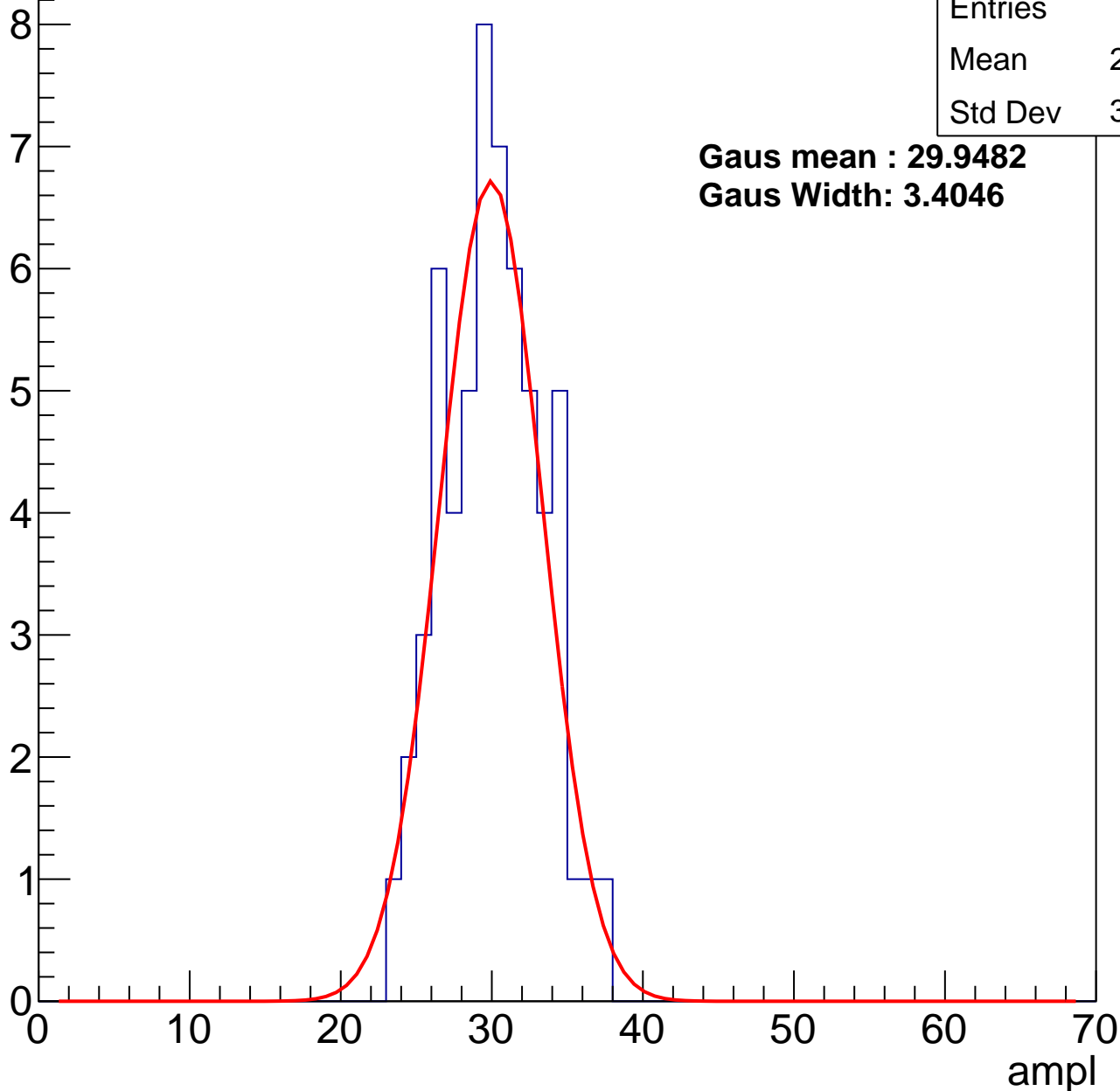
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.63
Std Dev	3.188

Gaus mean : 29.9482

Gaus Width: 3.4046



B0L002S, U17-ch32, adc1

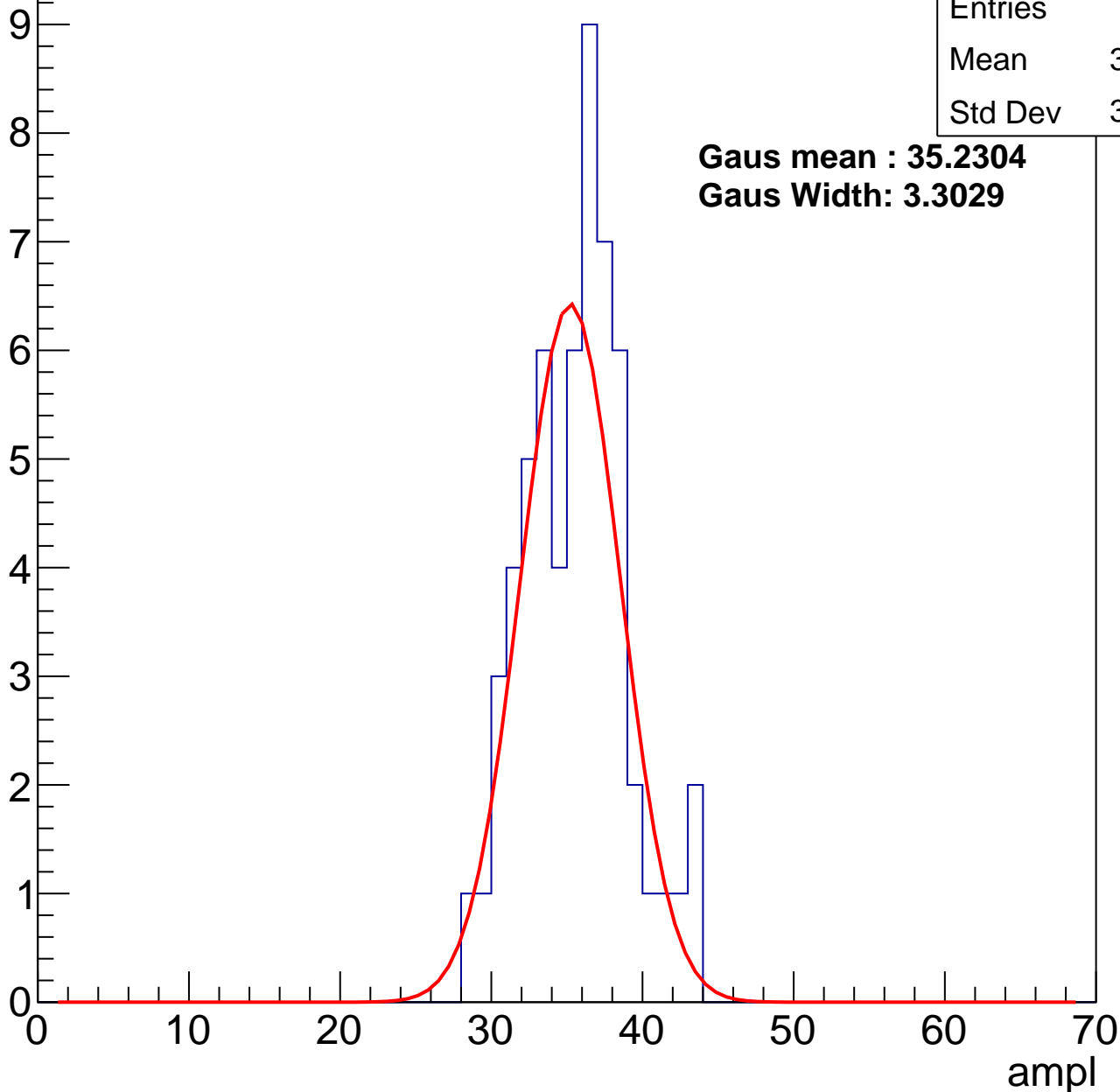
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	35.14
Std Dev	3.337

Gaus mean : 35.2304

Gaus Width: 3.3029



B0L002S, U17-ch32, adc2

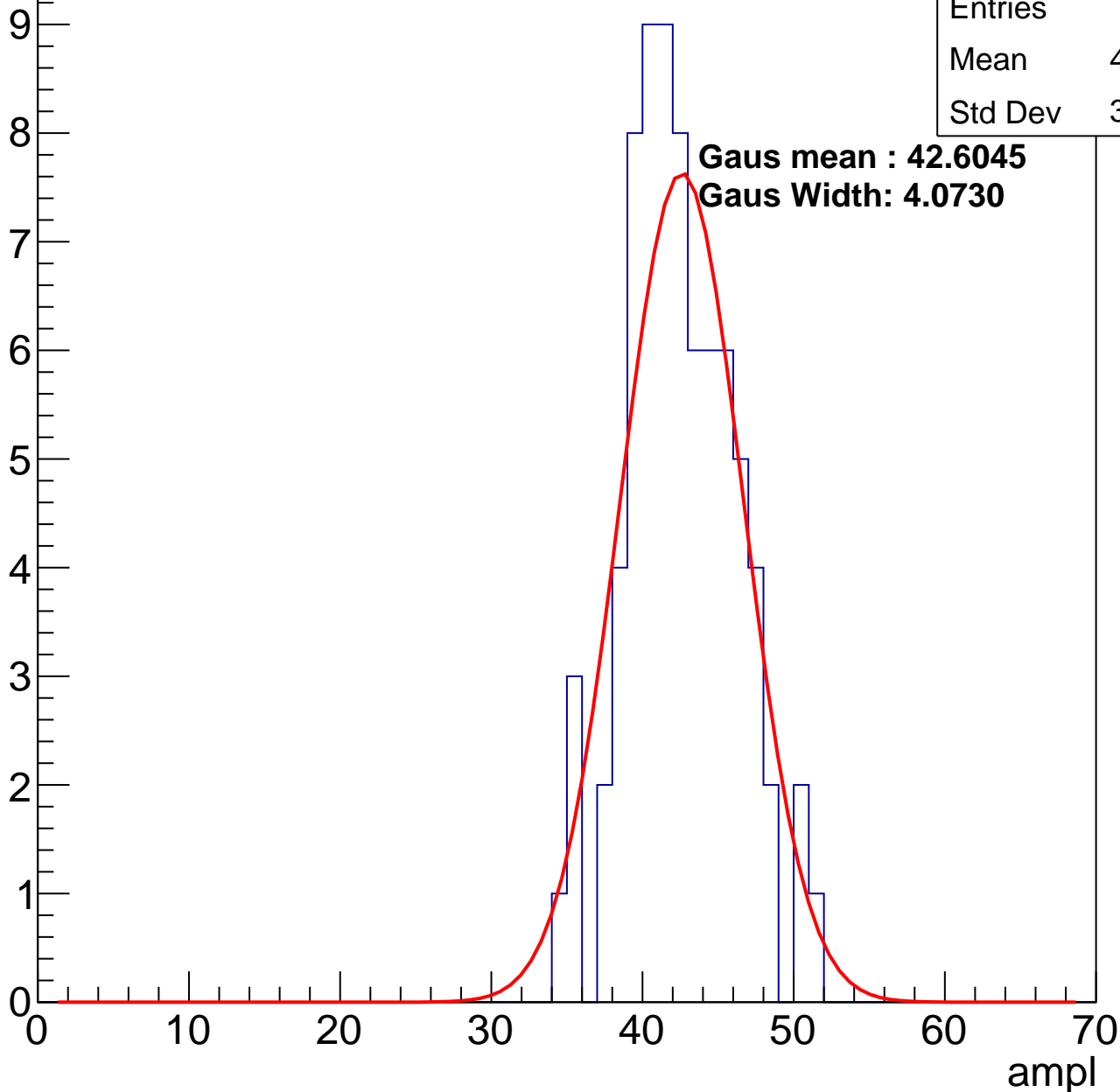
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	42.09
Std Dev	3.603

Gaus mean : 42.6045

Gaus Width: 4.0730

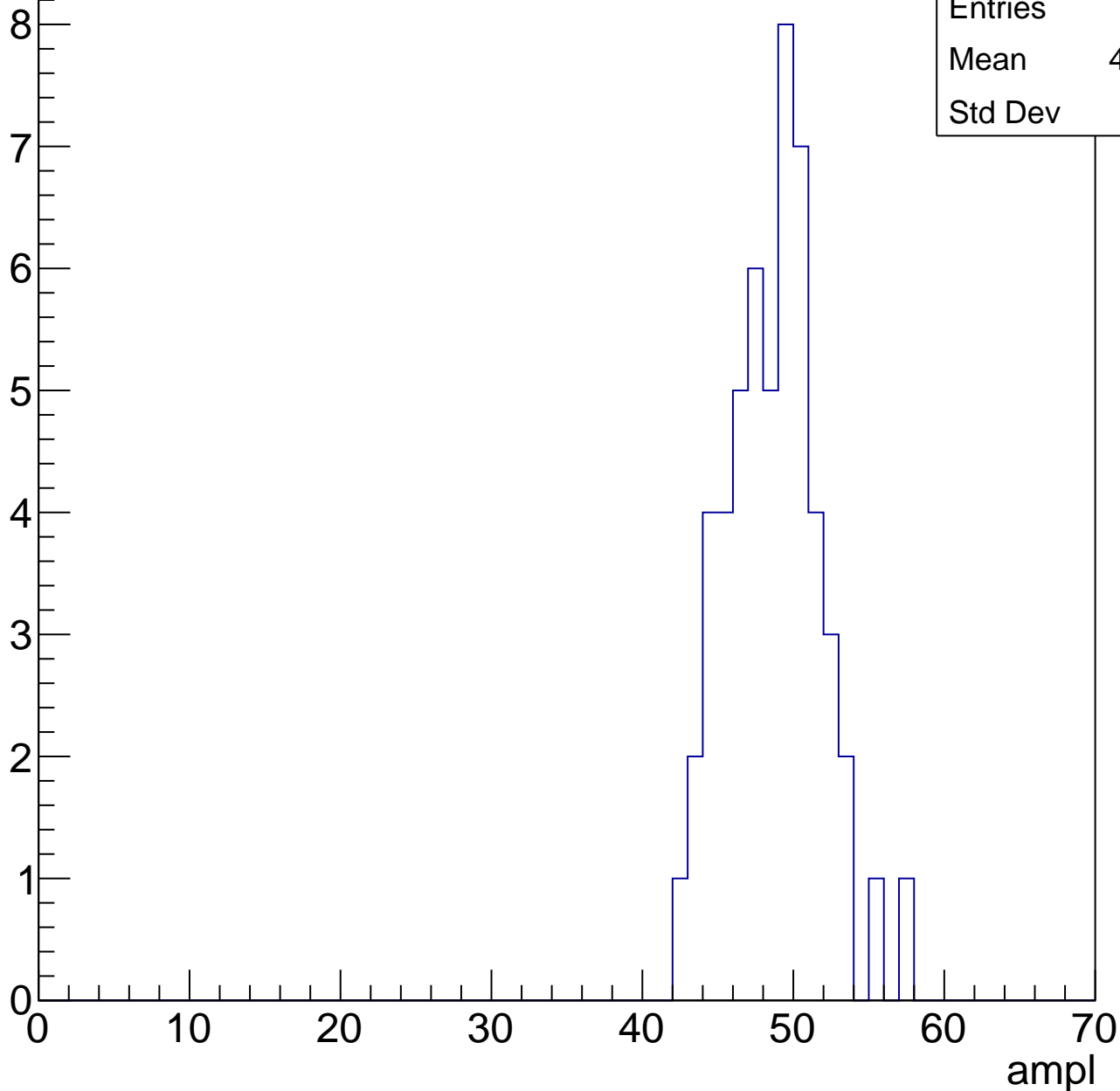


B0L002S, U17-ch32, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	48.23
Std Dev	3.1

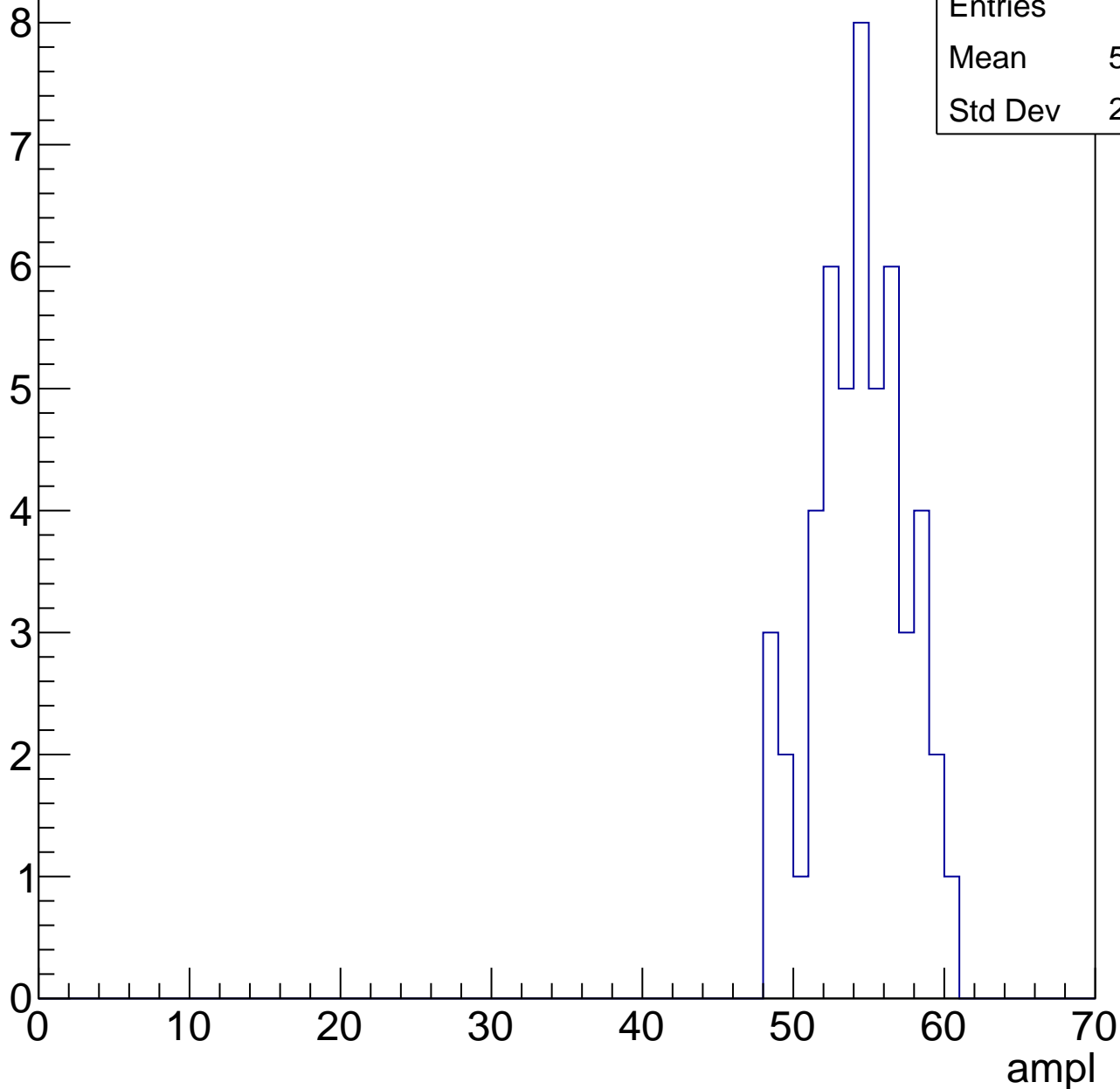


B0L002S, U17-ch32, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	53.94
Std Dev	2.983

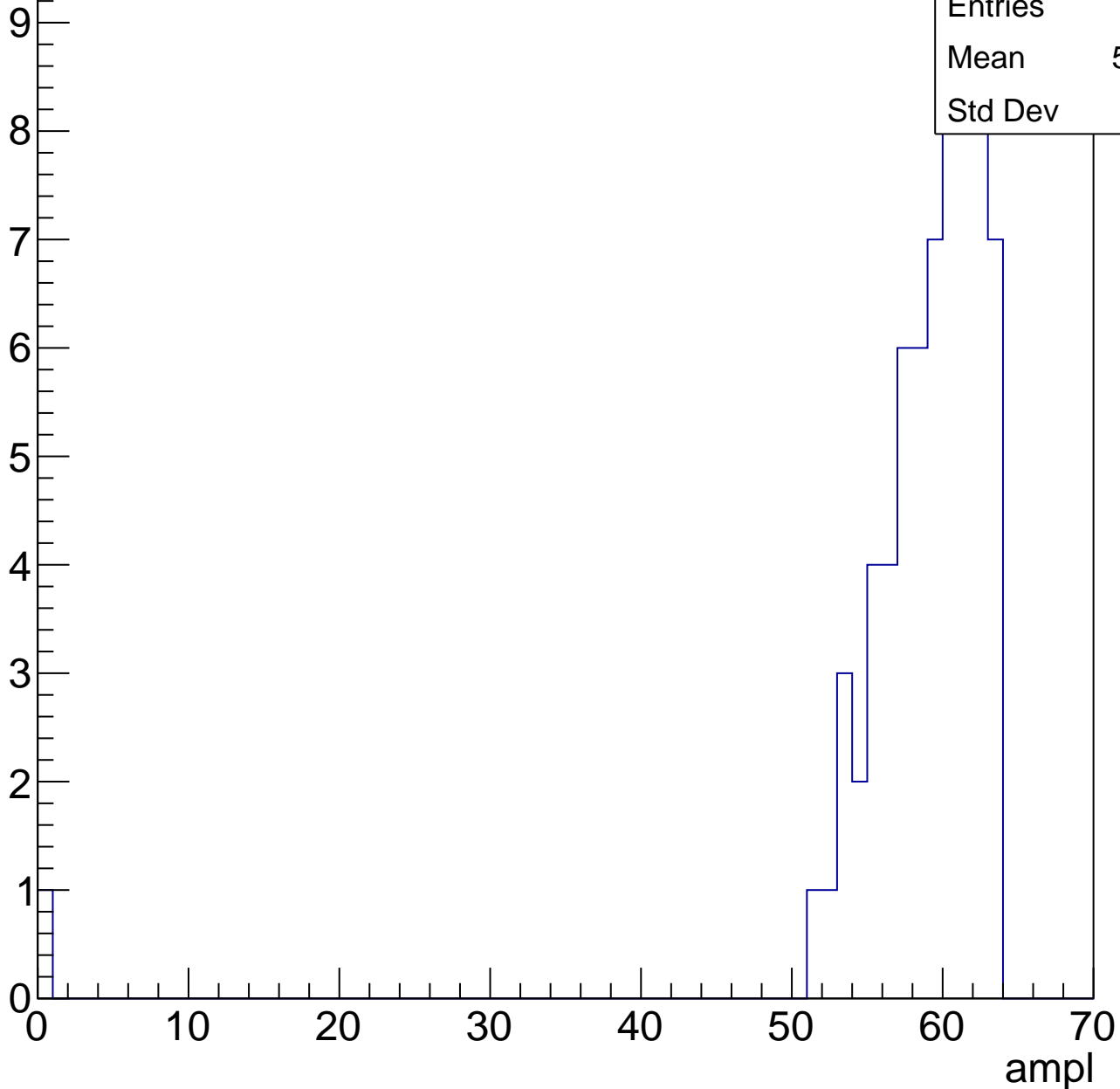


B0L002S, U17-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

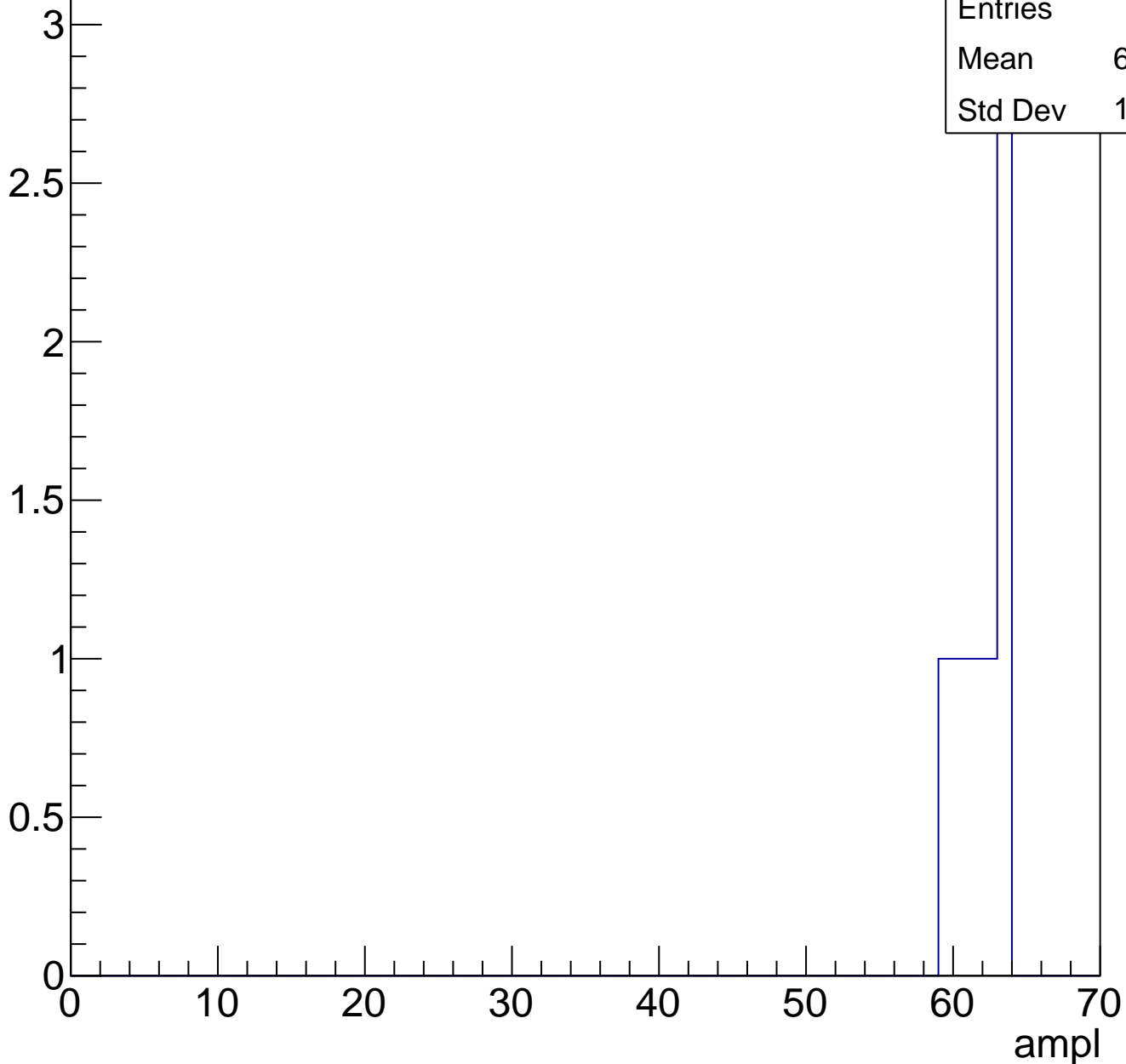
Entries	68
Mean	58.01
Std Dev	7.72



B0L002S, U17-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch33, adc0

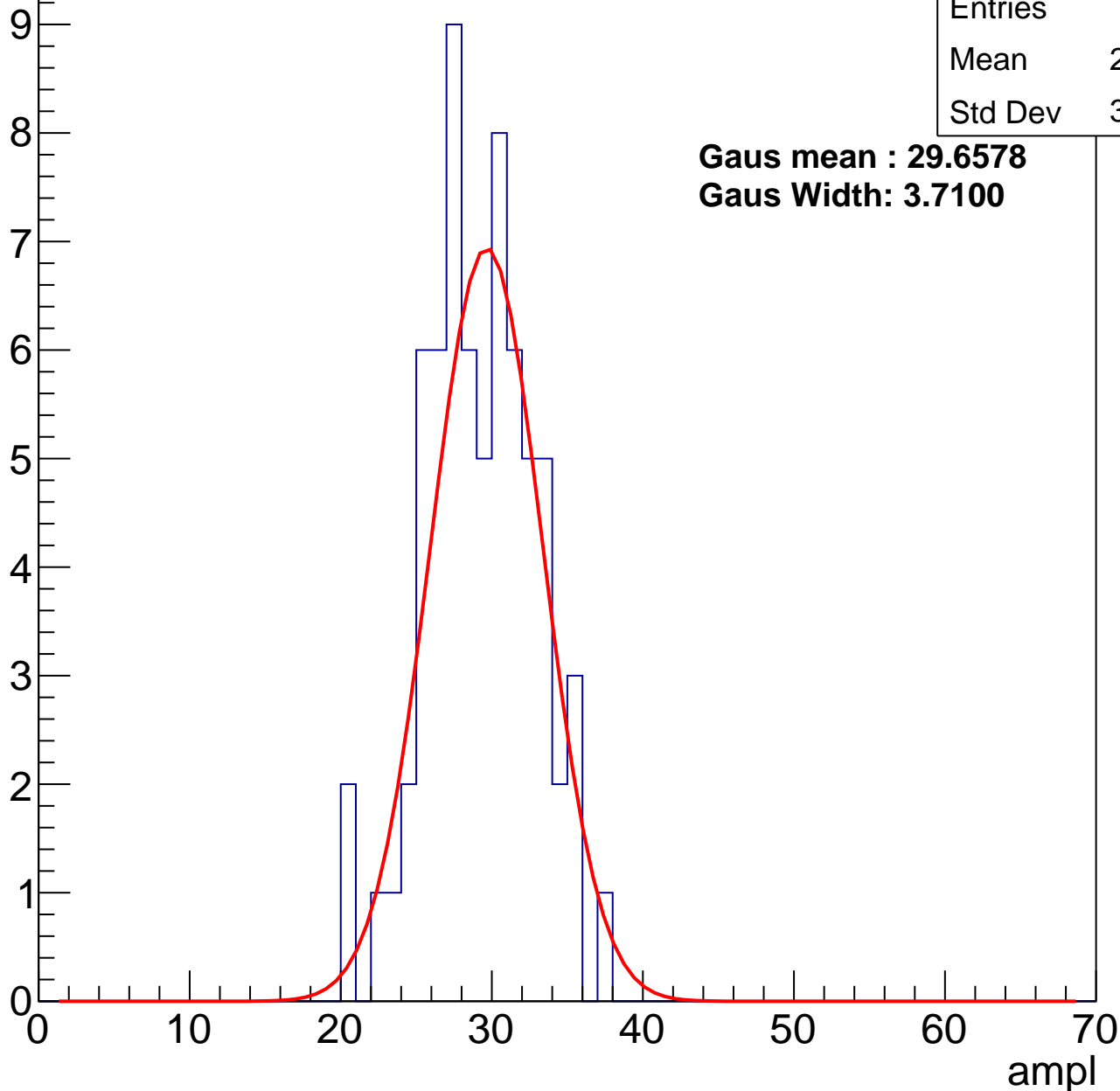
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.76
Std Dev	3.565

Gaus mean : 29.6578

Gaus Width: 3.7100



B0L002S, U17-ch33, adc1

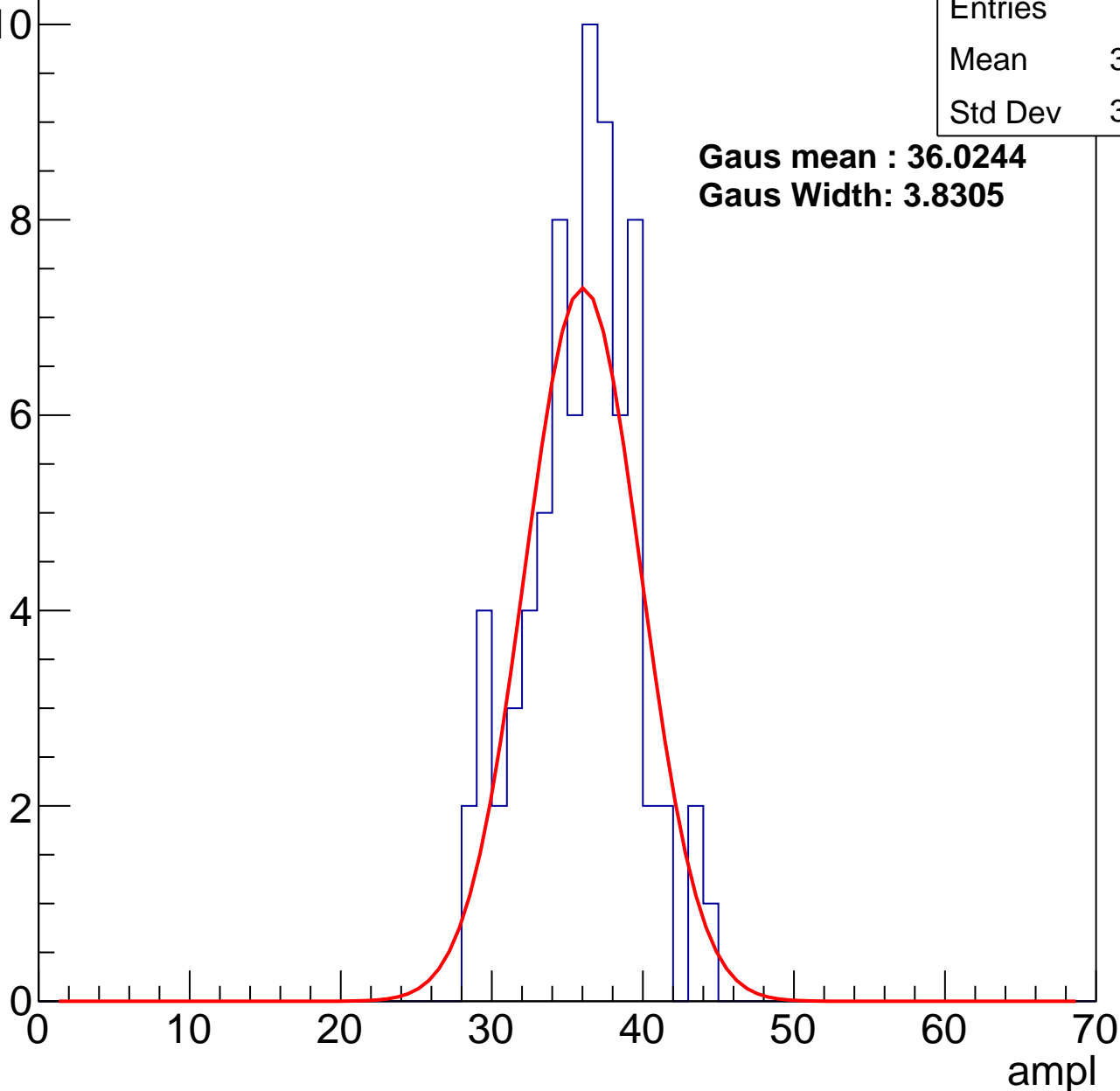
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	35.47
Std Dev	3.576

Gaus mean : 36.0244

Gaus Width: 3.8305



B0L002S, U17-ch33, adc2

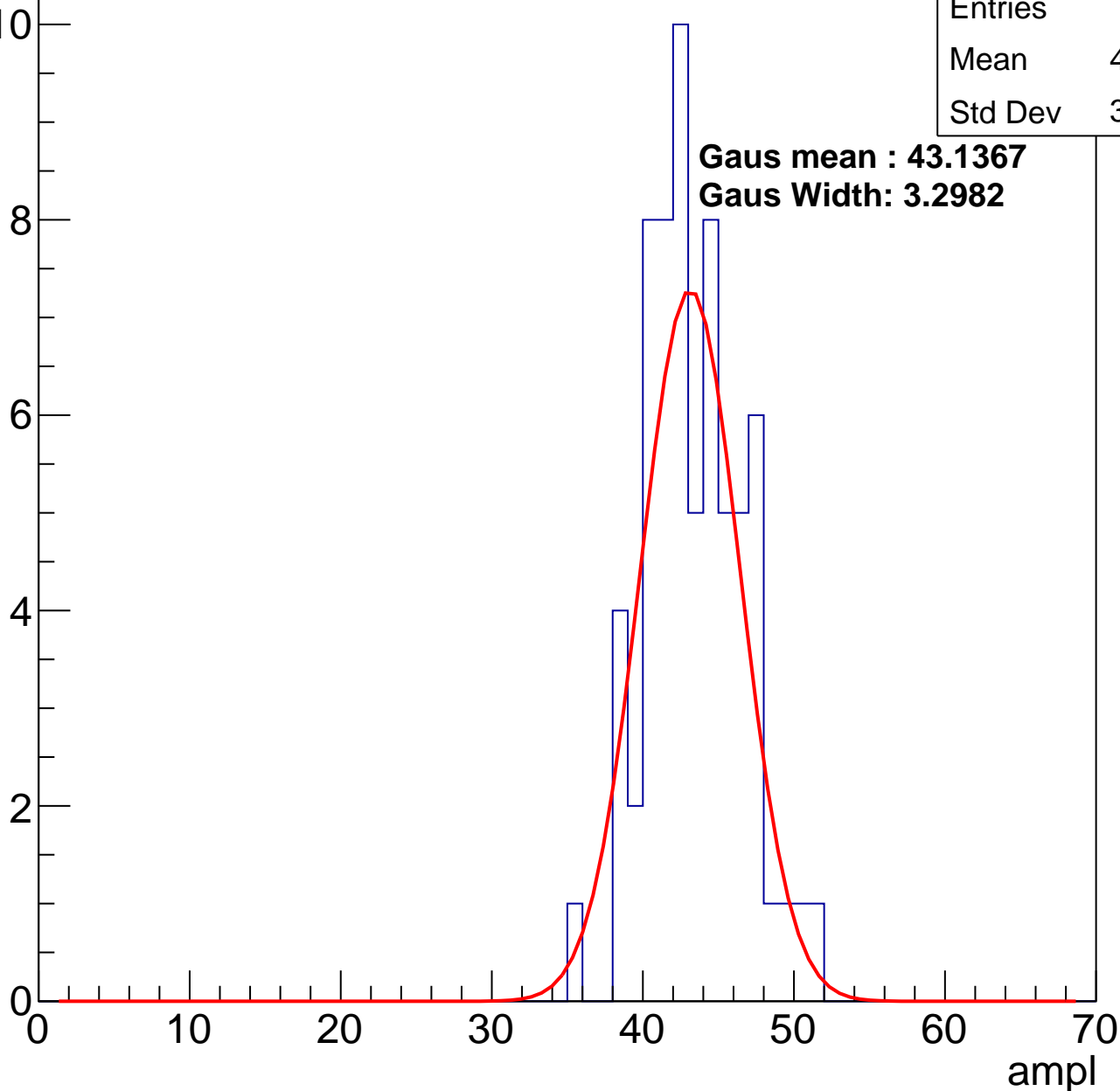
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.95
Std Dev	3.145

Gaus mean : 43.1367

Gaus Width: 3.2982

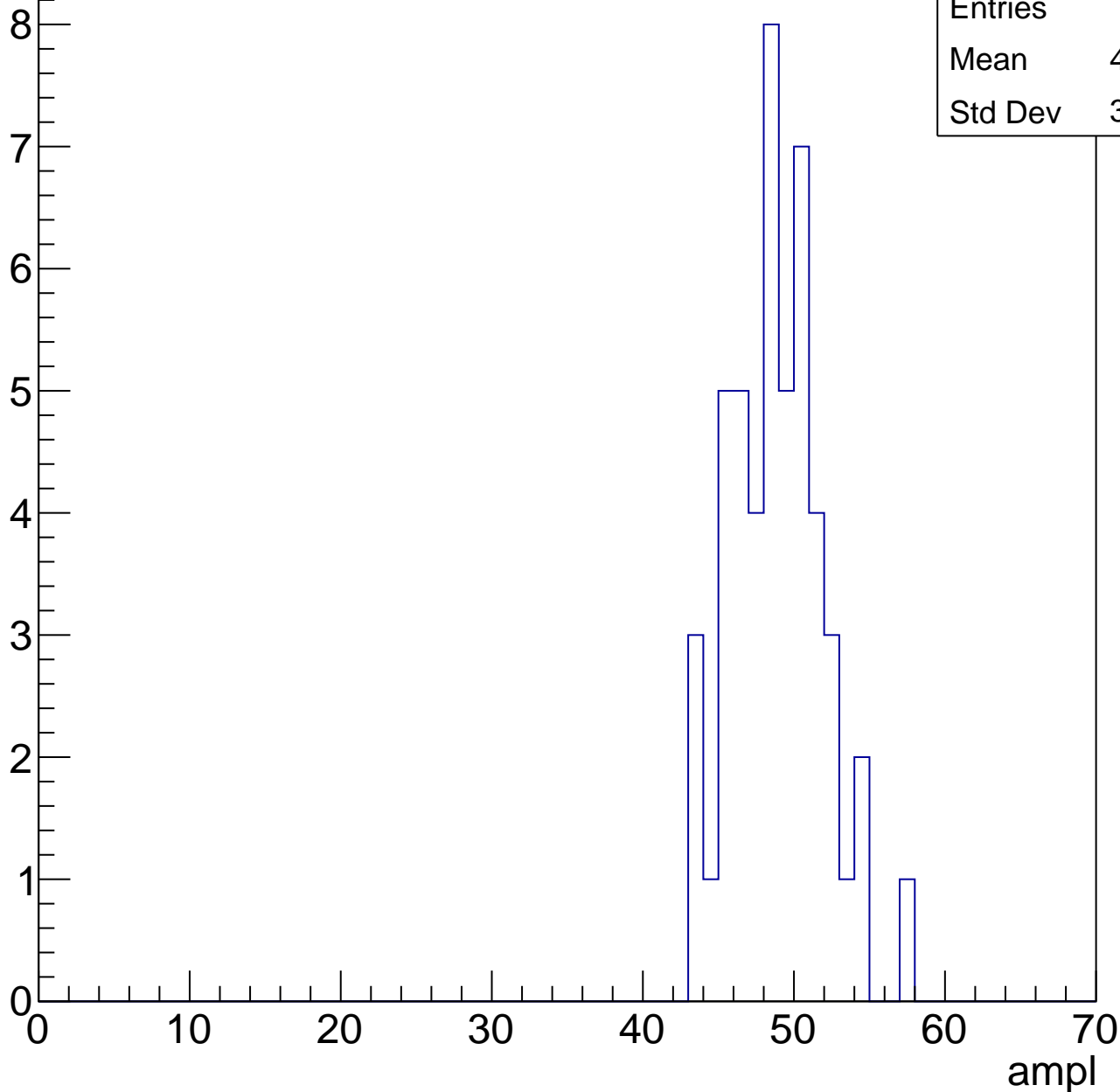


B0L002S, U17-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

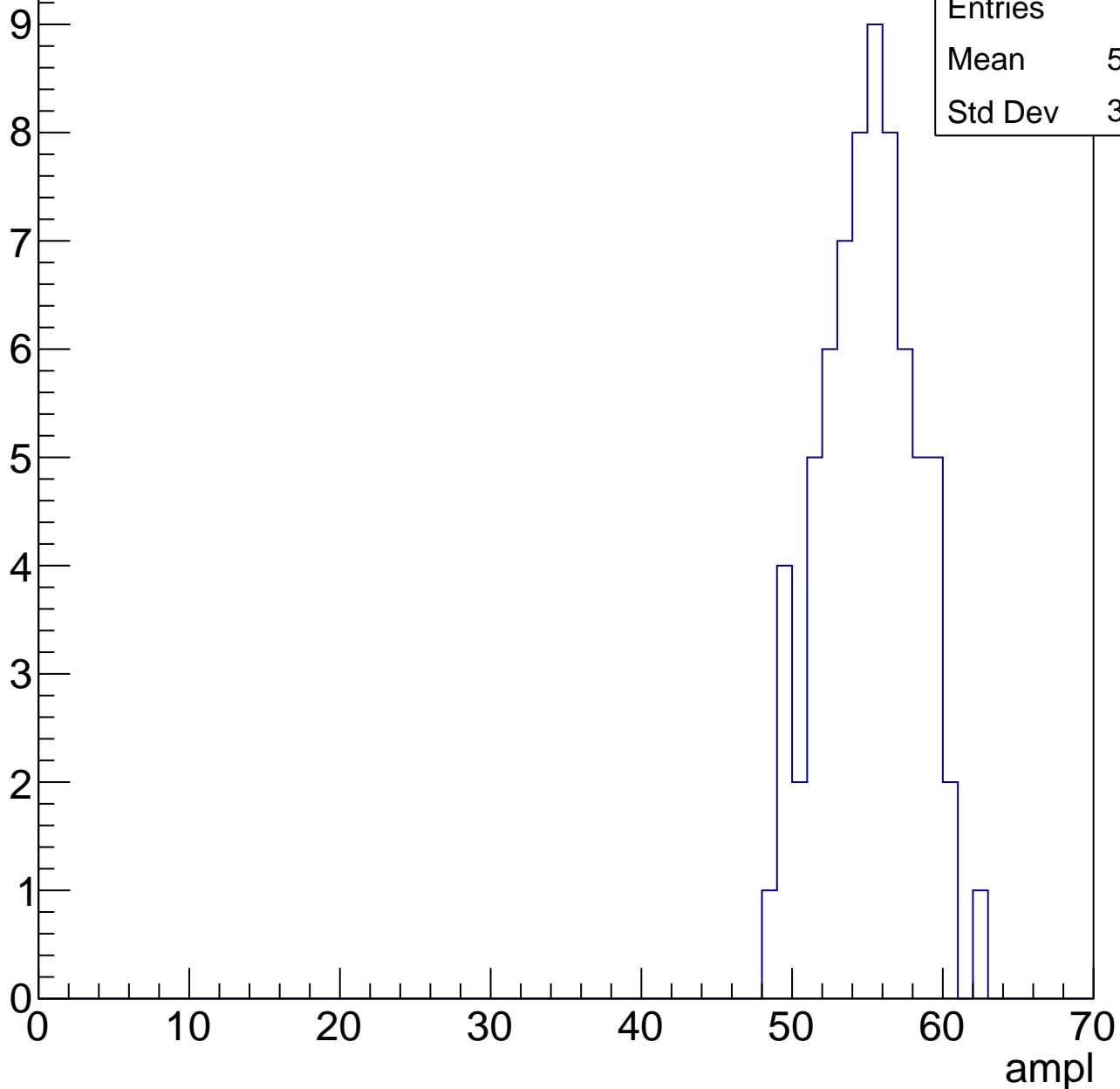
Entries	49
Mean	48.43
Std Dev	3.017



B0L002S, U17-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



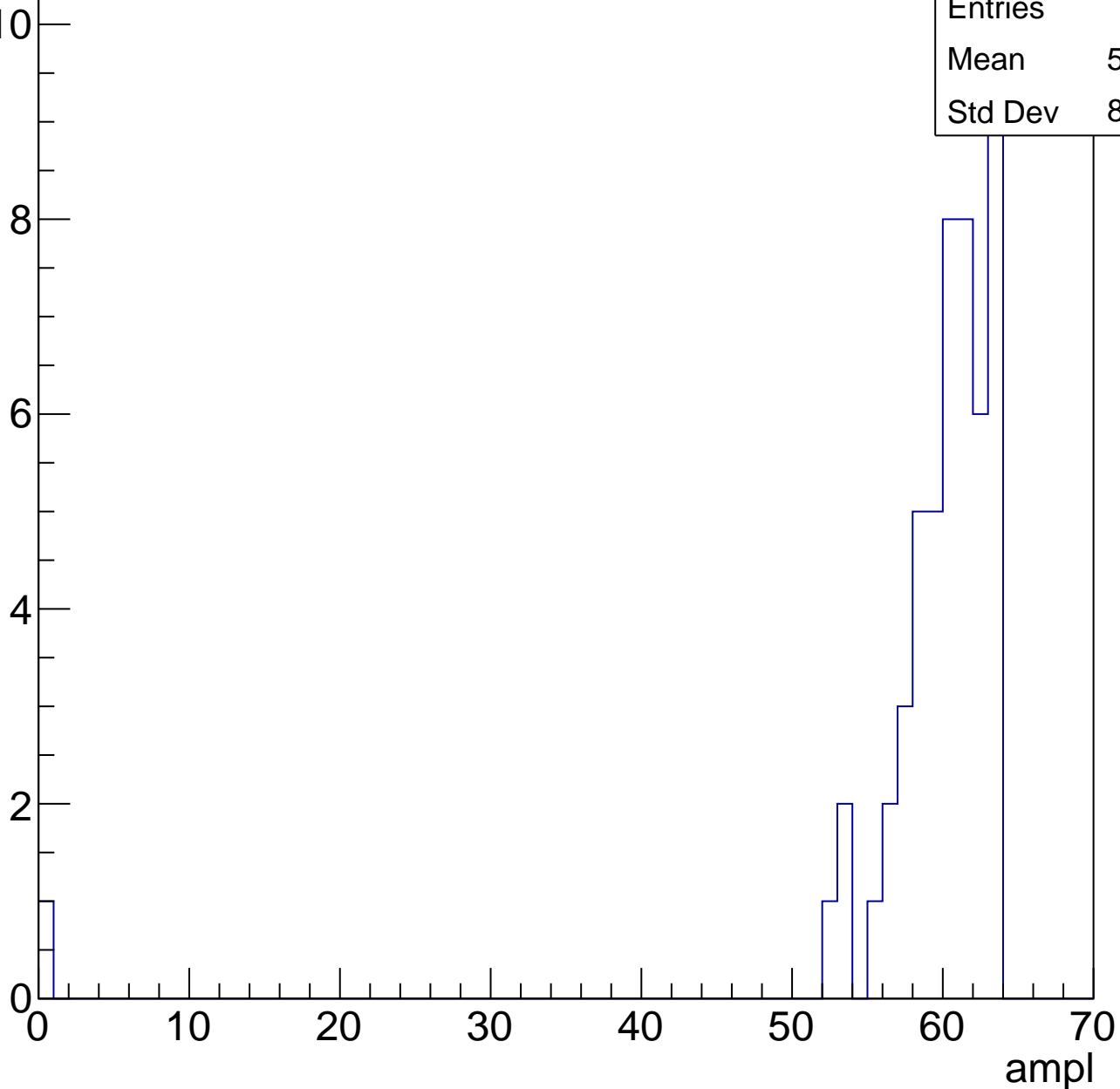
Entries	69
Mean	54.58
Std Dev	3.085

B0L002S, U17-ch33, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

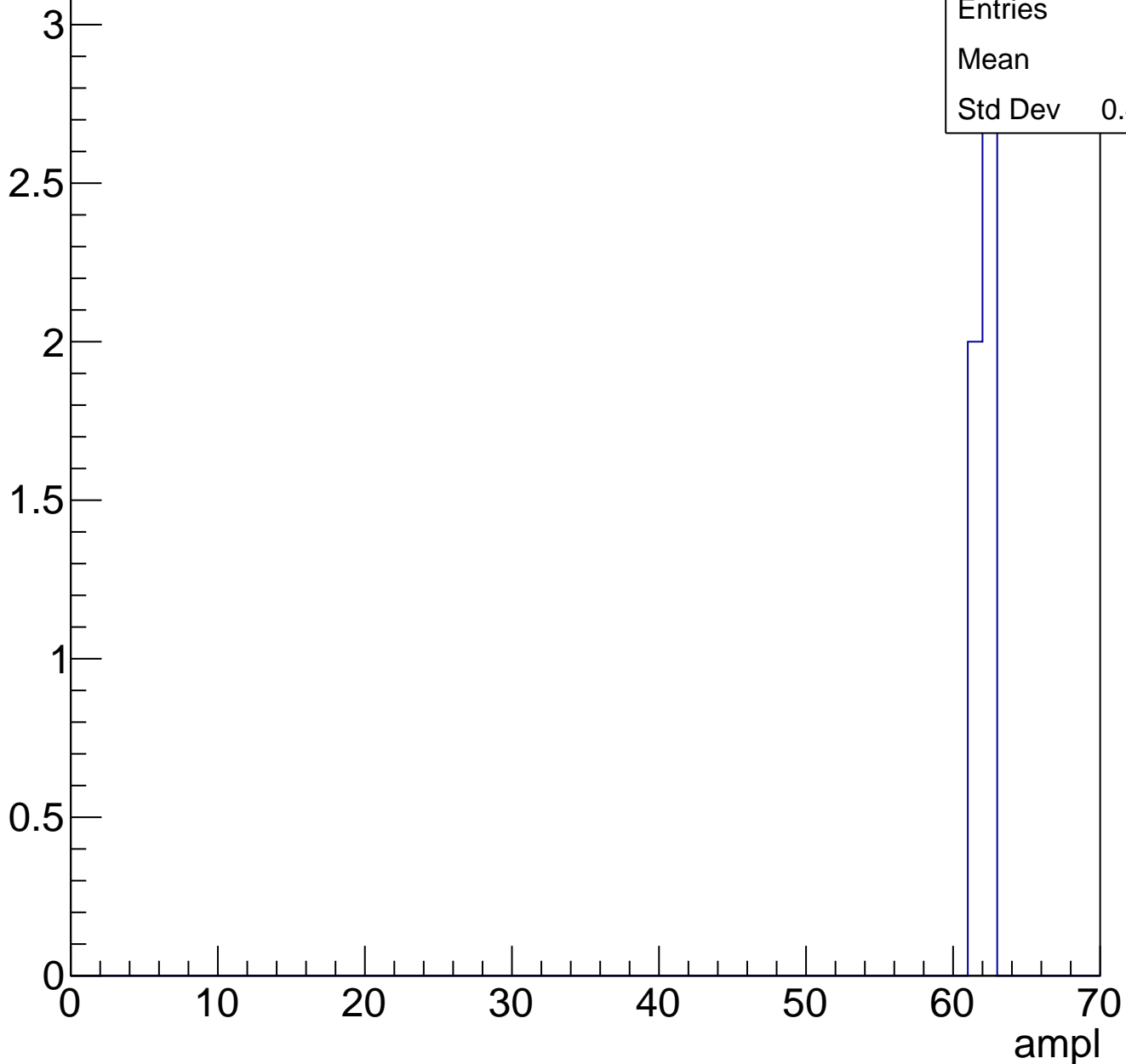
Entries	52
Mean	58.67
Std Dev	8.662



B0L002S, U17-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch34, adc0

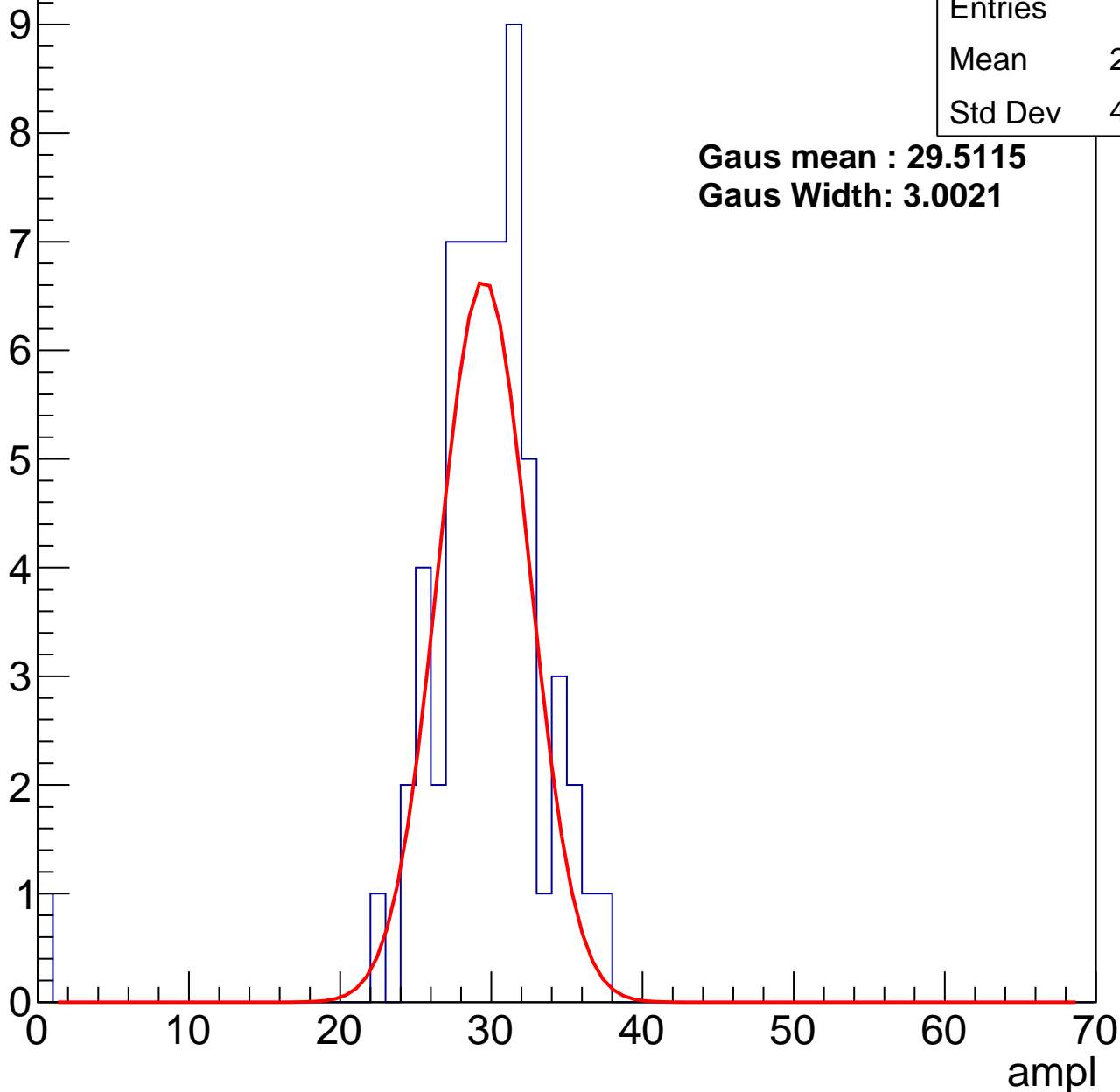
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	28.95
Std Dev	4.866

Gaus mean : 29.5115

Gaus Width: 3.0021



B0L002S, U17-ch34, adc1

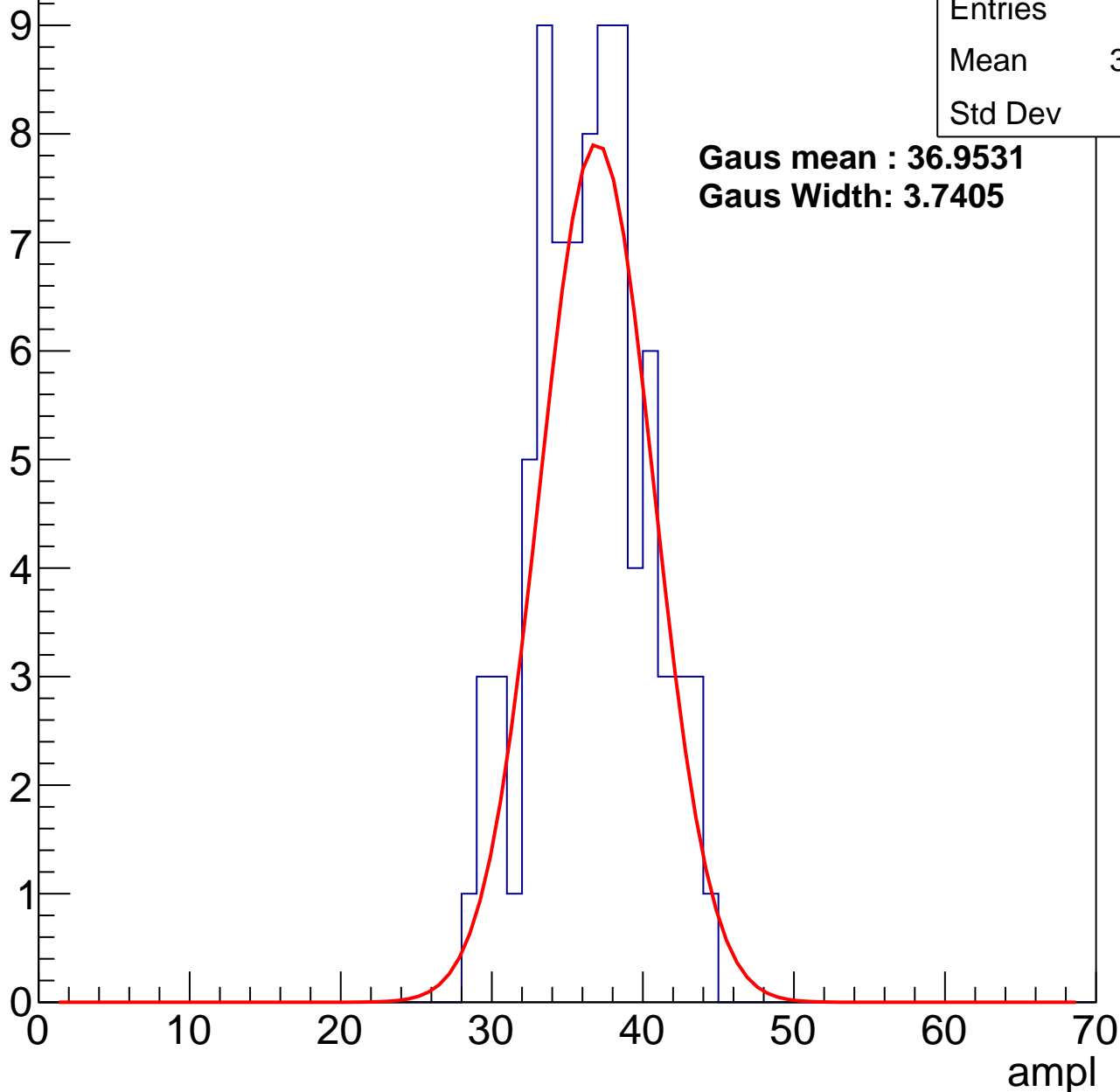
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	36.06
Std Dev	3.68

Gaus mean : 36.9531

Gaus Width: 3.7405



B0L002S, U17-ch34, adc2

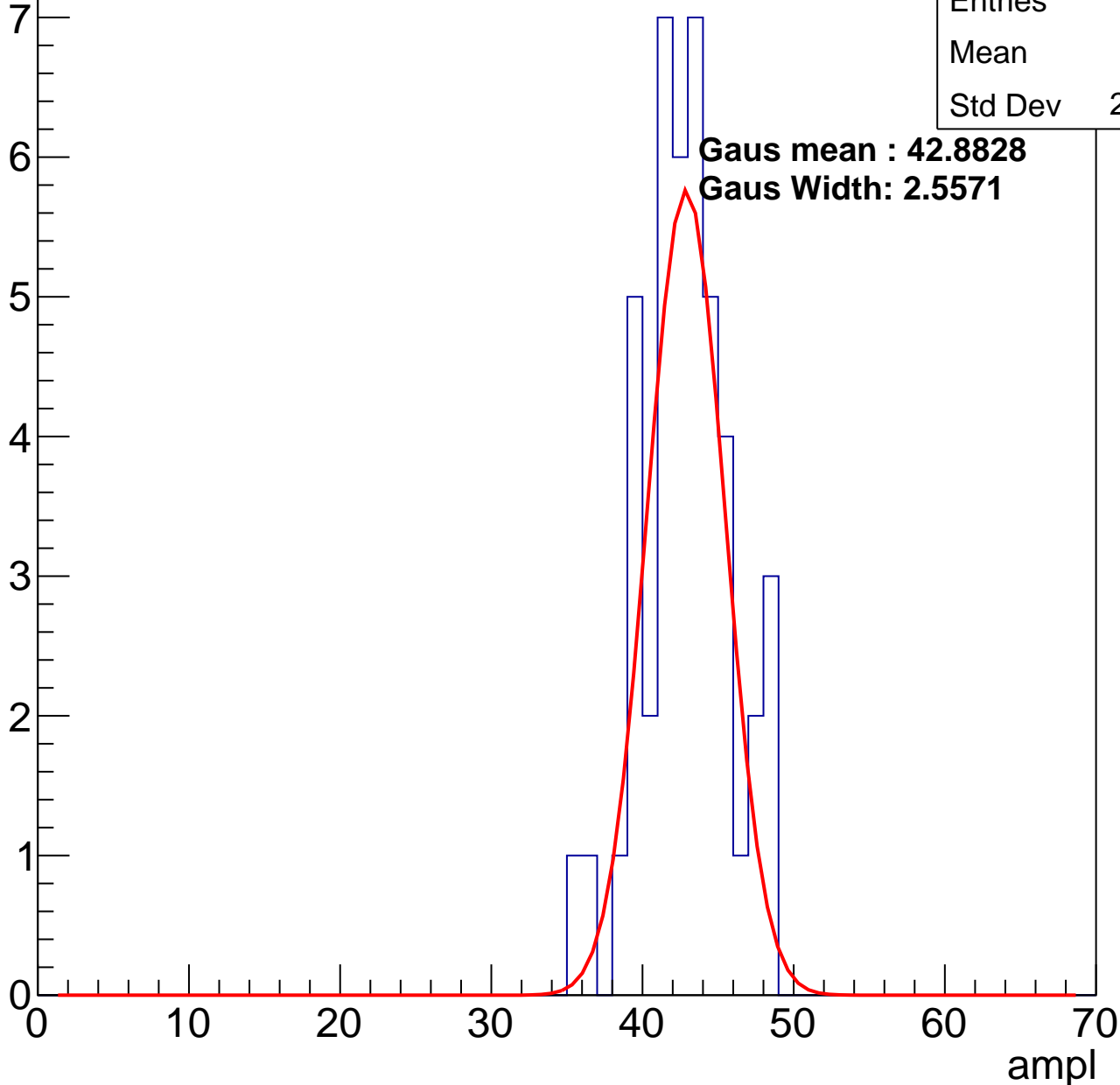
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	42.4
Std Dev	2.947

Gaus mean : 42.8828

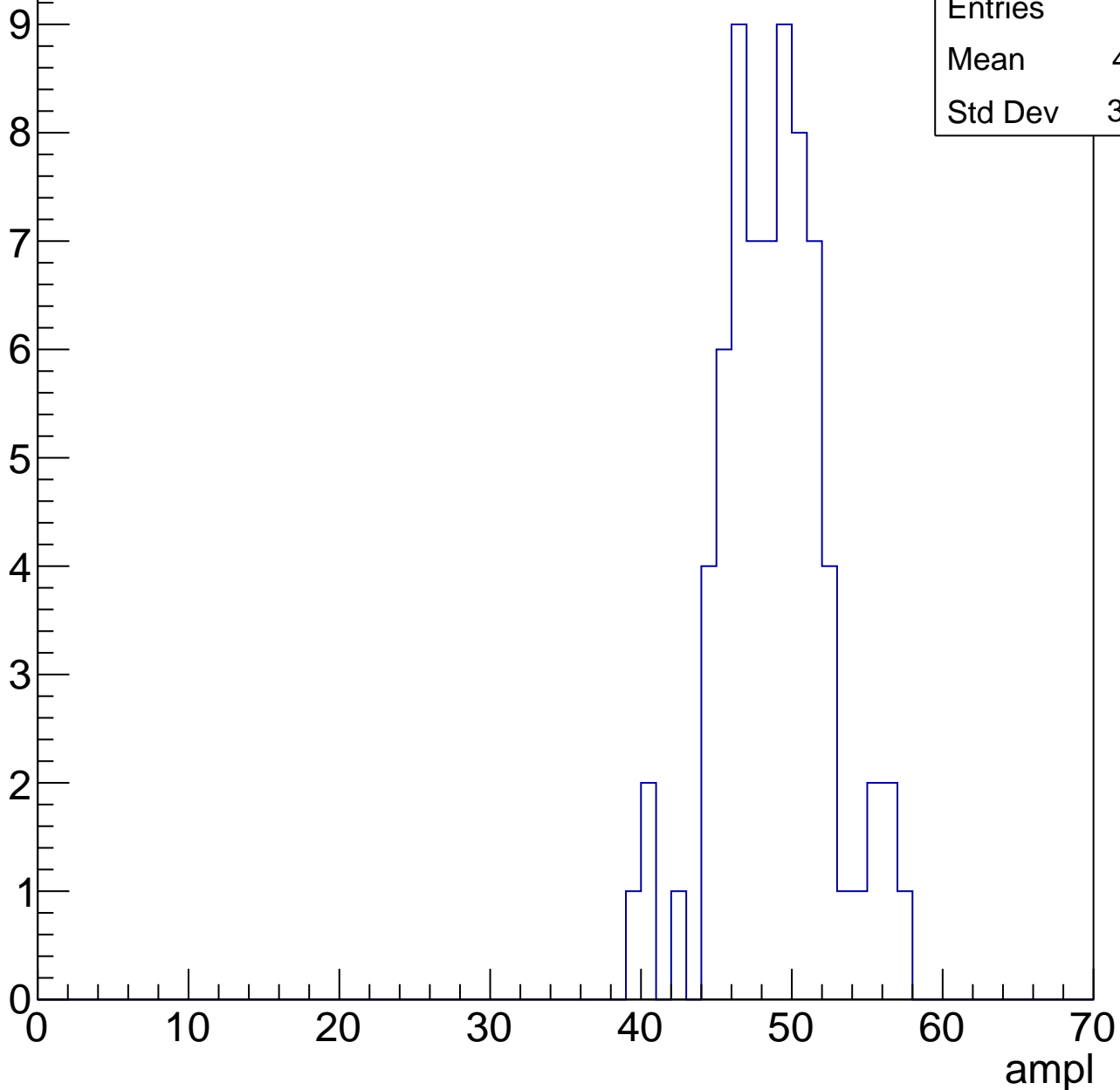
Gaus Width: 2.5571



B0L002S, U17-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

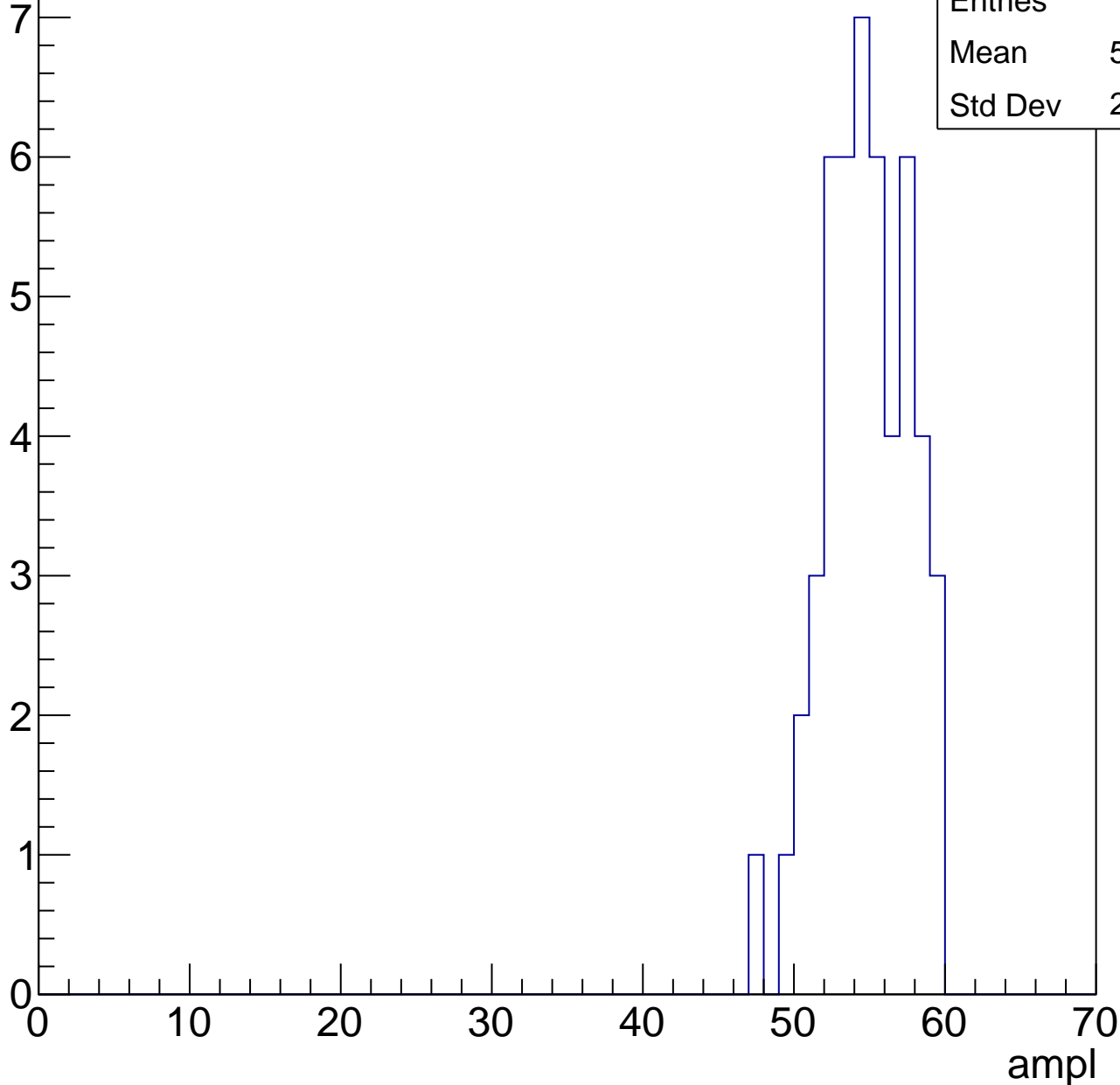


Entries	72
Mean	48.31
Std Dev	3.604

B0L002S, U17-ch34, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

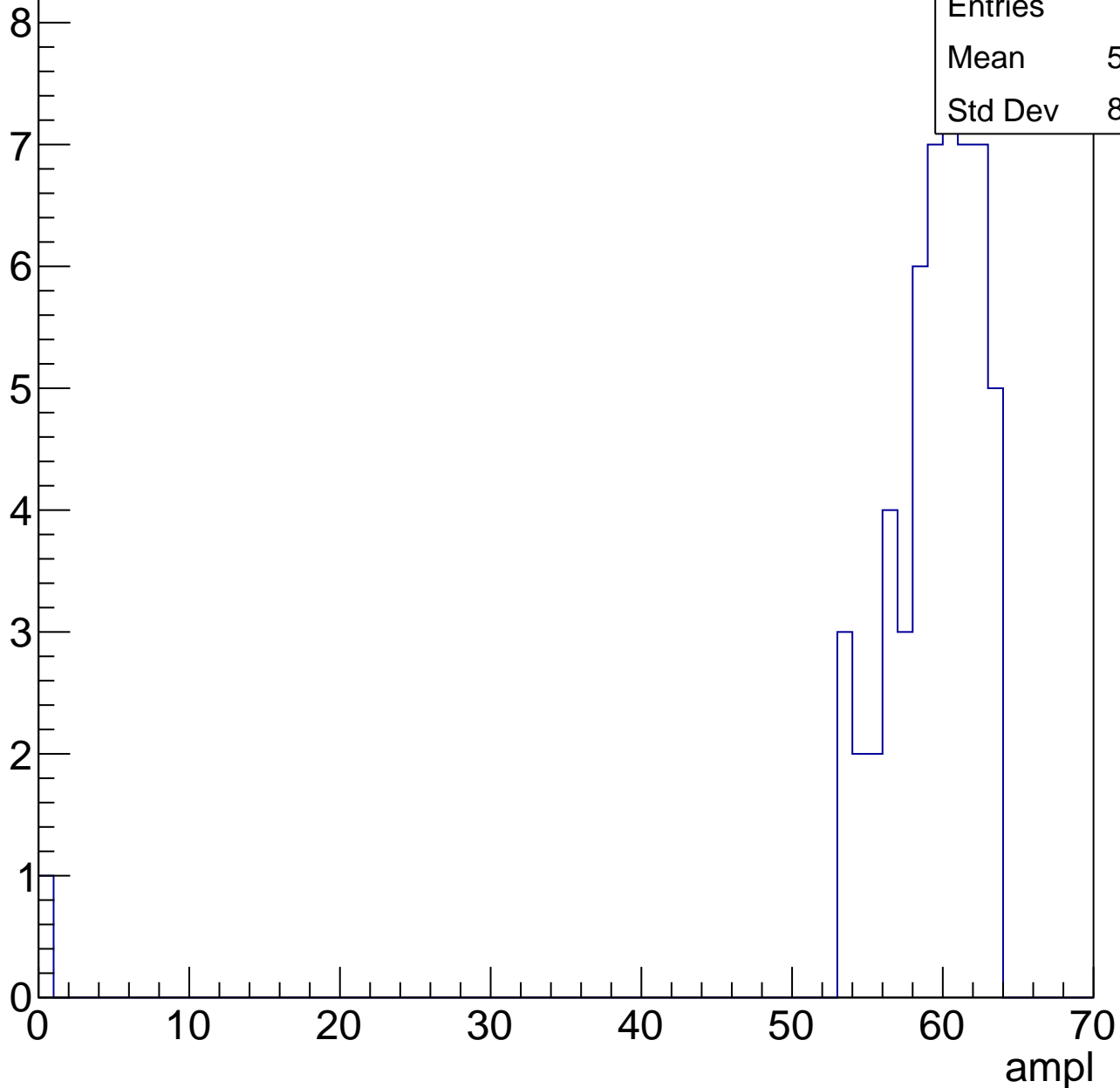


Entries	49
Mean	54.33
Std Dev	2.758

B0L002S, U17-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

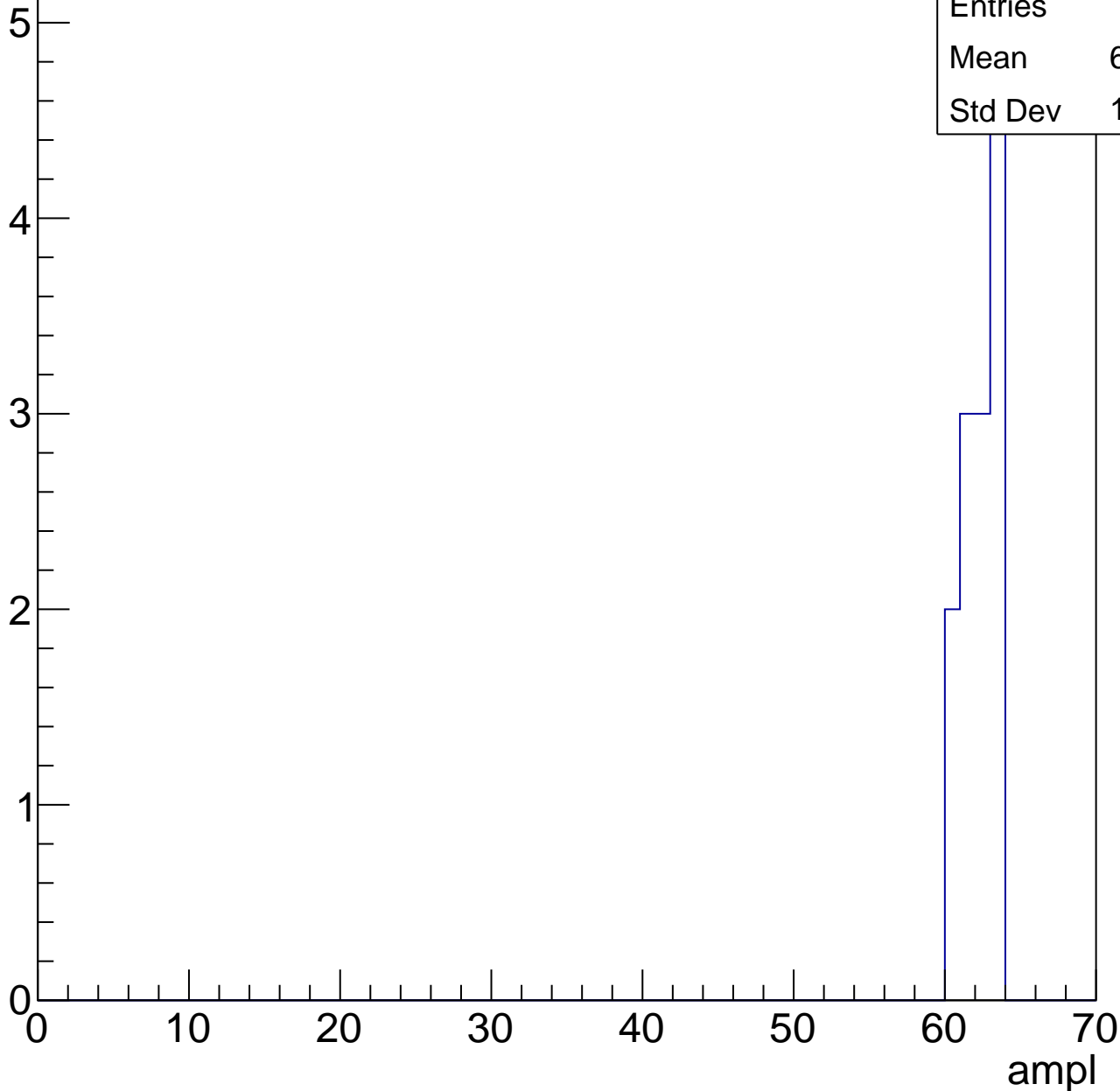


B0L002S, U17-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	13
Mean	61.85
Std Dev	1.099



B0L002S, U17-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch35, adc0

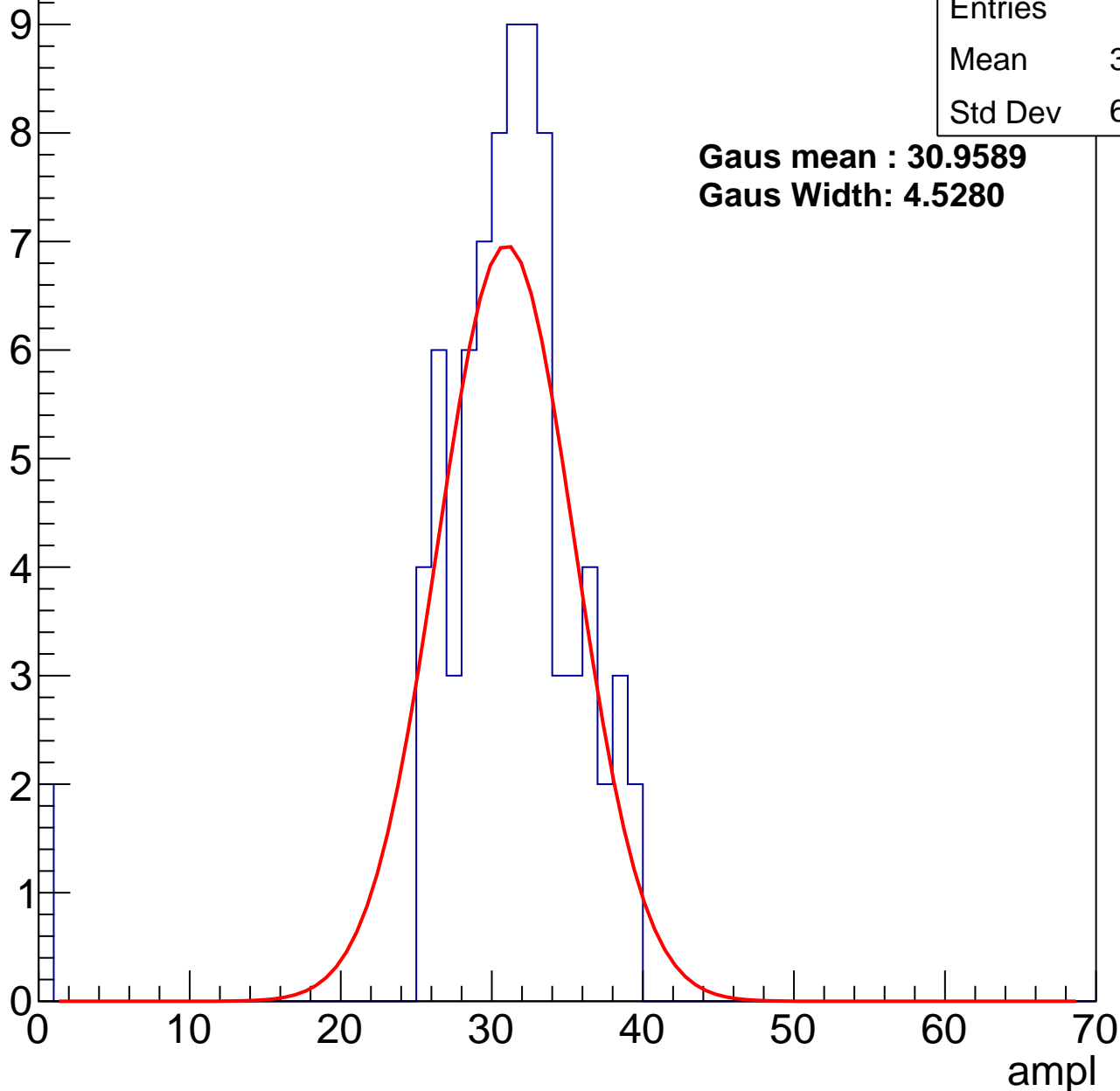
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.33
Std Dev	6.039

Gaus mean : 30.9589

Gaus Width: 4.5280



B0L002S, U17-ch35, adc1

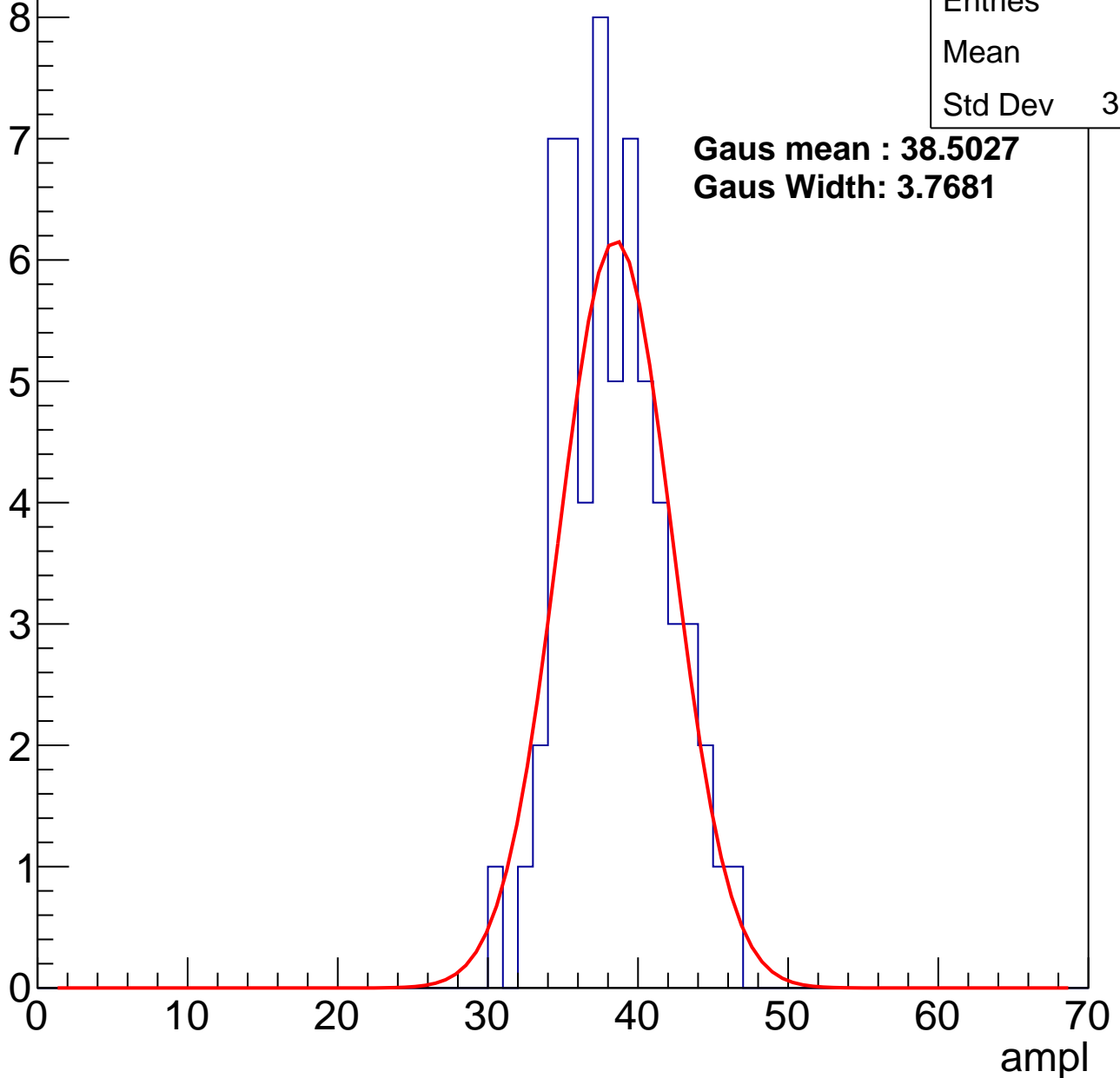
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.9
Std Dev	3.434

Gaus mean : 38.5027

Gaus Width: 3.7681



B0L002S, U17-ch35, adc2

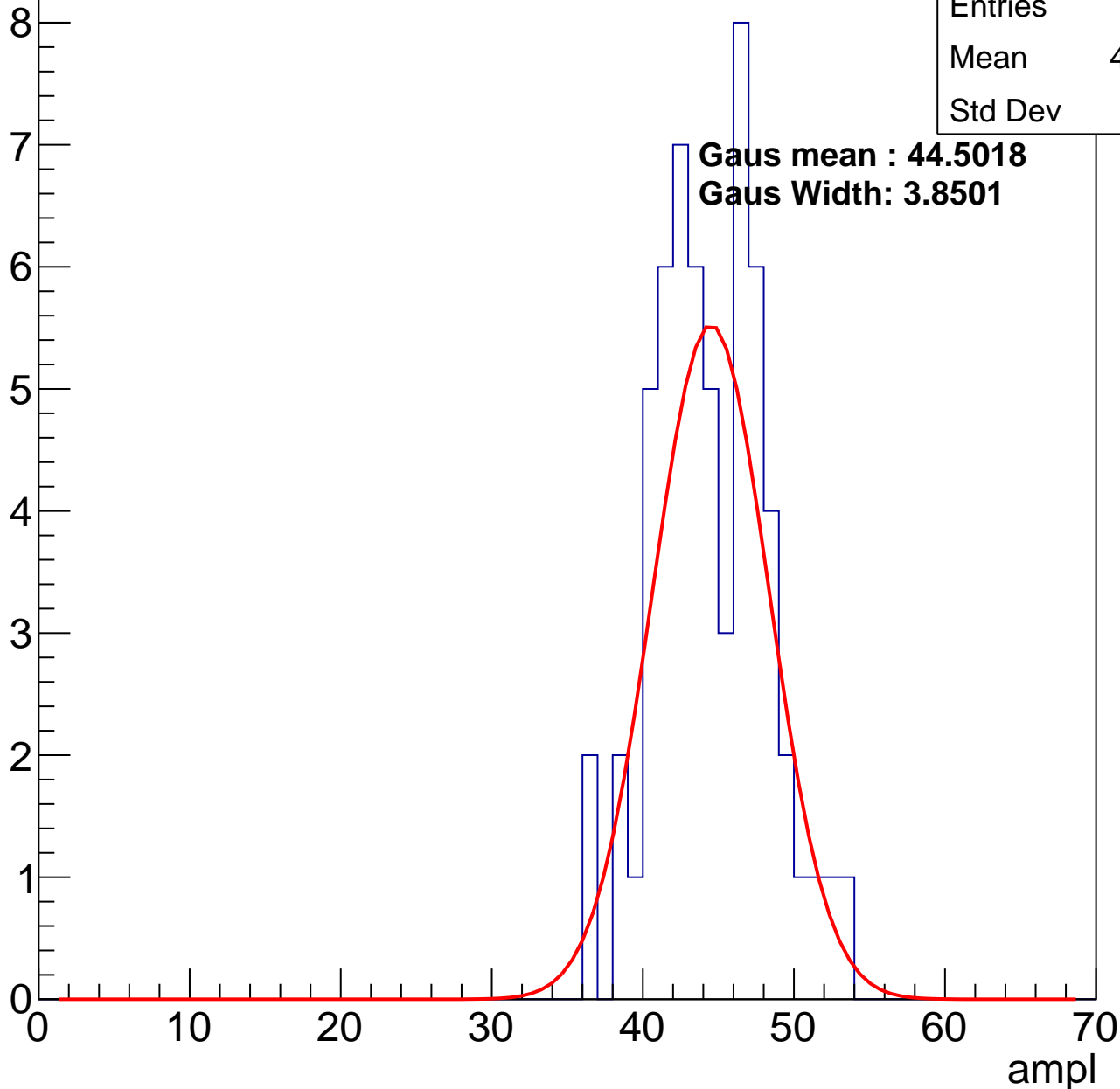
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.03
Std Dev	3.68

Gaus mean : 44.5018

Gaus Width: 3.8501

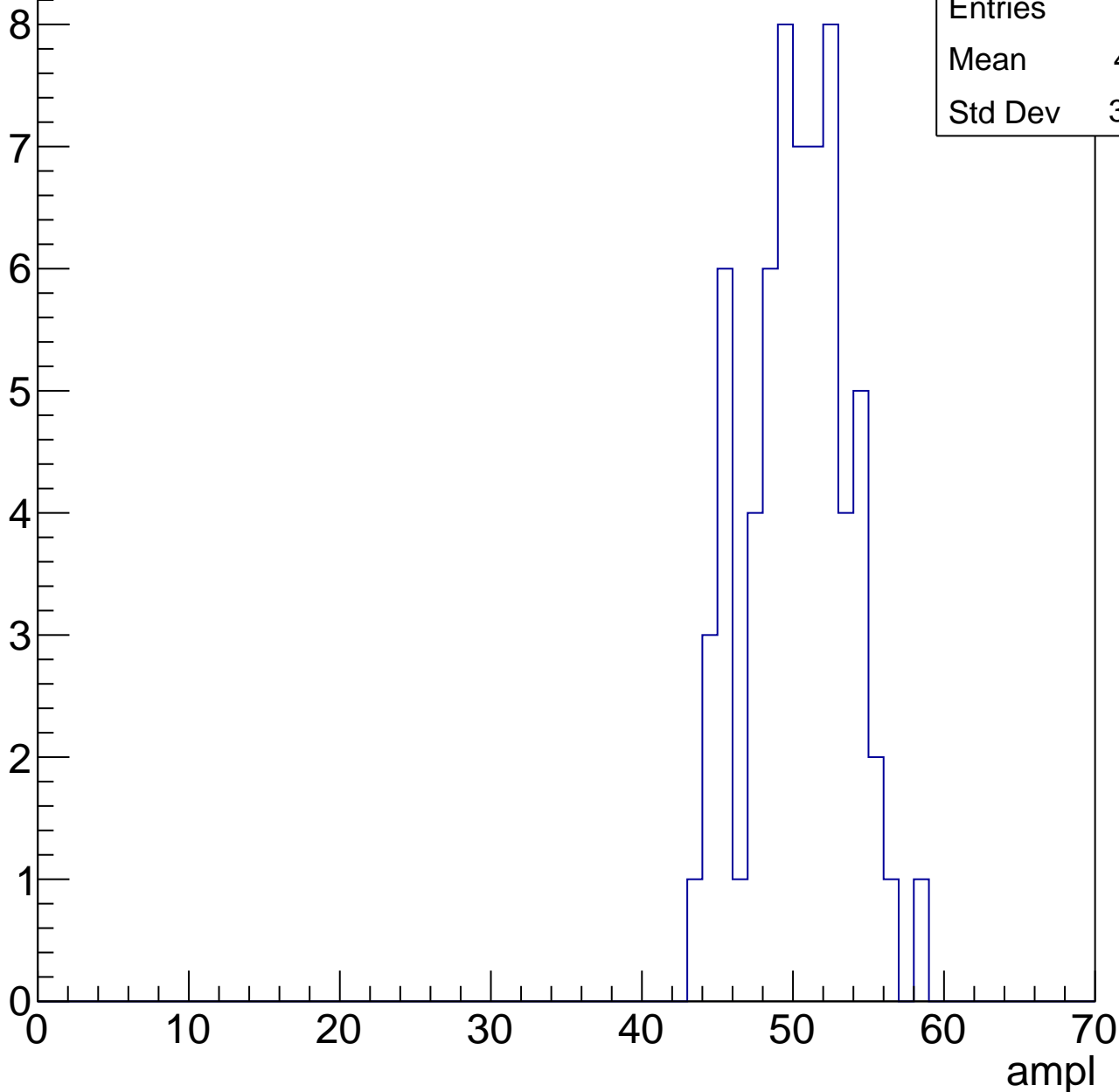


B0L002S, U17-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

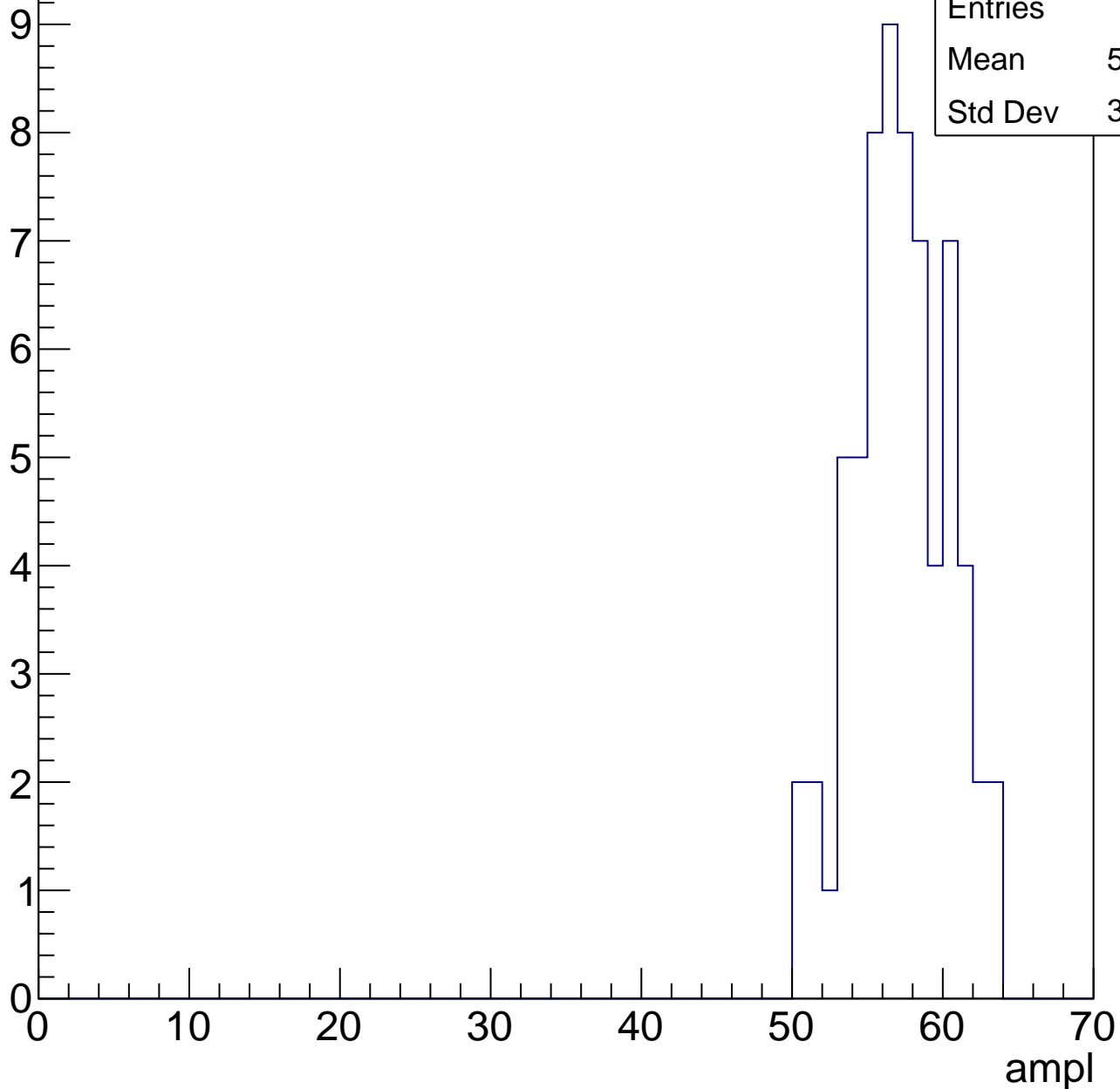
Entries	64
Mean	49.81
Std Dev	3.292



B0L002S, U17-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



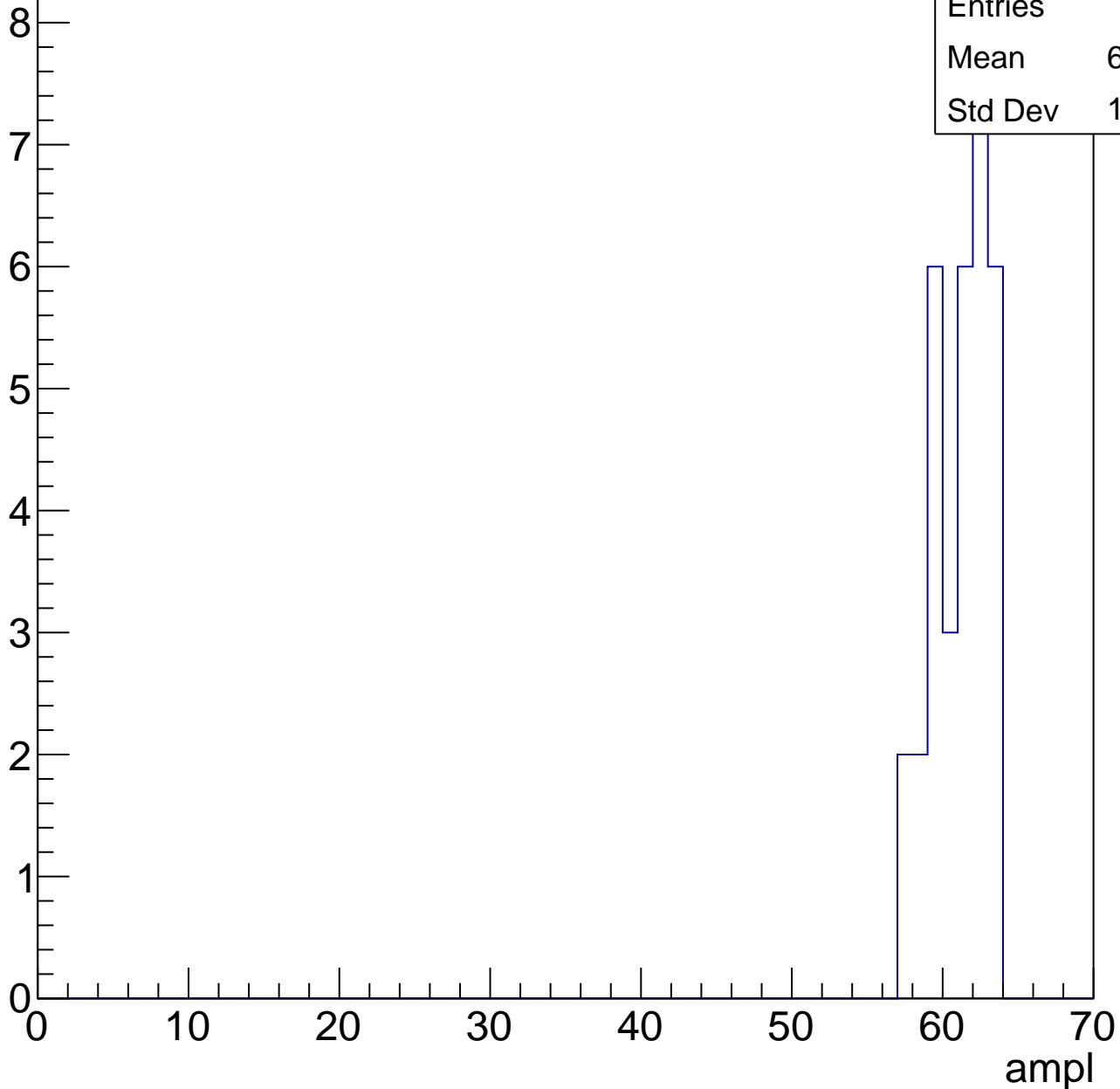
Entries	66
Mean	56.74
Std Dev	3.086

B0L002S, U17-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	60.73
Std Dev	1.797



B0L002S, U17-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch36, adc0

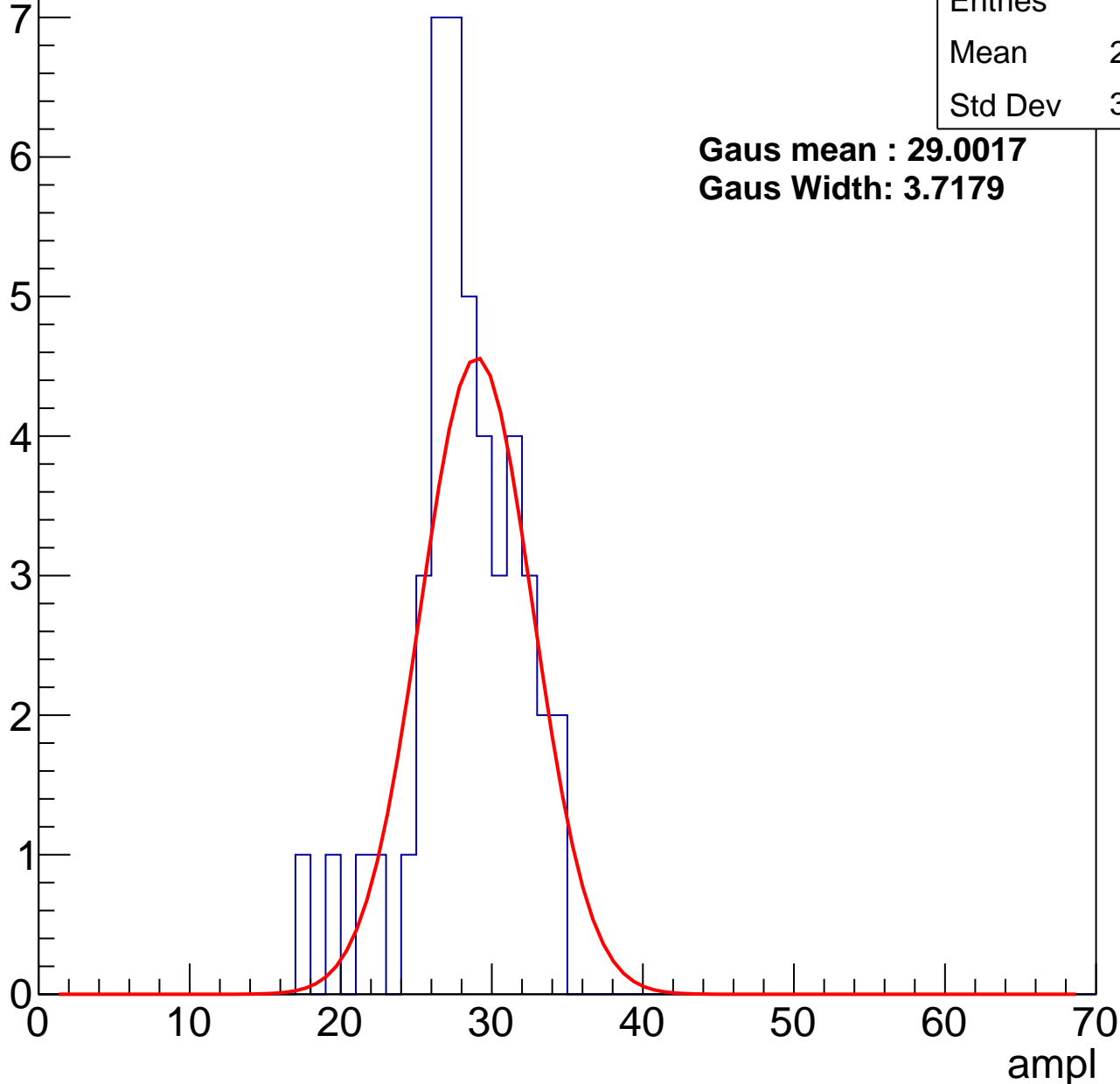
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	27.76
Std Dev	3.603

Gaus mean : 29.0017

Gaus Width: 3.7179



B0L002S, U17-ch36, adc1

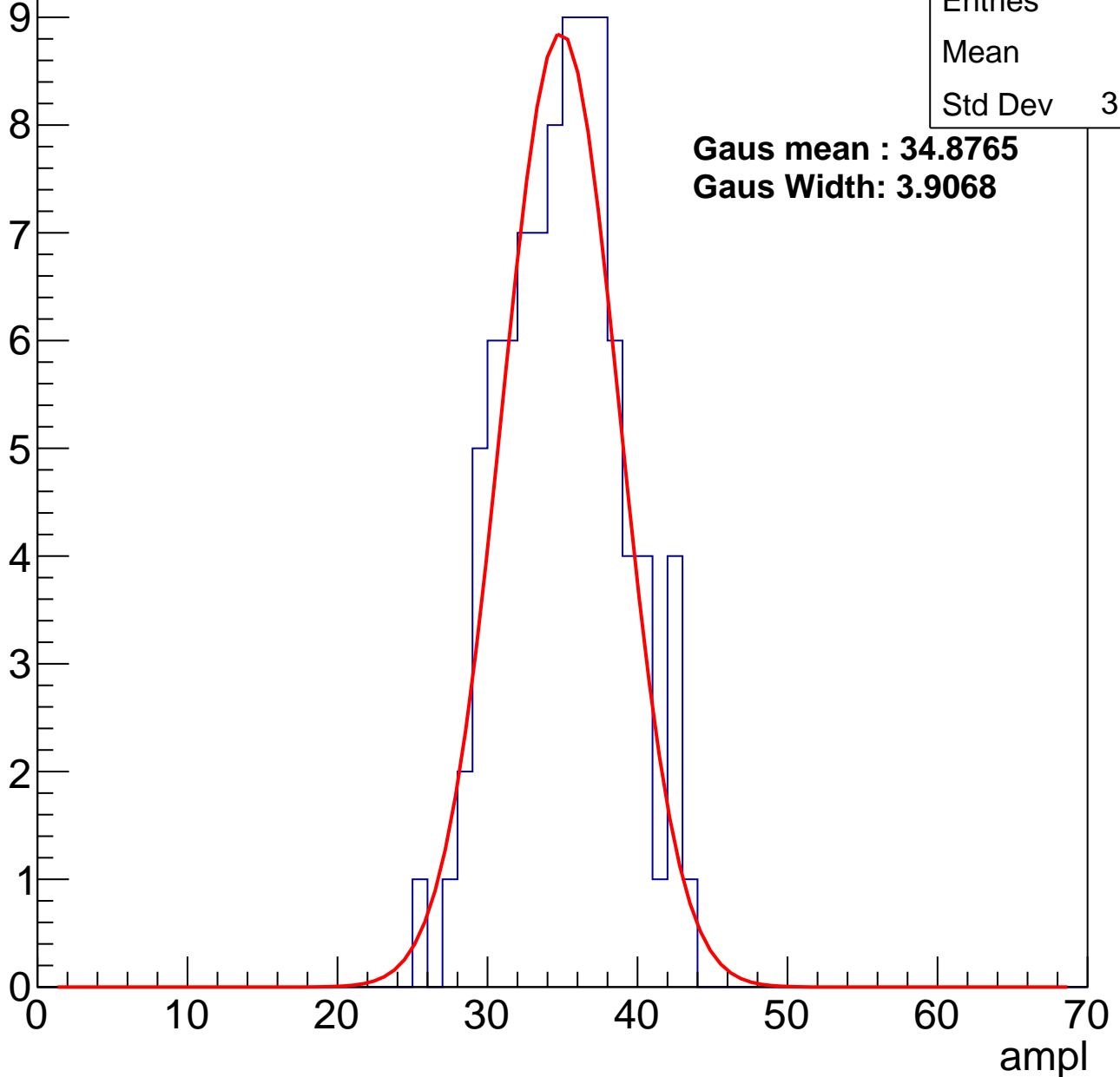
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	90
Mean	34.6
Std Dev	3.846

Gaus mean : 34.8765

Gaus Width: 3.9068



B0L002S, U17-ch36, adc2

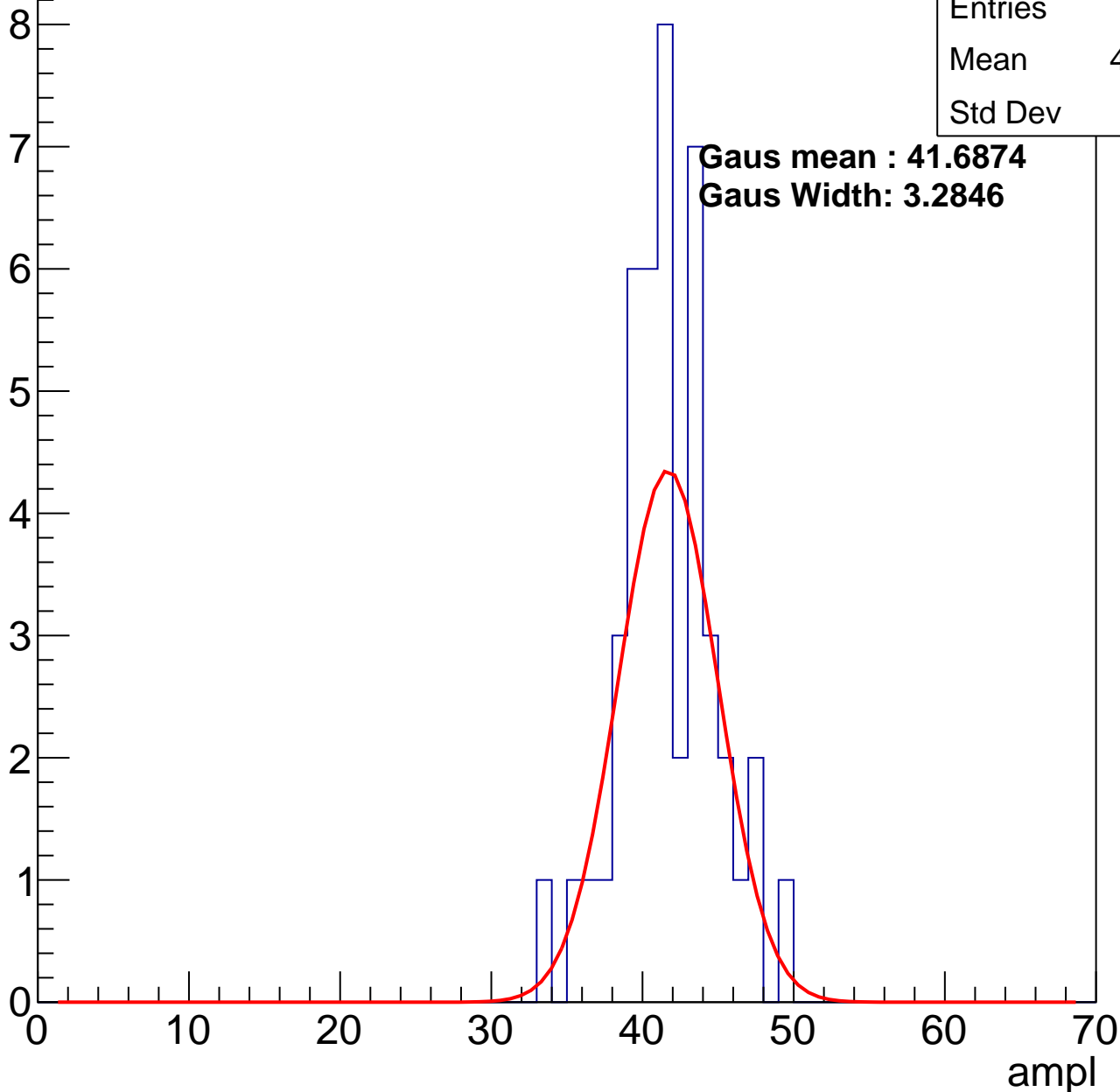
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	41.18
Std Dev	3.15

Gaus mean : 41.6874

Gaus Width: 3.2846

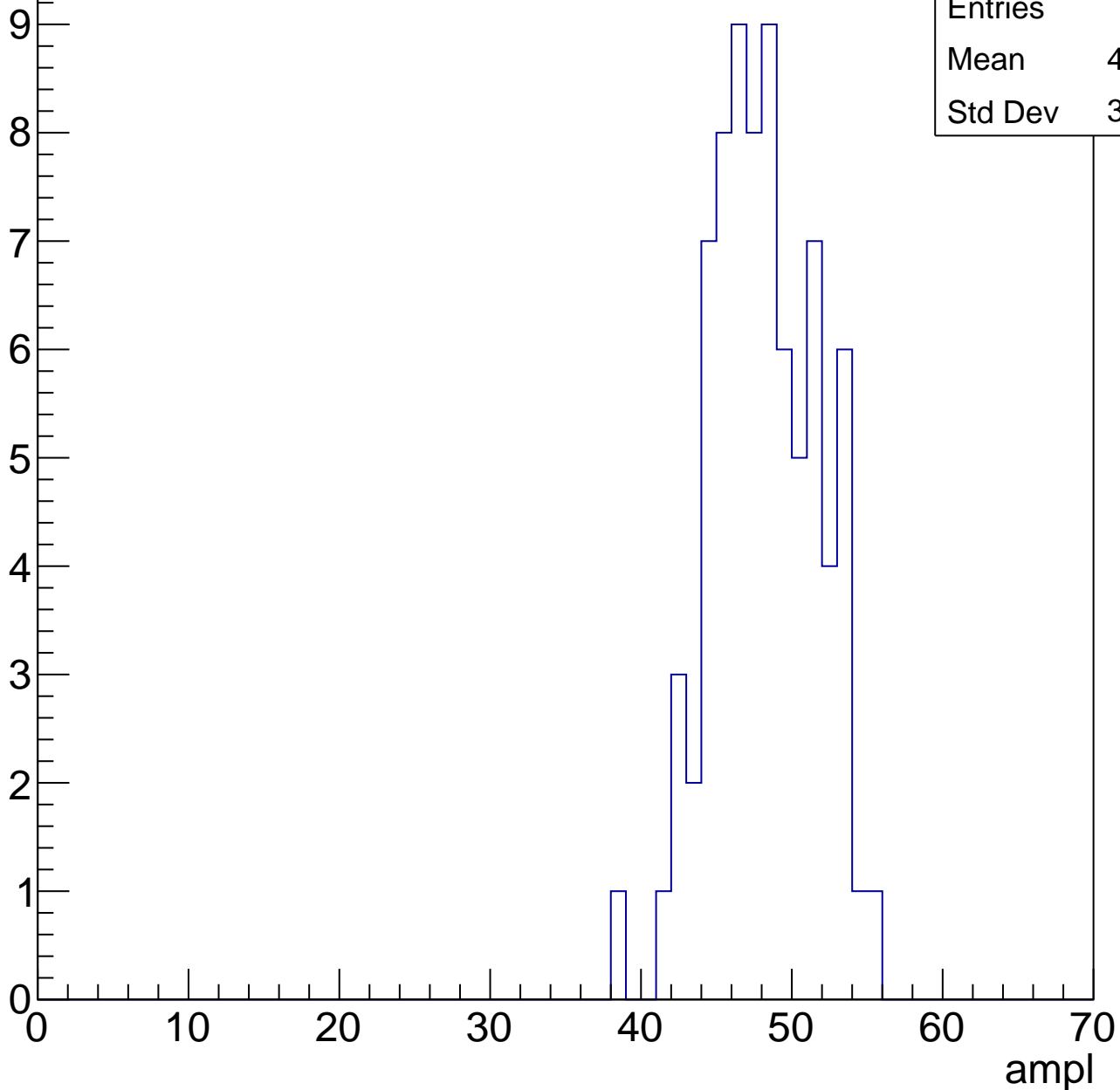


B0L002S, U17-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	47.65
Std Dev	3.423

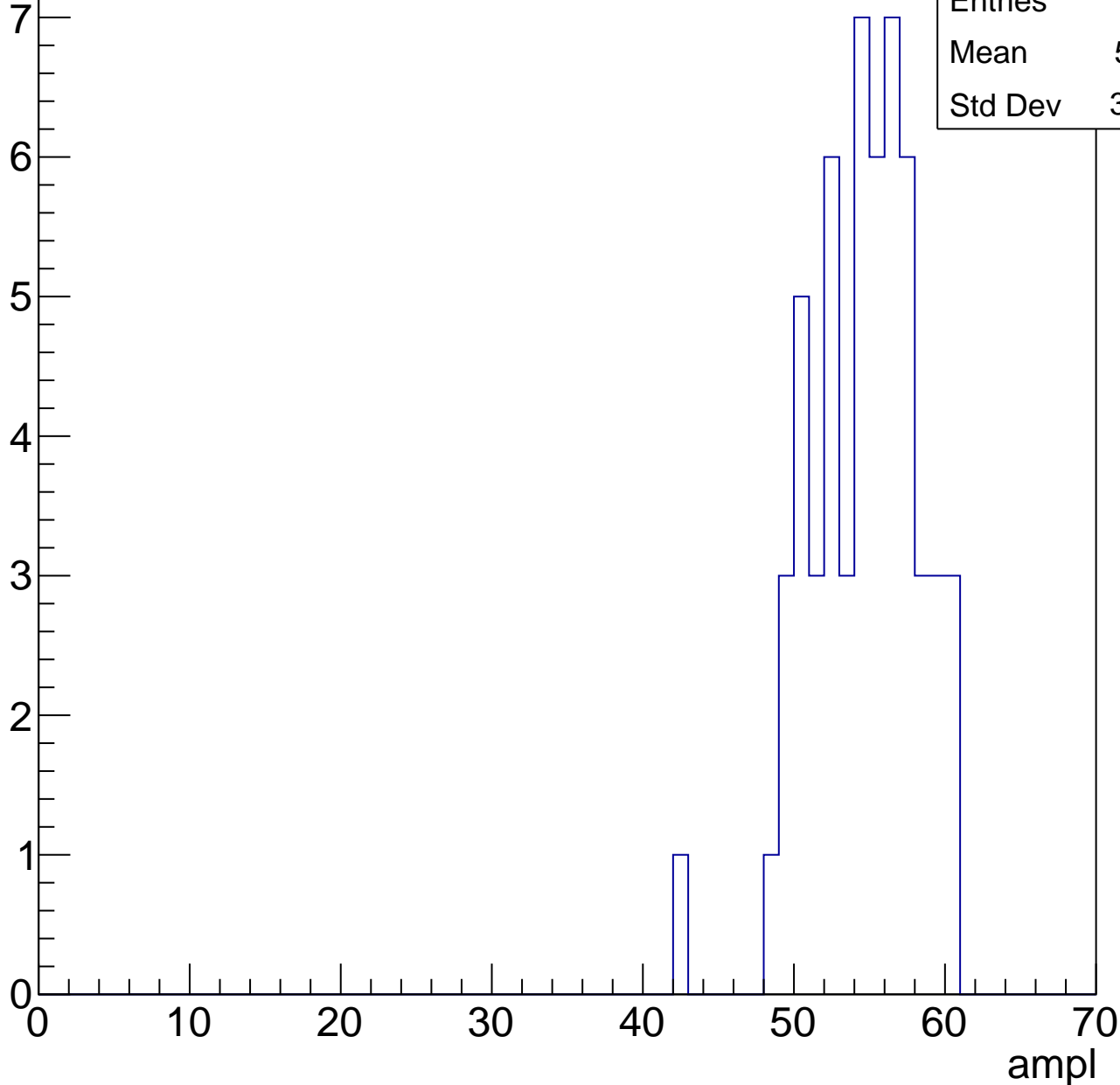


B0L002S, U17-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

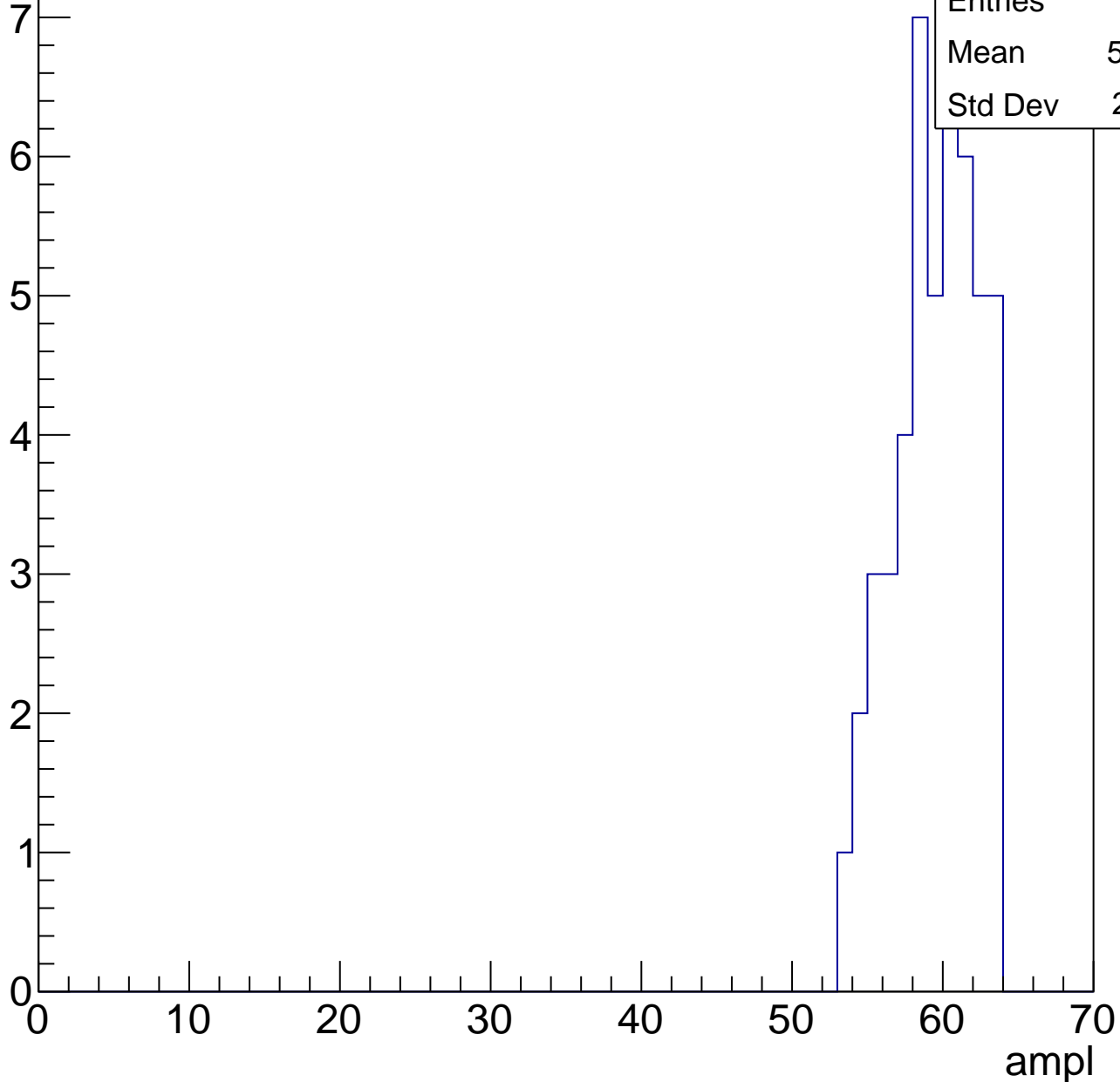
Entries	57
Mean	54.11
Std Dev	3.523



B0L002S, U17-ch36, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

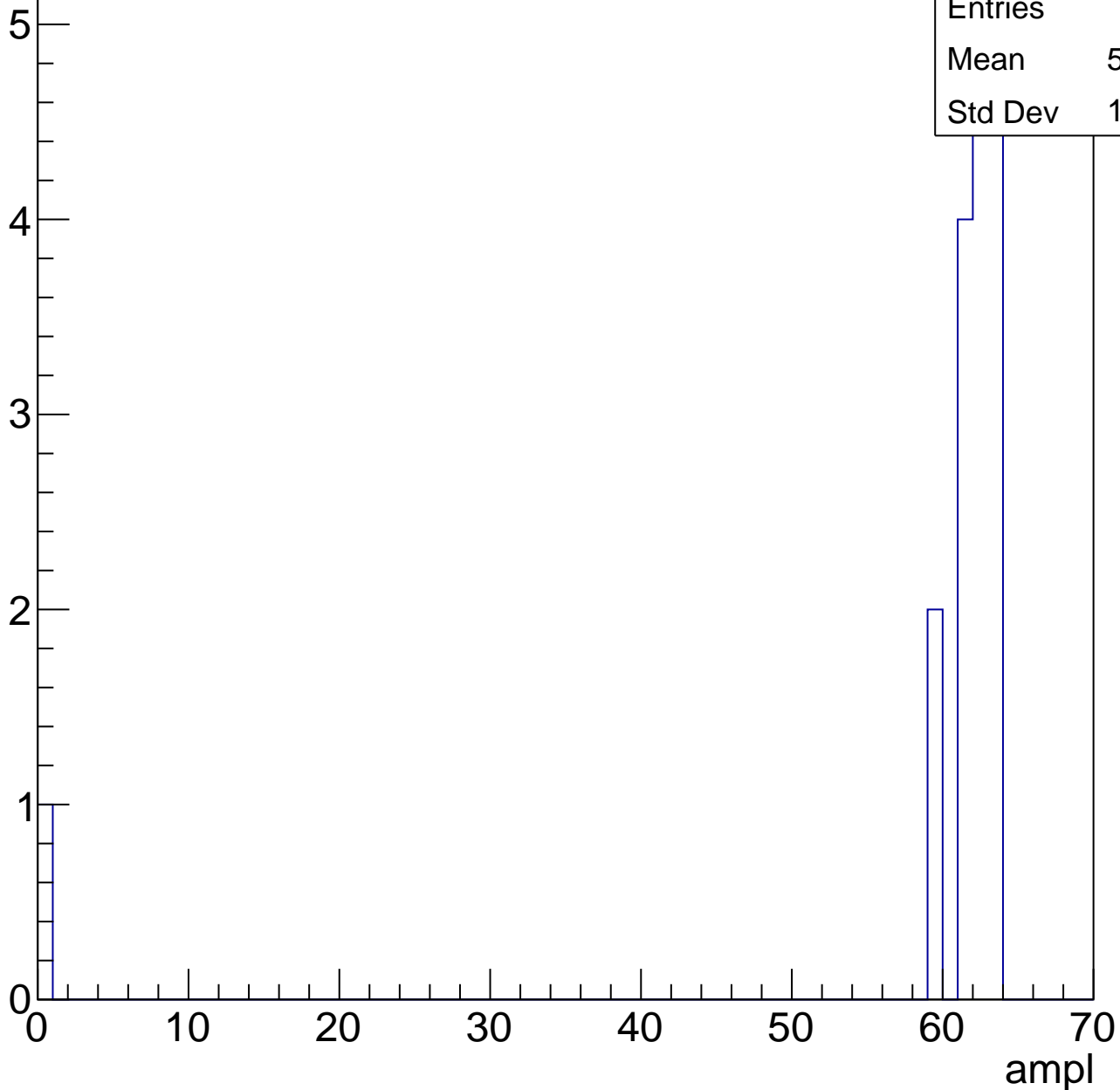


B0L002S, U17-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	58.06
Std Dev	14.57



B0L002S, U17-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch37, adc0

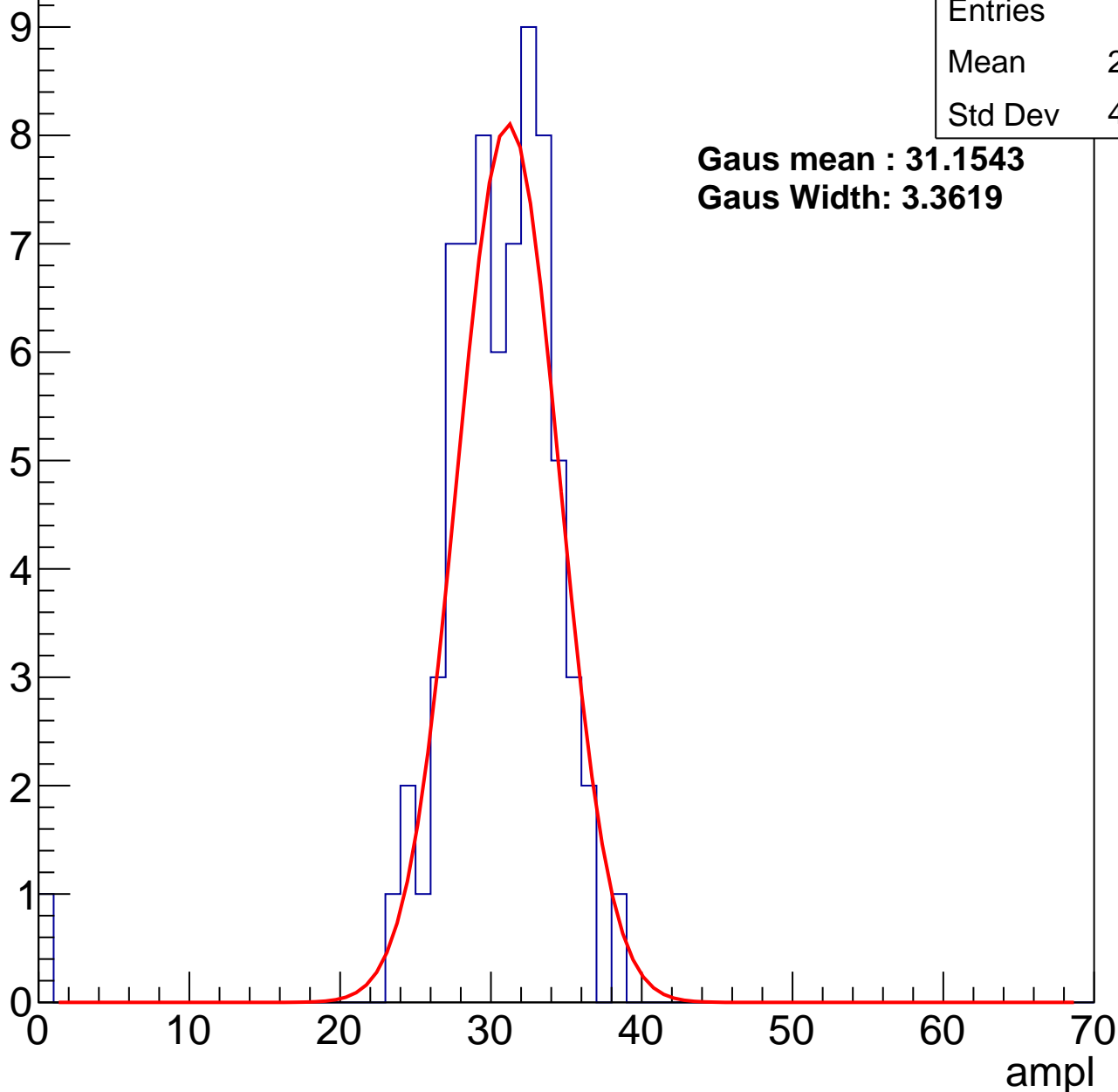
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.93
Std Dev	4.745

Gaus mean : 31.1543

Gaus Width: 3.3619



B0L002S, U17-ch37, adc1

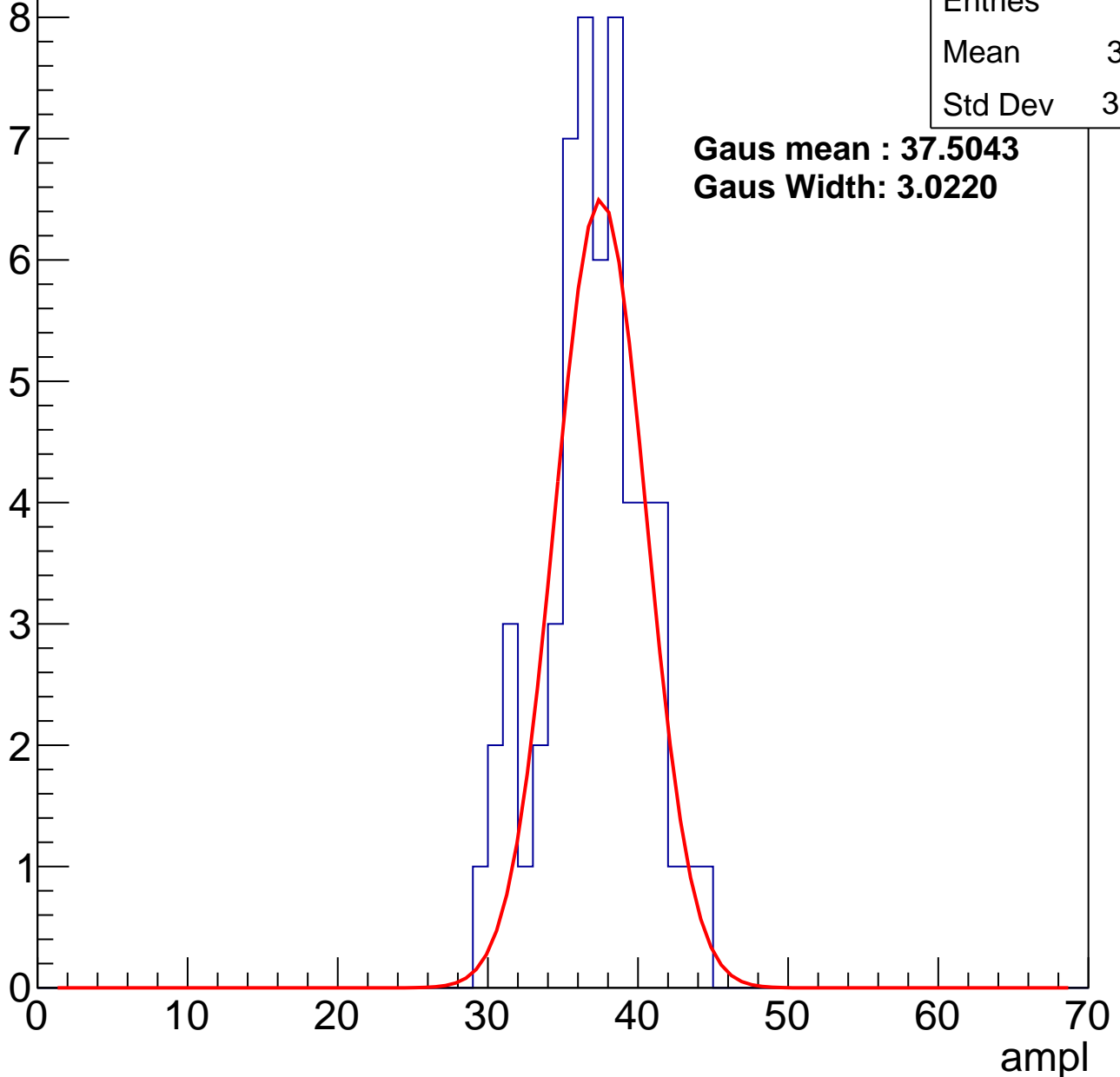
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.61
Std Dev	3.315

Gaus mean : 37.5043

Gaus Width: 3.0220



B0L002S, U17-ch37, adc2

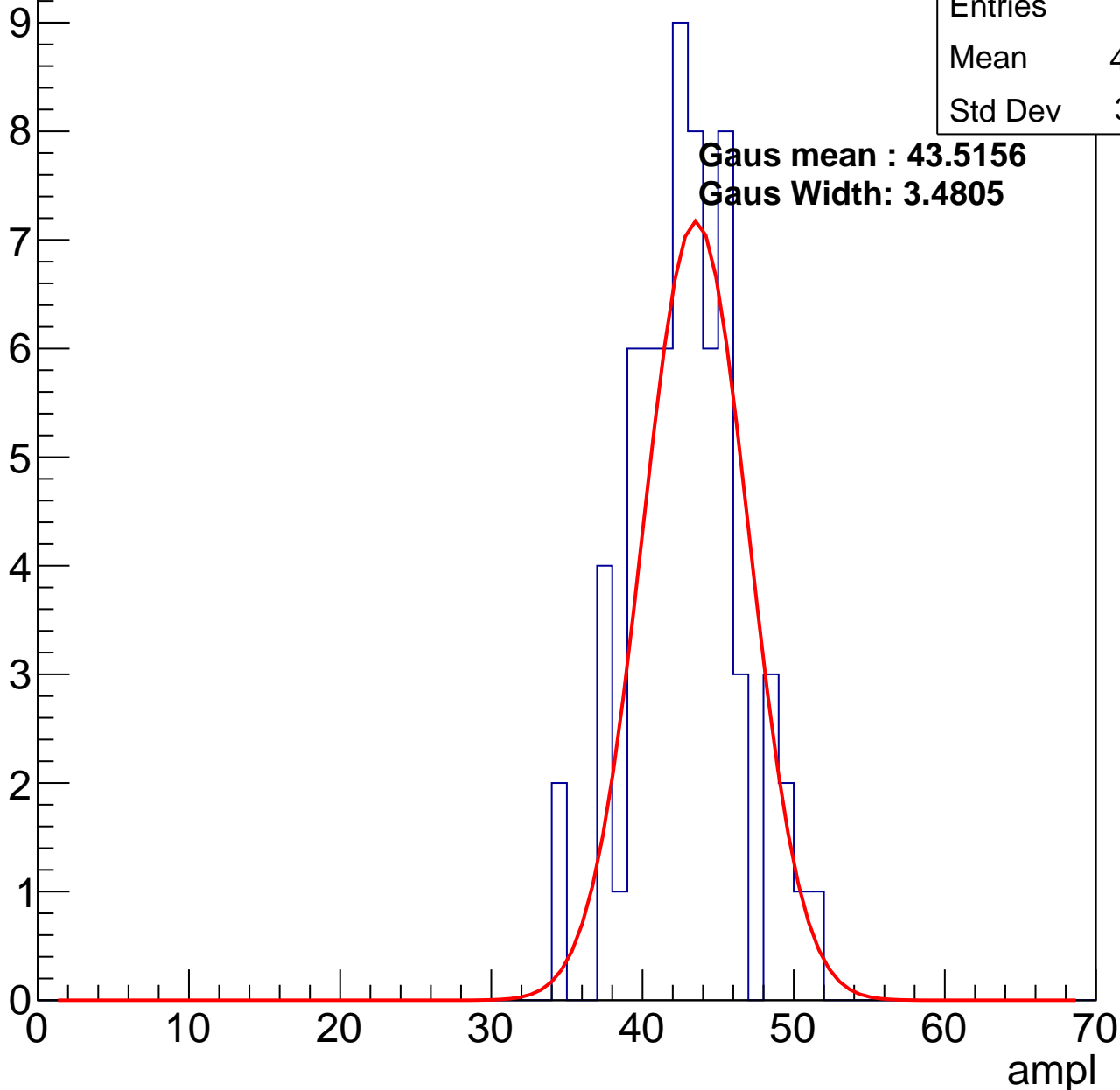
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.44
Std Dev	3.521

Gaus mean : 43.5156

Gaus Width: 3.4805

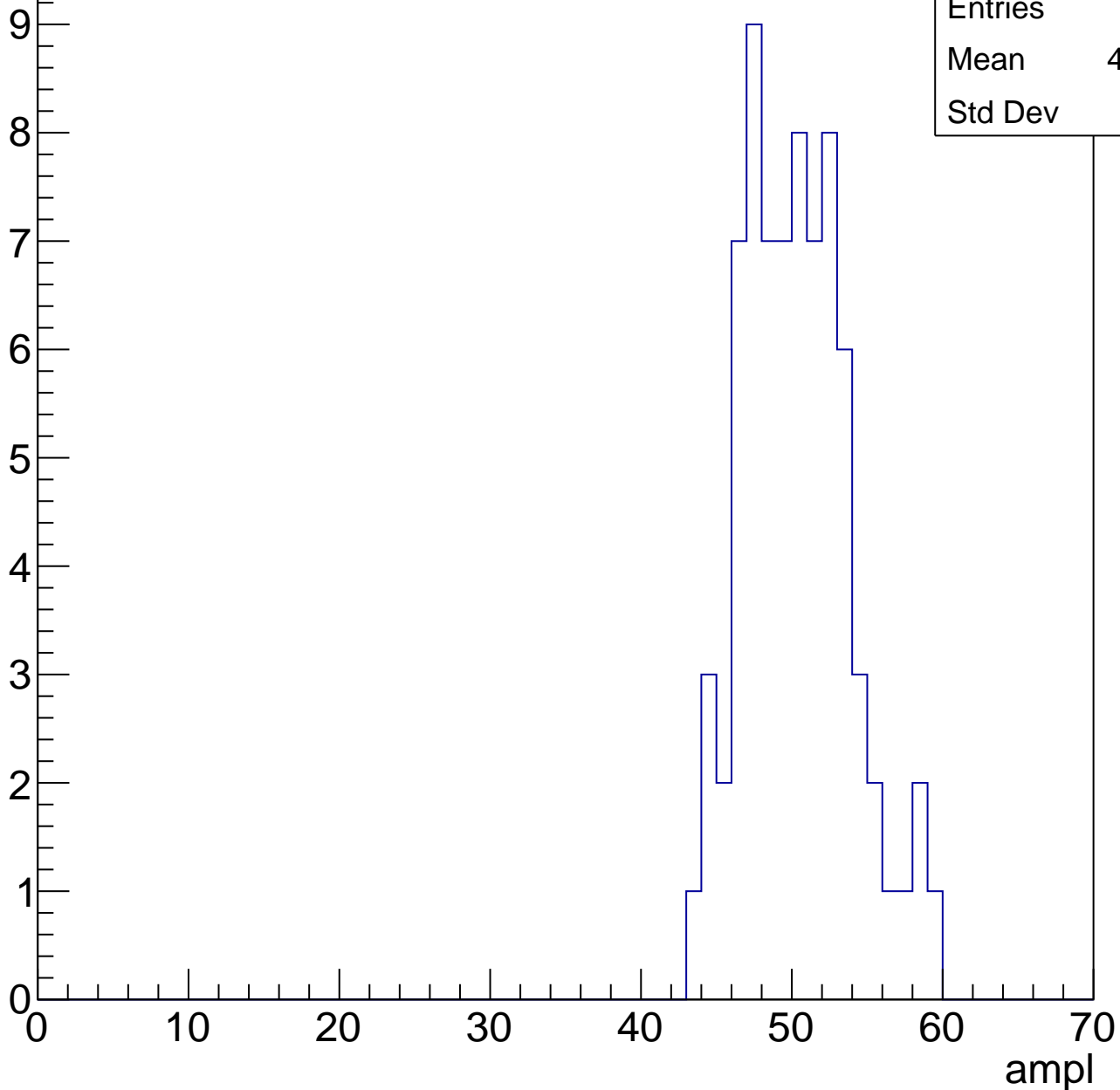


B0L002S, U17-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	49.87
Std Dev	3.5

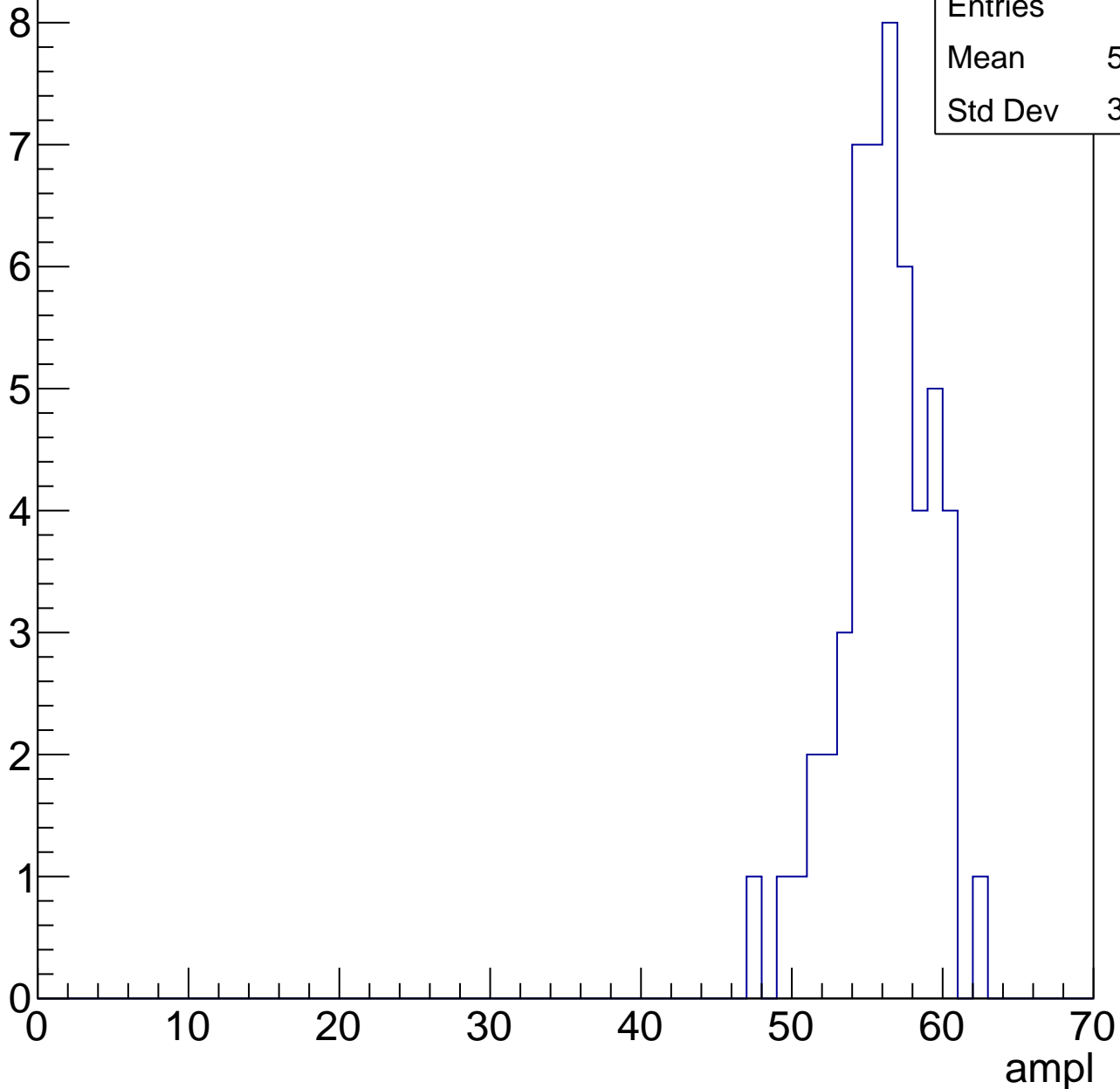


B0L002S, U17-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	55.63
Std Dev	3.007

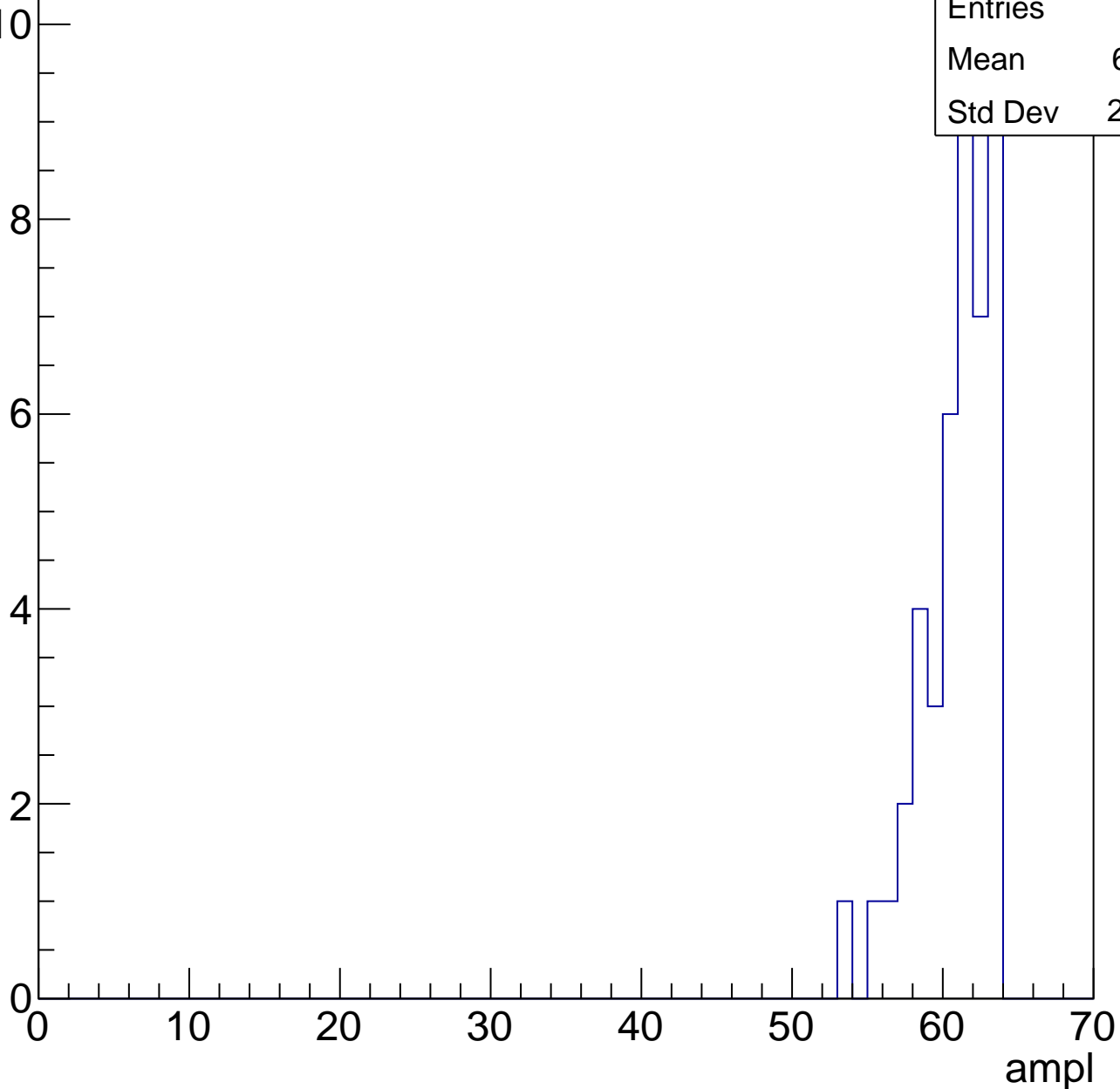


B0L002S, U17-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

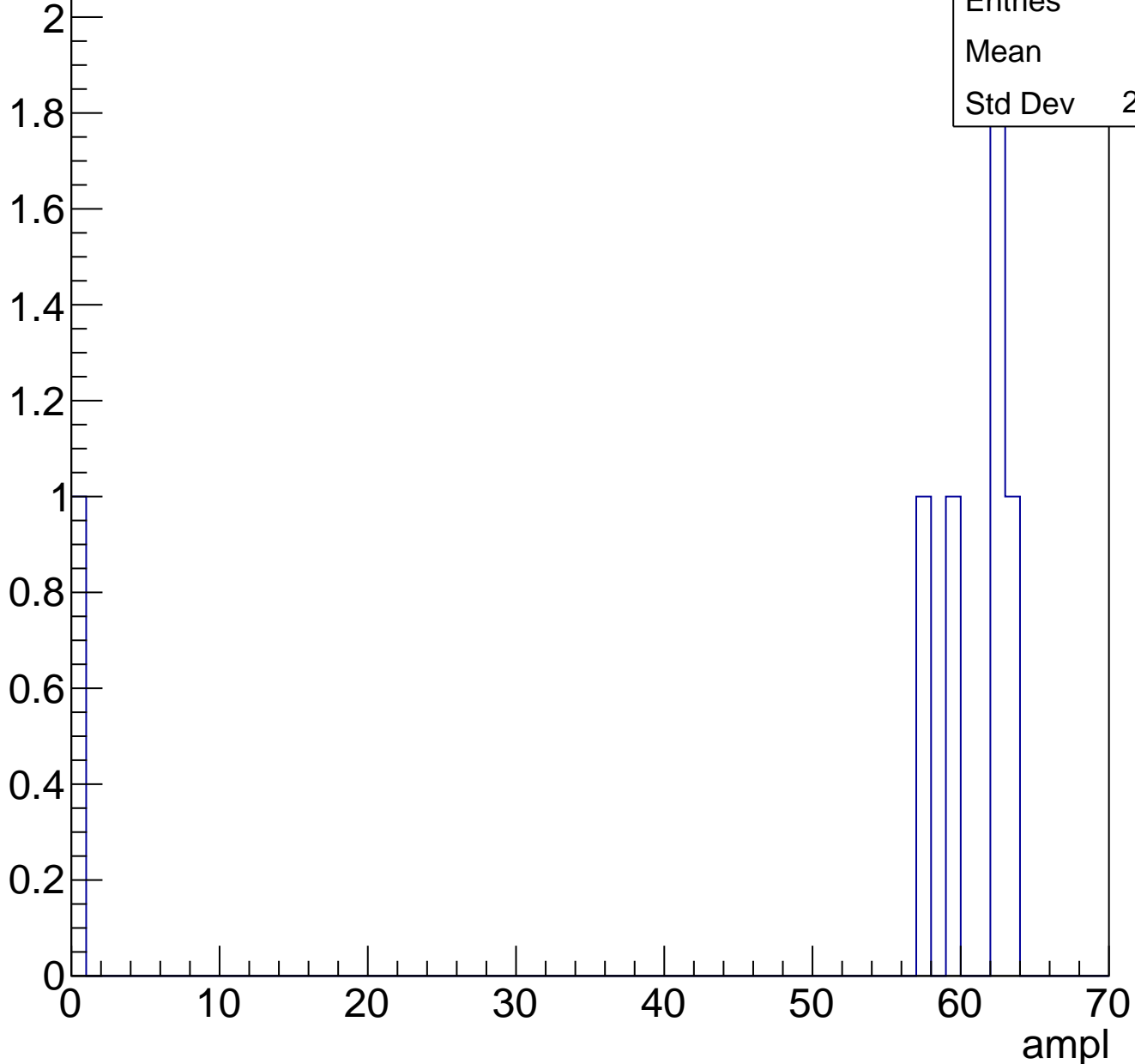
Entries	44
Mean	60.41
Std Dev	2.329



B0L002S, U17-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	6
Mean	50.5
Std Dev	22.68

B0L002S, U17-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U17-ch38, adc0

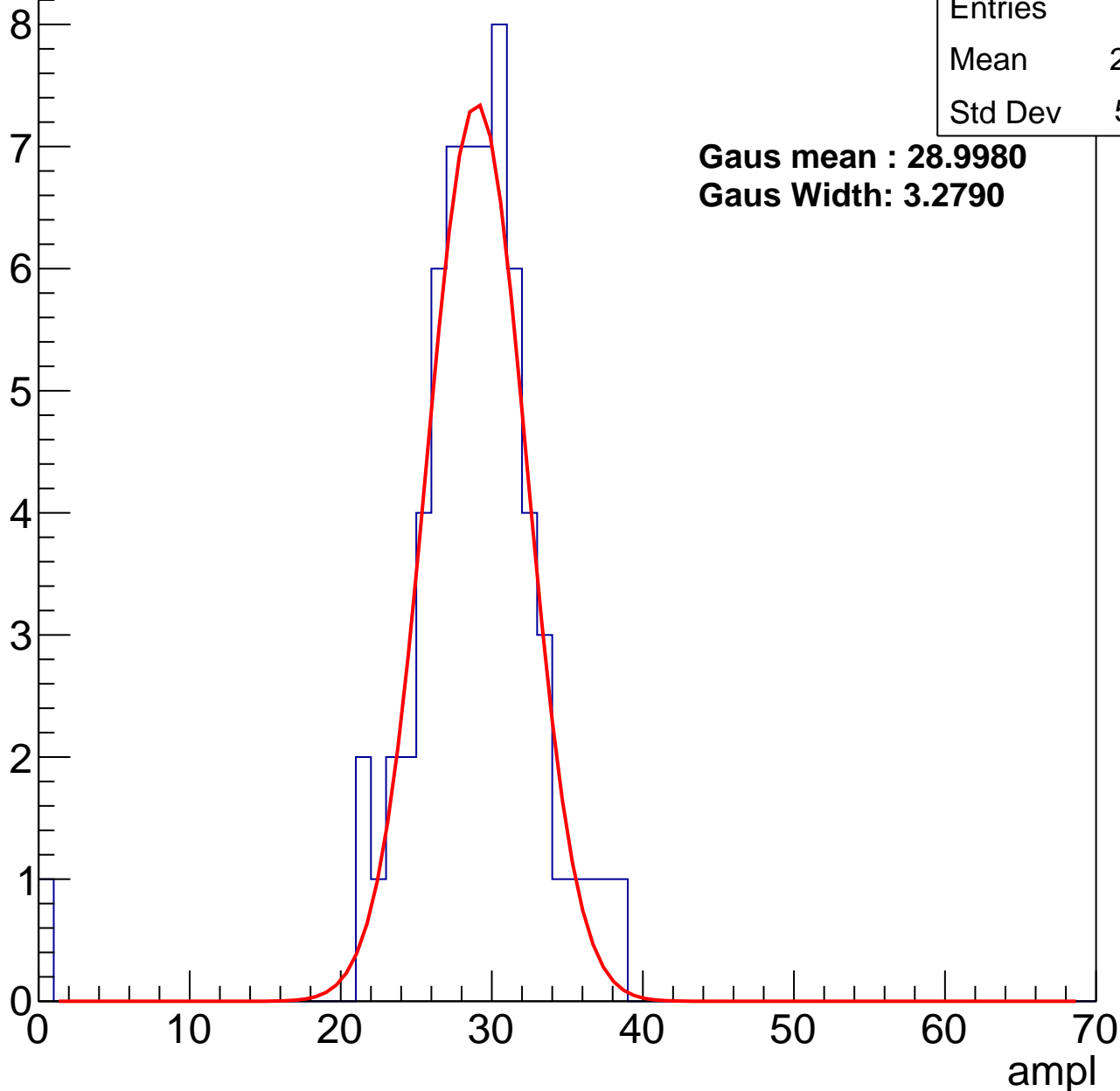
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	28.23
Std Dev	5.001

Gaus mean : 28.9980

Gaus Width: 3.2790



B0L002S, U17-ch38, adc1

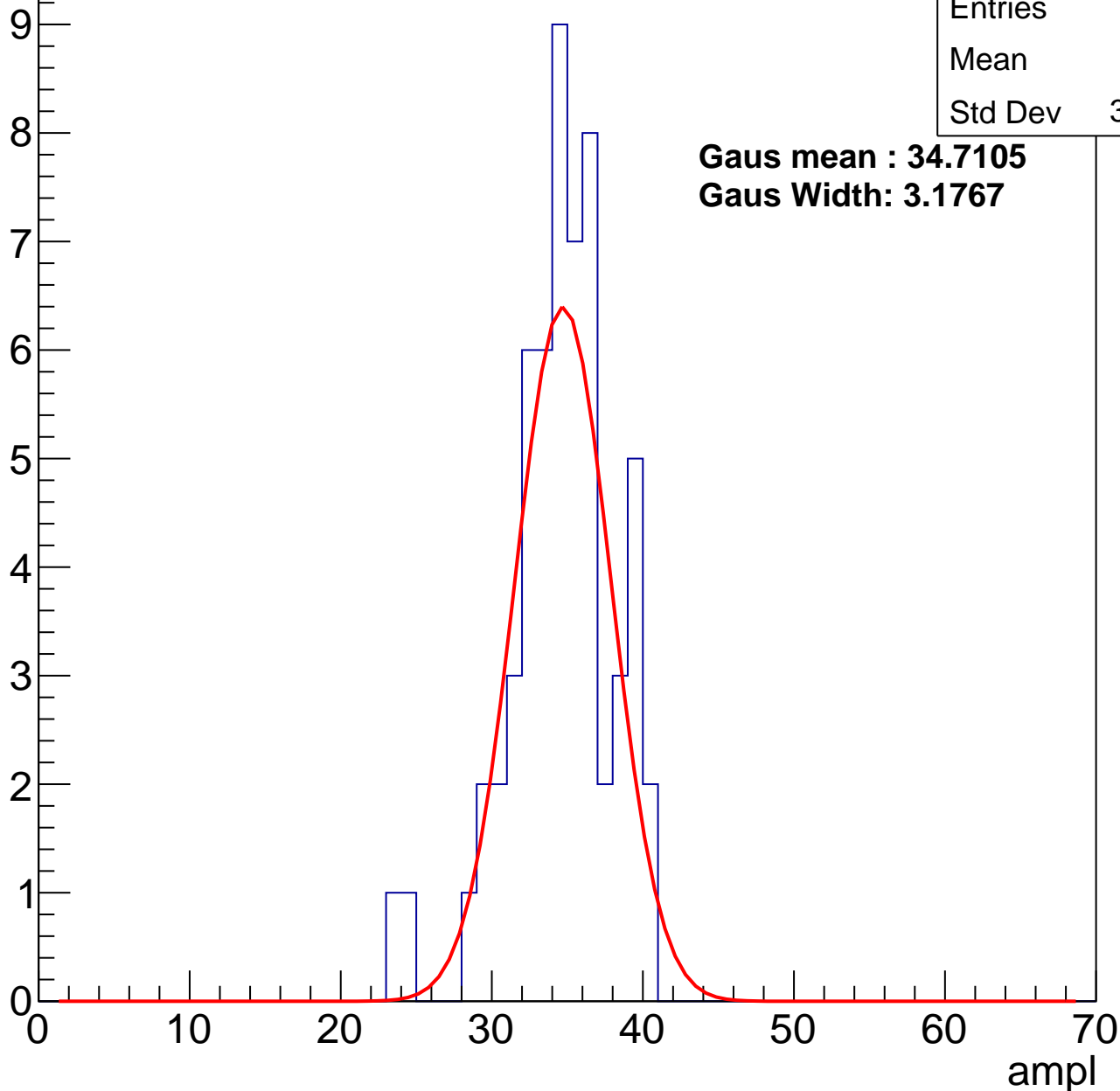
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	34.1
Std Dev	3.468

Gaus mean : 34.7105

Gaus Width: 3.1767



B0L002S, U17-ch38, adc2

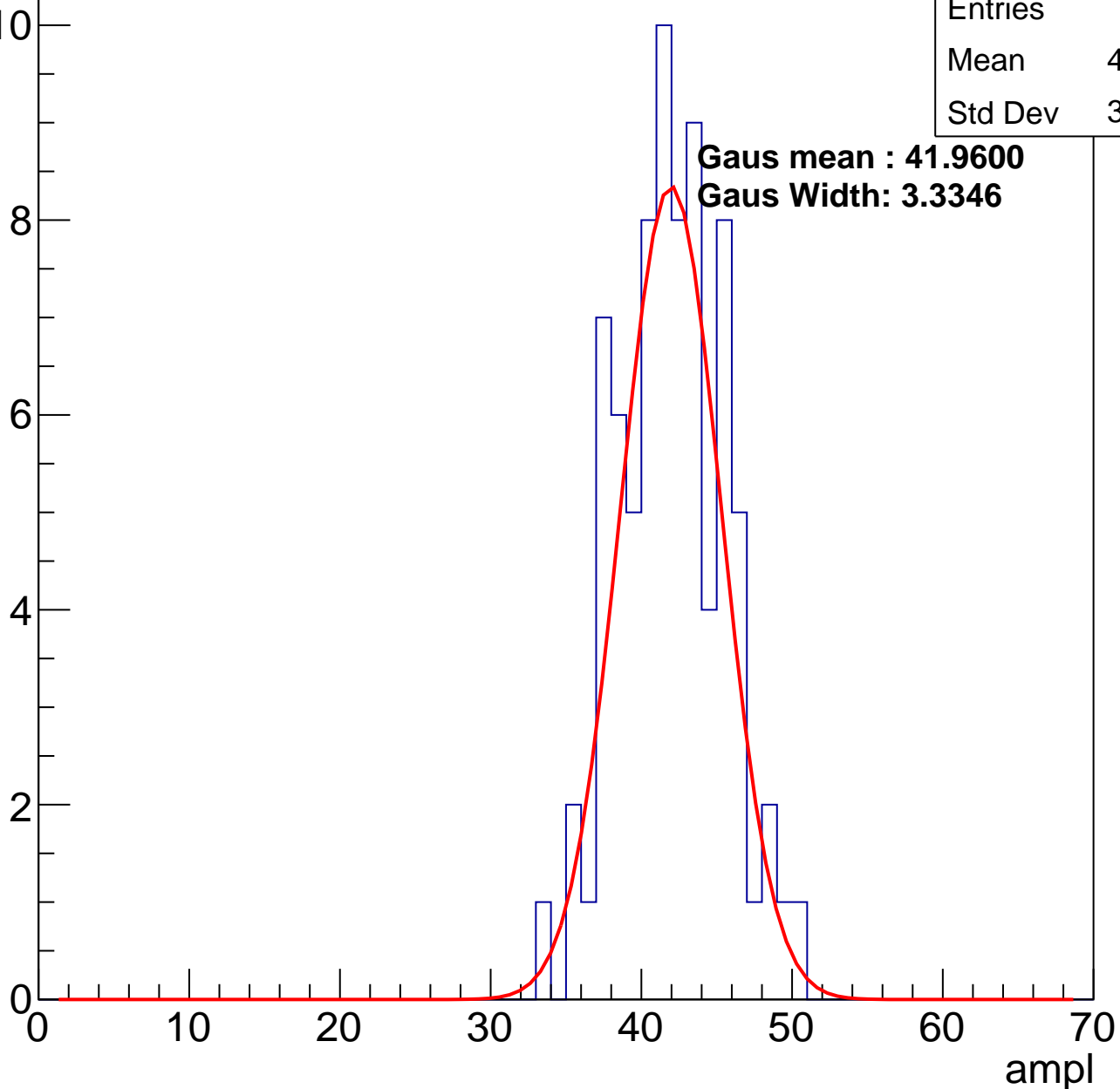
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	41.54
Std Dev	3.456

Gaus mean : 41.9600

Gaus Width: 3.3346

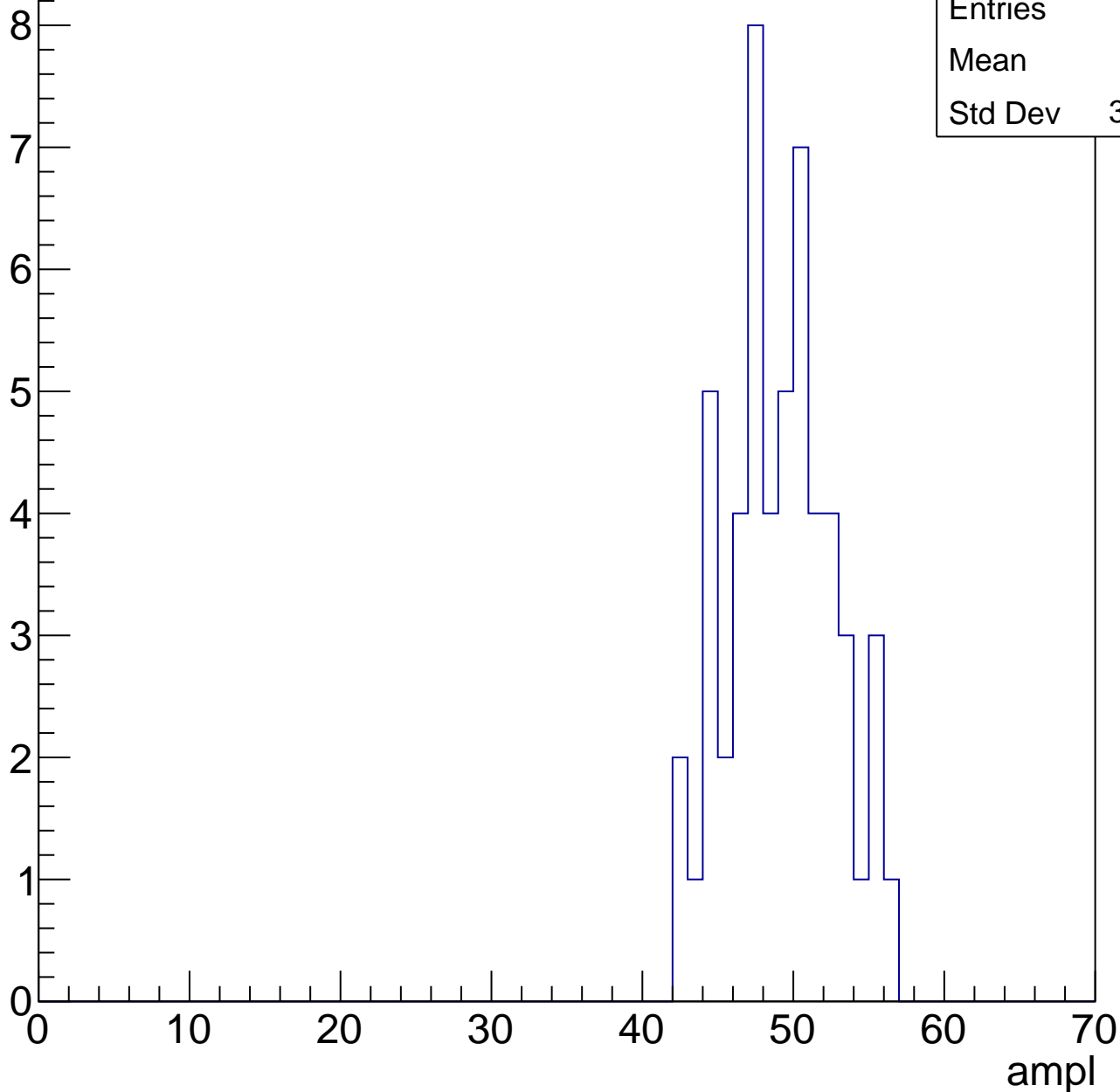


B0L002S, U17-ch38, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	48.7
Std Dev	3.462

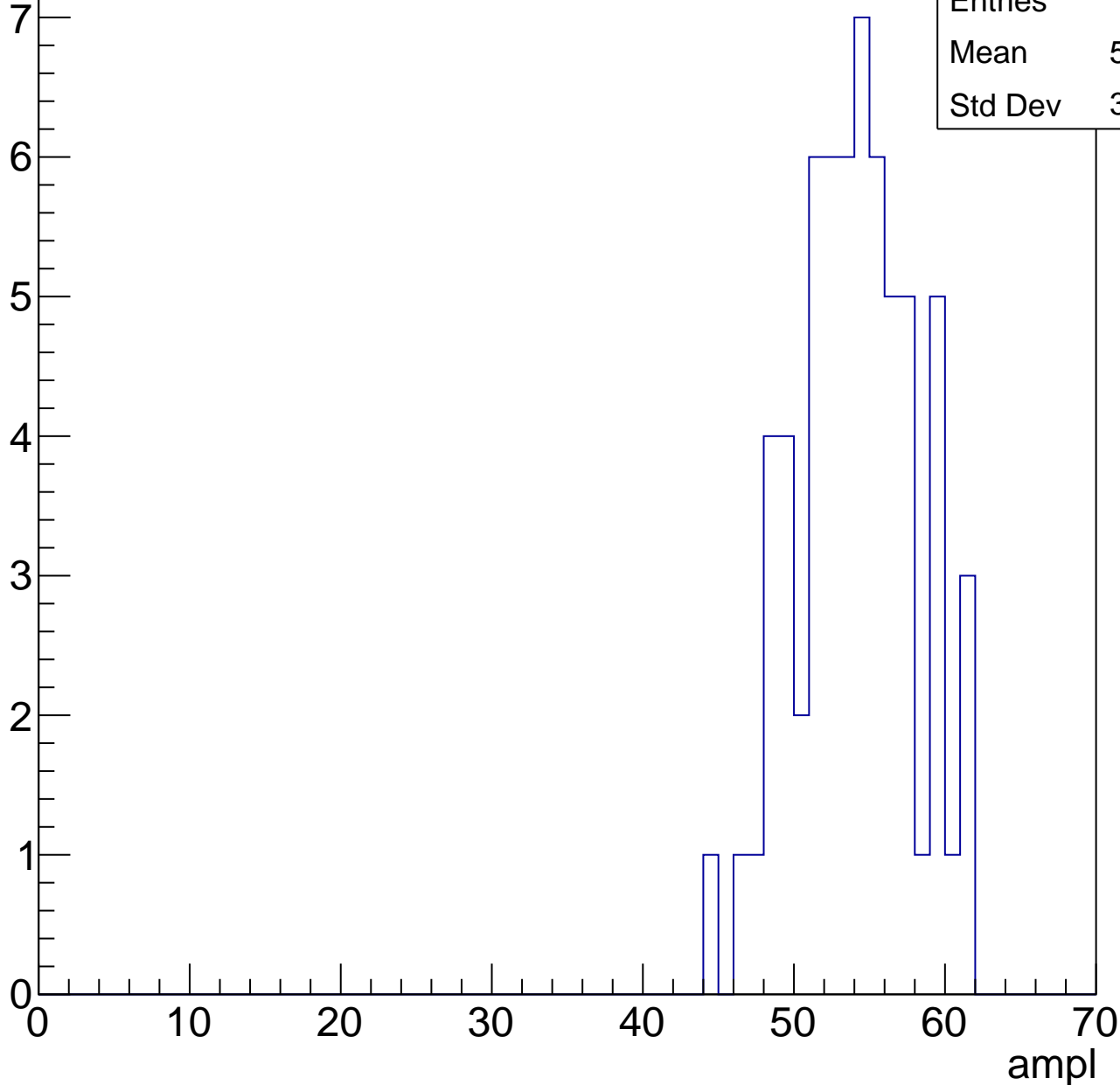


B0L002S, U17-ch38, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

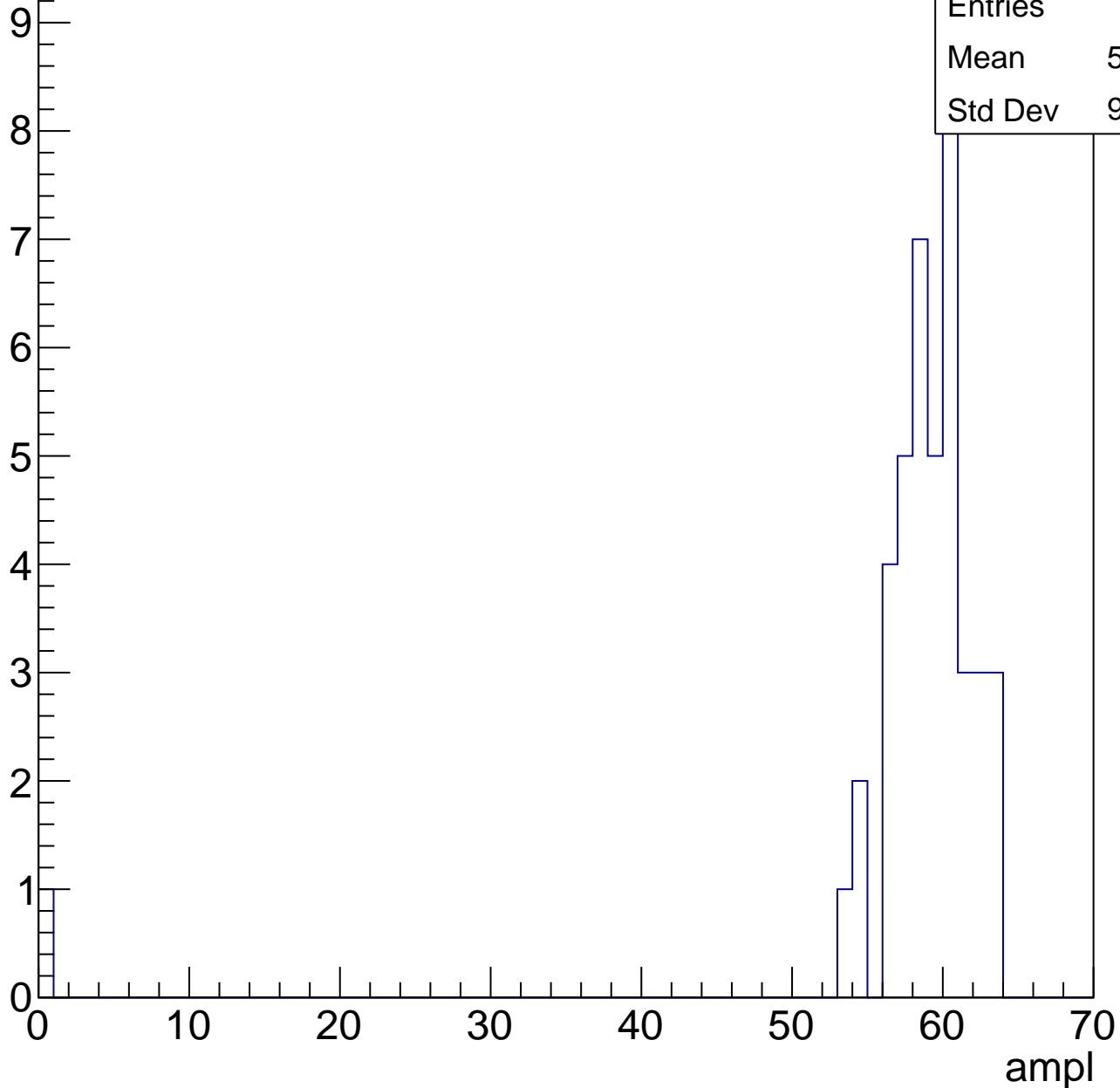
Entries	64
Mean	53.59
Std Dev	3.868



B0L002S, U17-ch38, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

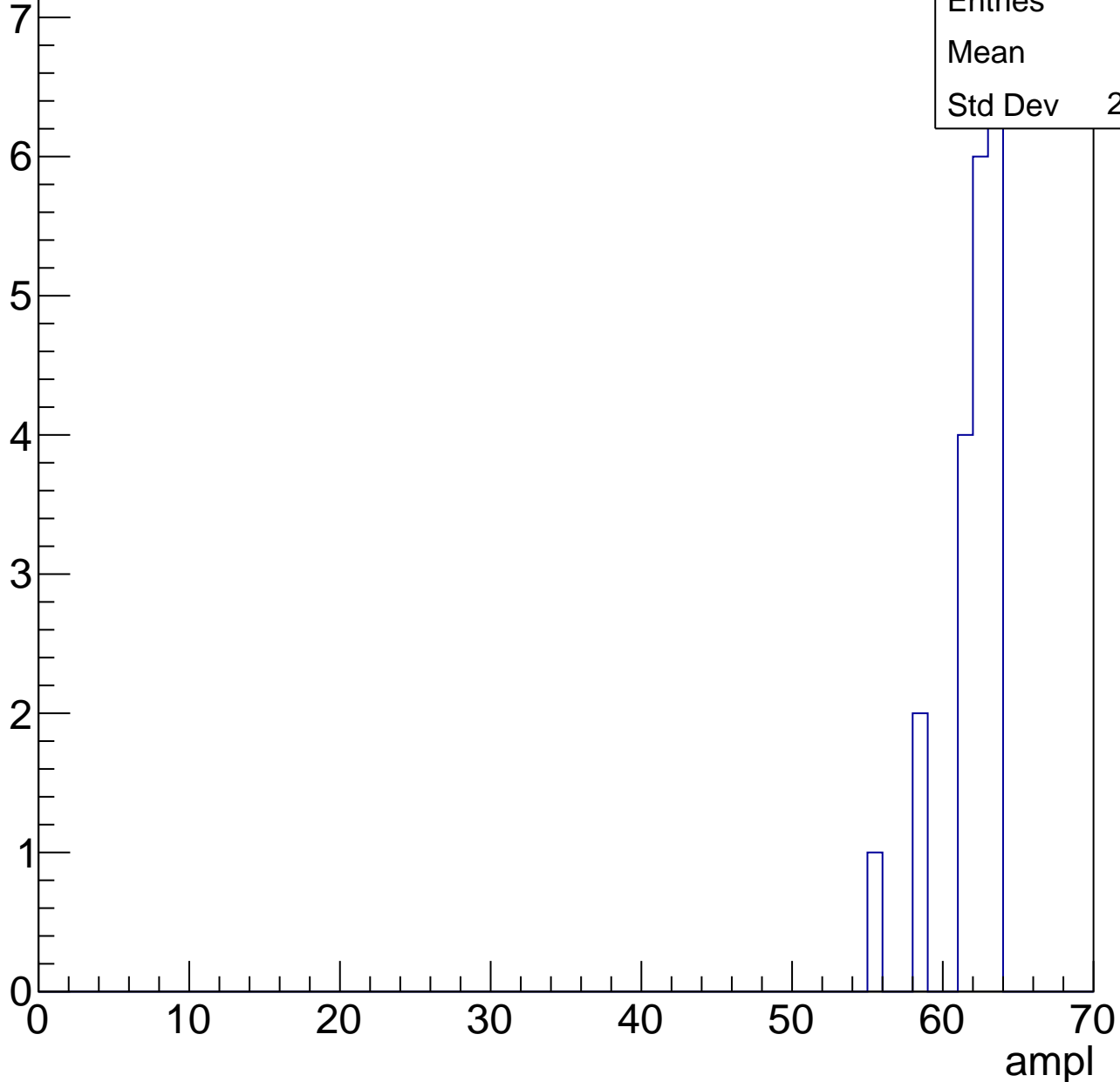


B0L002S, U17-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

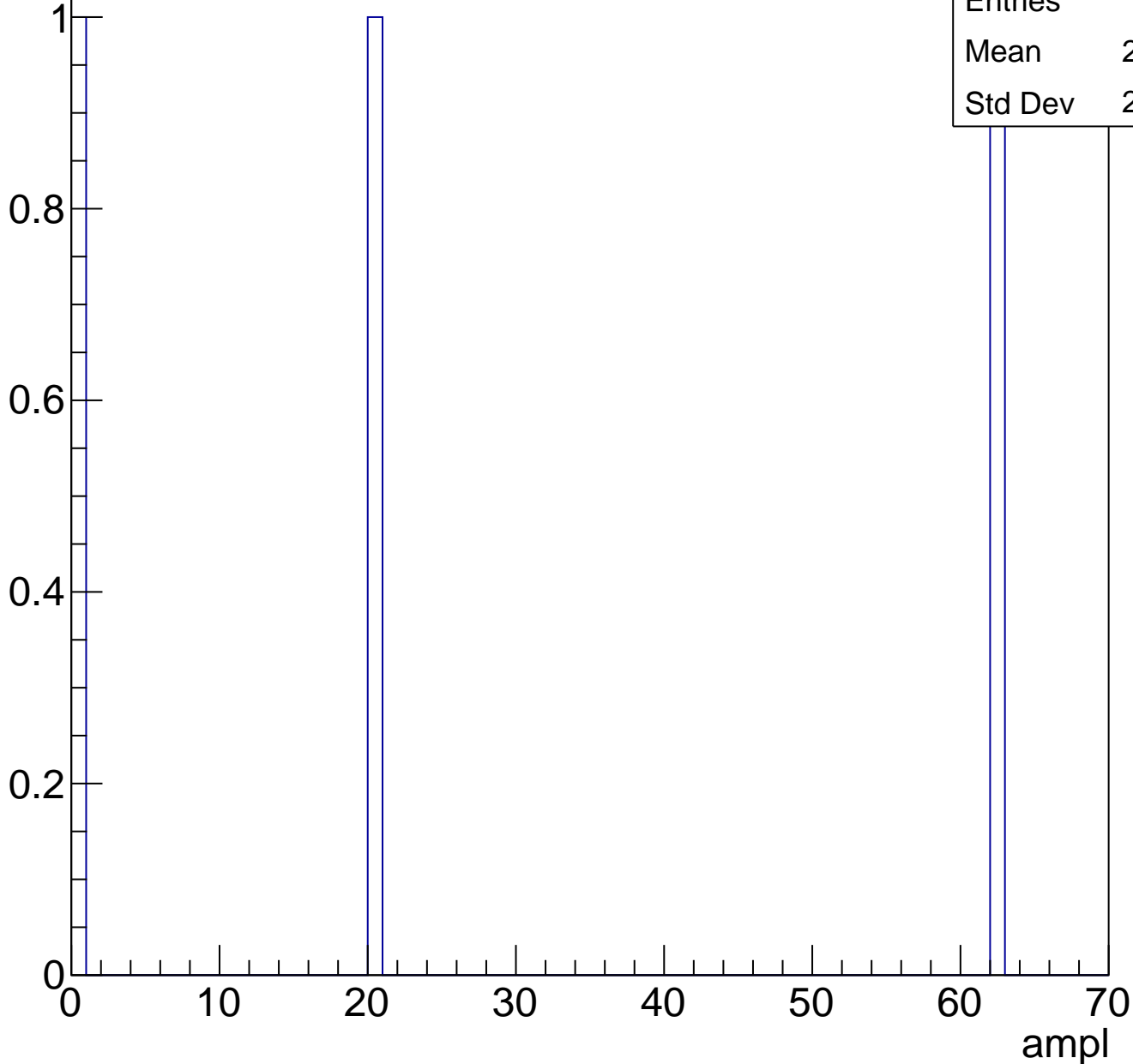
Entries	20
Mean	61.4
Std Dev	2.059



B0L002S, U17-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	27.33
Std Dev	25.84

B0L002S, U17-ch39, adc0

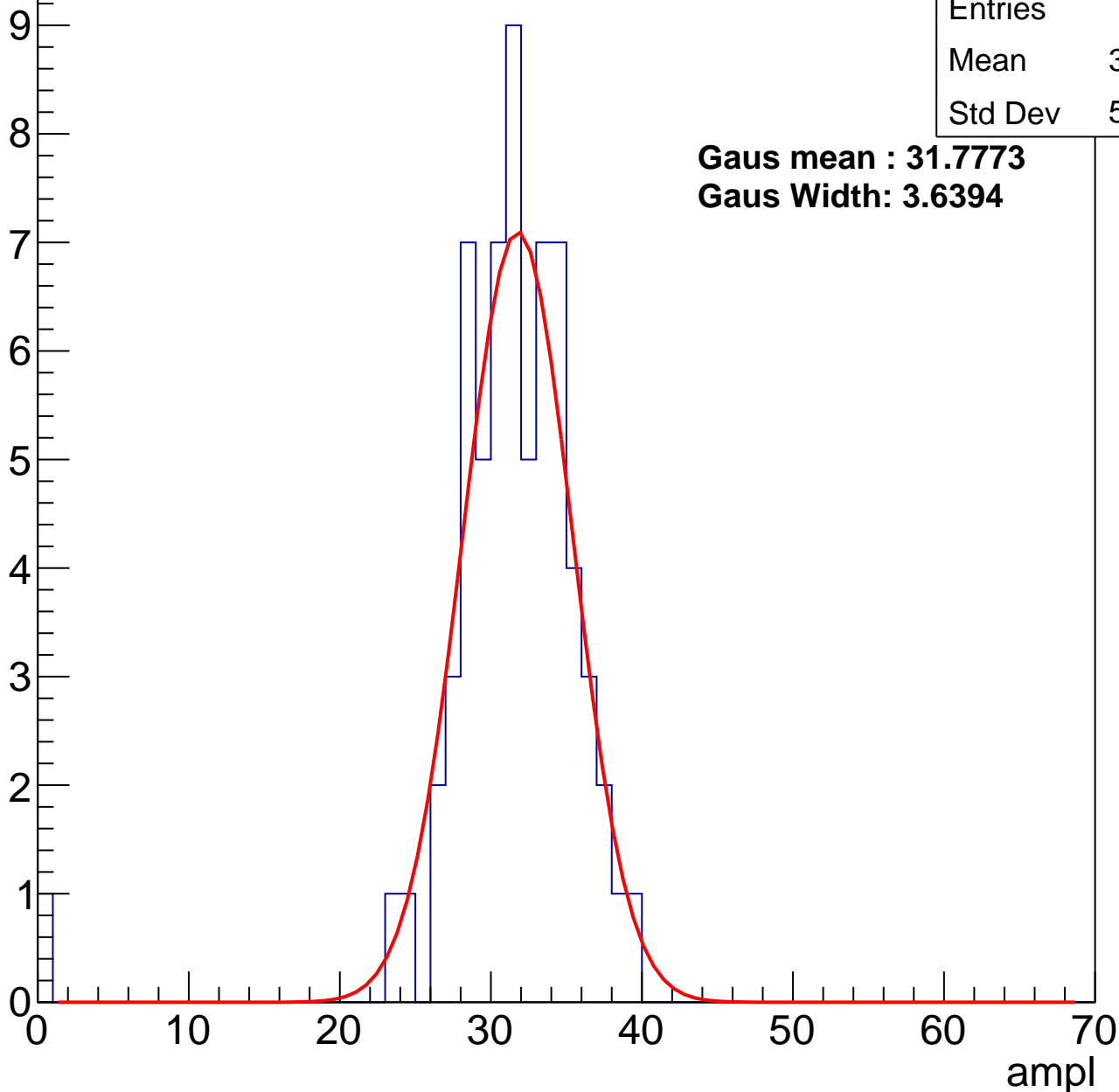
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.88
Std Dev	5.047

Gaus mean : 31.7773

Gaus Width: 3.6394



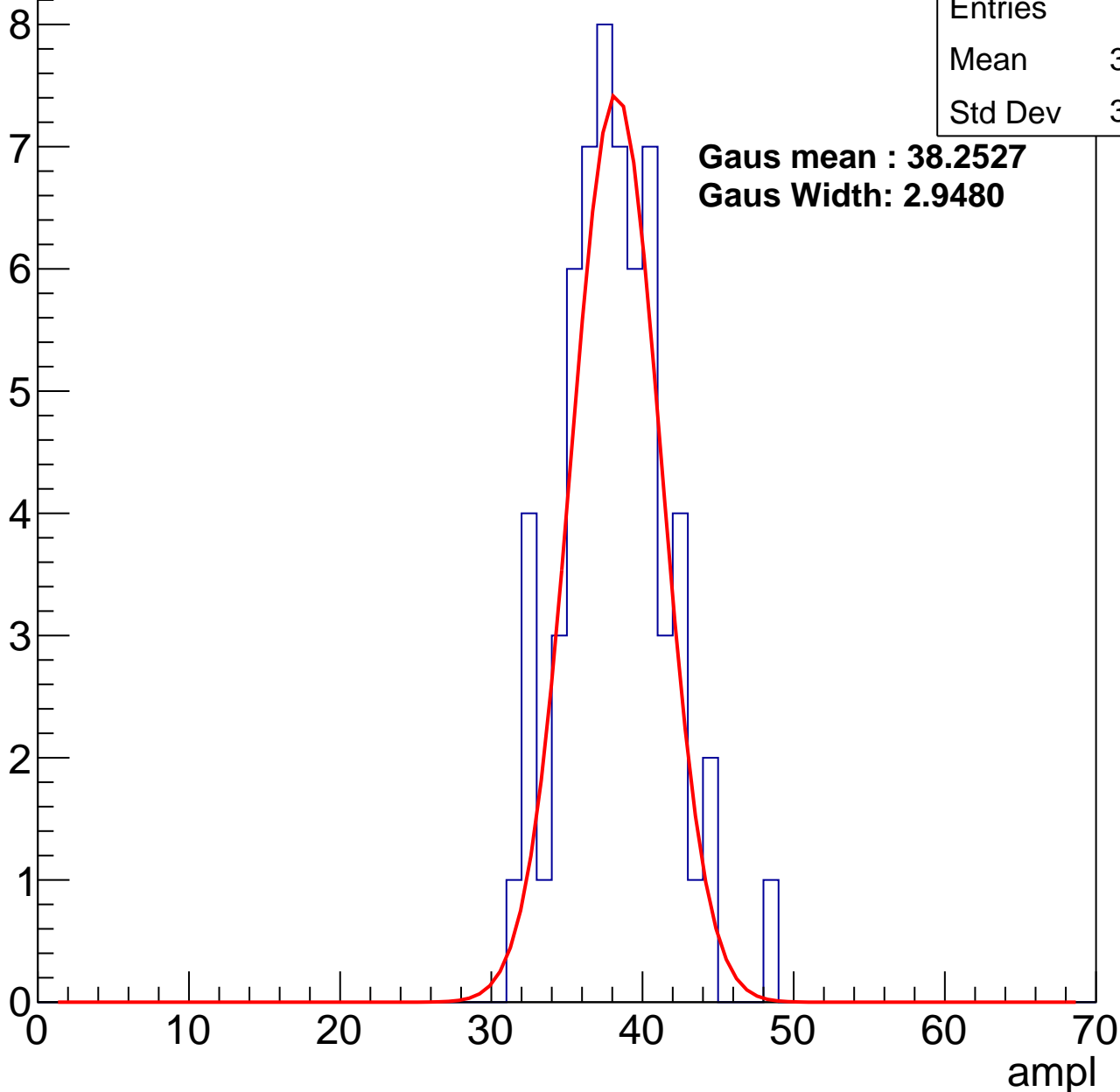
B0L002S, U17-ch39, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.74
Std Dev	3.324

Gaus mean : 38.2527
Gaus Width: 2.9480



B0L002S, U17-ch39, adc2

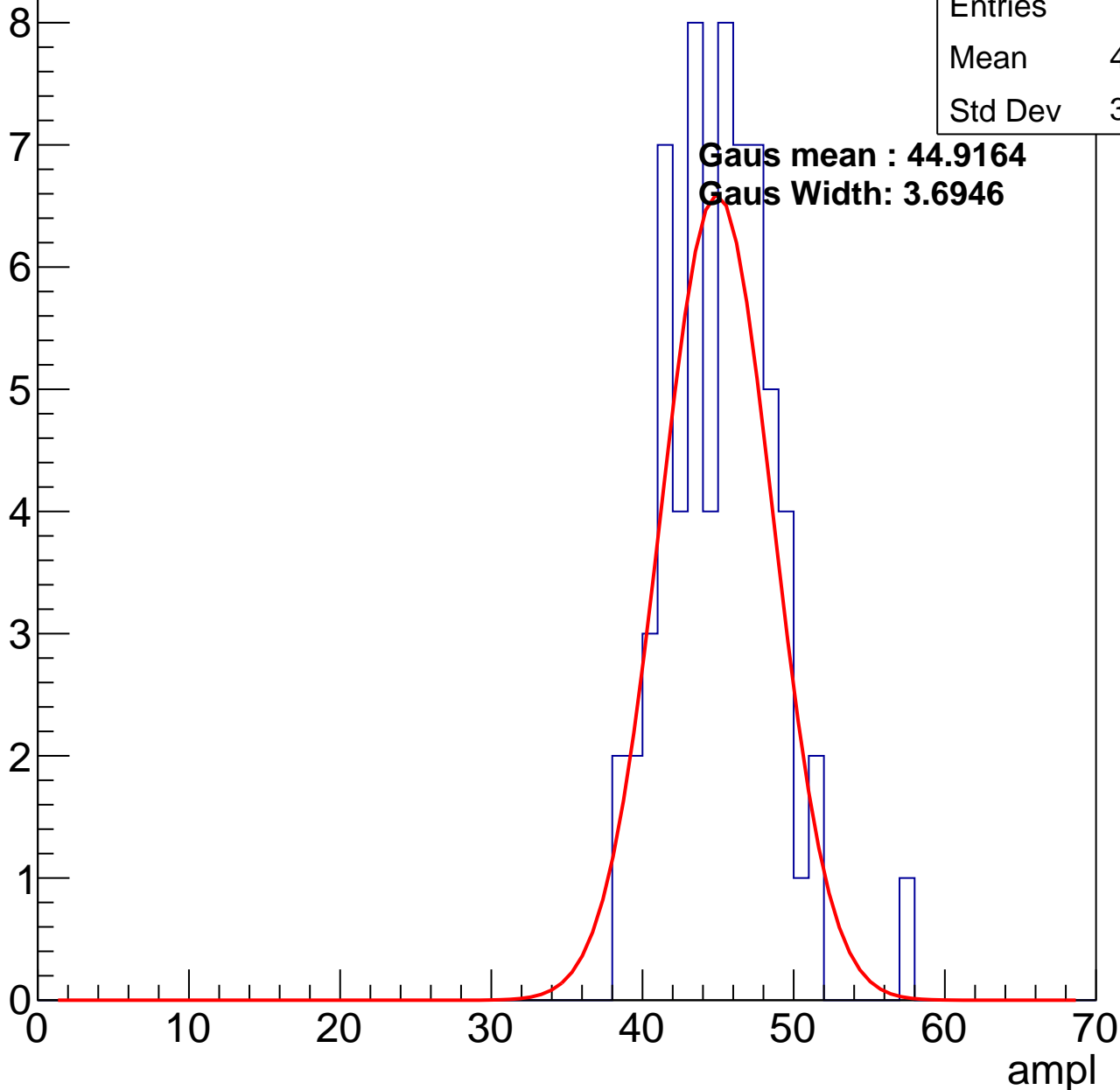
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.69
Std Dev	3.525

Gaus mean : 44.9164

Gaus Width: 3.6946

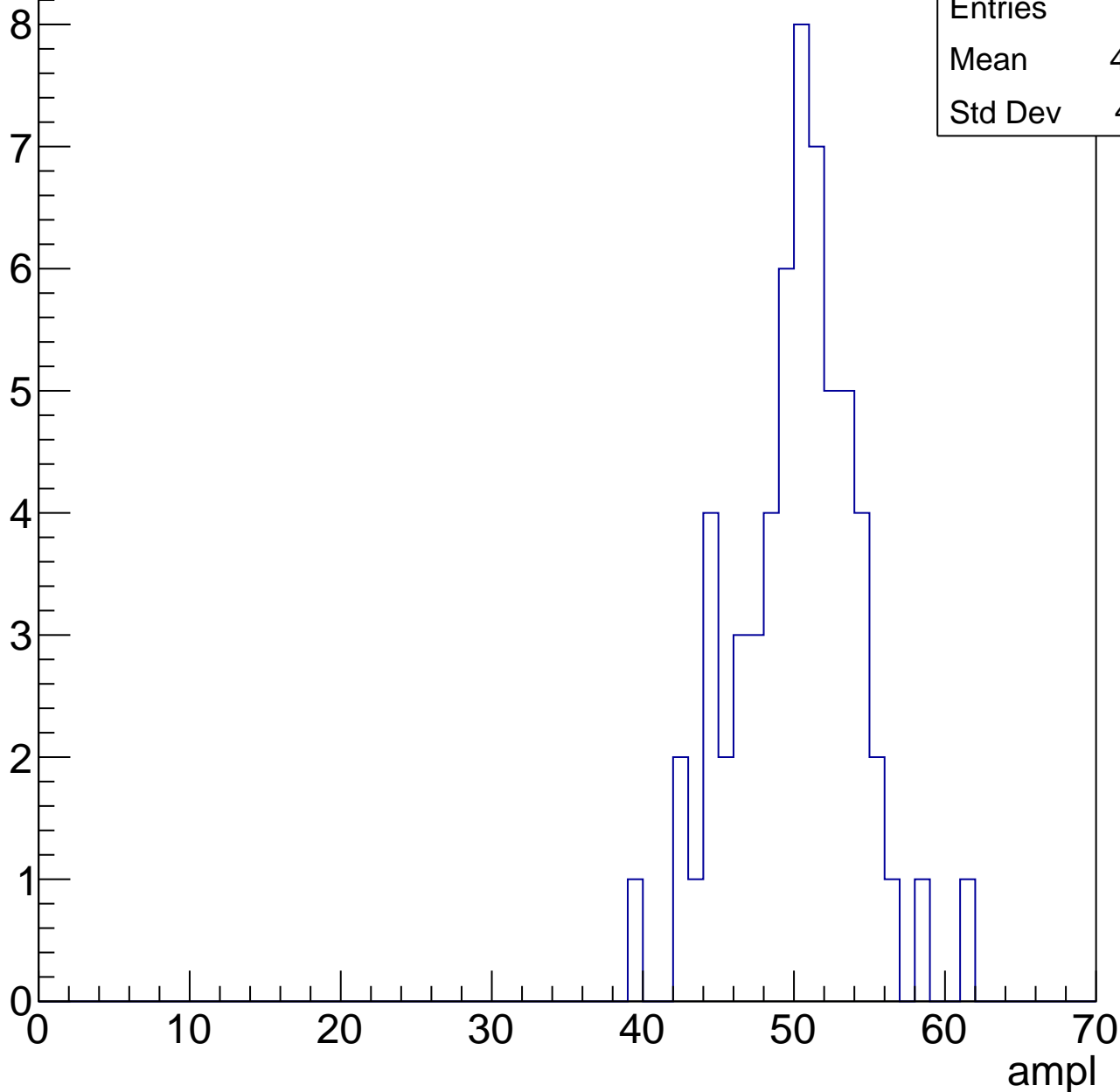


B0L002S, U17-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

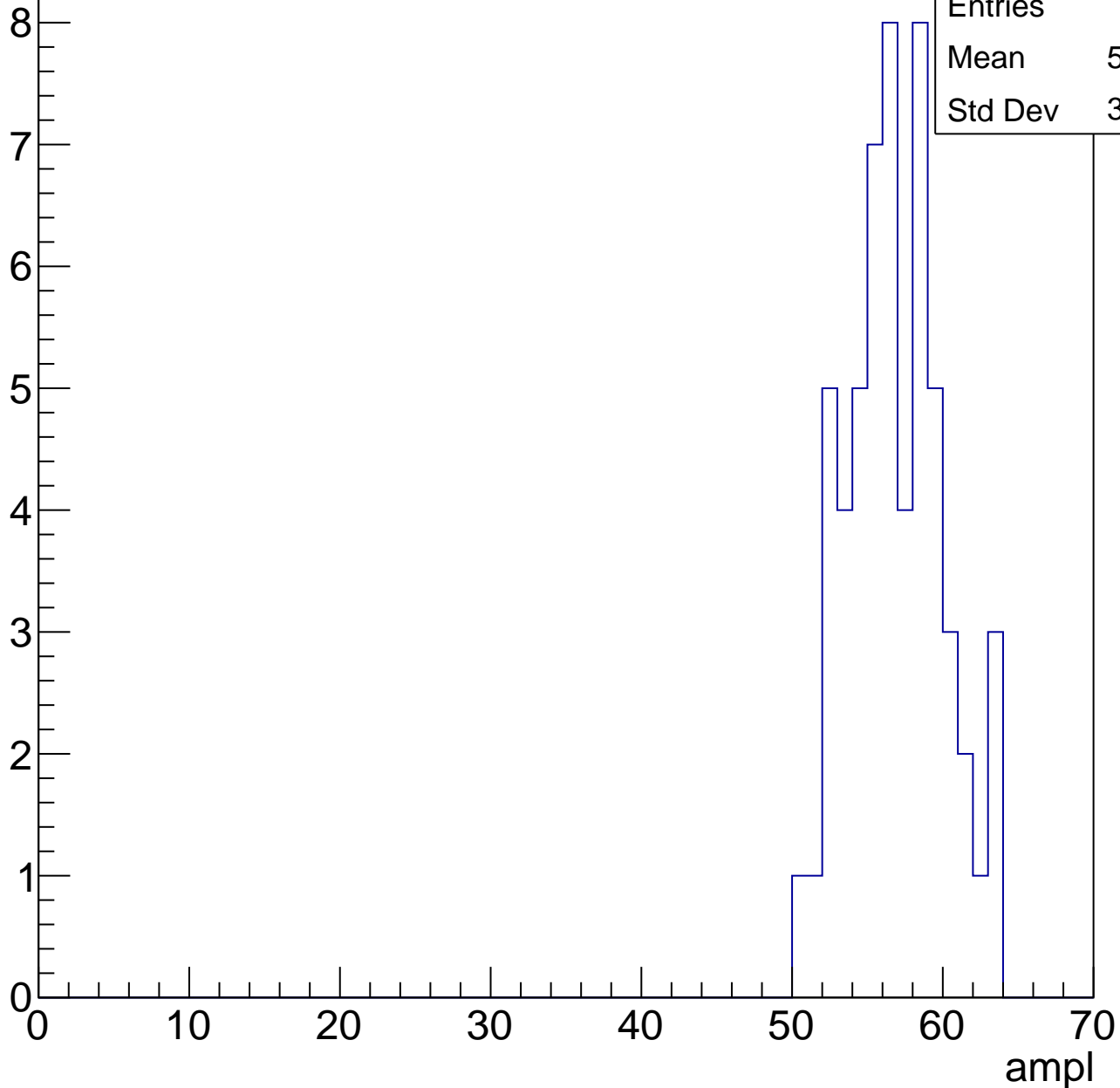
Entries	60
Mean	49.67
Std Dev	4.061



B0L002S, U17-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



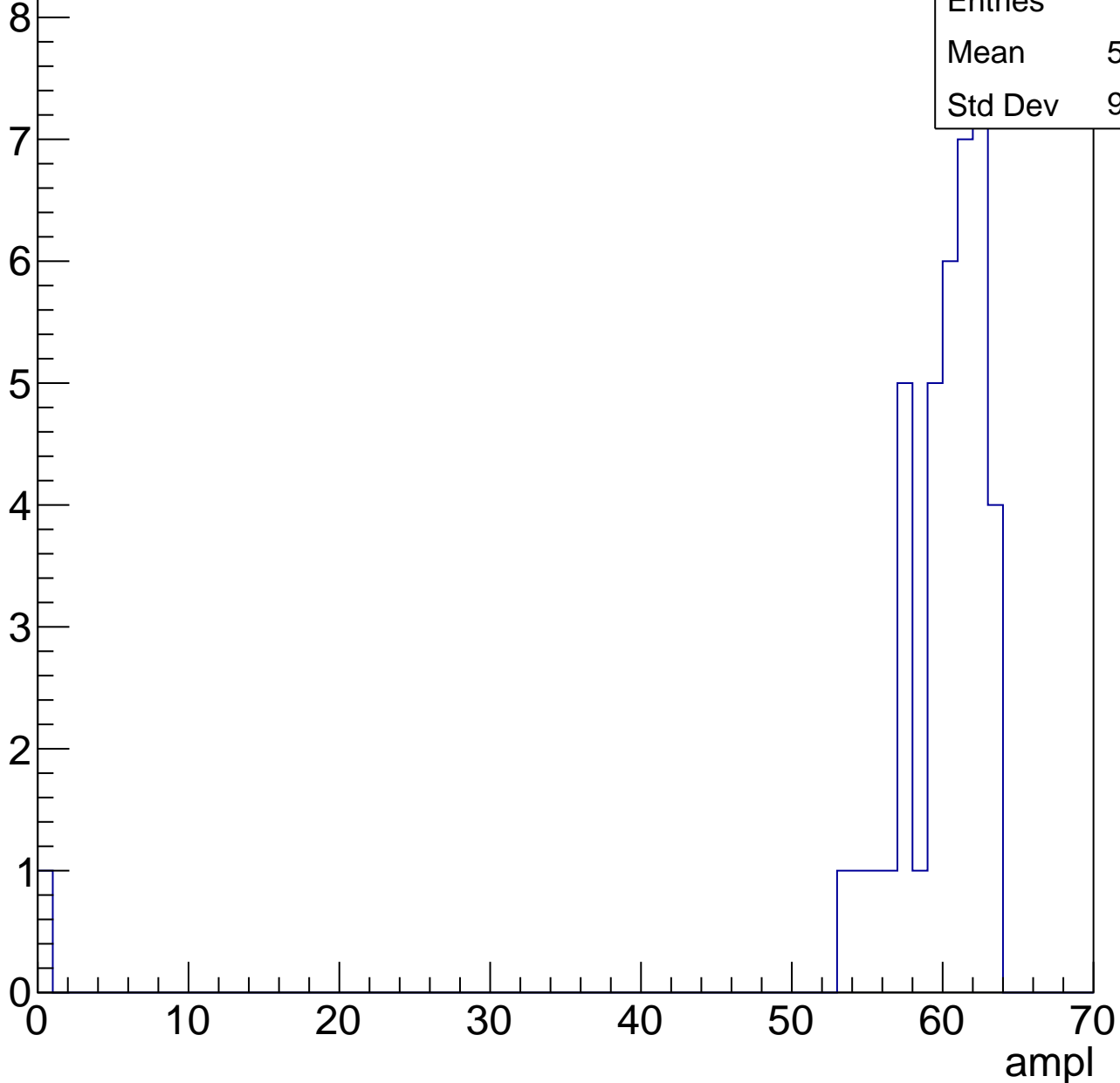
Entries	57
Mean	56.42
Std Dev	3.123

B0L002S, U17-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.32
Std Dev	9.549



B0L002S, U17-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch40, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	29.1
Std Dev	3.317

Gaus mean : 29.2044

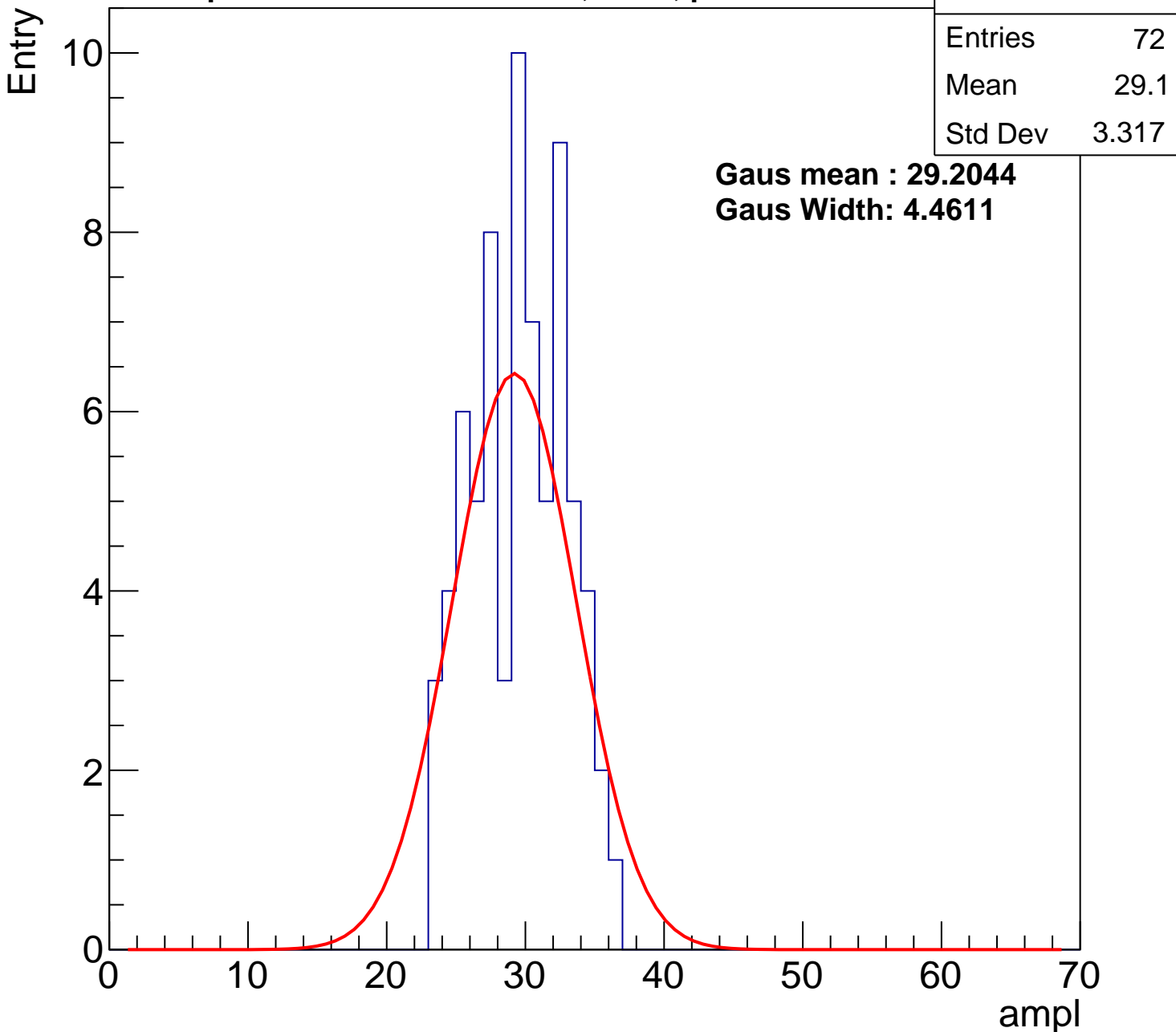
Gaus Width: 4.4611

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch40, adc1

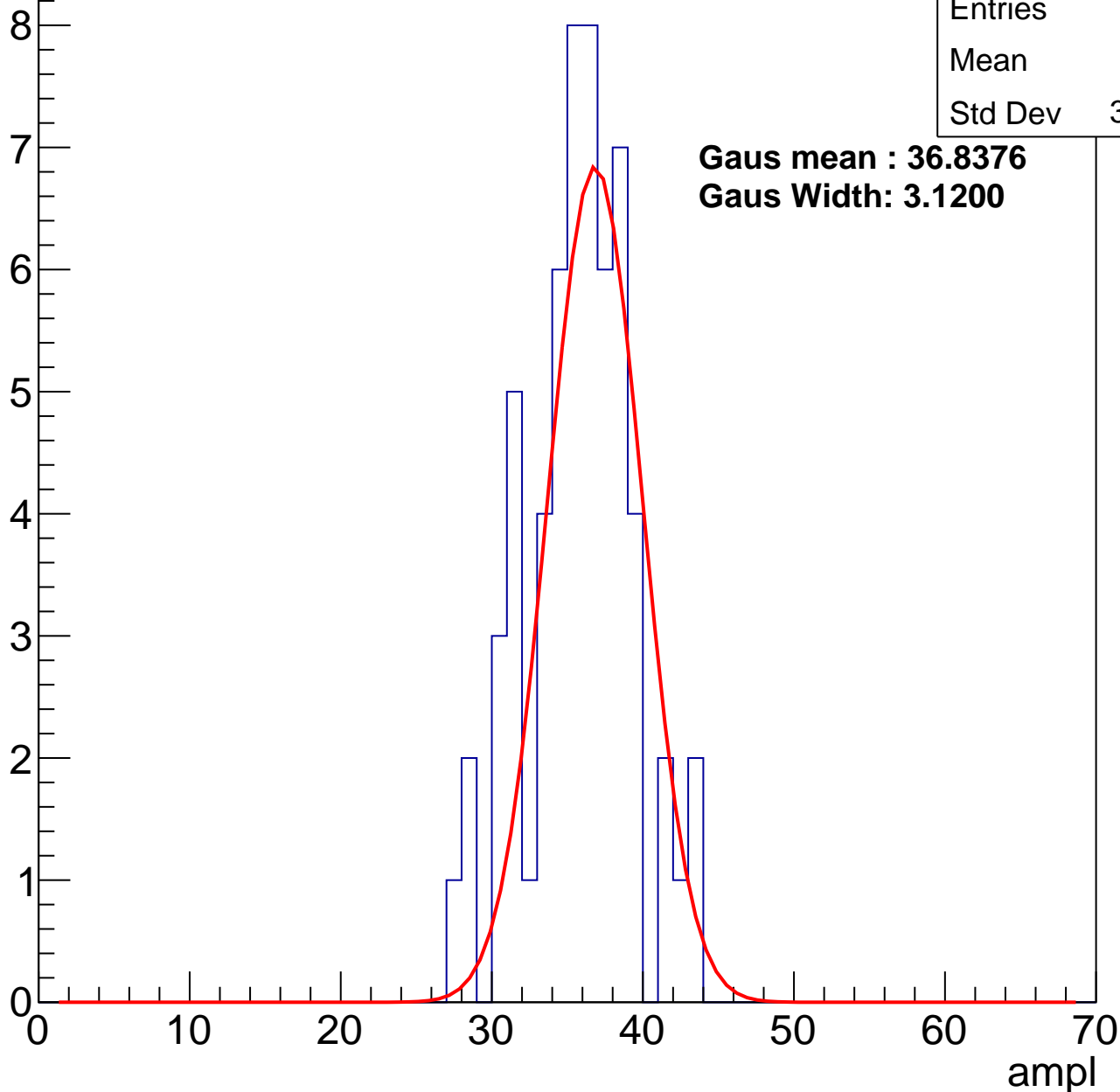
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	35.3
Std Dev	3.537

Gaus mean : 36.8376

Gaus Width: 3.1200



B0L002S, U17-ch40, adc2

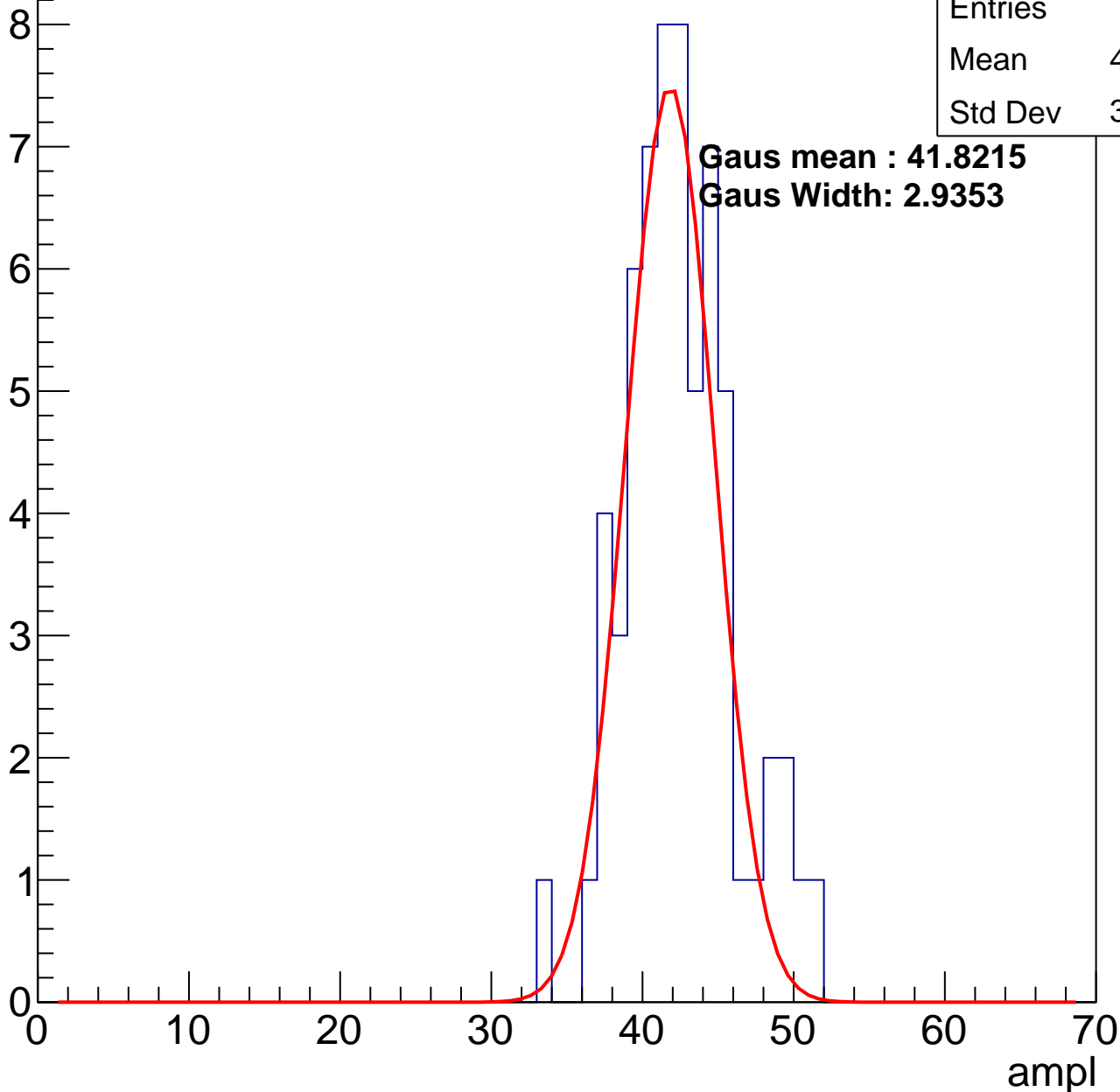
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	41.98
Std Dev	3.539

Gaus mean : 41.8215

Gaus Width: 2.9353

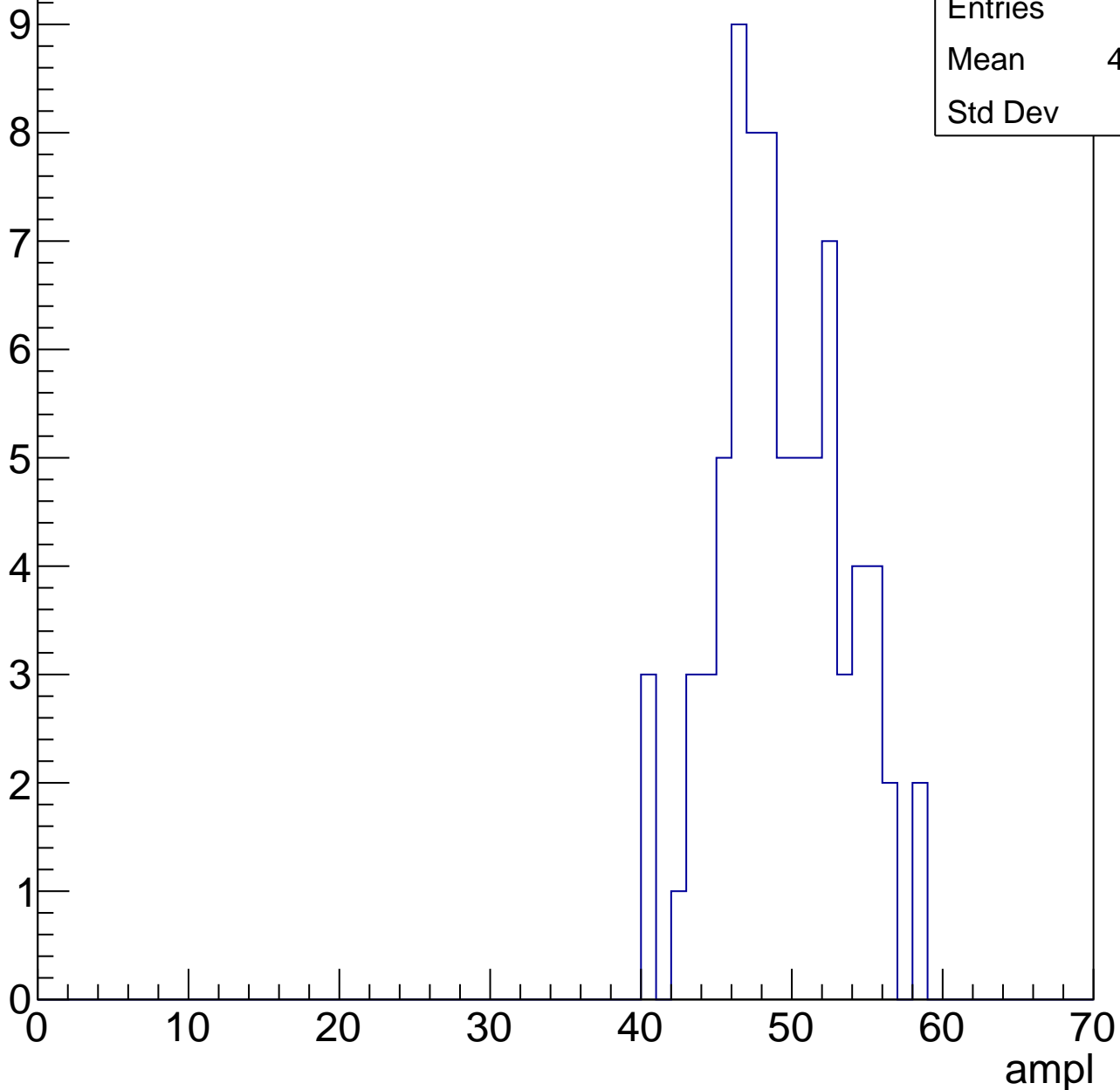


B0L002S, U17-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	48.82
Std Dev	4.13

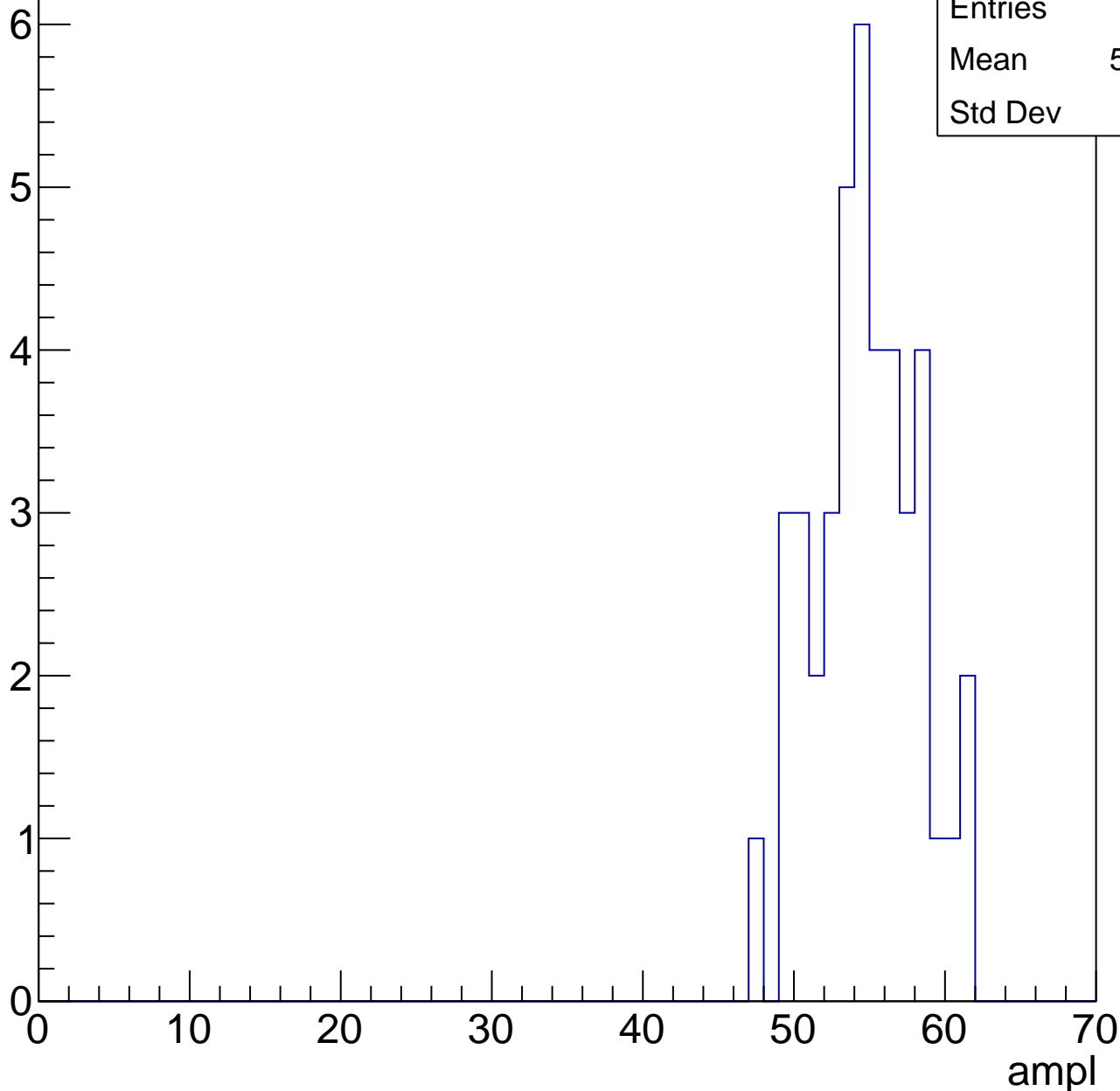


B0L002S, U17-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	54.26
Std Dev	3.36

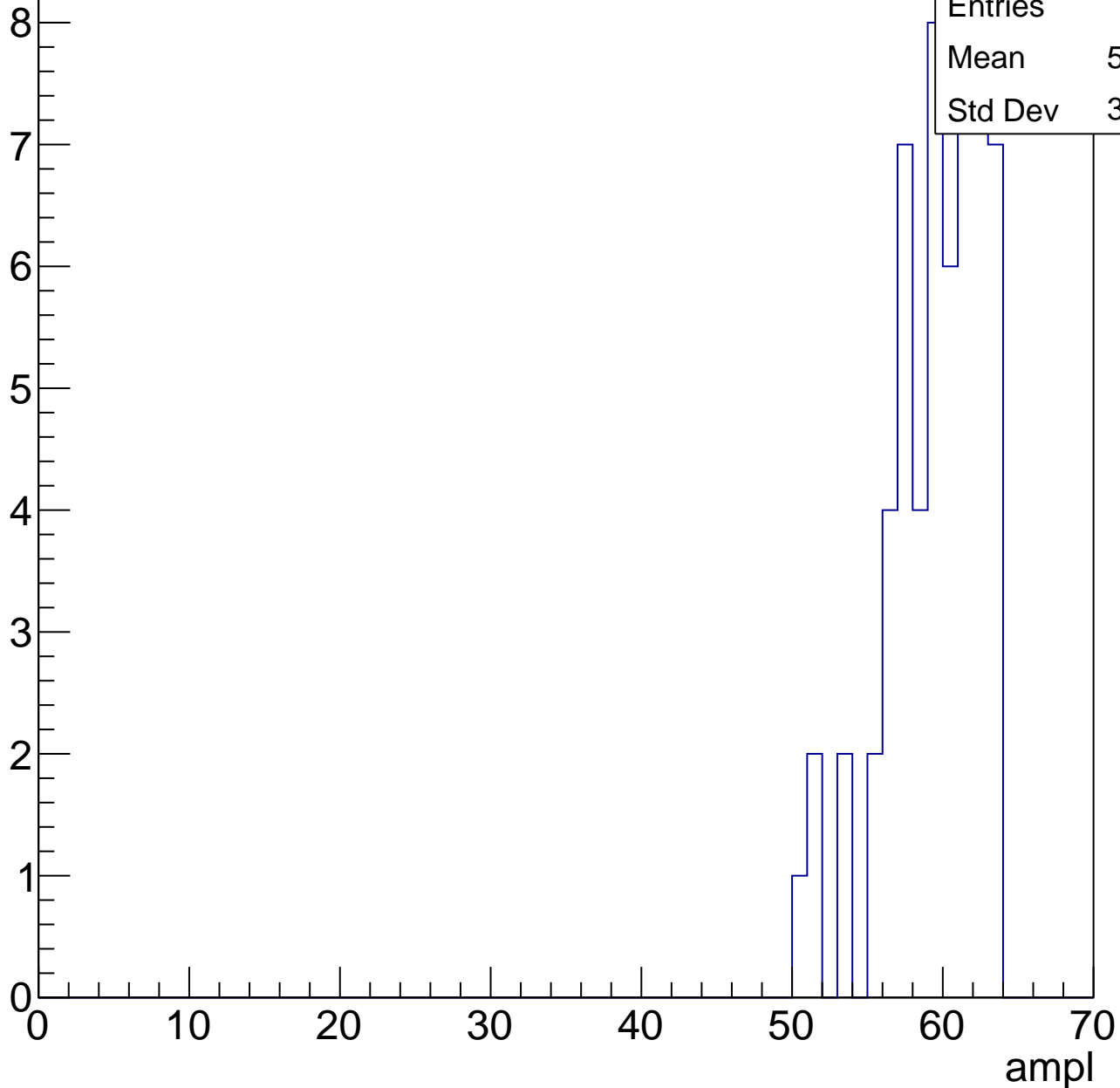


B0L002S, U17-ch40, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

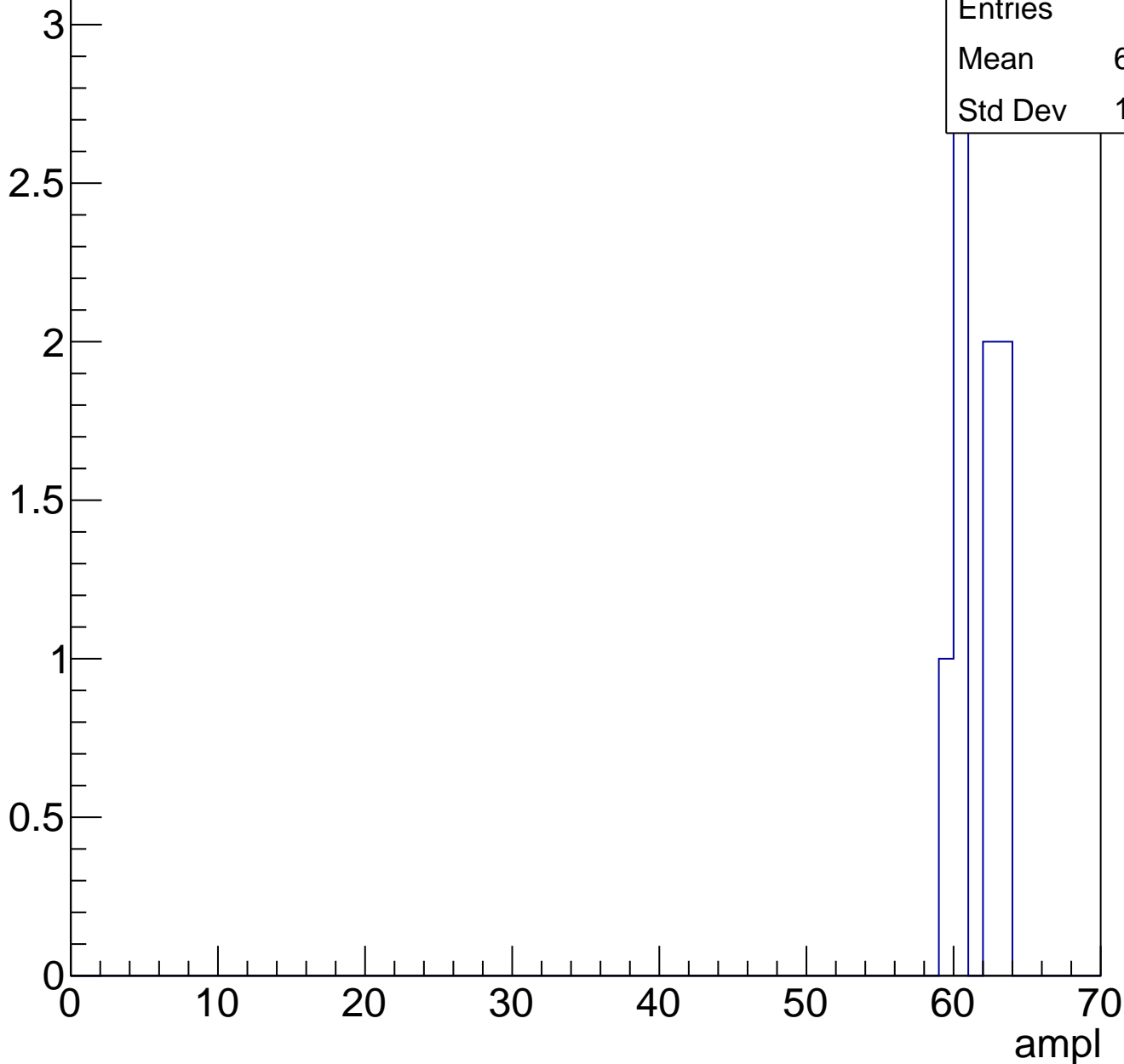
Entries	59
Mean	58.98
Std Dev	3.197



B0L002S, U17-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

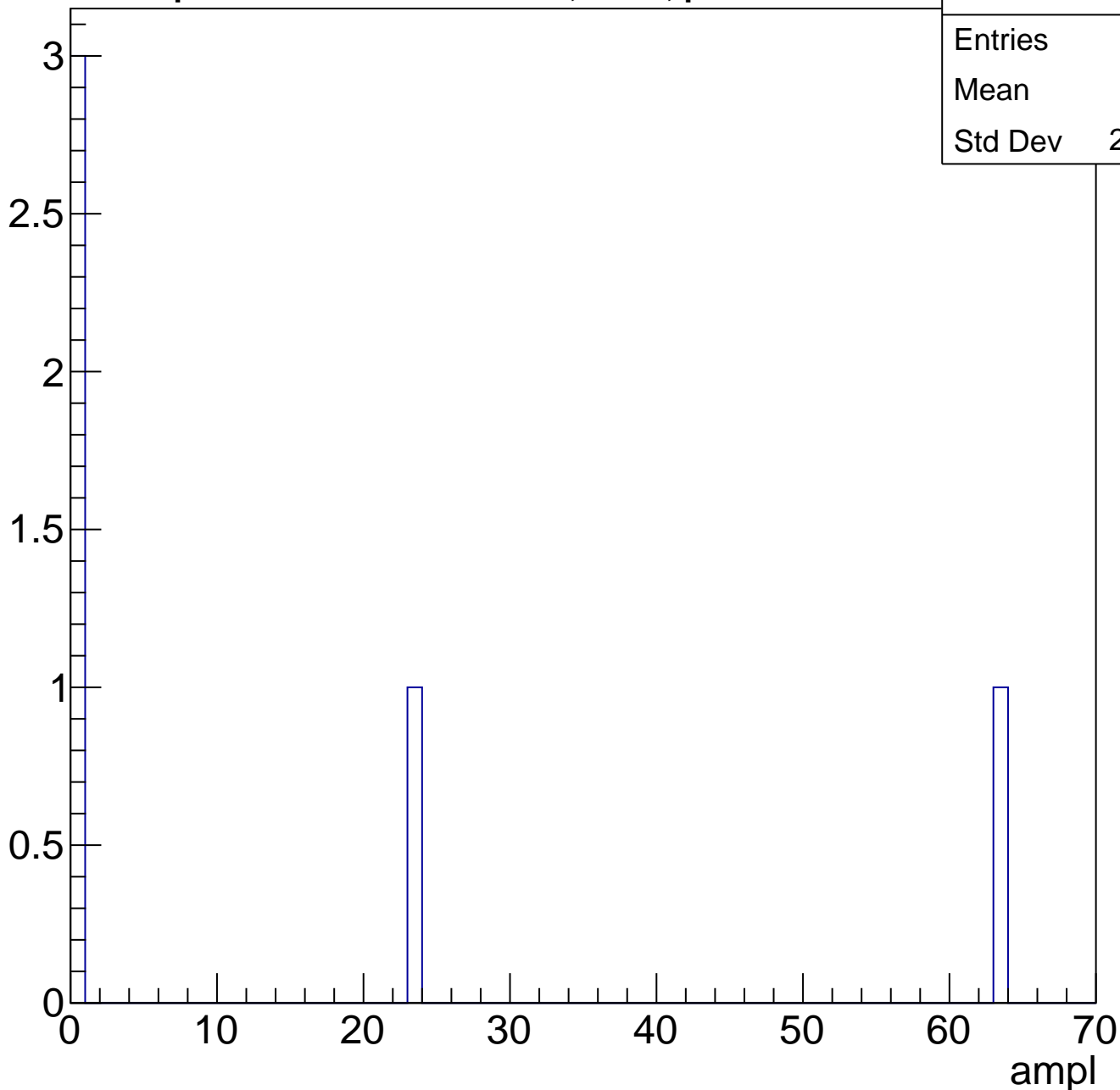
Entry



B0L002S, U17-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	17.2
Std Dev	24.57

B0L002S, U17-ch41, adc0

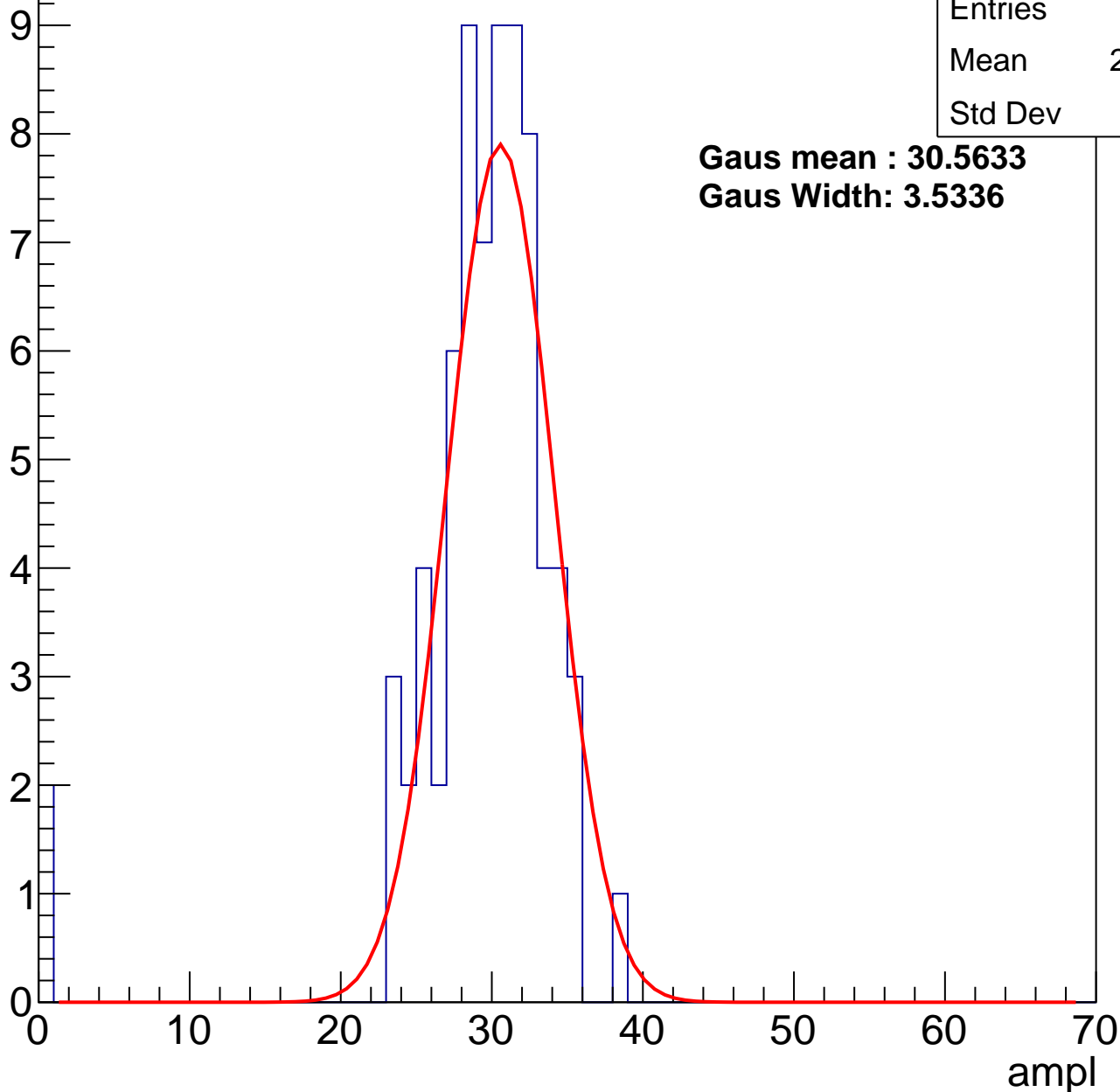
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	28.79
Std Dev	5.76

Gaus mean : 30.5633

Gaus Width: 3.5336



B0L002S, U17-ch41, adc1

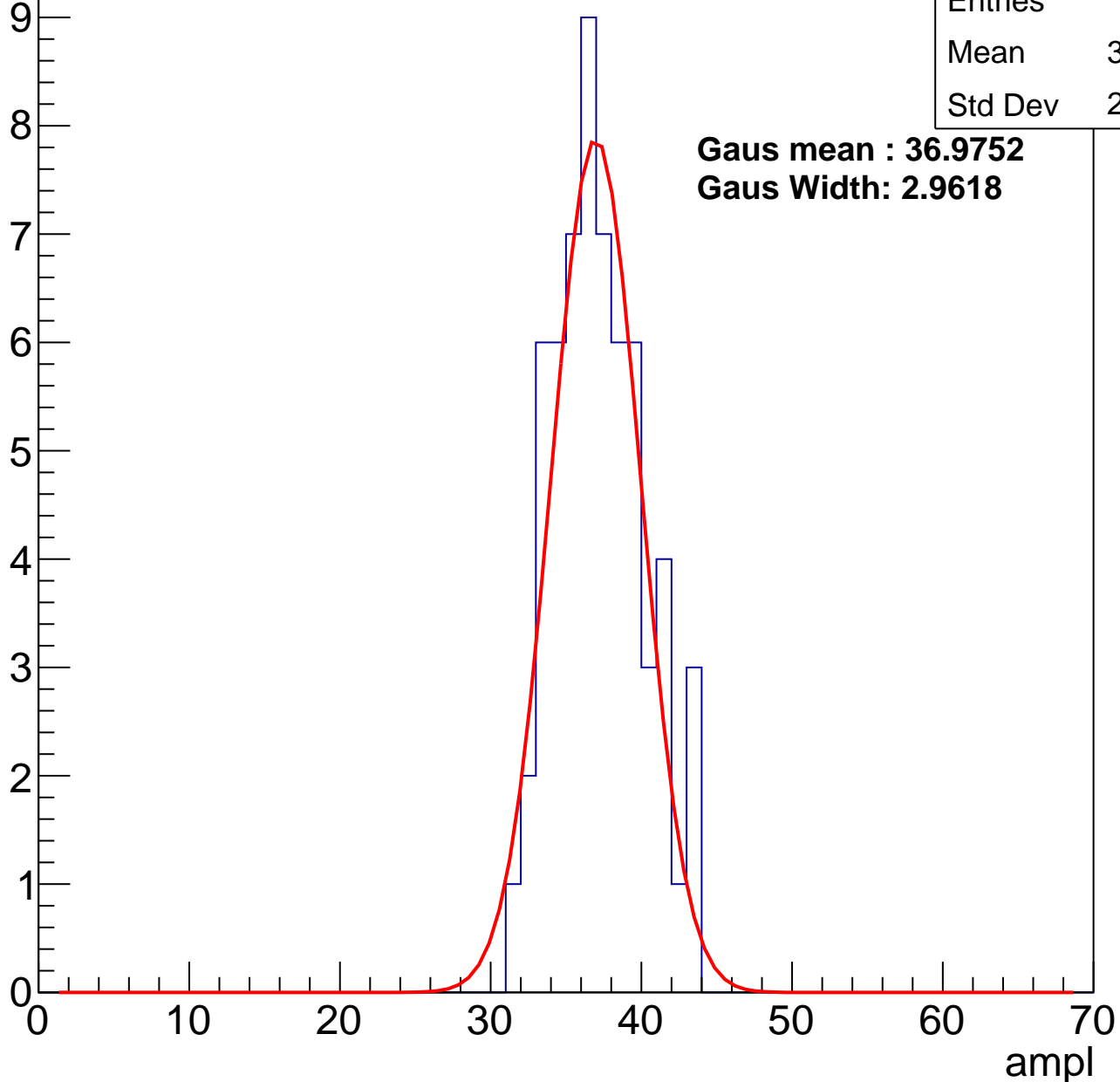
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.75
Std Dev	2.929

Gaus mean : 36.9752

Gaus Width: 2.9618



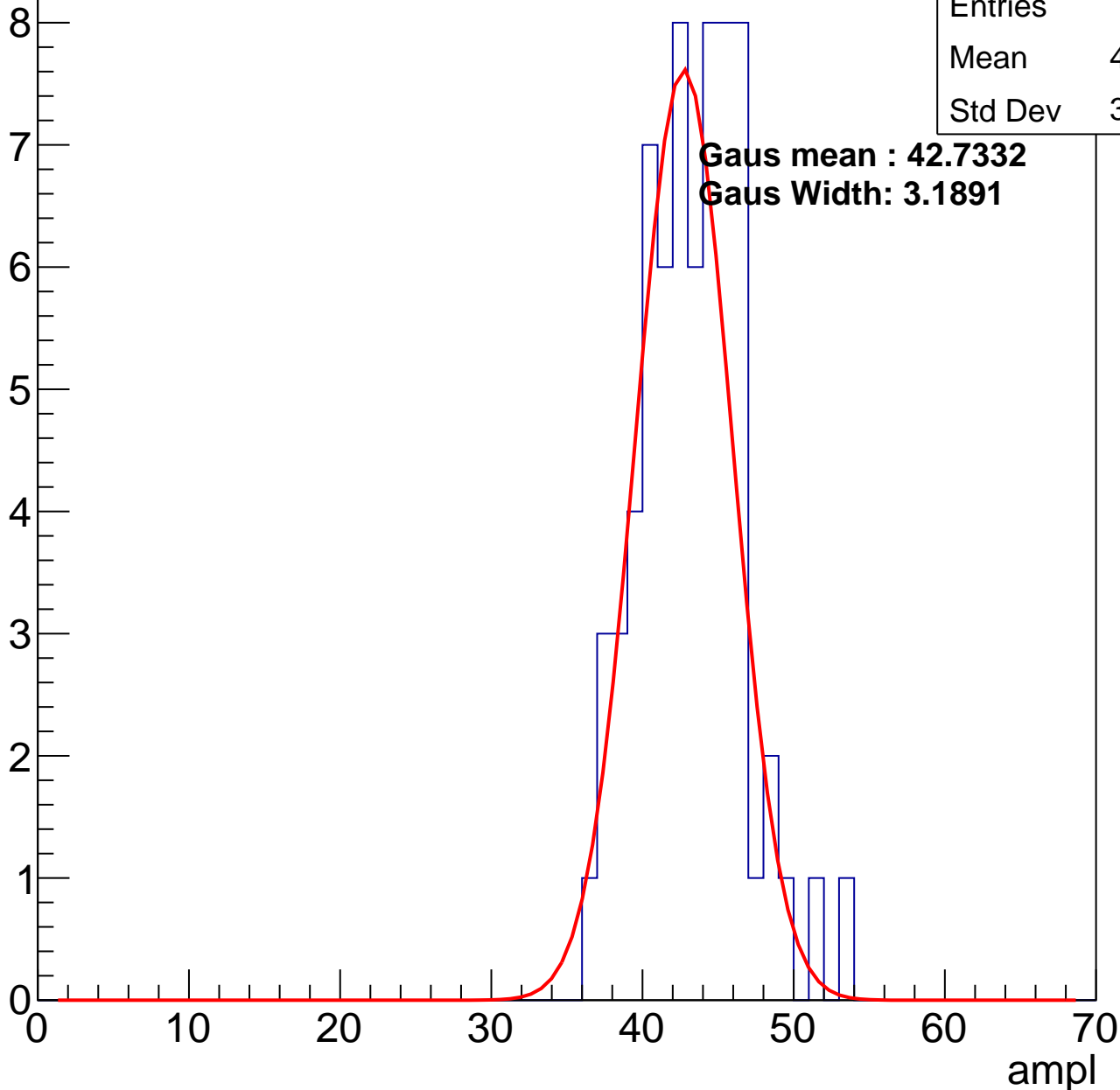
B0L002S, U17-ch41, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.84
Std Dev	3.359

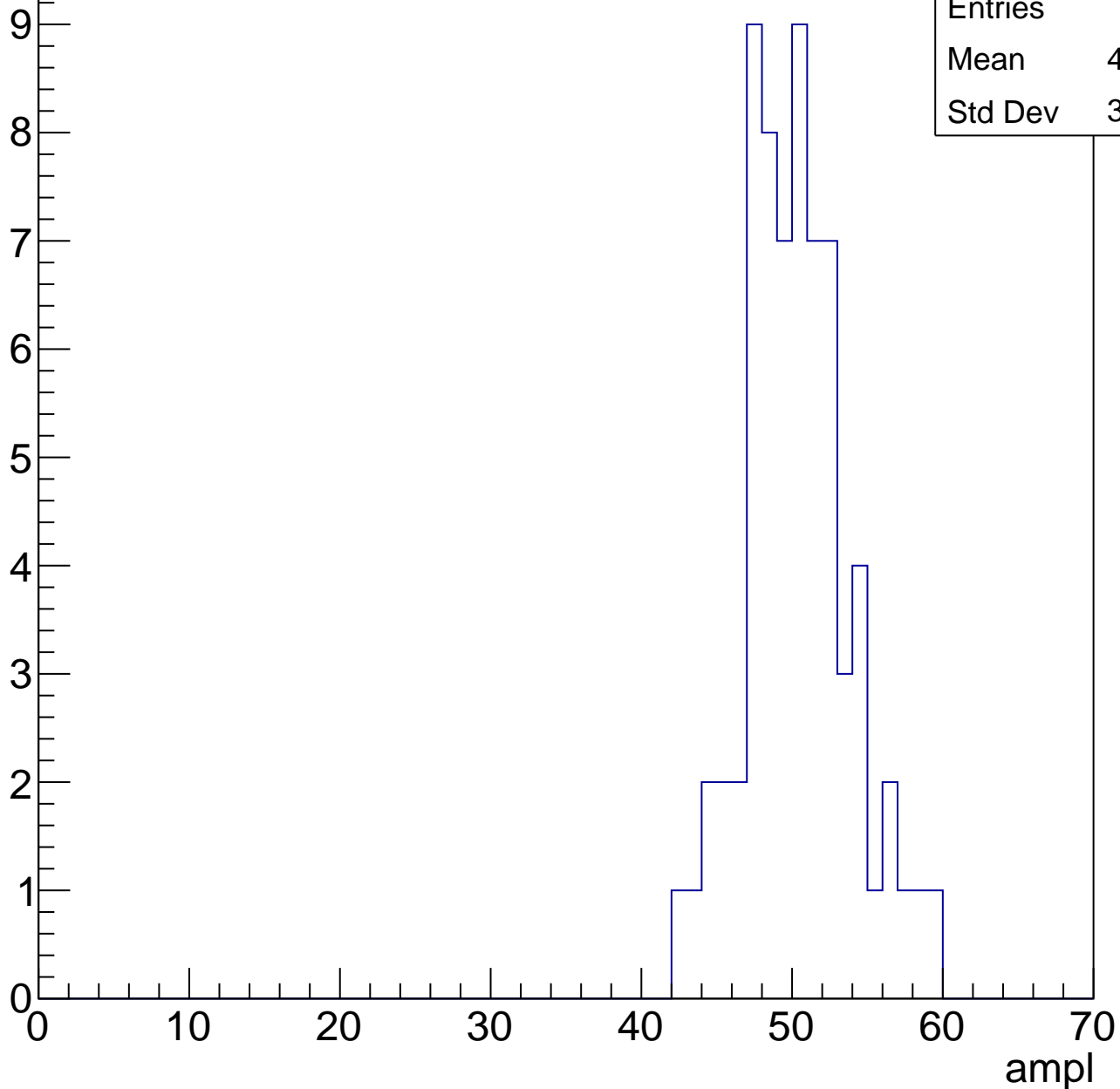
Gaus mean : 42.7332
Gaus Width: 3.1891



B0L002S, U17-ch41, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

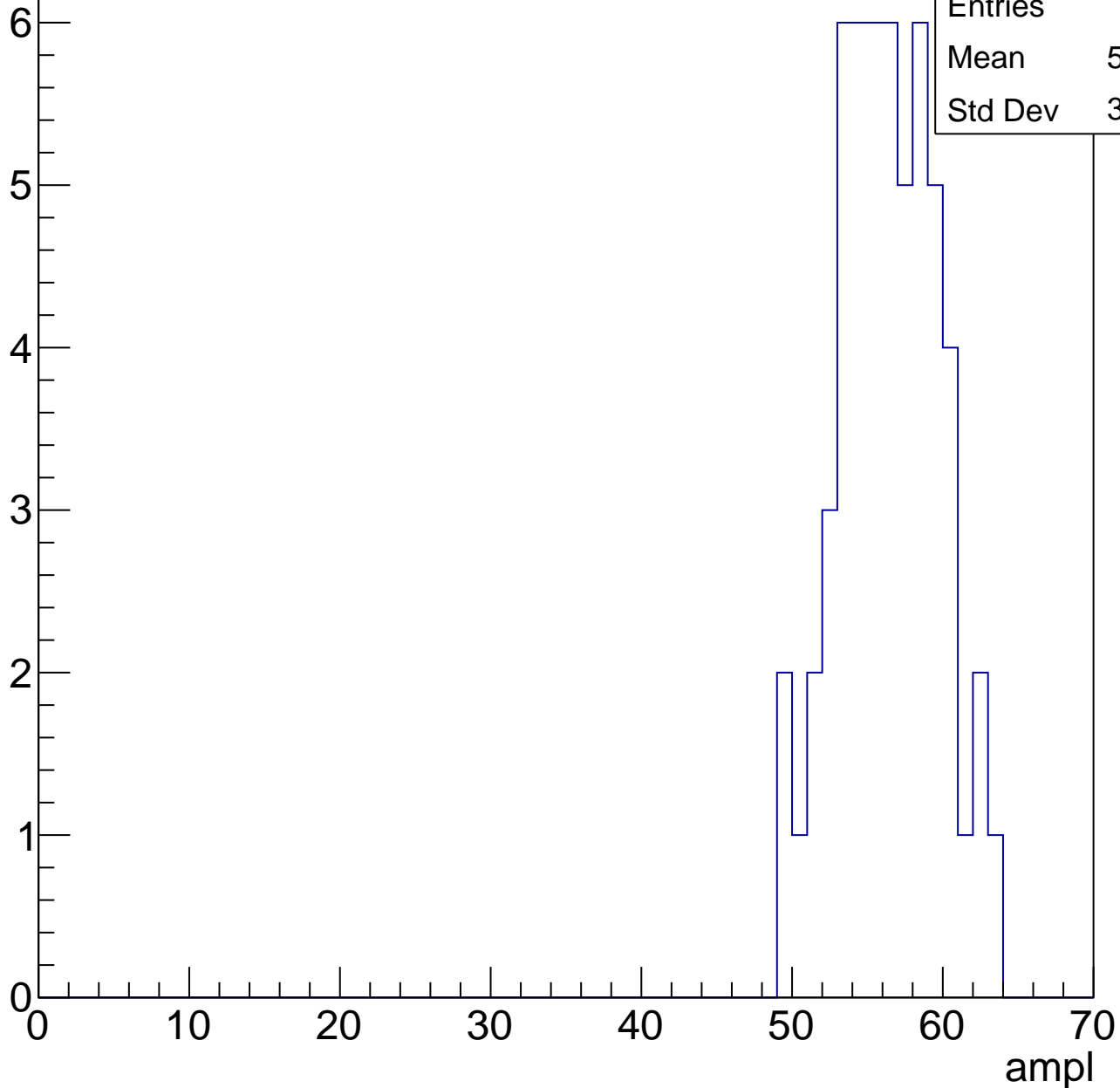


Entries	68
Mean	49.88
Std Dev	3.437

B0L002S, U17-ch41, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

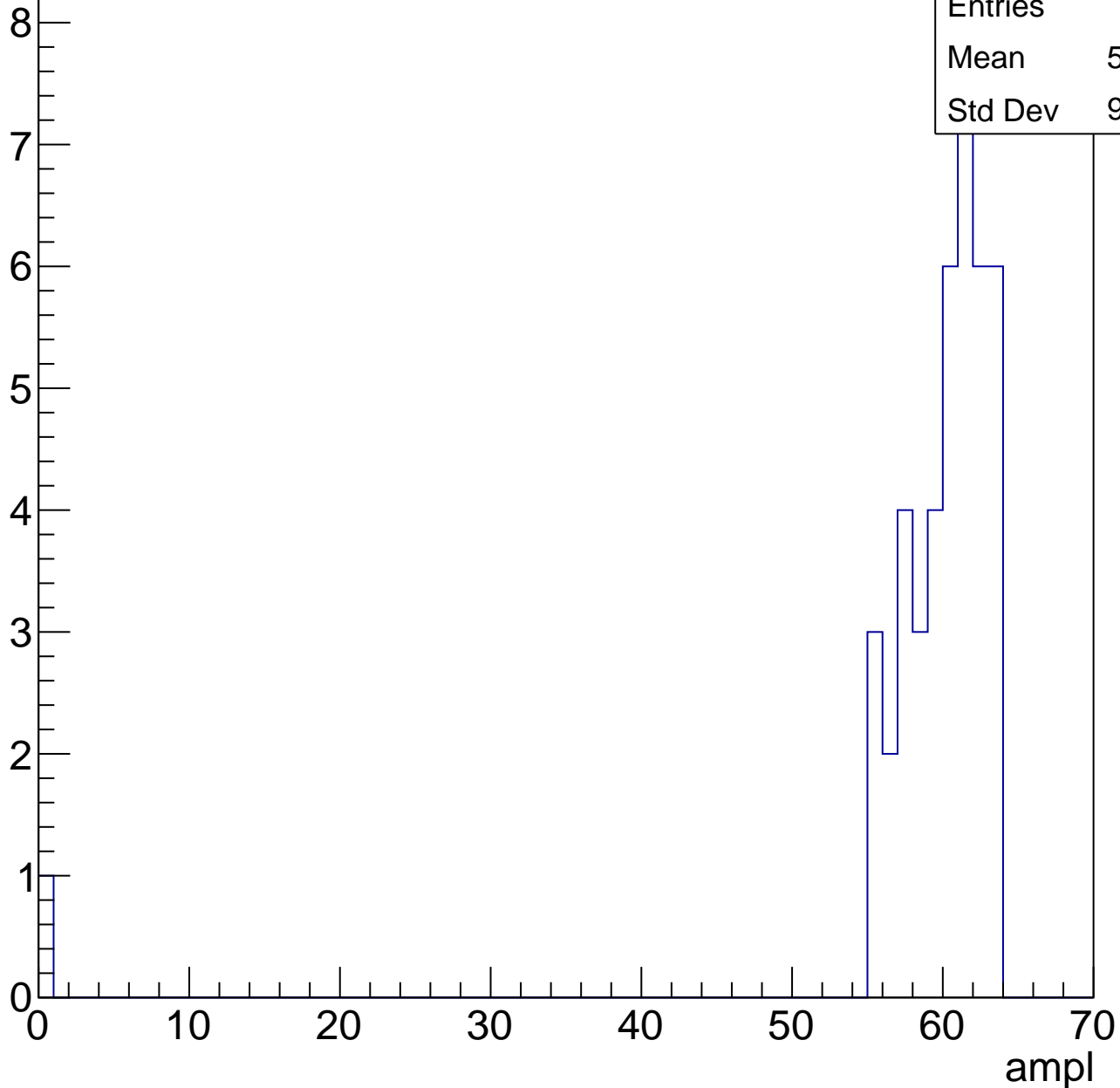


B0L002S, U17-ch41, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	58.44
Std Dev	9.327



B0L002S, U17-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch42, adc0

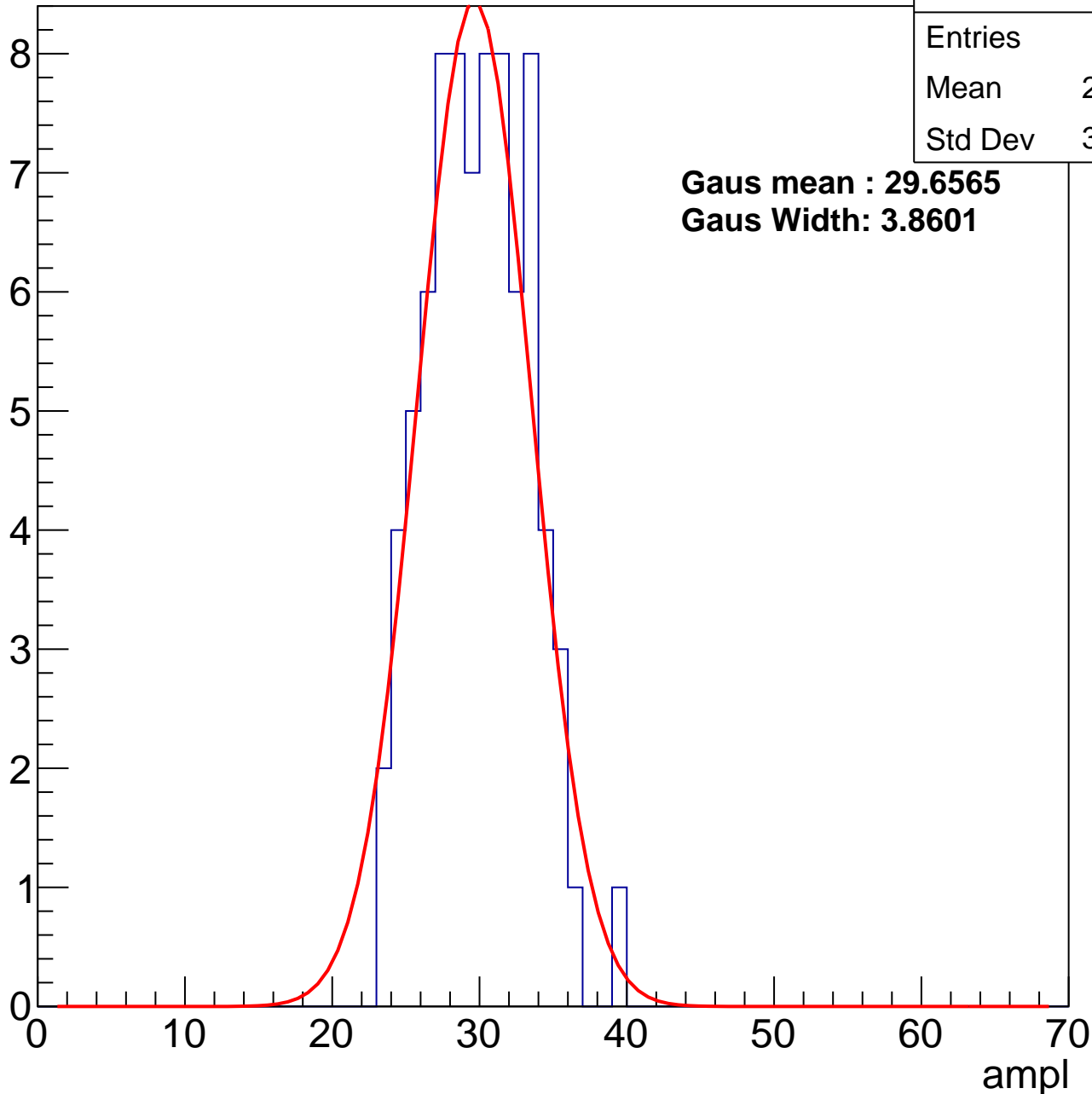
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	29.44
Std Dev	3.393

Gaus mean : 29.6565

Gaus Width: 3.8601



B0L002S, U17-ch42, adc1

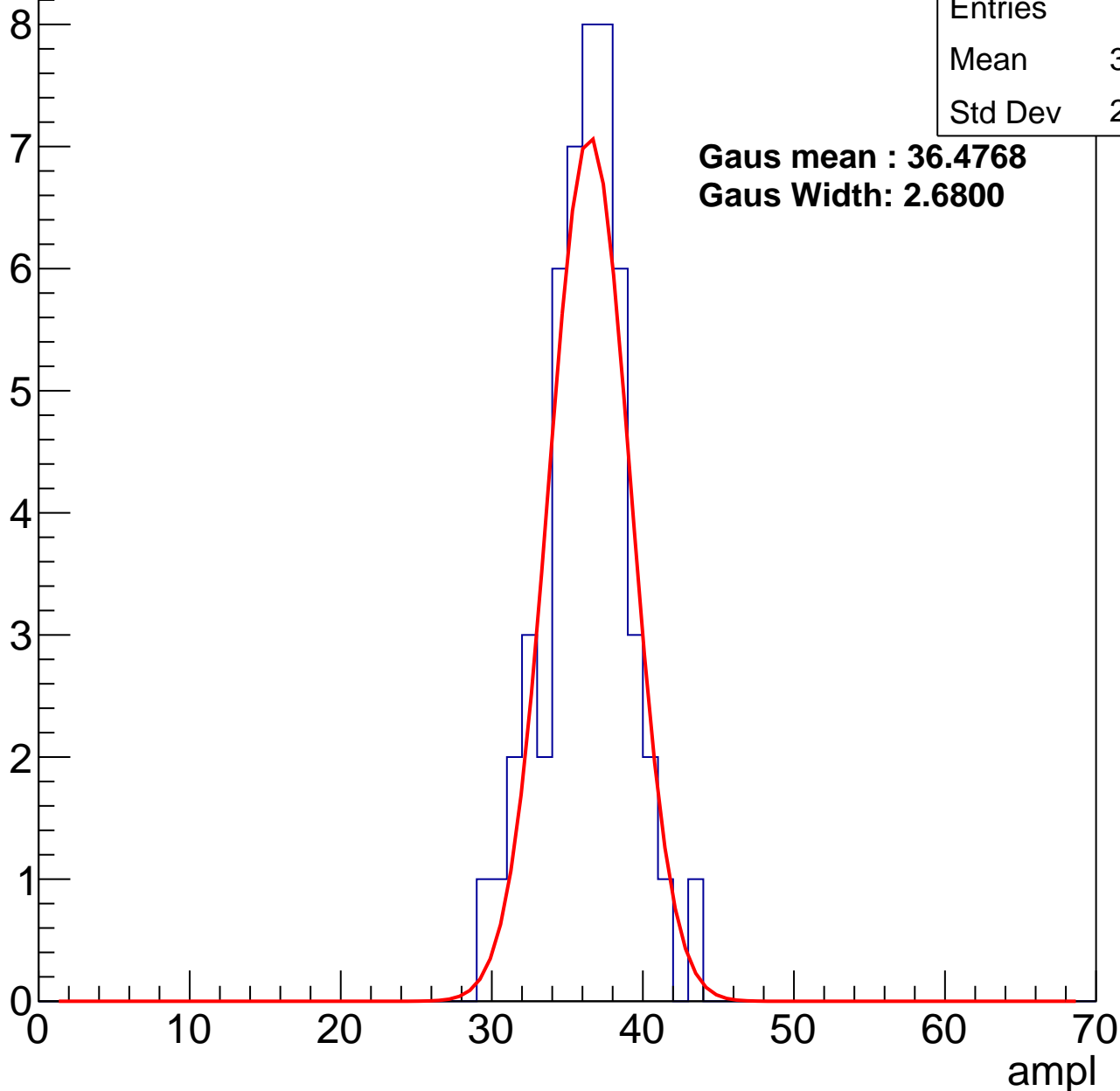
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	35.78
Std Dev	2.782

Gaus mean : 36.4768

Gaus Width: 2.6800



B0L002S, U17-ch42, adc2

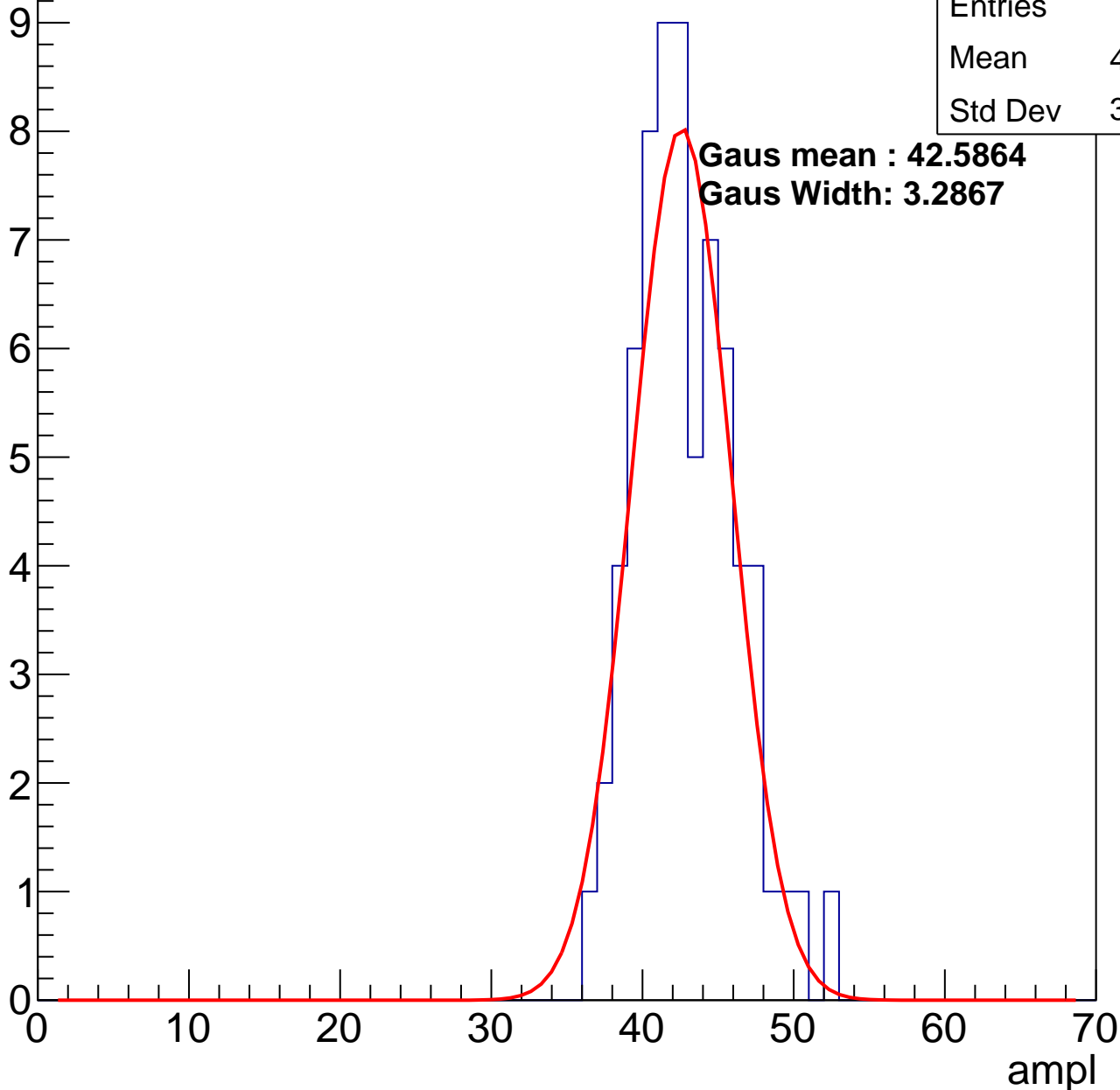
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	42.42
Std Dev	3.259

Gaus mean : 42.5864

Gaus Width: 3.2867

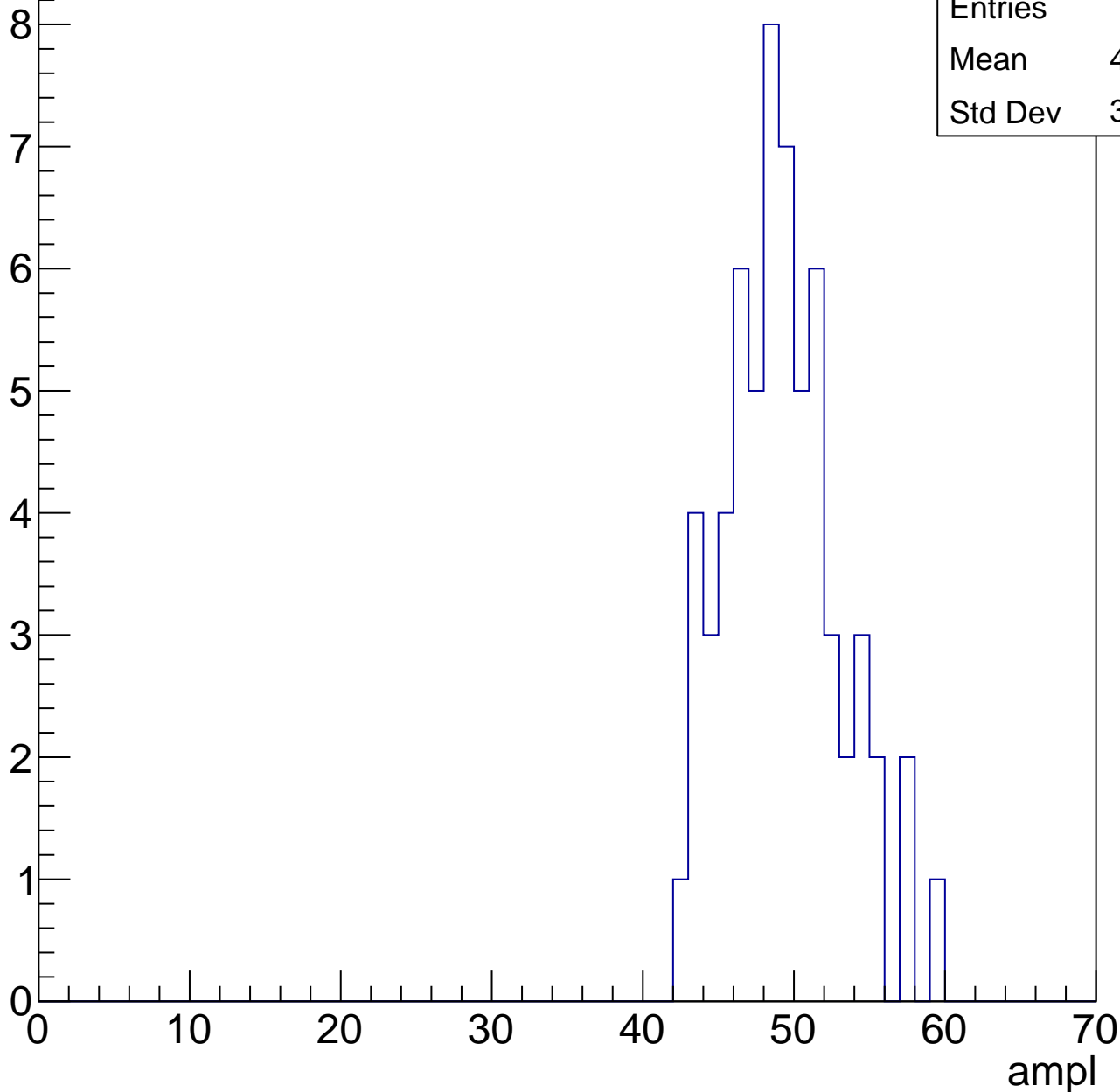


B0L002S, U17-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

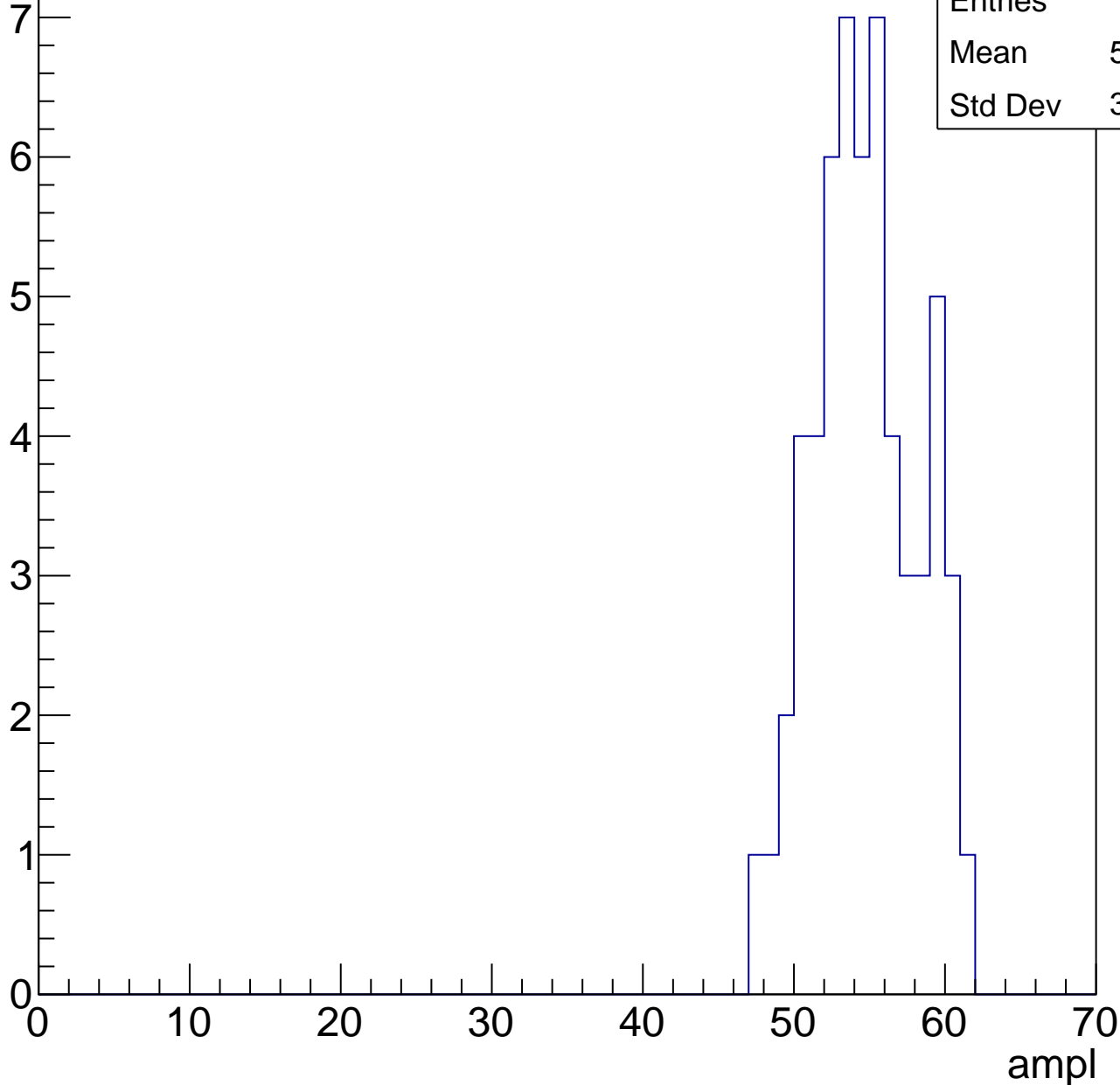
Entries	62
Mean	48.82
Std Dev	3.757



B0L002S, U17-ch42, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

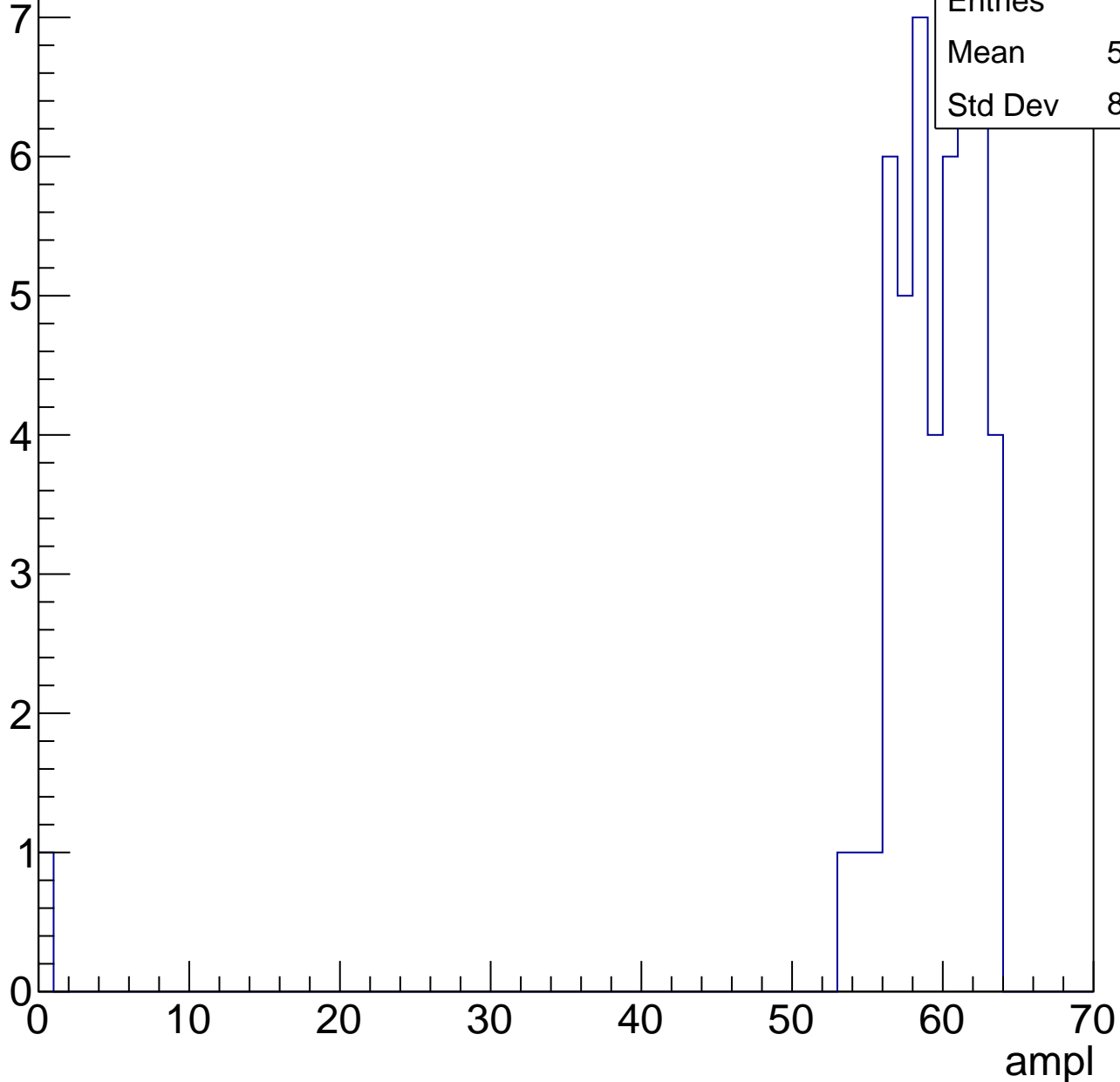


Entries	57
Mean	54.28
Std Dev	3.355

B0L002S, U17-ch42, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

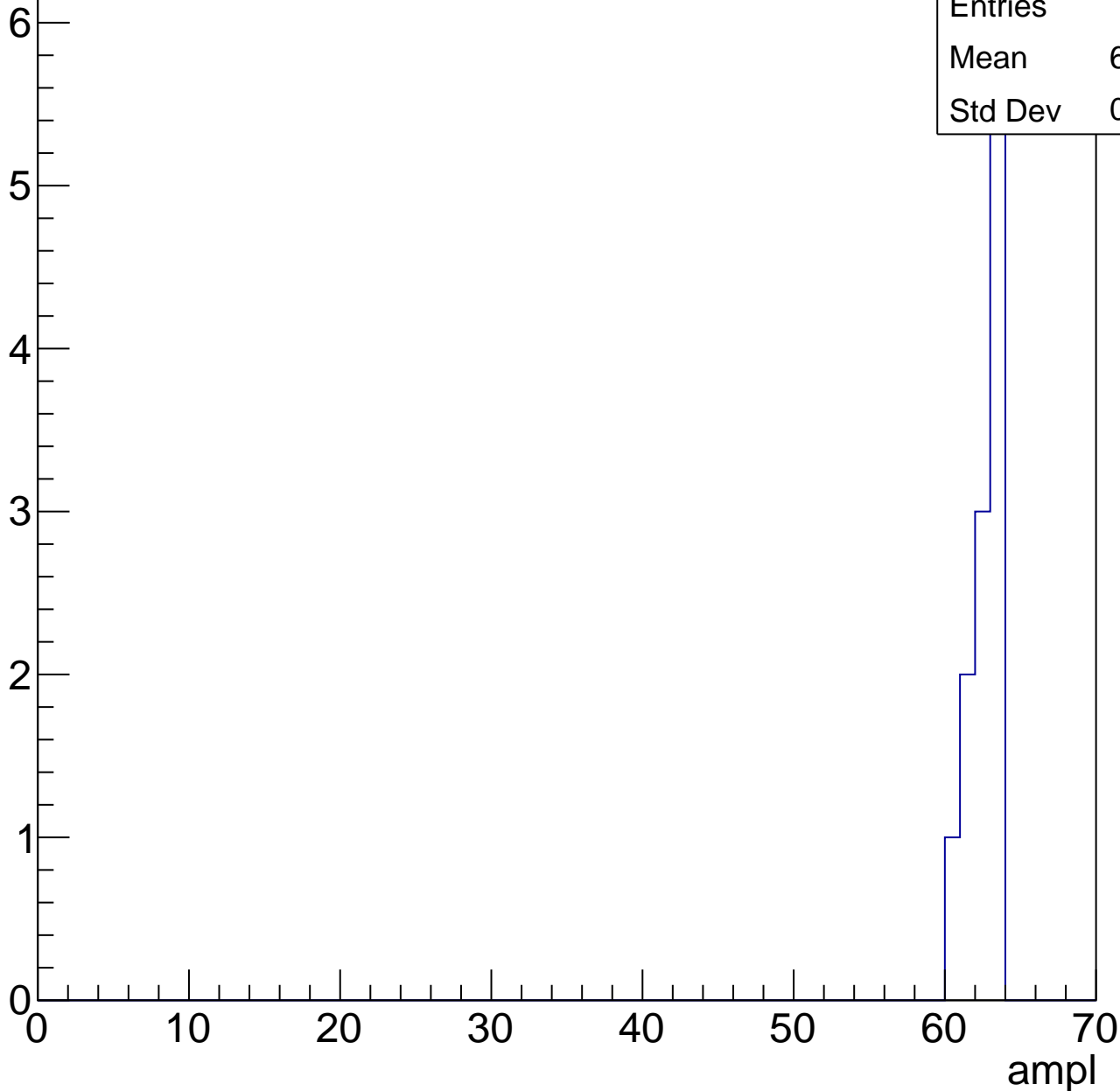


B0L002S, U17-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	62.17
Std Dev	0.986



B0L002S, U17-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U17-ch43, adc0

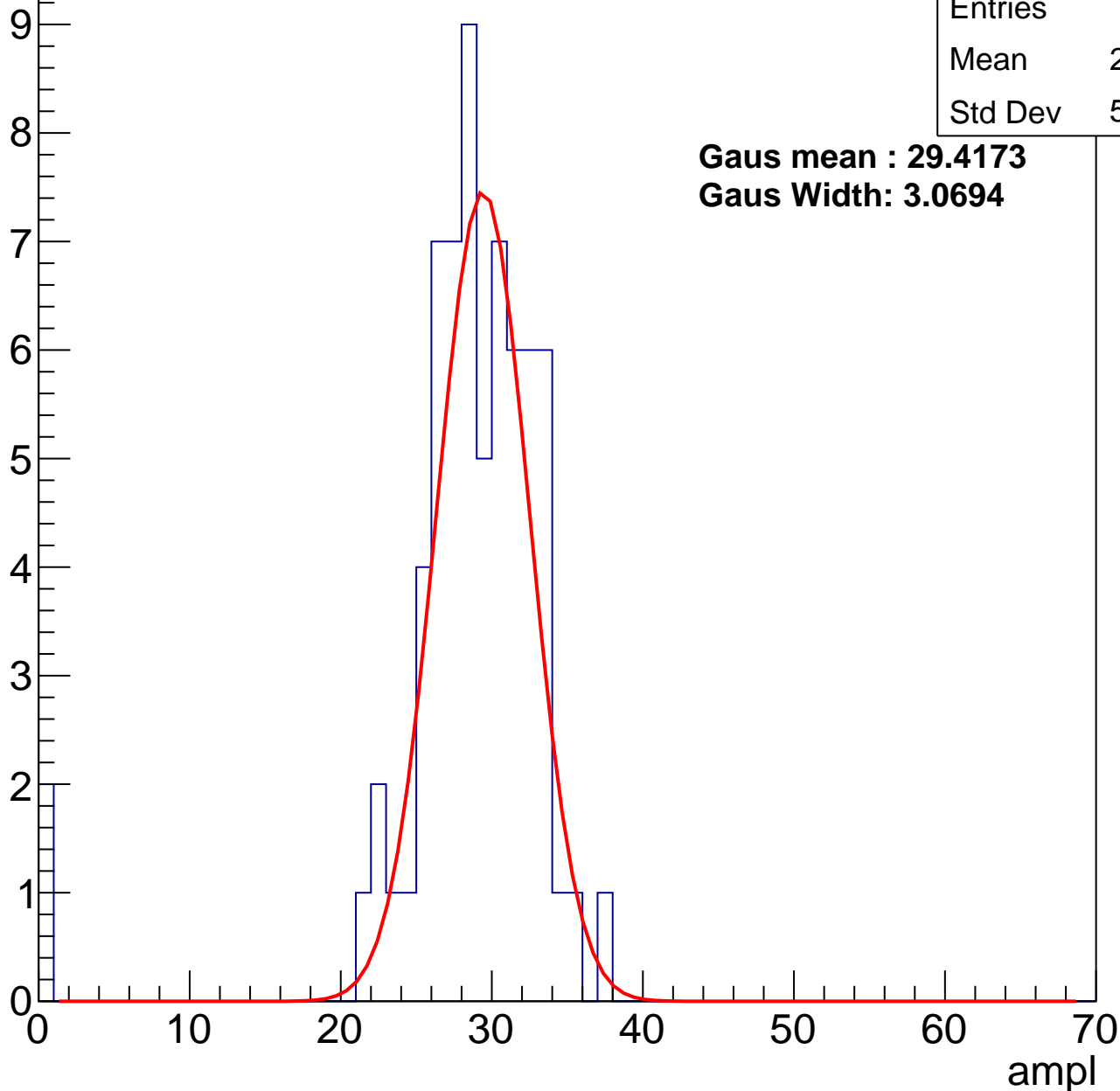
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	27.94
Std Dev	5.859

Gaus mean : 29.4173

Gaus Width: 3.0694

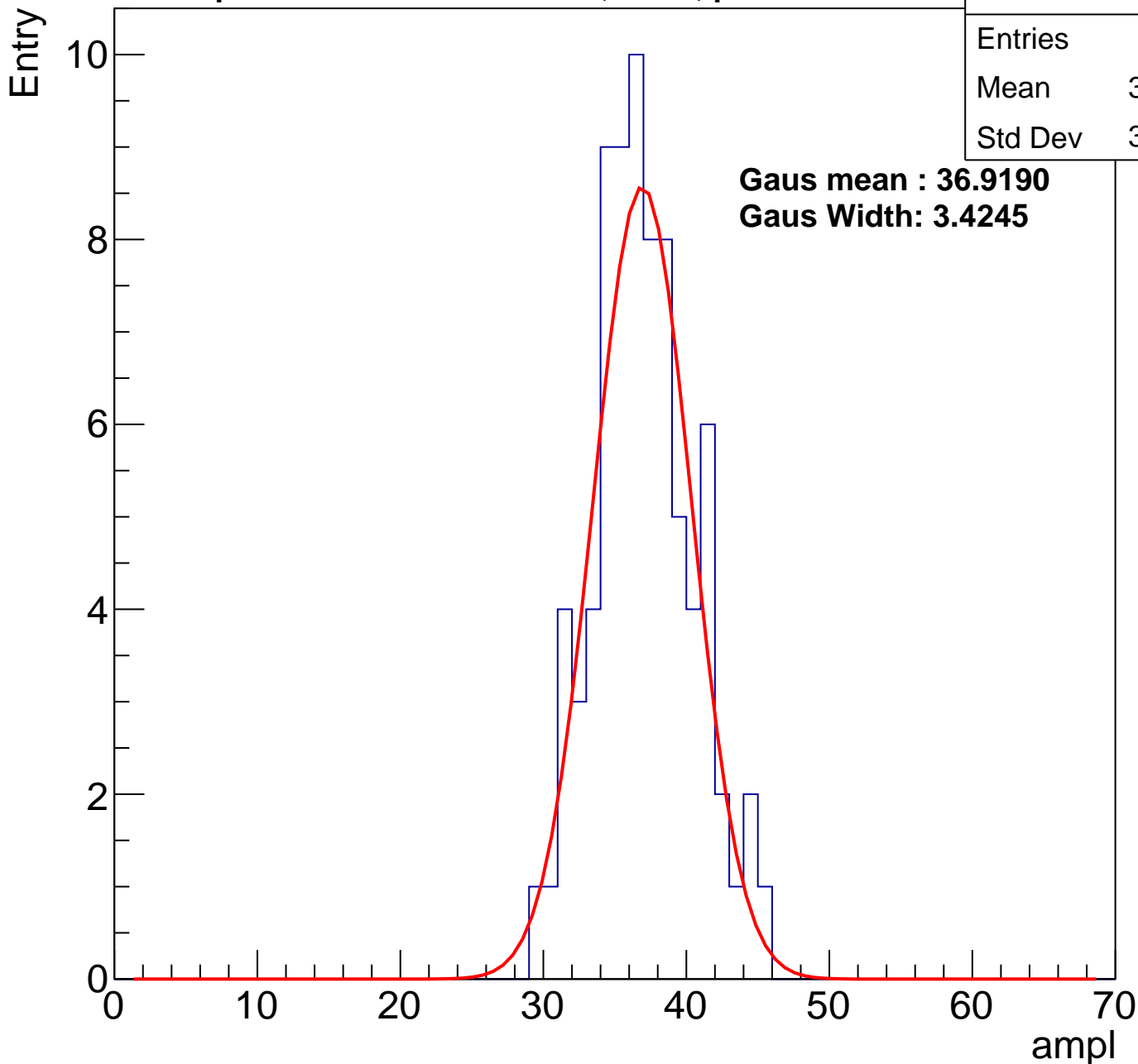


B0L002S, U17-ch43, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	36.58
Std Dev	3.429

Gaus mean : 36.9190
Gaus Width: 3.4245



B0L002S, U17-ch43, adc2

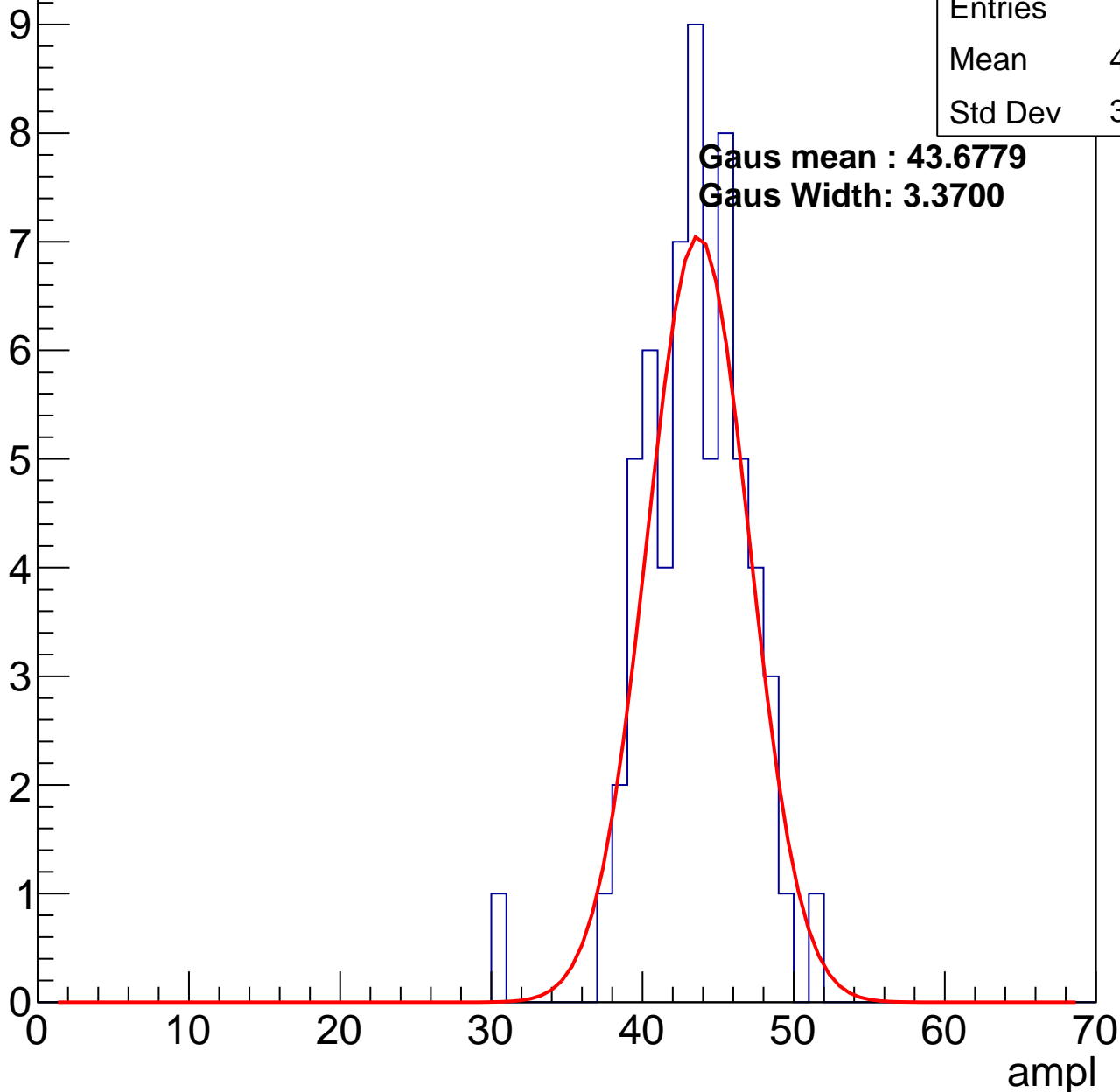
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	42.98
Std Dev	3.438

Gaus mean : 43.6779

Gaus Width: 3.3700

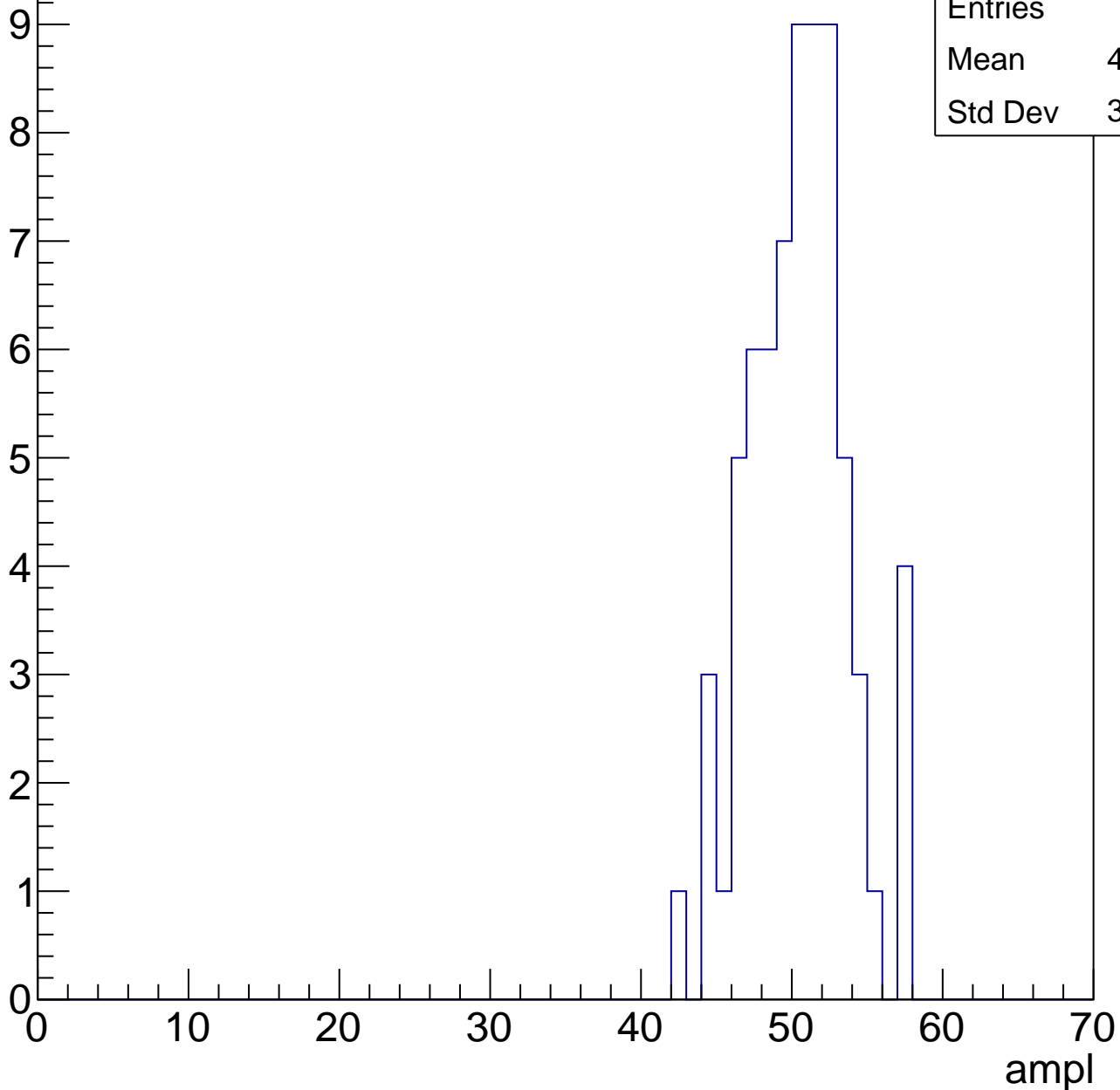


B0L002S, U17-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

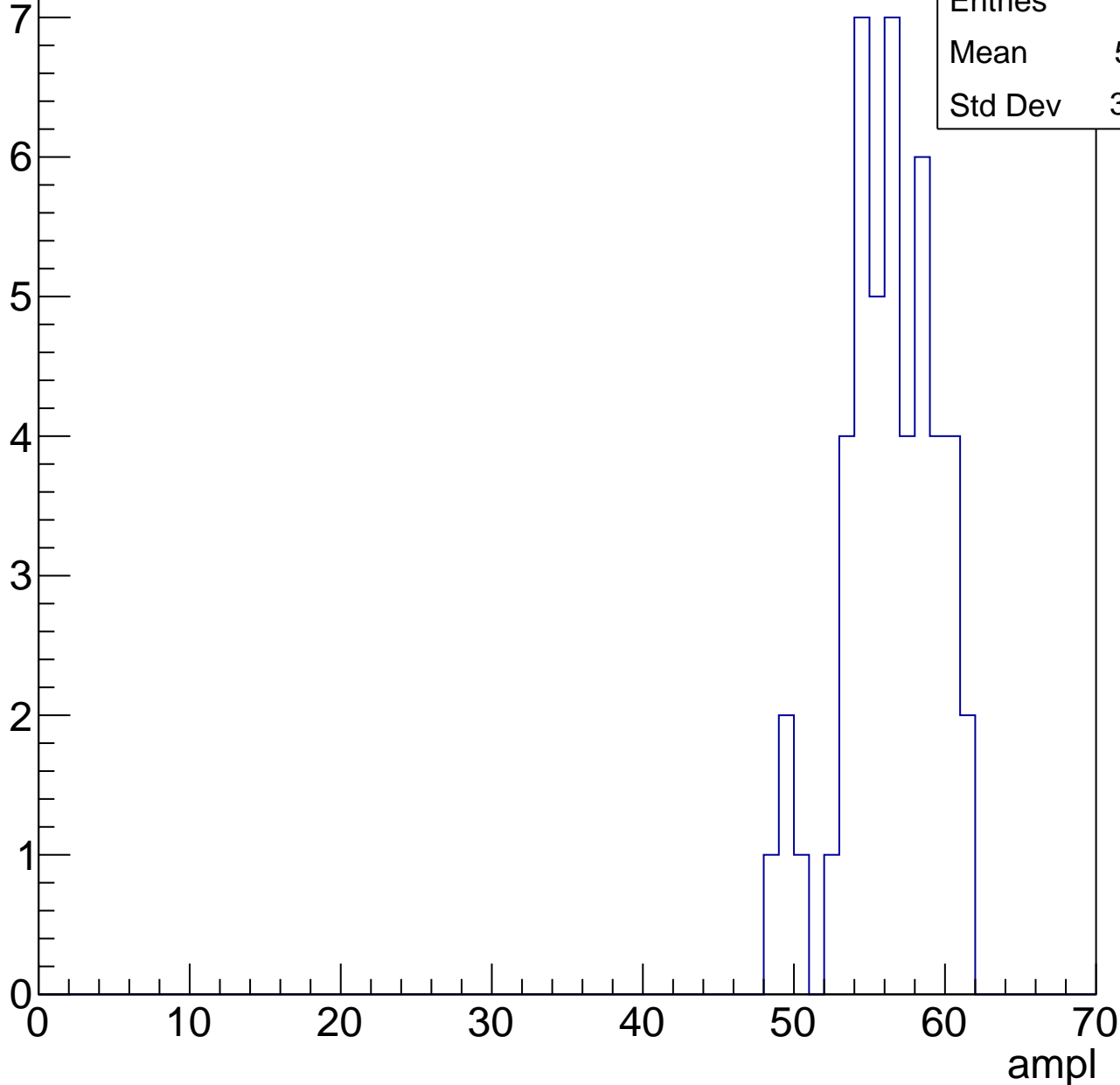
Entries	69
Mean	49.99
Std Dev	3.233



B0L002S, U17-ch43, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

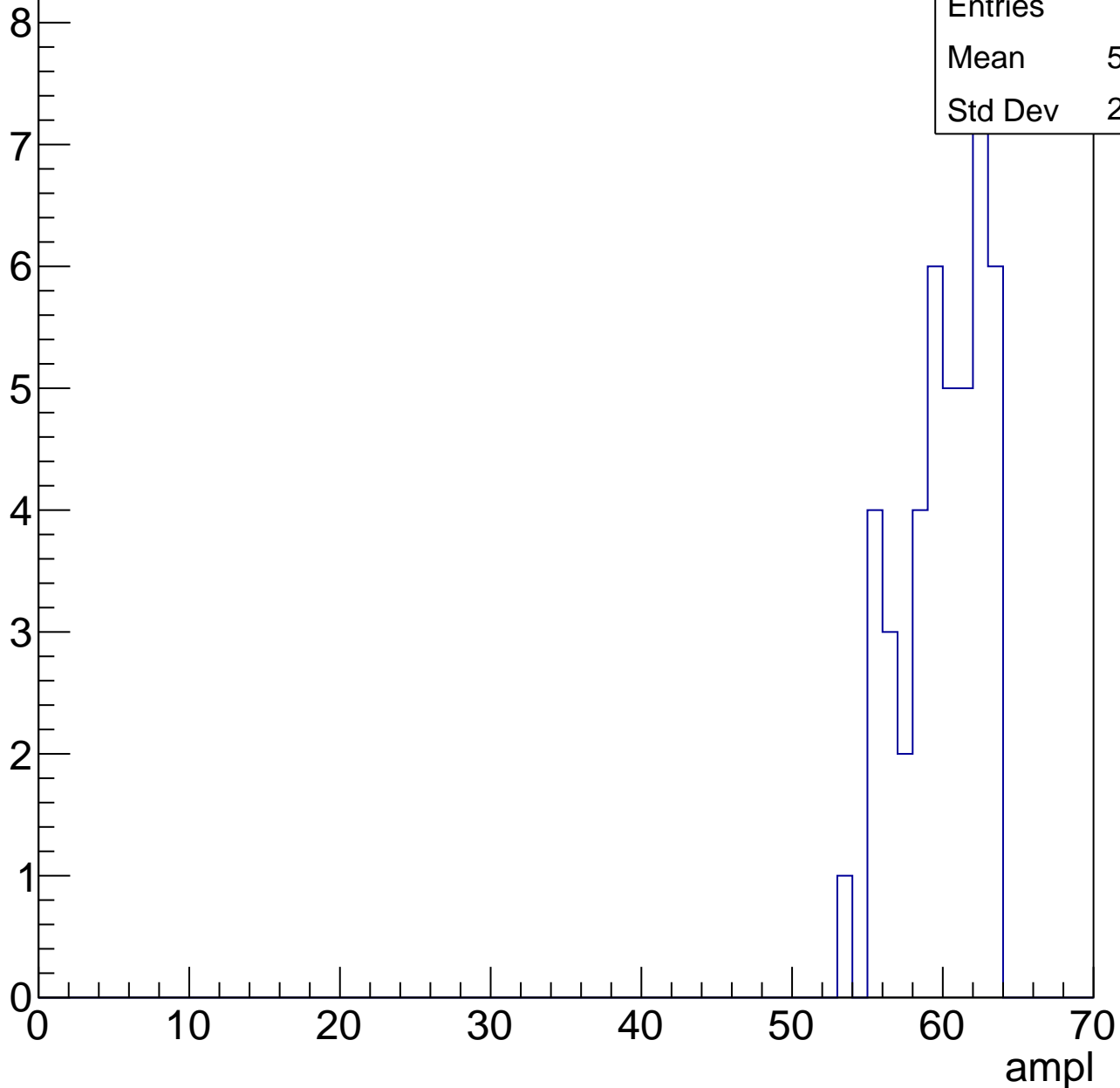


B0L002S, U17-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

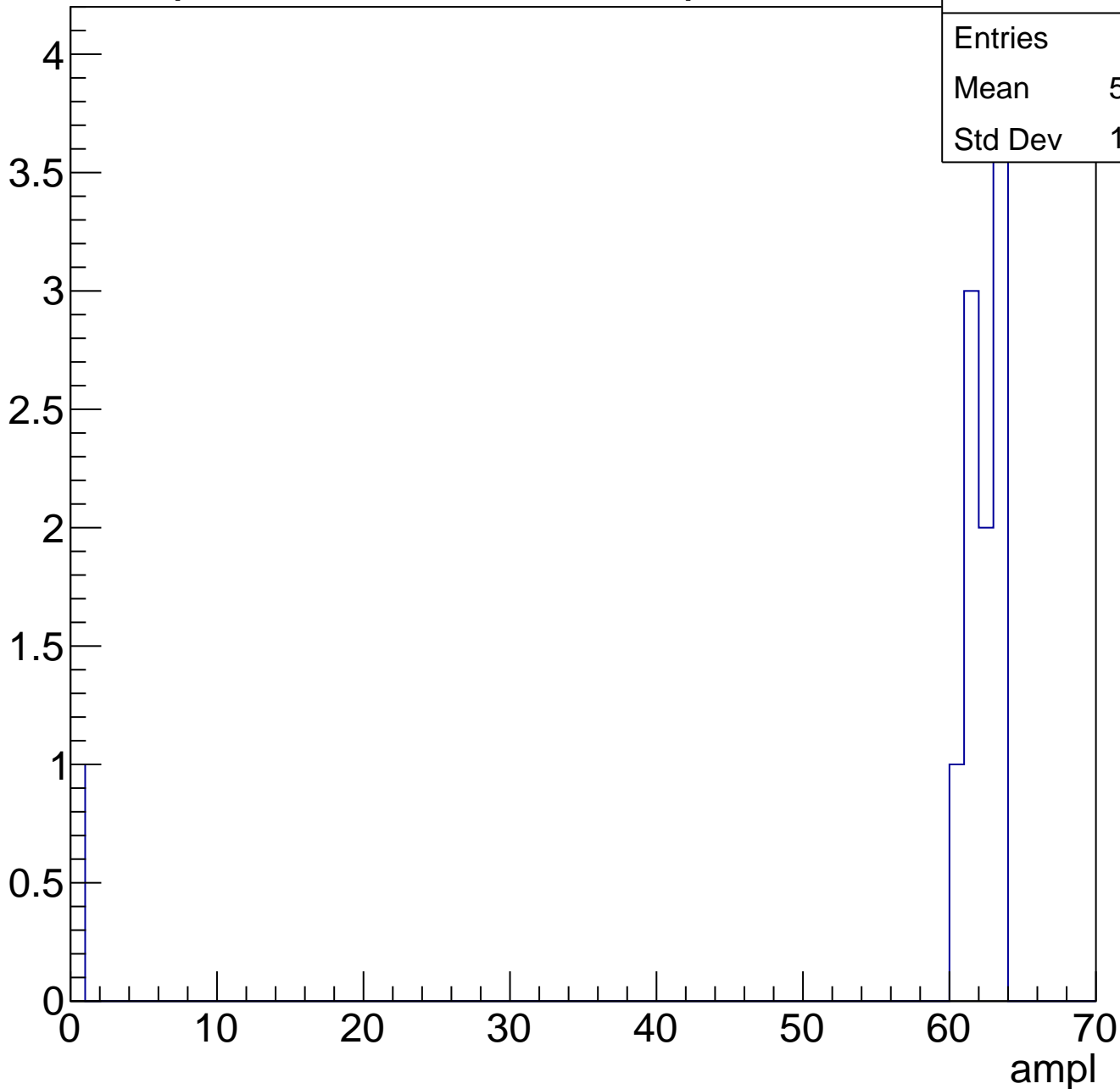
Entries	44
Mean	59.55
Std Dev	2.692



B0L002S, U17-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch44, adc0

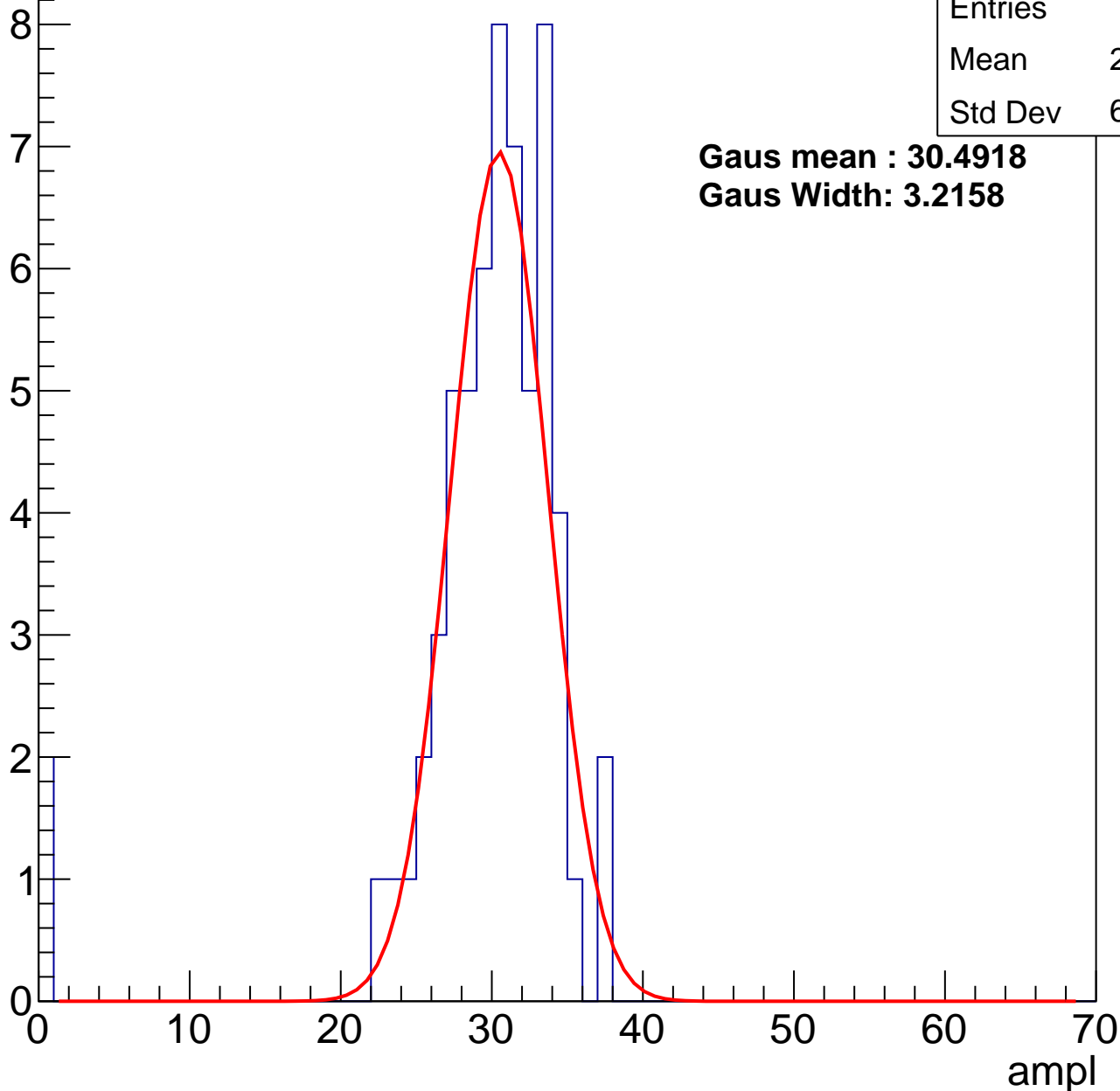
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.05
Std Dev	6.205

Gaus mean : 30.4918

Gaus Width: 3.2158



B0L002S, U17-ch44, adc1

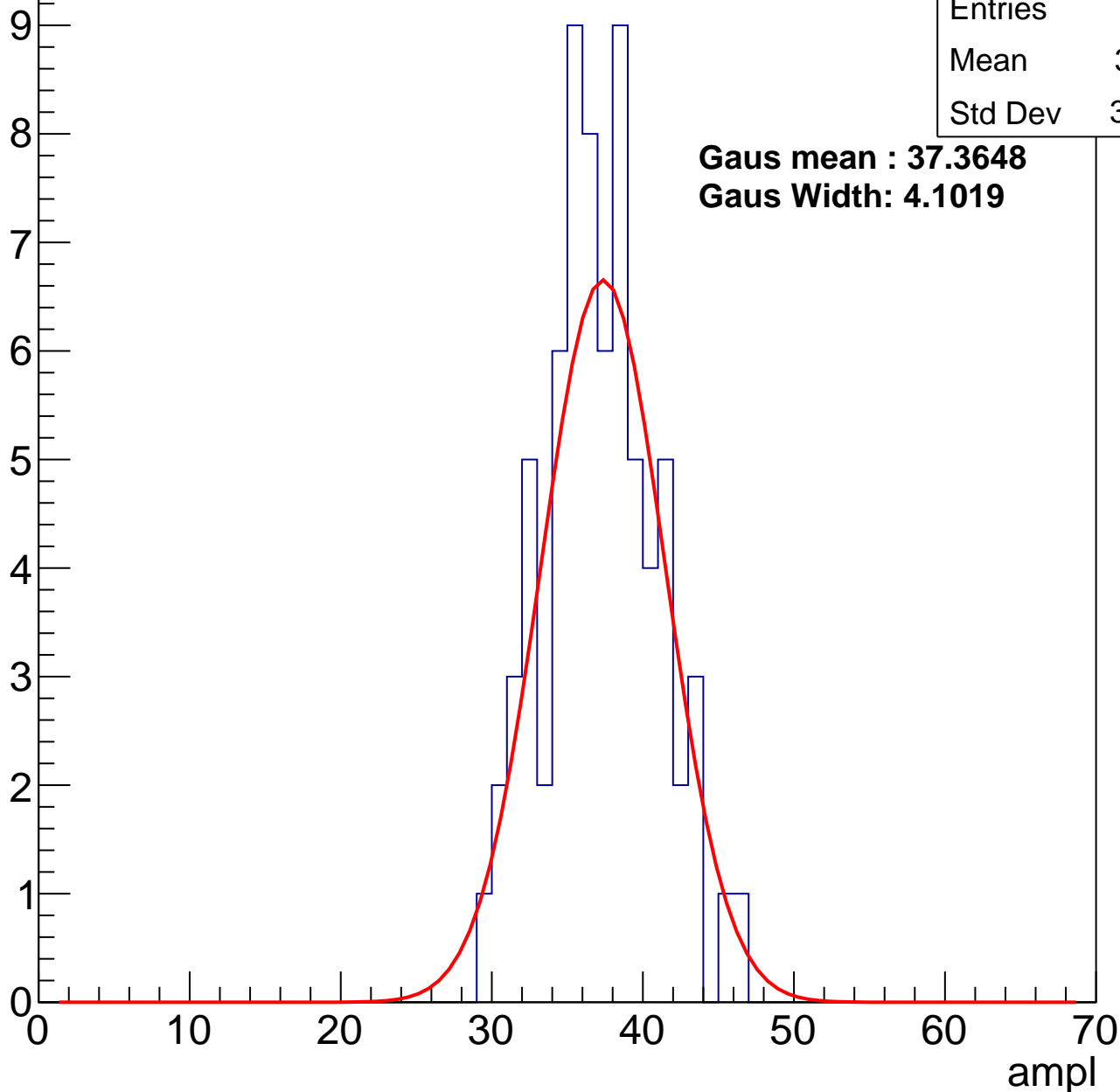
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.71
Std Dev	3.665

Gaus mean : 37.3648

Gaus Width: 4.1019



B0L002S, U17-ch44, adc2

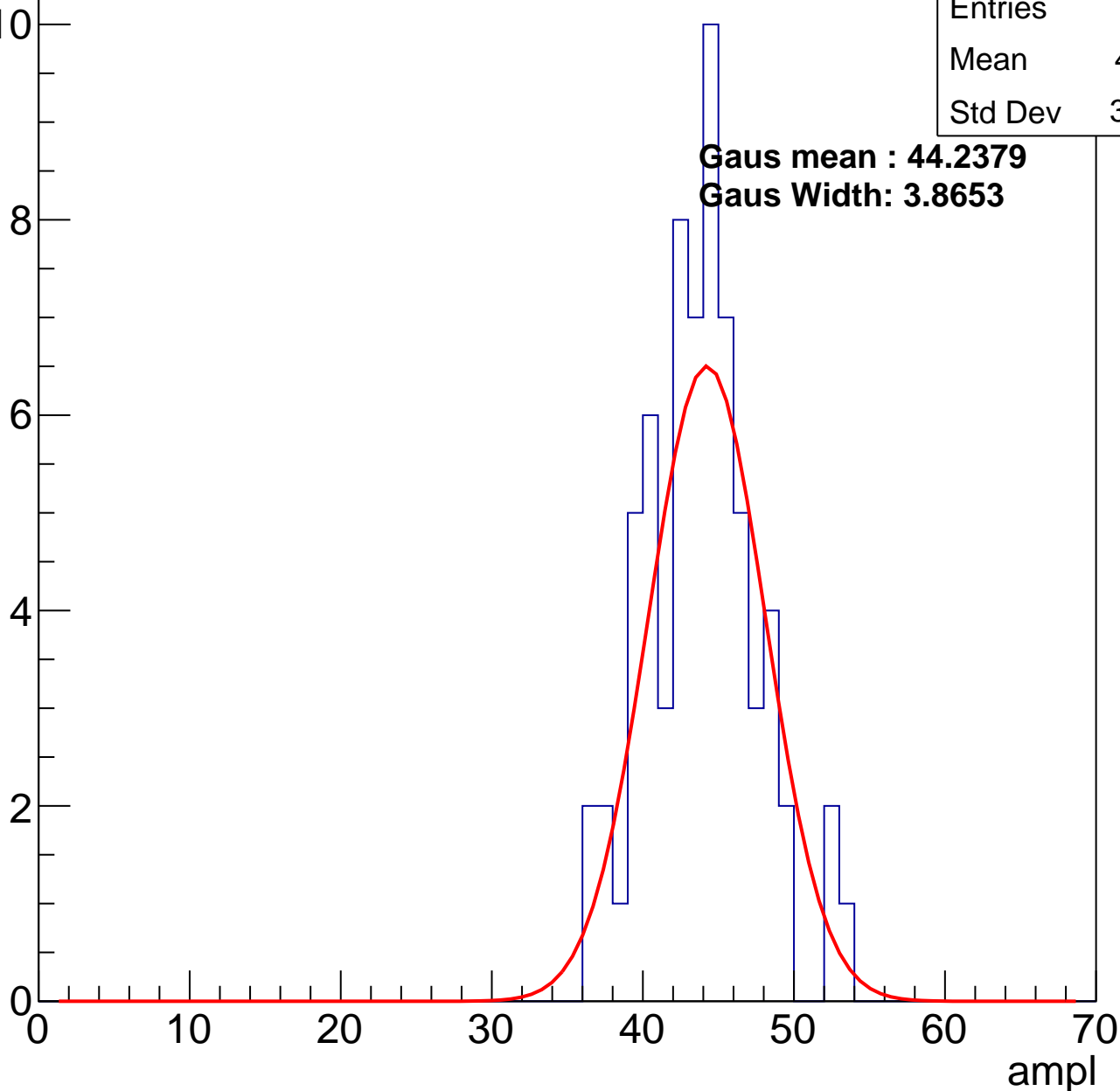
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.41
Std Dev	3.643

Gaus mean : 44.2379

Gaus Width: 3.8653

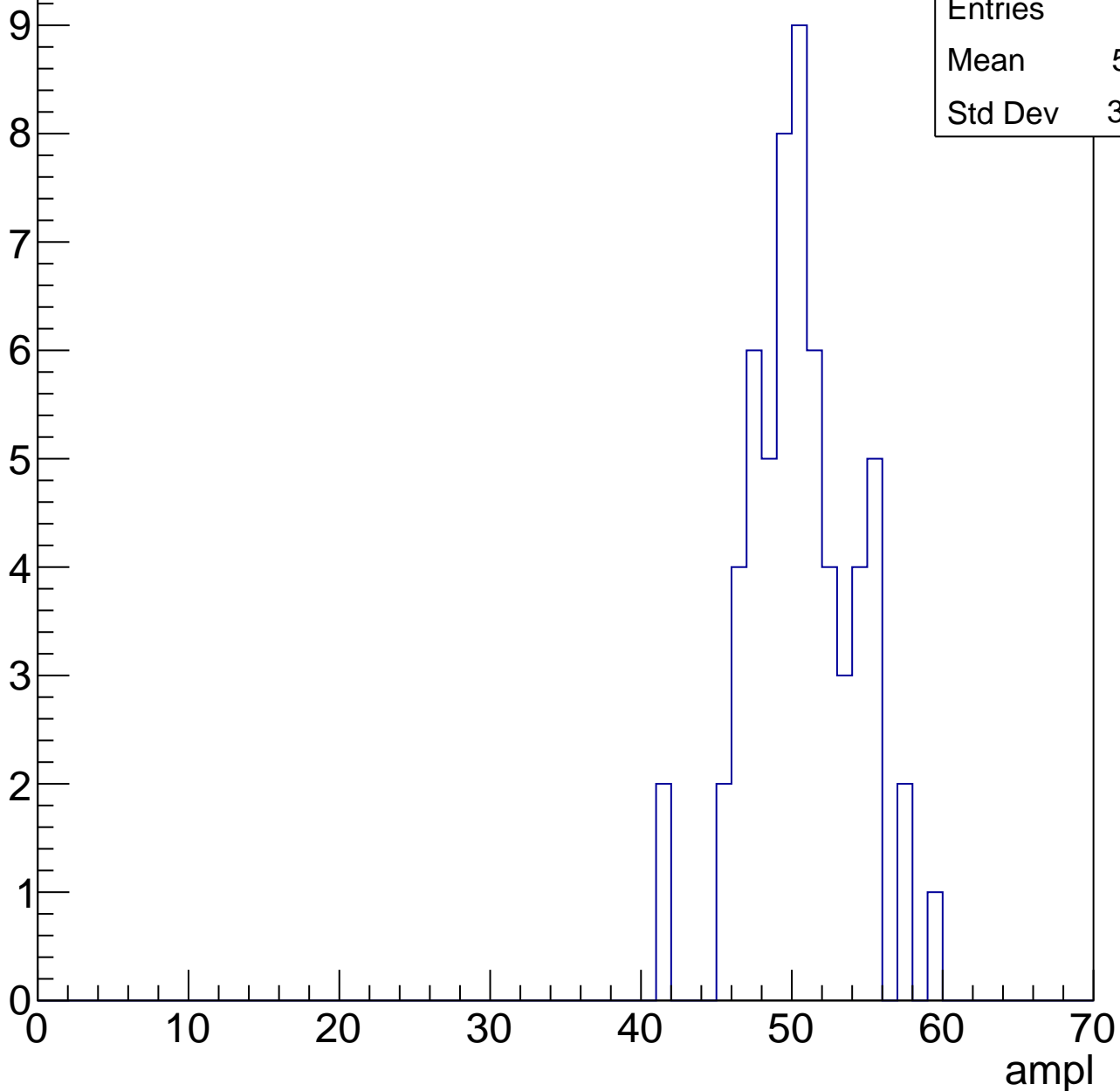


B0L002S, U17-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	50.11
Std Dev	3.563

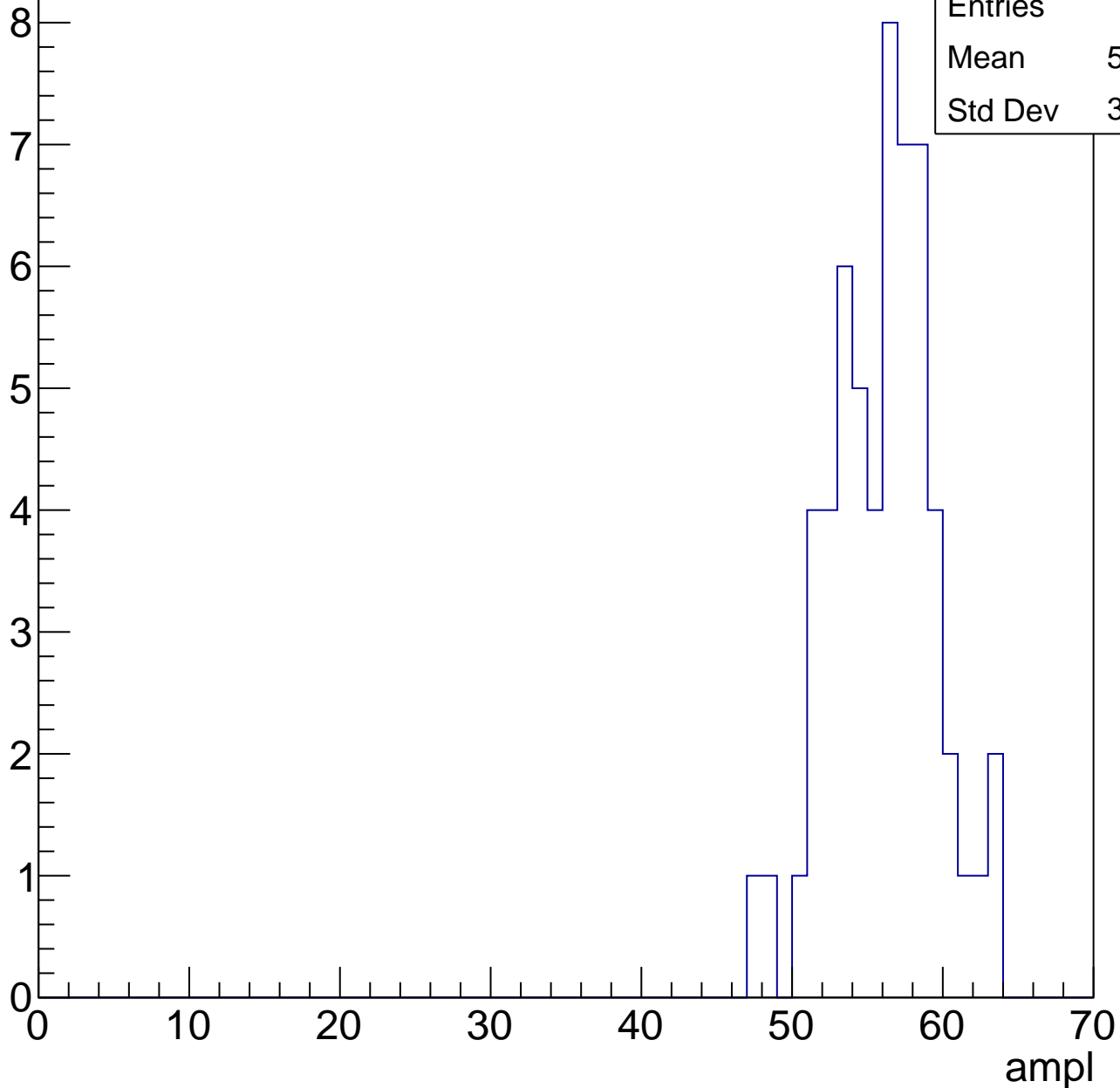


B0L002S, U17-ch44, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	55.57
Std Dev	3.409



B0L002S, U17-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

59.95

Std Dev

2.241

0

0

10

20

30

40

50

60

70

ampl

0

0

10

20

30

40

50

60

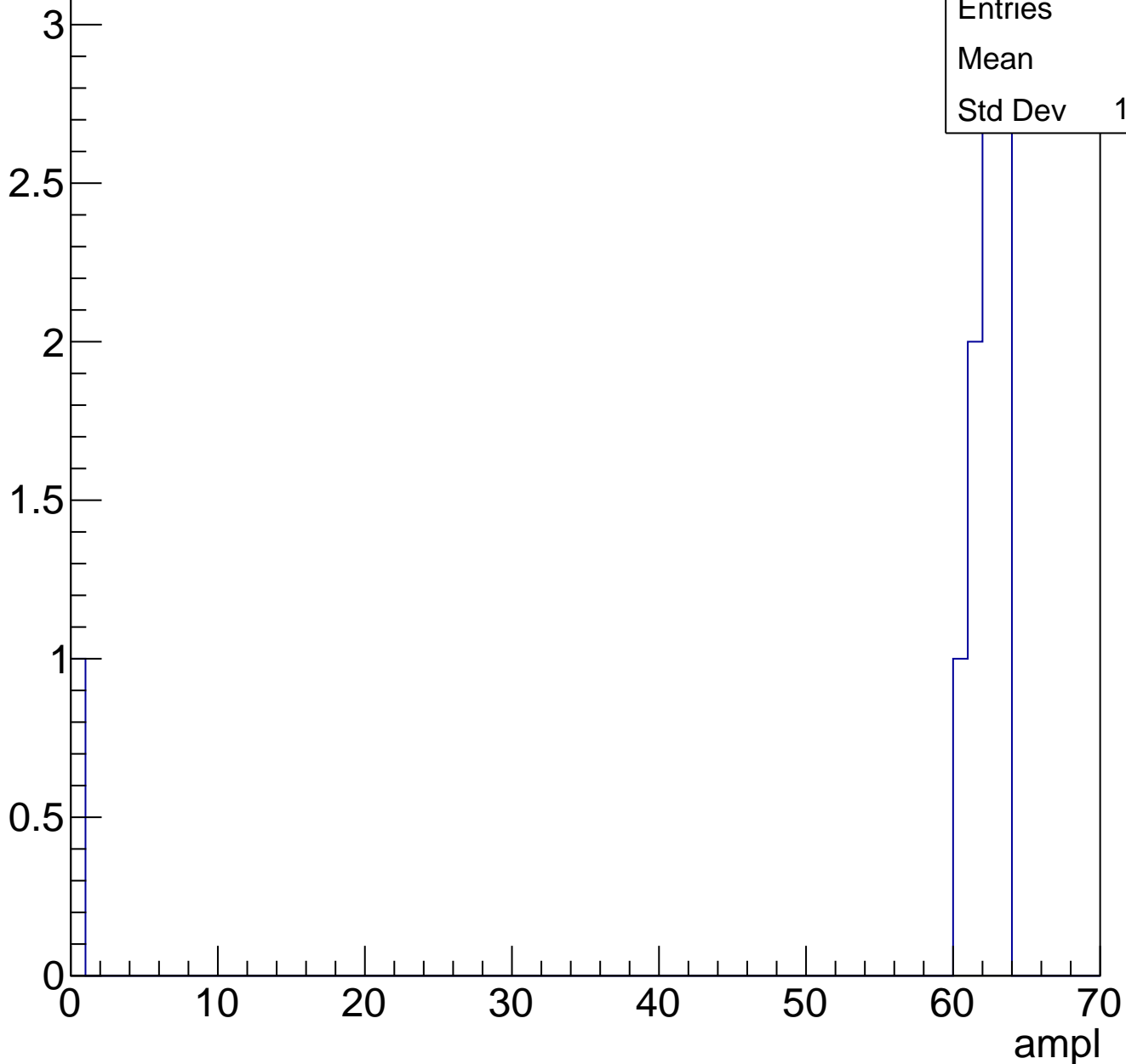
70

ampl

B0L002S, U17-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch45, adc0

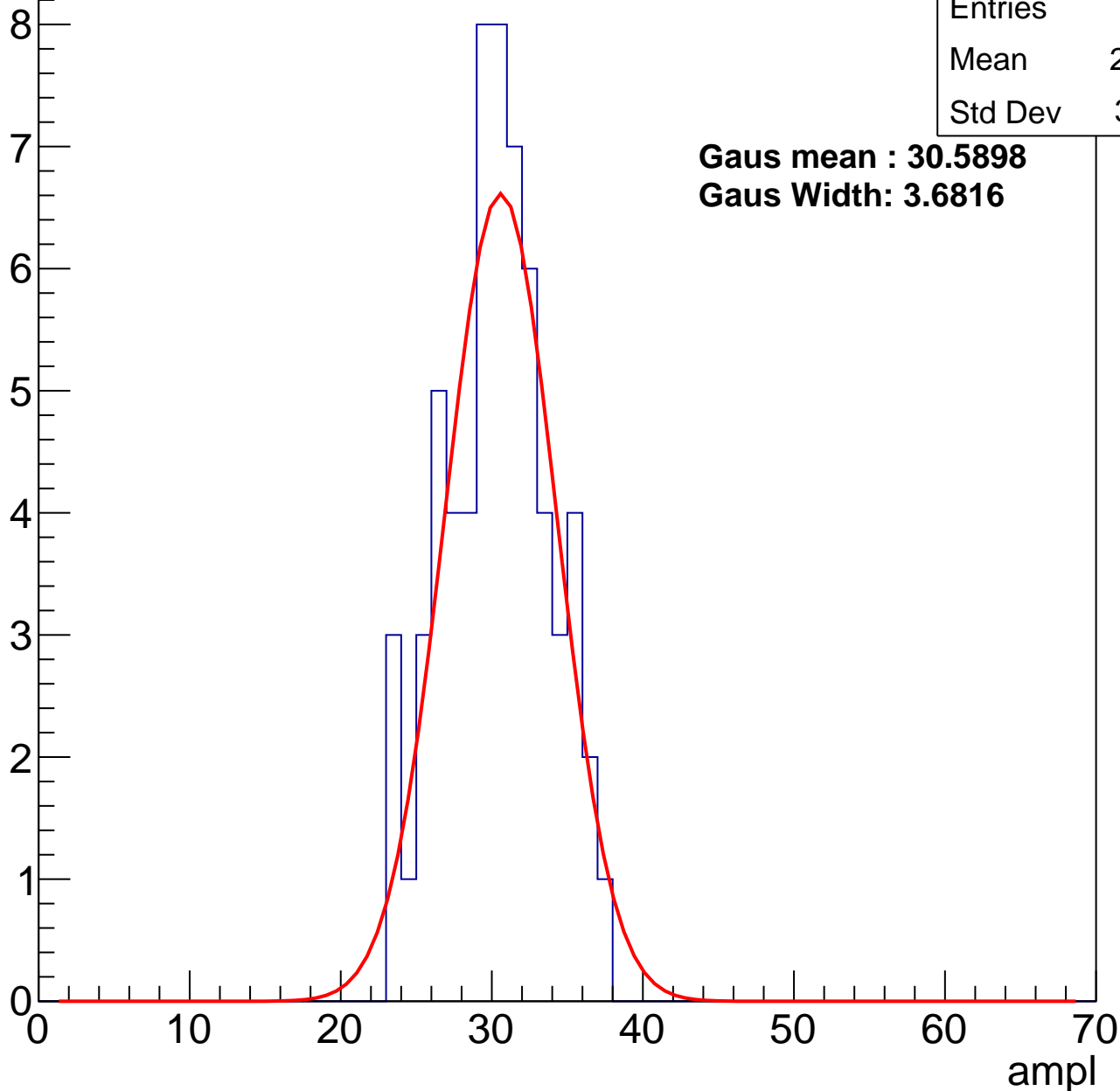
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.87
Std Dev	3.411

Gaus mean : 30.5898

Gaus Width: 3.6816



B0L002S, U17-ch45, adc1

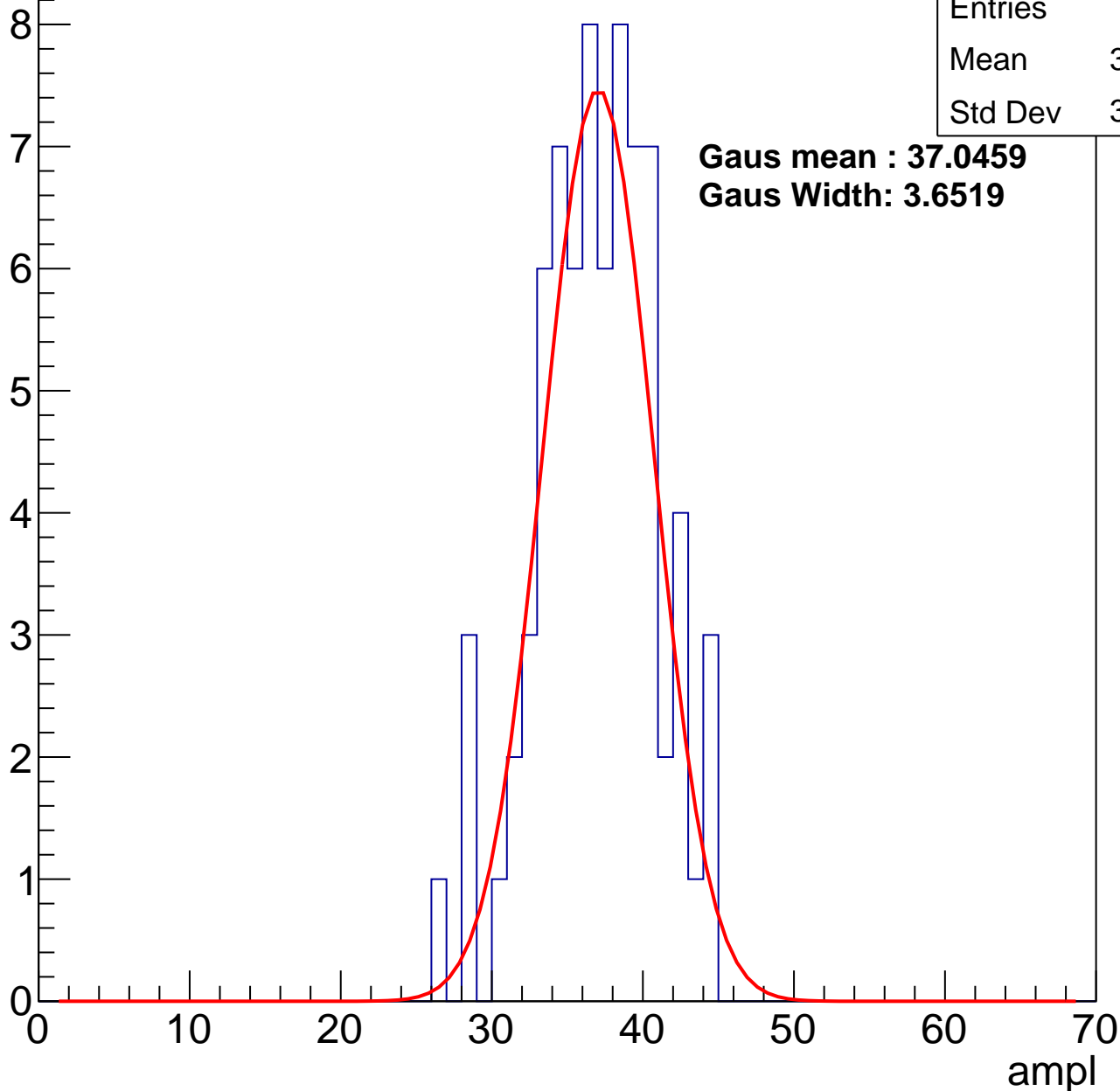
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	36.48
Std Dev	3.907

Gaus mean : 37.0459

Gaus Width: 3.6519



B0L002S, U17-ch45, adc2

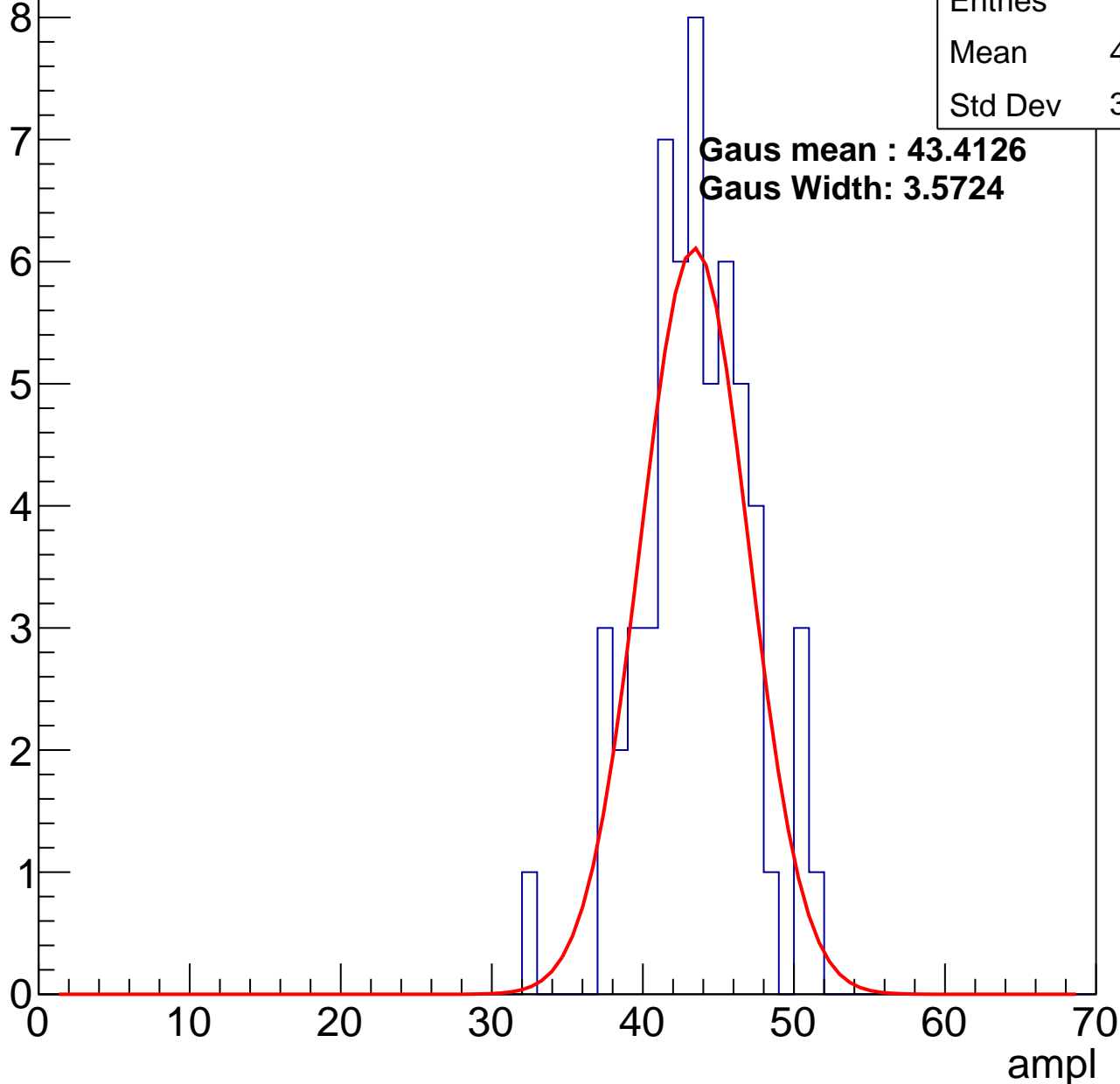
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.03
Std Dev	3.624

Gaus mean : 43.4126

Gaus Width: 3.5724



B0L002S, U17-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1

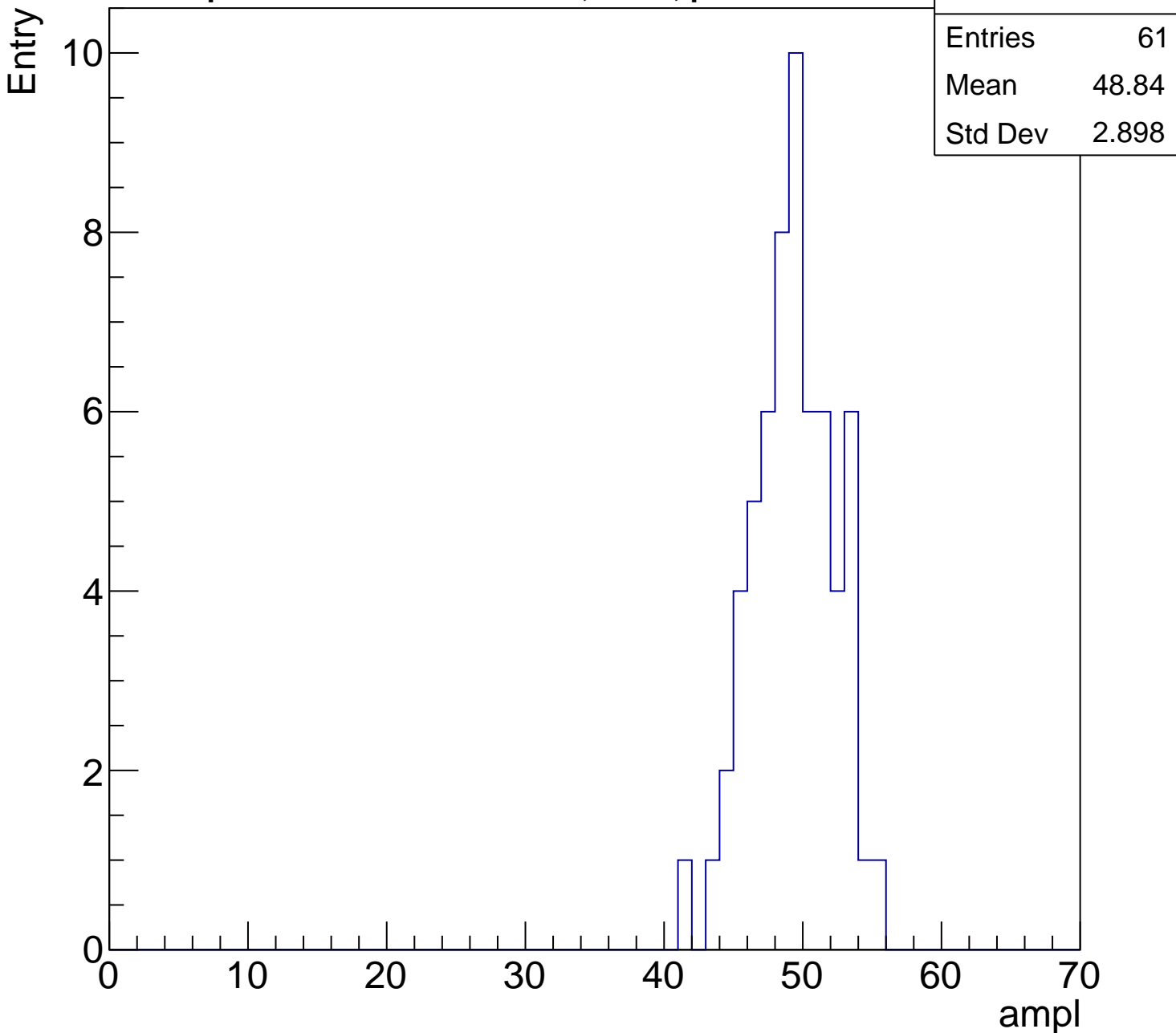
Entries	61
Mean	48.84
Std Dev	2.898

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

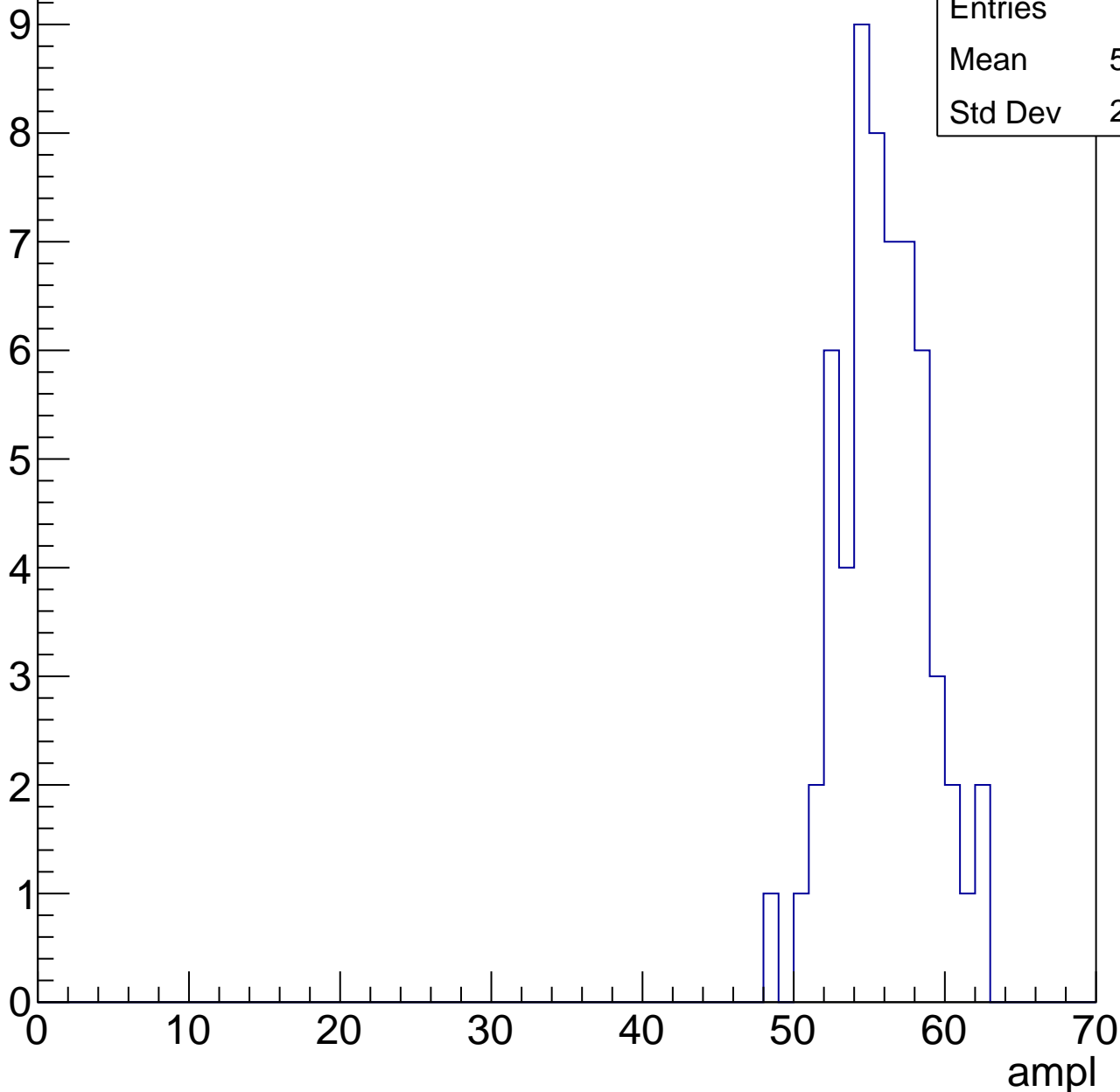


B0L002S, U17-ch45, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	55.44
Std Dev	2.895

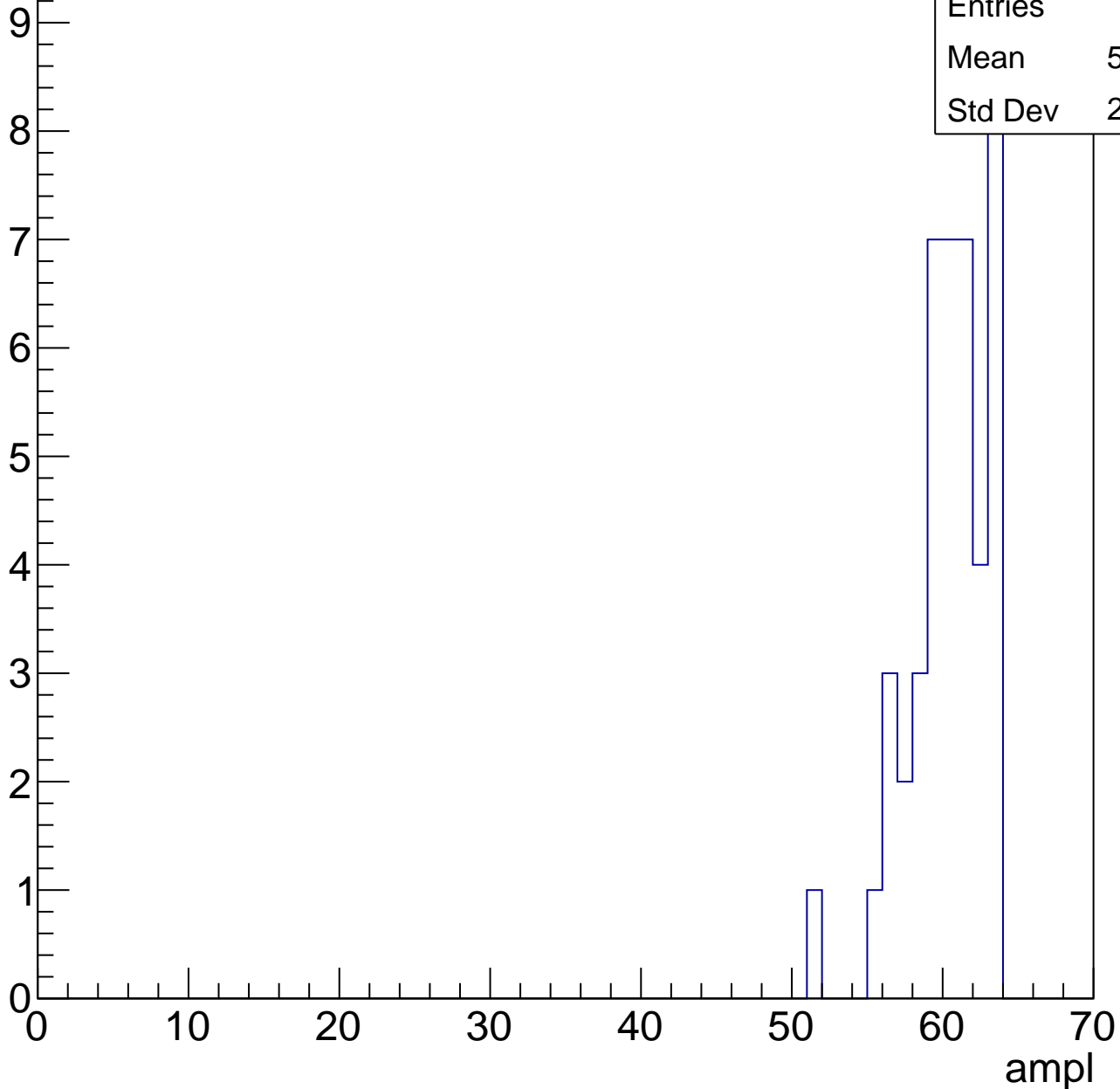


B0L002S, U17-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	59.93
Std Dev	2.588



B0L002S, U17-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

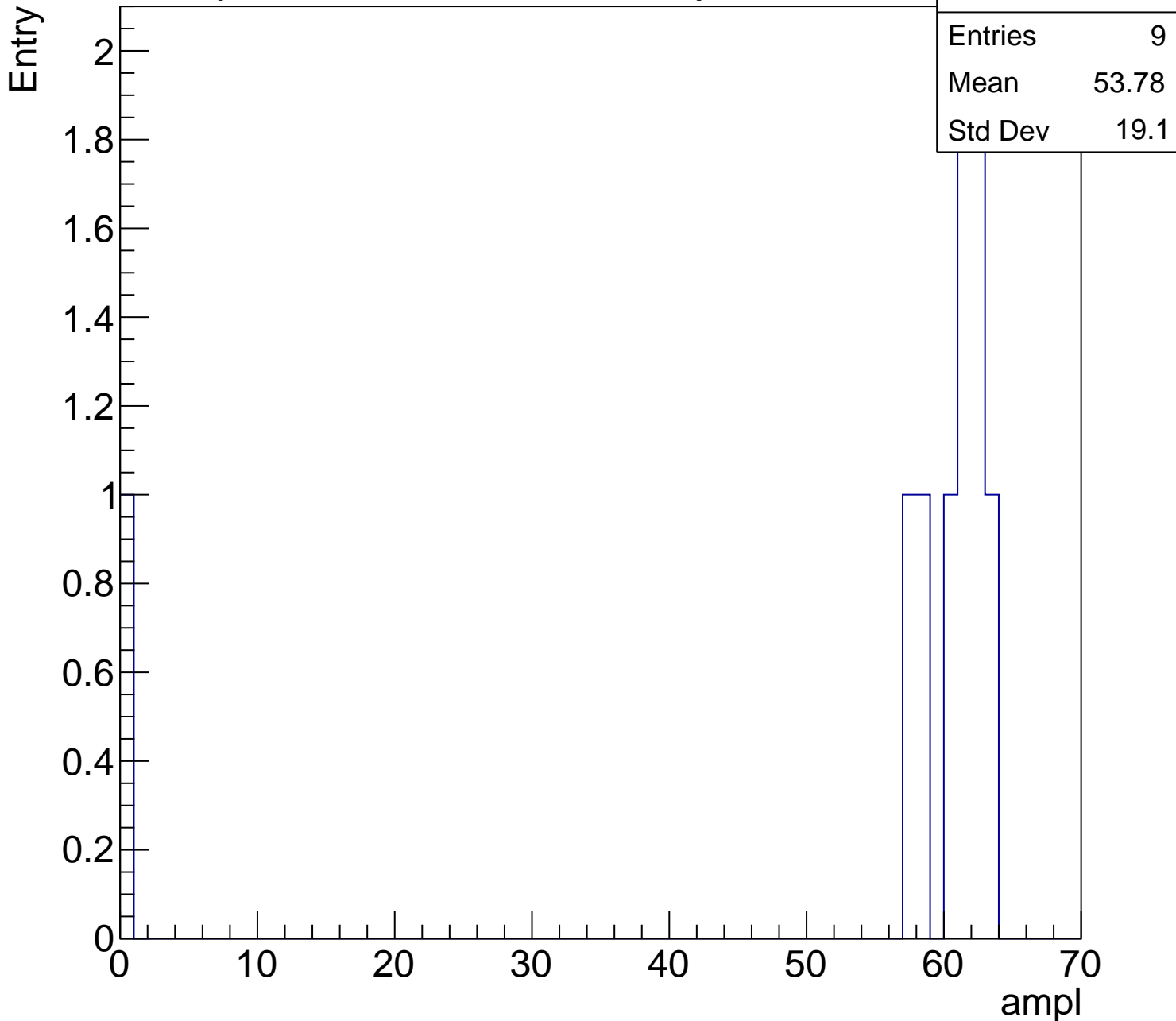
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	53.78
Std Dev	19.1

ampl

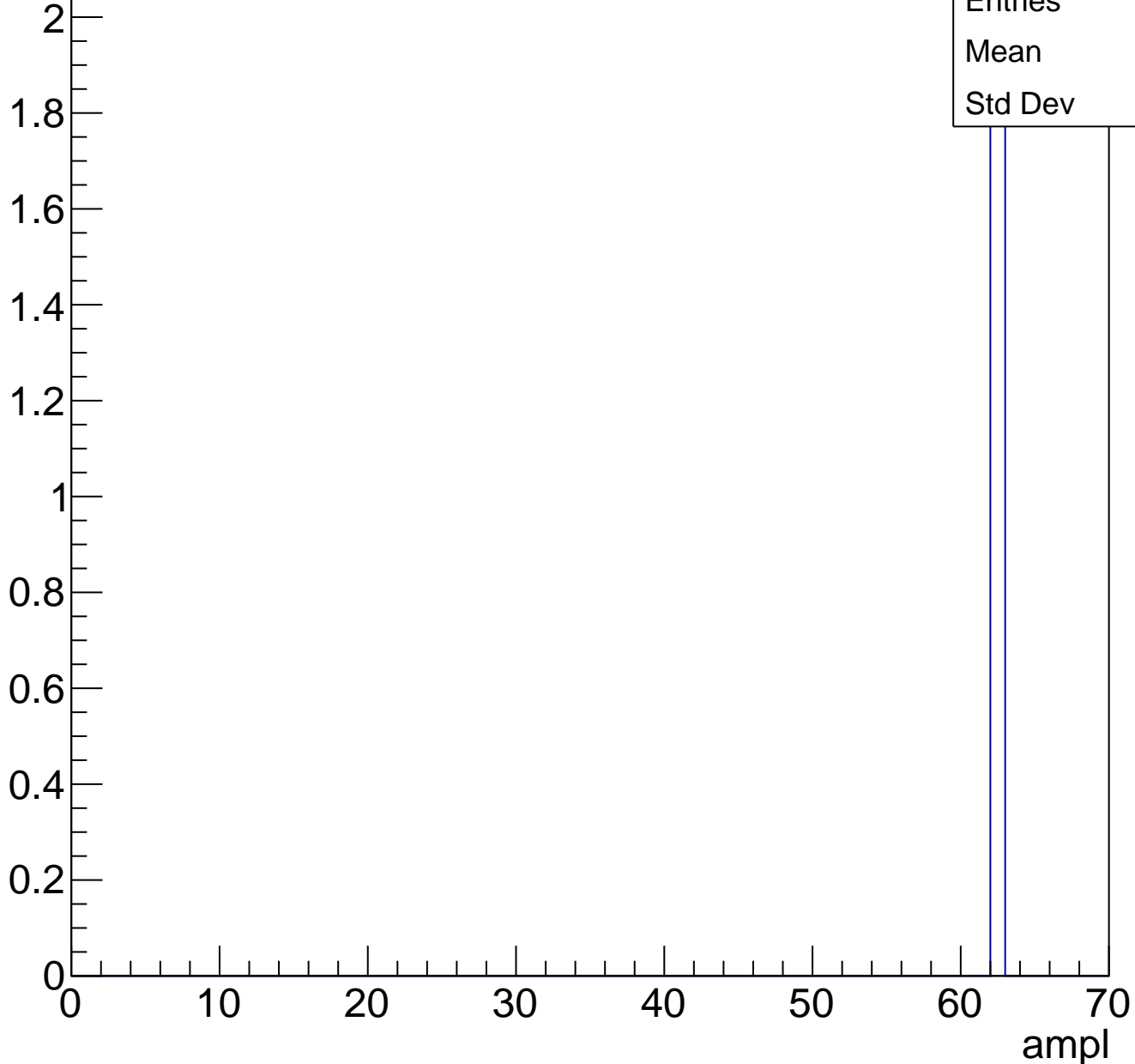
0 10 20 30 40 50 60 70



B0L002S, U17-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch46, adc0

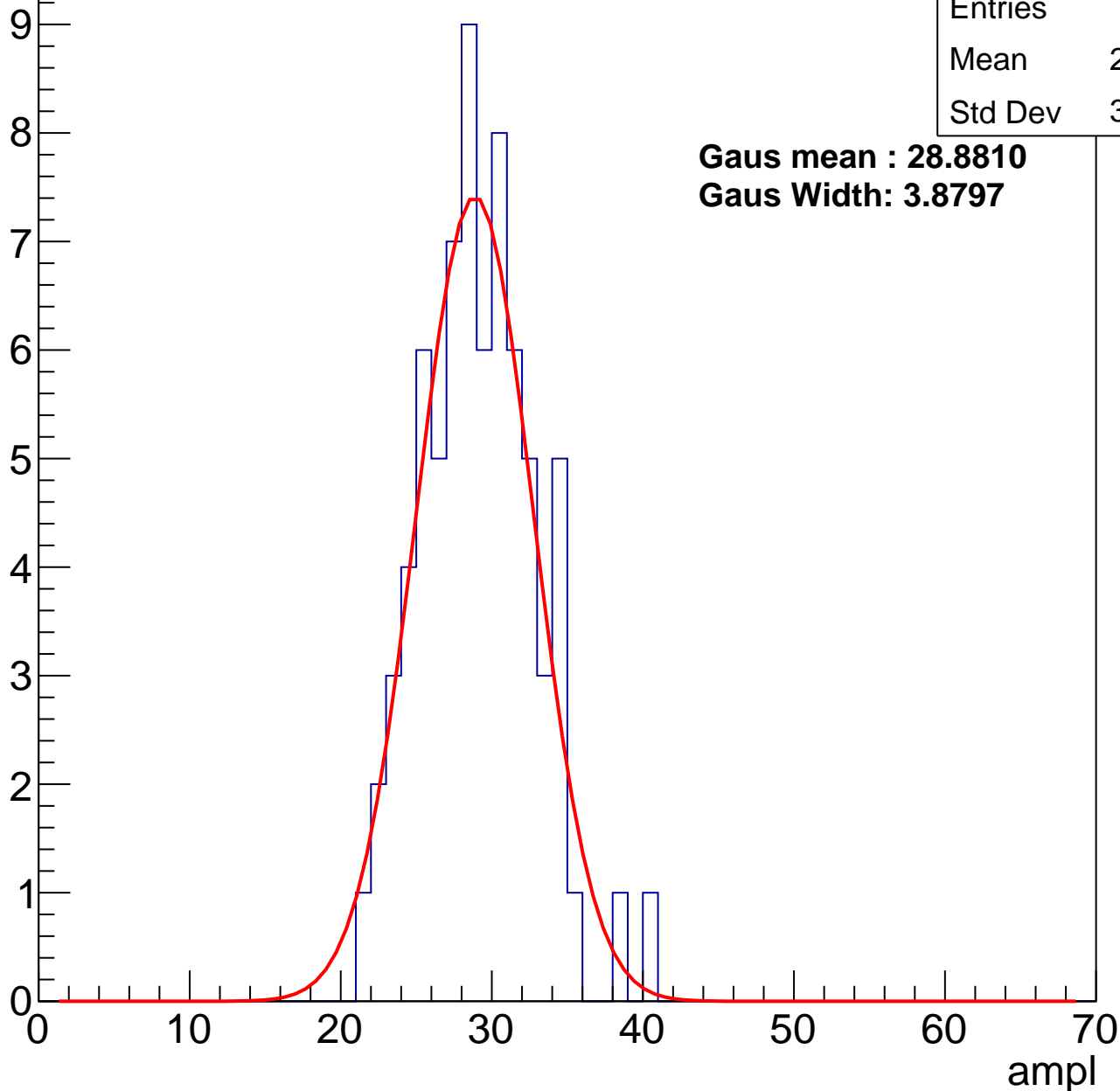
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	28.67
Std Dev	3.753

Gaus mean : 28.8810

Gaus Width: 3.8797



B0L002S, U17-ch46, adc1

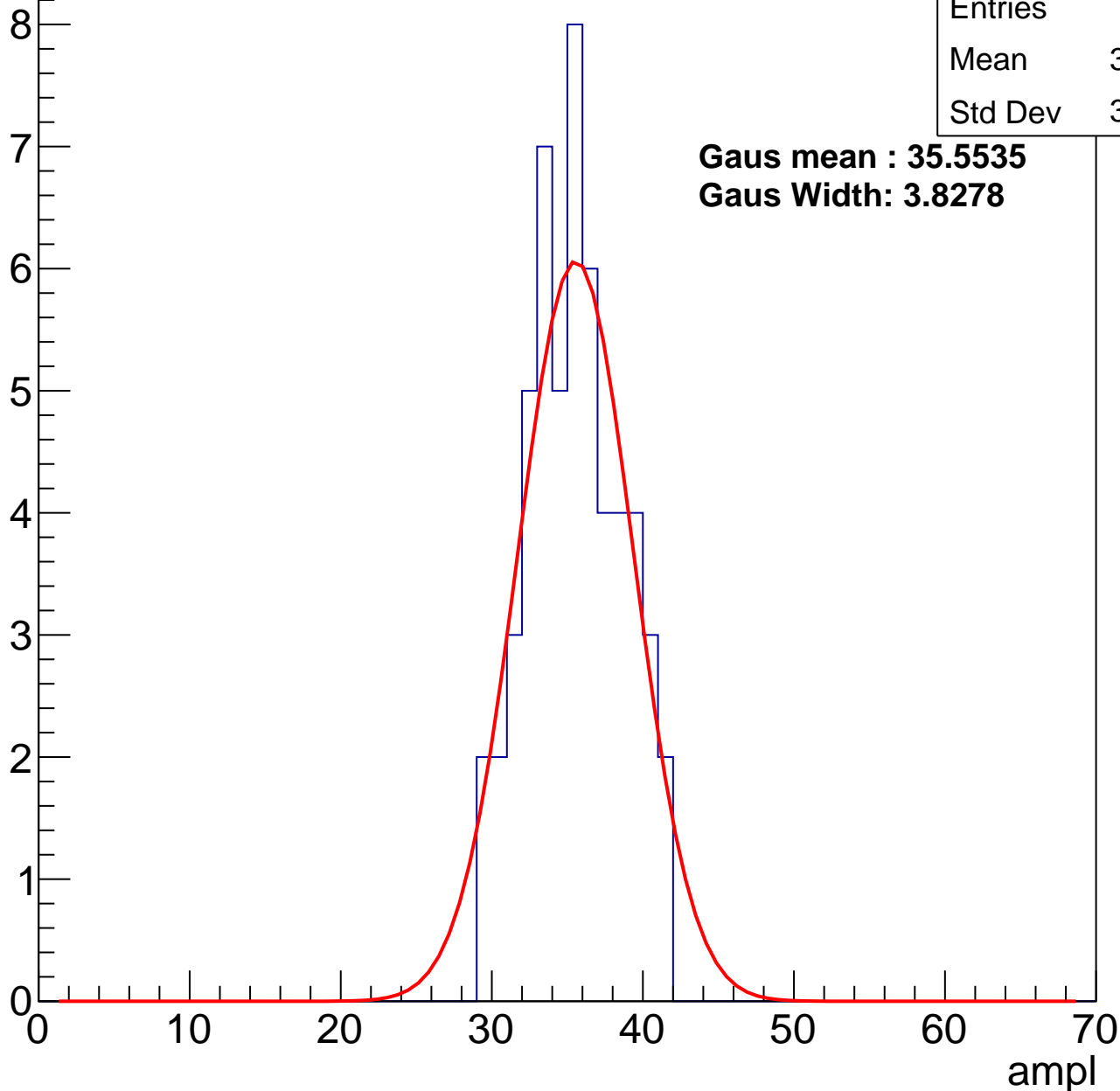
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	35.02
Std Dev	3.066

Gaus mean : 35.5535

Gaus Width: 3.8278



B0L002S, U17-ch46, adc2

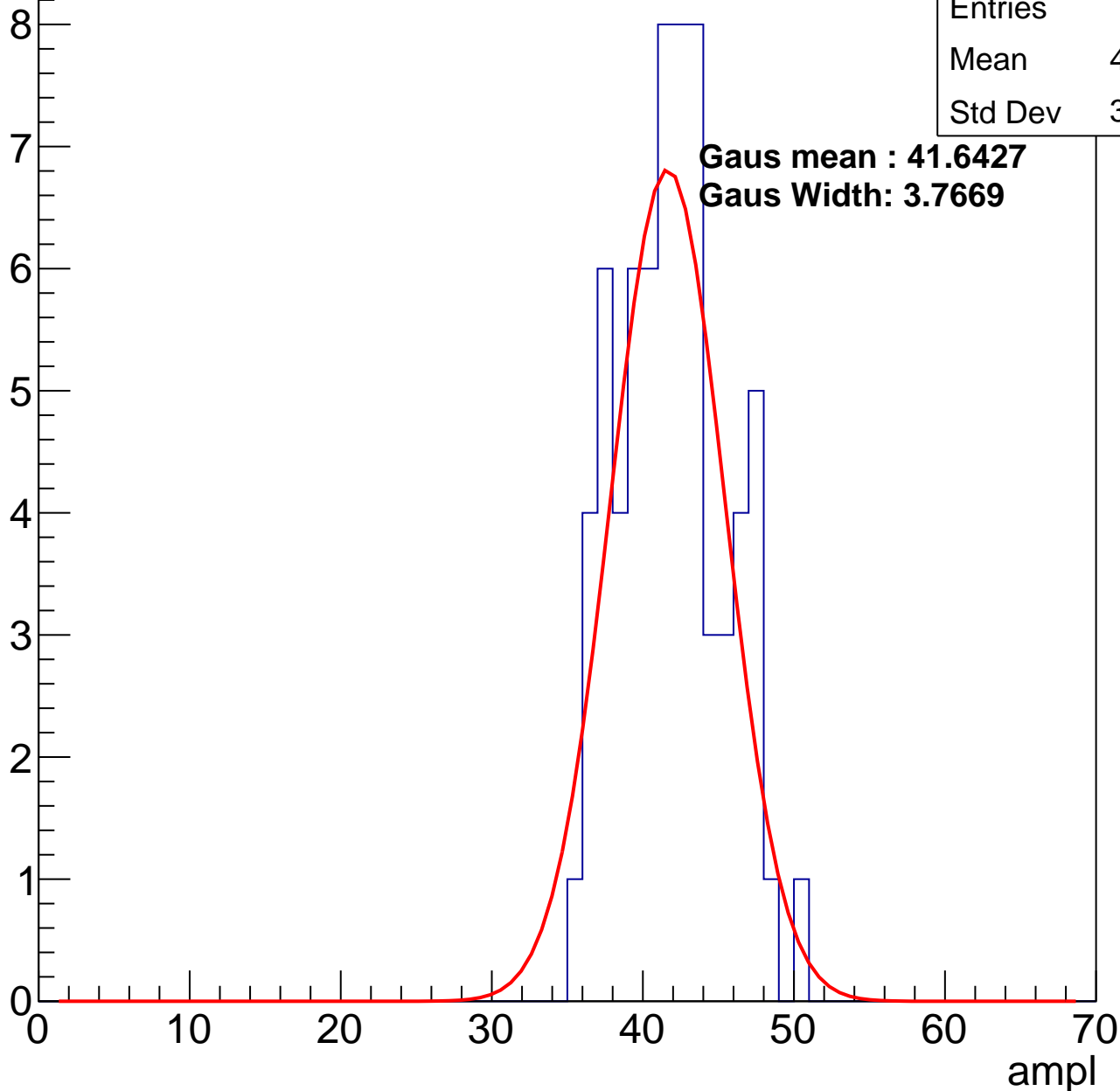
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	41.46
Std Dev	3.449

Gaus mean : 41.6427

Gaus Width: 3.7669

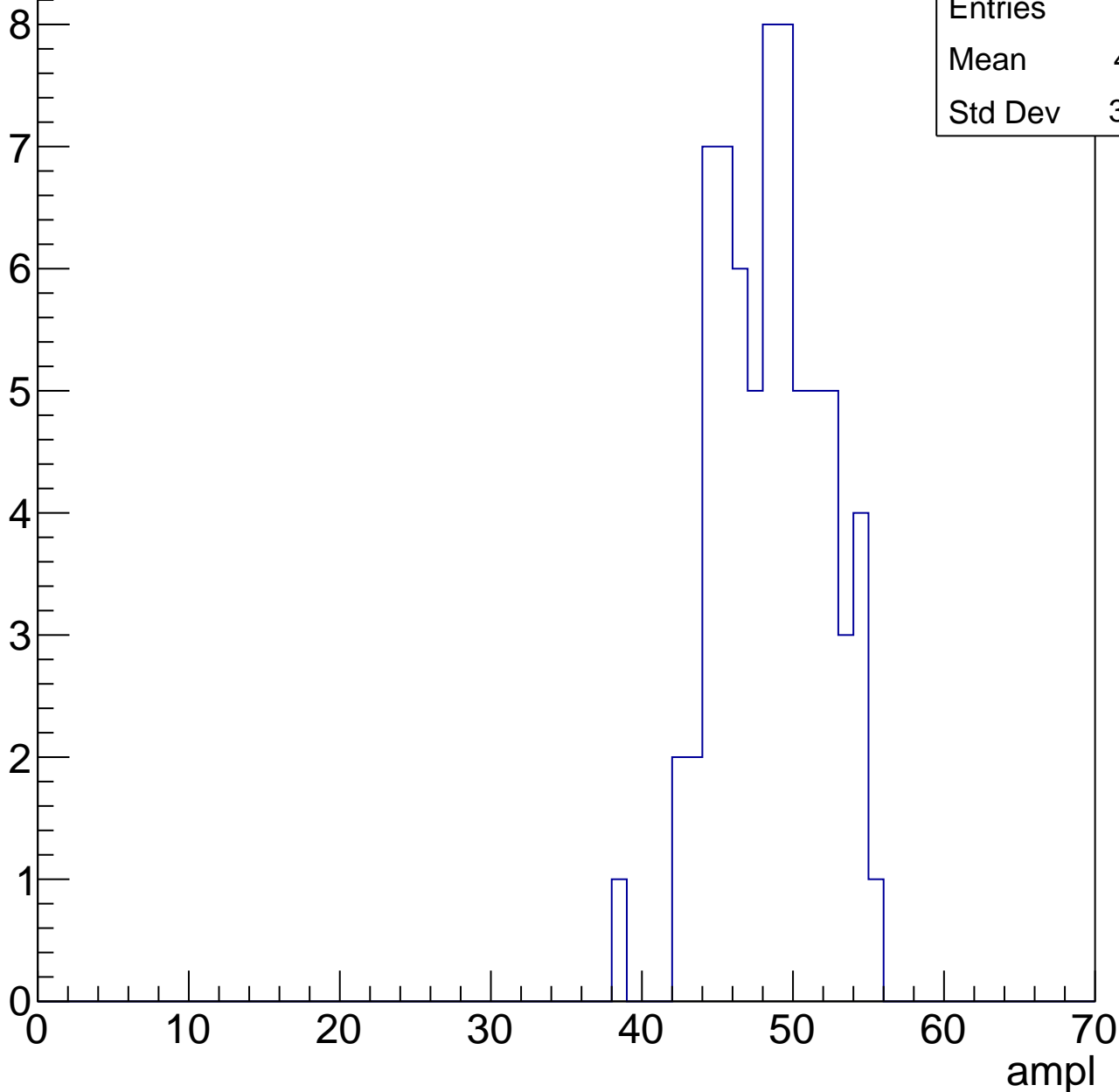


B0L002S, U17-ch46, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	48.01
Std Dev	3.504

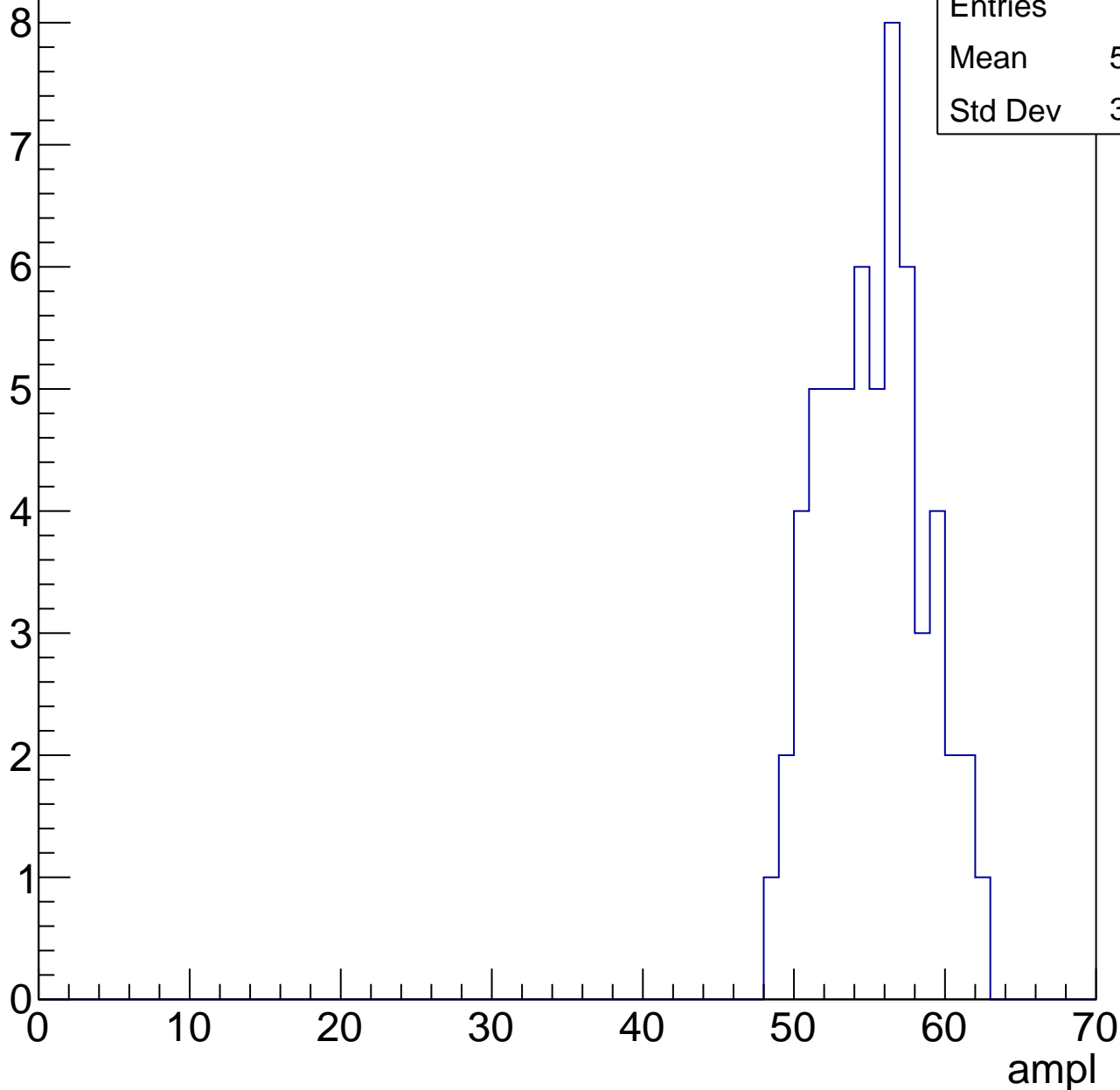


B0L002S, U17-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	54.73
Std Dev	3.349

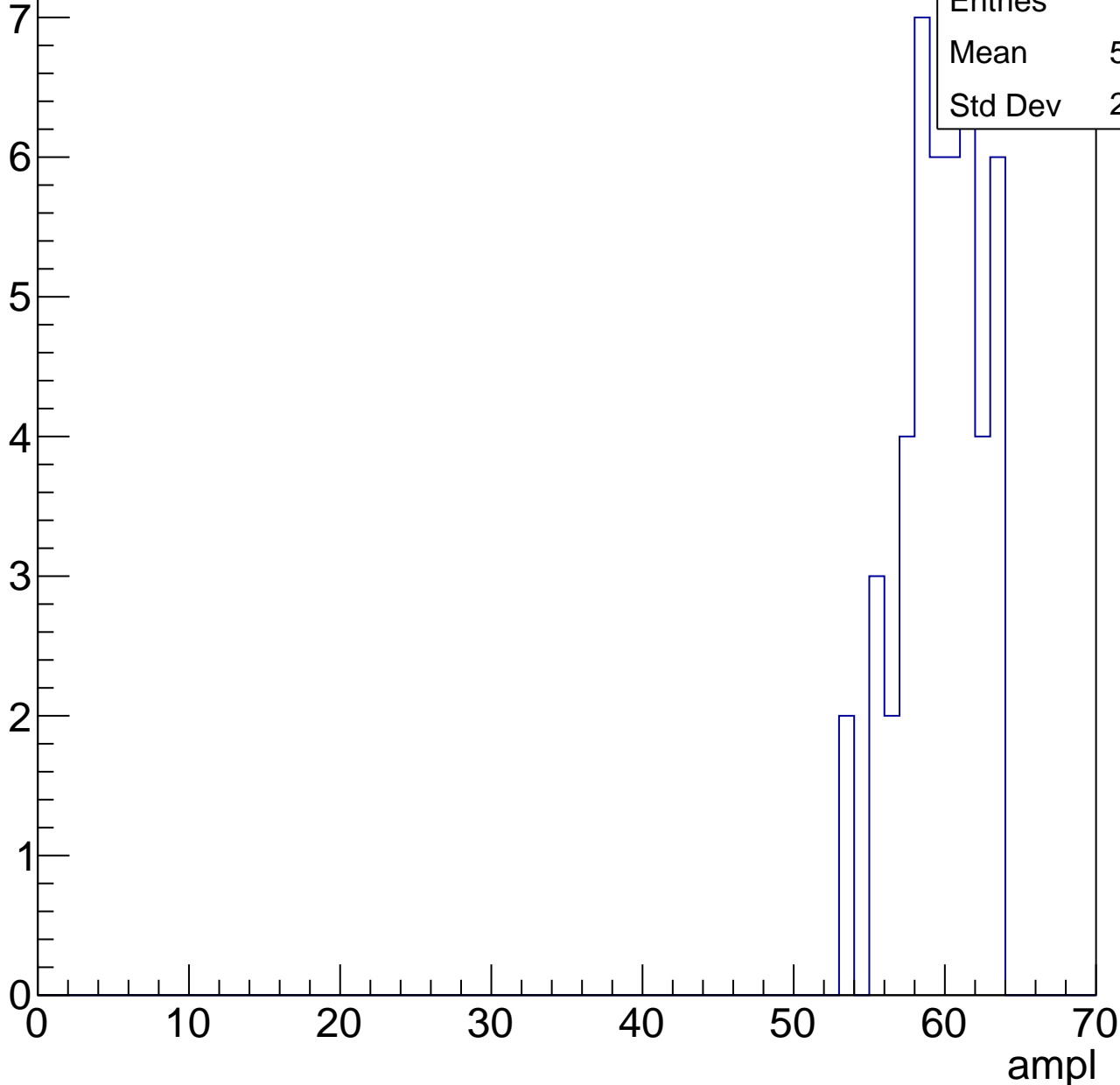


B0L002S, U17-ch46, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

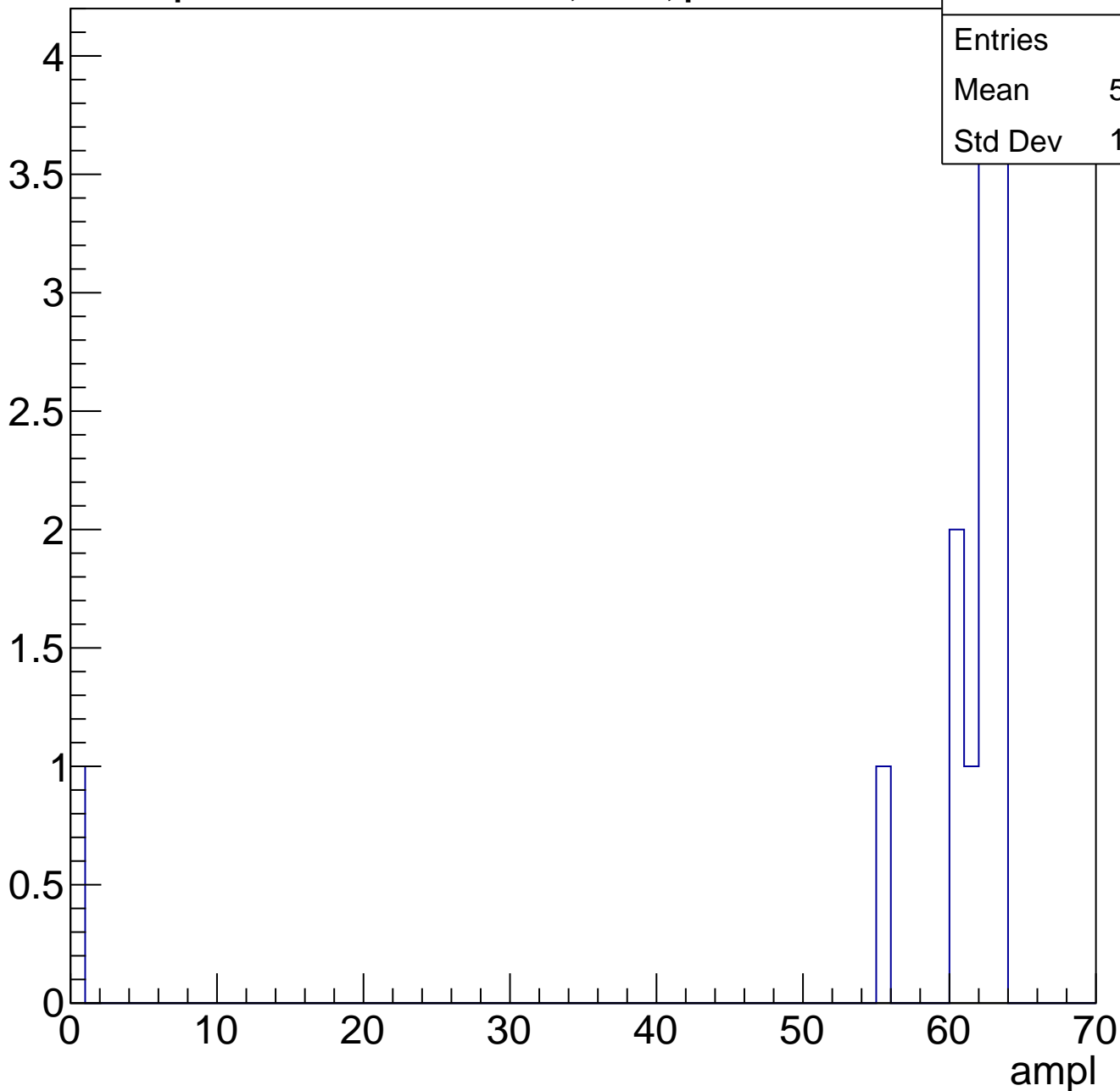
Entries	47
Mean	59.23
Std Dev	2.627



B0L002S, U17-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

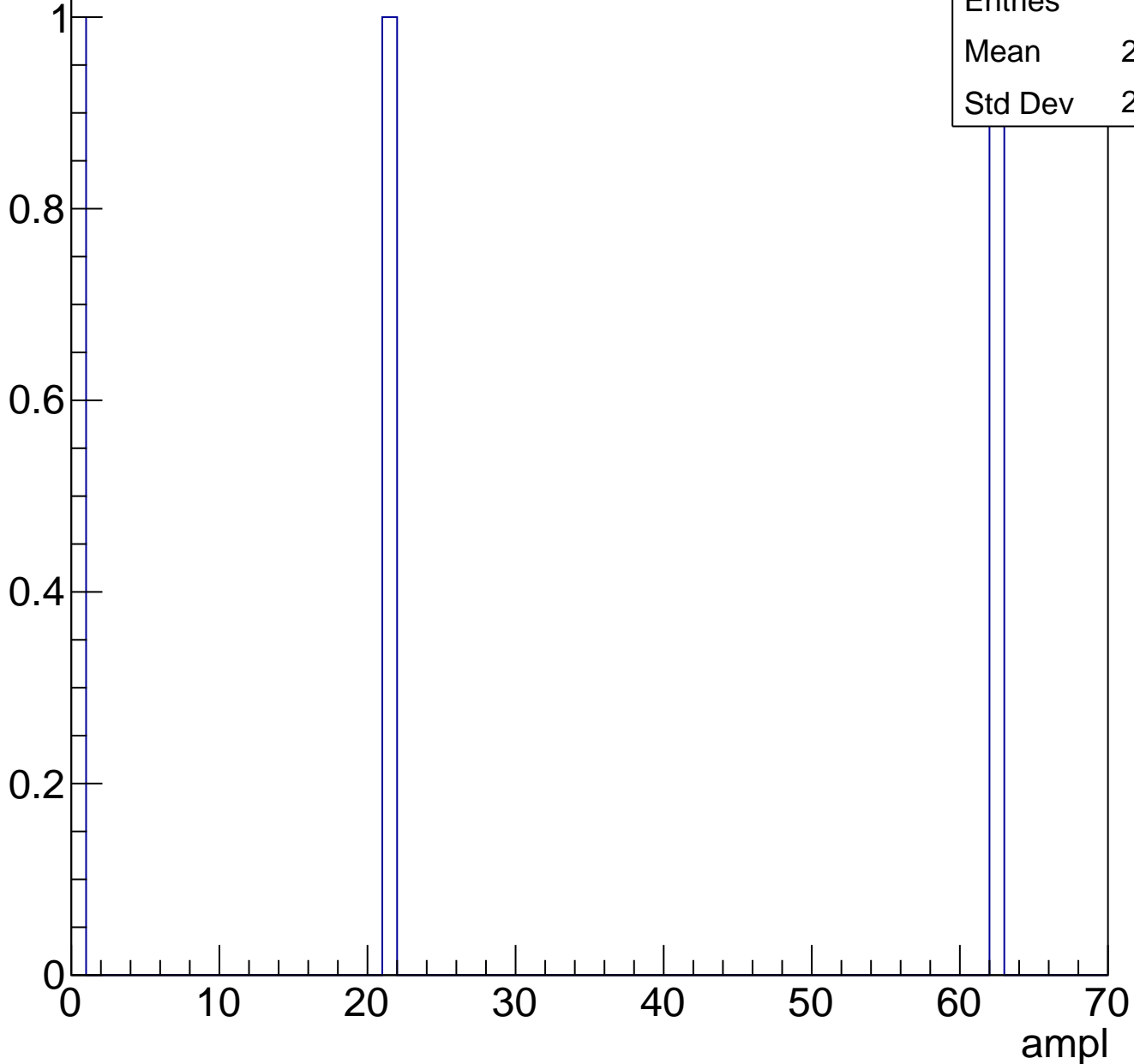
Entry



B0L002S, U17-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	27.67
Std Dev	25.75

B0L002S, U17-ch47, adc0

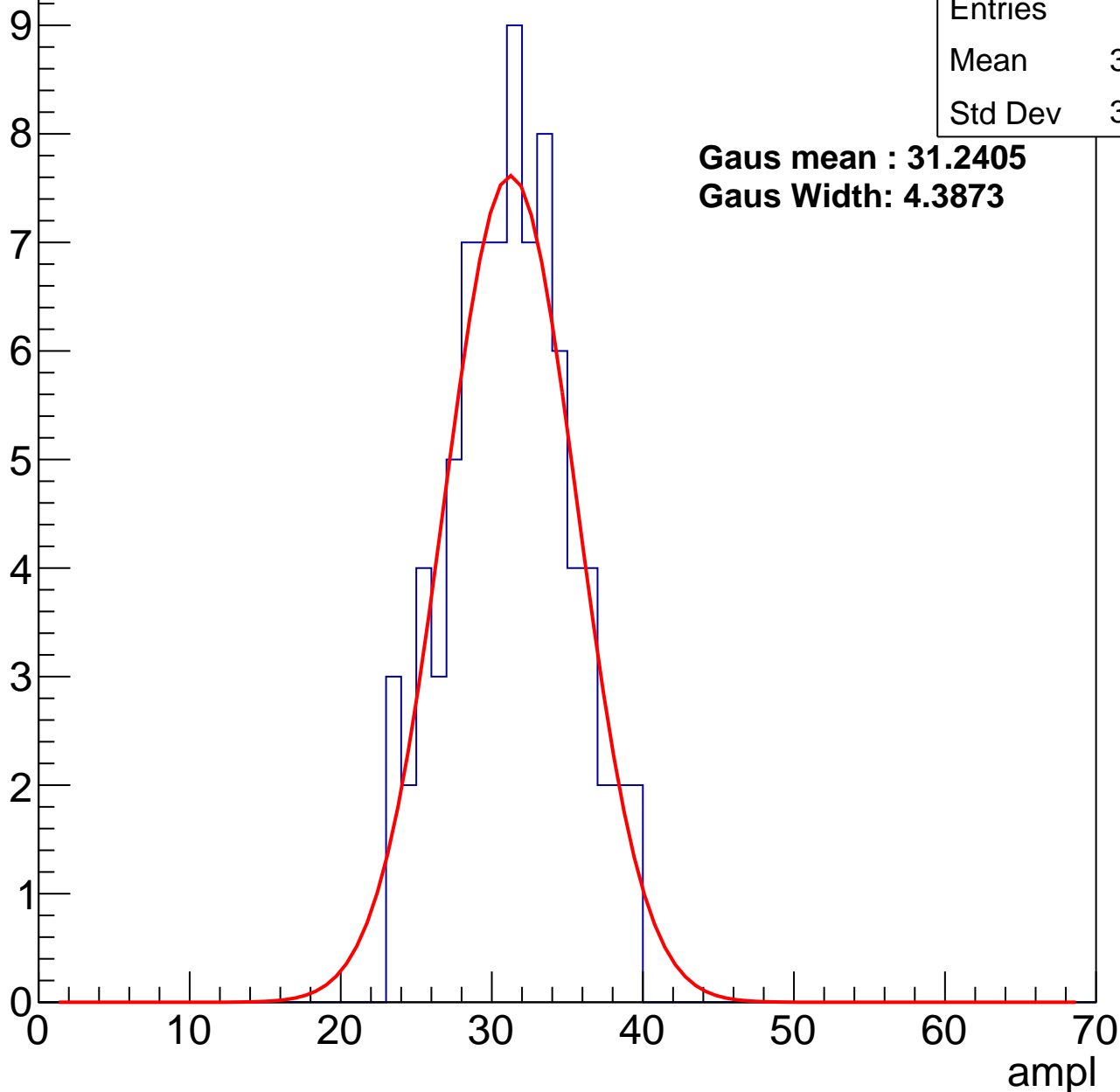
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	30.76
Std Dev	3.884

Gaus mean : 31.2405

Gaus Width: 4.3873



B0L002S, U17-ch47, adc1

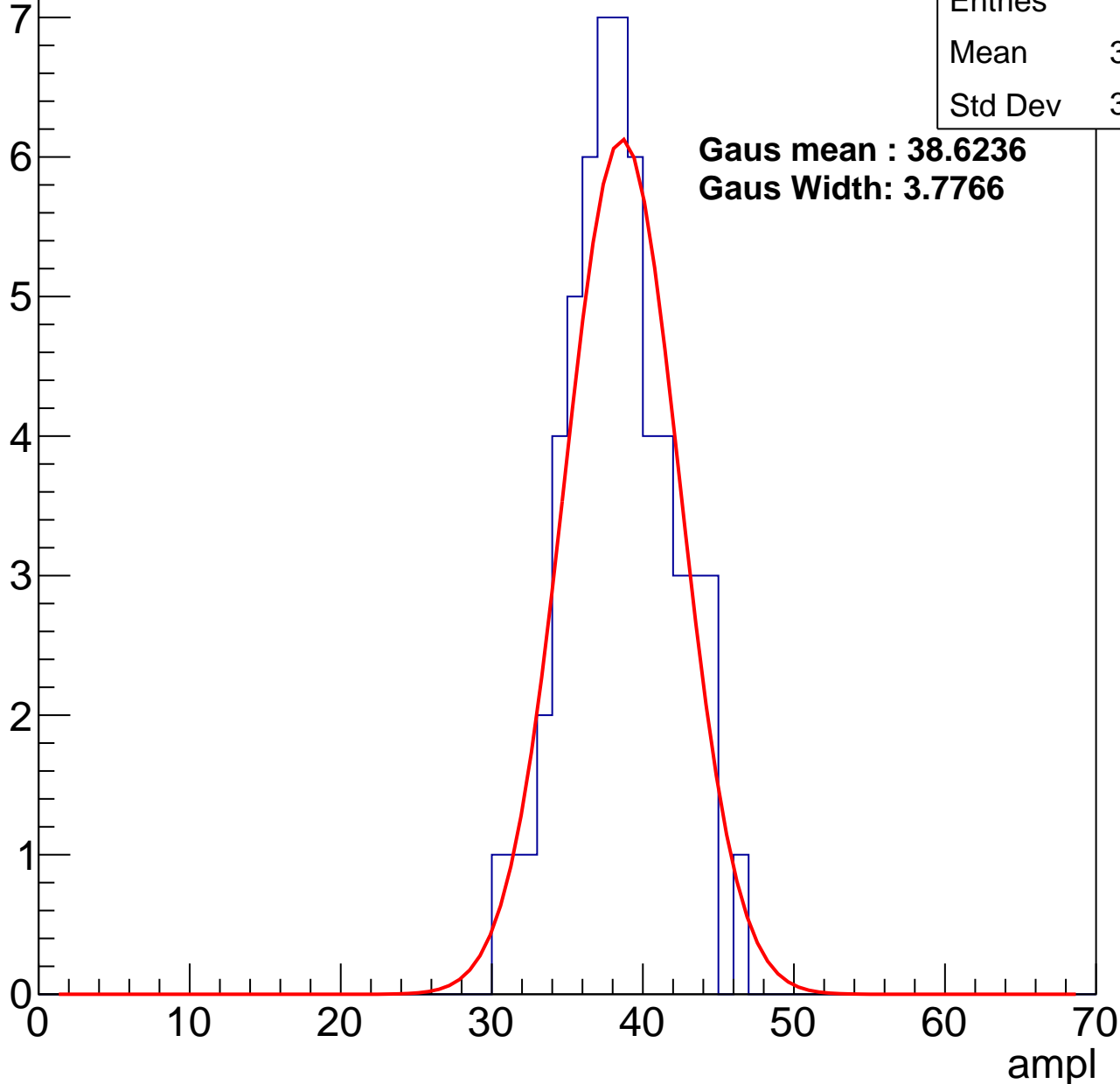
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.97
Std Dev	3.454

Gaus mean : 38.6236

Gaus Width: 3.7766



B0L002S, U17-ch47, adc2

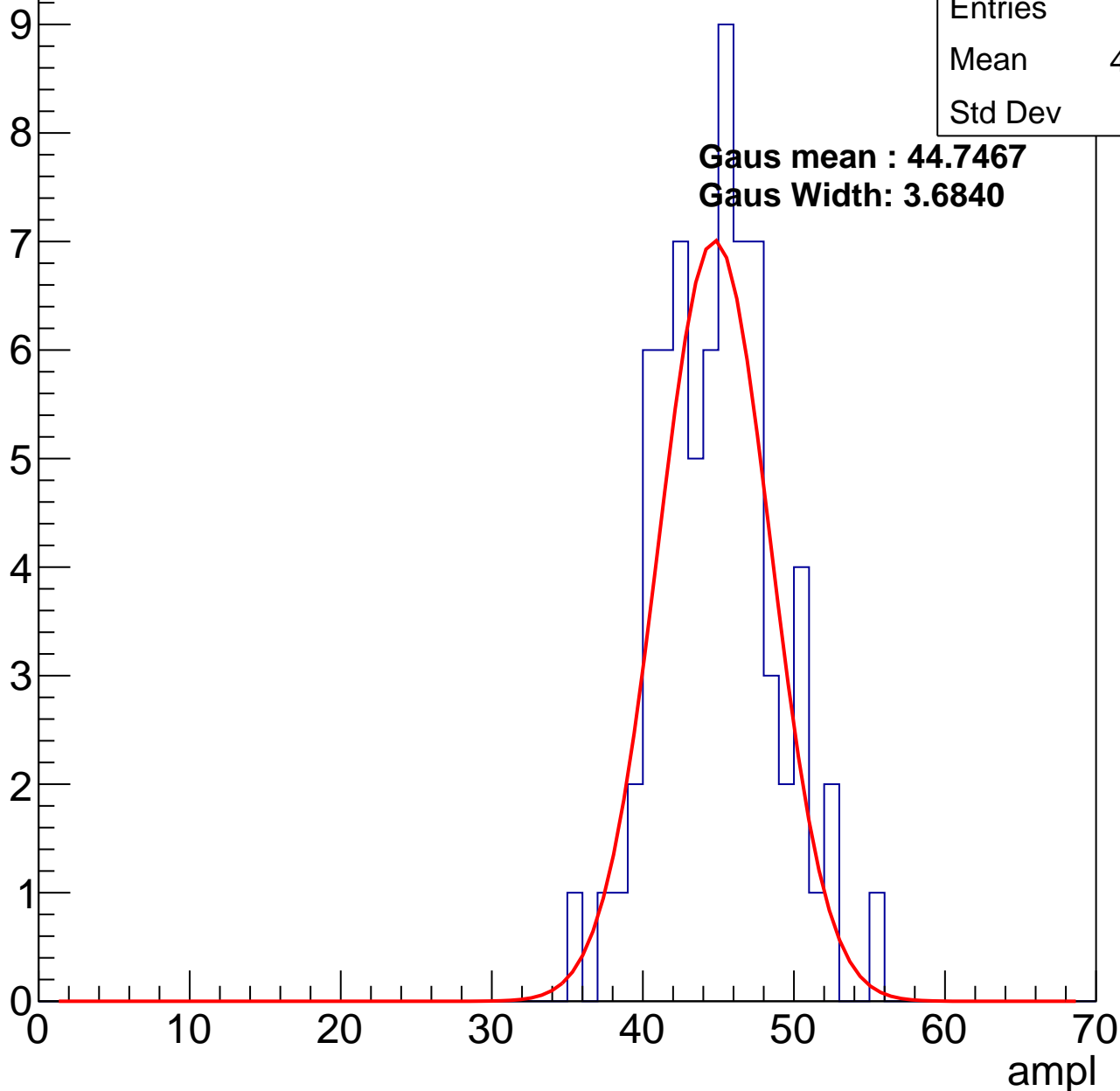
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.44
Std Dev	3.8

Gaus mean : 44.7467

Gaus Width: 3.6840

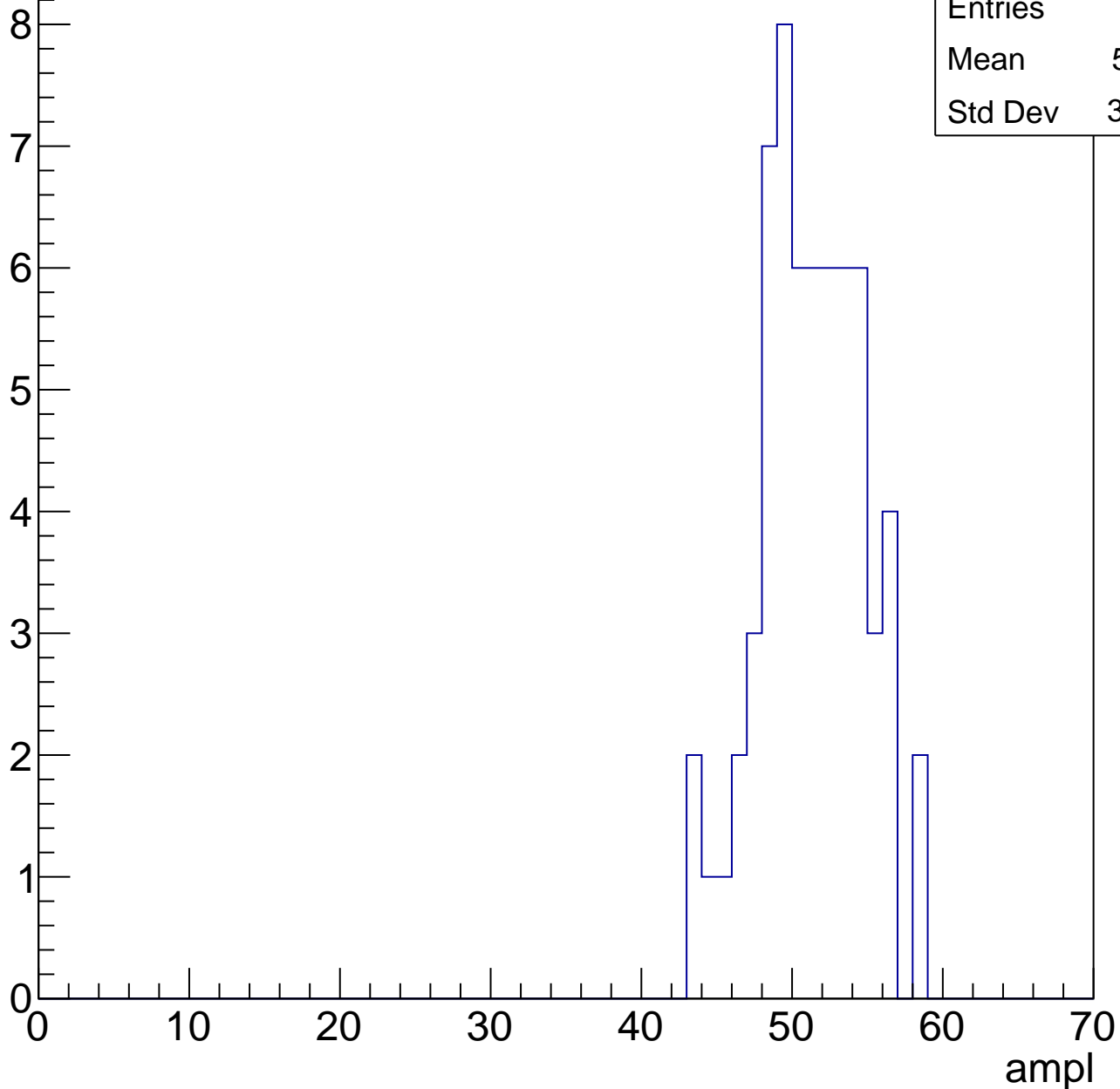


B0L002S, U17-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	50.81
Std Dev	3.427



B0L002S, U17-ch47, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	47
Mean	56.34
Std Dev	2.897

Entry

10

8

6

4

2

0

0

10

20

30

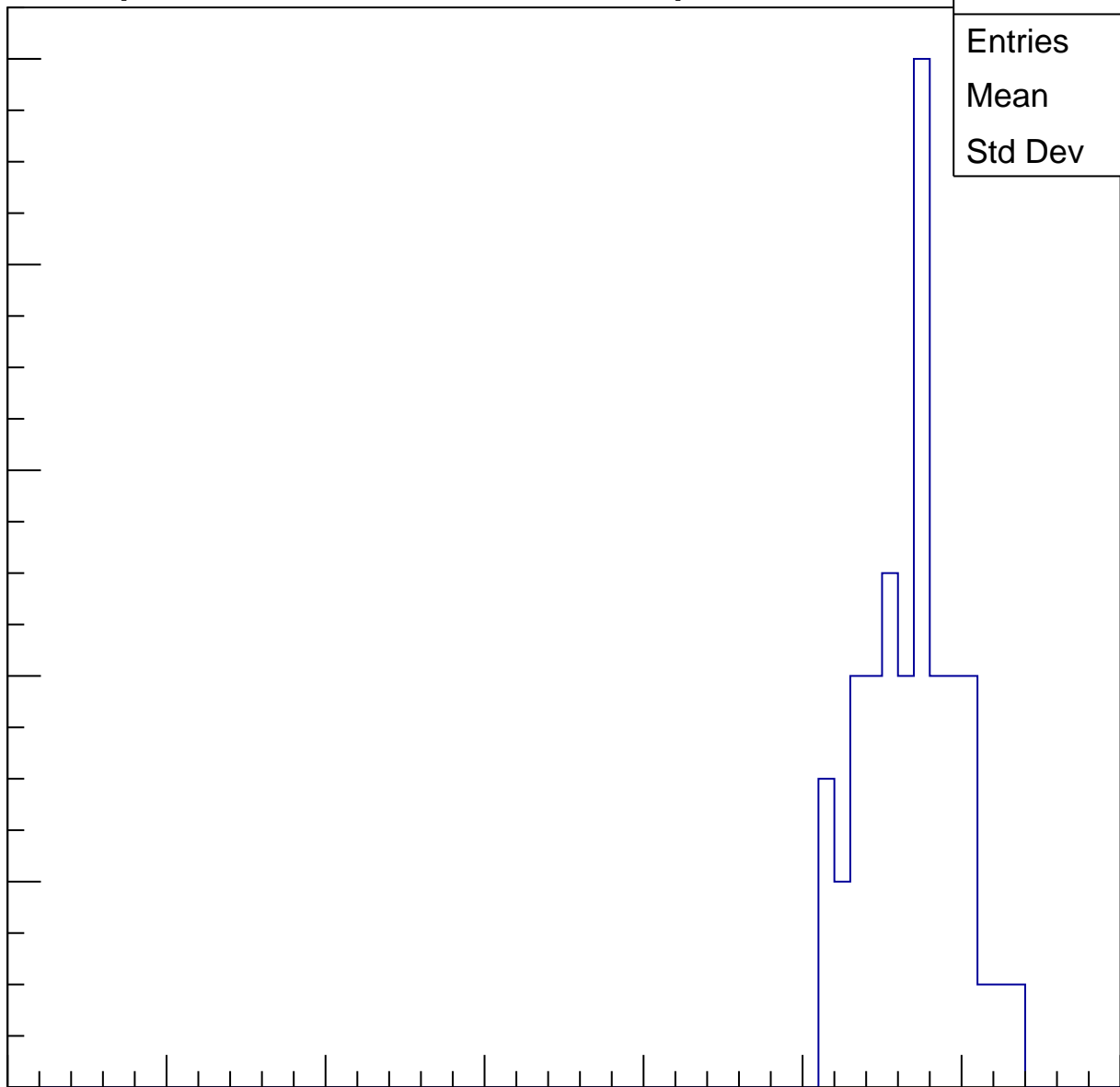
40

50

60

70

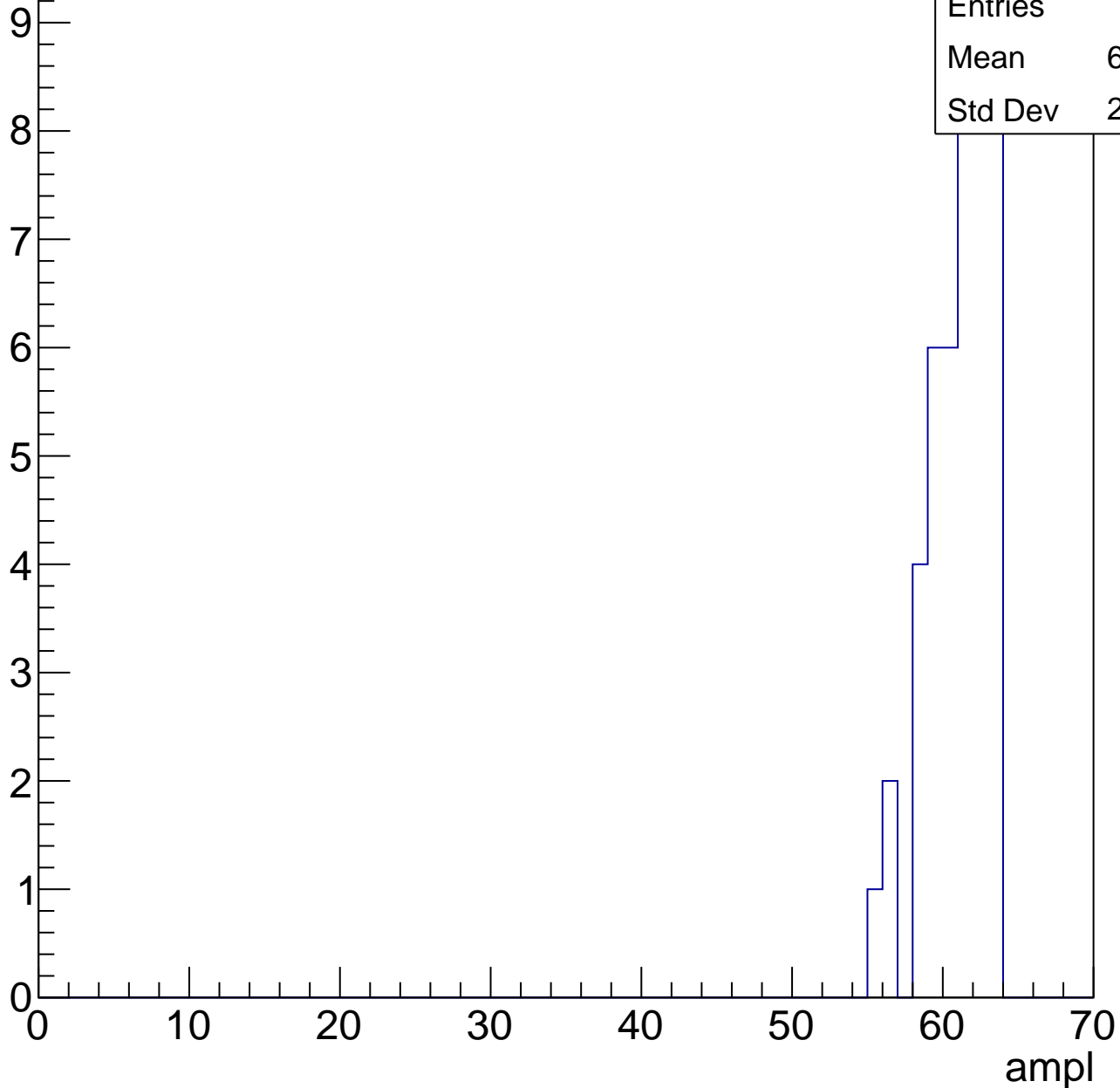
ampl



B0L002S, U17-ch47, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch48, adc0

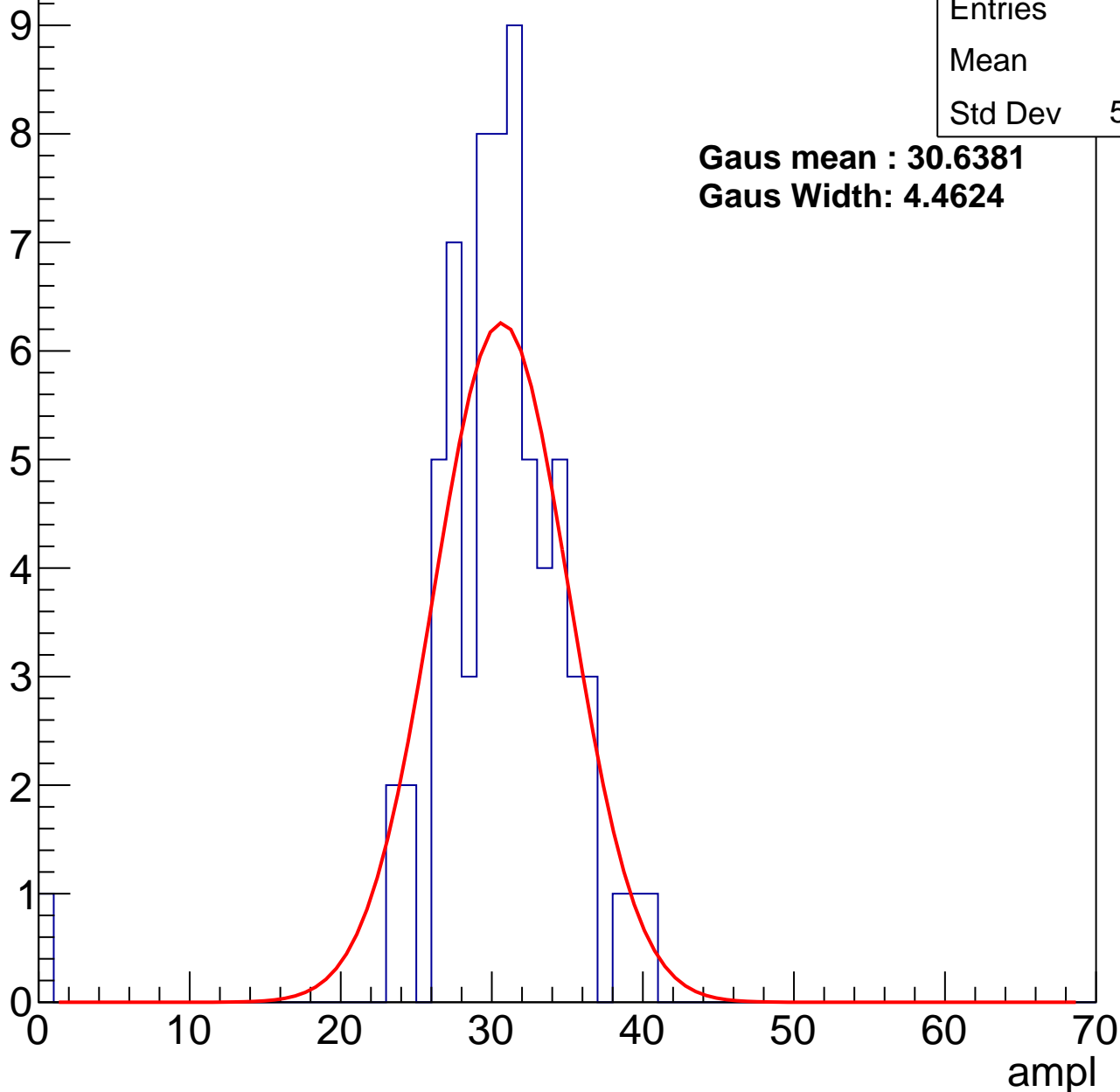
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30
Std Dev	5.154

Gaus mean : 30.6381

Gaus Width: 4.4624



B0L002S, U17-ch48, adc1

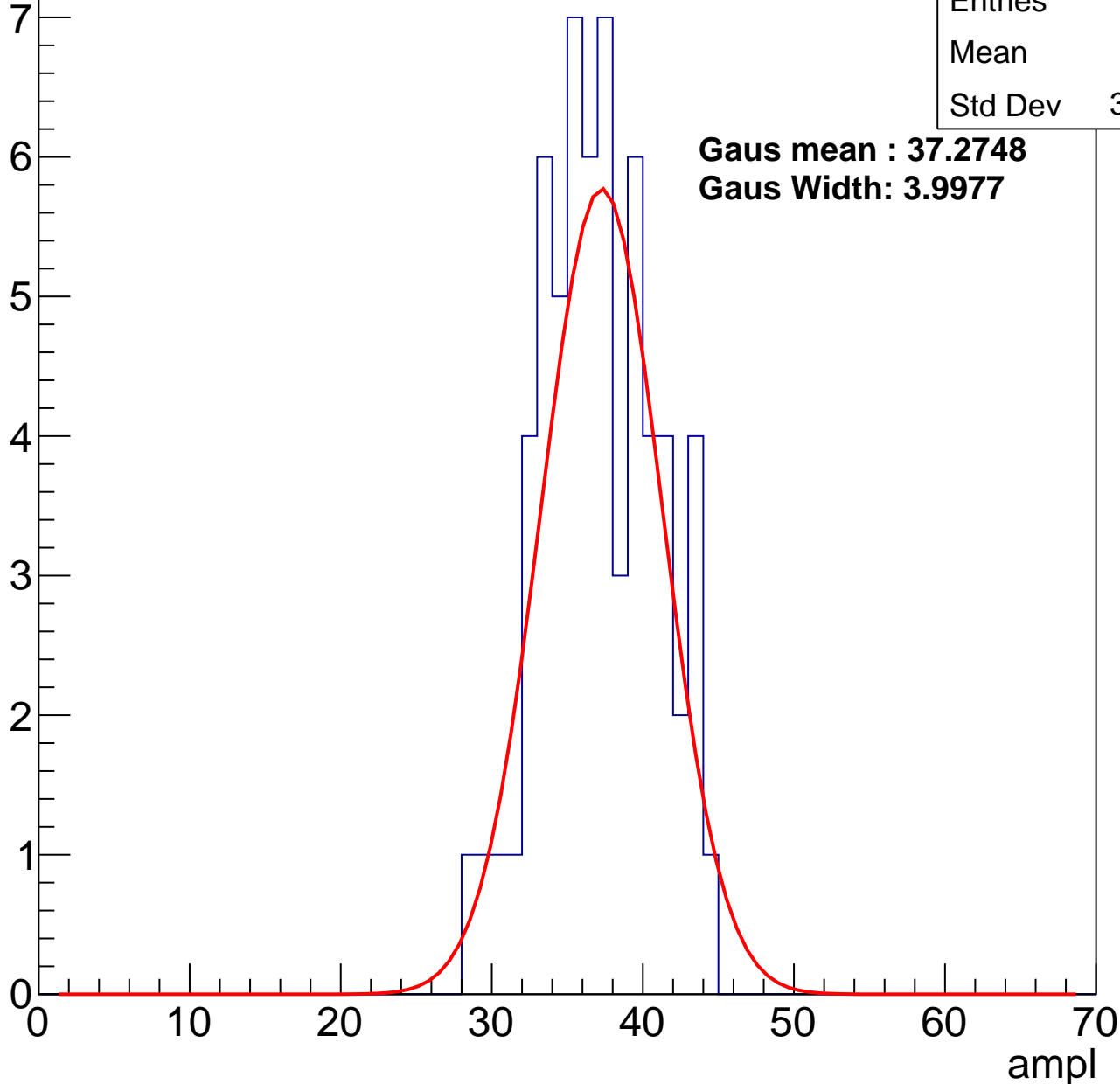
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.6
Std Dev	3.706

Gaus mean : 37.2748

Gaus Width: 3.9977



B0L002S, U17-ch48, adc2

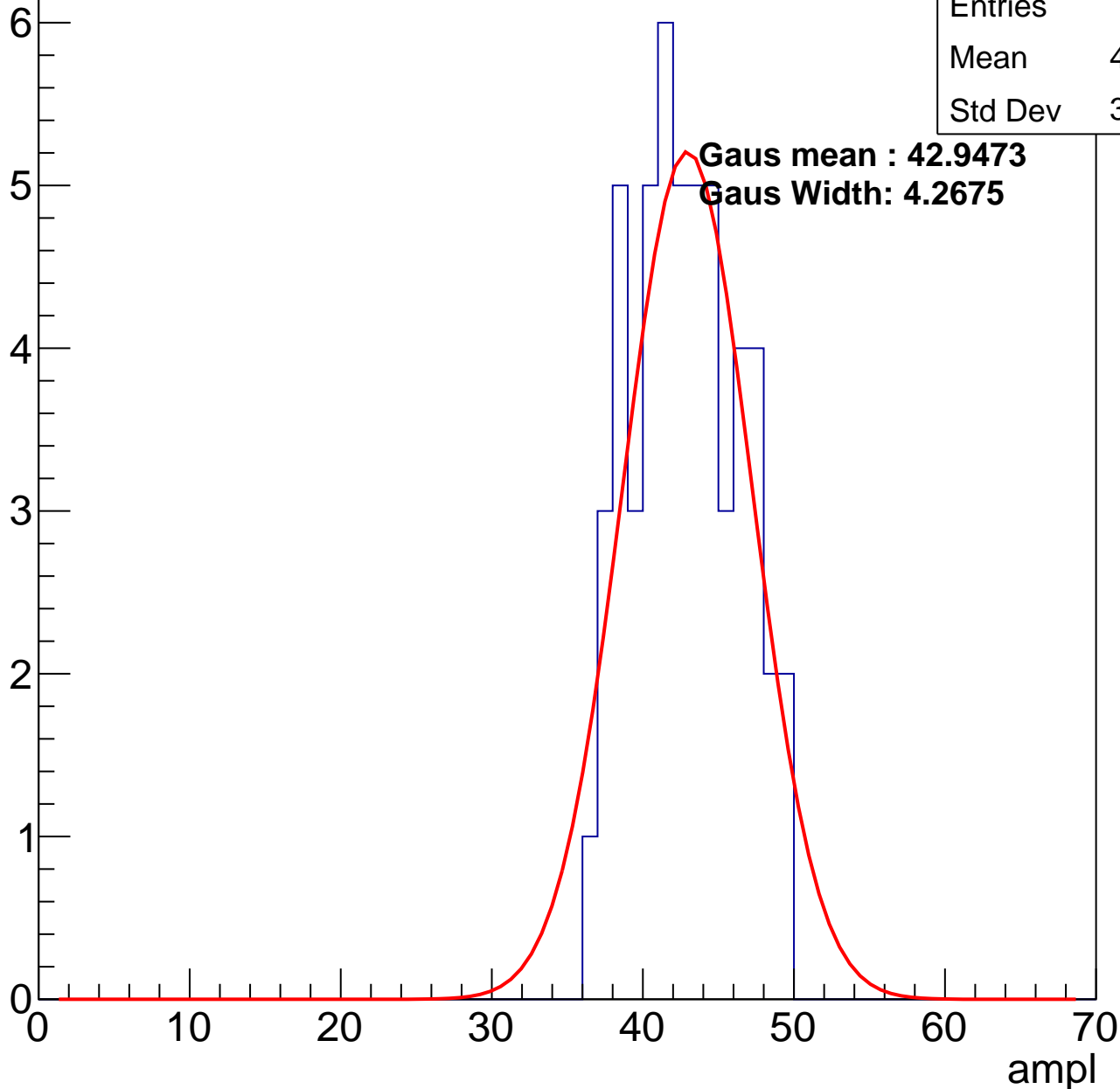
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	42.38
Std Dev	3.427

Gaus mean : 42.9473

Gaus Width: 4.2675

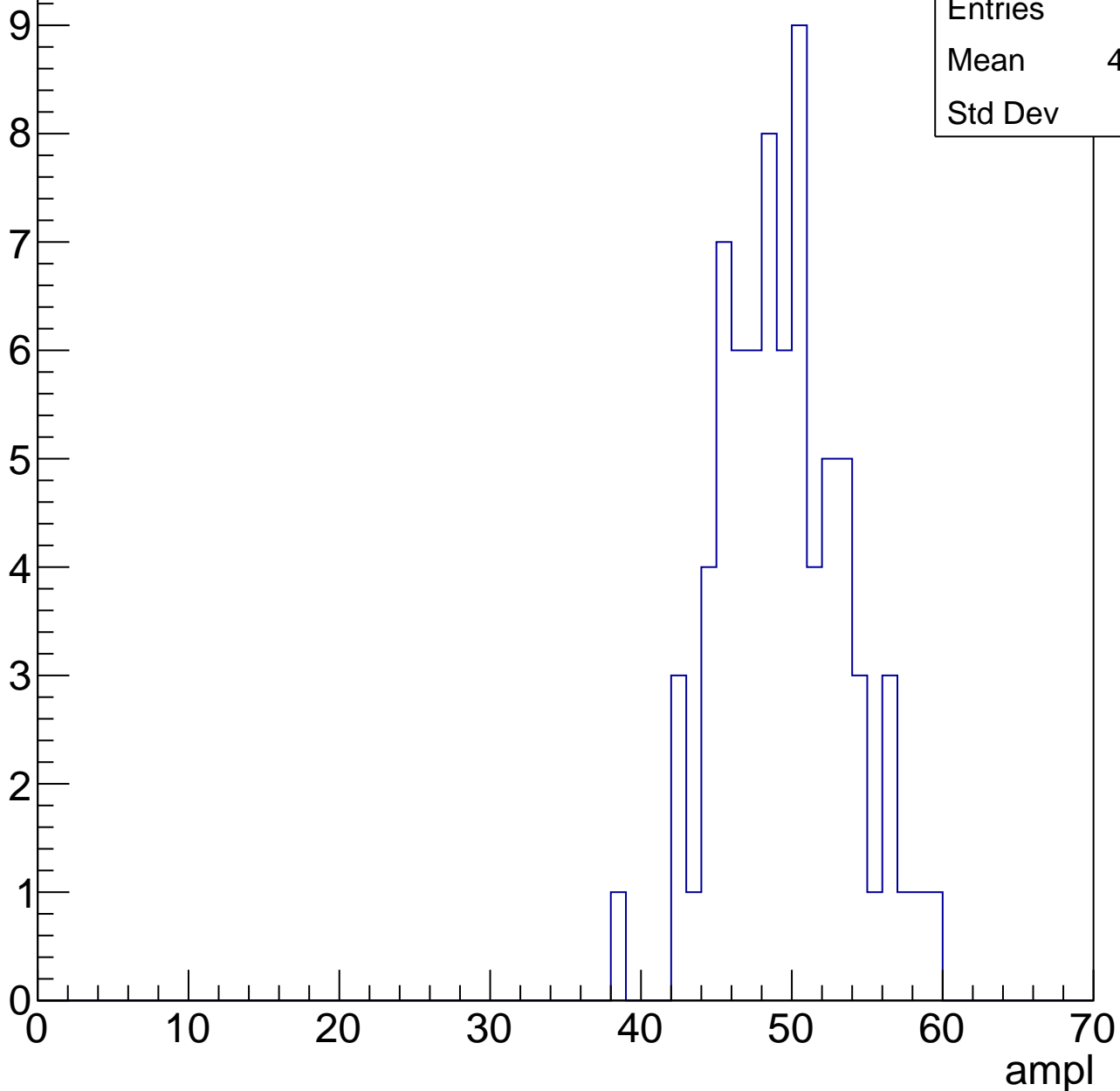


B0L002S, U17-ch48, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

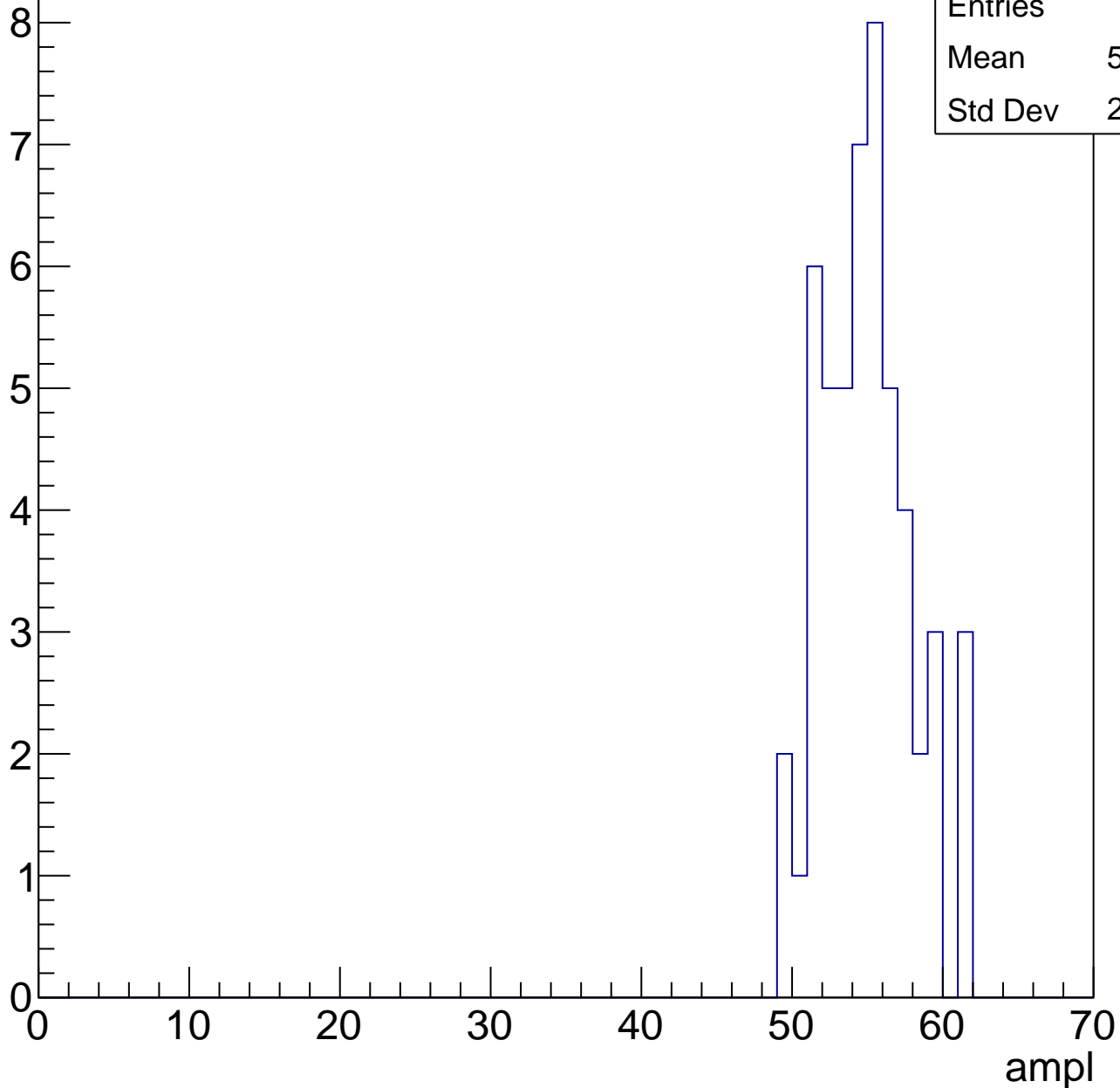
Entries	75
Mean	48.96
Std Dev	4.09



B0L002S, U17-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1

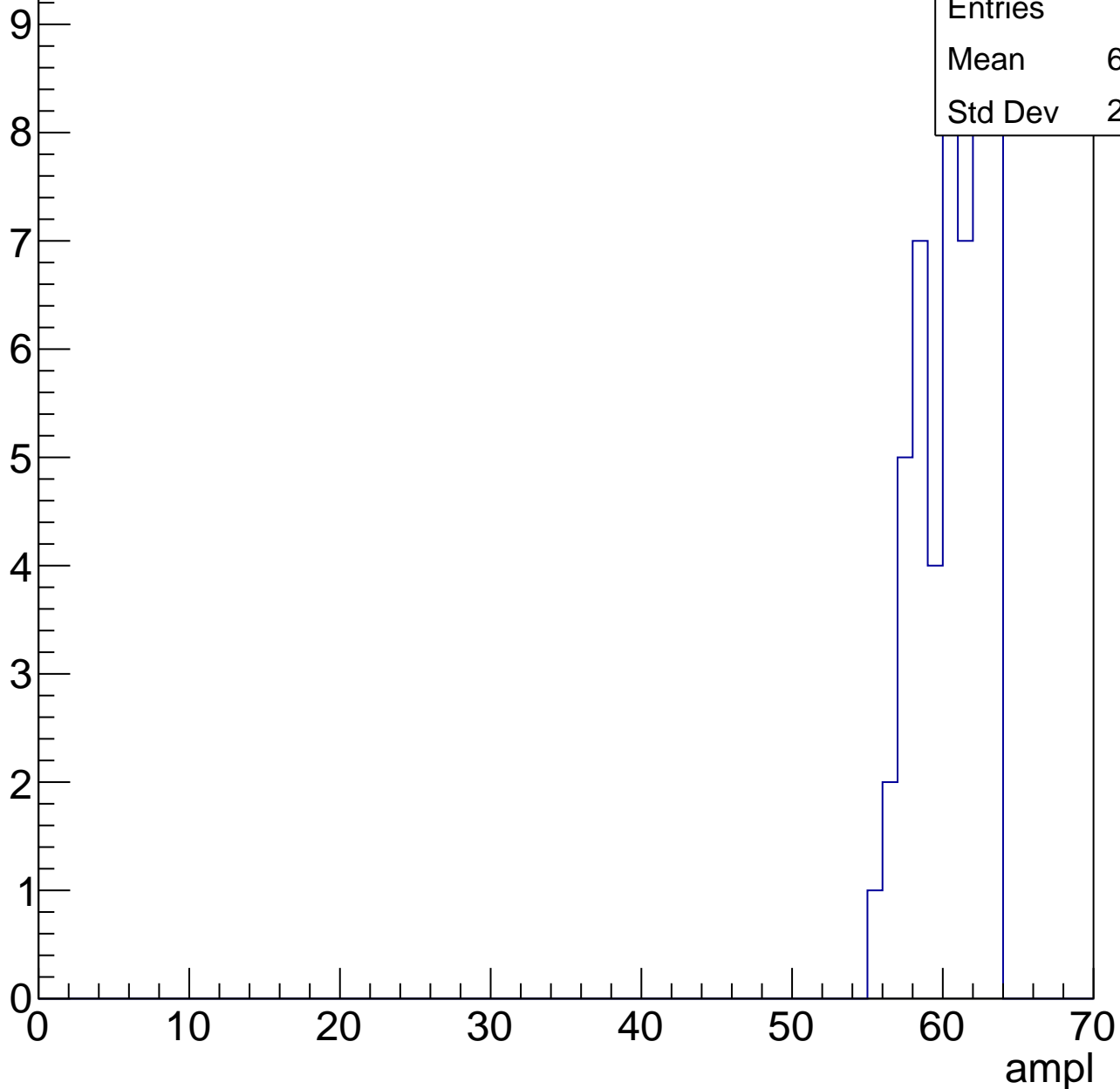
Entry



B0L002S, U17-ch48, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	52
Mean	60.08
Std Dev	2.209

B0L002S, U17-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.14
Std Dev	21.34

ampl

0 10 20 30 40 50 60 70

B0L002S, U17-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch49, adc0

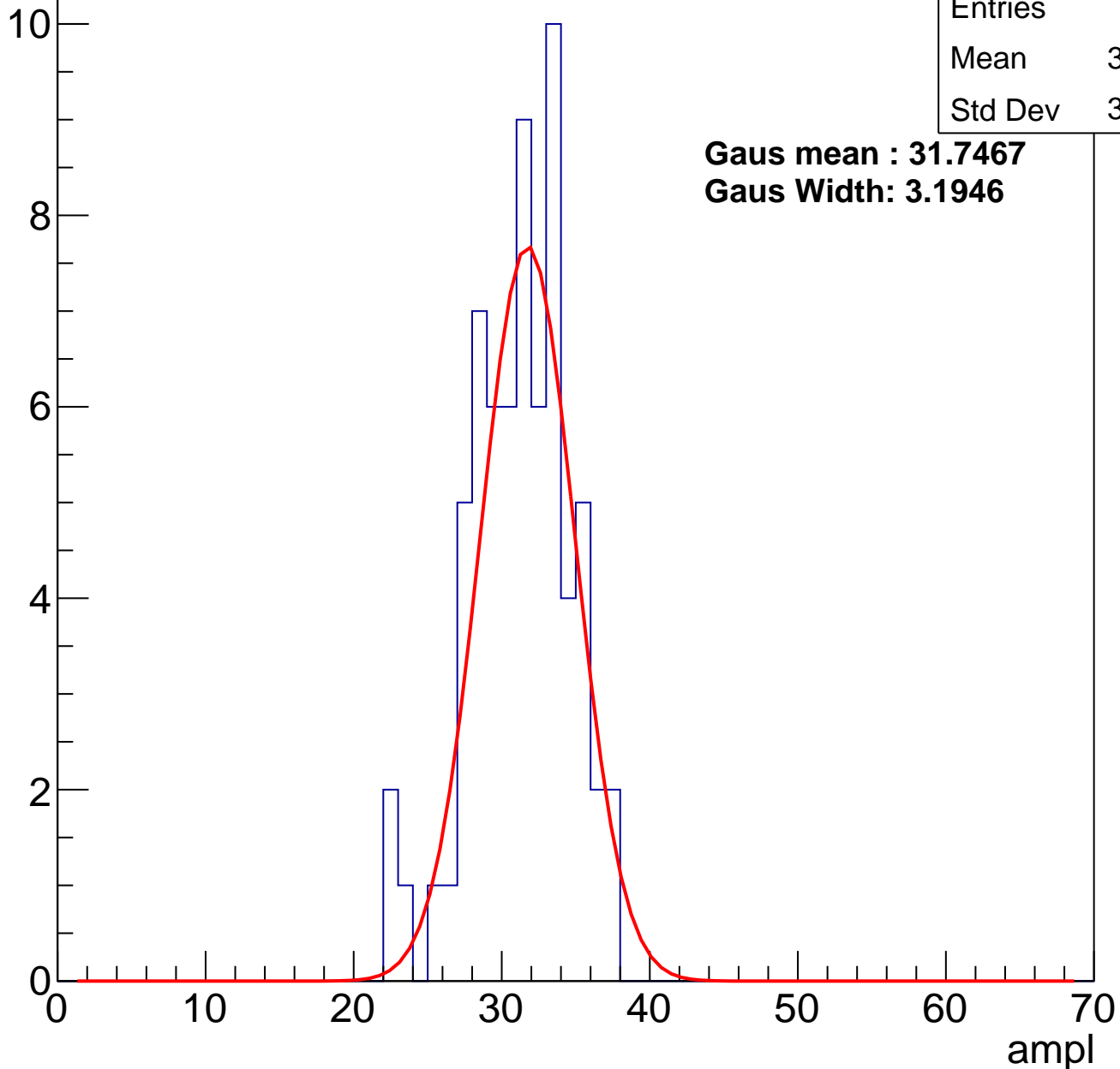
calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	30.76
Std Dev	3.324

Gaus mean : 31.7467

Gaus Width: 3.1946

Entry



B0L002S, U17-ch49, adc1

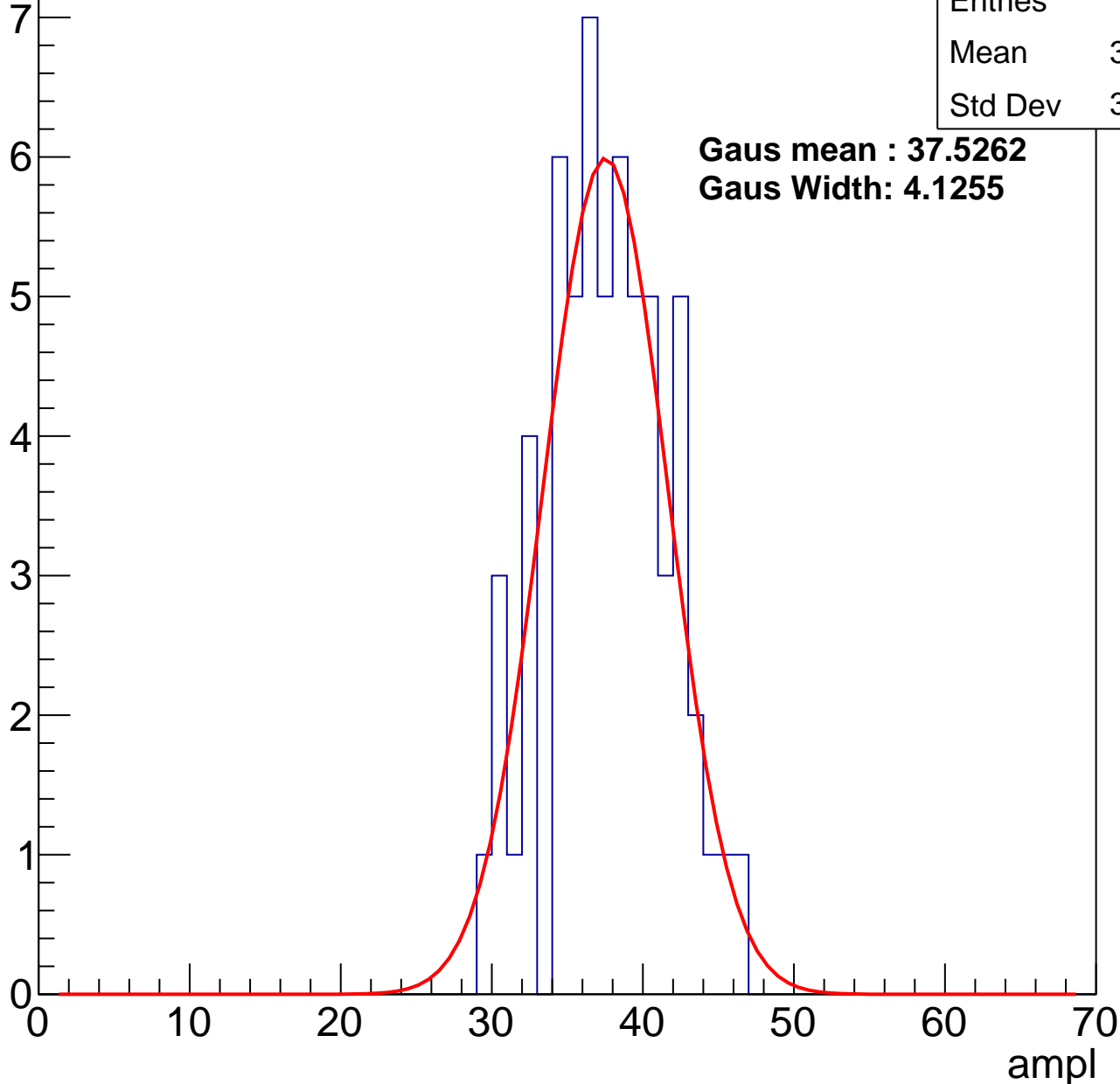
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.23
Std Dev	3.915

Gaus mean : 37.5262

Gaus Width: 4.1255



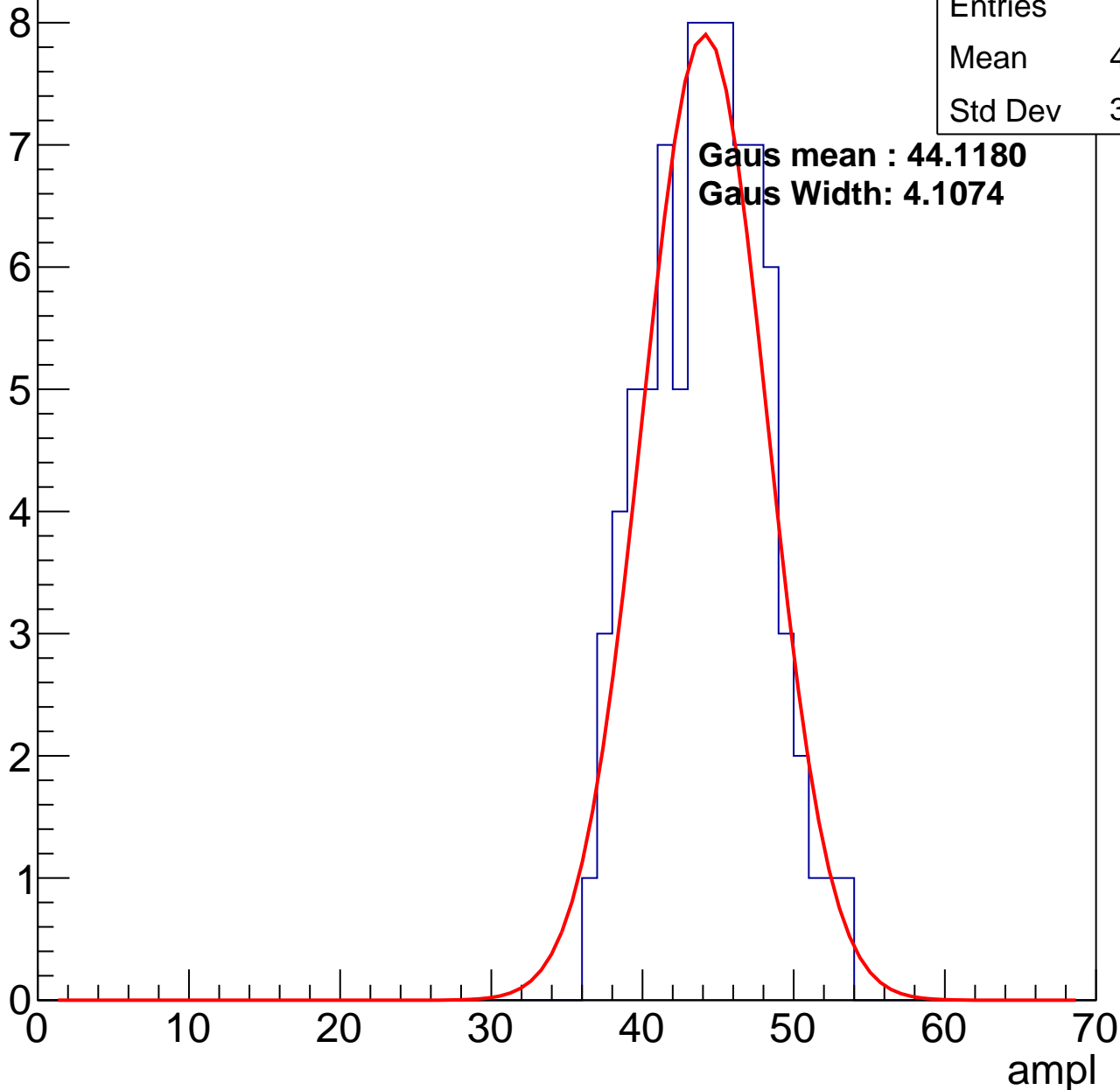
B0L002S, U17-ch49, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	43.77
Std Dev	3.798

Gaus mean : 44.1180
Gaus Width: 4.1074

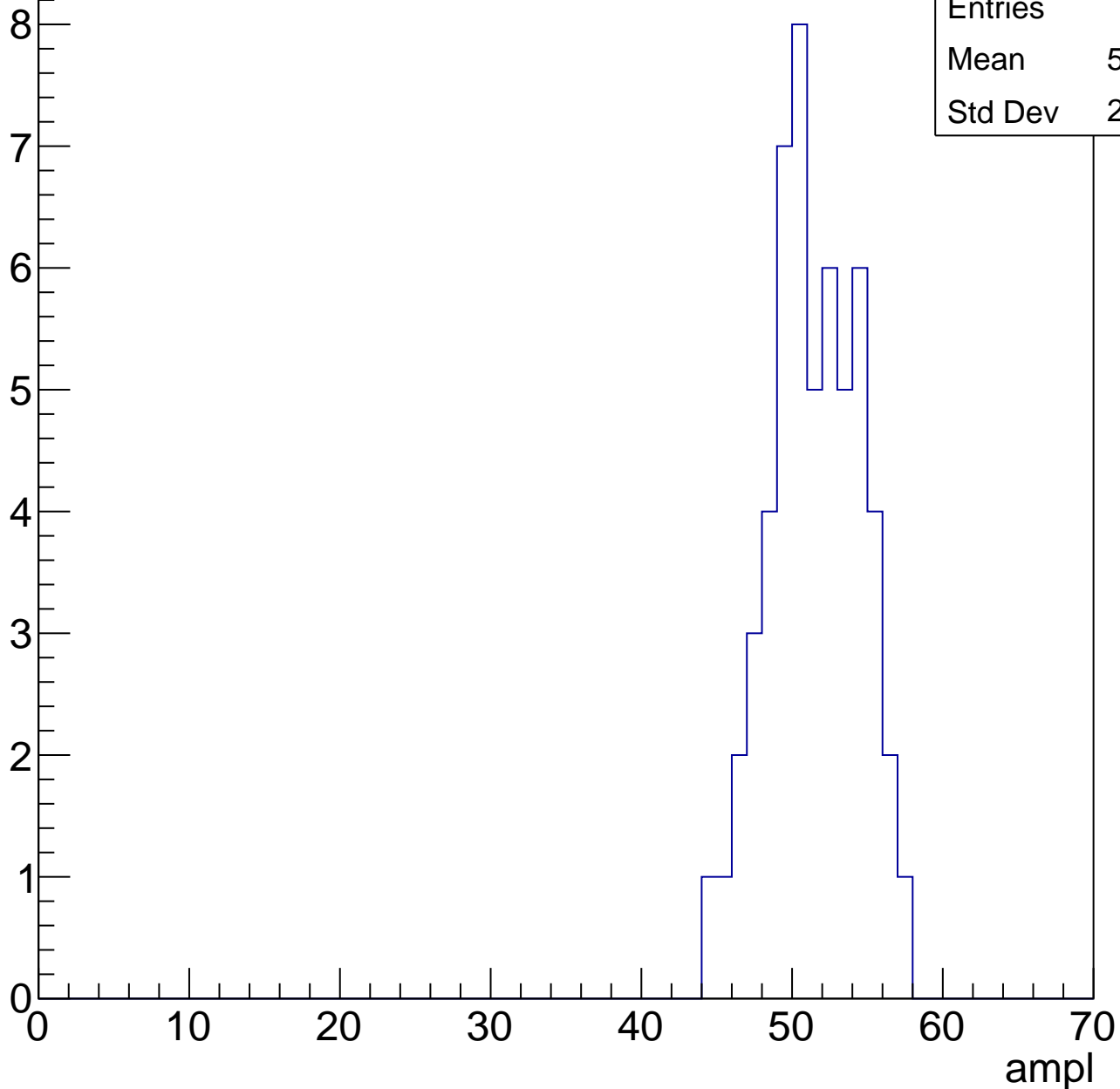


B0L002S, U17-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

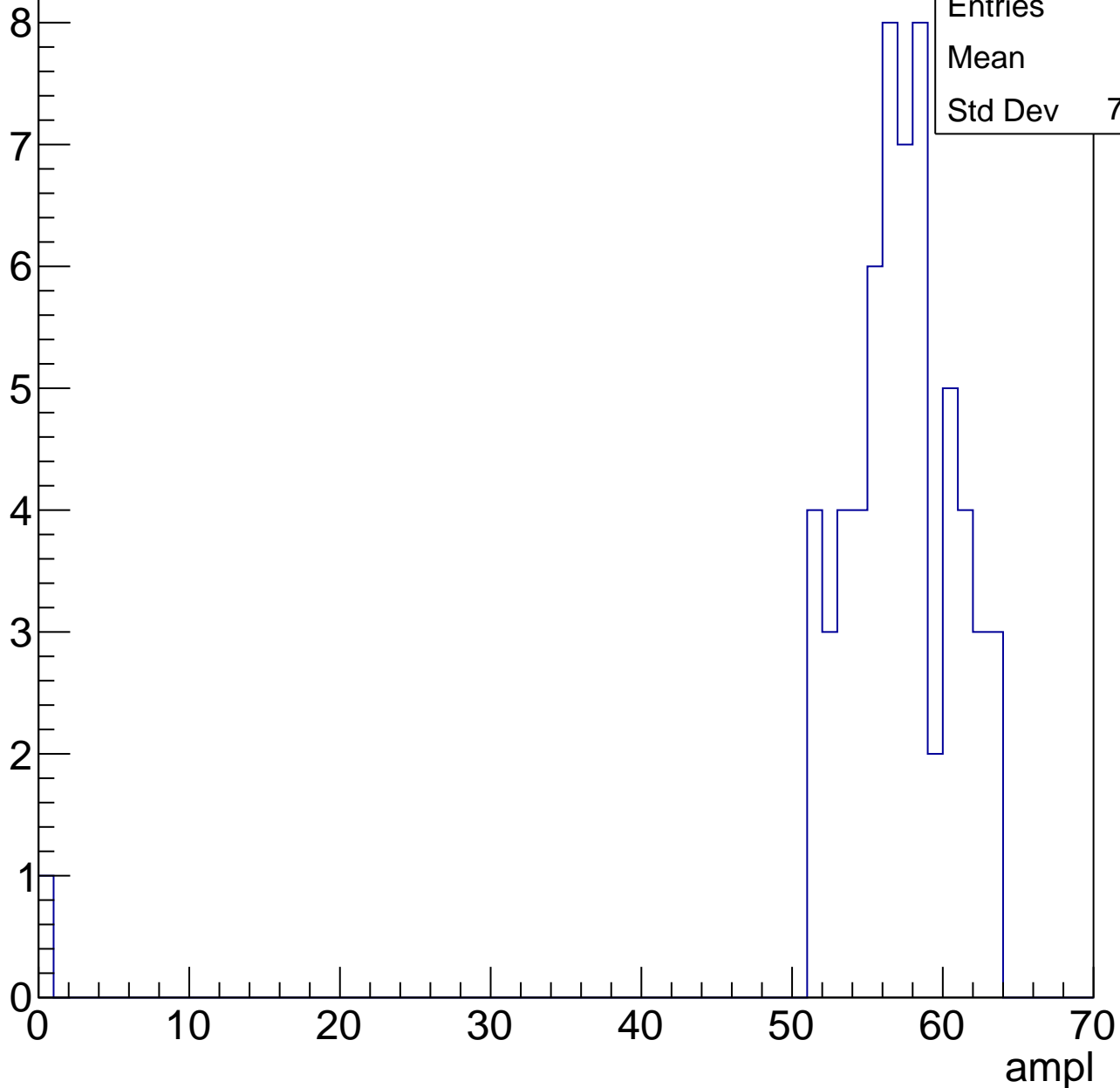
Entries	55
Mean	50.95
Std Dev	2.969



B0L002S, U17-ch49, adc4

calib_packv5_042523_0143.root, FC#8, port C1

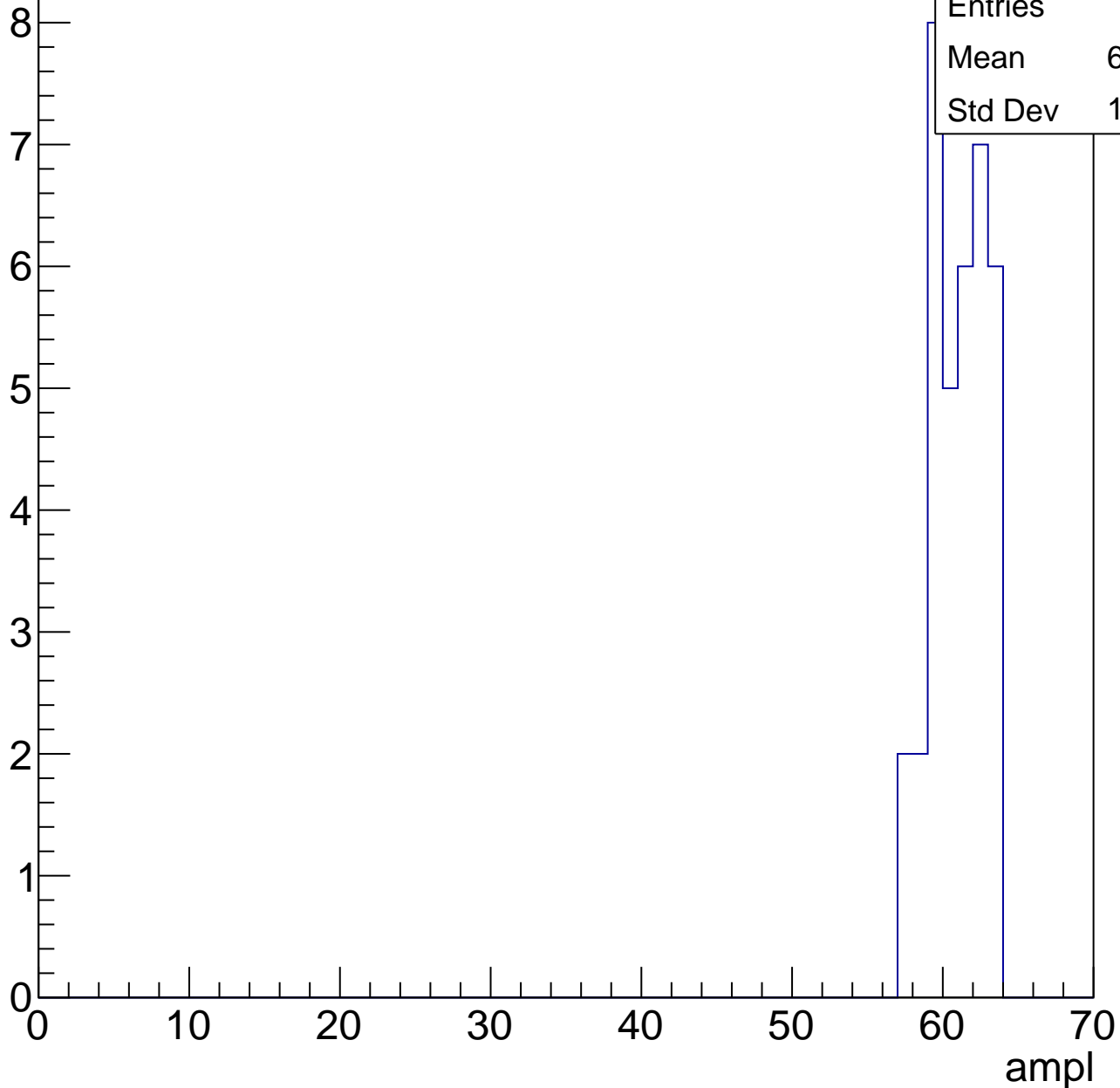
Entry



B0L002S, U17-ch49, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch50, adc0

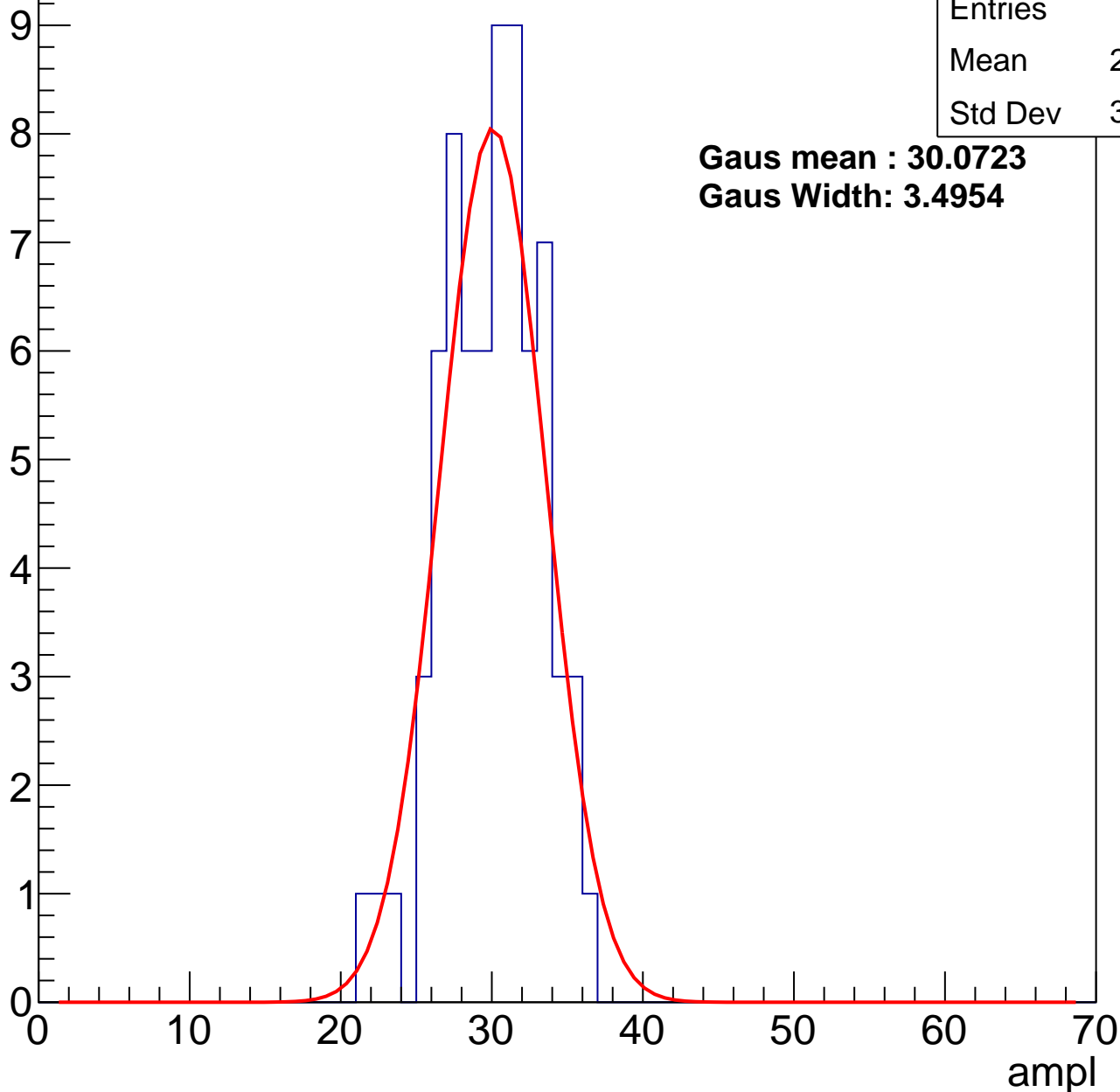
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.57
Std Dev	3.183

Gaus mean : 30.0723

Gaus Width: 3.4954



B0L002S, U17-ch50, adc1

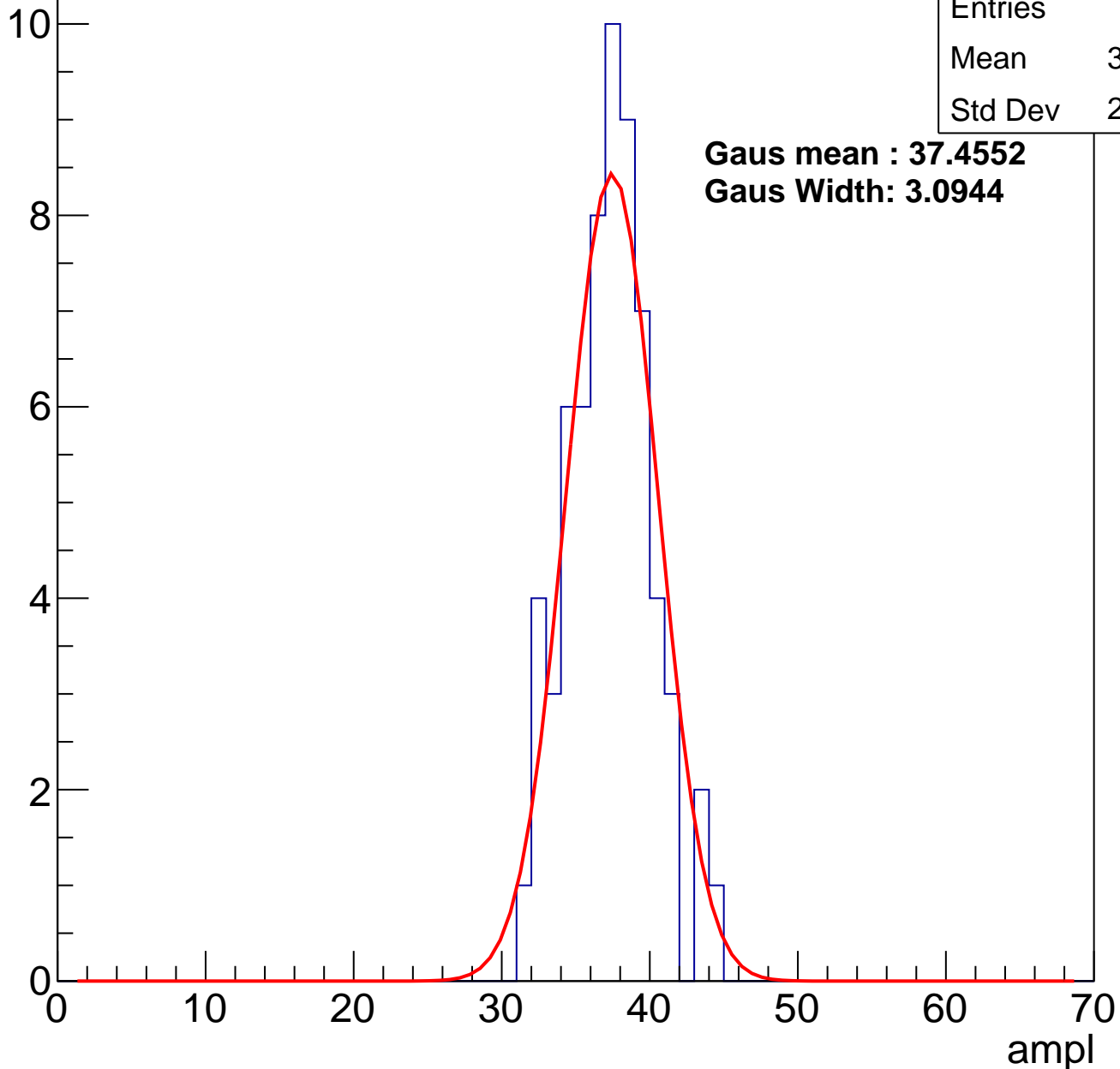
calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	36.84
Std Dev	2.824

Gaus mean : 37.4552

Gaus Width: 3.0944

Entry



B0L002S, U17-ch50, adc2

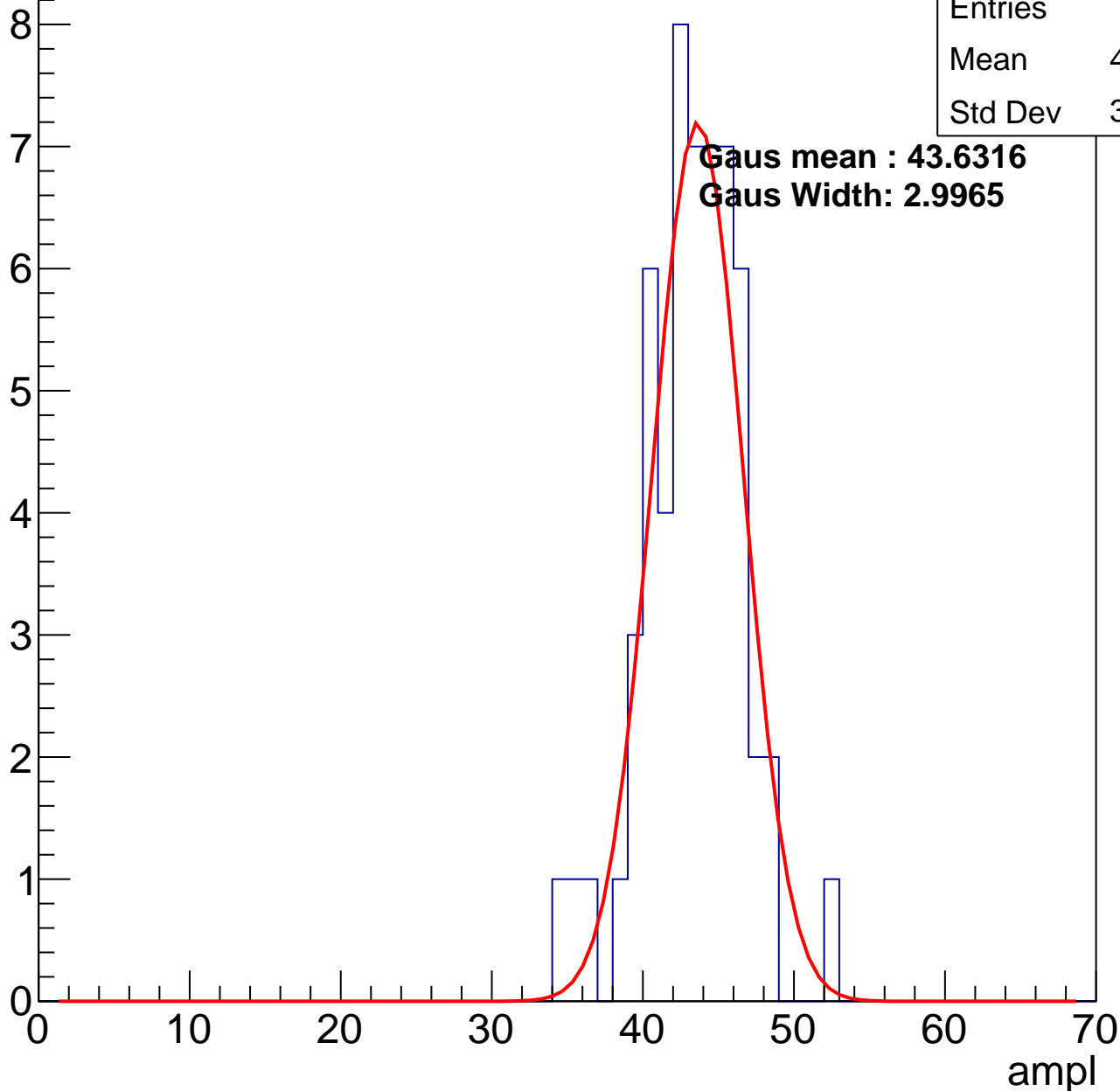
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.84
Std Dev	3.232

Gaus mean : 43.6316

Gaus Width: 2.9965

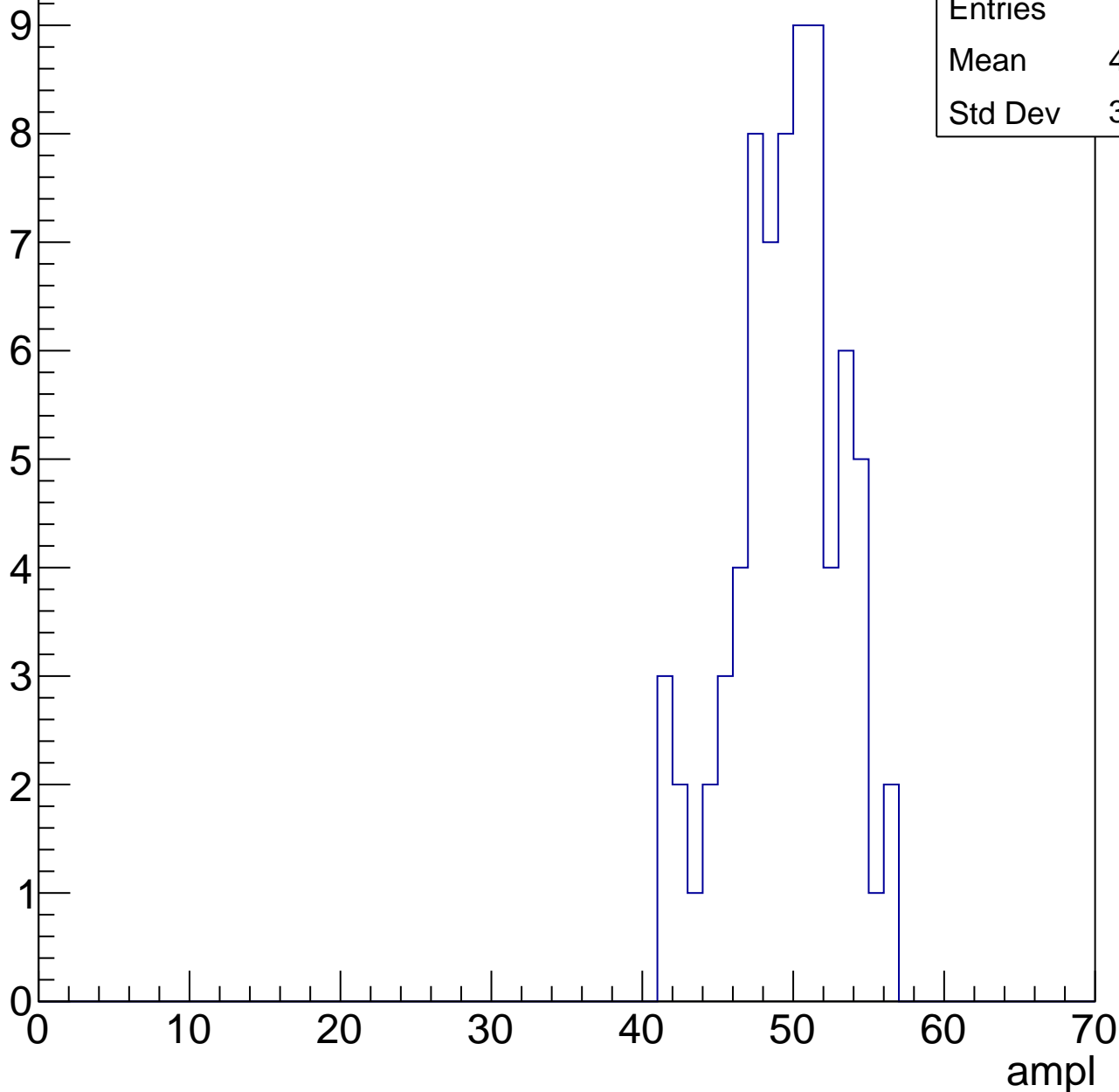


B0L002S, U17-ch50, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	49.09
Std Dev	3.553

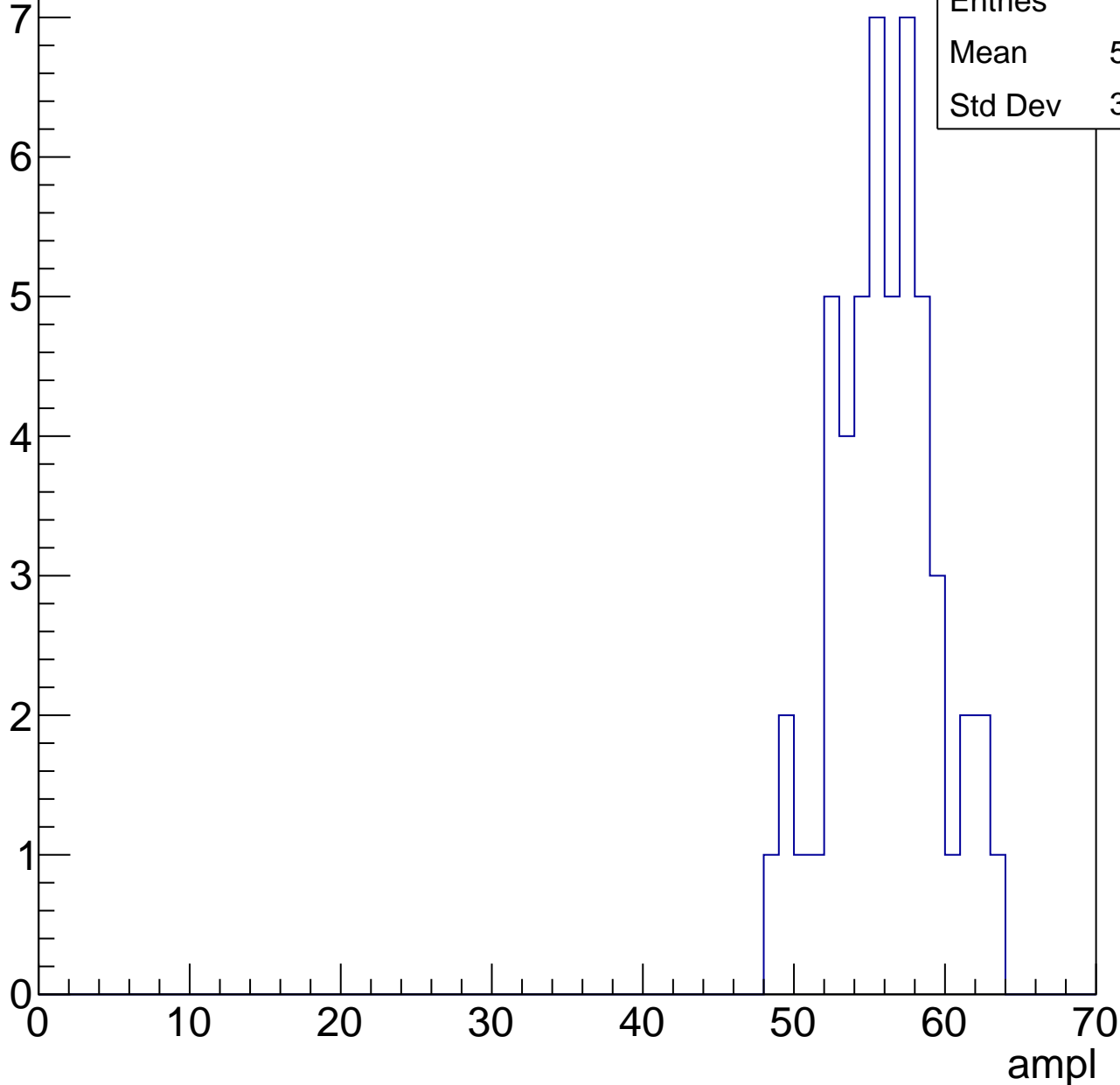


B0L002S, U17-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

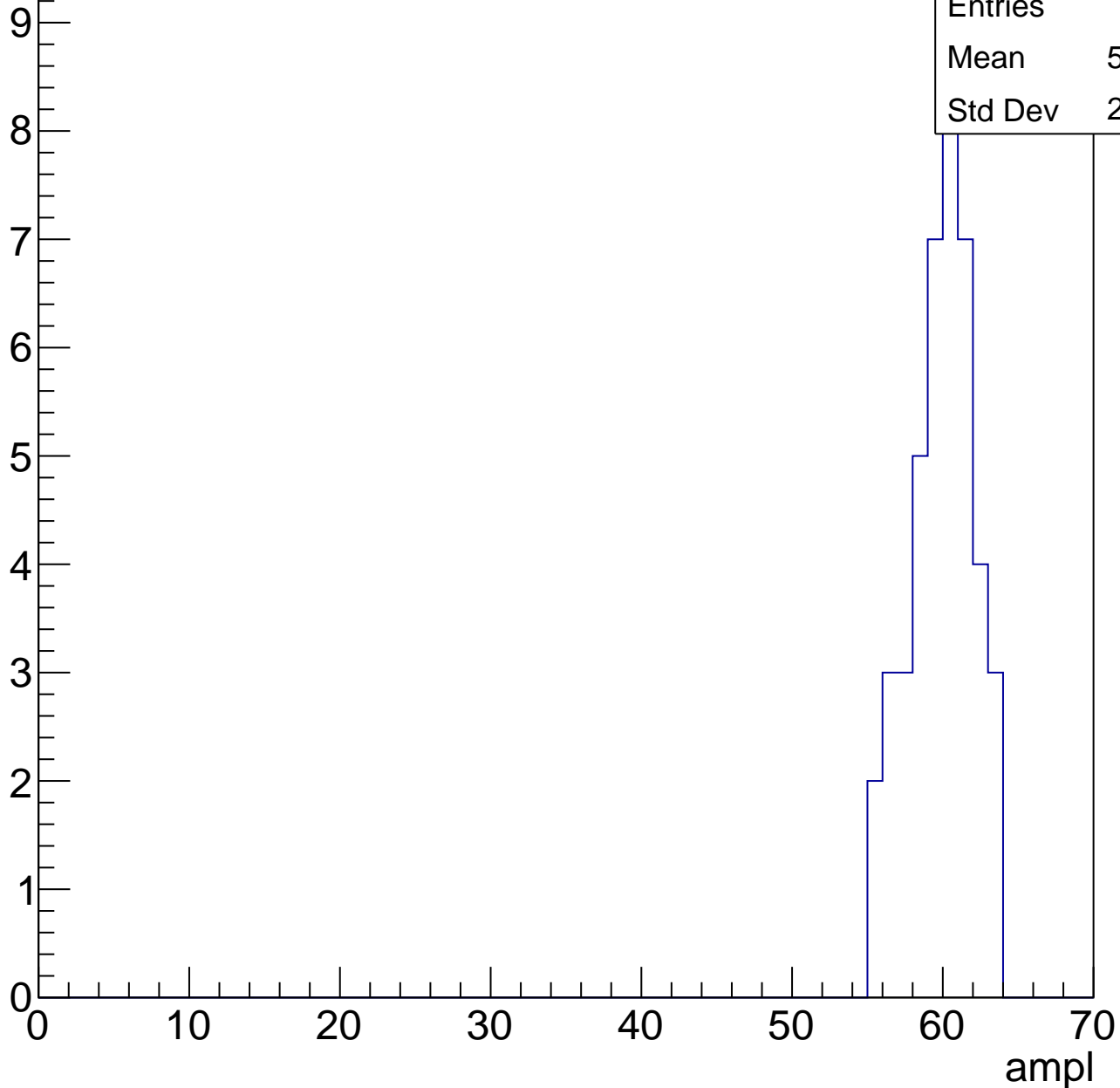
Entries	52
Mean	55.56
Std Dev	3.388



B0L002S, U17-ch50, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

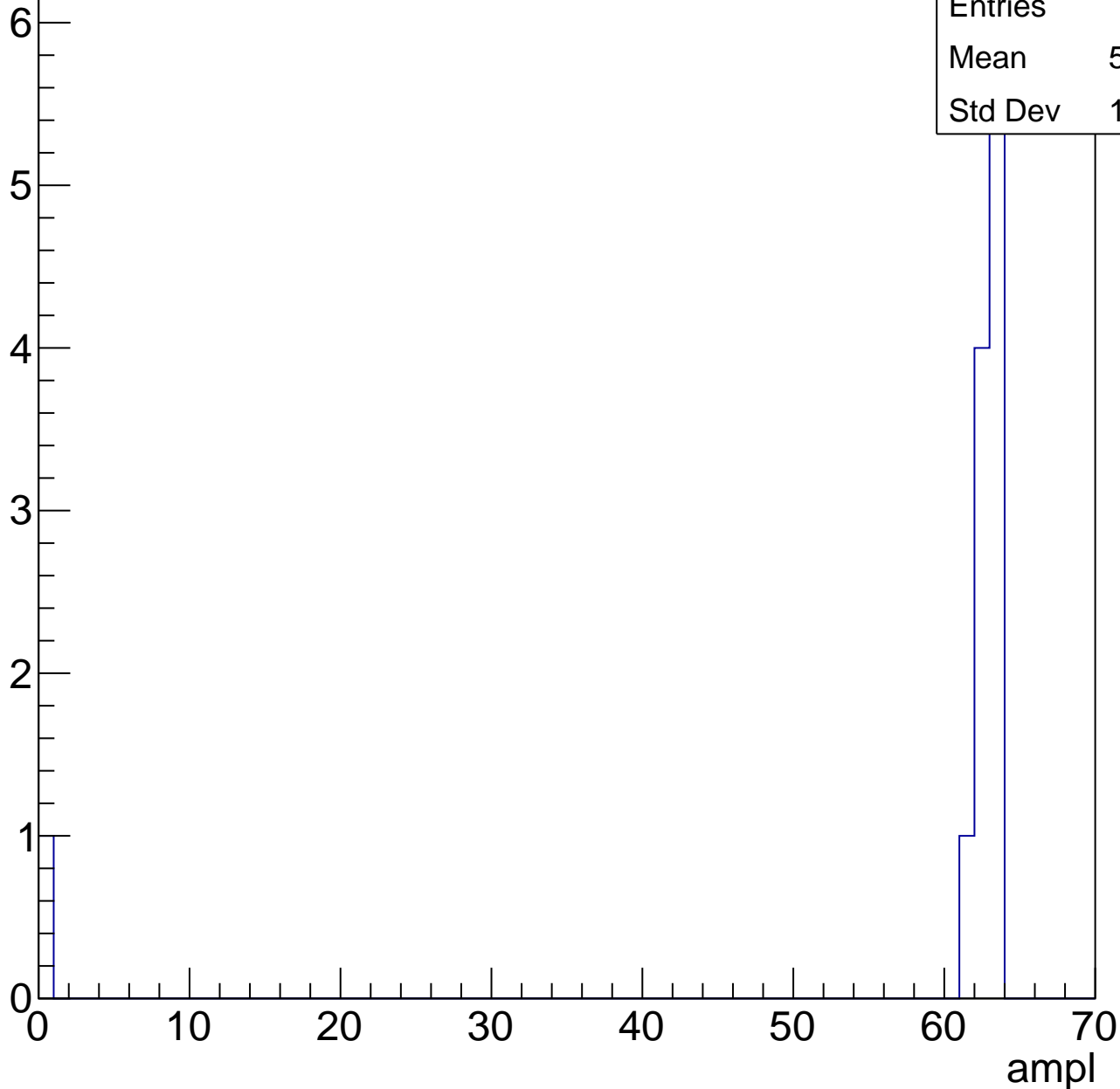


B0L002S, U17-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	57.25
Std Dev	17.27



B0L002S, U17-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch51, adc0

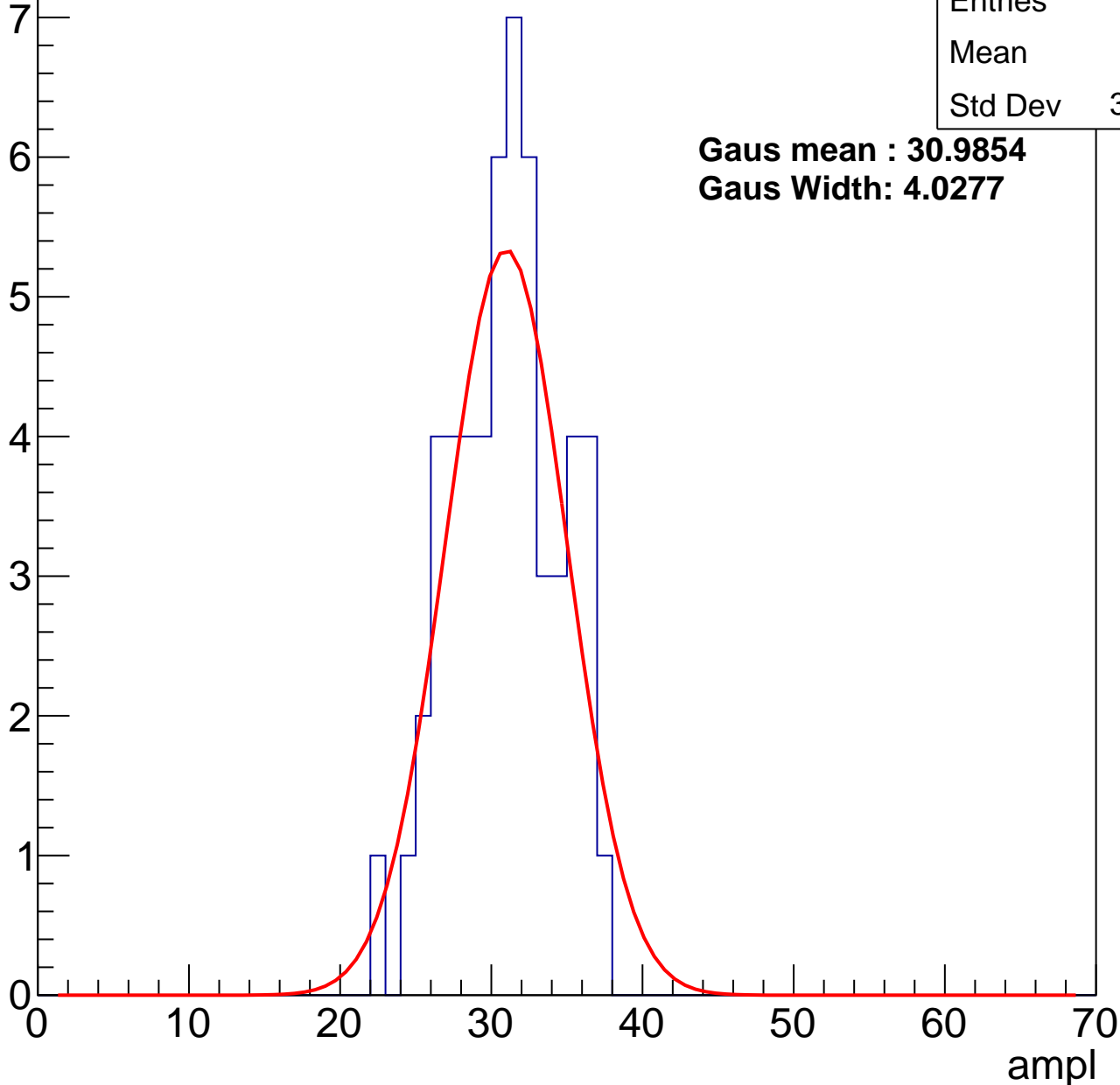
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	30.5
Std Dev	3.484

Gaus mean : 30.9854

Gaus Width: 4.0277



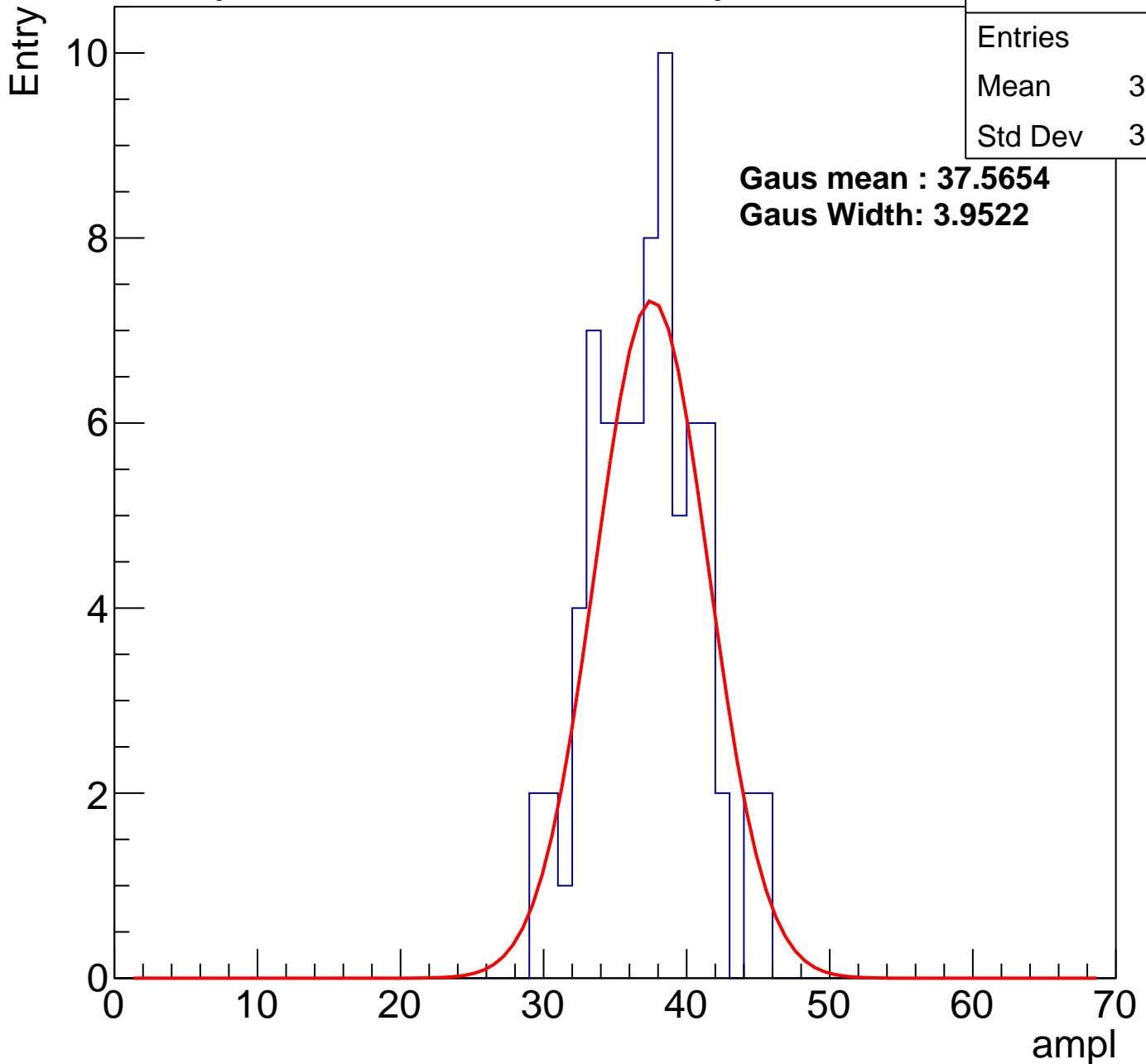
B0L002S, U17-ch51, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	36.76
Std Dev	3.669

Gaus mean : 37.5654

Gaus Width: 3.9522



B0L002S, U17-ch51, adc2

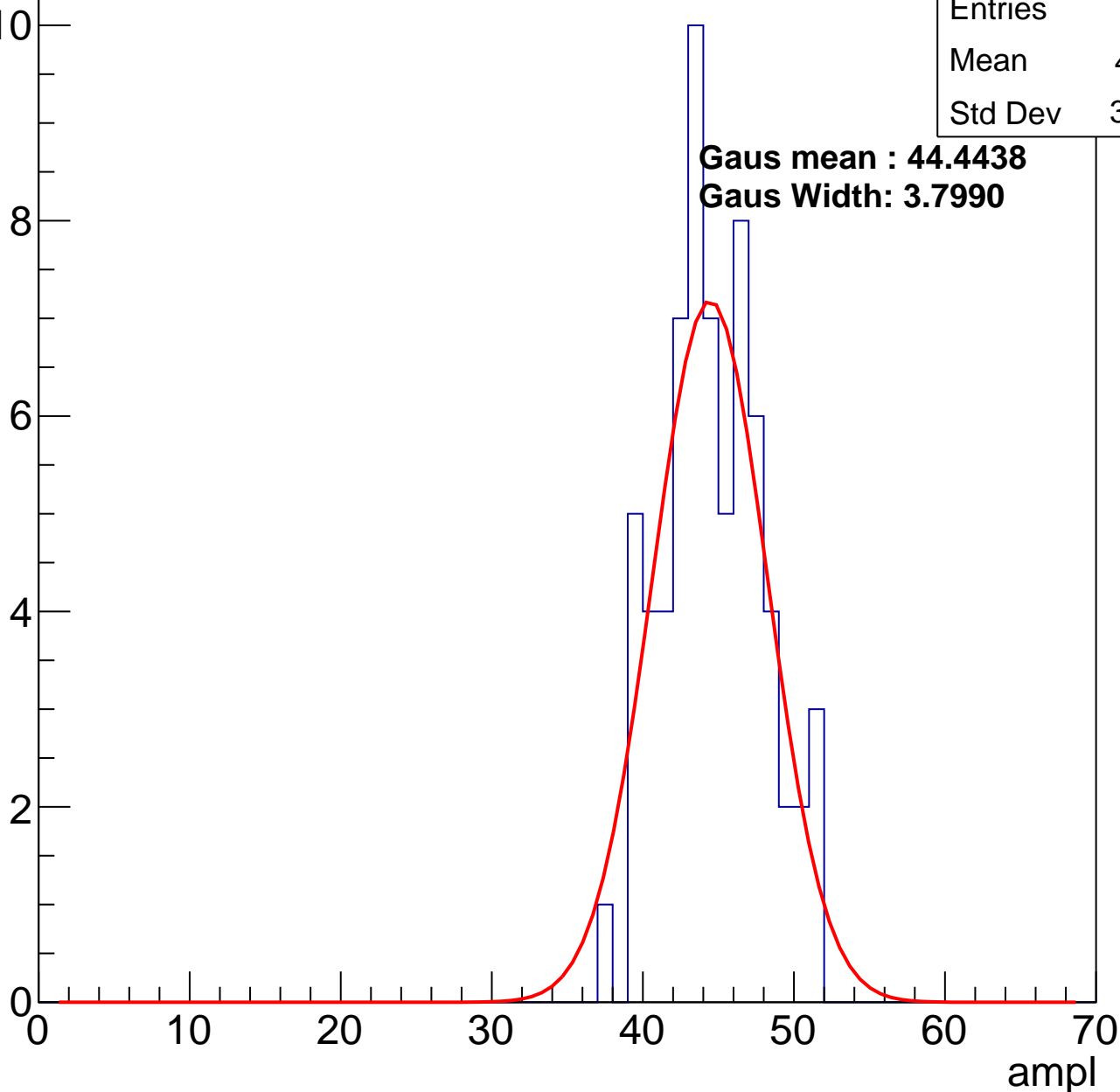
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	44.21
Std Dev	3.283

Gaus mean : 44.4438

Gaus Width: 3.7990

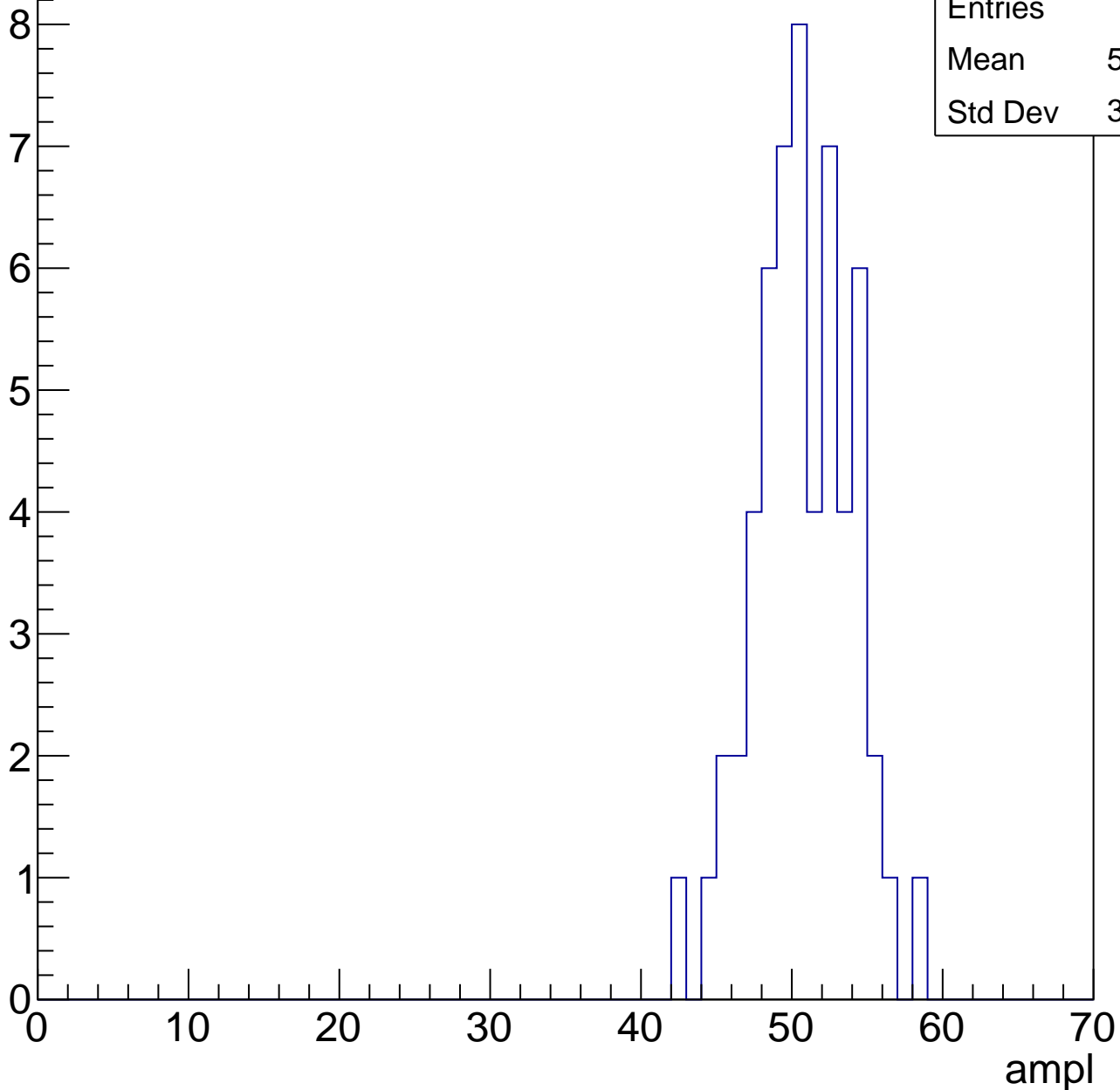


B0L002S, U17-ch51, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	50.27
Std Dev	3.159

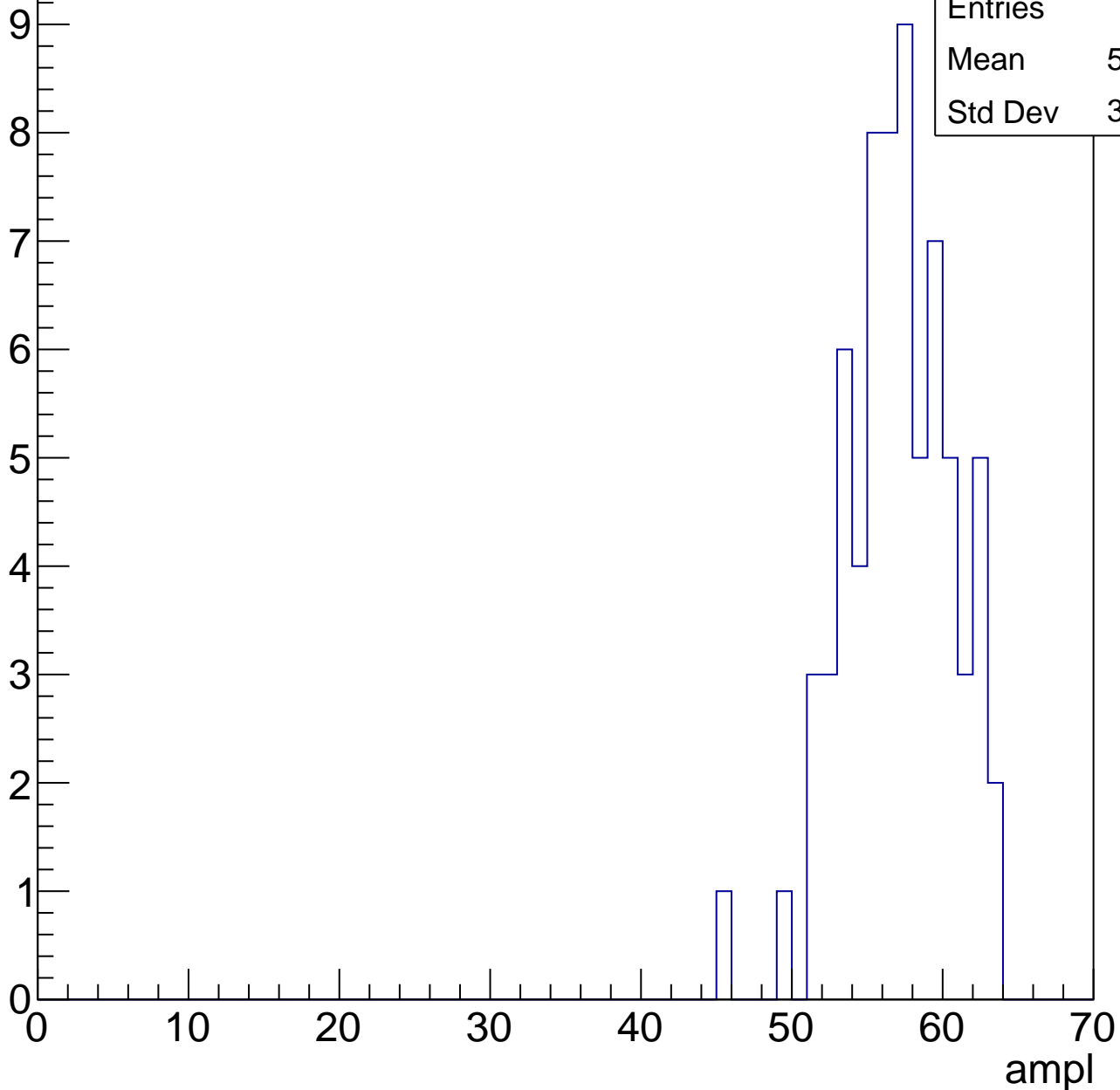


B0L002S, U17-ch51, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	56.57
Std Dev	3.532

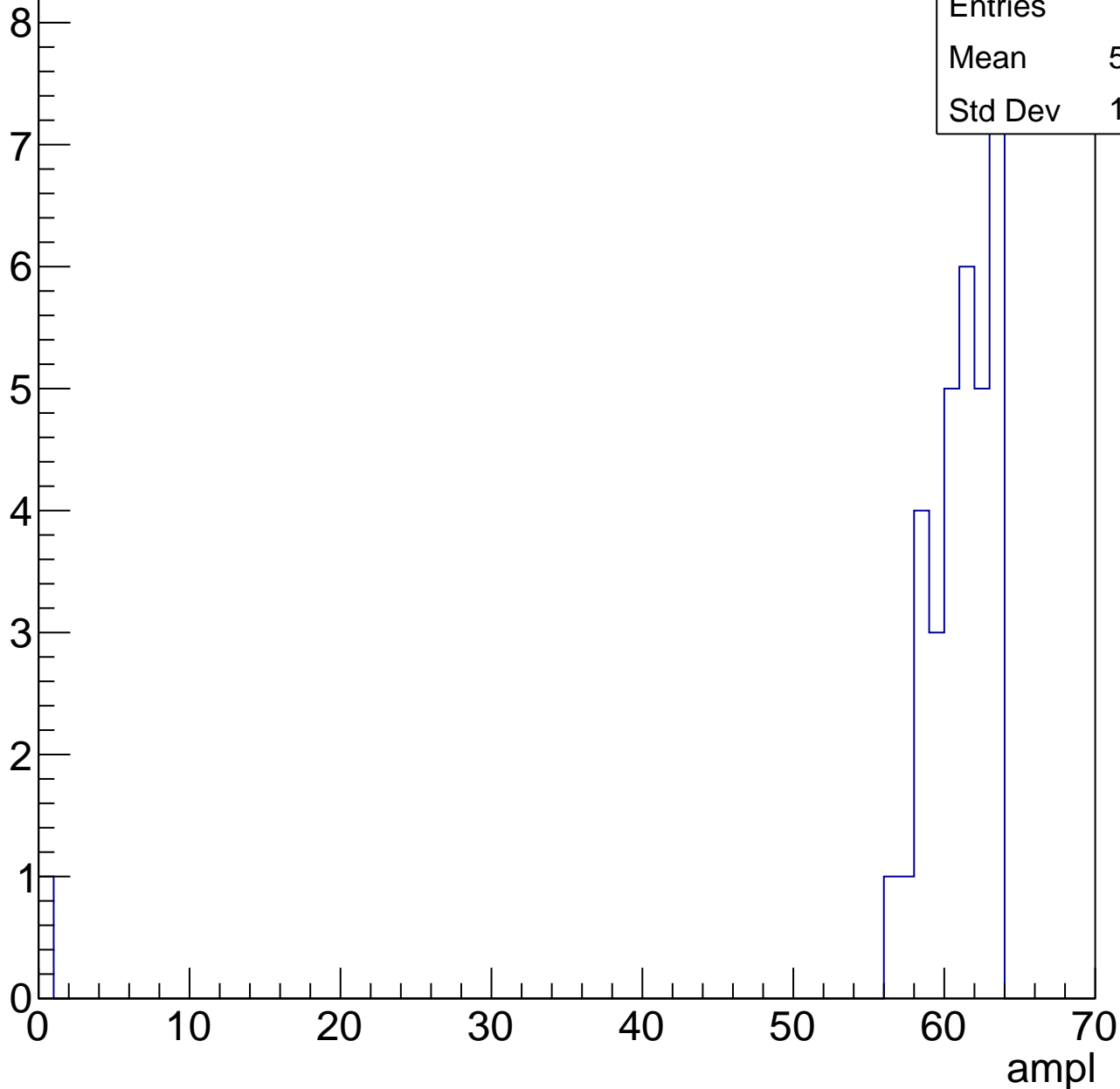


B0L002S, U17-ch51, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.88
Std Dev	10.43



B0L002S, U17-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch52, adc0

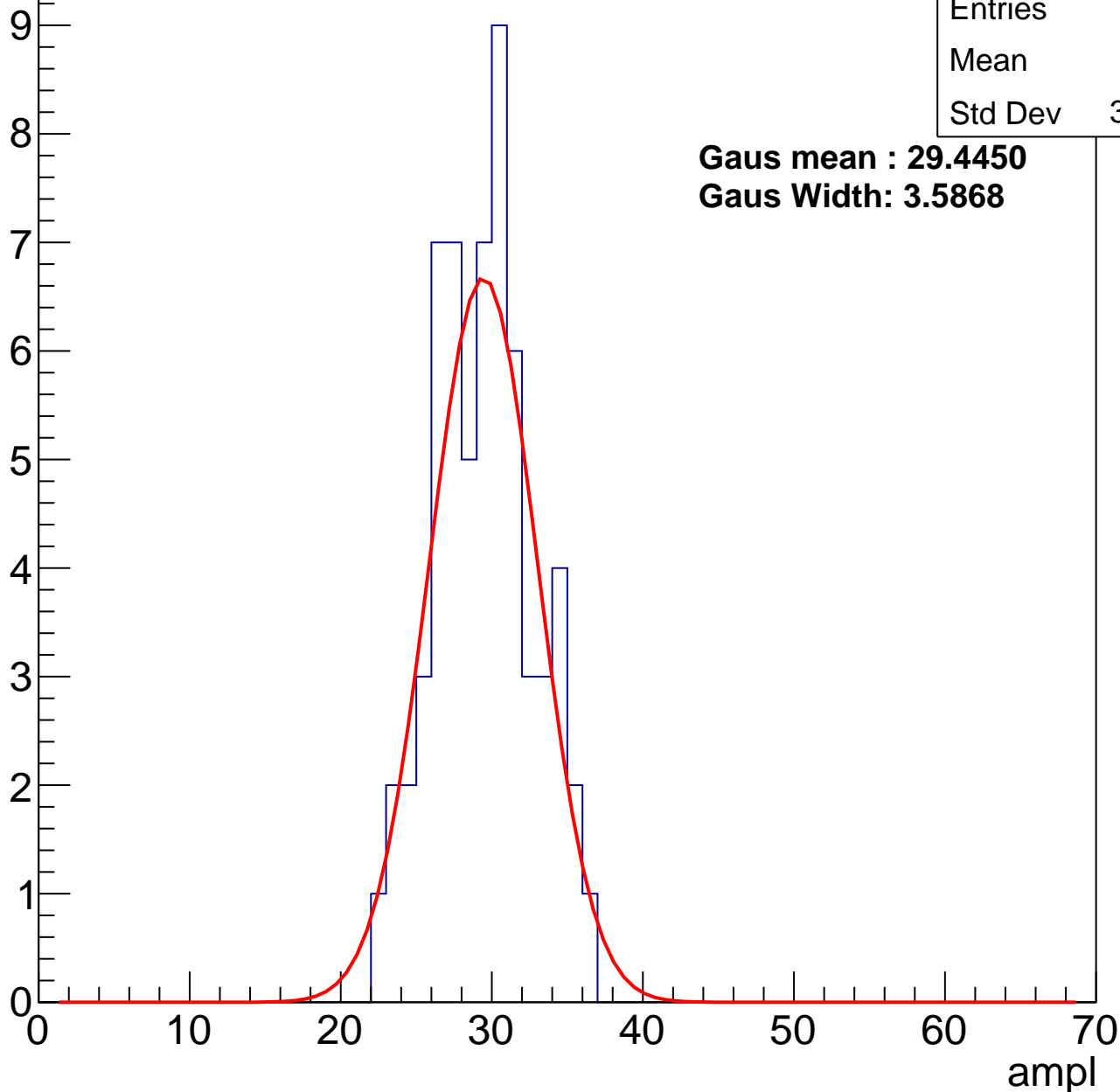
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29
Std Dev	3.223

Gaus mean : 29.4450

Gaus Width: 3.5868



B0L002S, U17-ch52, adc1

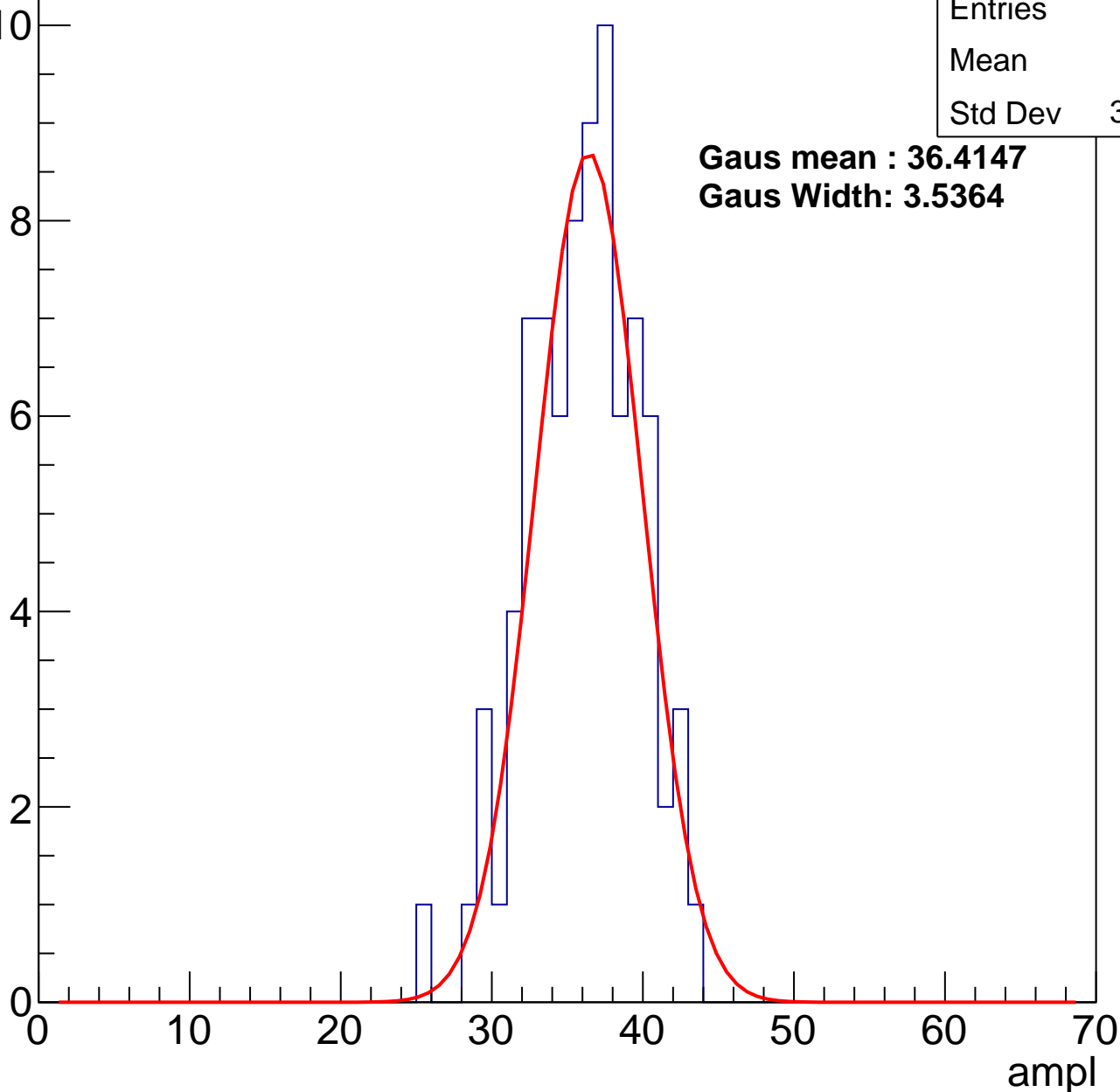
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	35.6
Std Dev	3.598

Gaus mean : 36.4147

Gaus Width: 3.5364



B0L002S, U17-ch52, adc2

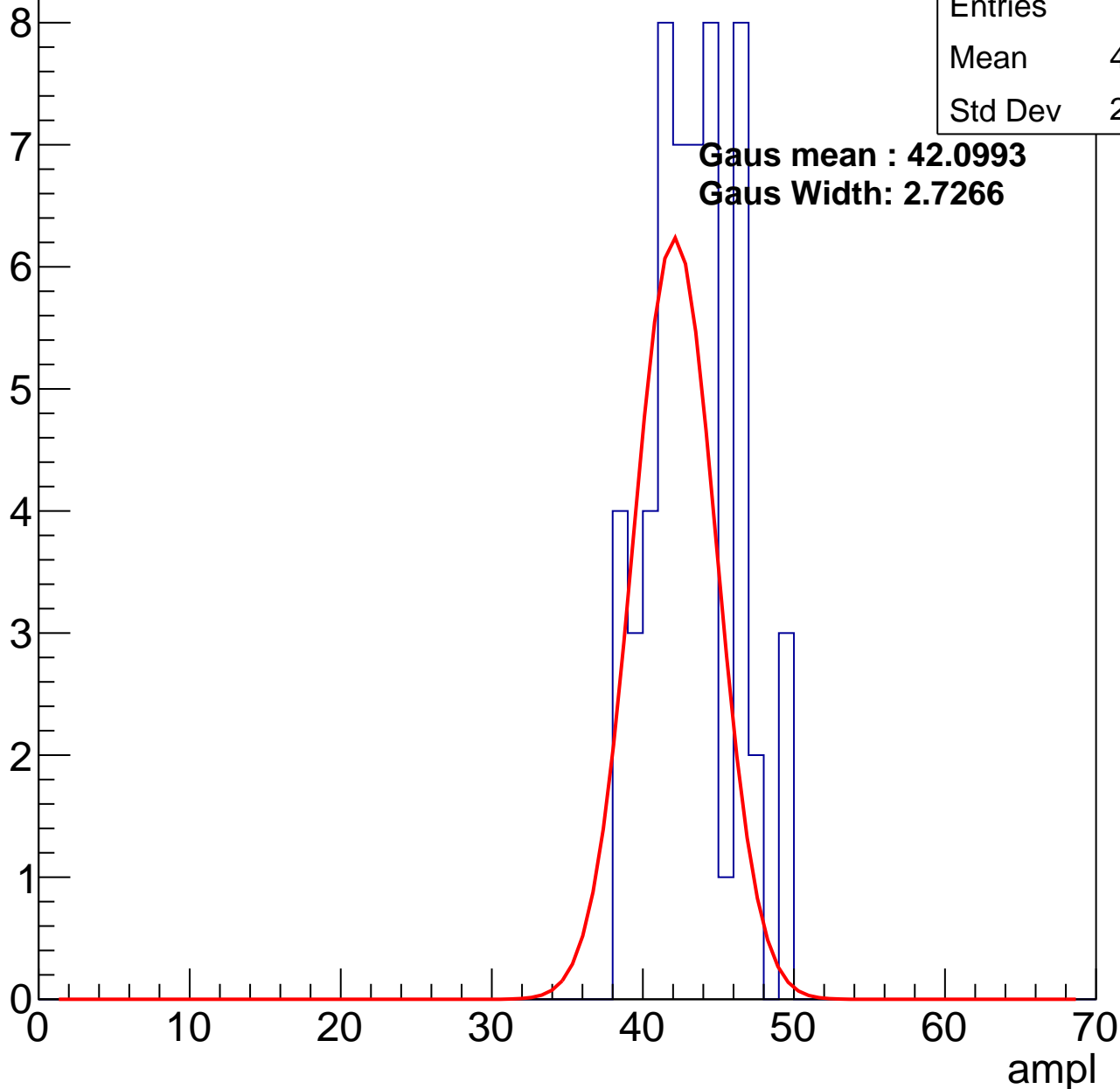
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	42.87
Std Dev	2.848

Gaus mean : 42.0993

Gaus Width: 2.7266

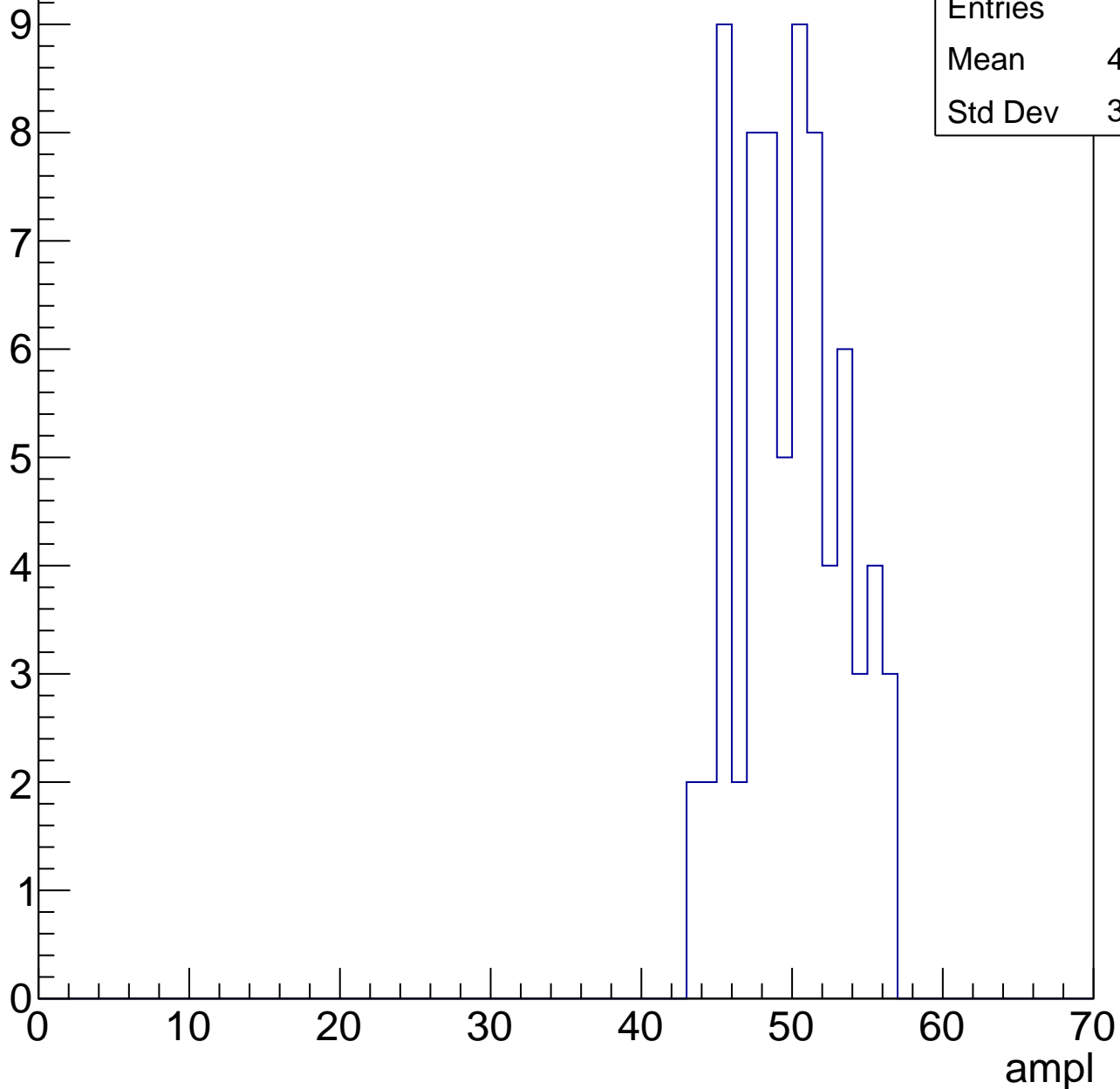


B0L002S, U17-ch52, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

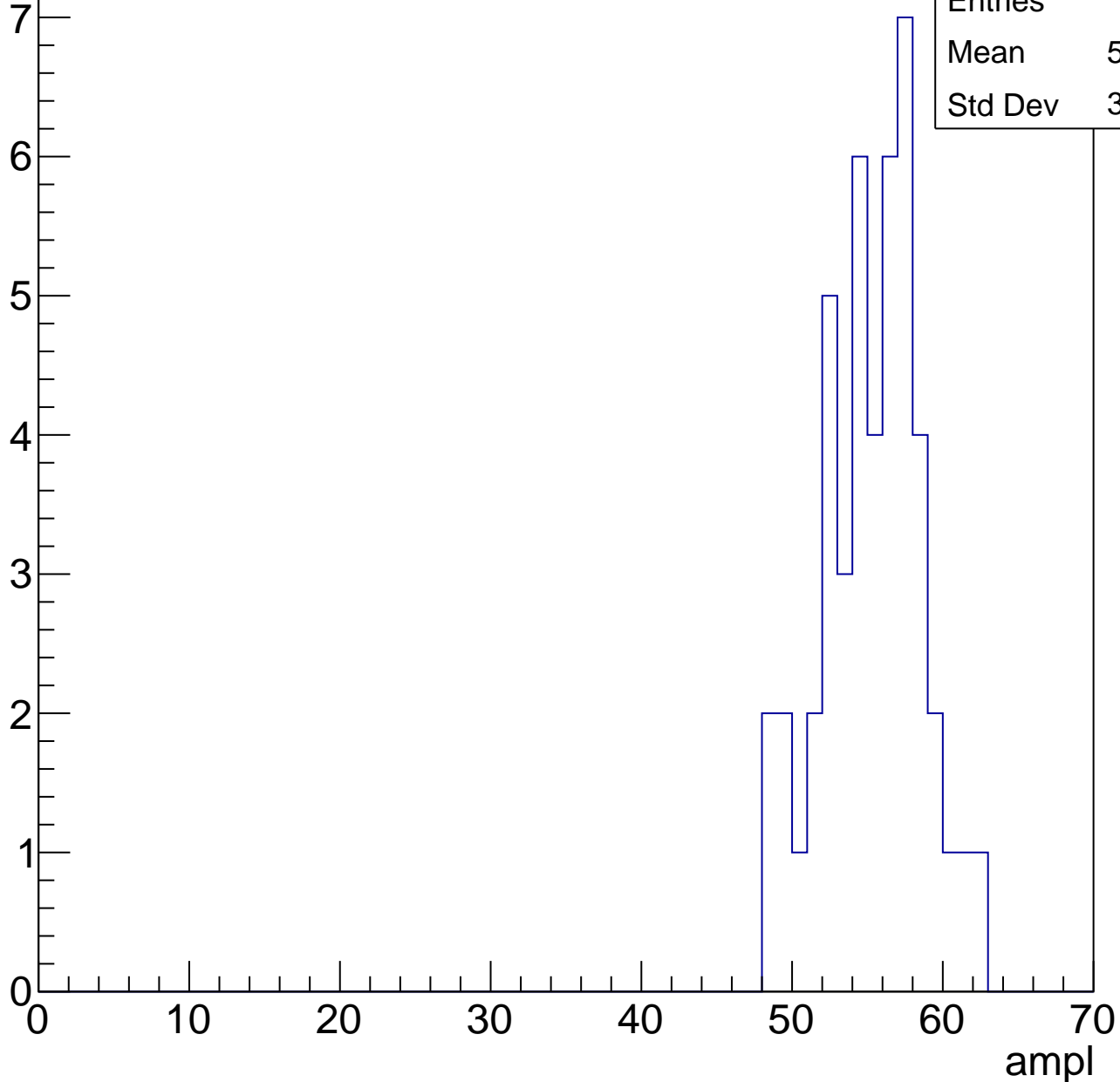
Entries	73
Mean	49.45
Std Dev	3.408



B0L002S, U17-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

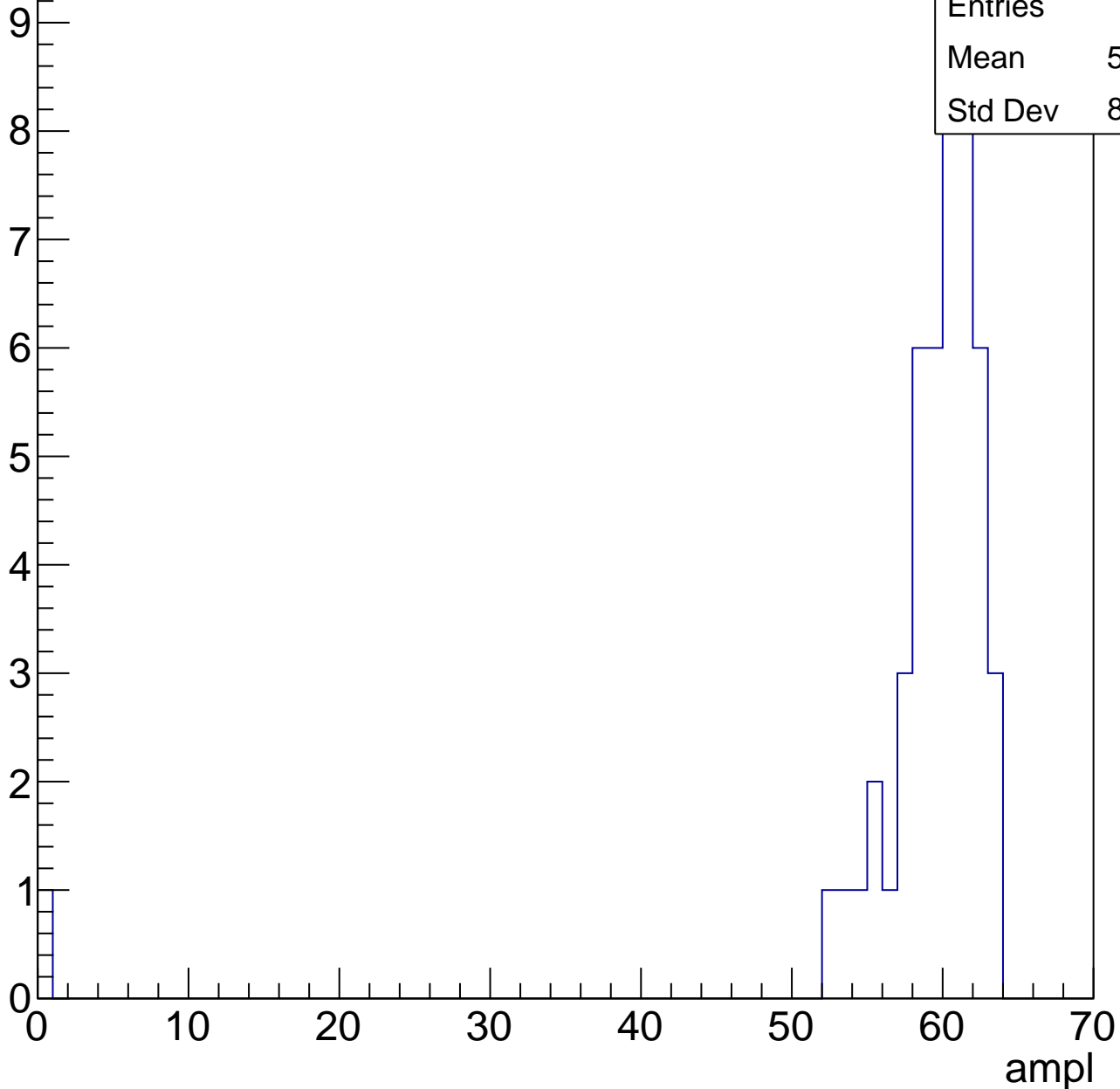


B0L002S, U17-ch52, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.12
Std Dev	8.759

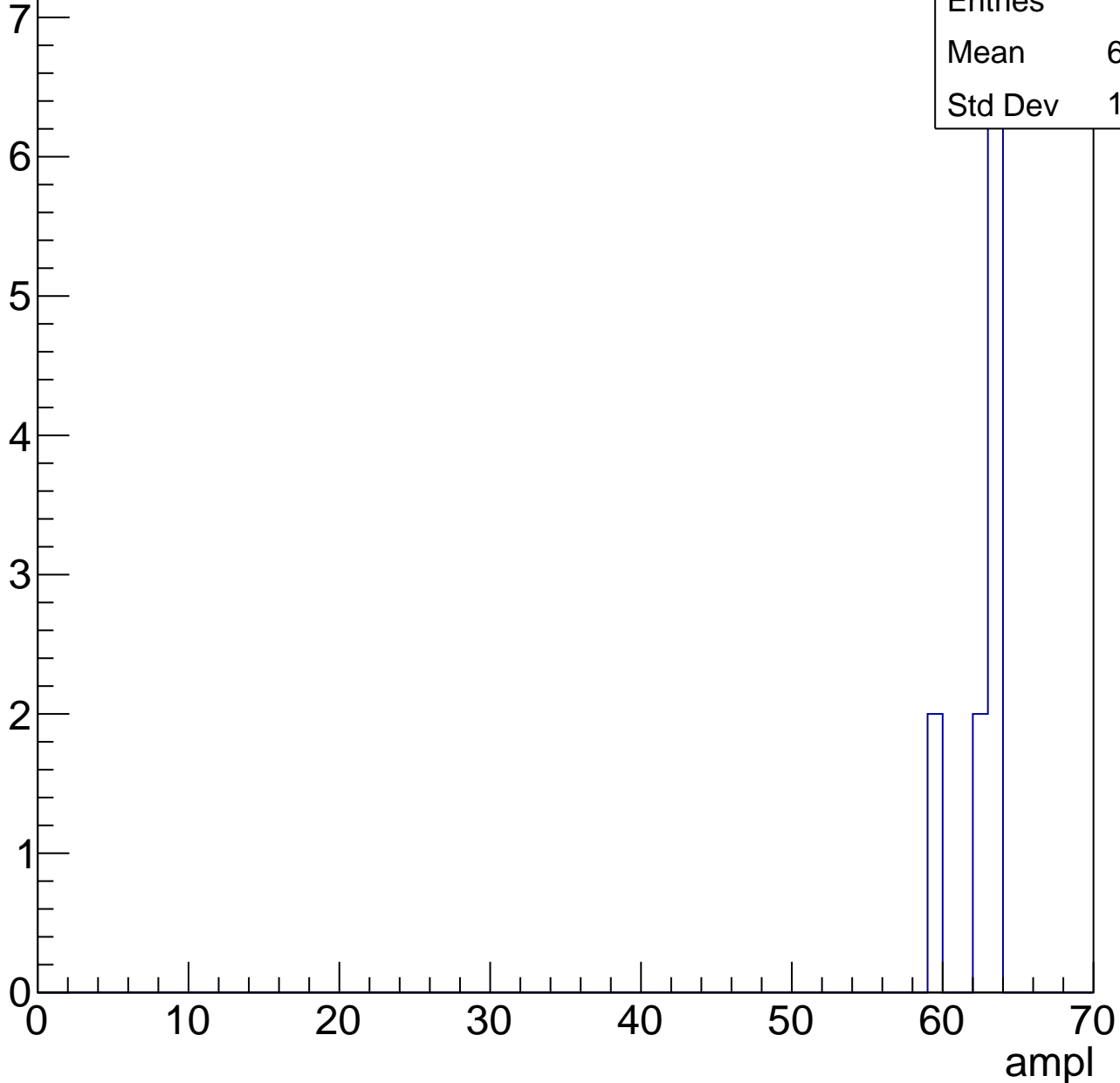


B0L002S, U17-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	11
Mean	62.09
Std Dev	1.505



B0L002S, U17-ch52, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L002S, U17-ch53, adc0

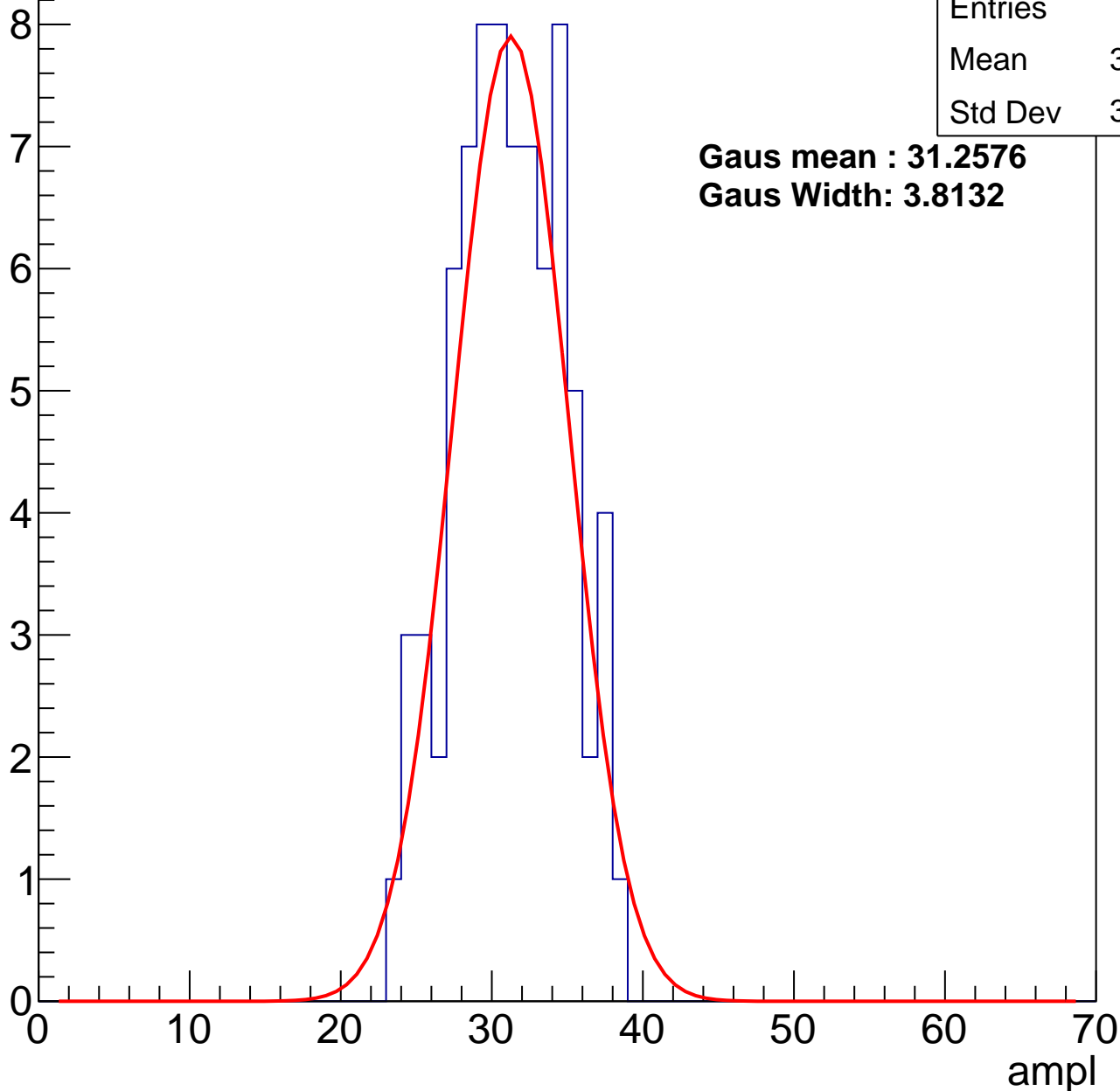
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	30.72
Std Dev	3.559

Gaus mean : 31.2576

Gaus Width: 3.8132



B0L002S, U17-ch53, adc1

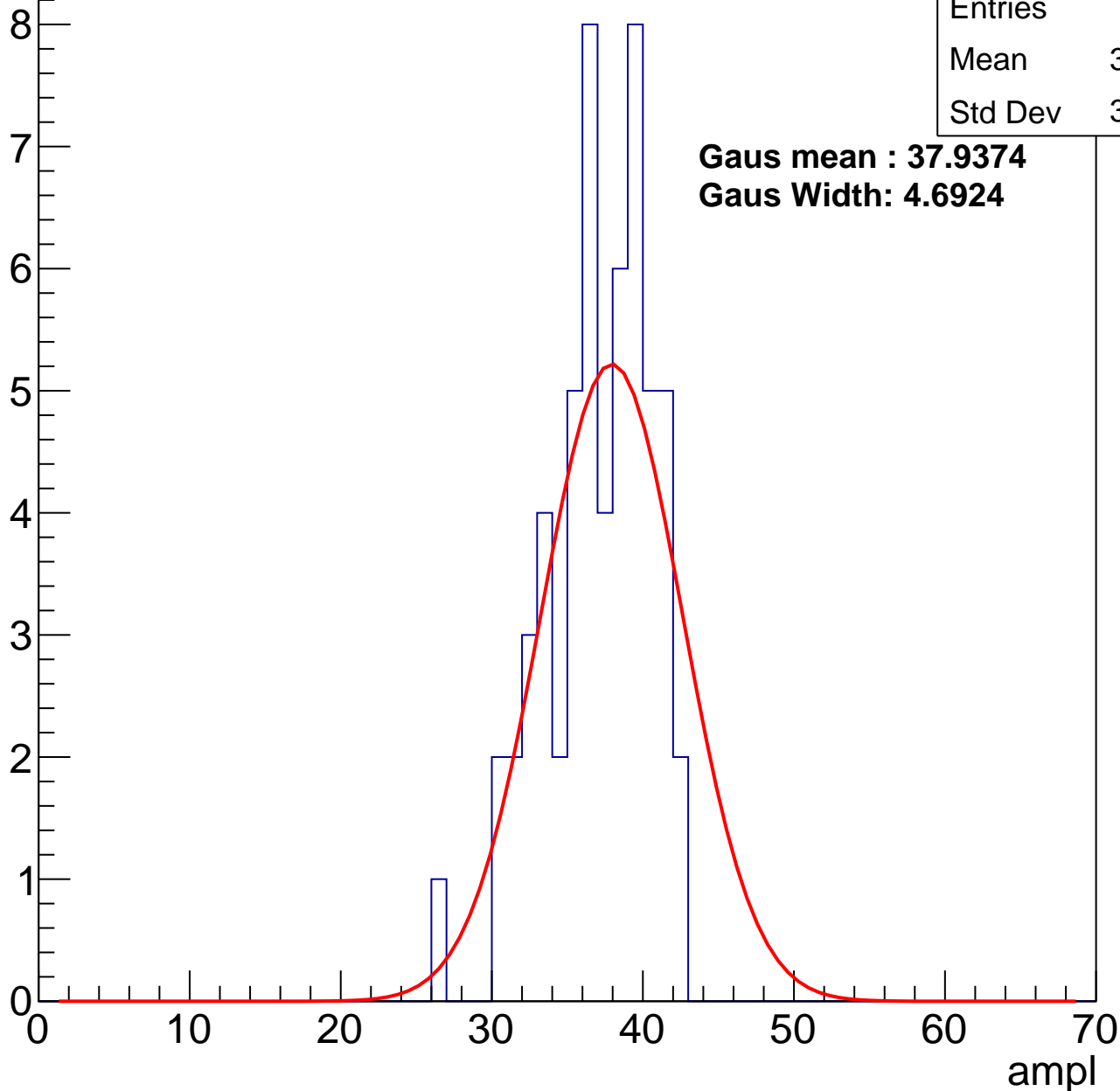
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.56
Std Dev	3.449

Gaus mean : 37.9374

Gaus Width: 4.6924



B0L002S, U17-ch53, adc2

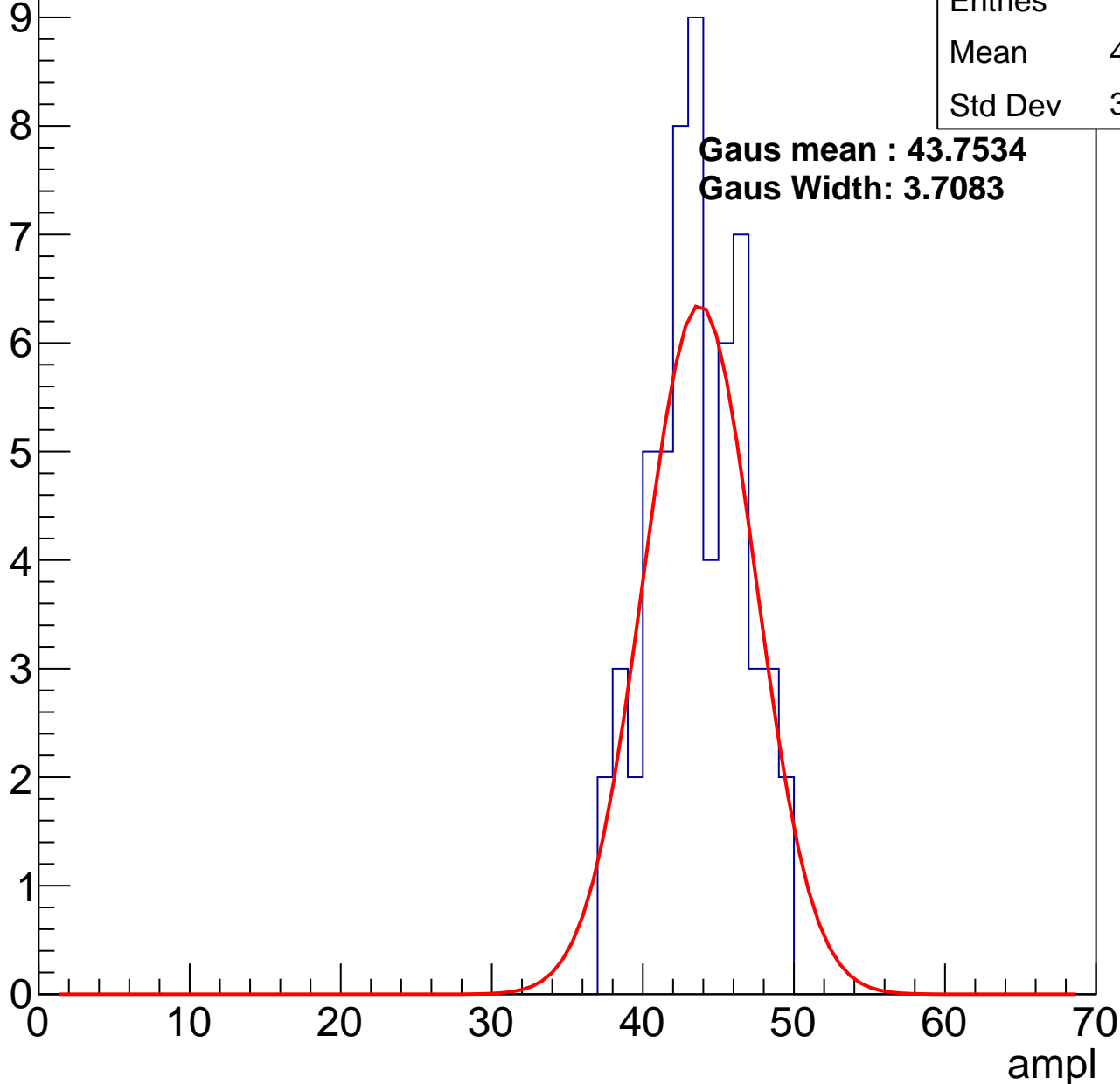
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.14
Std Dev	3.017

Gaus mean : 43.7534

Gaus Width: 3.7083

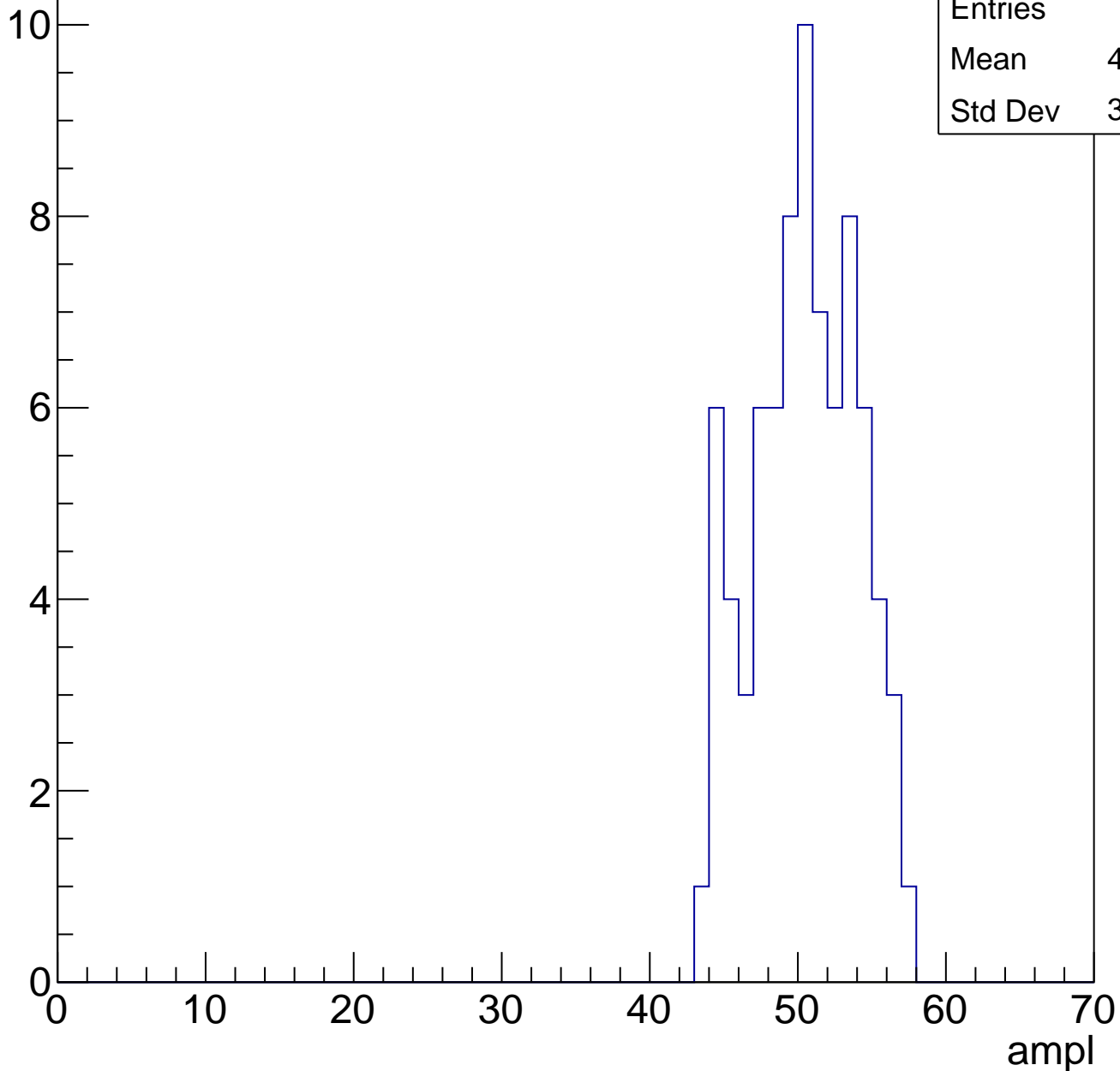


B0L002S, U17-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	49.99
Std Dev	3.477

Entry

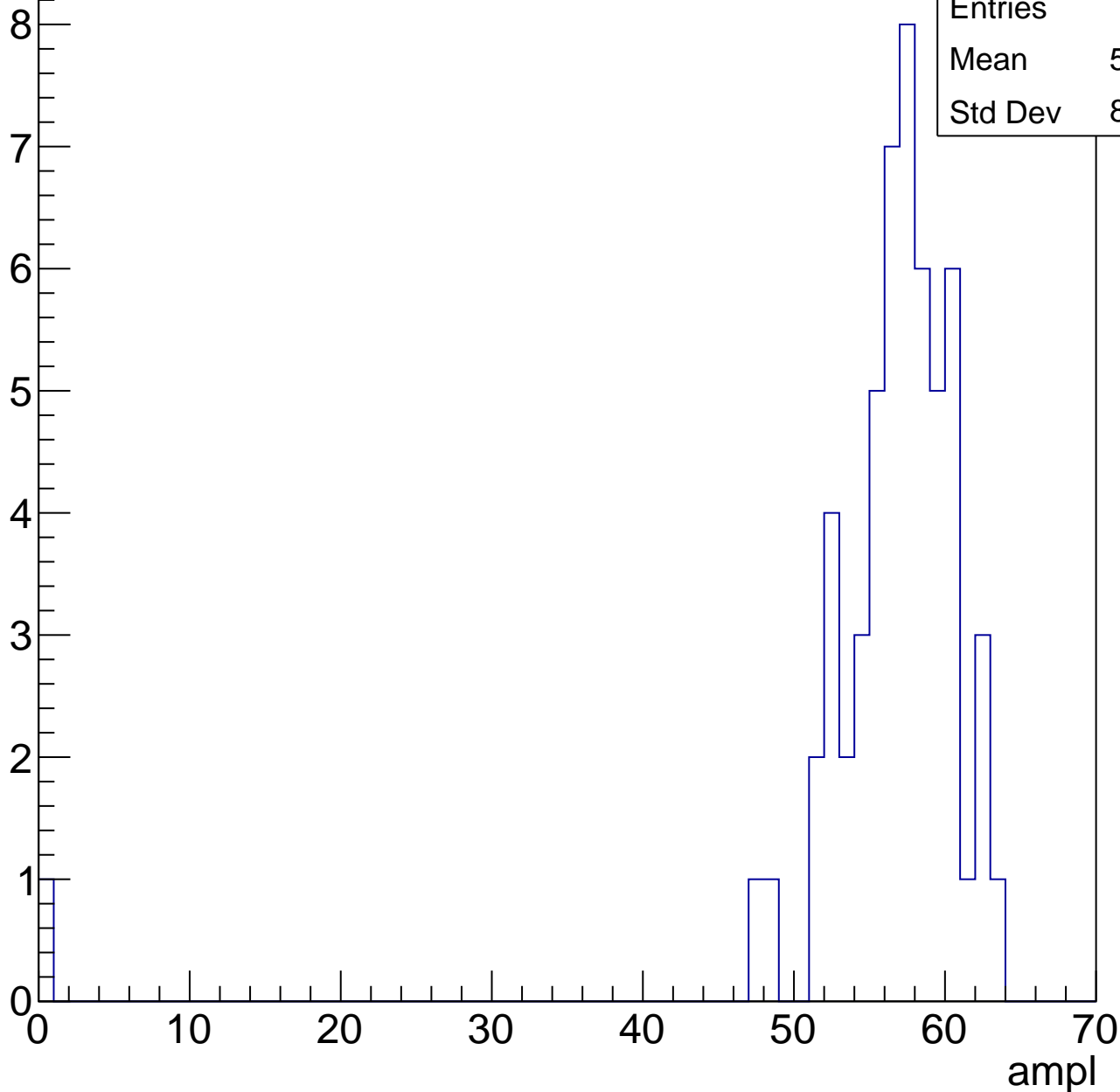


B0L002S, U17-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	55.52
Std Dev	8.207

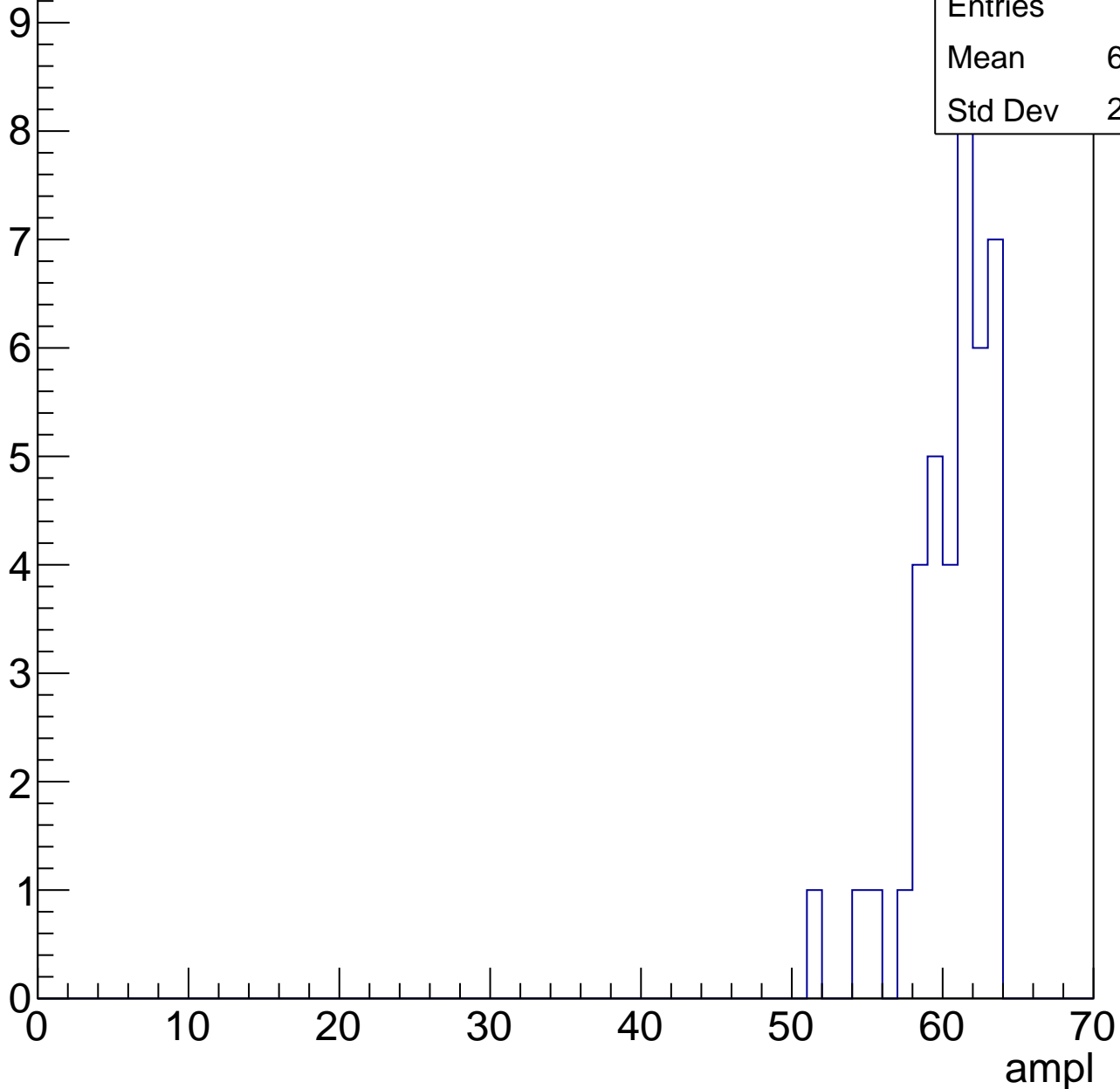


B0L002S, U17-ch53, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	60.15
Std Dev	2.617



B0L002S, U17-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L002S, U17-ch54, adc0

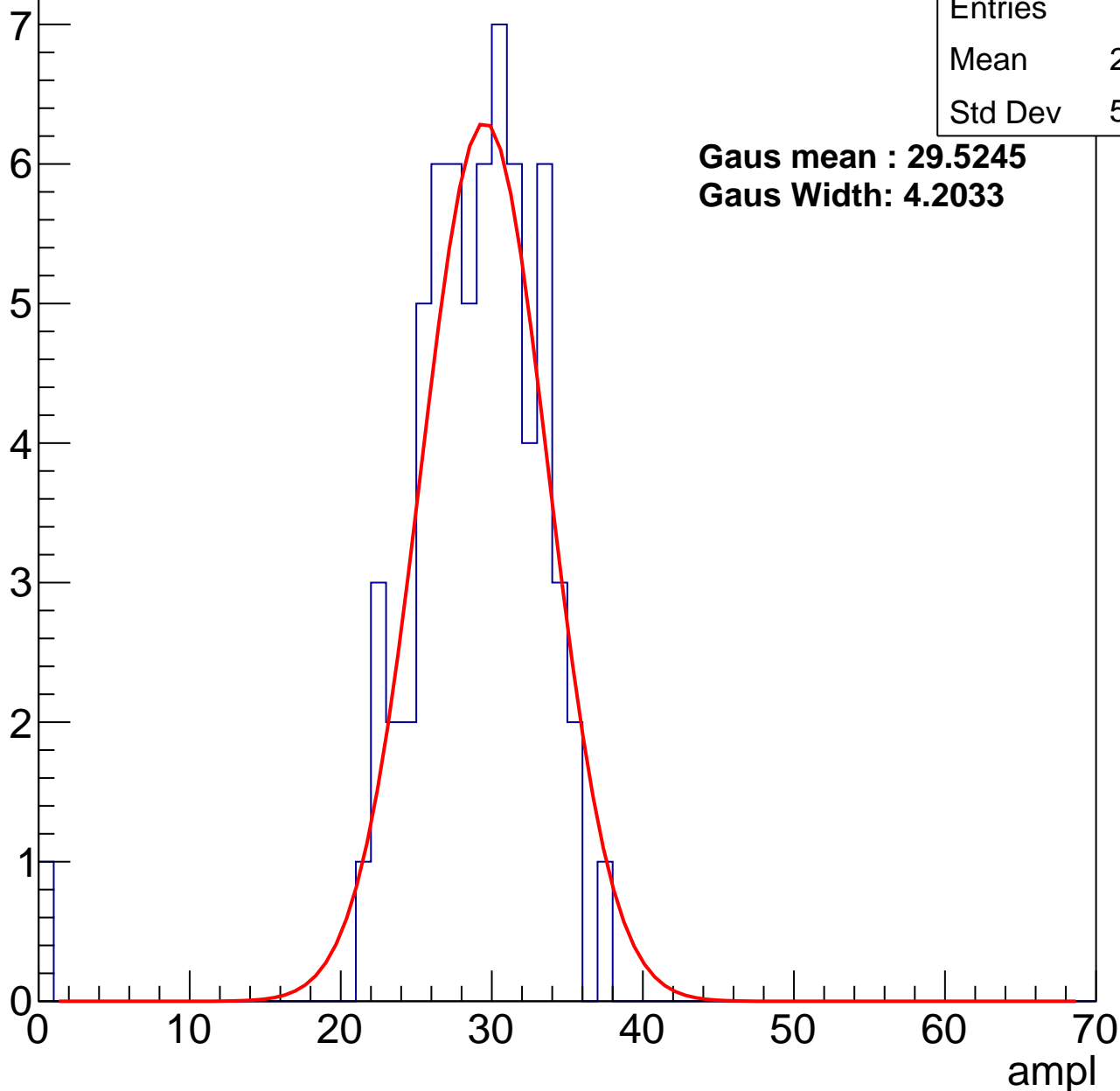
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	28.32
Std Dev	5.046

Gaus mean : 29.5245

Gaus Width: 4.2033



B0L002S, U17-ch54, adc1

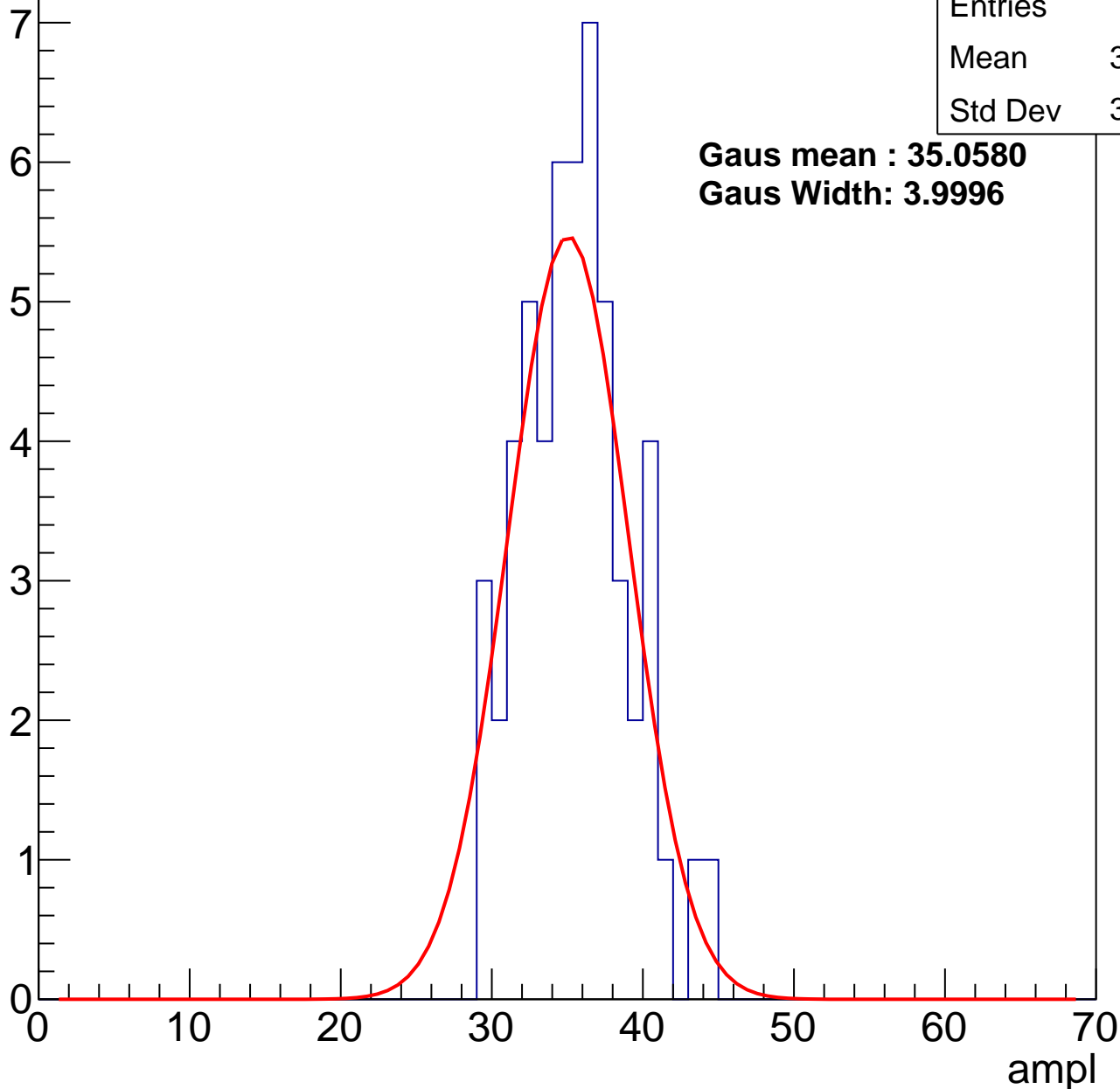
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	35.07
Std Dev	3.485

Gaus mean : 35.0580

Gaus Width: 3.9996



B0L002S, U17-ch54, adc2

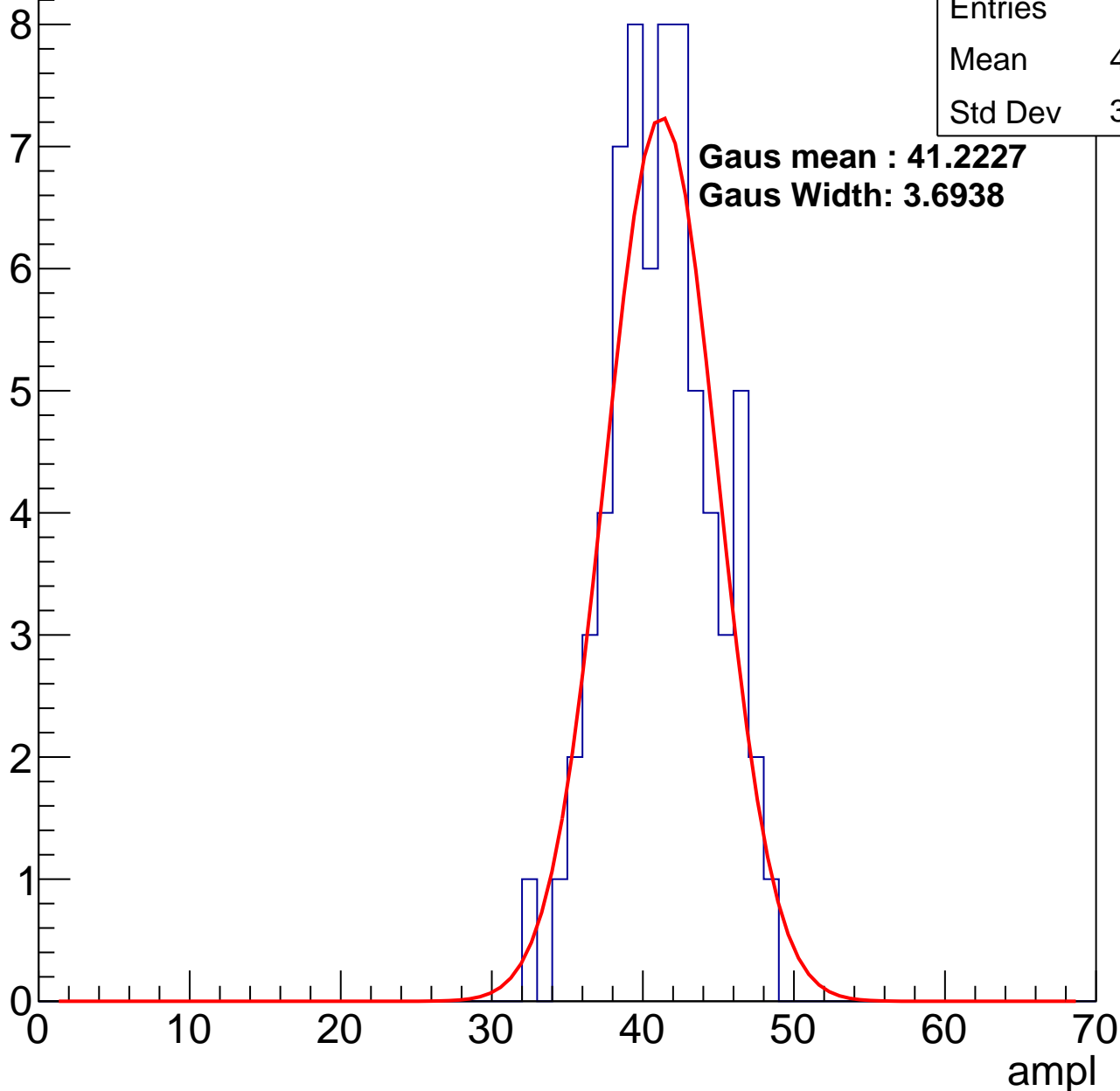
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	40.76
Std Dev	3.422

Gaus mean : 41.2227

Gaus Width: 3.6938

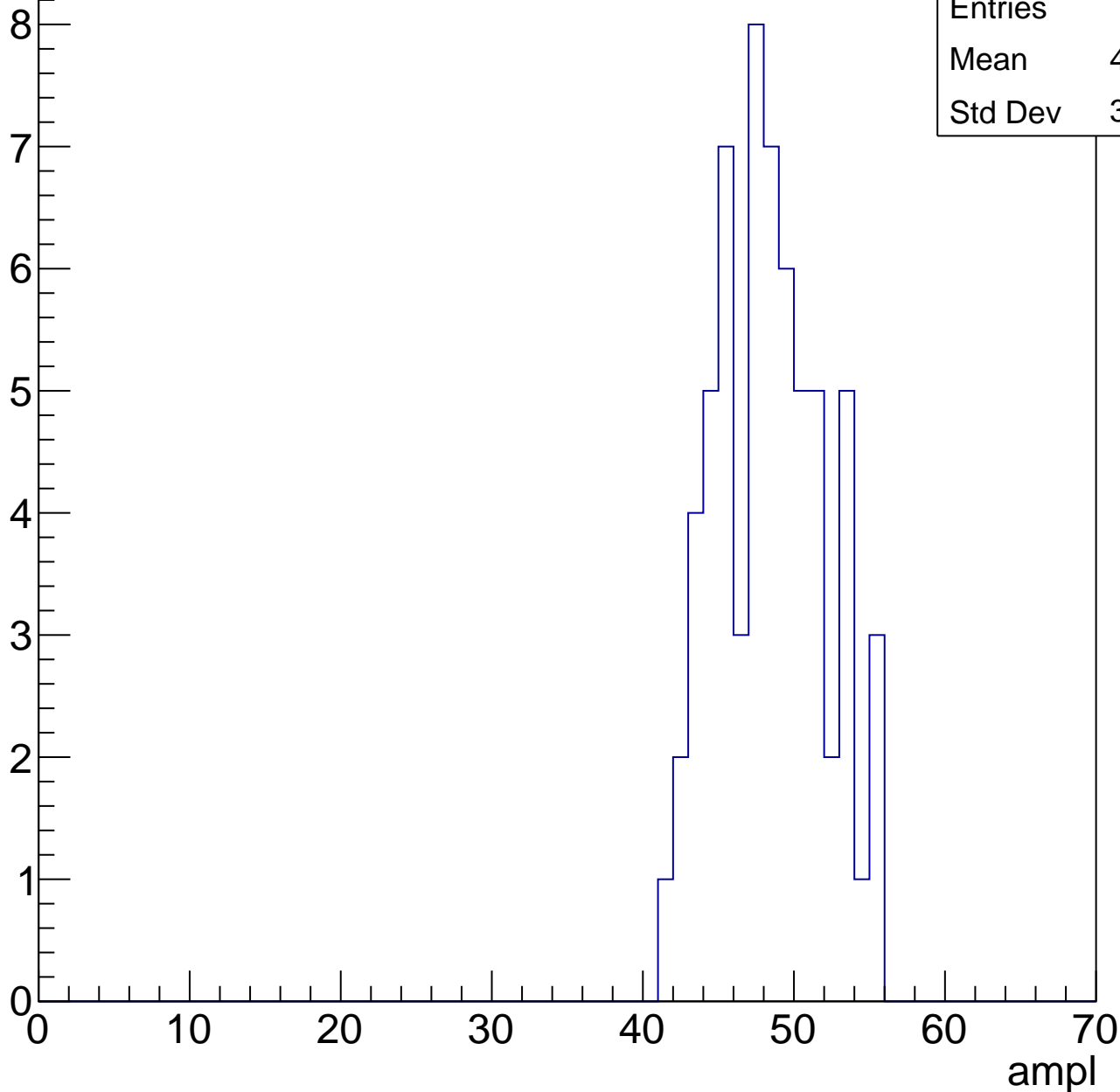


B0L002S, U17-ch54, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	47.95
Std Dev	3.524

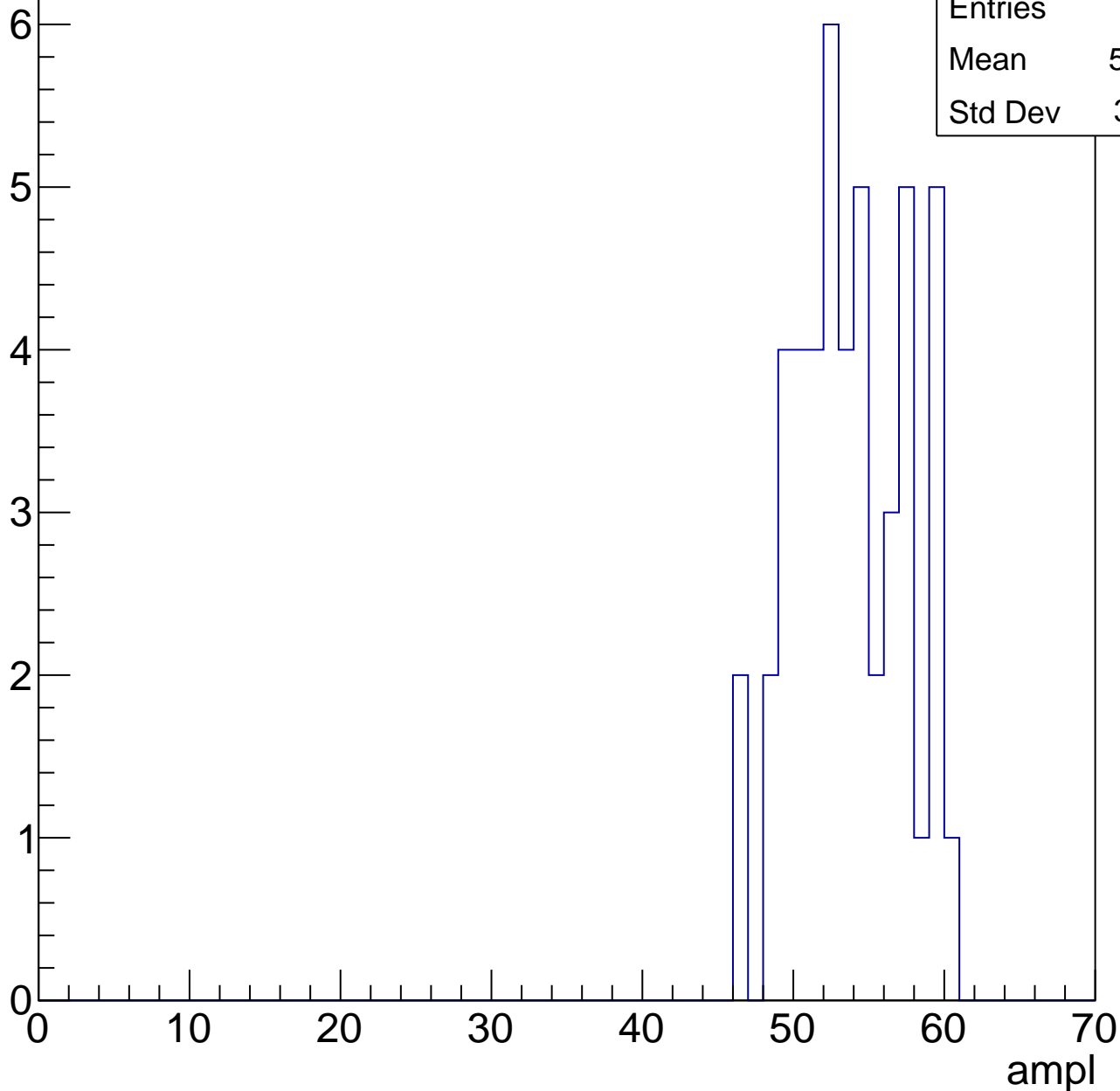


B0L002S, U17-ch54, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	53.29
Std Dev	3.651

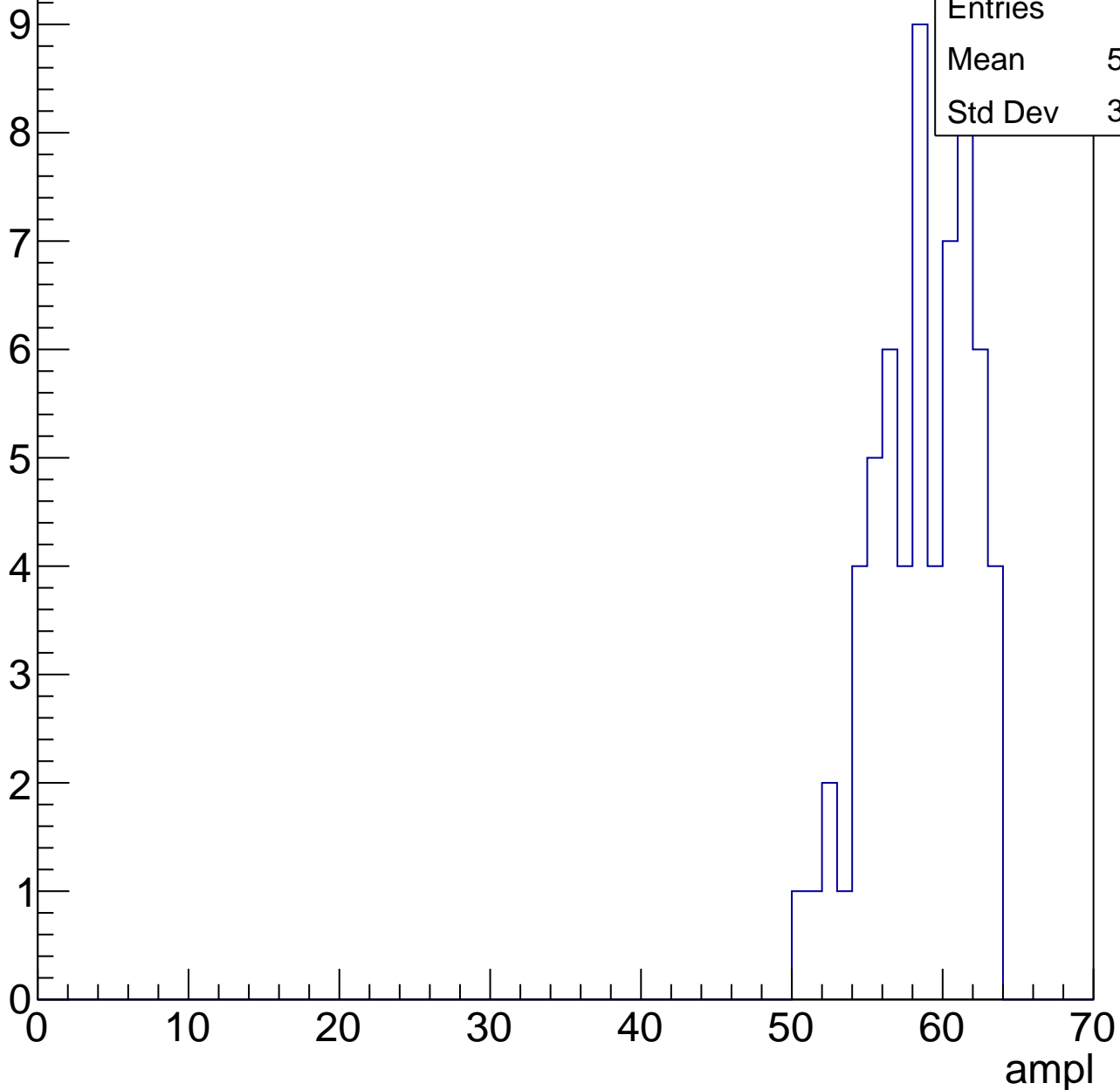


B0L002S, U17-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	58.16
Std Dev	3.223

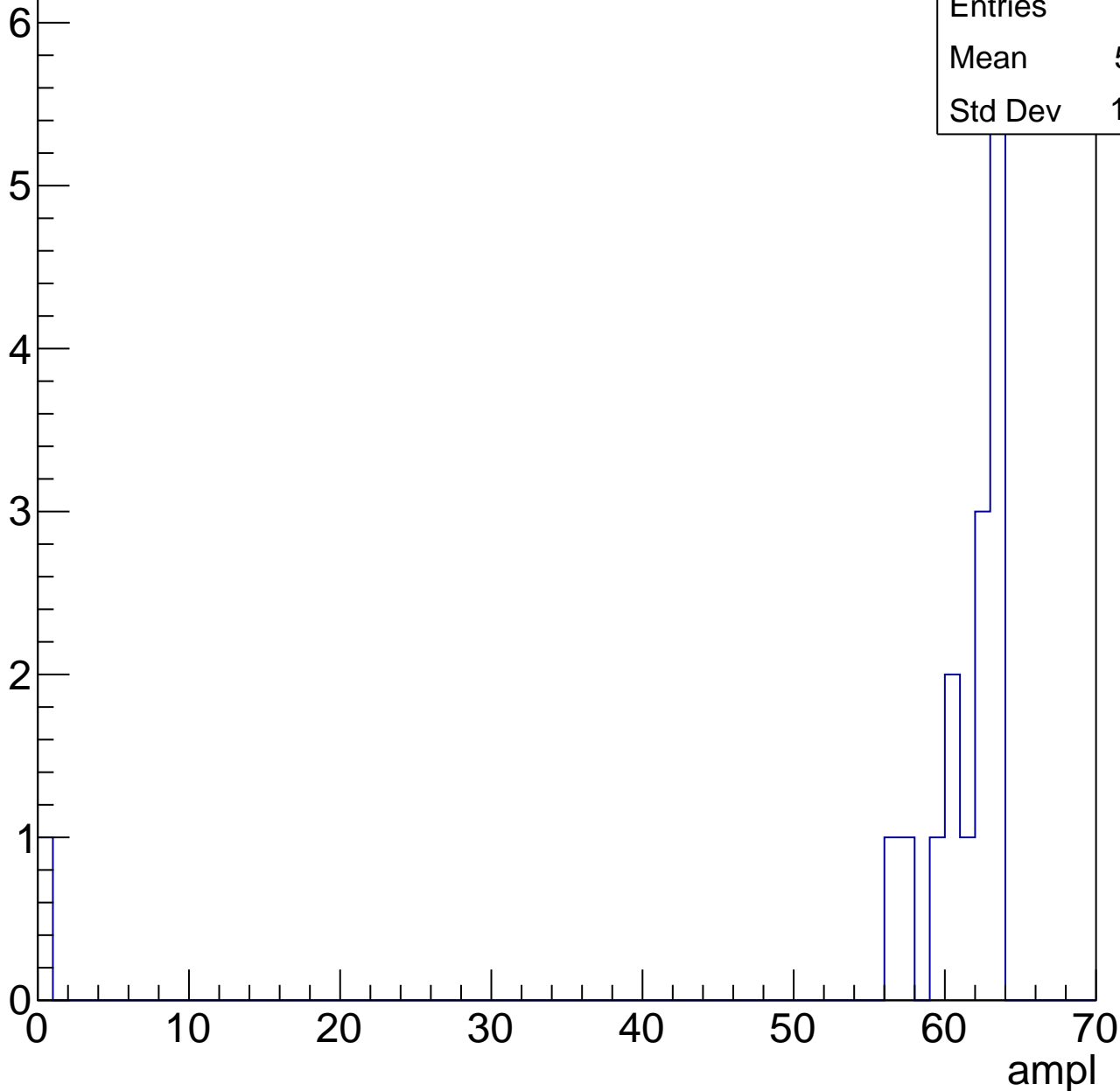


B0L002S, U17-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

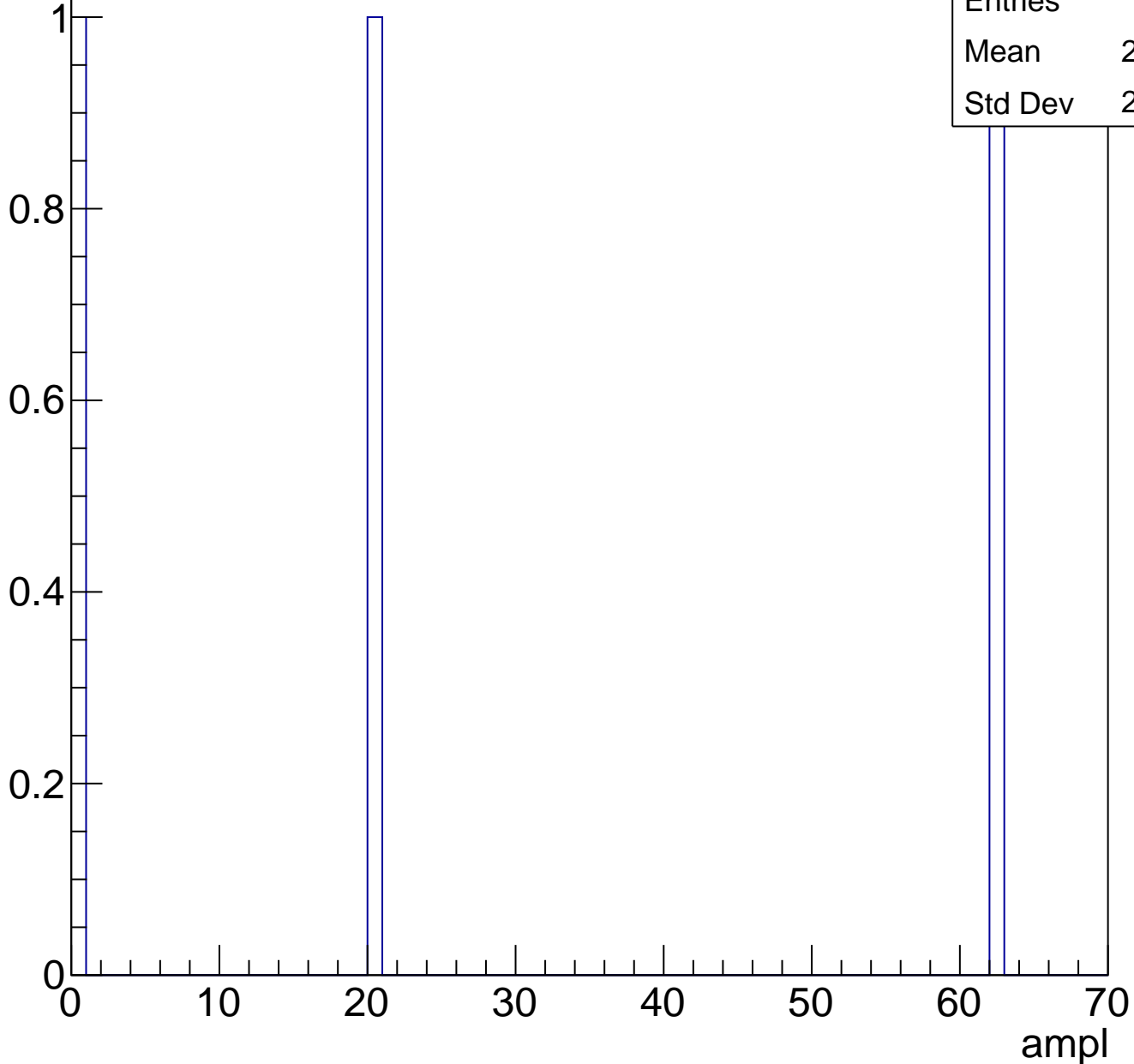
Entries	16
Mean	57.31
Std Dev	14.95



B0L002S, U17-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	27.33
Std Dev	25.84

B0L002S, U17-ch55, adc0

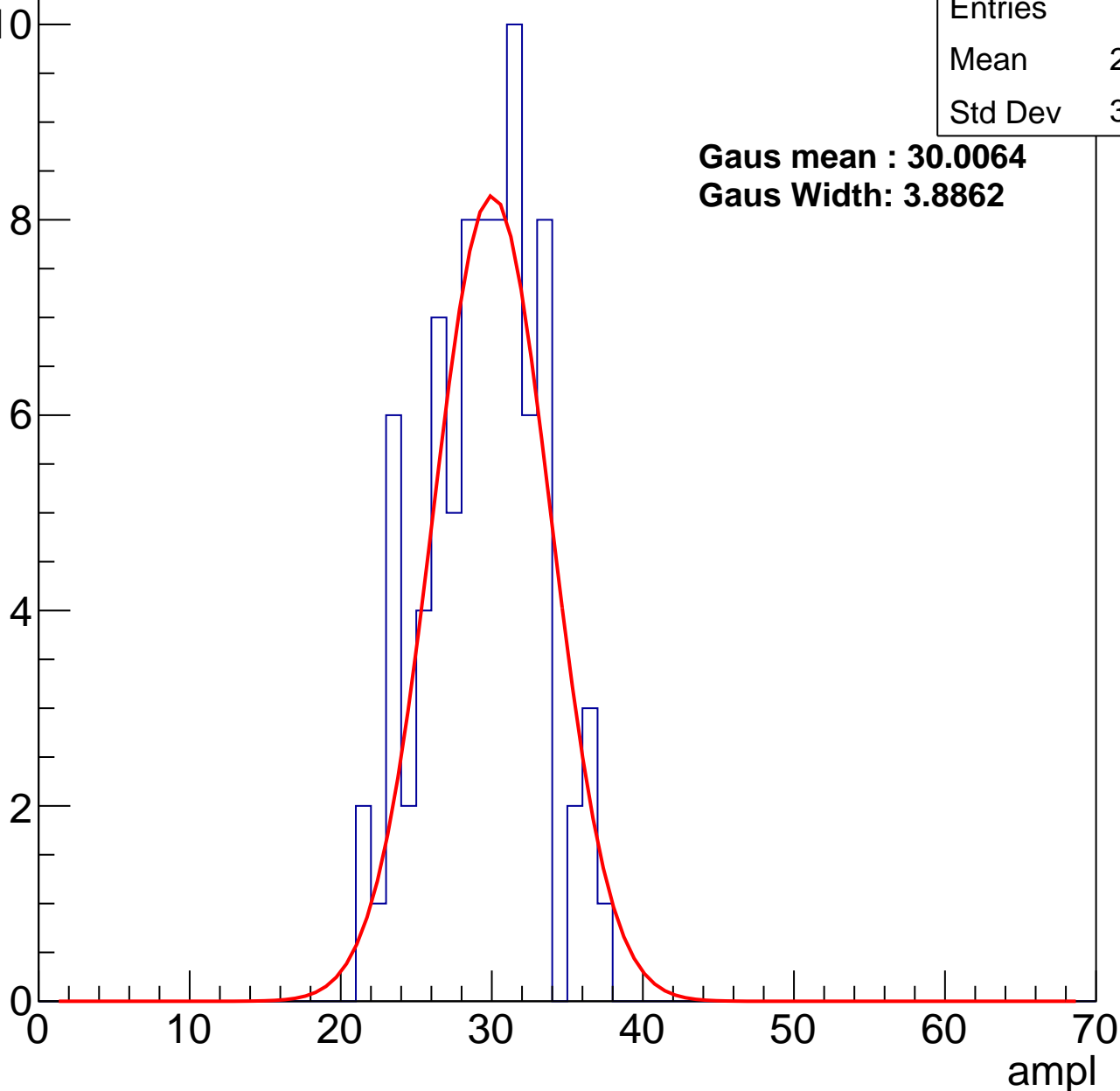
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	28.94
Std Dev	3.703

Gaus mean : 30.0064

Gaus Width: 3.8862



B0L002S, U17-ch55, adc1

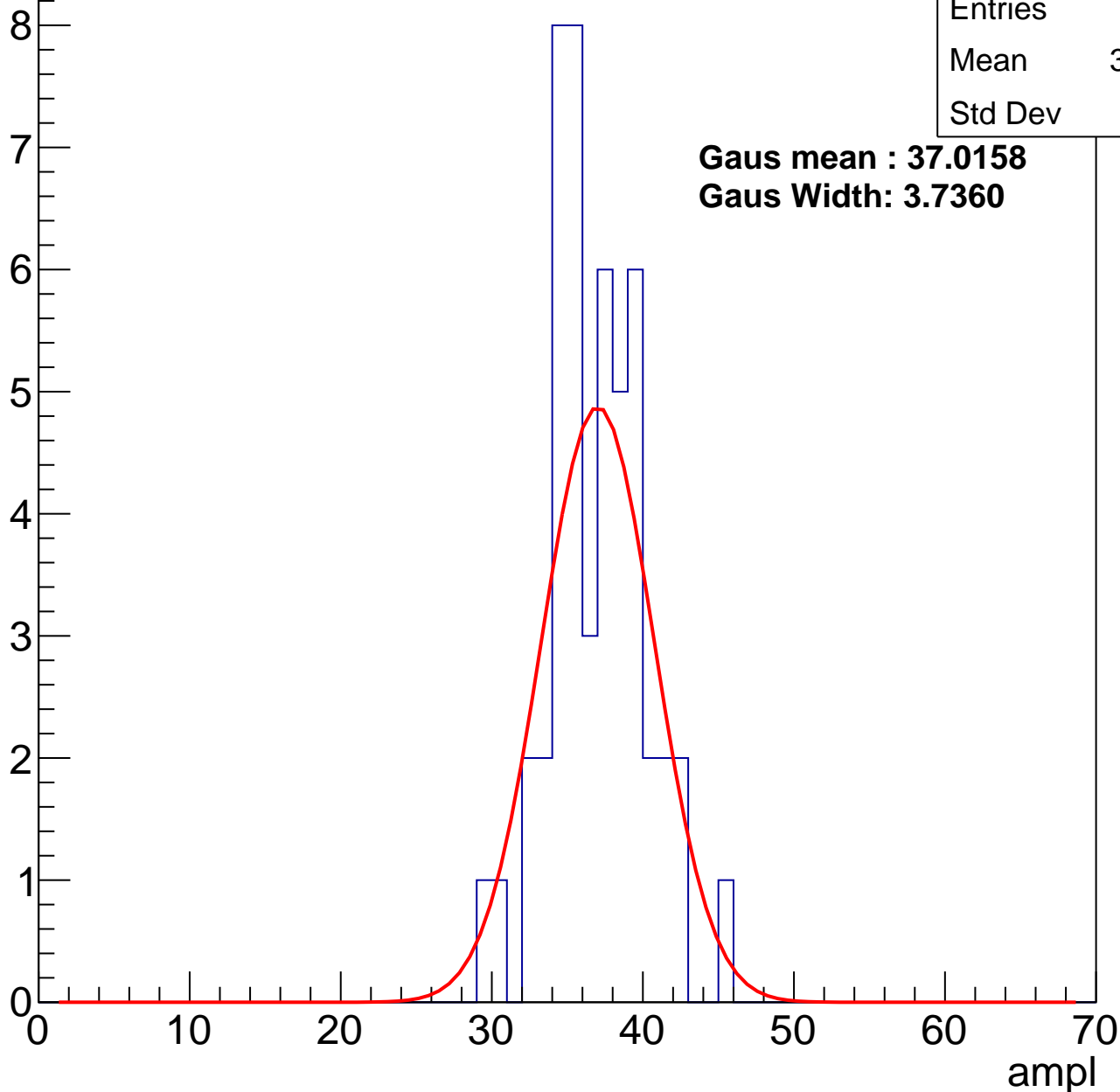
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	36.45
Std Dev	3.13

Gaus mean : 37.0158

Gaus Width: 3.7360



B0L002S, U17-ch55, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	41.62
Std Dev	3.312

Gaus mean : 42.1529

Gaus Width: 3.9802

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

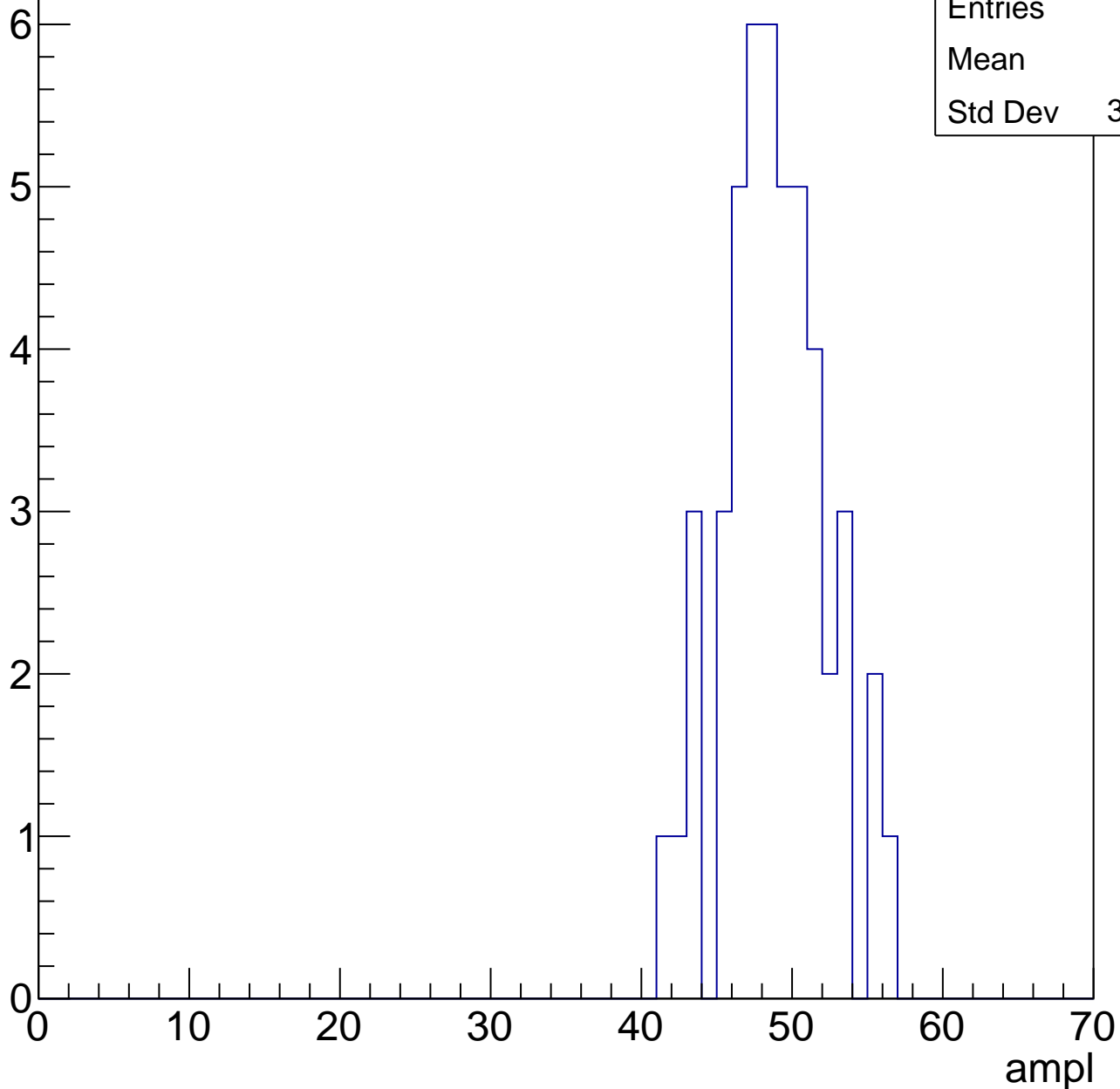


B0L002S, U17-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	48.4
Std Dev	3.375

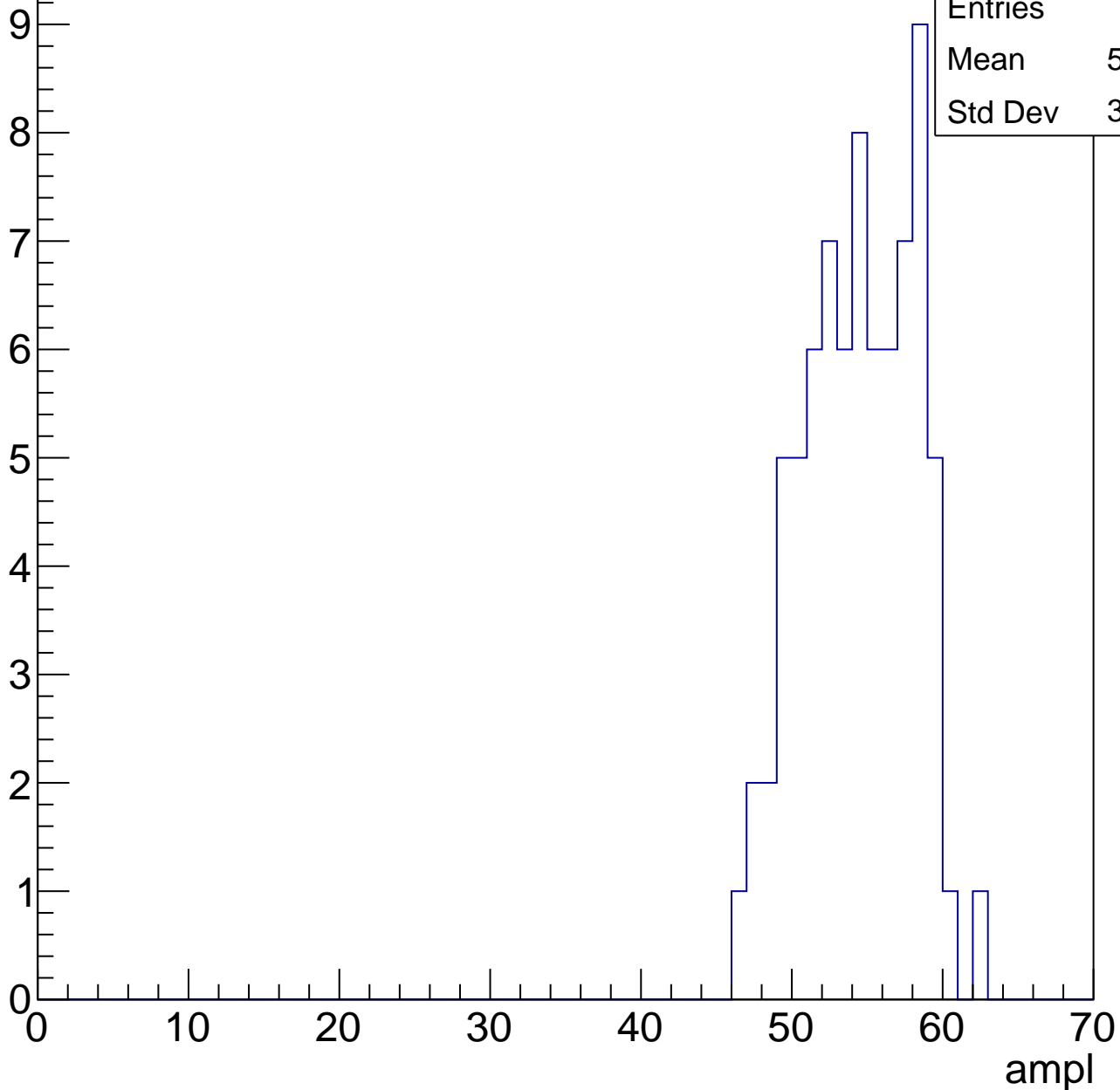


B0L002S, U17-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

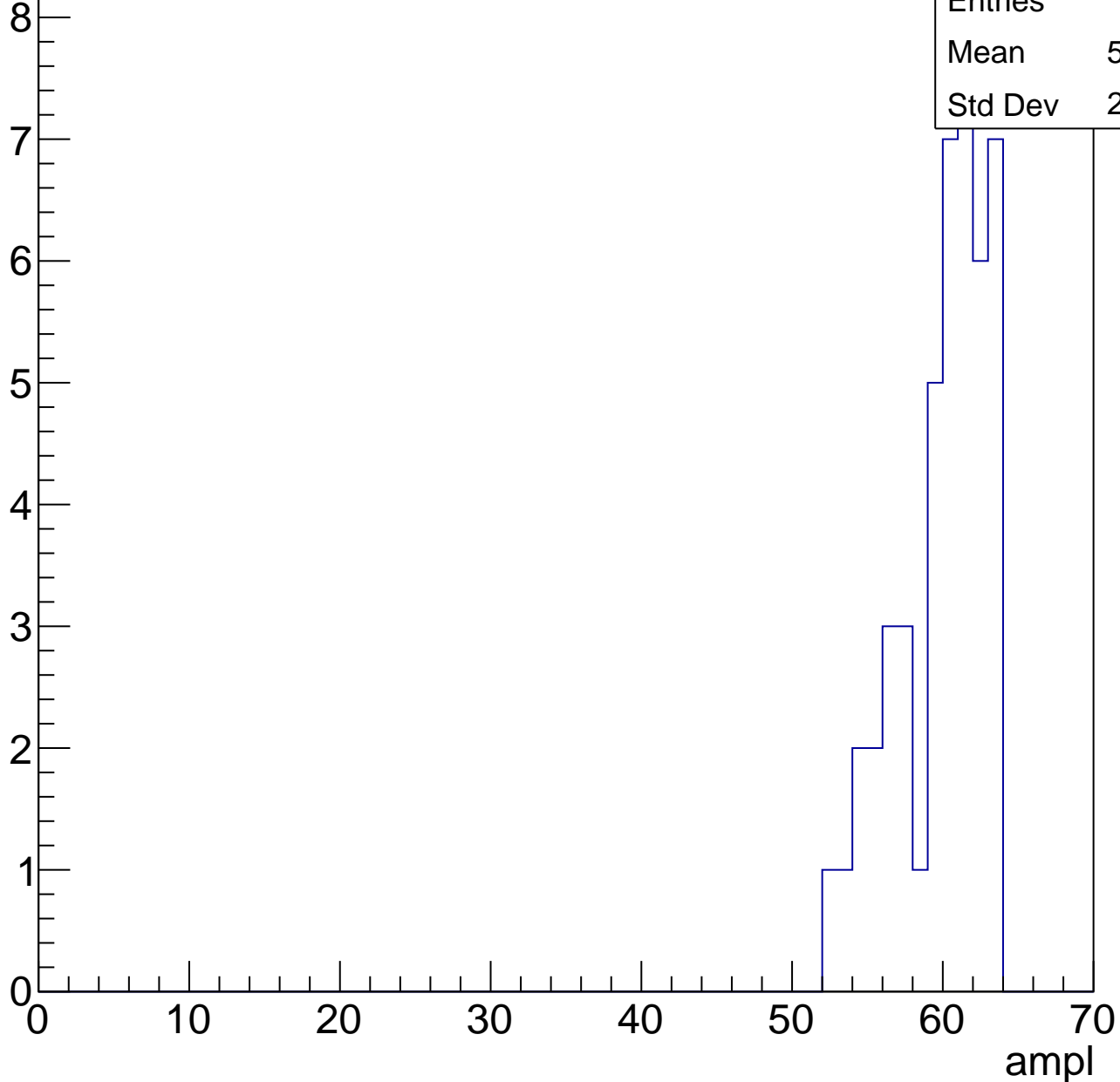
Entries	77
Mean	53.96
Std Dev	3.584



B0L002S, U17-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

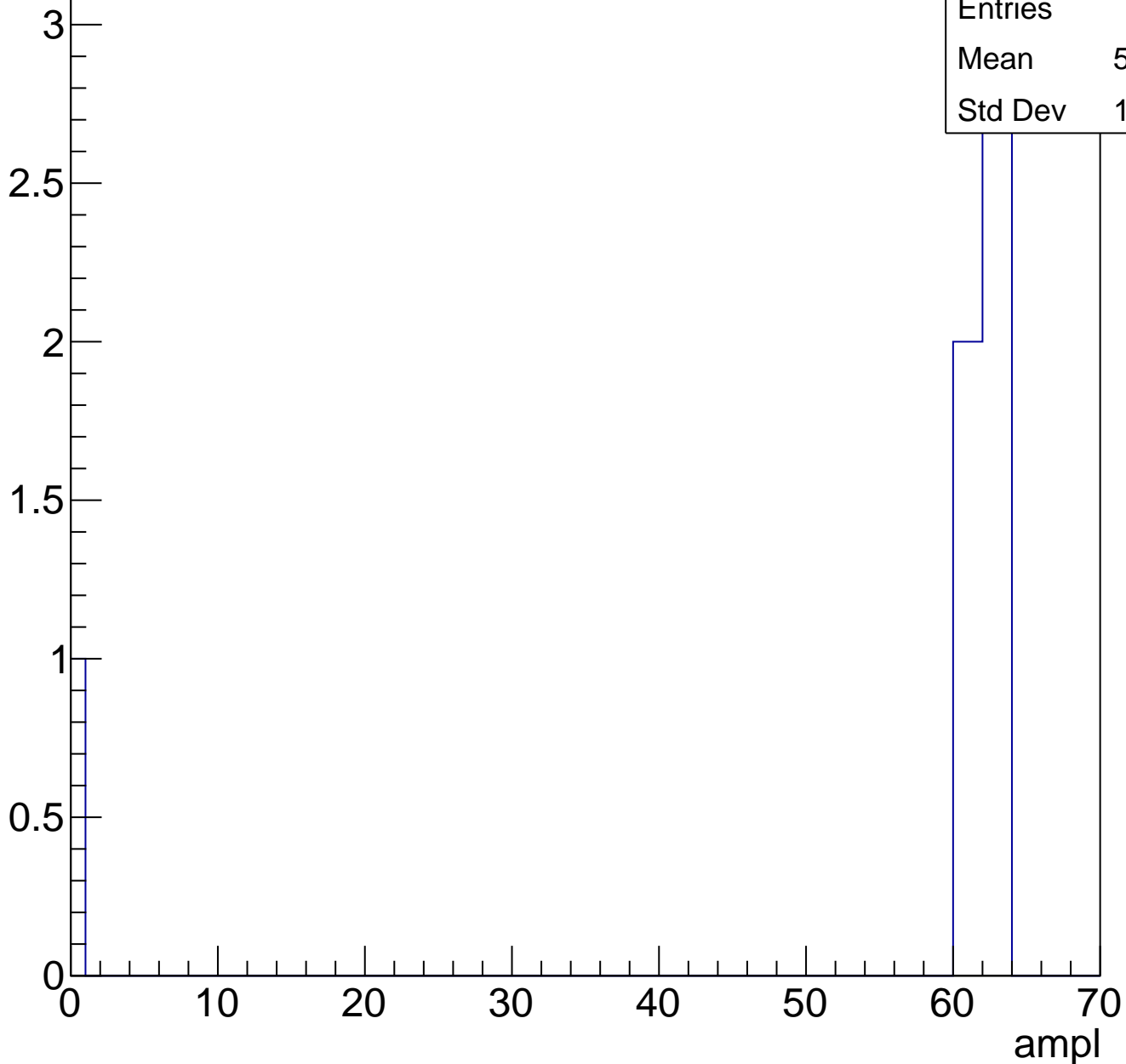
Entry



B0L002S, U17-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch56, adc0

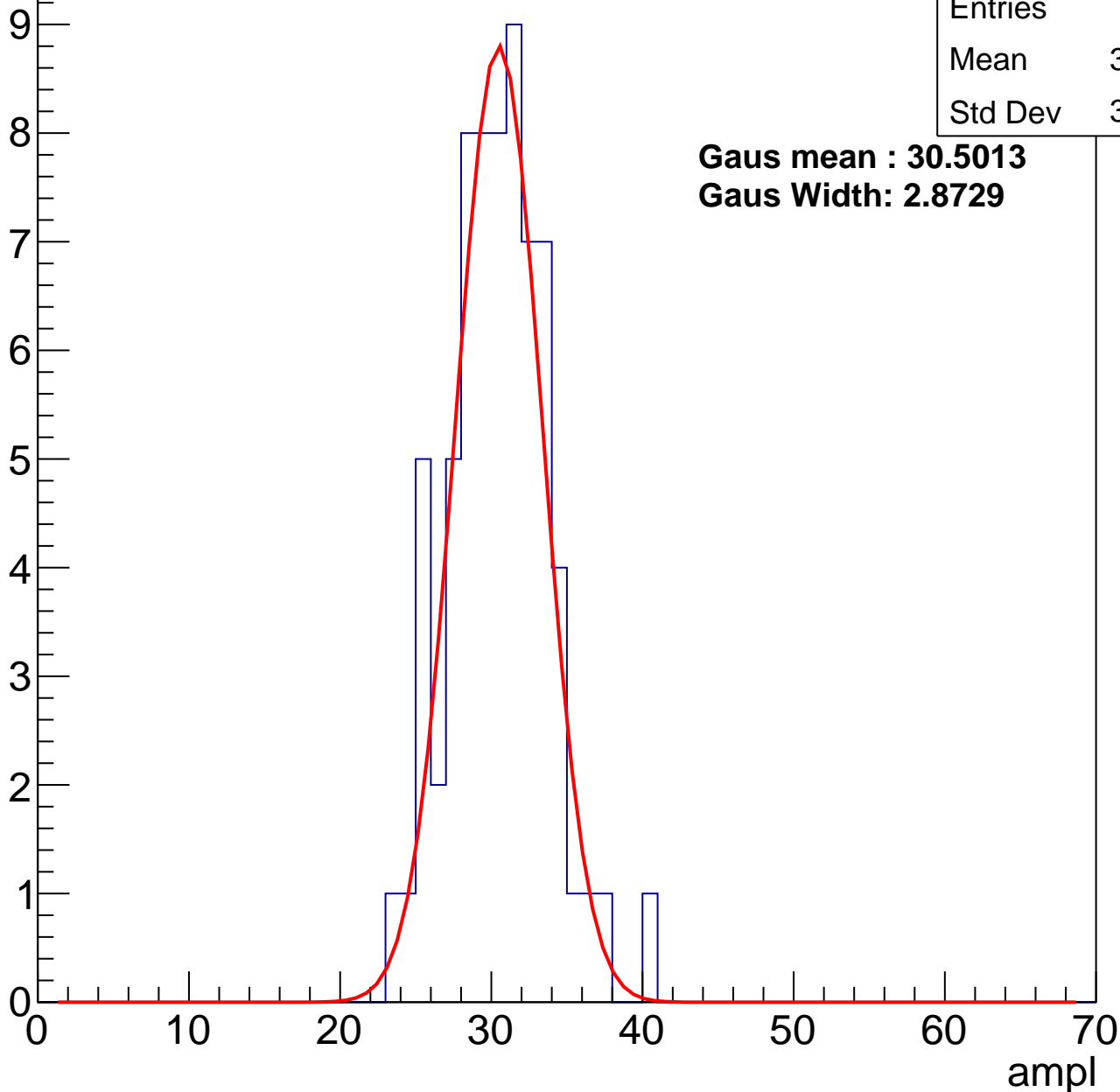
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.04
Std Dev	3.187

Gaus mean : 30.5013

Gaus Width: 2.8729



B0L002S, U17-ch56, adc1

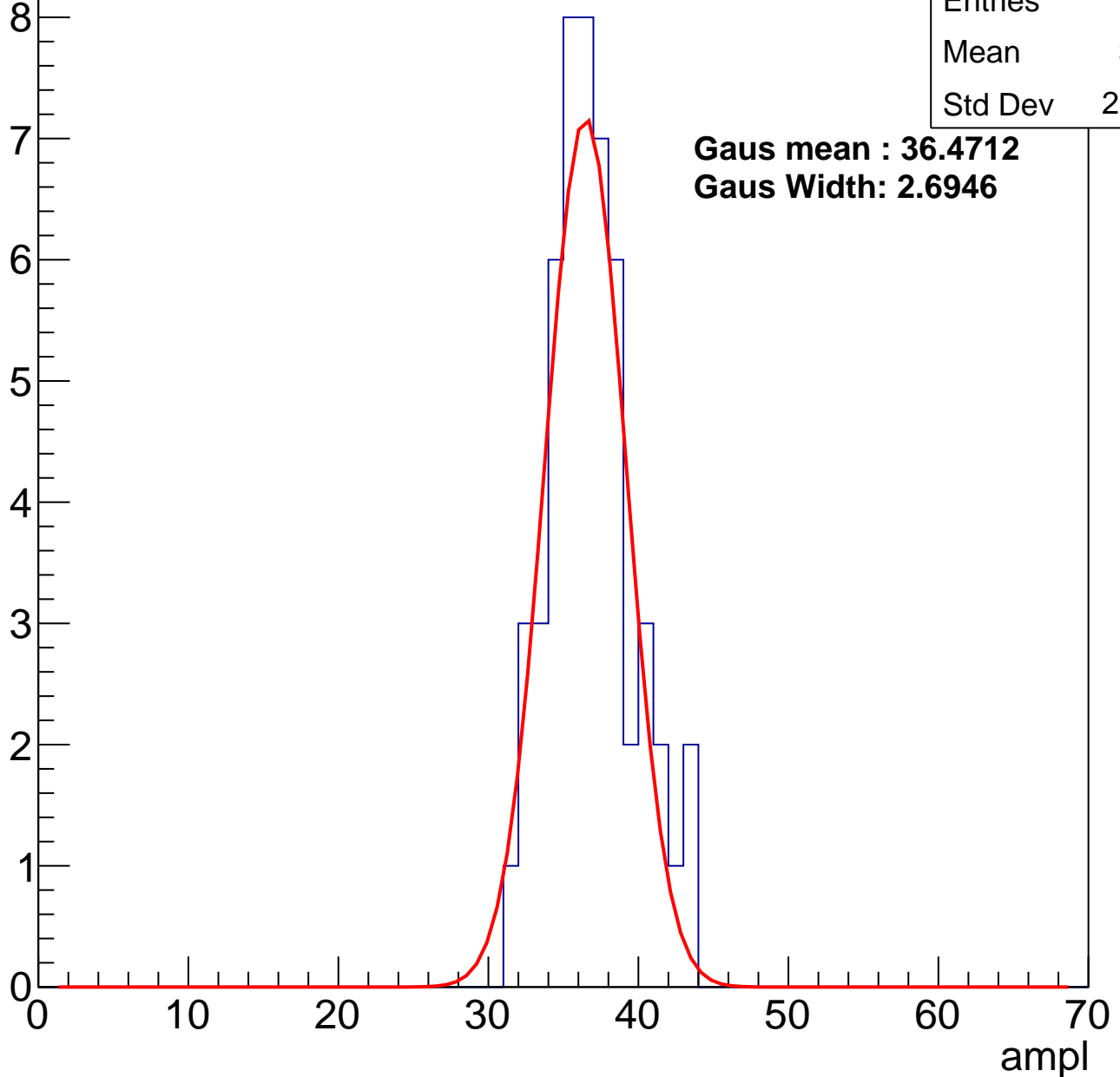
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	36.4
Std Dev	2.789

Gaus mean : 36.4712

Gaus Width: 2.6946



B0L002S, U17-ch56, adc2

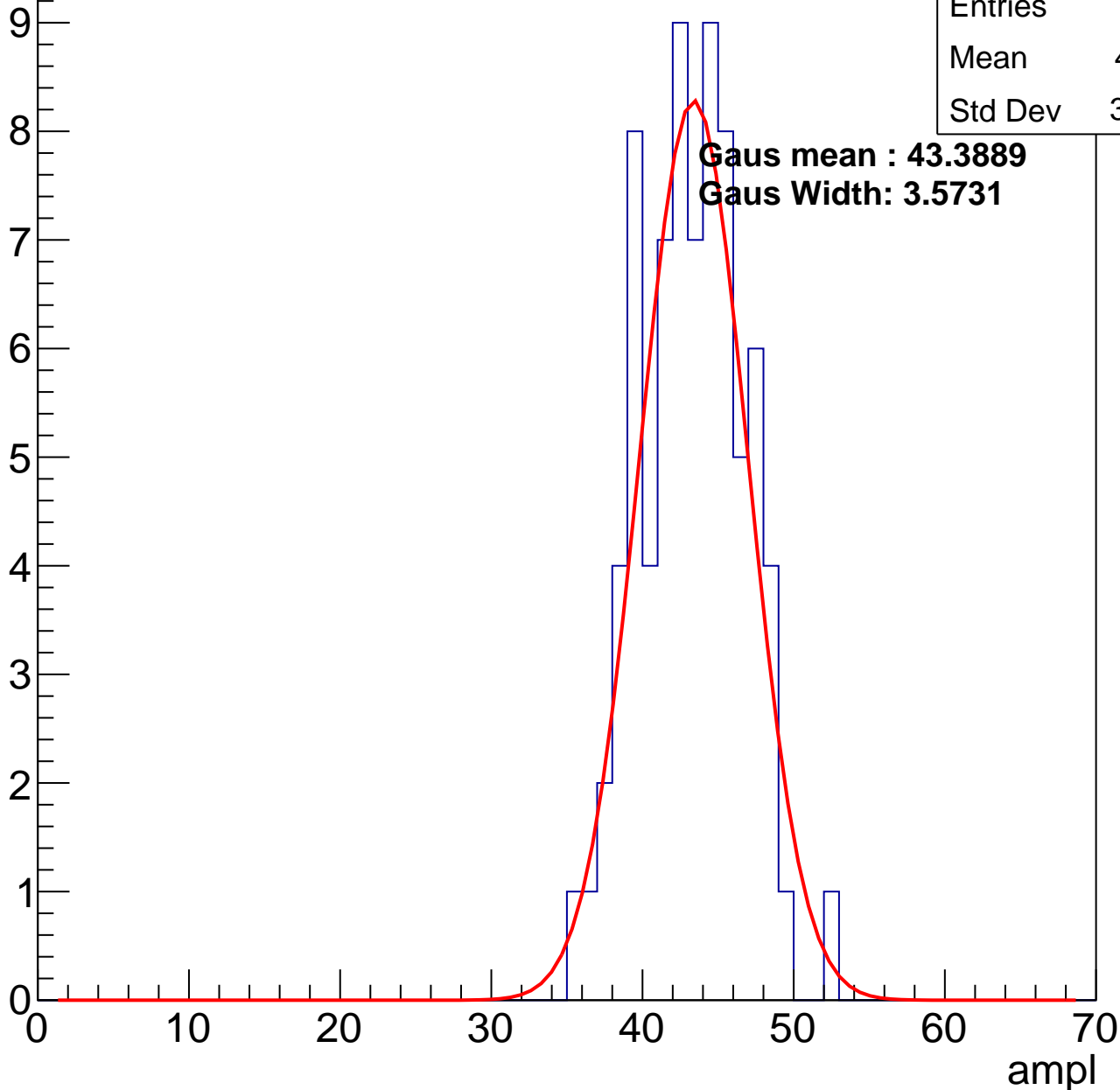
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	42.81
Std Dev	3.385

Gaus mean : 43.3889

Gaus Width: 3.5731

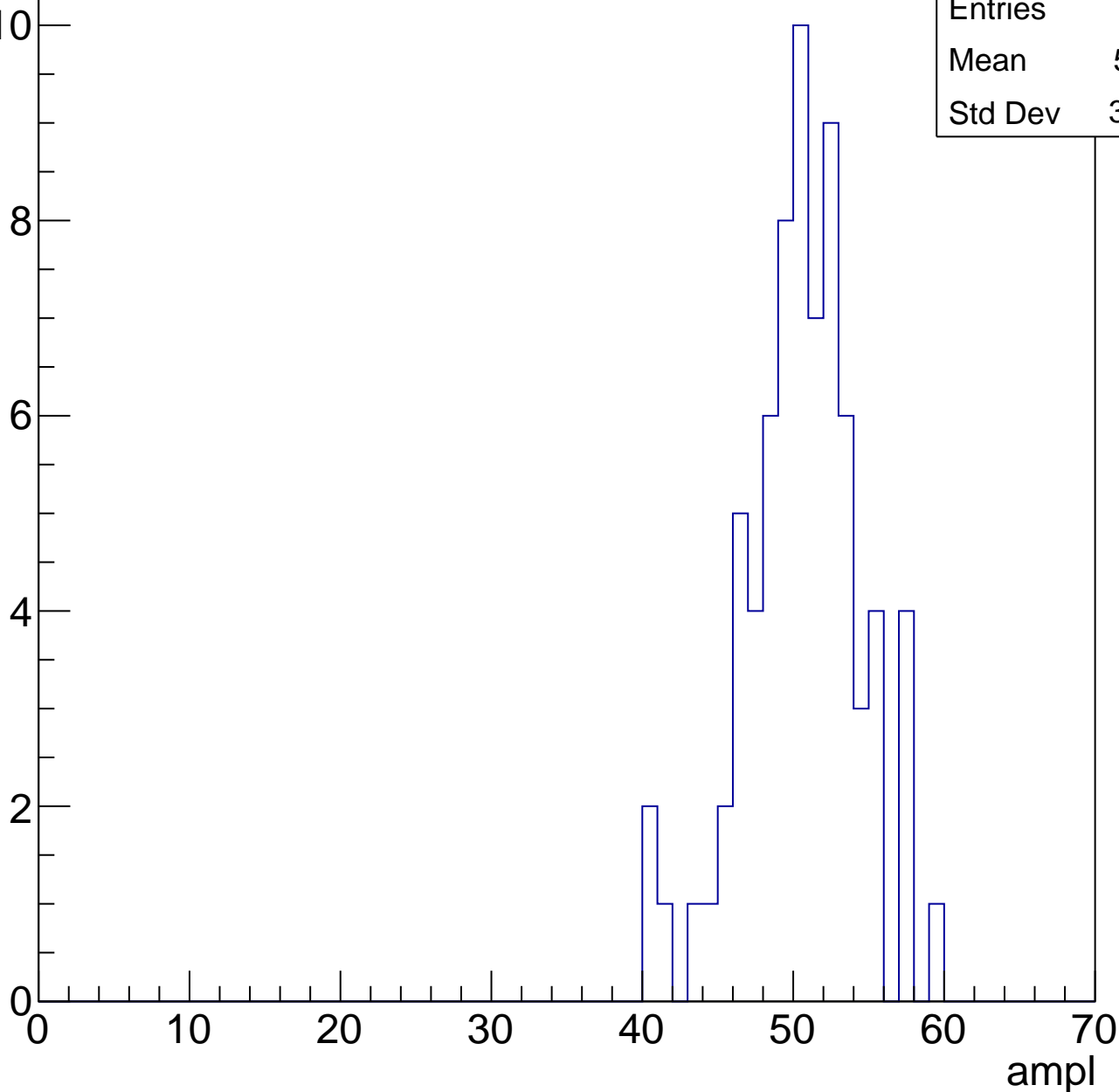


B0L002S, U17-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	50.11
Std Dev	3.829

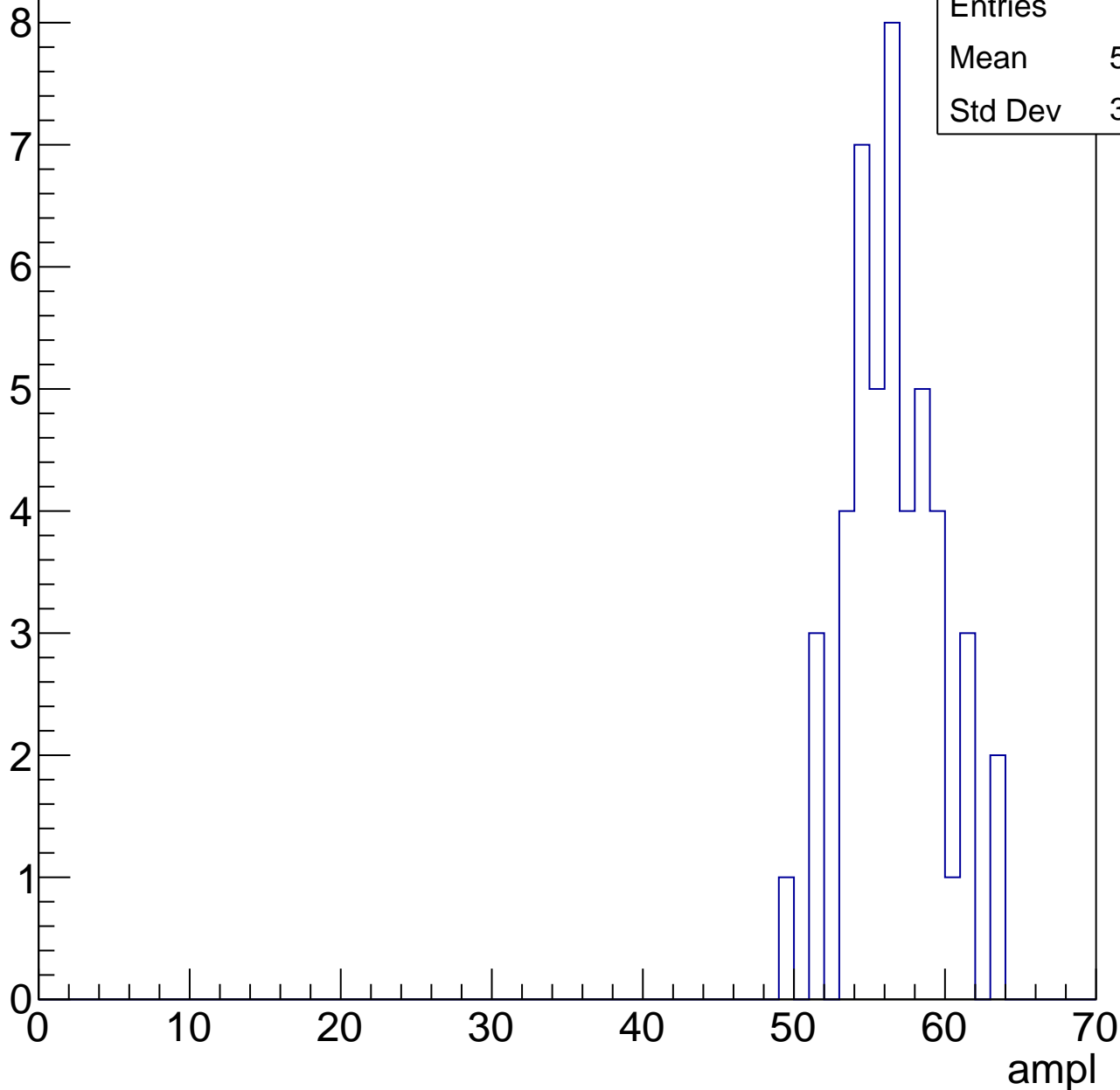


B0L002S, U17-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

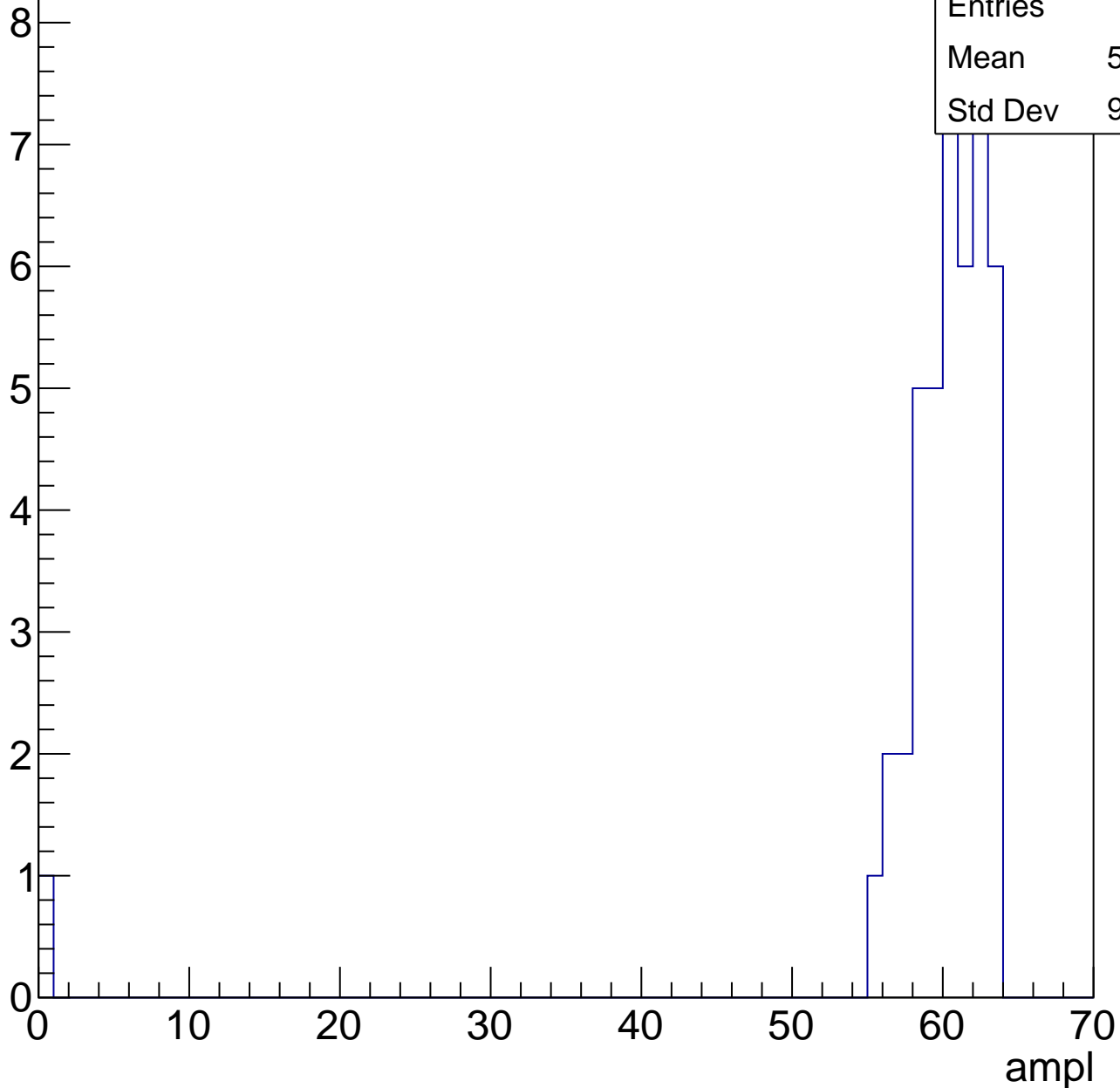
Entries	47
Mean	56.13
Std Dev	3.064



B0L002S, U17-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

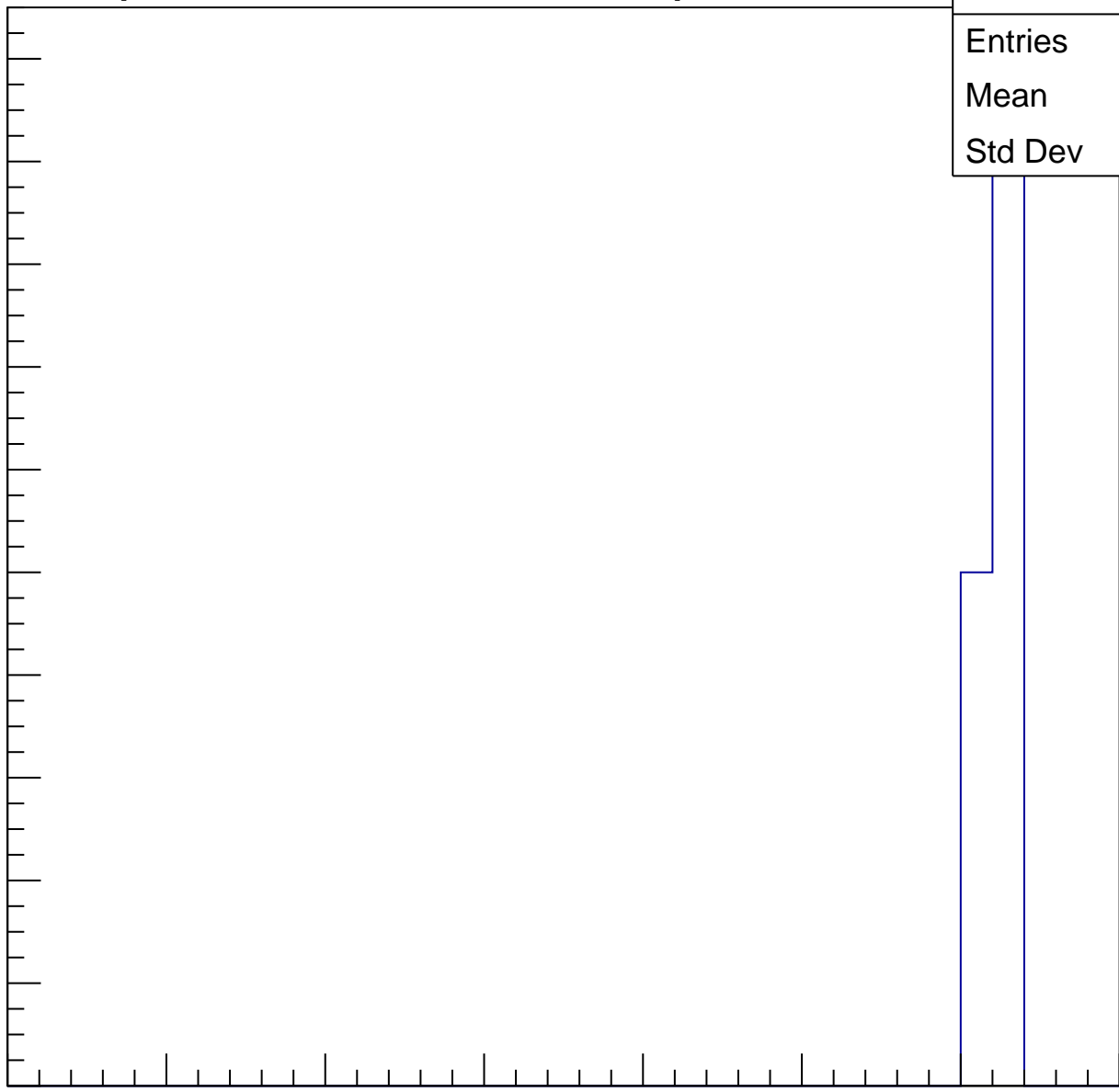
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch57, adc0

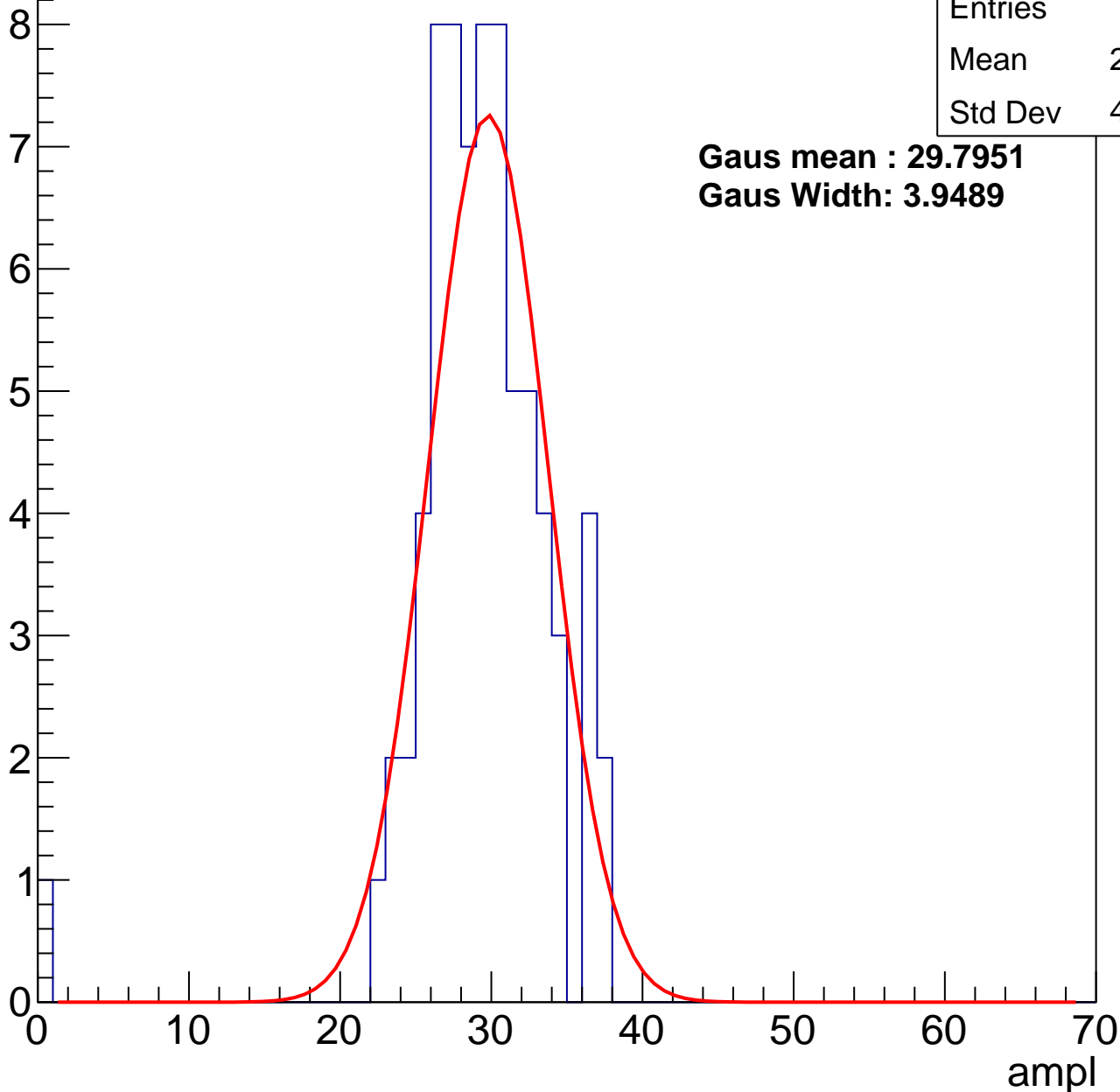
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	28.82
Std Dev	4.889

Gaus mean : 29.7951

Gaus Width: 3.9489



B0L002S, U17-ch57, adc1

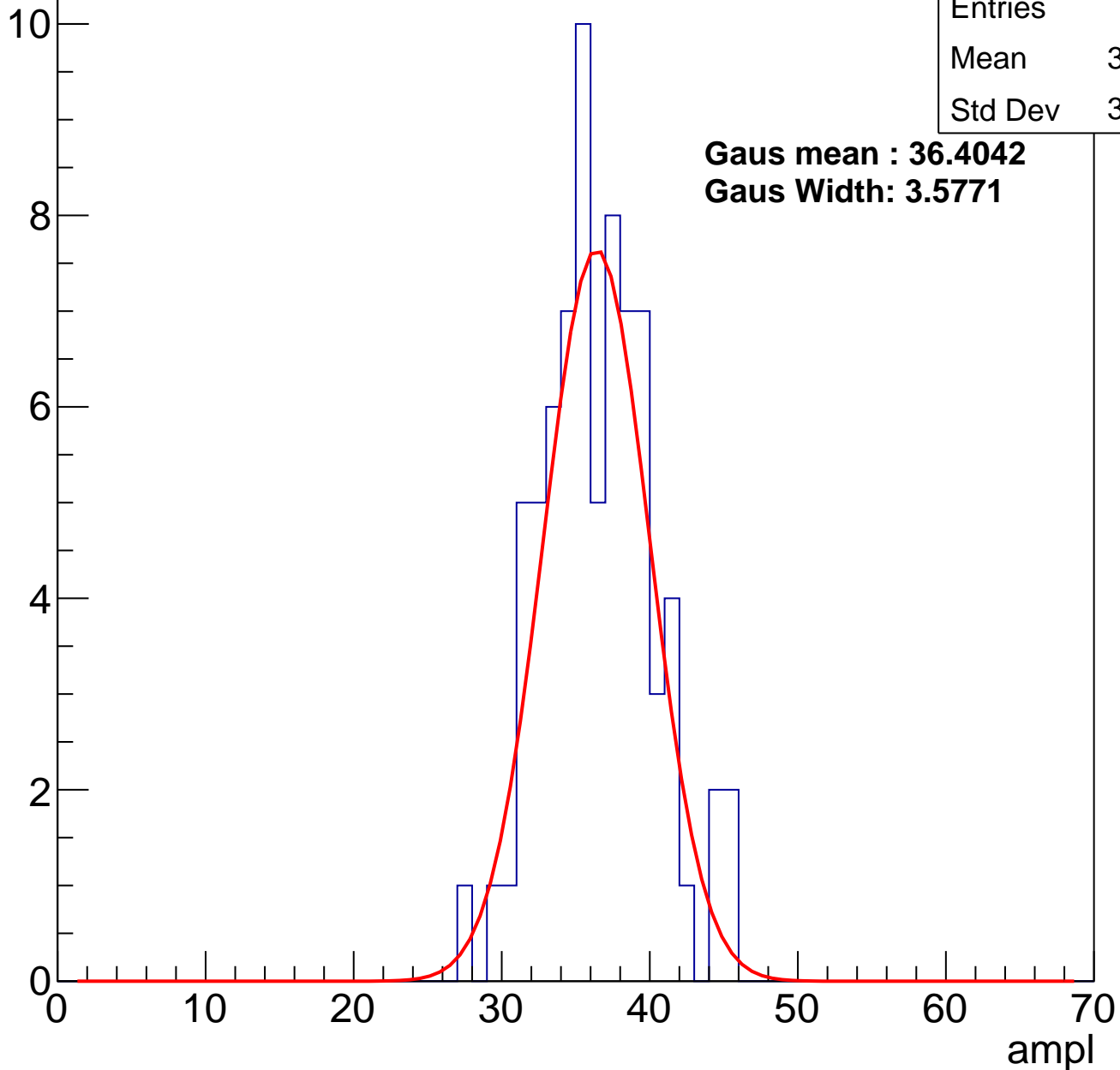
calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	36.08
Std Dev	3.716

Gaus mean : 36.4042

Gaus Width: 3.5771

Entry



B0L002S, U17-ch57, adc2

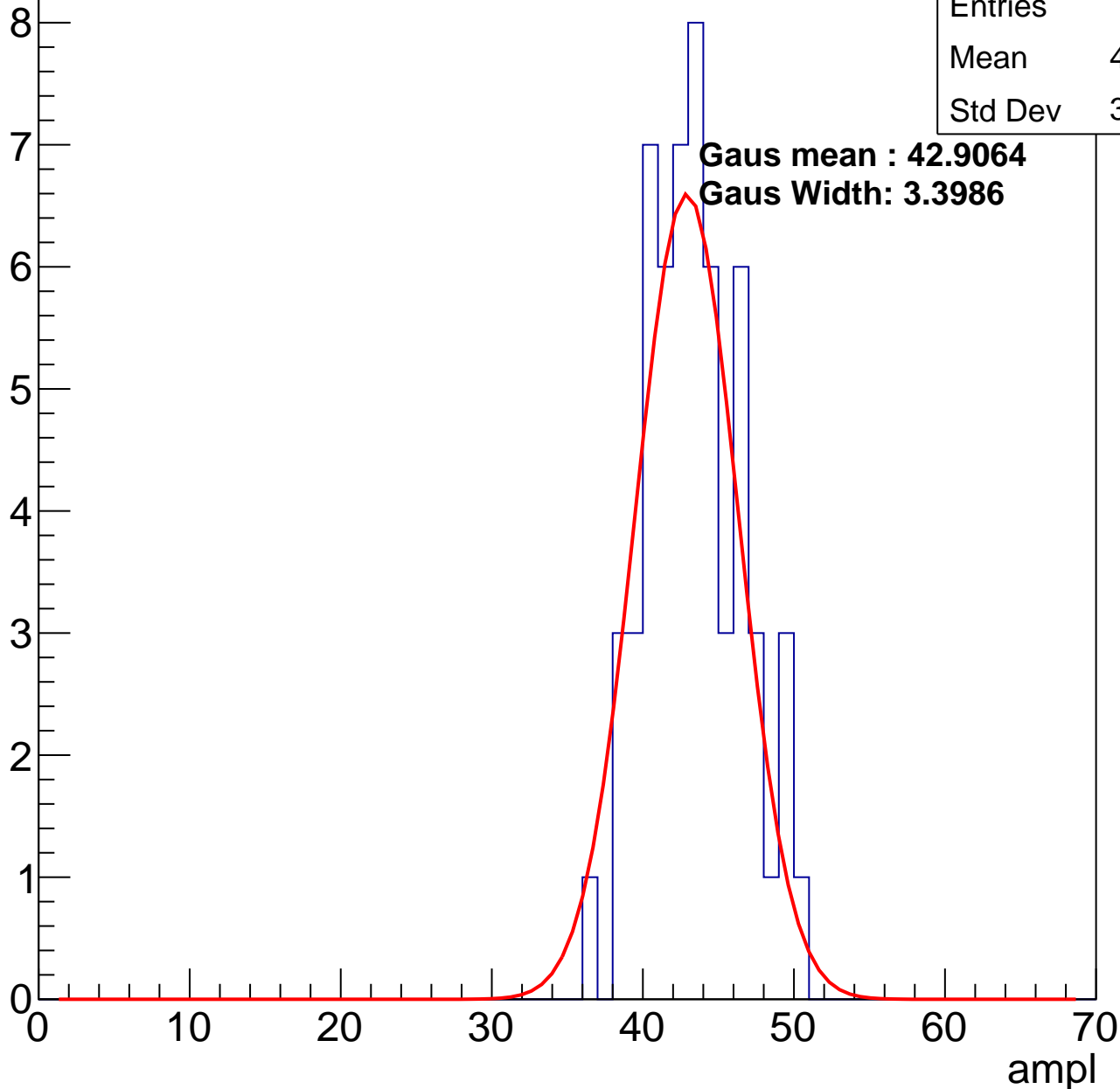
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.97
Std Dev	3.129

Gaus mean : 42.9064

Gaus Width: 3.3986

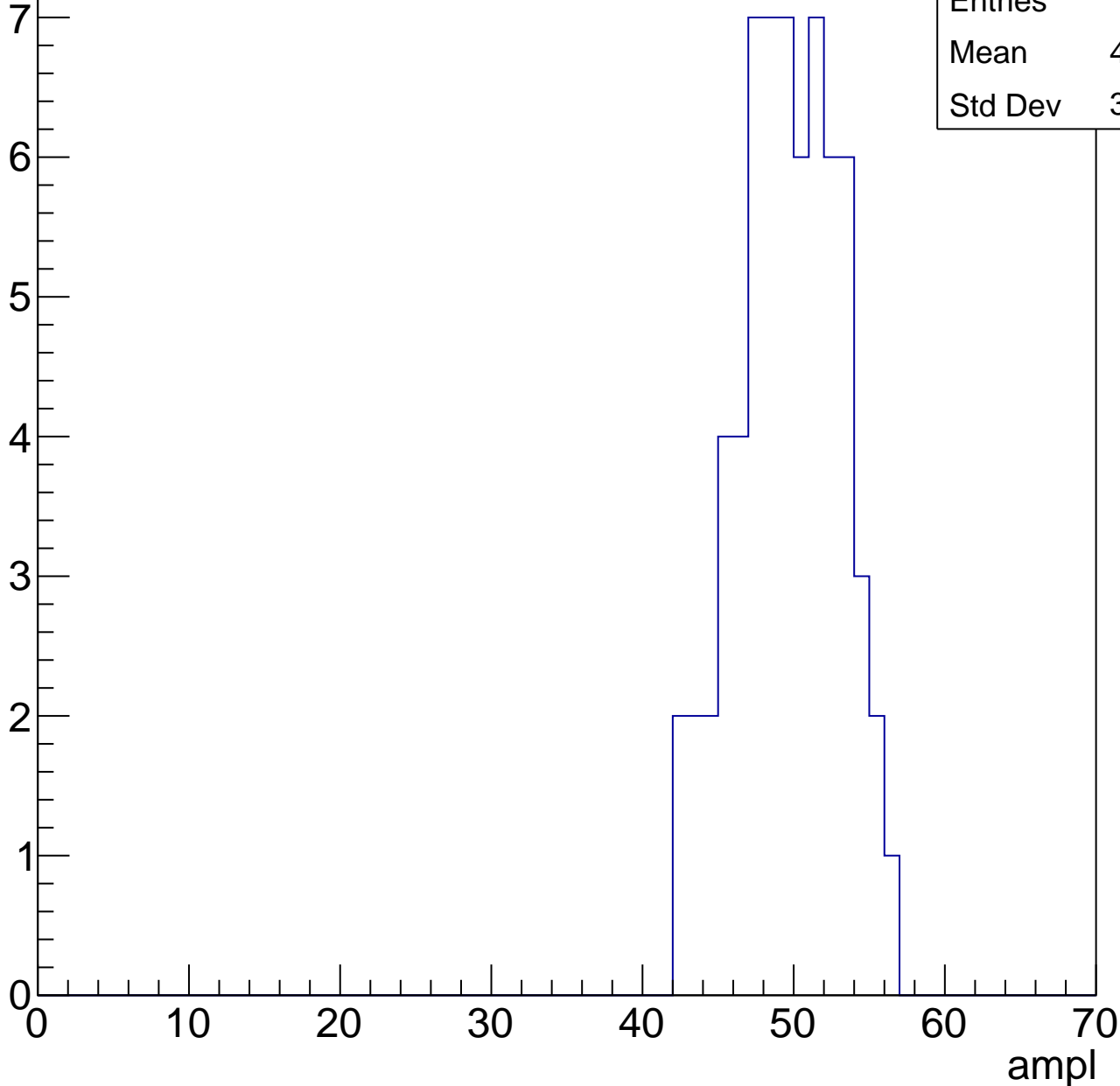


B0L002S, U17-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	49.17
Std Dev	3.333

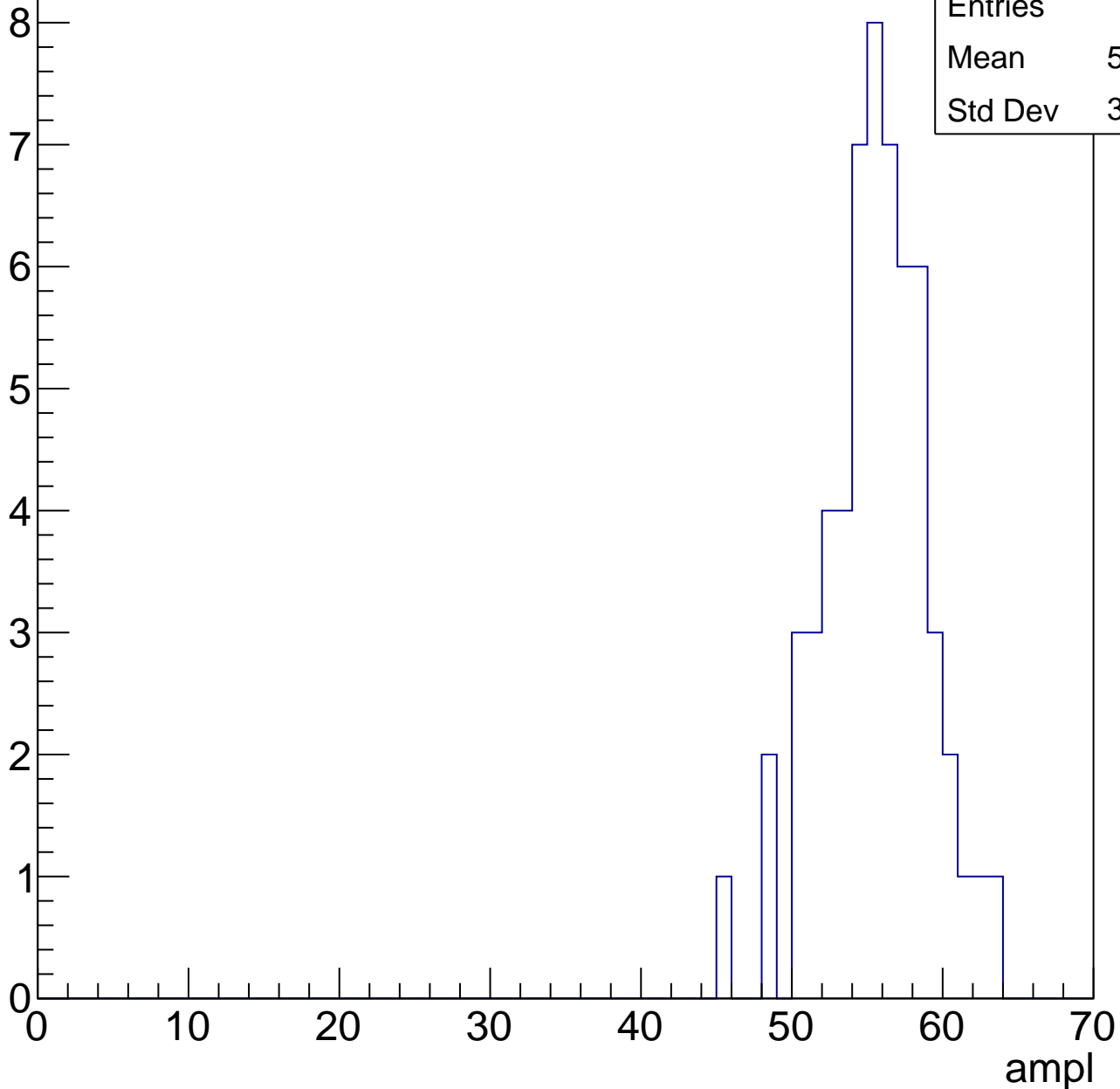


B0L002S, U17-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	55.03
Std Dev	3.474



B0L002S, U17-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.94
Std Dev	8.826

ampl

0

10

20

30

40

50

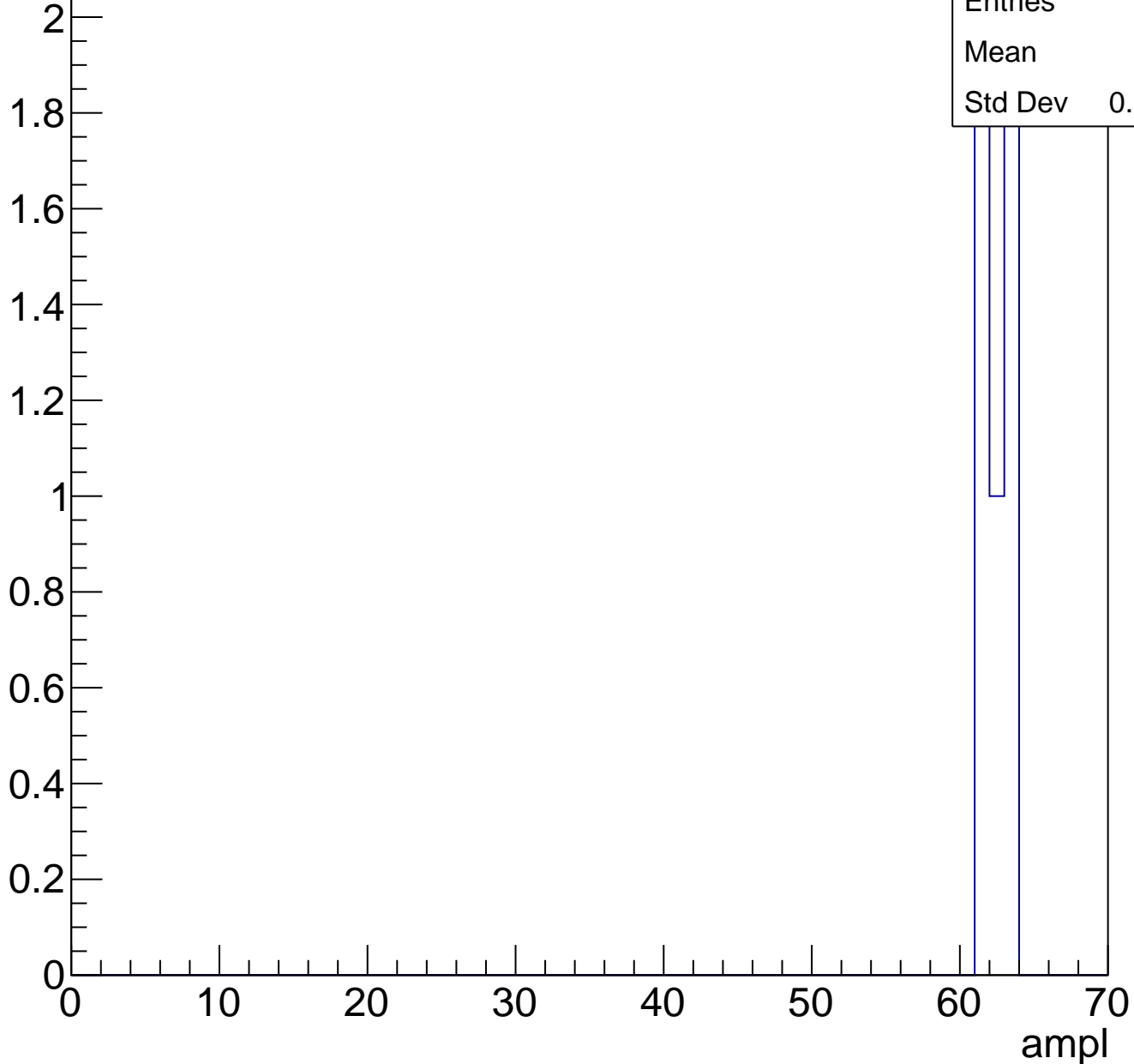
60

70

B0L002S, U17-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch58, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	30.01
Std Dev	4.71

Gaus mean : 30.7402

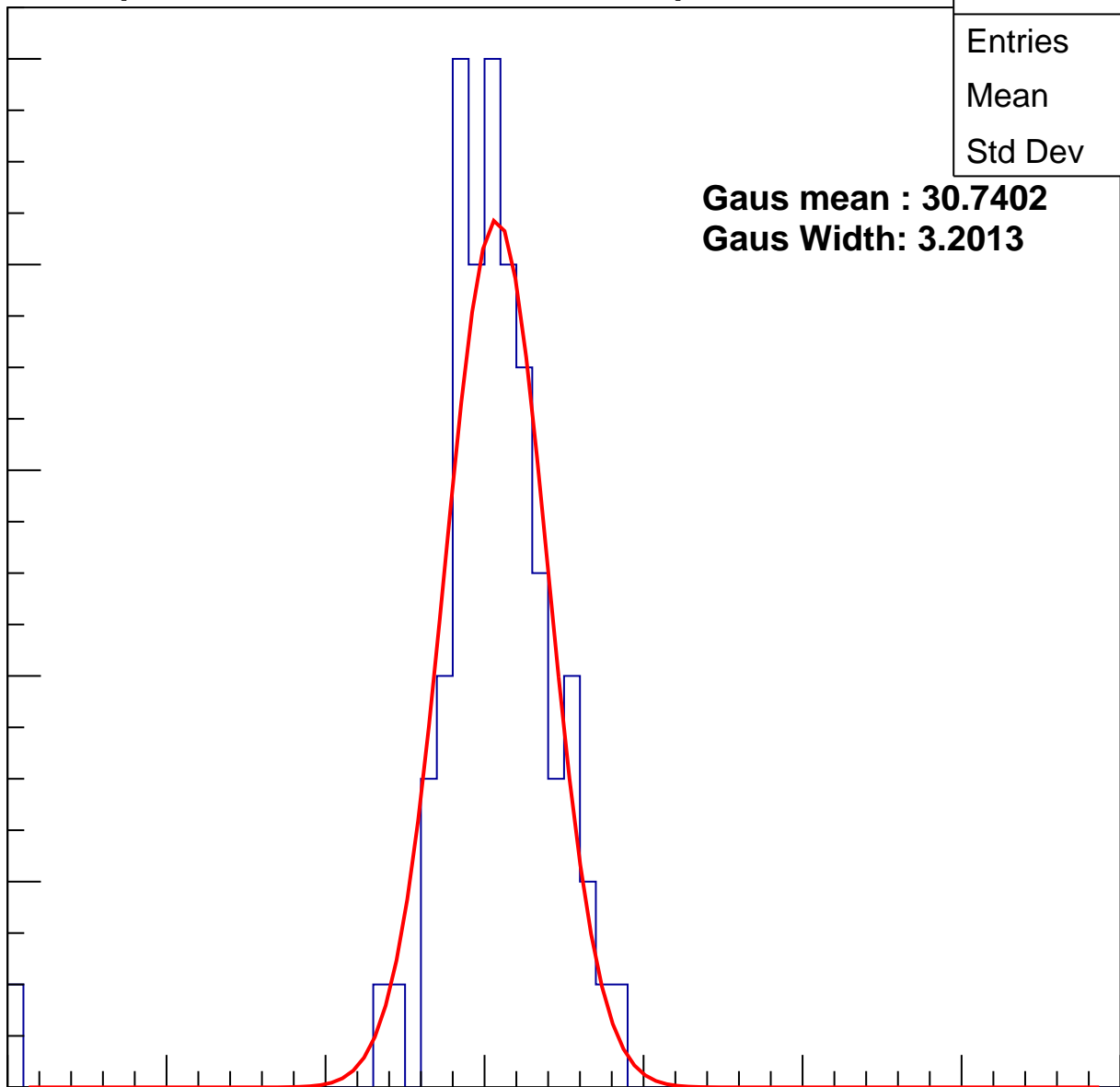
Gaus Width: 3.2013

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch58, adc1

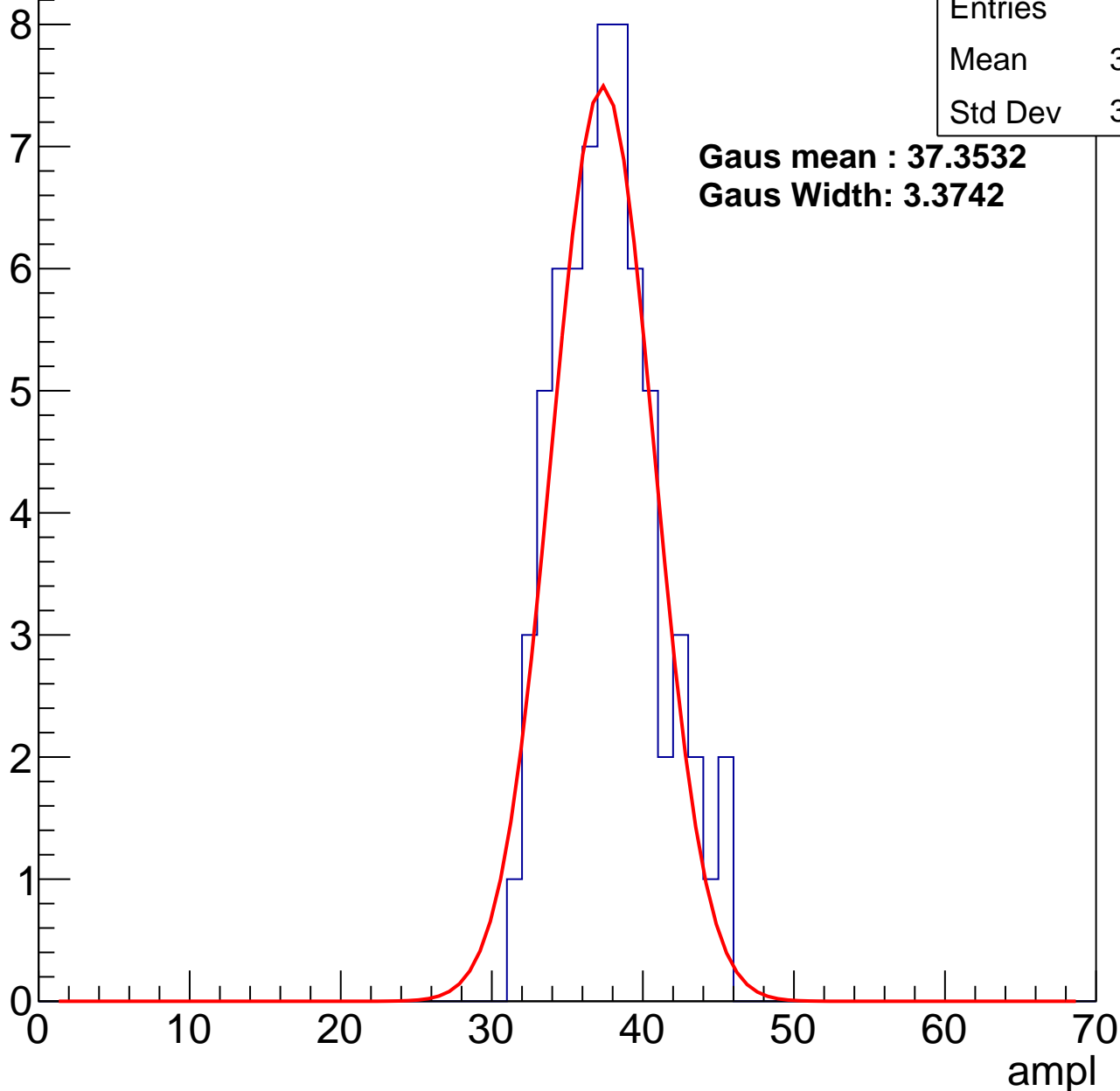
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.23
Std Dev	3.295

Gaus mean : 37.3532

Gaus Width: 3.3742



B0L002S, U17-ch58, adc2

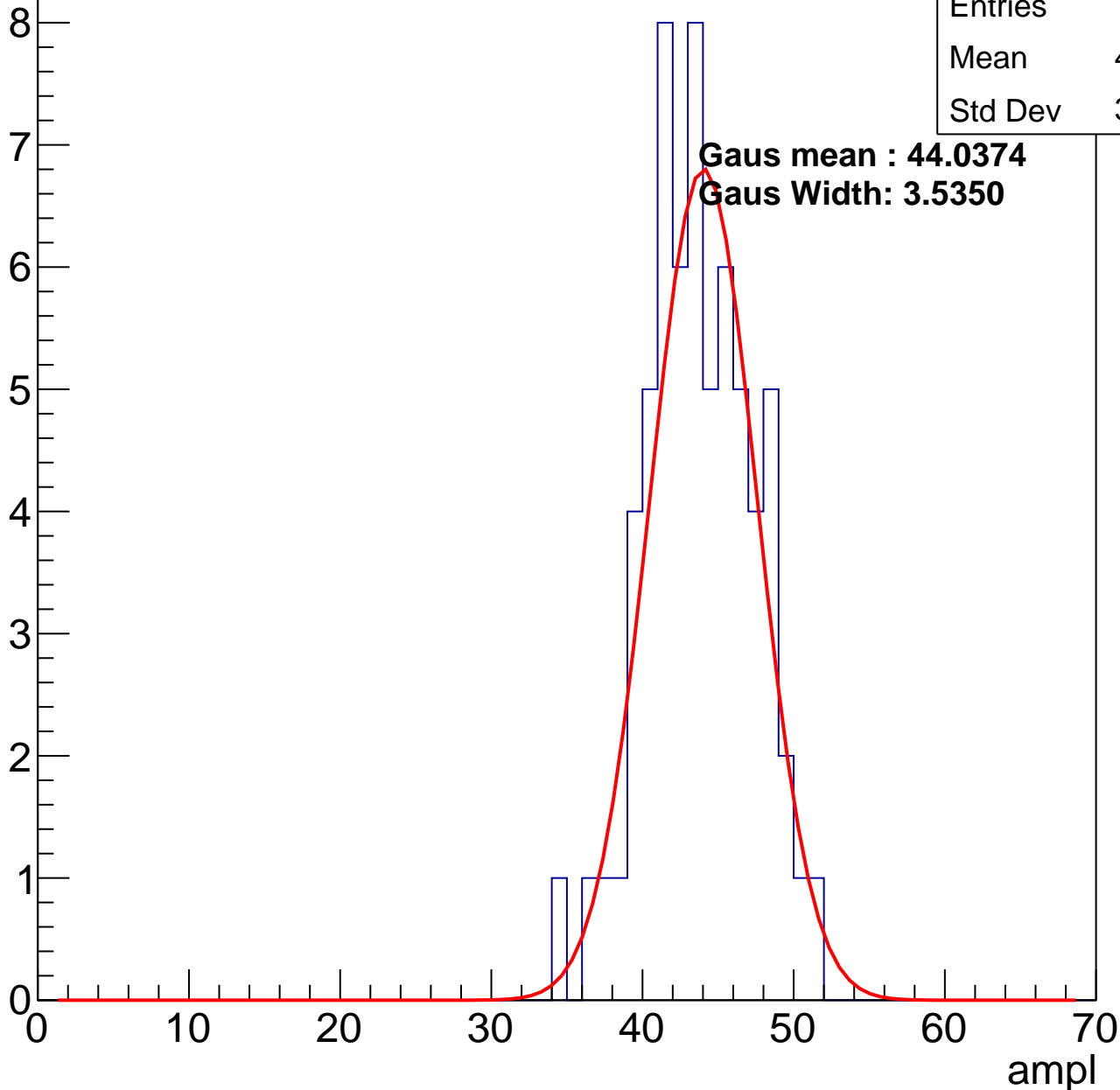
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.31
Std Dev	3.491

Gaus mean : 44.0374

Gaus Width: 3.5350

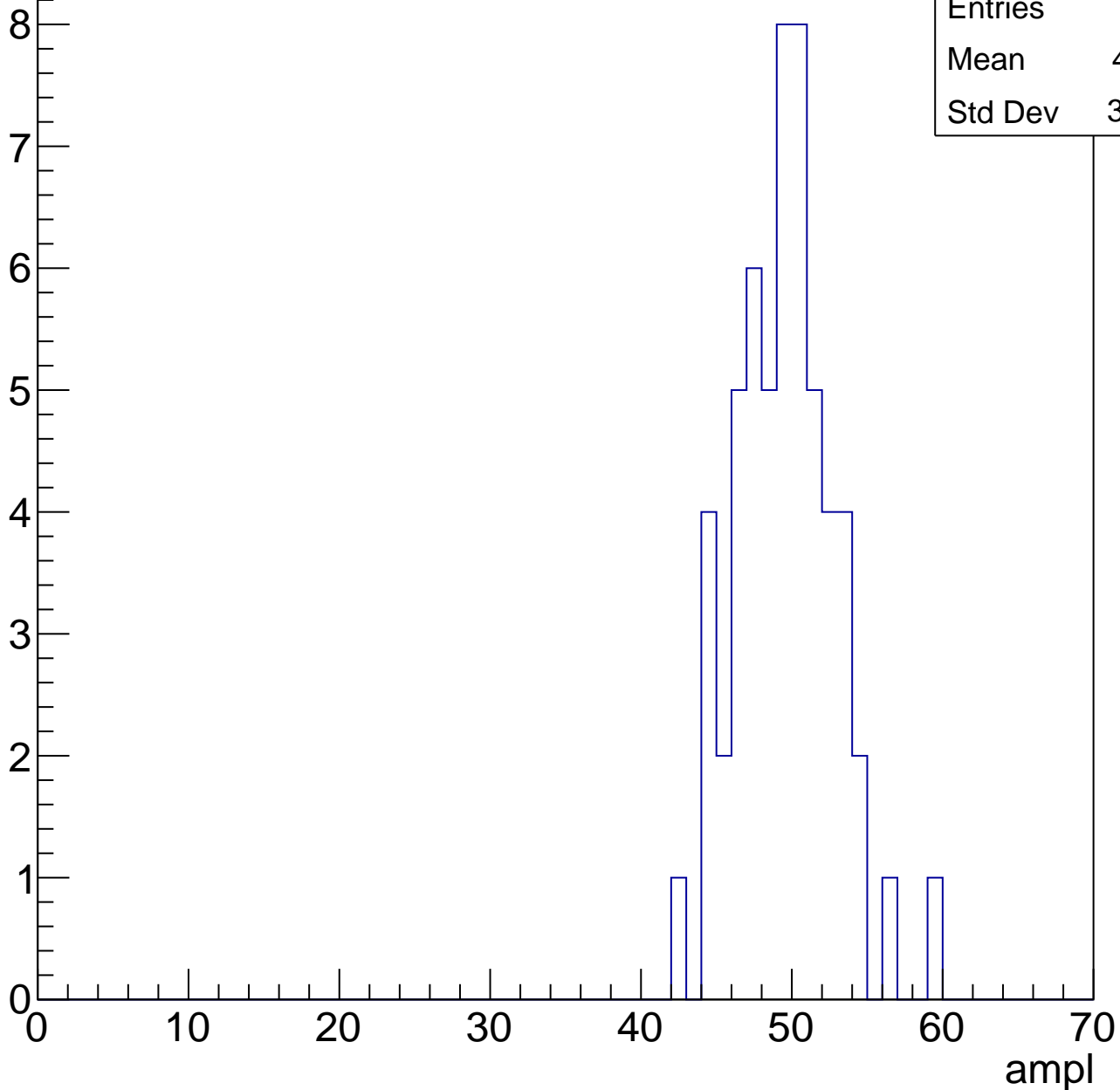


B0L002S, U17-ch58, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	49.11
Std Dev	3.222

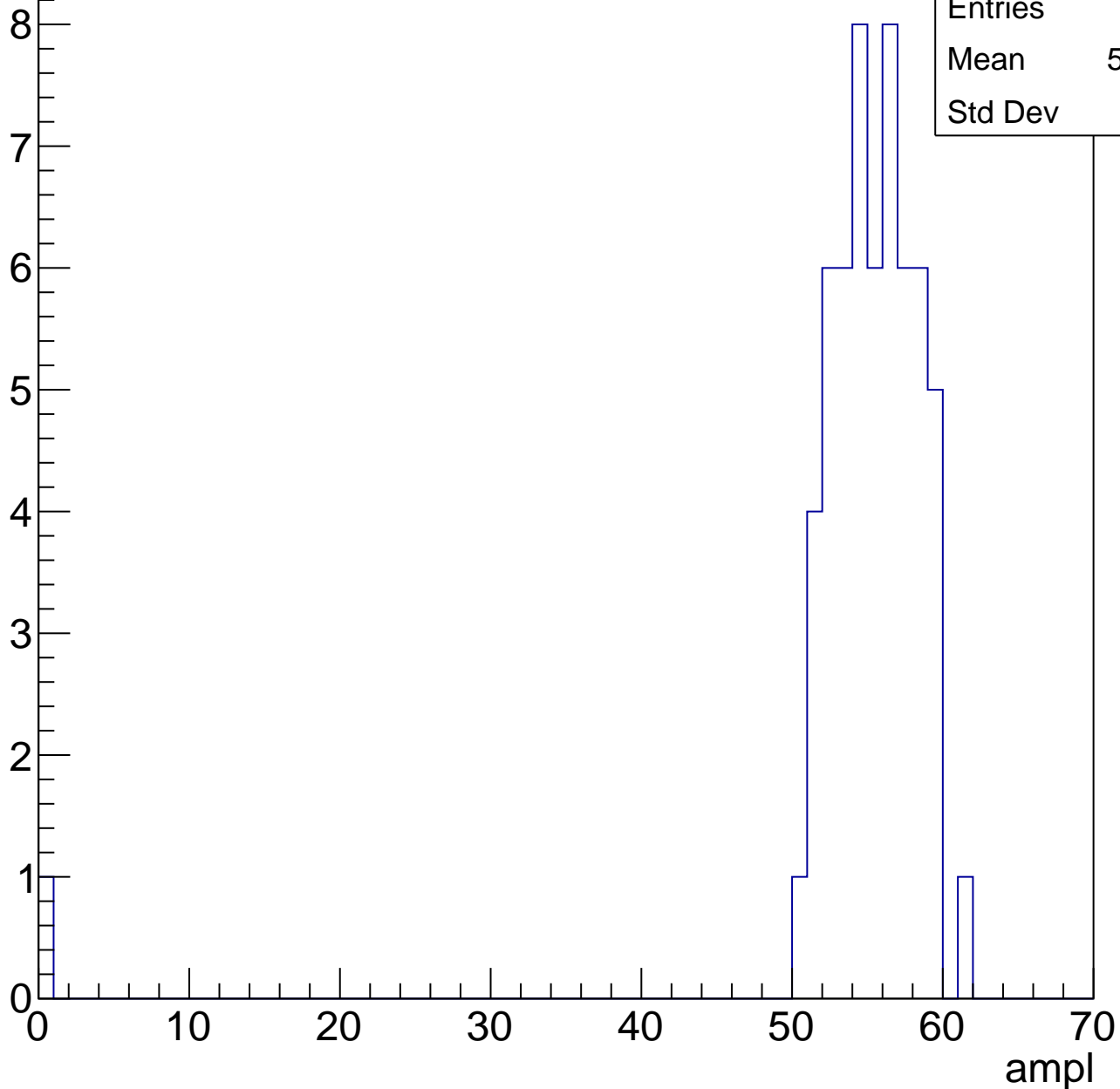


B0L002S, U17-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

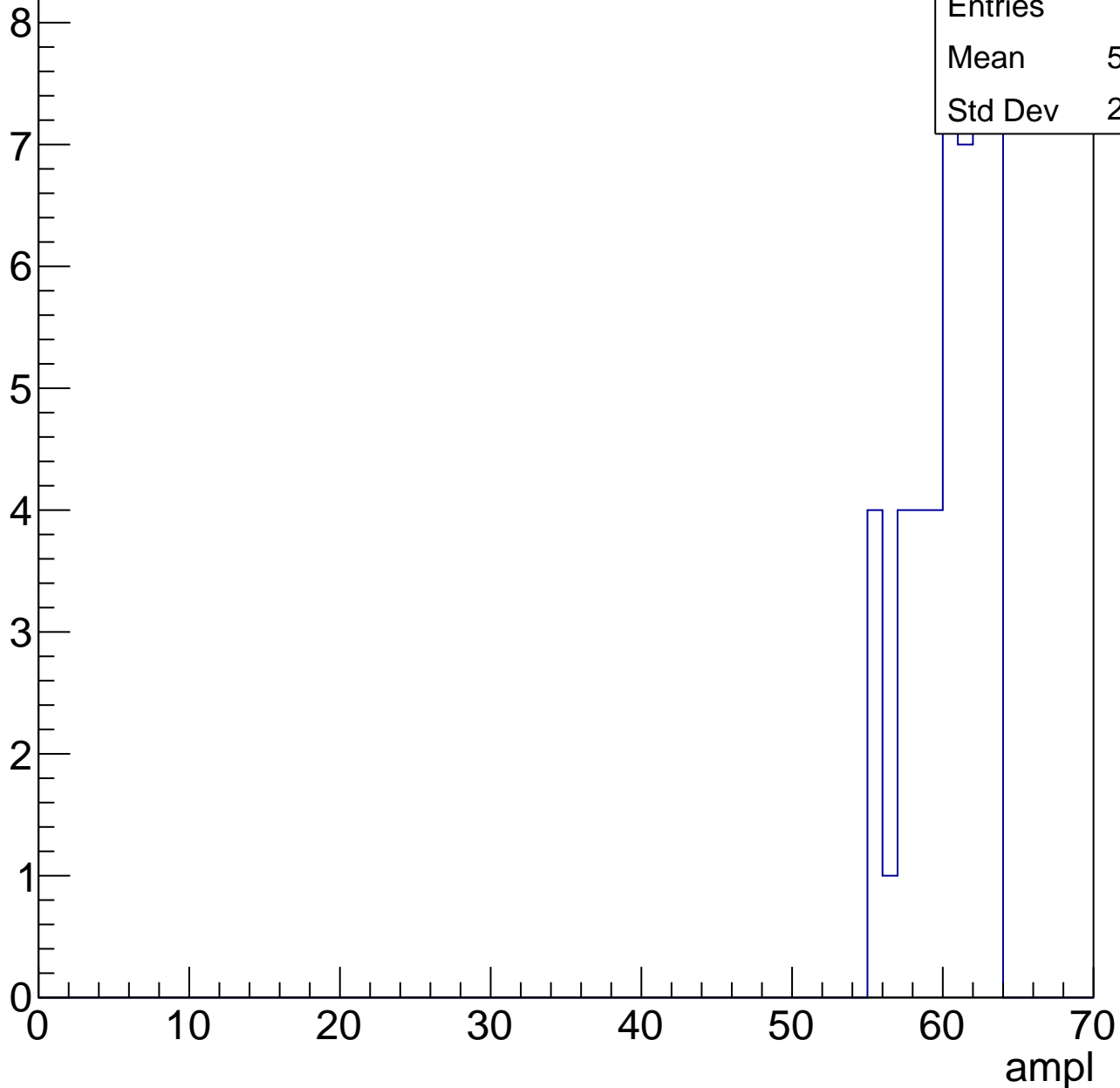
Entries	58
Mean	54.14
Std Dev	7.61



B0L002S, U17-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

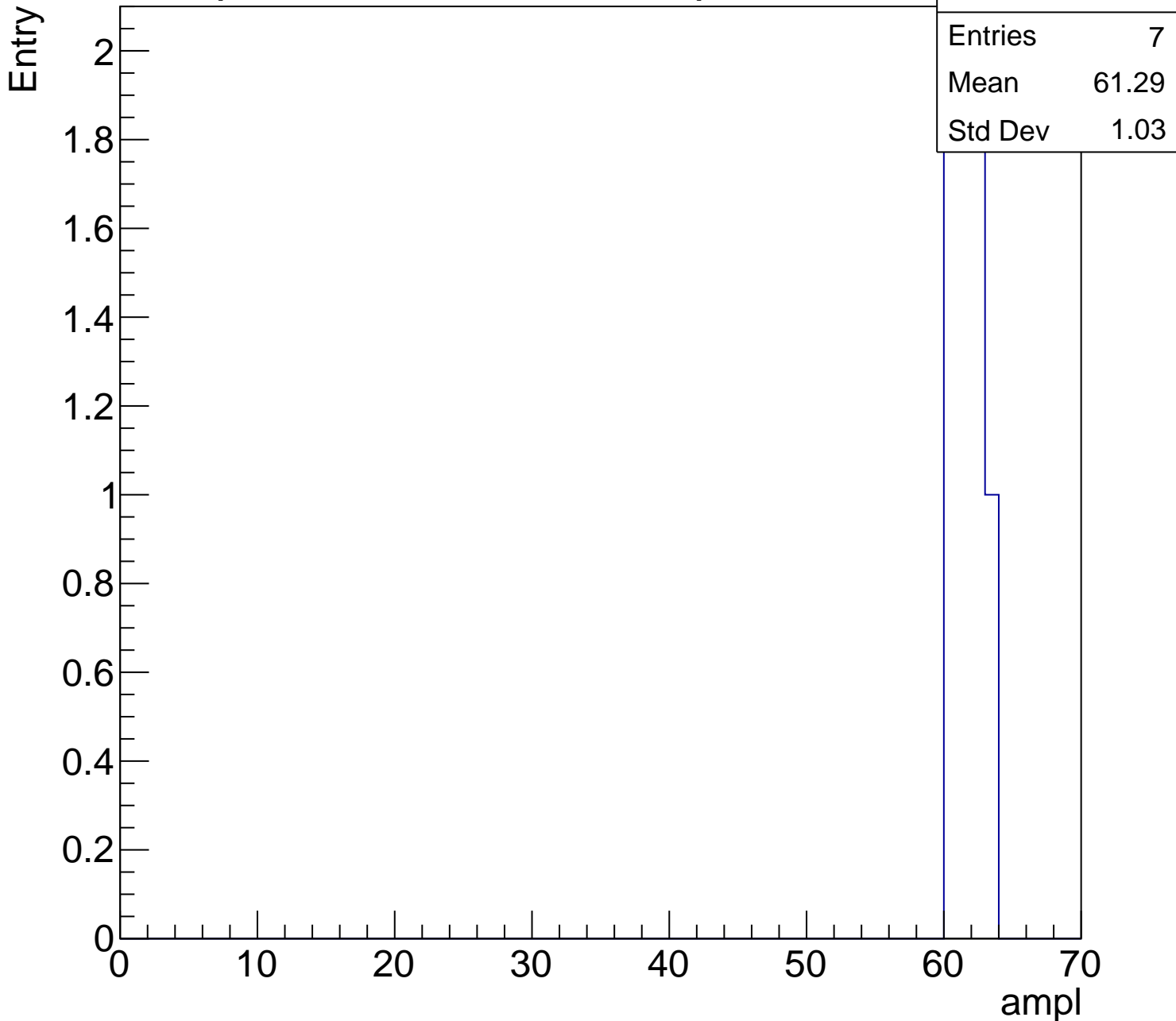
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.29
Std Dev	1.03

0 10 20 30 40 50 60 70

ampl



B0L002S, U17-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch59, adc0

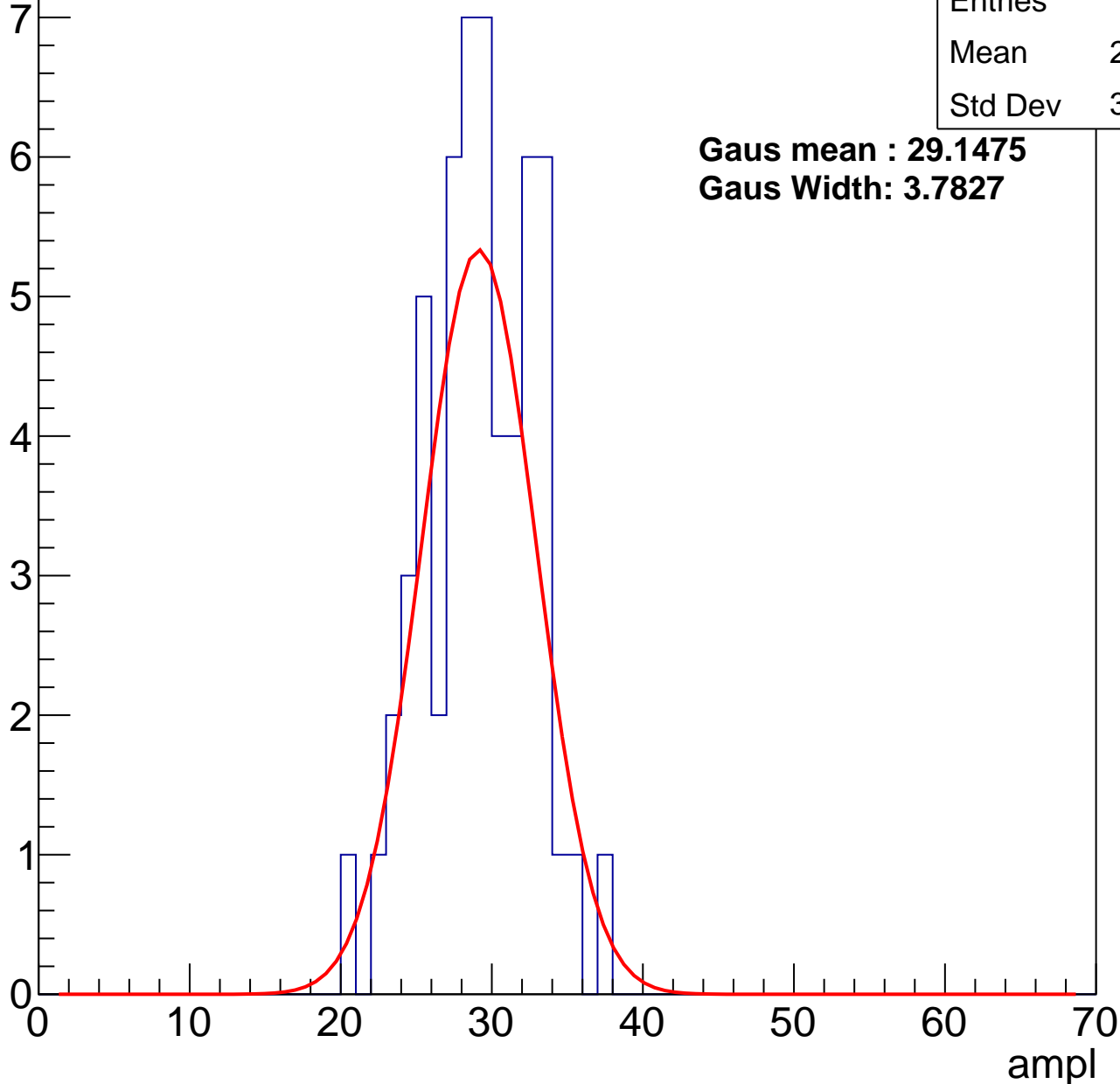
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	28.74
Std Dev	3.497

Gaus mean : 29.1475

Gaus Width: 3.7827



B0L002S, U17-ch59, adc1

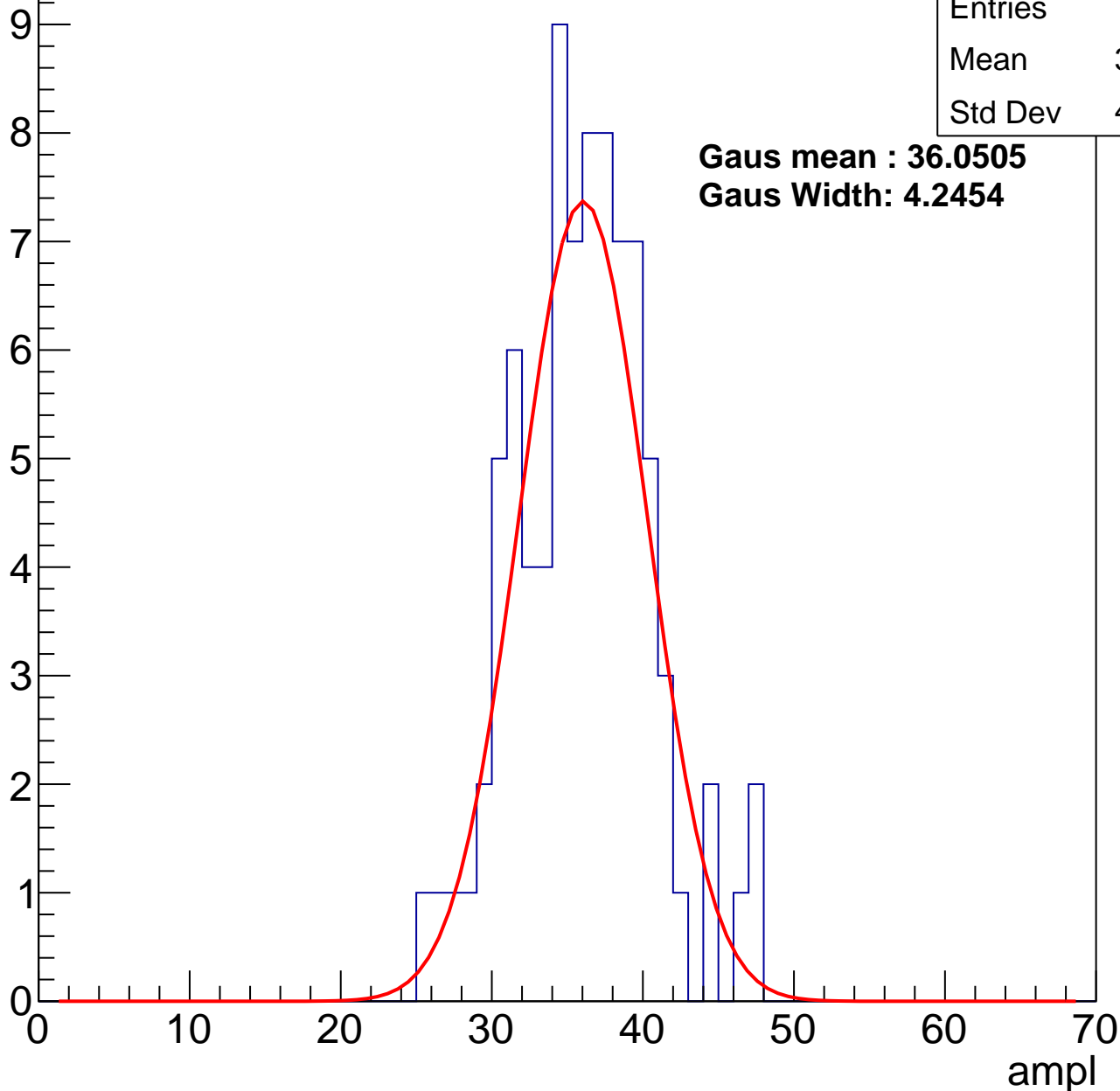
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	35.61
Std Dev	4.451

Gaus mean : 36.0505

Gaus Width: 4.2454



B0L002S, U17-ch59, adc2

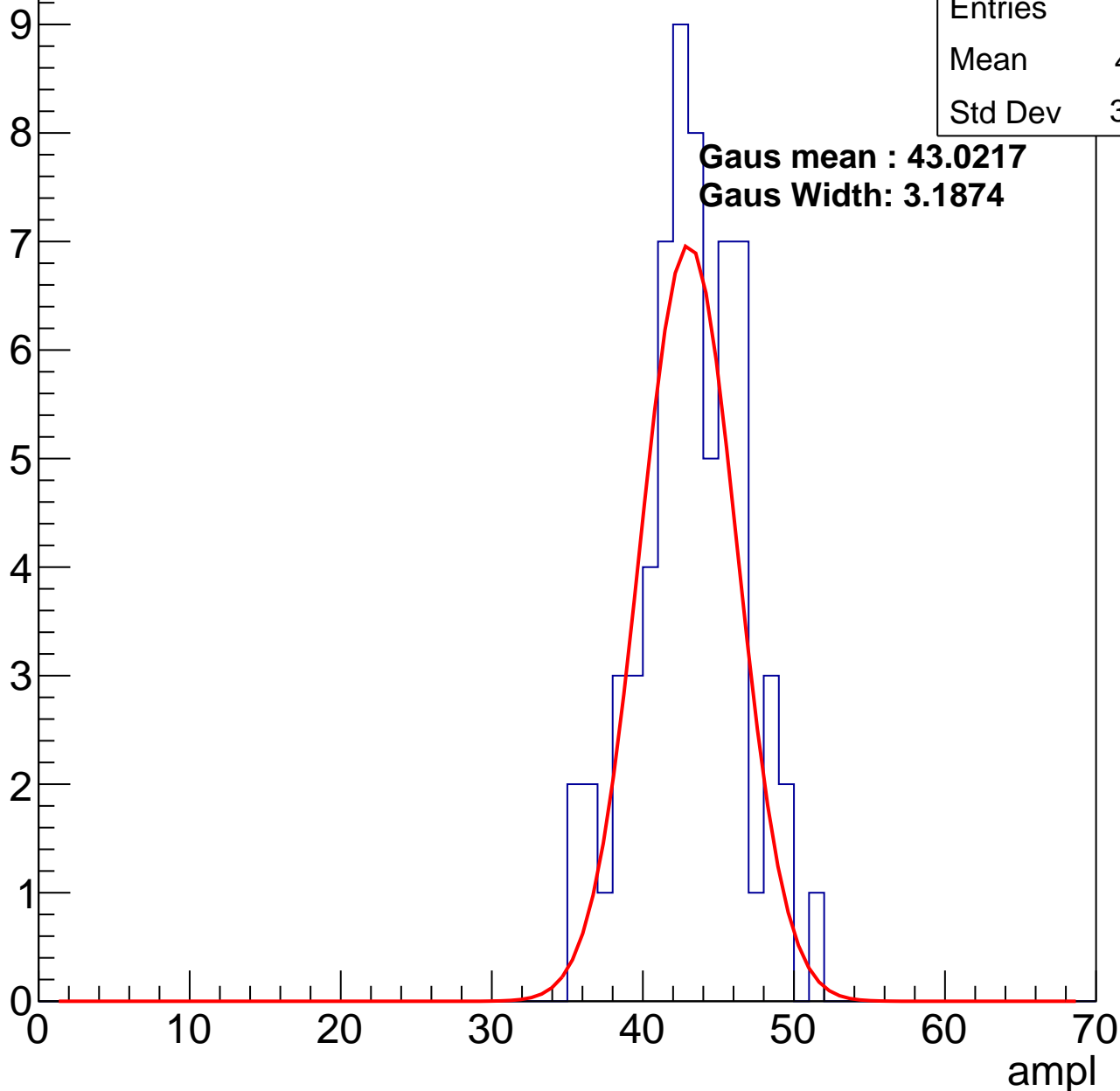
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	42.71
Std Dev	3.454

Gaus mean : 43.0217

Gaus Width: 3.1874

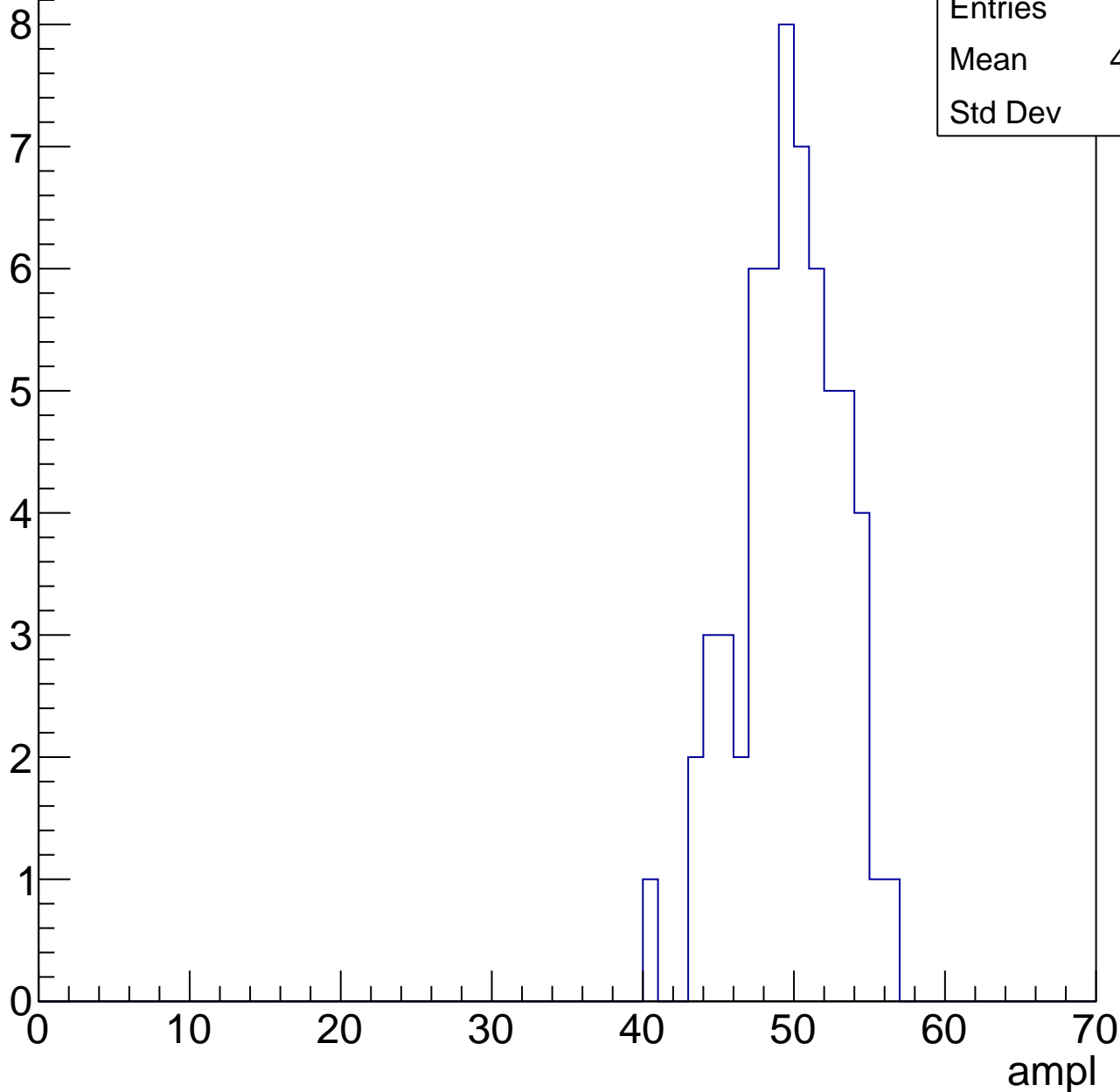


B0L002S, U17-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	49.25
Std Dev	3.32

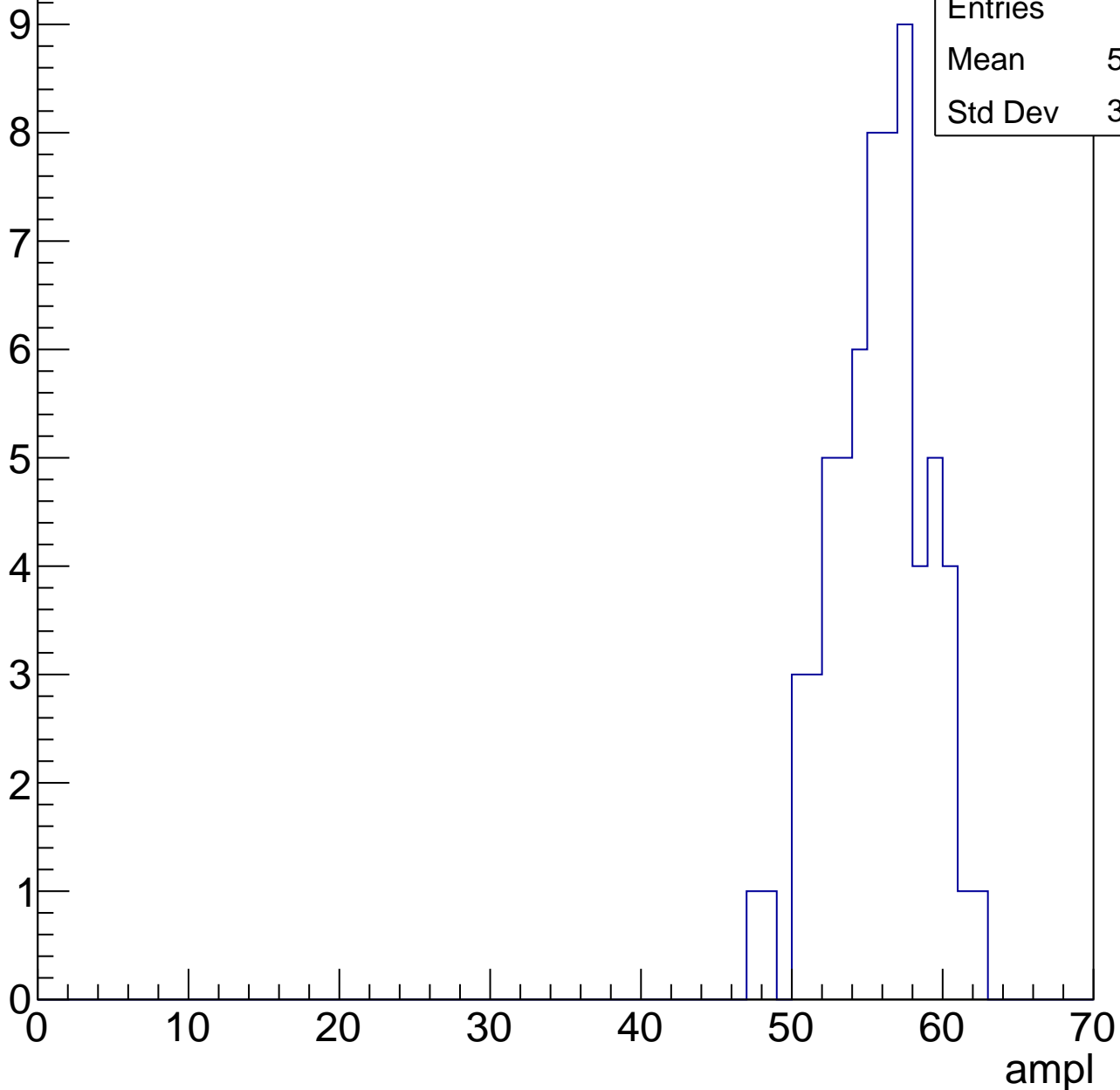


B0L002S, U17-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	55.28
Std Dev	3.179

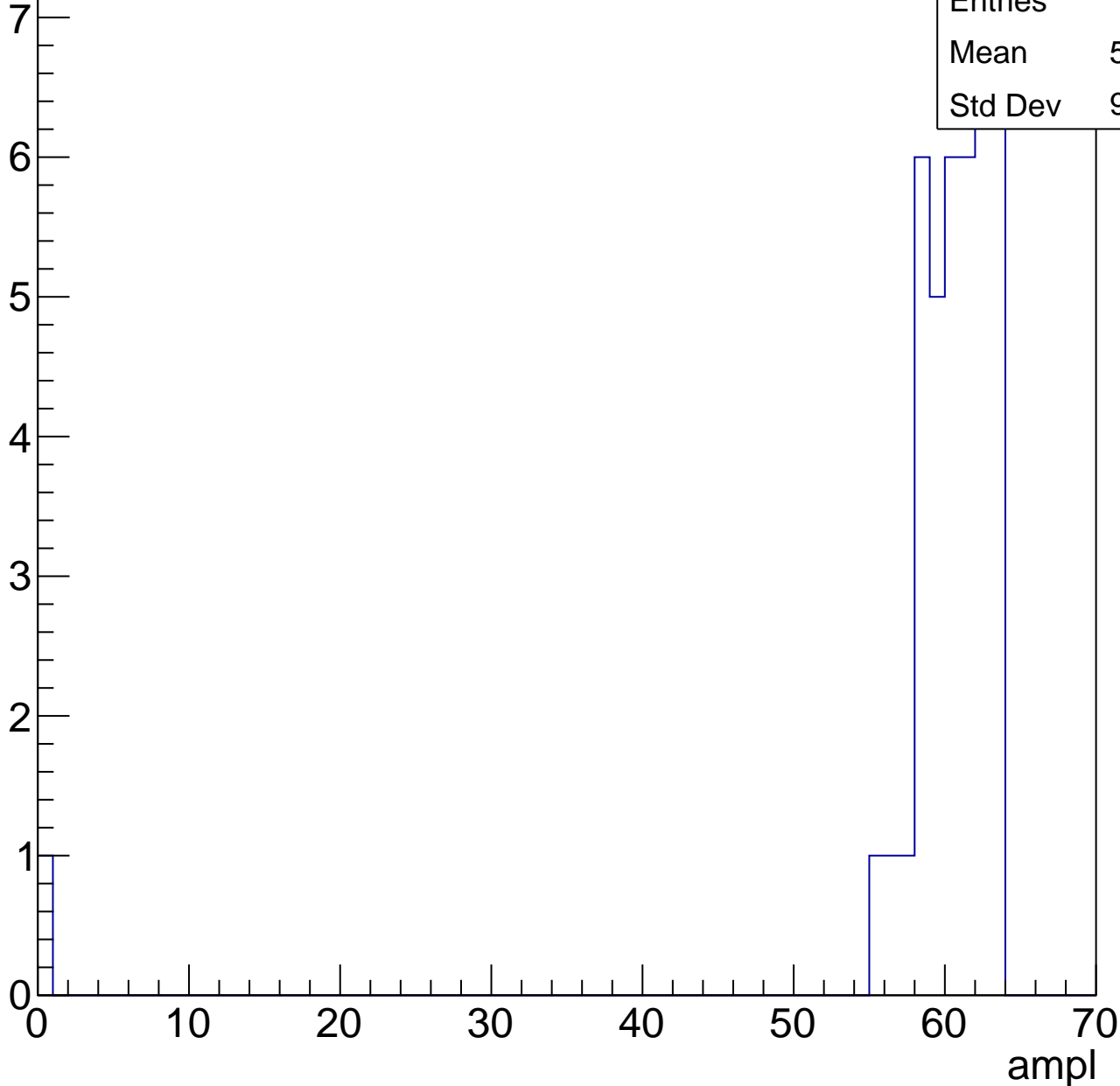


B0L002S, U17-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

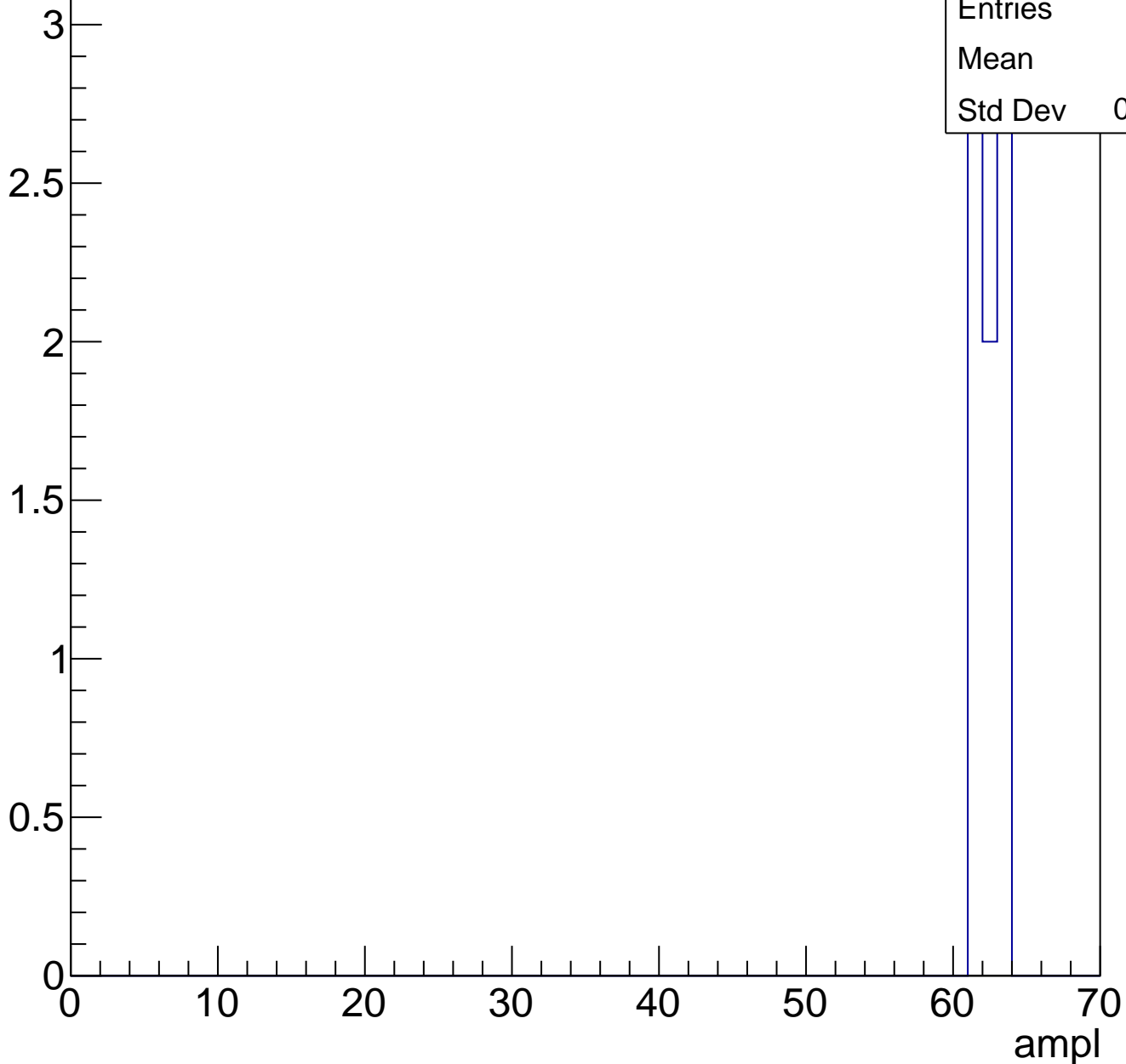
Entries	41
Mean	58.83
Std Dev	9.525



B0L002S, U17-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch60, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	28.99
Std Dev	2.957

Gaus mean : 29.6101

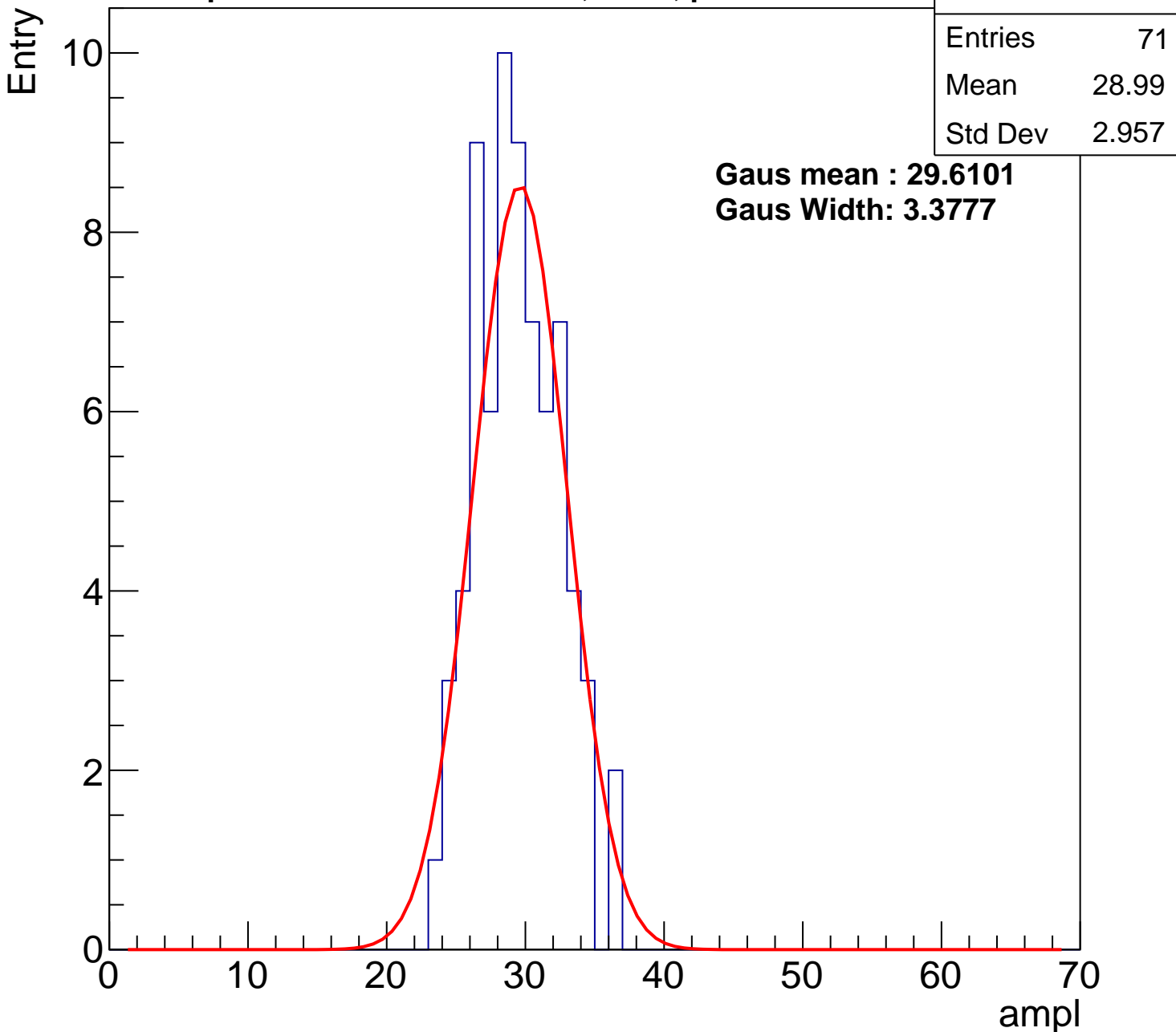
Gaus Width: 3.3777

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch60, adc1

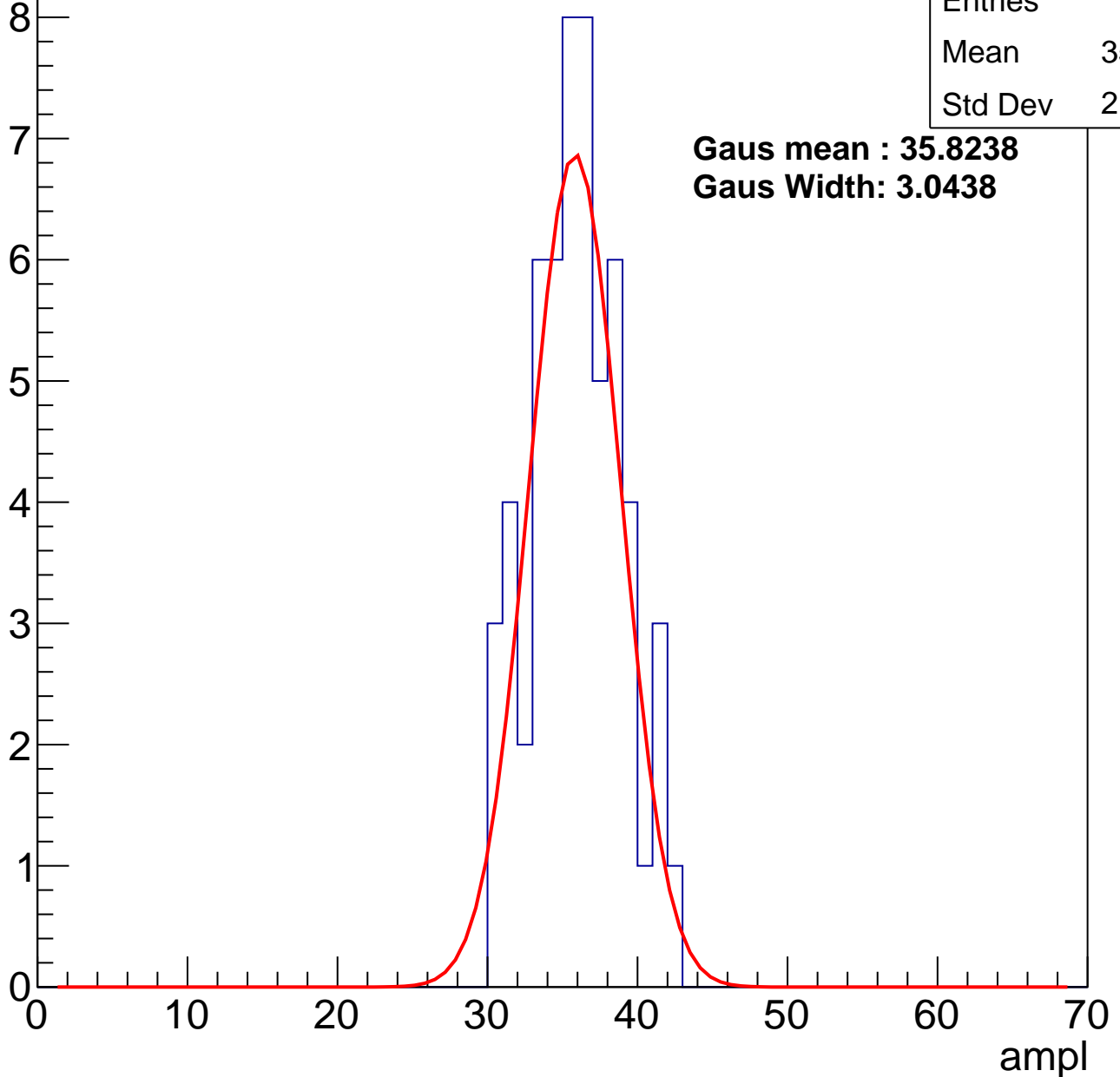
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	35.47
Std Dev	2.968

Gaus mean : 35.8238

Gaus Width: 3.0438



B0L002S, U17-ch60, adc2

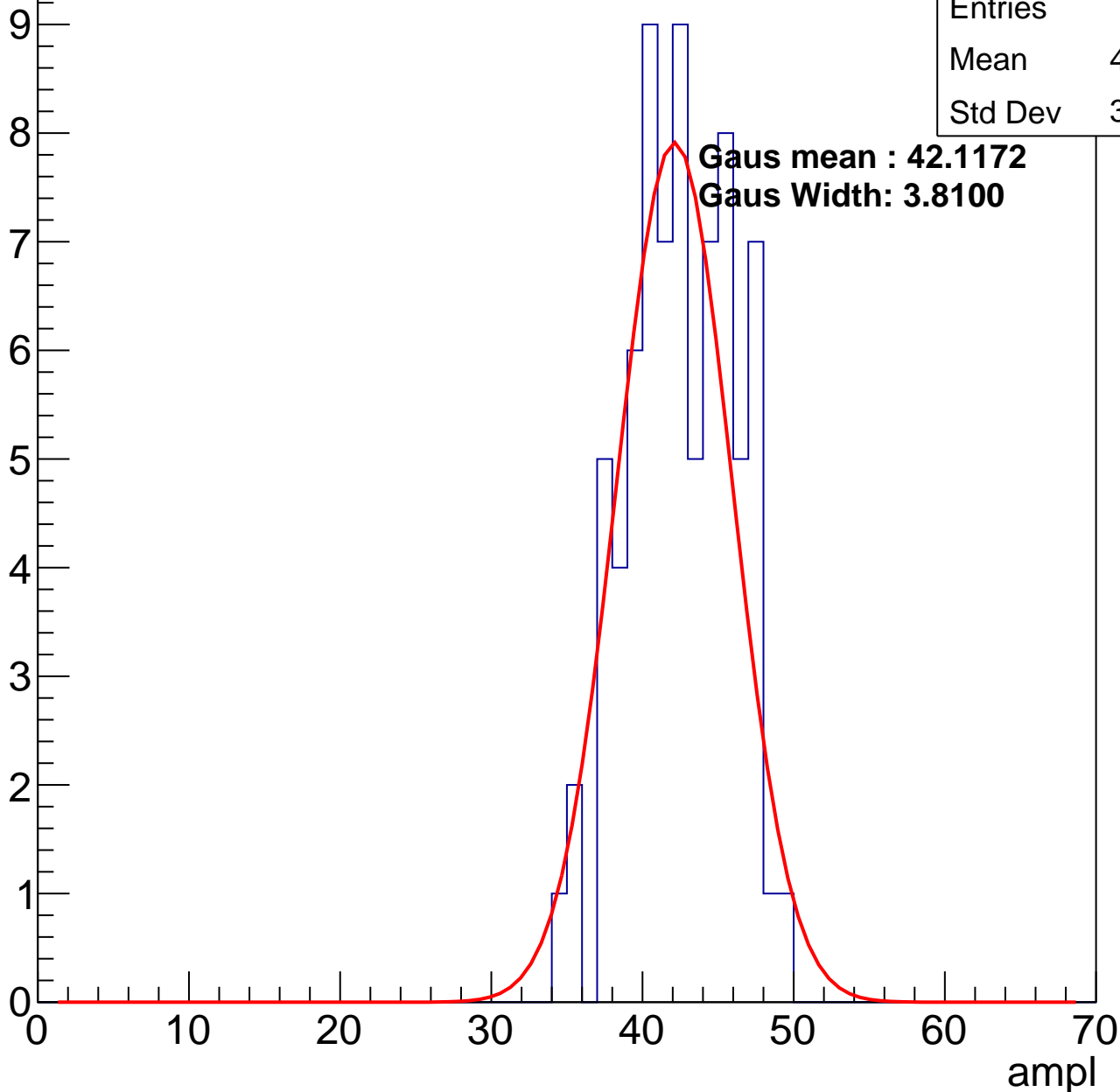
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	42.06
Std Dev	3.405

Gaus mean : 42.1172

Gaus Width: 3.8100



B0L002S, U17-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1

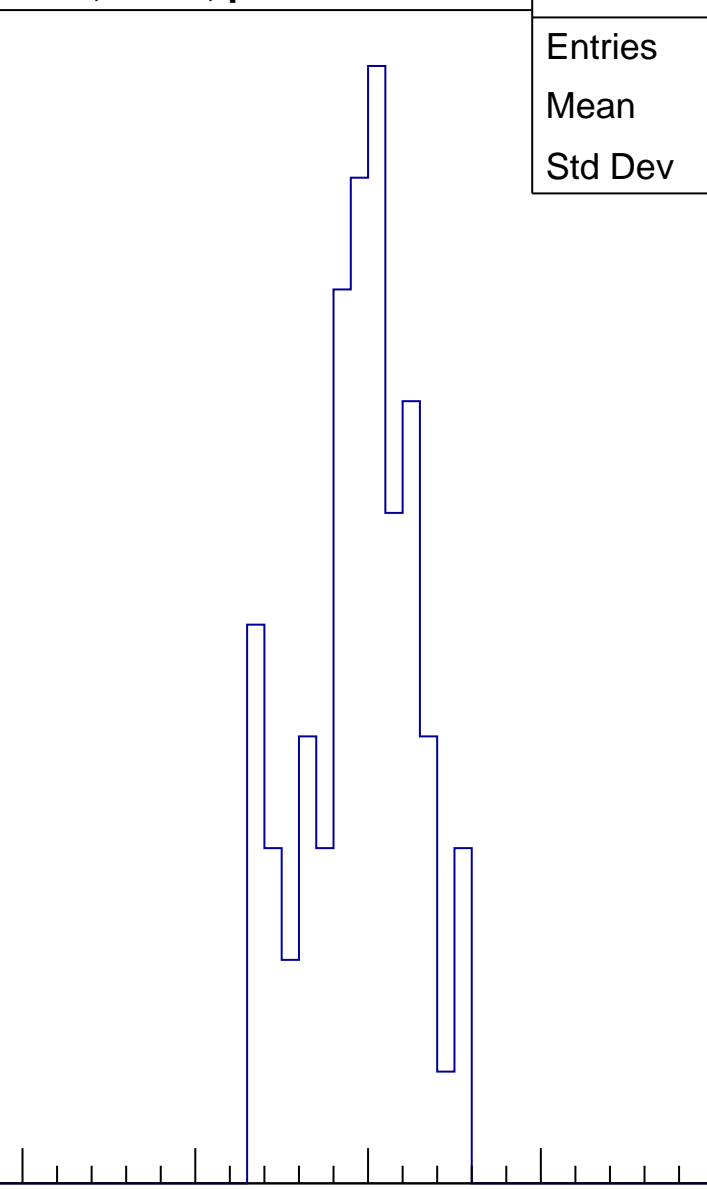
Entries	65
Mean	49.05
Std Dev	3.13

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

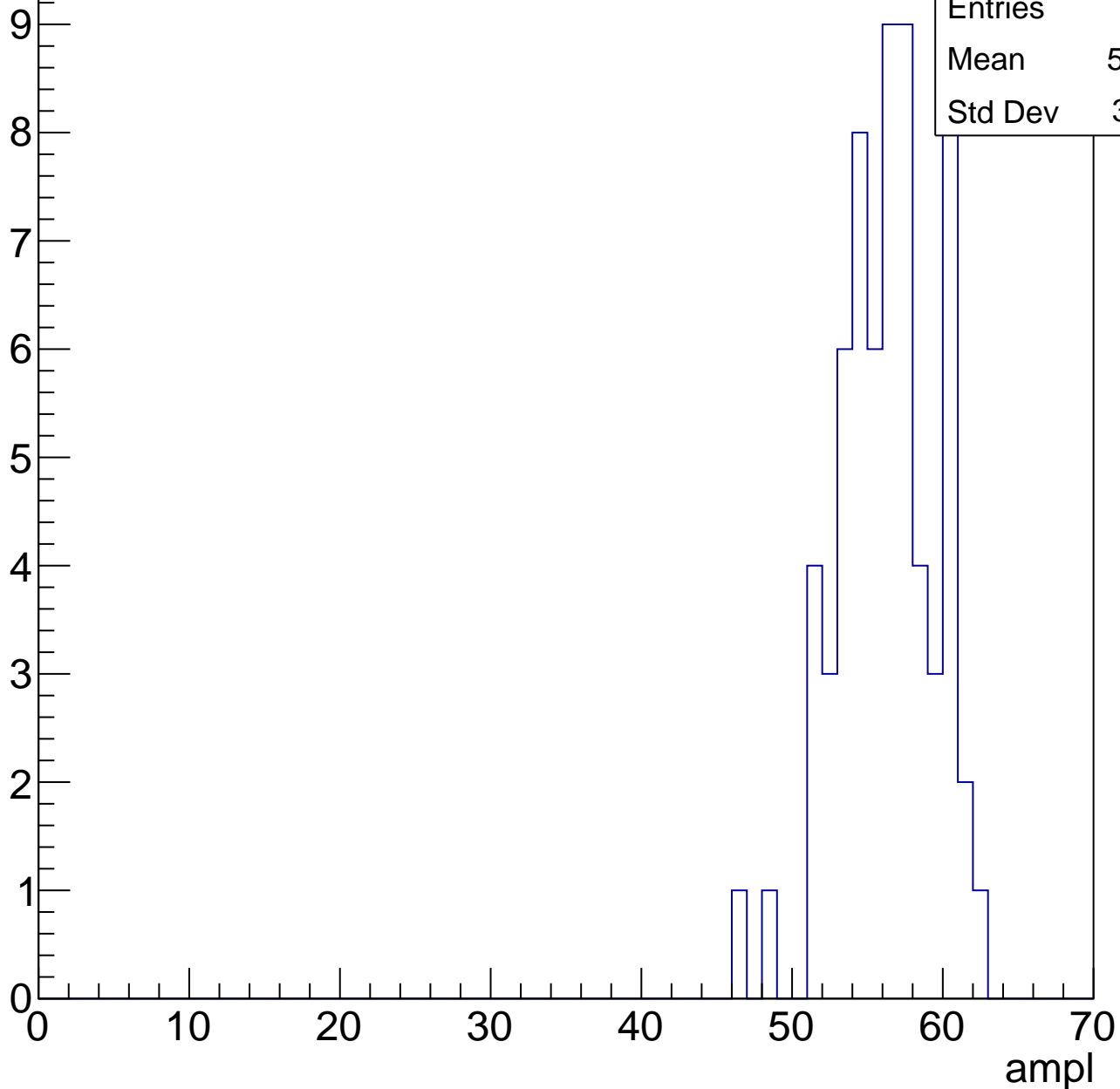
ampl



B0L002S, U17-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

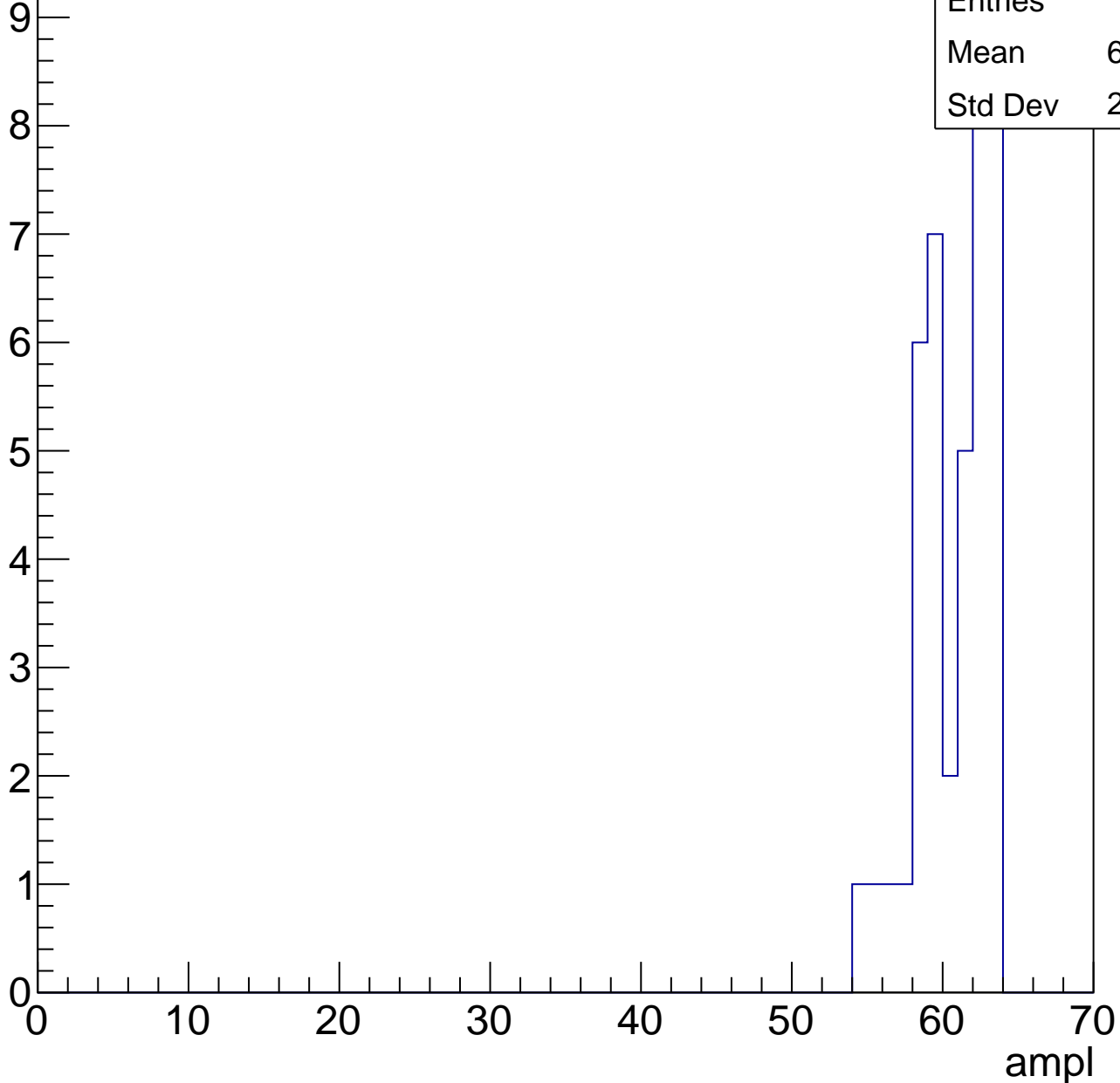


B0L002S, U17-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

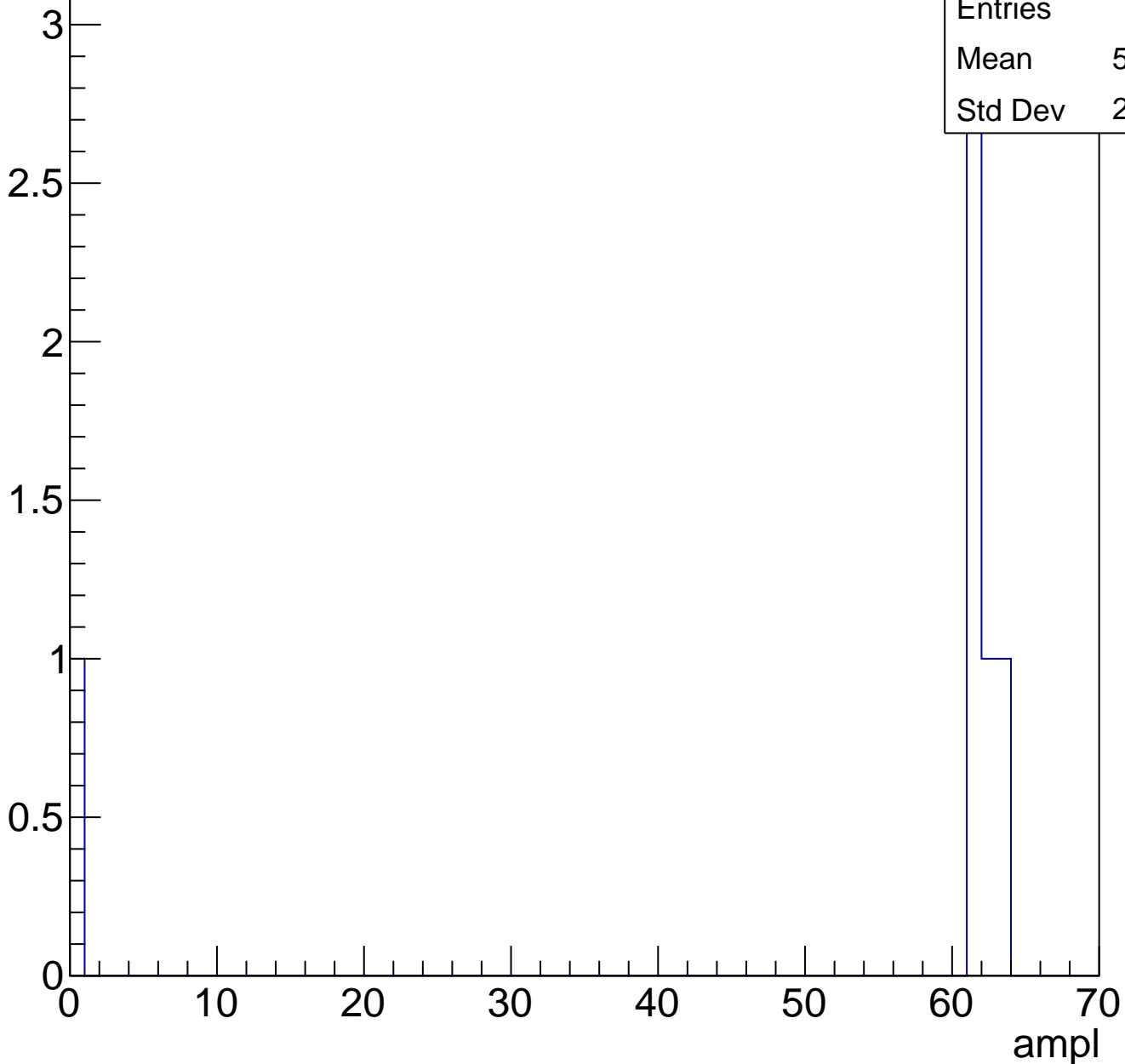
Entries	41
Mean	60.27
Std Dev	2.379



B0L002S, U17-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch61, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	98
Mean	29.86
Std Dev	5.188

Gaus mean : 31.2644

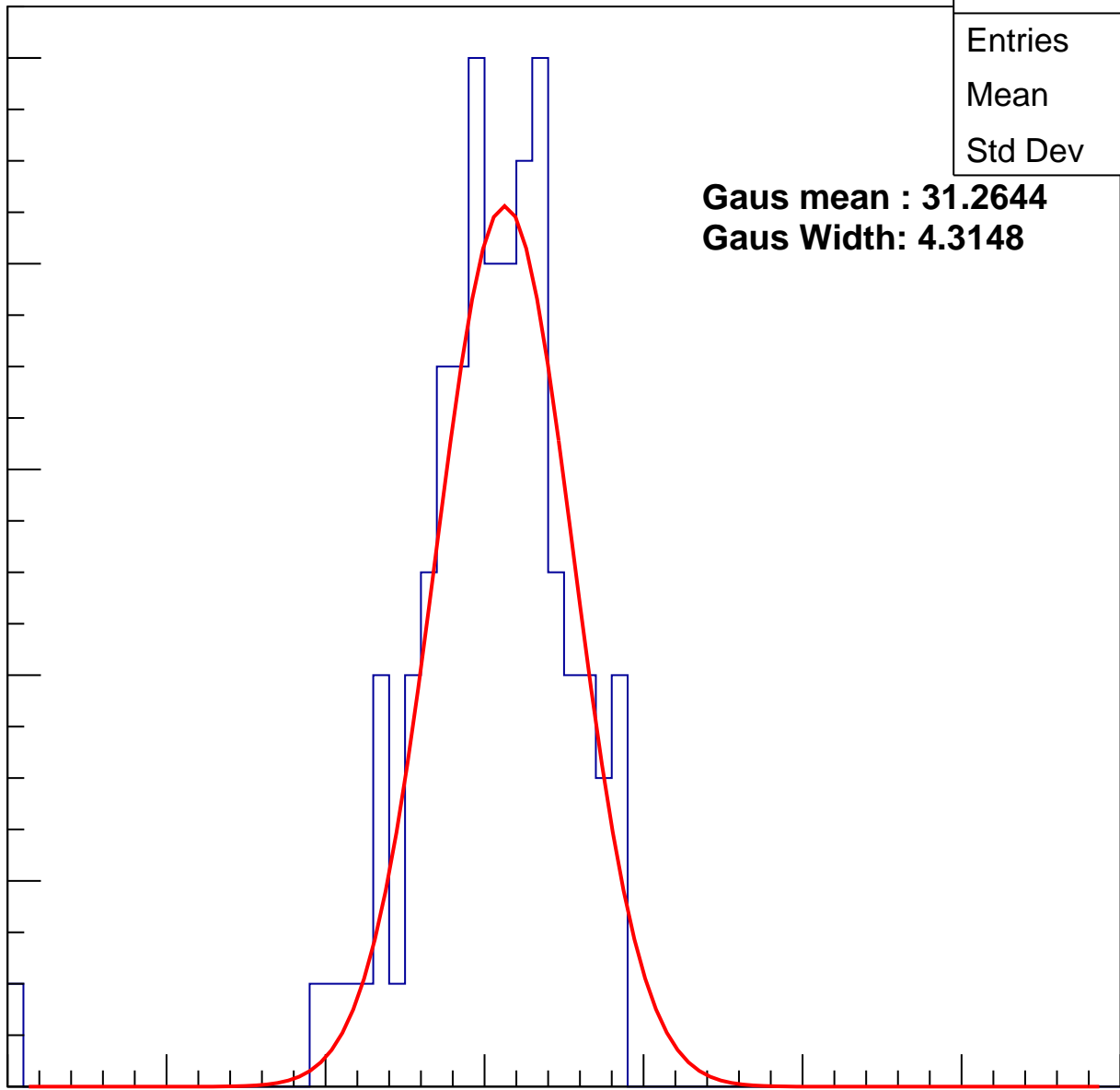
Gaus Width: 4.3148

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch61, adc1

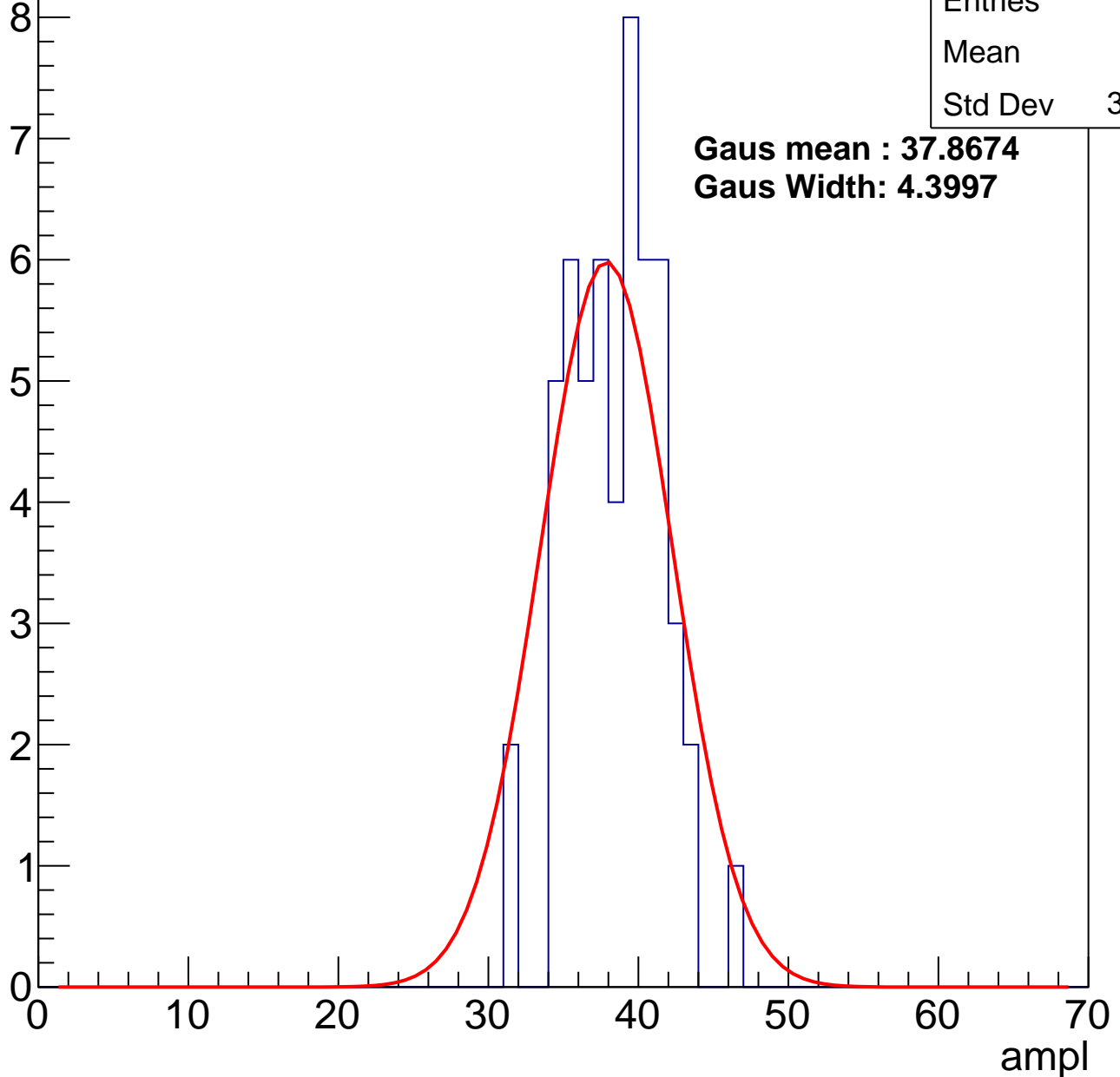
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	38
Std Dev	3.061

Gaus mean : 37.8674

Gaus Width: 4.3997



B0L002S, U17-ch61, adc2

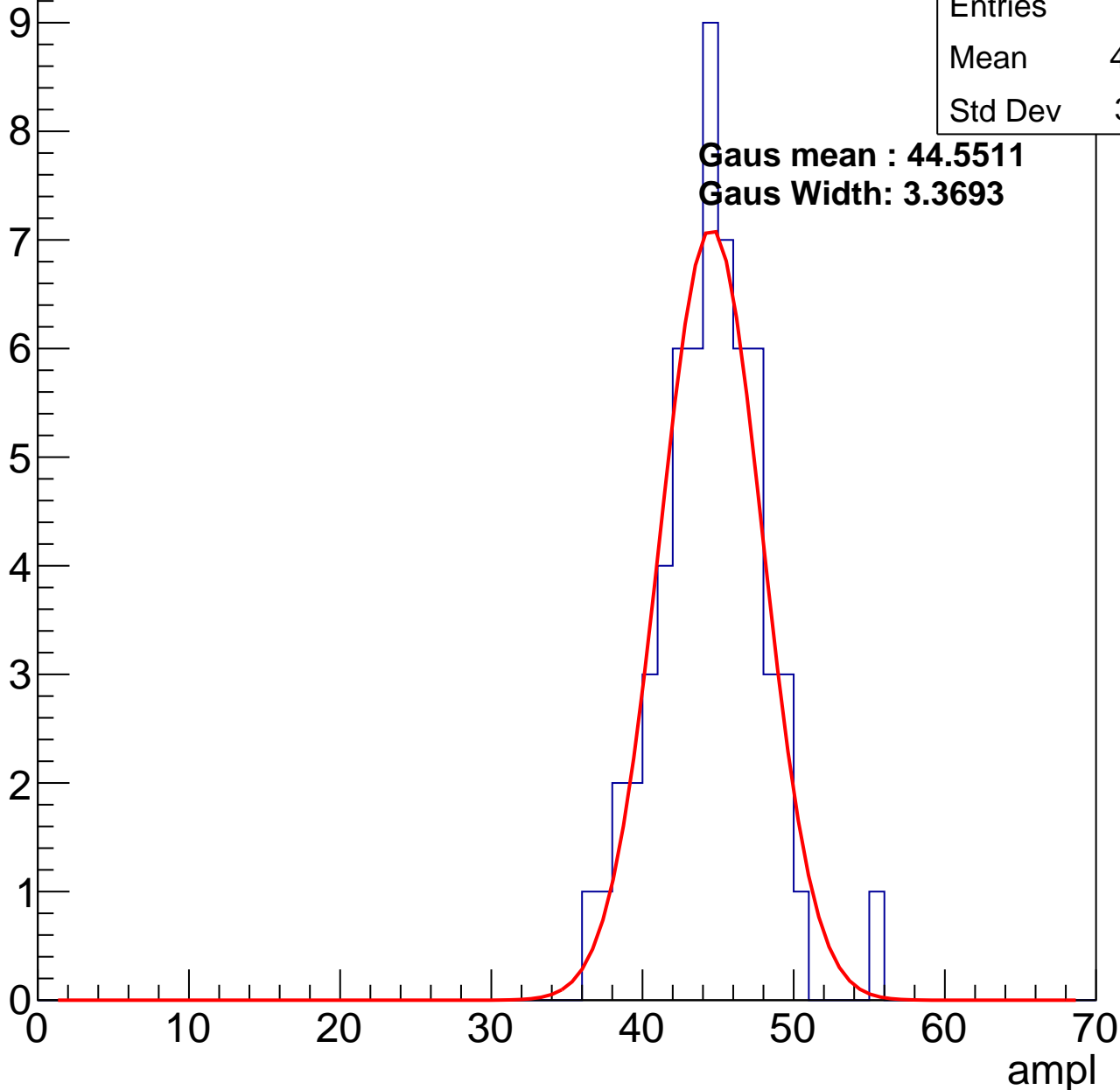
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.03
Std Dev	3.421

Gaus mean : 44.5511

Gaus Width: 3.3693

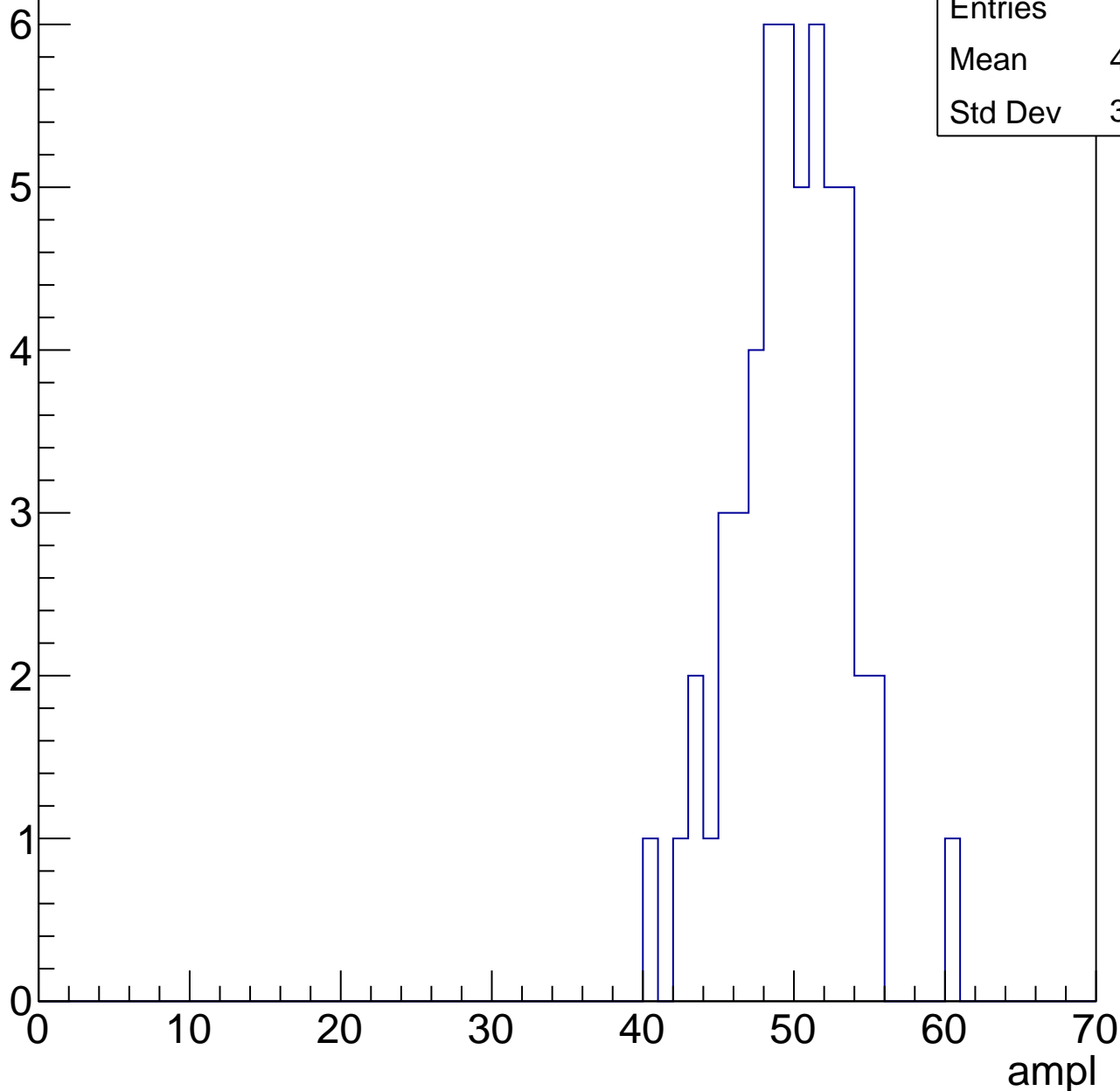


B0L002S, U17-ch61, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	49.32
Std Dev	3.674

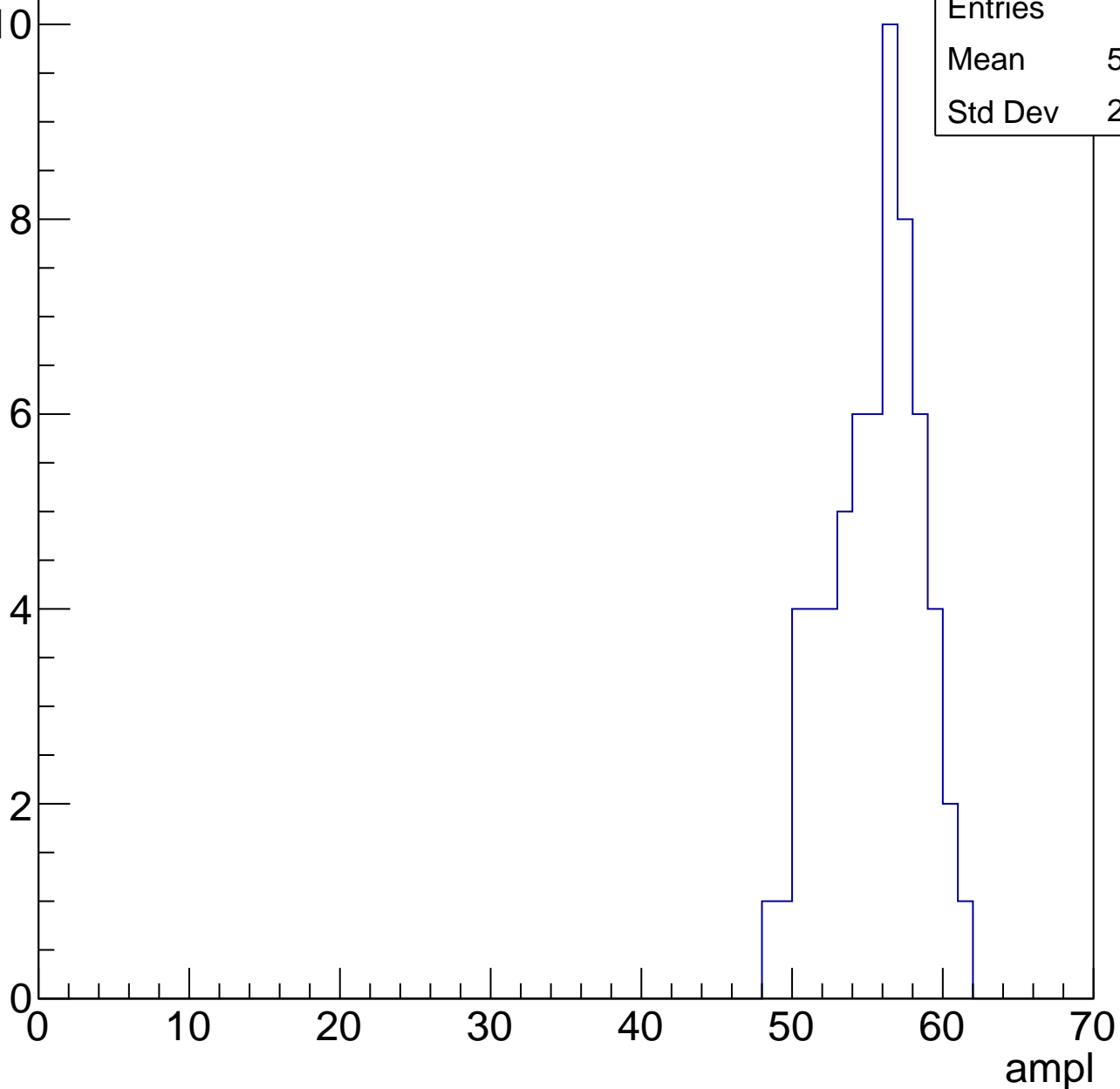


B0L002S, U17-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	54.98
Std Dev	2.997

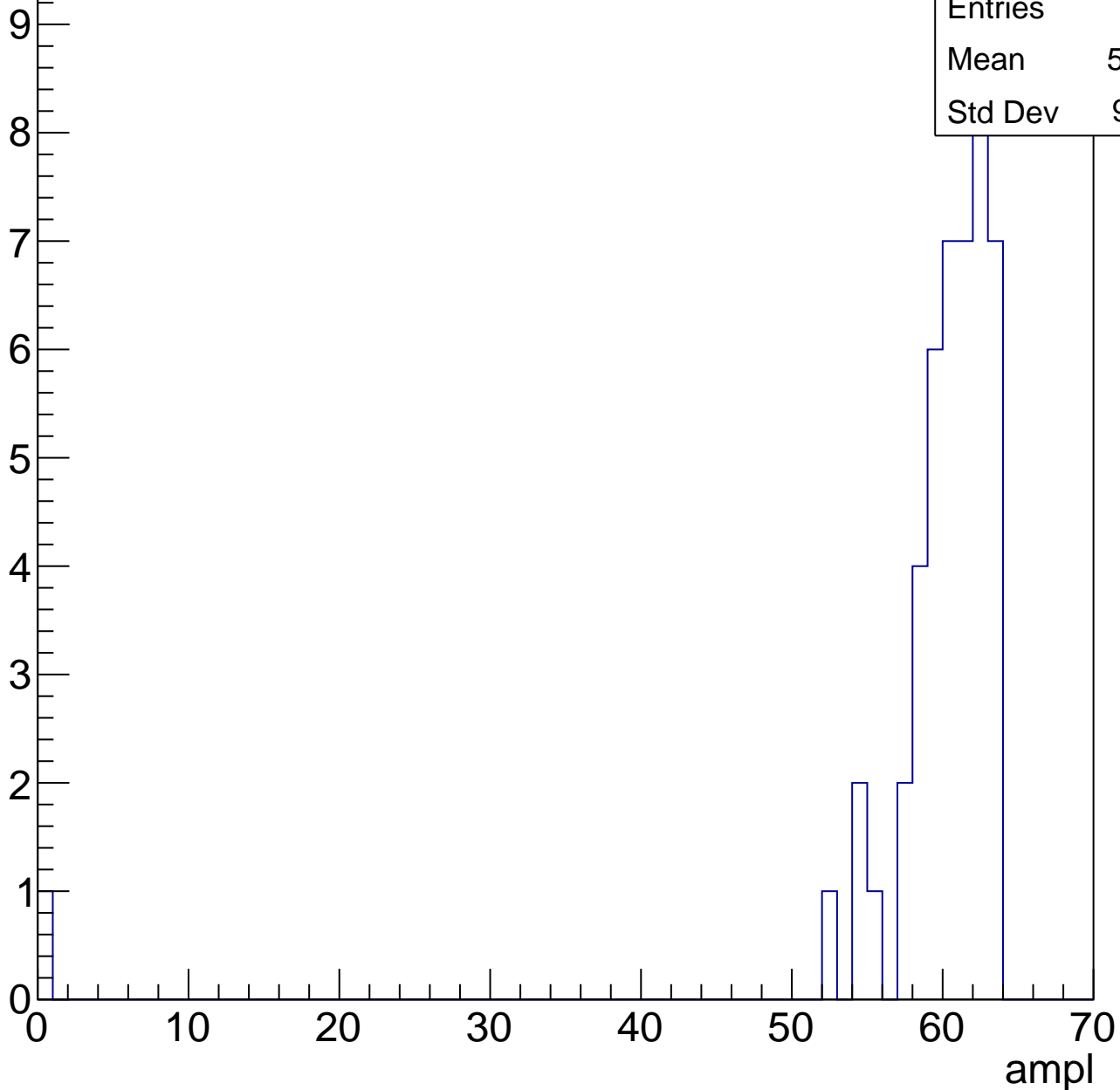


B0L002S, U17-ch61, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	58.74
Std Dev	9.031



B0L002S, U17-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch62, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	28.74
Std Dev	4.701

Gaus mean : 29.5383

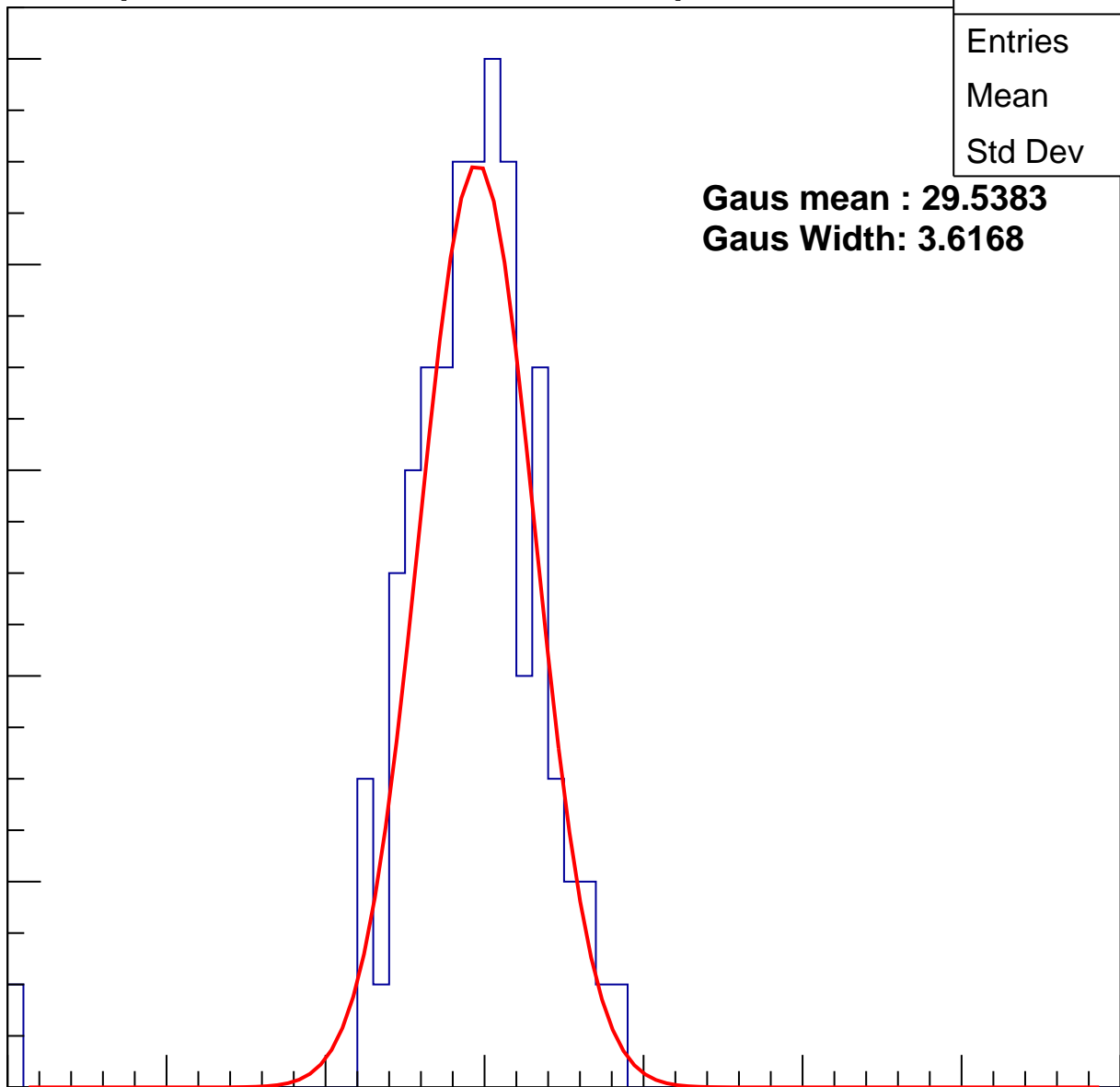
Gaus Width: 3.6168

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch62, adc1

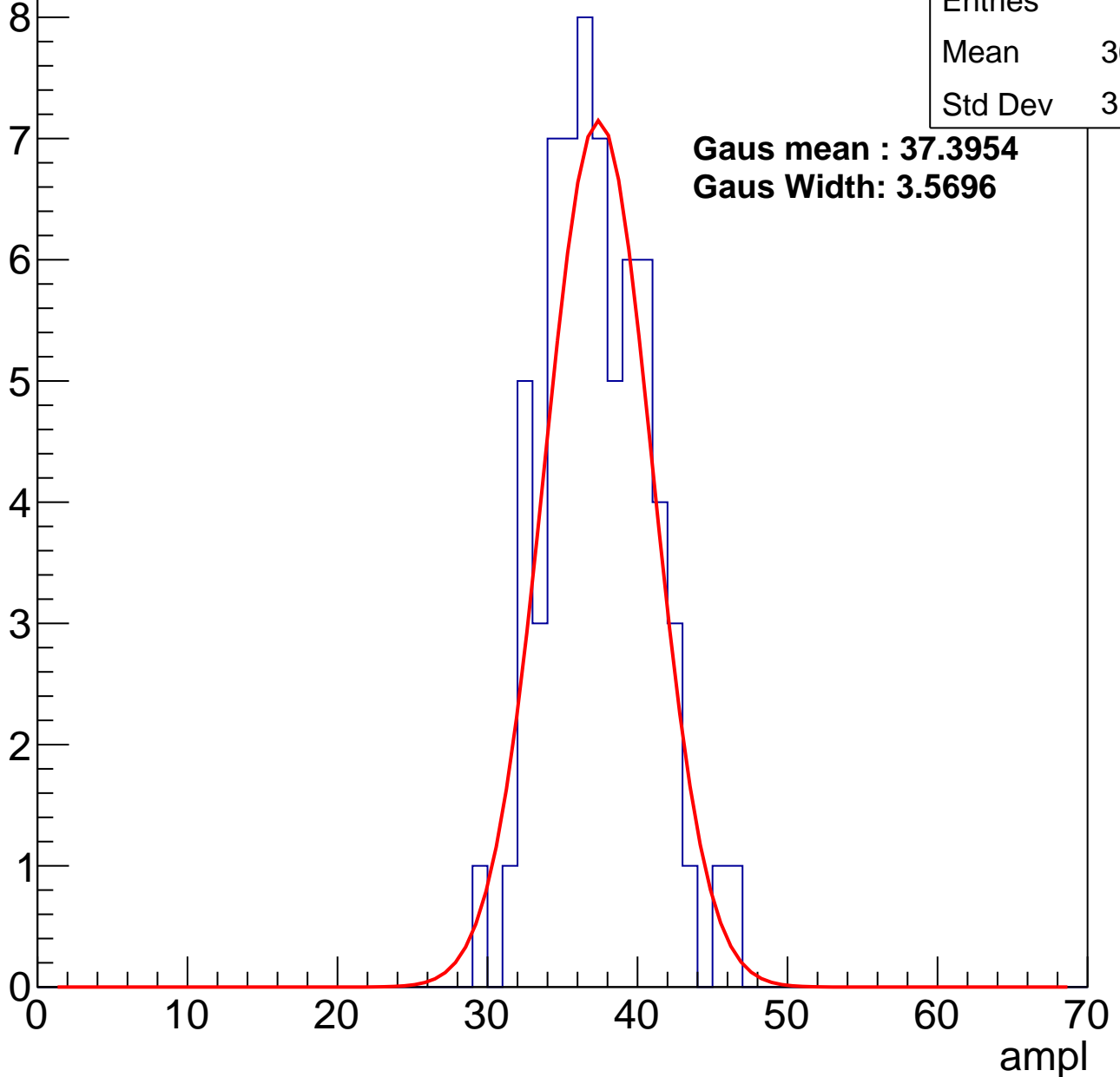
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.92
Std Dev	3.426

Gaus mean : 37.3954

Gaus Width: 3.5696



B0L002S, U17-ch62, adc2

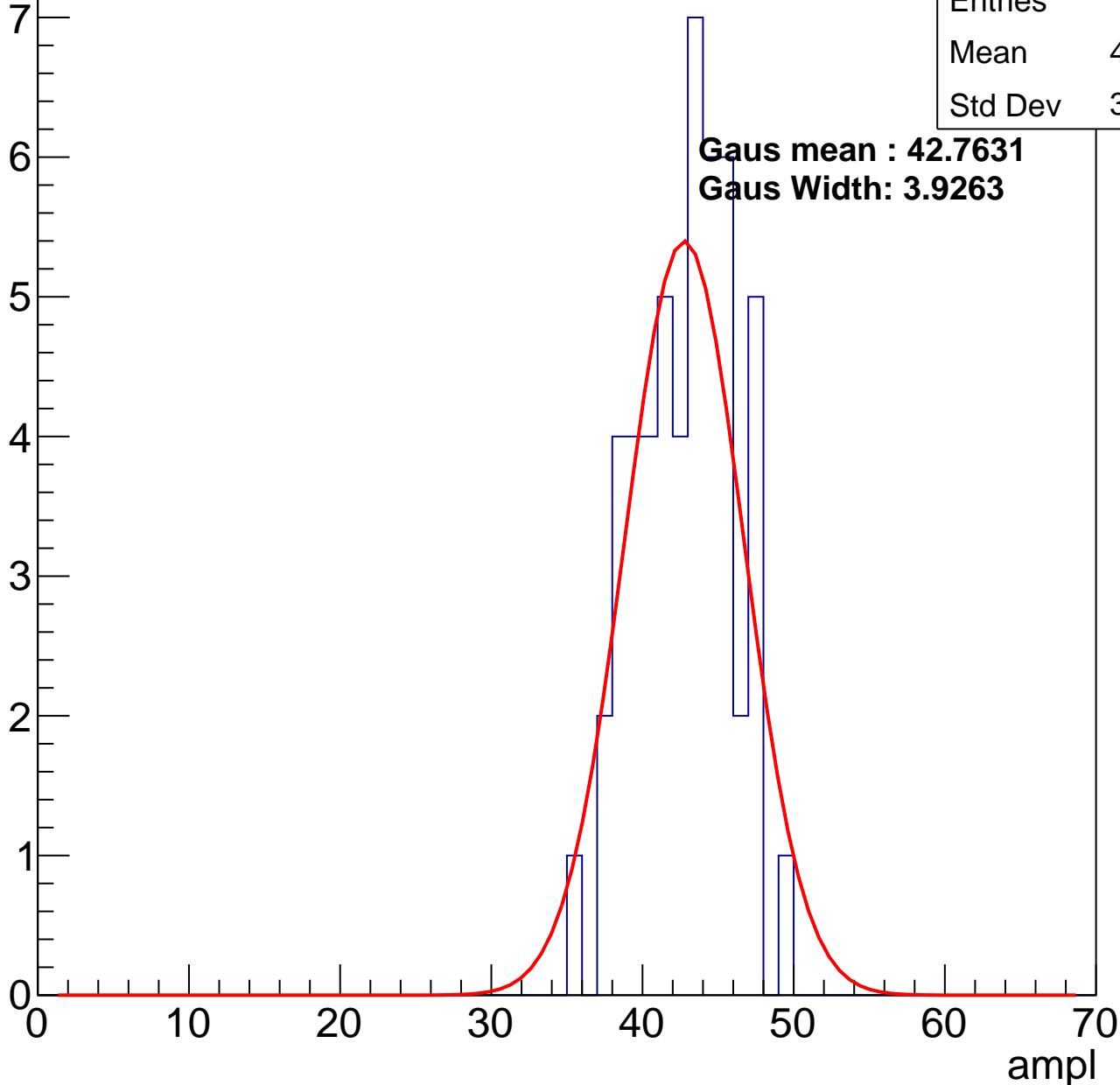
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	42.37
Std Dev	3.143

Gaus mean : 42.7631

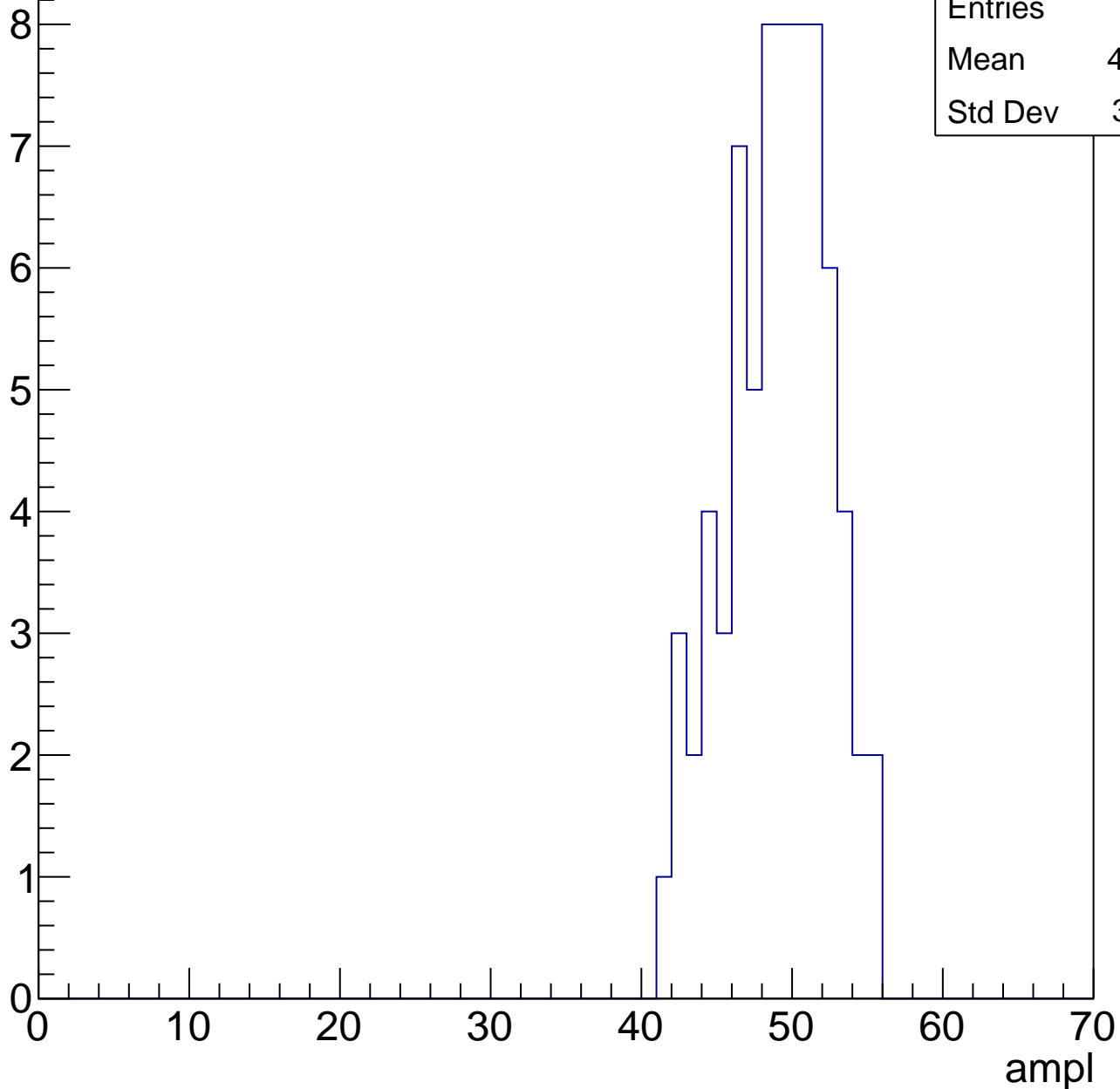
Gaus Width: 3.9263



B0L002S, U17-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



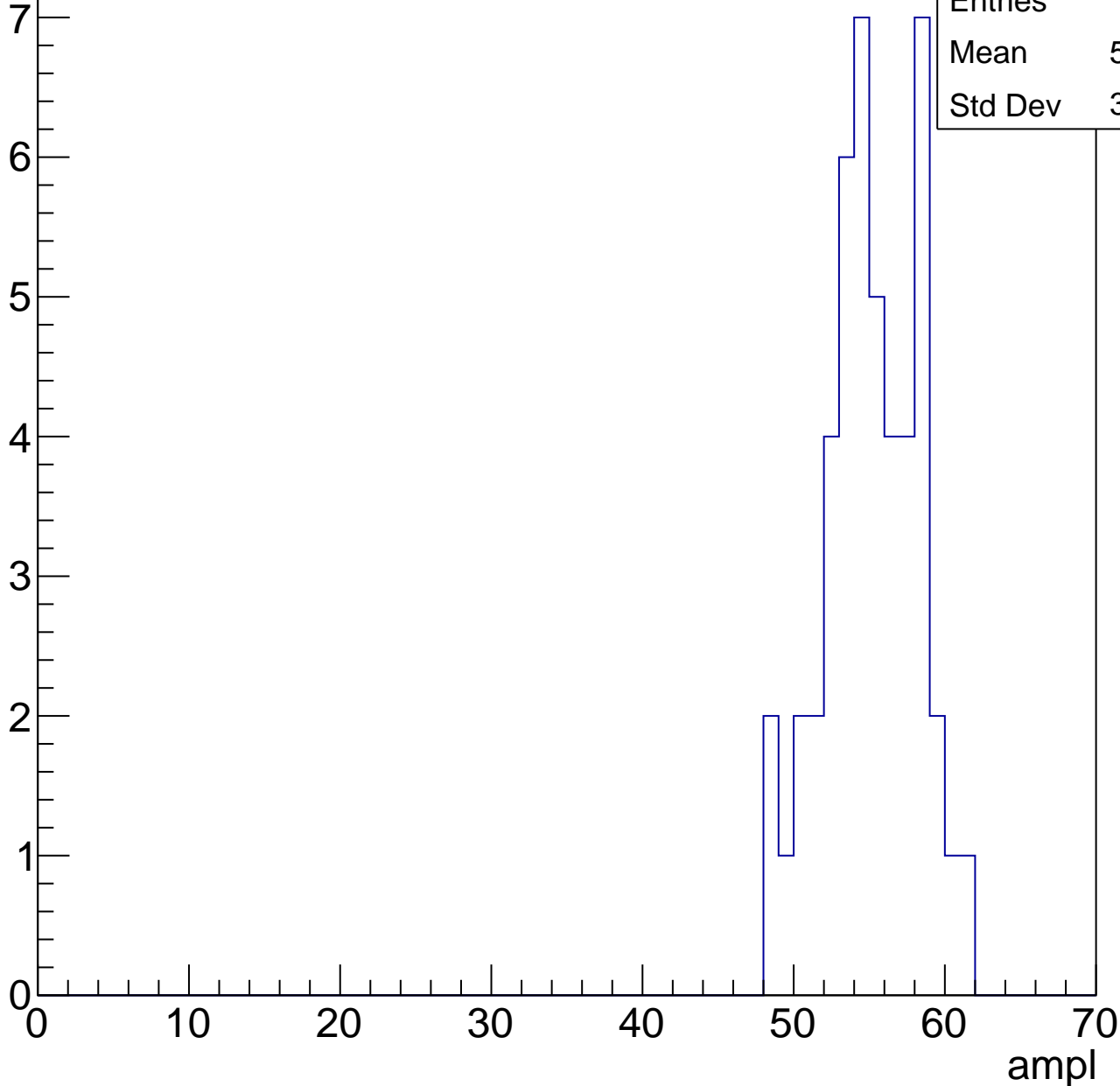
Entries	71
Mean	48.55
Std Dev	3.331

B0L002S, U17-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

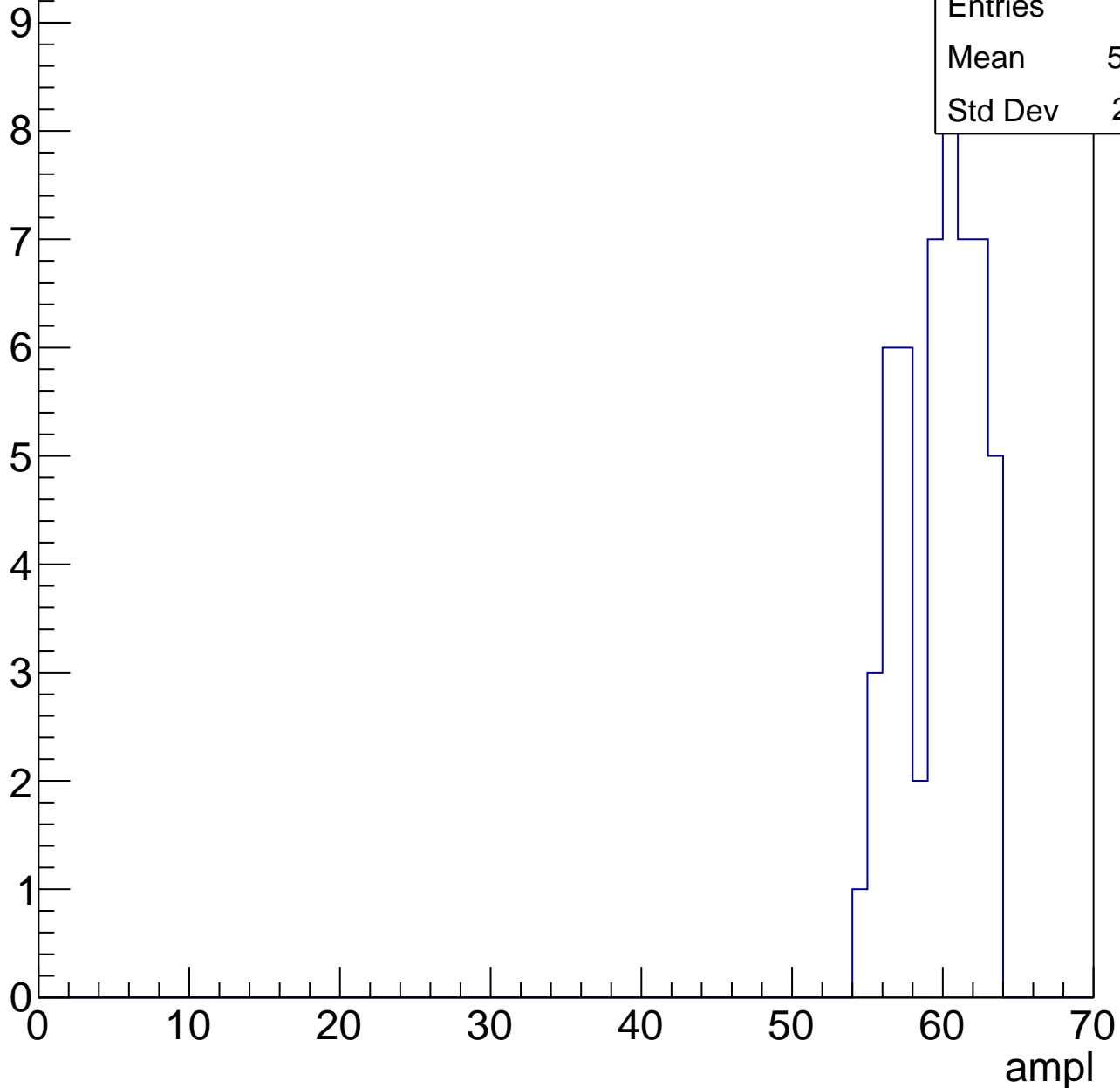
Entries	48
Mean	54.65
Std Dev	3.072



B0L002S, U17-ch62, adc5

calib_packv5_042523_0143.root, FC#8, port C1

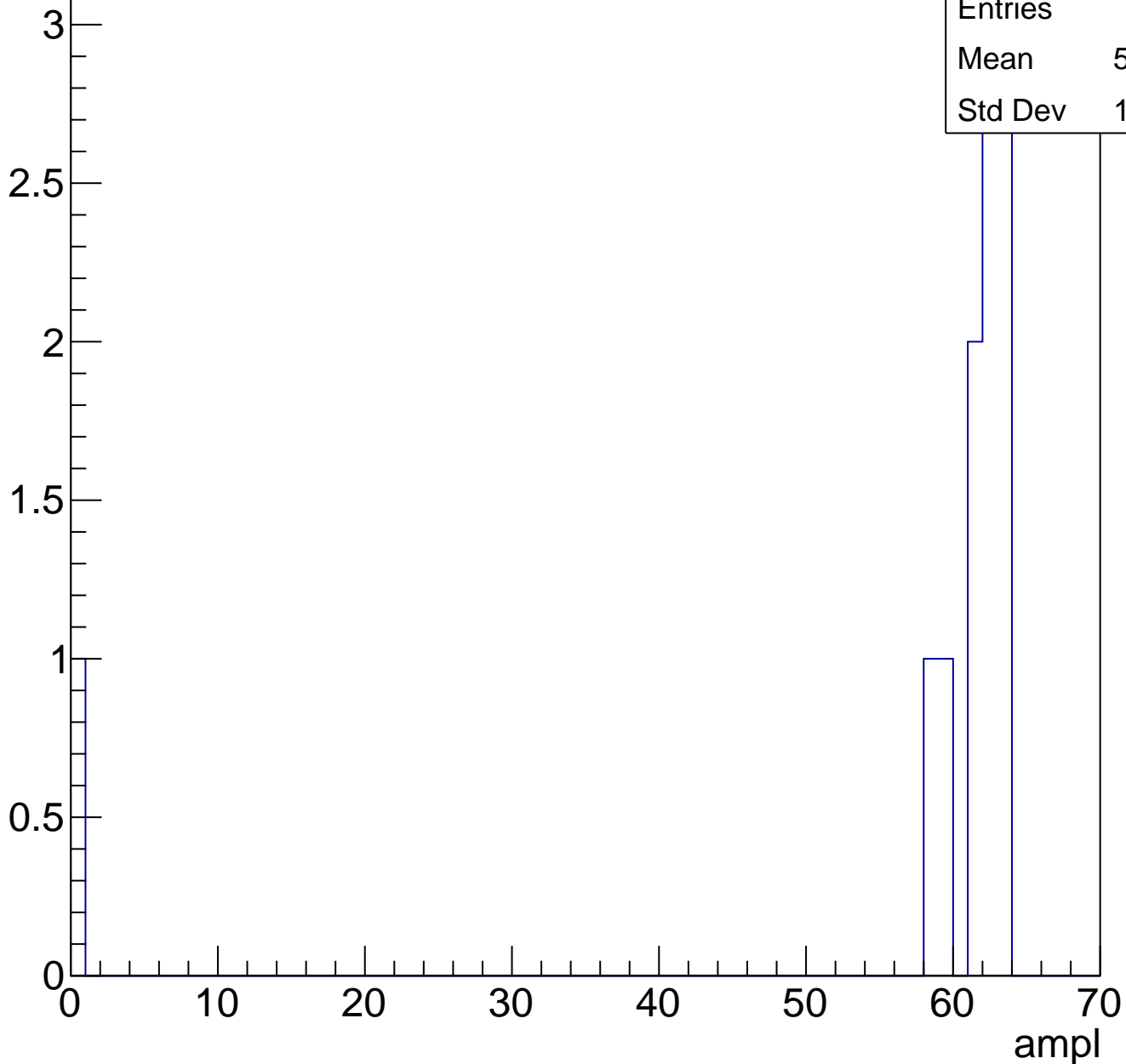
Entry



B0L002S, U17-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch62, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch63, adc0

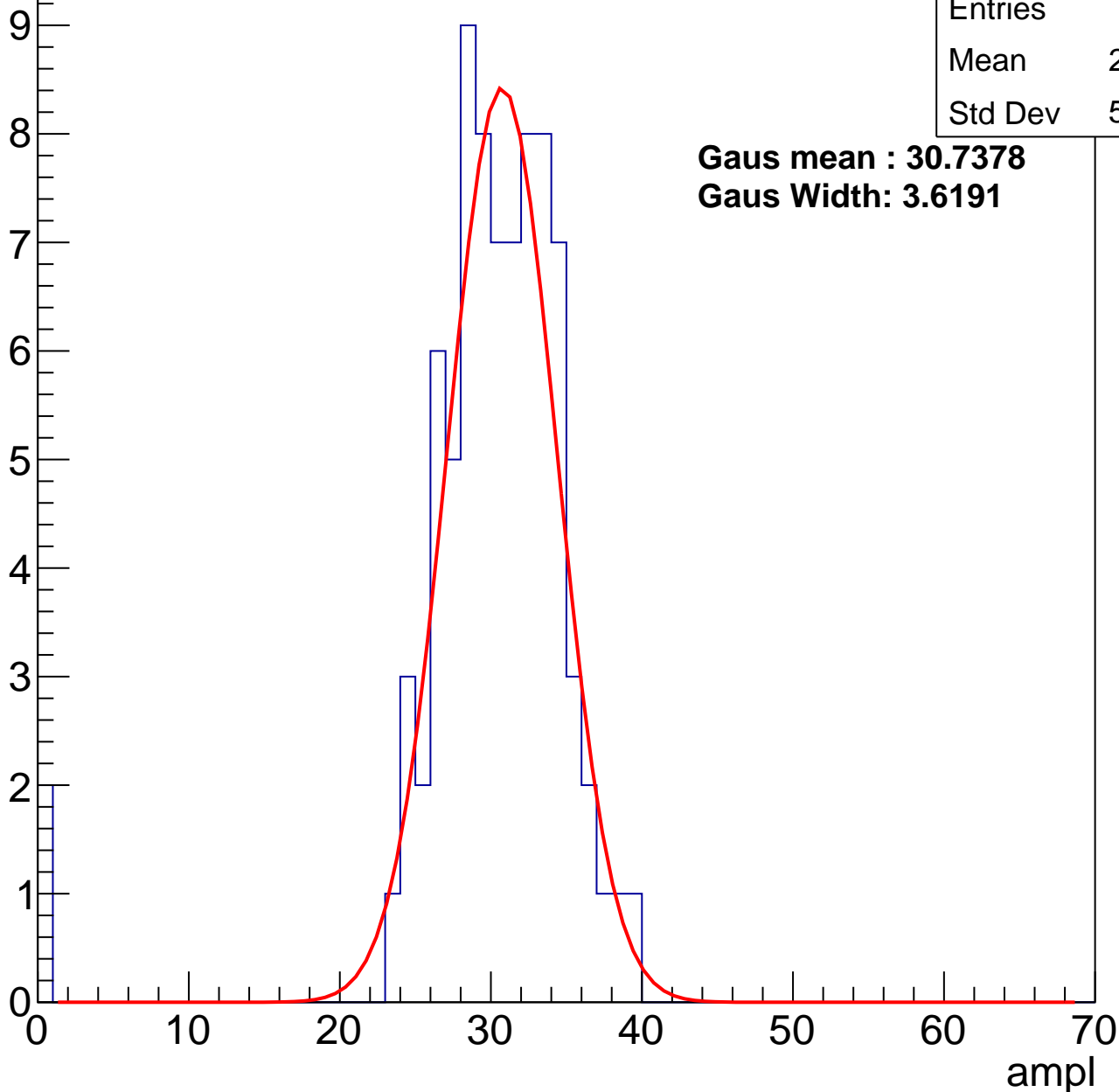
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	29.58
Std Dev	5.818

Gaus mean : 30.7378

Gaus Width: 3.6191



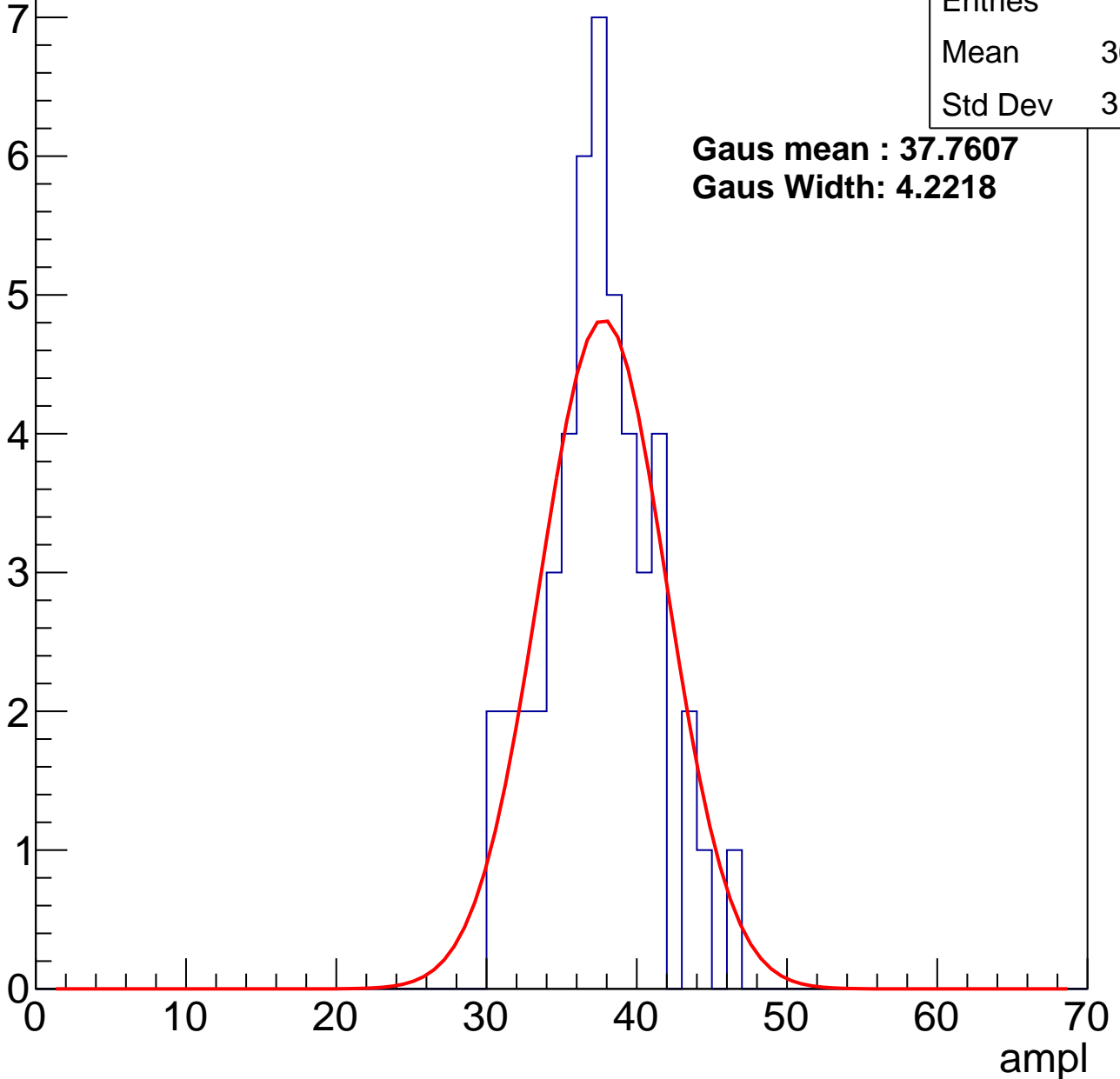
B0L002S, U17-ch63, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	36.98
Std Dev	3.579

Gaus mean : 37.7607
Gaus Width: 4.2218



B0L002S, U17-ch63, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	42.85
Std Dev	3.791

Gaus mean : 43.3188

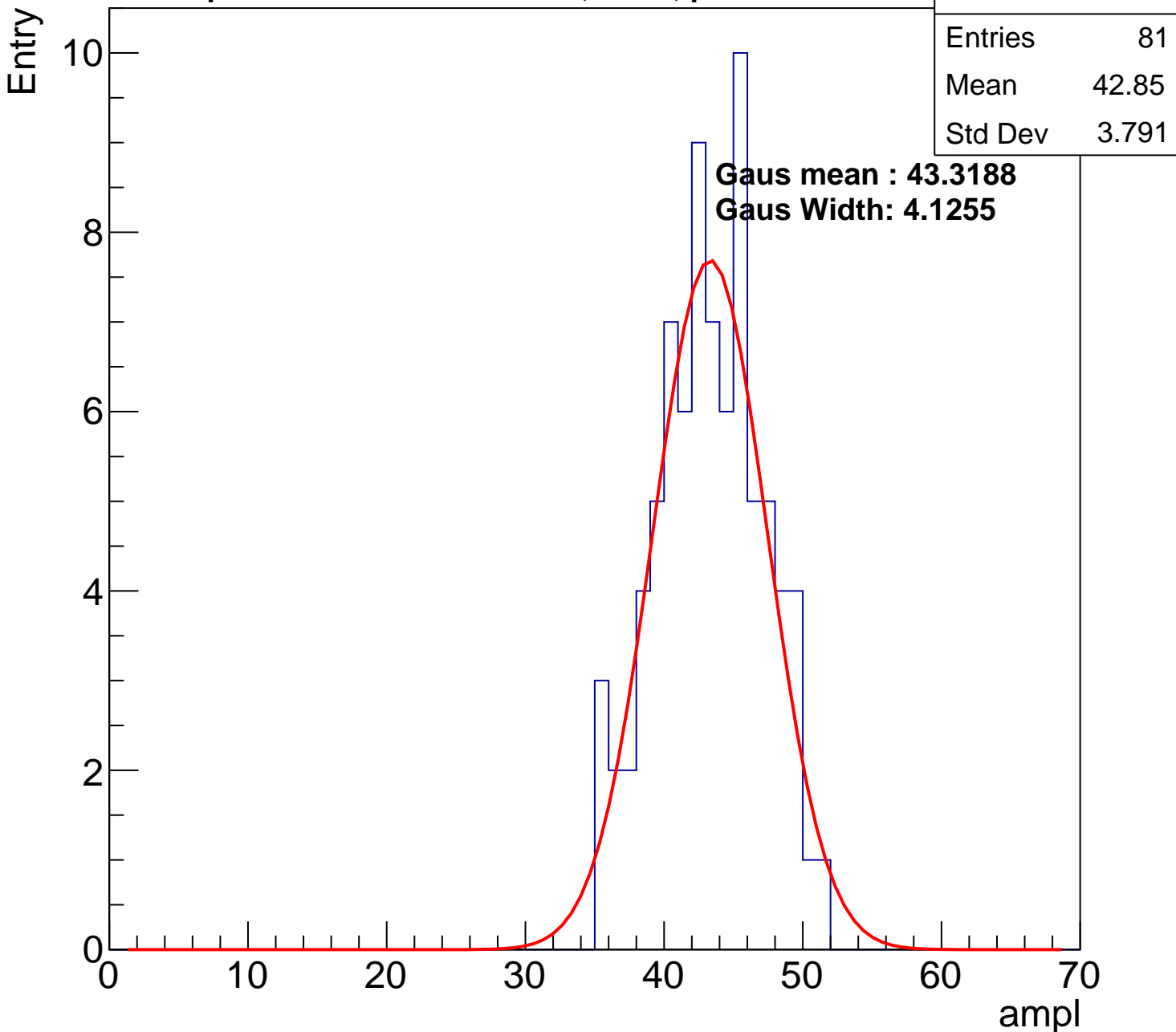
Gaus Width: 4.1255

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

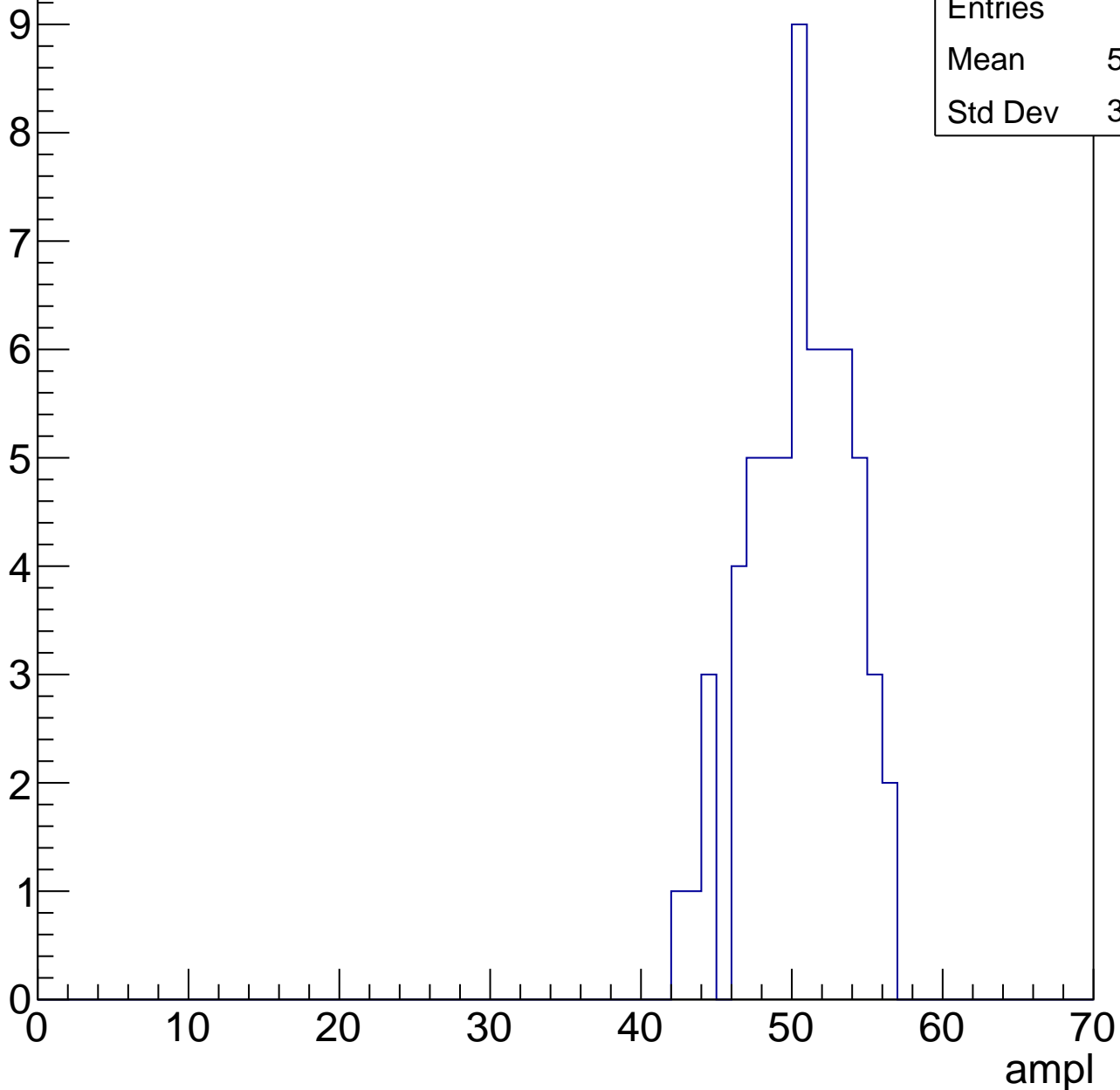


B0L002S, U17-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	50.07
Std Dev	3.304



B0L002S, U17-ch63, adc4

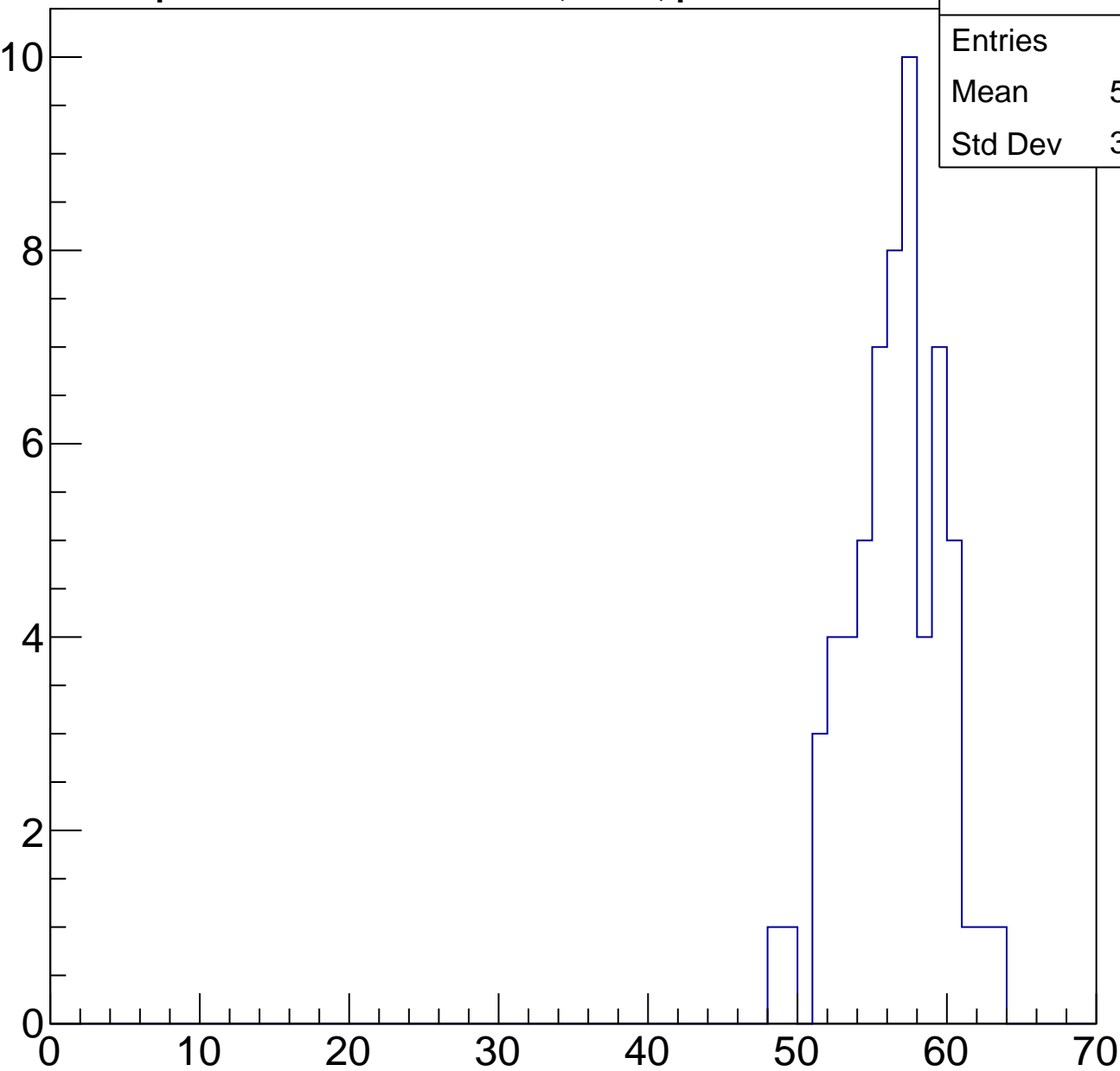
calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	56.03
Std Dev	3.095

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

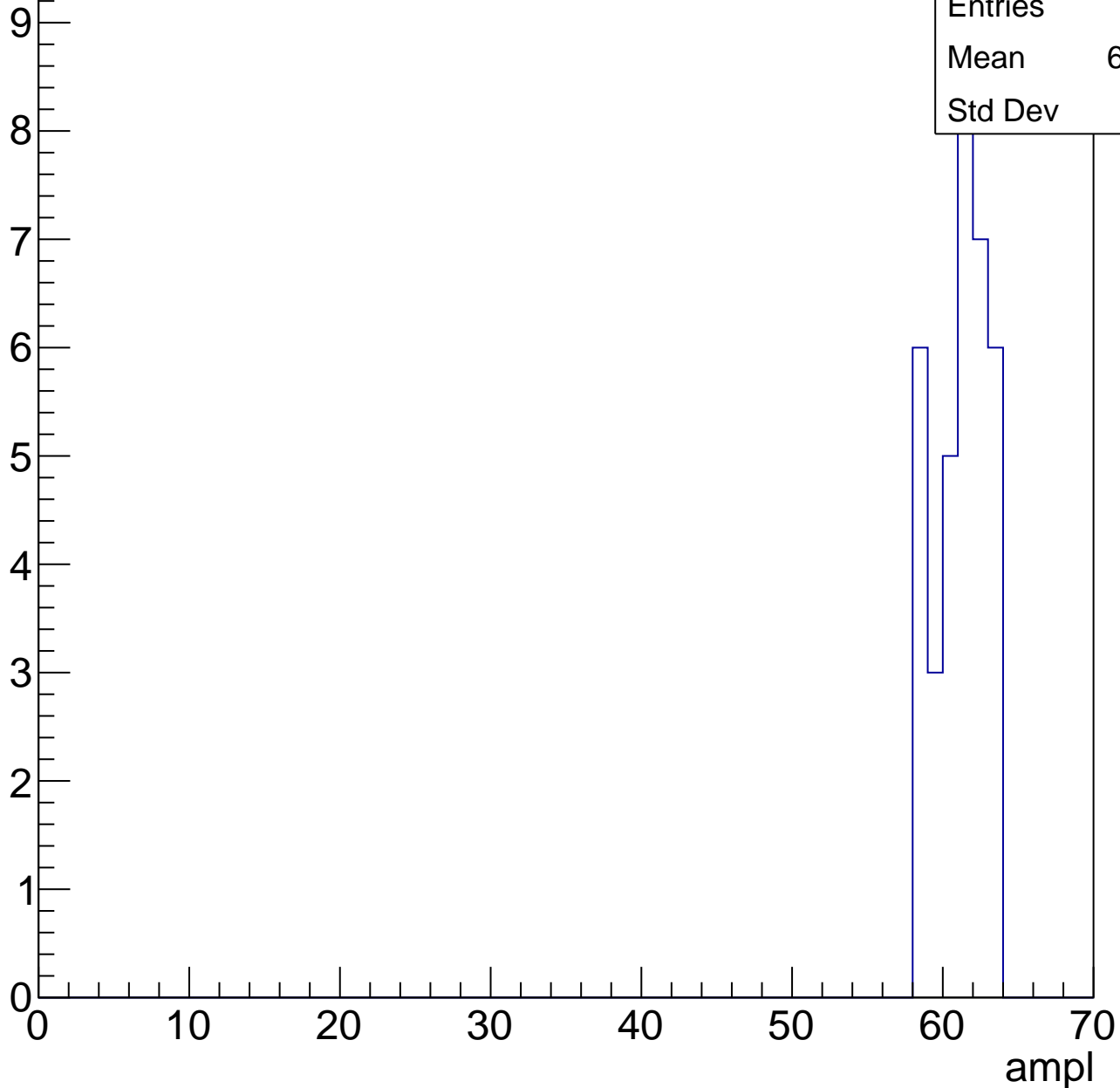


B0L002S, U17-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	60.72
Std Dev	1.66



B0L002S, U17-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch64, adc0

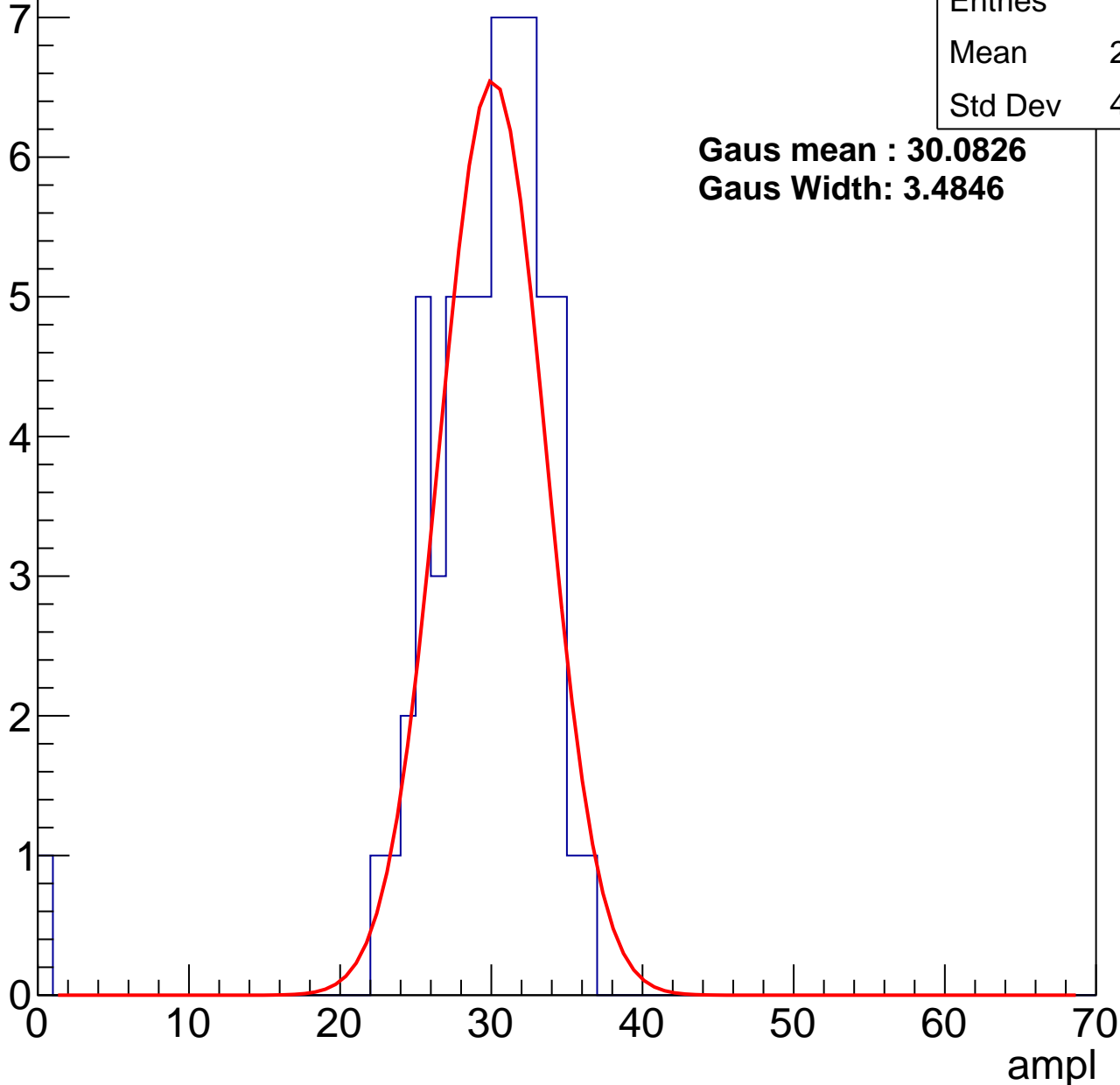
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.07
Std Dev	4.952

Gaus mean : 30.0826

Gaus Width: 3.4846



B0L002S, U17-ch64, adc1

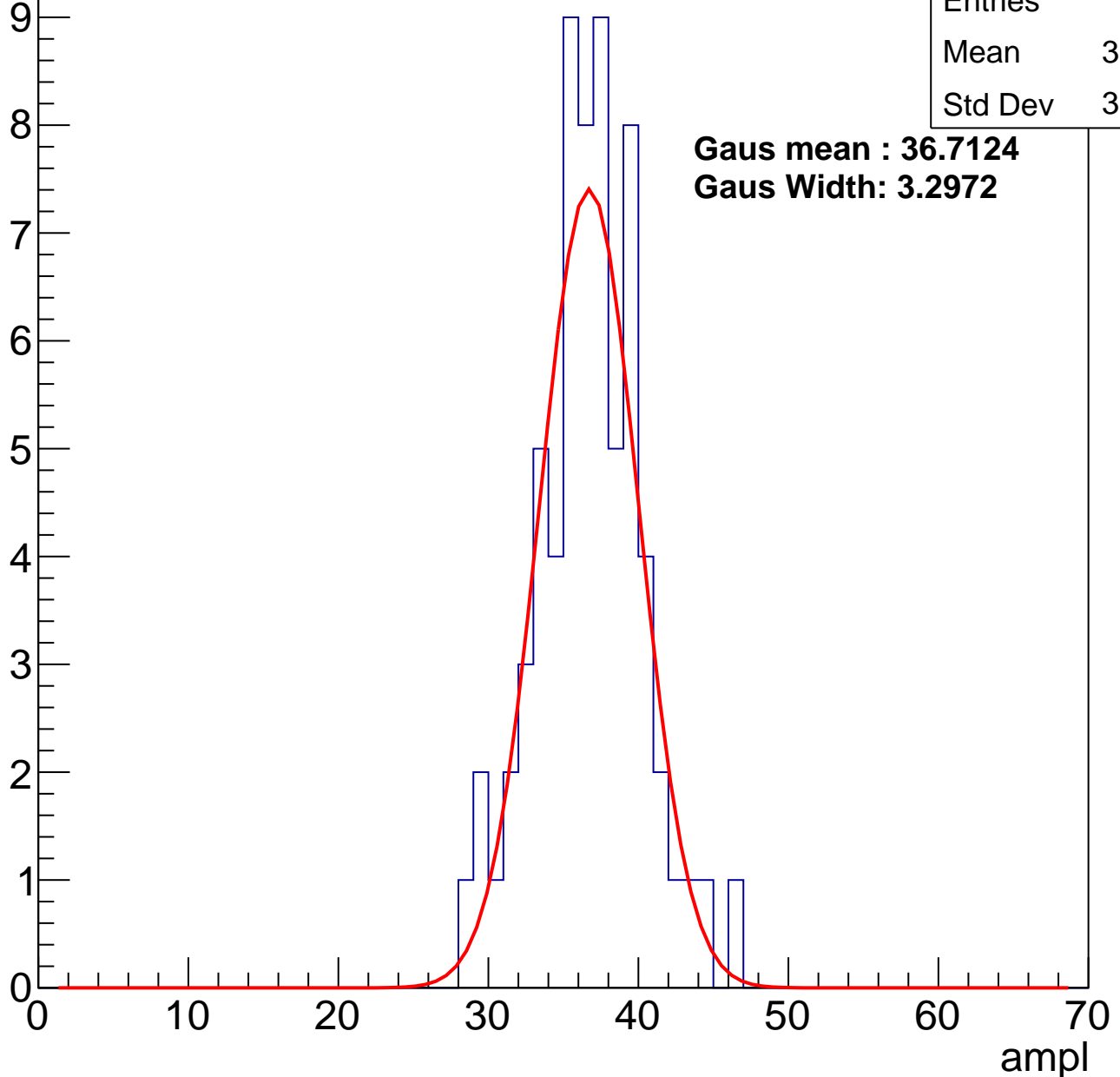
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.27
Std Dev	3.509

Gaus mean : 36.7124

Gaus Width: 3.2972



B0L002S, U17-ch64, adc2

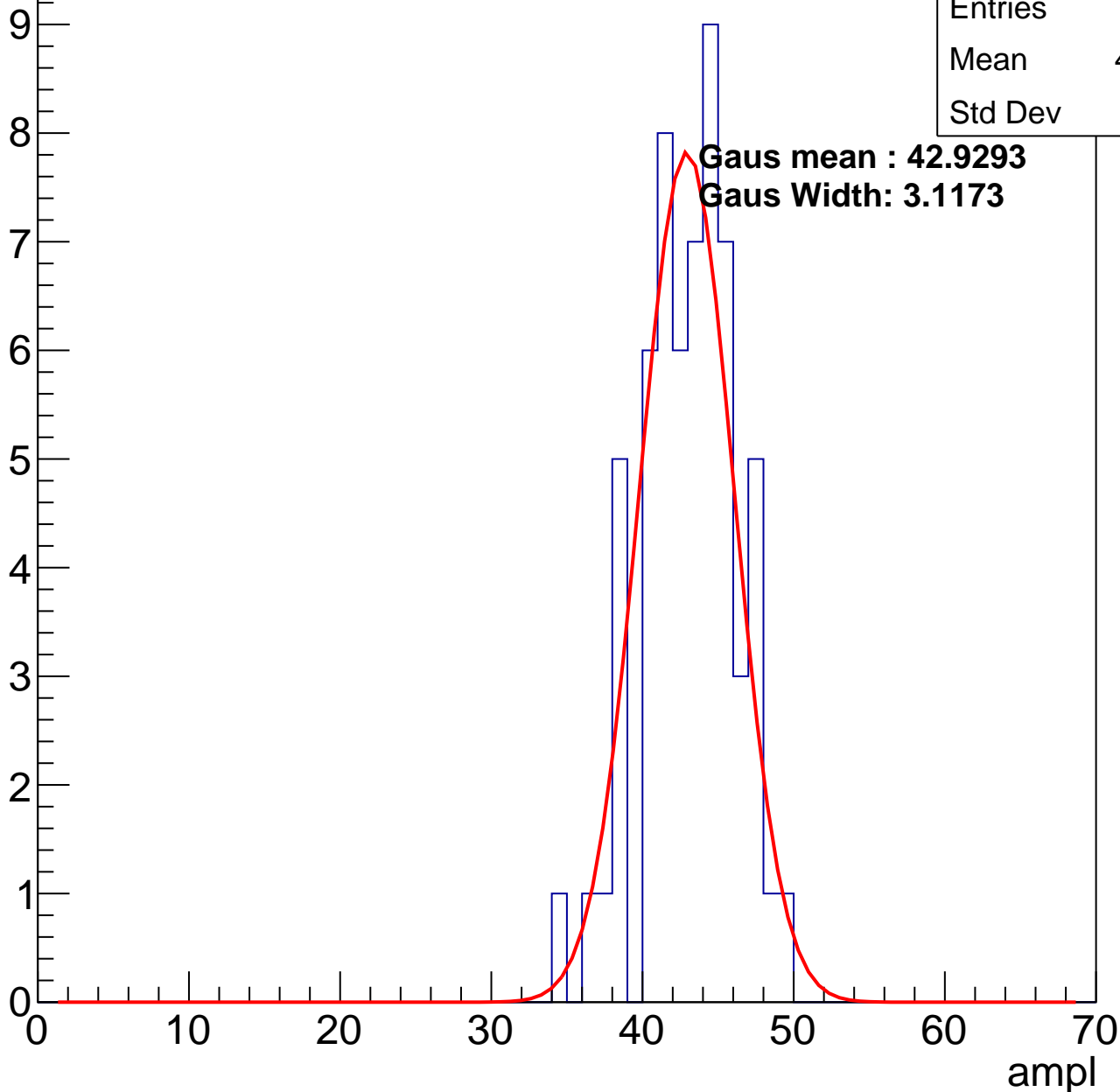
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.61
Std Dev	3.08

Gaus mean : 42.9293

Gaus Width: 3.1173

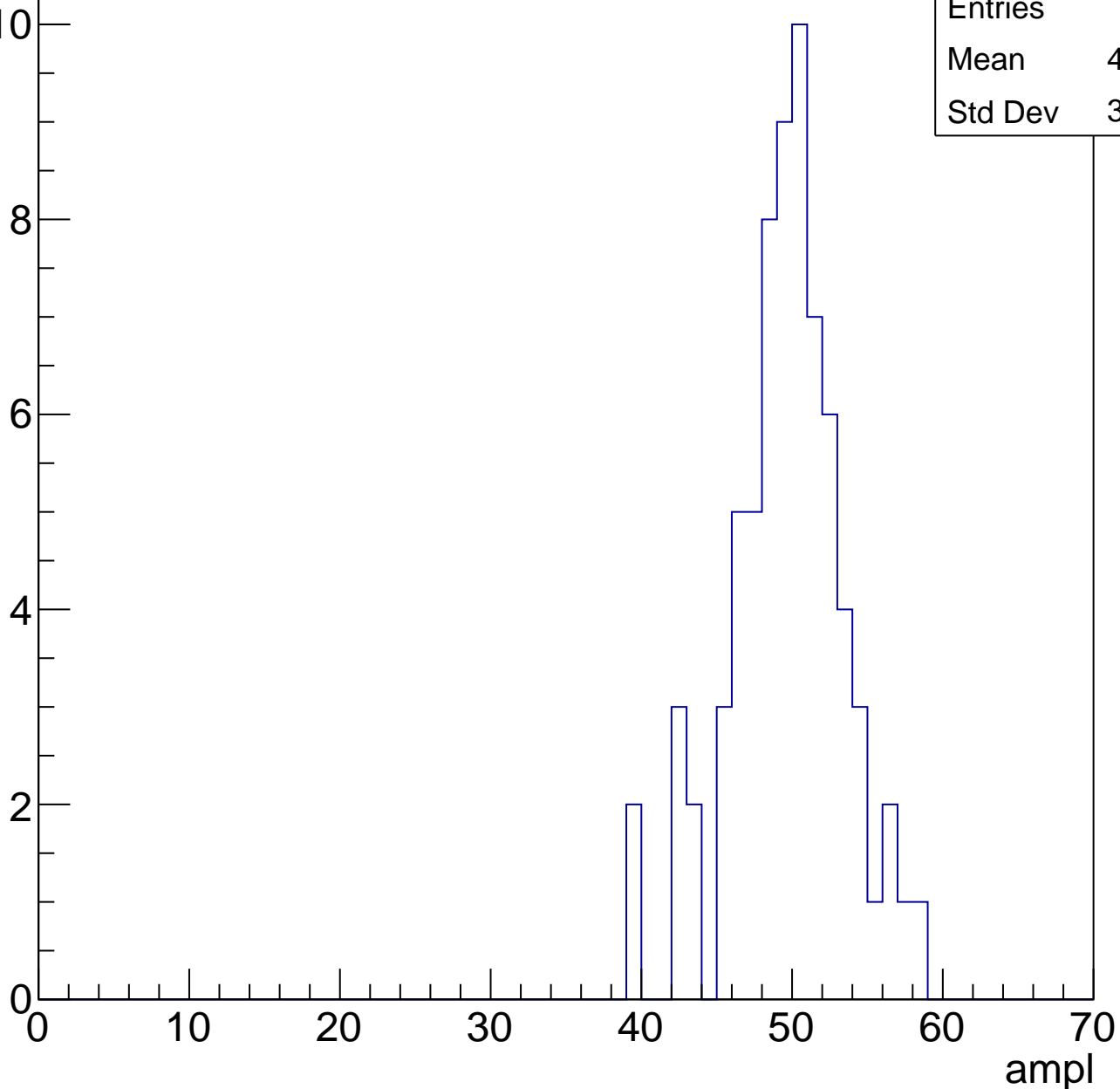


B0L002S, U17-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	49.17
Std Dev	3.815

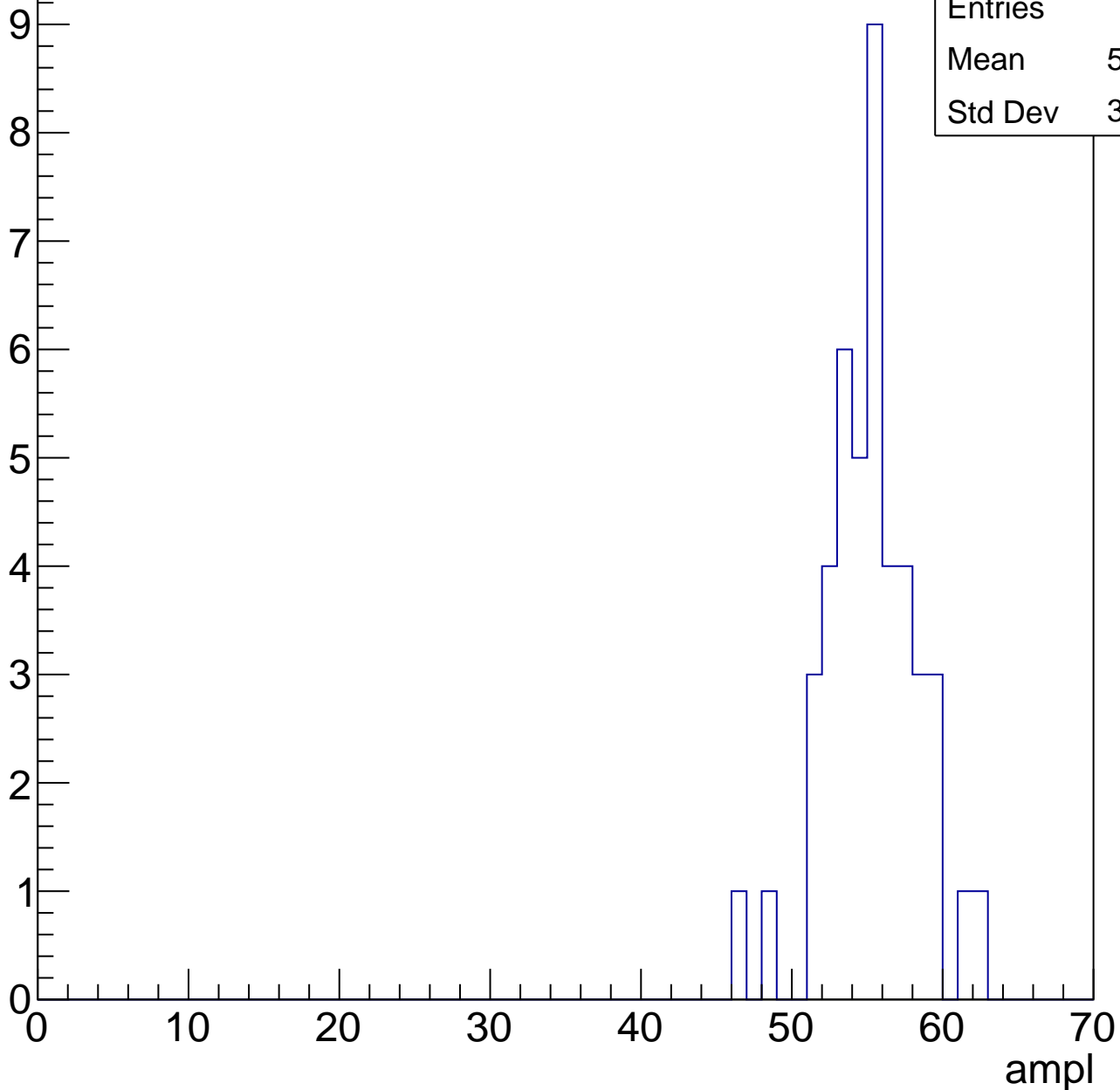


B0L002S, U17-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

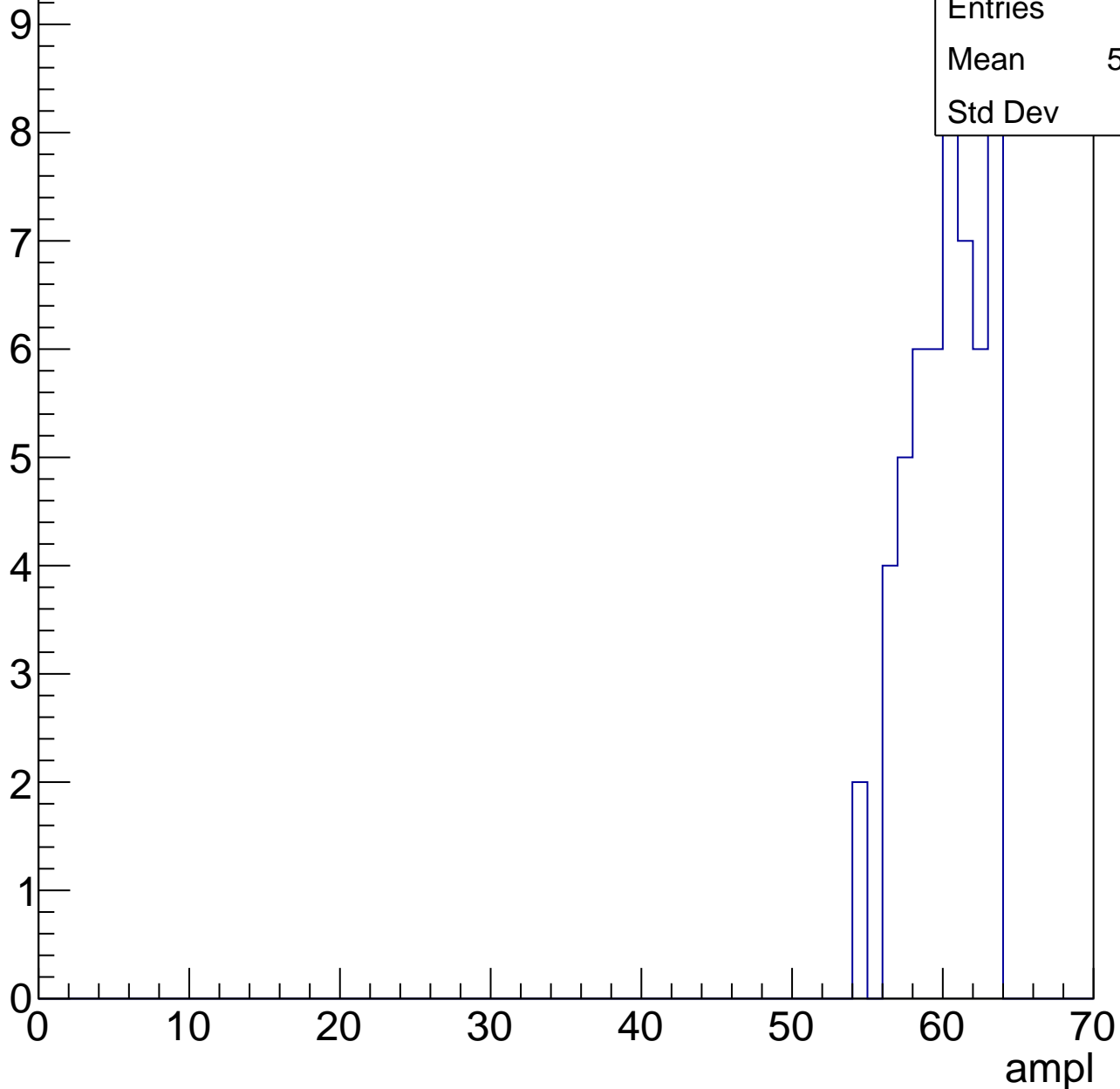
Entries	45
Mean	54.76
Std Dev	3.056



B0L002S, U17-ch64, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch64, adc6

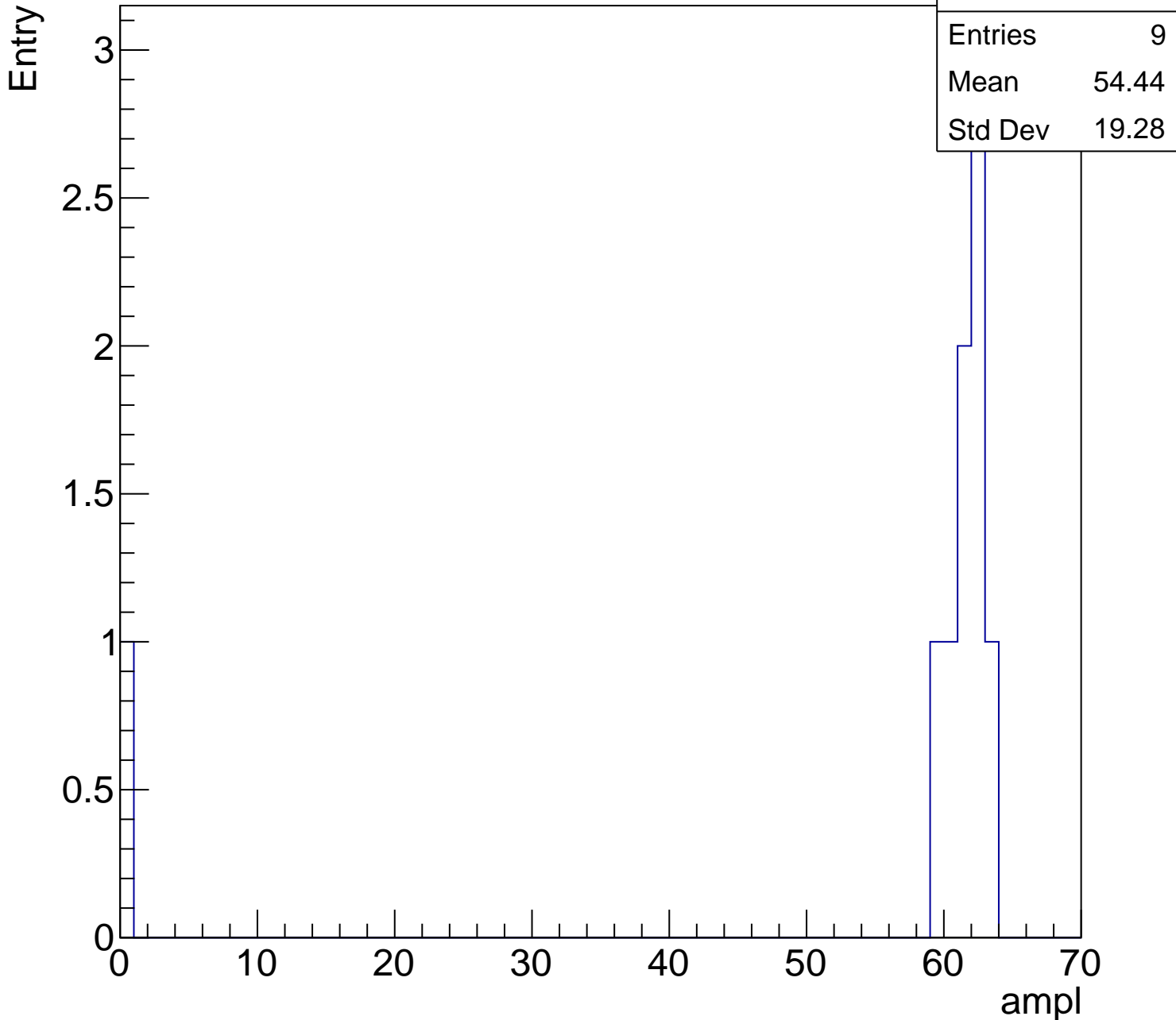
calib_packv5_042523_0143.root, FC#8, port C1

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	54.44
Std Dev	19.28

ampl



B0L002S, U17-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch65, adc0

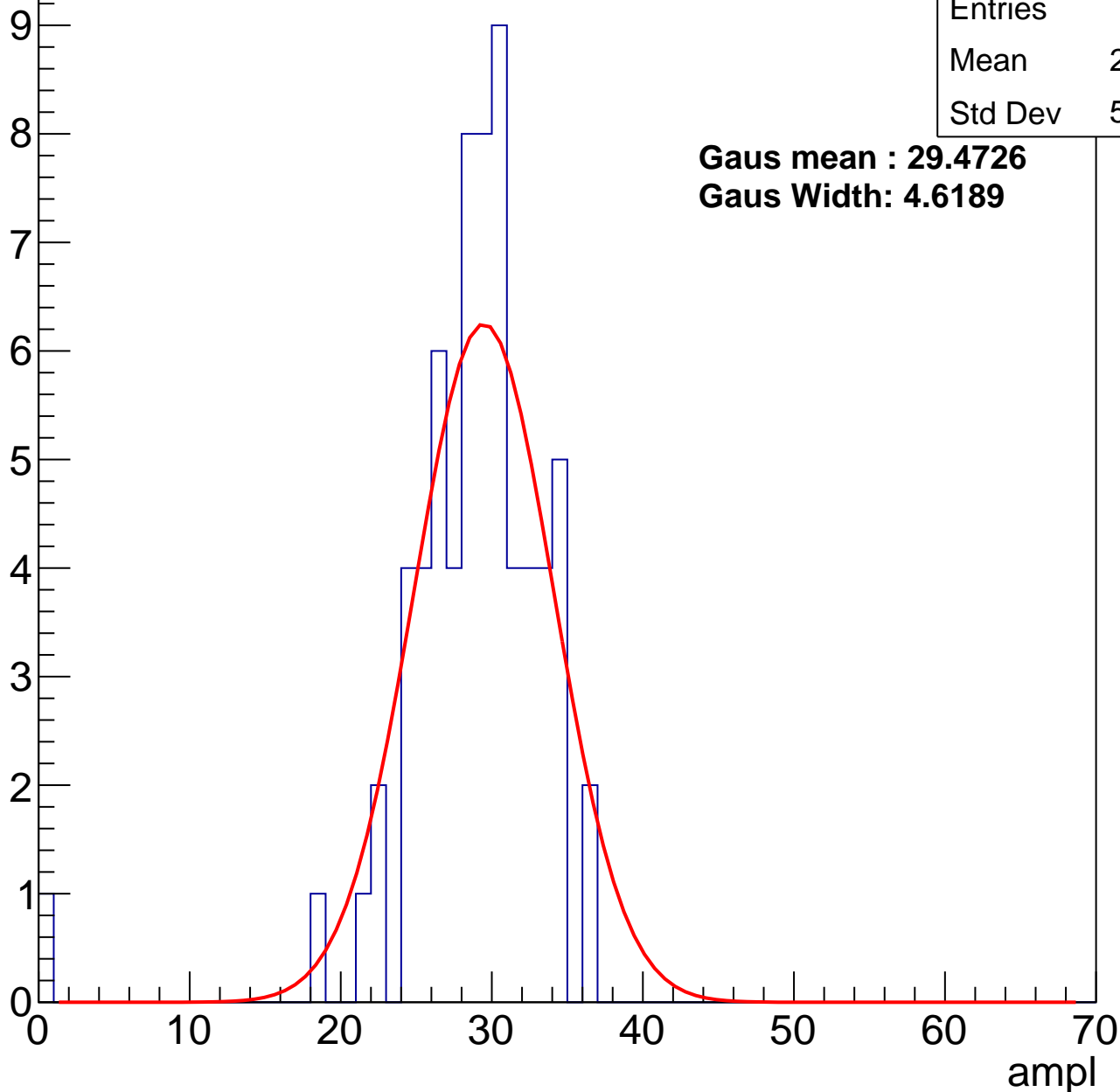
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	28.28
Std Dev	5.007

Gaus mean : 29.4726

Gaus Width: 4.6189



B0L002S, U17-ch65, adc1

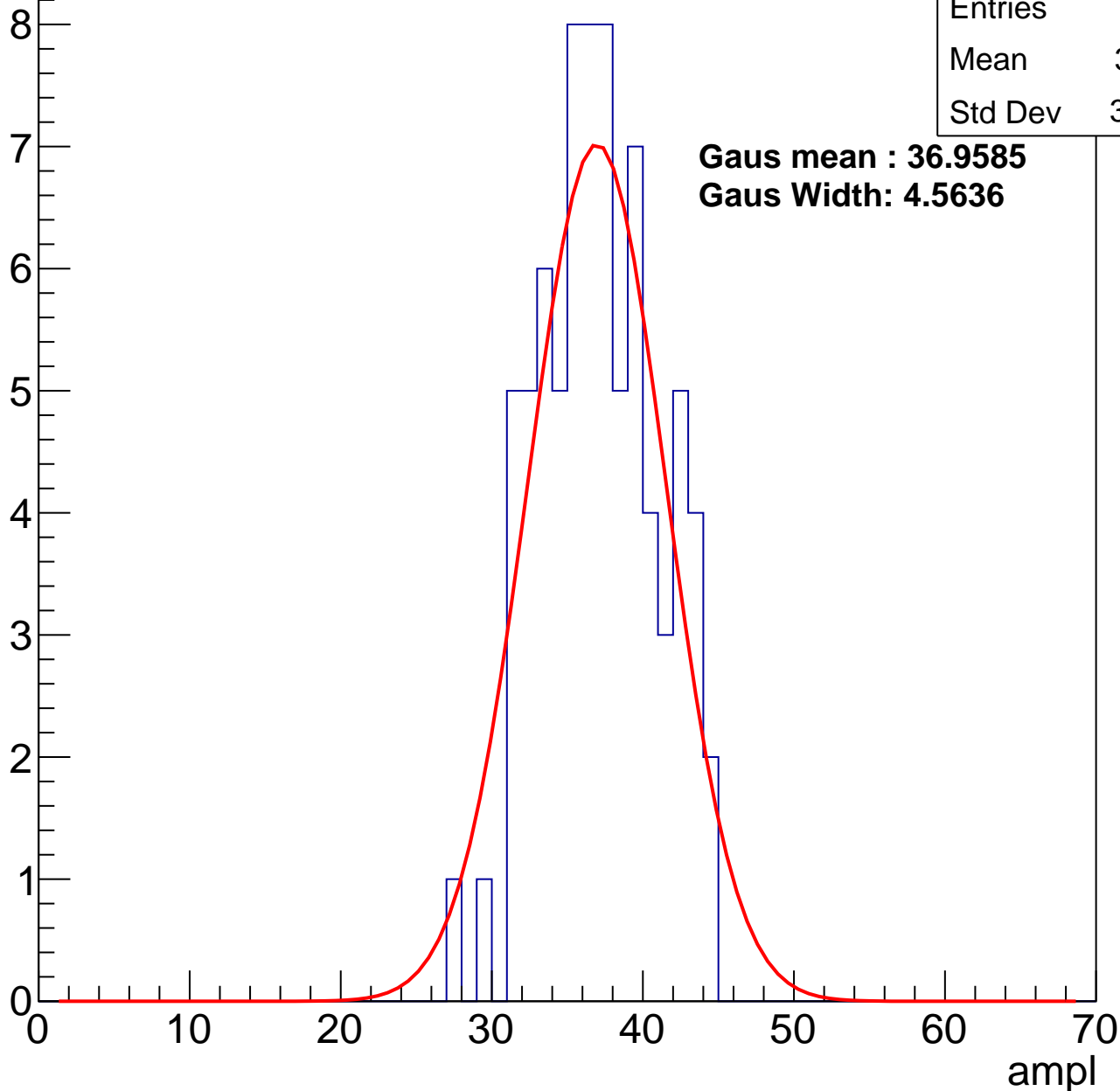
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.61
Std Dev	3.818

Gaus mean : 36.9585

Gaus Width: 4.5636



B0L002S, U17-ch65, adc2

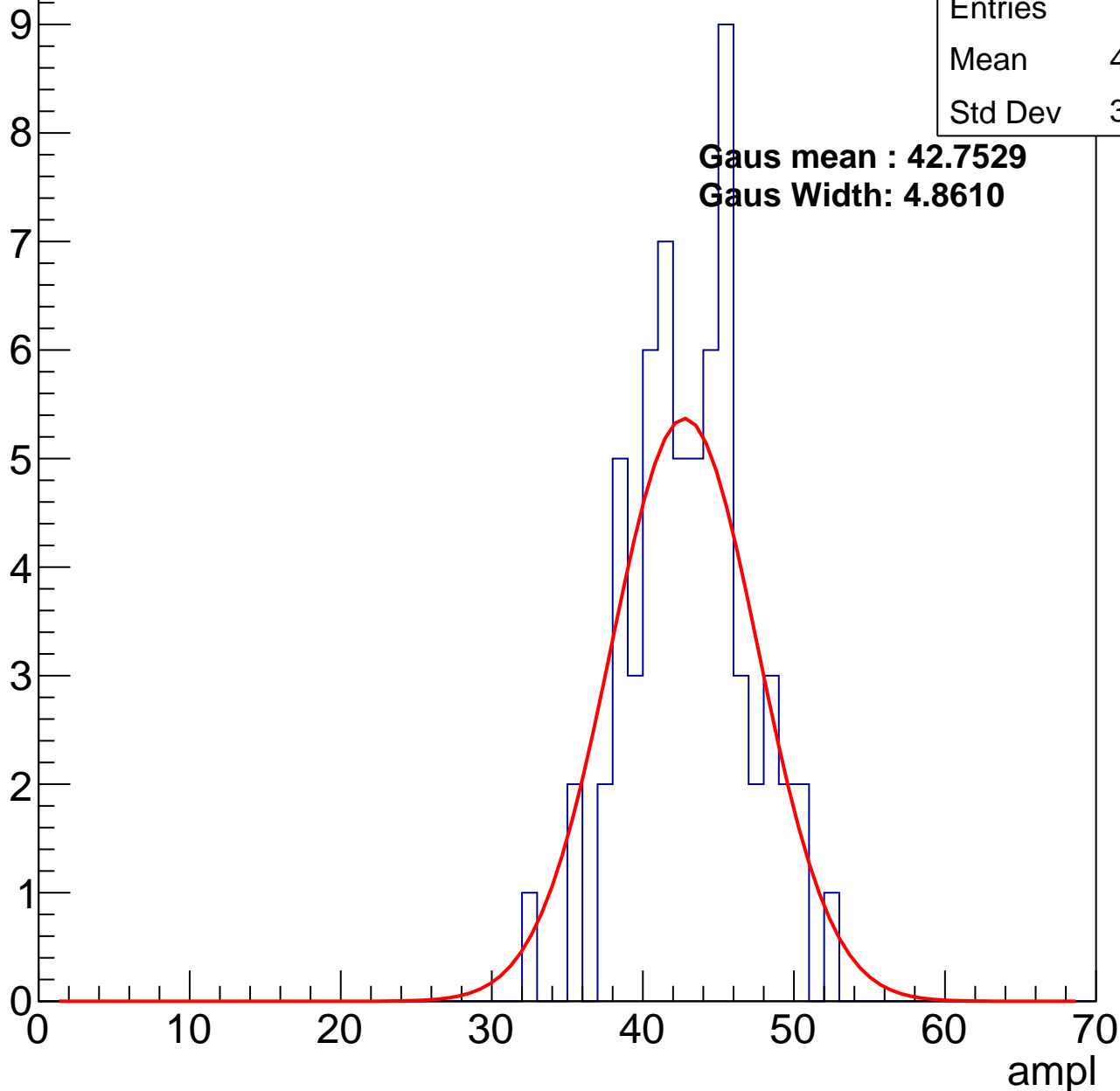
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.66
Std Dev	3.954

Gaus mean : 42.7529

Gaus Width: 4.8610

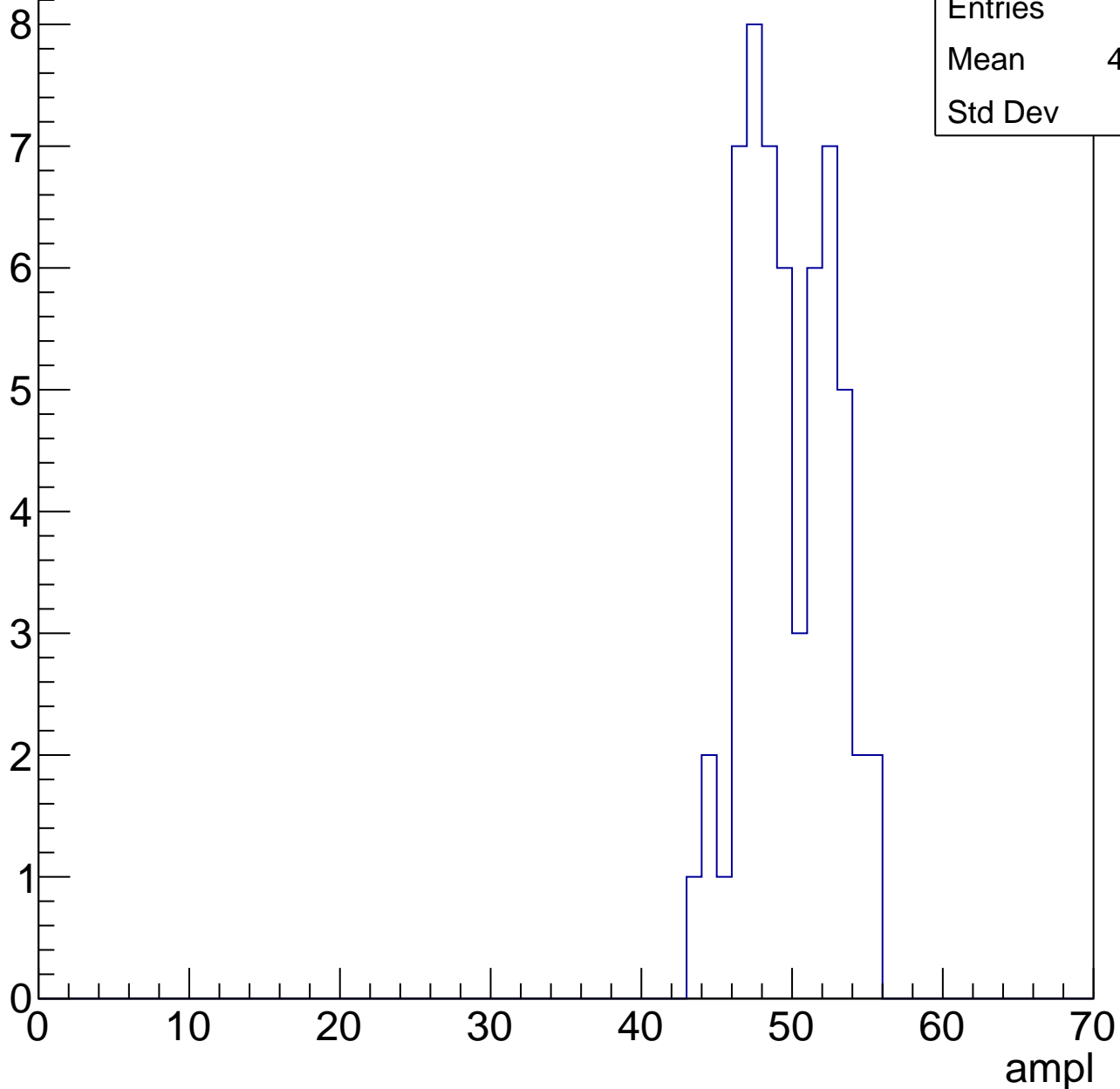


B0L002S, U17-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49.25
Std Dev	2.94

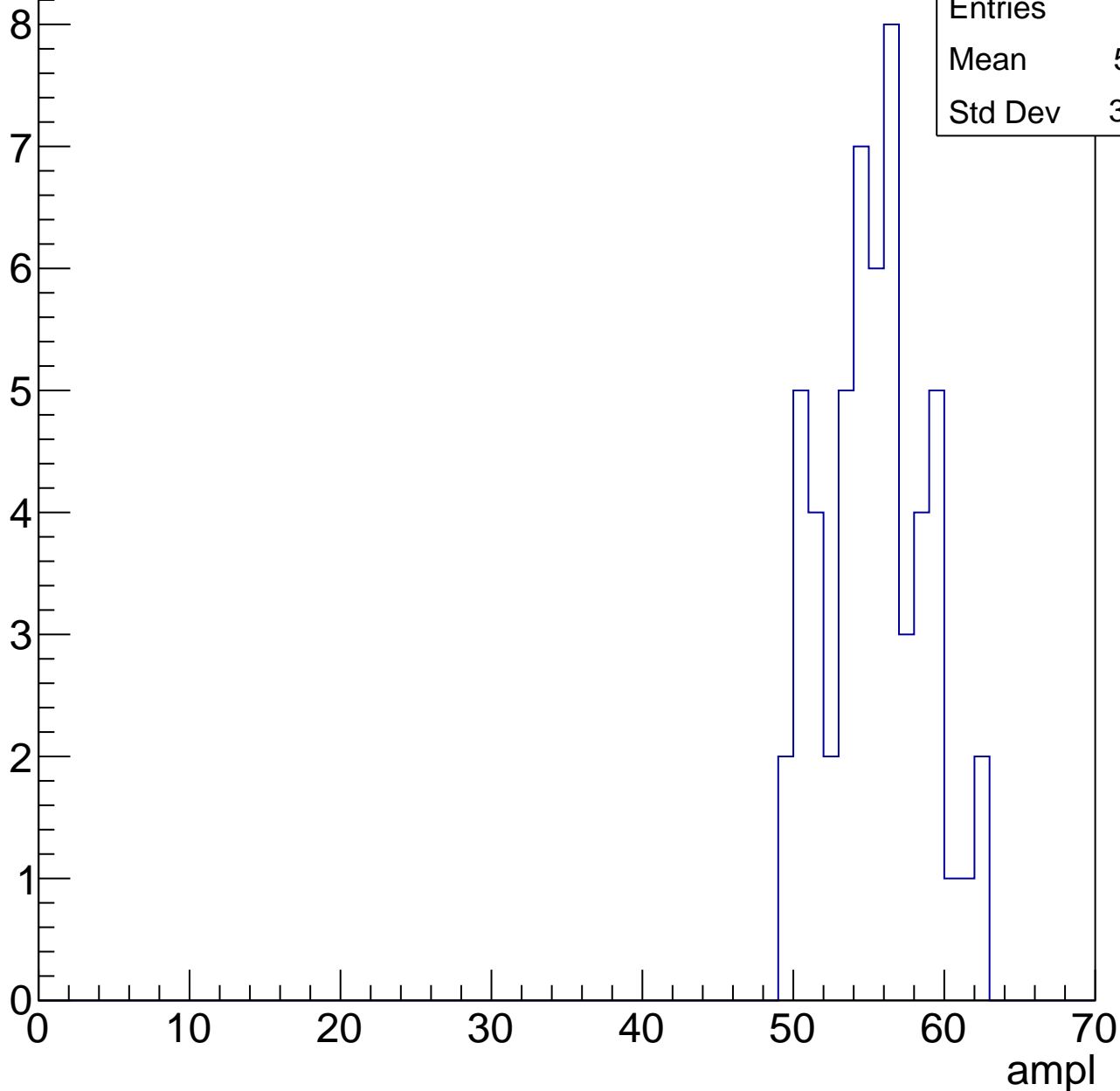


B0L002S, U17-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	54.91
Std Dev	3.304

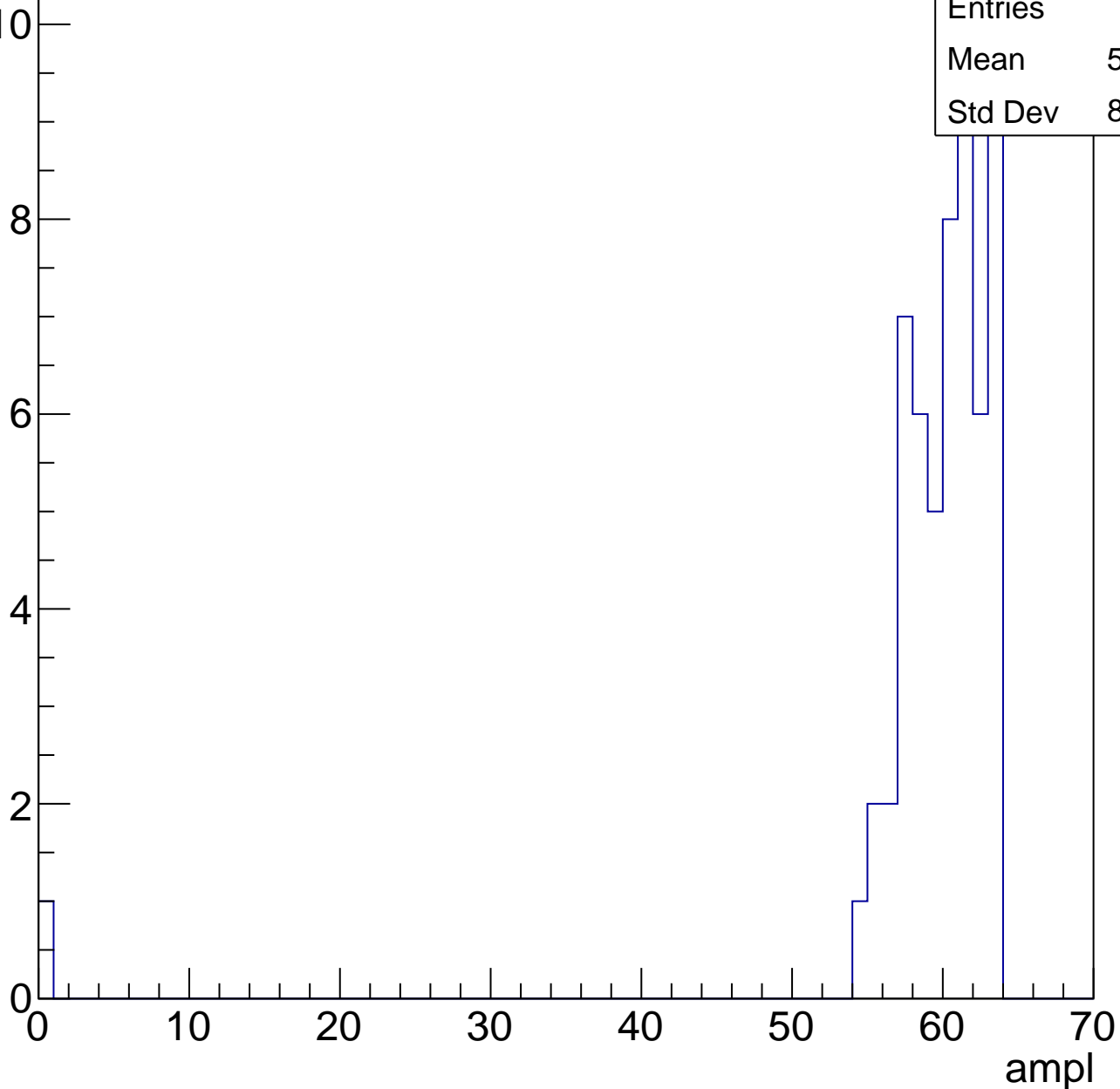


B0L002S, U17-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

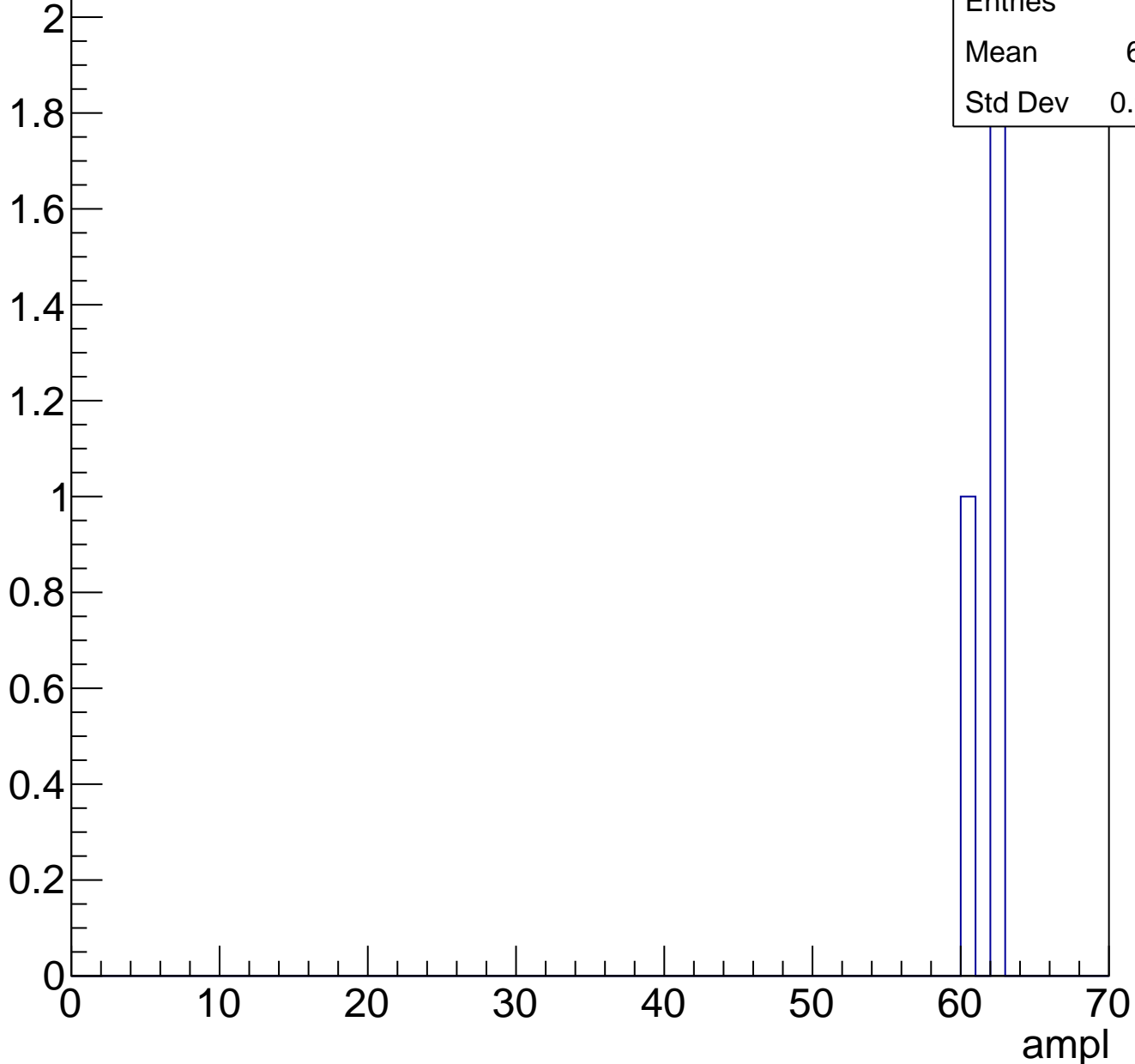
Entries	57
Mean	58.75
Std Dev	8.213



B0L002S, U17-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	61.33
Std Dev	0.9428

B0L002S, U17-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch66, adc0

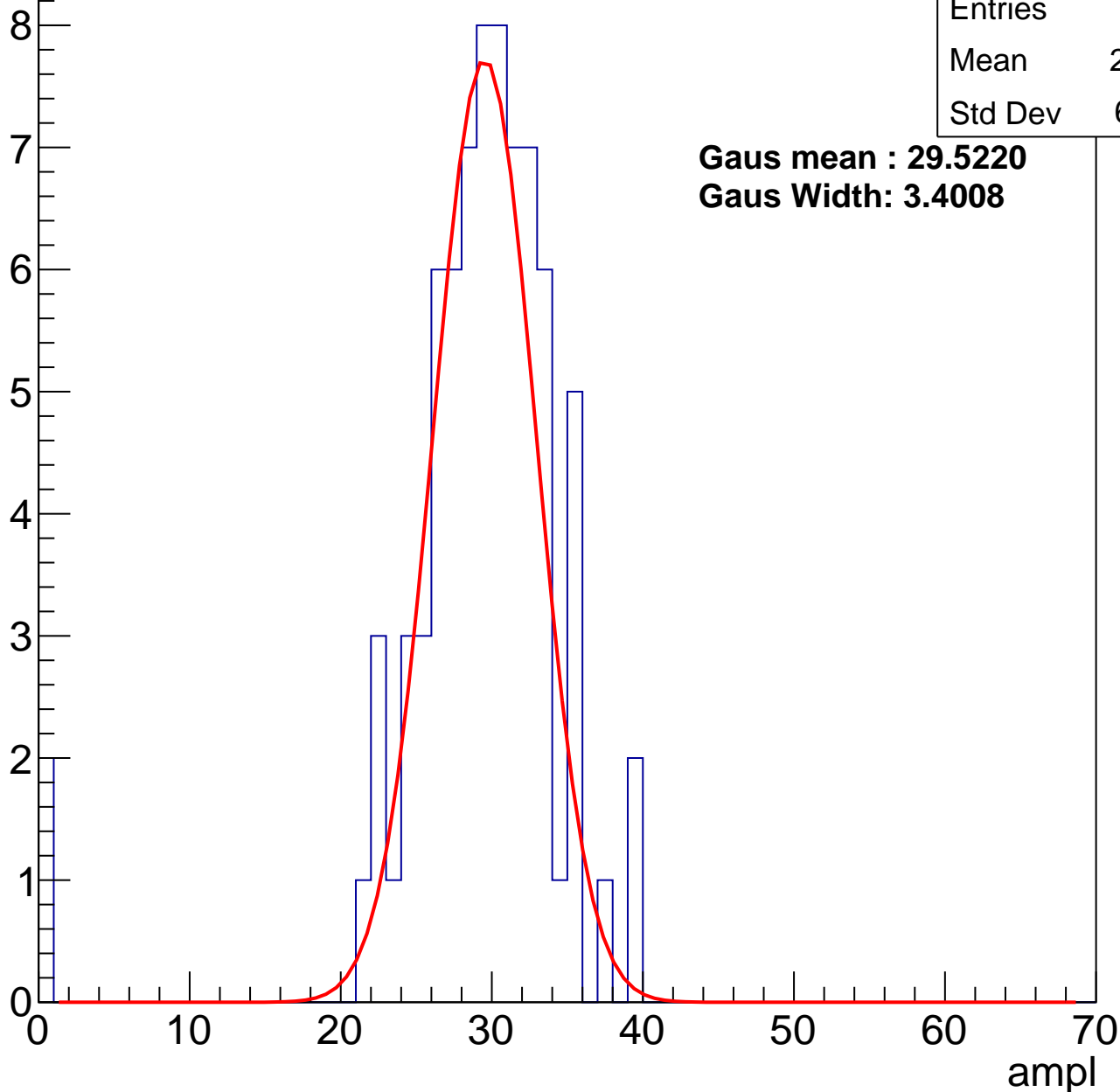
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	28.65
Std Dev	6.021

Gaus mean : 29.5220

Gaus Width: 3.4008



B0L002S, U17-ch66, adc1

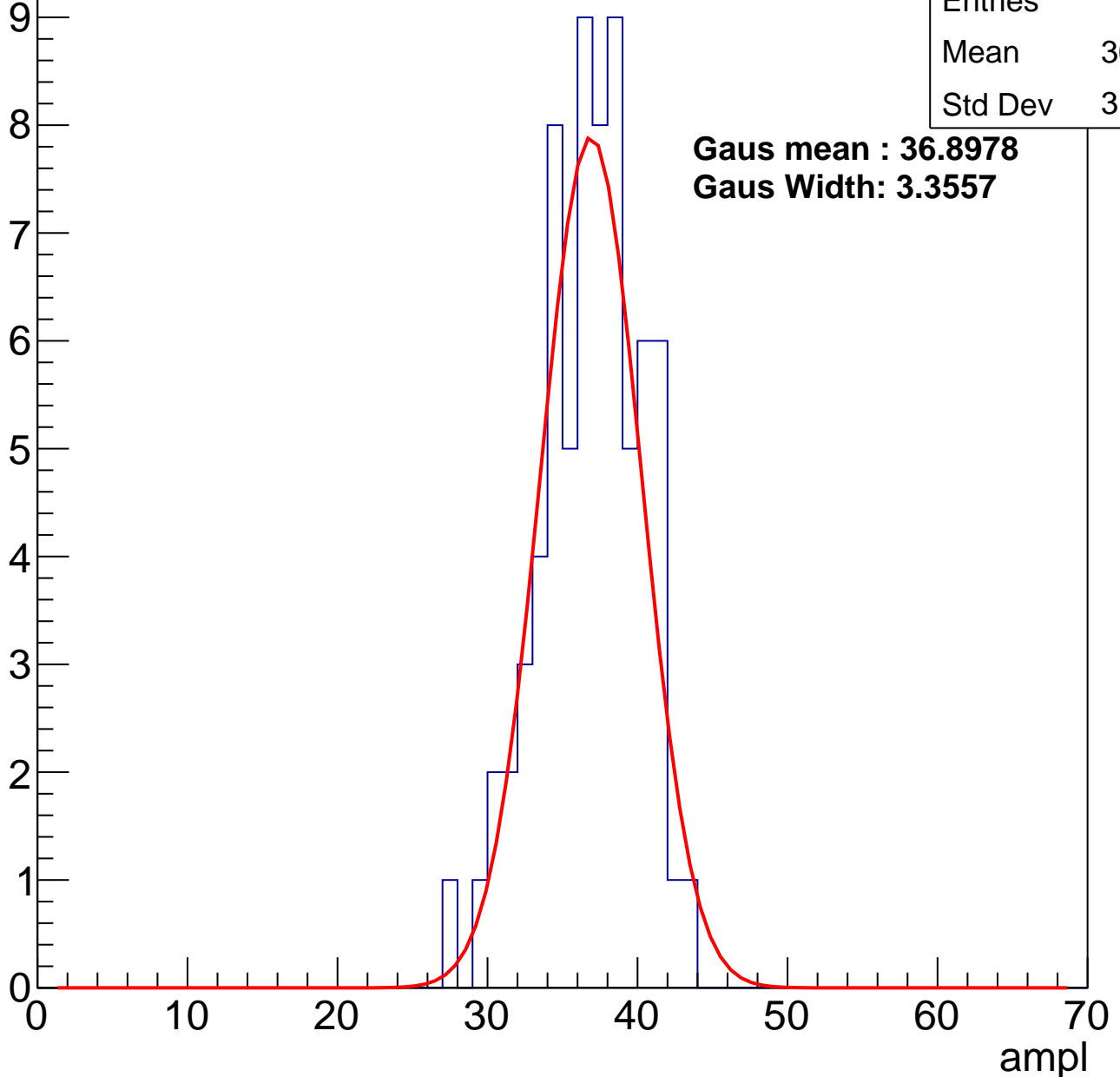
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.35
Std Dev	3.323

Gaus mean : 36.8978

Gaus Width: 3.3557



B0L002S, U17-ch66, adc2

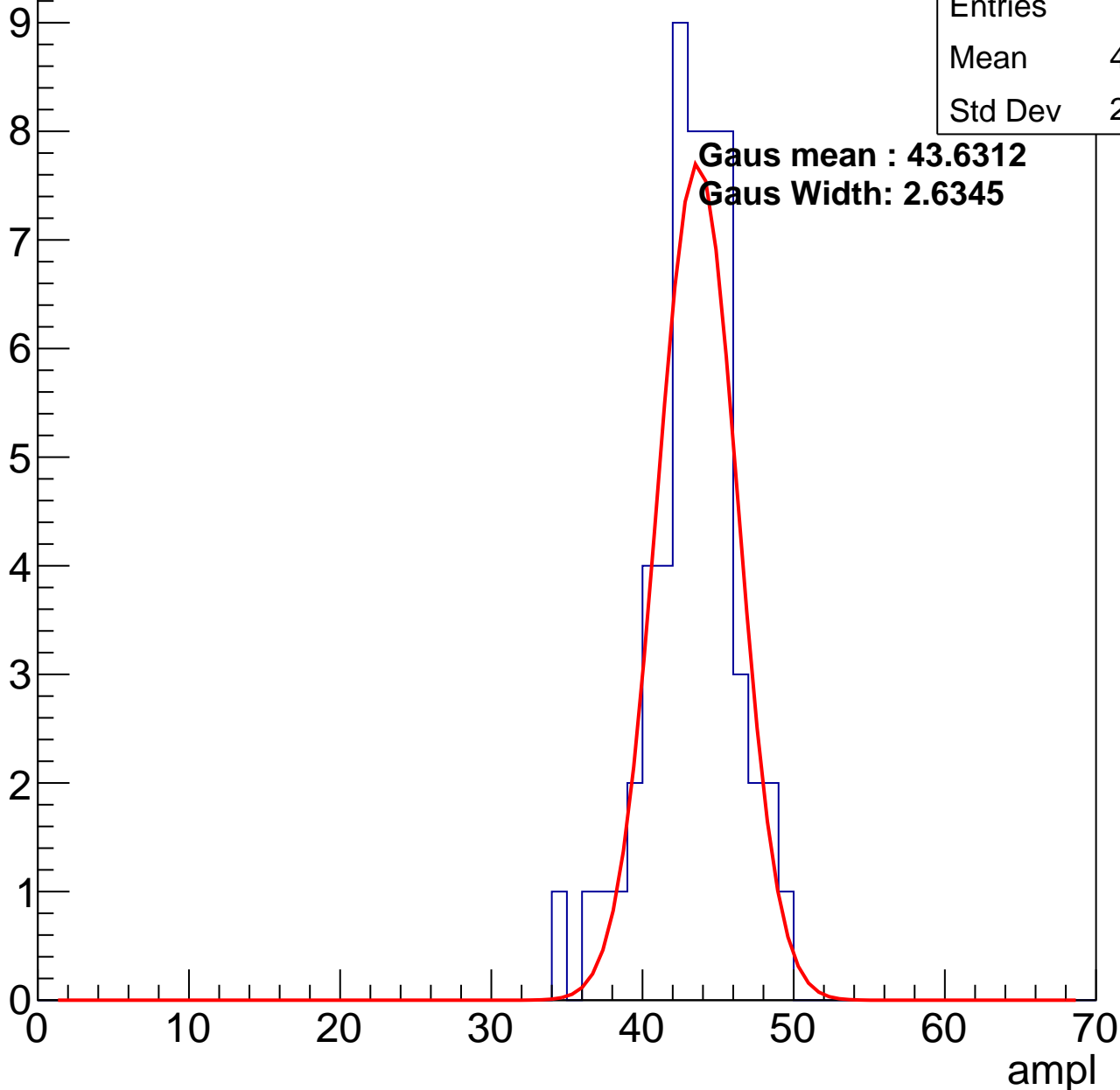
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	42.87
Std Dev	2.917

Gaus mean : 43.6312

Gaus Width: 2.6345

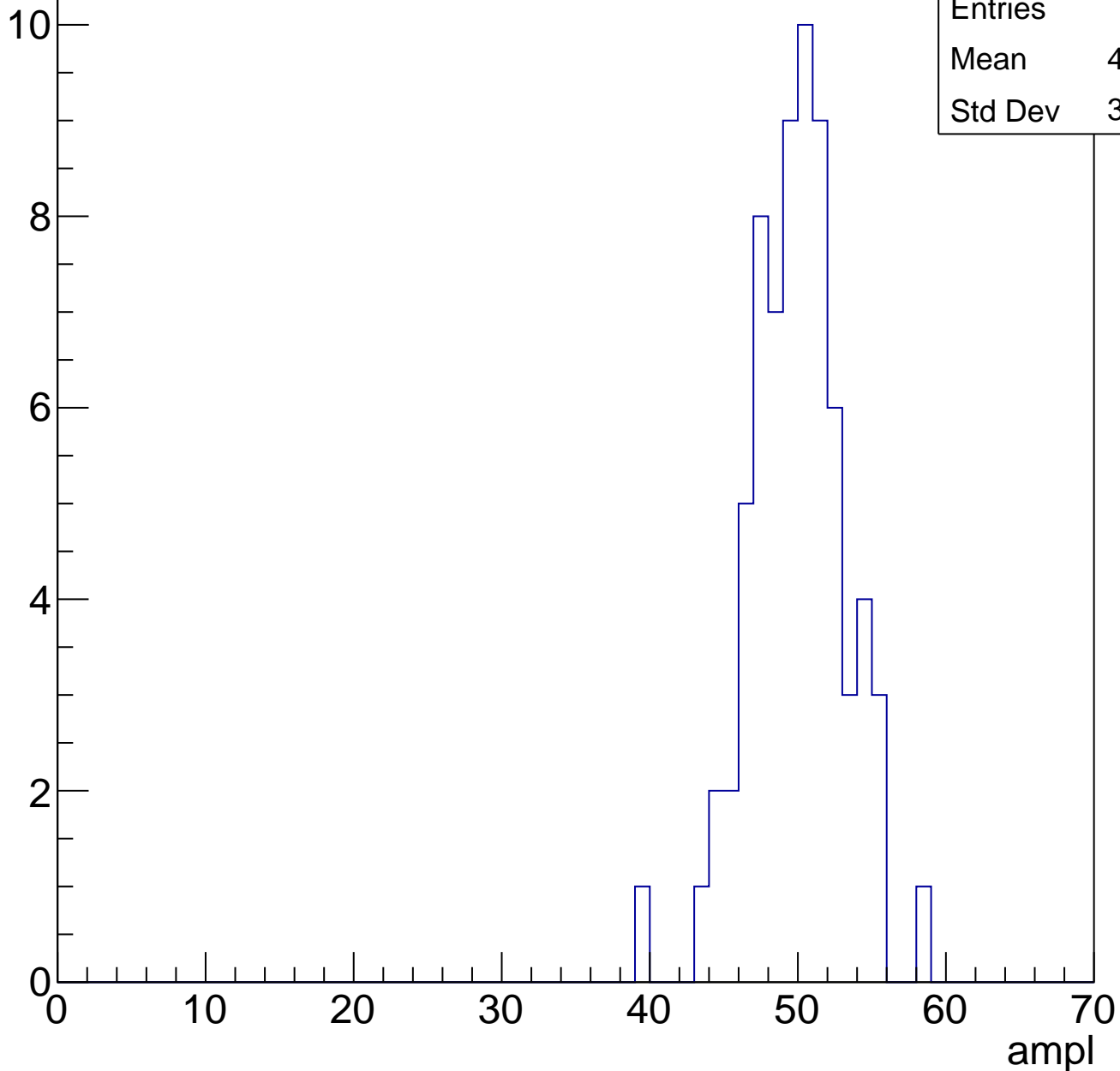


B0L002S, U17-ch66, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	49.46
Std Dev	3.193

Entry

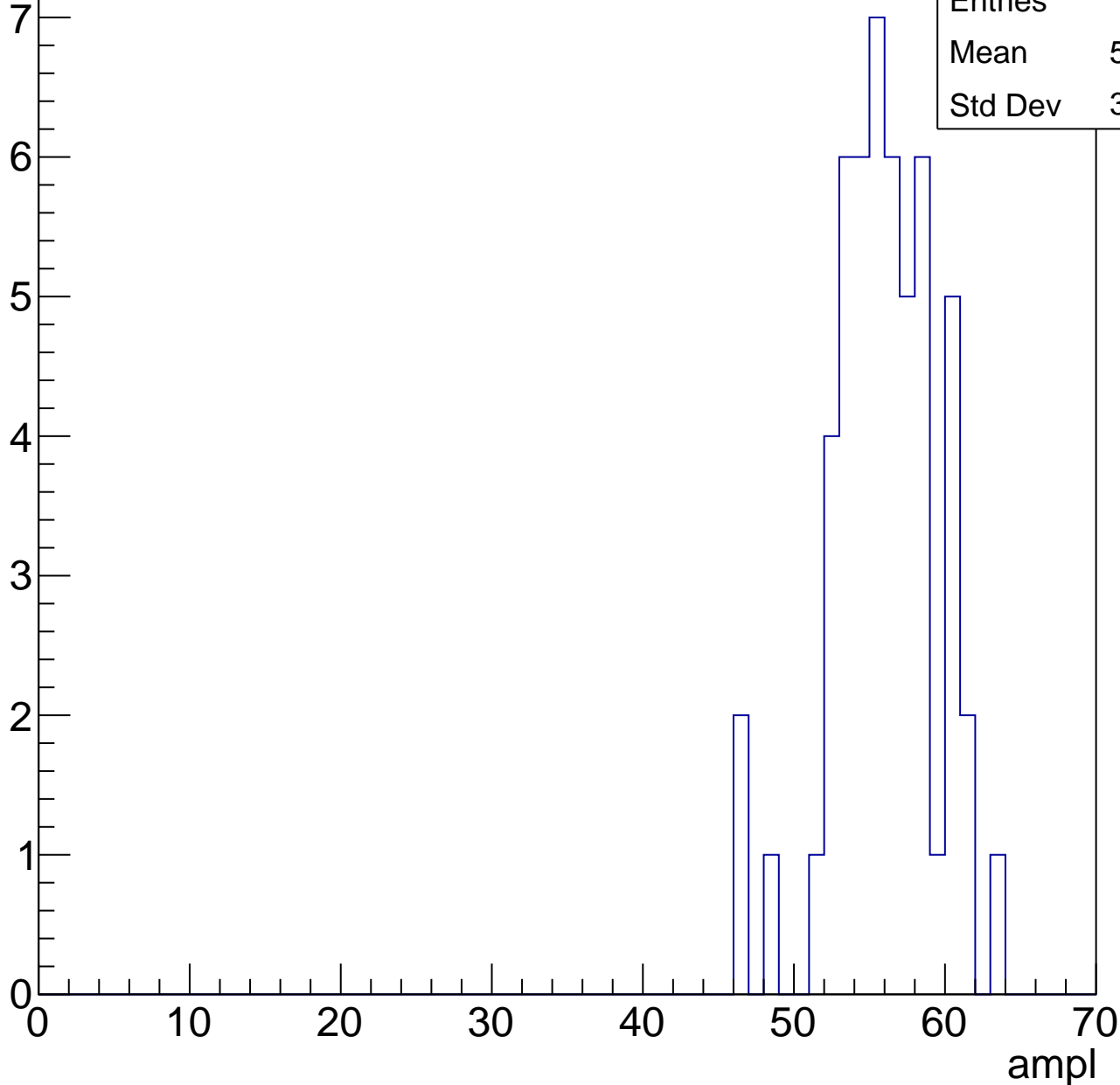


B0L002S, U17-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

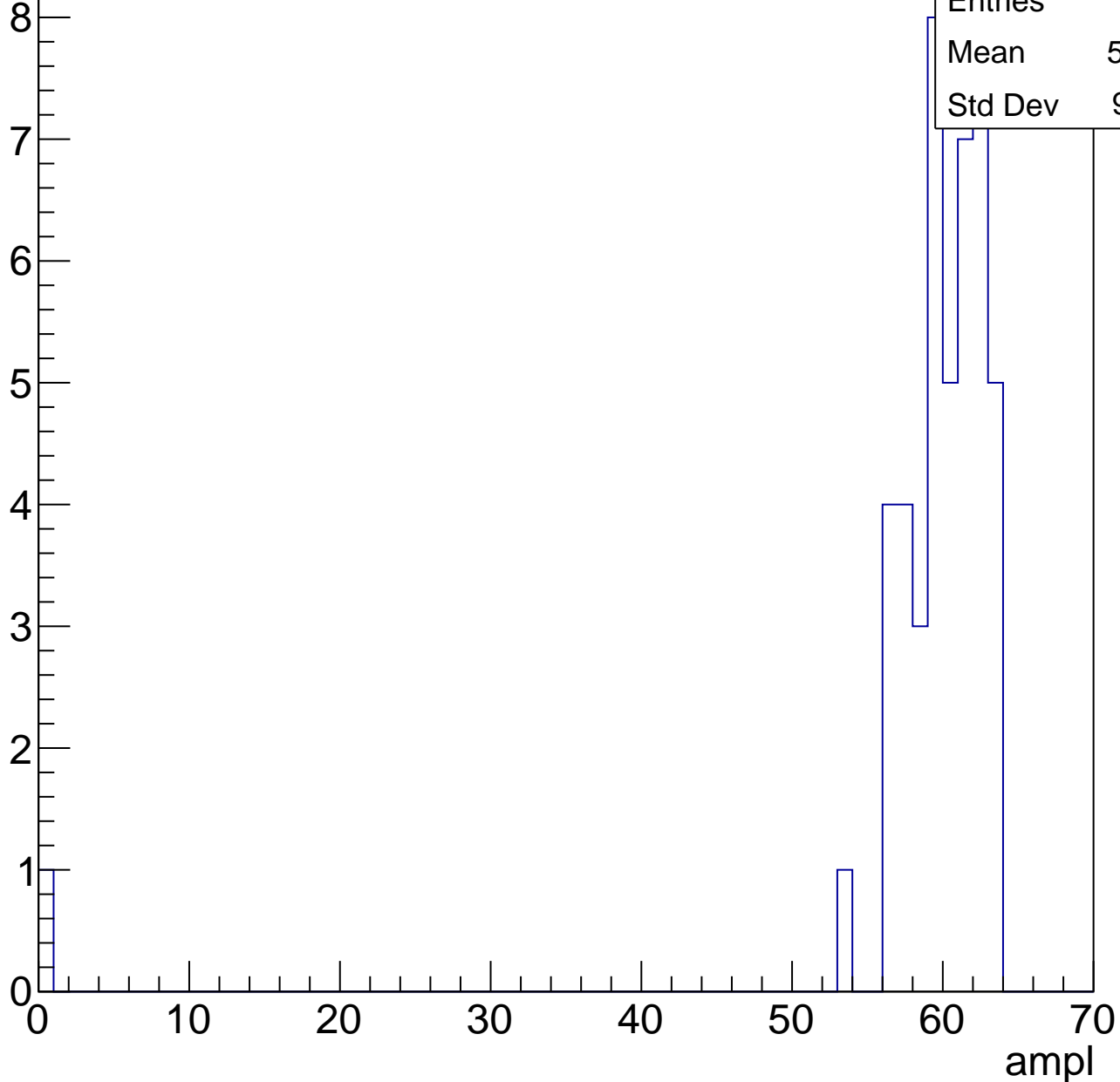
Entries	53
Mean	55.45
Std Dev	3.473



B0L002S, U17-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1

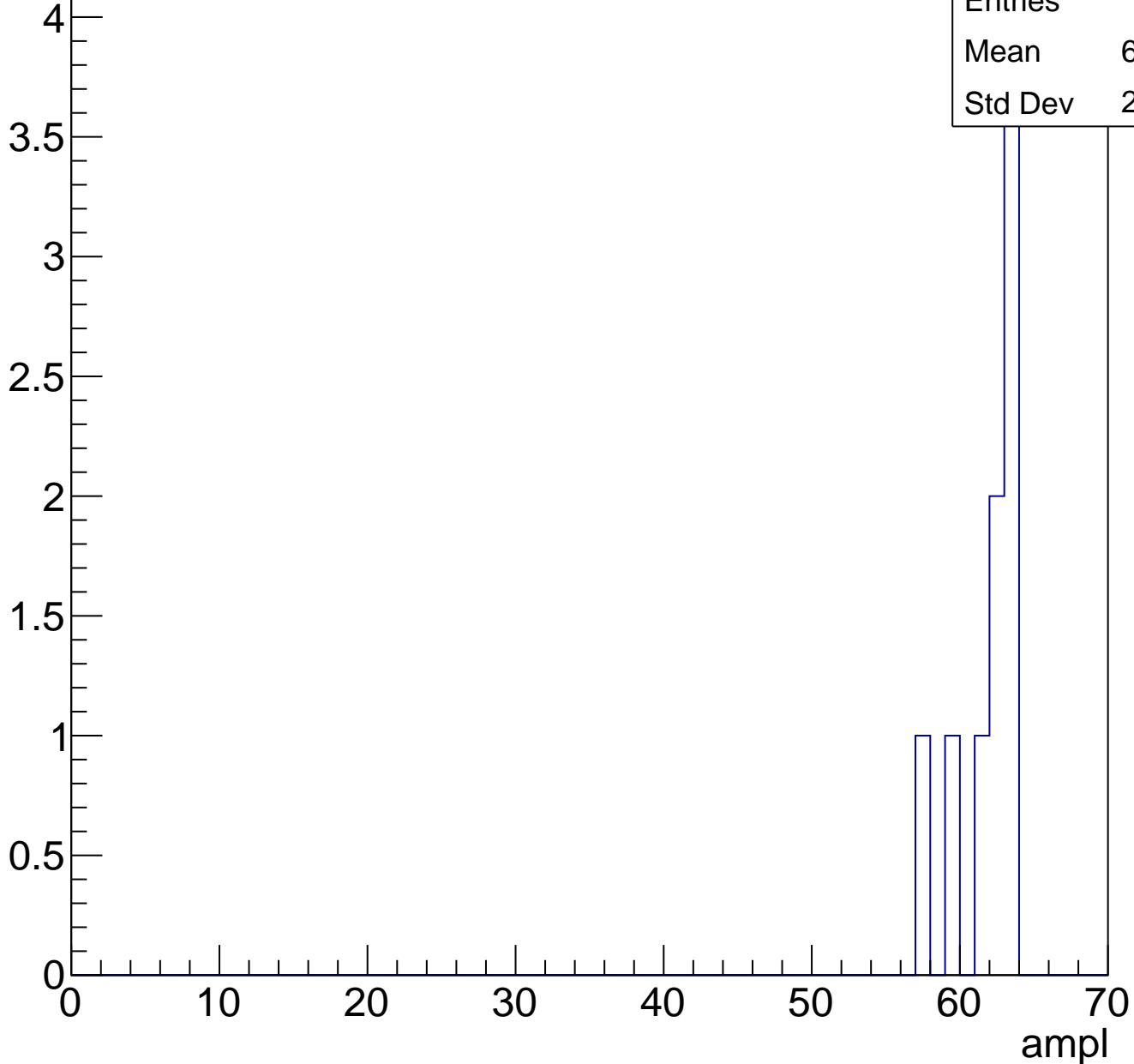
Entry



B0L002S, U17-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch67, adc0

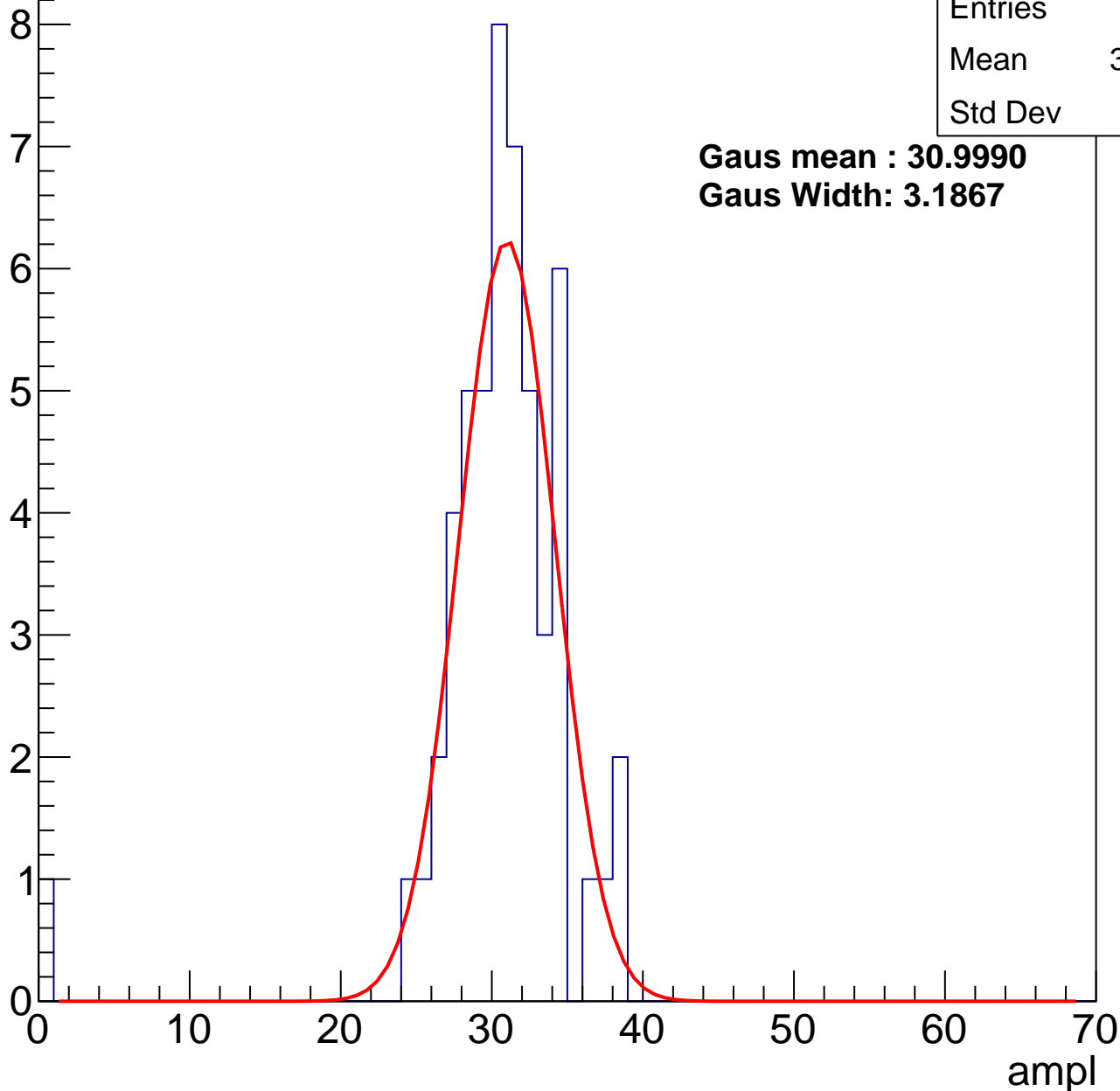
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	30.06
Std Dev	5.22

Gaus mean : 30.9990

Gaus Width: 3.1867



B0L002S, U17-ch67, adc1

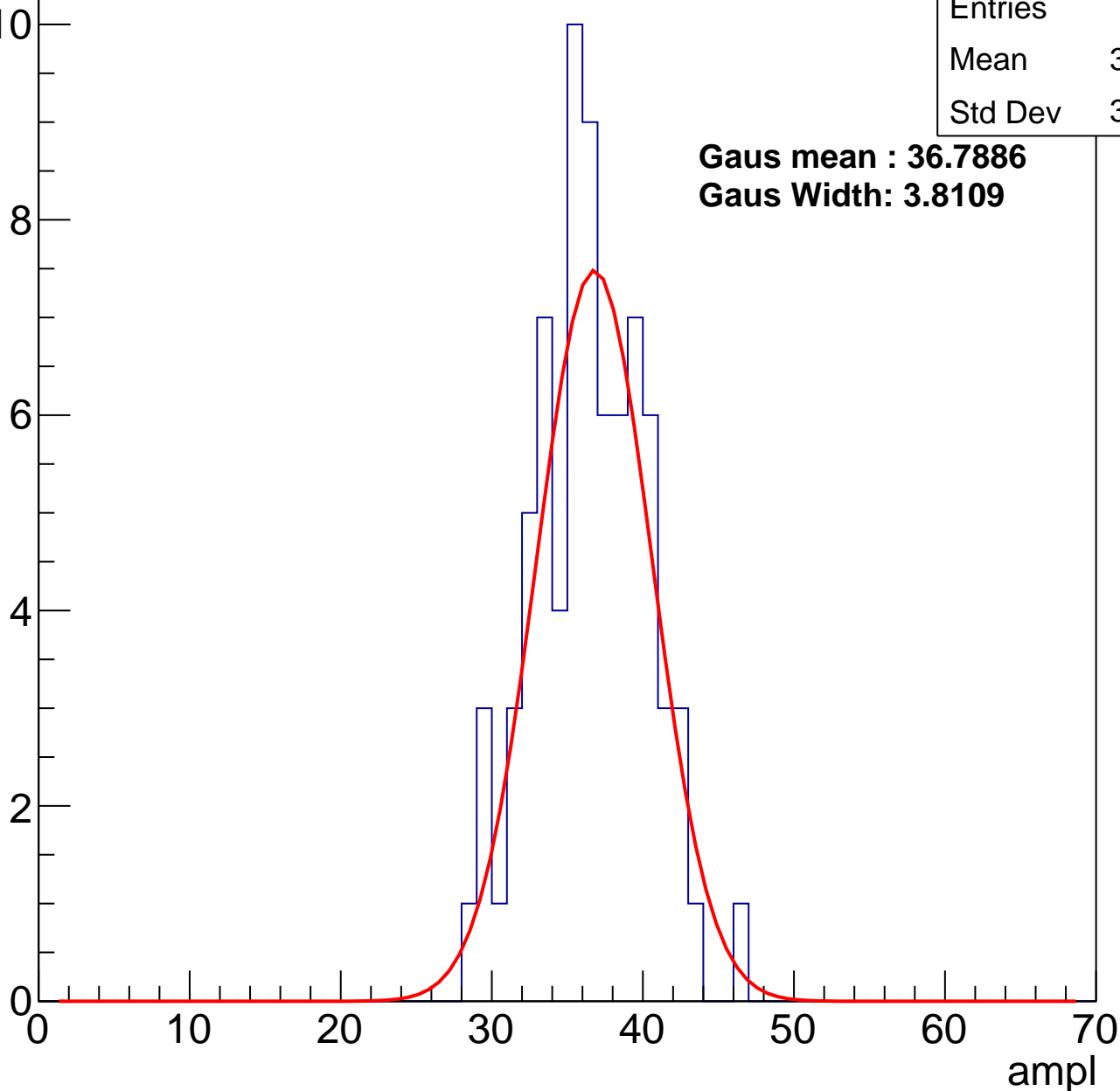
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.05
Std Dev	3.649

Gaus mean : 36.7886

Gaus Width: 3.8109



B0L002S, U17-ch67, adc2

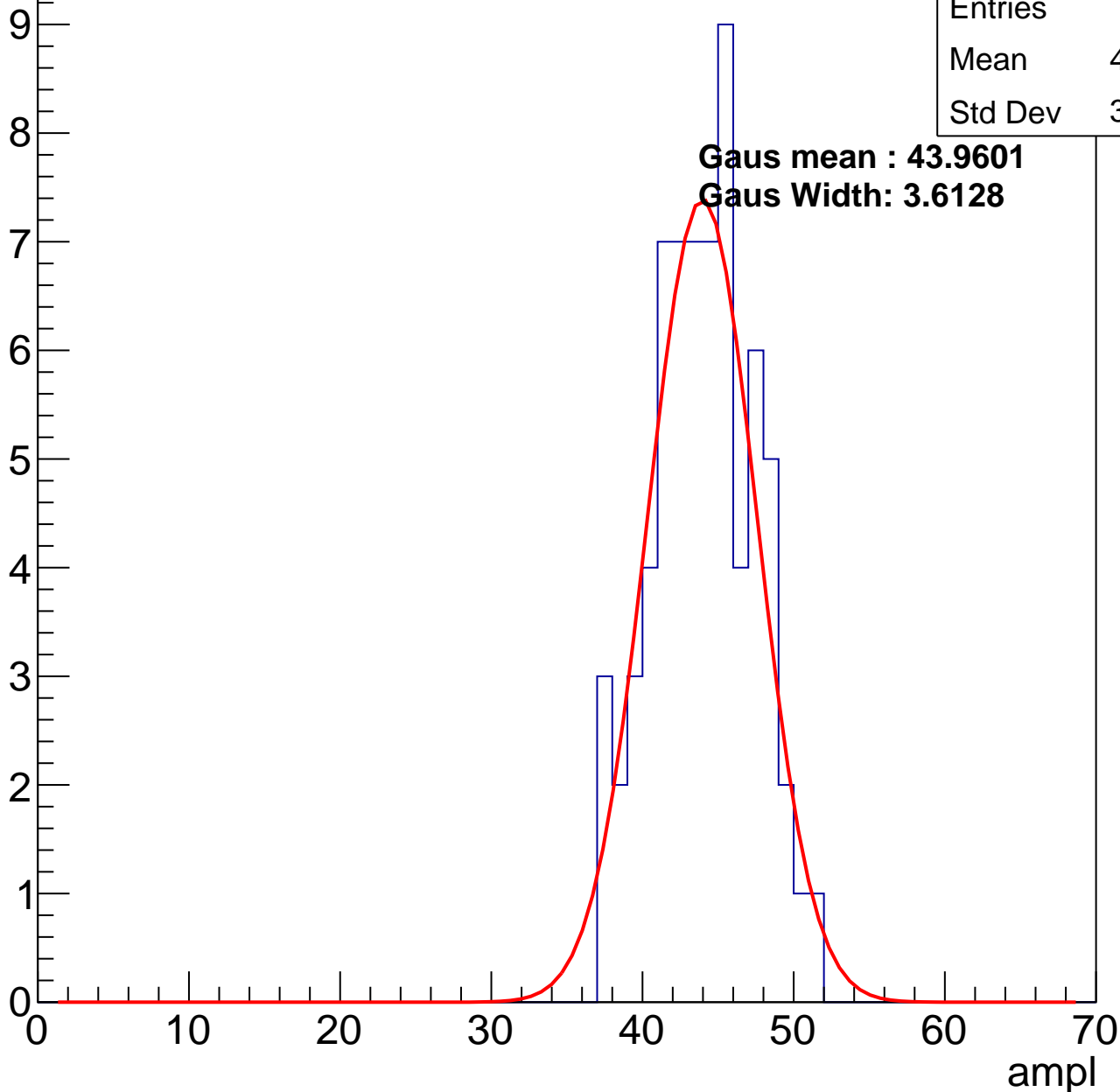
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.59
Std Dev	3.295

Gaus mean : 43.9601

Gaus Width: 3.6128

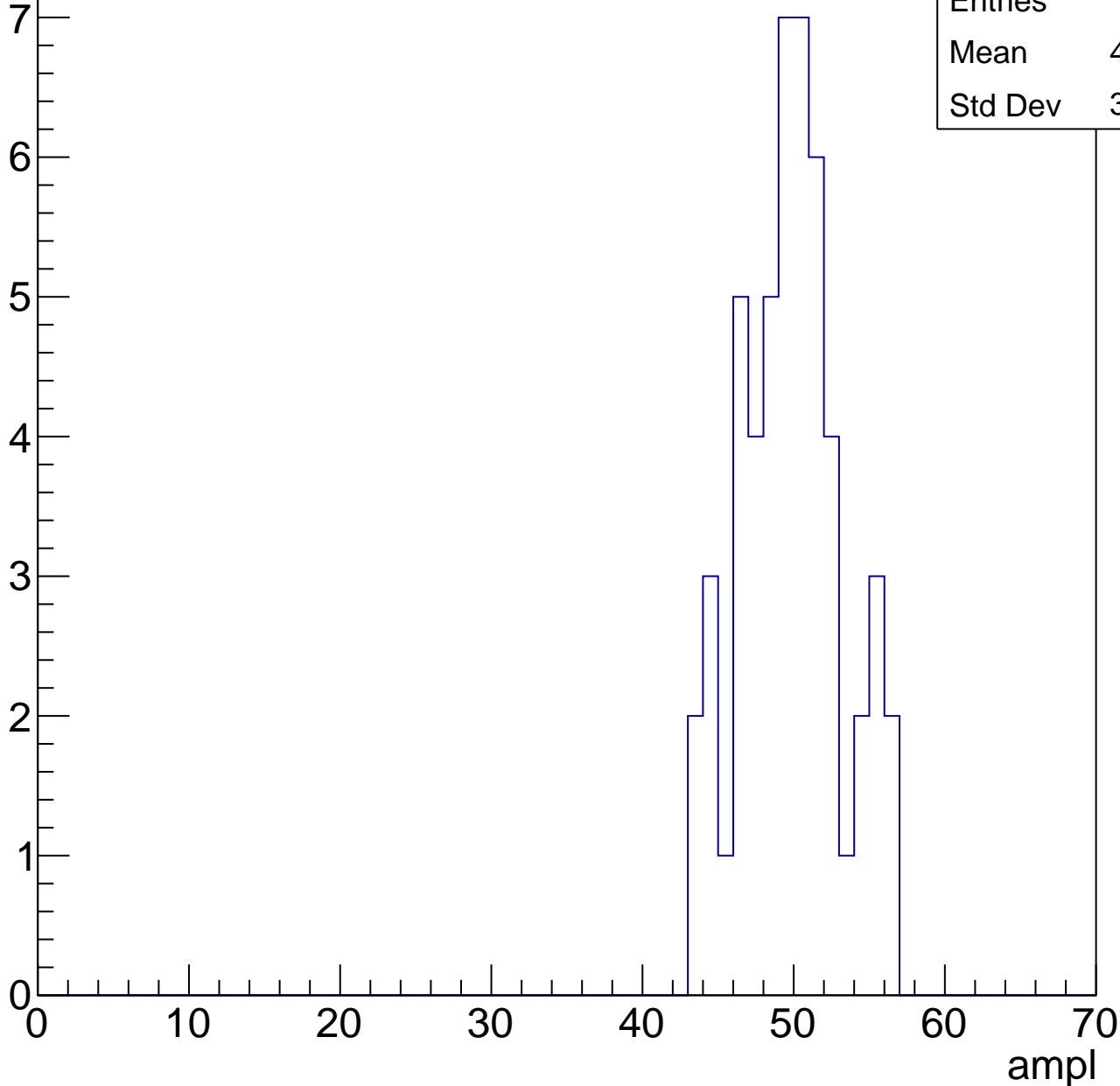


B0L002S, U17-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	49.35
Std Dev	3.287

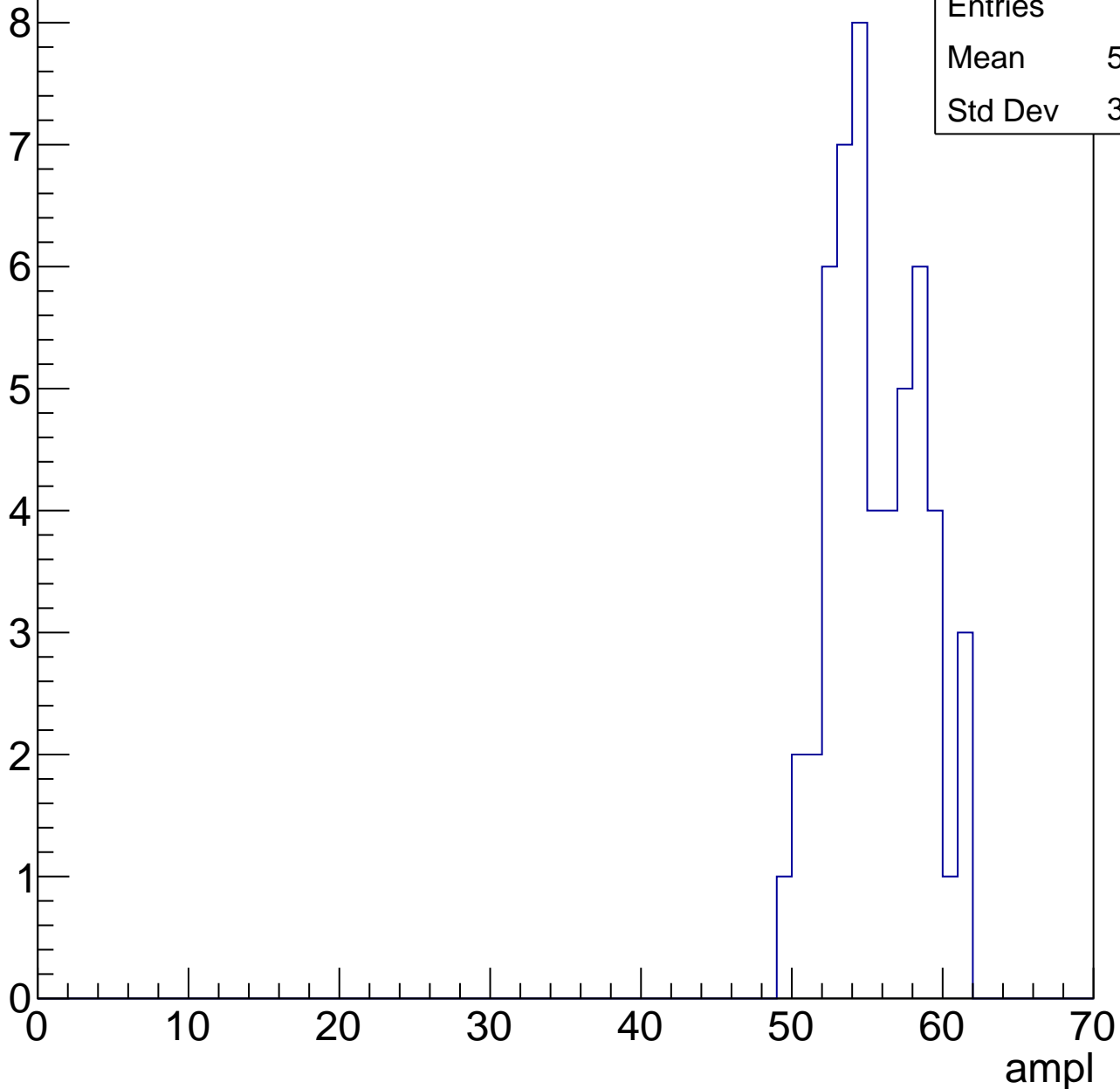


B0L002S, U17-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	55.13
Std Dev	3.016

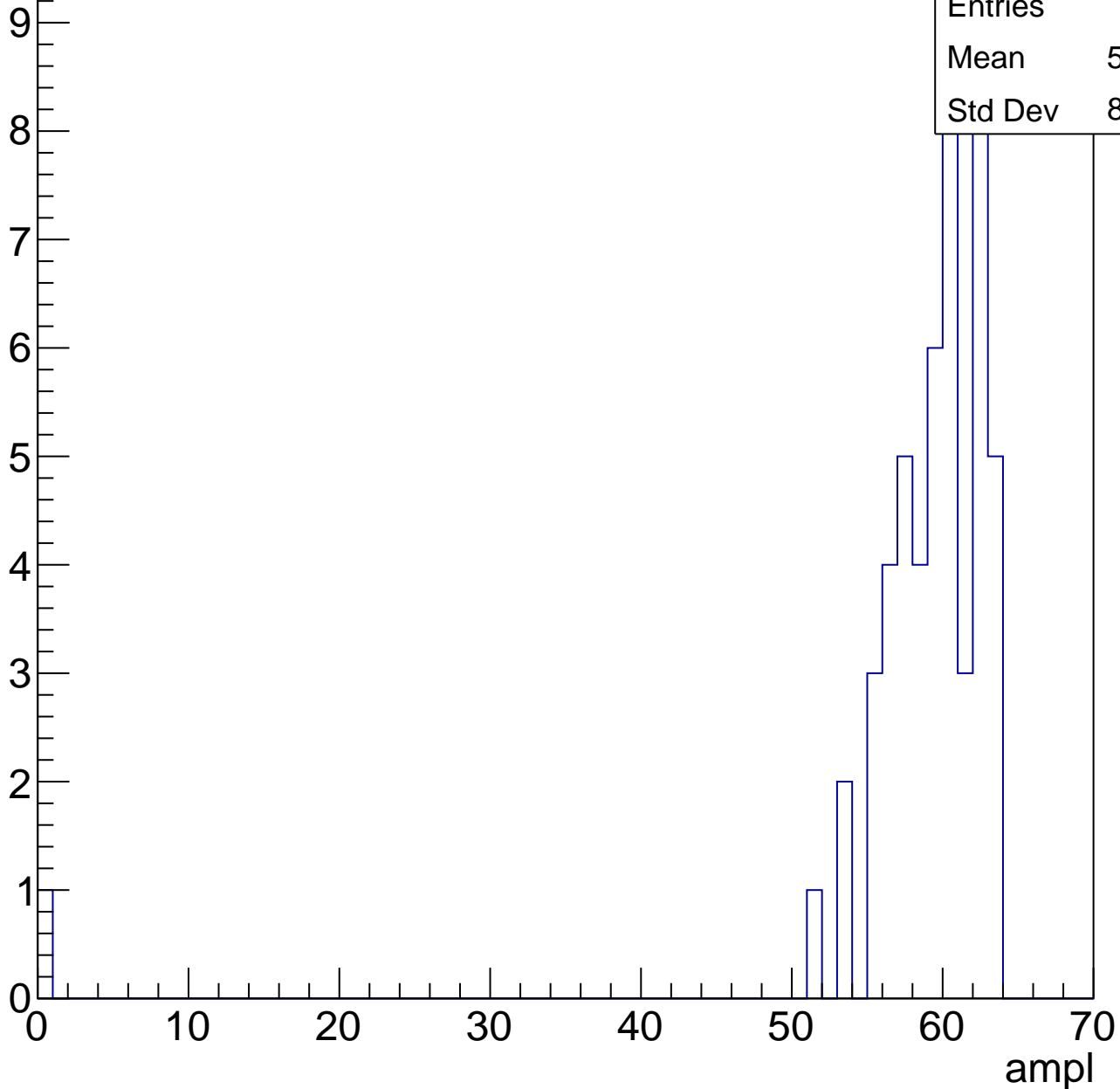


B0L002S, U17-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

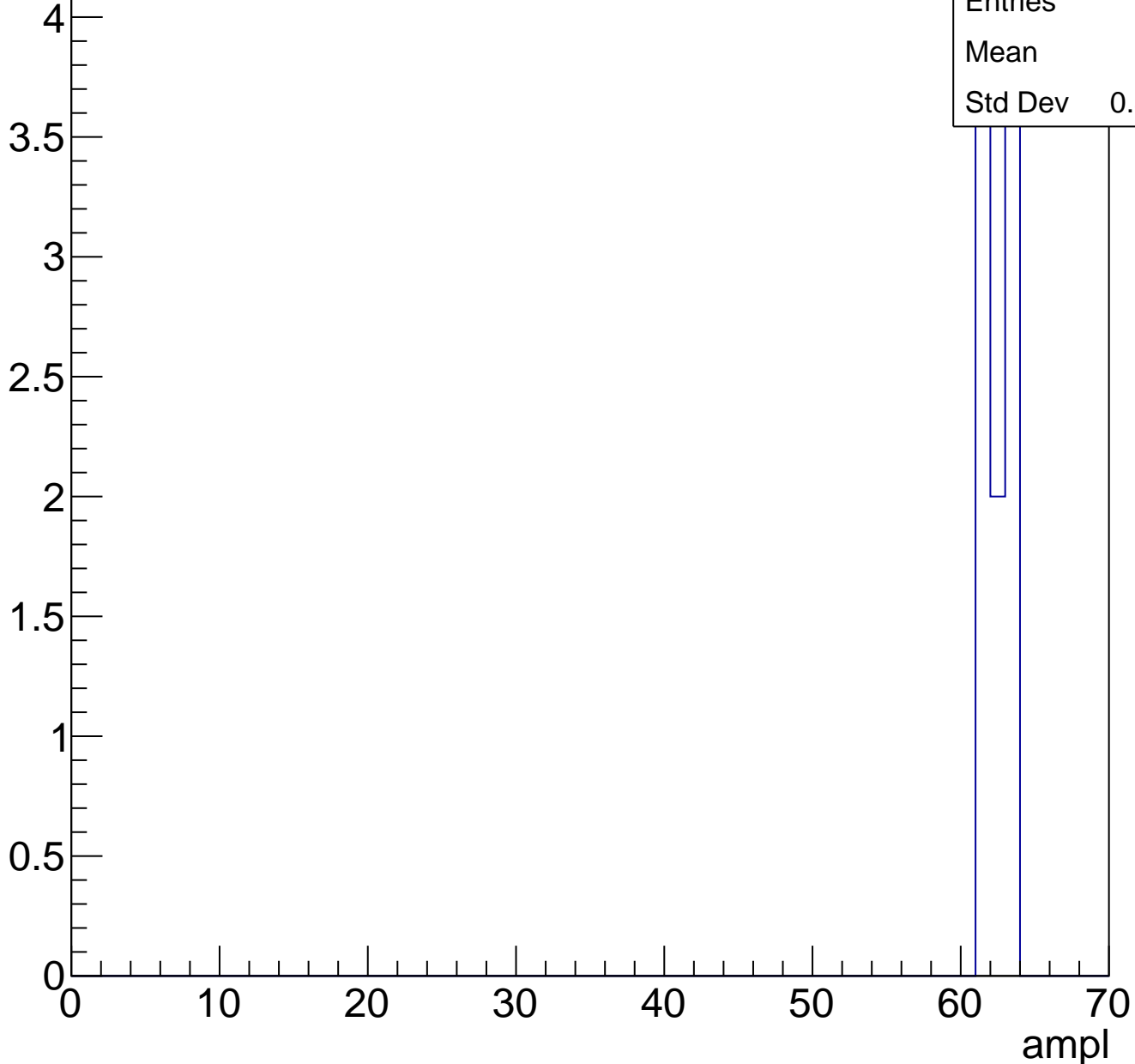
Entries	51
Mean	57.86
Std Dev	8.668



B0L002S, U17-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U17-ch68, adc0

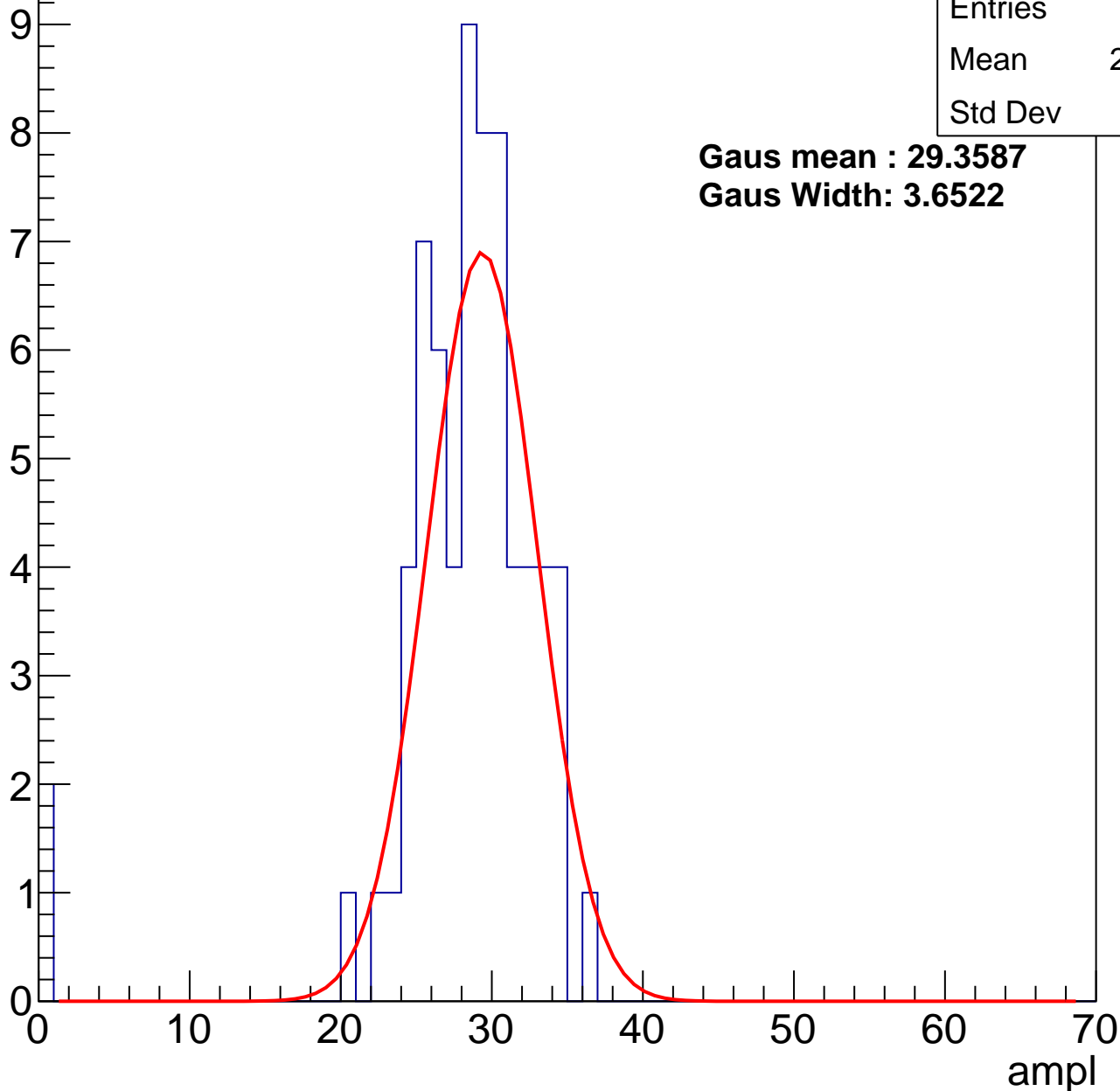
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	27.65
Std Dev	5.8

Gaus mean : 29.3587

Gaus Width: 3.6522



B0L002S, U17-ch68, adc1

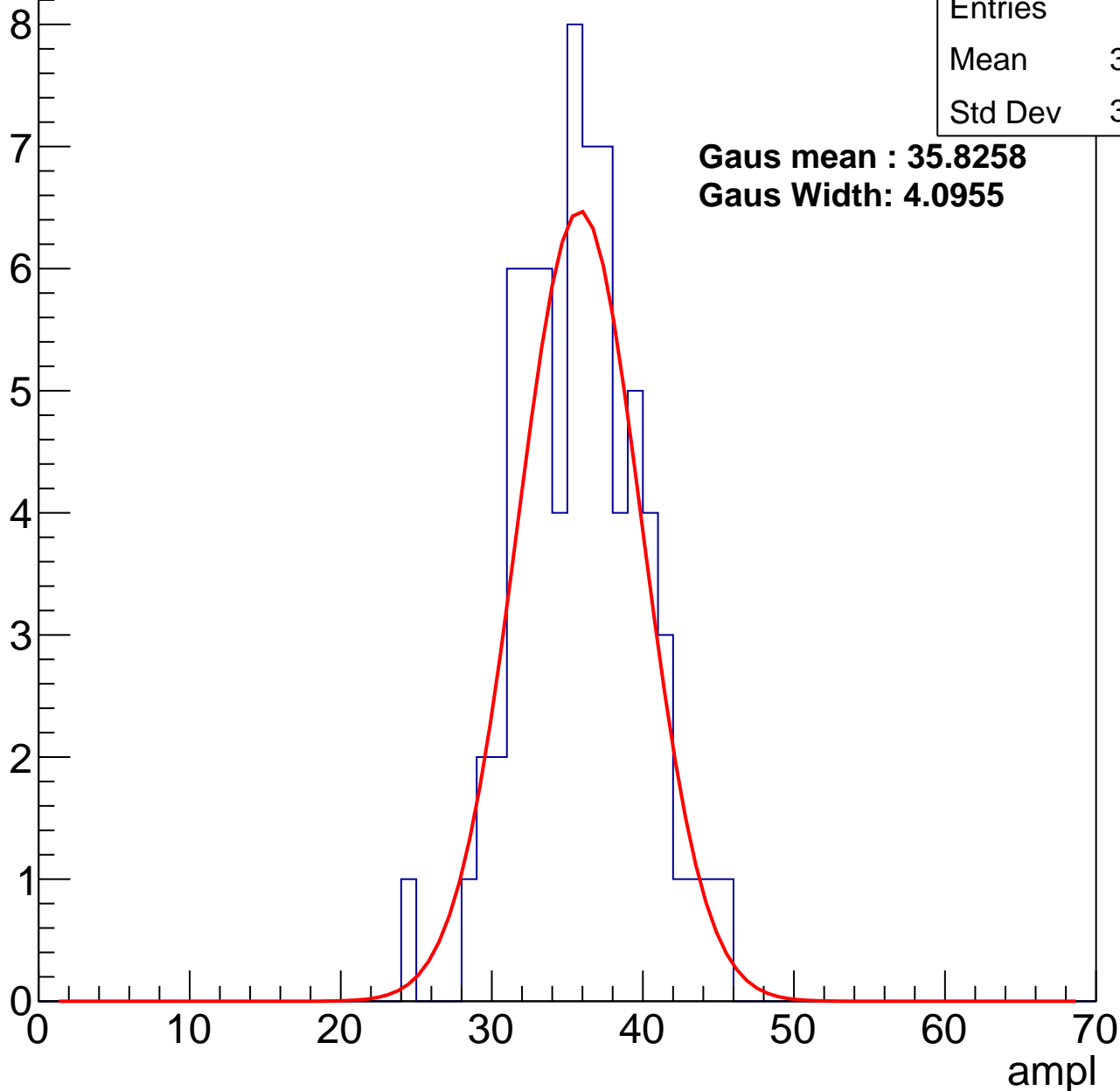
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	35.39
Std Dev	3.987

Gaus mean : 35.8258

Gaus Width: 4.0955



B0L002S, U17-ch68, adc2

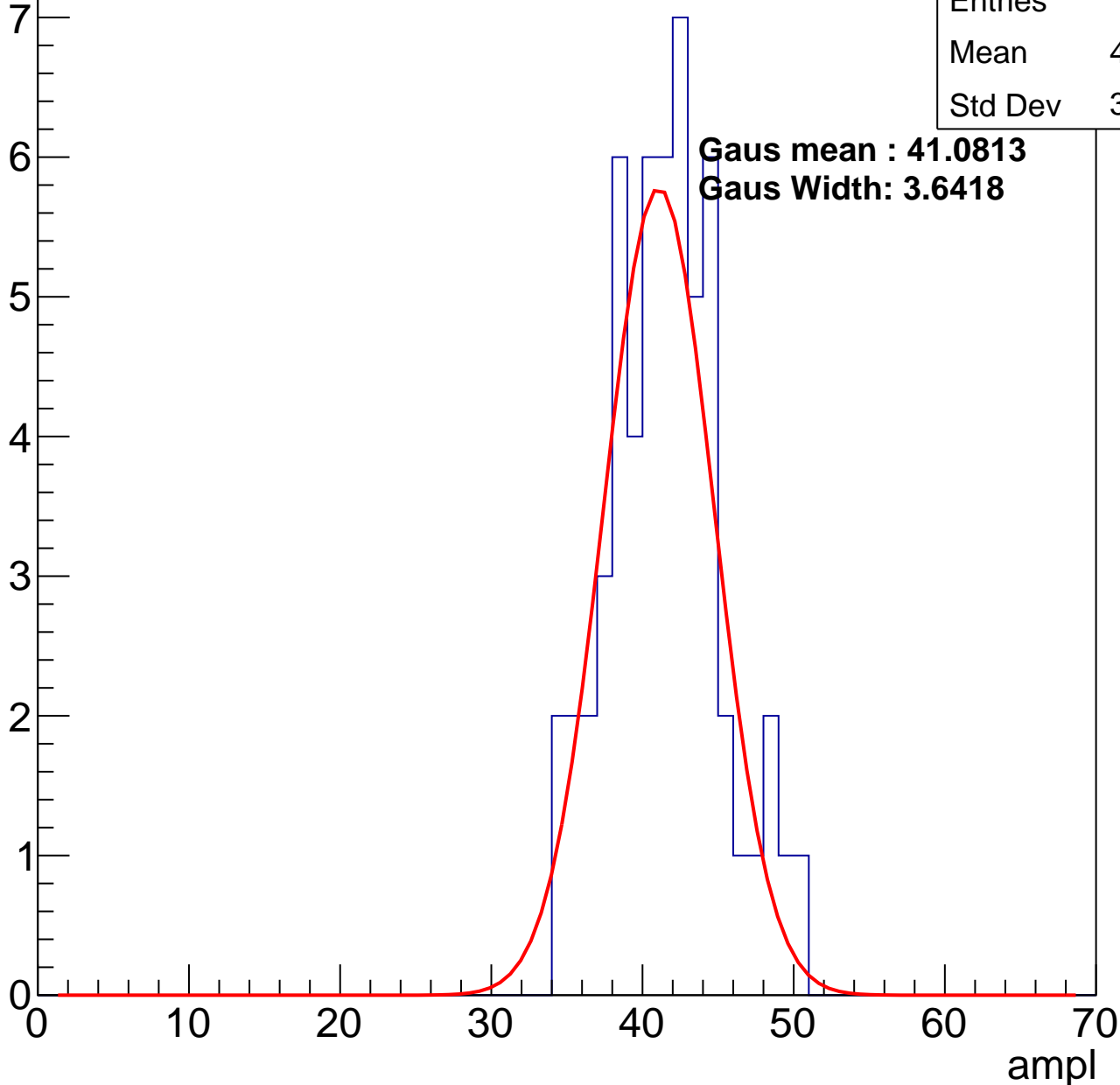
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	41.09
Std Dev	3.653

Gaus mean : 41.0813

Gaus Width: 3.6418

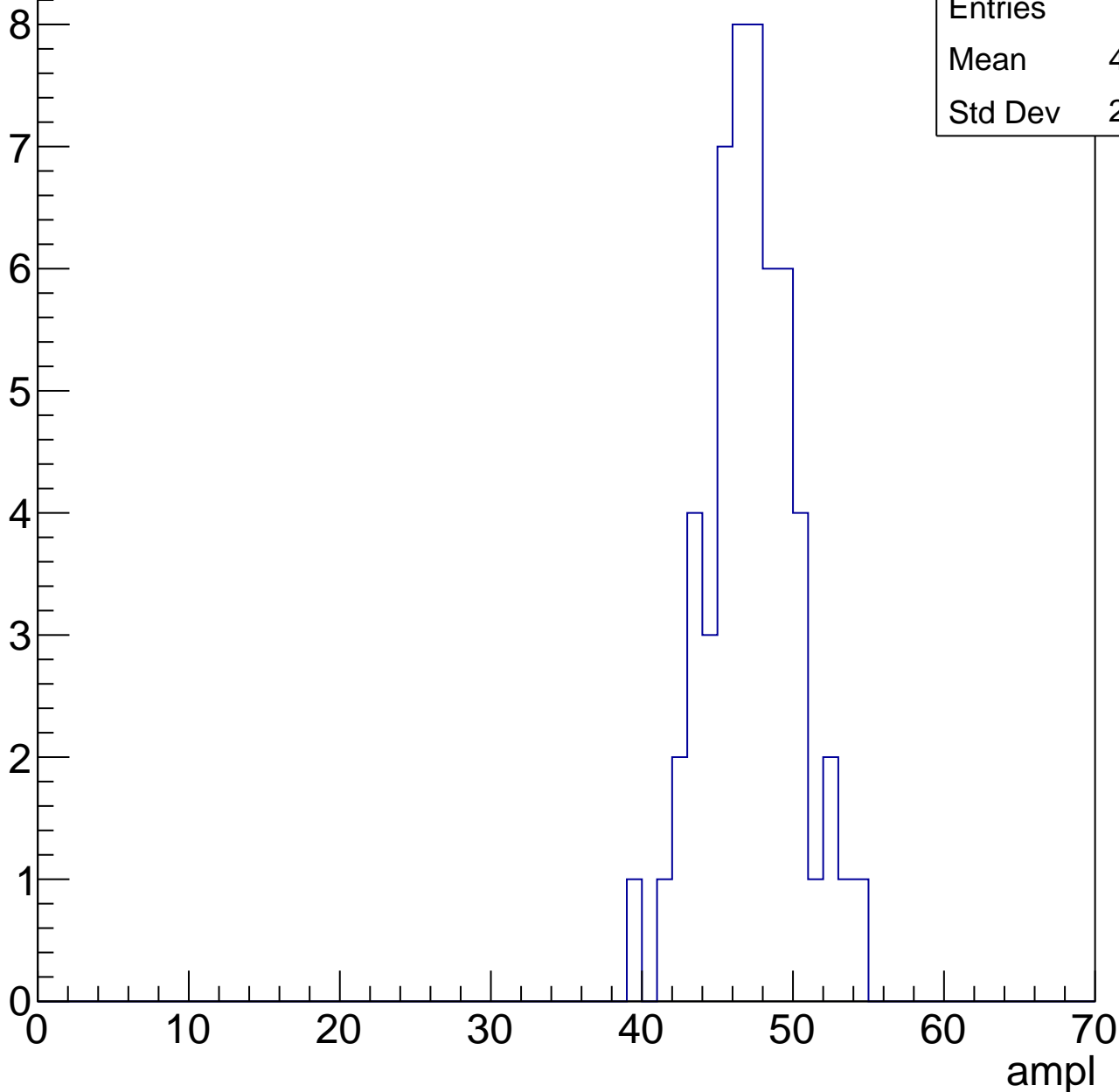


B0L002S, U17-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	46.75
Std Dev	2.986

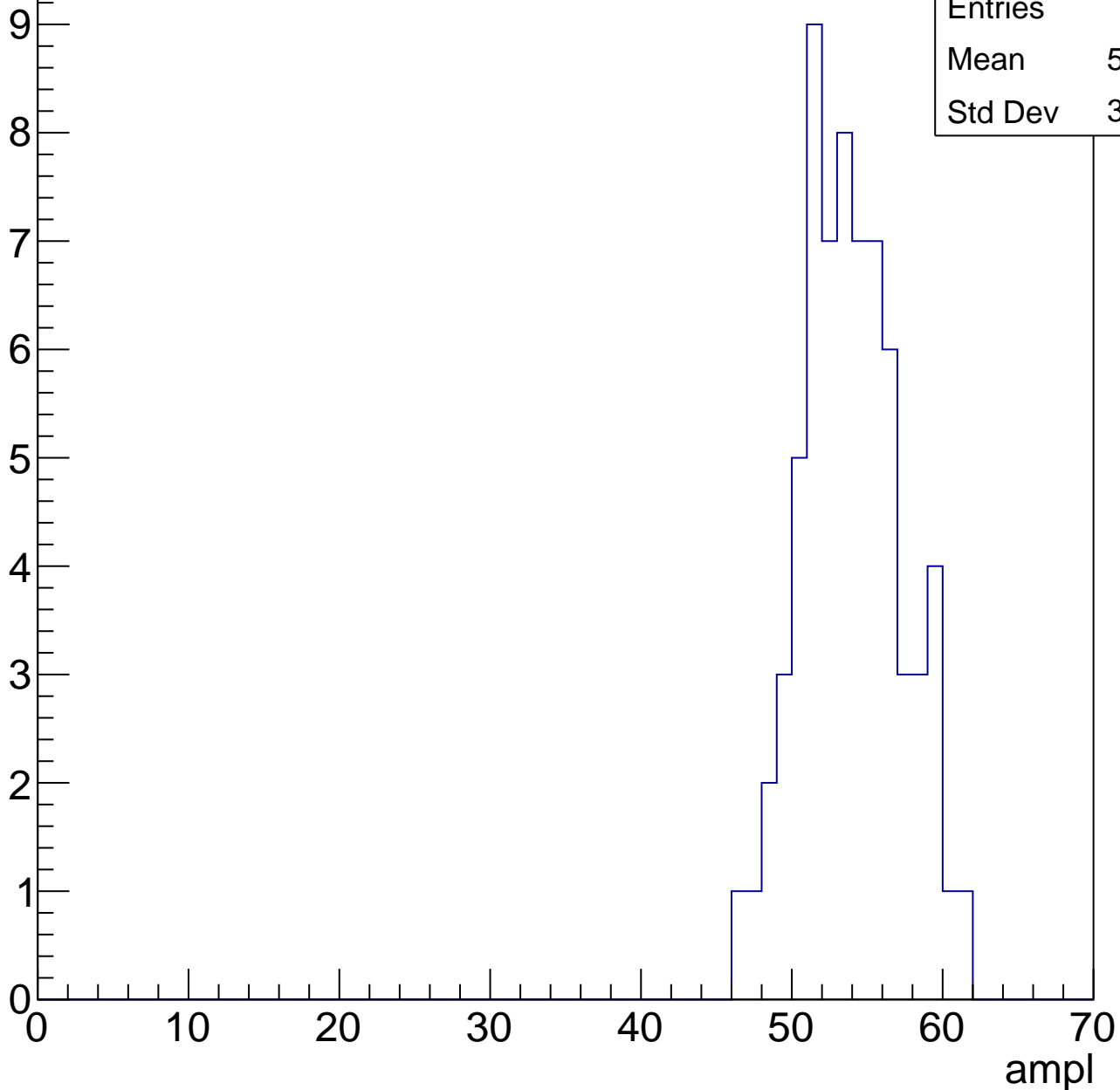


B0L002S, U17-ch68, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

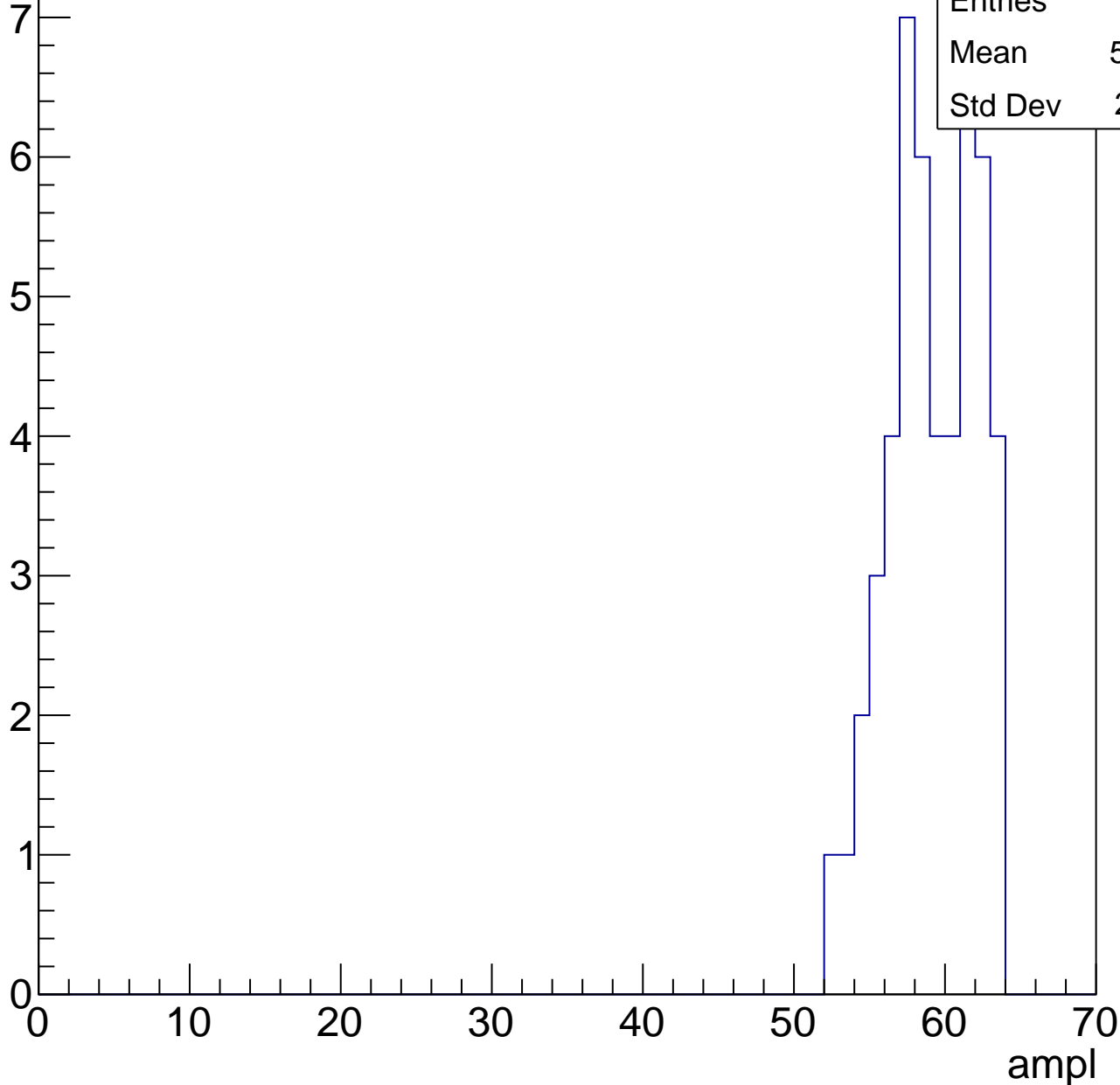
Entries	68
Mean	53.44
Std Dev	3.269



B0L002S, U17-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	49
Mean	58.69
Std Dev	2.851

B0L002S, U17-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

5

4

3

2

1

0

Entries	20
Mean	58.05
Std Dev	13.4

ampl

B0L002S, U17-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch69, adc0

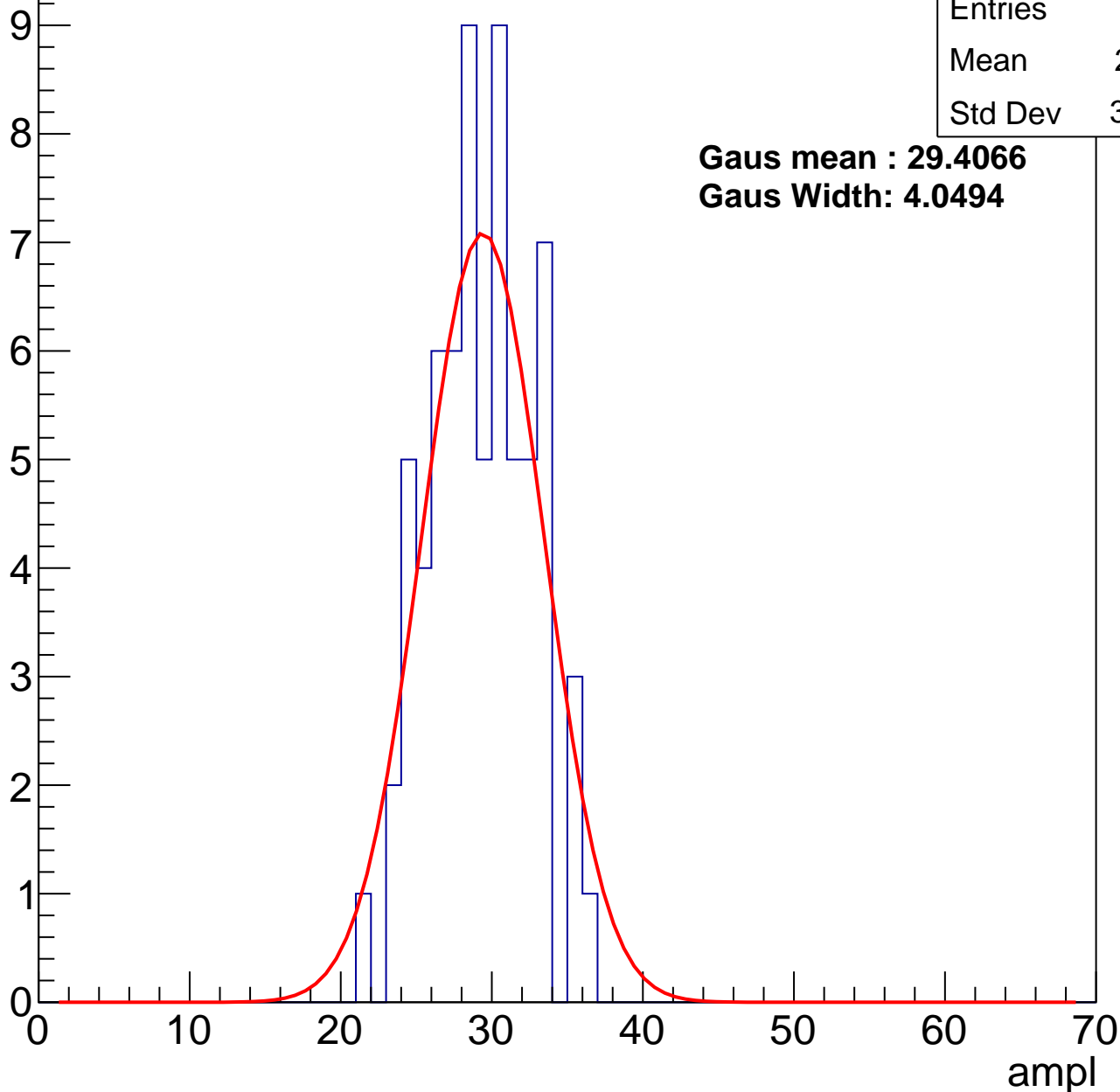
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.81
Std Dev	3.327

Gaus mean : 29.4066

Gaus Width: 4.0494



B0L002S, U17-ch69, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	36.36
Std Dev	3.198

Gaus mean : 36.9264

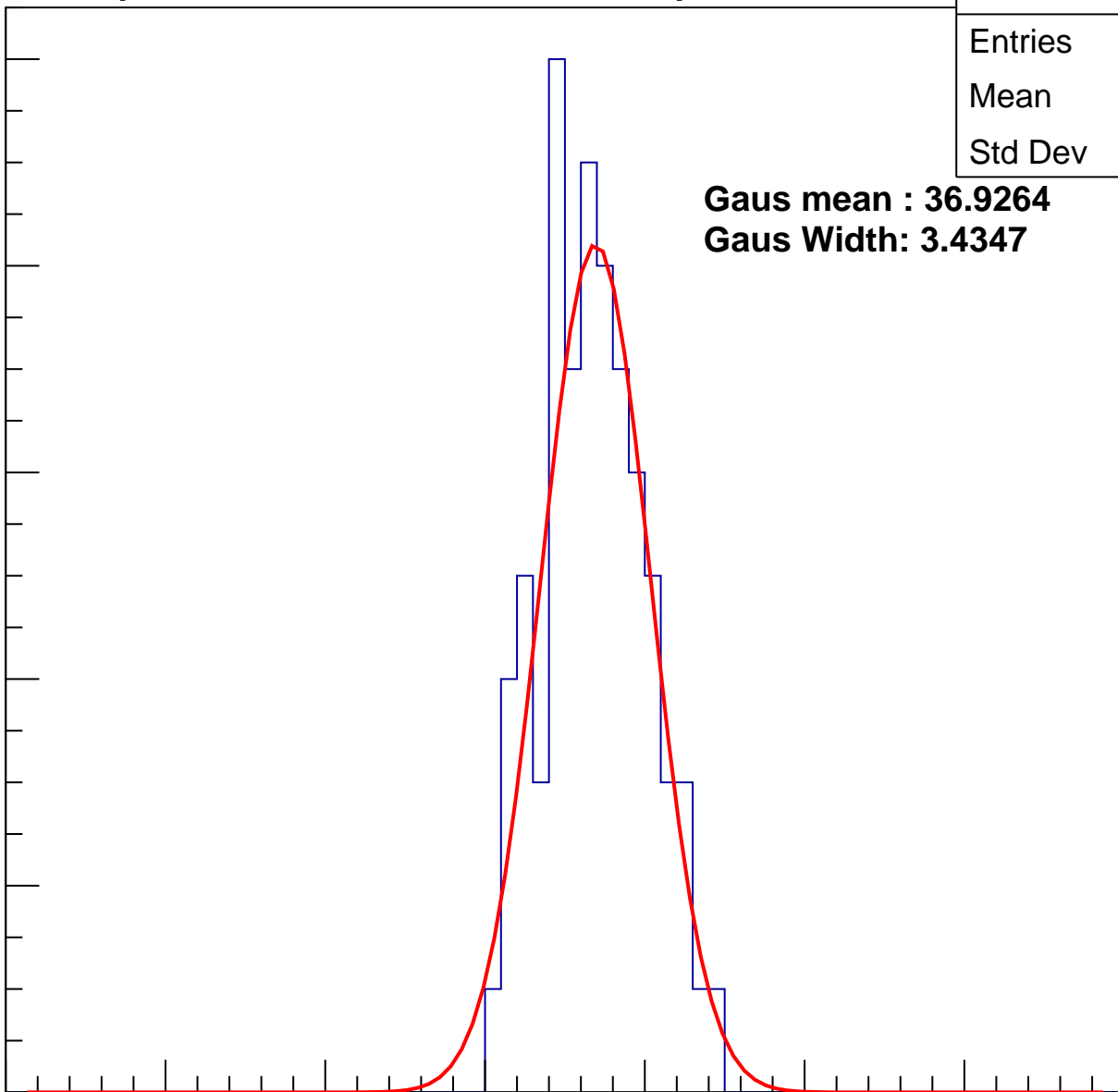
Gaus Width: 3.4347

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U17-ch69, adc2

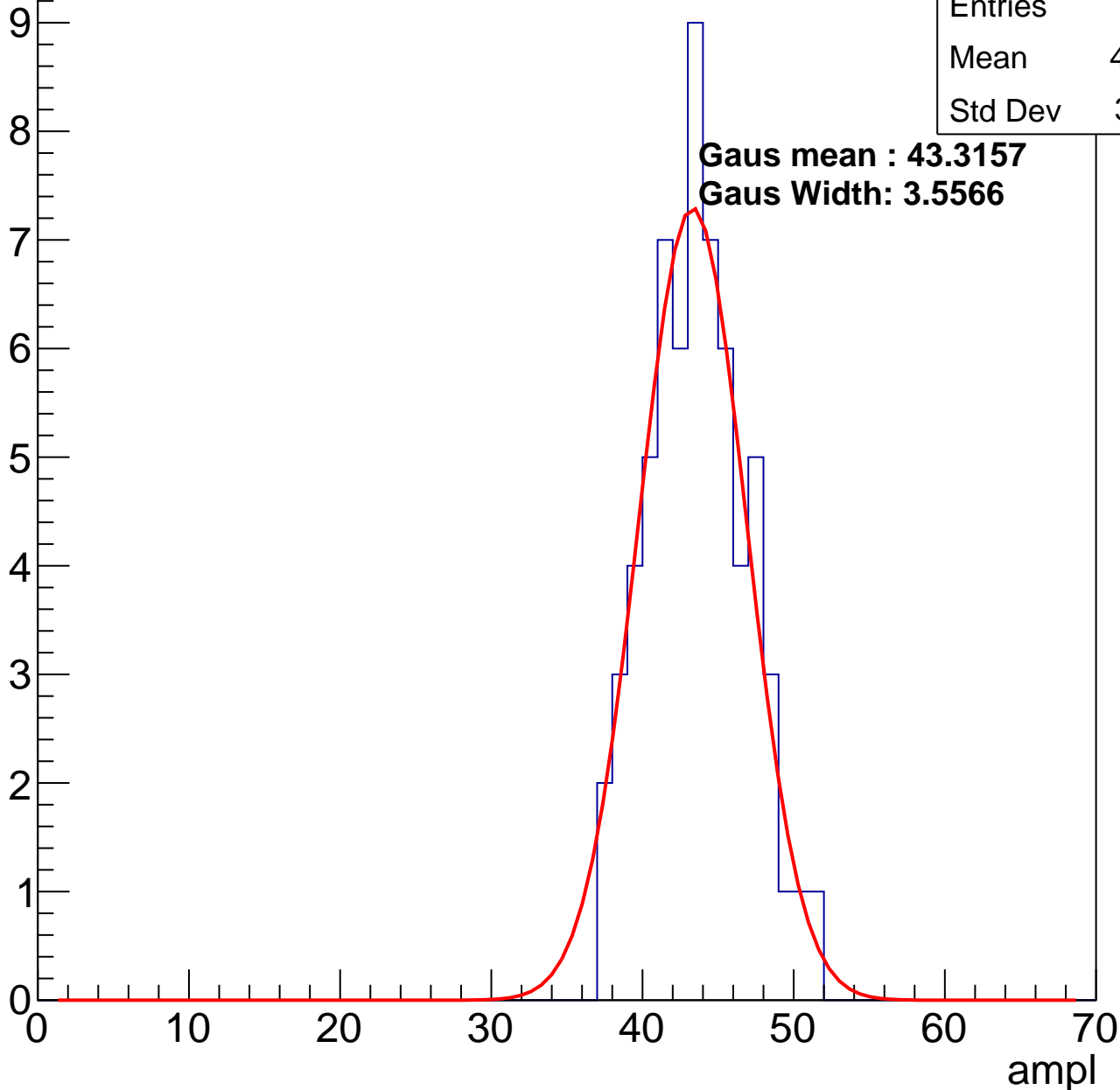
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.14
Std Dev	3.211

Gaus mean : 43.3157

Gaus Width: 3.5566

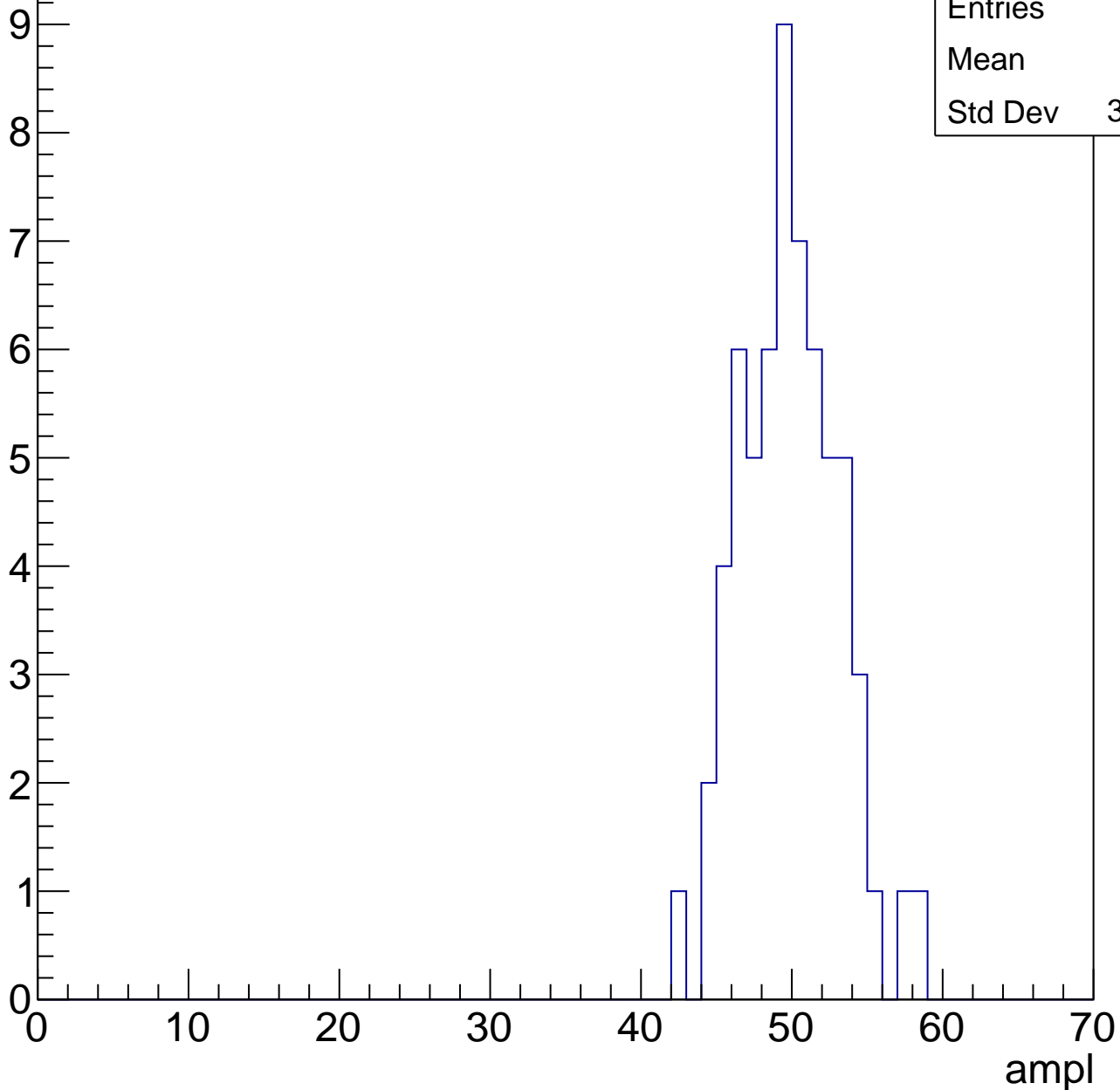


B0L002S, U17-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	49.4
Std Dev	3.225

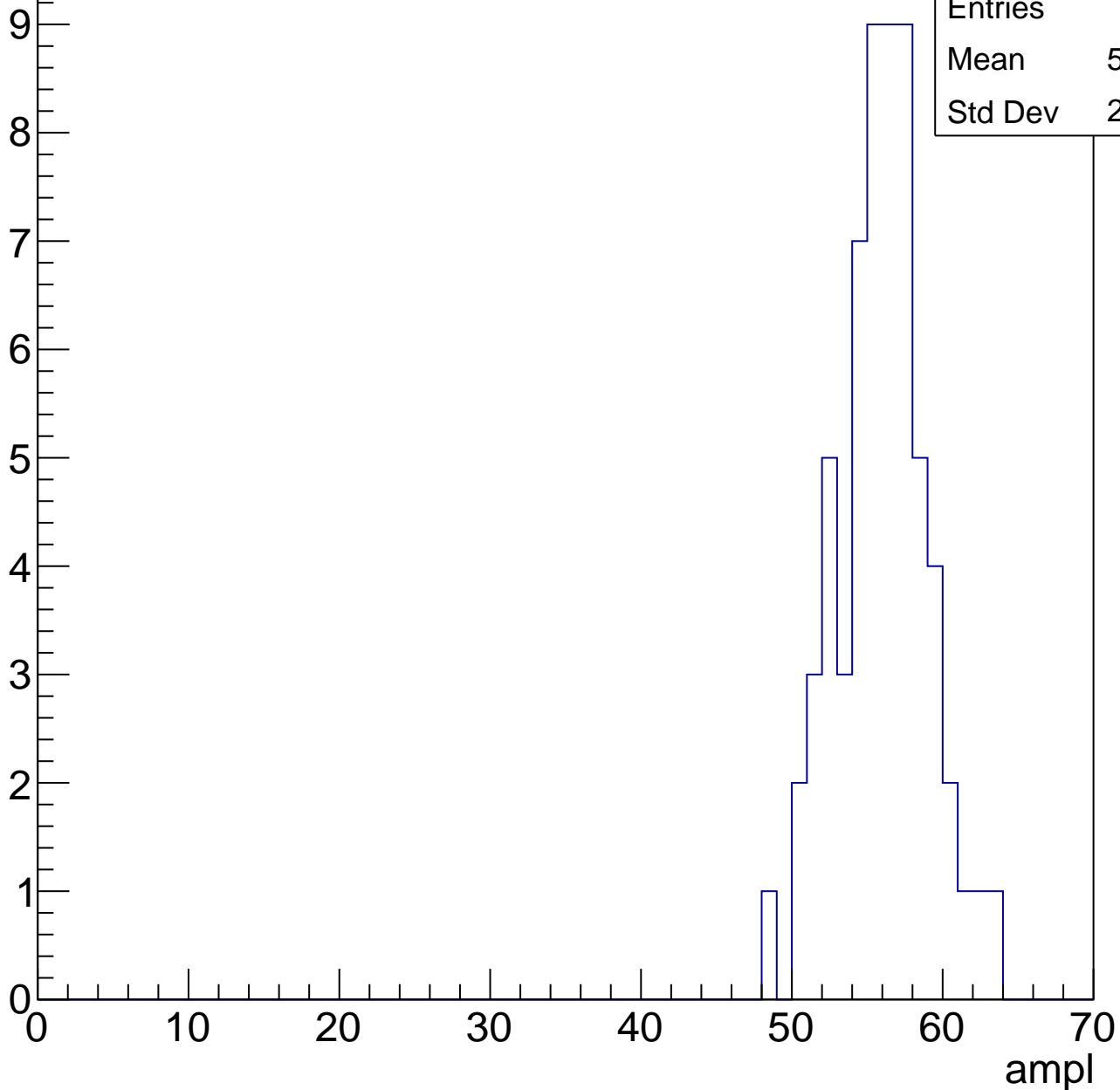


B0L002S, U17-ch69, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.52
Std Dev	2.972

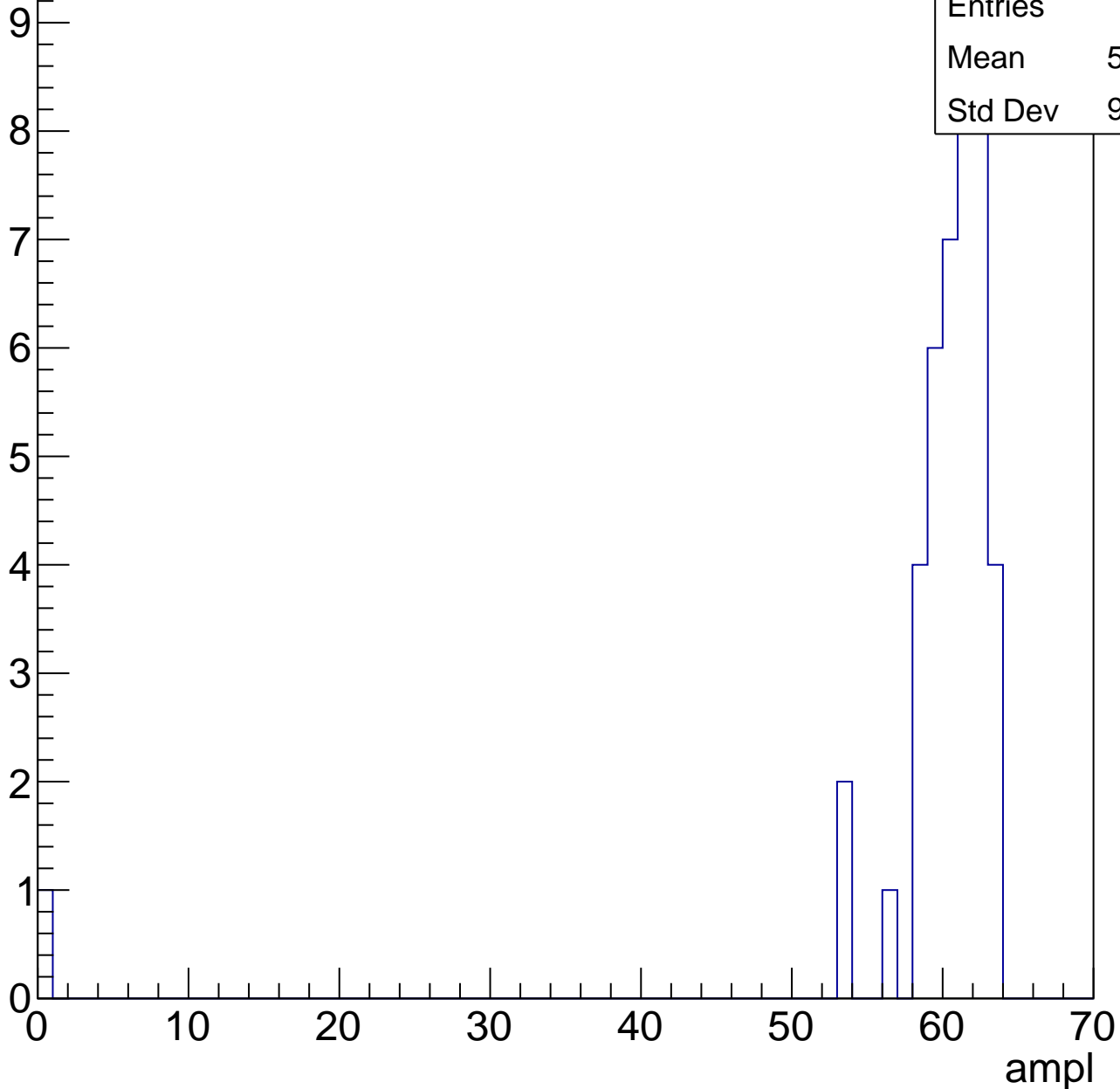


B0L002S, U17-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

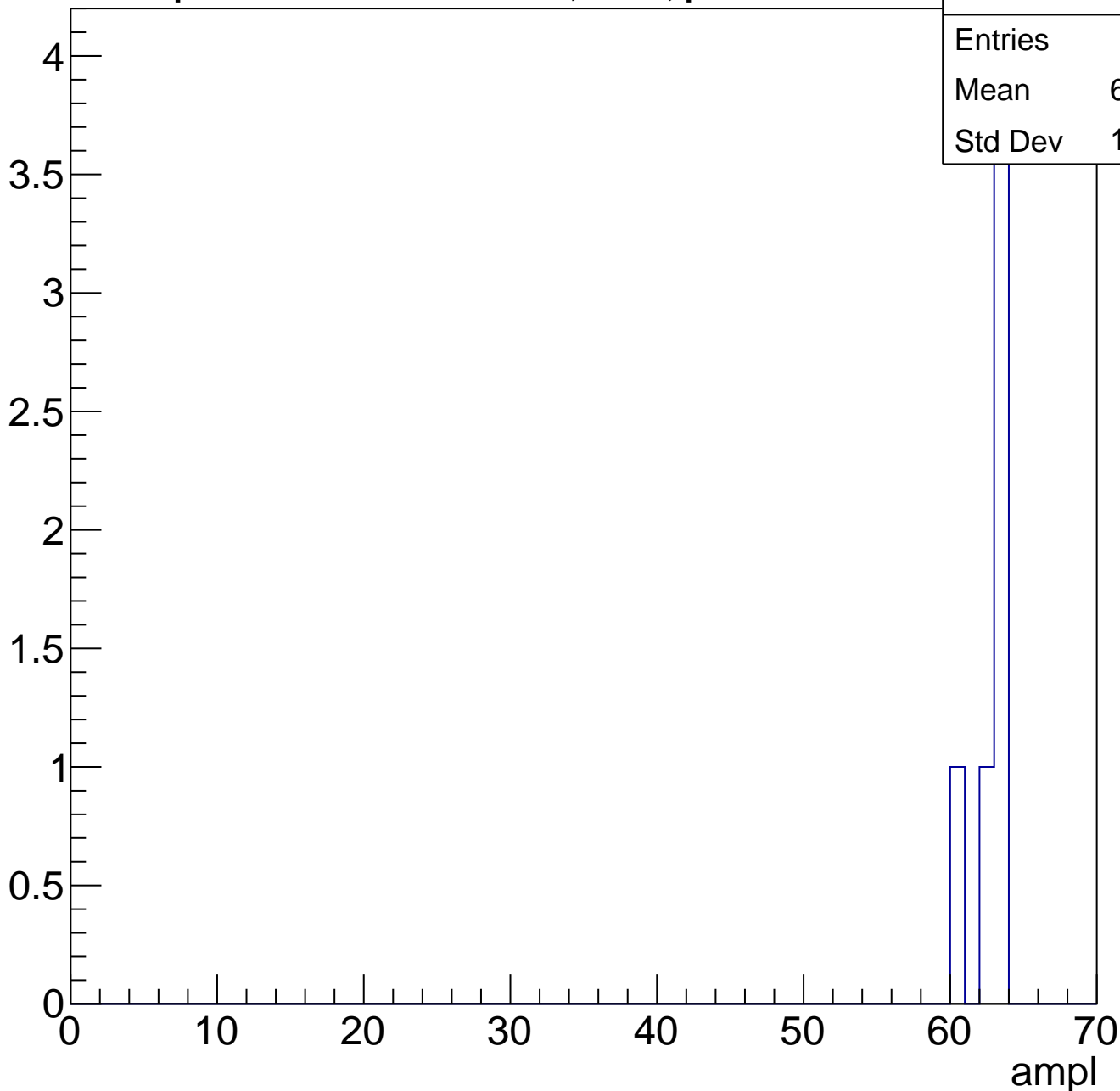
Entries	42
Mean	58.69
Std Dev	9.438



B0L002S, U17-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

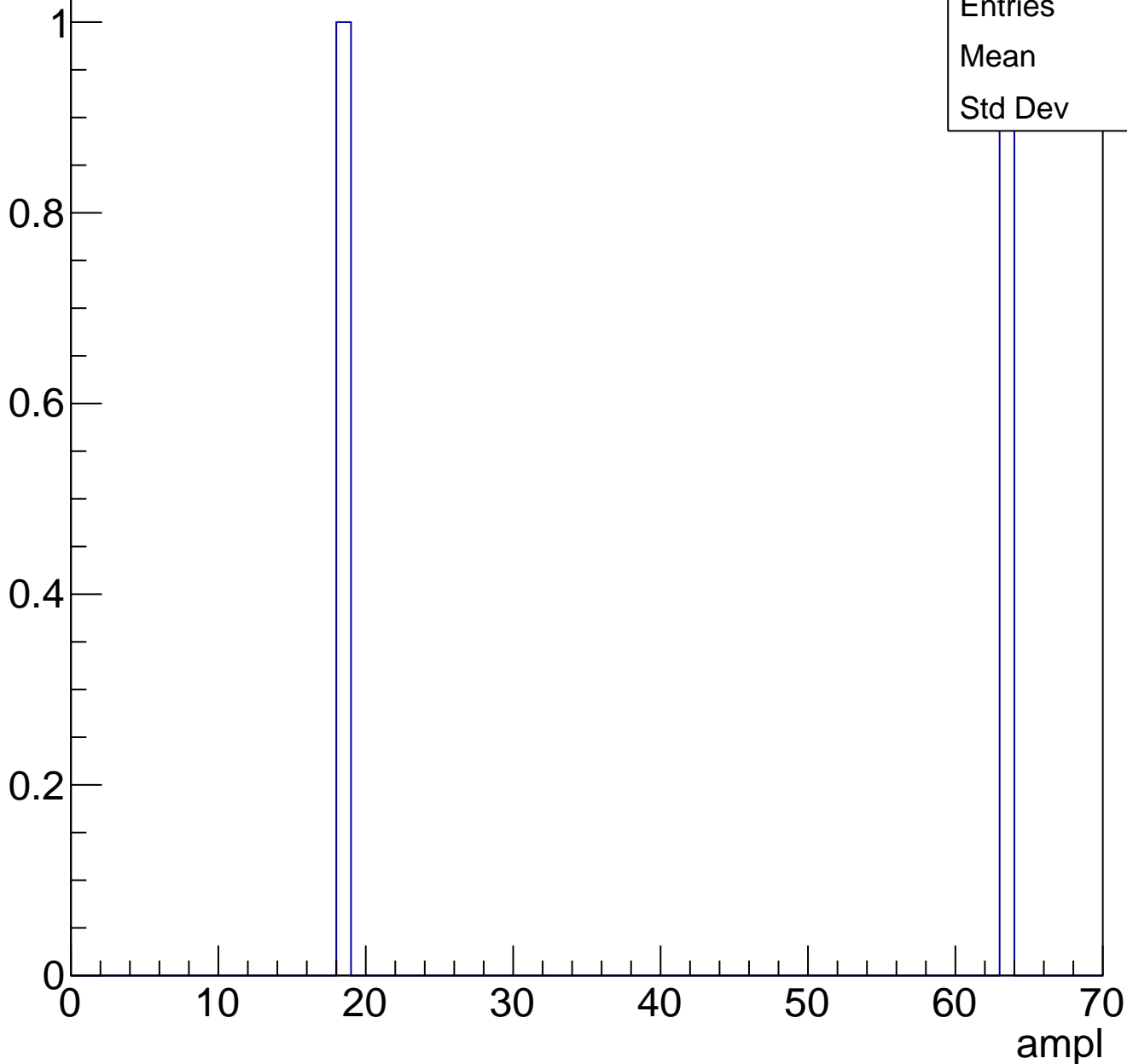
Entry



B0L002S, U17-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch70, adc0

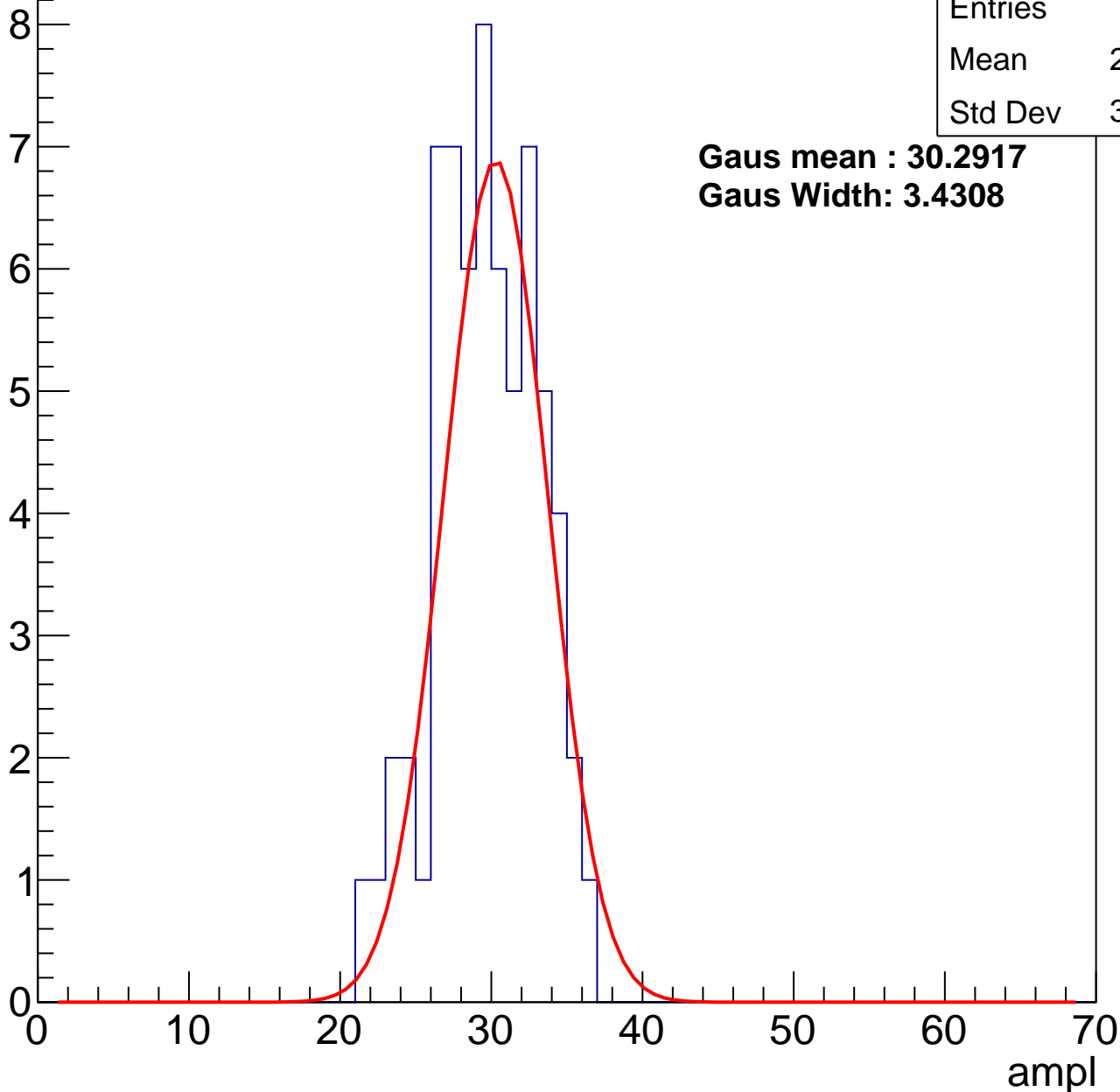
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.22
Std Dev	3.363

Gaus mean : 30.2917

Gaus Width: 3.4308



B0L002S, U17-ch70, adc1

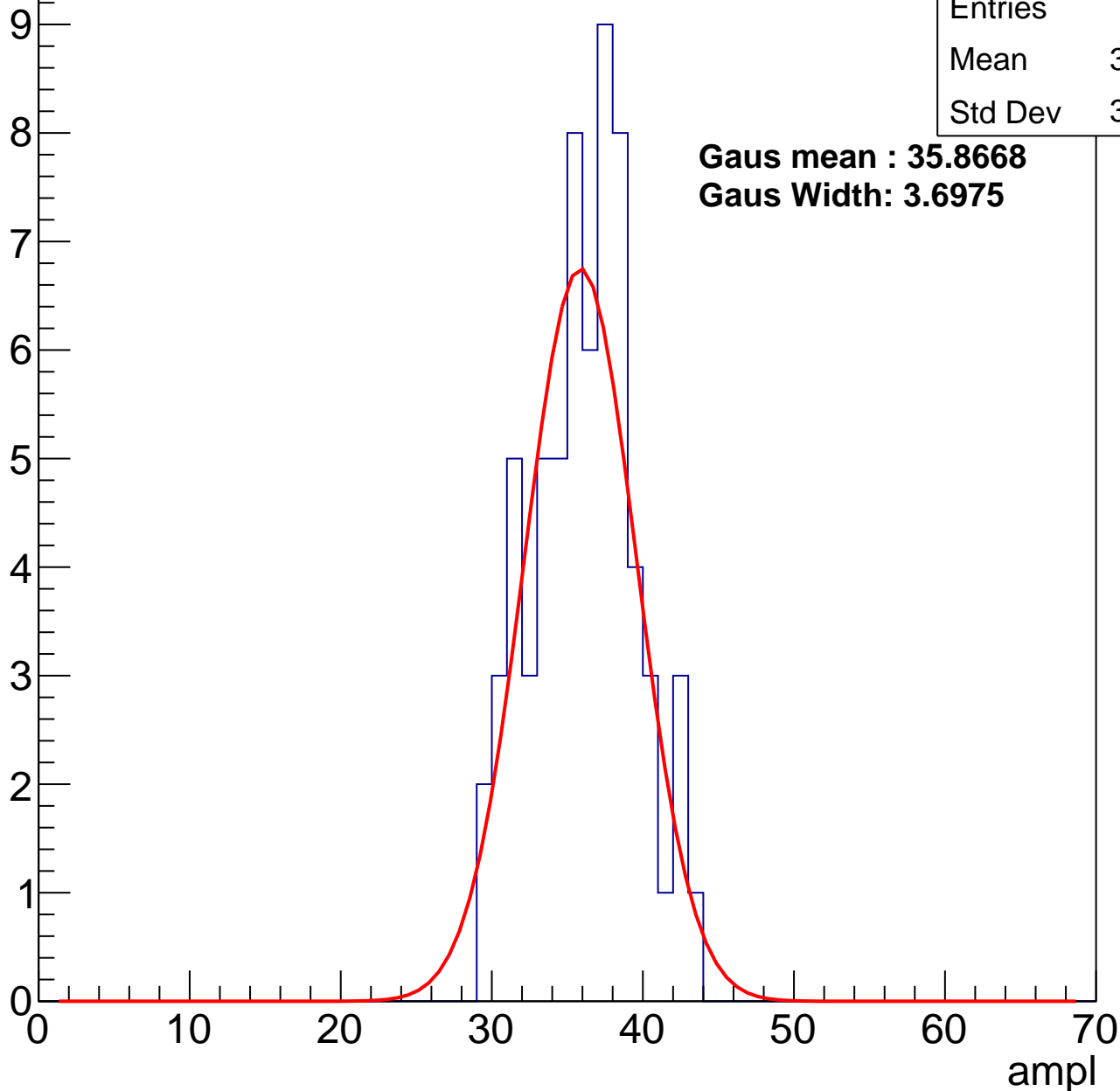
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	35.65
Std Dev	3.373

Gaus mean : 35.8668

Gaus Width: 3.6975



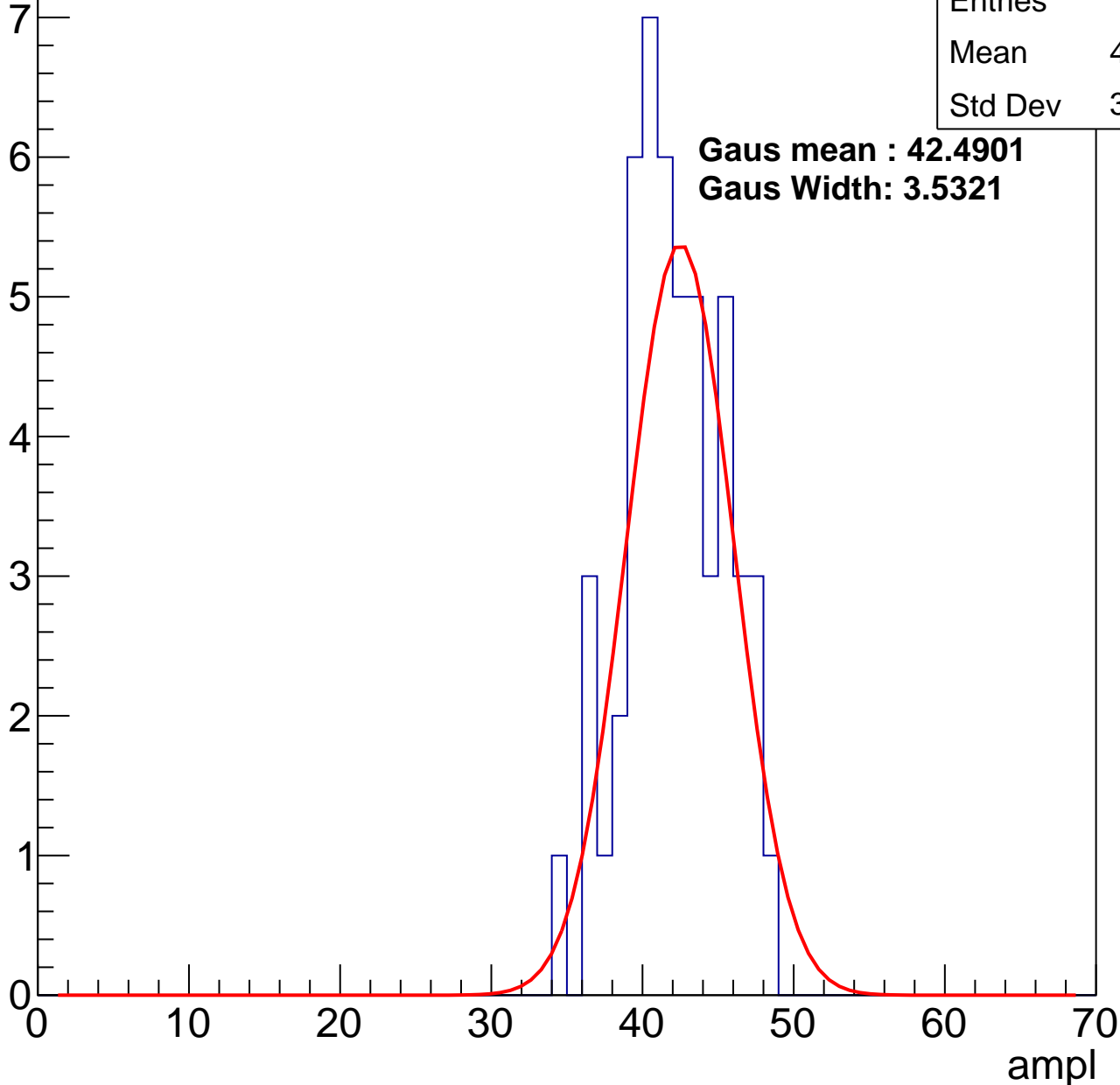
B0L002S, U17-ch70, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	41.65
Std Dev	3.229

Gaus mean : 42.4901
Gaus Width: 3.5321

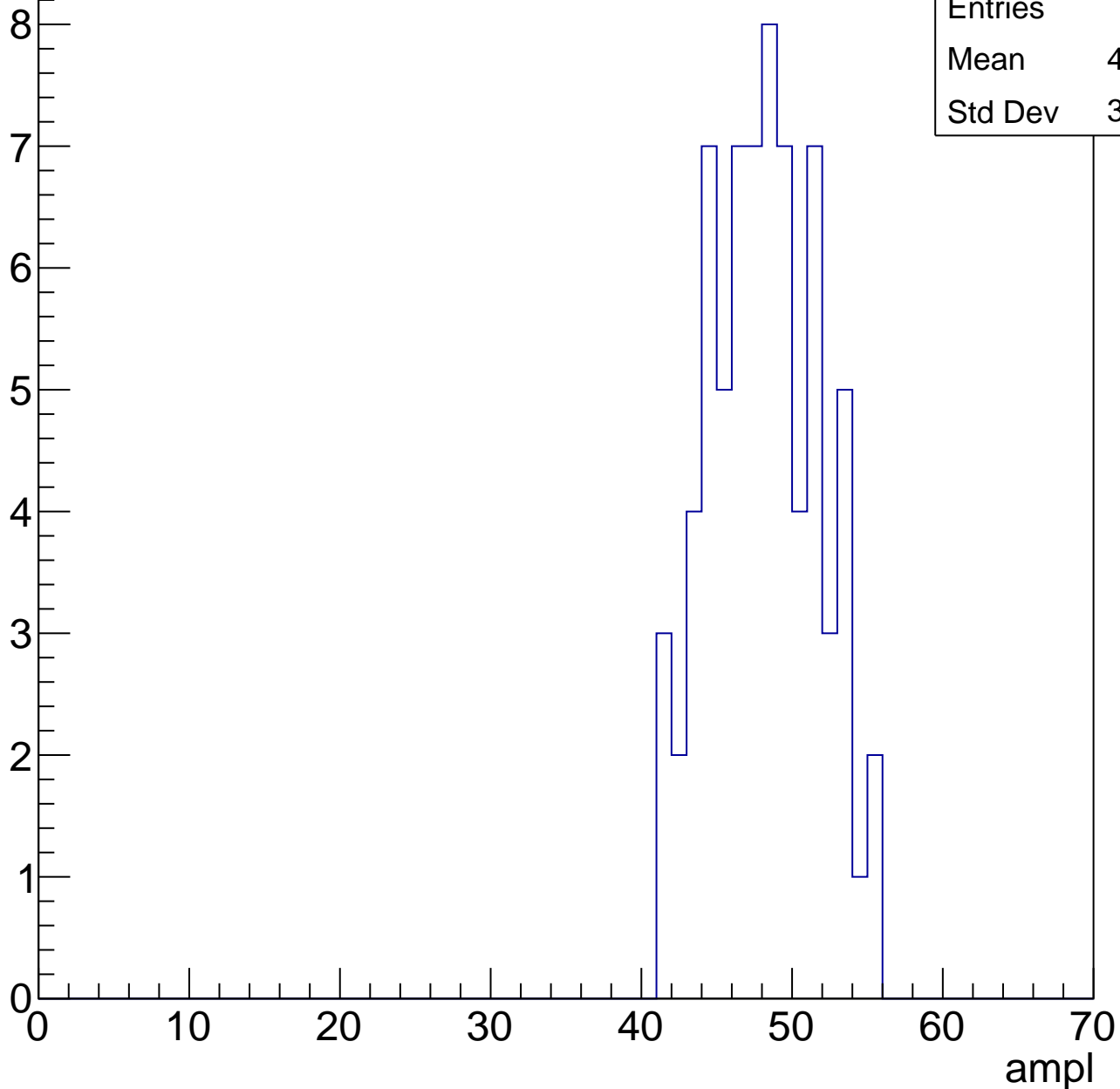


B0L002S, U17-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	47.67
Std Dev	3.528

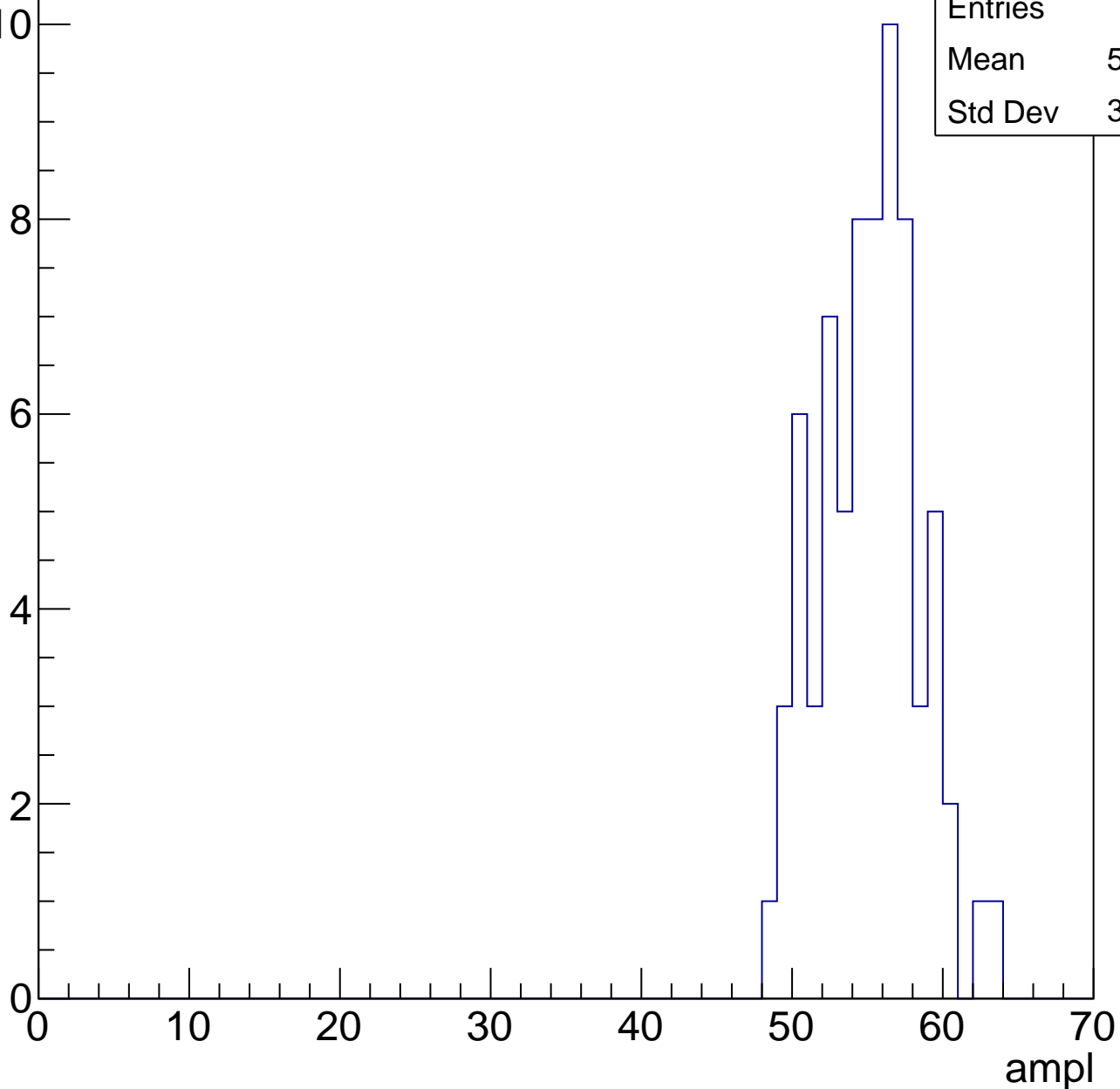


B0L002S, U17-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	54.63
Std Dev	3.247

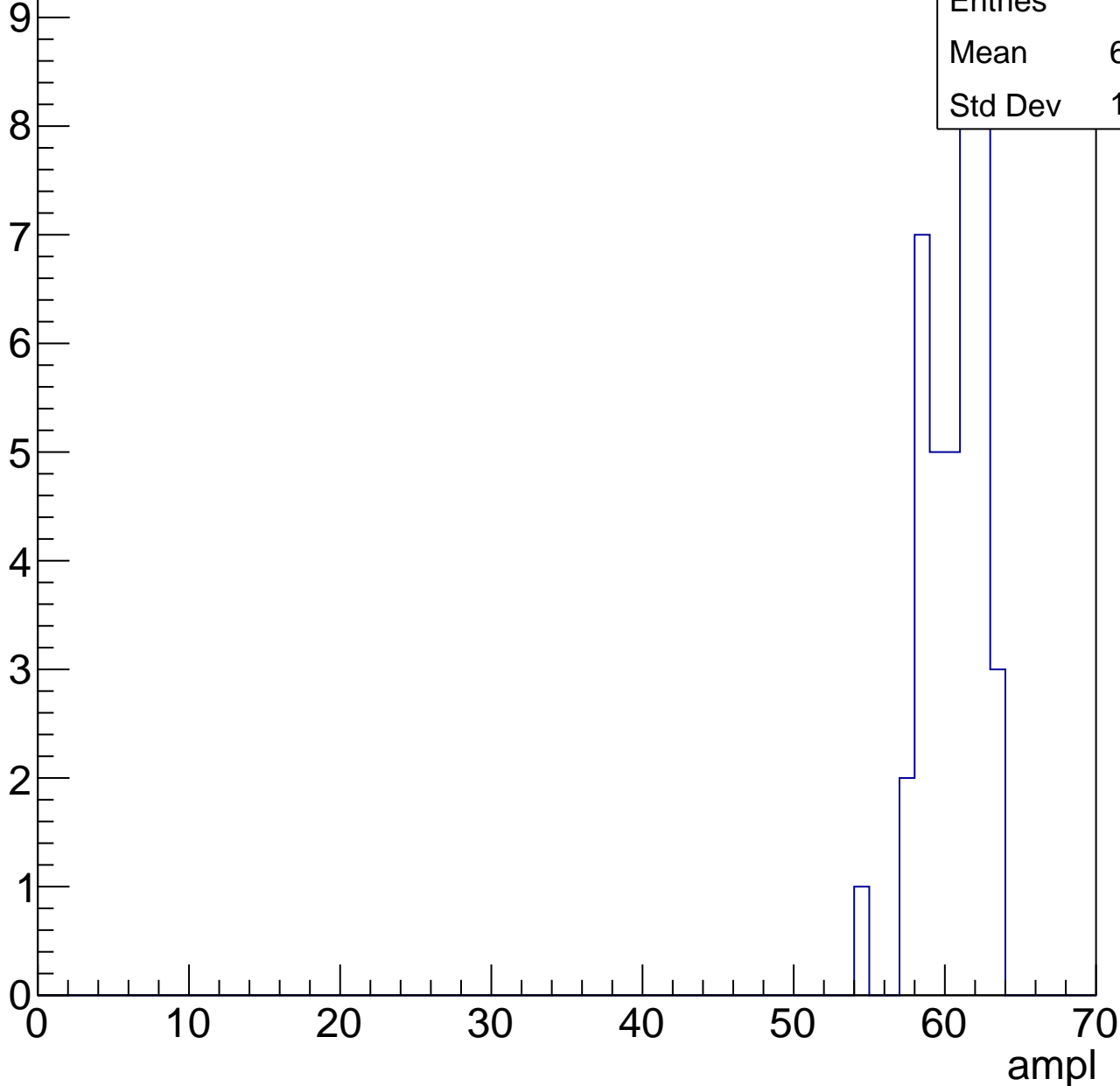


B0L002S, U17-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	60.08
Std Dev	1.967

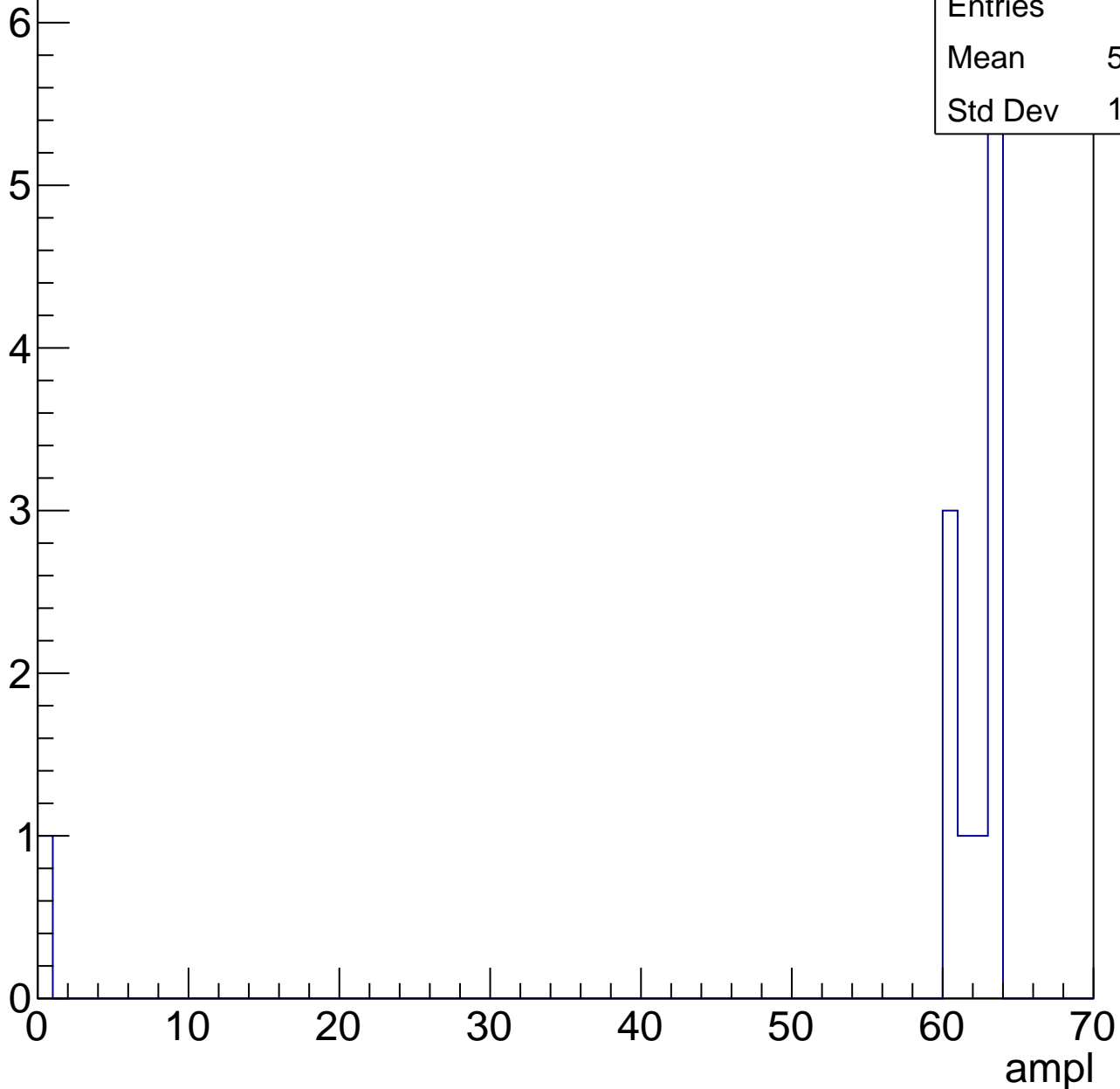


B0L002S, U17-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	56.75
Std Dev	17.16



B0L002S, U17-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch71, adc0

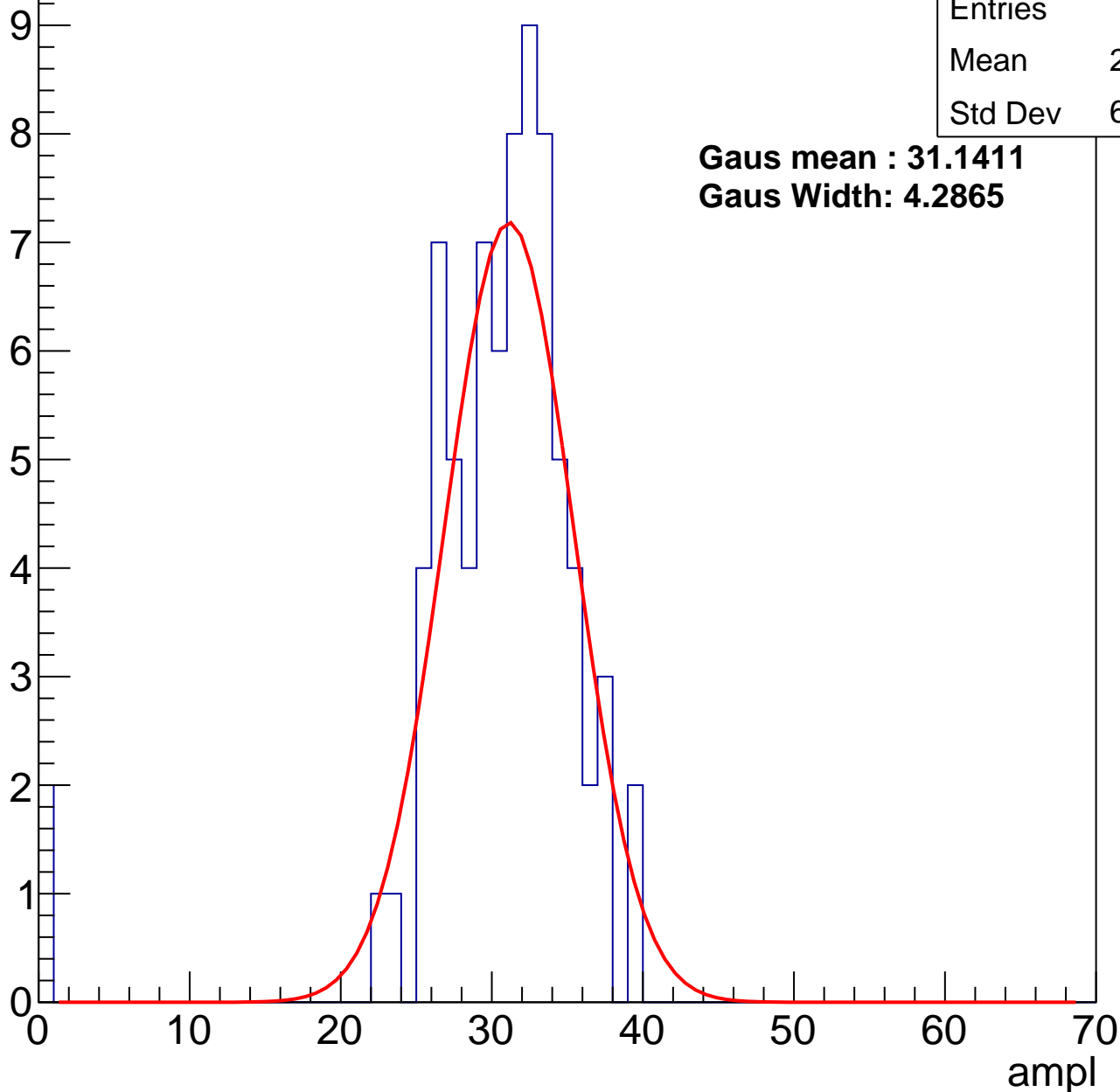
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.85
Std Dev	6.064

Gaus mean : 31.1411

Gaus Width: 4.2865



B0L002S, U17-ch71, adc1

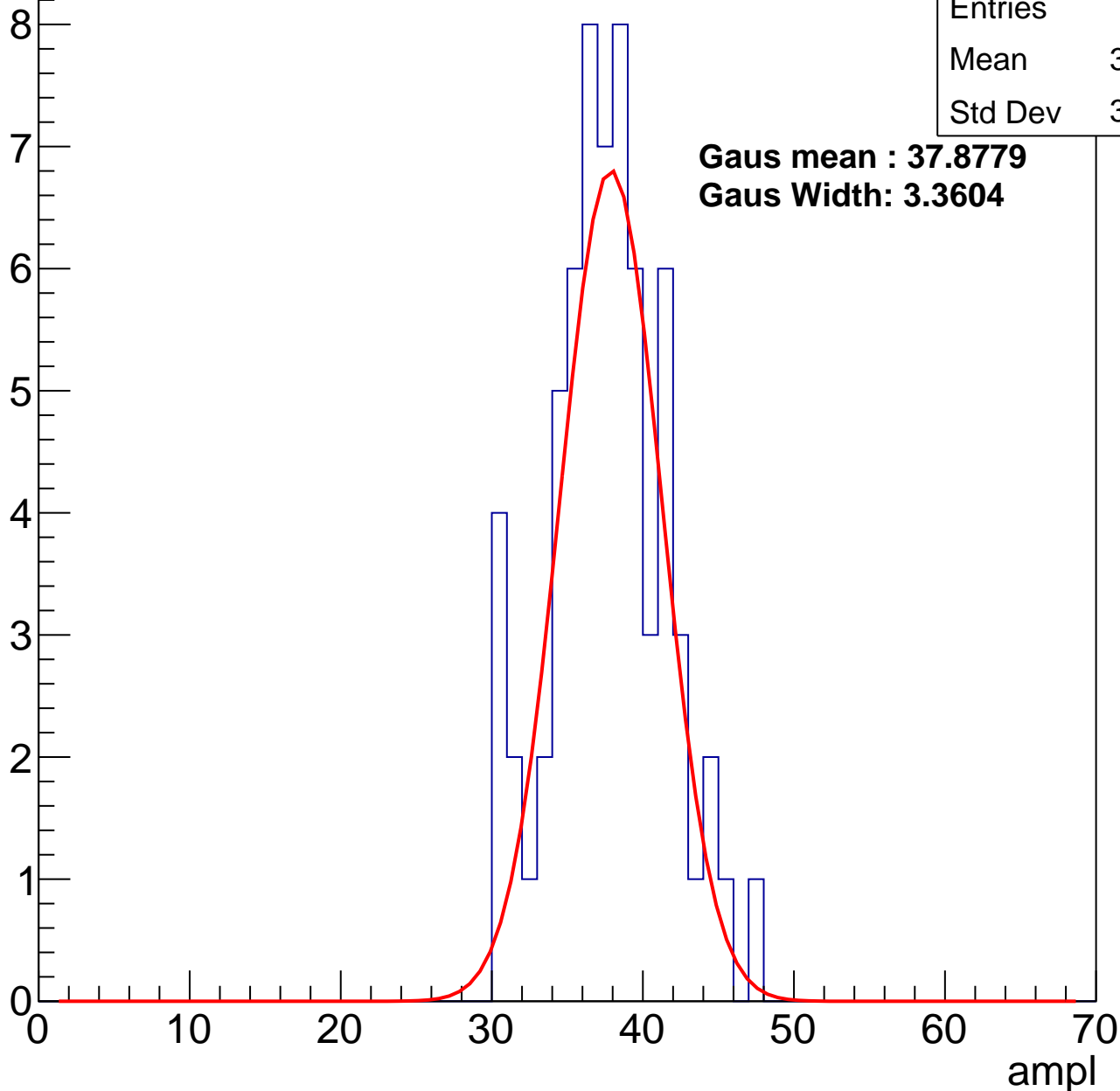
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.27
Std Dev	3.744

Gaus mean : 37.8779

Gaus Width: 3.3604

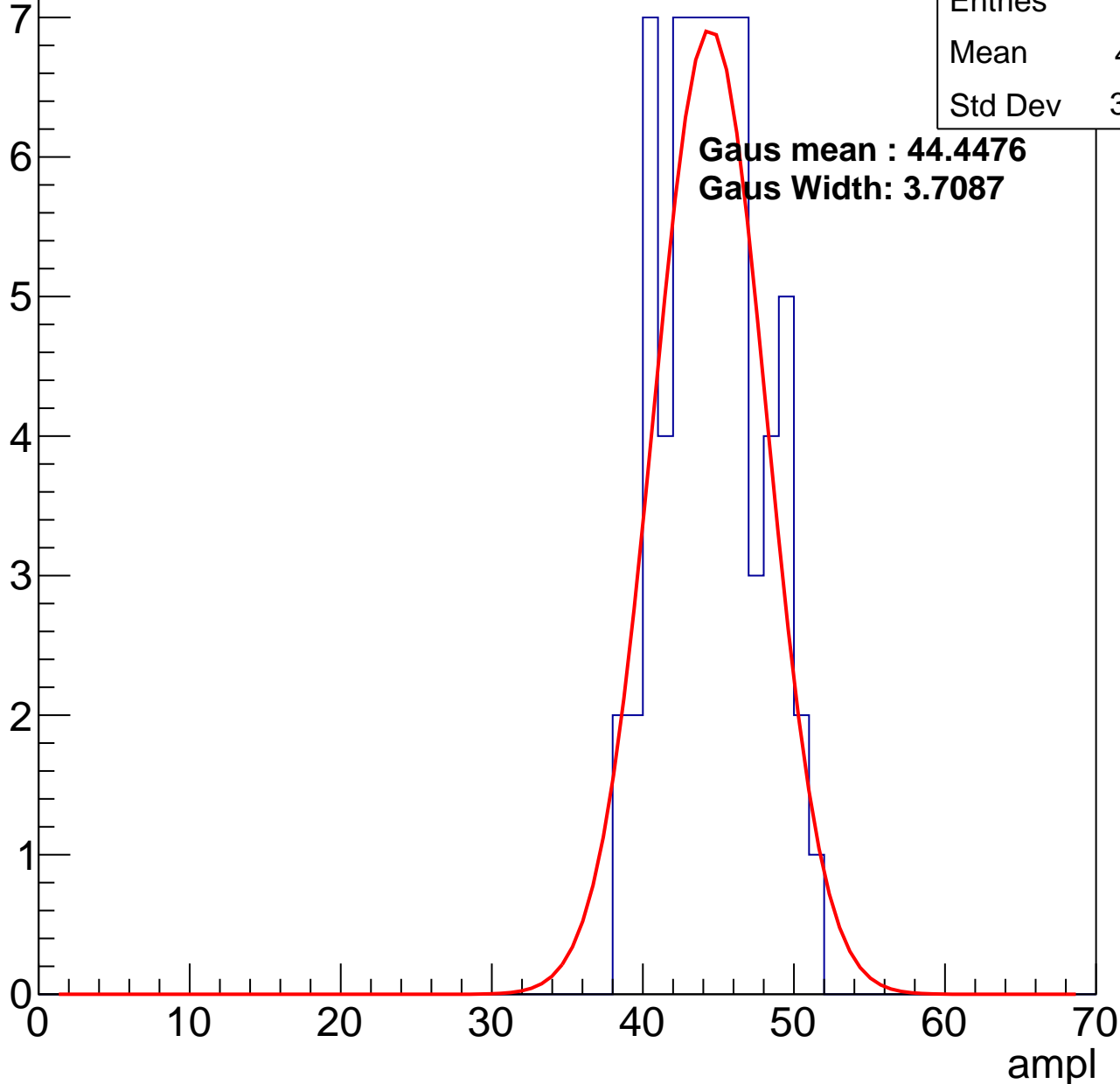


B0L002S, U17-ch71, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.11
Std Dev	3.225

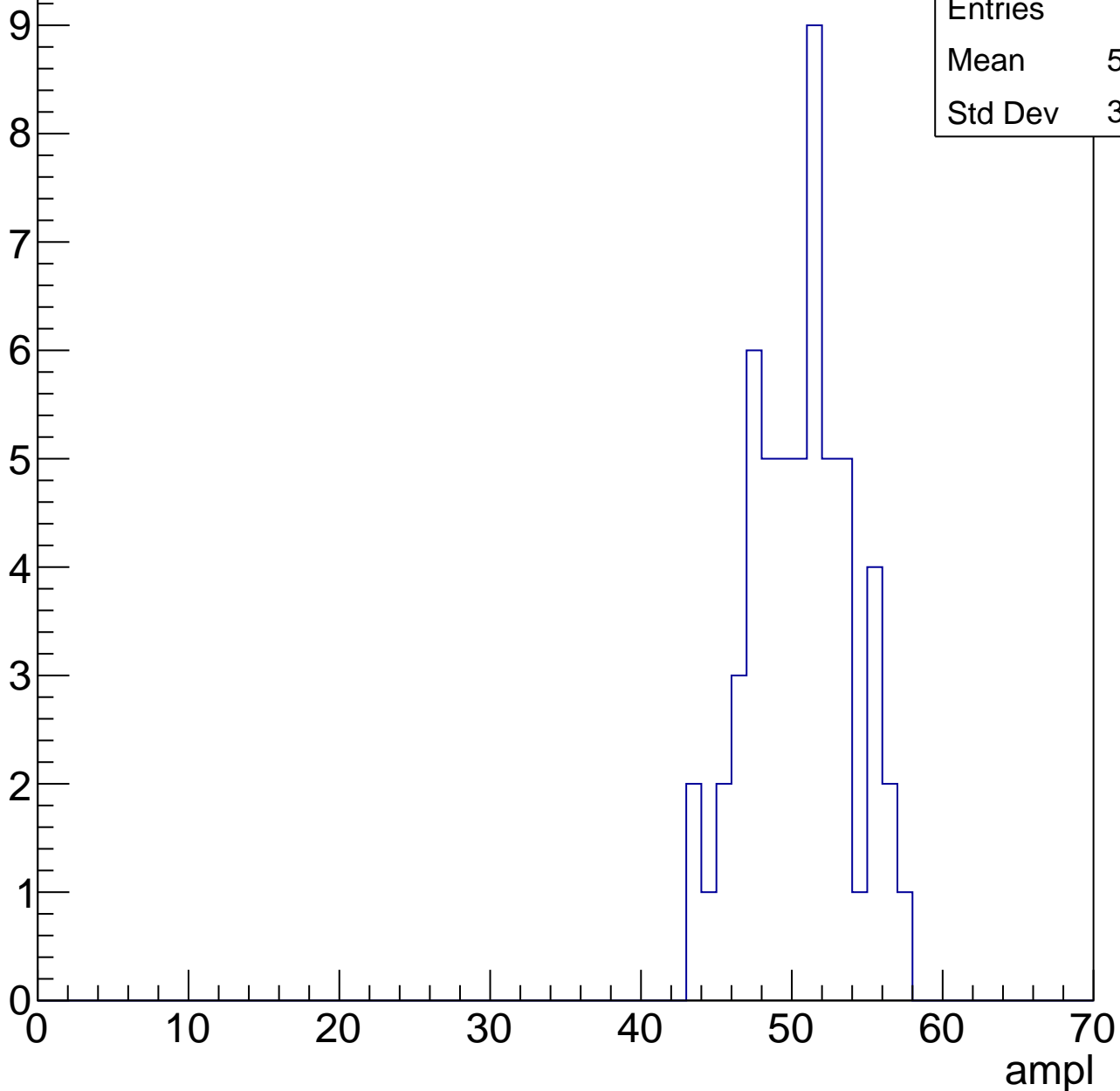


B0L002S, U17-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	50.04
Std Dev	3.333

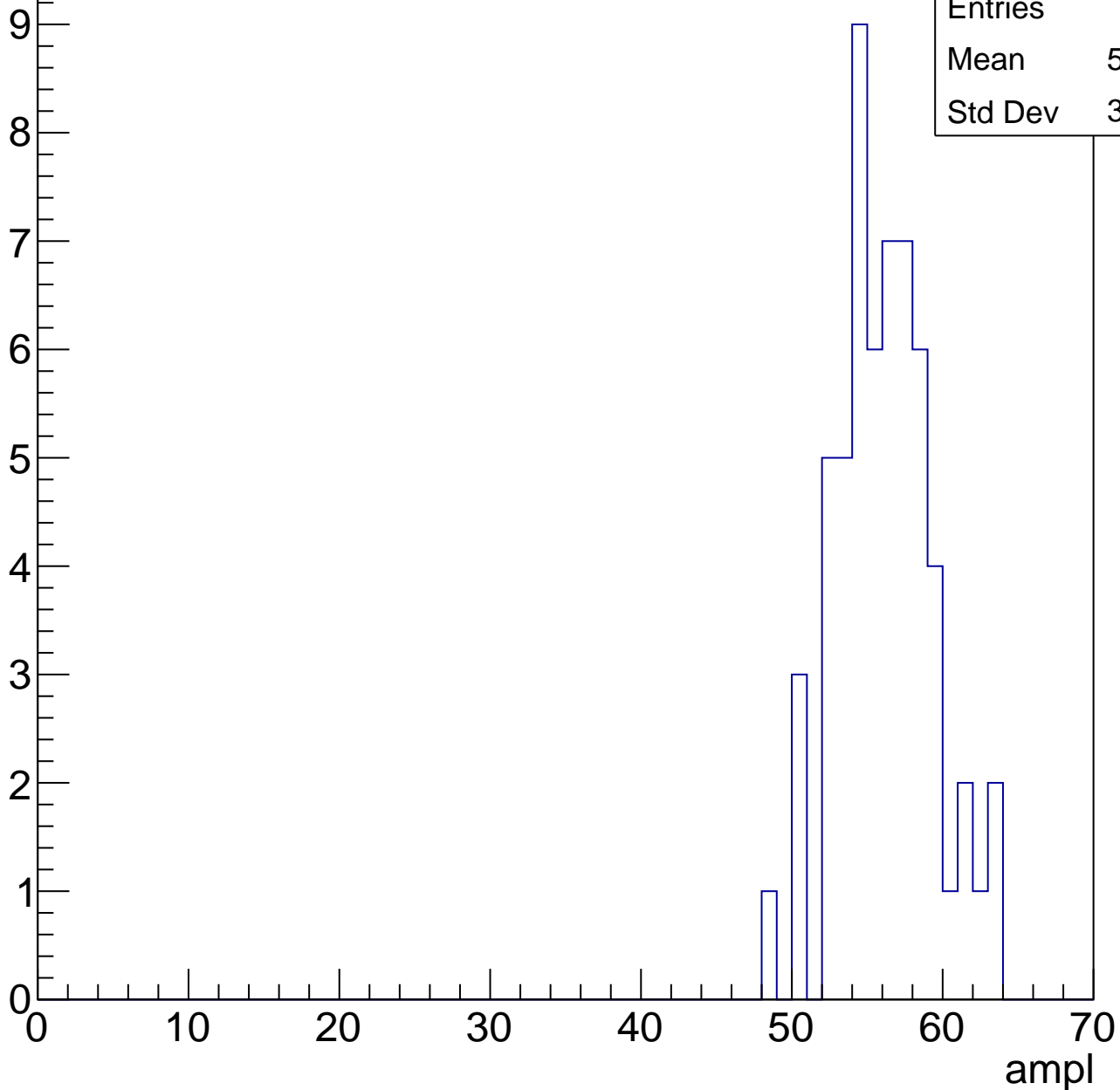


B0L002S, U17-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	55.66
Std Dev	3.187

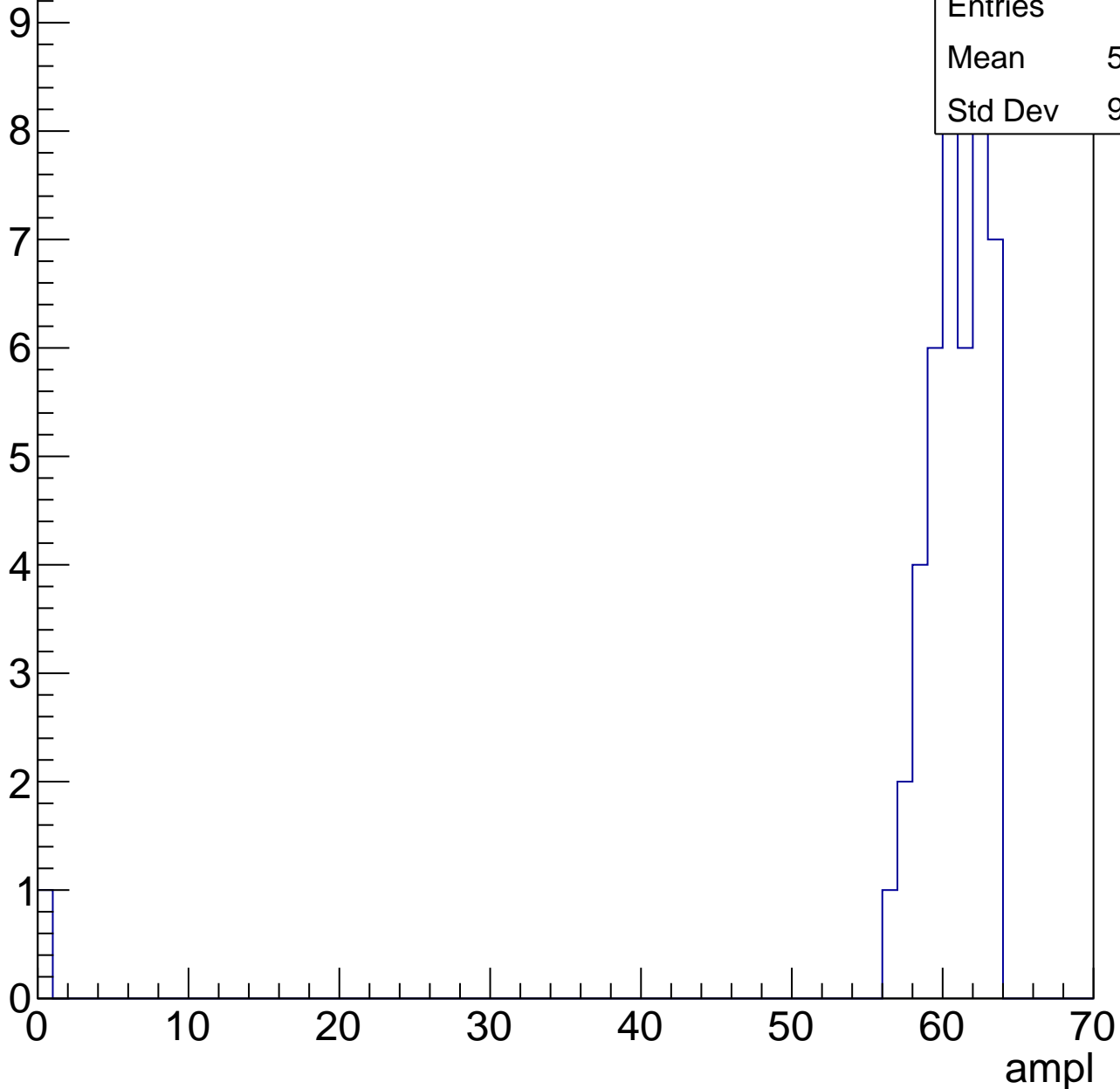


B0L002S, U17-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

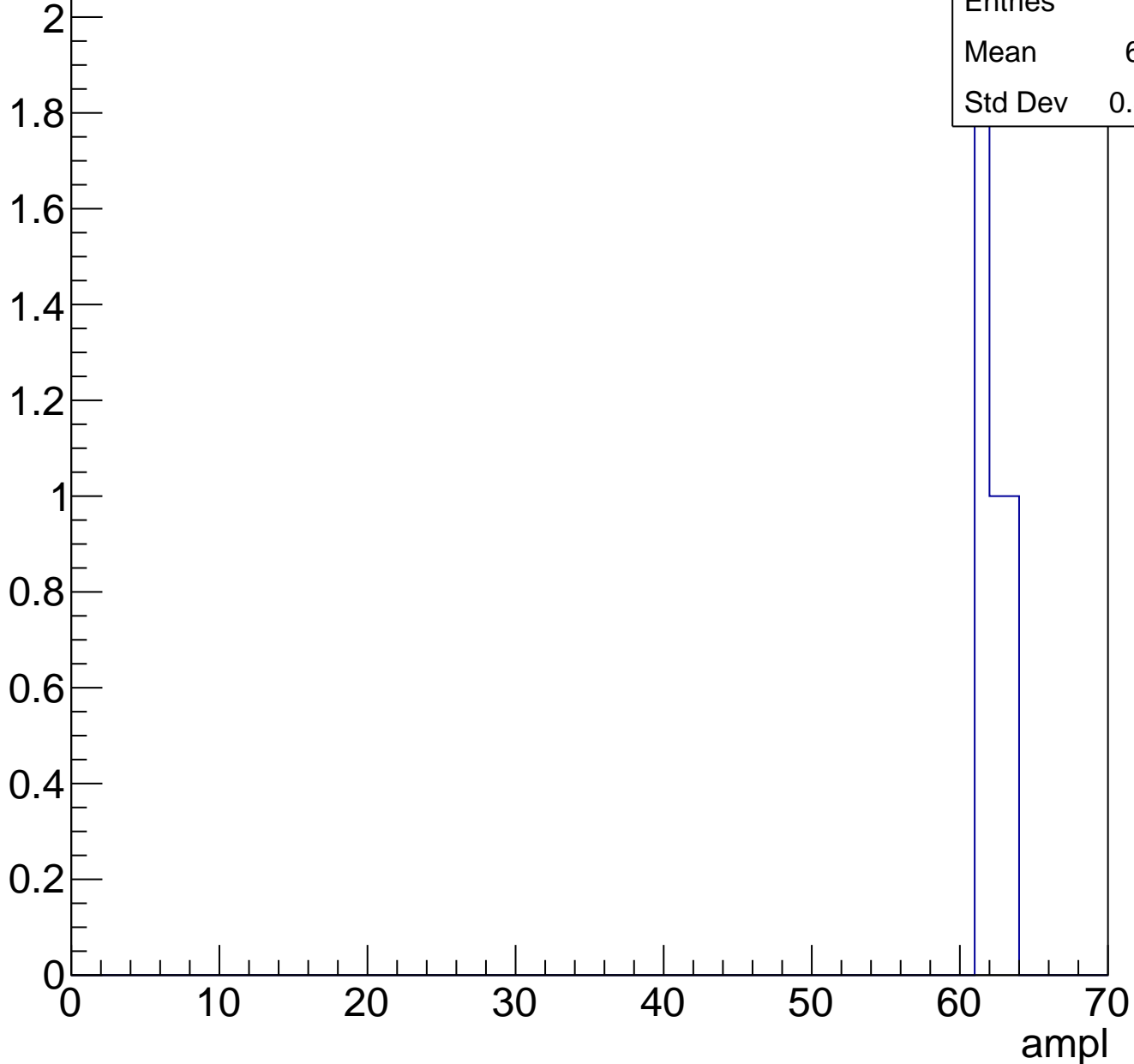
Entries	44
Mean	59.07
Std Dev	9.193



B0L002S, U17-ch71, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch72, adc0

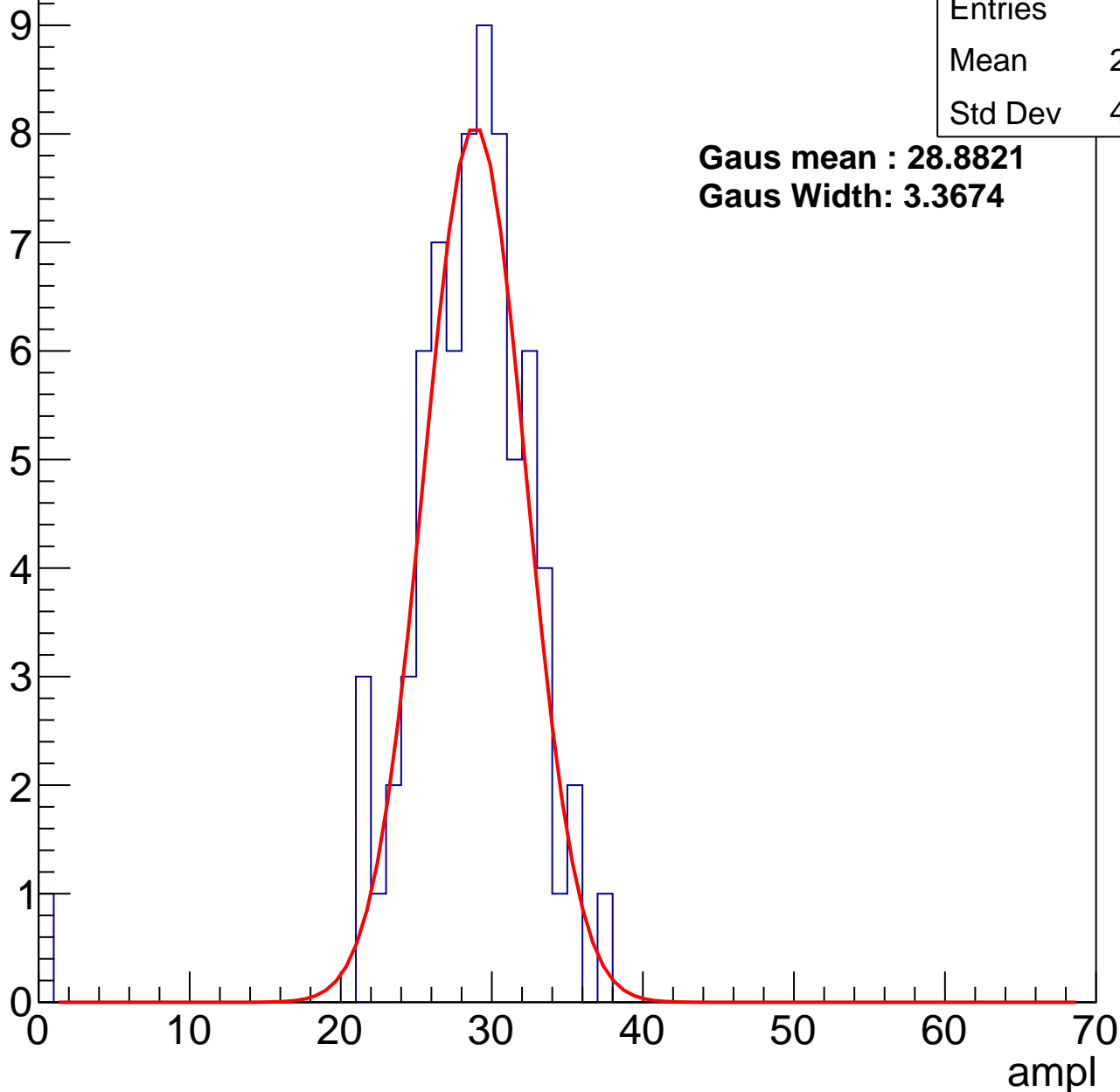
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	27.97
Std Dev	4.754

Gaus mean : 28.8821

Gaus Width: 3.3674



B0L002S, U17-ch72, adc1

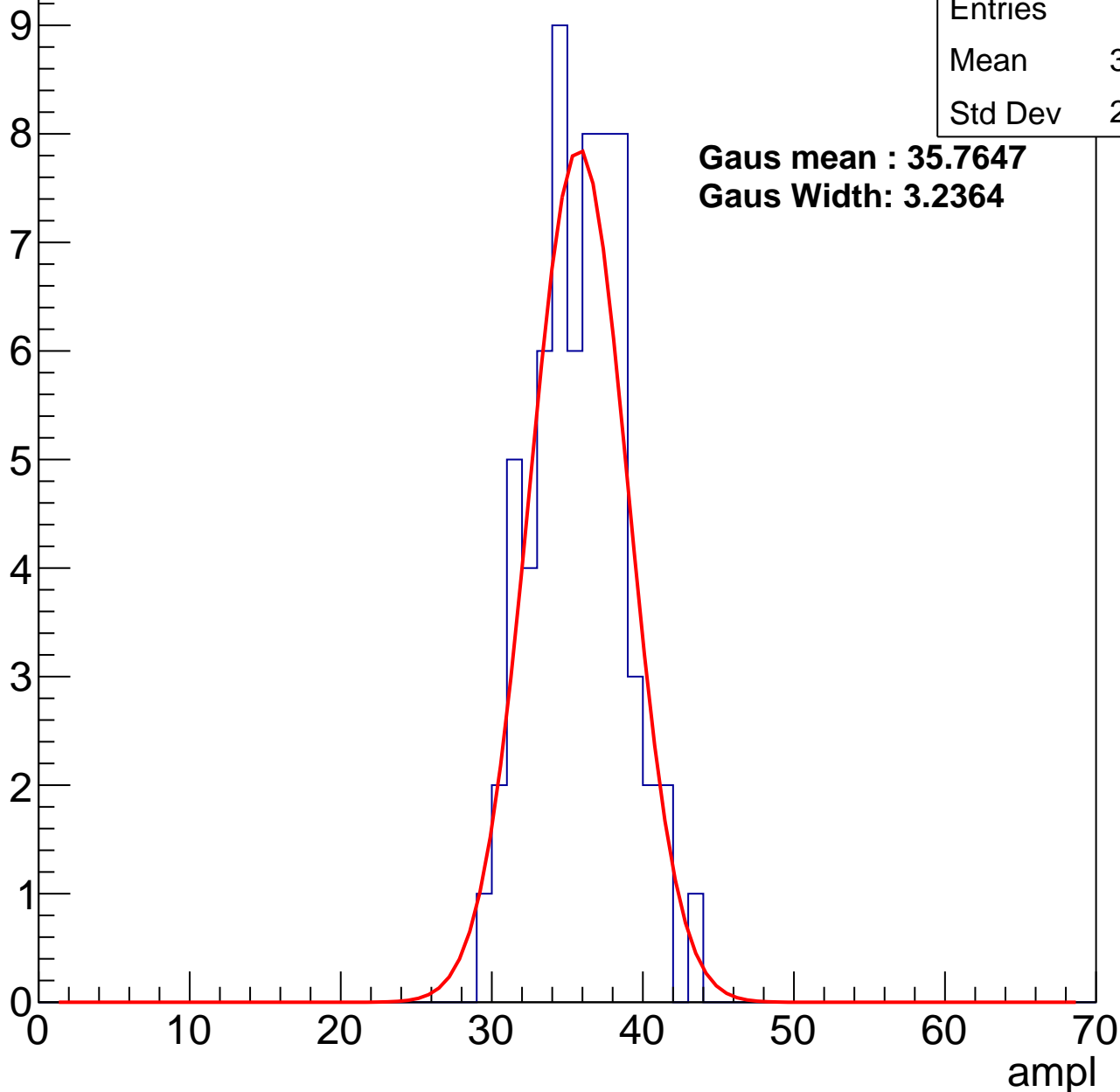
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.32
Std Dev	2.972

Gaus mean : 35.7647

Gaus Width: 3.2364



B0L002S, U17-ch72, adc2

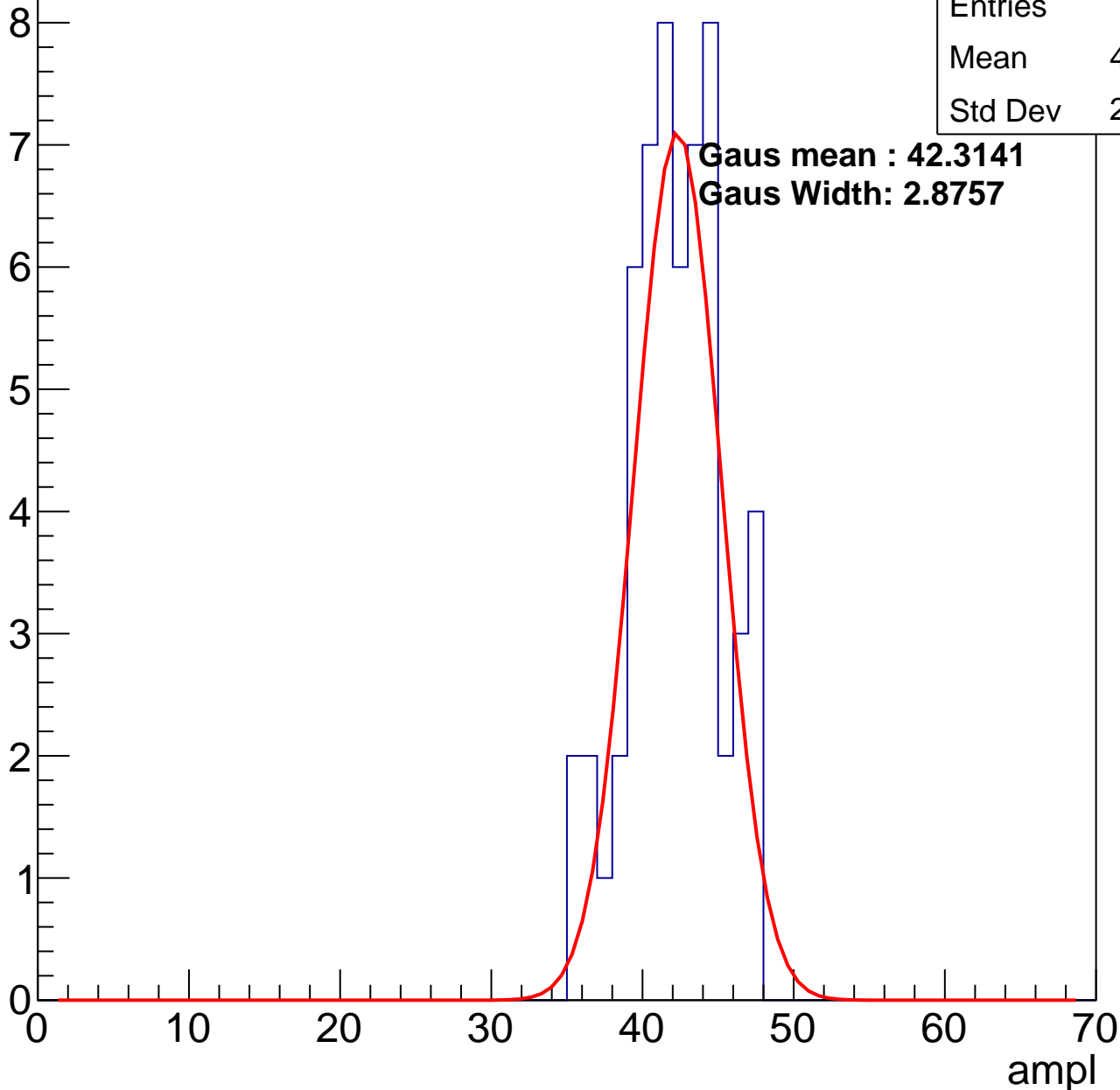
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	41.69
Std Dev	2.984

Gaus mean : 42.3141

Gaus Width: 2.8757

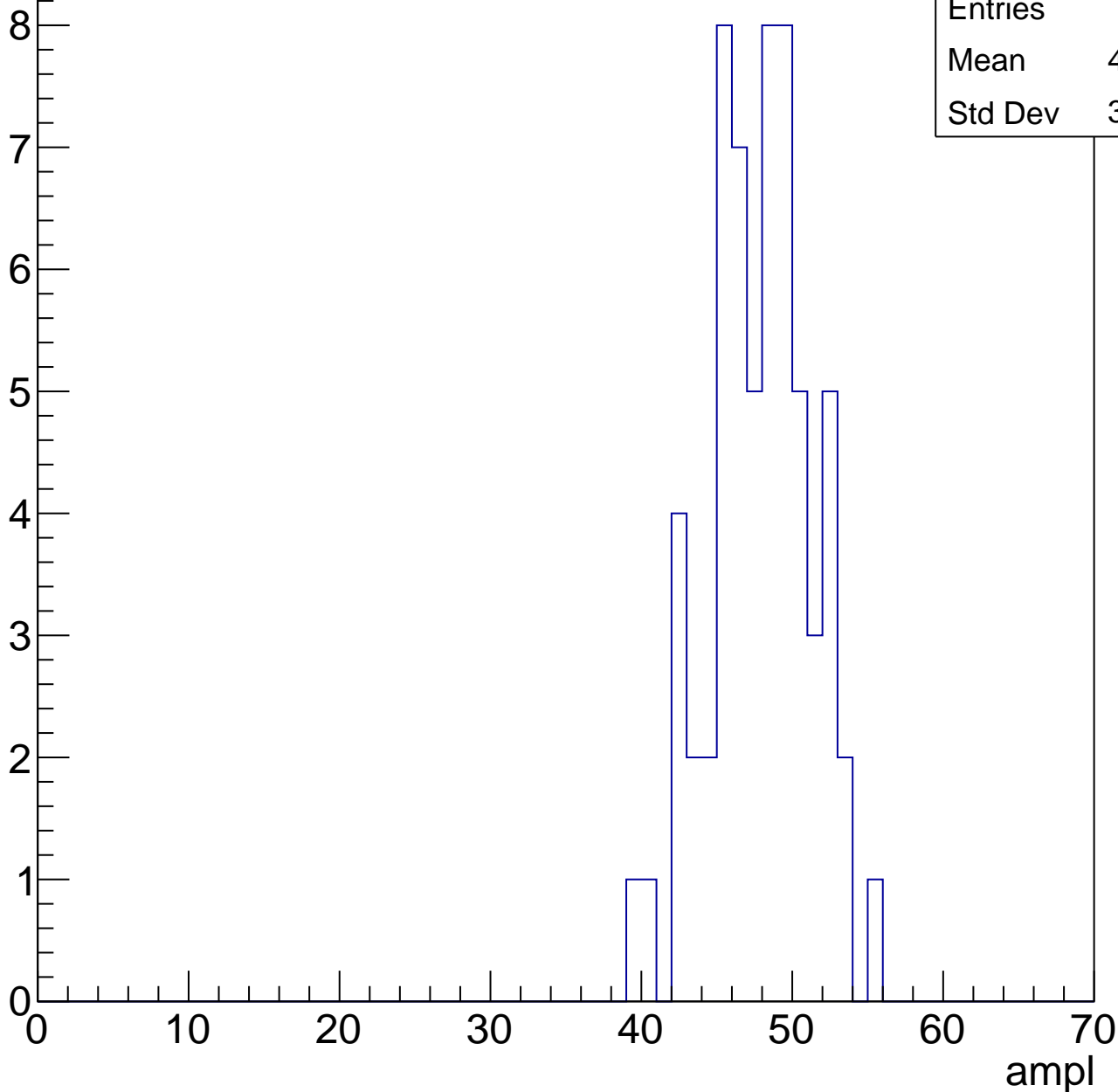


B0L002S, U17-ch72, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

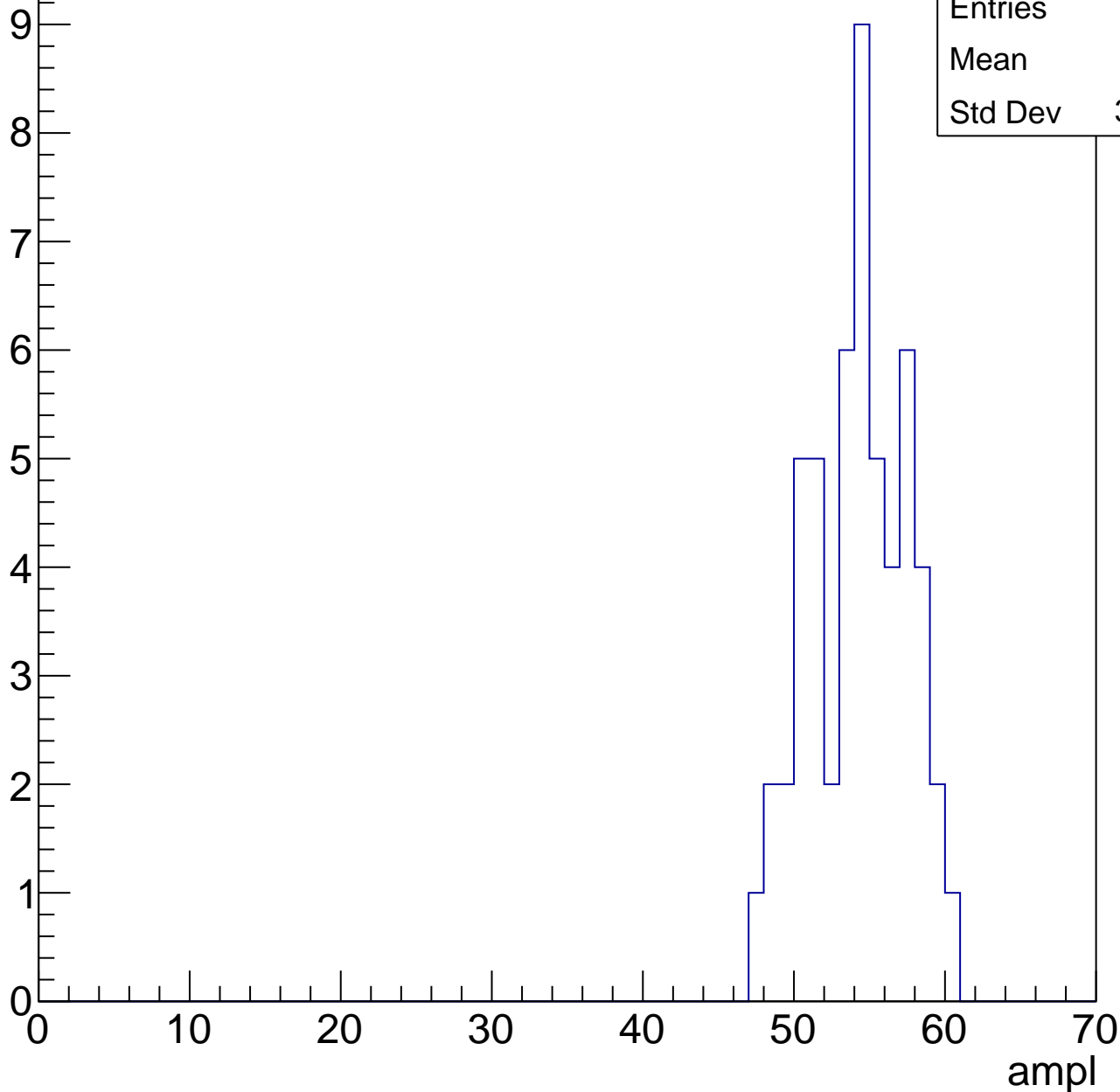
Entries	62
Mean	47.39
Std Dev	3.328



B0L002S, U17-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



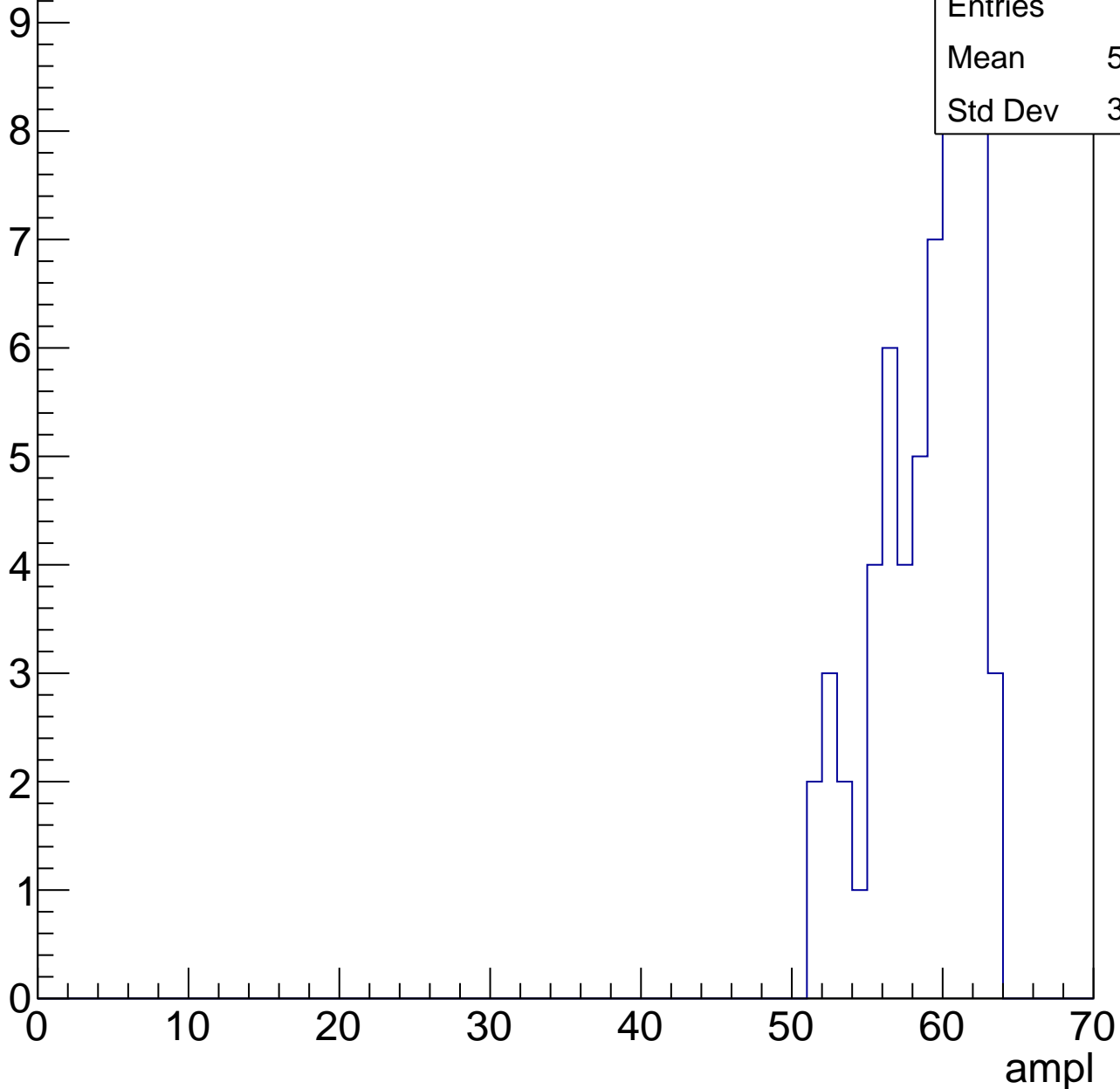
Entries	54
Mean	53.8
Std Dev	3.141

B0L002S, U17-ch72, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	58.37
Std Dev	3.239

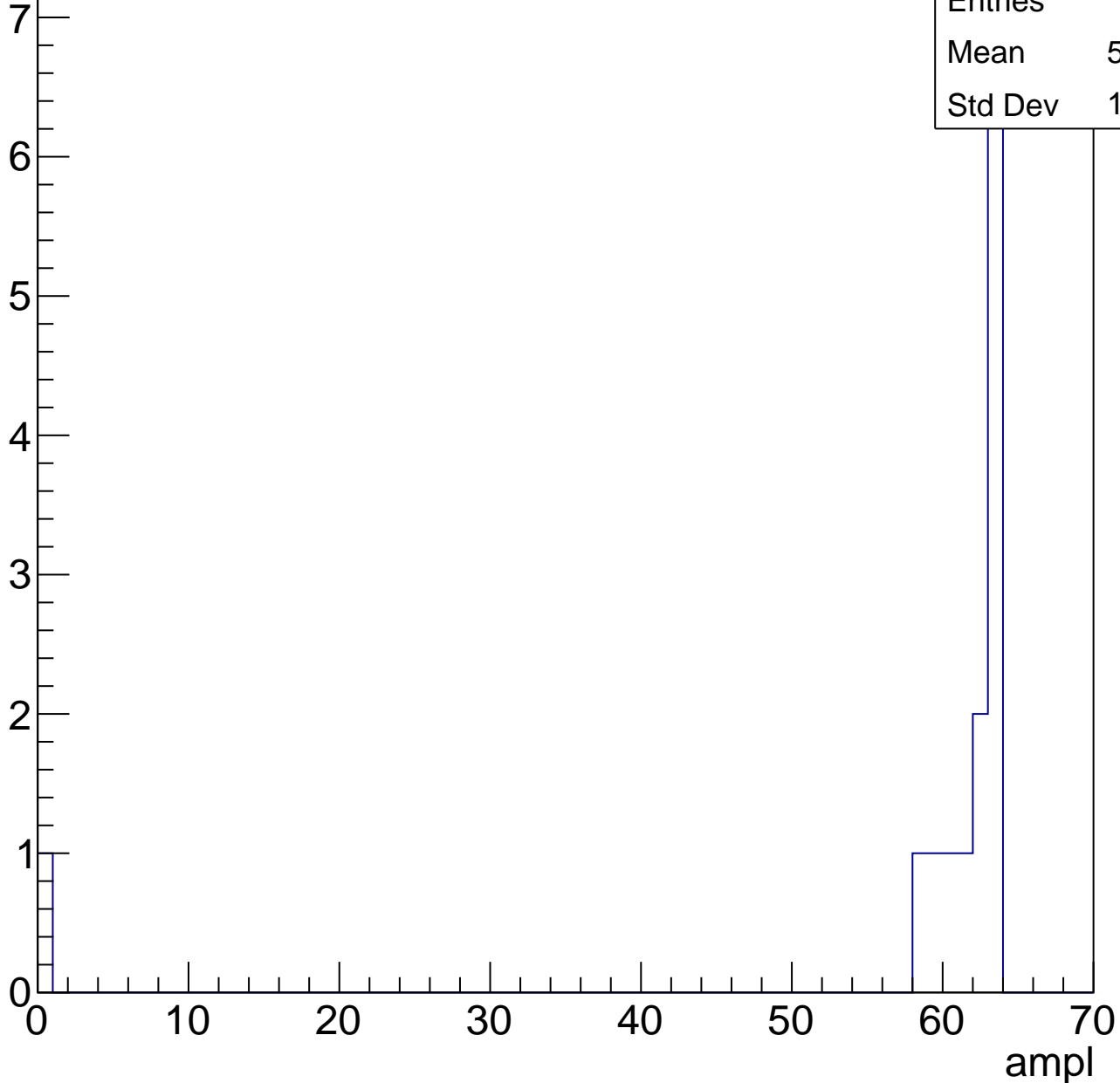


B0L002S, U17-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	14
Mean	57.36
Std Dev	15.99



B0L002S, U17-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch73, adc0

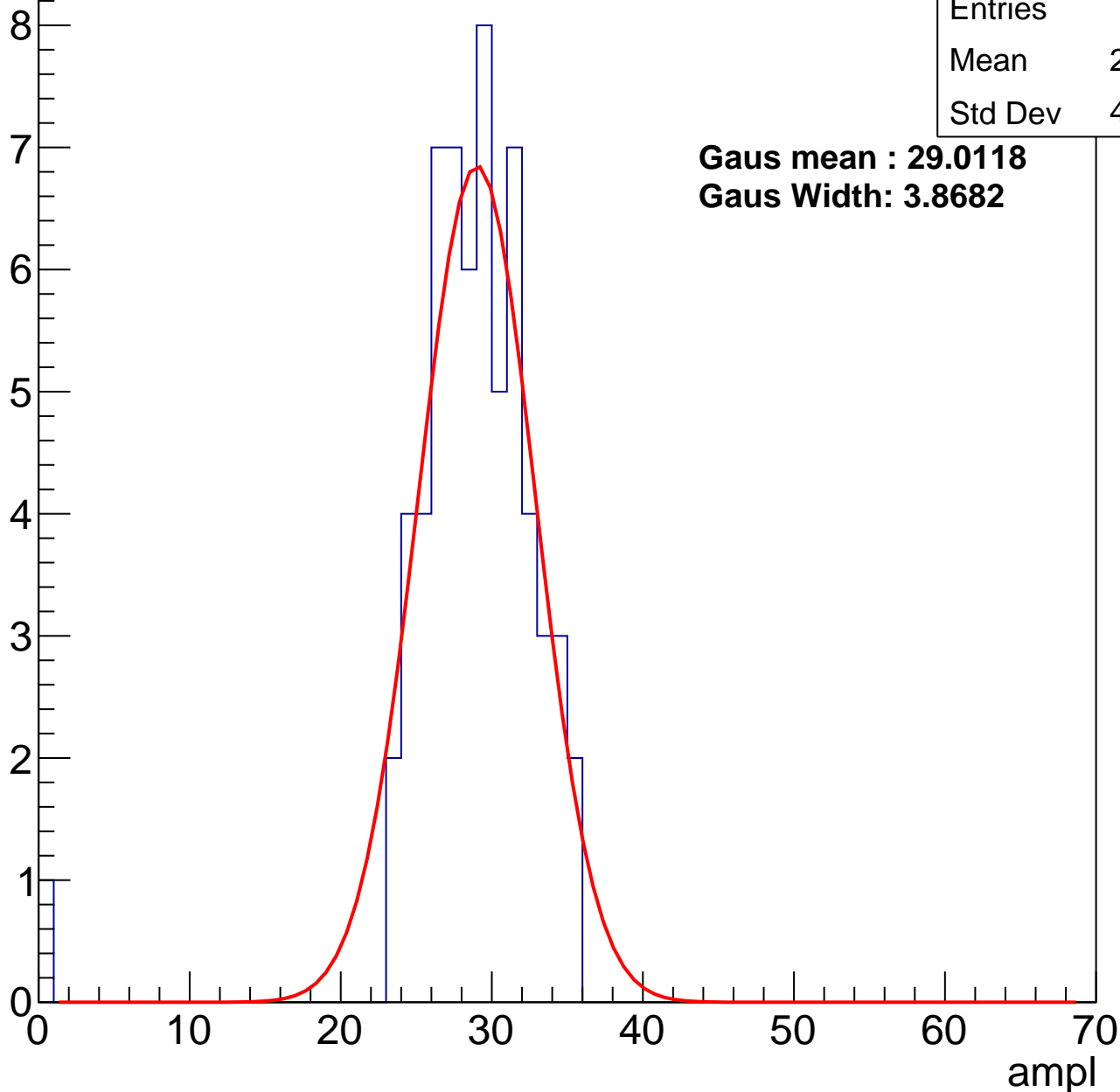
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	28.24
Std Dev	4.716

Gaus mean : 29.0118

Gaus Width: 3.8682



B0L002S, U17-ch73, adc1

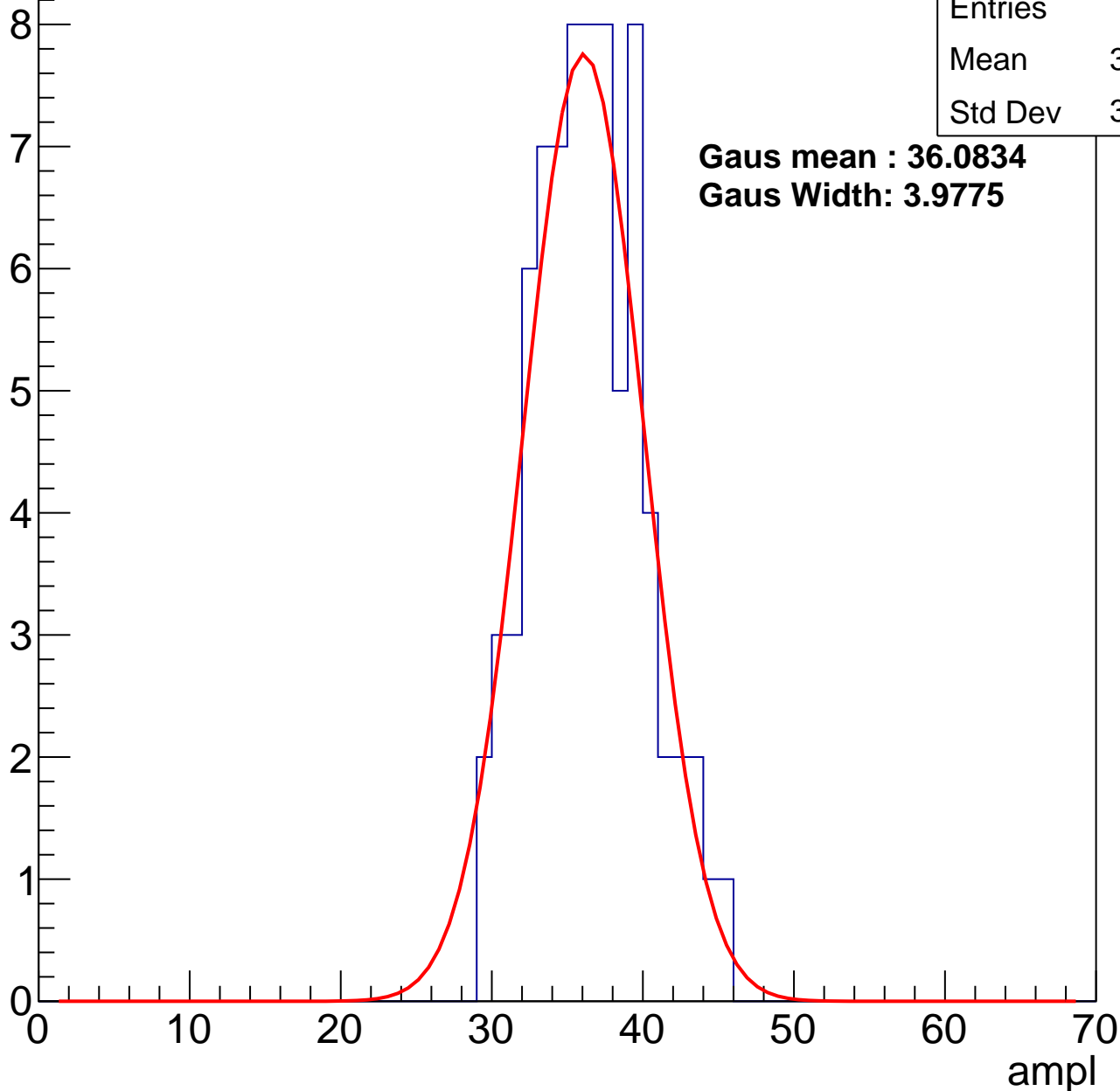
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	35.96
Std Dev	3.613

Gaus mean : 36.0834

Gaus Width: 3.9775



B0L002S, U17-ch73, adc2

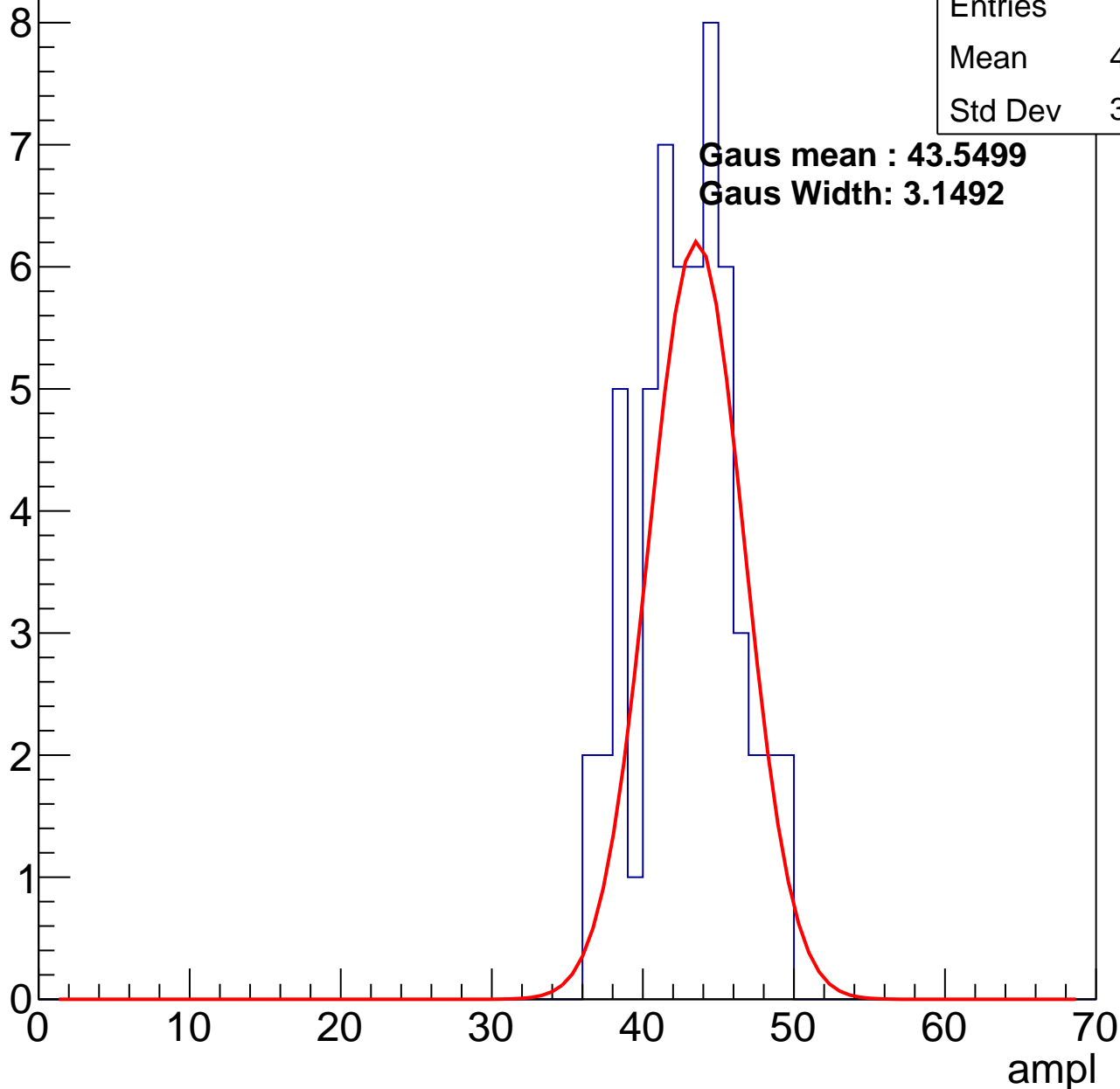
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.46
Std Dev	3.207

Gaus mean : 43.5499

Gaus Width: 3.1492

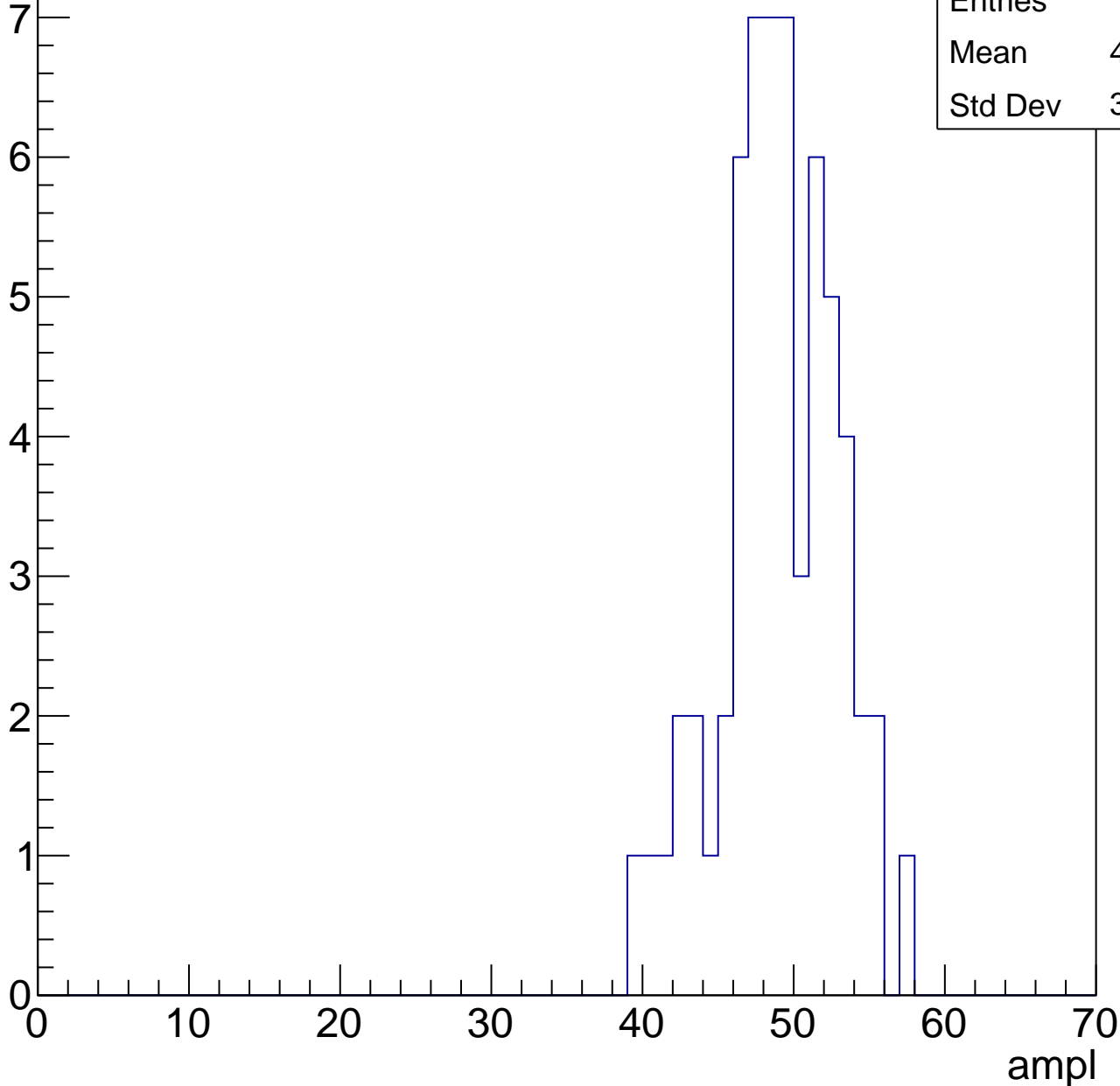


B0L002S, U17-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	48.52
Std Dev	3.806

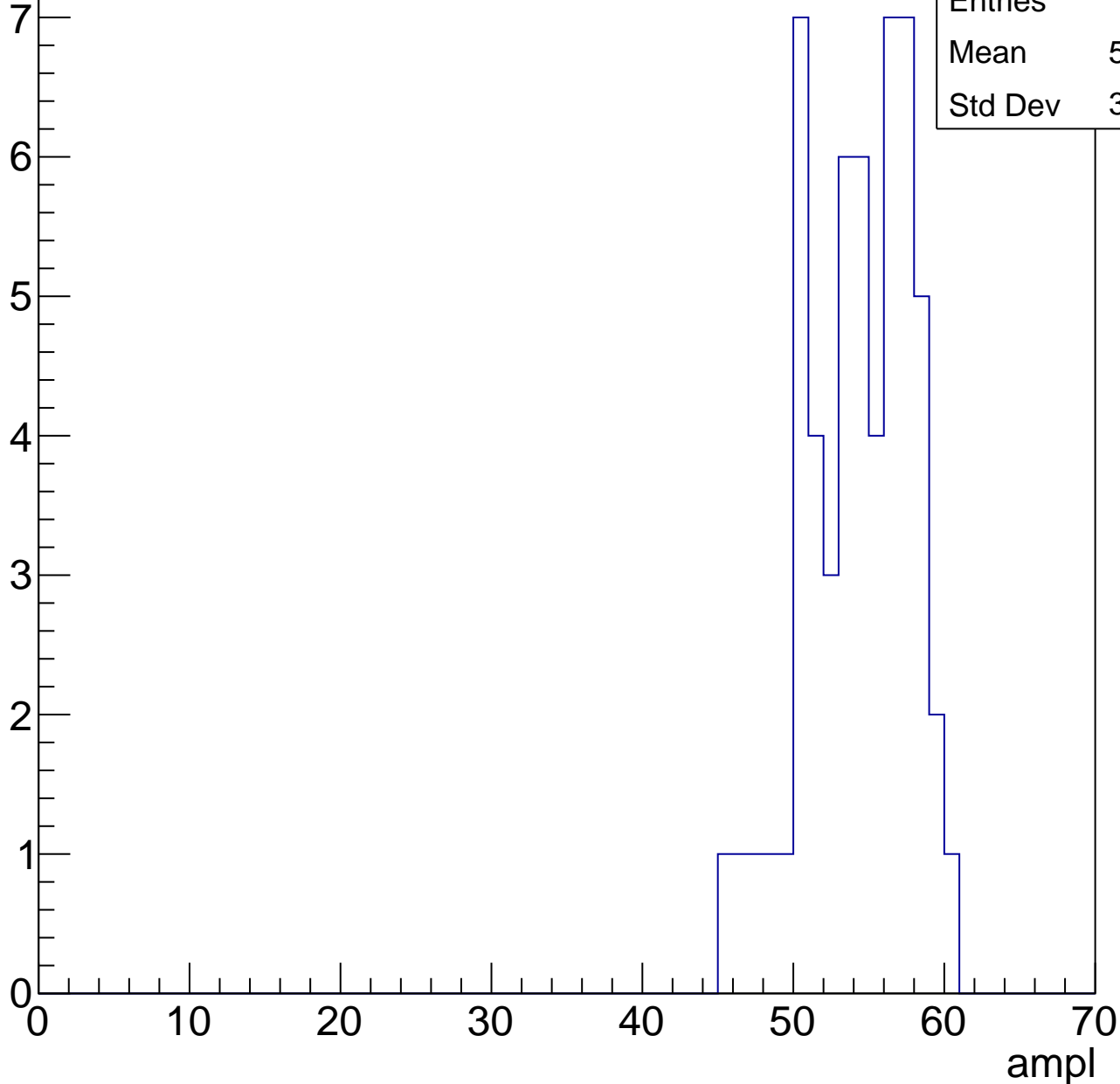


B0L002S, U17-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

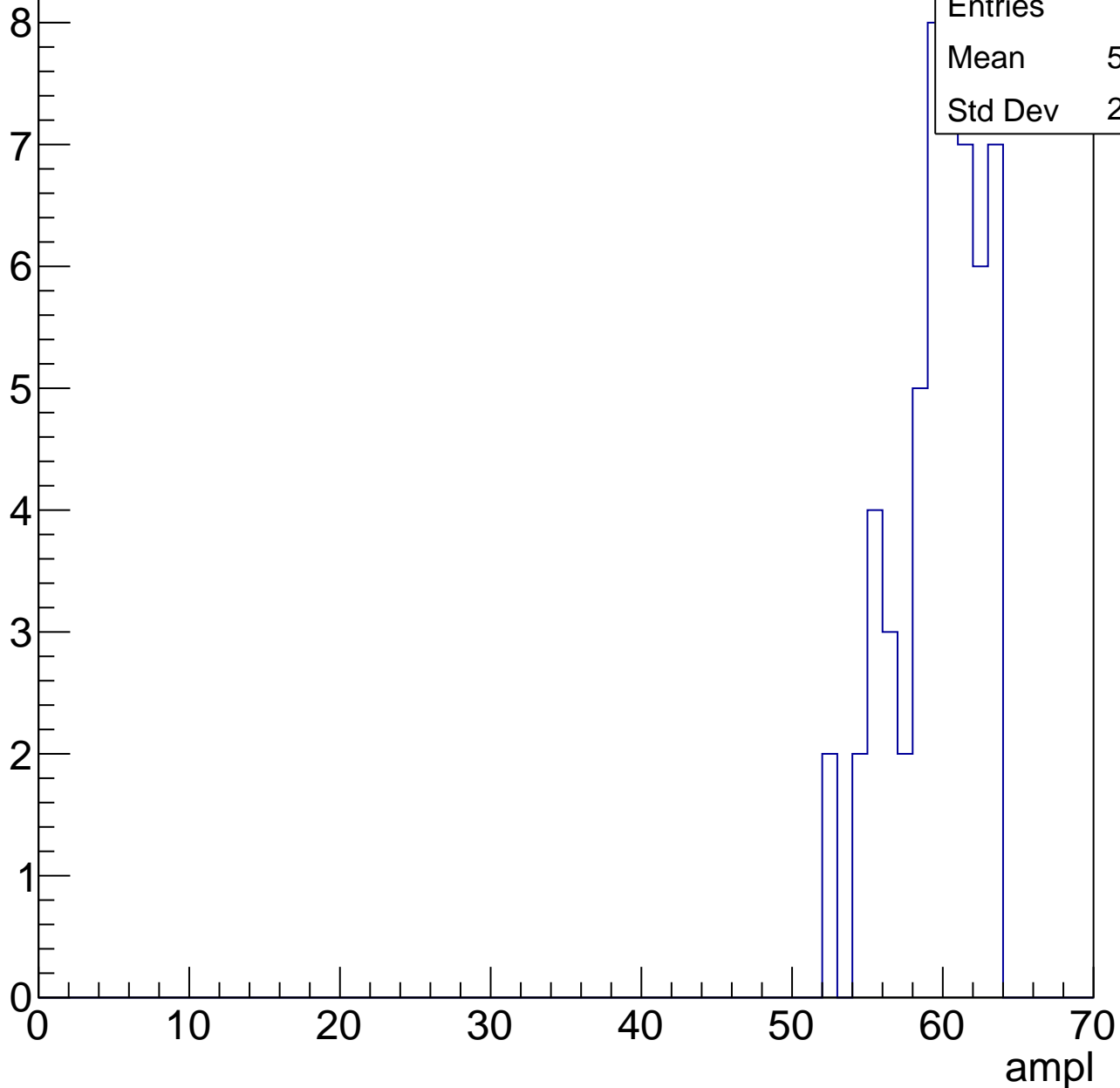
Entries	57
Mean	53.79
Std Dev	3.453



B0L002S, U17-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch73, adc6

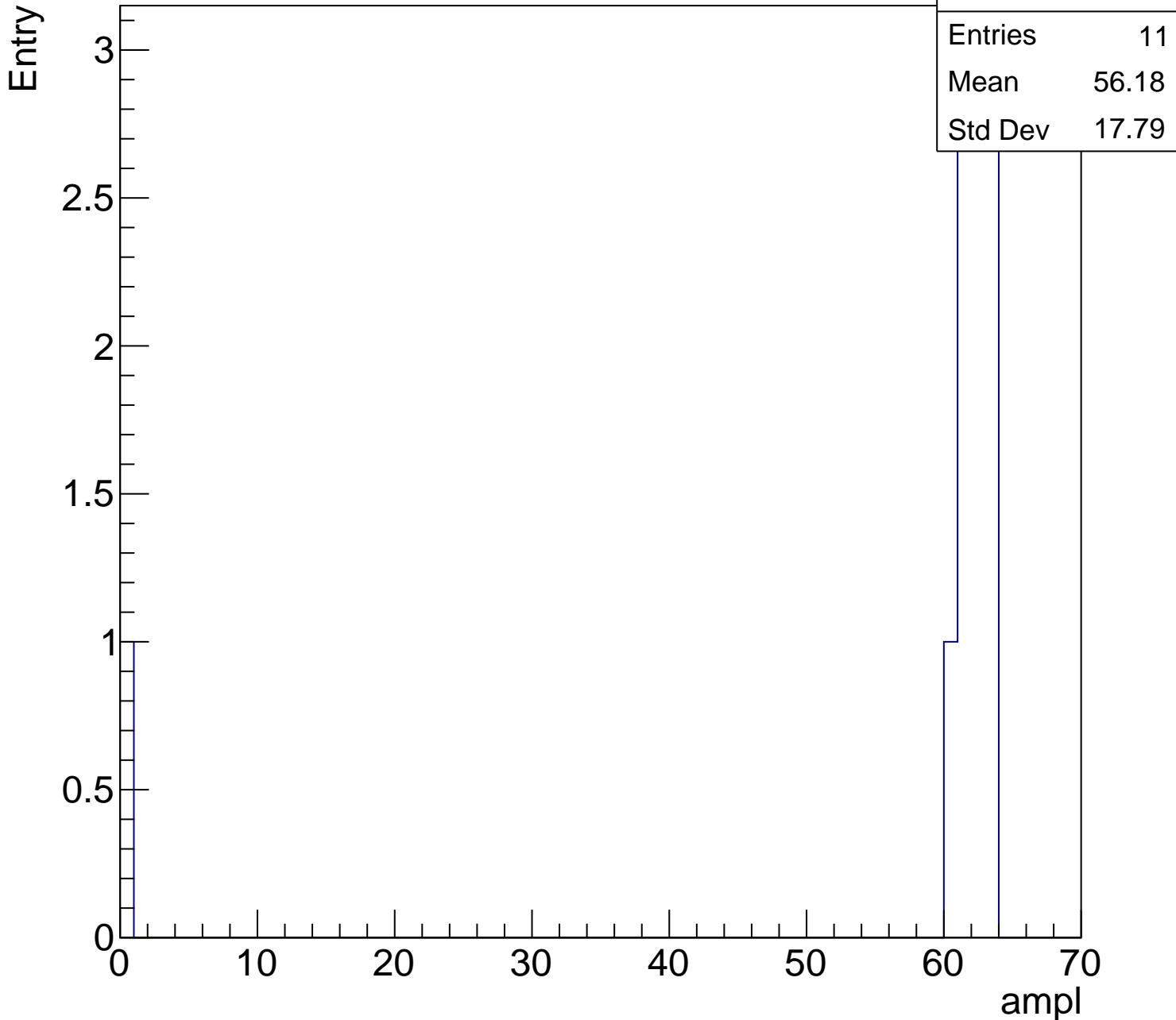
calib_packv5_042523_0143.root, FC#8, port C1

Entry

3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	56.18
Std Dev	17.79

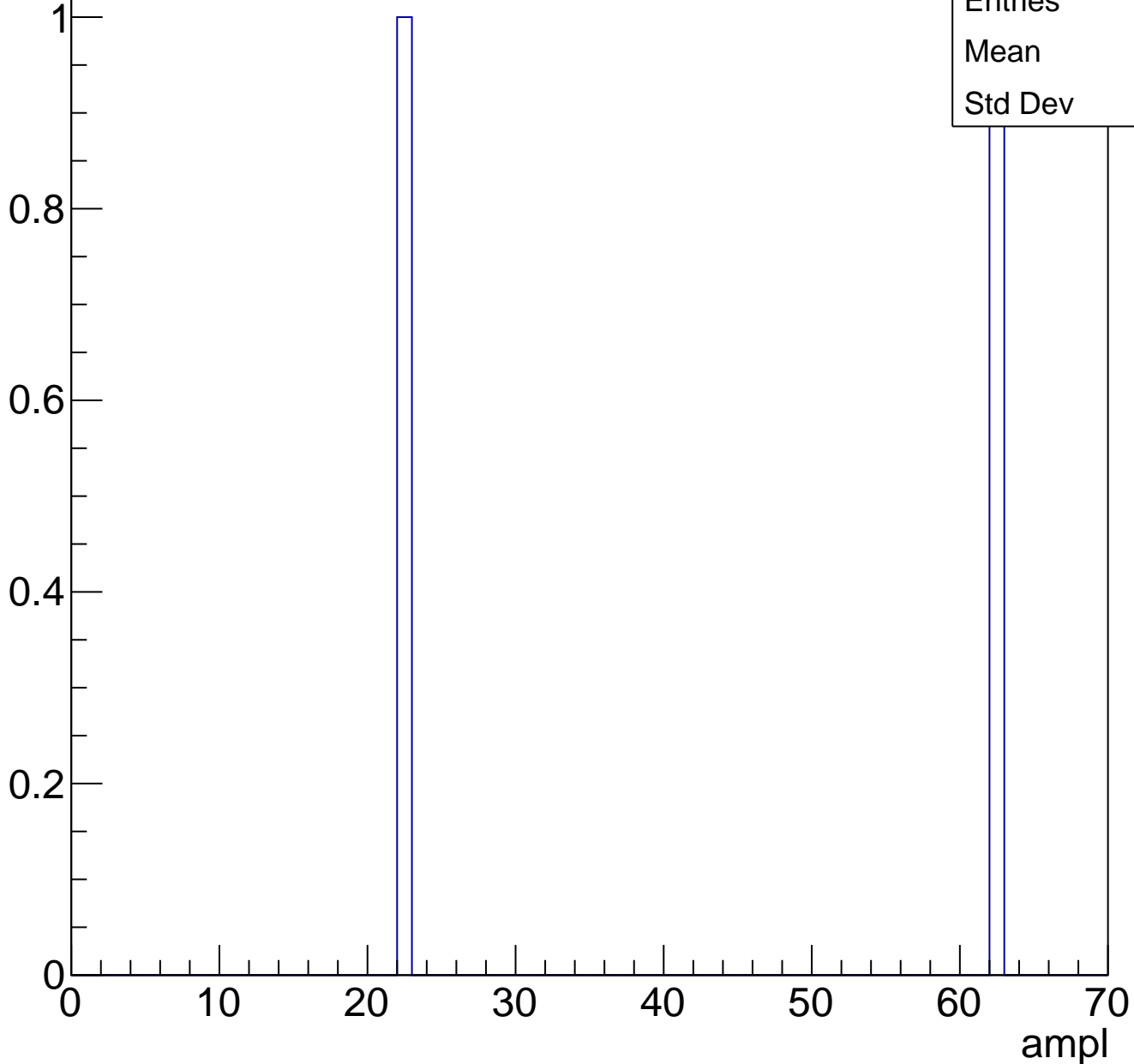
ampl



B0L002S, U17-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch74, adc0

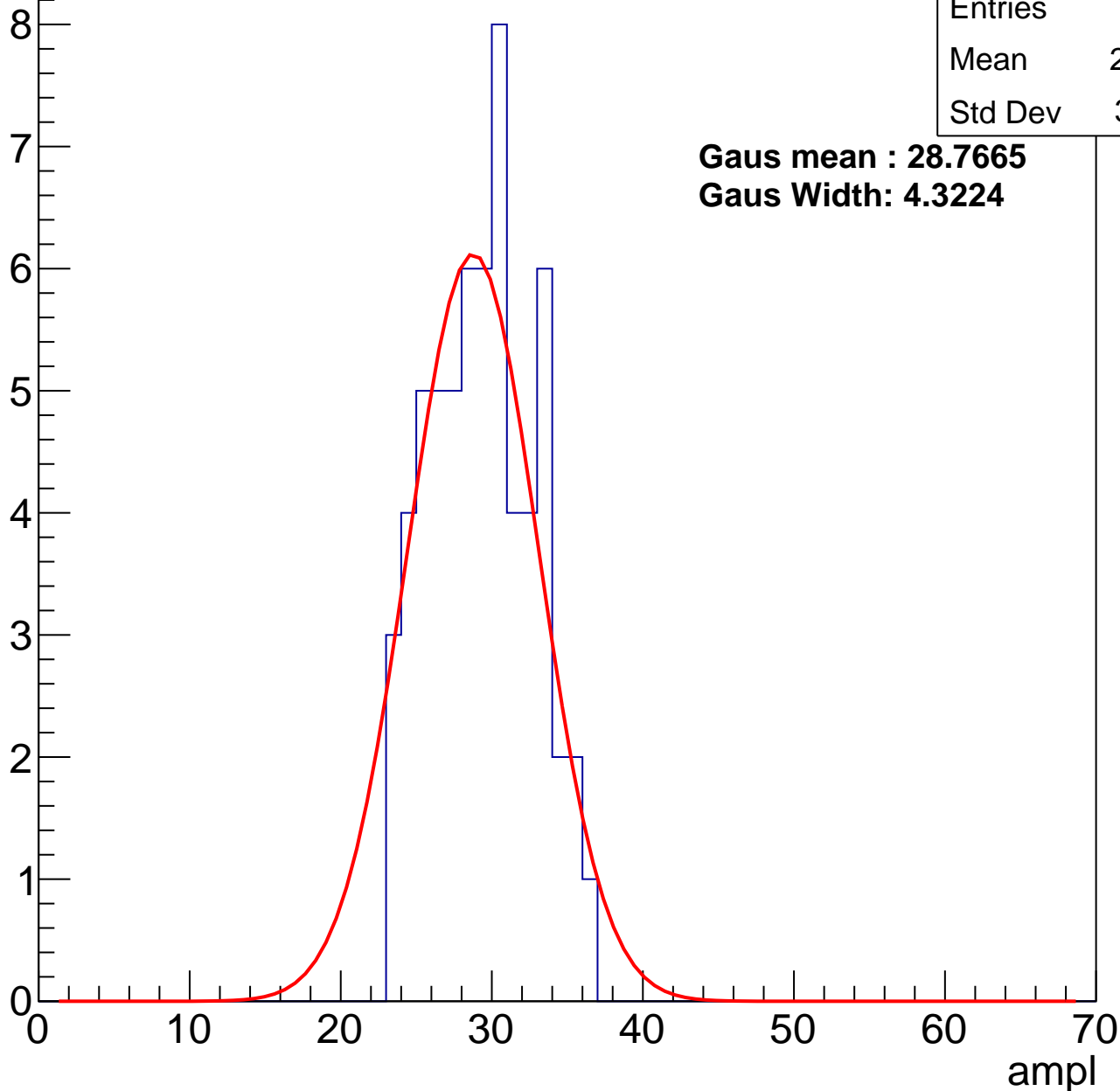
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	28.87
Std Dev	3.351

Gaus mean : 28.7665

Gaus Width: 4.3224



B0L002S, U17-ch74, adc1

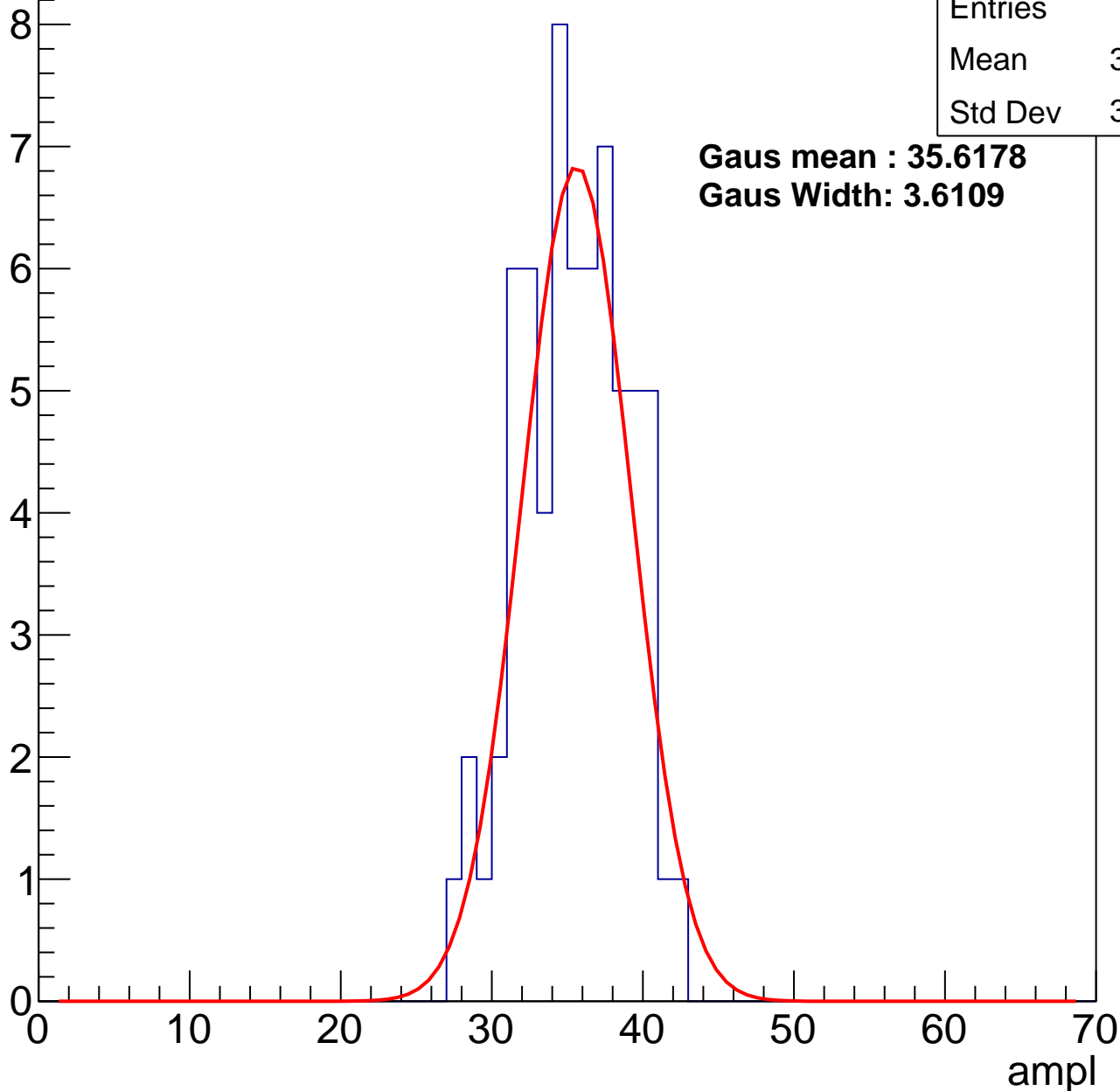
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	34.95
Std Dev	3.462

Gaus mean : 35.6178

Gaus Width: 3.6109



B0L002S, U17-ch74, adc2

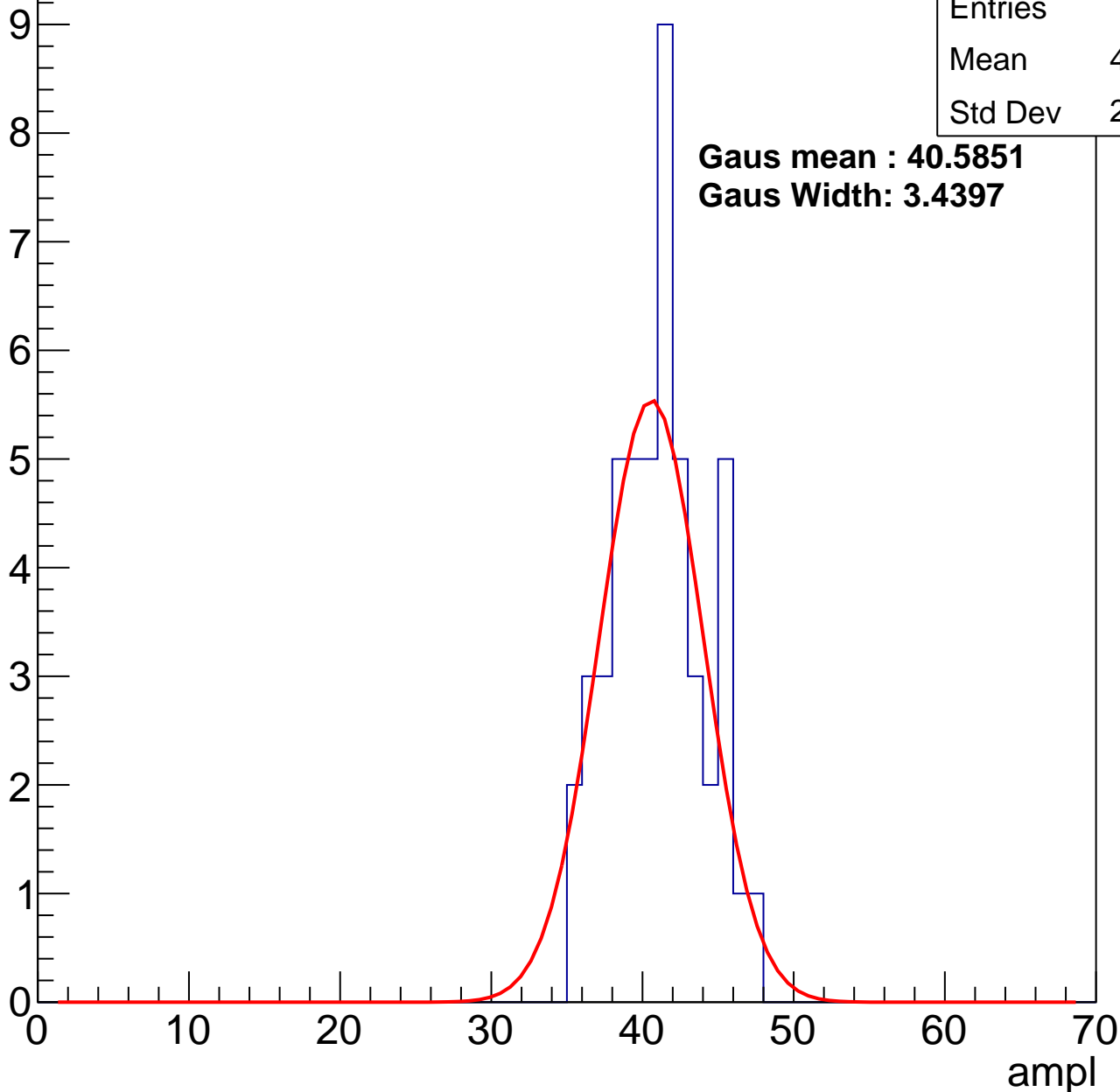
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	40.57
Std Dev	2.969

Gaus mean : 40.5851

Gaus Width: 3.4397



B0L002S, U17-ch74, adc3

calib_packv5_042523_0143.root, FC#8, port C1

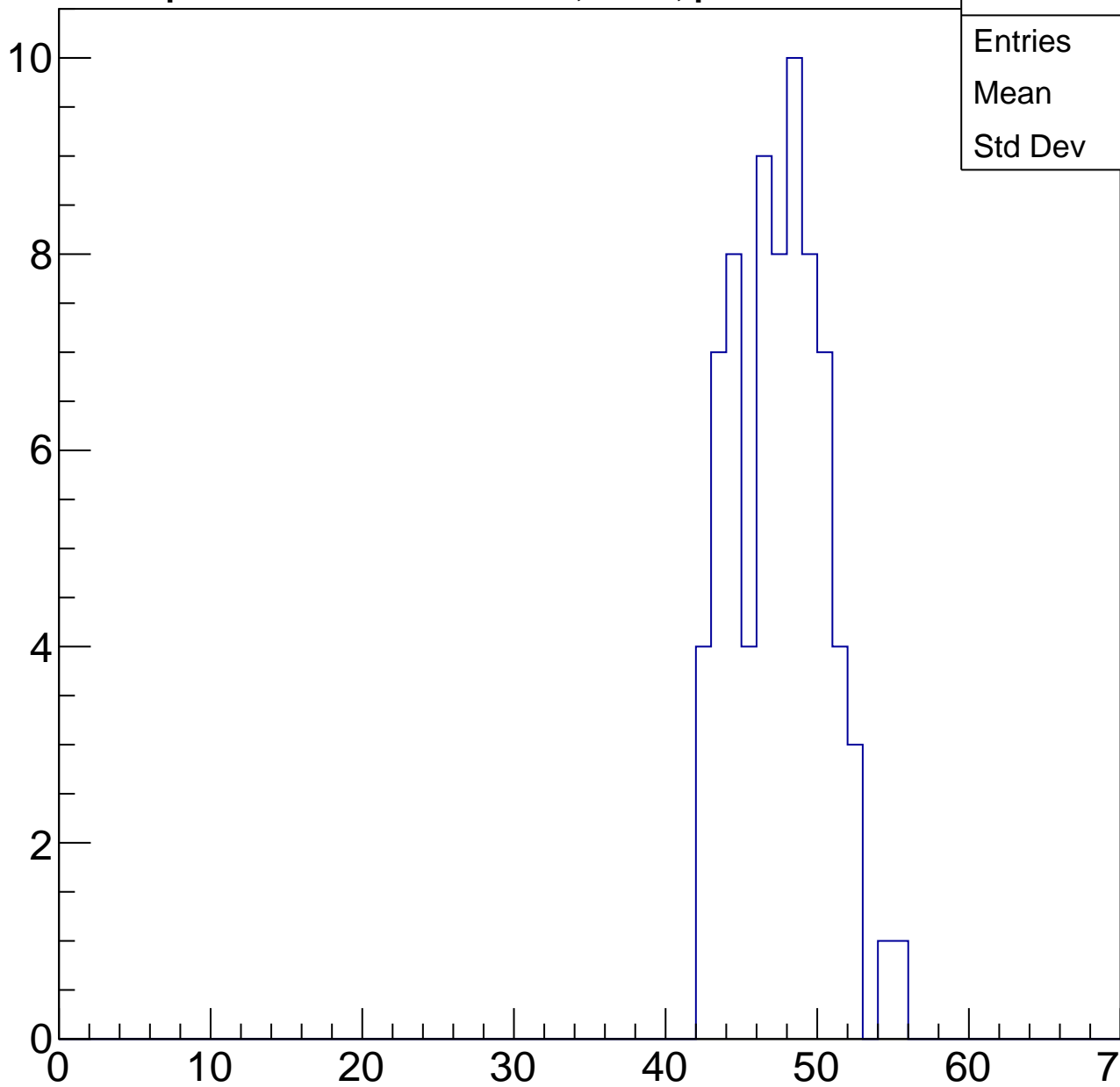
Entries	74
Mean	47.05
Std Dev	3

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

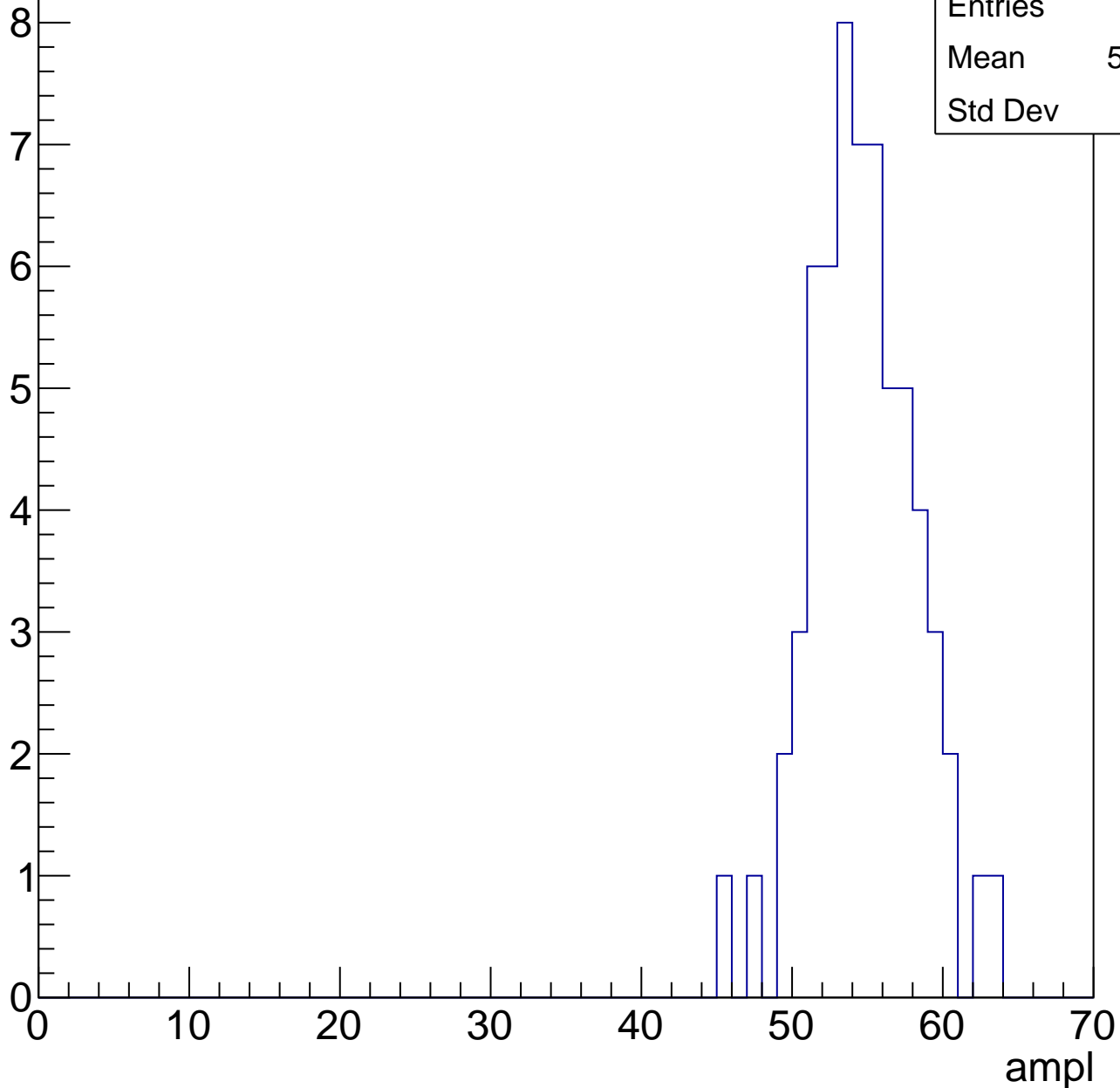


B0L002S, U17-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

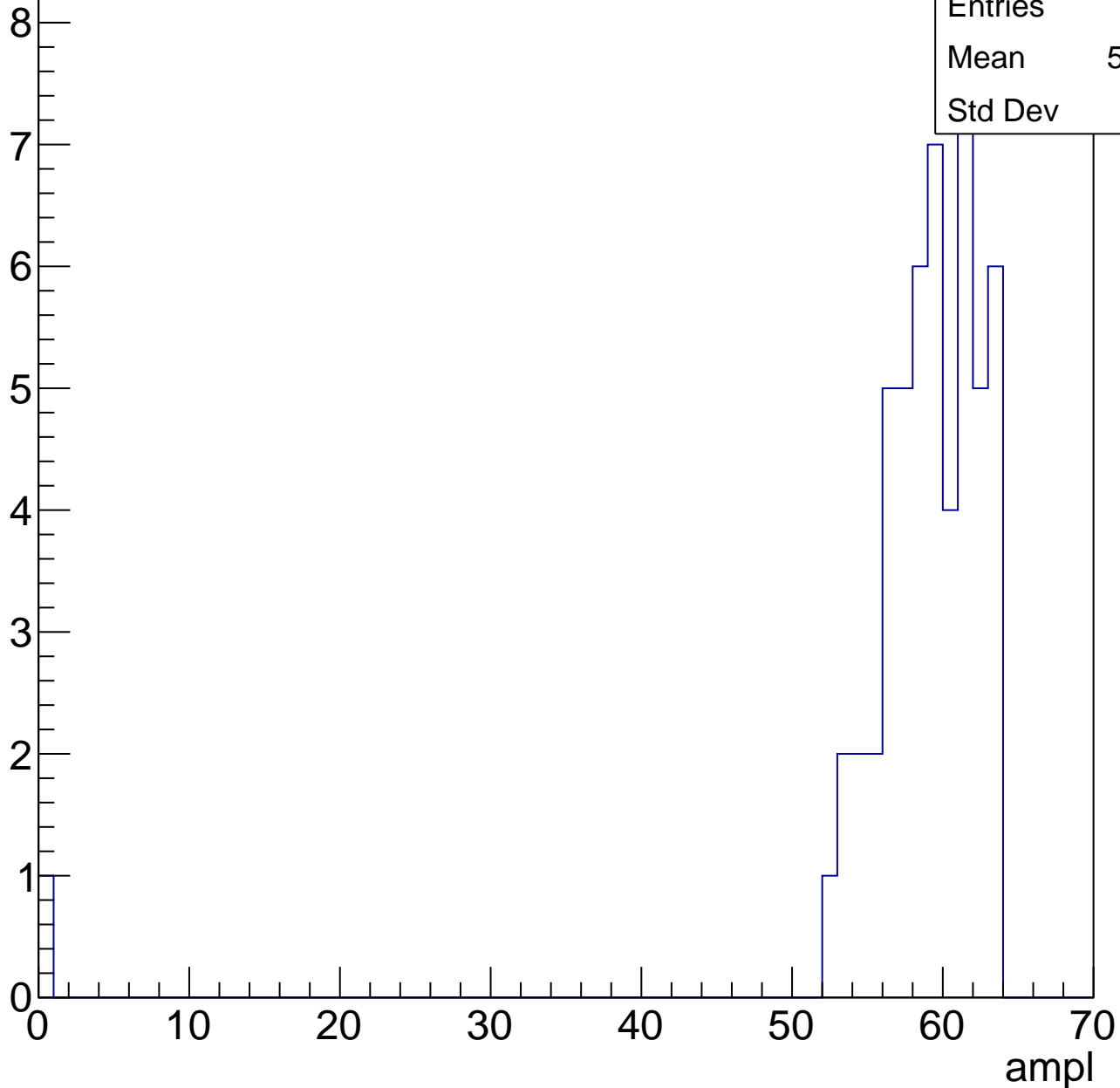
Entries	62
Mean	54.26
Std Dev	3.45



B0L002S, U17-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1

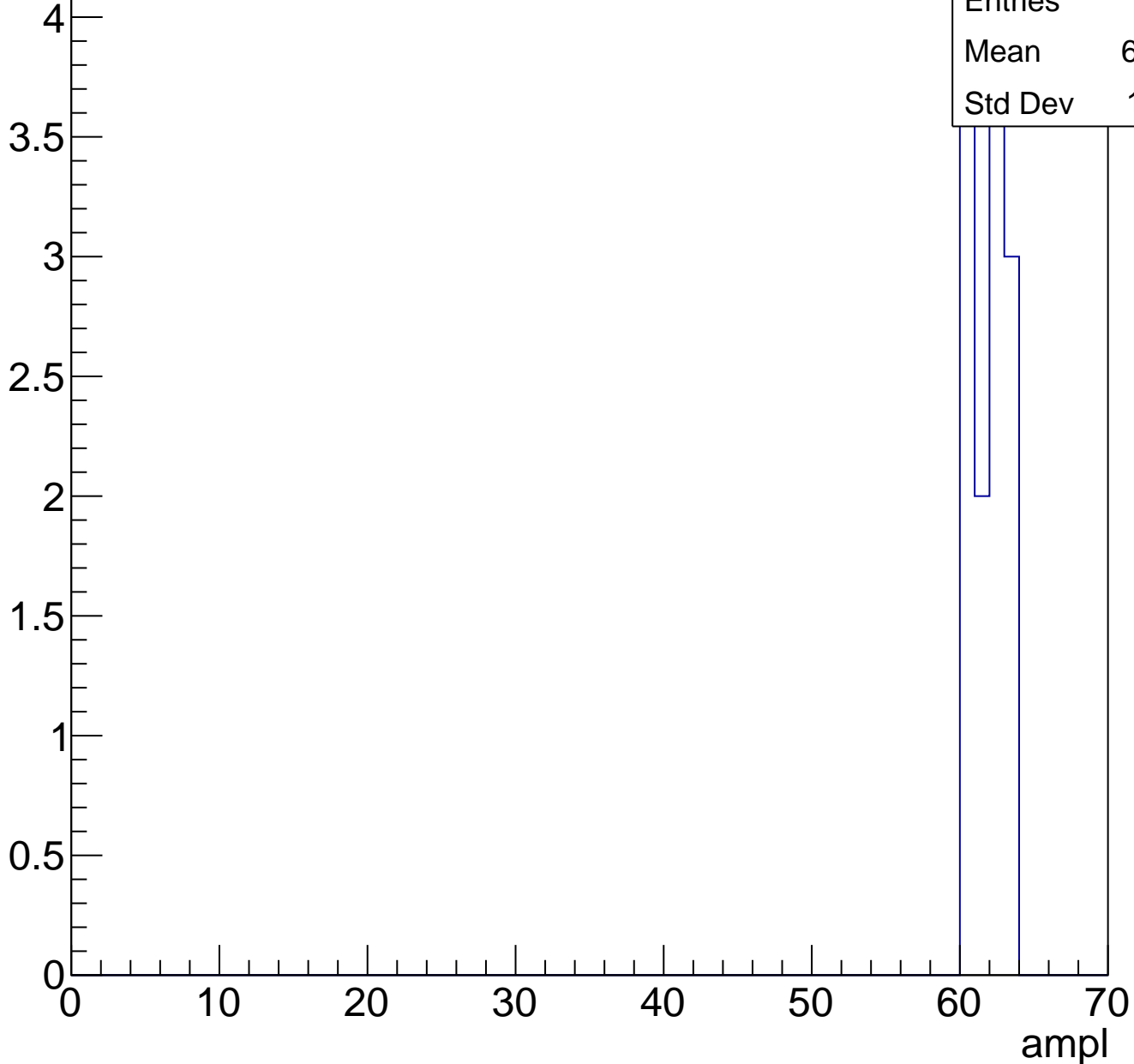
Entry



B0L002S, U17-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L002S, U17-ch75, adc0

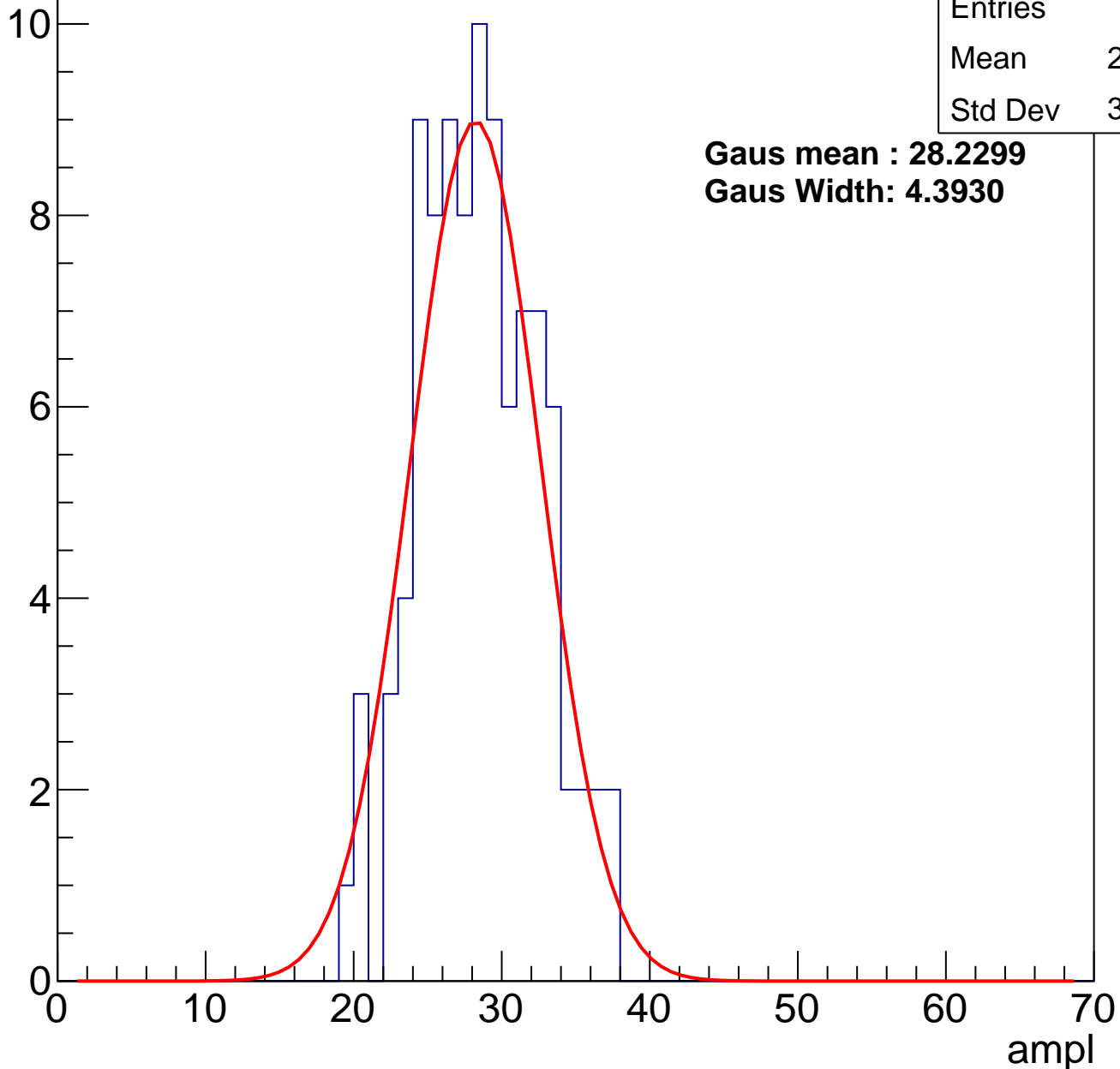
calib_packv5_042523_0143.root, FC#8, port C1

Entries	98
Mean	28.03
Std Dev	3.988

Gaus mean : 28.2299

Gaus Width: 4.3930

Entry



B0L002S, U17-ch75, adc1

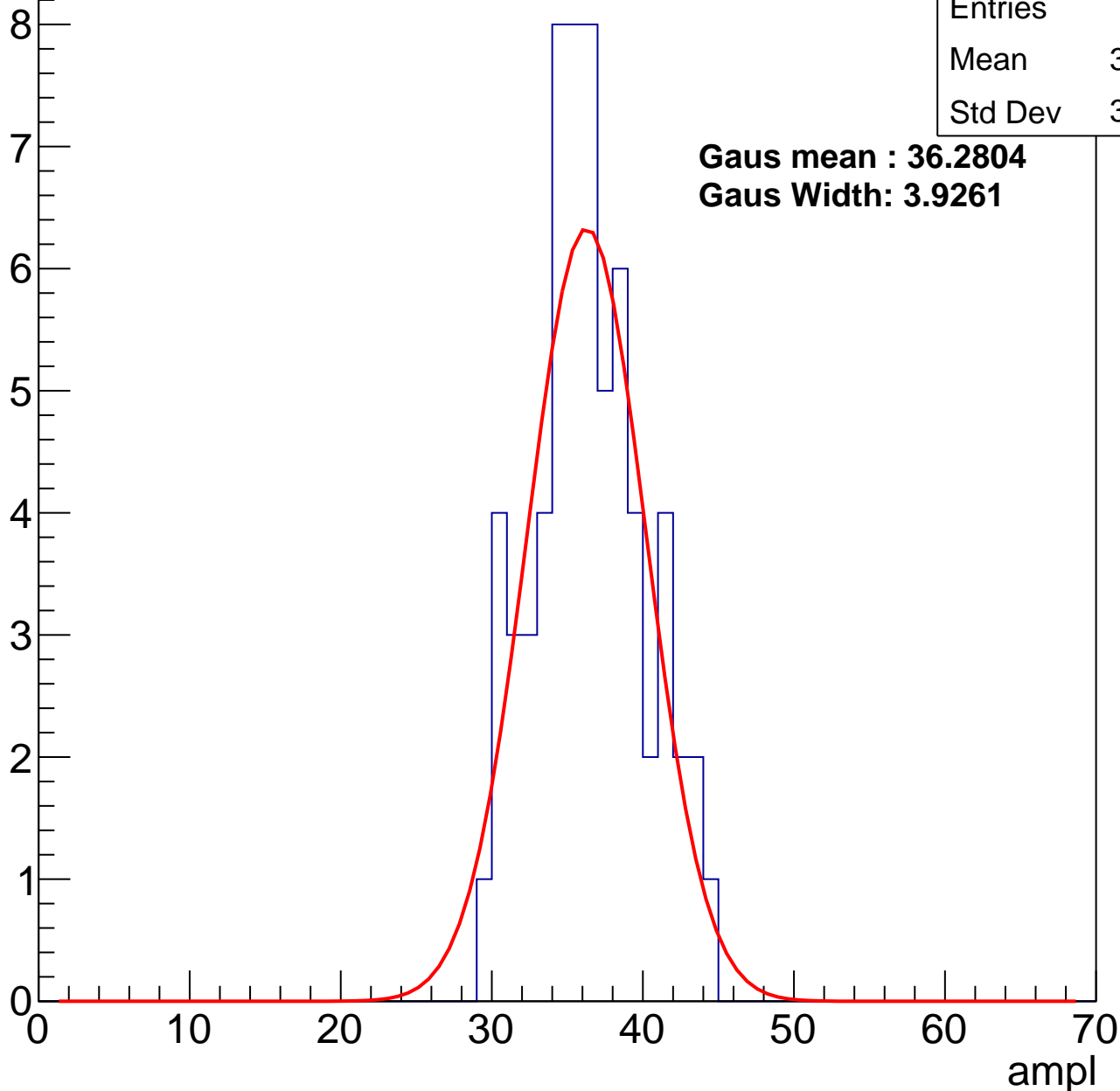
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.95
Std Dev	3.558

Gaus mean : 36.2804

Gaus Width: 3.9261



B0L002S, U17-ch75, adc2

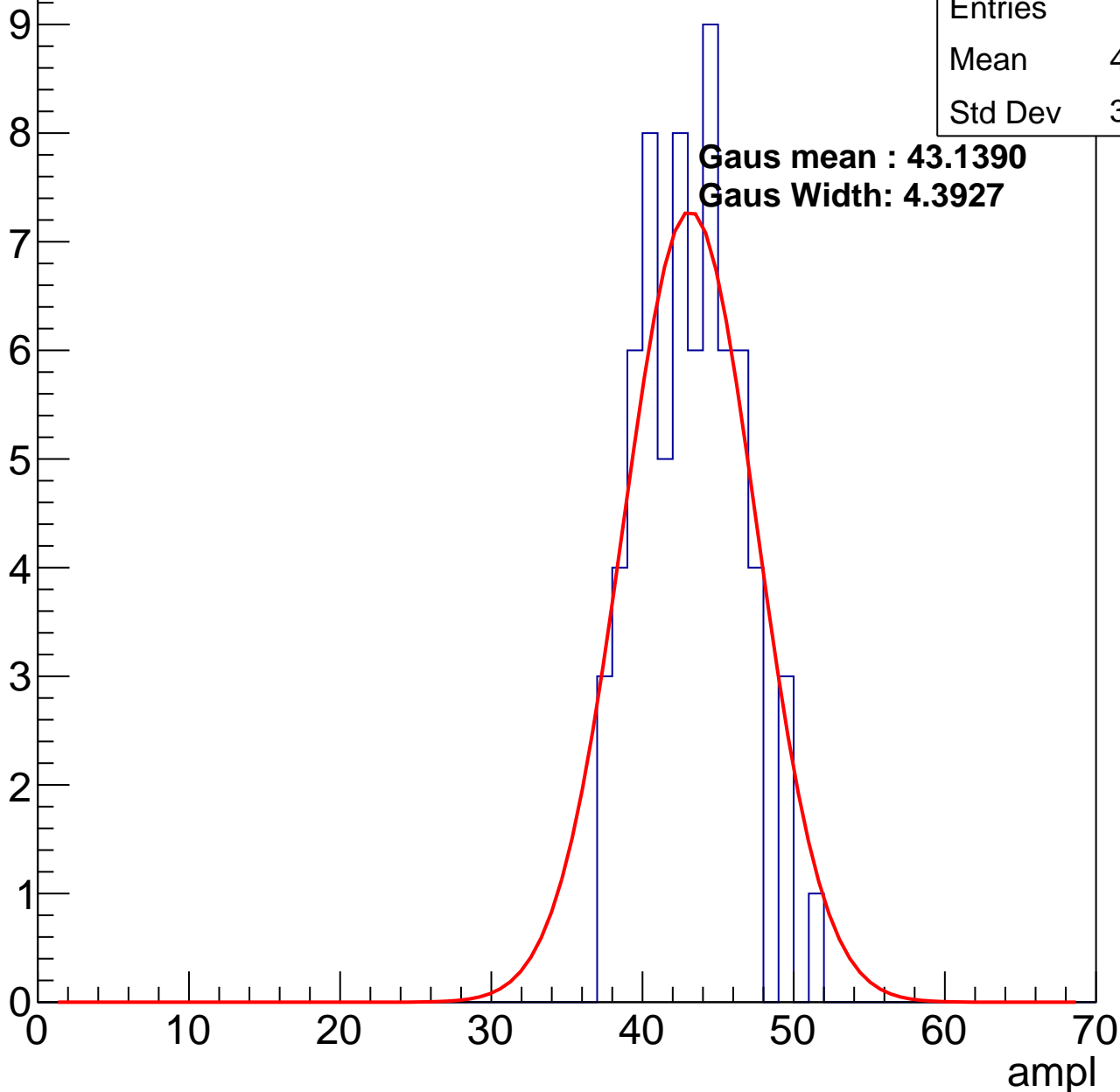
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	42.67
Std Dev	3.229

Gaus mean : 43.1390

Gaus Width: 4.3927

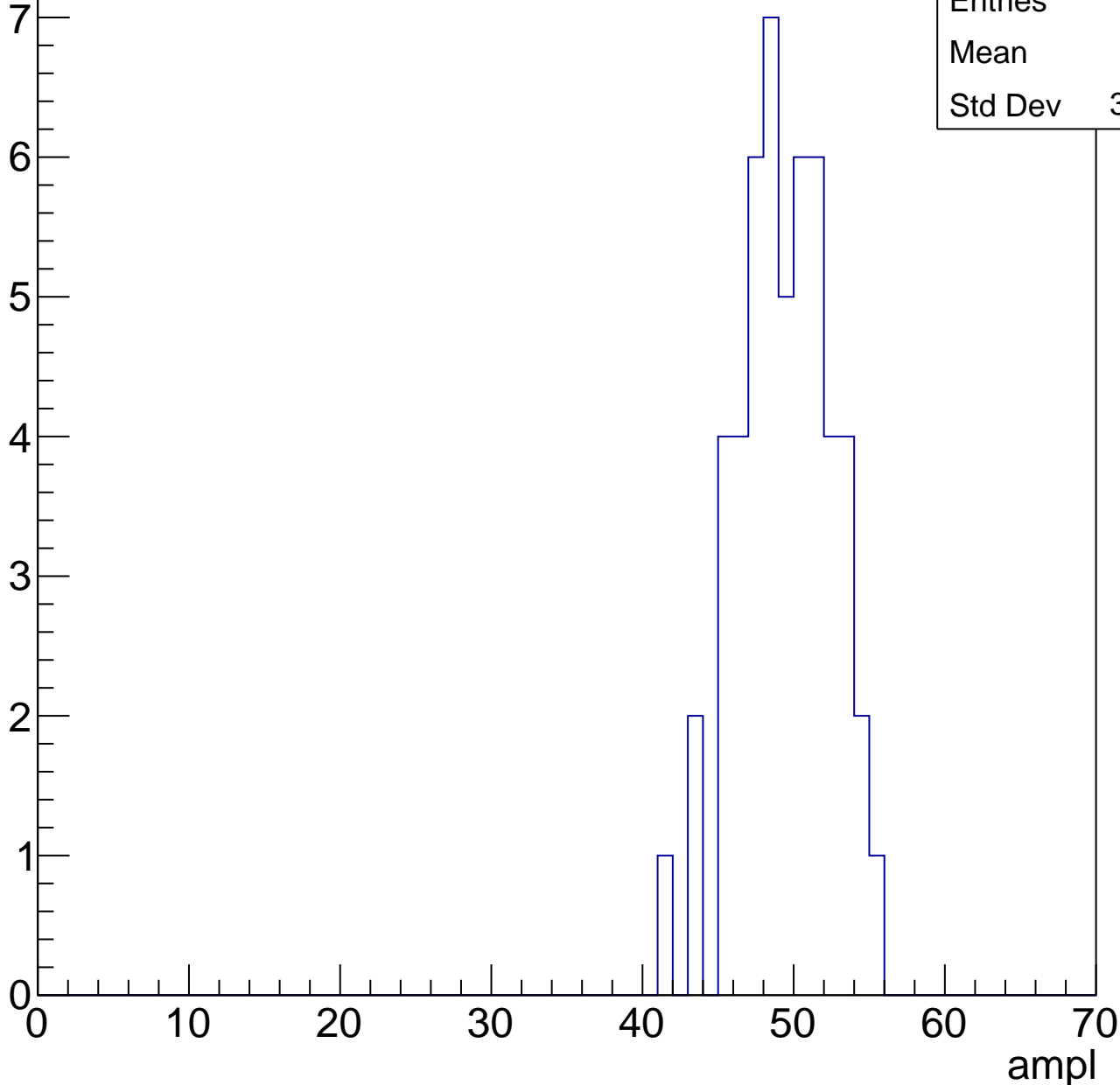


B0L002S, U17-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

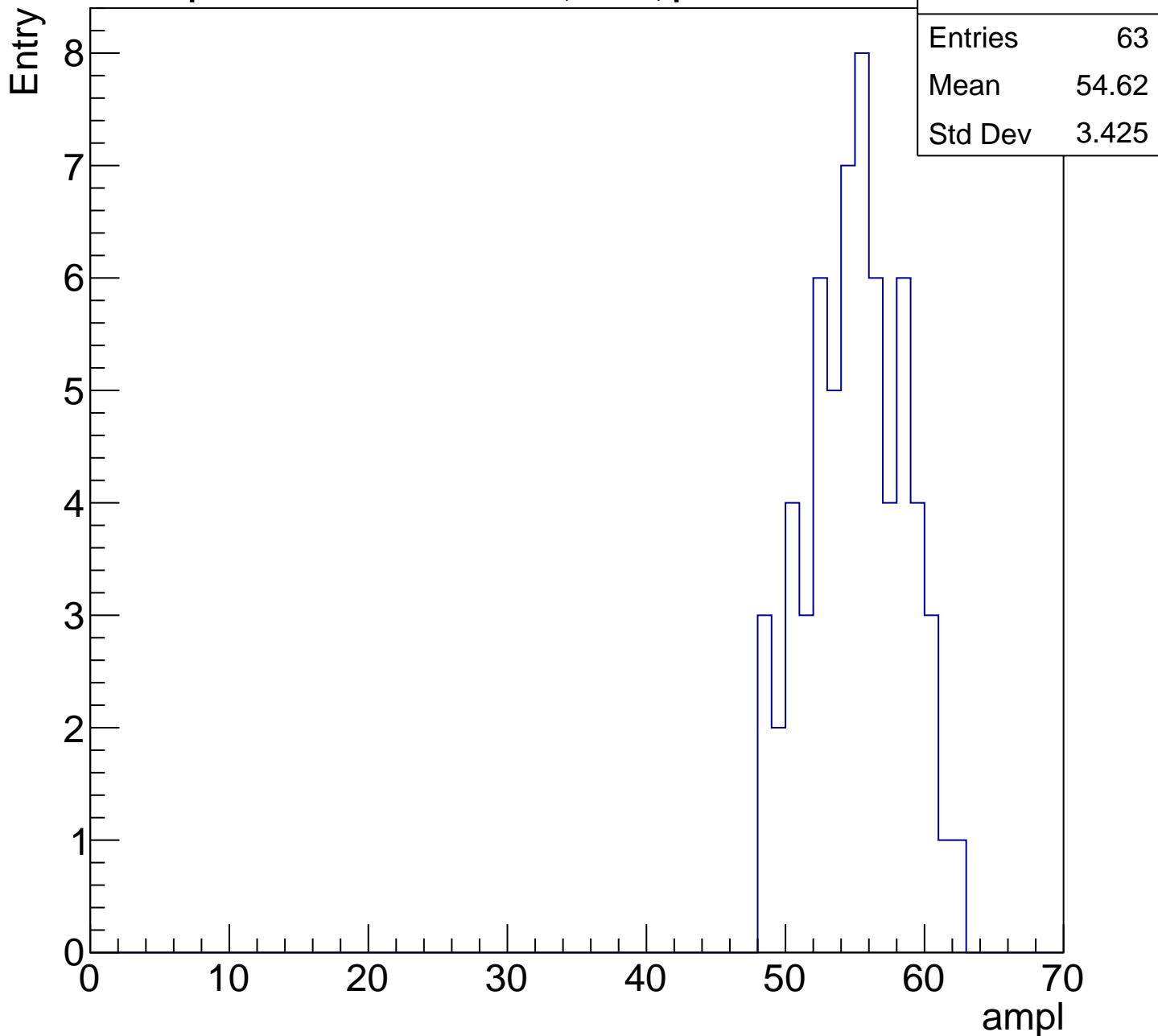
Entry

Entries	52
Mean	48.9
Std Dev	3.046



B0L002S, U17-ch75, adc4

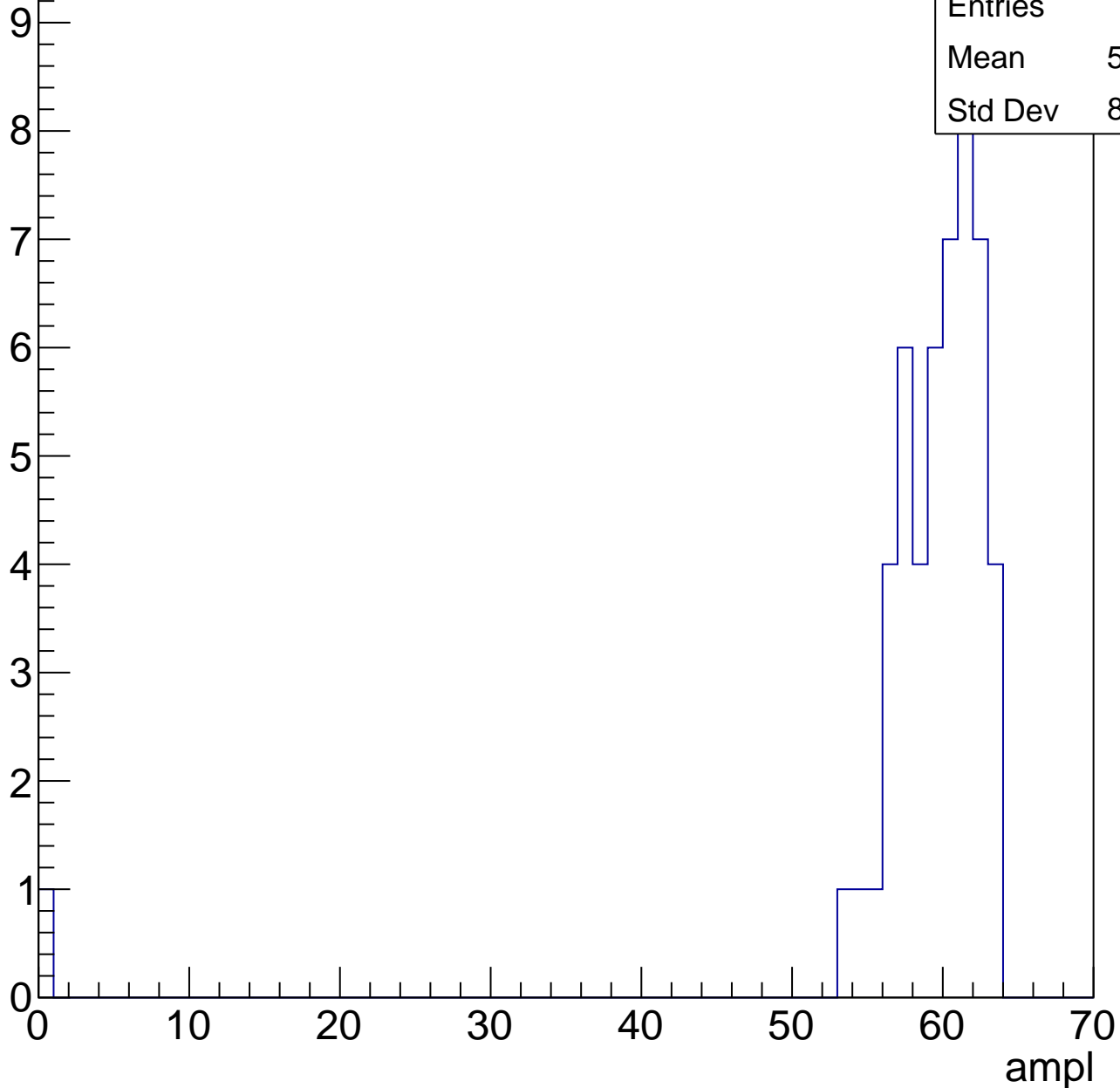
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U17-ch75, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch76, adc0

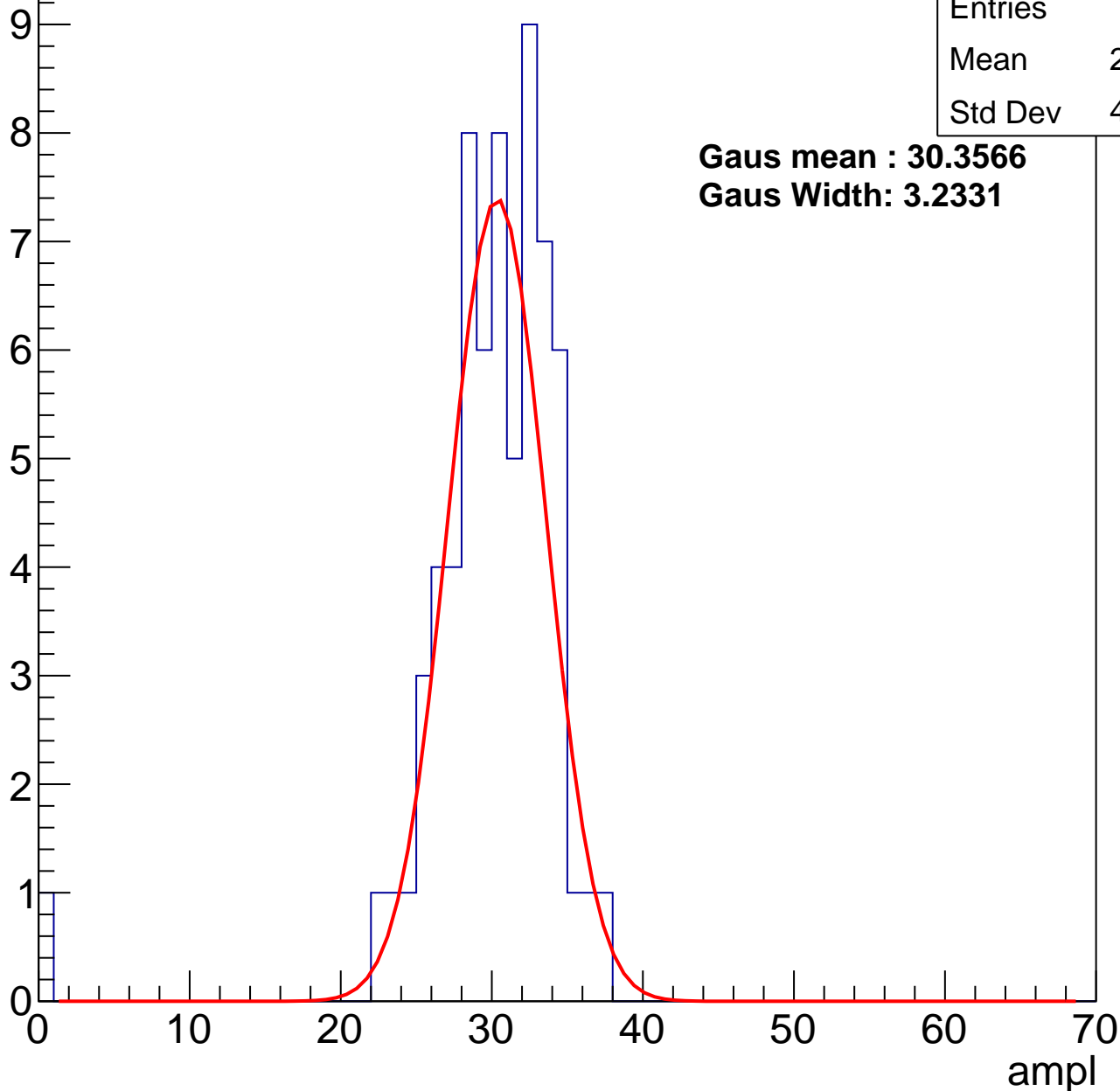
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.55
Std Dev	4.823

Gaus mean : 30.3566

Gaus Width: 3.2331



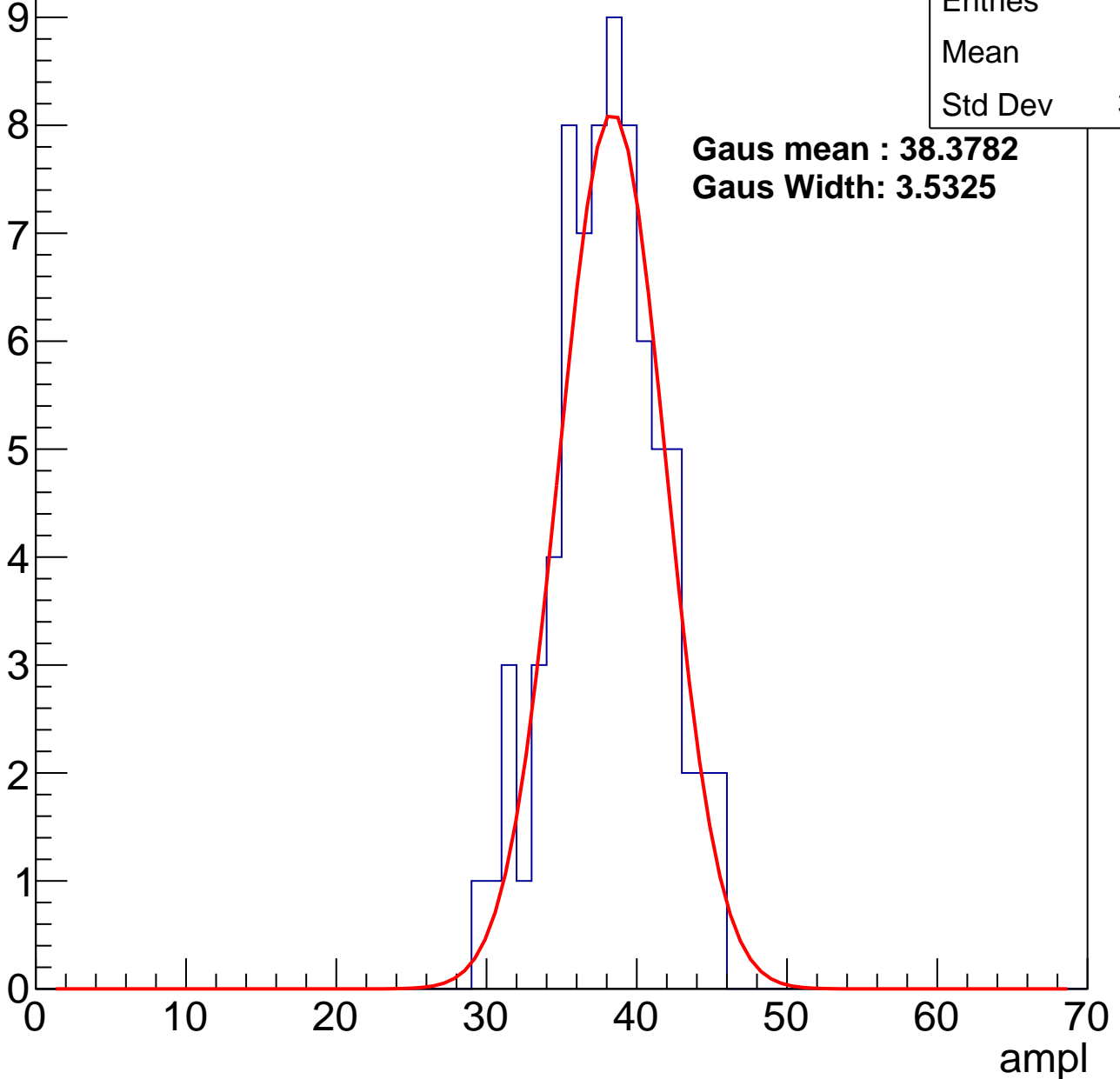
B0L002S, U17-ch76, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.6
Std Dev	3.54

Gaus mean : 38.3782
Gaus Width: 3.5325

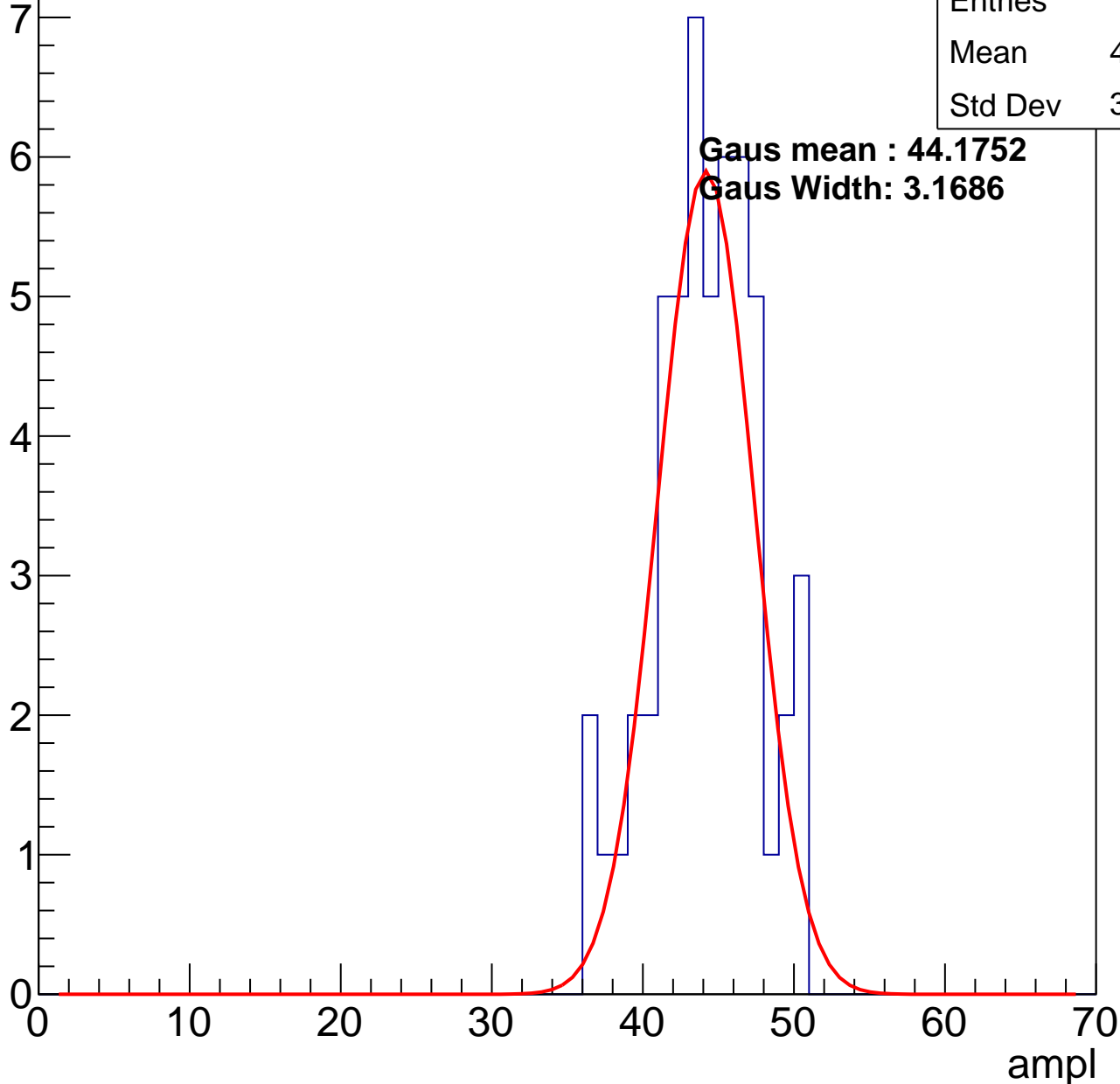


B0L002S, U17-ch76, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.74
Std Dev	3.399

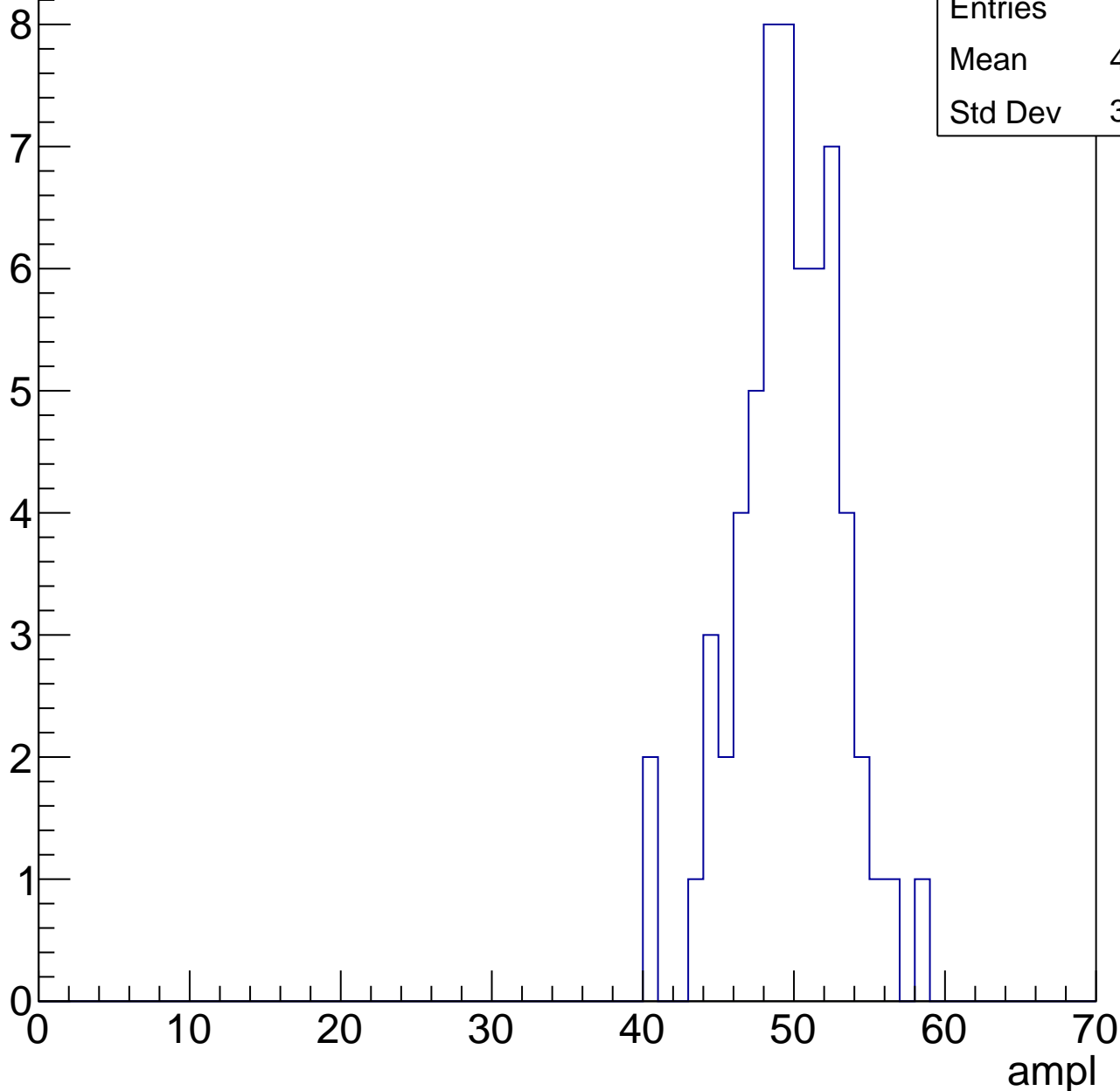


B0L002S, U17-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.16
Std Dev	3.484

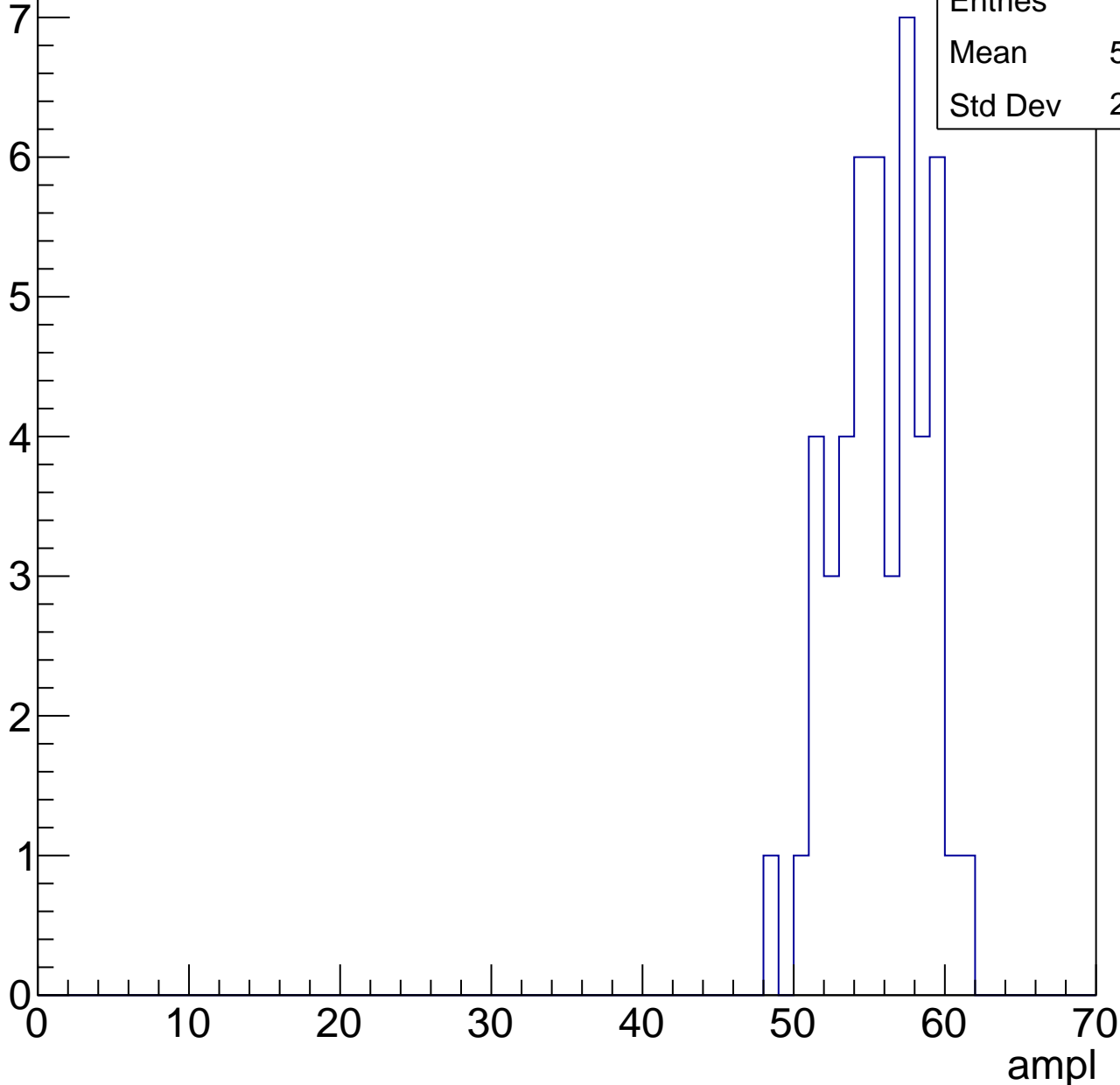


B0L002S, U17-ch76, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	55.28
Std Dev	2.944

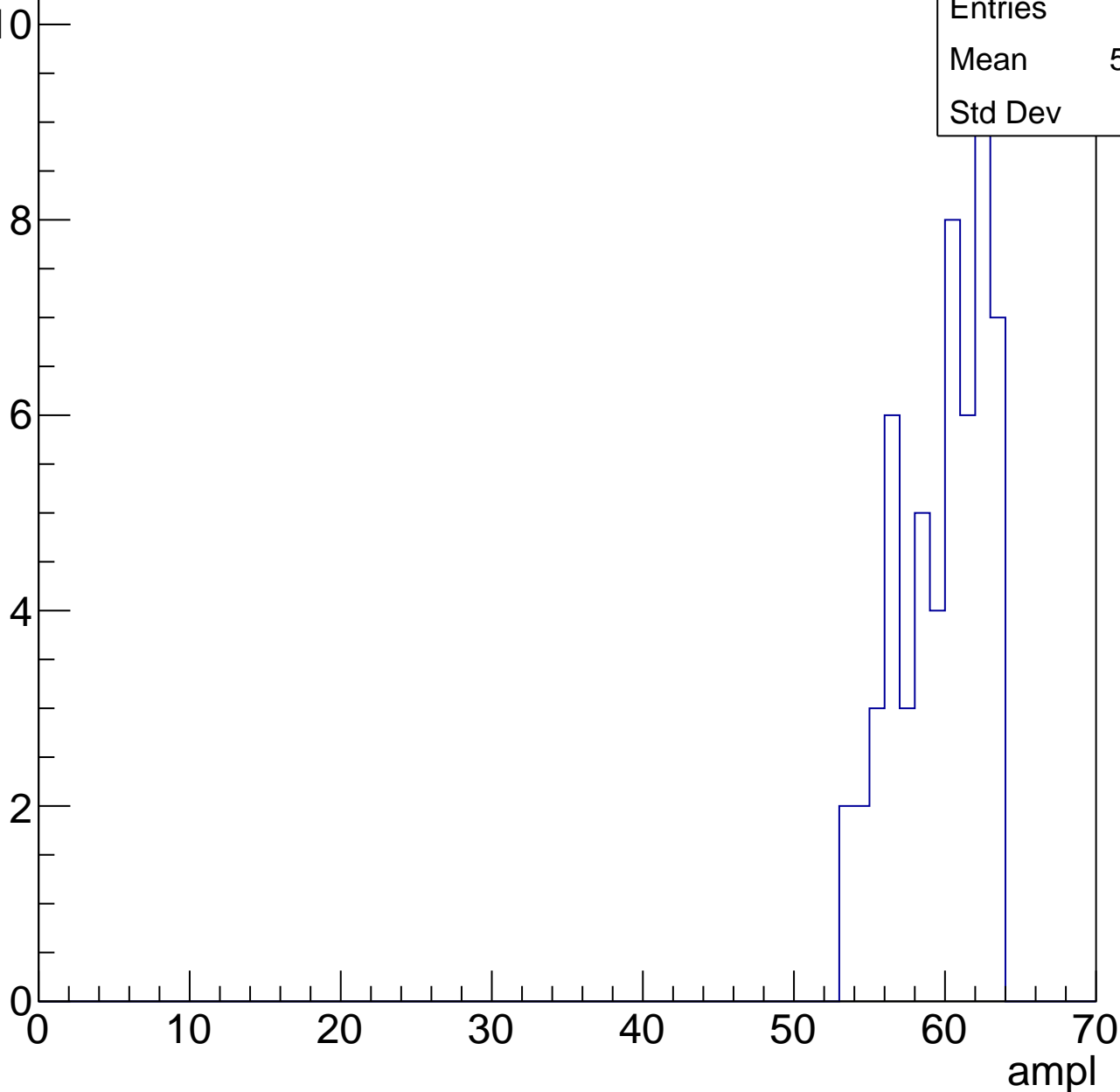


B0L002S, U17-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

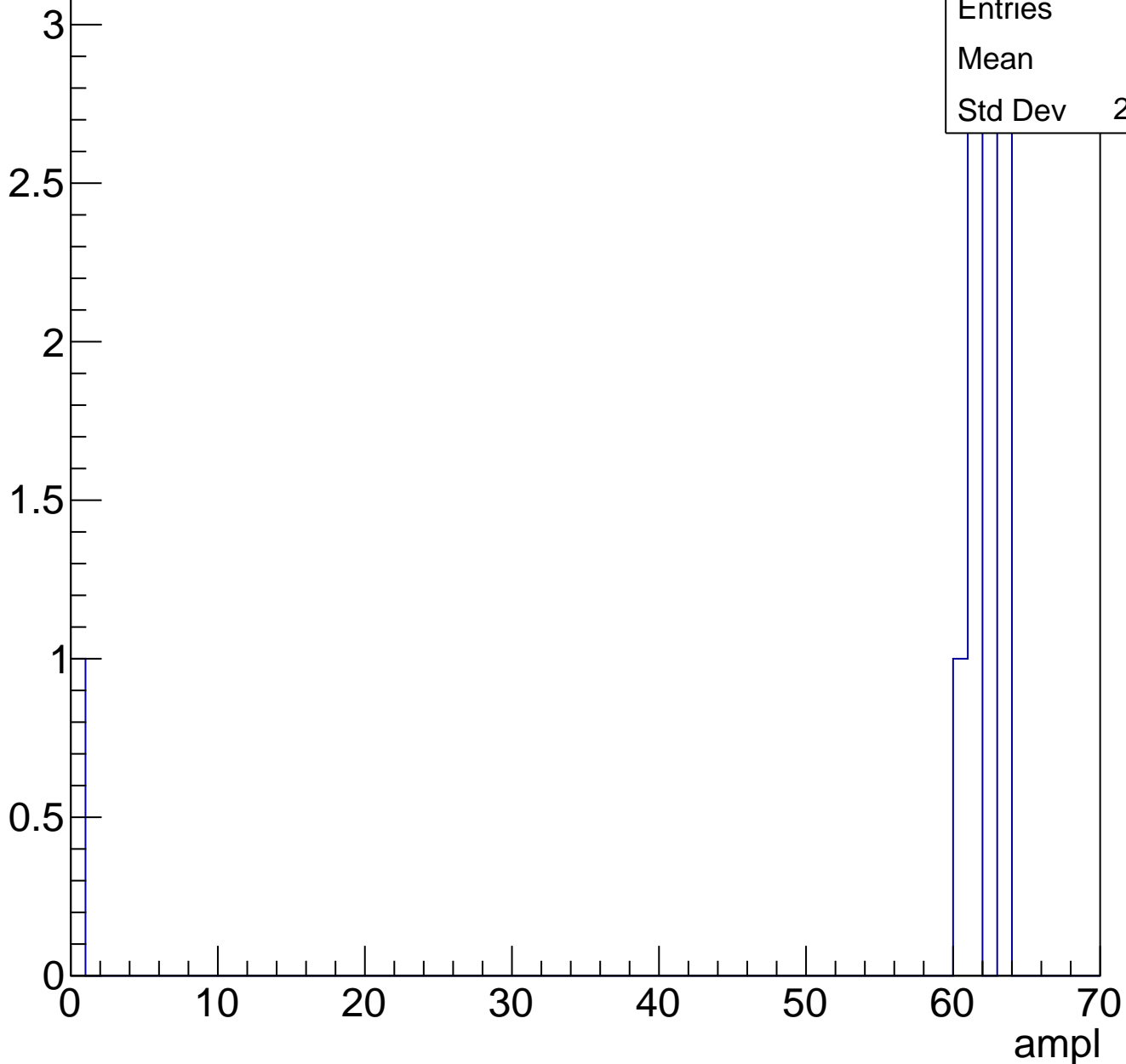
Entries	56
Mean	59.27
Std Dev	2.9



B0L002S, U17-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	8
Mean	54
Std Dev	20.44

B0L002S, U17-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch77, adc0

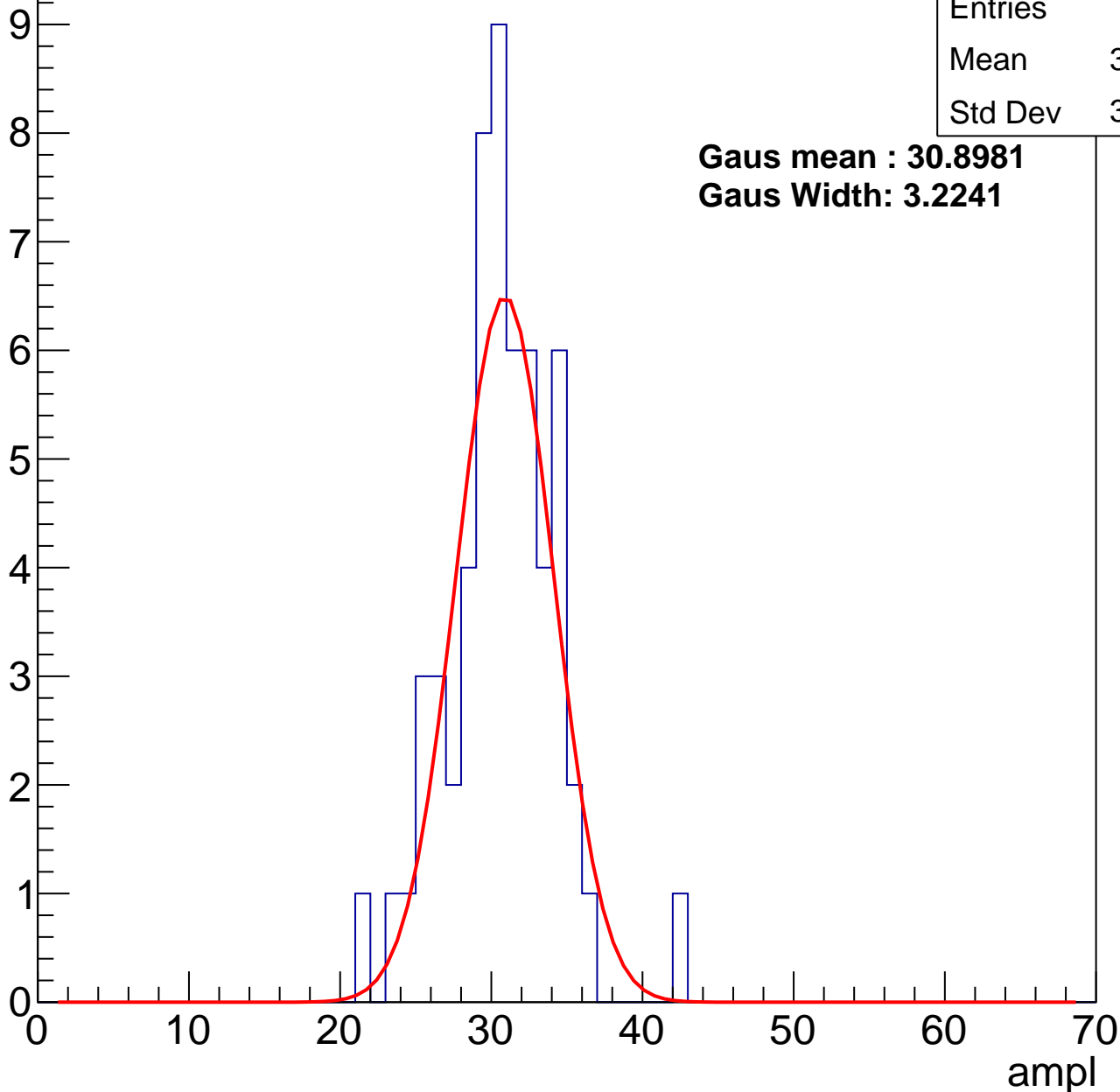
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	30.19
Std Dev	3.526

Gaus mean : 30.8981

Gaus Width: 3.2241



B0L002S, U17-ch77, adc1

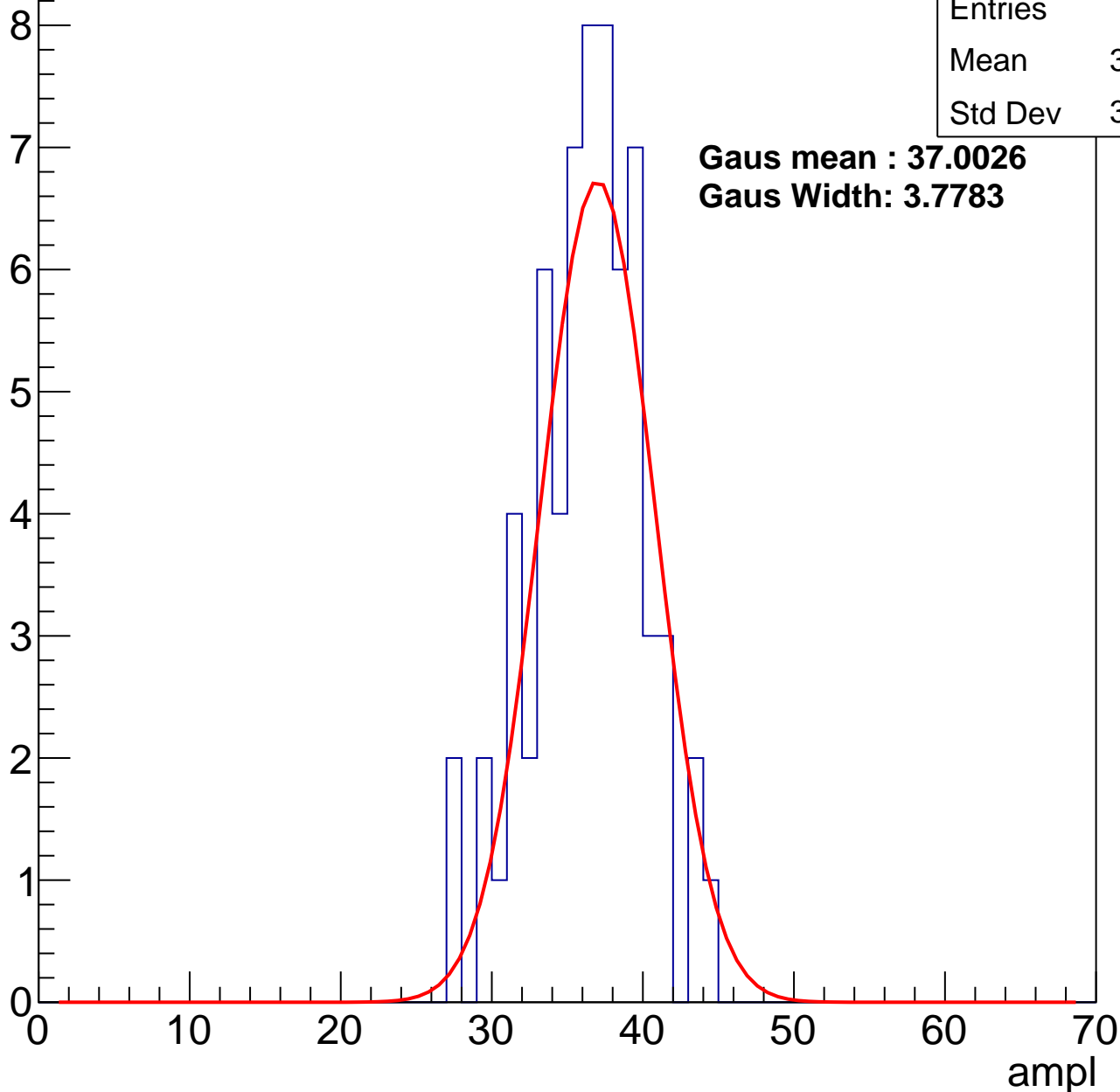
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	35.86
Std Dev	3.659

Gaus mean : 37.0026

Gaus Width: 3.7783



B0L002S, U17-ch77, adc2

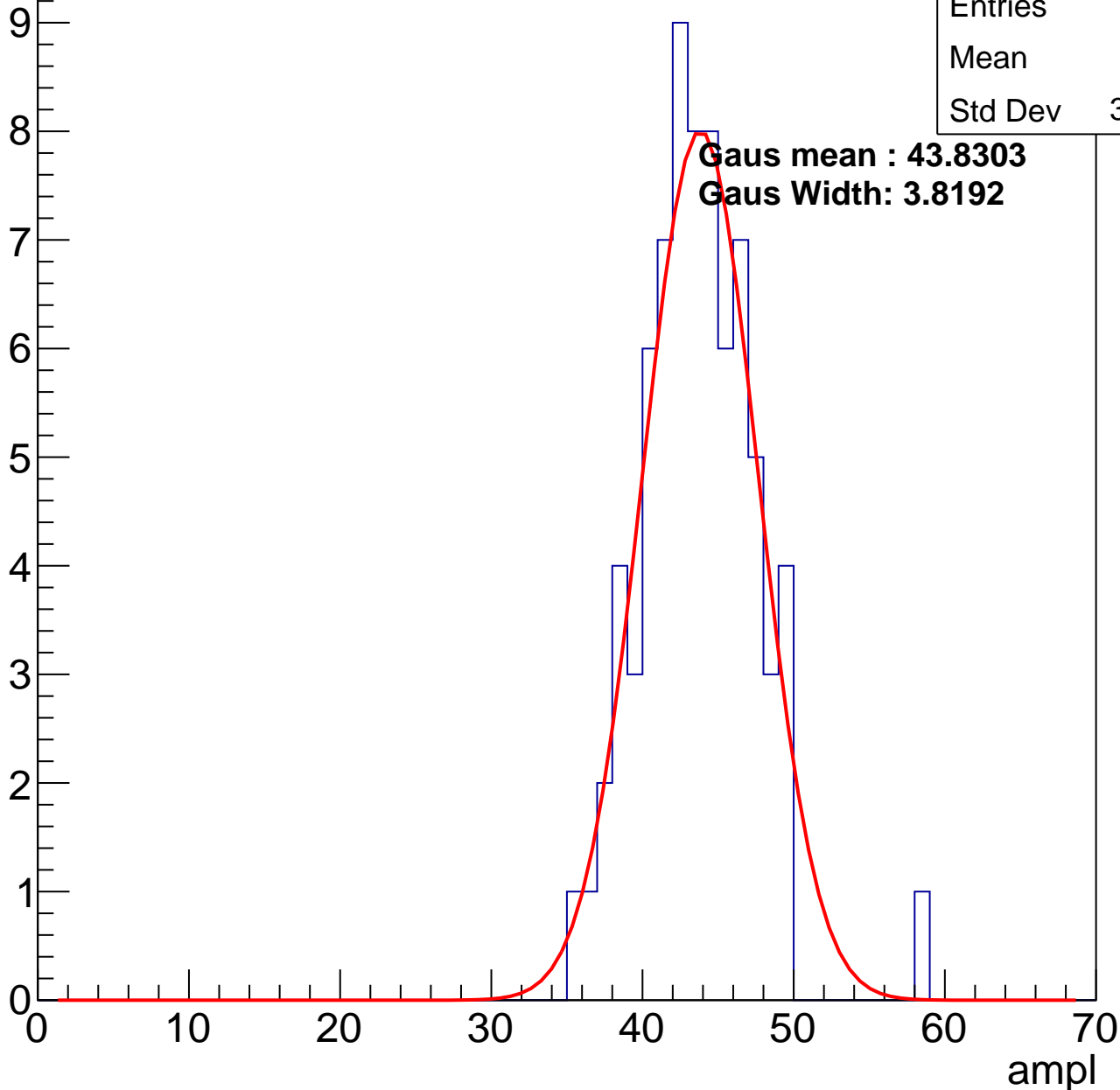
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	43.2
Std Dev	3.724

Gaus mean : 43.8303

Gaus Width: 3.8192

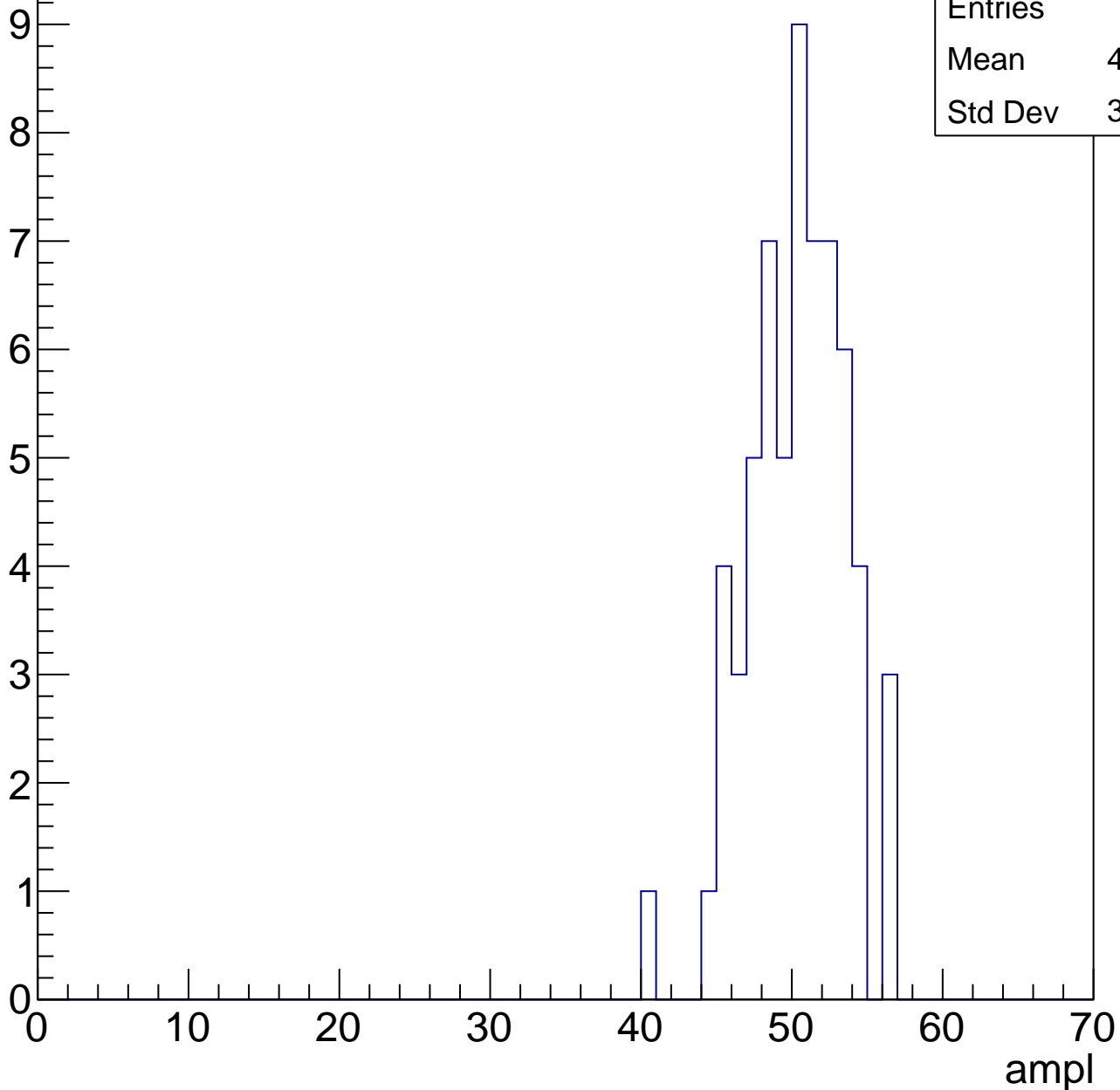


B0L002S, U17-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	49.85
Std Dev	3.167

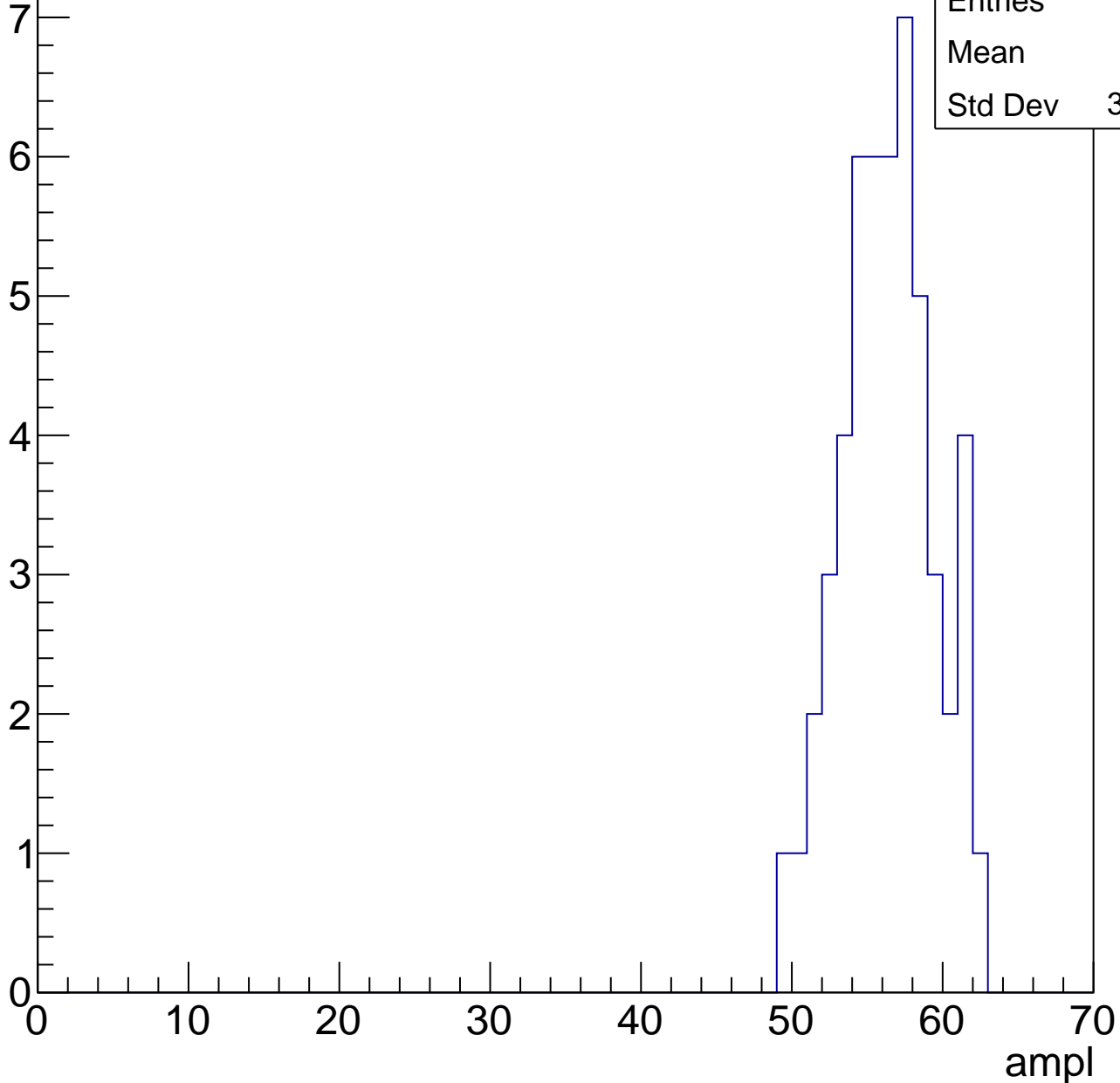


B0L002S, U17-ch77, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.9
Std Dev	3.037

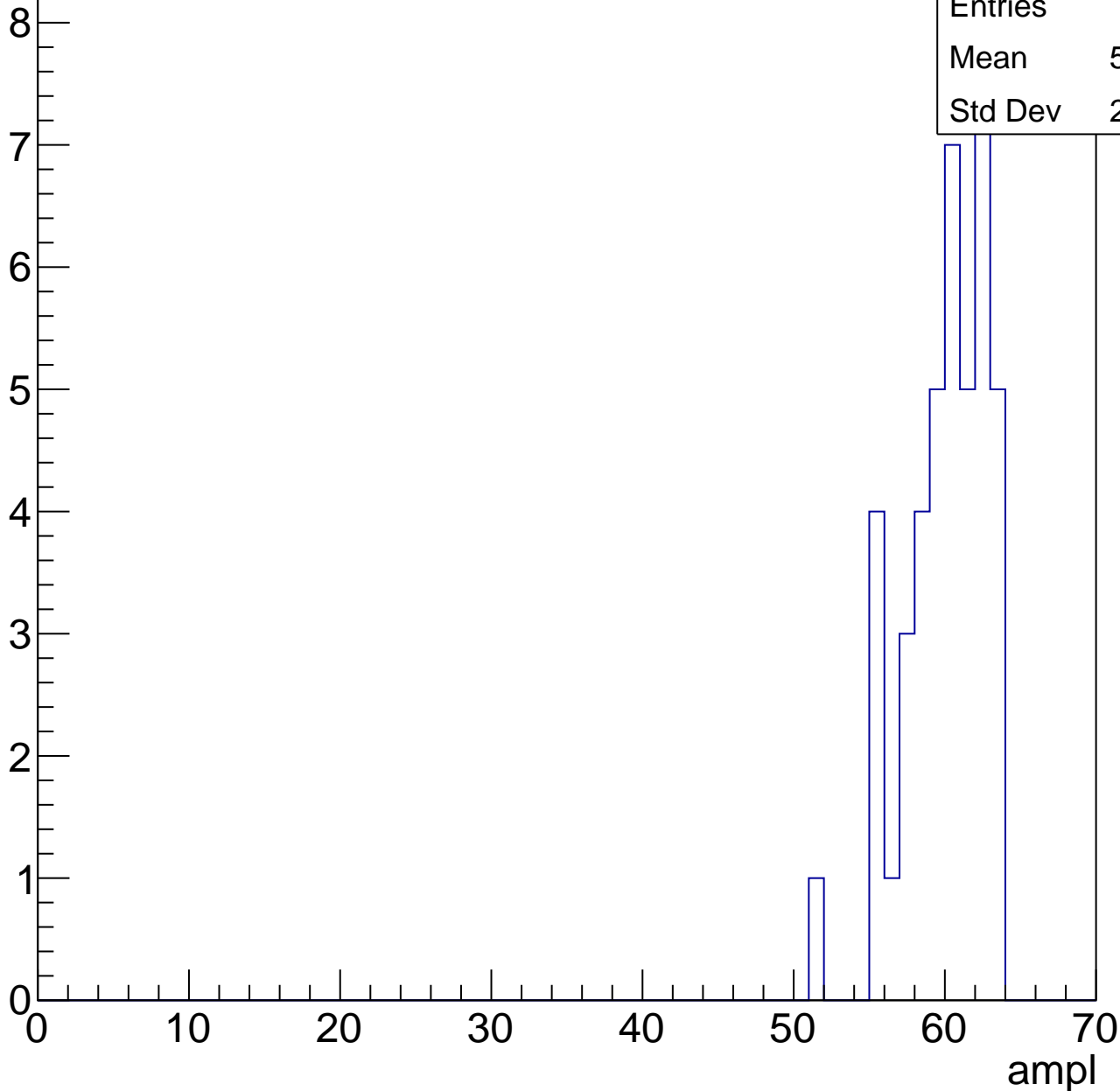


B0L002S, U17-ch77, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	59.56
Std Dev	2.722

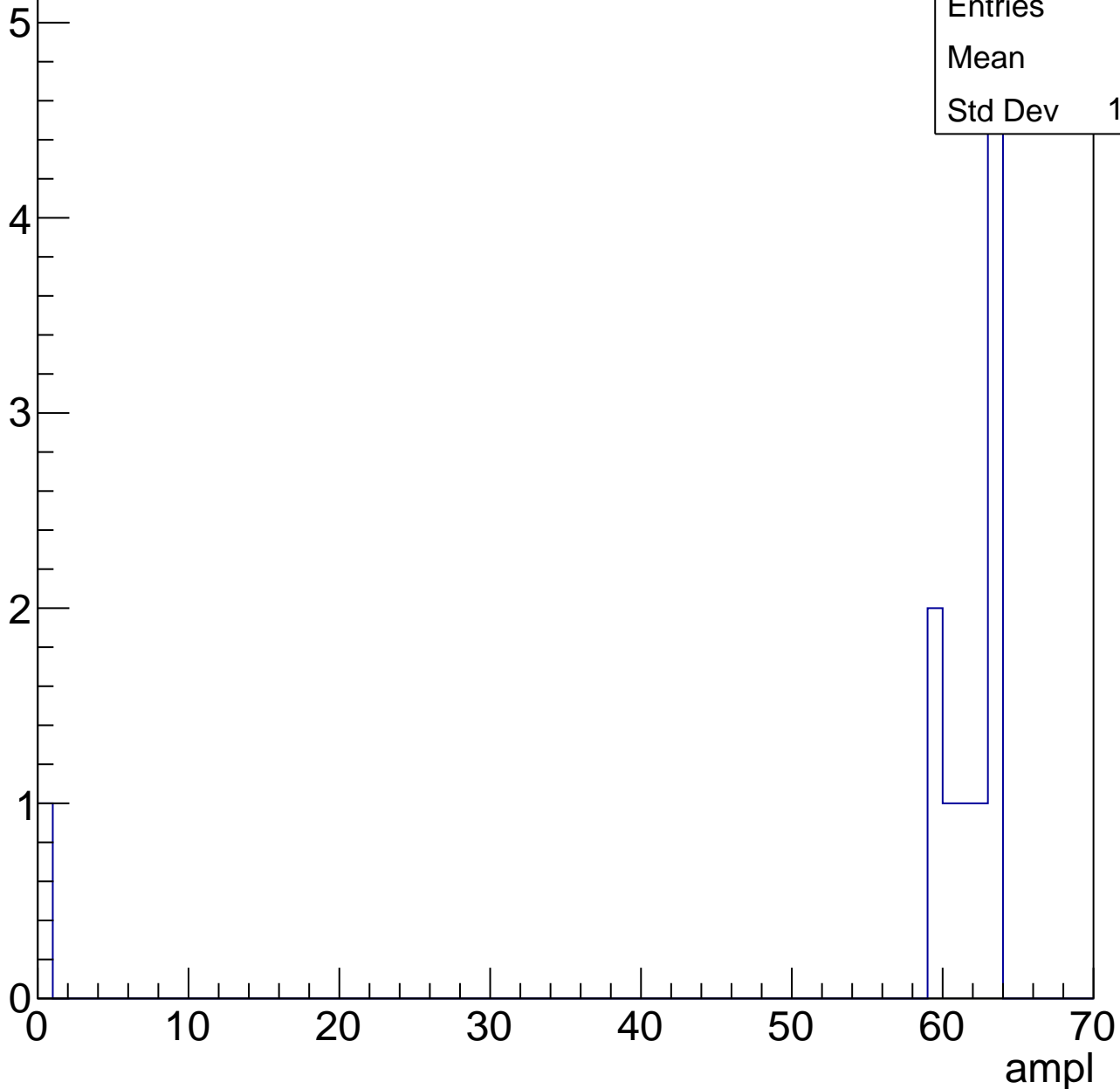


B0L002S, U17-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

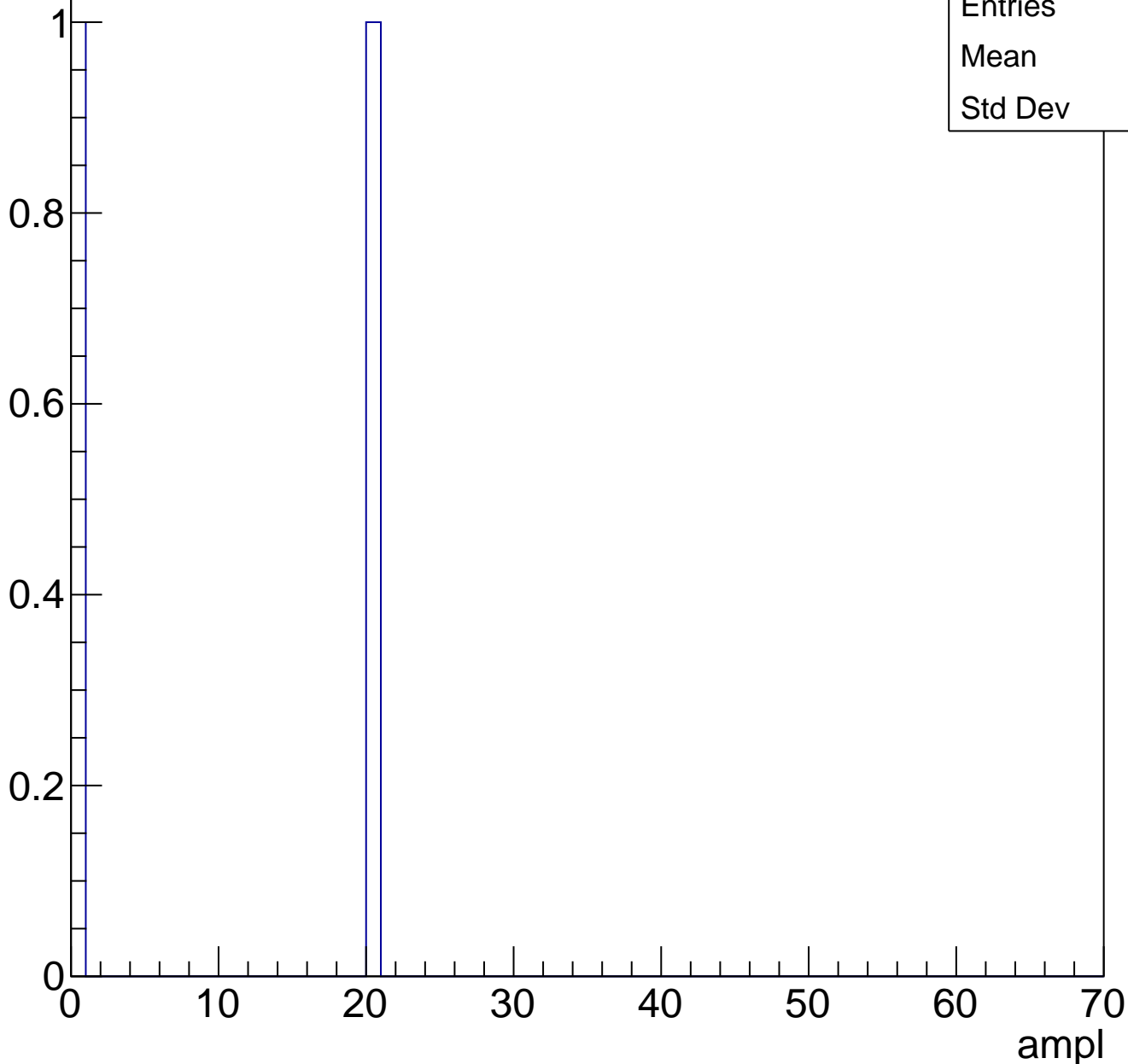
Entries	11
Mean	56
Std Dev	17.78



B0L002S, U17-ch77, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U17-ch78, adc0

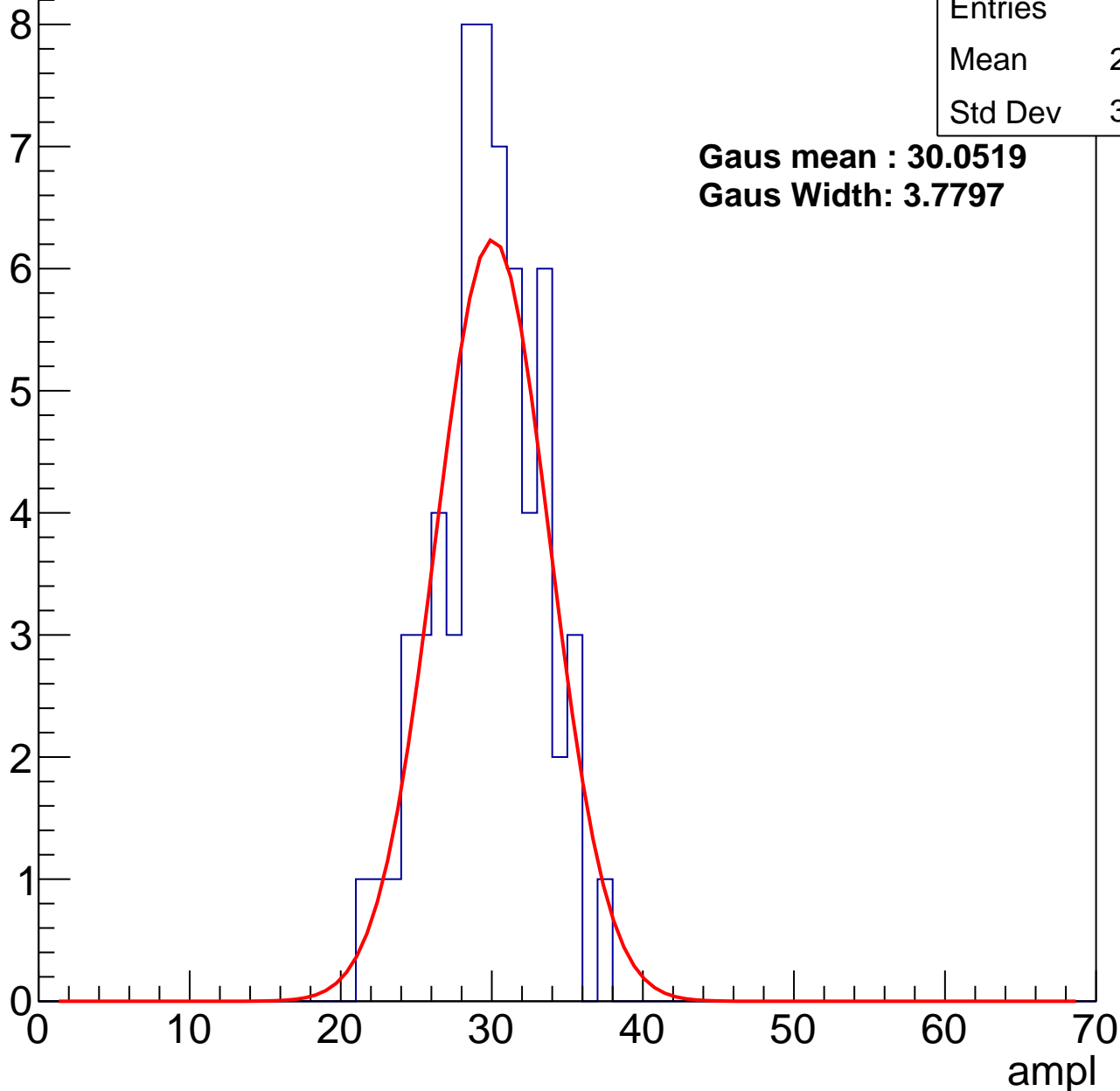
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.28
Std Dev	3.407

Gaus mean : 30.0519

Gaus Width: 3.7797



B0L002S, U17-ch78, adc1

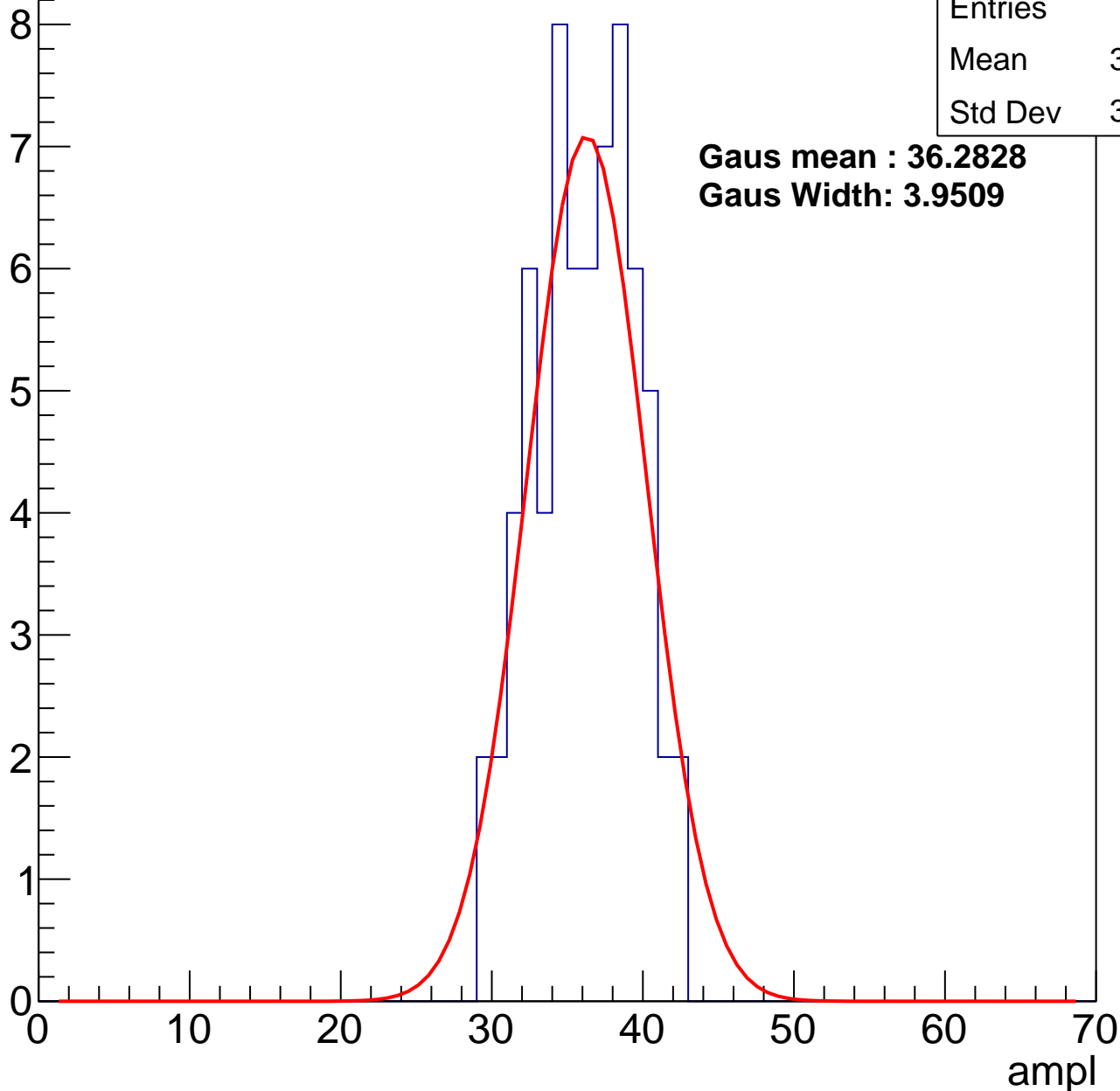
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.69
Std Dev	3.273

Gaus mean : 36.2828

Gaus Width: 3.9509



B0L002S, U17-ch78, adc2

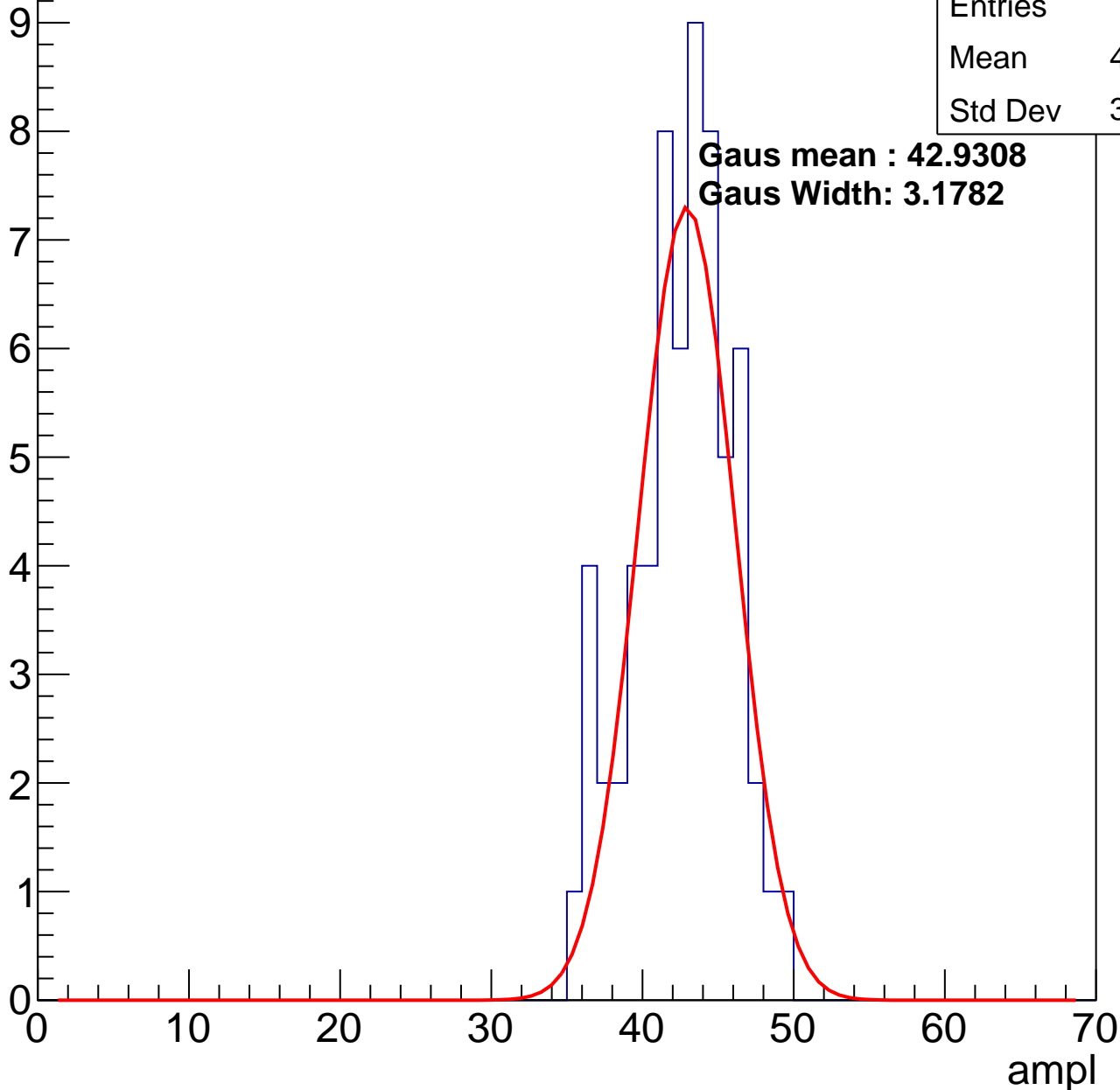
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.16
Std Dev	3.213

Gaus mean : 42.9308

Gaus Width: 3.1782

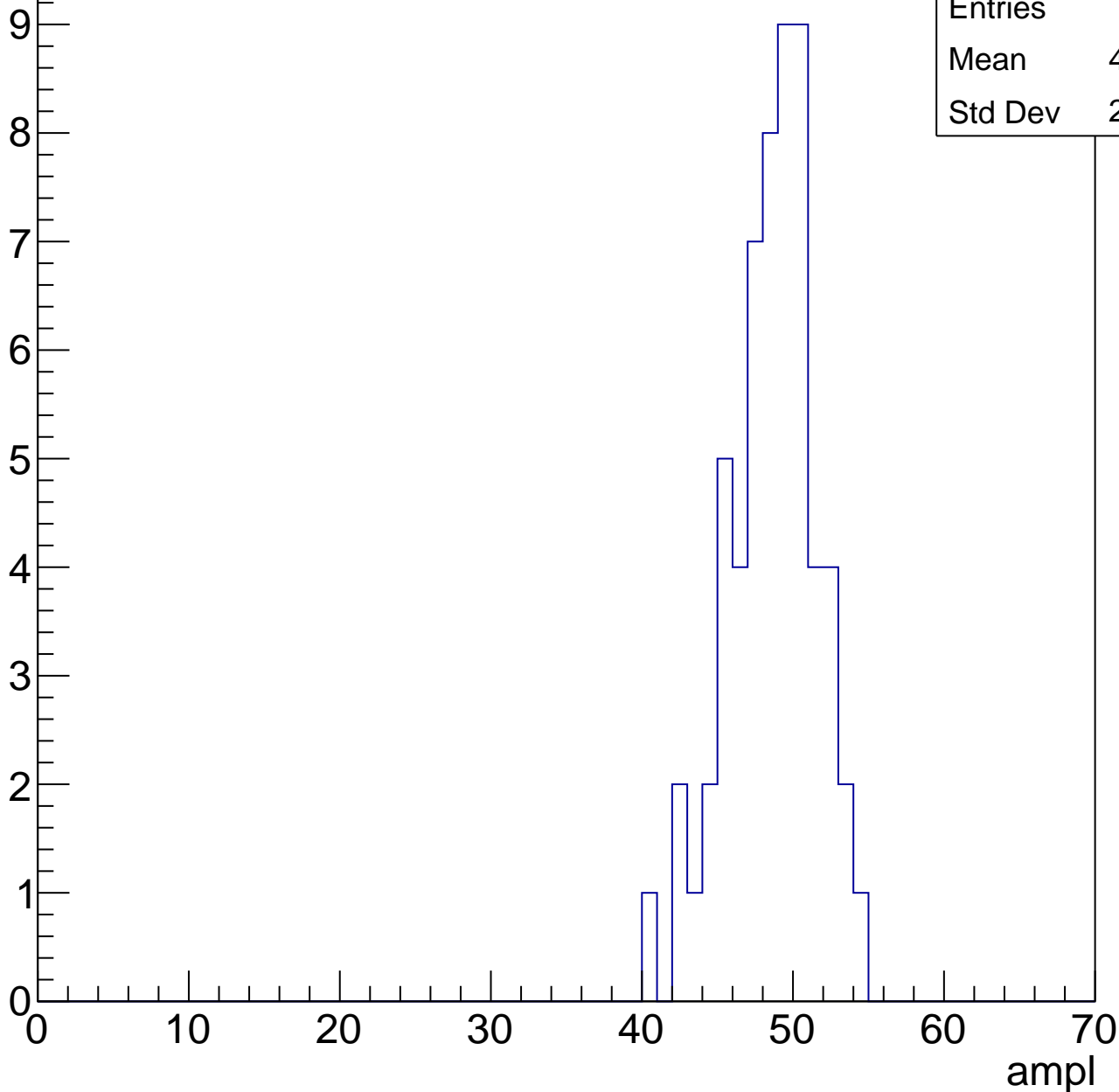


B0L002S, U17-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	48.14
Std Dev	2.885

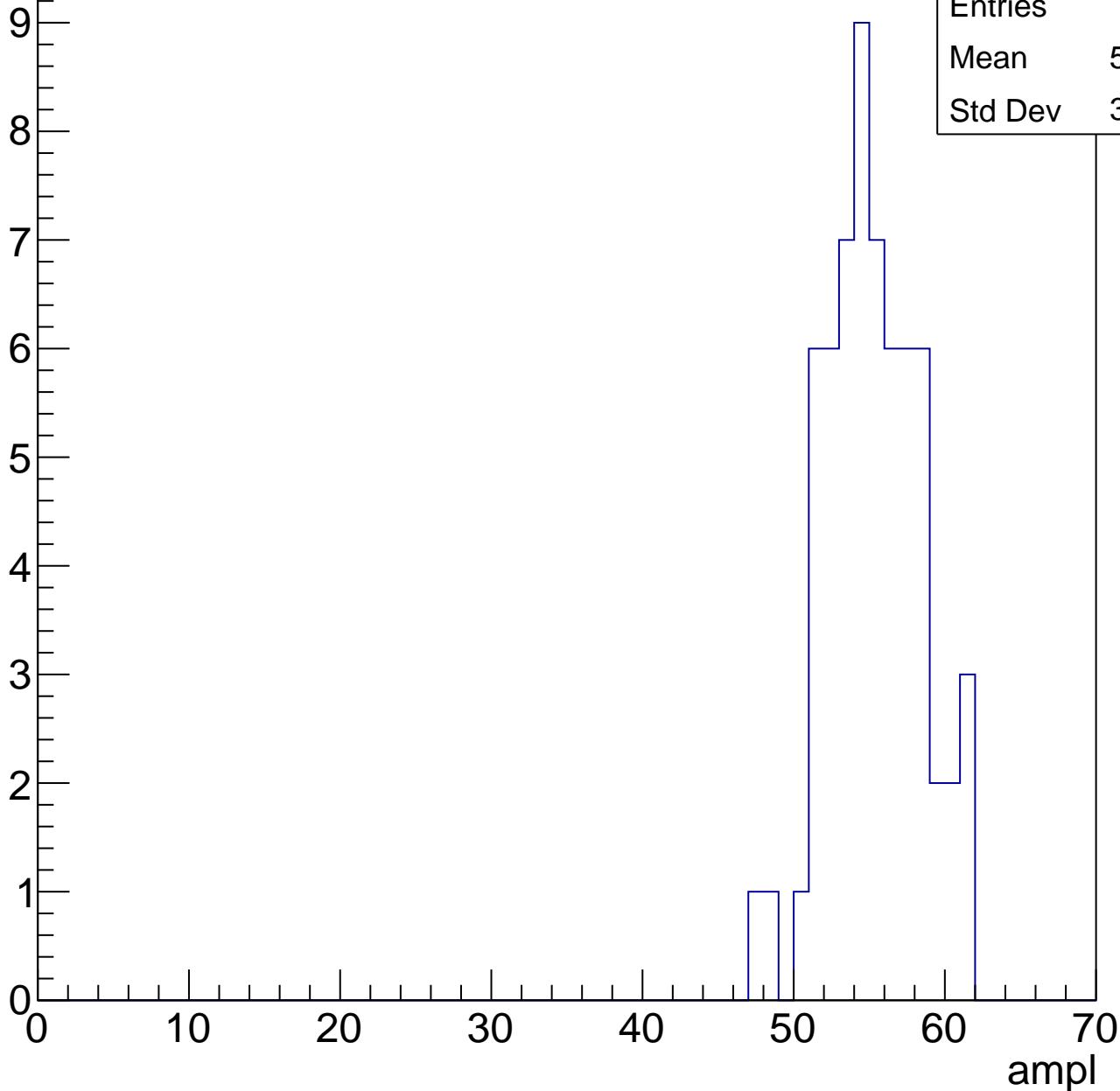


B0L002S, U17-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	54.79
Std Dev	3.077

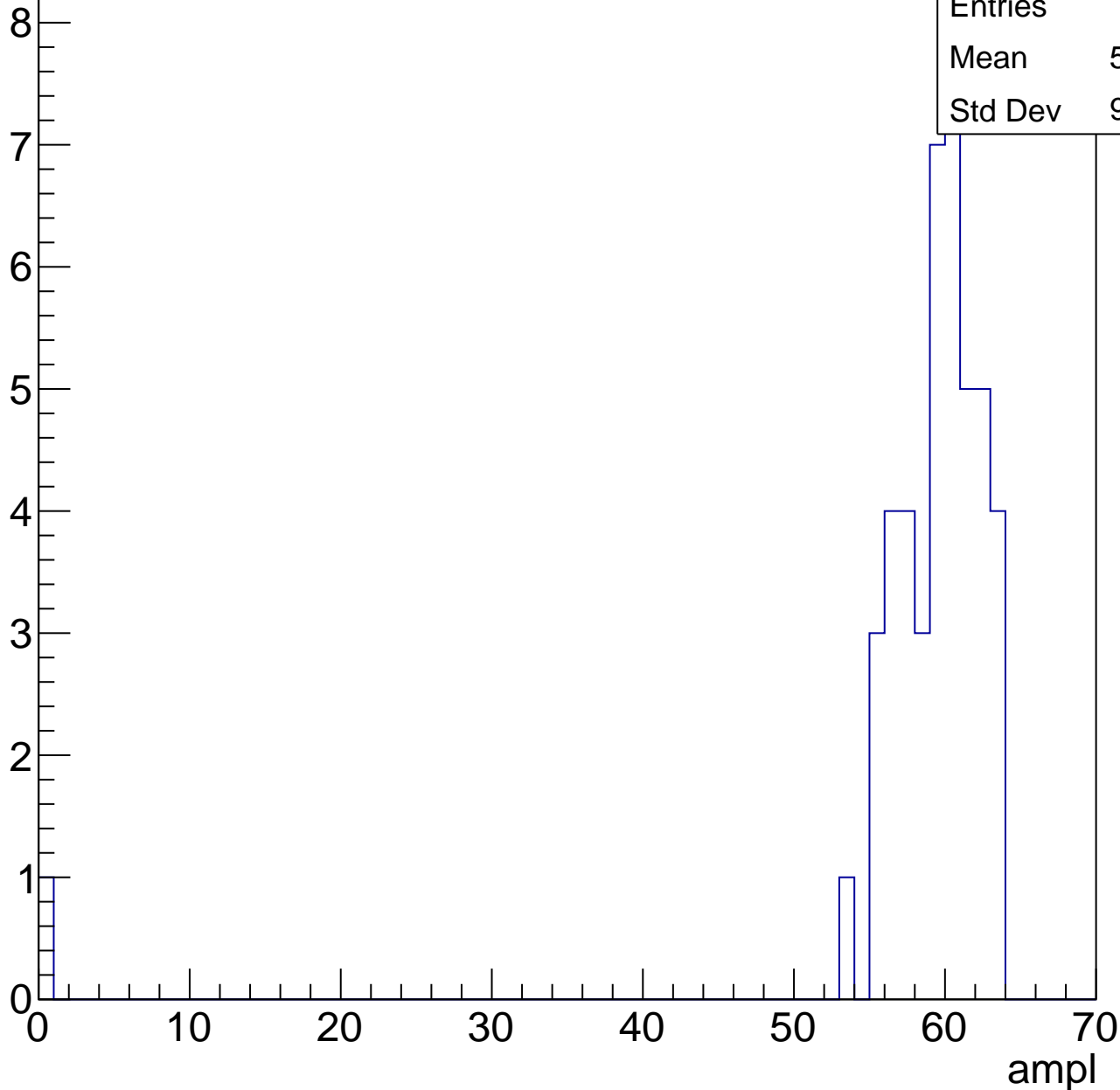


B0L002S, U17-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	57.87
Std Dev	9.067

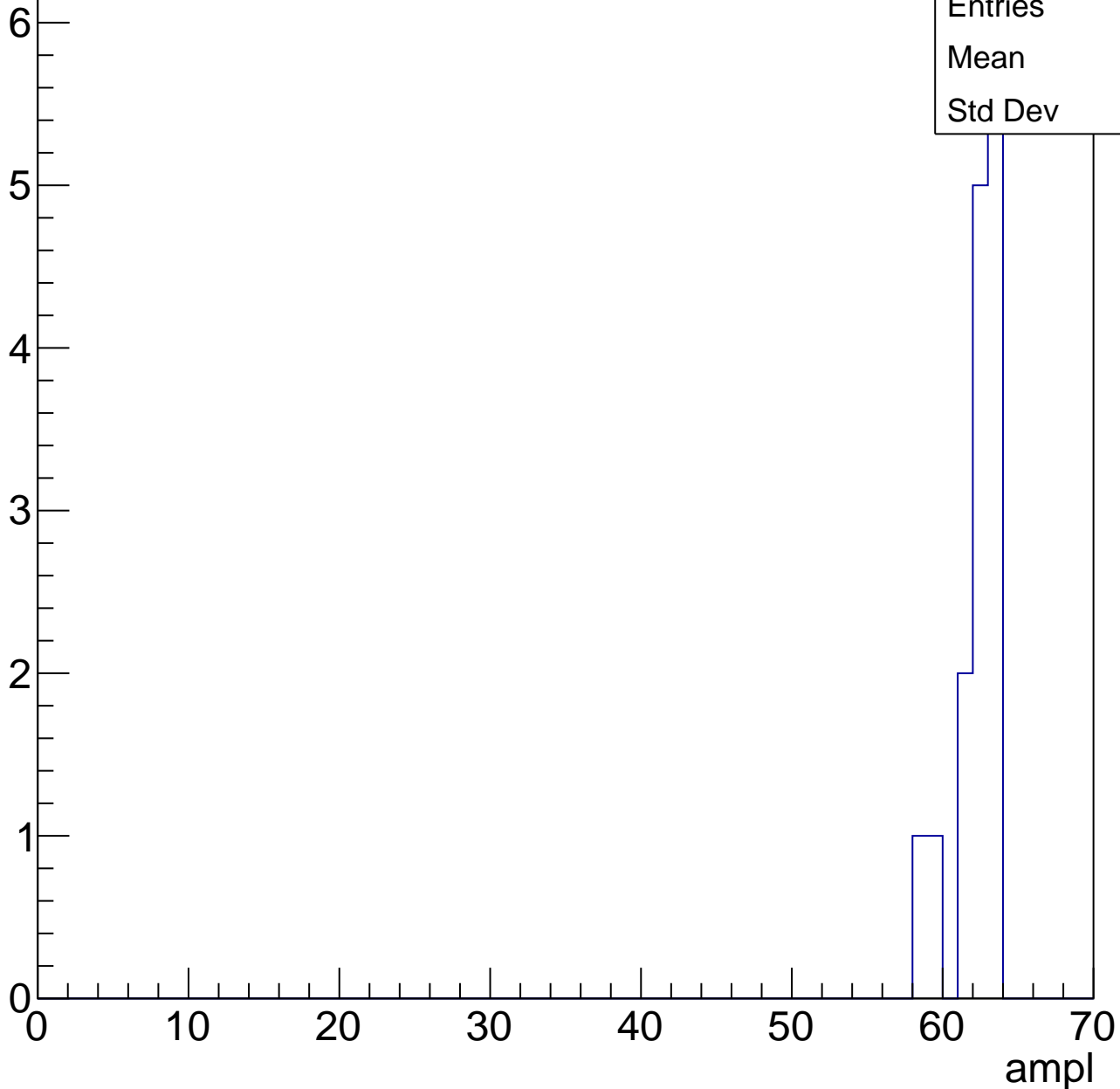


B0L002S, U17-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	15
Mean	61.8
Std Dev	1.47



B0L002S, U17-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L002S, U17-ch79, adc0

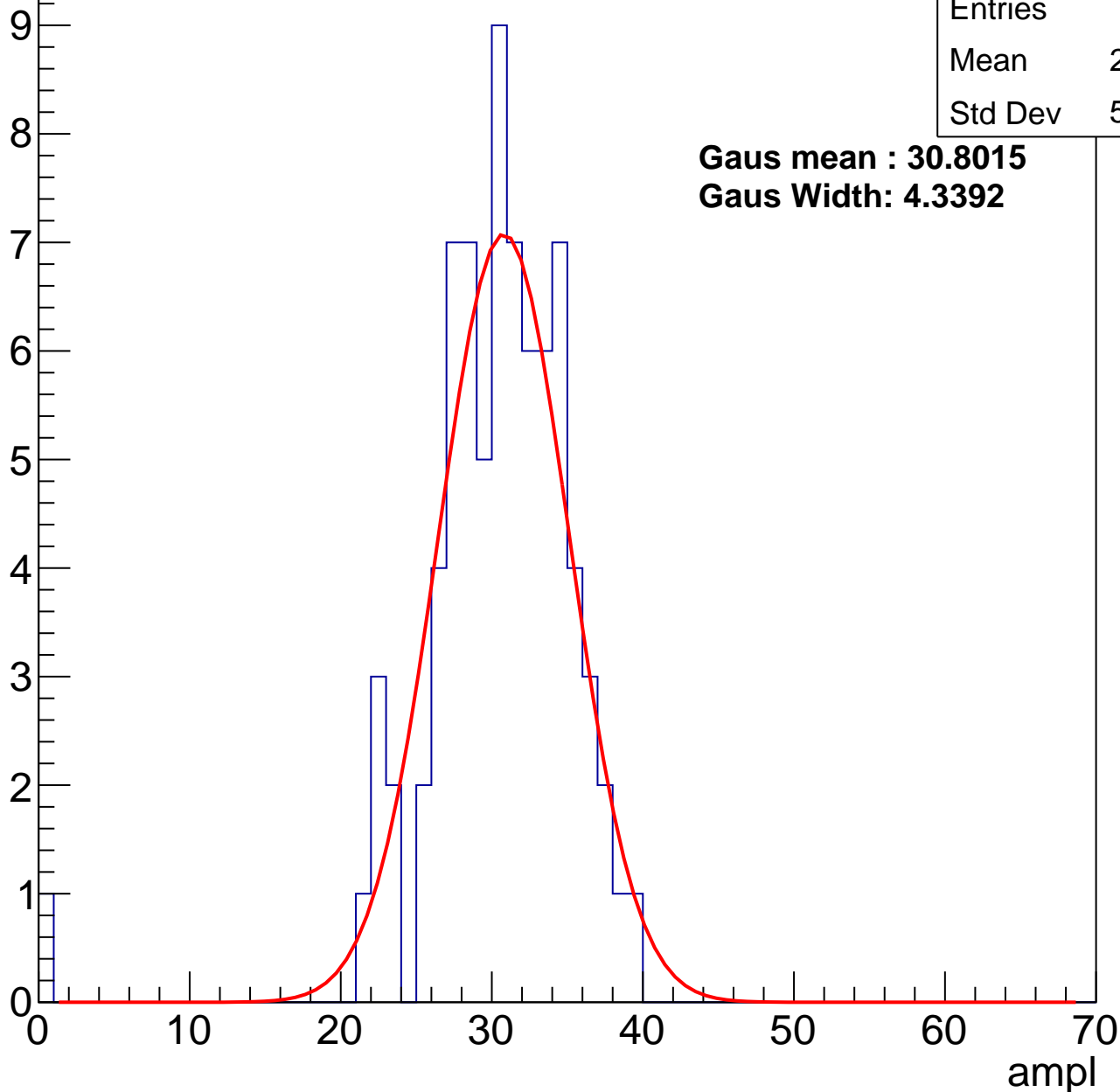
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.88
Std Dev	5.223

Gaus mean : 30.8015

Gaus Width: 4.3392



B0L002S, U17-ch79, adc1

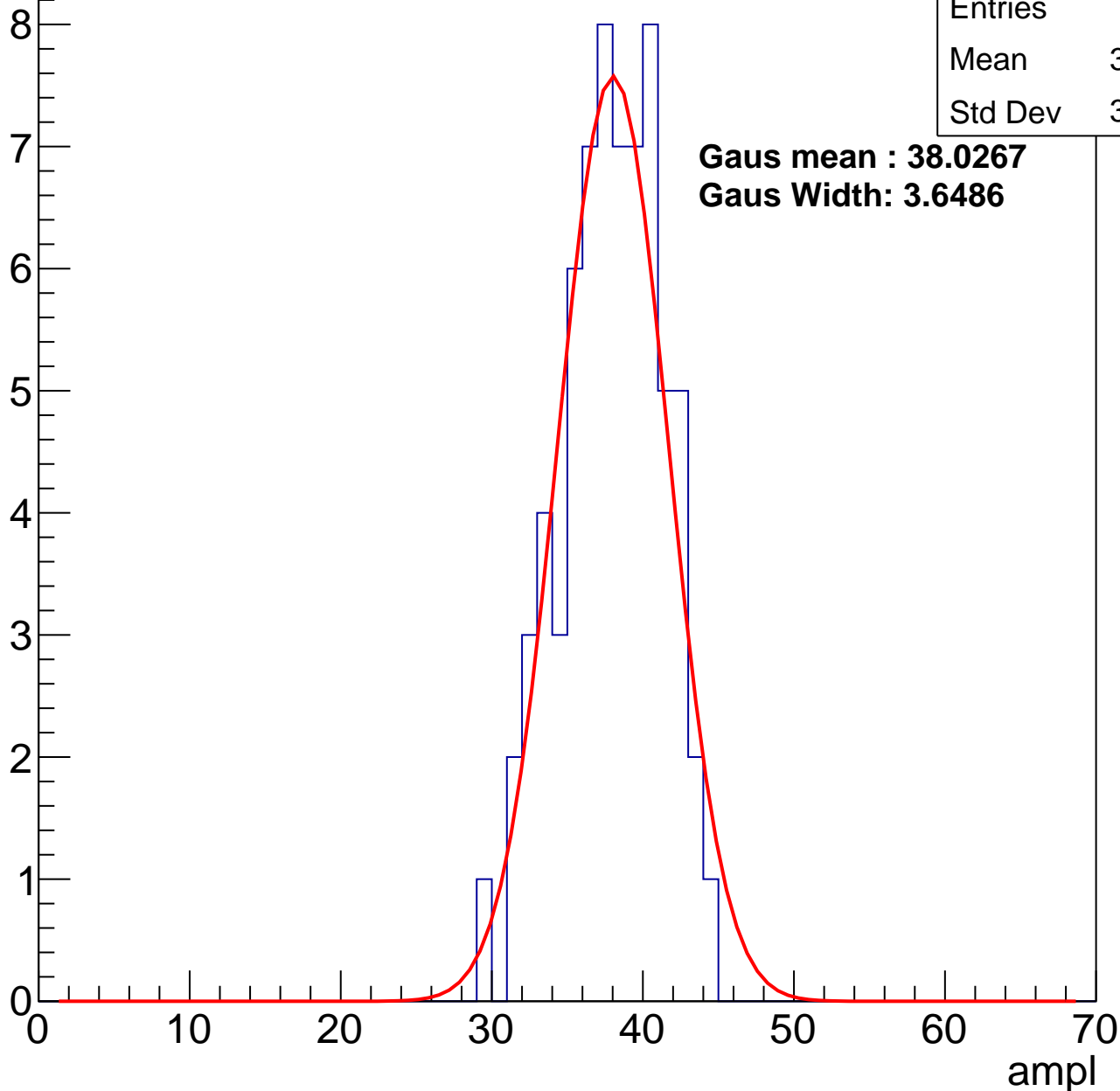
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.43
Std Dev	3.303

Gaus mean : 38.0267

Gaus Width: 3.6486



B0L002S, U17-ch79, adc2

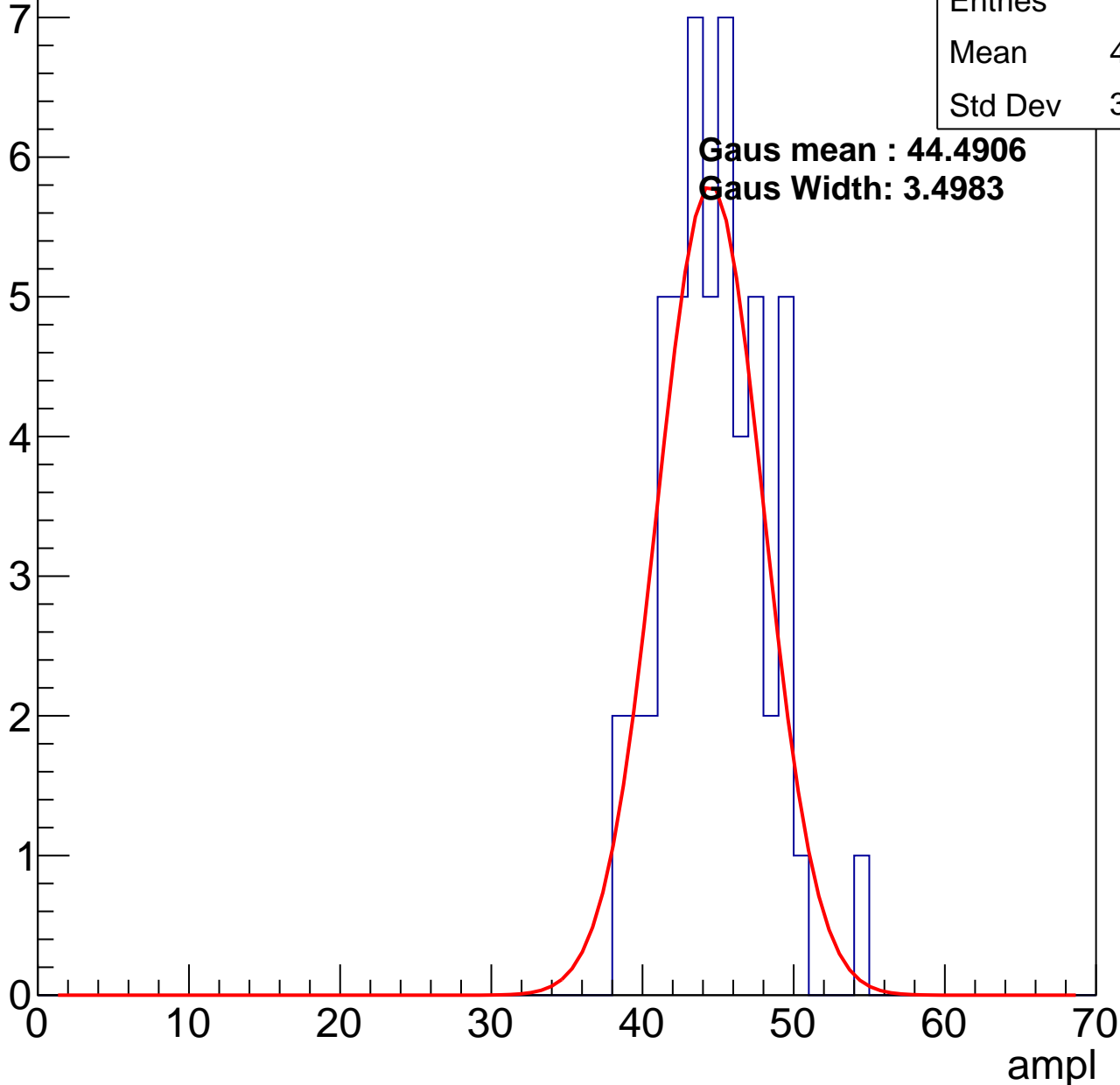
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.32
Std Dev	3.312

Gaus mean : 44.4906

Gaus Width: 3.4983

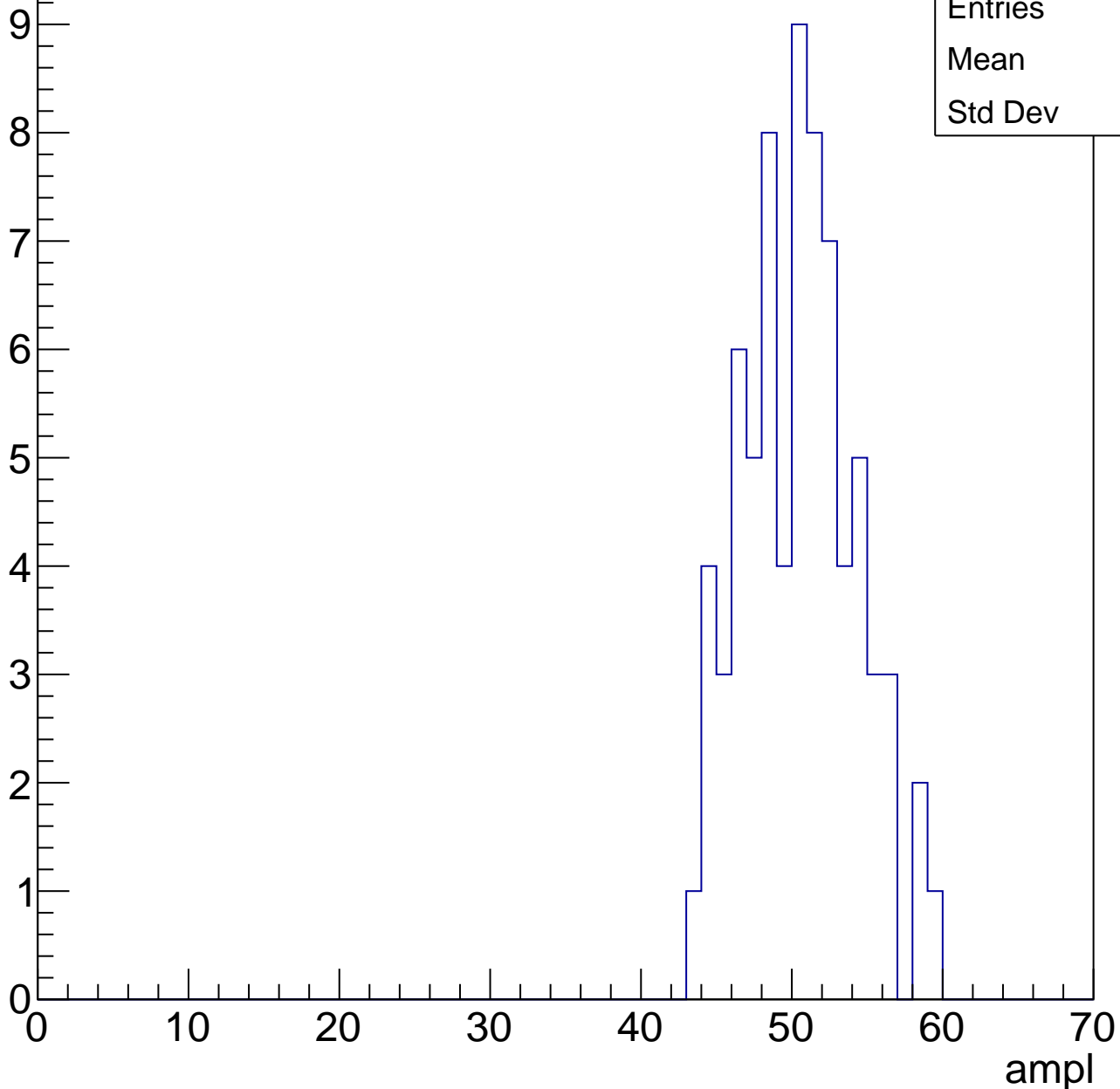


B0L002S, U17-ch79, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	50.1
Std Dev	3.68



B0L002S, U17-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	55.36
Std Dev	7.706

Entry

10

8

6

4

2

0

0

10

20

30

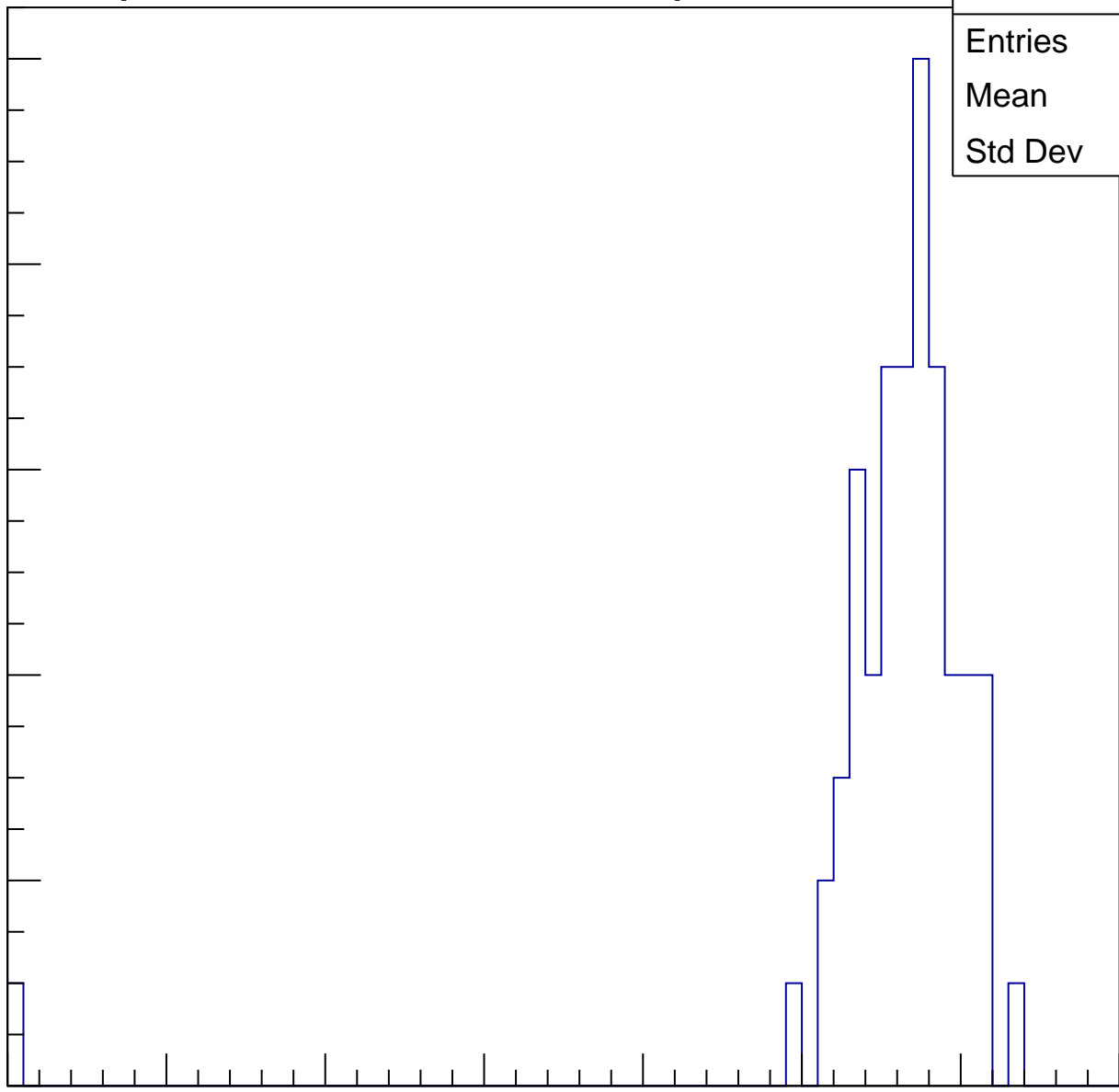
40

50

60

70

ampl

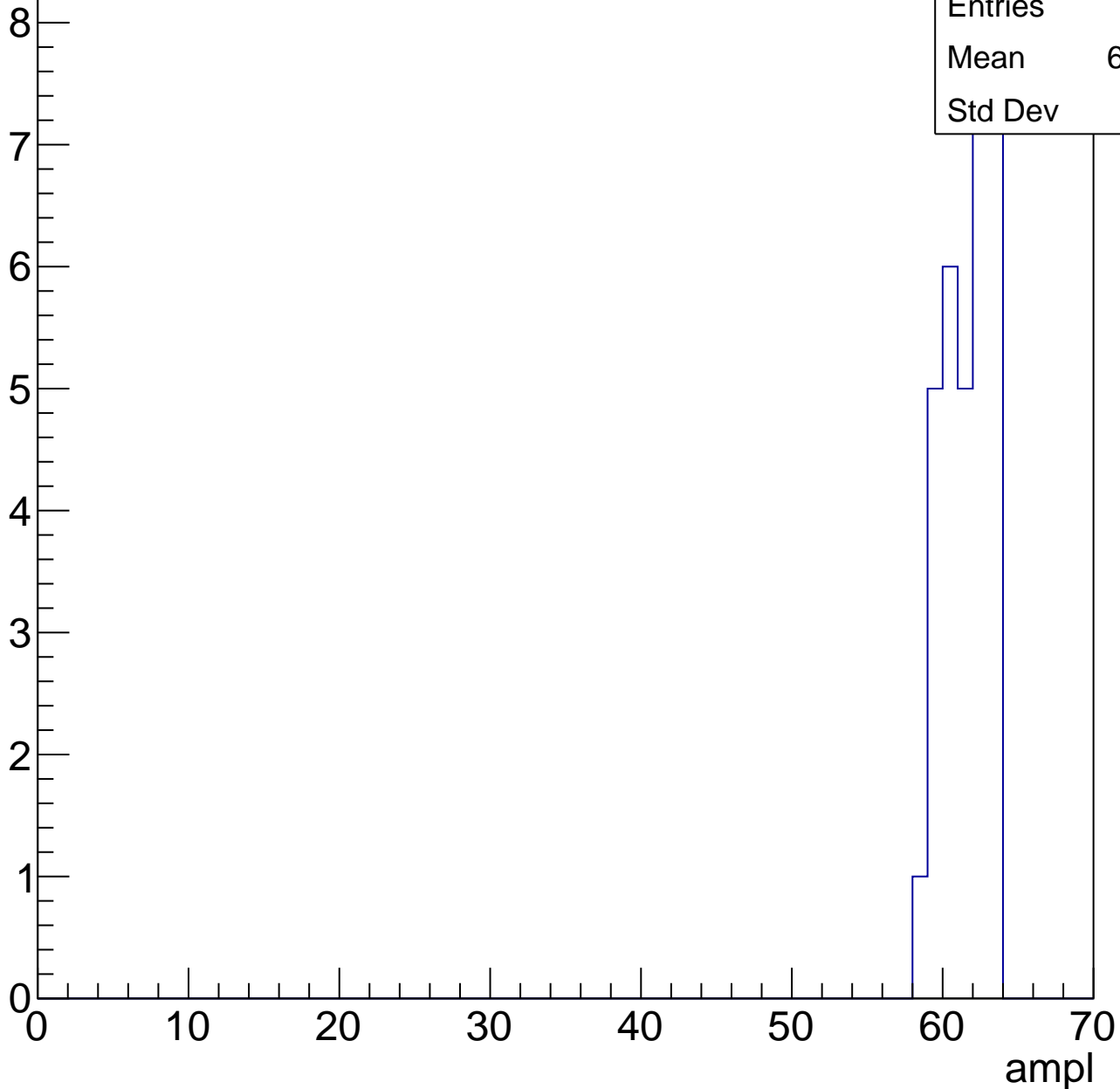


B0L002S, U17-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	61.15
Std Dev	1.5



B0L002S, U17-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B0L002S, U17-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch80, adc0

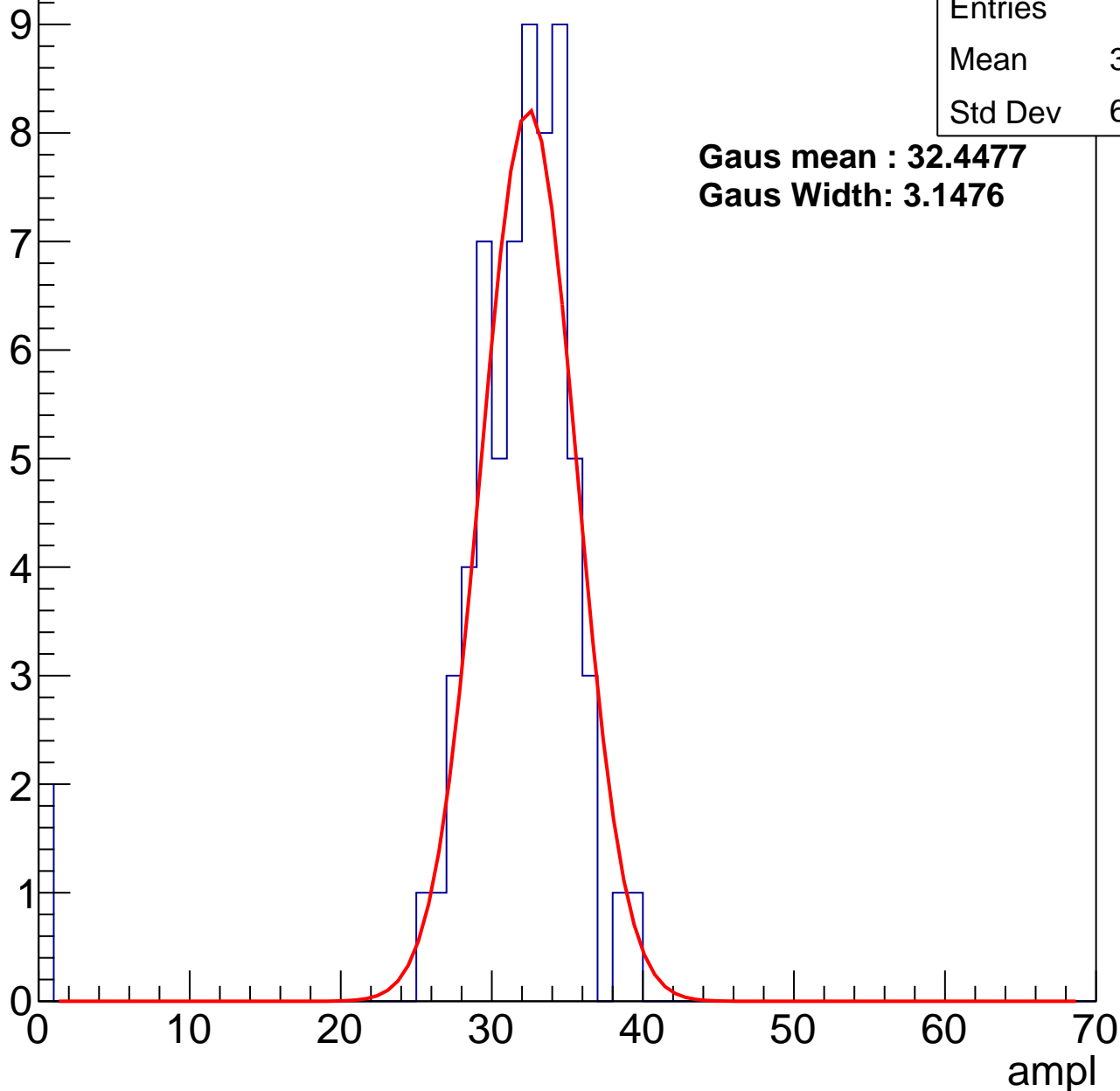
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.79
Std Dev	6.139

Gaus mean : 32.4477

Gaus Width: 3.1476



B0L002S, U17-ch80, adc1

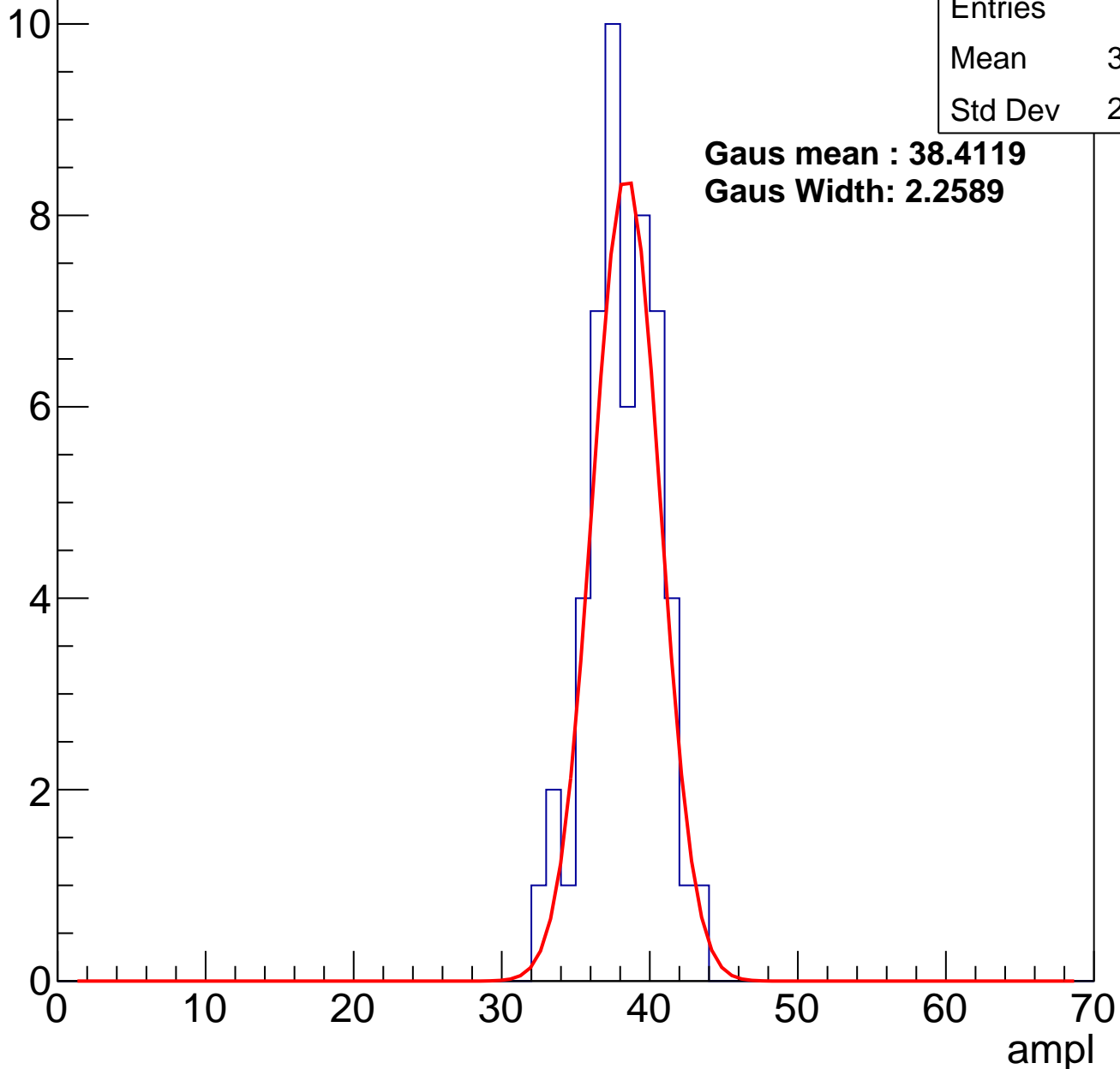
calib_packv5_042523_0143.root, FC#8, port C1

Entries	52
Mean	37.75
Std Dev	2.344

Gaus mean : 38.4119

Gaus Width: 2.2589

Entry



B0L002S, U17-ch80, adc2

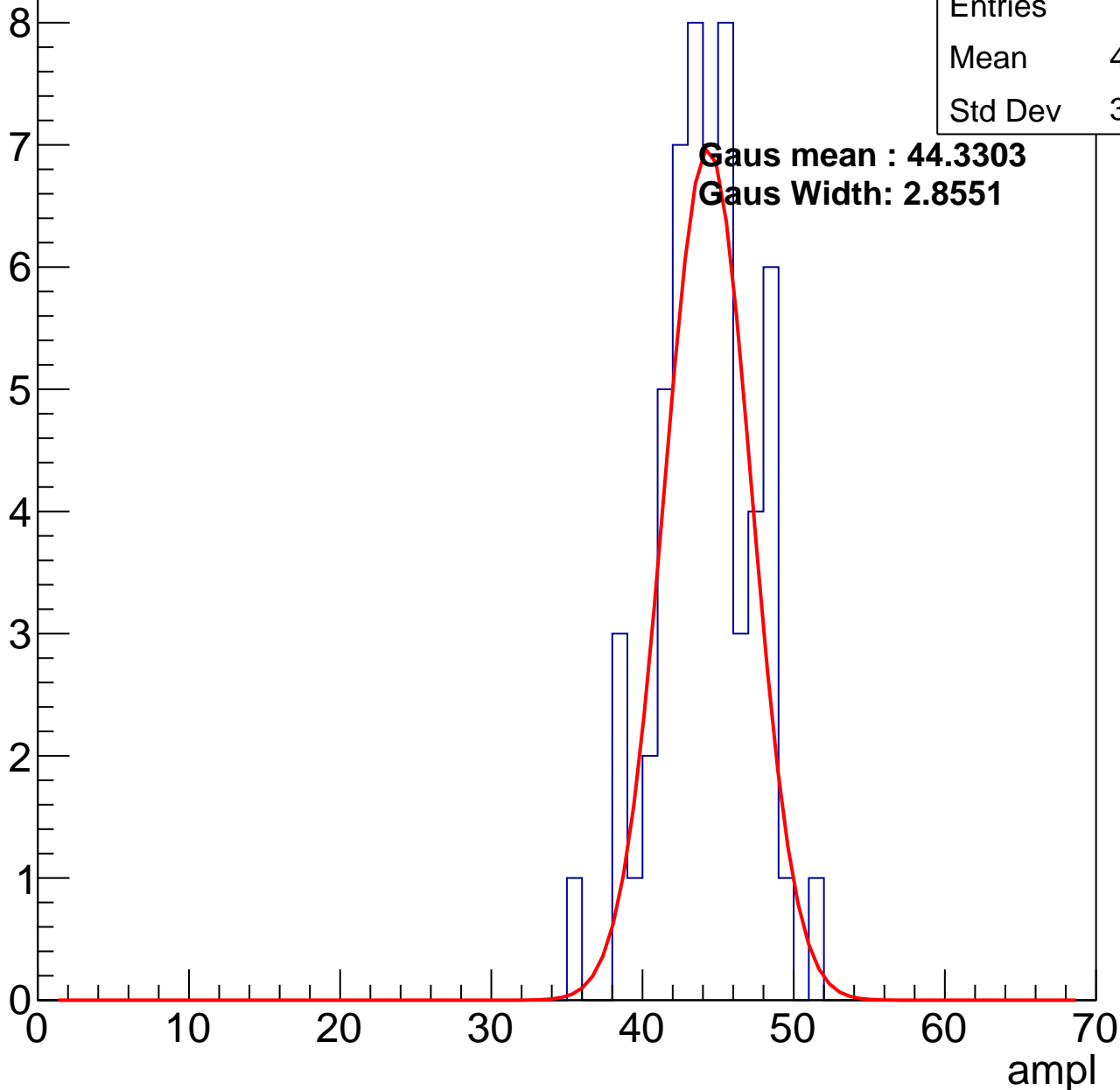
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.74
Std Dev	3.104

Gaus mean : 44.3303

Gaus Width: 2.8551

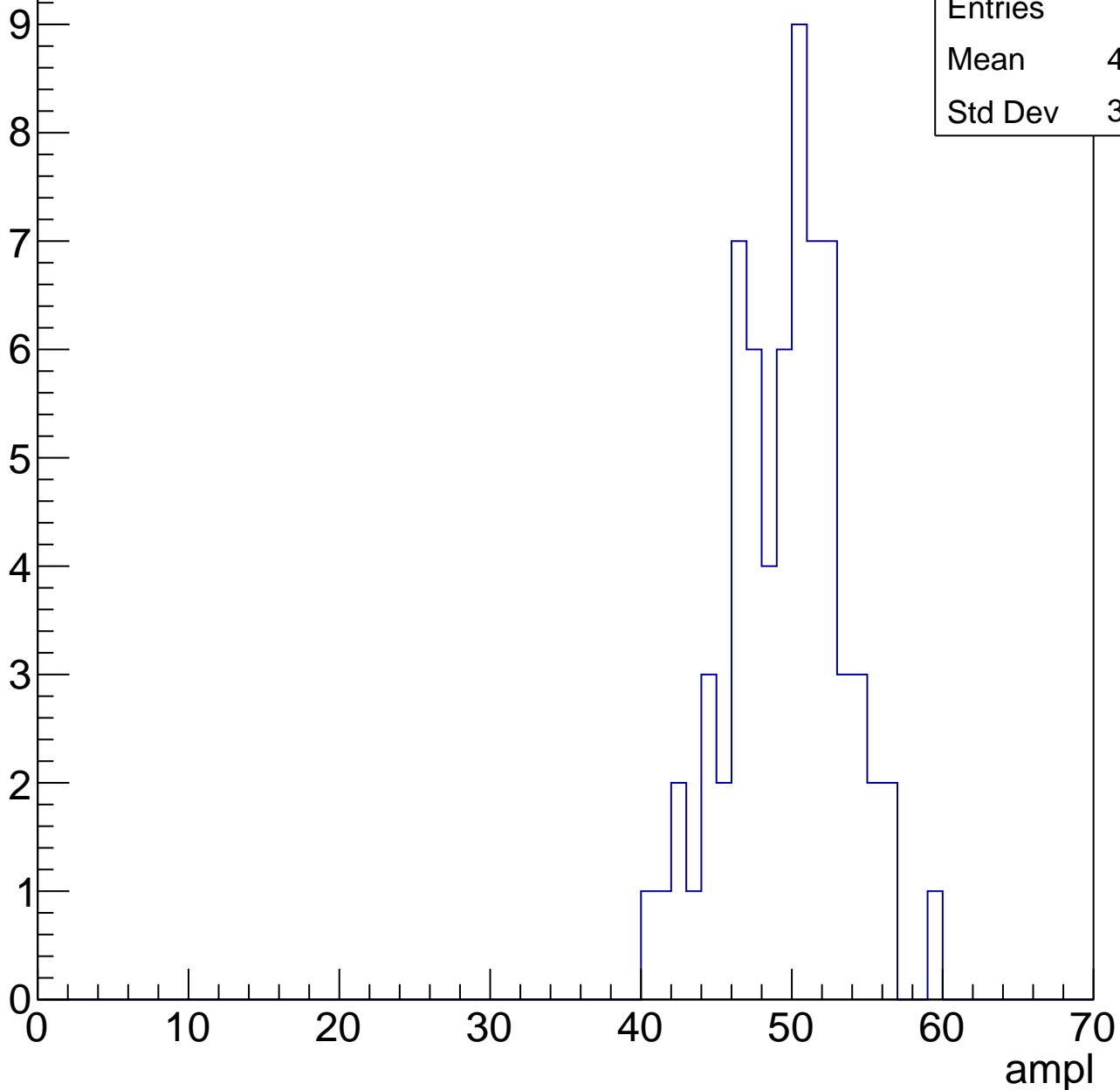


B0L002S, U17-ch80, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	49.15
Std Dev	3.802

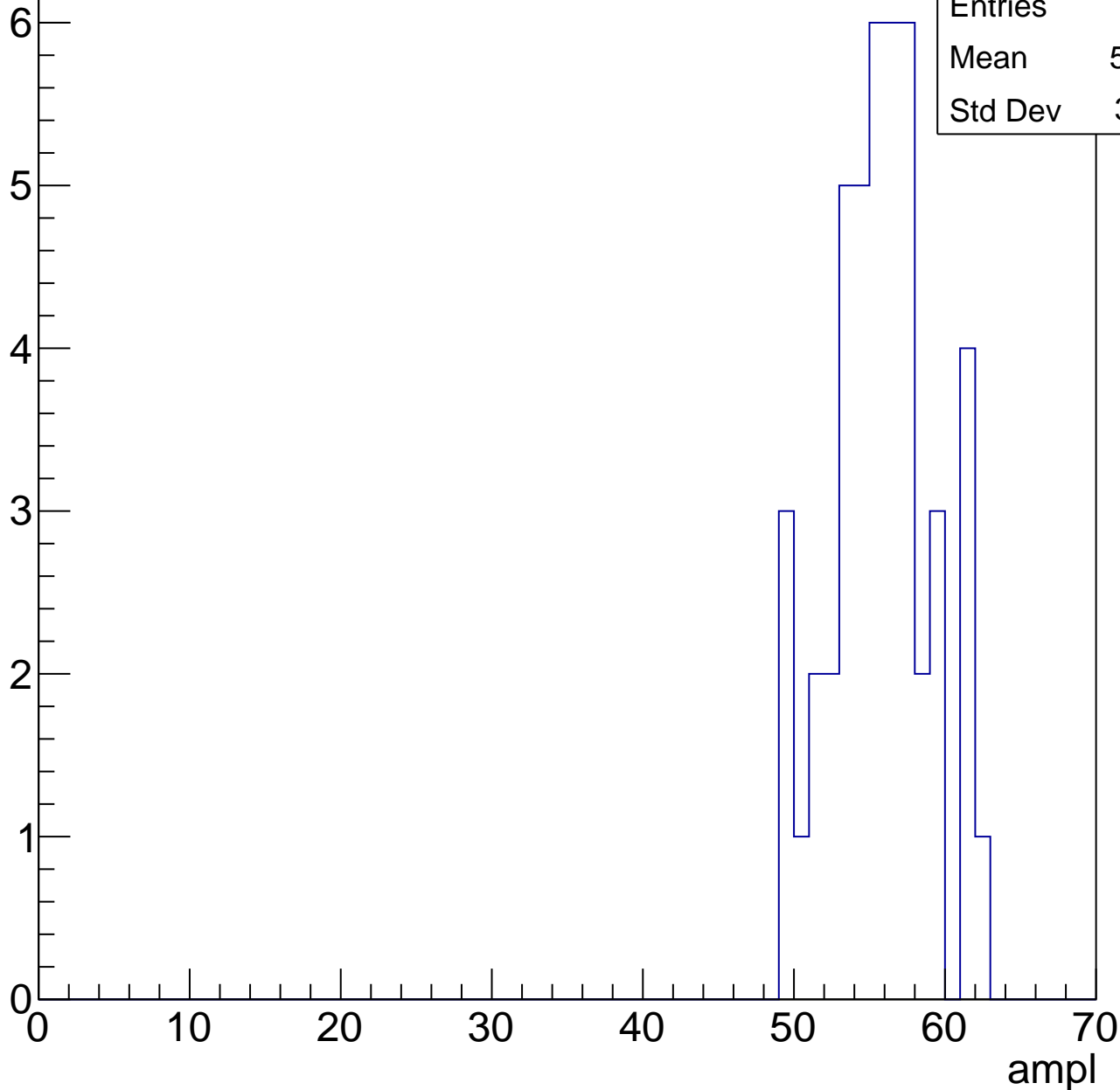


B0L002S, U17-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

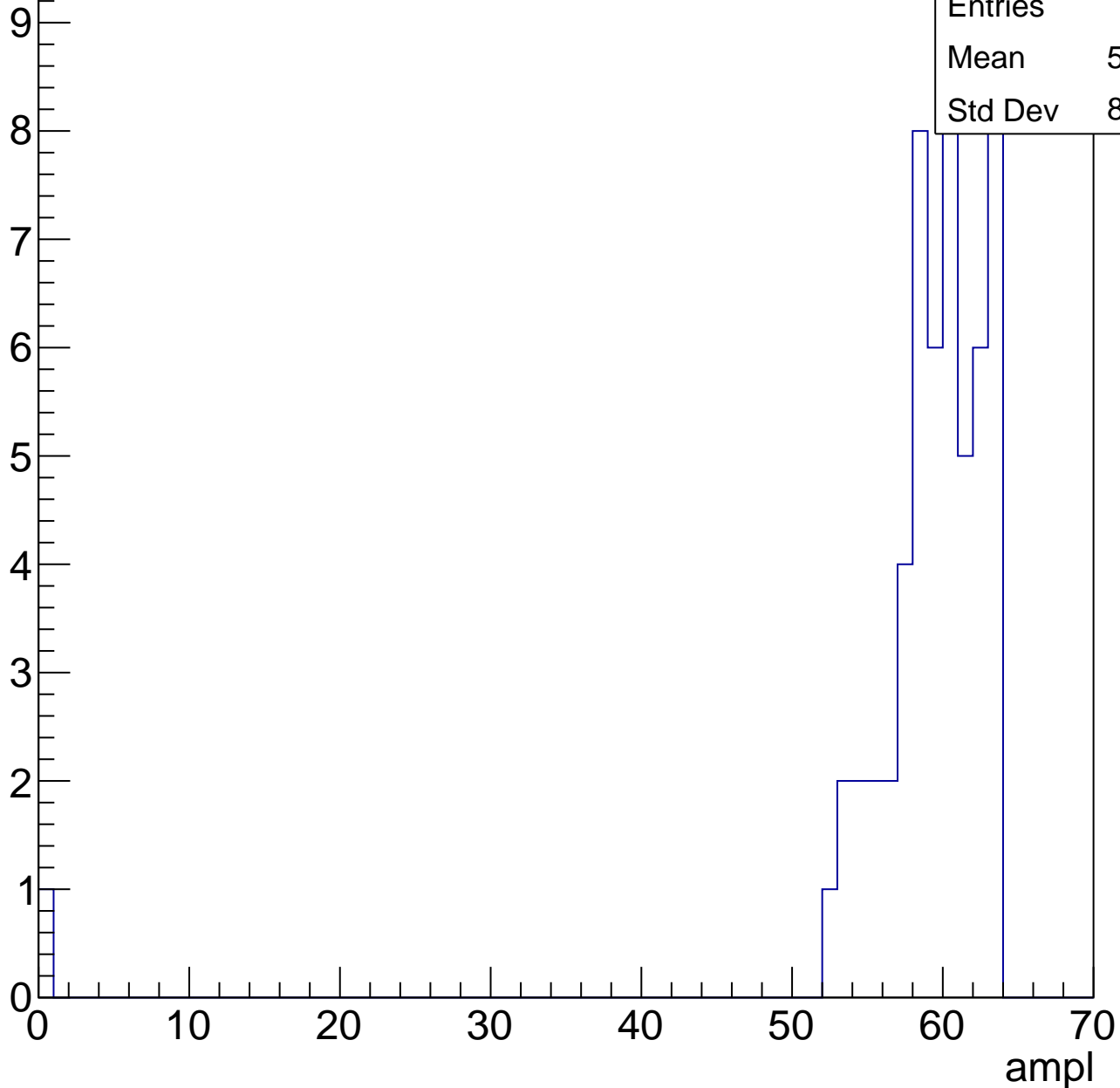
Entries	46
Mean	55.33
Std Dev	3.271



B0L002S, U17-ch80, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch81, adc0

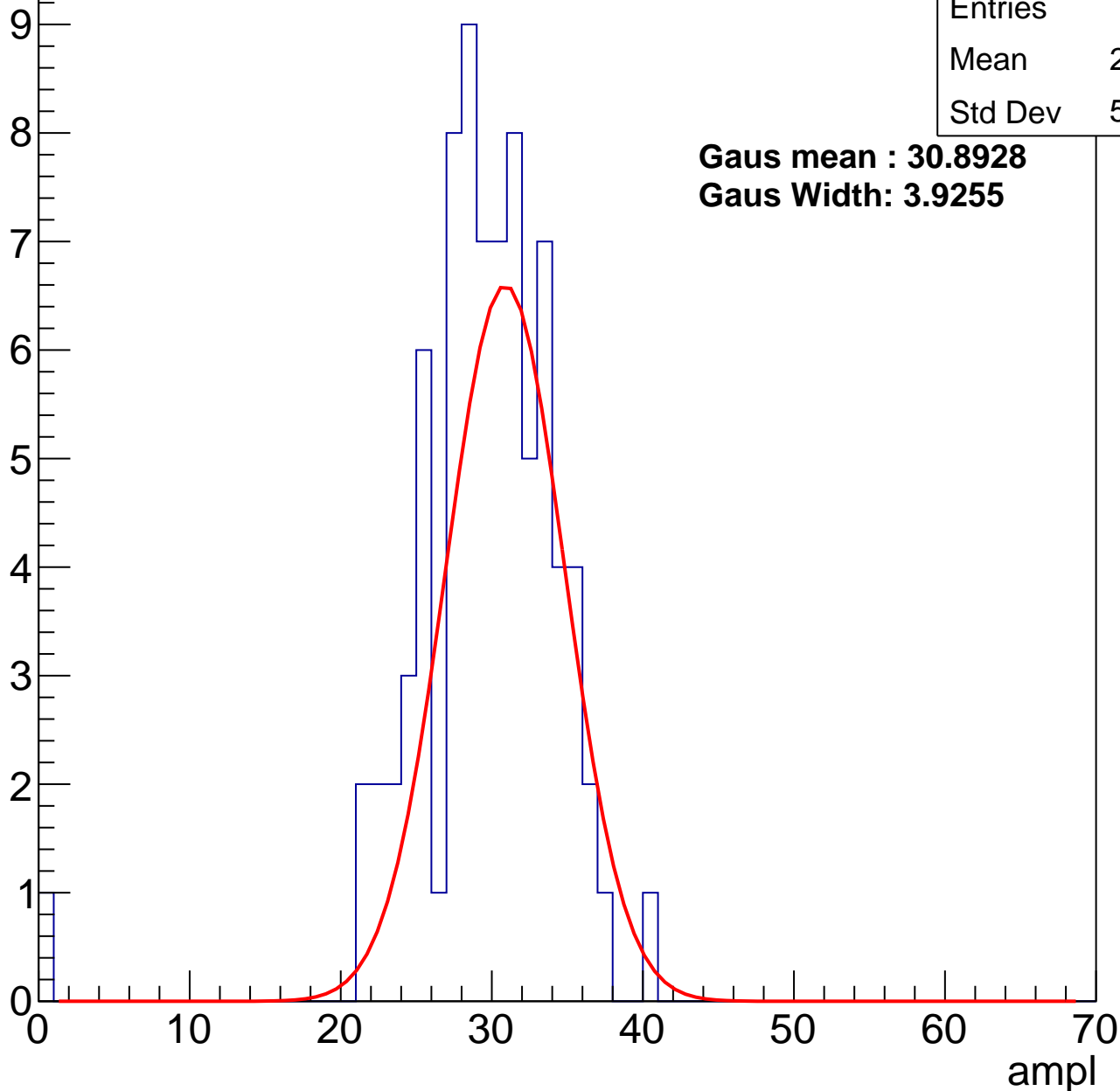
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	29.06
Std Dev	5.102

Gaus mean : 30.8928

Gaus Width: 3.9255



B0L002S, U17-ch81, adc1

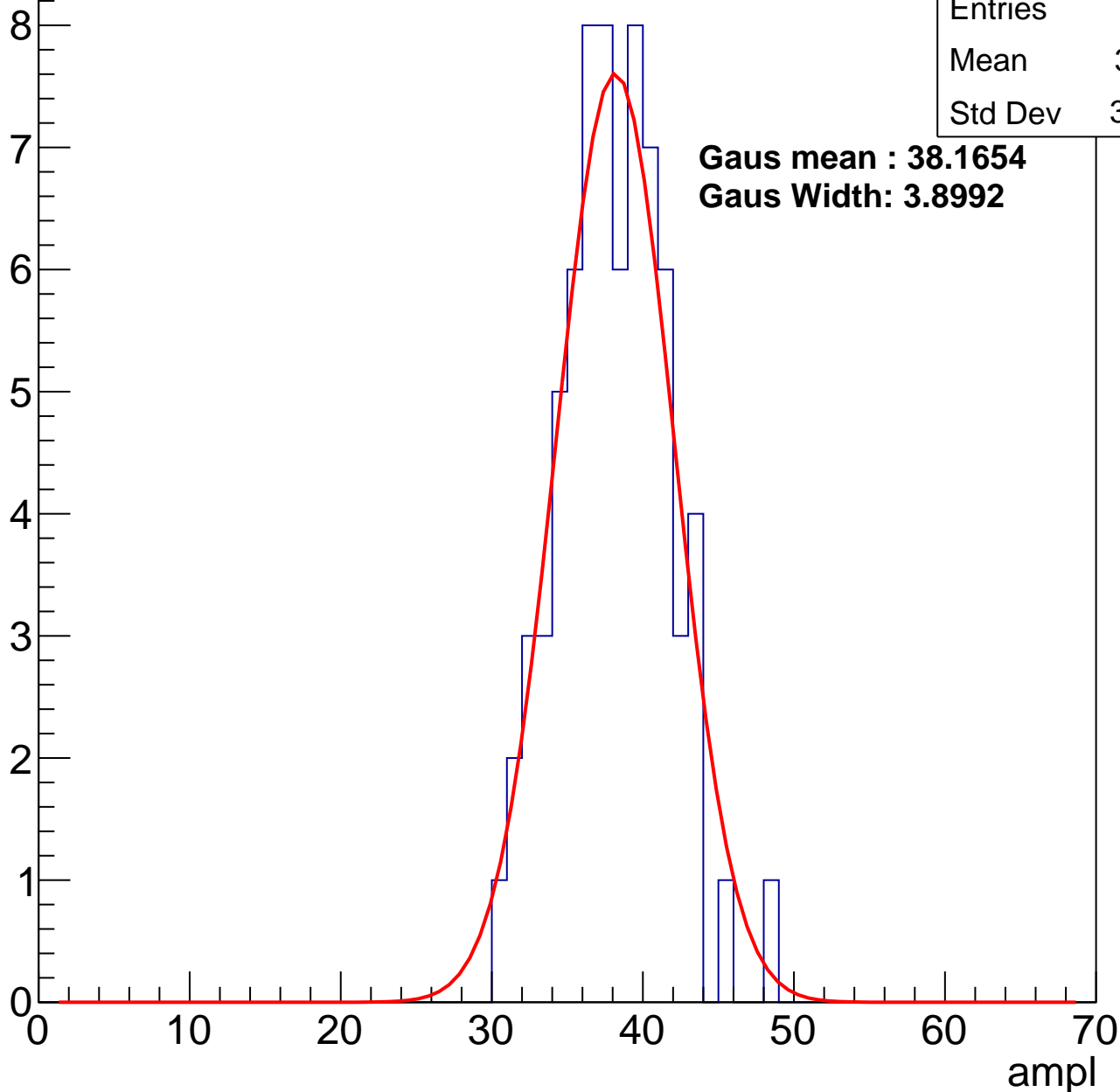
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	37.61
Std Dev	3.526

Gaus mean : 38.1654

Gaus Width: 3.8992



B0L002S, U17-ch81, adc2

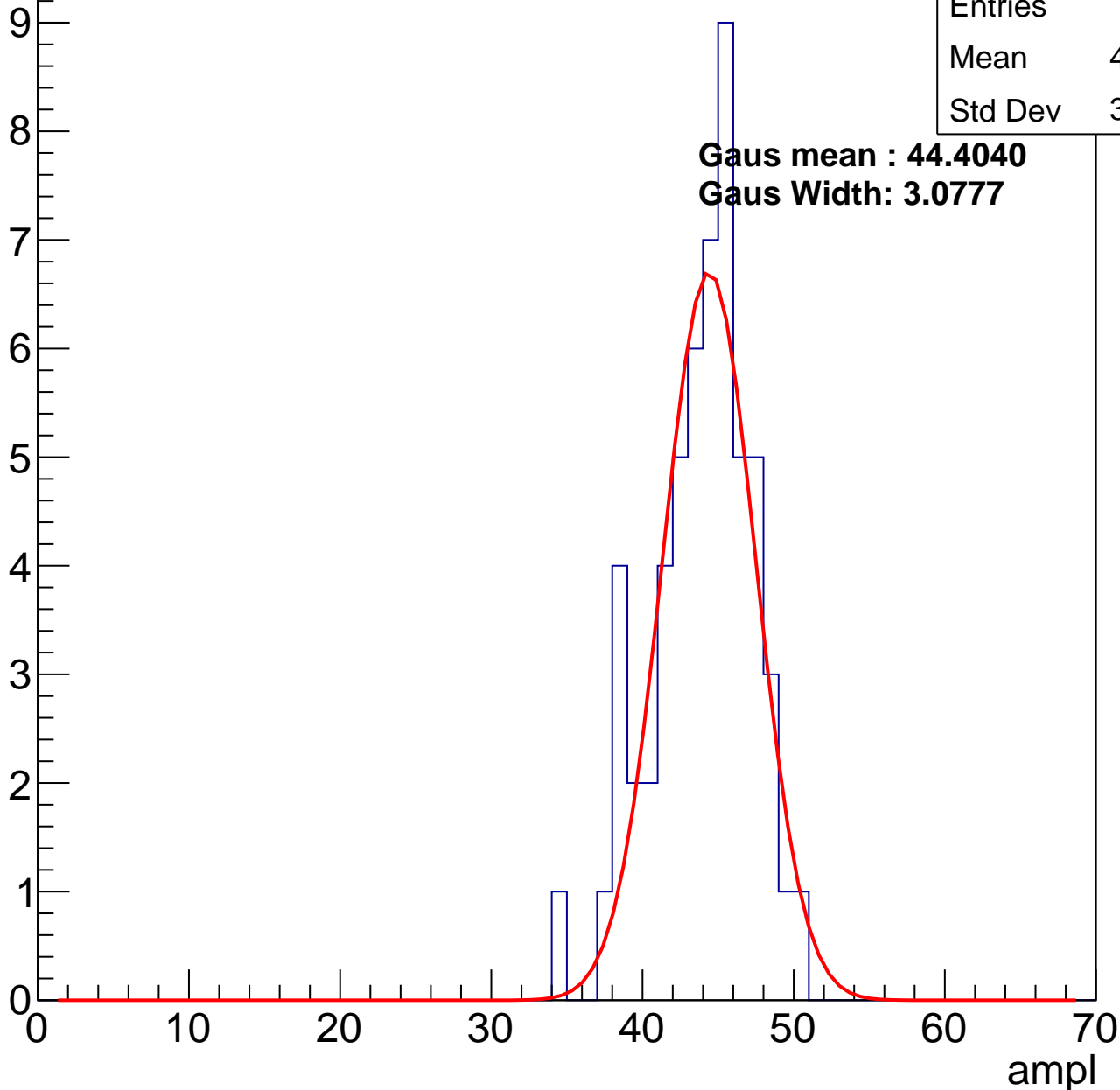
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.46
Std Dev	3.284

Gaus mean : 44.4040

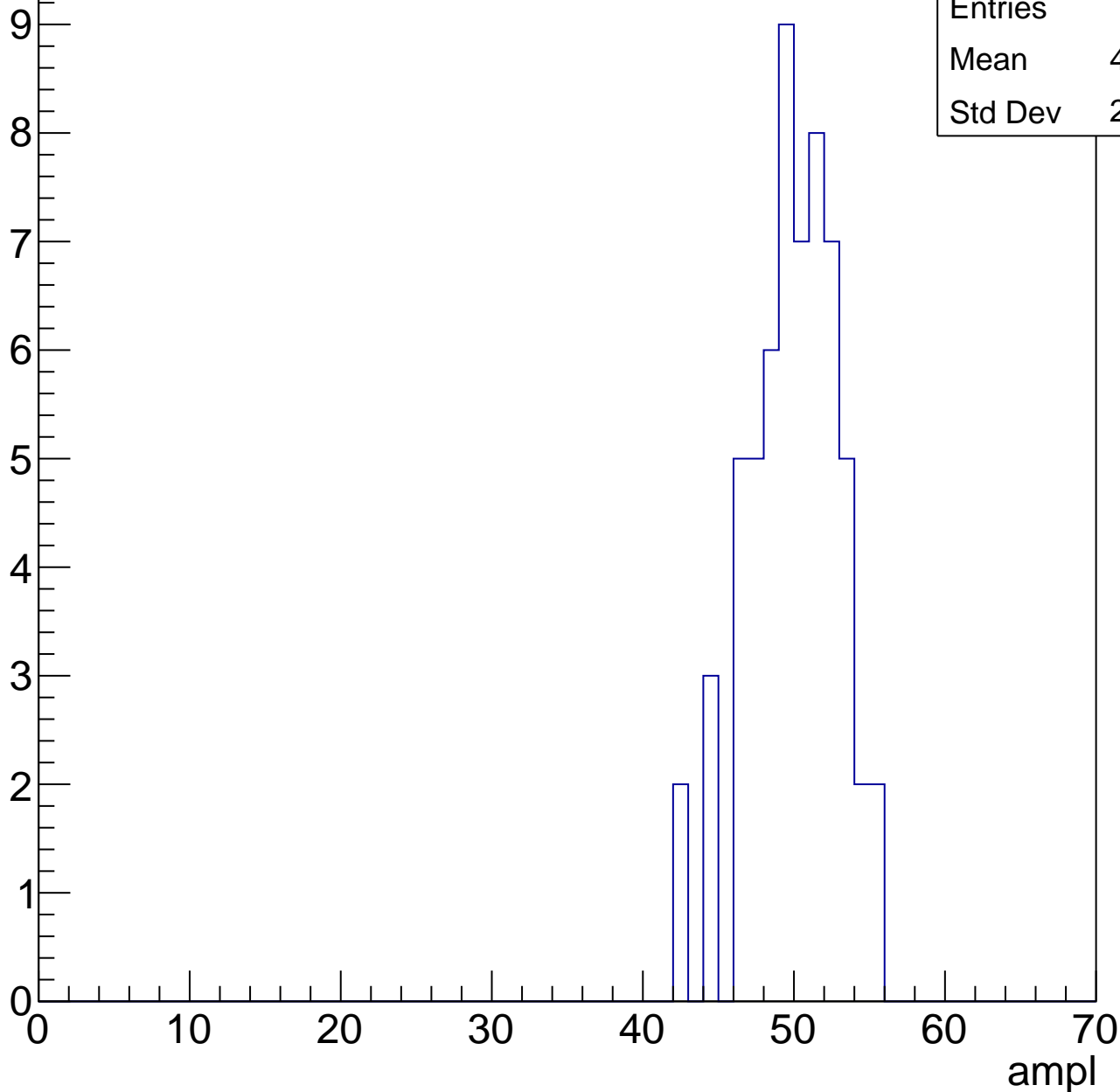
Gaus Width: 3.0777



B0L002S, U17-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

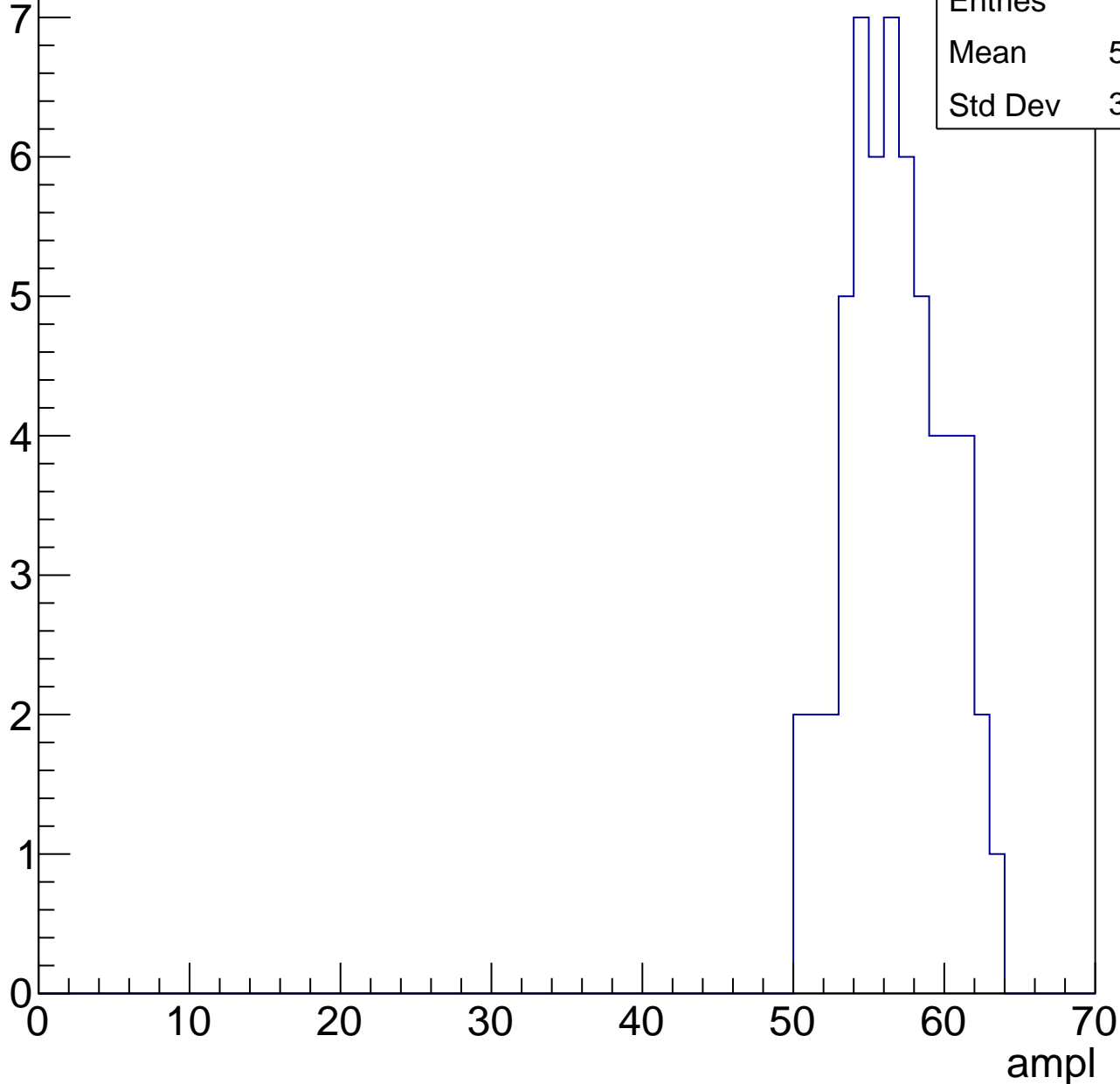


B0L002S, U17-ch81, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.32
Std Dev	3.174

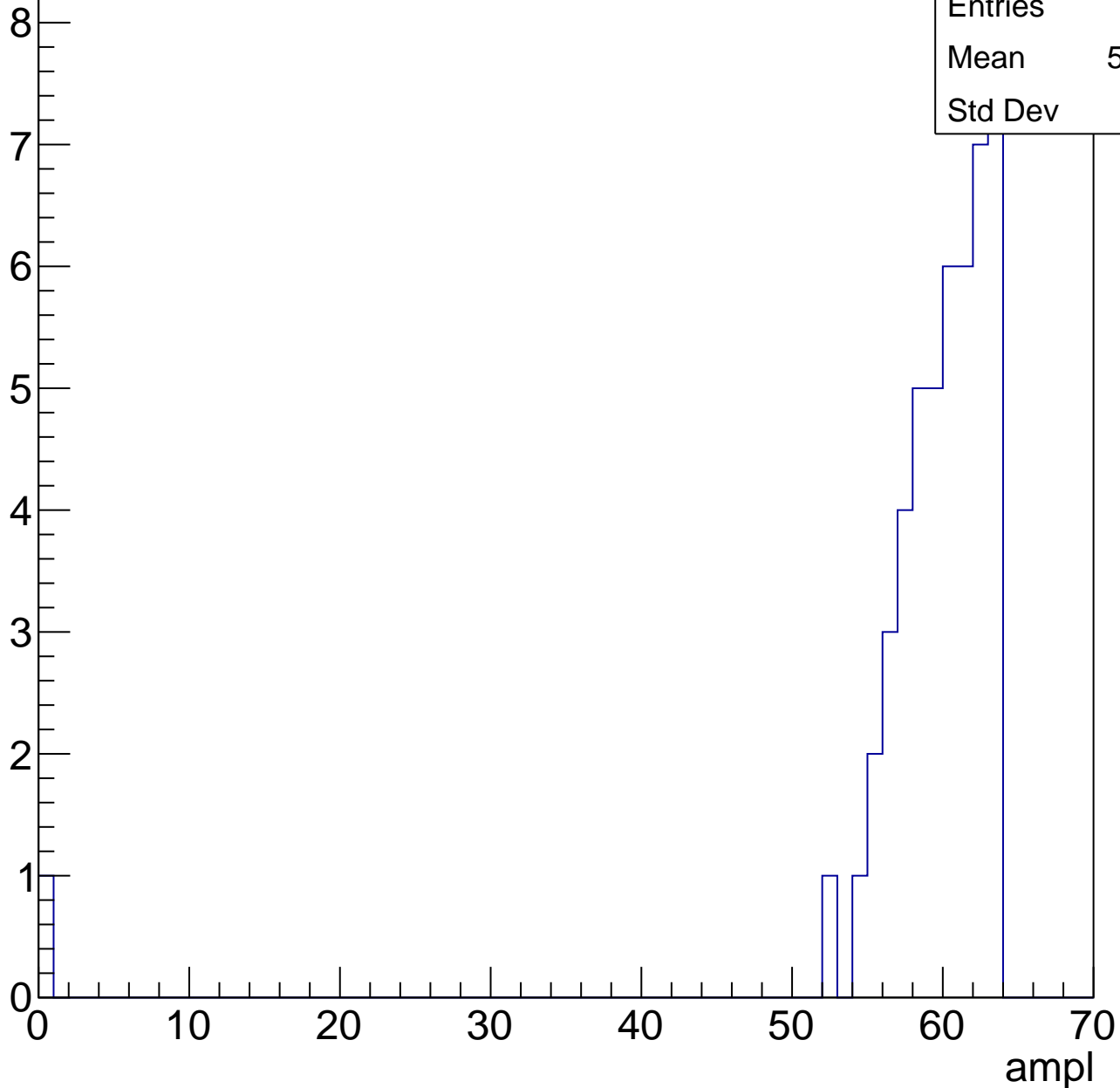


B0L002S, U17-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.39
Std Dev	8.85



B0L002S, U17-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch82, adc0

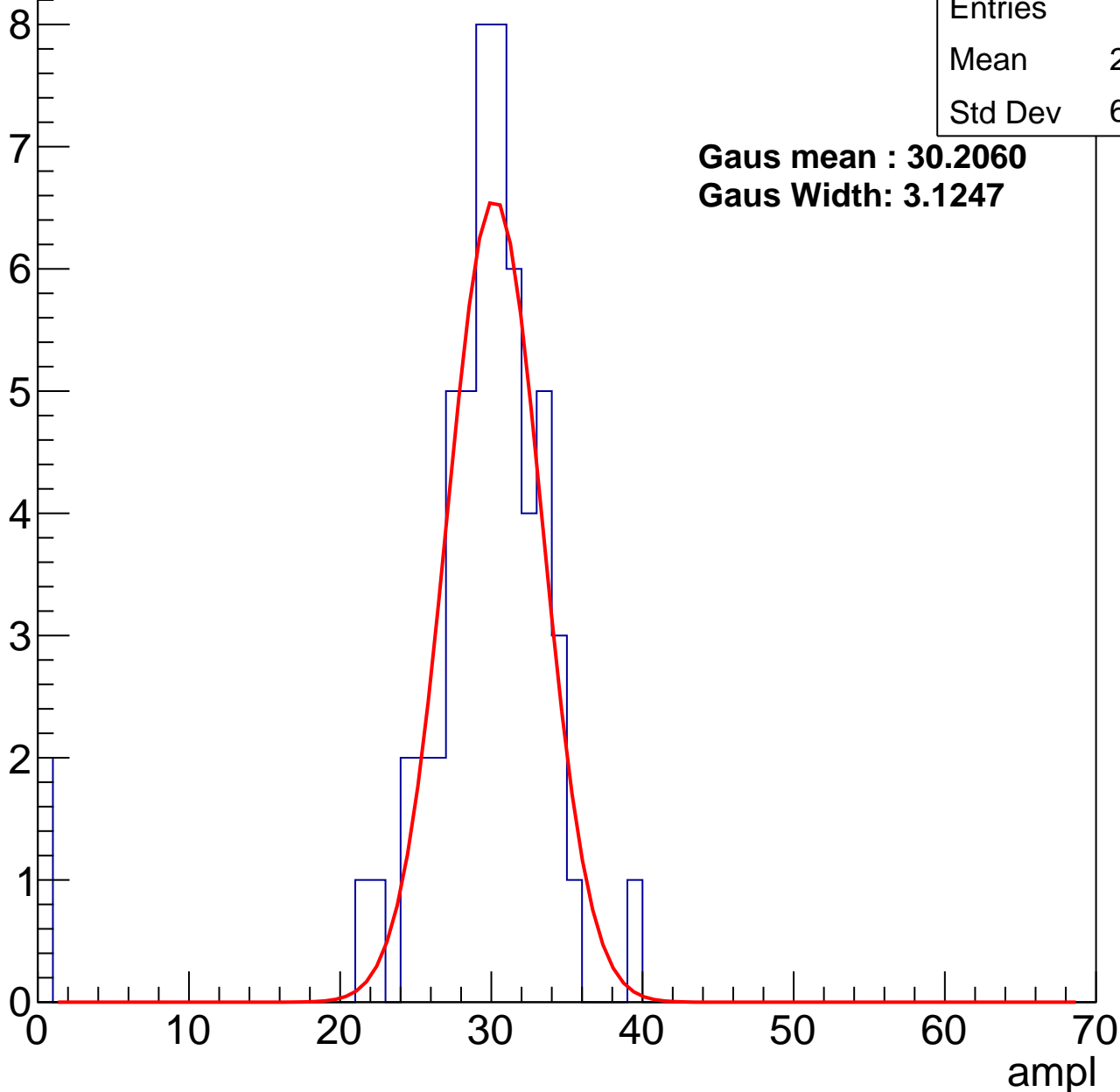
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	28.48
Std Dev	6.358

Gaus mean : 30.2060

Gaus Width: 3.1247



B0L002S, U17-ch82, adc1

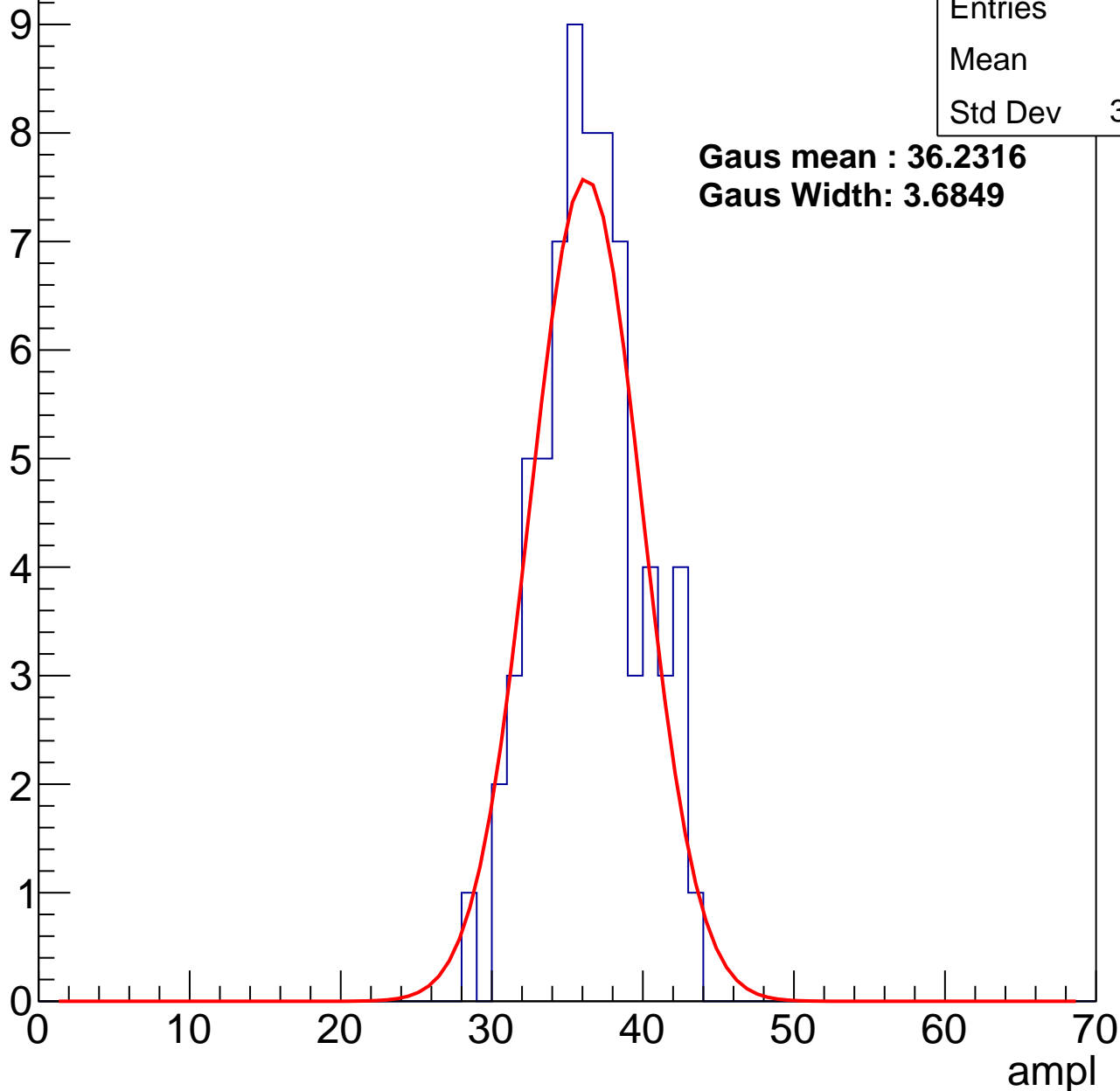
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	36
Std Dev	3.312

Gaus mean : 36.2316

Gaus Width: 3.6849



B0L002S, U17-ch82, adc2

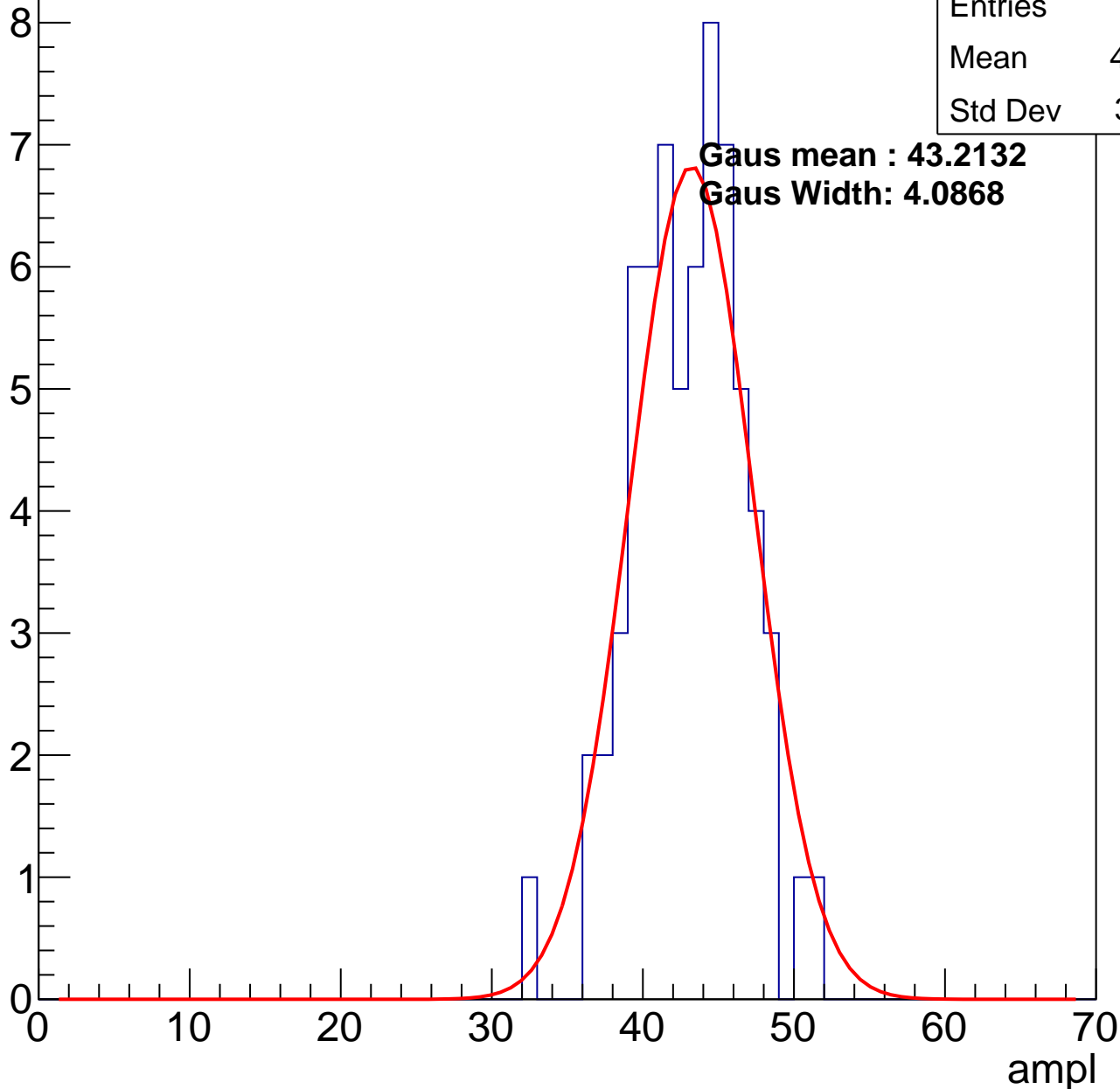
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.55
Std Dev	3.601

Gaus mean : 43.2132

Gaus Width: 4.0868



B0L002S, U17-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	49.03
Std Dev	3.283

Entry

10

8

6

4

2

0

0

10

20

30

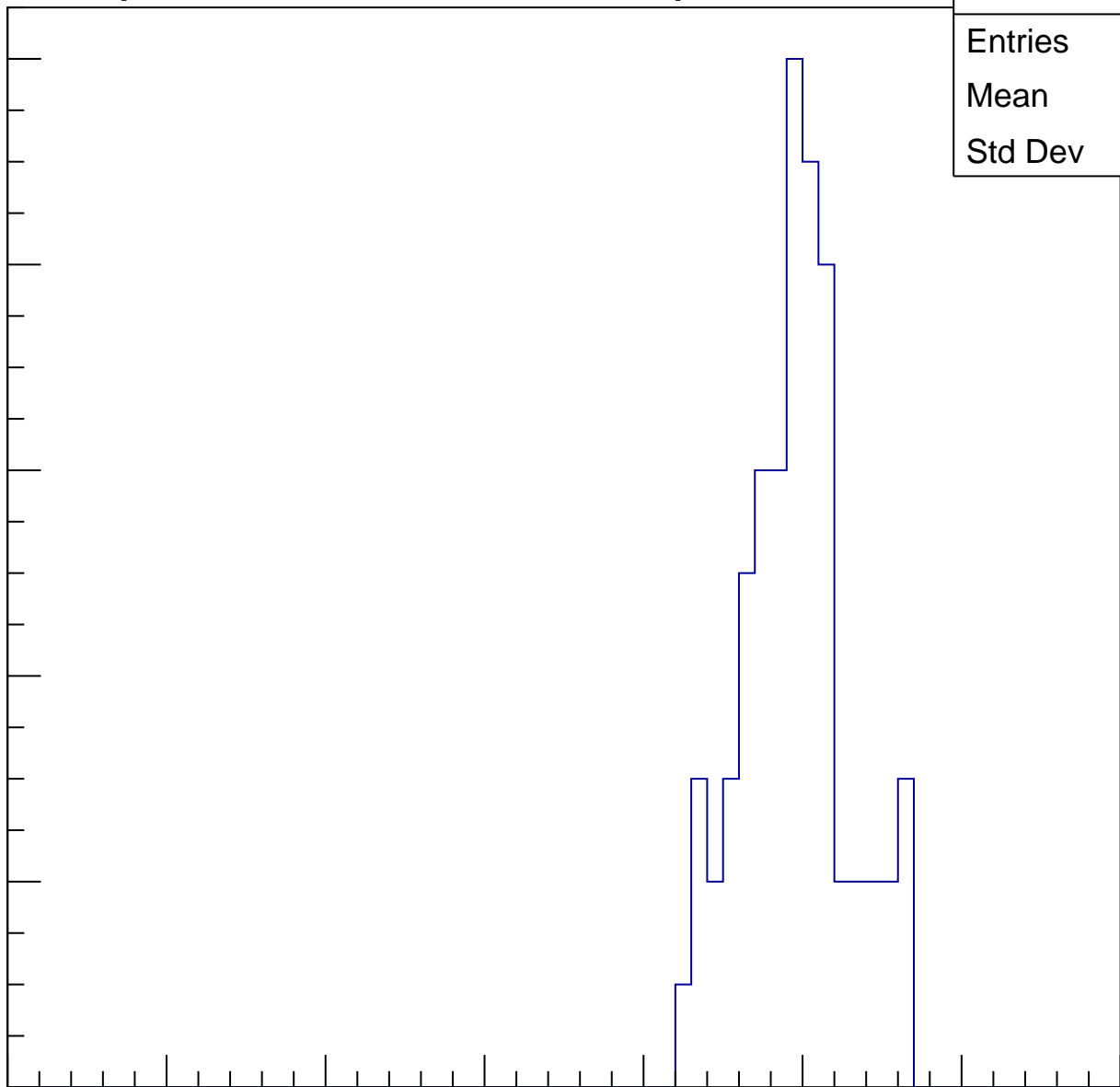
40

50

60

70

ampl

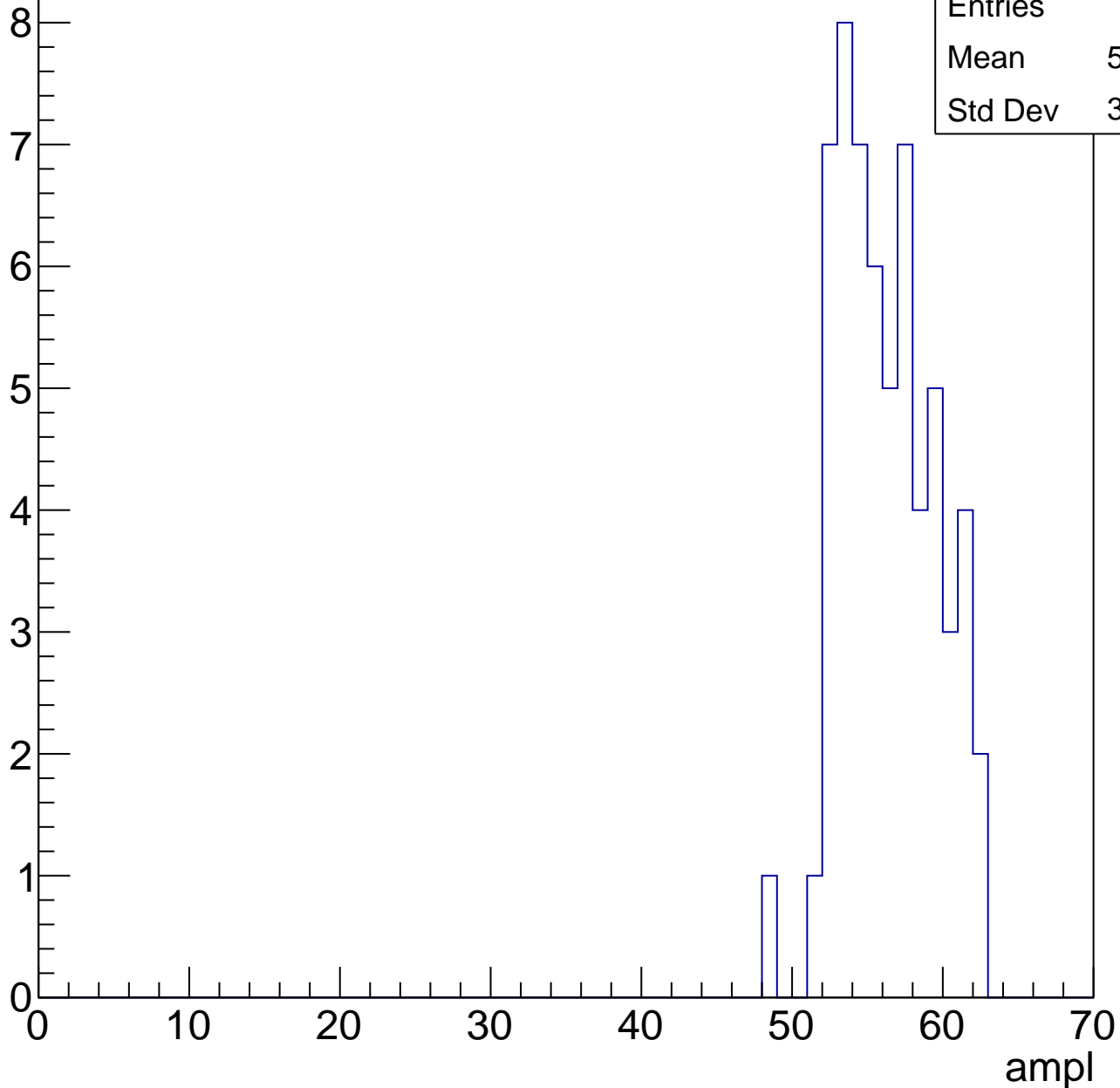


B0L002S, U17-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	55.82
Std Dev	3.144



B0L002S, U17-ch82, adc5

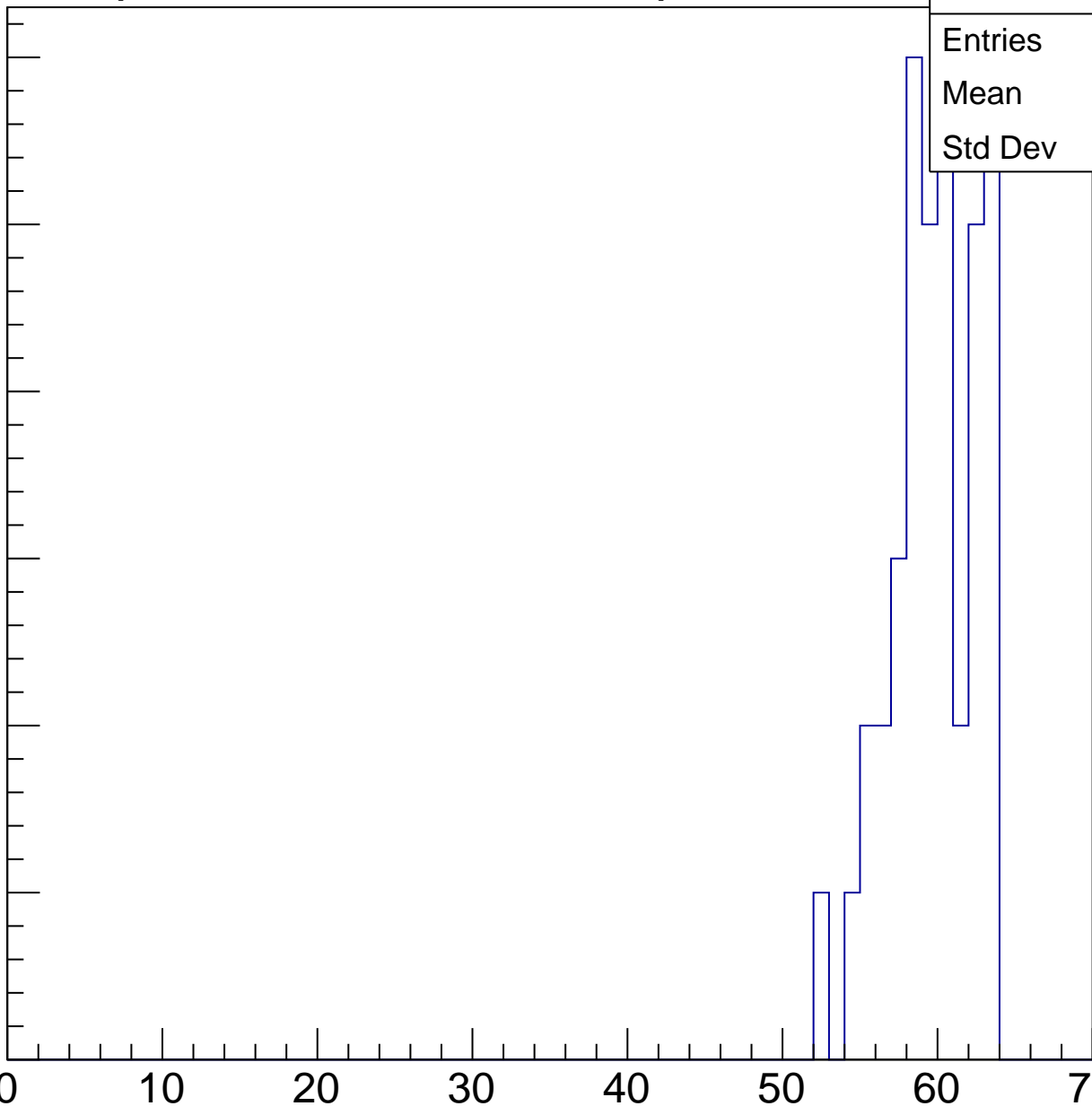
calib_packv5_042523_0143.root, FC#8, port C1

Entry

6
5
4
3
2
1
0

Entries	39
Mean	59.28
Std Dev	2.745

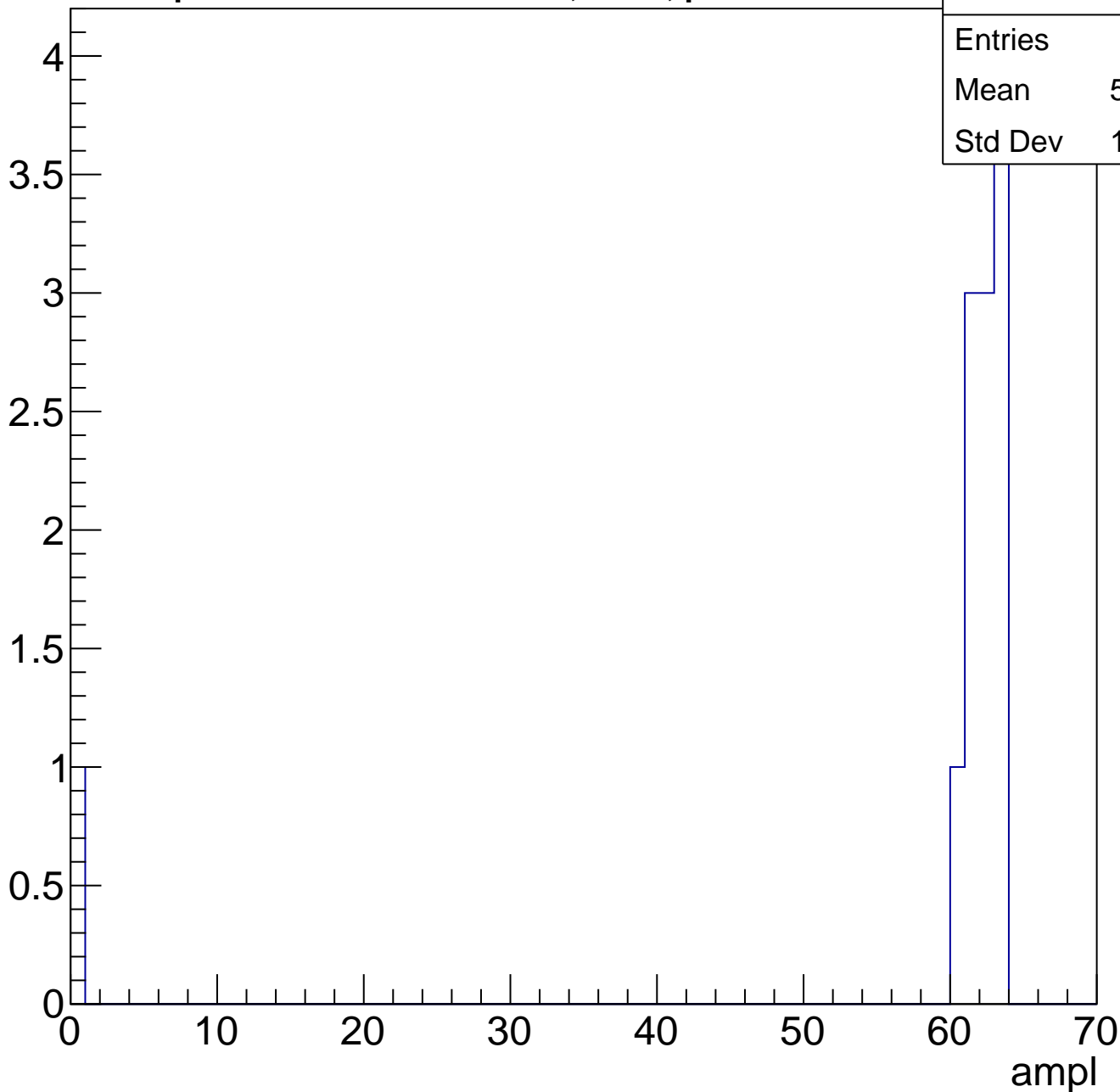
ampl



B0L002S, U17-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

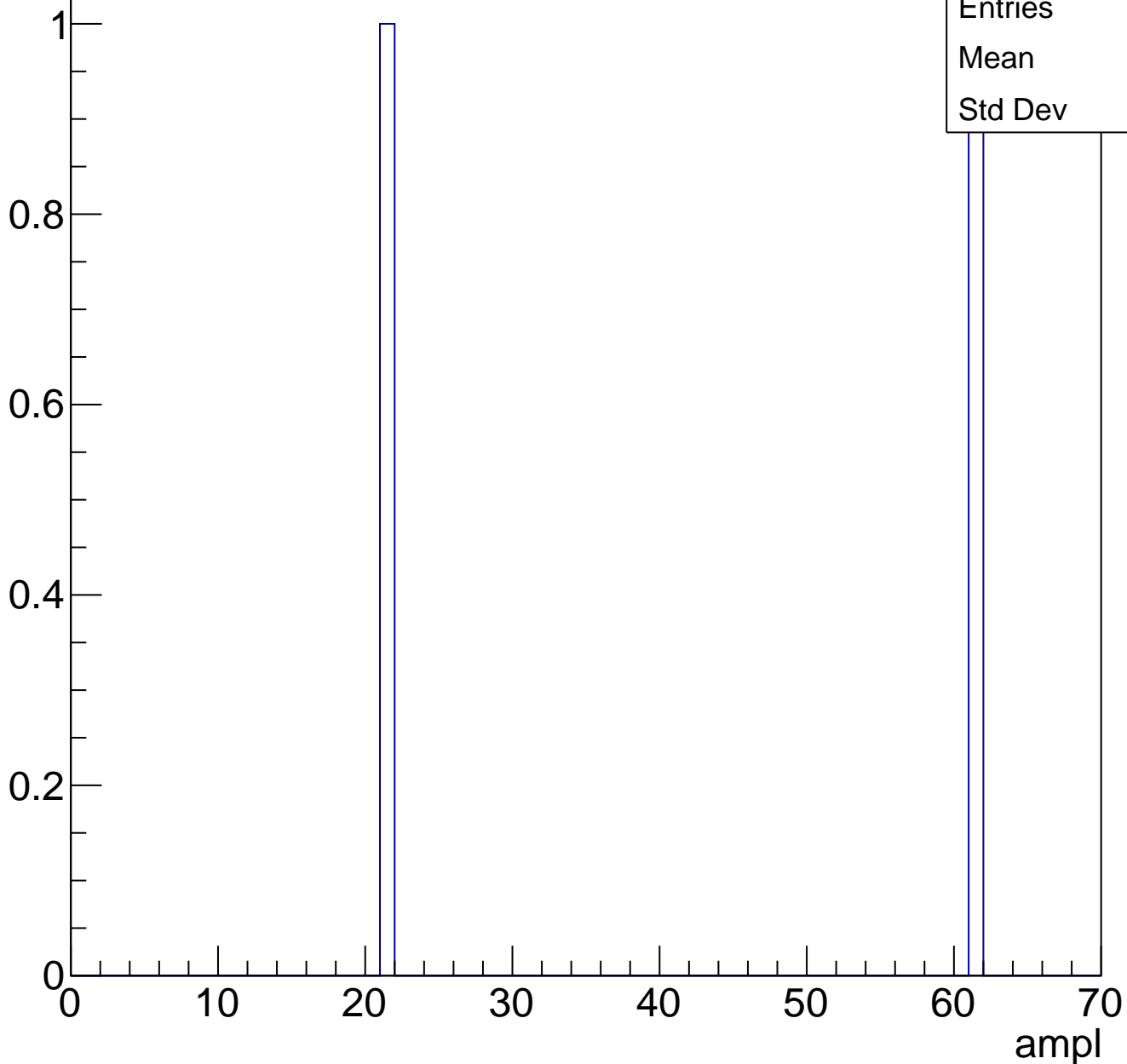


Entries	12
Mean	56.75
Std Dev	17.14

B0L002S, U17-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch83, adc0

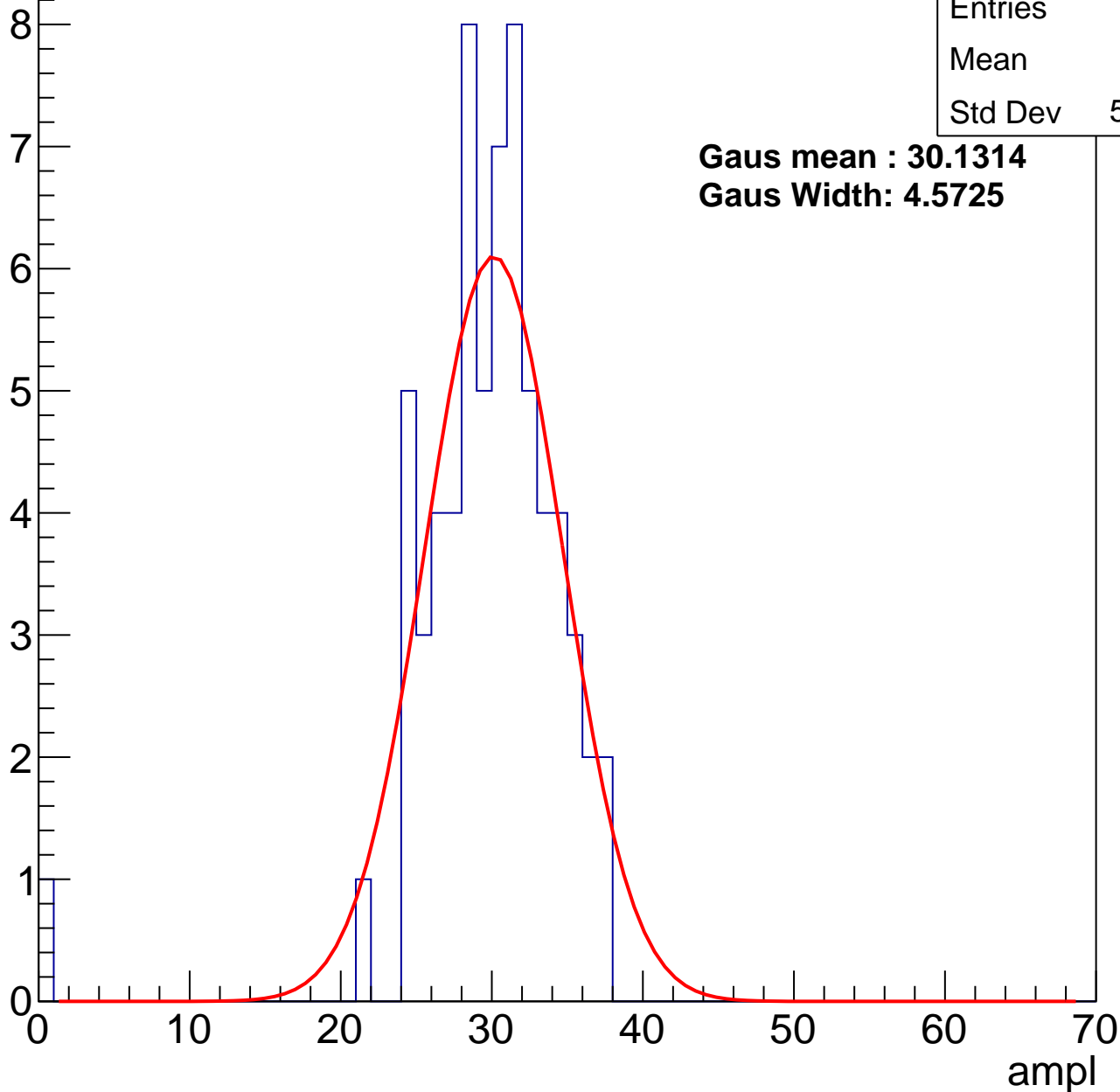
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.3
Std Dev	5.096

Gaus mean : 30.1314

Gaus Width: 4.5725



B0L002S, U17-ch83, adc1

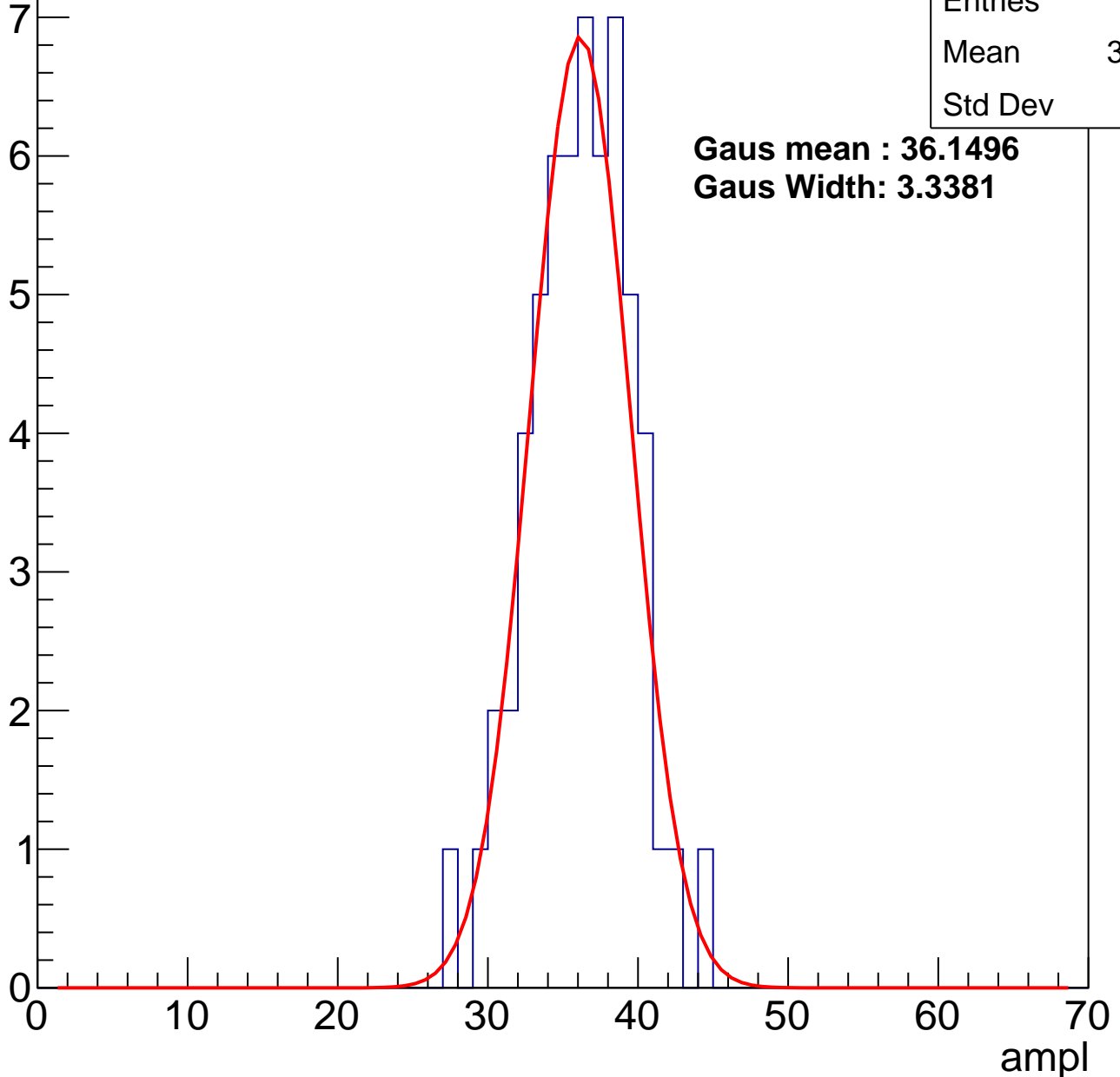
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	35.71
Std Dev	3.33

Gaus mean : 36.1496

Gaus Width: 3.3381



B0L002S, U17-ch83, adc2

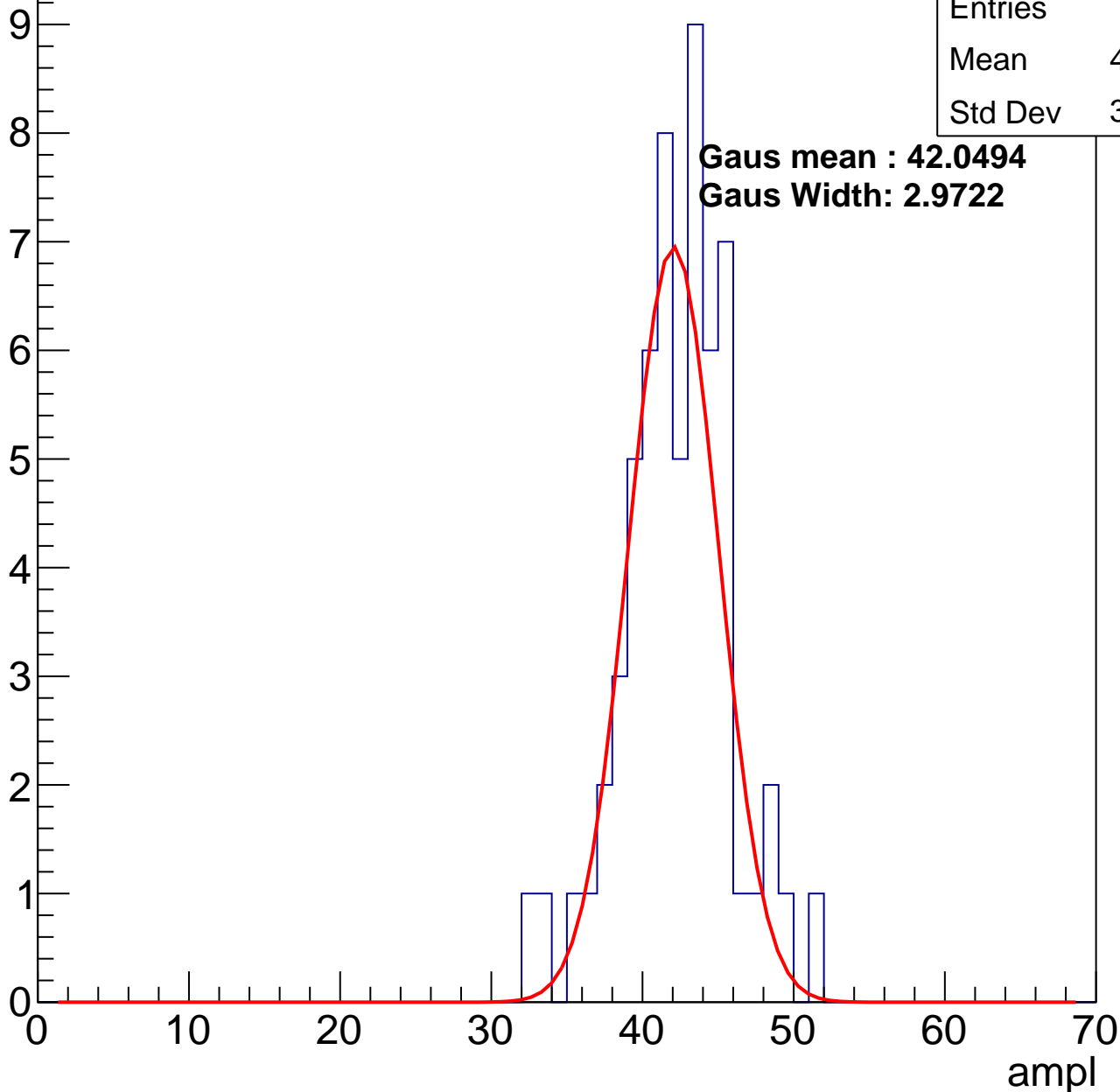
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	41.84
Std Dev	3.572

Gaus mean : 42.0494

Gaus Width: 2.9722

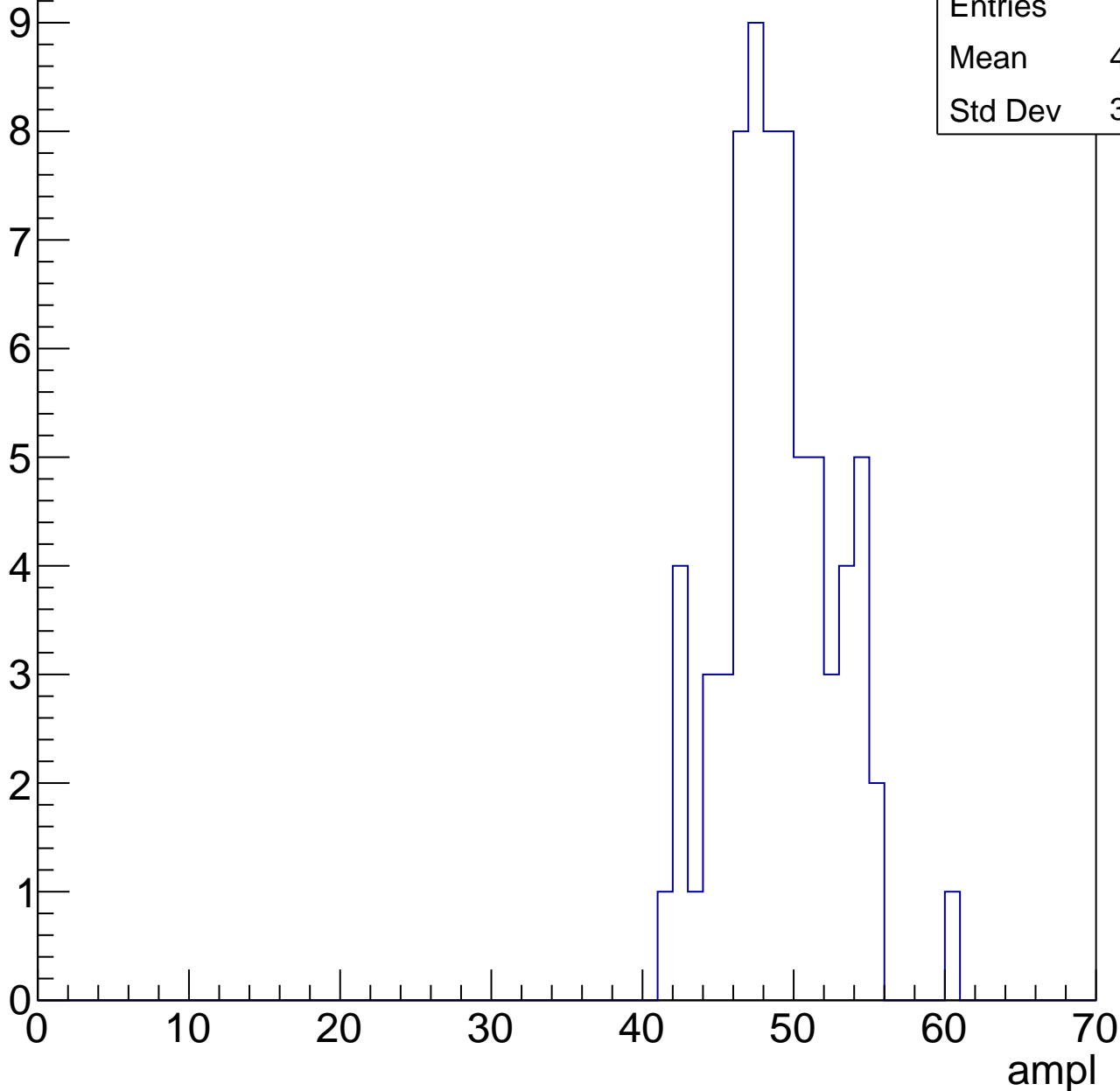


B0L002S, U17-ch83, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	48.56
Std Dev	3.694

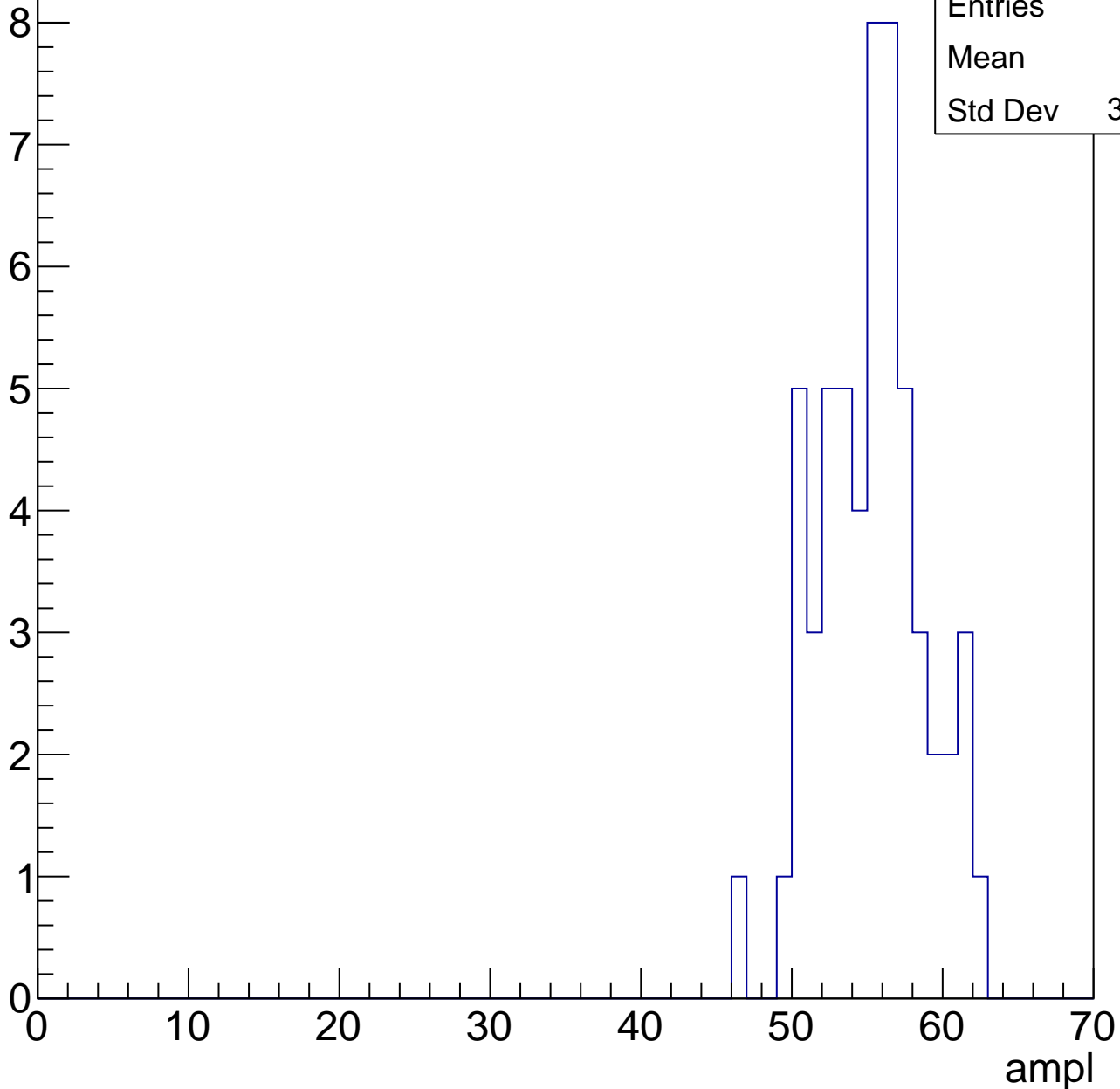


B0L002S, U17-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	54.8
Std Dev	3.409

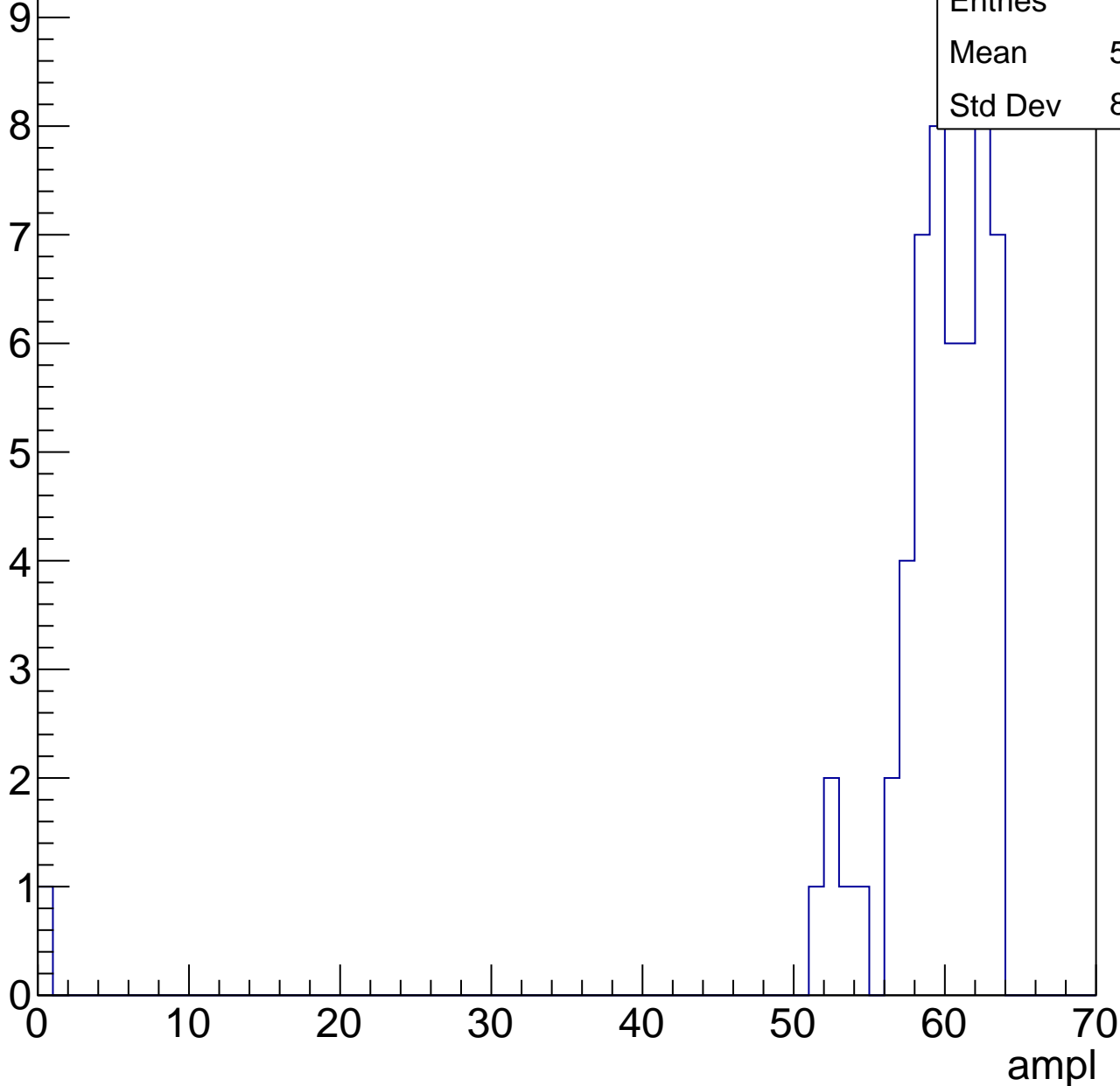


B0L002S, U17-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

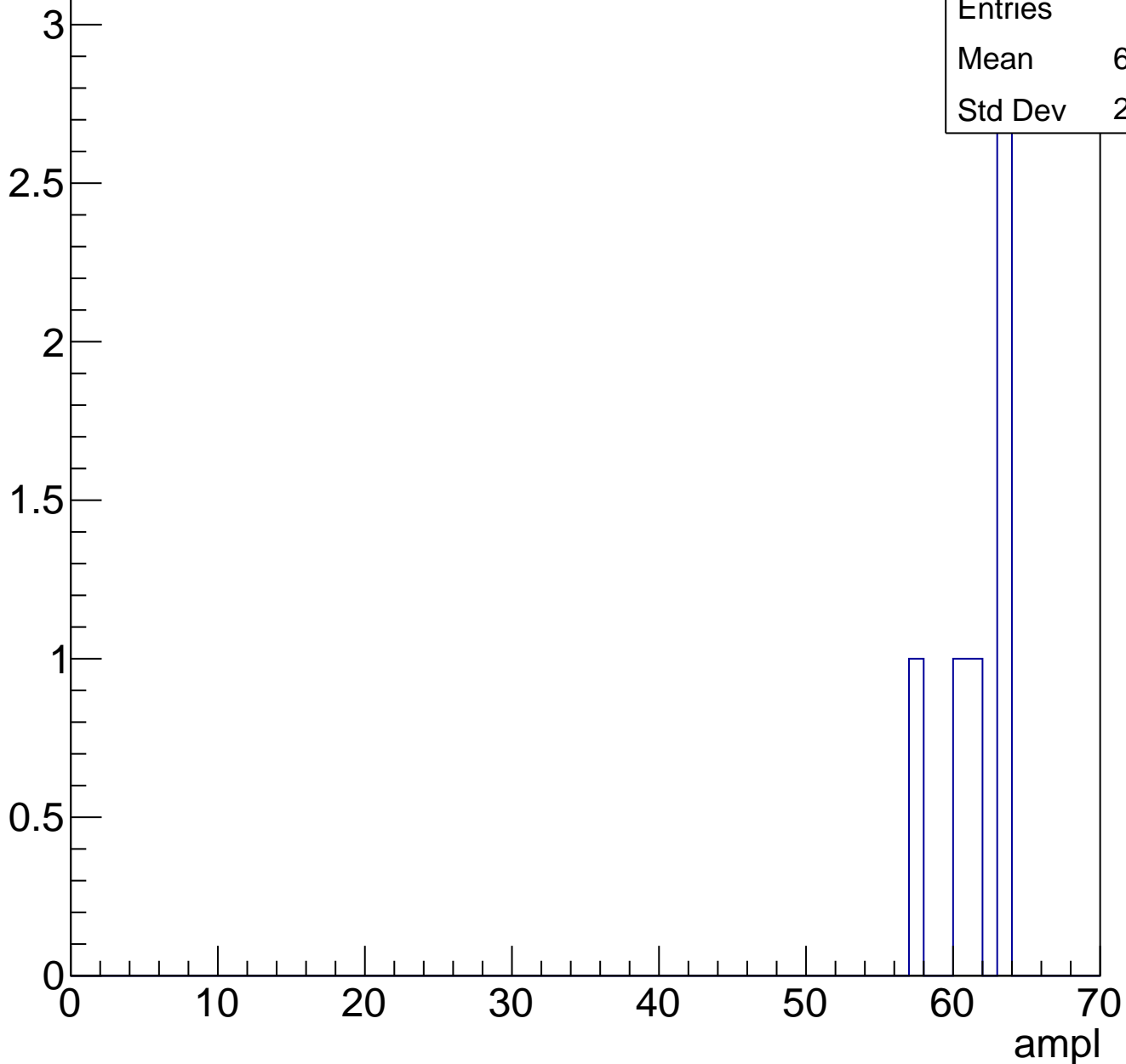
Entries	55
Mean	58.27
Std Dev	8.463



B0L002S, U17-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch84, adc0

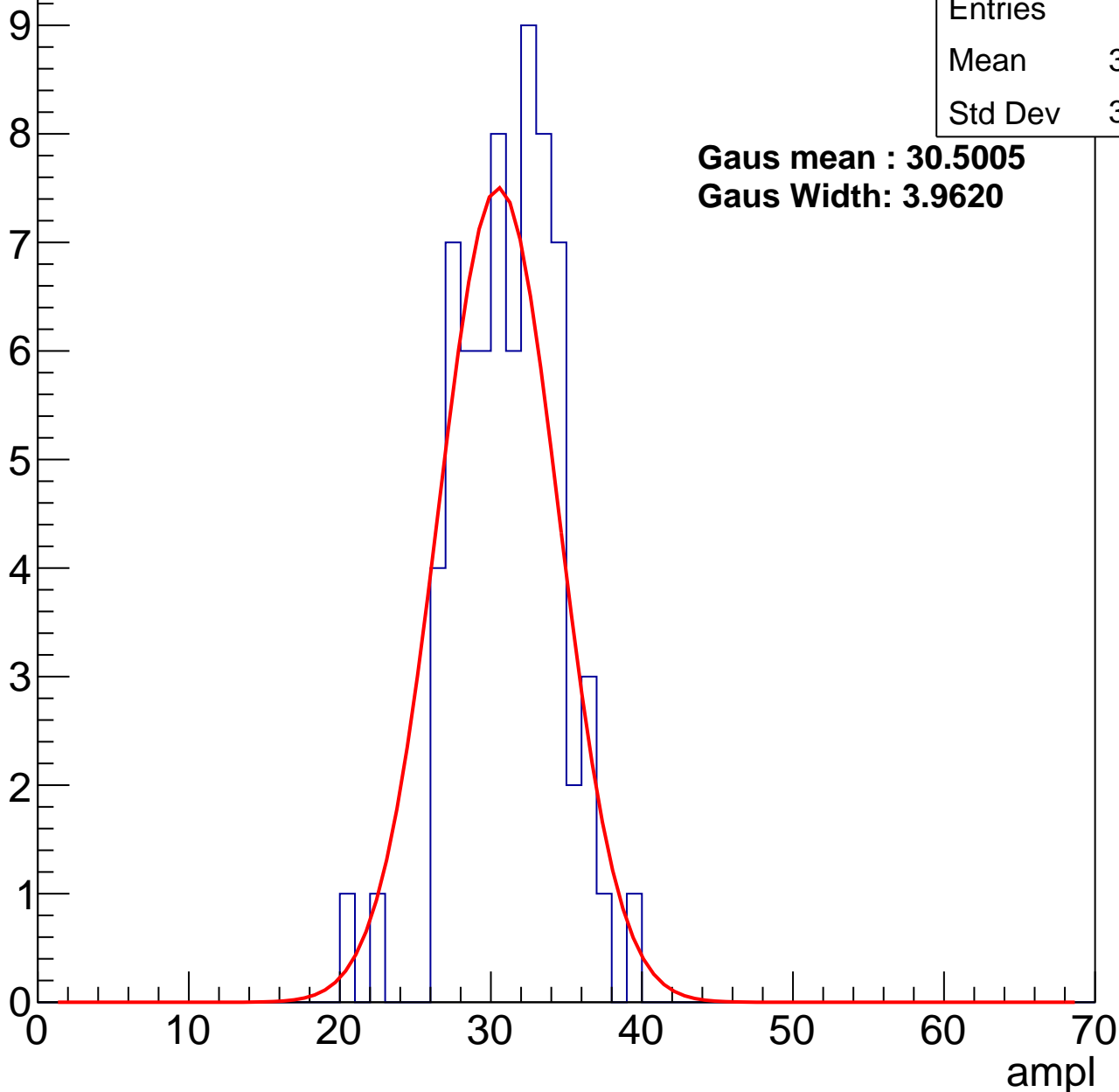
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.67
Std Dev	3.388

Gaus mean : 30.5005

Gaus Width: 3.9620



B0L002S, U17-ch84, adc1

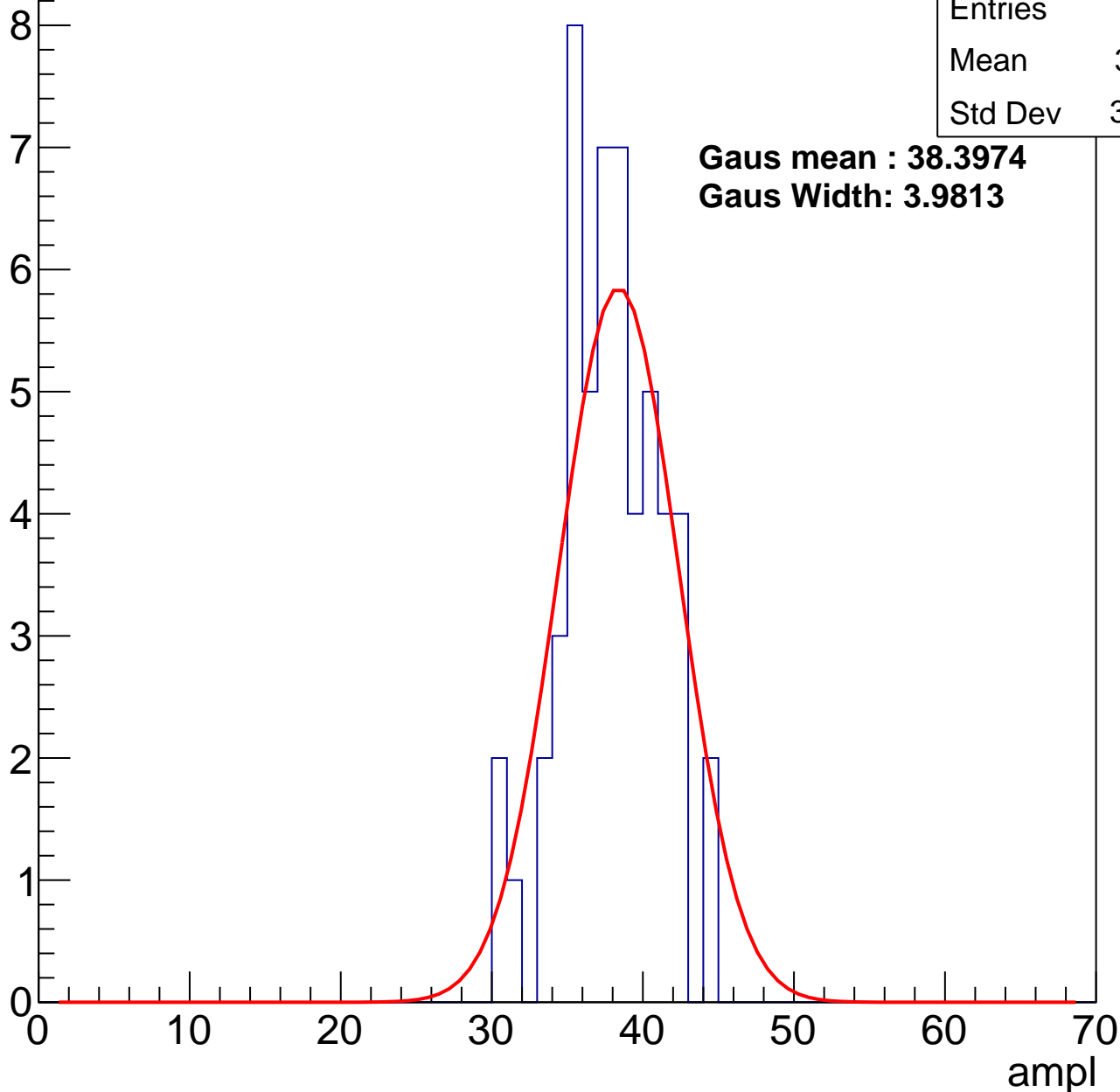
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	37.41
Std Dev	3.194

Gaus mean : 38.3974

Gaus Width: 3.9813



B0L002S, U17-ch84, adc2

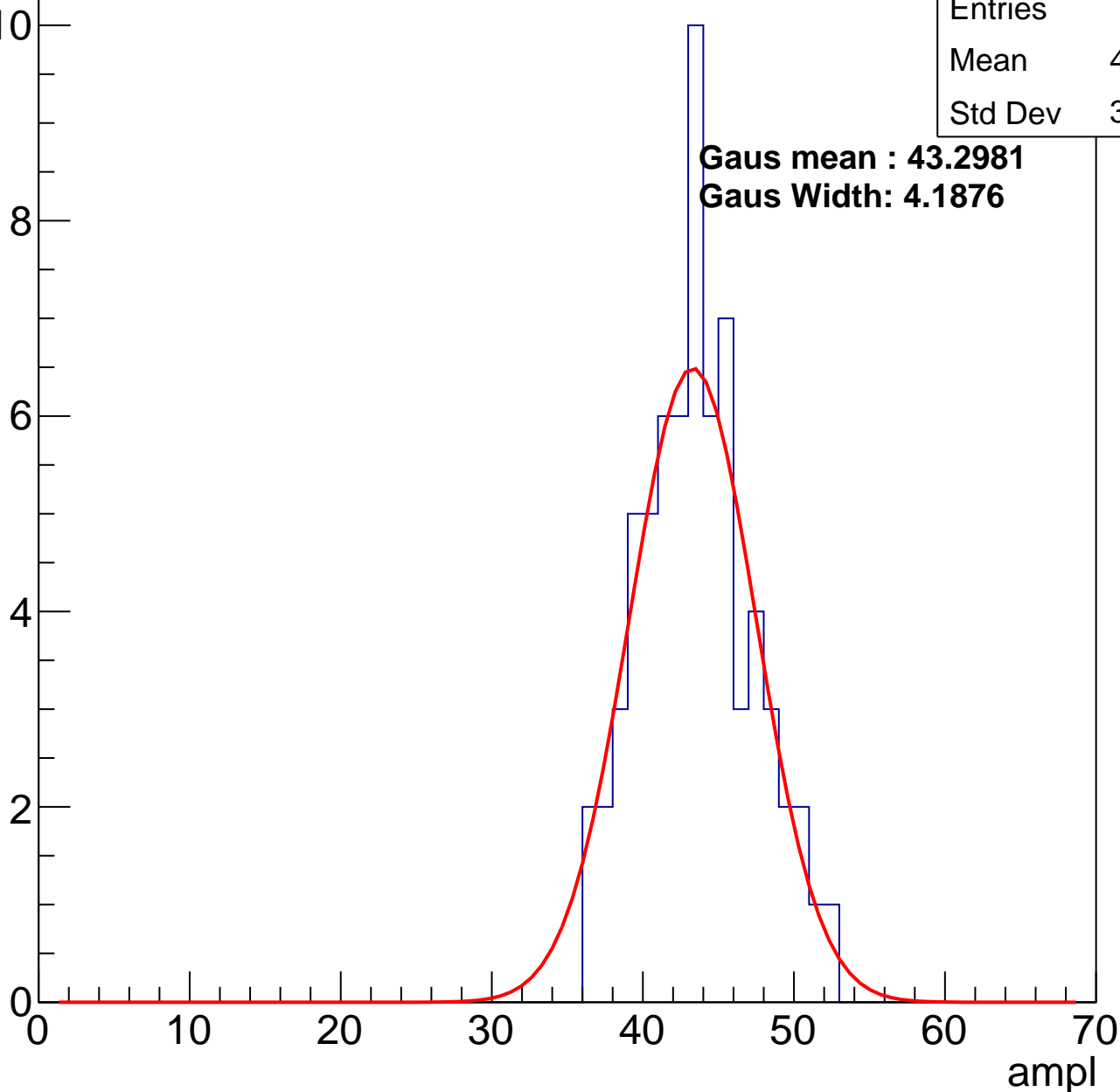
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.13
Std Dev	3.666

Gaus mean : 43.2981

Gaus Width: 4.1876

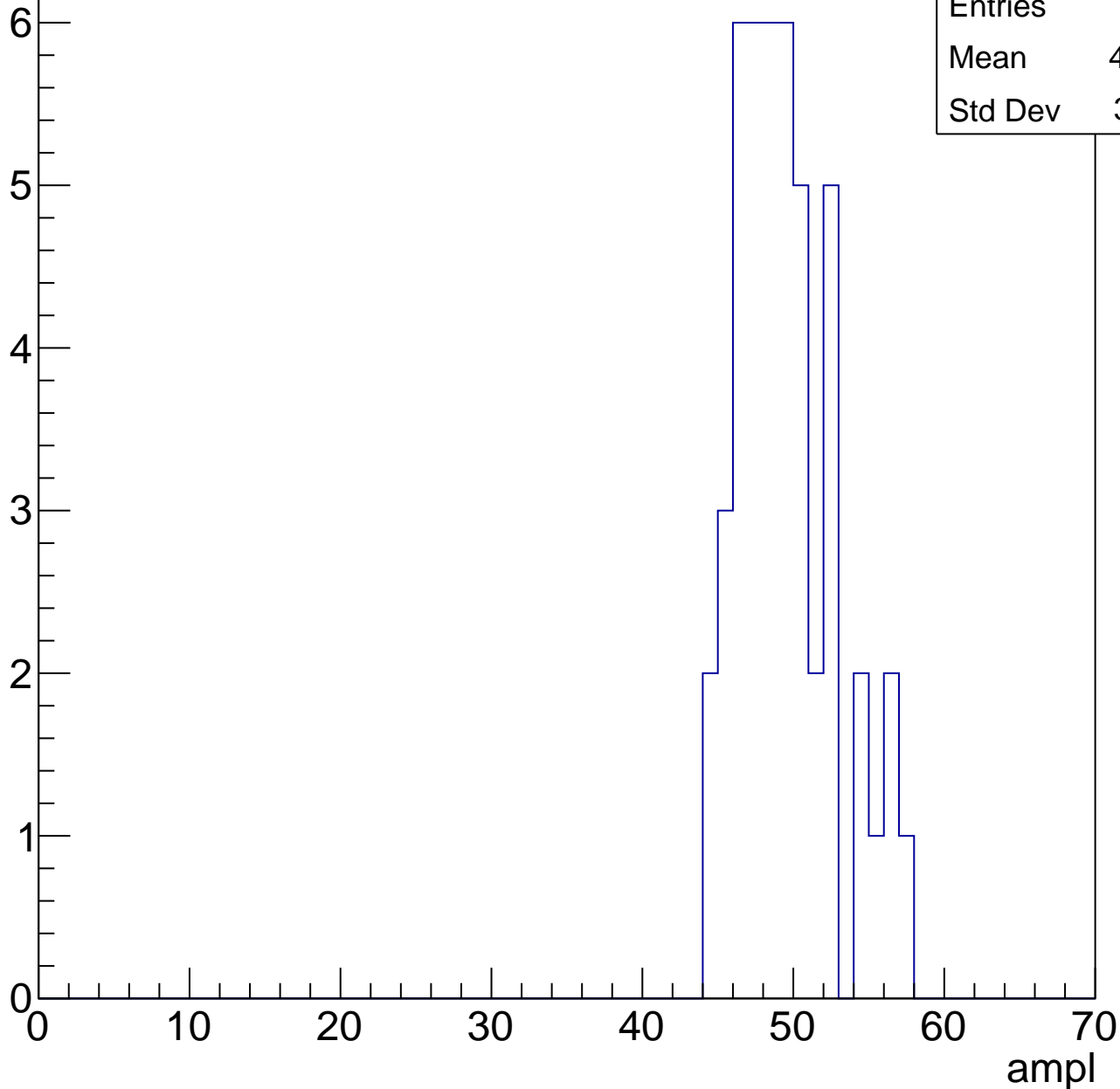


B0L002S, U17-ch84, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	49.09
Std Dev	3.221

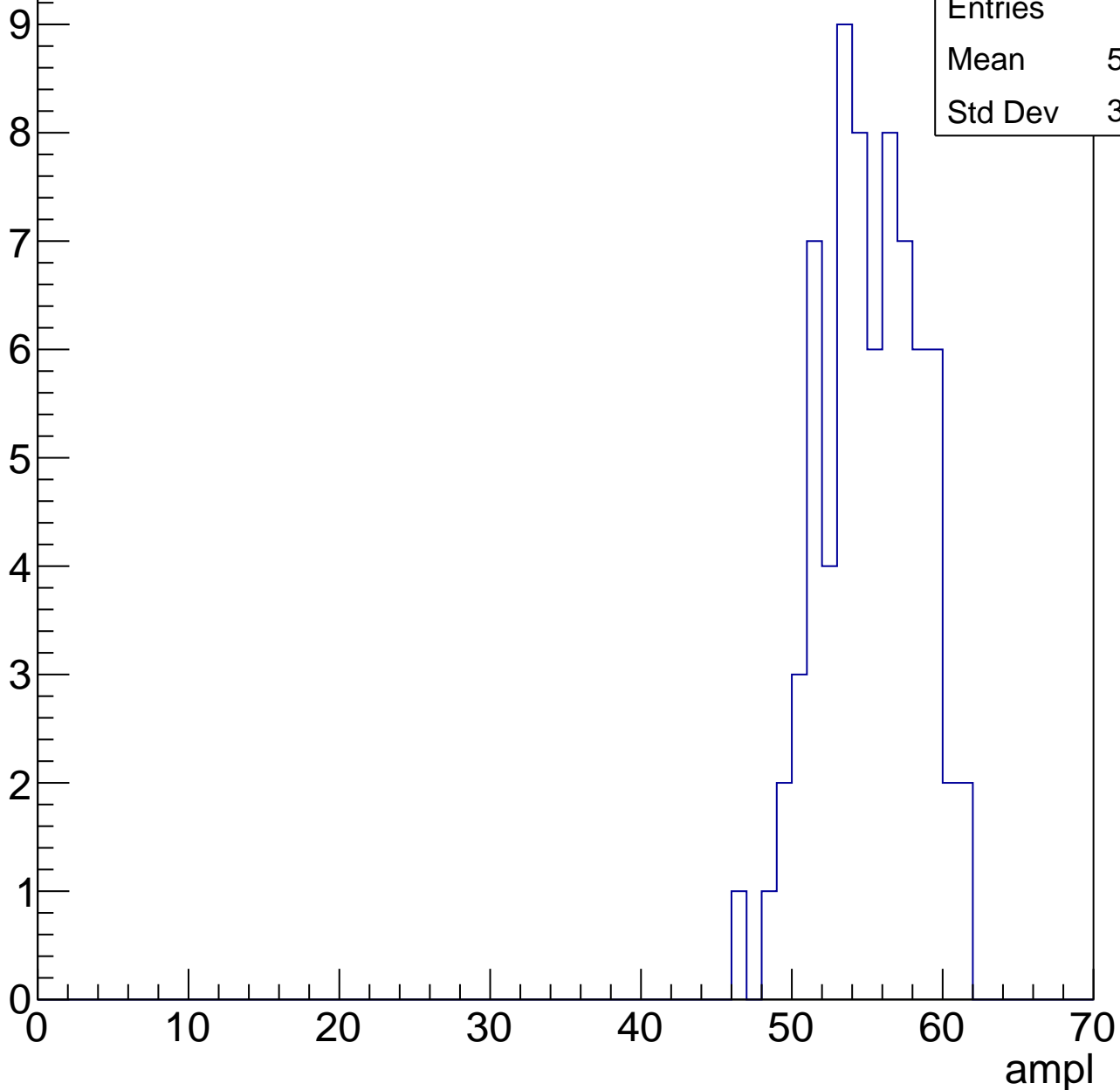


B0L002S, U17-ch84, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	54.68
Std Dev	3.269

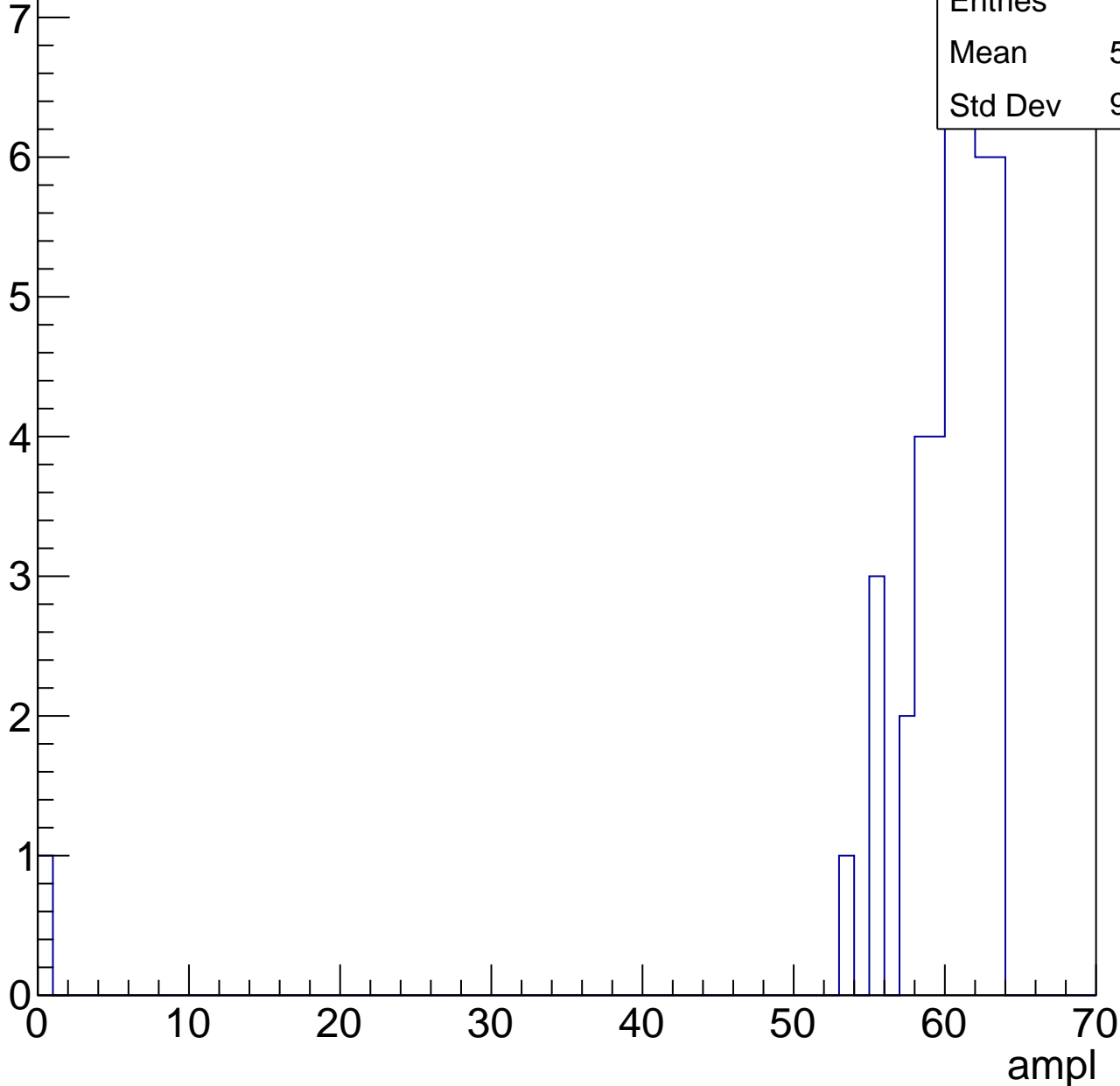


B0L002S, U17-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

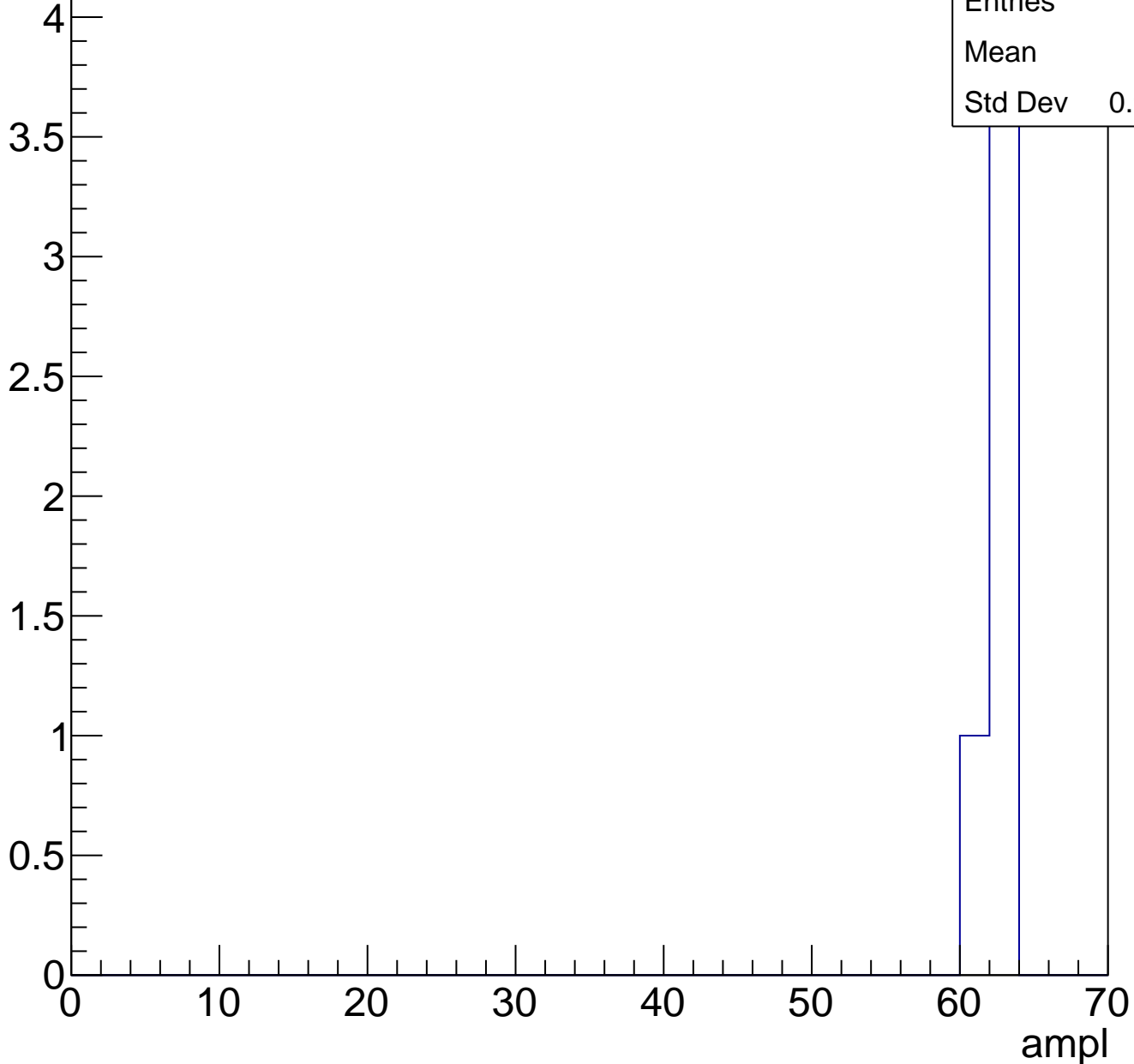
Entries	41
Mean	58.46
Std Dev	9.564



B0L002S, U17-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch85, adc0

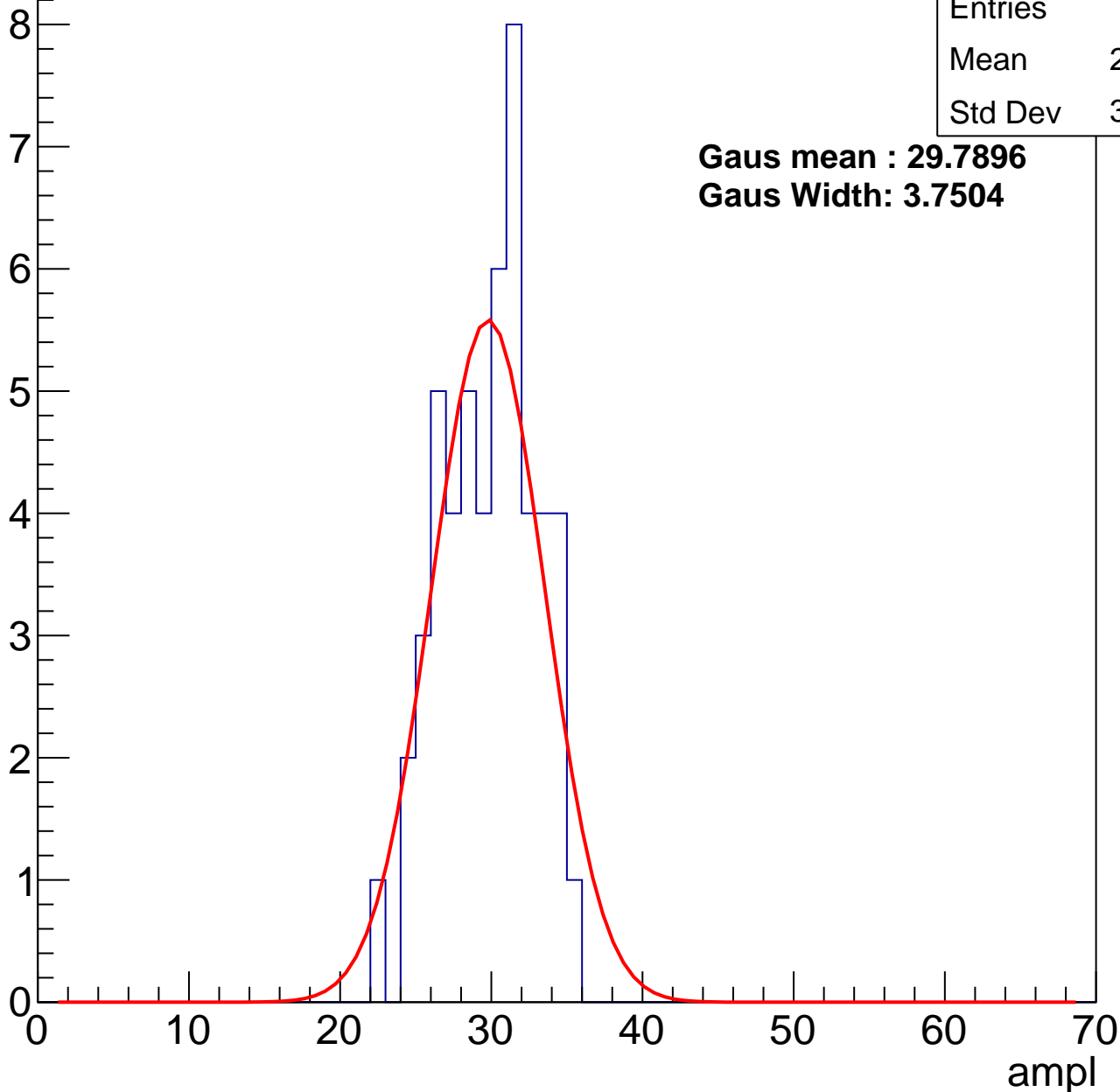
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	29.37
Std Dev	3.068

Gaus mean : 29.7896

Gaus Width: 3.7504



B0L002S, U17-ch85, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	36.75
Std Dev	3.998

Gaus mean : 37.4404

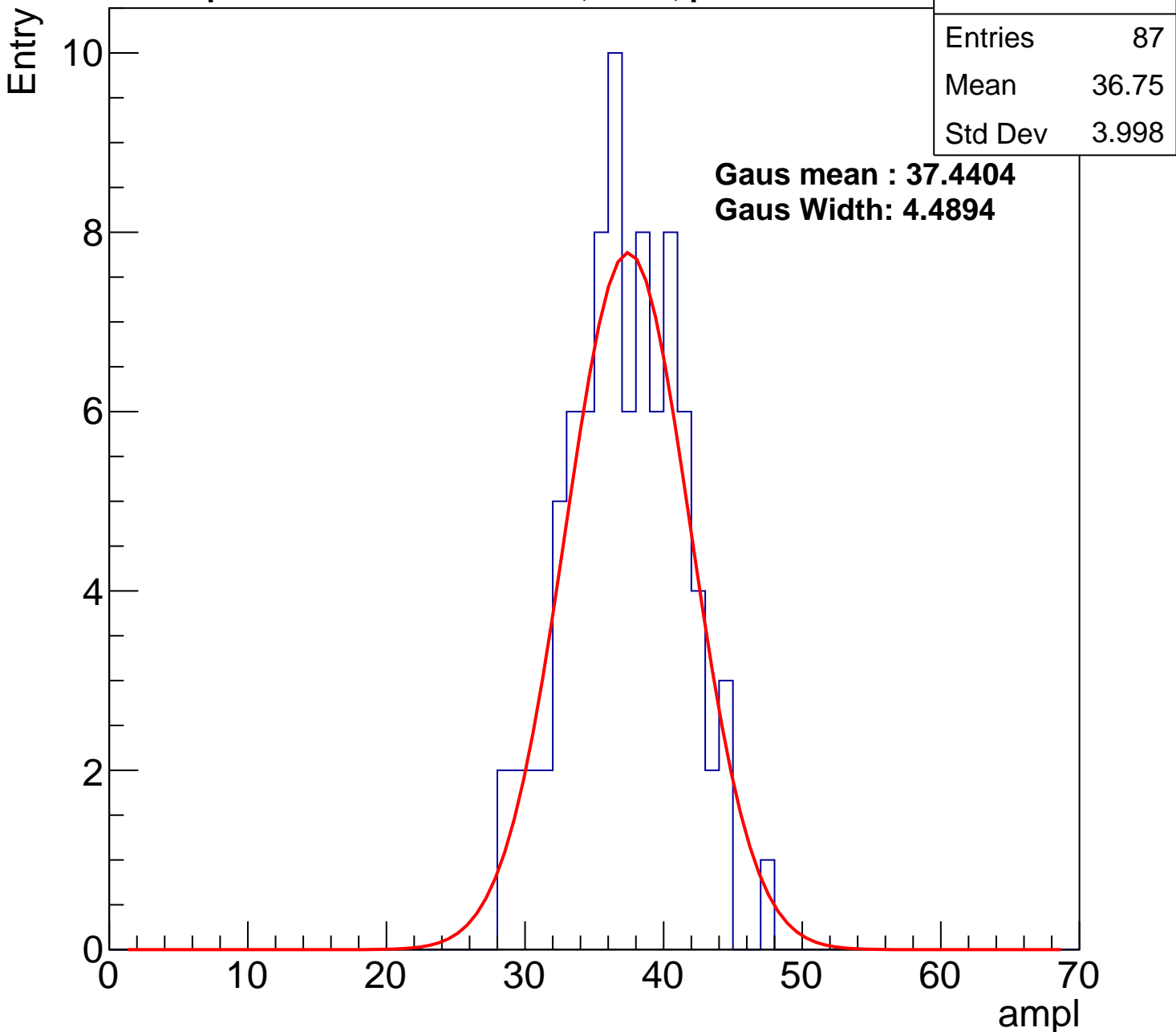
Gaus Width: 4.4894

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch85, adc2

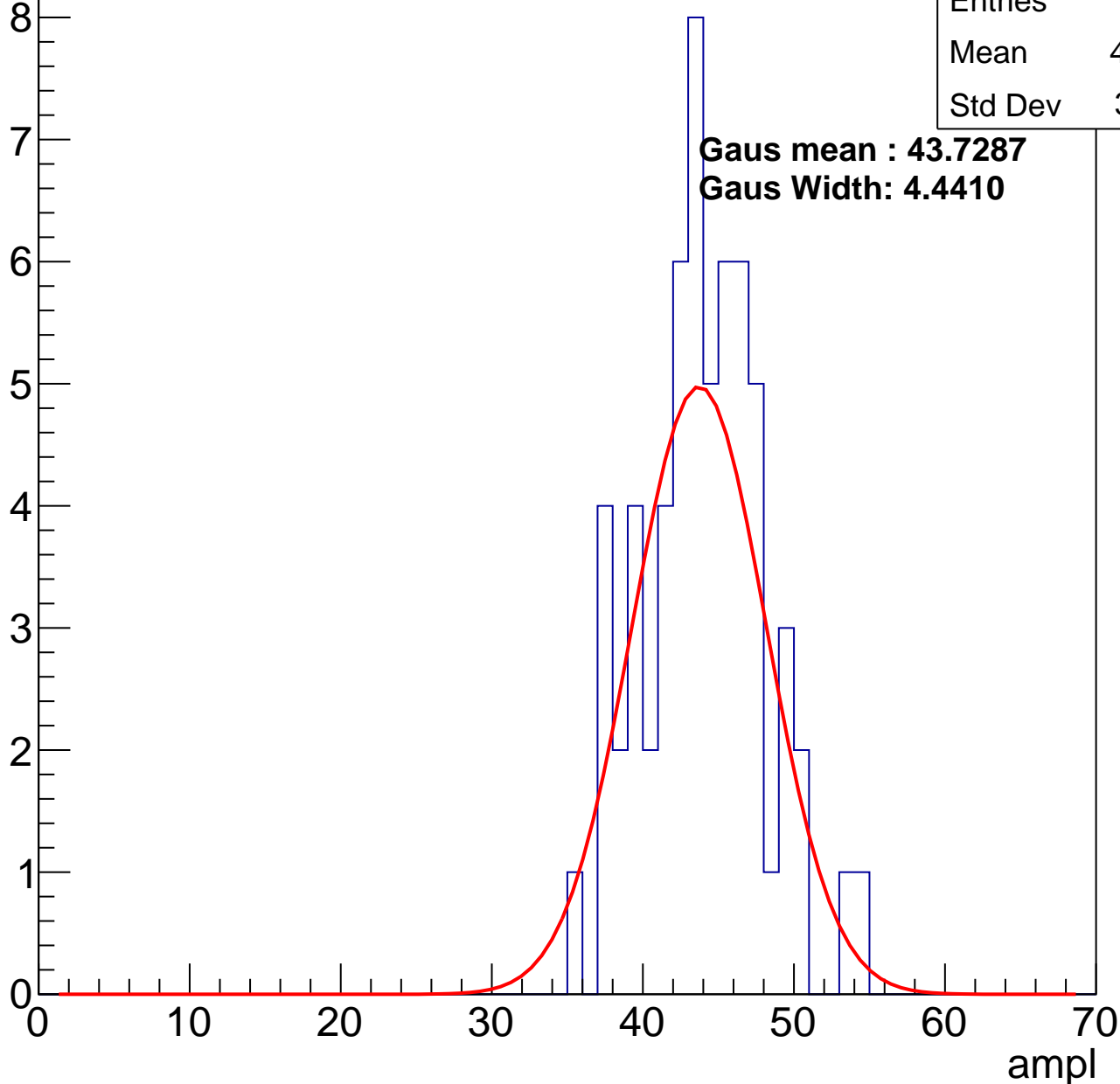
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.57
Std Dev	3.961

Gaus mean : 43.7287

Gaus Width: 4.4410

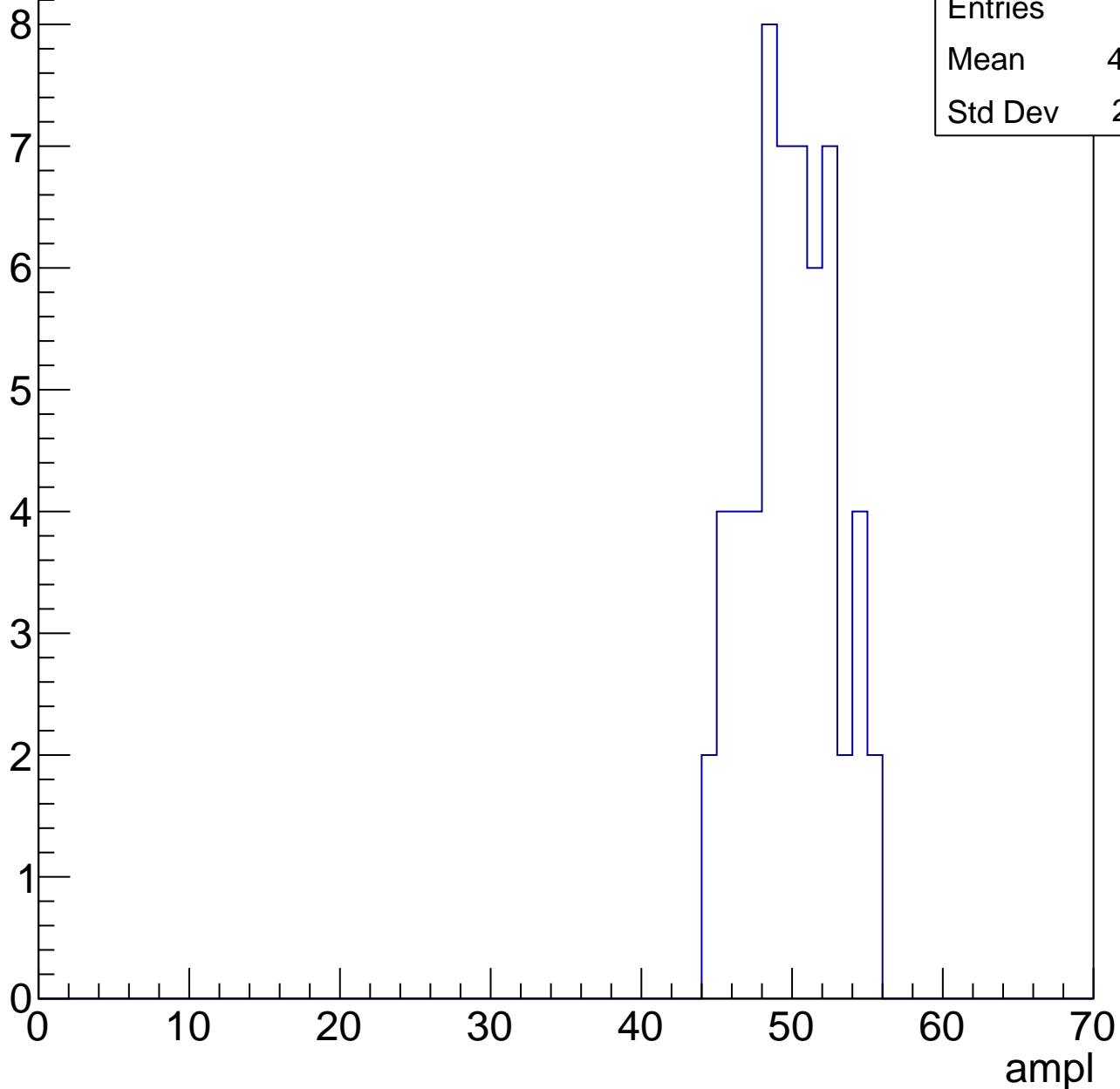


B0L002S, U17-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

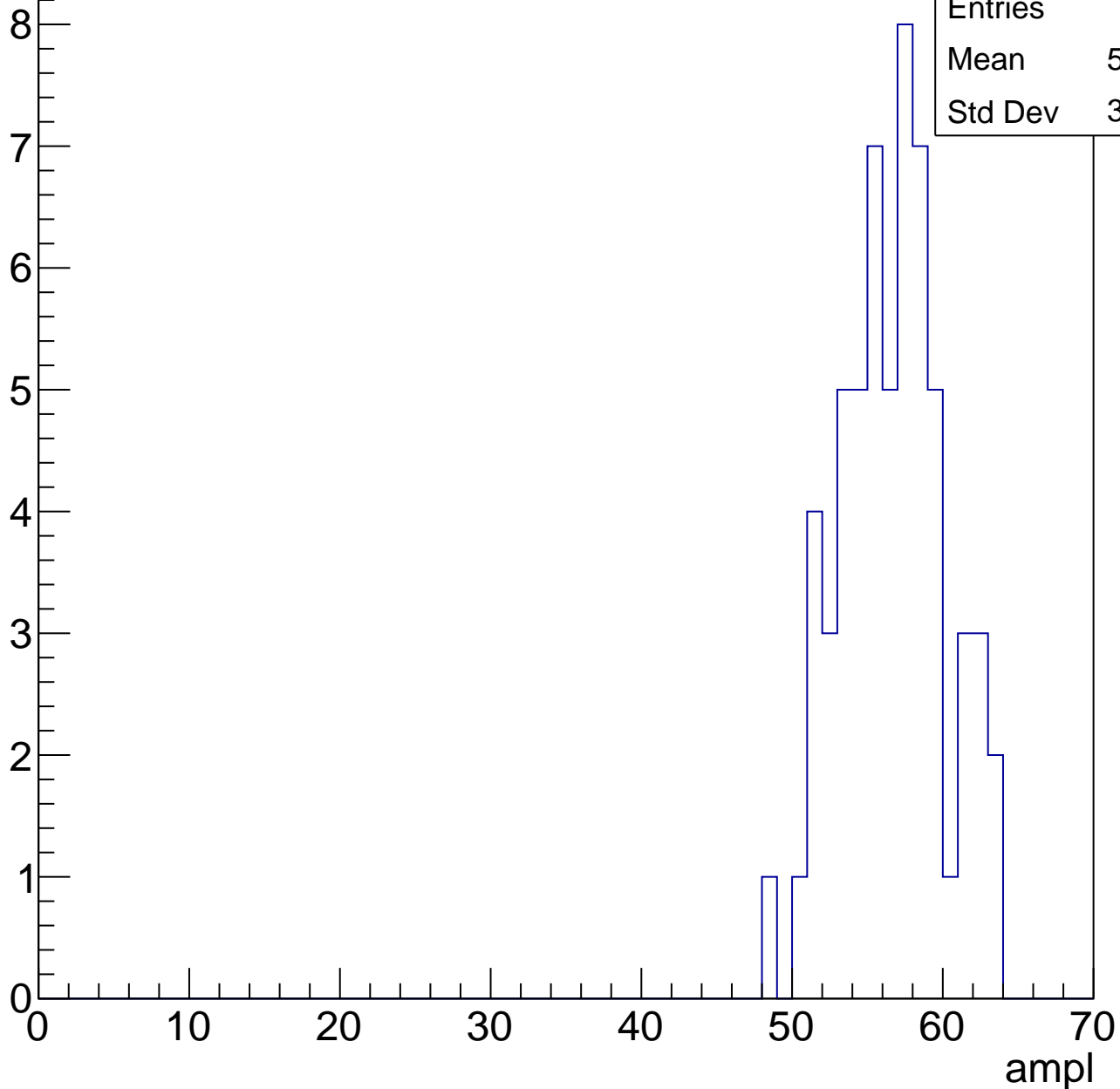
Entries	57
Mean	49.46
Std Dev	2.841



B0L002S, U17-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

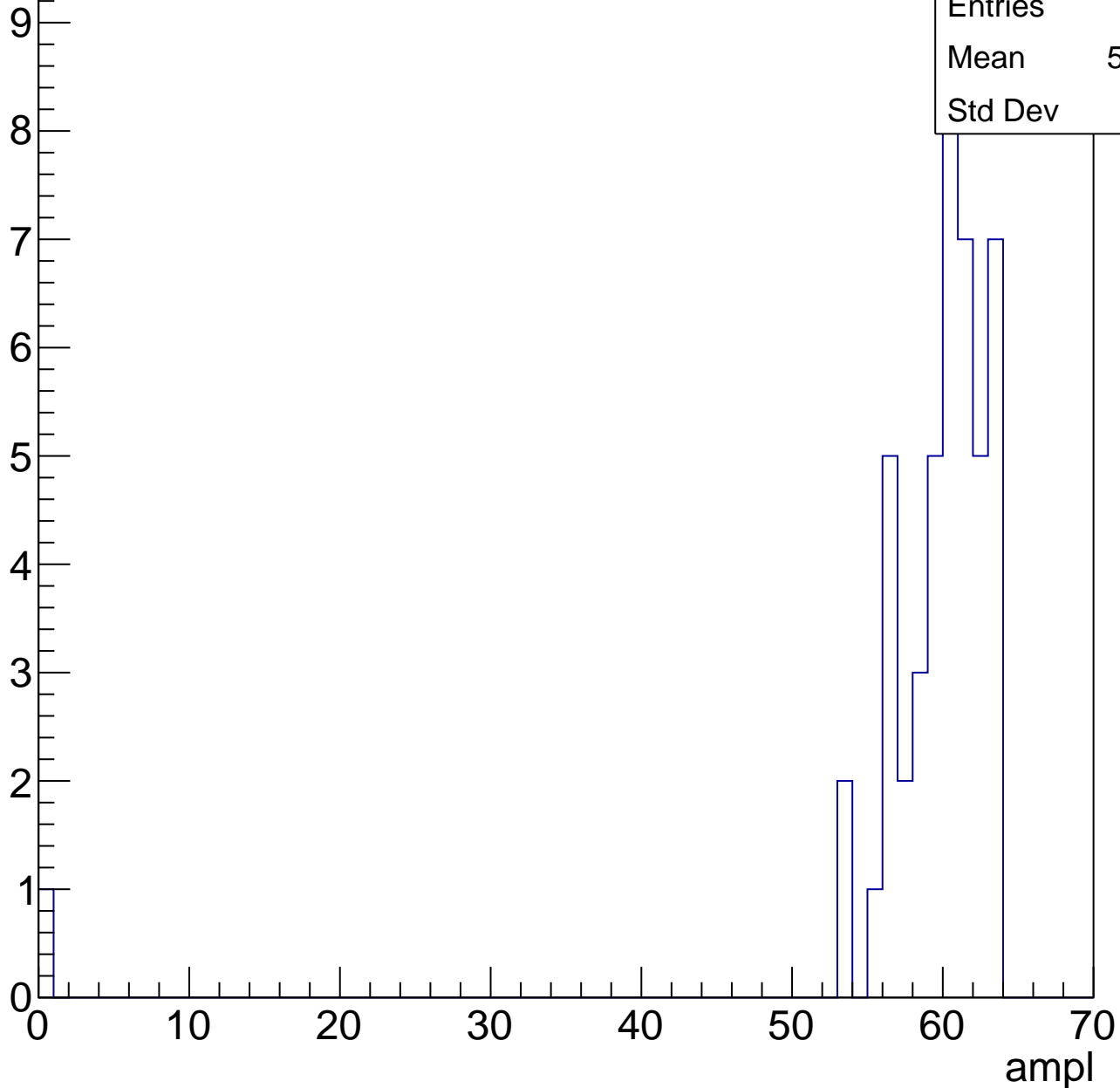
Entry



B0L002S, U17-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch86, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	30.24
Std Dev	3.594

Gaus mean : 30.9606

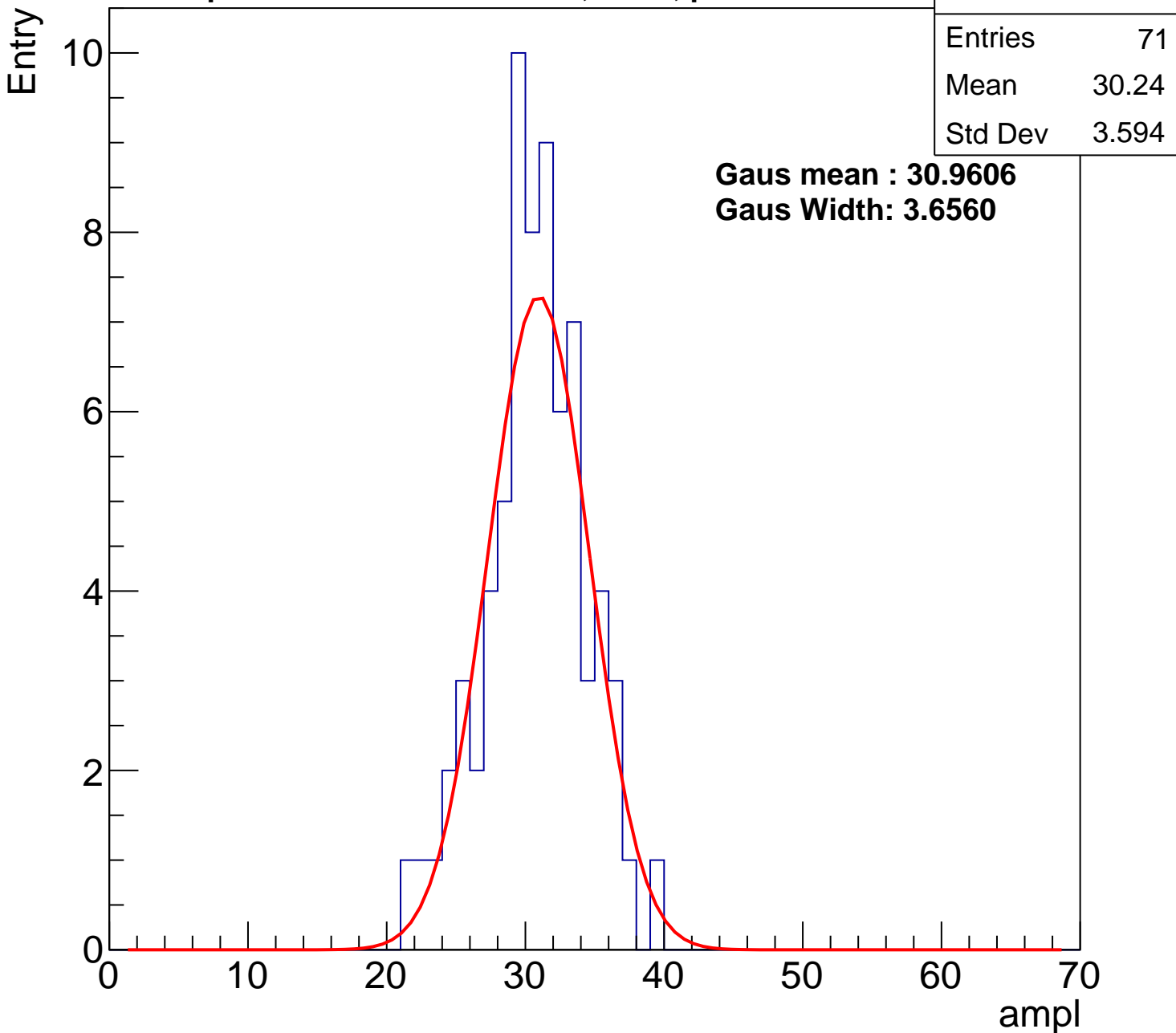
Gaus Width: 3.6560

Entry

10
8
6
4
2
0

ampl

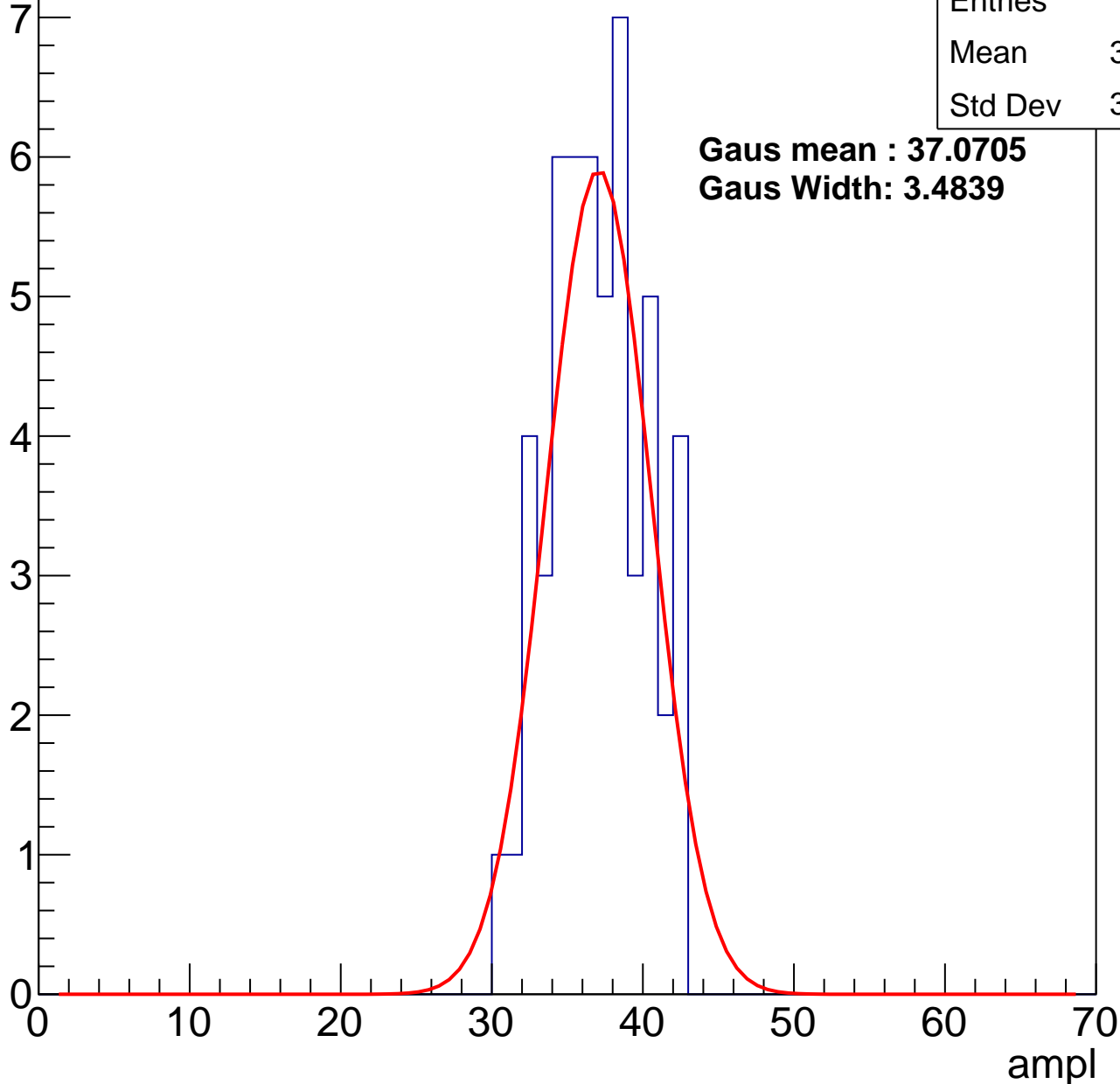
0 10 20 30 40 50 60 70



B0L002S, U17-ch86, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch86, adc2

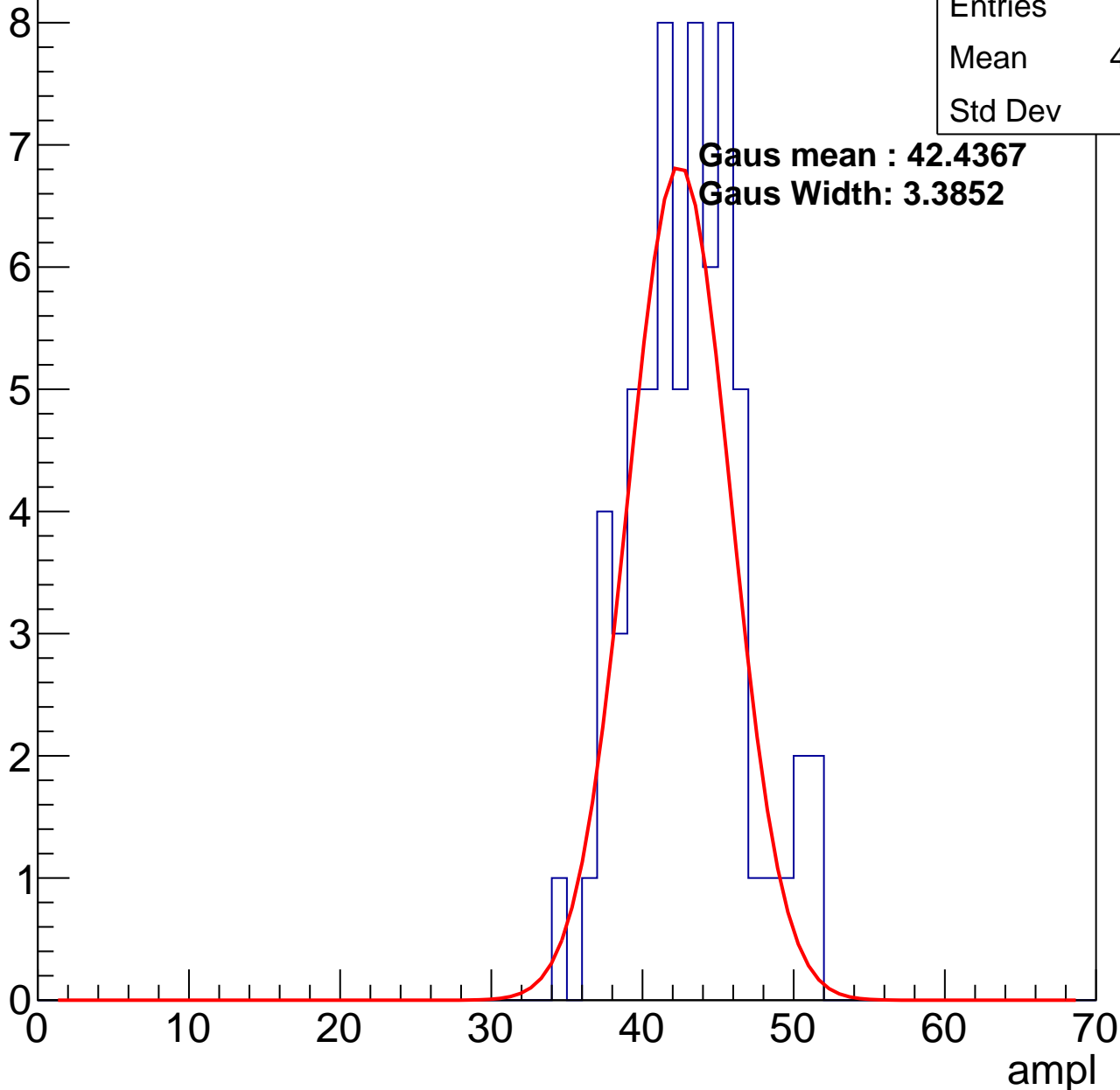
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.56
Std Dev	3.66

Gaus mean : 42.4367

Gaus Width: 3.3852

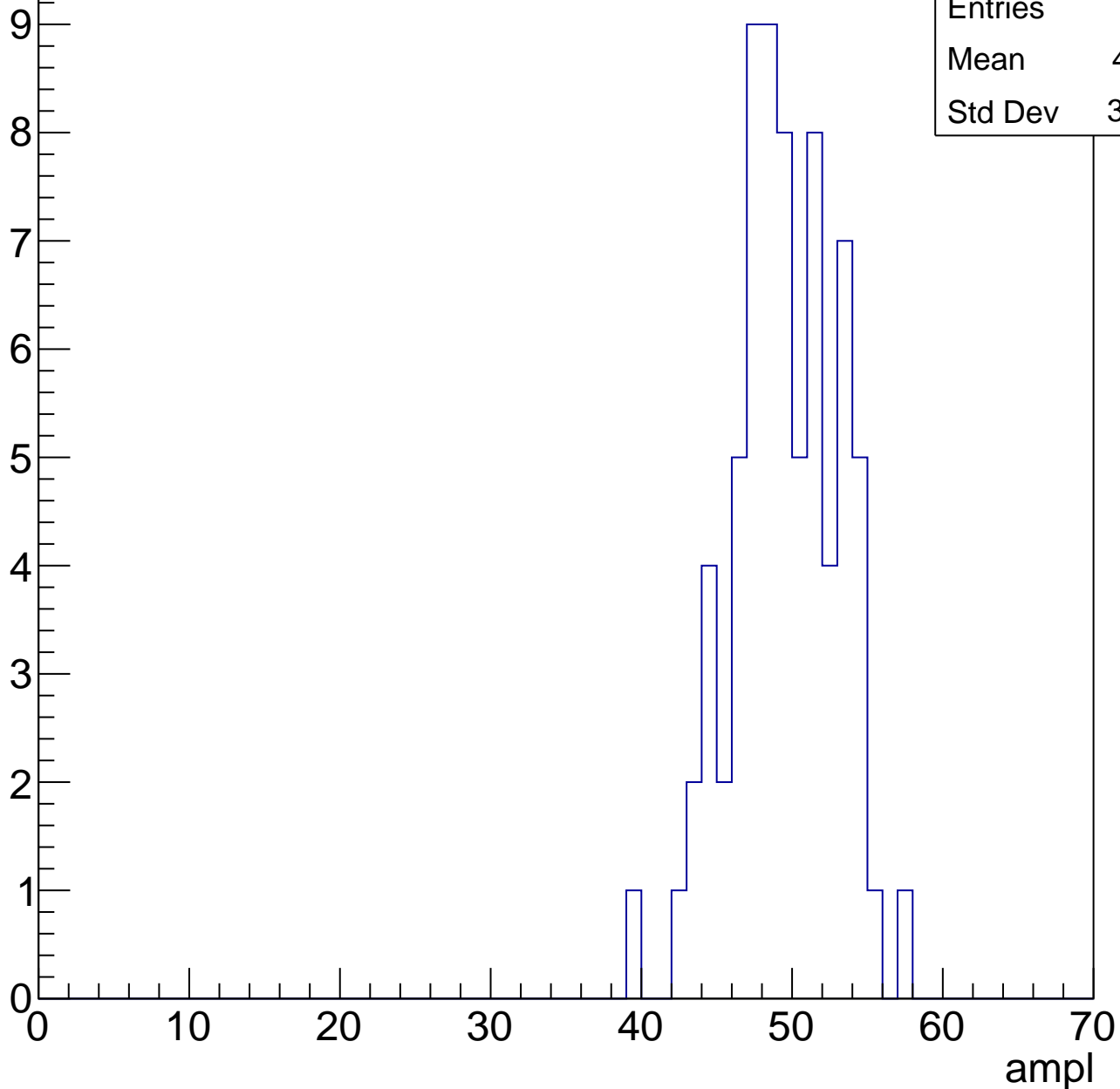


B0L002S, U17-ch86, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	49.01
Std Dev	3.442

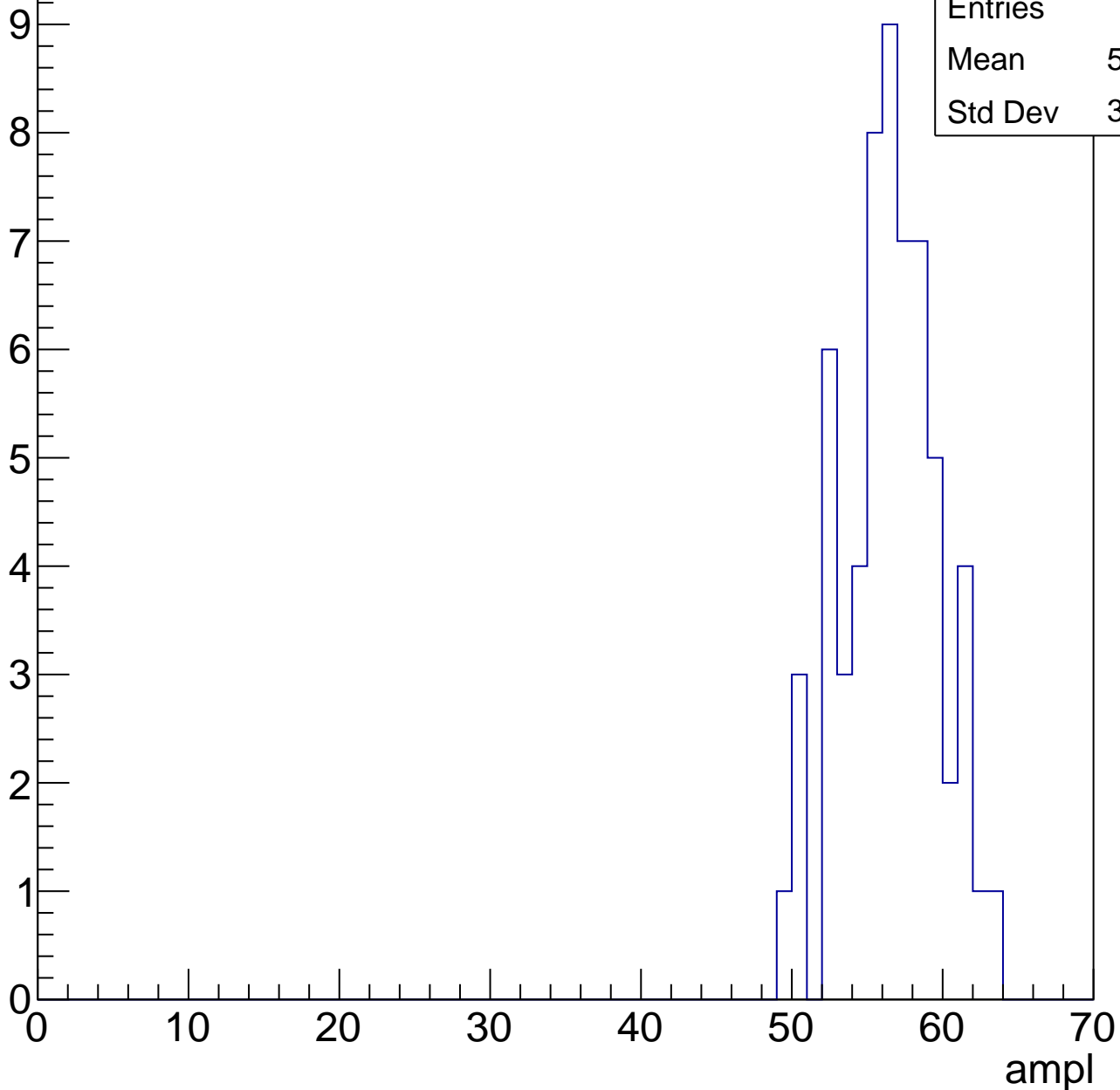


B0L002S, U17-ch86, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.05
Std Dev	3.138



B0L002S, U17-ch86, adc5

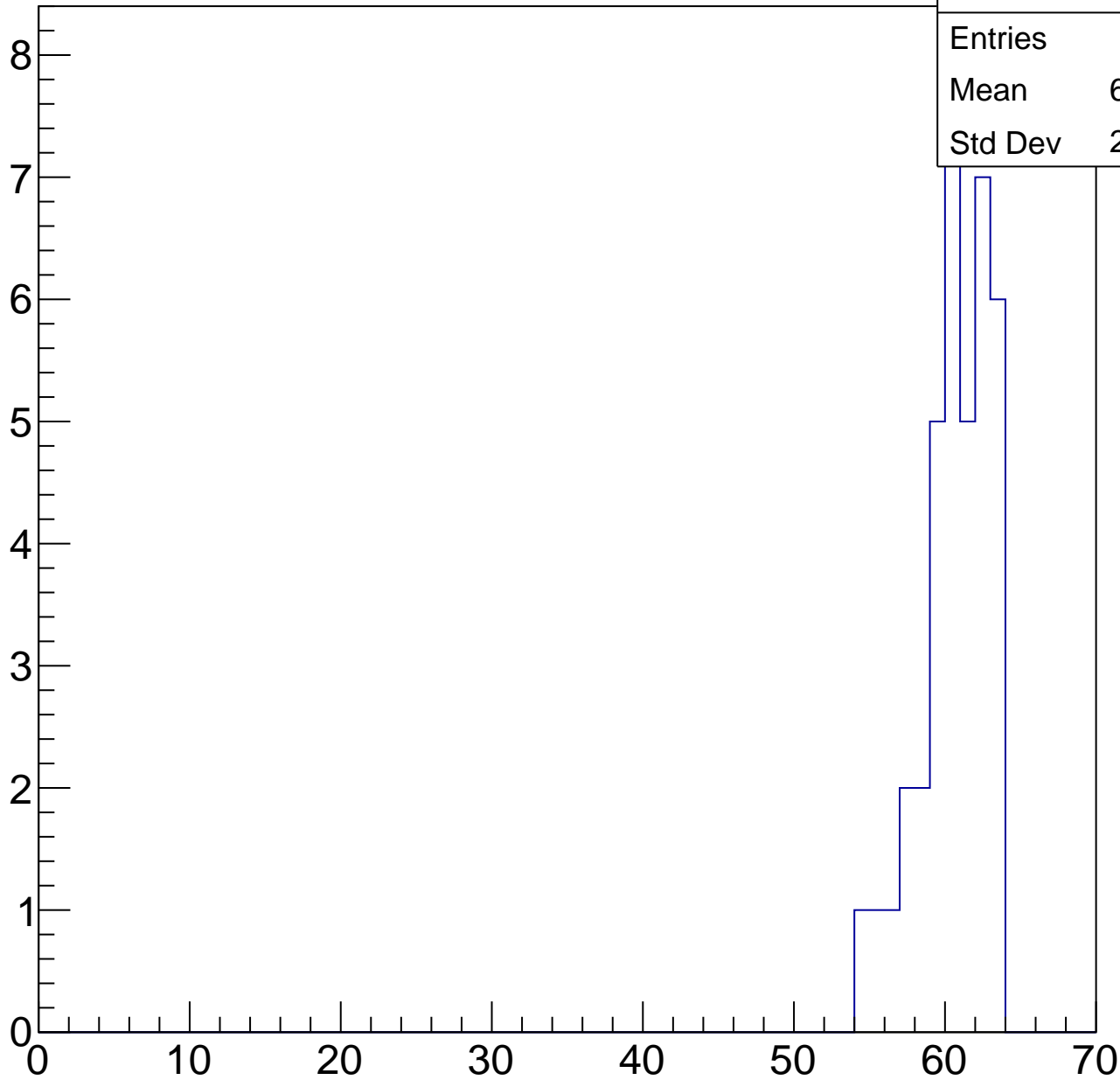
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.18
Std Dev	2.258

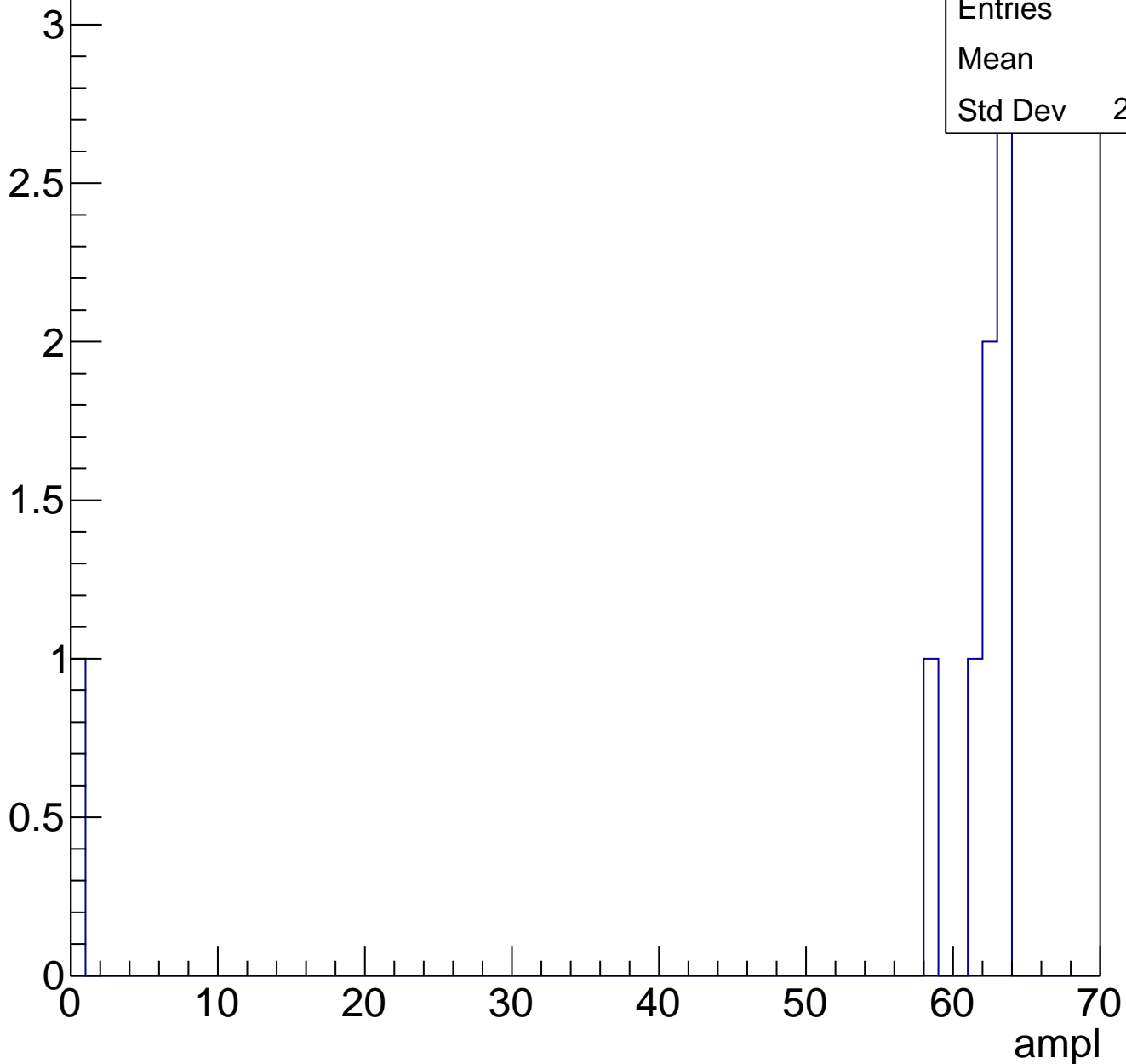
ampl



B0L002S, U17-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch87, adc0

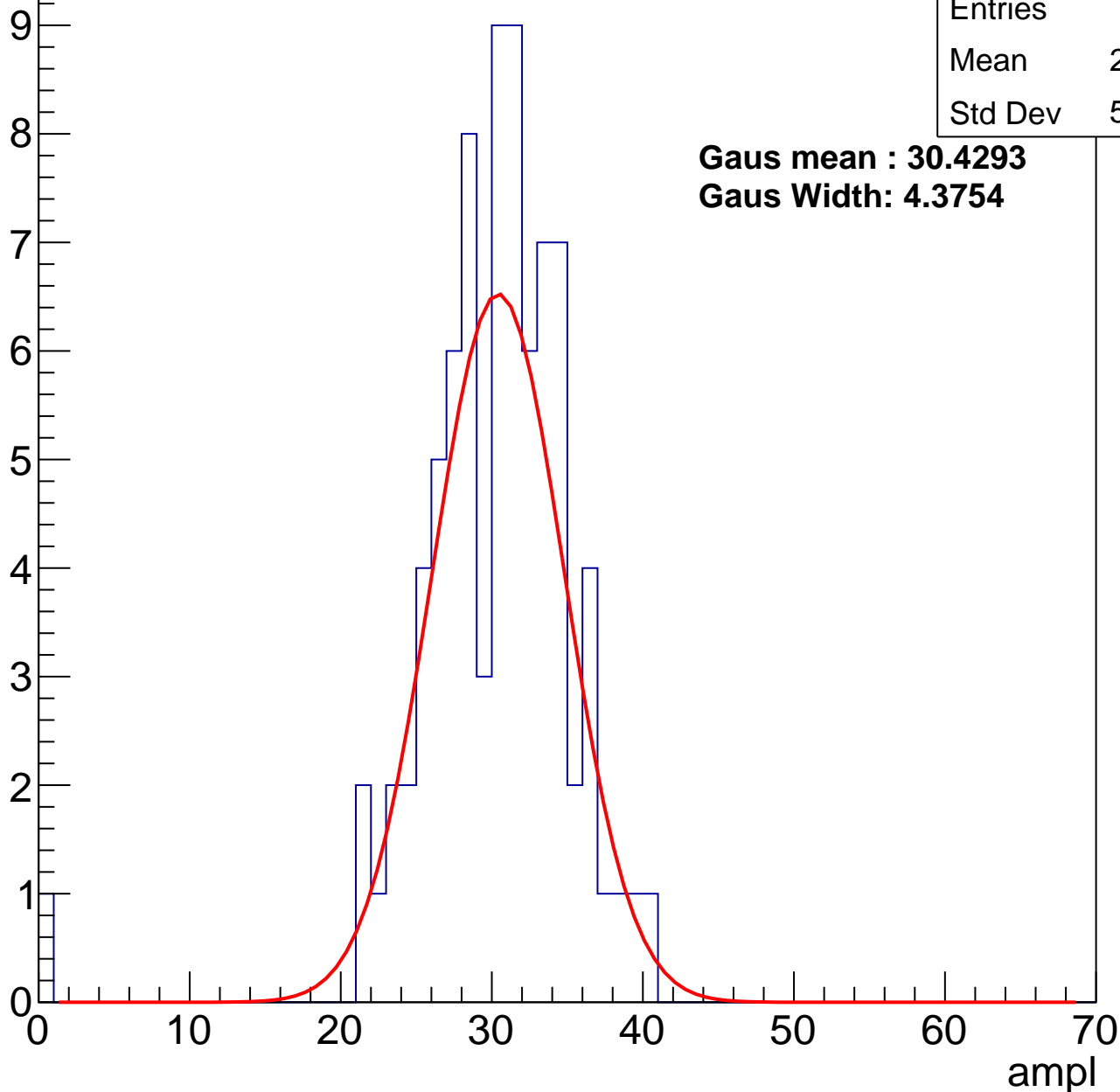
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	29.74
Std Dev	5.245

Gaus mean : 30.4293

Gaus Width: 4.3754



B0L002S, U17-ch87, adc1

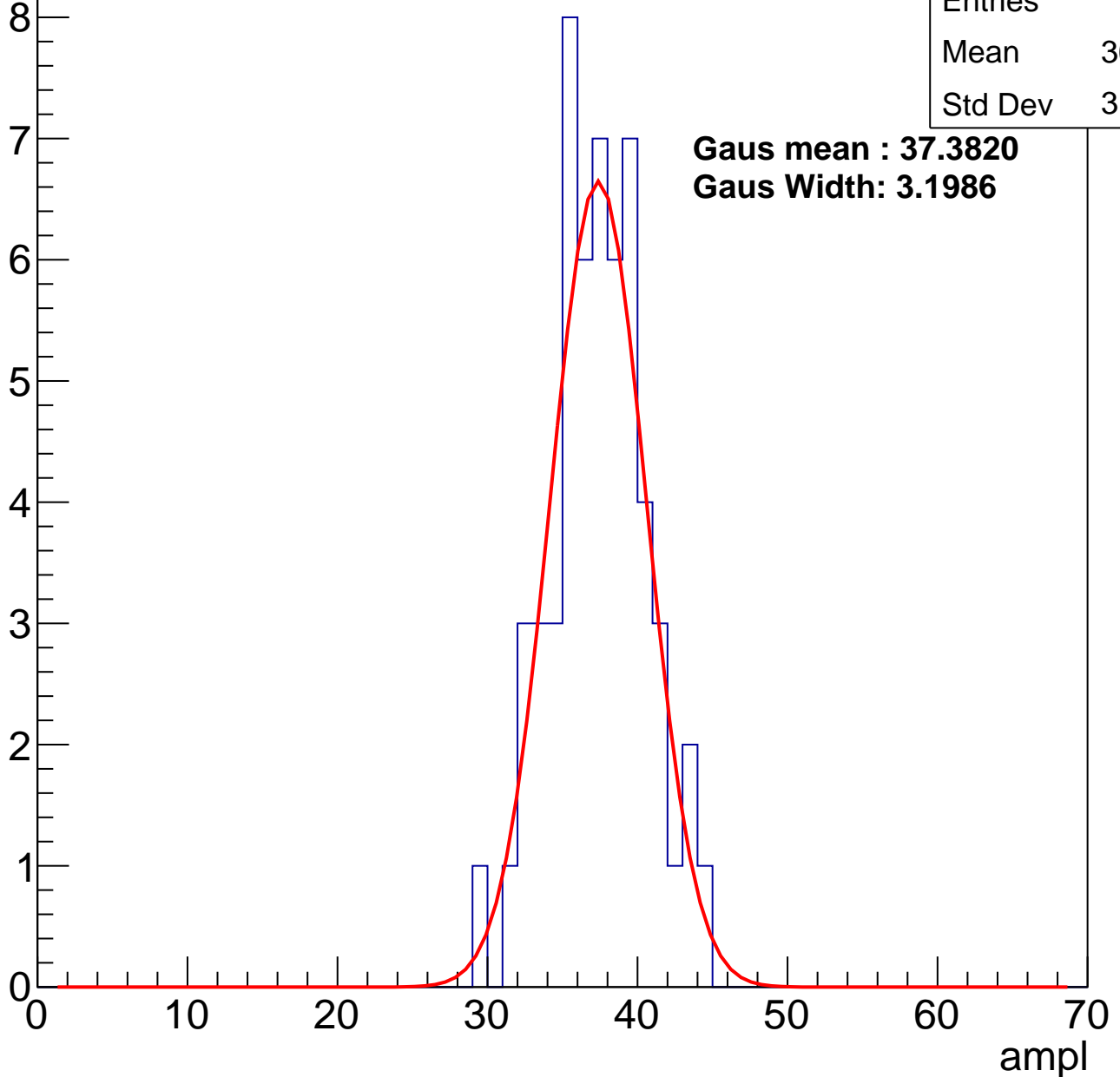
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.93
Std Dev	3.139

Gaus mean : 37.3820

Gaus Width: 3.1986



B0L002S, U17-ch87, adc2

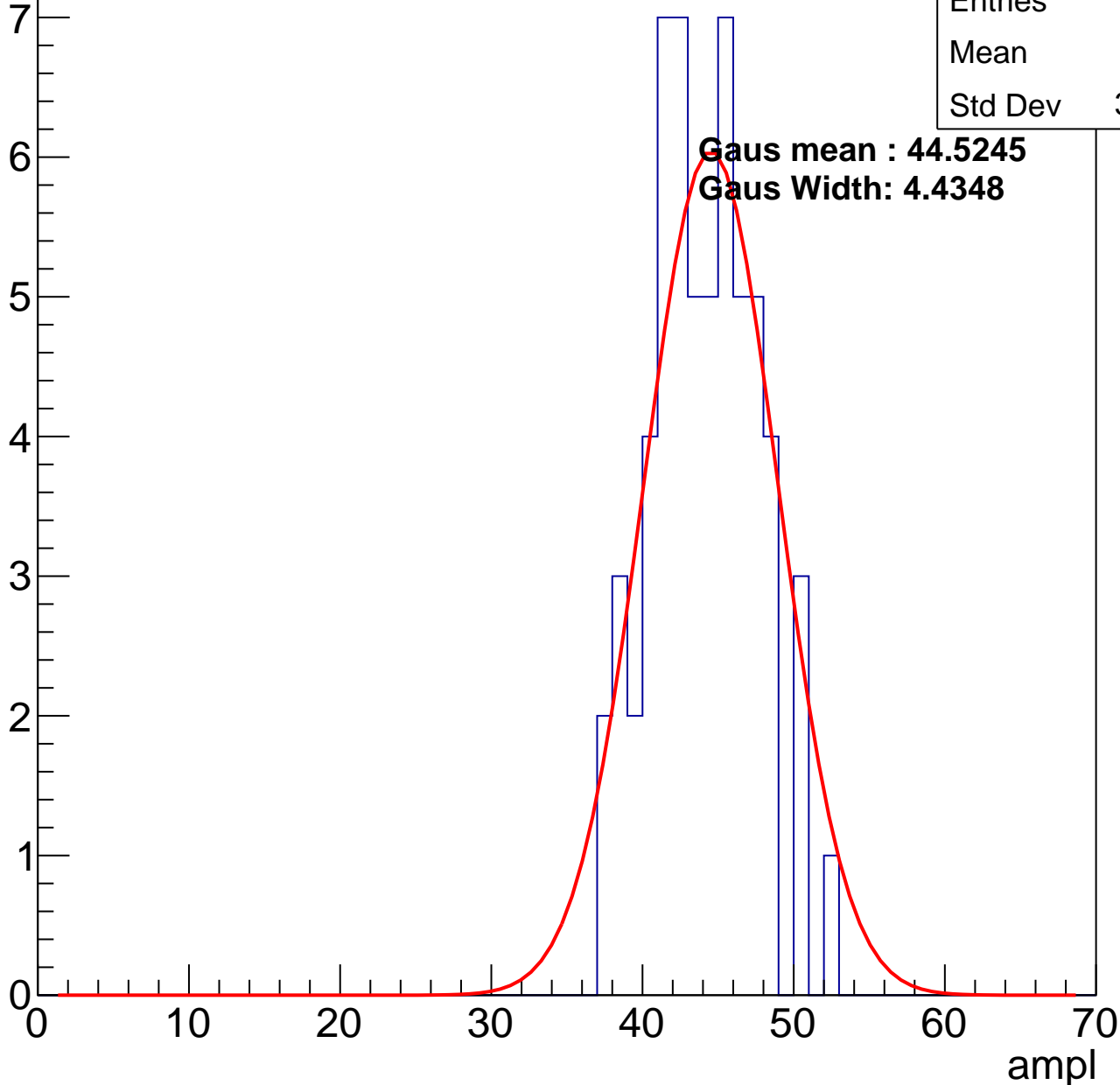
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.6
Std Dev	3.451

Gaus mean : 44.5245

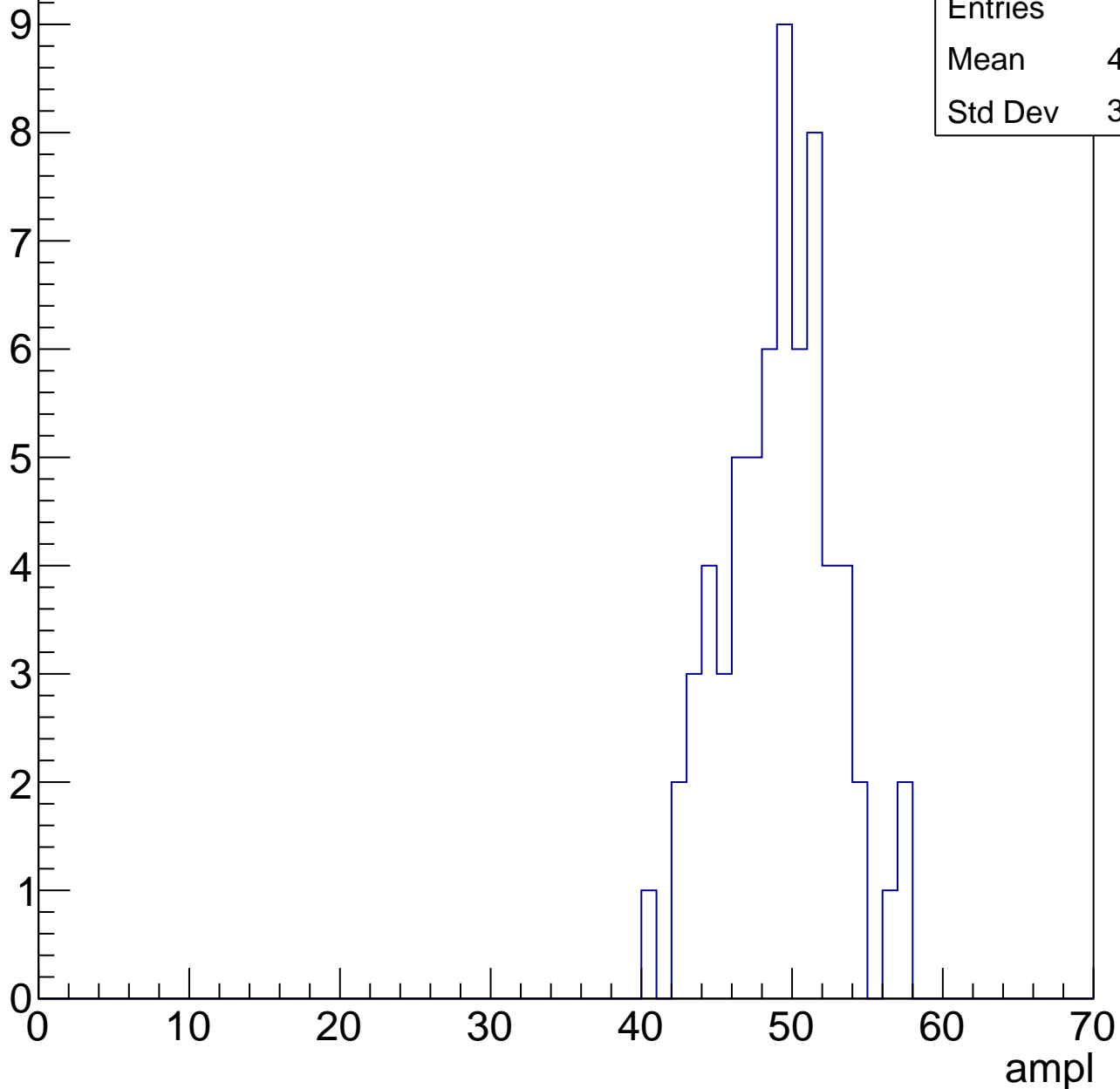
Gaus Width: 4.4348



B0L002S, U17-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

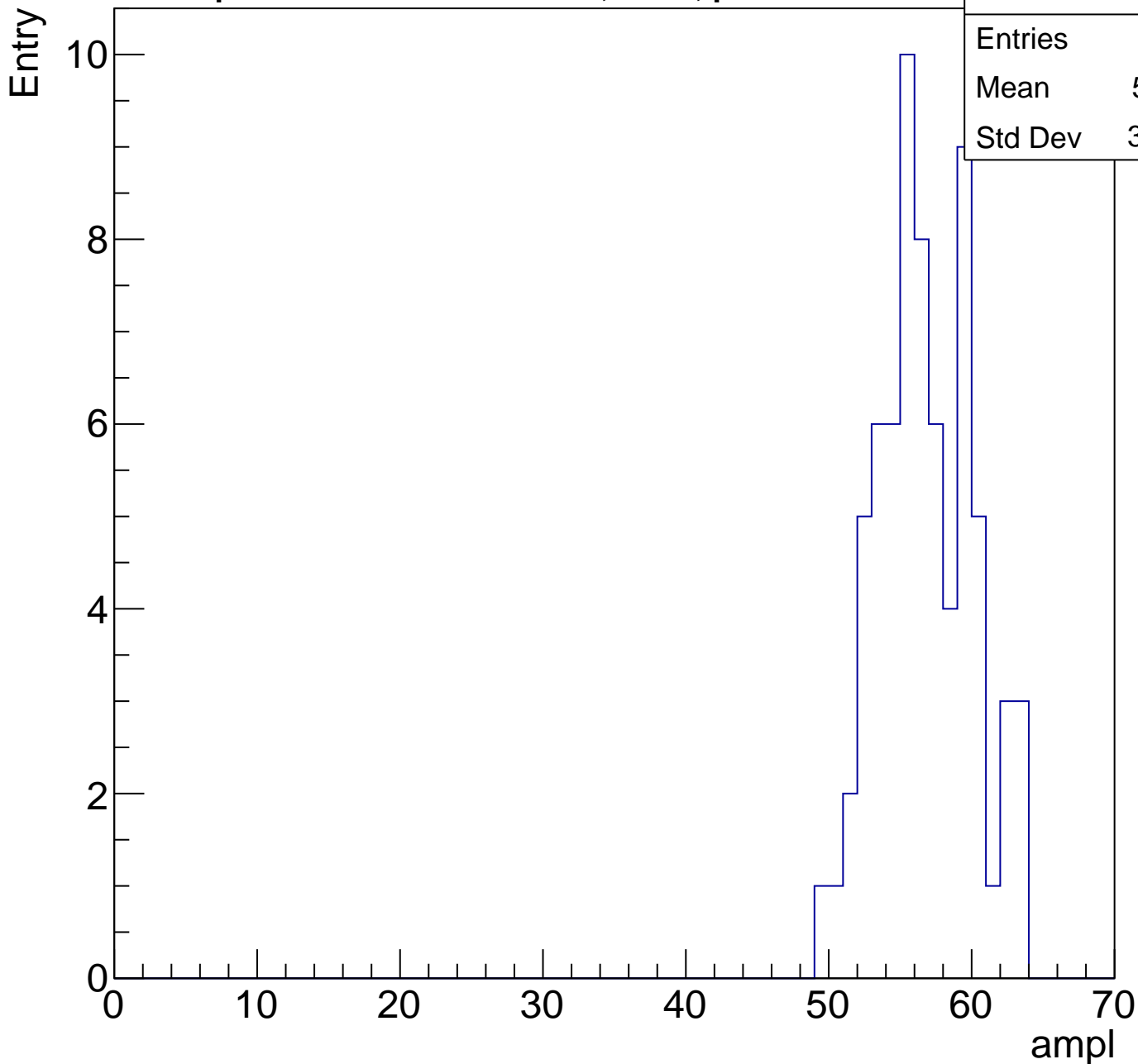
Entry



B0L002S, U17-ch87, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	56.31
Std Dev	3.293

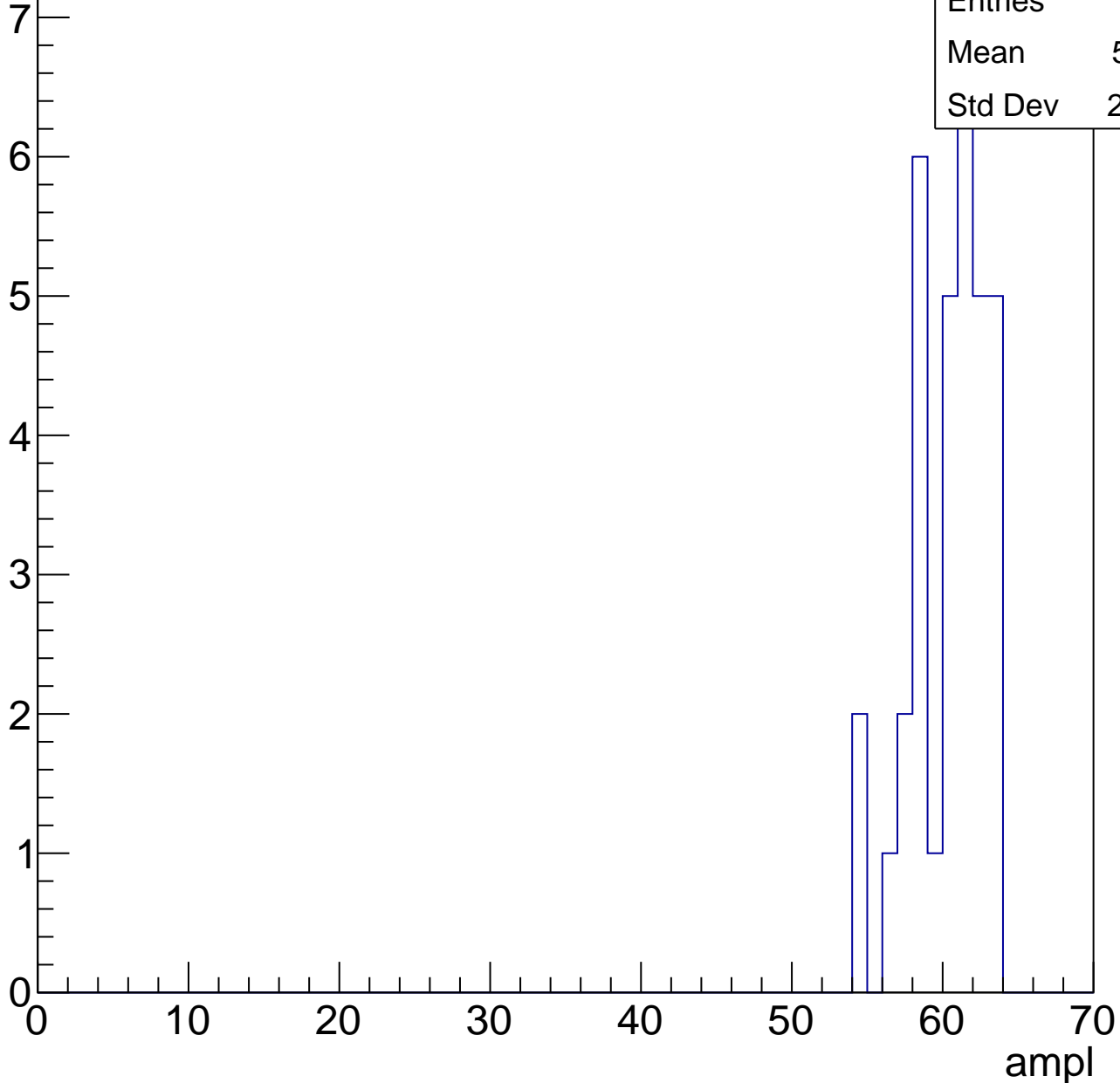


B0L002S, U17-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

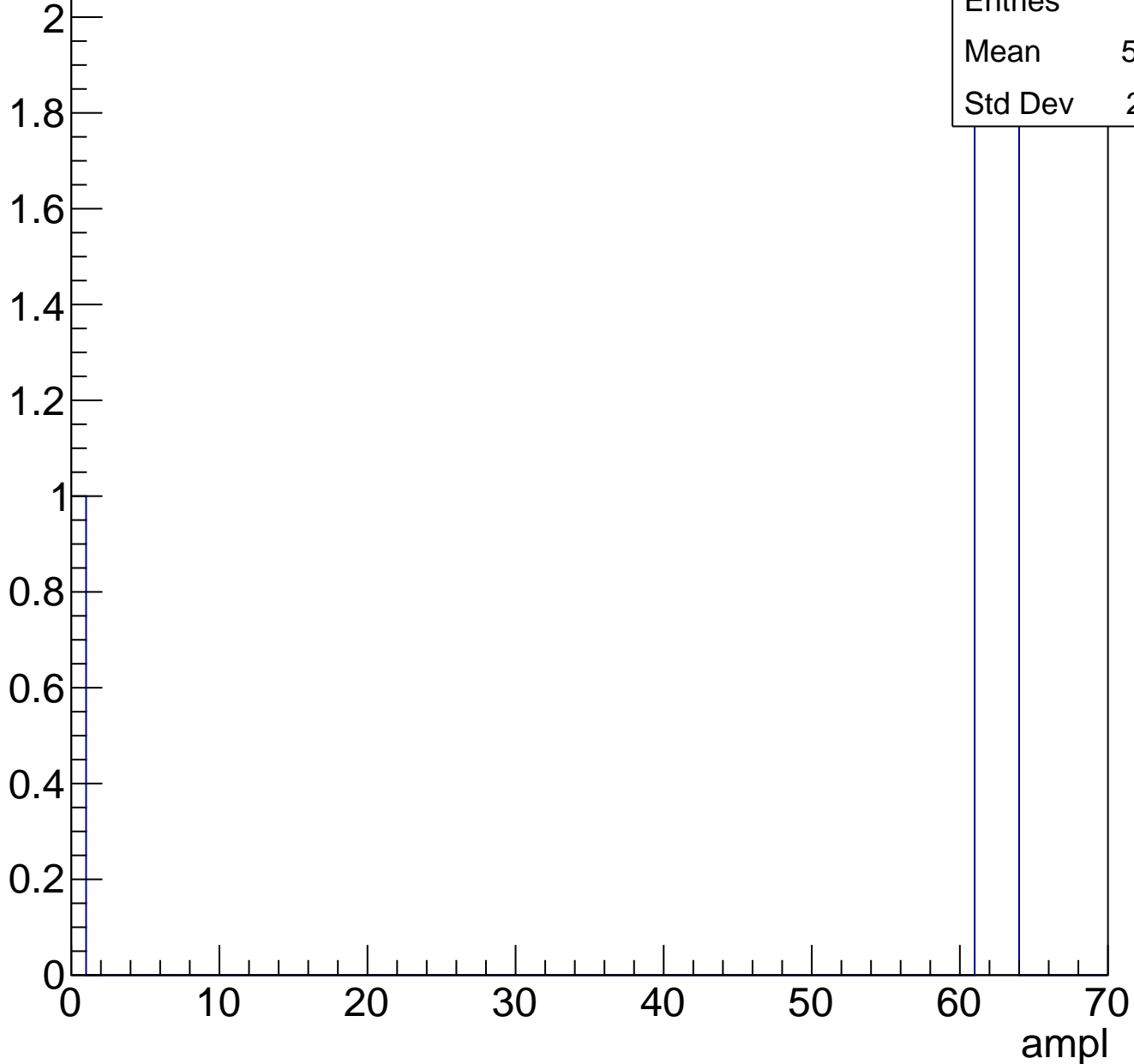
Entries	34
Mean	59.91
Std Dev	2.442



B0L002S, U17-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch88, adc0

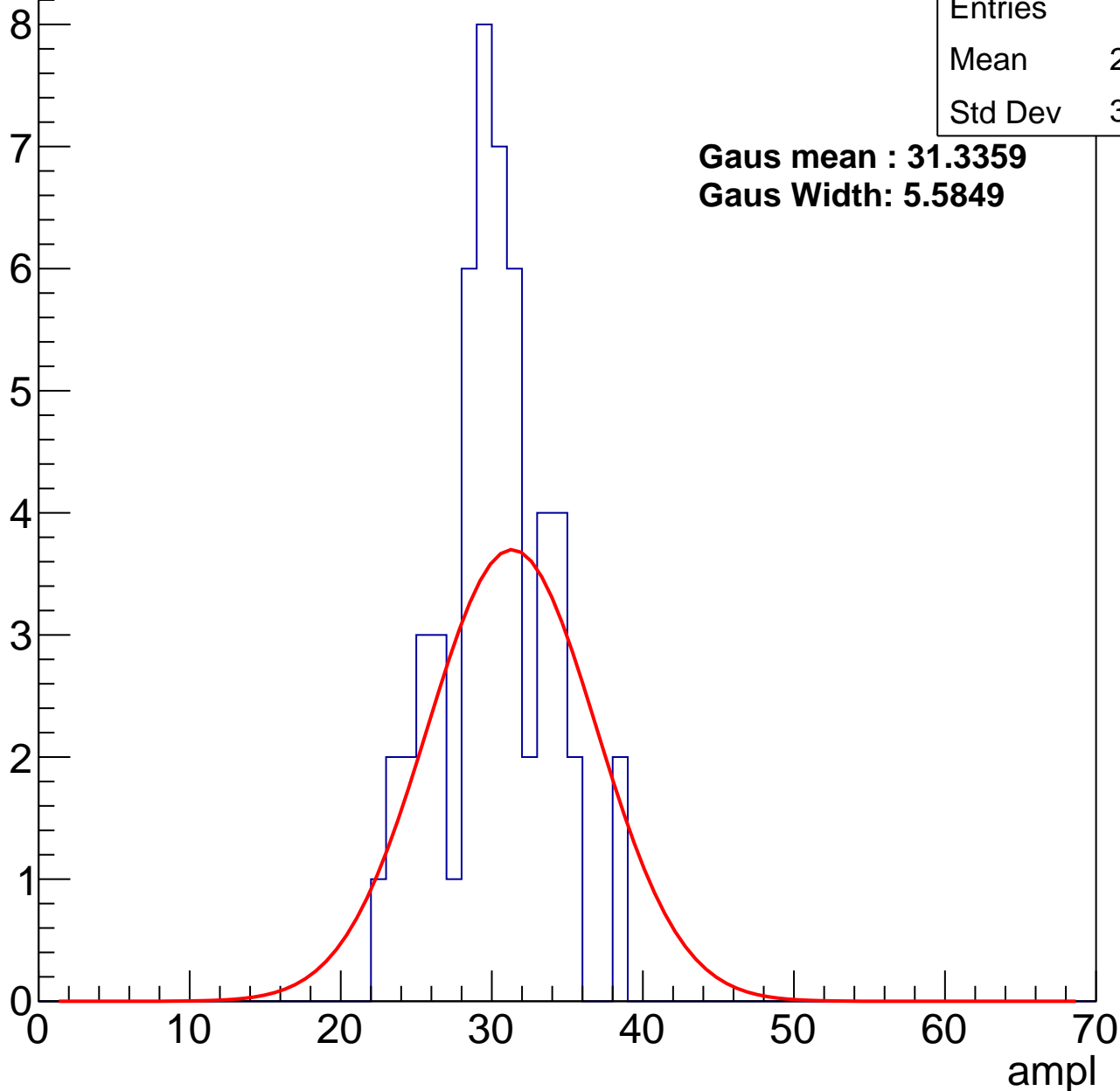
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	29.62
Std Dev	3.588

Gaus mean : 31.3359

Gaus Width: 5.5849



B0L002S, U17-ch88, adc1

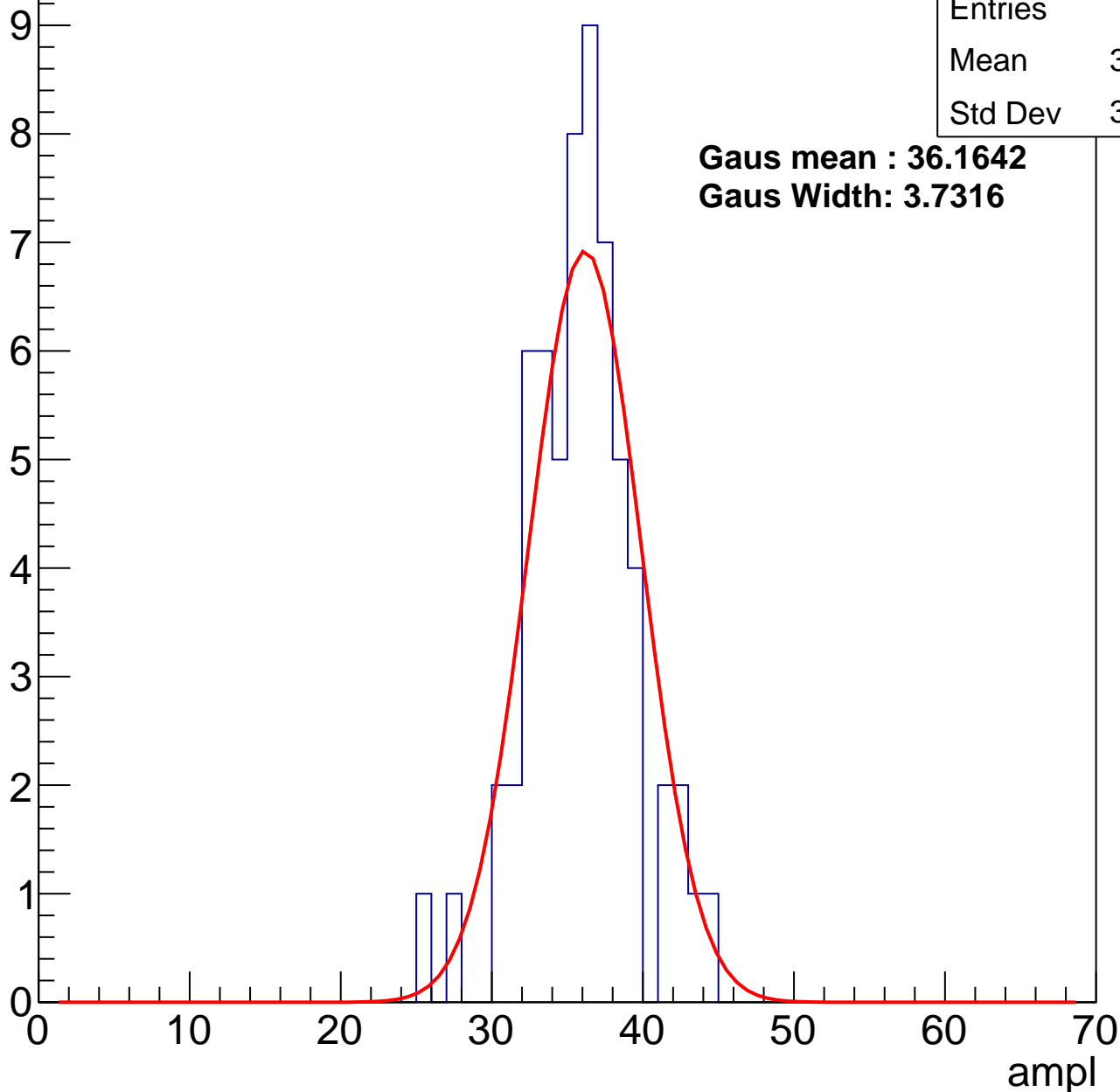
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	35.42
Std Dev	3.545

Gaus mean : 36.1642

Gaus Width: 3.7316



B0L002S, U17-ch88, adc2

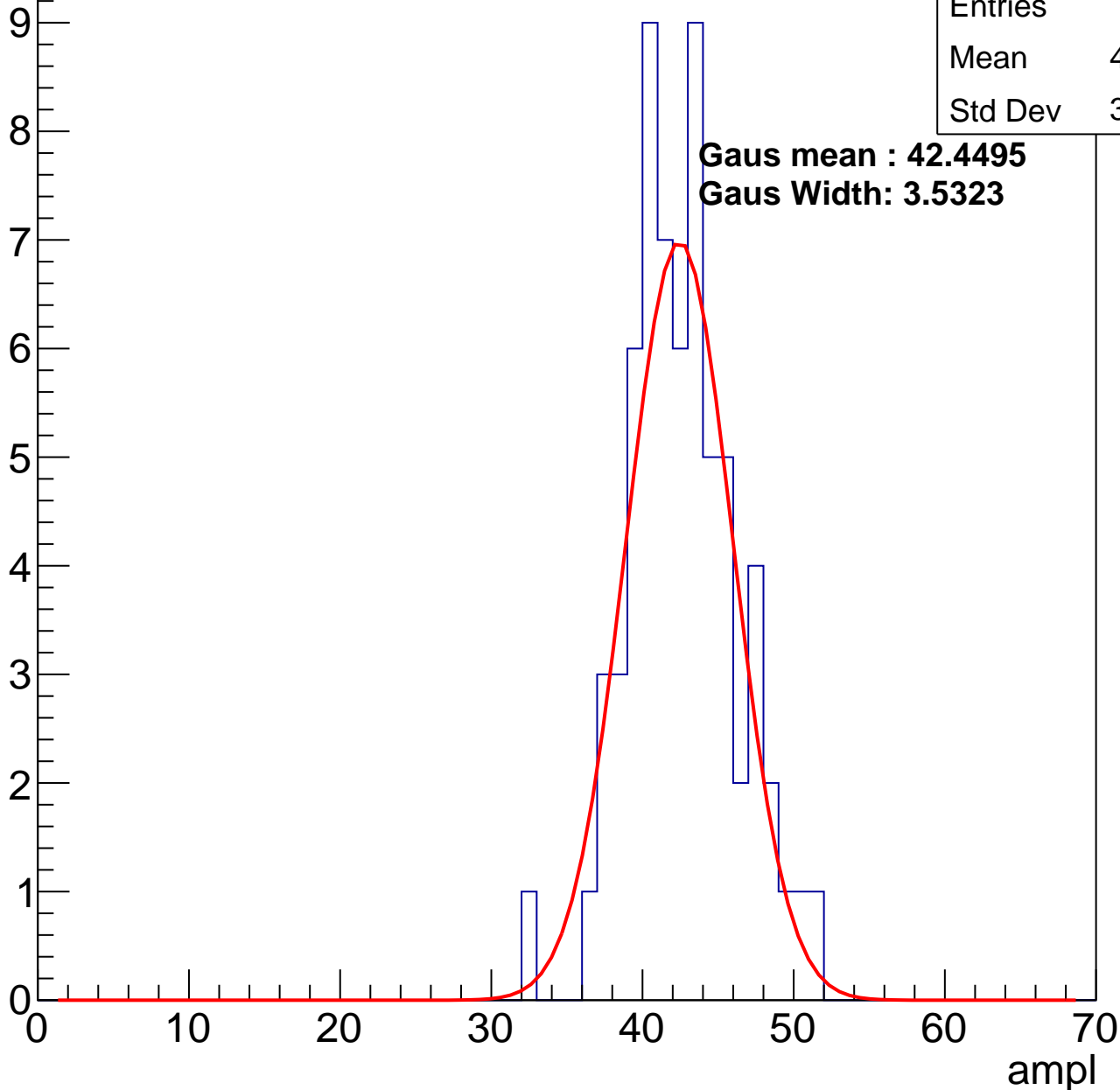
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.18
Std Dev	3.537

Gaus mean : 42.4495

Gaus Width: 3.5323

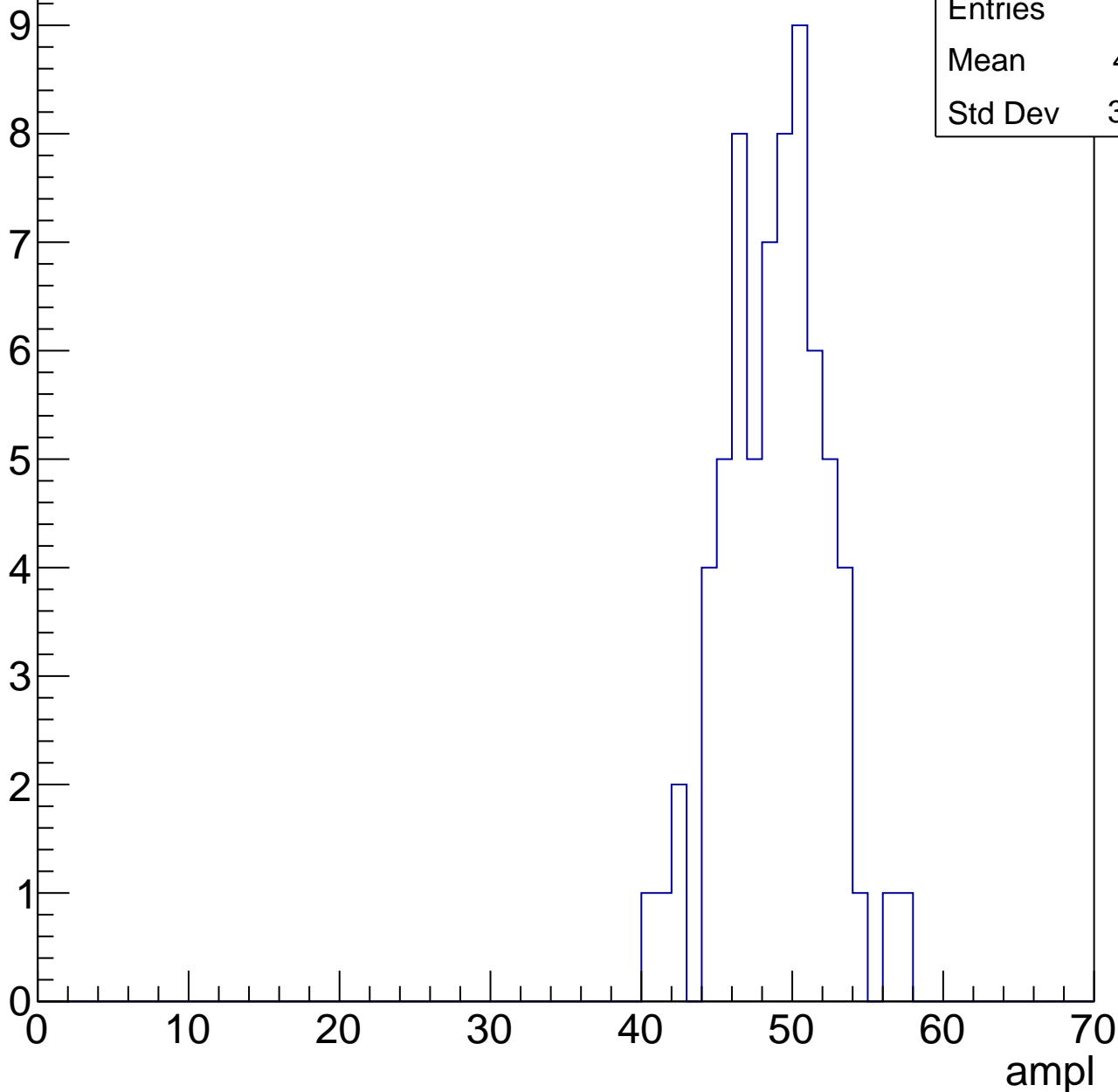


B0L002S, U17-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	48.41
Std Dev	3.384

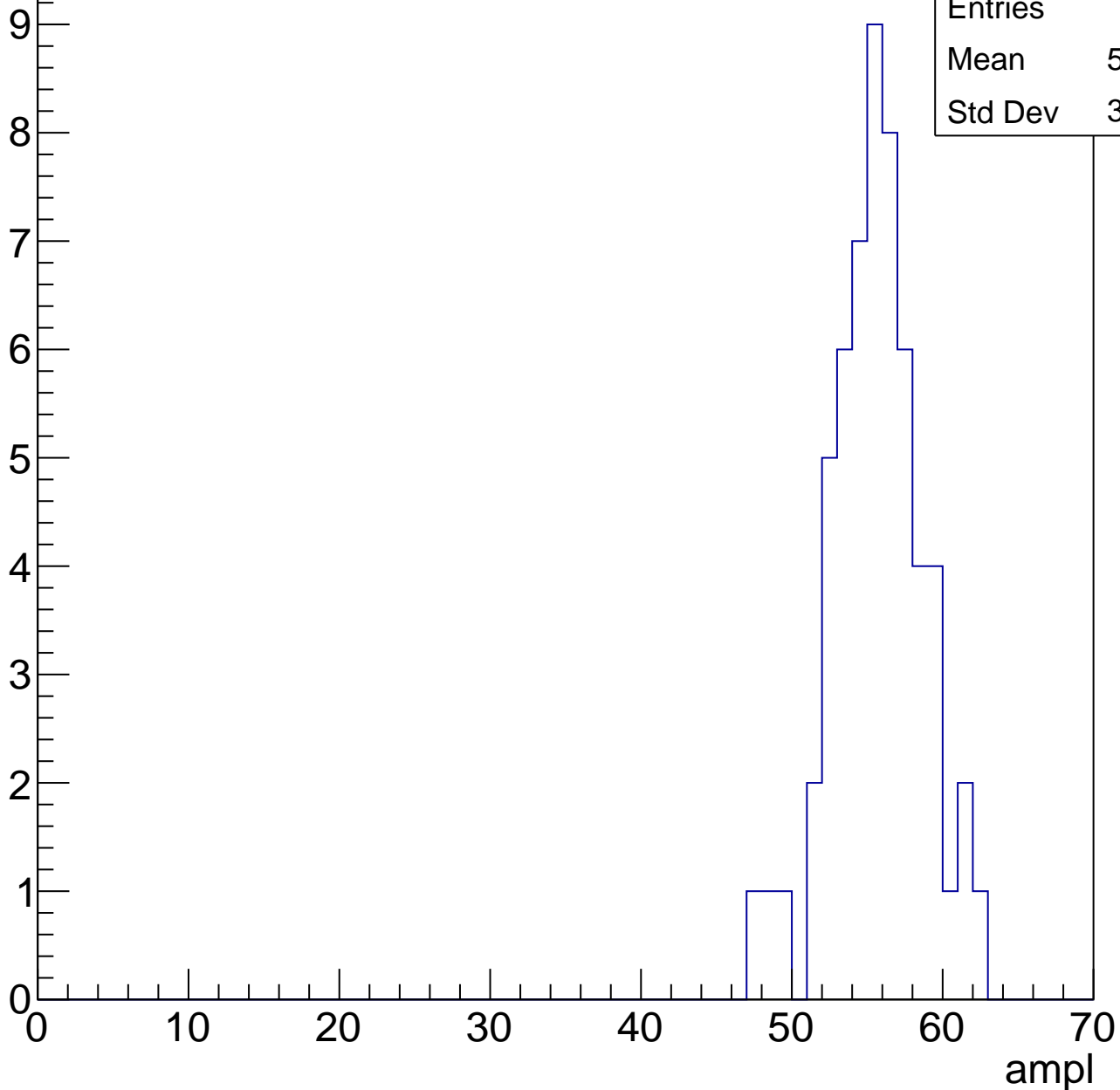


B0L002S, U17-ch88, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	55.16
Std Dev	3.033

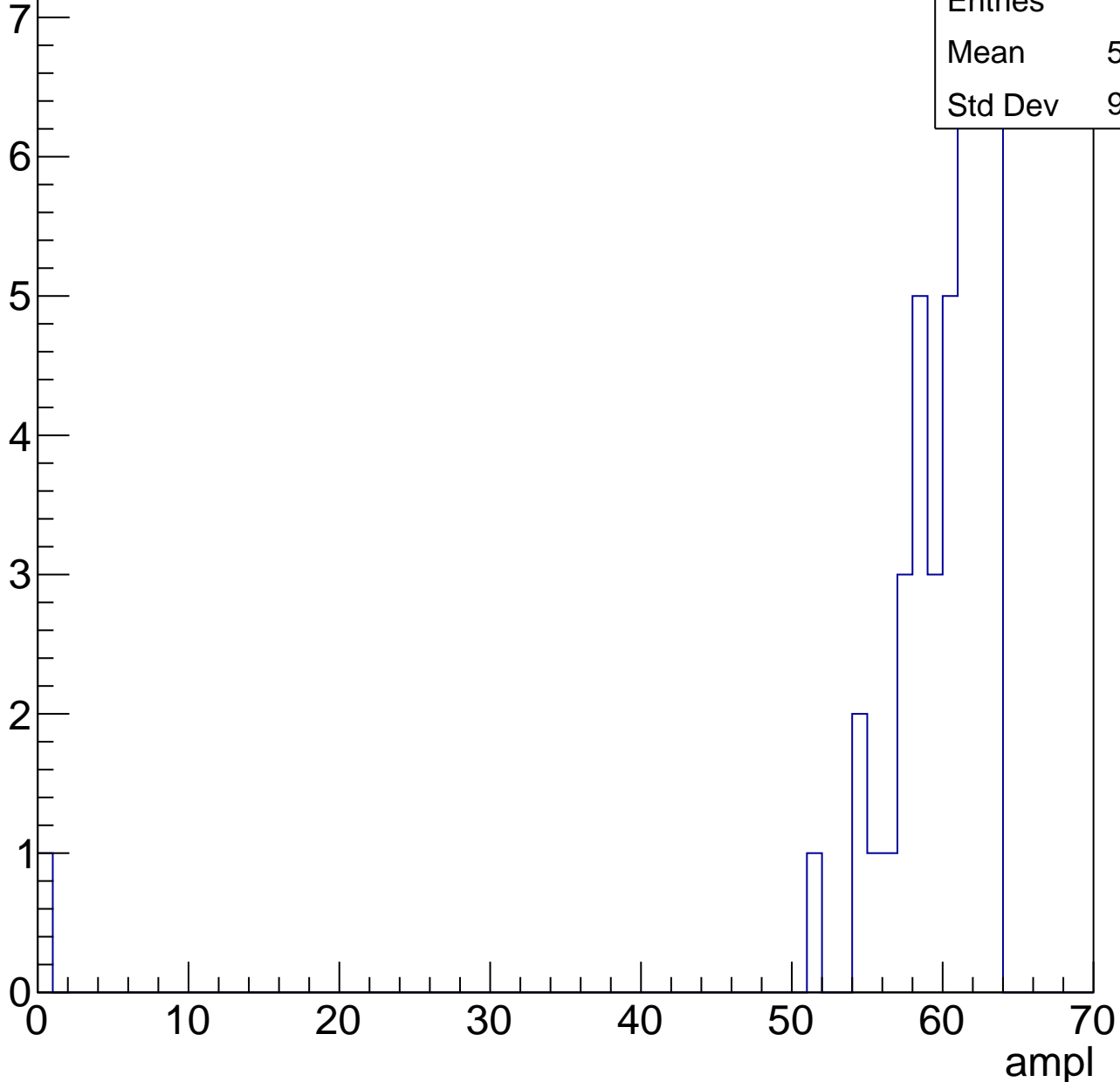


B0L002S, U17-ch88, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

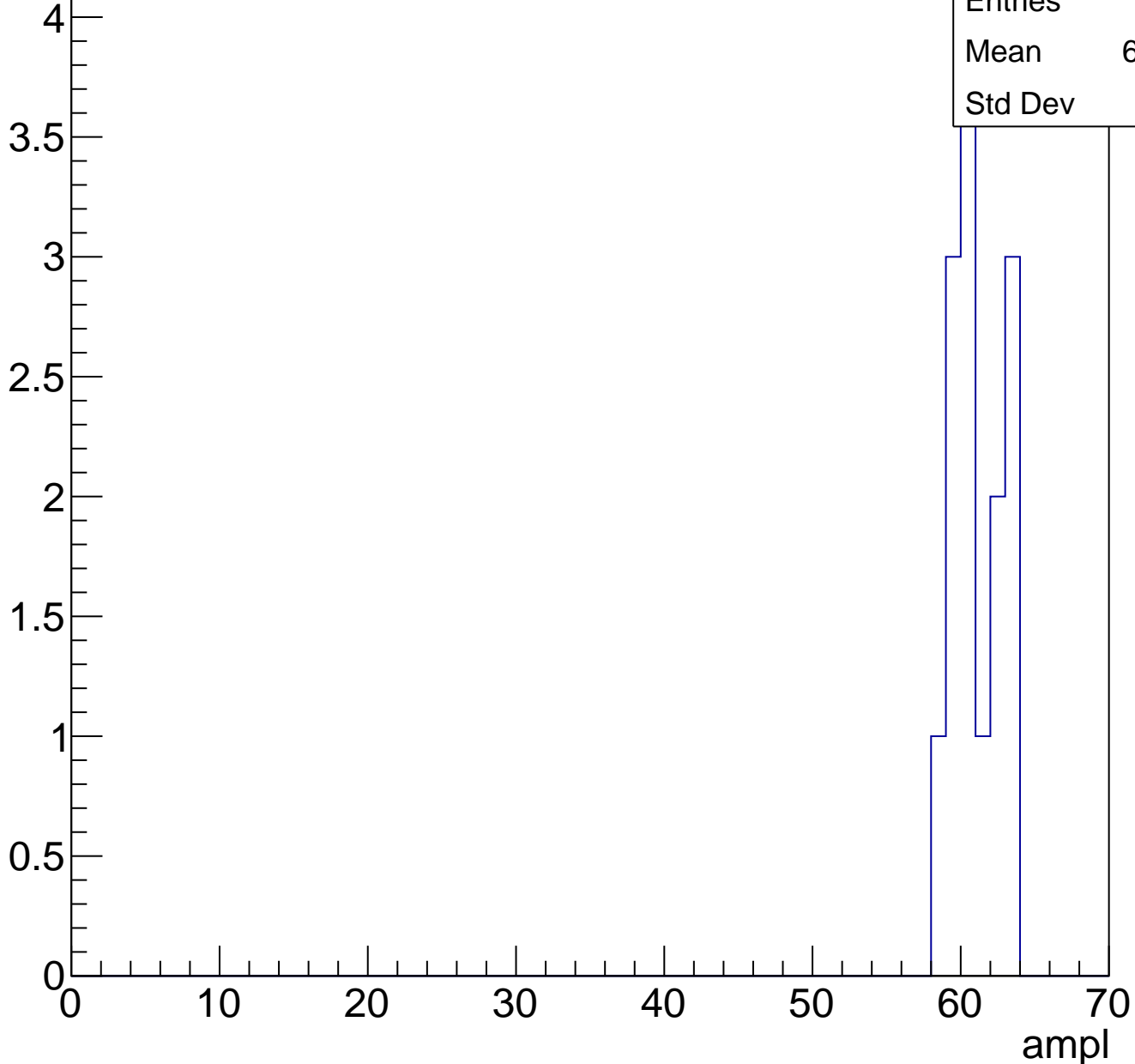
Entries	43
Mean	58.37
Std Dev	9.435



B0L002S, U17-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	14
Mean	60.64
Std Dev	1.63

B0L002S, U17-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch89, adc0

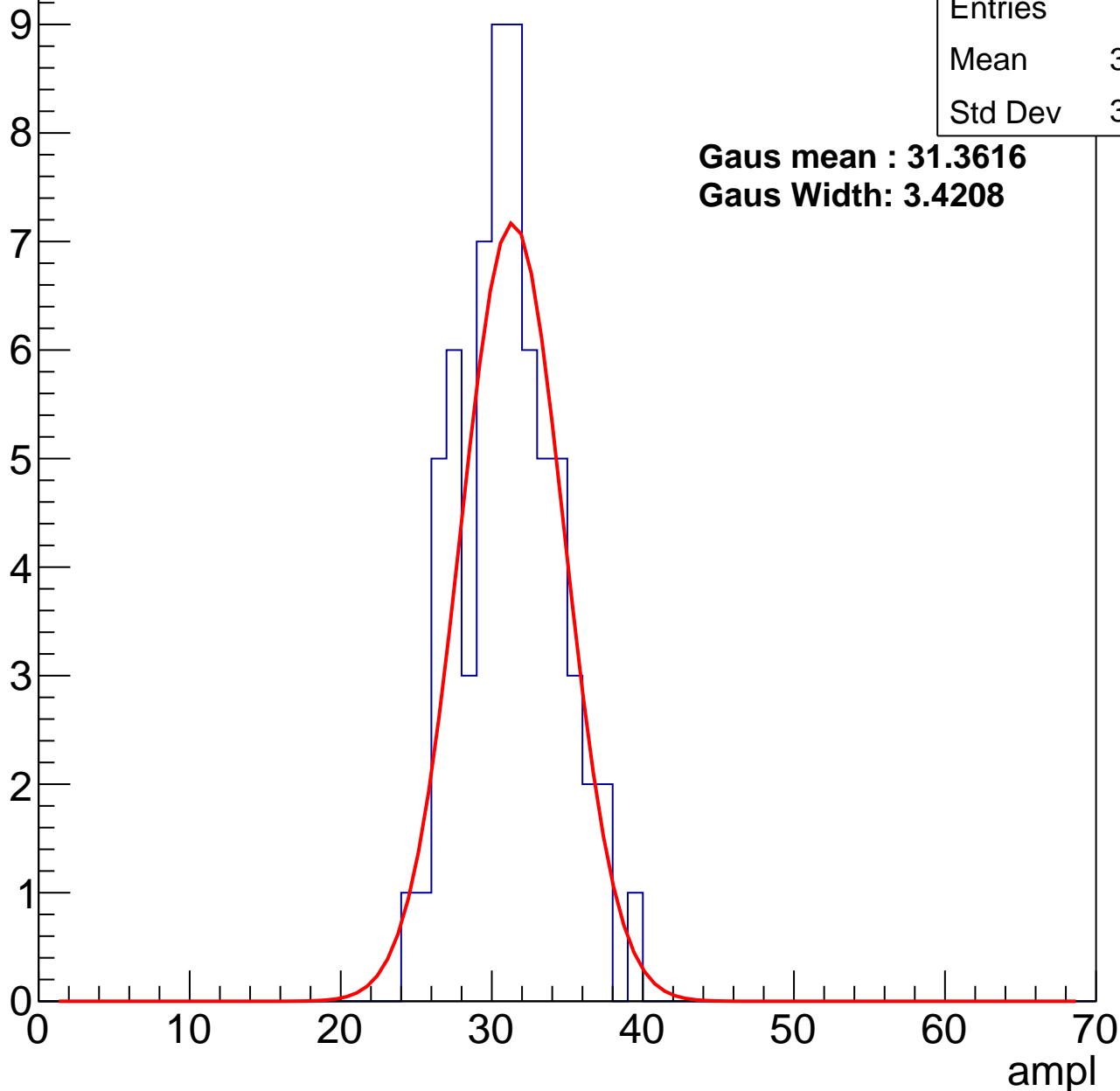
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.68
Std Dev	3.206

Gaus mean : 31.3616

Gaus Width: 3.4208



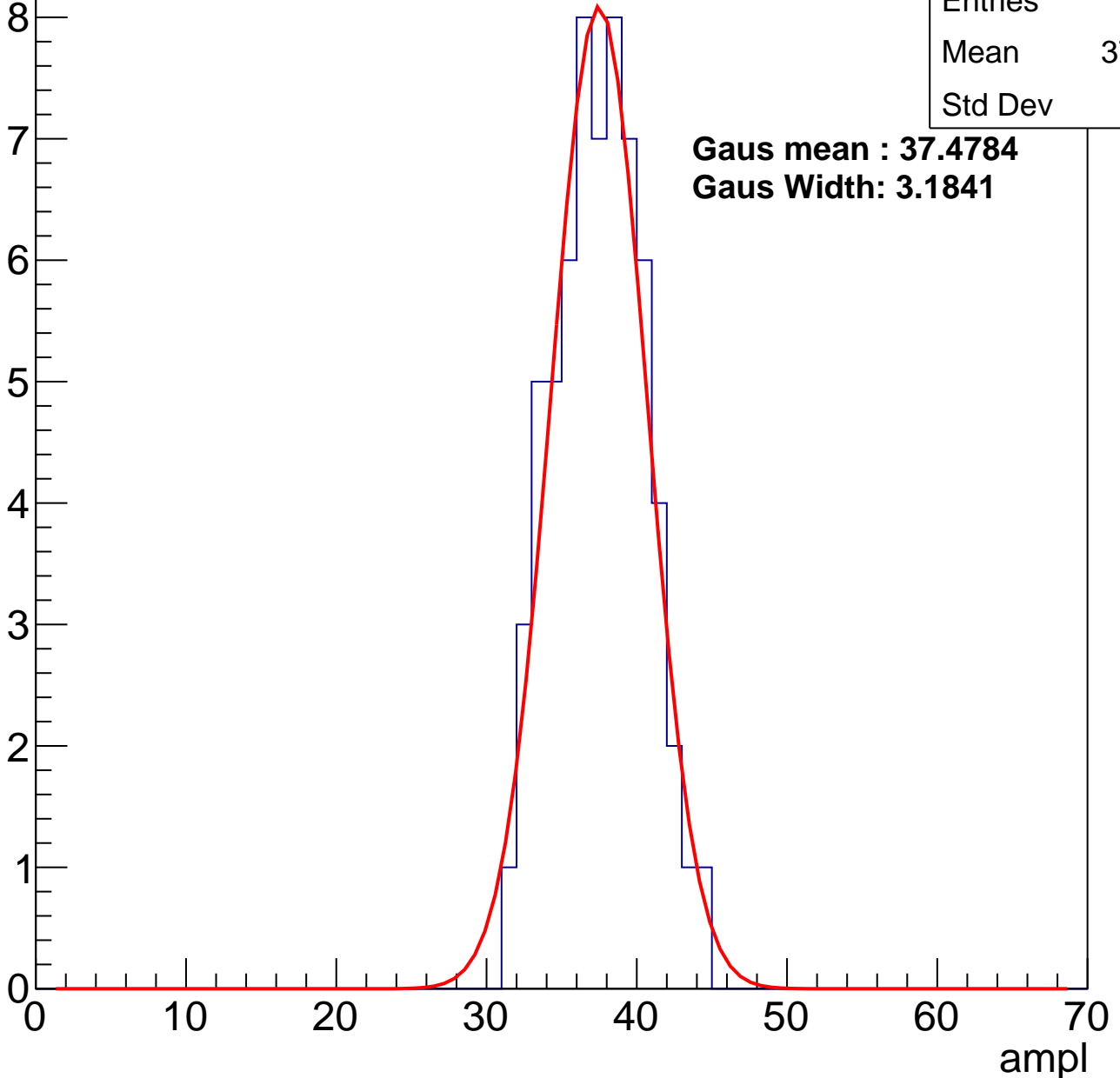
B0L002S, U17-ch89, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.05
Std Dev	2.95

Gaus mean : 37.4784
Gaus Width: 3.1841



B0L002S, U17-ch89, adc2

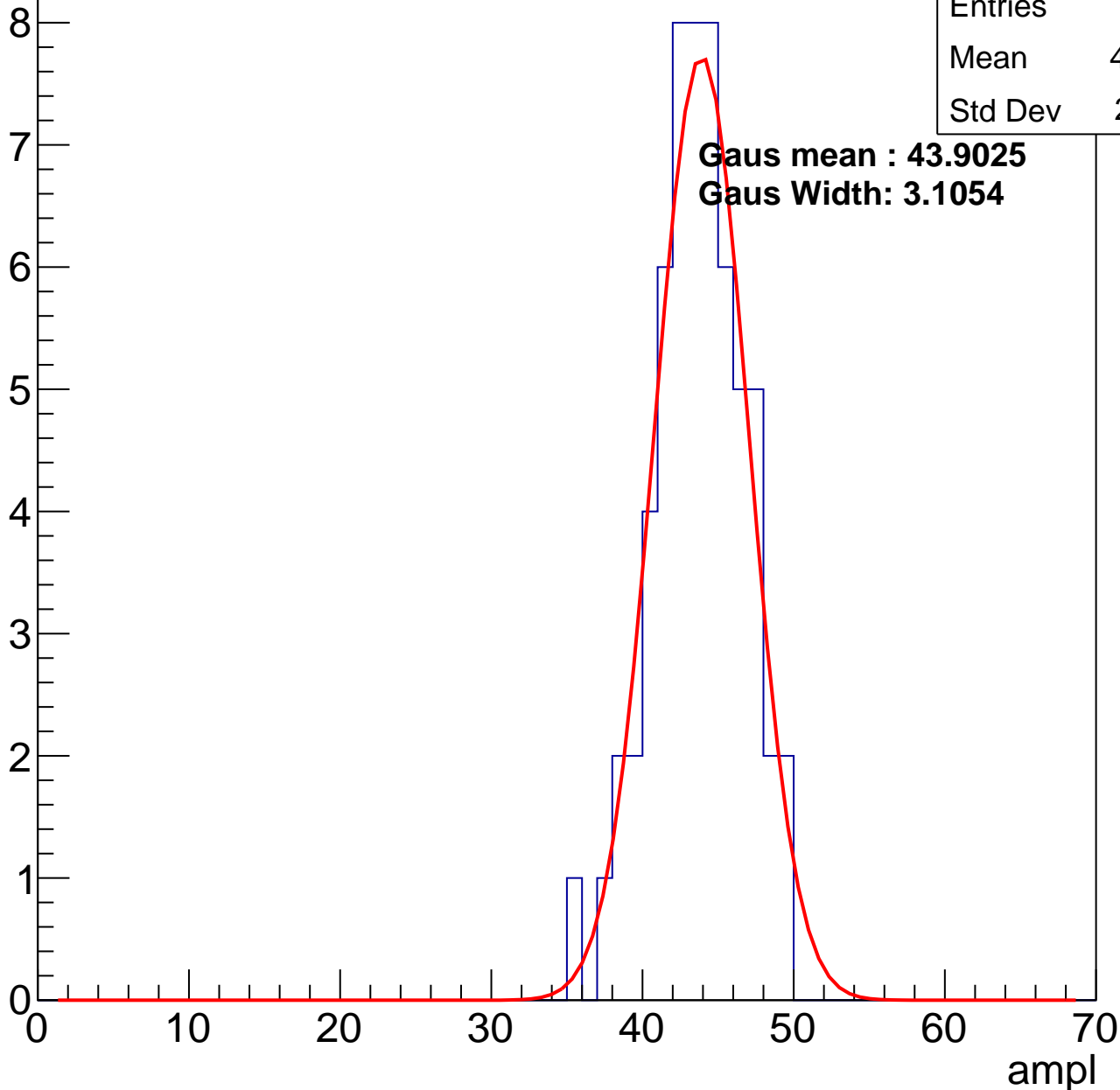
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.22
Std Dev	2.961

Gaus mean : 43.9025

Gaus Width: 3.1054

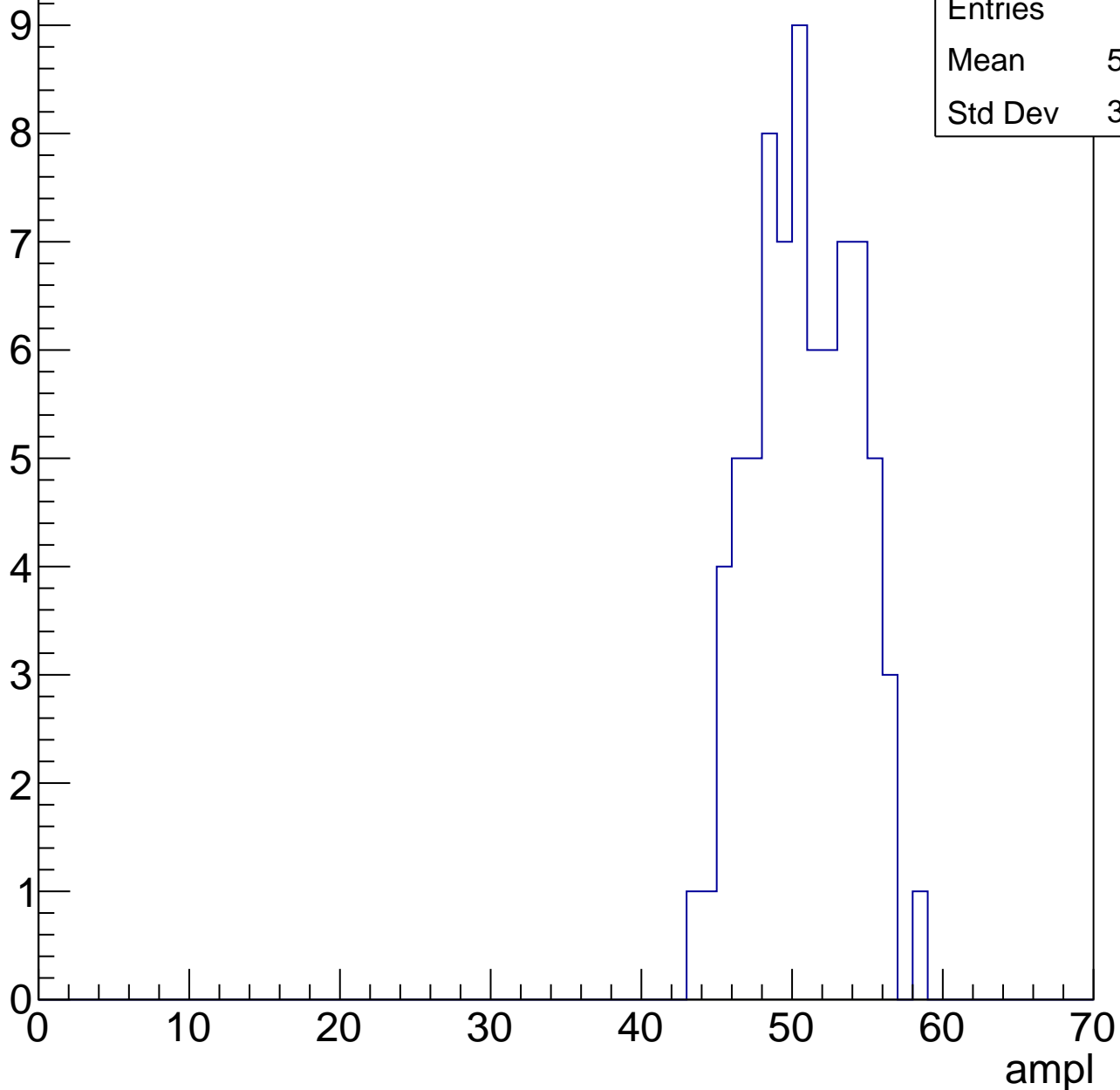


B0L002S, U17-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	50.36
Std Dev	3.349

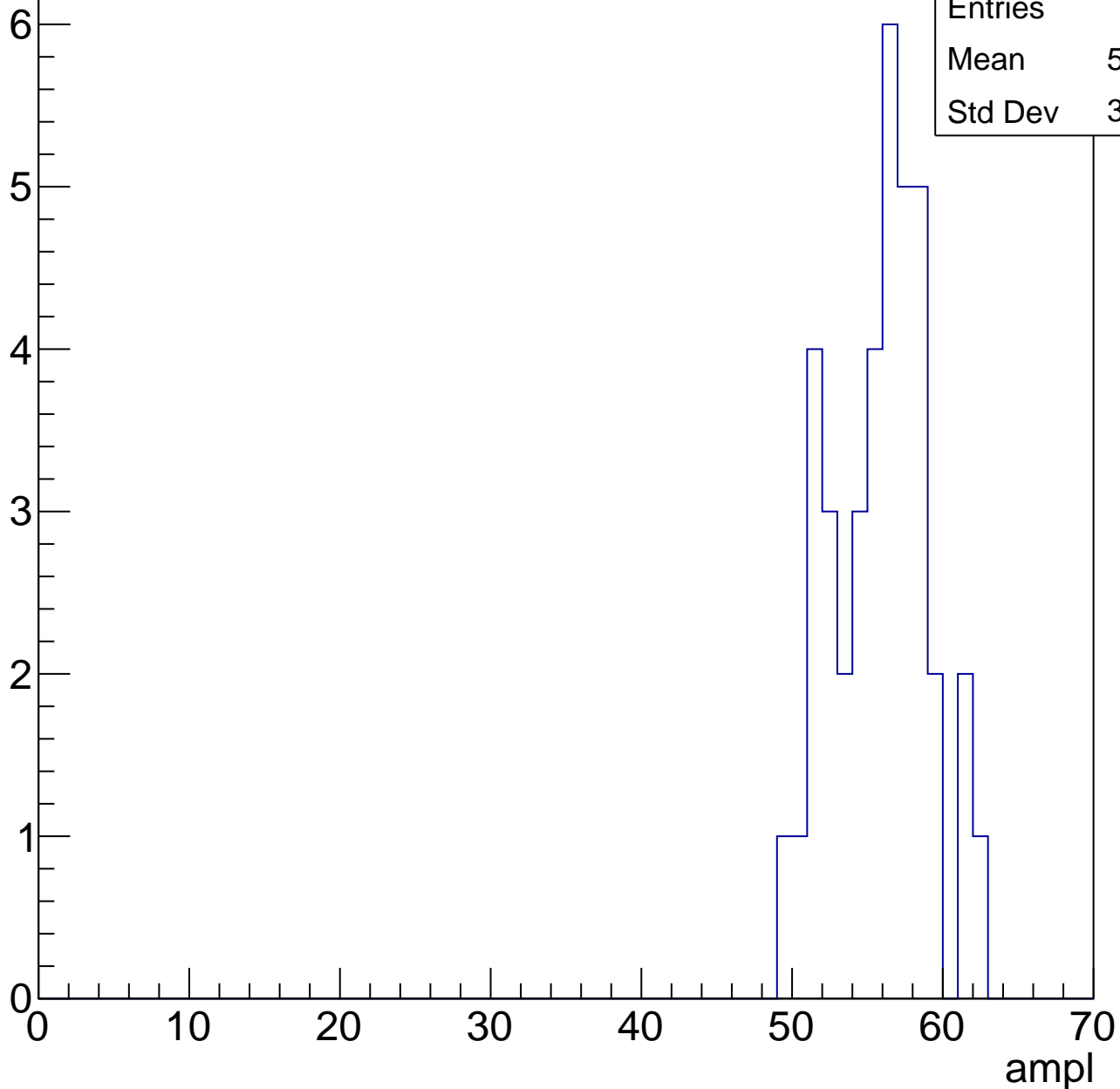


B0L002S, U17-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

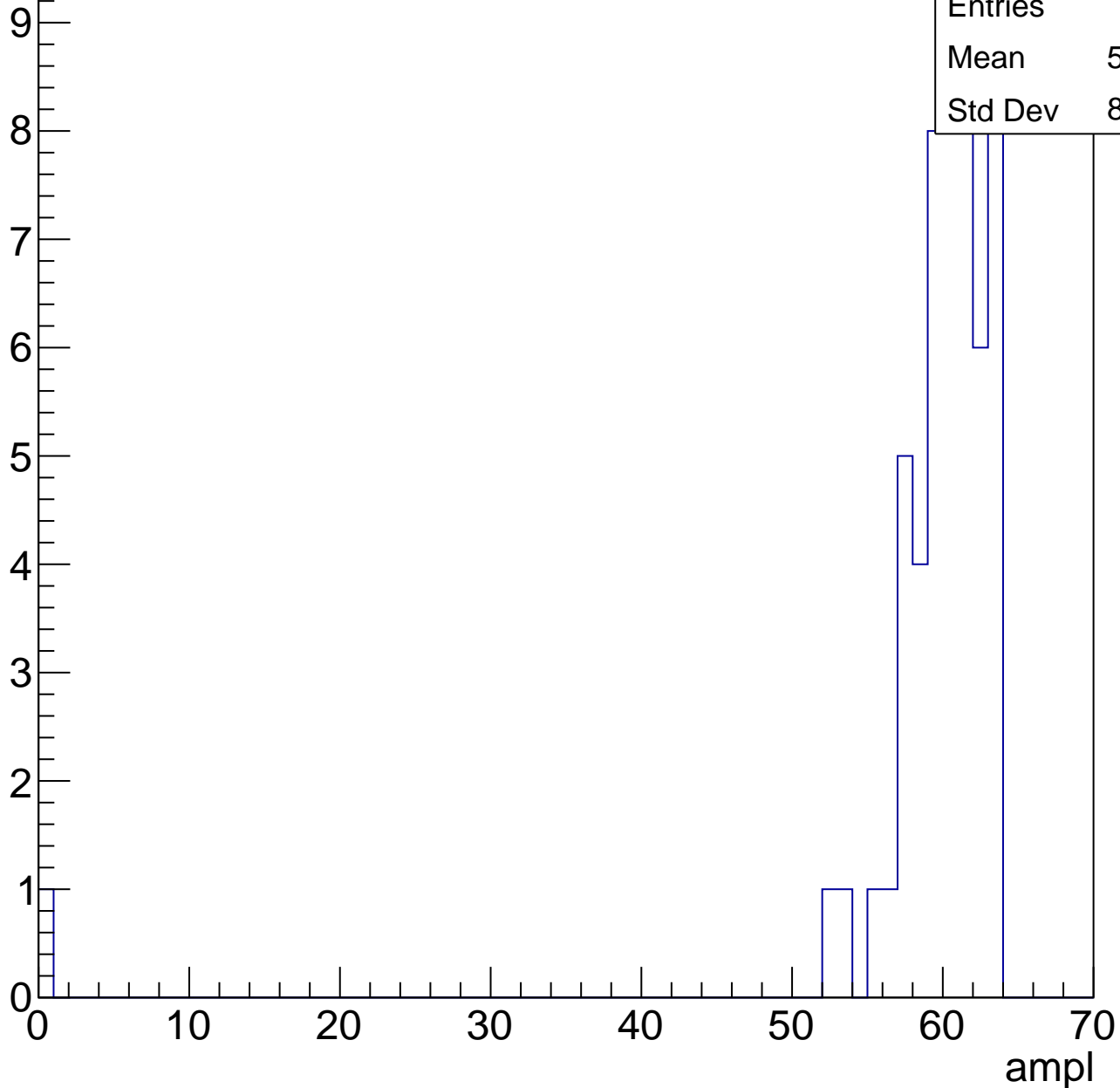
Entries	39
Mean	55.38
Std Dev	3.127



B0L002S, U17-ch89, adc5

calib_packv5_042523_0143.root, FC#8, port C1

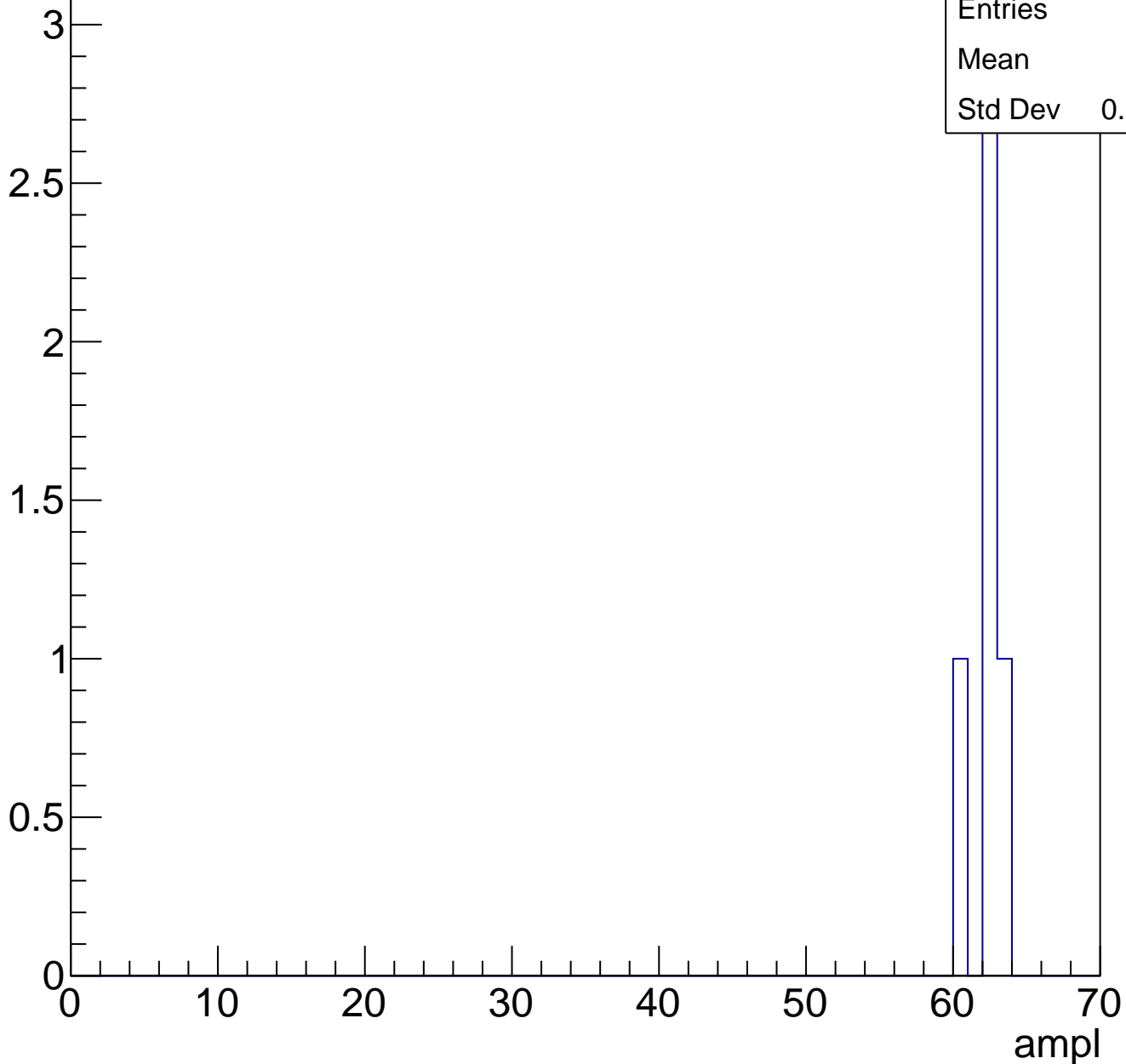
Entry



B0L002S, U17-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B0L002S, U17-ch90, adc0

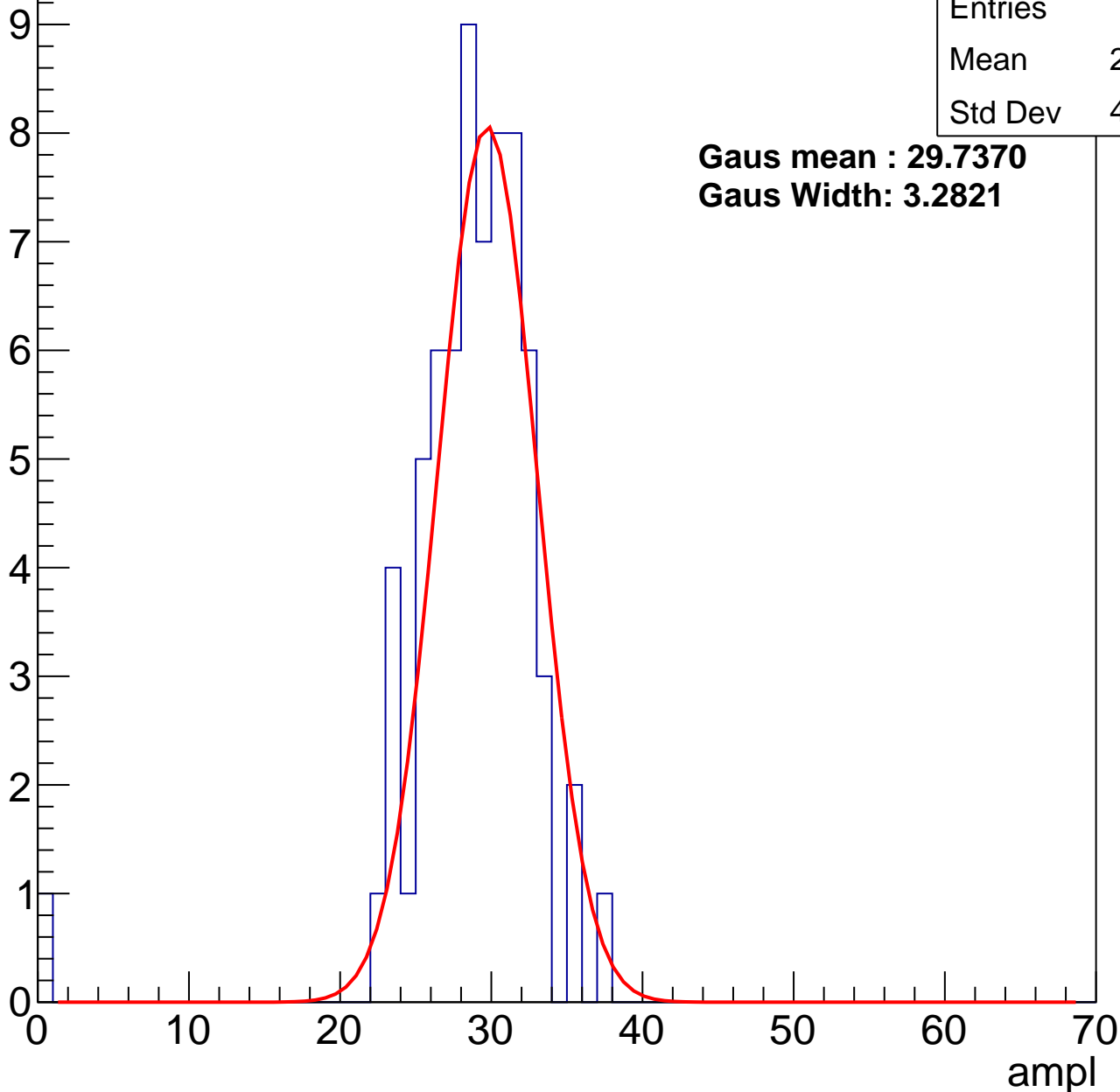
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.26
Std Dev	4.645

Gaus mean : 29.7370

Gaus Width: 3.2821



B0L002S, U17-ch90, adc1

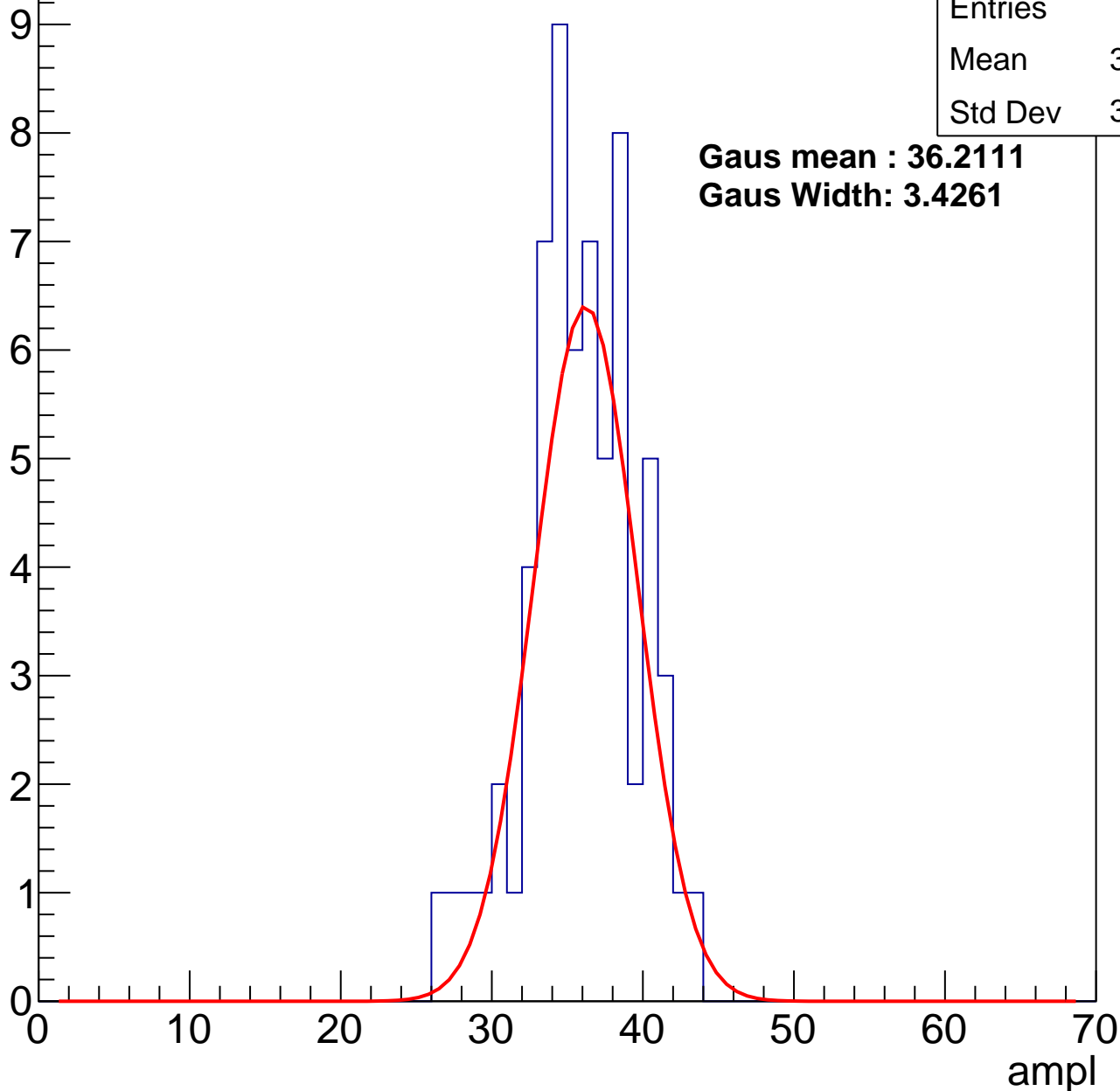
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.43
Std Dev	3.582

Gaus mean : 36.2111

Gaus Width: 3.4261



B0L002S, U17-ch90, adc2

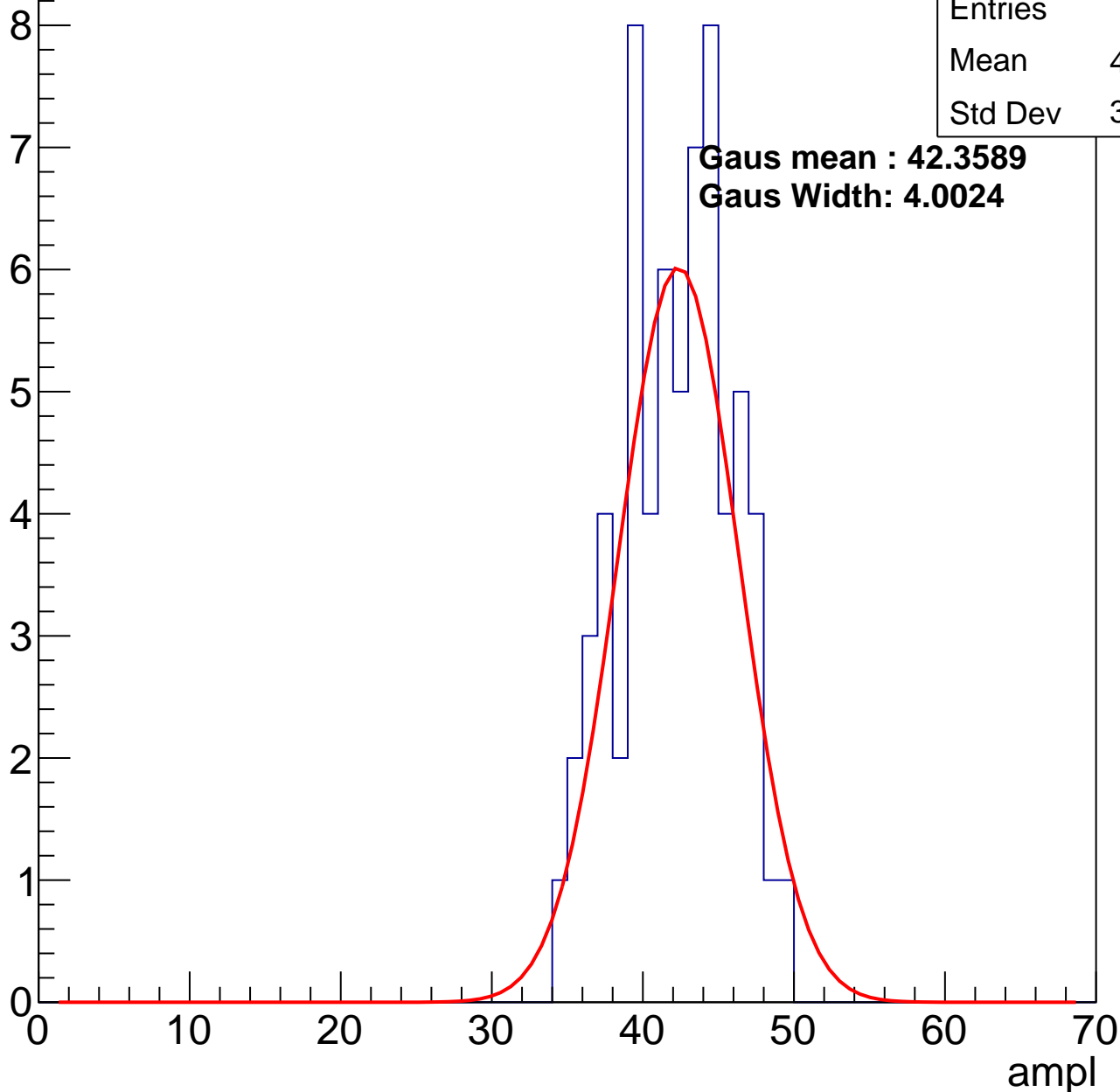
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	41.72
Std Dev	3.576

Gaus mean : 42.3589

Gaus Width: 4.0024

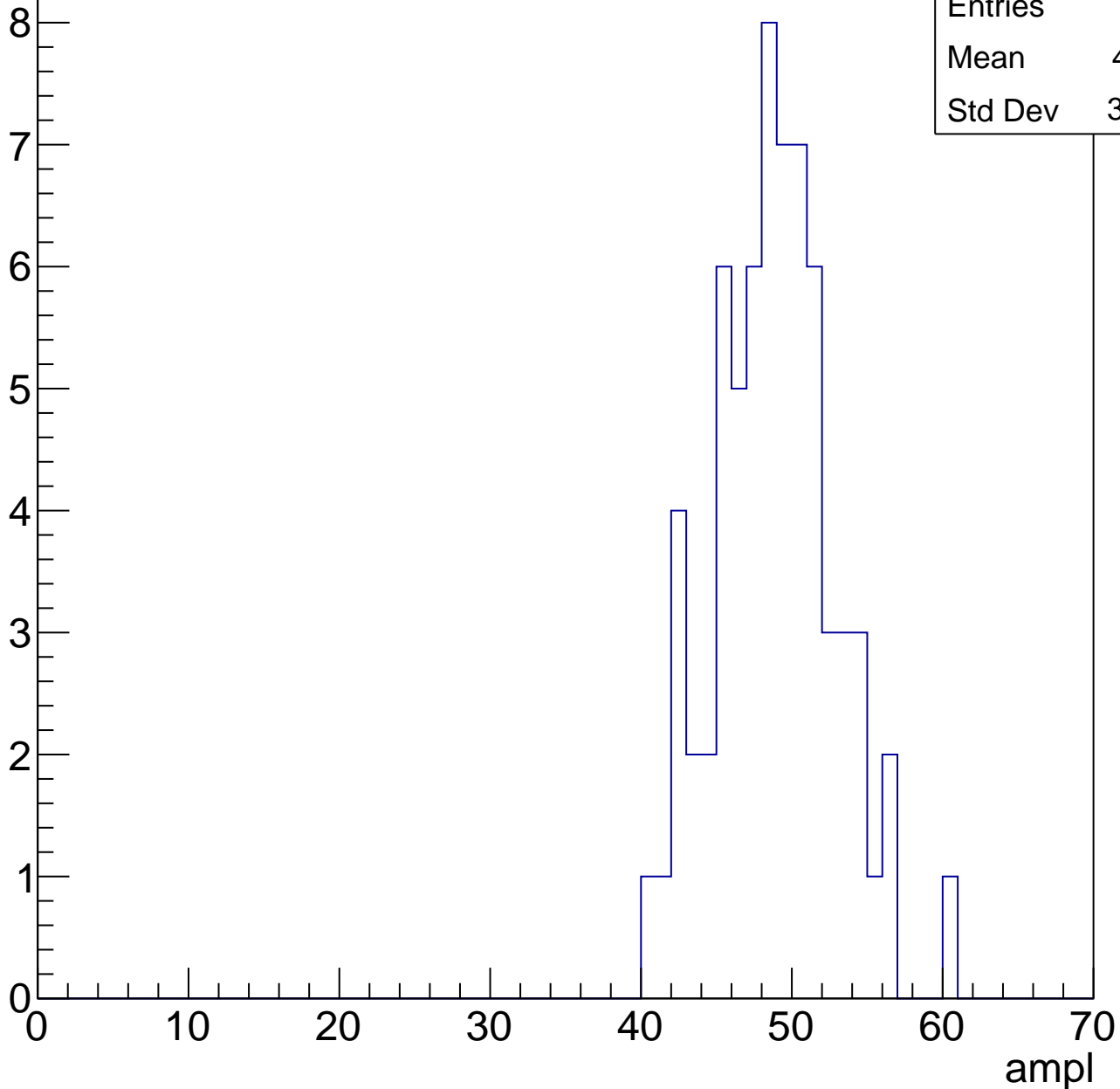


B0L002S, U17-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	48.41
Std Dev	3.927

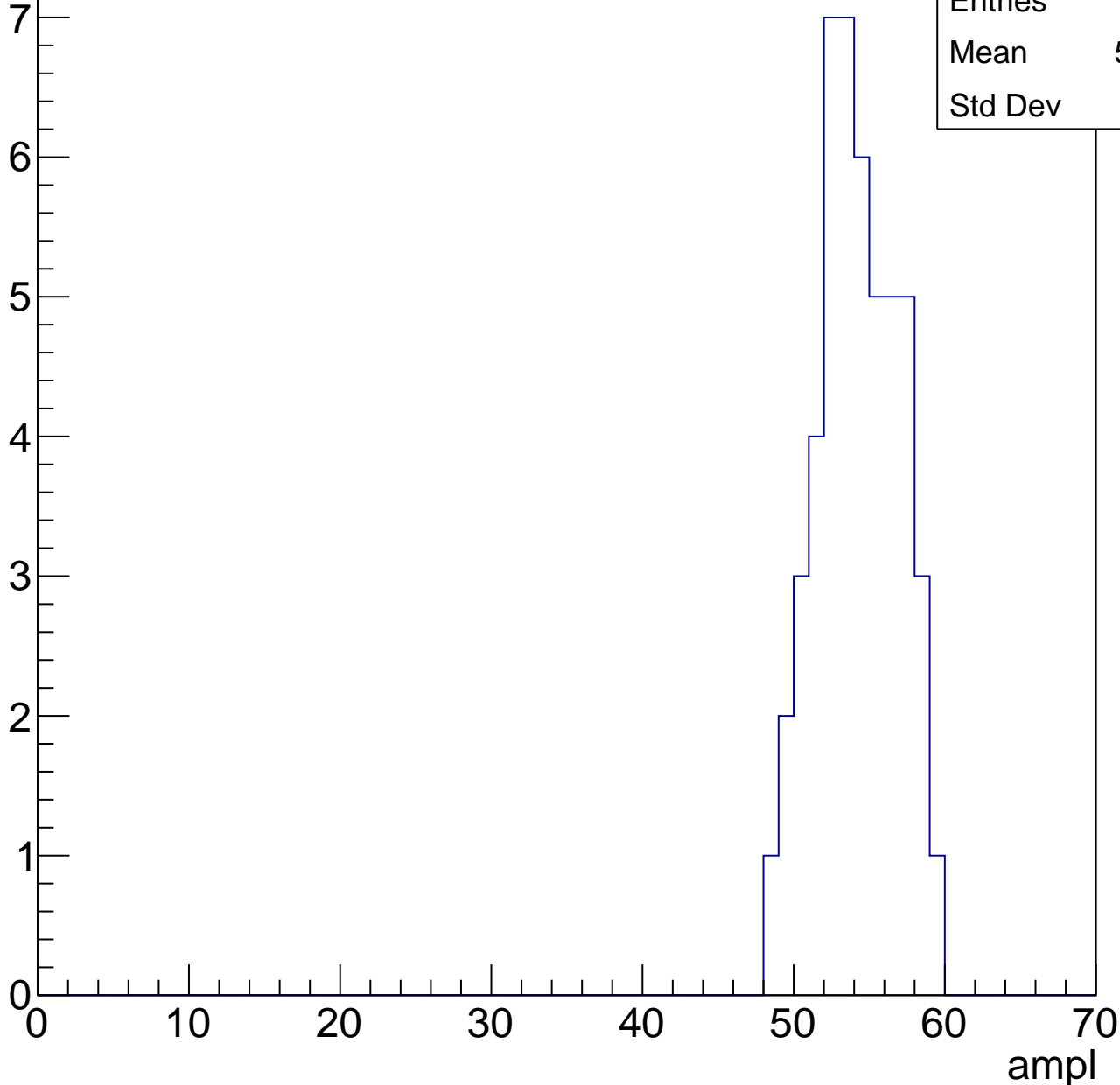


B0L002S, U17-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

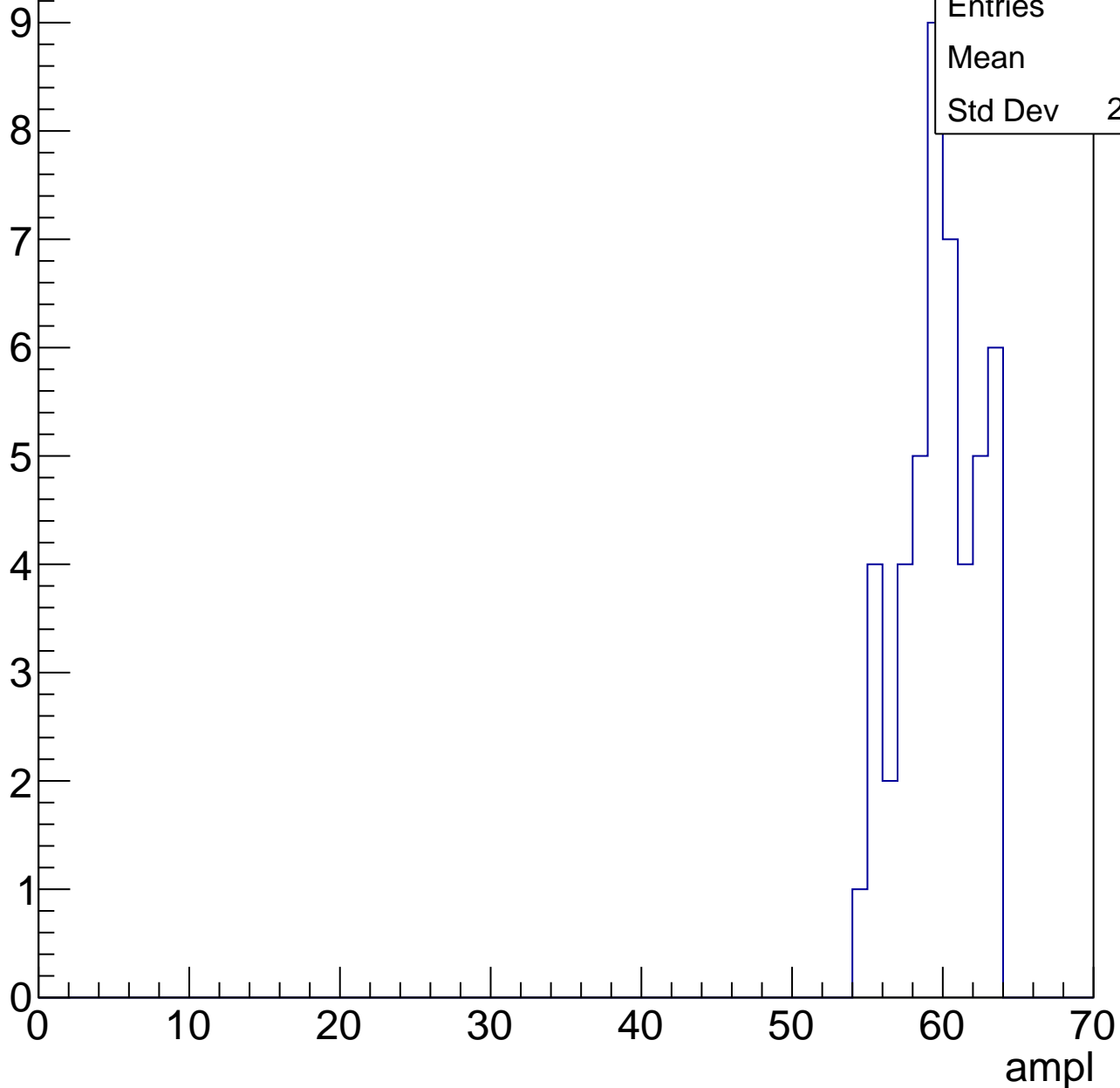
Entries	49
Mean	53.71
Std Dev	2.65



B0L002S, U17-ch90, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

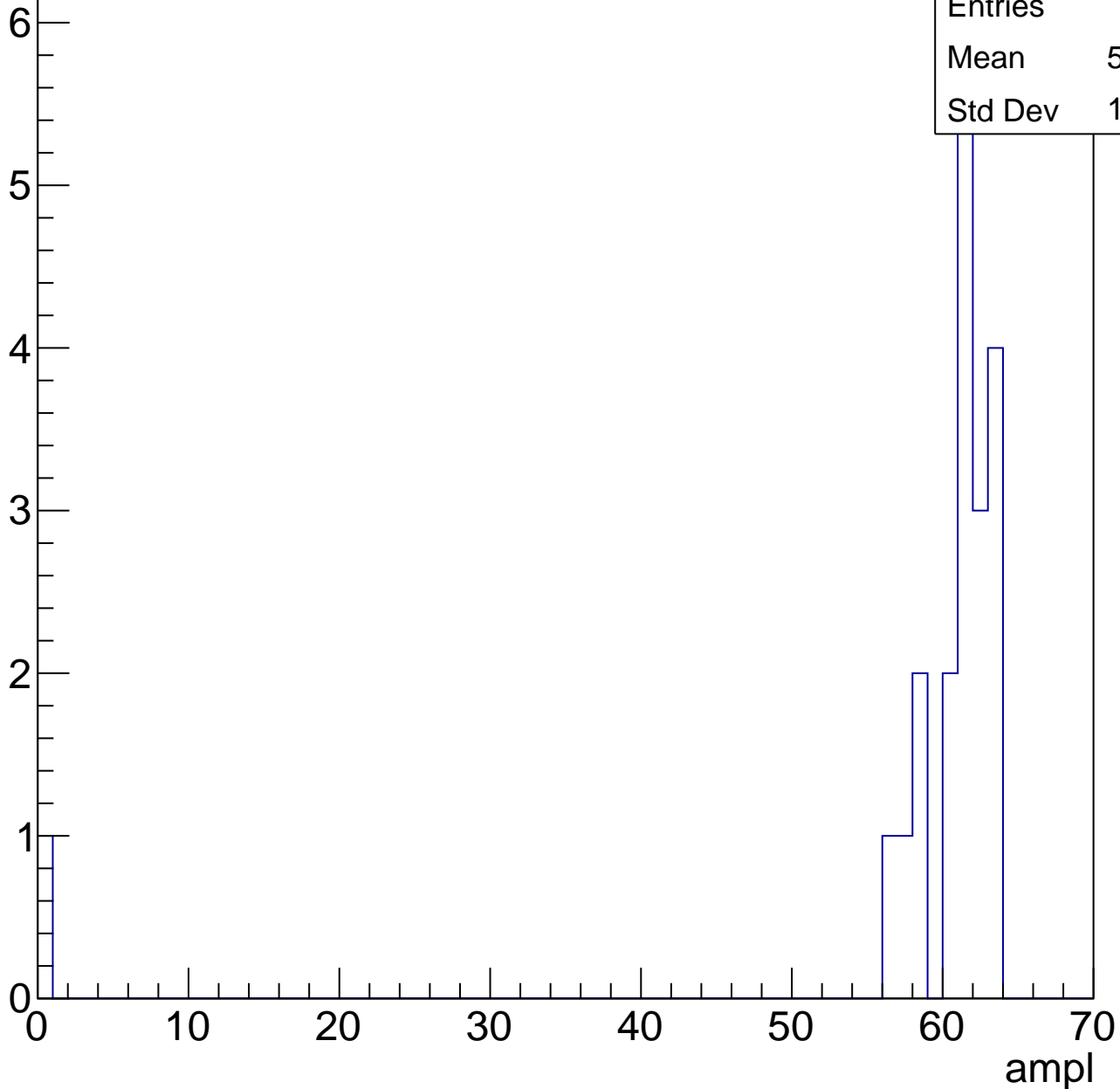


B0L002S, U17-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

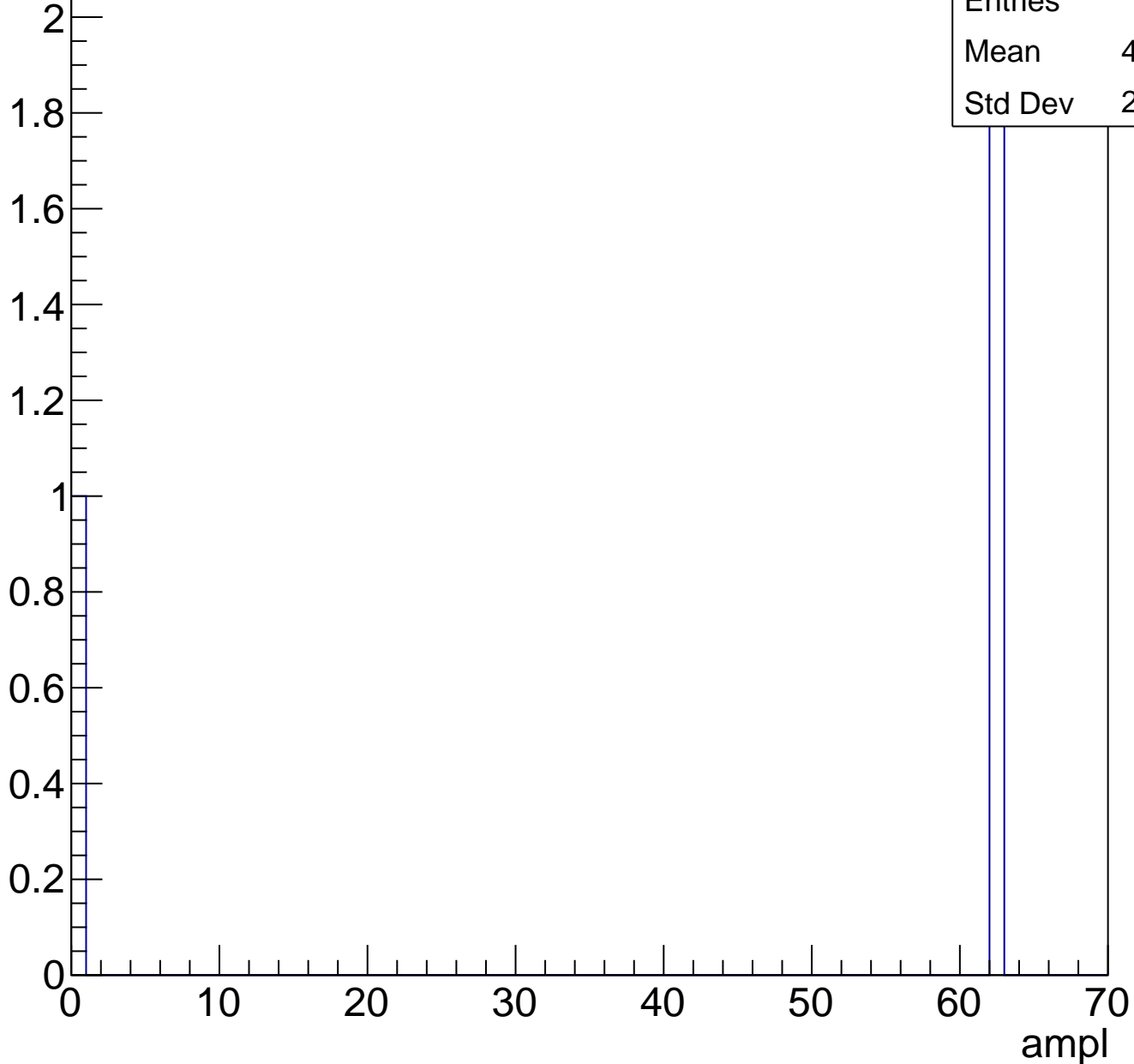
Entries	20
Mean	57.65
Std Dev	13.37



B0L002S, U17-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch91, adc0

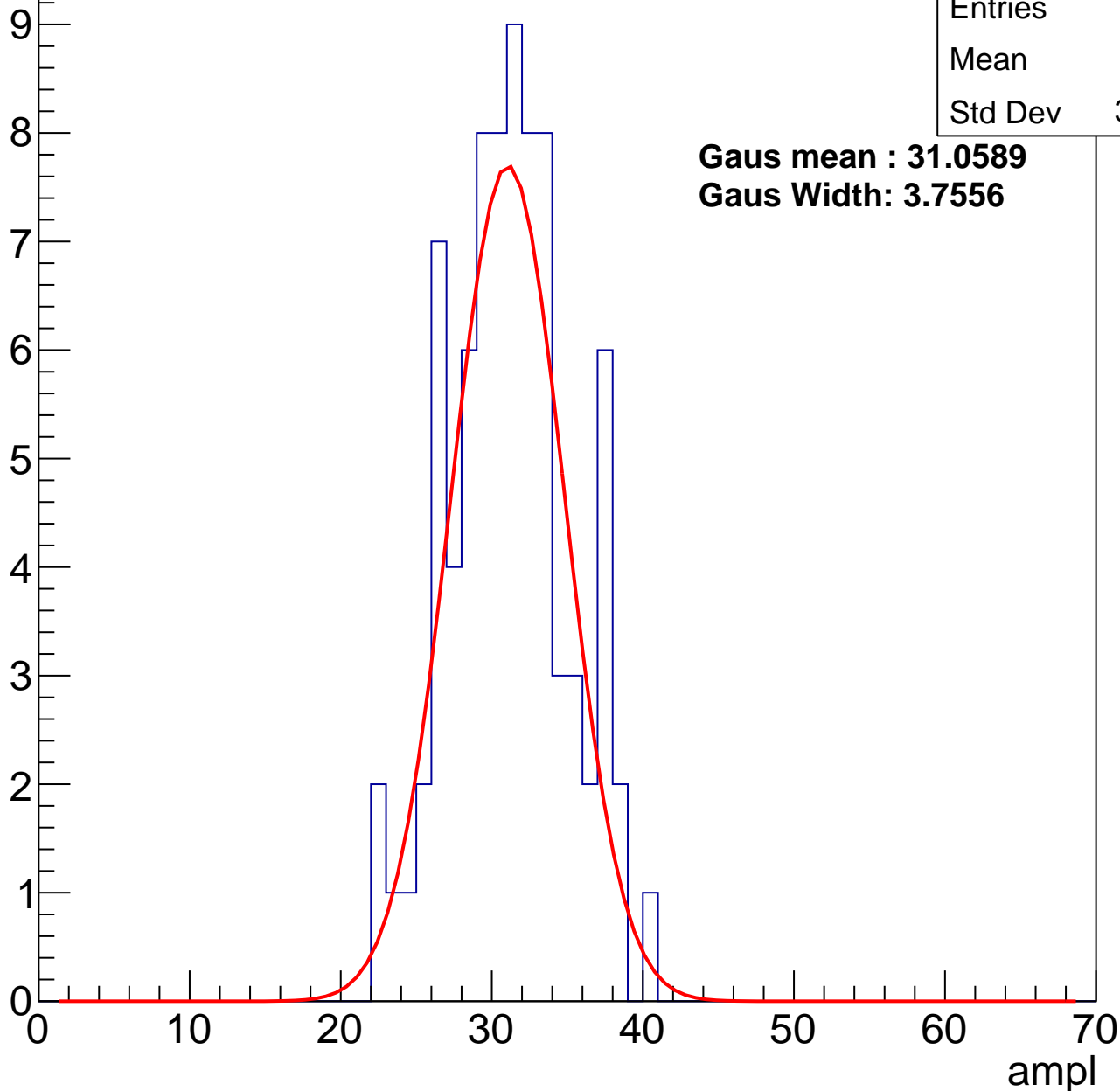
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	30.7
Std Dev	3.901

Gaus mean : 31.0589

Gaus Width: 3.7556



B0L002S, U17-ch91, adc1

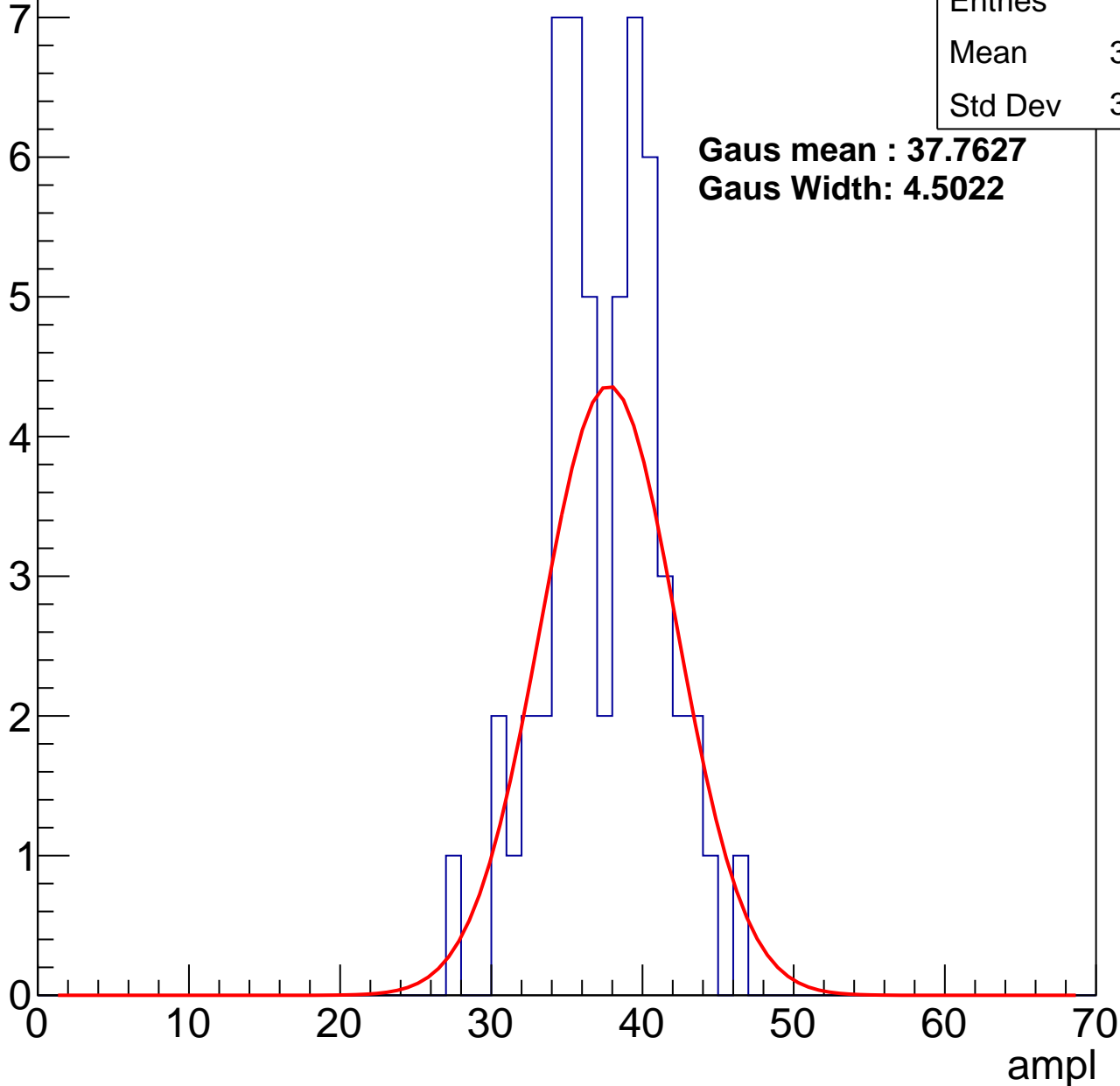
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.98
Std Dev	3.777

Gaus mean : 37.7627

Gaus Width: 4.5022



B0L002S, U17-ch91, adc2

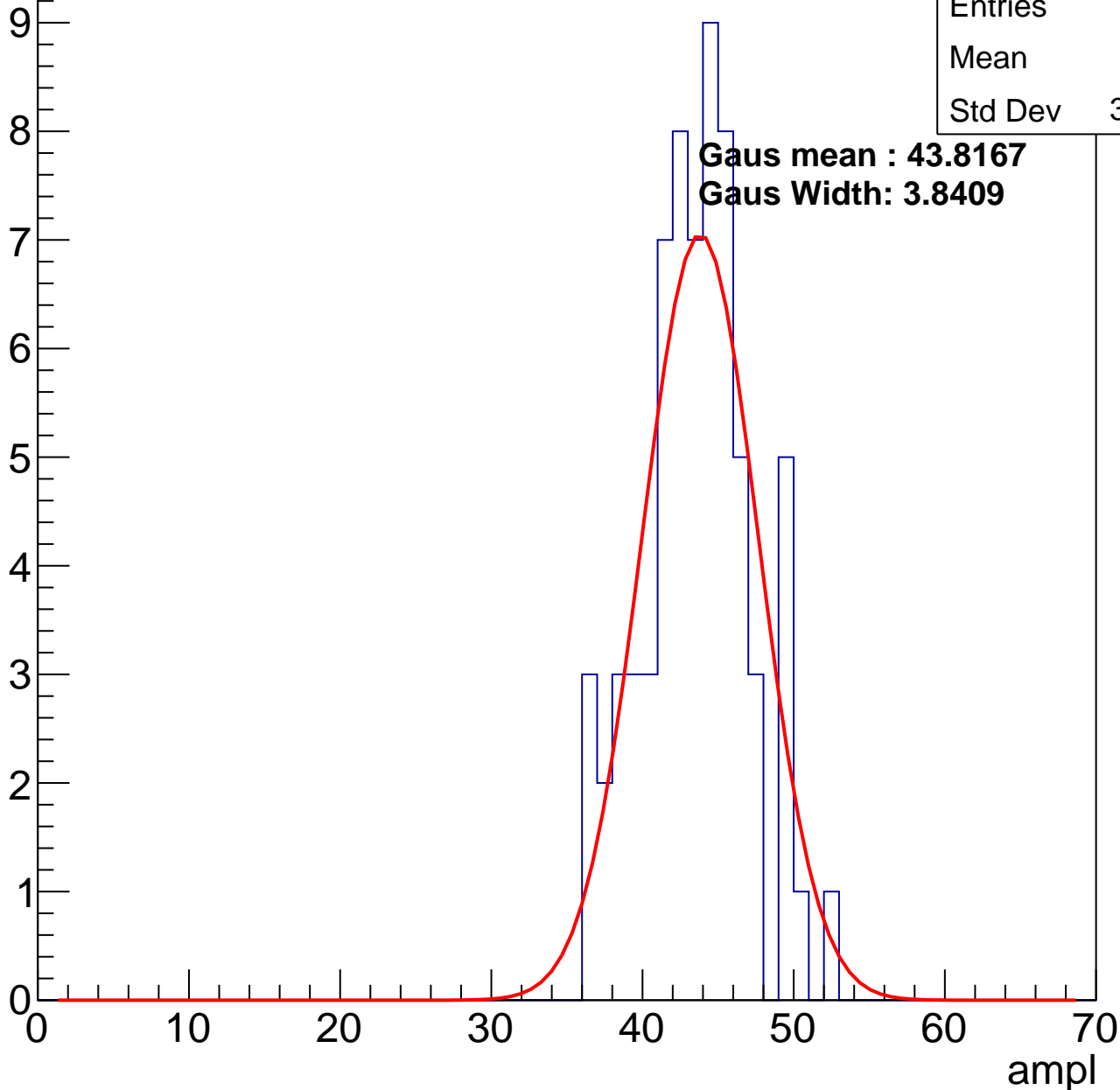
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.1
Std Dev	3.532

Gaus mean : 43.8167

Gaus Width: 3.8409



B0L002S, U17-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	53
Mean	49.68
Std Dev	3.149

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

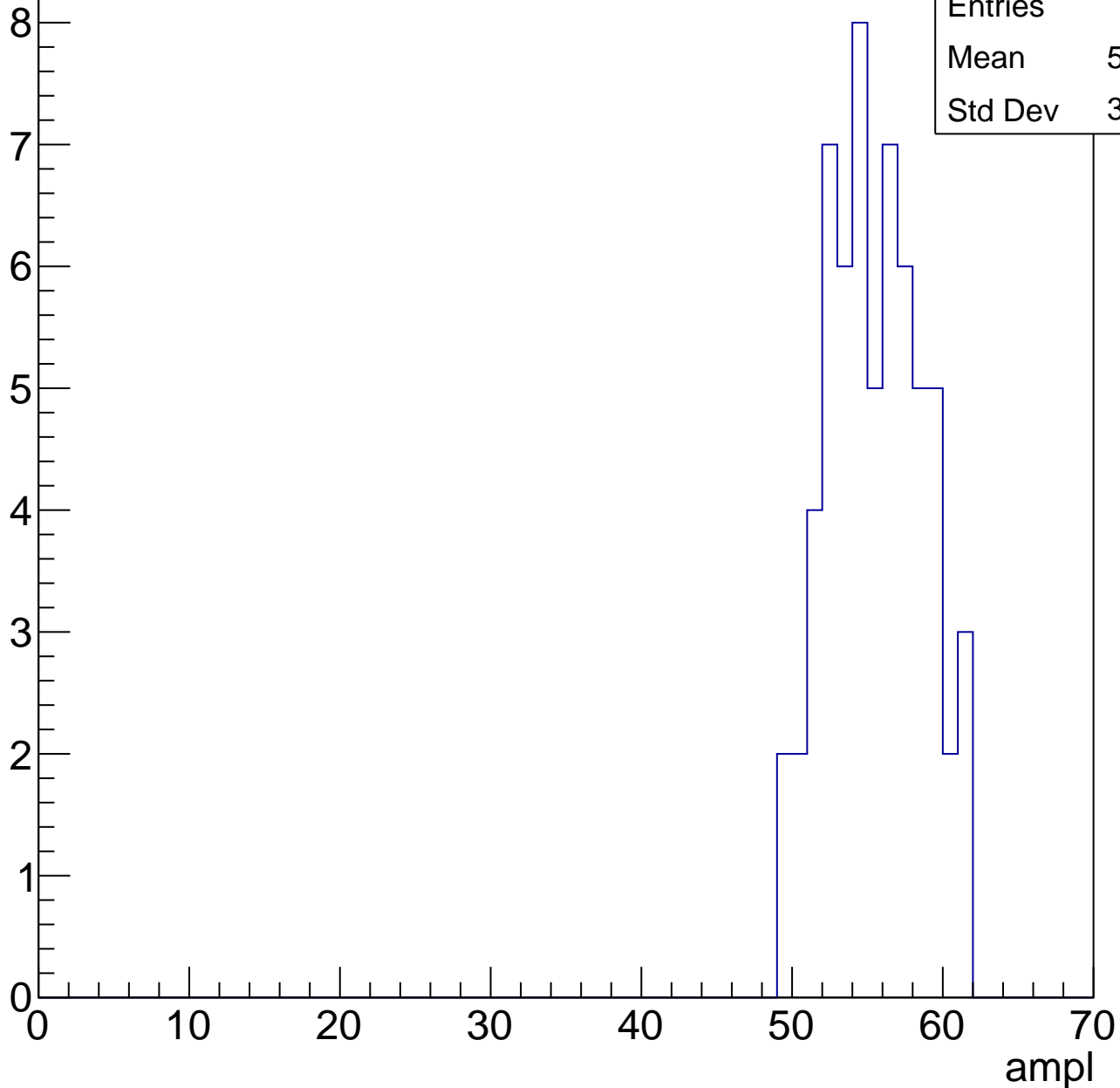
70

B0L002S, U17-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

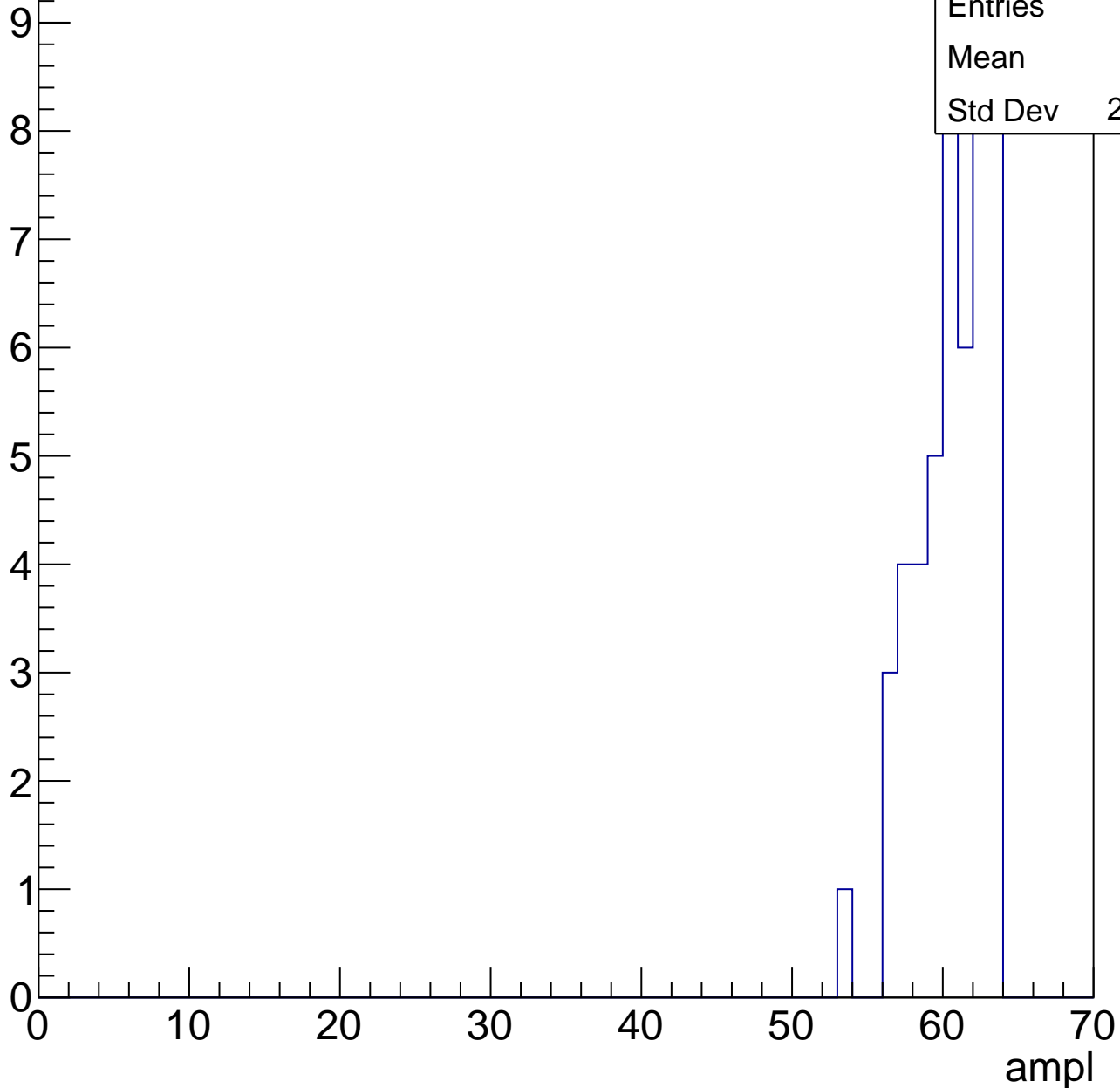
Entries	62
Mean	55.05
Std Dev	3.097



B0L002S, U17-ch91, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch92, adc0

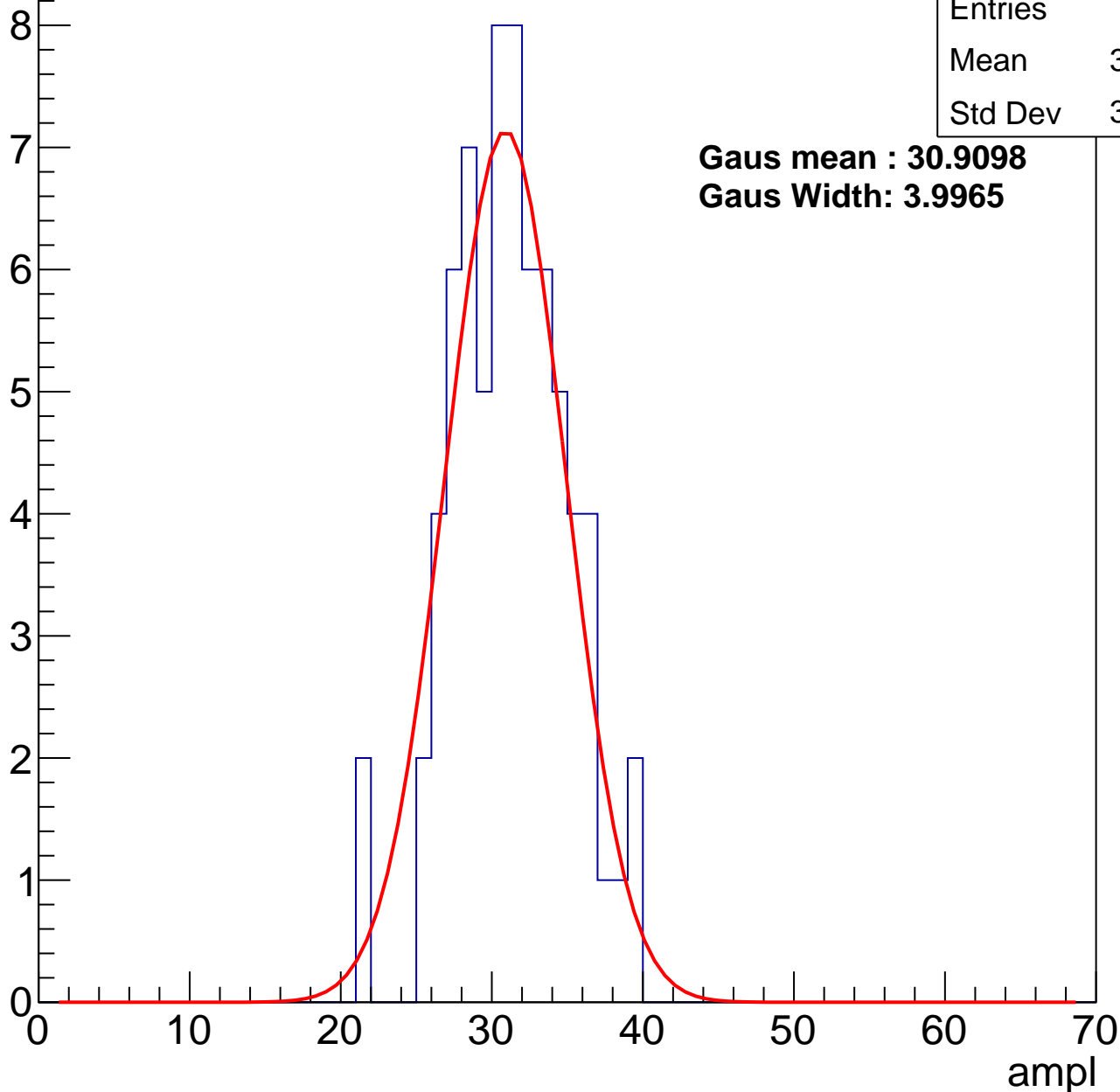
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.76
Std Dev	3.766

Gaus mean : 30.9098

Gaus Width: 3.9965



B0L002S, U17-ch92, adc1

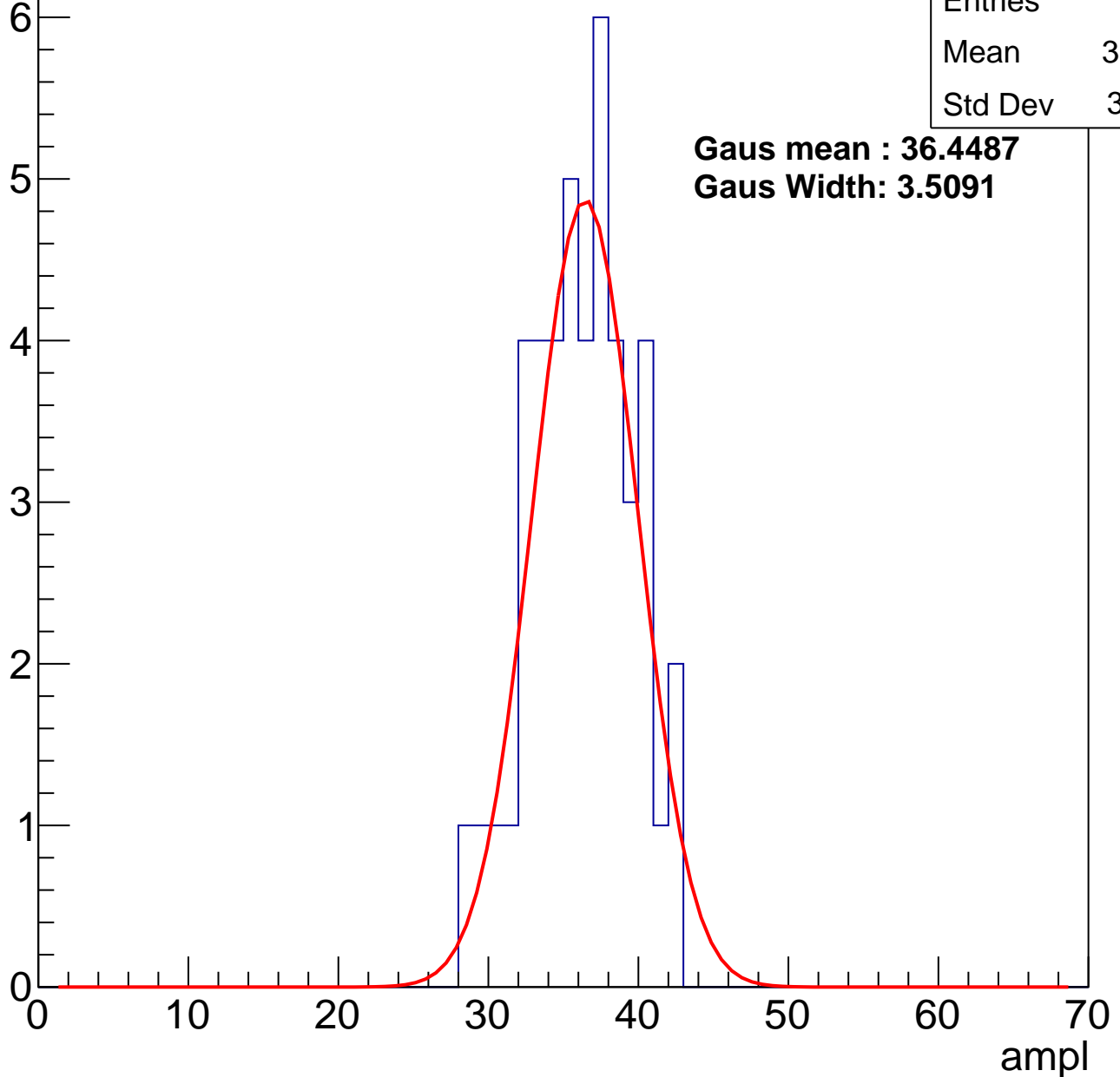
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	35.76
Std Dev	3.341

Gaus mean : 36.4487

Gaus Width: 3.5091



B0L002S, U17-ch92, adc2

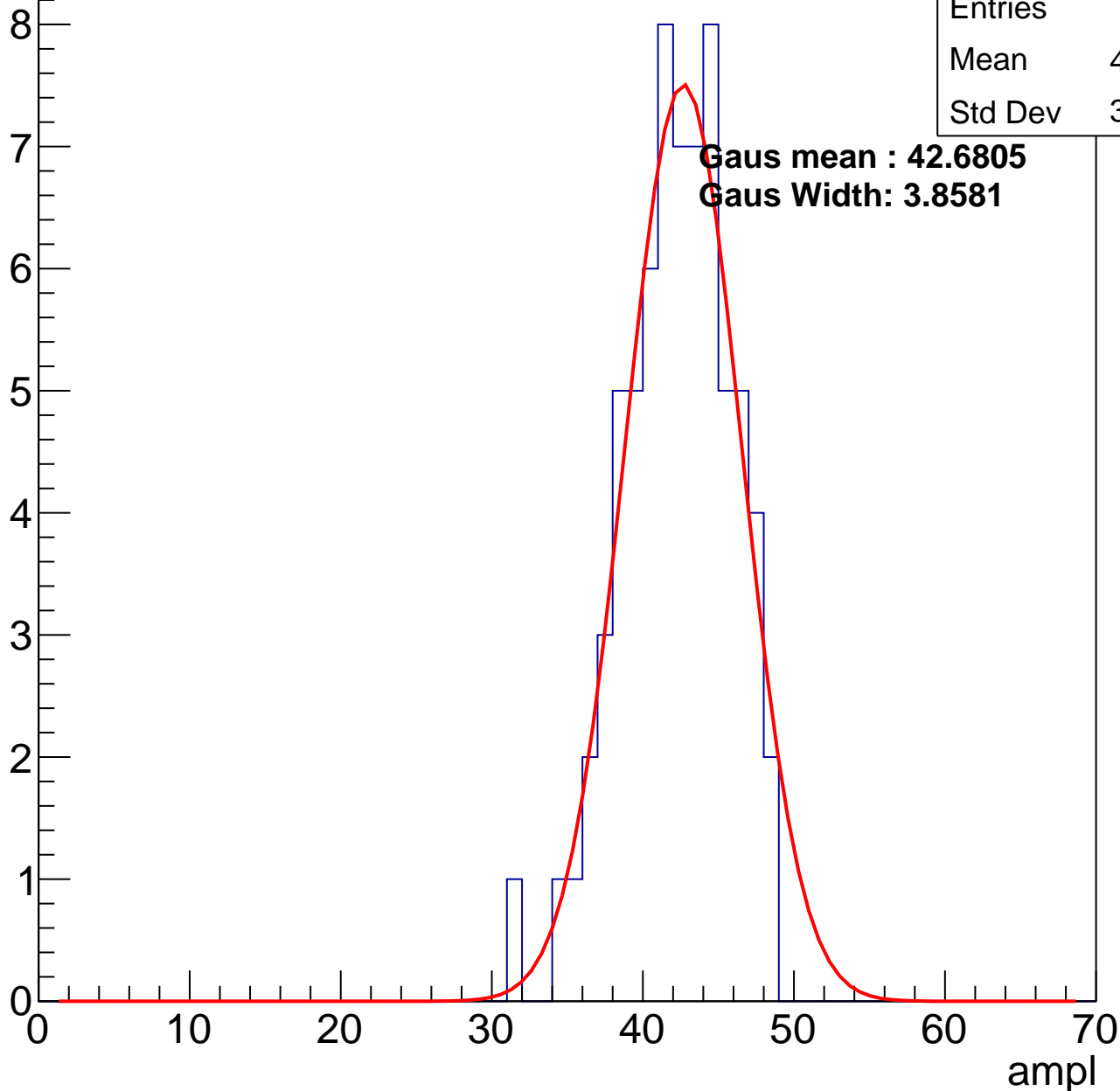
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	41.74
Std Dev	3.524

Gaus mean : 42.6805

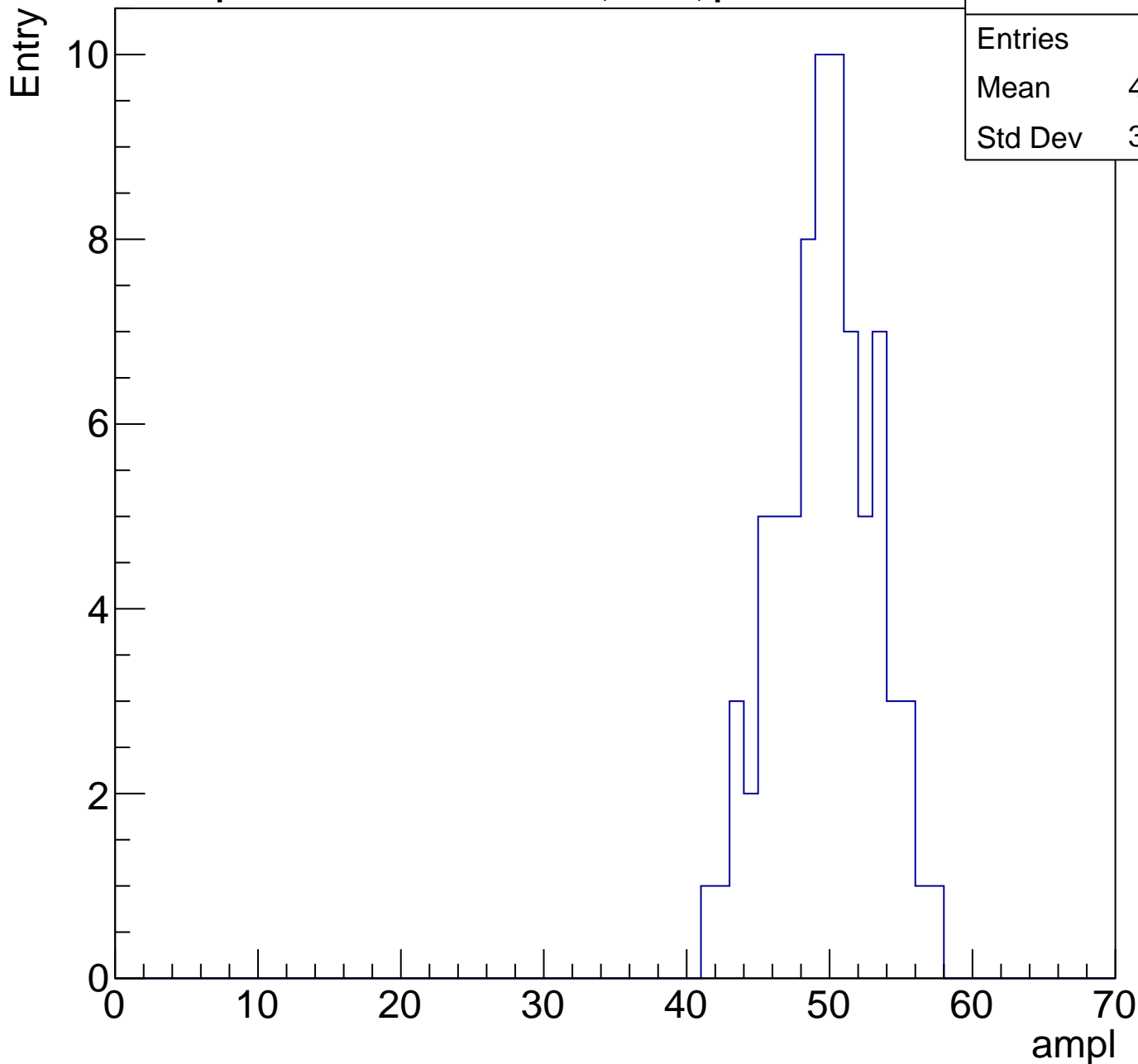
Gaus Width: 3.8581



B0L002S, U17-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	49.25
Std Dev	3.438

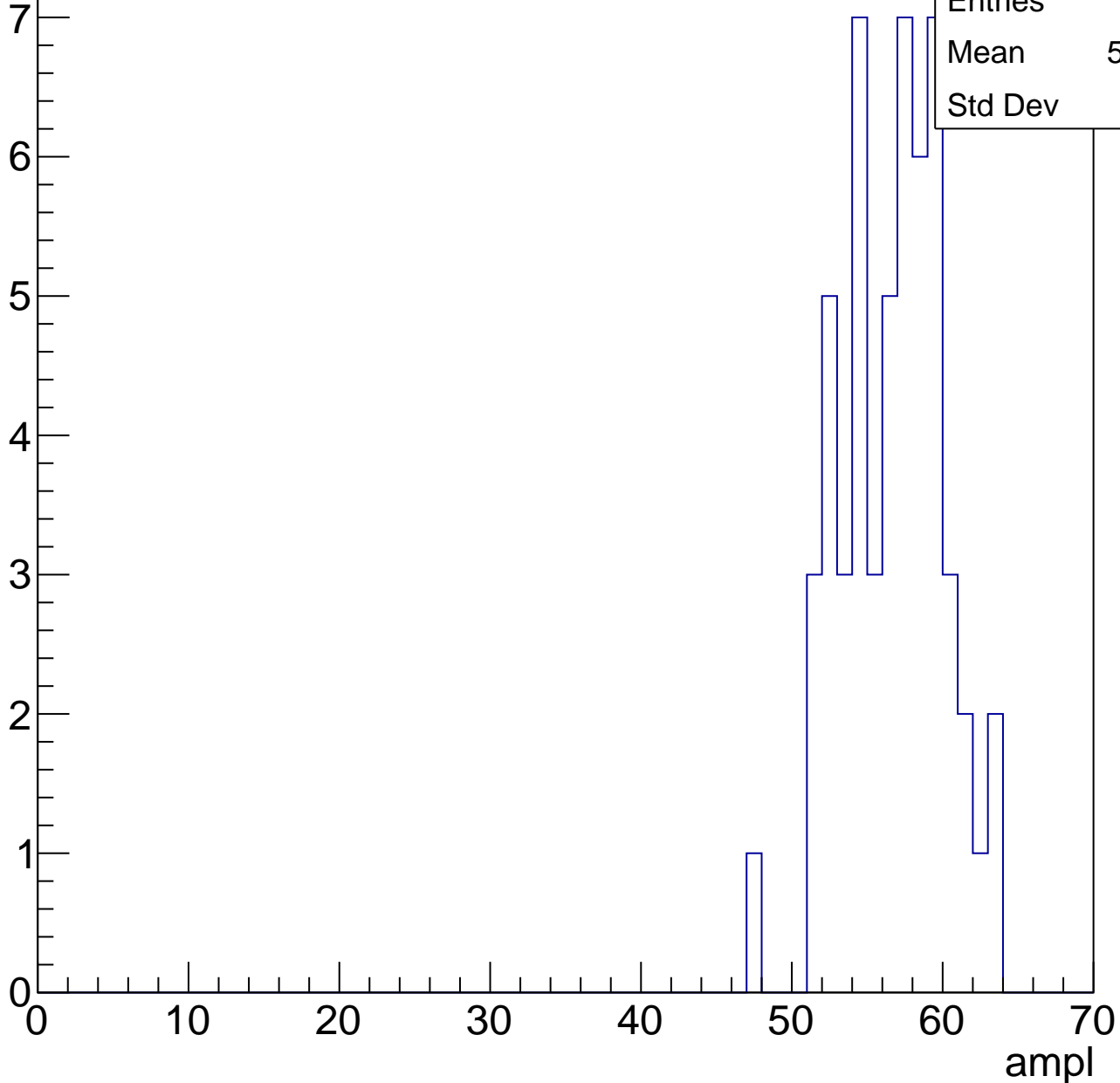


B0L002S, U17-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.22
Std Dev	3.35

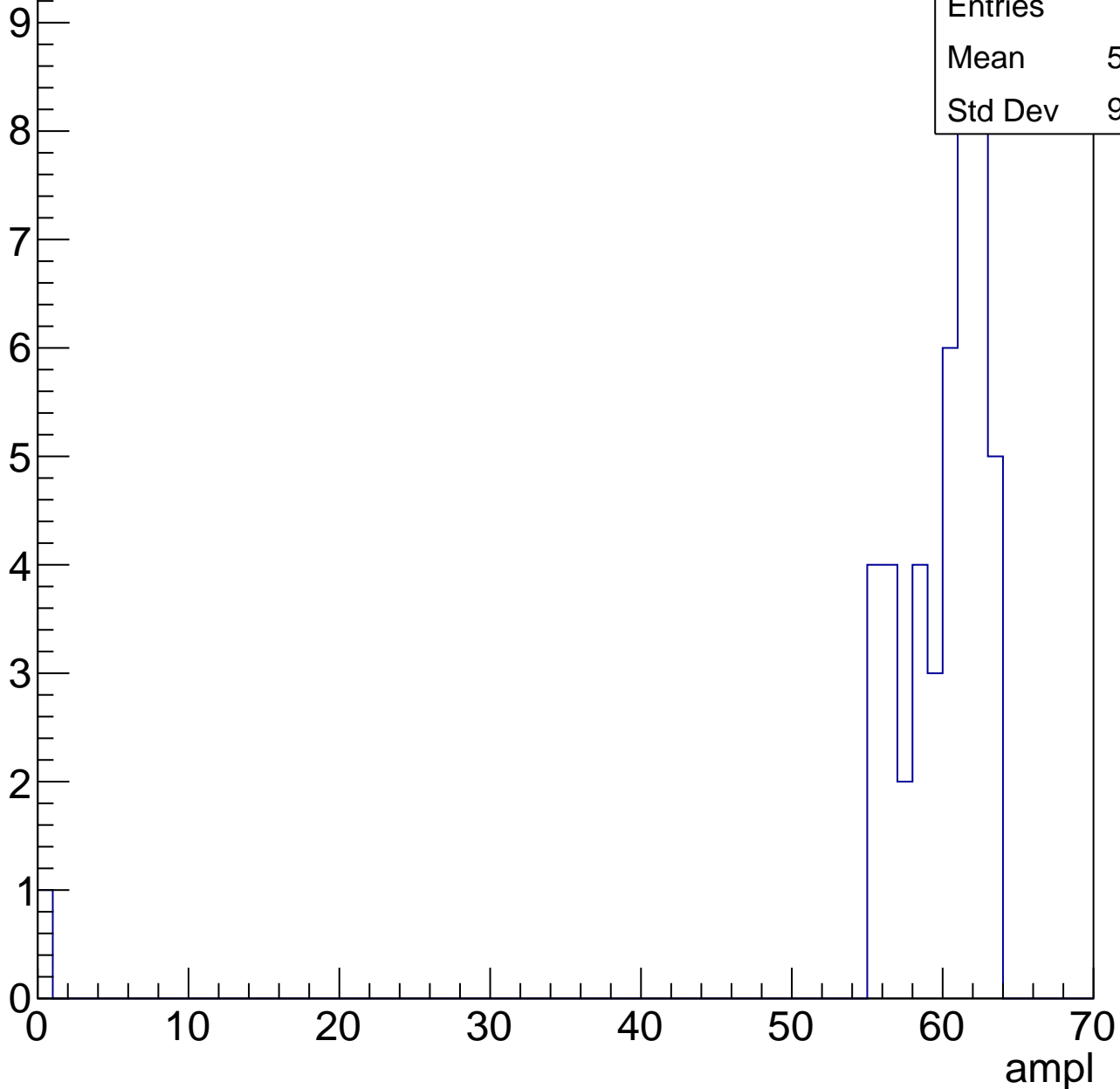


B0L002S, U17-ch92, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	58.43
Std Dev	9.062



B0L002S, U17-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch93, adc0

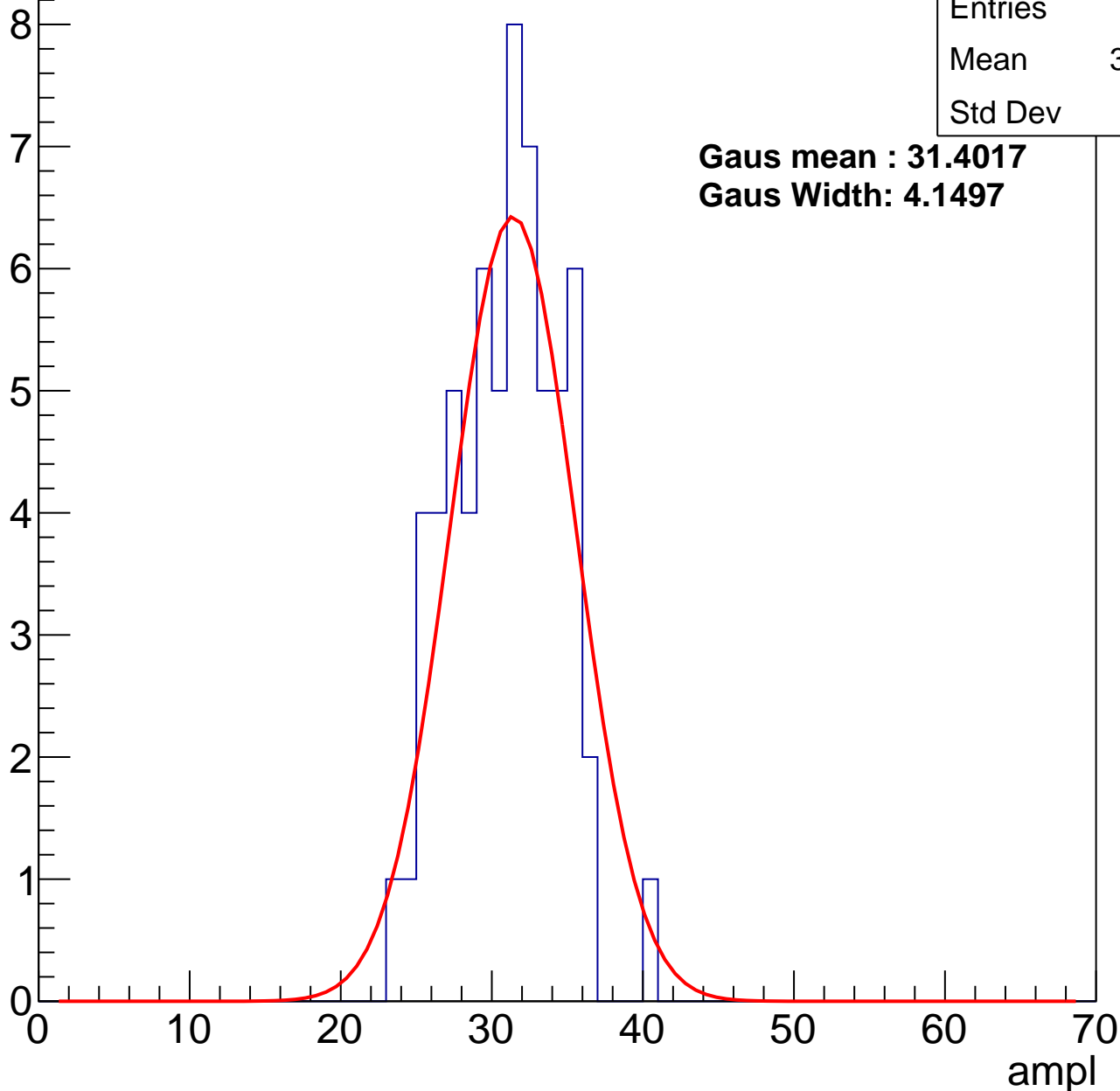
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.48
Std Dev	3.5

Gaus mean : 31.4017

Gaus Width: 4.1497



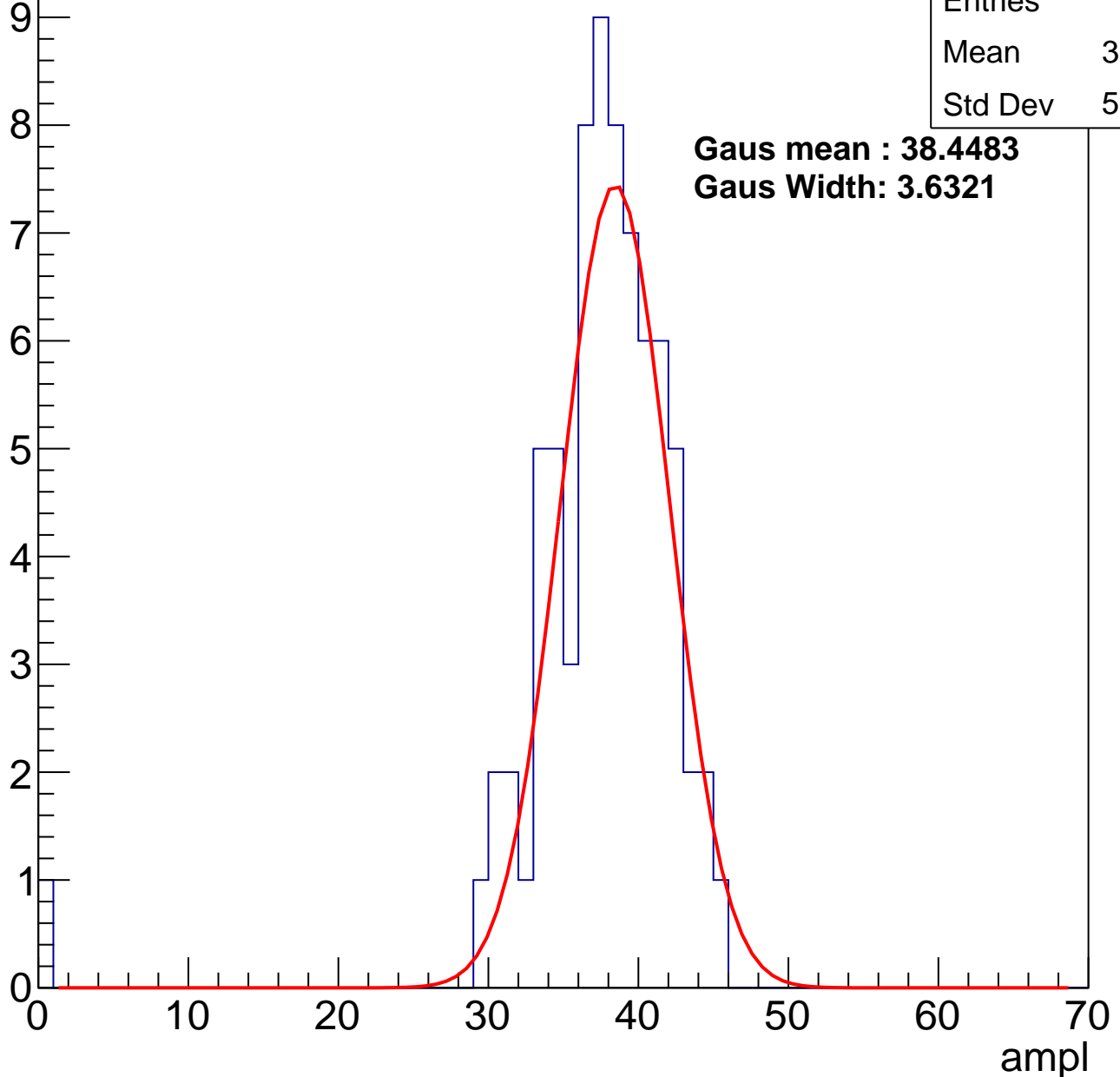
B0L002S, U17-ch93, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	36.97
Std Dev	5.597

Gaus mean : 38.4483
Gaus Width: 3.6321



B0L002S, U17-ch93, adc2

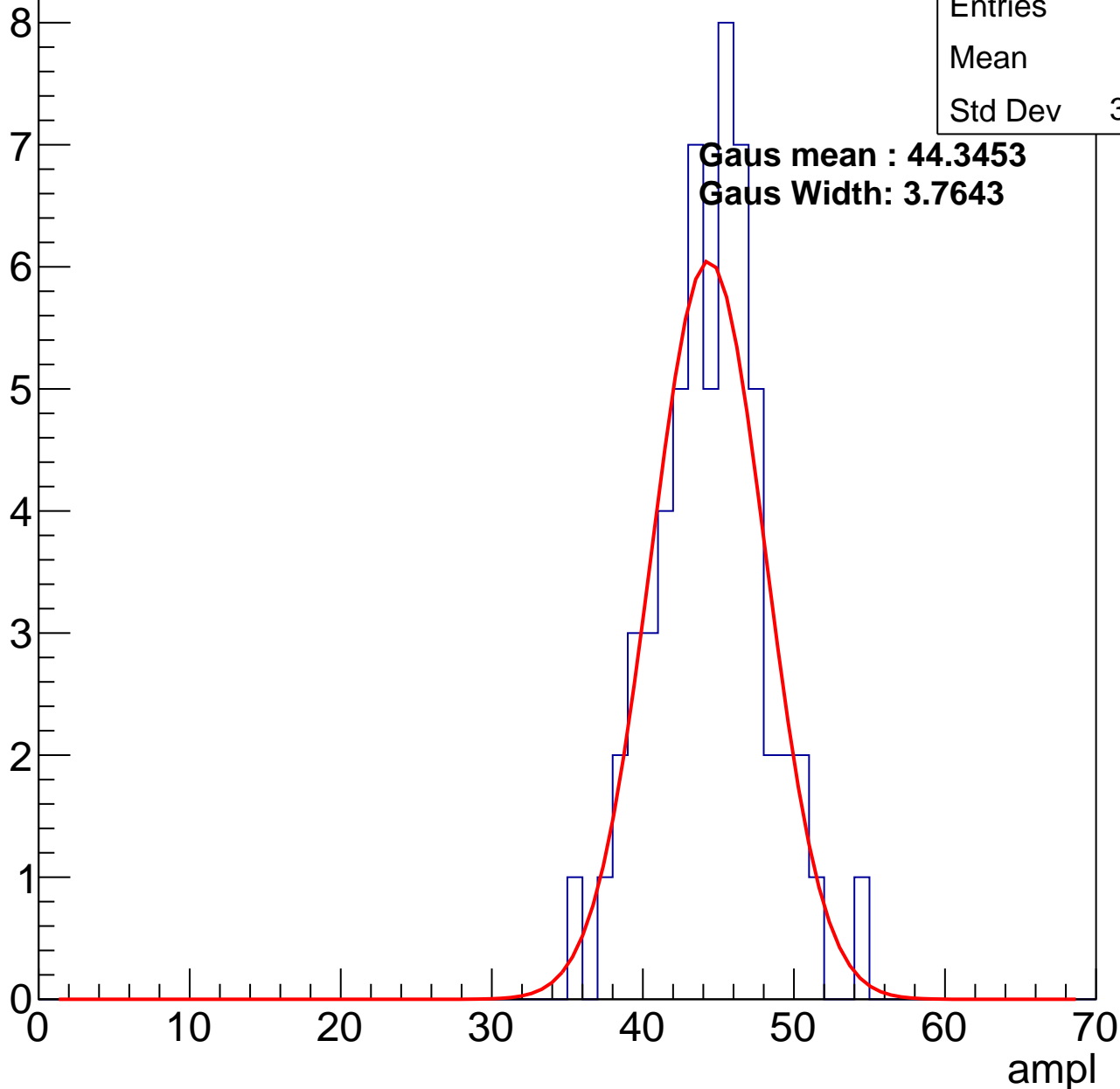
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44
Std Dev	3.617

Gaus mean : 44.3453

Gaus Width: 3.7643

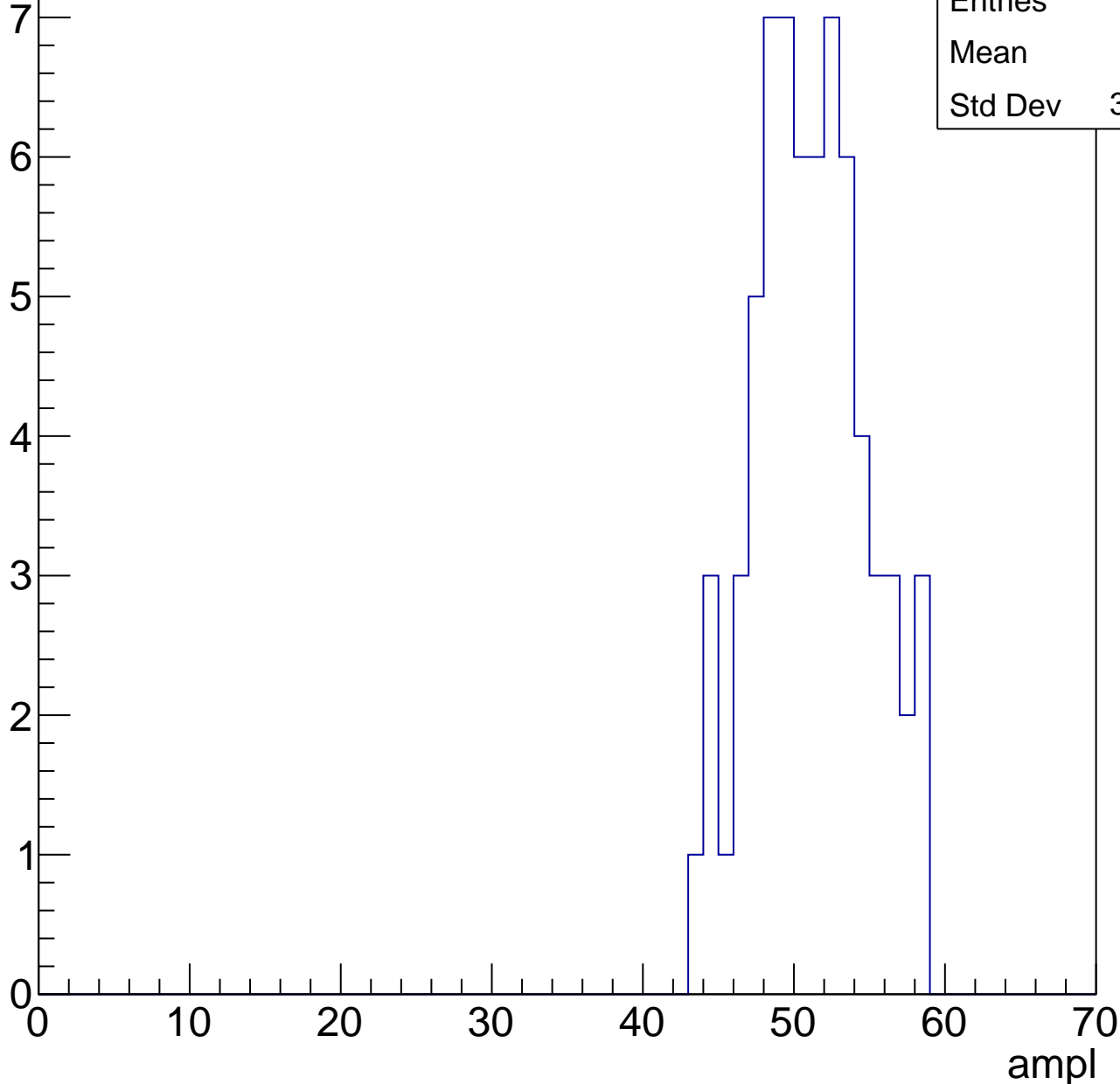


B0L002S, U17-ch93, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.7
Std Dev	3.669

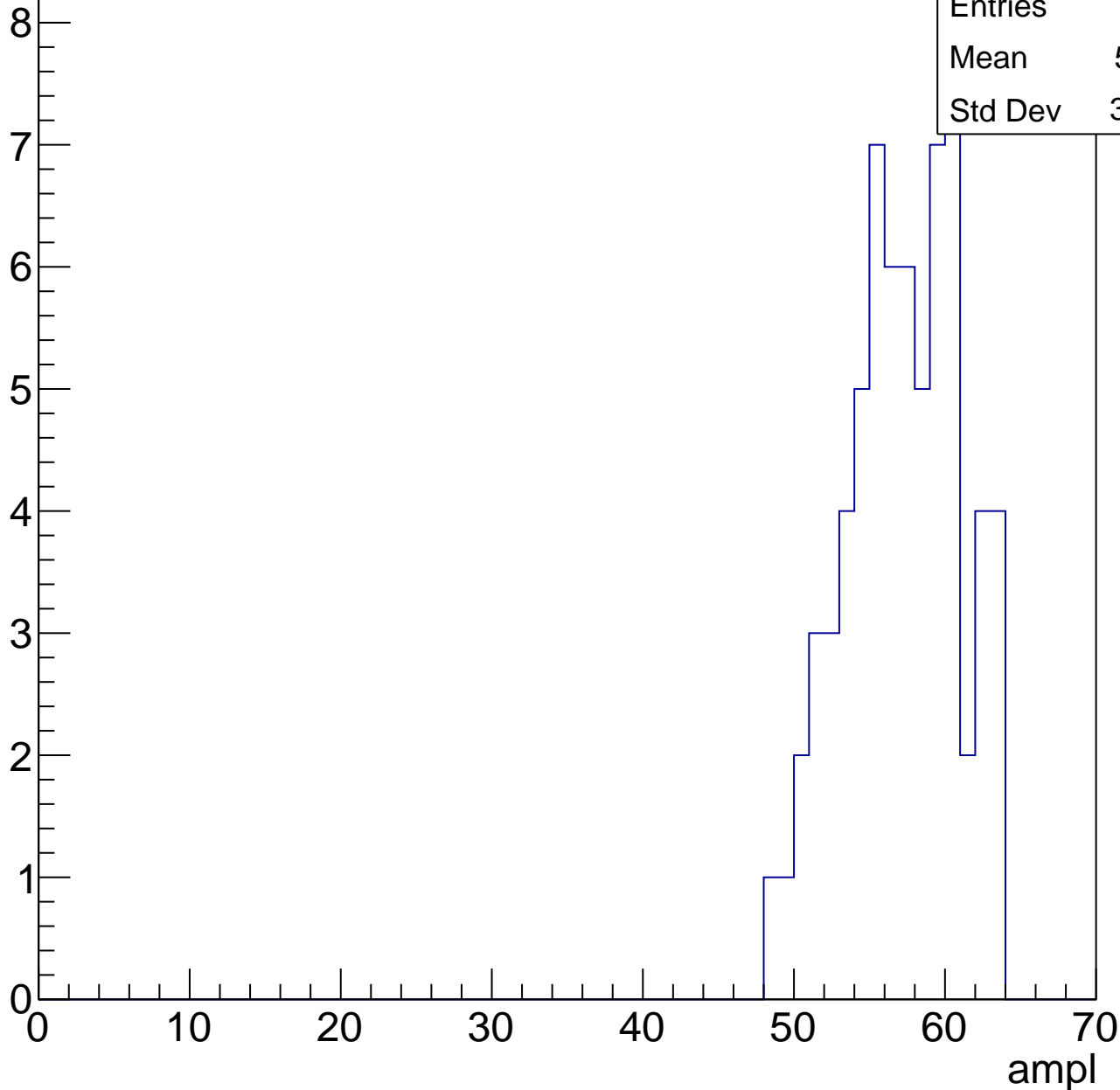


B0L002S, U17-ch93, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	56.71
Std Dev	3.726

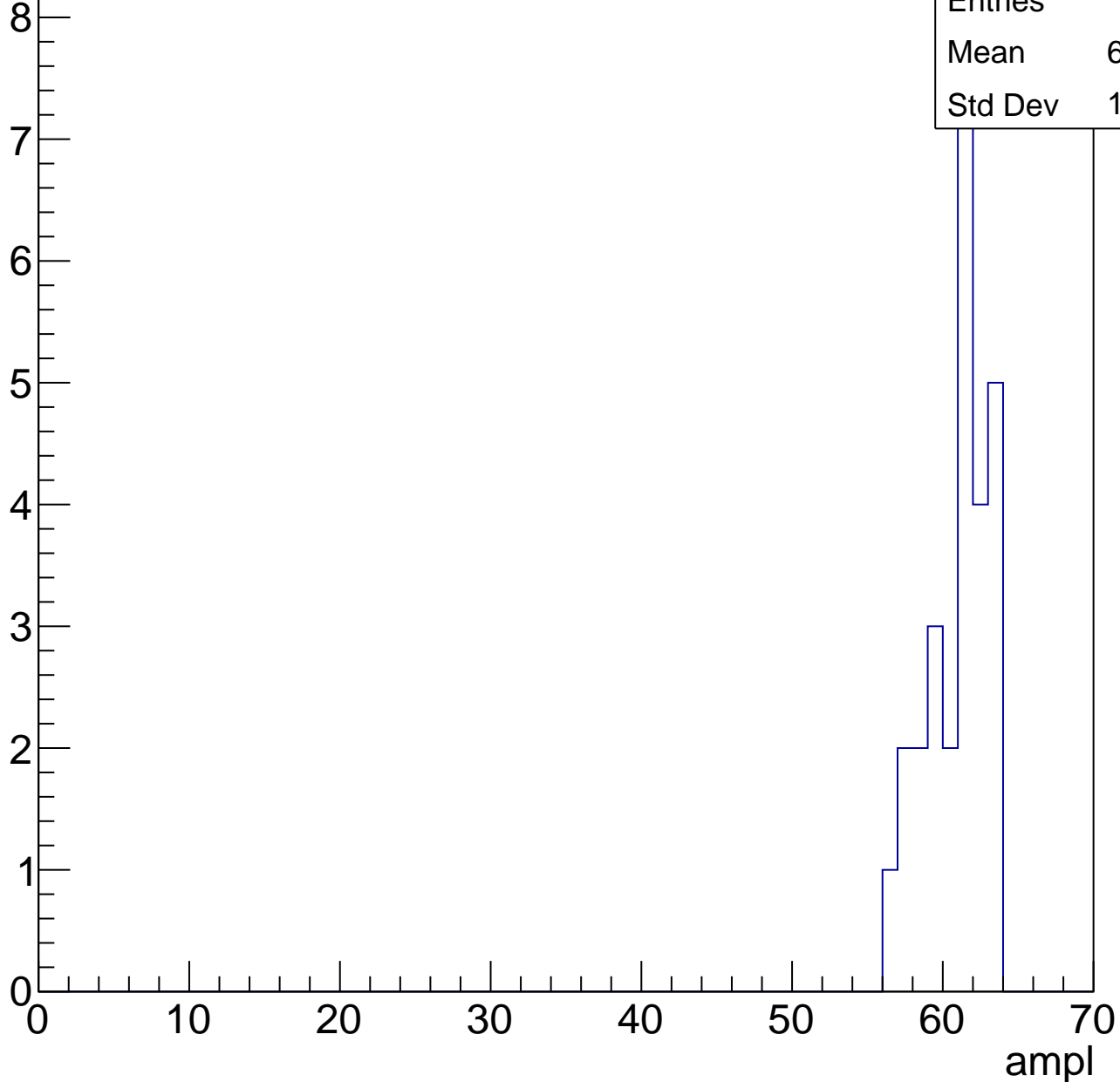


B0L002S, U17-ch93, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

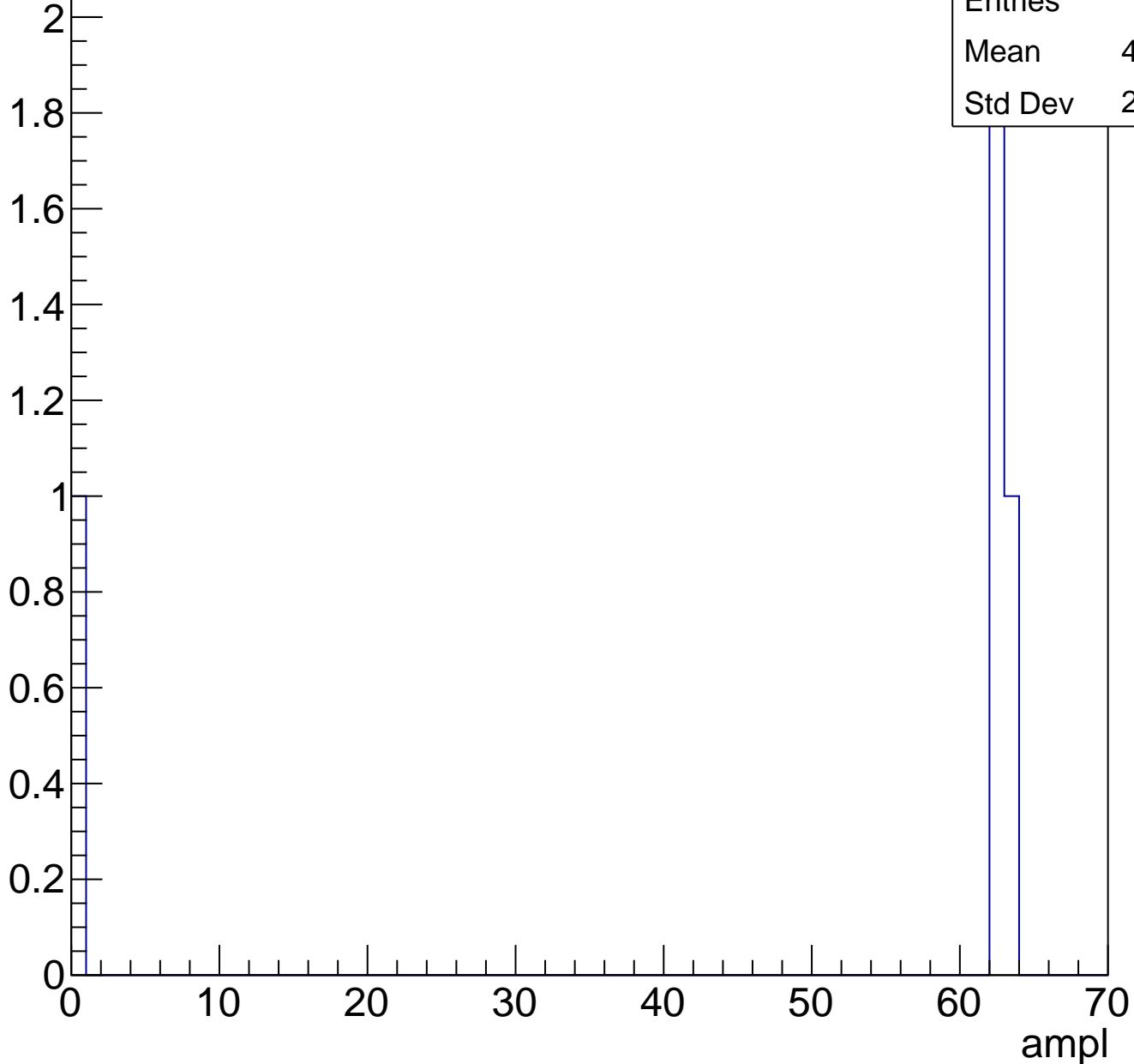
Entries	27
Mean	60.52
Std Dev	1.988



B0L002S, U17-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch94, adc0

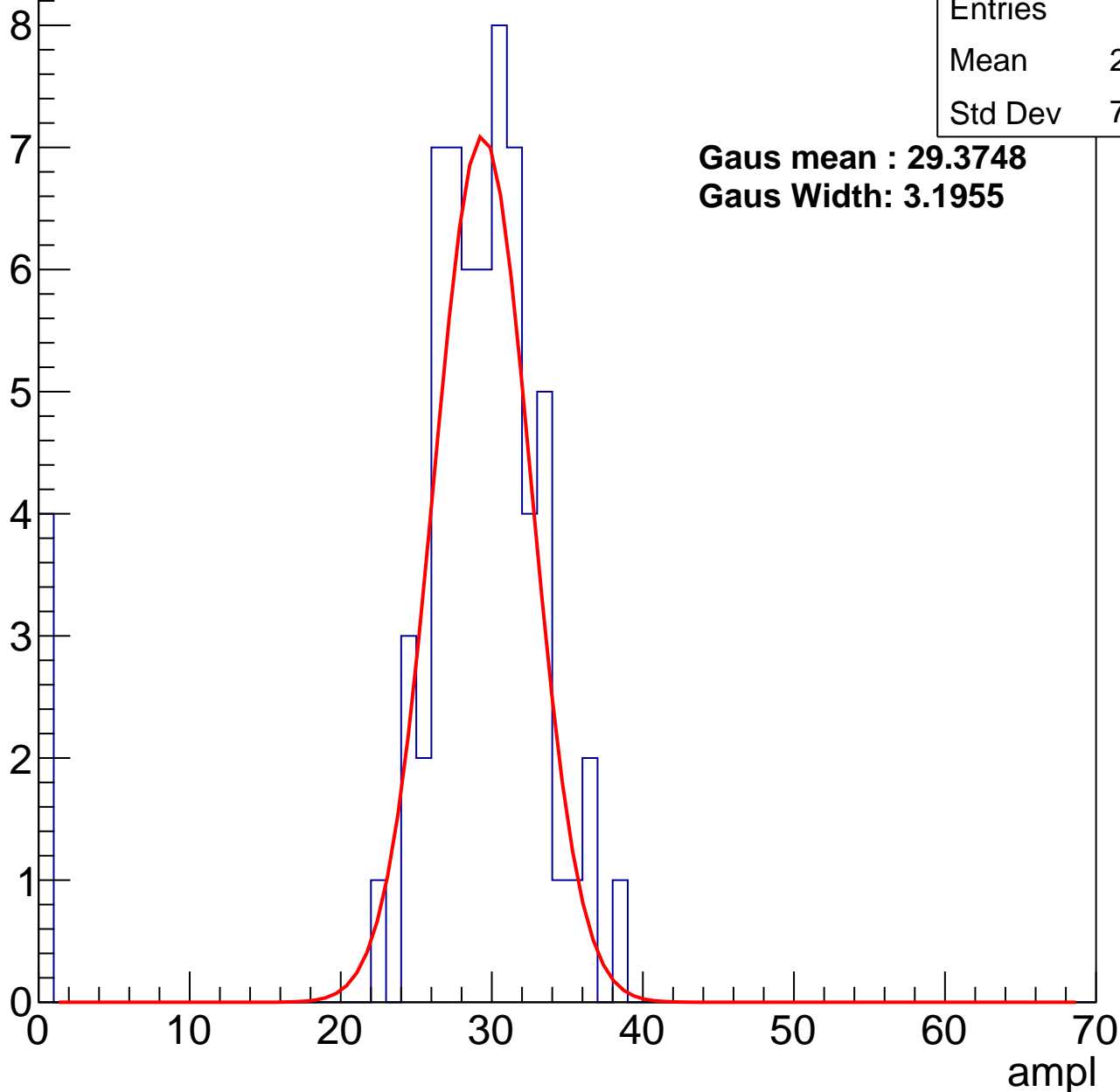
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	27.48
Std Dev	7.704

Gaus mean : 29.3748

Gaus Width: 3.1955



B0L002S, U17-ch94, adc1

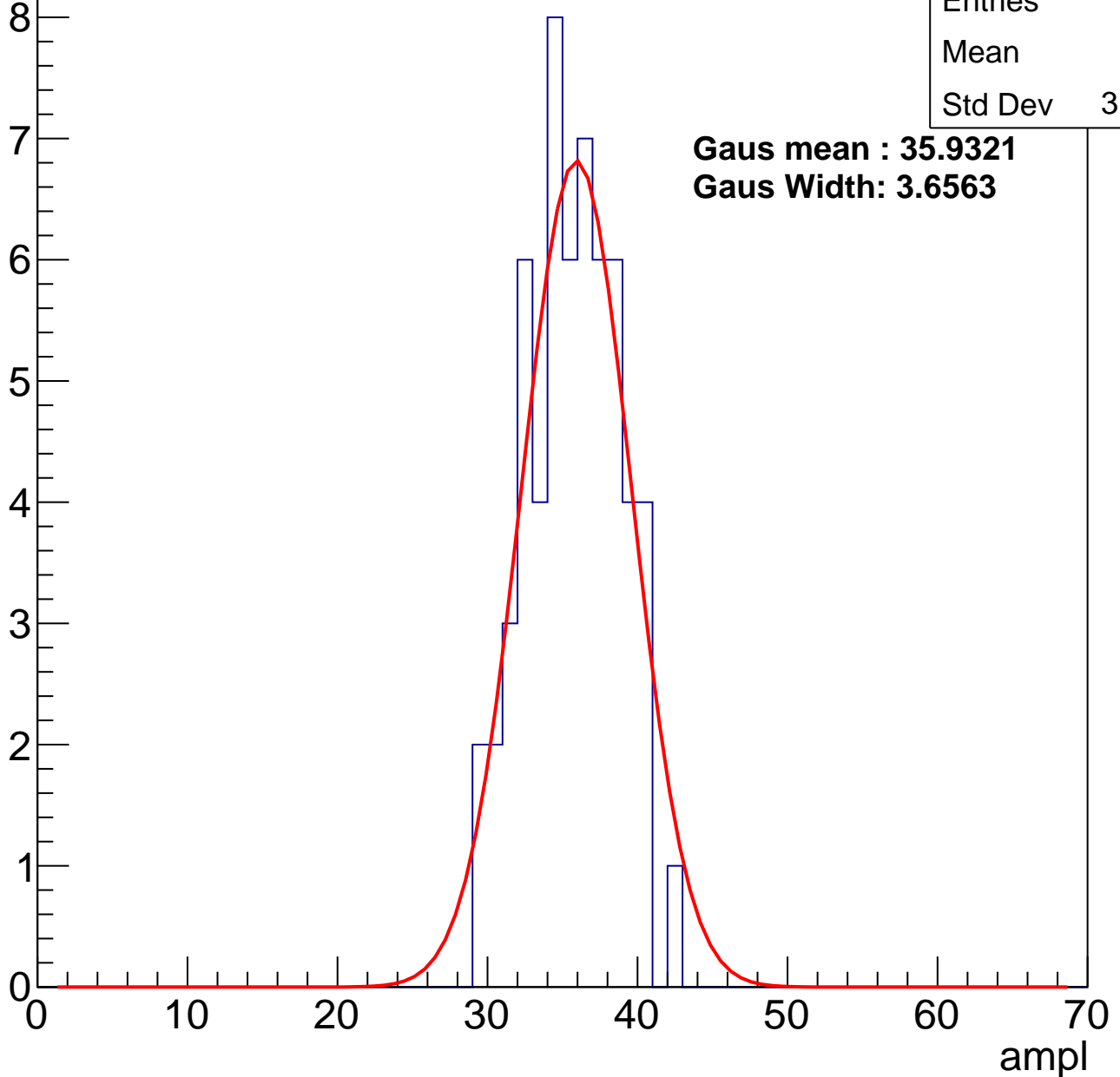
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	35.2
Std Dev	3.035

Gaus mean : 35.9321

Gaus Width: 3.6563



B0L002S, U17-ch94, adc2

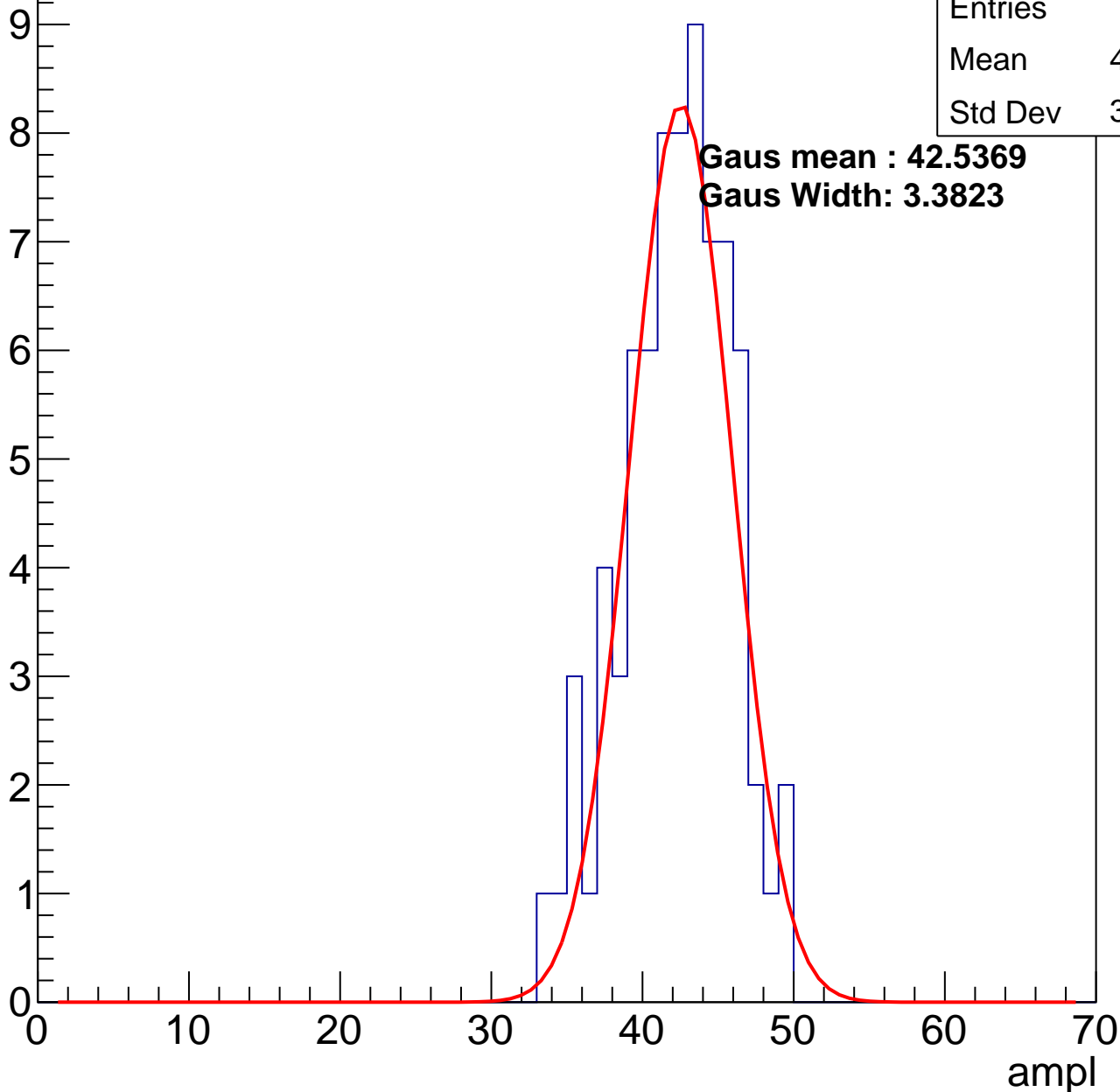
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	41.79
Std Dev	3.538

Gaus mean : 42.5369

Gaus Width: 3.3823

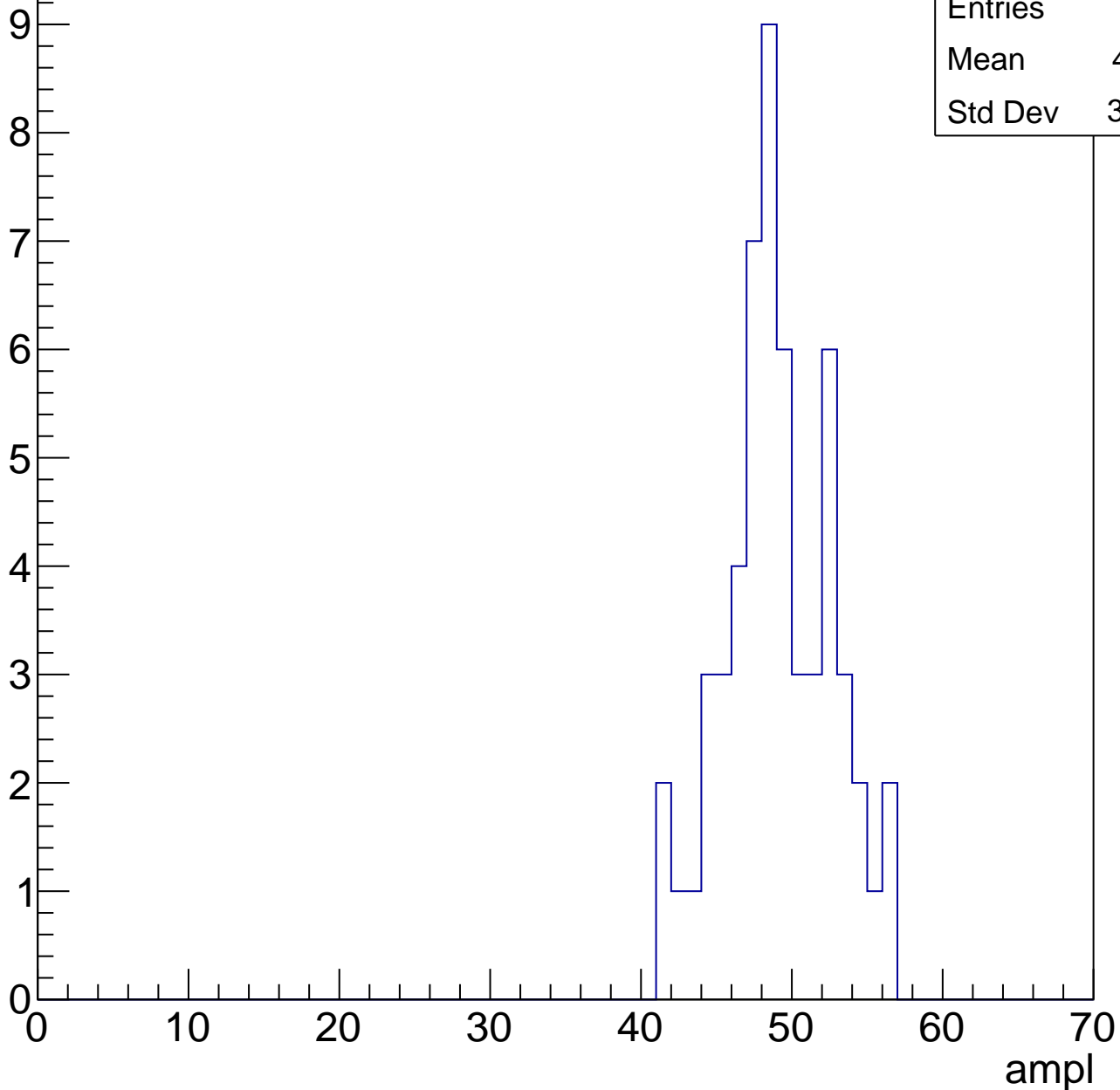


B0L002S, U17-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	48.61
Std Dev	3.539

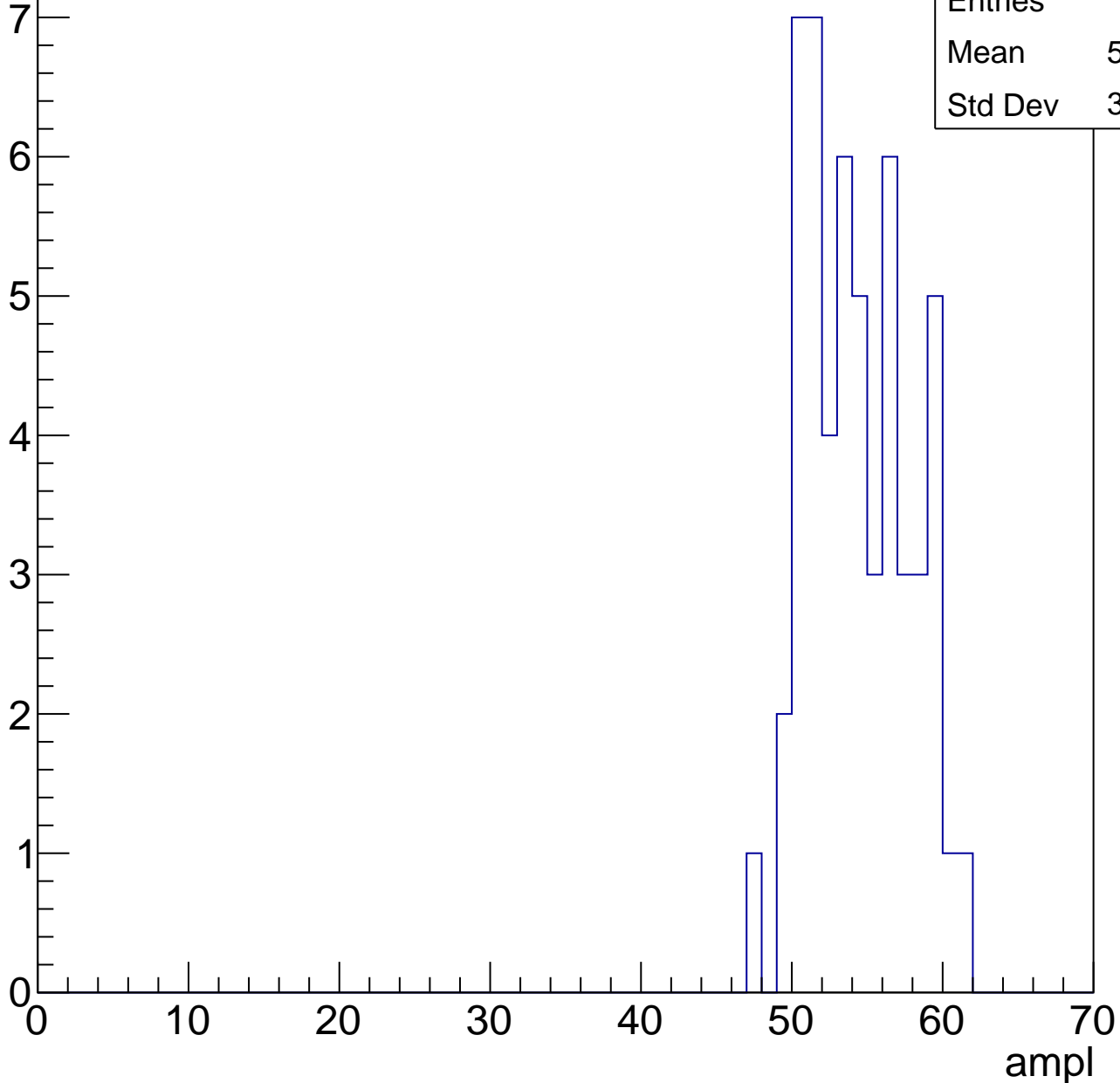


B0L002S, U17-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	53.89
Std Dev	3.354

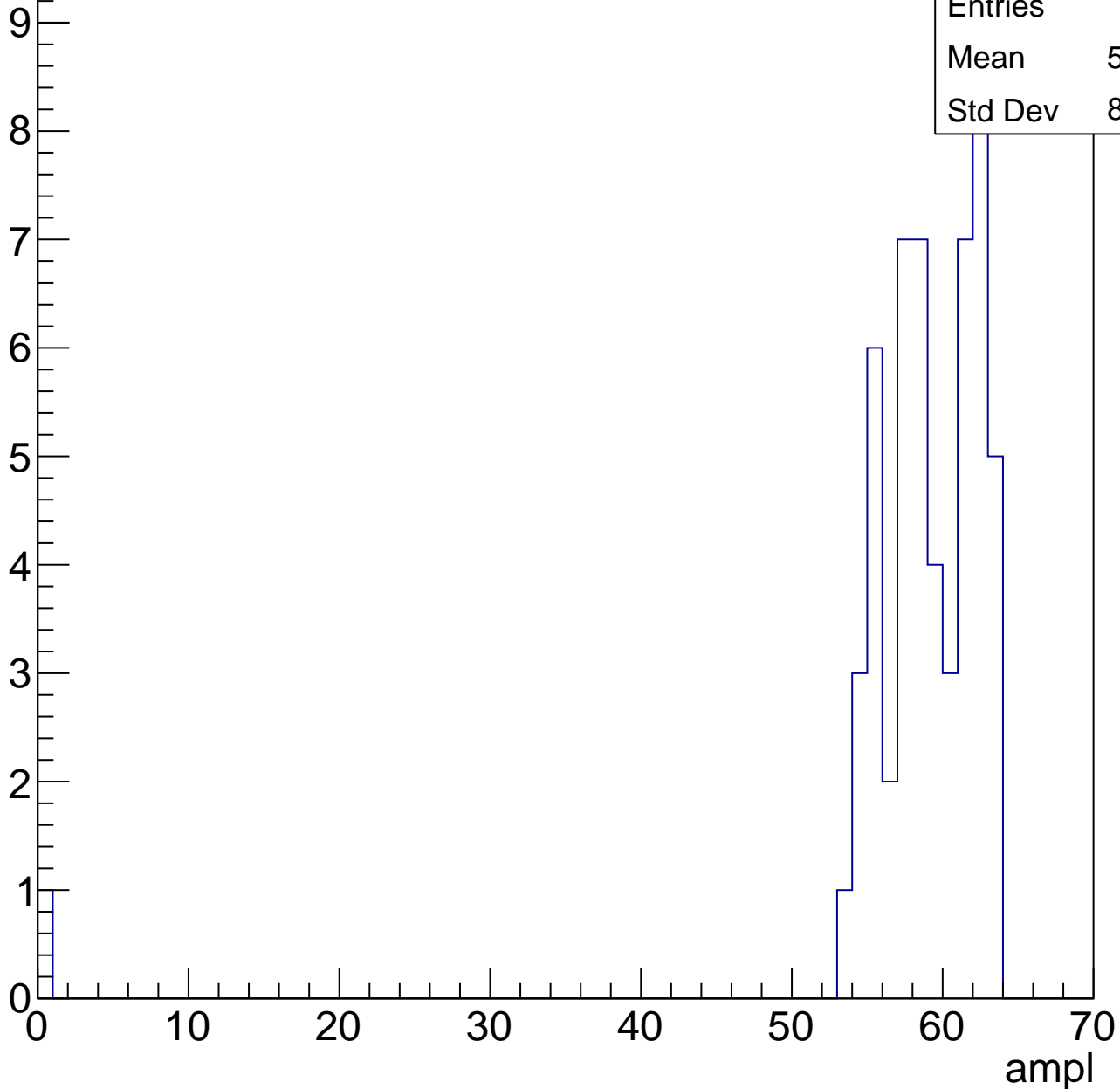


B0L002S, U17-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

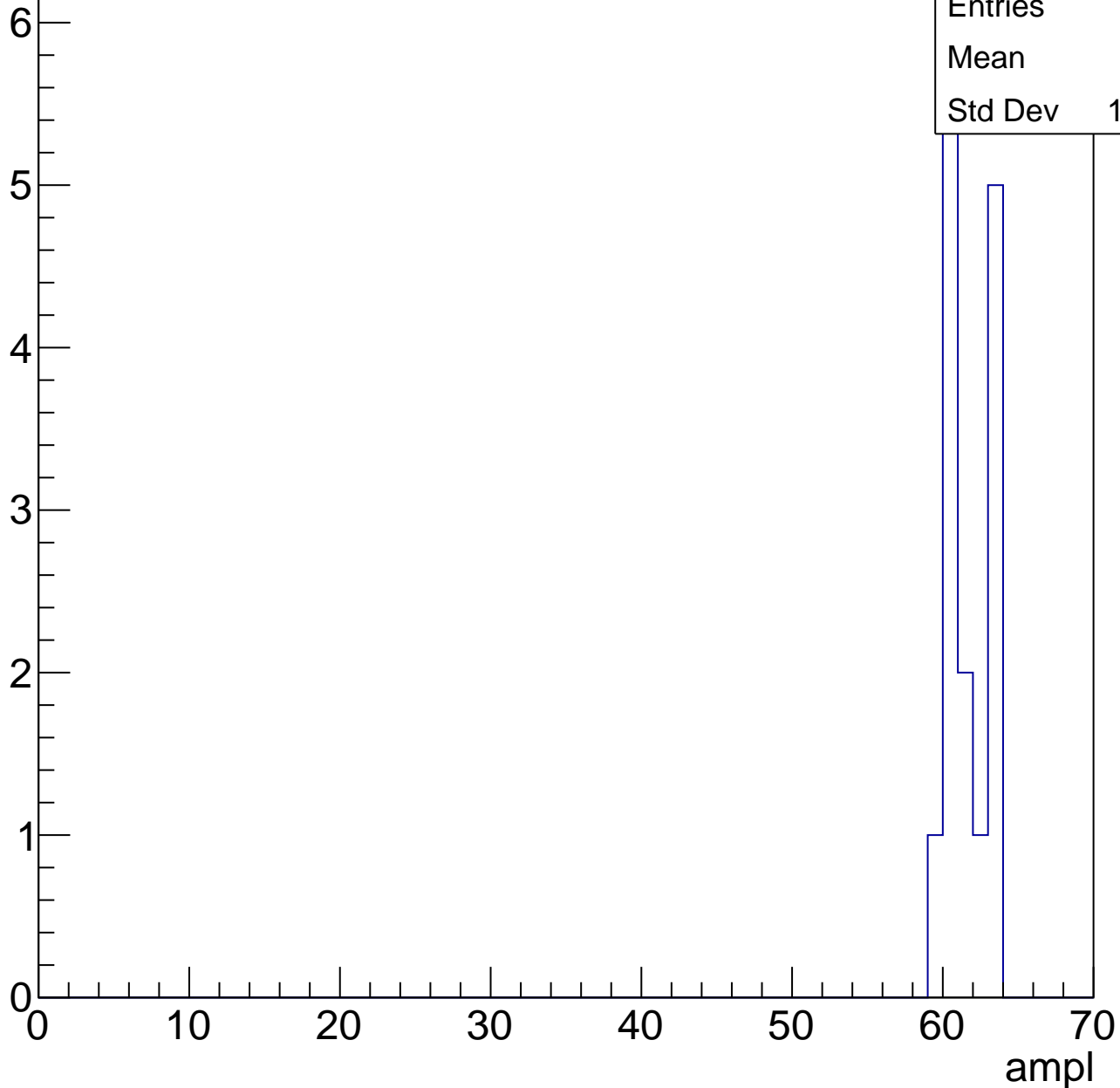
Entries	55
Mean	57.78
Std Dev	8.368



B0L002S, U17-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch95, adc0

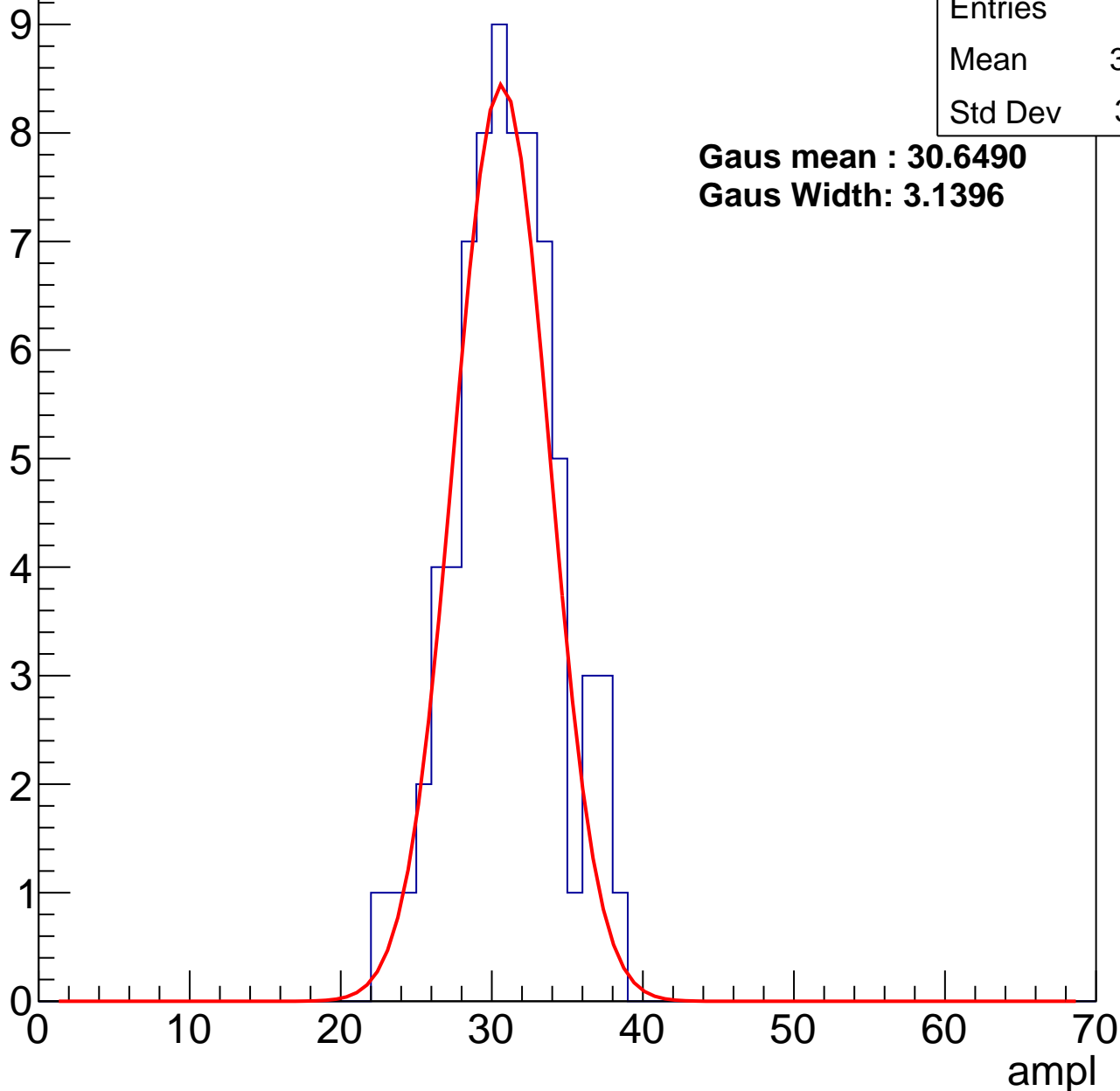
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.49
Std Dev	3.401

Gaus mean : 30.6490

Gaus Width: 3.1396



B0L002S, U17-ch95, adc1

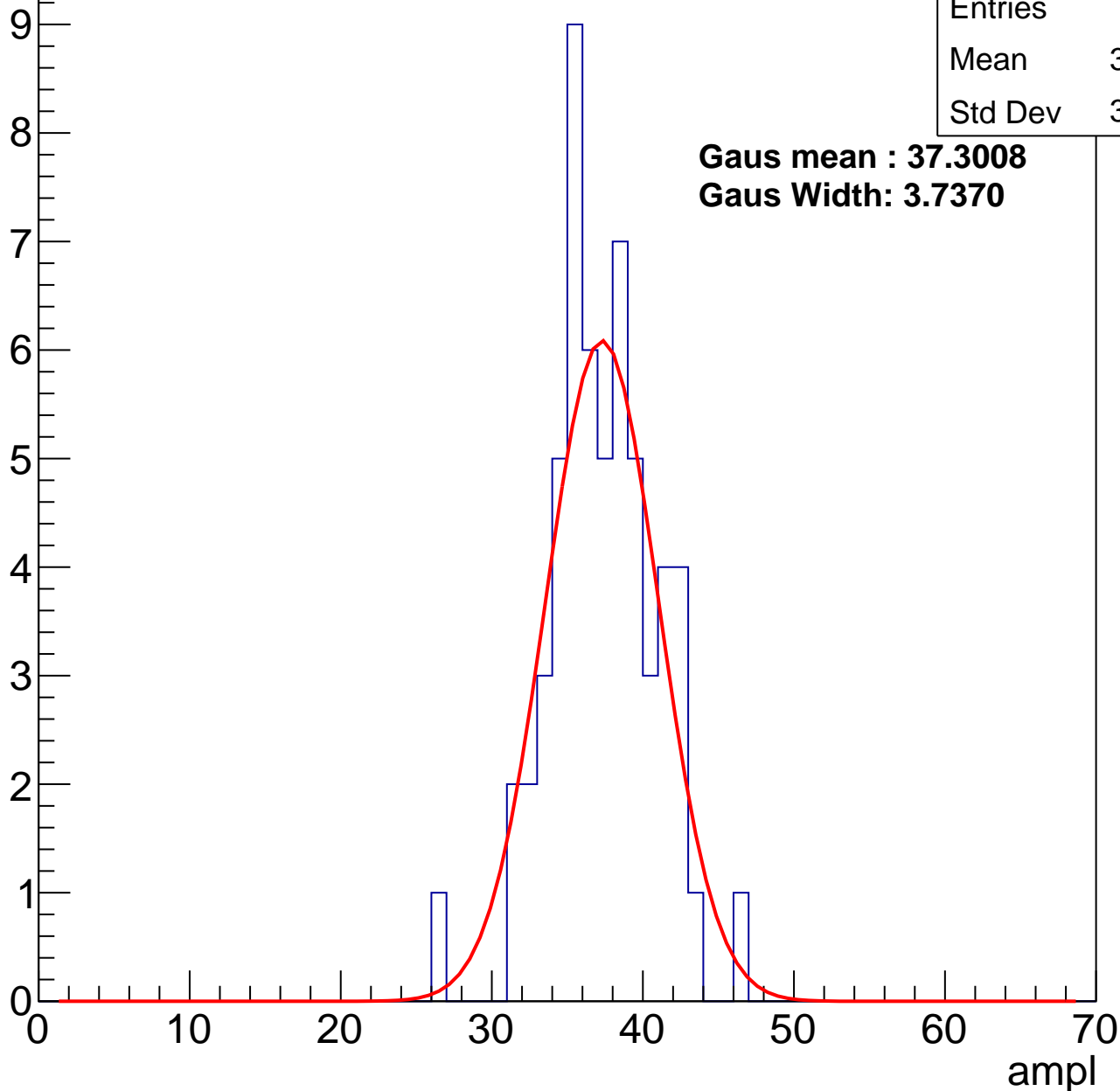
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	36.88
Std Dev	3.504

Gaus mean : 37.3008

Gaus Width: 3.7370



B0L002S, U17-ch95, adc2

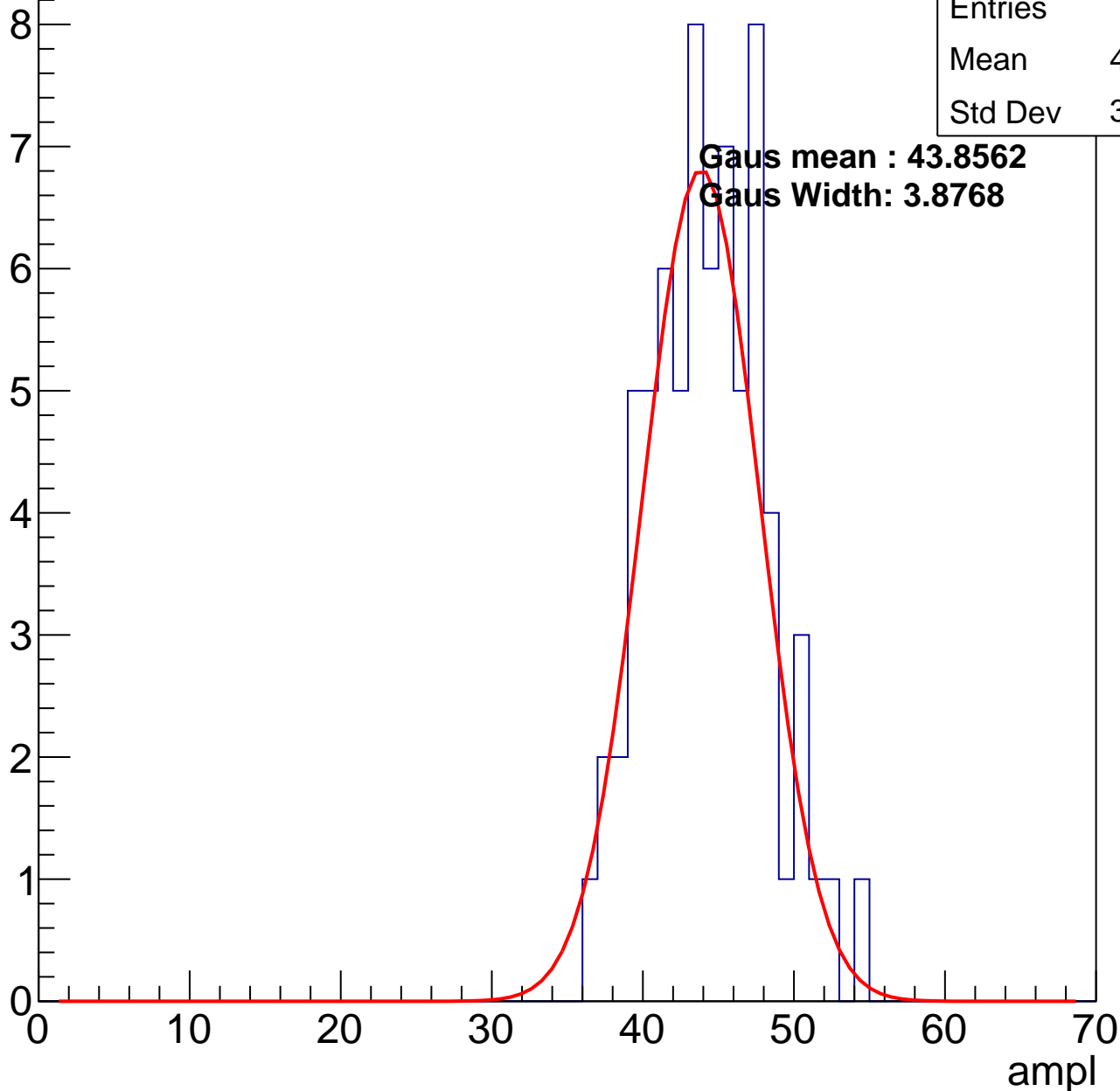
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.86
Std Dev	3.806

Gaus mean : 43.8562

Gaus Width: 3.8768

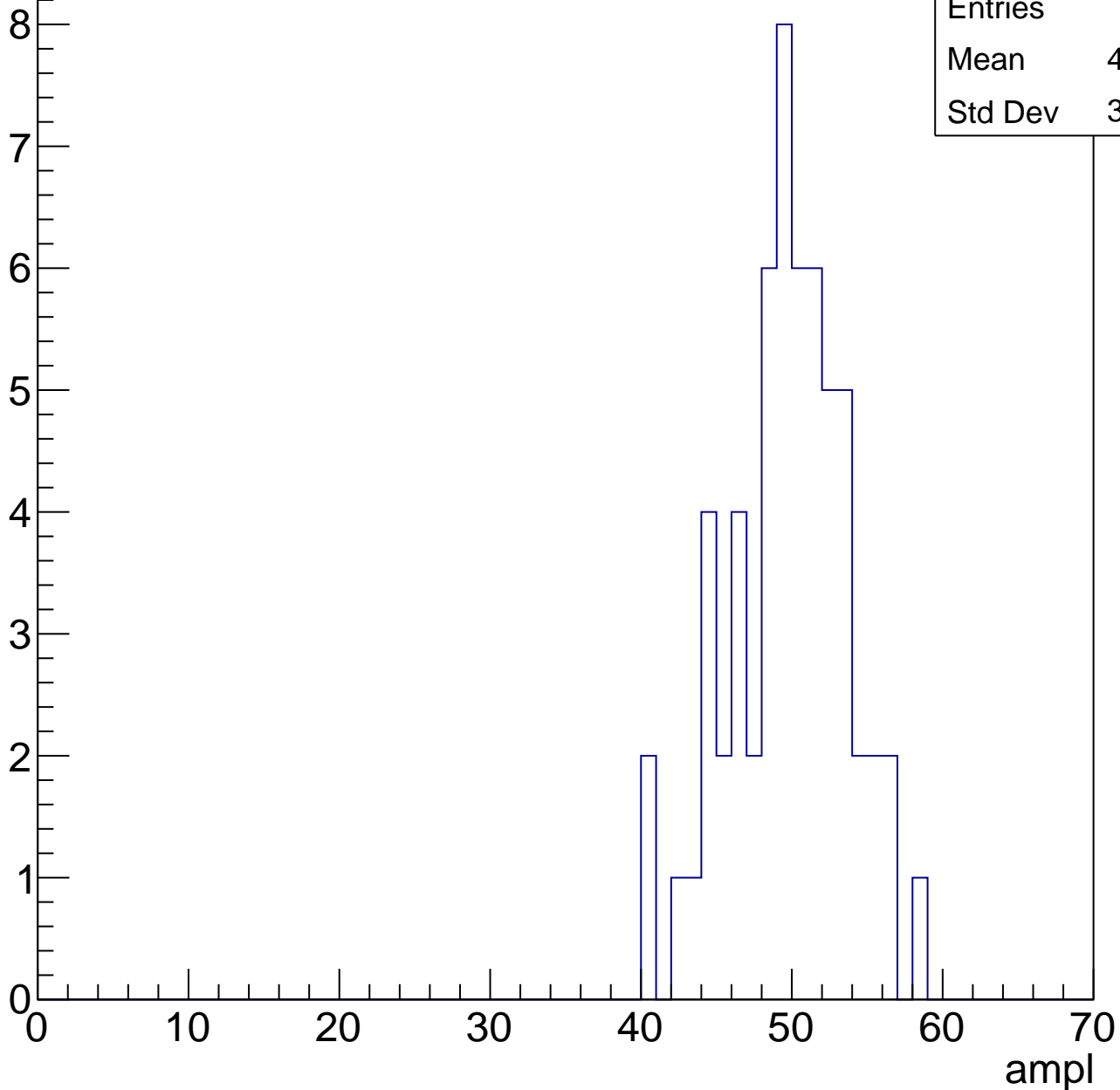


B0L002S, U17-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	49.29
Std Dev	3.862

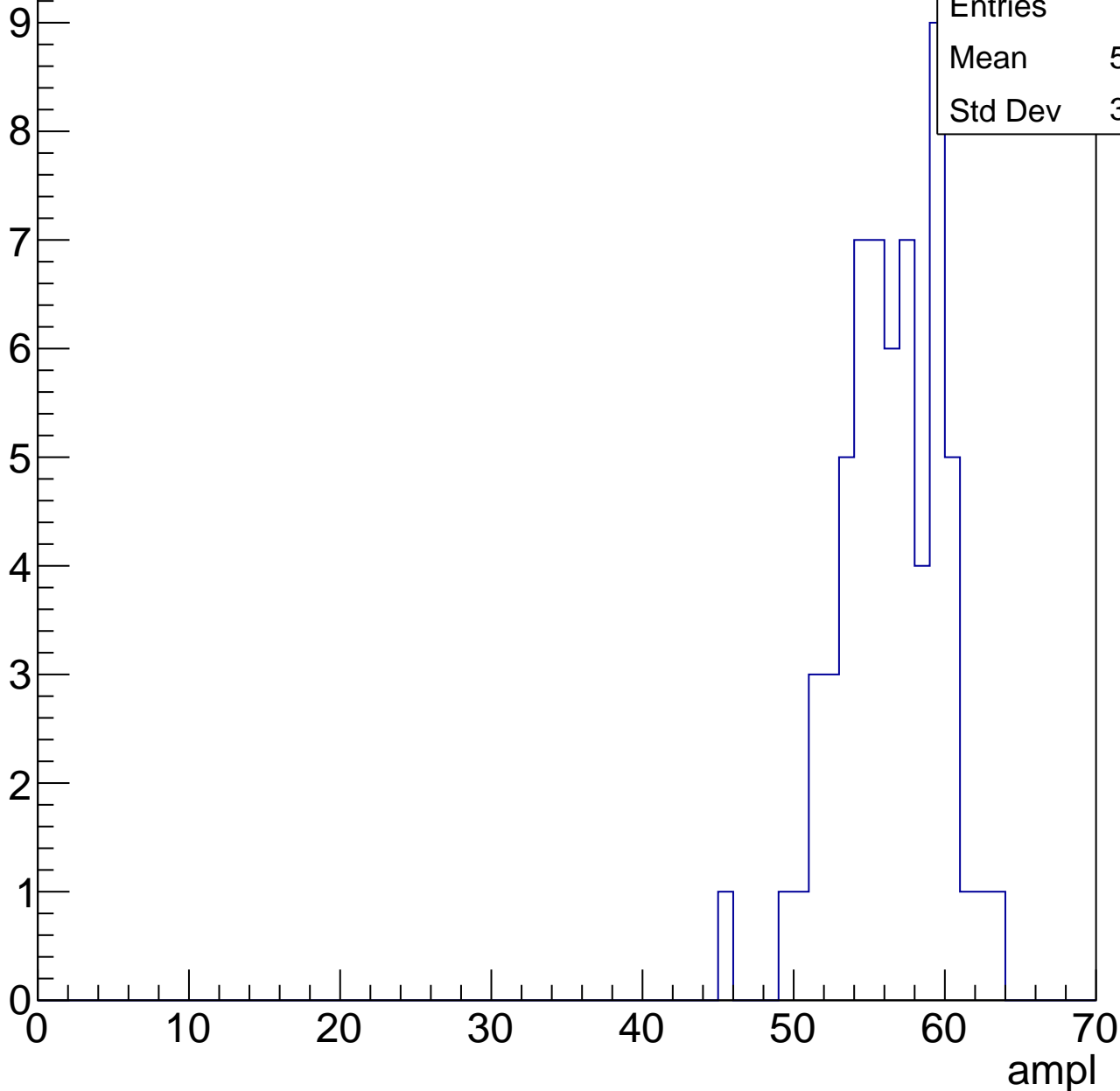


B0L002S, U17-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.89
Std Dev	3.365

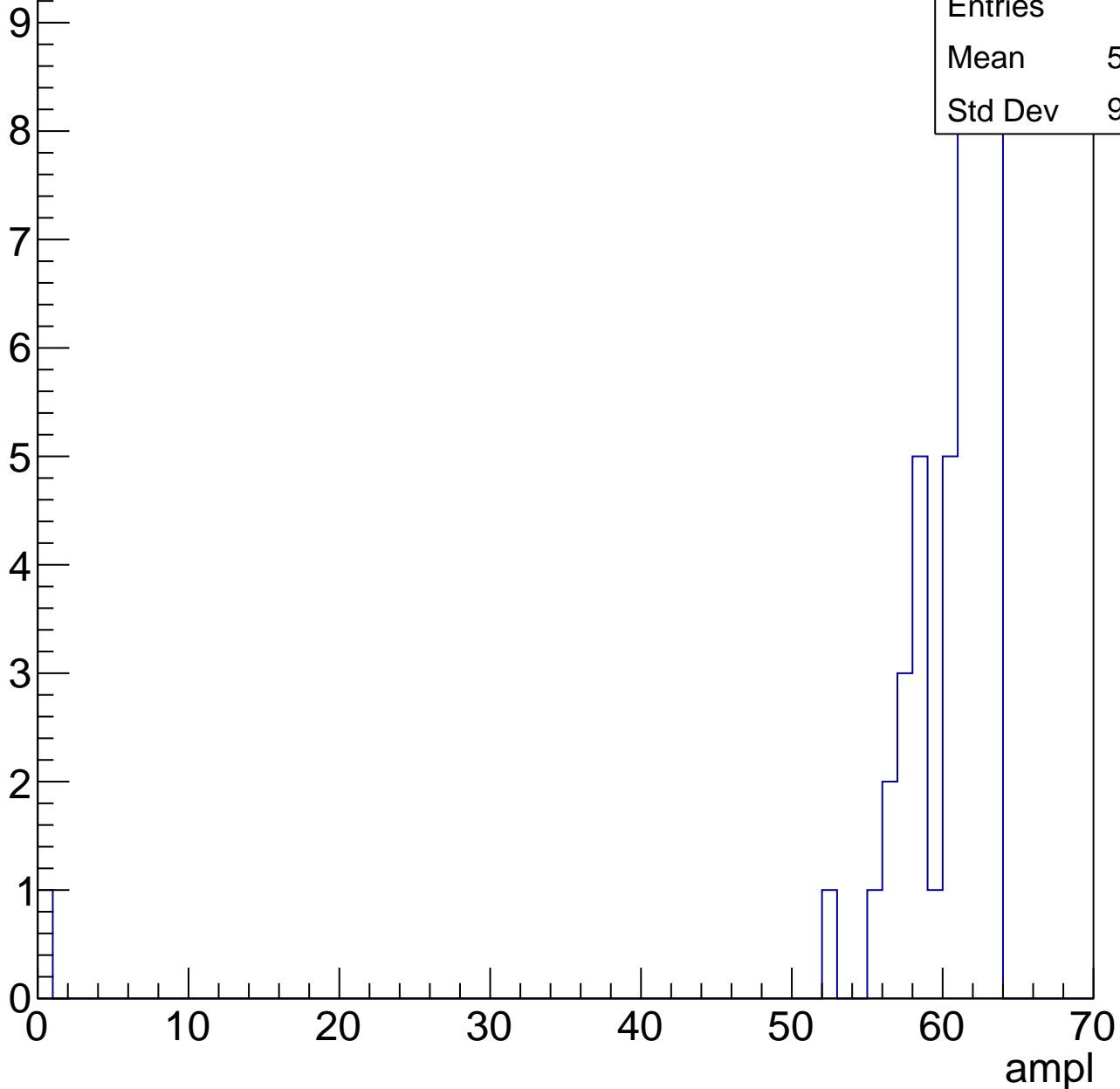


B0L002S, U17-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	58.86
Std Dev	9.334



B0L002S, U17-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch96, adc0

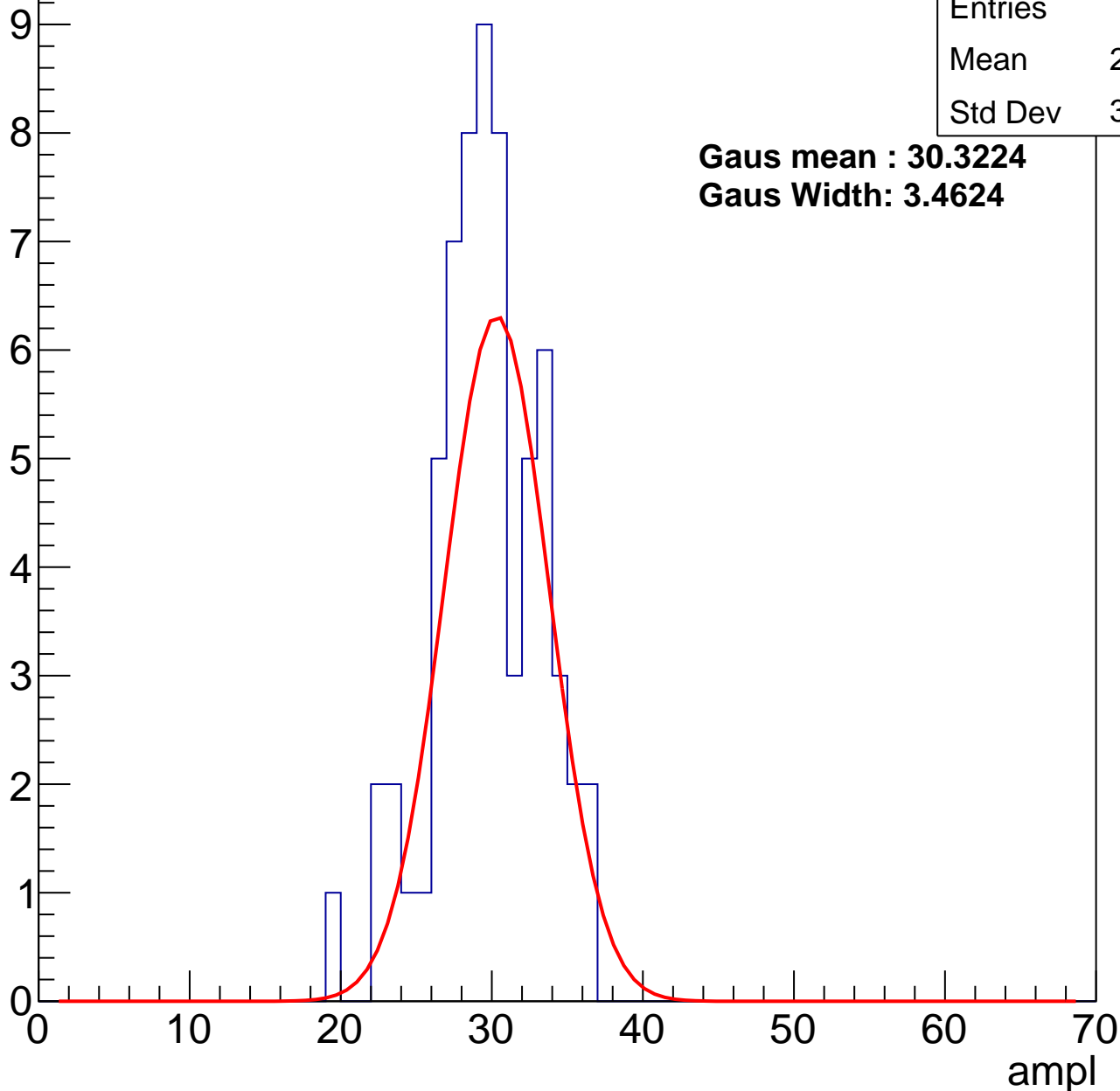
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.18
Std Dev	3.499

Gaus mean : 30.3224

Gaus Width: 3.4624



B0L002S, U17-ch96, adc1

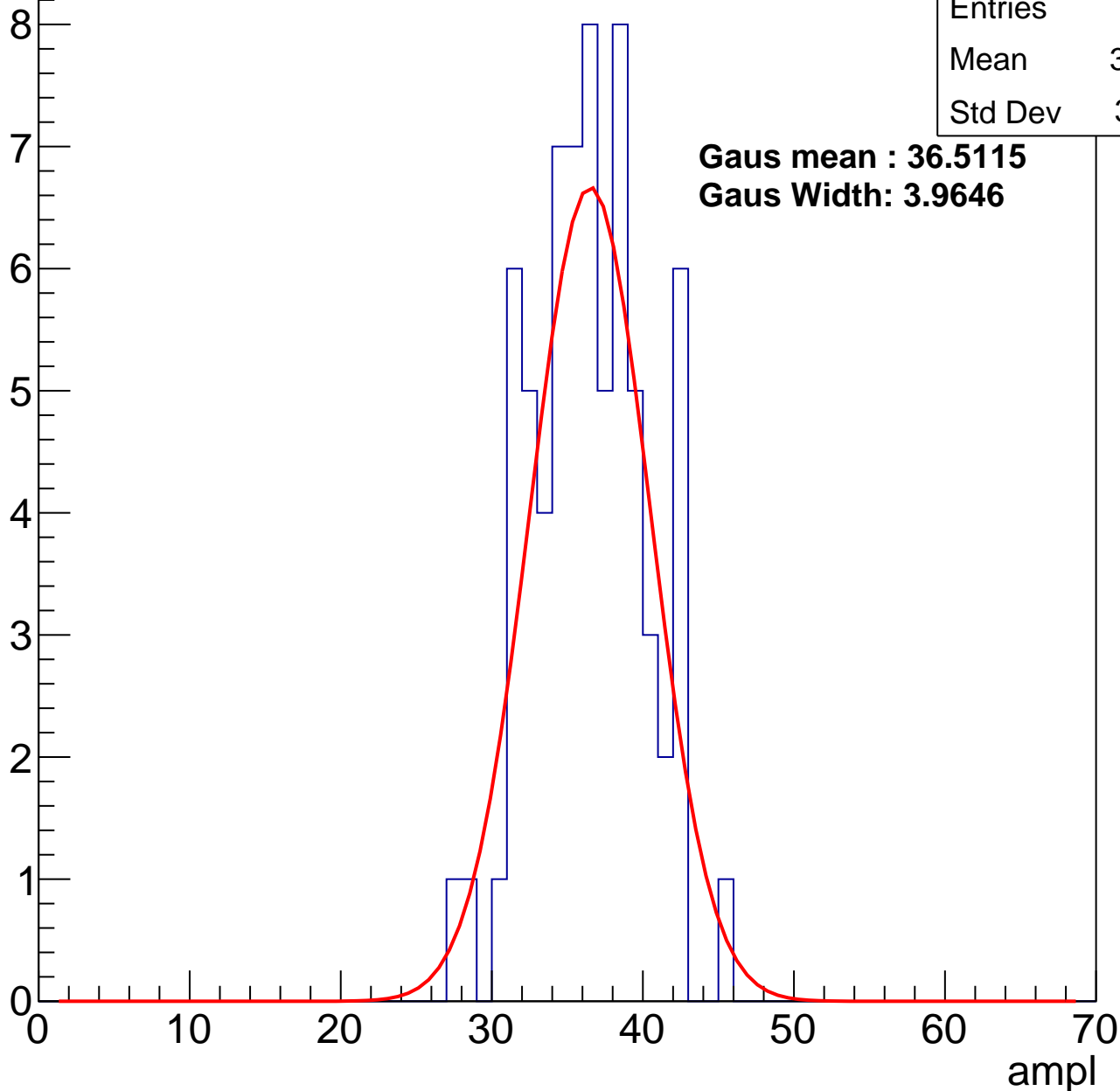
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	35.96
Std Dev	3.701

Gaus mean : 36.5115

Gaus Width: 3.9646



B0L002S, U17-ch96, adc2

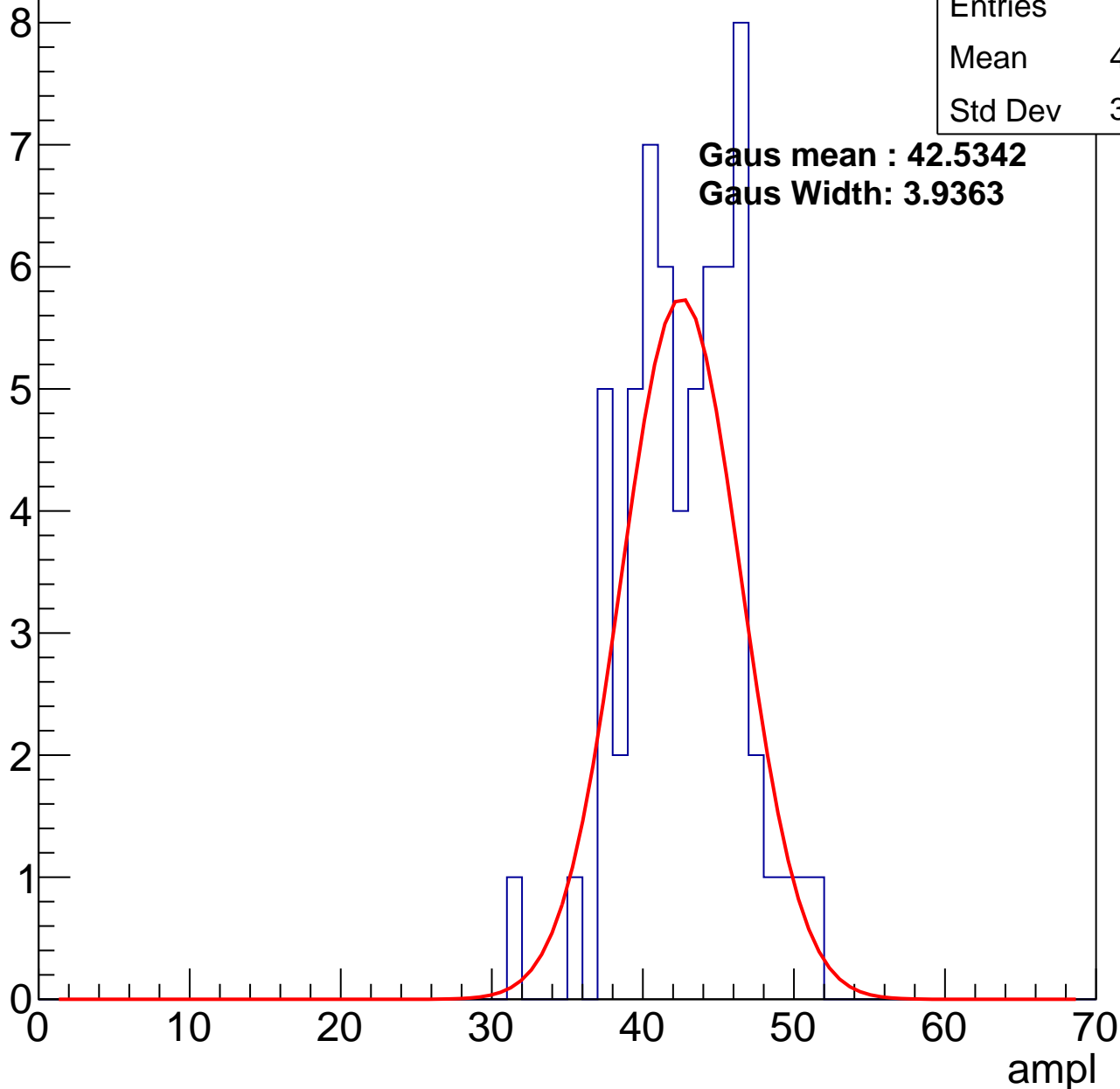
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	42.34
Std Dev	3.789

Gaus mean : 42.5342

Gaus Width: 3.9363

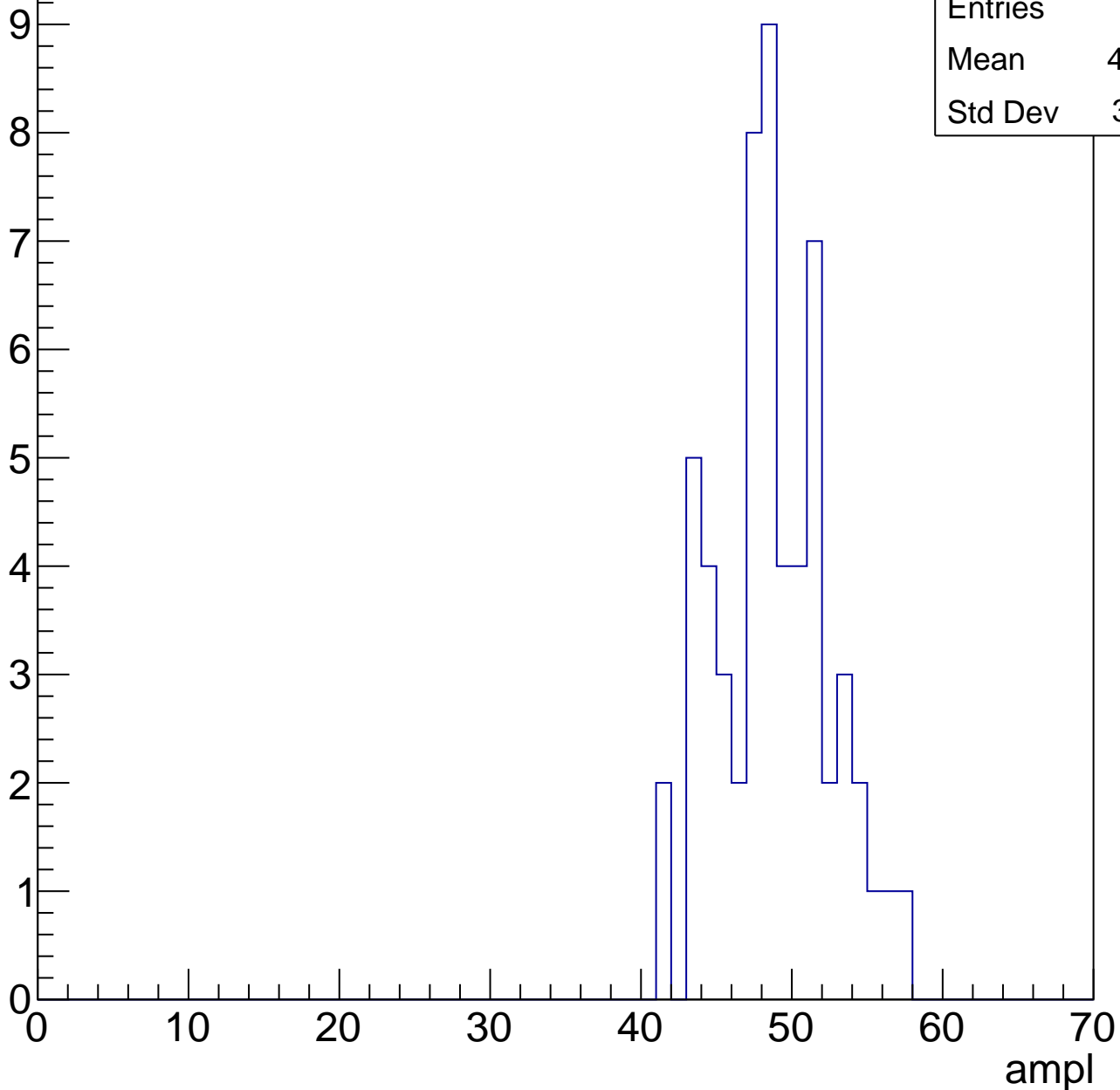


B0L002S, U17-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

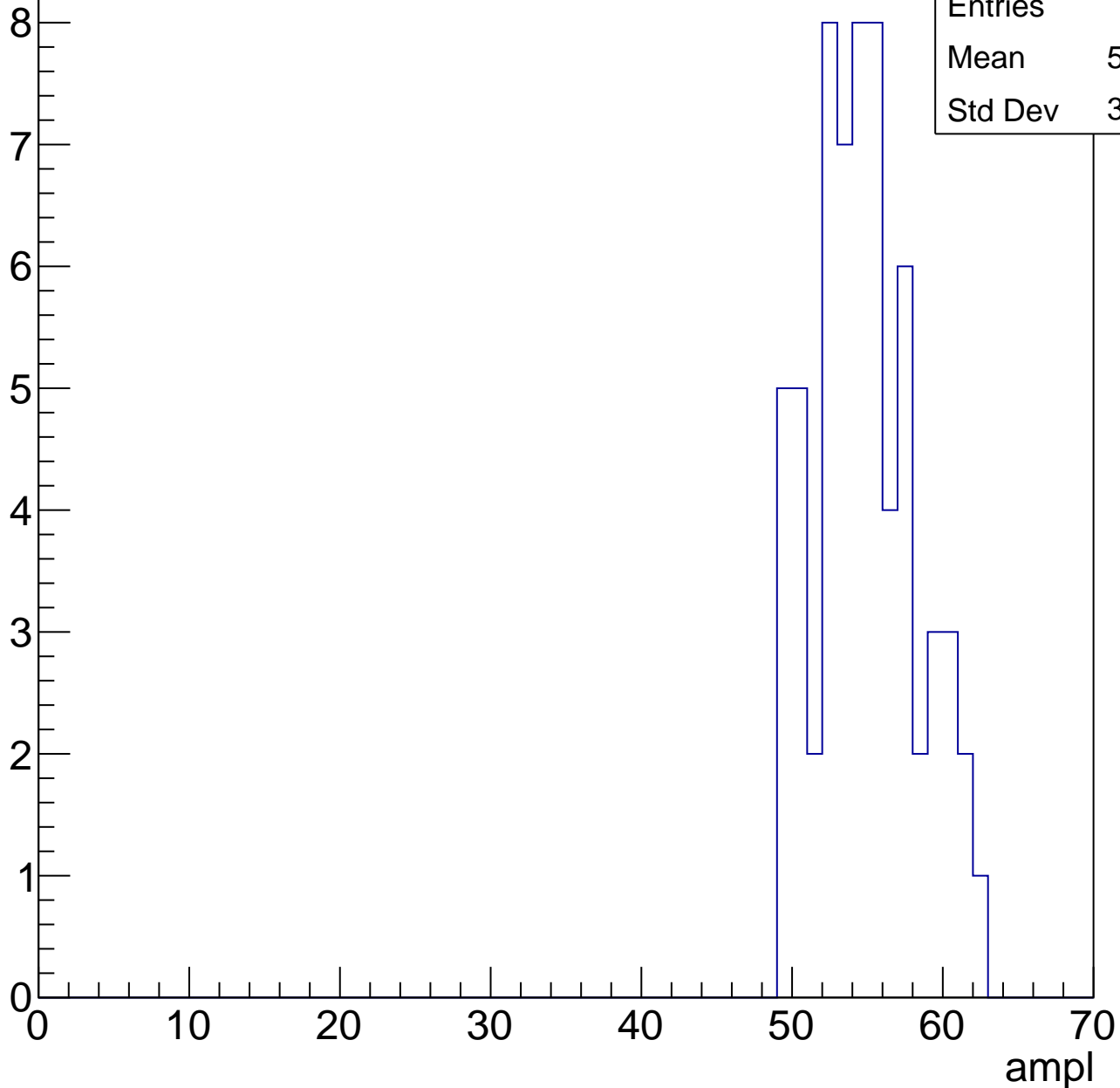
Entries	58
Mean	48.28
Std Dev	3.671



B0L002S, U17-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

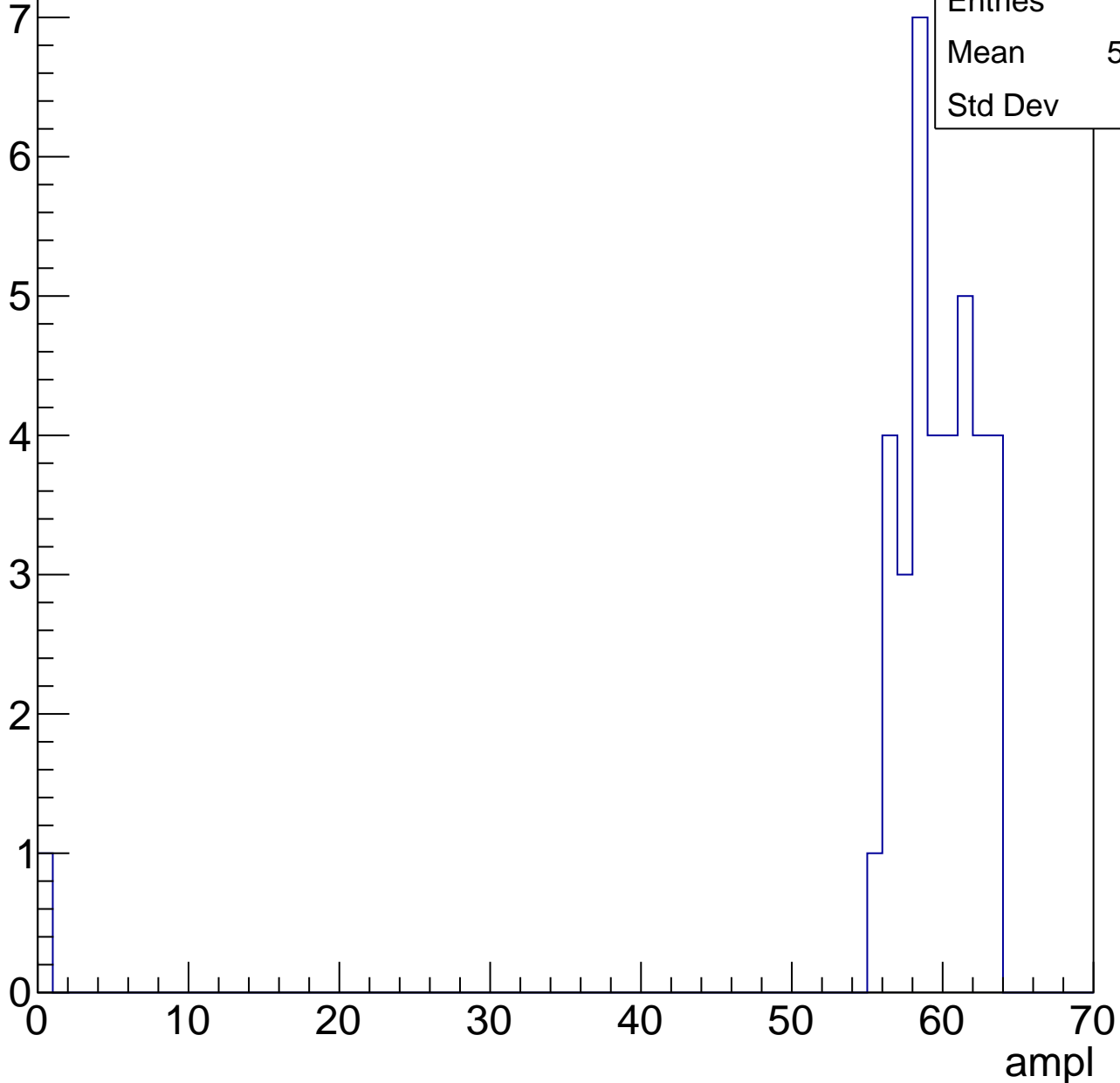


B0L002S, U17-ch96, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	57.76
Std Dev	9.89

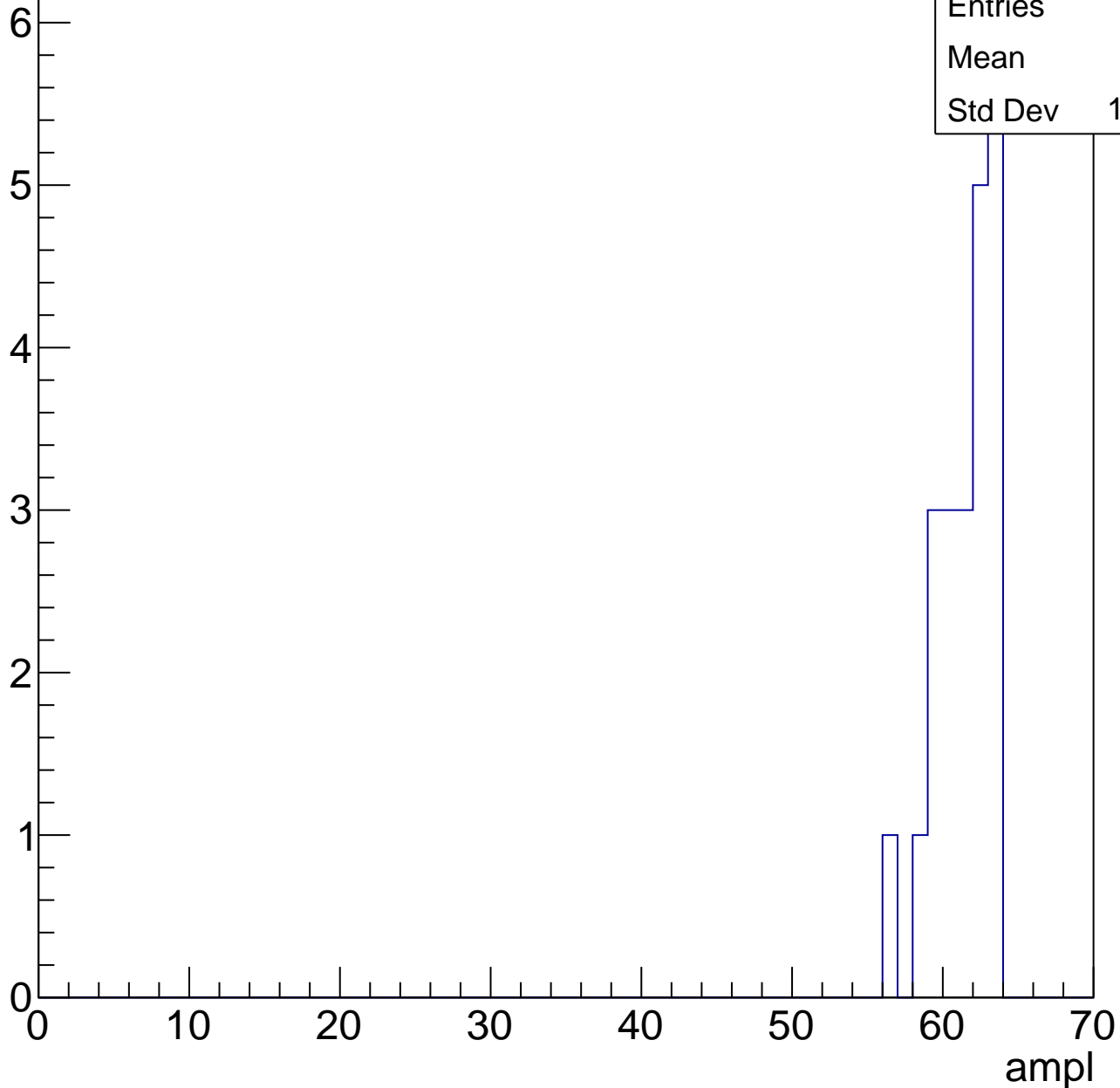


B0L002S, U17-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	61
Std Dev	1.883



B0L002S, U17-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch97, adc0

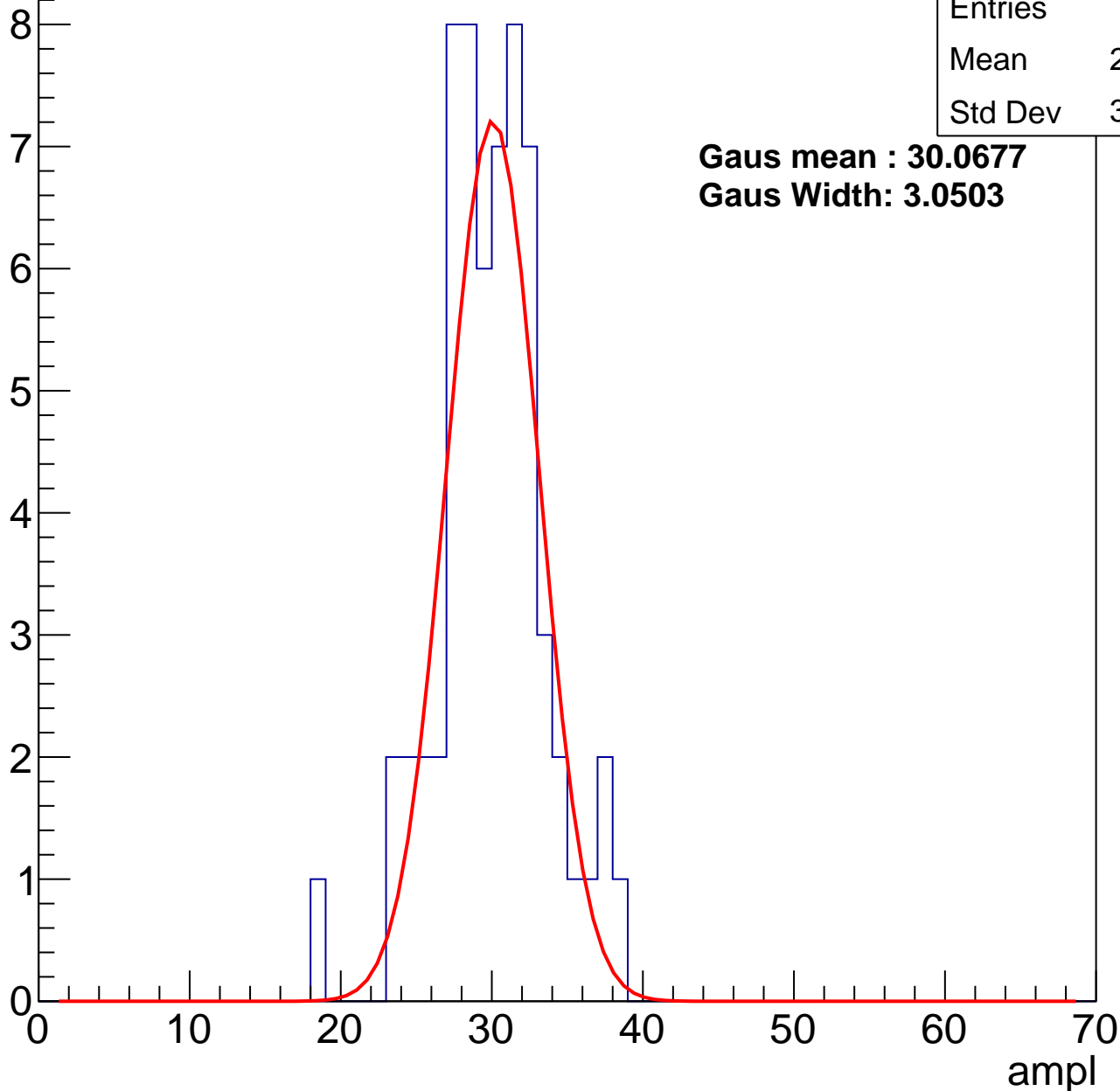
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.52
Std Dev	3.585

Gaus mean : 30.0677

Gaus Width: 3.0503



B0L002S, U17-ch97, adc1

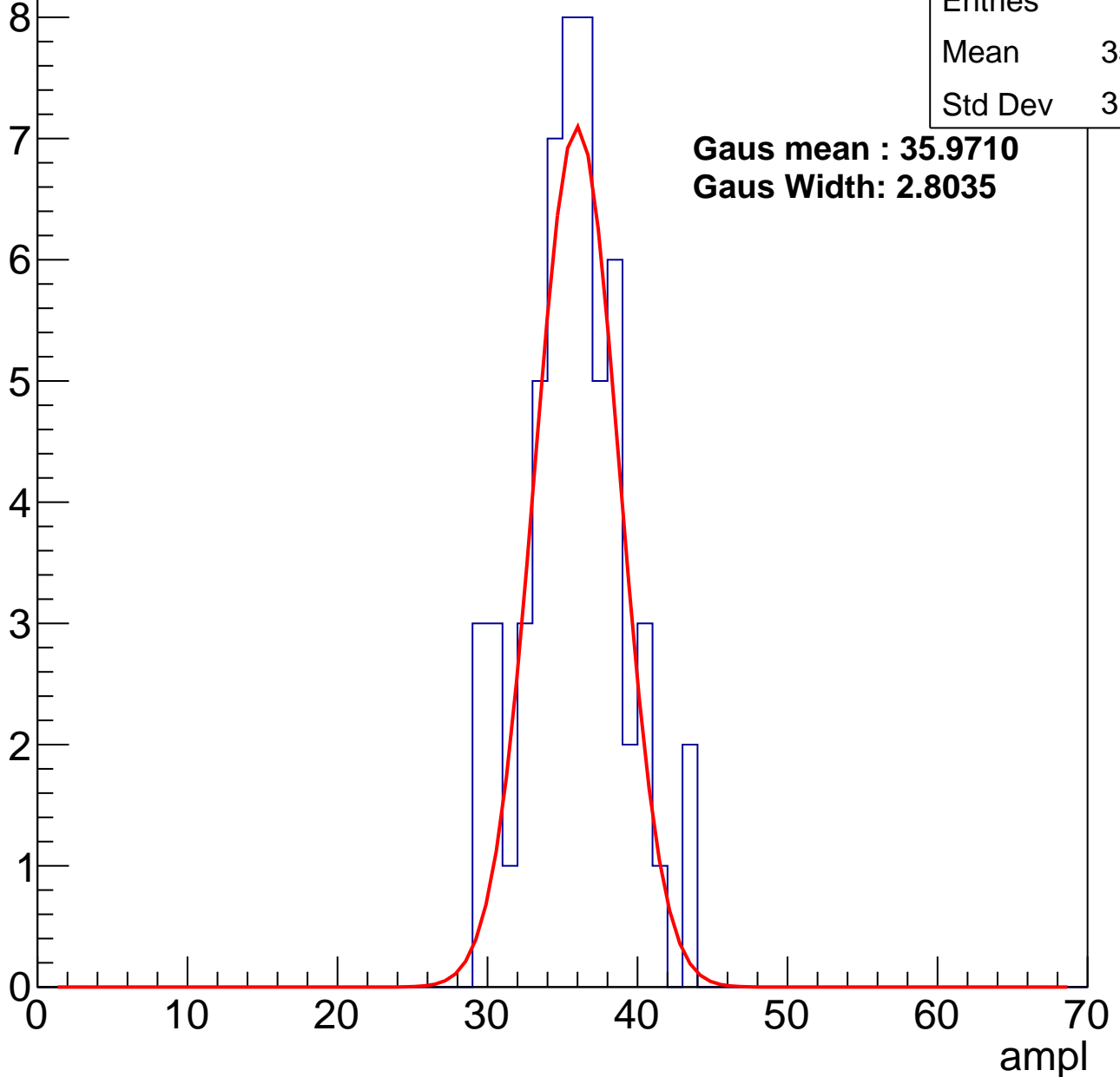
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	35.32
Std Dev	3.245

Gaus mean : 35.9710

Gaus Width: 2.8035



B0L002S, U17-ch97, adc2

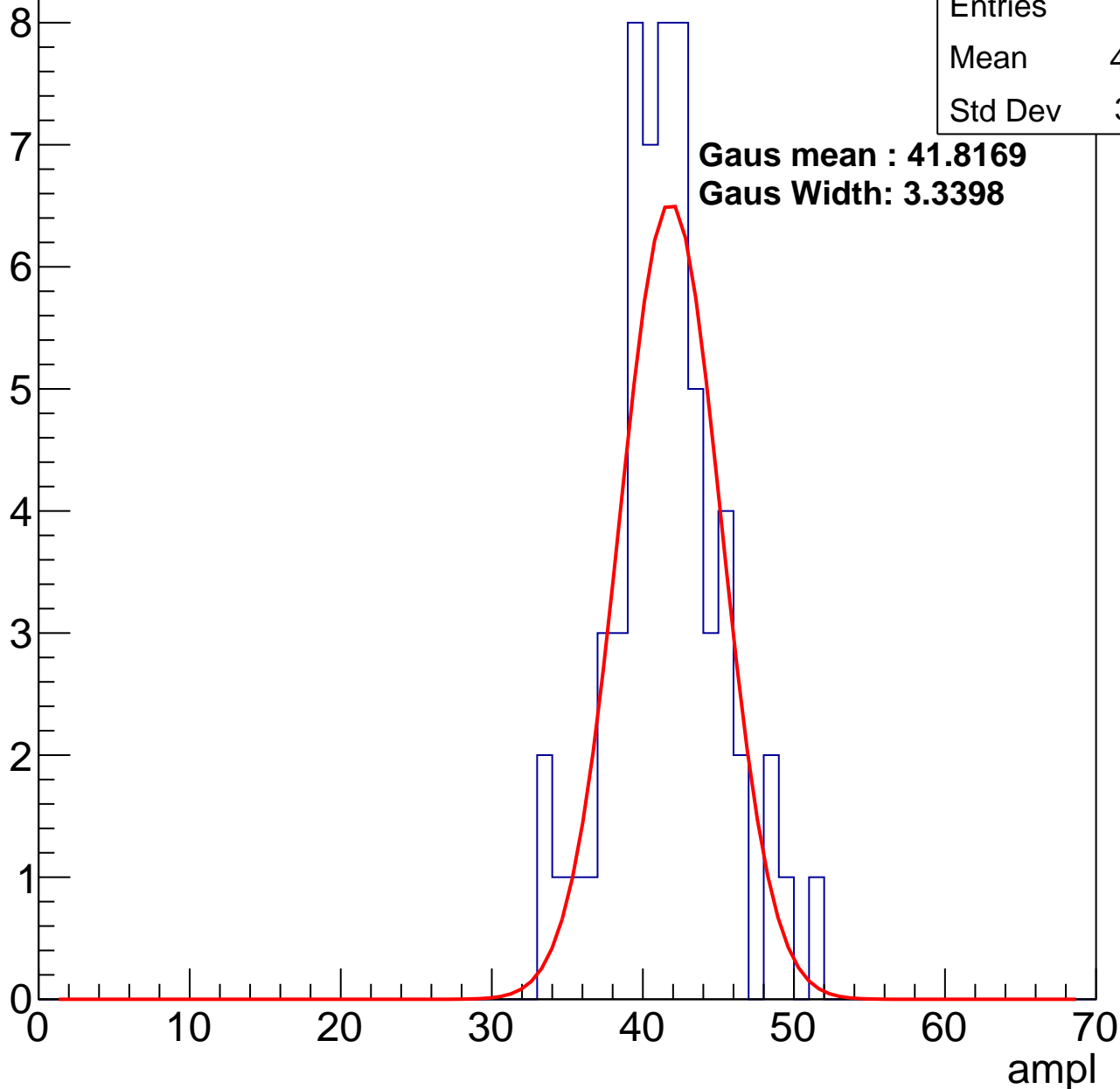
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	41.12
Std Dev	3.611

Gaus mean : 41.8169

Gaus Width: 3.3398



B0L002S, U17-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

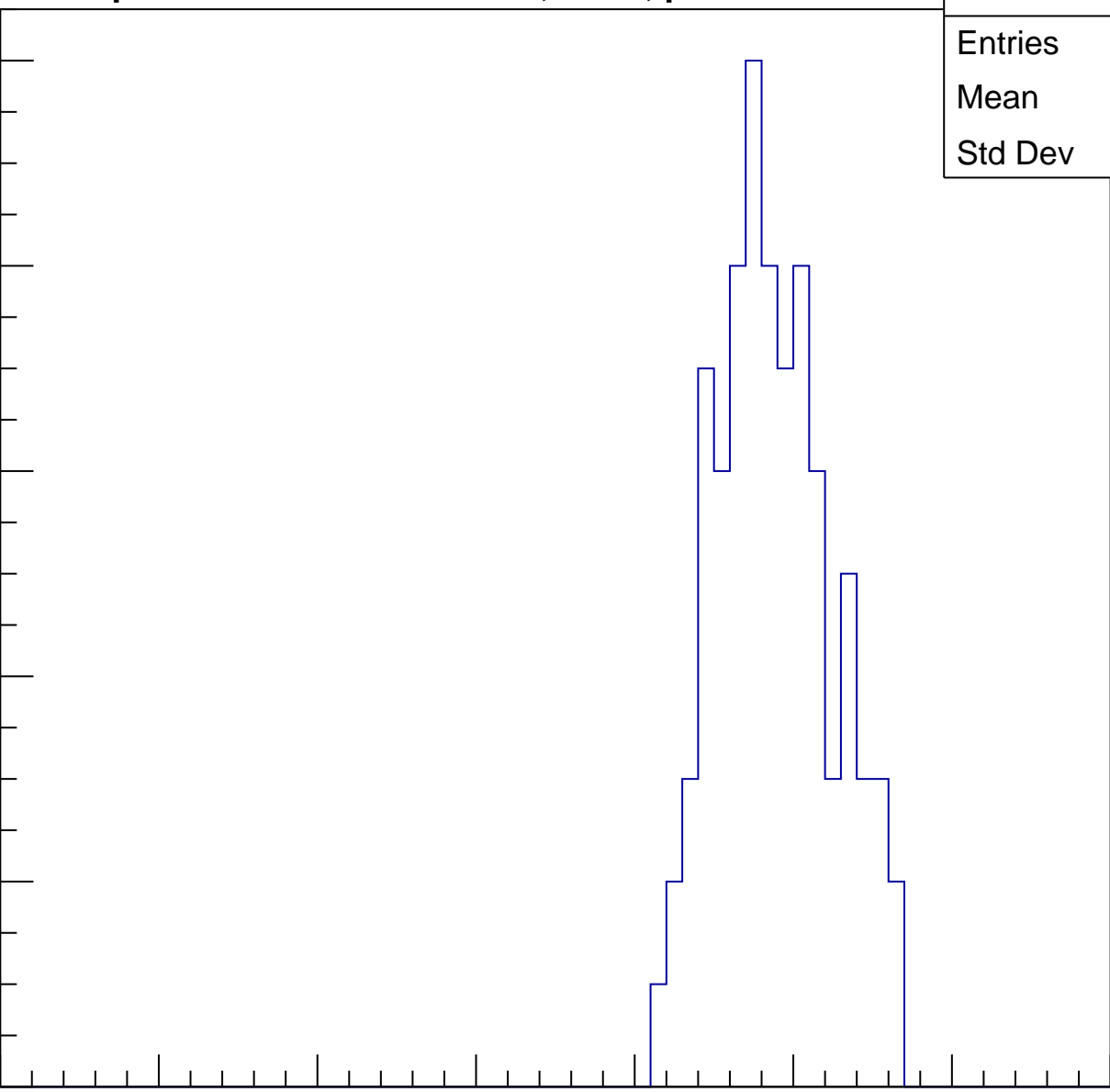
Entries	82
Mean	48.33
Std Dev	3.568

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

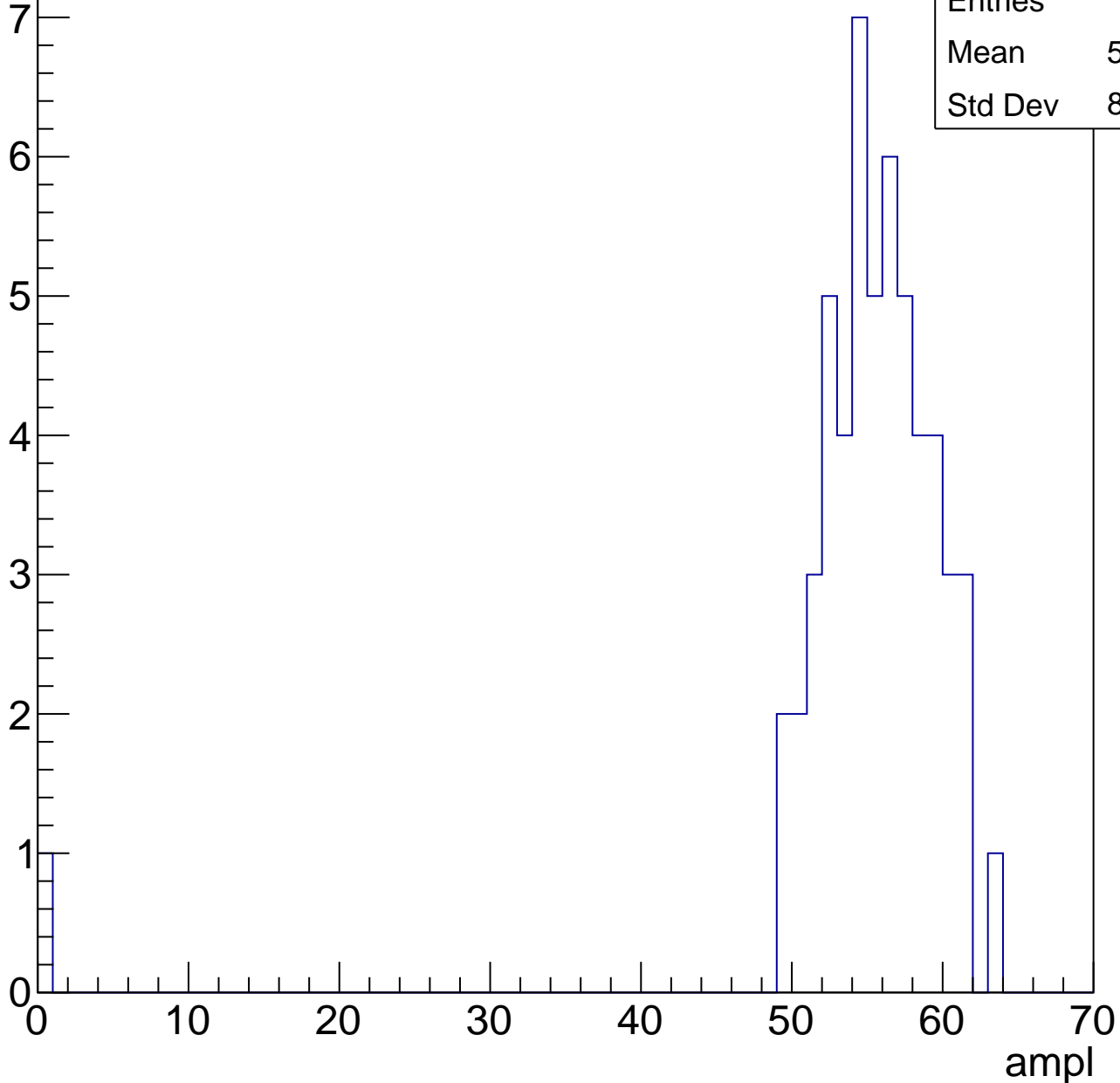


B0L002S, U17-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	54.38
Std Dev	8.107

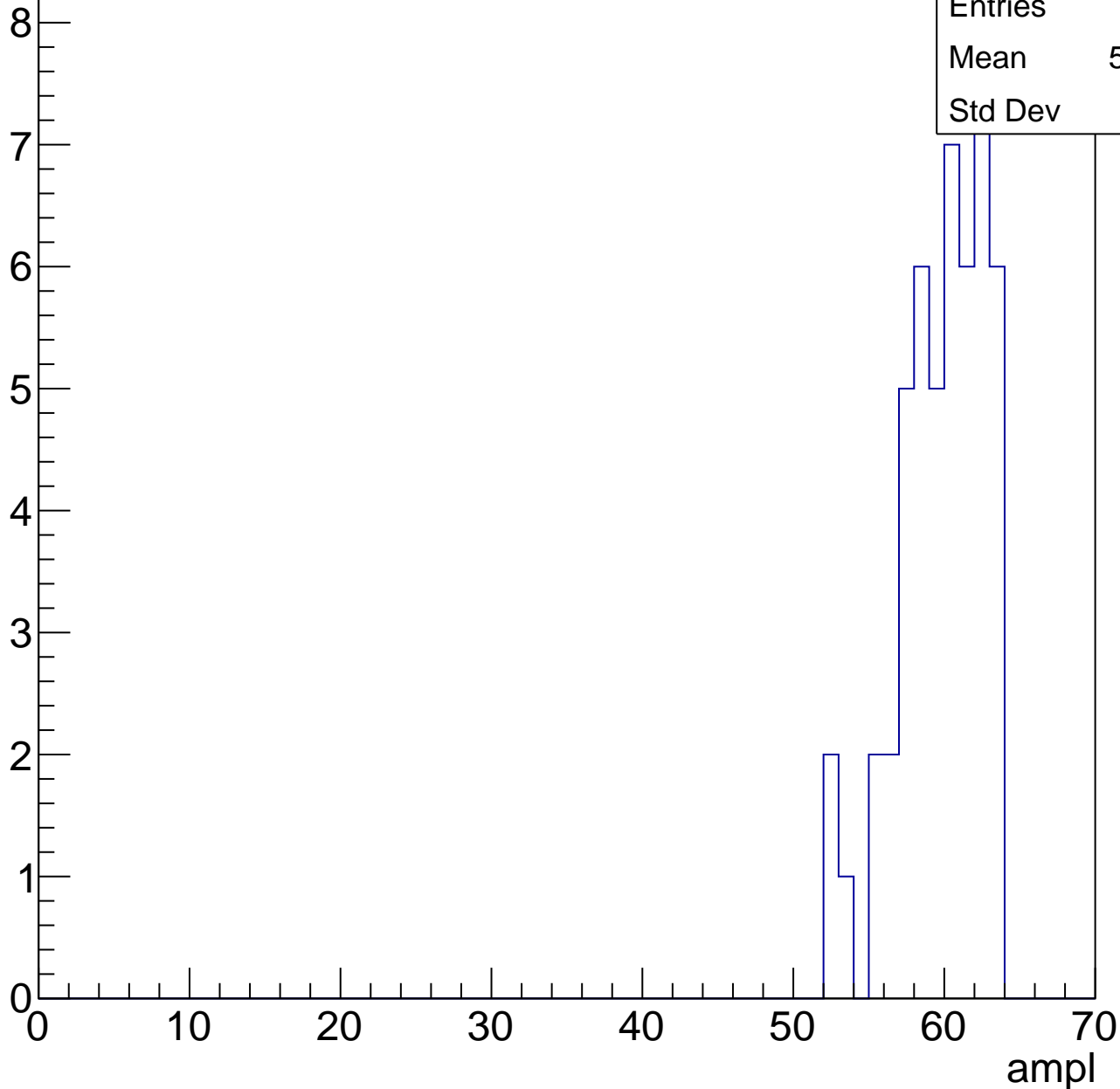


B0L002S, U17-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	59.34
Std Dev	2.84



B0L002S, U17-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch98, adc0

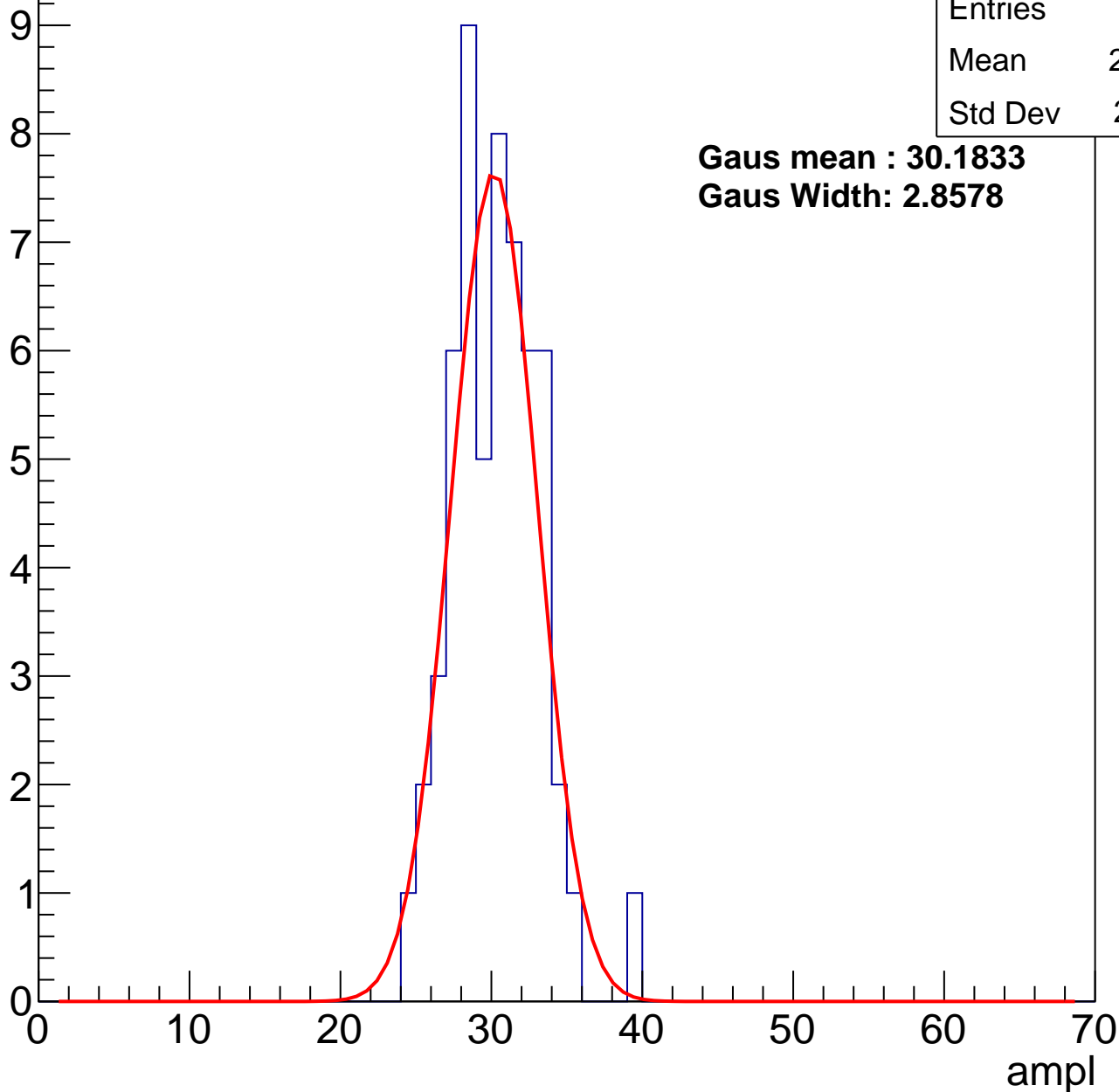
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.82
Std Dev	2.811

Gaus mean : 30.1833

Gaus Width: 2.8578



B0L002S, U17-ch98, adc1

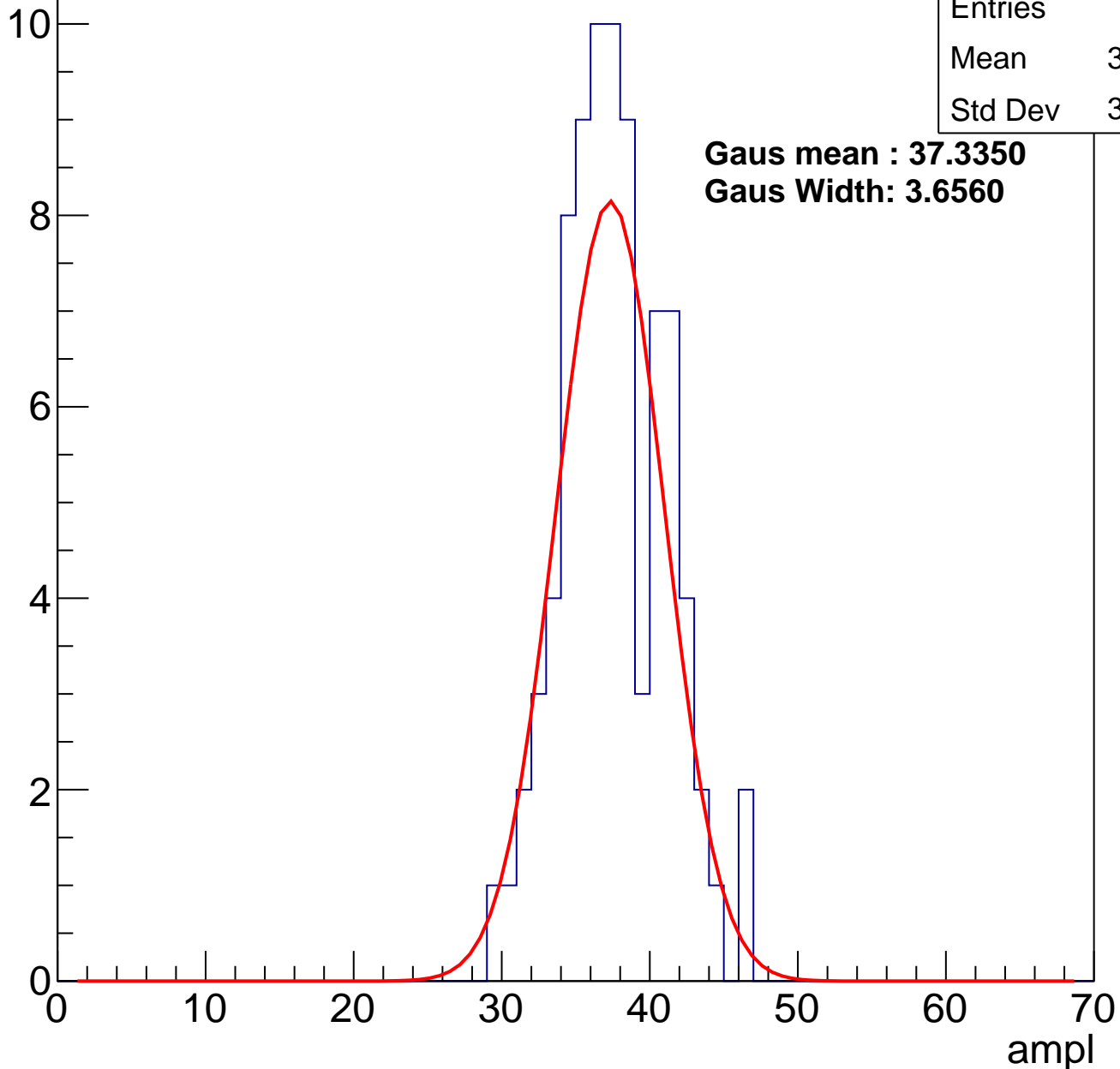
calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	37.13
Std Dev	3.505

Gaus mean : 37.3350

Gaus Width: 3.6560

Entry



B0L002S, U17-ch98, adc2

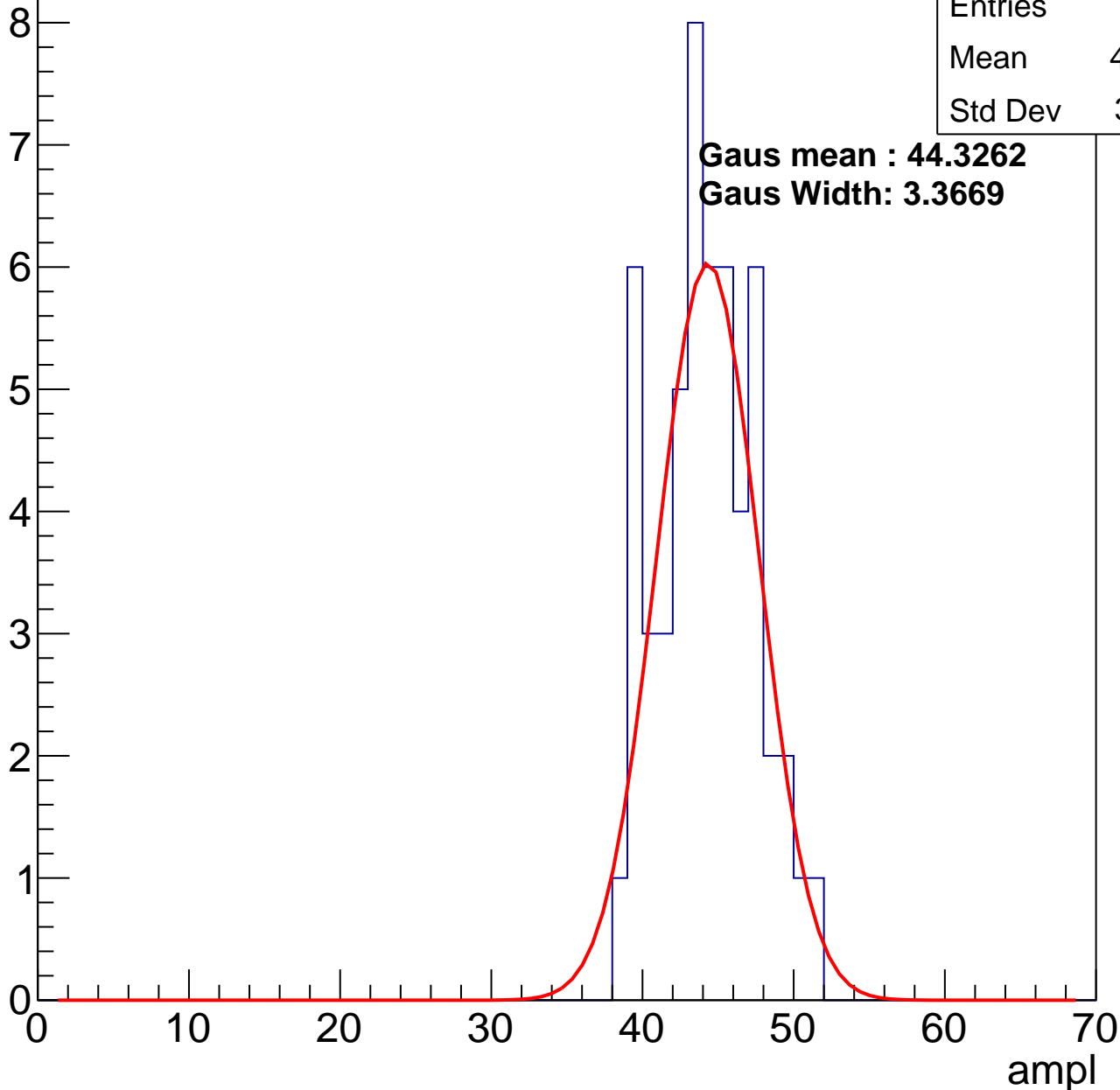
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.78
Std Dev	3.131

Gaus mean : 44.3262

Gaus Width: 3.3669

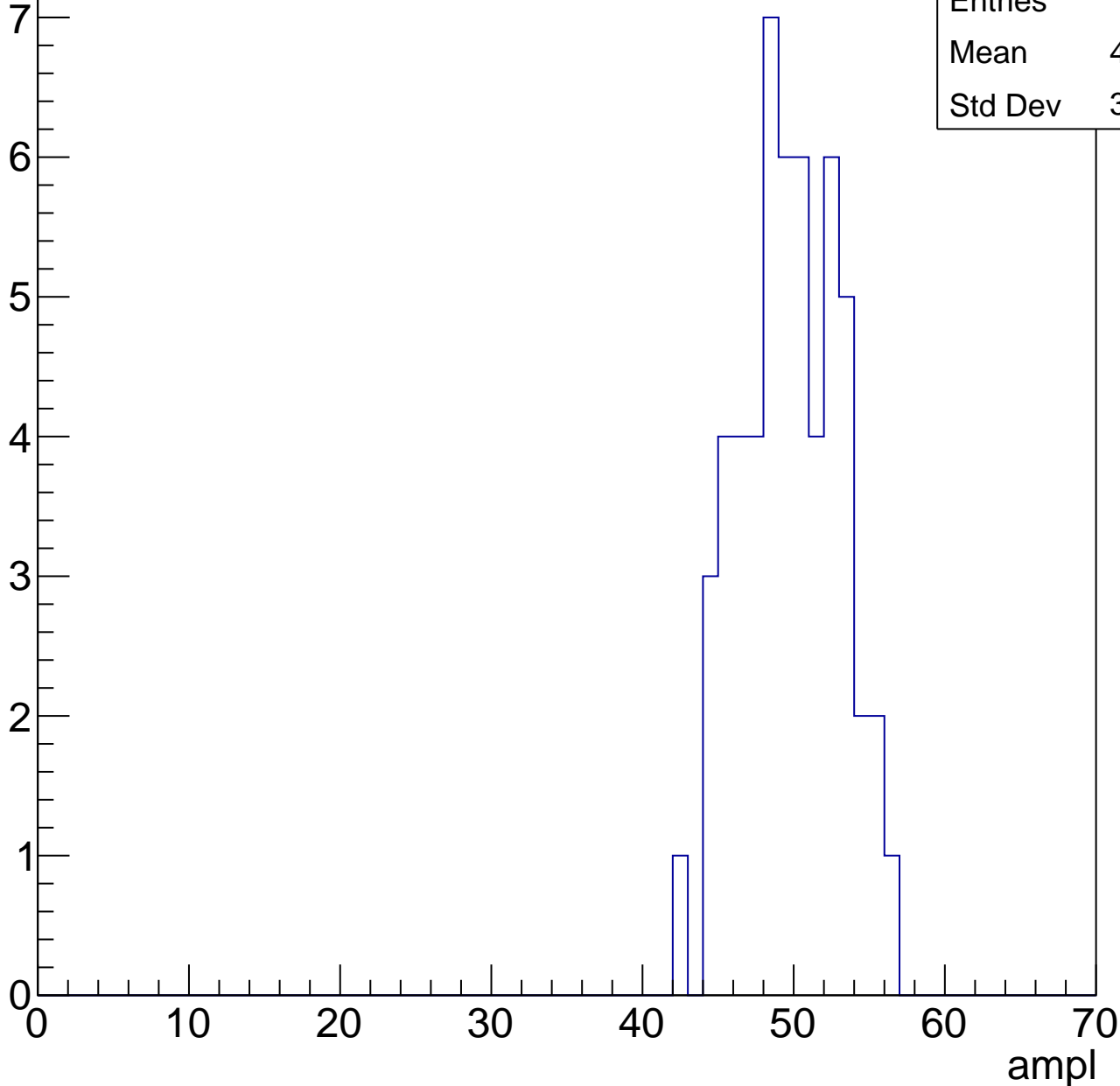


B0L002S, U17-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	49.29
Std Dev	3.217

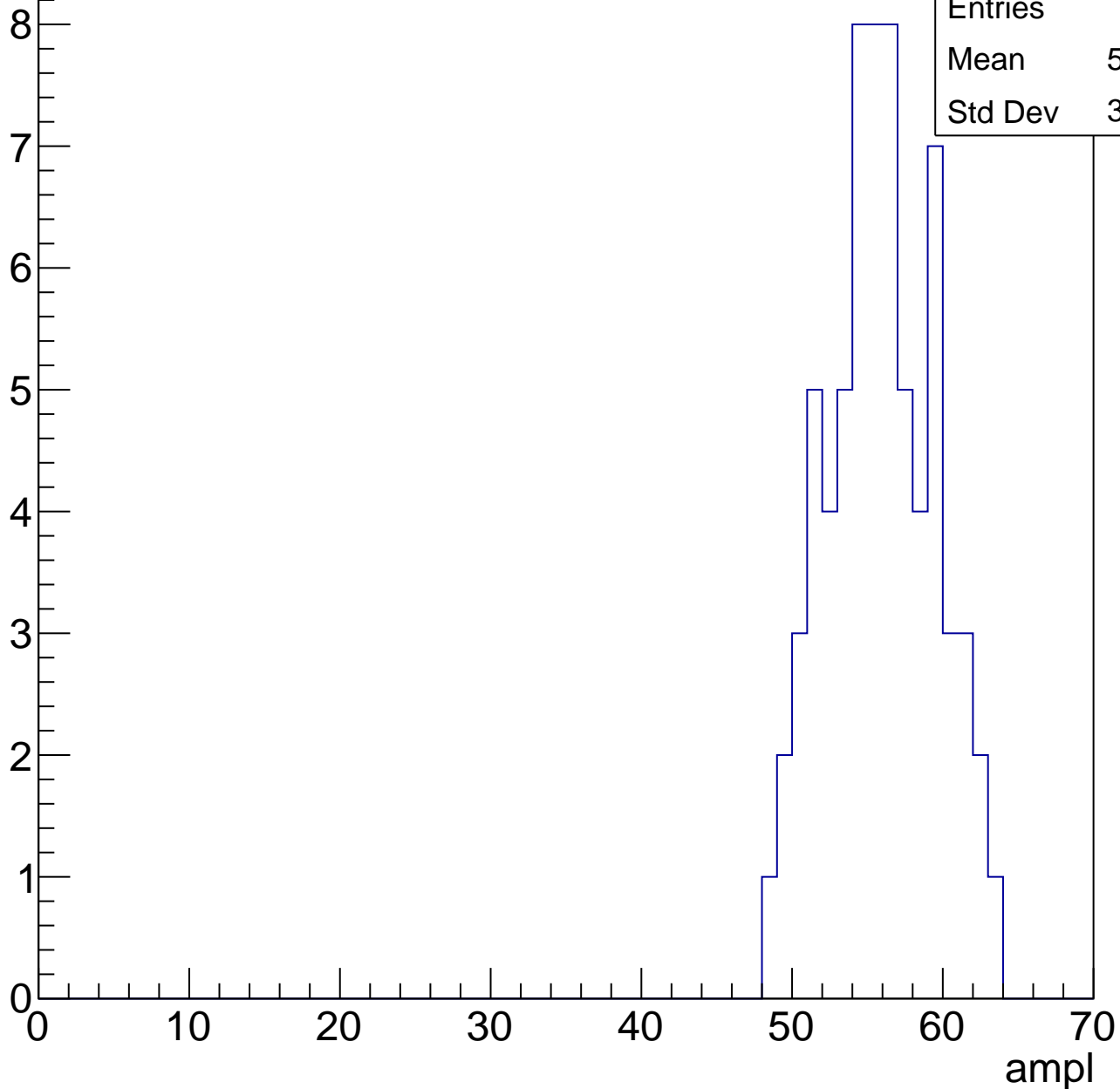


B0L002S, U17-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	55.42
Std Dev	3.507

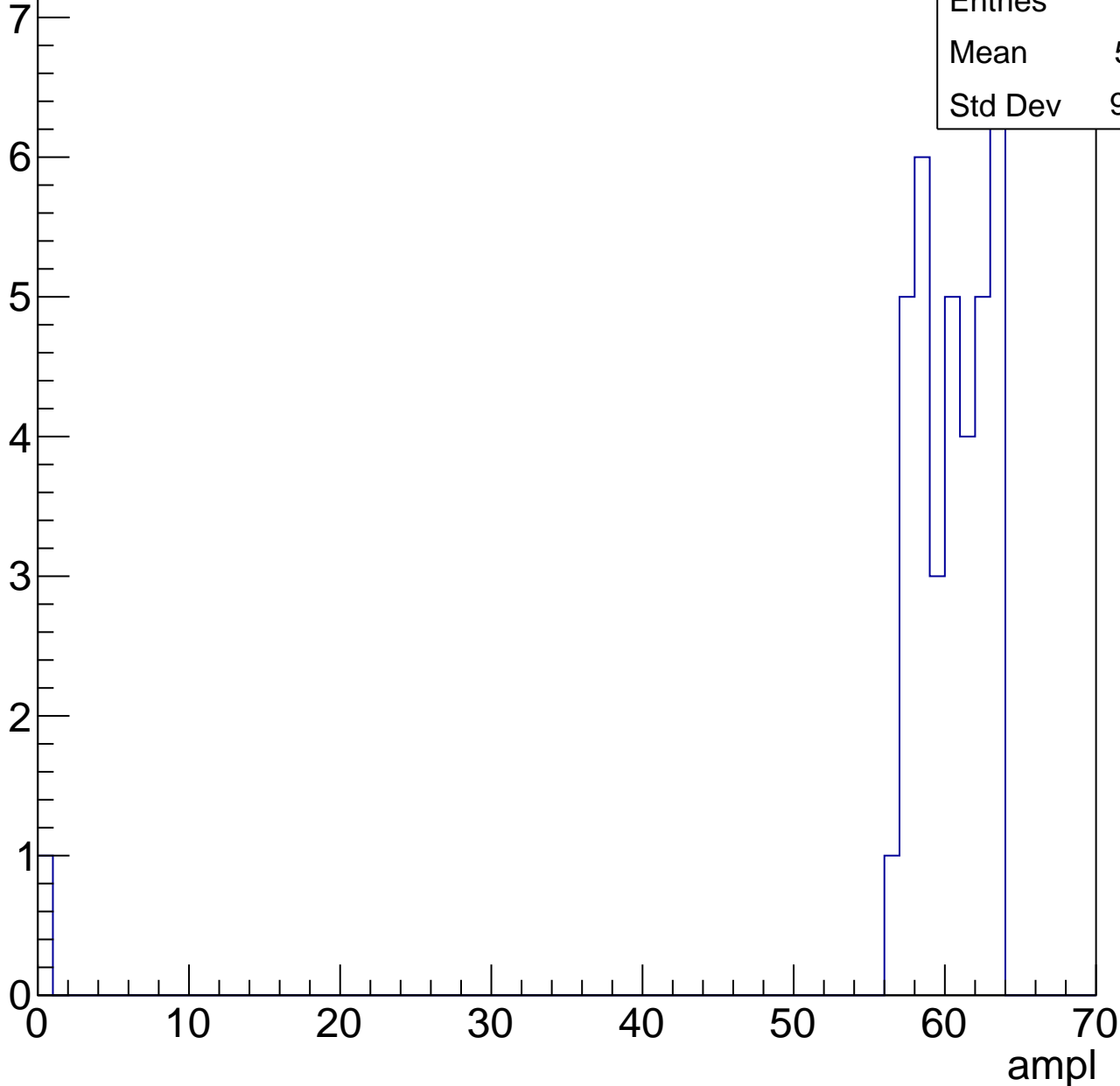


B0L002S, U17-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

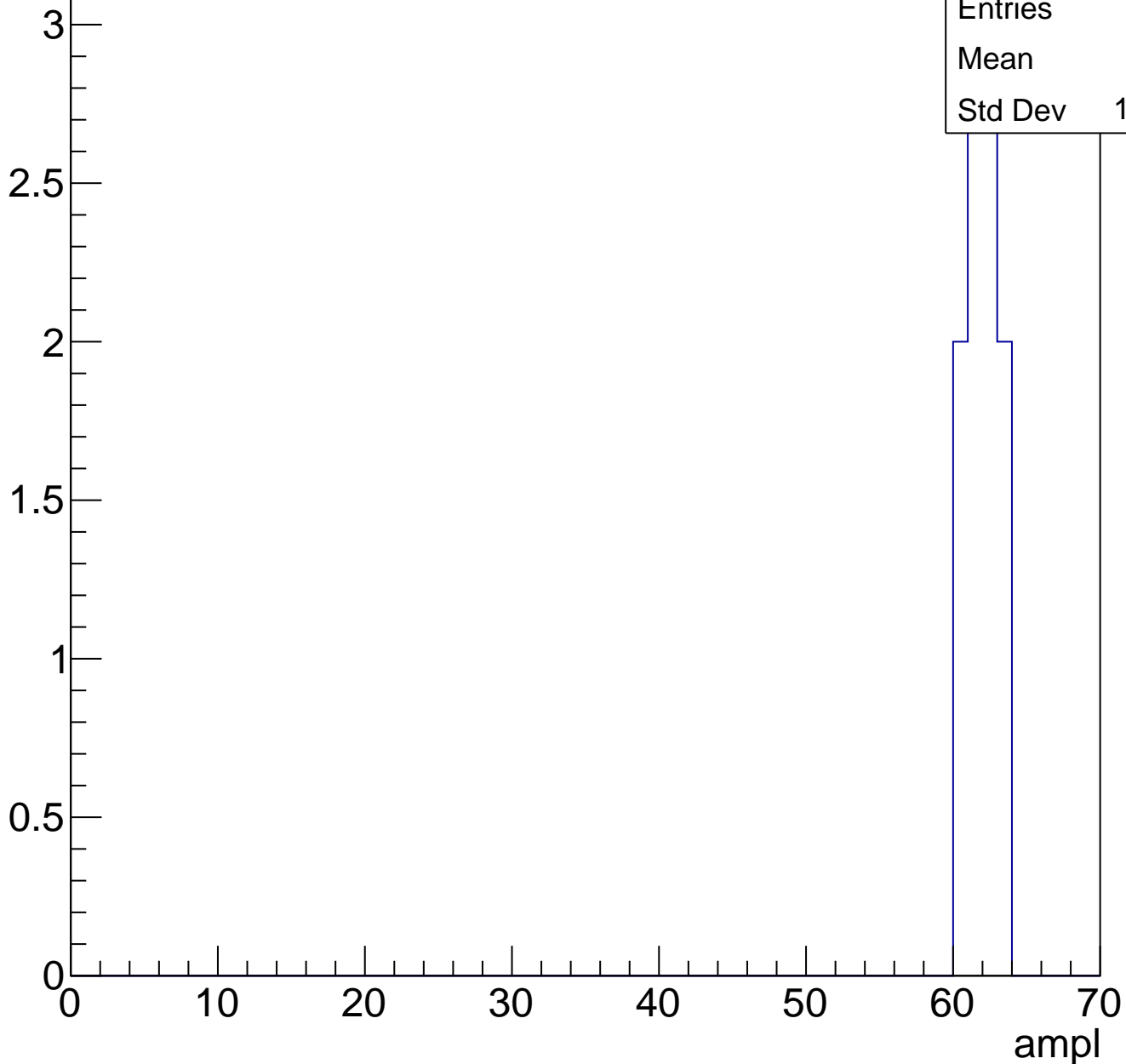
Entries	37
Mean	58.41
Std Dev	9.974



B0L002S, U17-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch99, adc0

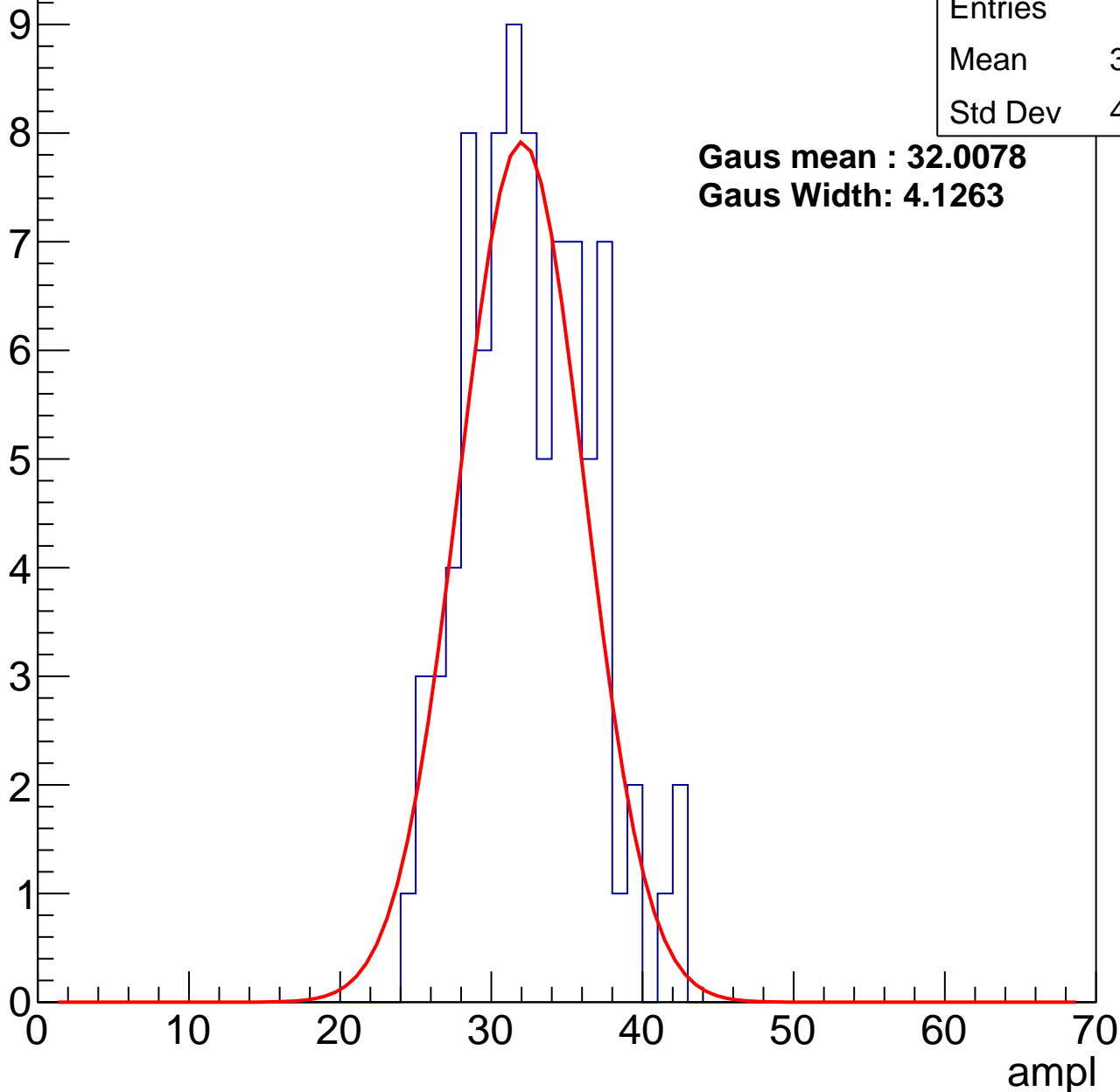
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	32.02
Std Dev	4.023

Gaus mean : 32.0078

Gaus Width: 4.1263



B0L002S, U17-ch99, adc1

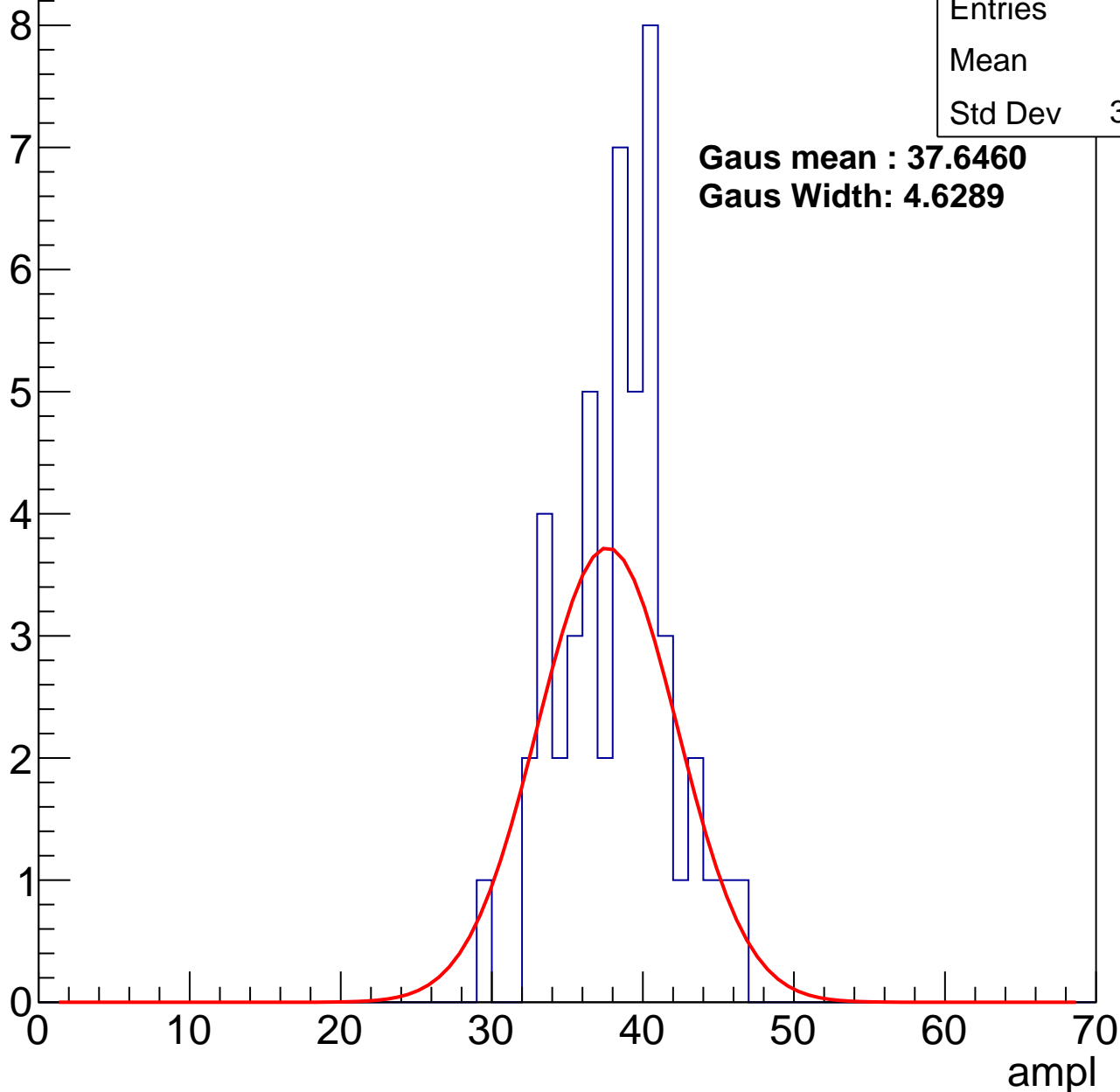
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	37.9
Std Dev	3.572

Gaus mean : 37.6460

Gaus Width: 4.6289



B0L002S, U17-ch99, adc2

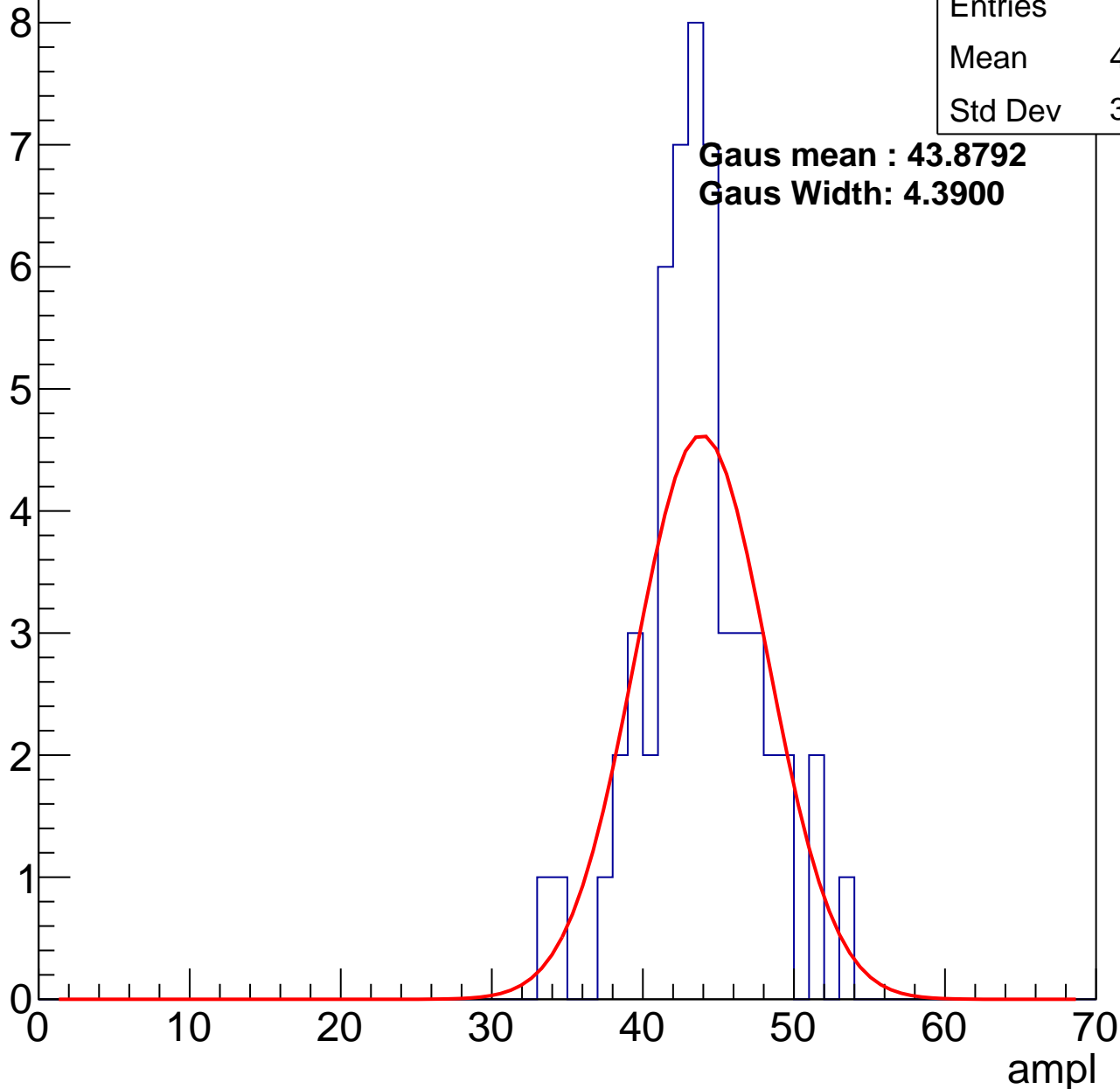
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.19
Std Dev	3.864

Gaus mean : 43.8792

Gaus Width: 4.3900

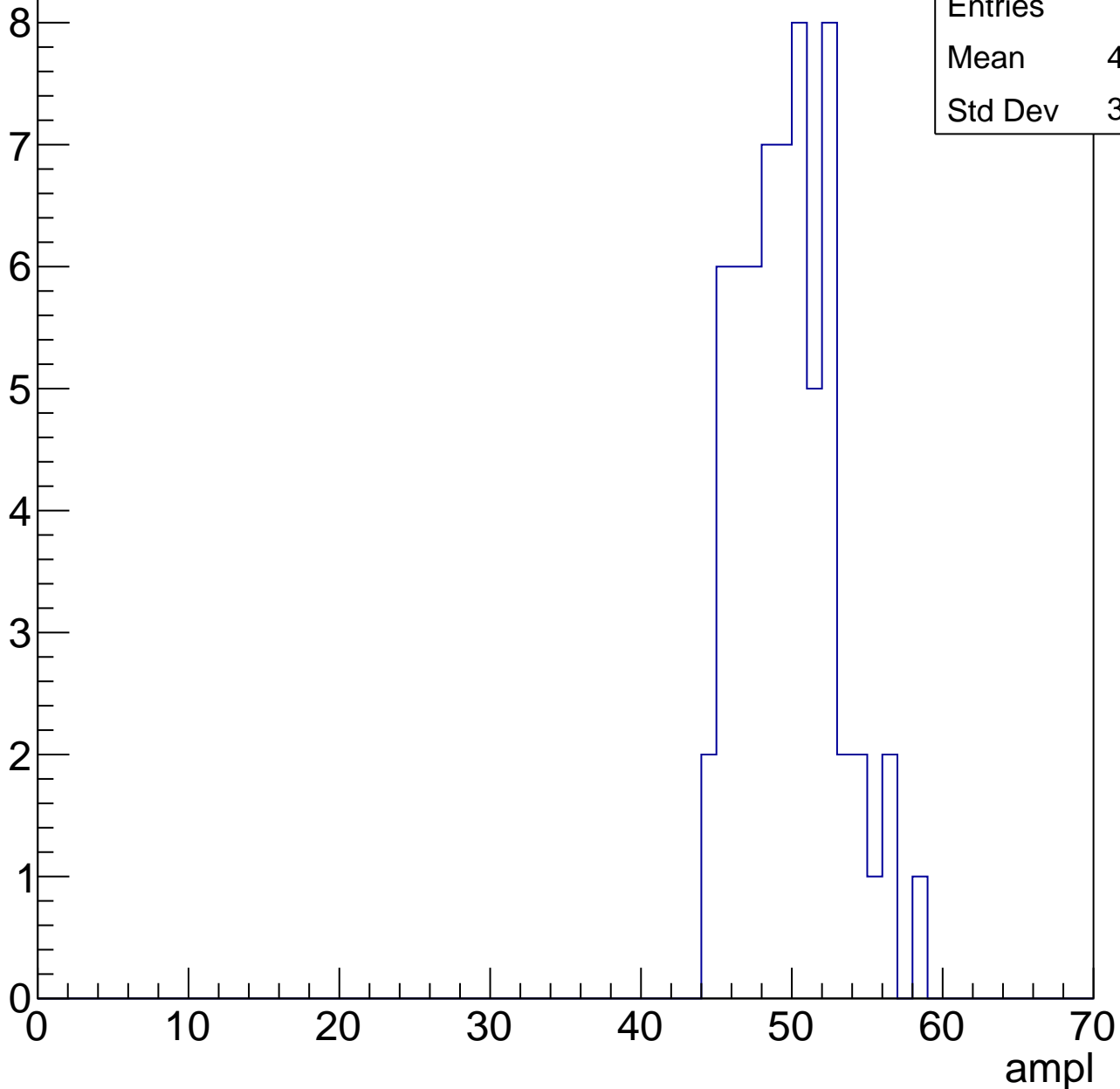


B0L002S, U17-ch99, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

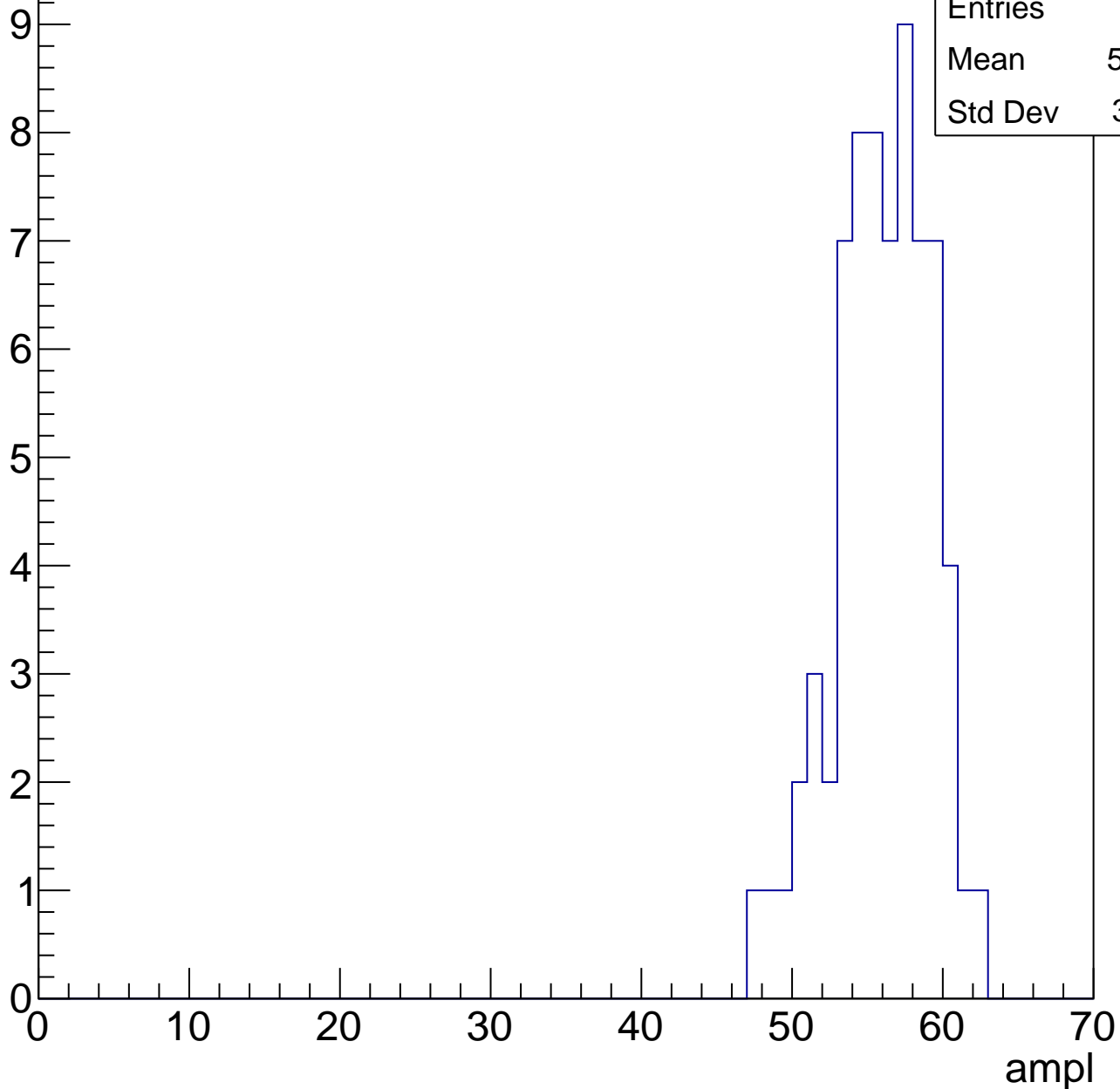
Entries	63
Mean	49.29
Std Dev	3.144



B0L002S, U17-ch99, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

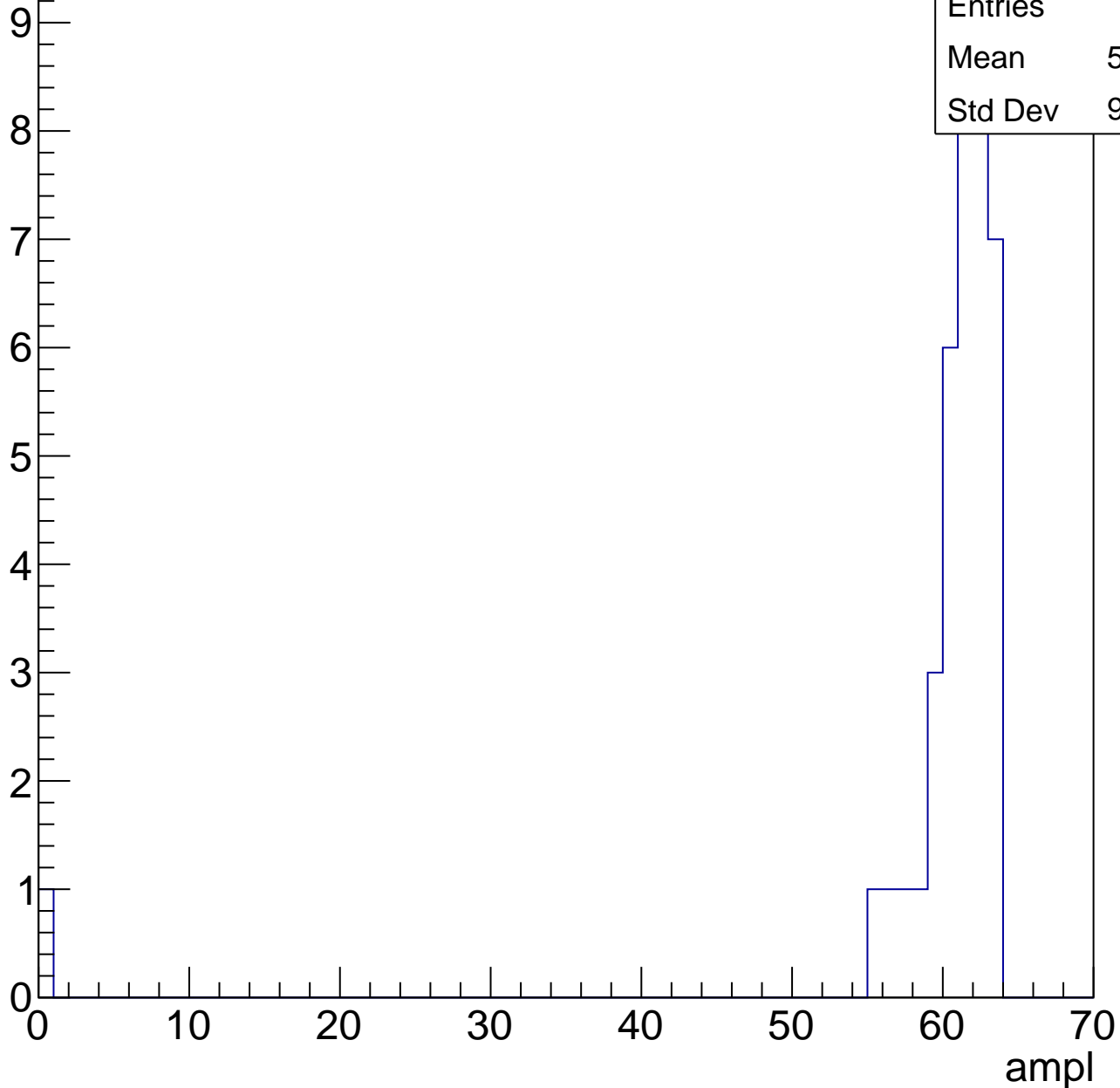


Entries	69
Mean	55.52
Std Dev	3.151

B0L002S, U17-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

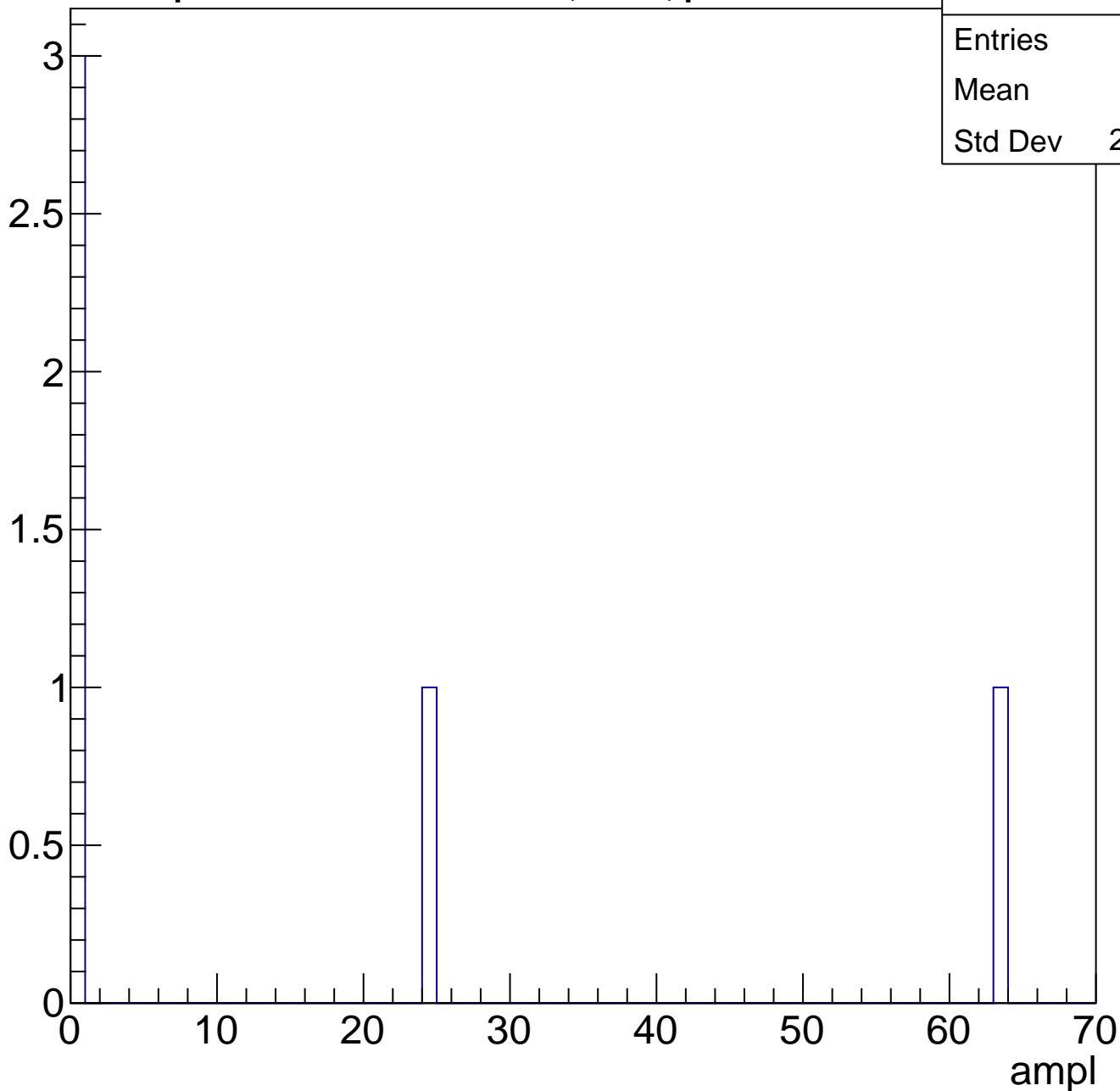
Entry



B0L002S, U17-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch100, adc0

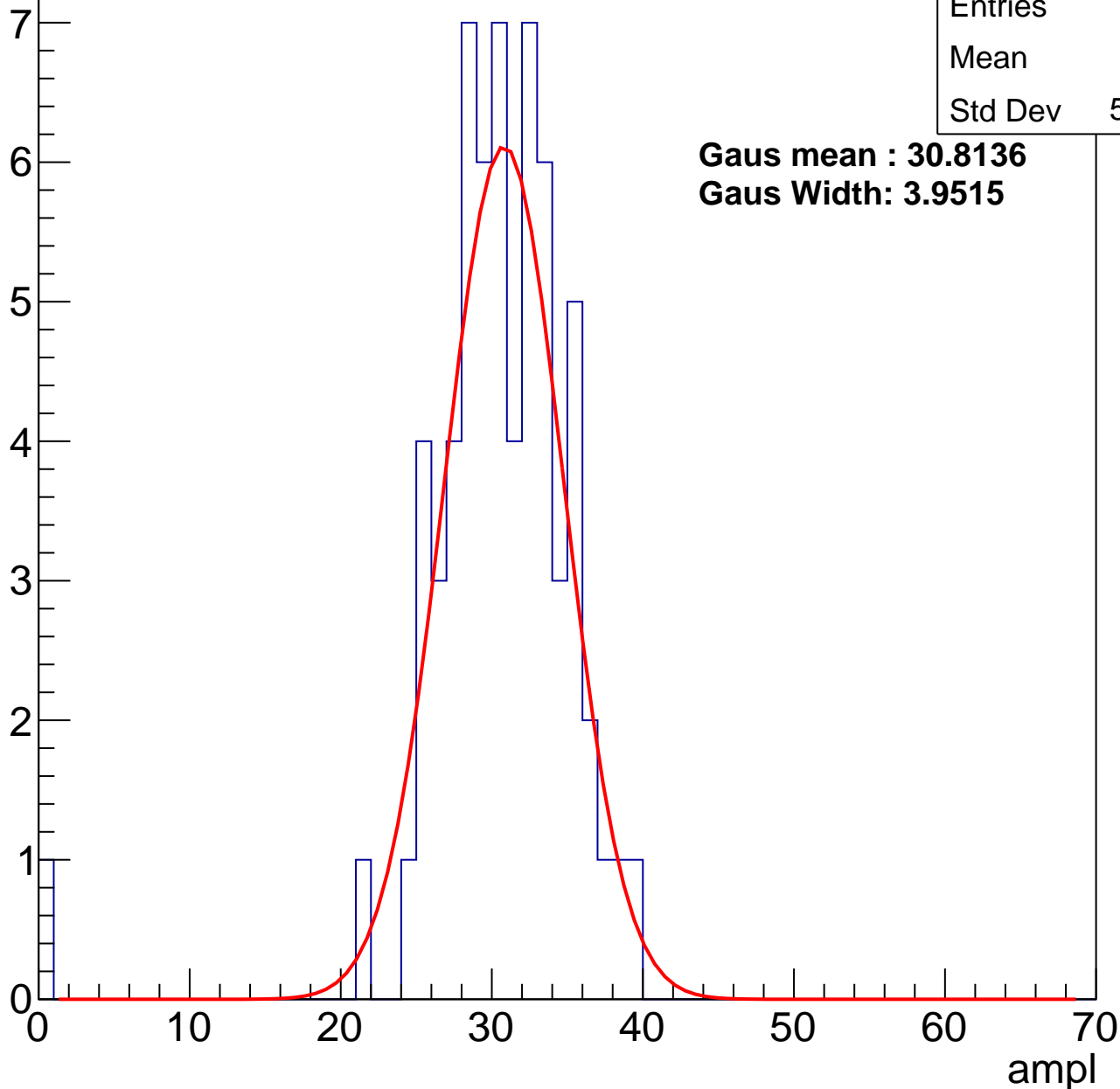
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30
Std Dev	5.244

Gaus mean : 30.8136

Gaus Width: 3.9515



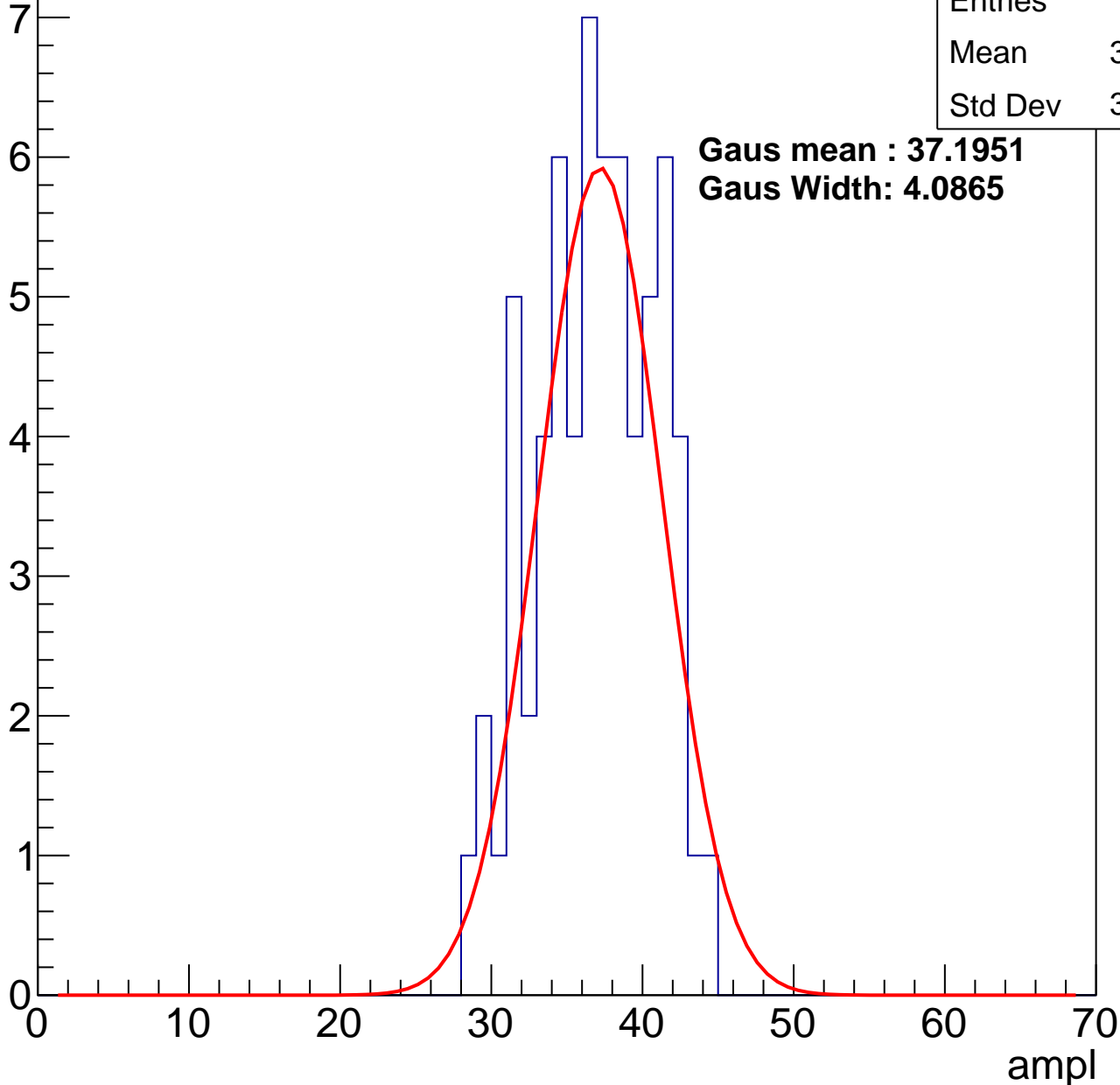
B0L002S, U17-ch100, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.46
Std Dev	3.839

Gaus mean : 37.1951
Gaus Width: 4.0865



B0L002S, U17-ch100, adc2

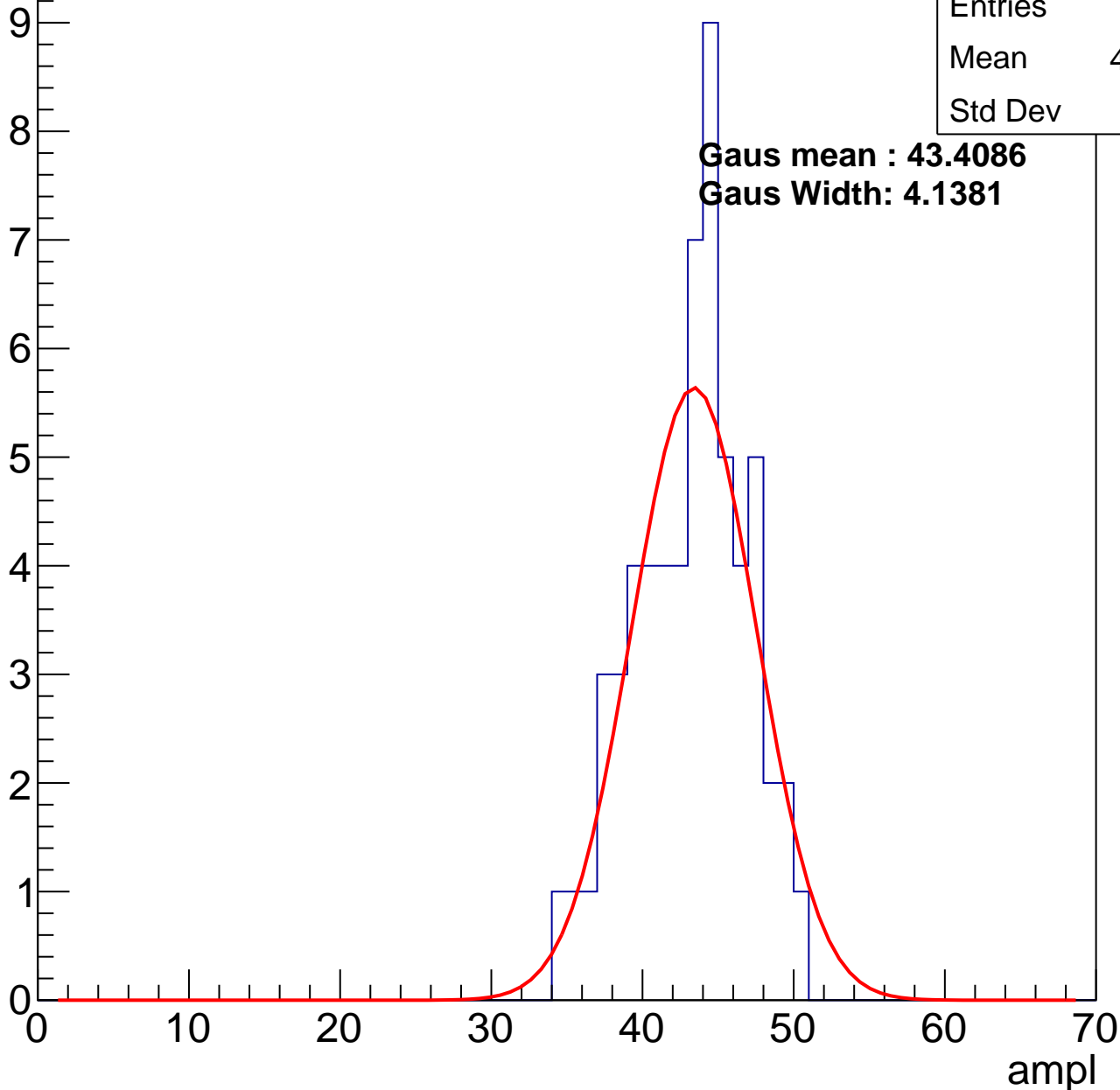
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	42.72
Std Dev	3.67

Gaus mean : 43.4086

Gaus Width: 4.1381

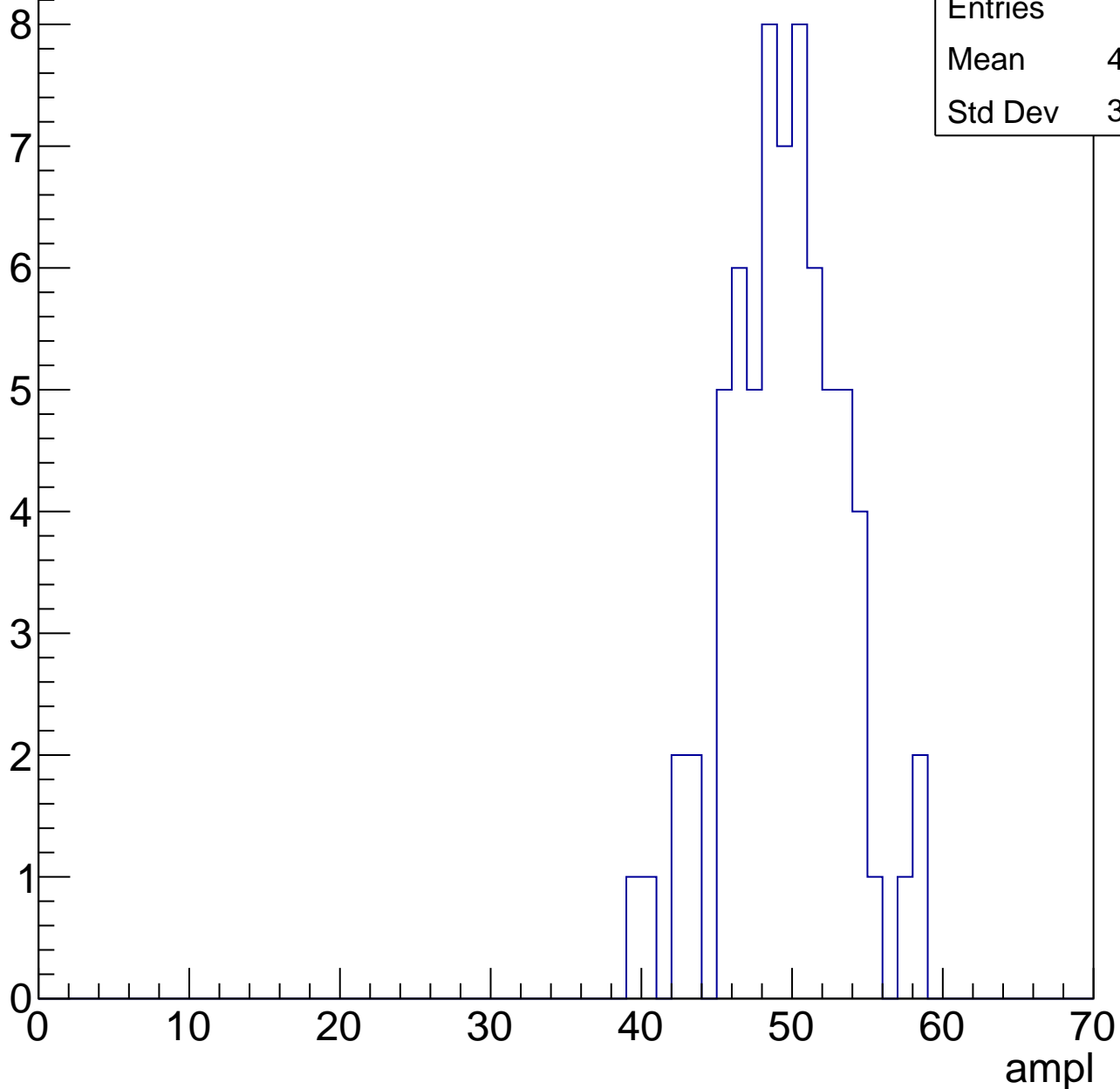


B0L002S, U17-ch100, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.09
Std Dev	3.866

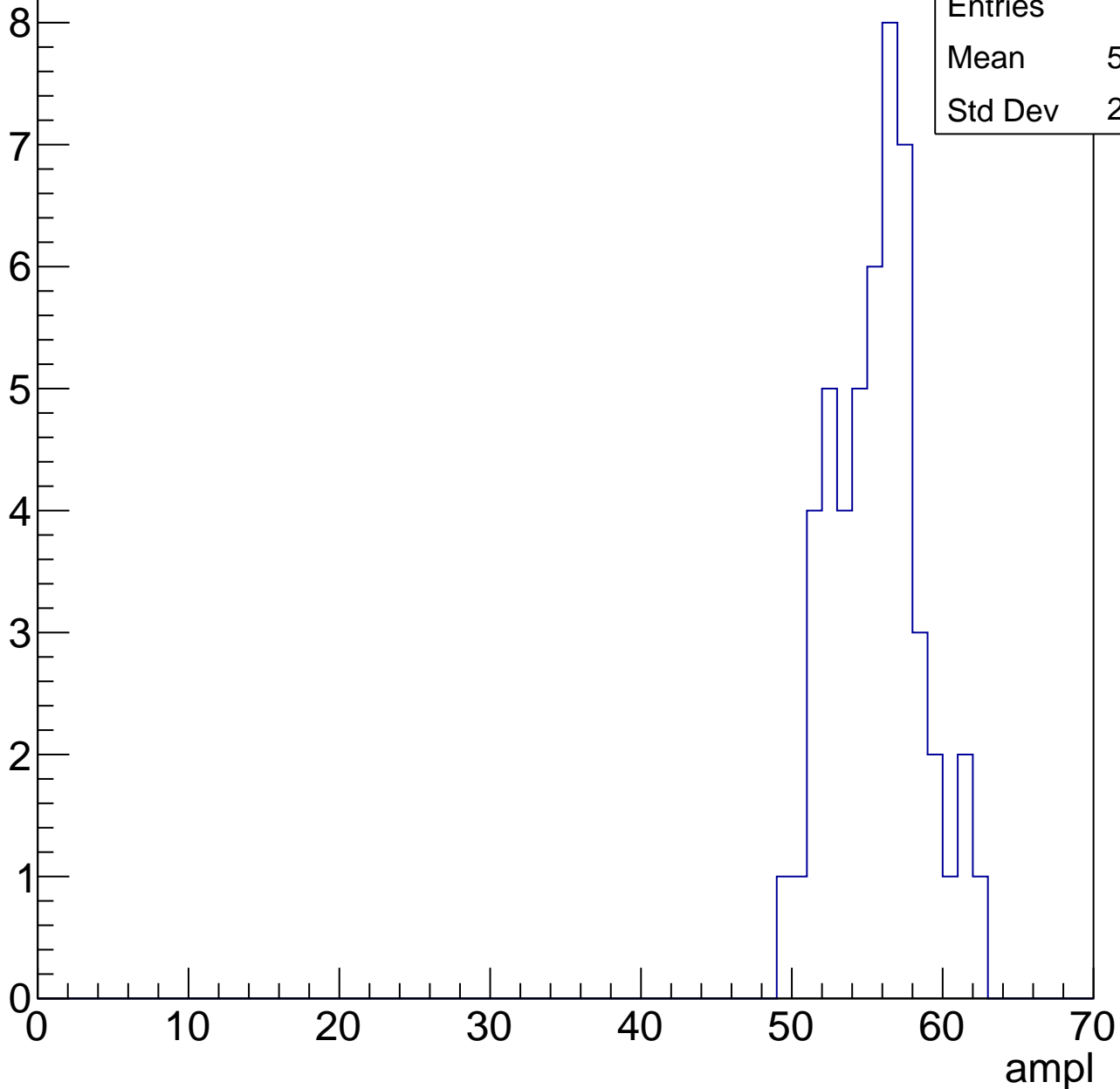


B0L002S, U17-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

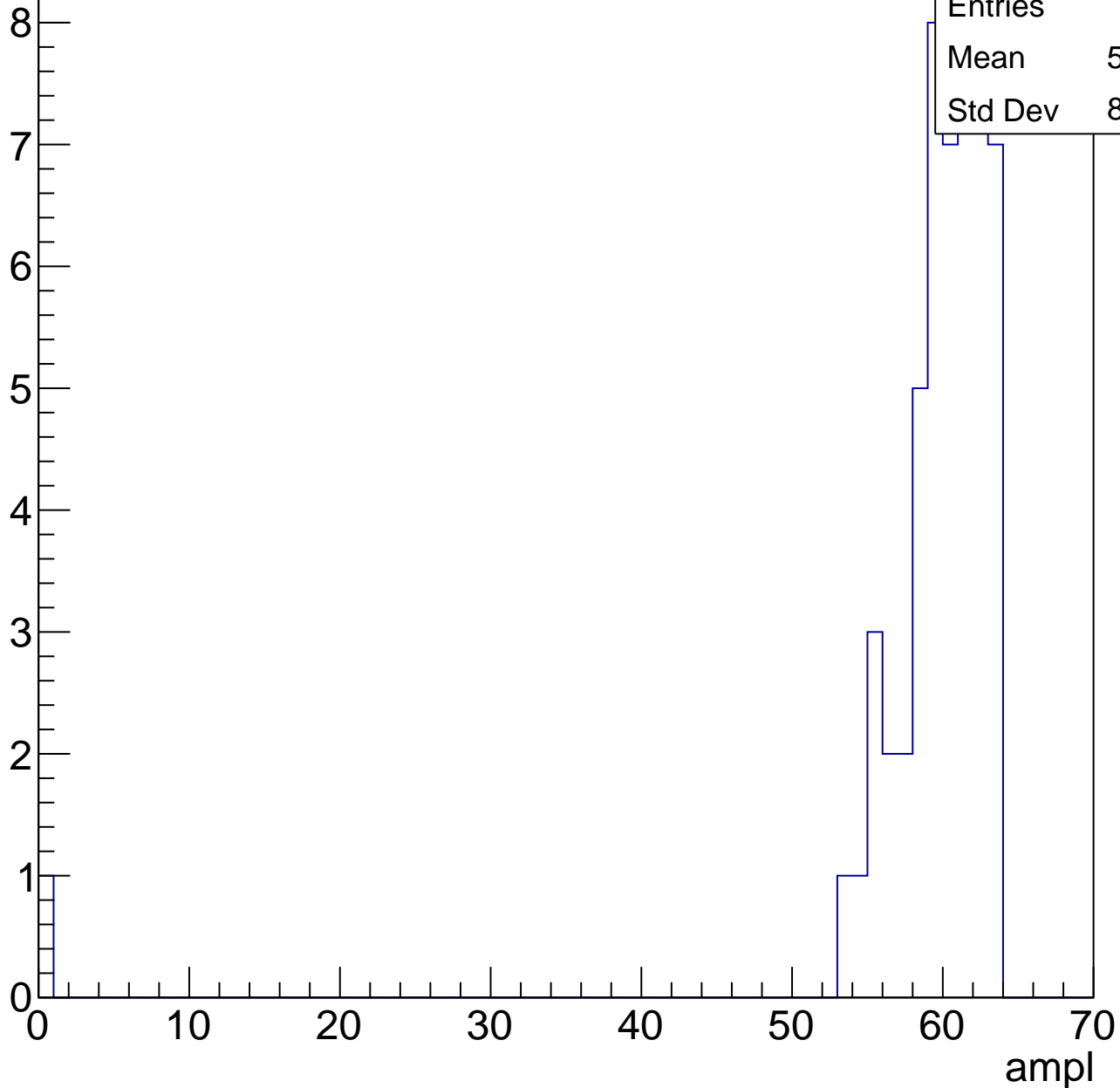
Entries	50
Mean	55.16
Std Dev	2.935



B0L002S, U17-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

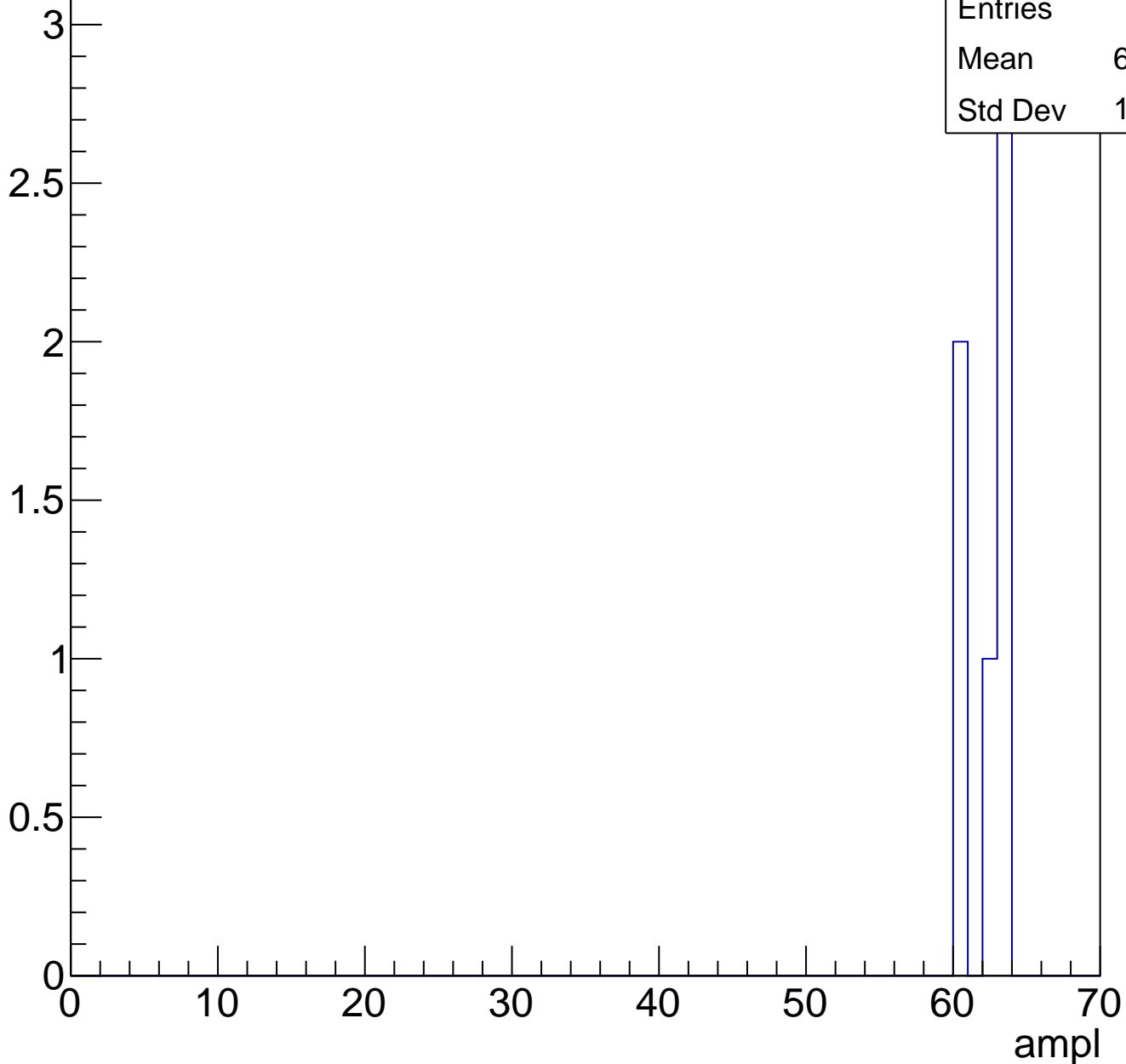
Entry



B0L002S, U17-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

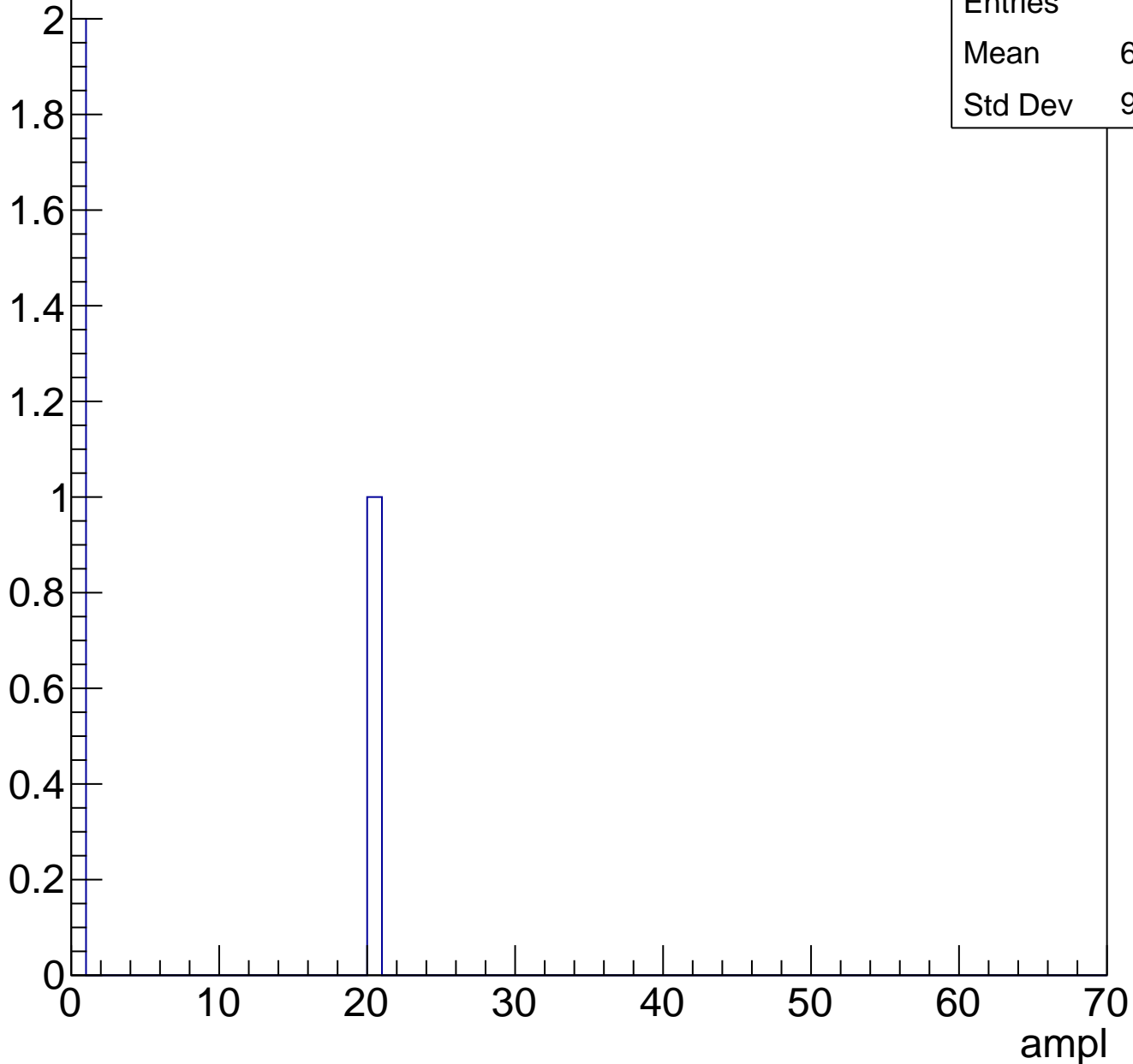
Entry



B0L002S, U17-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L002S, U17-ch101, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	30.9
Std Dev	4.744

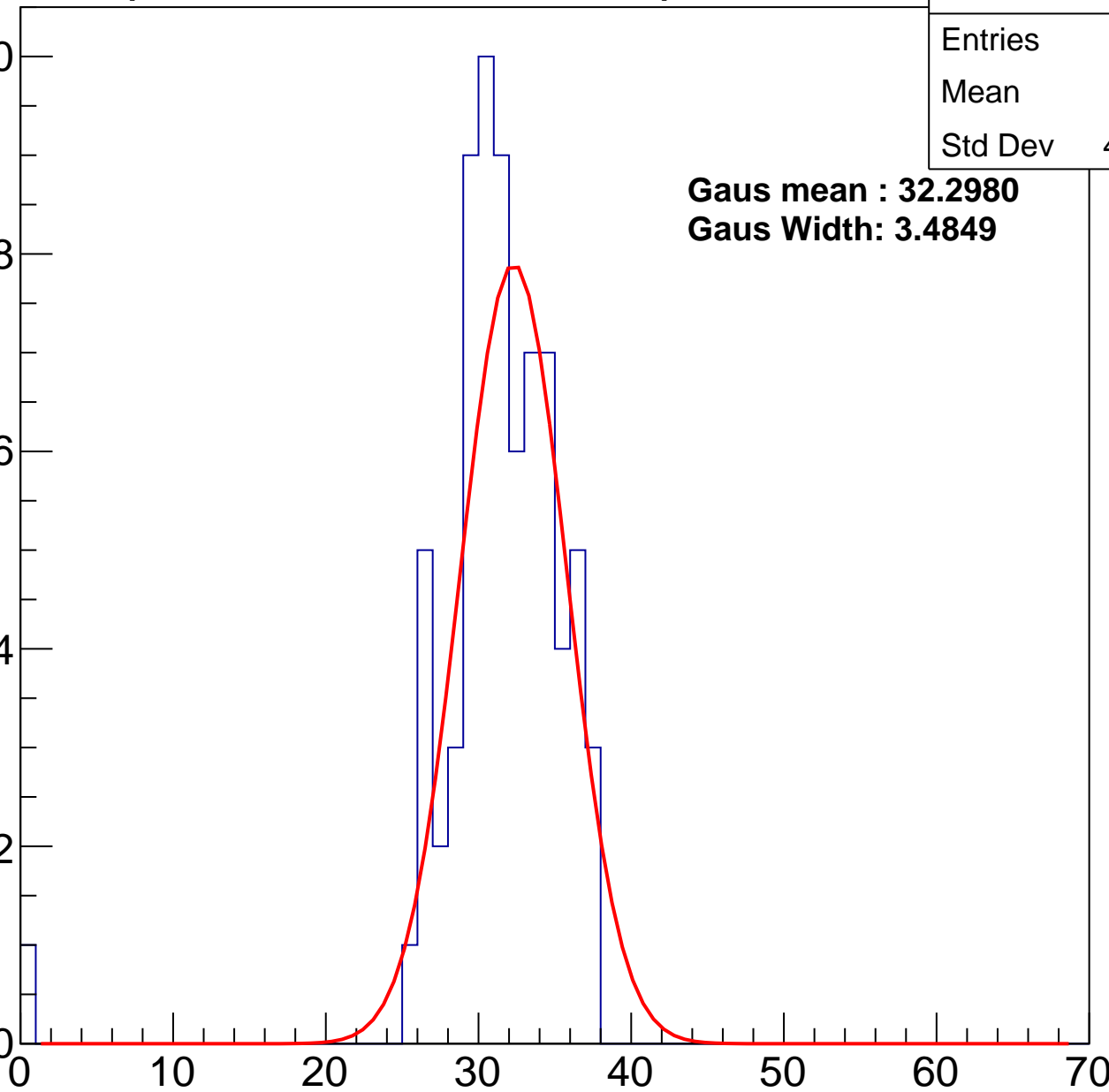
Gaus mean : 32.2980

Gaus Width: 3.4849

Entry

10
8
6
4
2
0

ampl



B0L002S, U17-ch101, adc1

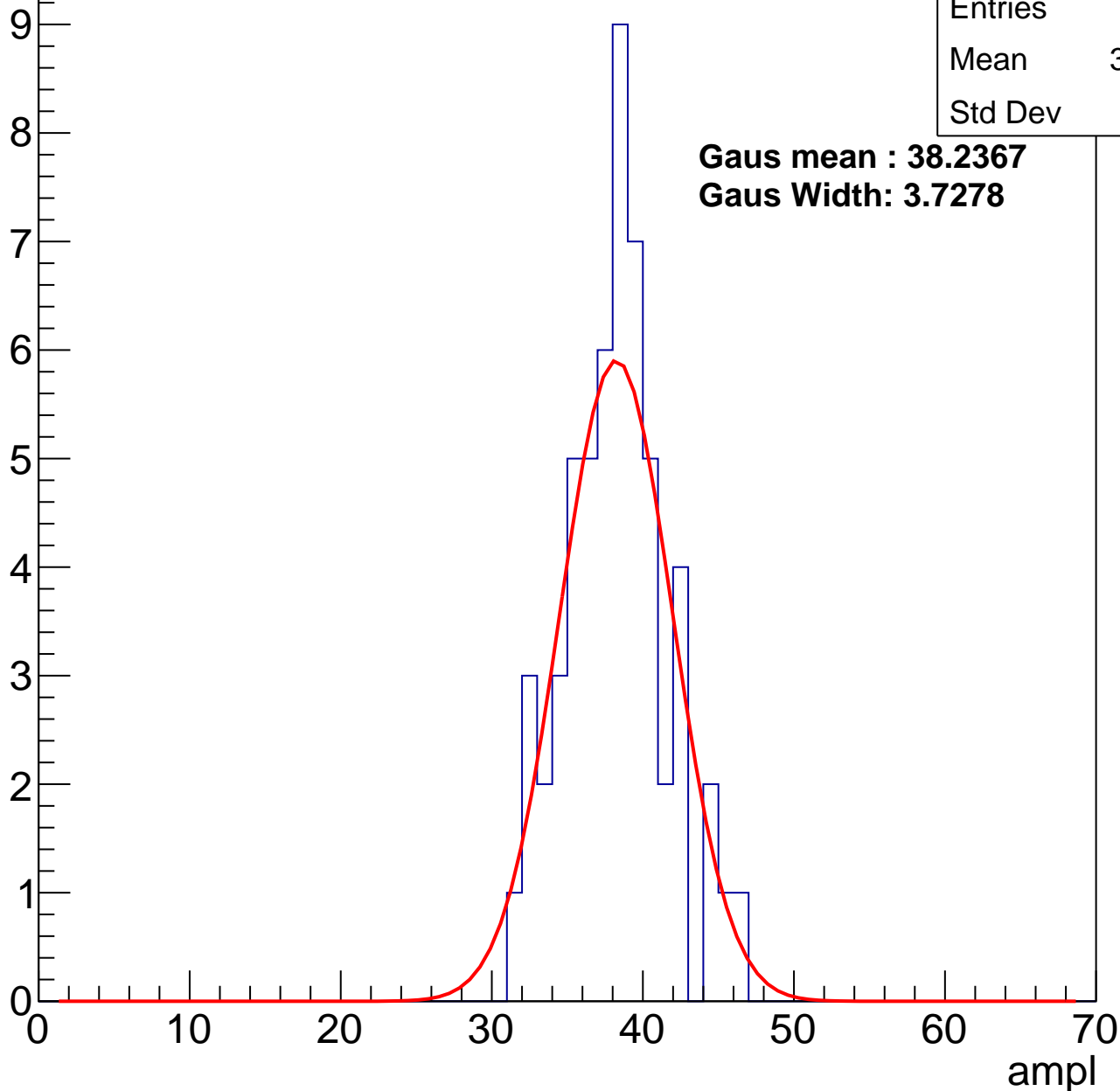
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	37.79
Std Dev	3.32

Gaus mean : 38.2367

Gaus Width: 3.7278



B0L002S, U17-ch101, adc2

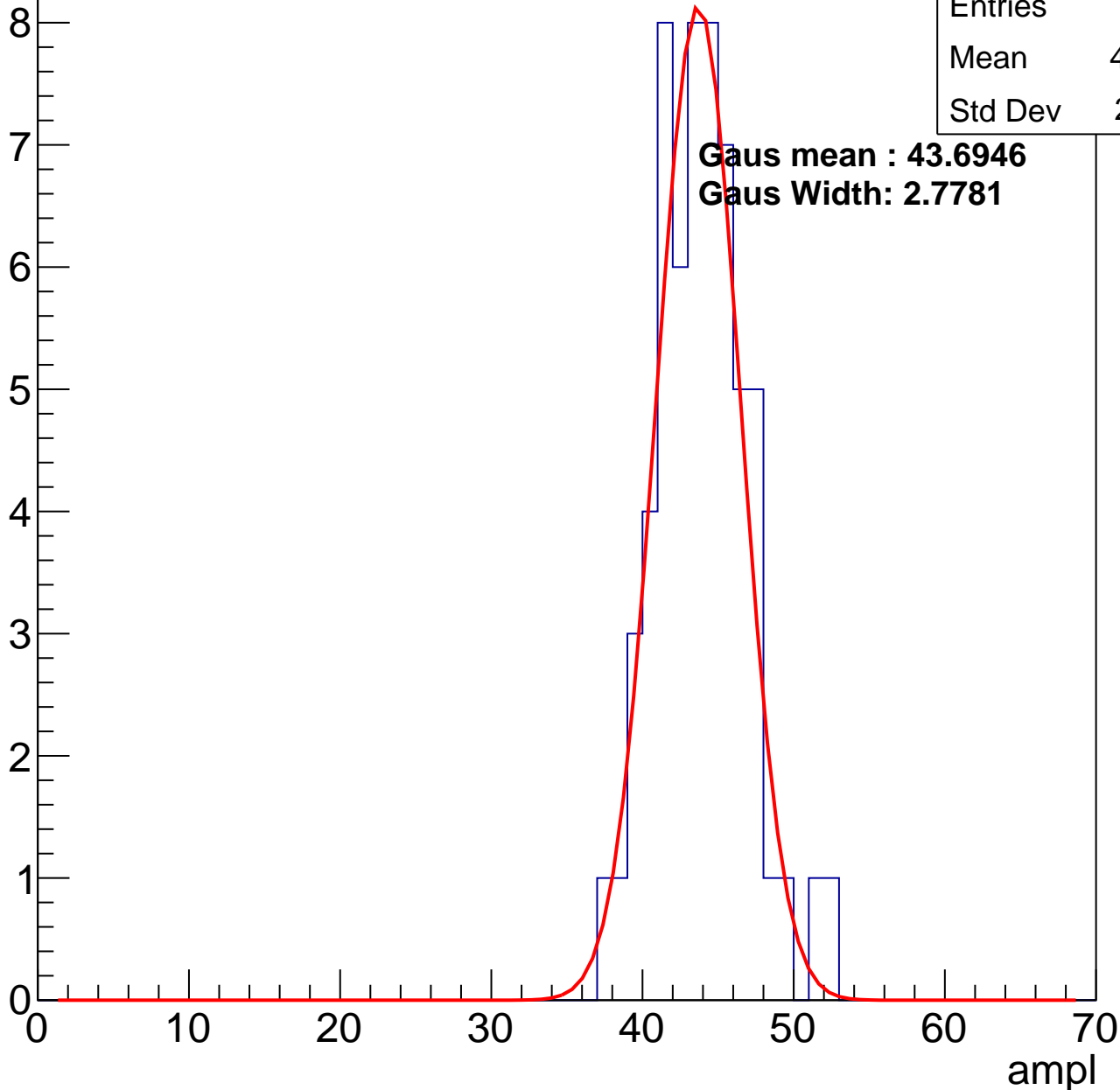
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.47
Std Dev	2.991

Gaus mean : 43.6946

Gaus Width: 2.7781

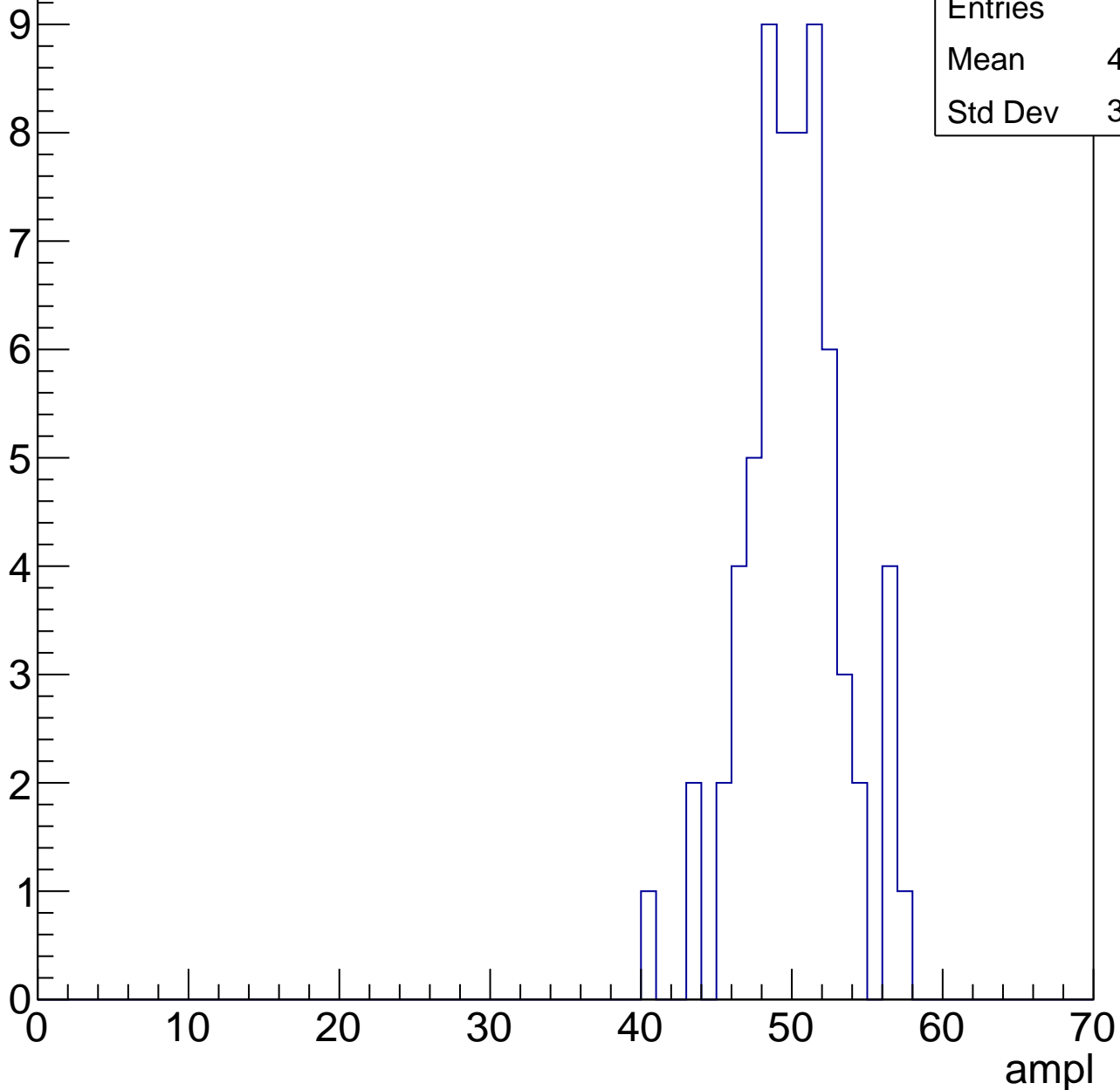


B0L002S, U17-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	49.66
Std Dev	3.256

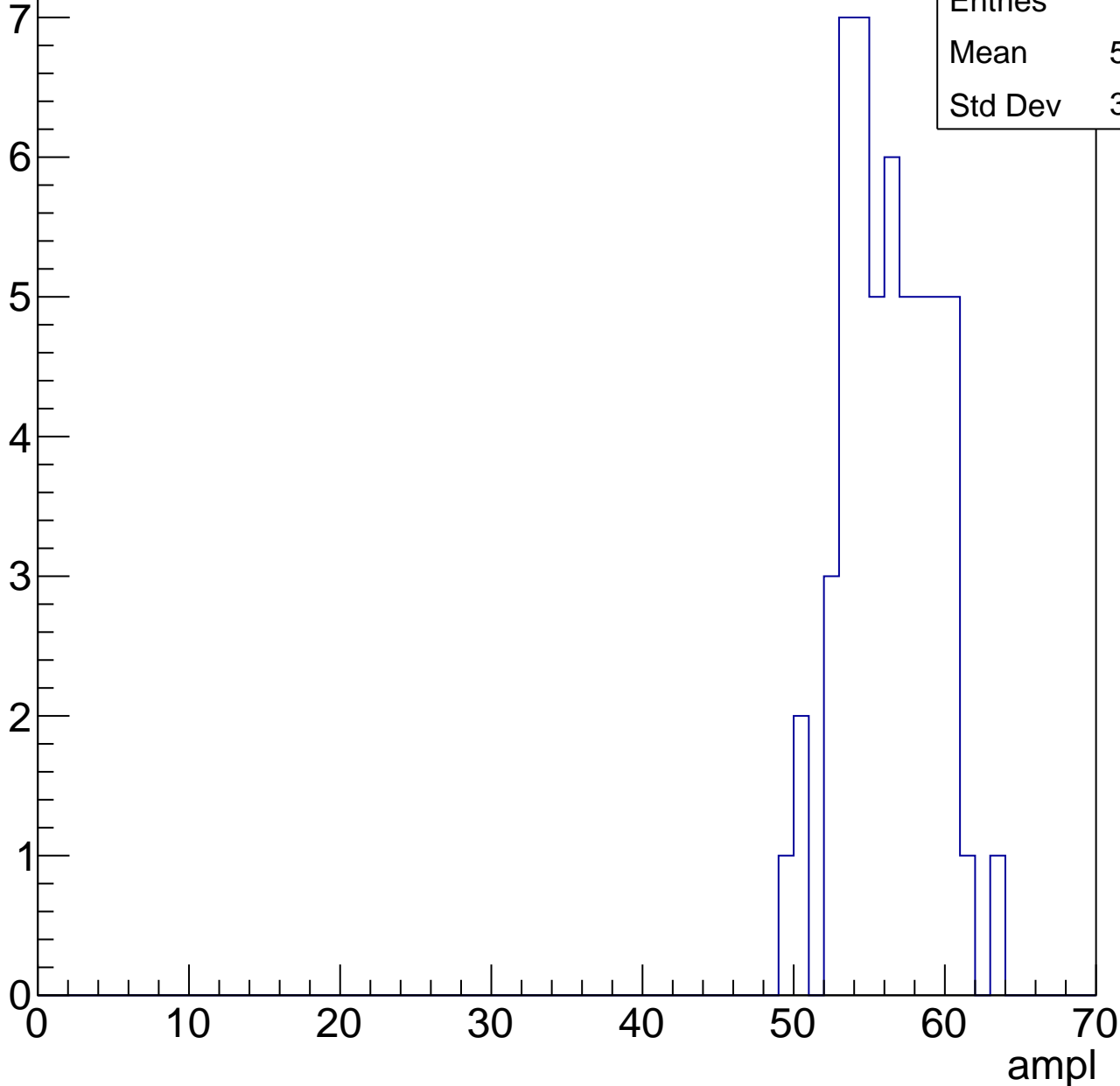


B0L002S, U17-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	55.83
Std Dev	3.033

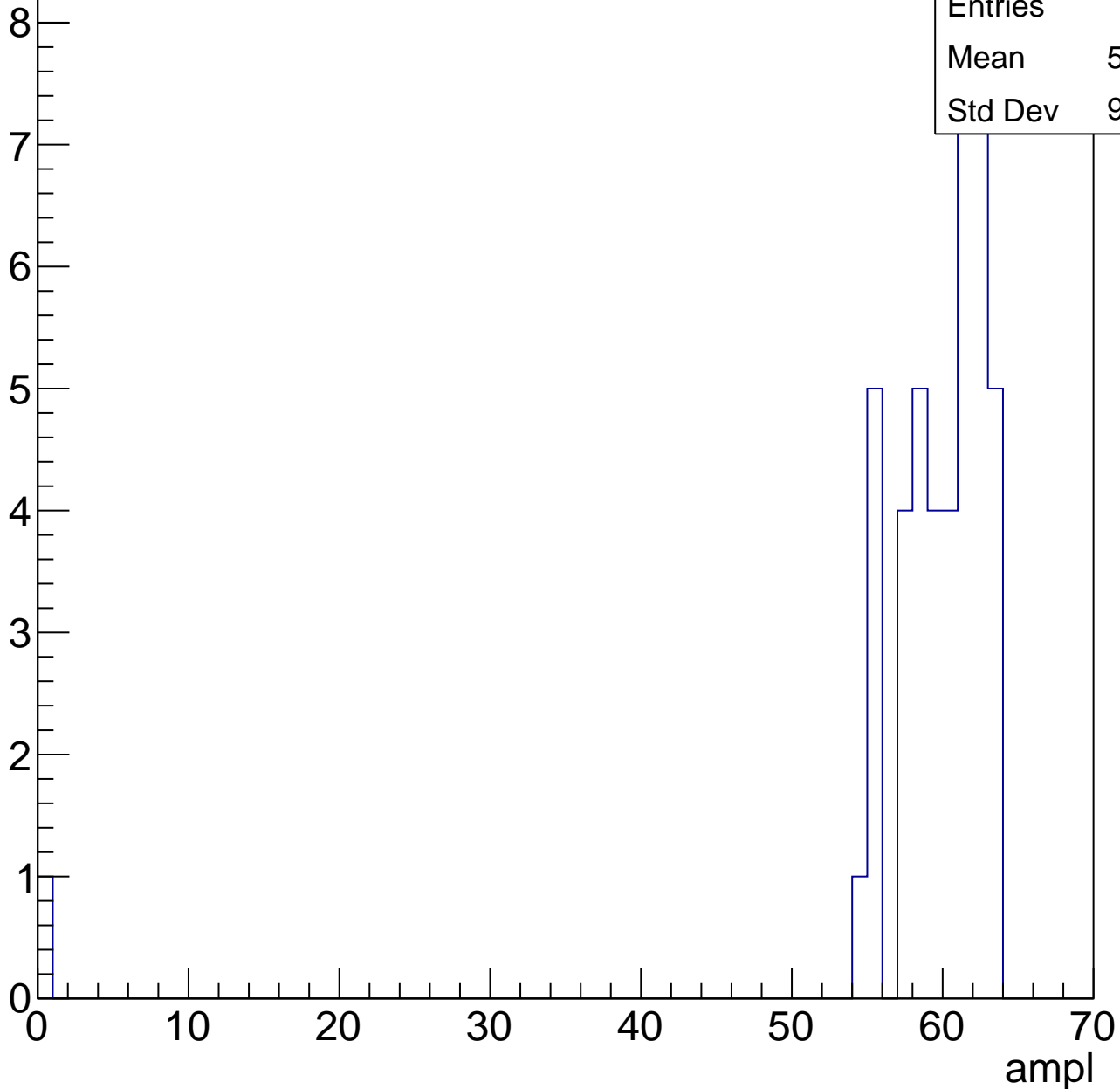


B0L002S, U17-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	58.27
Std Dev	9.154



B0L002S, U17-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

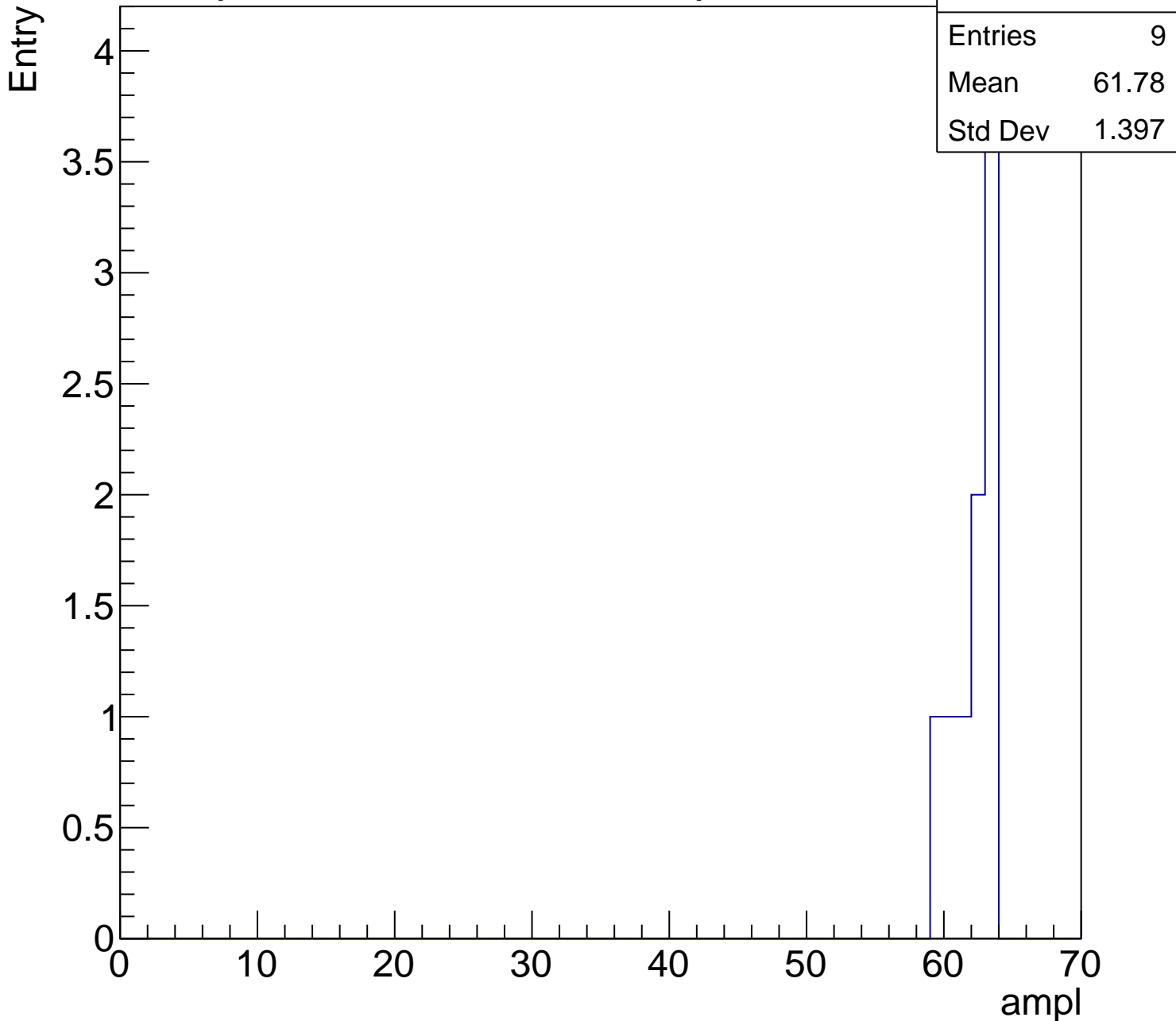
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.78
Std Dev	1.397

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch102, adc0

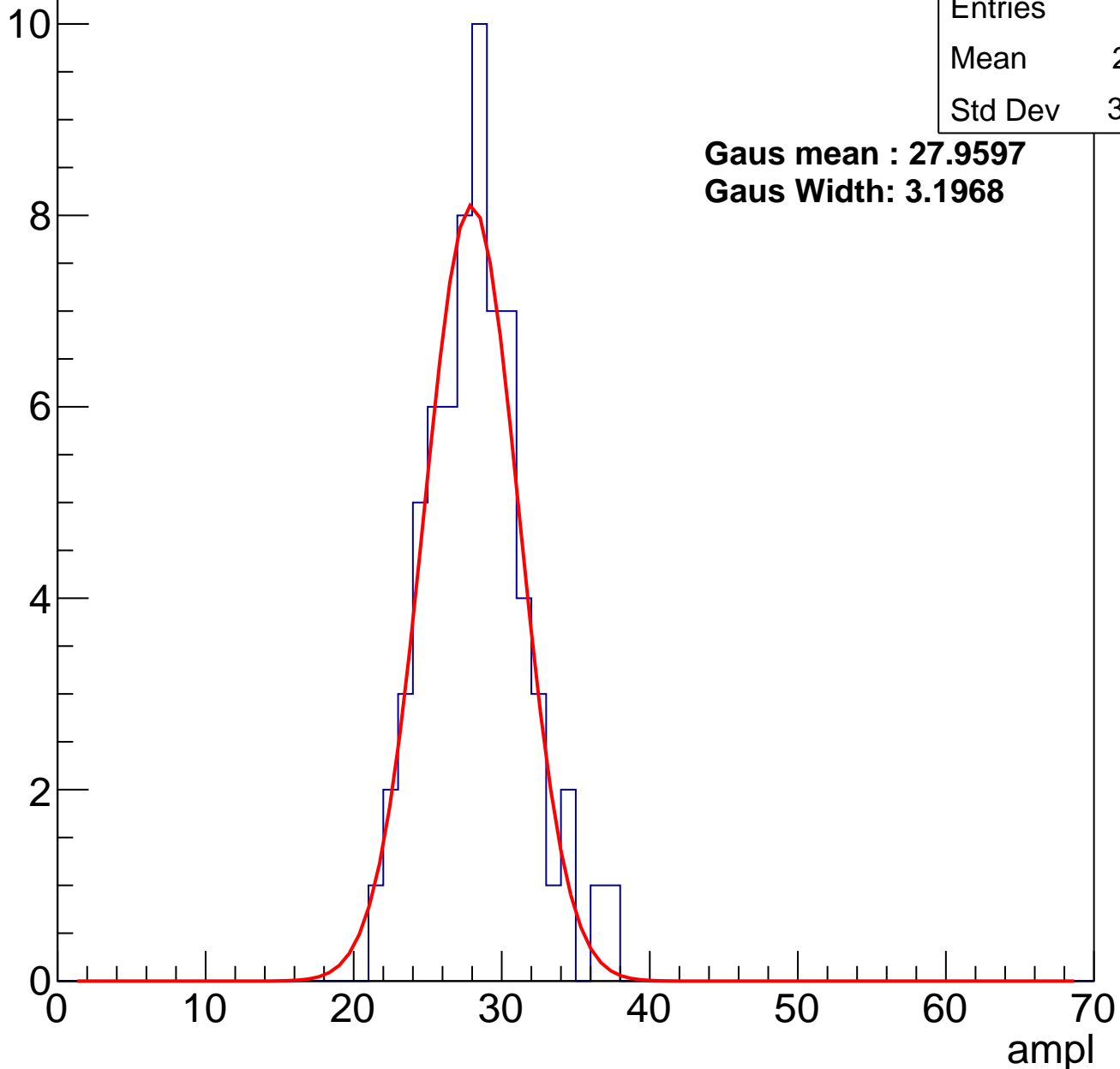
calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	27.81
Std Dev	3.279

Gaus mean : 27.9597

Gaus Width: 3.1968

Entry



B0L002S, U17-ch102, adc1

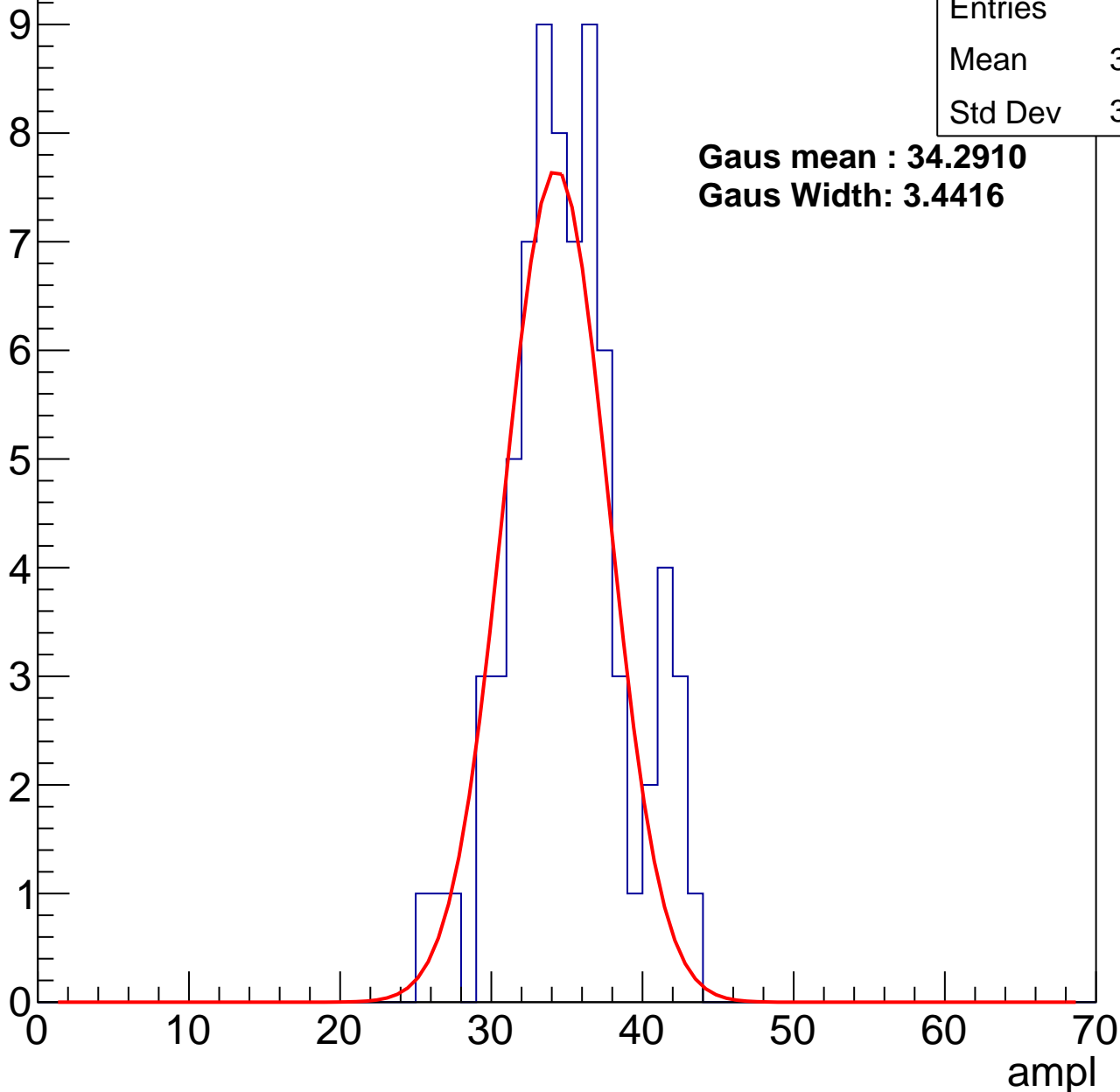
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	34.59
Std Dev	3.827

Gaus mean : 34.2910

Gaus Width: 3.4416



B0L002S, U17-ch102, adc2

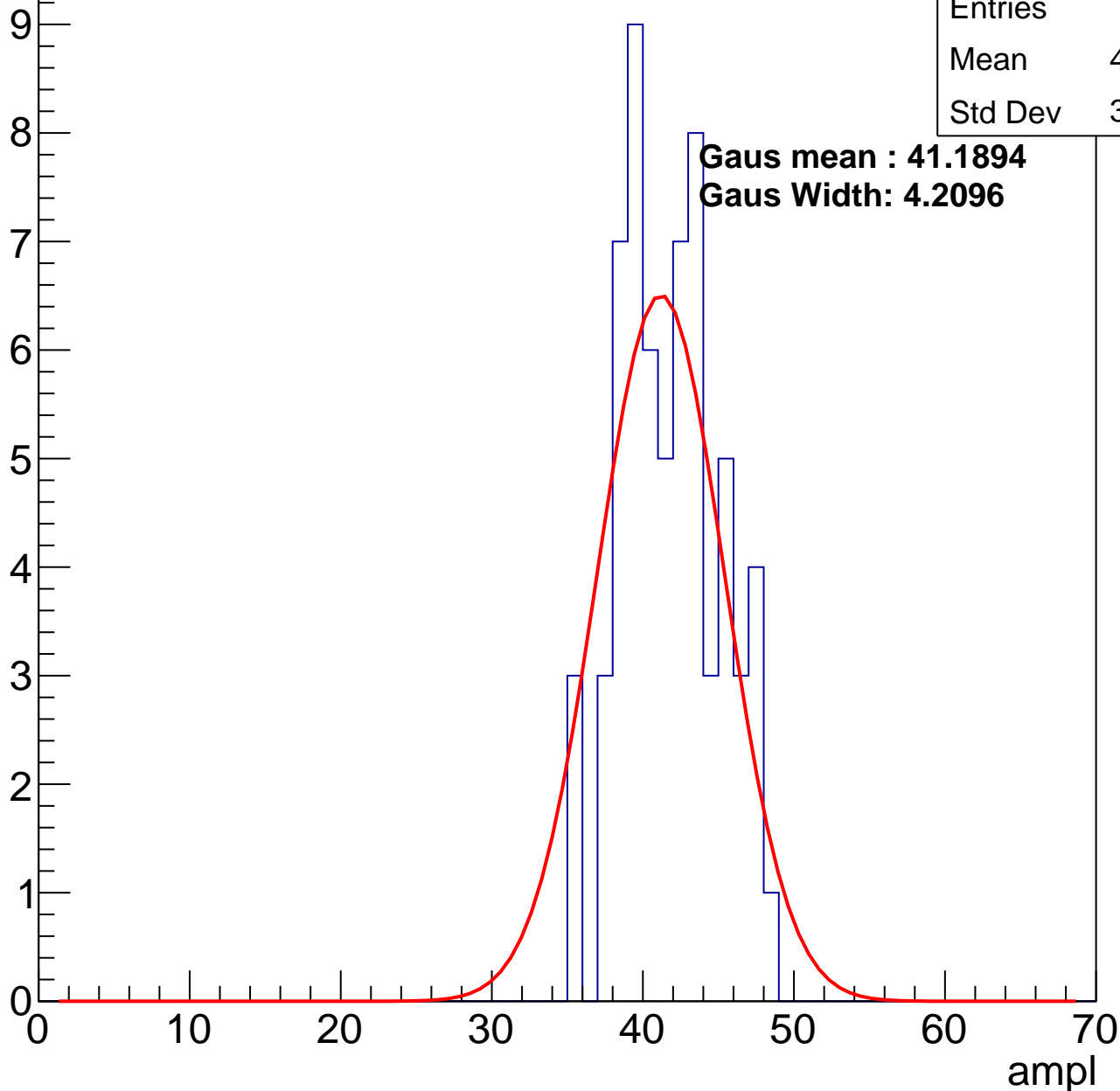
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	41.36
Std Dev	3.228

Gaus mean : 41.1894

Gaus Width: 4.2096

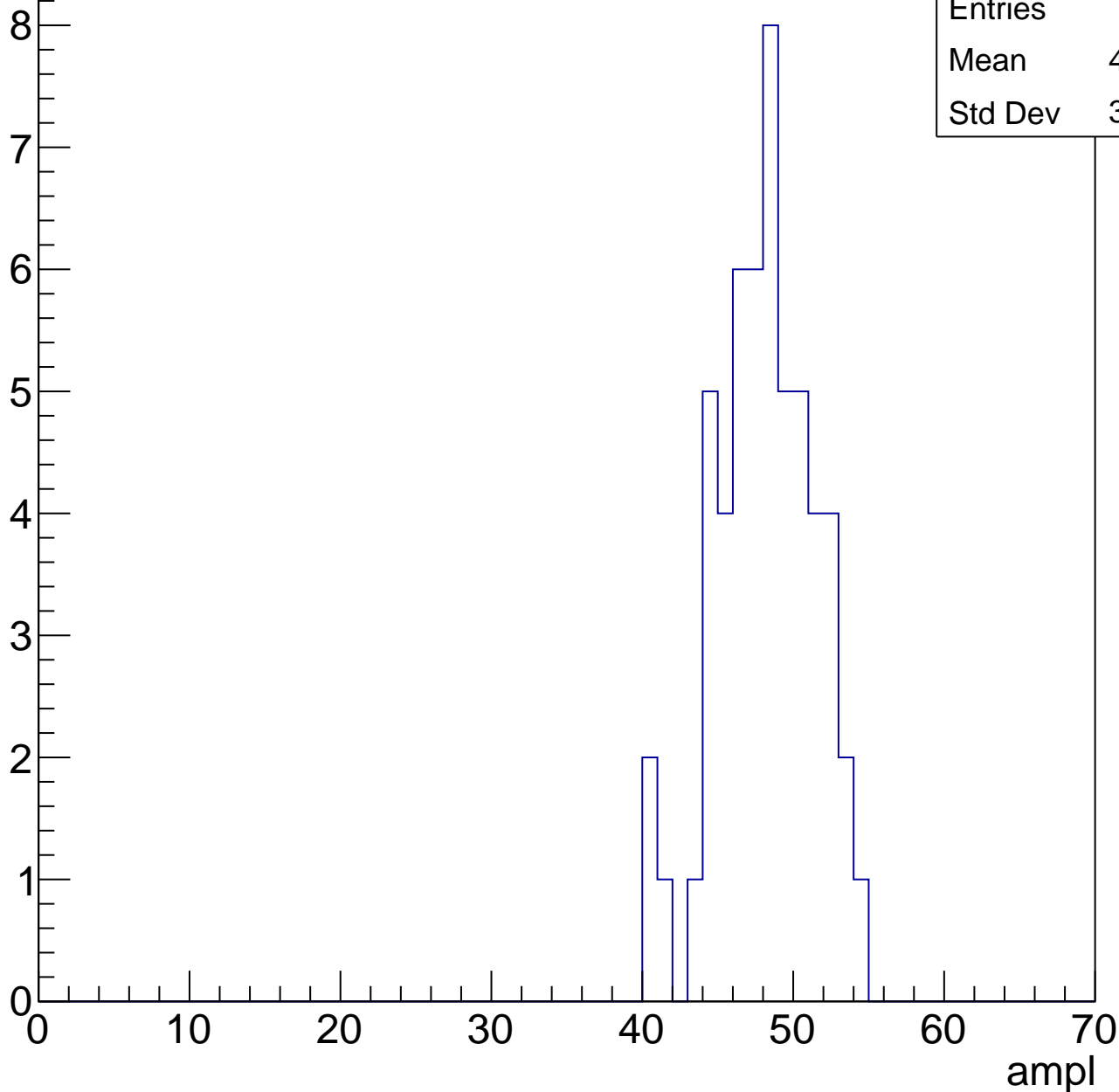


B0L002S, U17-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	47.65
Std Dev	3.198

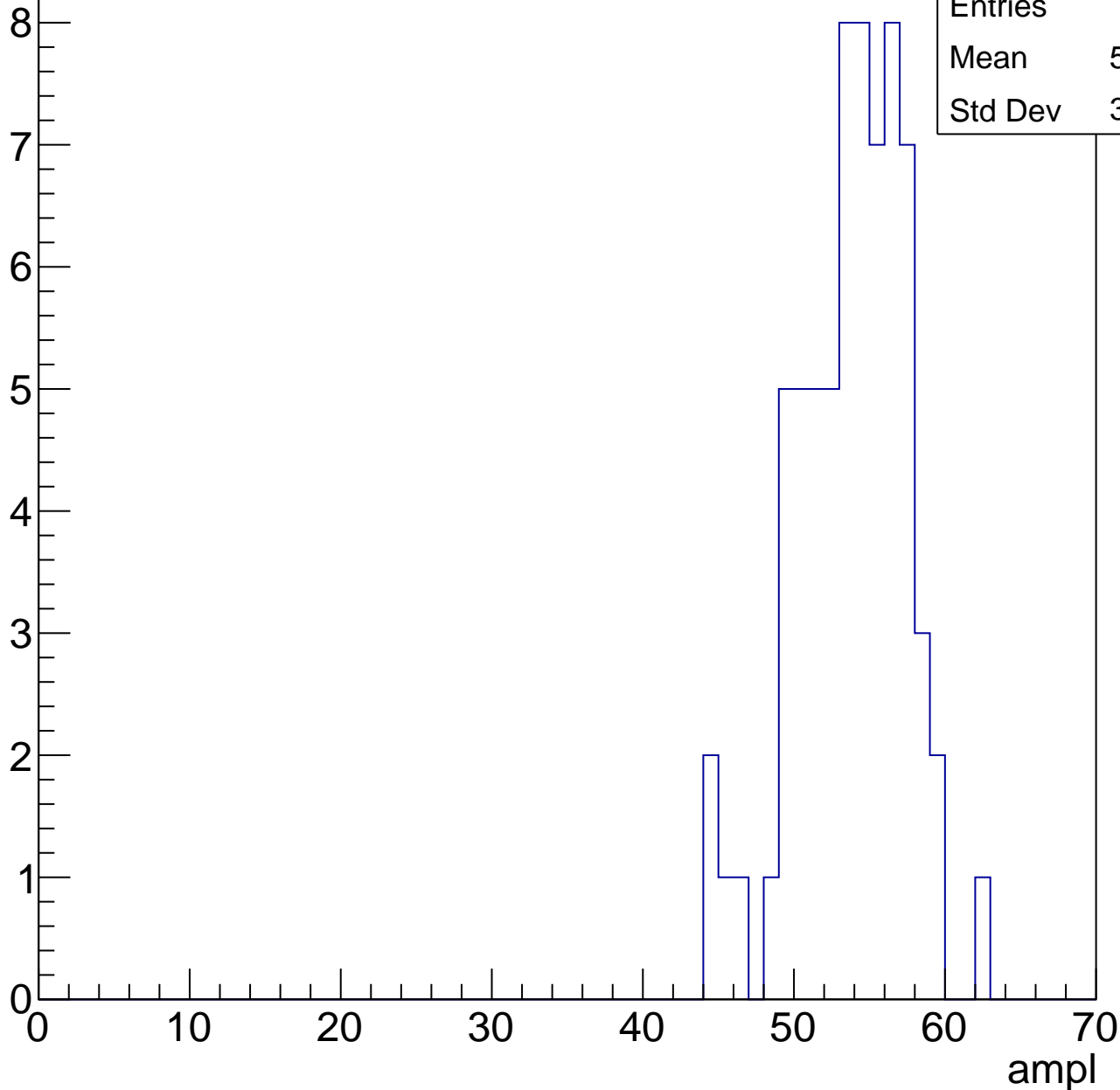


B0L002S, U17-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

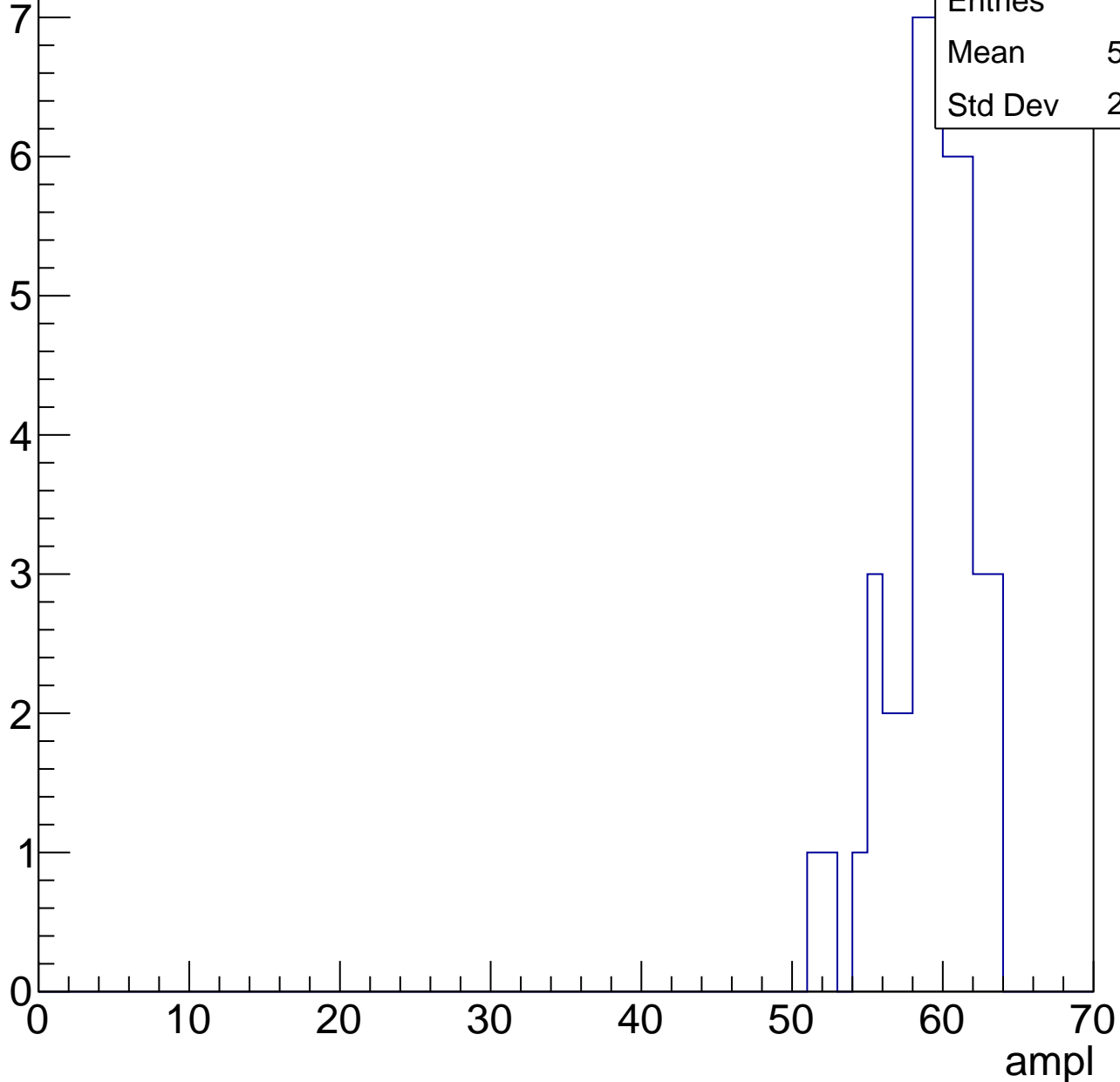
Entries	69
Mean	53.32
Std Dev	3.593



B0L002S, U17-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

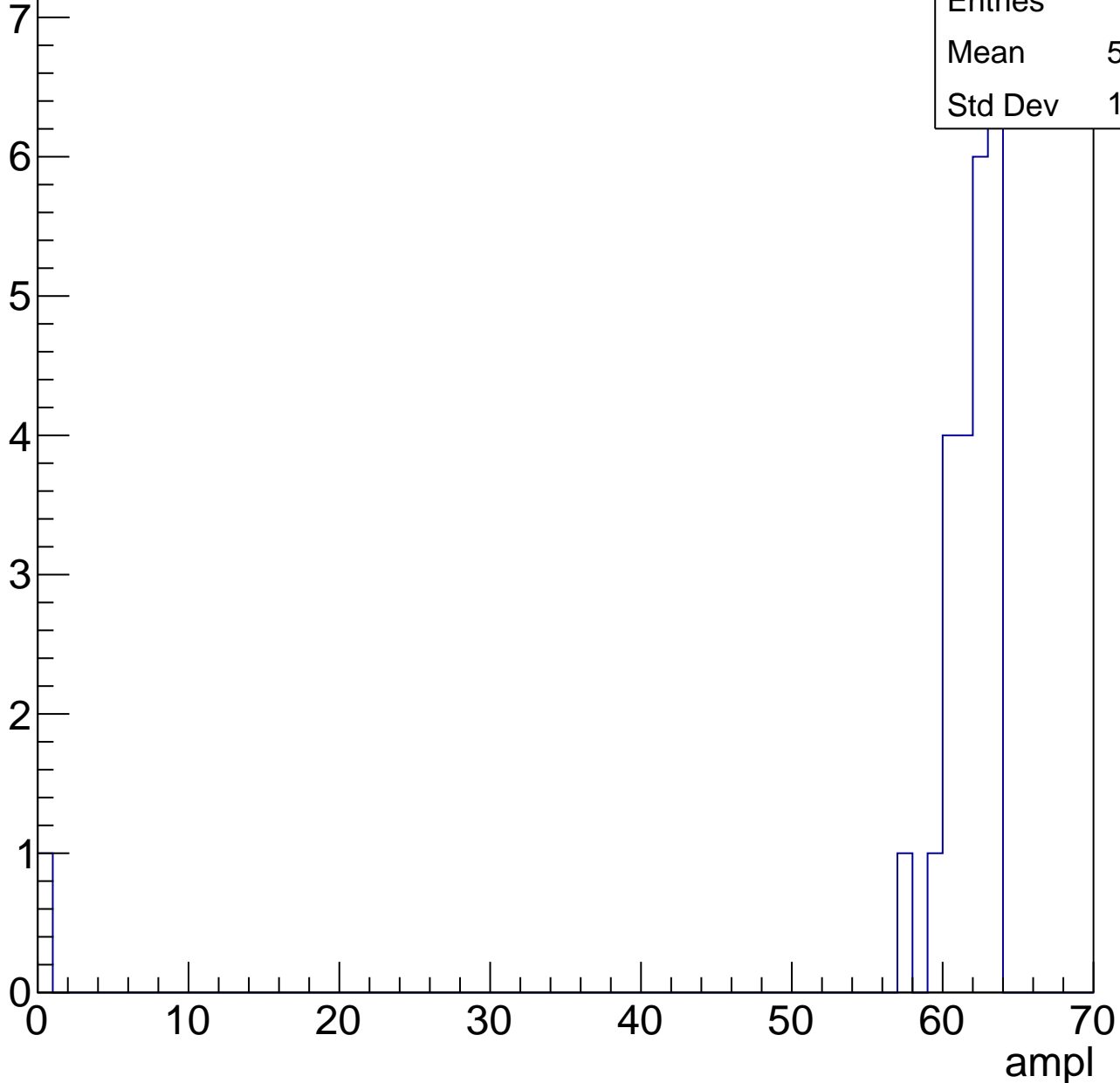


B0L002S, U17-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.88
Std Dev	12.37



B0L002S, U17-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch103, adc0

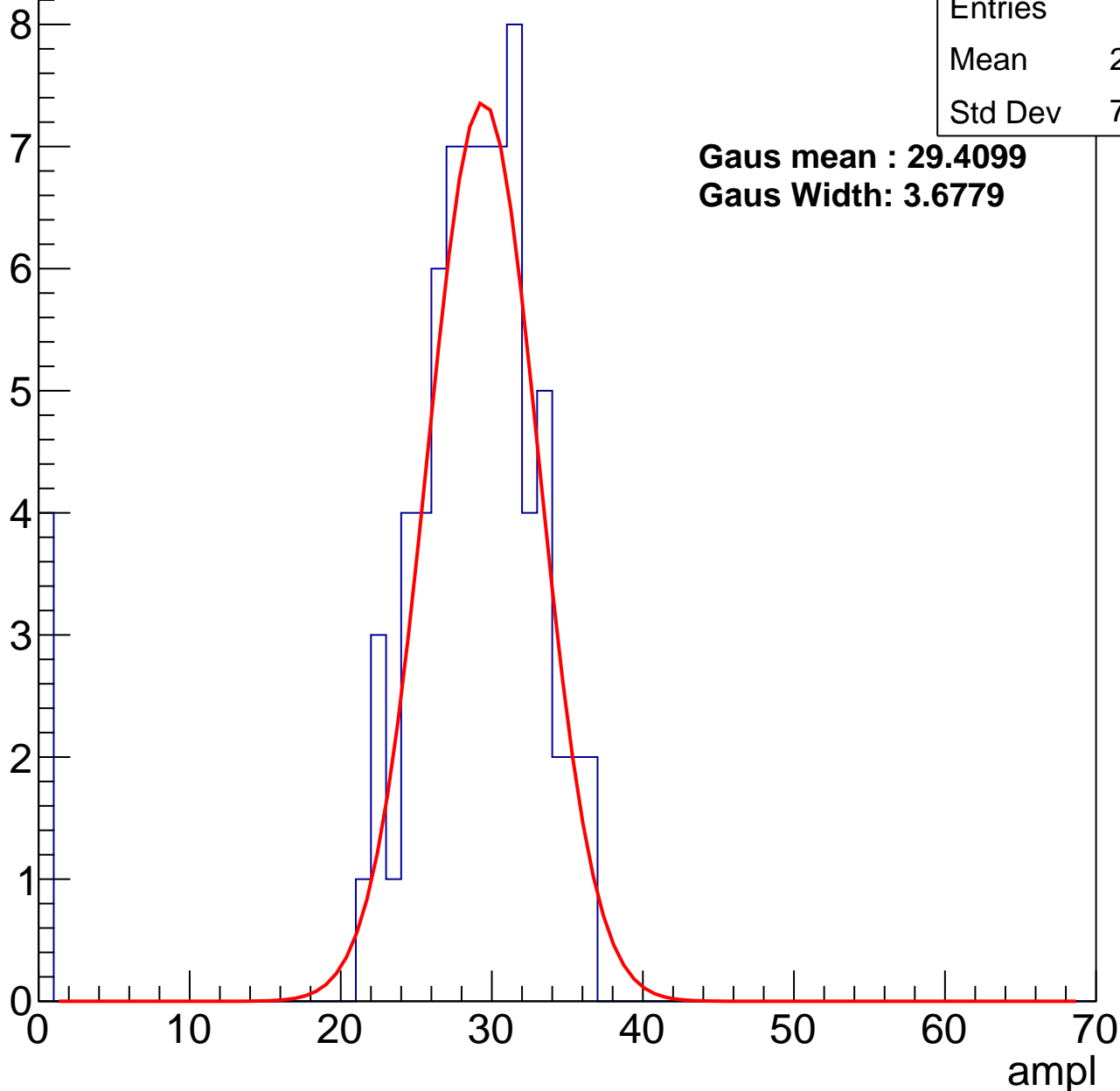
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	27.18
Std Dev	7.345

Gaus mean : 29.4099

Gaus Width: 3.6779



B0L002S, U17-ch103, adc1

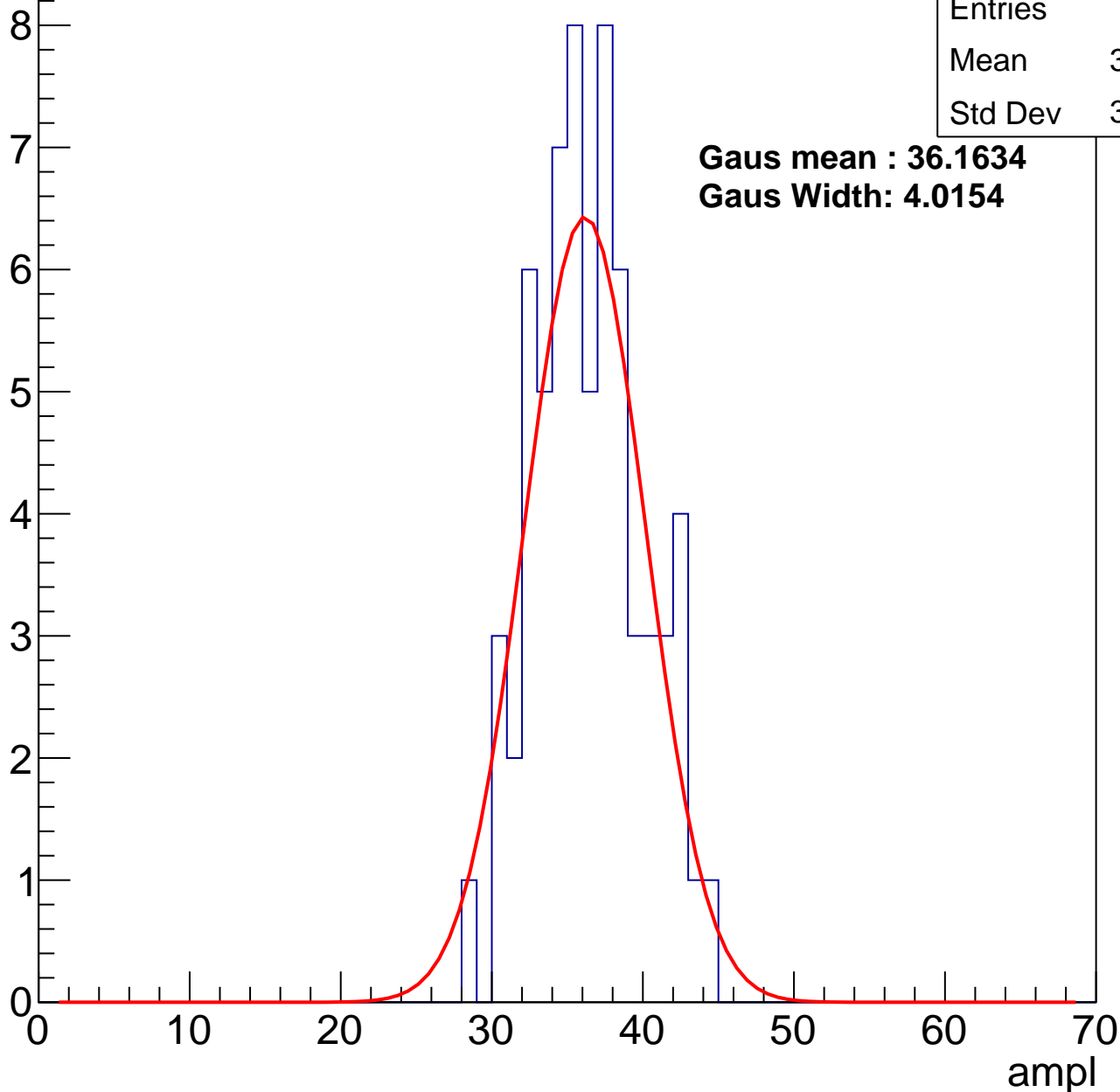
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	35.97
Std Dev	3.563

Gaus mean : 36.1634

Gaus Width: 4.0154



B0L002S, U17-ch103, adc2

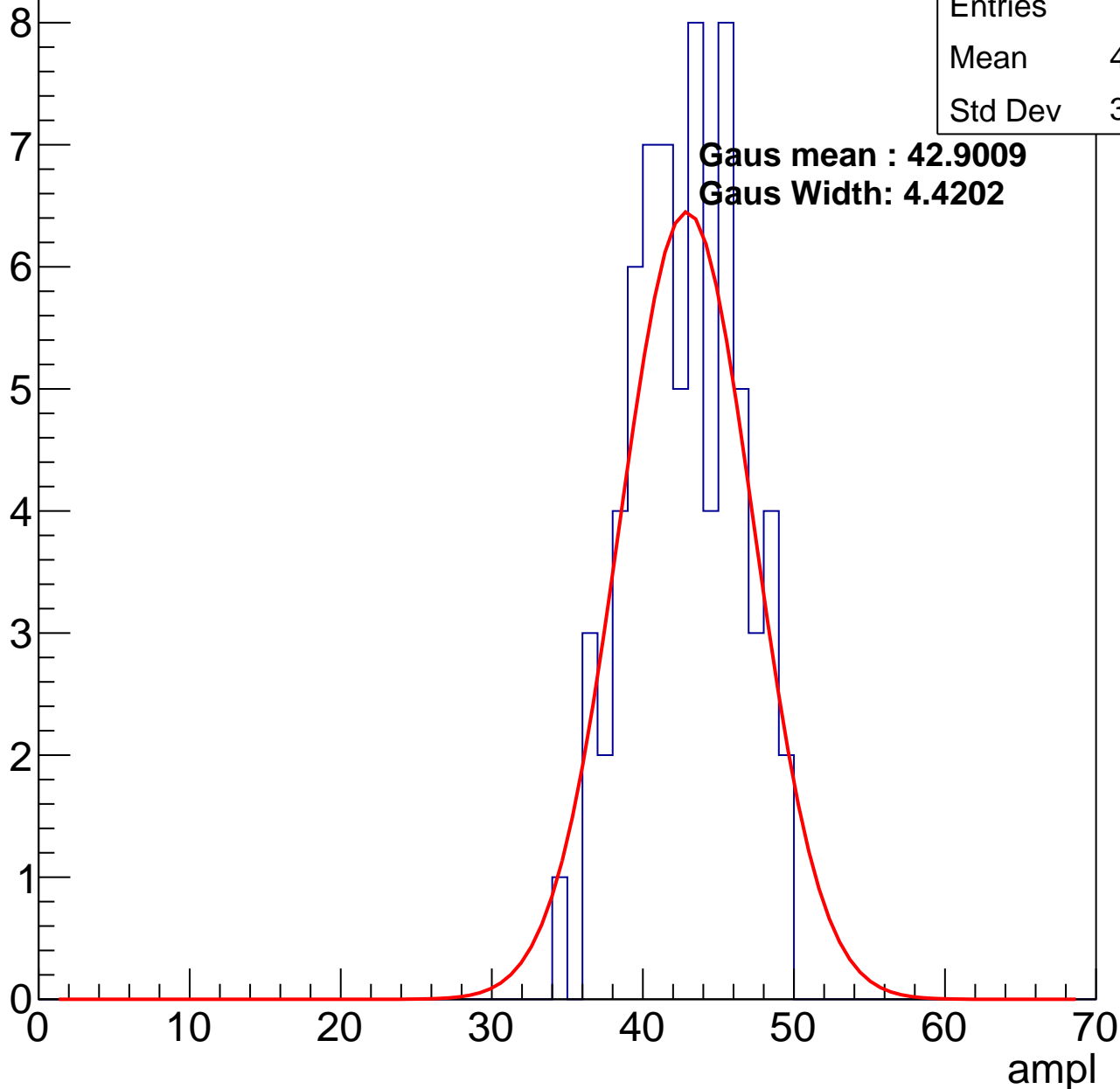
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	42.32
Std Dev	3.532

Gaus mean : 42.9009

Gaus Width: 4.4202

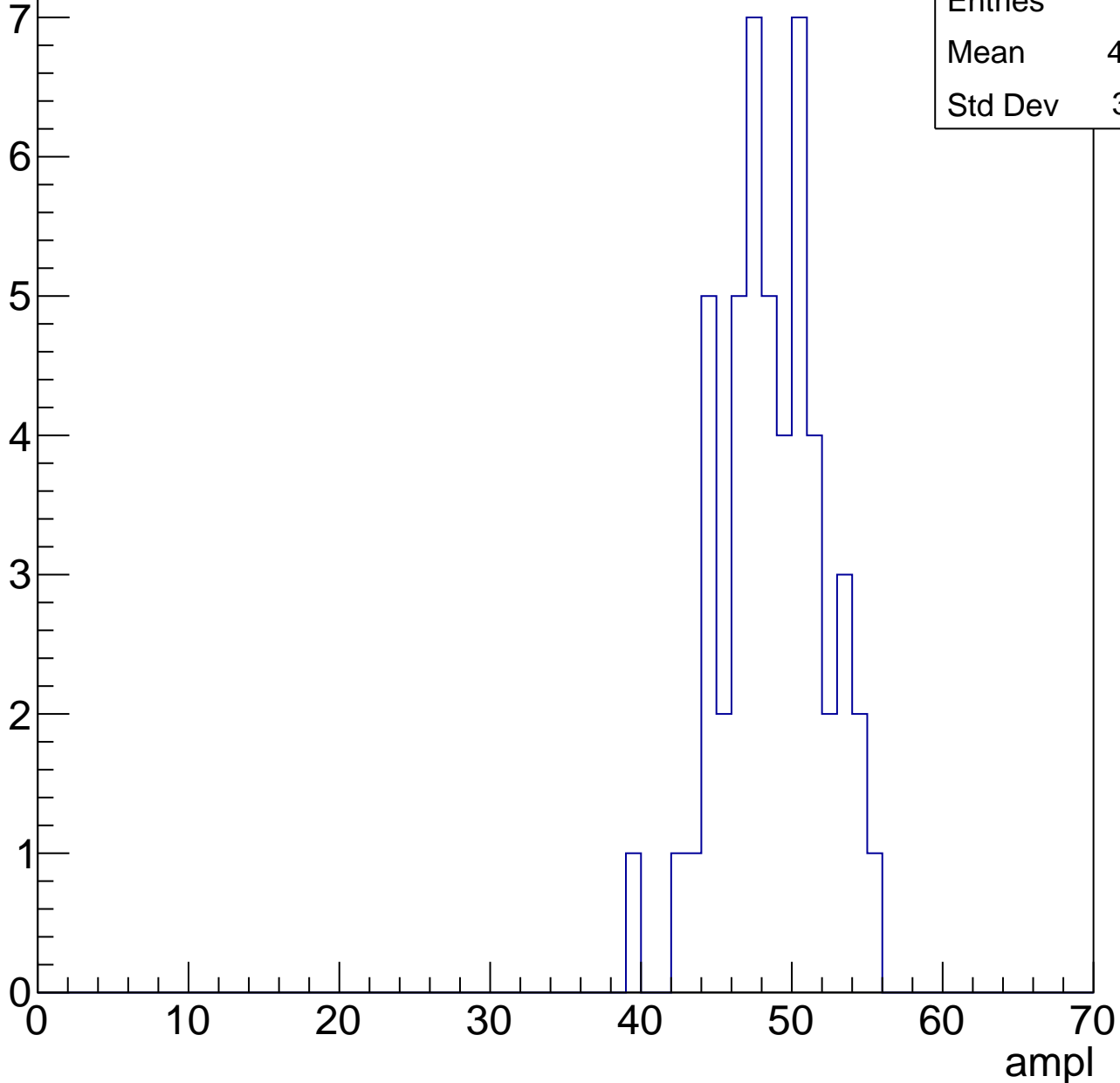


B0L002S, U17-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

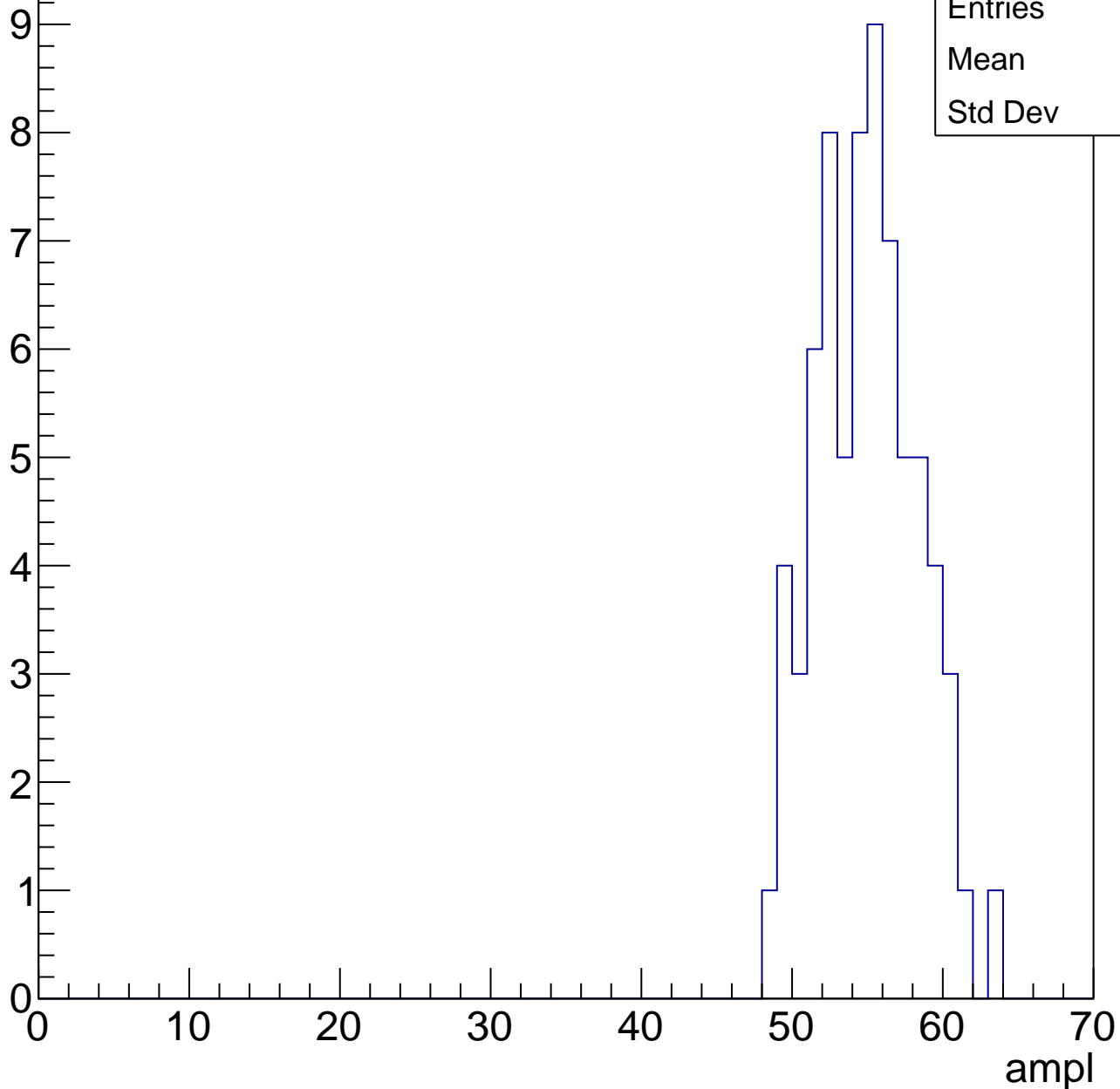
Entries	50
Mean	48.18
Std Dev	3.351



B0L002S, U17-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

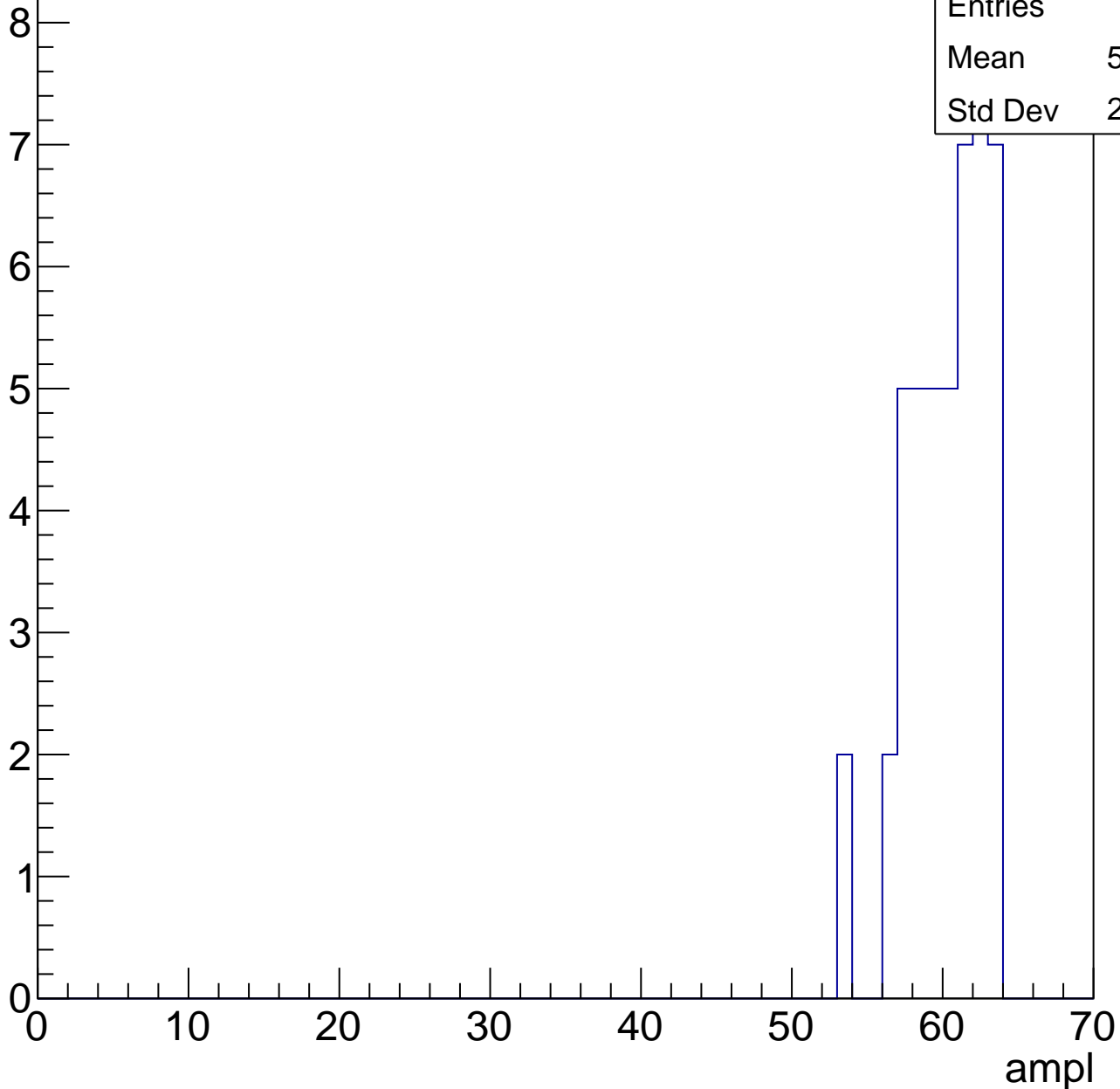


B0L002S, U17-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	59.83
Std Dev	2.556



B0L002S, U17-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

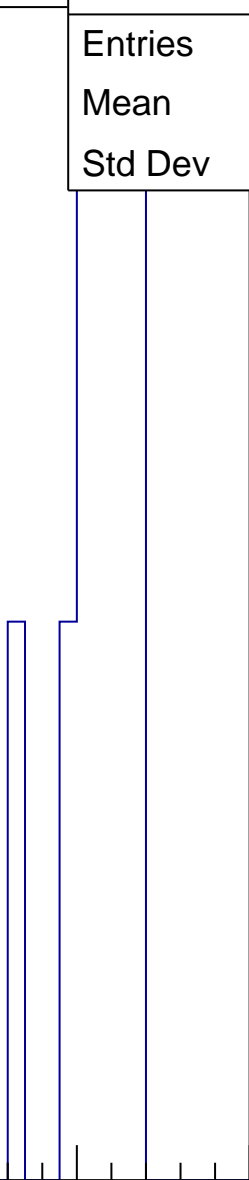
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	60.7
Std Dev	2.002

0 10 20 30 40 50 60 70

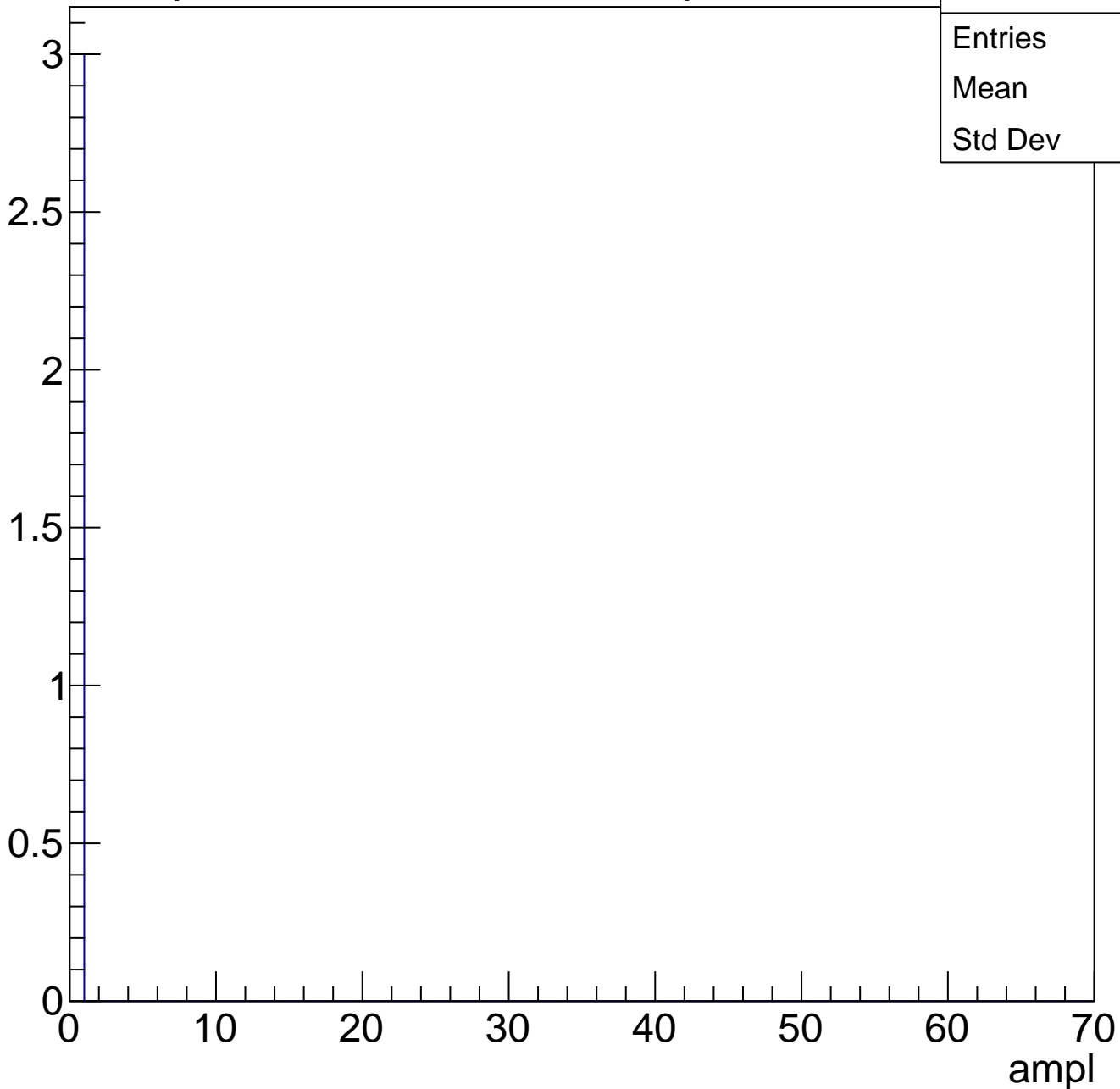
ampl



B0L002S, U17-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L002S, U17-ch104, adc0

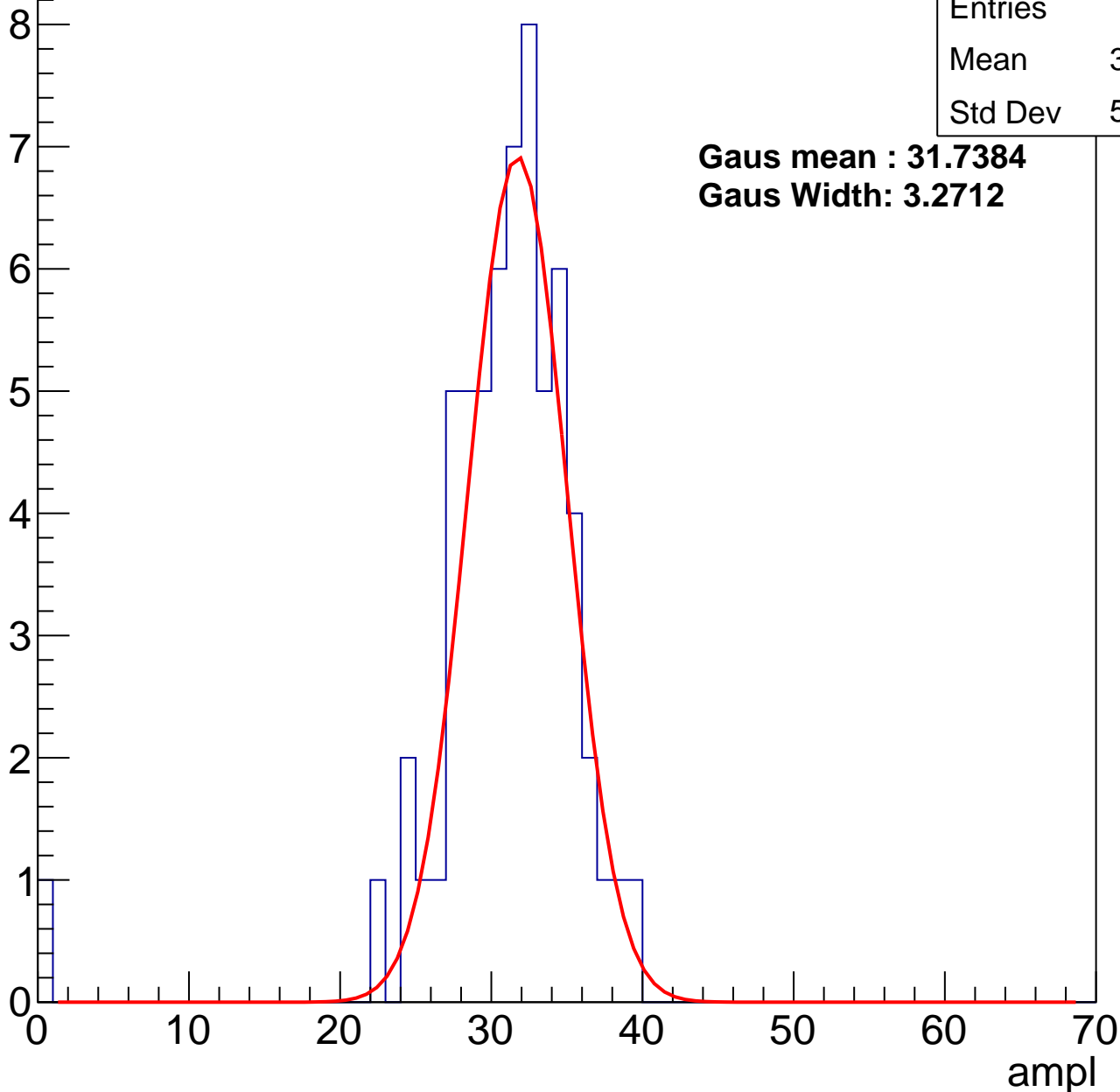
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.47
Std Dev	5.205

Gaus mean : 31.7384

Gaus Width: 3.2712



B0L002S, U17-ch104, adc1

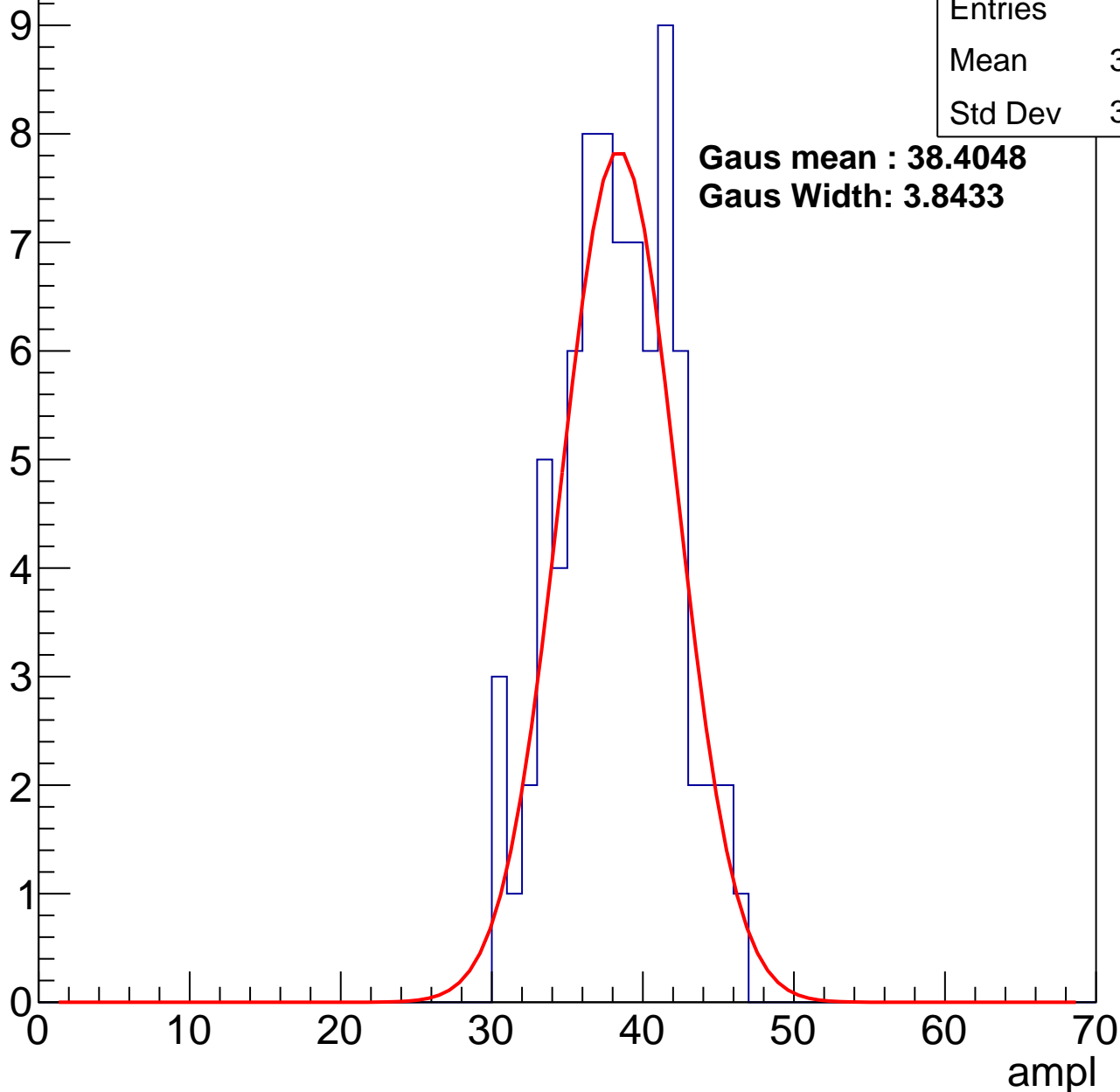
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	37.85
Std Dev	3.708

Gaus mean : 38.4048

Gaus Width: 3.8433



B0L002S, U17-ch104, adc2

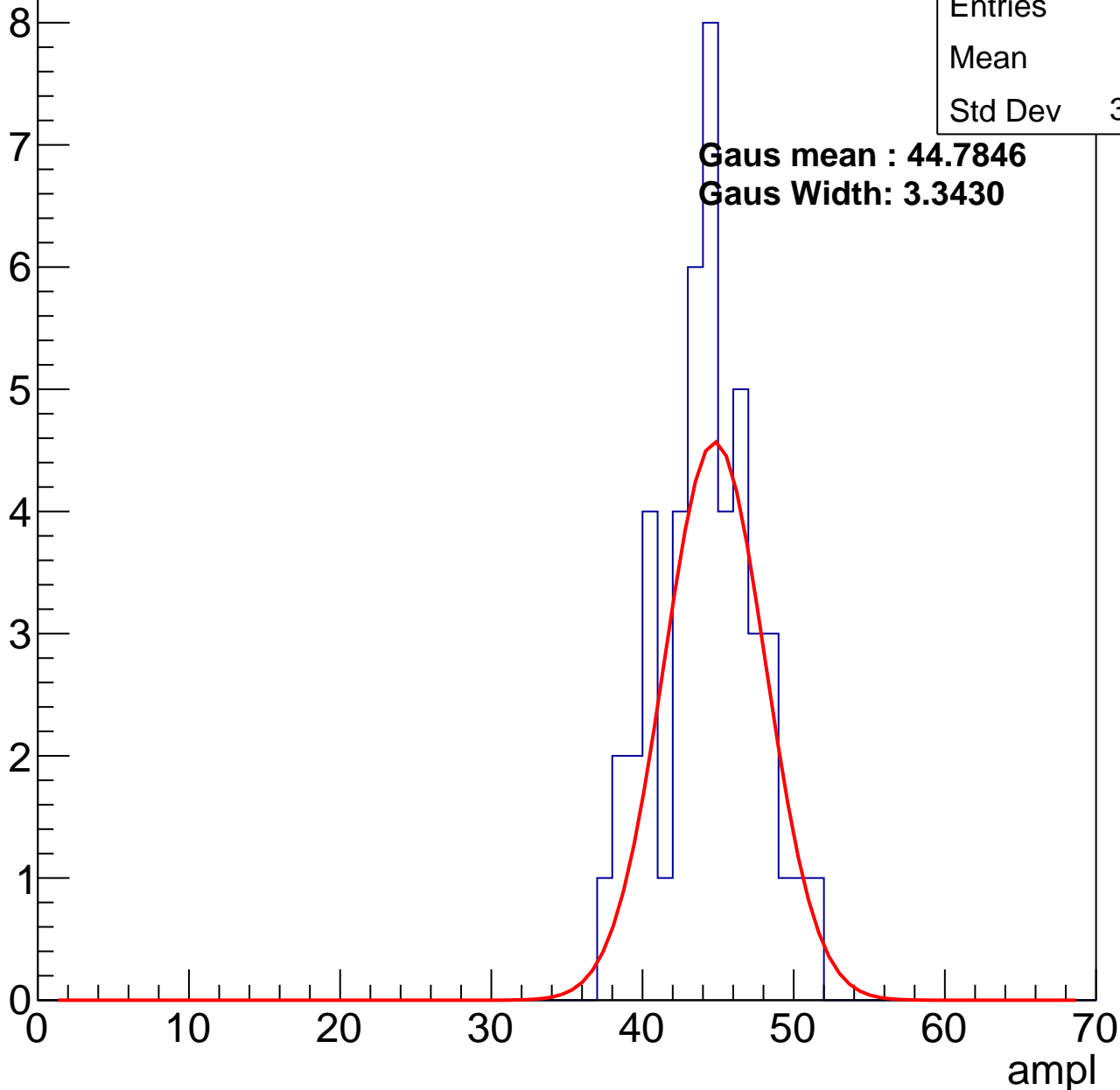
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	43.8
Std Dev	3.207

Gaus mean : 44.7846

Gaus Width: 3.3430

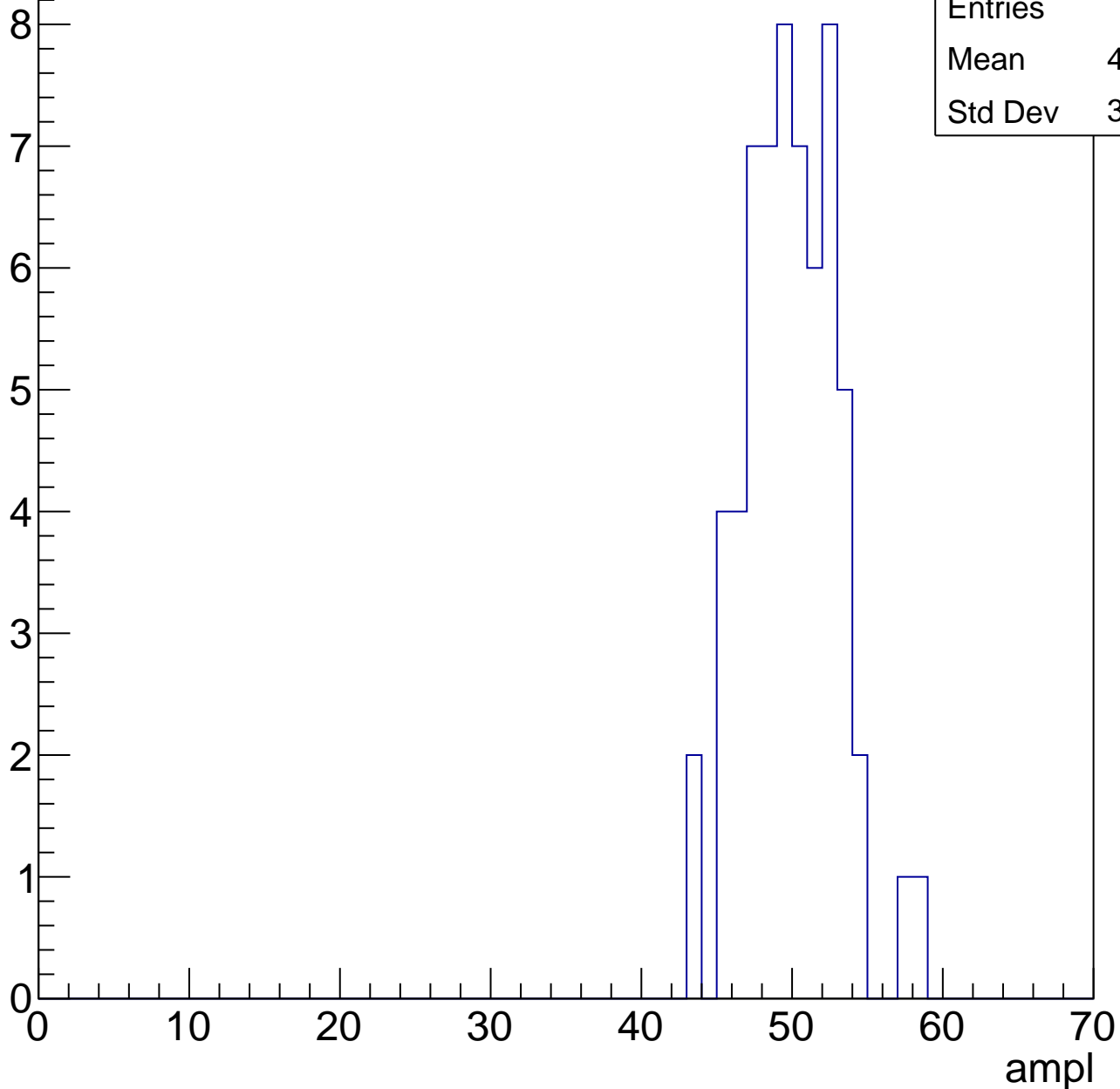


B0L002S, U17-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	49.47
Std Dev	3.036

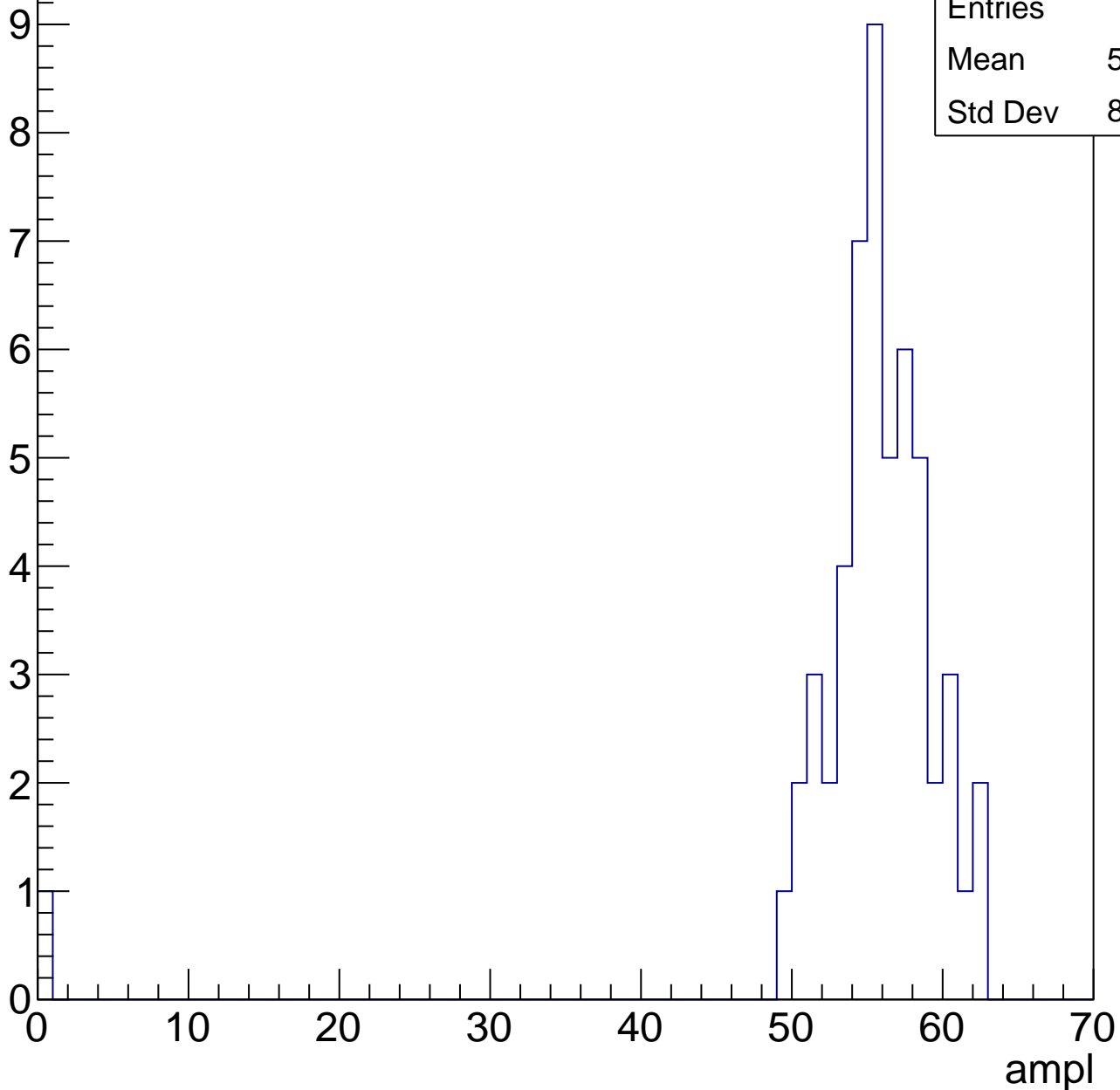


B0L002S, U17-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

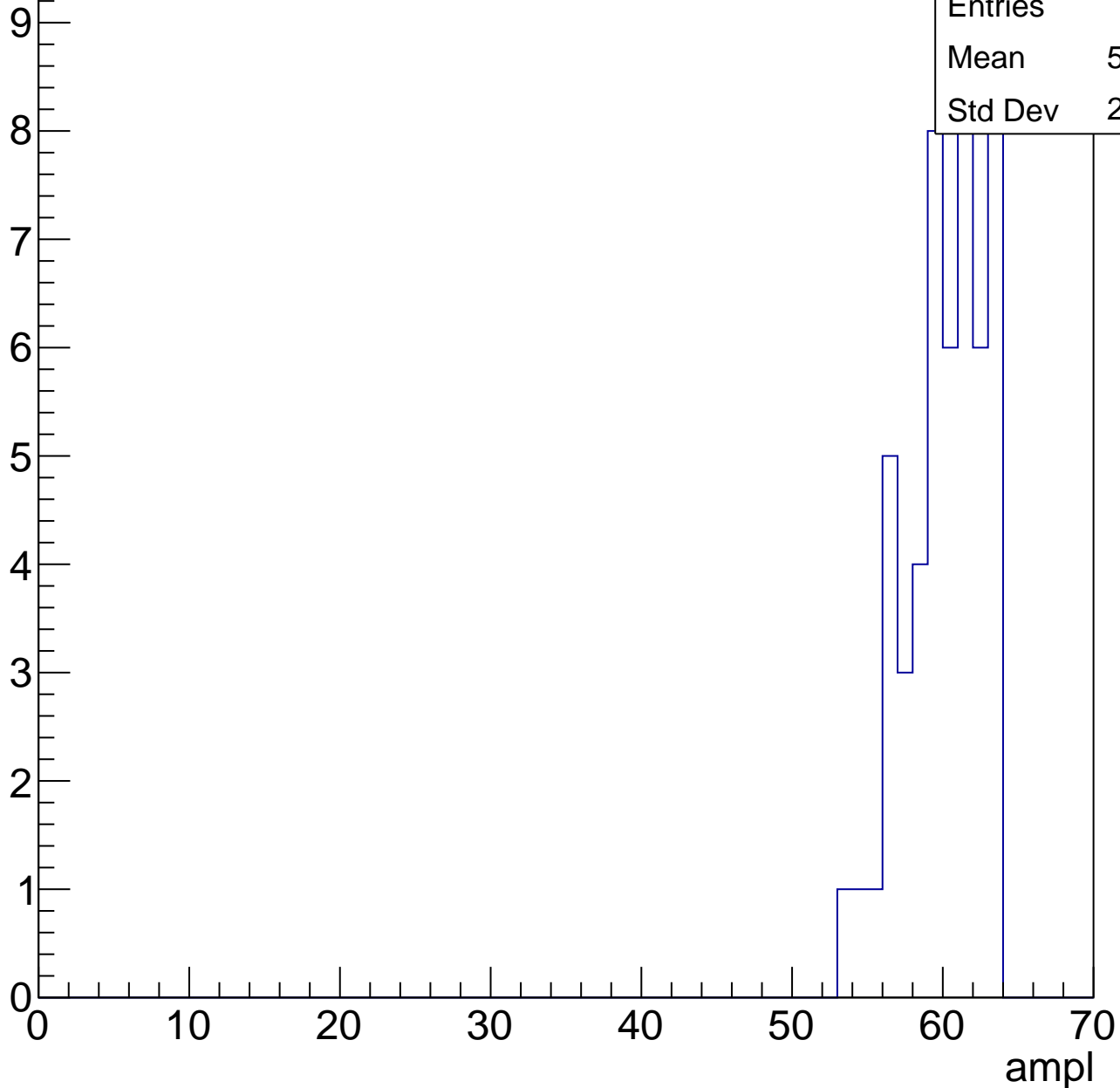
Entries	53
Mean	54.45
Std Dev	8.125



B0L002S, U17-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

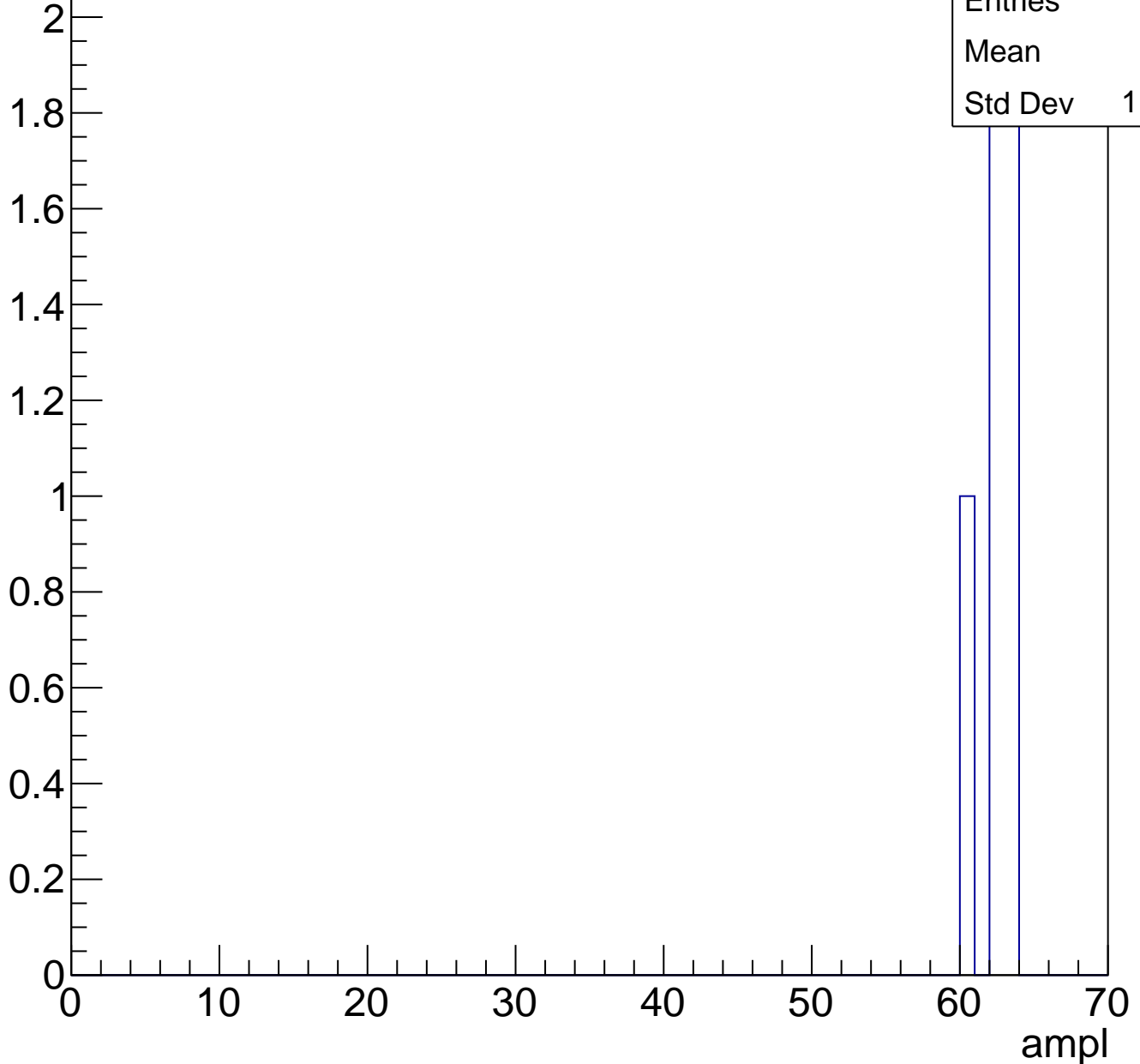
Entry



B0L002S, U17-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch105, adc0

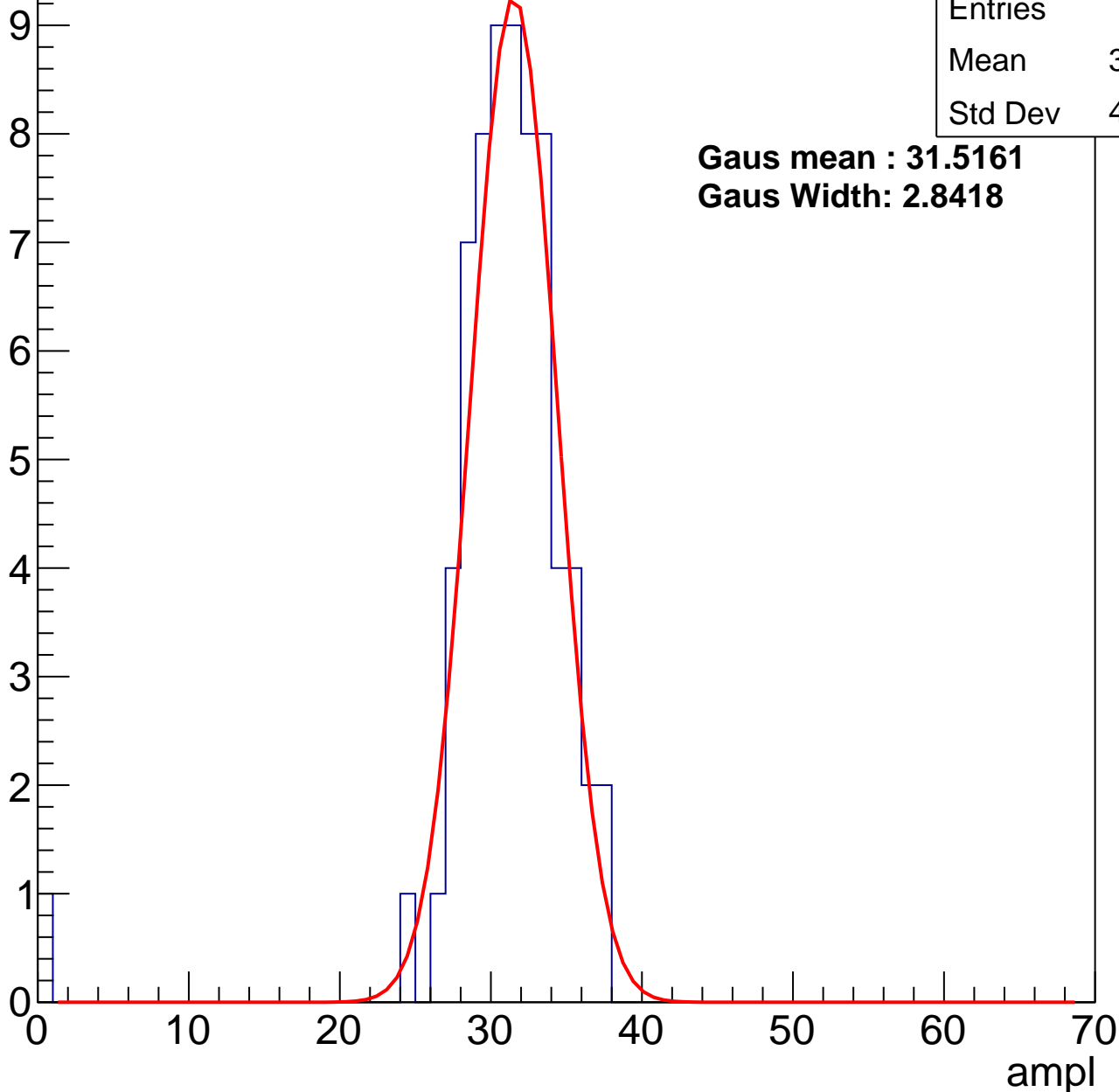
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.54
Std Dev	4.619

Gaus mean : 31.5161

Gaus Width: 2.8418



B0L002S, U17-ch105, adc1

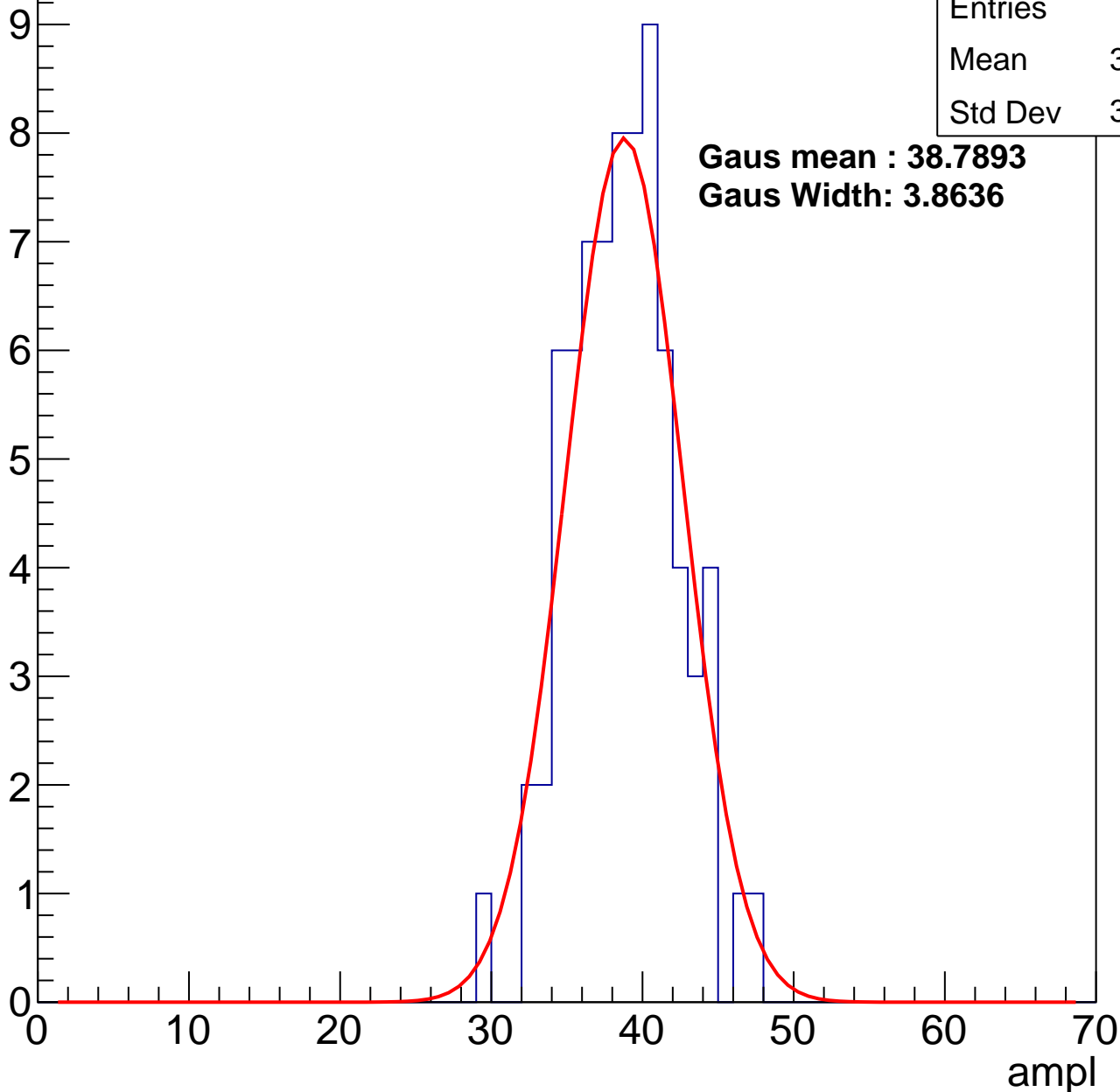
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.29
Std Dev	3.479

Gaus mean : 38.7893

Gaus Width: 3.8636



B0L002S, U17-ch105, adc2

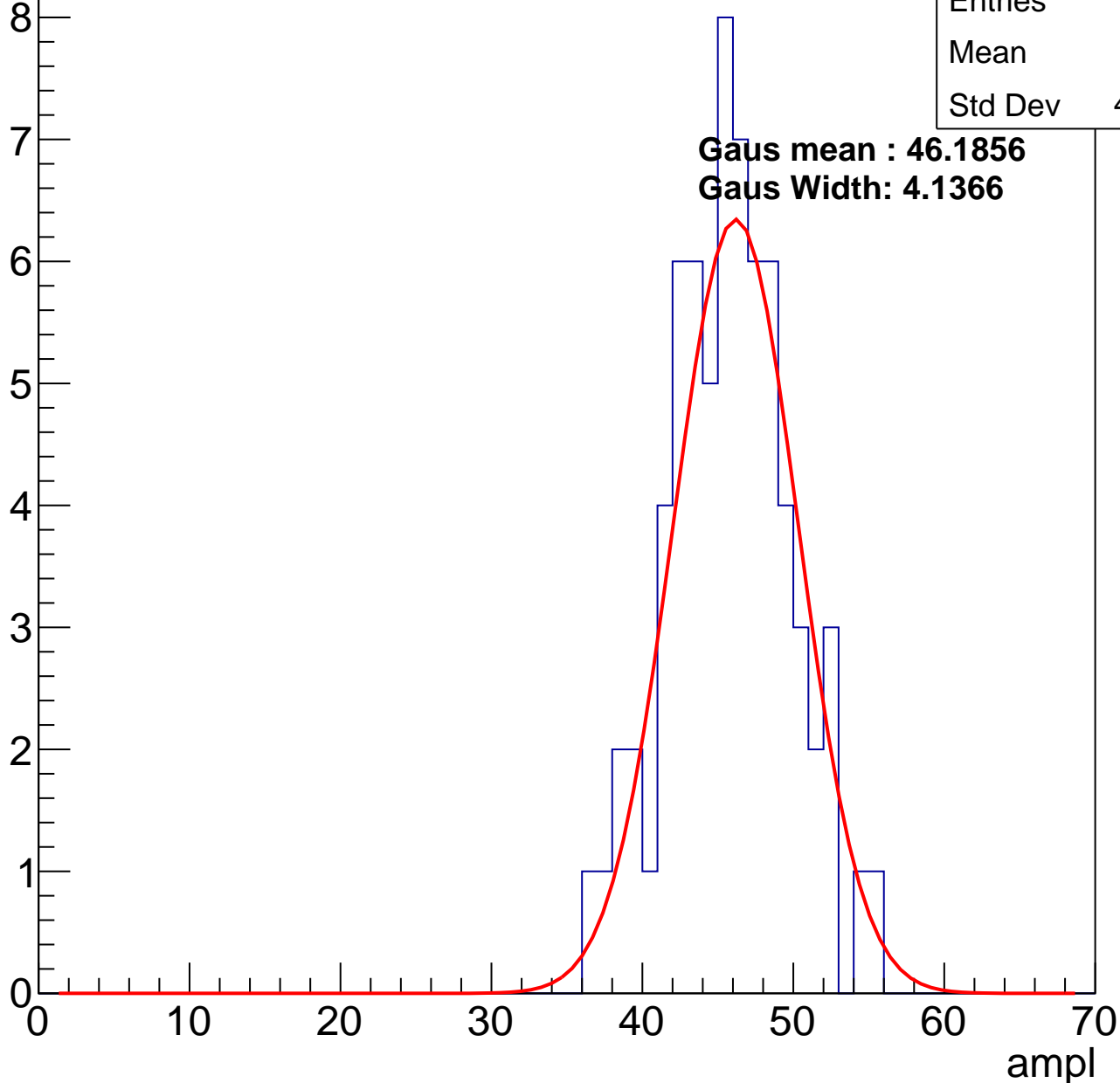
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	45.3
Std Dev	4.001

Gaus mean : 46.1856

Gaus Width: 4.1366

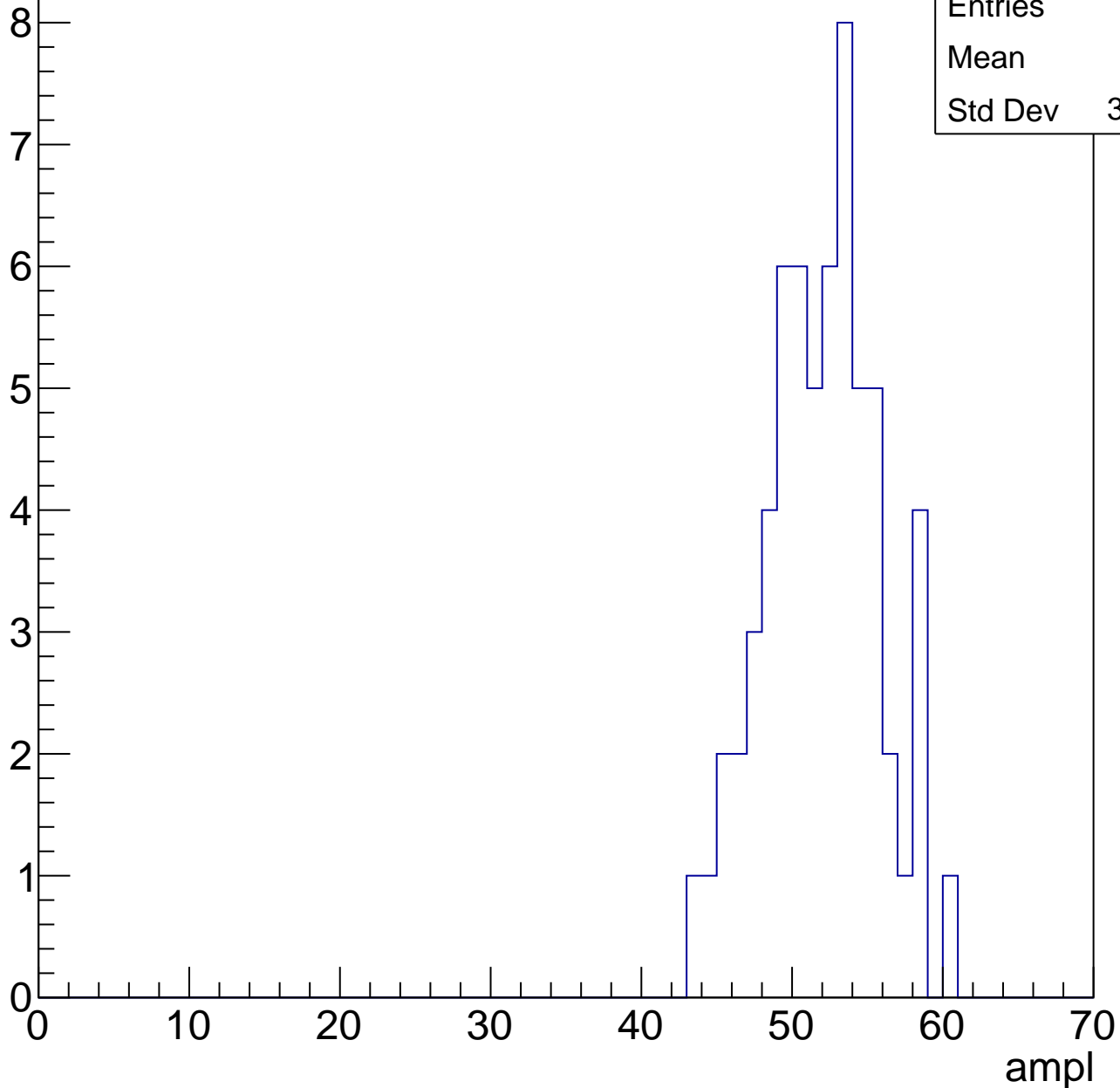


B0L002S, U17-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.5
Std Dev	3.723

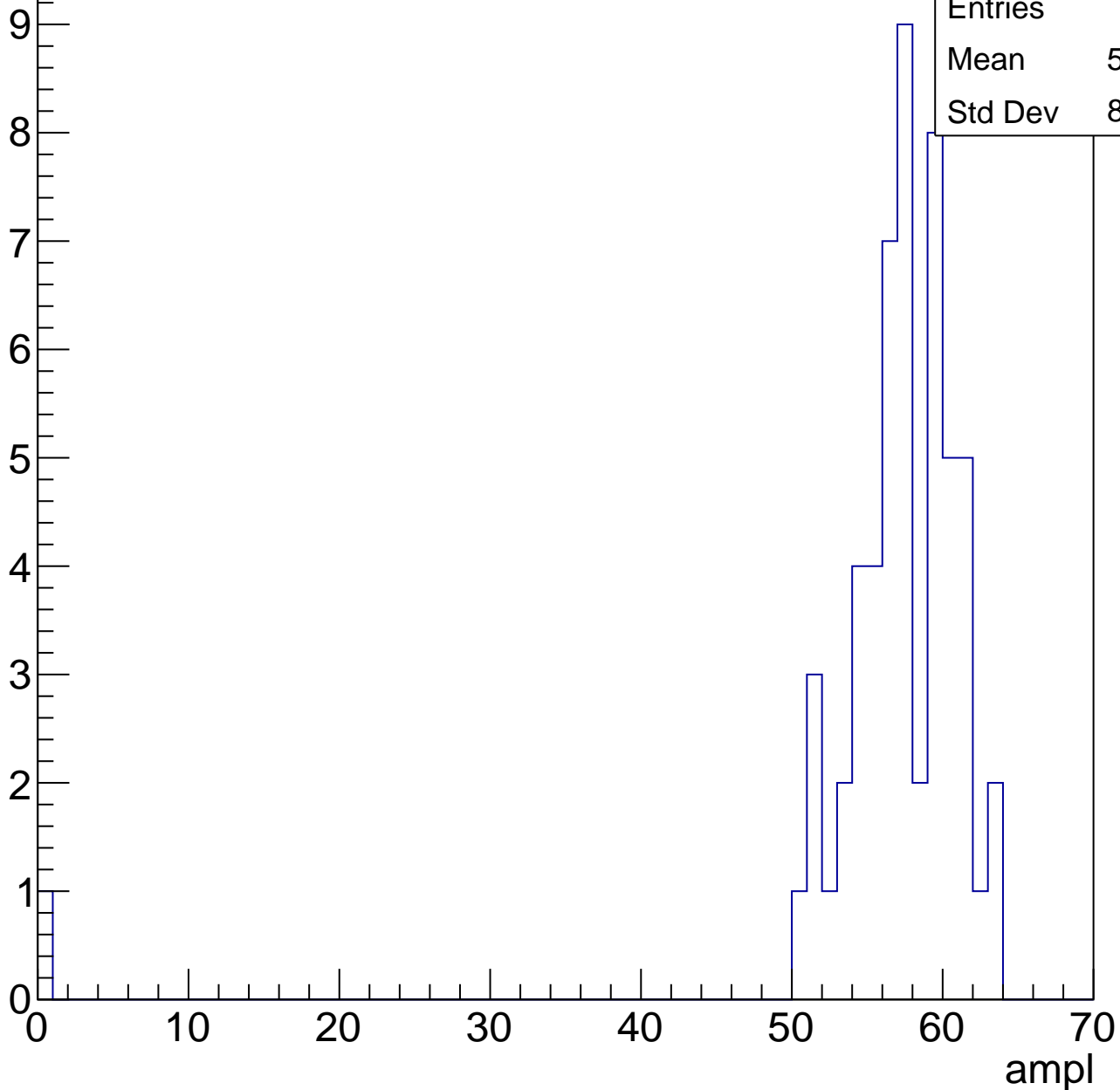


B0L002S, U17-ch105, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.05
Std Dev	8.234

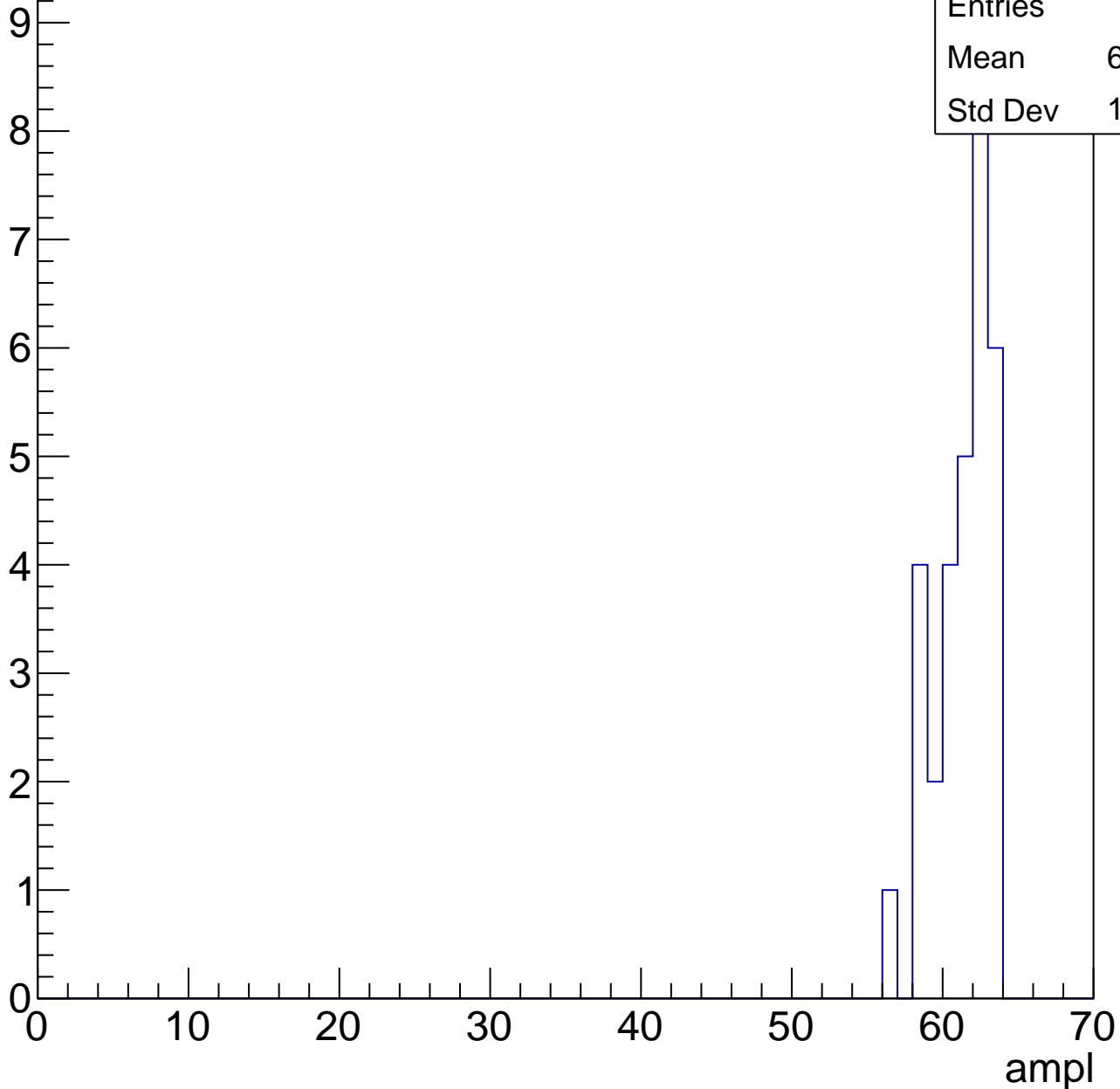


B0L002S, U17-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	60.87
Std Dev	1.845



B0L002S, U17-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch106, adc0

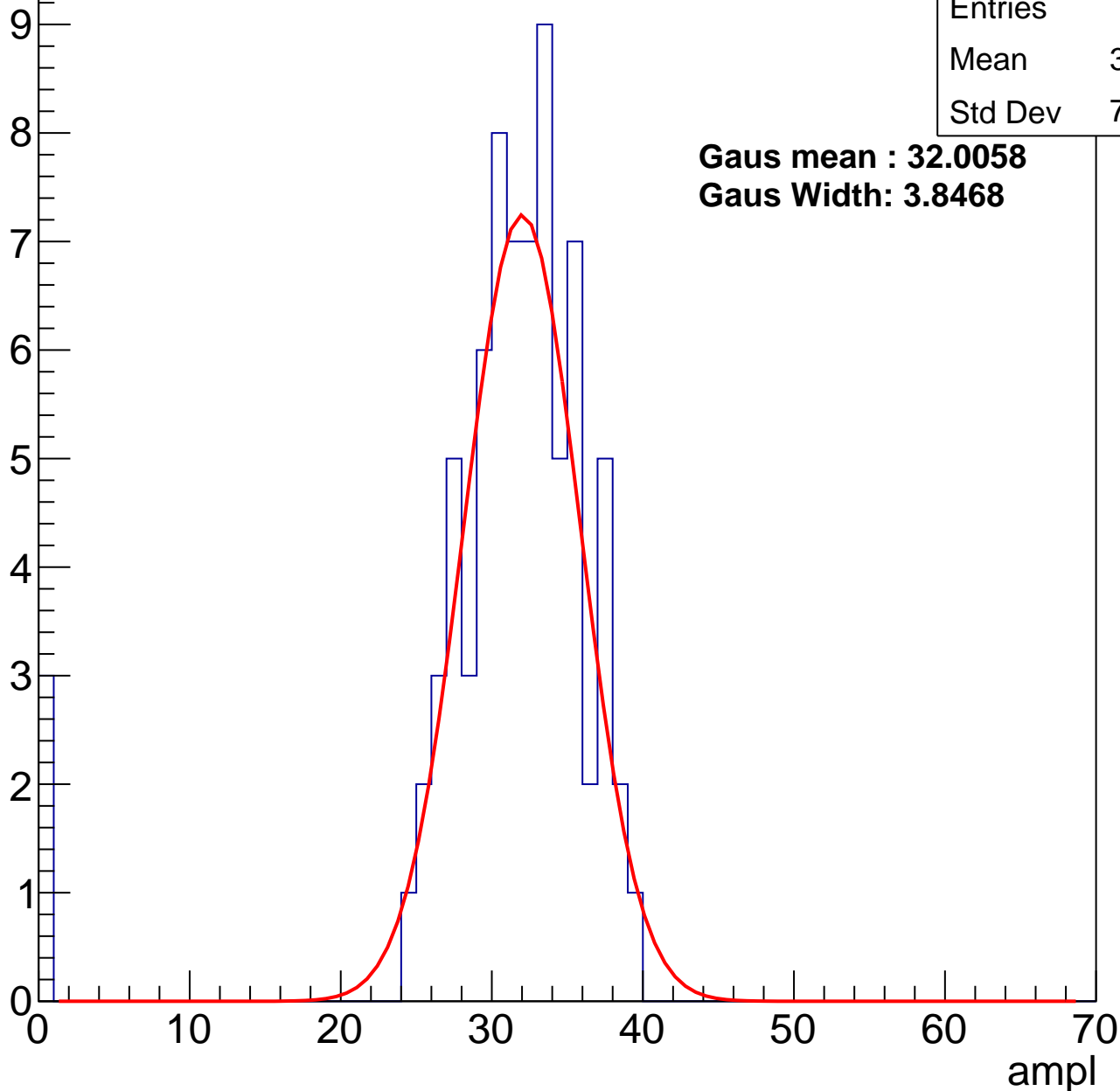
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	30.39
Std Dev	7.054

Gaus mean : 32.0058

Gaus Width: 3.8468



B0L002S, U17-ch106, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	39.17
Std Dev	3.417

Gaus mean : 39.9814

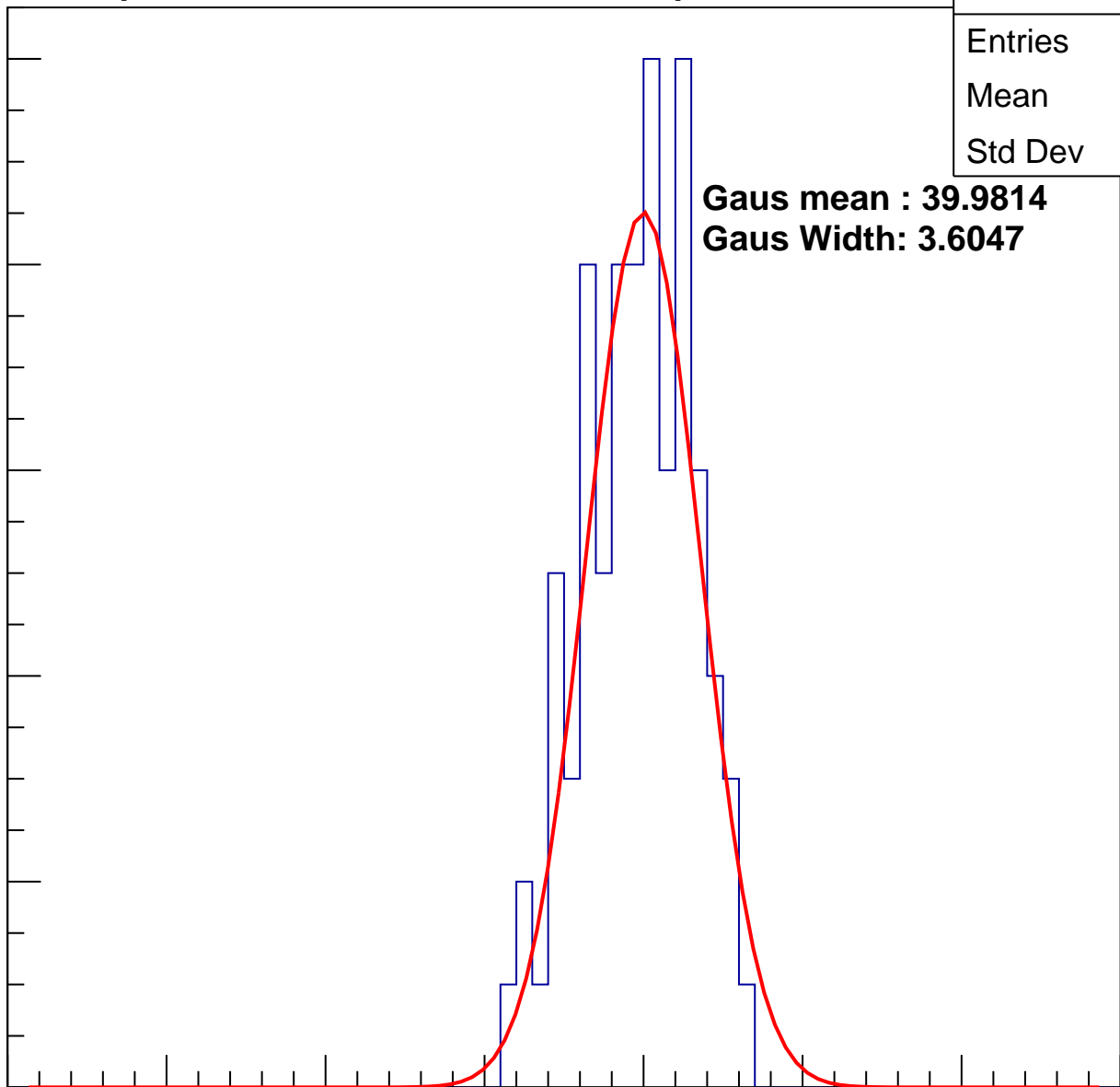
Gaus Width: 3.6047

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch106, adc2

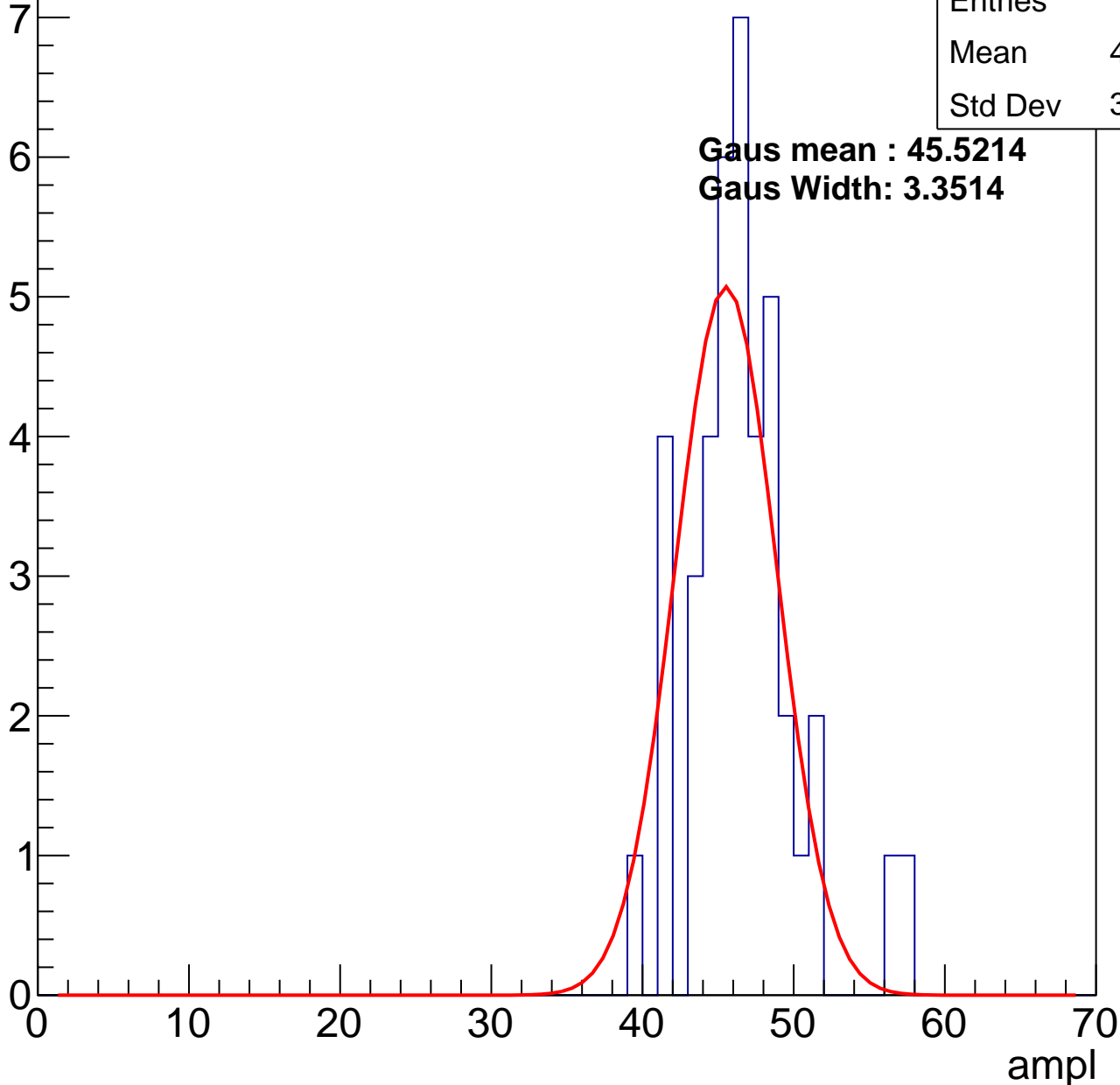
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	46.12
Std Dev	3.583

Gaus mean : 45.5214

Gaus Width: 3.3514

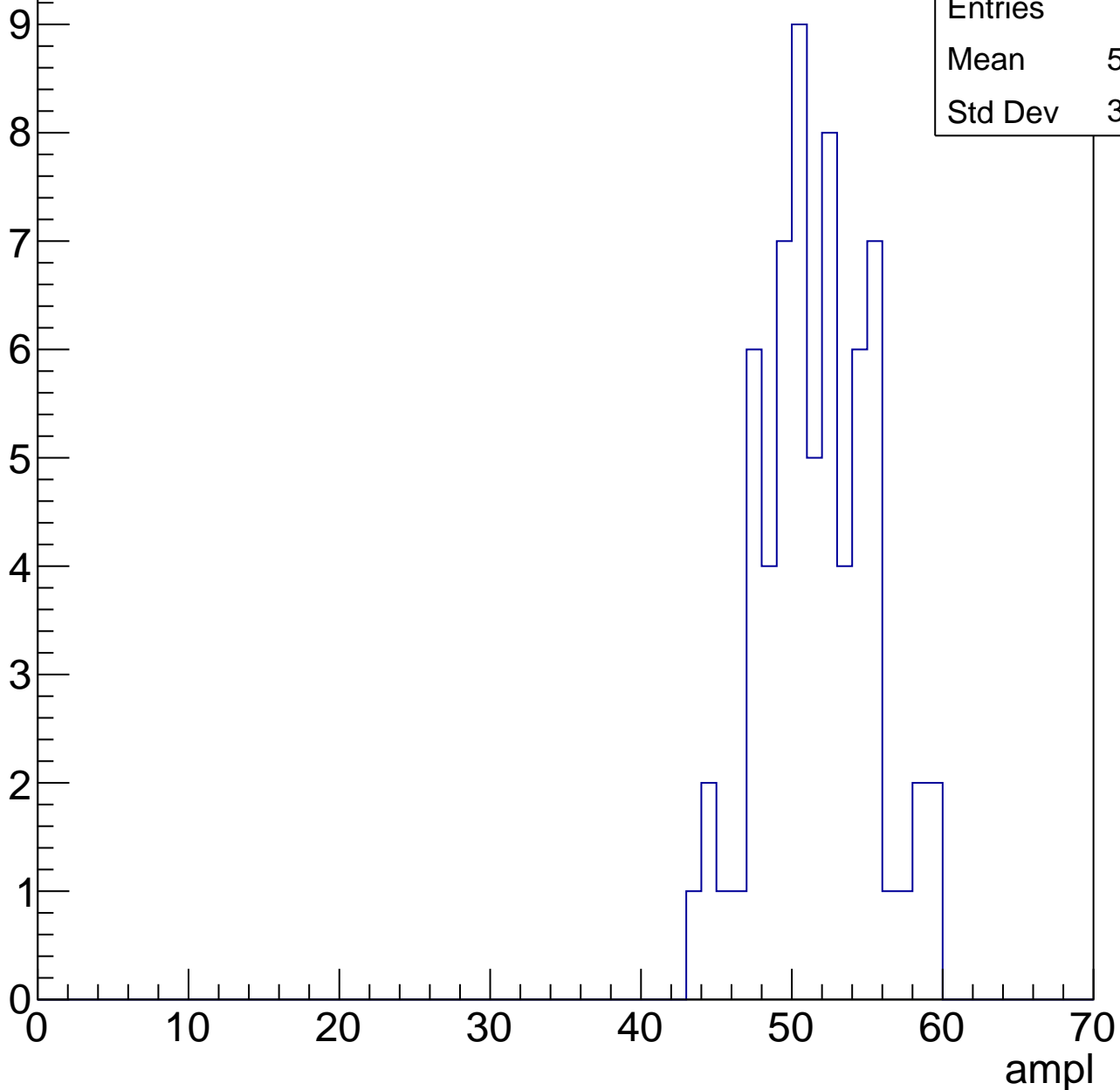


B0L002S, U17-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	51.16
Std Dev	3.598

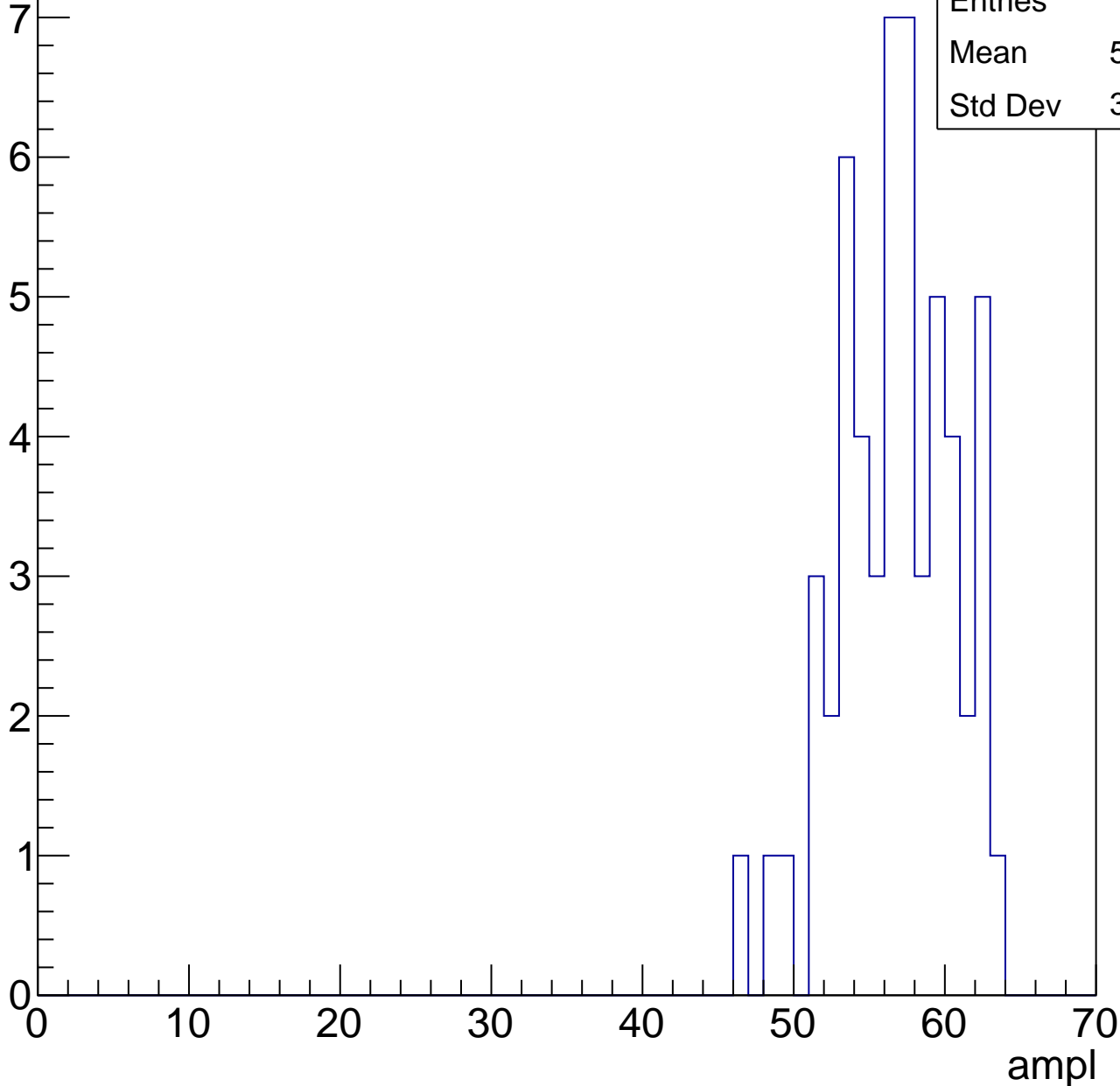


B0L002S, U17-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.25
Std Dev	3.805

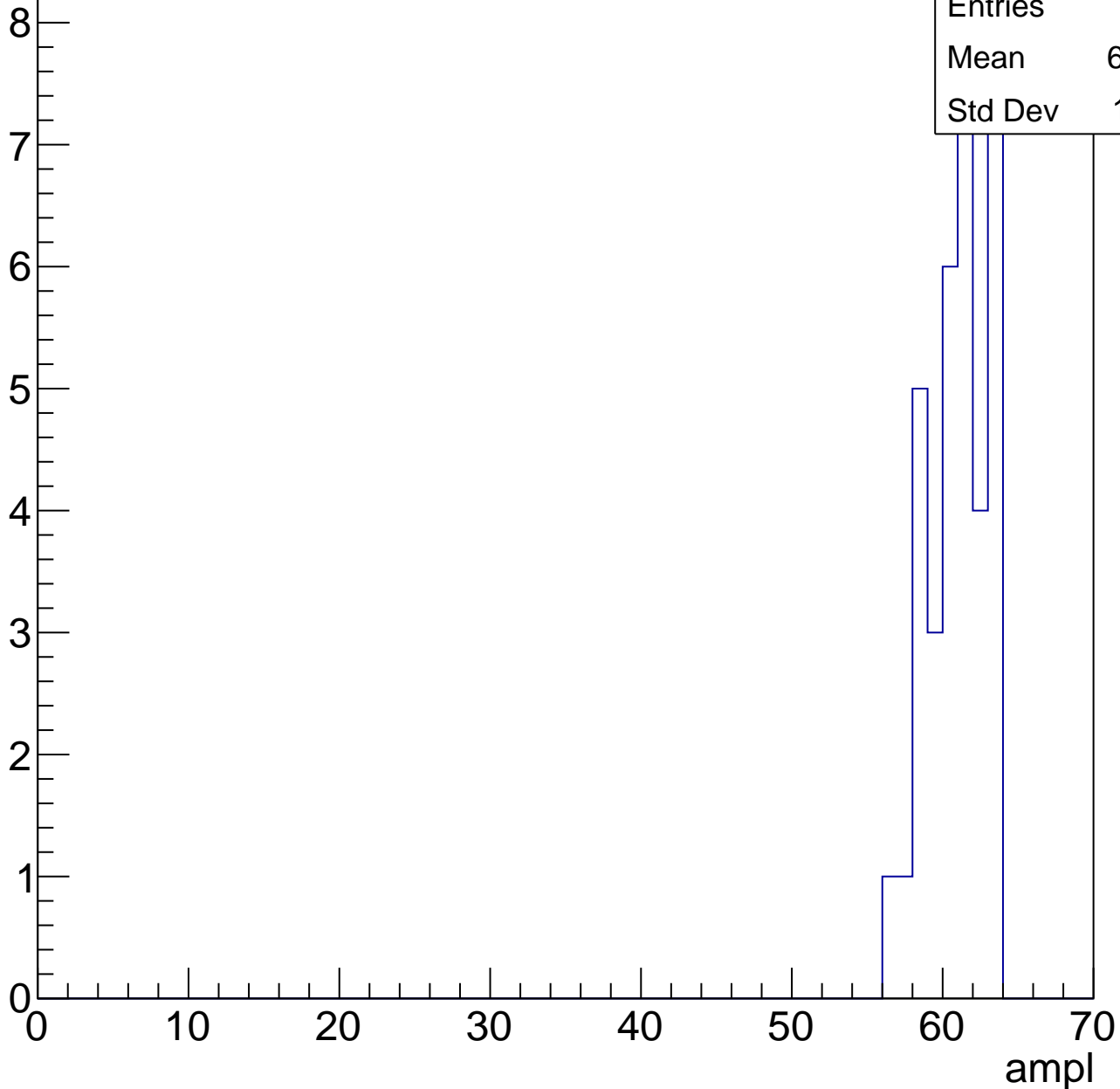


B0L002S, U17-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

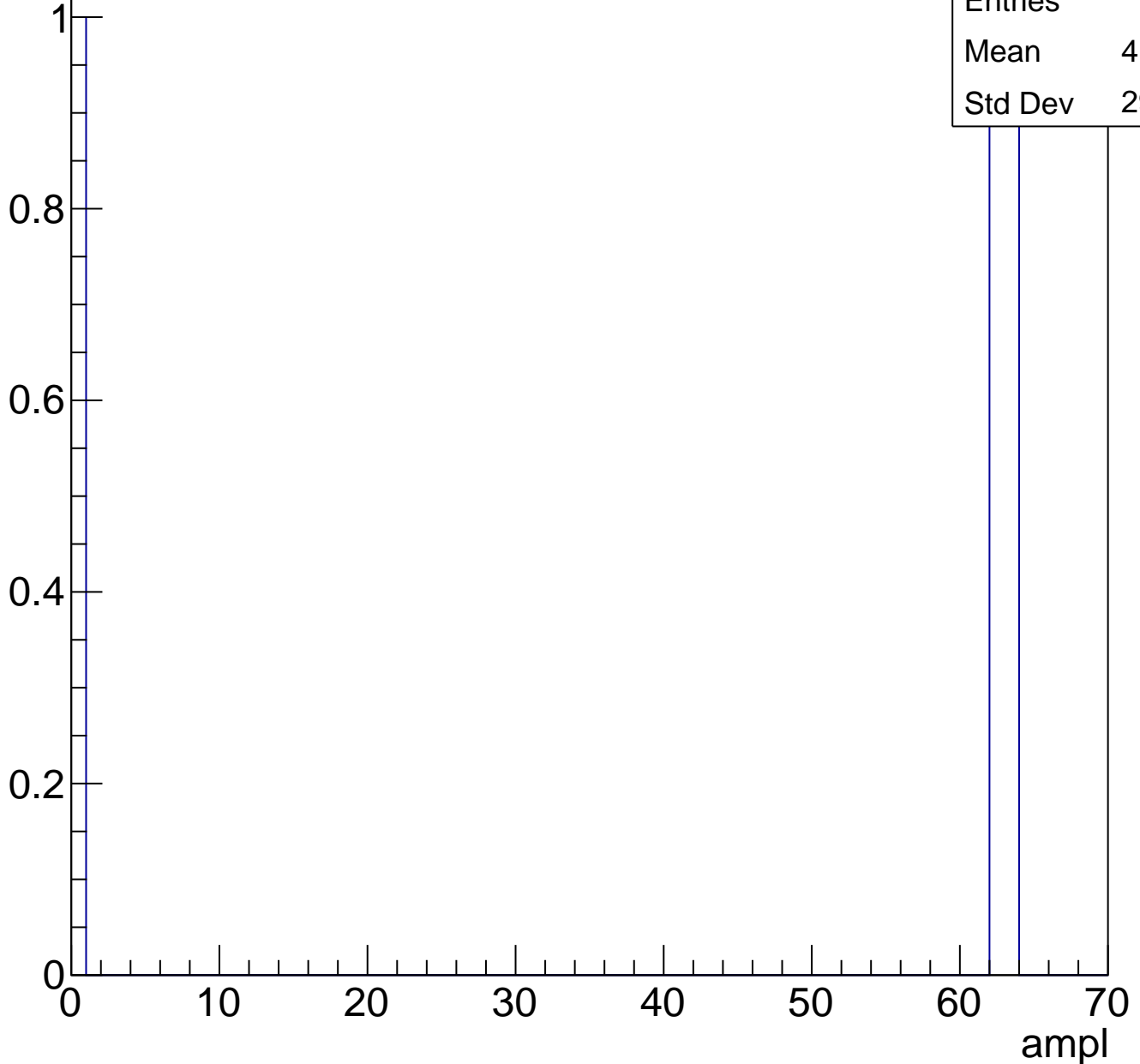
Entries	36
Mean	60.56
Std Dev	1.921



B0L002S, U17-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch107, adc0

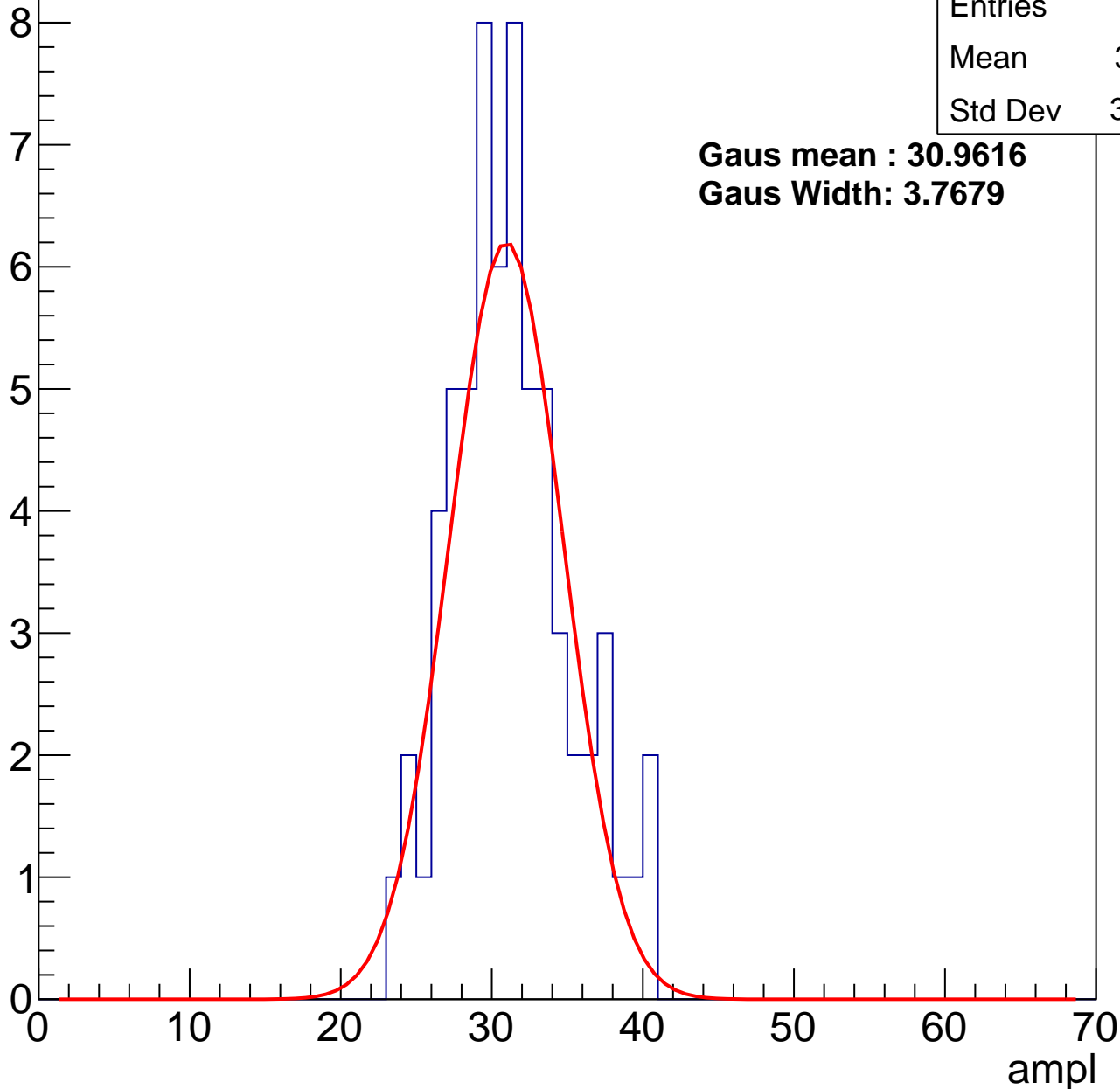
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.81
Std Dev	3.905

Gaus mean : 30.9616

Gaus Width: 3.7679



B0L002S, U17-ch107, adc1

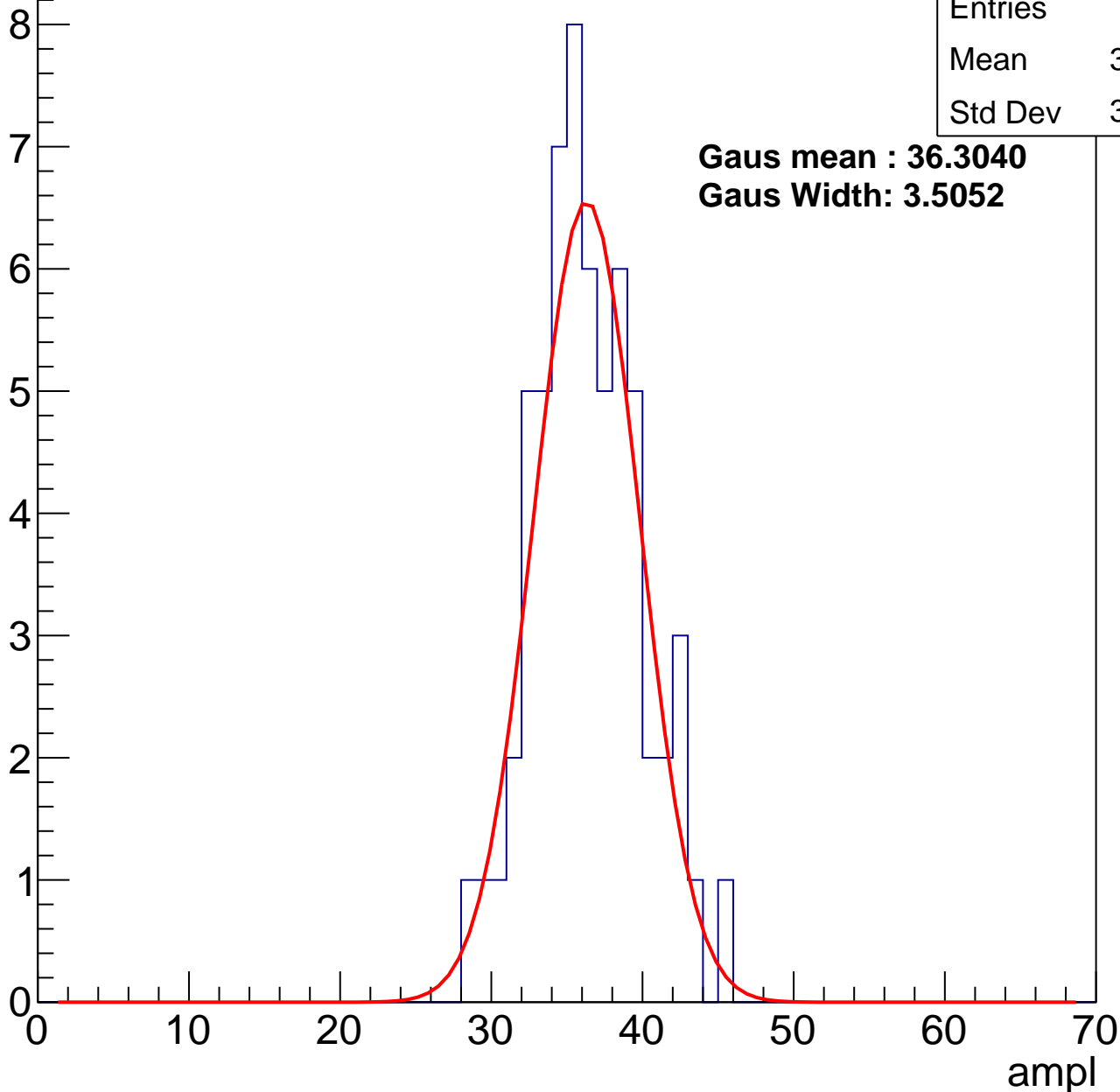
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	35.93
Std Dev	3.515

Gaus mean : 36.3040

Gaus Width: 3.5052



B0L002S, U17-ch107, adc2

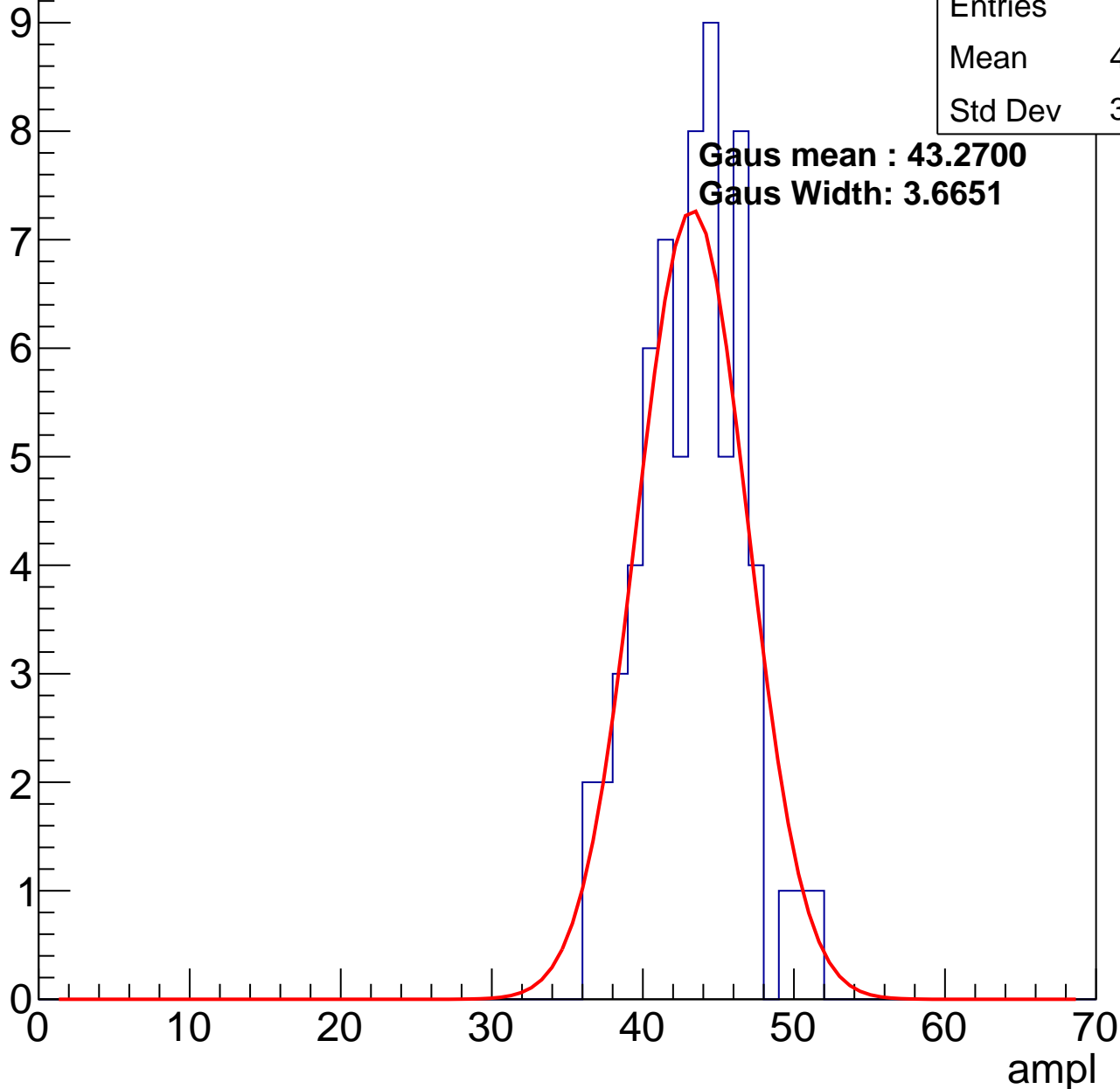
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.79
Std Dev	3.273

Gaus mean : 43.2700

Gaus Width: 3.6651



B0L002S, U17-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

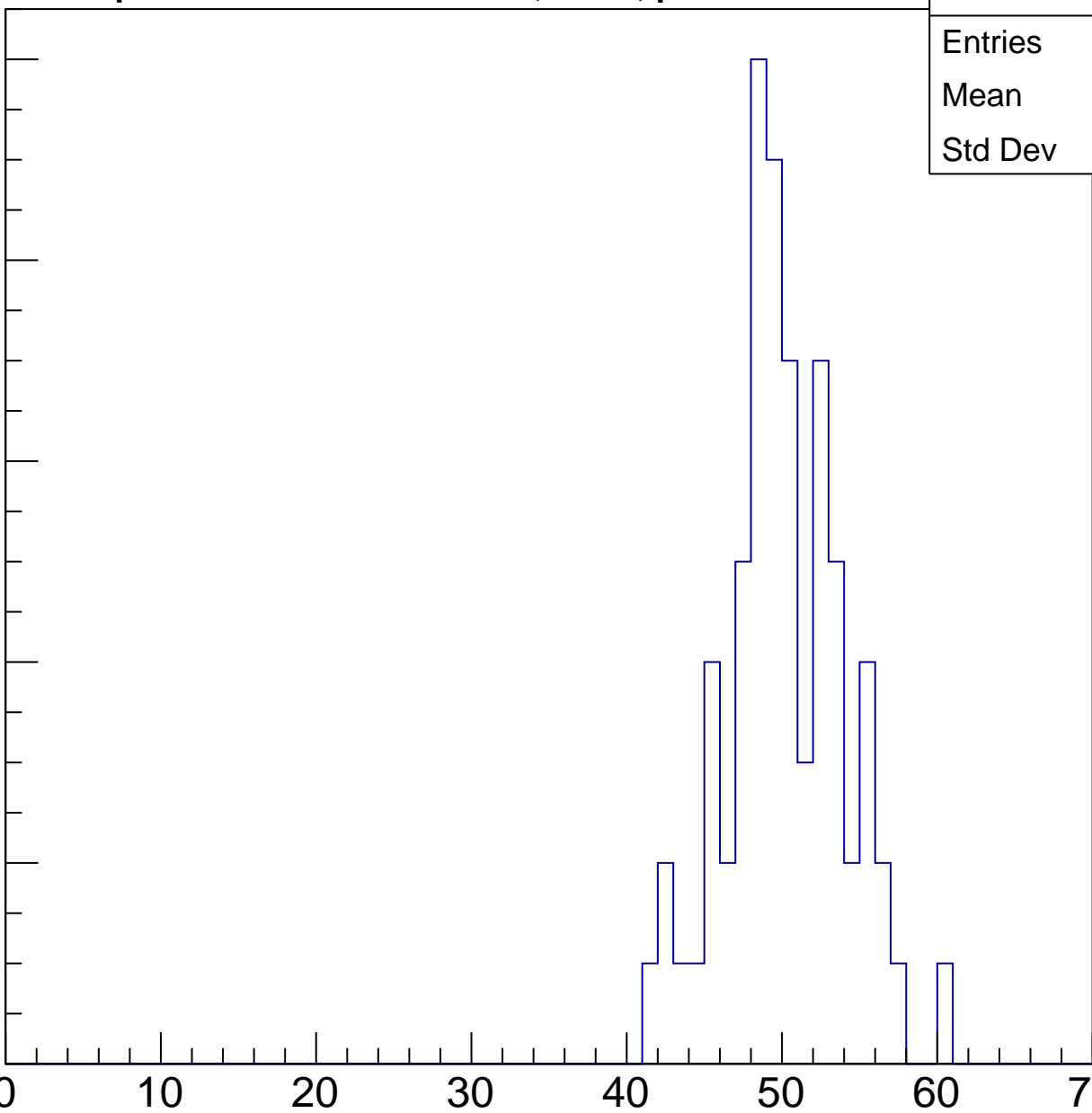
Entries	67
Mean	49.69
Std Dev	3.75

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

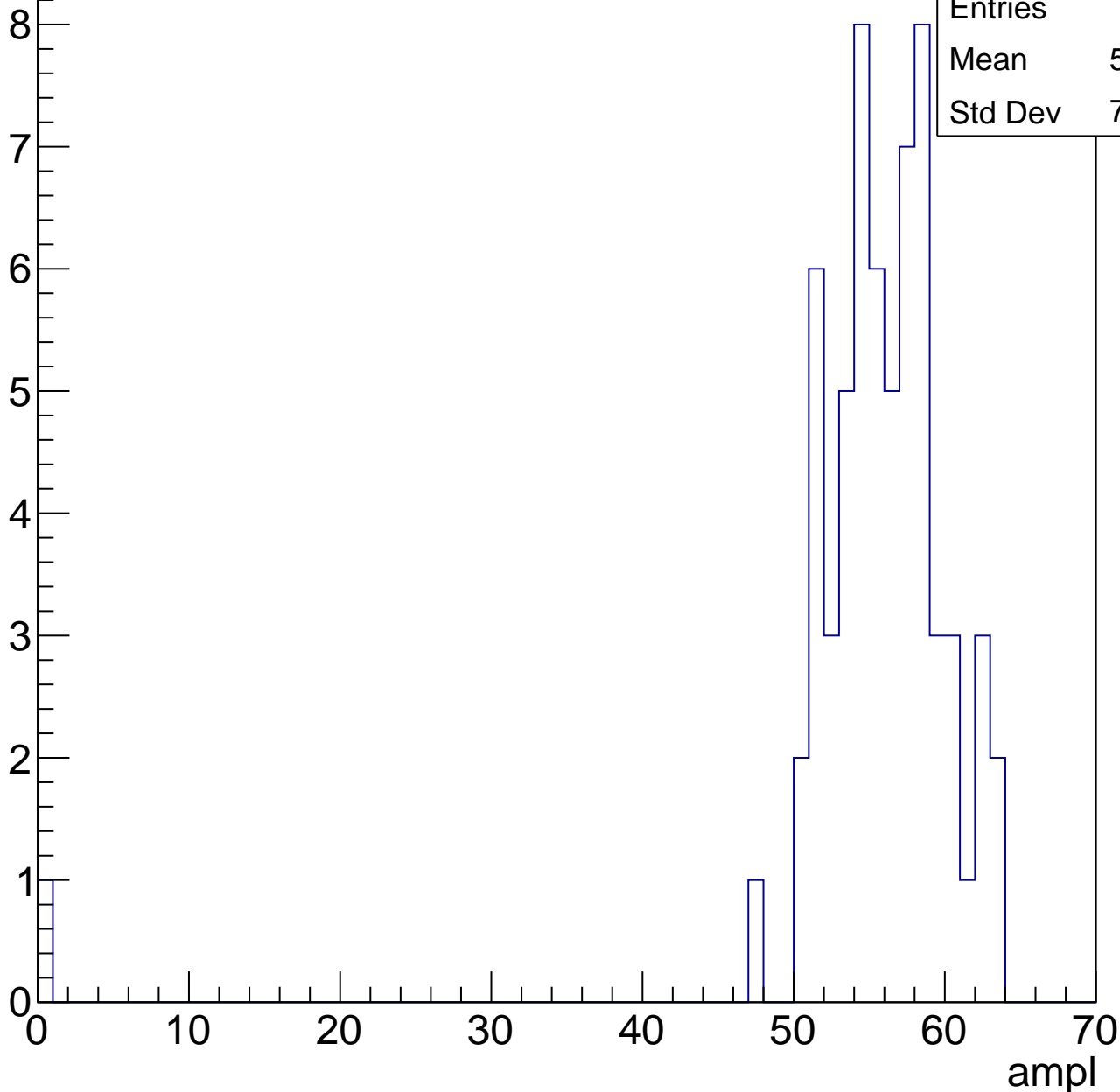


B0L002S, U17-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

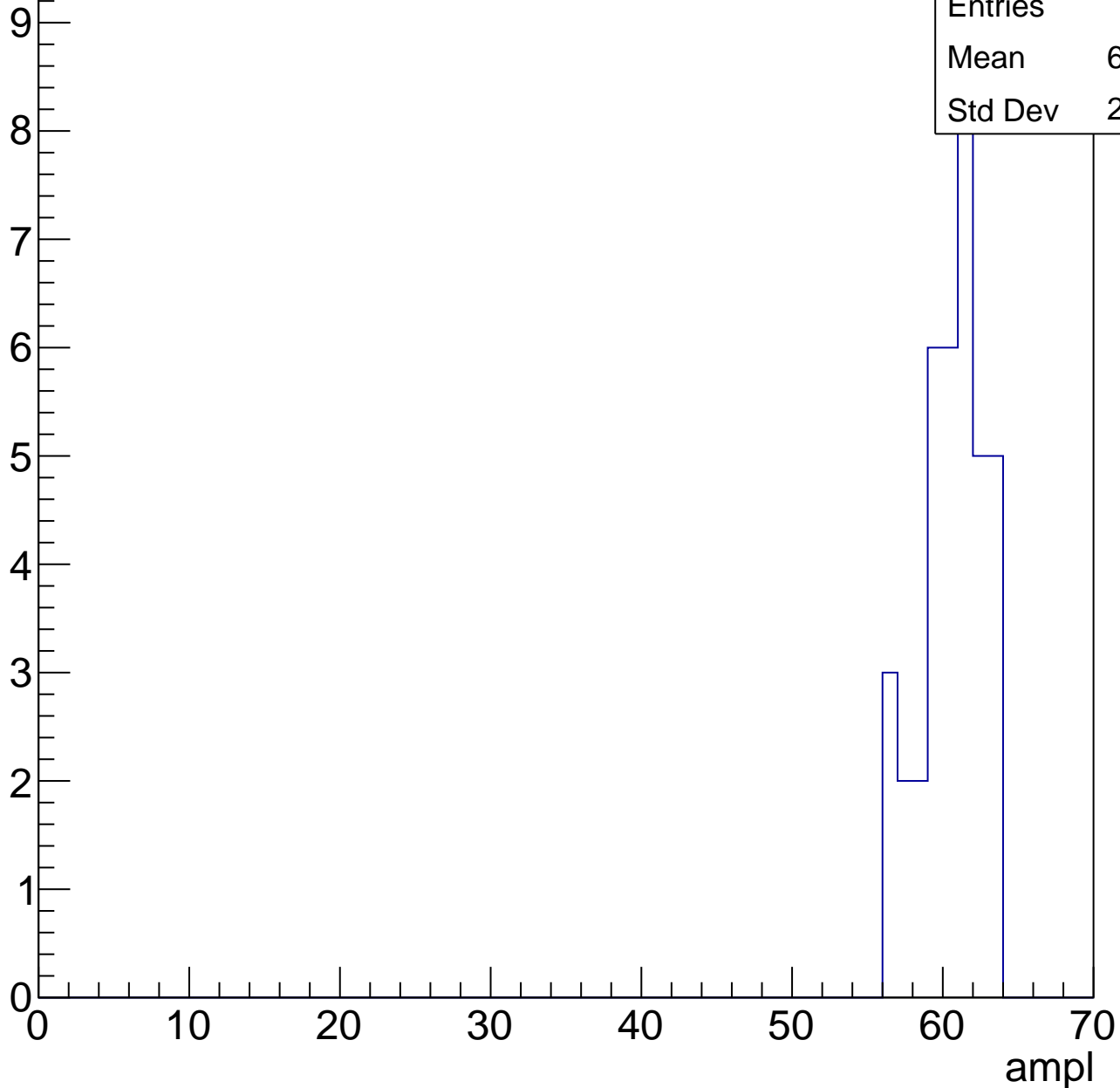
Entries	64
Mean	54.83
Std Dev	7.733



B0L002S, U17-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

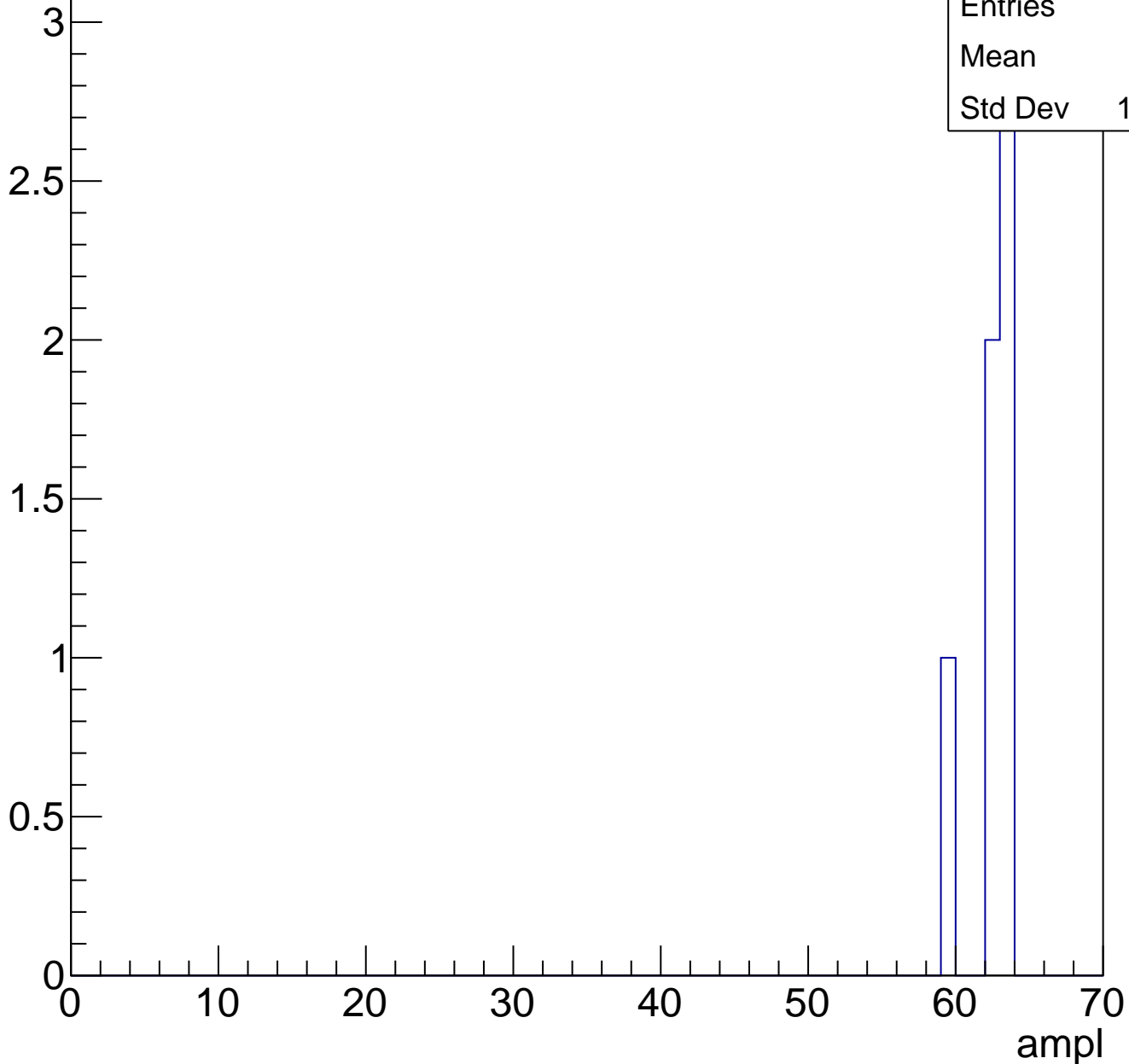
Entry



B0L002S, U17-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch108, adc0

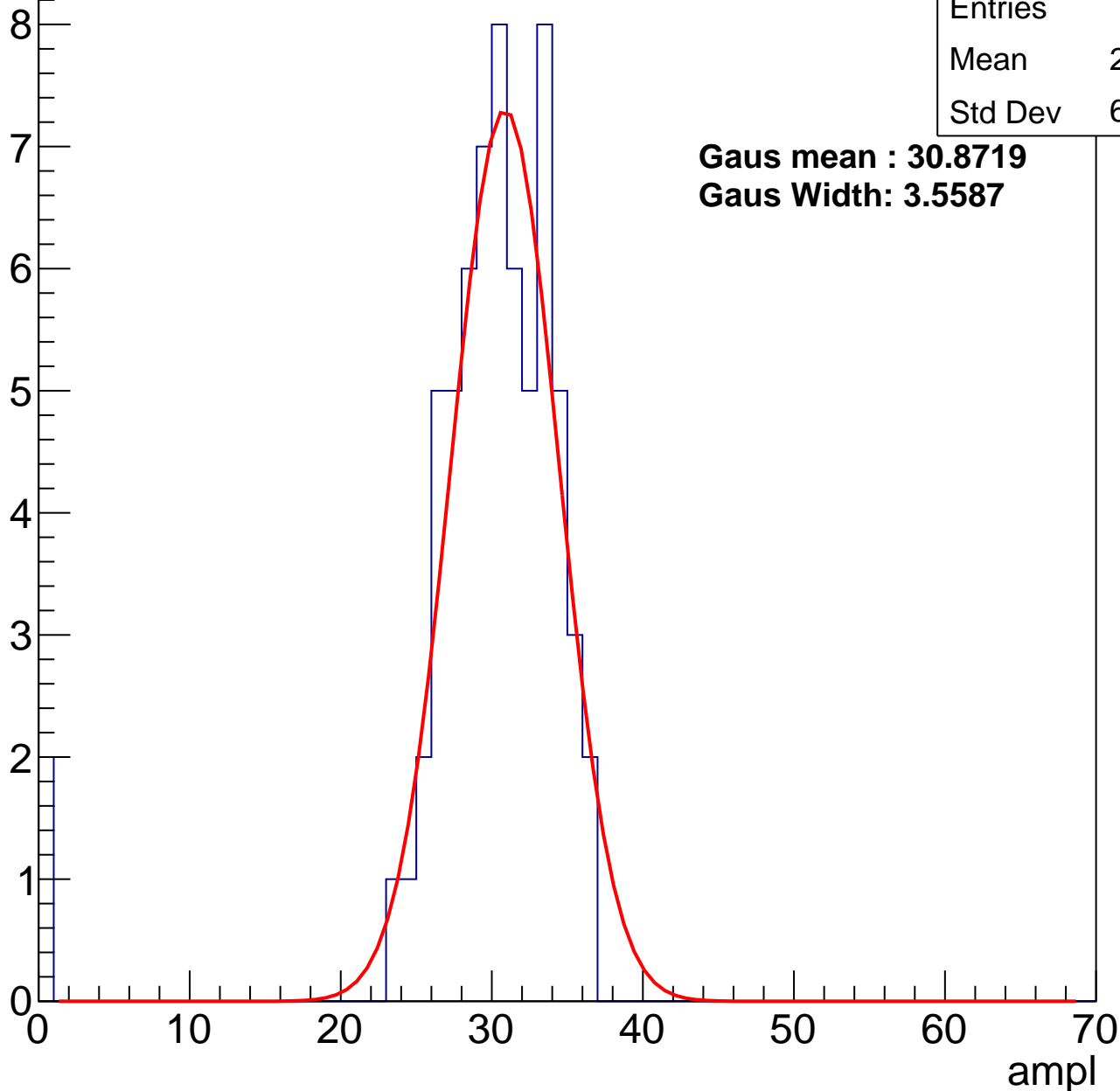
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.24
Std Dev	6.003

Gaus mean : 30.8719

Gaus Width: 3.5587



B0L002S, U17-ch108, adc1

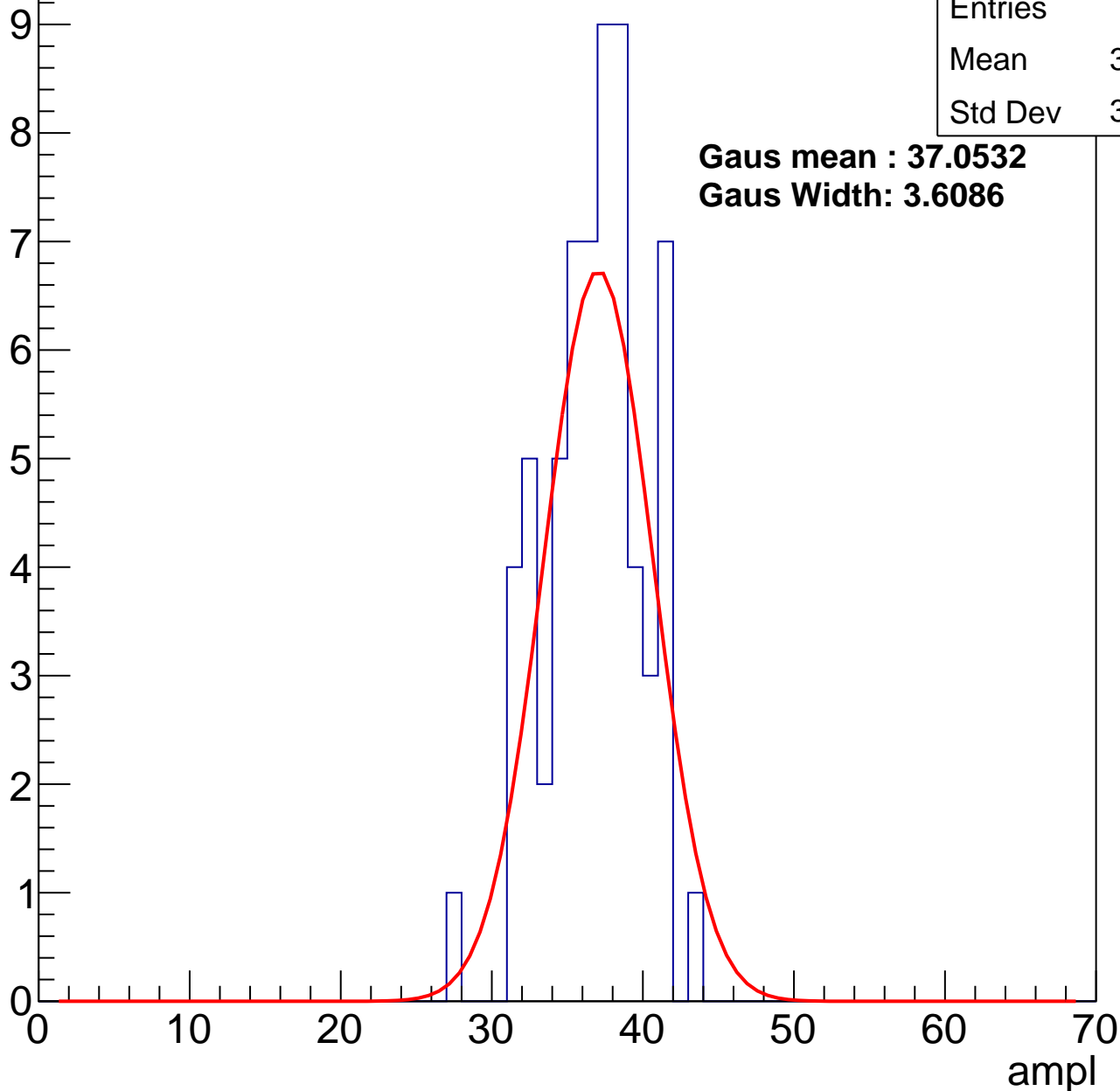
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.33
Std Dev	3.192

Gaus mean : 37.0532

Gaus Width: 3.6086



B0L002S, U17-ch108, adc2

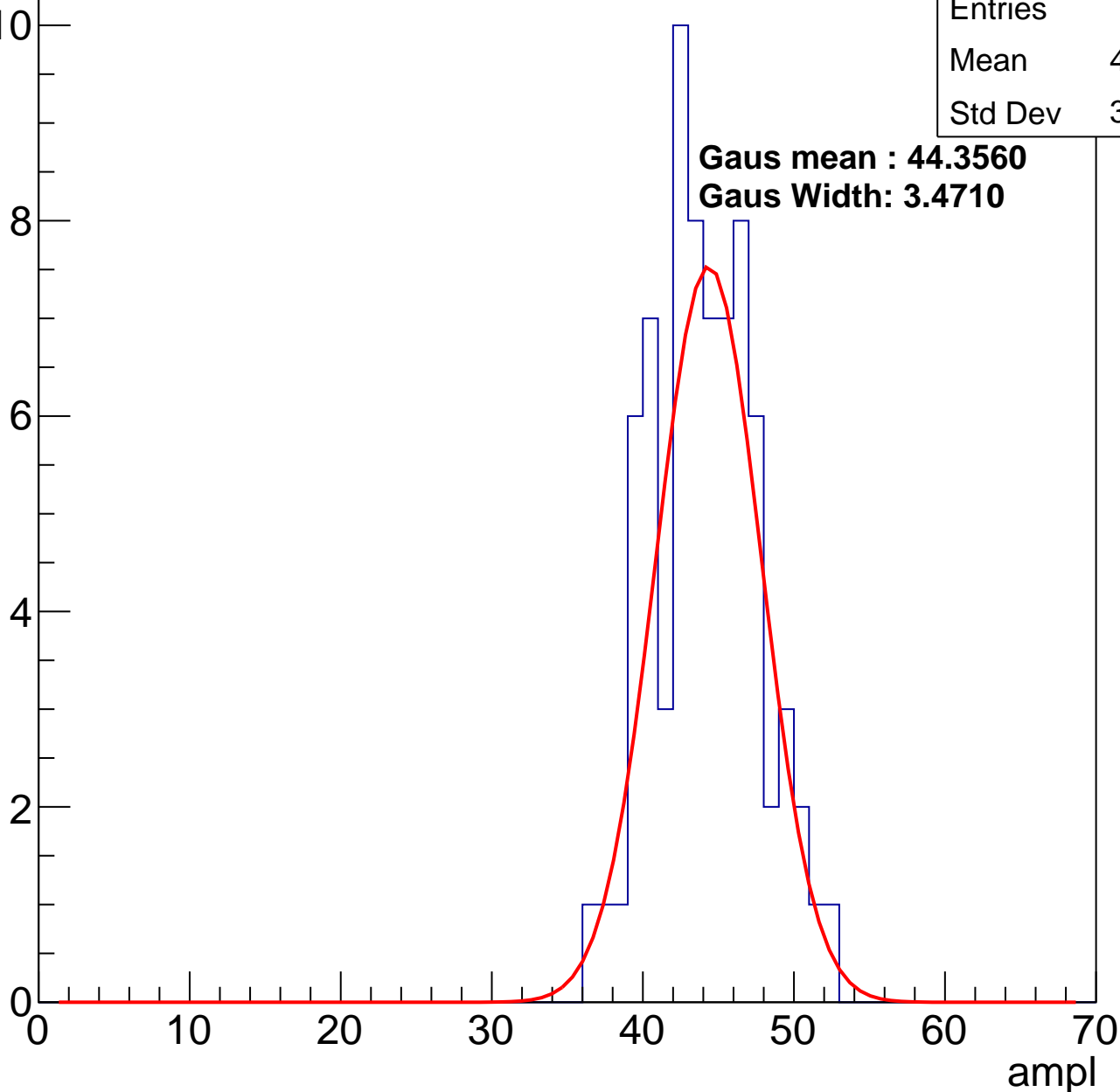
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.66
Std Dev	3.414

Gaus mean : 44.3560

Gaus Width: 3.4710

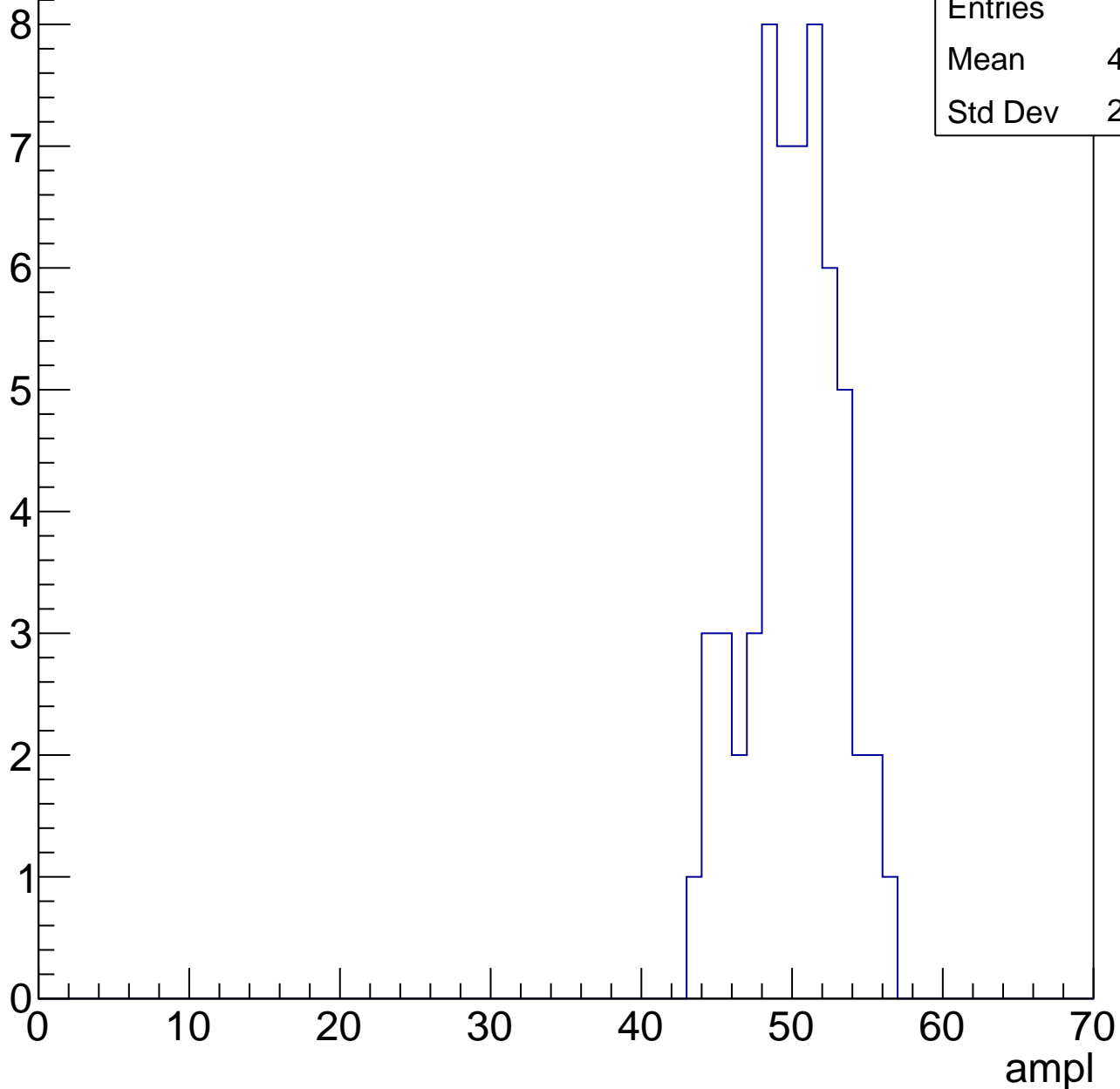


B0L002S, U17-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

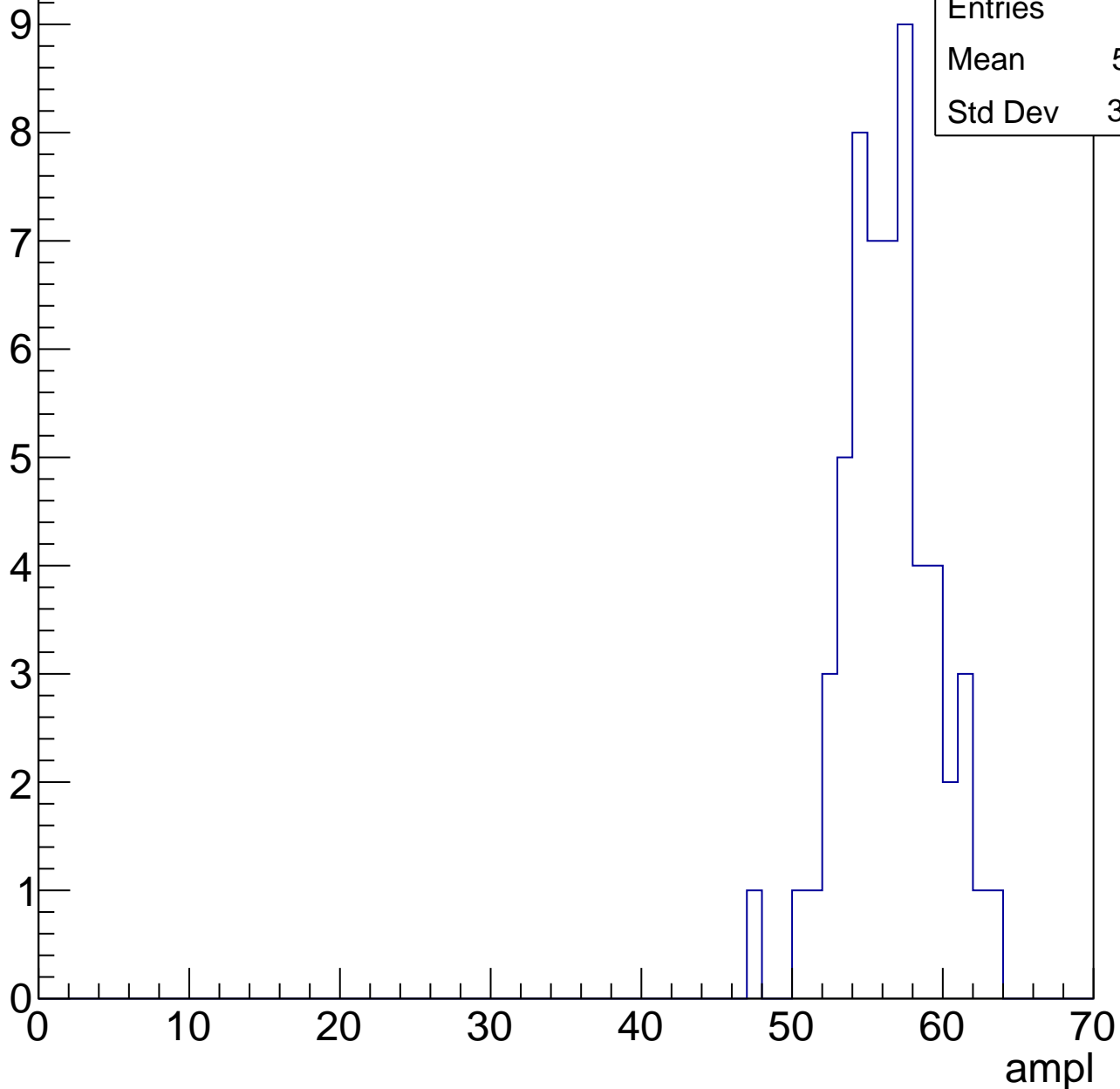
Entries	58
Mean	49.64
Std Dev	2.987



B0L002S, U17-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

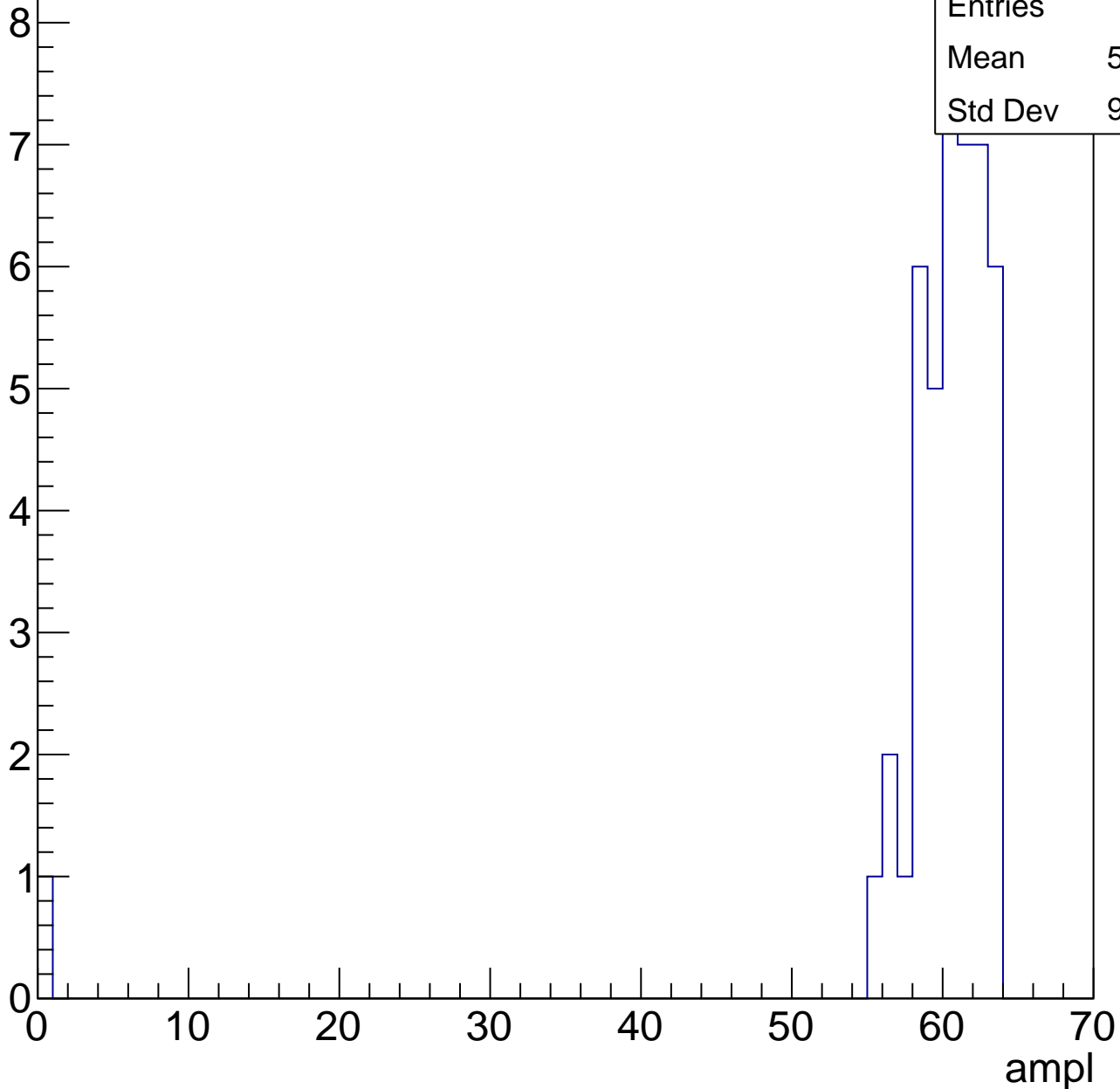


B0L002S, U17-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

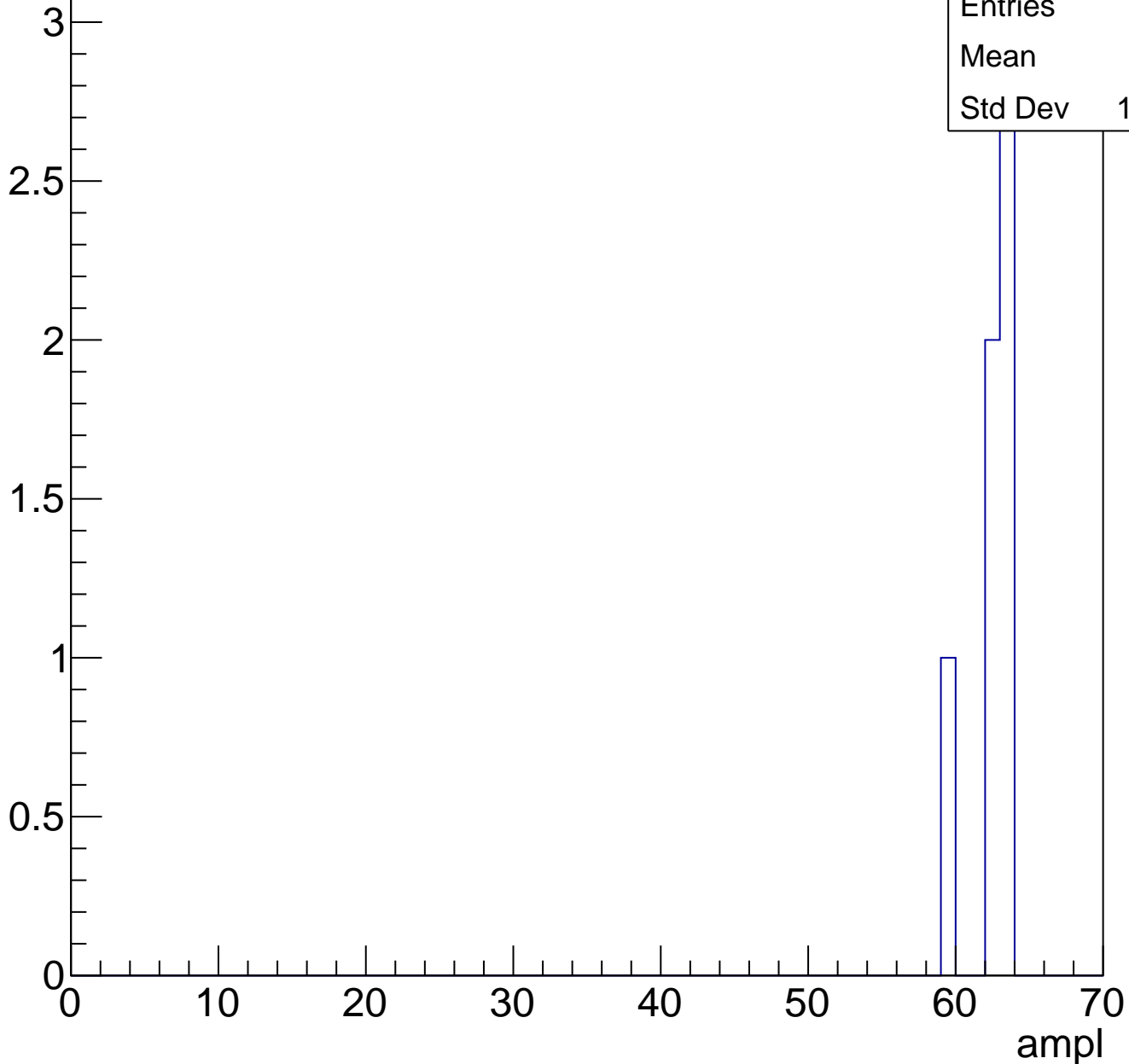
Entries	44
Mean	58.77
Std Dev	9.192



B0L002S, U17-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch109, adc0

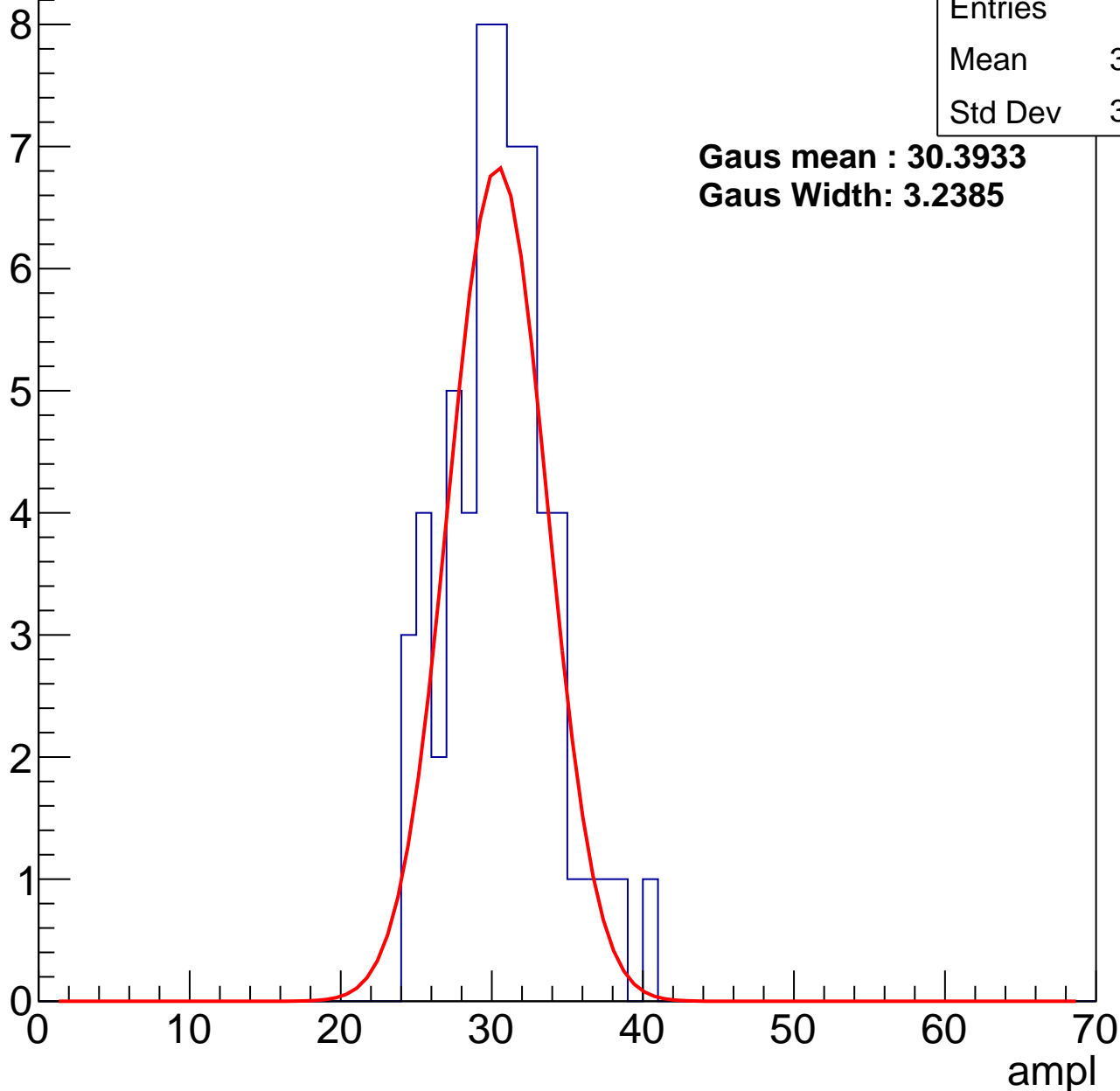
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	30.13
Std Dev	3.428

Gaus mean : 30.3933

Gaus Width: 3.2385



B0L002S, U17-ch109, adc1

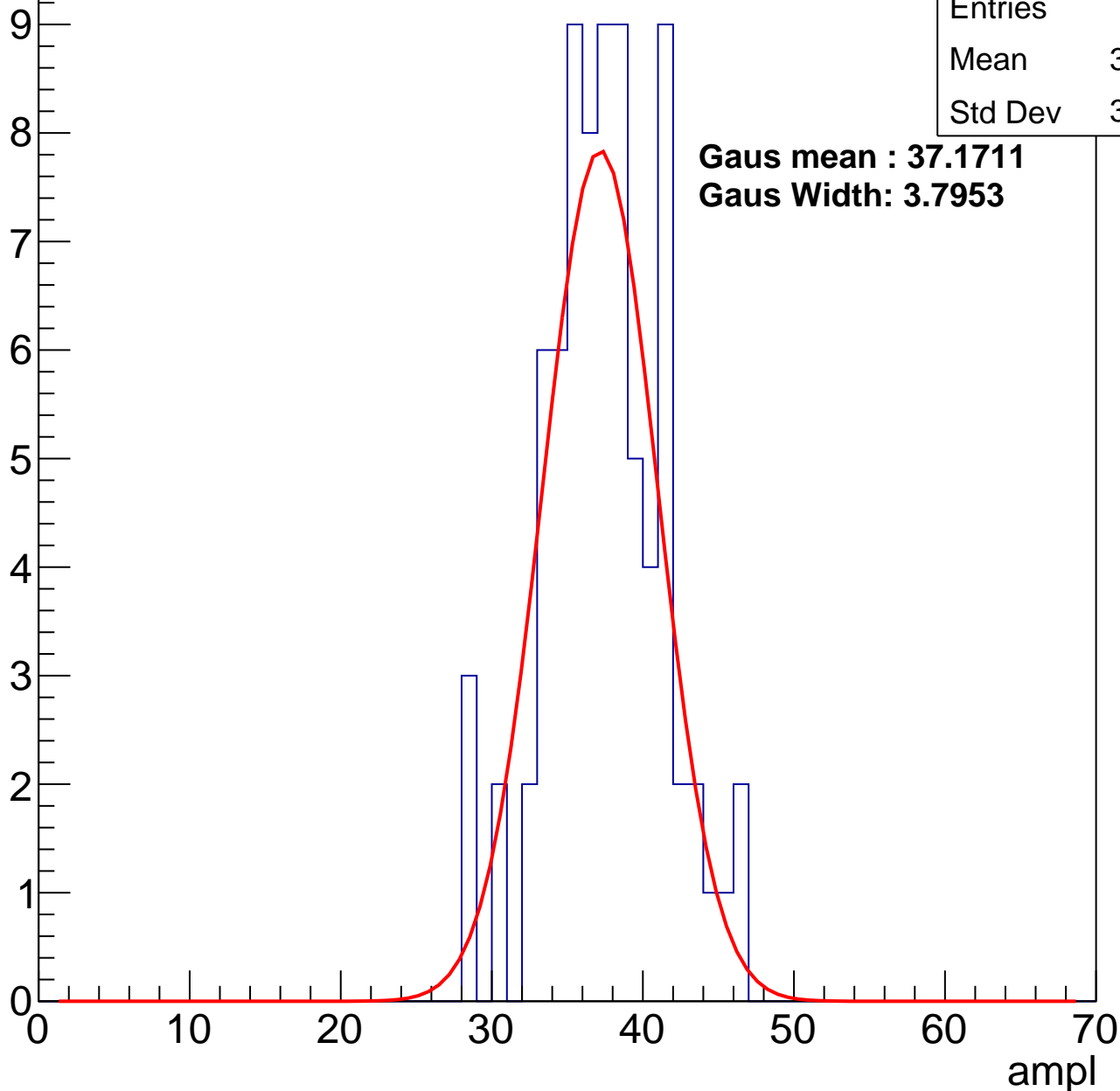
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	37.04
Std Dev	3.858

Gaus mean : 37.1711

Gaus Width: 3.7953



B0L002S, U17-ch109, adc2

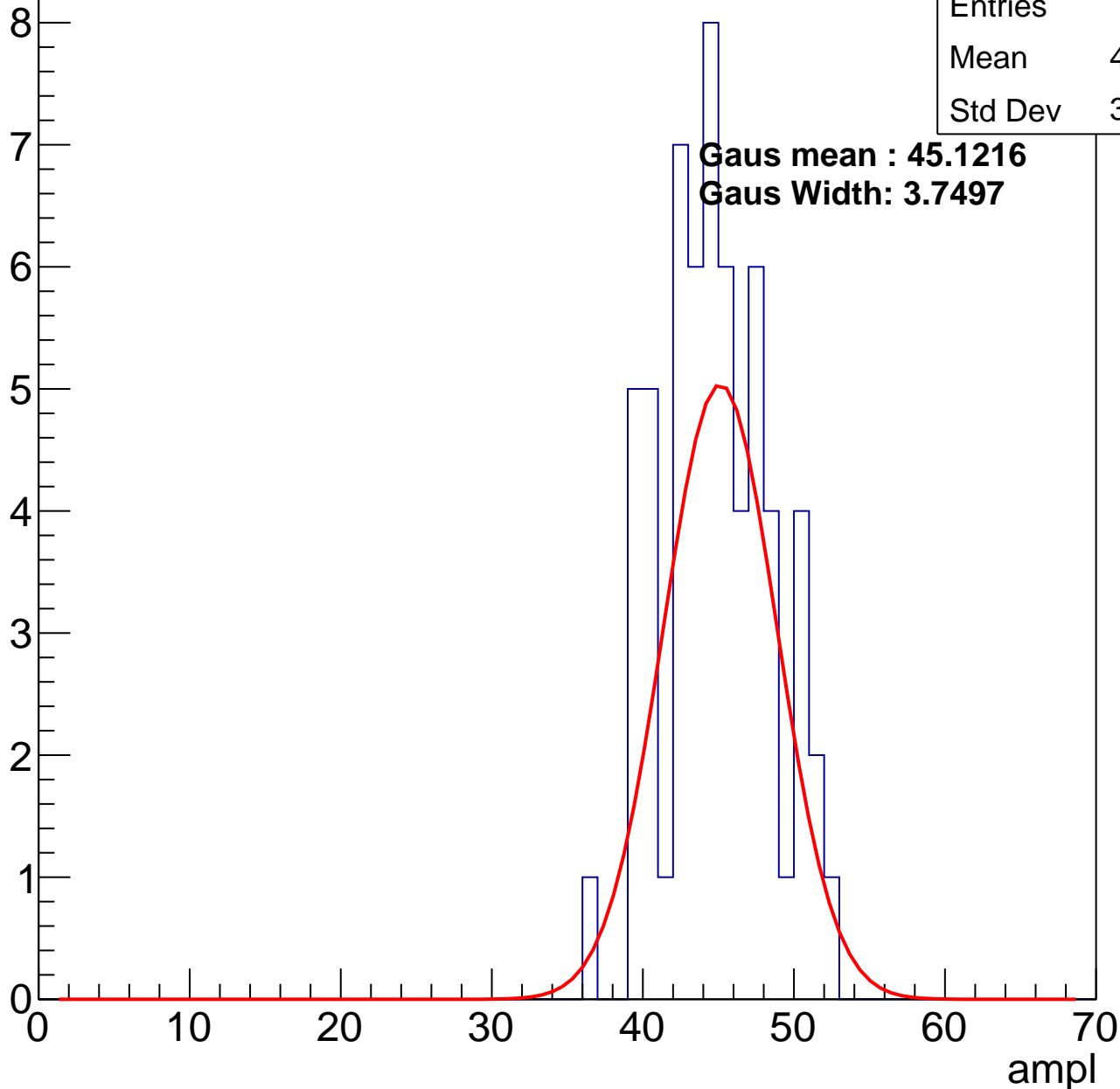
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.38
Std Dev	3.563

Gaus mean : 45.1216

Gaus Width: 3.7497

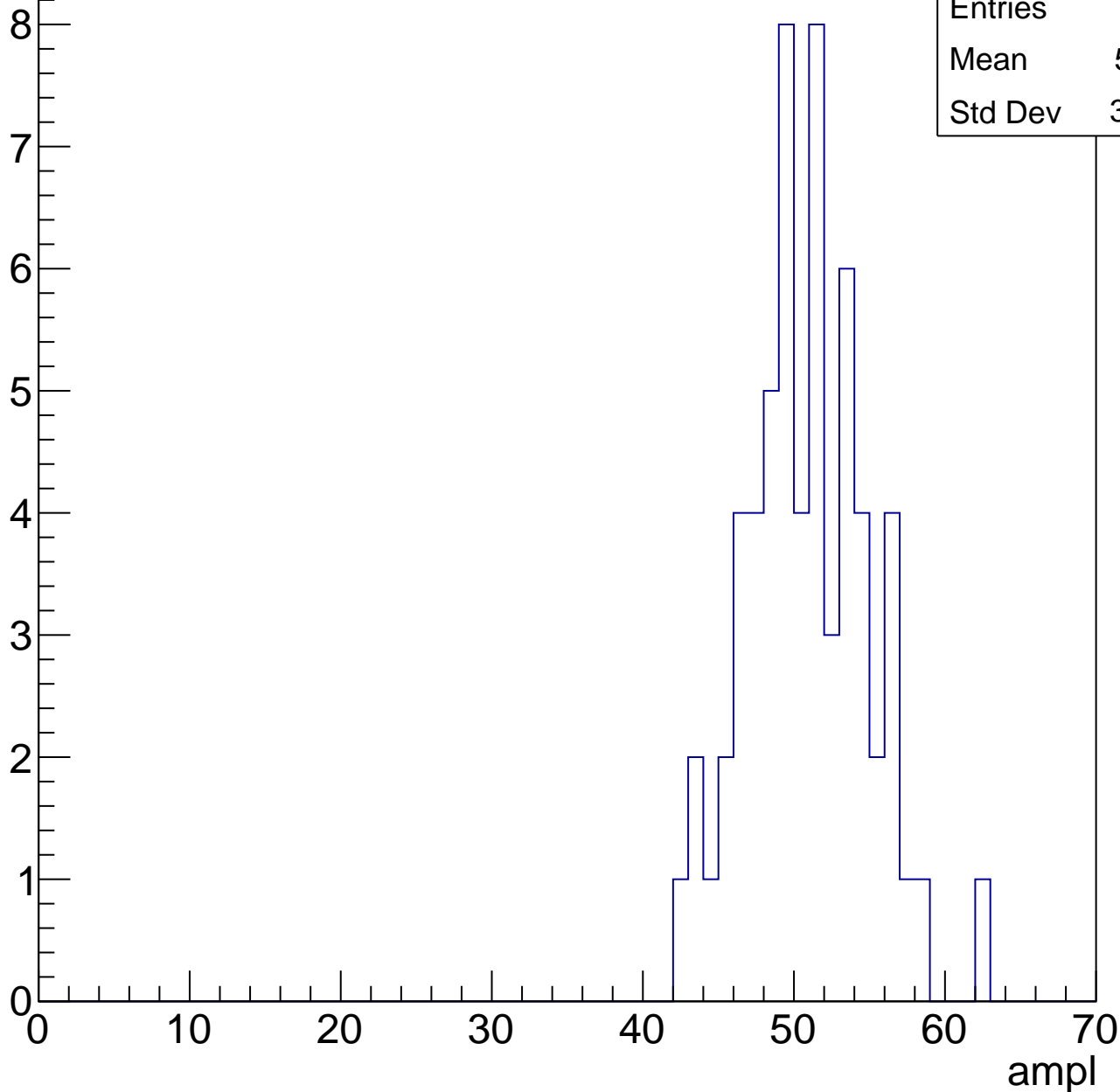


B0L002S, U17-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

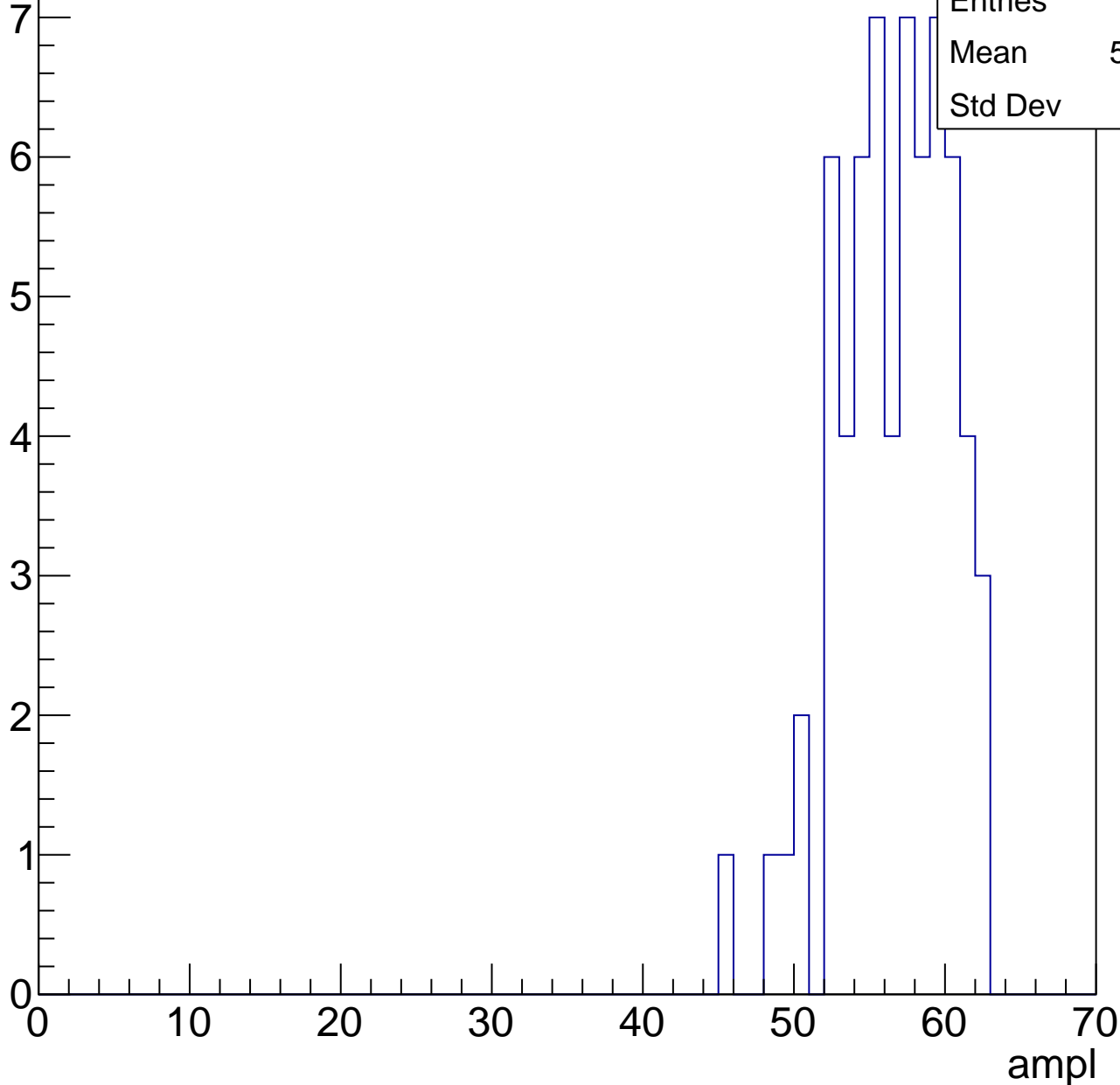
Entries	61
Mean	50.41
Std Dev	3.956



B0L002S, U17-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

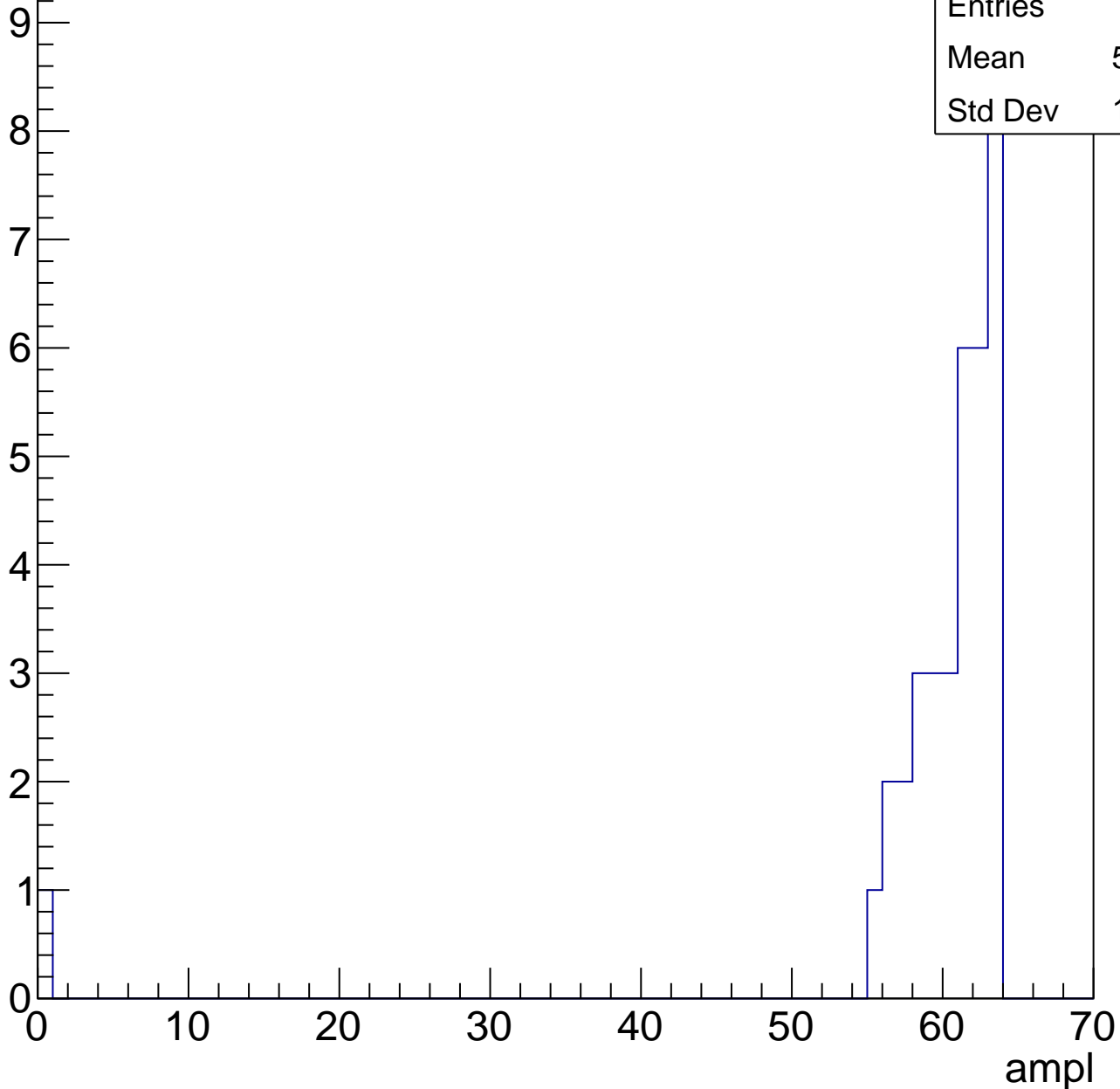


B0L002S, U17-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

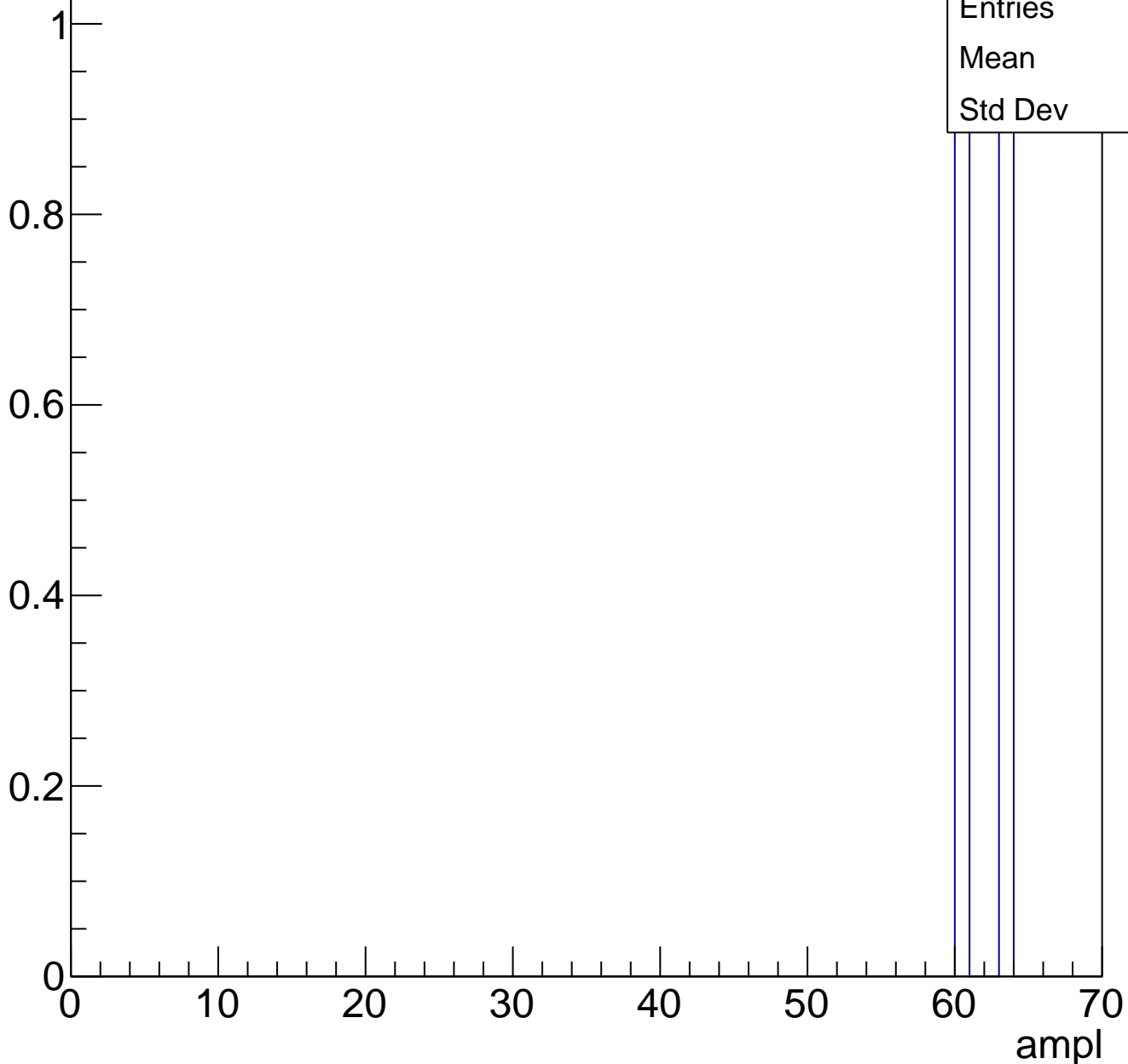
Entries	36
Mean	58.81
Std Dev	10.21



B0L002S, U17-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

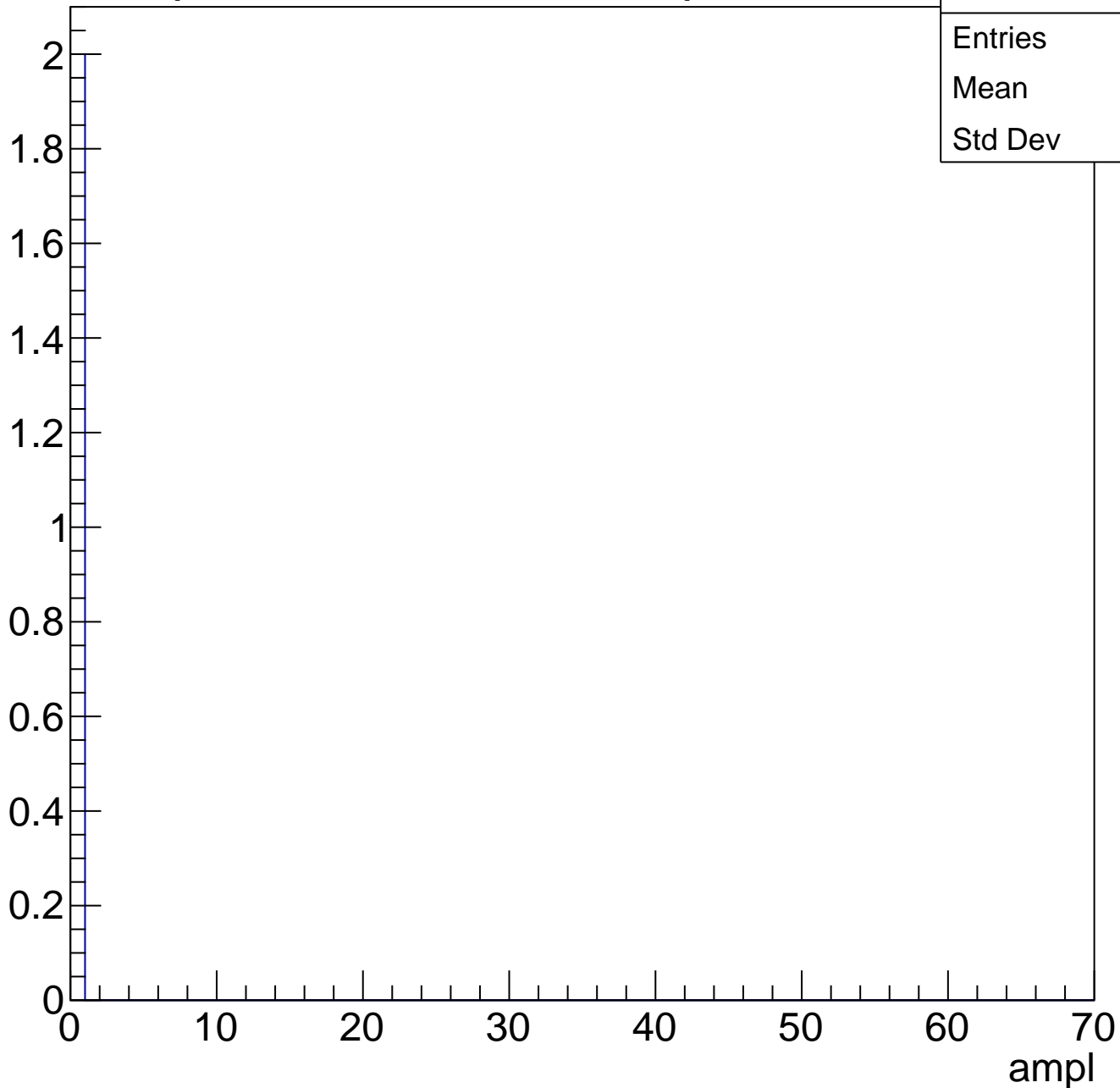
Entry



B0L002S, U17-ch109, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch110, adc0

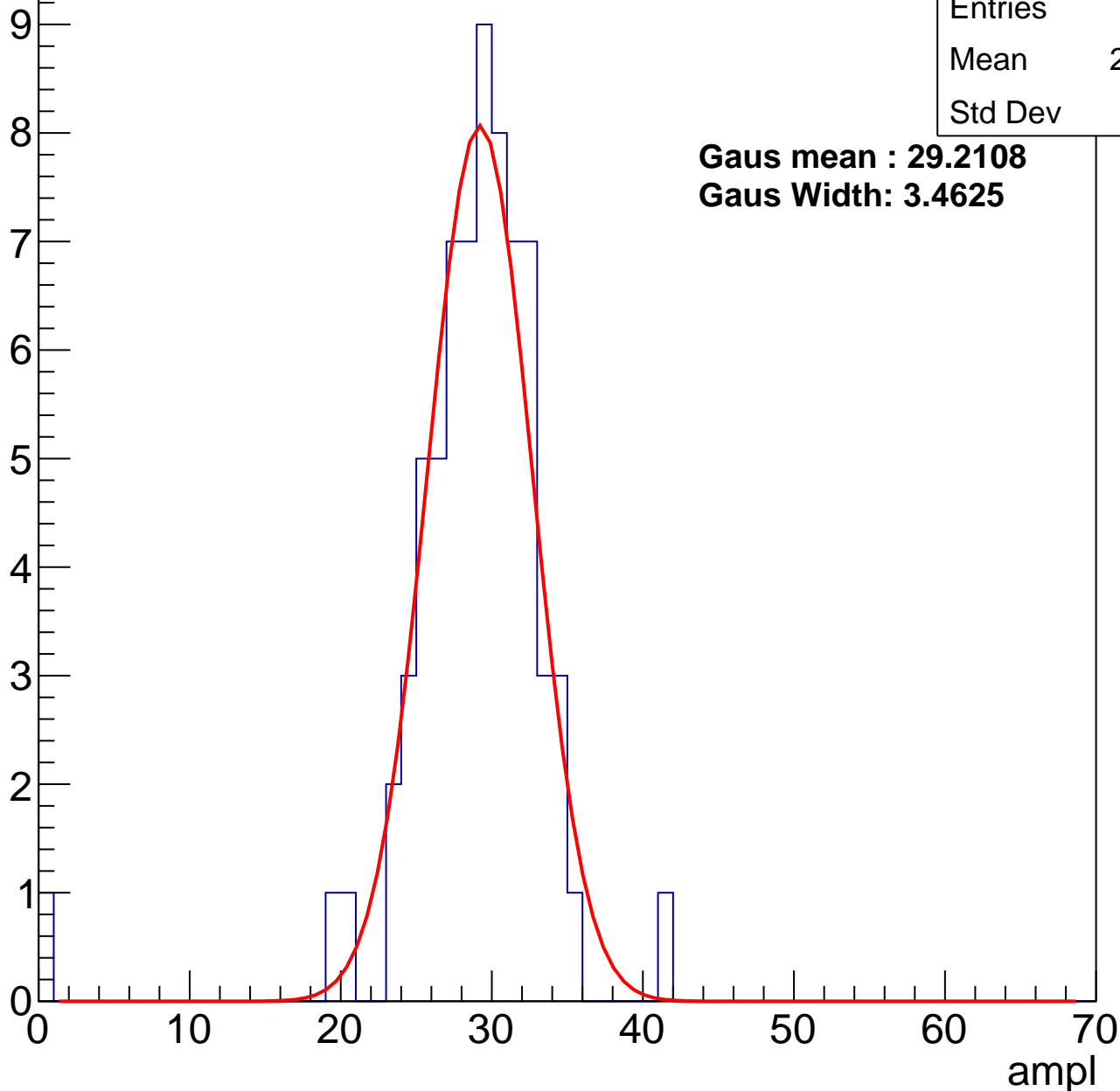
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	28.39
Std Dev	4.9

Gaus mean : 29.2108

Gaus Width: 3.4625



B0L002S, U17-ch110, adc1

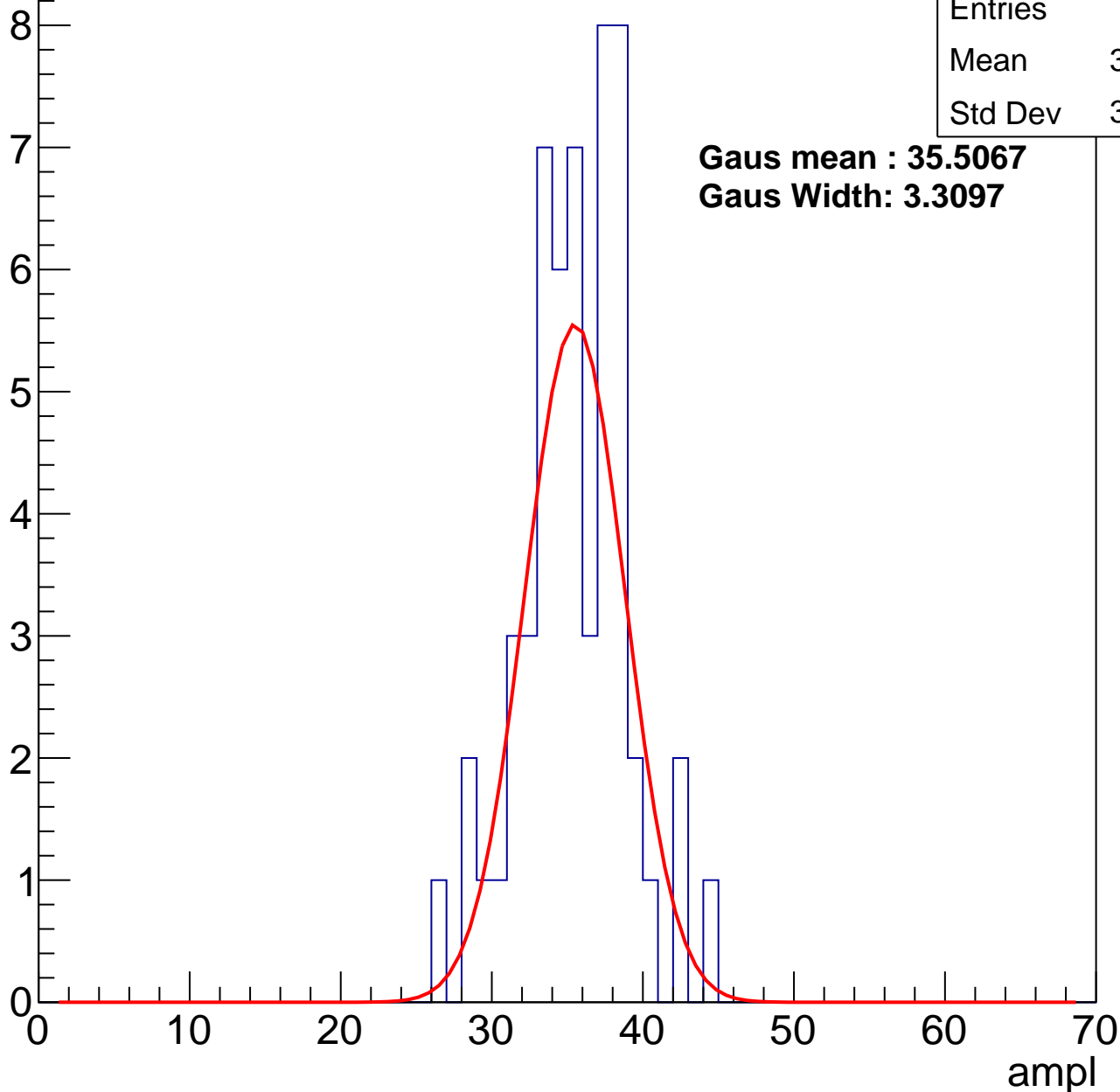
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	35.07
Std Dev	3.515

Gaus mean : 35.5067

Gaus Width: 3.3097



B0L002S, U17-ch110, adc2

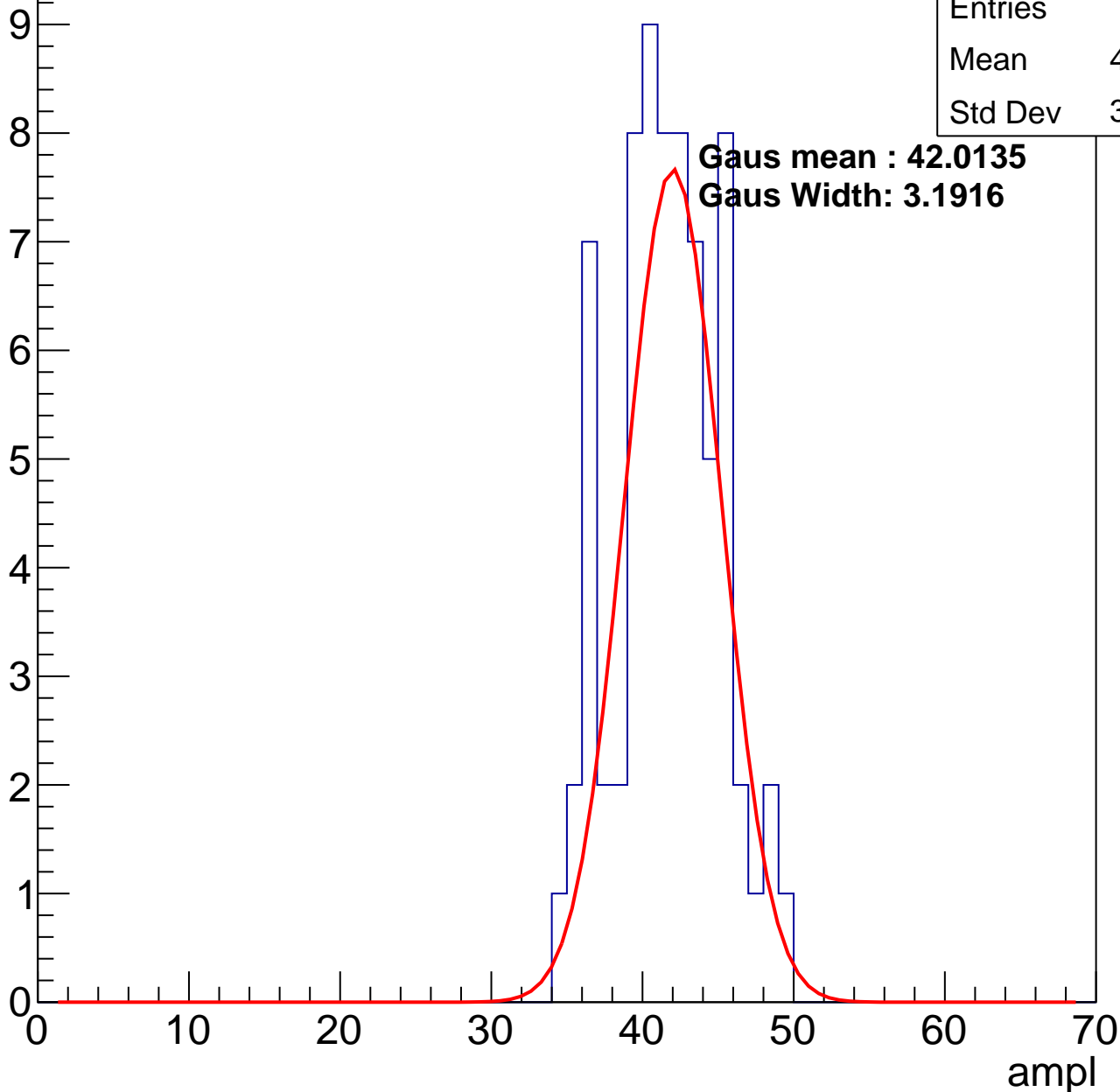
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	41.19
Std Dev	3.395

Gaus mean : 42.0135

Gaus Width: 3.1916

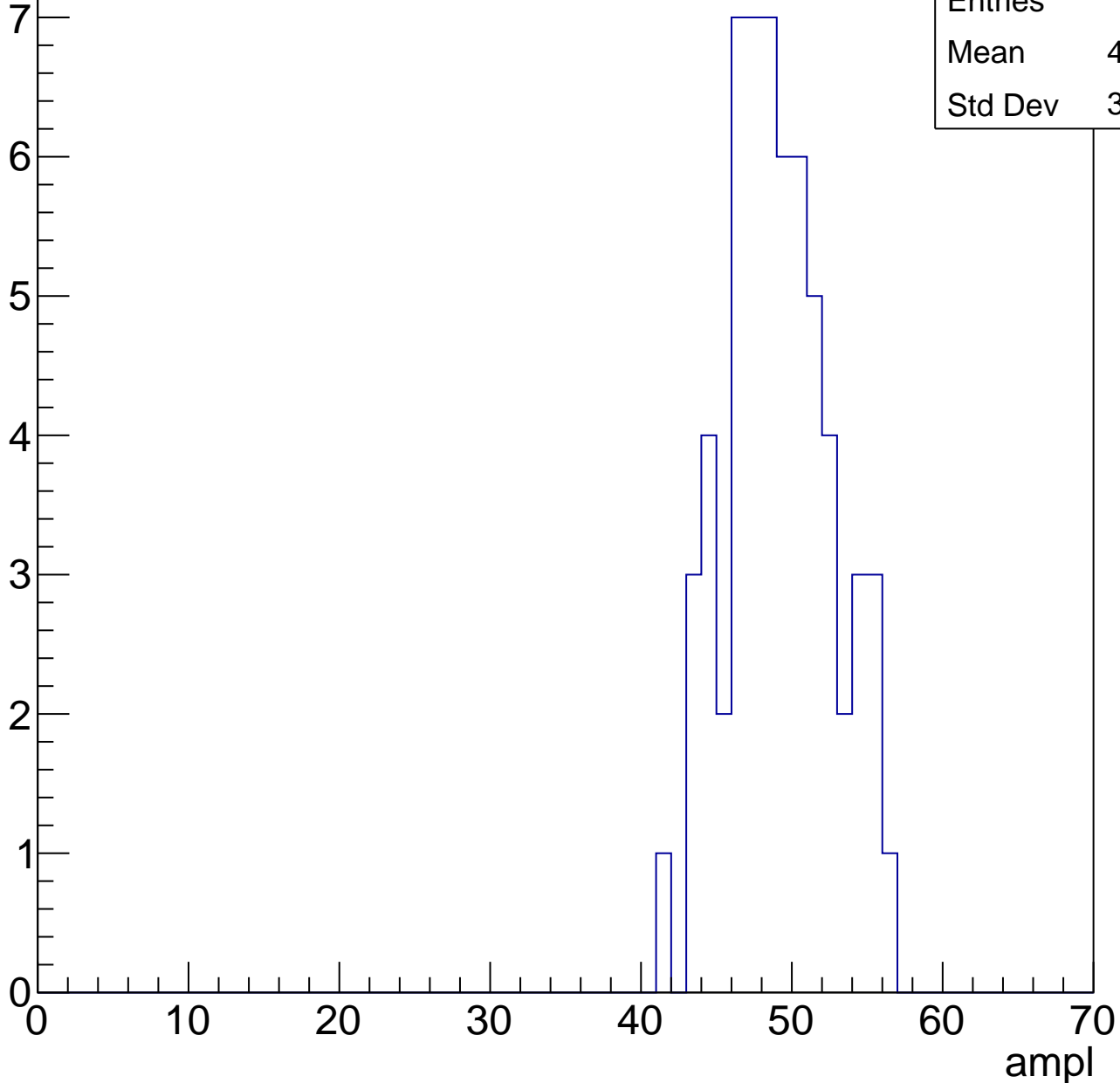


B0L002S, U17-ch110, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	48.67
Std Dev	3.439

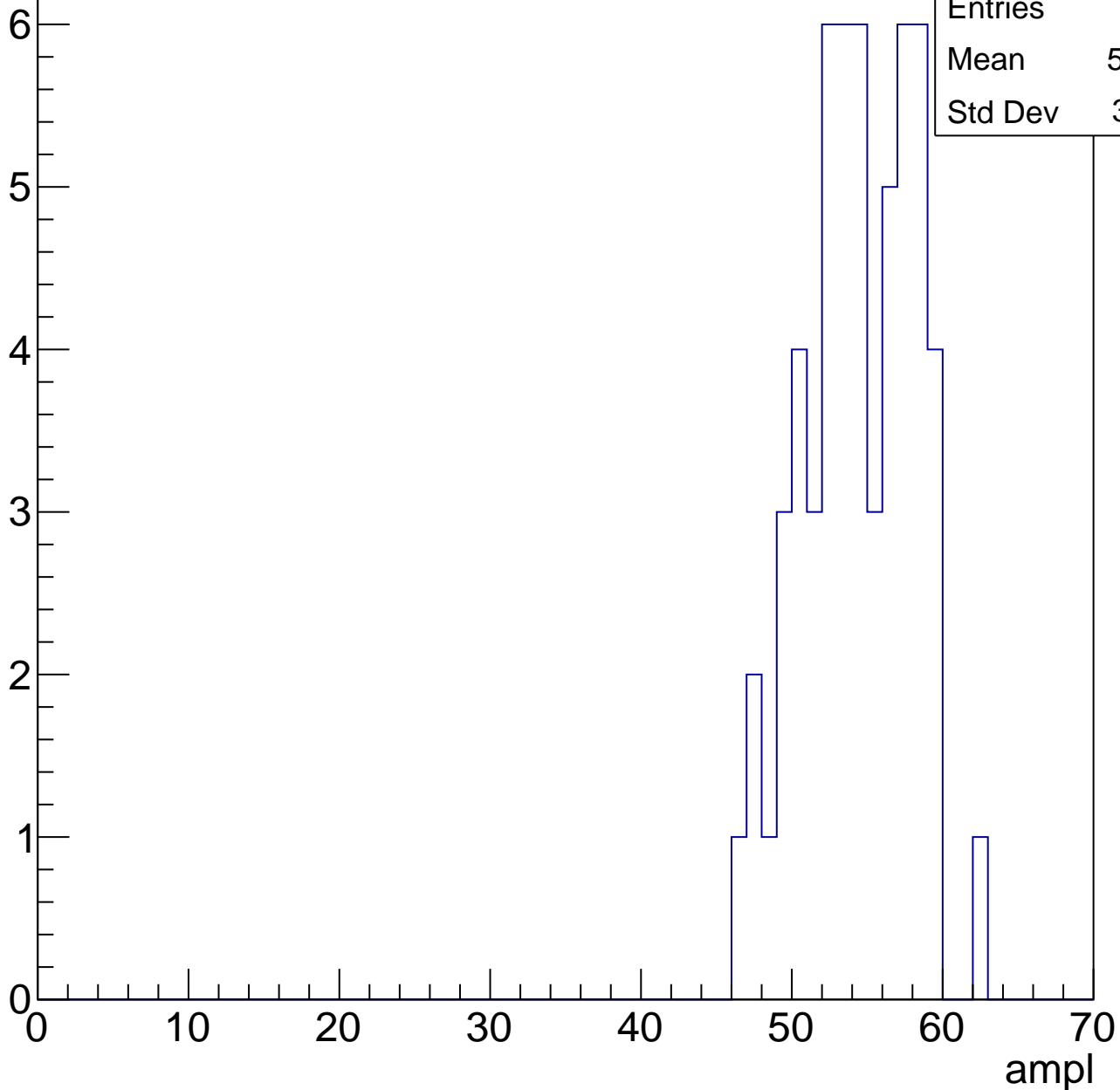


B0L002S, U17-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

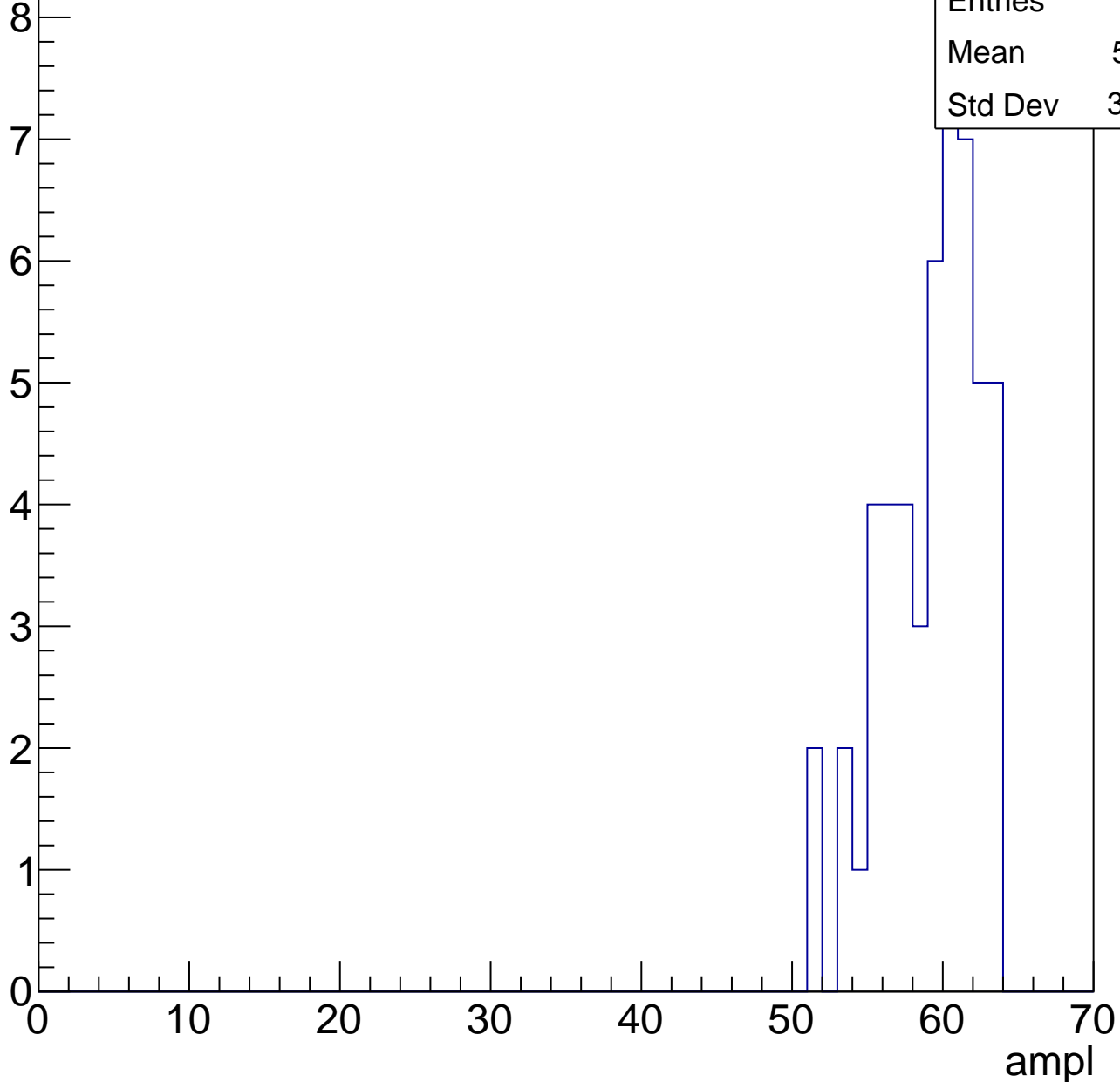
Entries	57
Mean	53.95
Std Dev	3.581



B0L002S, U17-ch110, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

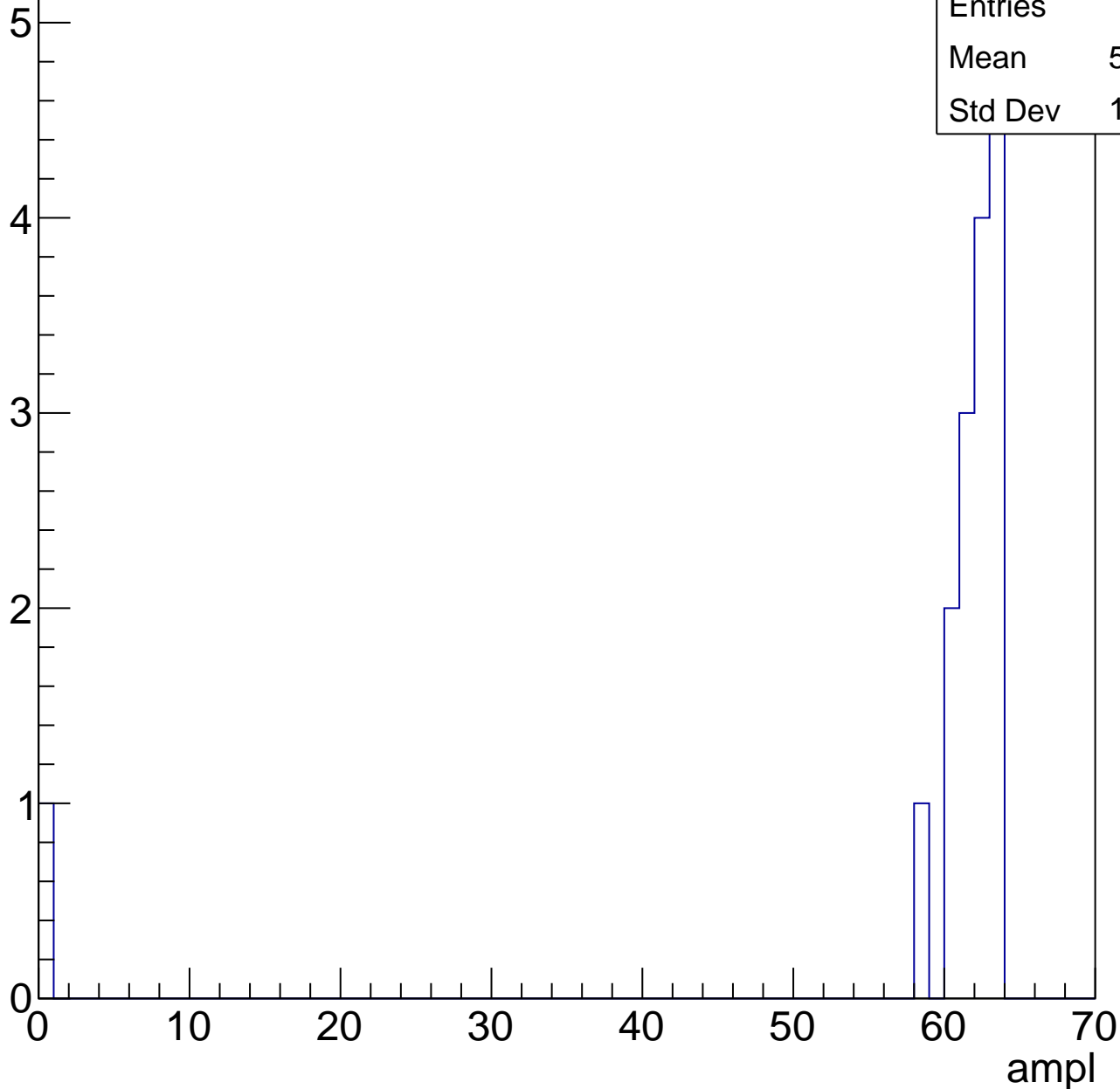


B0L002S, U17-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	16
Mean	57.75
Std Dev	14.97



B0L002S, U17-ch110, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U17-ch111, adc0

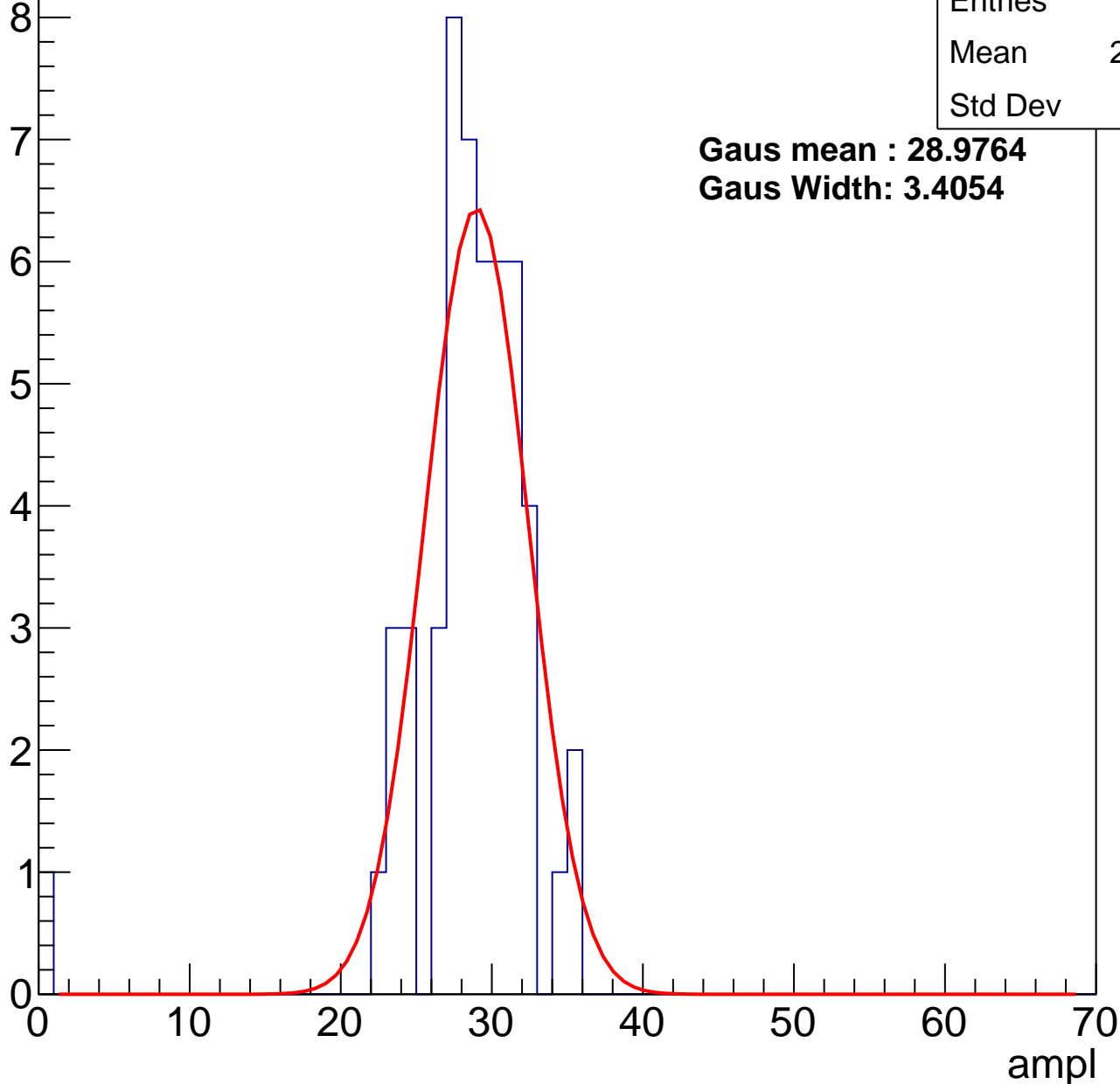
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	27.94
Std Dev	4.94

Gaus mean : 28.9764

Gaus Width: 3.4054



B0L002S, U17-ch111, adc1

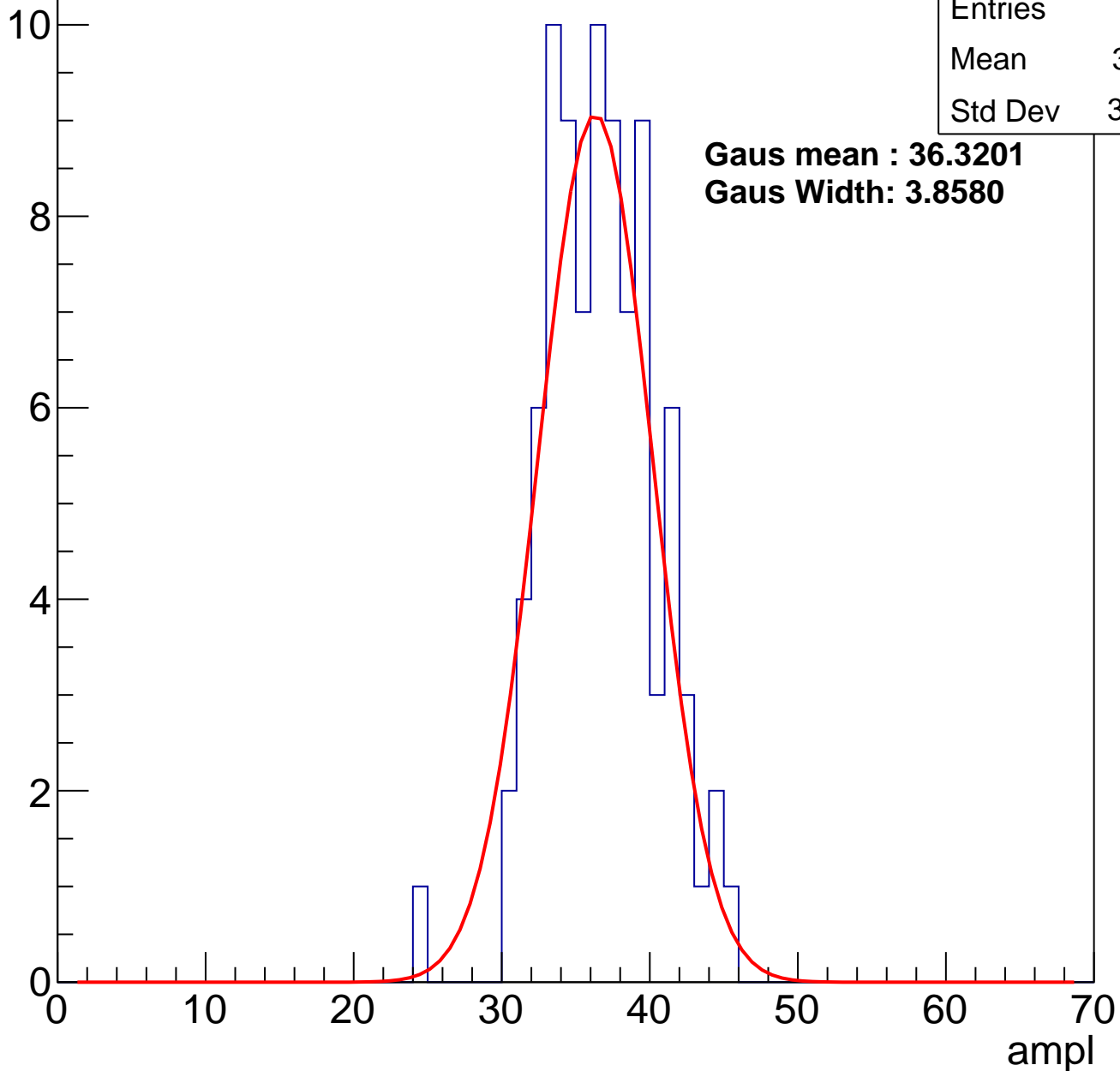
calib_packv5_042523_0143.root, FC#8, port C1

Entries	90
Mean	36.21
Std Dev	3.686

Gaus mean : 36.3201

Gaus Width: 3.8580

Entry



B0L002S, U17-ch111, adc2

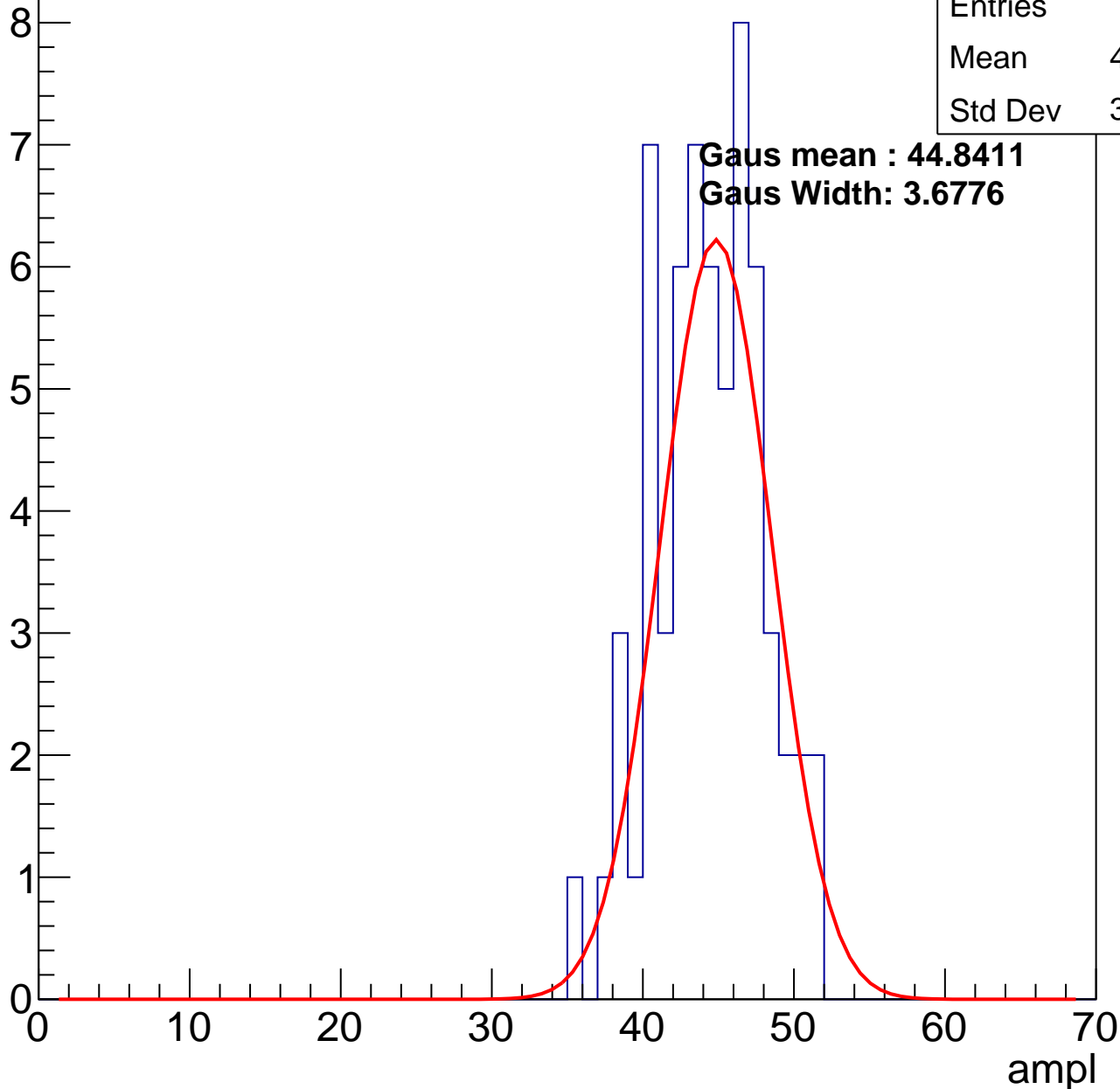
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.87
Std Dev	3.543

Gaus mean : 44.8411

Gaus Width: 3.6776

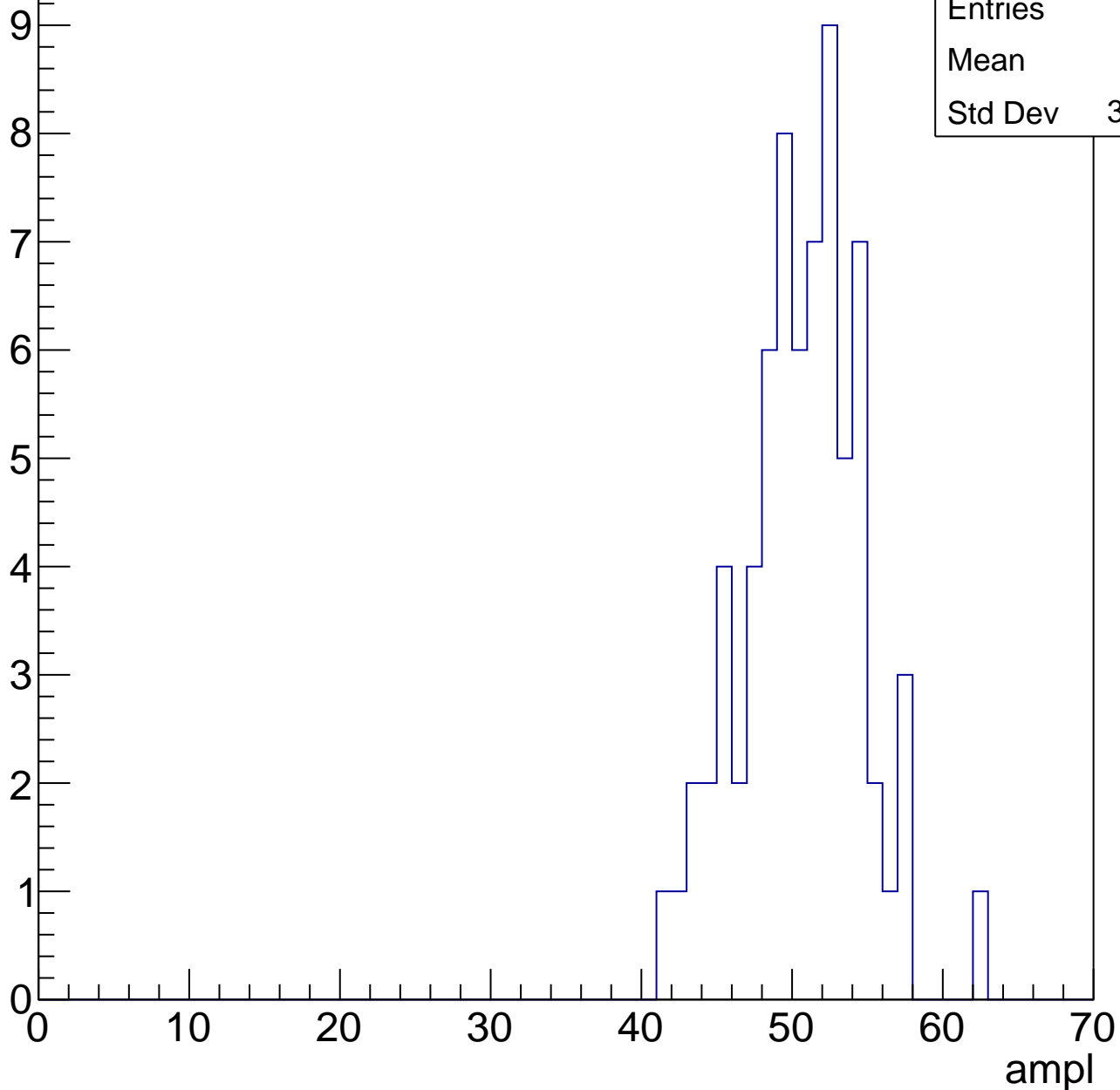


B0L002S, U17-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	50.2
Std Dev	3.917

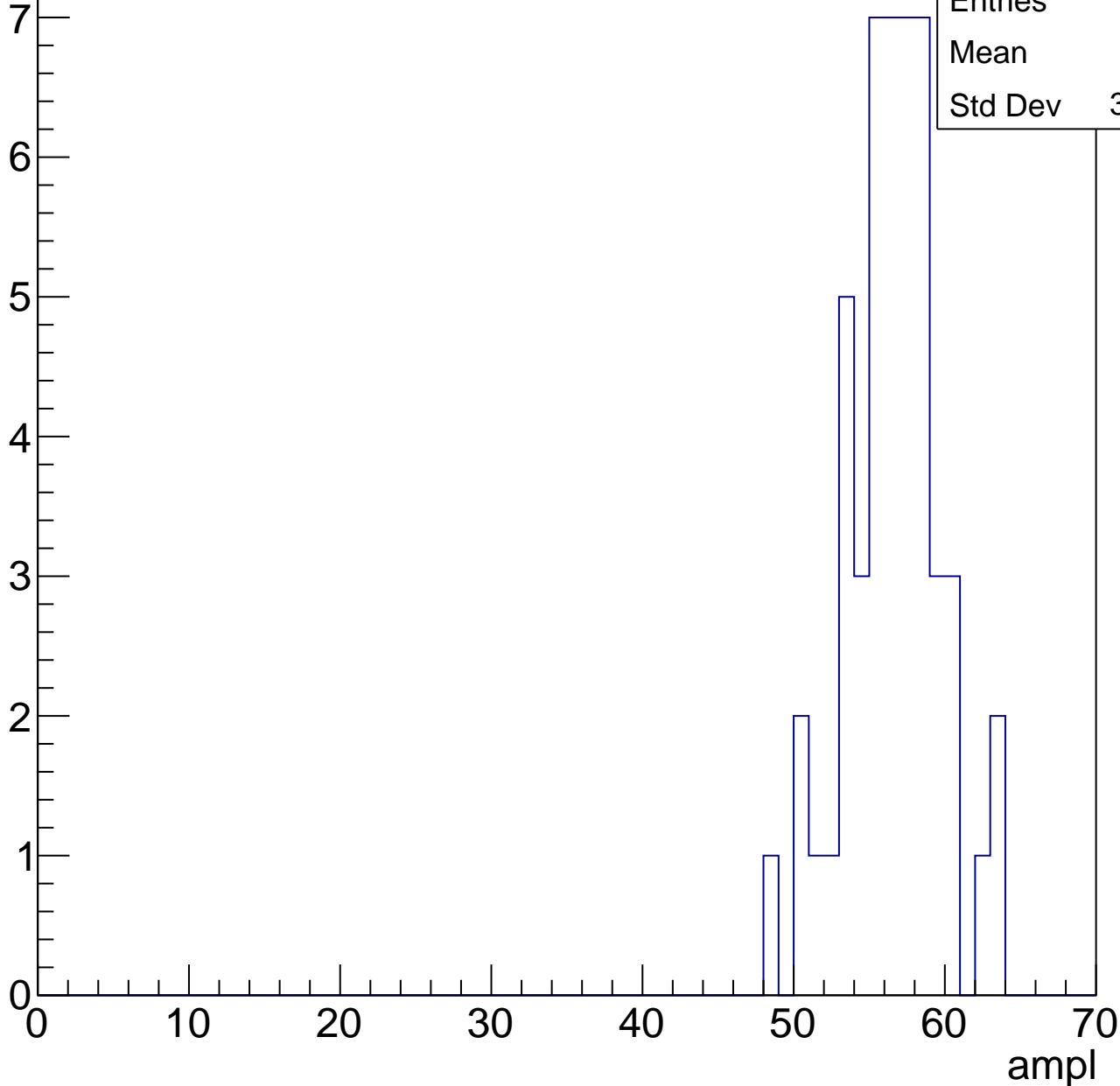


B0L002S, U17-ch111, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.1
Std Dev	3.113

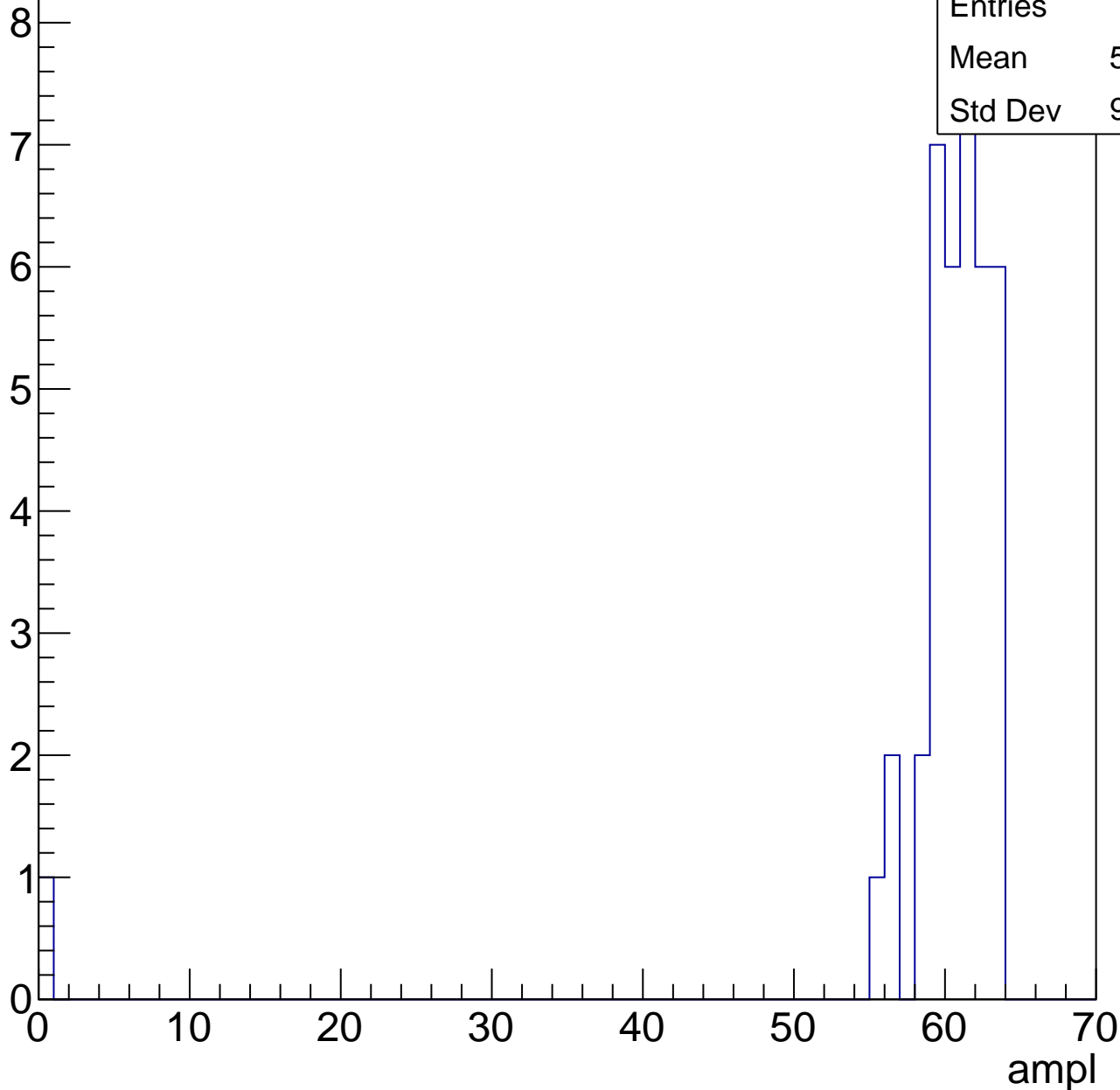


B0L002S, U17-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

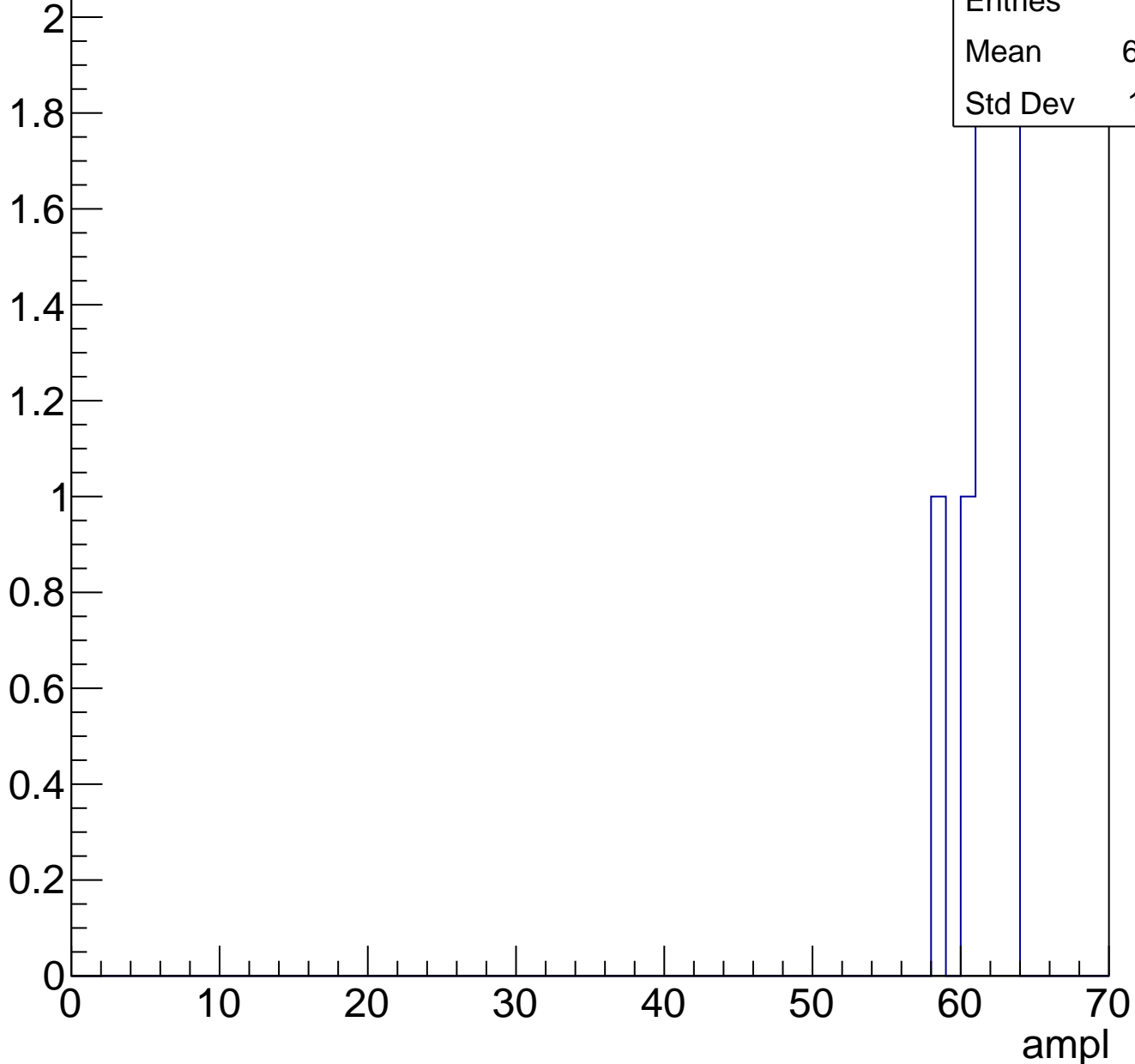
Entries	39
Mean	58.82
Std Dev	9.745



B0L002S, U17-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

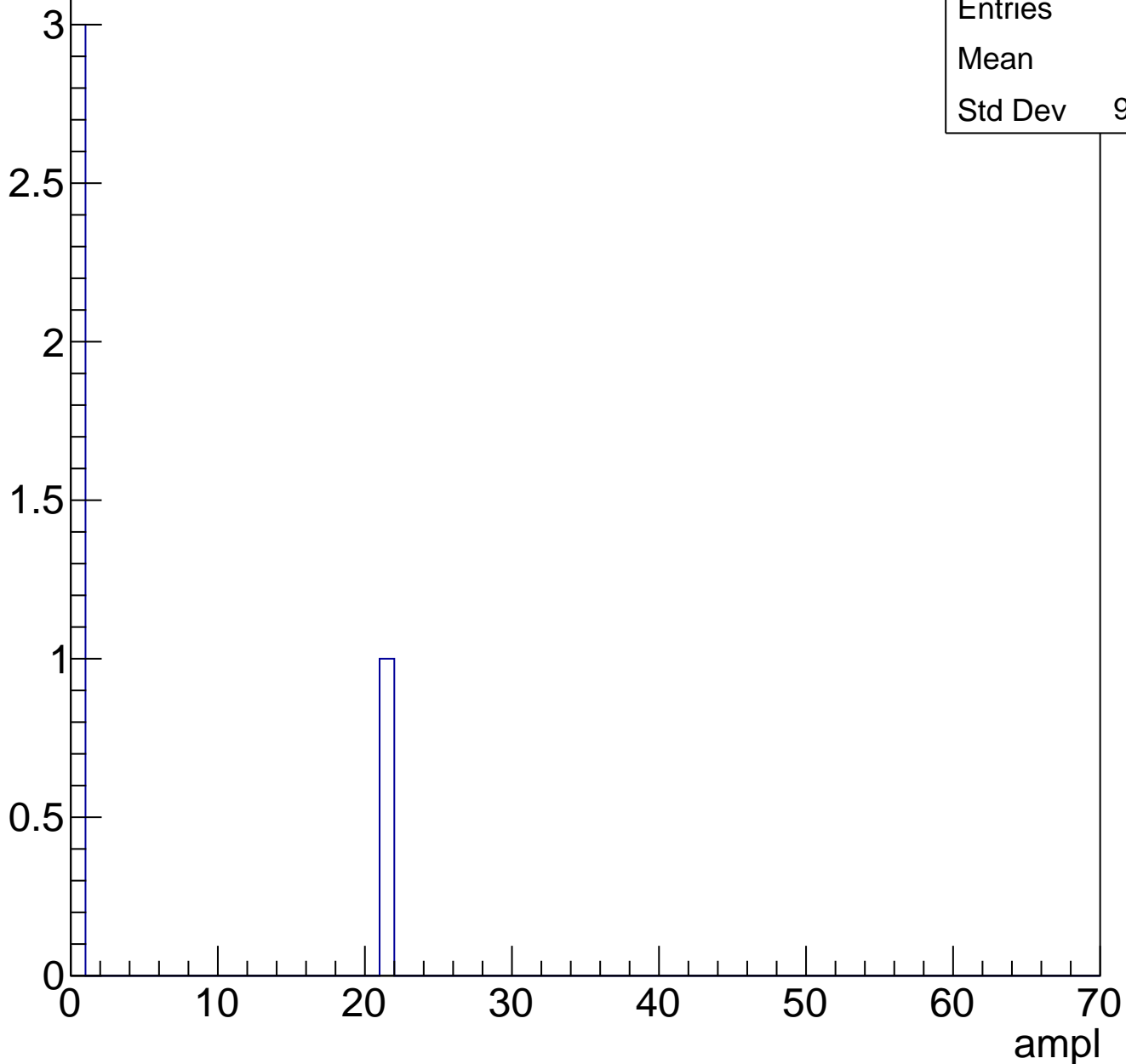


Entries	8
Mean	61.25
Std Dev	1.561

B0L002S, U17-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch112, adc0

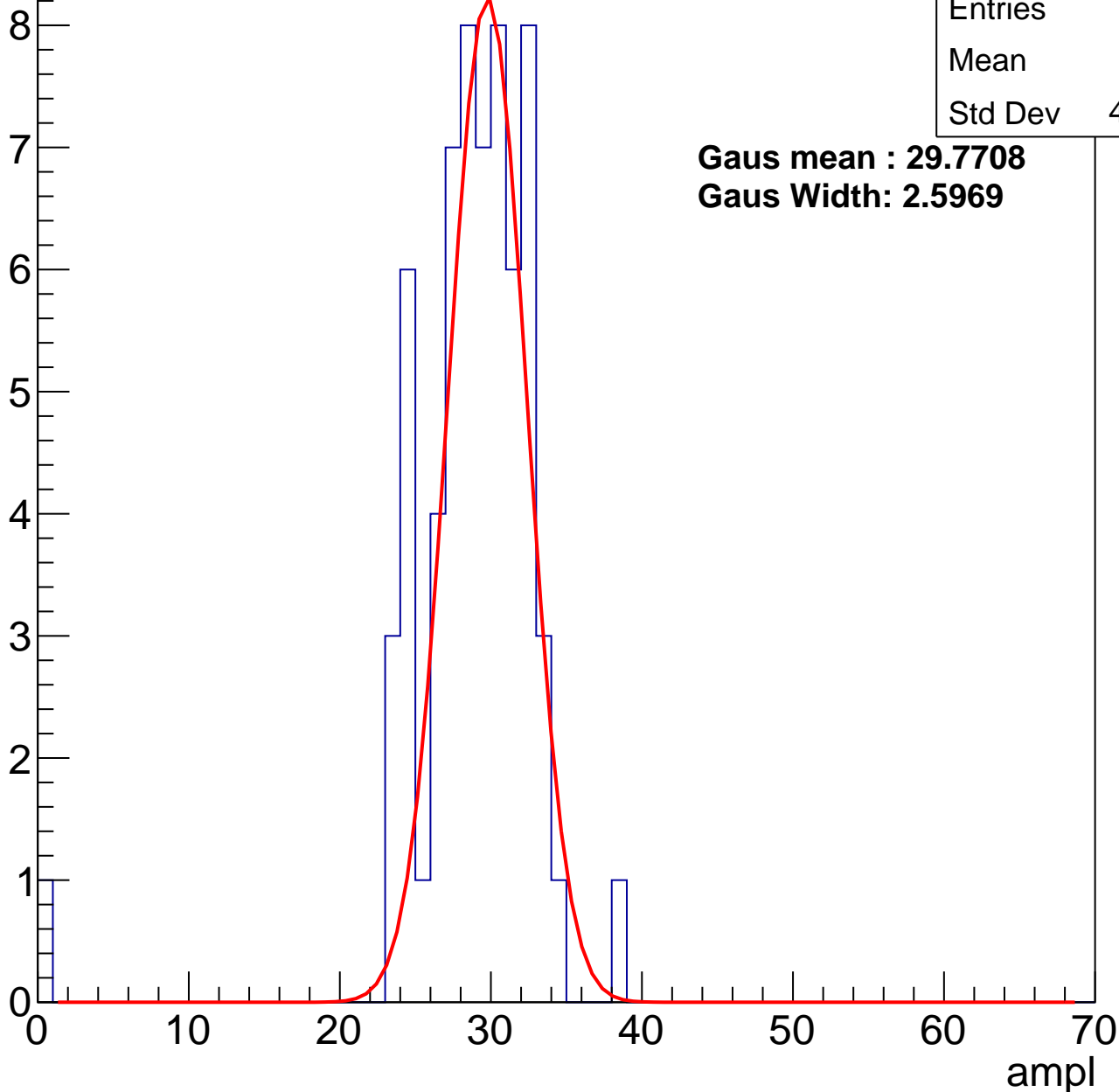
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	28.3
Std Dev	4.699

Gaus mean : 29.7708

Gaus Width: 2.5969



B0L002S, U17-ch112, adc1

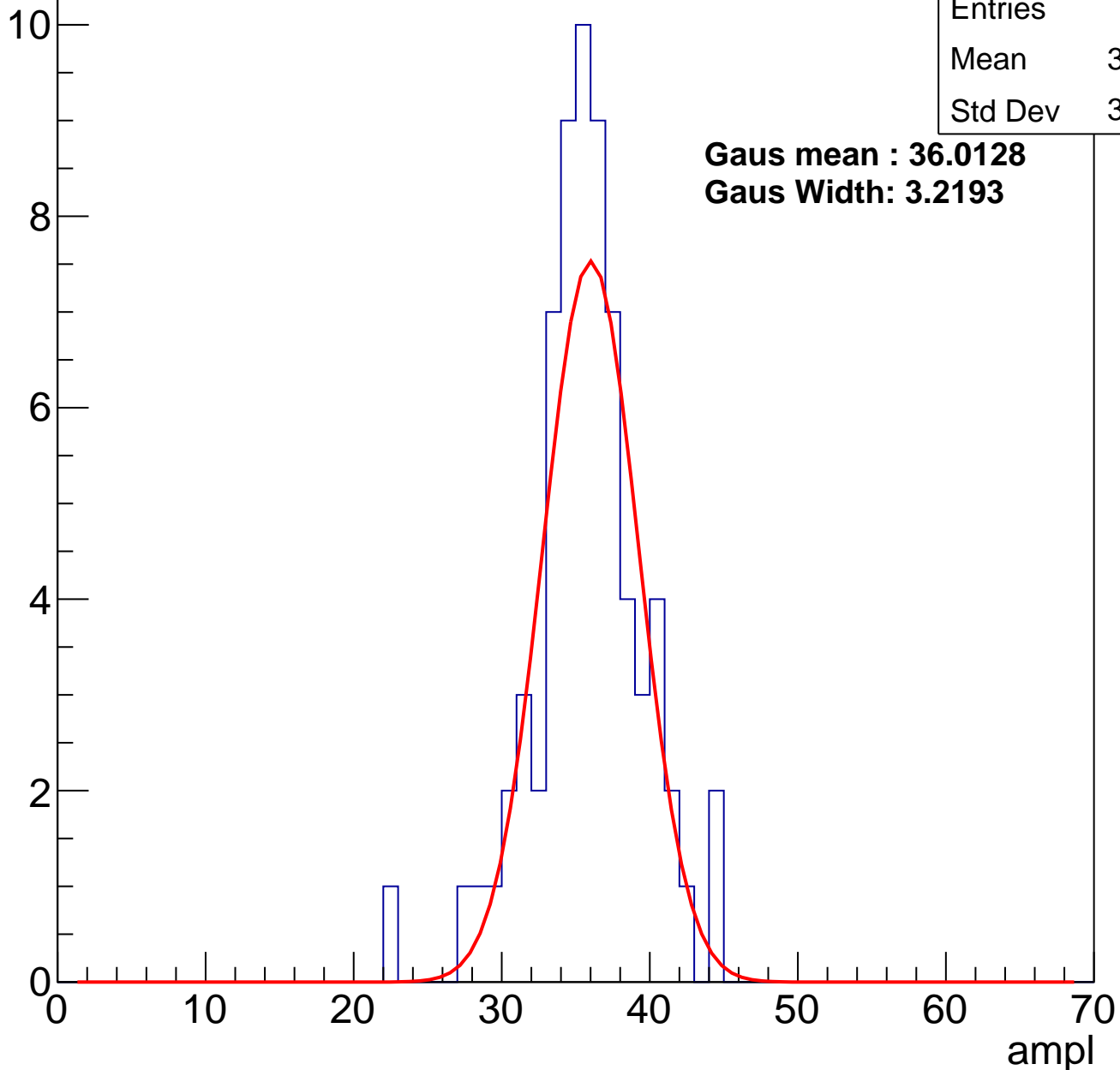
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	35.28
Std Dev	3.749

Gaus mean : 36.0128

Gaus Width: 3.2193

Entry



B0L002S, U17-ch112, adc2

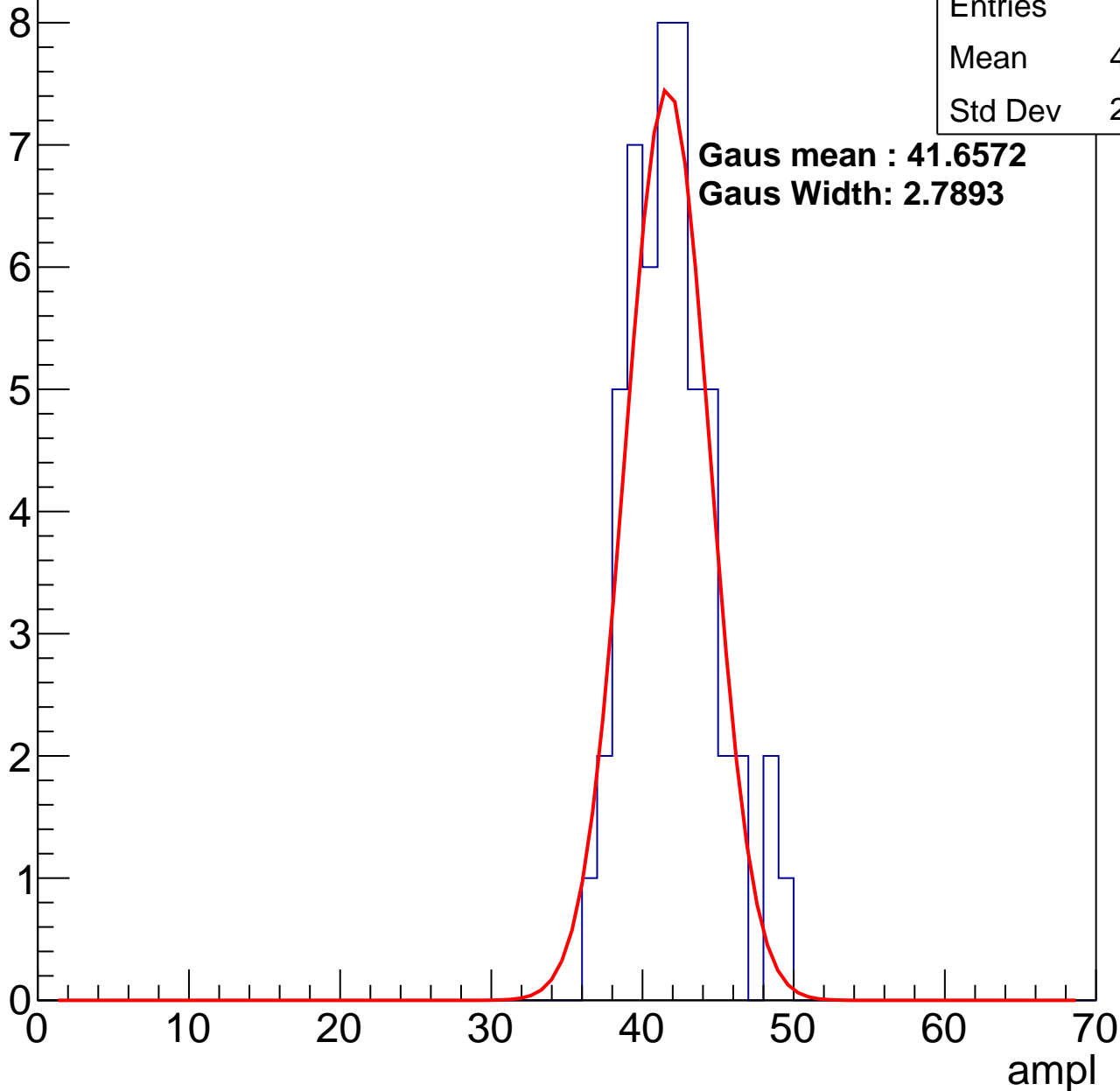
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	41.46
Std Dev	2.859

Gaus mean : 41.6572

Gaus Width: 2.7893



B0L002S, U17-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

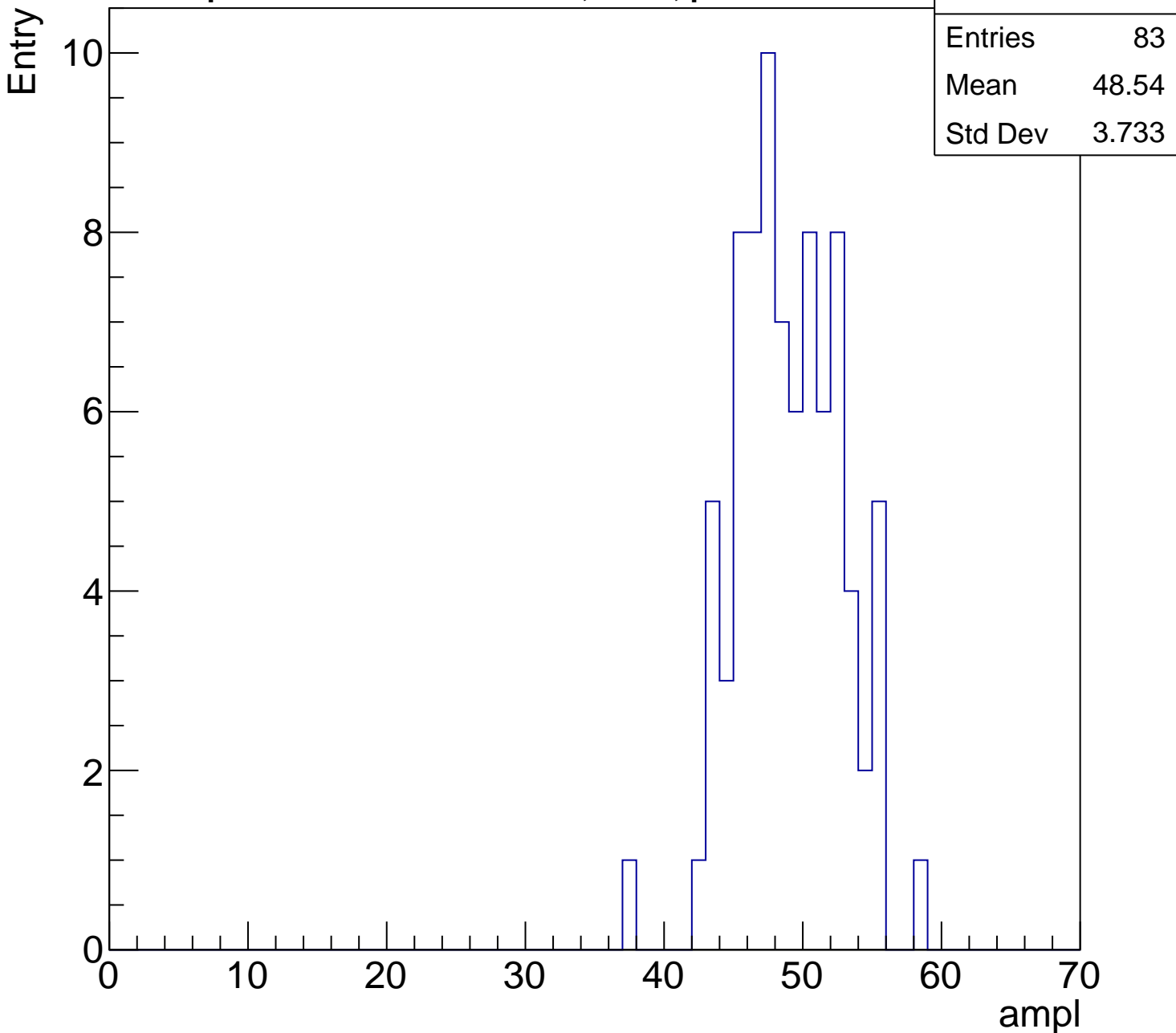
Entries	83
Mean	48.54
Std Dev	3.733

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

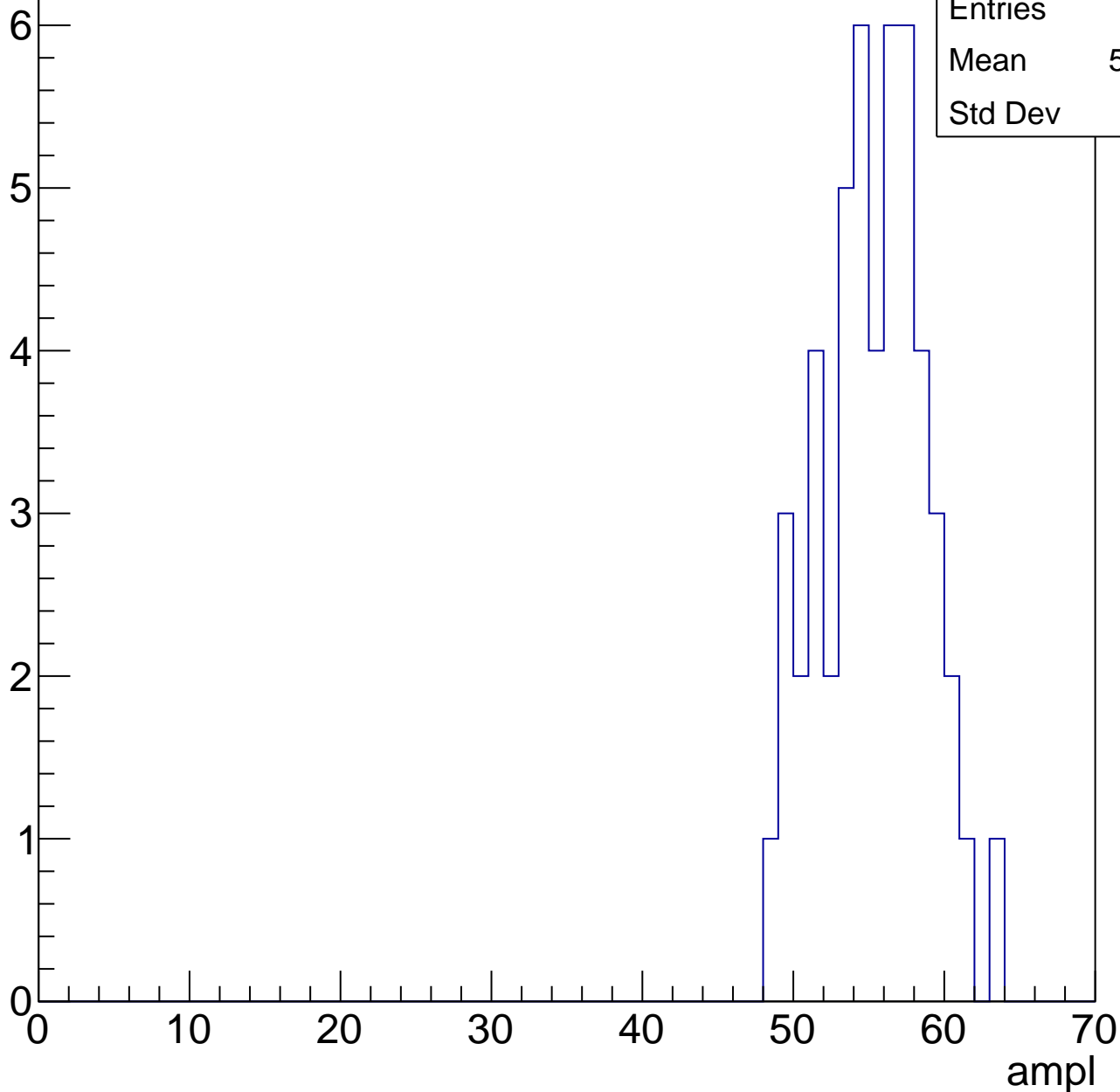


B0L002S, U17-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

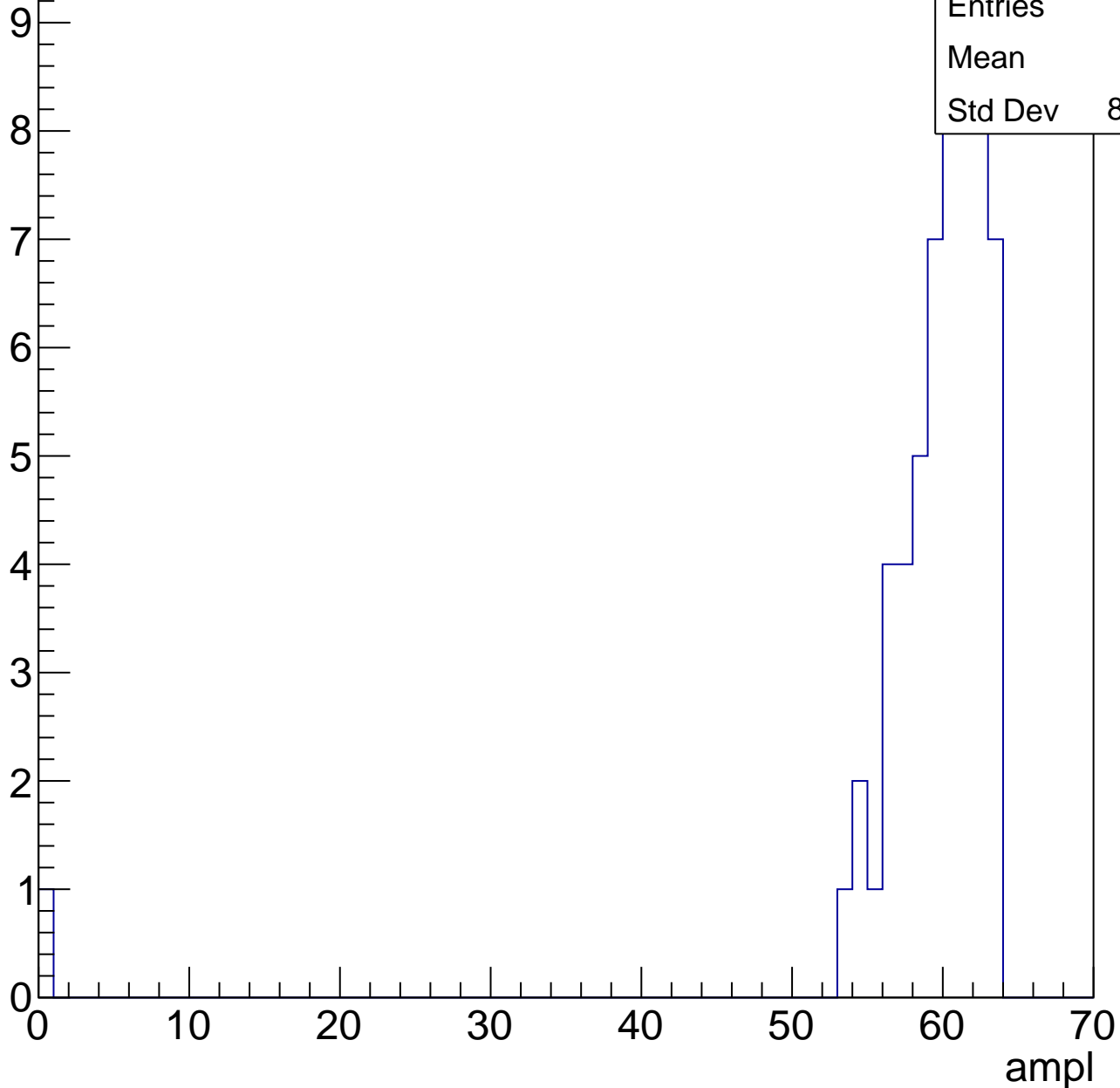
Entries	50
Mean	54.86
Std Dev	3.4



B0L002S, U17-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	58
Mean	58.6
Std Dev	8.166

B0L002S, U17-ch112, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch113, adc0

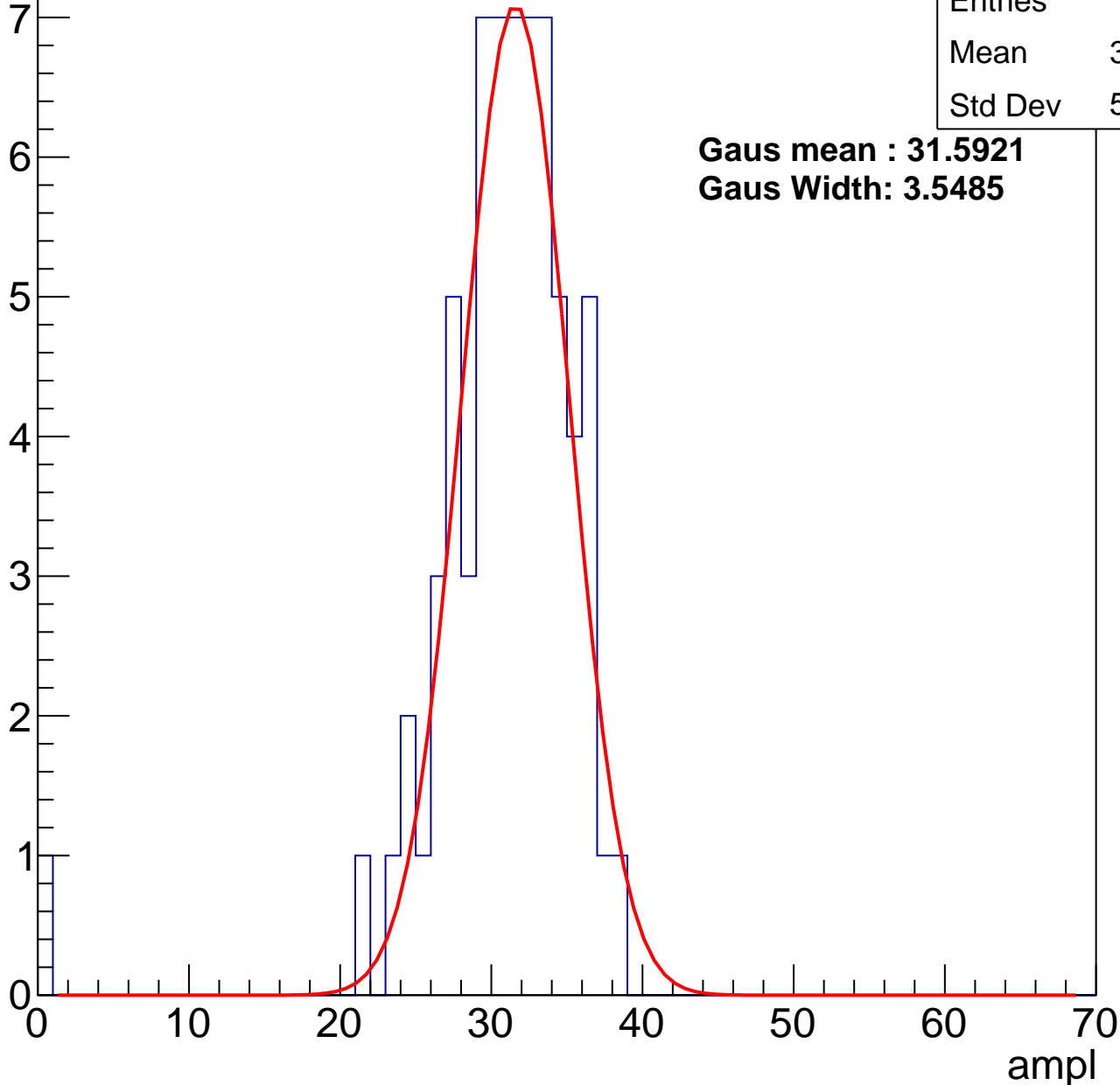
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.35
Std Dev	5.147

Gaus mean : 31.5921

Gaus Width: 3.5485



B0L002S, U17-ch113, adc1

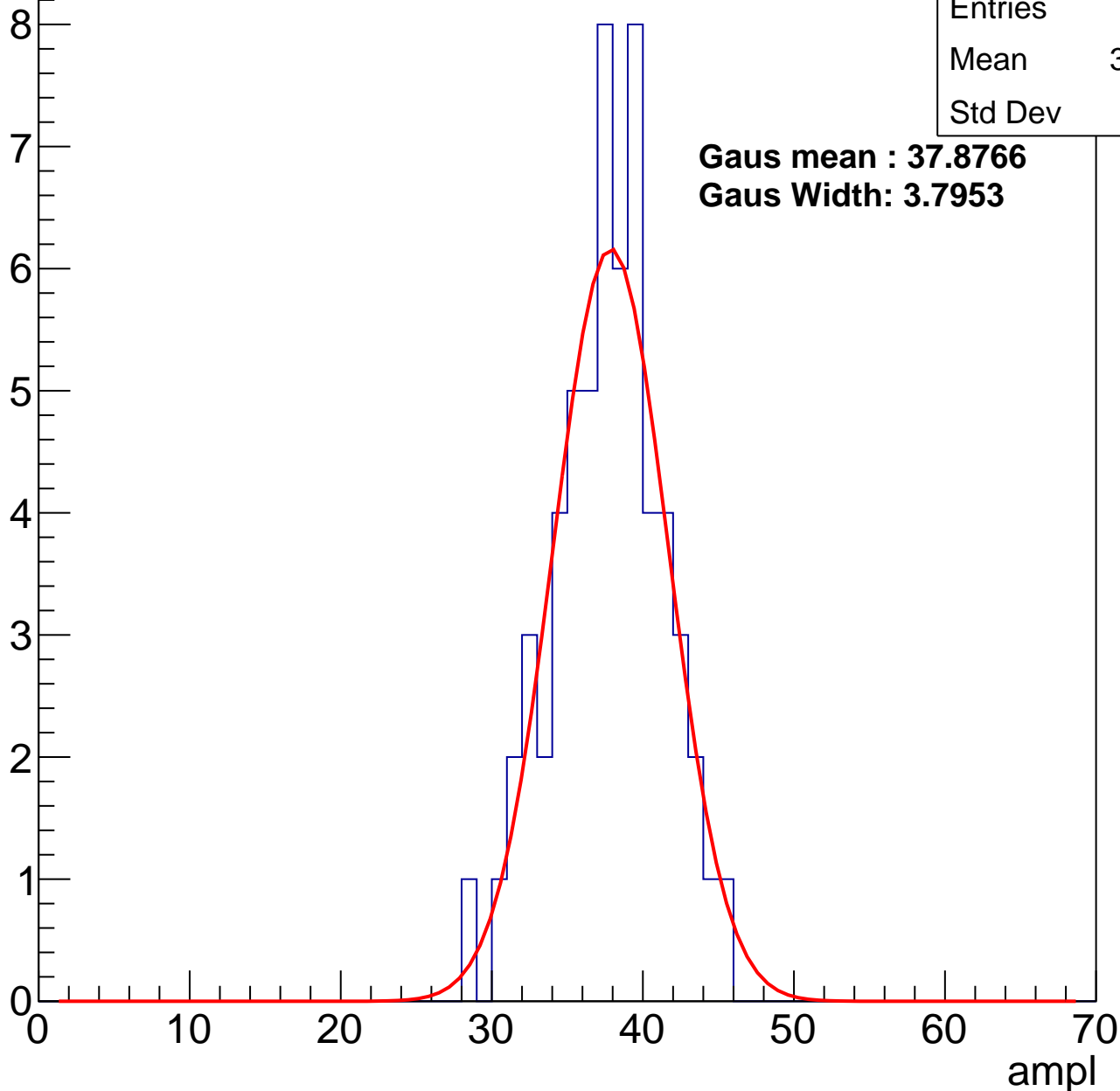
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.23
Std Dev	3.57

Gaus mean : 37.8766

Gaus Width: 3.7953



B0L002S, U17-ch113, adc2

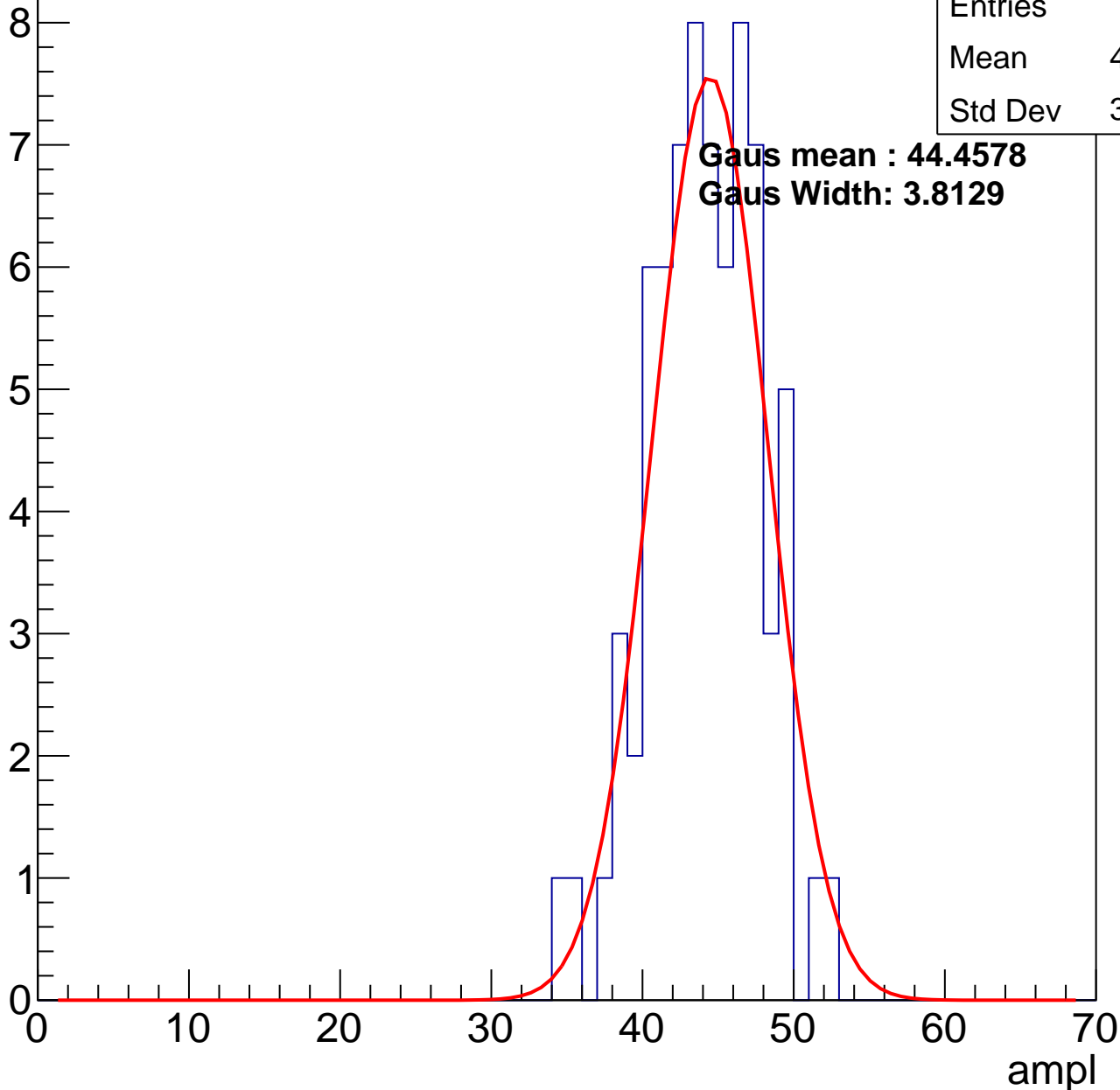
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	43.68
Std Dev	3.615

Gaus mean : 44.4578

Gaus Width: 3.8129

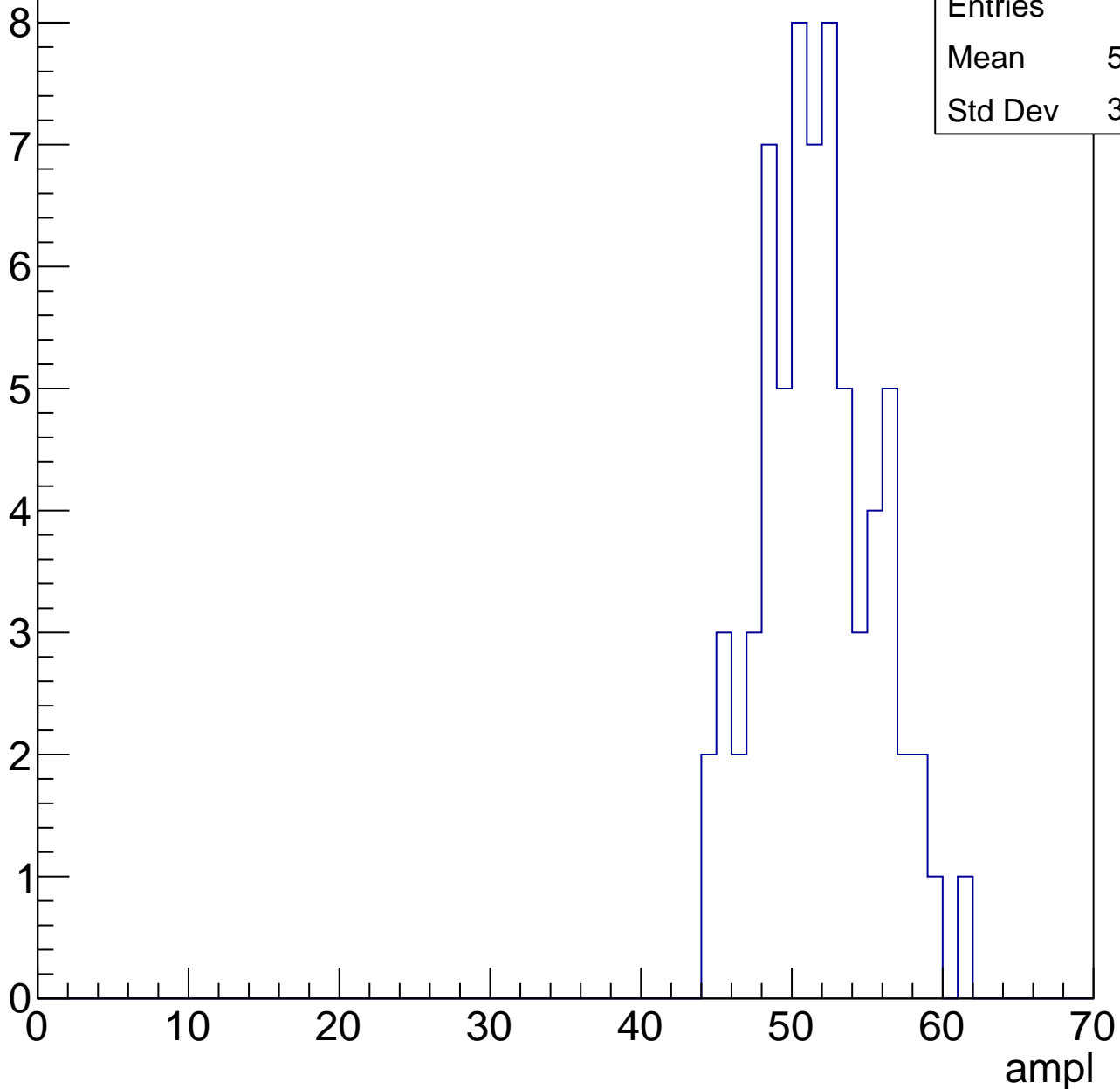


B0L002S, U17-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	51.28
Std Dev	3.772

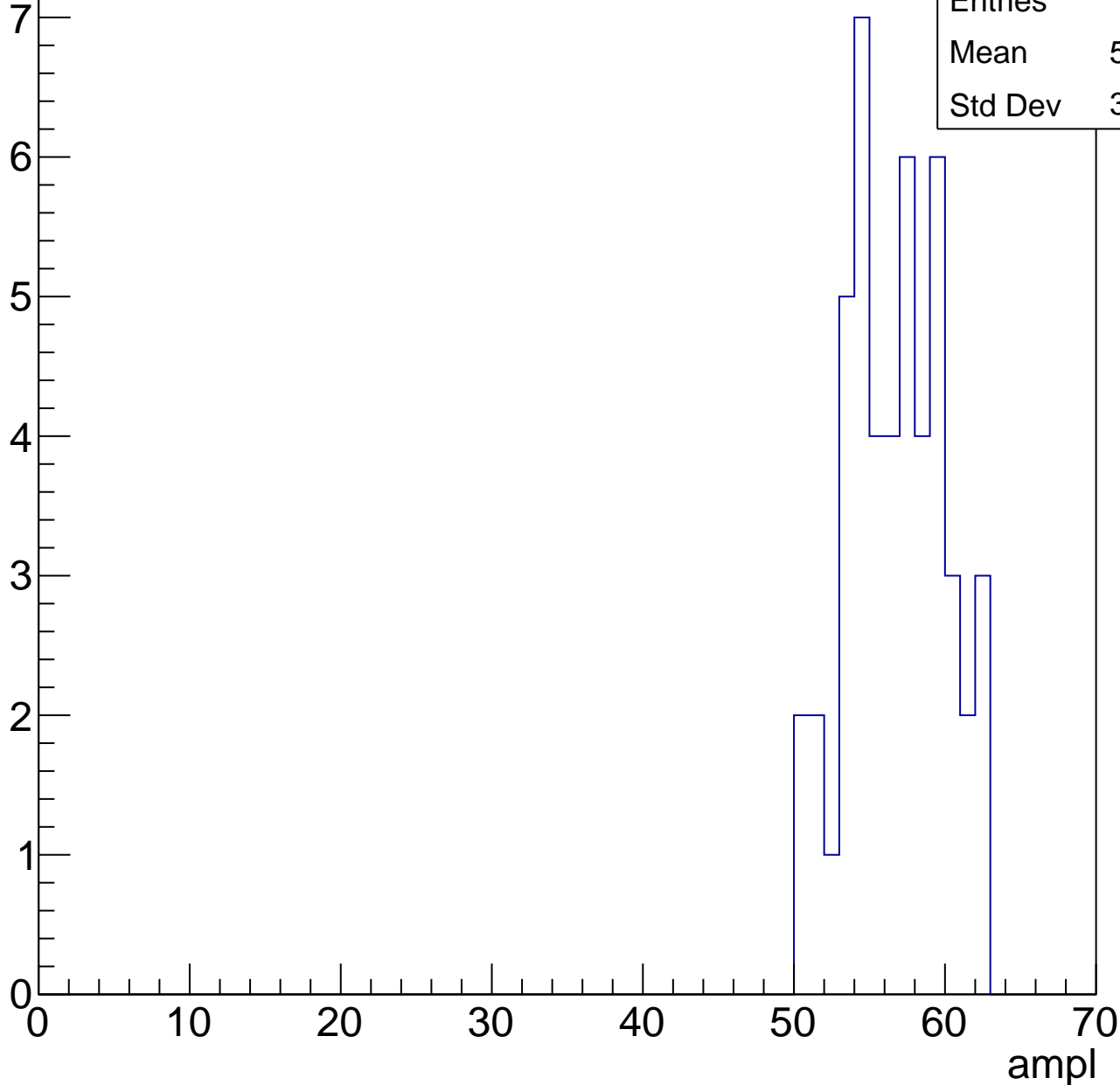


B0L002S, U17-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	56.27
Std Dev	3.174

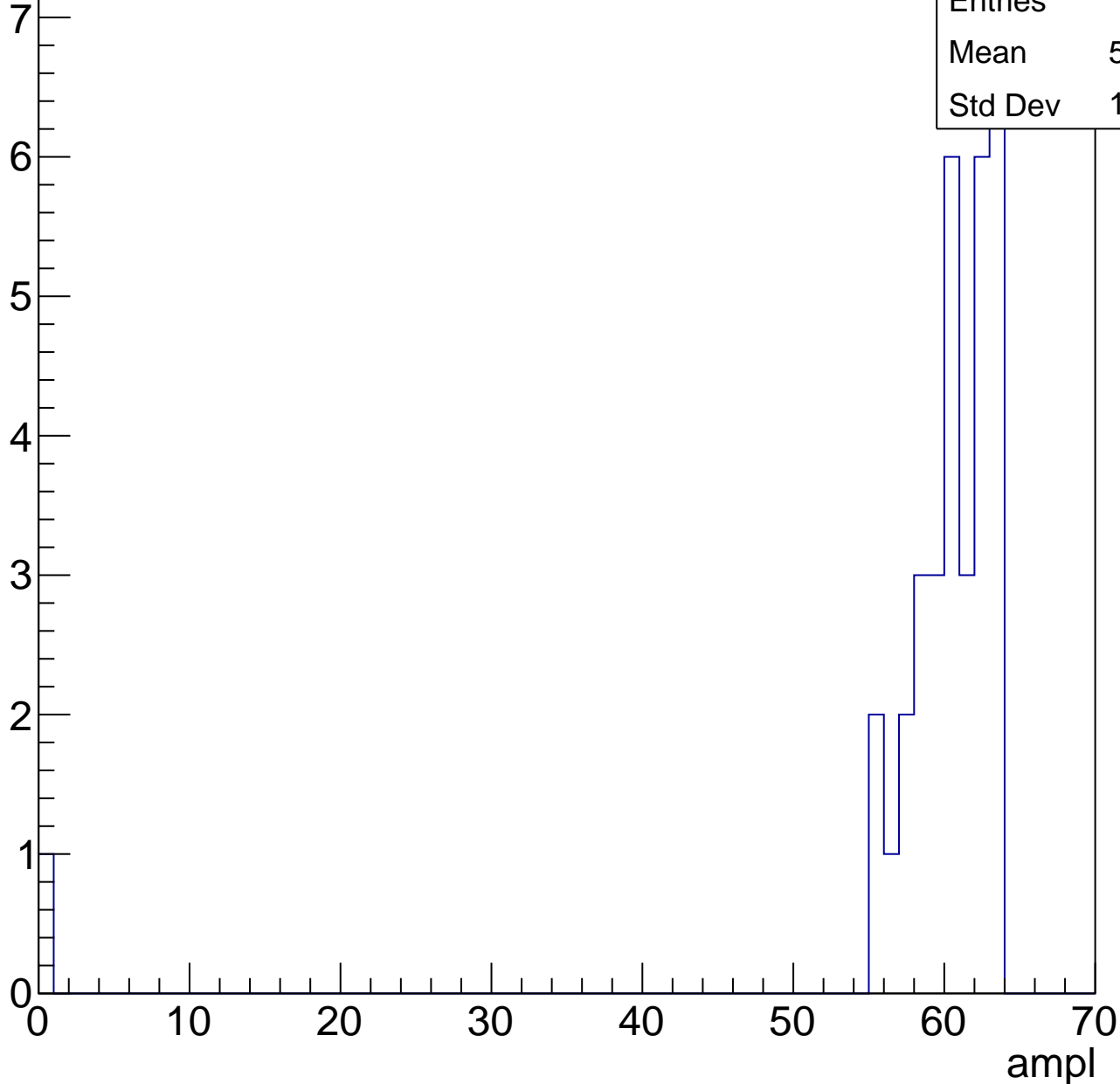


B0L002S, U17-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

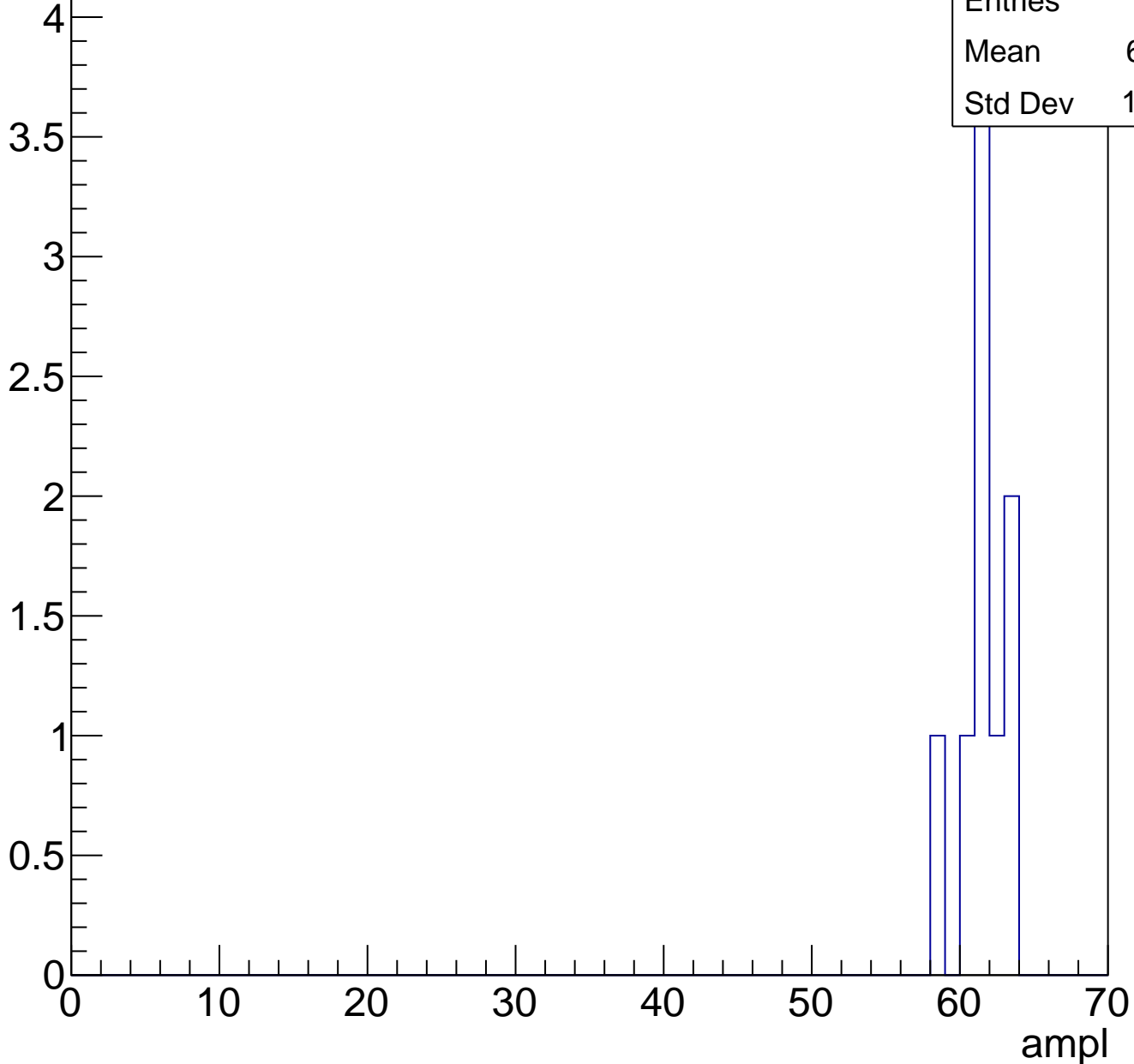
Entries	34
Mean	58.44
Std Dev	10.44



B0L002S, U17-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

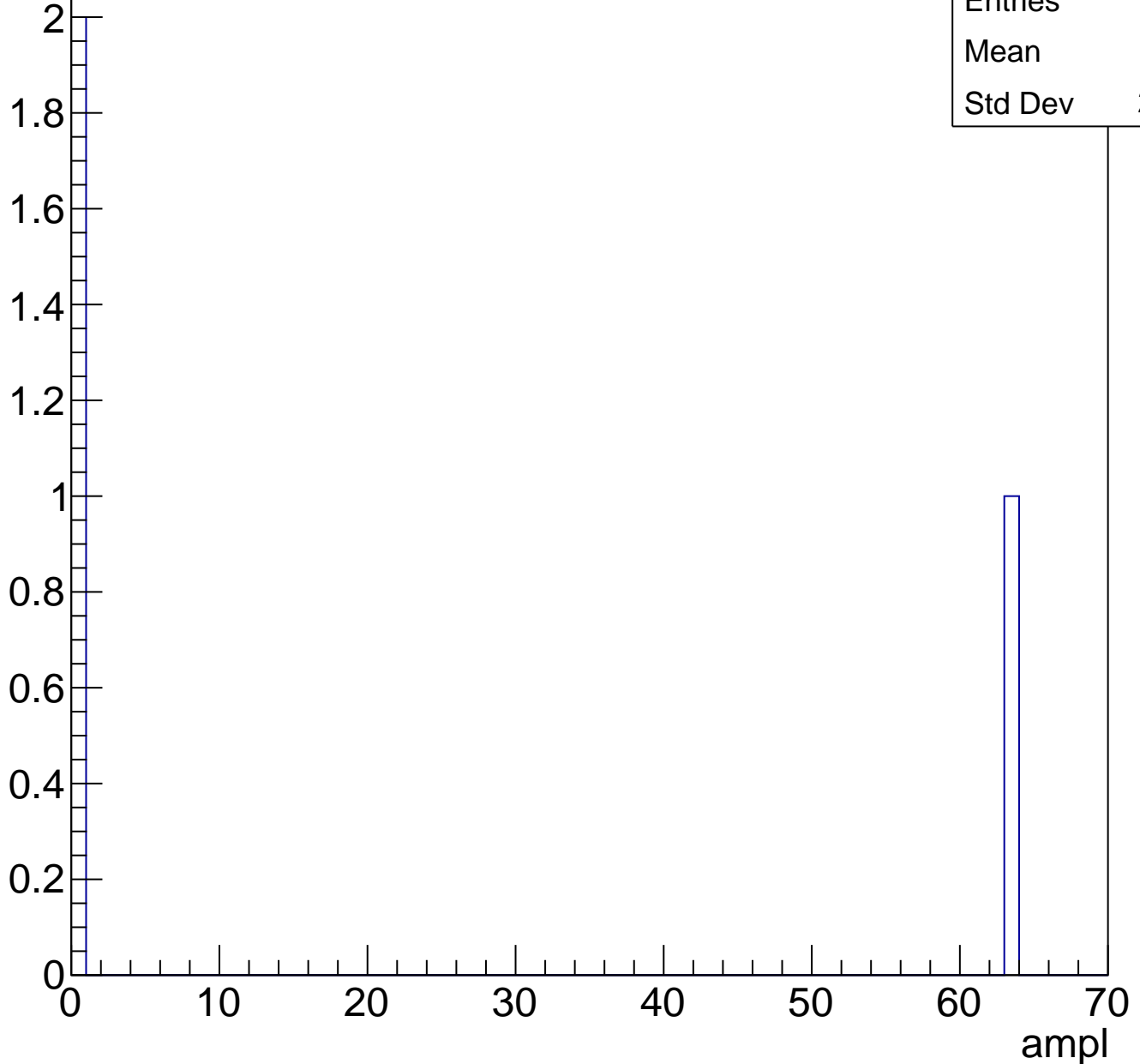
Entry



B0L002S, U17-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch114, adc0

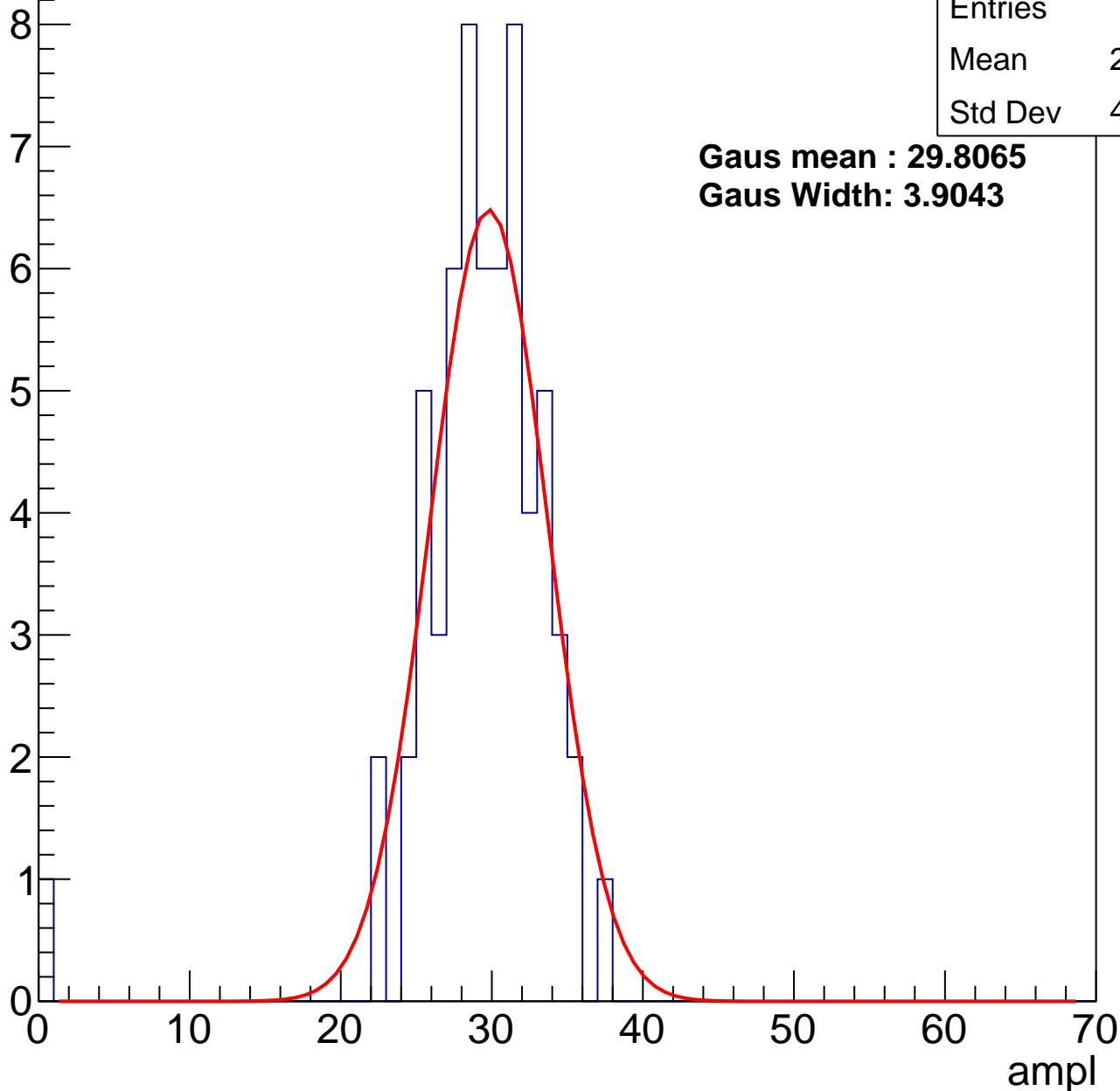
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	28.79
Std Dev	4.903

Gaus mean : 29.8065

Gaus Width: 3.9043



B0L002S, U17-ch114, adc1

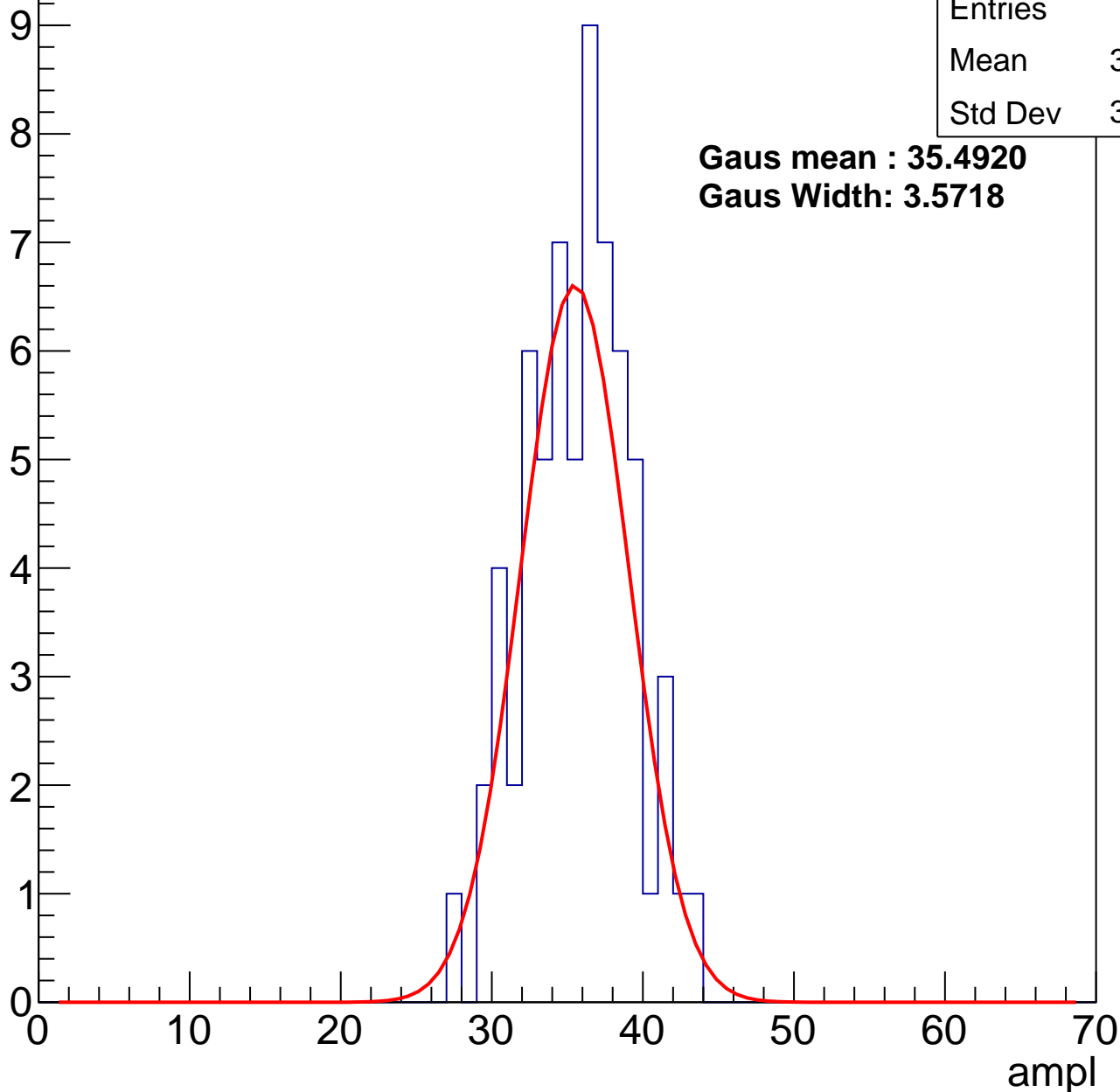
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.25
Std Dev	3.424

Gaus mean : 35.4920

Gaus Width: 3.5718



B0L002S, U17-ch114, adc2

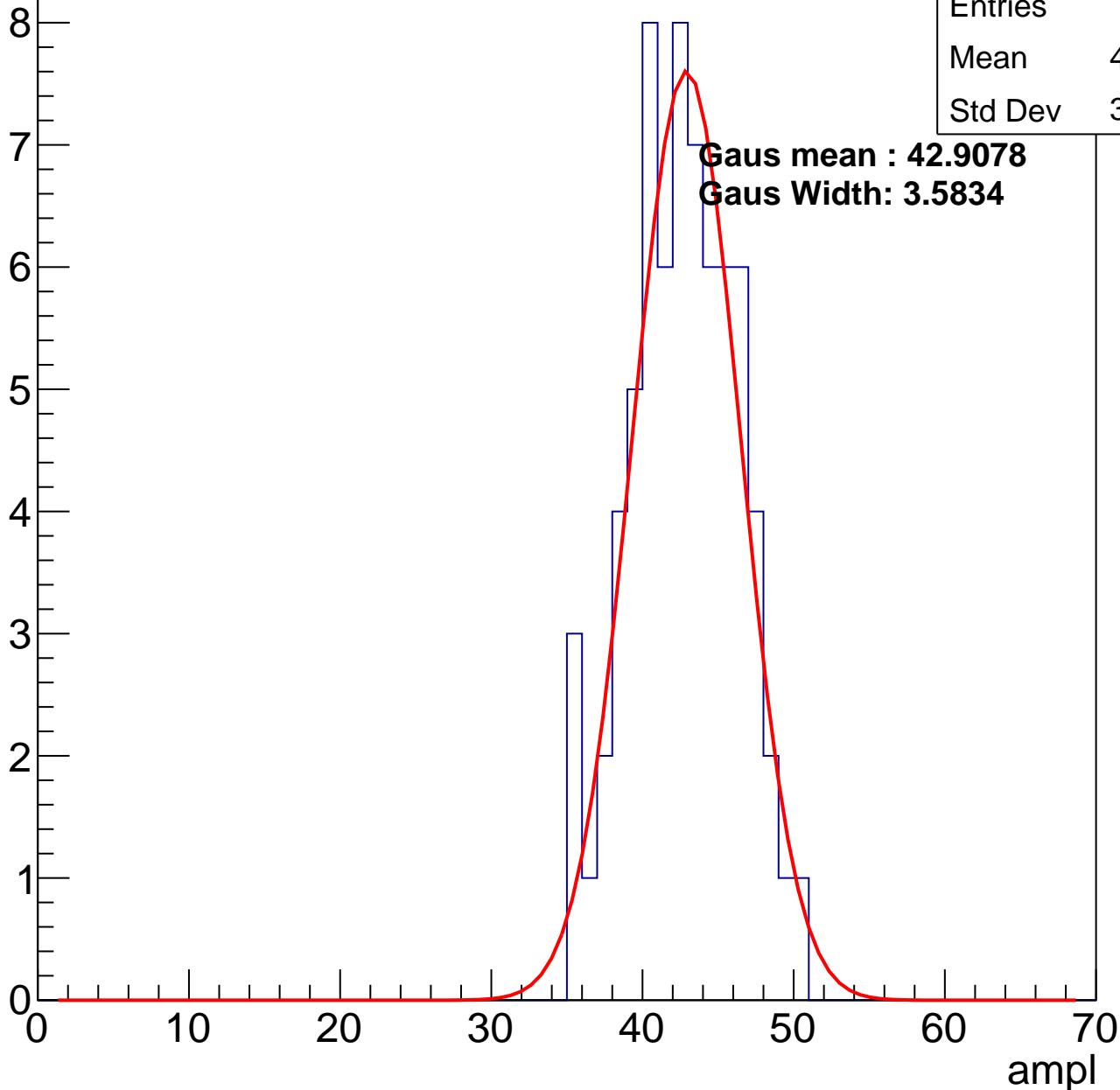
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.26
Std Dev	3.467

Gaus mean : 42.9078

Gaus Width: 3.5834

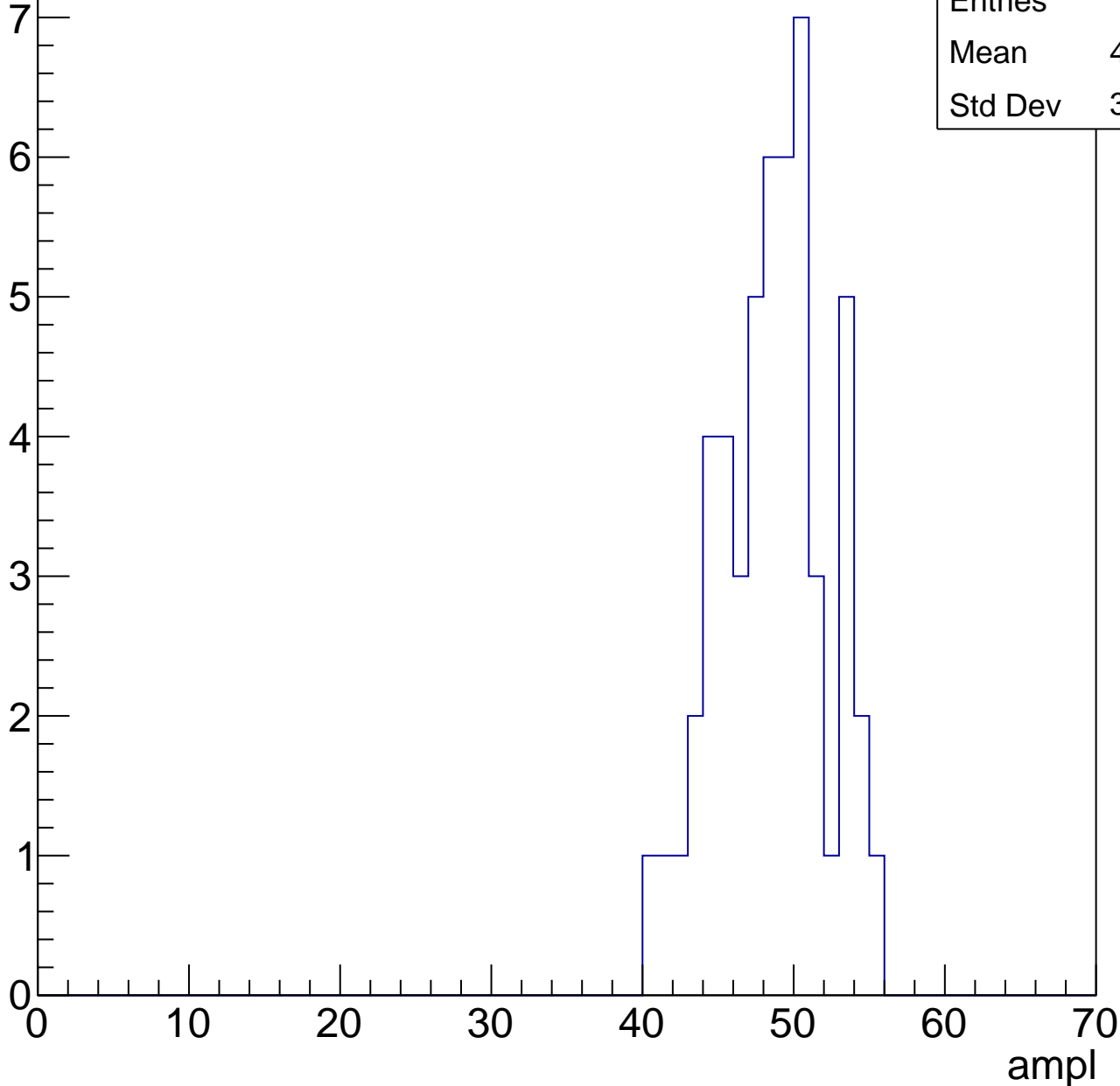


B0L002S, U17-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	48.13
Std Dev	3.503

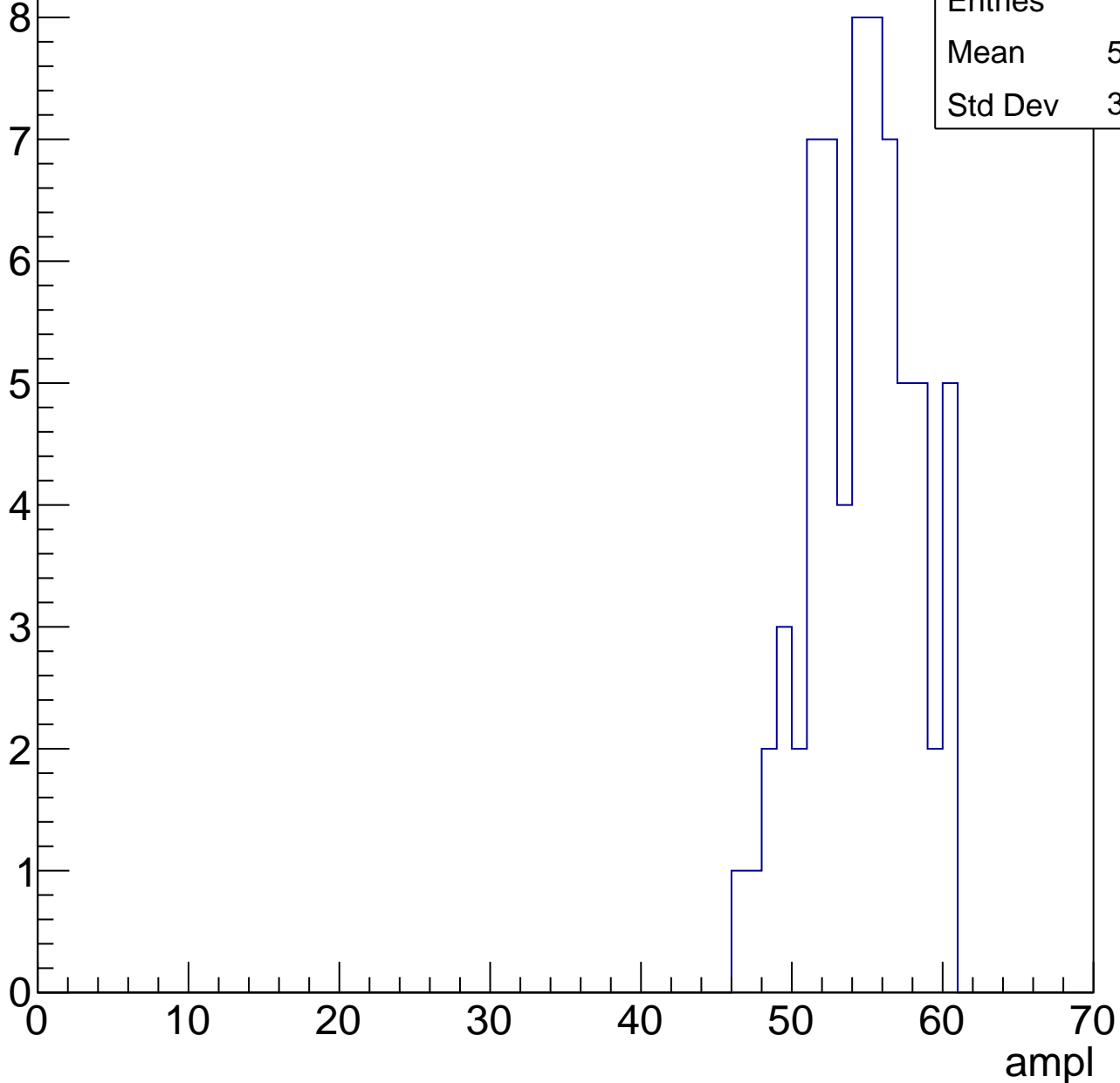


B0L002S, U17-ch114, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	54.12
Std Dev	3.406

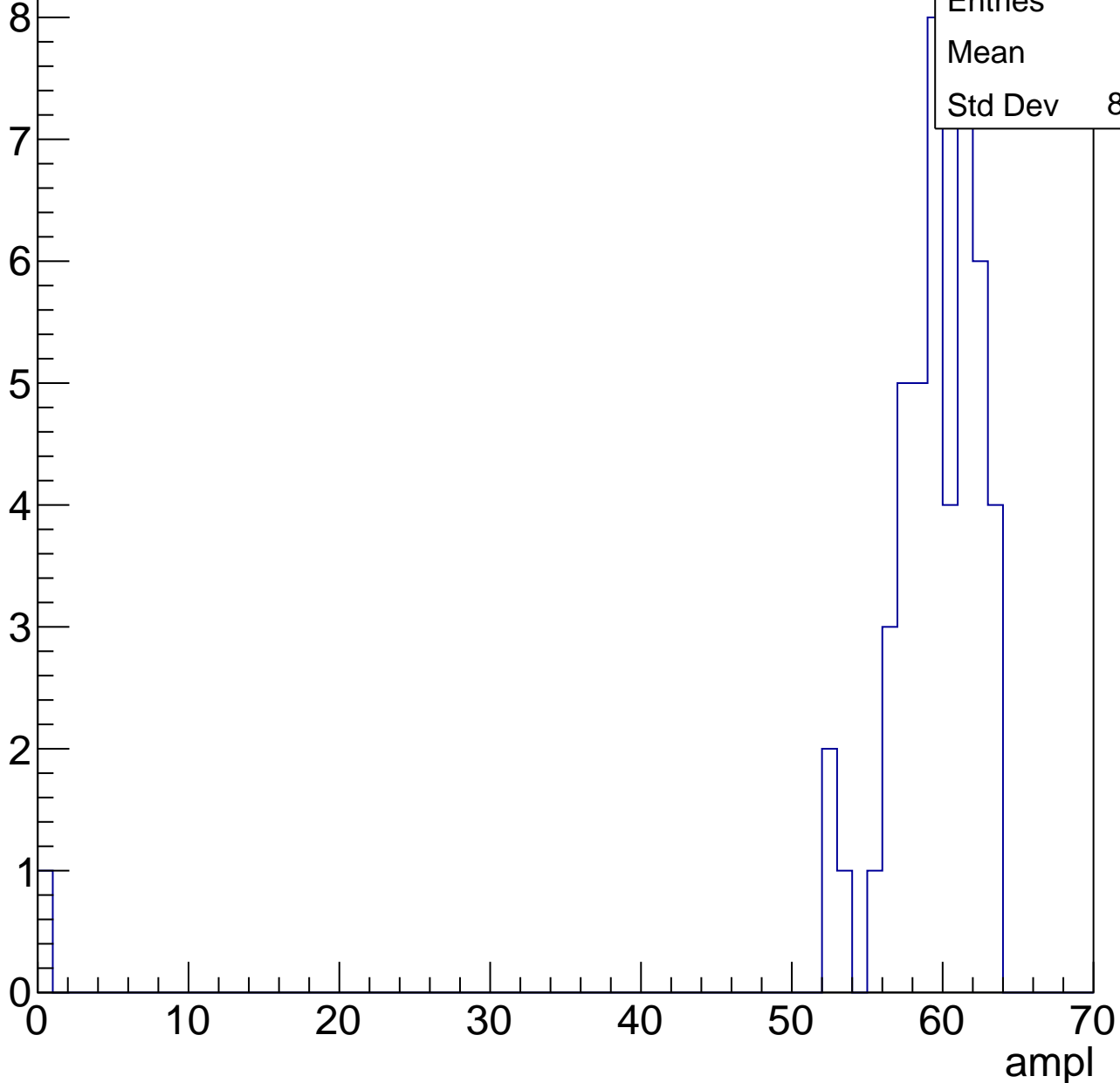


B0L002S, U17-ch114, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	57.9
Std Dev	8.872

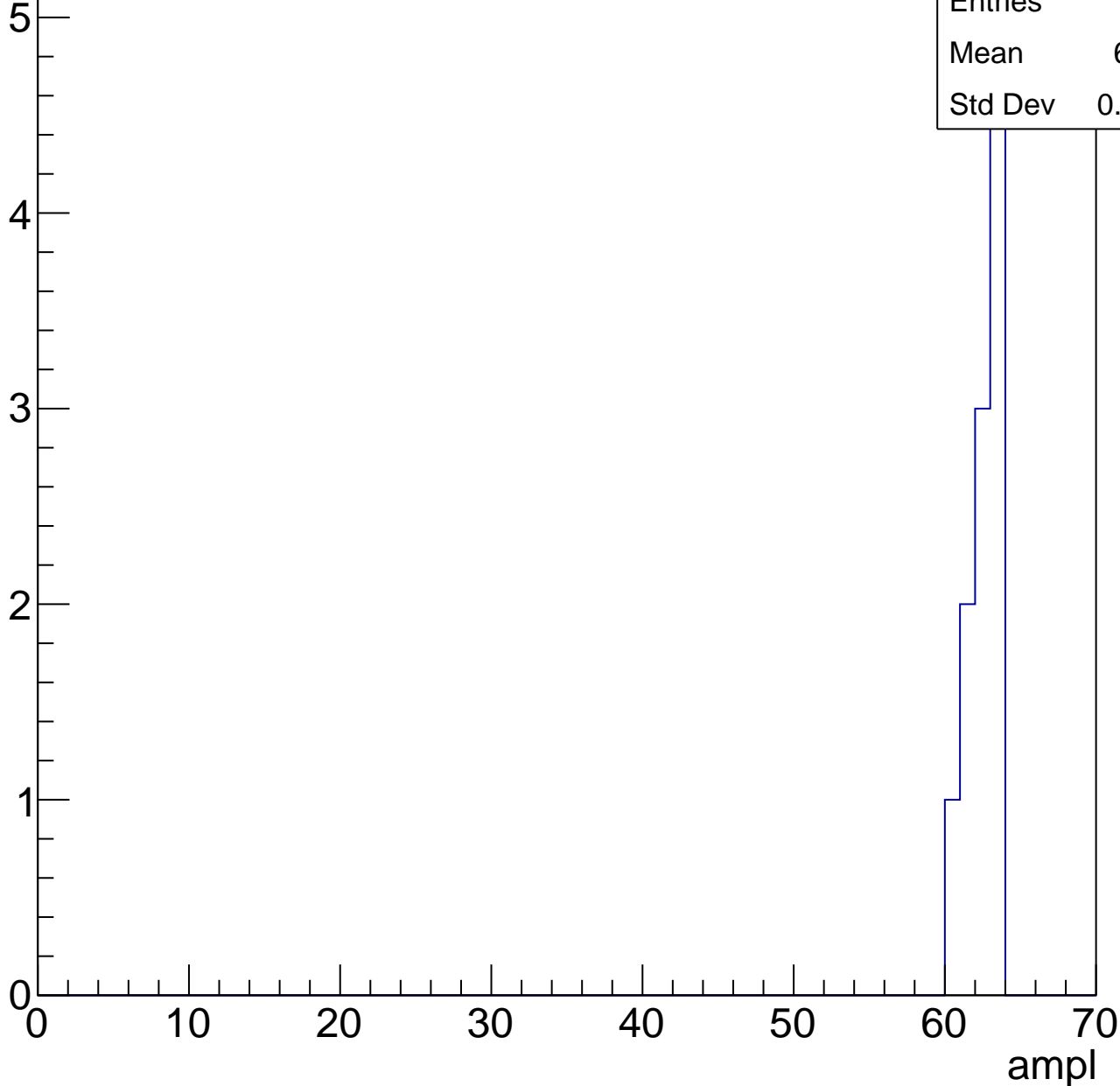


B0L002S, U17-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

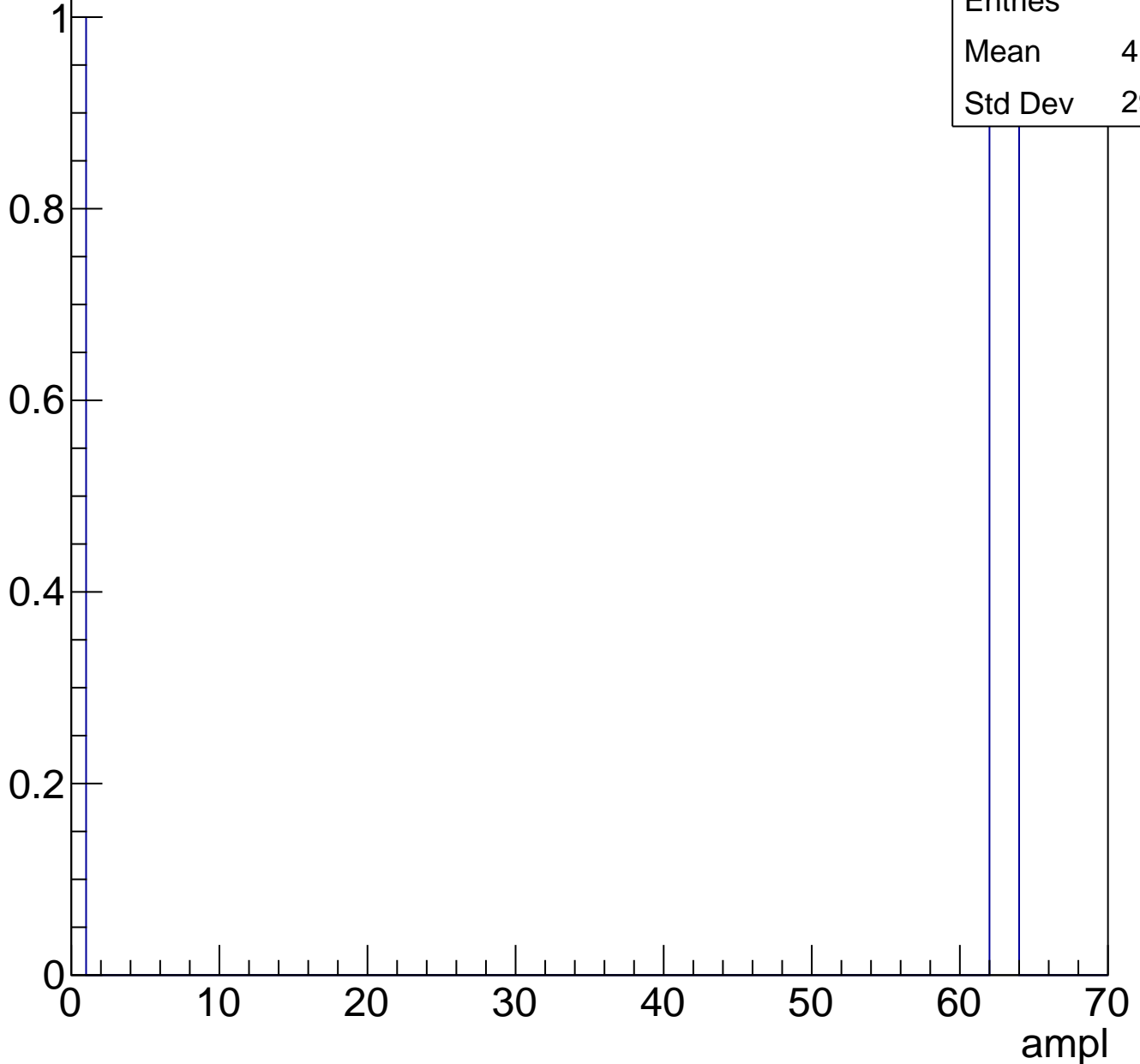
Entries	11
Mean	62.09
Std Dev	0.9959



B0L002S, U17-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch115, adc0

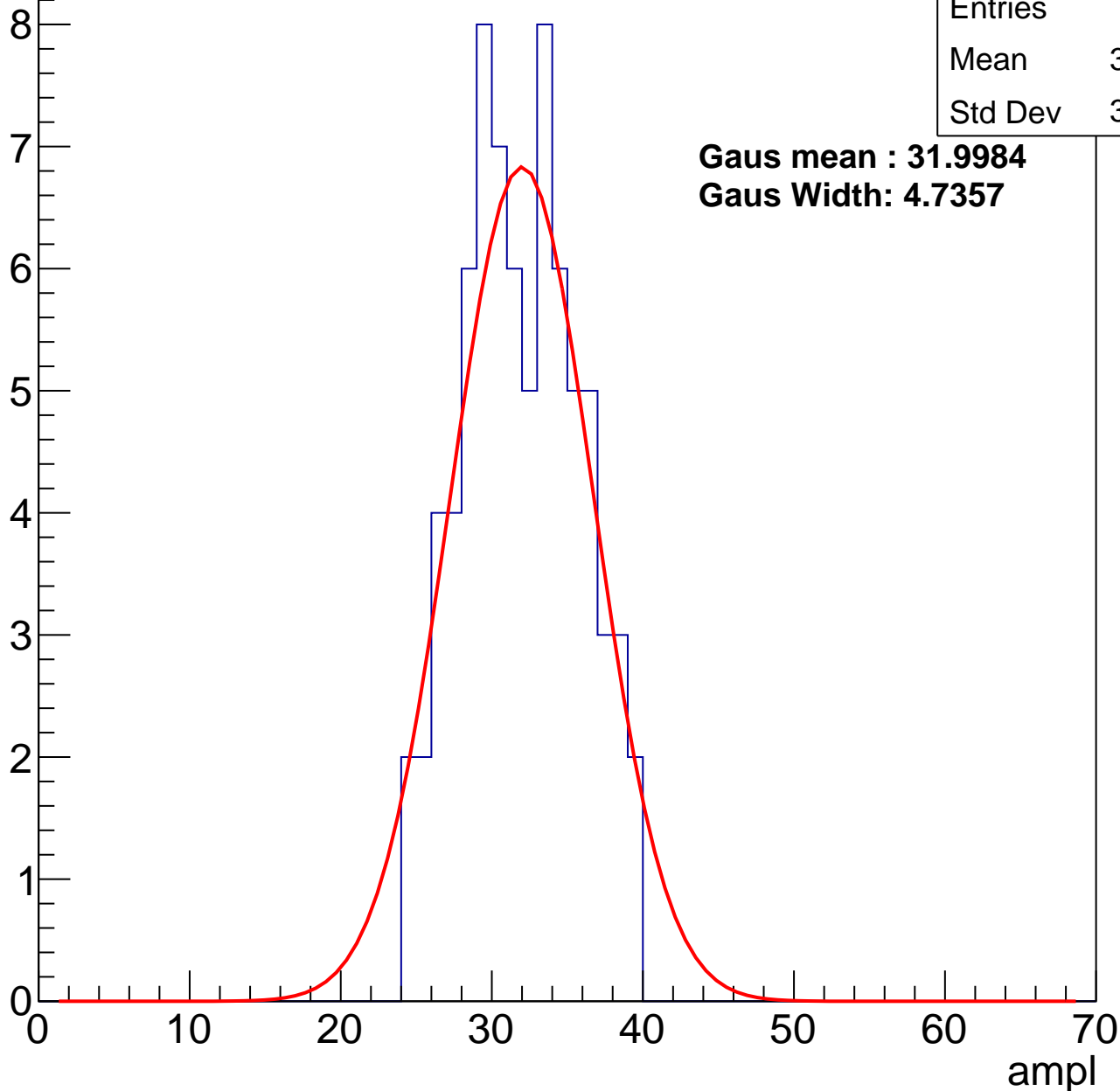
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.47
Std Dev	3.785

Gaus mean : 31.9984

Gaus Width: 4.7357



B0L002S, U17-ch115, adc1

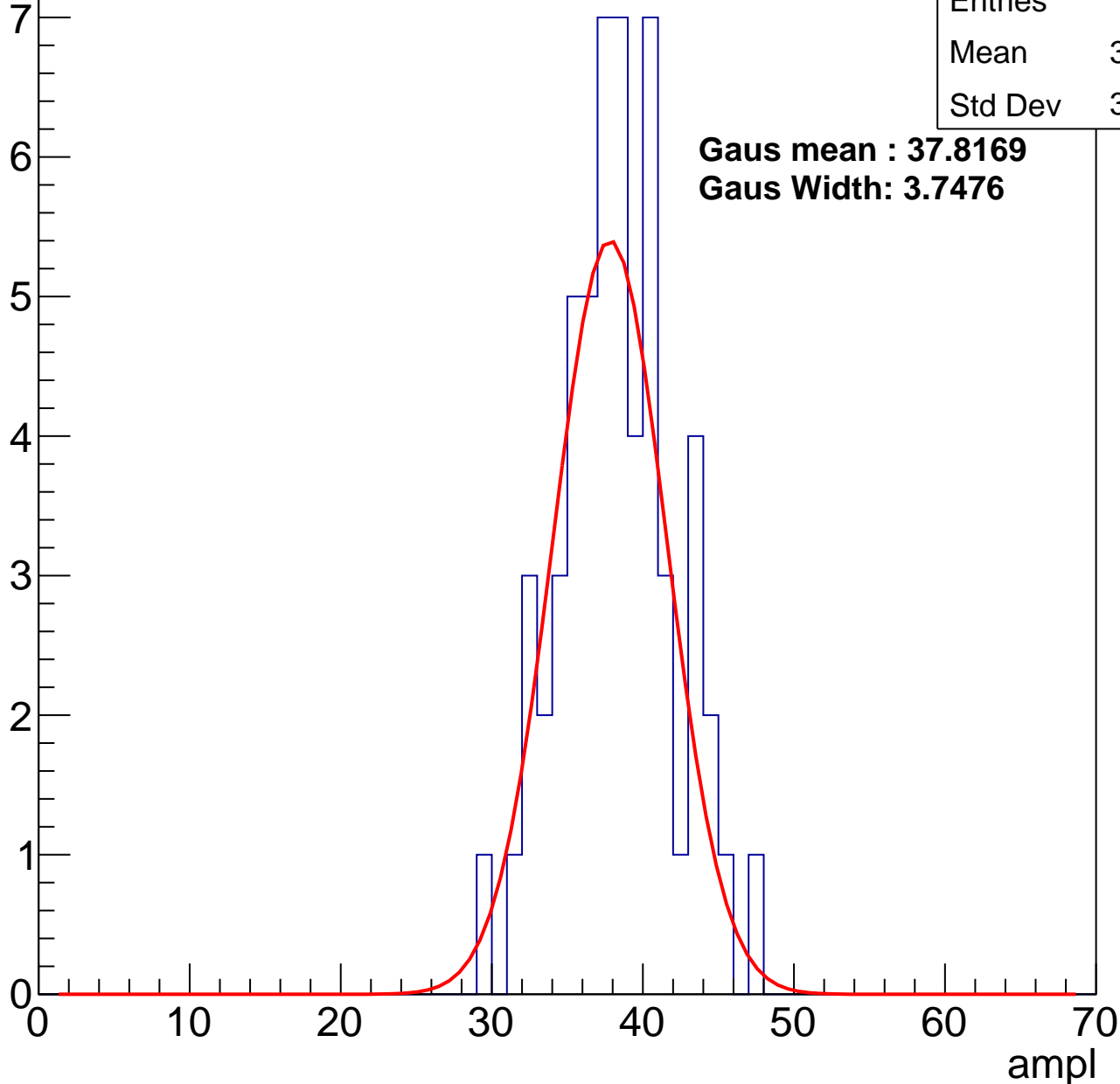
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	37.84
Std Dev	3.708

Gaus mean : 37.8169

Gaus Width: 3.7476



B0L002S, U17-ch115, adc2

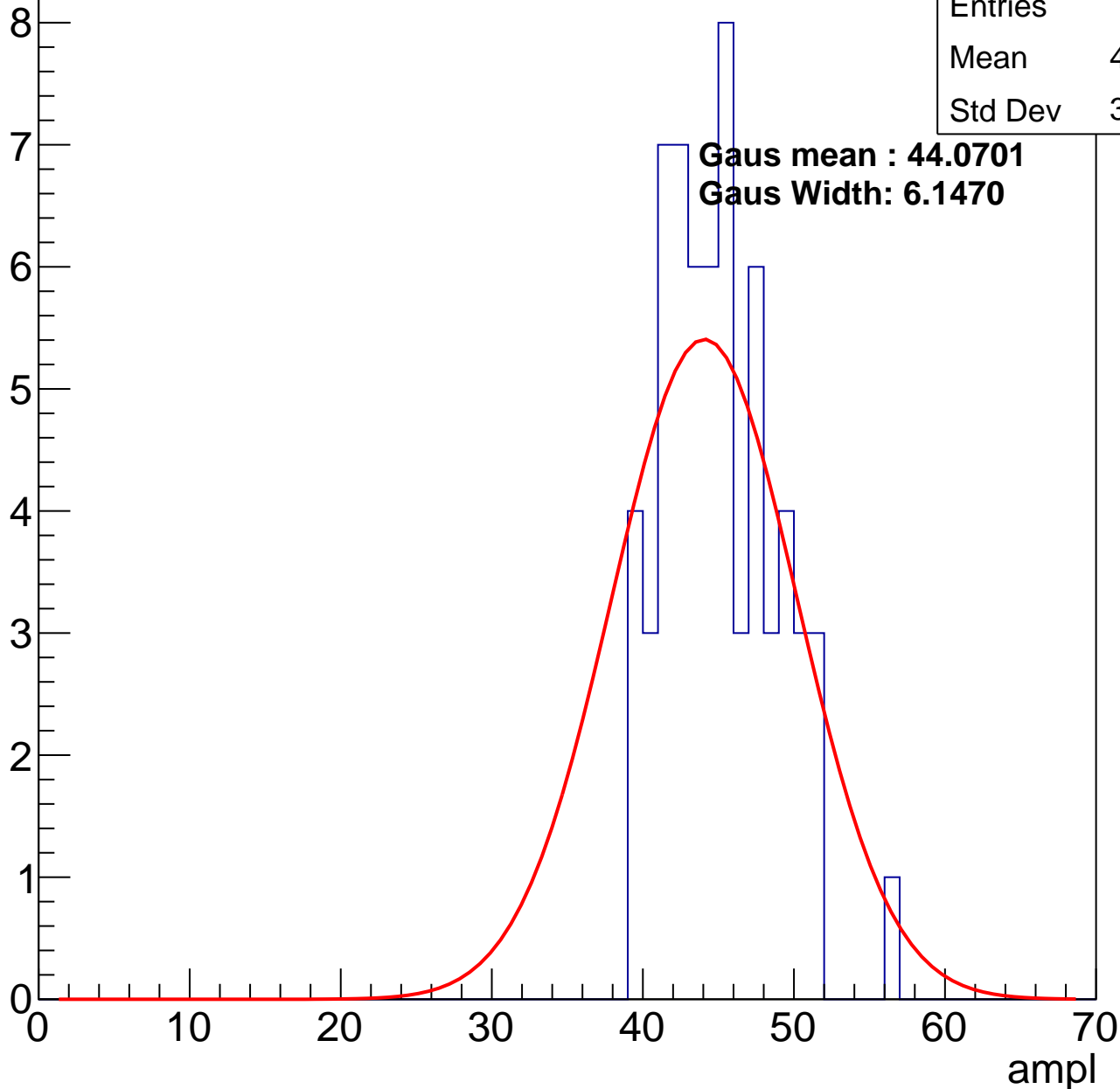
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.66
Std Dev	3.619

Gaus mean : 44.0701

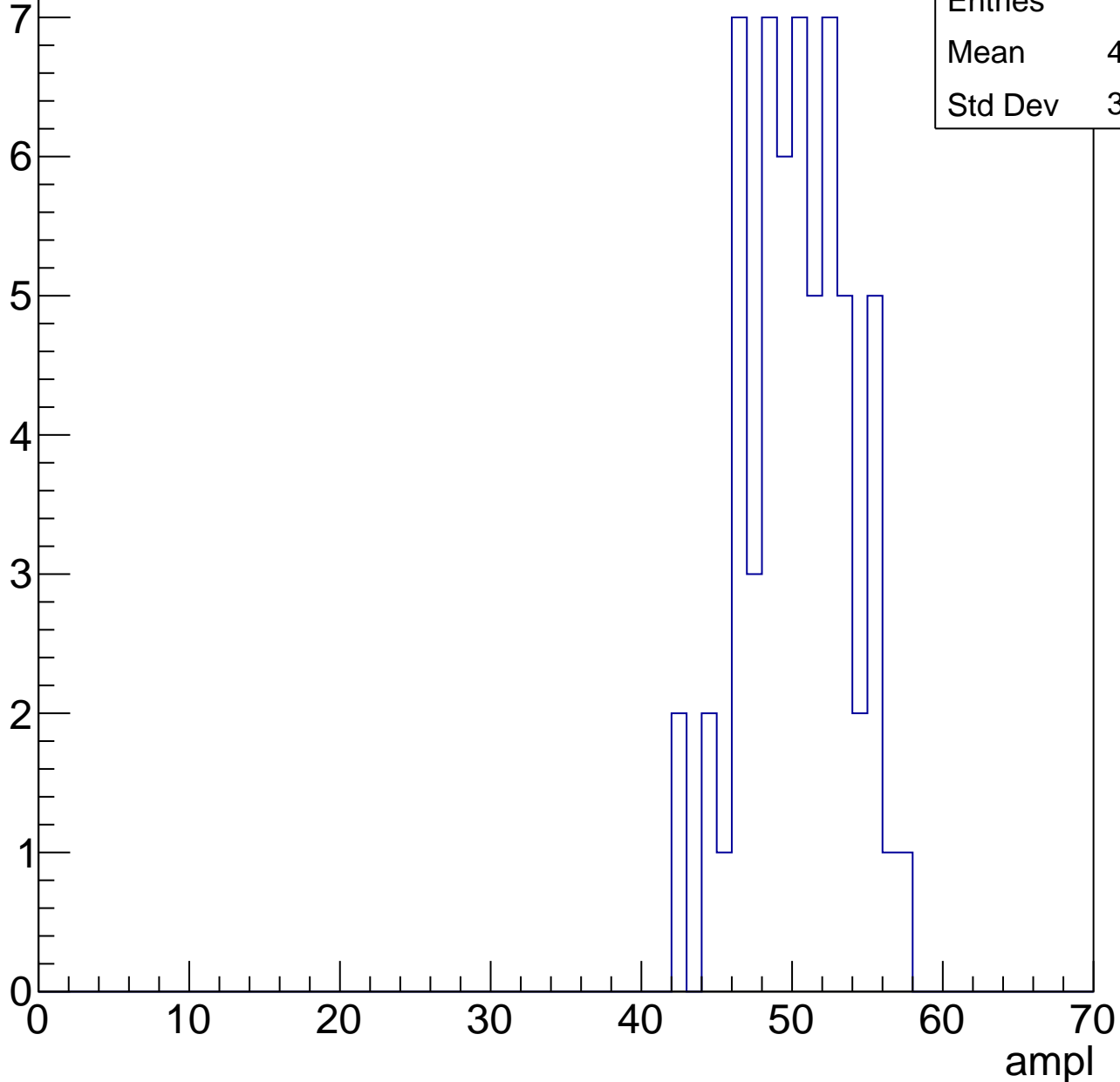
Gaus Width: 6.1470



B0L002S, U17-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



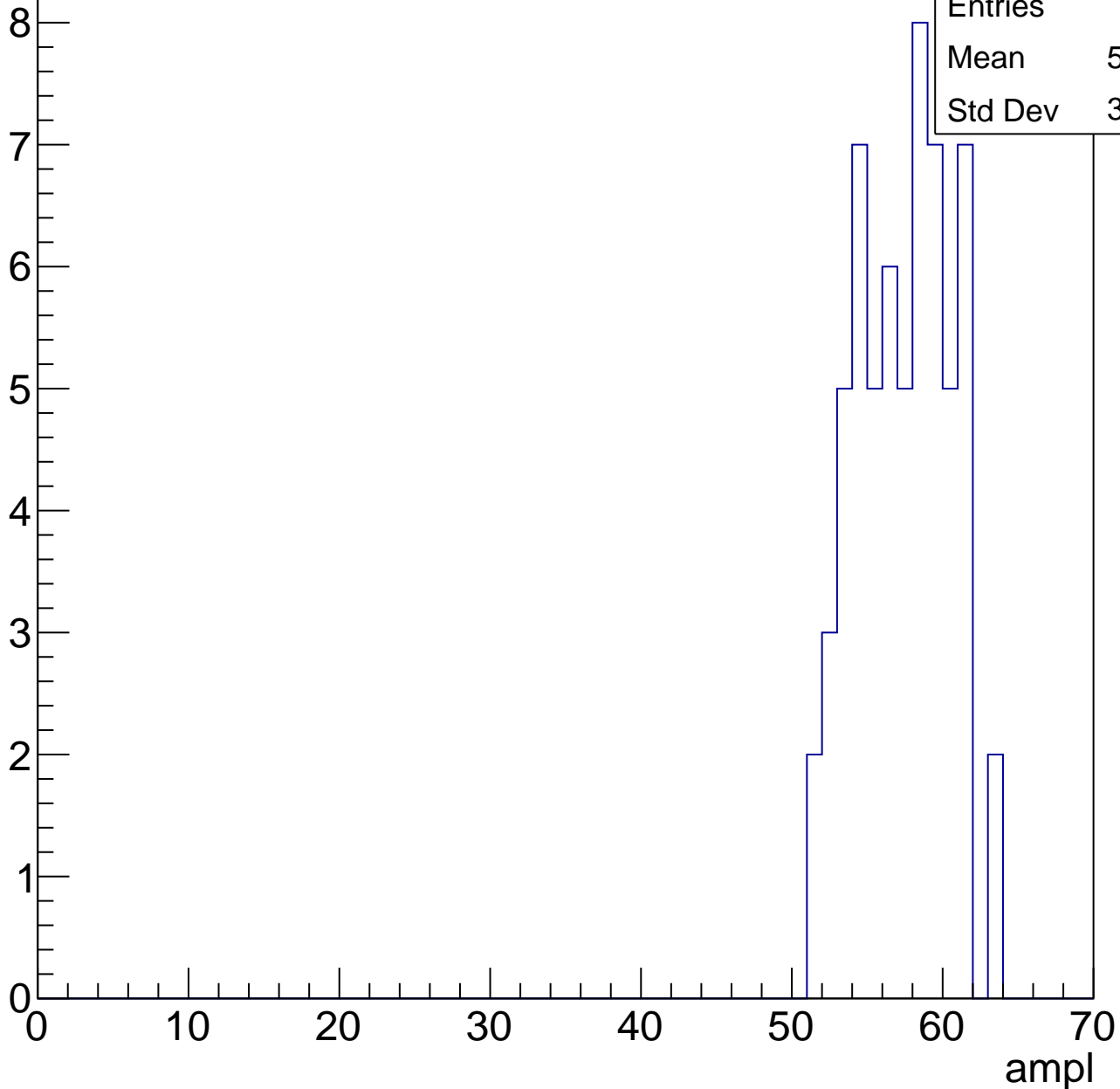
Entries	61
Mean	49.84
Std Dev	3.427

B0L002S, U17-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	56.89
Std Dev	3.059

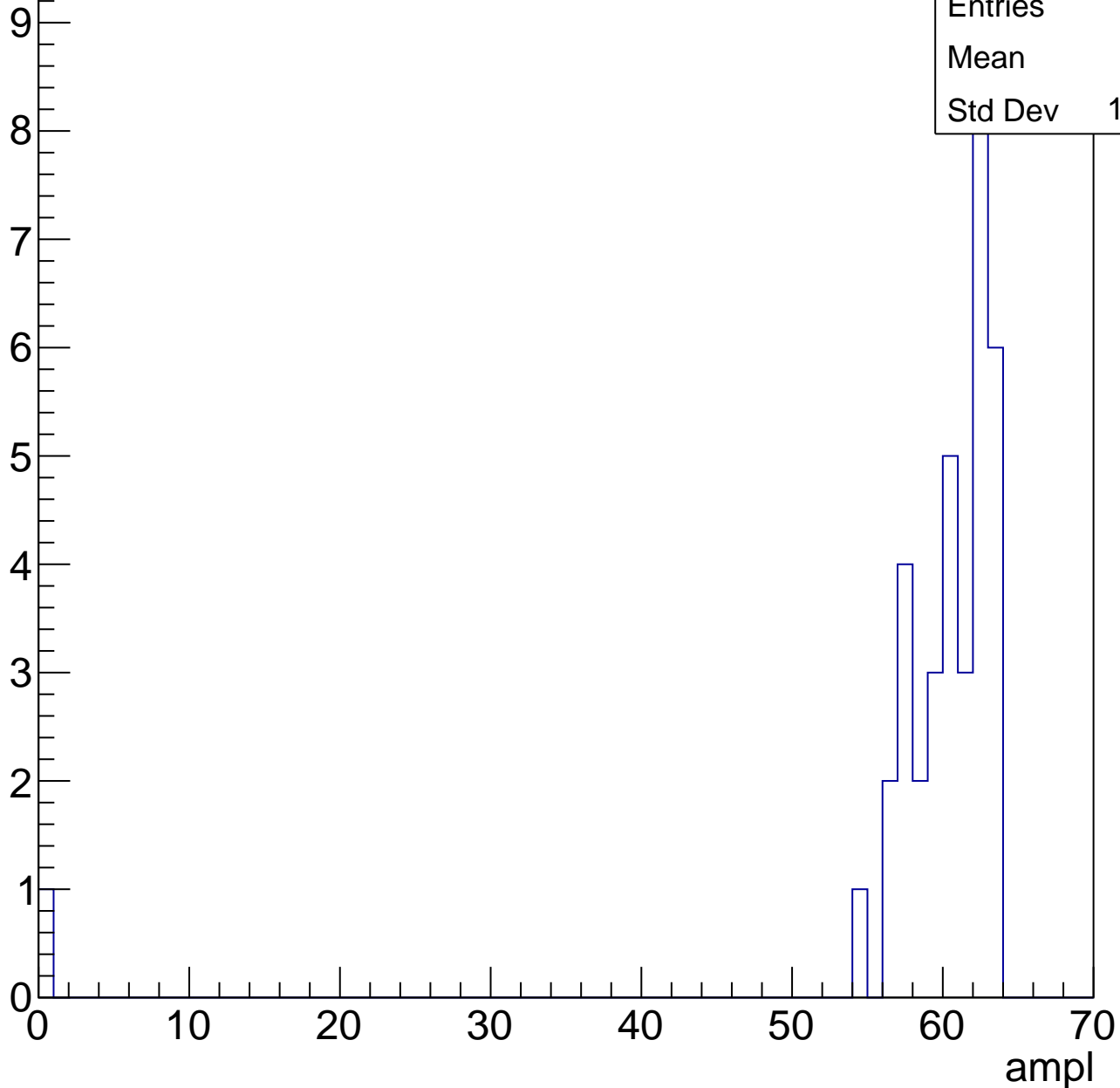


B0L002S, U17-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	58.5
Std Dev	10.17



B0L002S, U17-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

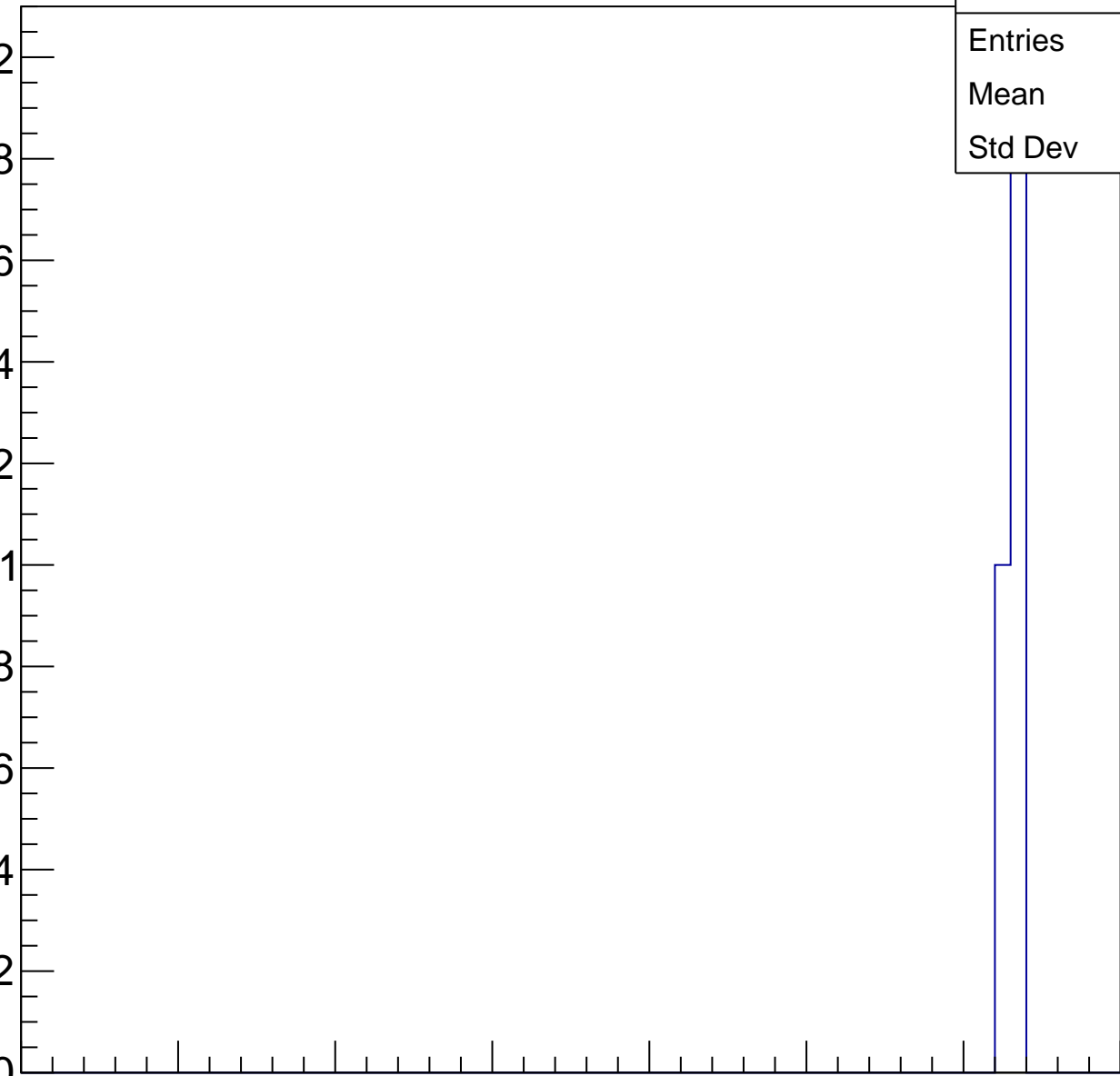
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



B0L002S, U17-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch116, adc0

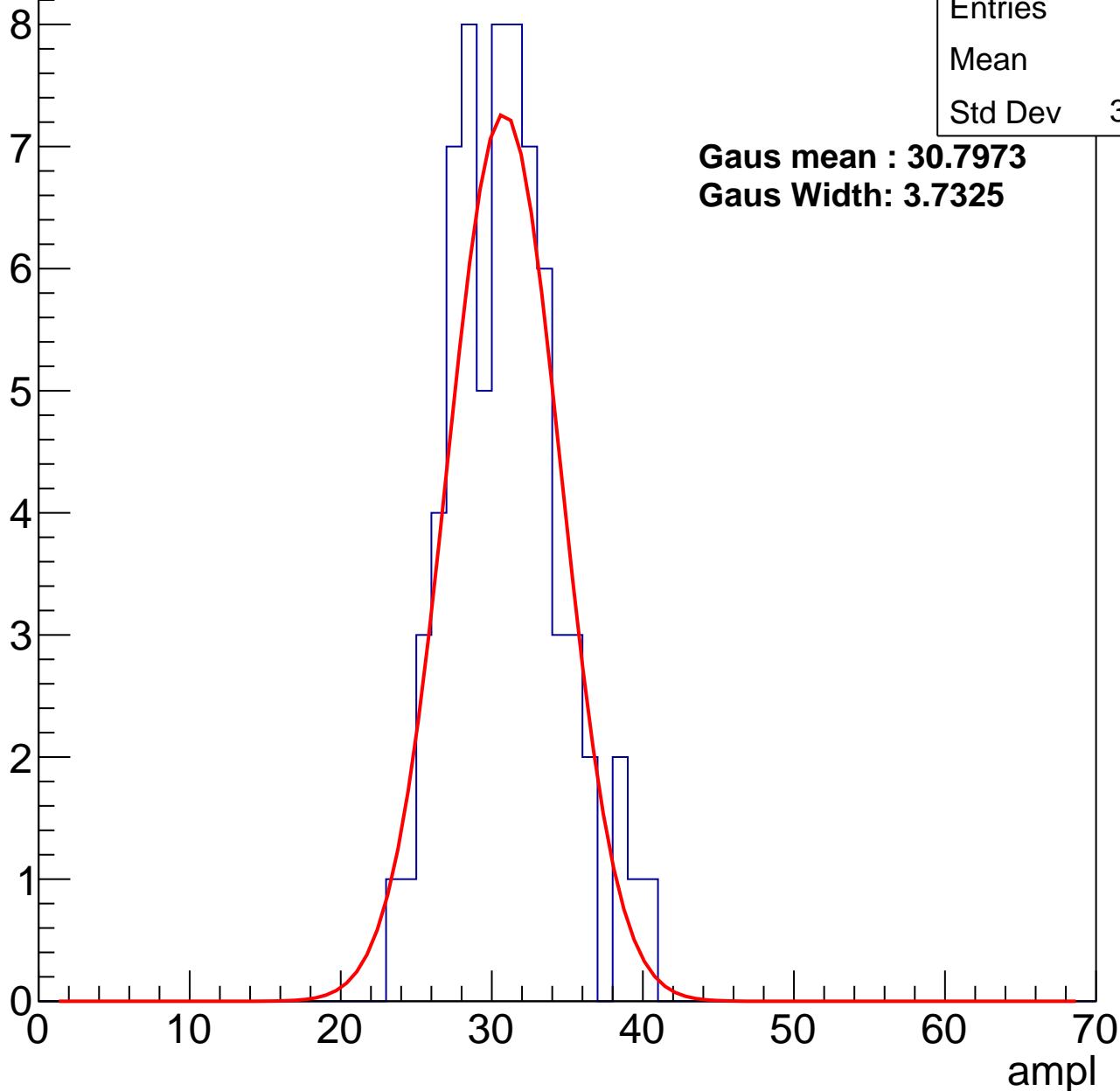
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.4
Std Dev	3.599

Gaus mean : 30.7973

Gaus Width: 3.7325



B0L002S, U17-ch116, adc1

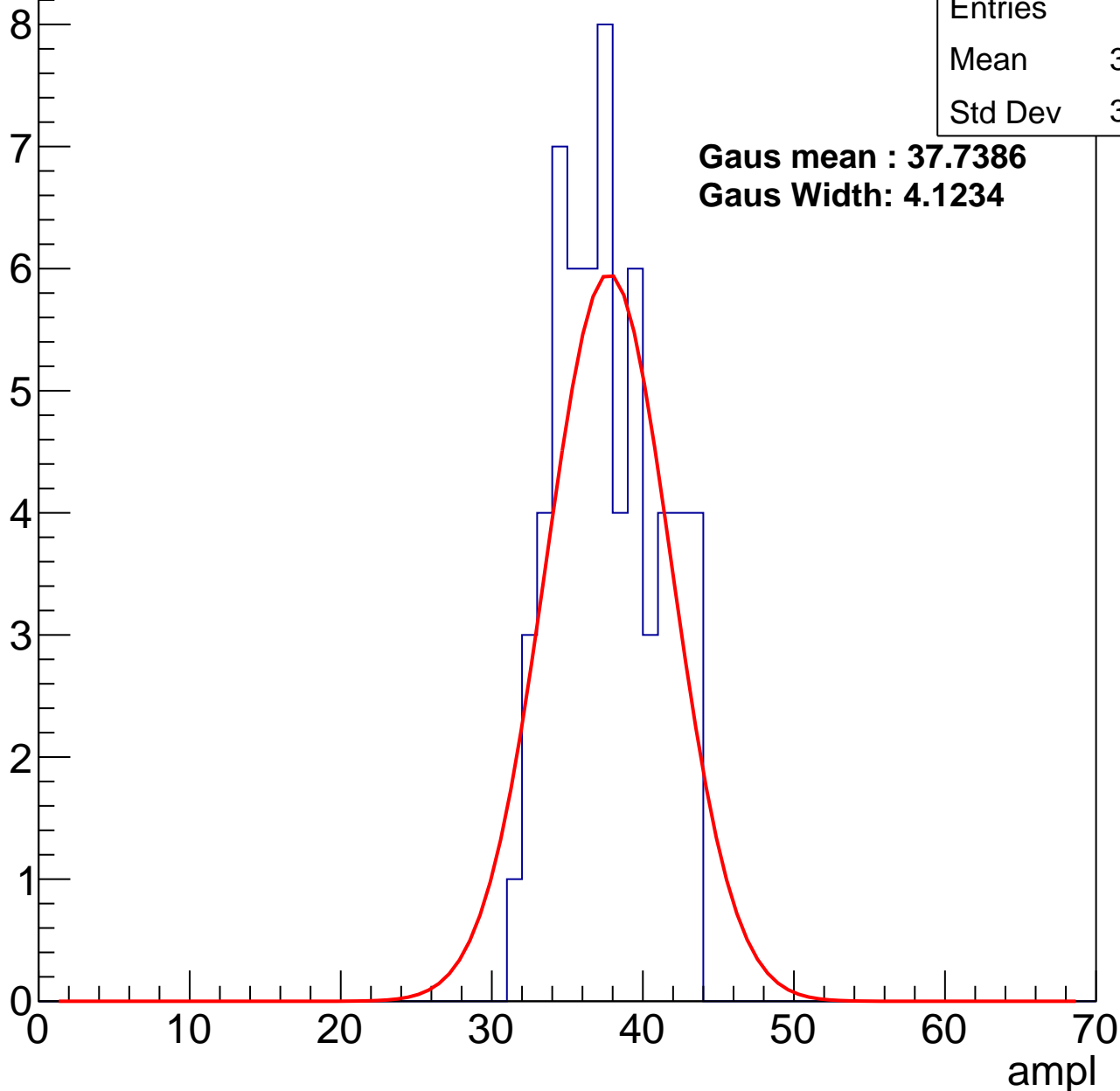
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.15
Std Dev	3.239

Gaus mean : 37.7386

Gaus Width: 4.1234



B0L002S, U17-ch116, adc2

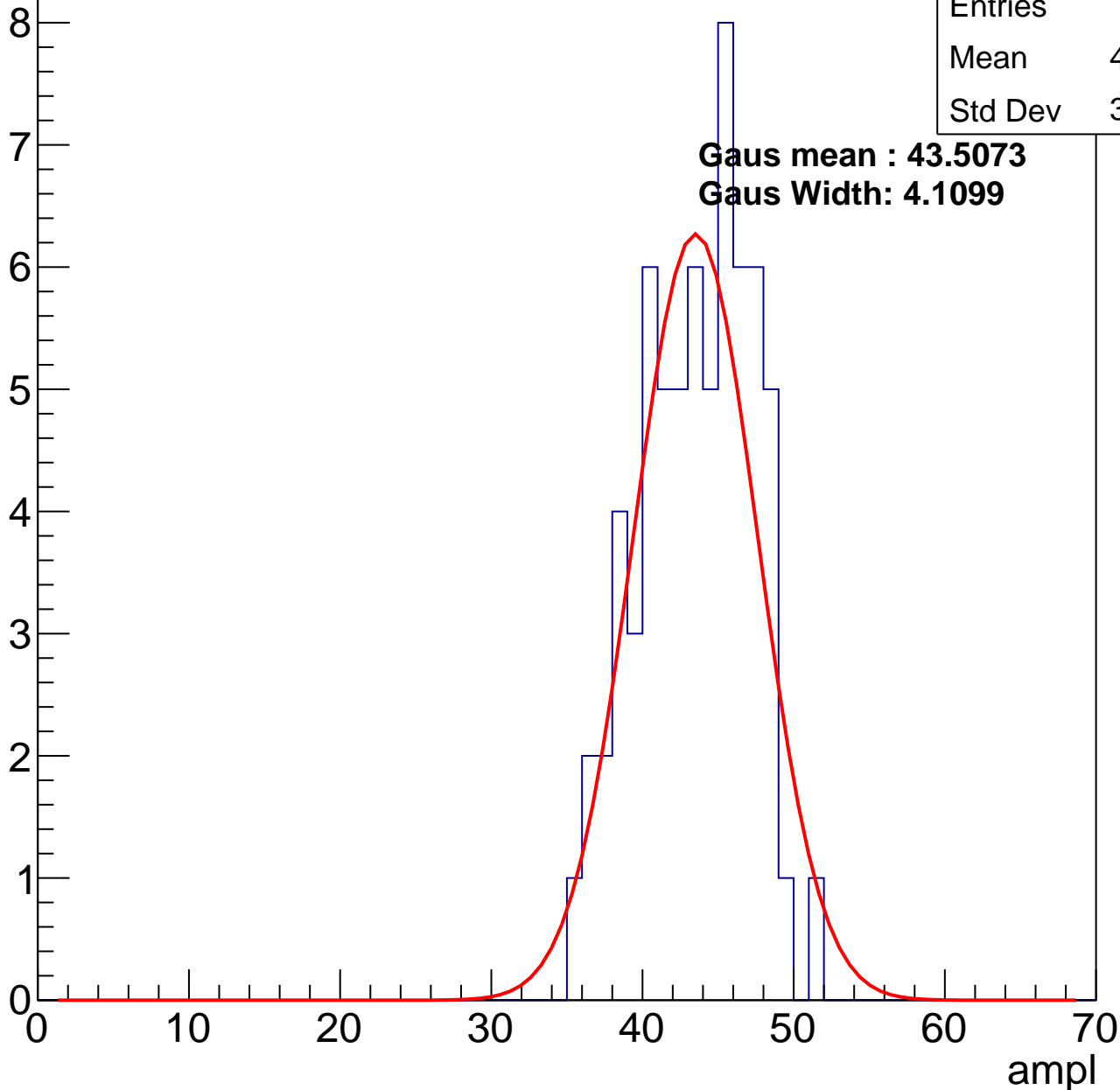
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.05
Std Dev	3.637

Gaus mean : 43.5073

Gaus Width: 4.1099

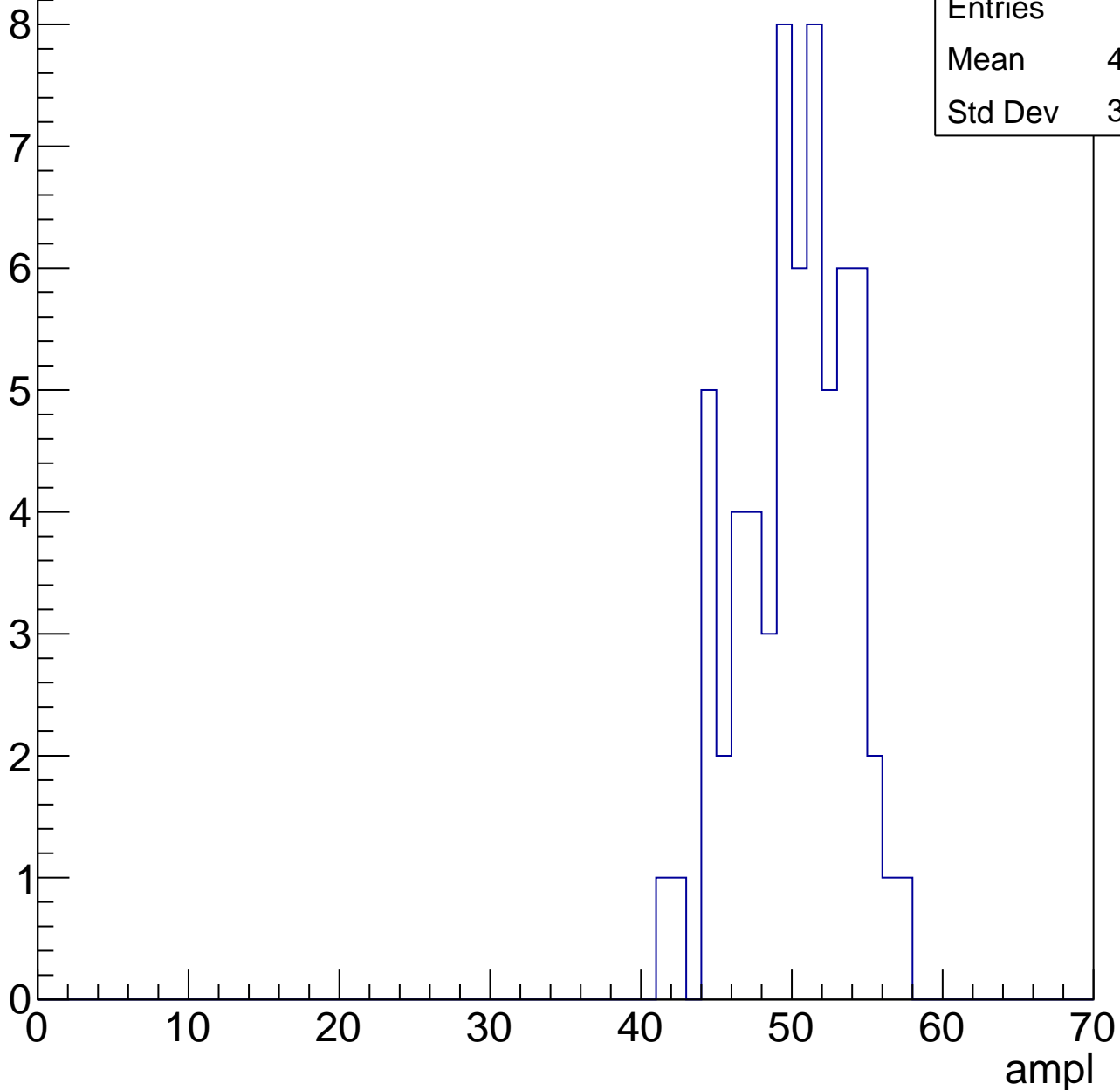


B0L002S, U17-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	49.75
Std Dev	3.572

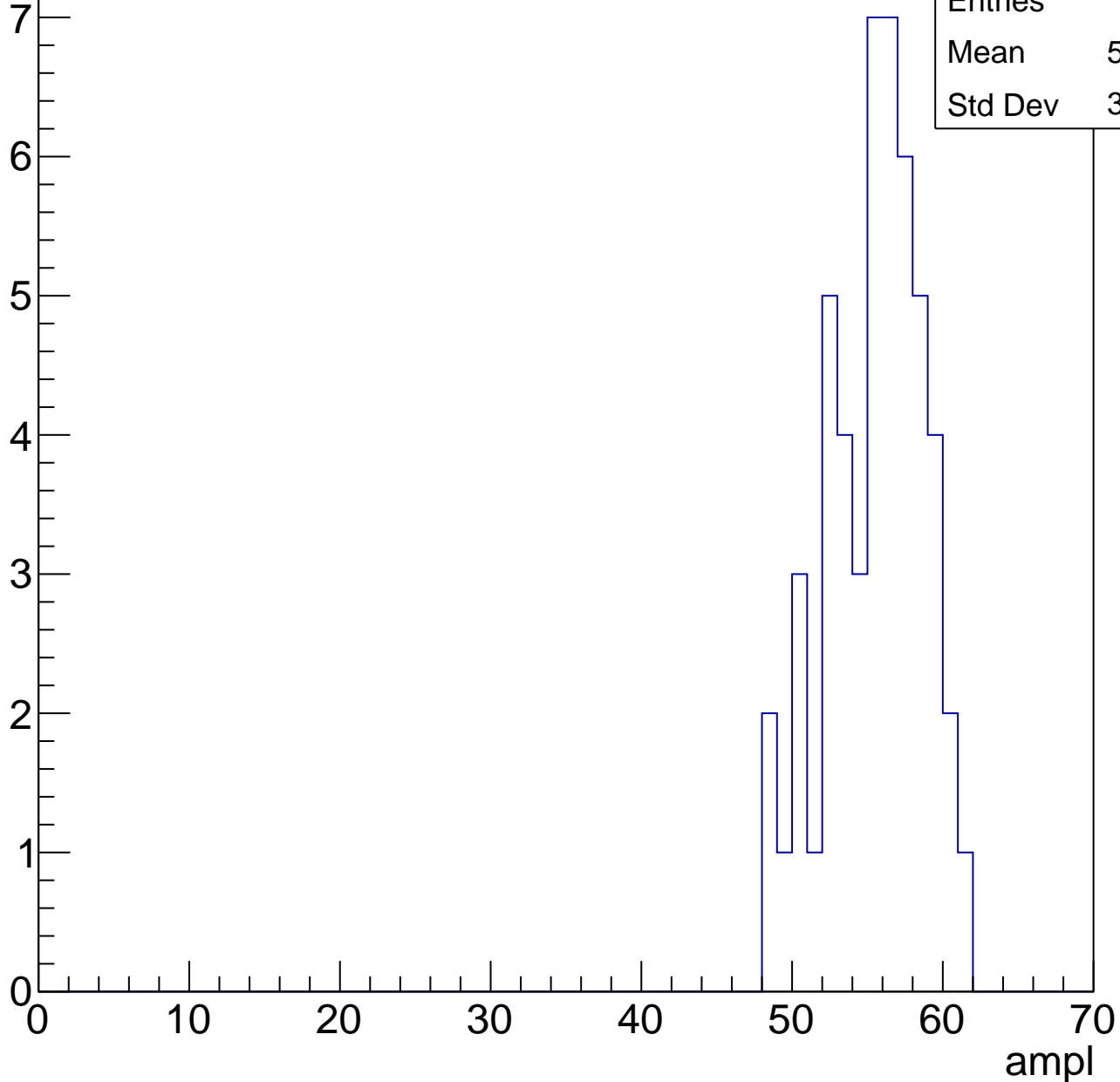


B0L002S, U17-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.02
Std Dev	3.178

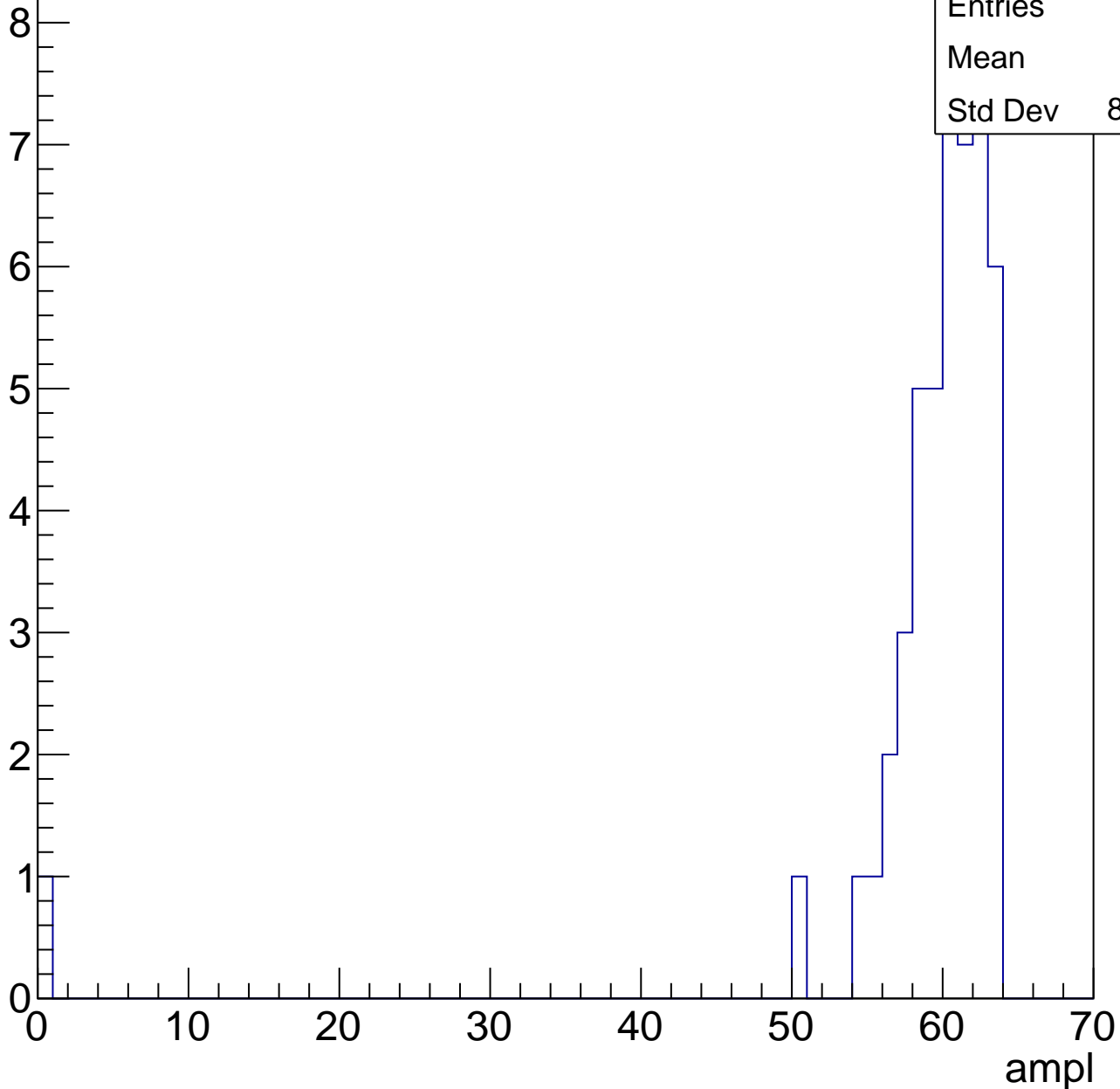


B0L002S, U17-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

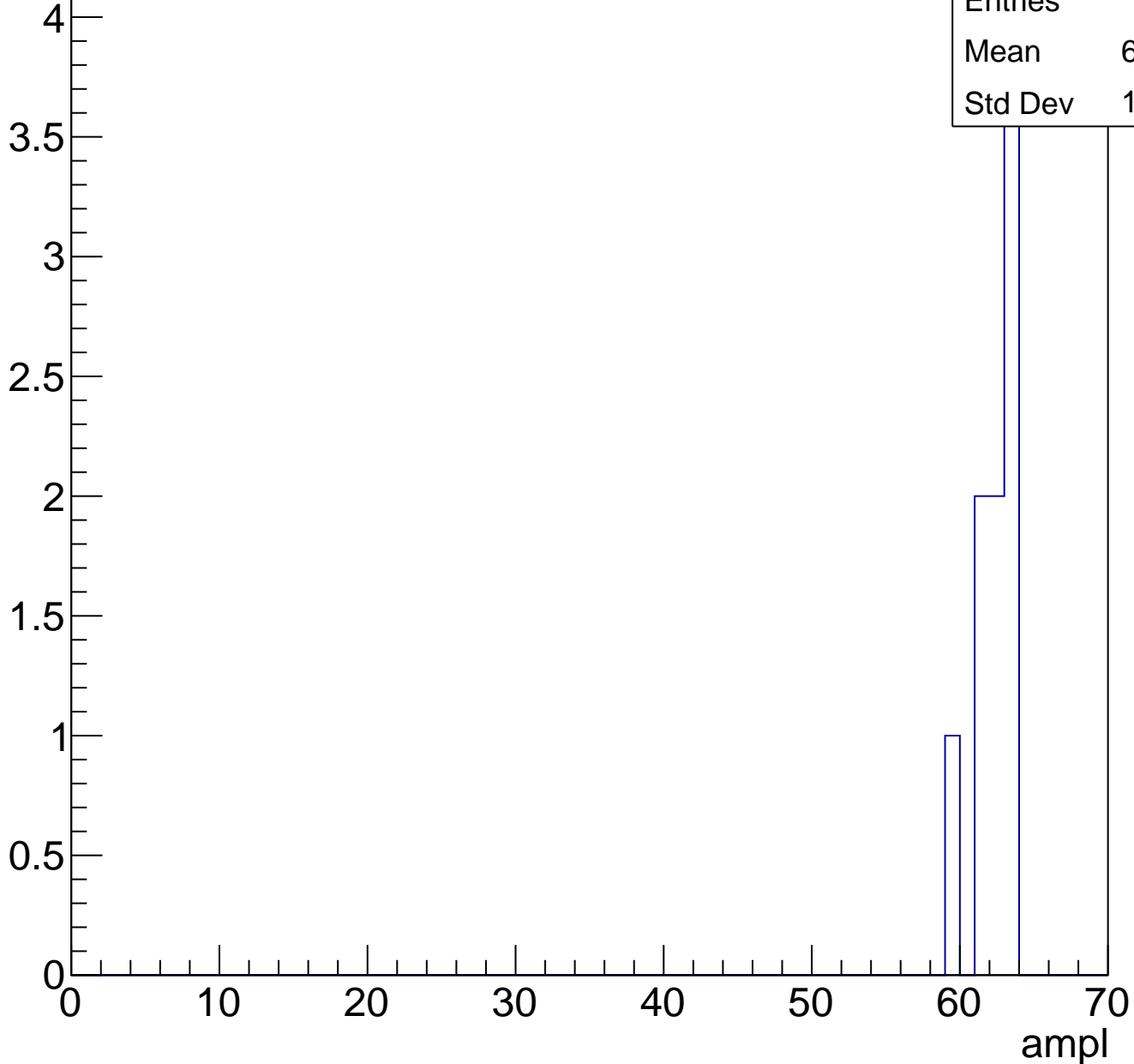
Entries	48
Mean	58.5
Std Dev	8.933



B0L002S, U17-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch116, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch117, adc0

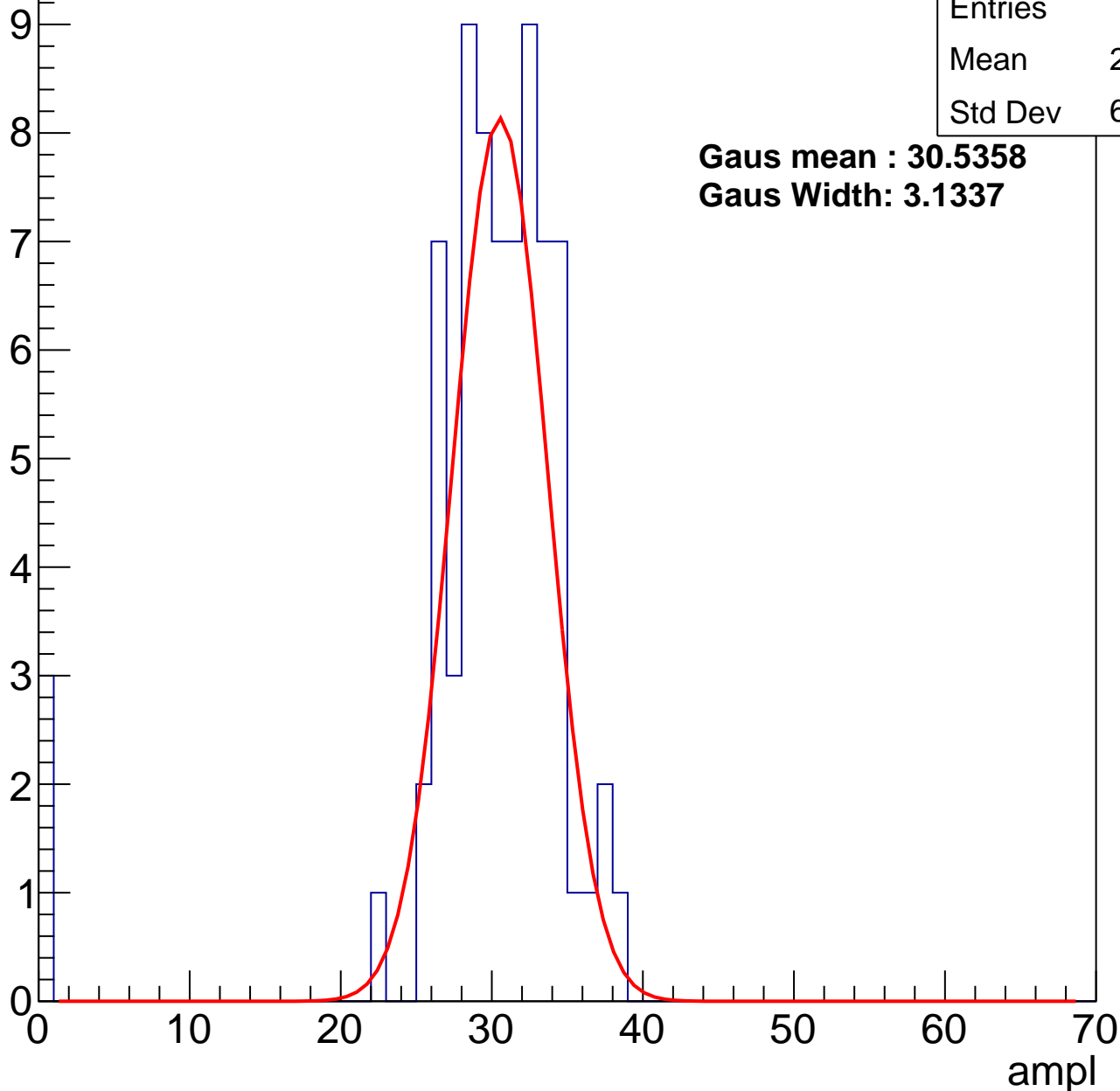
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	29.15
Std Dev	6.717

Gaus mean : 30.5358

Gaus Width: 3.1337



B0L002S, U17-ch117, adc1

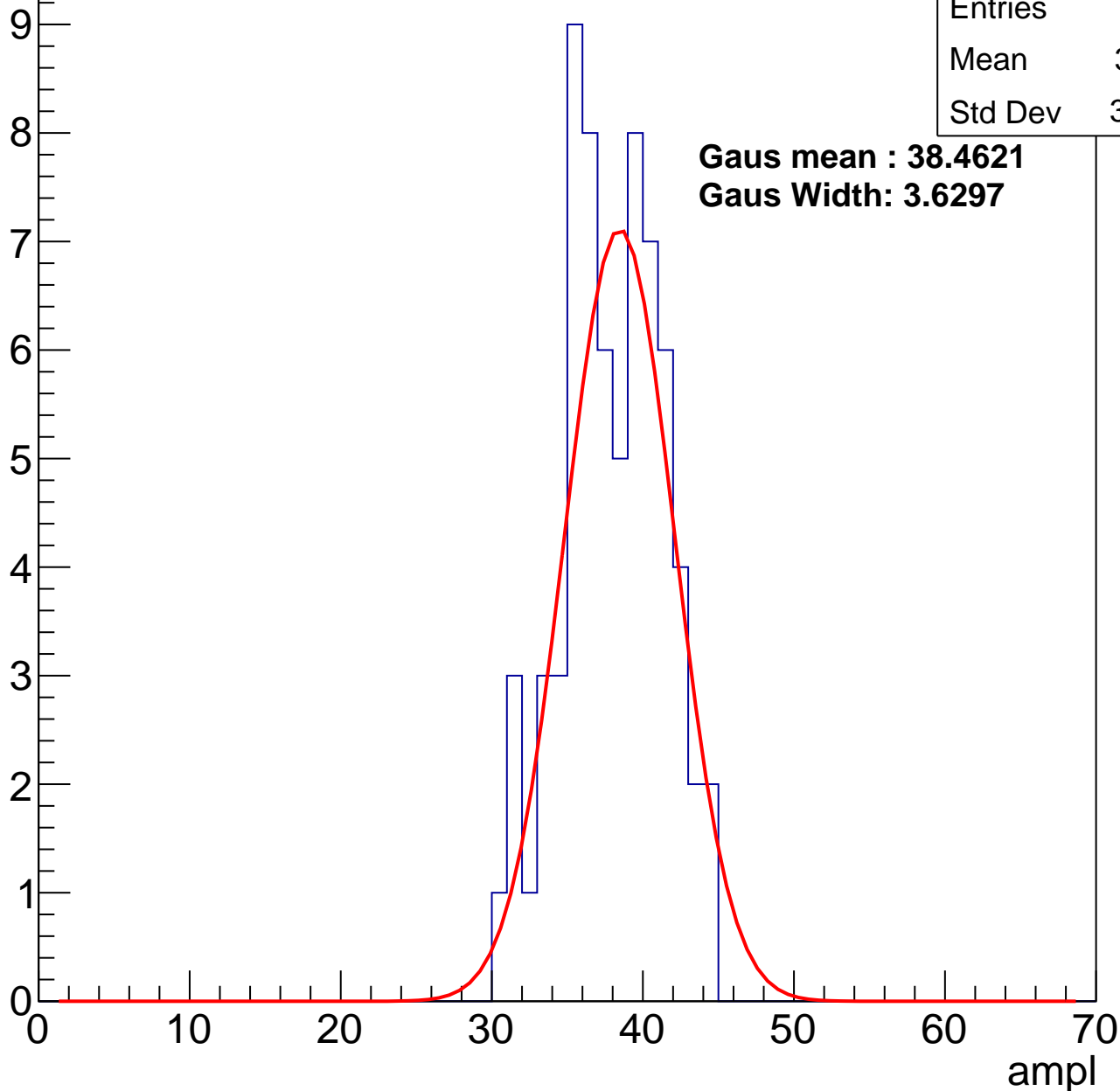
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.51
Std Dev	3.319

Gaus mean : 38.4621

Gaus Width: 3.6297



B0L002S, U17-ch117, adc2

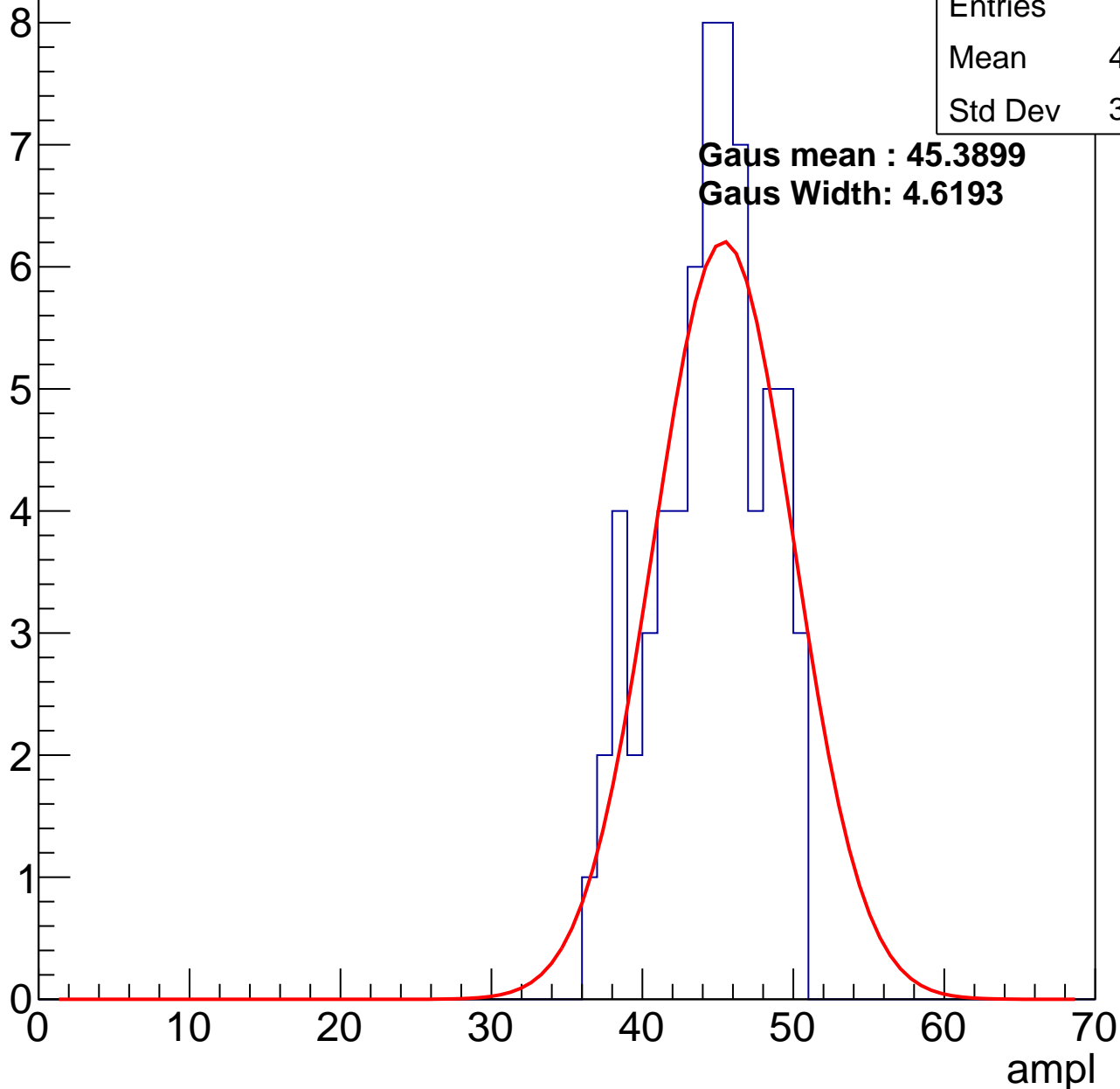
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.05
Std Dev	3.582

Gaus mean : 45.3899

Gaus Width: 4.6193

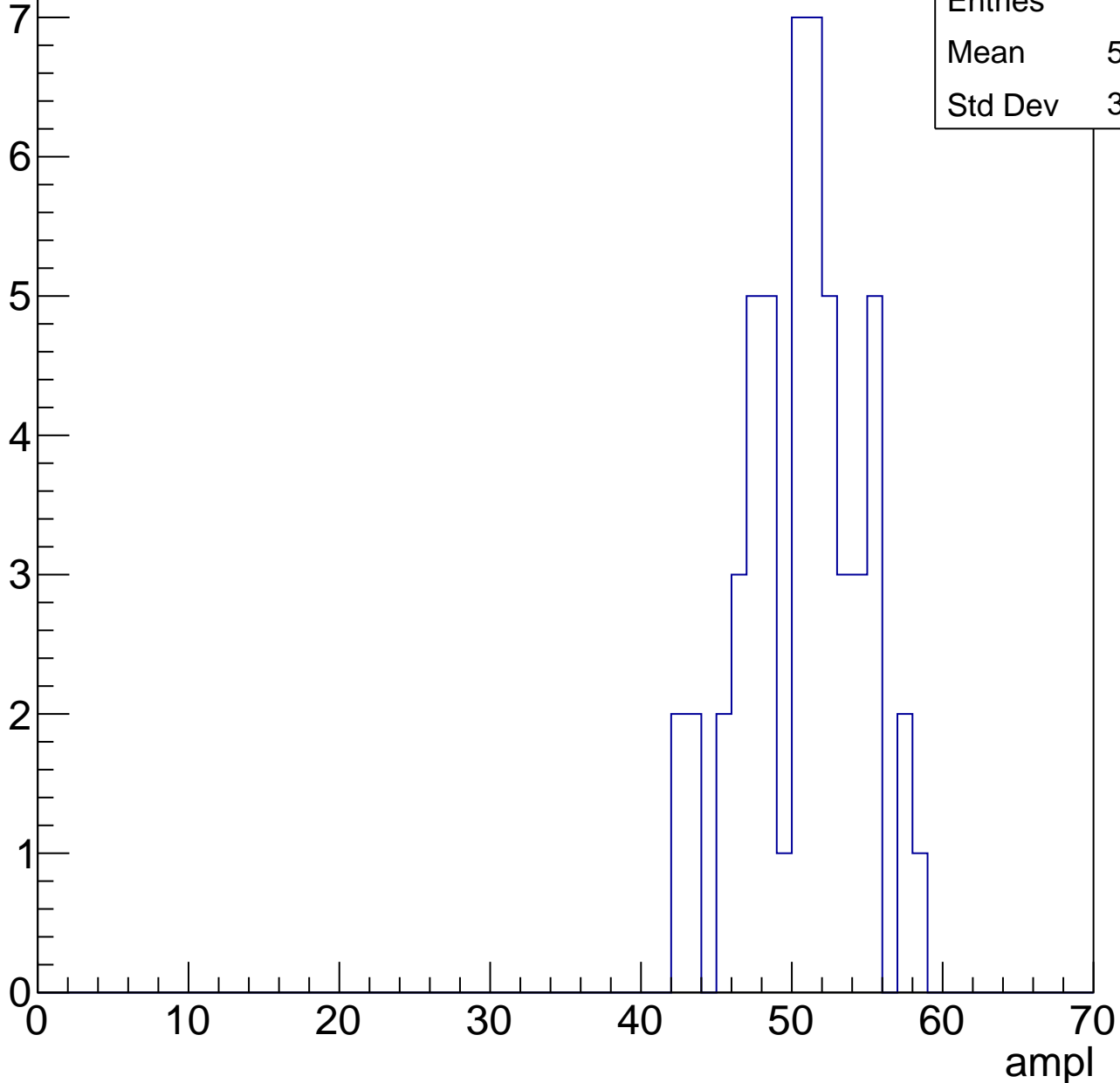


B0L002S, U17-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	50.13
Std Dev	3.832

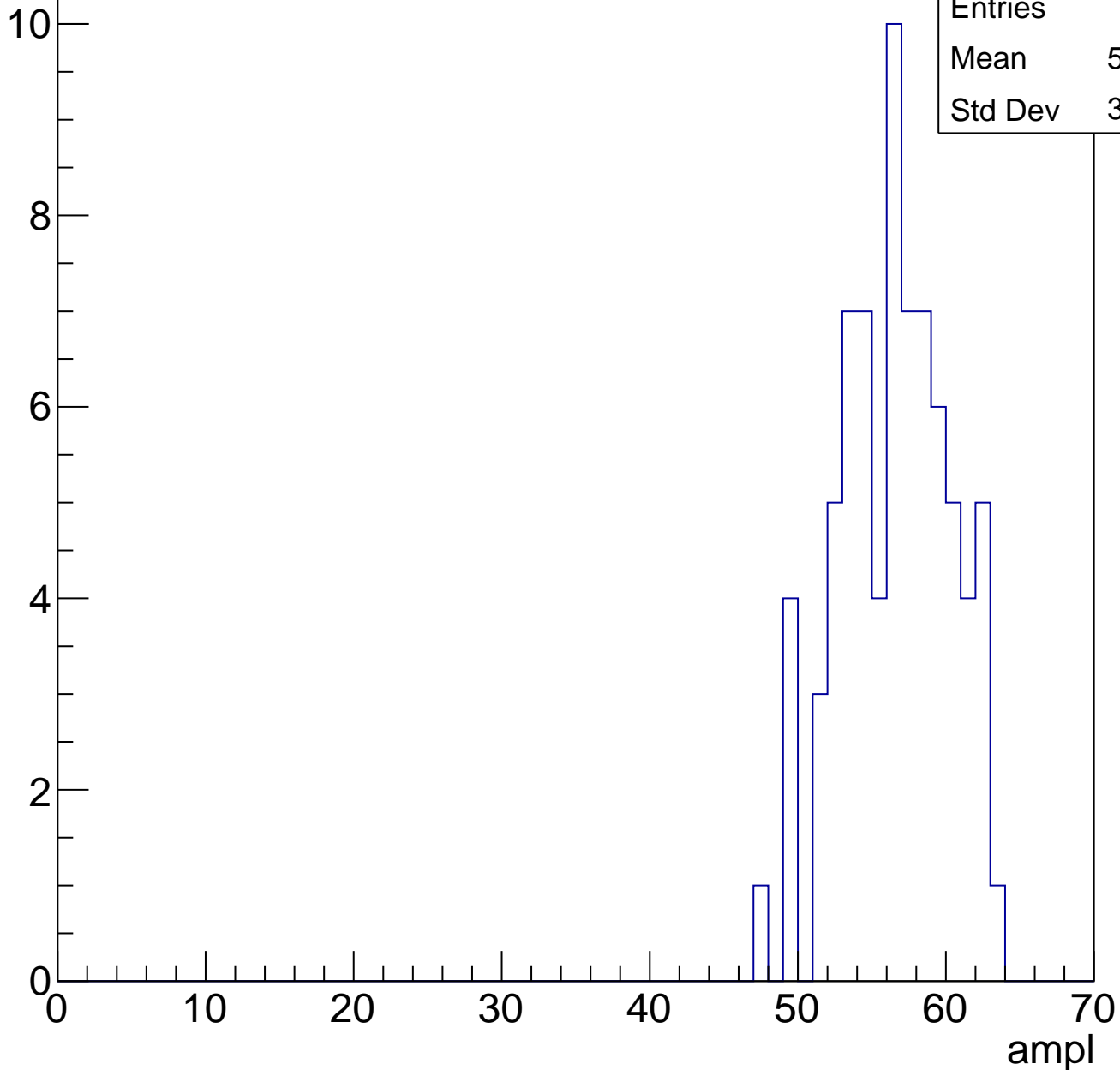


B0L002S, U17-ch117, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	56.07
Std Dev	3.672

Entry

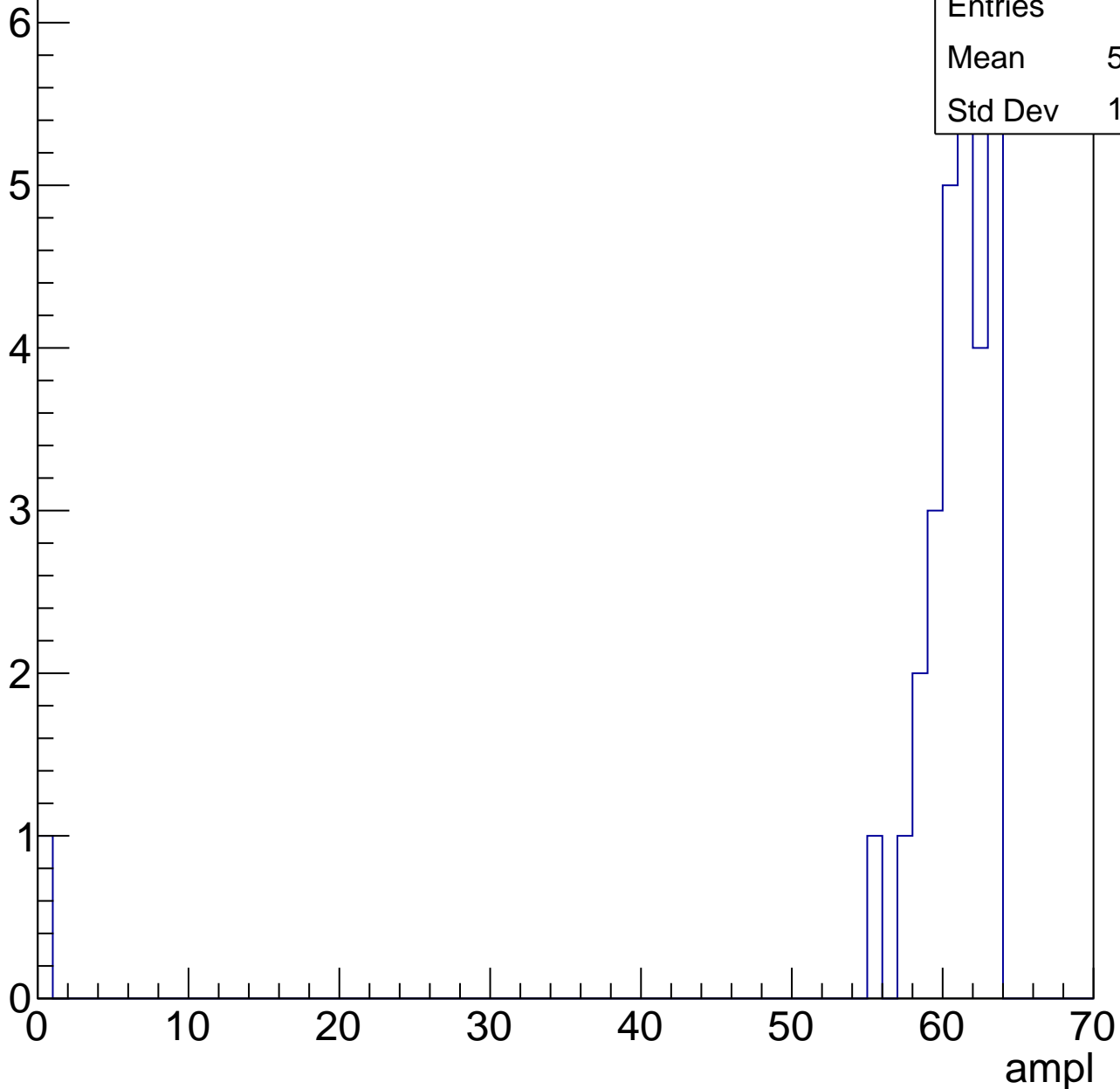


B0L002S, U17-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.52
Std Dev	11.23



B0L002S, U17-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch118, adc0

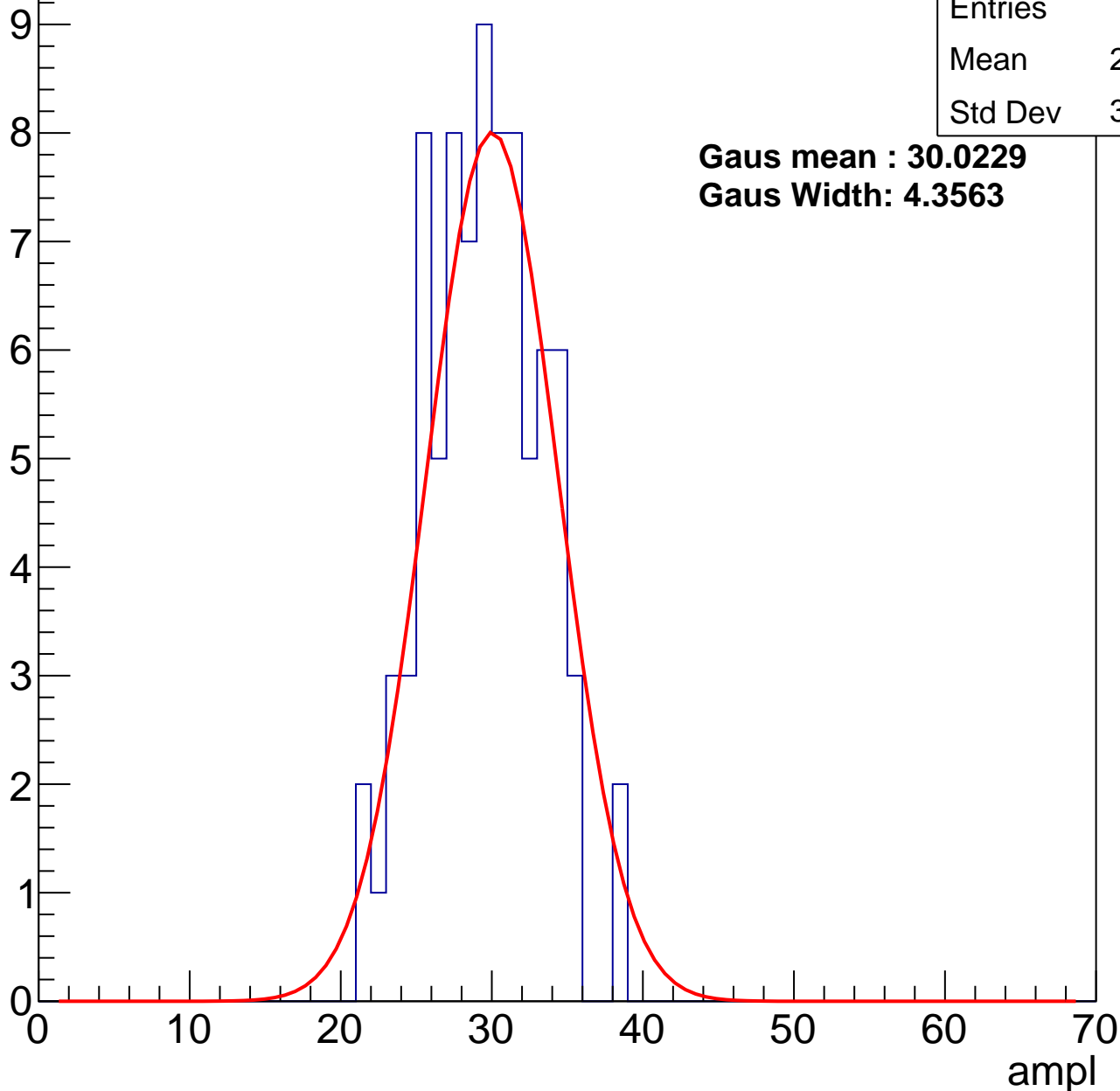
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	29.04
Std Dev	3.737

Gaus mean : 30.0229

Gaus Width: 4.3563



B0L002S, U17-ch118, adc1

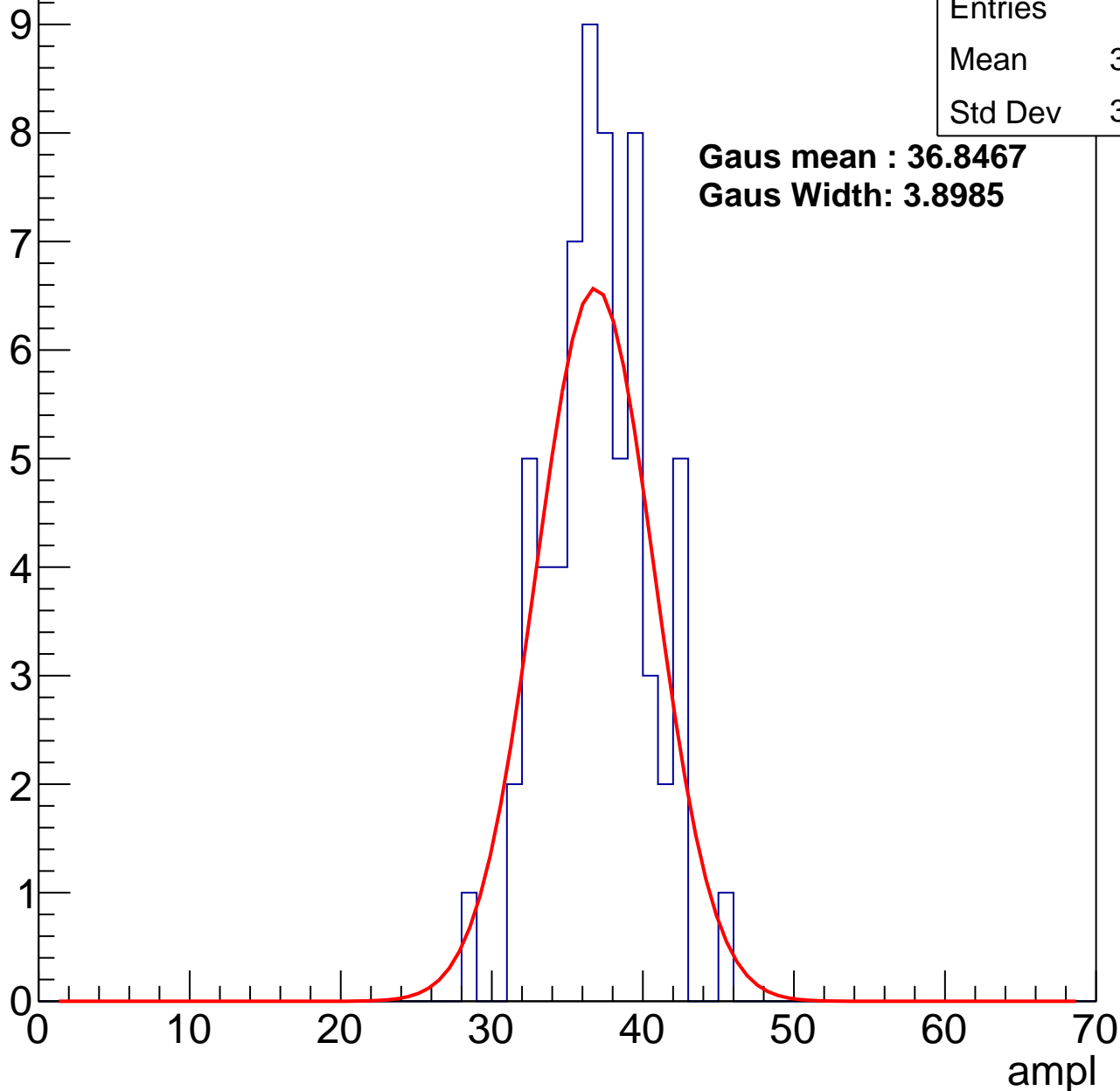
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.59
Std Dev	3.282

Gaus mean : 36.8467

Gaus Width: 3.8985



B0L002S, U17-ch118, adc2

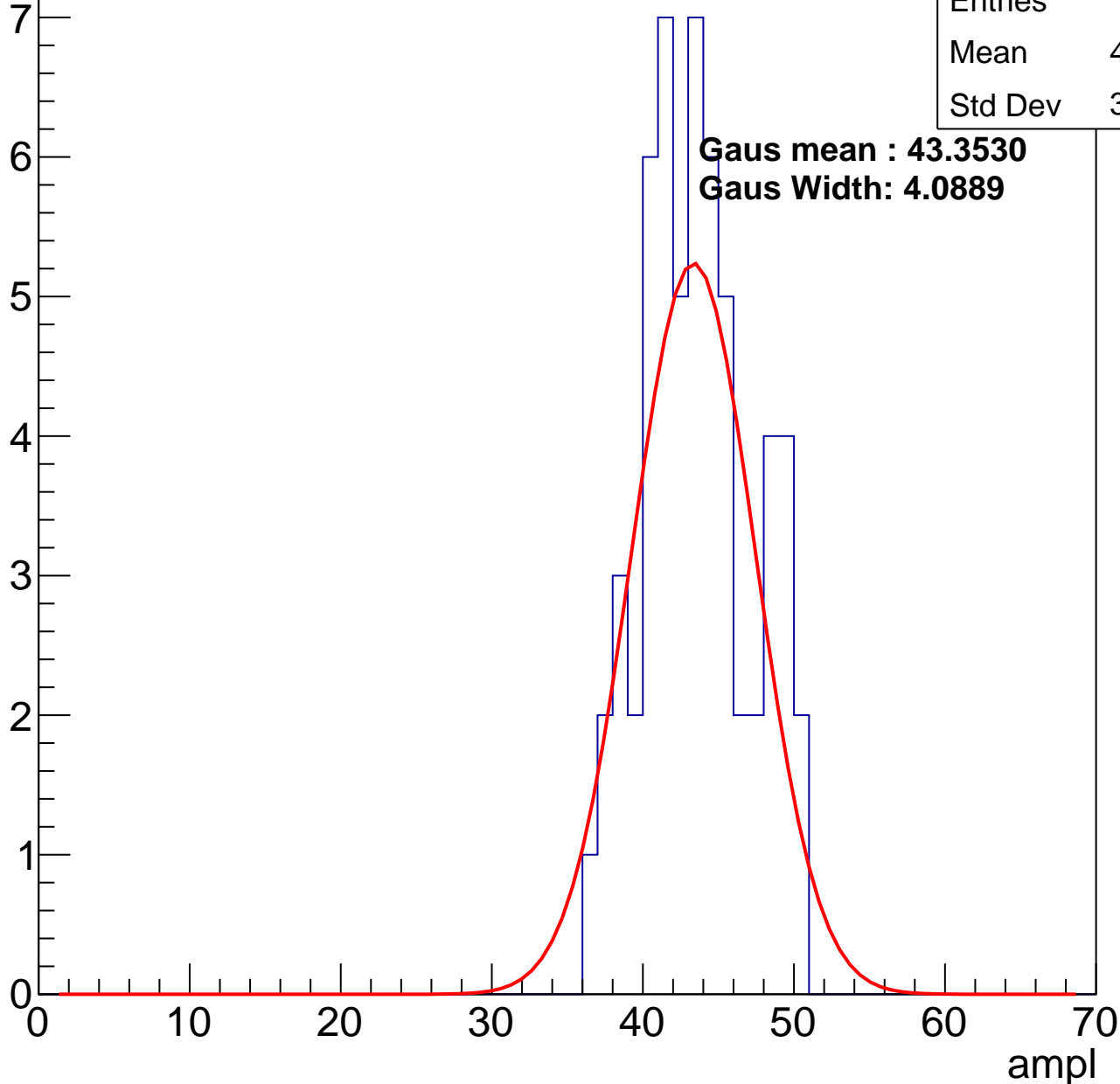
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.16
Std Dev	3.552

Gaus mean : 43.3530

Gaus Width: 4.0889

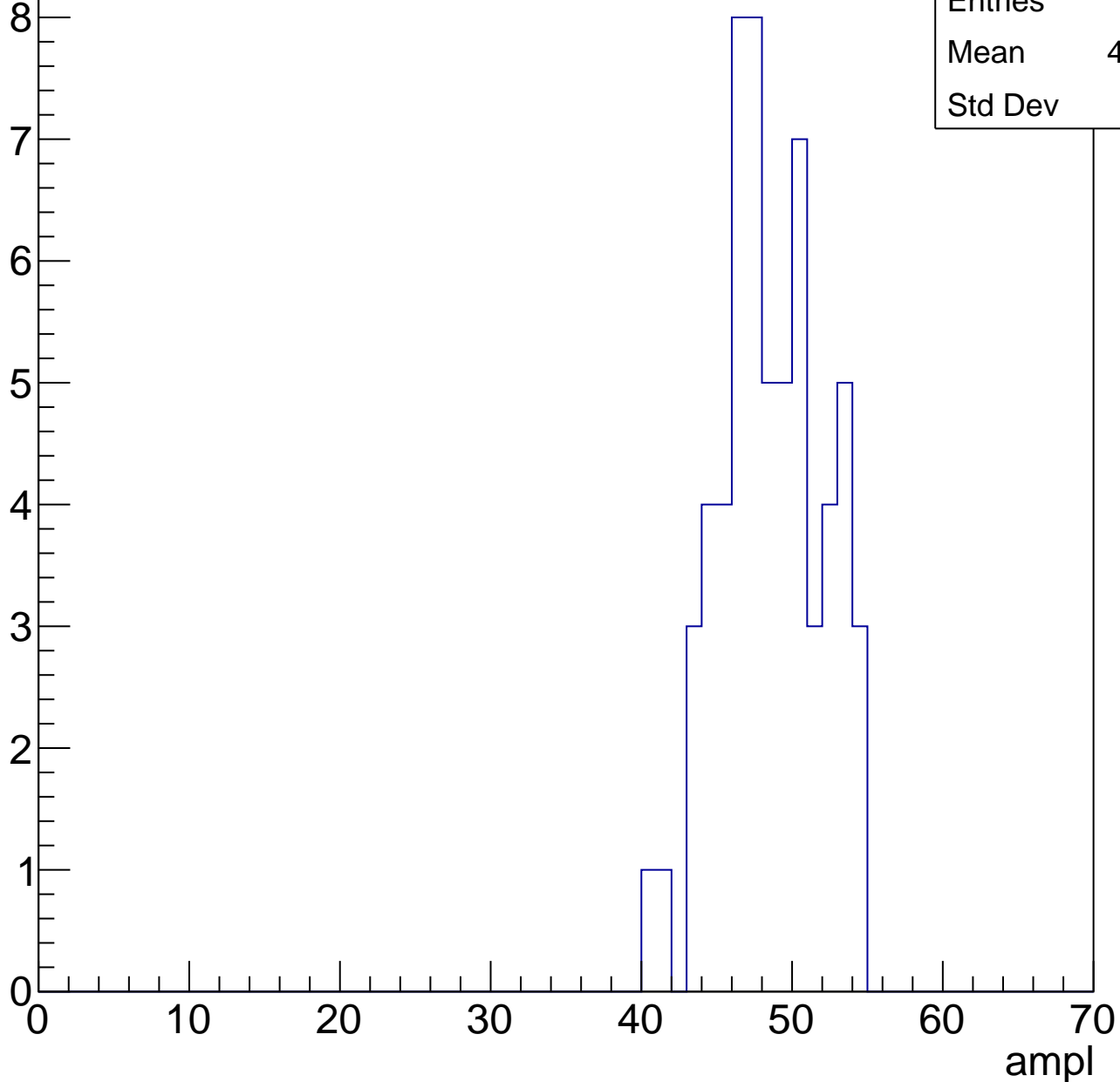


B0L002S, U17-ch118, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

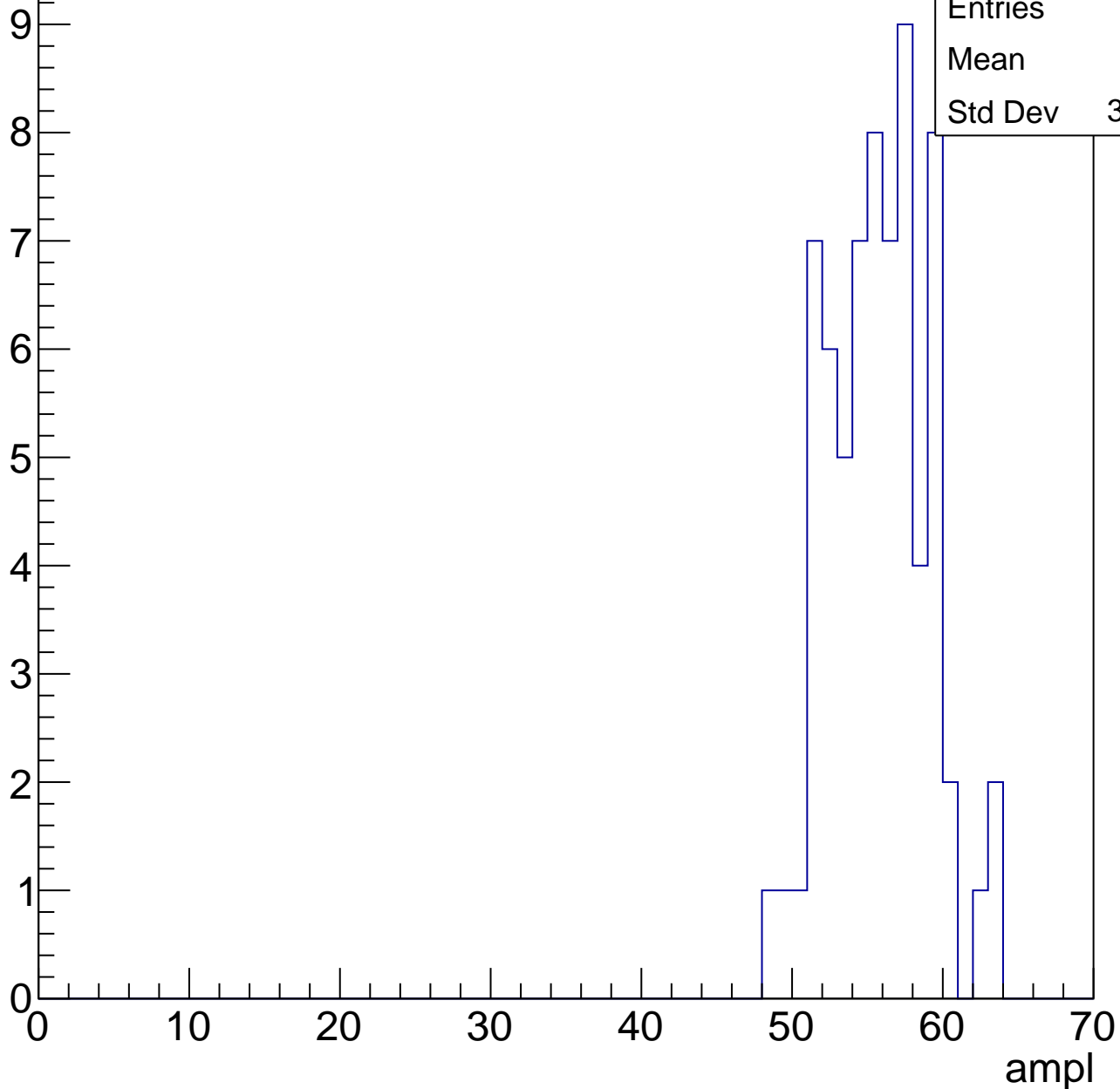
Entries	61
Mean	48.08
Std Dev	3.35



B0L002S, U17-ch118, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



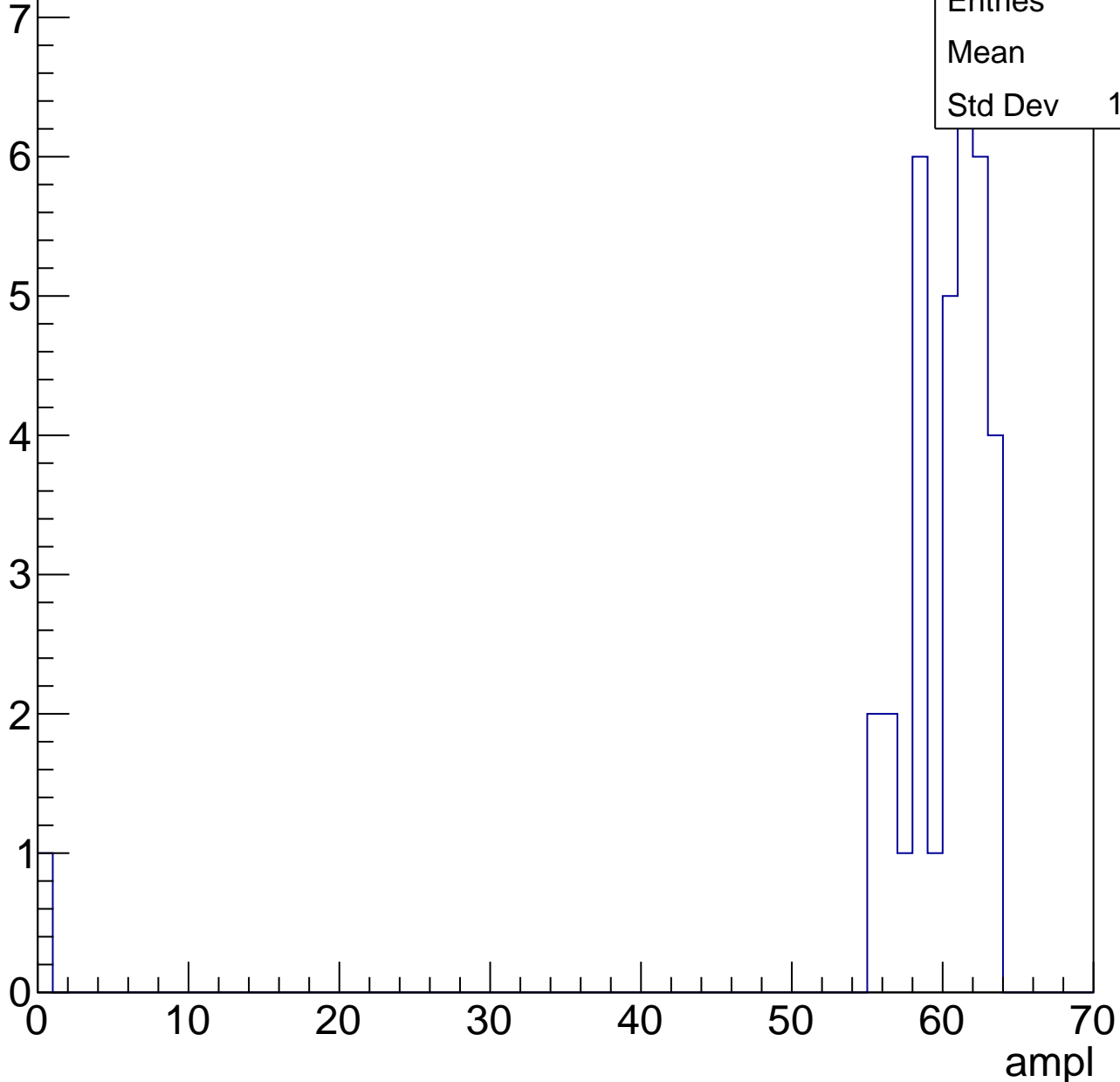
Entries	69
Mean	55.3
Std Dev	3.254

B0L002S, U17-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

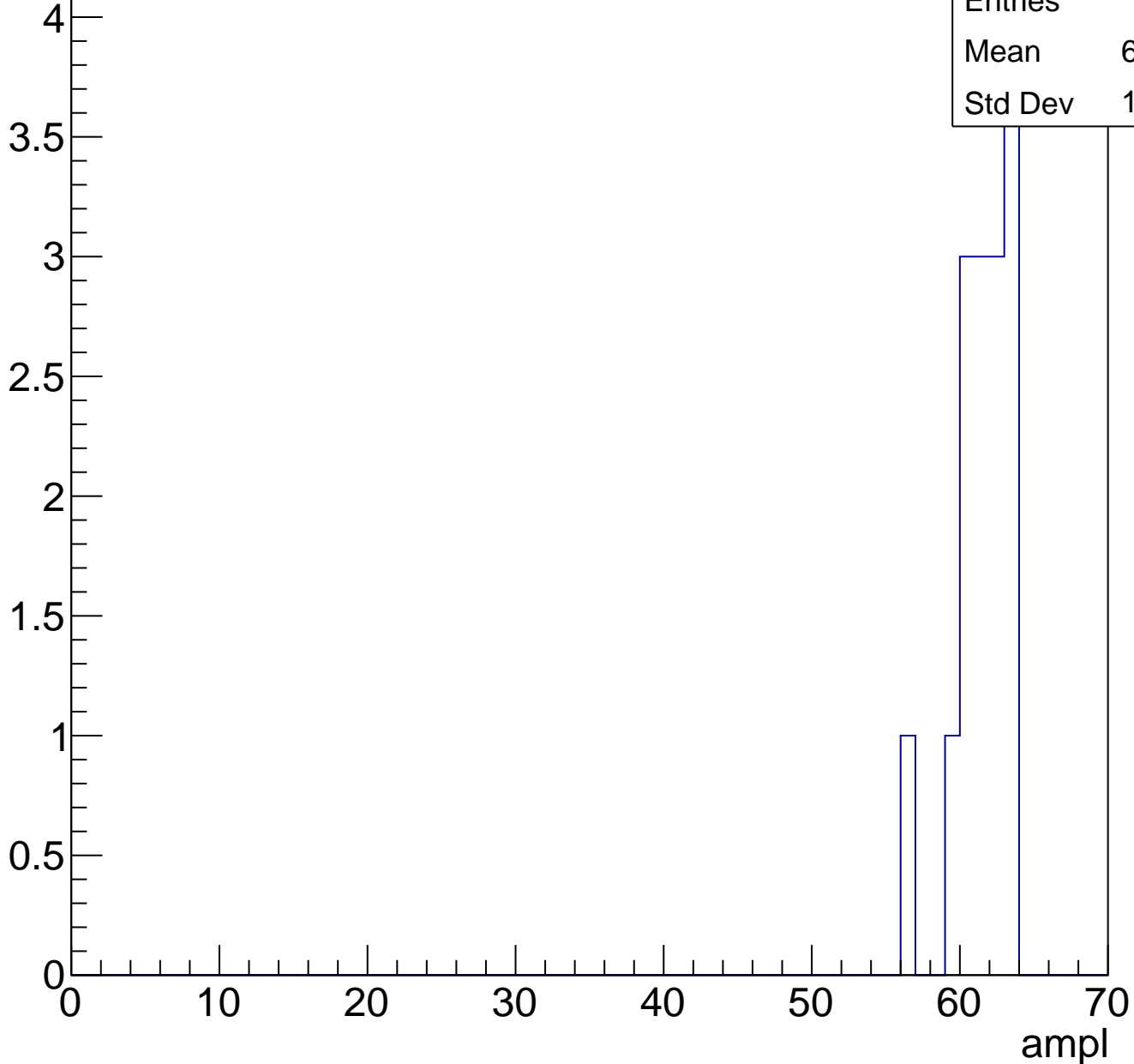
Entries	35
Mean	58.2
Std Dev	10.24



B0L002S, U17-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	15
Mean	61.07
Std Dev	1.843

B0L002S, U17-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch119, adc0

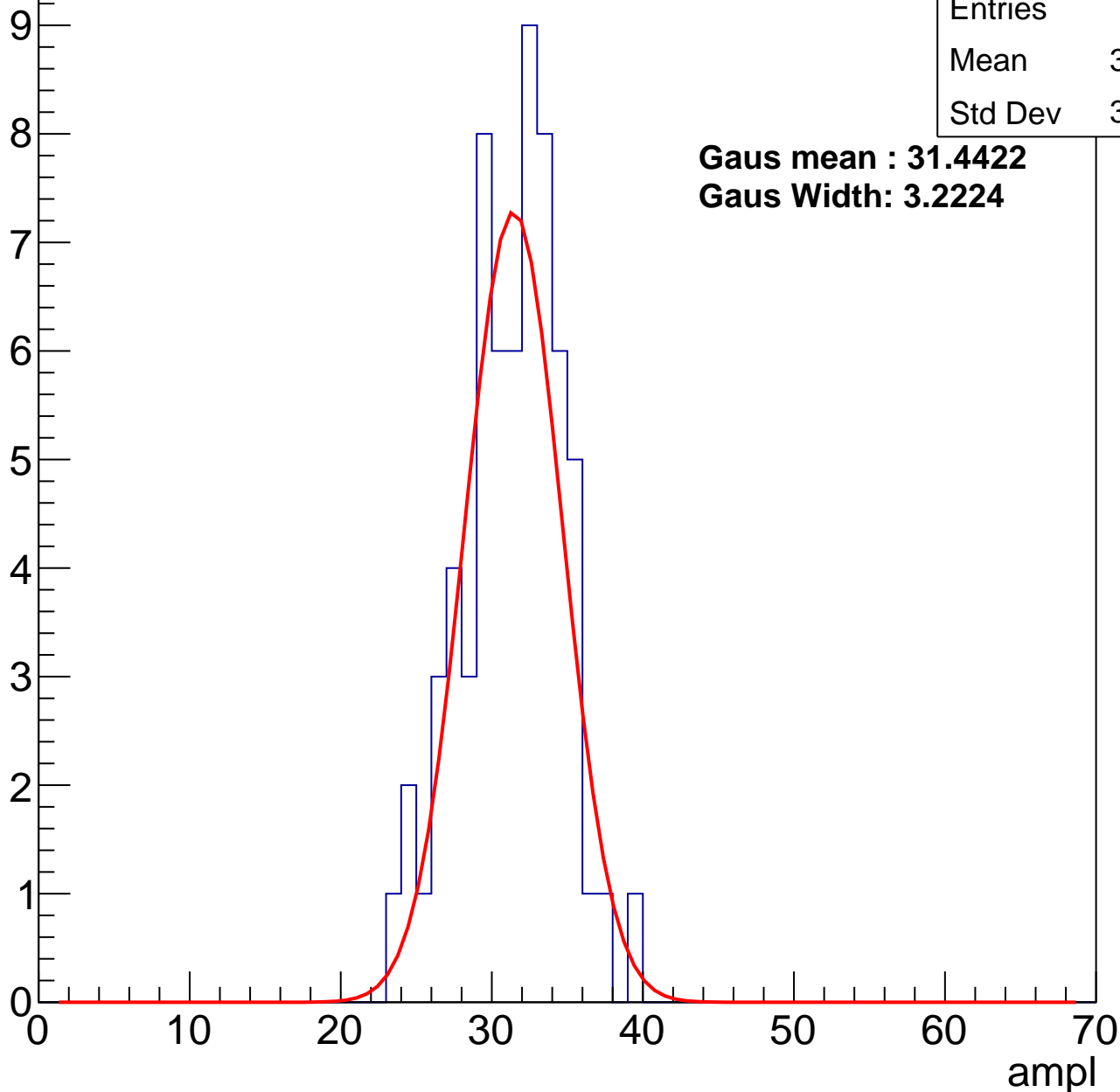
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.88
Std Dev	3.284

Gaus mean : 31.4422

Gaus Width: 3.2224



B0L002S, U17-ch119, adc1

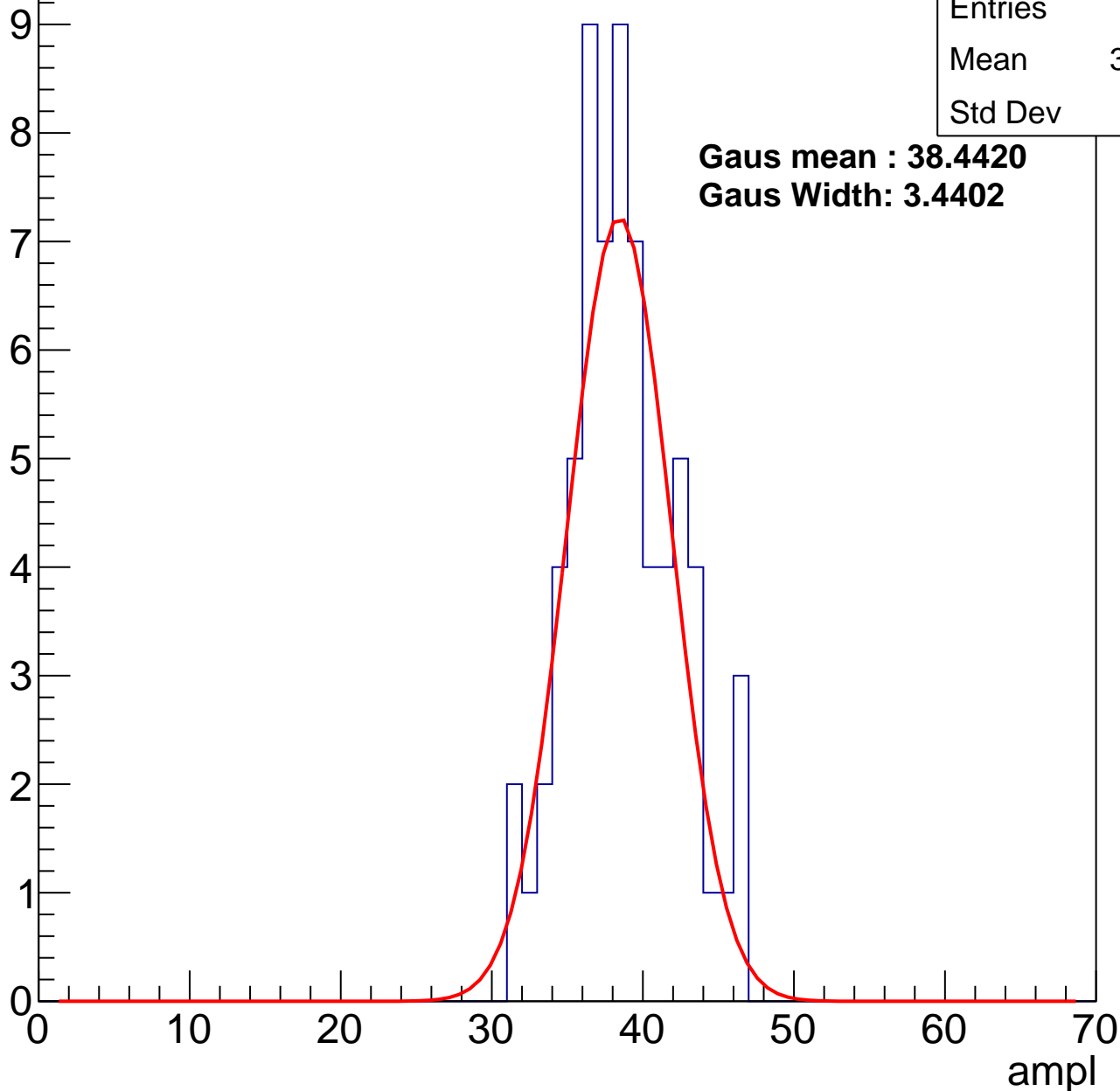
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38.26
Std Dev	3.53

Gaus mean : 38.4420

Gaus Width: 3.4402



B0L002S, U17-ch119, adc2

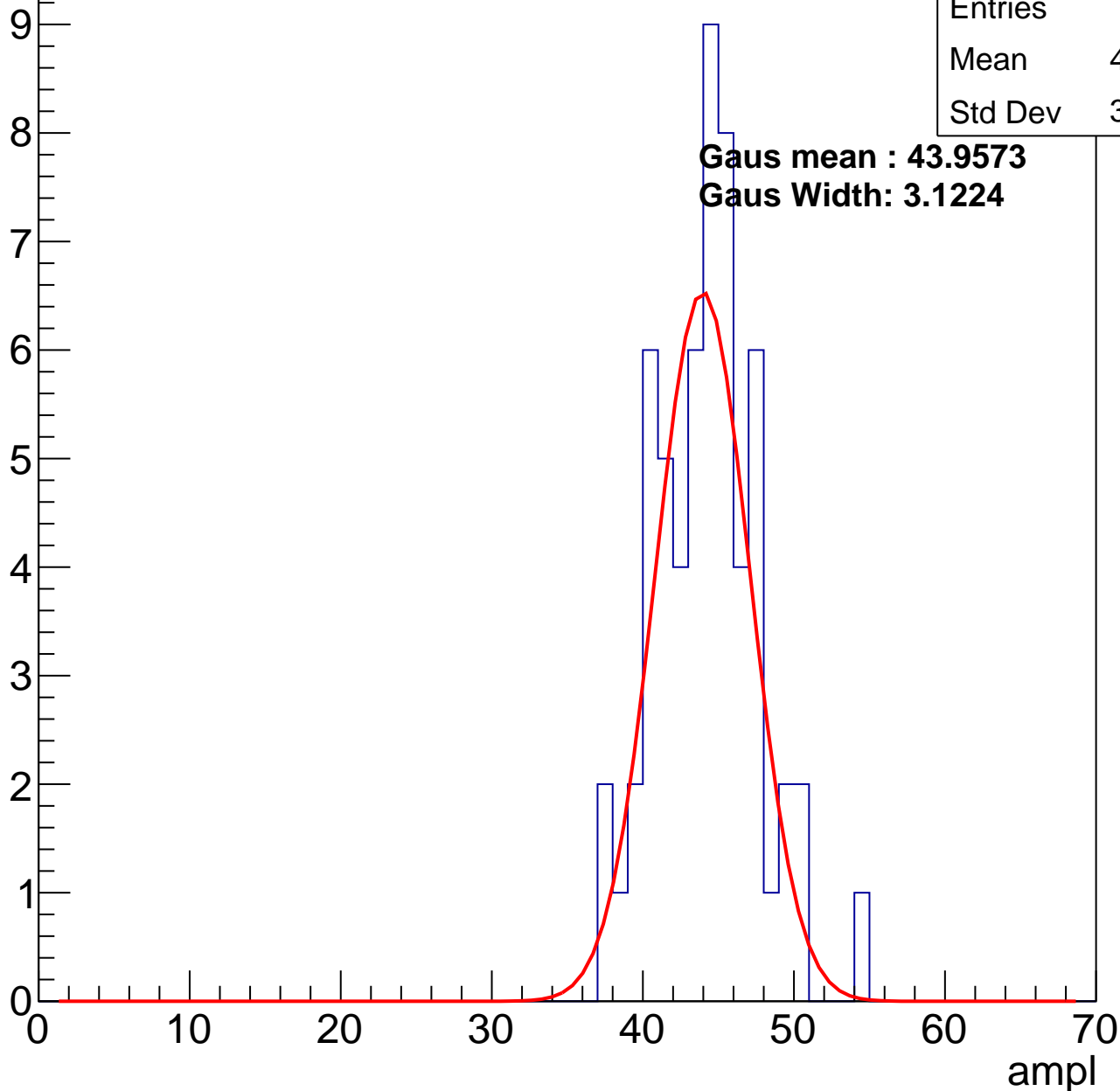
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.78
Std Dev	3.345

Gaus mean : 43.9573

Gaus Width: 3.1224

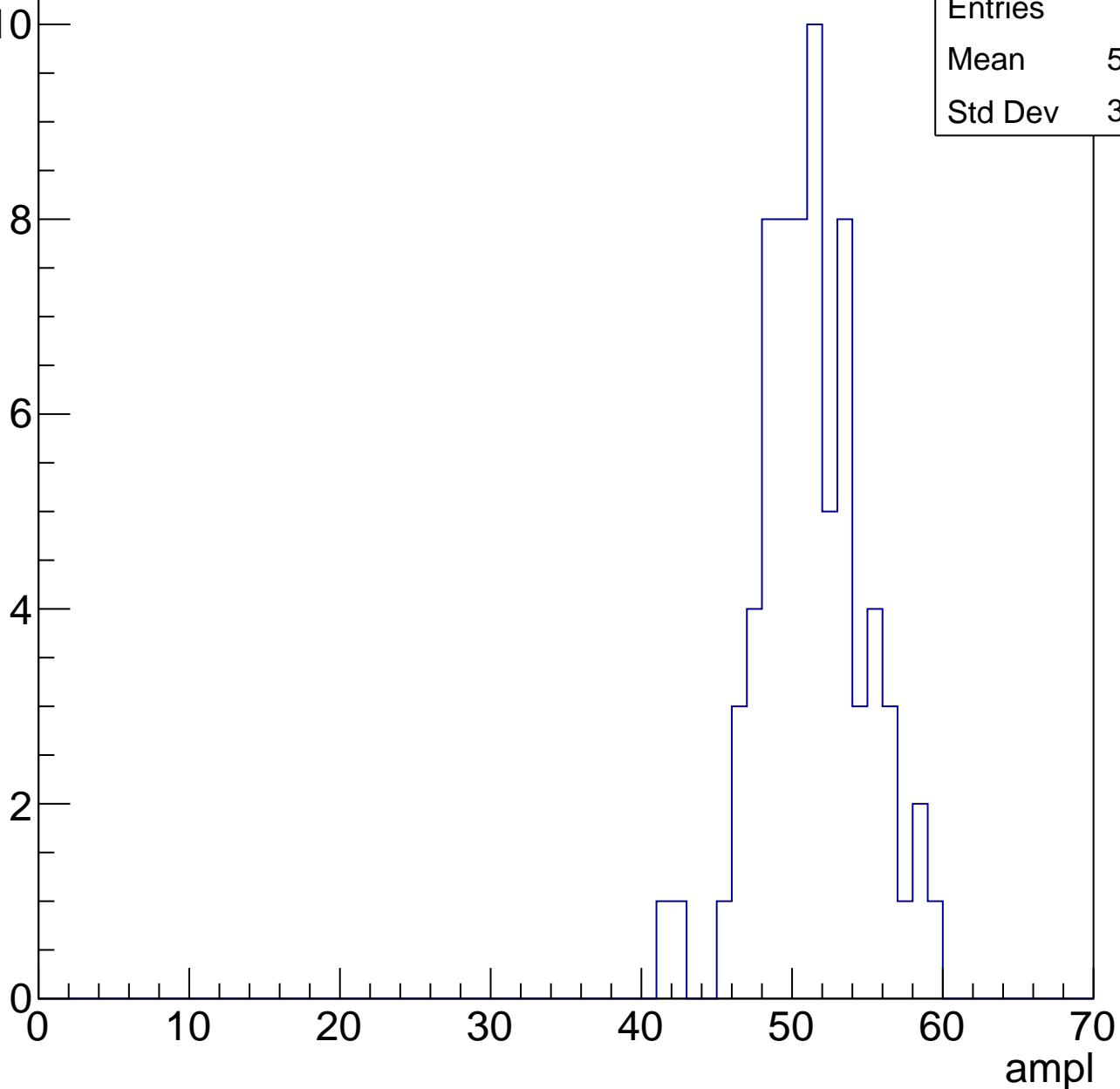


B0L002S, U17-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

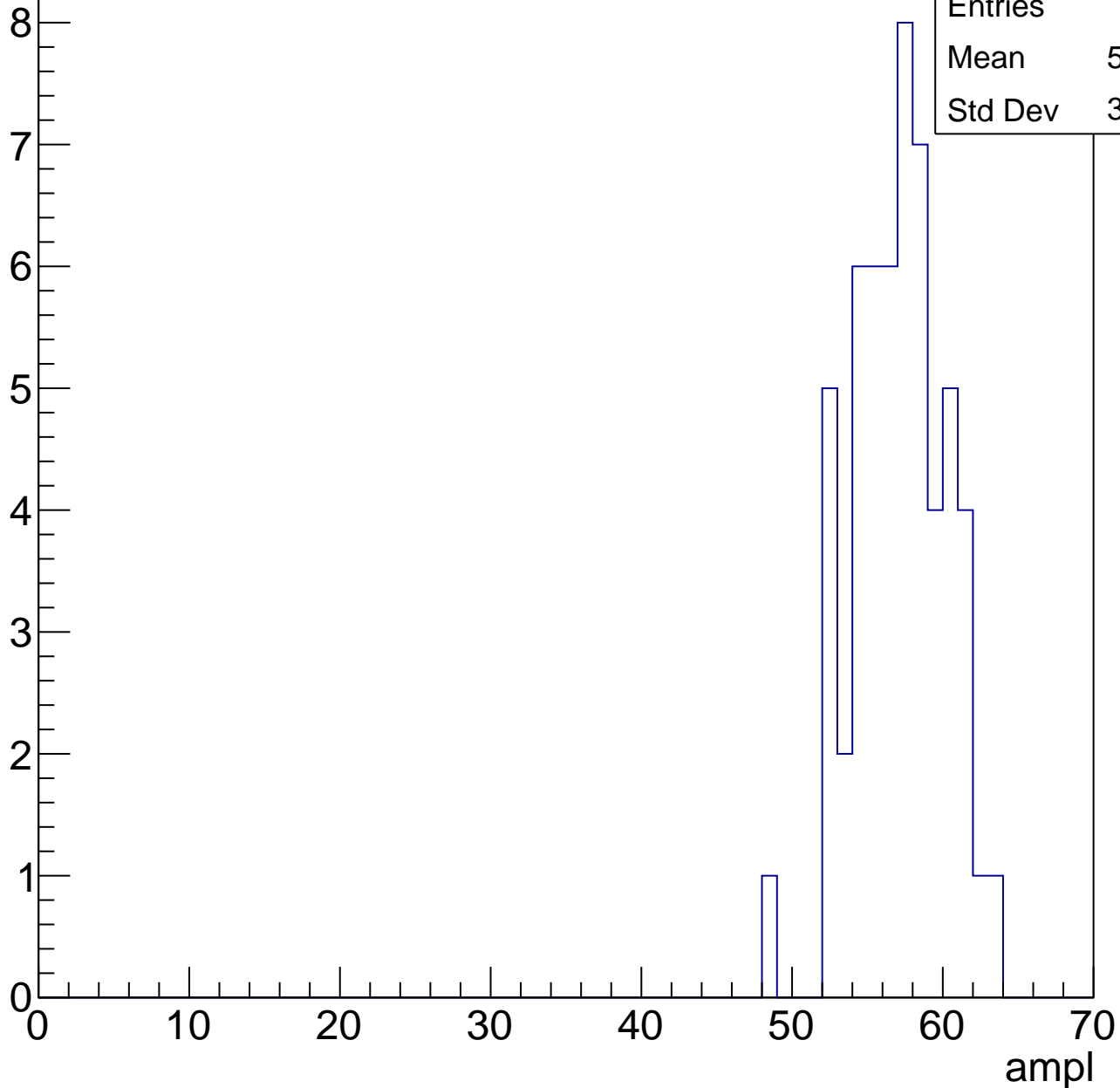
Entries	71
Mean	50.79
Std Dev	3.492



B0L002S, U17-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



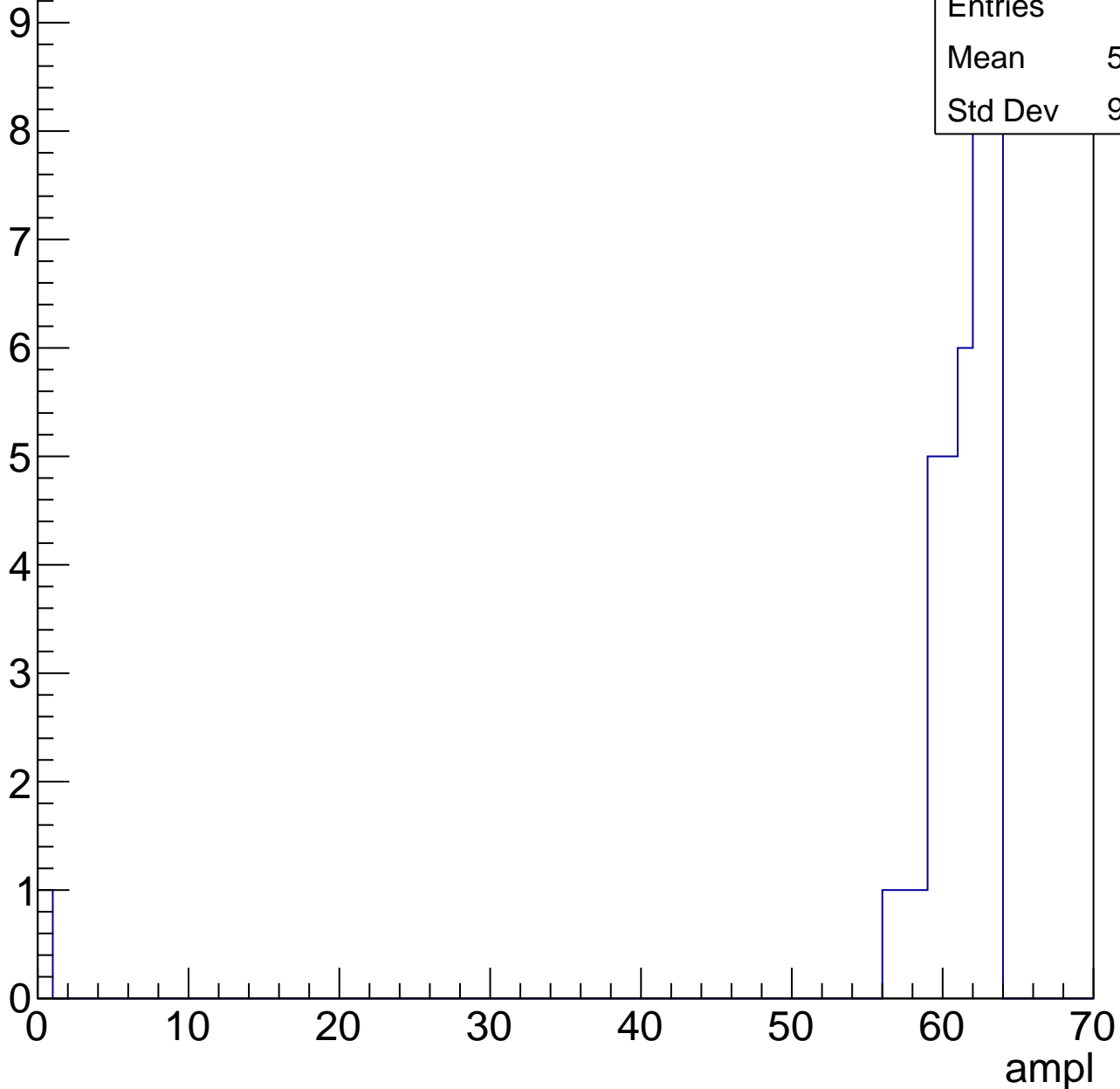
Entries	56
Mean	56.62
Std Dev	3.009

B0L002S, U17-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.39
Std Dev	9.925



B0L002S, U17-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch120, adc0

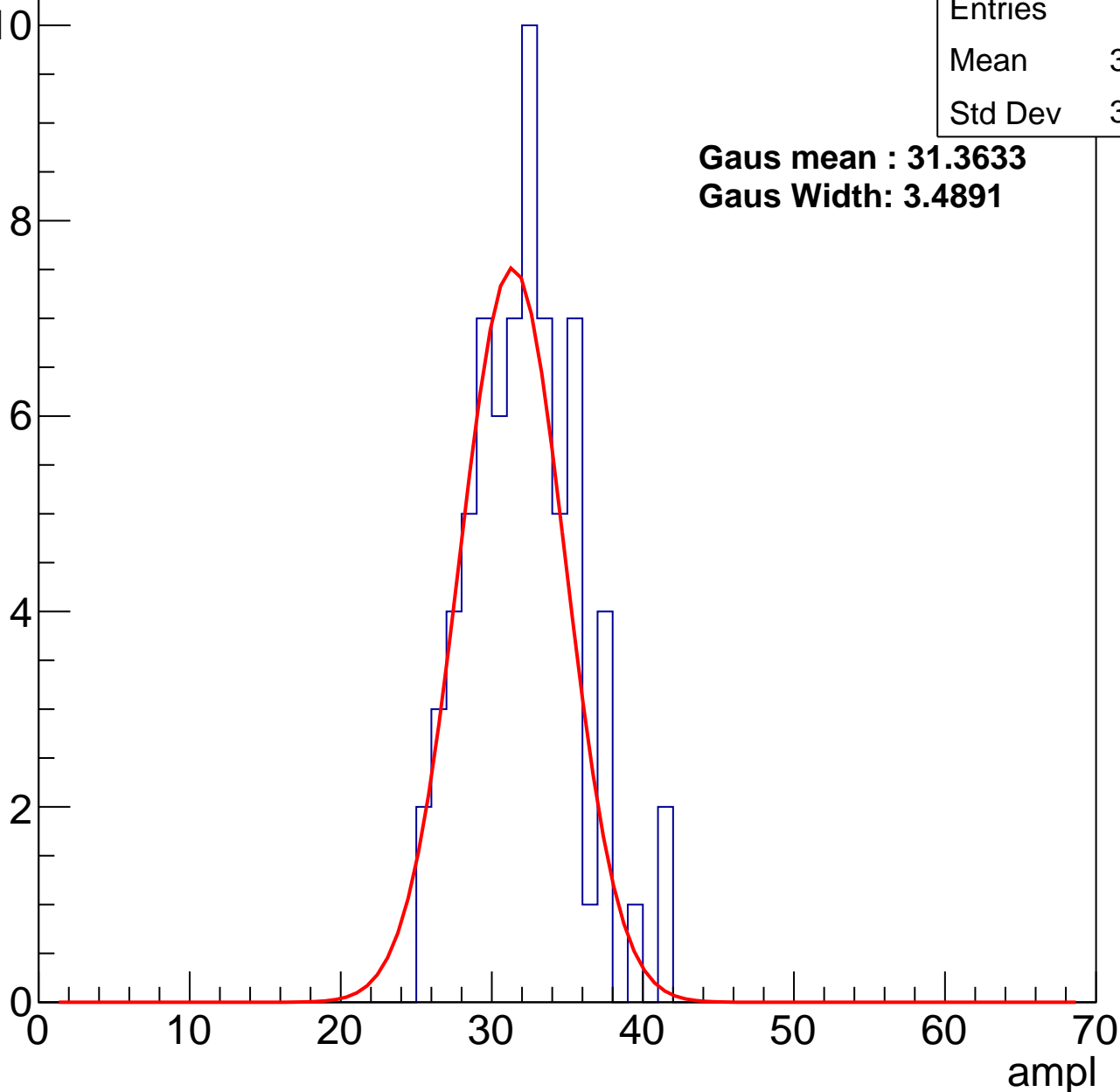
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.65
Std Dev	3.537

Gaus mean : 31.3633

Gaus Width: 3.4891



B0L002S, U17-ch120, adc1

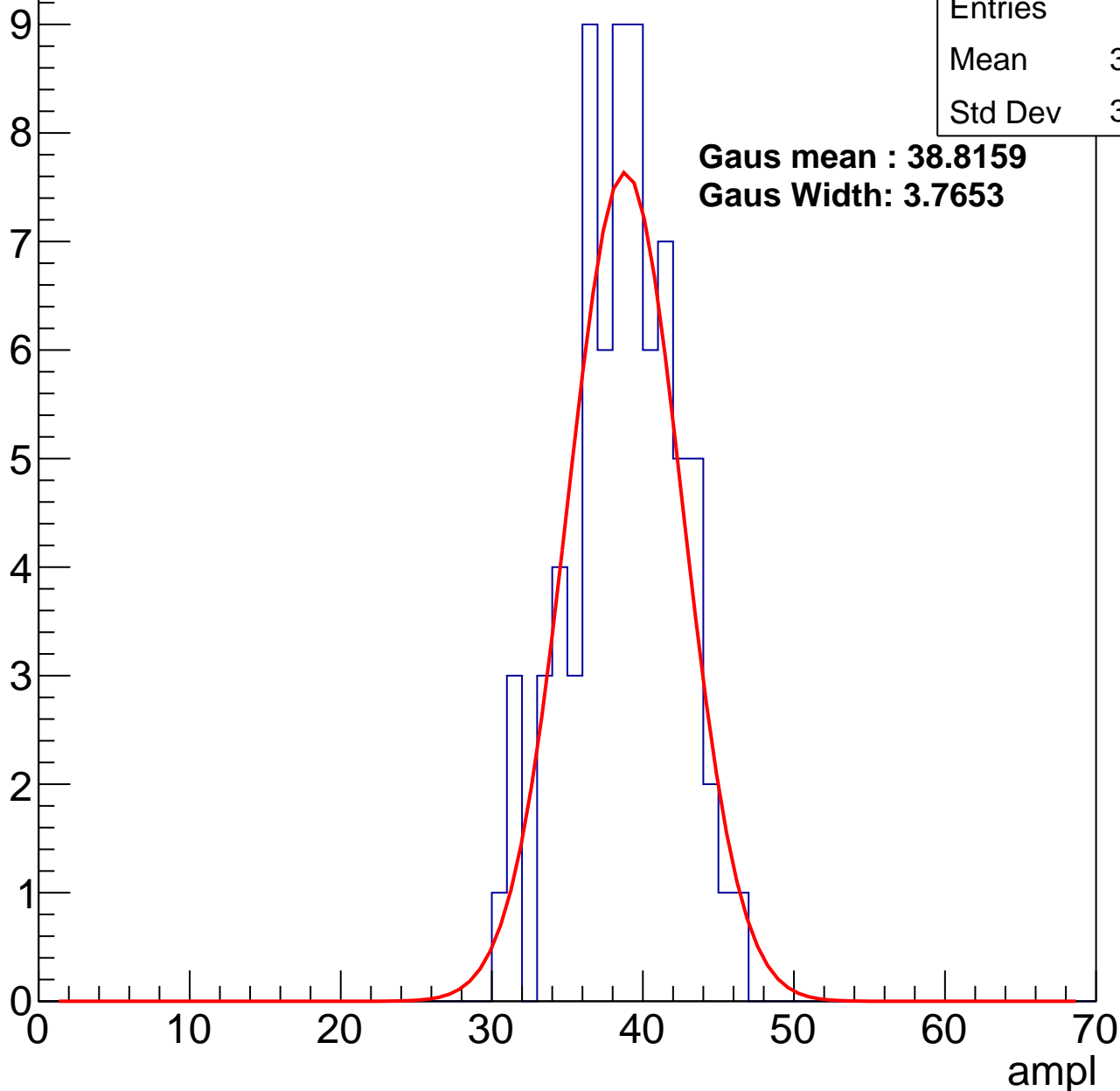
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	38.28
Std Dev	3.486

Gaus mean : 38.8159

Gaus Width: 3.7653



B0L002S, U17-ch120, adc2

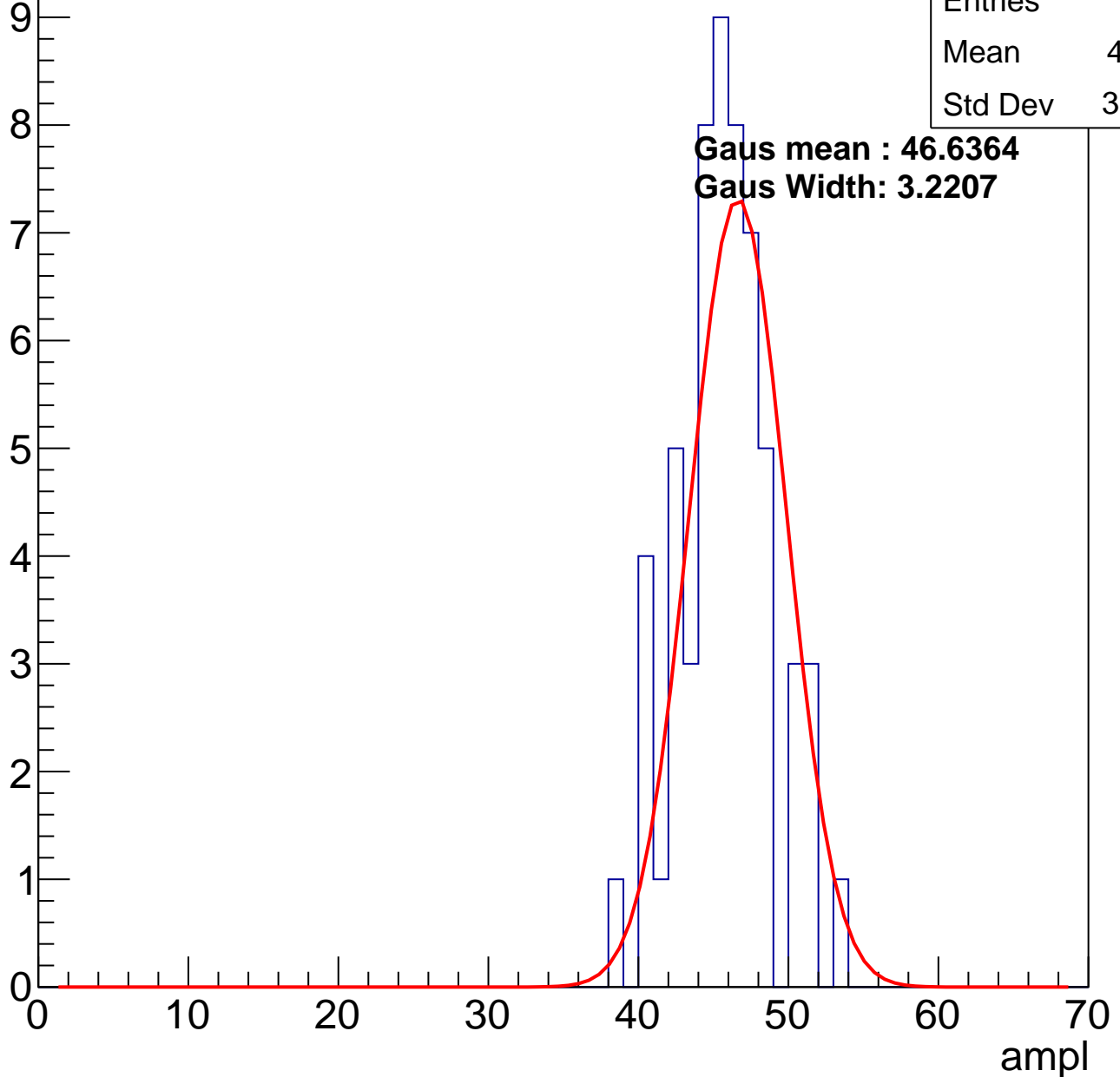
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	45.31
Std Dev	3.086

Gaus mean : 46.6364

Gaus Width: 3.2207

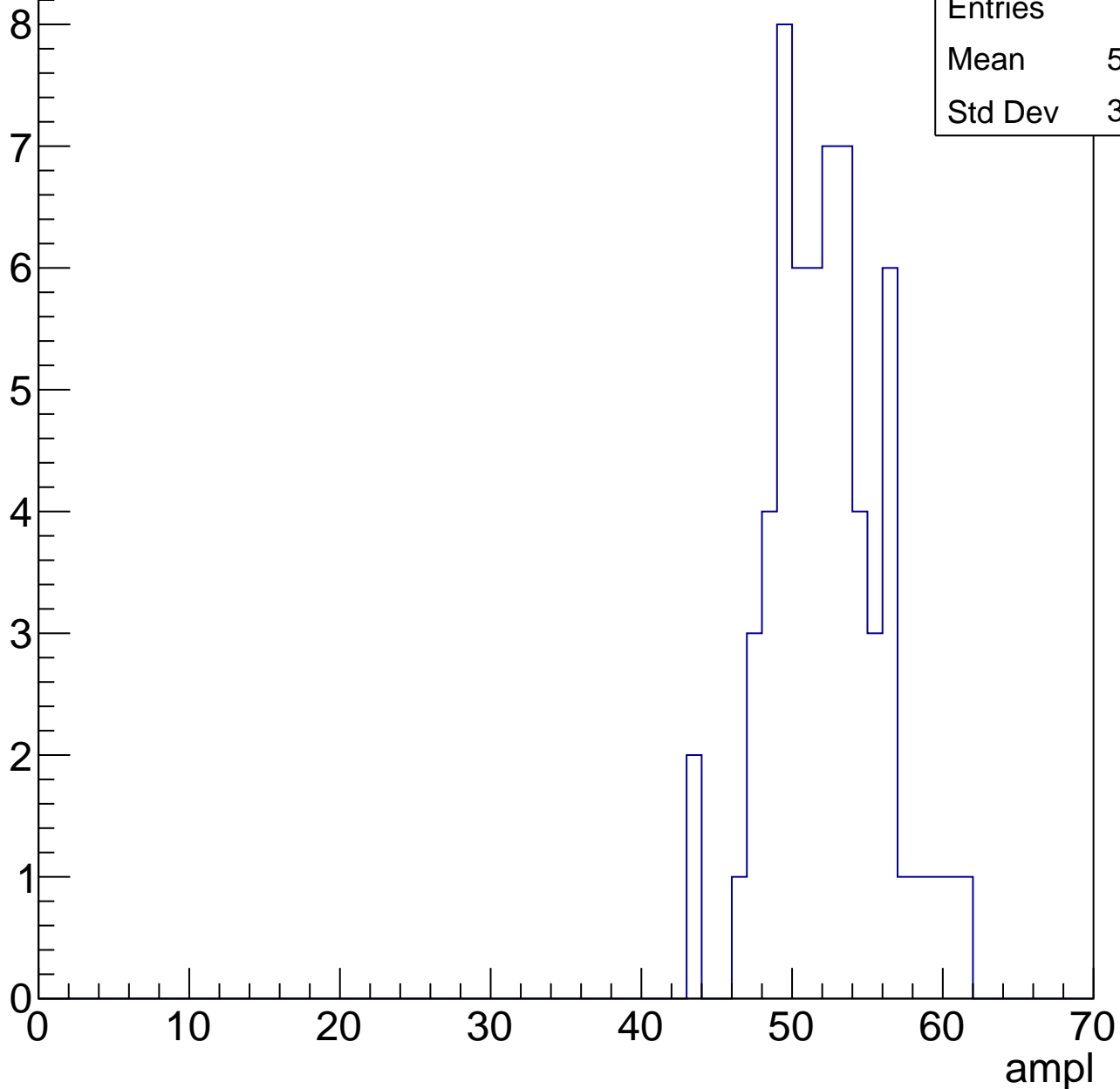


B0L002S, U17-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.77
Std Dev	3.674

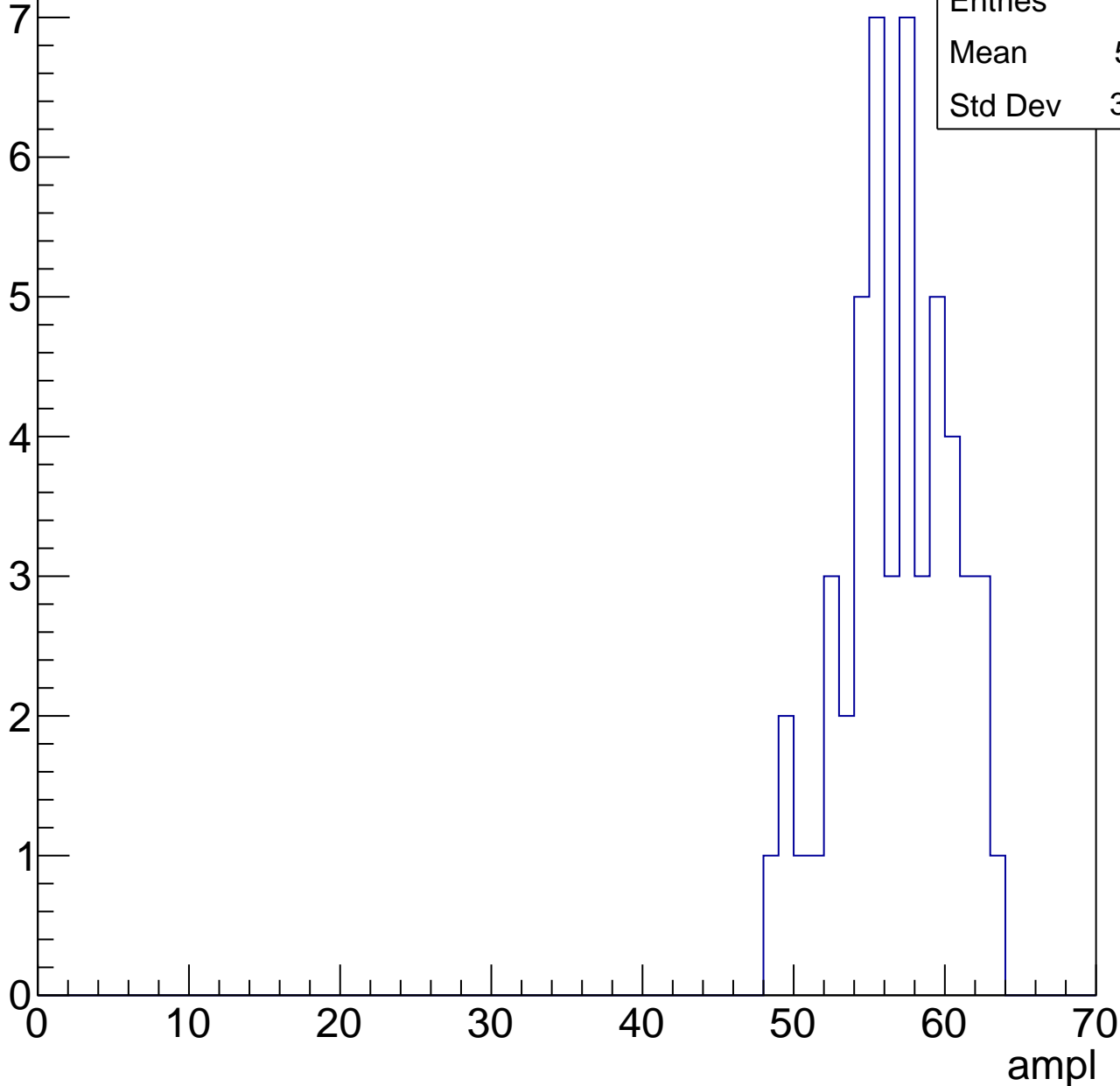


B0L002S, U17-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.31
Std Dev	3.627

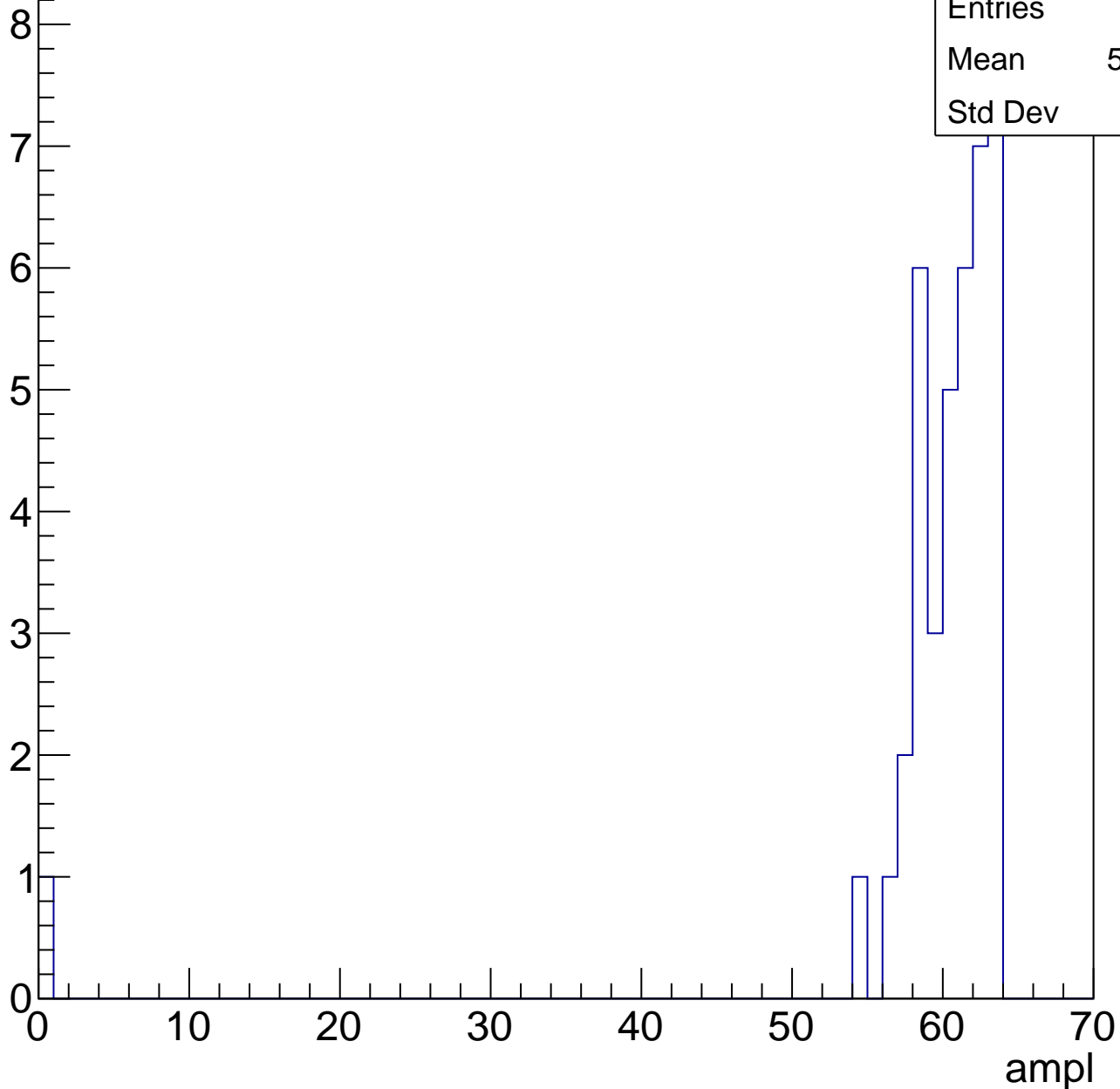


B0L002S, U17-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	58.83
Std Dev	9.68



B0L002S, U17-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U17-ch121, adc0

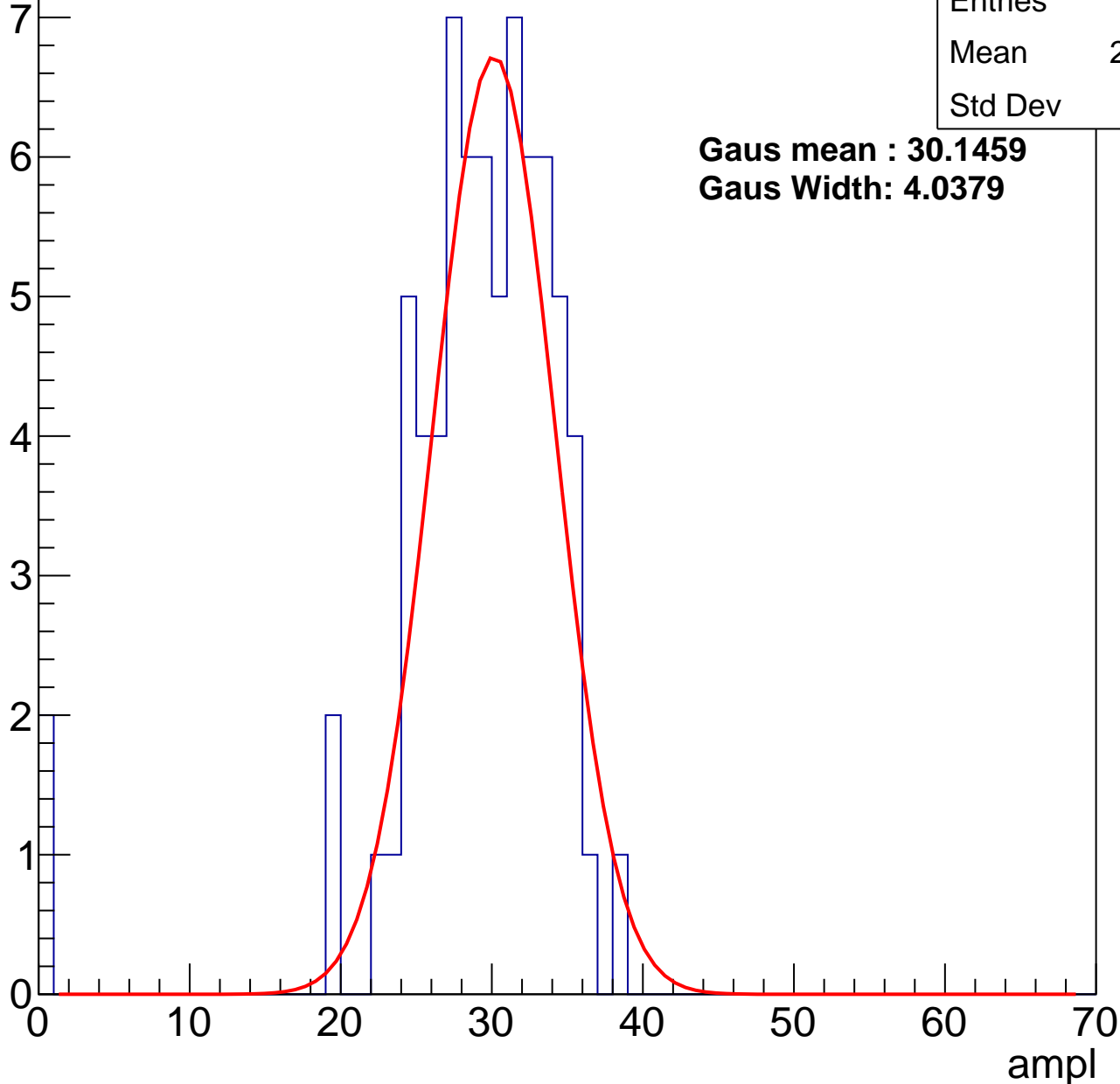
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	28.48
Std Dev	6.18

Gaus mean : 30.1459

Gaus Width: 4.0379



B0L002S, U17-ch121, adc1

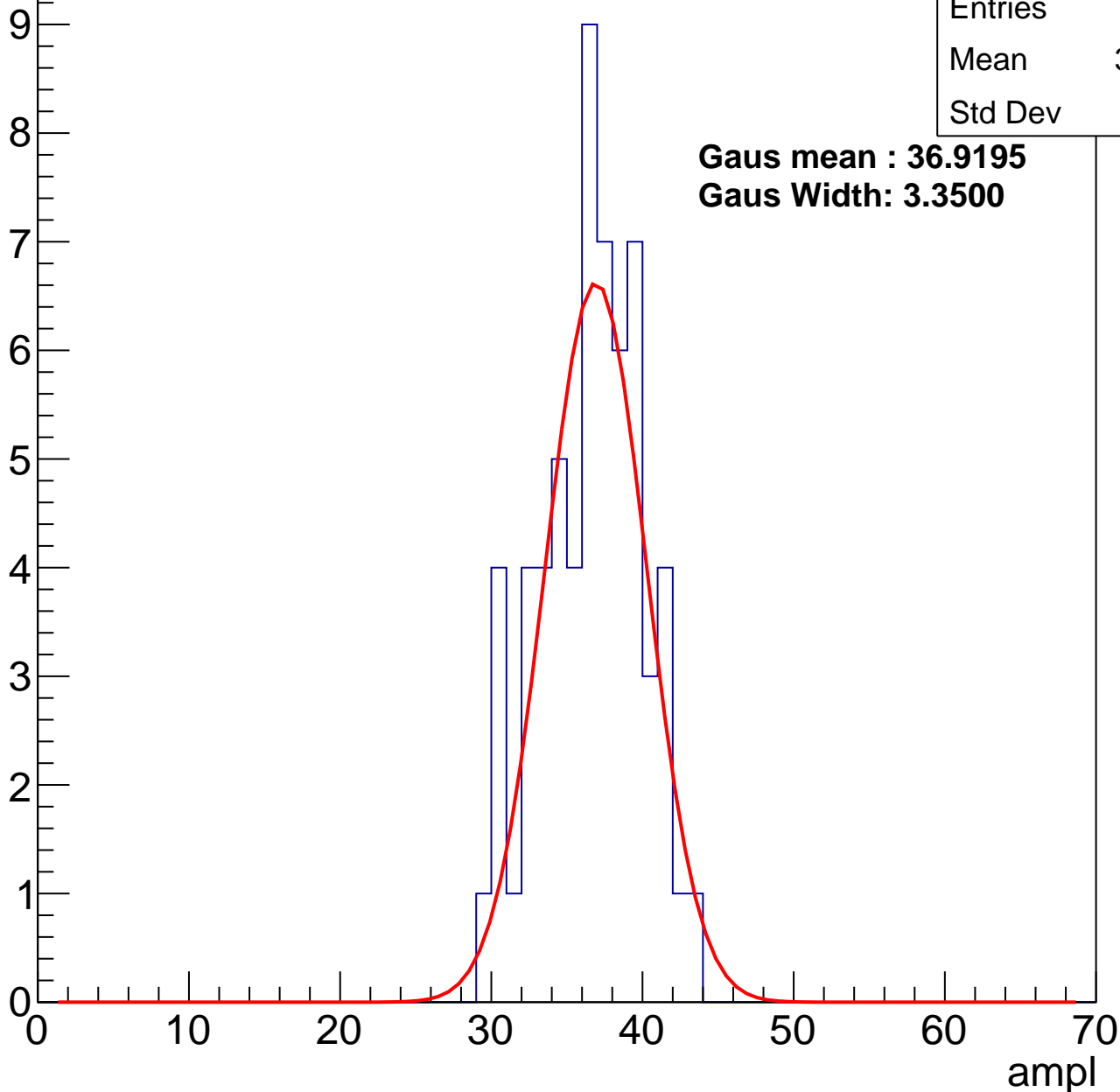
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.11
Std Dev	3.31

Gaus mean : 36.9195

Gaus Width: 3.3500



B0L002S, U17-ch121, adc2

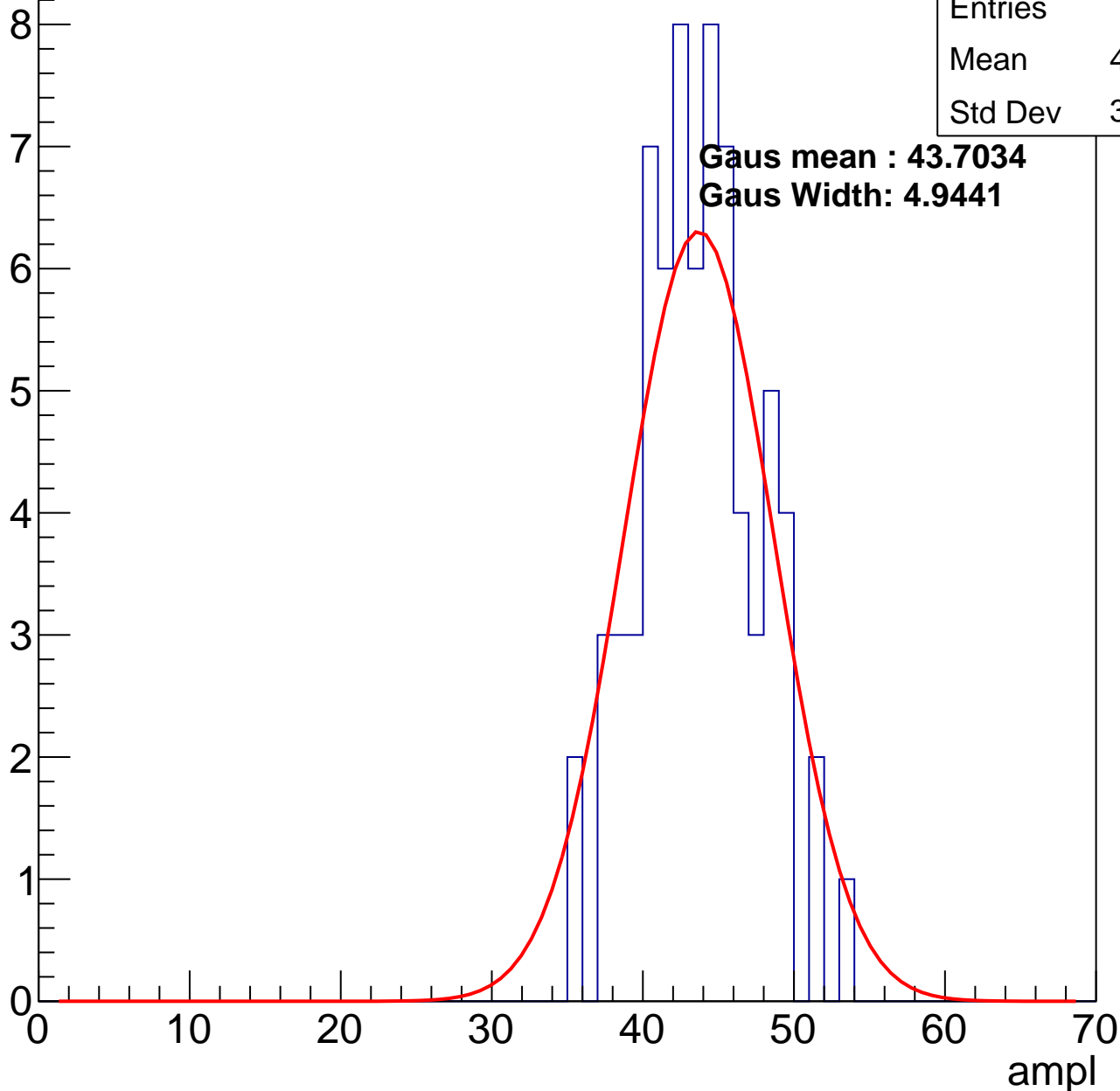
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.26
Std Dev	3.848

Gaus mean : 43.7034

Gaus Width: 4.9441

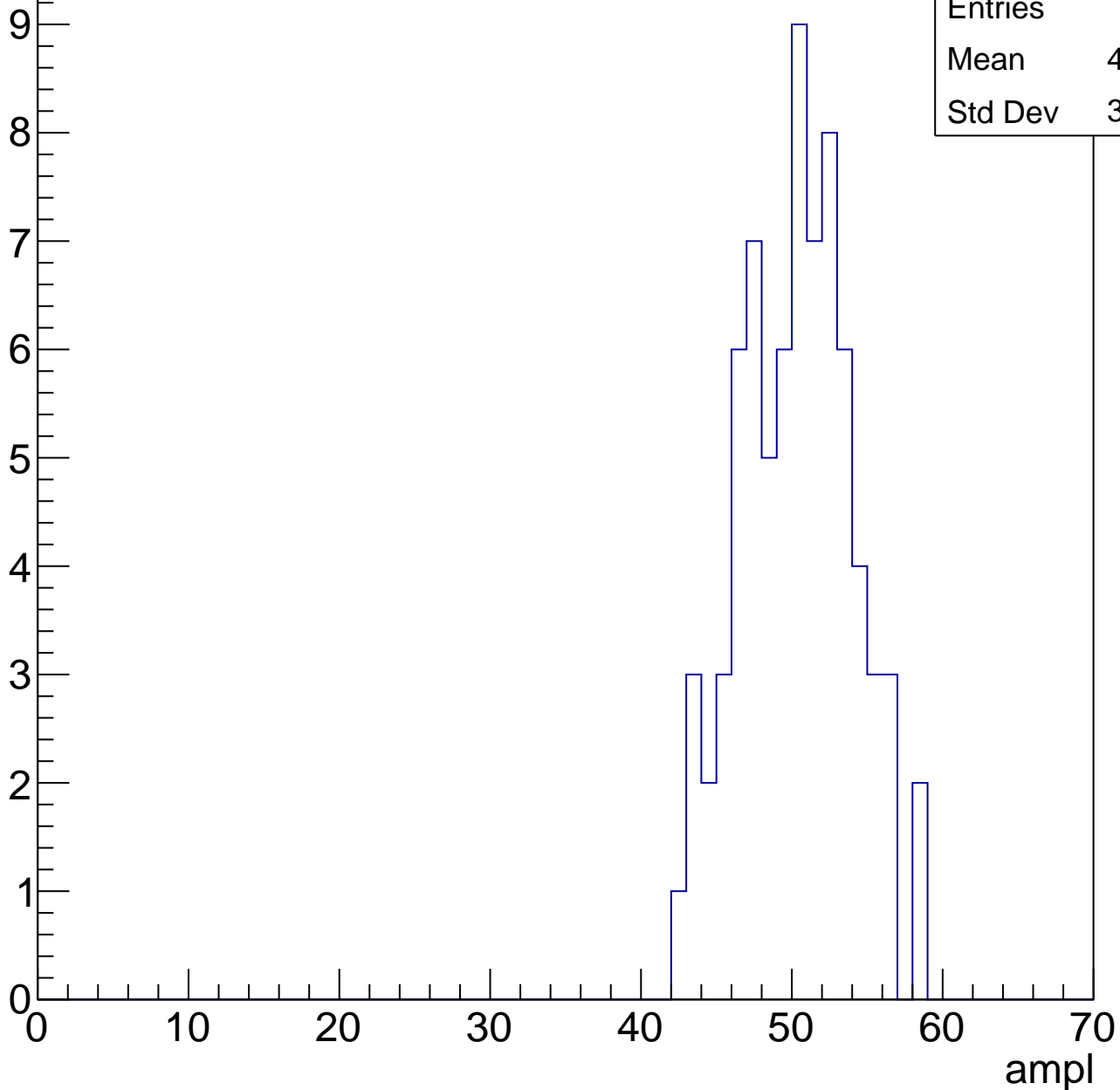


B0L002S, U17-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	49.85
Std Dev	3.669

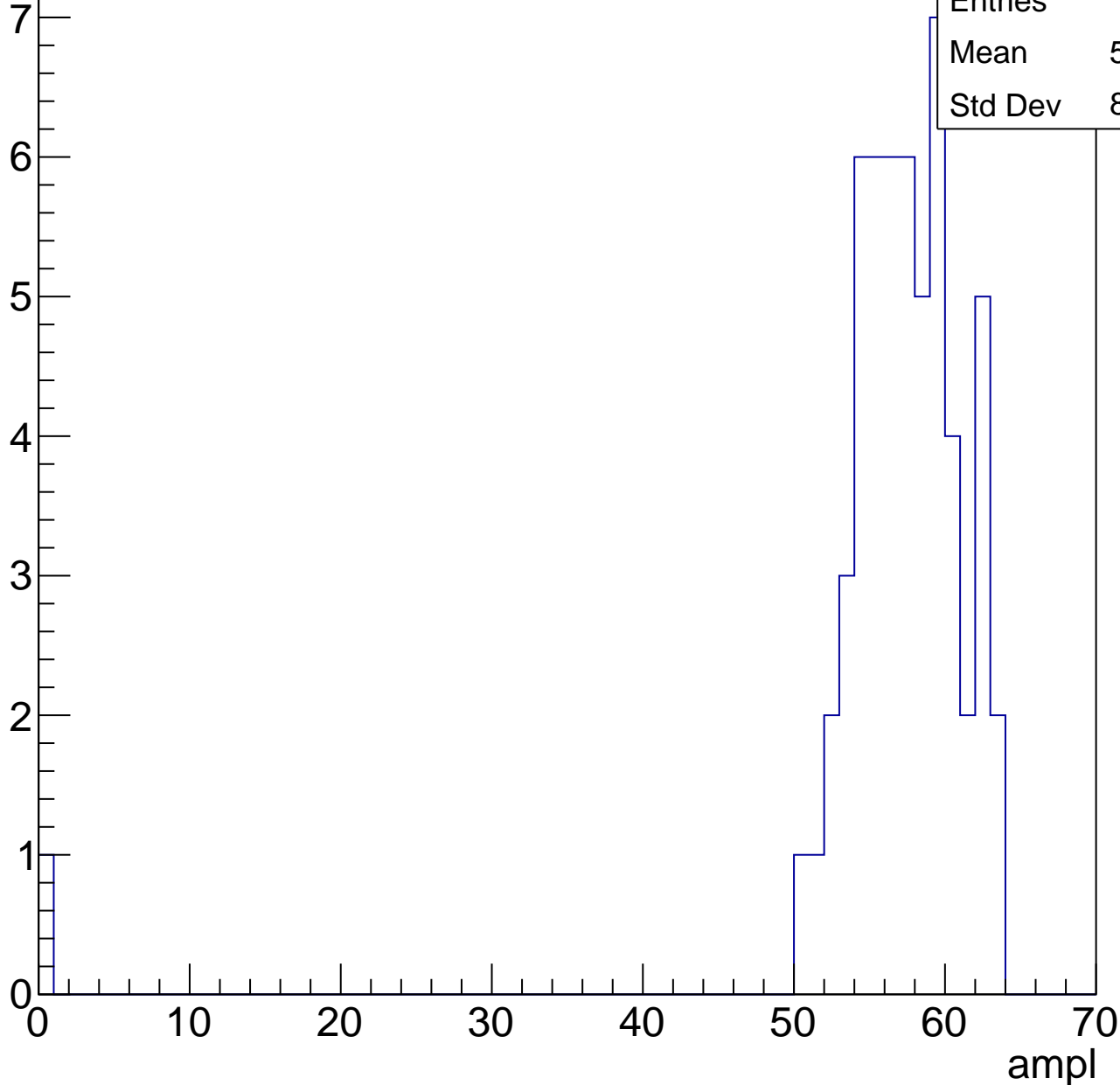


B0L002S, U17-ch121, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.09
Std Dev	8.129

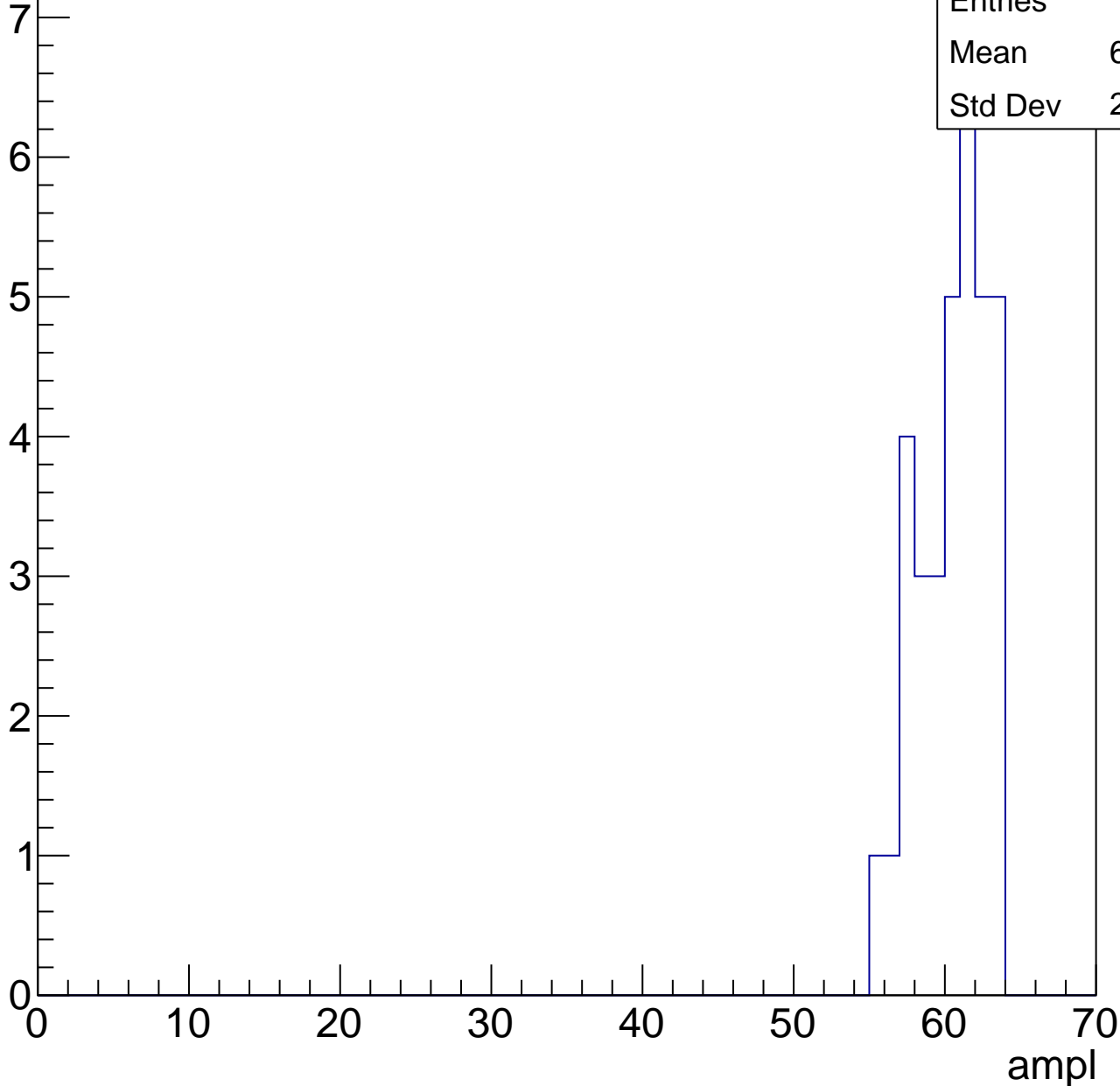


B0L002S, U17-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	60.06
Std Dev	2.195



B0L002S, U17-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch122, adc0

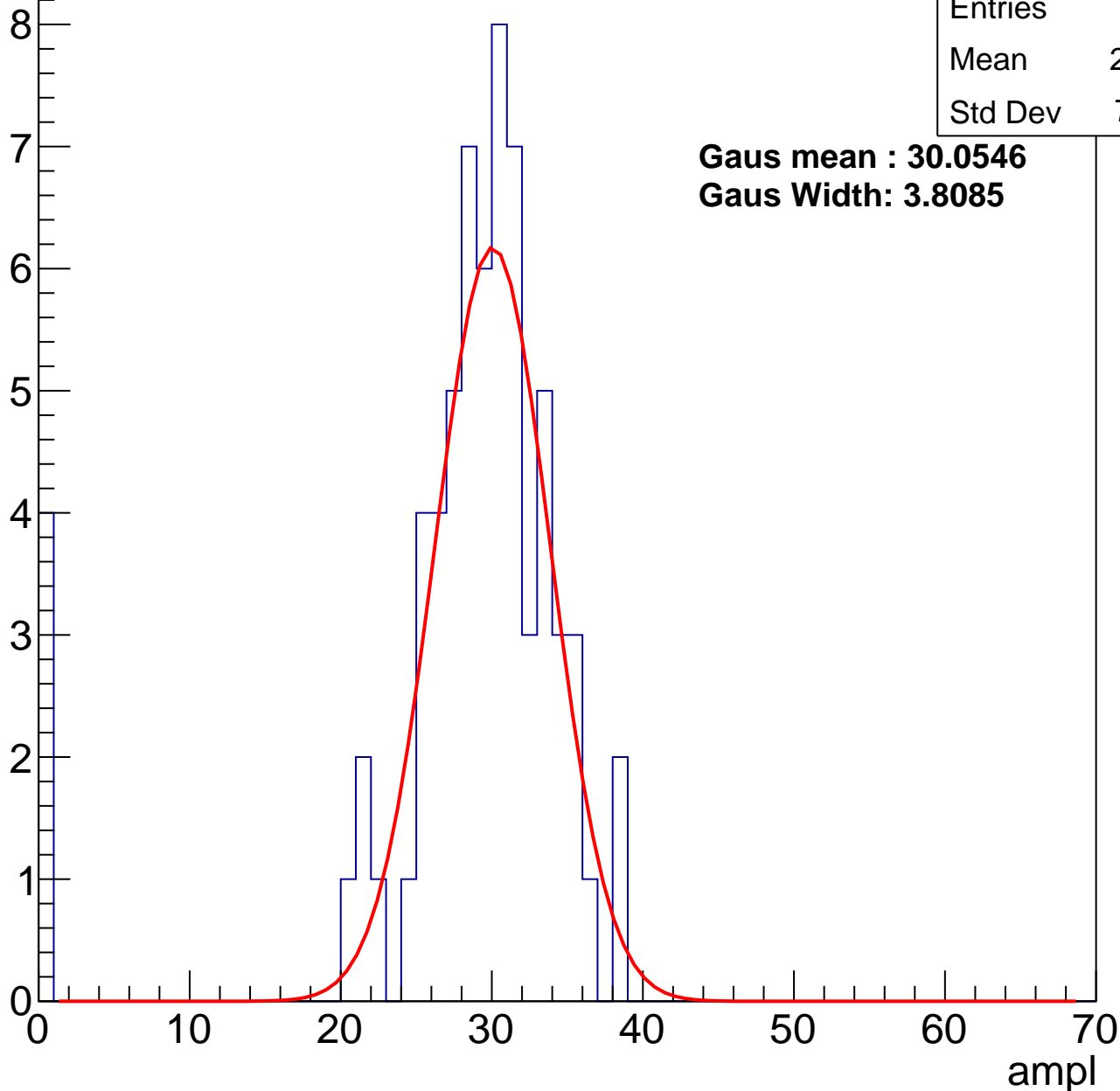
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	27.67
Std Dev	7.911

Gaus mean : 30.0546

Gaus Width: 3.8085



B0L002S, U17-ch122, adc1

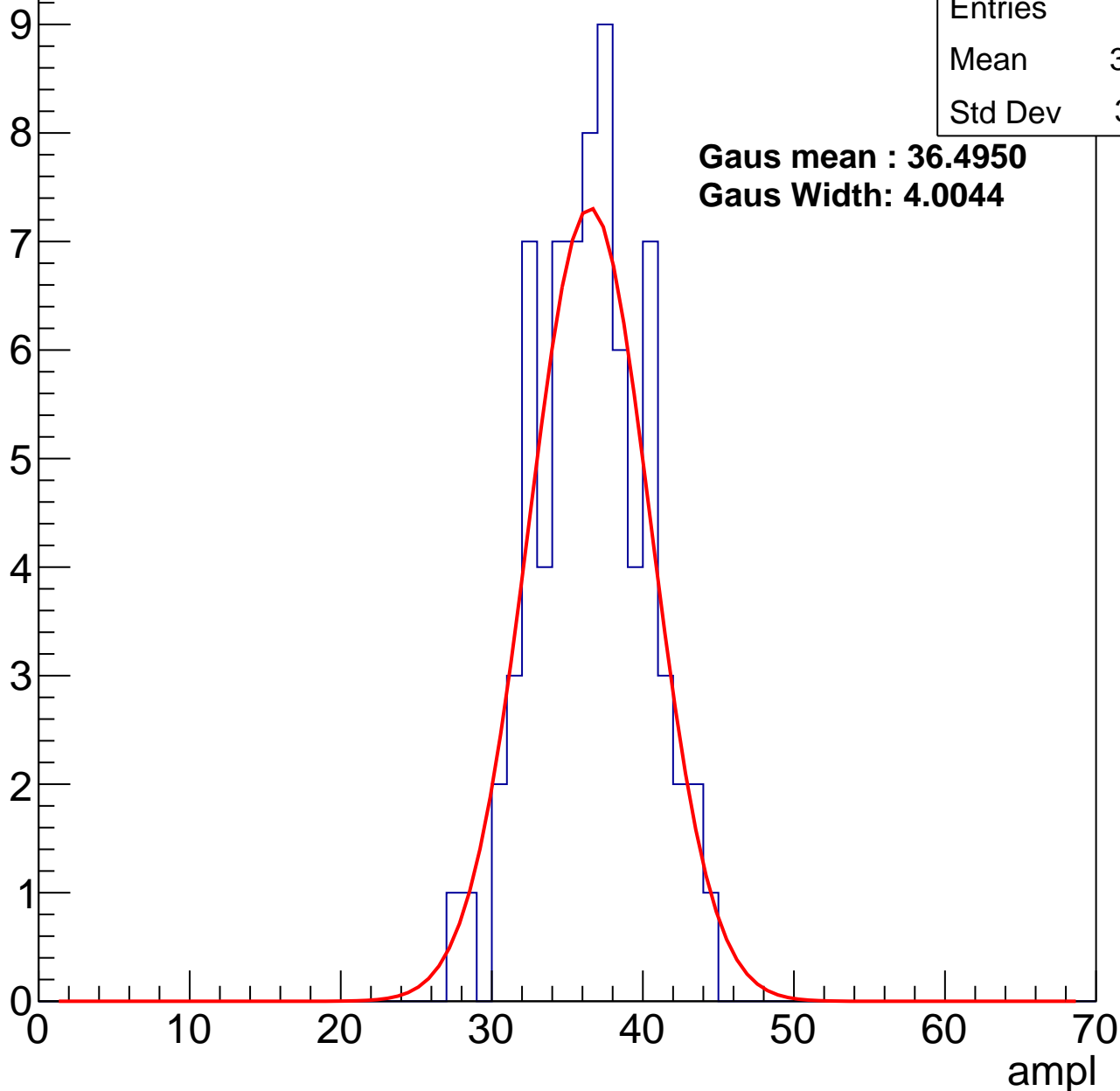
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	36.07
Std Dev	3.611

Gaus mean : 36.4950

Gaus Width: 4.0044



B0L002S, U17-ch122, adc2

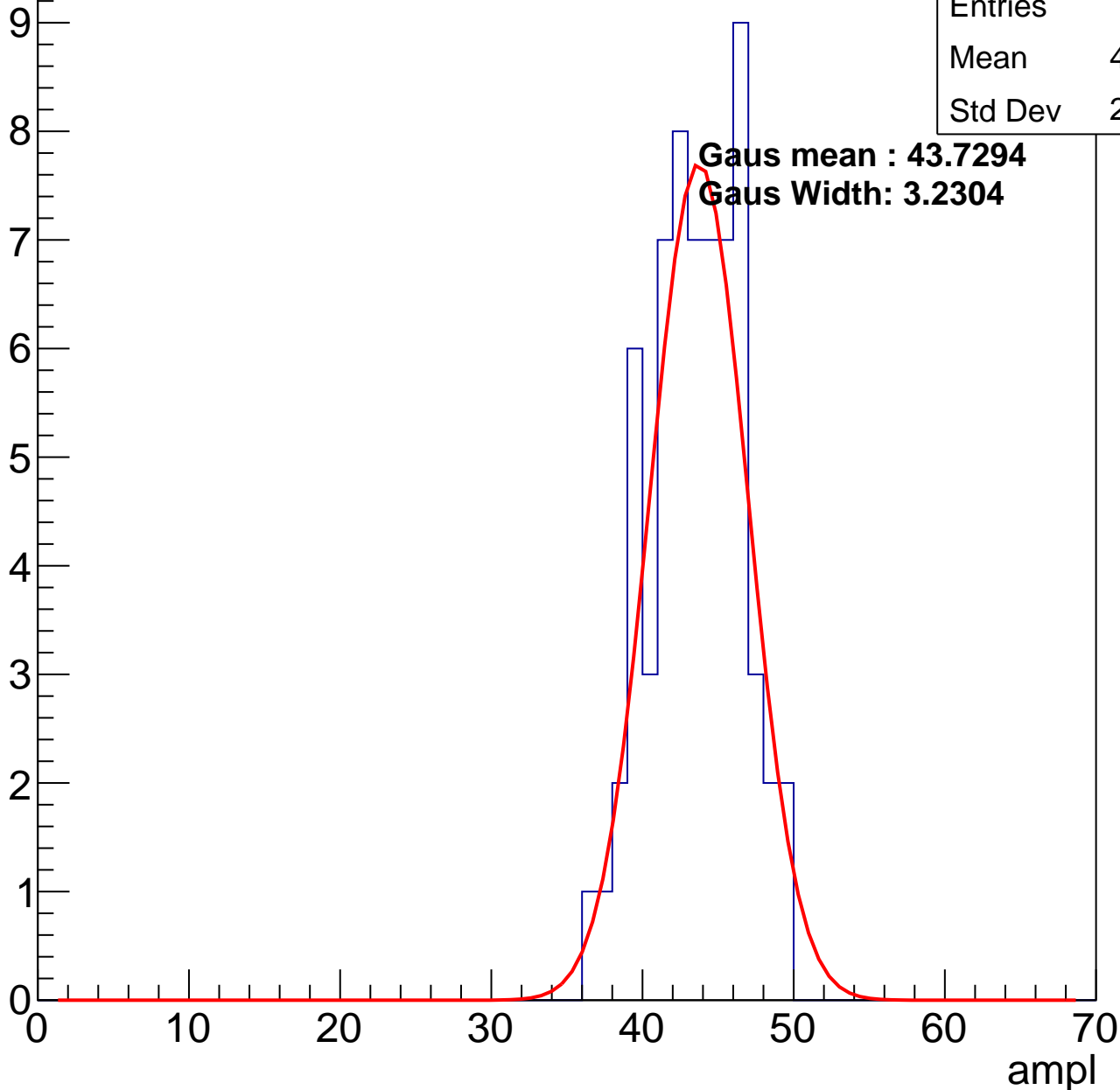
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.06
Std Dev	2.987

Gaus mean : 43.7294

Gaus Width: 3.2304

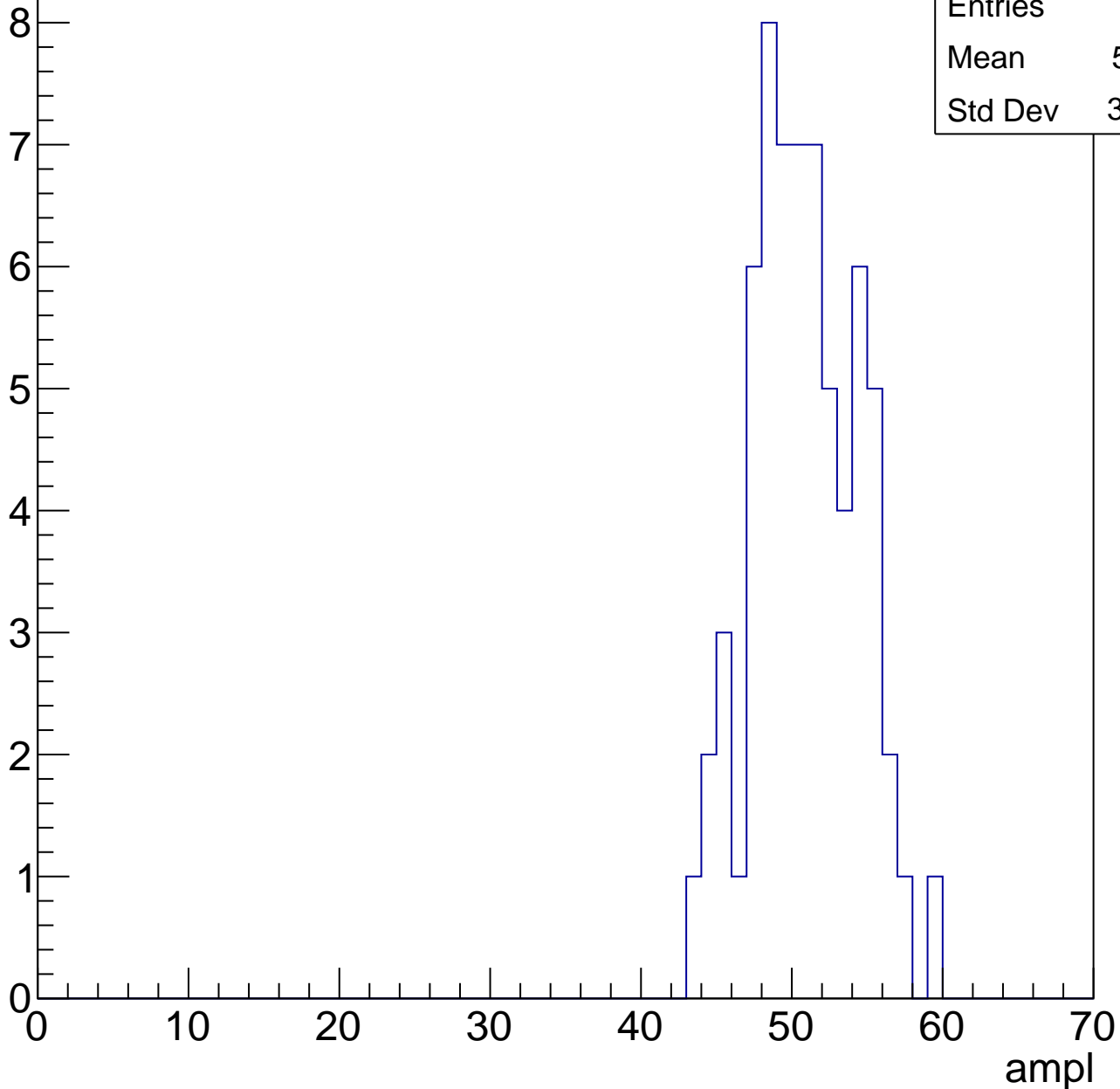


B0L002S, U17-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.41
Std Dev	3.438

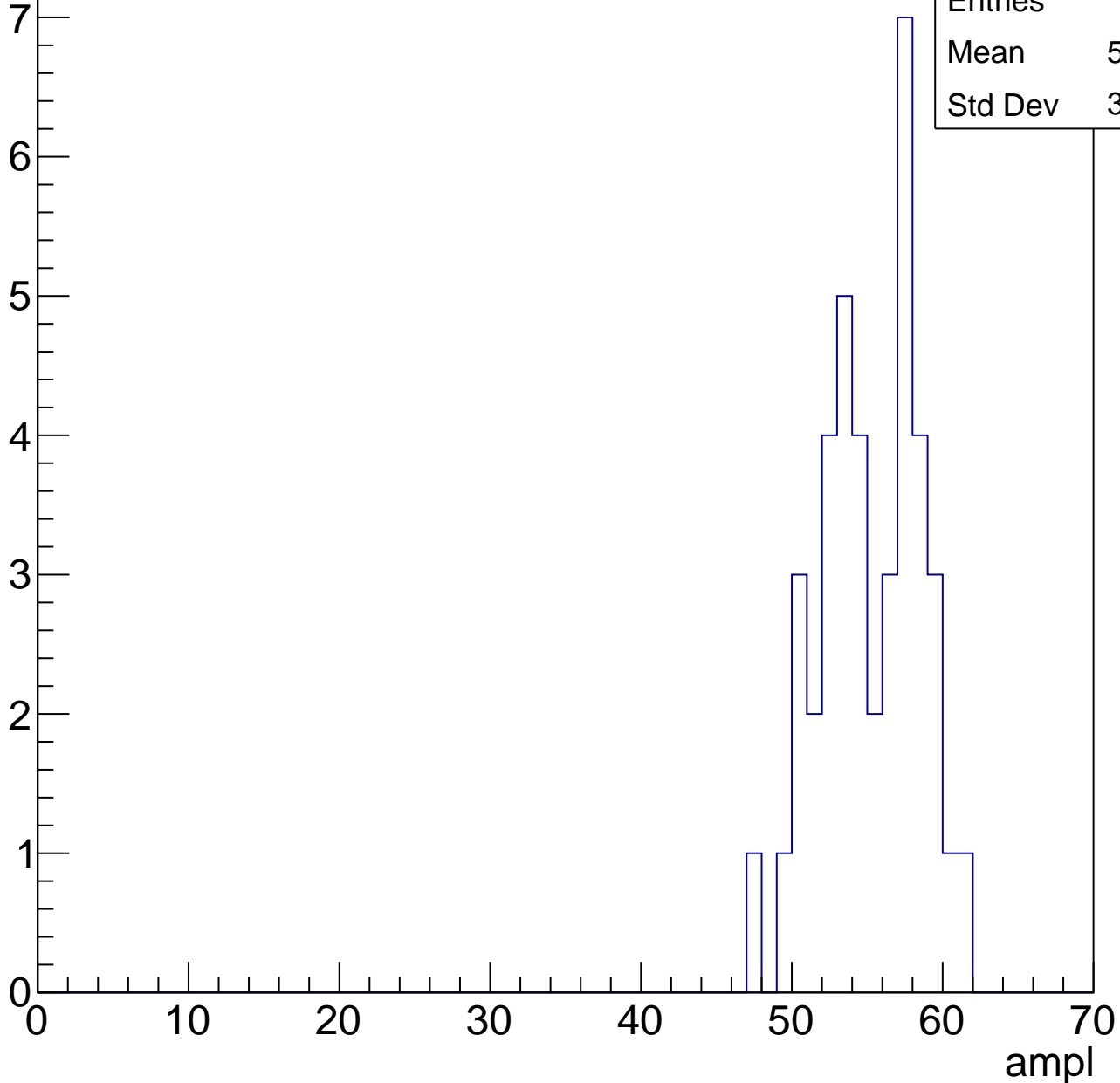


B0L002S, U17-ch122, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	54.73
Std Dev	3.276

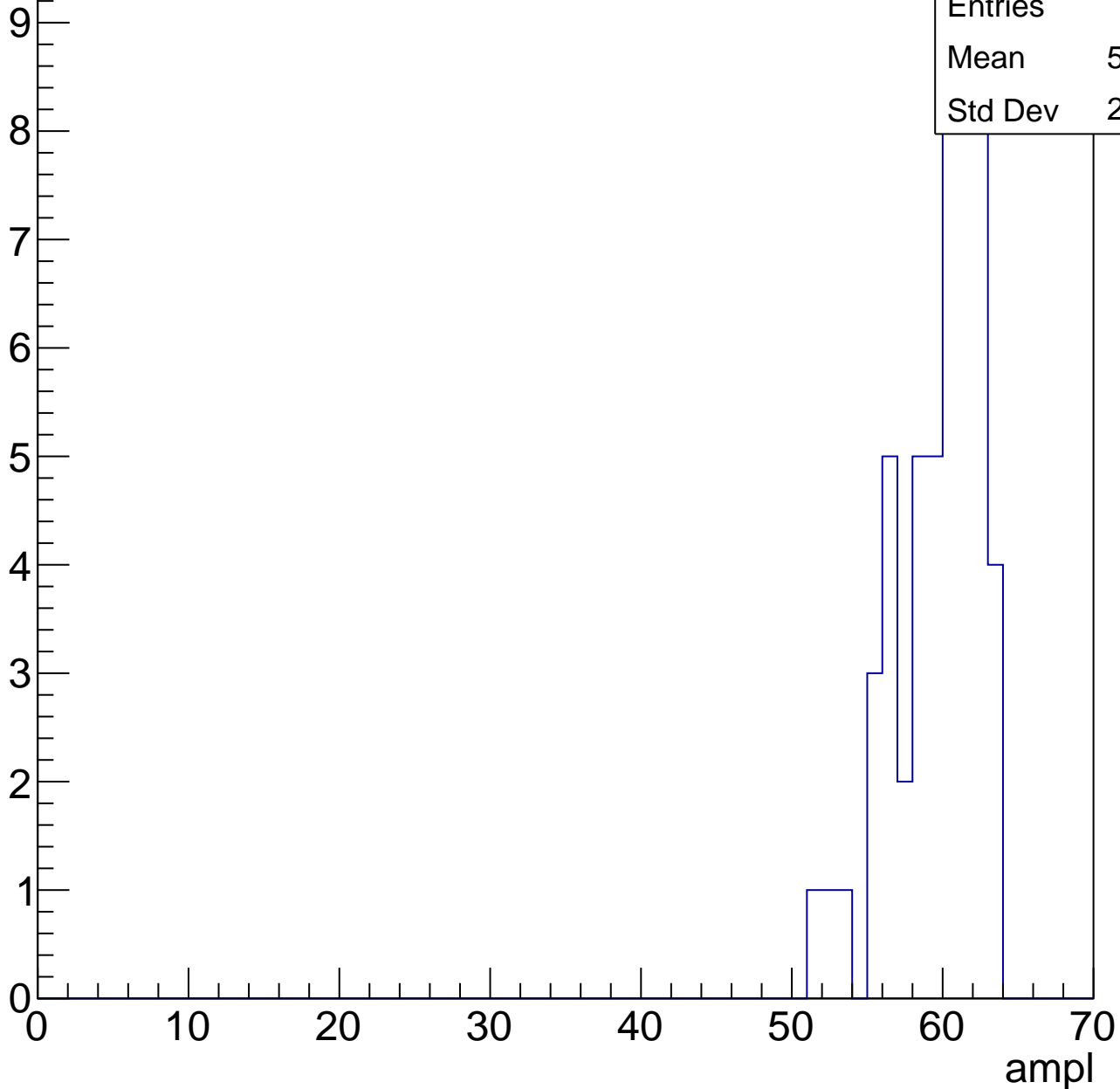


B0L002S, U17-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	59.17
Std Dev	2.887

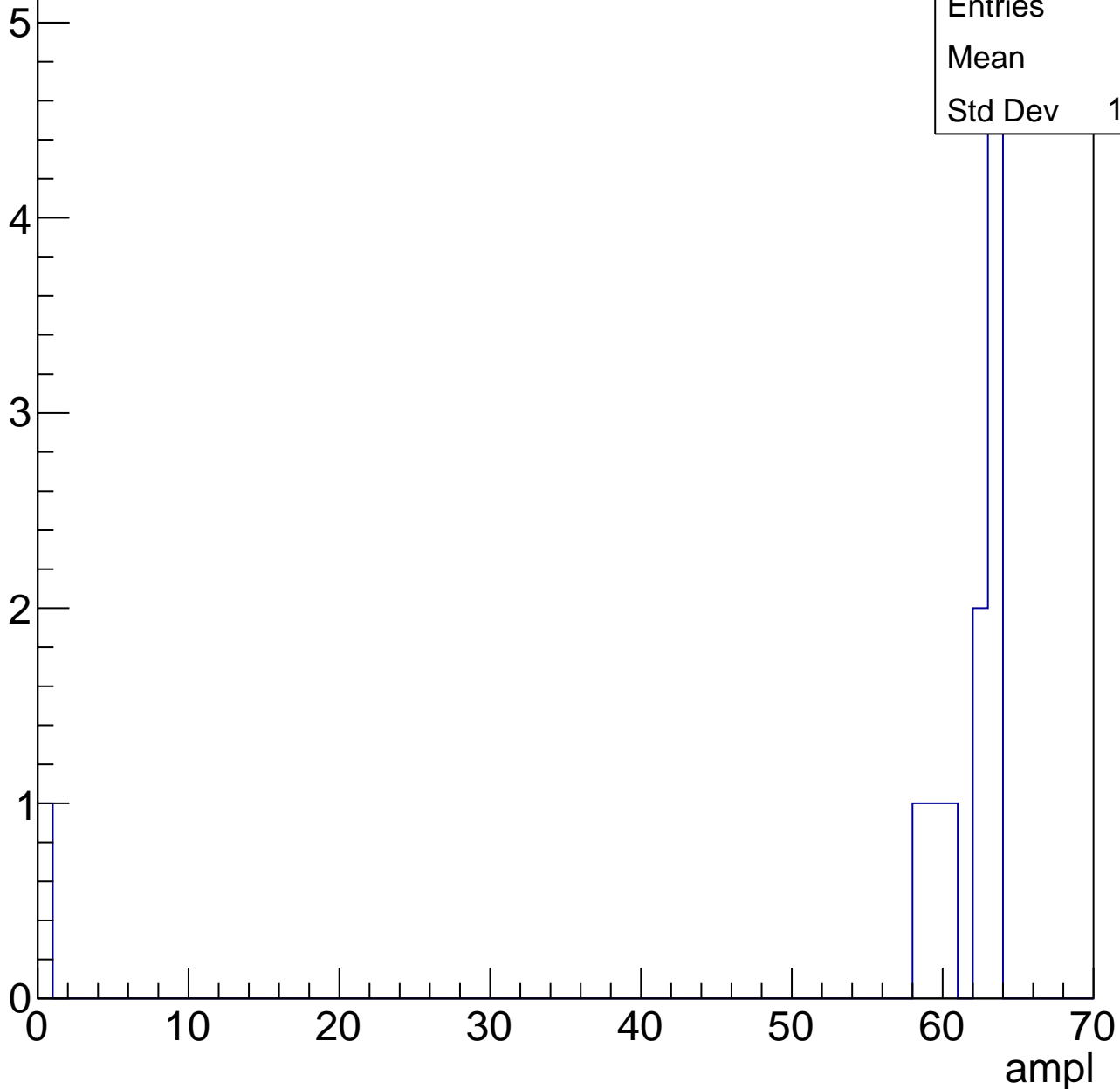


B0L002S, U17-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	11
Mean	56
Std Dev	17.79



B0L002S, U17-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch123, adc0

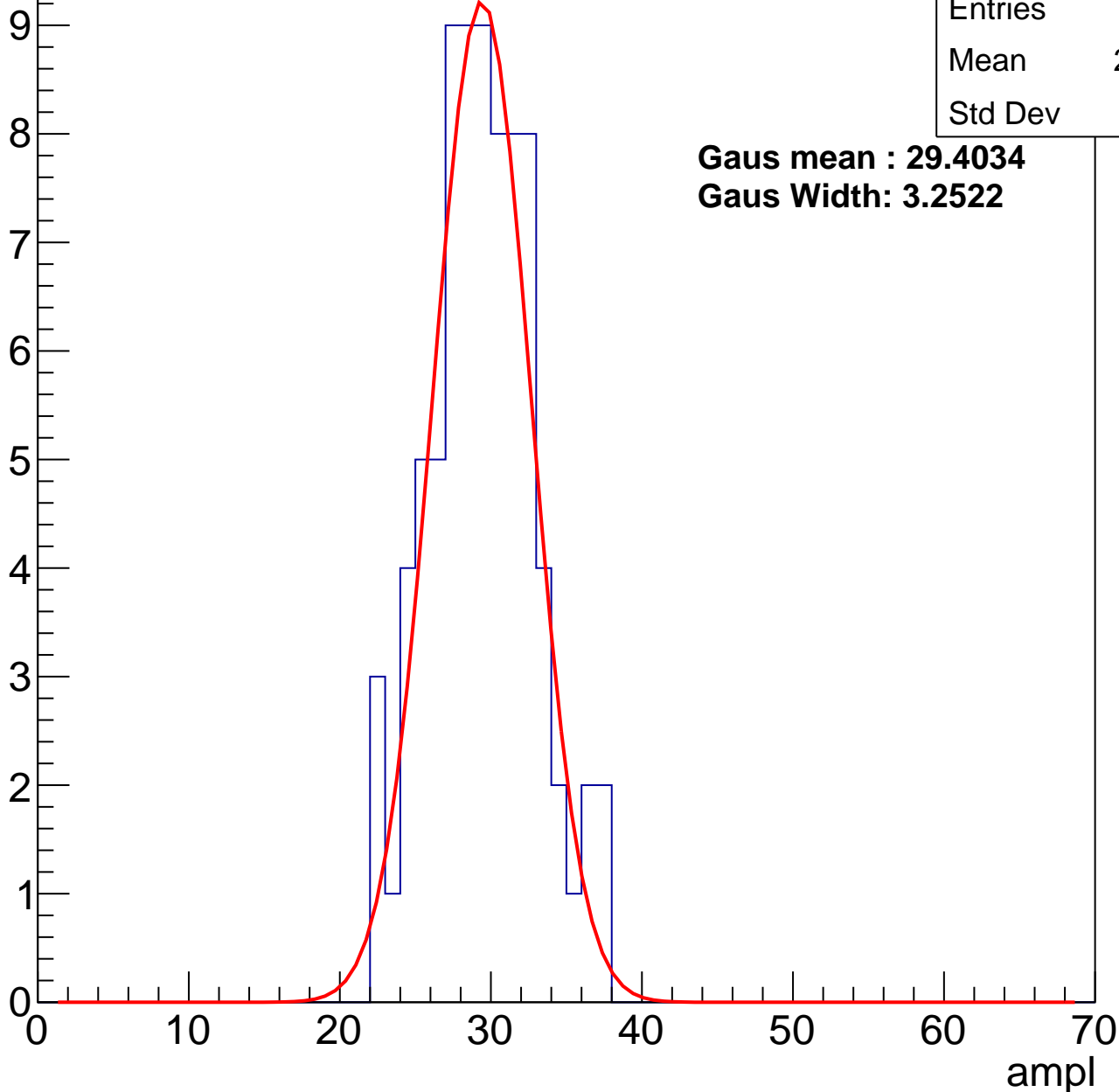
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	29.01
Std Dev	3.43

Gaus mean : 29.4034

Gaus Width: 3.2522



B0L002S, U17-ch123, adc1

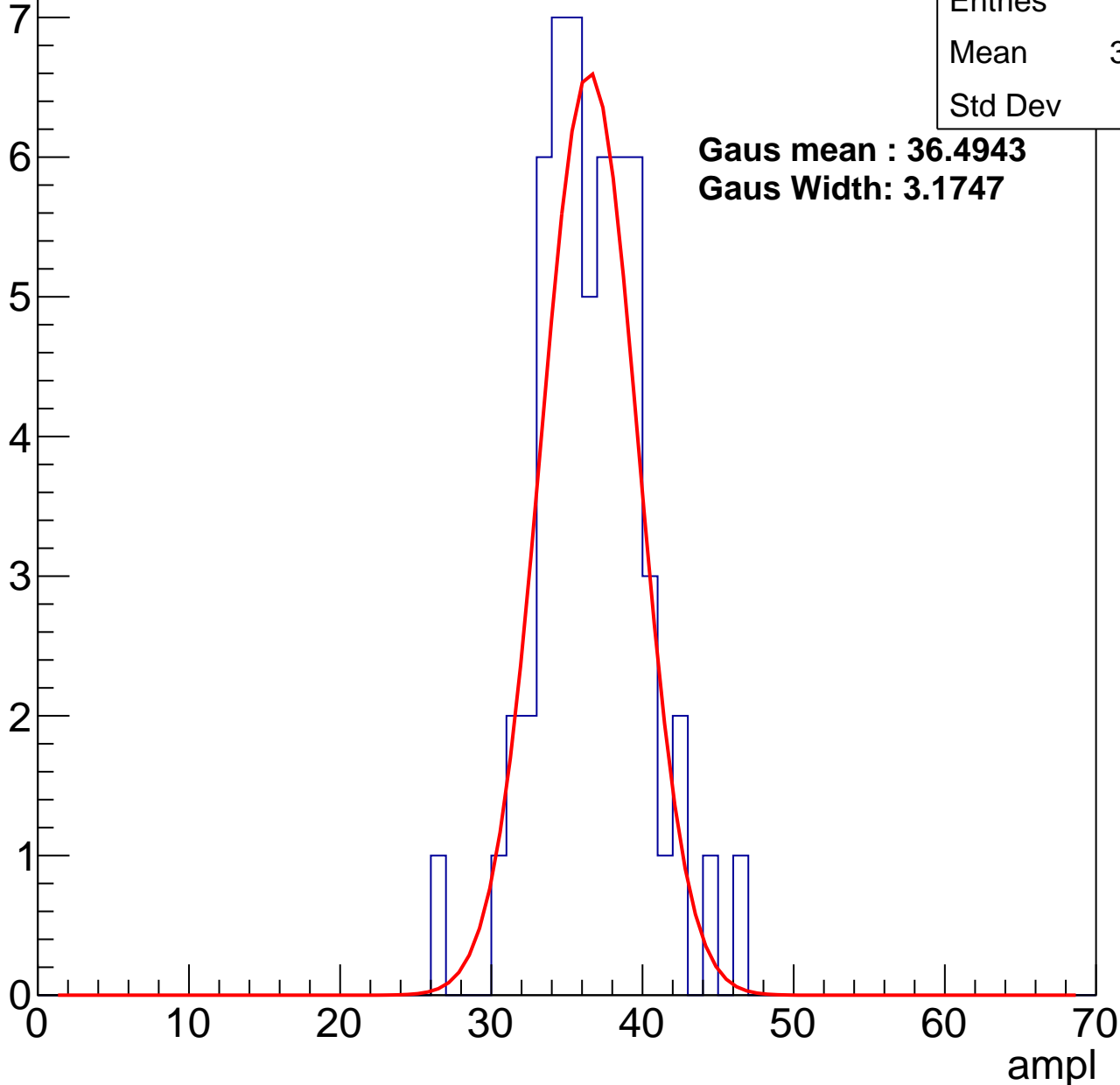
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.18
Std Dev	3.5

Gaus mean : 36.4943

Gaus Width: 3.1747



B0L002S, U17-ch123, adc2

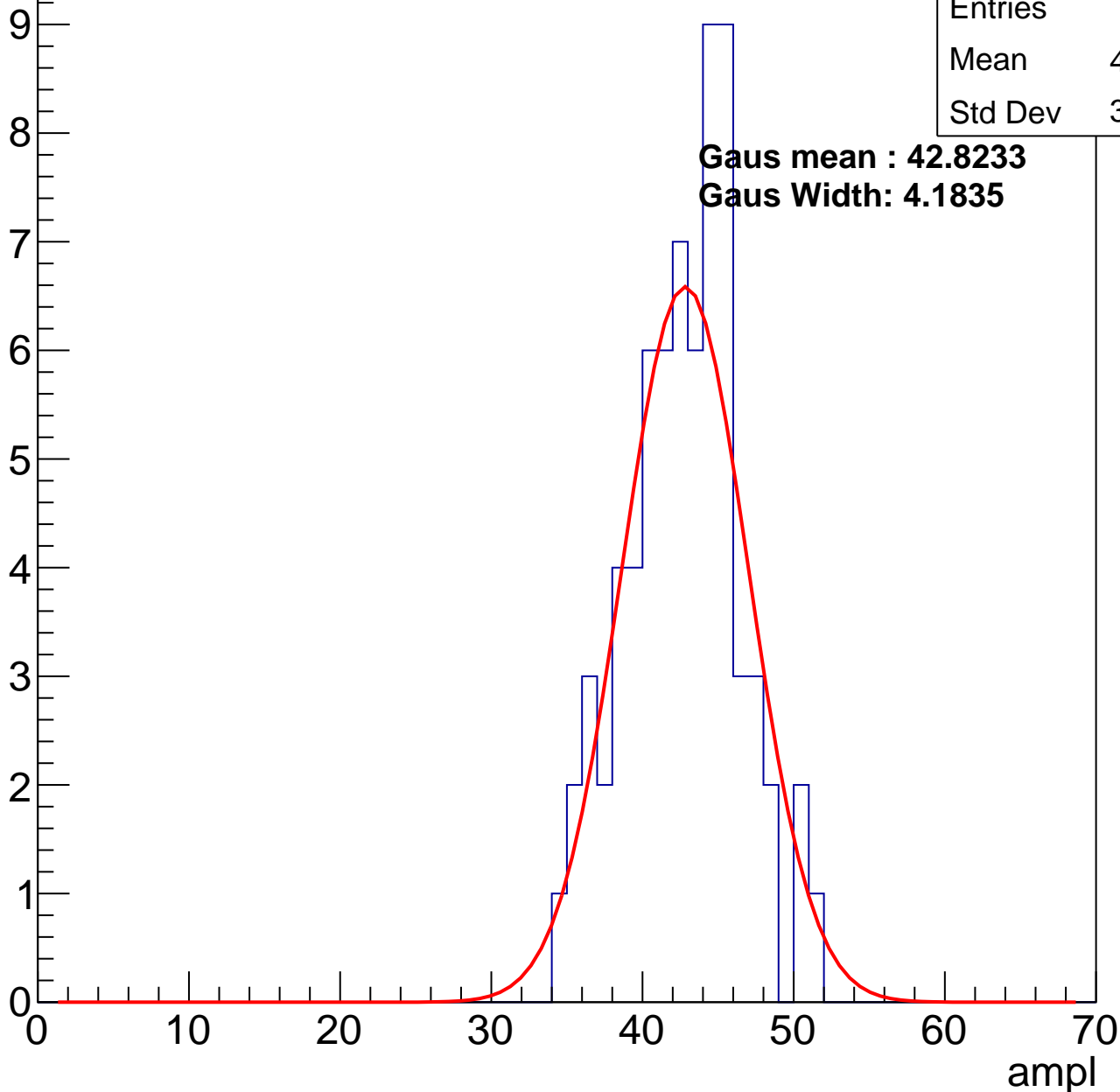
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.27
Std Dev	3.726

Gaus mean : 42.8233

Gaus Width: 4.1835

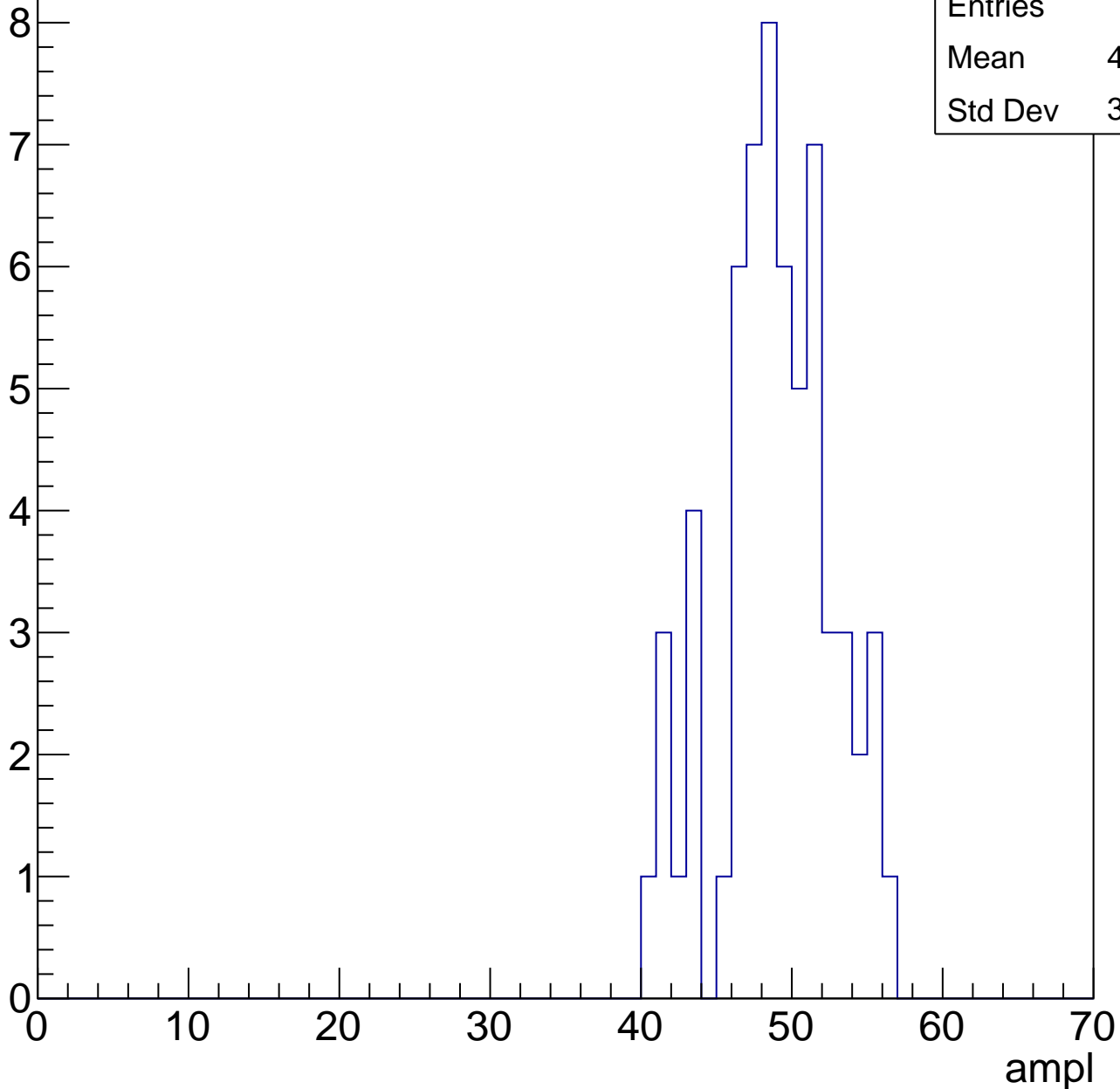


B0L002S, U17-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

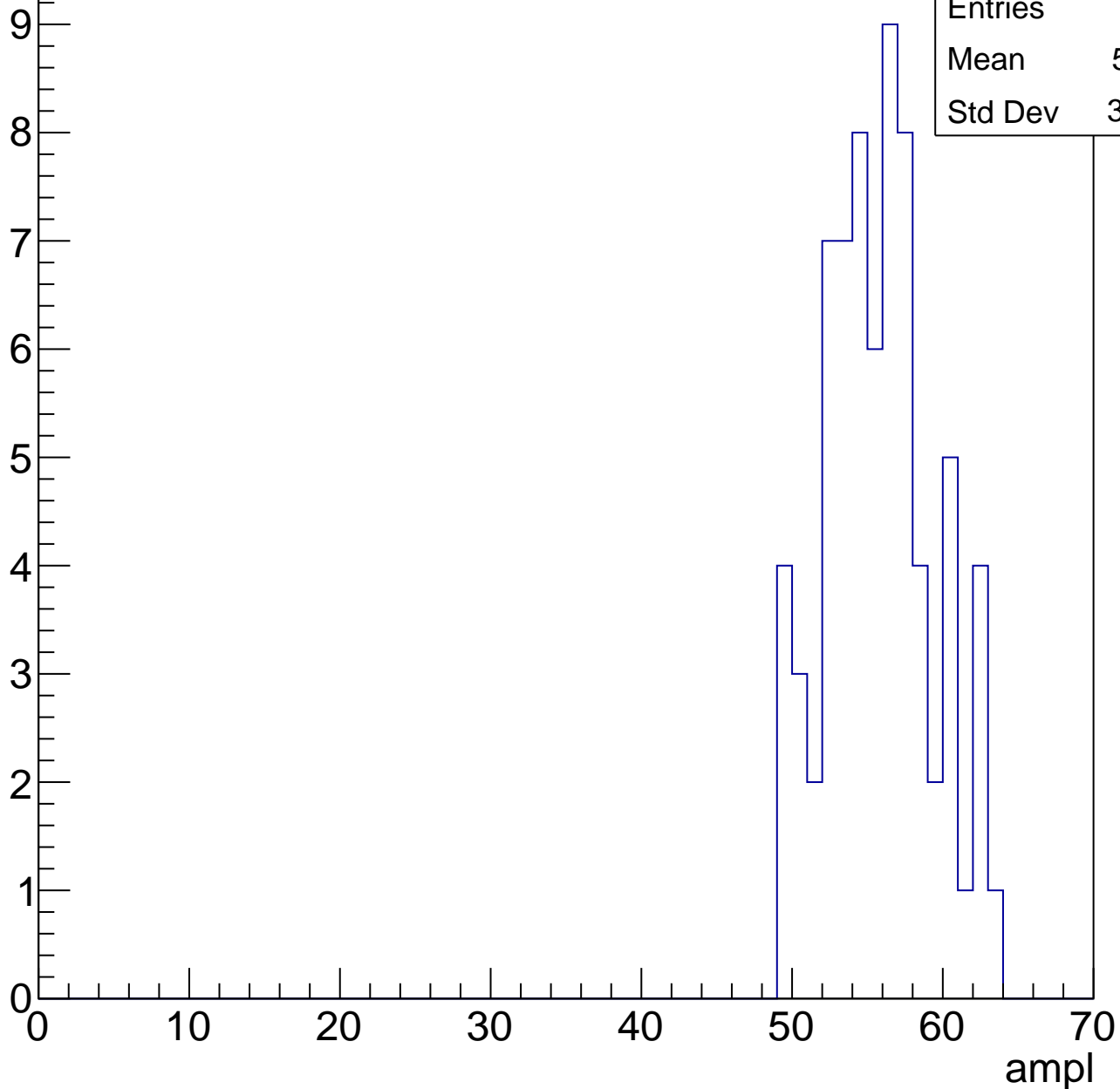
Entries	61
Mean	48.46
Std Dev	3.775



B0L002S, U17-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

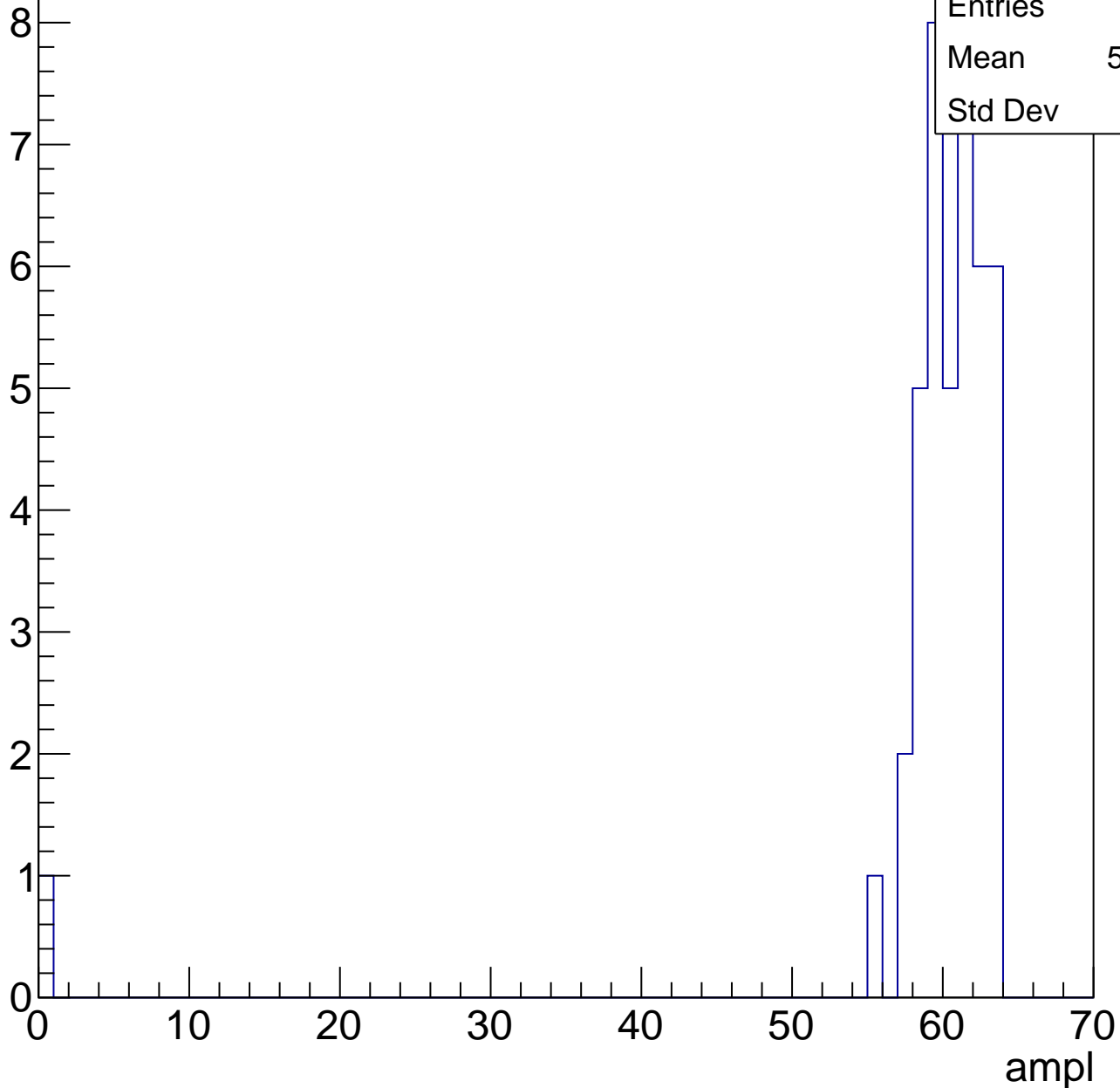


Entries	71
Mean	55.31
Std Dev	3.507

B0L002S, U17-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

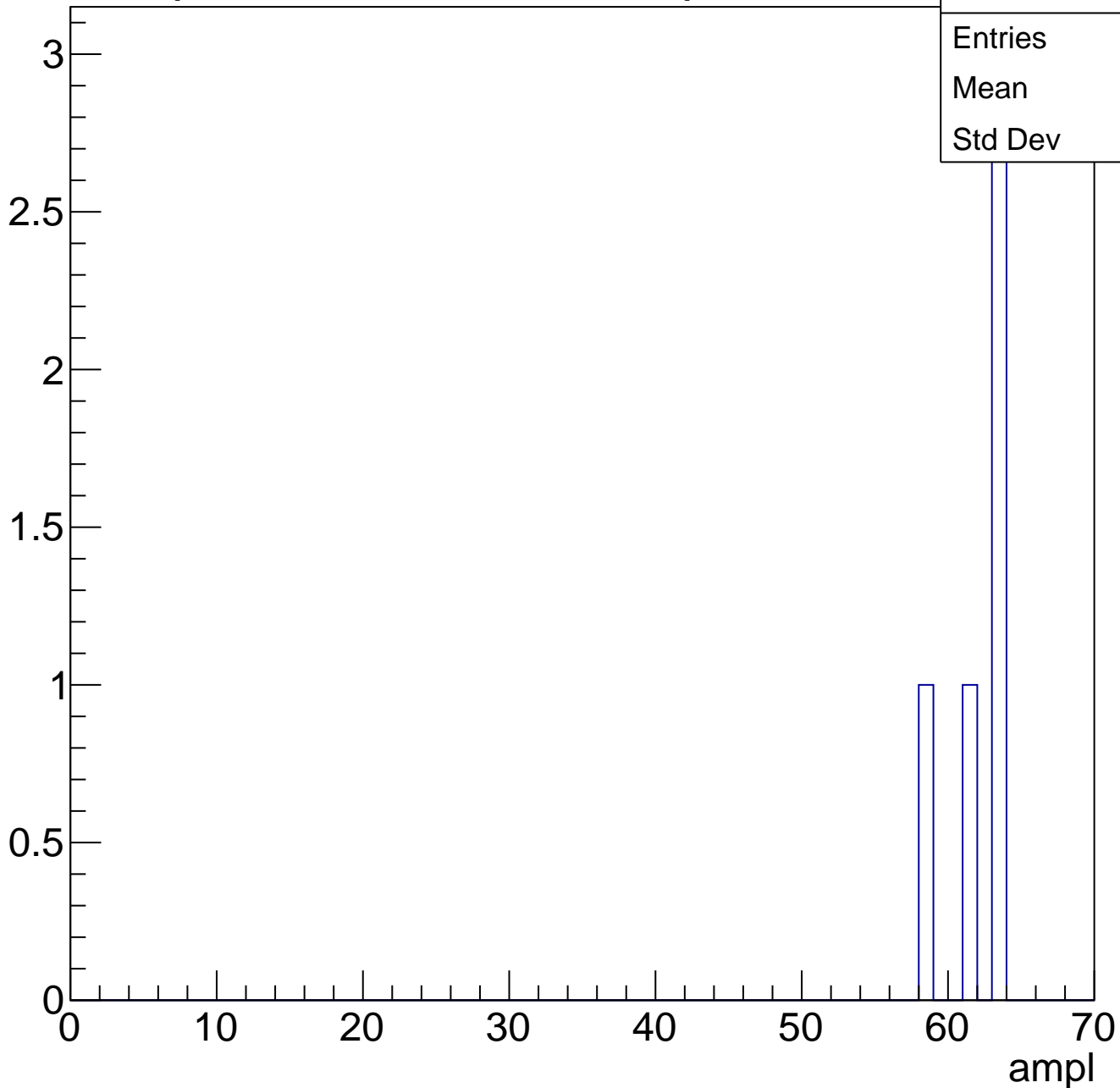
Entry



B0L002S, U17-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch124, adc0

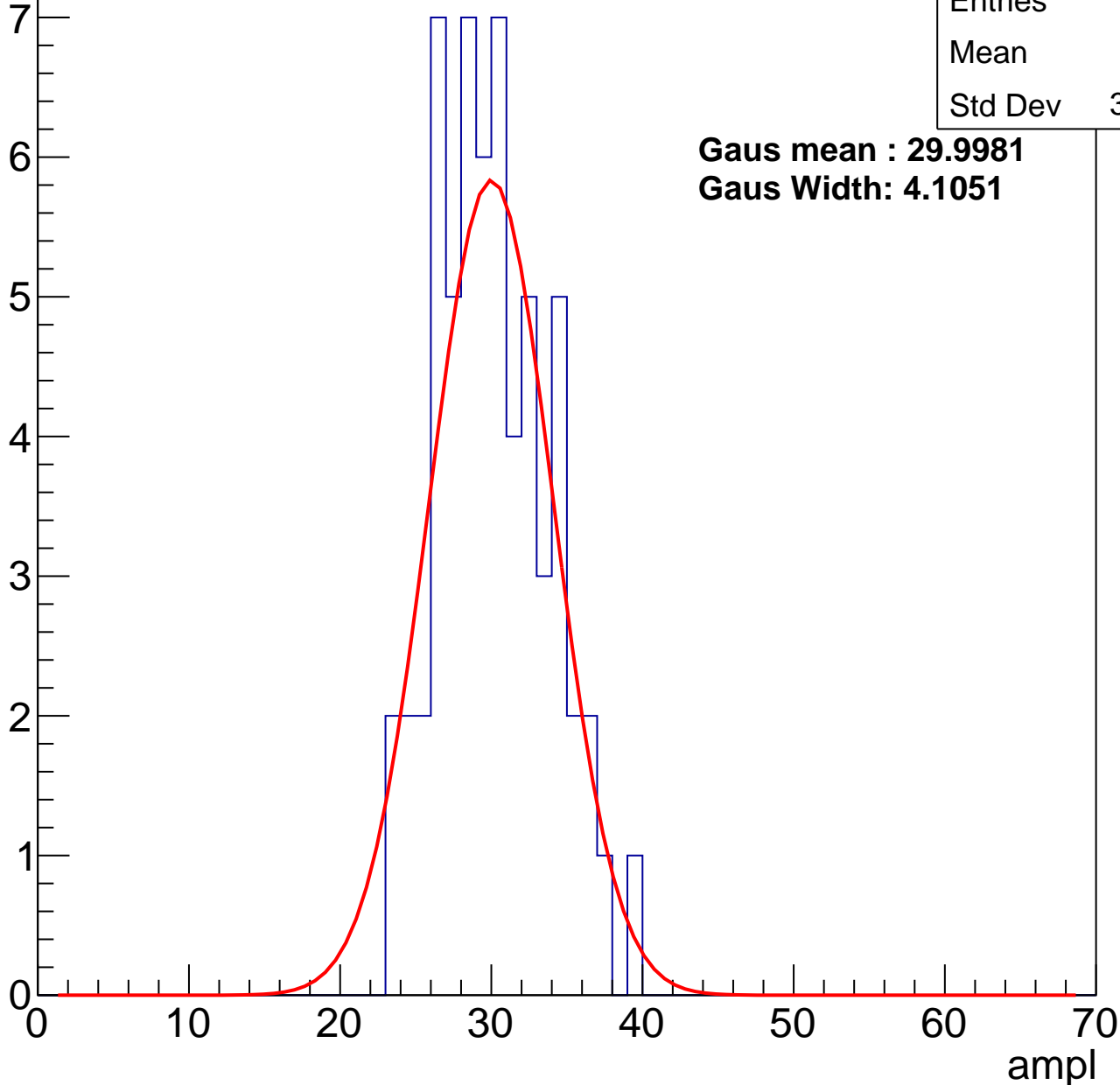
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.7
Std Dev	3.596

Gaus mean : 29.9981

Gaus Width: 4.1051



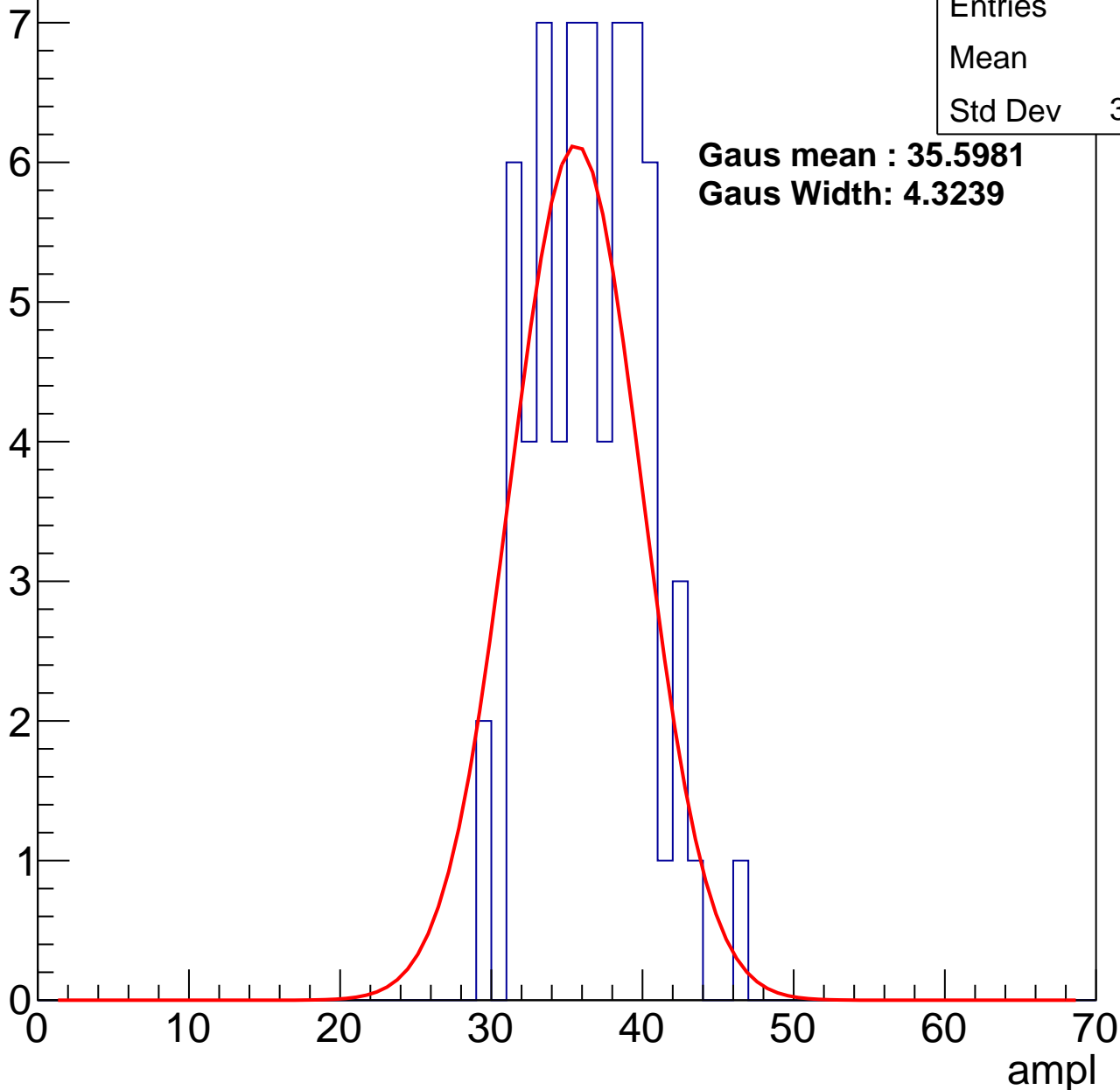
B0L002S, U17-ch124, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.1
Std Dev	3.604

Gaus mean : 35.5981
Gaus Width: 4.3239



B0L002S, U17-ch124, adc2

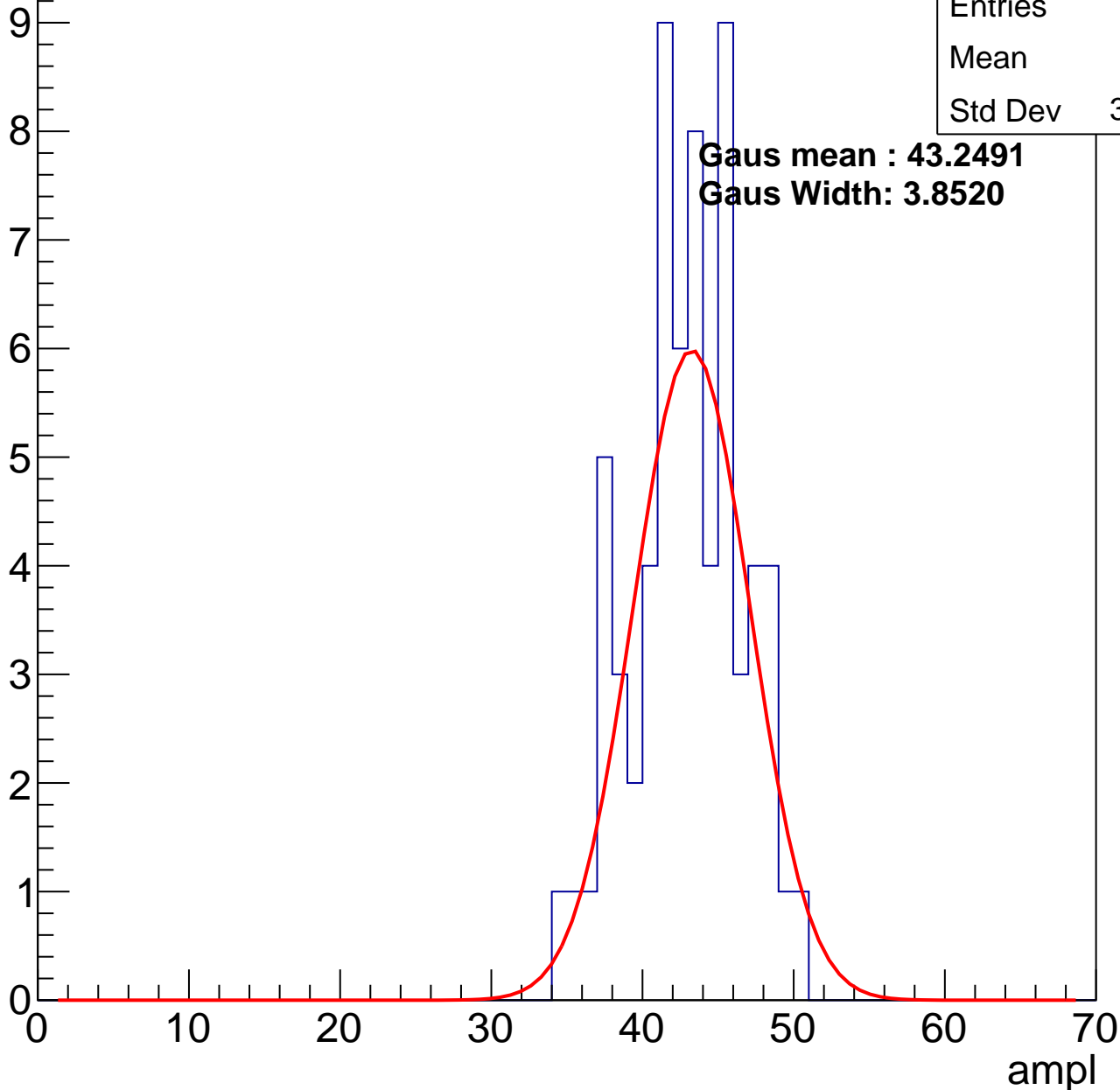
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.5
Std Dev	3.607

Gaus mean : 43.2491

Gaus Width: 3.8520

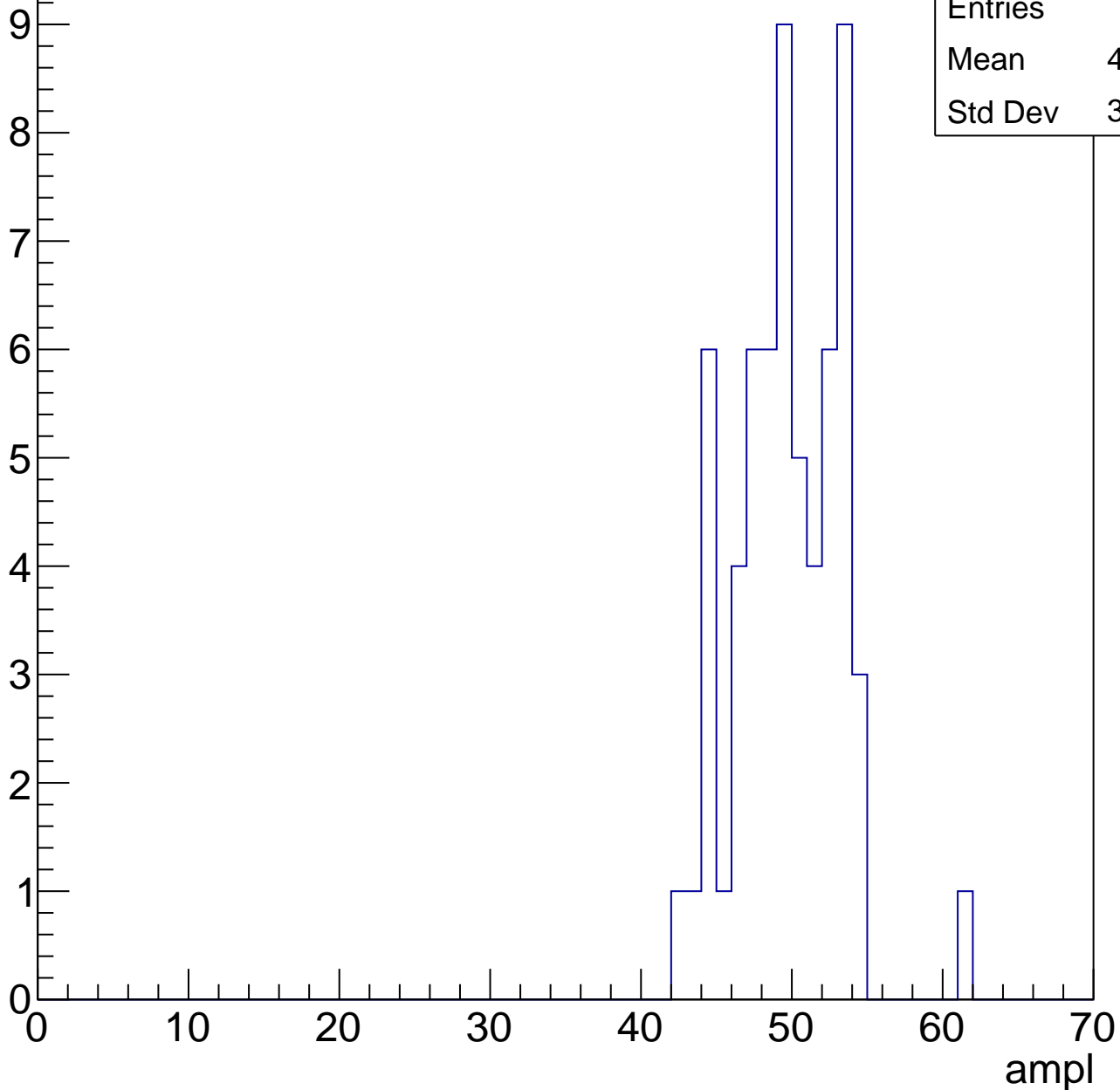


B0L002S, U17-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	49.27
Std Dev	3.479

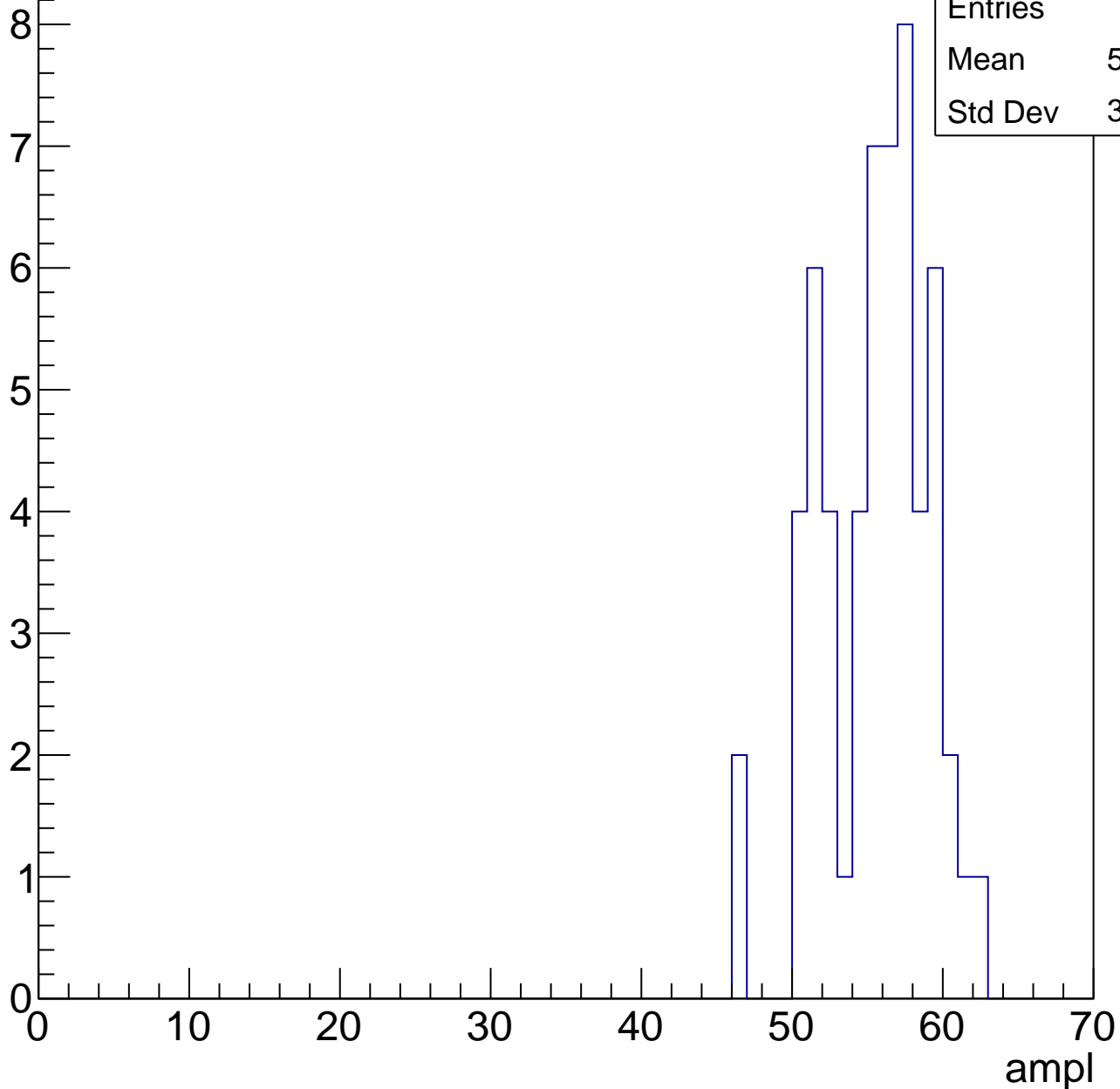


B0L002S, U17-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	55.04
Std Dev	3.524

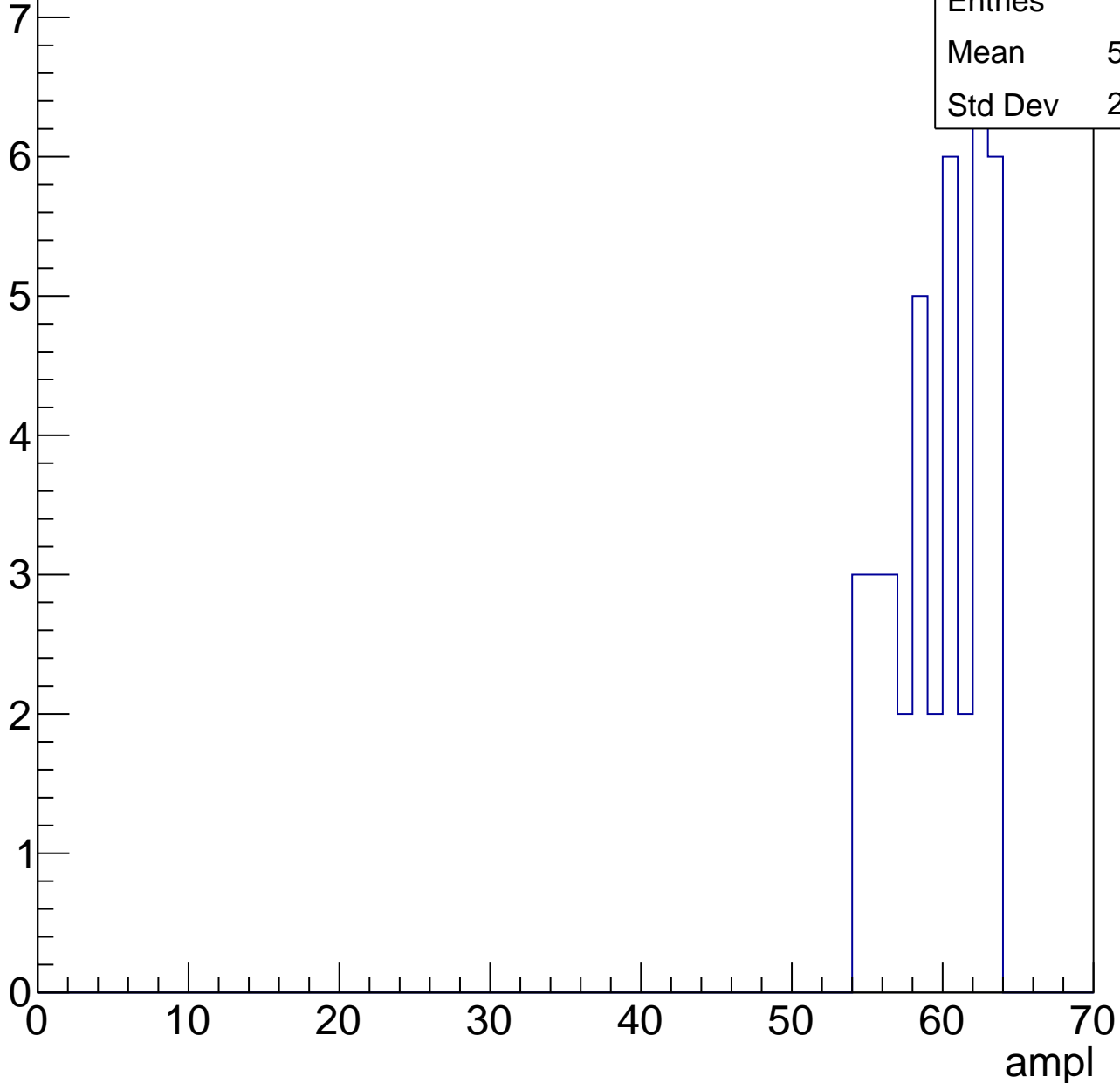


B0L002S, U17-ch124, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.26
Std Dev	2.924

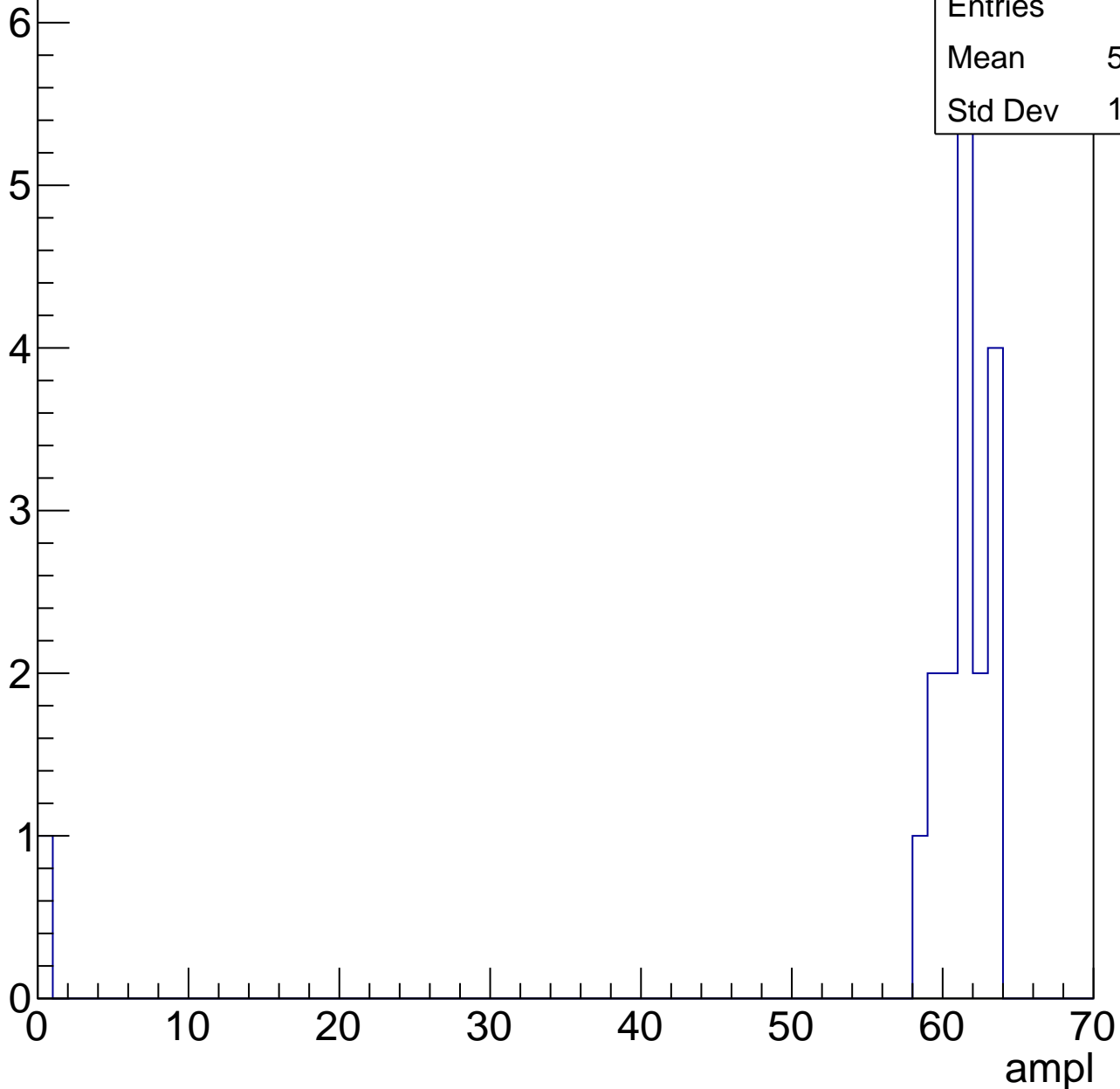


B0L002S, U17-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	18
Mean	57.67
Std Dev	14.06



B0L002S, U17-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch125, adc0

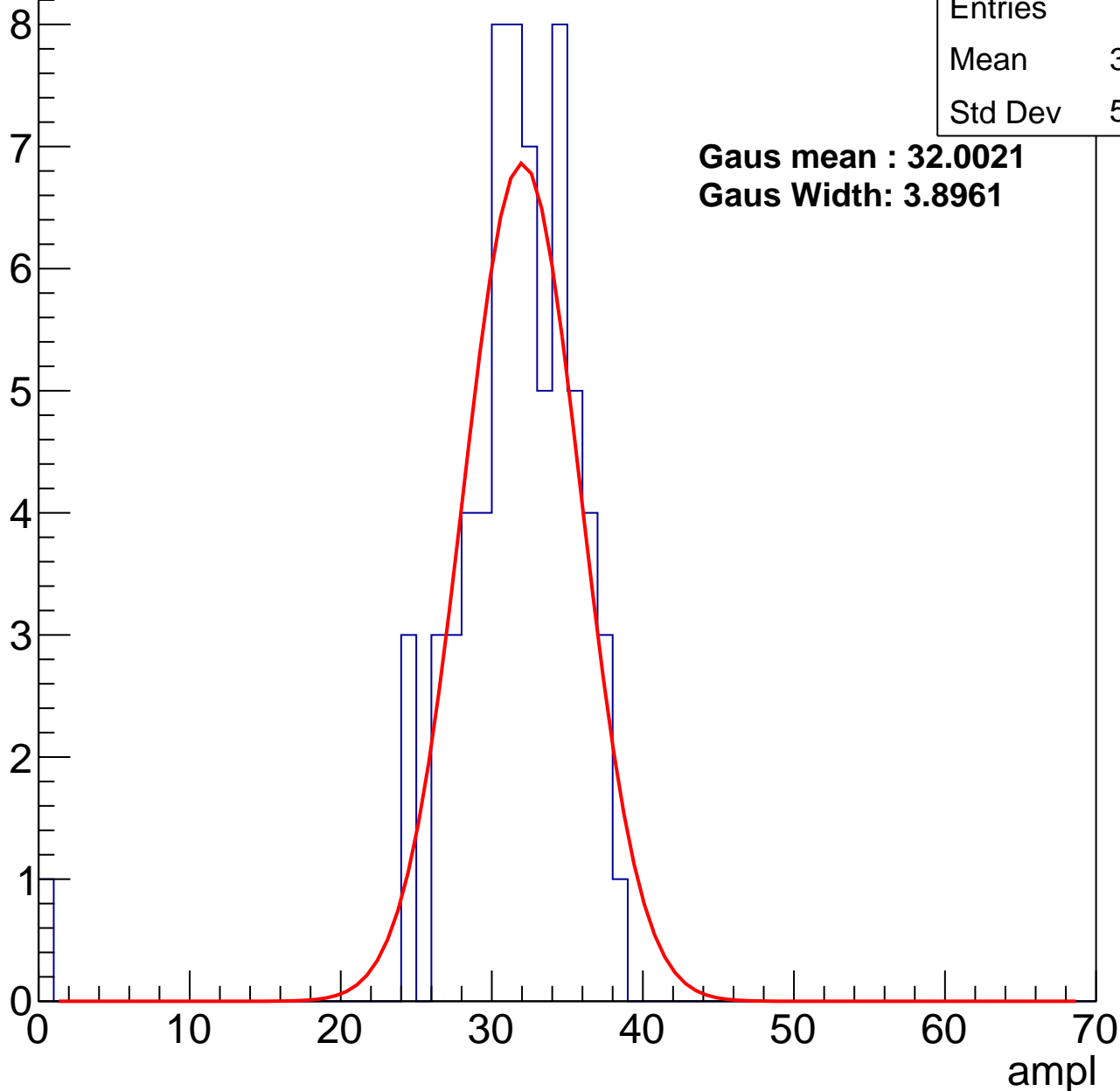
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.99
Std Dev	5.077

Gaus mean : 32.0021

Gaus Width: 3.8961



B0L002S, U17-ch125, adc1

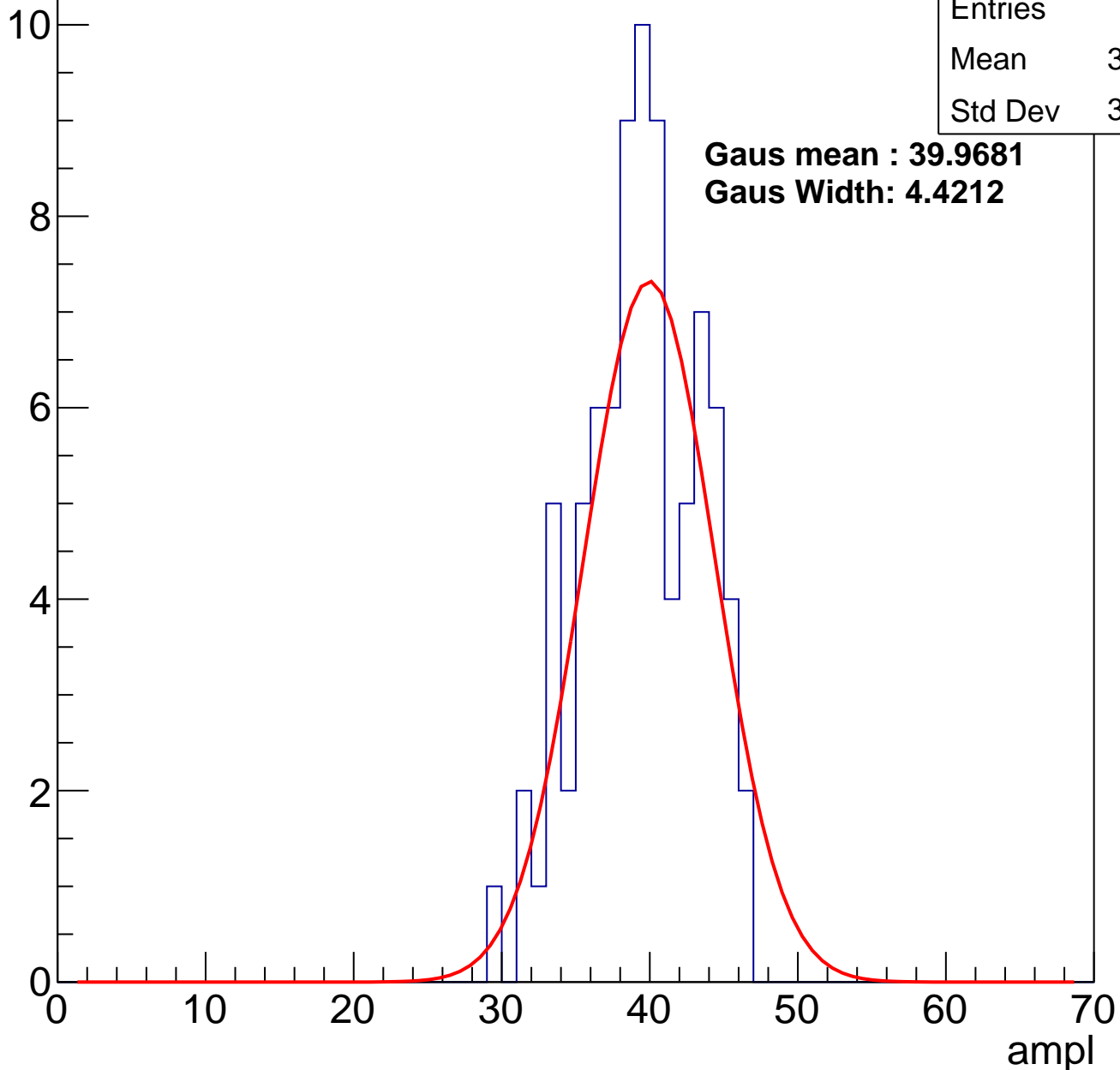
calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	38.95
Std Dev	3.854

Gaus mean : 39.9681

Gaus Width: 4.4212

Entry



B0L002S, U17-ch125, adc2

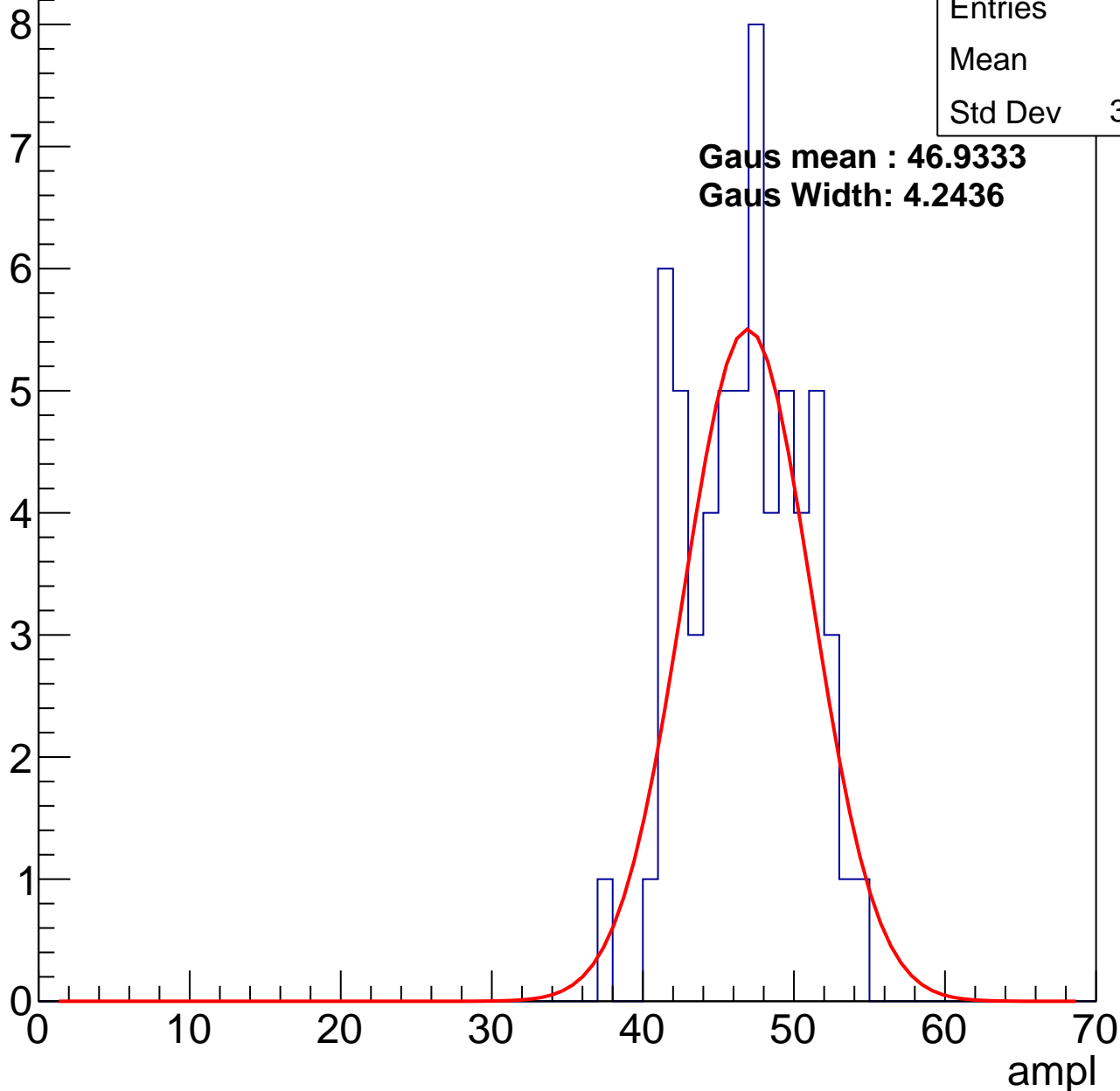
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	46.3
Std Dev	3.774

Gaus mean : 46.9333

Gaus Width: 4.2436

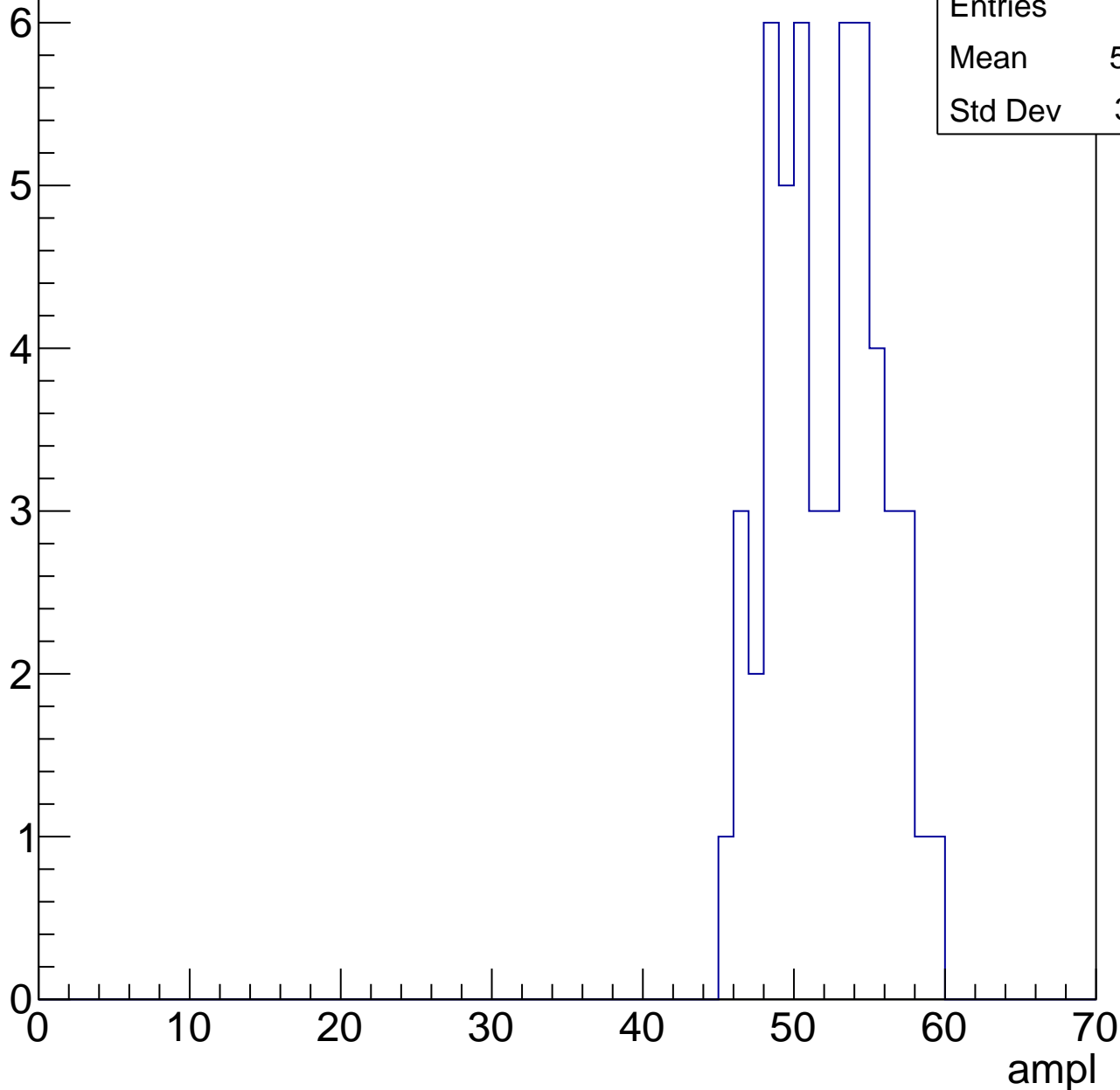


B0L002S, U17-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	51.64
Std Dev	3.481

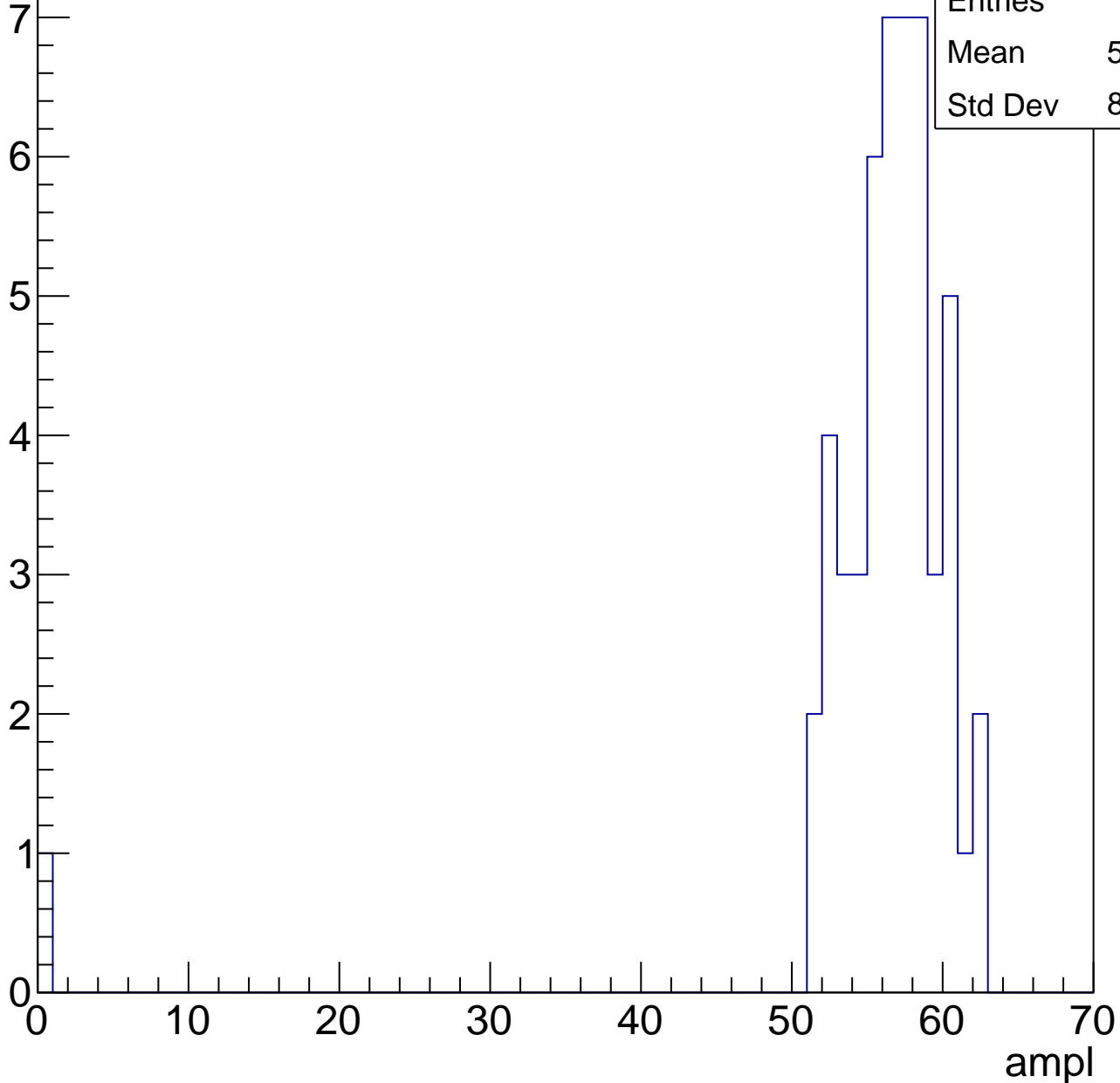


B0L002S, U17-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

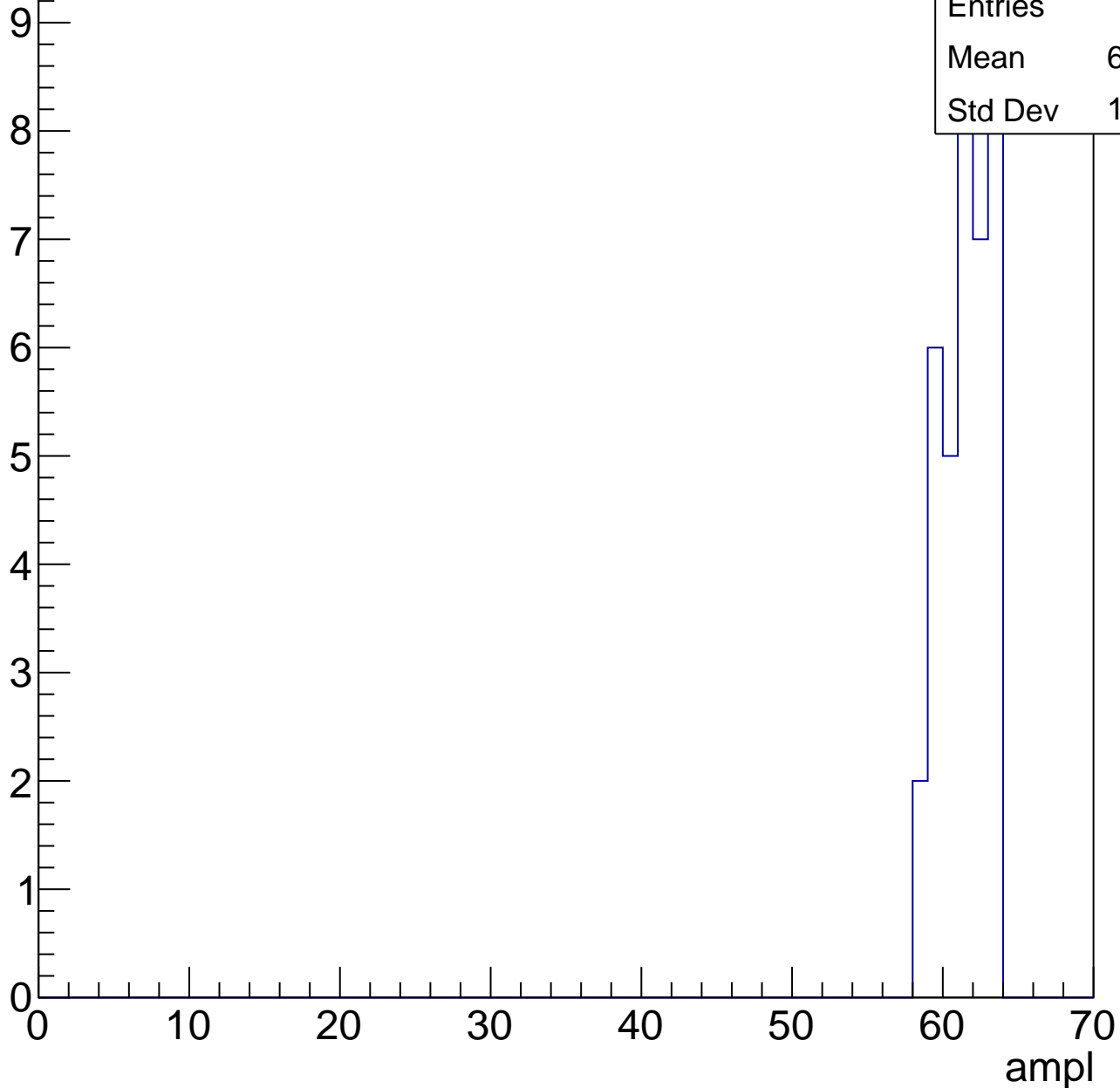
Entries	51
Mean	55.29
Std Dev	8.294



B0L002S, U17-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U17-ch126, adc0

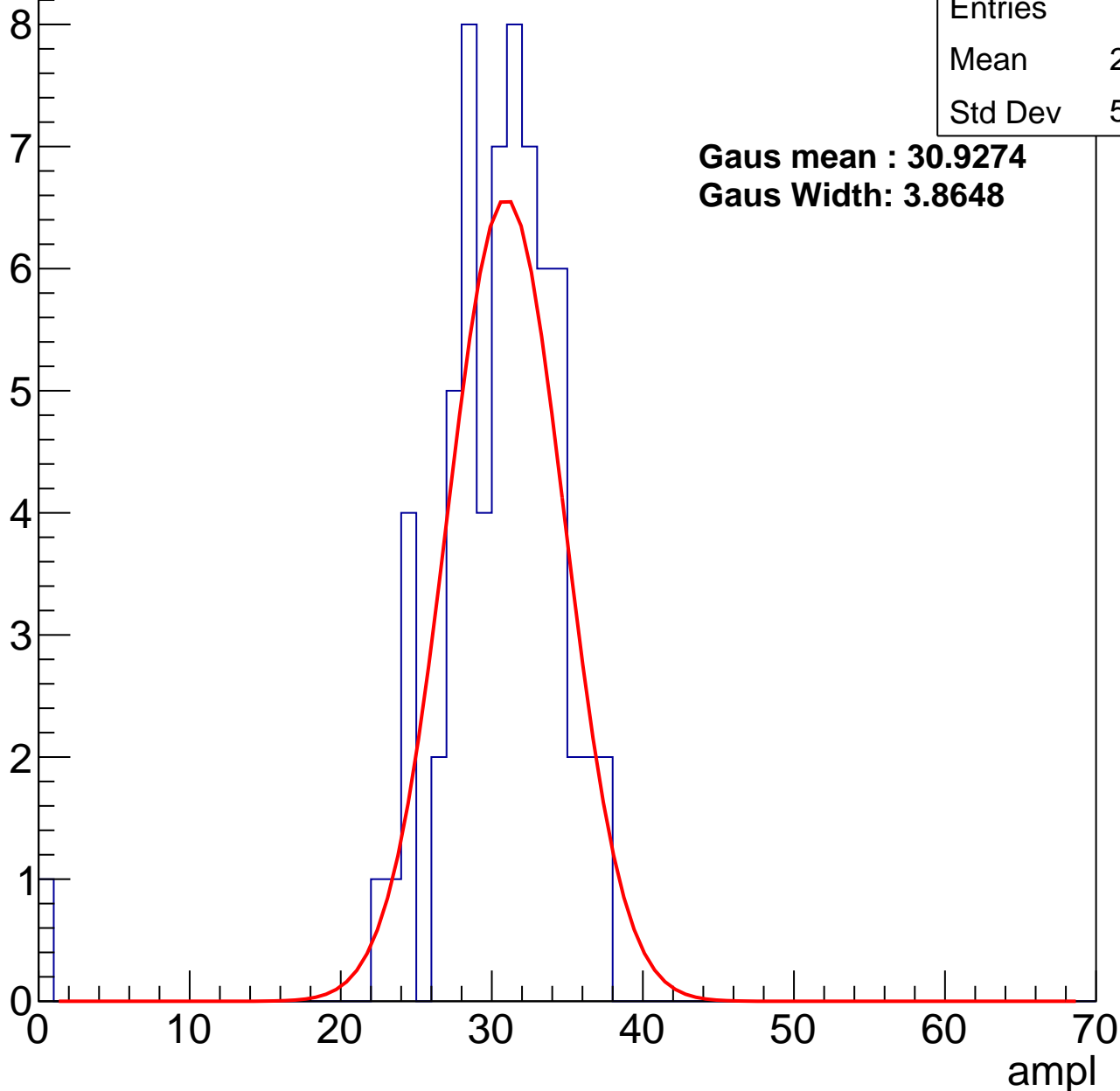
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.82
Std Dev	5.036

Gaus mean : 30.9274

Gaus Width: 3.8648



B0L002S, U17-ch126, adc1

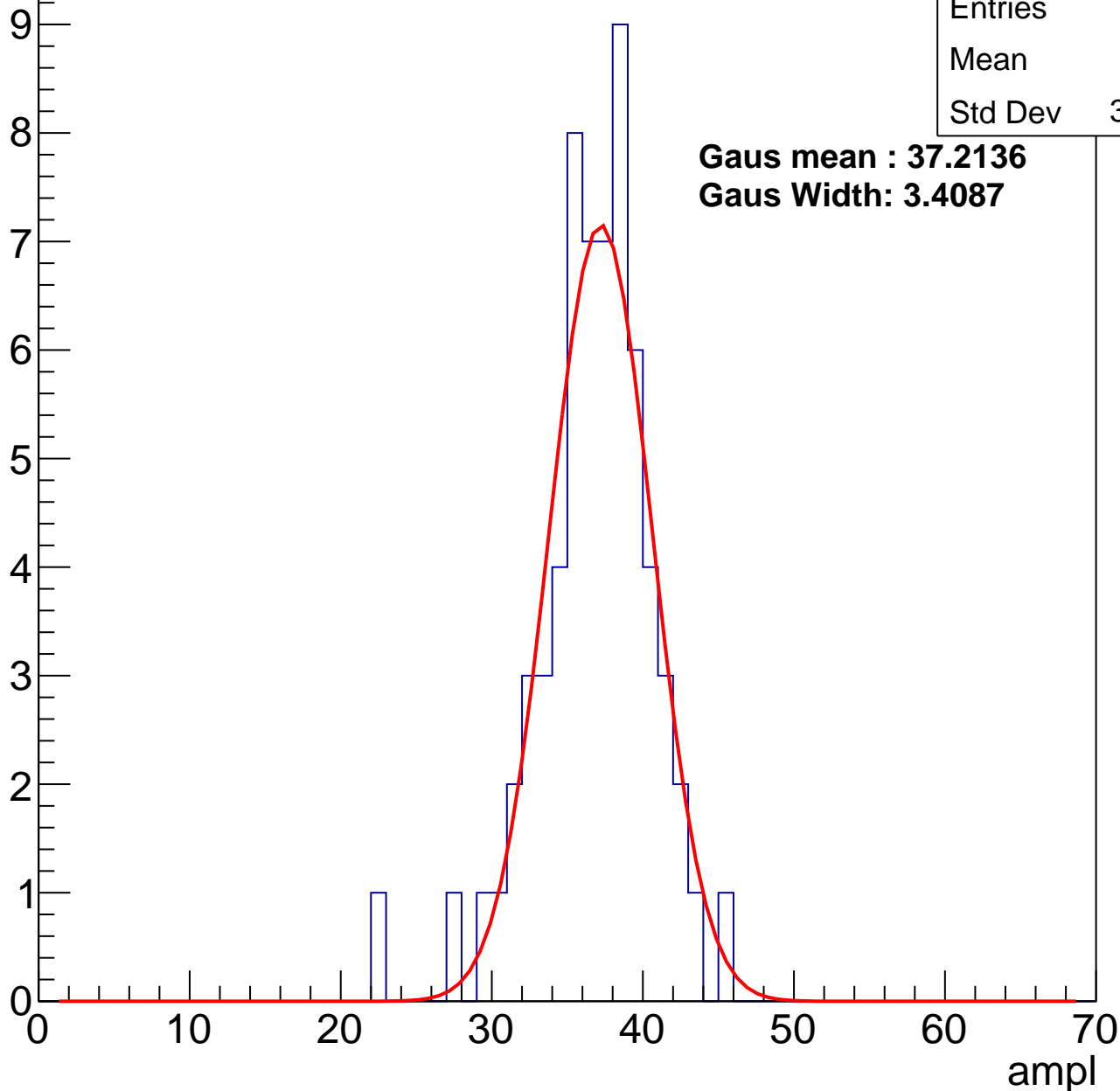
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.3
Std Dev	3.839

Gaus mean : 37.2136

Gaus Width: 3.4087



B0L002S, U17-ch126, adc2

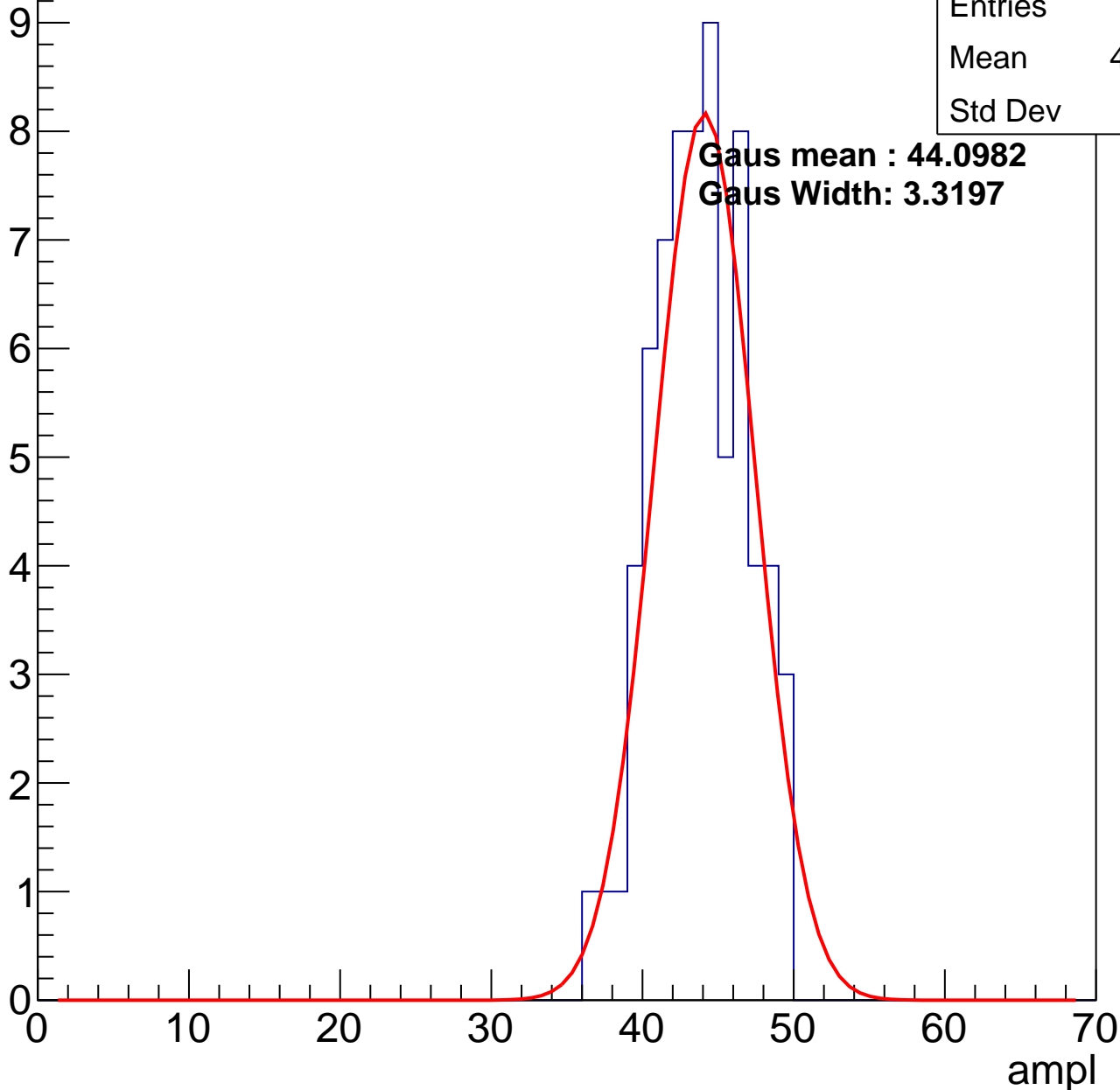
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.33
Std Dev	3.02

Gaus mean : 44.0982

Gaus Width: 3.3197

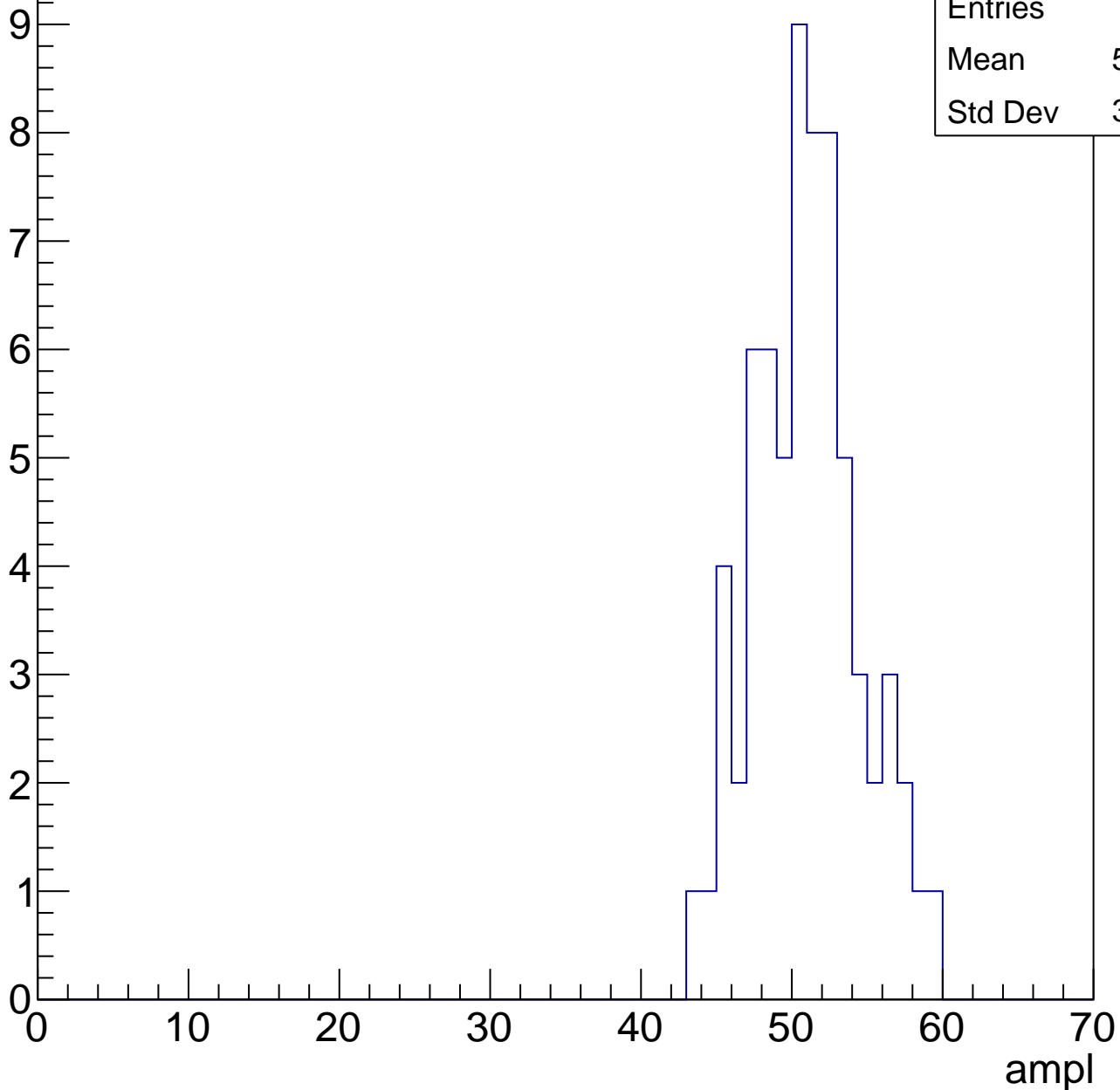


B0L002S, U17-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.51
Std Dev	3.491

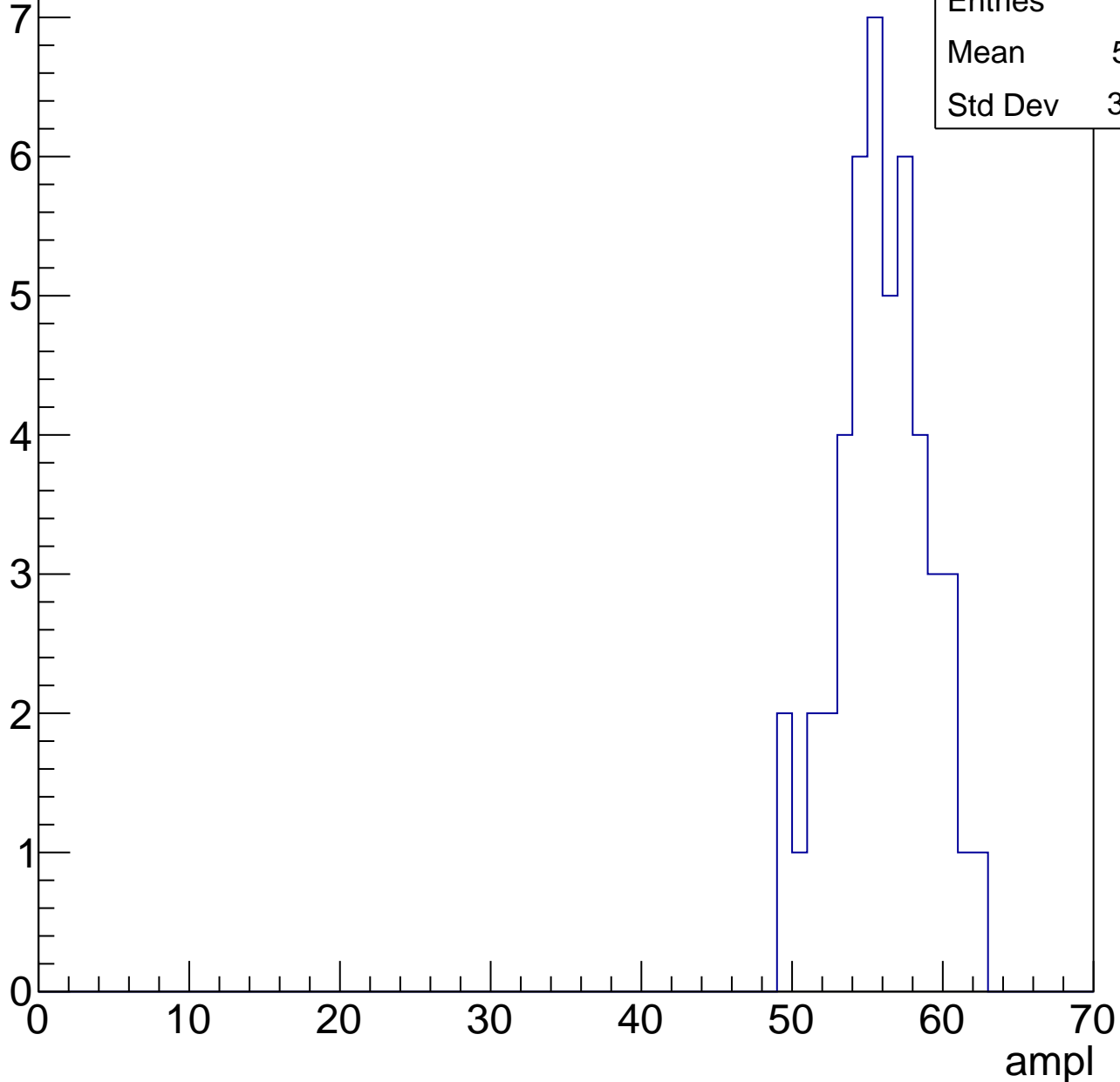


B0L002S, U17-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	55.51
Std Dev	3.024

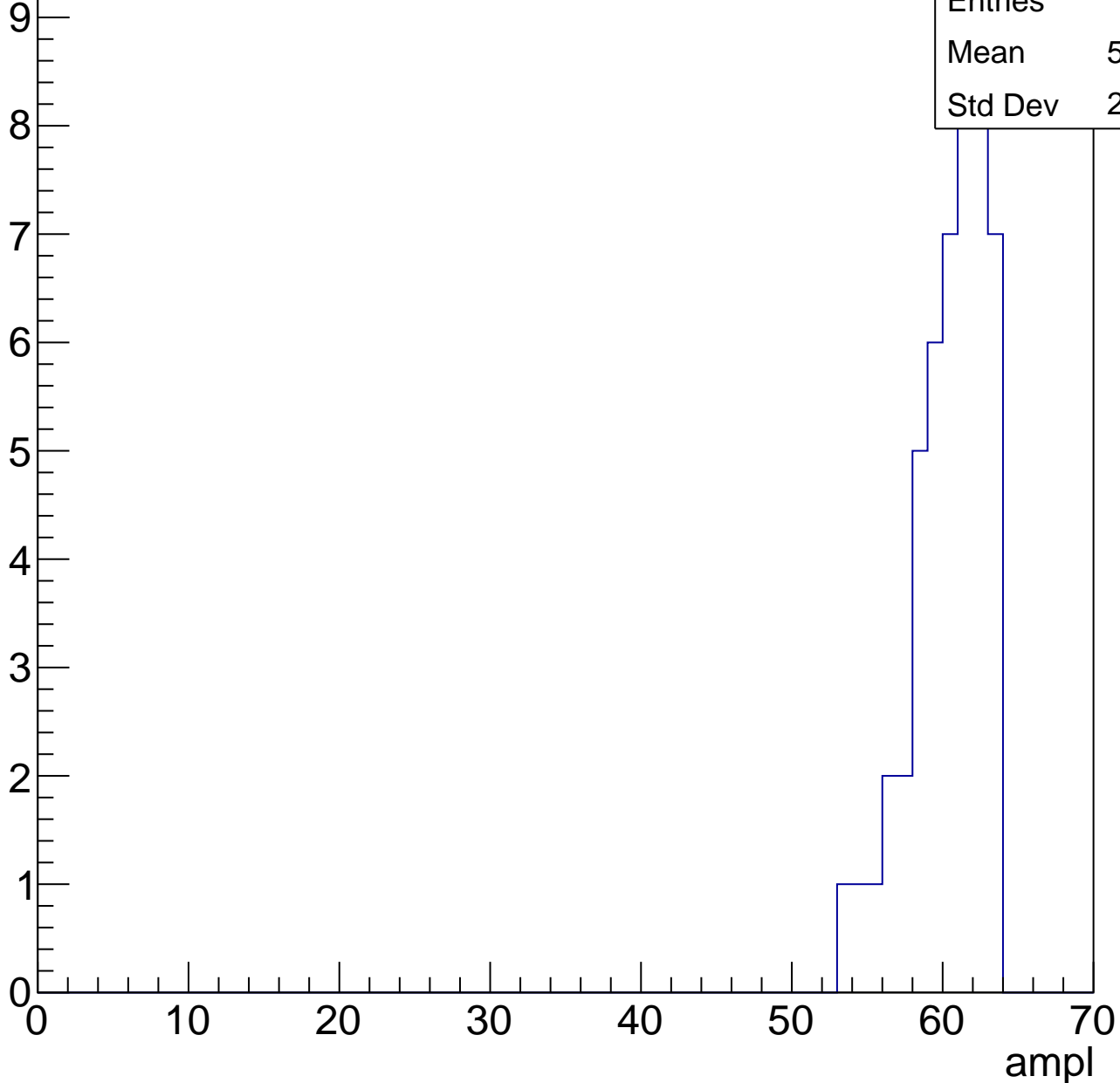


B0L002S, U17-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

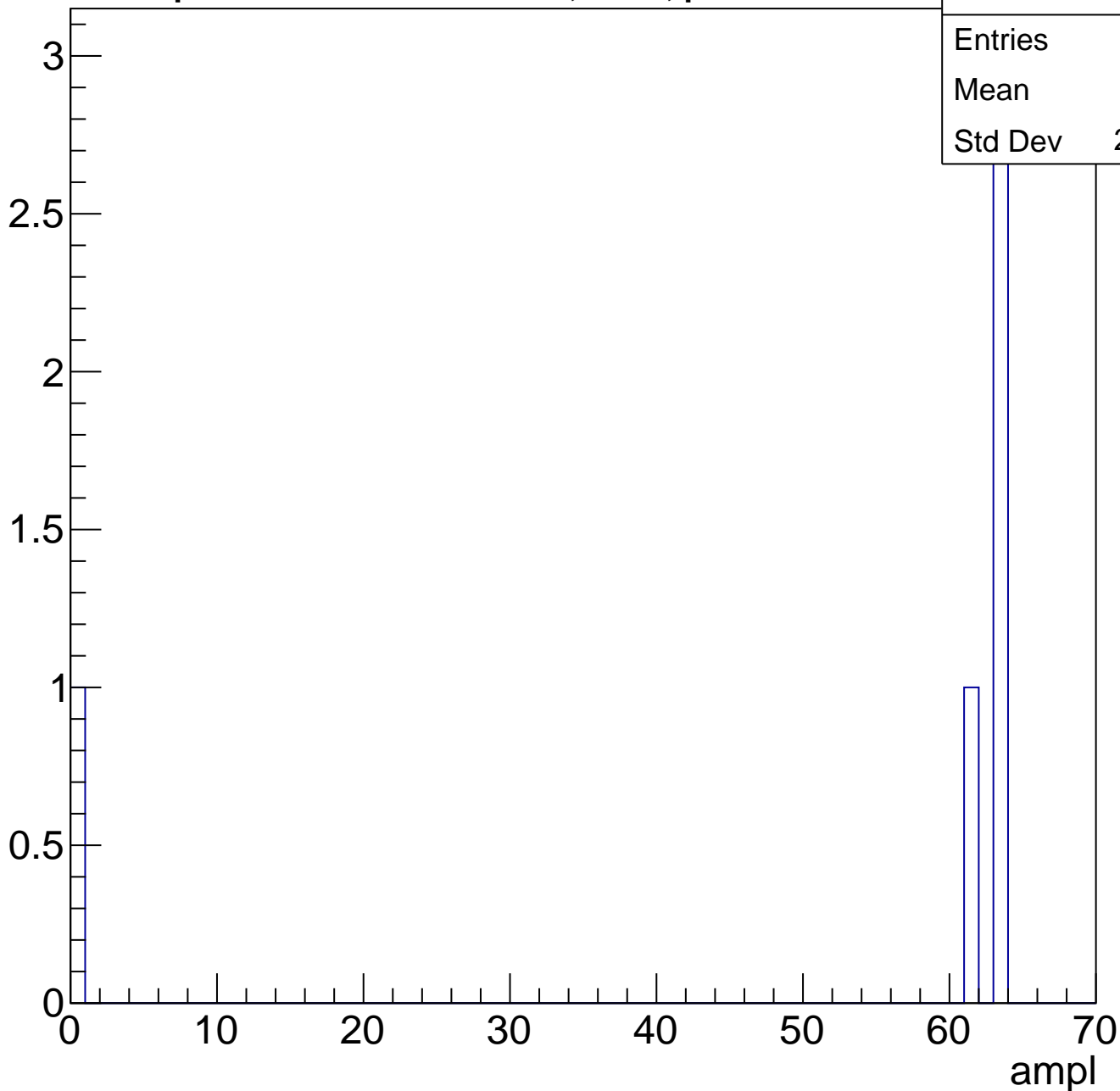
Entries	49
Mean	59.98
Std Dev	2.445



B0L002S, U17-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U17-ch127, adc0

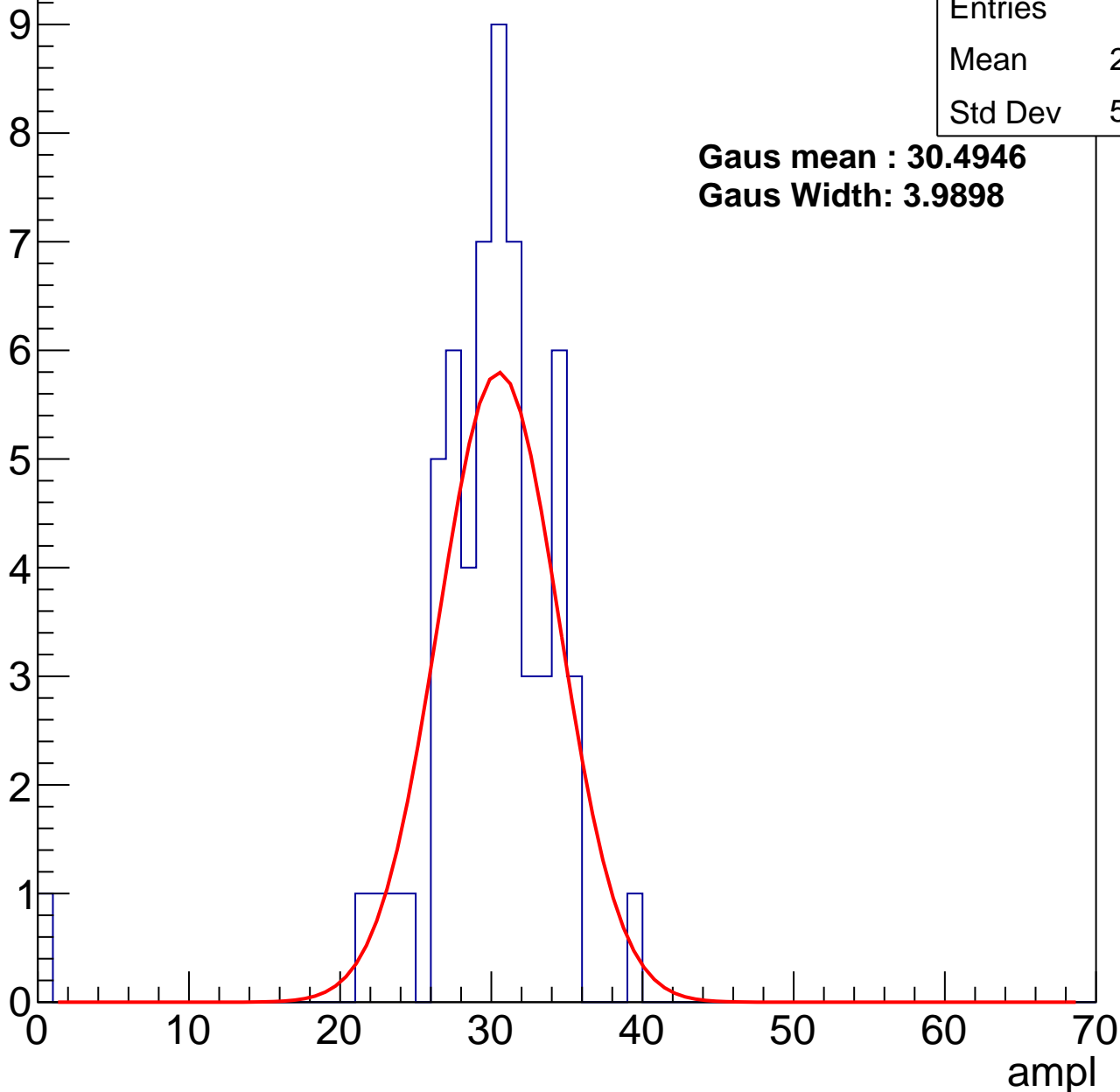
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.27
Std Dev	5.132

Gaus mean : 30.4946

Gaus Width: 3.9898



B0L002S, U17-ch127, adc1

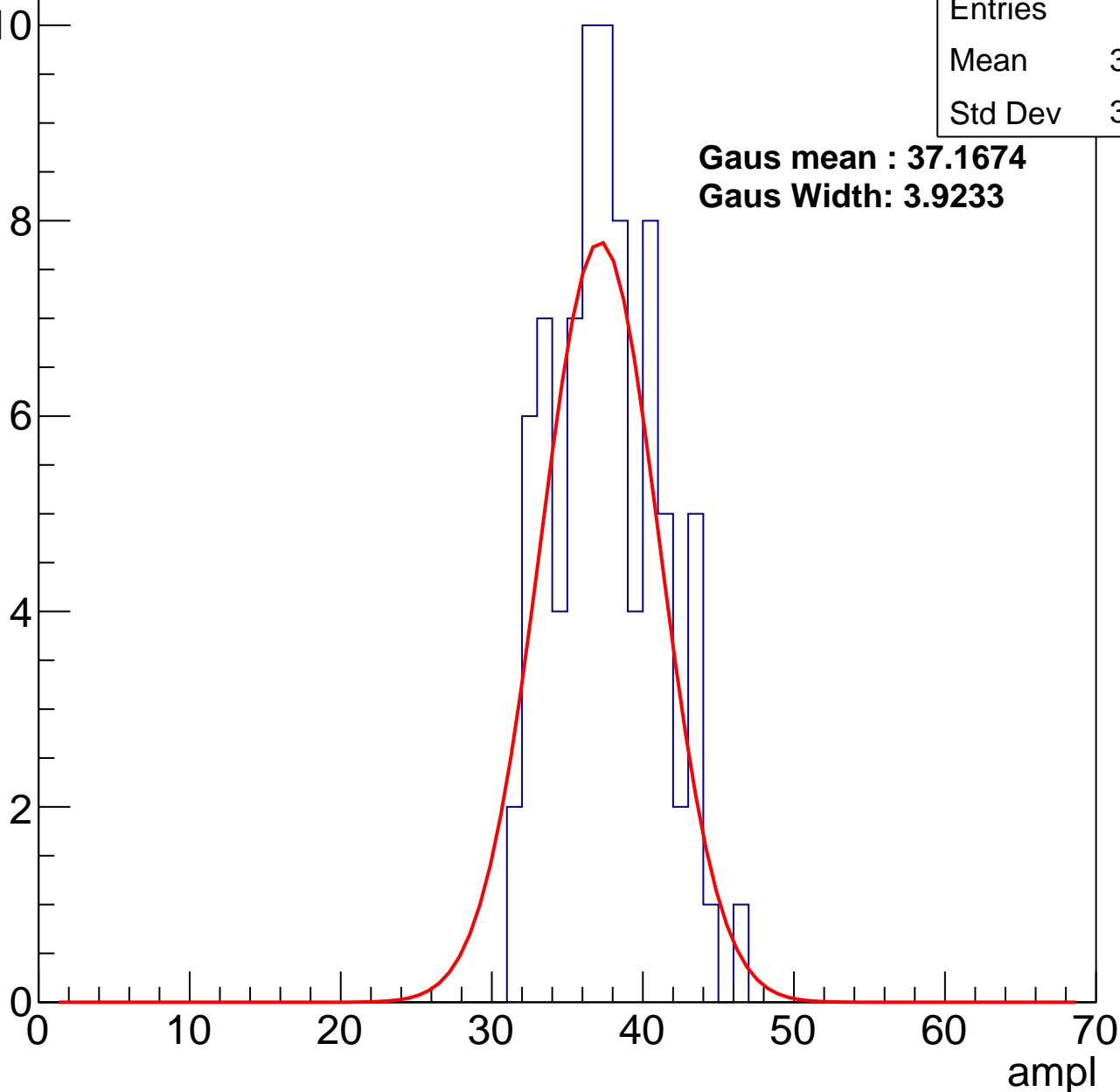
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	37.12
Std Dev	3.433

Gaus mean : 37.1674

Gaus Width: 3.9233



B0L002S, U17-ch127, adc2

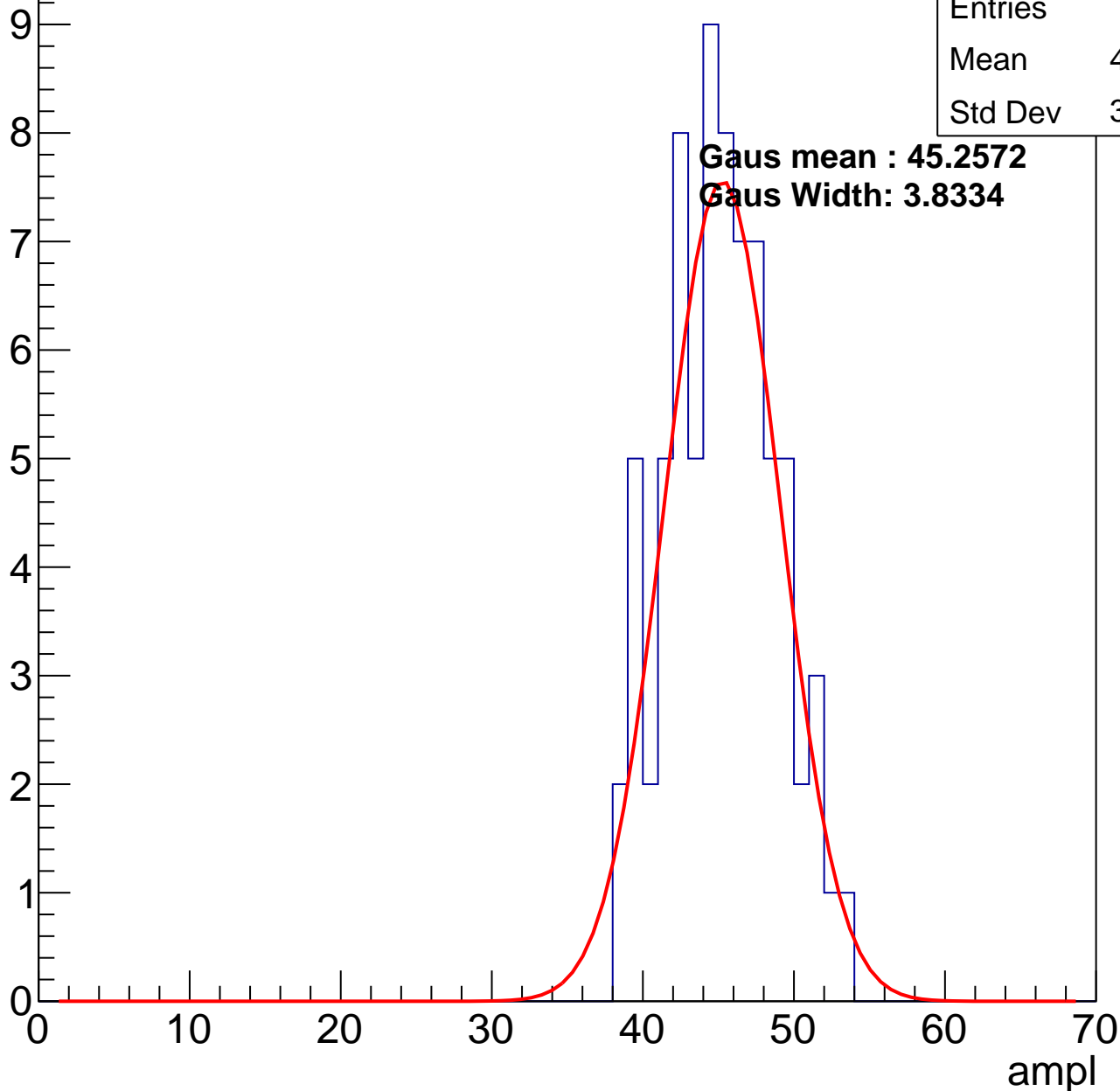
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.76
Std Dev	3.532

Gaus mean : 45.2572

Gaus Width: 3.8334

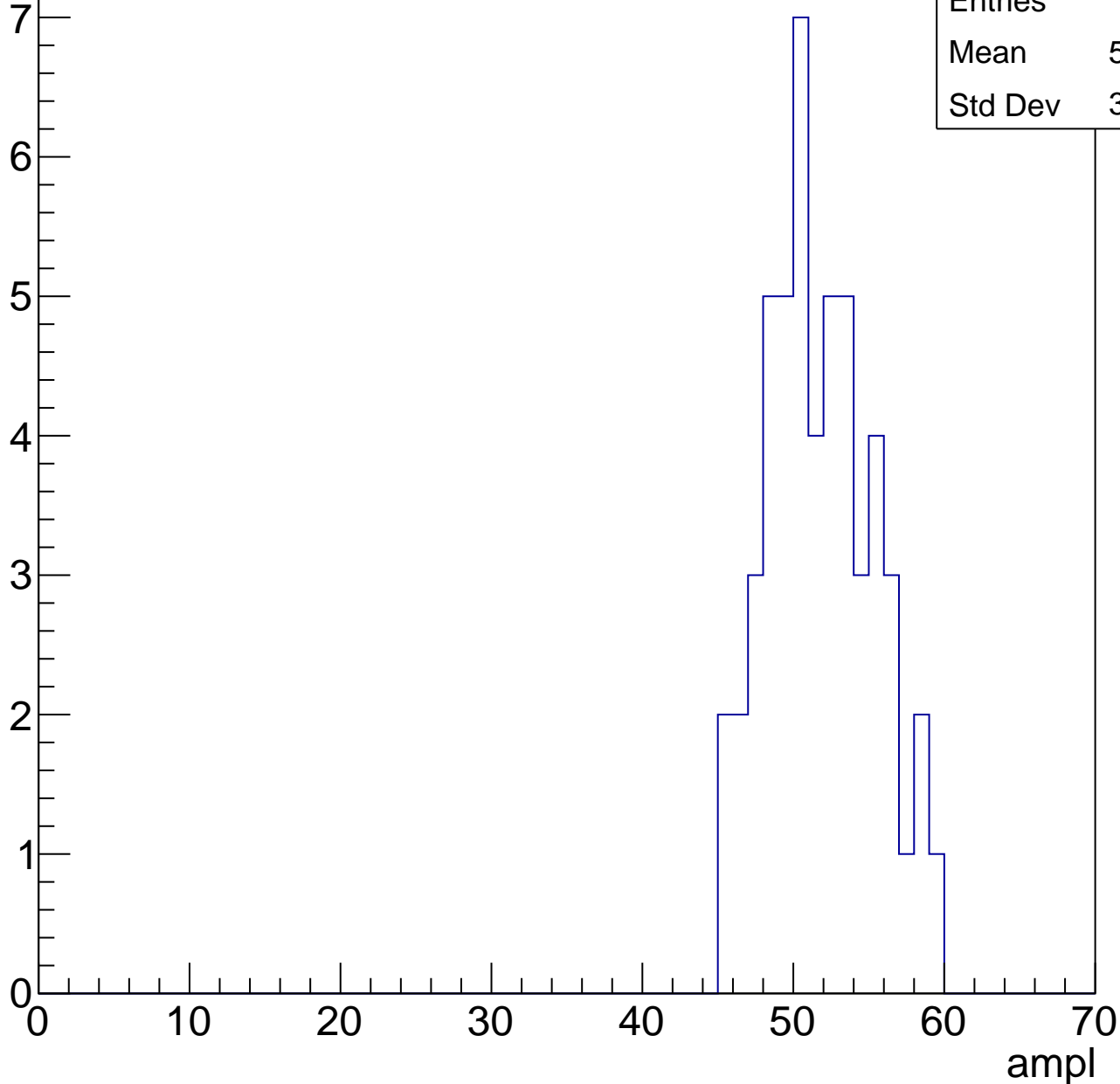


B0L002S, U17-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	51.33
Std Dev	3.468

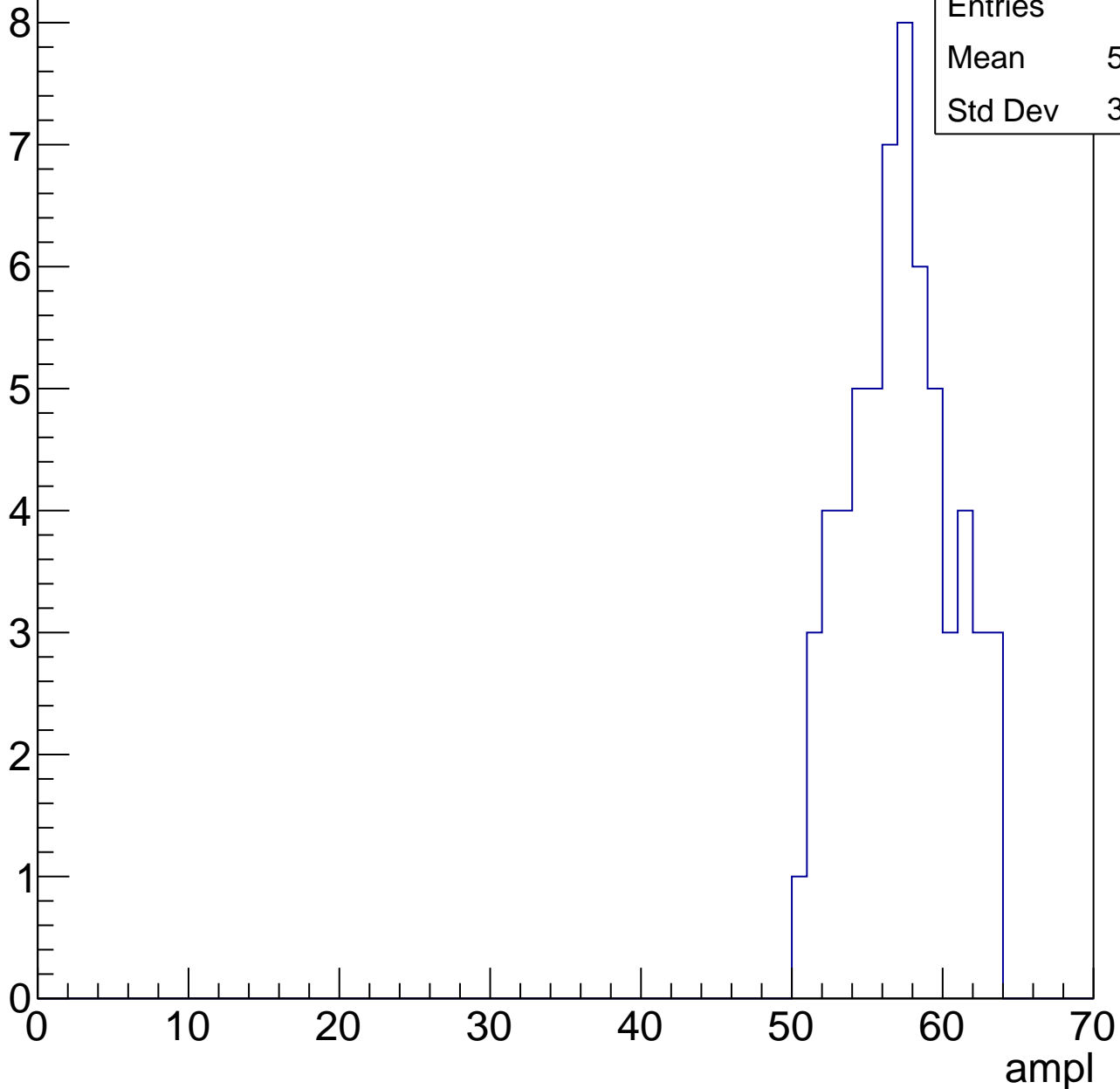


B0L002S, U17-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.69
Std Dev	3.356

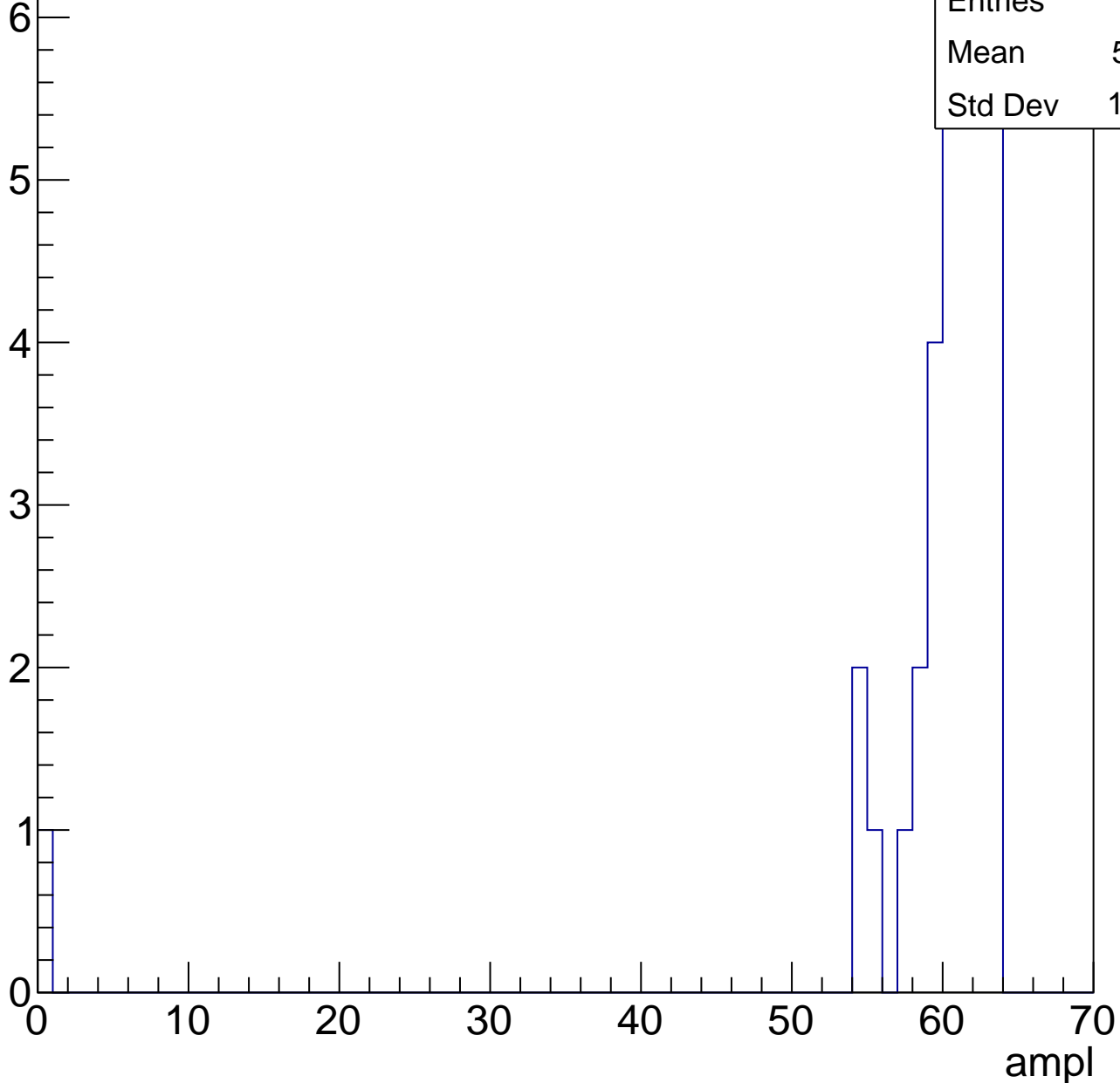


B0L002S, U17-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.51
Std Dev	10.32



B0L002S, U17-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

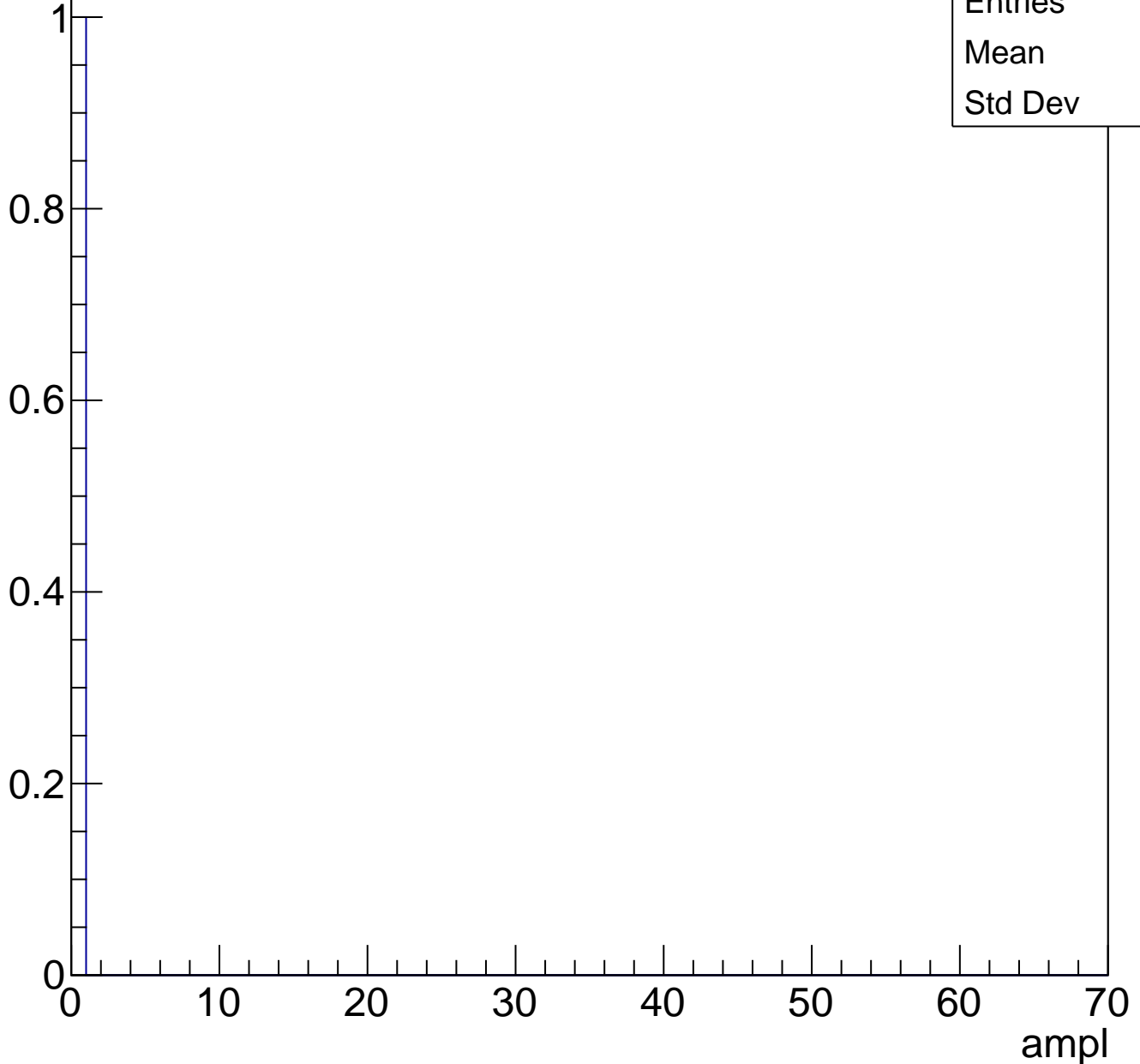
Entry



B0L002S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0