

B0L101S, U18-ch0, adc0

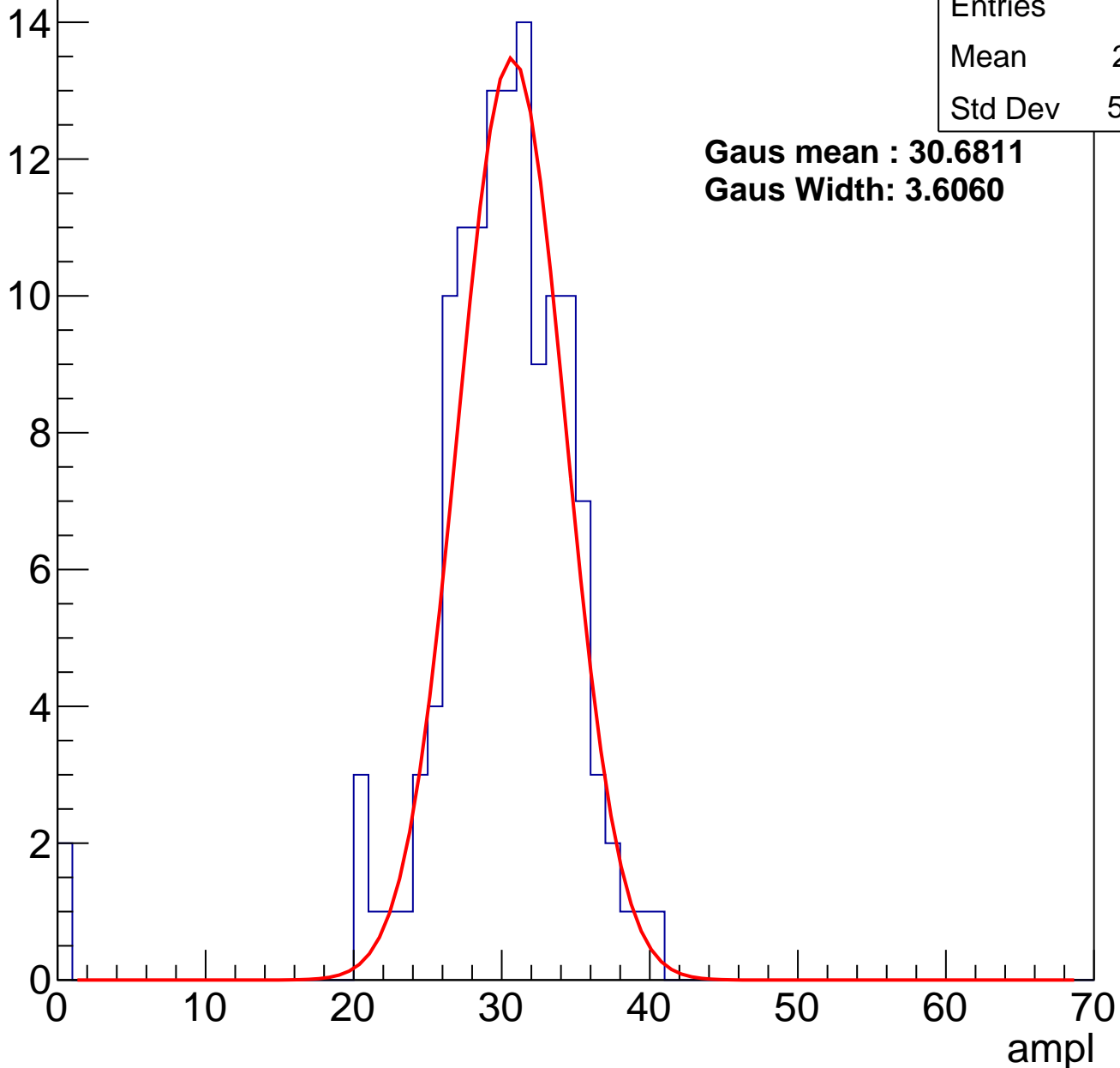
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.51
Std Dev	5.324

Gaus mean : 30.6811

Gaus Width: 3.6060

Entry



B0L101S, U18-ch0, adc1

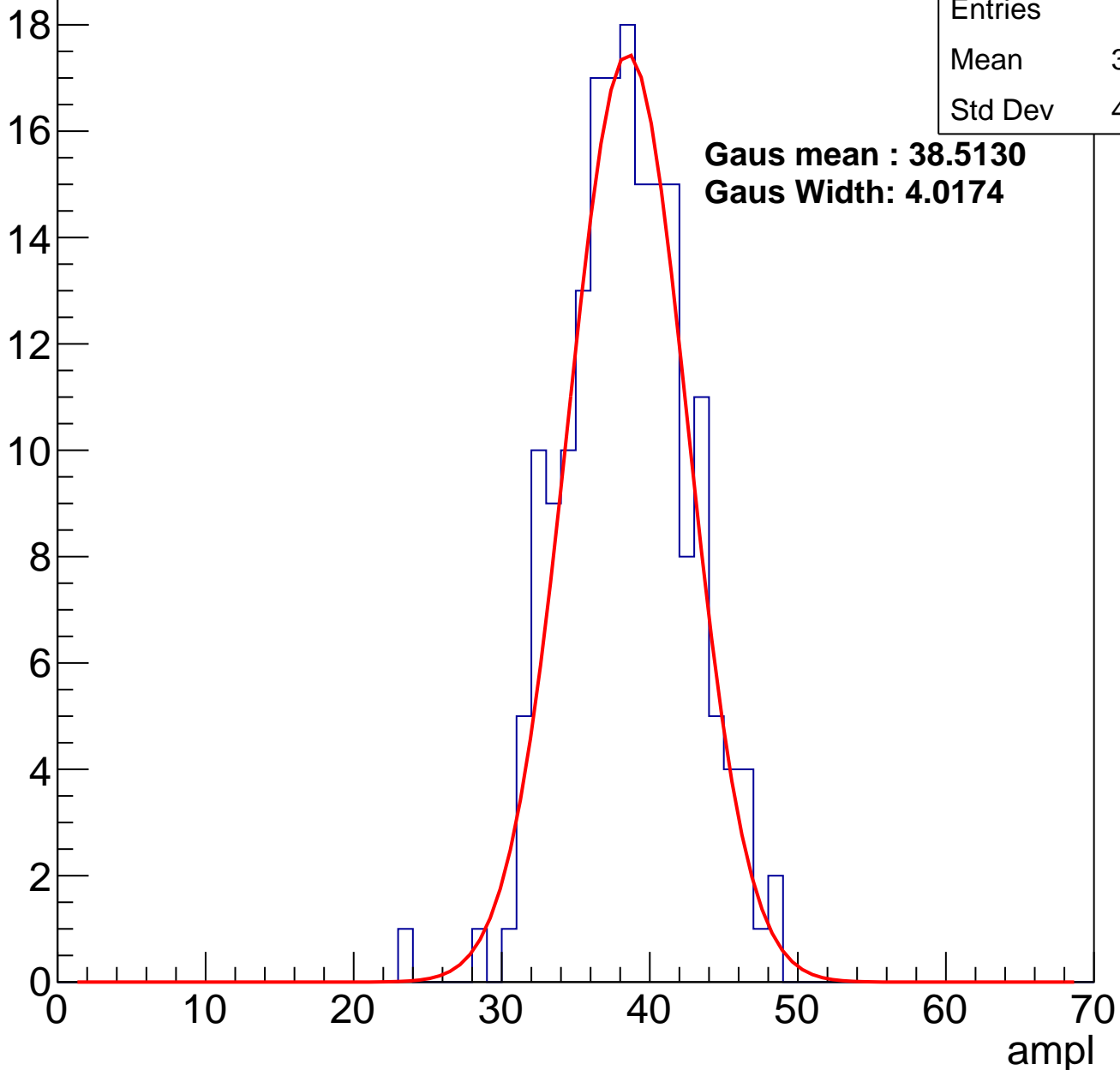
calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	37.96
Std Dev	4.132

Gaus mean : 38.5130

Gaus Width: 4.0174

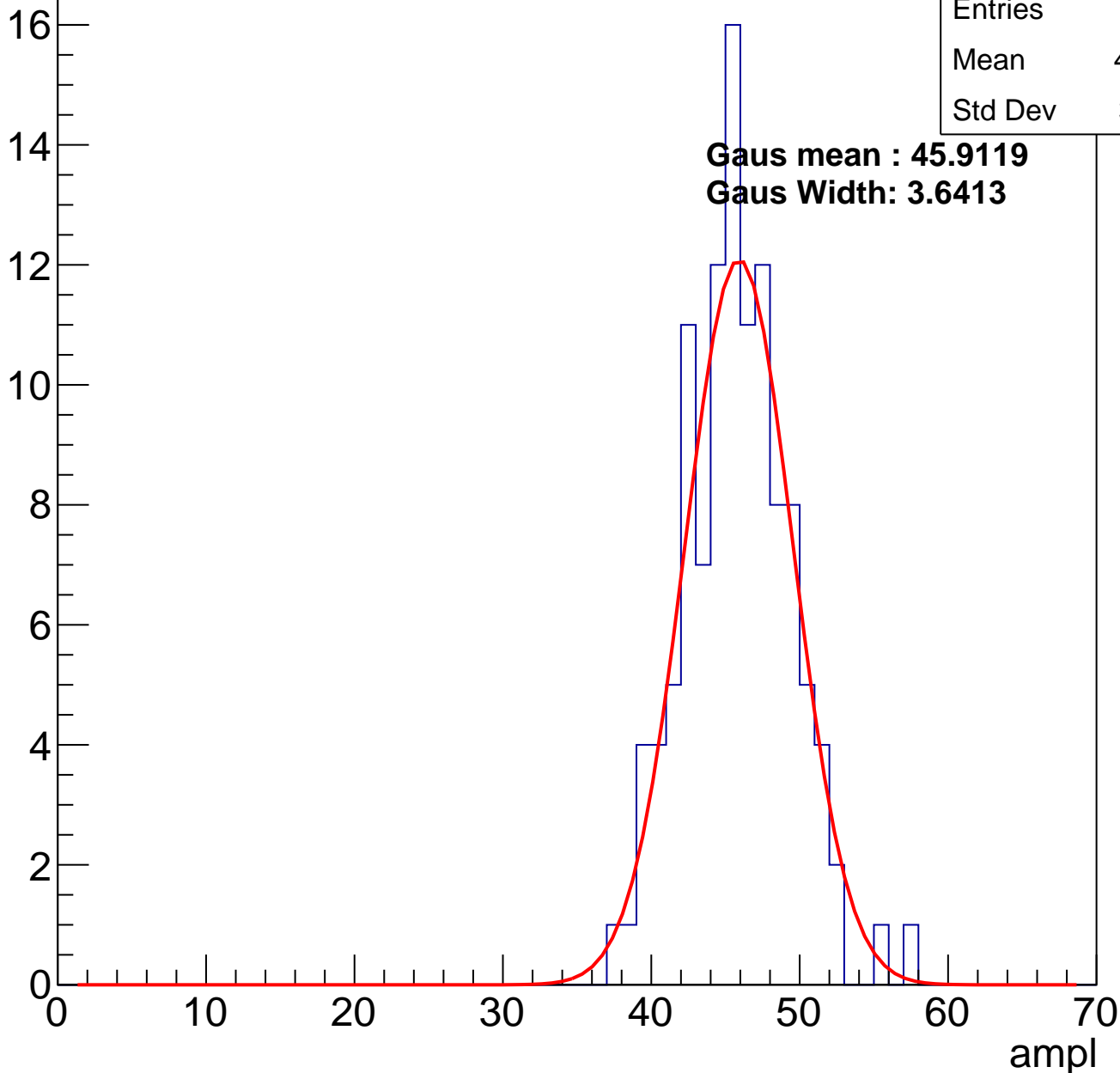
Entry



B0L101S, U18-ch0, adc2

calib_packv5_042523_0143.root, FC#1, port C1

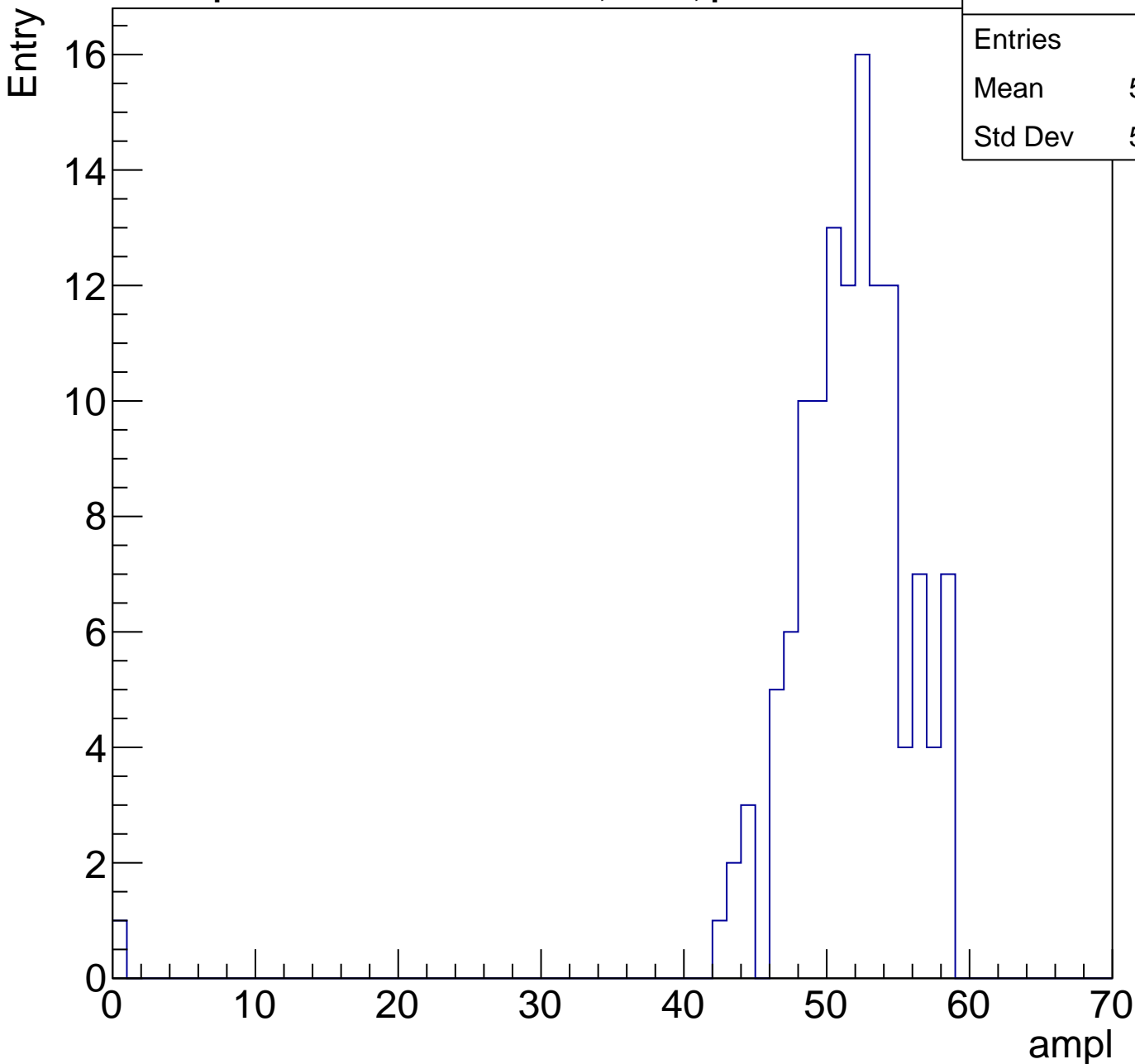
Entry



B0L101S, U18-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	50.93
Std Dev	5.809



B0L101S, U18-ch0, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

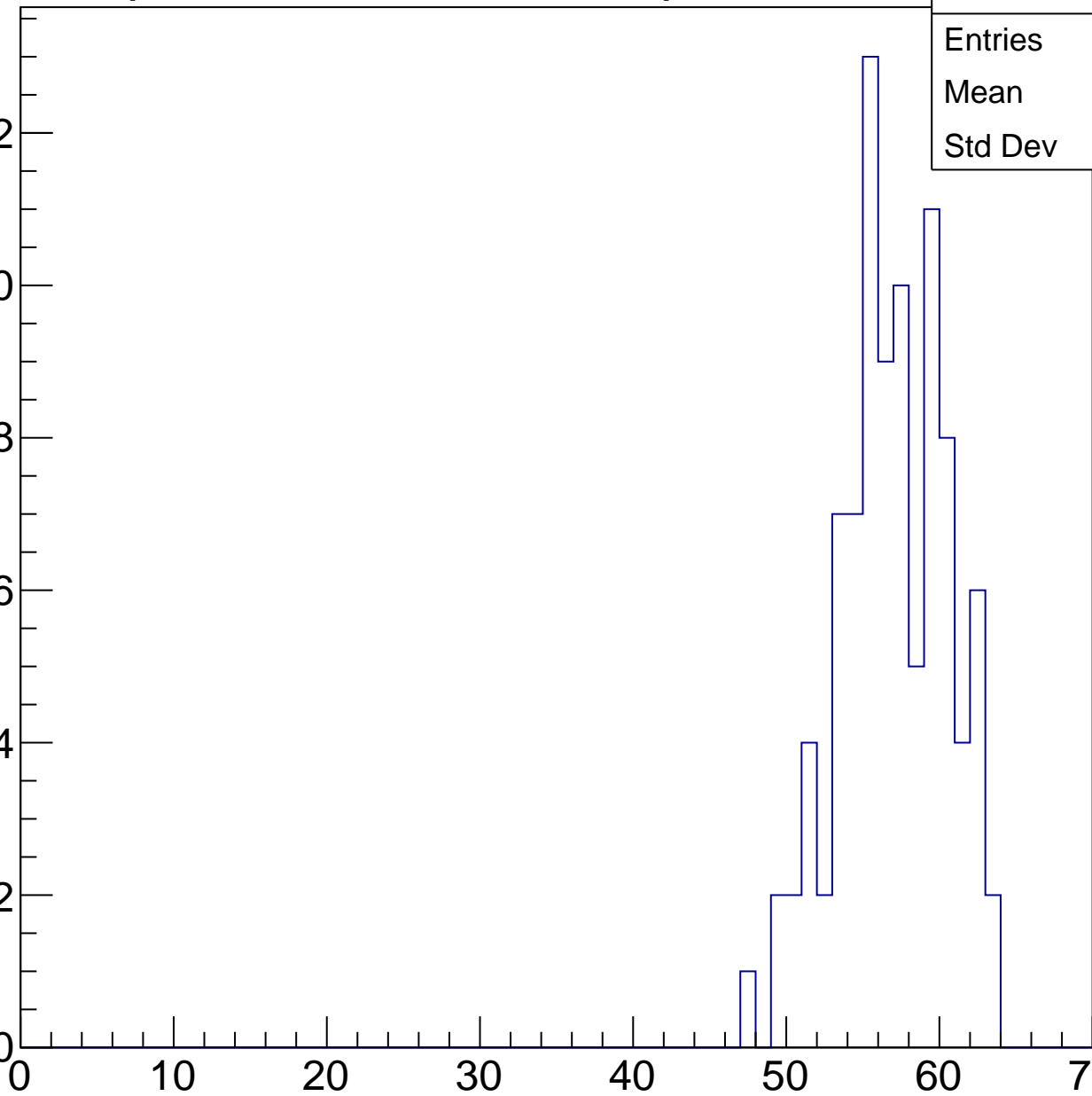
4

2

0

Entries	93
Mean	56.47
Std Dev	3.497

ampl



B0L101S, U18-ch0, adc5

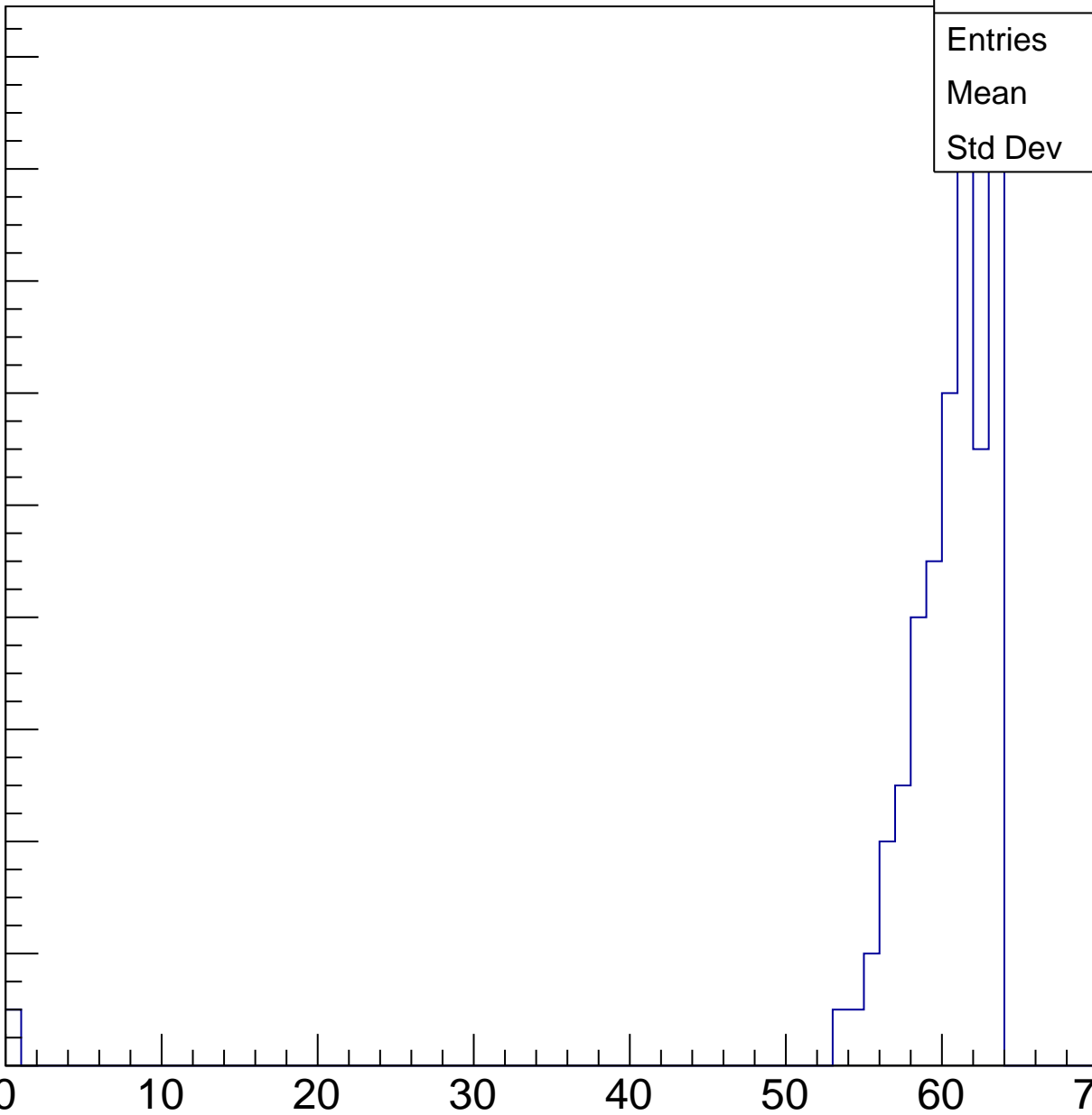
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.47
Std Dev	6.811

ampl



B0L101S, U18-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch1, adc0

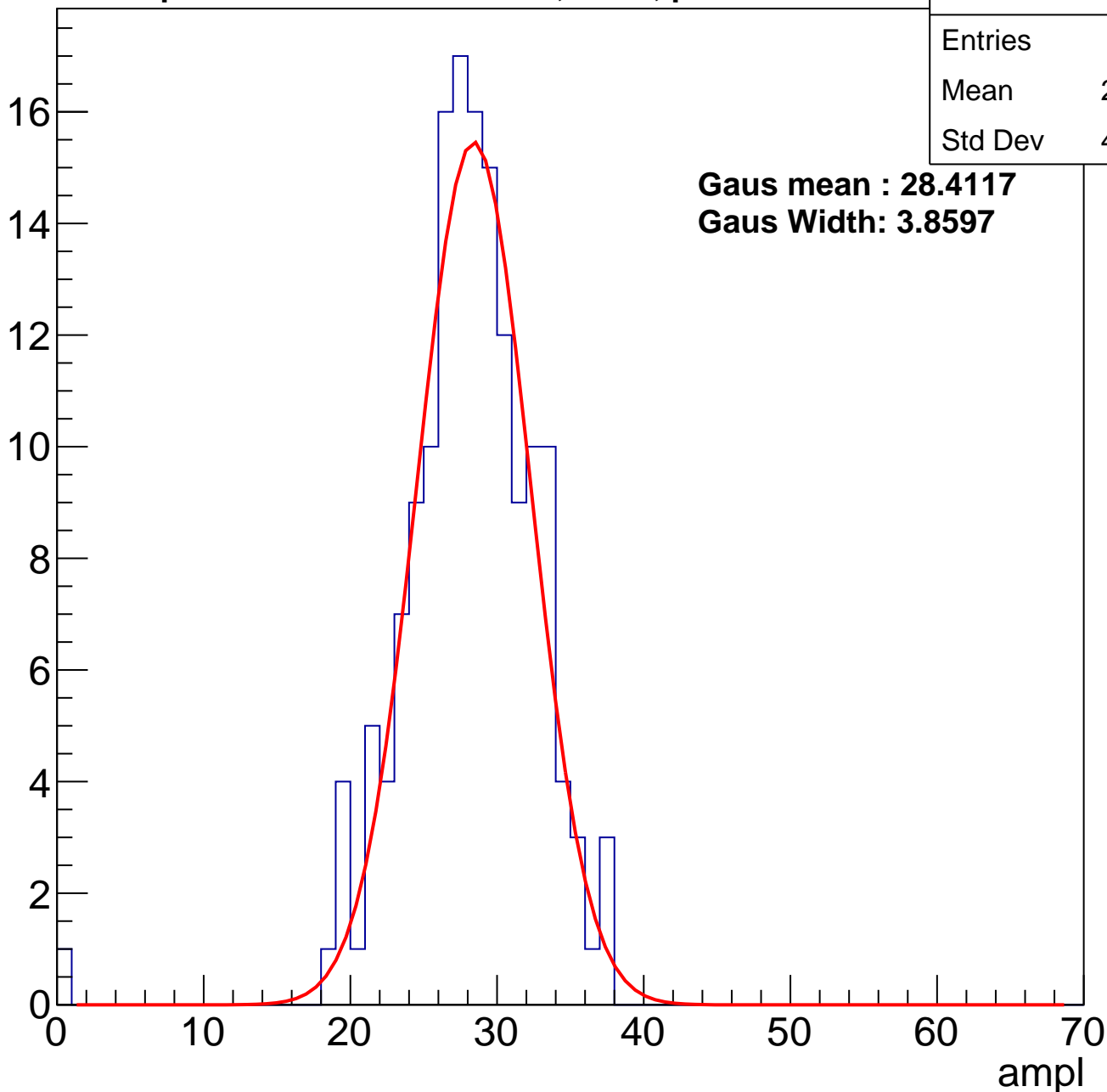
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	27.65
Std Dev	4.559

Gaus mean : 28.4117

Gaus Width: 3.8597

Entry



B0L101S, U18-ch1, adc1

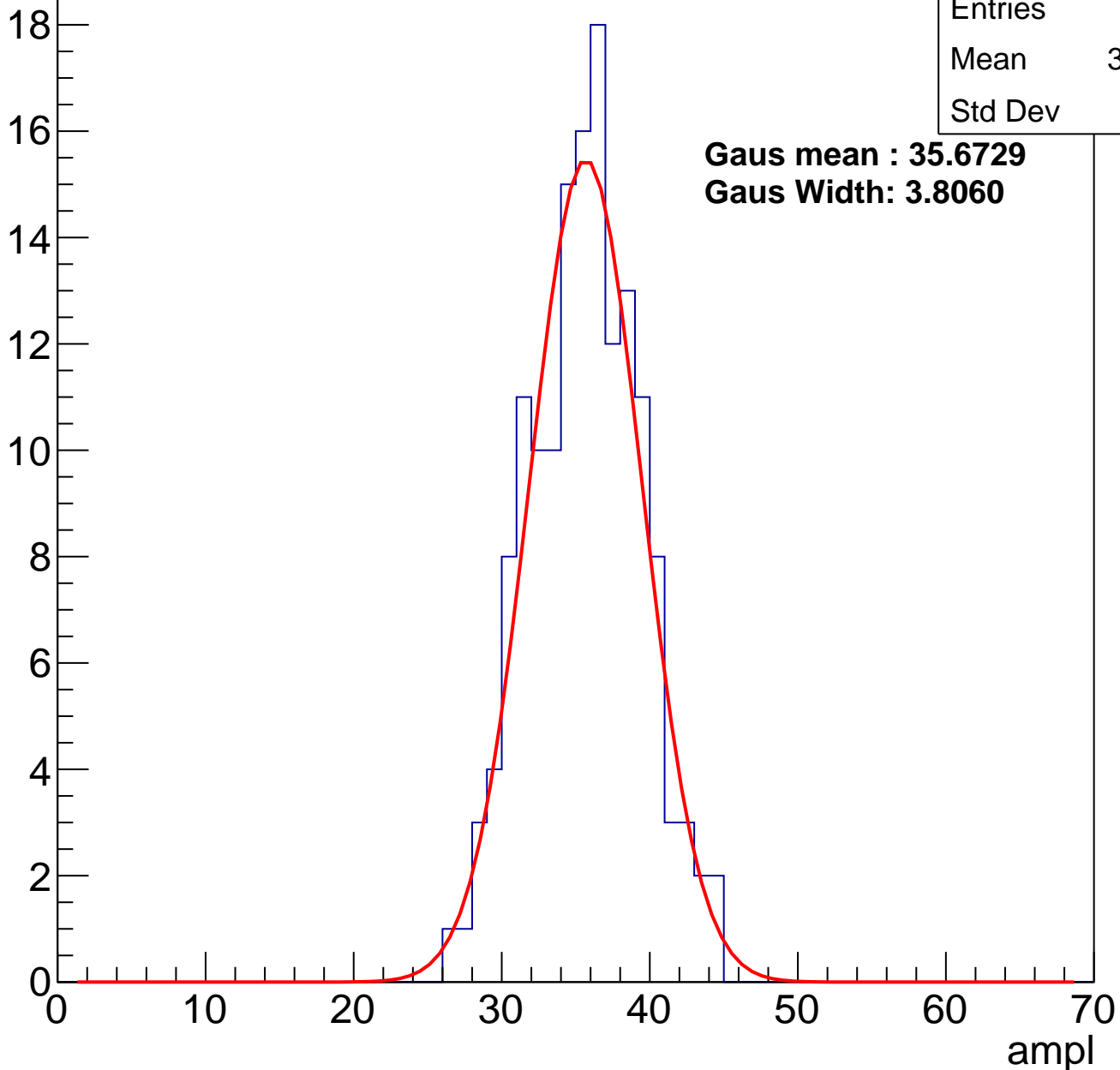
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	35.18
Std Dev	3.69

Gaus mean : 35.6729

Gaus Width: 3.8060

Entry



B0L101S, U18-ch1, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

118

Mean

42.64

Std Dev

3.571

Gaus mean : 42.8156

Gaus Width: 3.2705

0

10

20

30

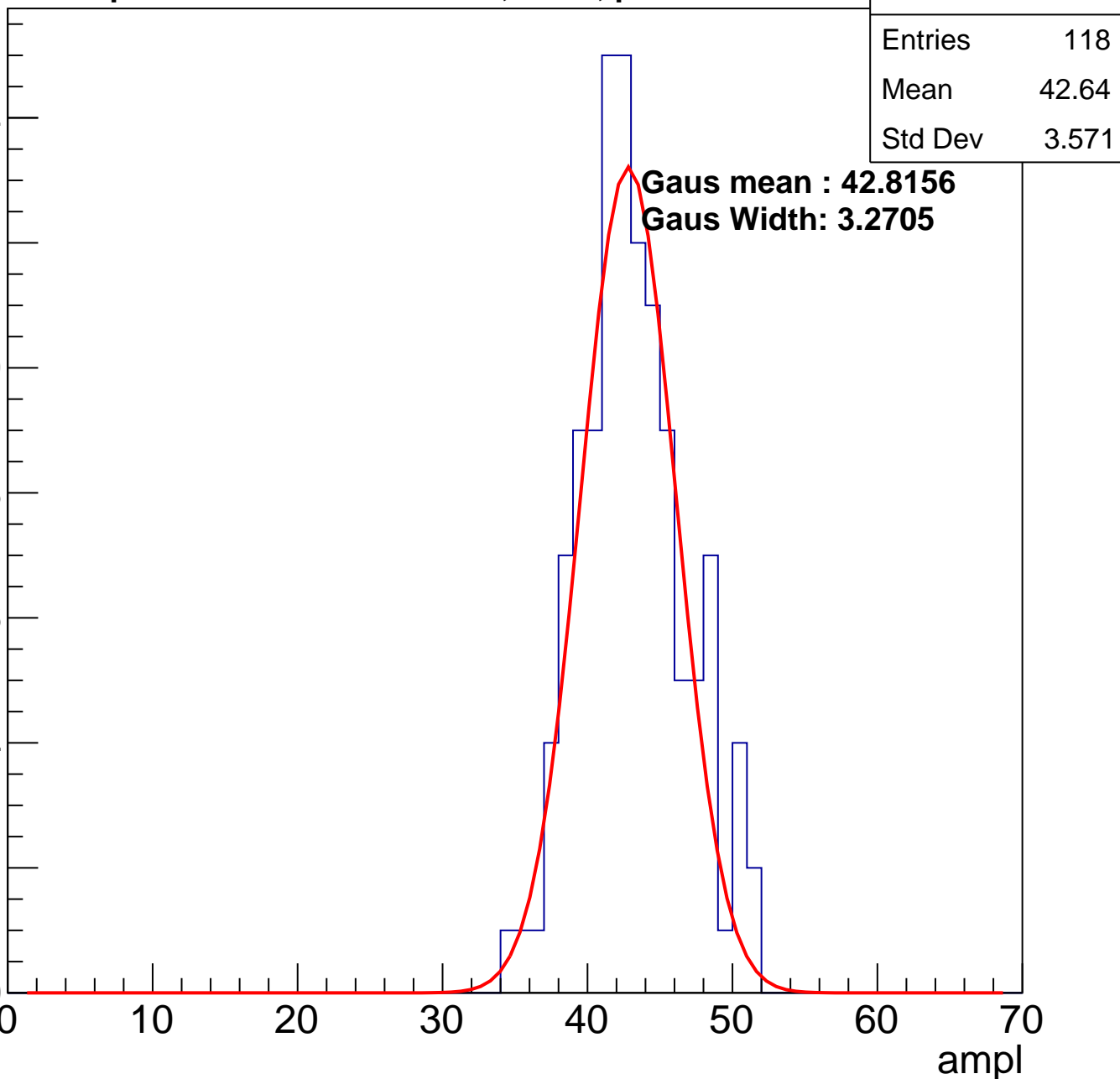
40

50

60

70

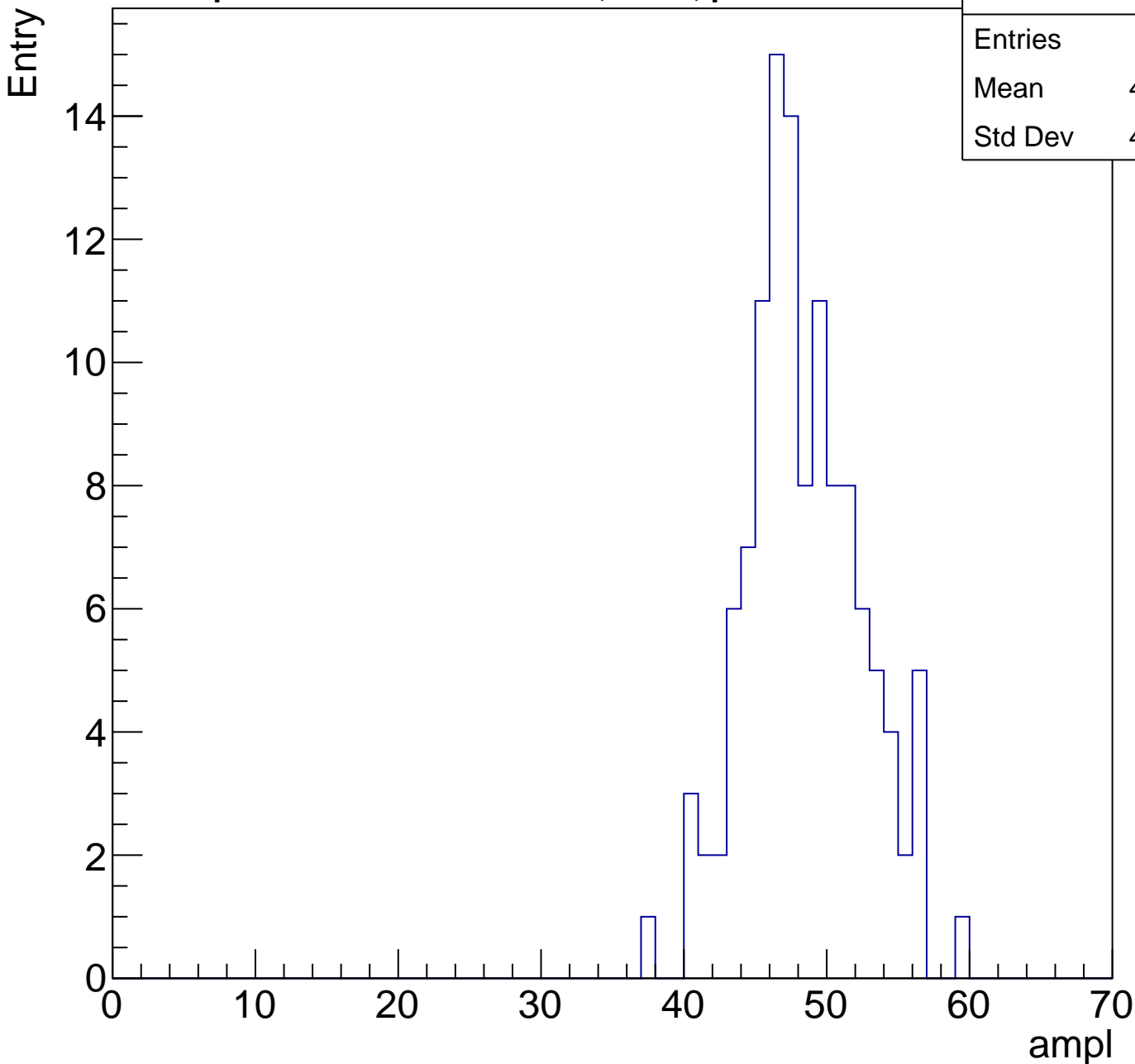
ampl



B0L101S, U18-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	47.94
Std Dev	4.034



B0L101S, U18-ch1, adc4

calib_packv5_042523_0143.root, FC#1, port C1

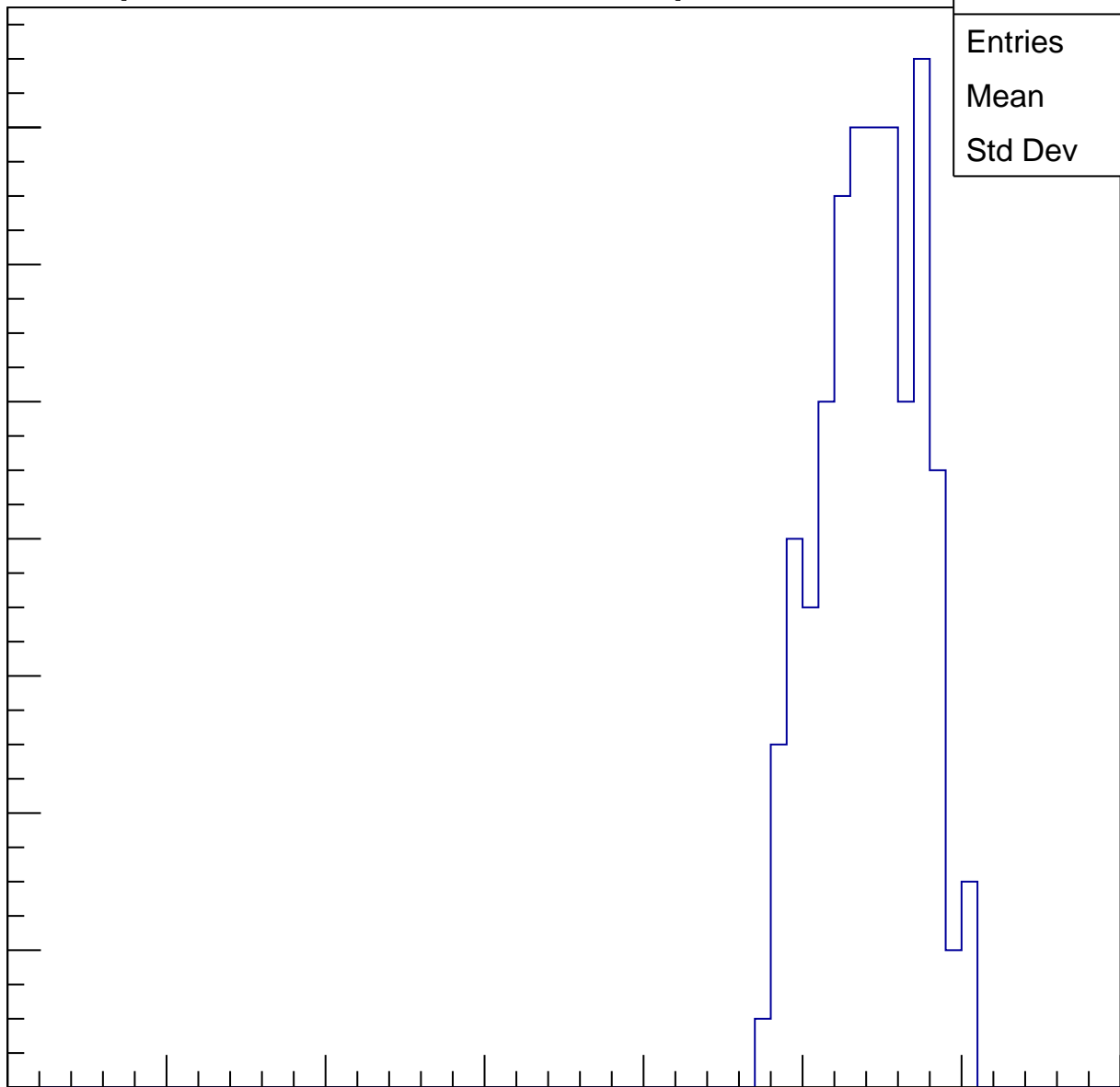
Entries	125
Mean	53.74
Std Dev	3.072

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch1, adc5

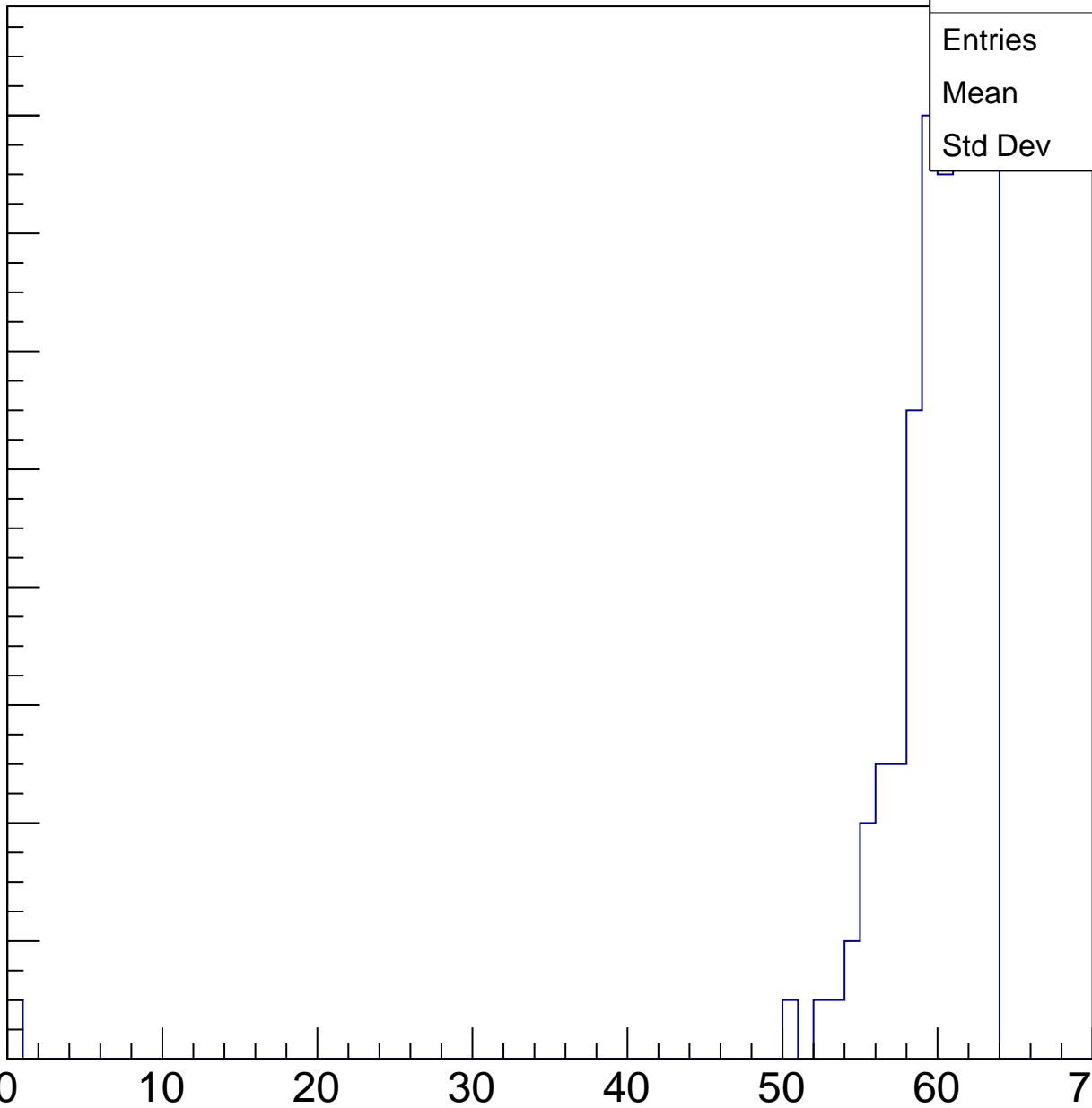
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	59.2
Std Dev	6.219

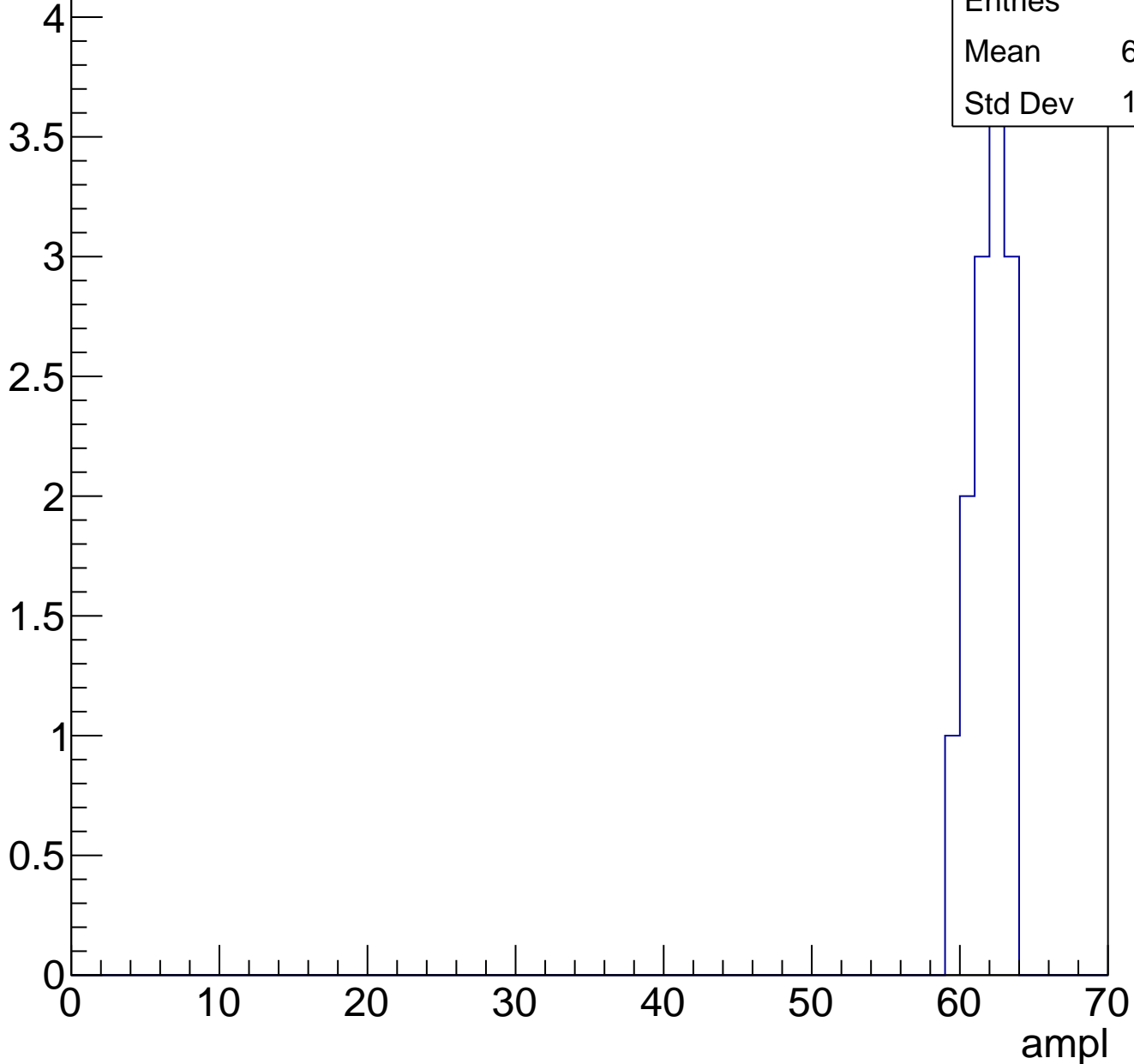
ampl



B0L101S, U18-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

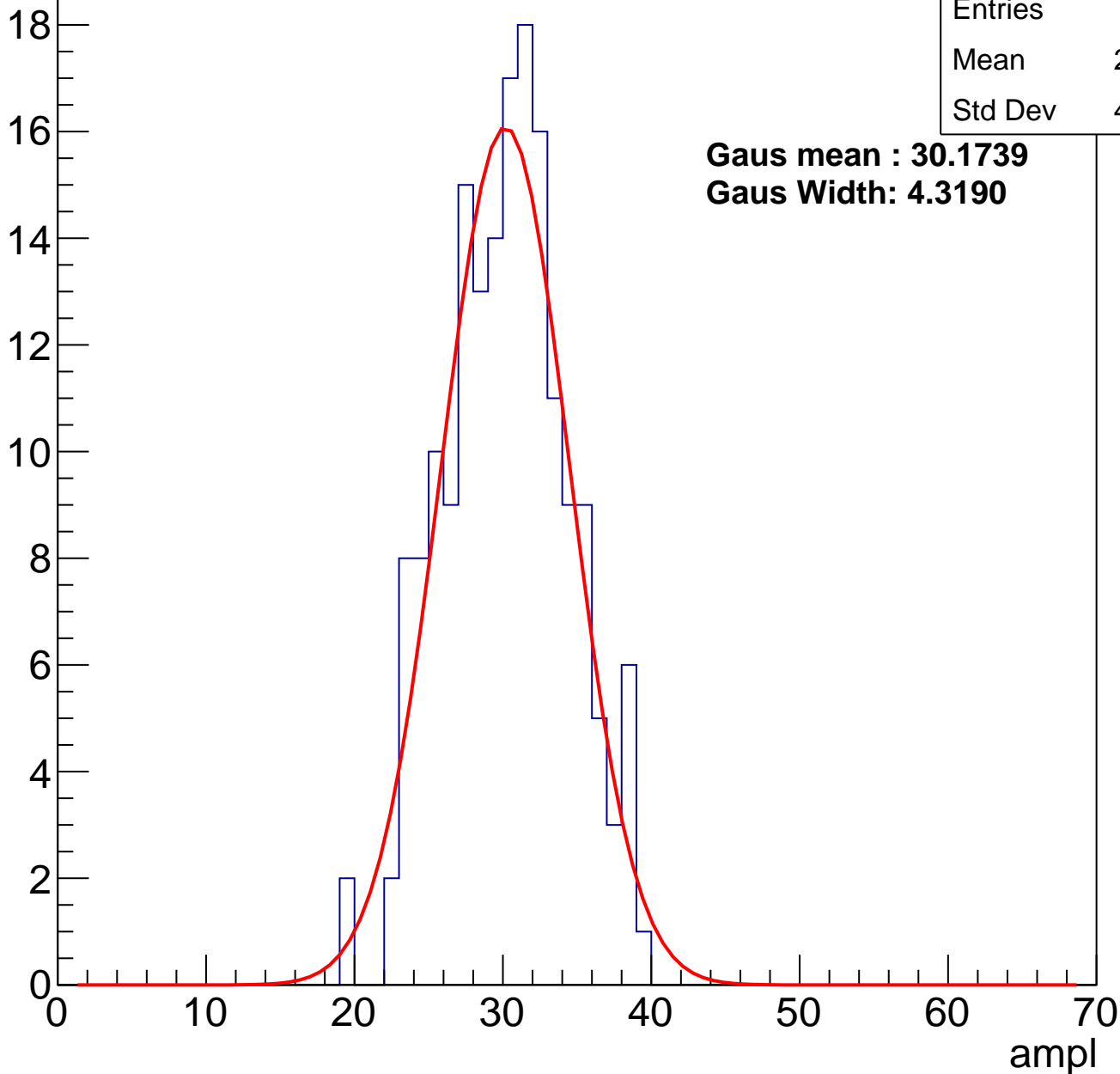


Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch2, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	36.41
Std Dev	3.561

Gaus mean : 37.1047
Gaus Width: 3.2711

Entry

10

8

6

4

2

0

0

10

20

30

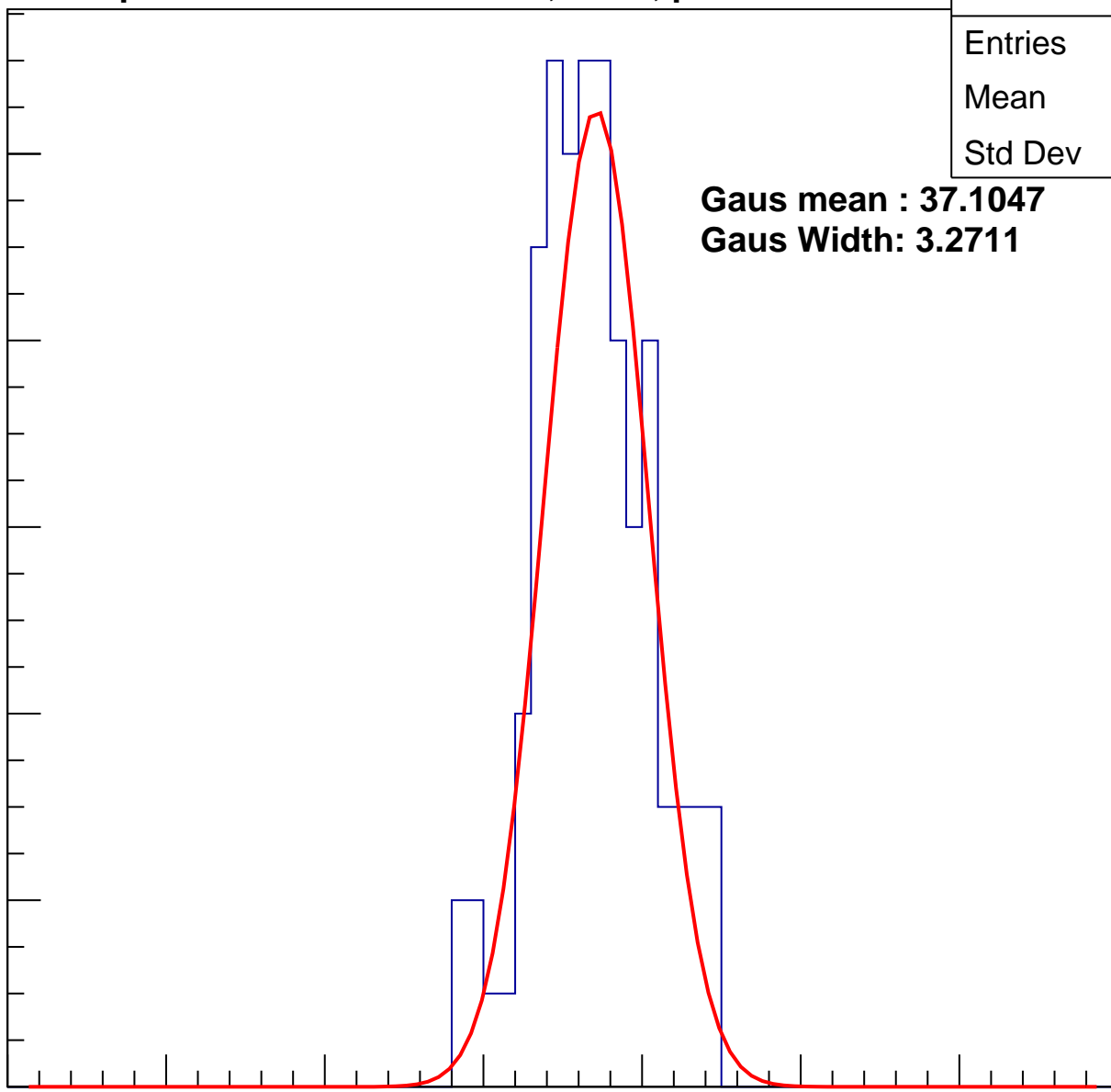
40

50

60

70

ampl



B0L101S, U18-ch2, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries	171
Mean	43.42
Std Dev	3.82

Gaus mean : 43.7269

Gaus Width: 3.8734

0

10

20

30

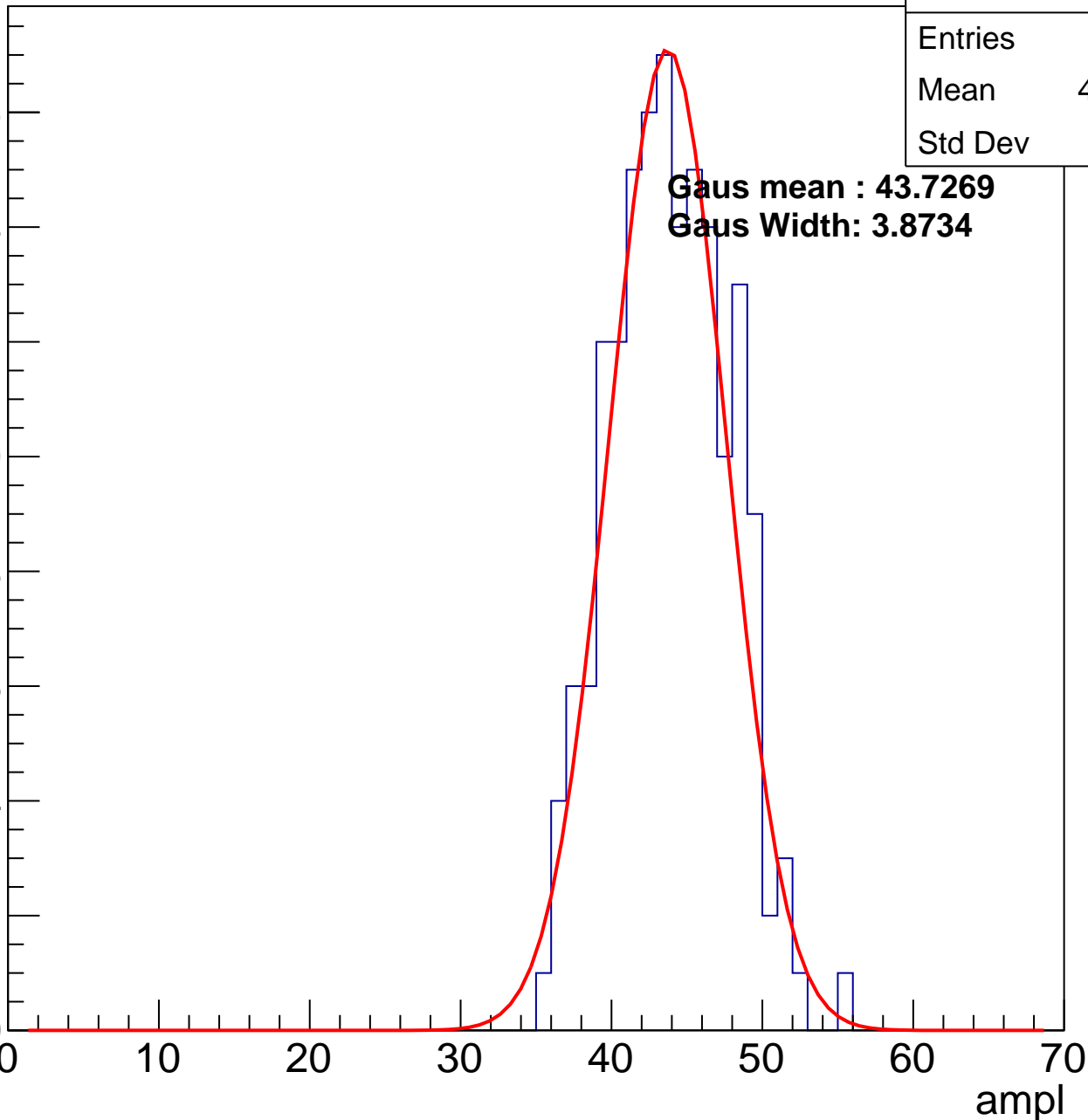
40

50

60

70

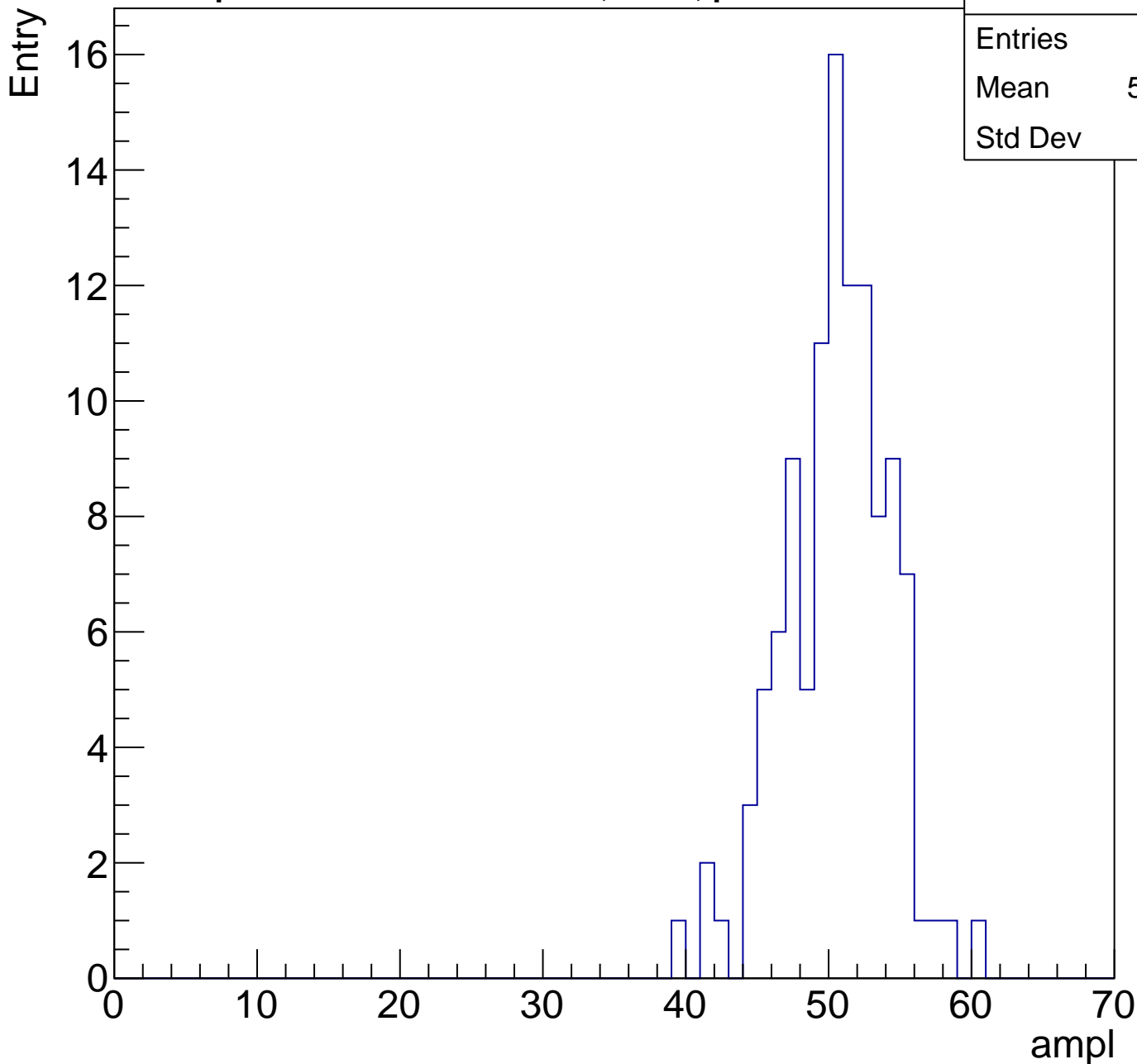
ampl



B0L101S, U18-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	50.09
Std Dev	3.66



B0L101S, U18-ch2, adc4

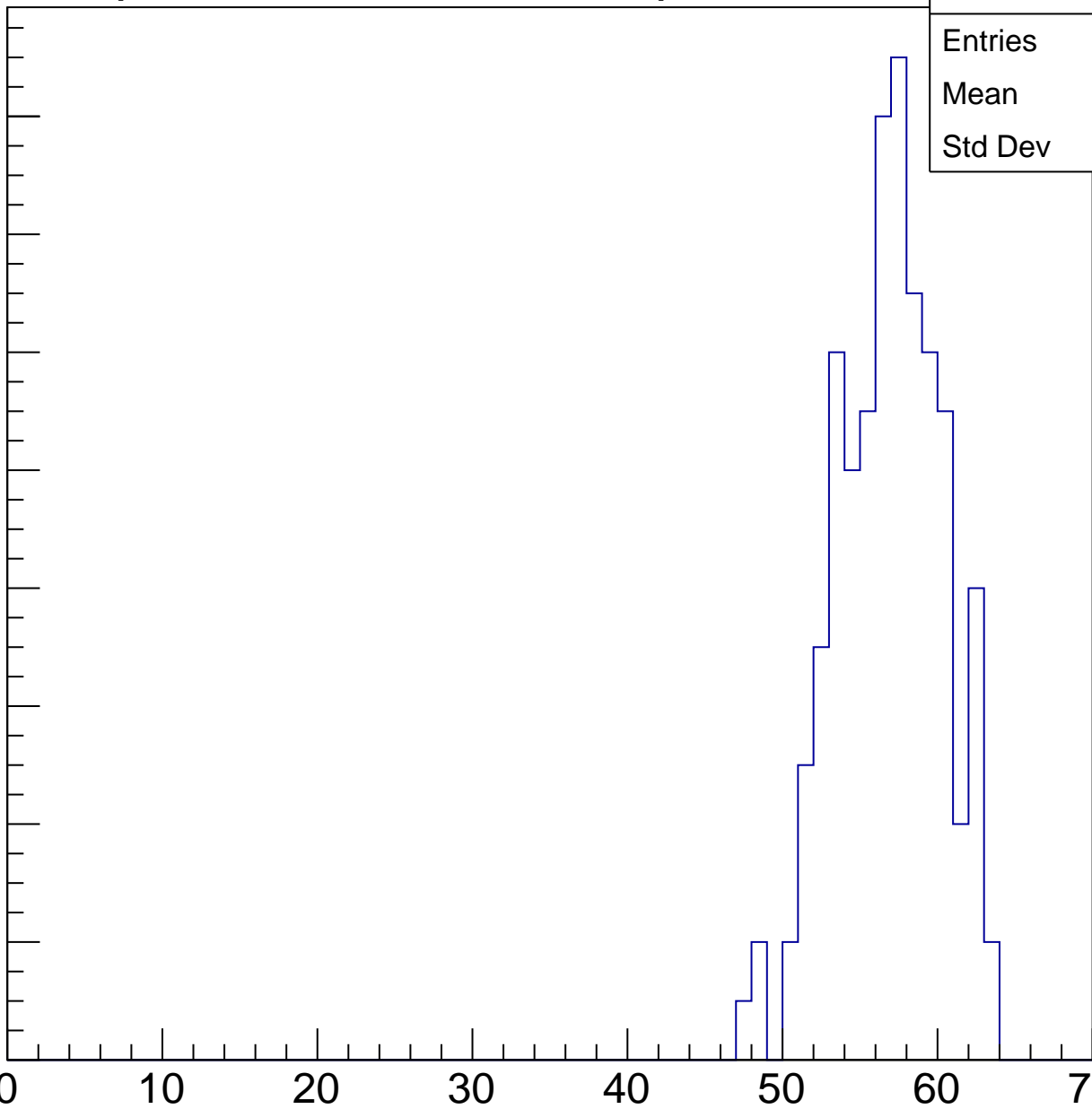
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	56.36
Std Dev	3.357

ampl



B0L101S, U18-ch2, adc5

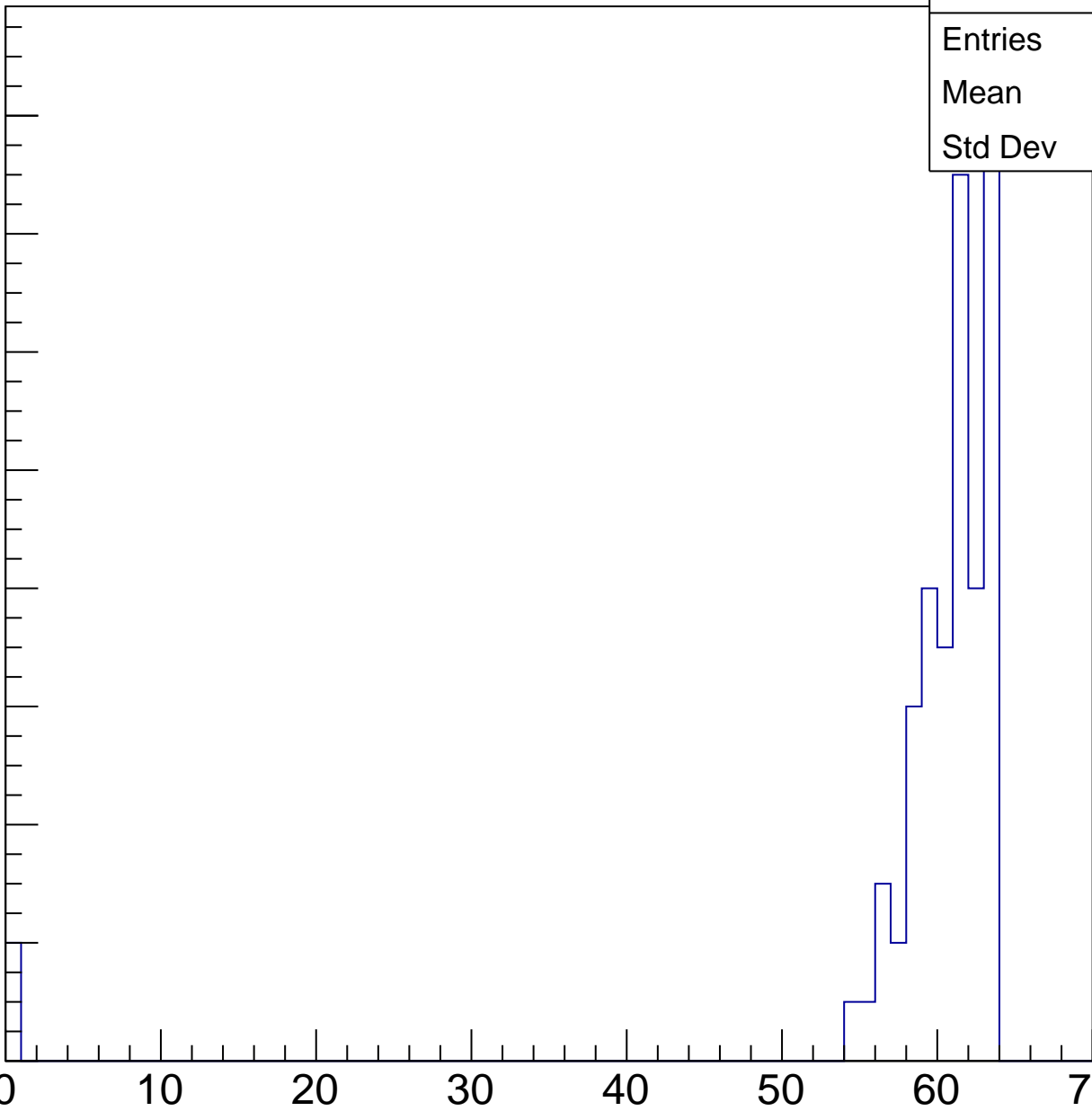
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	70
Mean	58.76
Std Dev	10.32

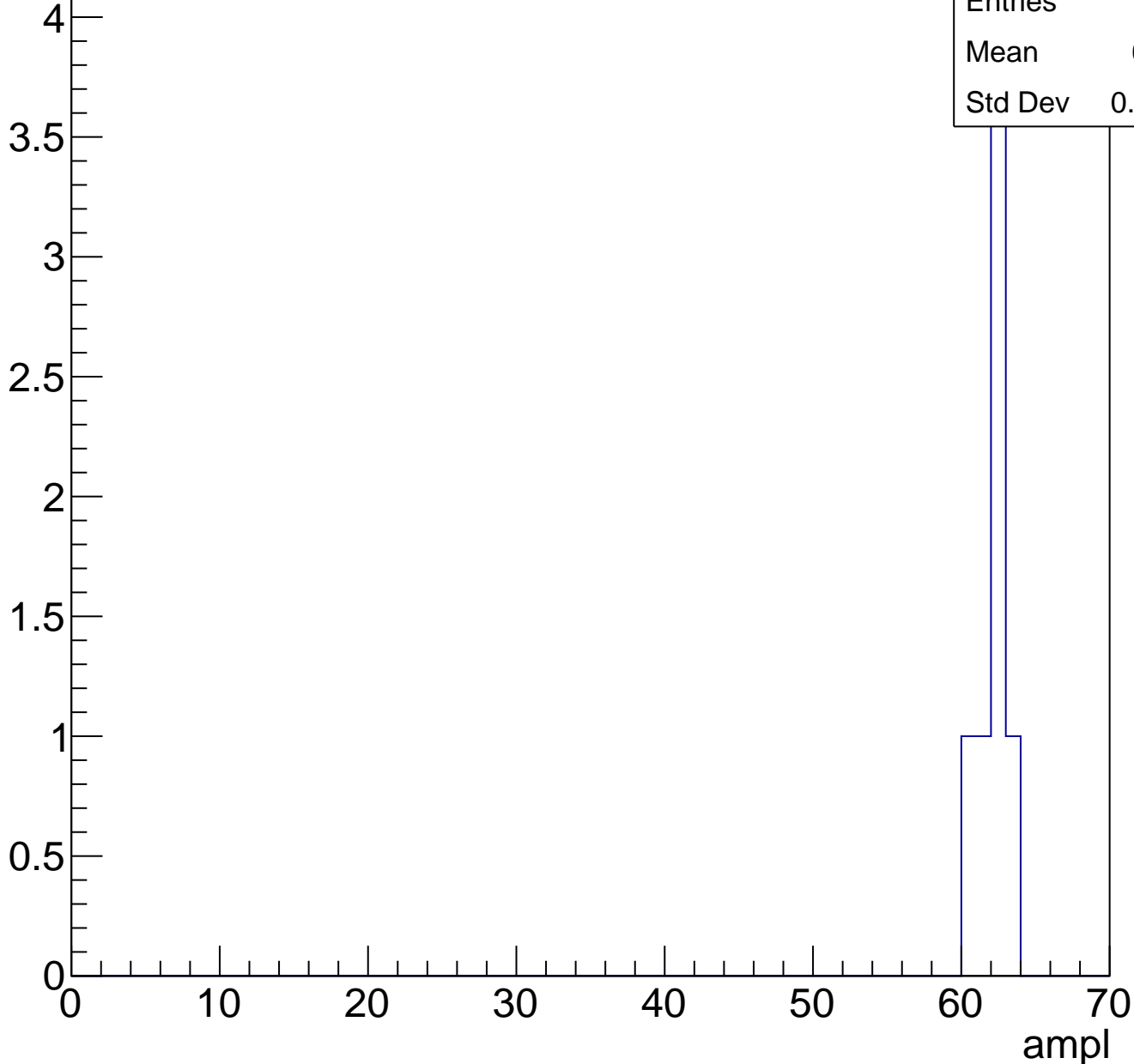
ampl



B0L101S, U18-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L101S, U18-ch3, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	29.27
Std Dev	3.811

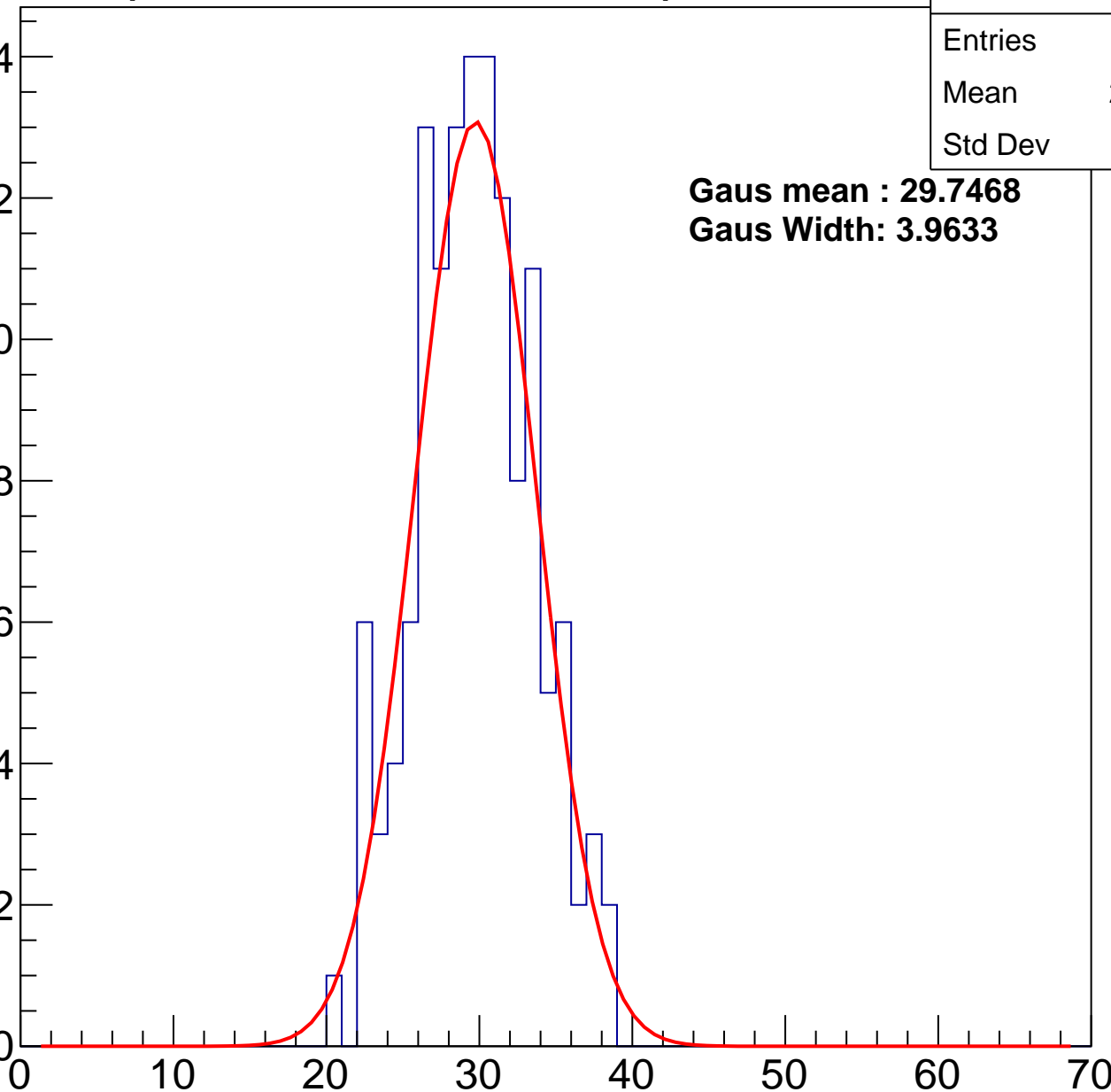
Gaus mean : 29.7468

Gaus Width: 3.9633

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch3, adc1

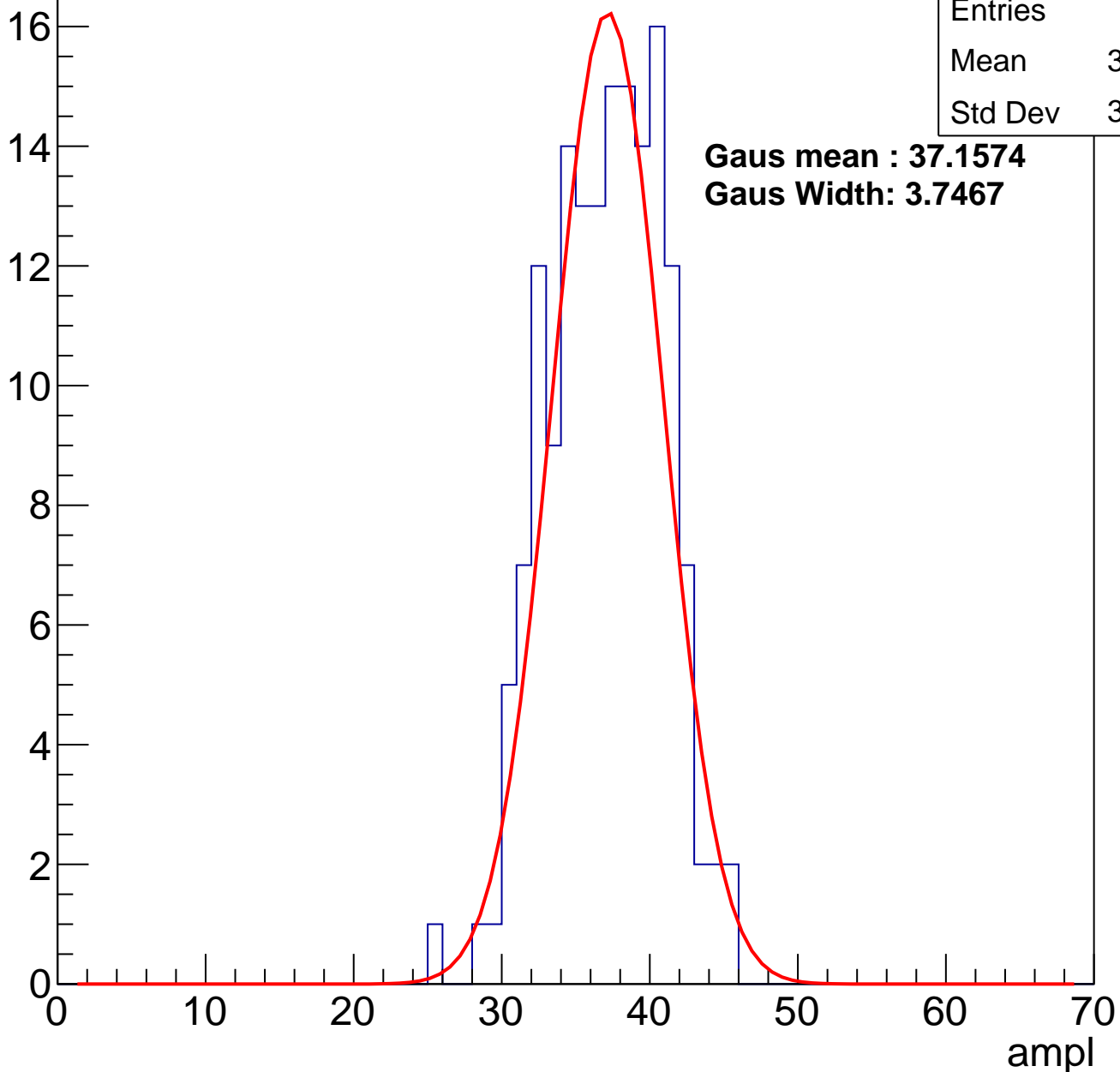
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	36.58
Std Dev	3.757

Gaus mean : 37.1574

Gaus Width: 3.7467

Entry



B0L101S, U18-ch3, adc2

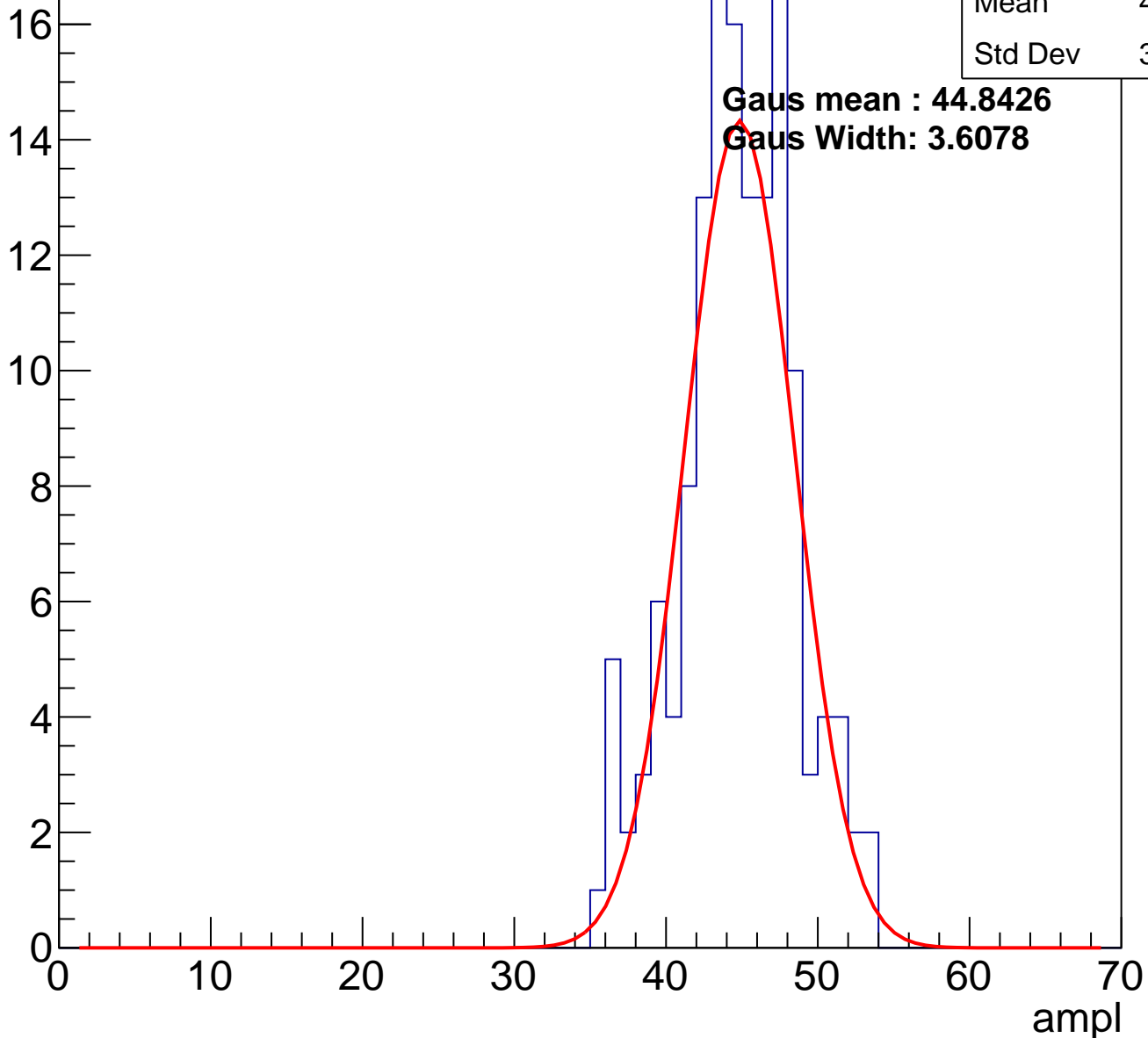
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	143
Mean	44.26
Std Dev	3.773

Gaus mean : 44.8426

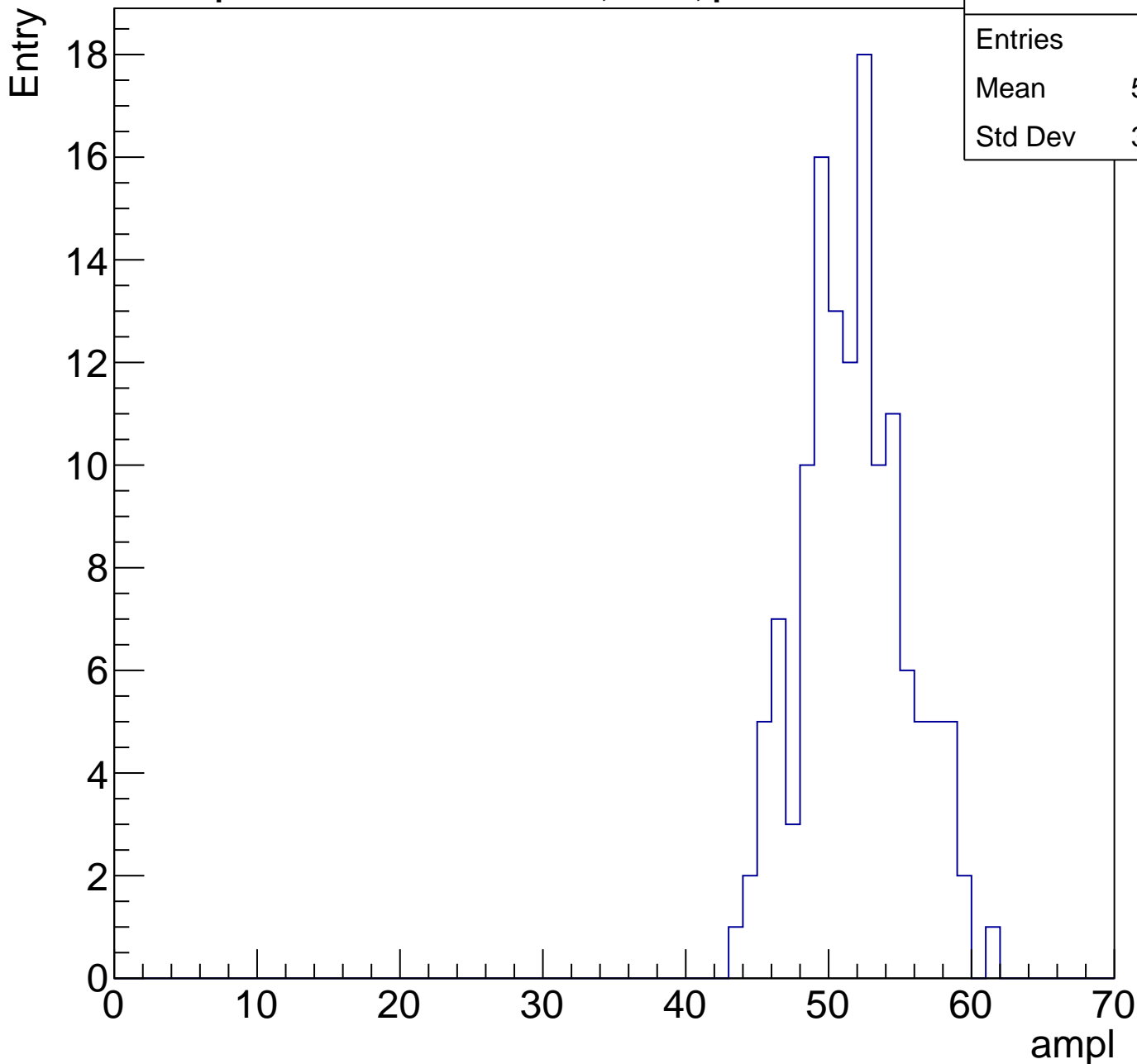
Gaus Width: 3.6078



B0L101S, U18-ch3, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	51.28
Std Dev	3.644



B0L101S, U18-ch3, adc4

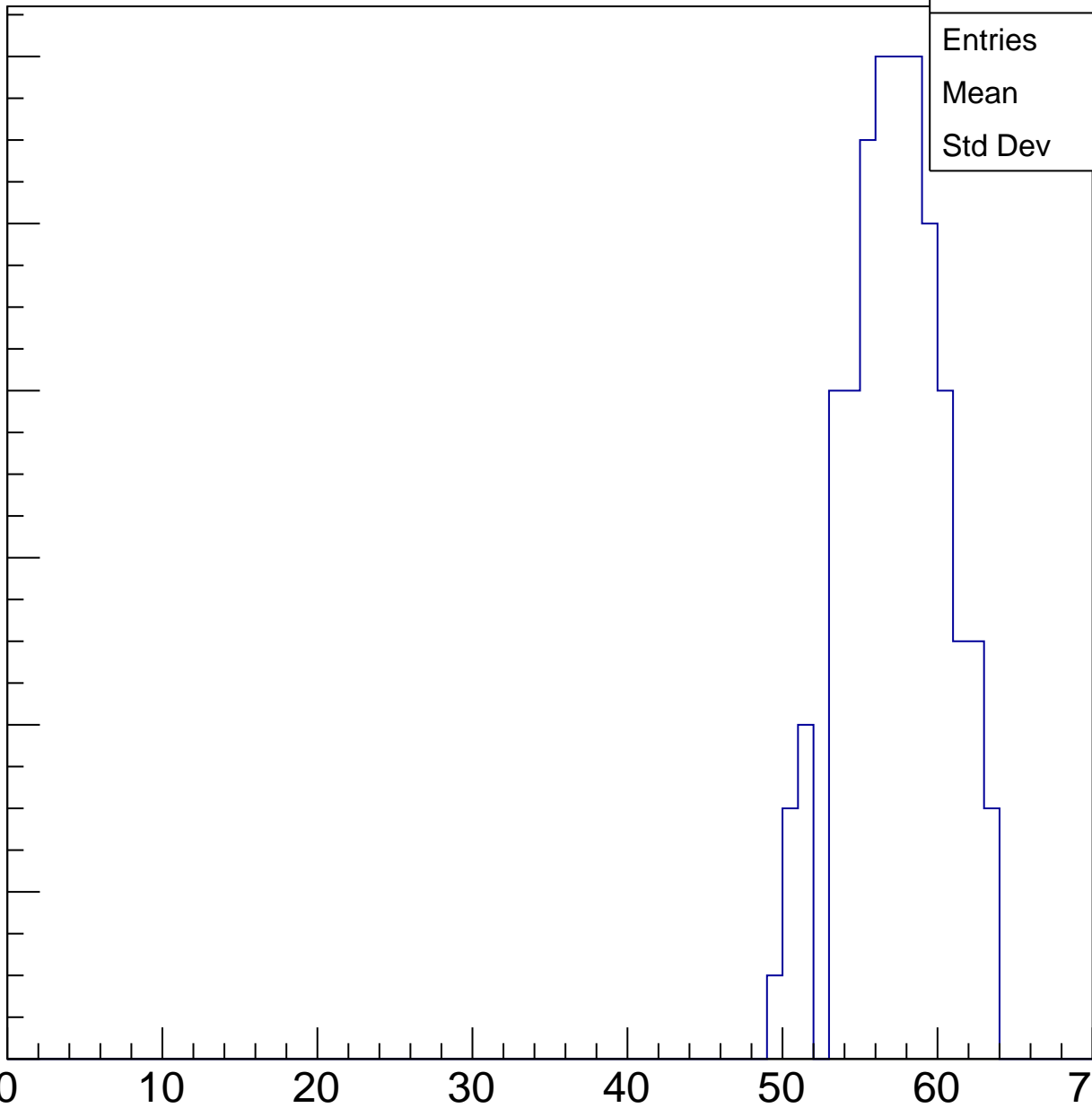
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	102
Mean	56.76
Std Dev	3.197

ampl



B0L101S, U18-ch3, adc5

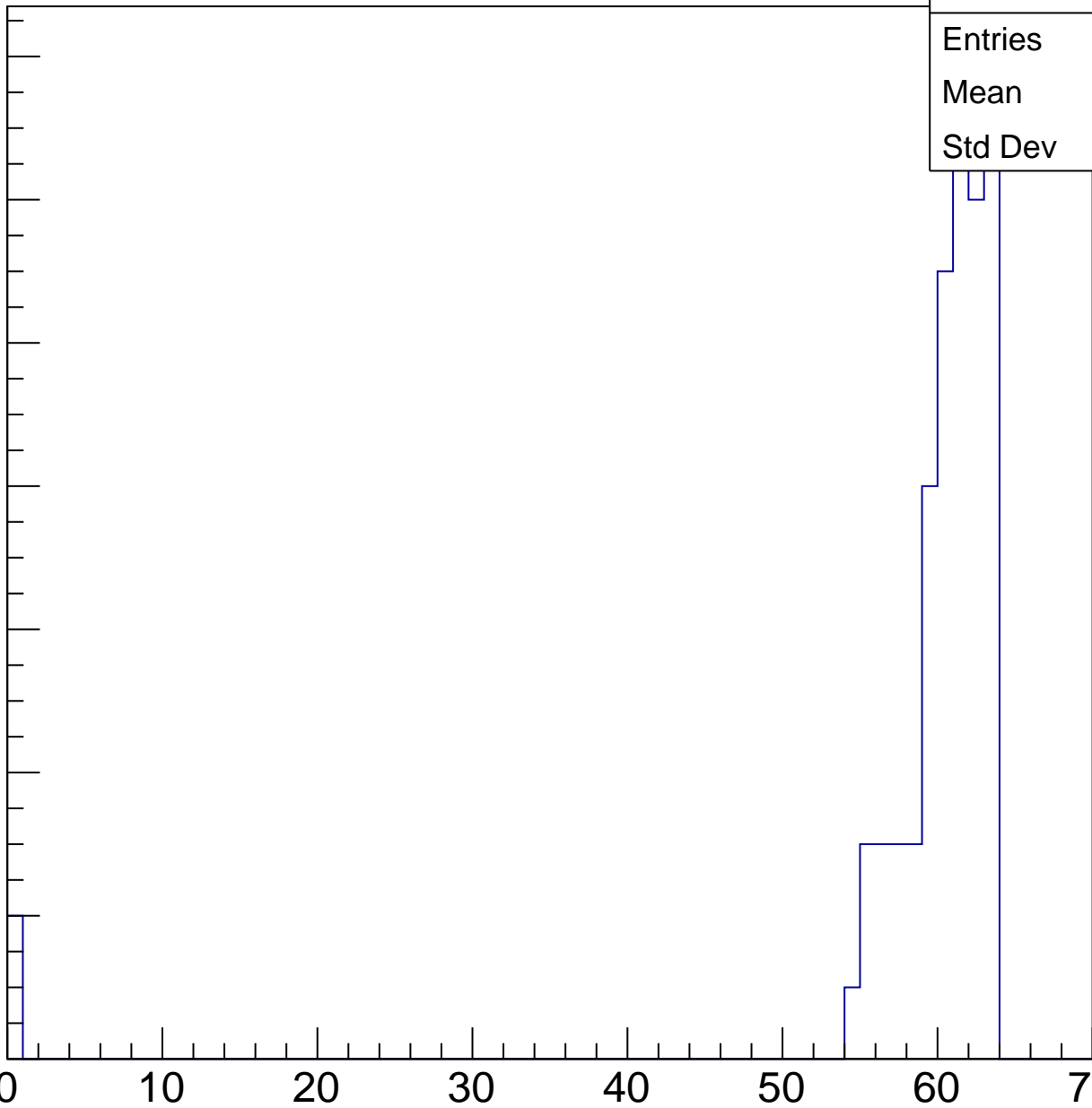
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	58.67
Std Dev	10.11

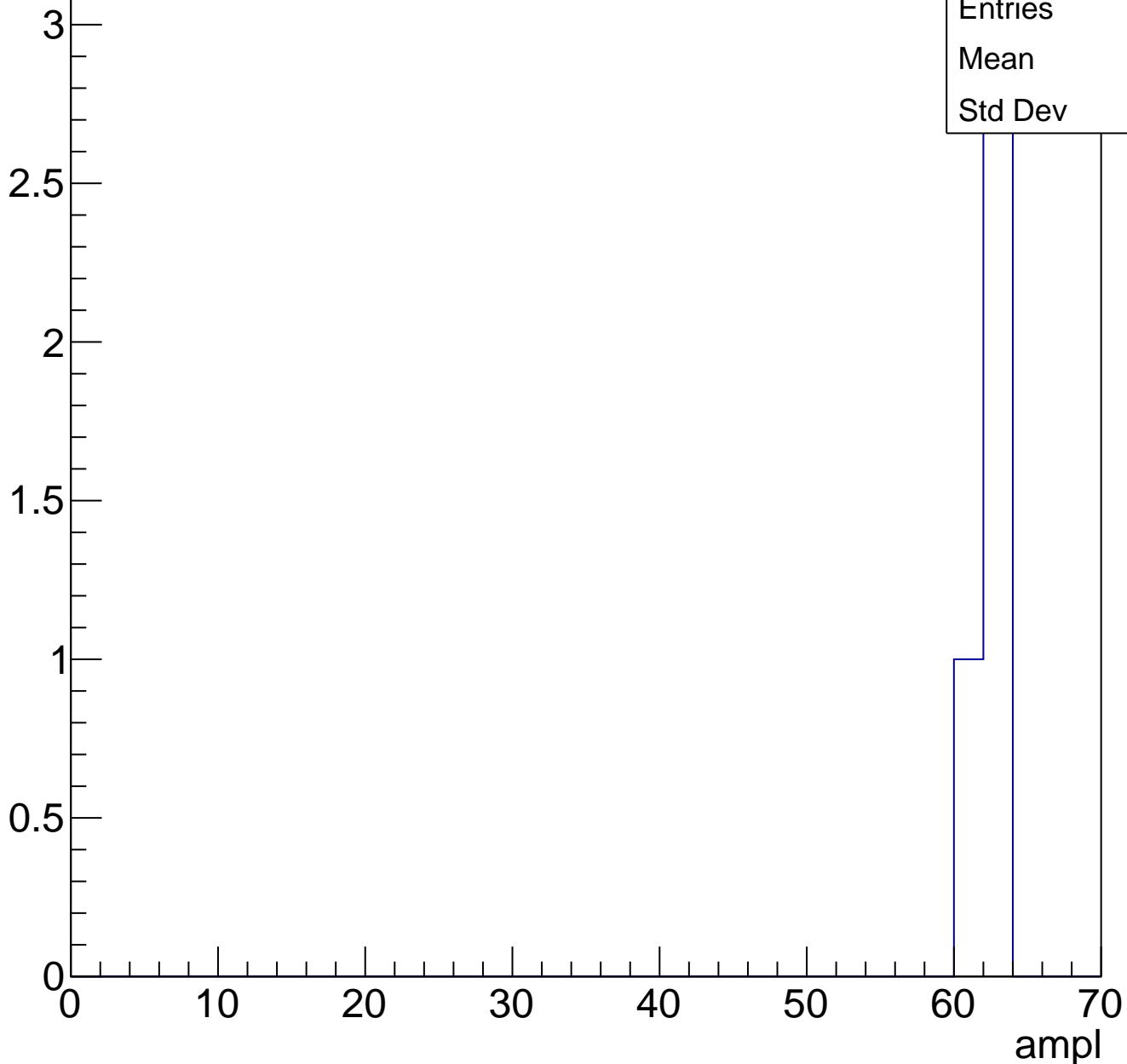
ampl



B0L101S, U18-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L101S, U18-ch4, adc0

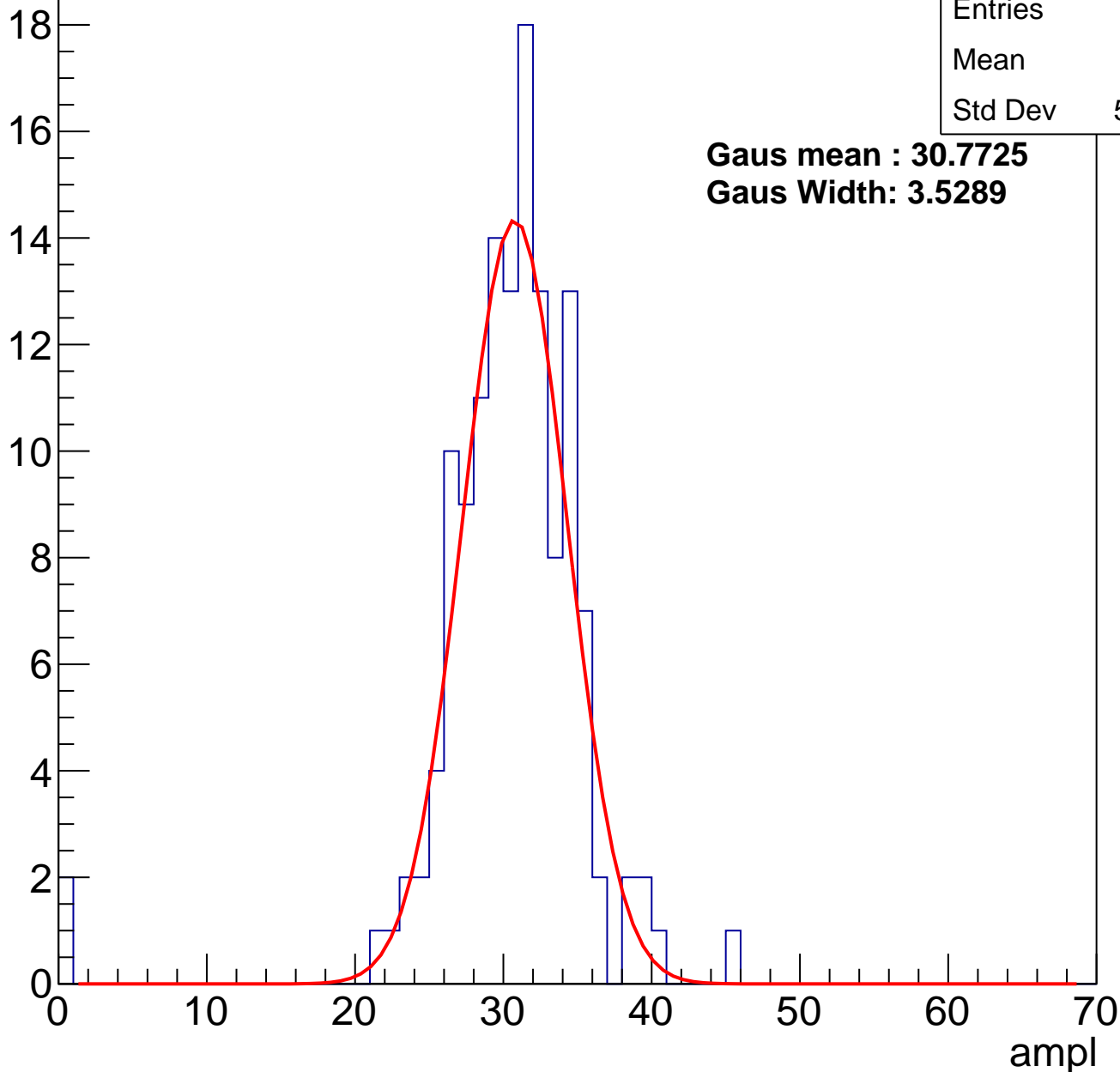
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	30
Std Dev	5.234

Gaus mean : 30.7725

Gaus Width: 3.5289

Entry



B0L101S, U18-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.79
Std Dev	3.521

Gaus mean : 37.3553

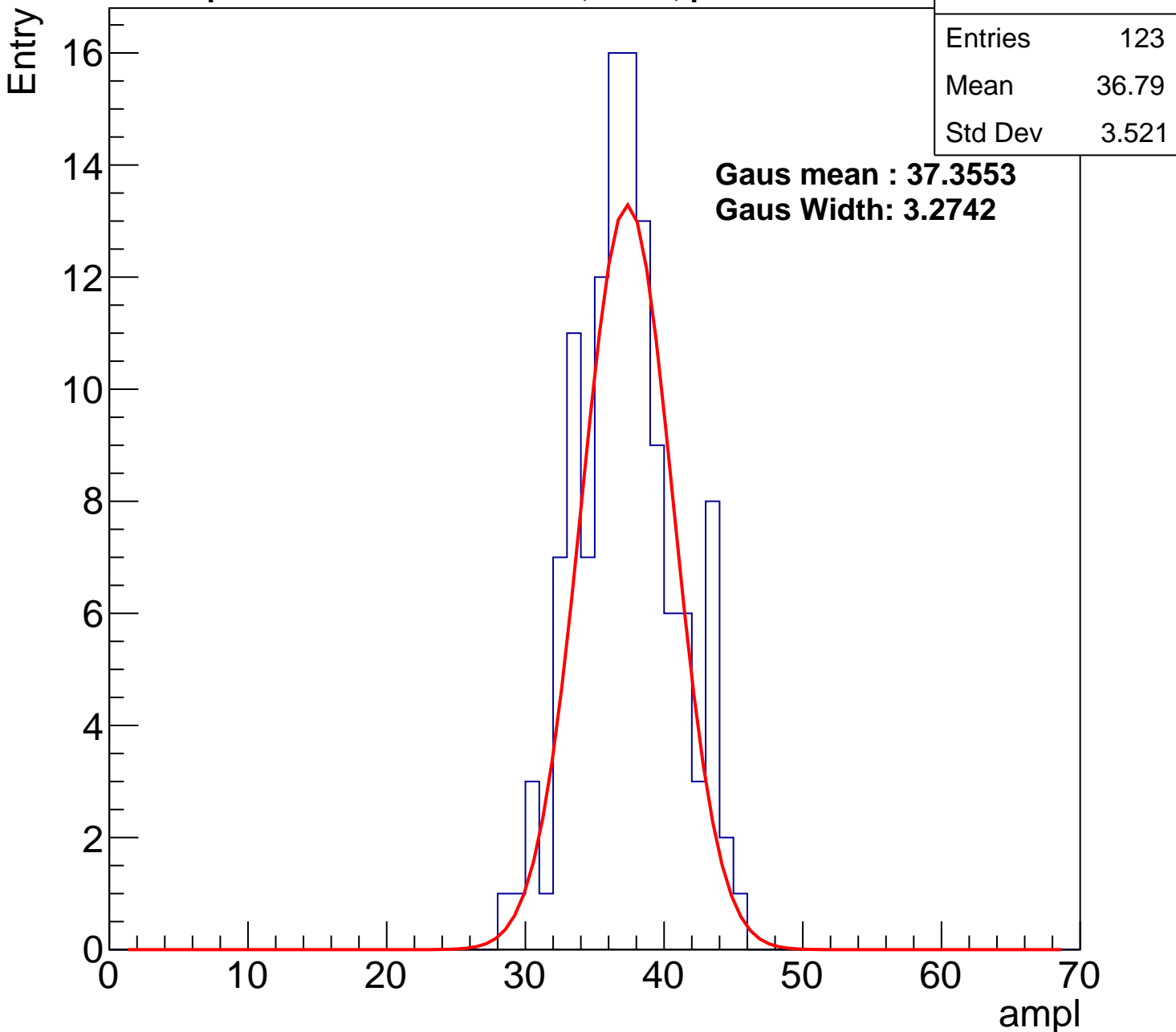
Gaus Width: 3.2742

Entry

16
14
12
10
8
6
4
2
0

ampl

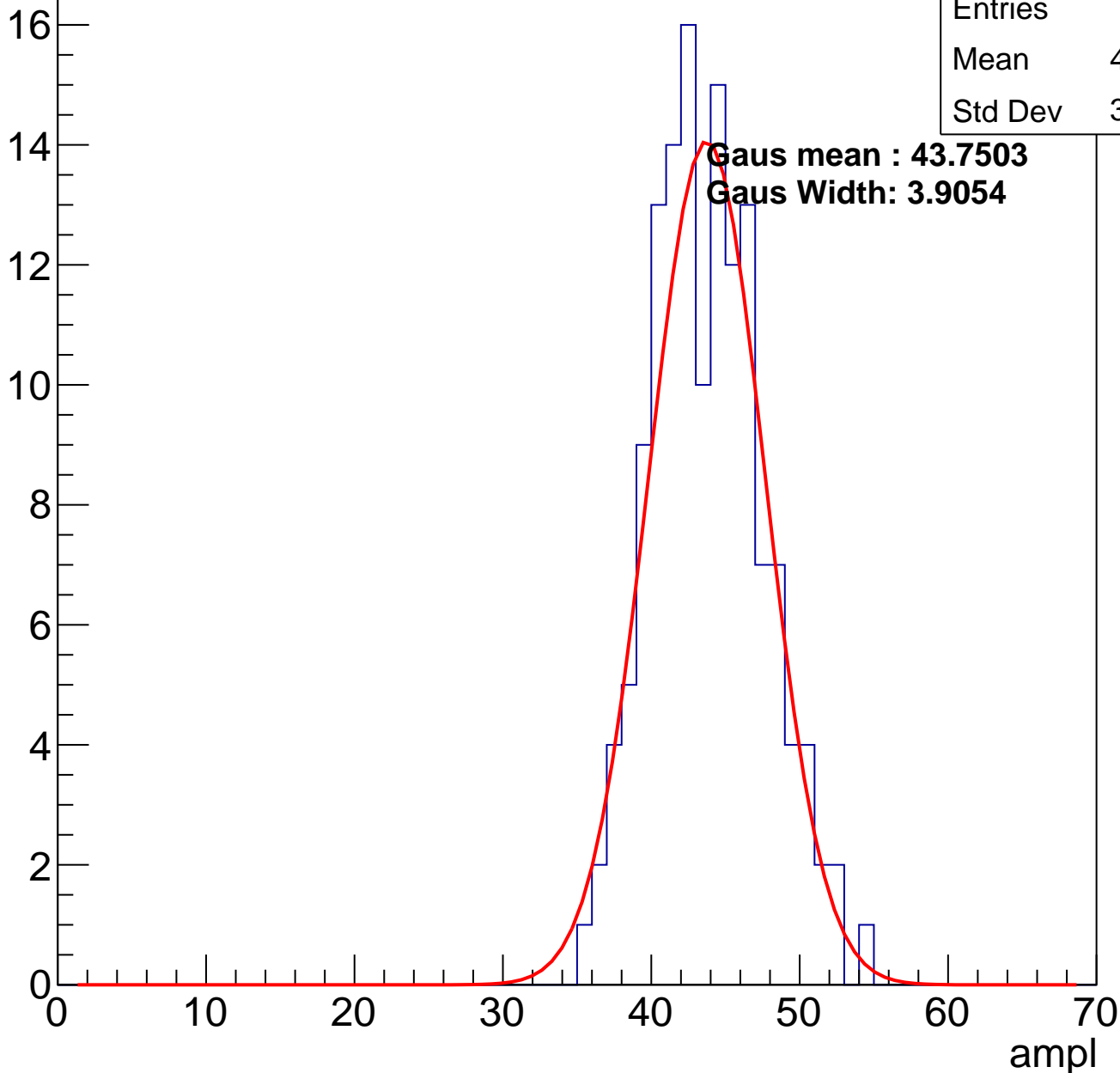
0 10 20 30 40 50 60 70



B0L101S, U18-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch4, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	49.95
Std Dev	3.501

Entry

12

10

8

6

4

2

0

0

10

20

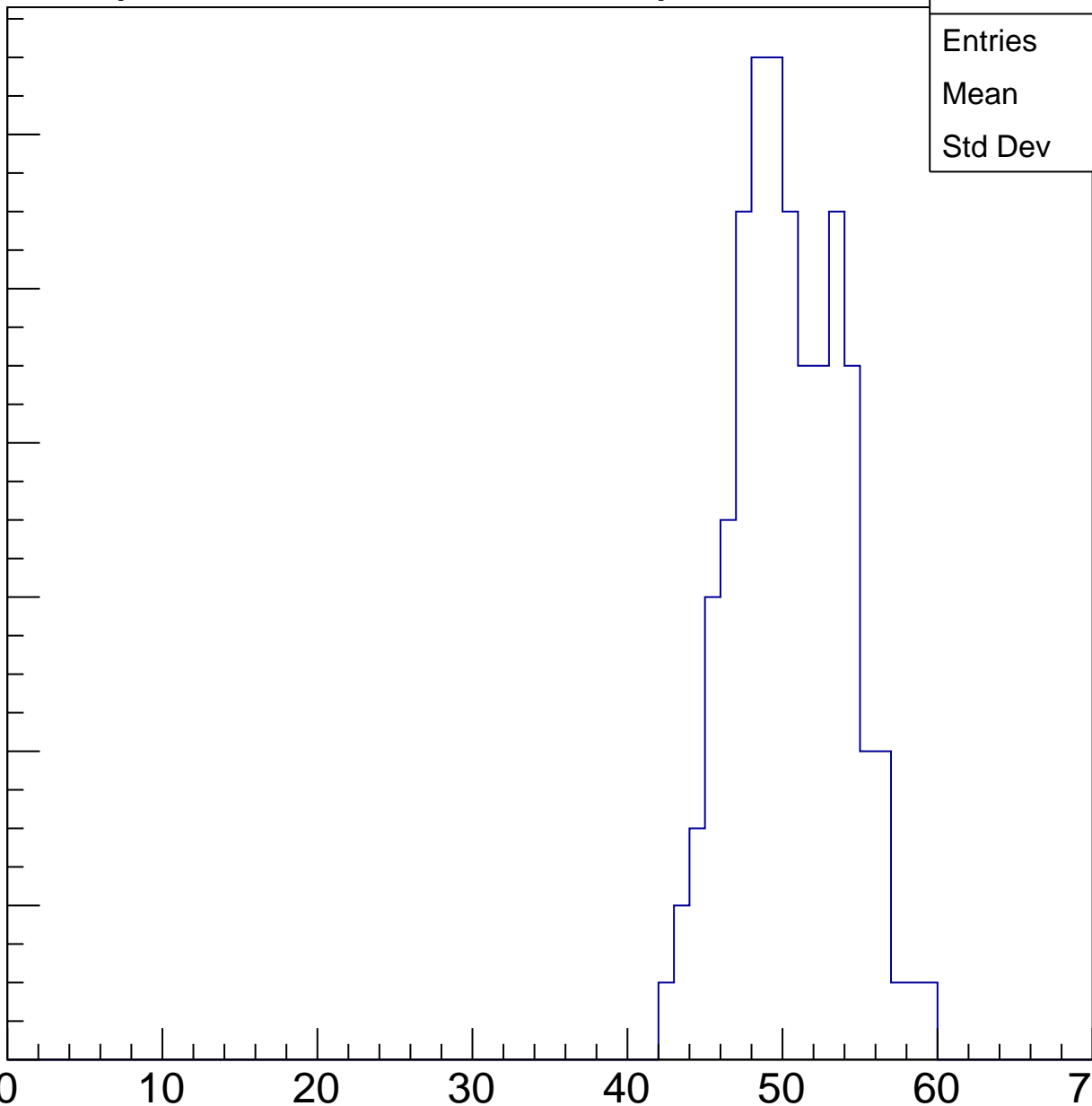
30

40

50

60

ampl



B0L101S, U18-ch4, adc4

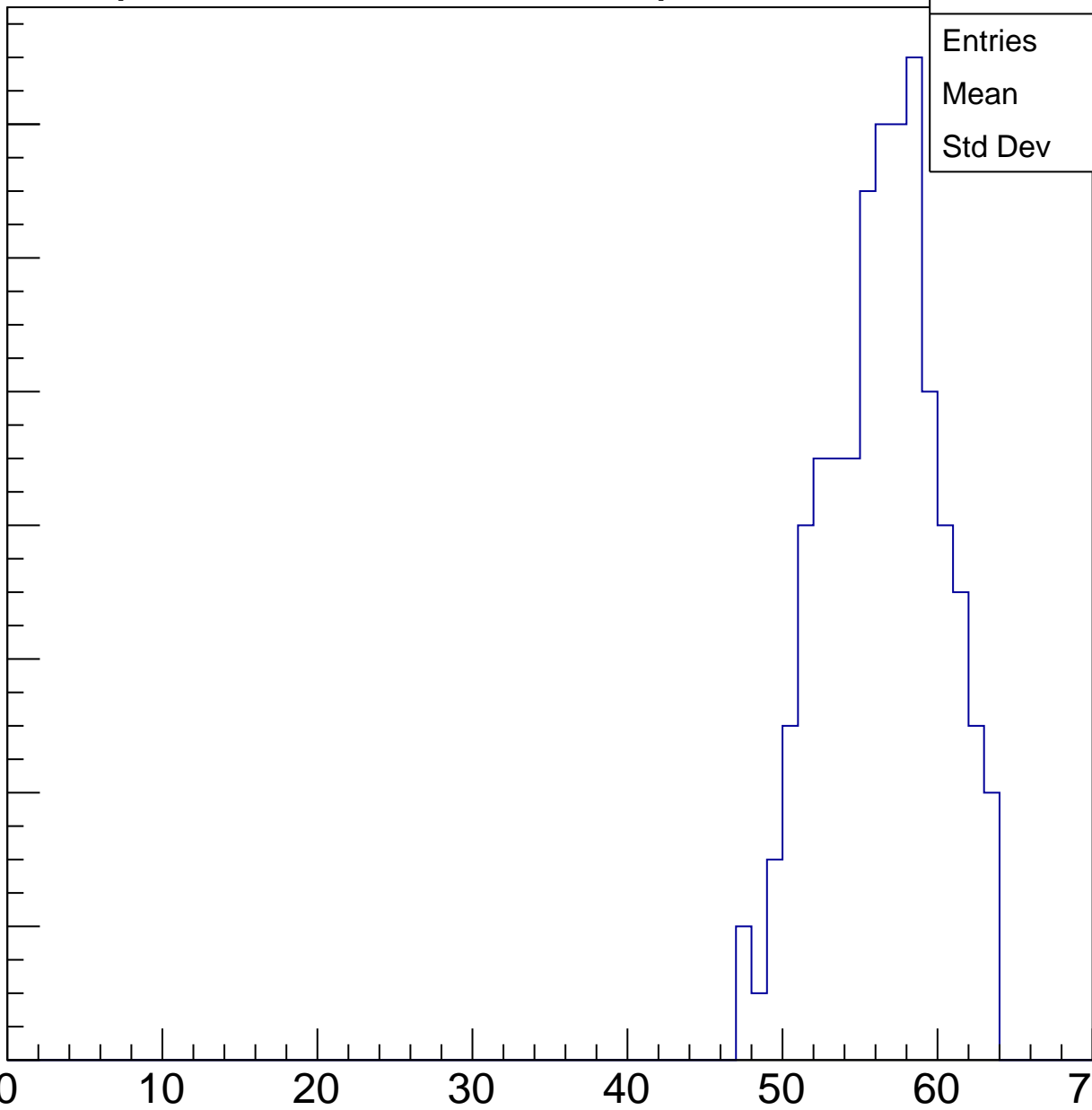
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	136
Mean	55.91
Std Dev	3.707

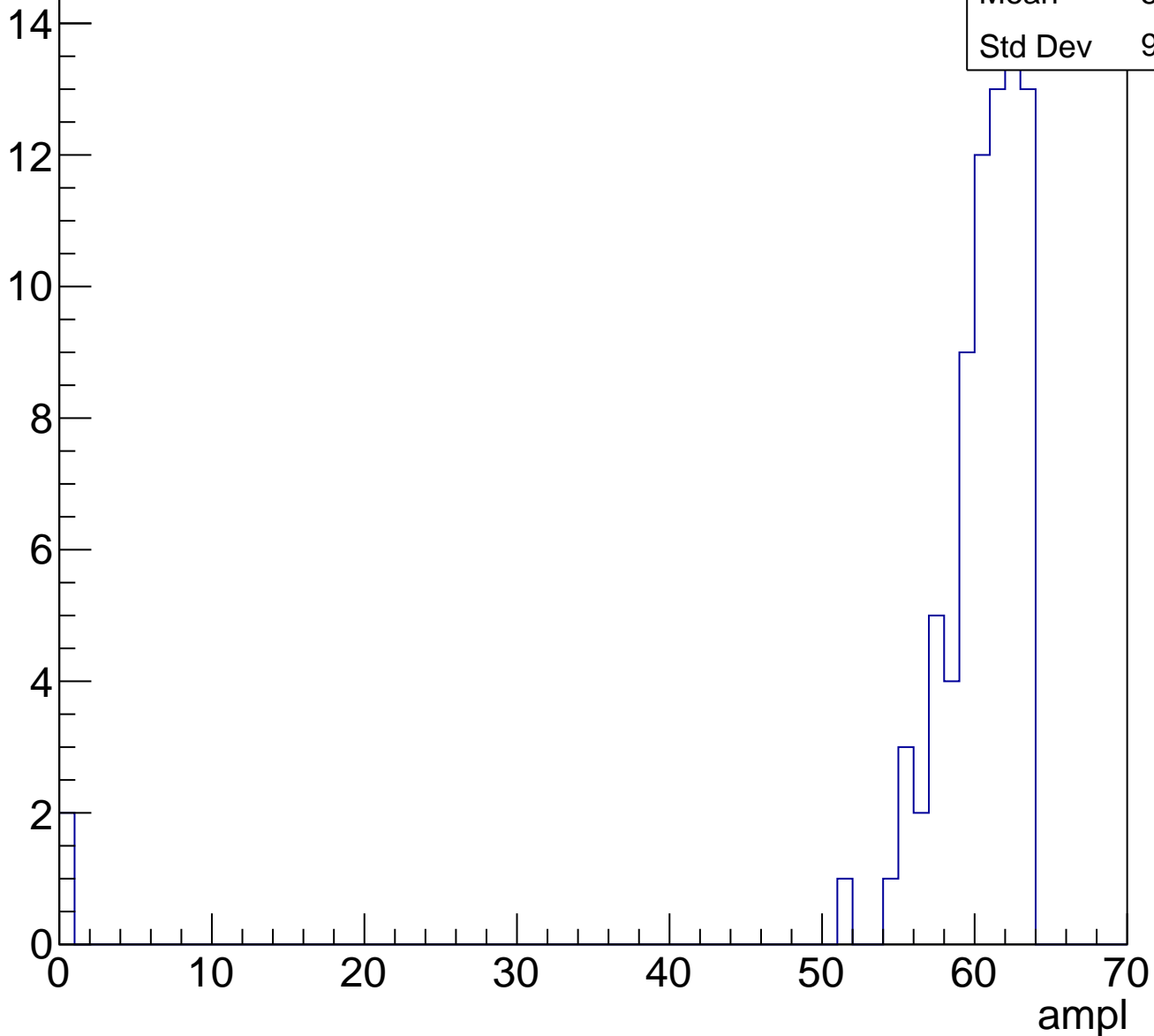
ampl



B0L101S, U18-ch4, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch5, adc0

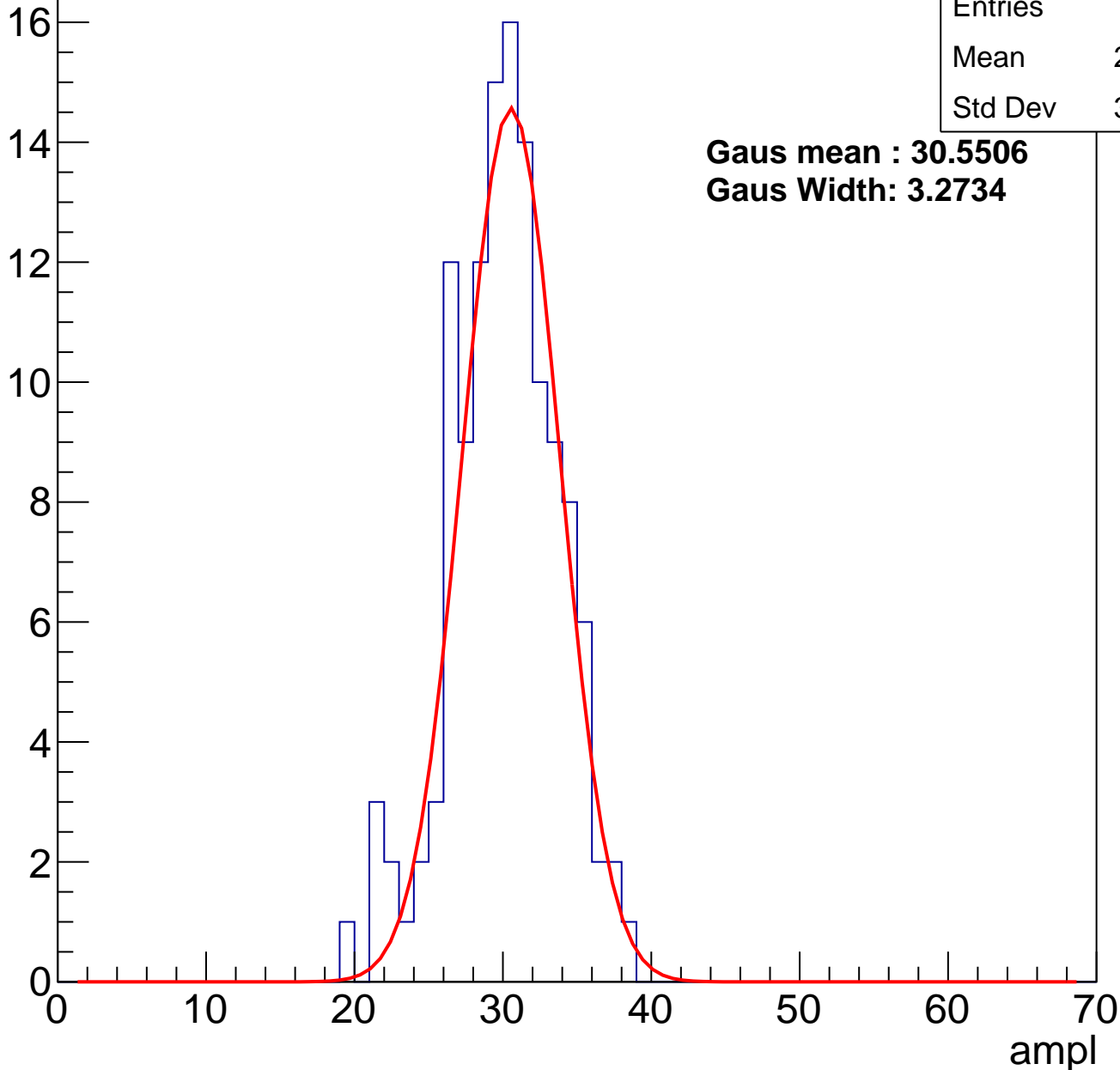
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	29.65
Std Dev	3.596

Gaus mean : 30.5506

Gaus Width: 3.2734

Entry



B0L101S, U18-ch5, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	36.87
Std Dev	3.609

Gaus mean : 37.1634

Gaus Width: 3.4116

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

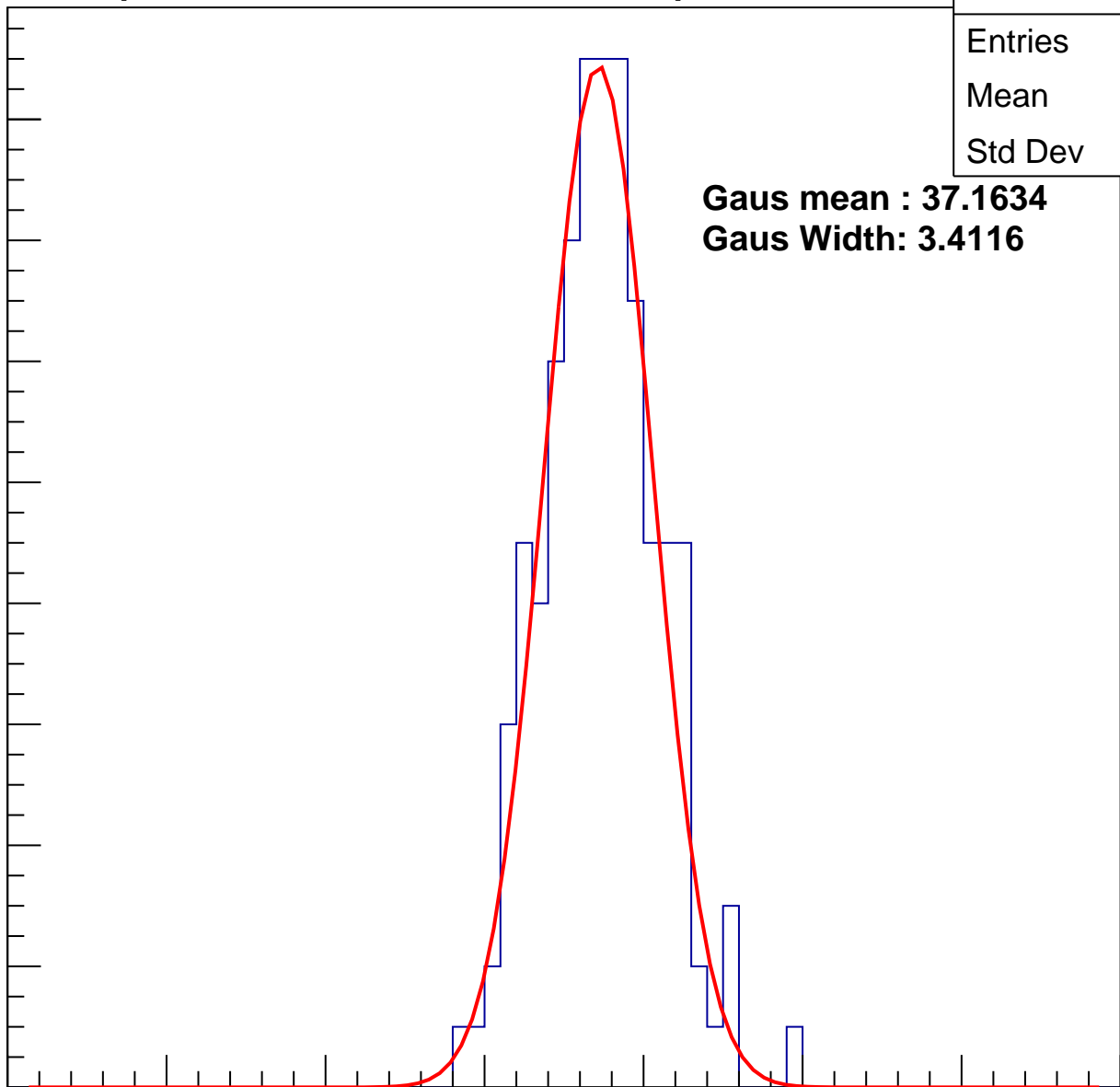
40

50

60

70

ampl



B0L101S, U18-ch5, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	43.65
Std Dev	3.326

Gaus mean : 44.2047

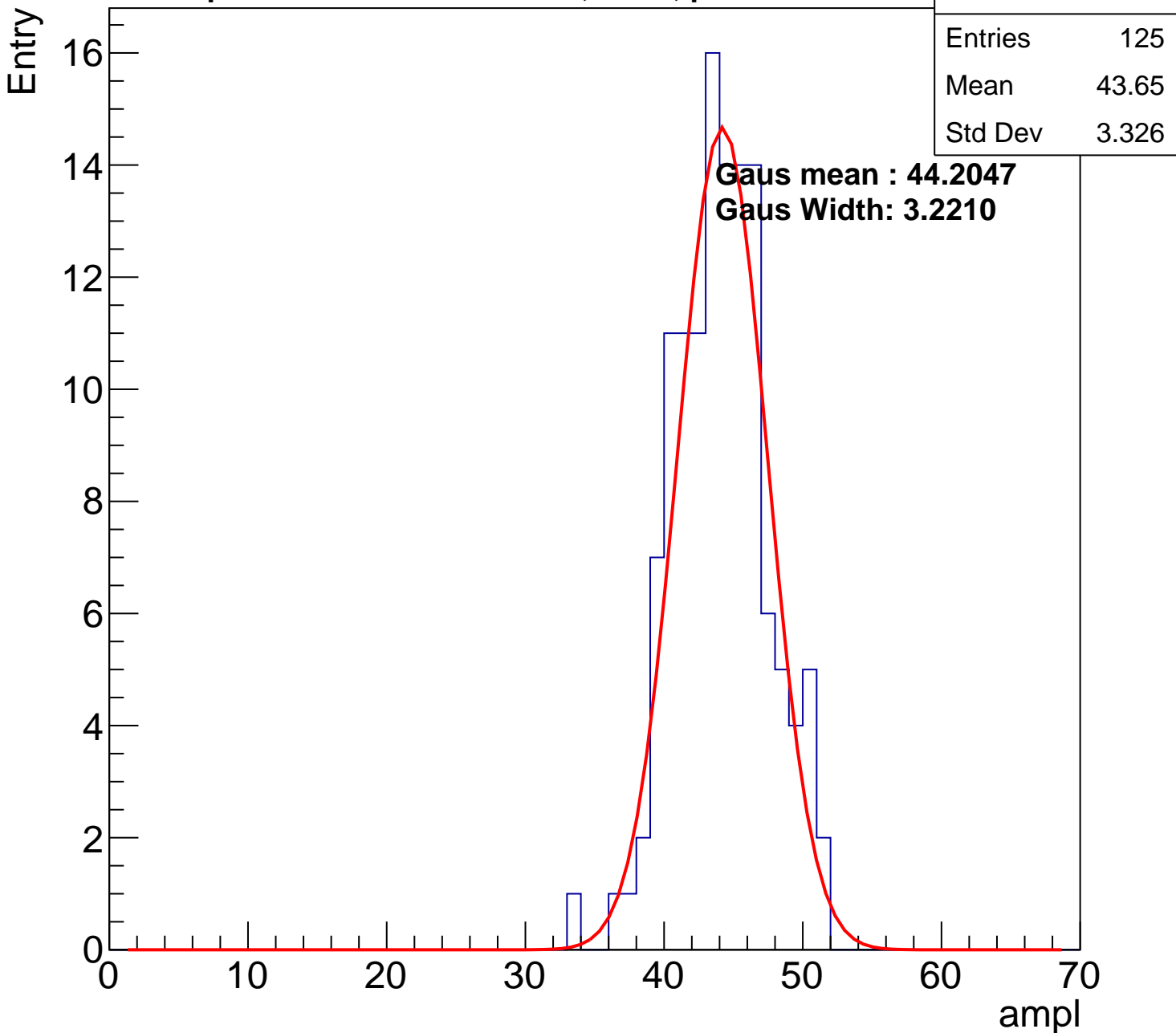
Gaus Width: 3.2210

Entry

16
14
12
10
8
6
4
2
0

ampl

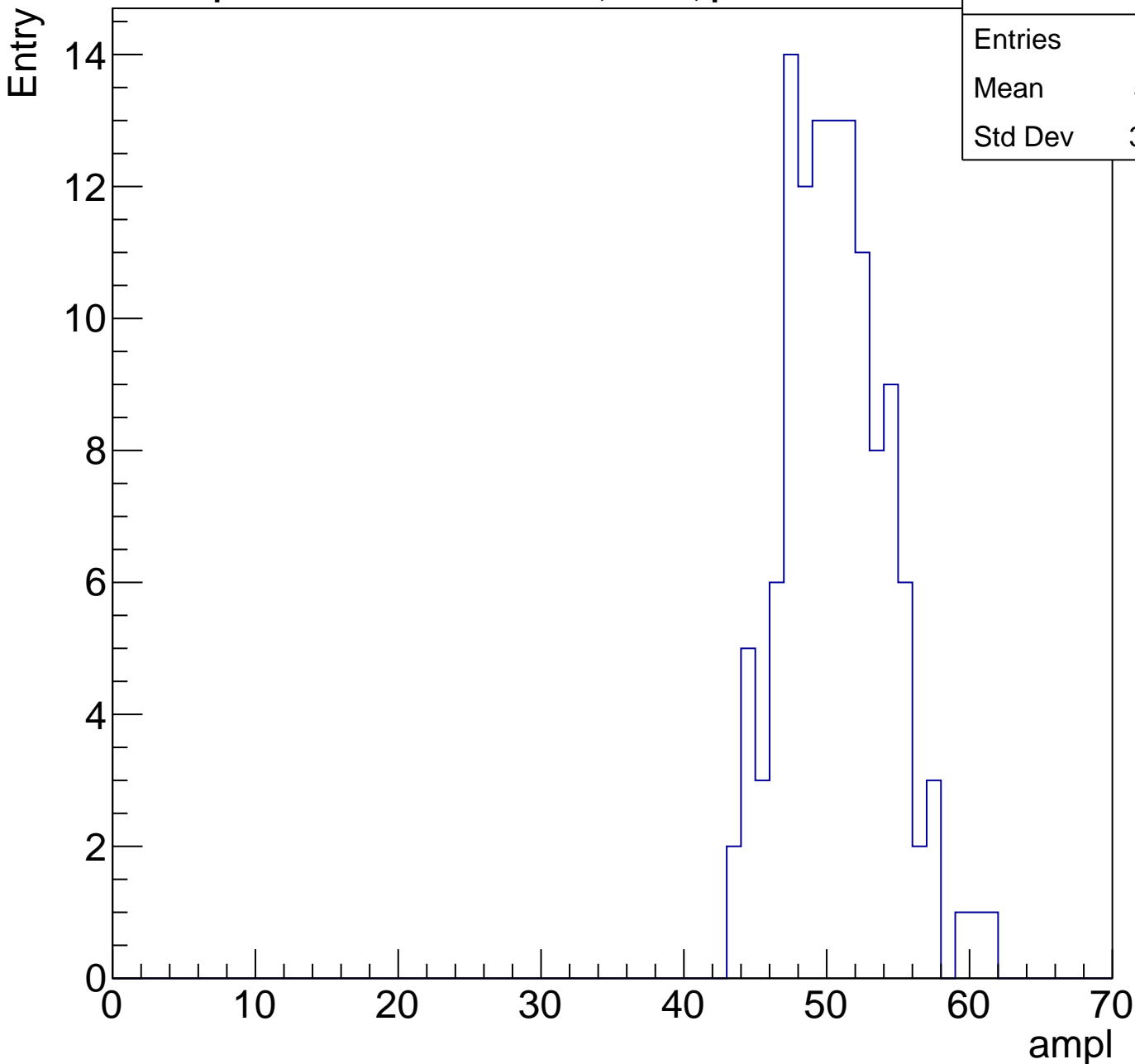
0 10 20 30 40 50 60 70



B0L101S, U18-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	50.21
Std Dev	3.578



B0L101S, U18-ch5, adc4

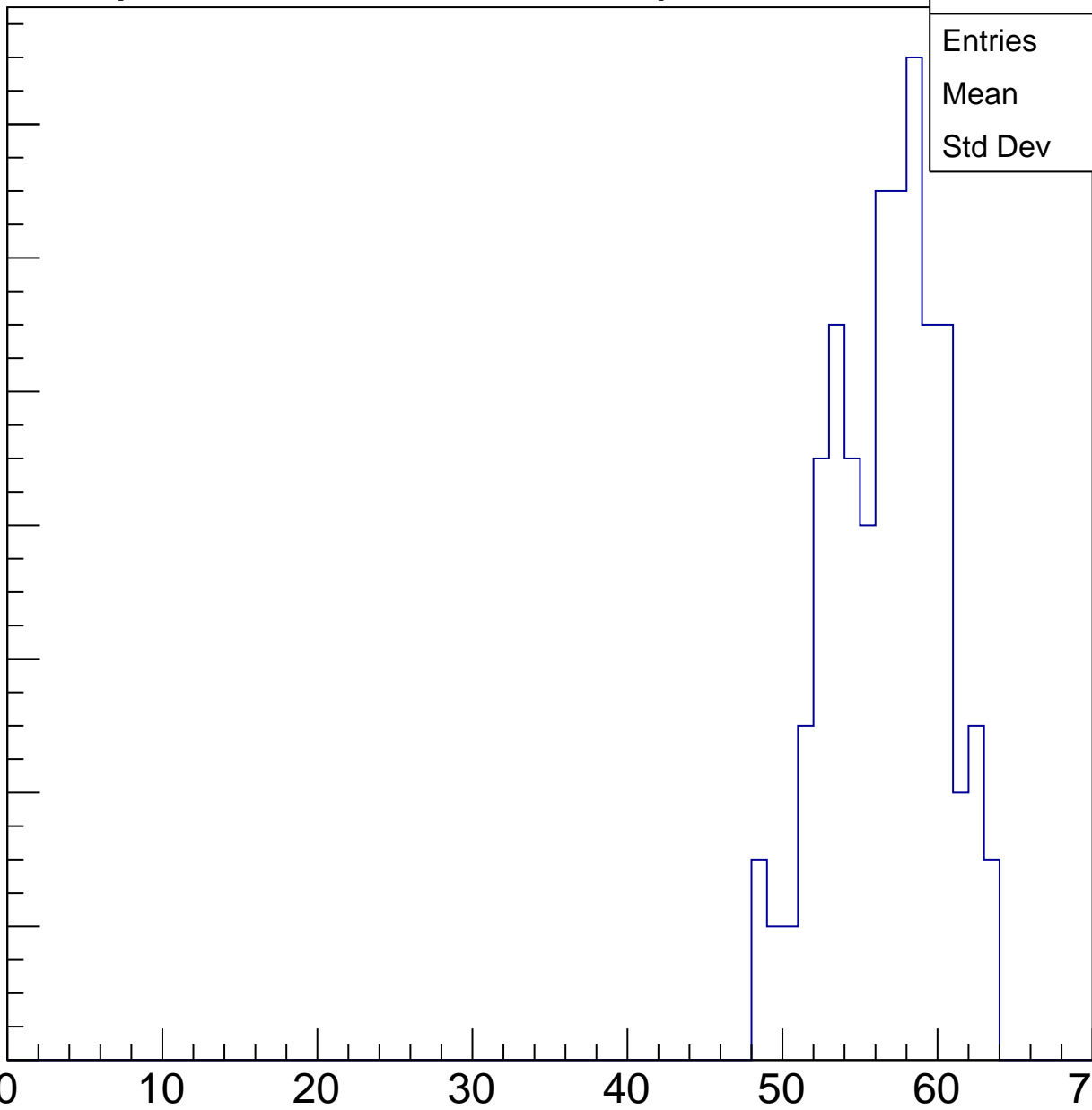
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	124
Mean	56.17
Std Dev	3.53

ampl



B0L101S, U18-ch5, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

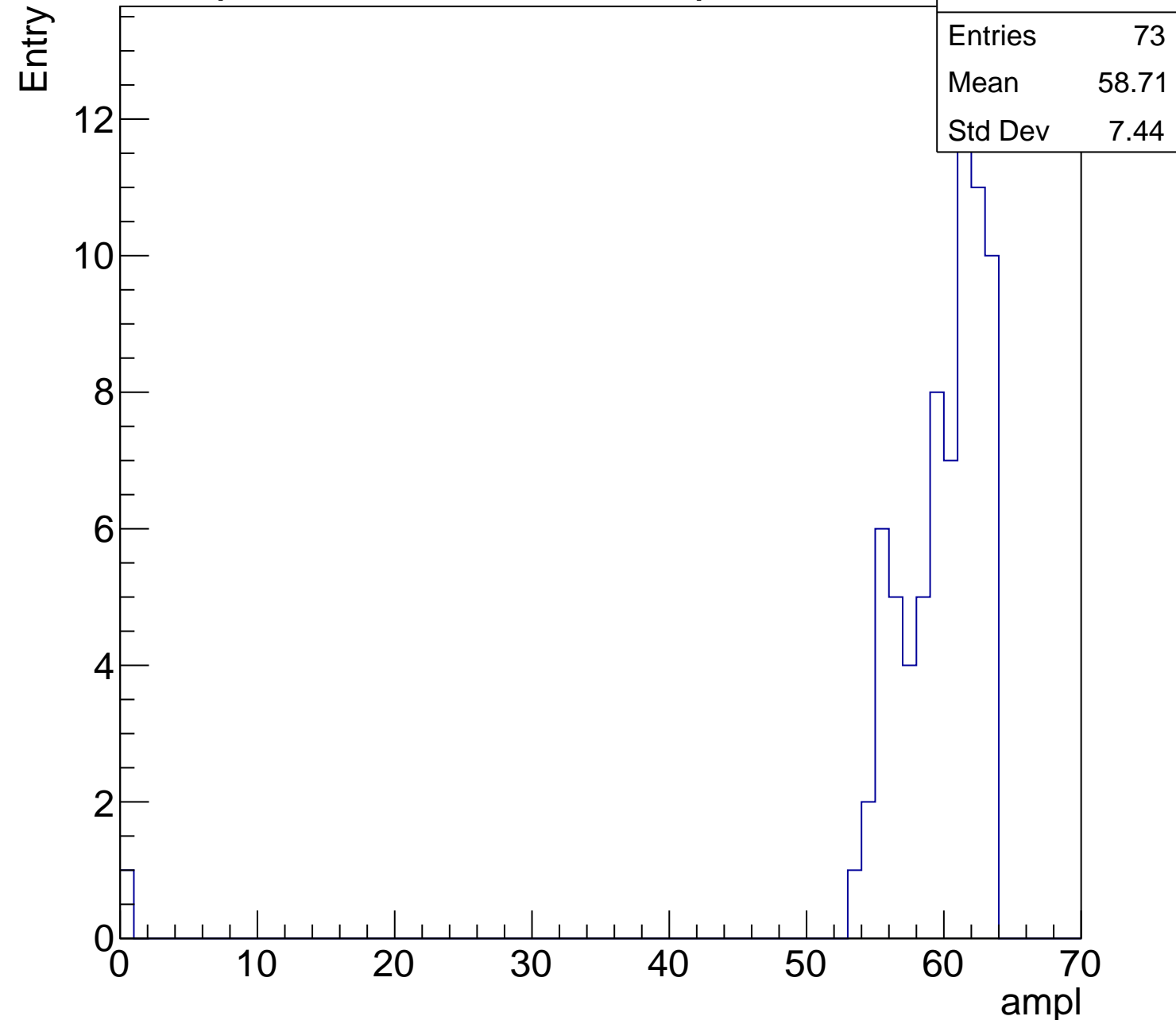
40

50

60

ampl

Entries	73
Mean	58.71
Std Dev	7.44

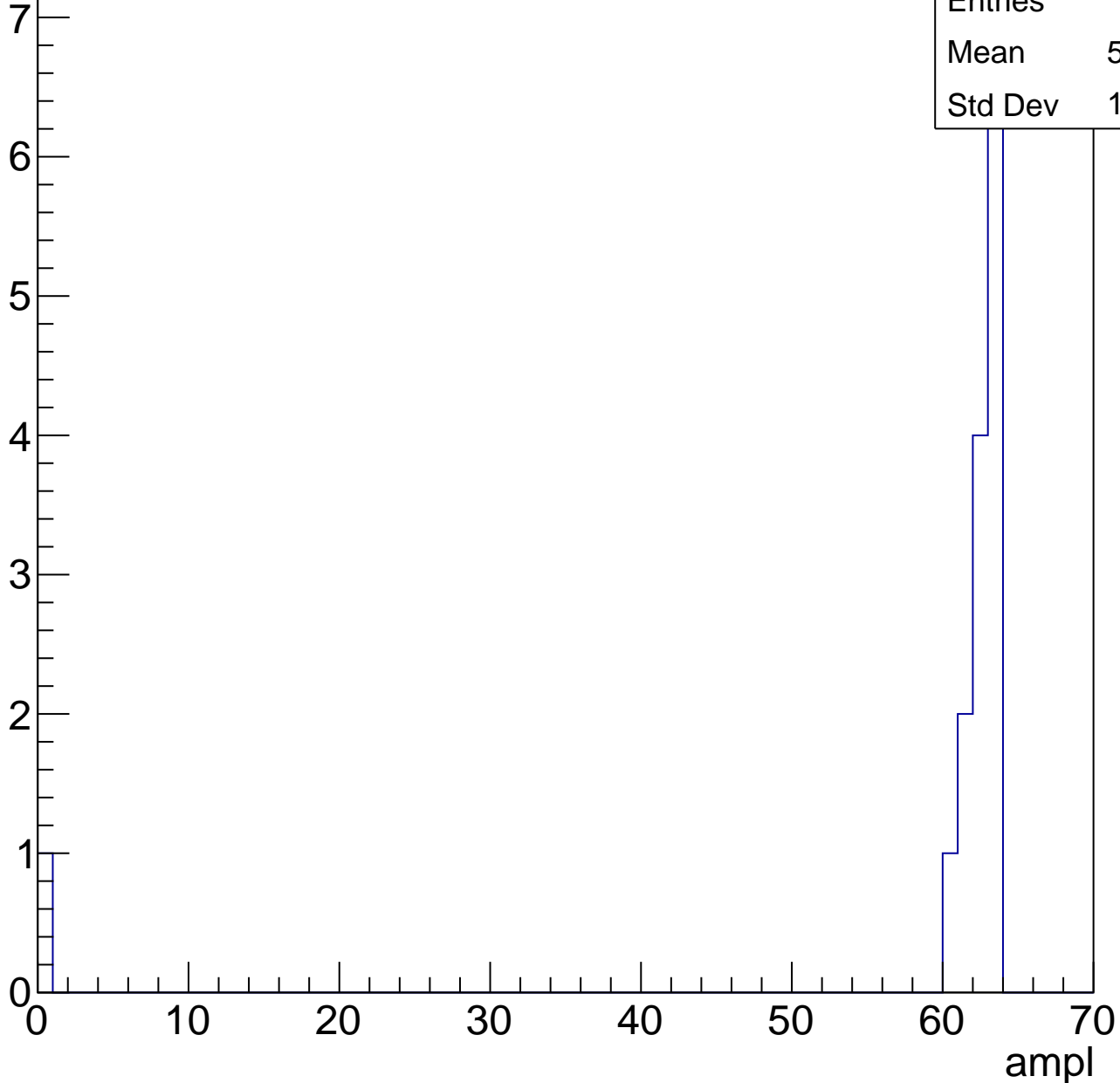


B0L101S, U18-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	58.07
Std Dev	15.55



B0L101S, U18-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



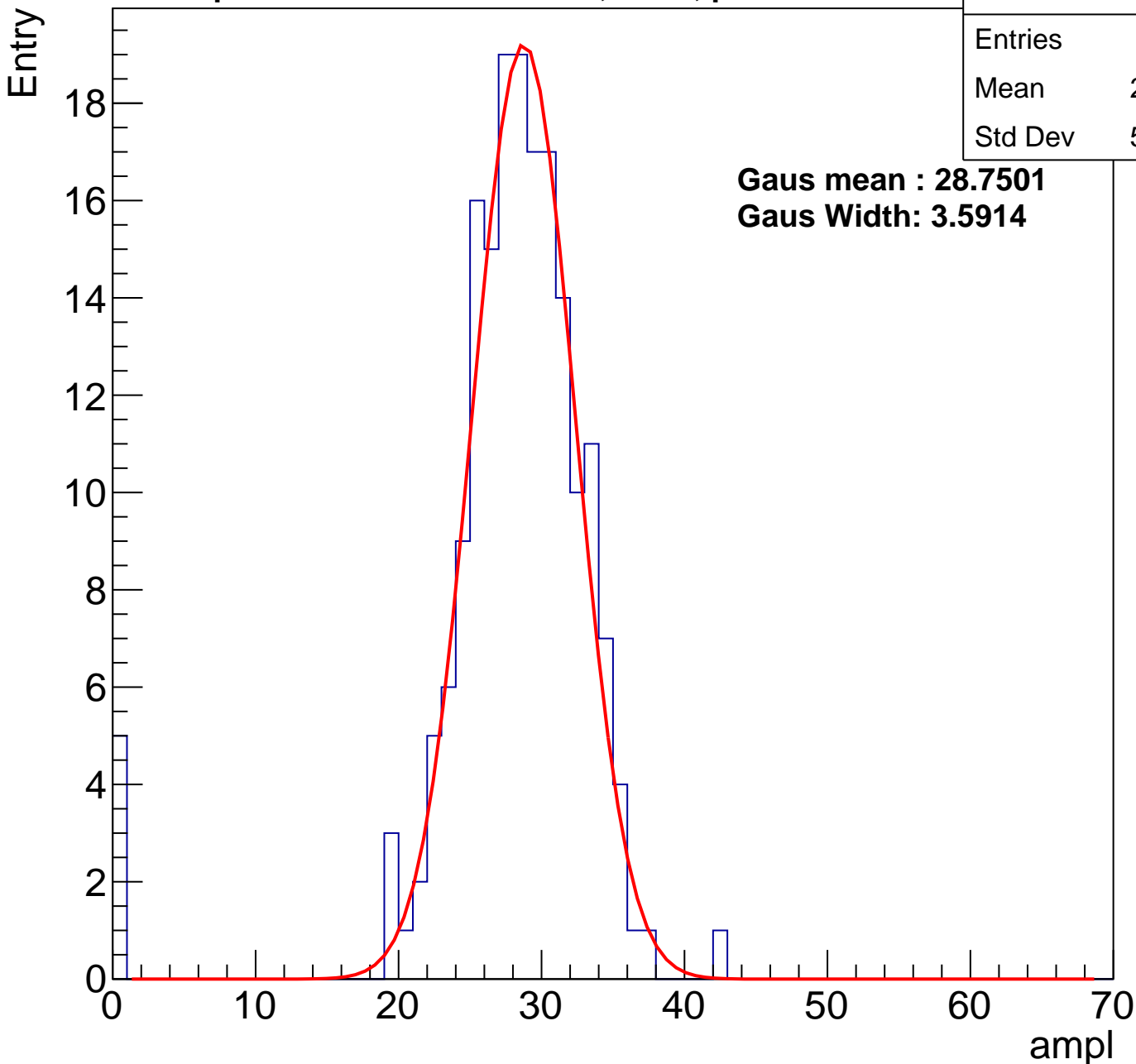
Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch6, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	183
Mean	27.49
Std Dev	5.918

Gaus mean : 28.7501
Gaus Width: 3.5914



B0L101S, U18-ch6, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	35.99
Std Dev	3.472

Gaus mean : 35.9374

Gaus Width: 3.0803

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

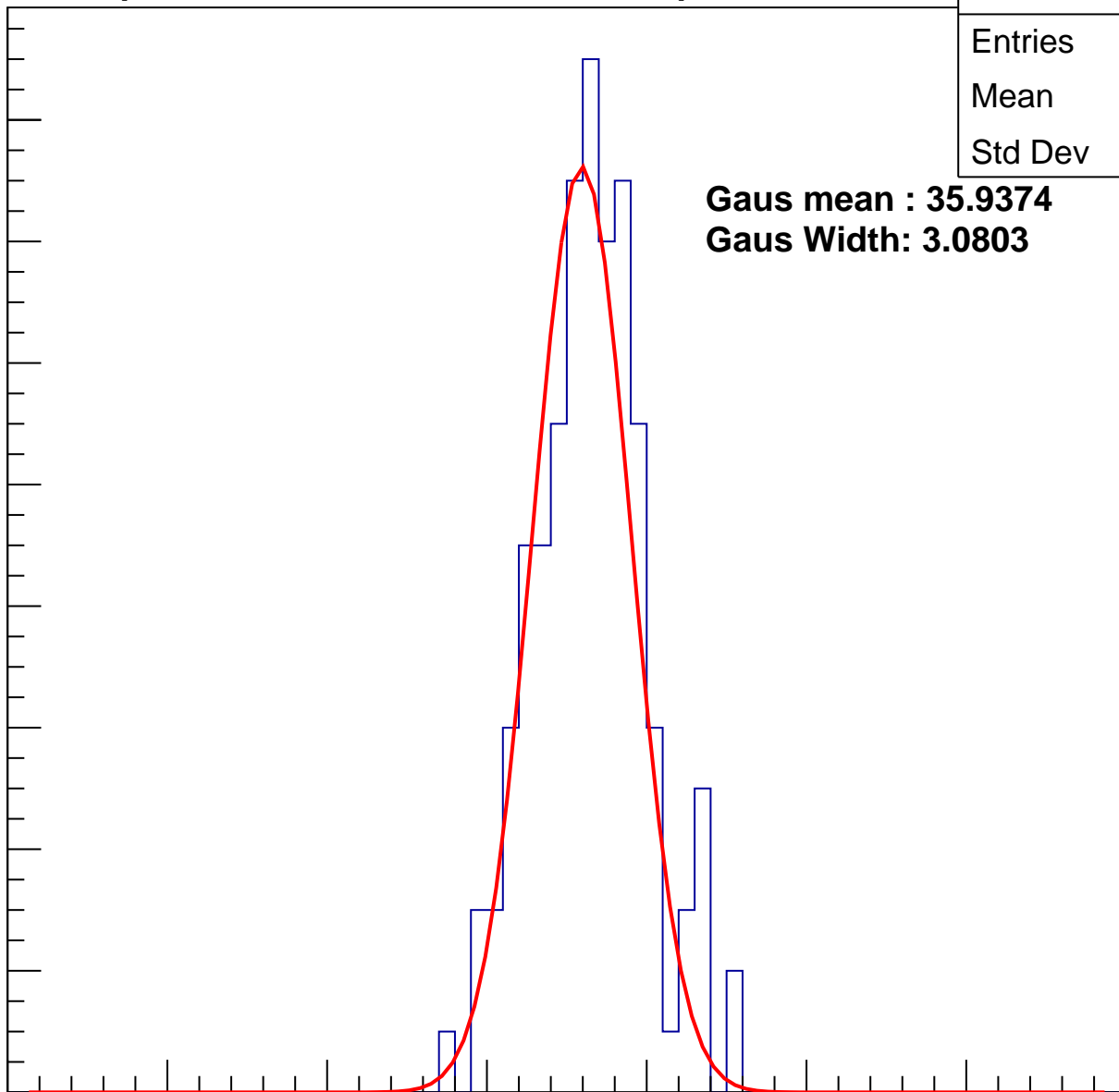
40

50

60

70

ampl



B0L101S, U18-ch6, adc2

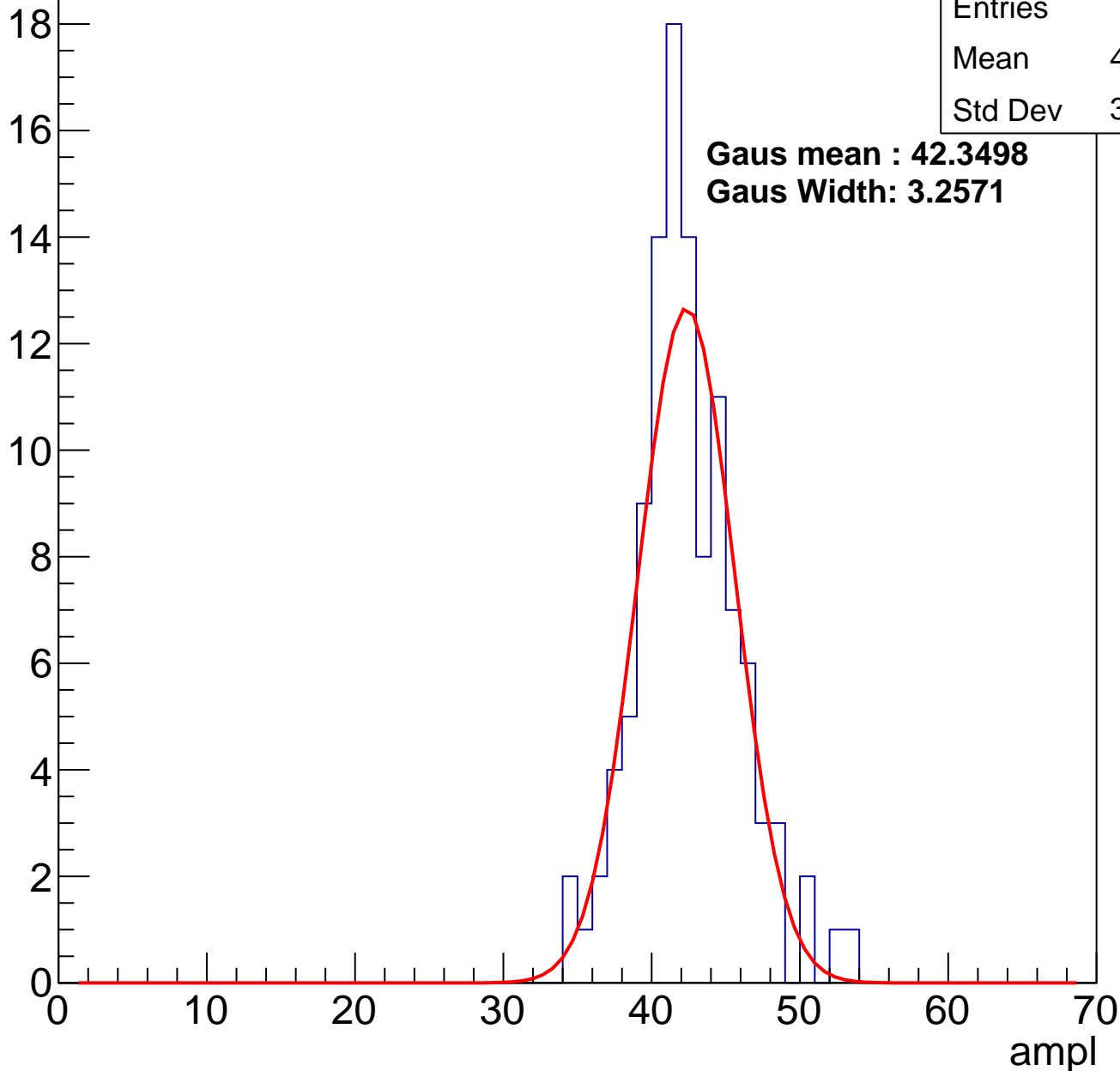
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	41.97
Std Dev	3.457

Gaus mean : 42.3498

Gaus Width: 3.2571

Entry



B0L101S, U18-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

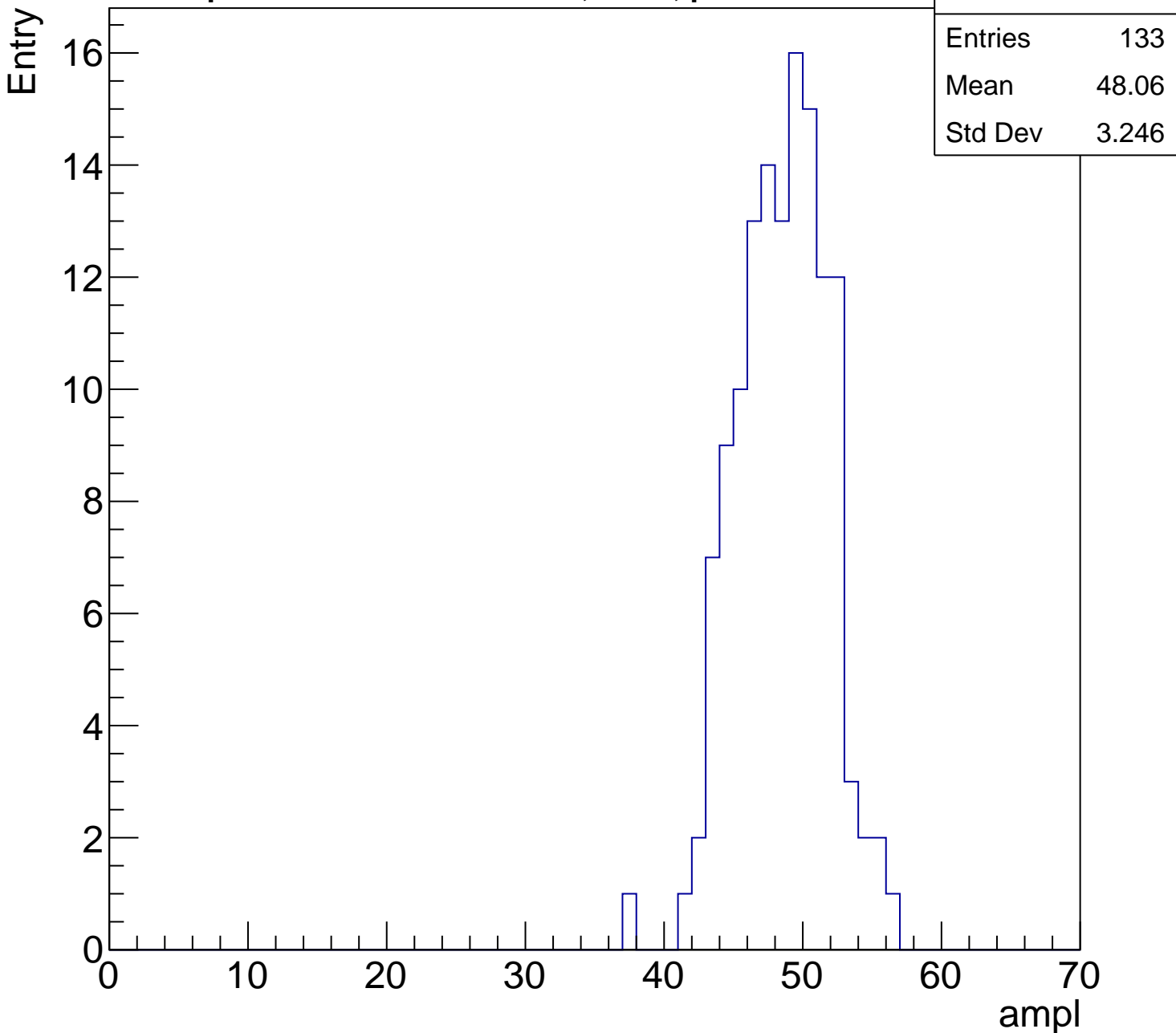
Entries	133
Mean	48.06
Std Dev	3.246

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

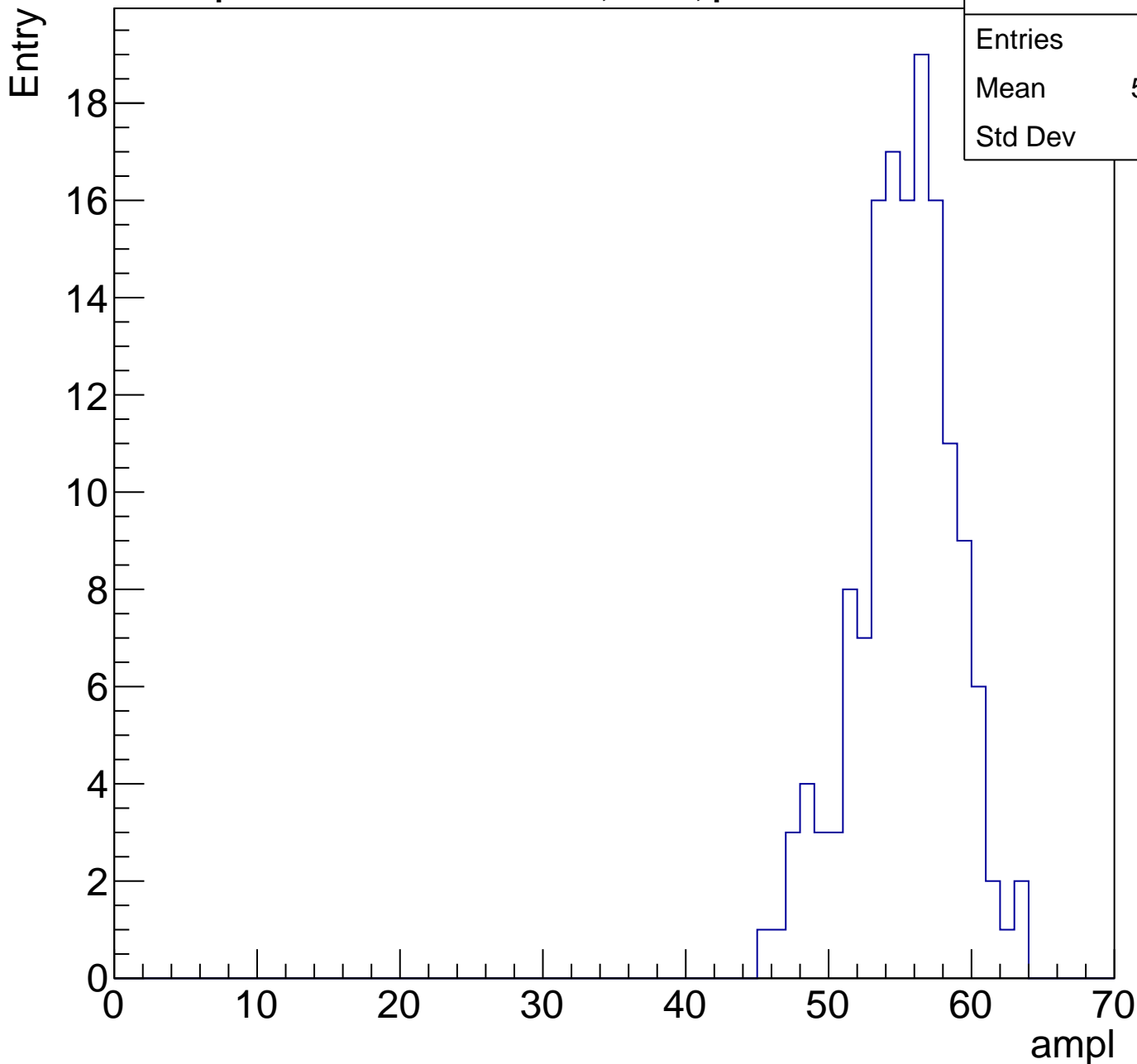
ampl



B0L101S, U18-ch6, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	54.86
Std Dev	3.47



B0L101S, U18-ch6, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

74

Mean

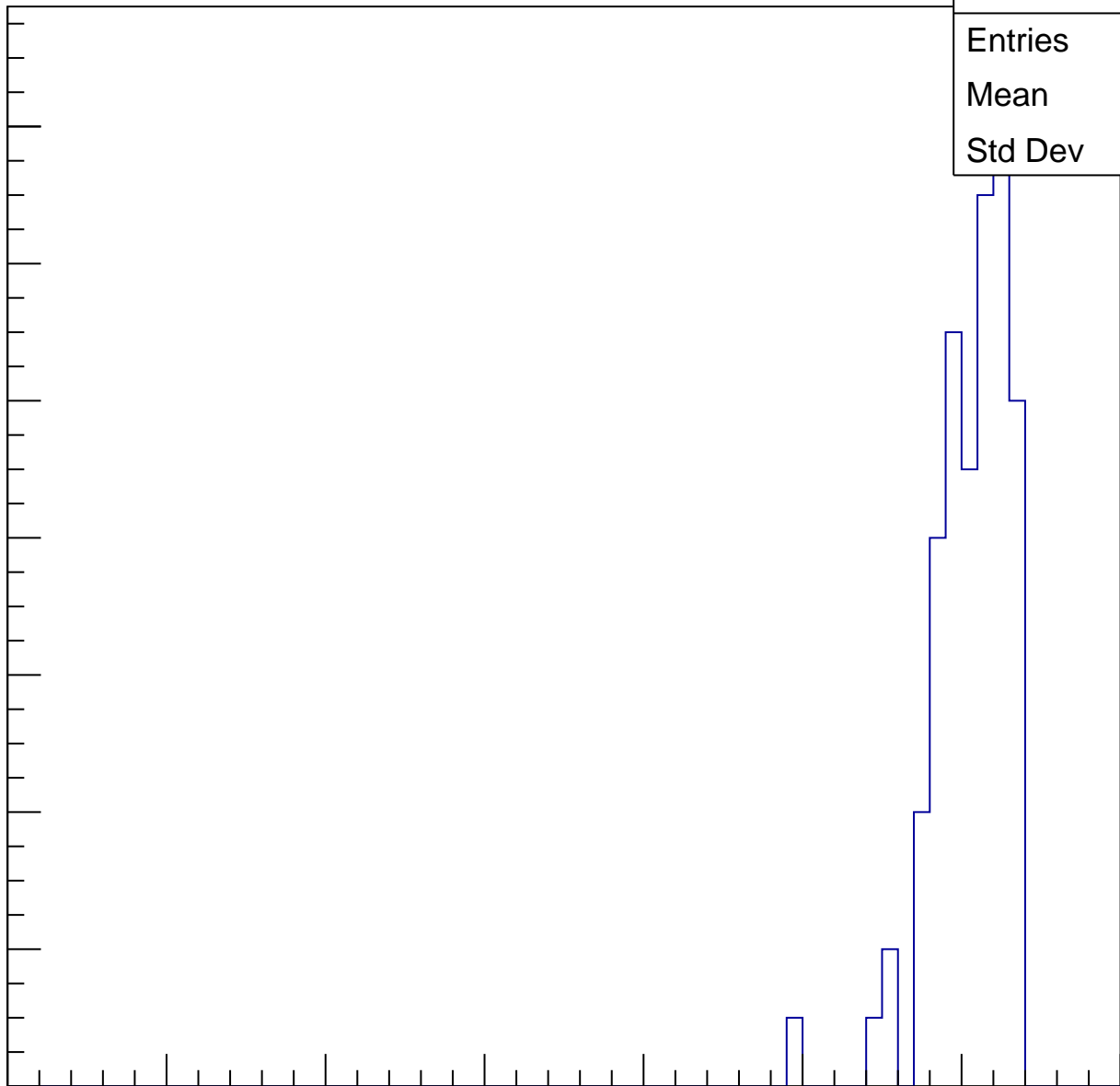
60.09

Std Dev

2.461

ampl

0 10 20 30 40 50 60 70

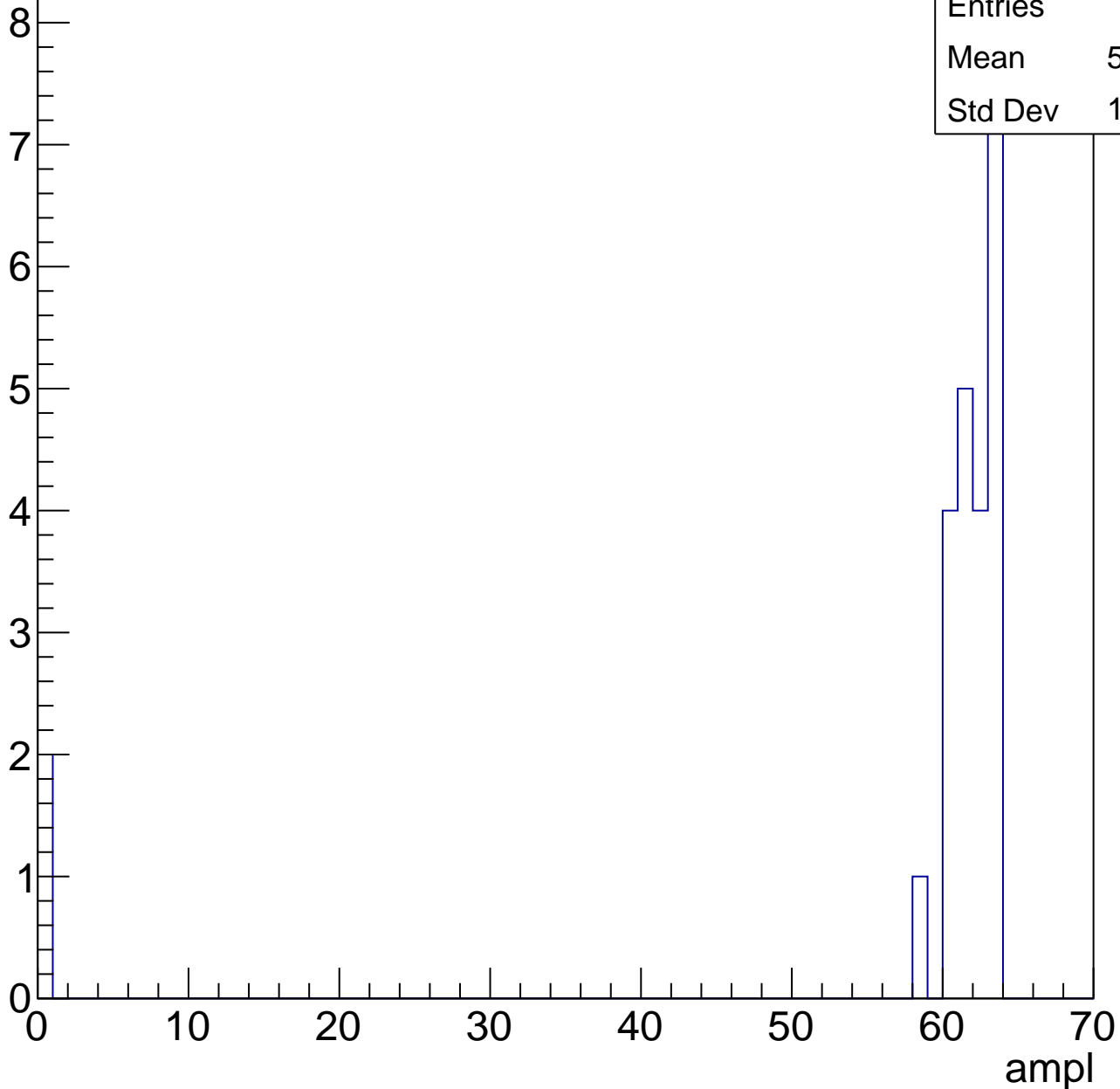


B0L101S, U18-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

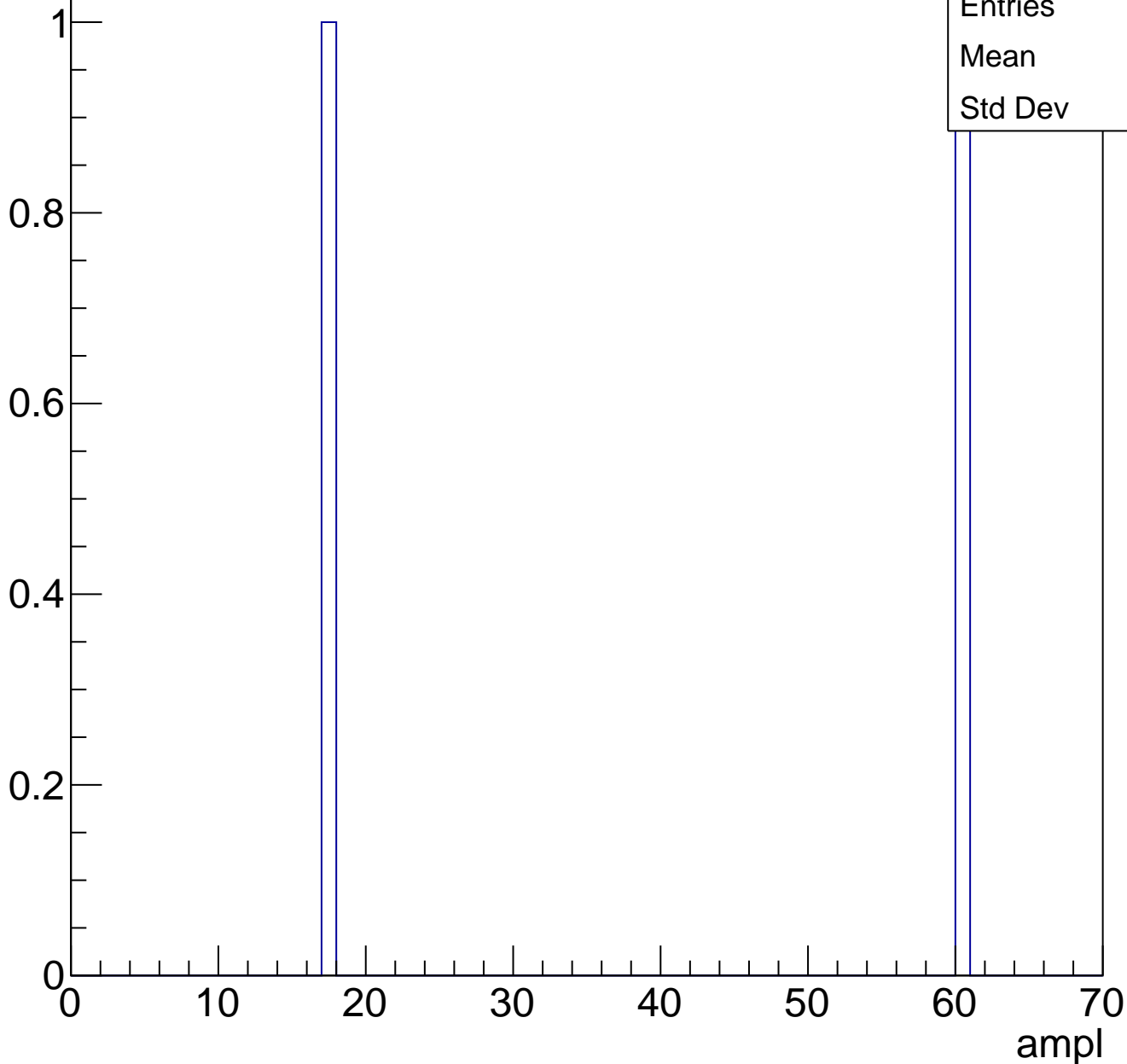
Entries	24
Mean	56.46
Std Dev	17.07



B0L101S, U18-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch7, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

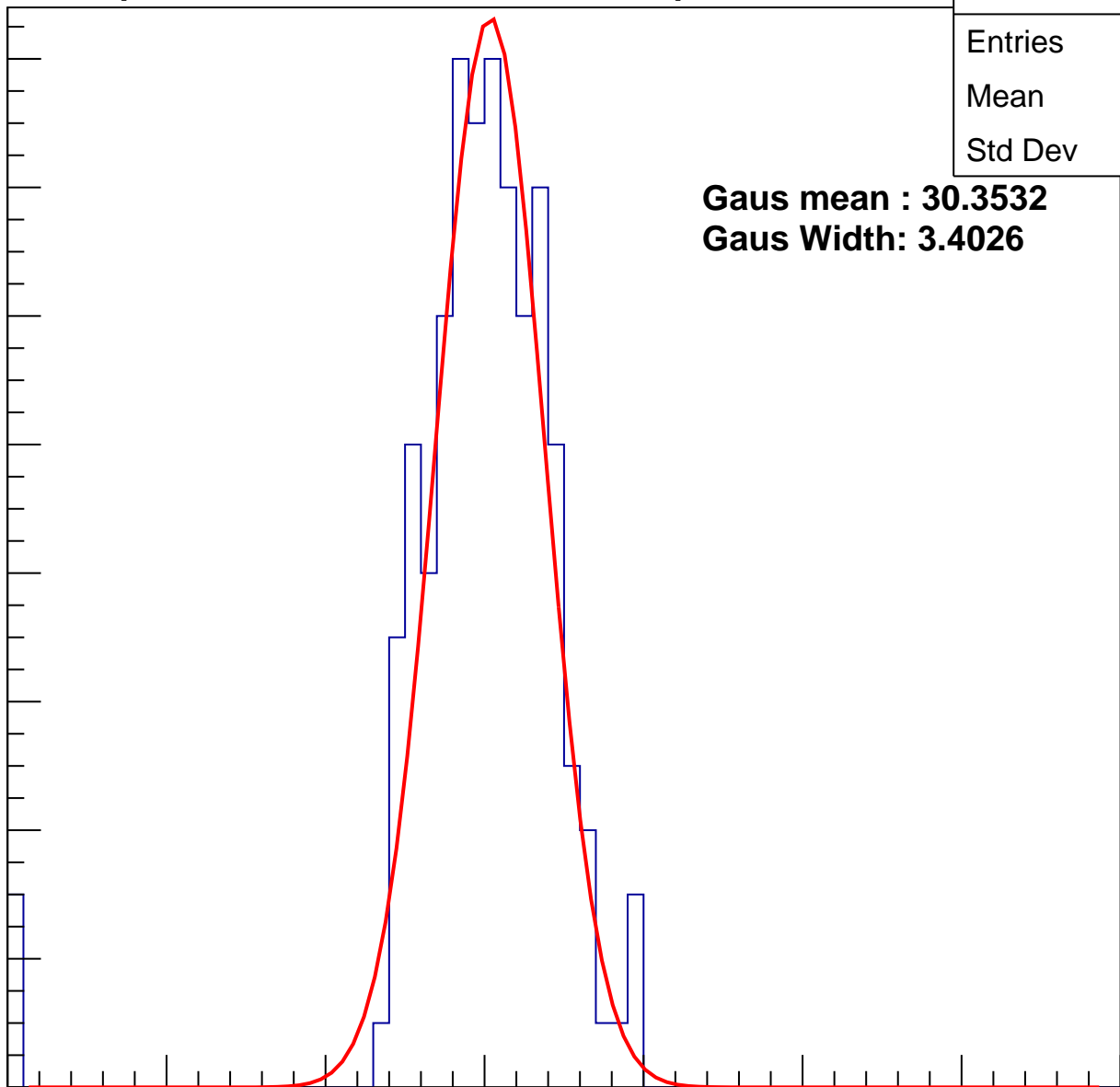
16
14
12
10
8
6
4
2
0

Entries	152
Mean	29.39
Std Dev	5.43

Gaus mean : 30.3532
Gaus Width: 3.4026

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch7, adc1

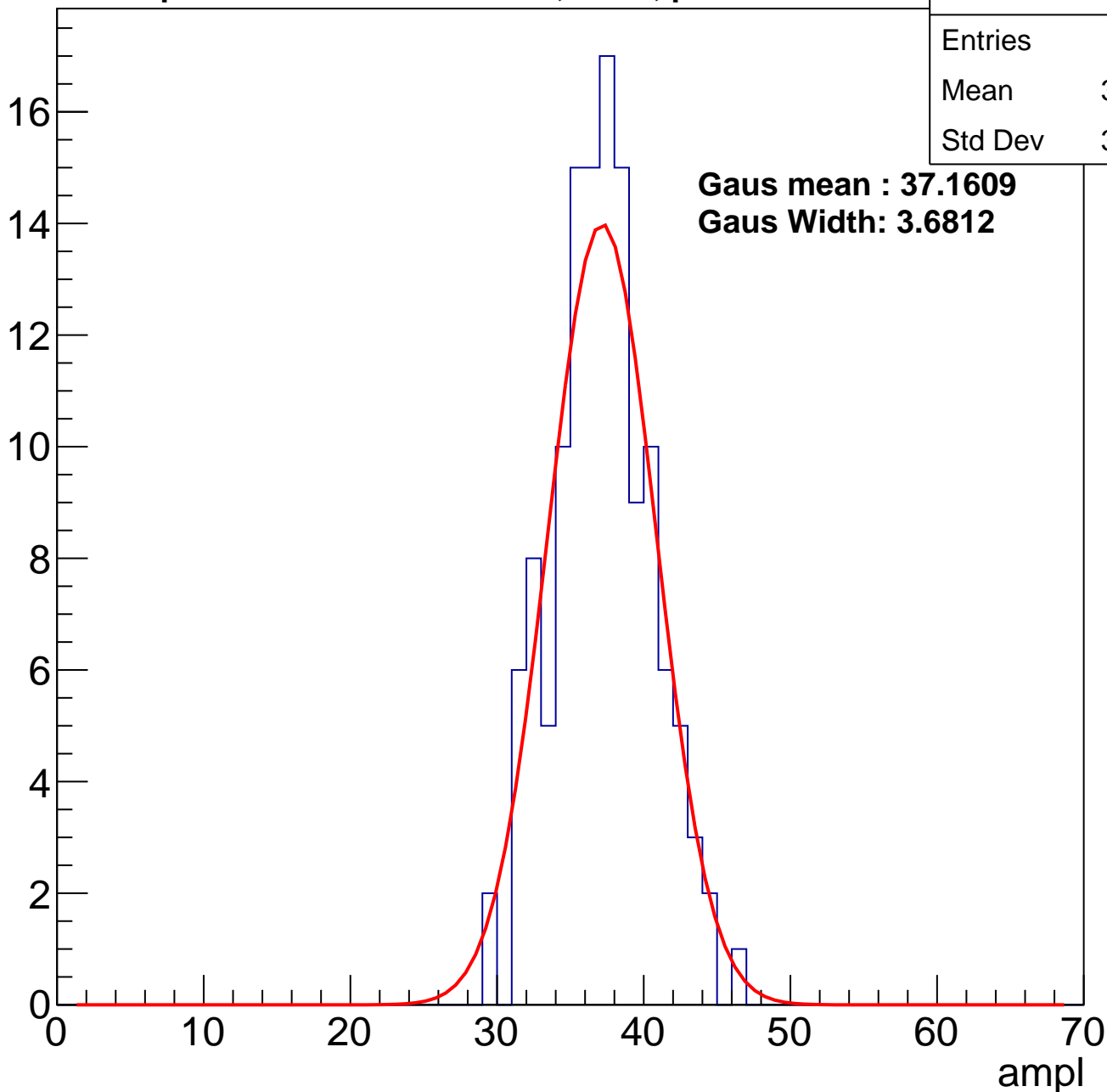
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	36.74
Std Dev	3.328

Gaus mean : 37.1609

Gaus Width: 3.6812

Entry



B0L101S, U18-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

145

Mean

43.84

Std Dev

3.379

Gaus mean : 44.2705

Gaus Width: 3.5166

0

10

20

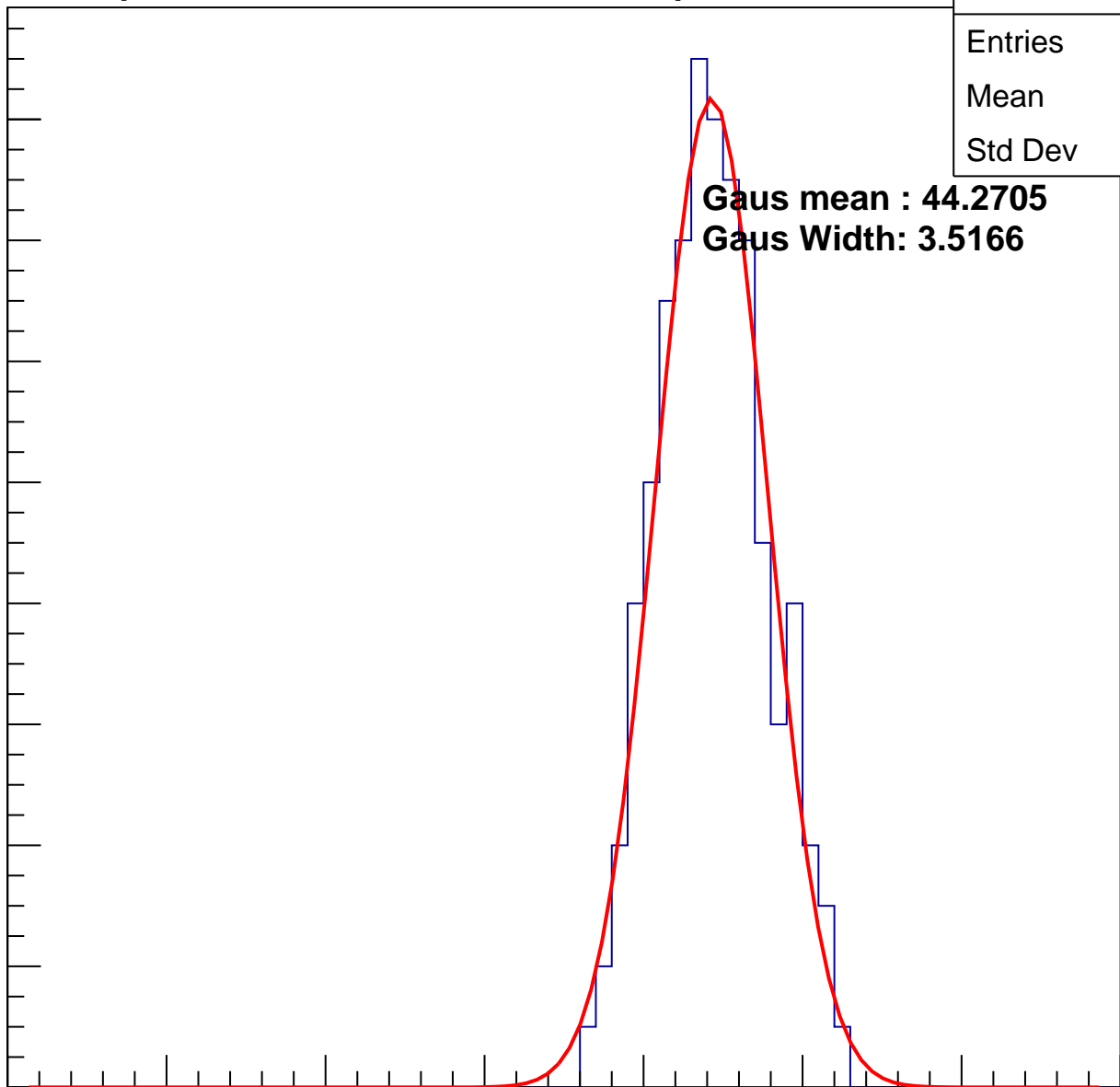
30

40

50

60

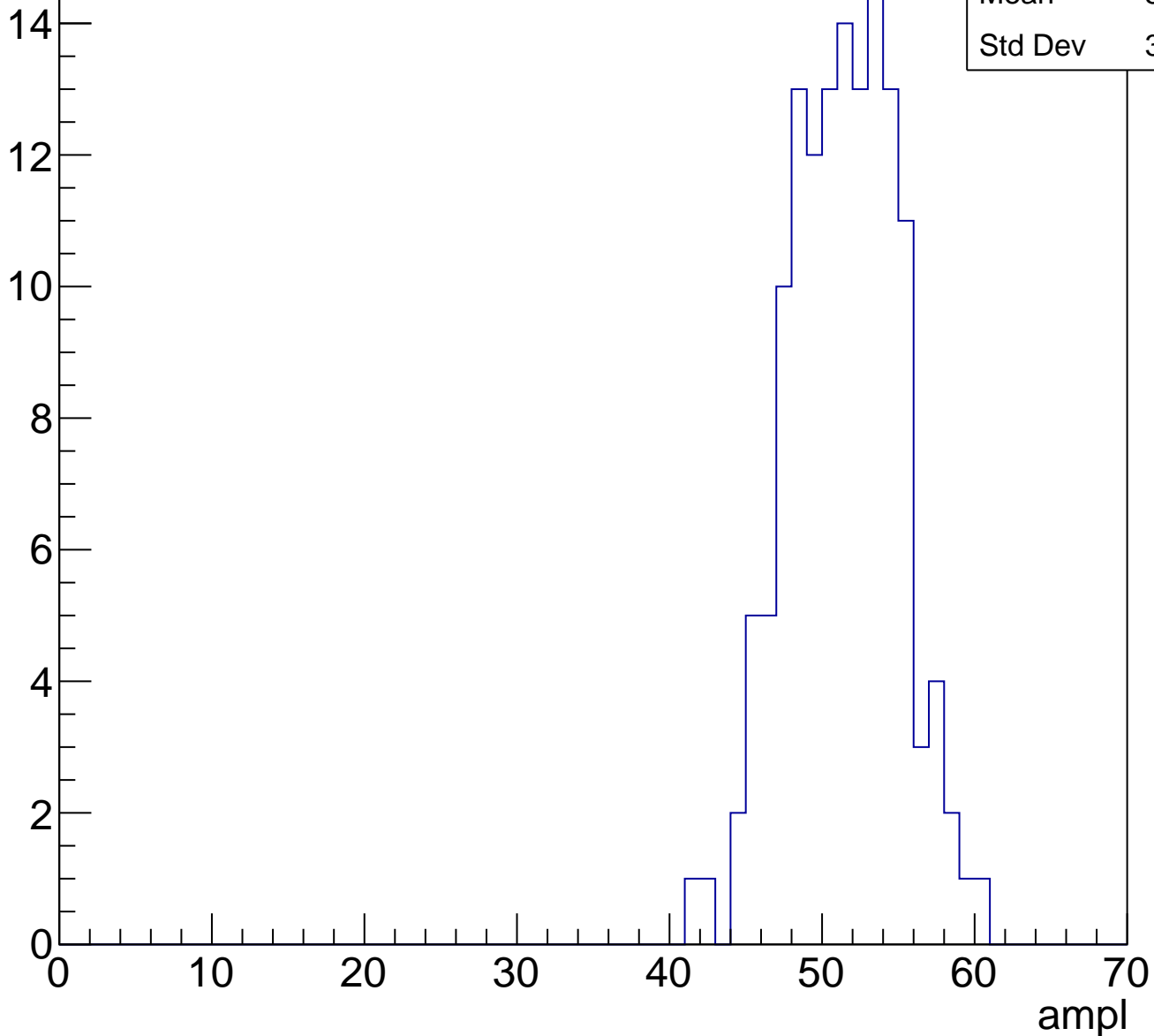
ampl



B0L101S, U18-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

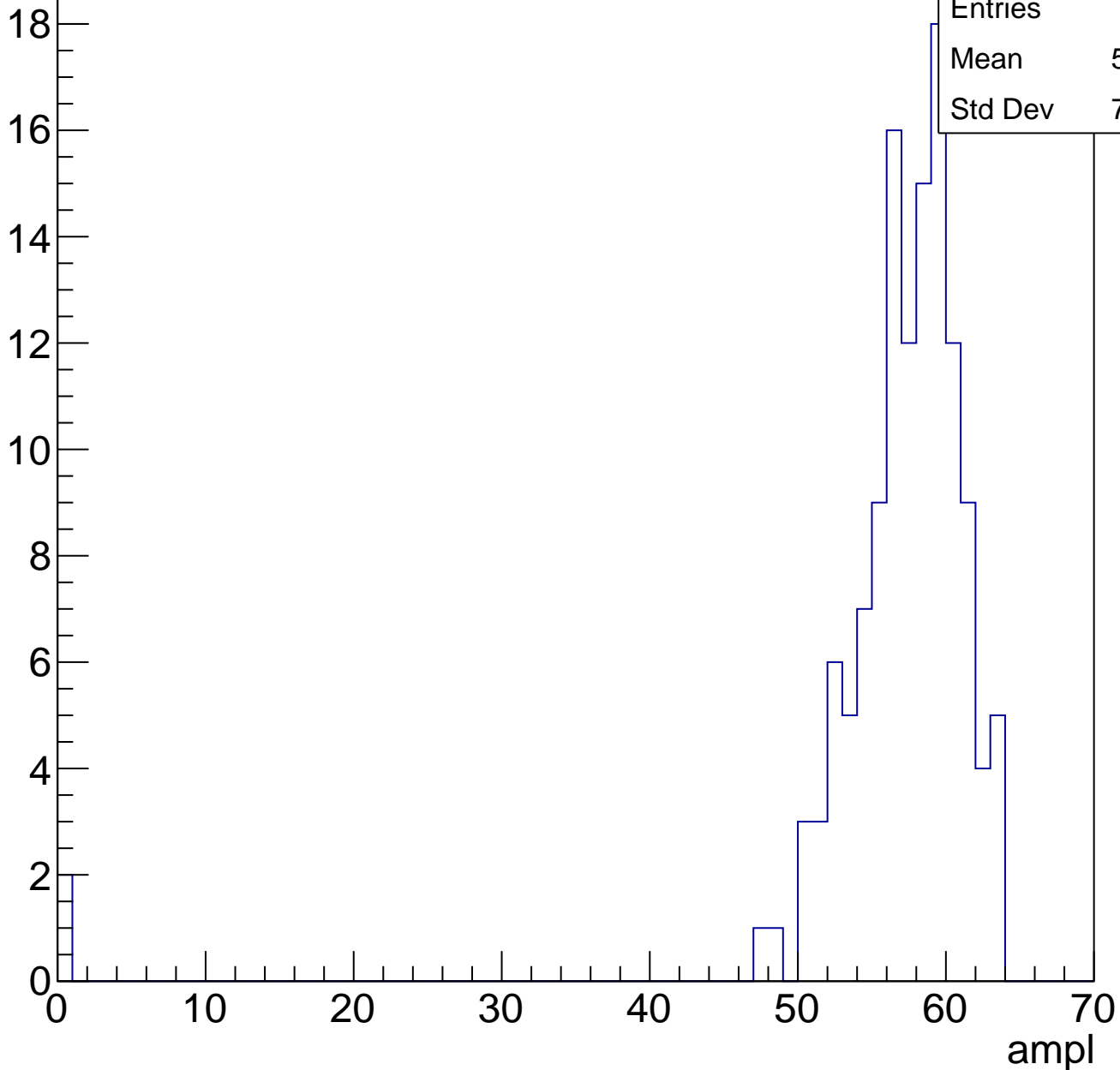


B0L101S, U18-ch7, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	56.19
Std Dev	7.813

Entry



B0L101S, U18-ch7, adc5

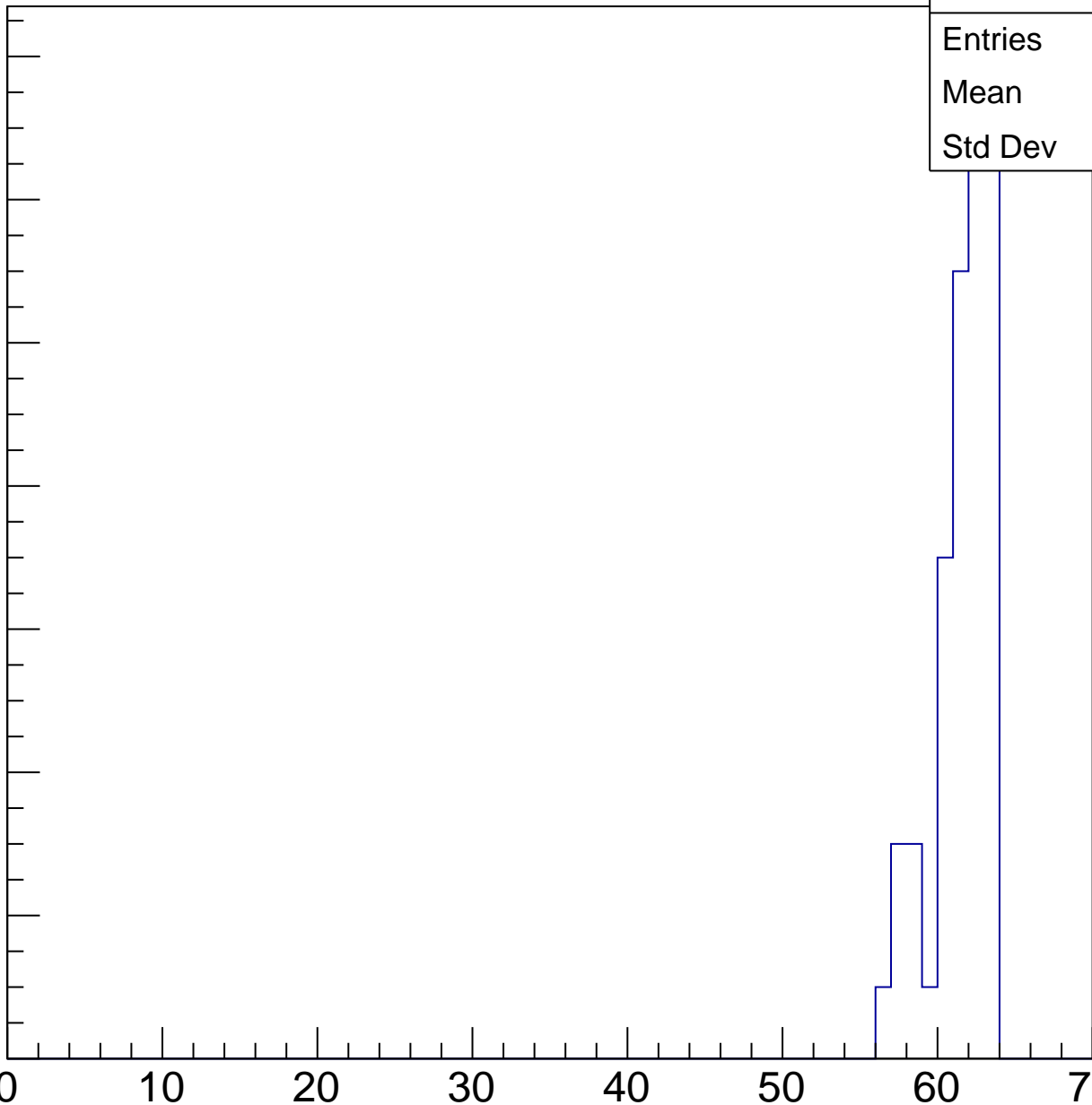
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	53
Mean	61.11
Std Dev	1.839

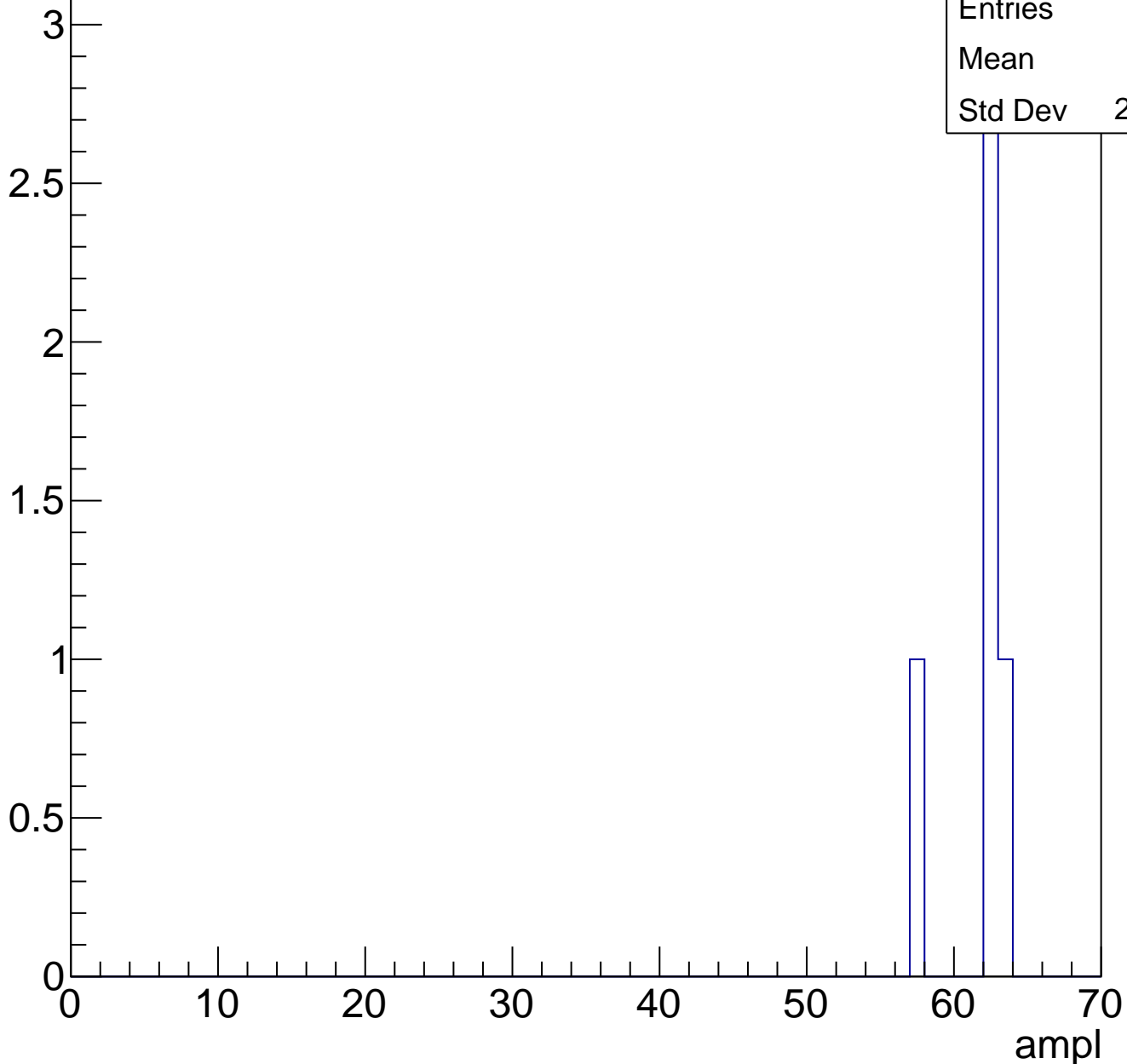
ampl



B0L101S, U18-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

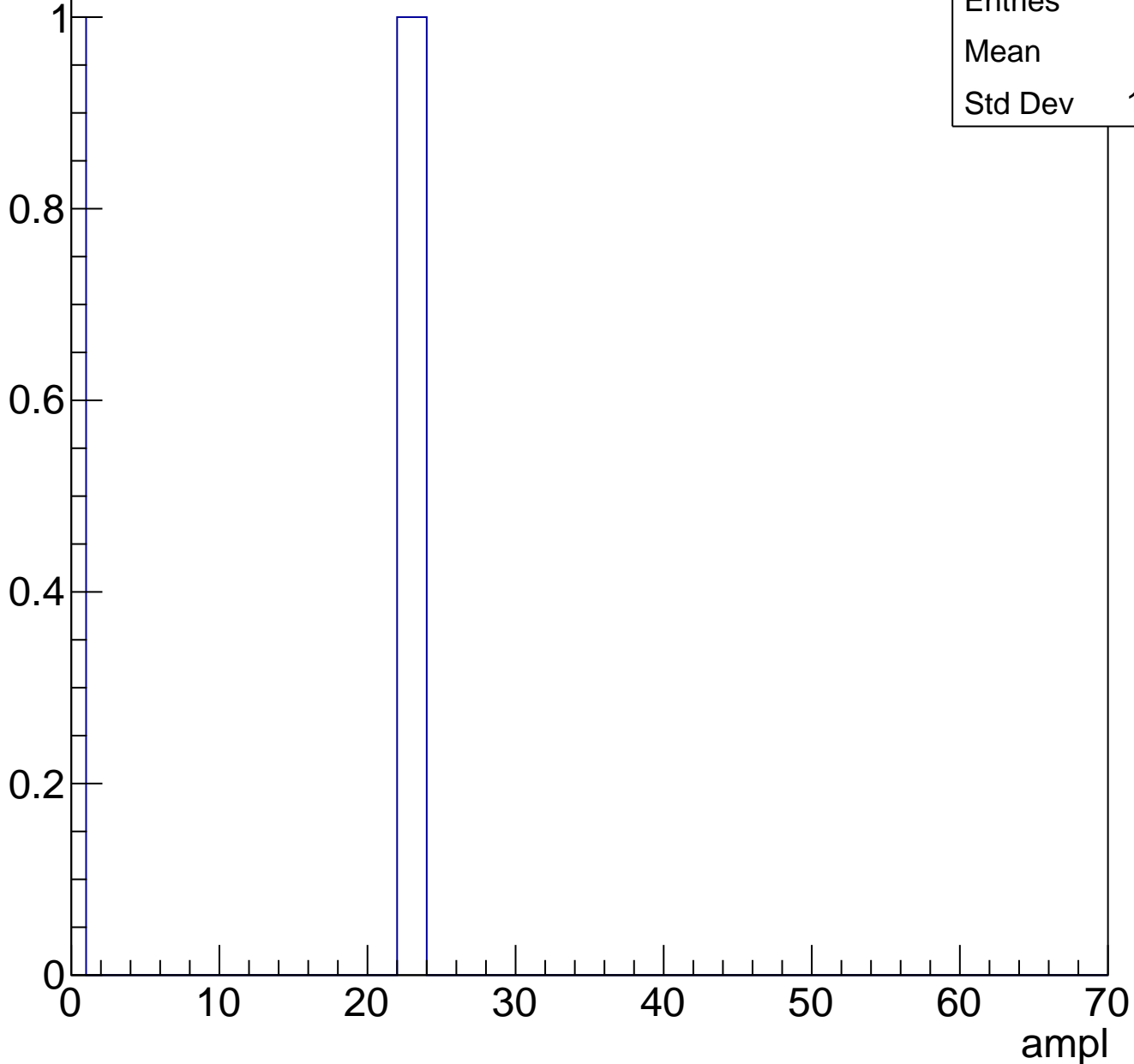
Entry



B0L101S, U18-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

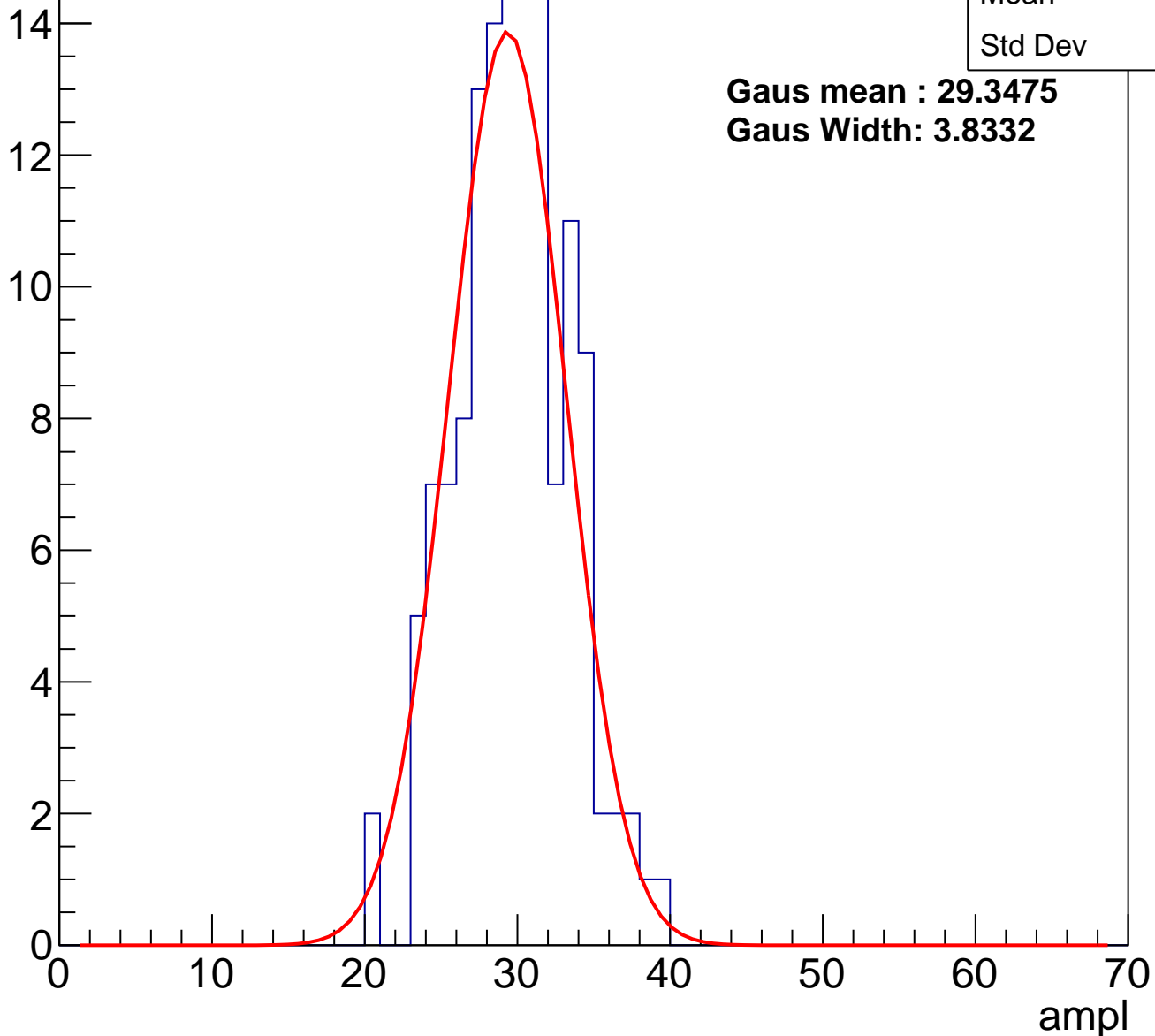
Entry



B0L101S, U18-ch8, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



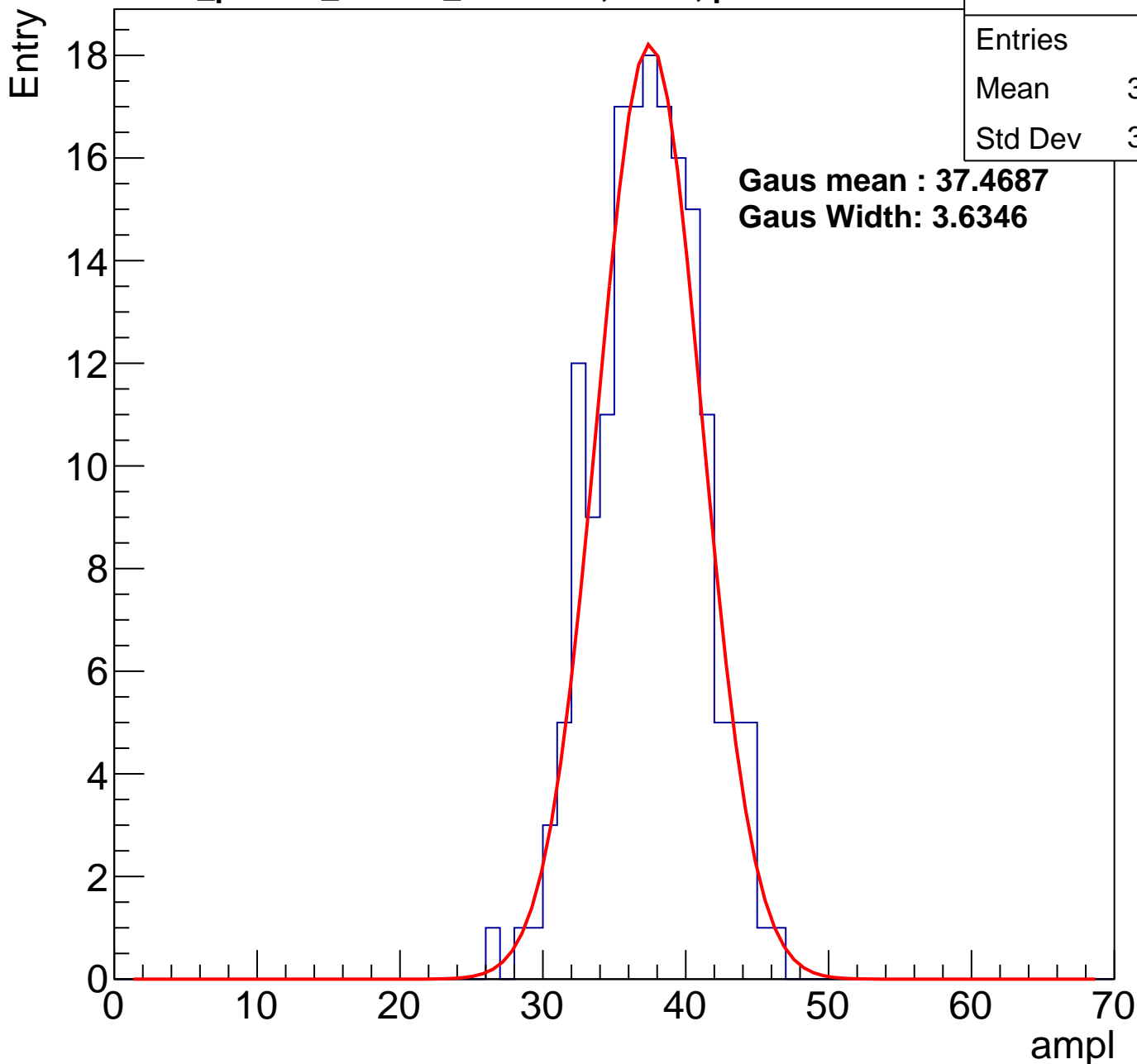
B0L101S, U18-ch8, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	36.92
Std Dev	3.673

Gaus mean : 37.4687

Gaus Width: 3.6346



B0L101S, U18-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

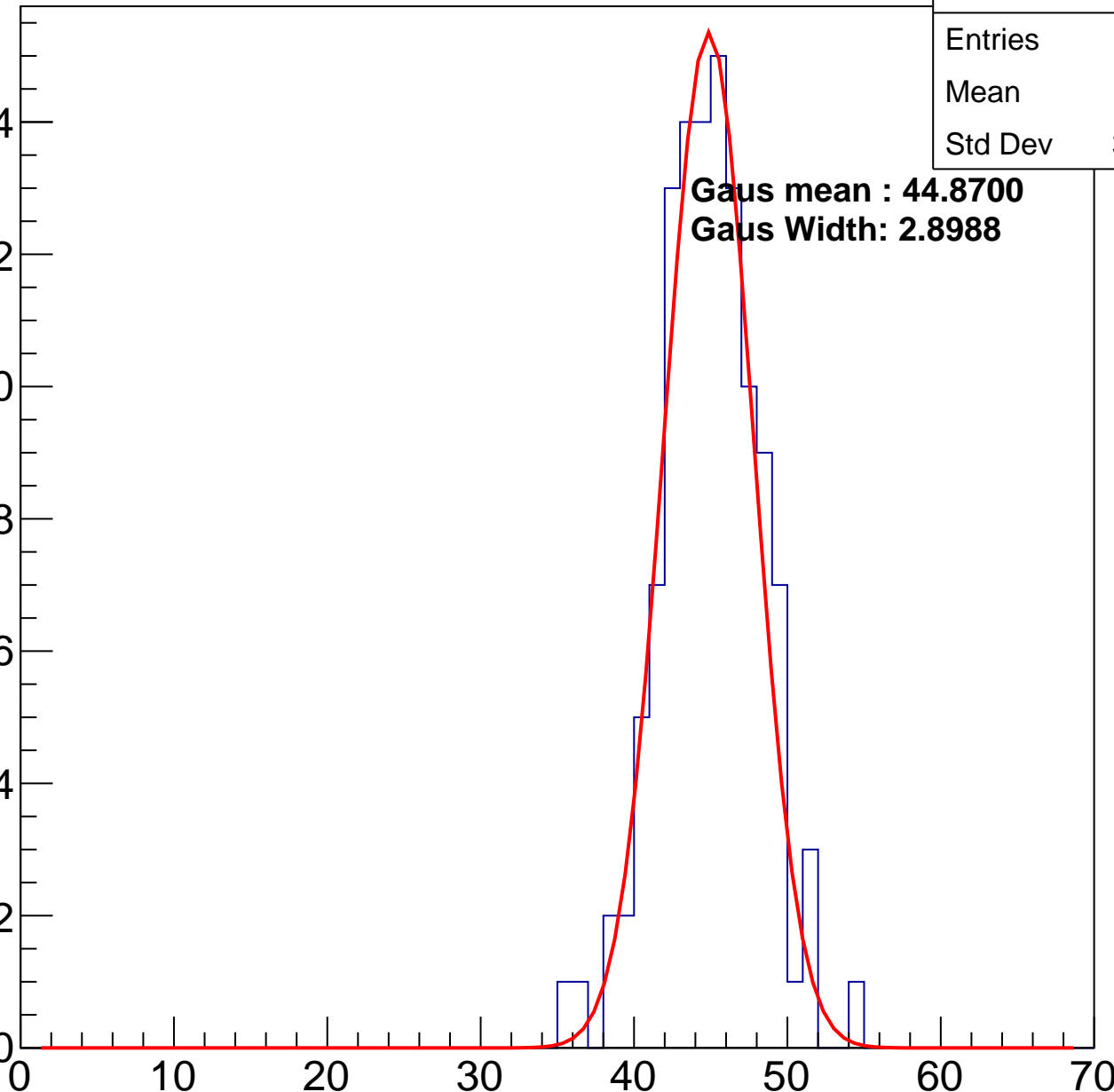
70

ampl

Entries	118
Mean	44.5
Std Dev	3.196

Gaus mean : 44.8700

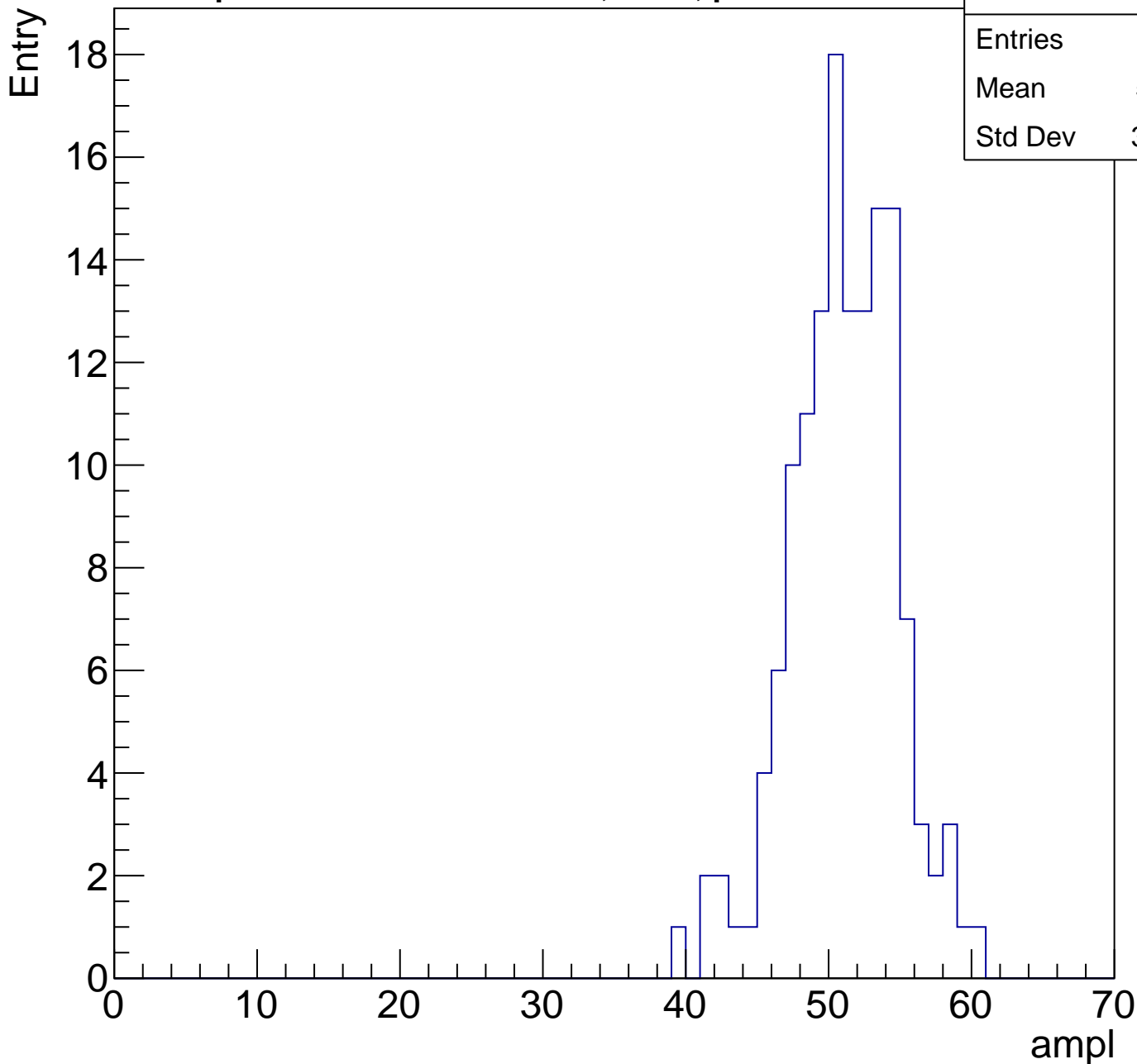
Gaus Width: 2.8988



B0L101S, U18-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	50.61
Std Dev	3.734



B0L101S, U18-ch8, adc4

calib_packv5_042523_0143.root, FC#1, port C1

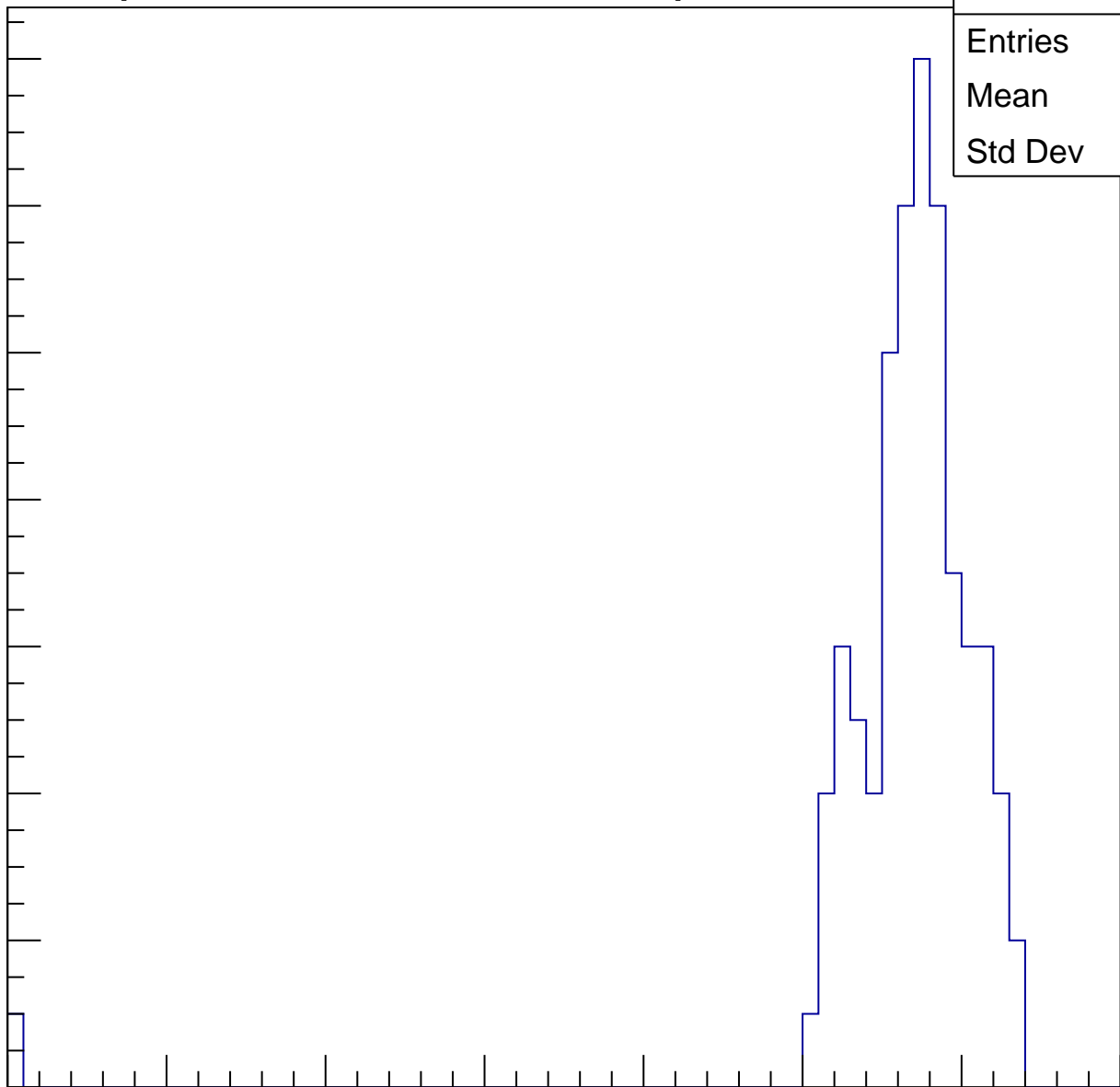
Entries	94
Mean	56.13
Std Dev	6.564

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch8, adc5

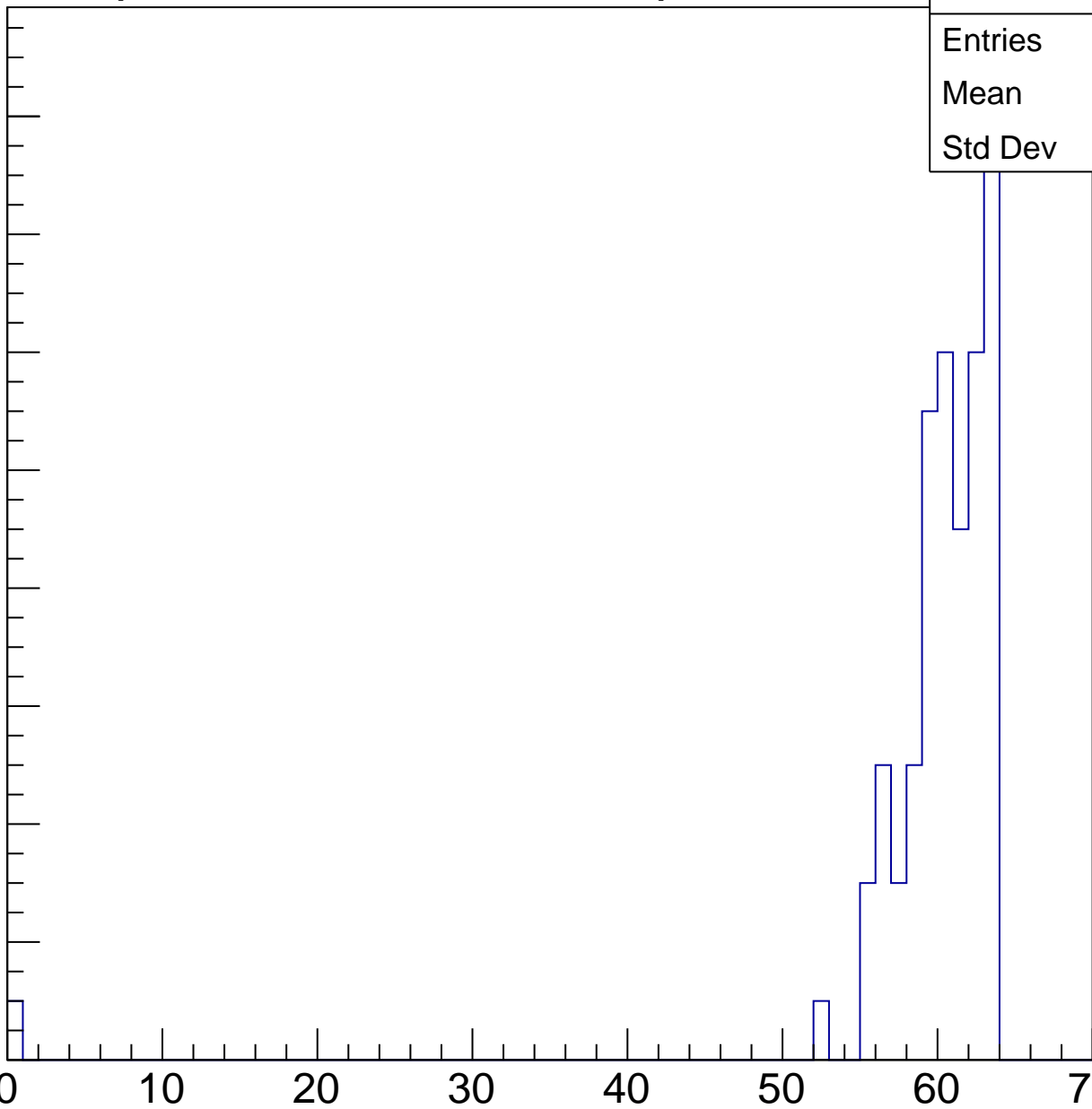
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.38
Std Dev	7.166

ampl

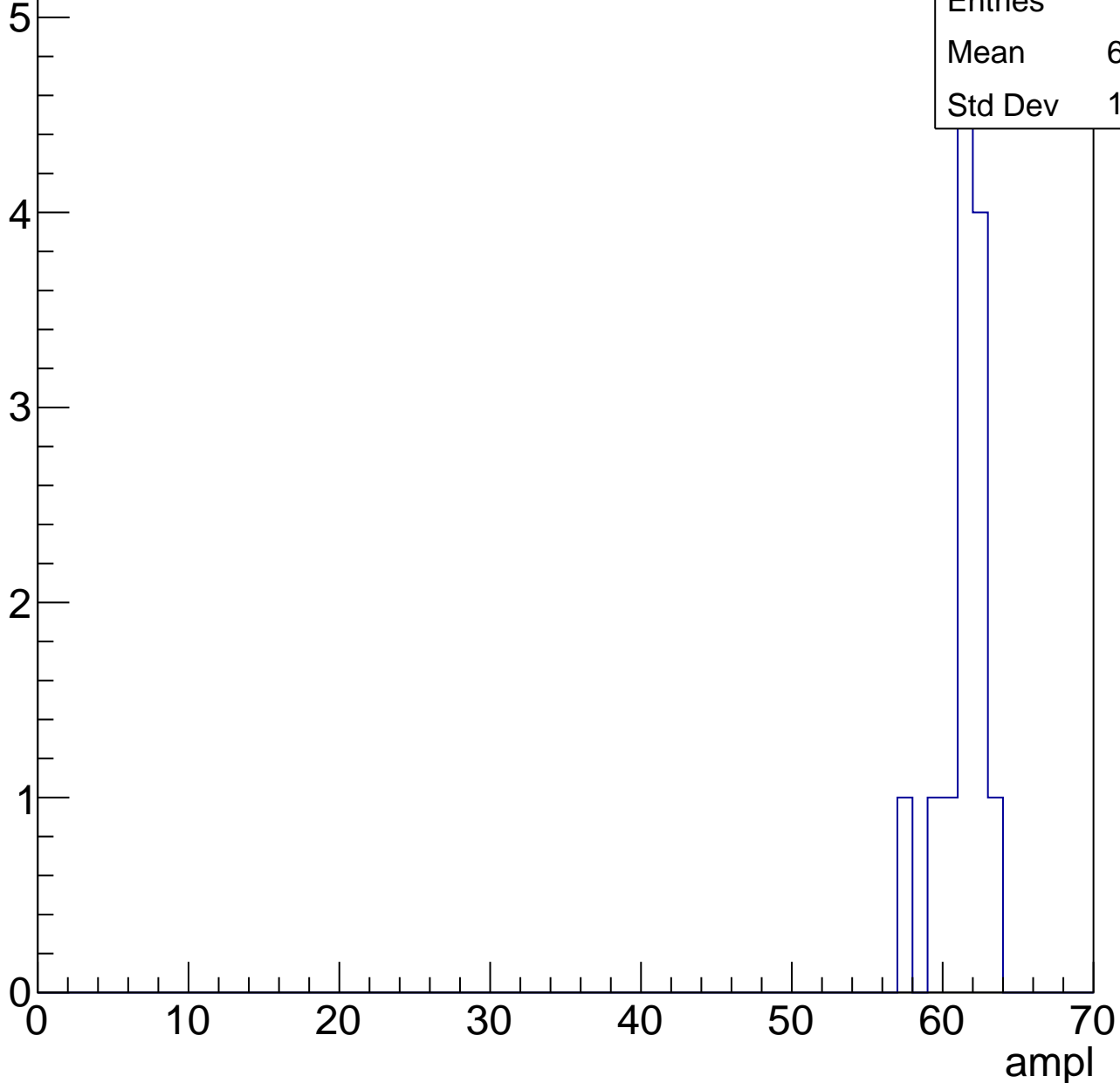


B0L101S, U18-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	60.92
Std Dev	1.492



B0L101S, U18-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch9, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

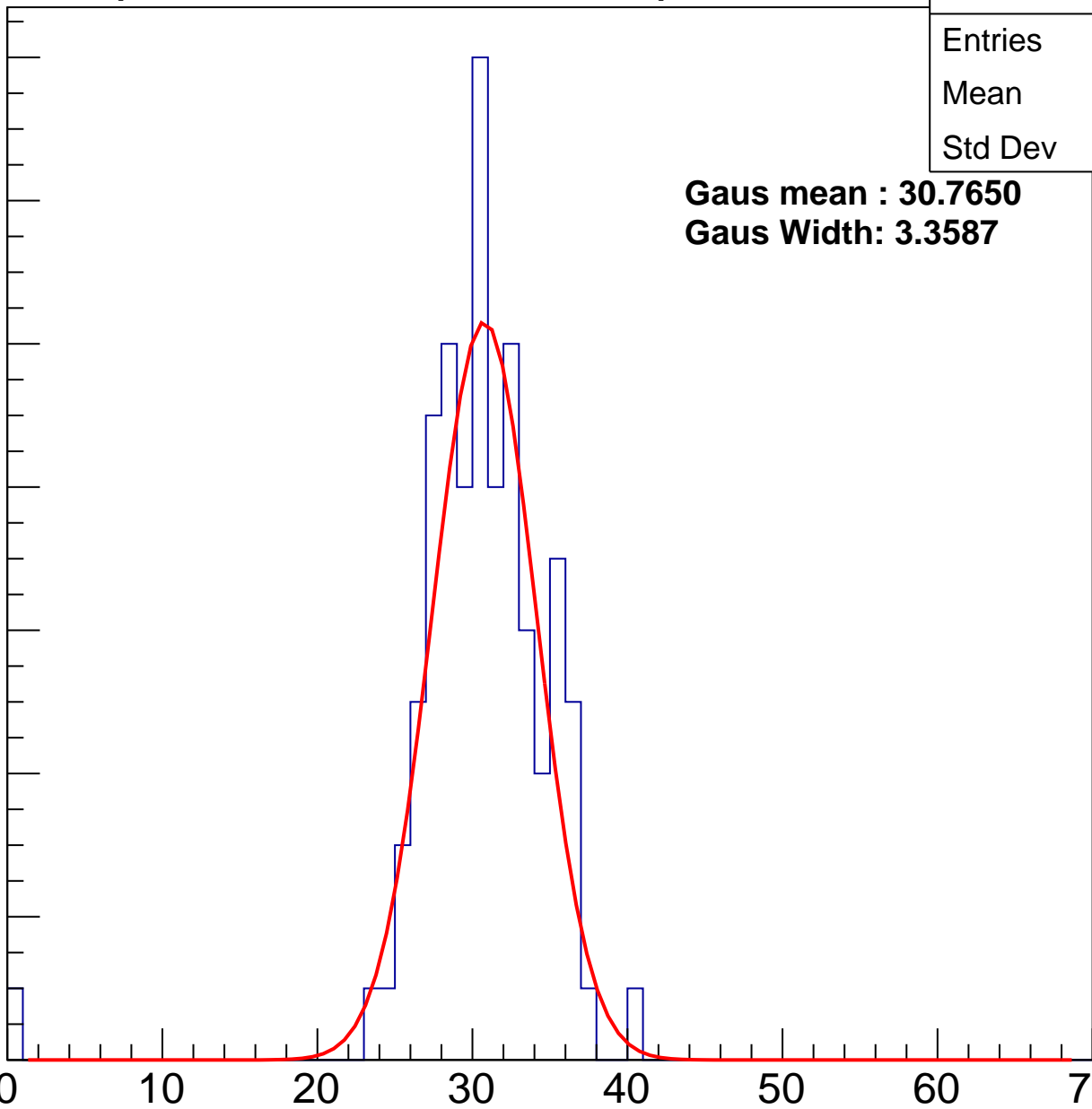
14
12
10
8
6
4
2
0

Entries	94
Mean	30.12
Std Dev	4.54

Gaus mean : 30.7650

Gaus Width: 3.3587

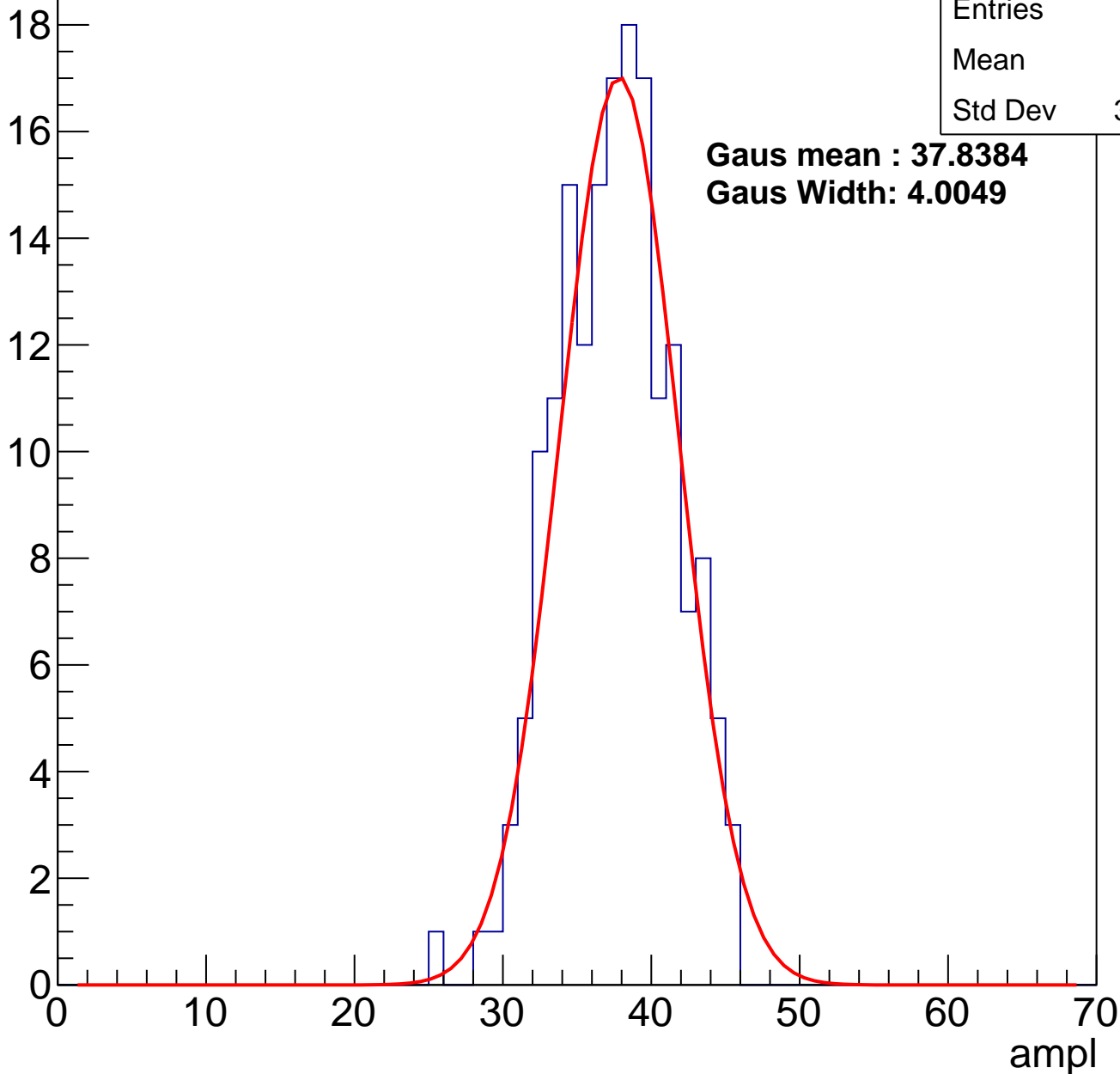
ampl



B0L101S, U18-ch9, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



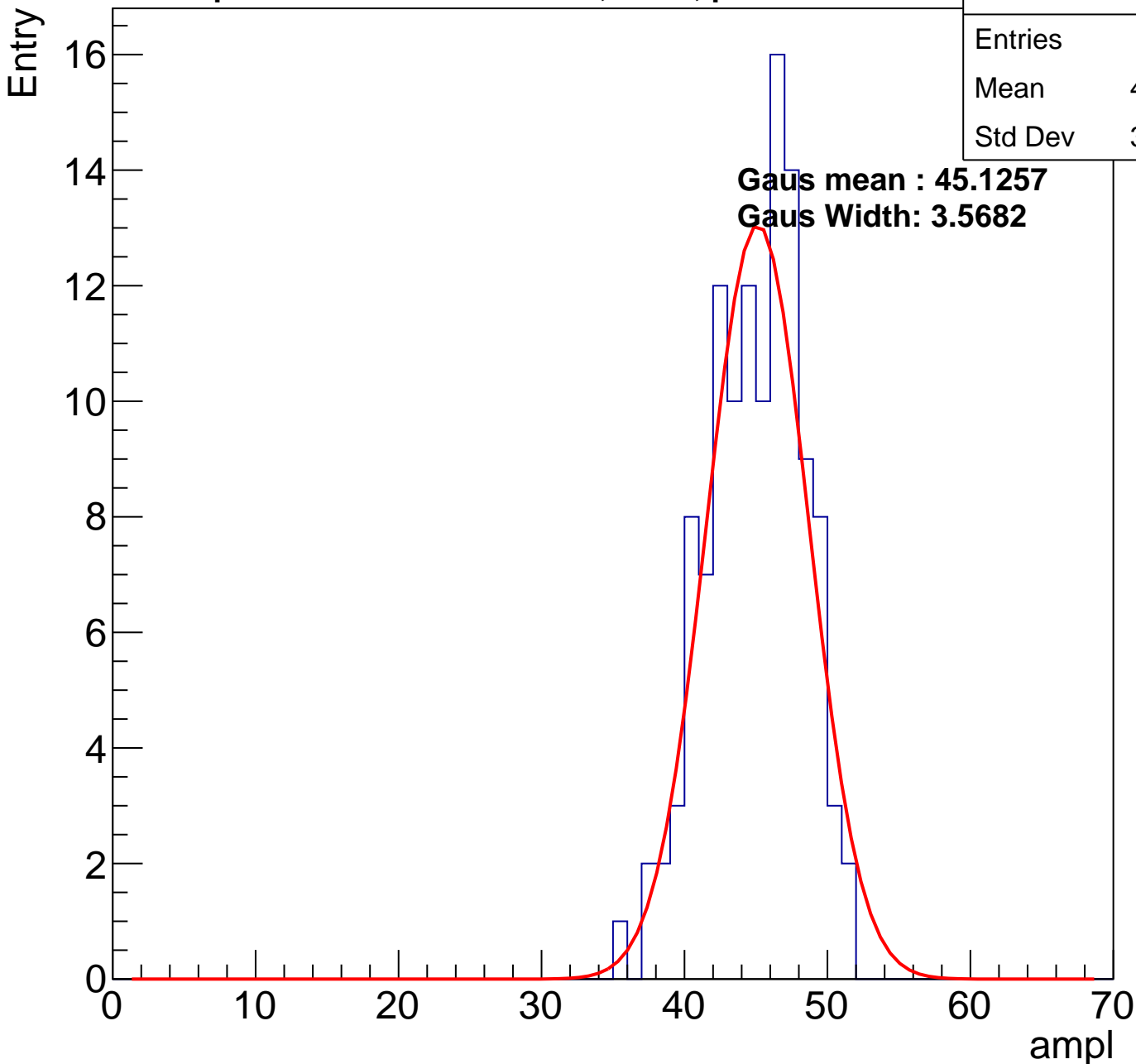
B0L101S, U18-ch9, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	44.46
Std Dev	3.305

Gaus mean : 45.1257

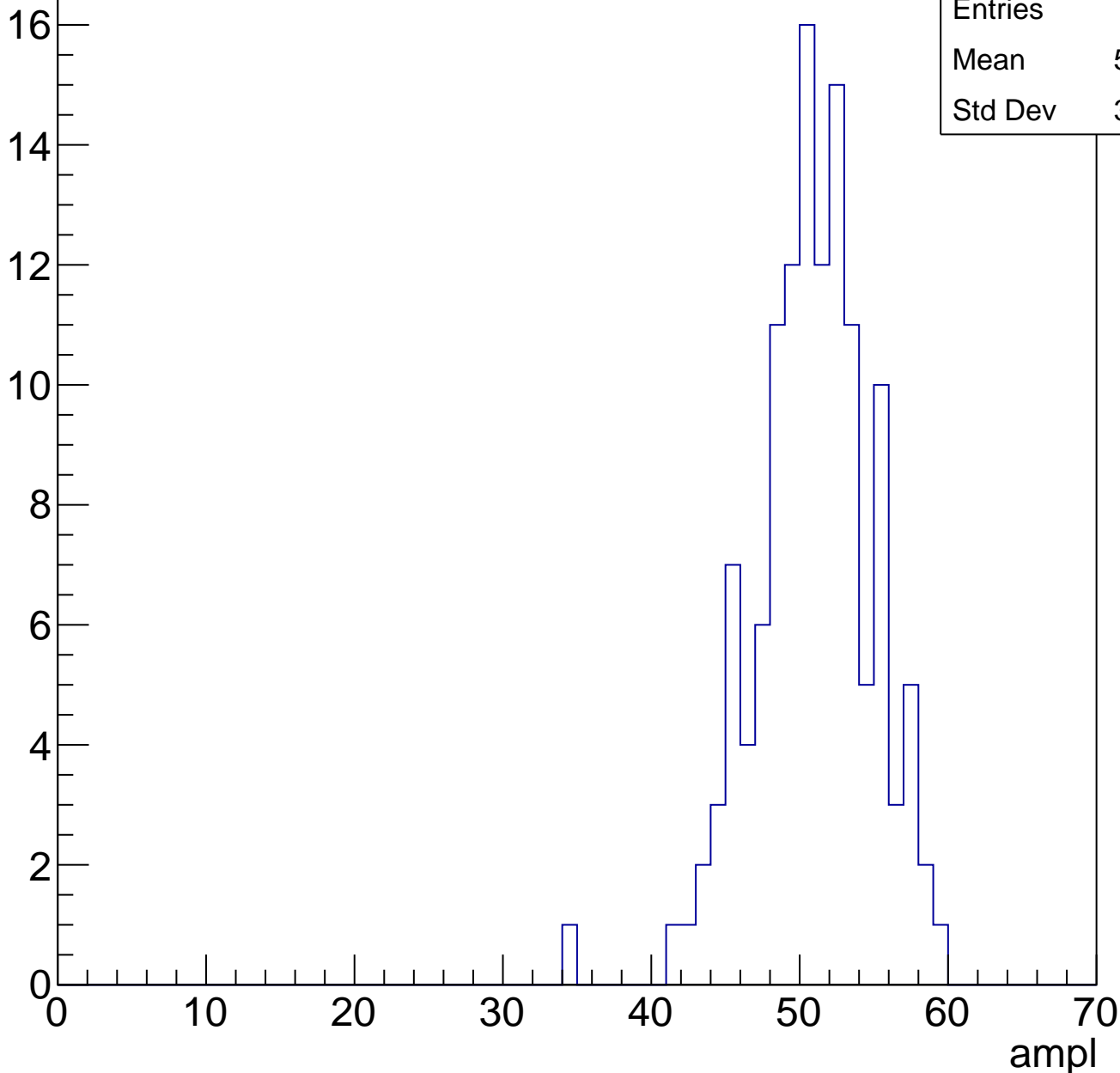
Gaus Width: 3.5682



B0L101S, U18-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

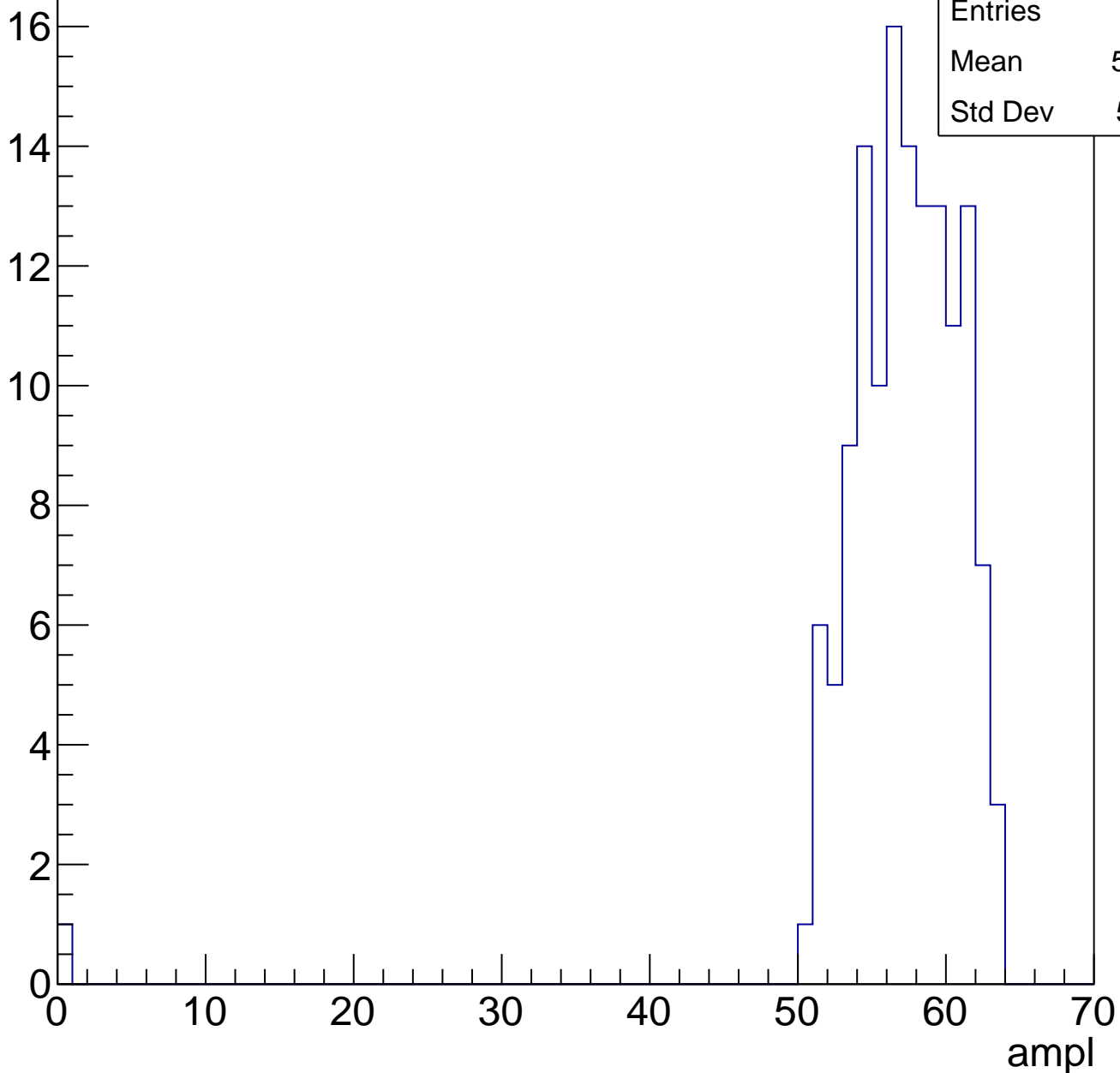
Entry



B0L101S, U18-ch9, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch9, adc5

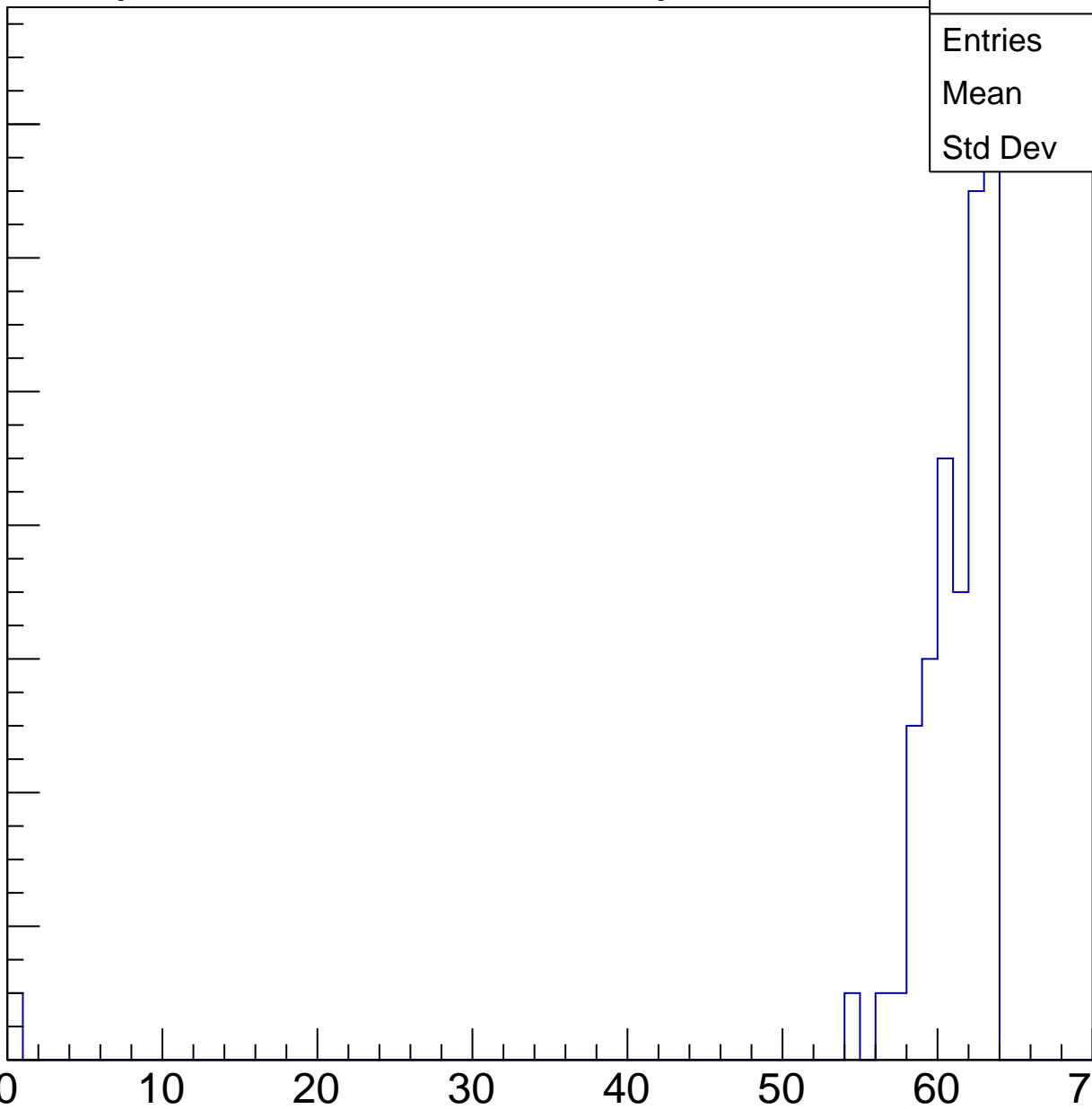
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.81
Std Dev	8.108

ampl



B0L101S, U18-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U18-ch10, adc0

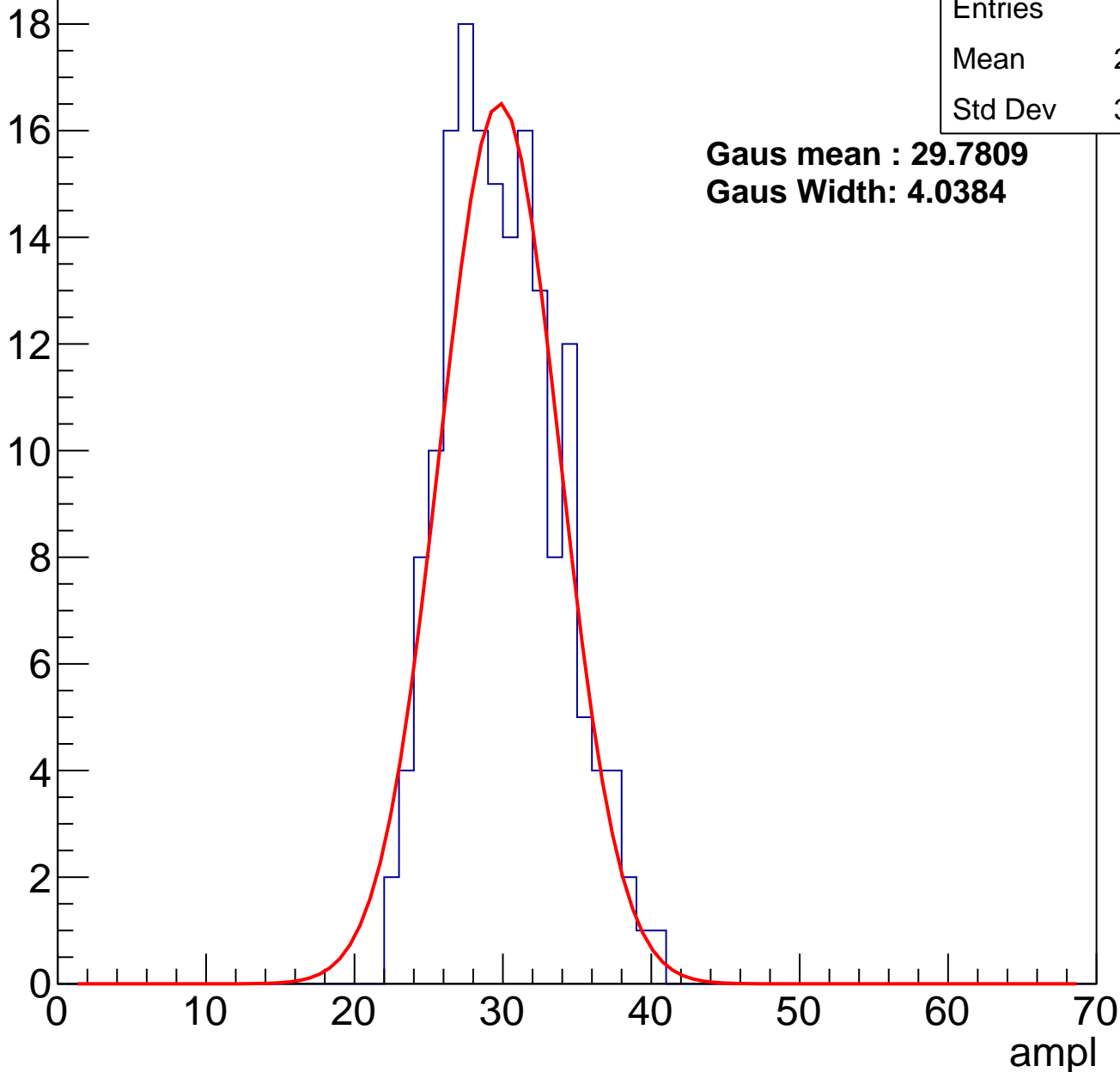
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	29.52
Std Dev	3.789

Gaus mean : 29.7809

Gaus Width: 4.0384

Entry

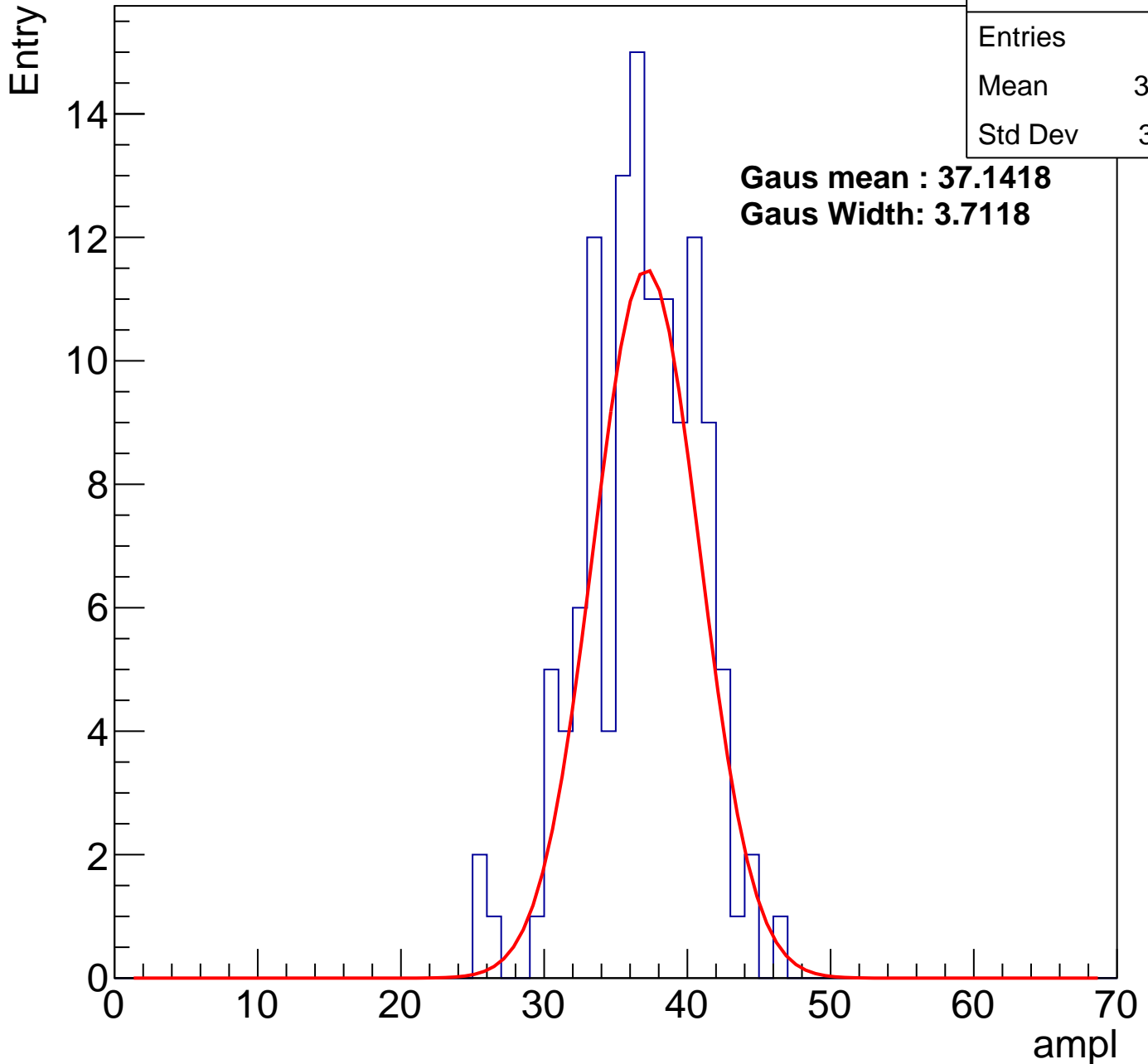


B0L101S, U18-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.37
Std Dev	3.911

Gaus mean : 37.1418
Gaus Width: 3.7118



B0L101S, U18-ch10, adc2

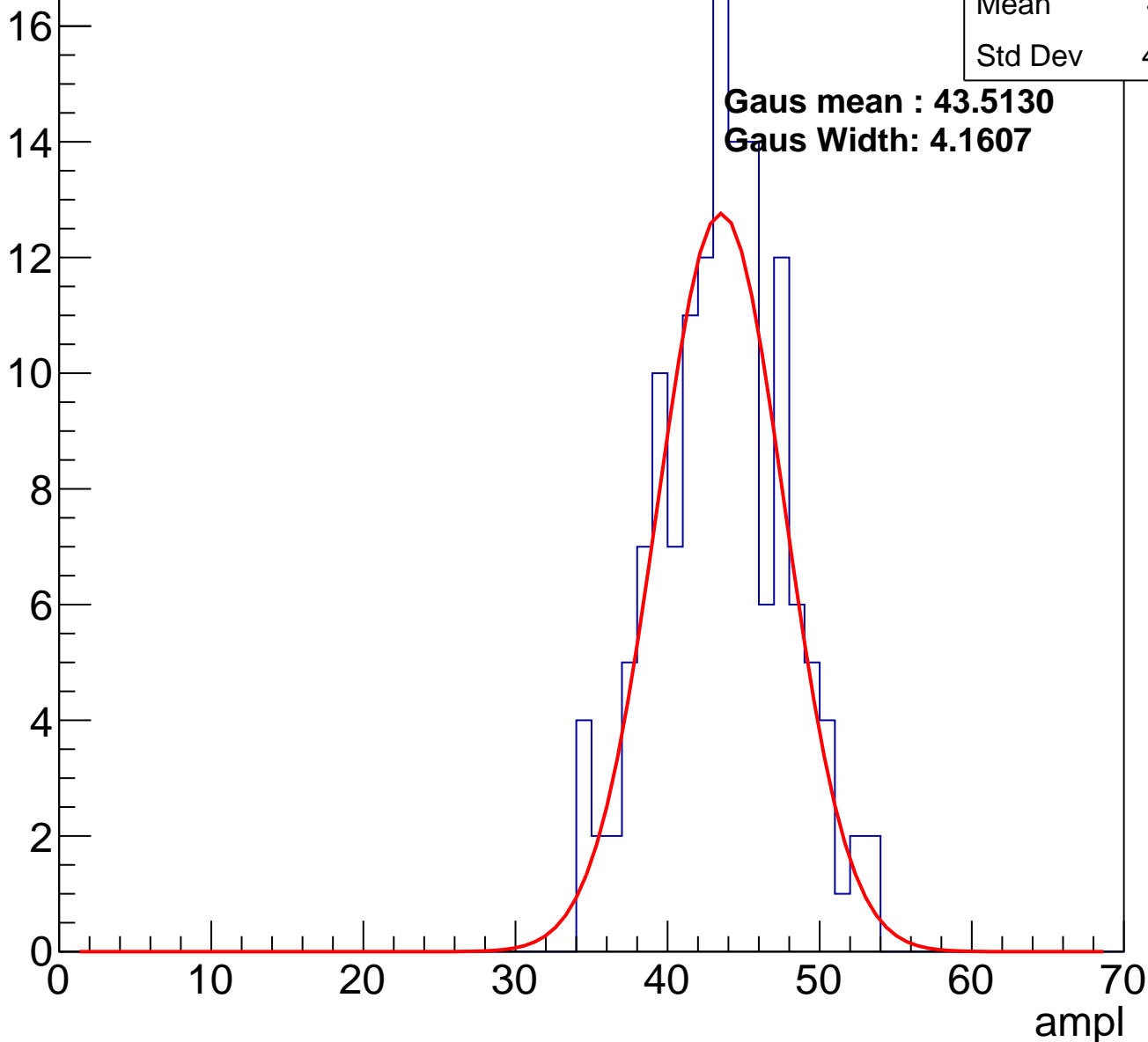
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	143
Mean	43.11
Std Dev	4.139

Gaus mean : 43.5130

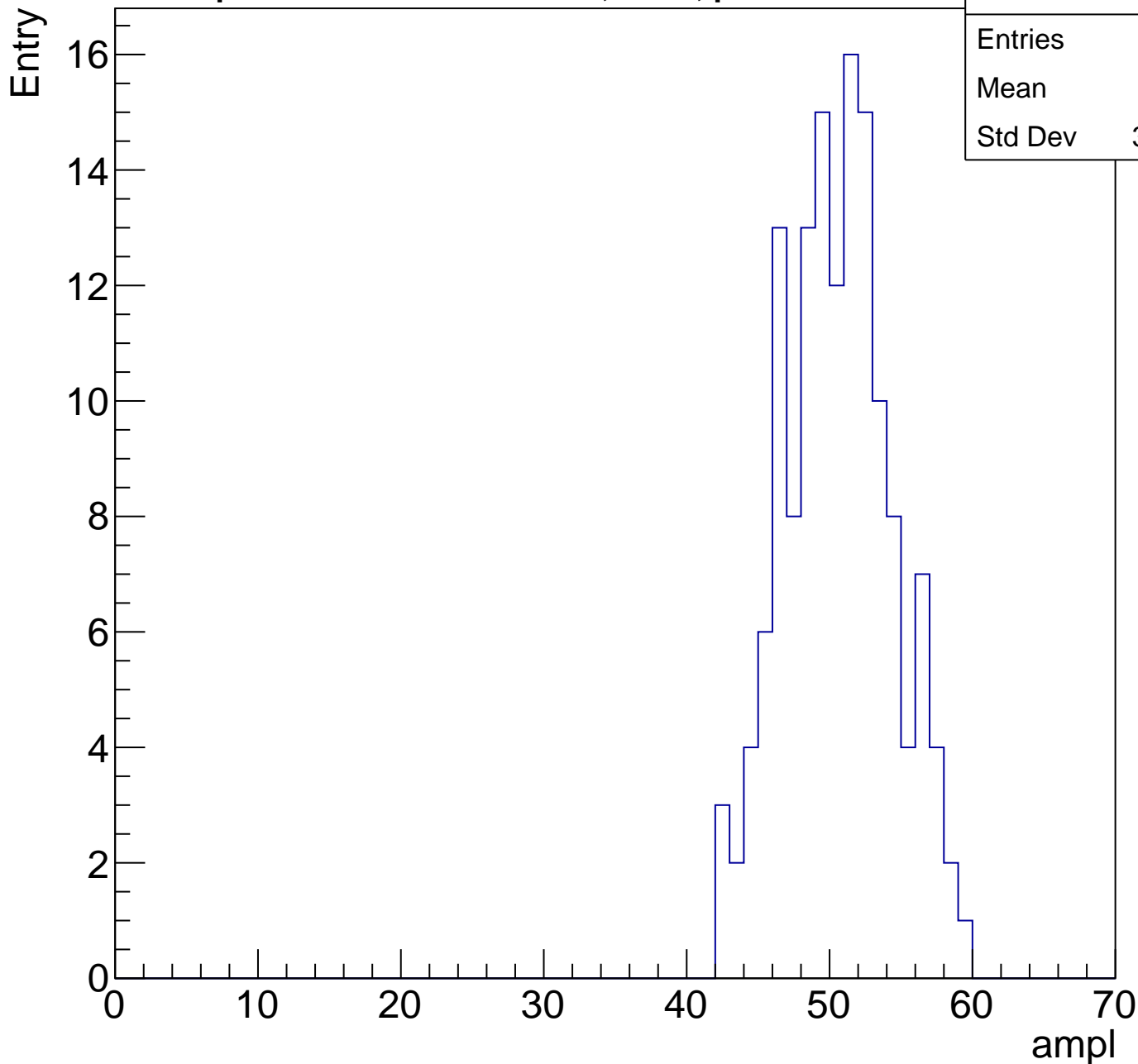
Gaus Width: 4.1607



B0L101S, U18-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	50.1
Std Dev	3.722



B0L101S, U18-ch10, adc4

calib_packv5_042523_0143.root, FC#1, port C1

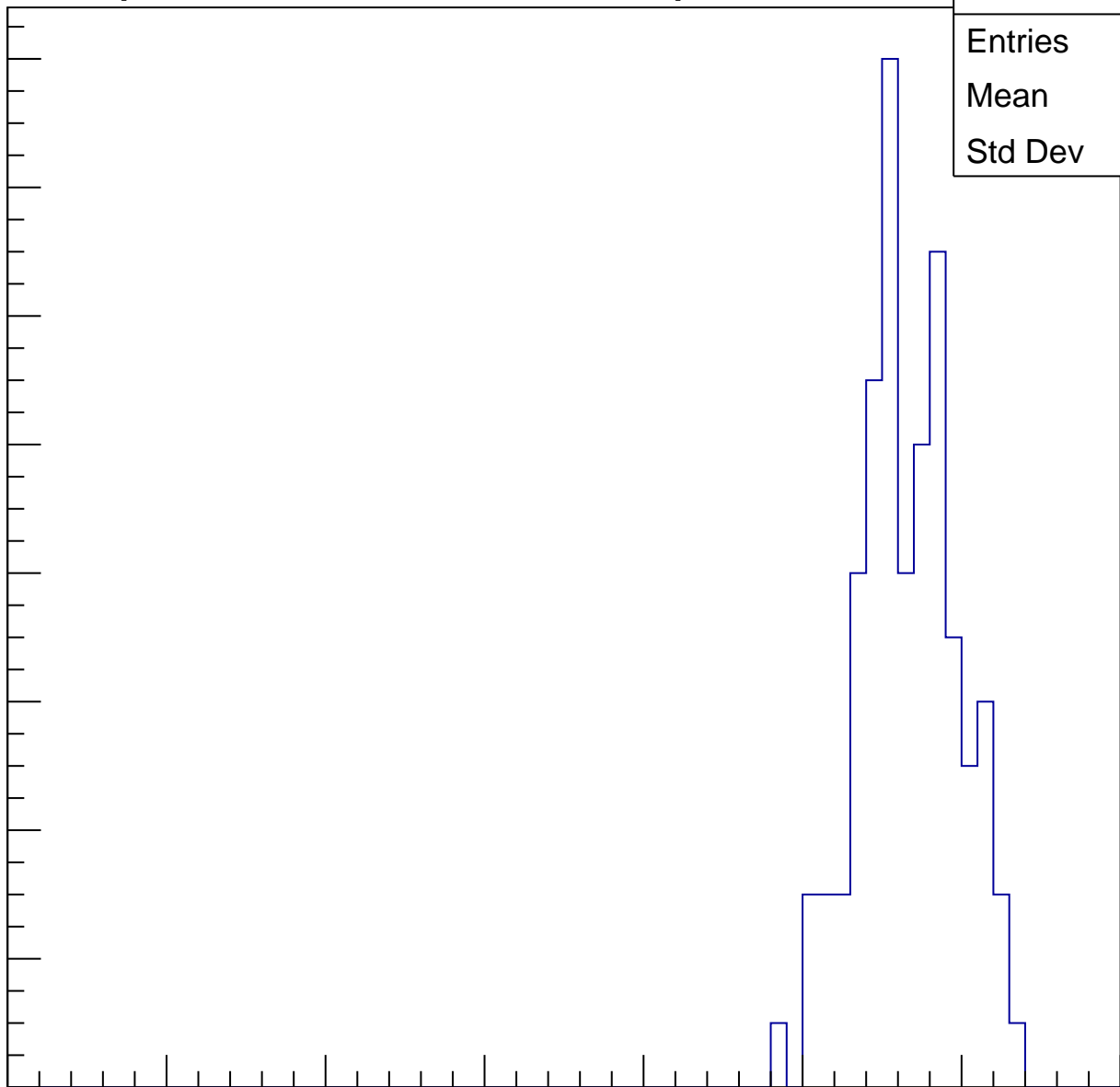
Entries	98
Mean	56.17
Std Dev	3.091

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch10, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	80
Mean	59.24
Std Dev	7.096

ampl

0

10

20

30

40

50

60

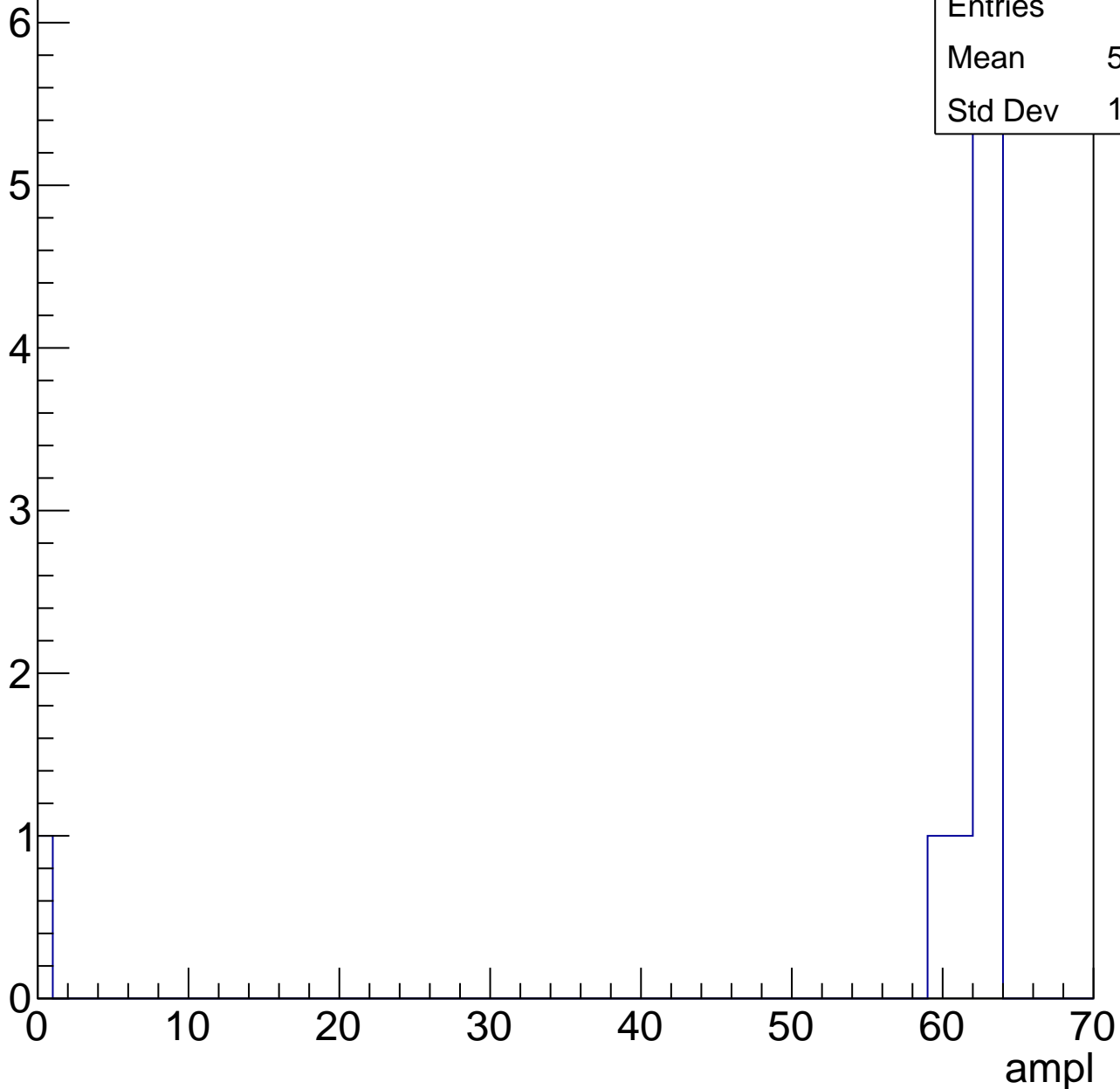
70

B0L101S, U18-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

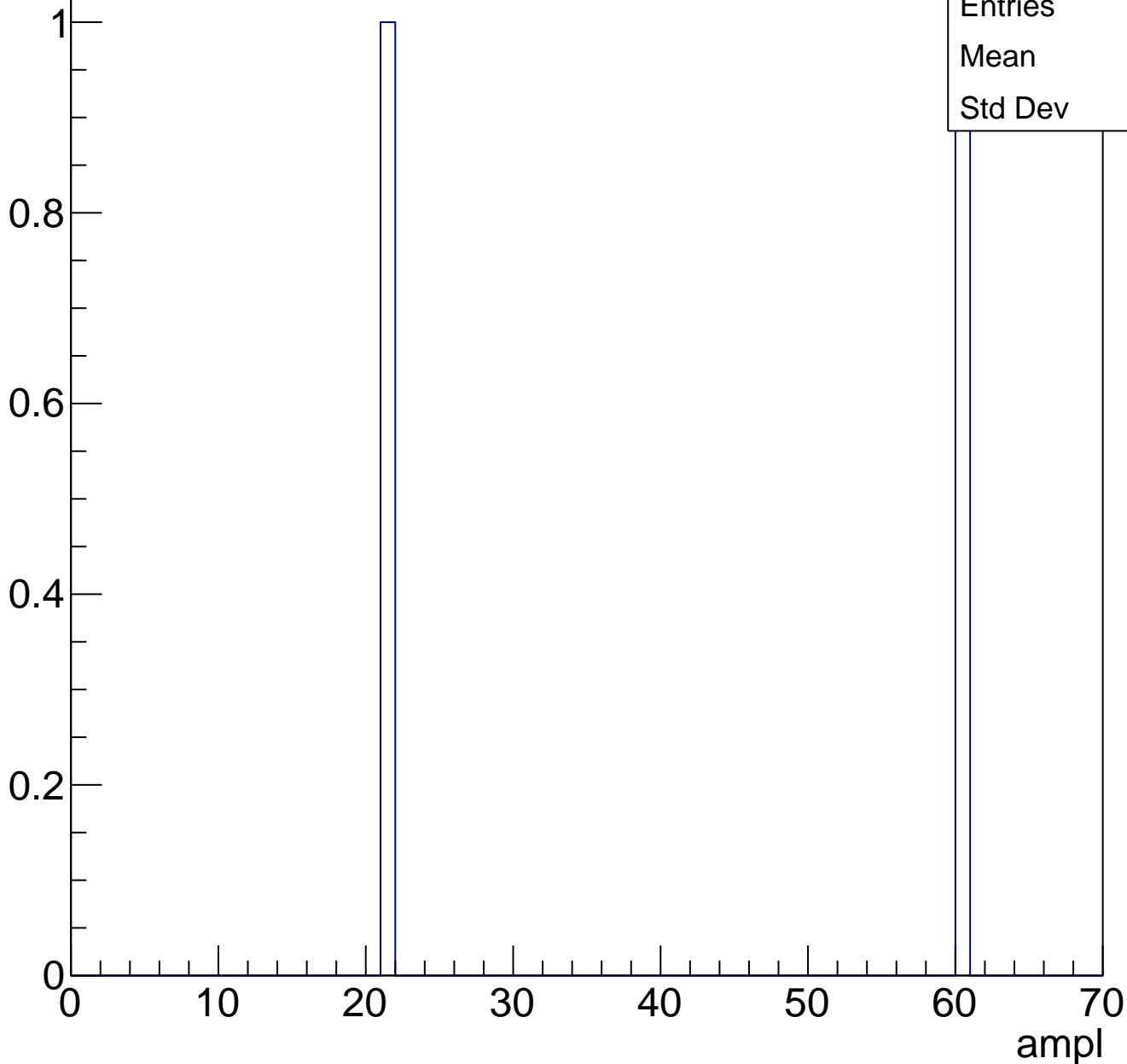
Entries	16
Mean	58.12
Std Dev	15.05



B0L101S, U18-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch11, adc0

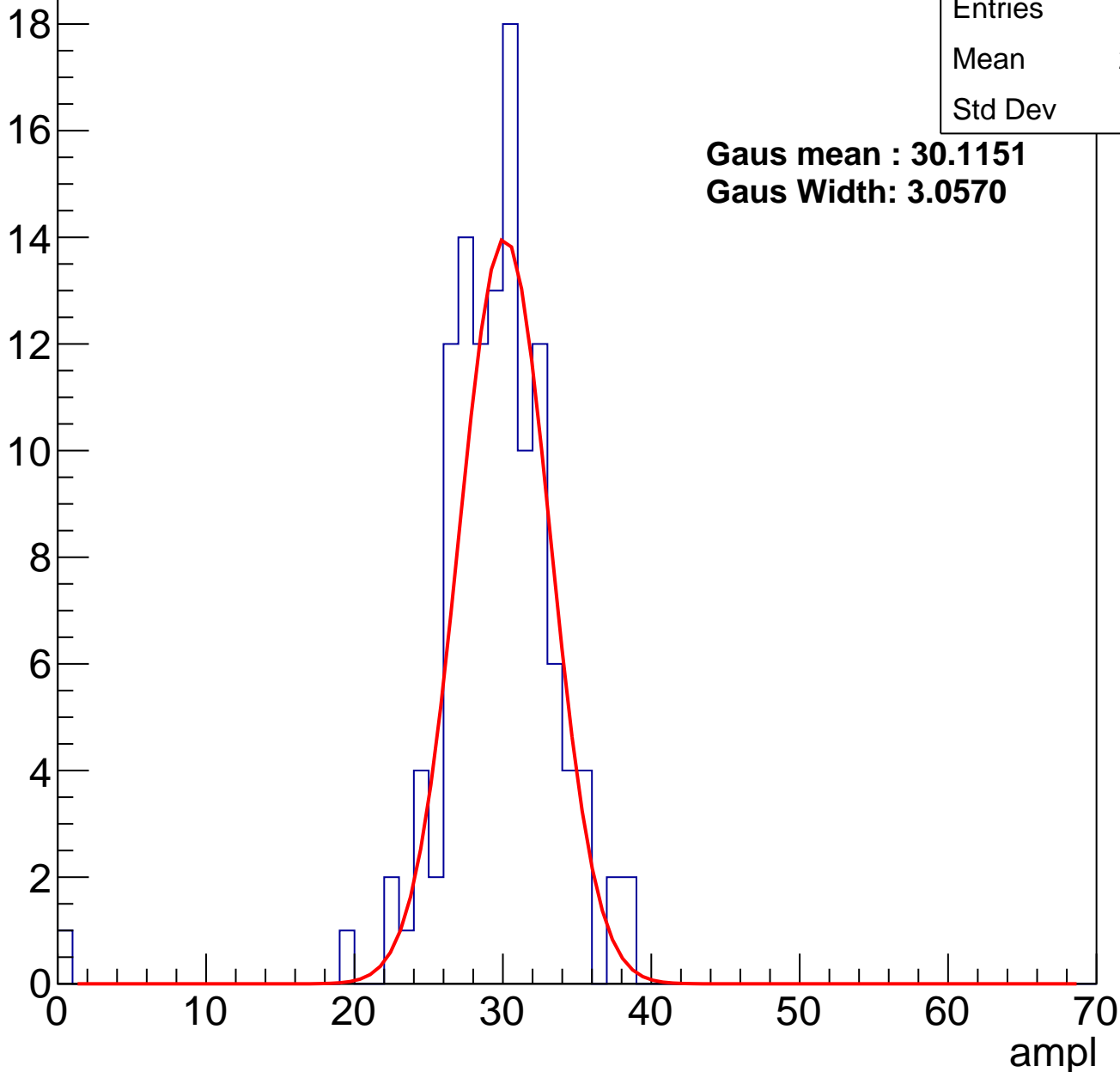
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	29.11
Std Dev	4.26

Gaus mean : 30.1151

Gaus Width: 3.0570

Entry



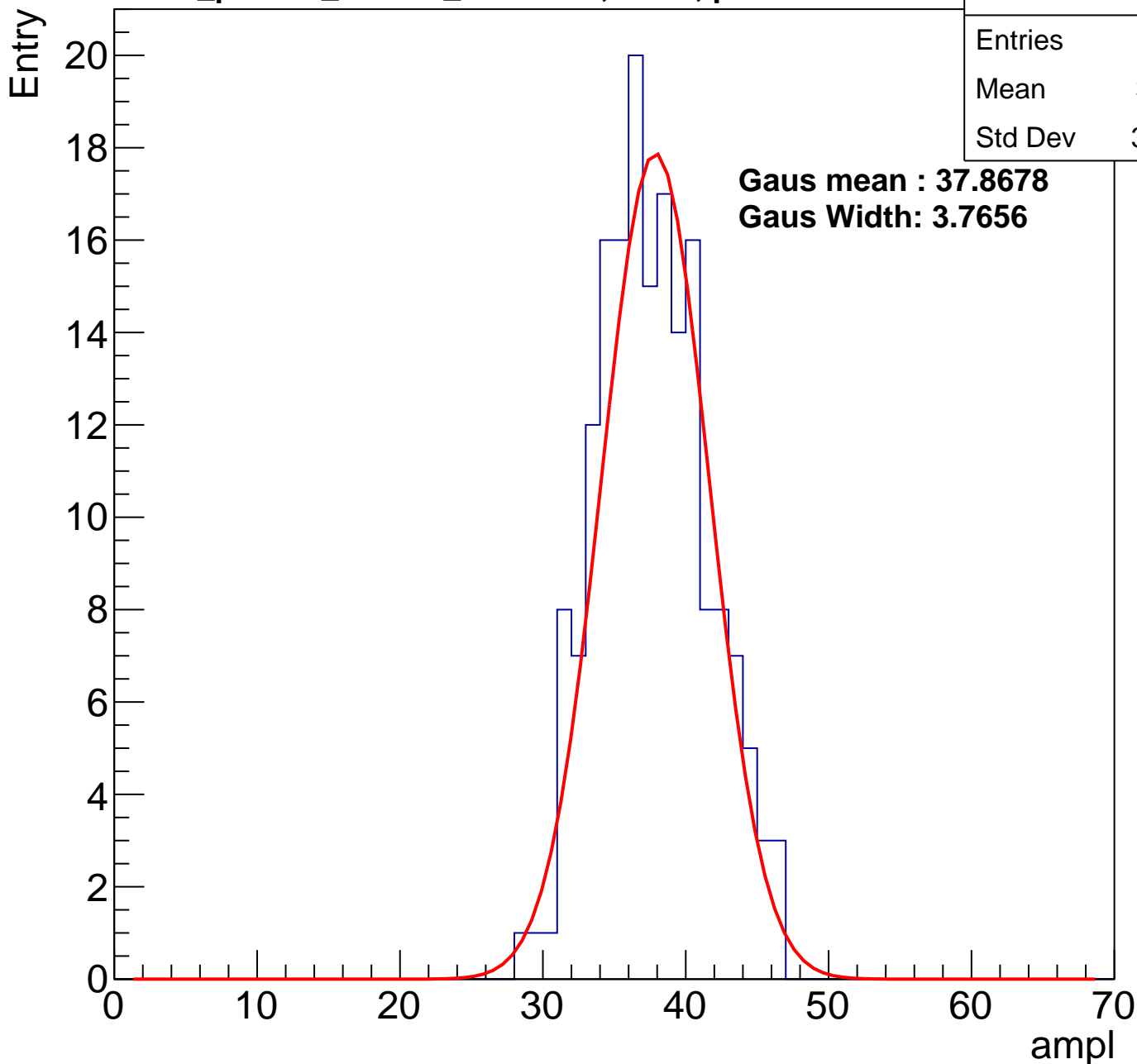
B0L101S, U18-ch11, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	37.21
Std Dev	3.782

Gaus mean : 37.8678

Gaus Width: 3.7656



B0L101S, U18-ch11, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	44.77
Std Dev	3.667

Gaus mean : 45.1551
Gaus Width: 3.3138

Entry

12

10

8

6

4

2

0

0

10

20

30

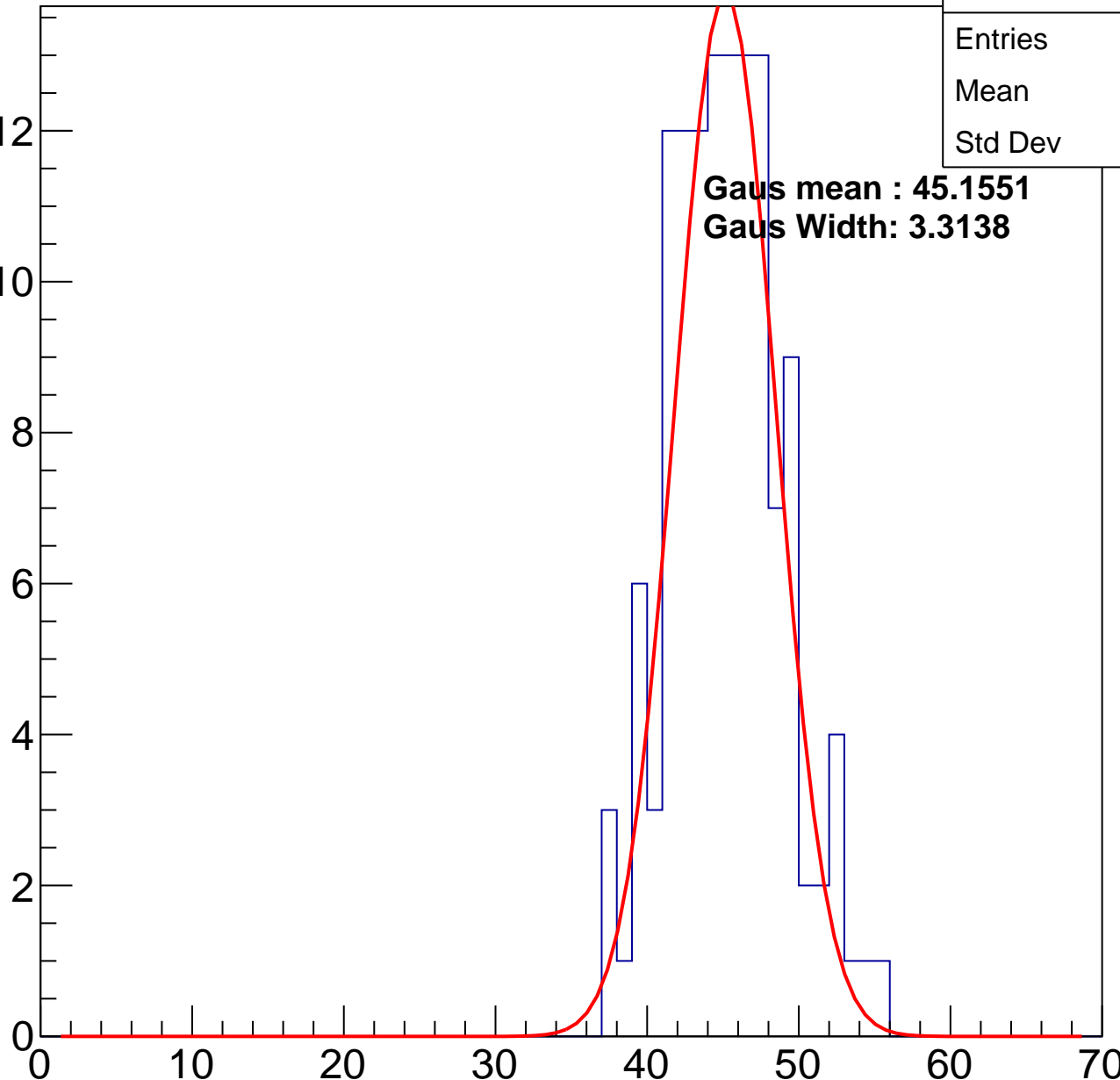
40

50

60

70

ampl



B0L101S, U18-ch11, adc3

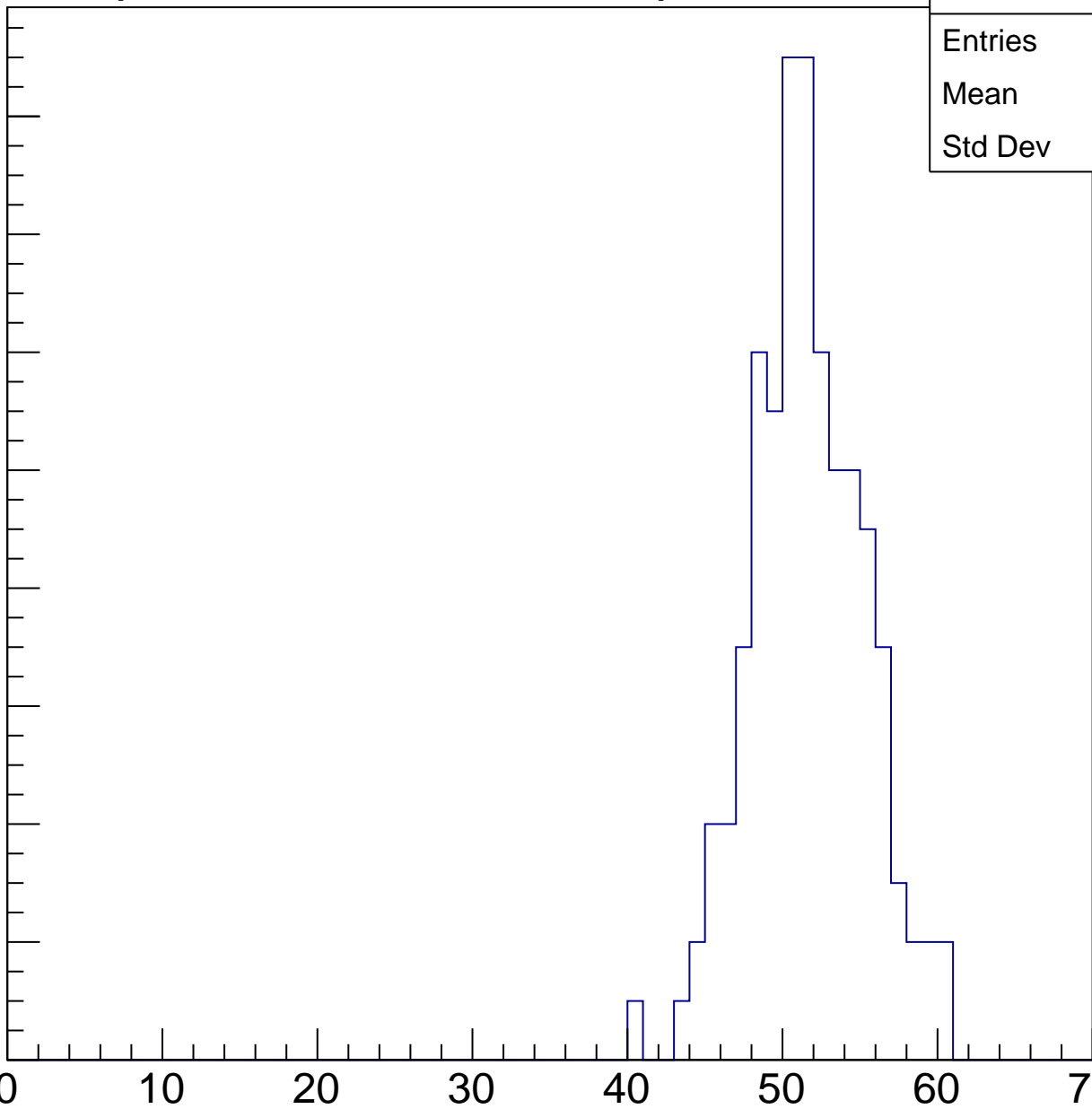
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	51.14
Std Dev	3.64

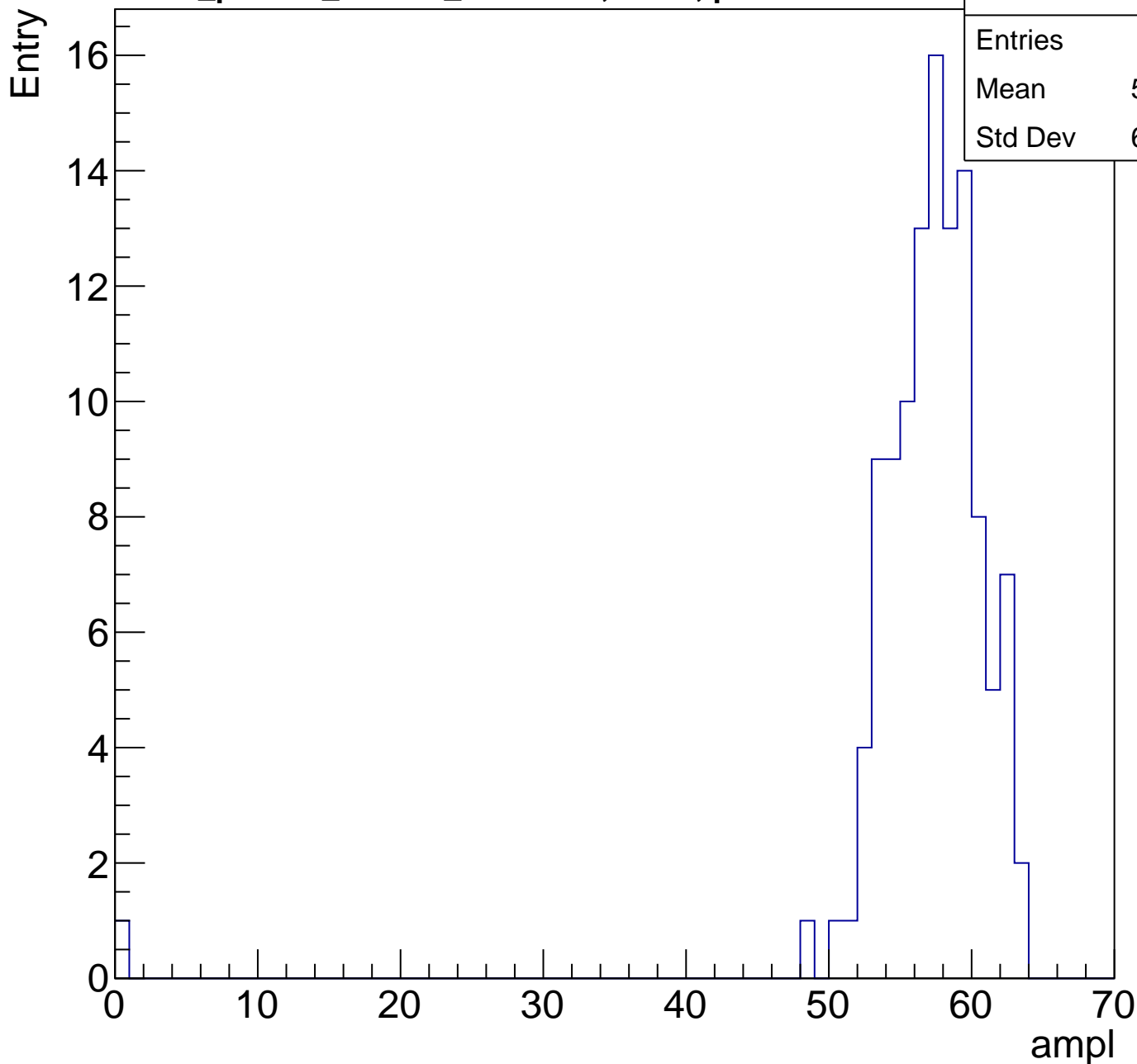
ampl



B0L101S, U18-ch11, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	56.45
Std Dev	6.089



B0L101S, U18-ch11, adc5

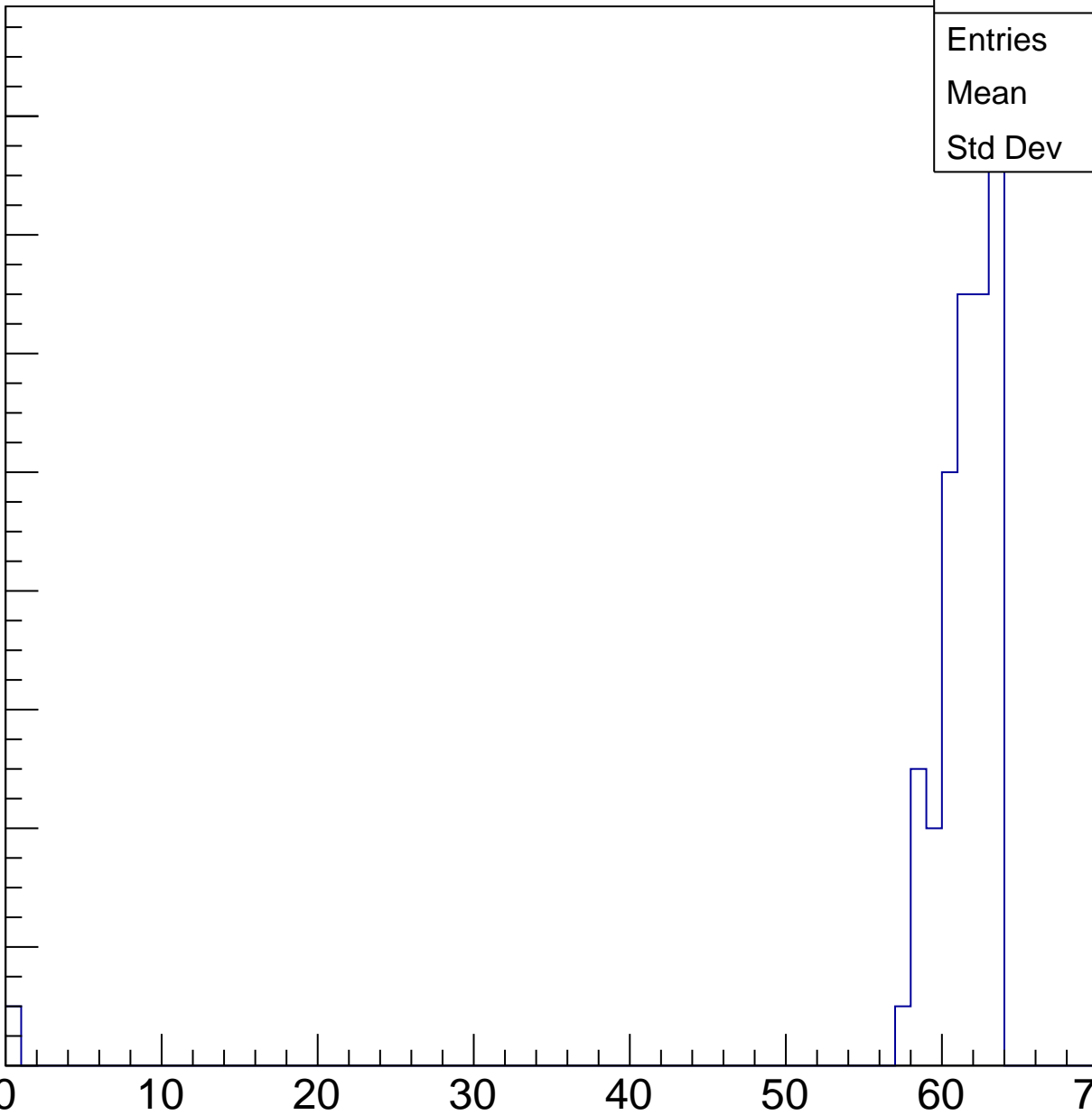
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	64
Mean	60.2
Std Dev	7.754

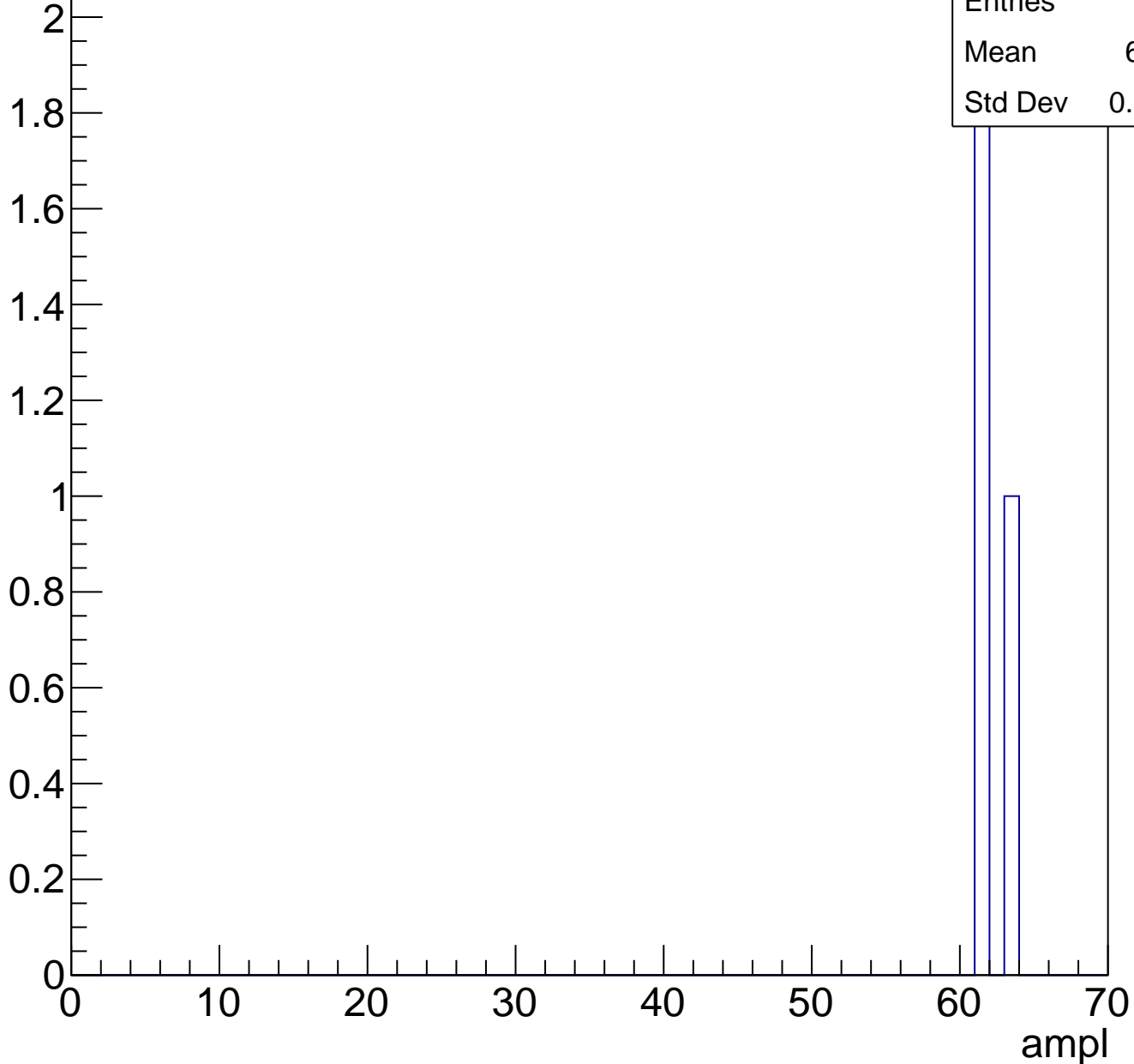
ampl



B0L101S, U18-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	61.67
Std Dev	0.9428

B0L101S, U18-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch12, adc0

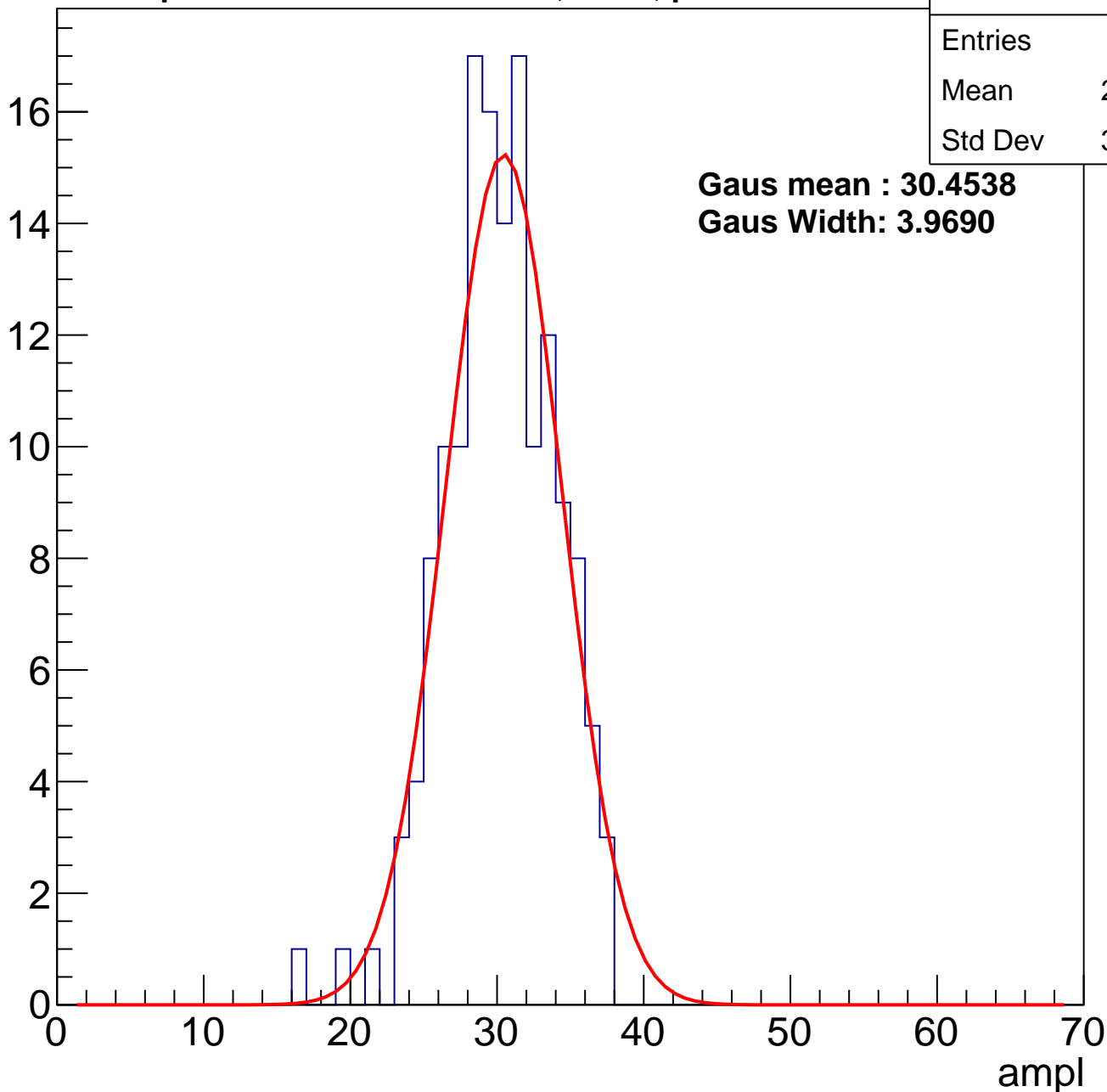
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	29.74
Std Dev	3.708

Gaus mean : 30.4538

Gaus Width: 3.9690

Entry



B0L101S, U18-ch12, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	37.54
Std Dev	3.494

Gaus mean : 37.9030

Gaus Width: 3.4239

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

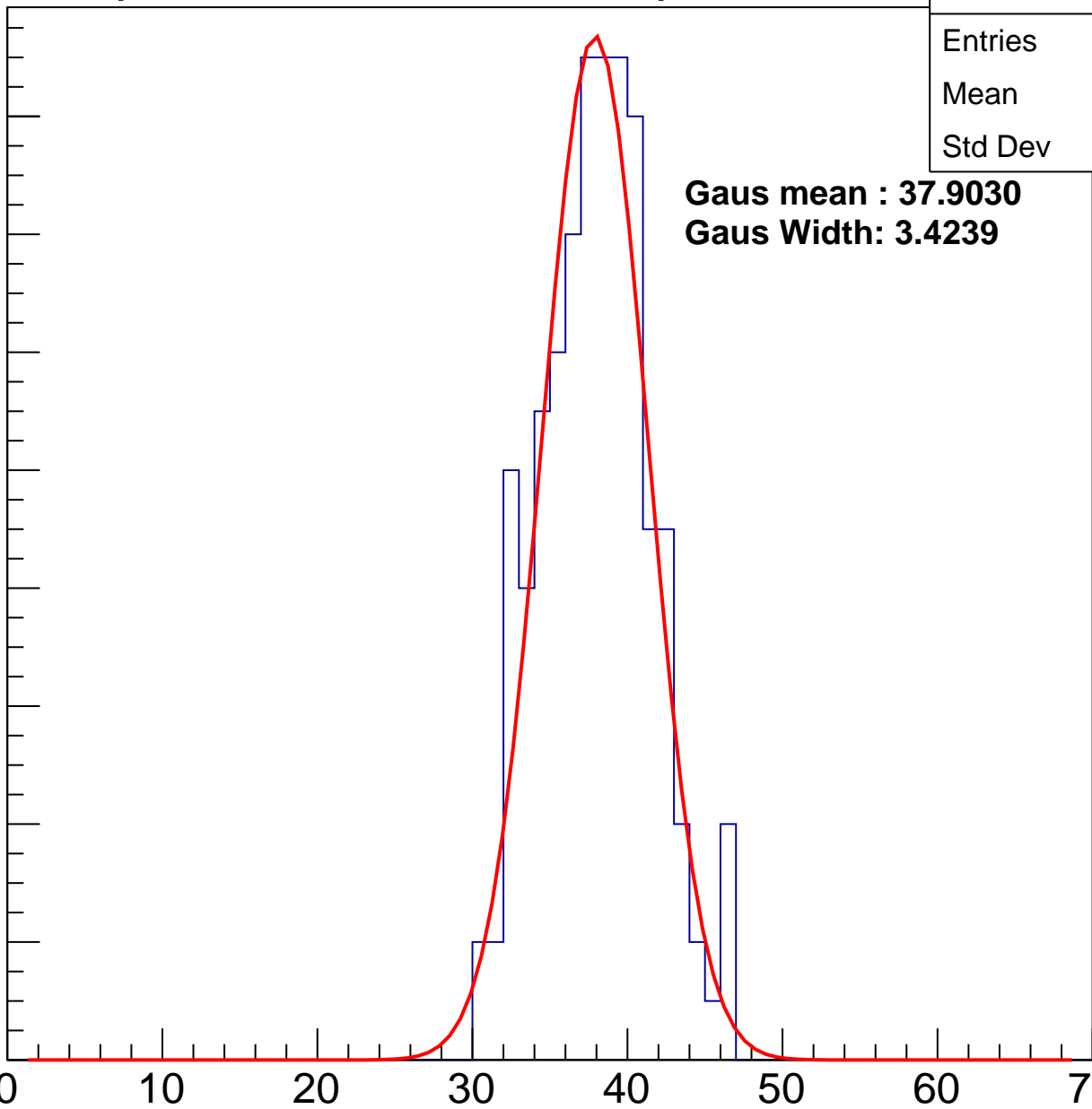
40

50

60

70

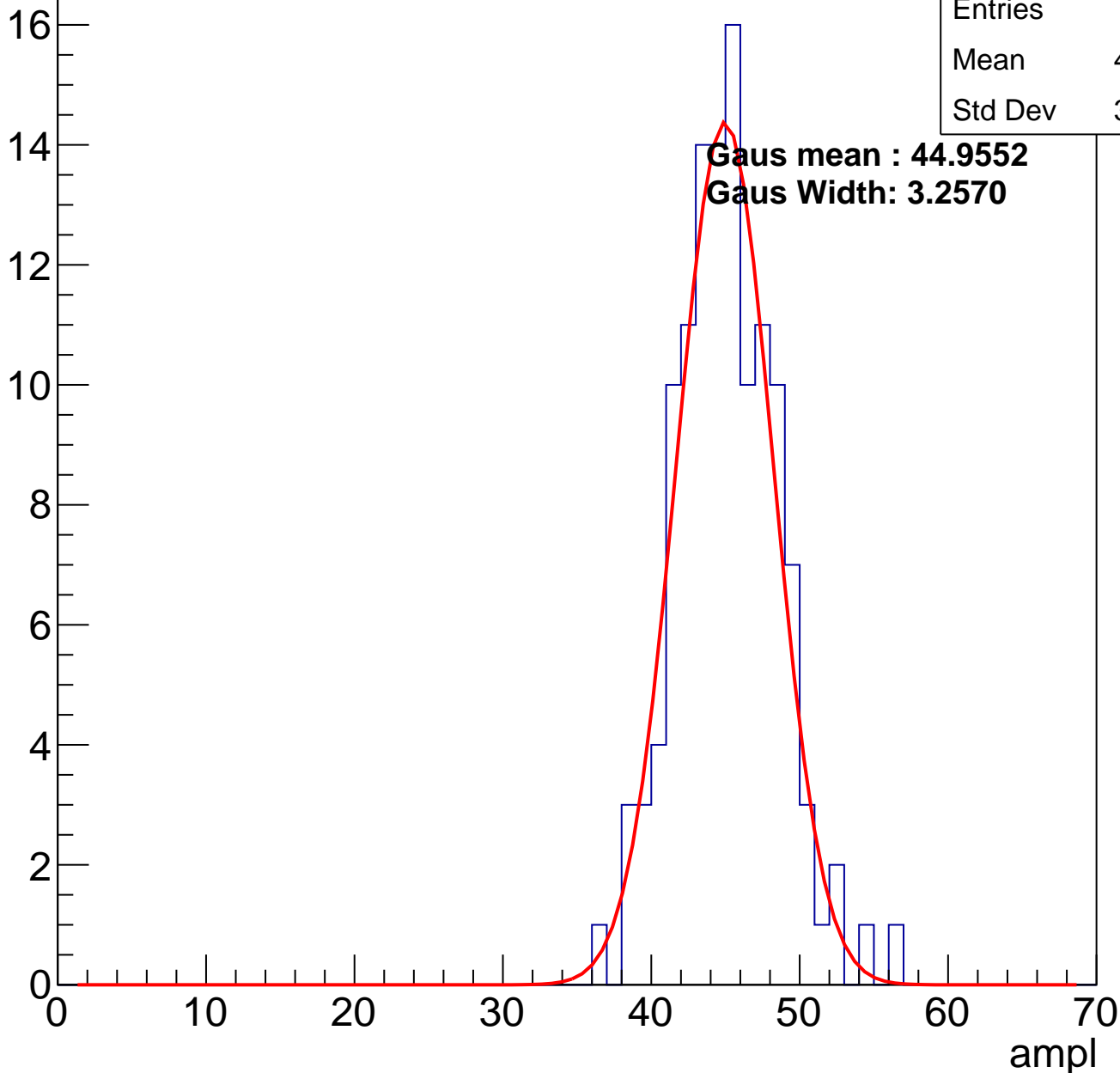
ampl



B0L101S, U18-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

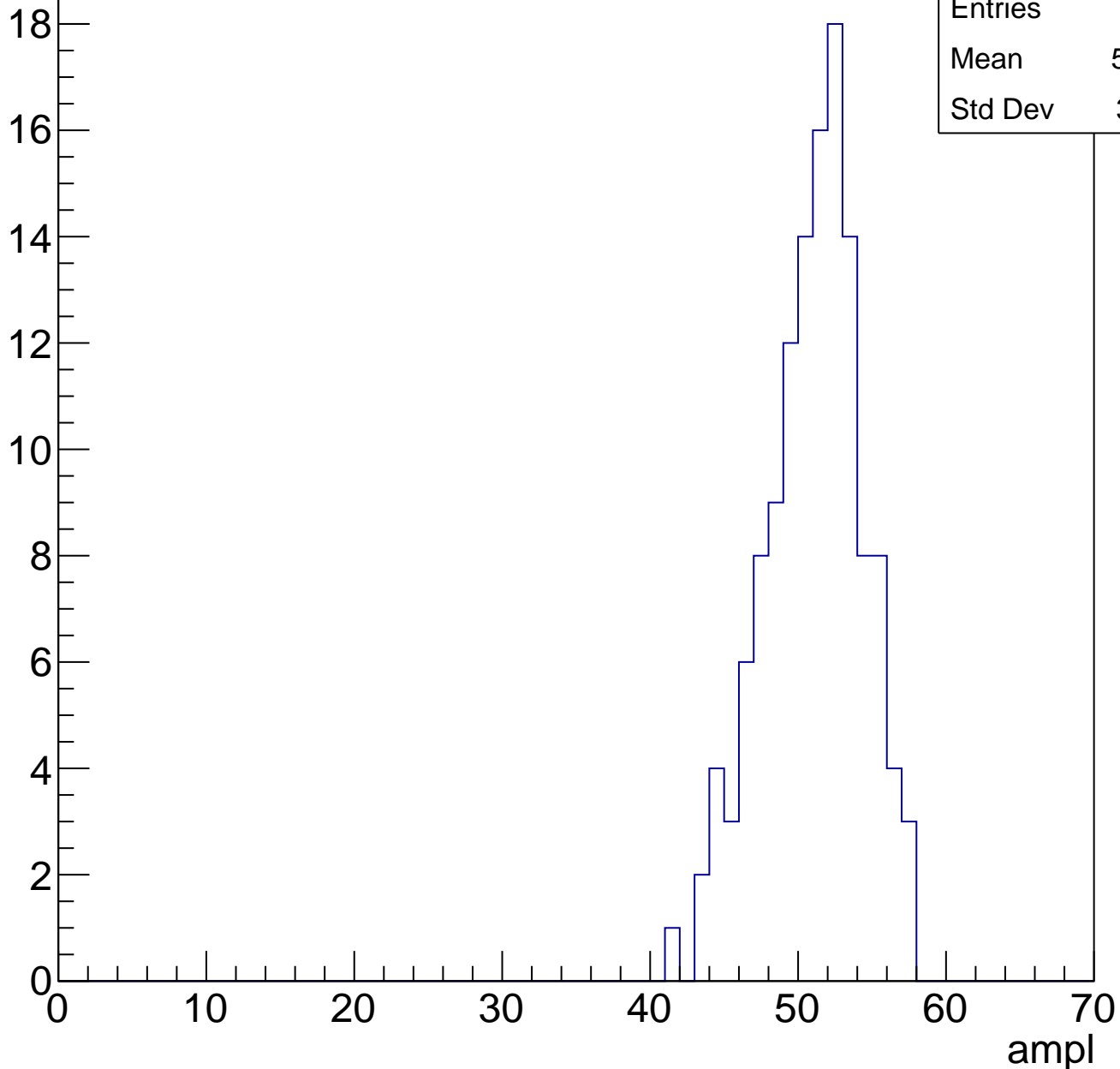


B0L101S, U18-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	50.55
Std Dev	3.291

Entry



B0L101S, U18-ch12, adc4

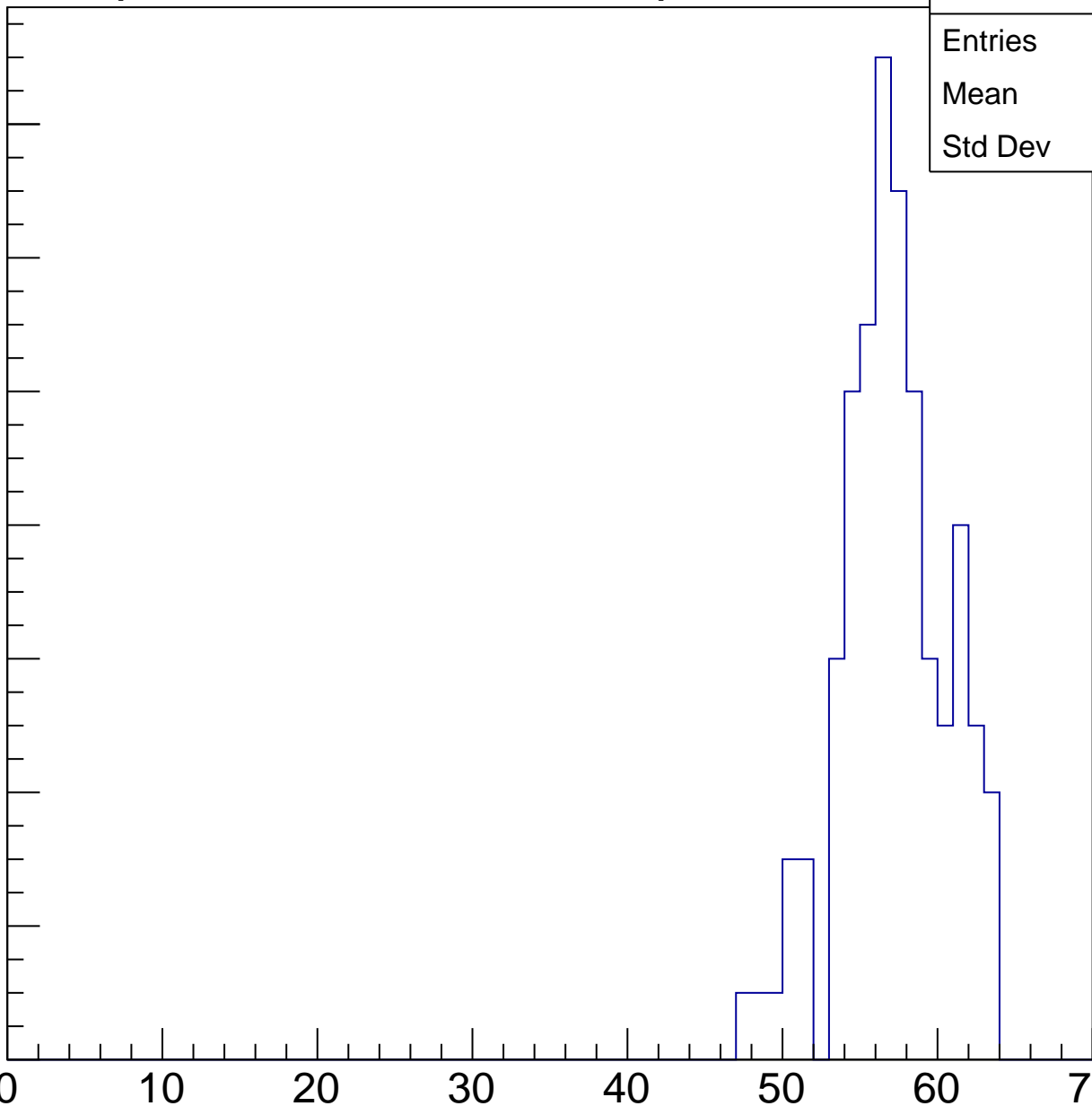
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	56.62
Std Dev	3.439

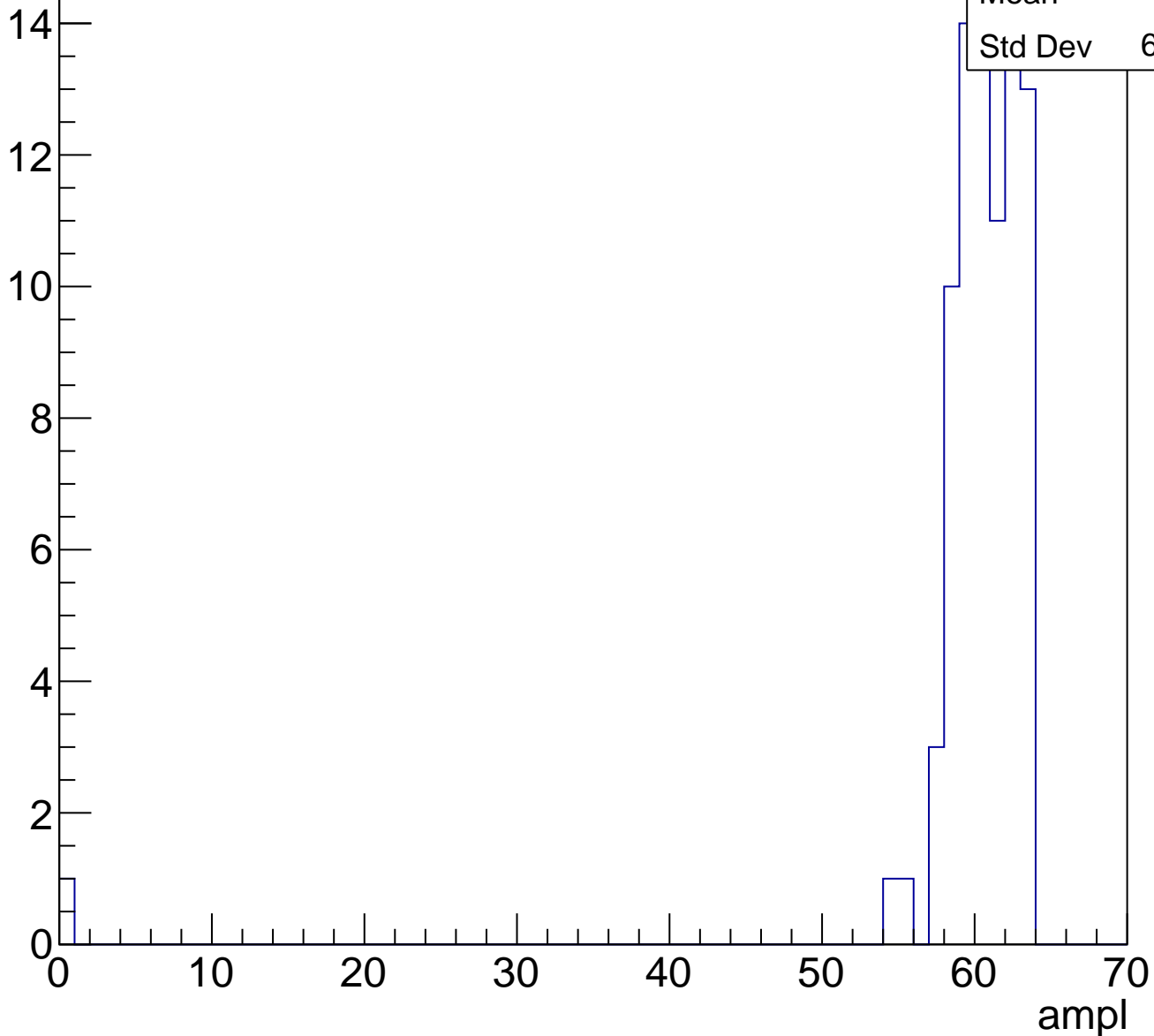
ampl



B0L101S, U18-ch12, adc5

calib_packv5_042523_0143.root, FC#1, port C1

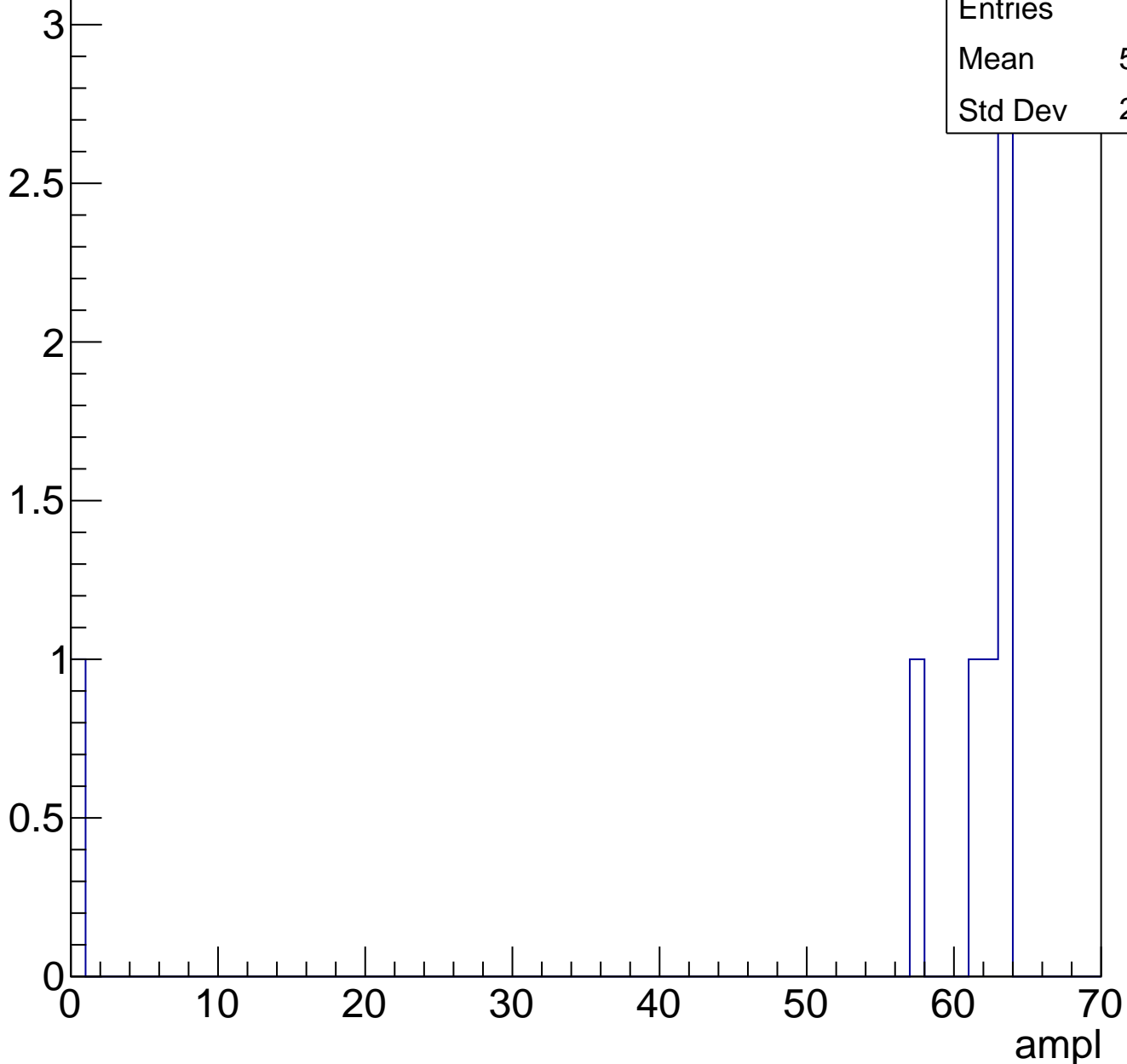
Entry



B0L101S, U18-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch13, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	29.75
Std Dev	5.694

Gaus mean : 30.7485

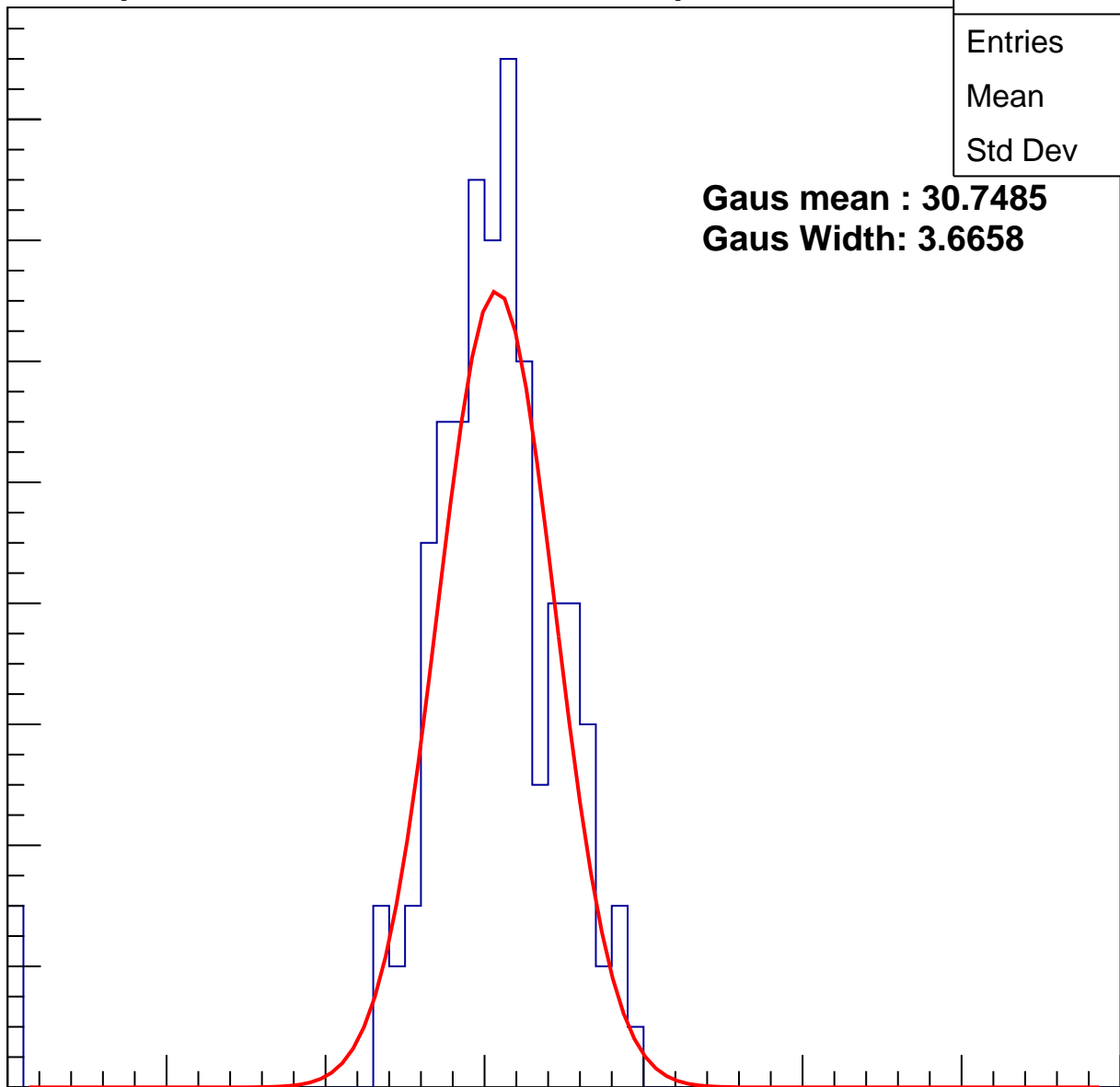
Gaus Width: 3.6658

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch13, adc1

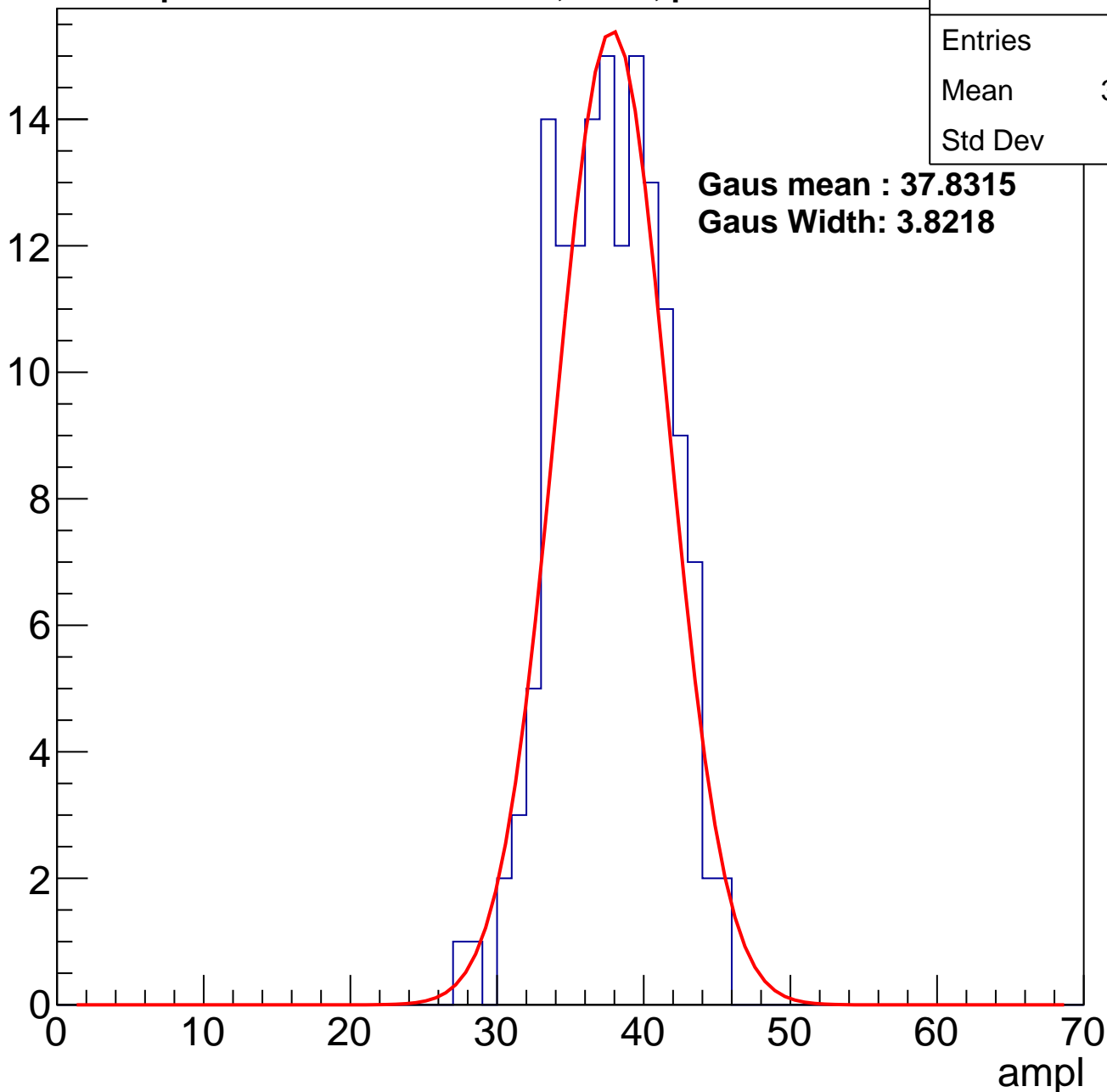
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	37.24
Std Dev	3.62

Gaus mean : 37.8315

Gaus Width: 3.8218

Entry



B0L101S, U18-ch13, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	44.67
Std Dev	3.522

Gaus mean : 45.2385

Gaus Width: 3.8923

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

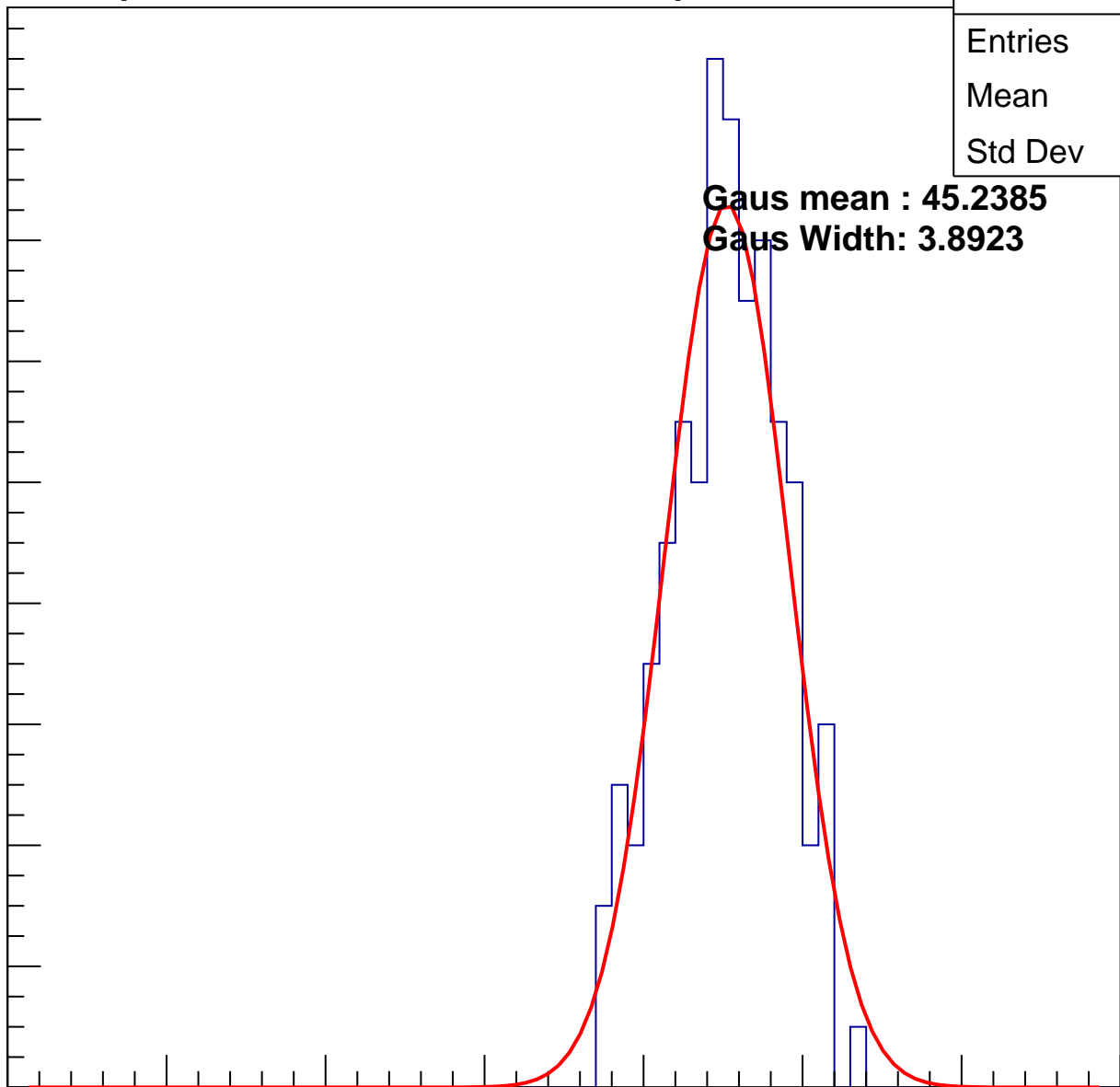
40

50

60

ampl

70



B0L101S, U18-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

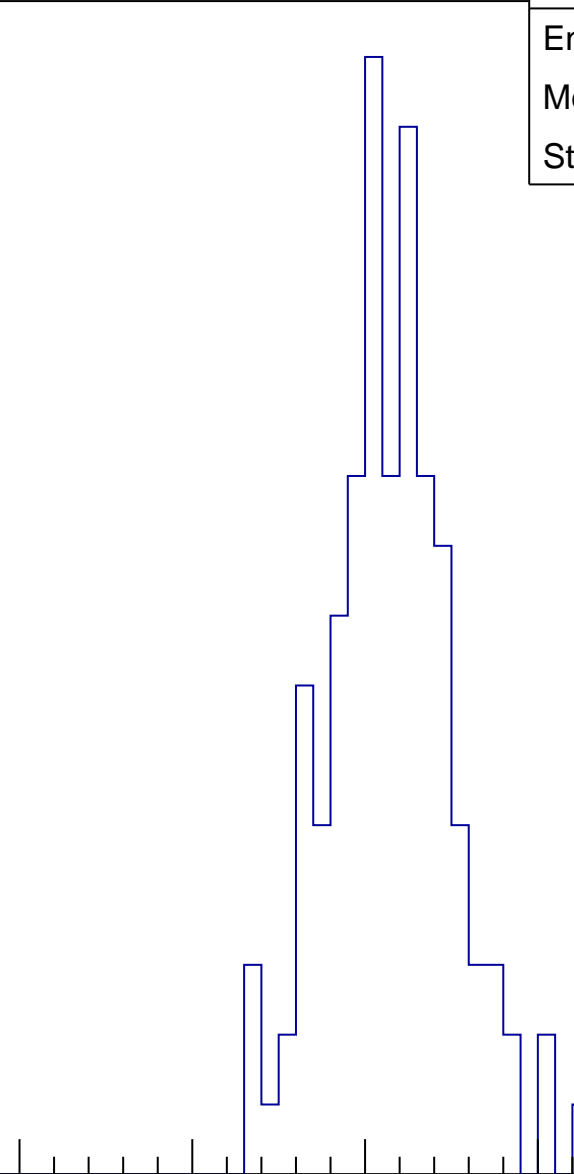
Entries	112
Mean	51
Std Dev	3.63

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch13, adc4

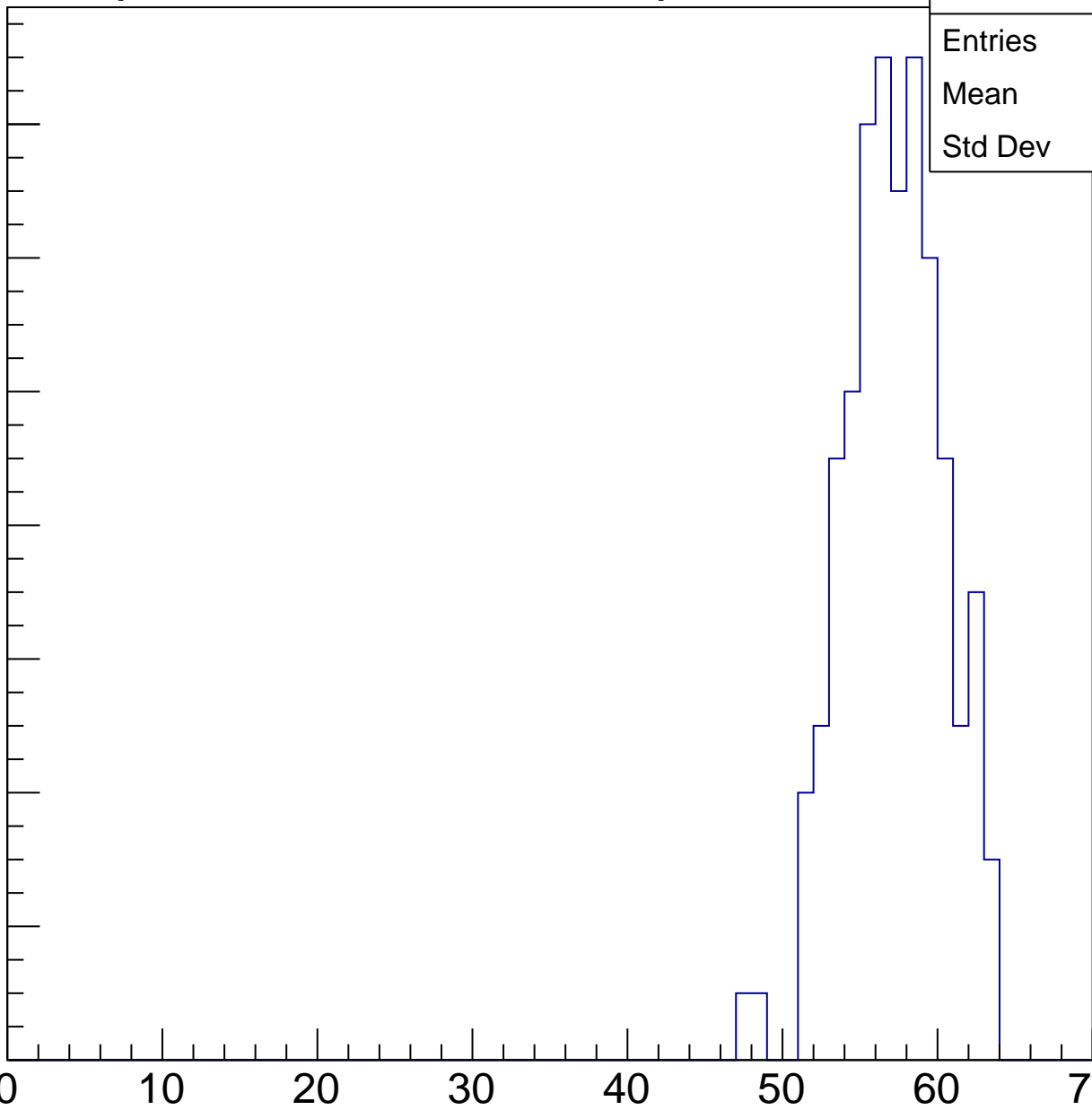
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	56.69
Std Dev	3.188

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch13, adc5

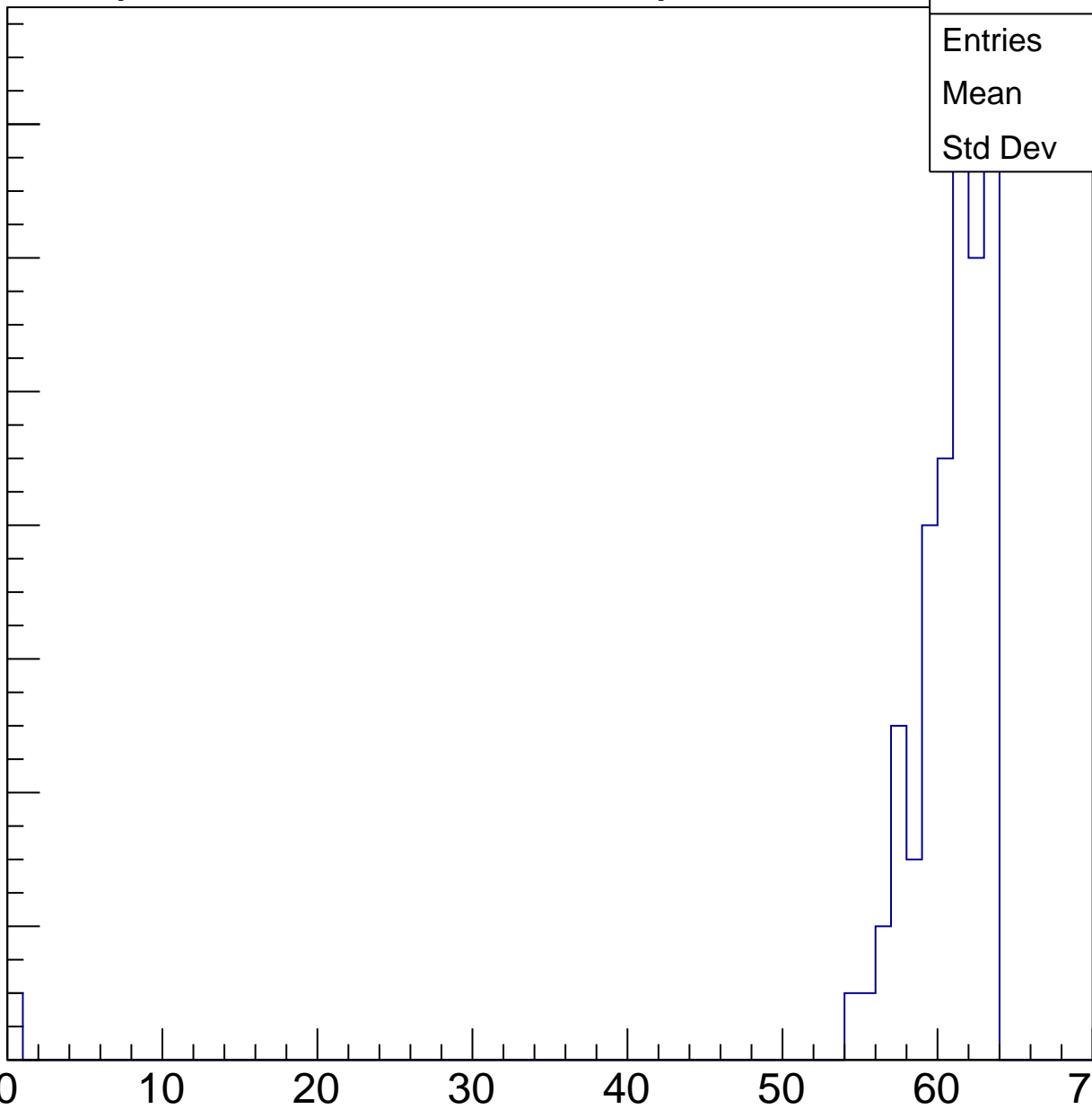
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.67
Std Dev	7.405

ampl



B0L101S, U18-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch14, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	28.81
Std Dev	4.938

Gaus mean : 29.6765

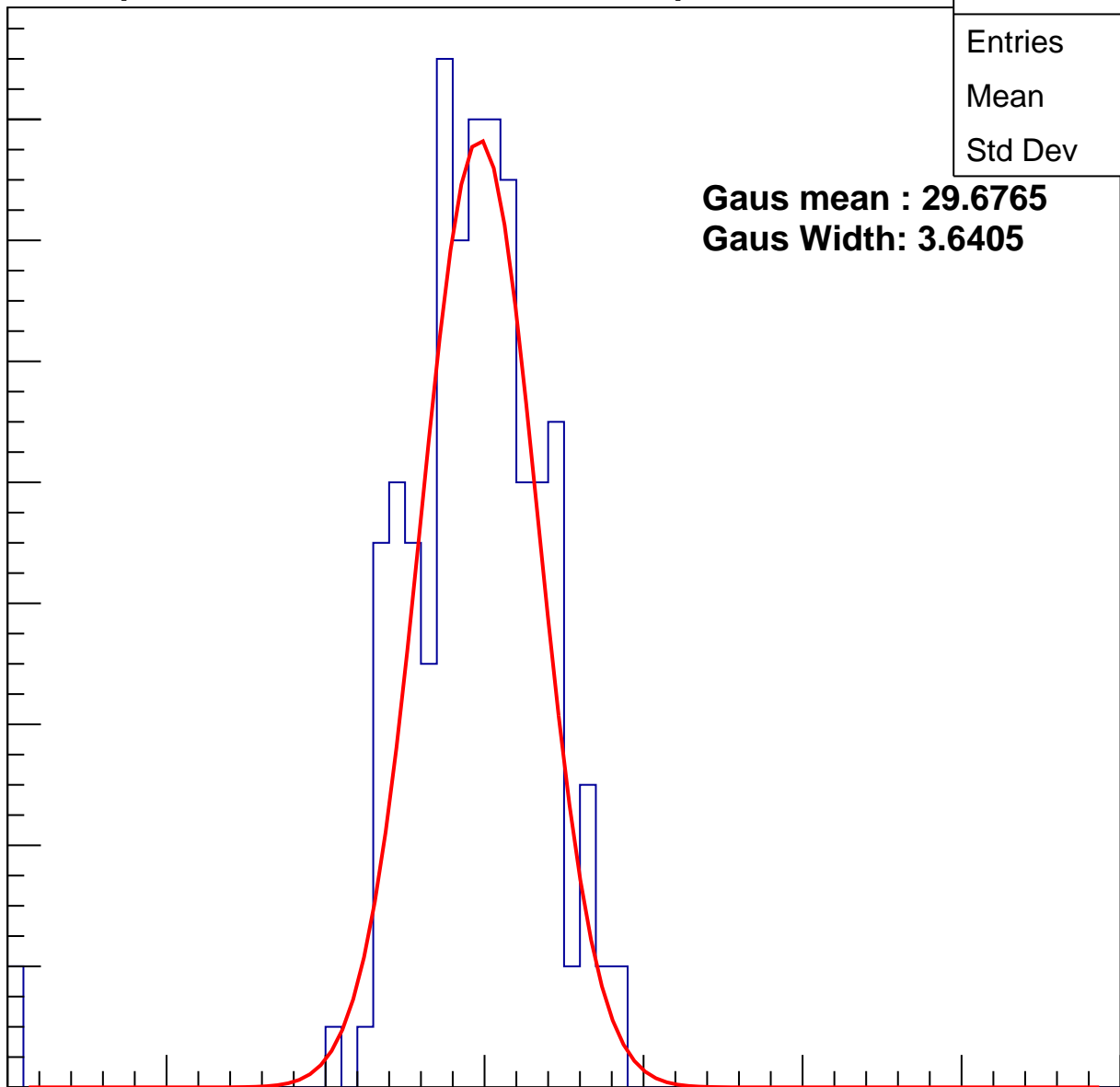
Gaus Width: 3.6405

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



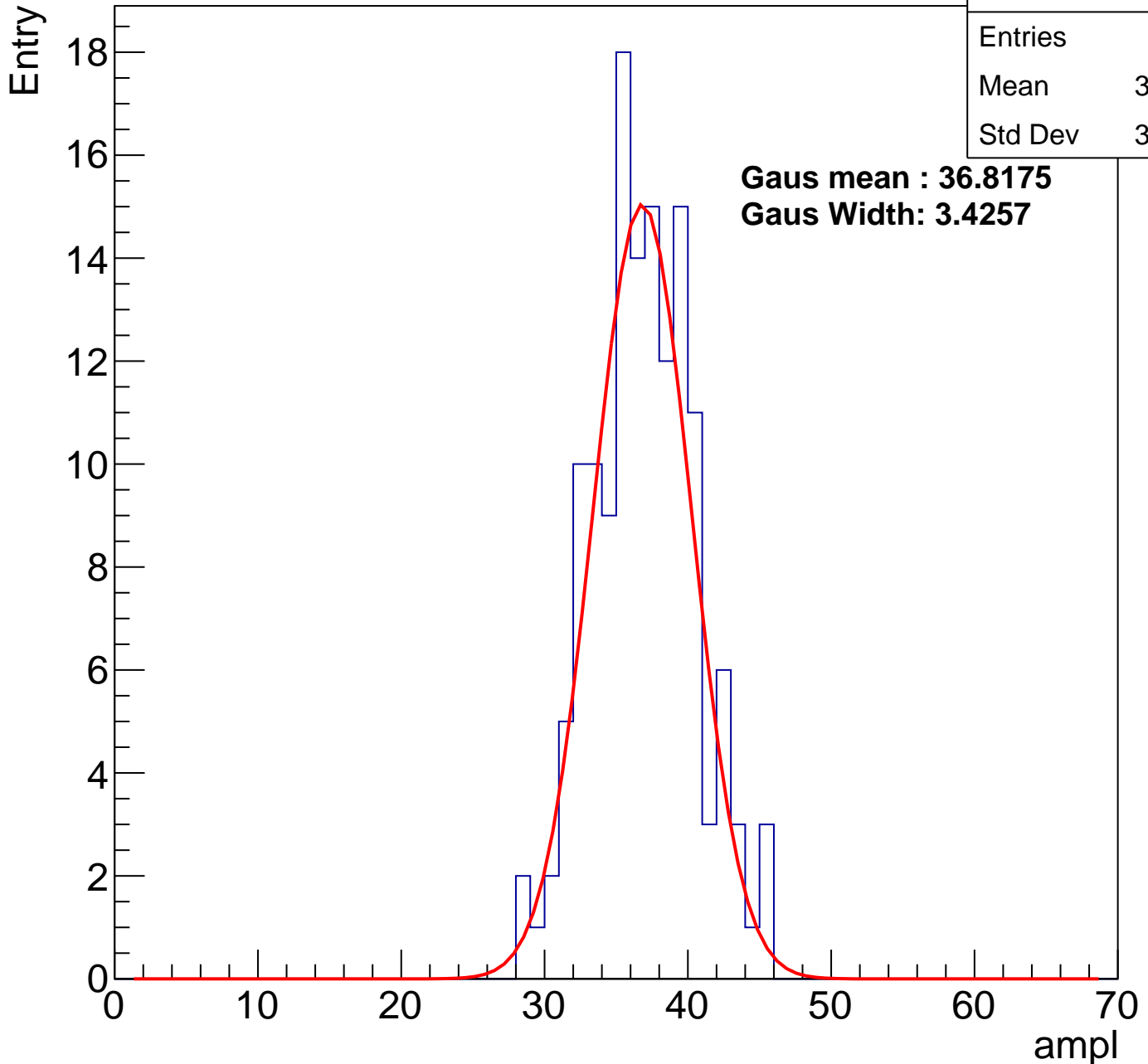
B0L101S, U18-ch14, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	36.49
Std Dev	3.549

Gaus mean : 36.8175

Gaus Width: 3.4257



B0L101S, U18-ch14, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	43.51
Std Dev	3.378

Gaus mean : 44.0301

Gaus Width: 3.5785

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

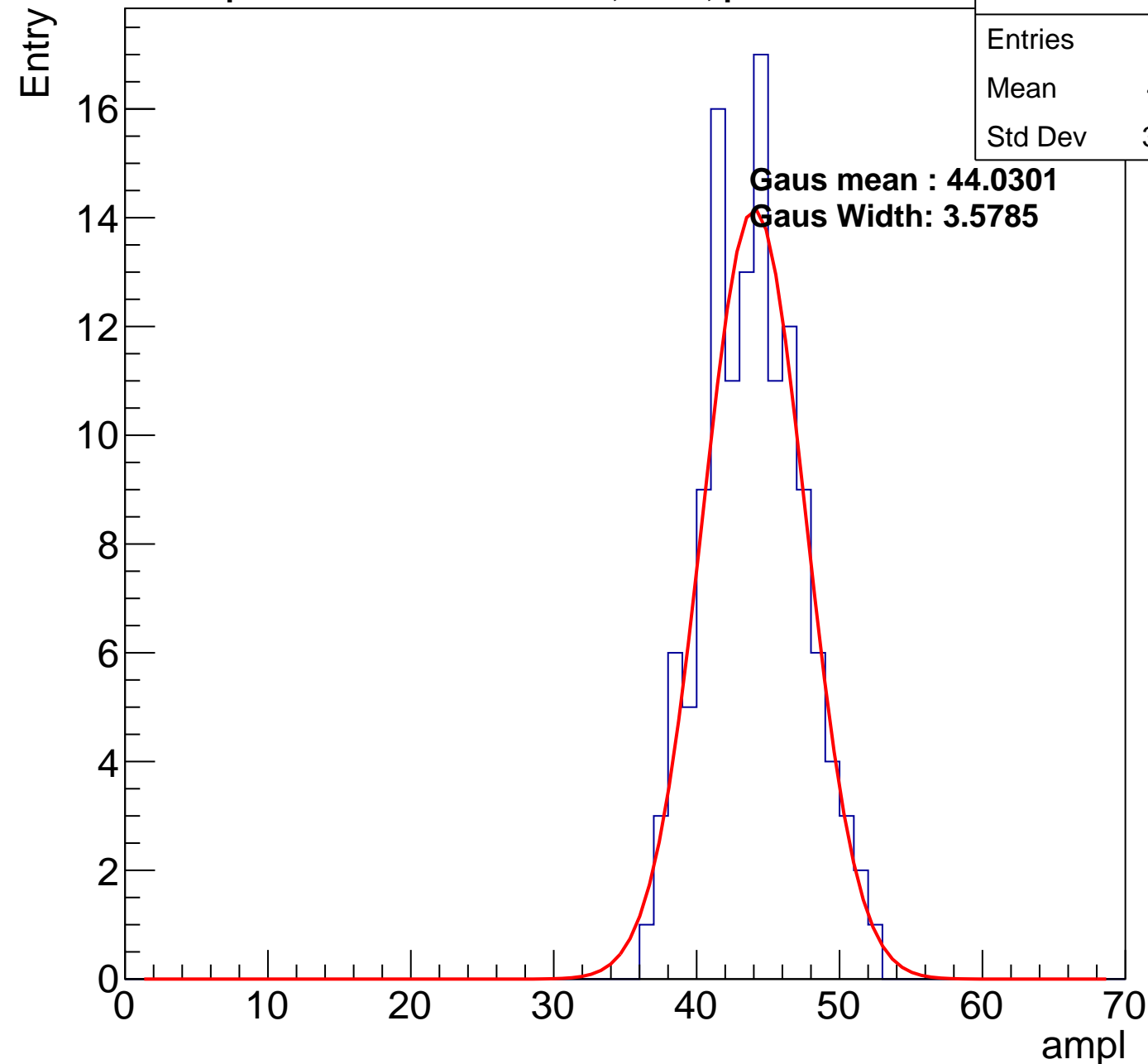
40

50

60

70

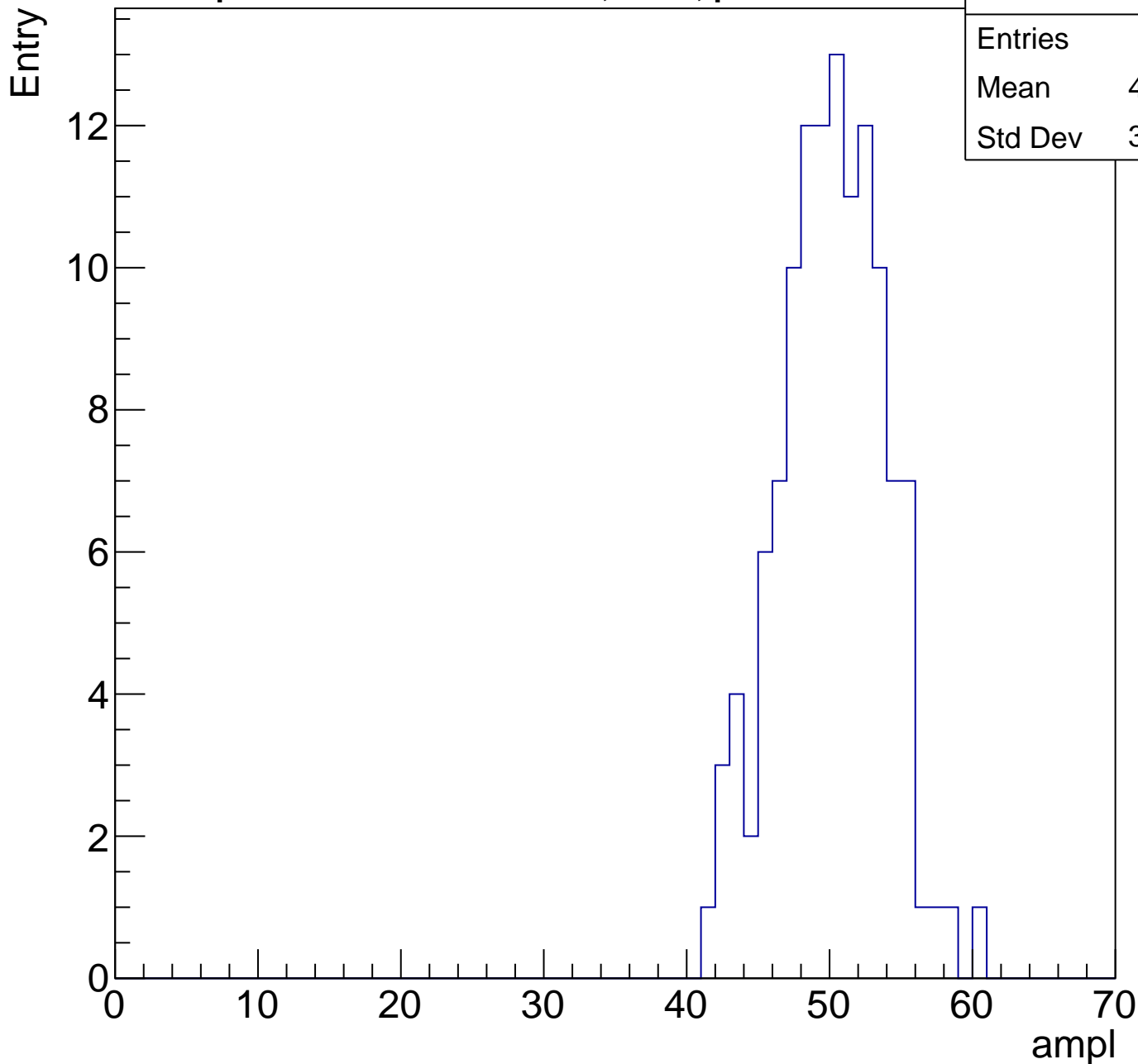
ampl



B0L101S, U18-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	49.69
Std Dev	3.666

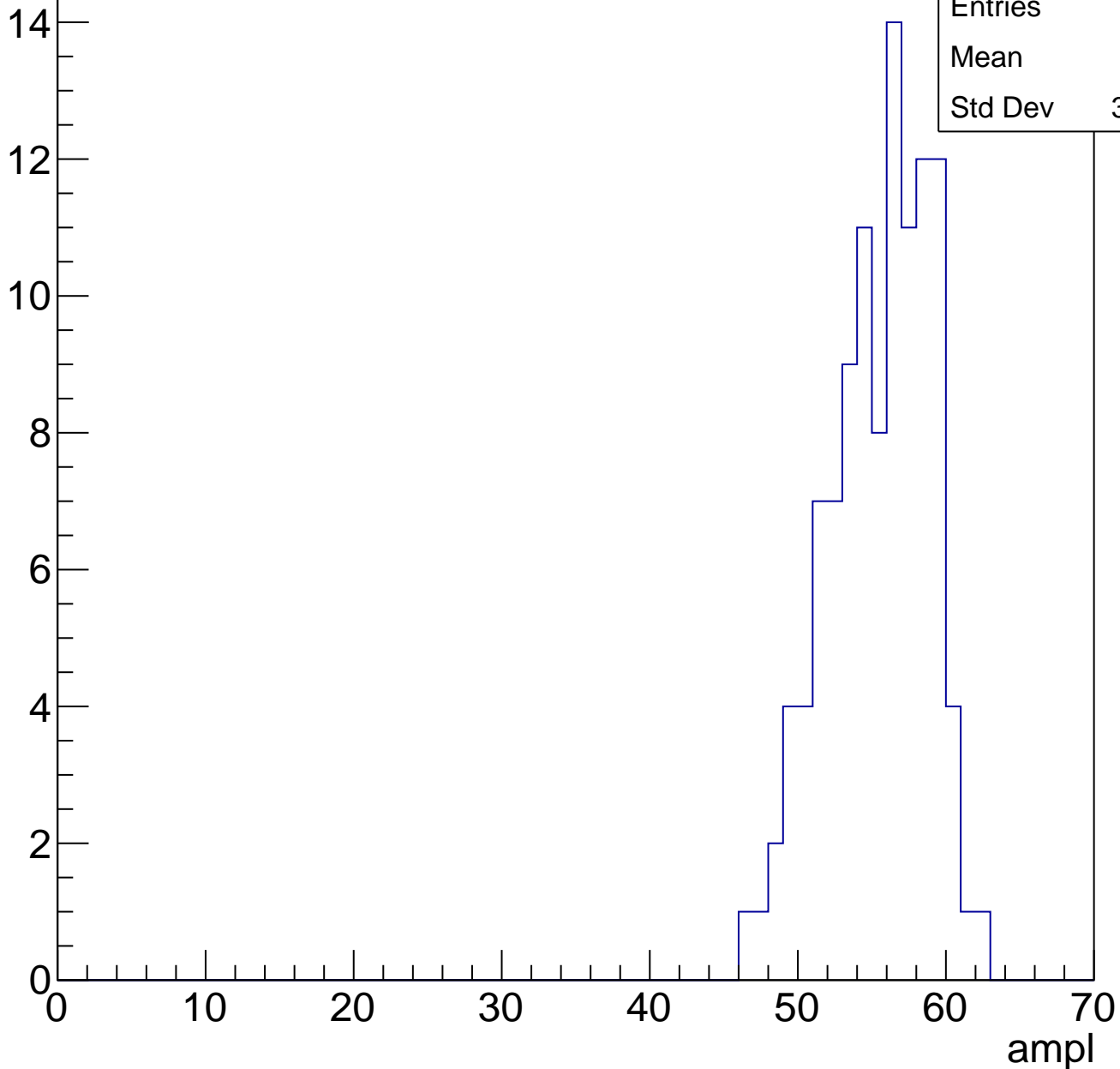


B0L101S, U18-ch14, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55
Std Dev	3.389

Entry



B0L101S, U18-ch14, adc5

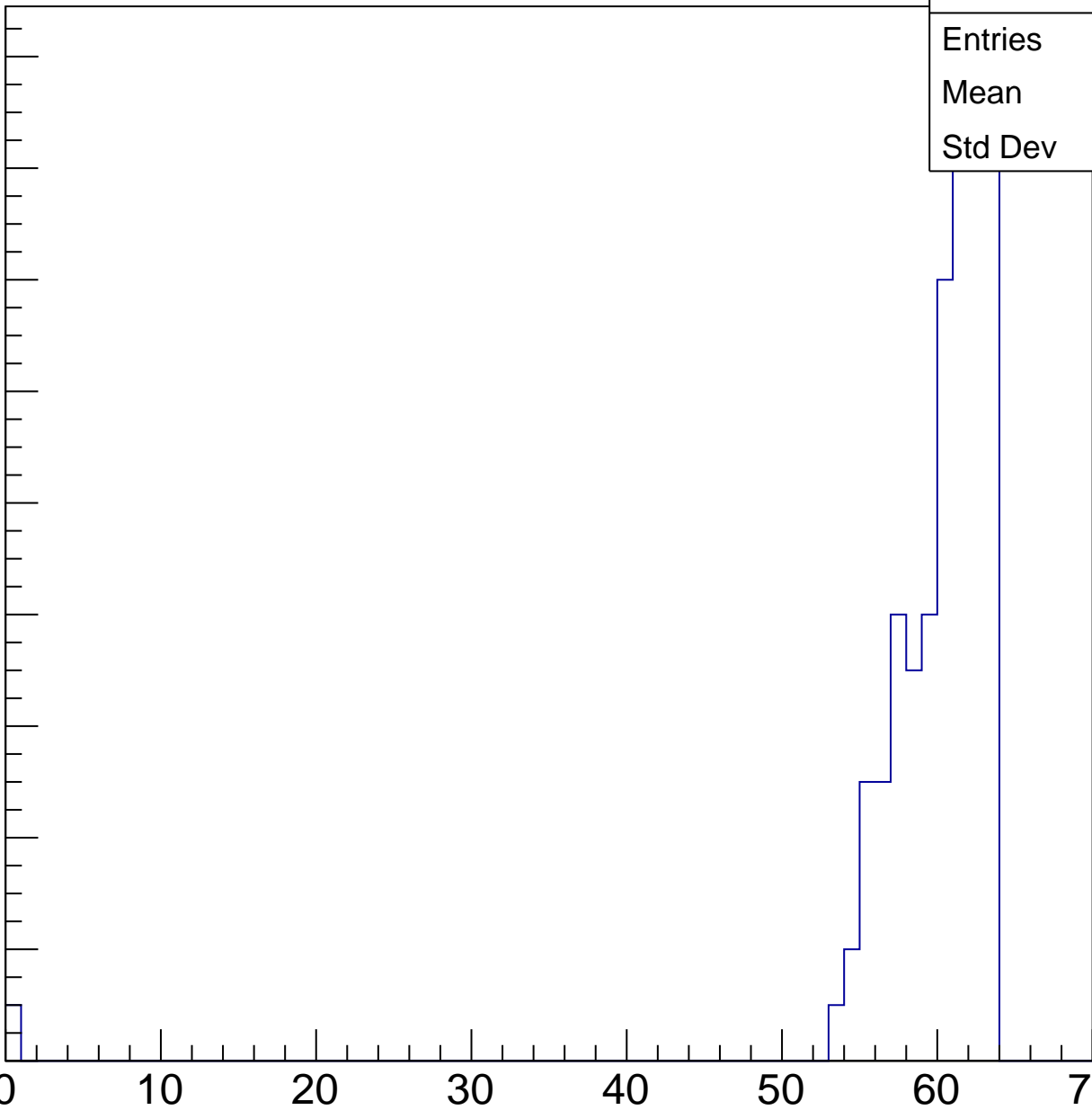
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	101
Mean	59.31
Std Dev	6.462

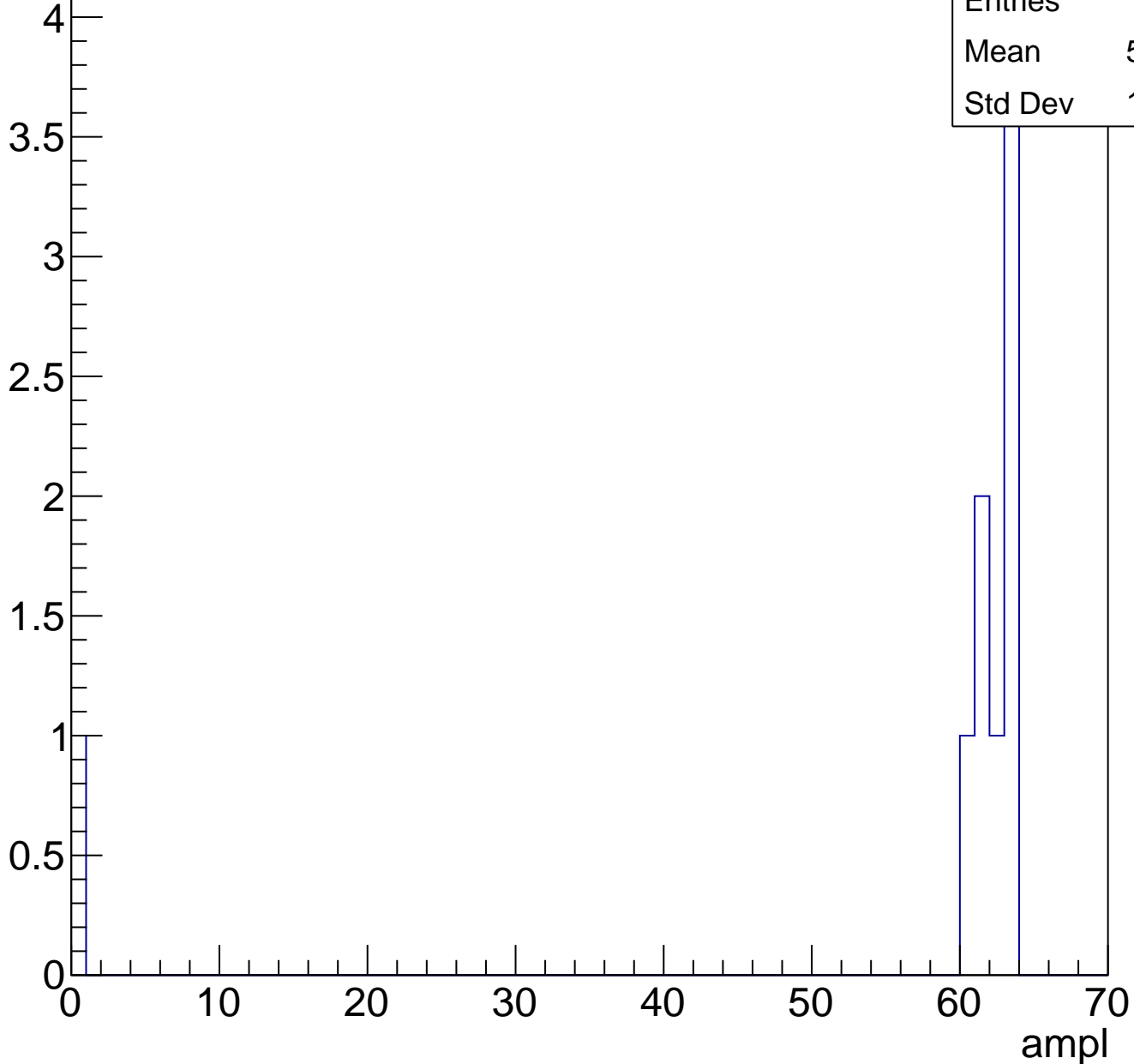
ampl



B0L101S, U18-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

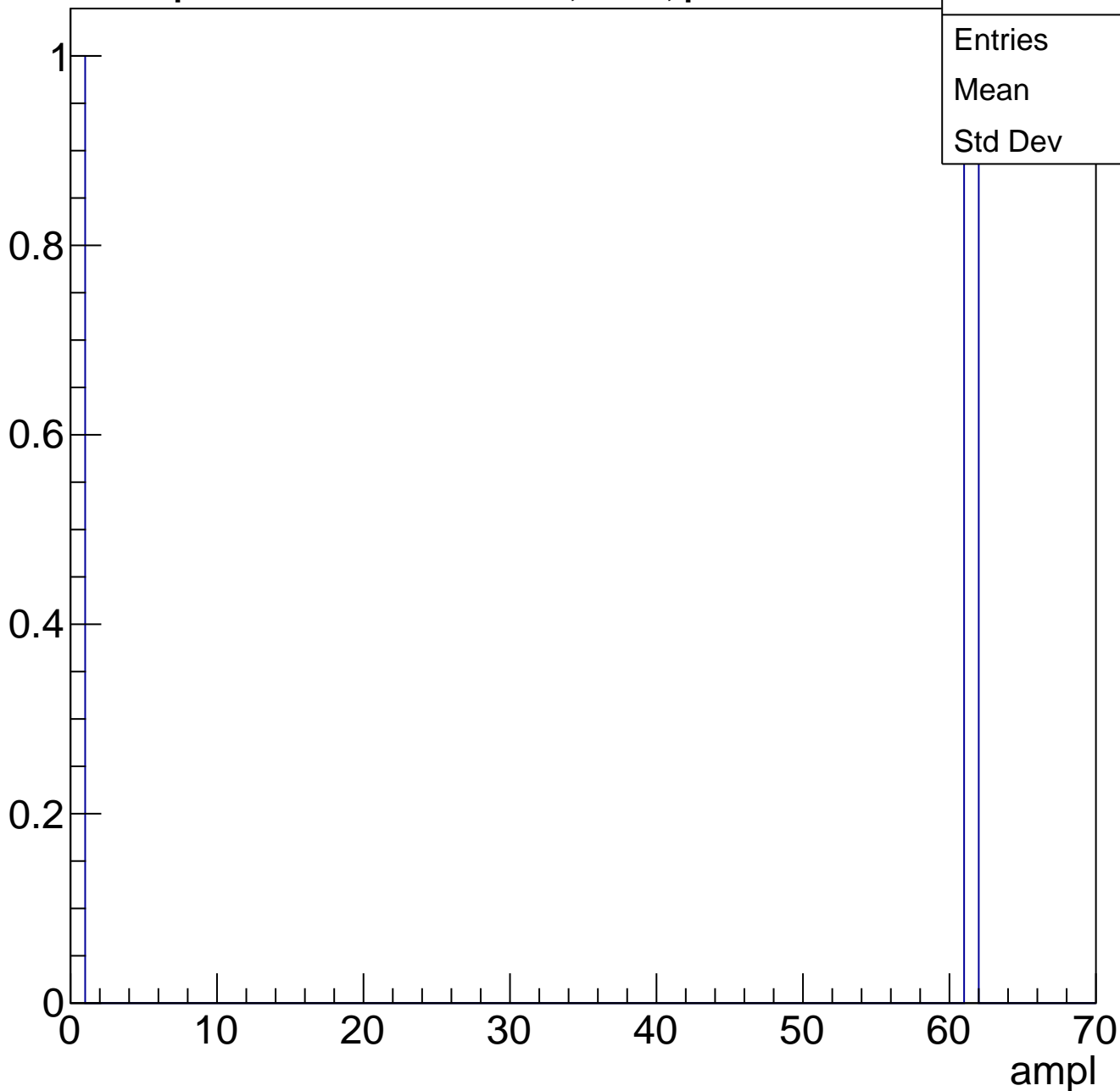
Entry



B0L101S, U18-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch15, adc0

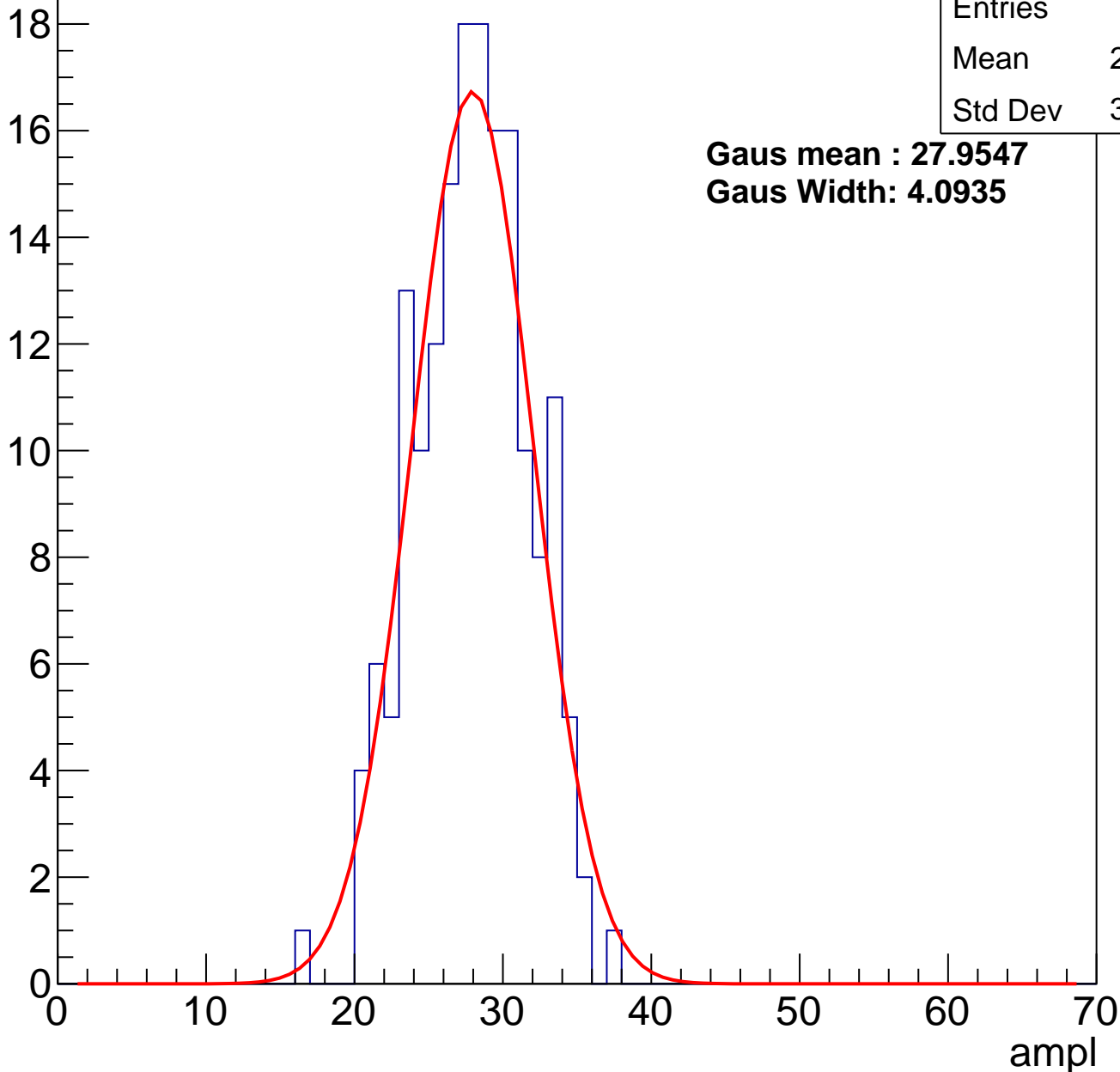
calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	27.49
Std Dev	3.772

Gaus mean : 27.9547

Gaus Width: 4.0935

Entry



B0L101S, U18-ch15, adc1

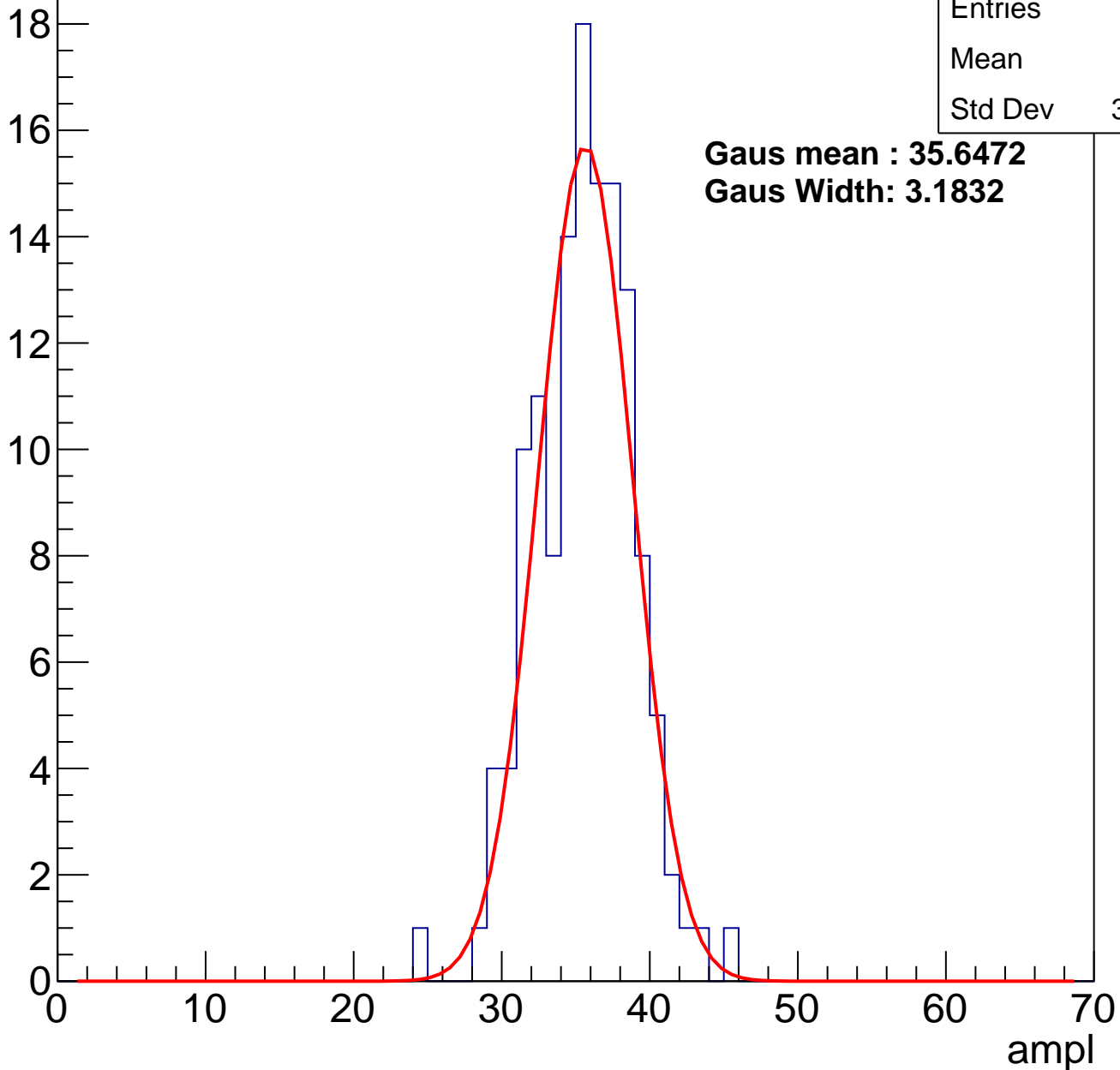
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	35.1
Std Dev	3.312

Gaus mean : 35.6472

Gaus Width: 3.1832

Entry



B0L101S, U18-ch15, adc2

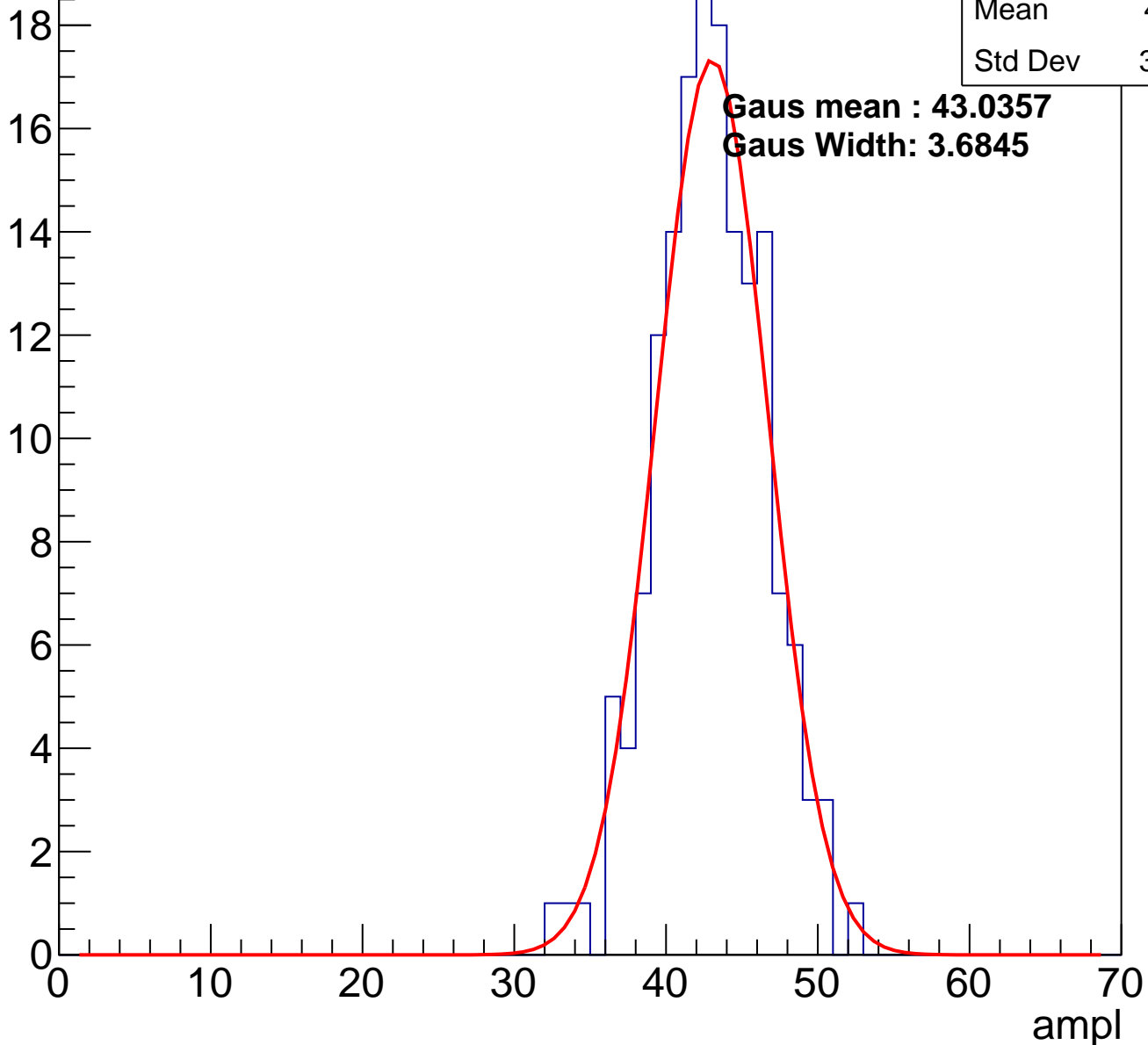
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	42.51
Std Dev	3.555

Gaus mean : 43.0357

Gaus Width: 3.6845

Entry



B0L101S, U18-ch15, adc3

calib_packv5_042523_0143.root, FC#1, port C1

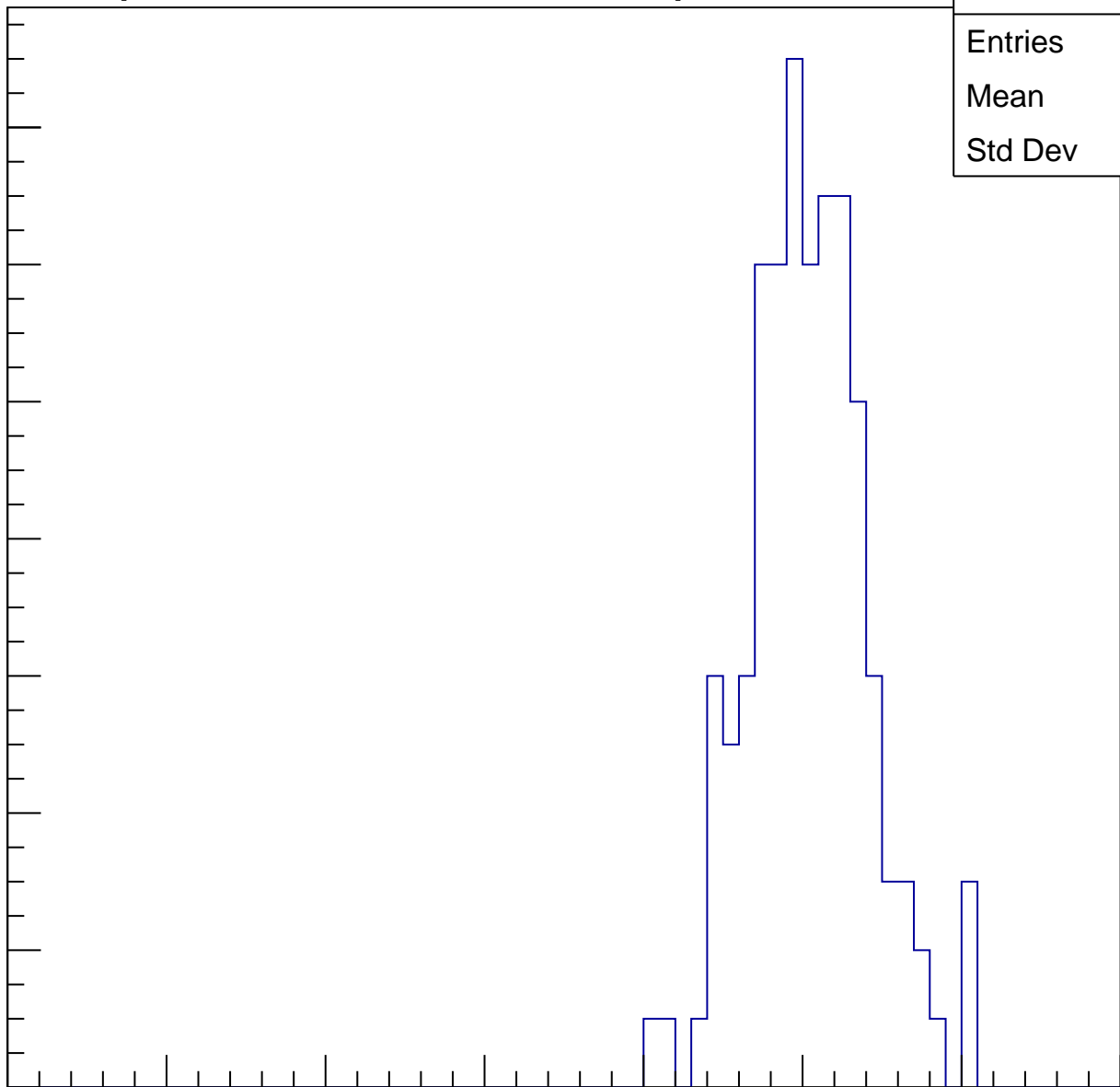
Entries	125
Mean	49.94
Std Dev	3.724

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch15, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	55.64
Std Dev	3.65

Entry

14

12

10

8

6

4

2

0

0

10

20

30

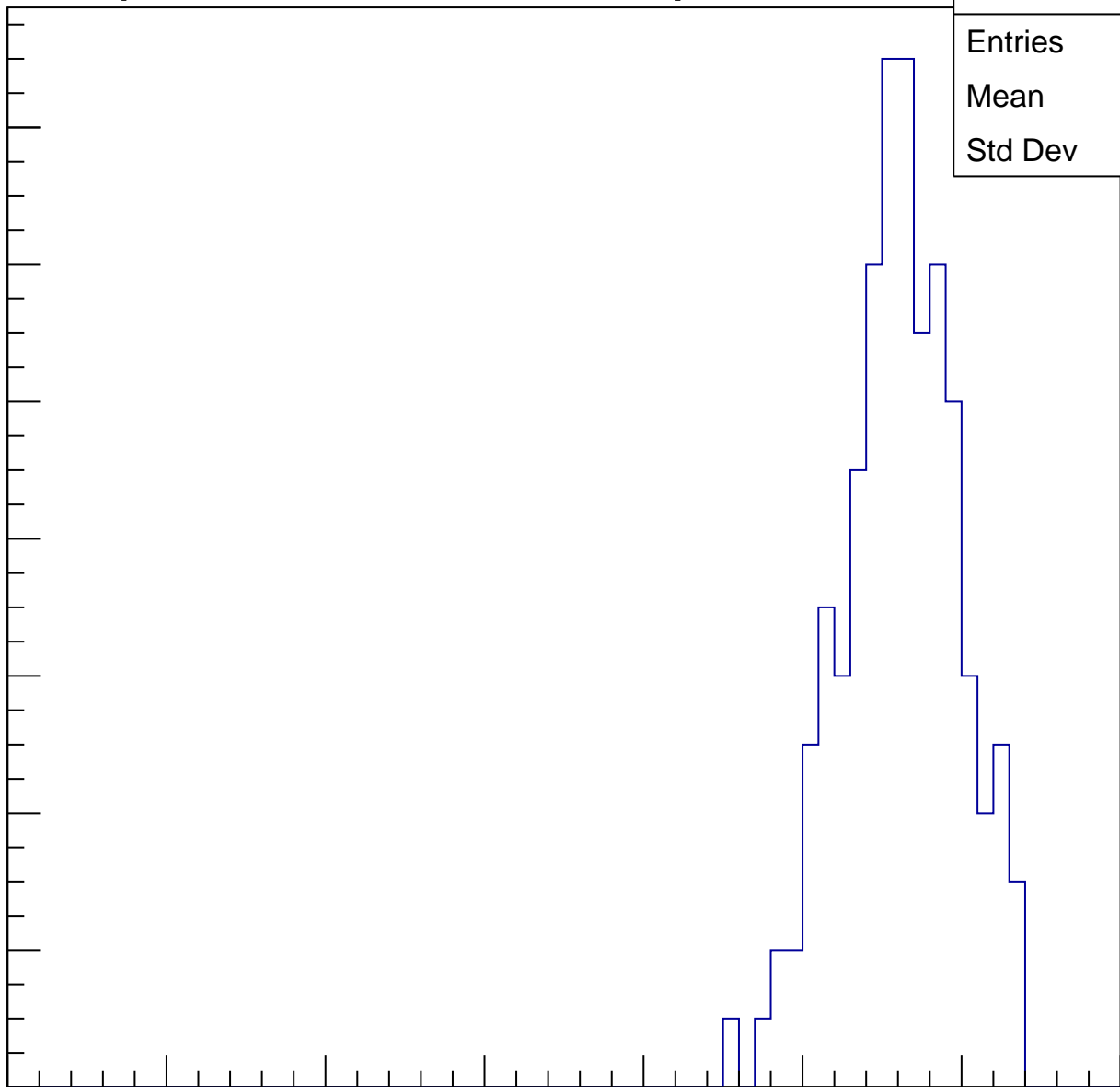
40

50

60

70

ampl



B0L101S, U18-ch15, adc5

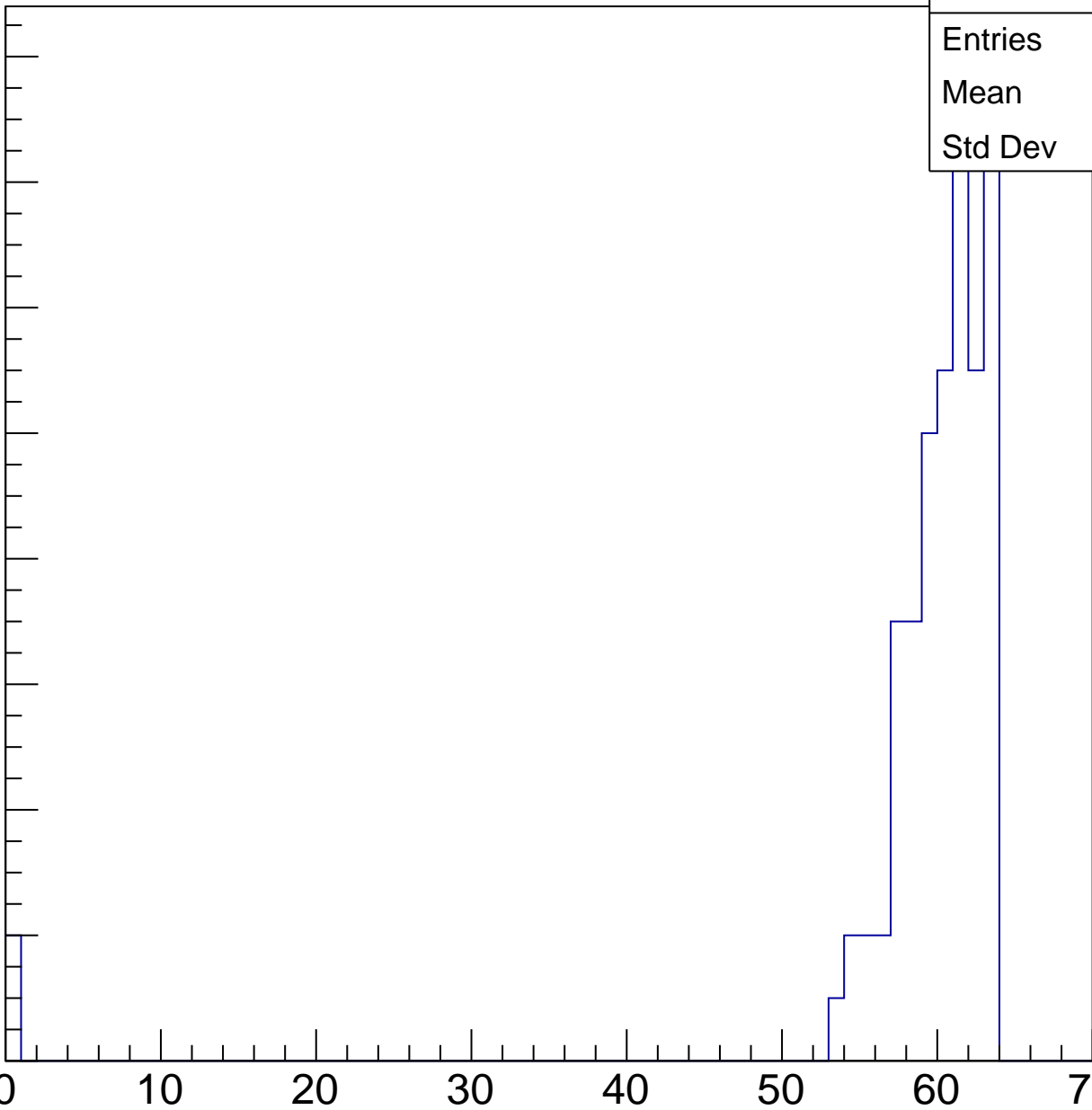
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.64
Std Dev	9.368

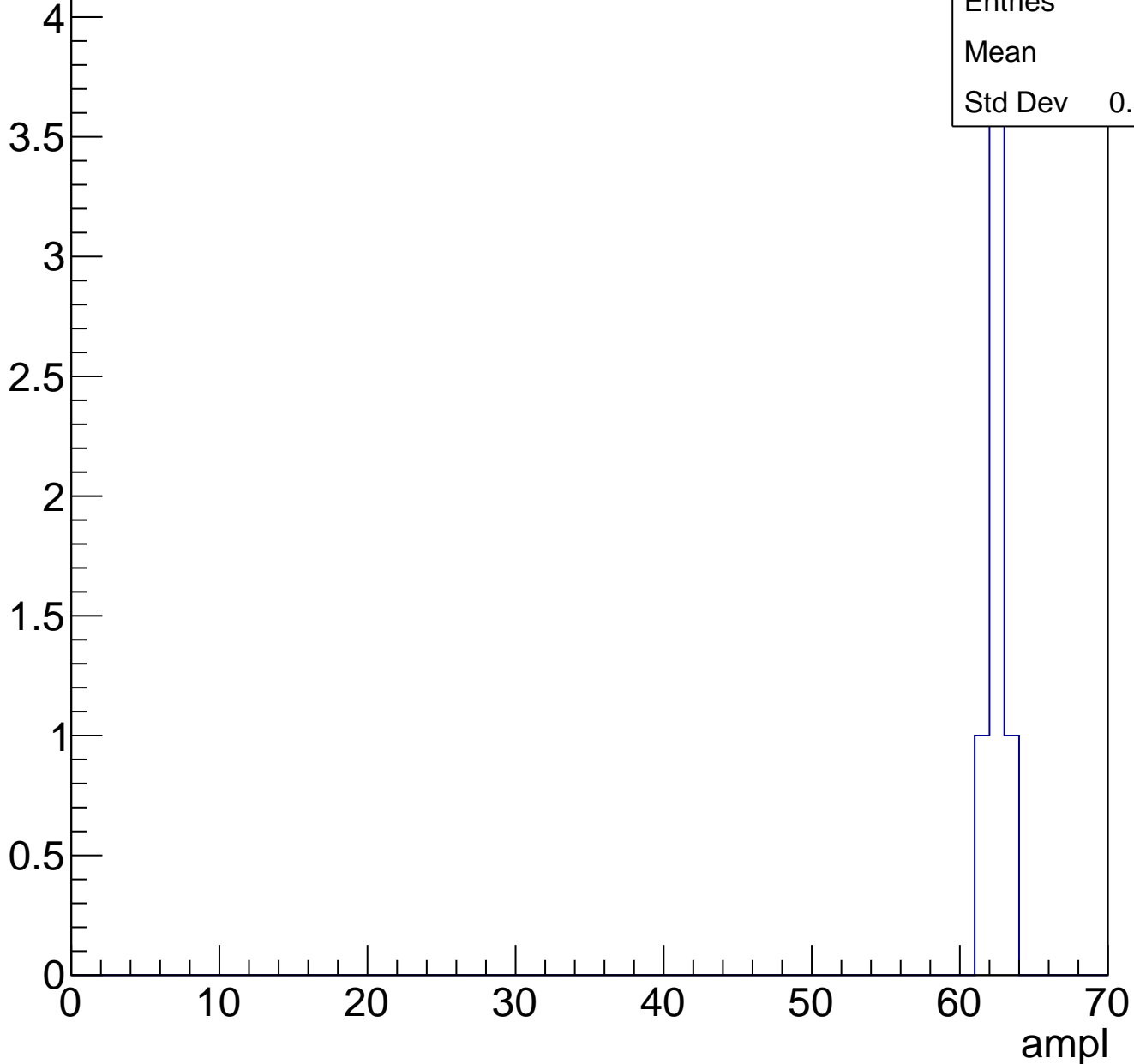
ampl



B0L101S, U18-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch16, adc0

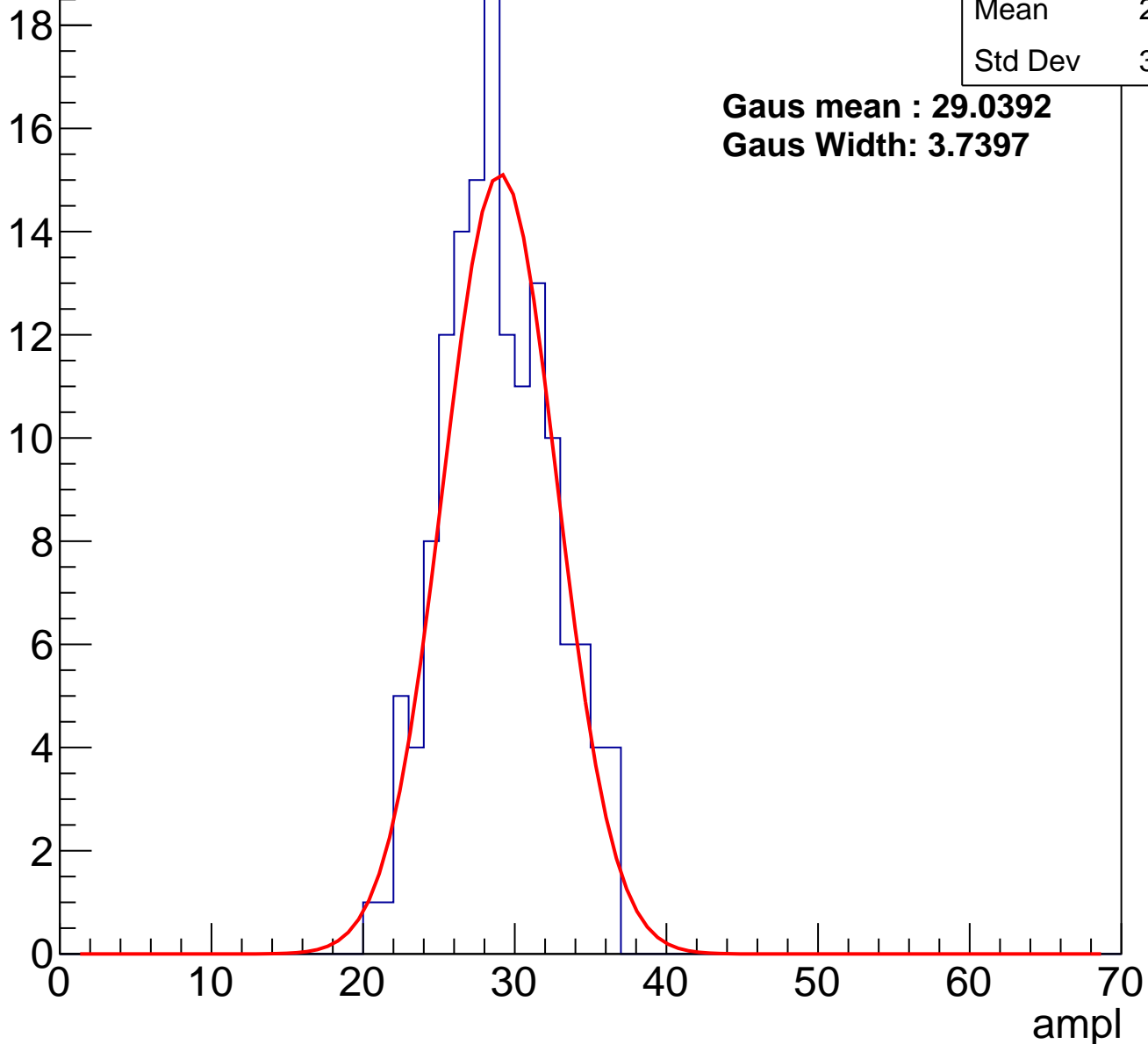
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.43
Std Dev	3.546

Gaus mean : 29.0392

Gaus Width: 3.7397

Entry

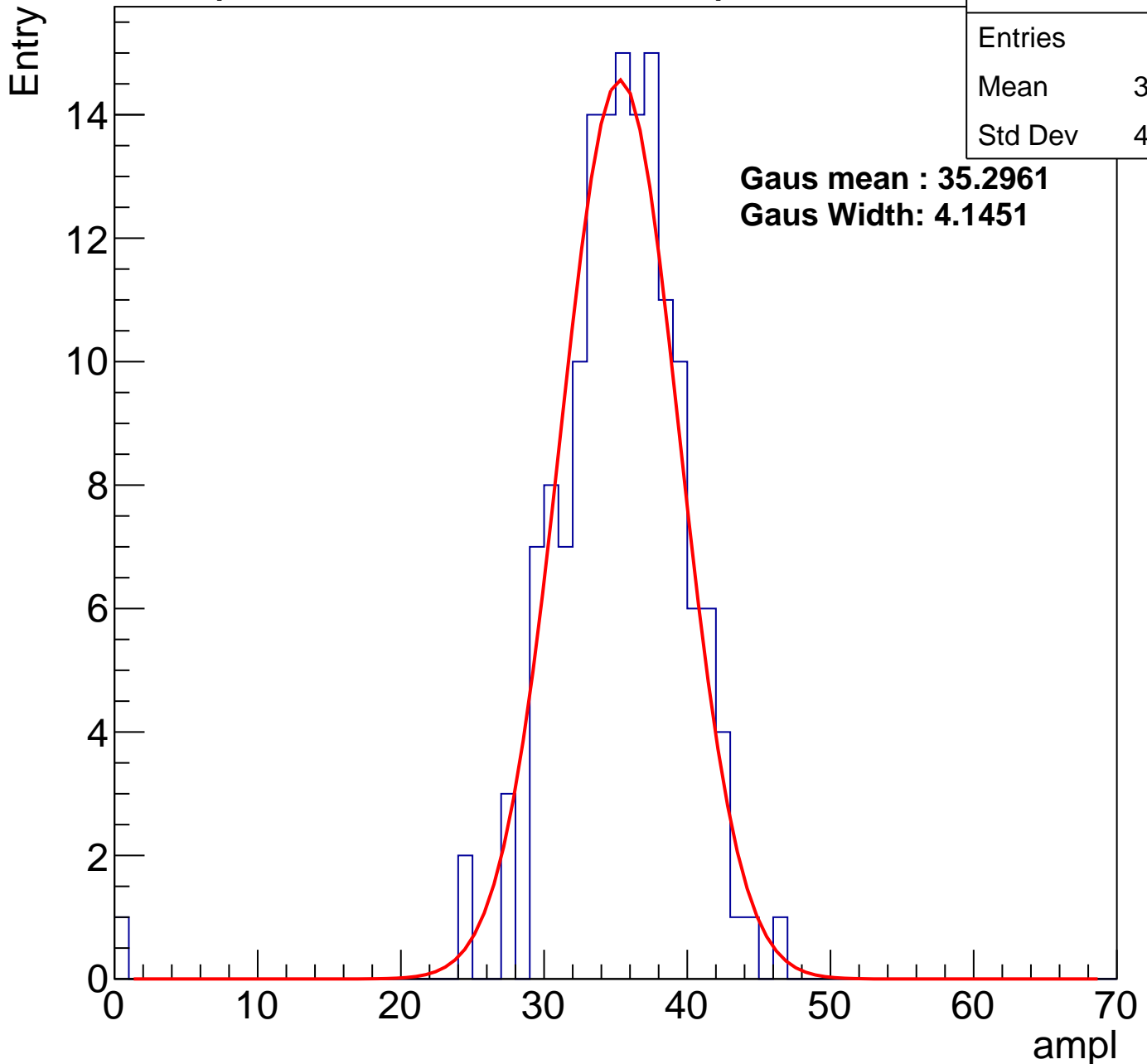


B0L101S, U18-ch16, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	34.84
Std Dev	4.858

Gaus mean : 35.2961
Gaus Width: 4.1451



B0L101S, U18-ch16, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	42.29
Std Dev	3.045

Gaus mean : 42.7981

Gaus Width: 3.2019

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

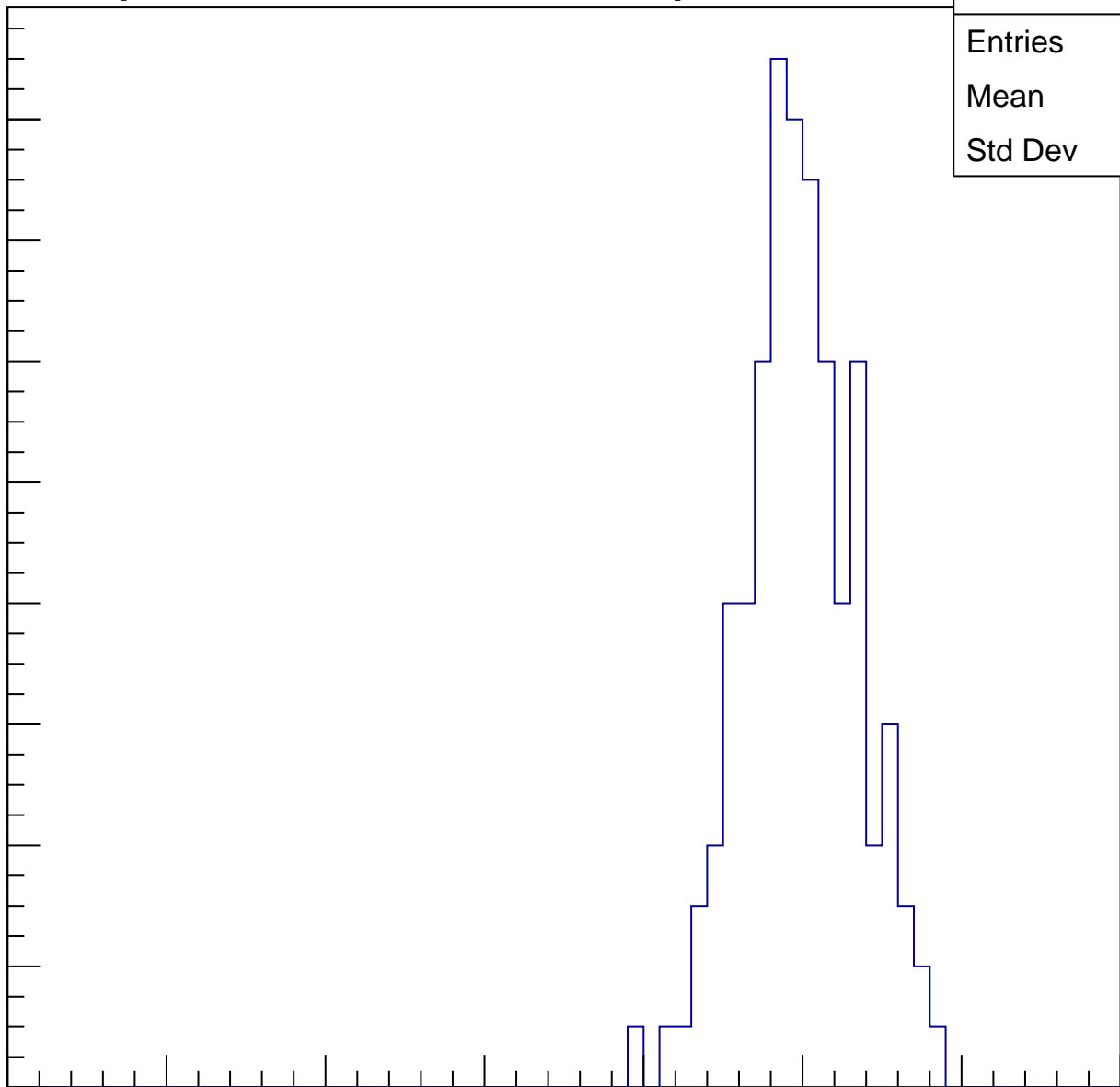
Entries	134
Mean	49.4
Std Dev	3.524

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

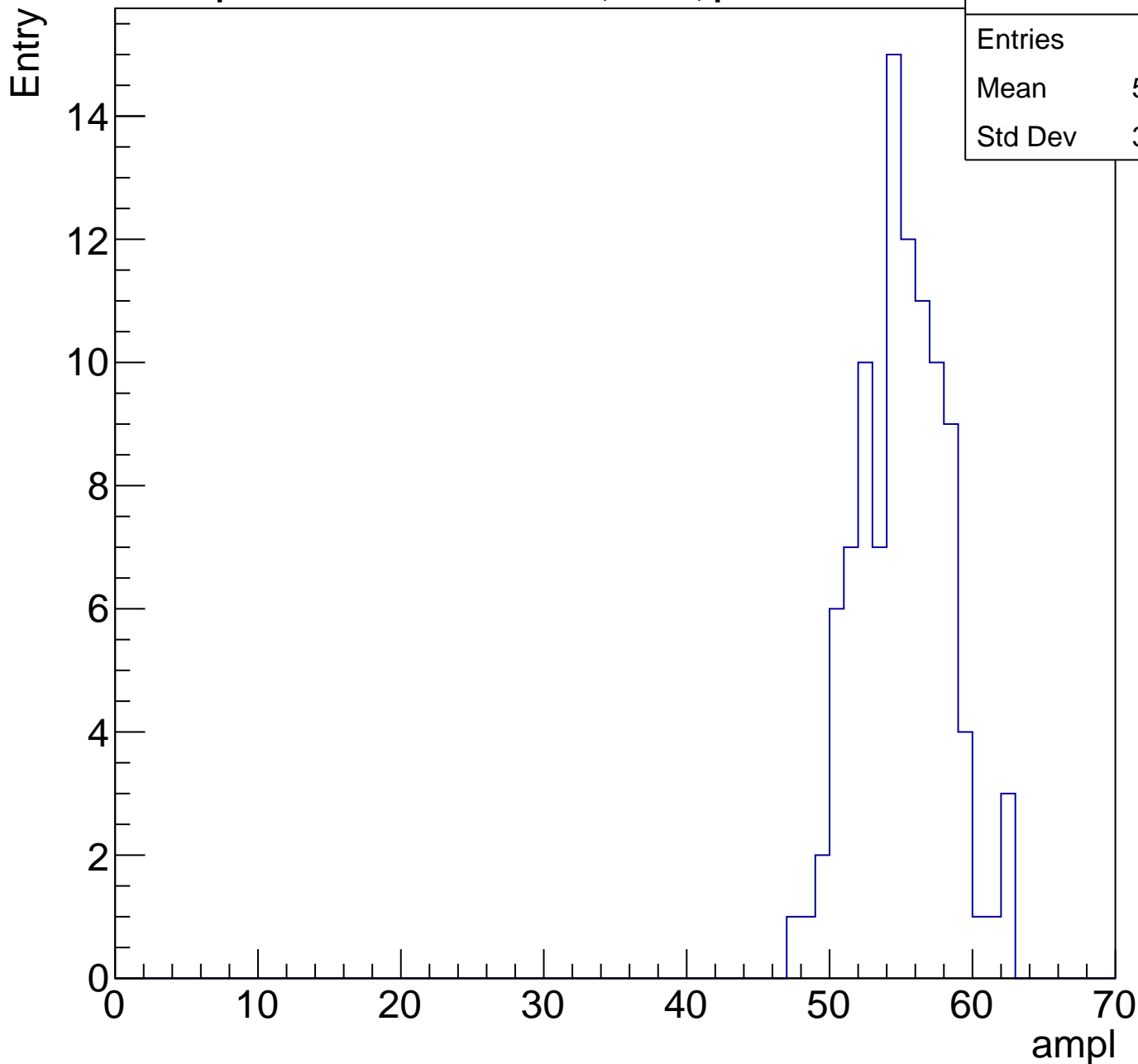
ampl



B0L101S, U18-ch16, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	54.62
Std Dev	3.114



B0L101S, U18-ch16, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

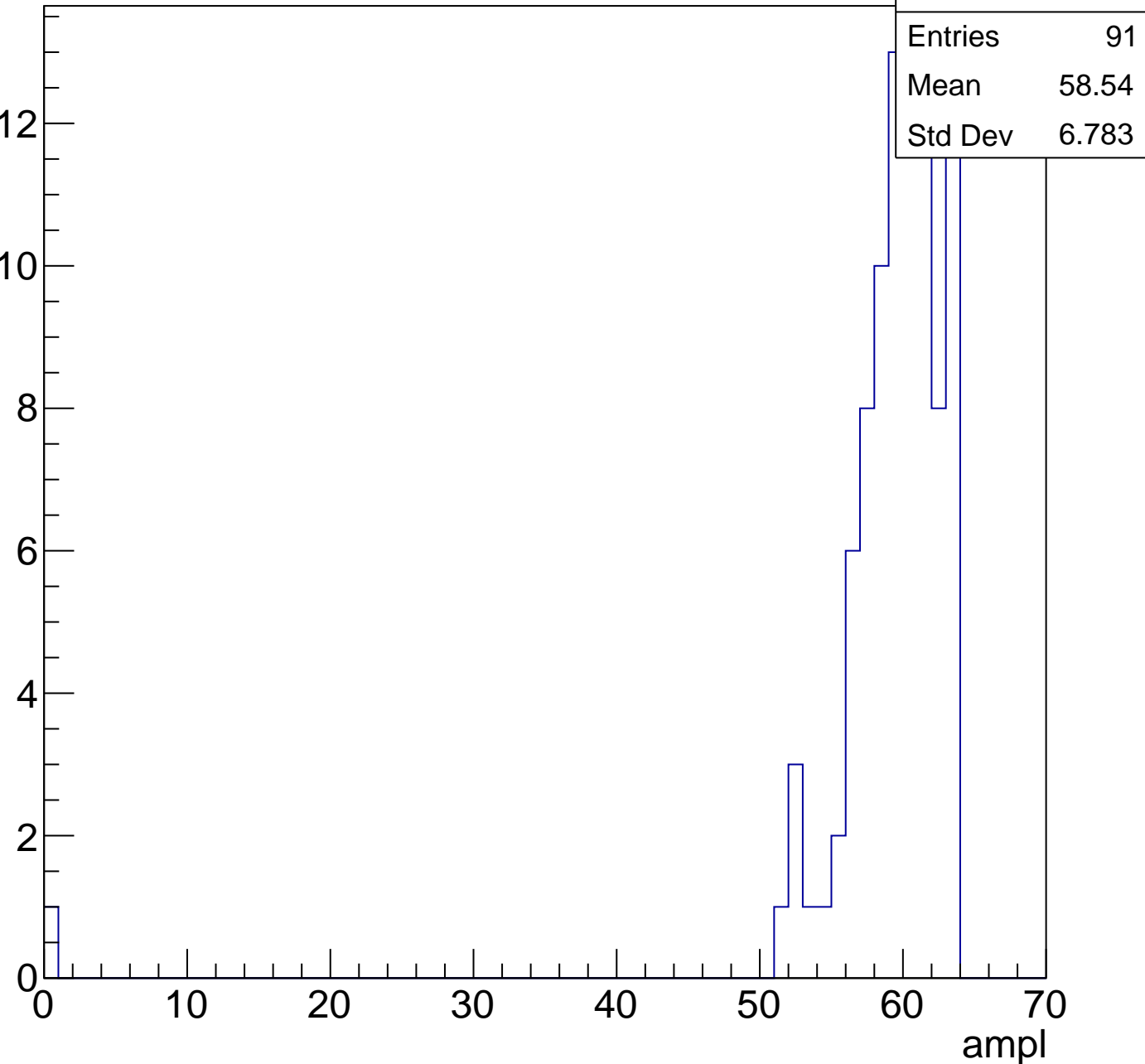
50

60

70

ampl

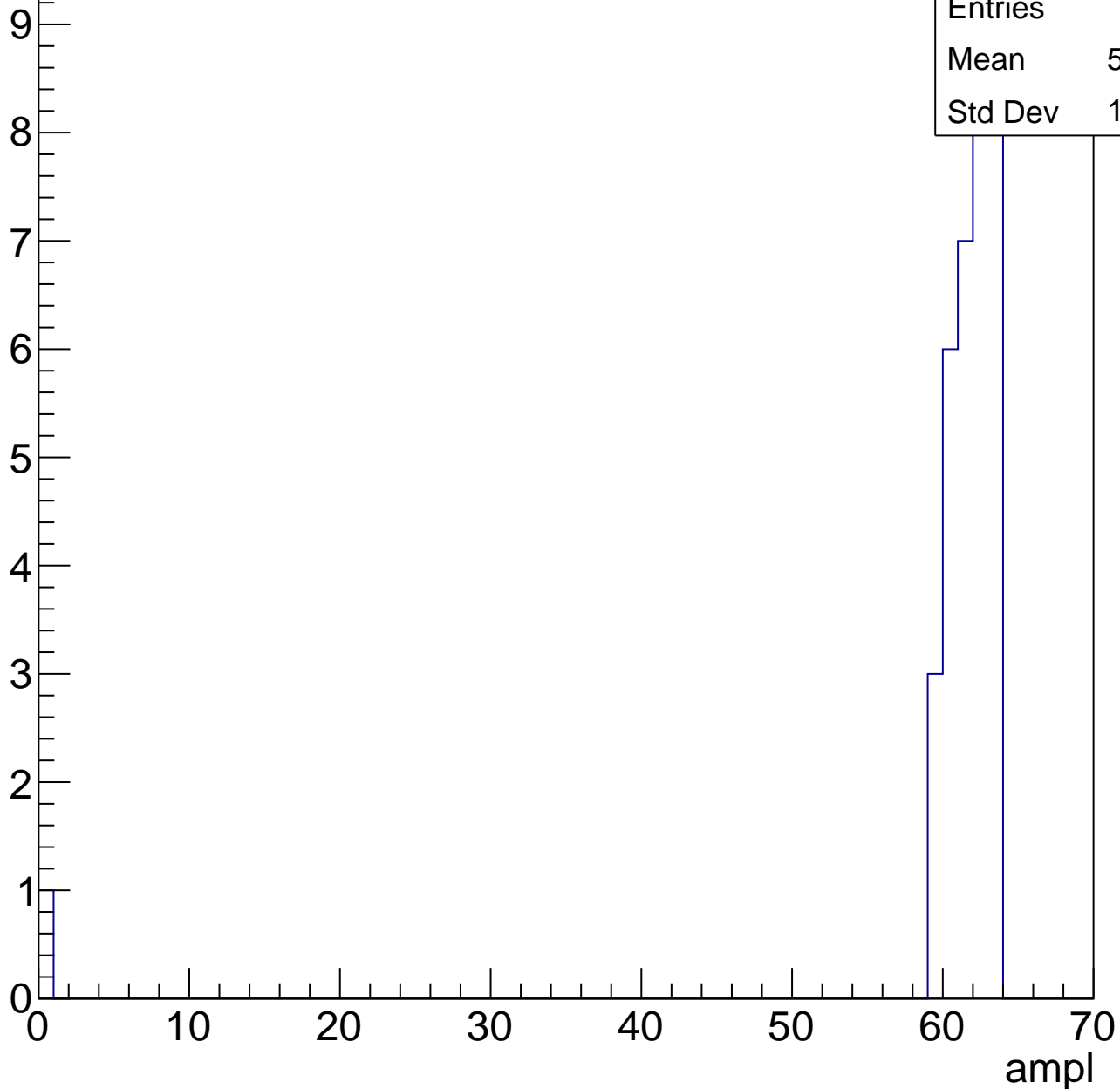
Entries	91
Mean	58.54
Std Dev	6.783



B0L101S, U18-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch17, adc0

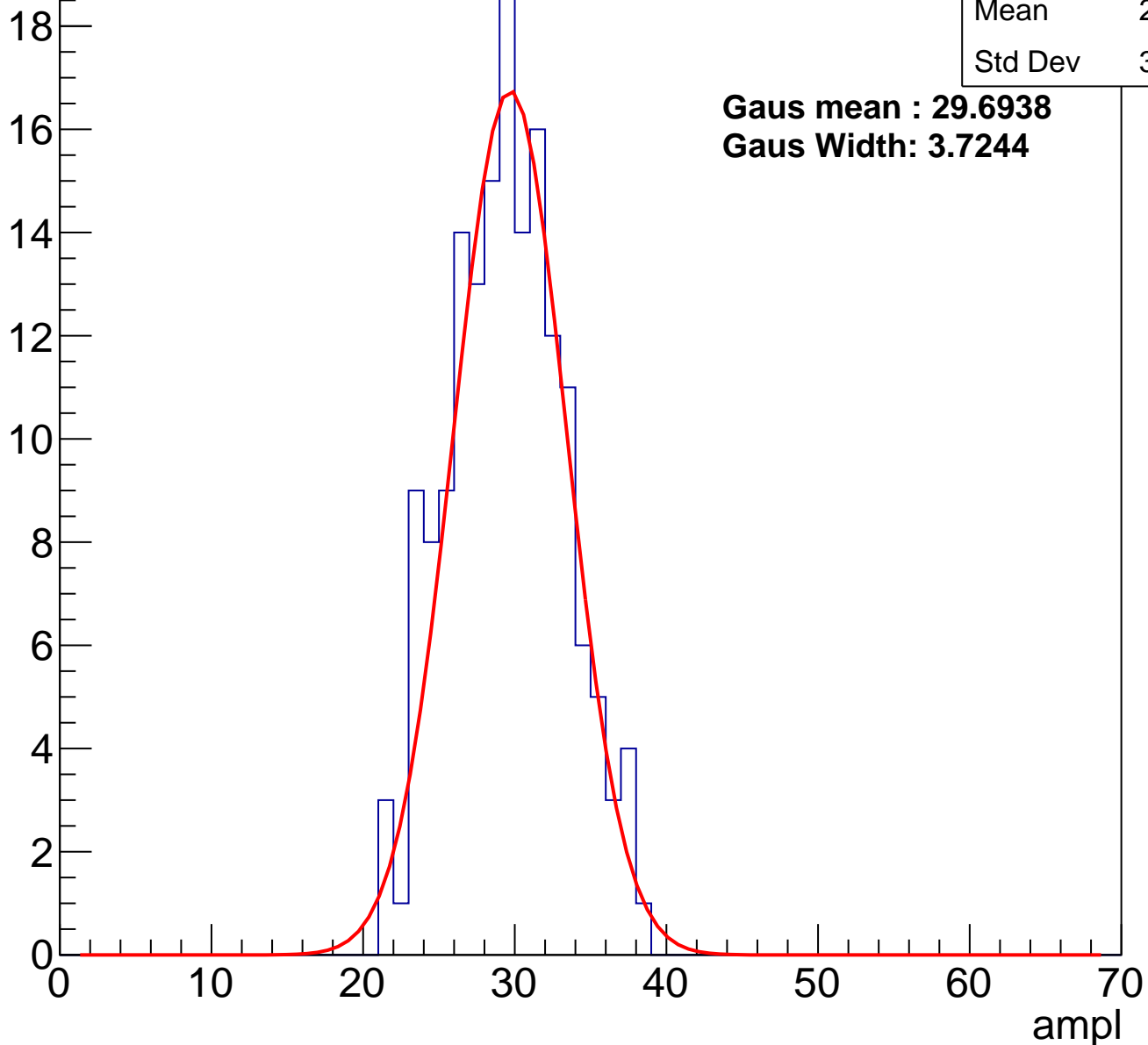
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.02
Std Dev	3.738

Gaus mean : 29.6938

Gaus Width: 3.7244

Entry



B0L101S, U18-ch17, adc1

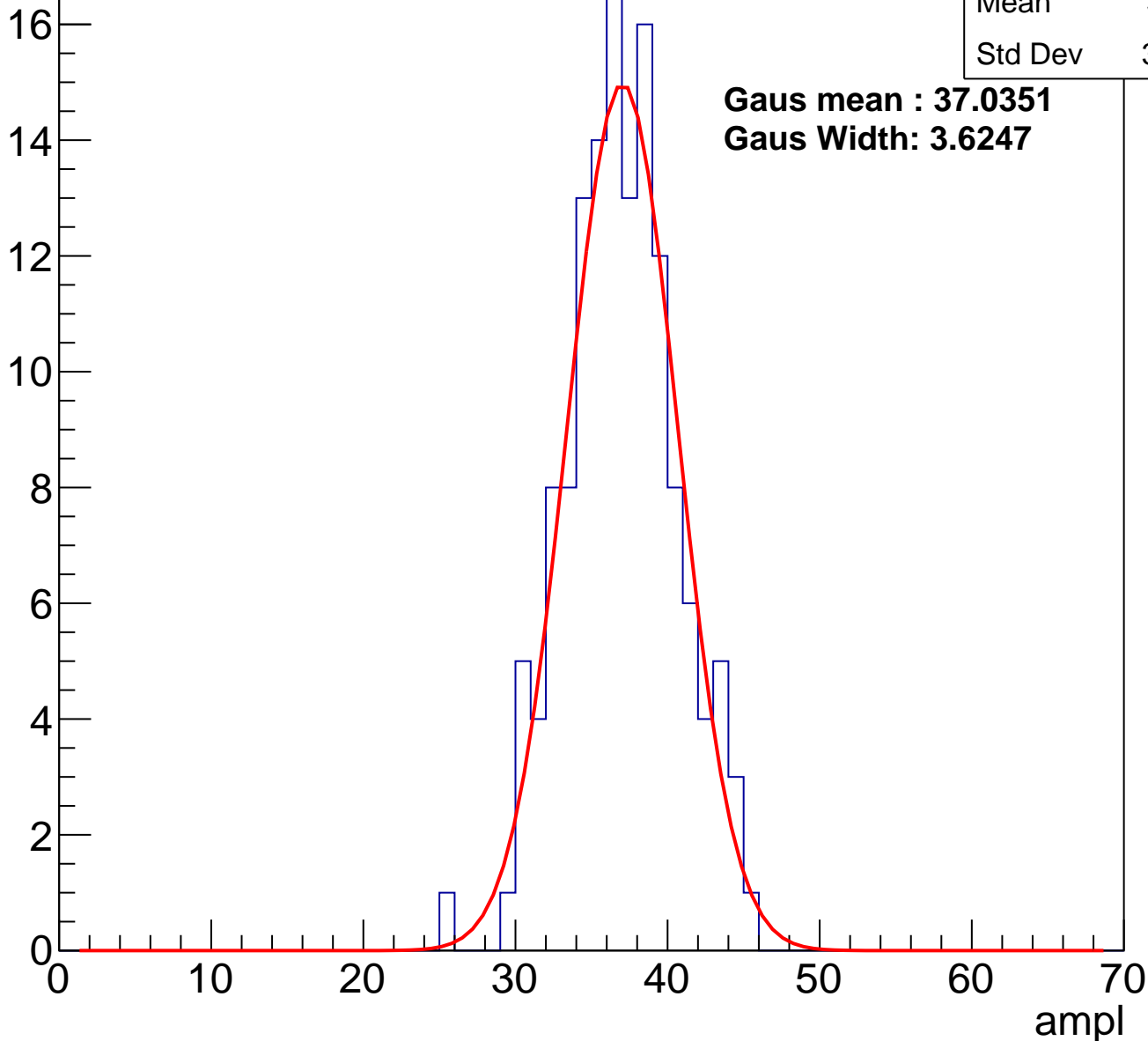
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	139
Mean	36.51
Std Dev	3.603

Gaus mean : 37.0351

Gaus Width: 3.6247



B0L101S, U18-ch17, adc2

calib_packv5_042523_0143.root, FC#1, port C1

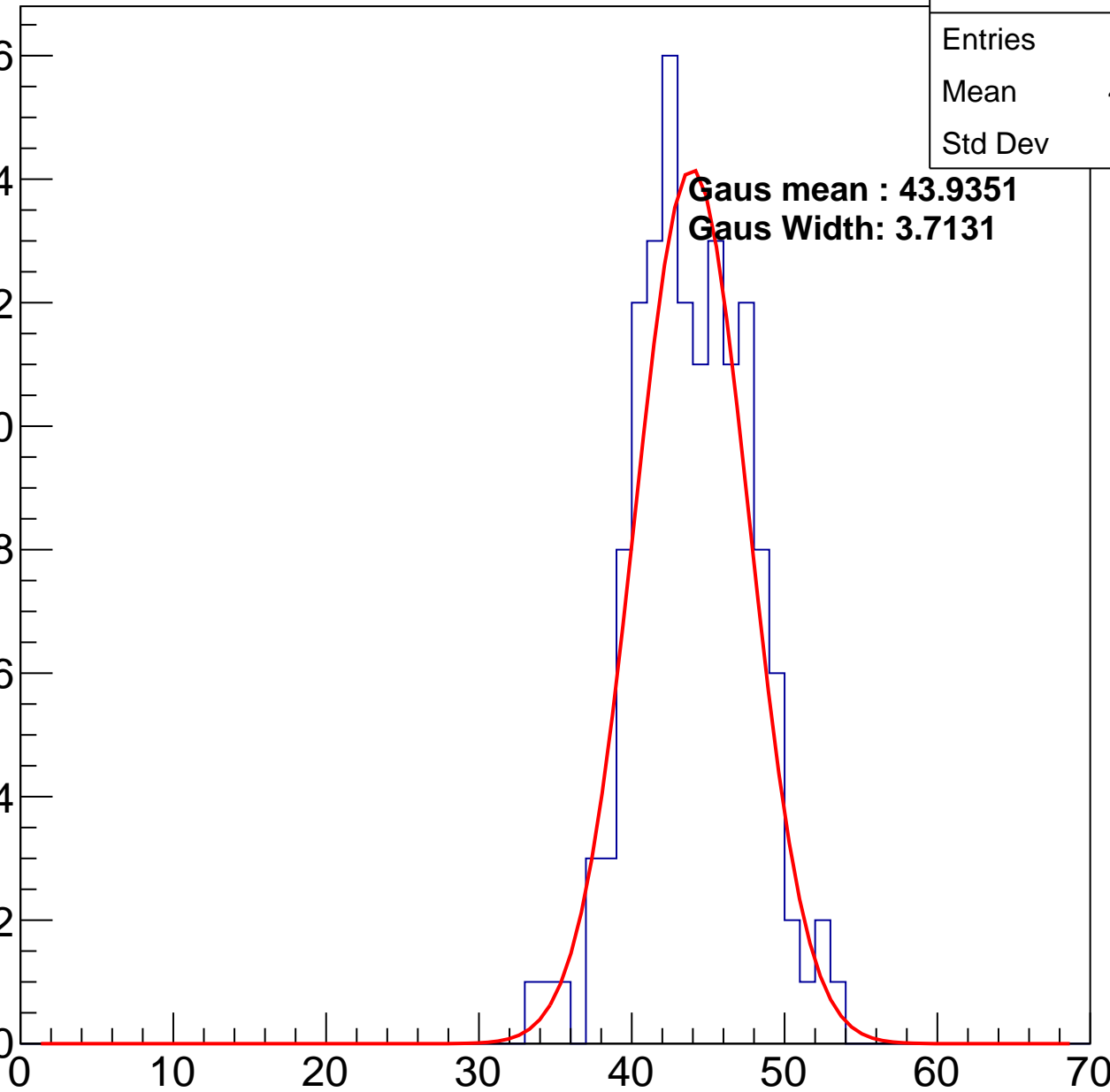
Entry

16
14
12
10
8
6
4
2
0

Entries	137
Mean	43.54
Std Dev	3.71

Gaus mean : 43.9351
Gaus Width: 3.7131

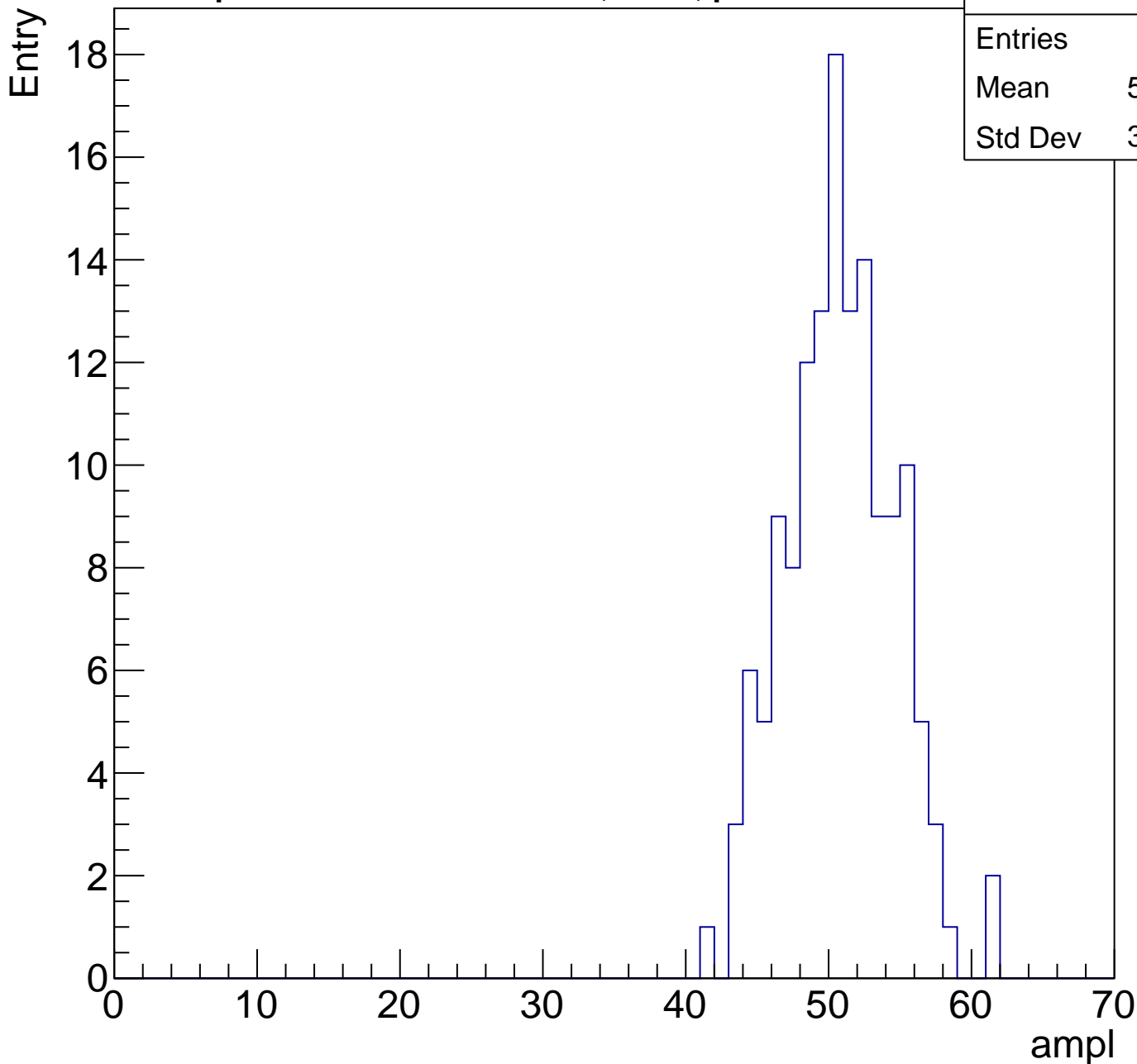
ampl



B0L101S, U18-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	50.33
Std Dev	3.773

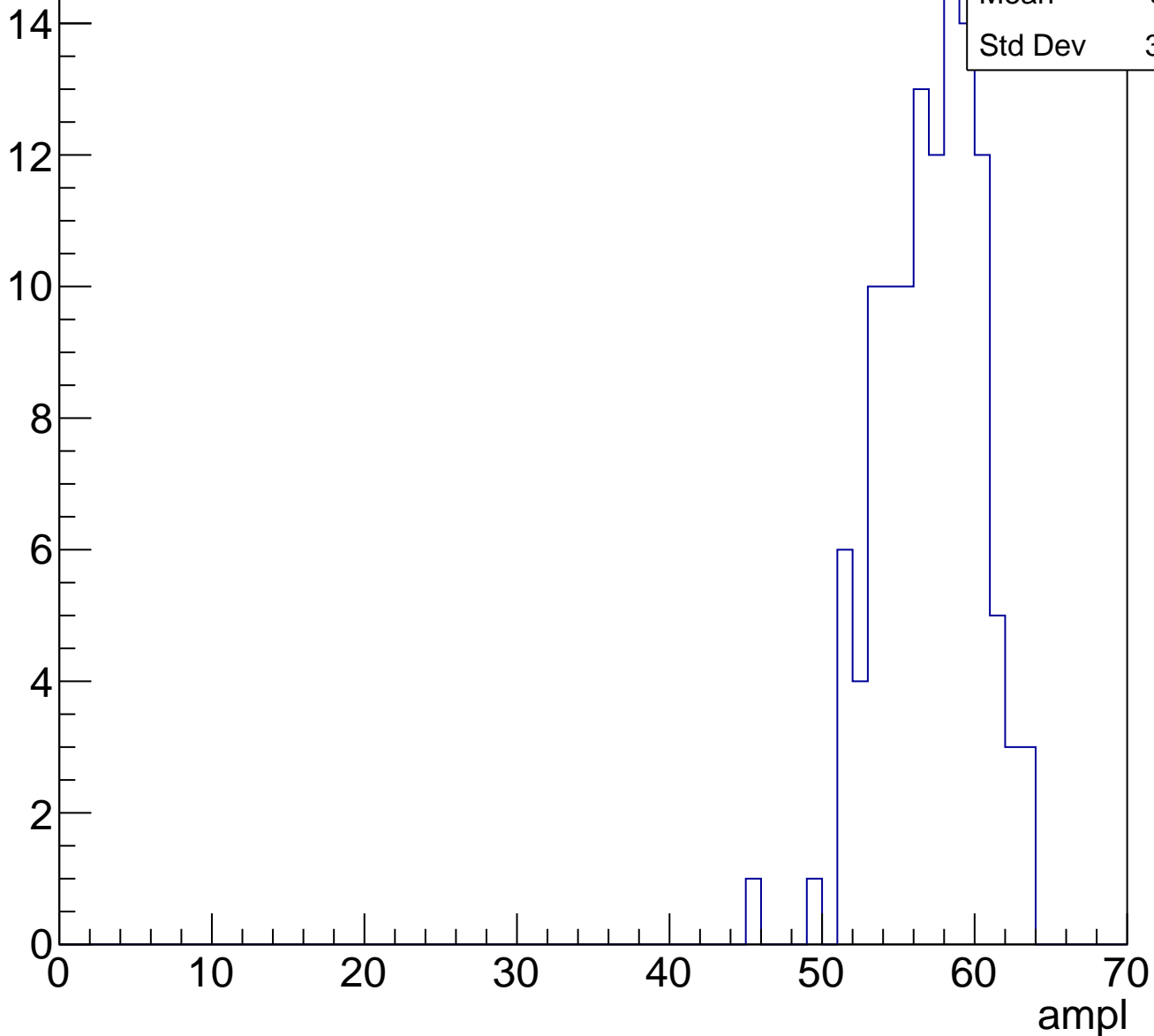


B0L101S, U18-ch17, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	56.61
Std Dev	3.242

Entry



B0L101S, U18-ch17, adc5

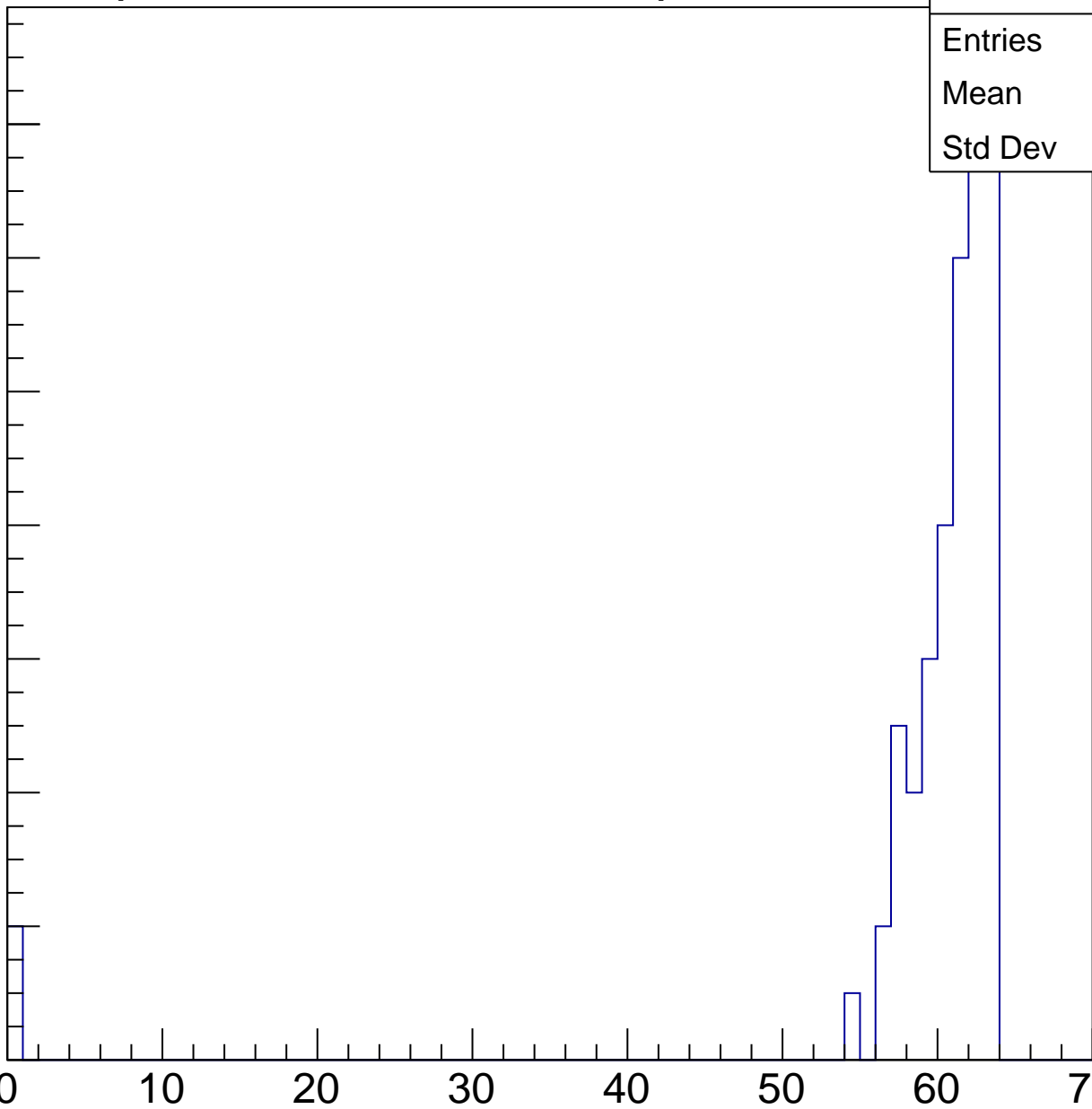
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	58.91
Std Dev	10.32

ampl



B0L101S, U18-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L101S, U18-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L101S, U18-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	28.25
Std Dev	4.933

Gaus mean : 29.2115

Gaus Width: 3.8423

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

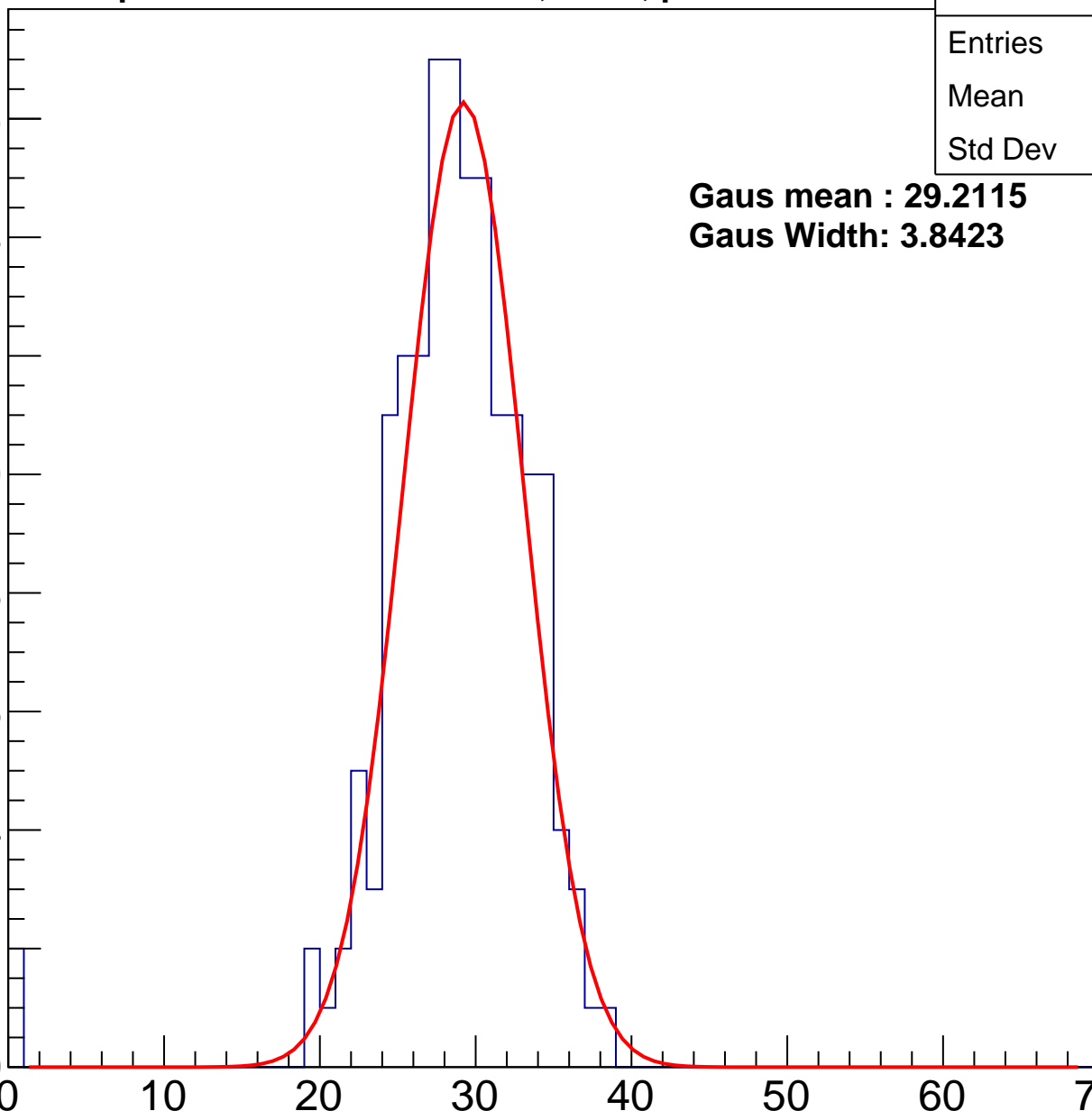
40

50

60

70

ampl

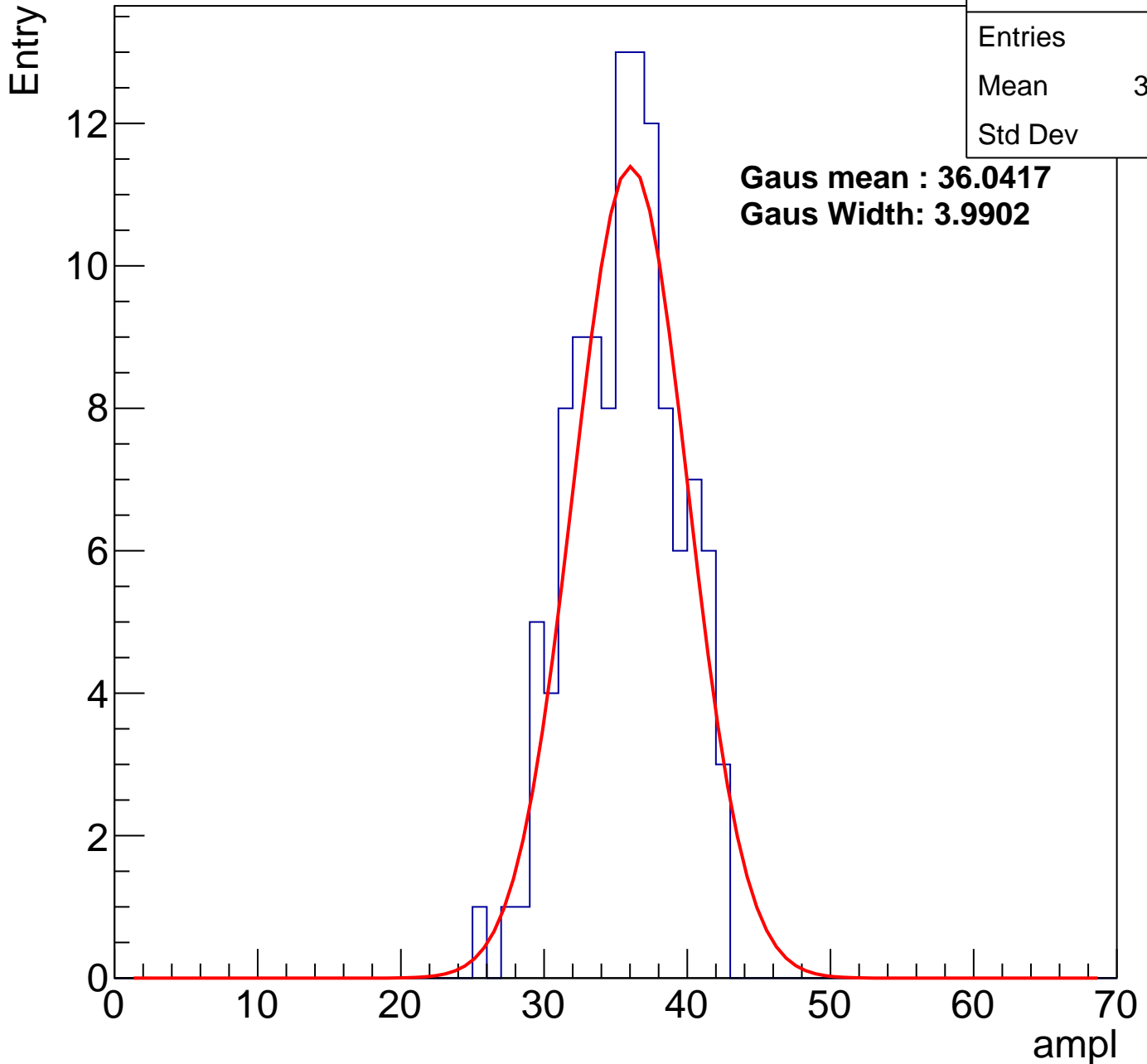


B0L101S, U18-ch18, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	35.15
Std Dev	3.64

Gaus mean : 36.0417
Gaus Width: 3.9902



B0L101S, U18-ch18, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	41.97
Std Dev	3.881

Gaus mean : 42.3172

Gaus Width: 3.9749

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

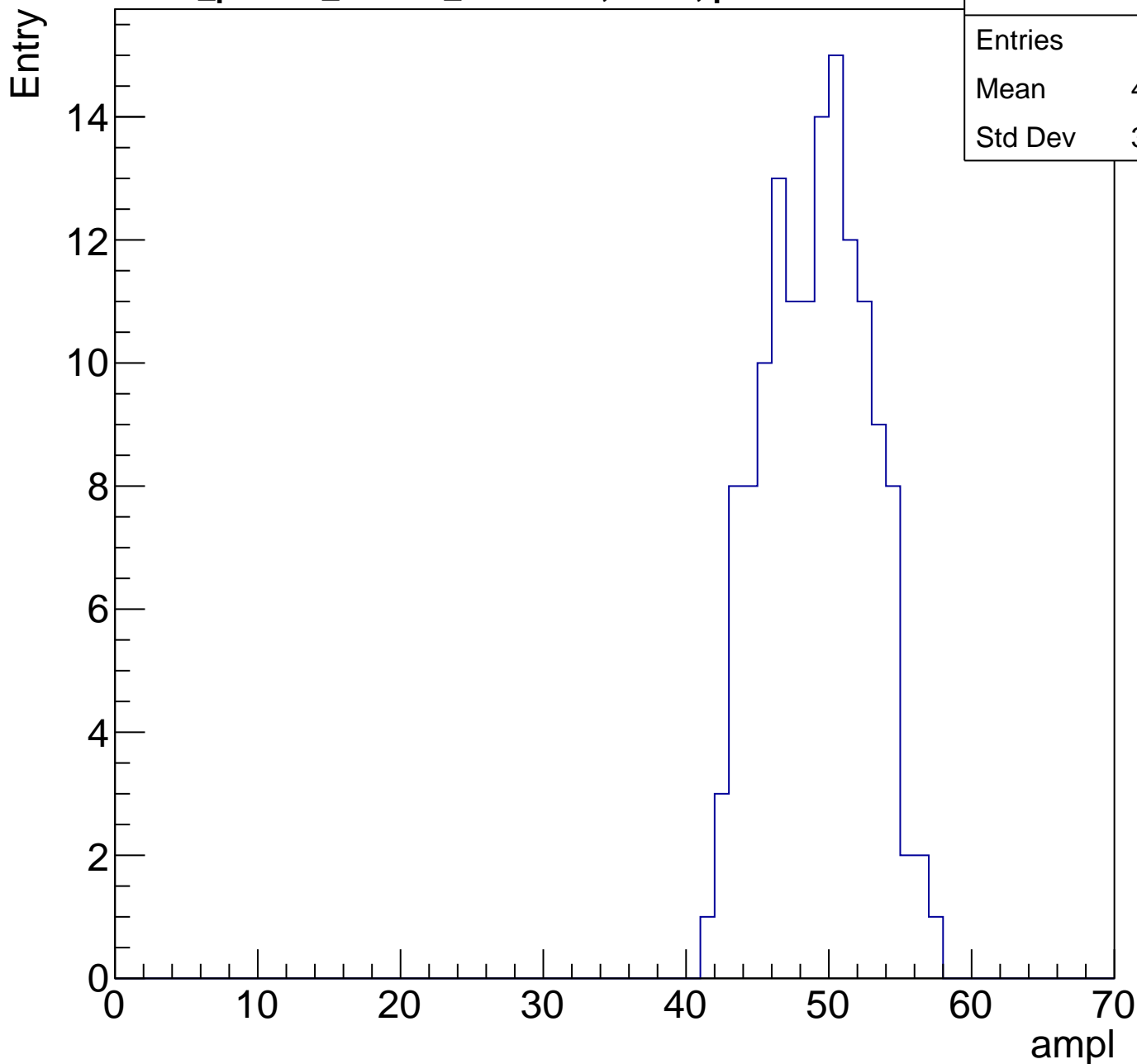
60

70

B0L101S, U18-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

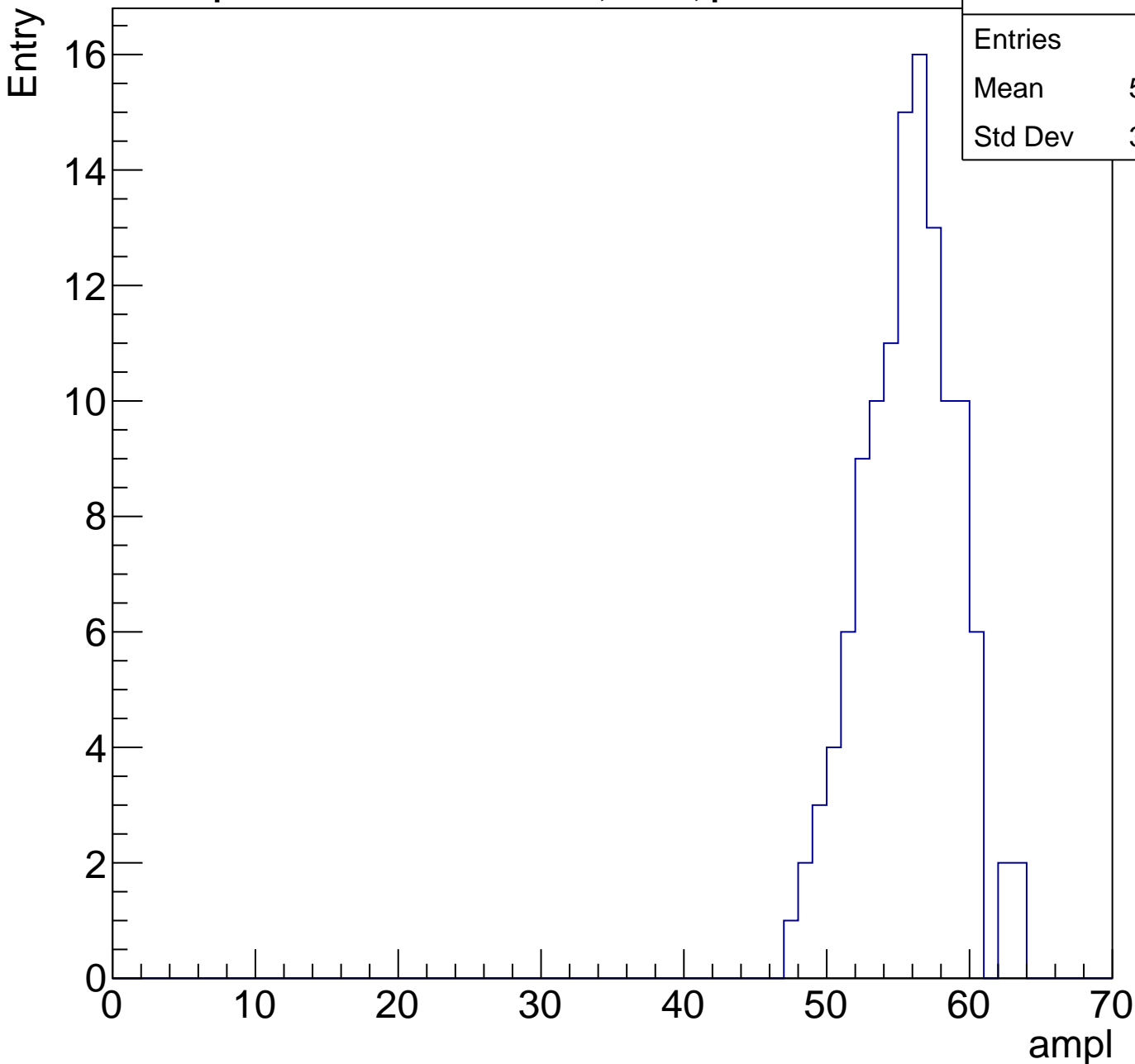
Entries	139
Mean	48.66
Std Dev	3.553



B0L101S, U18-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.25
Std Dev	3.272



B0L101S, U18-ch18, adc5

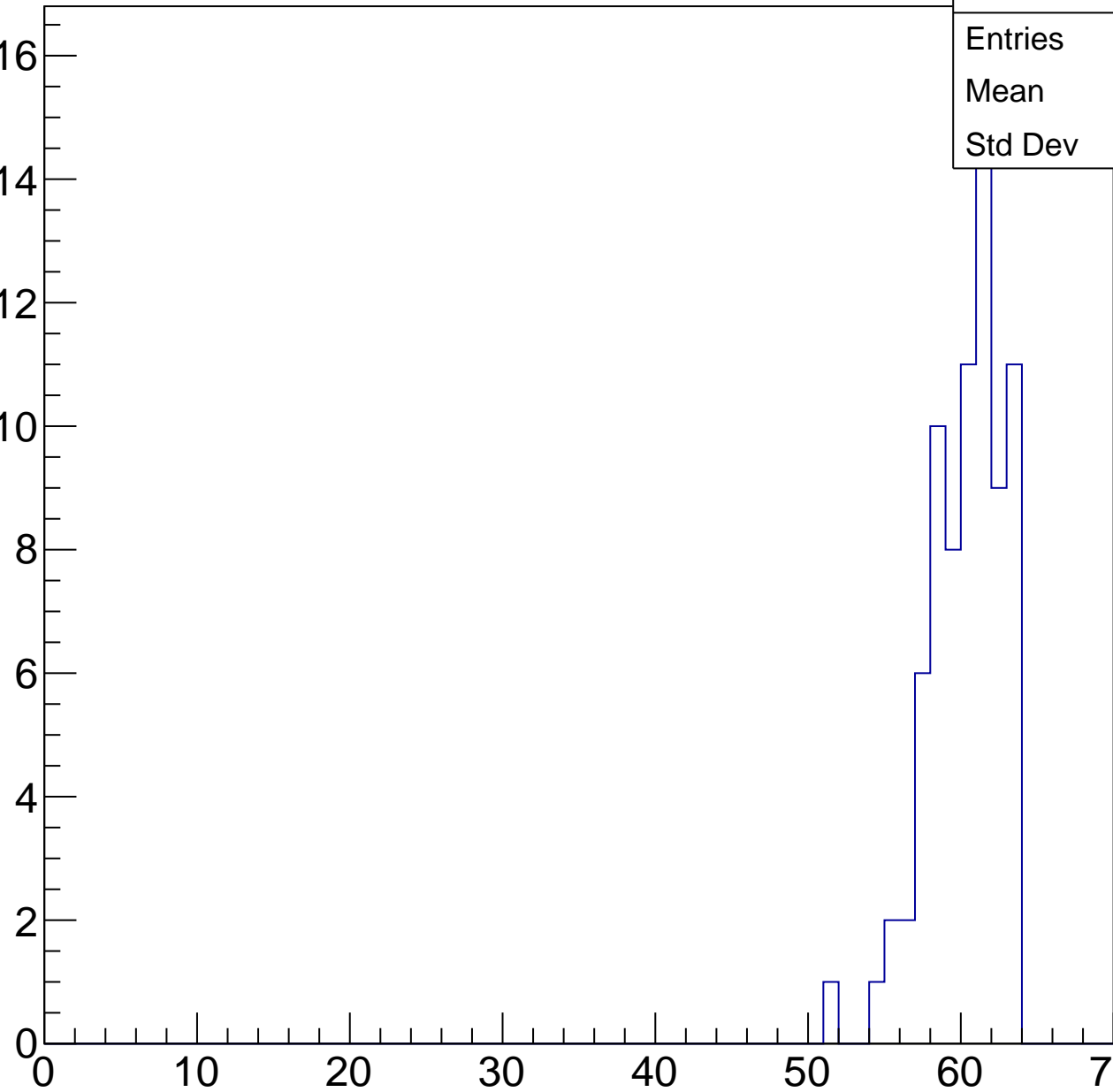
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.84
Std Dev	2.418

ampl

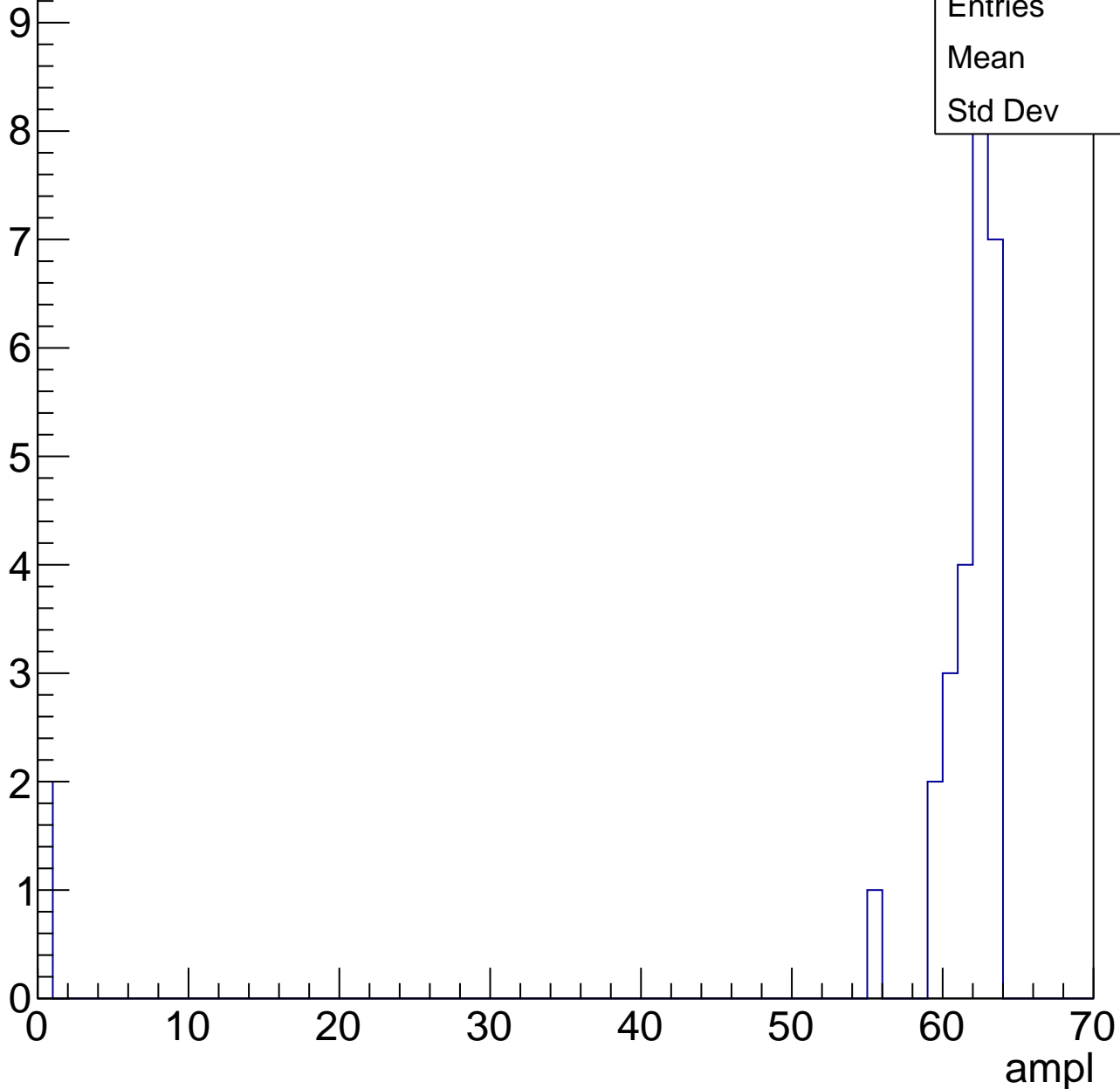


B0L101S, U18-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	28
Mean	57
Std Dev	15.9



B0L101S, U18-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch19, adc0

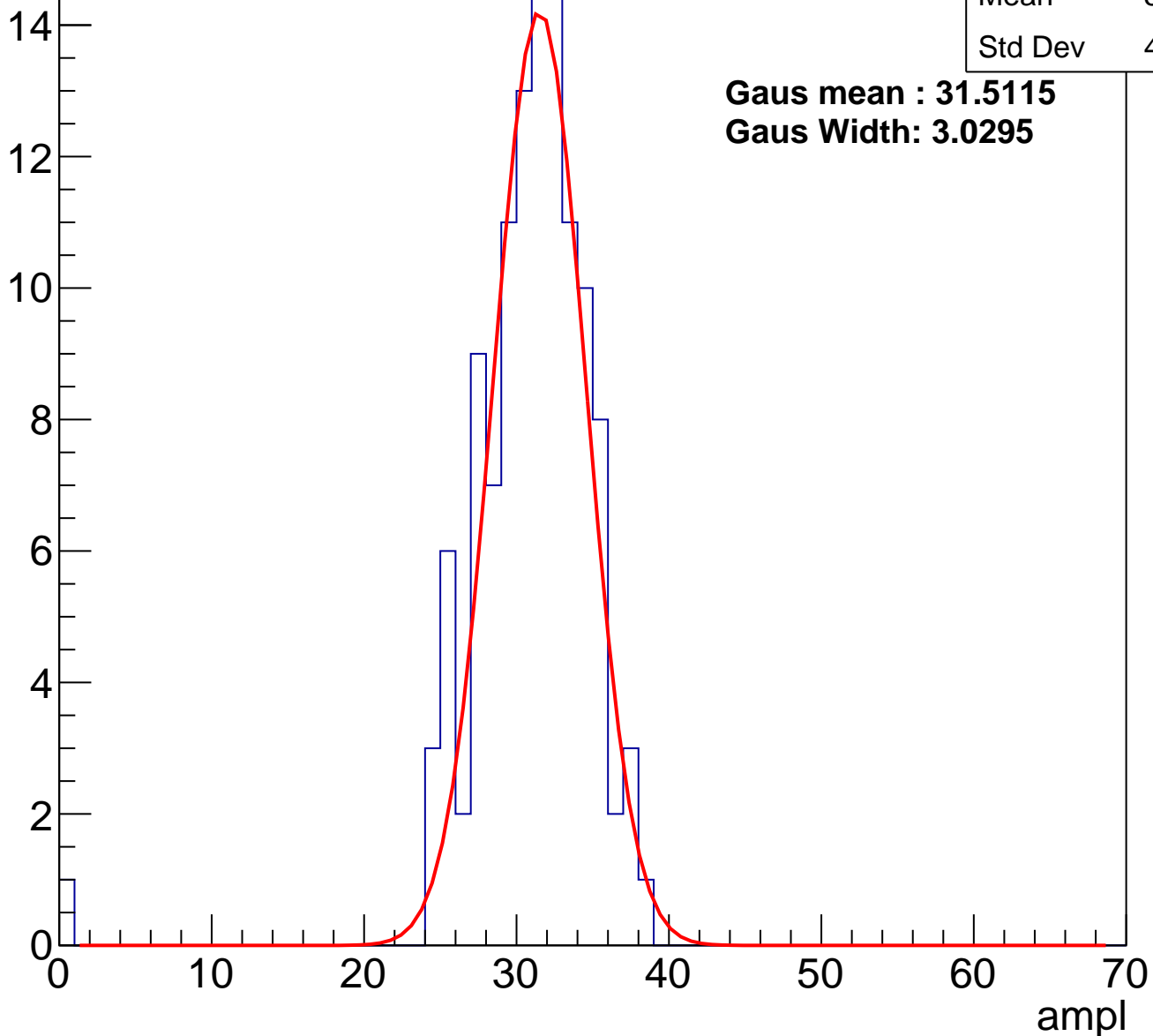
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	30.52
Std Dev	4.232

Gaus mean : 31.5115

Gaus Width: 3.0295

Entry



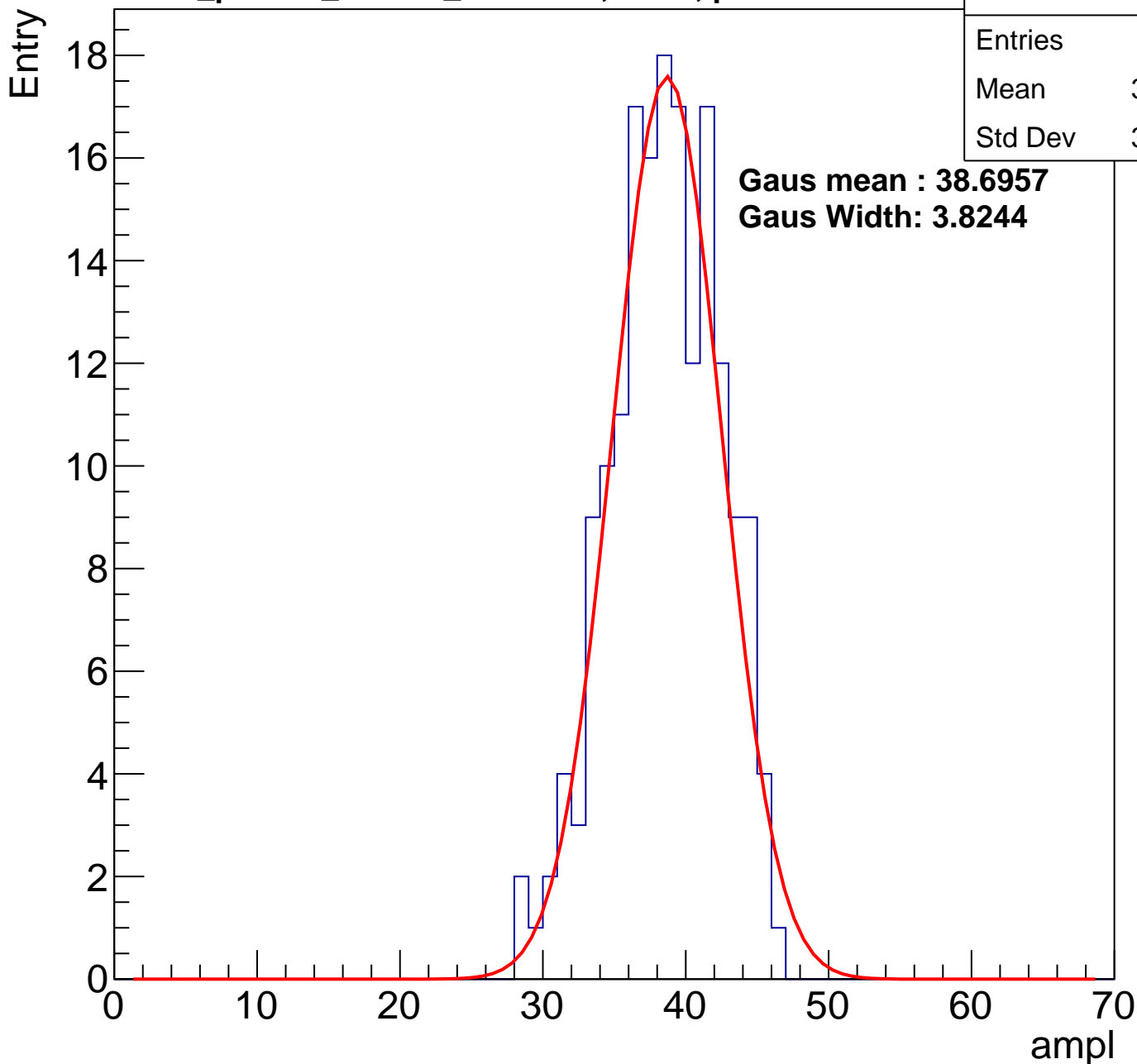
B0L101S, U18-ch19, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	38.09
Std Dev	3.789

Gaus mean : 38.6957

Gaus Width: 3.8244



B0L101S, U18-ch19, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	45.58
Std Dev	3.733

Gaus mean : 46.0774

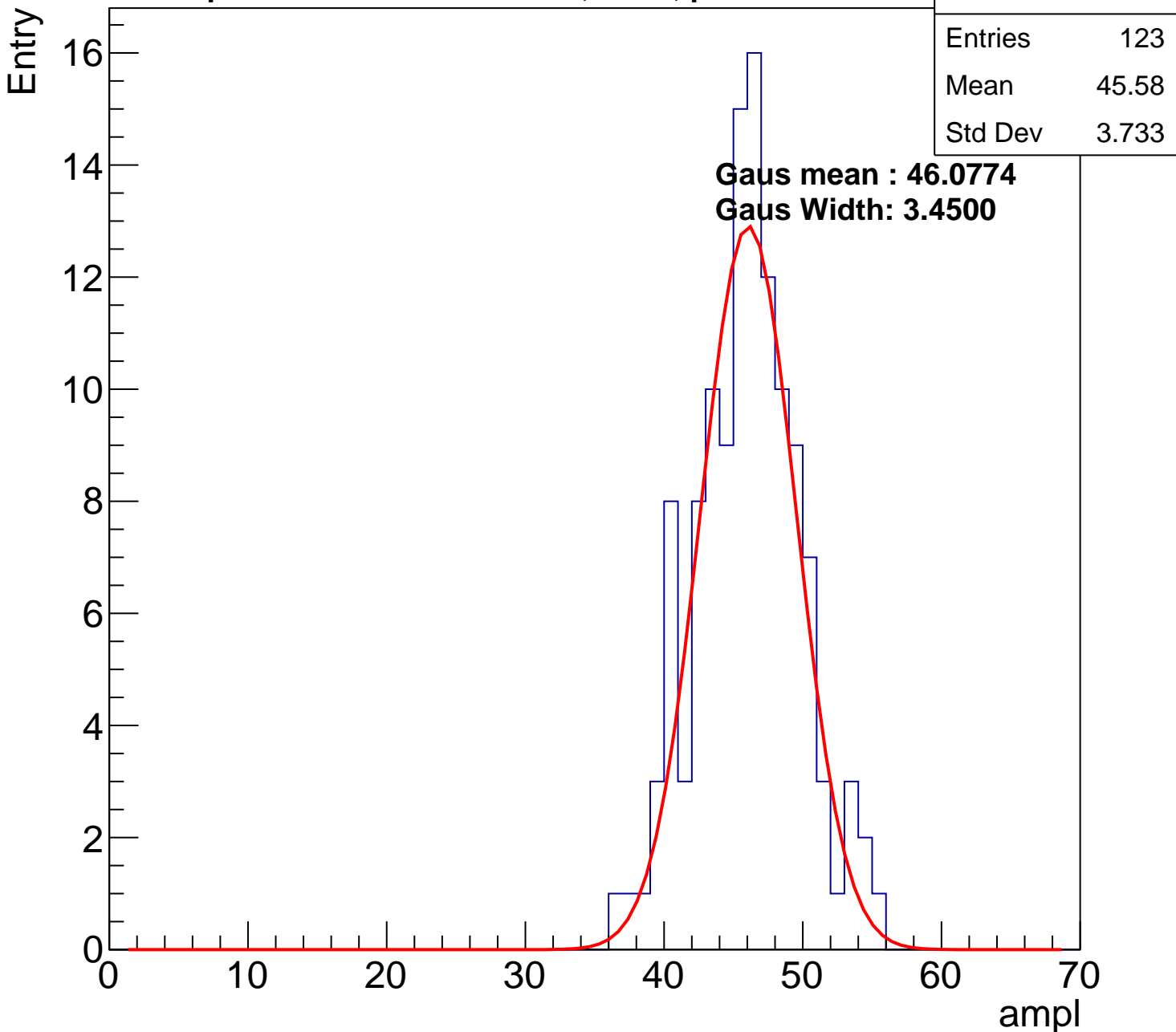
Gaus Width: 3.4500

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	51.97
Std Dev	3.558

Entry

14
12
10
8
6
4
2
0

0

10

20

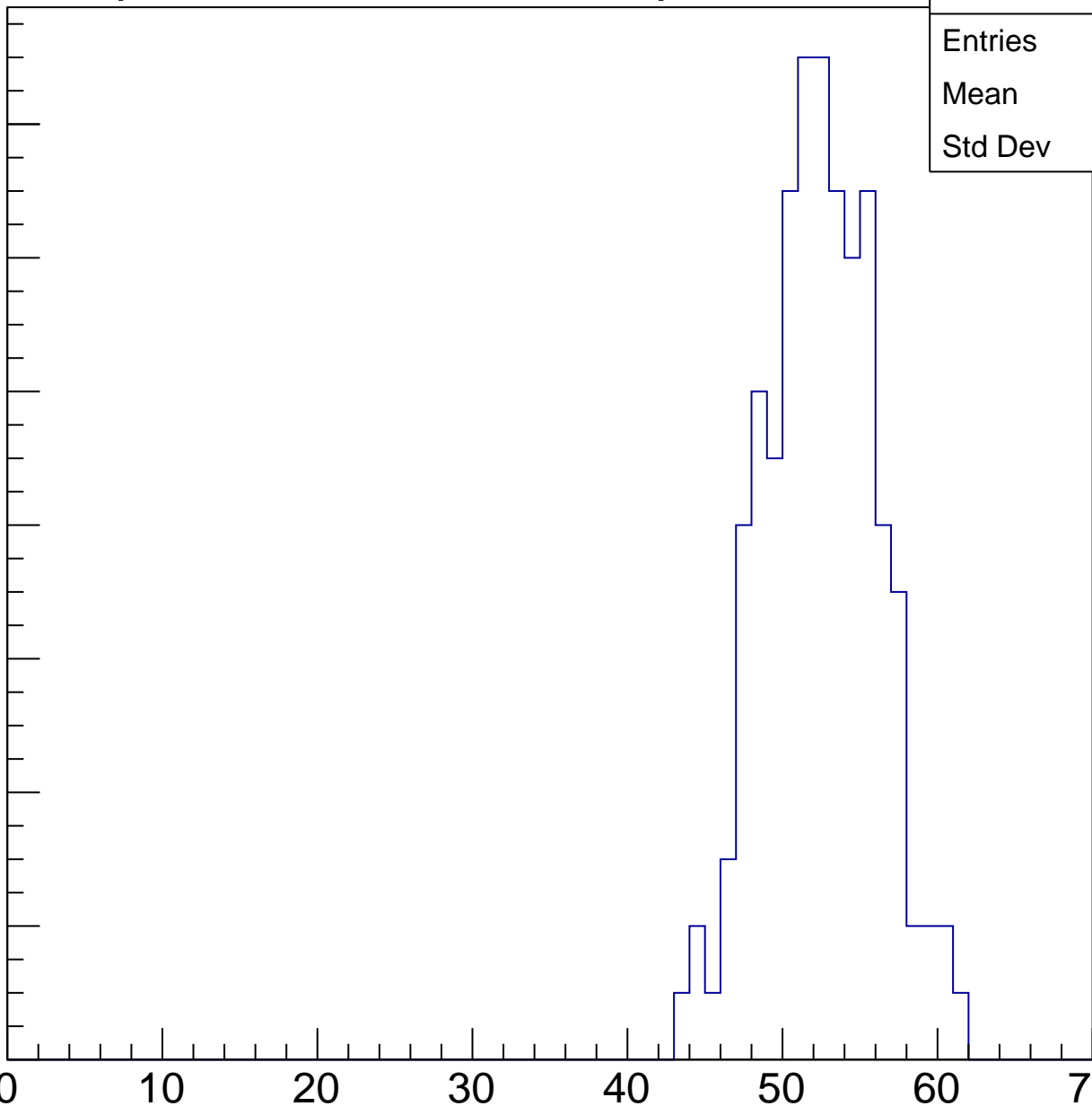
30

40

50

60

ampl



B0L101S, U18-ch19, adc4

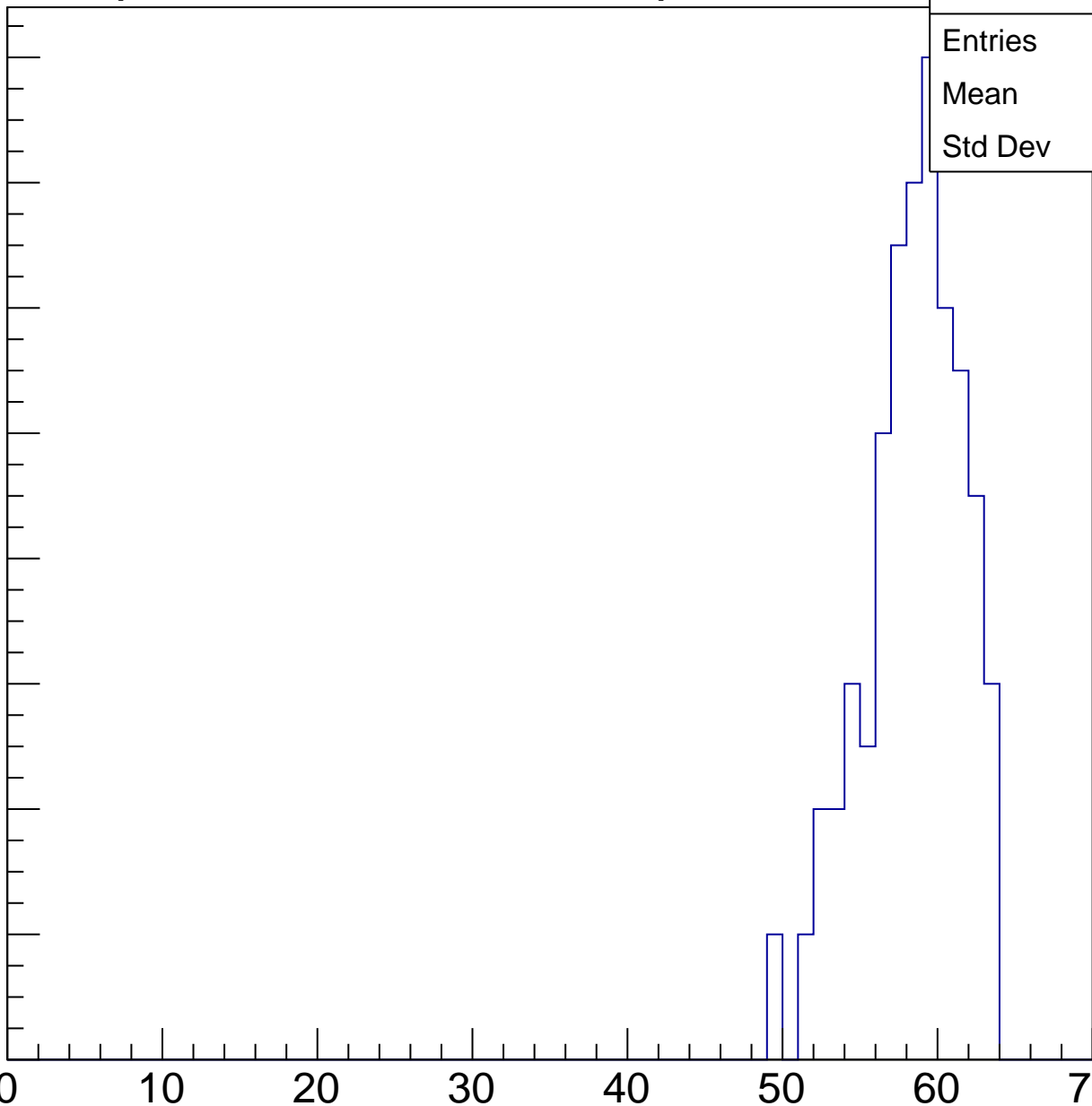
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	57.92
Std Dev	3.182

ampl



B0L101S, U18-ch19, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

48

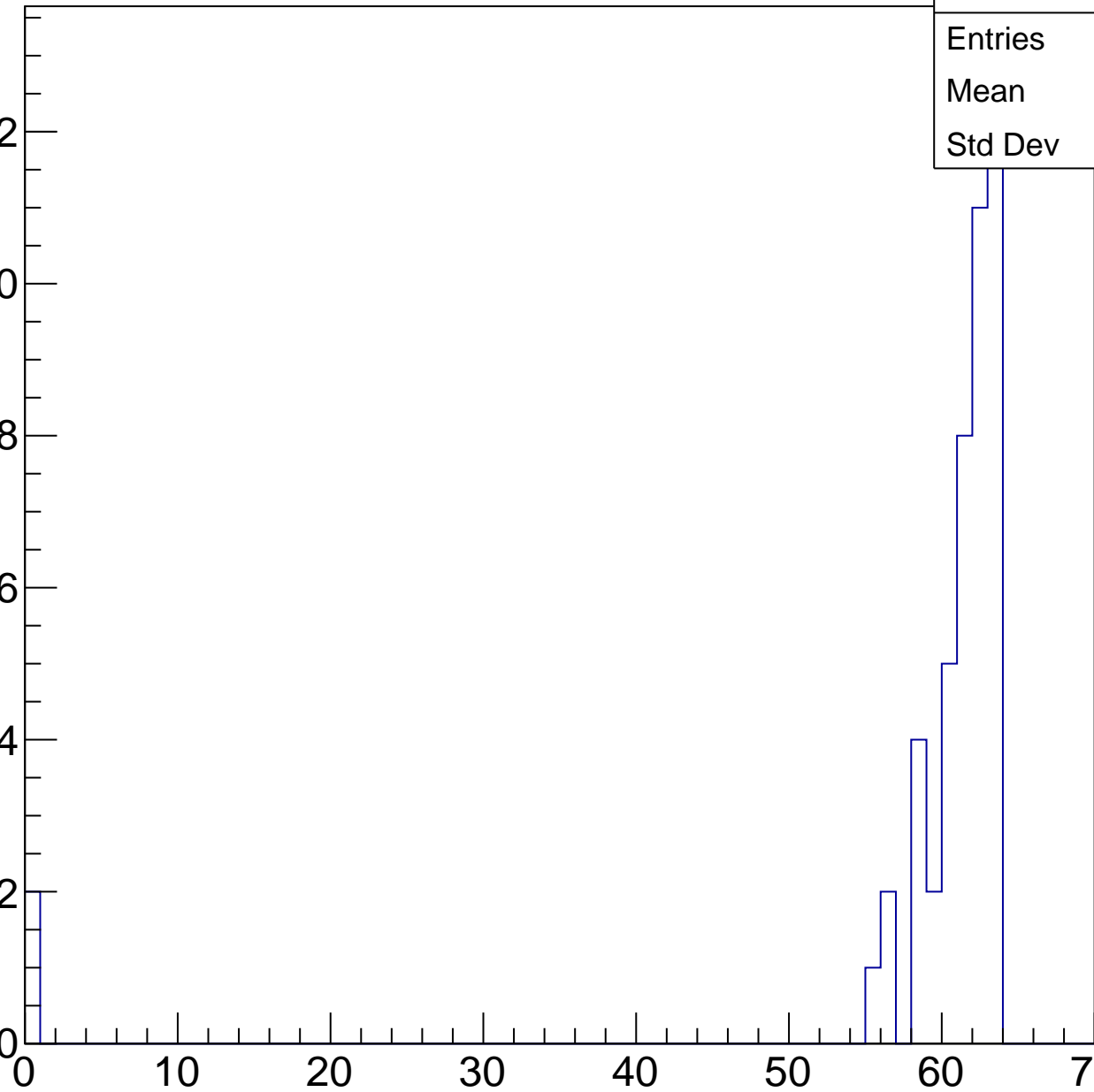
Mean

58.46

Std Dev

12.36

ampl



B0L101S, U18-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch20, adc0

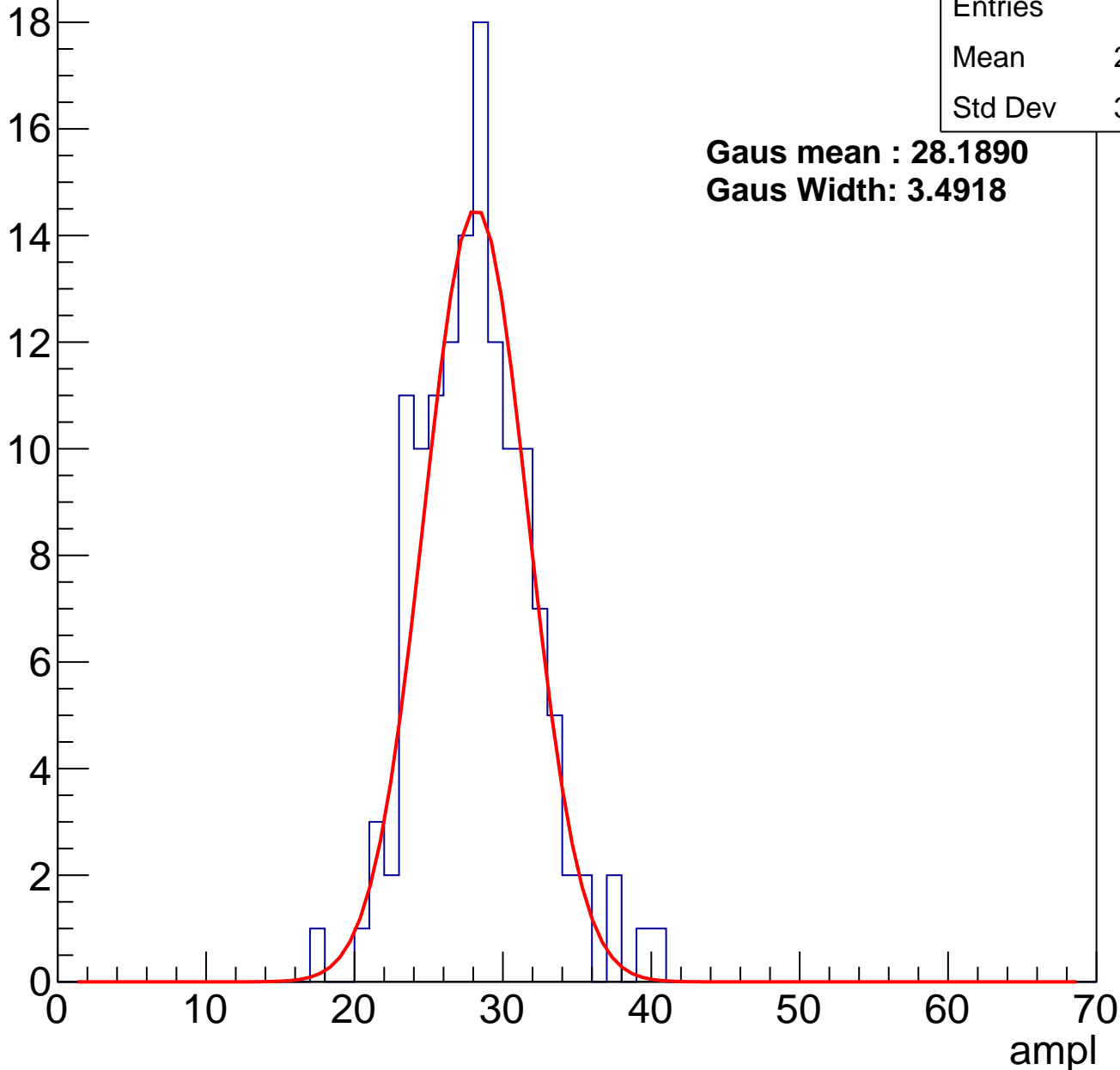
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	135
Mean	27.73
Std Dev	3.817

Gaus mean : 28.1890

Gaus Width: 3.4918



B0L101S, U18-ch20, adc1

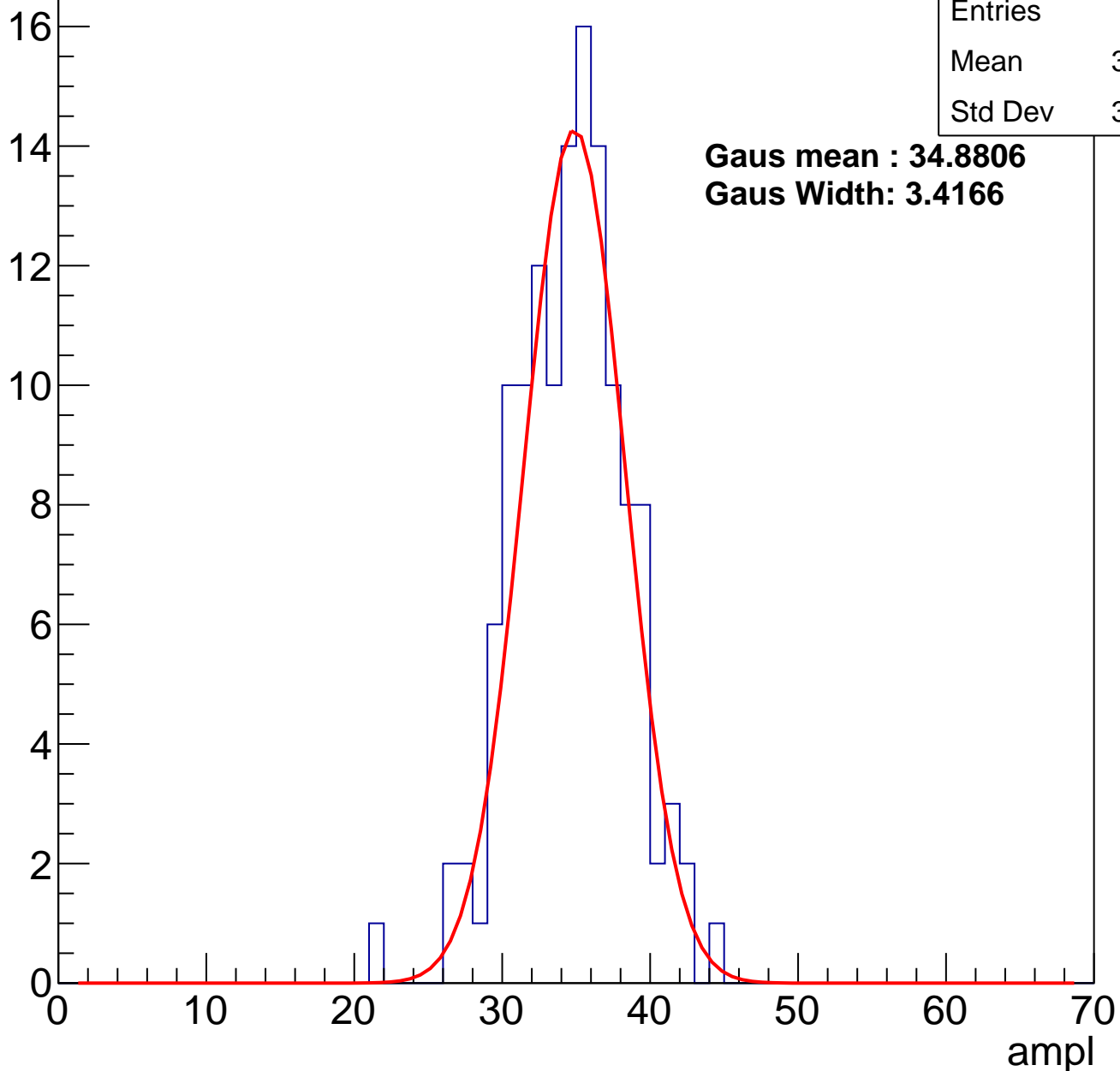
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	34.17
Std Dev	3.716

Gaus mean : 34.8806

Gaus Width: 3.4166

Entry



B0L101S, U18-ch20, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

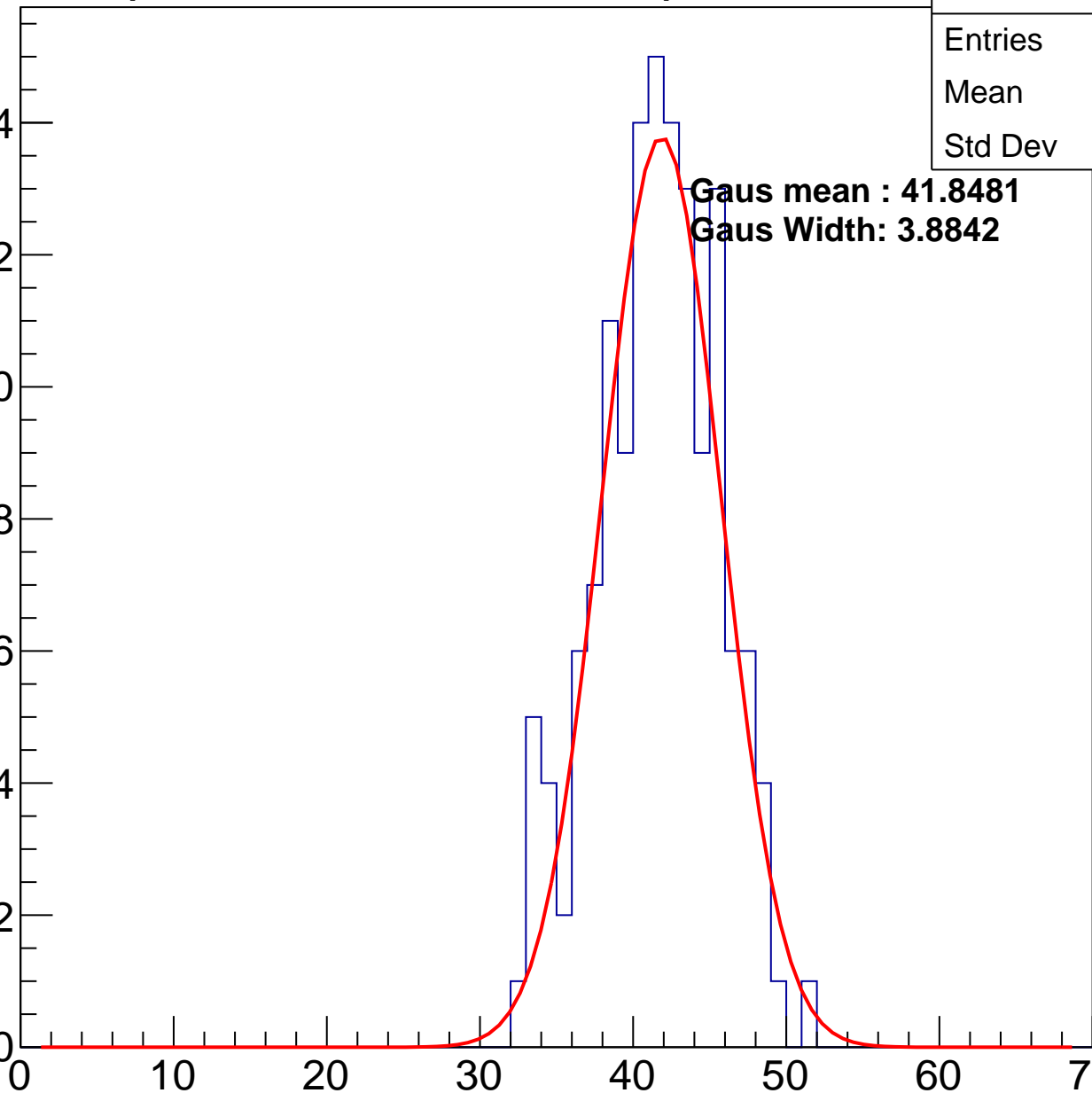
14
12
10
8
6
4
2
0

Entries	141
Mean	41.13
Std Dev	3.913

Gaus mean : 41.8481

Gaus Width: 3.8842

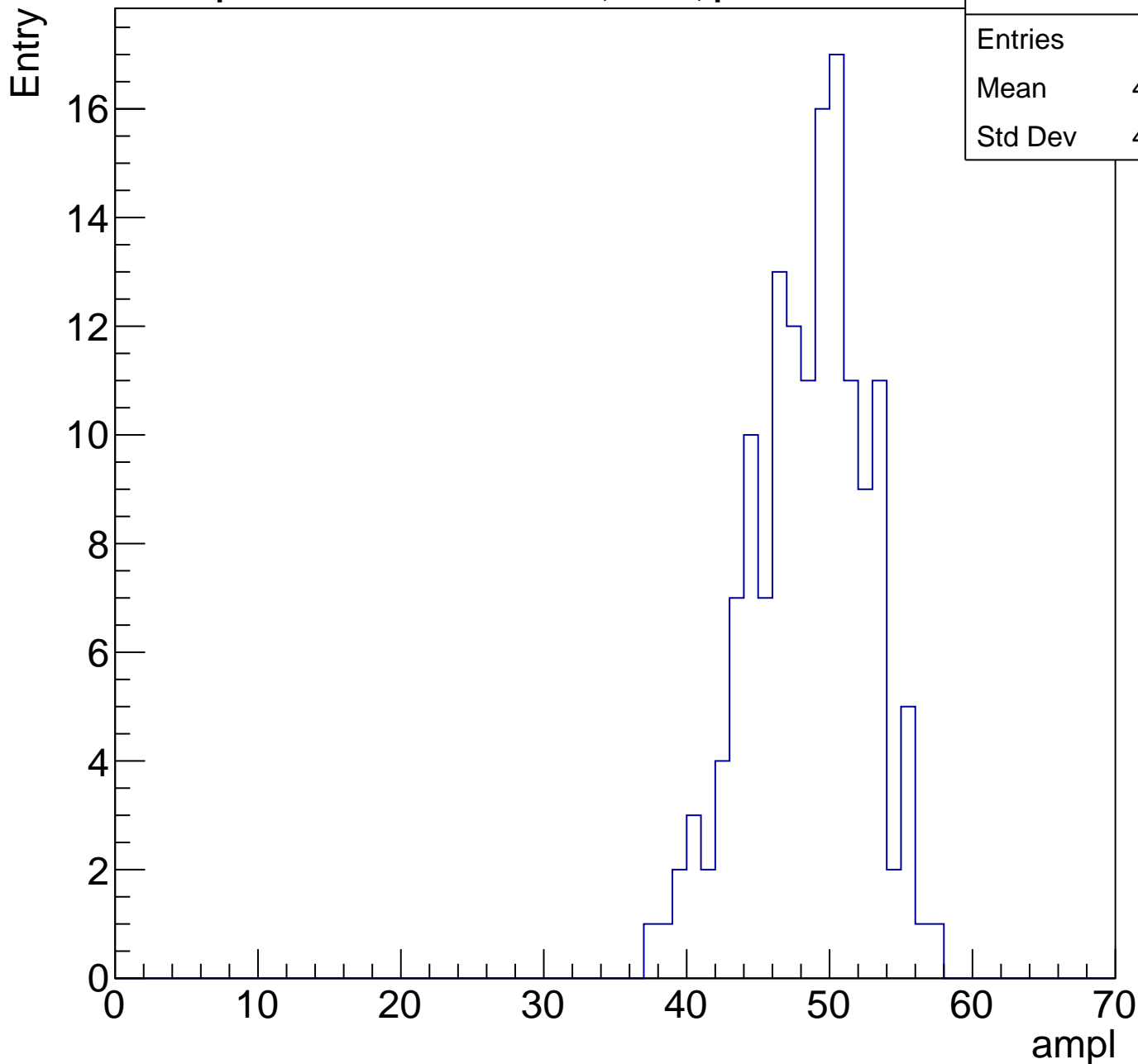
ampl



B0L101S, U18-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	48.02
Std Dev	4.006



B0L101S, U18-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

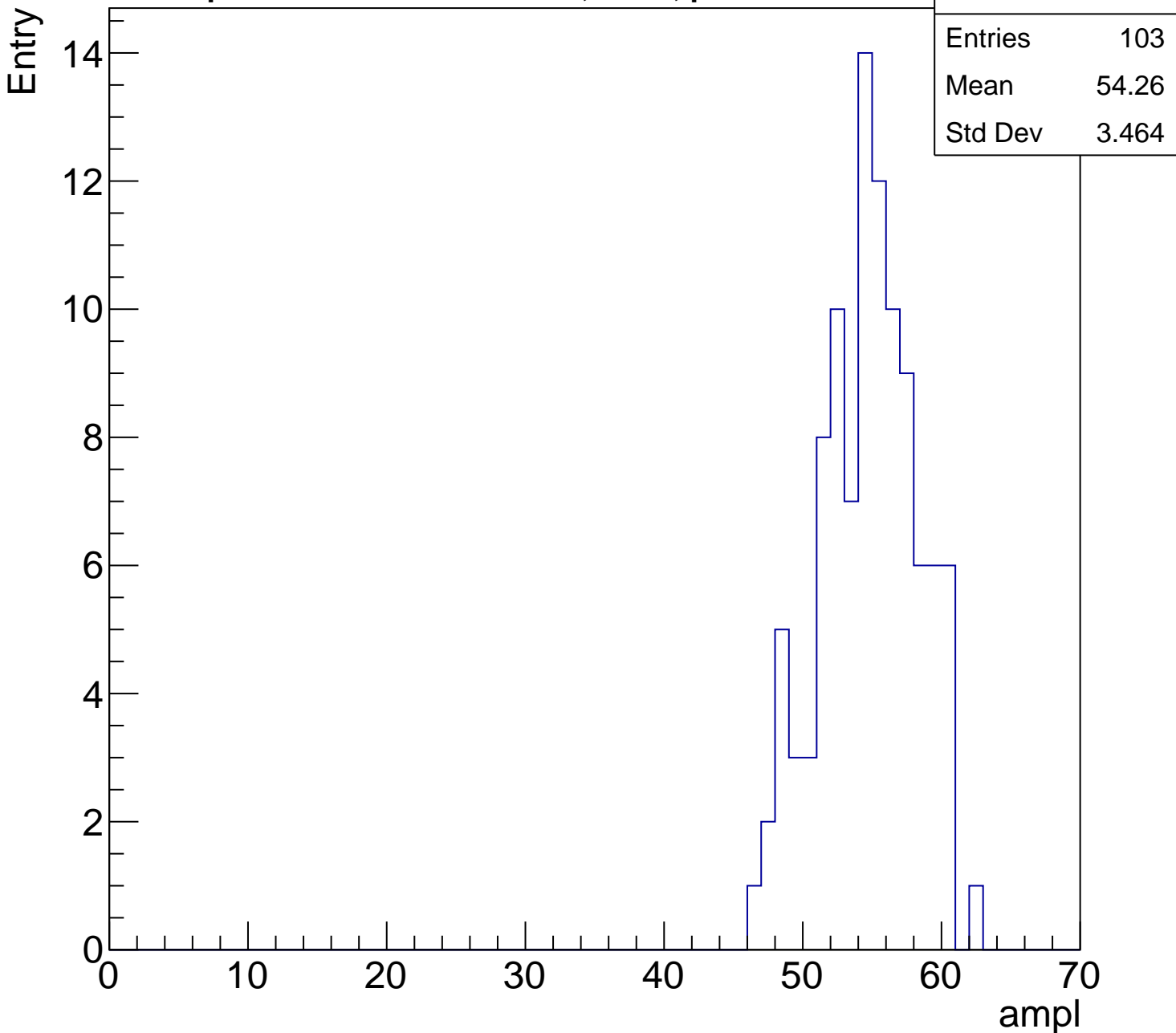
Entries	103
Mean	54.26
Std Dev	3.464

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch20, adc5

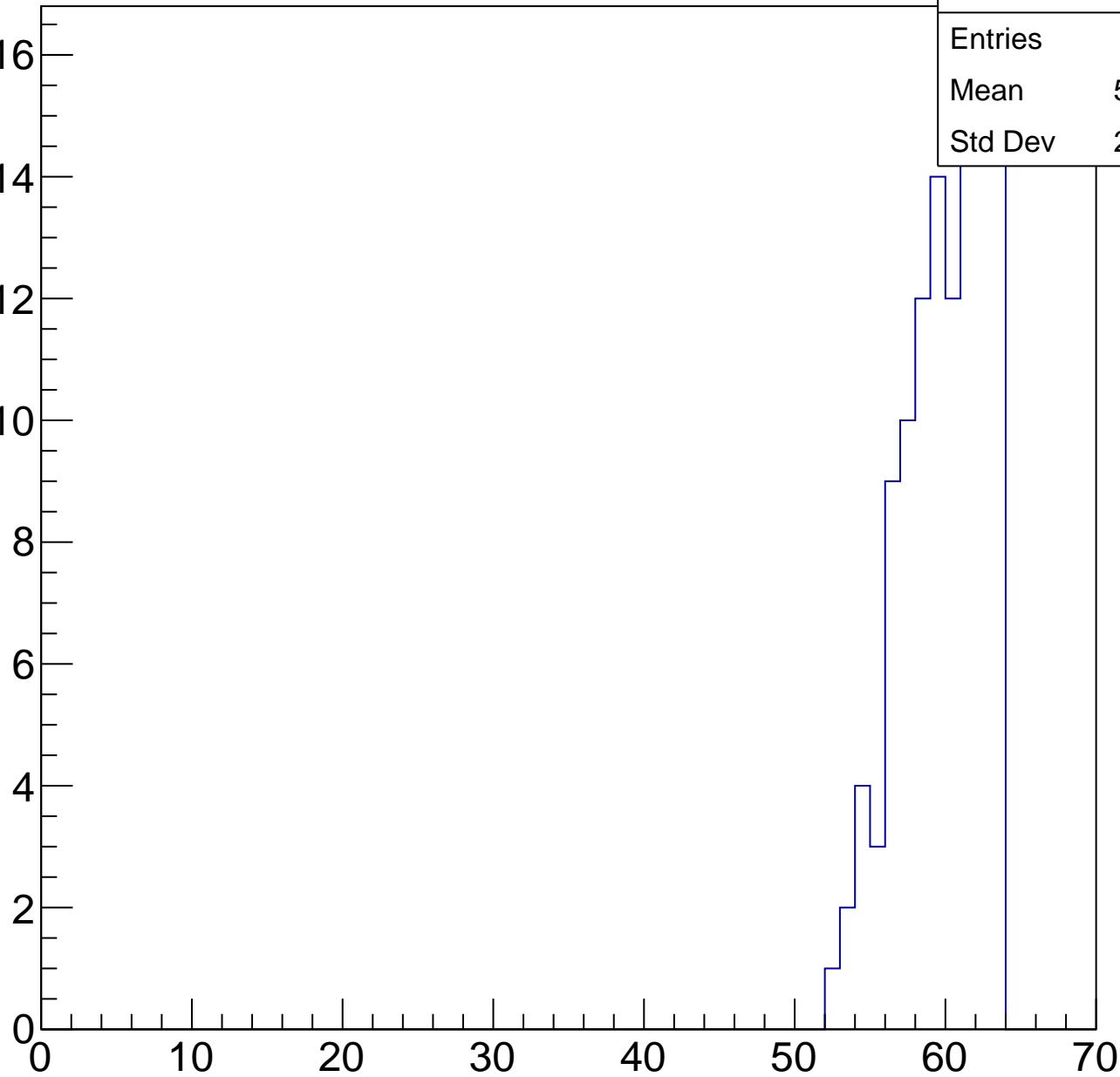
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	113
Mean	59.35
Std Dev	2.726

ampl

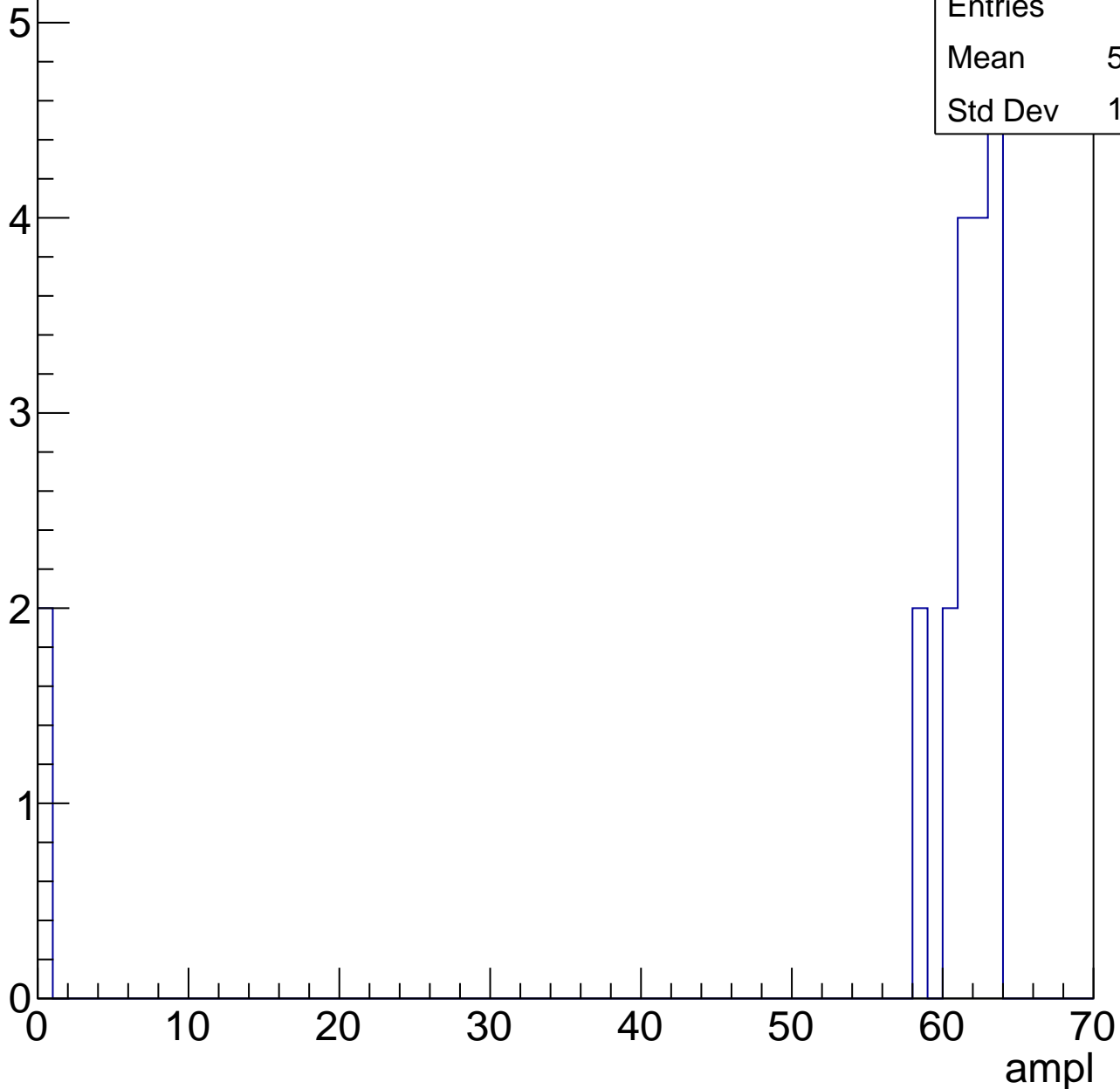


B0L101S, U18-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	54.89
Std Dev	18.89



B0L101S, U18-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch21, adc0

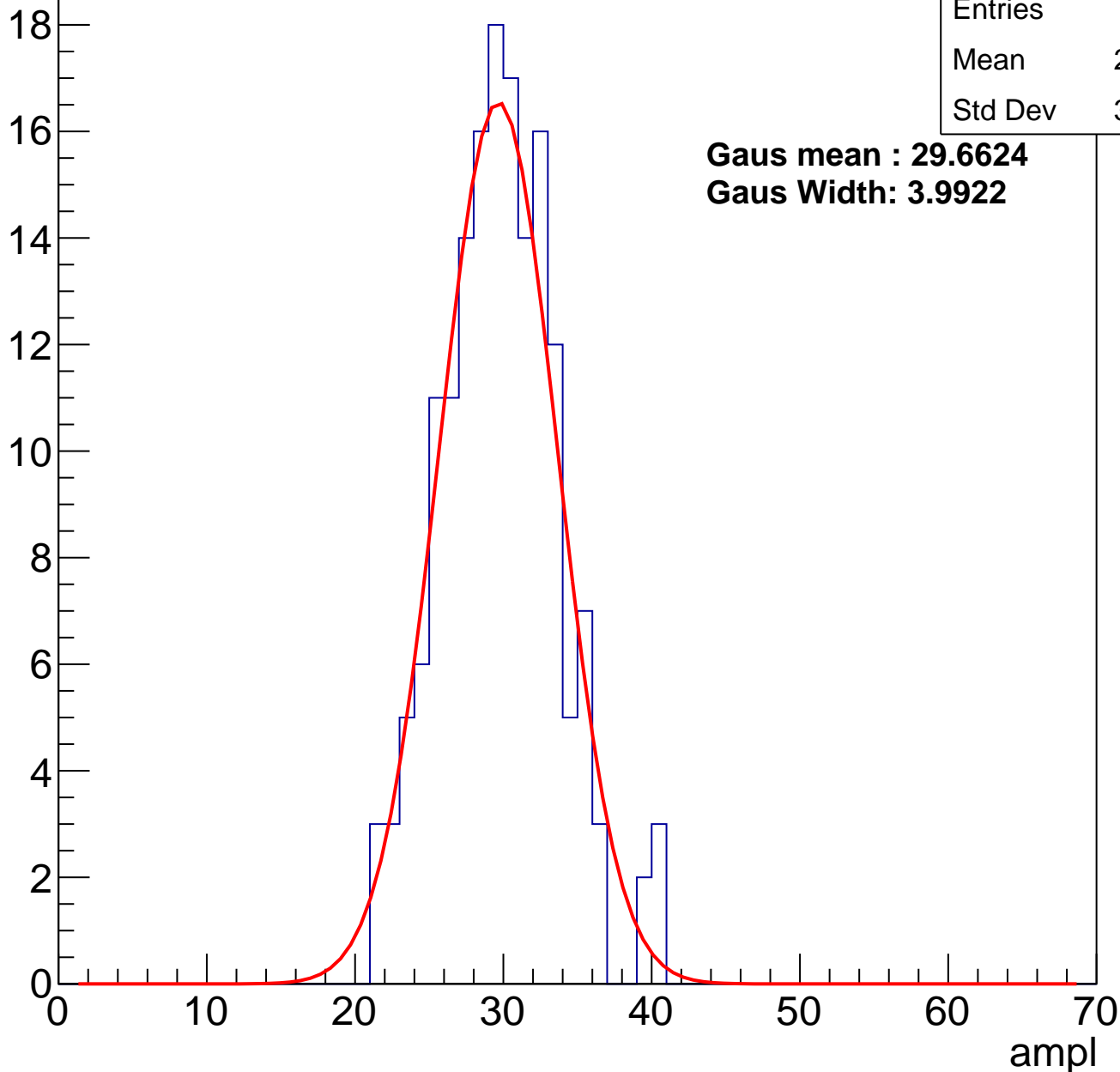
calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	29.34
Std Dev	3.875

Gaus mean : 29.6624

Gaus Width: 3.9922

Entry



B0L101S, U18-ch21, adc1

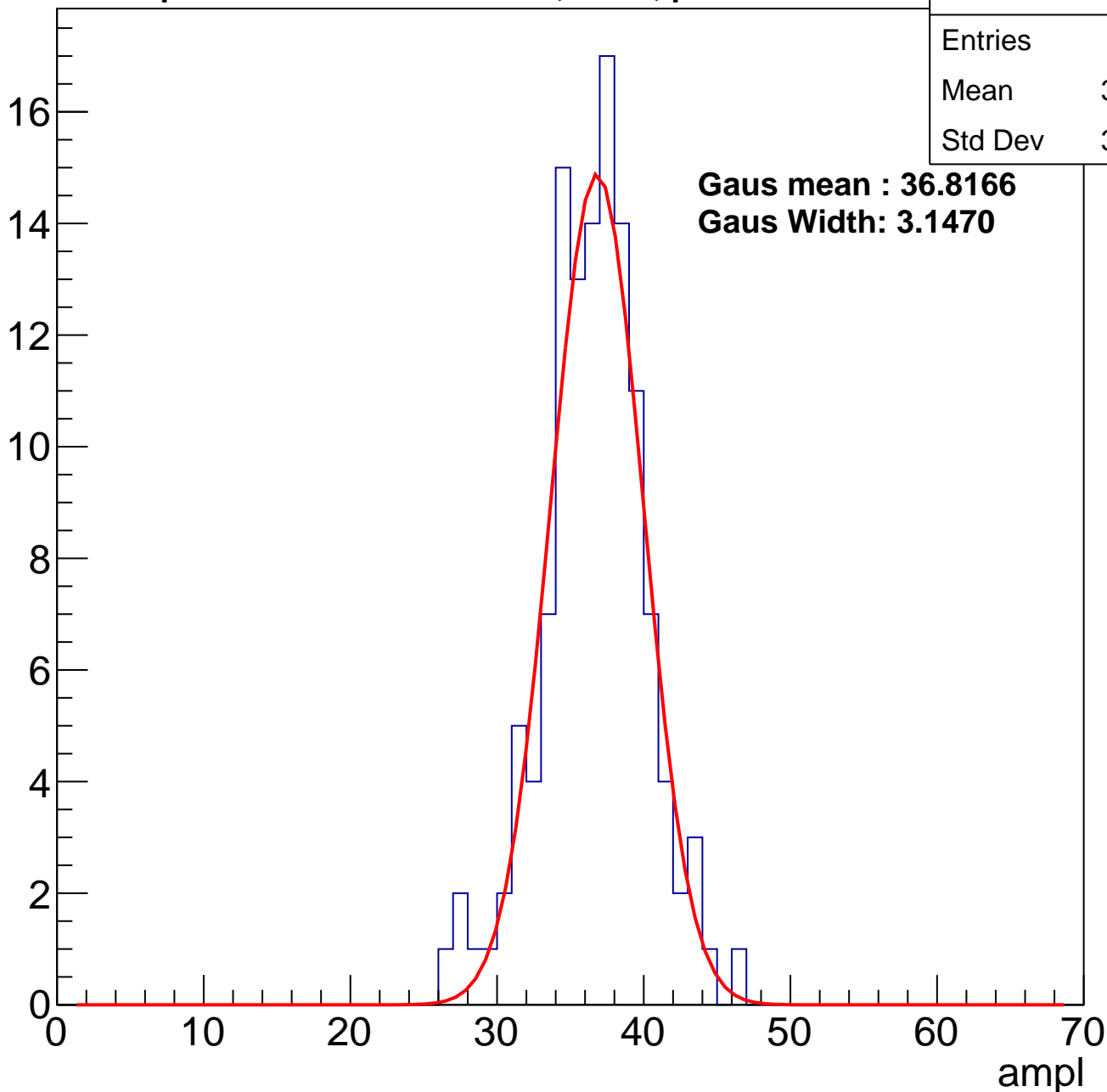
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	36.14
Std Dev	3.506

Gaus mean : 36.8166

Gaus Width: 3.1470

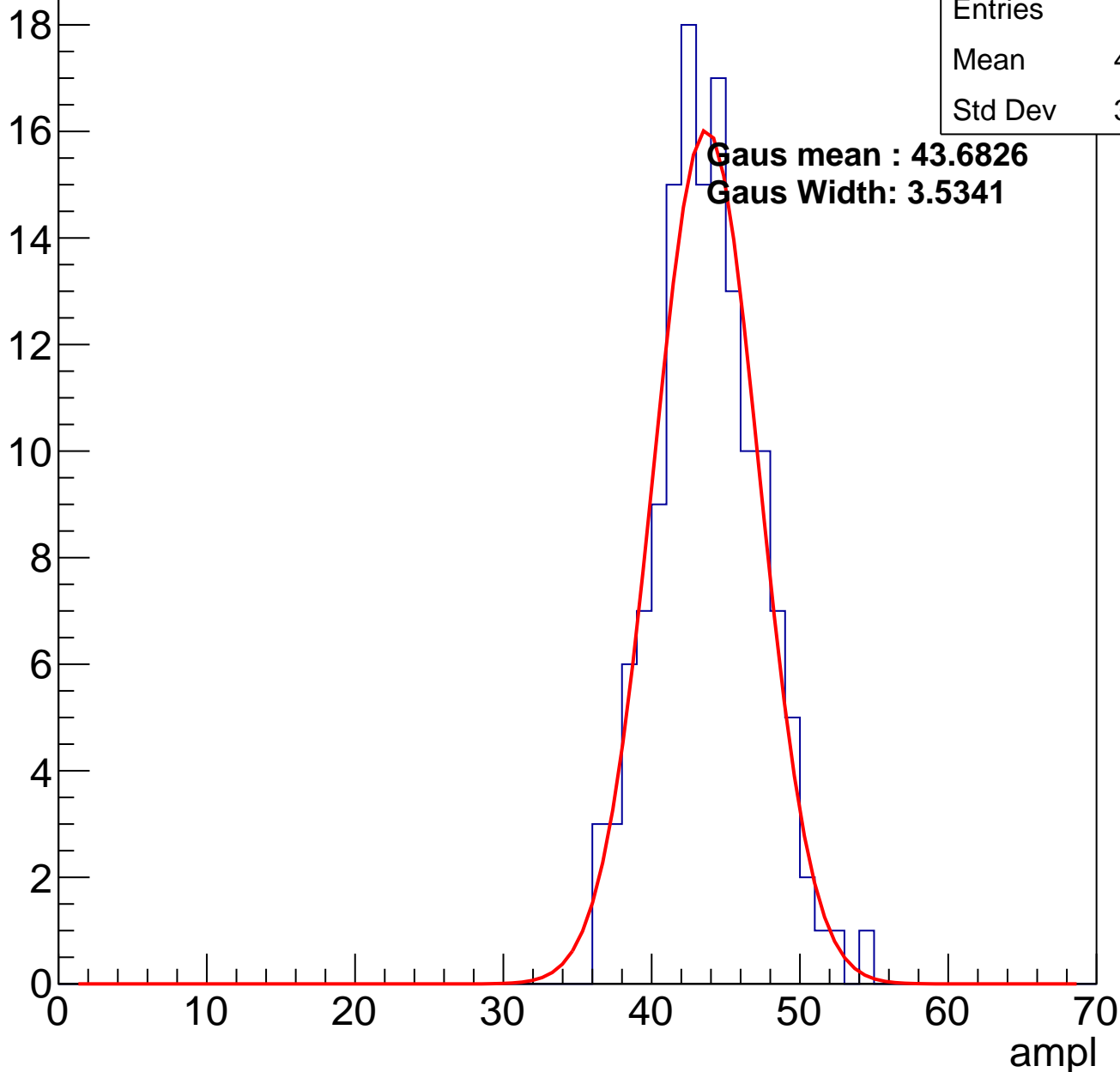
Entry



B0L101S, U18-ch21, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

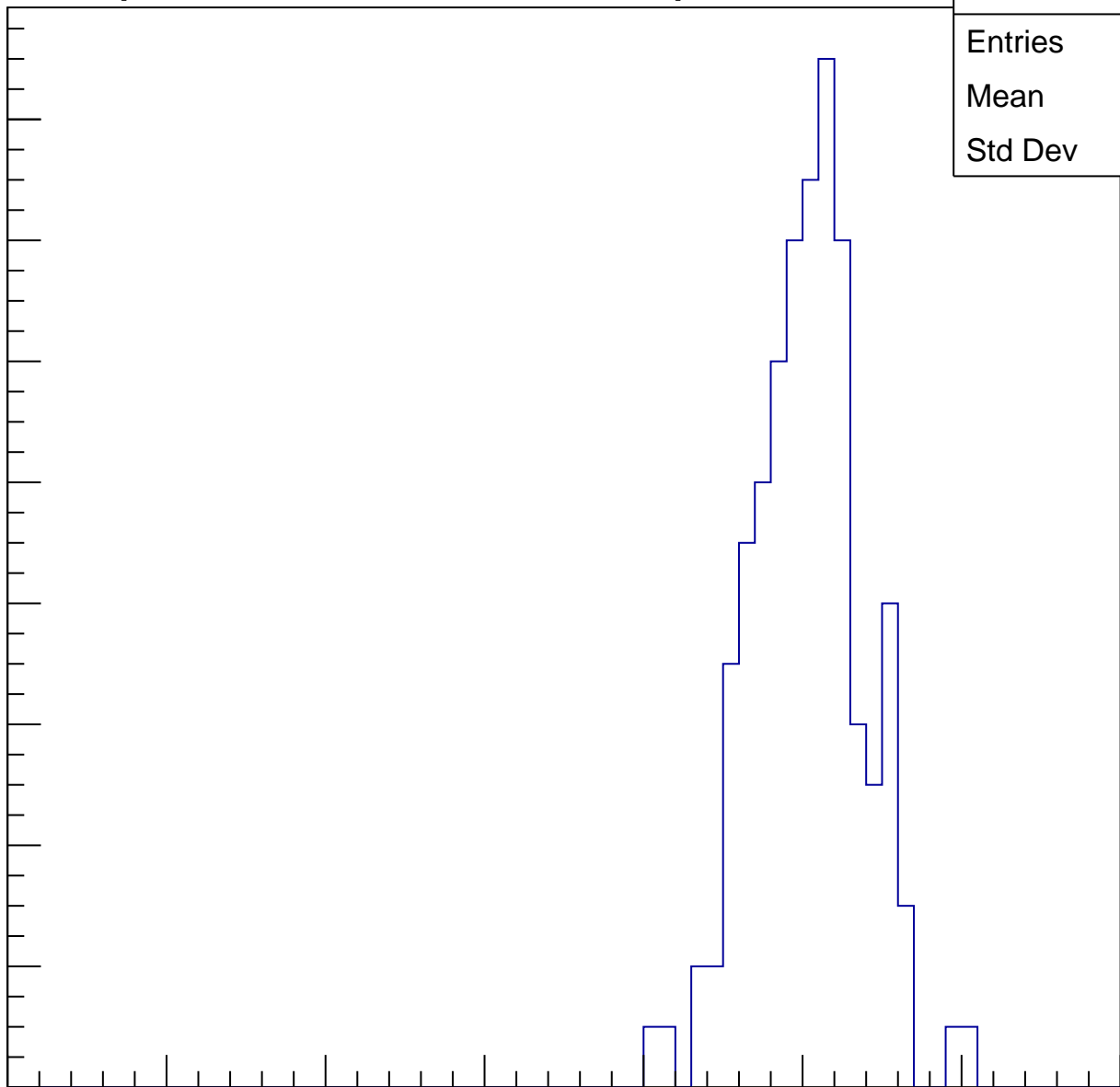
Entries	128
Mean	49.81
Std Dev	3.439

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch21, adc4

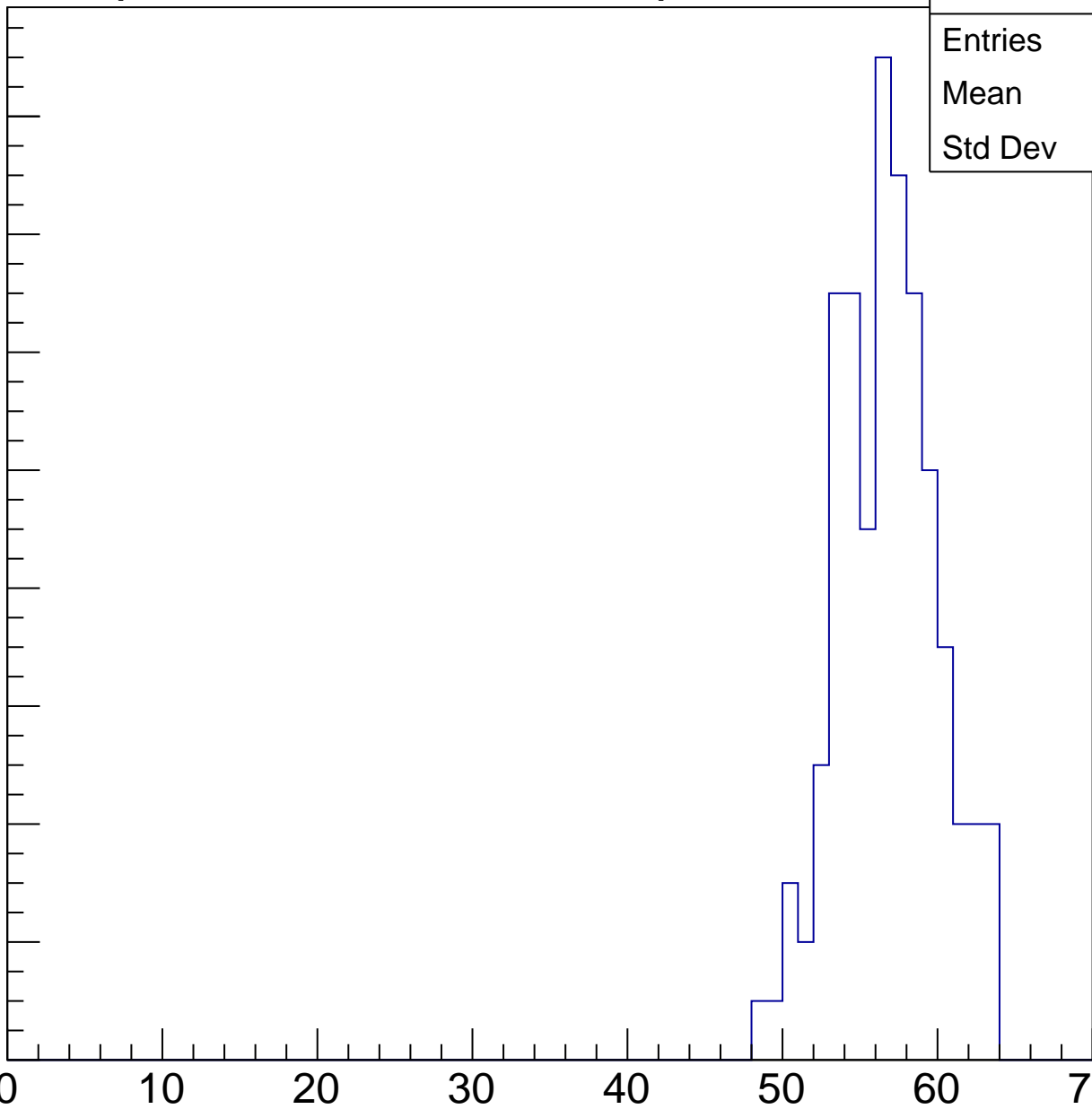
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	56.28
Std Dev	3.186

ampl



B0L101S, U18-ch21, adc5

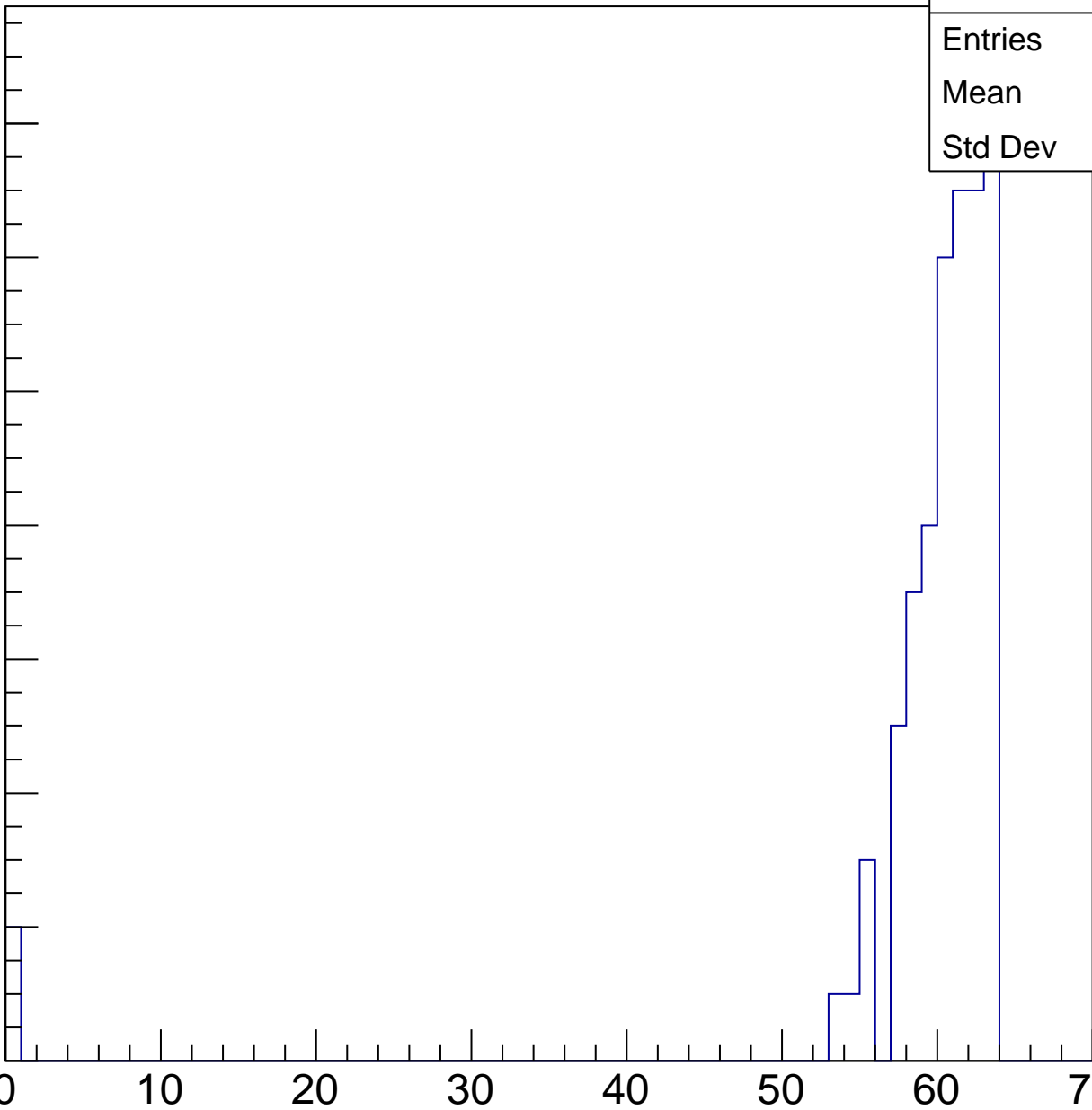
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	58.74
Std Dev	9.691

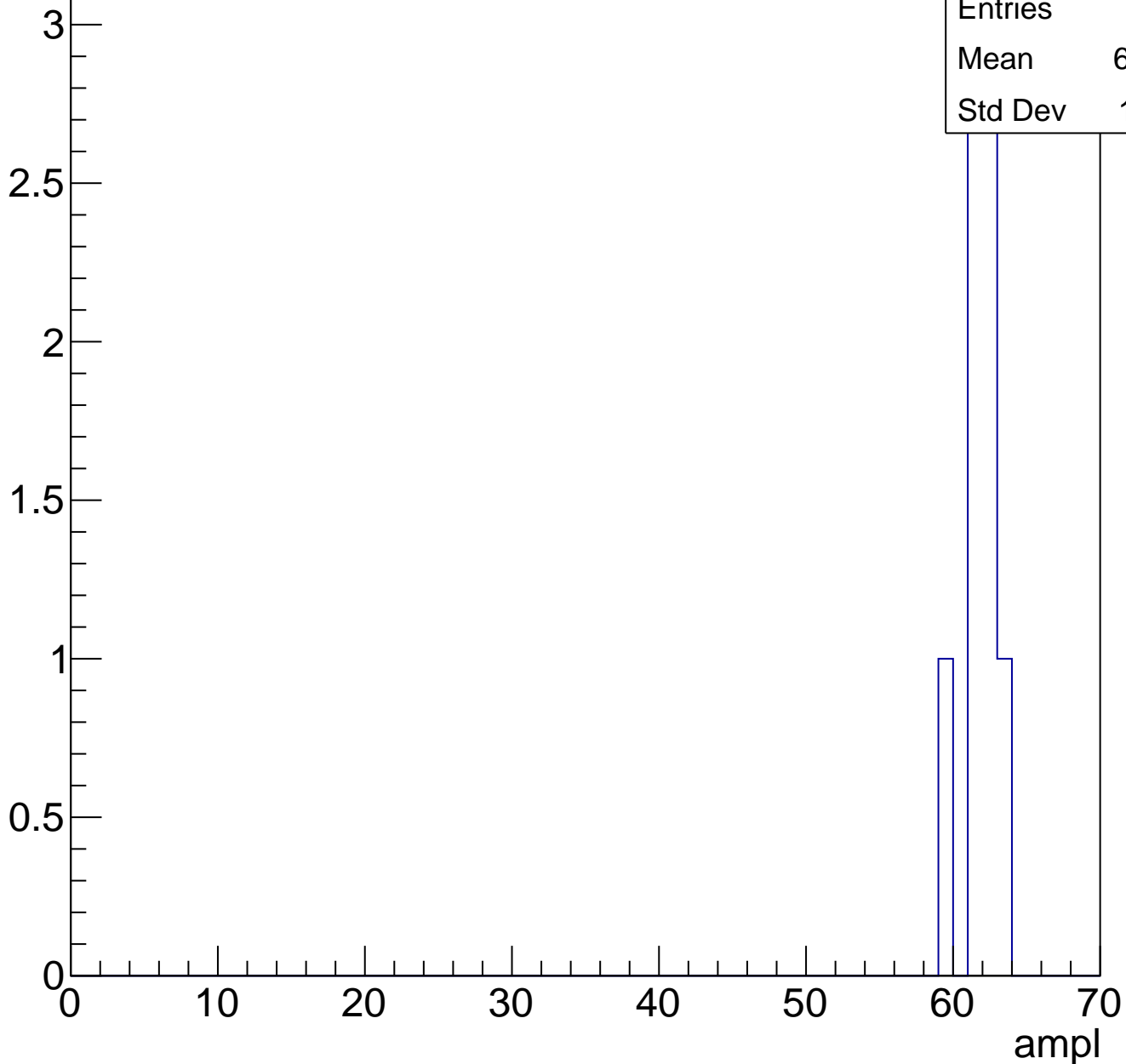
ampl



B0L101S, U18-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

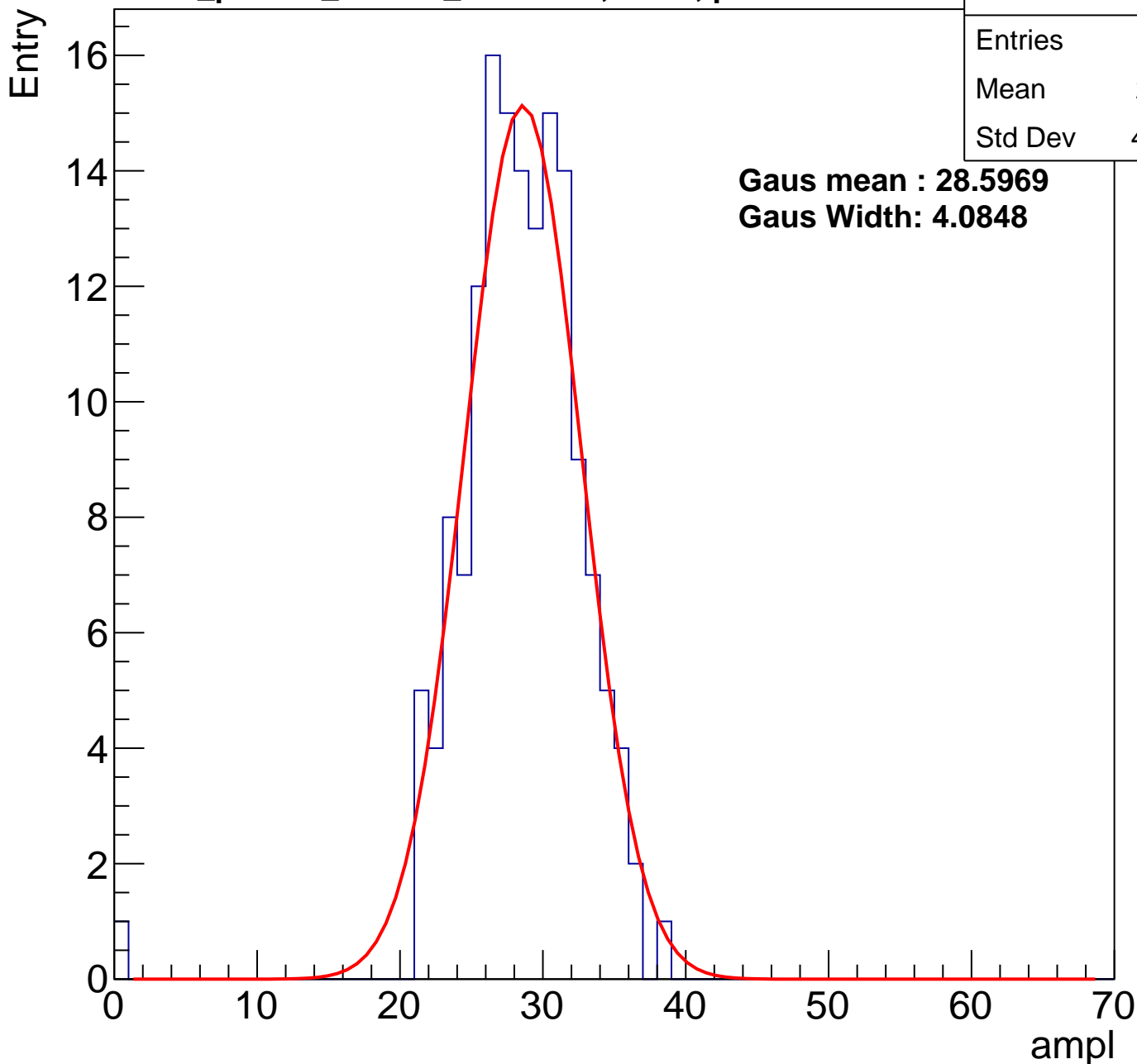
B0L101S, U18-ch22, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.01
Std Dev	4.287

Gaus mean : 28.5969

Gaus Width: 4.0848



B0L101S, U18-ch22, adc1

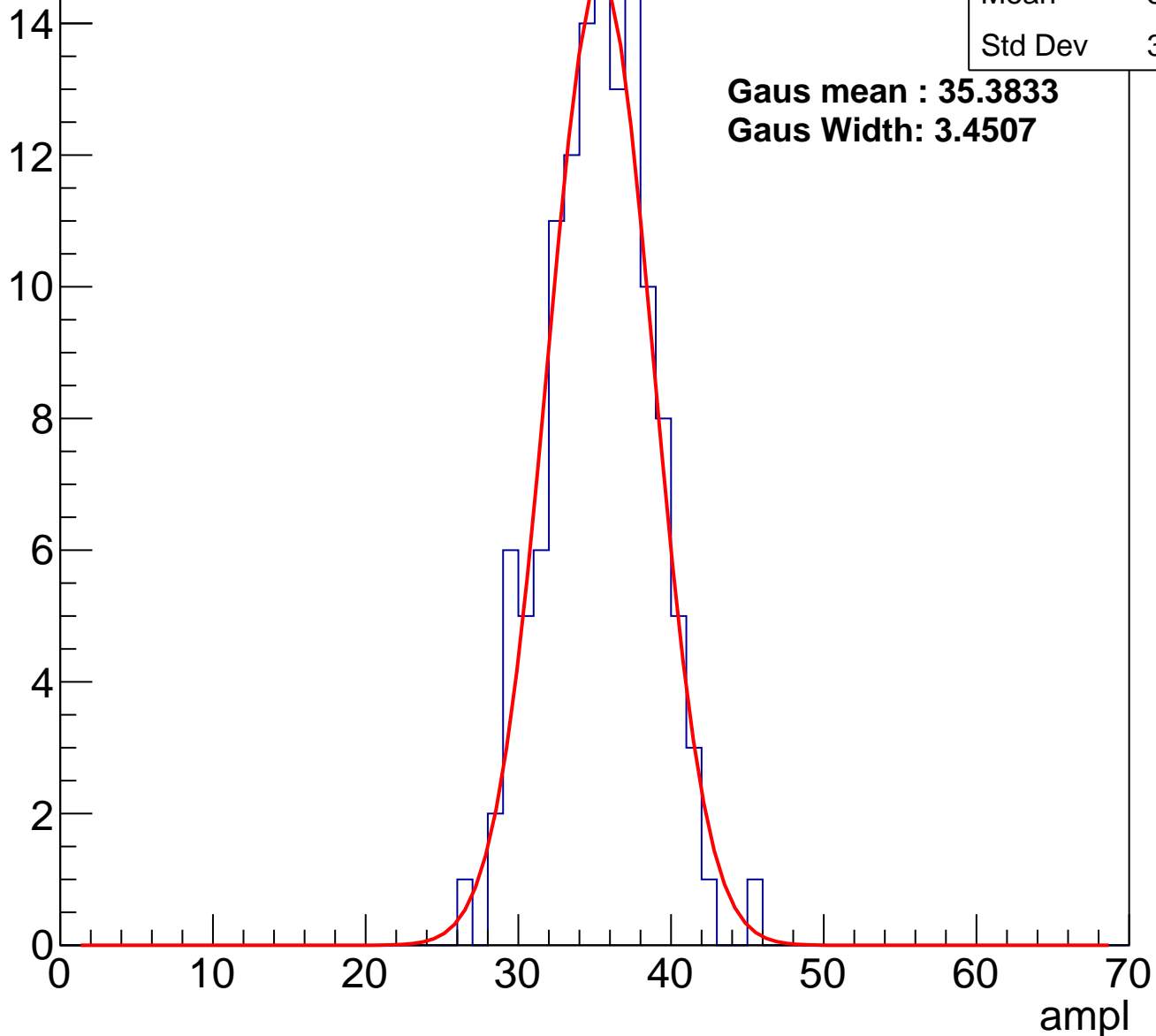
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	34.89
Std Dev	3.364

Gaus mean : 35.3833

Gaus Width: 3.4507

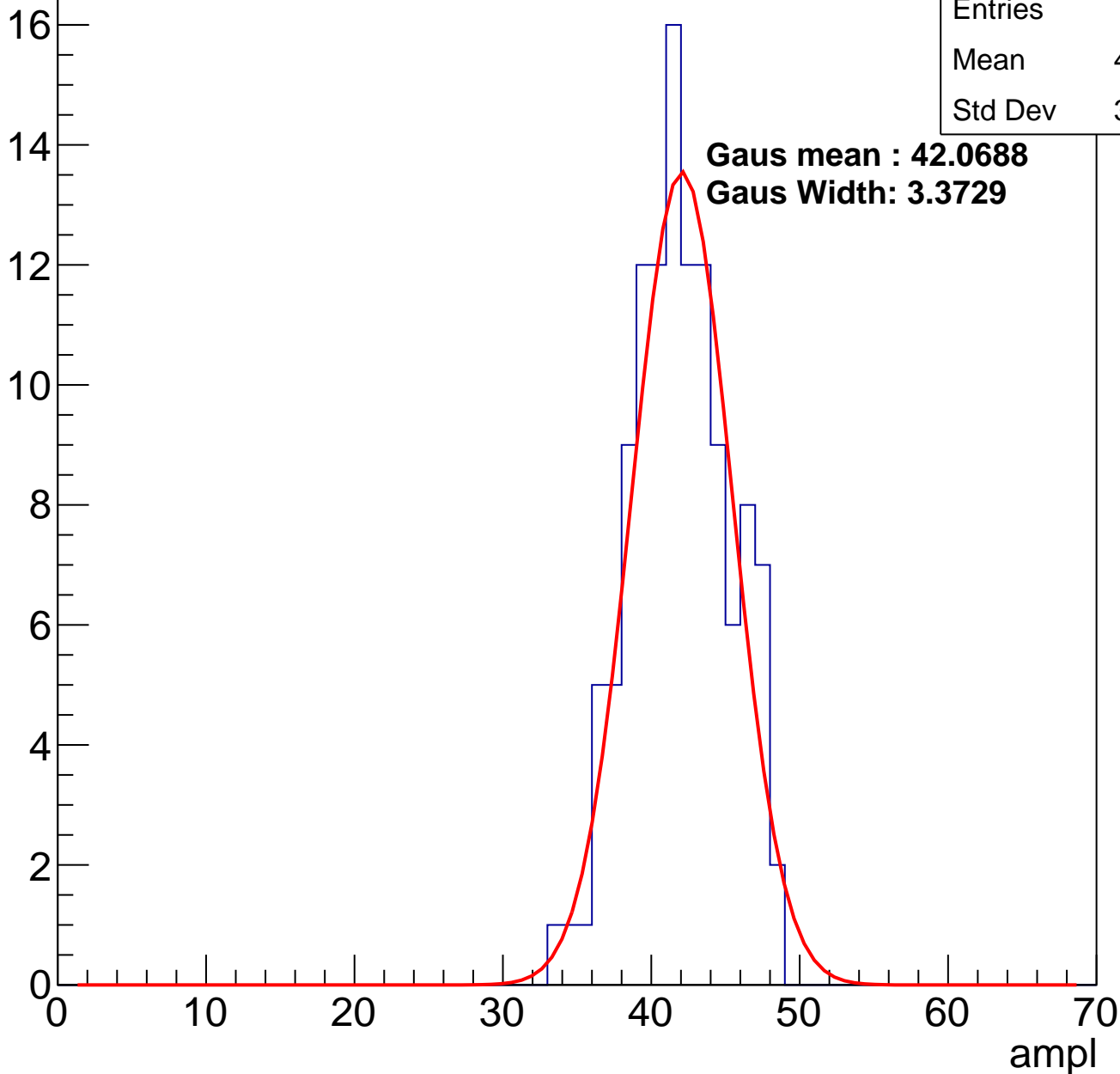
Entry



B0L101S, U18-ch22, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

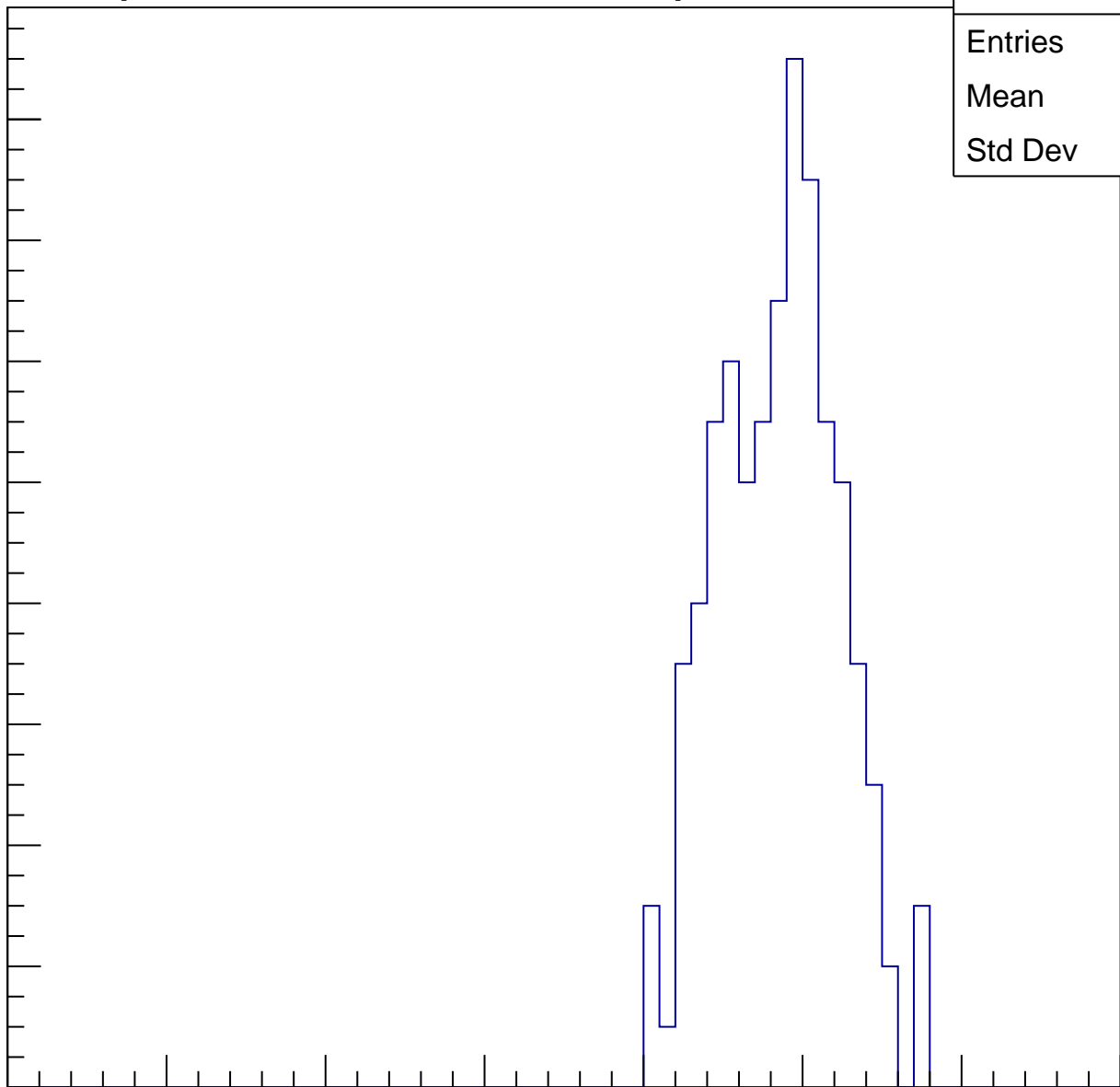
Entries	146
Mean	48.01
Std Dev	3.756

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

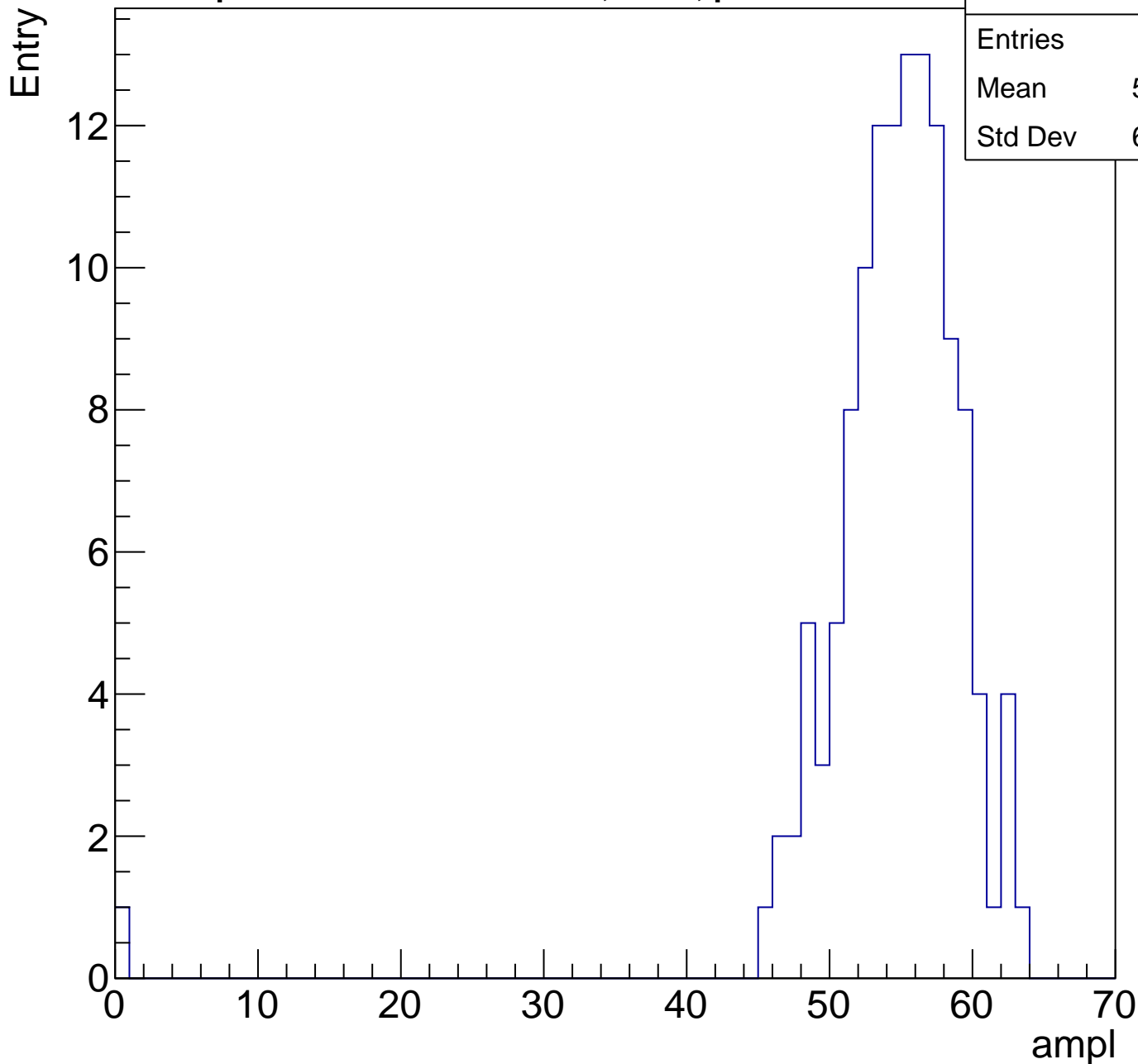
ampl



B0L101S, U18-ch22, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	54.07
Std Dev	6.126



B0L101S, U18-ch22, adc5

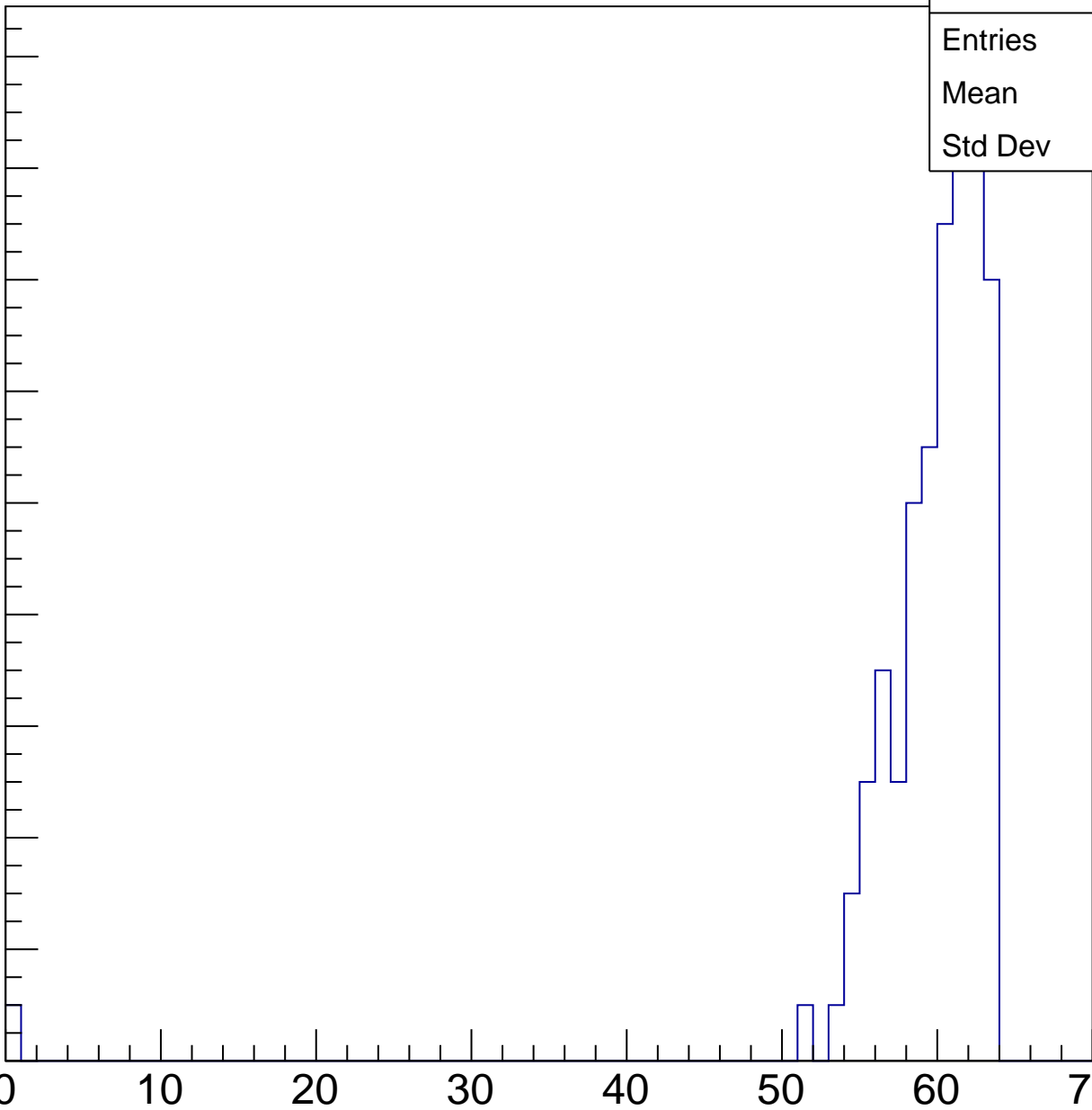
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	107
Mean	59.06
Std Dev	6.327

ampl

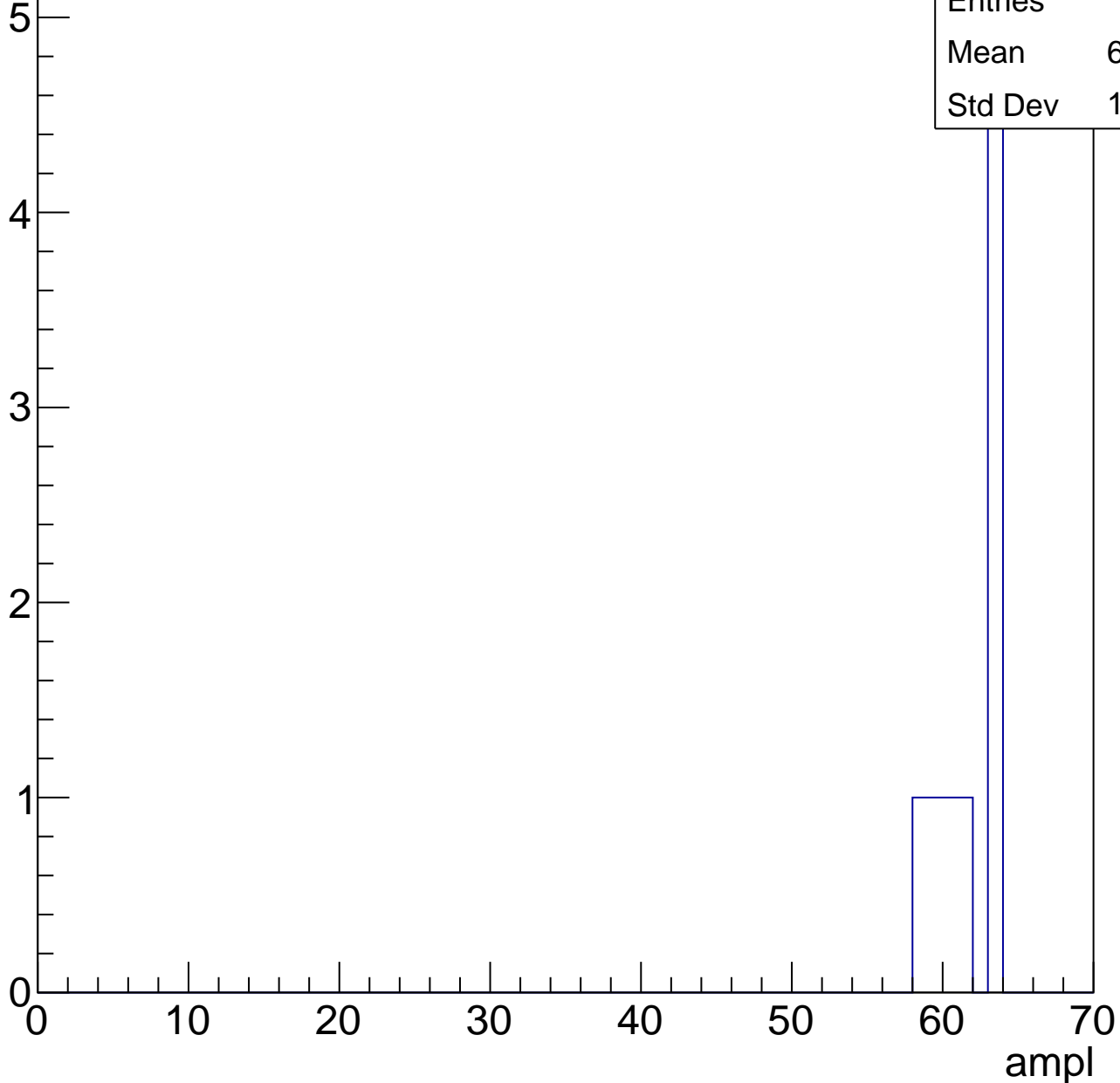


B0L101S, U18-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	61.44
Std Dev	1.892



B0L101S, U18-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch23, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	31.52
Std Dev	4.401

Gaus mean : 32.1921

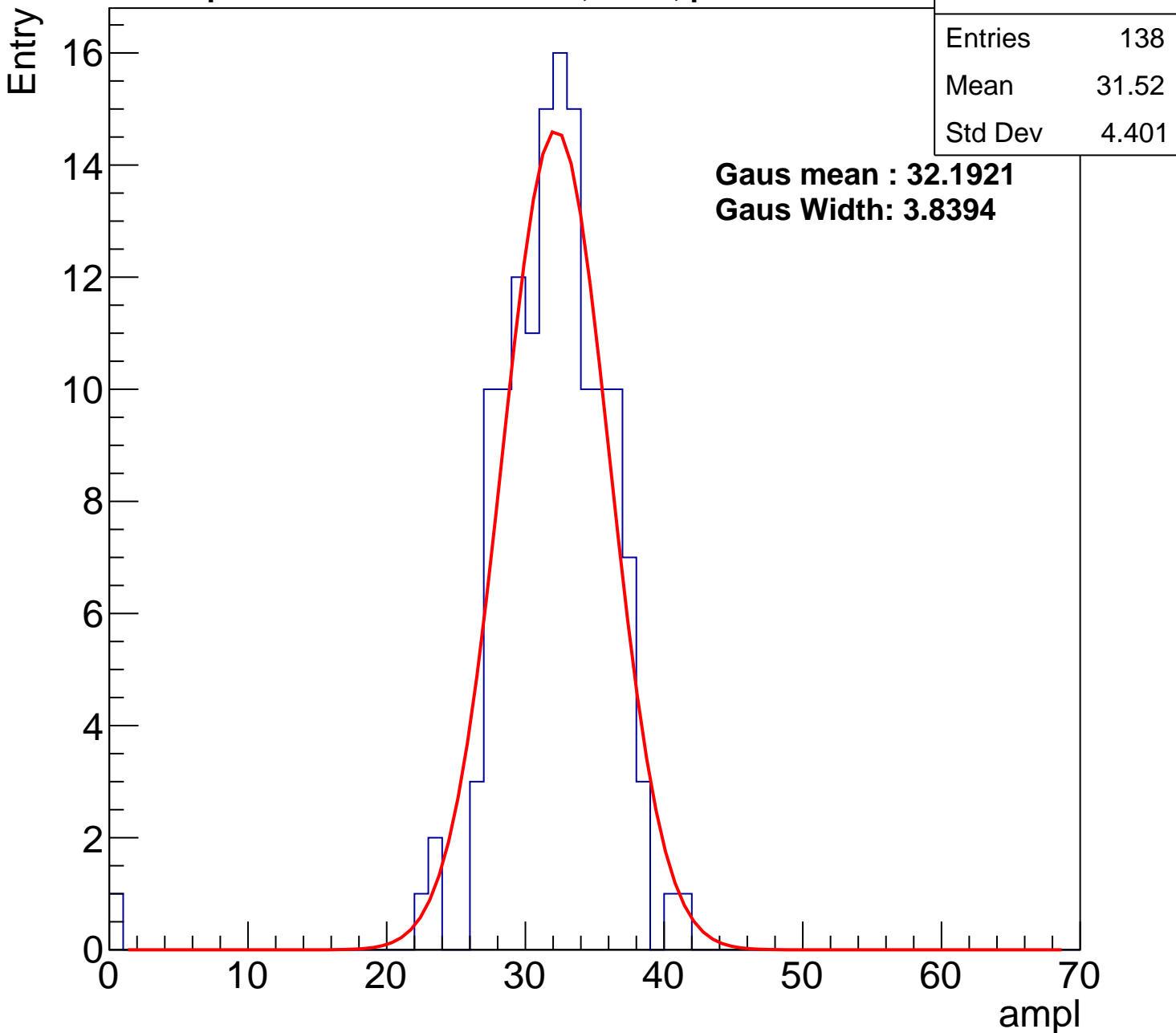
Gaus Width: 3.8394

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

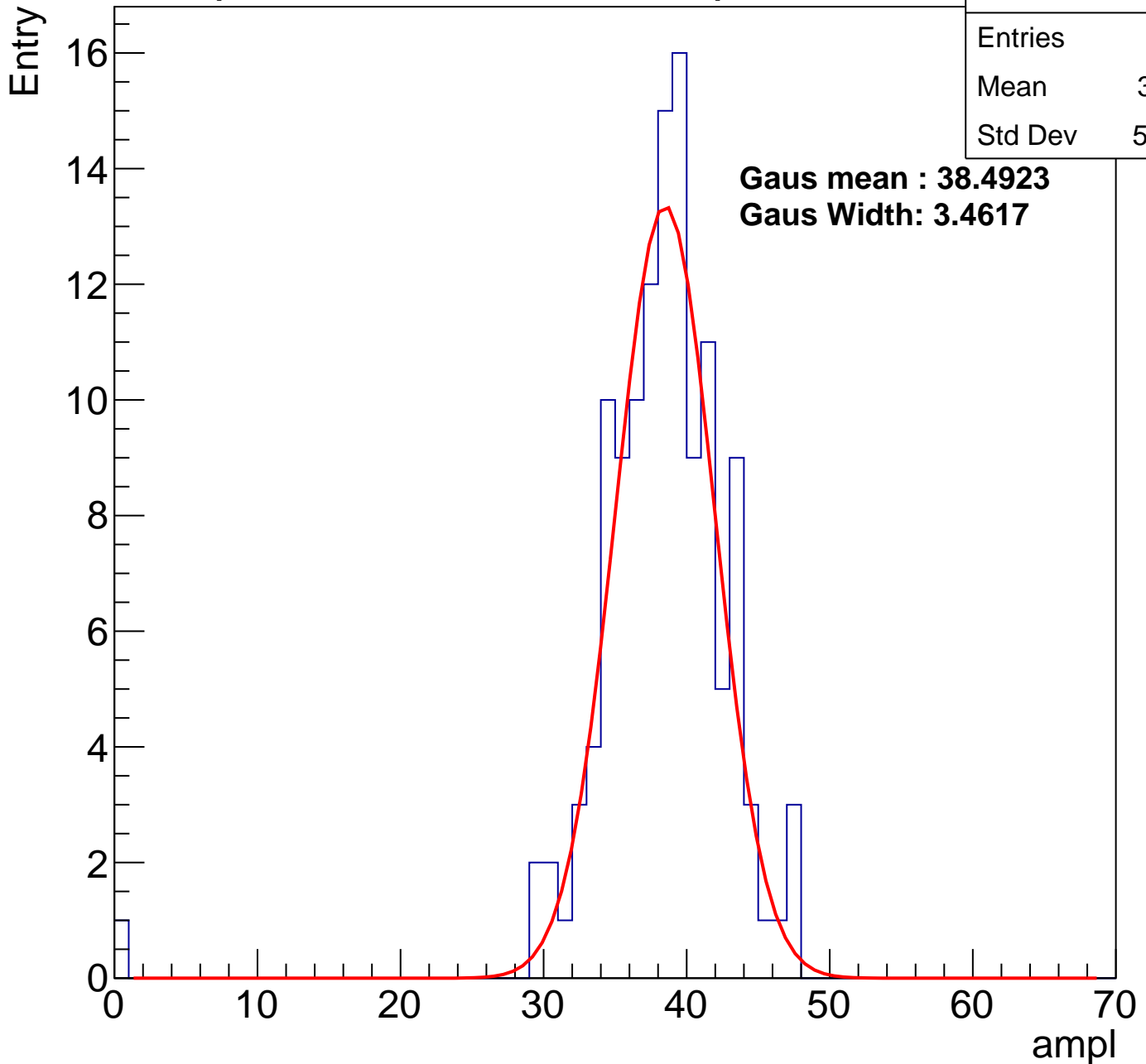


B0L101S, U18-ch23, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	37.81
Std Dev	5.019

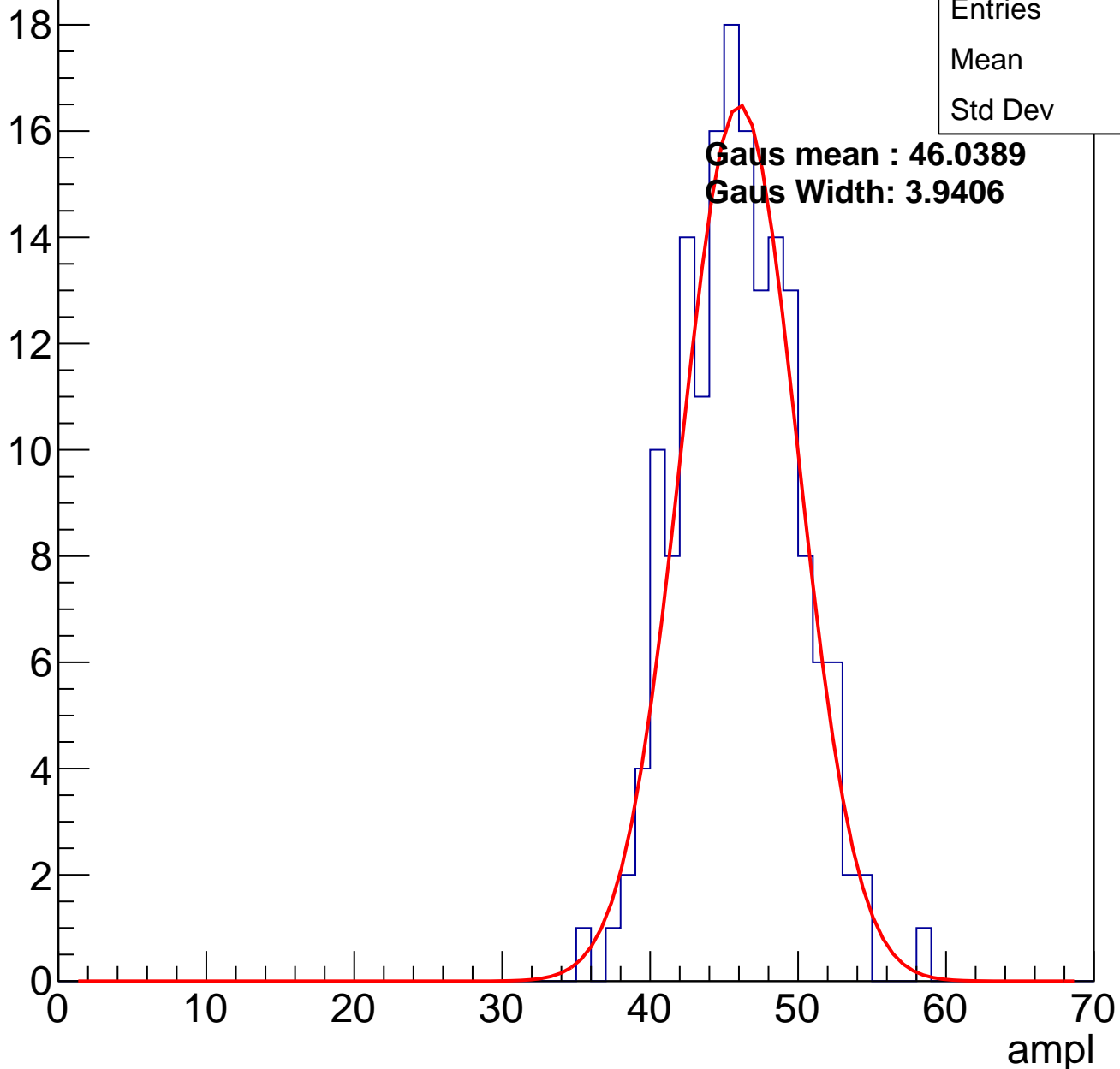
Gaus mean : 38.4923
Gaus Width: 3.4617



B0L101S, U18-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

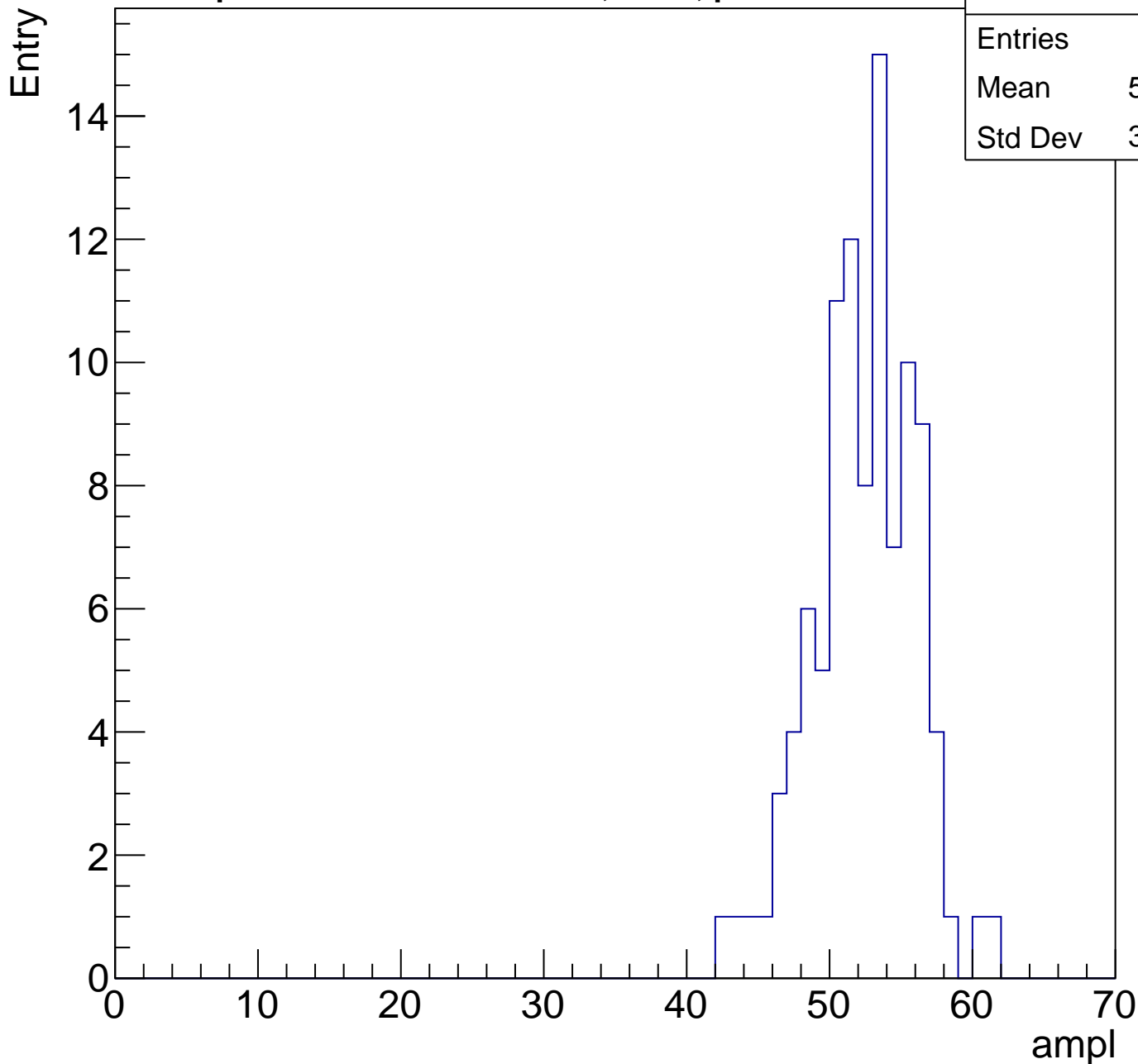
Entry



B0L101S, U18-ch23, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	51.93
Std Dev	3.527



B0L101S, U18-ch23, adc4

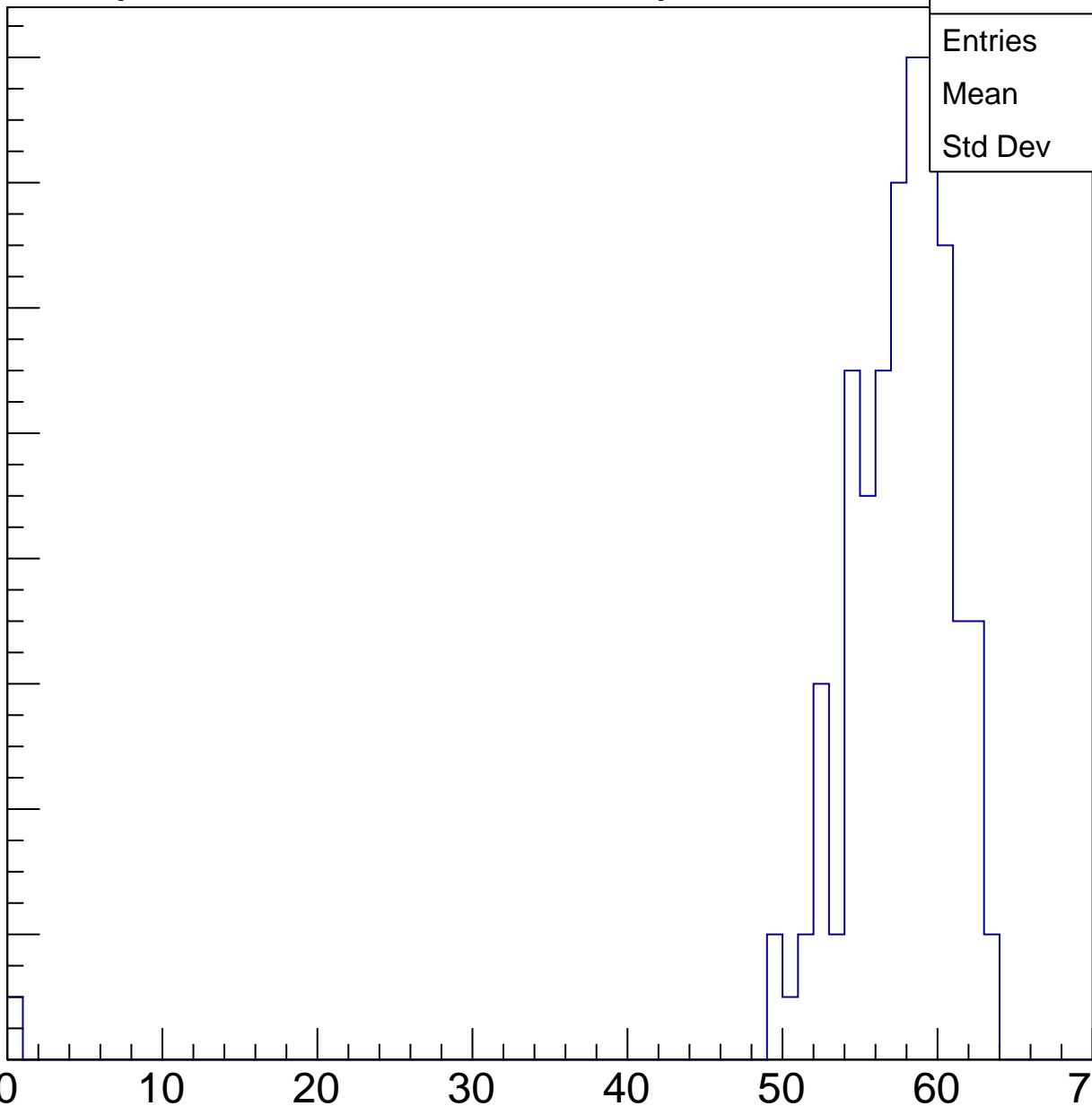
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	56.75
Std Dev	6.038

ampl



B0L101S, U18-ch23, adc5

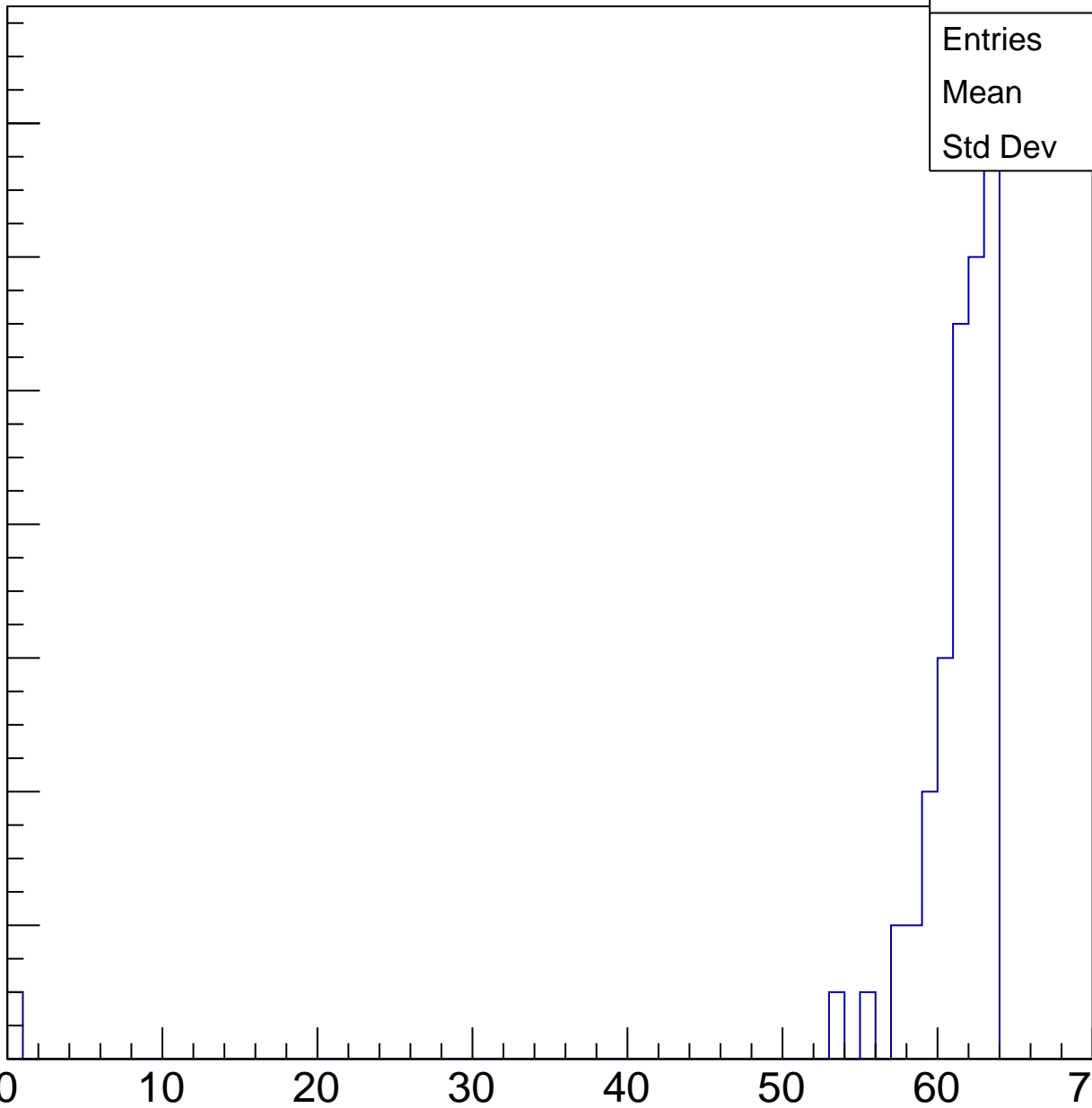
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	55
Mean	59.89
Std Dev	8.418

ampl



B0L101S, U18-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch24, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	25.97
Std Dev	4.403

Gaus mean : 26.4414

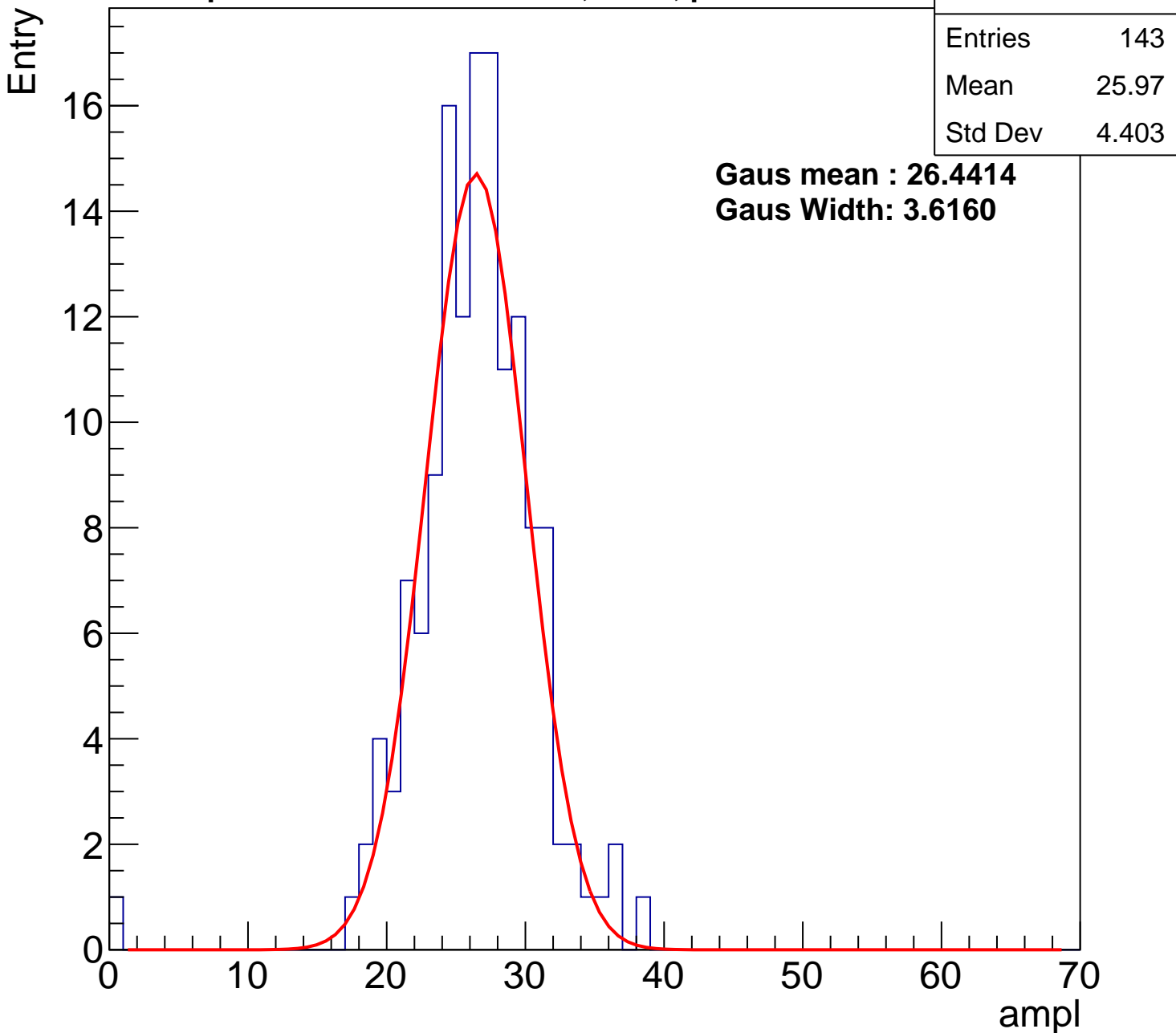
Gaus Width: 3.6160

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

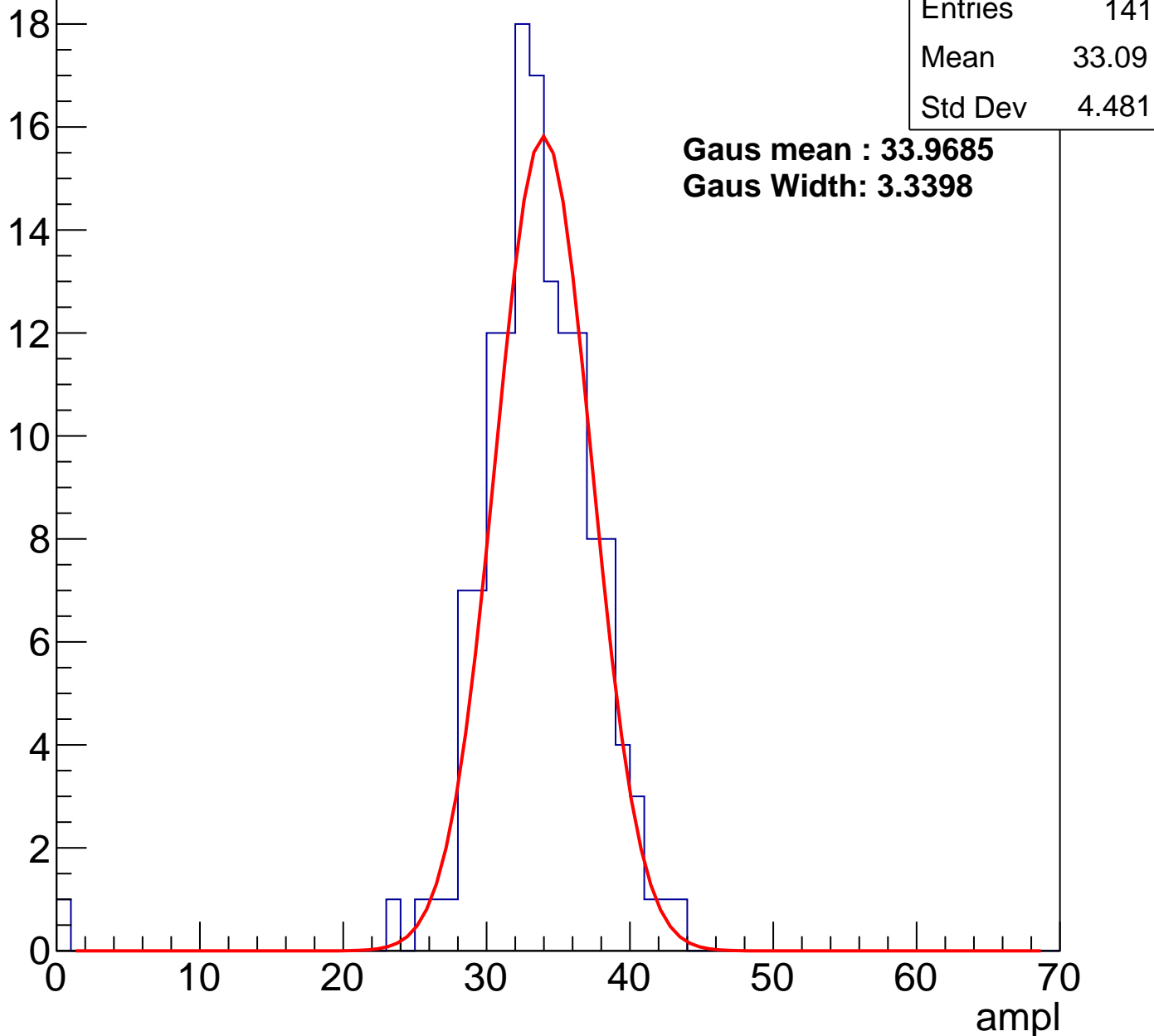
ampl



B0L101S, U18-ch24, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch24, adc2

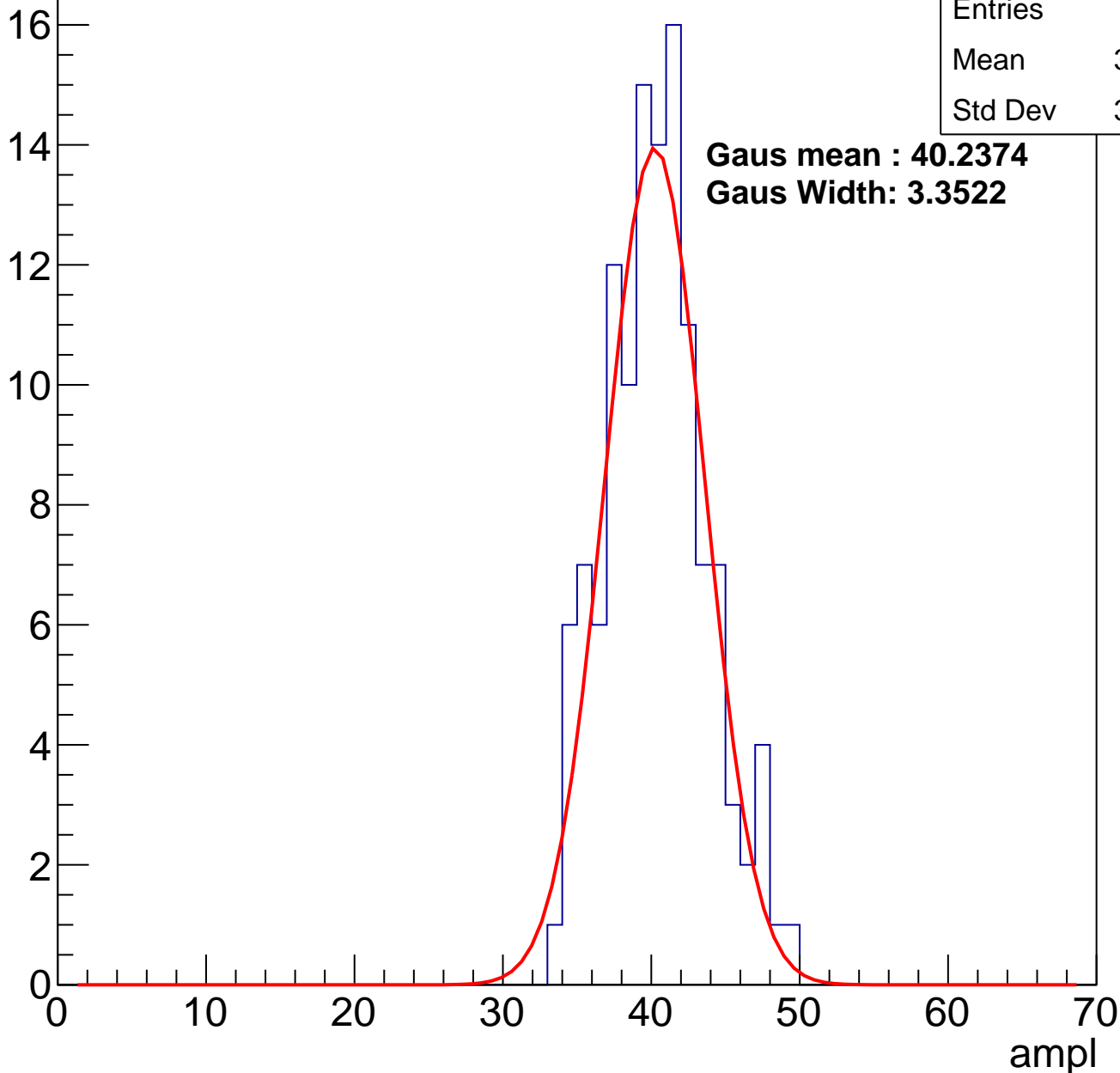
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	39.89
Std Dev	3.398

Gaus mean : 40.2374

Gaus Width: 3.3522

Entry



B0L101S, U18-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

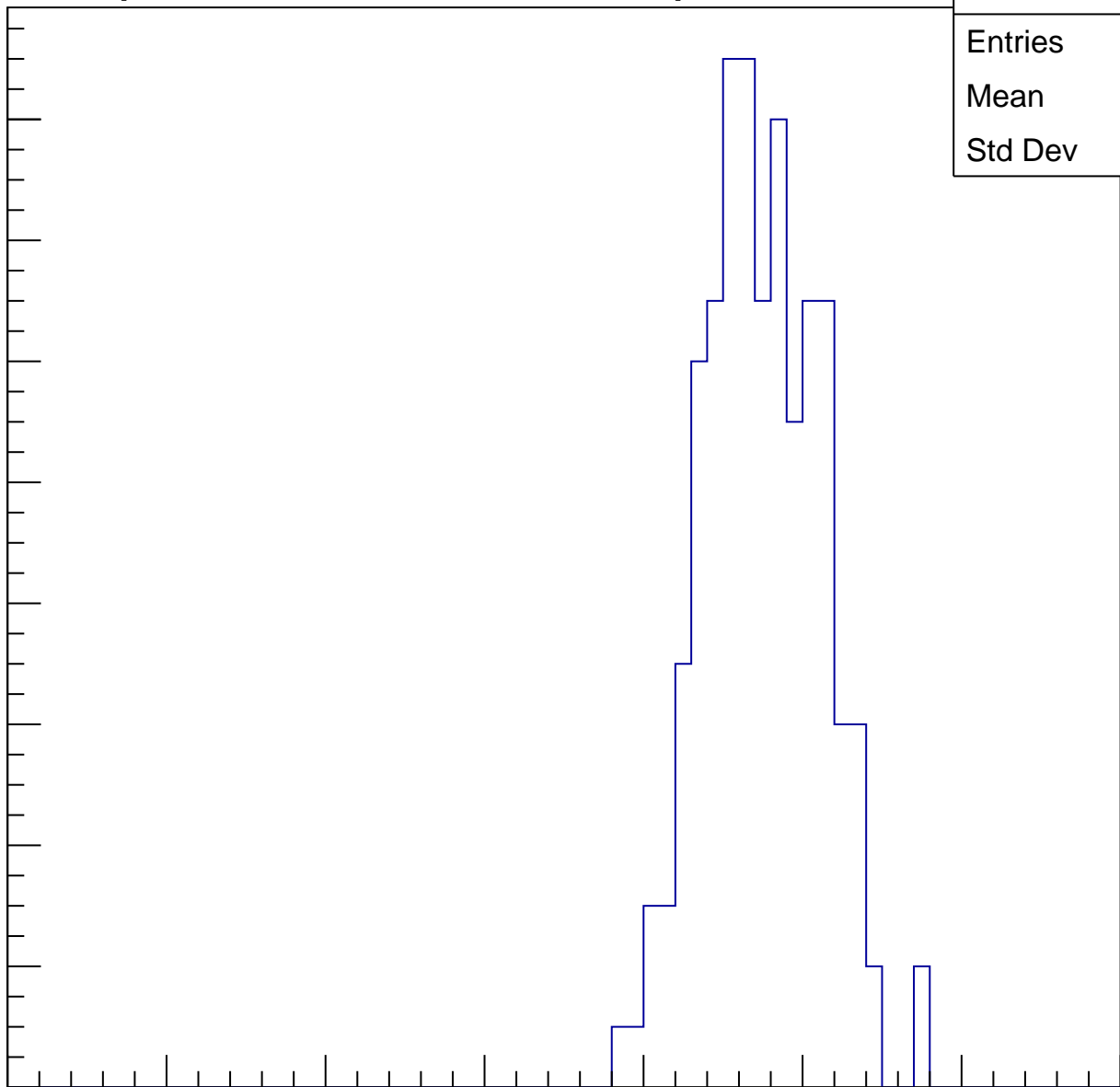
40

50

60

ampl

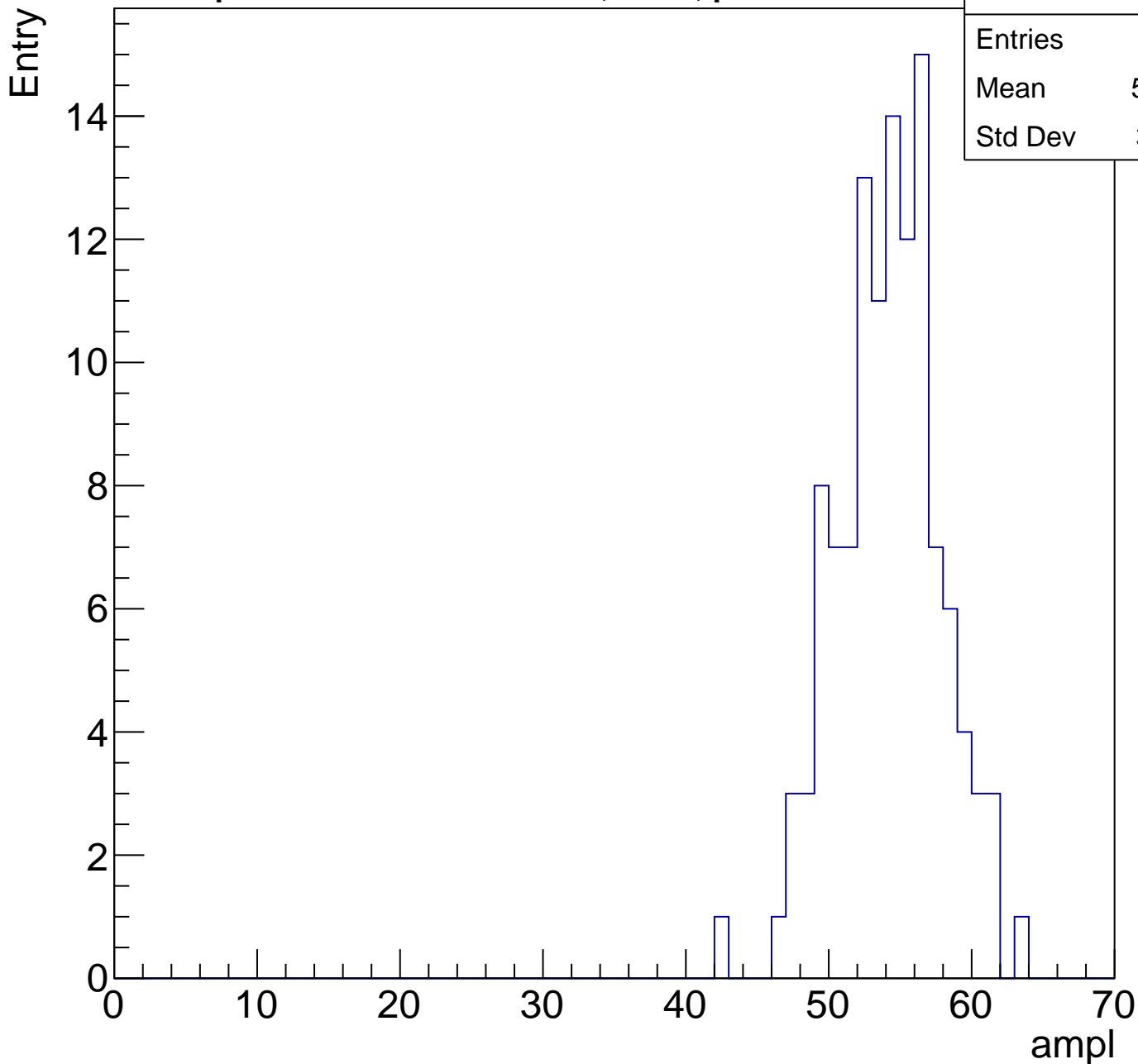
Entries	156
Mean	47
Std Dev	3.597



B0L101S, U18-ch24, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	53.75
Std Dev	3.621



B0L101S, U18-ch24, adc5

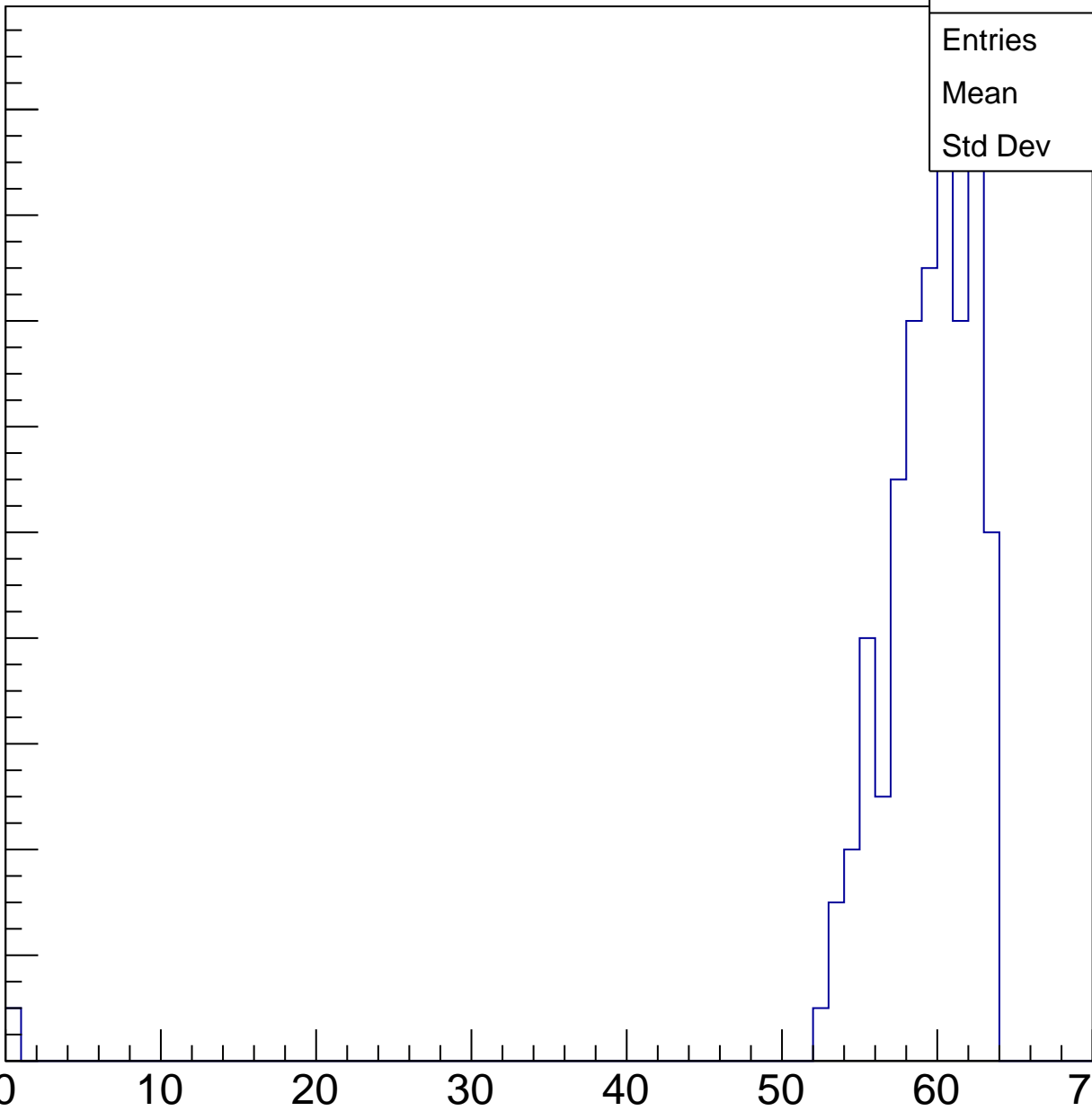
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	122
Mean	58.63
Std Dev	5.976

ampl

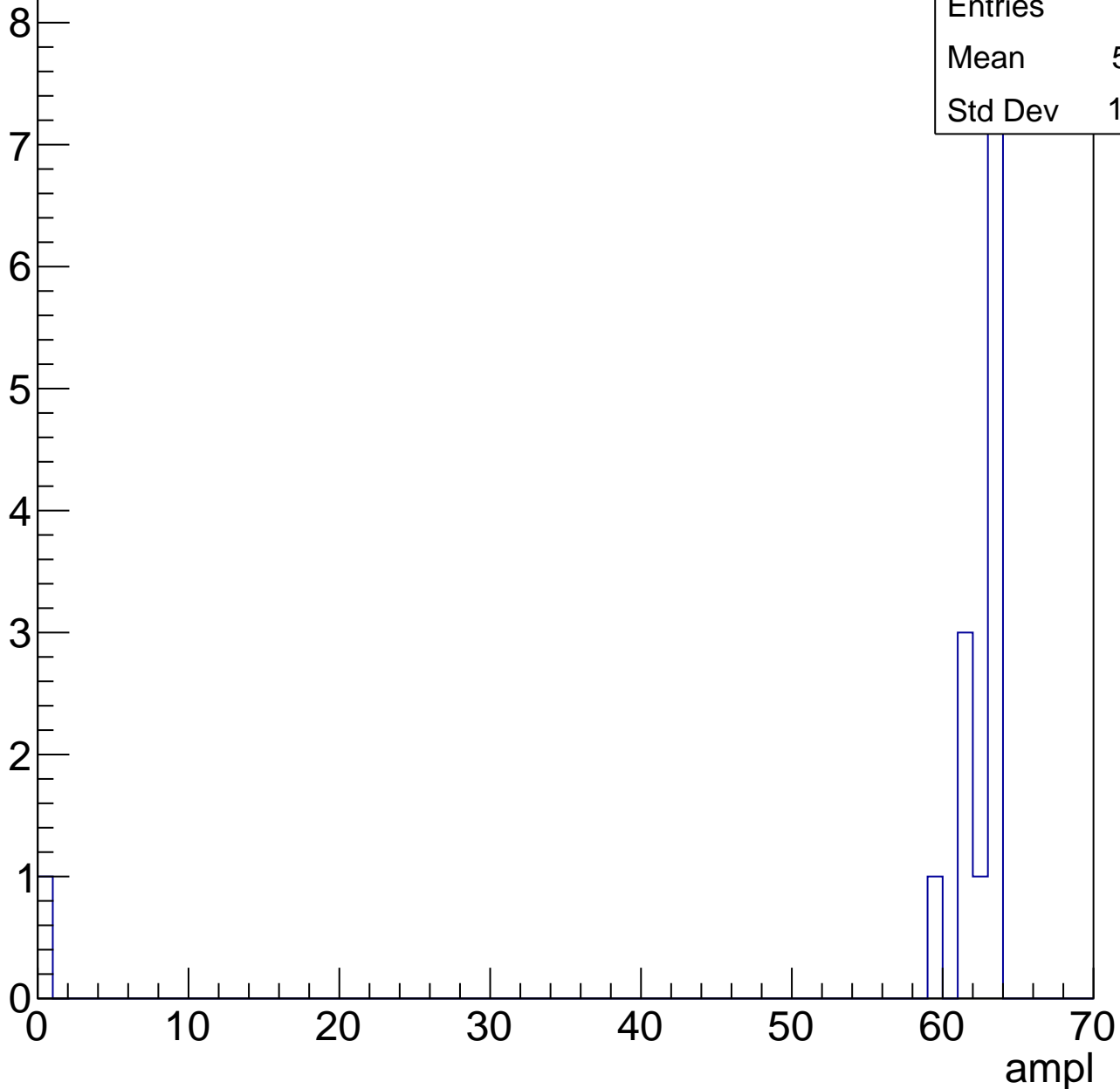


B0L101S, U18-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.71
Std Dev	16.05



B0L101S, U18-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

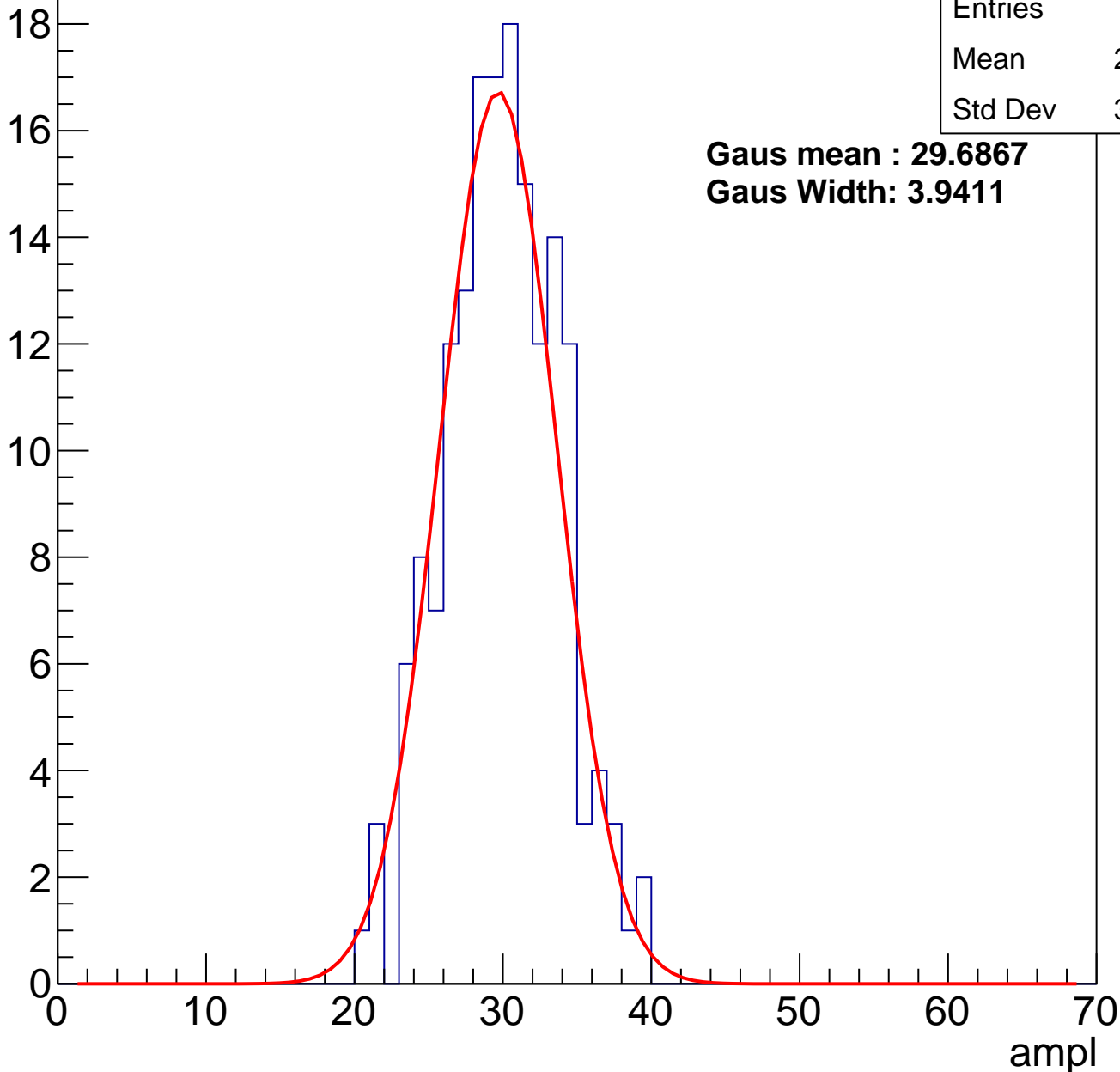


Entries	3
Mean	21
Std Dev	29.7

B0L101S, U18-ch25, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



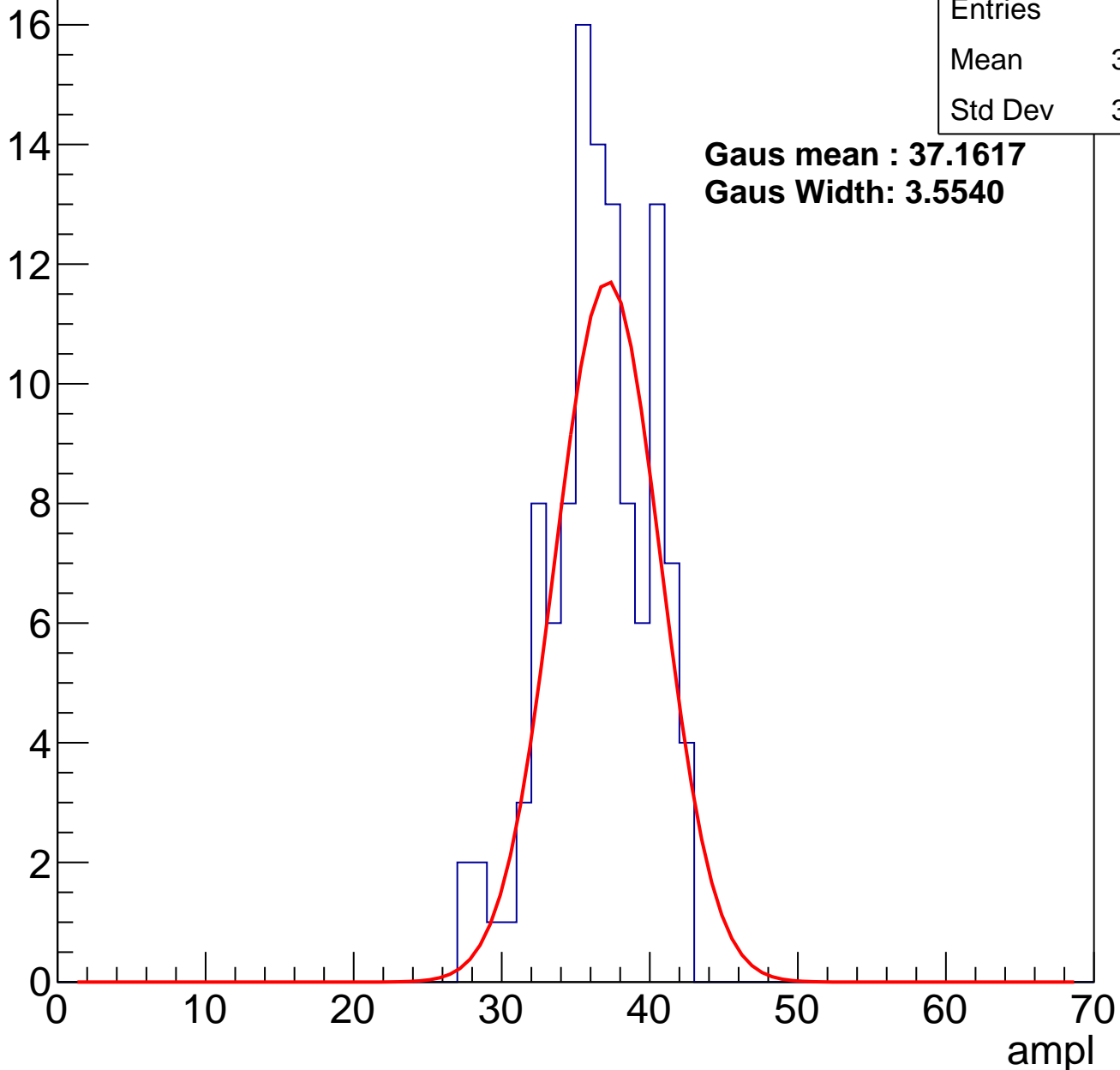
B0L101S, U18-ch25, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	36.12
Std Dev	3.412

Gaus mean : 37.1617
Gaus Width: 3.5540

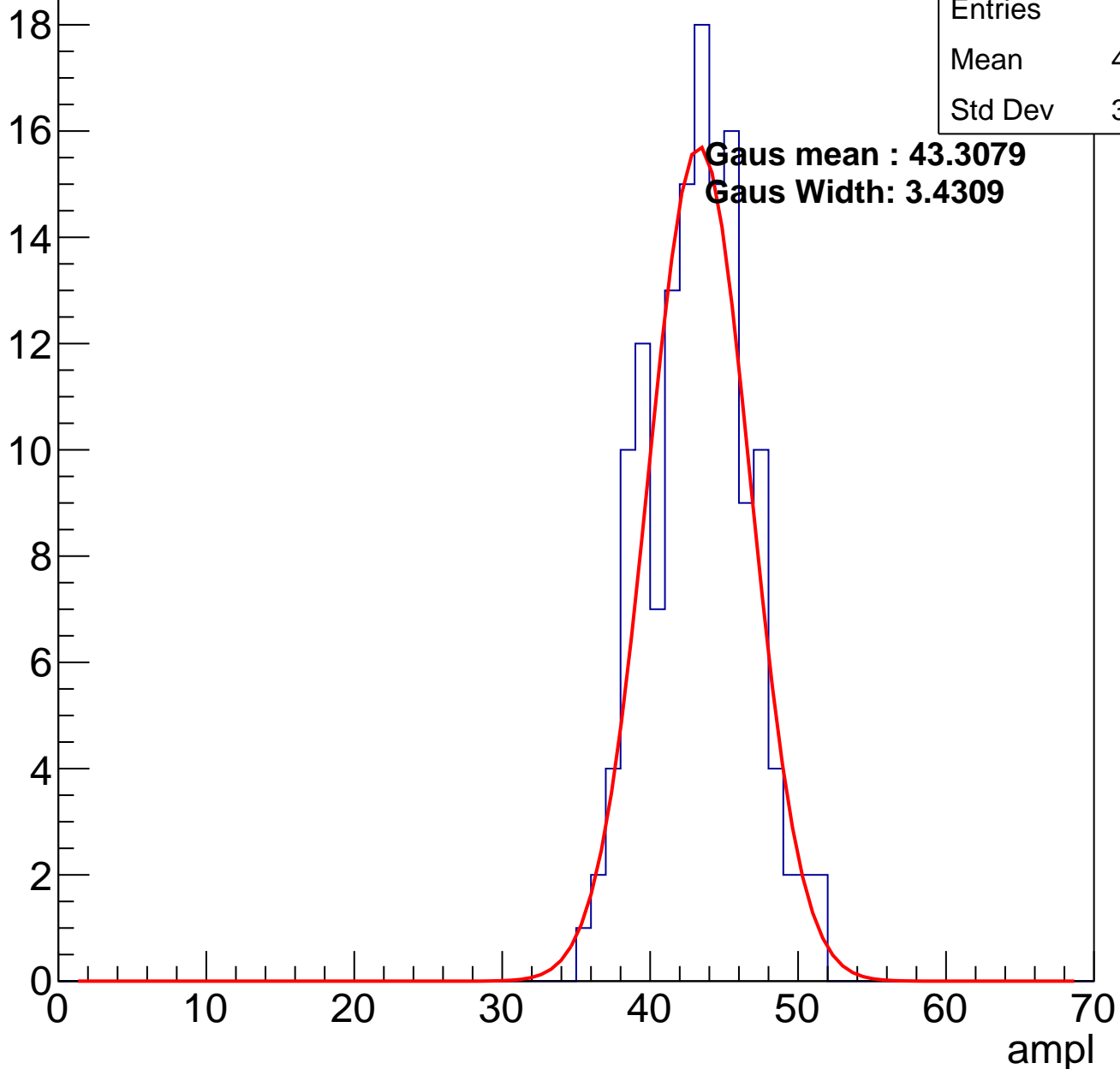
Entry



B0L101S, U18-ch25, adc2

calib_packv5_042523_0143.root, FC#1, port C1

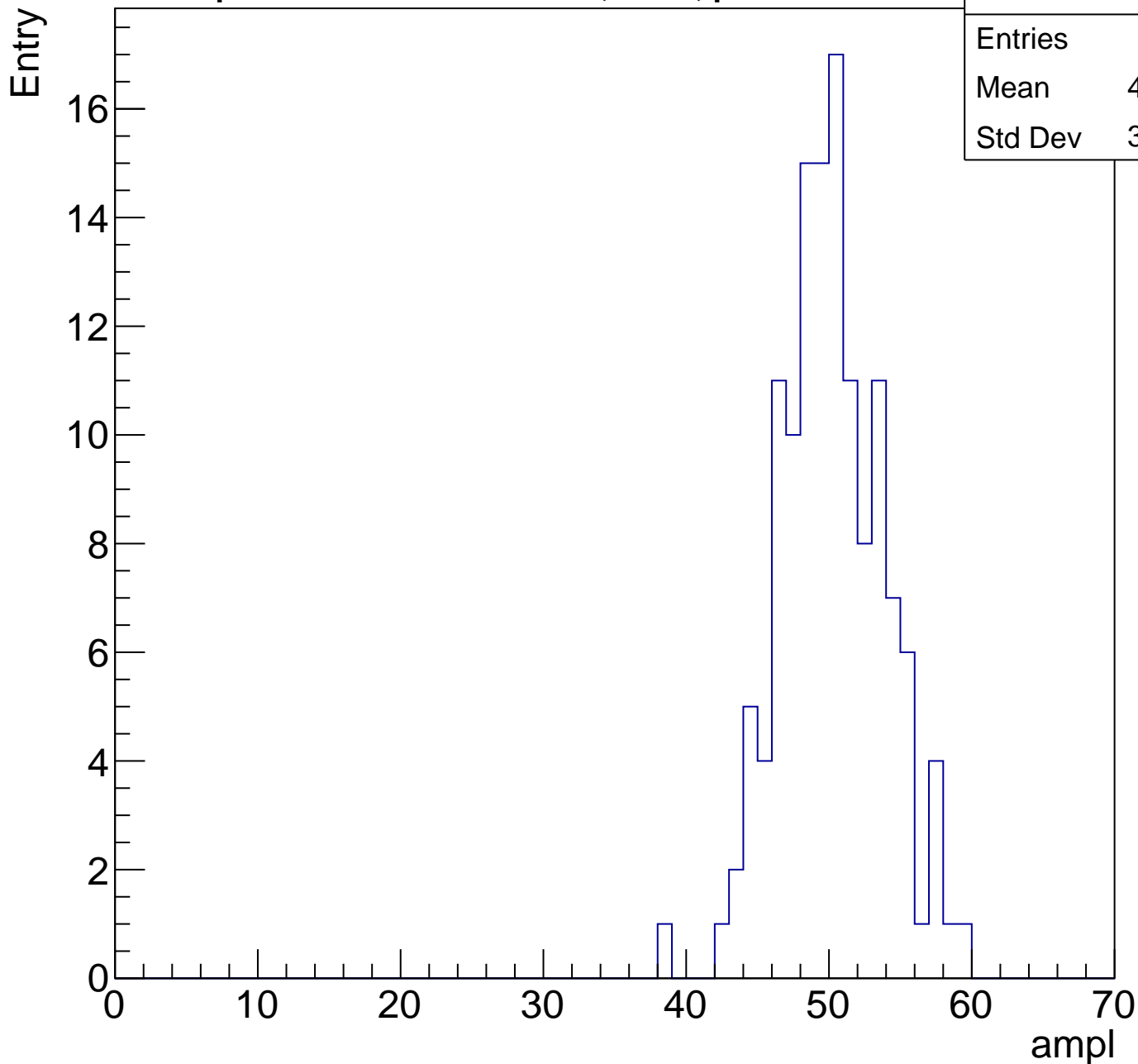
Entry



B0L101S, U18-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	49.74
Std Dev	3.602



B0L101S, U18-ch25, adc4

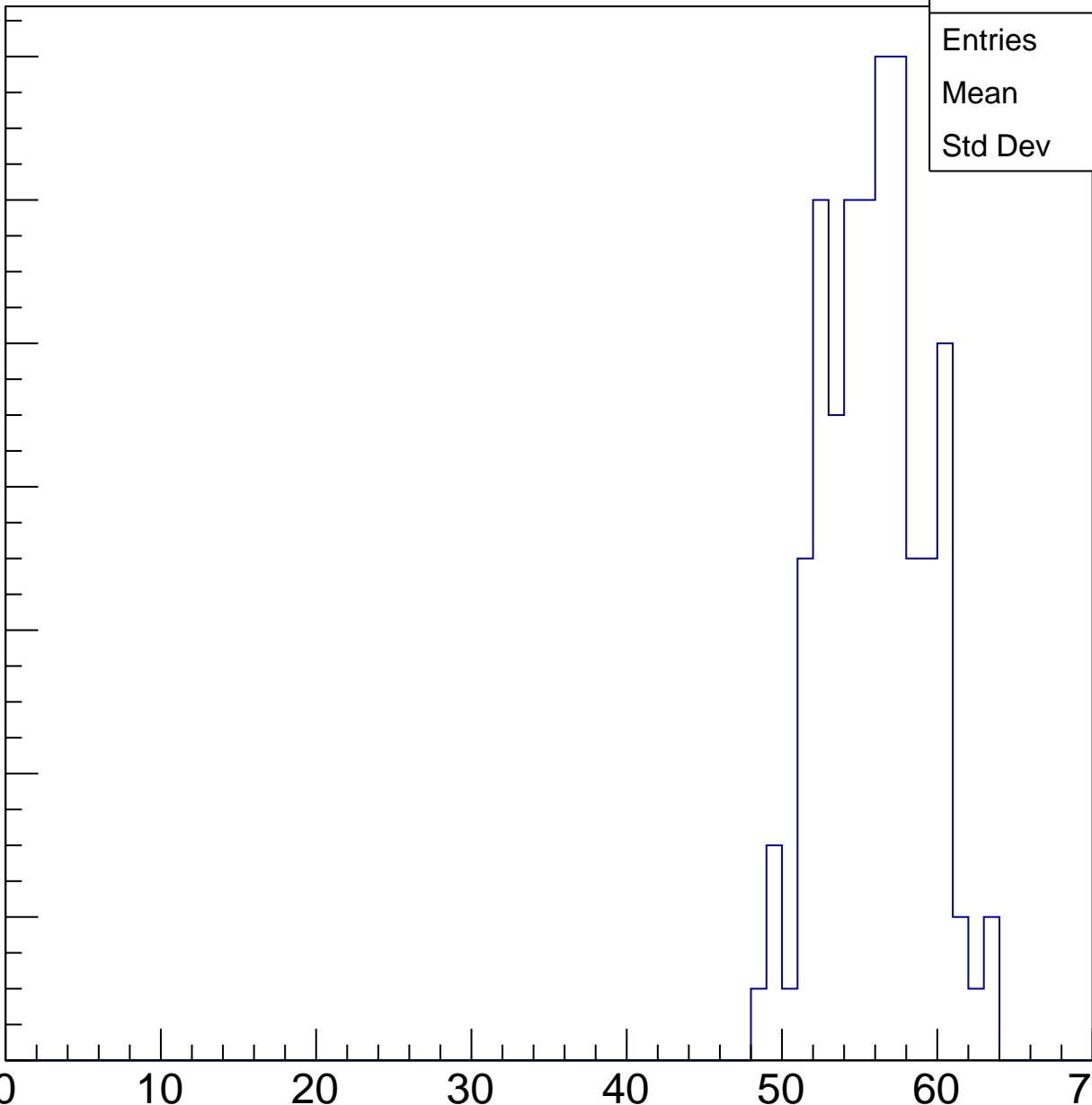
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	55.46
Std Dev	3.204

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch25, adc5

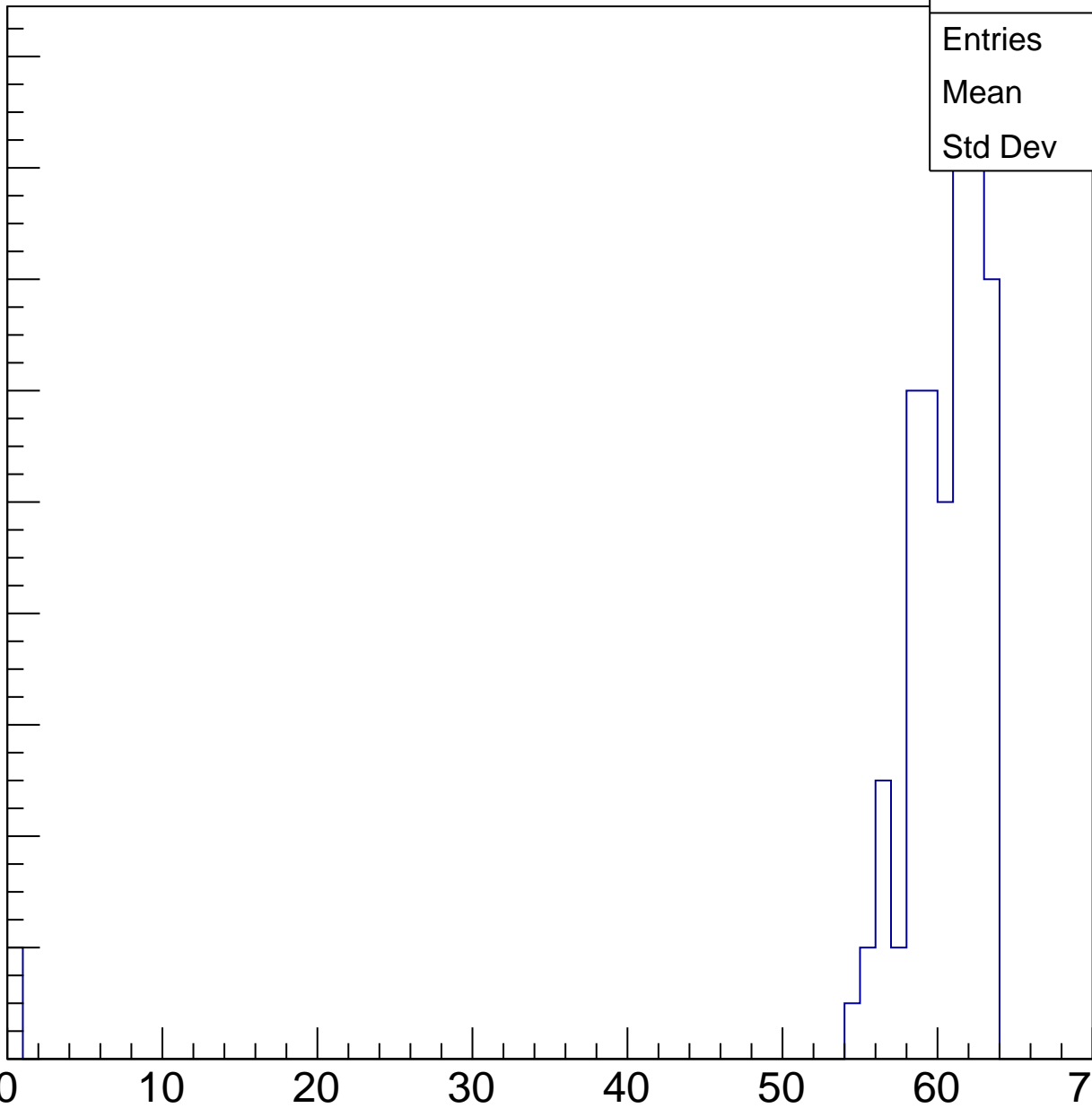
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.92
Std Dev	8.913

ampl



B0L101S, U18-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch26, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	30.45
Std Dev	4.529

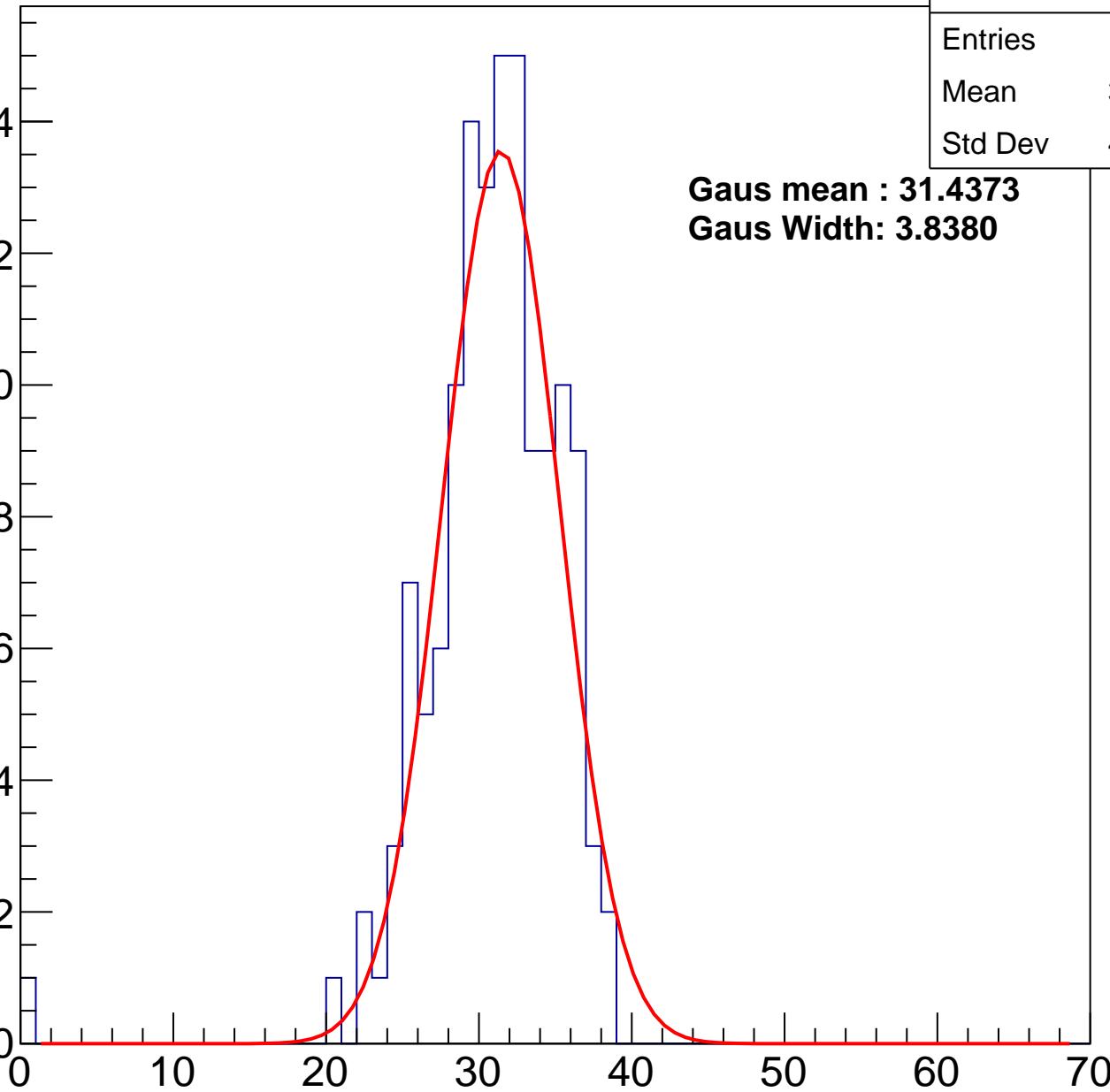
Gaus mean : 31.4373

Gaus Width: 3.8380

Entry

14
12
10
8
6
4
2
0

ampl

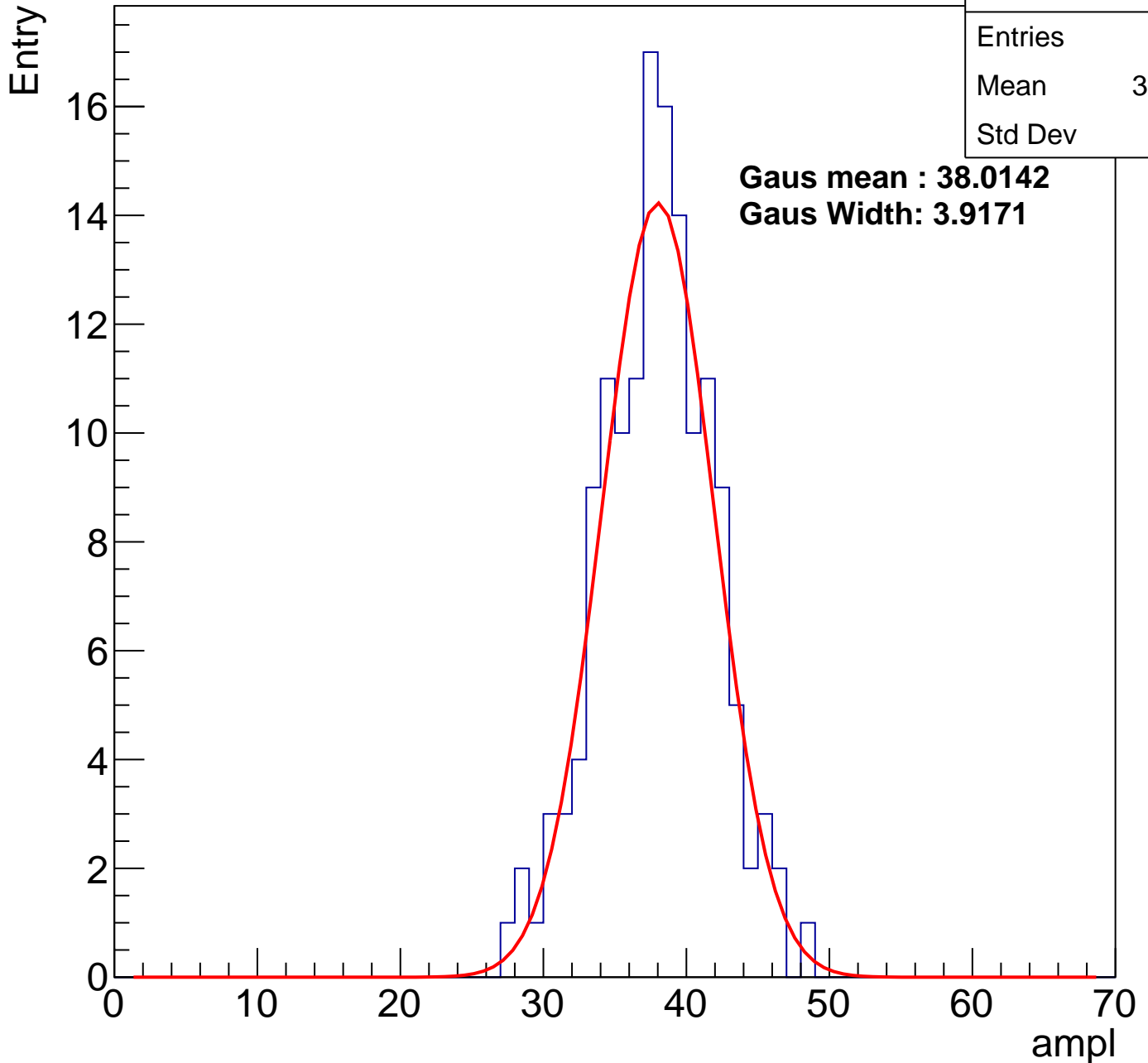


B0L101S, U18-ch26, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	37.45
Std Dev	3.95

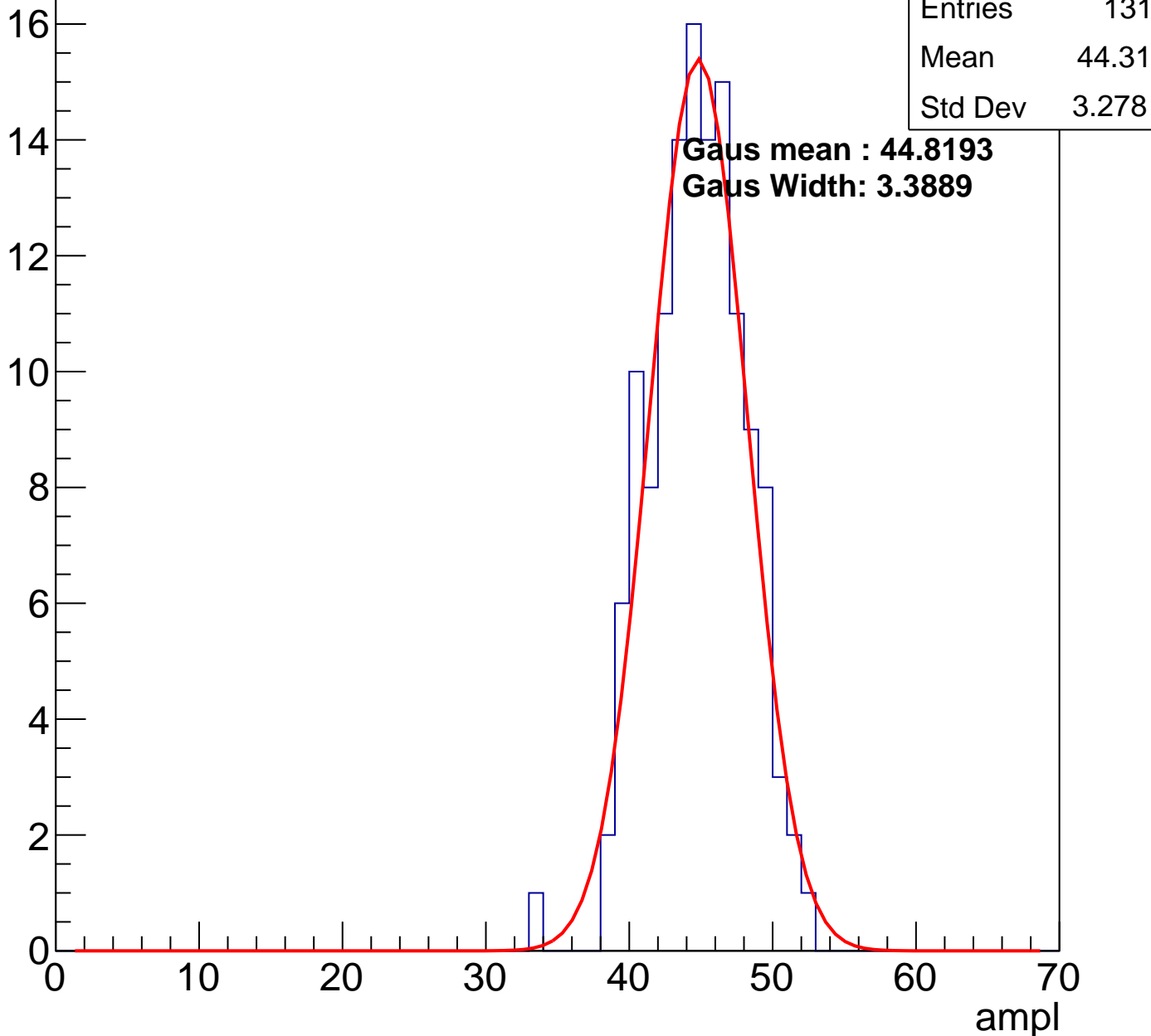
Gaus mean : 38.0142
Gaus Width: 3.9171



B0L101S, U18-ch26, adc2

calib_packv5_042523_0143.root, FC#1, port C1

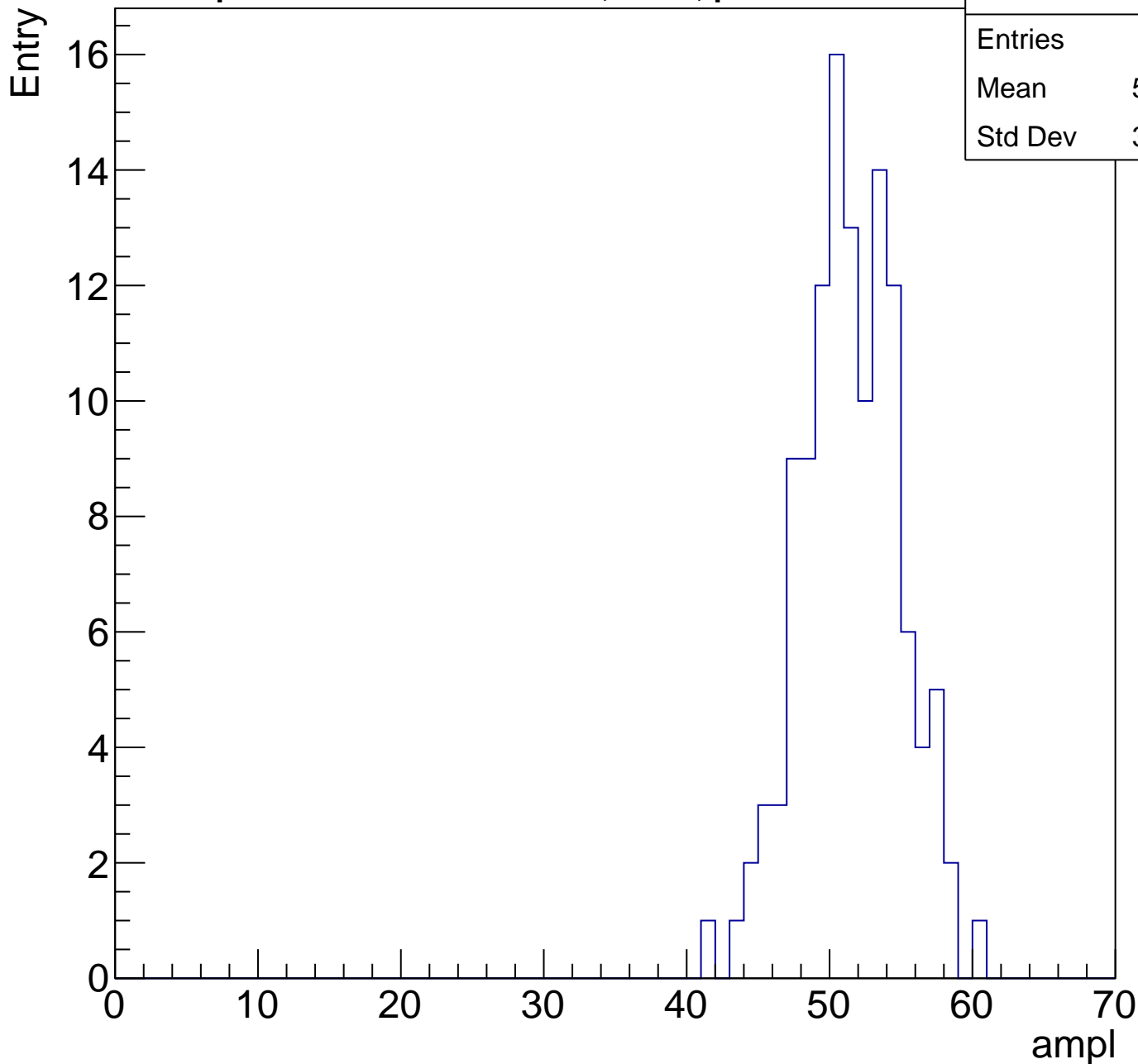
Entry



B0L101S, U18-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	51.02
Std Dev	3.449



B0L101S, U18-ch26, adc4

calib_packv5_042523_0143.root, FC#1, port C1

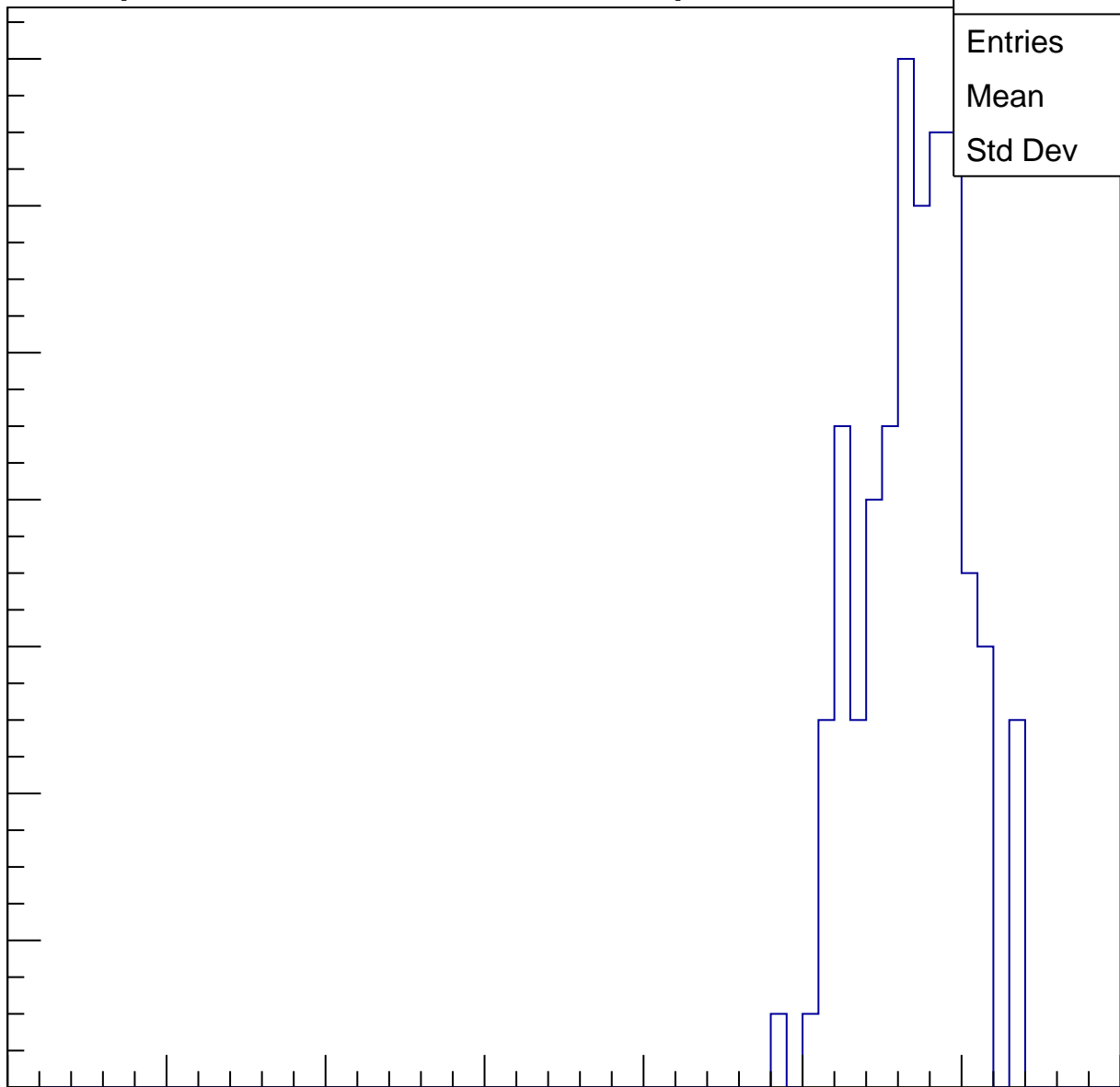
Entries	108
Mean	56.51
Std Dev	3.213

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch26, adc5

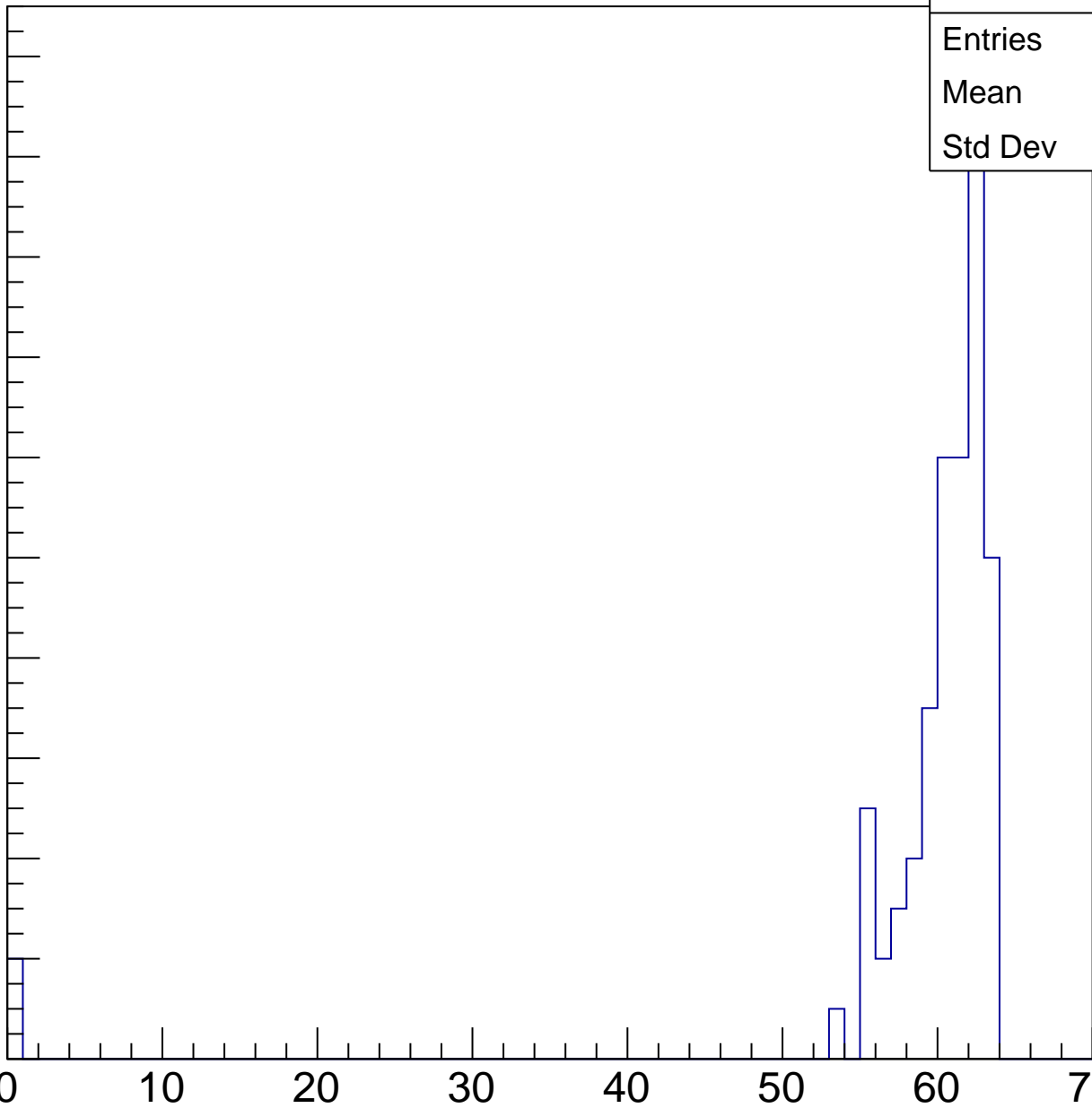
calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	78
Mean	58.69
Std Dev	9.809

ampl

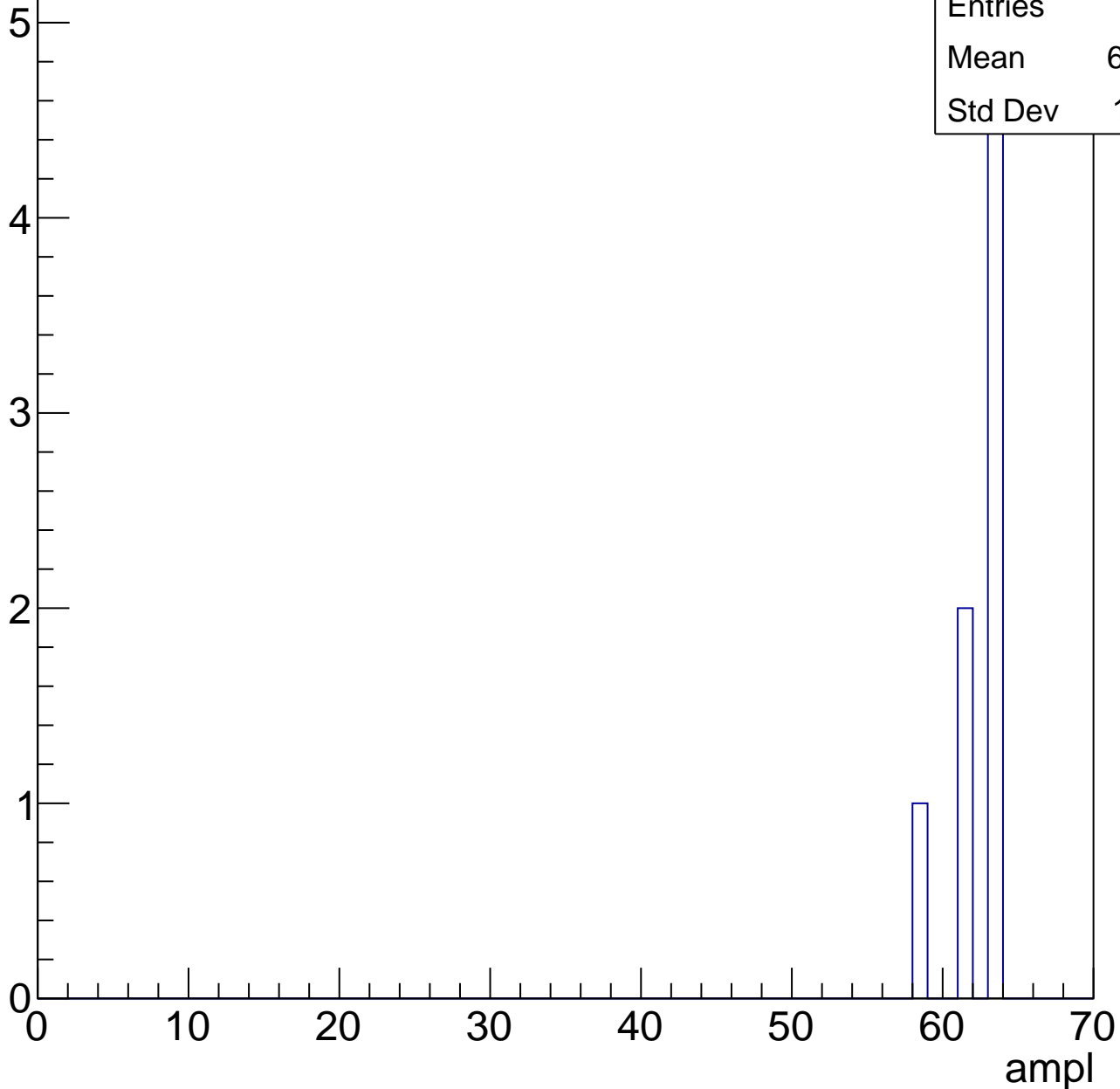


B0L101S, U18-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	61.88
Std Dev	1.691



B0L101S, U18-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

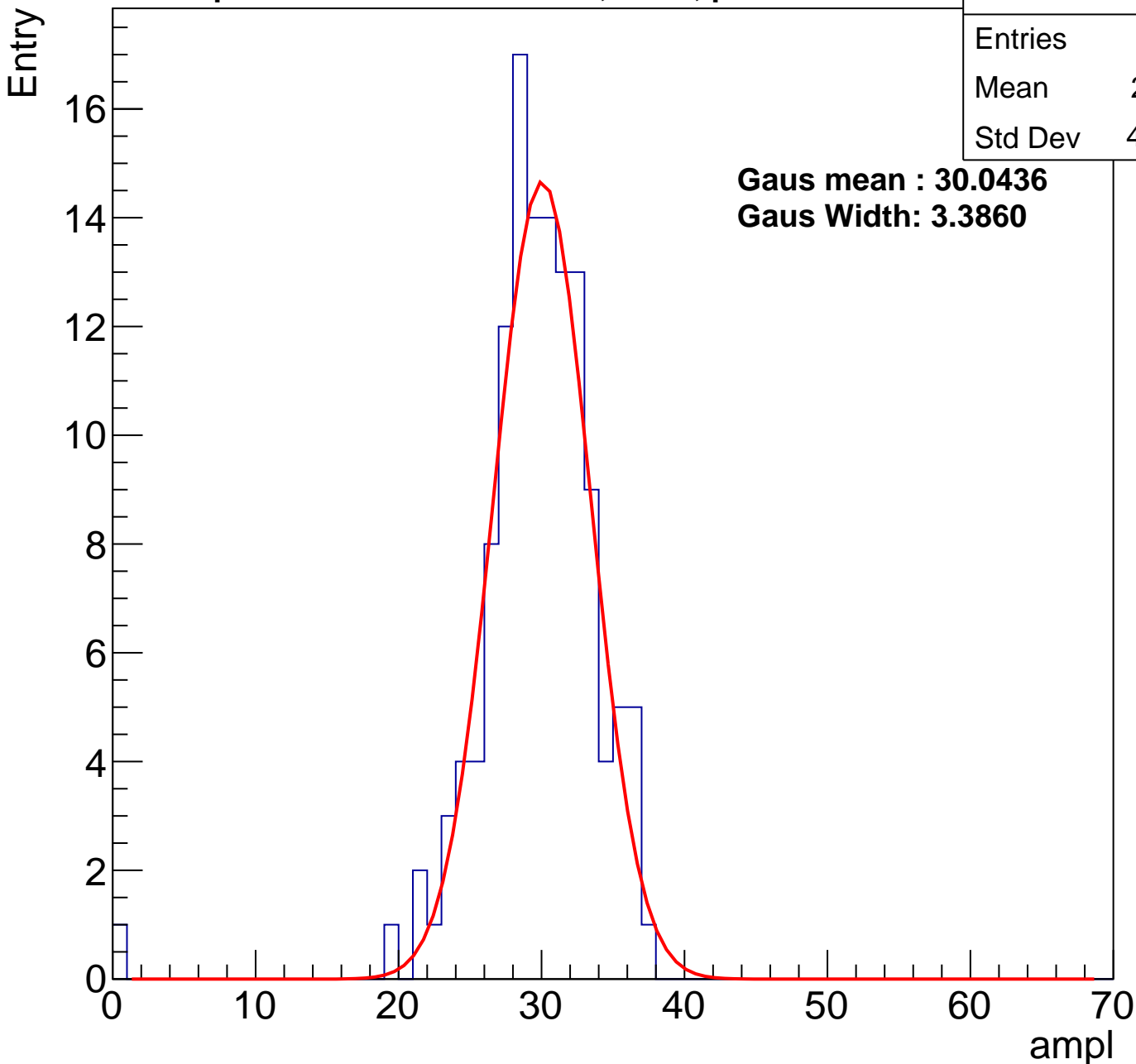
B0L101S, U18-ch27, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.21
Std Dev	4.314

Gaus mean : 30.0436

Gaus Width: 3.3860

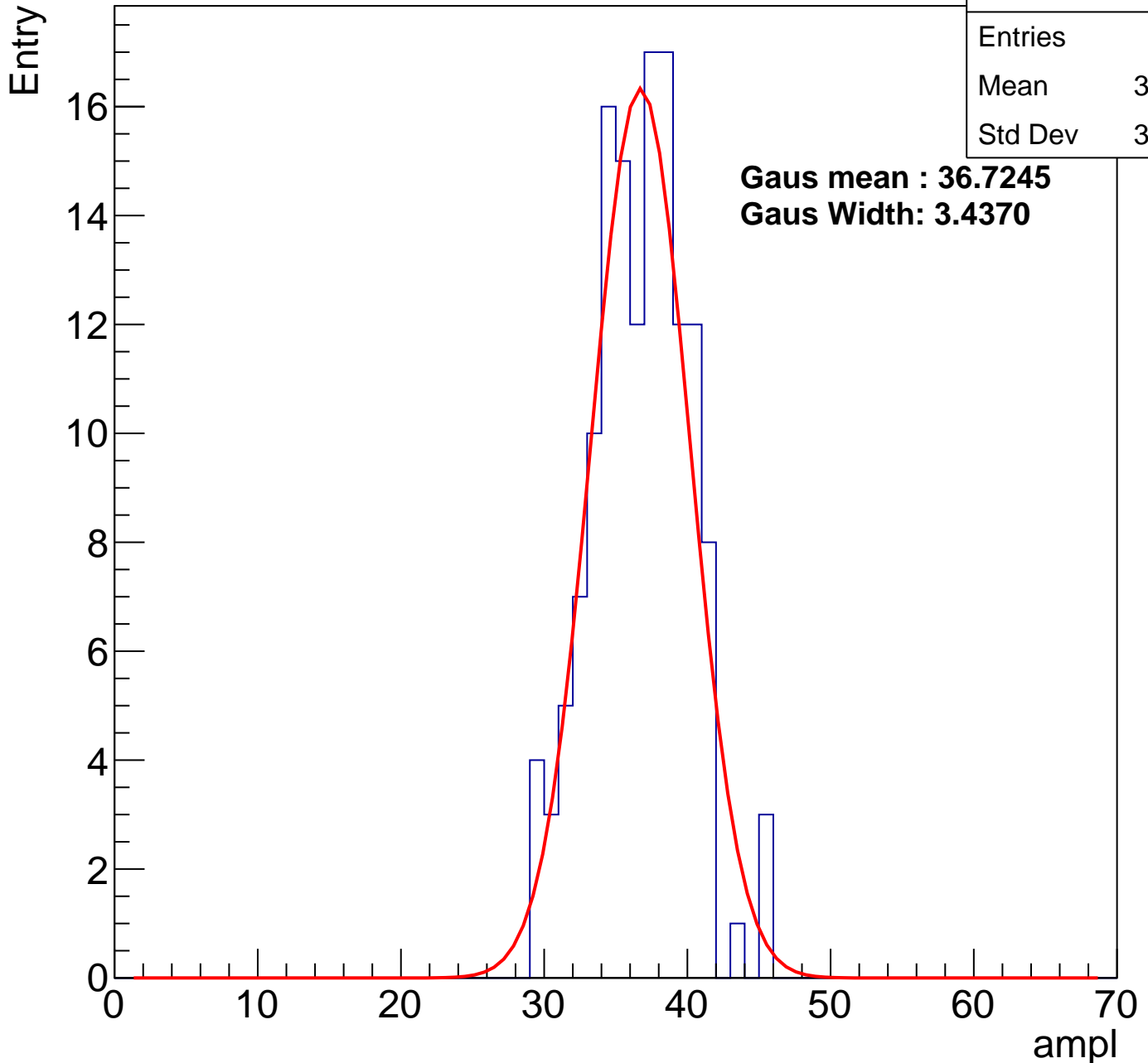


B0L101S, U18-ch27, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	36.23
Std Dev	3.335

Gaus mean : 36.7245
Gaus Width: 3.4370



B0L101S, U18-ch27, adc2

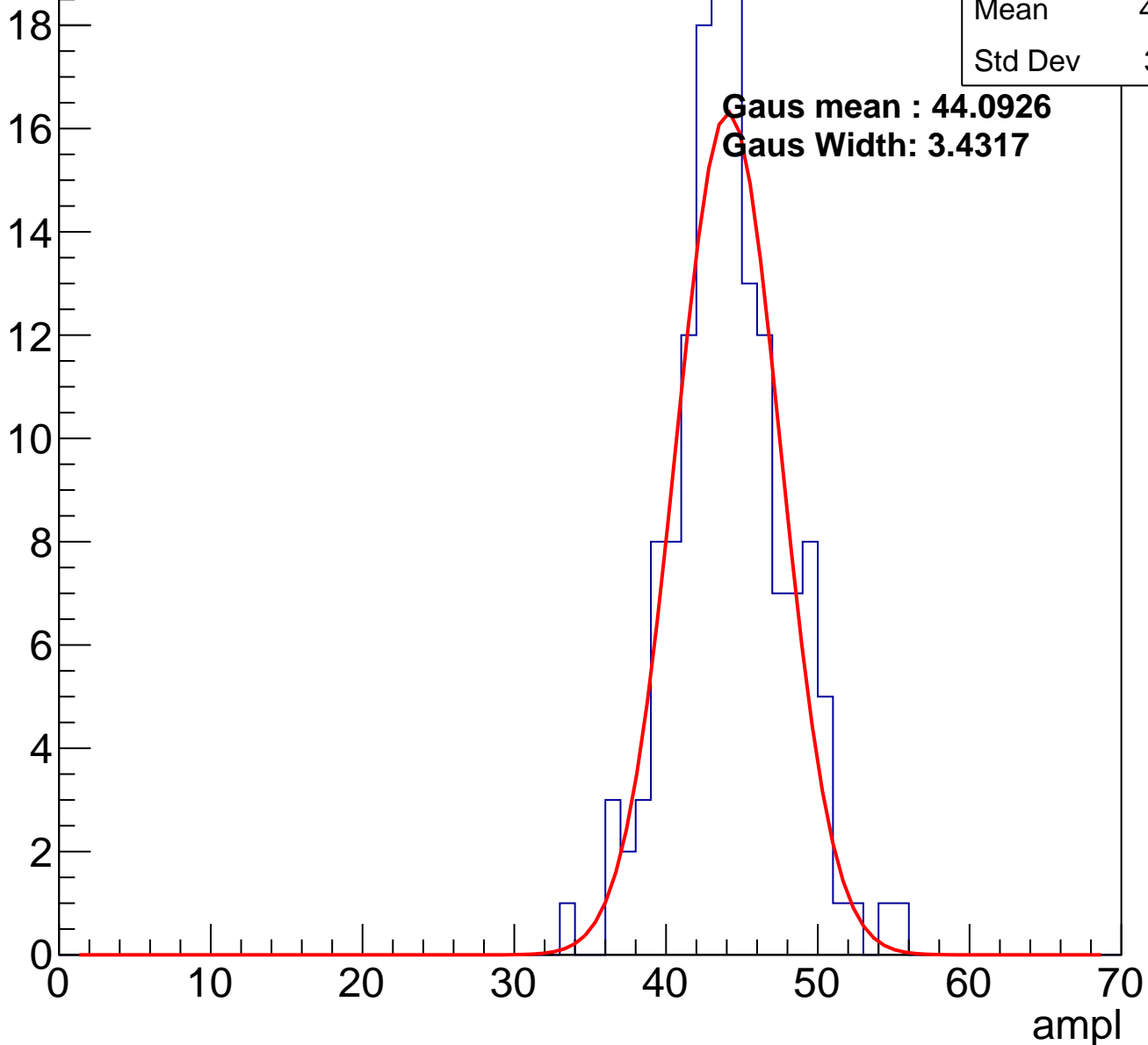
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	43.74
Std Dev	3.651

Gaus mean : 44.0926

Gaus Width: 3.4317

Entry



B0L101S, U18-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

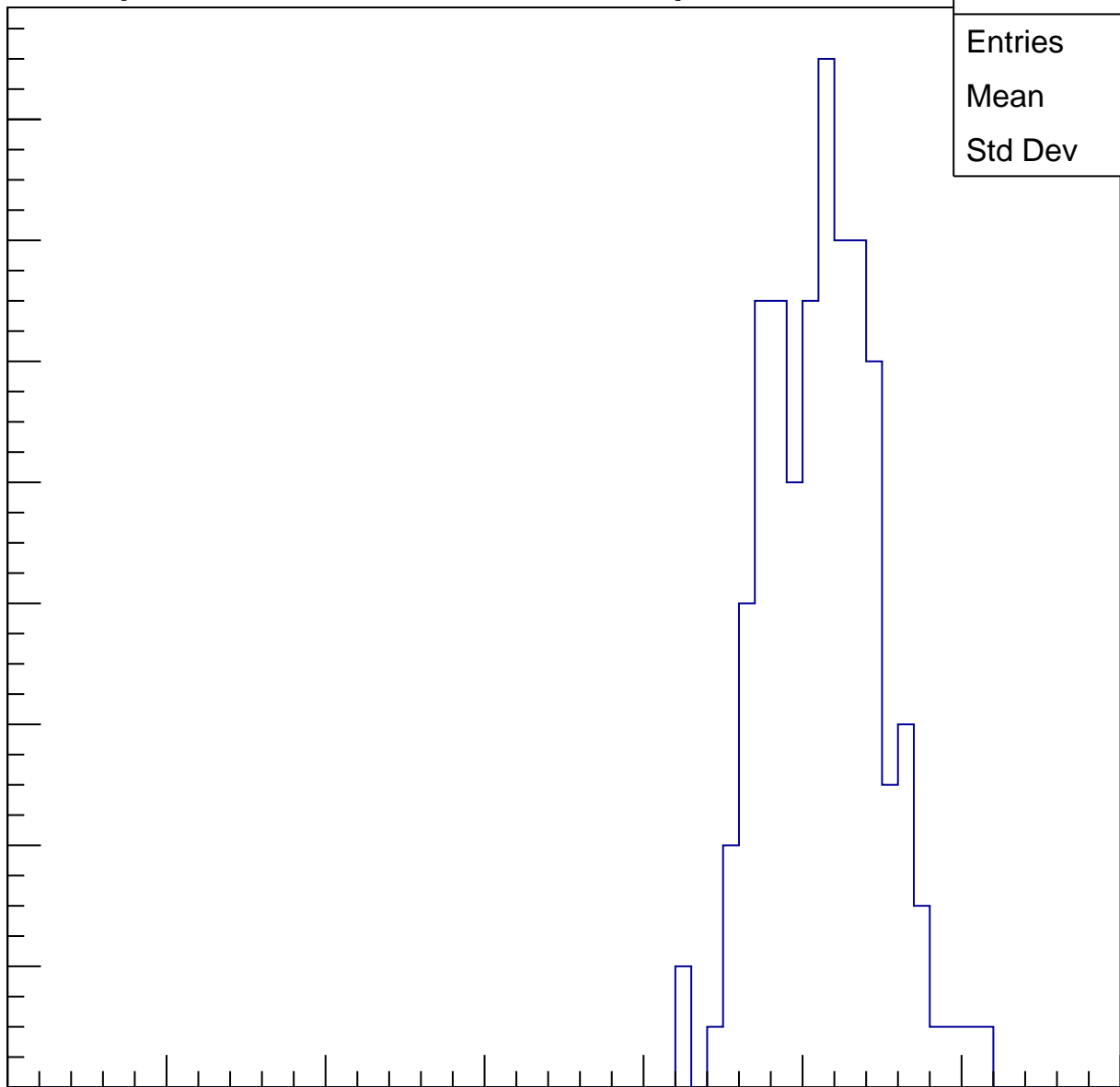
Entries	139
Mean	50.76
Std Dev	3.533

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch27, adc4

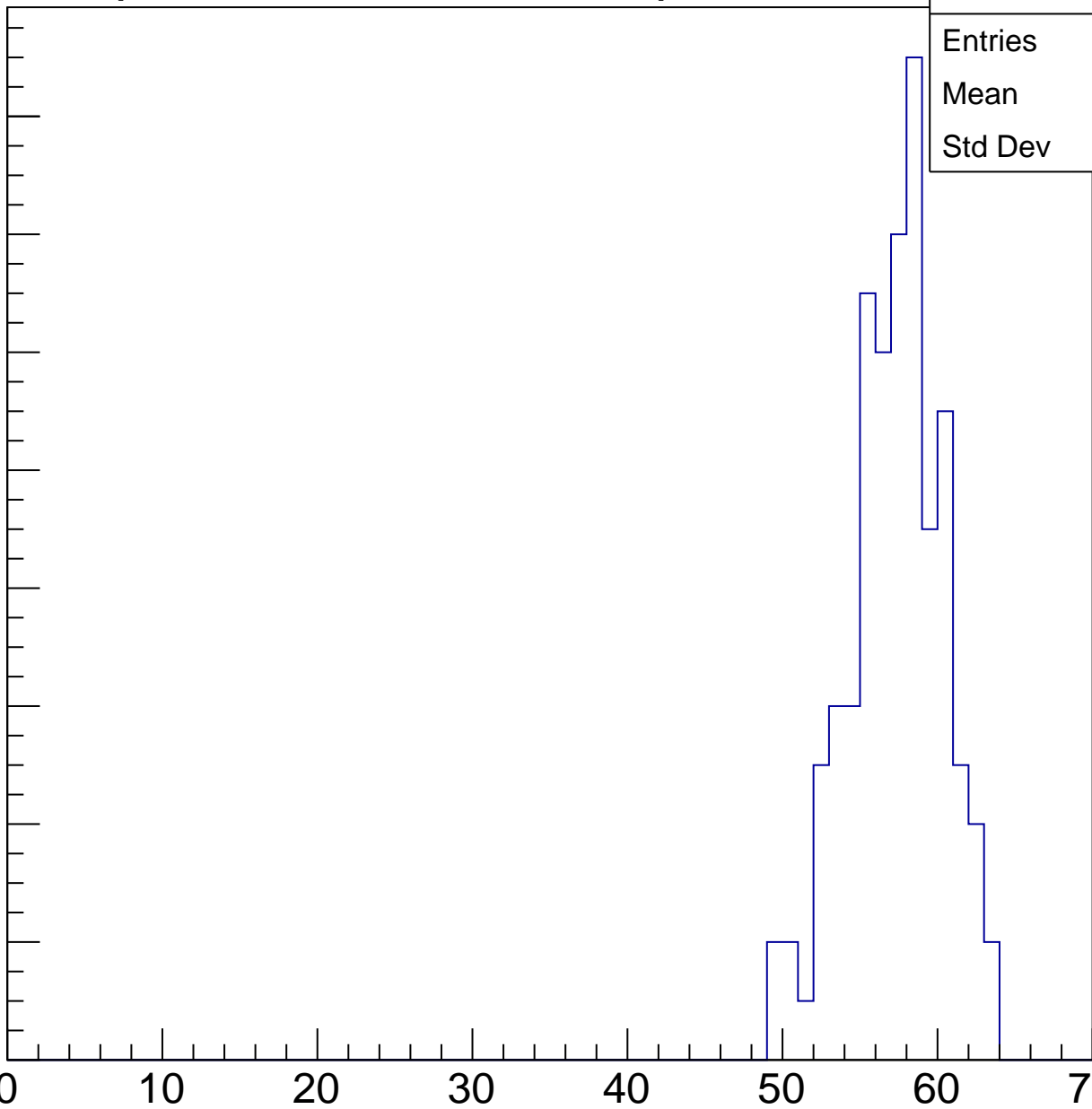
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	56.81
Std Dev	3.03

ampl



B0L101S, U18-ch27, adc5

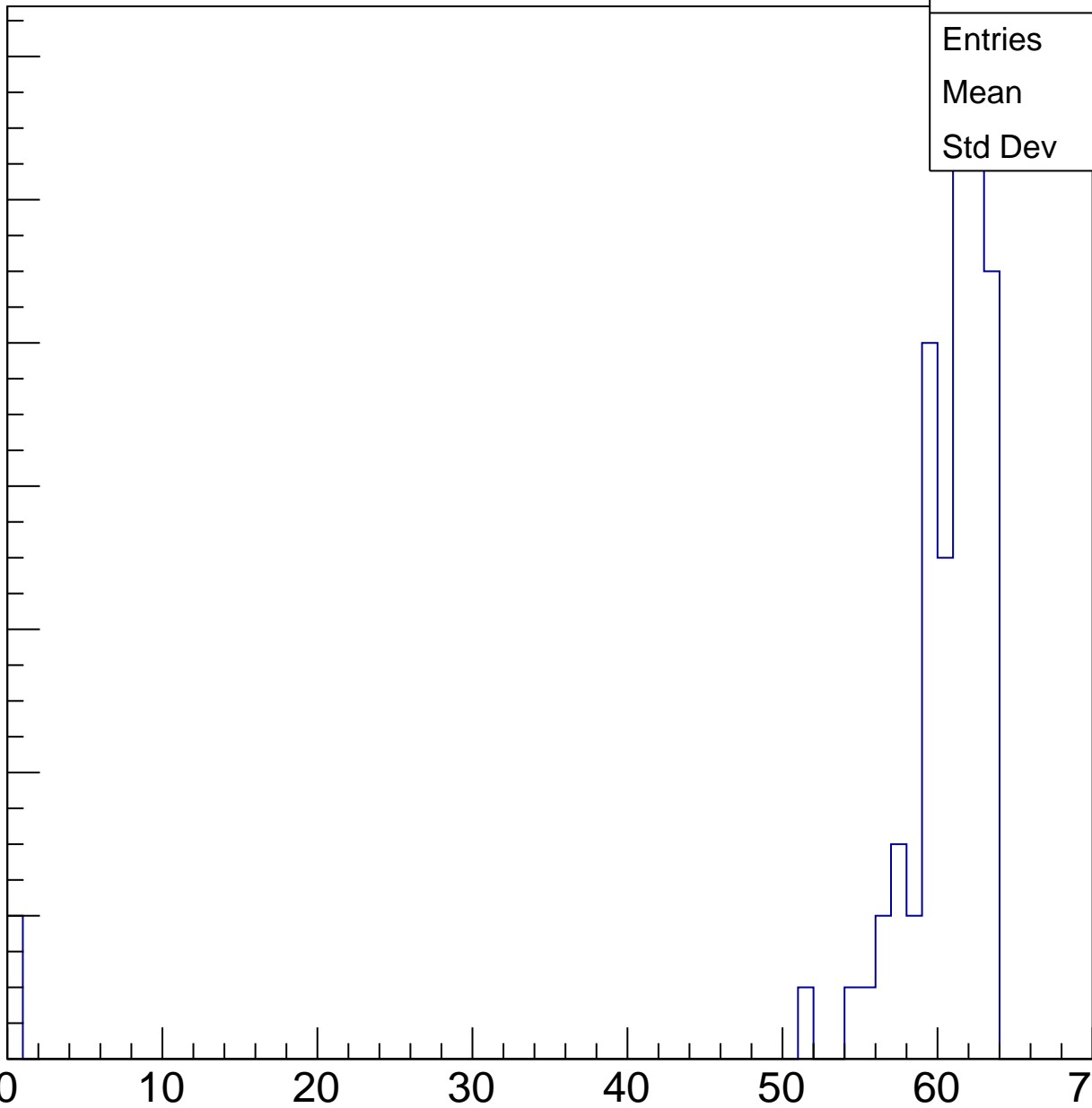
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	58.59
Std Dev	10.47

ampl

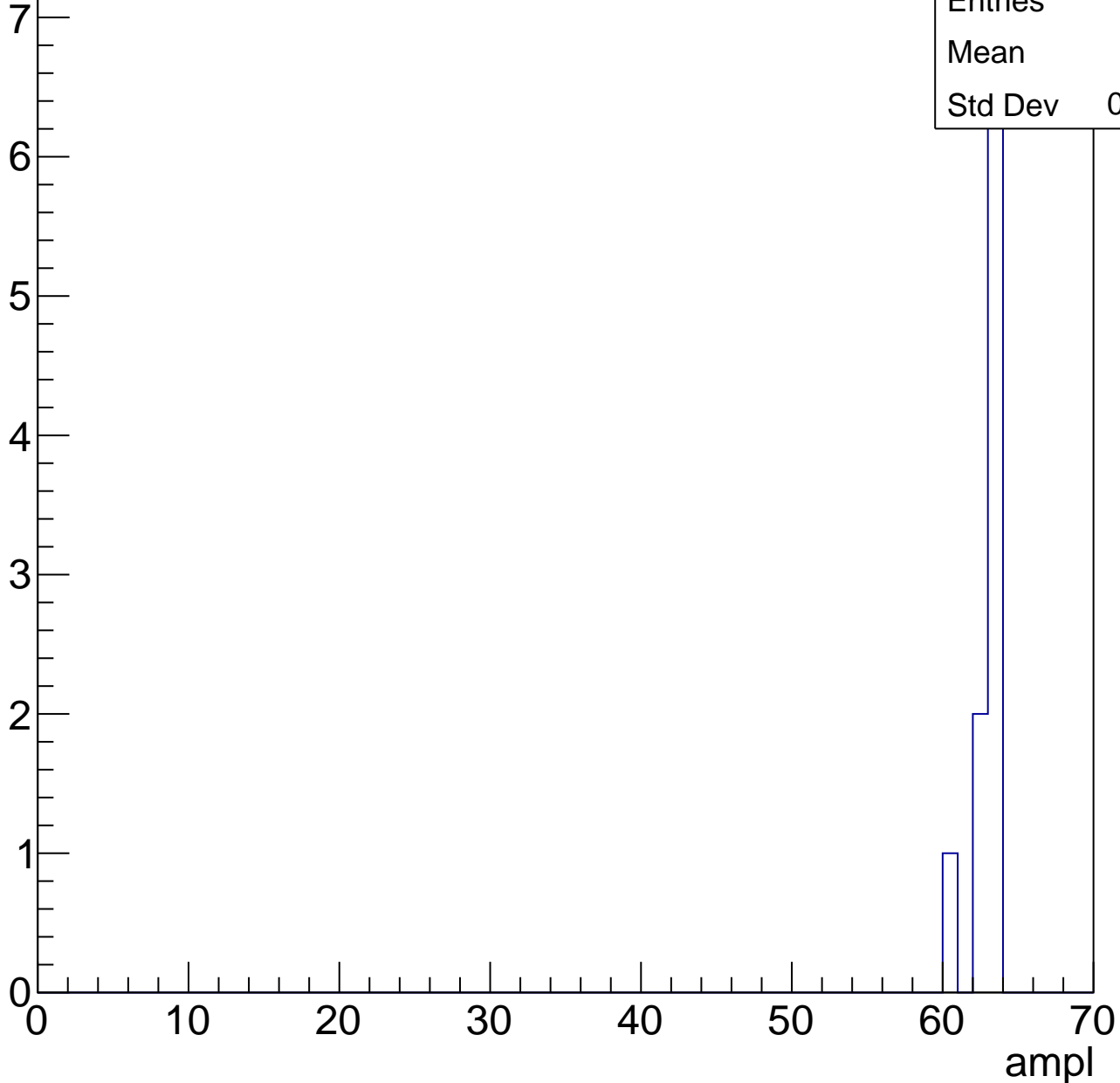


B0L101S, U18-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.5
Std Dev	0.922



B0L101S, U18-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch28, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	28.59
Std Dev	4.07

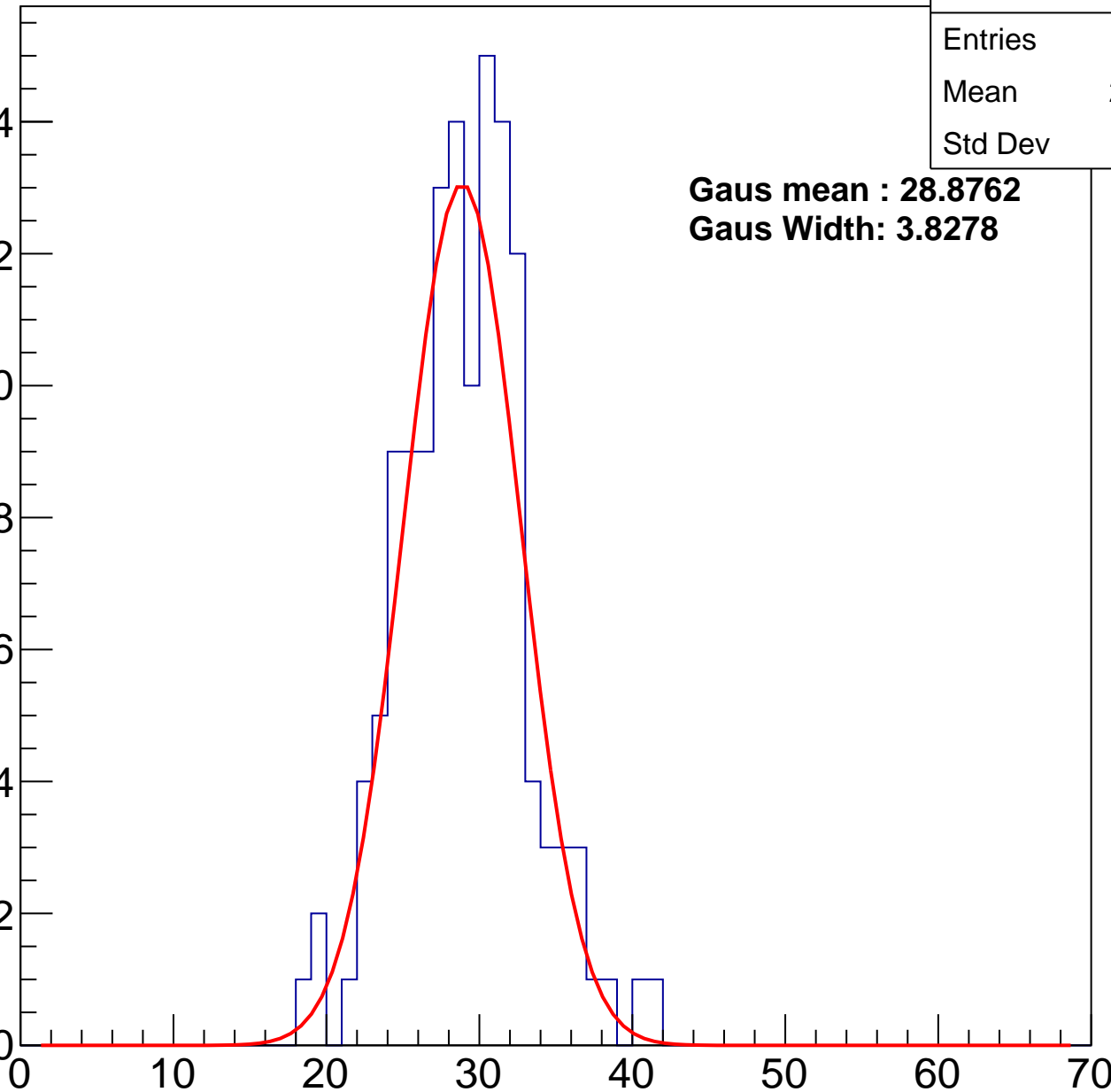
Gaus mean : 28.8762

Gaus Width: 3.8278

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch28, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	35.71
Std Dev	3.764

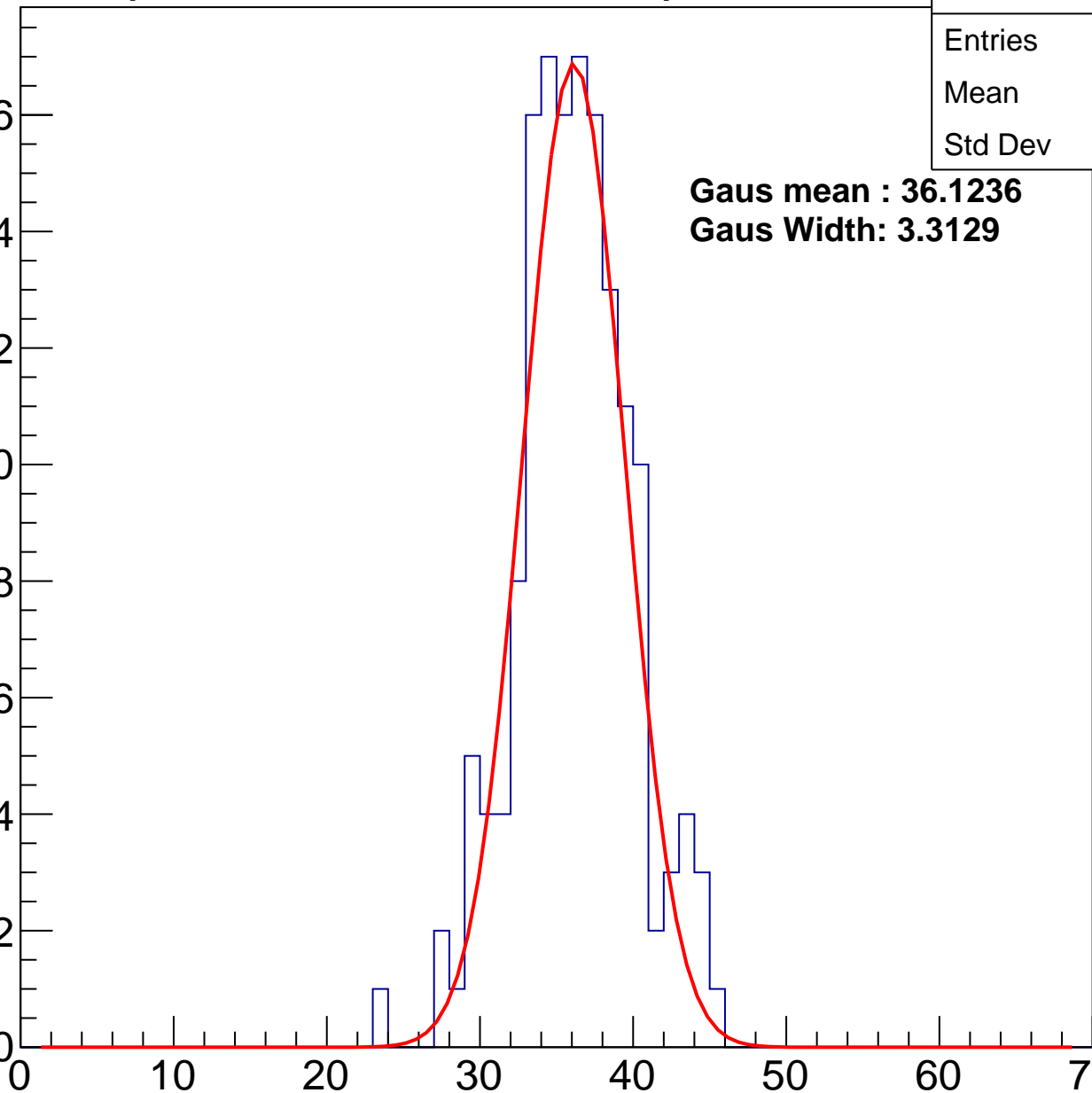
Gaus mean : 36.1236

Gaus Width: 3.3129

Entry

16
14
12
10
8
6
4
2
0

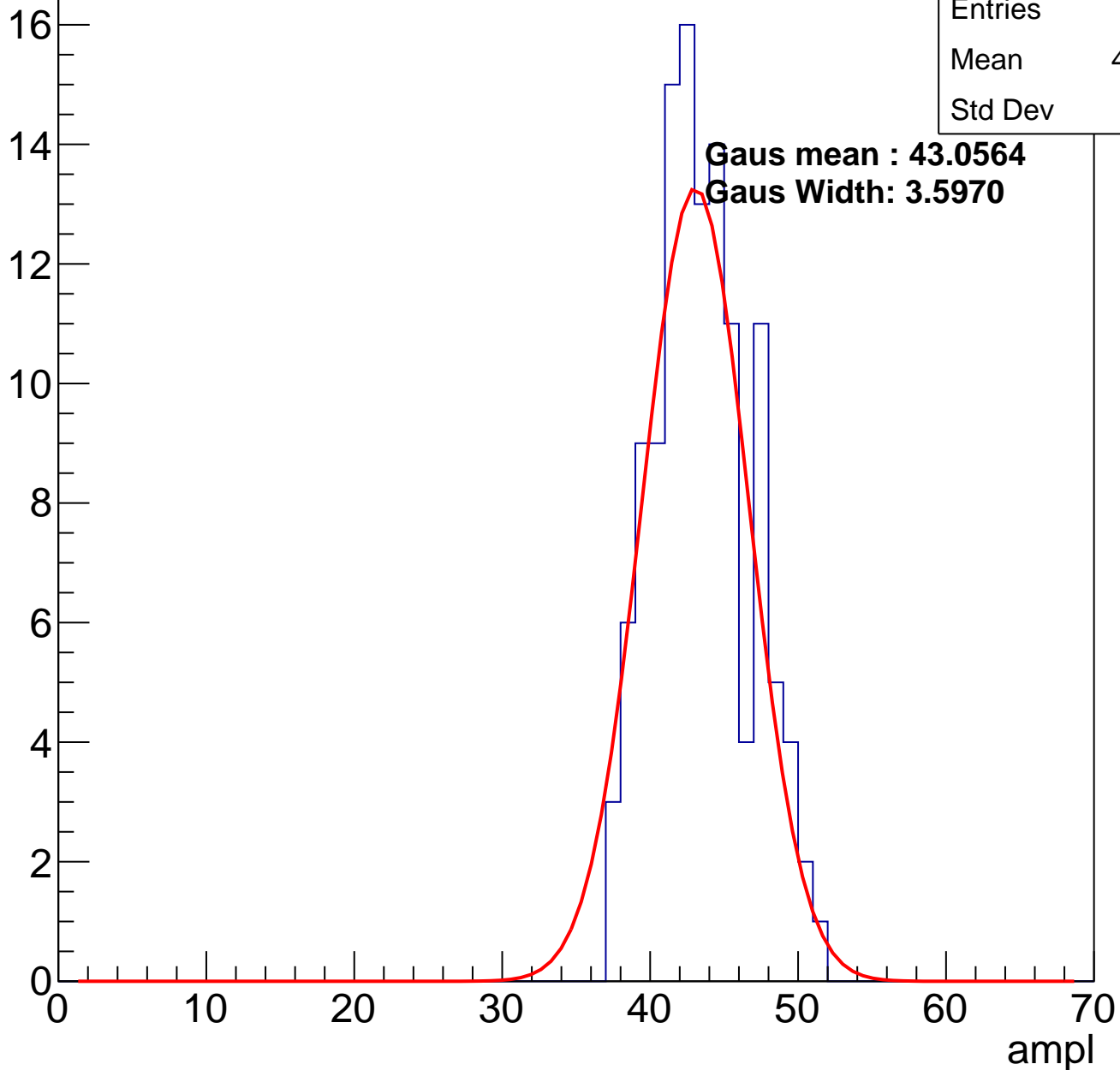
ampl



B0L101S, U18-ch28, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	49.76
Std Dev	3.686

Entry

16
14
12
10
8
6
4
2
0

0

10

20

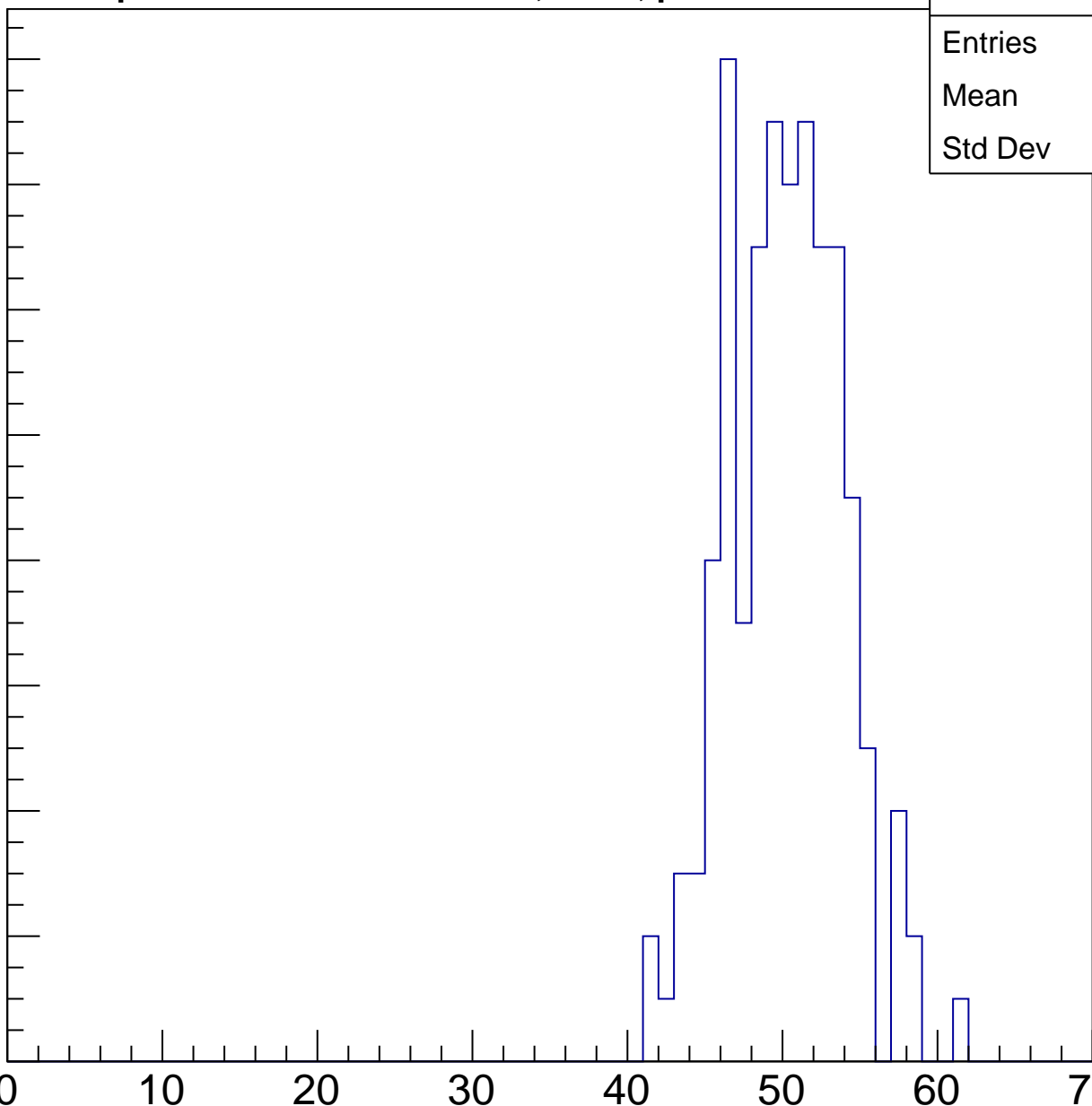
30

40

50

60

ampl



B0L101S, U18-ch28, adc4

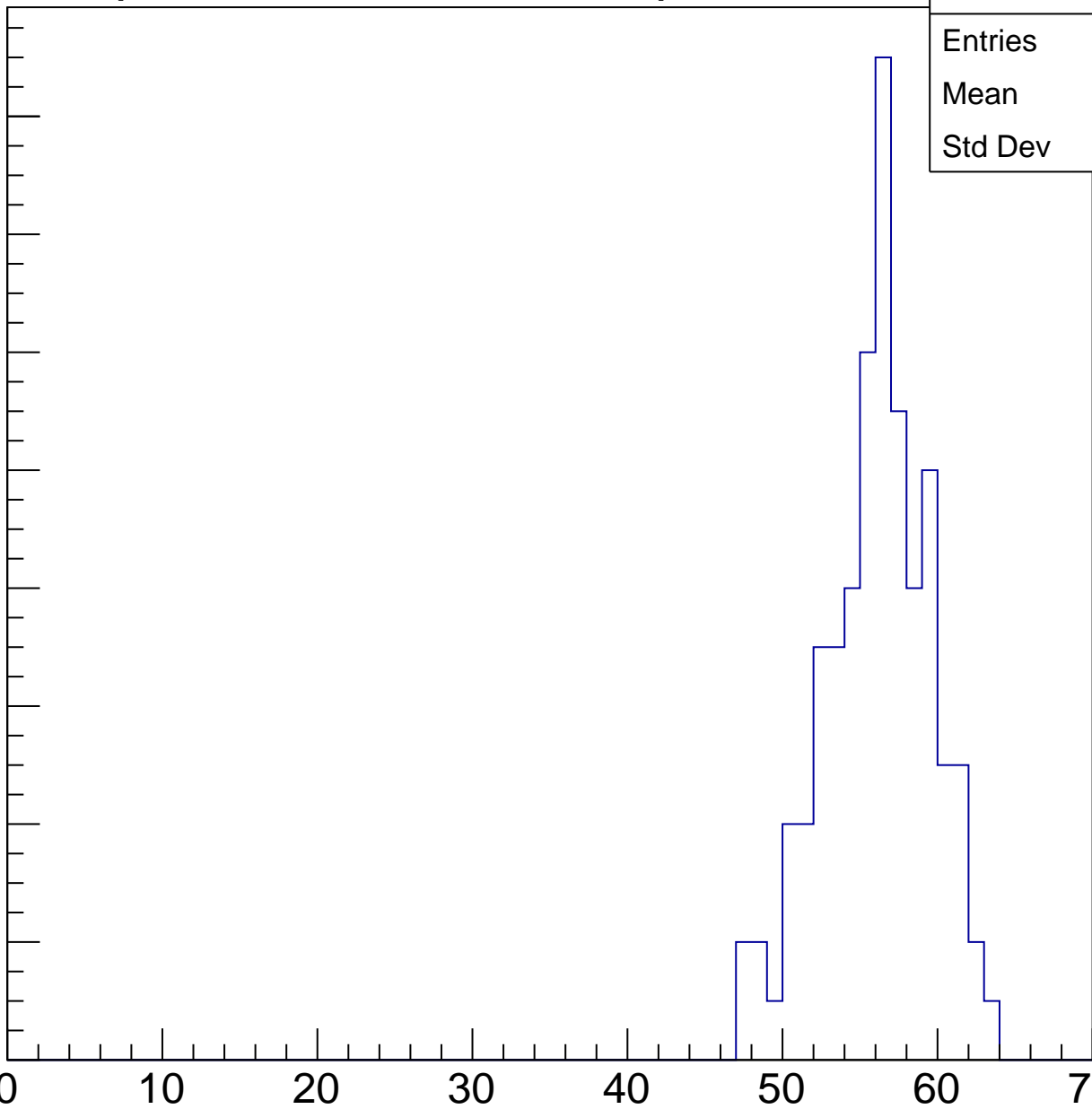
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	55.61
Std Dev	3.427

ampl



B0L101S, U18-ch28, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	93
Mean	60
Std Dev	2.396

0

2

4

6

8

10

12

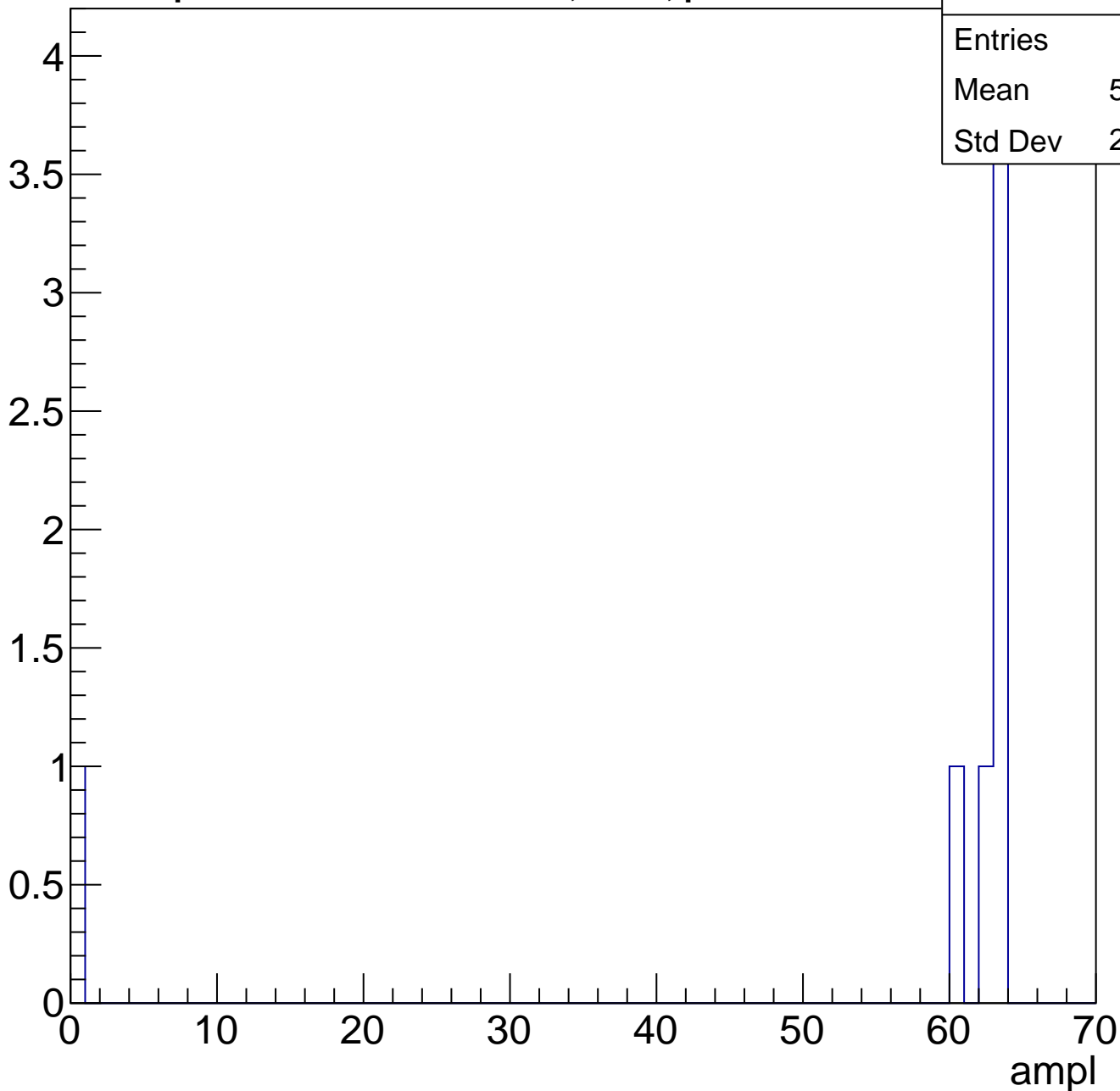
14

16

B0L101S, U18-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

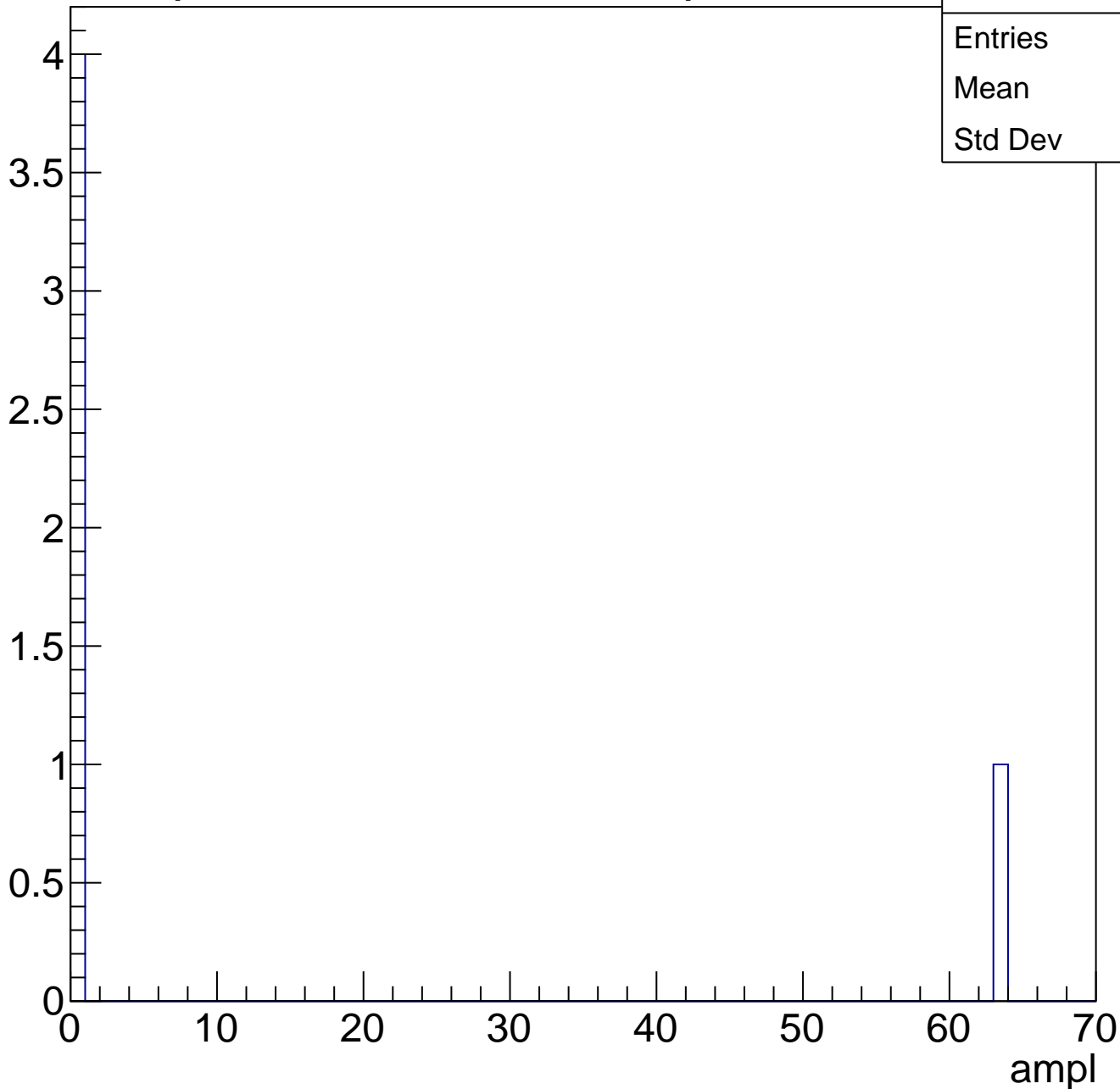
Entry



B0L101S, U18-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



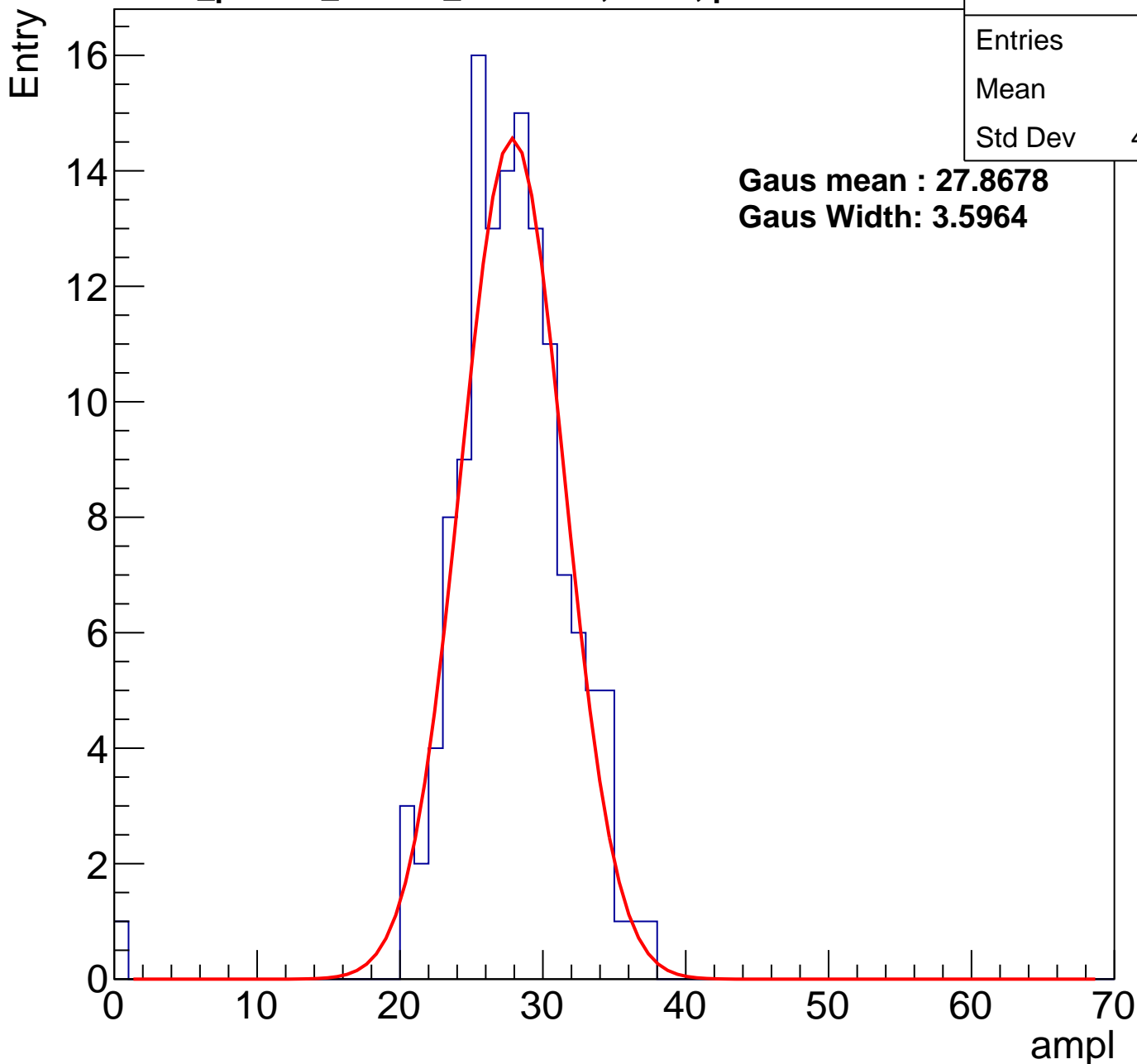
B0L101S, U18-ch29, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	27.3
Std Dev	4.234

Gaus mean : 27.8678

Gaus Width: 3.5964



B0L101S, U18-ch29, adc1

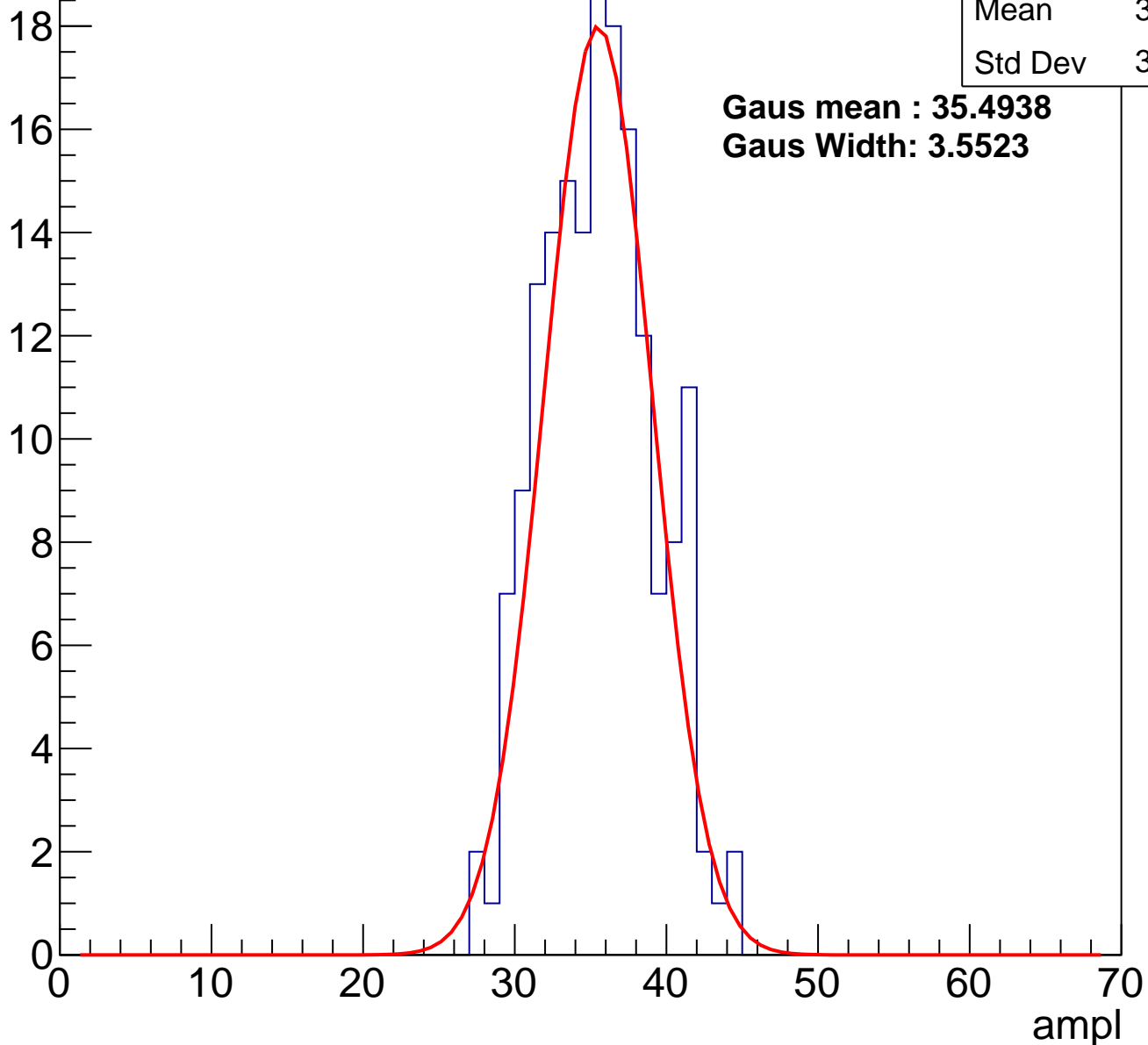
calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	35.07
Std Dev	3.654

Gaus mean : 35.4938

Gaus Width: 3.5523

Entry



B0L101S, U18-ch29, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

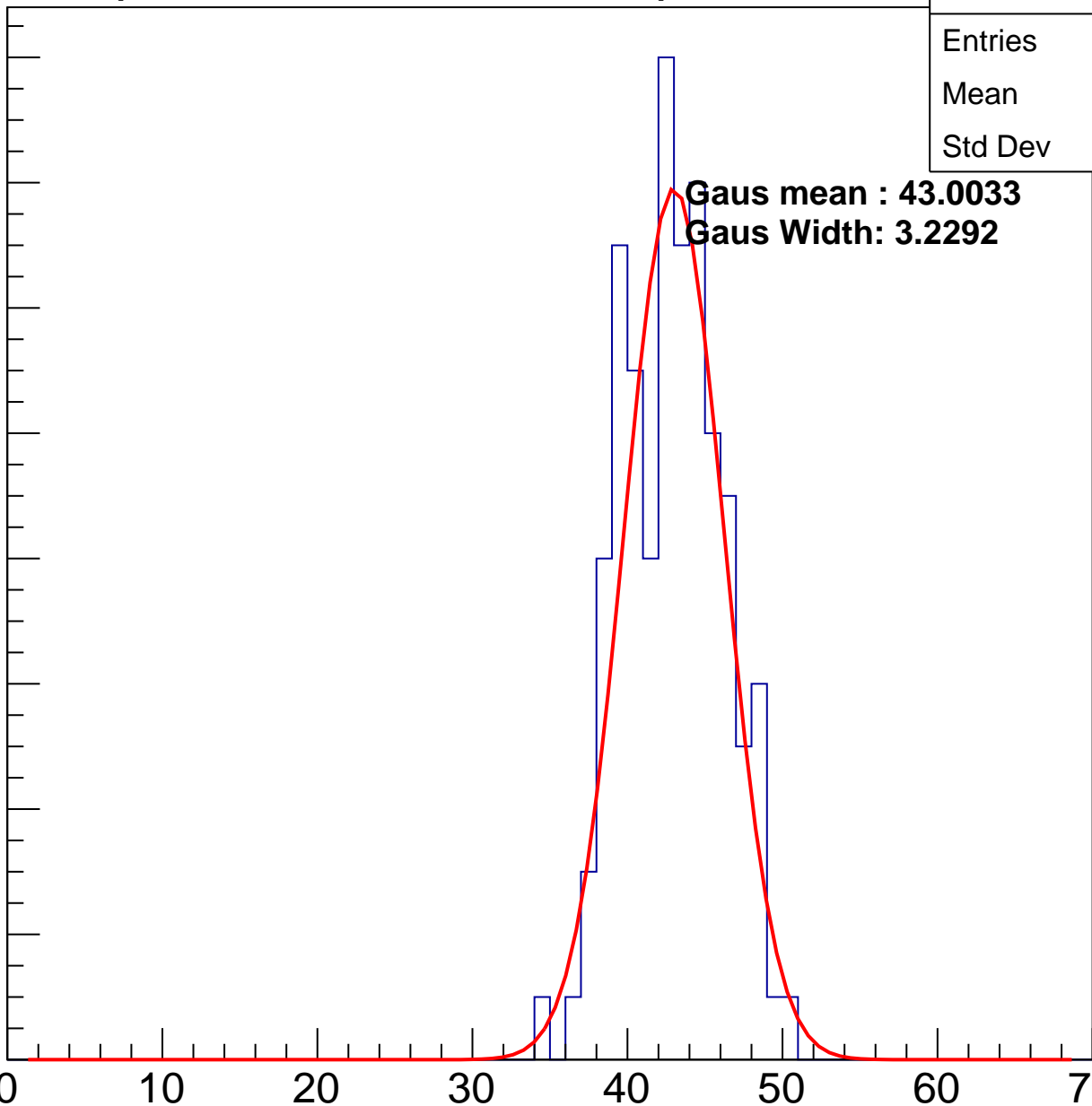
16
14
12
10
8
6
4
2
0

Entries	120
Mean	42.44
Std Dev	3.17

Gaus mean : 43.0033

Gaus Width: 3.2292

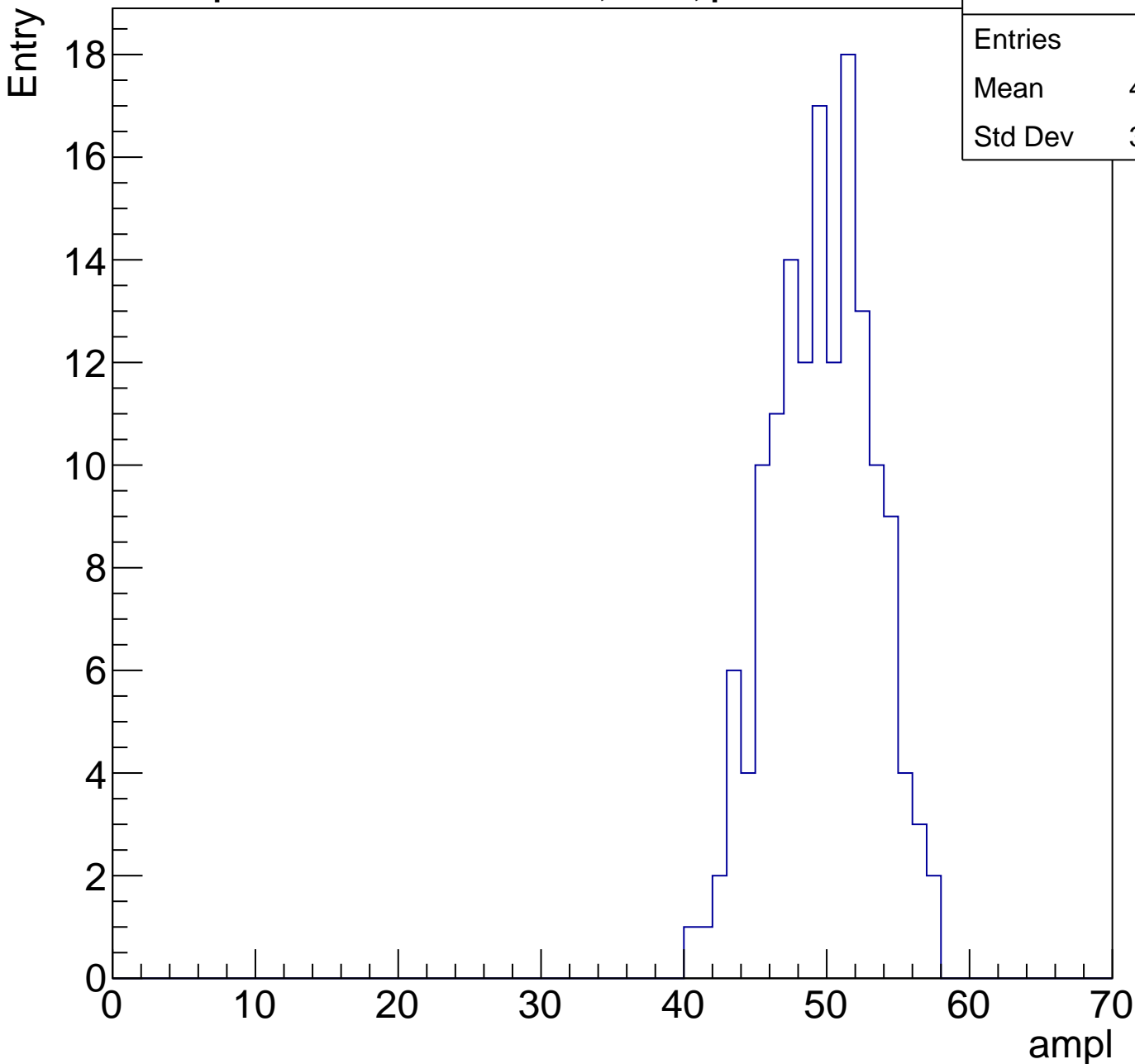
0 10 20 30 40 50 60 70
ampl



B0L101S, U18-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	49.22
Std Dev	3.556

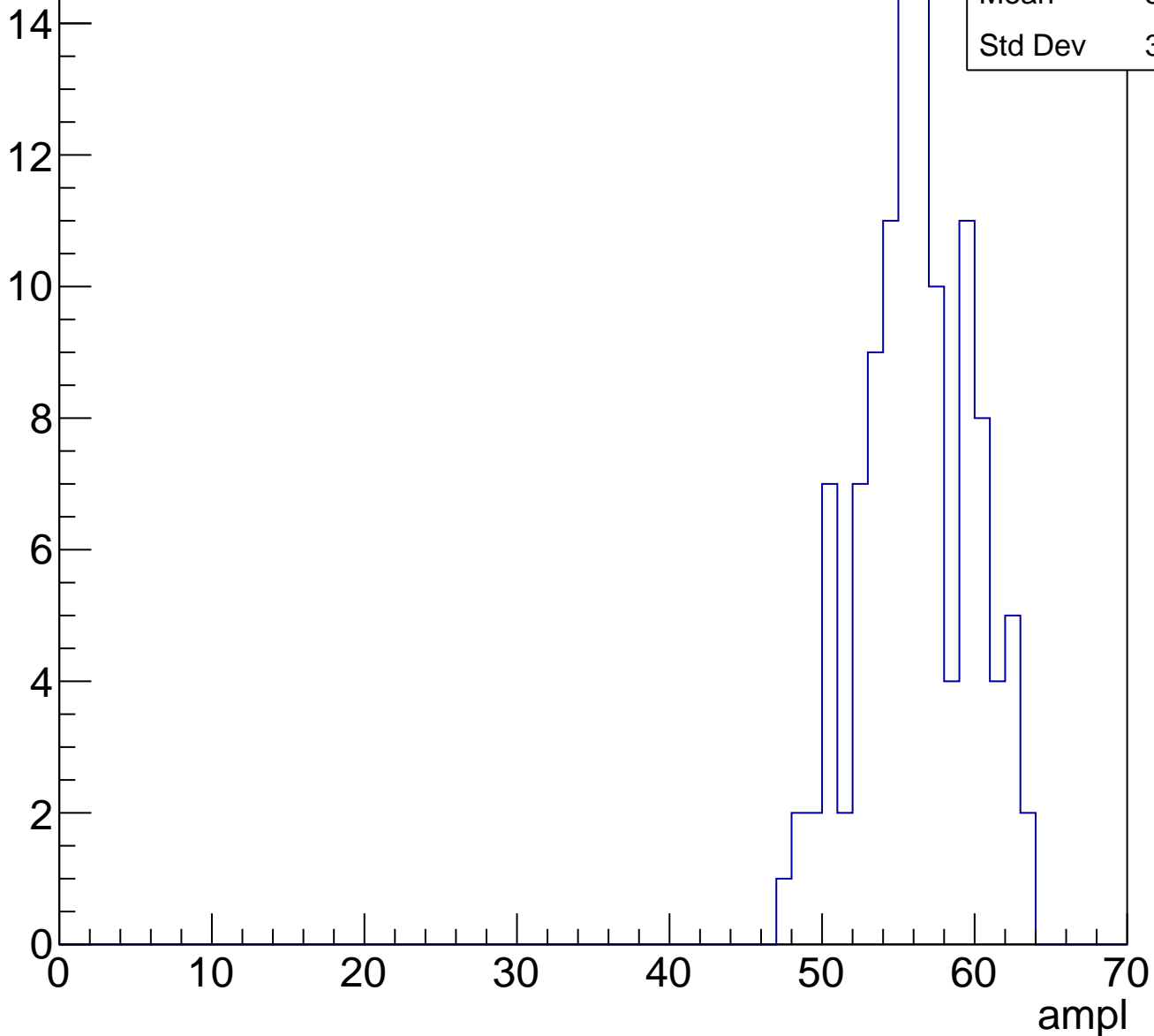


B0L101S, U18-ch29, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	55.69
Std Dev	3.588

Entry



B0L101S, U18-ch29, adc5

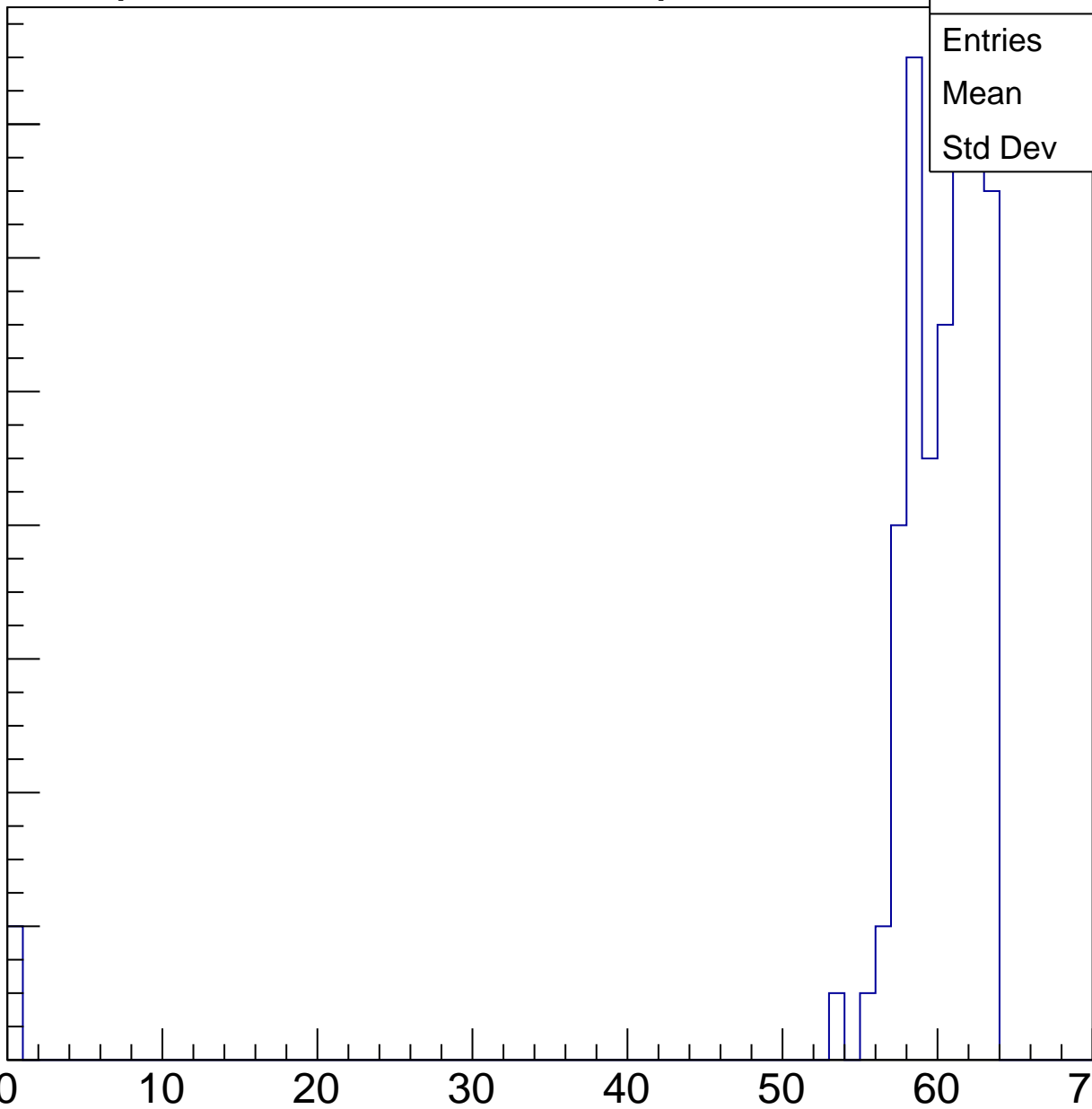
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.67
Std Dev	9.063

ampl

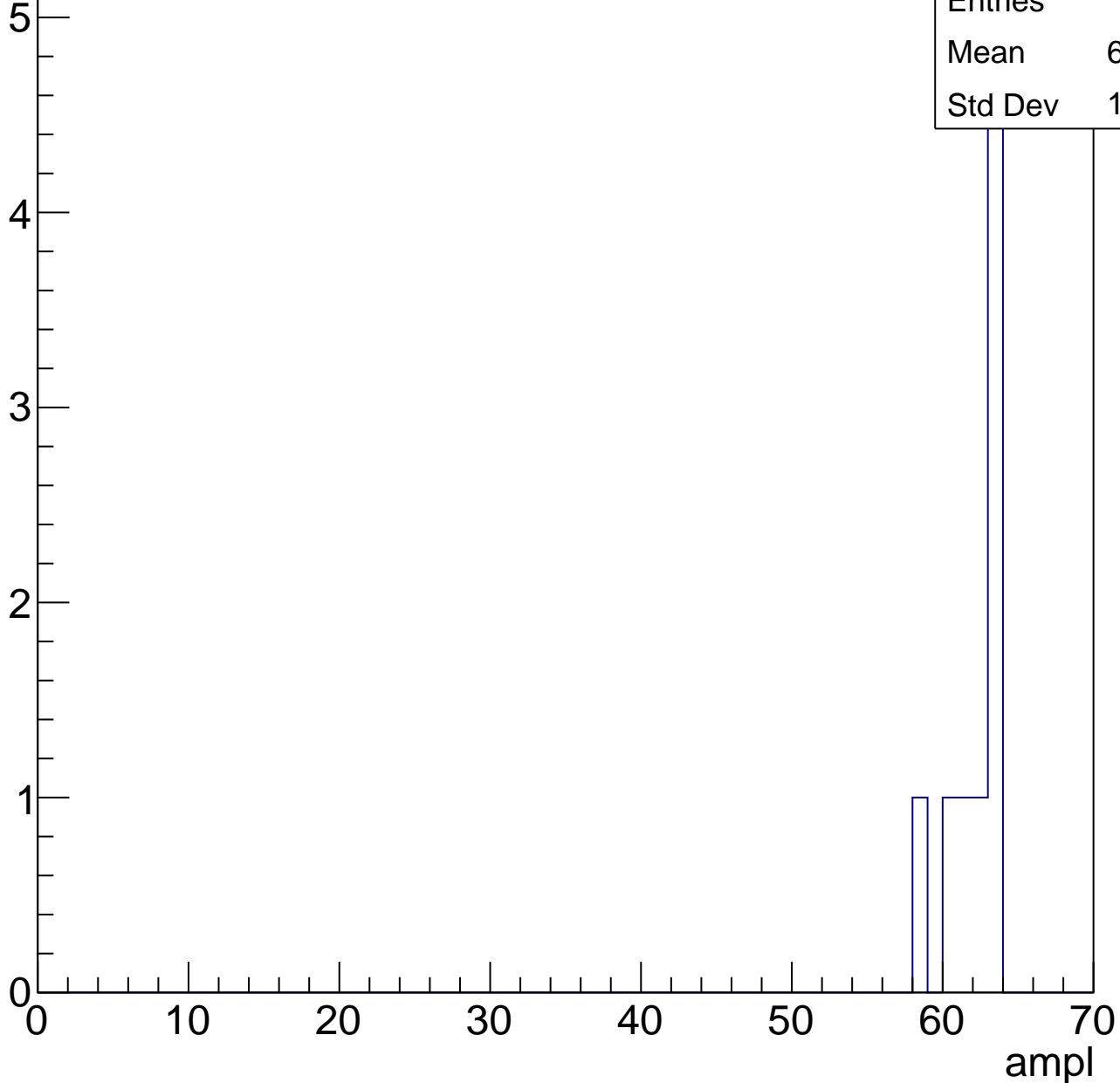


B0L101S, U18-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

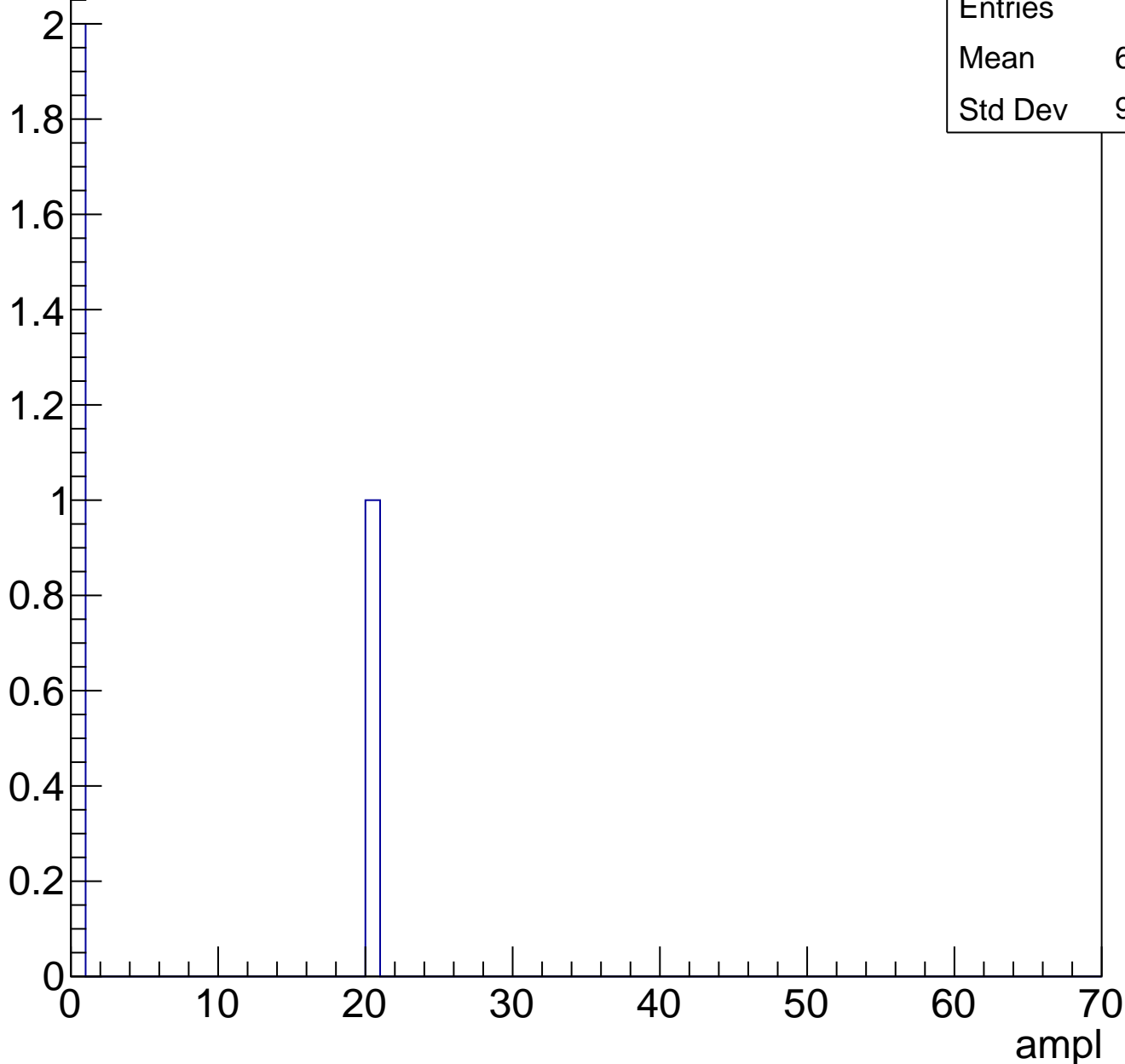
Entries	9
Mean	61.78
Std Dev	1.685



B0L101S, U18-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L101S, U18-ch30, adc0

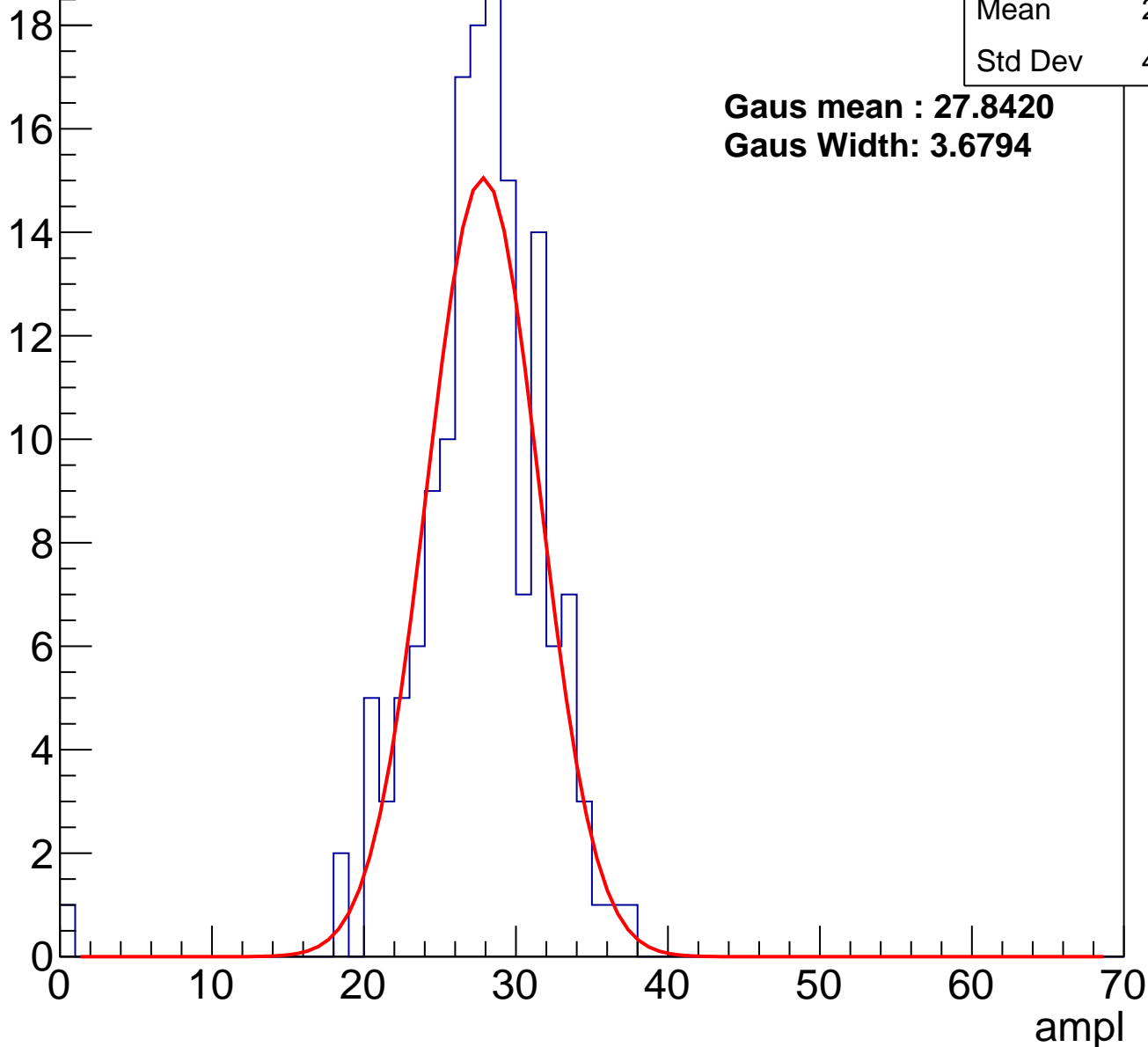
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	27.23
Std Dev	4.286

Gaus mean : 27.8420

Gaus Width: 3.6794

Entry



B0L101S, U18-ch30, adc1

calib_packv5_042523_0143.root, FC#1, port C1

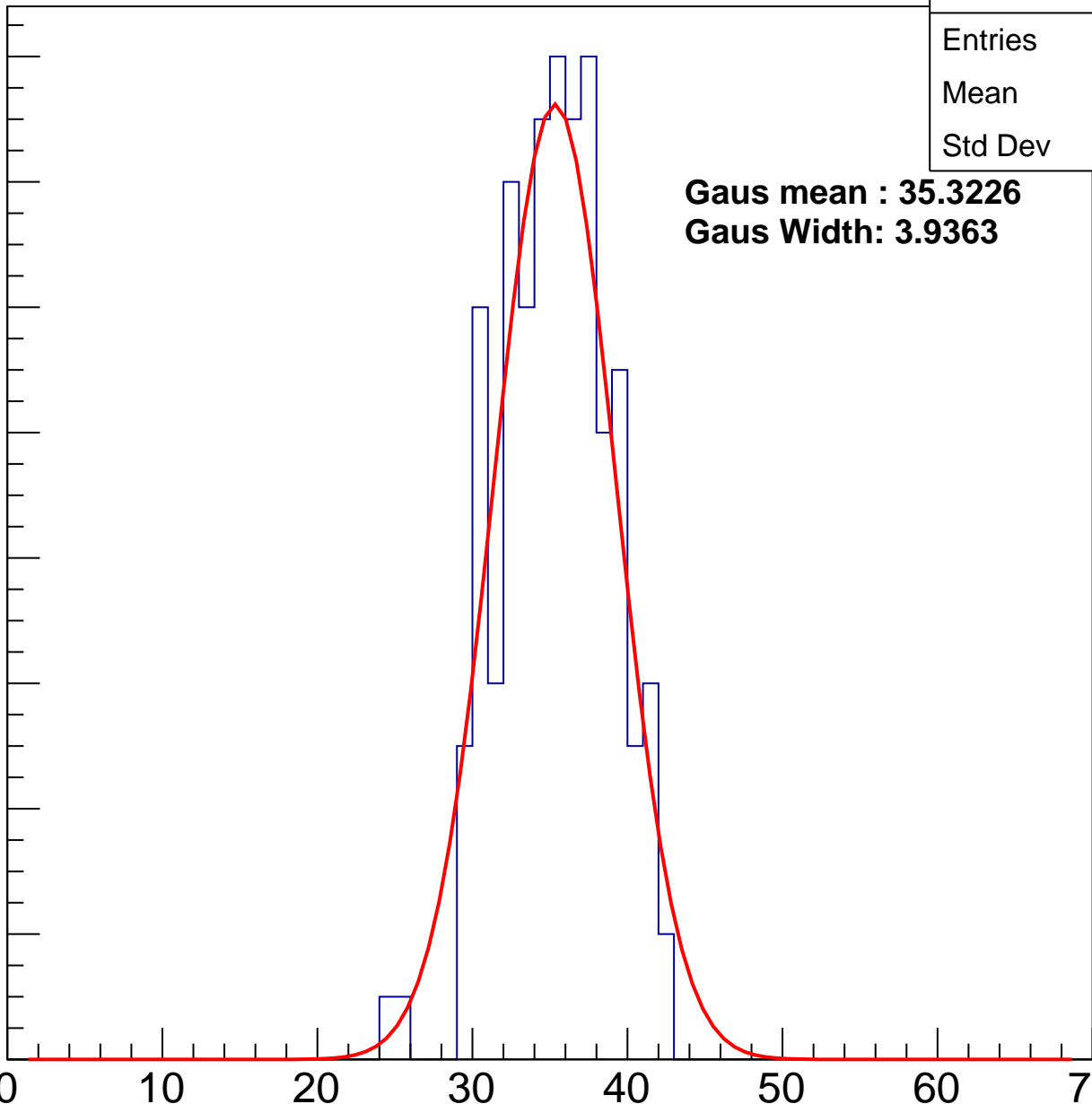
Entry

16
14
12
10
8
6
4
2
0

Entries	147
Mean	34.86
Std Dev	3.467

Gaus mean : 35.3226
Gaus Width: 3.9363

0 10 20 30 40 50 60 70
ampl



B0L101S, U18-ch30, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

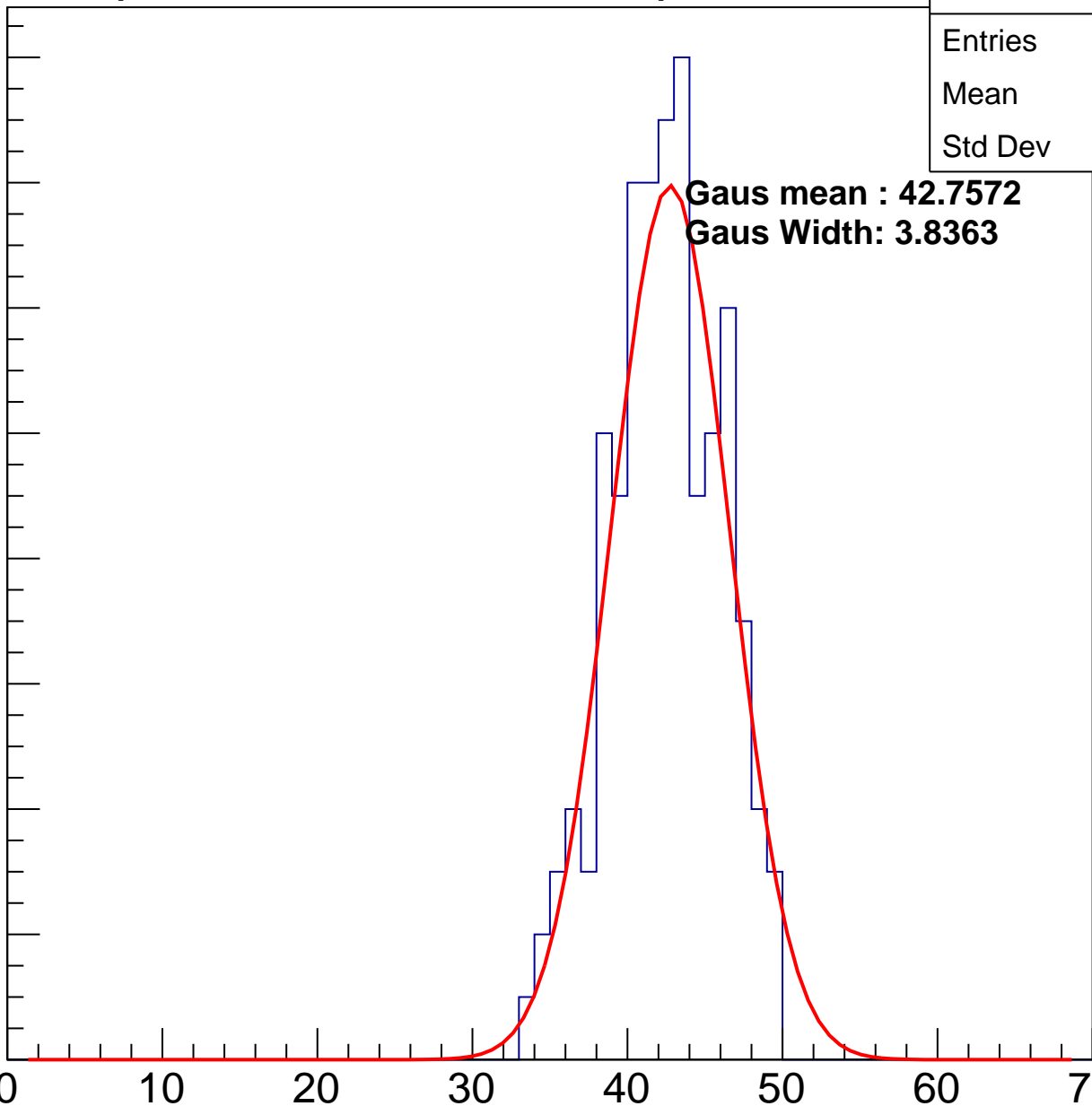
16
14
12
10
8
6
4
2
0

Entries	136
Mean	41.99
Std Dev	3.521

Gaus mean : 42.7572

Gaus Width: 3.8363

ampl

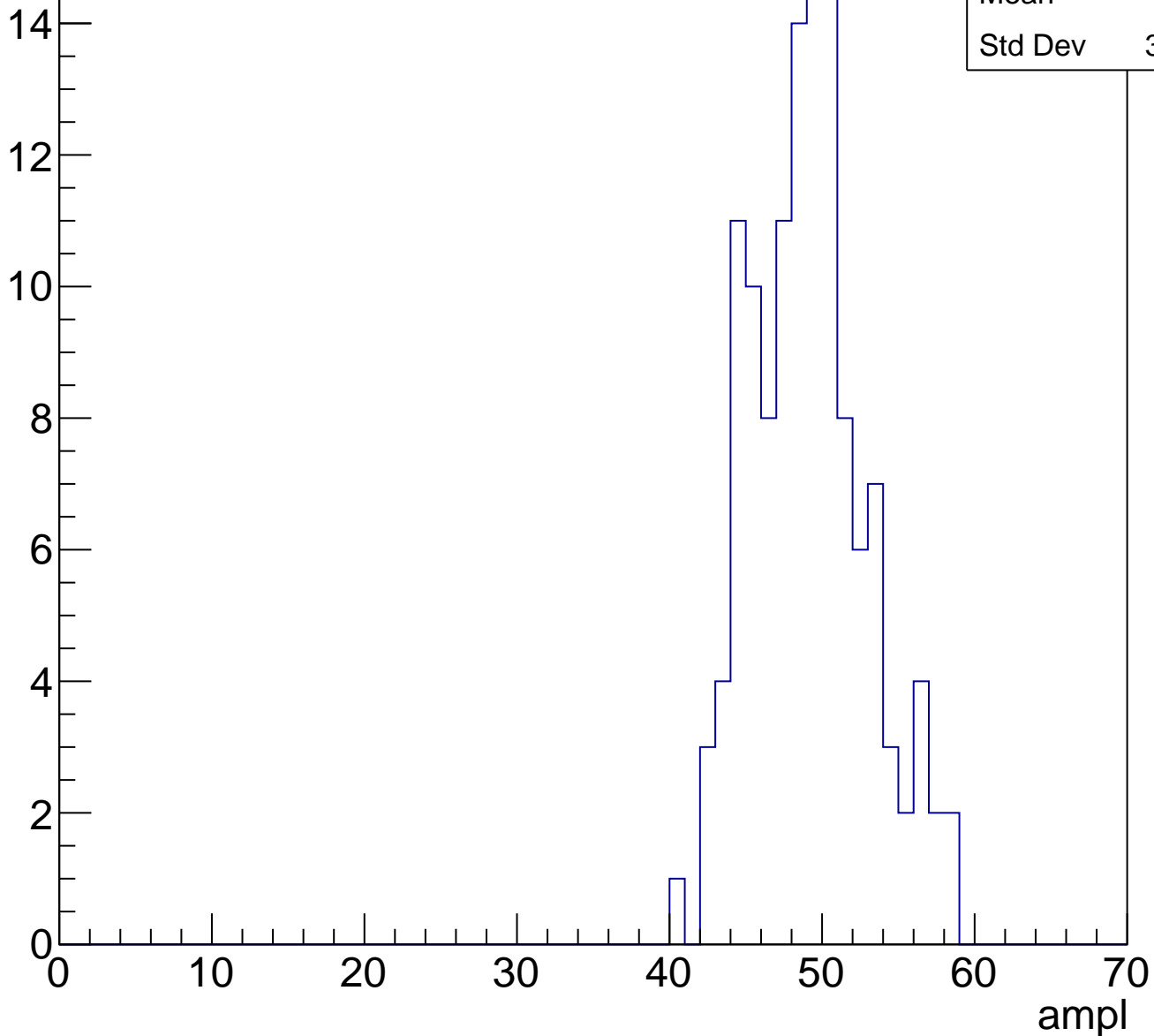


B0L101S, U18-ch30, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	48.66
Std Dev	3.746

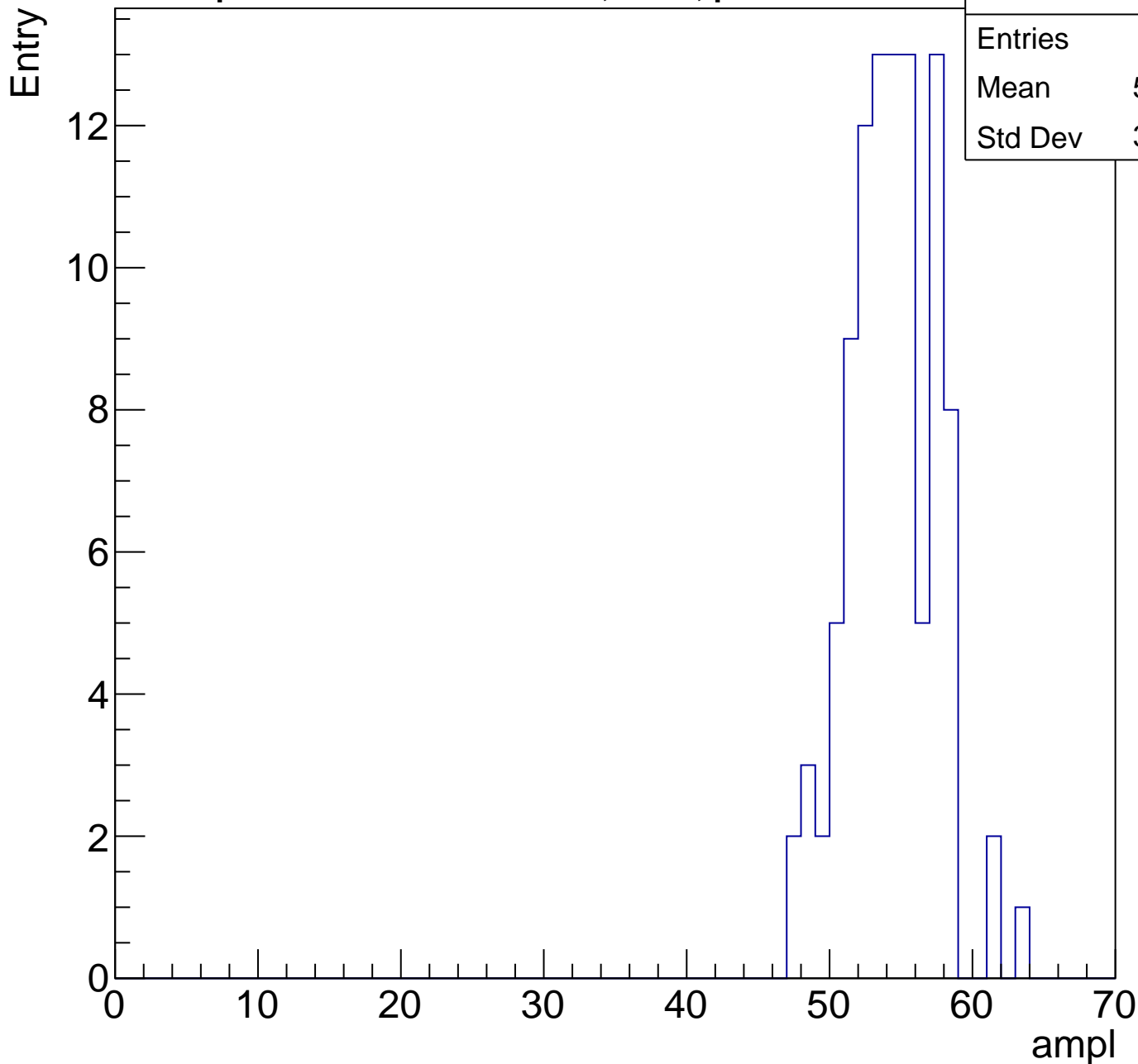
Entry



B0L101S, U18-ch30, adc4

calib_packv5_042523_0143.root, FC#1, port C1

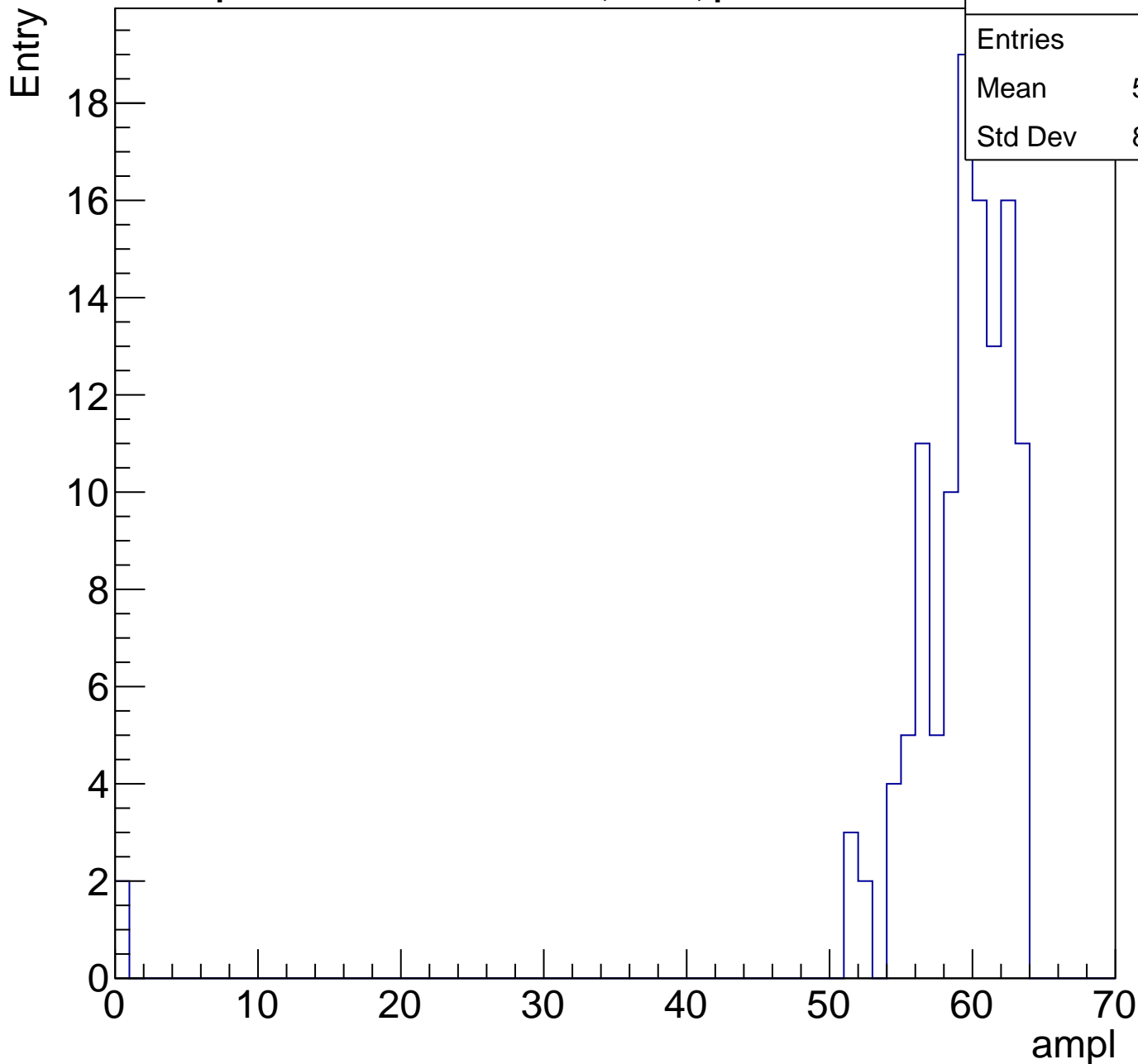
Entries	101
Mean	53.91
Std Dev	3.041



B0L101S, U18-ch30, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	58.02
Std Dev	8.184



B0L101S, U18-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

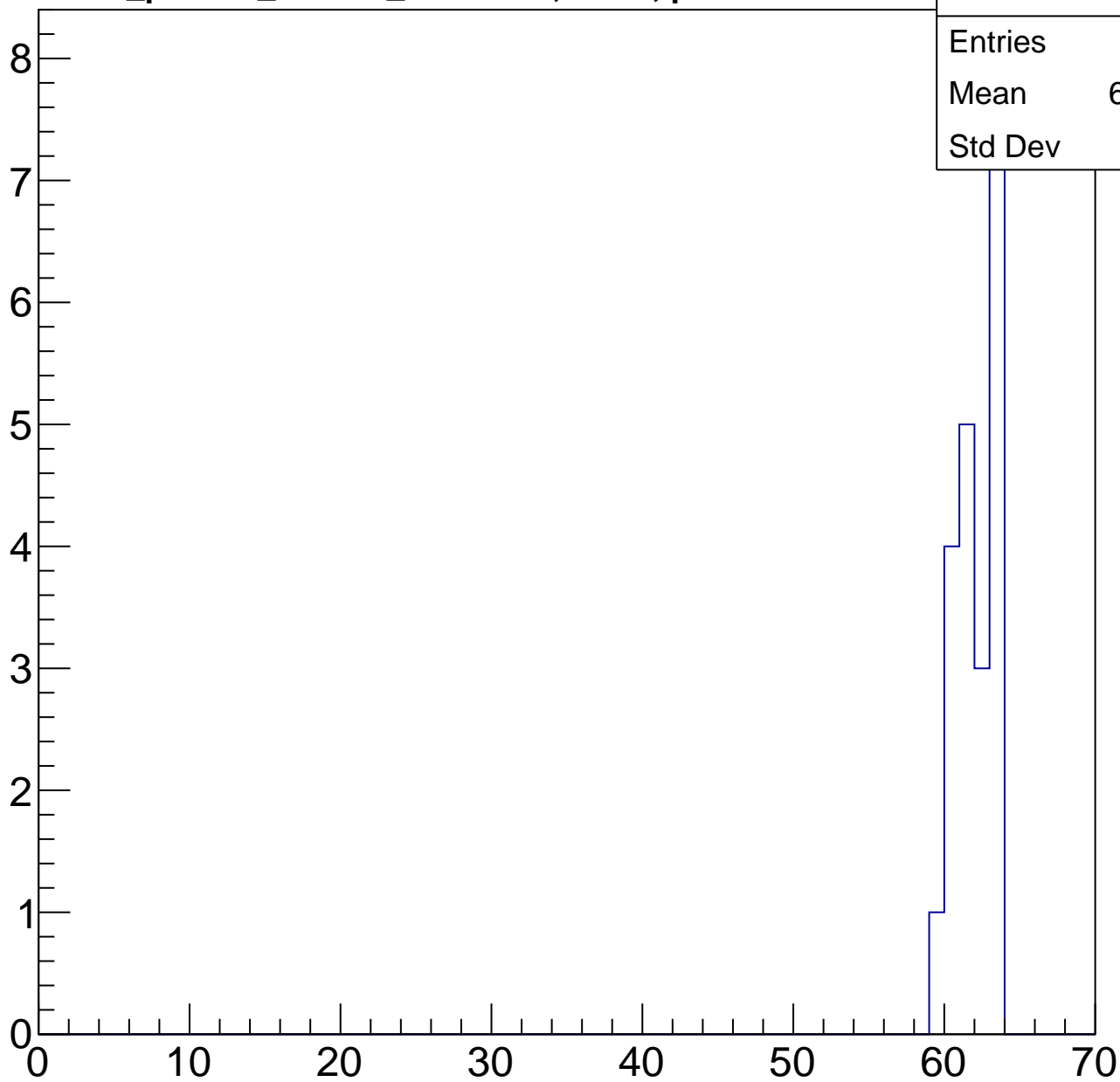
Entry

8
7
6
5
4
3
2
1
0

Entries	21
Mean	61.62
Std Dev	1.29

ampl

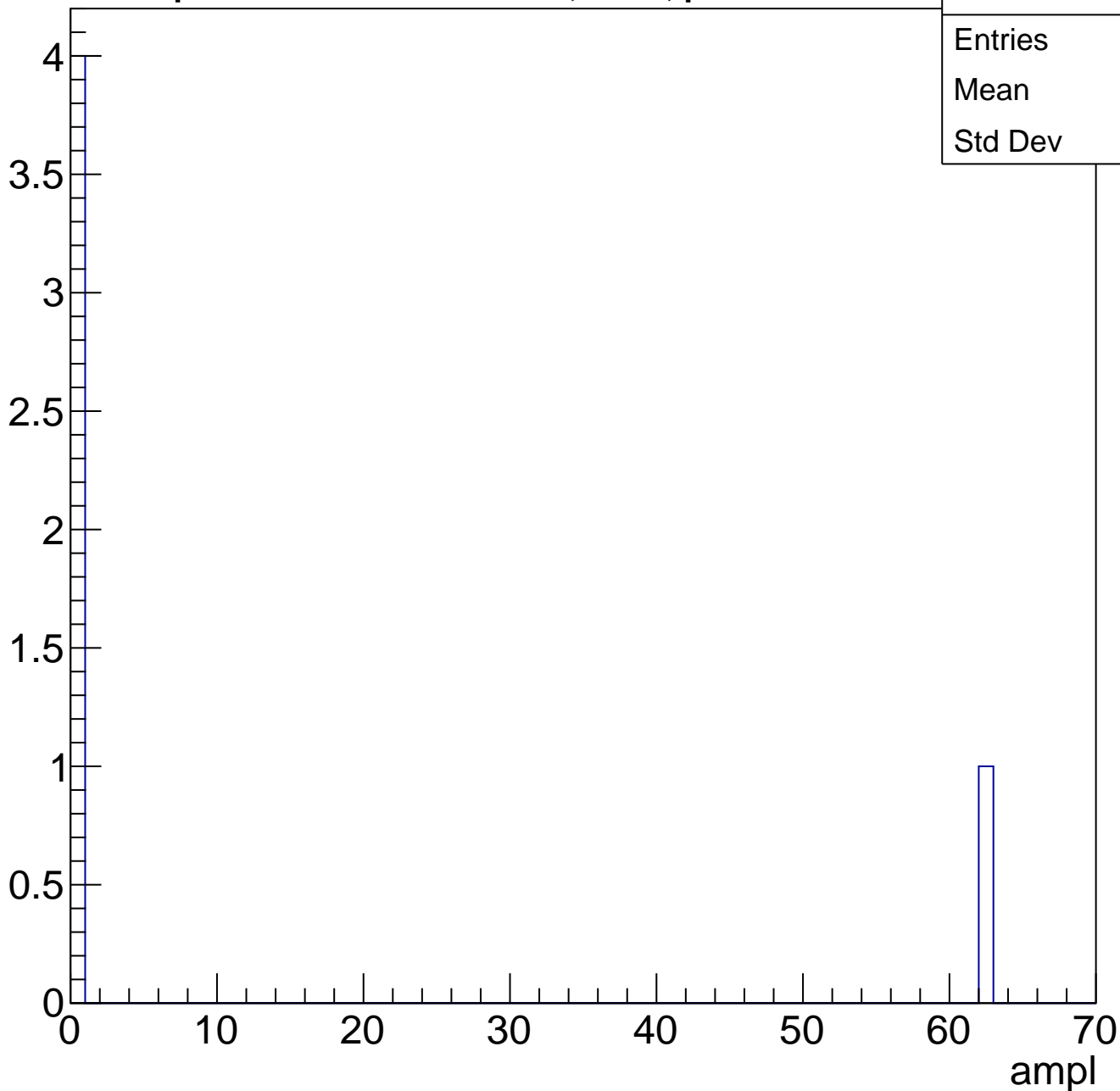
0 10 20 30 40 50 60 70



B0L101S, U18-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch31, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	28.67
Std Dev	4.029

Gaus mean : 29.3811

Gaus Width: 2.9677

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

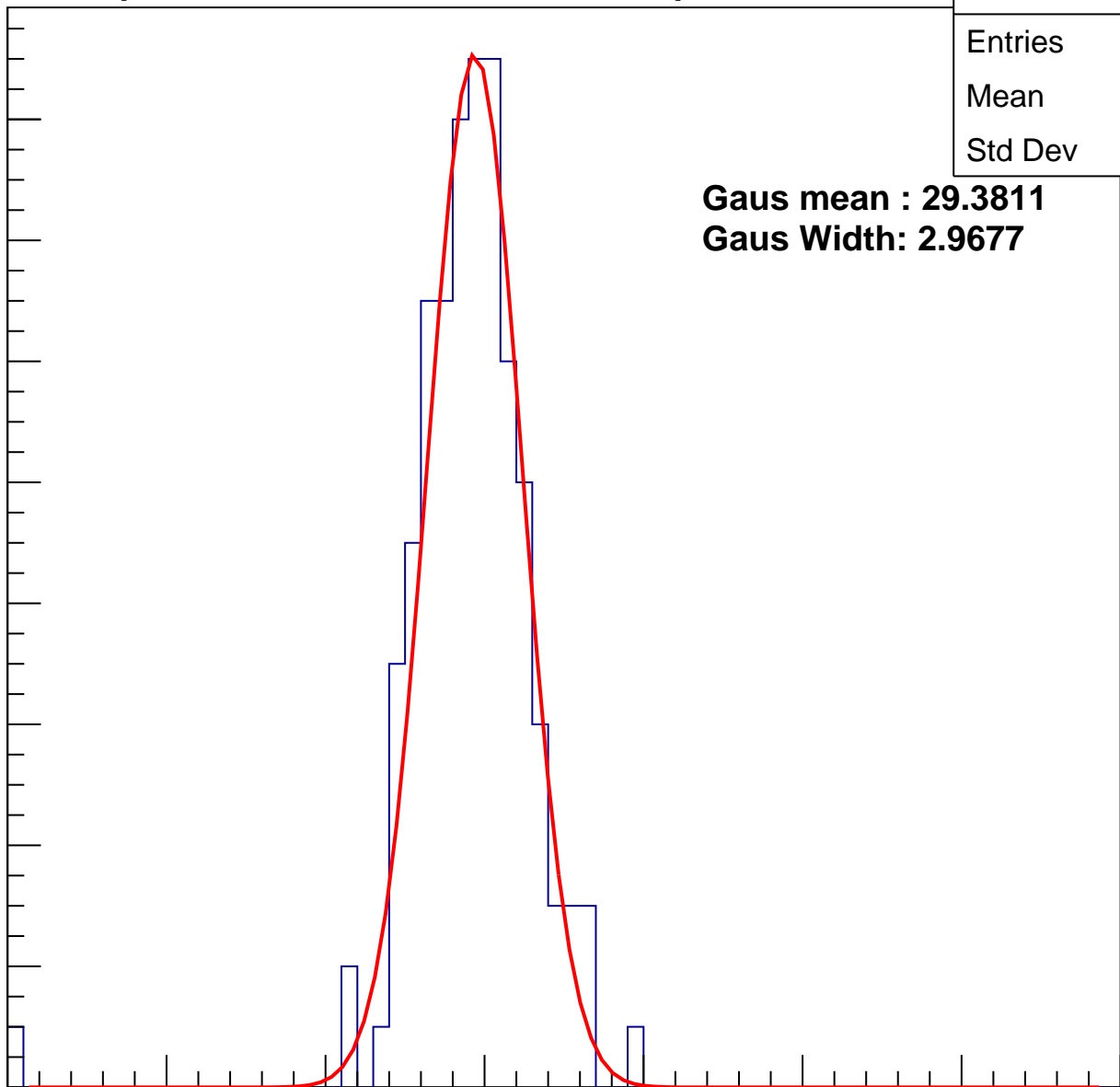
40

50

60

70

ampl



B0L101S, U18-ch31, adc1

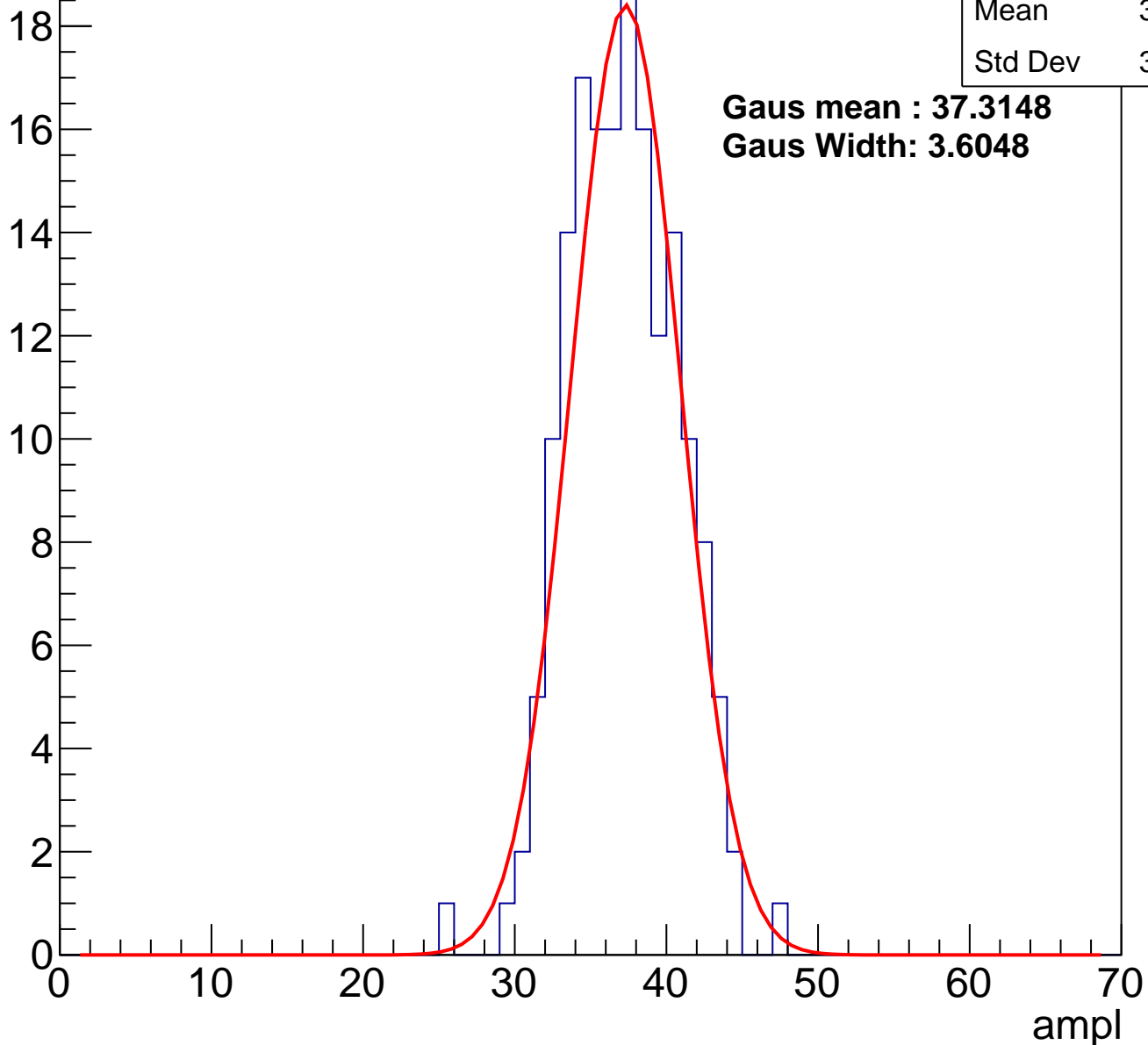
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	36.69
Std Dev	3.529

Gaus mean : 37.3148

Gaus Width: 3.6048

Entry



B0L101S, U18-ch31, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

136

Mean

44.24

Std Dev

3.538

Gaus mean : 44.7683

Gaus Width: 3.5217

0

10

20

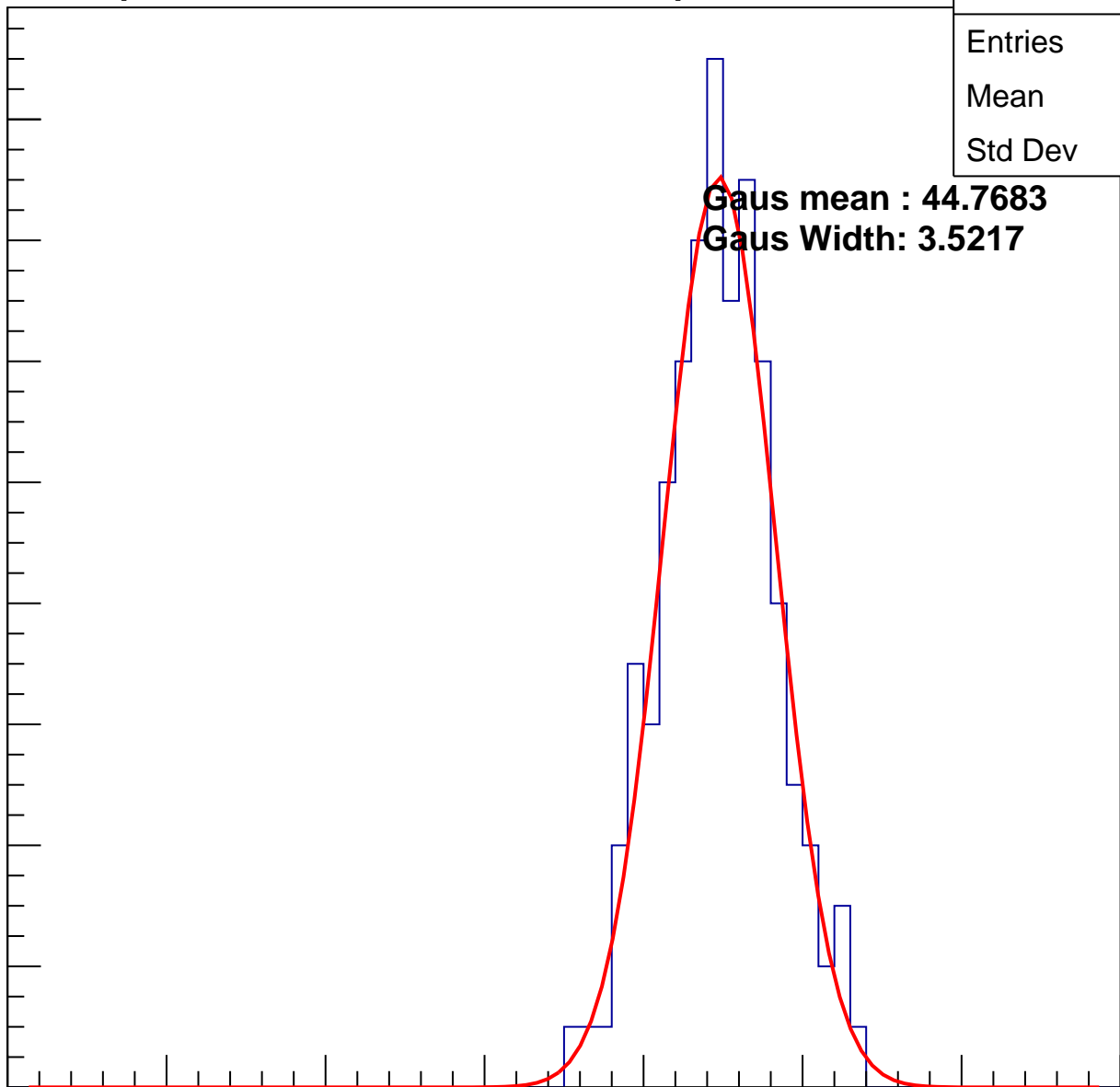
30

40

50

60

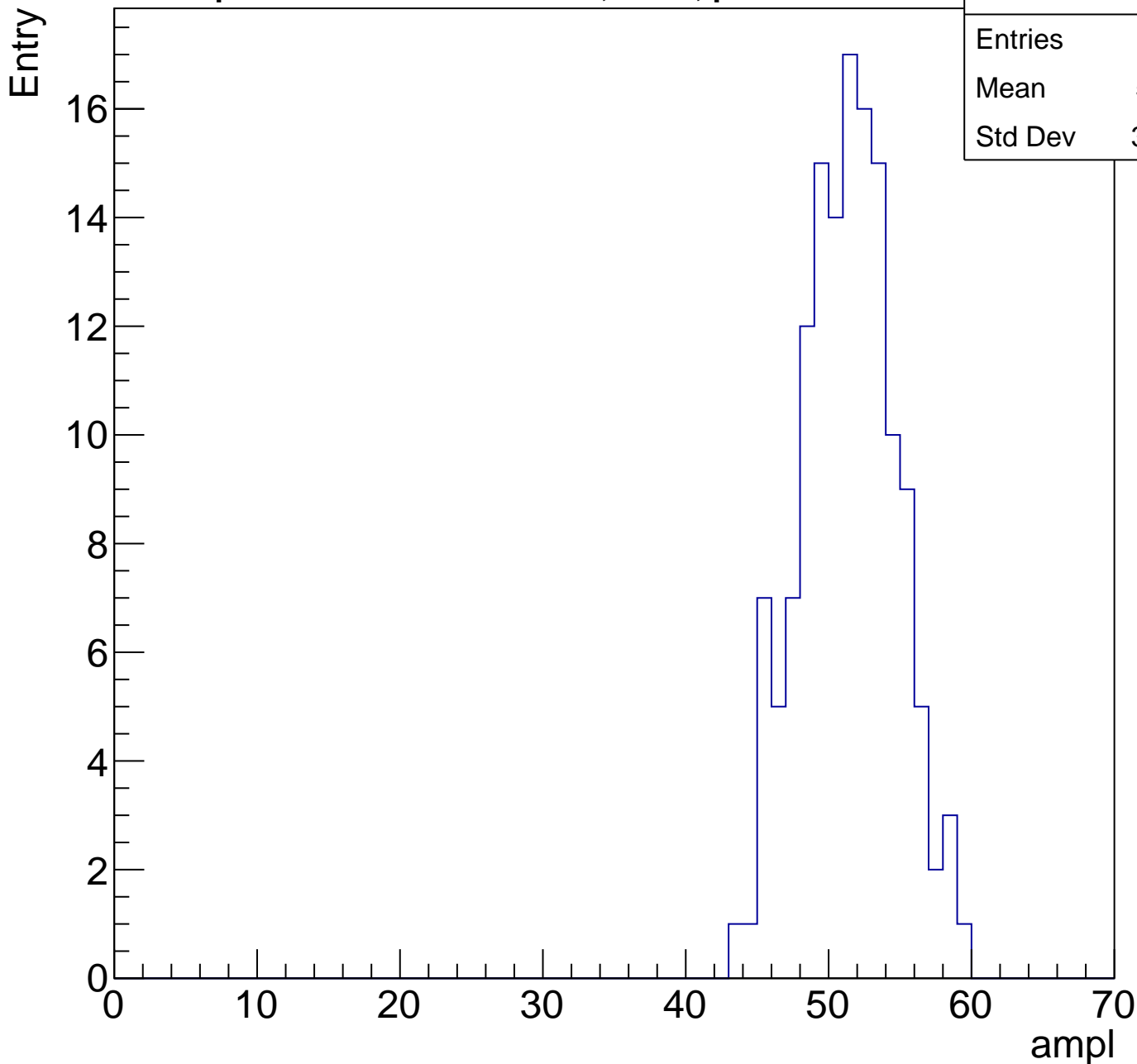
ampl



B0L101S, U18-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	50.91
Std Dev	3.274



B0L101S, U18-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	57.32
Std Dev	2.939

Entry

12

10

8

6

4

2

0

0

10

20

30

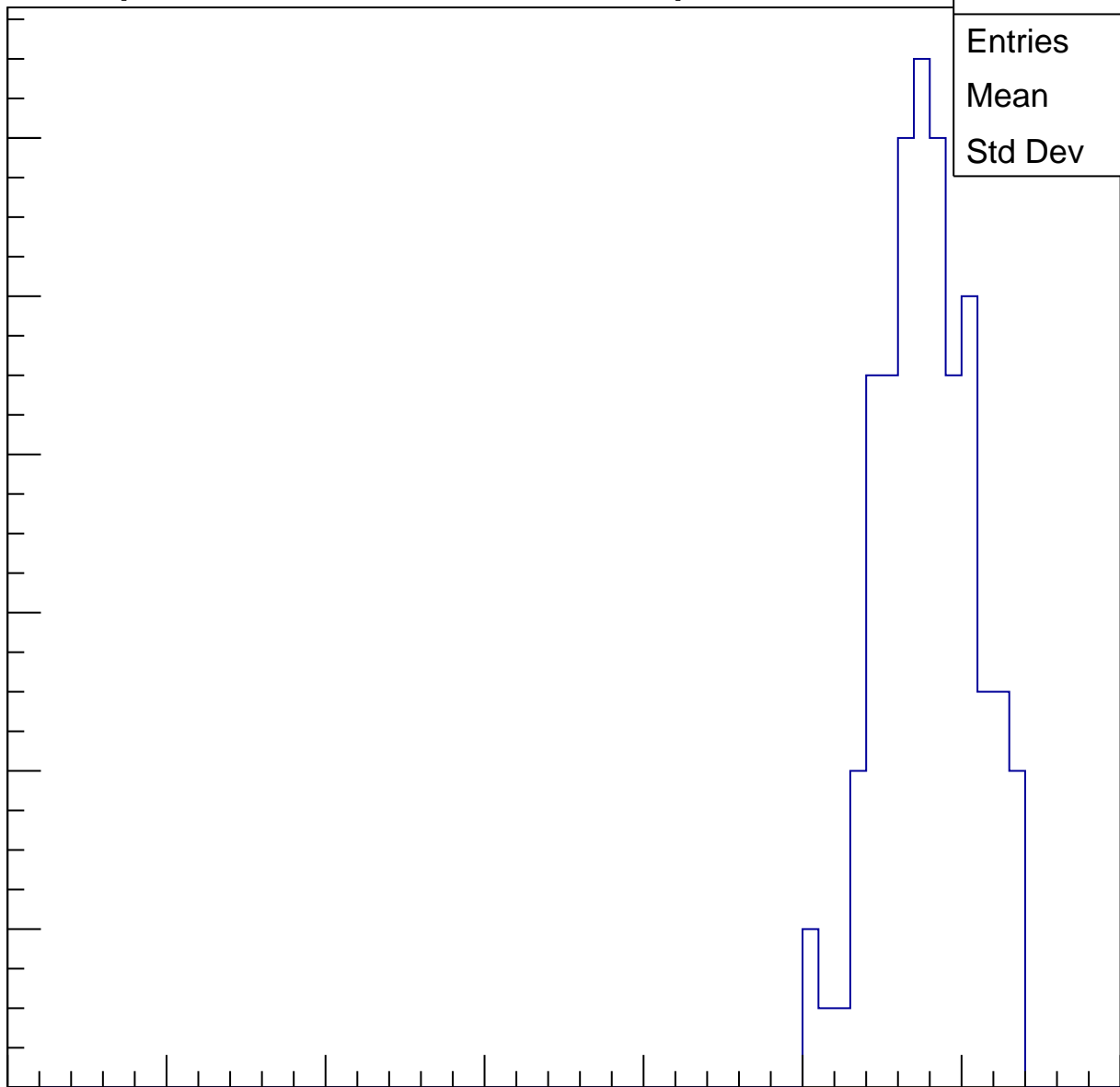
40

50

60

70

ampl



B0L101S, U18-ch31, adc5

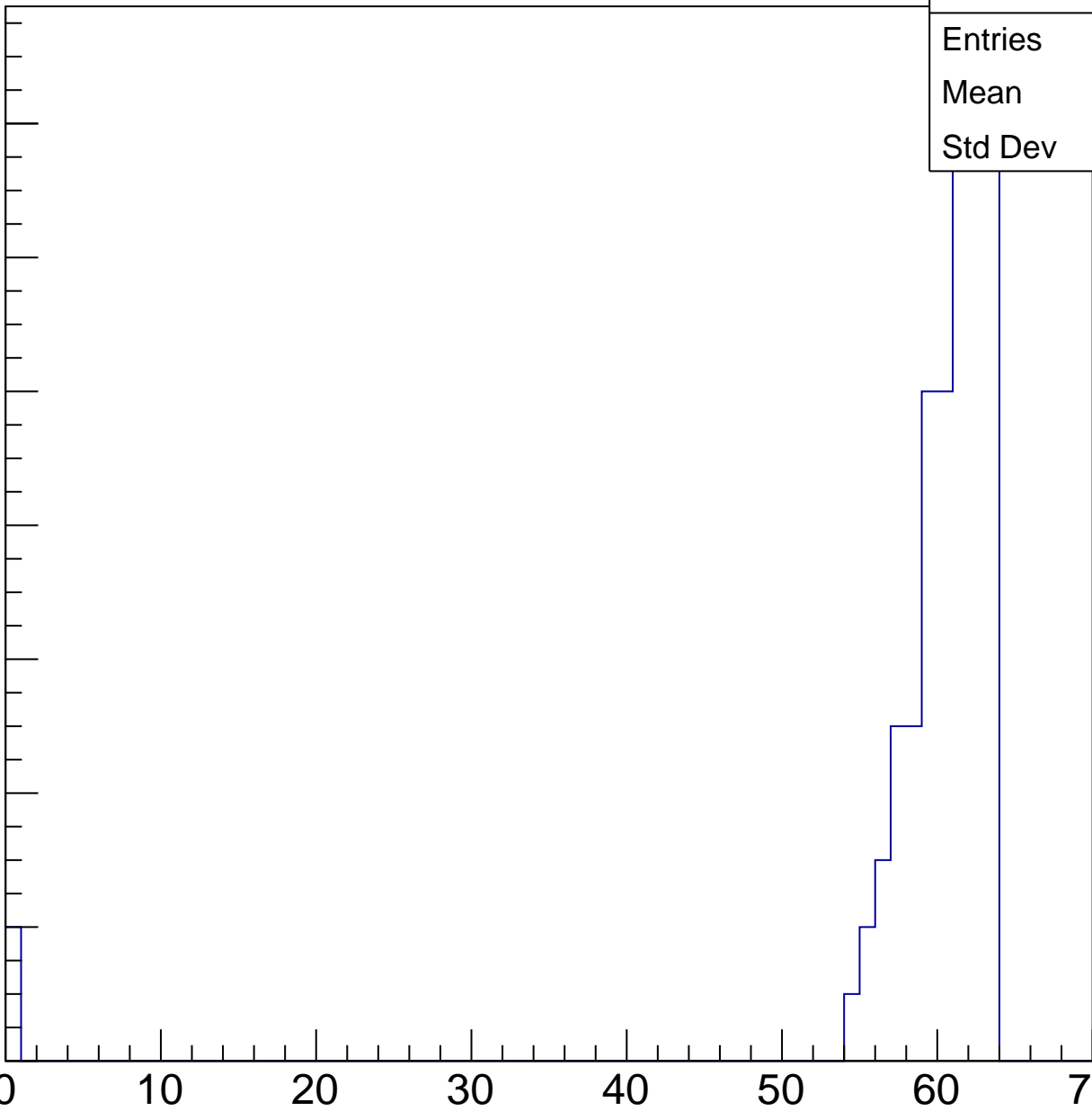
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	58.88
Std Dev	9.515

ampl



B0L101S, U18-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch32, adc0

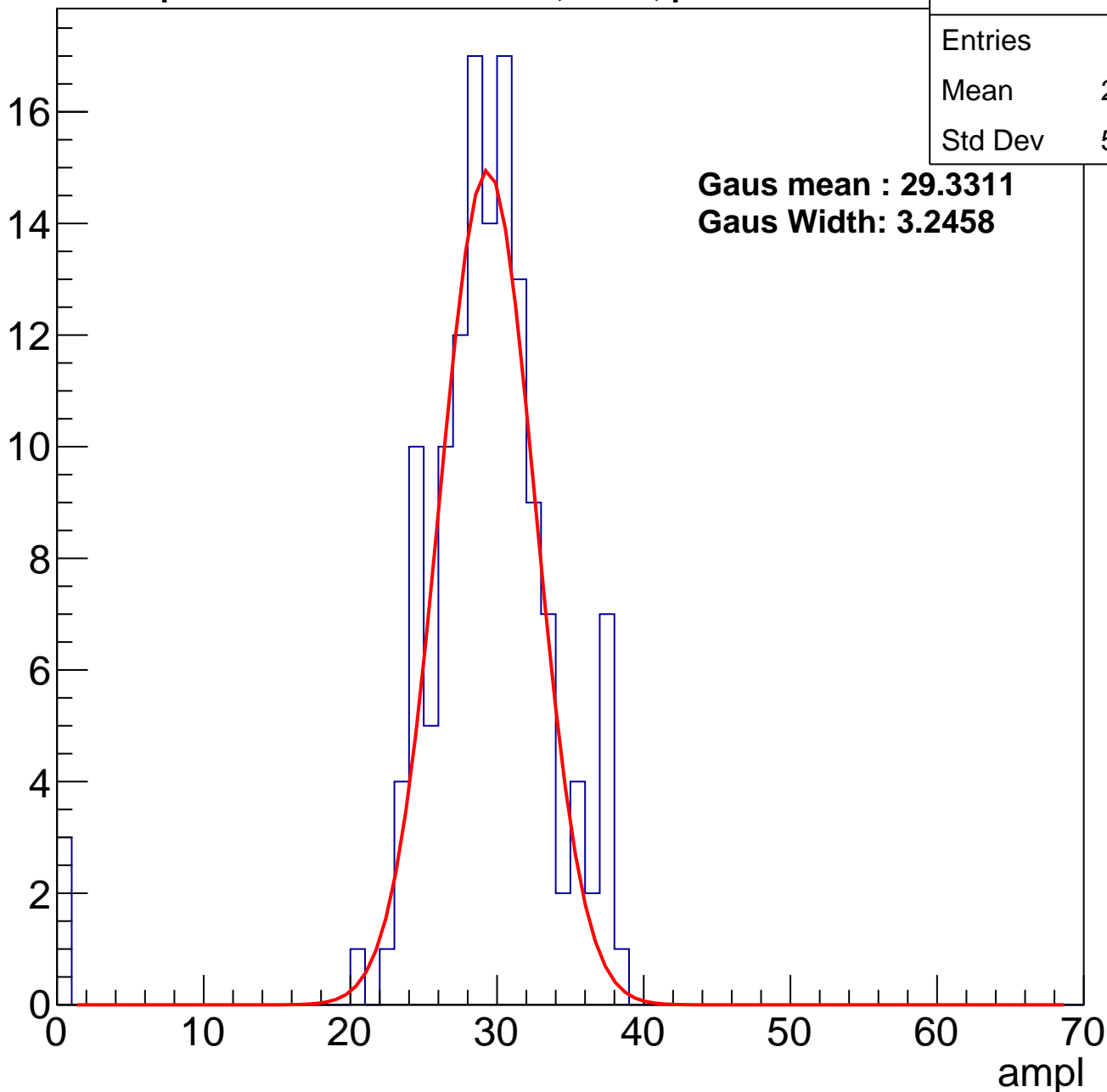
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	28.59
Std Dev	5.589

Gaus mean : 29.3311

Gaus Width: 3.2458

Entry



B0L101S, U18-ch32, adc1

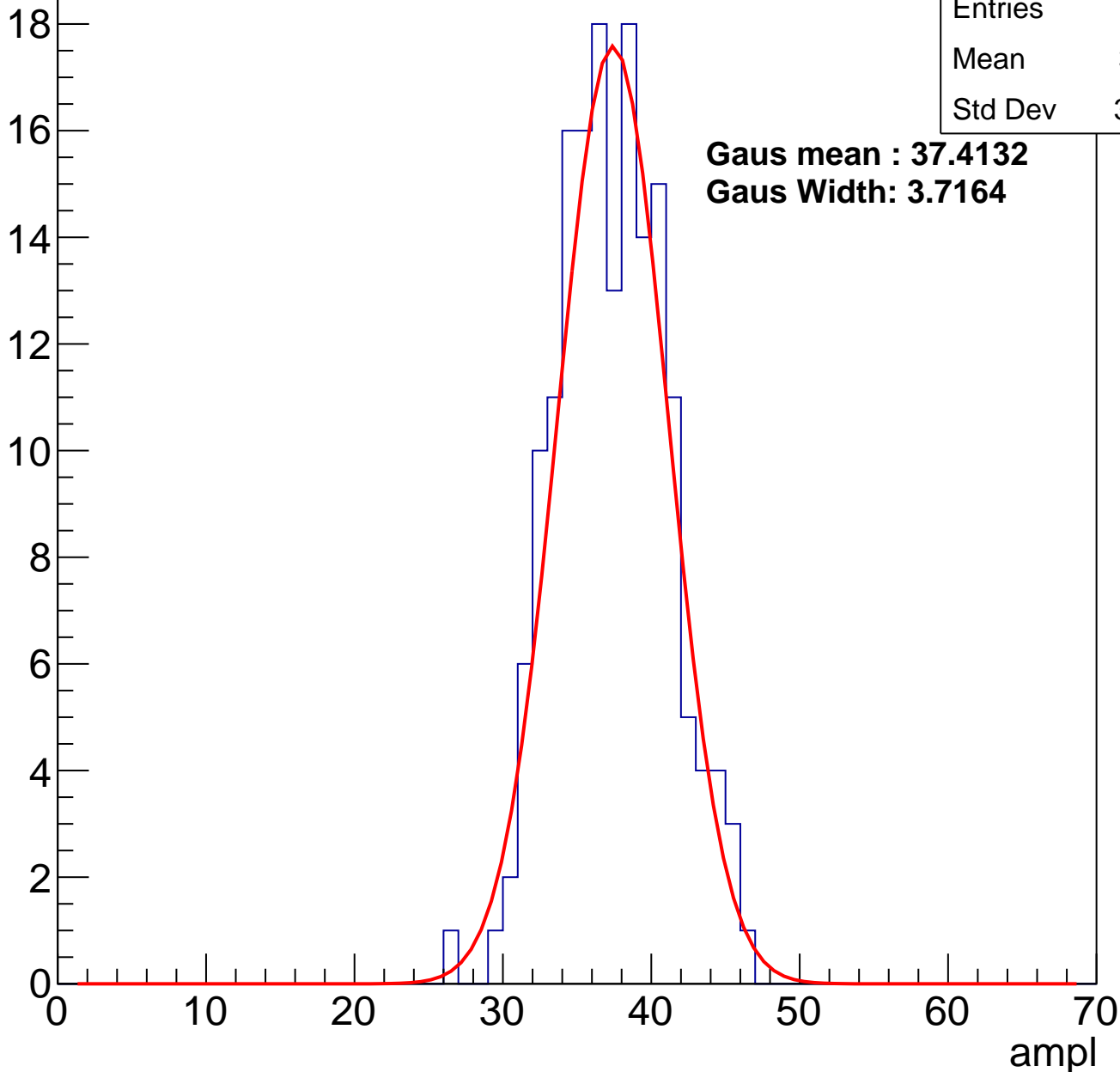
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	36.91
Std Dev	3.667

Gaus mean : 37.4132

Gaus Width: 3.7164

Entry



B0L101S, U18-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	44.44
Std Dev	3.424

Gaus mean : 45.2010

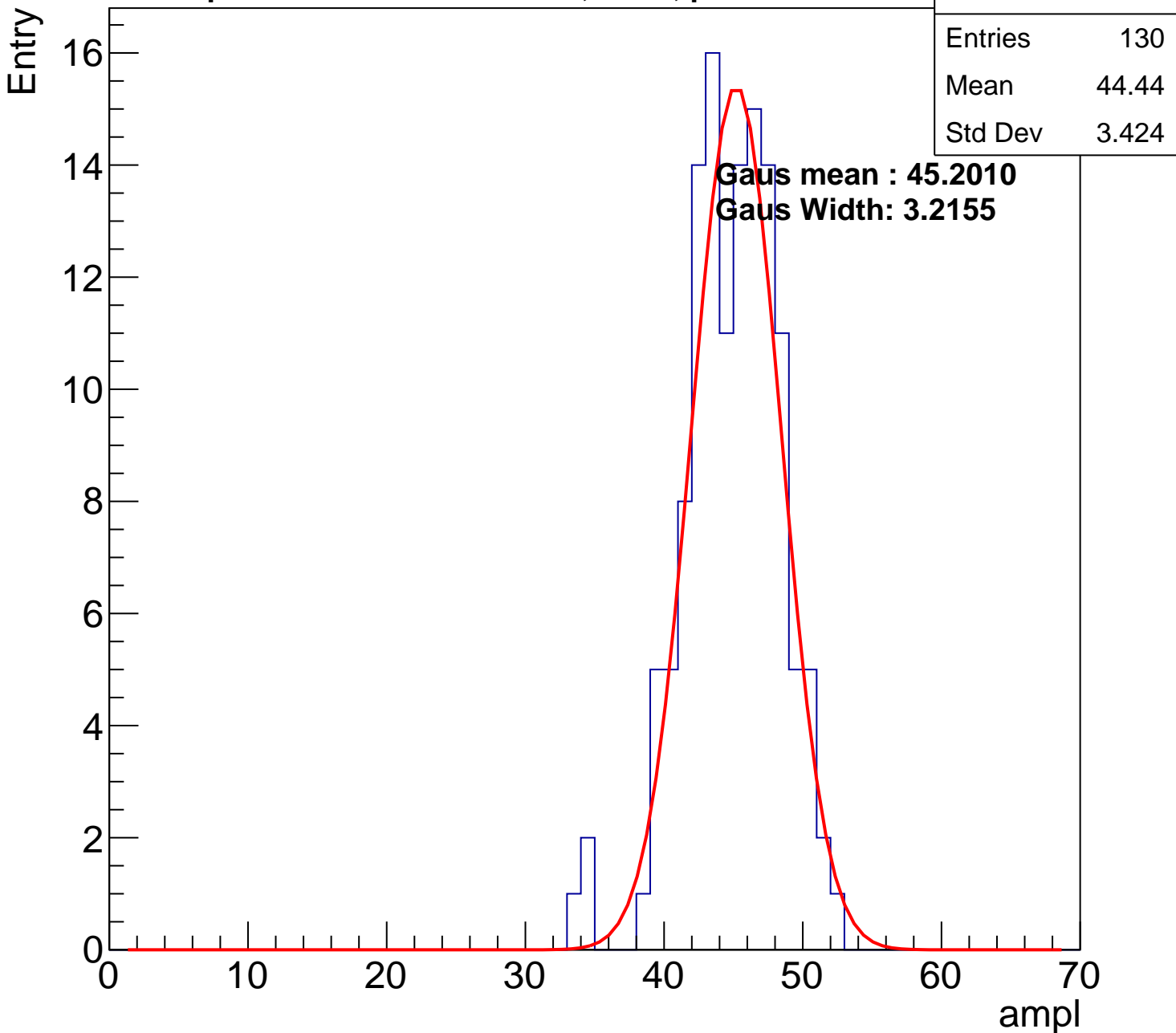
Gaus Width: 3.2155

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

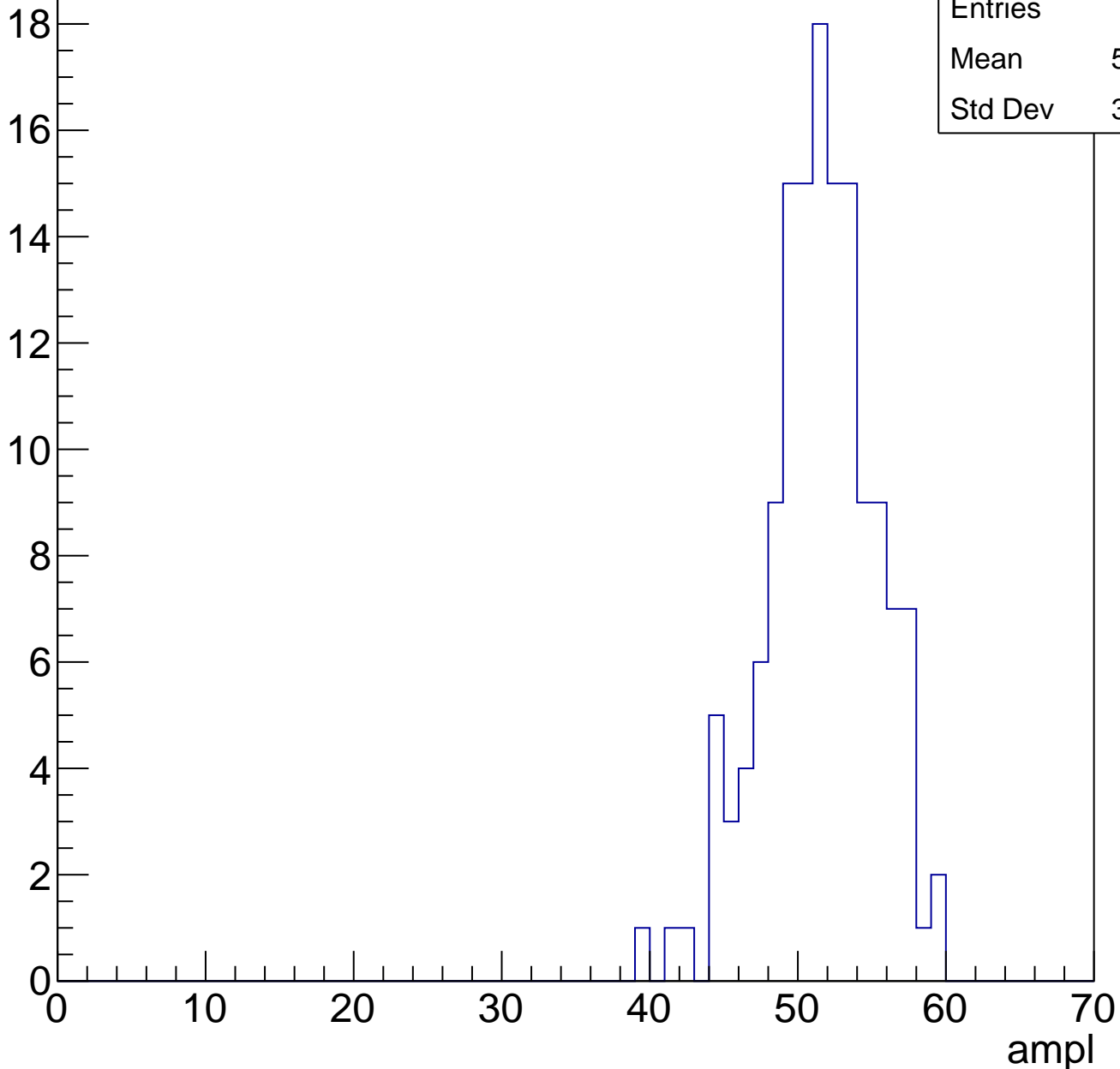


B0L101S, U18-ch32, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	51.06
Std Dev	3.677

Entry



B0L101S, U18-ch32, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

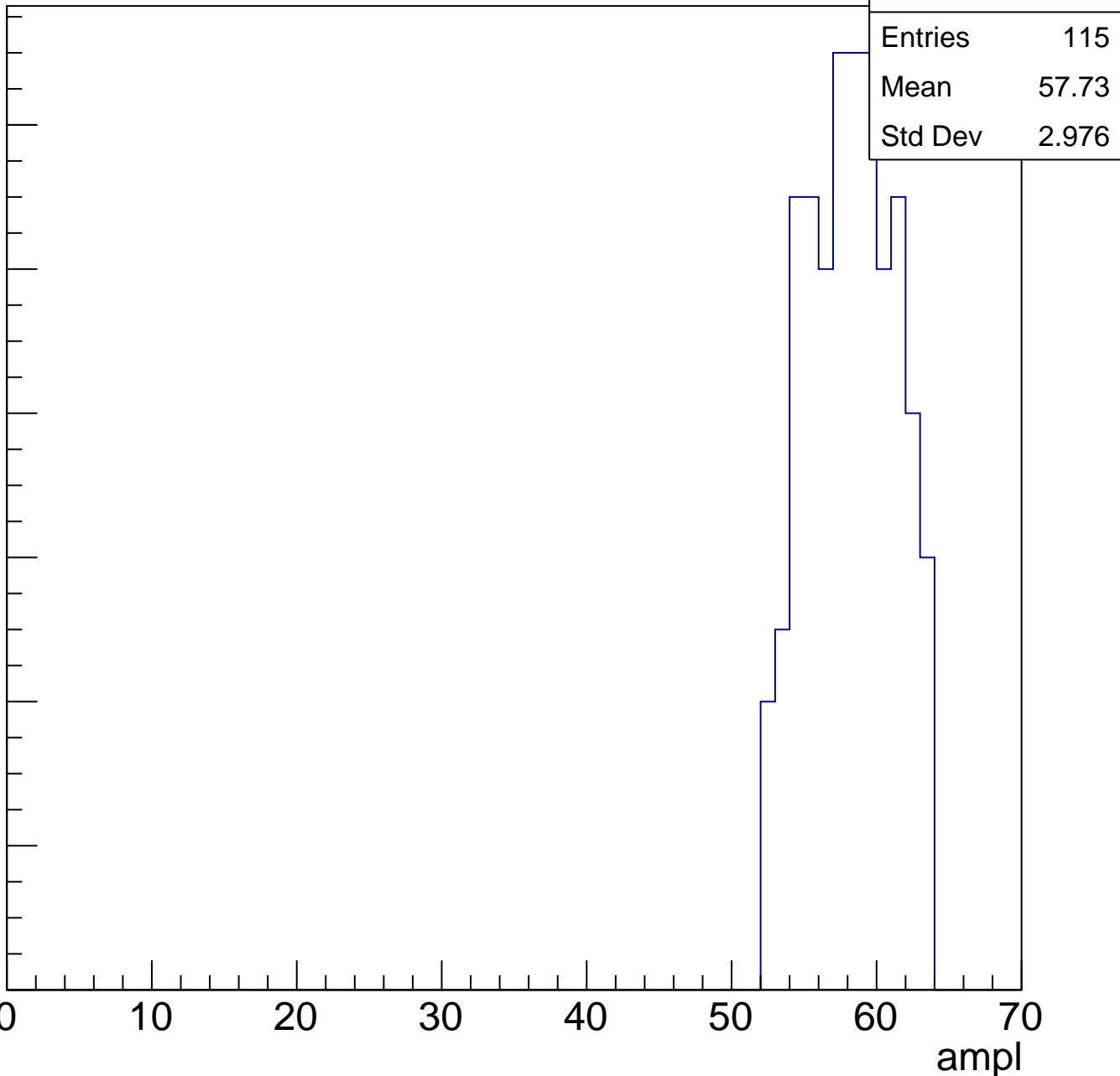
40

50

60

ampl

Entries	115
Mean	57.73
Std Dev	2.976



B0L101S, U18-ch32, adc5

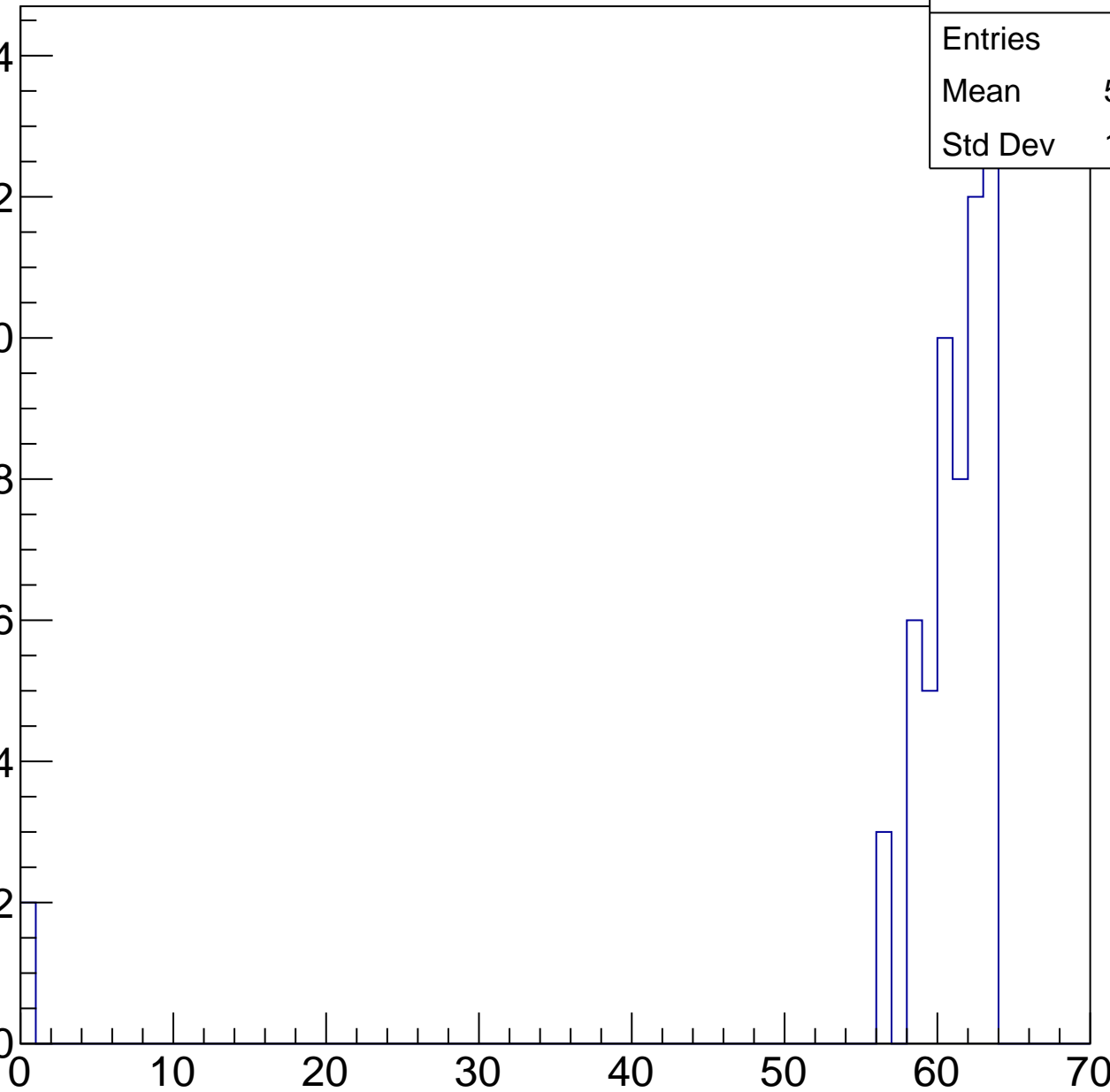
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	60
Mean	58.75
Std Dev	11.08

ampl



B0L101S, U18-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch33, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	30.96
Std Dev	3.576

Gaus mean : 31.5744

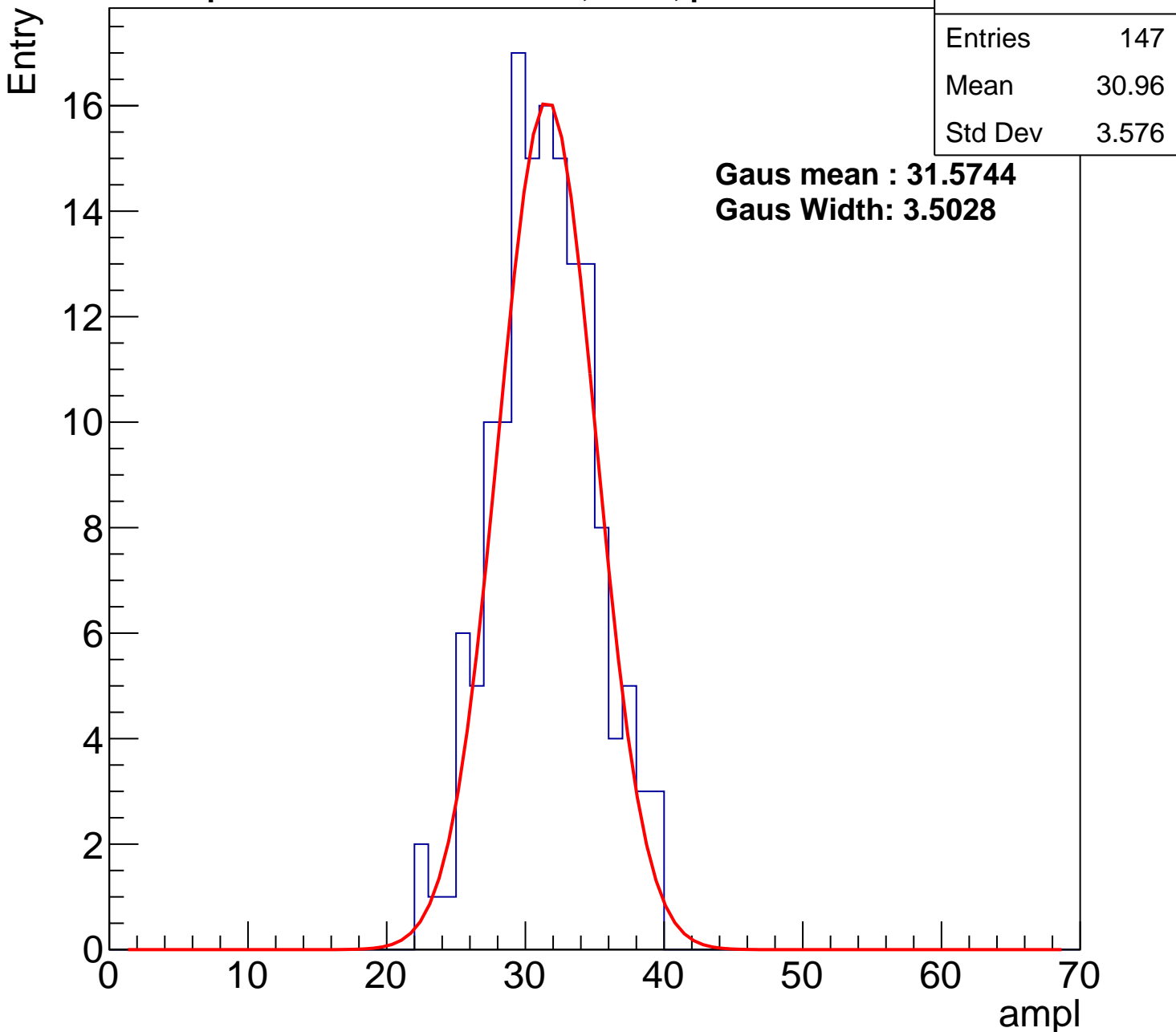
Gaus Width: 3.5028

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch33, adc1

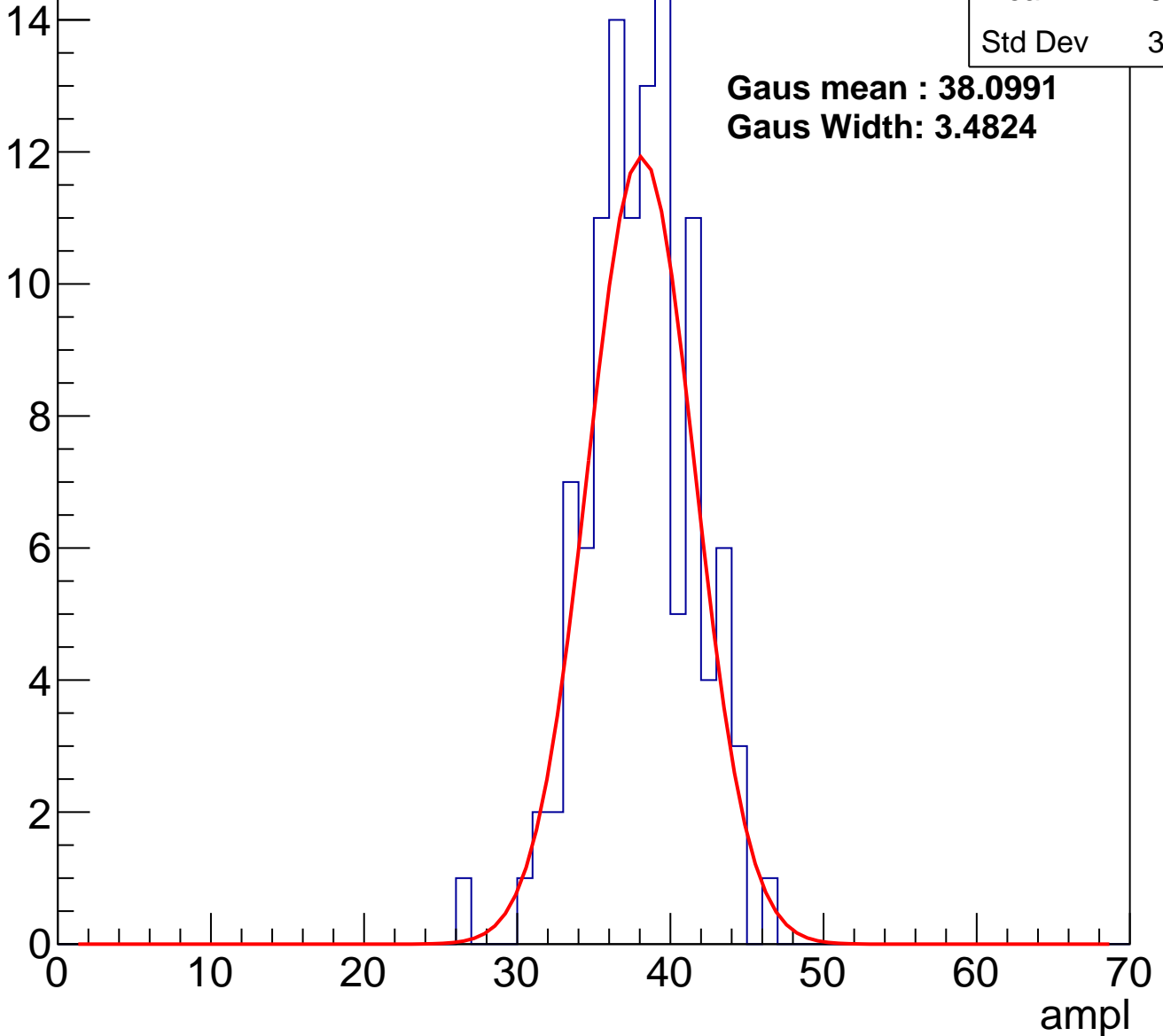
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	113
Mean	37.58
Std Dev	3.412

Gaus mean : 38.0991

Gaus Width: 3.4824



B0L101S, U18-ch33, adc2

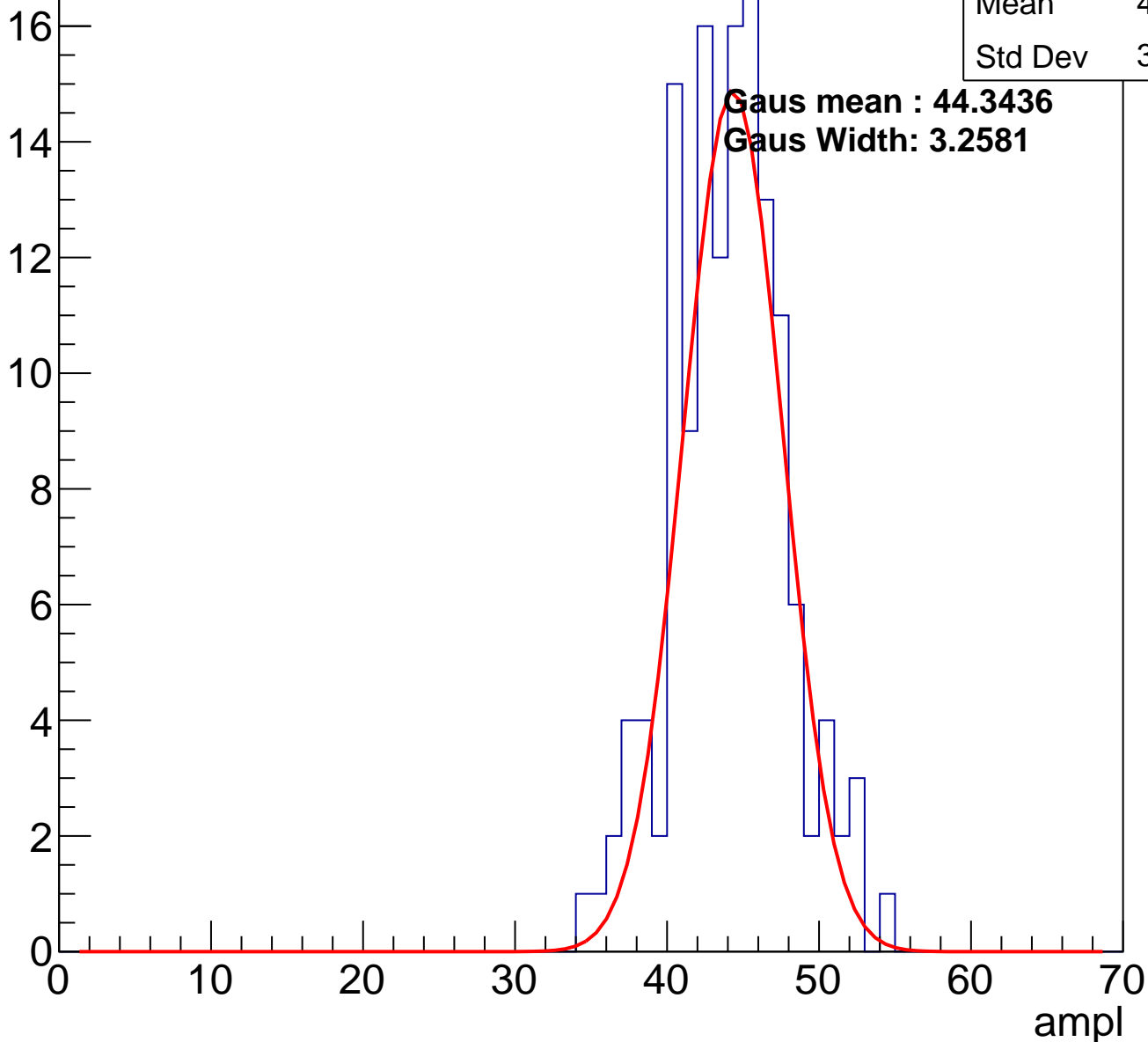
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	141
Mean	43.67
Std Dev	3.699

Gaus mean : 44.3436

Gaus Width: 3.2581

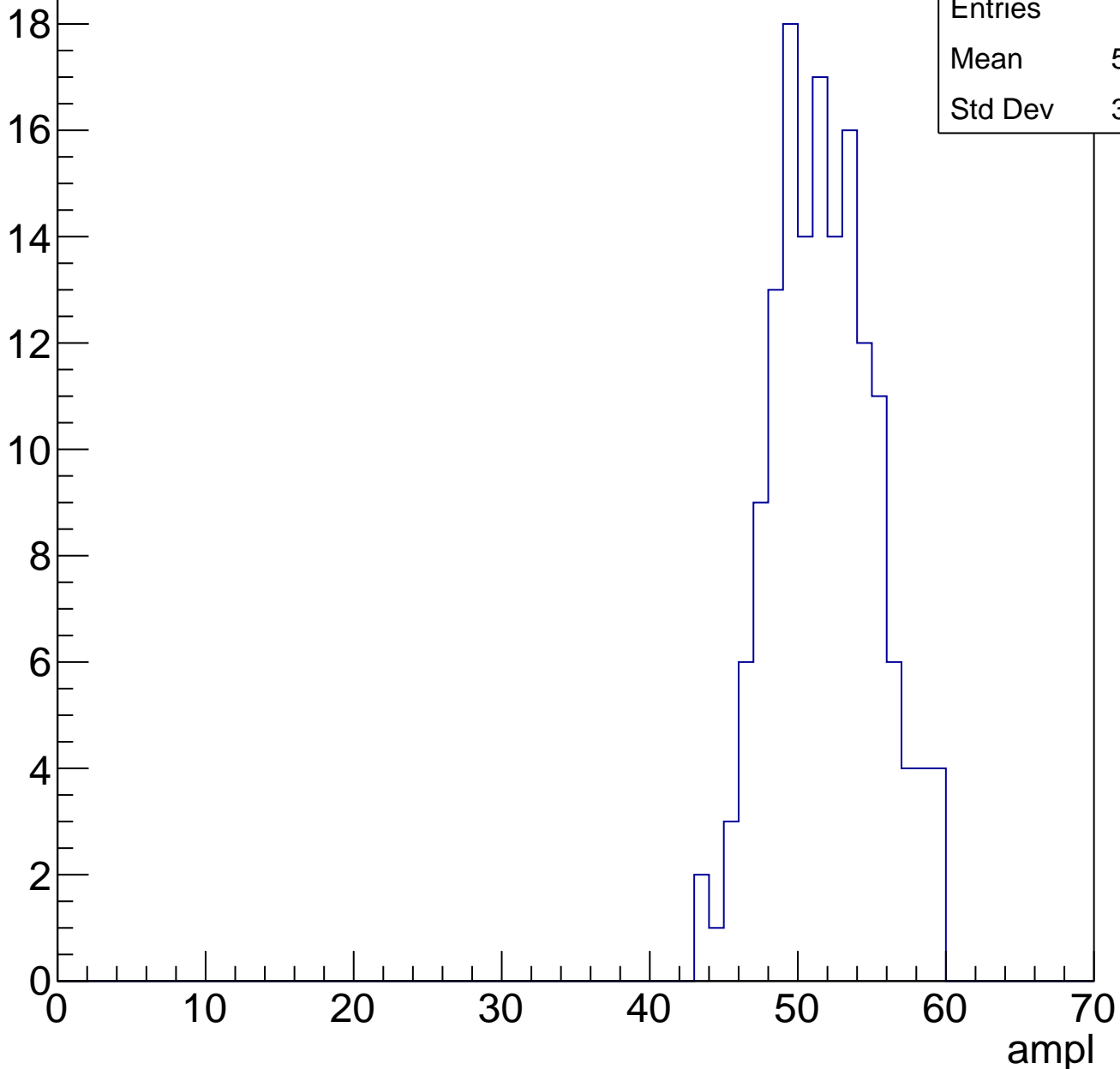


B0L101S, U18-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	51.29
Std Dev	3.492

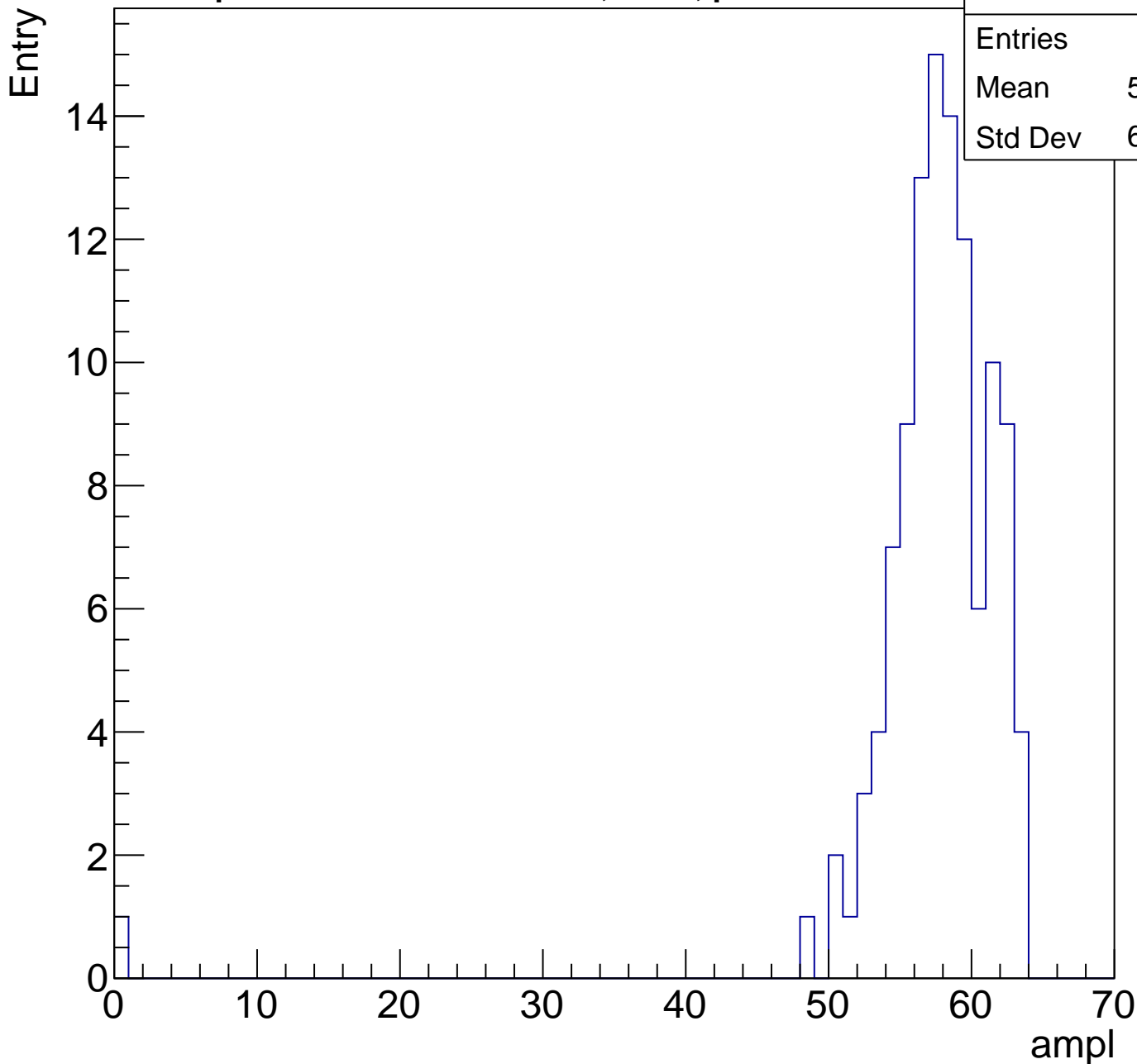
Entry



B0L101S, U18-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	56.96
Std Dev	6.267



B0L101S, U18-ch33, adc5

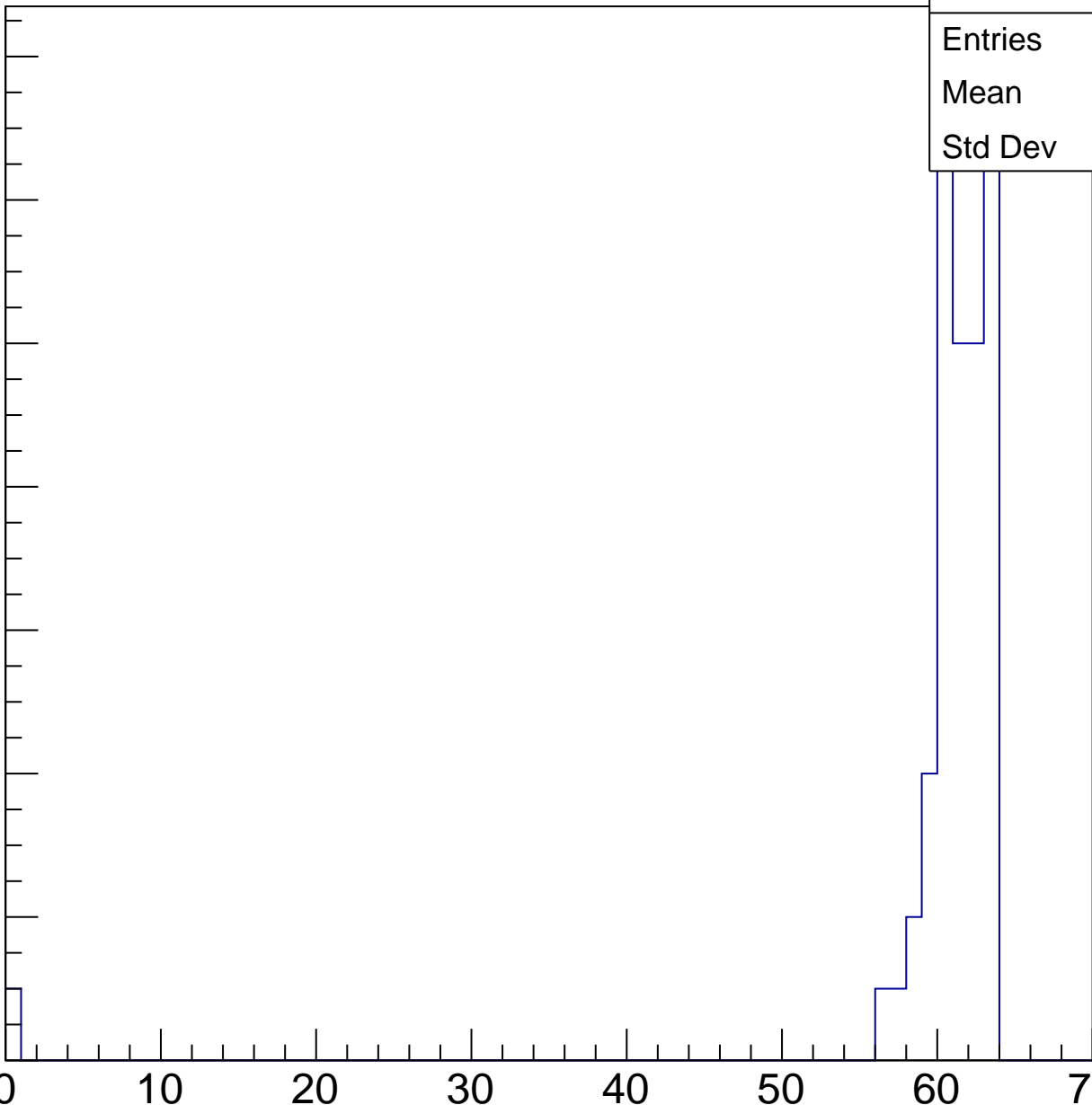
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	57
Mean	59.95
Std Dev	8.179

ampl



B0L101S, U18-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch34, adc0

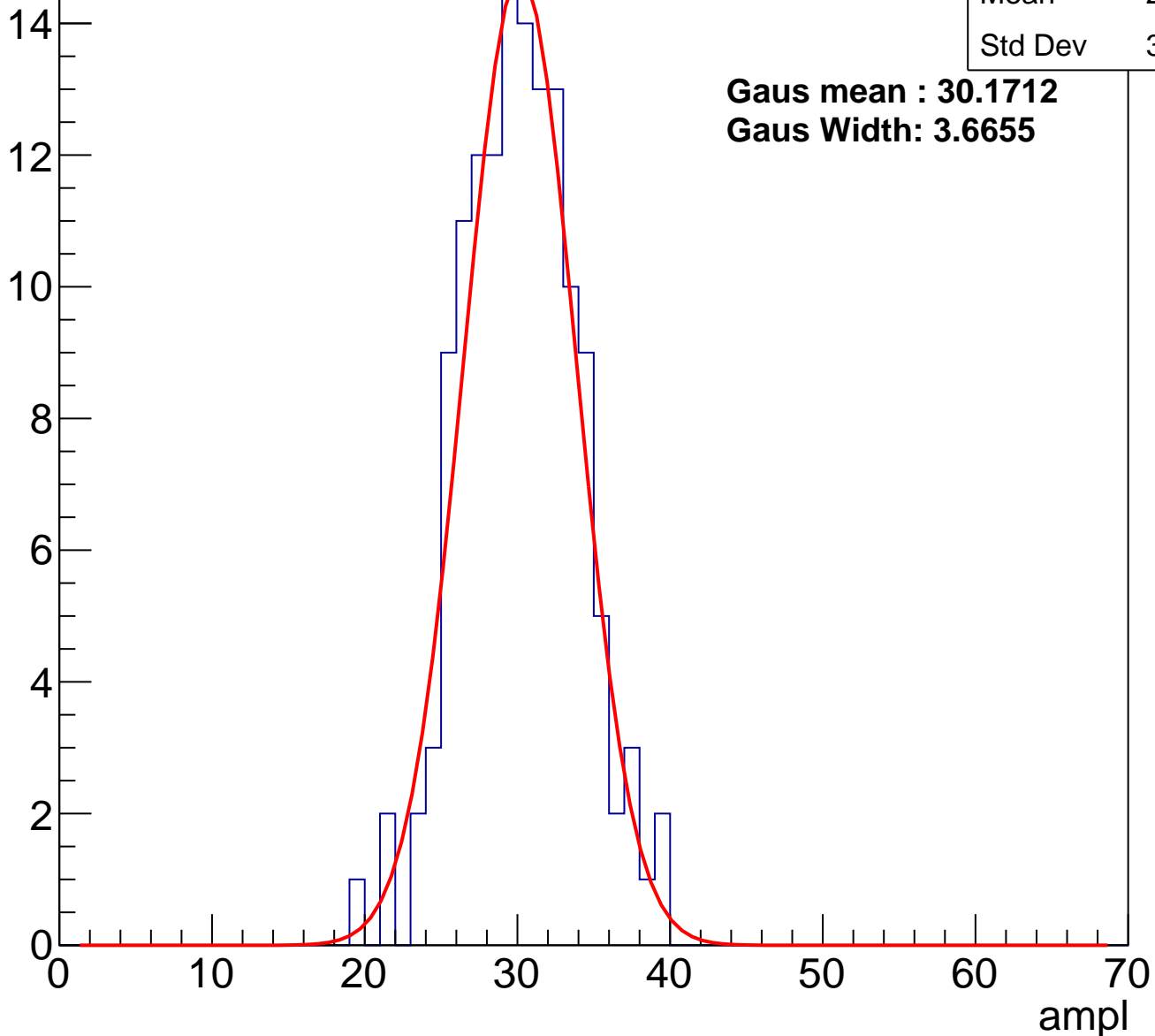
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	29.74
Std Dev	3.717

Gaus mean : 30.1712

Gaus Width: 3.6655

Entry



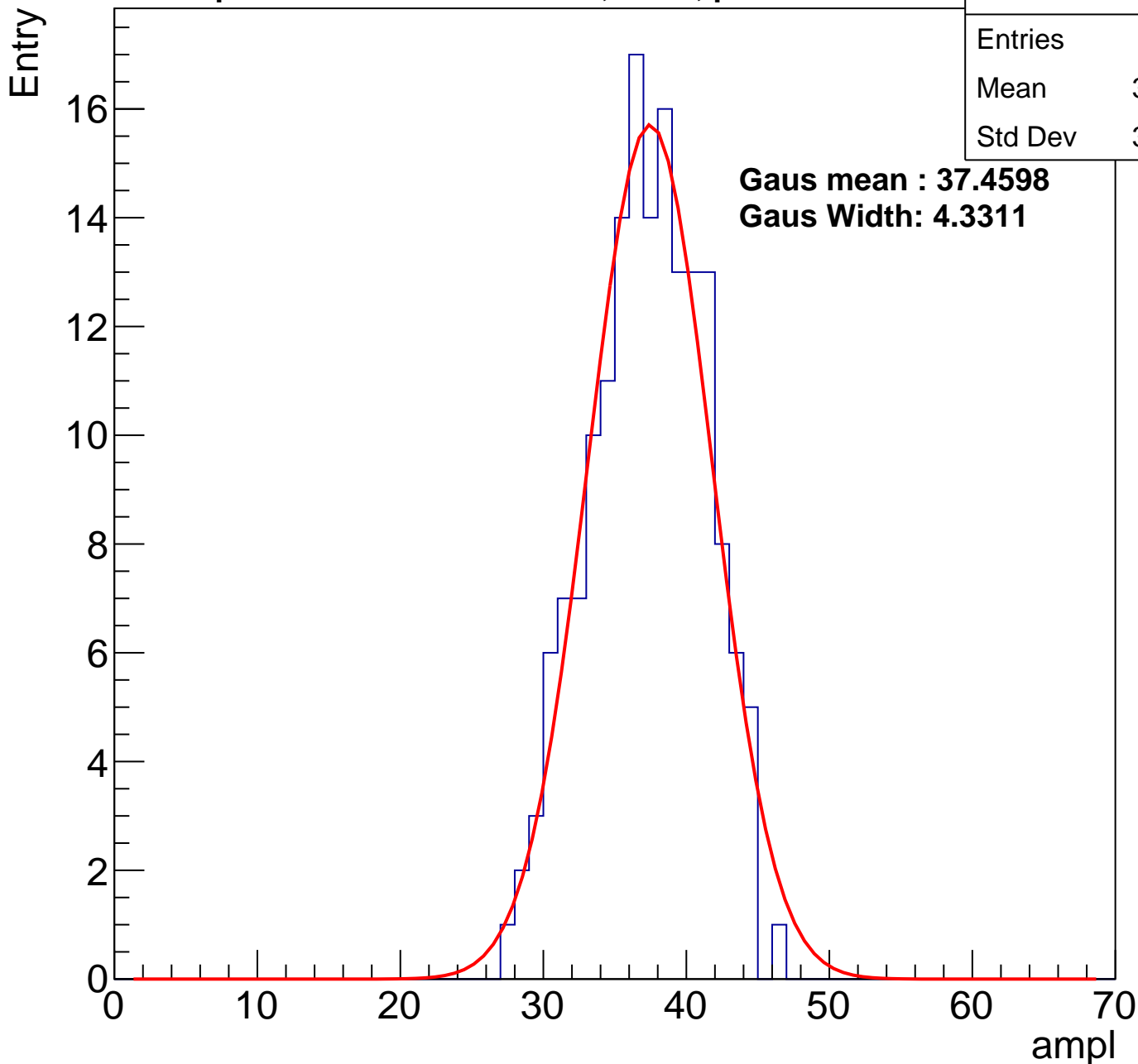
B0L101S, U18-ch34, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	36.78
Std Dev	3.972

Gaus mean : 37.4598

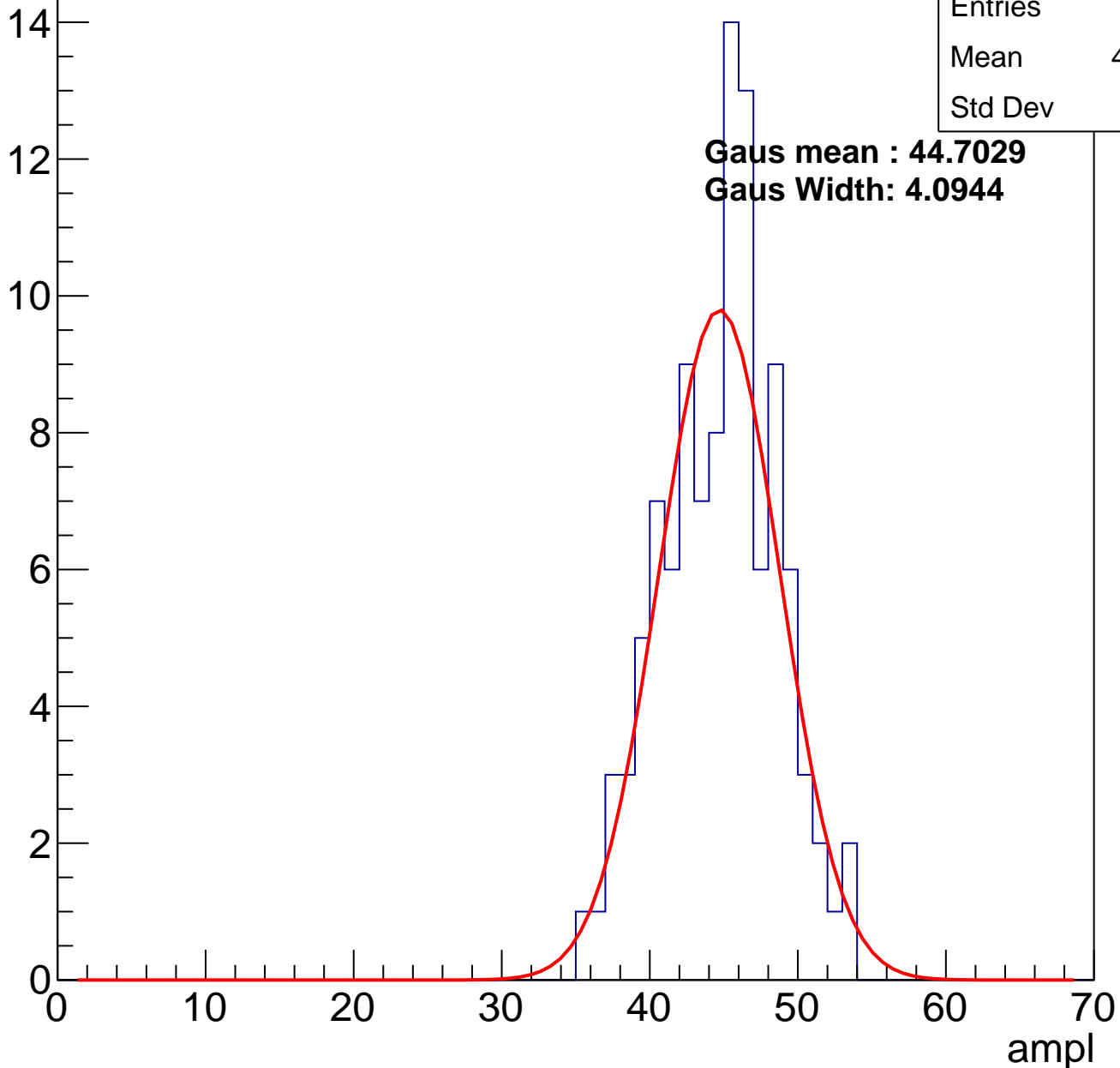
Gaus Width: 4.3311



B0L101S, U18-ch34, adc2

calib_packv5_042523_0143.root, FC#1, port C1

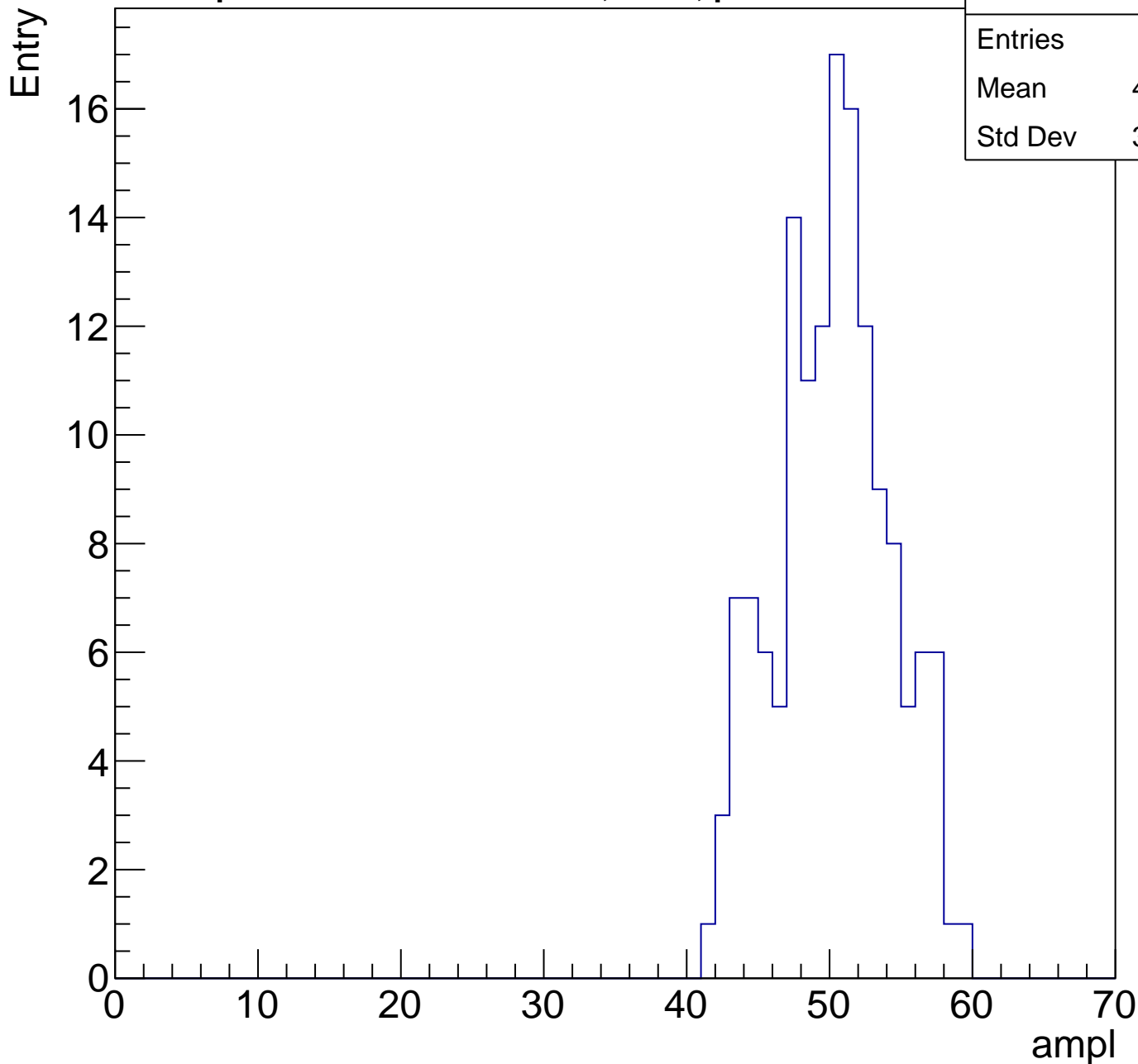
Entry



B0L101S, U18-ch34, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	49.79
Std Dev	3.978



B0L101S, U18-ch34, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	56.25
Std Dev	3.169

Entry

12

10

8

6

4

2

0

0

10

20

30

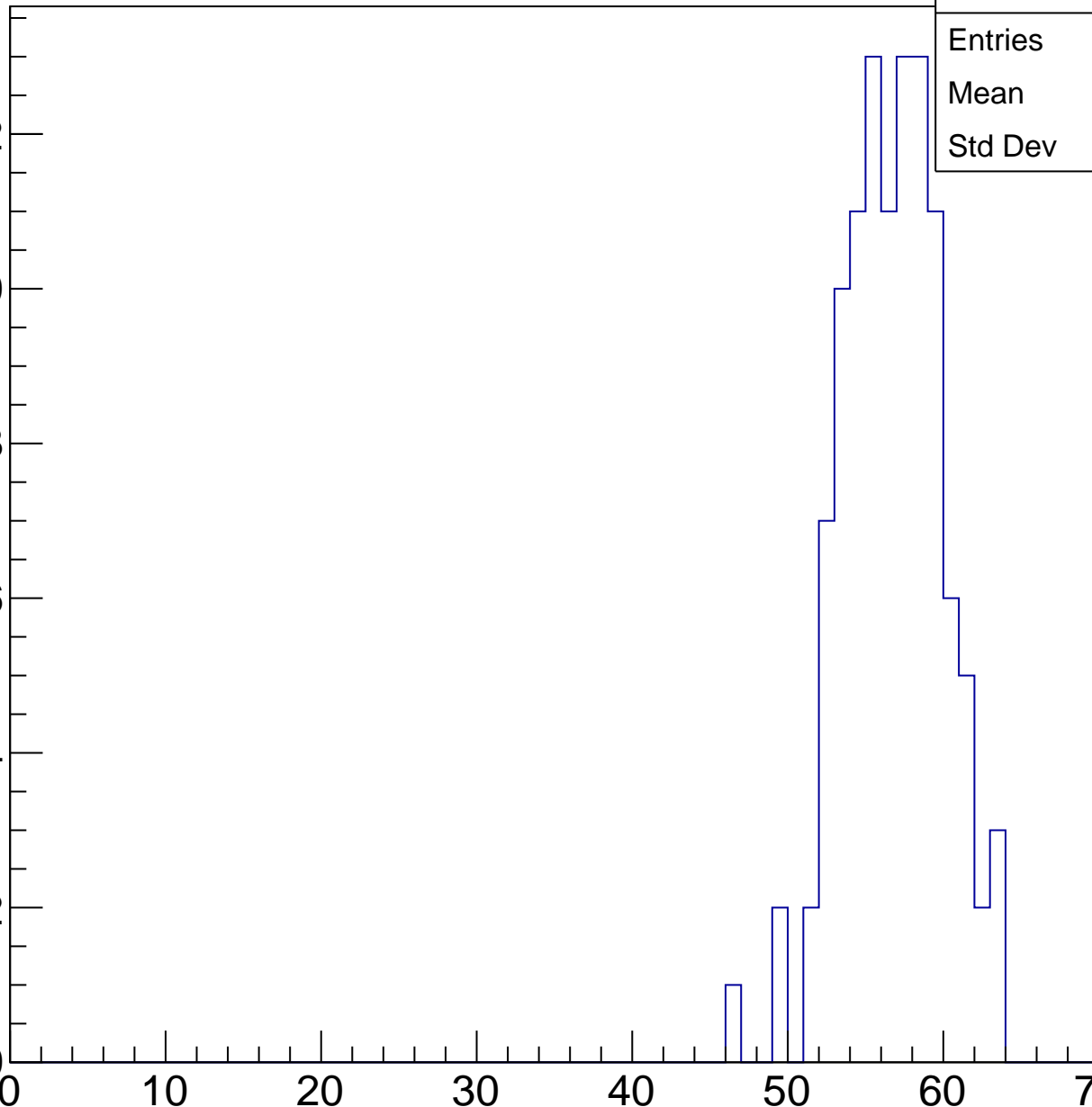
40

50

60

70

ampl



B0L101S, U18-ch34, adc5

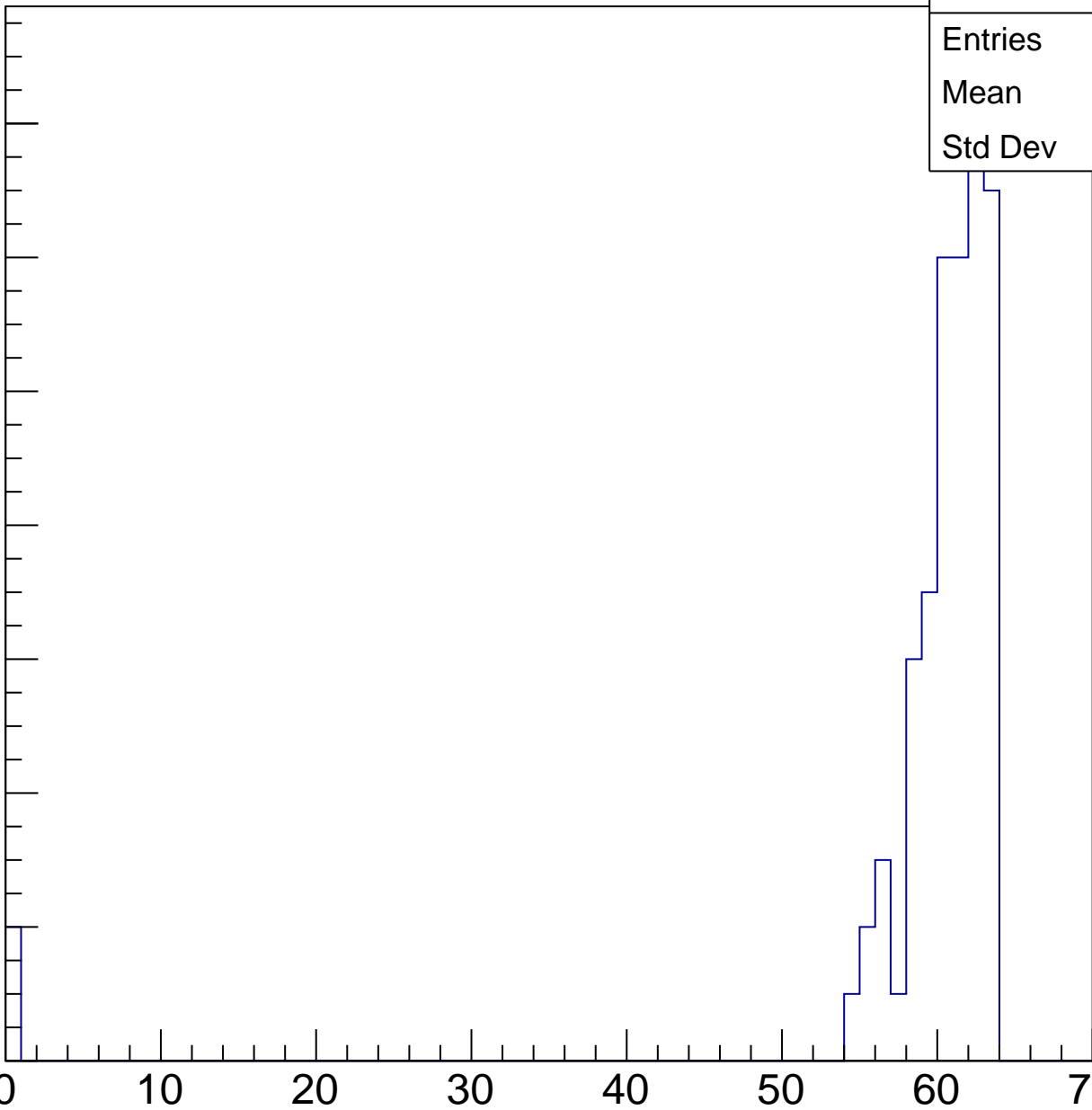
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	58.8
Std Dev	10.04

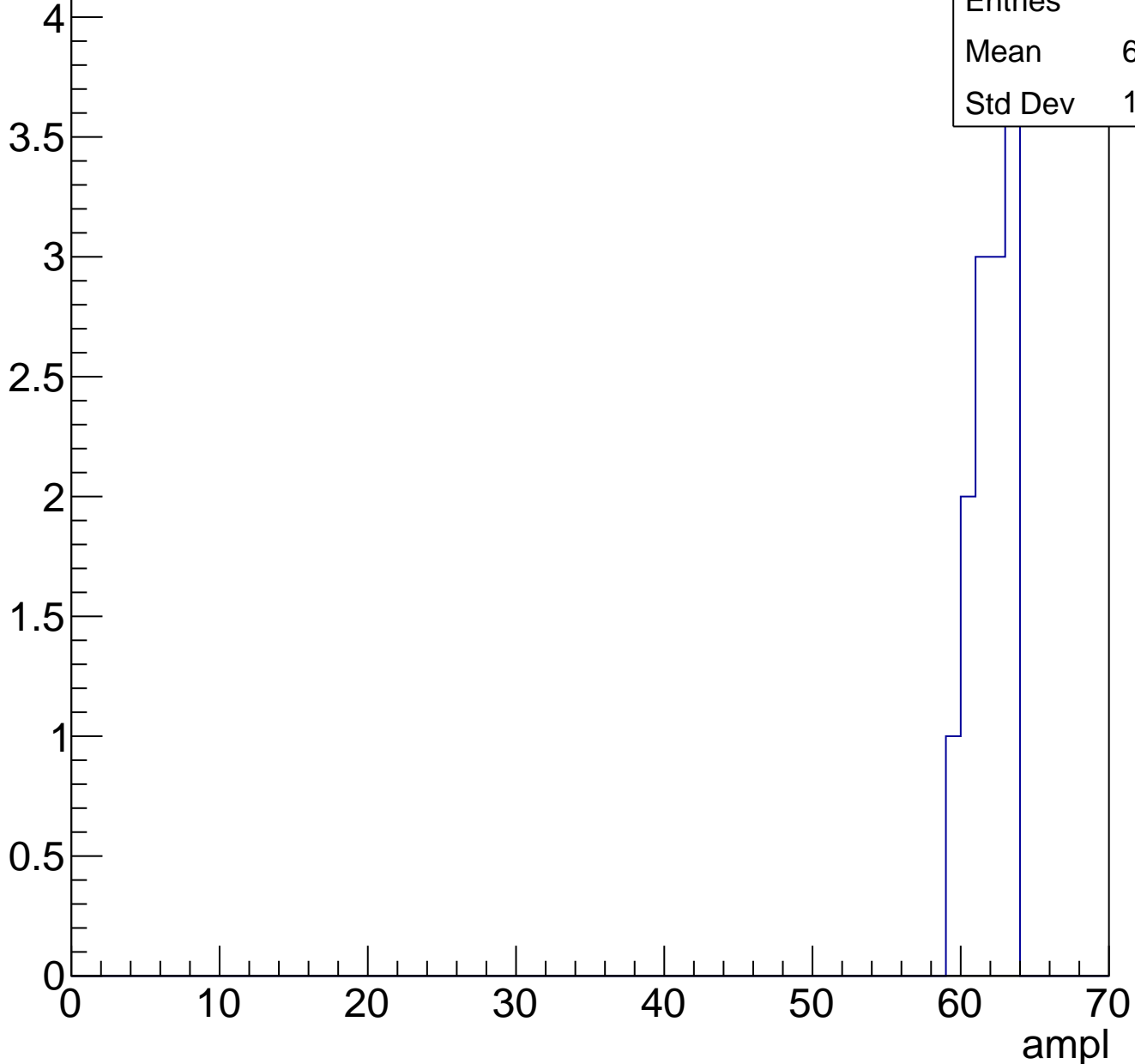
ampl



B0L101S, U18-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch35, adc0

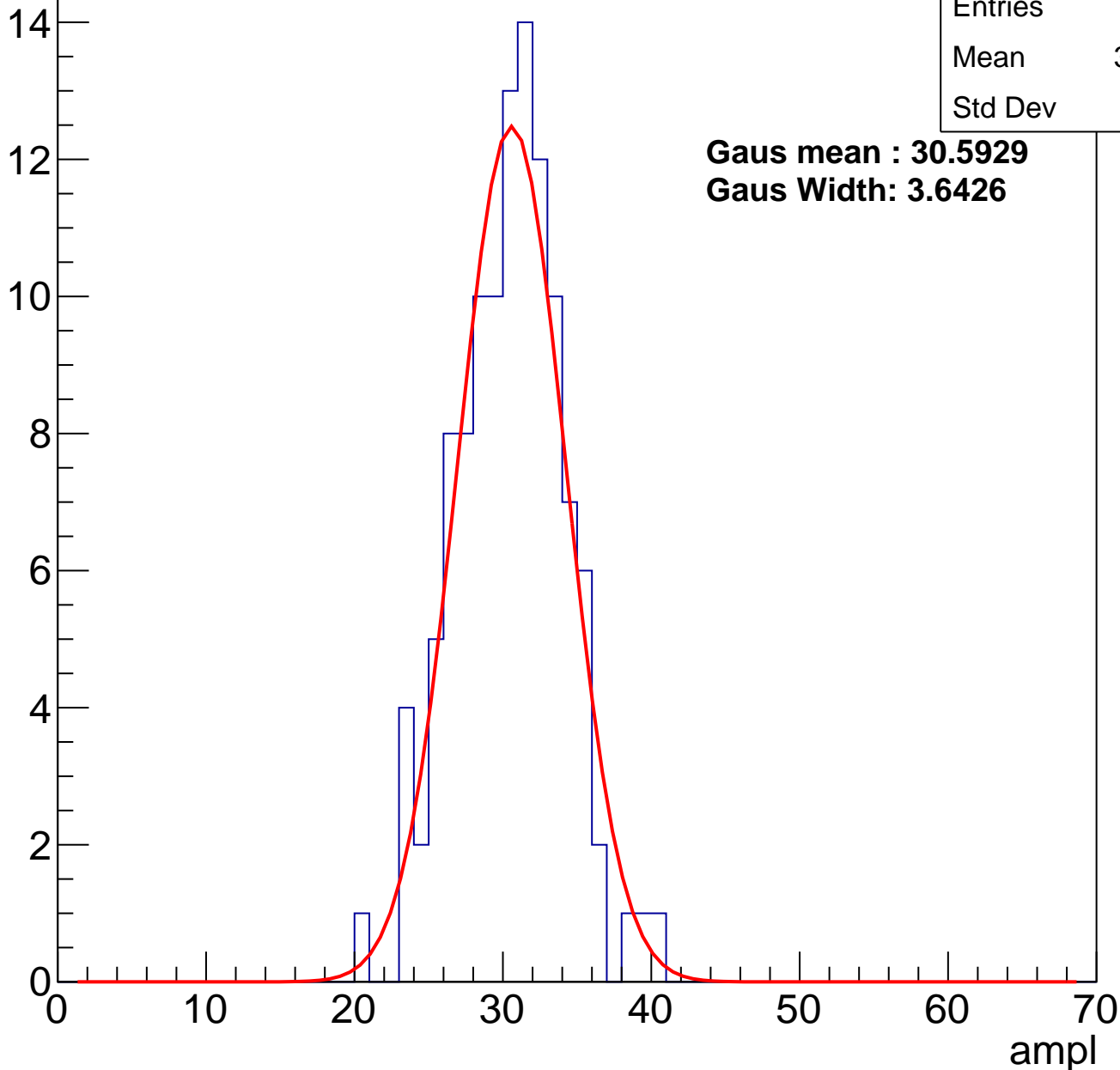
calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	30.03
Std Dev	3.58

Gaus mean : 30.5929

Gaus Width: 3.6426

Entry



B0L101S, U18-ch35, adc1

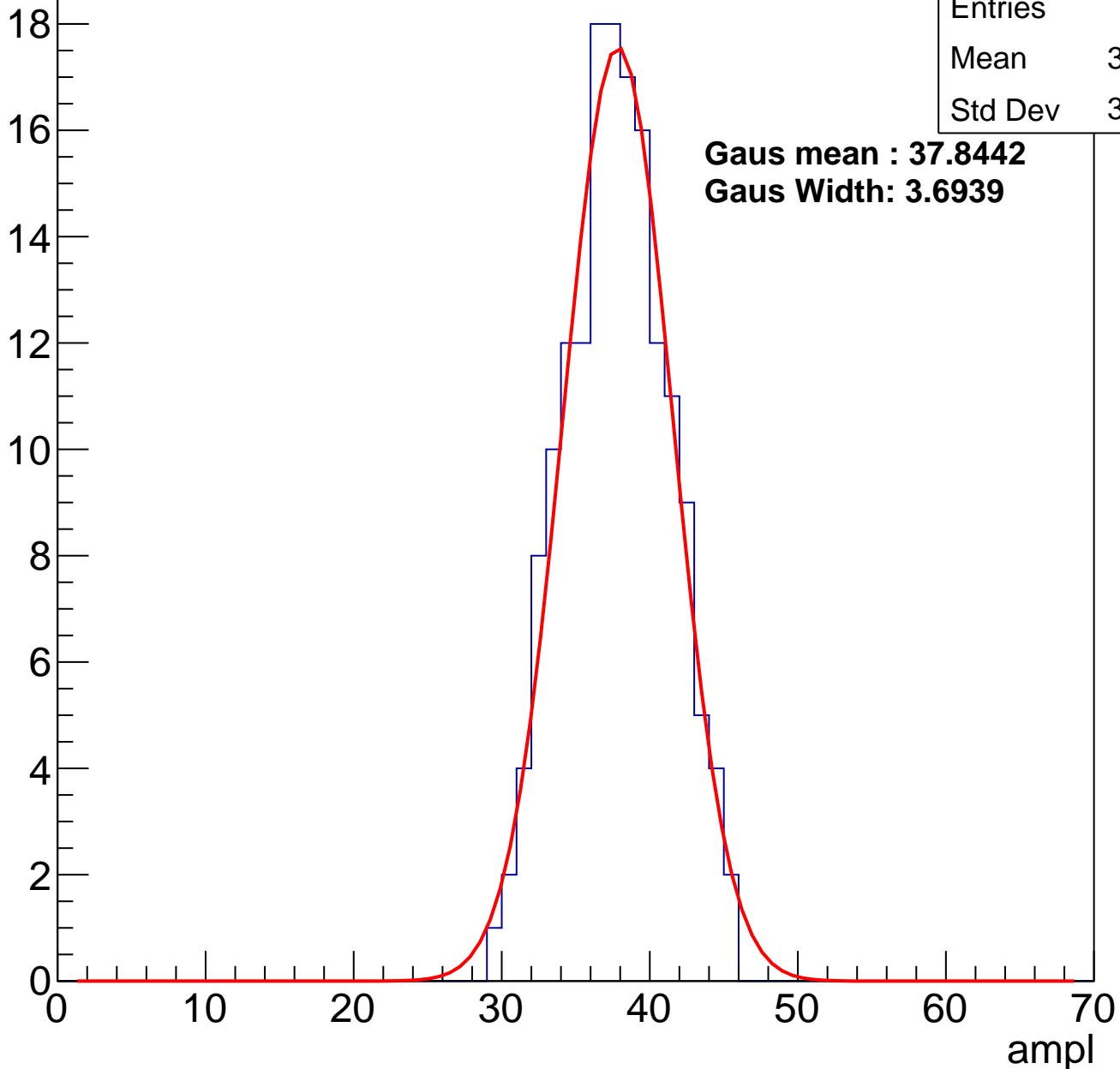
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	37.27
Std Dev	3.455

Gaus mean : 37.8442

Gaus Width: 3.6939

Entry



B0L101S, U18-ch35, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

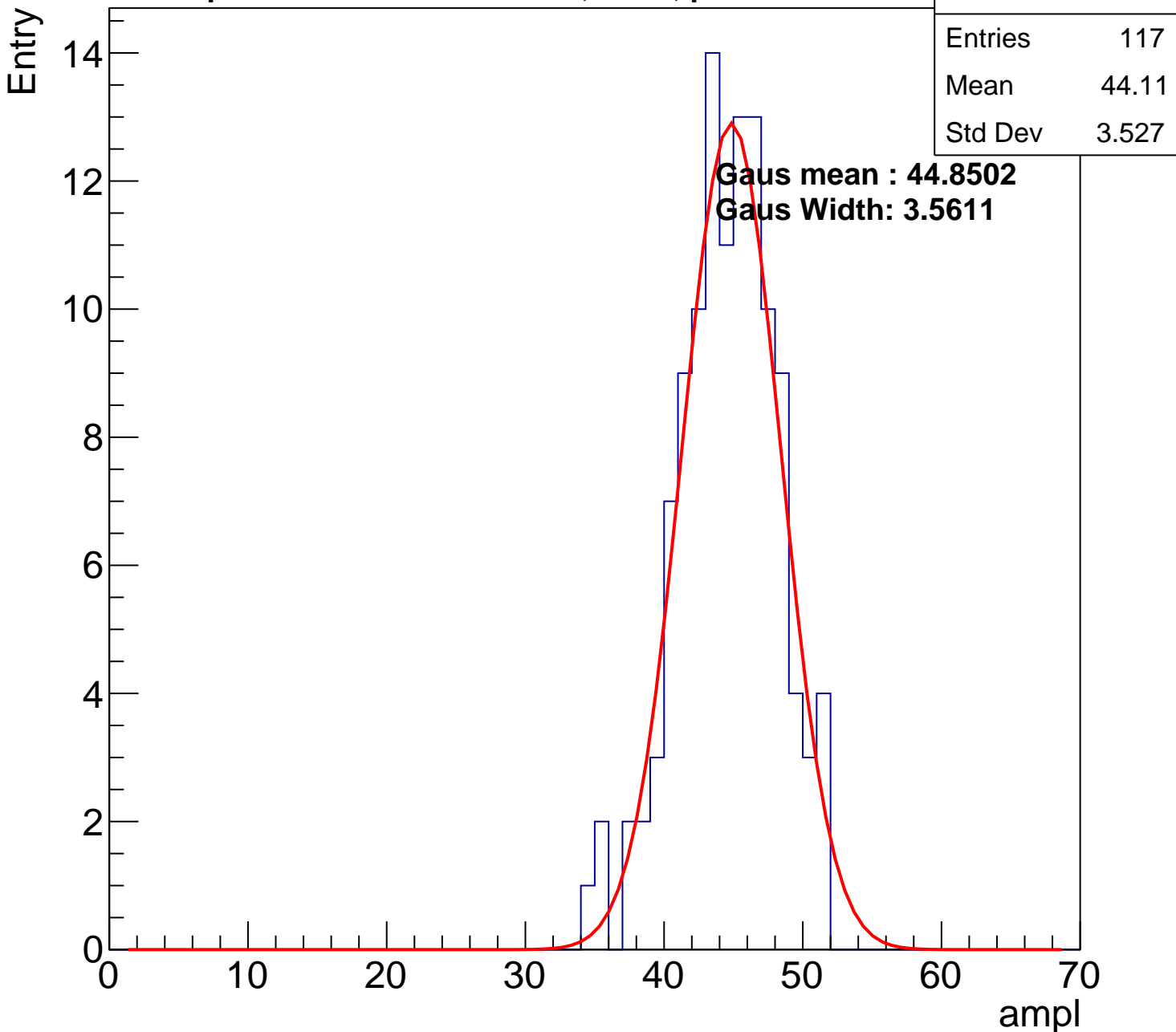
Entries	117
Mean	44.11
Std Dev	3.527

Gaus mean : 44.8502

Gaus Width: 3.5611

ampl

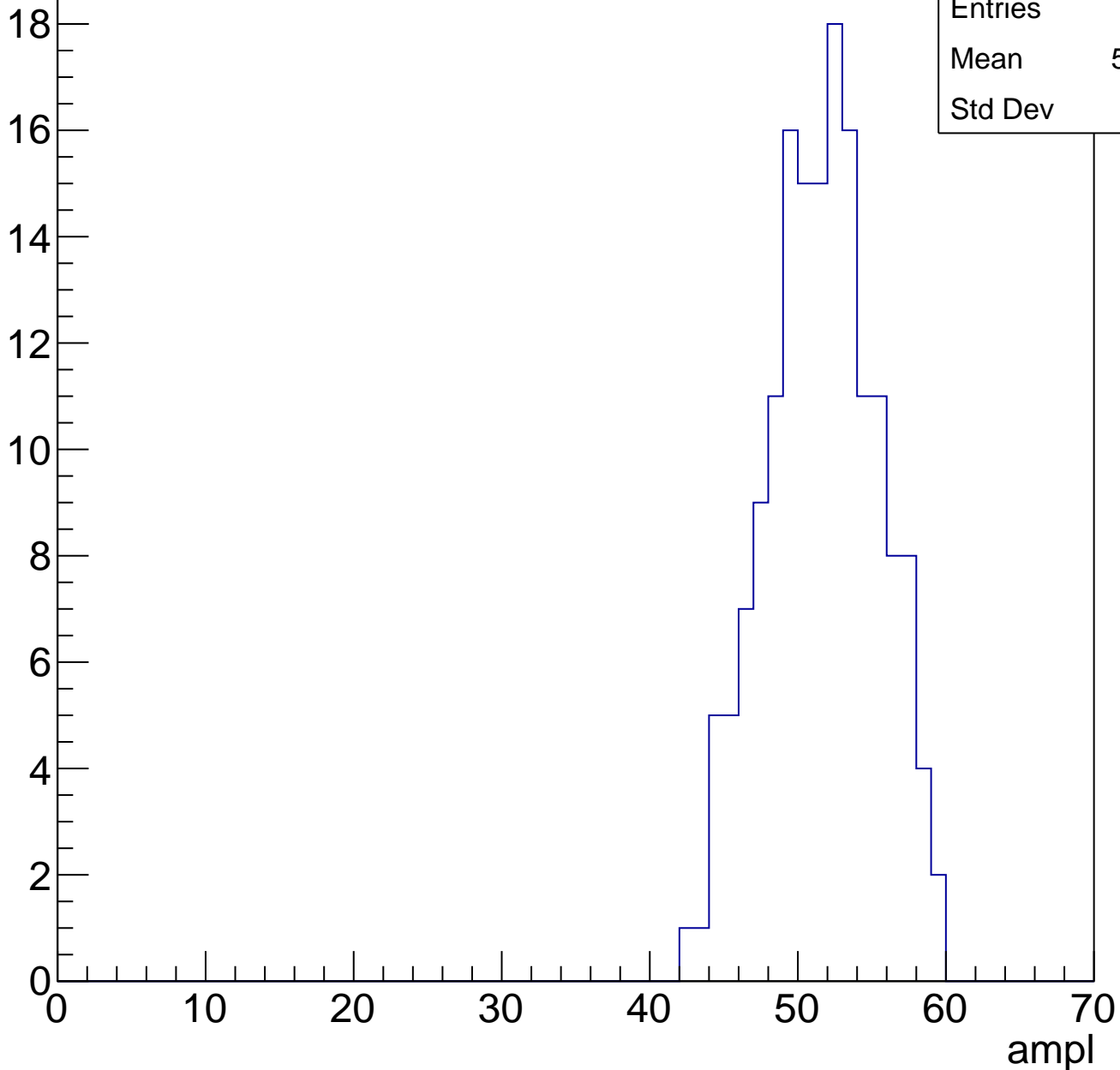
0 10 20 30 40 50 60 70



B0L101S, U18-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

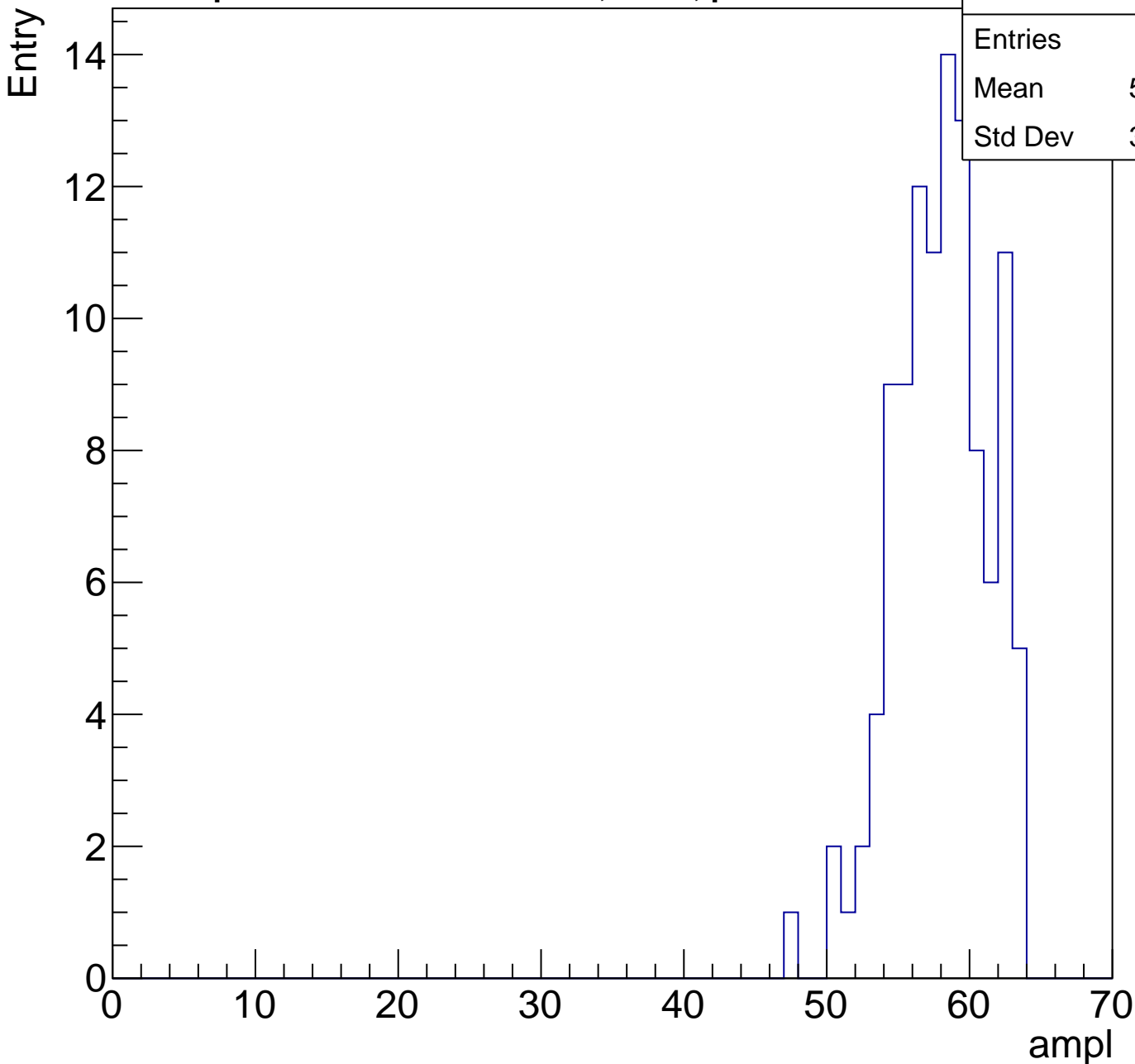
Entry



B0L101S, U18-ch35, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	57.56
Std Dev	3.238



B0L101S, U18-ch35, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

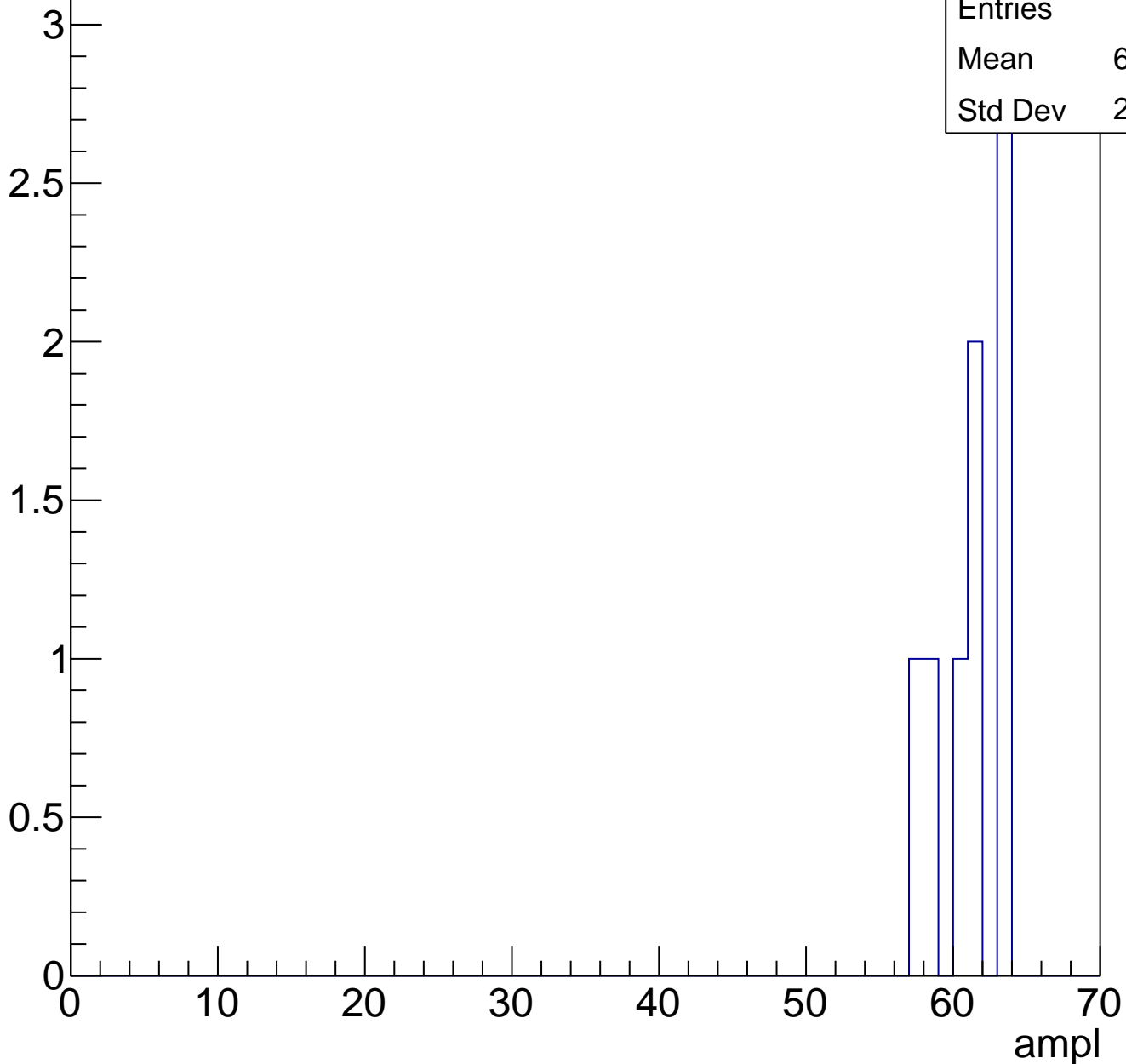
ampl

Entries	52
Mean	58.83
Std Dev	11.84

B0L101S, U18-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

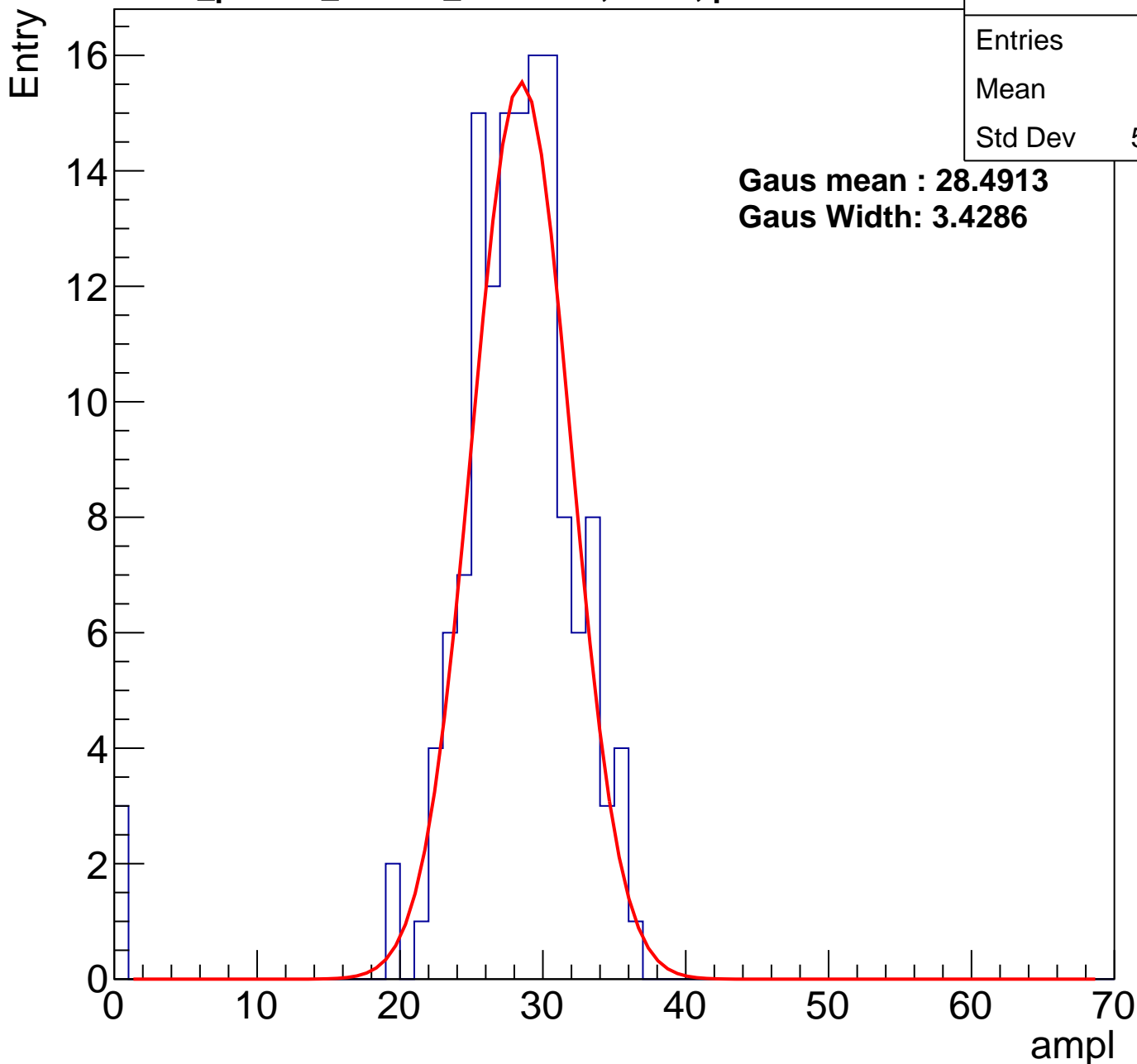
B0L101S, U18-ch36, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	27.4
Std Dev	5.262

Gaus mean : 28.4913

Gaus Width: 3.4286

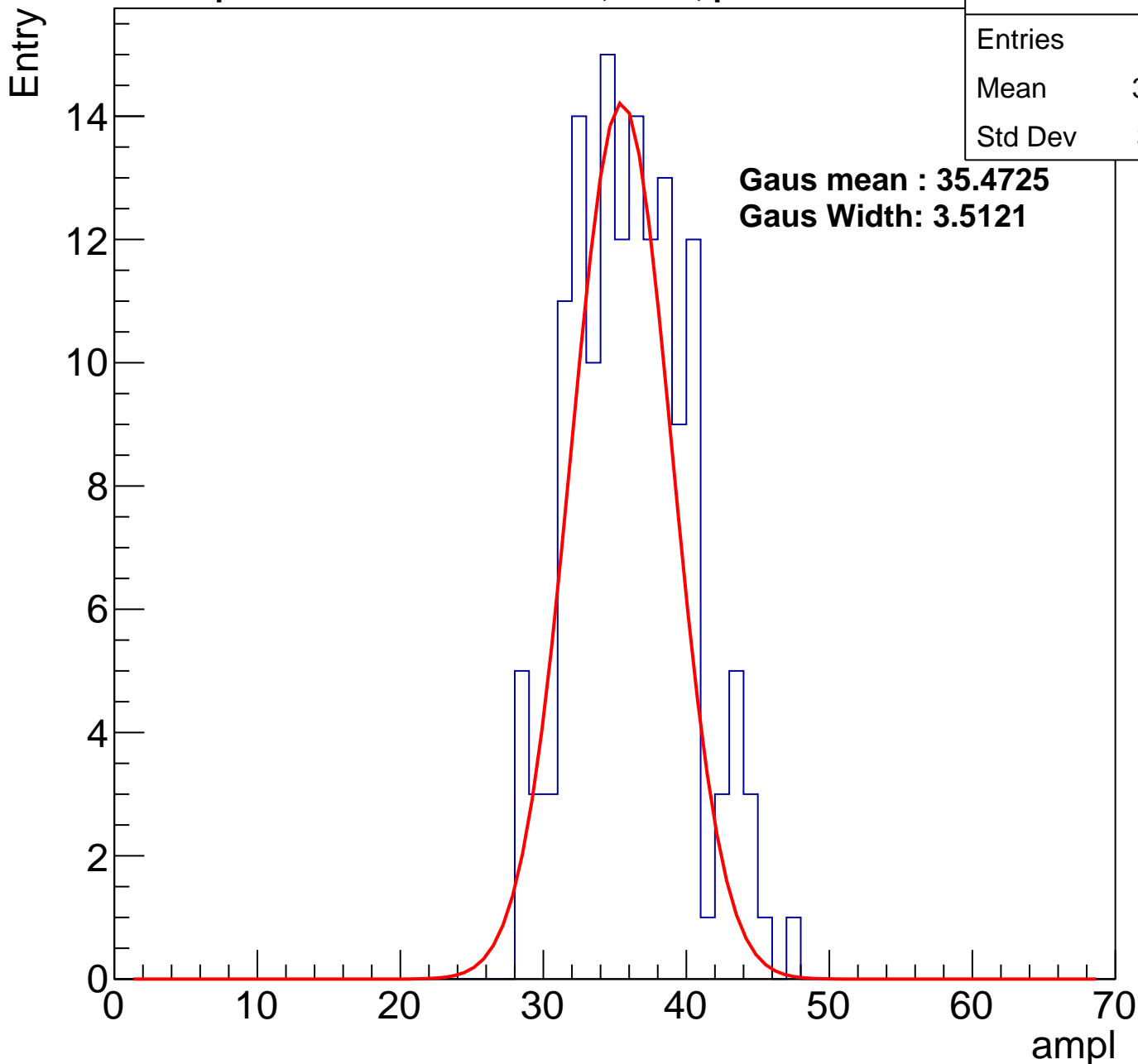


B0L101S, U18-ch36, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	35.68
Std Dev	3.981

Gaus mean : 35.4725
Gaus Width: 3.5121



B0L101S, U18-ch36, adc2

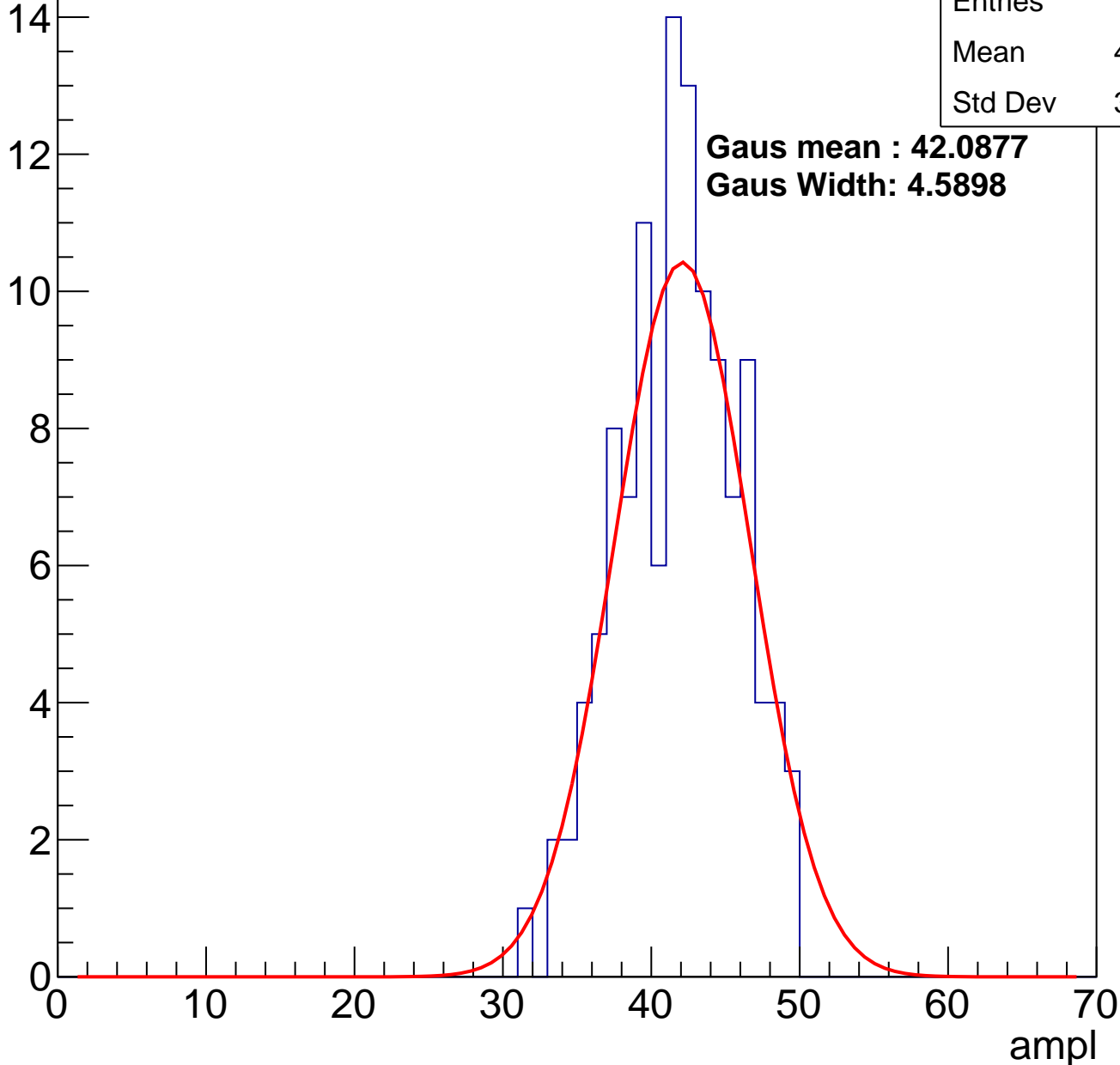
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	119
Mean	41.33
Std Dev	3.917

Gaus mean : 42.0877

Gaus Width: 4.5898



B0L101S, U18-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

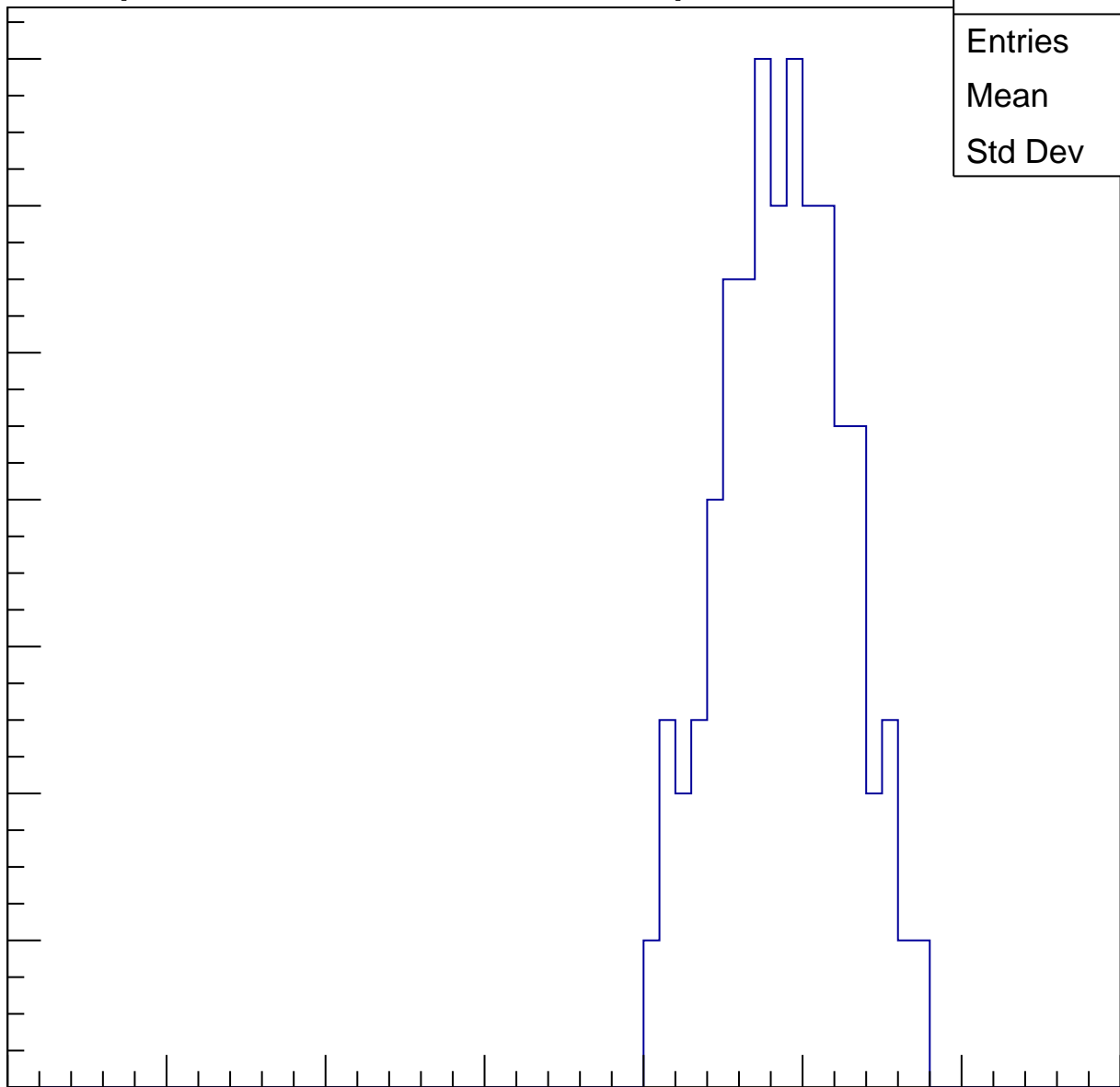
Entries	141
Mean	48.33
Std Dev	3.886

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

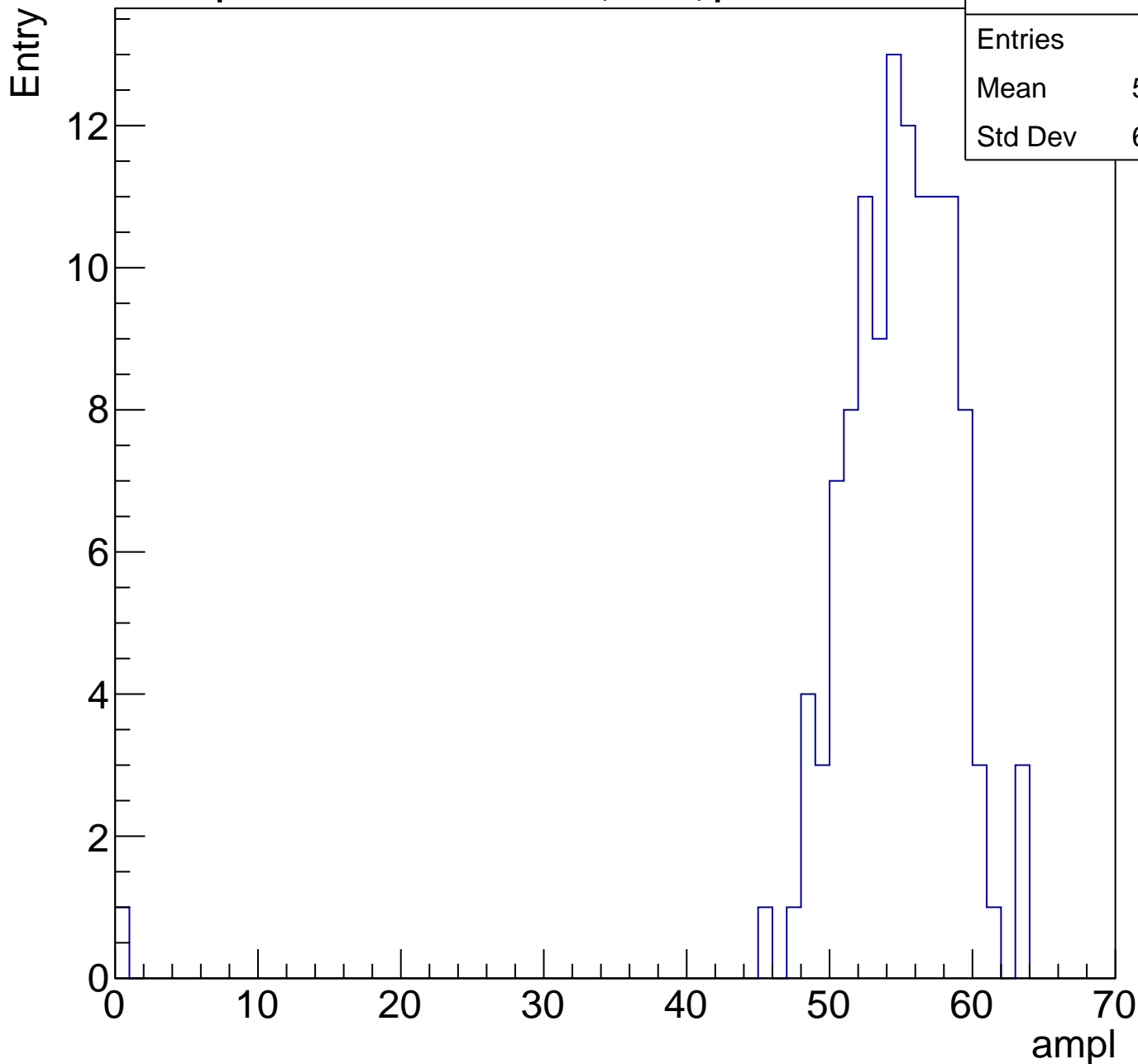
ampl



B0L101S, U18-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	54.09
Std Dev	6.129



B0L101S, U18-ch36, adc5

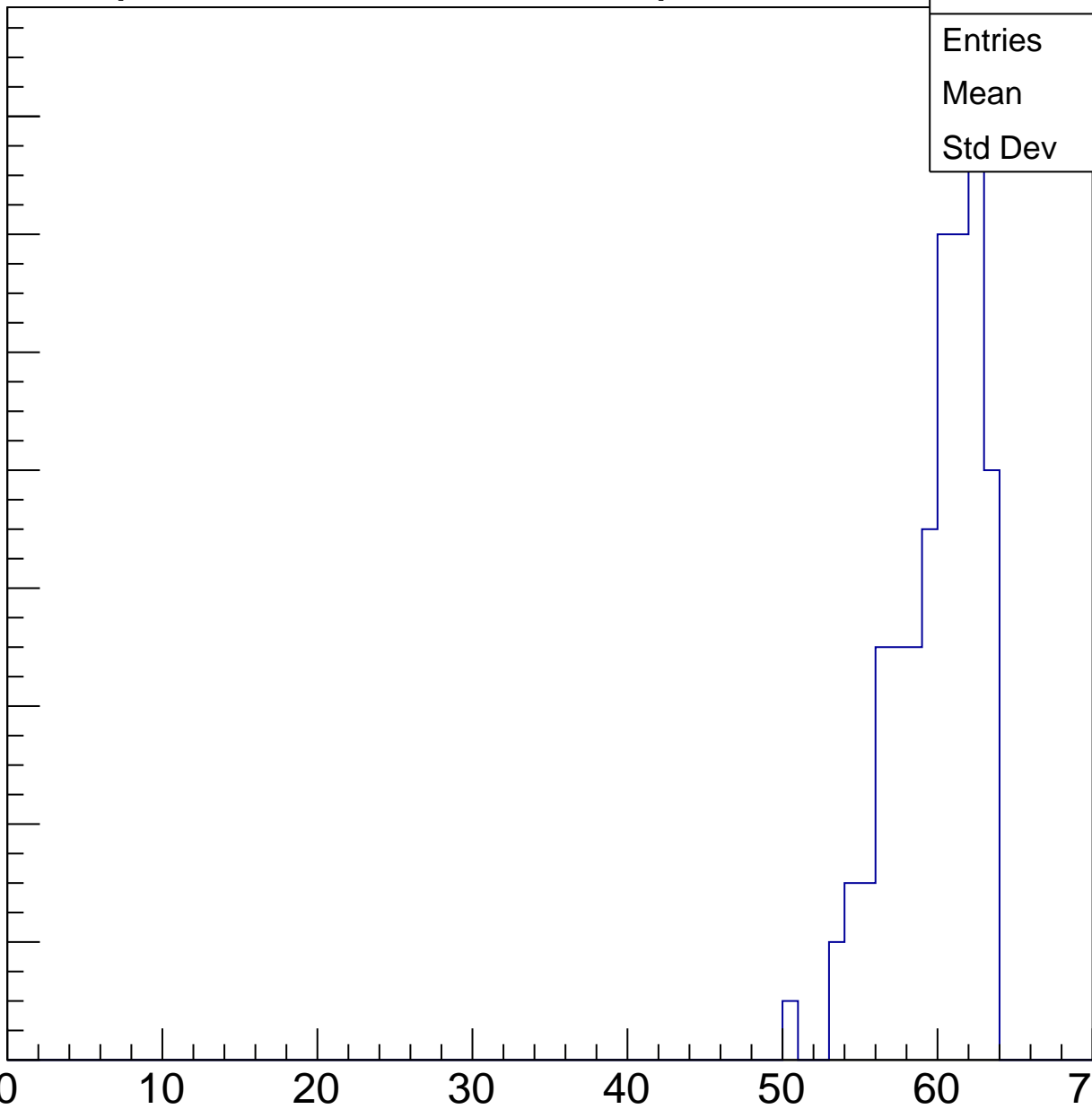
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	59.46
Std Dev	2.801

ampl

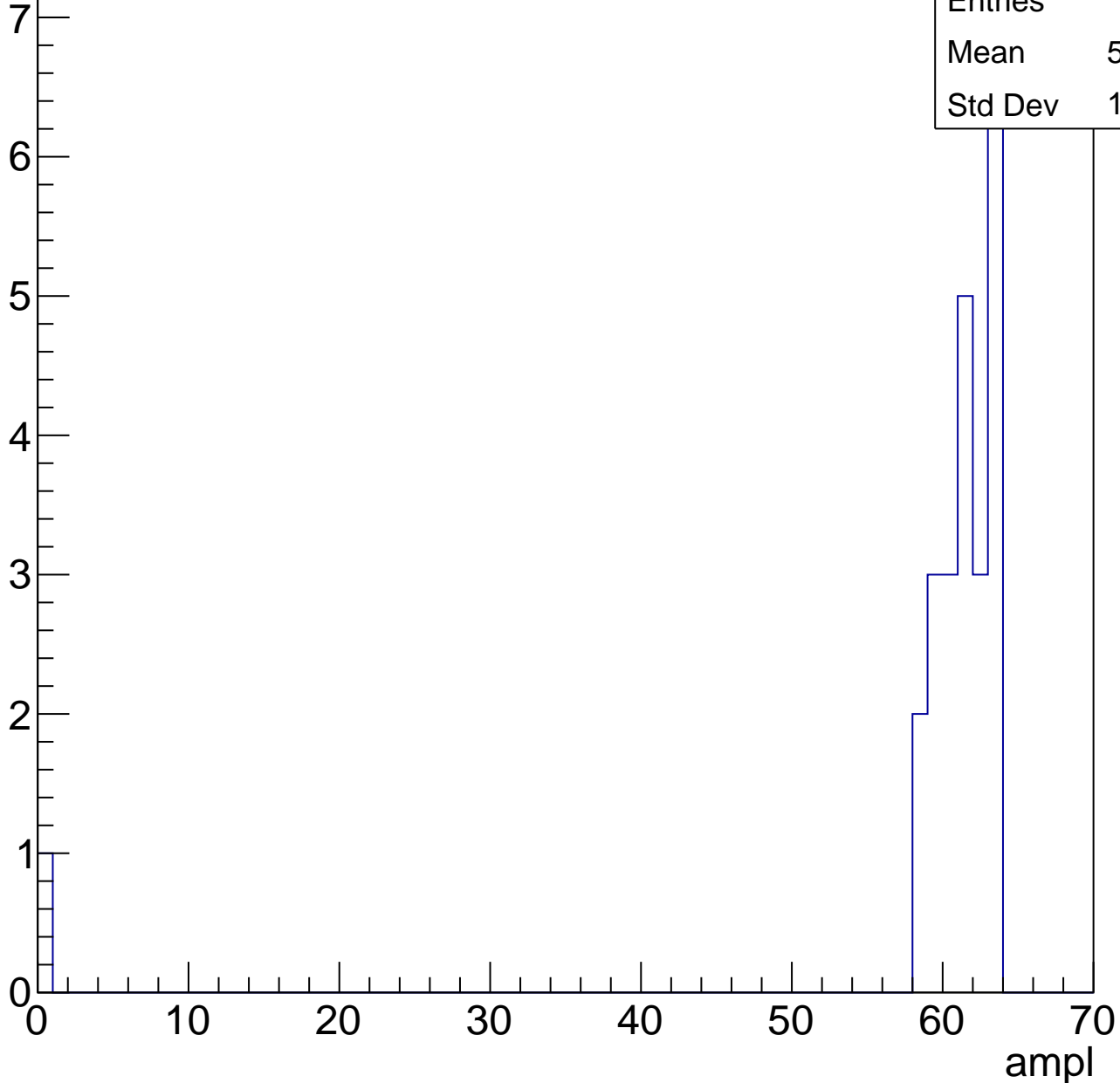


B0L101S, U18-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	24
Mean	58.54
Std Dev	12.32



B0L101S, U18-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

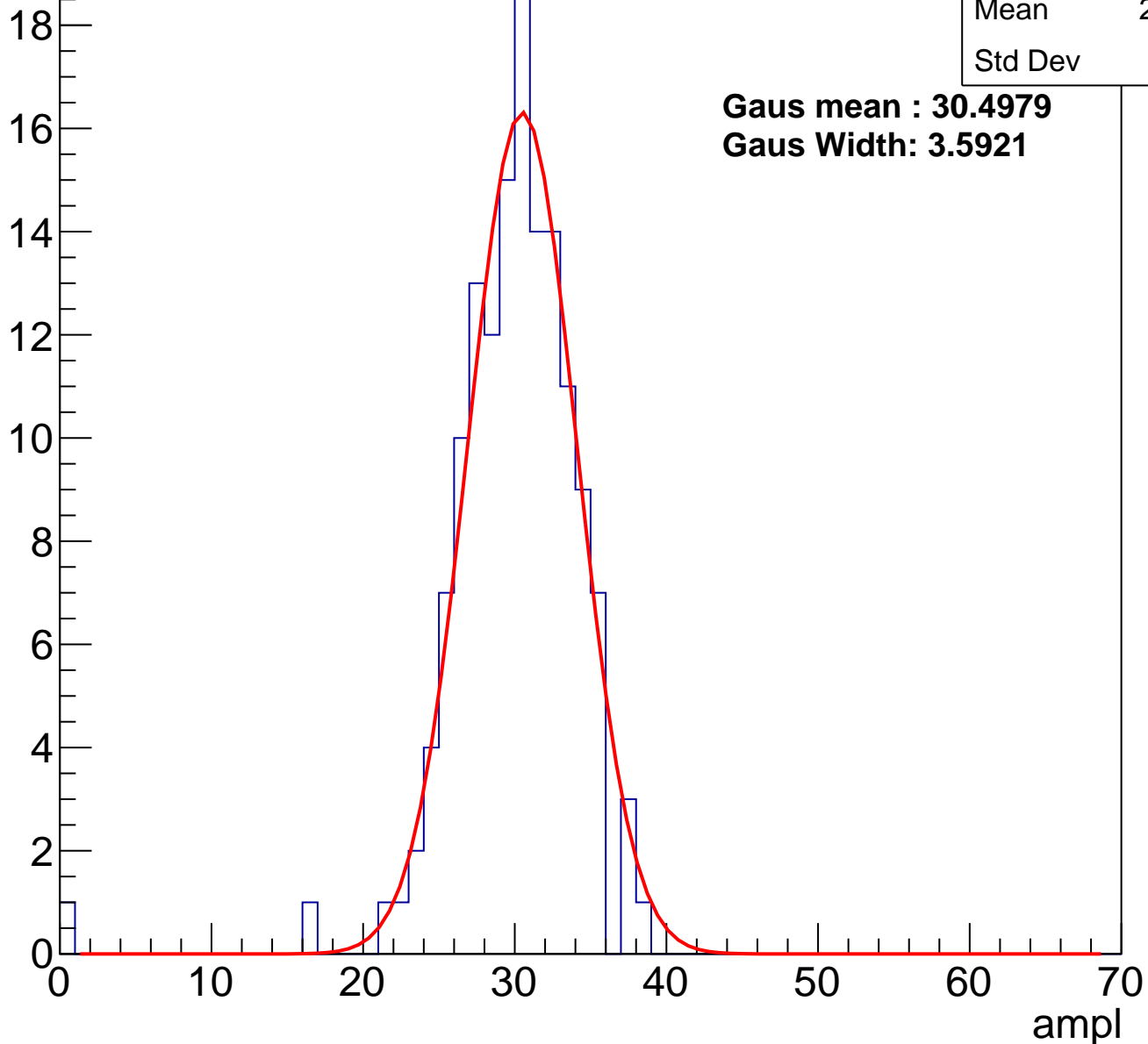


Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch37, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	145
Mean	29.47
Std Dev	4.28

Gaus mean : 30.4979

Gaus Width: 3.5921

B0L101S, U18-ch37, adc1

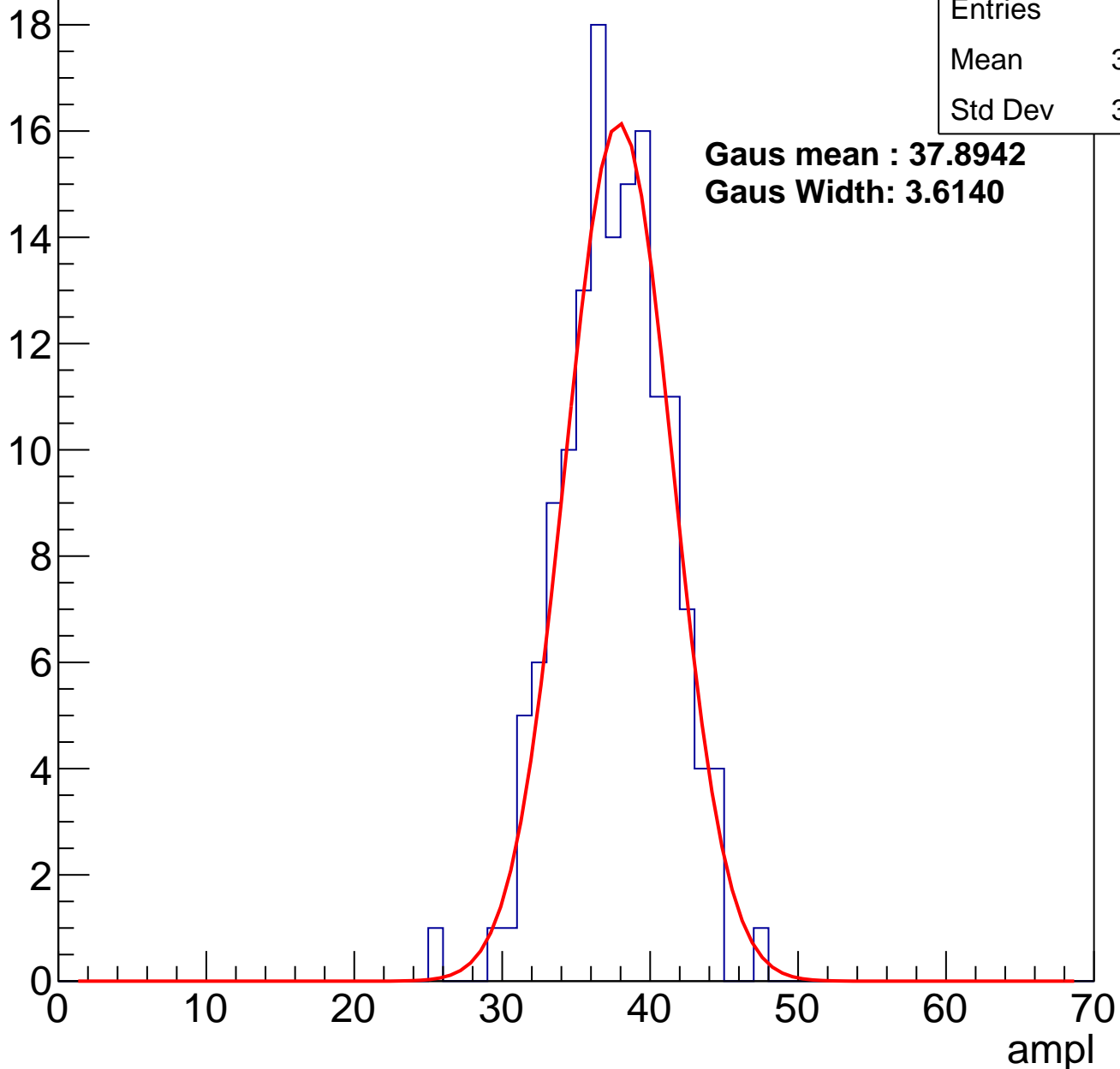
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	37.16
Std Dev	3.553

Gaus mean : 37.8942

Gaus Width: 3.6140

Entry



B0L101S, U18-ch37, adc2

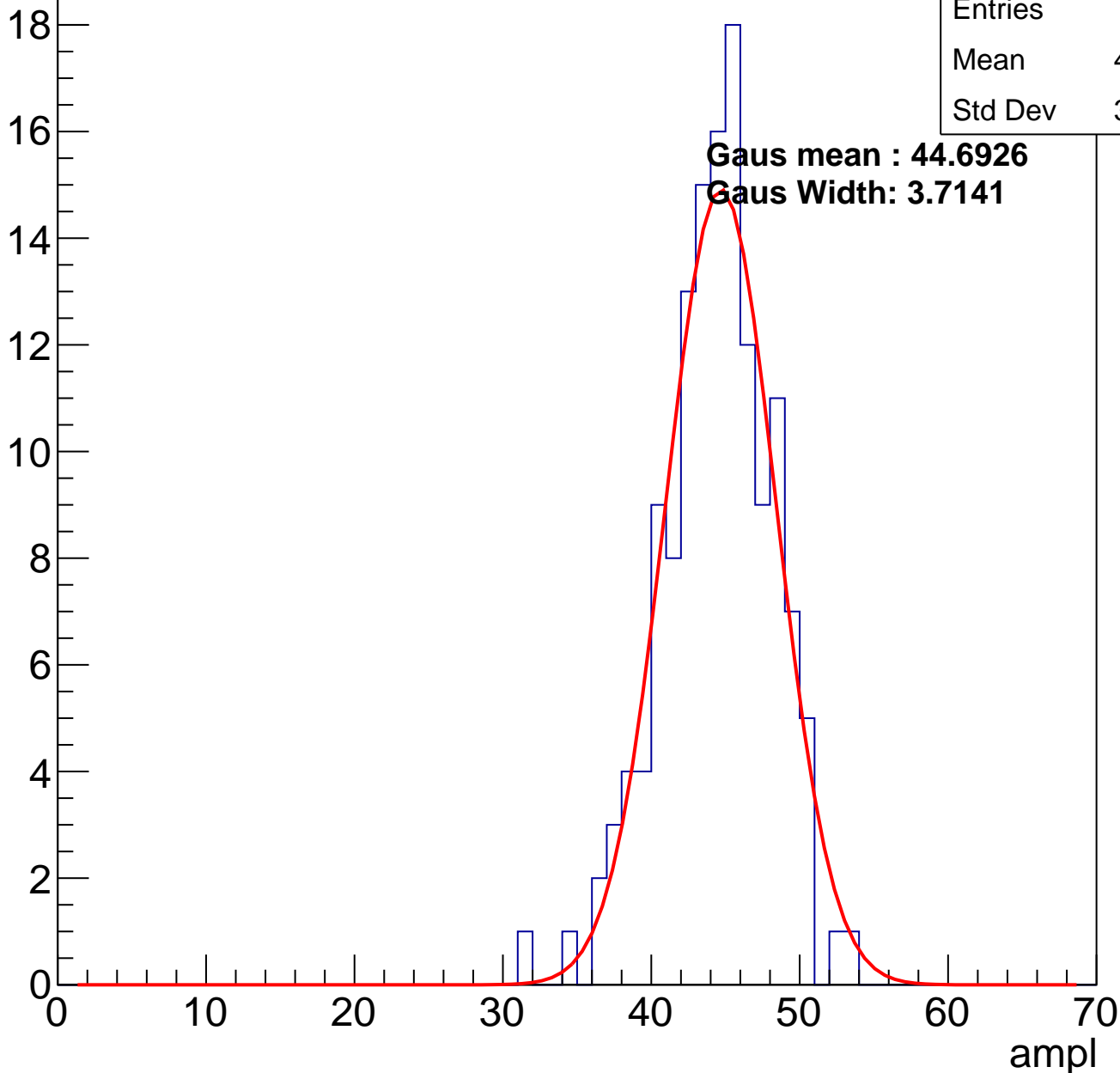
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	140
Mean	43.93
Std Dev	3.677

Gaus mean : 44.6926

Gaus Width: 3.7141



B0L101S, U18-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

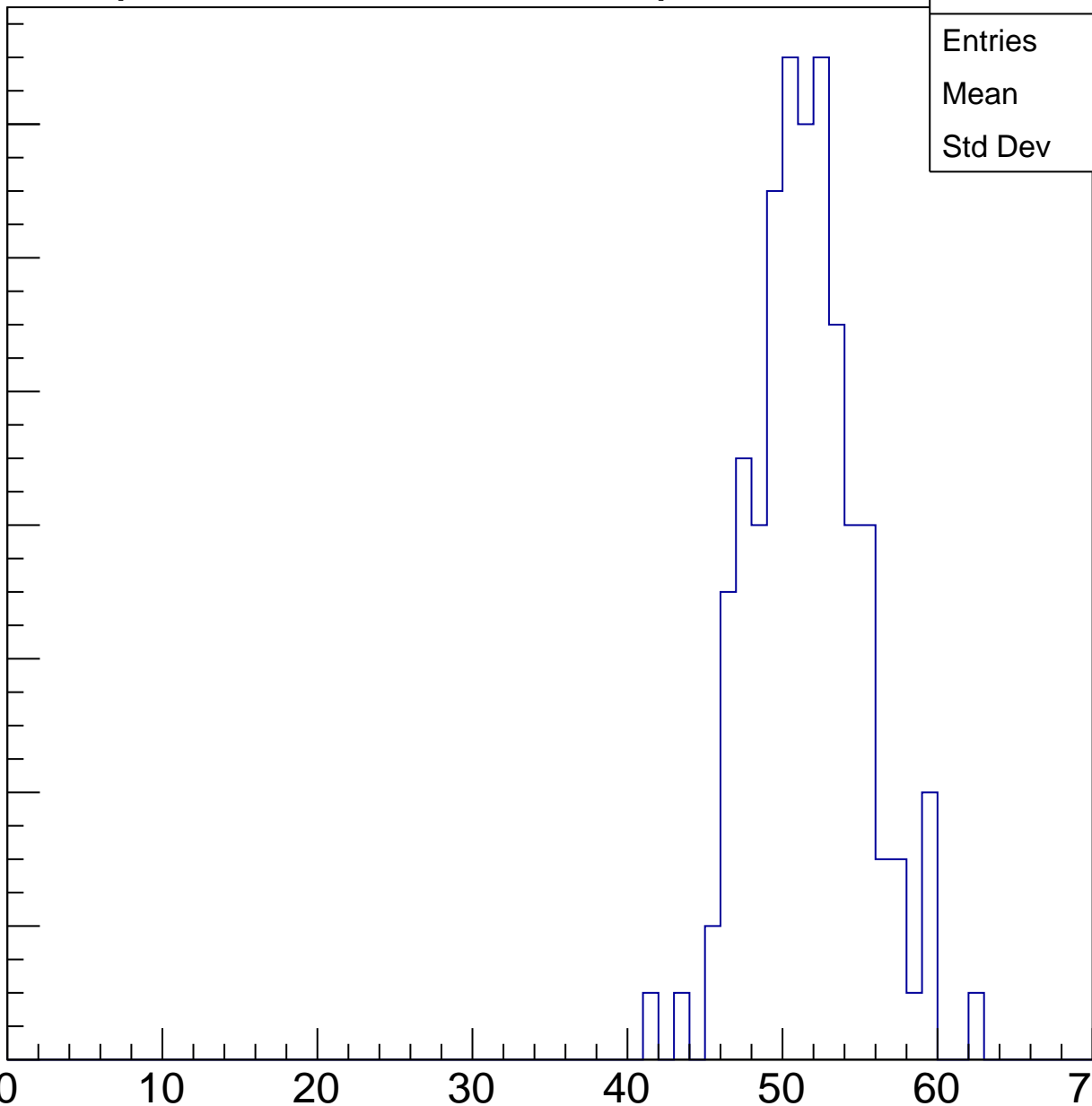
Entries	124
Mean	51.08
Std Dev	3.548

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

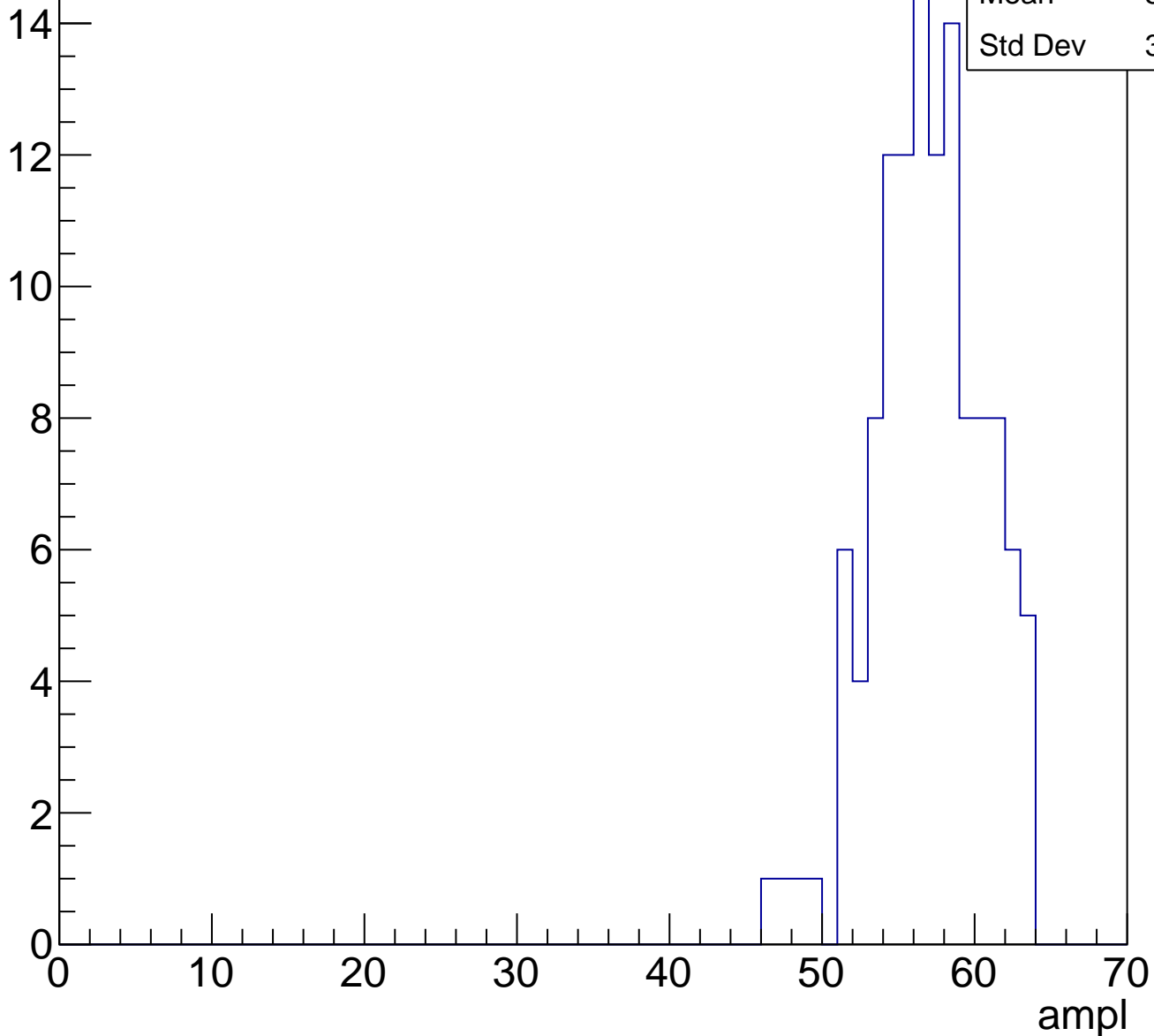


B0L101S, U18-ch37, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	56.55
Std Dev	3.544

Entry



B0L101S, U18-ch37, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

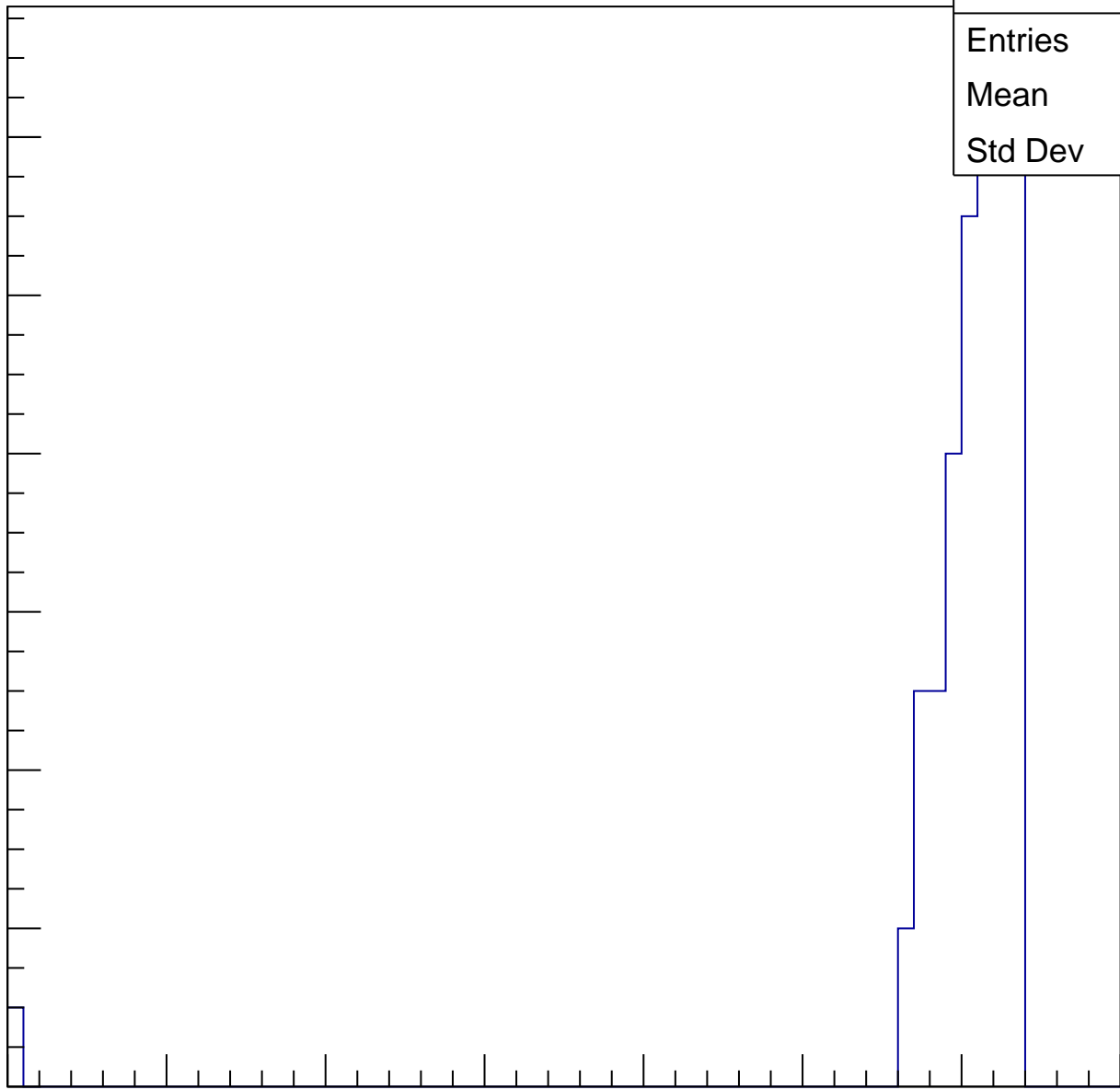
2

0

Entries	70
Mean	59.66
Std Dev	7.443

ampl

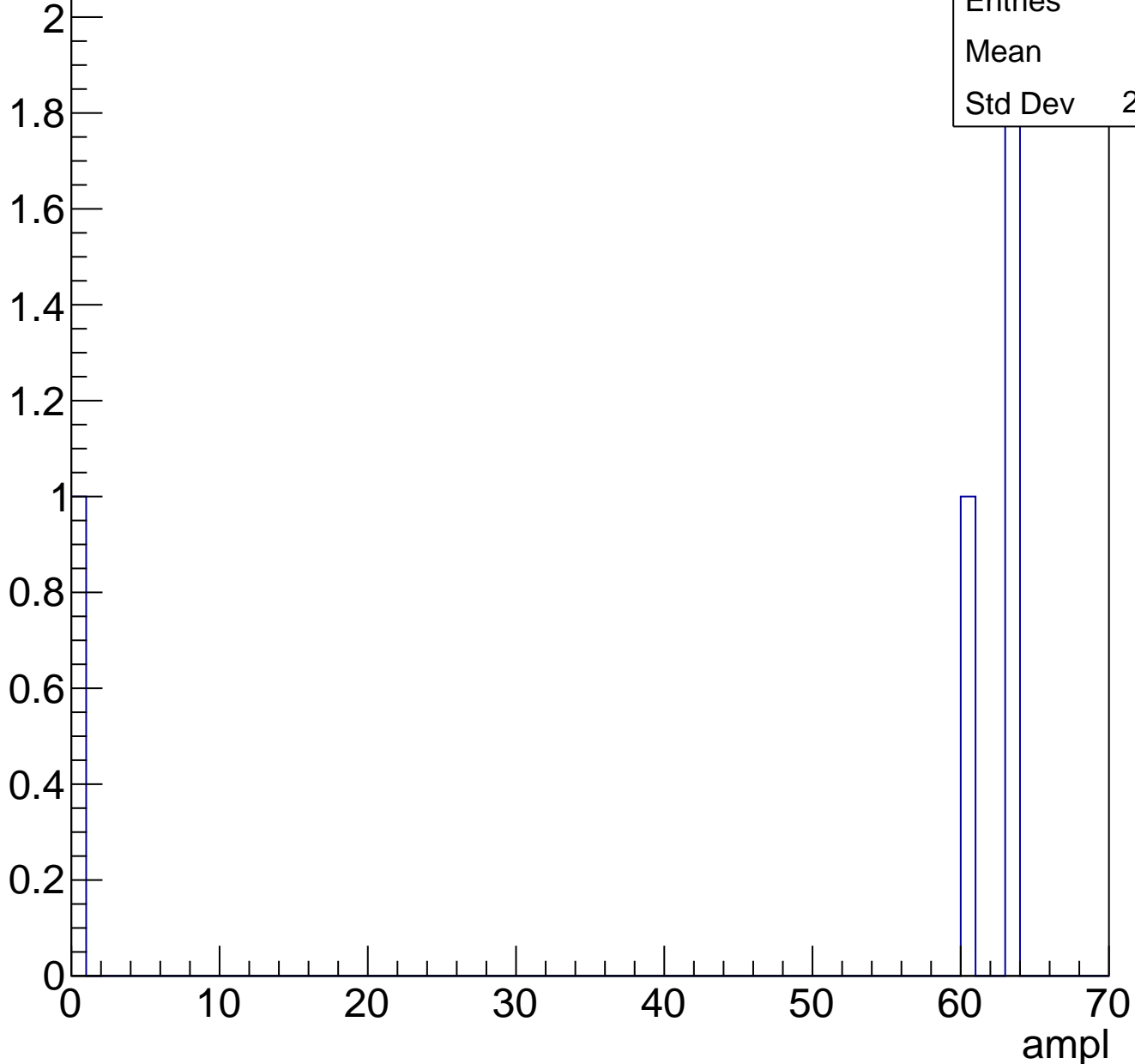
0 10 20 30 40 50 60 70



B0L101S, U18-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch38, adc0

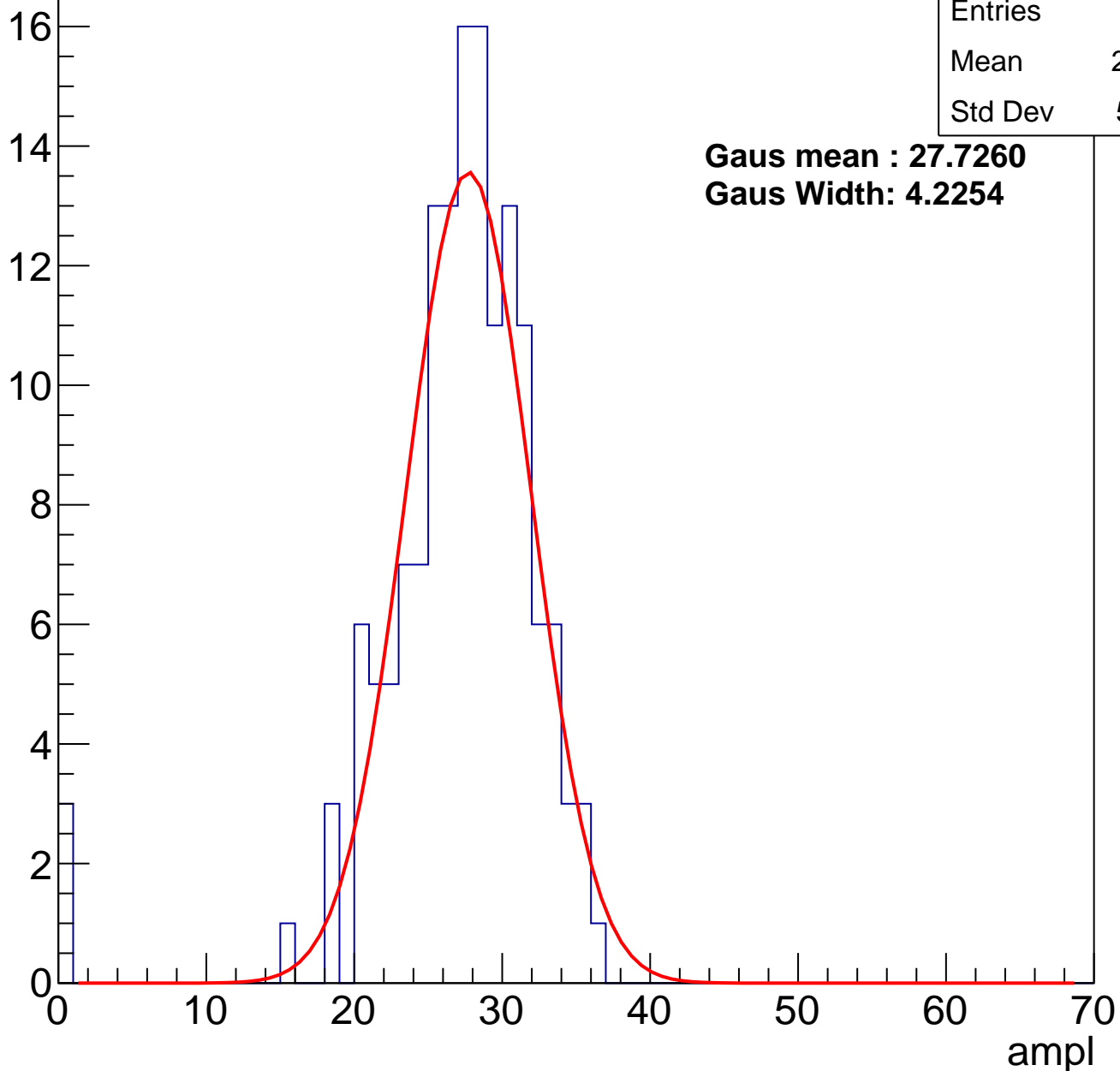
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	26.57
Std Dev	5.511

Gaus mean : 27.7260

Gaus Width: 4.2254

Entry



B0L101S, U18-ch38, adc1

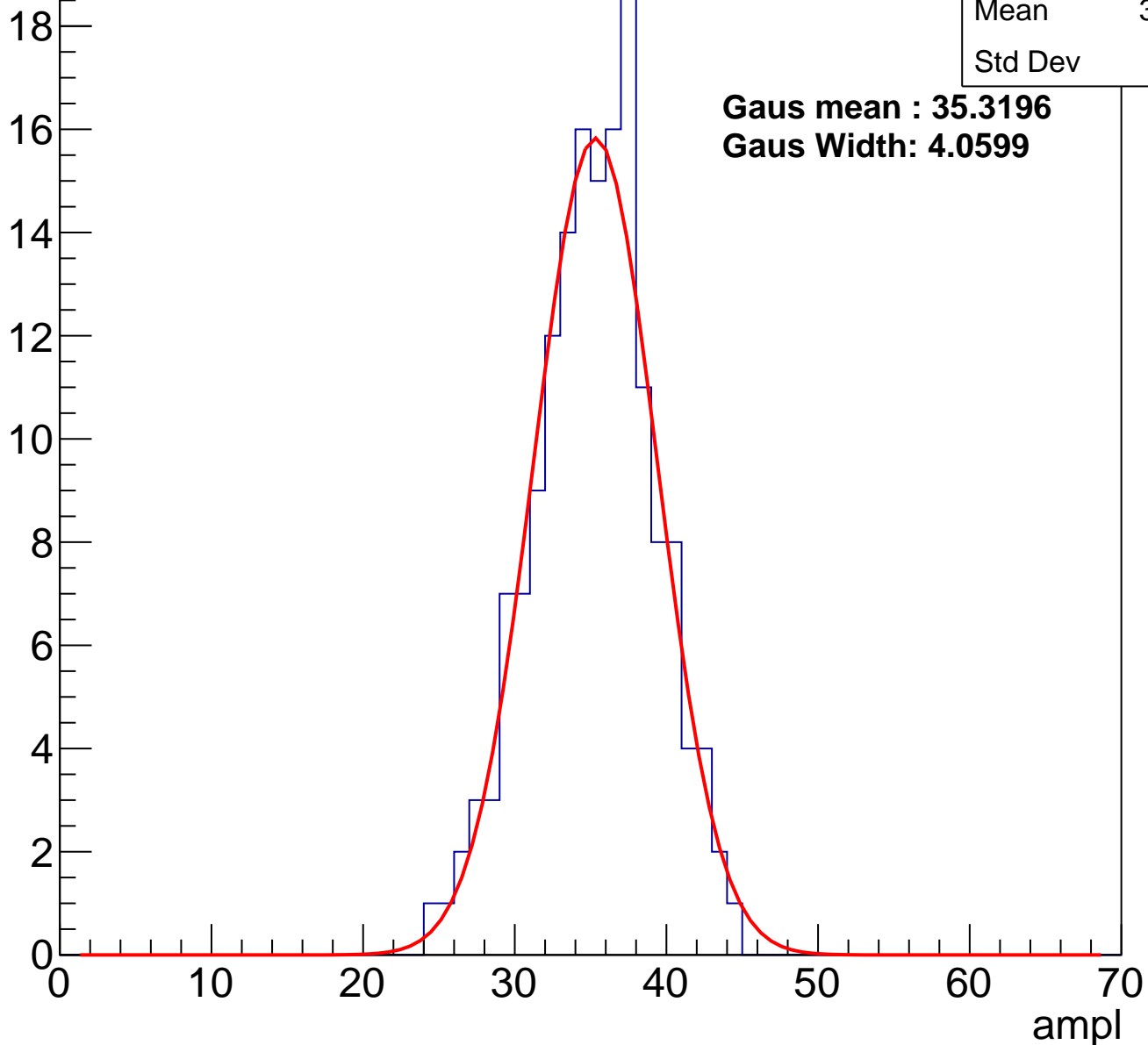
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	34.75
Std Dev	3.94

Gaus mean : 35.3196

Gaus Width: 4.0599

Entry

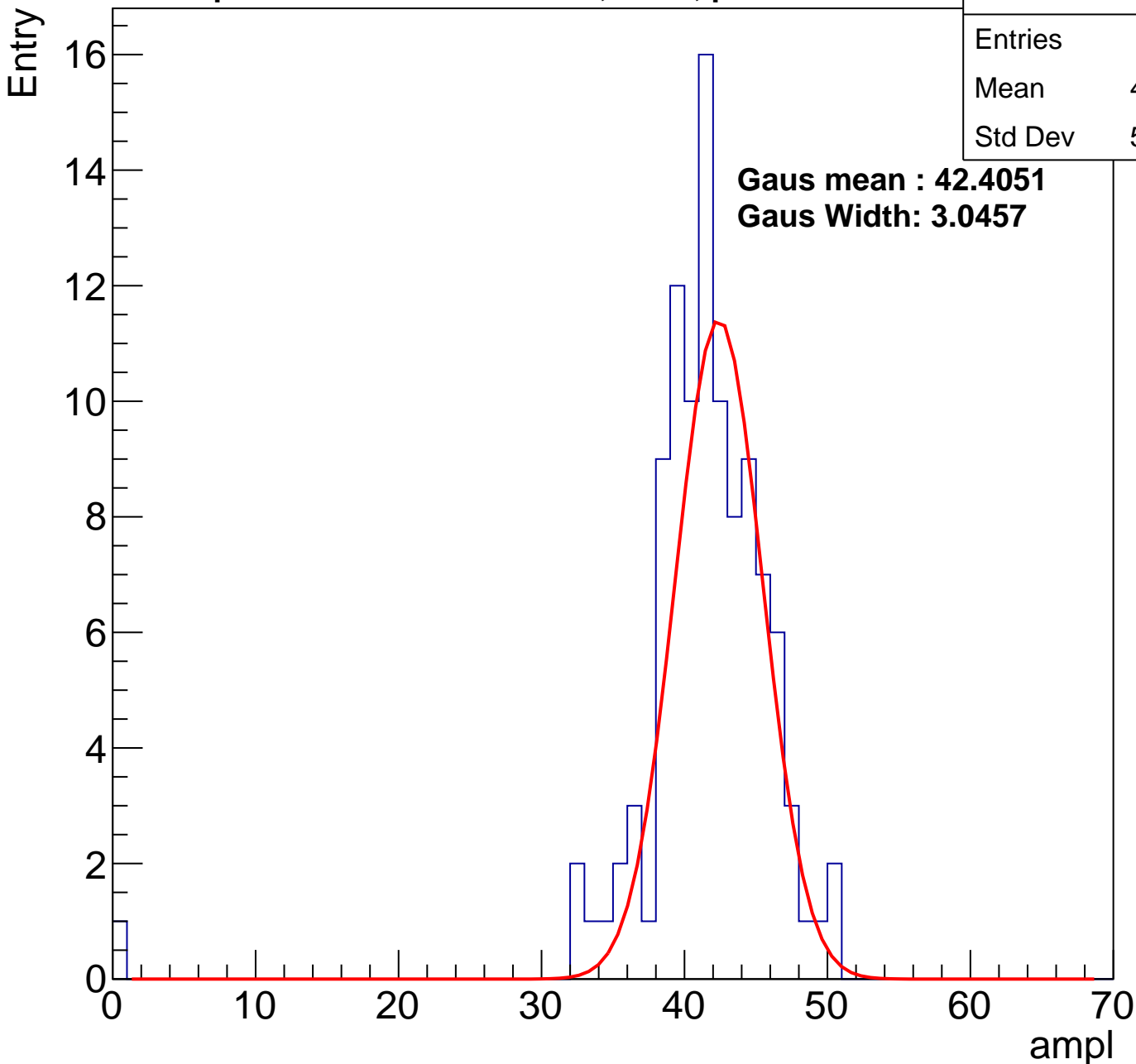


B0L101S, U18-ch38, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	40.96
Std Dev	5.368

Gaus mean : 42.4051
Gaus Width: 3.0457

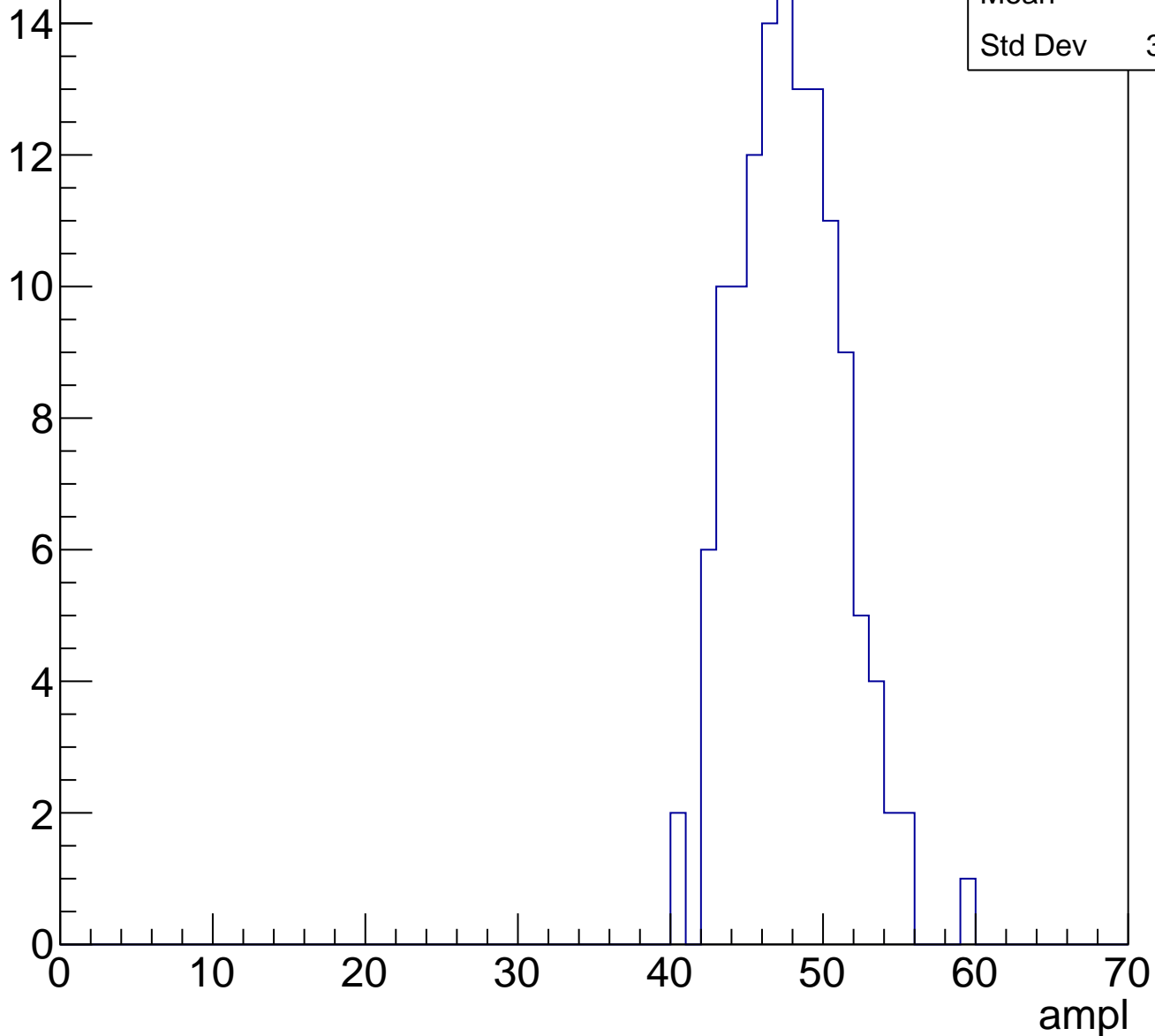


B0L101S, U18-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	47.36
Std Dev	3.398

Entry



B0L101S, U18-ch38, adc4

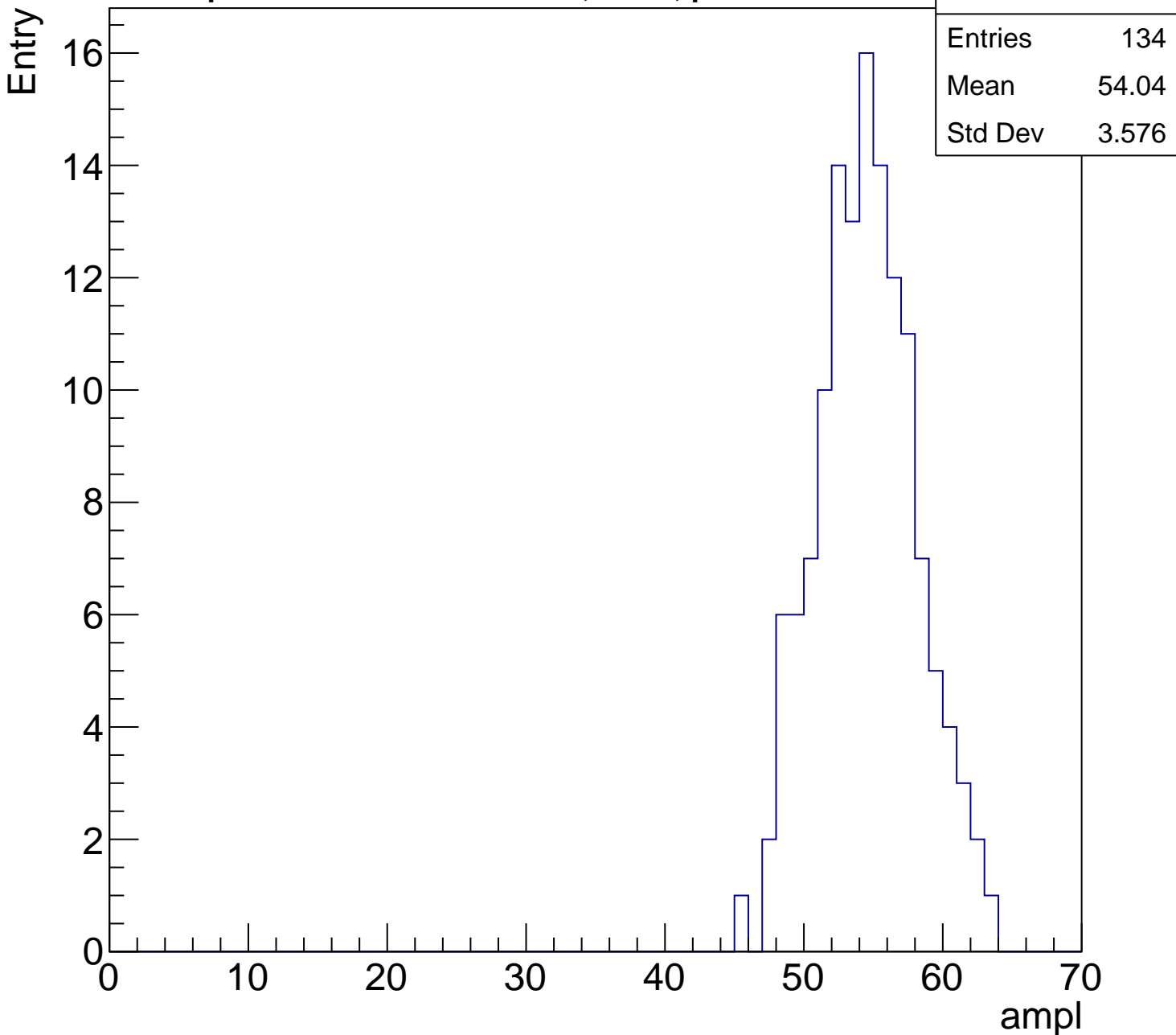
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	134
Mean	54.04
Std Dev	3.576

ampl



B0L101S, U18-ch38, adc5

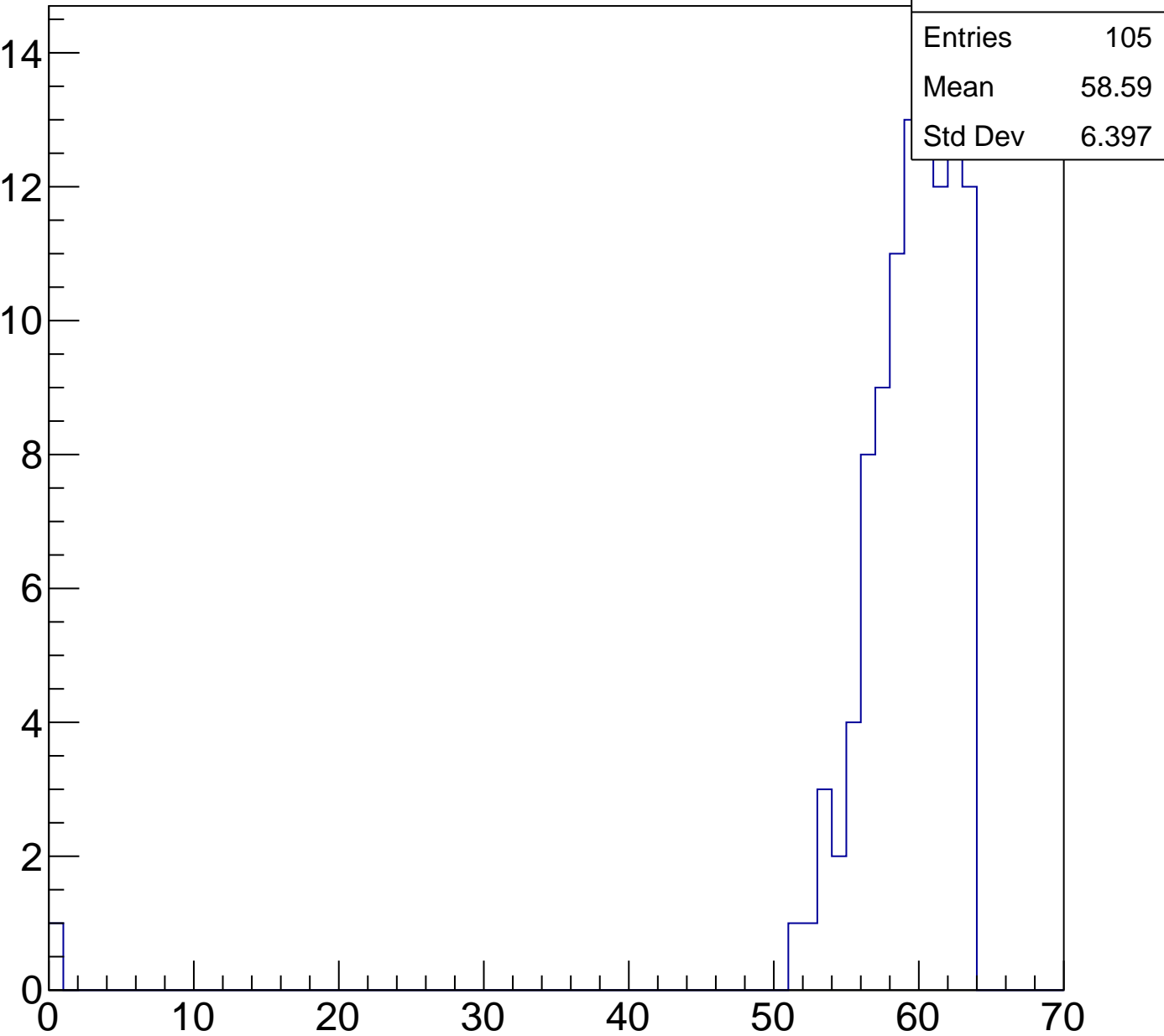
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	105
Mean	58.59
Std Dev	6.397

ampl

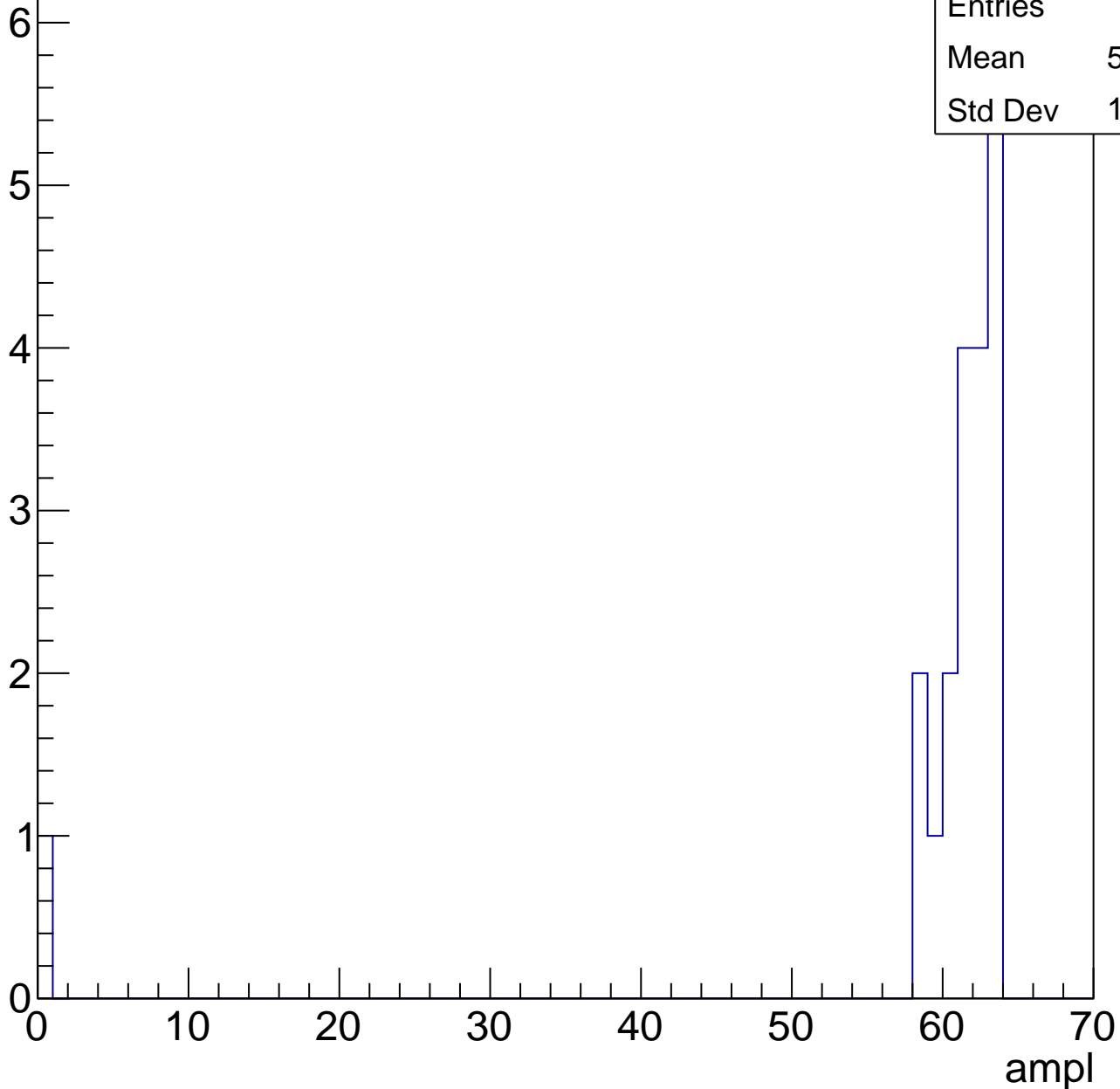


B0L101S, U18-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

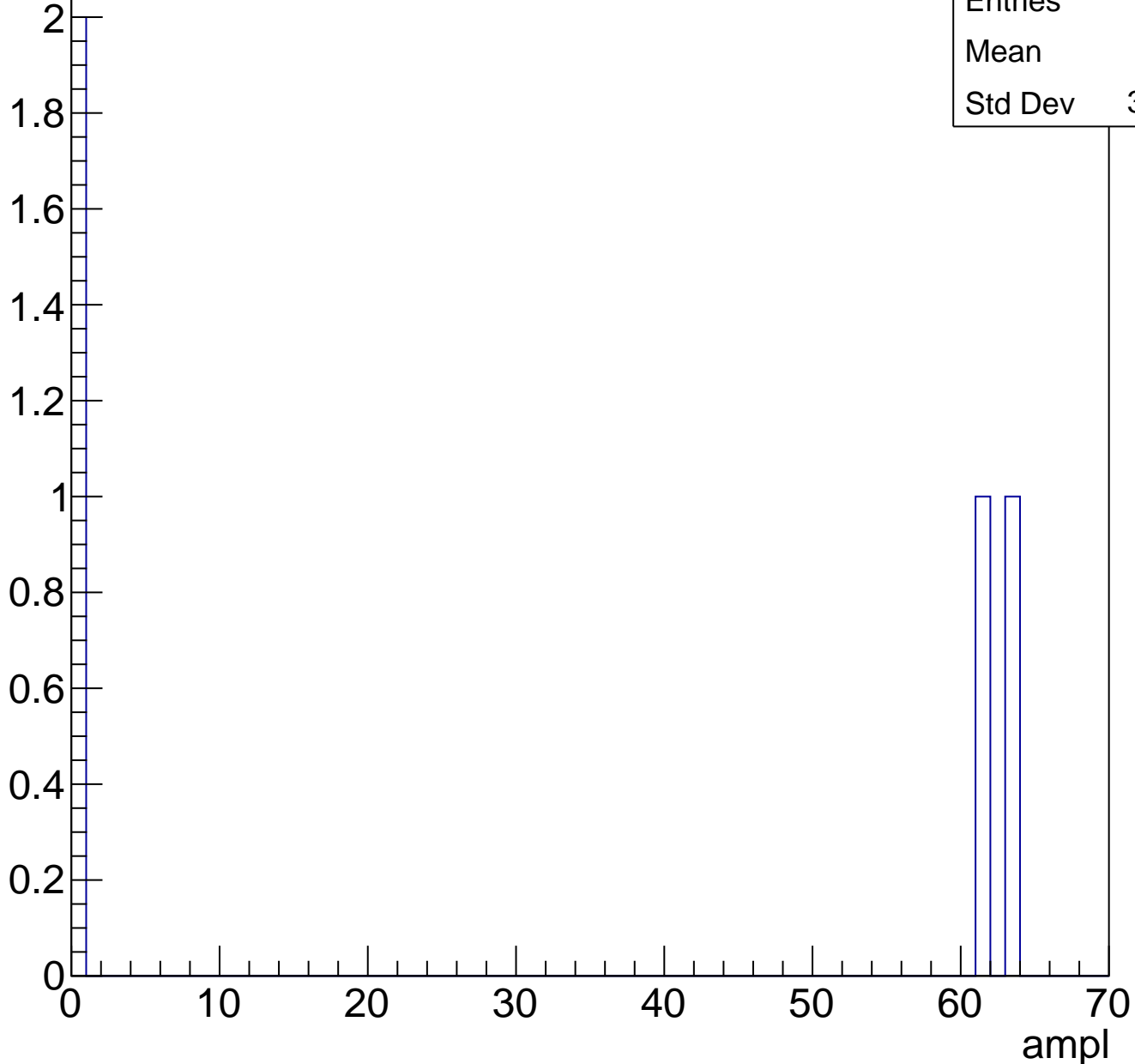
Entries	20
Mean	58.25
Std Dev	13.46



B0L101S, U18-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	31
Std Dev	31.01

B0L101S, U18-ch39, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	28.89
Std Dev	4.208

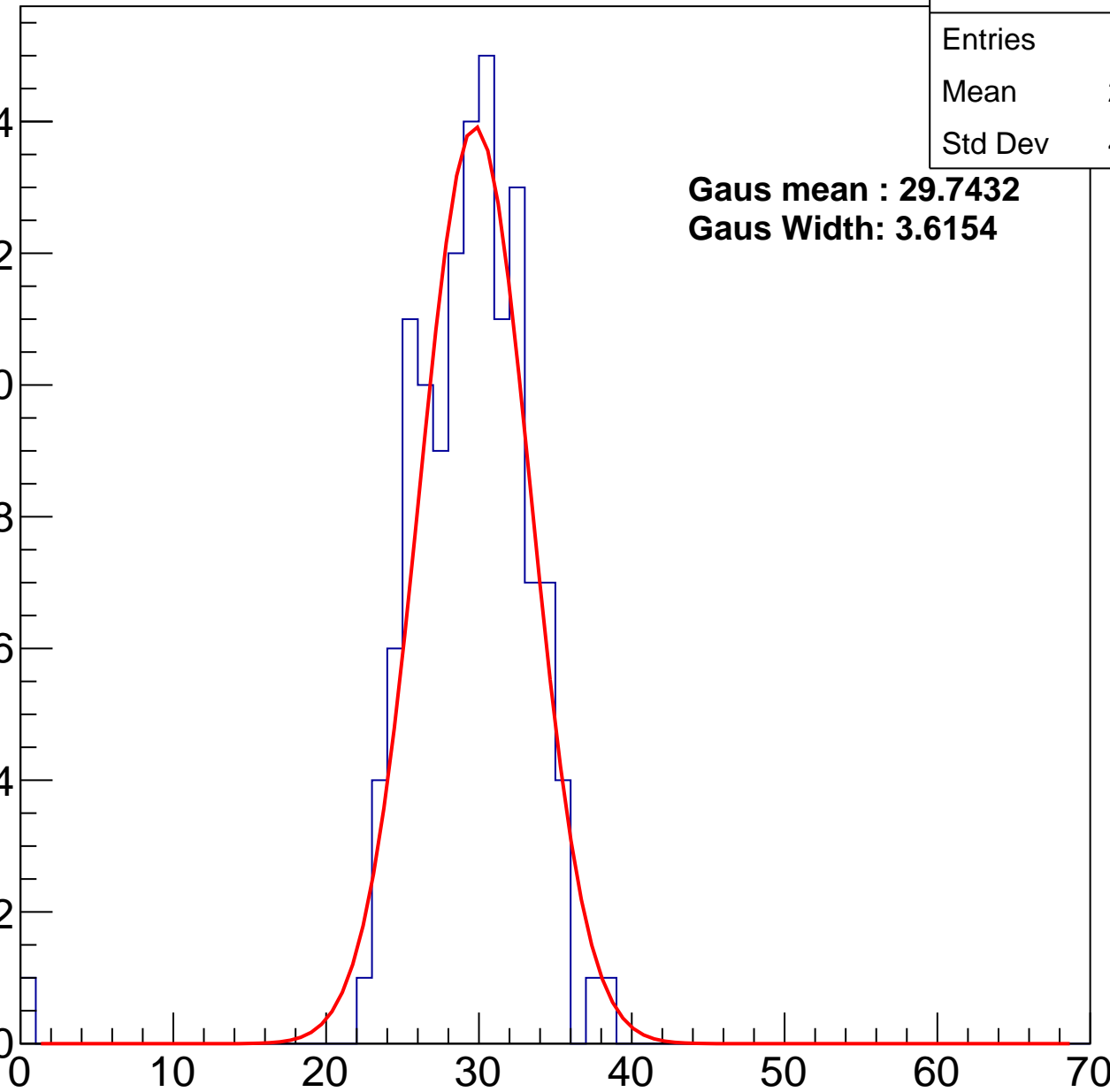
Gaus mean : 29.7432

Gaus Width: 3.6154

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	36.47
Std Dev	3.811

Gaus mean : 36.9030

Gaus Width: 3.7583

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

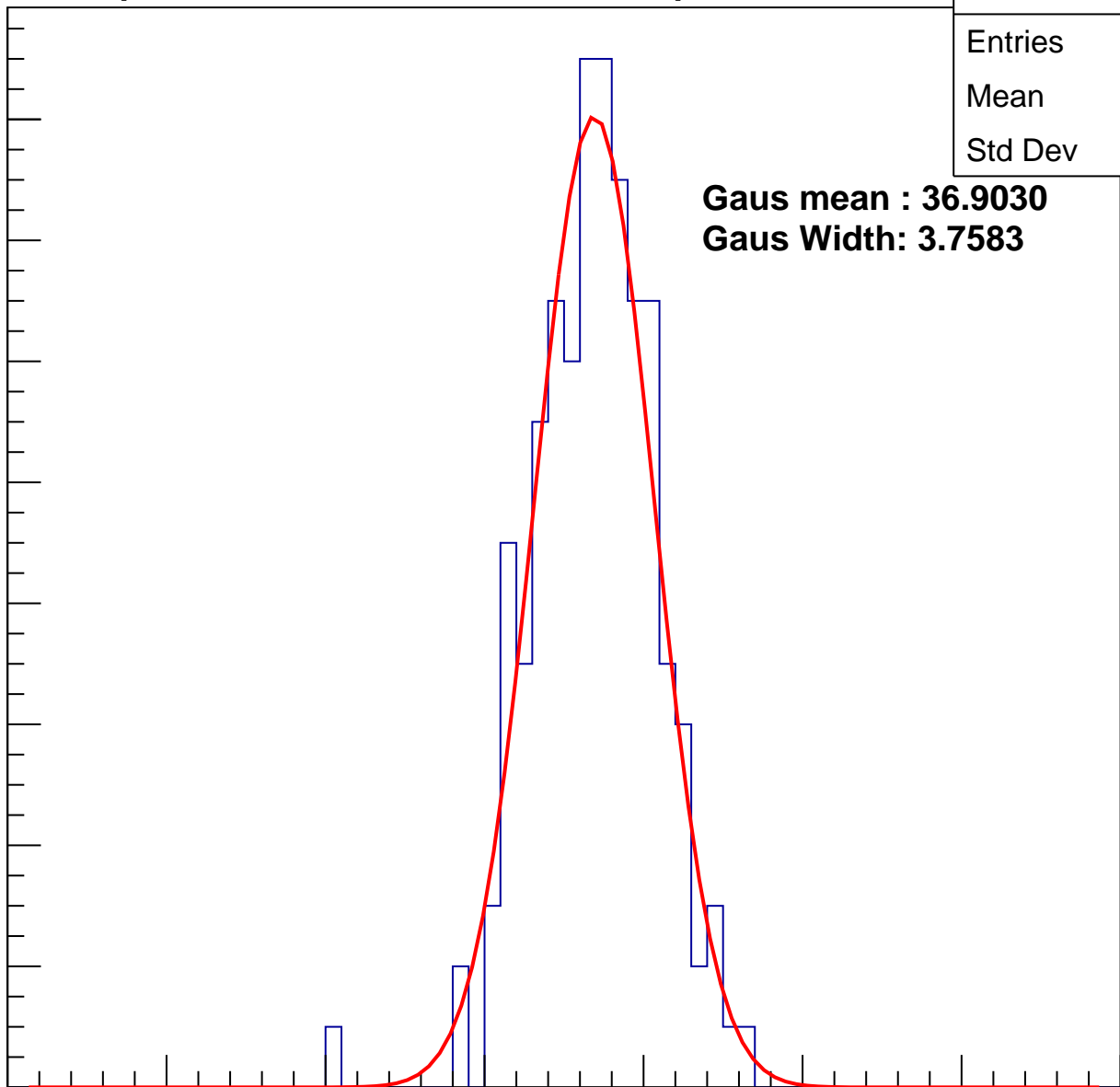
40

50

60

70

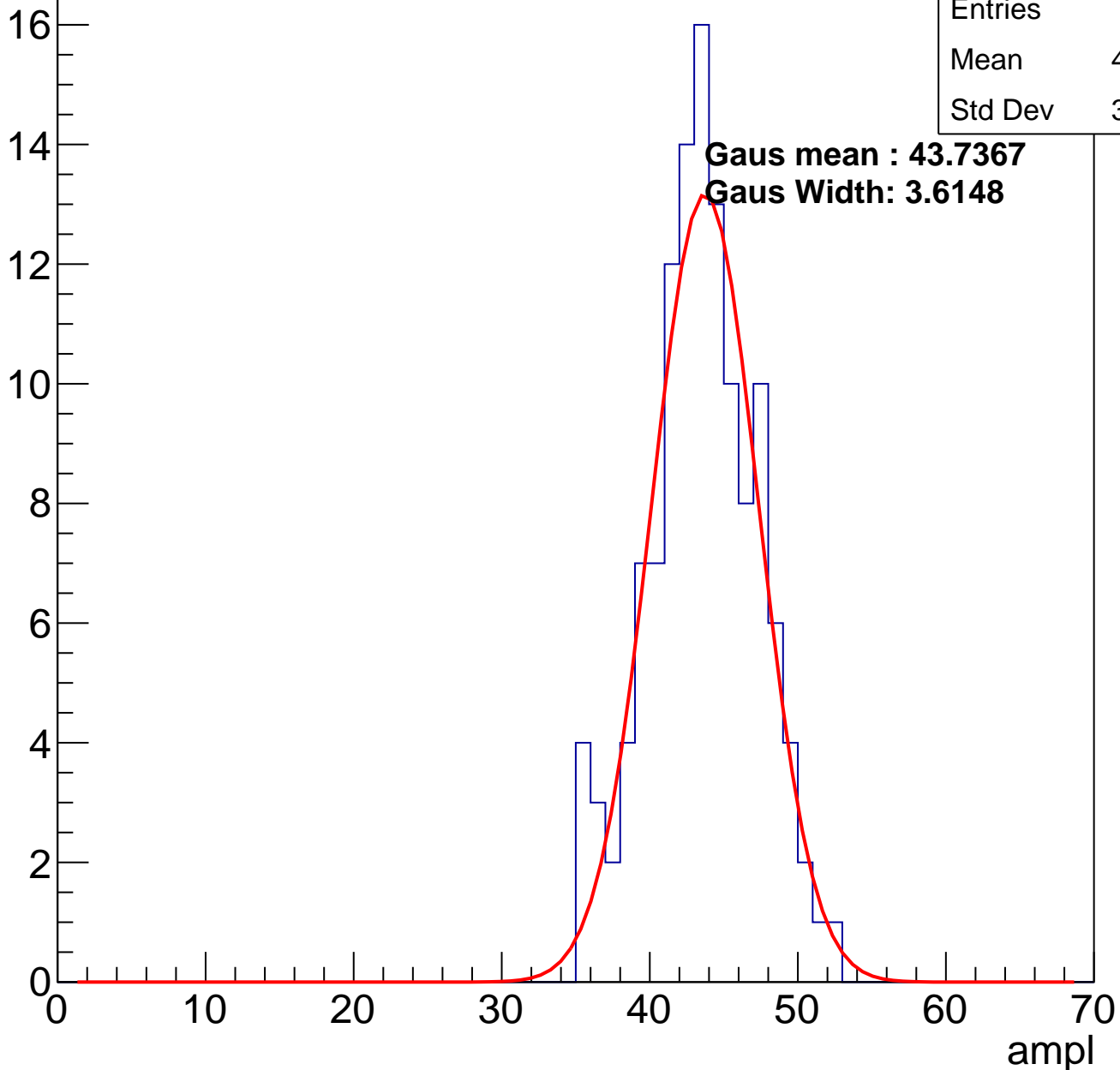
ampl



B0L101S, U18-ch39, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	50.44
Std Dev	3.885

Entry

16

14

12

10

8

6

4

2

0

0

10

20

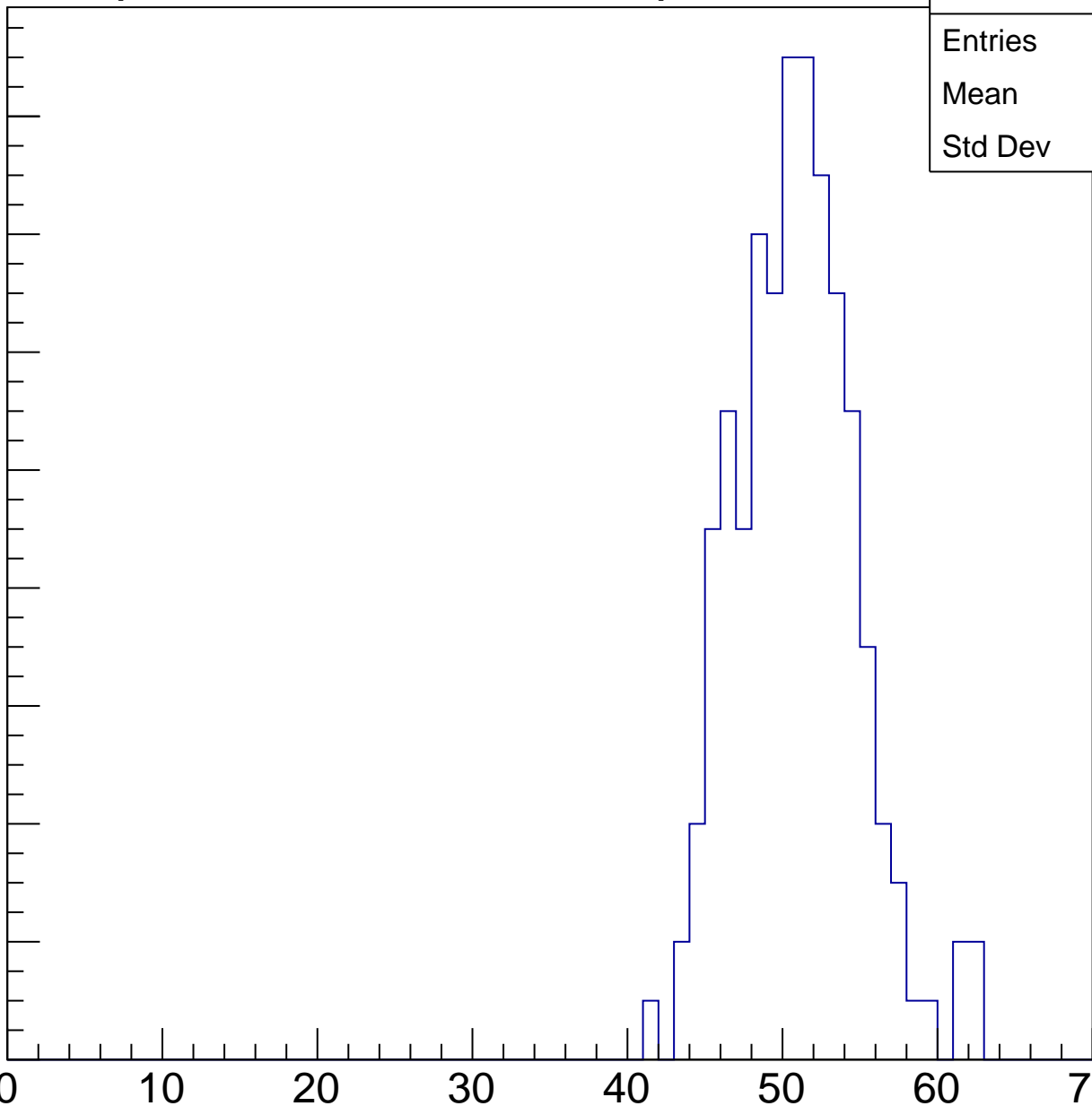
30

40

50

60

ampl



B0L101S, U18-ch39, adc4

calib_packv5_042523_0143.root, FC#1, port C1

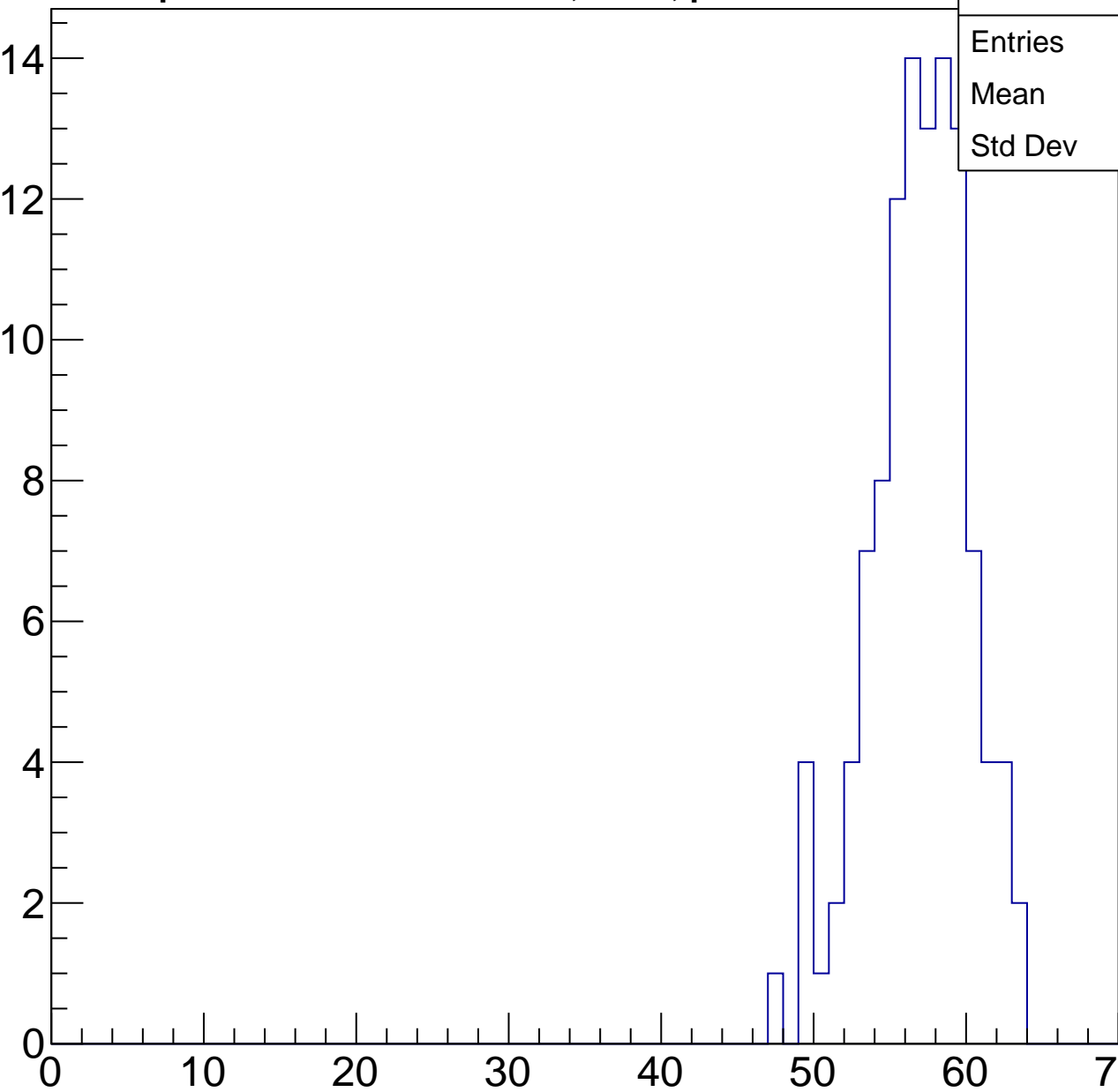
Entries	110
Mean	56.44
Std Dev	3.238

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch39, adc5

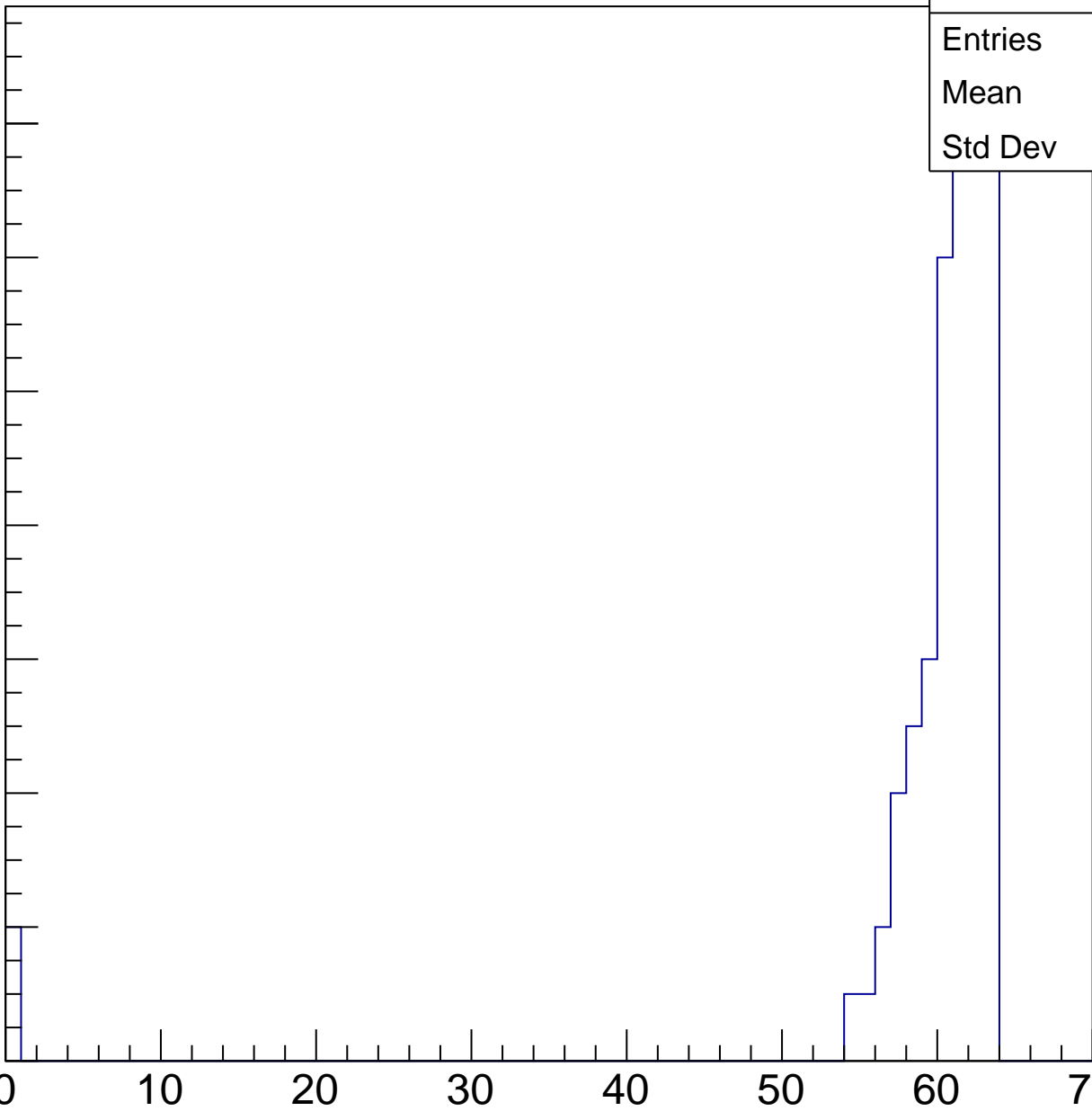
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	58.95
Std Dev	9.918

ampl



B0L101S, U18-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L101S, U18-ch40, adc0

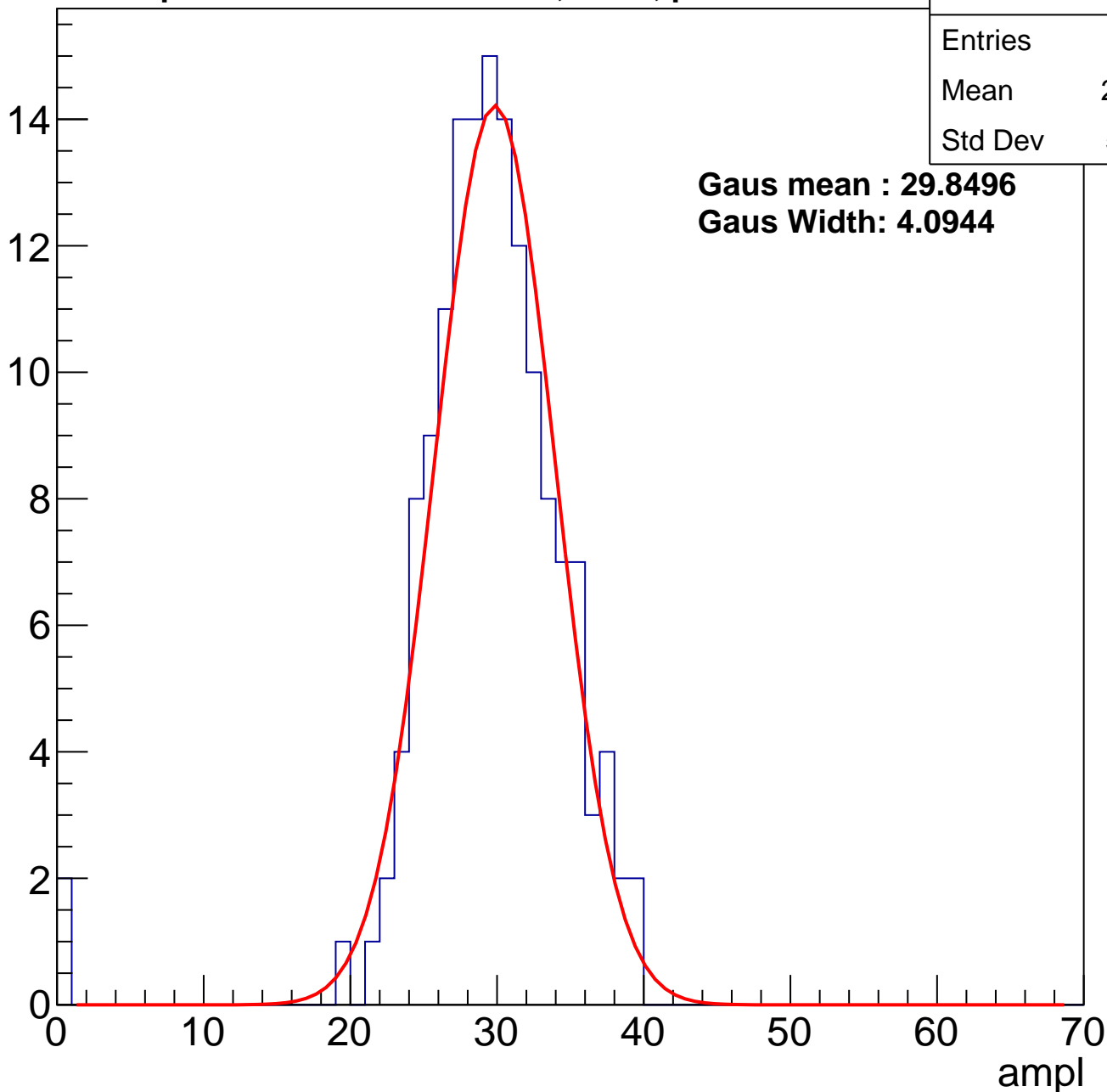
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	29.02
Std Dev	5.211

Gaus mean : 29.8496

Gaus Width: 4.0944

Entry



B0L101S, U18-ch40, adc1

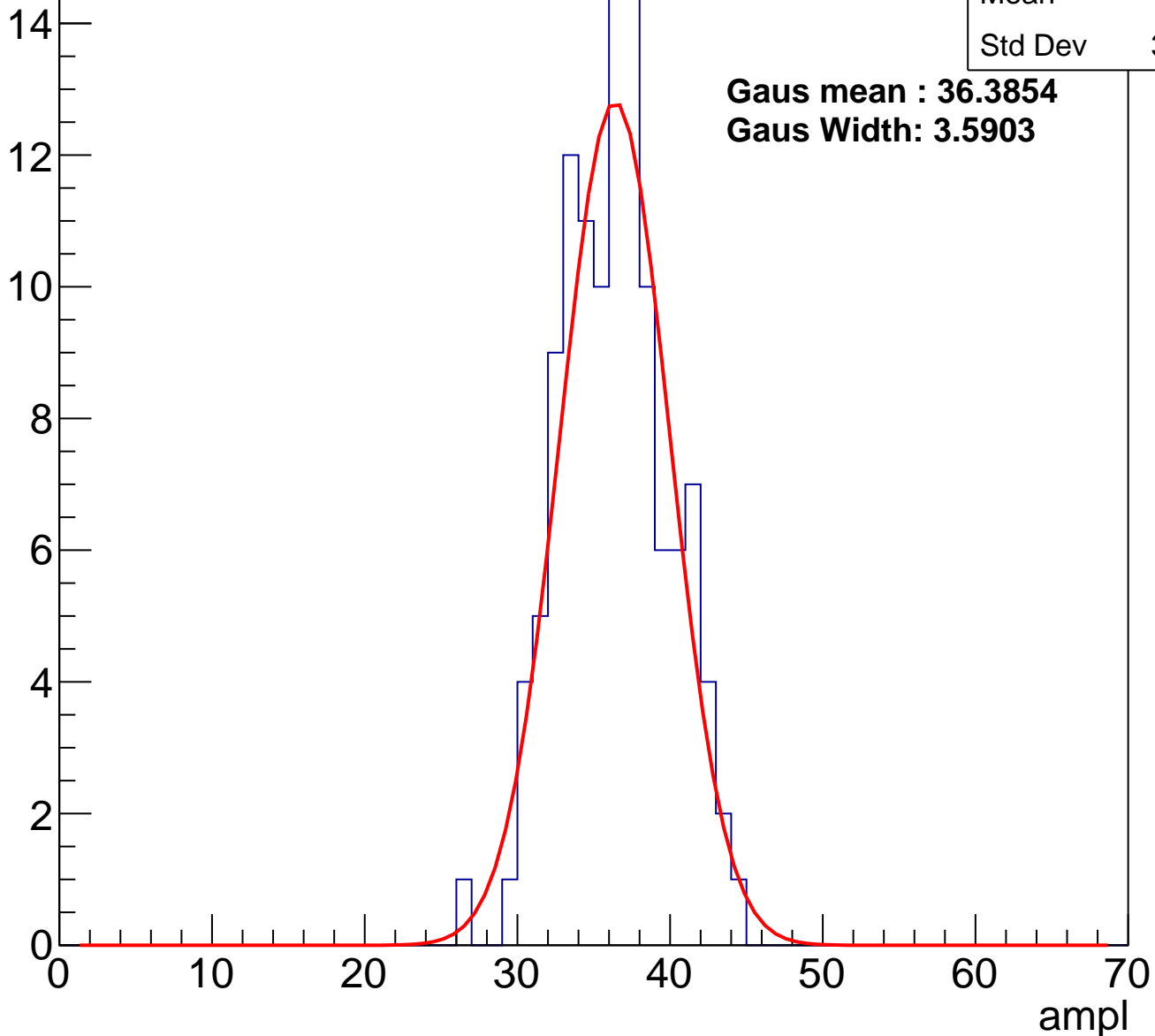
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	35.9
Std Dev	3.441

Gaus mean : 36.3854

Gaus Width: 3.5903

Entry



B0L101S, U18-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

152

Mean

42.91

Std Dev

3.726

Gaus mean : 43.6079

Gaus Width: 3.3117

0

10

20

30

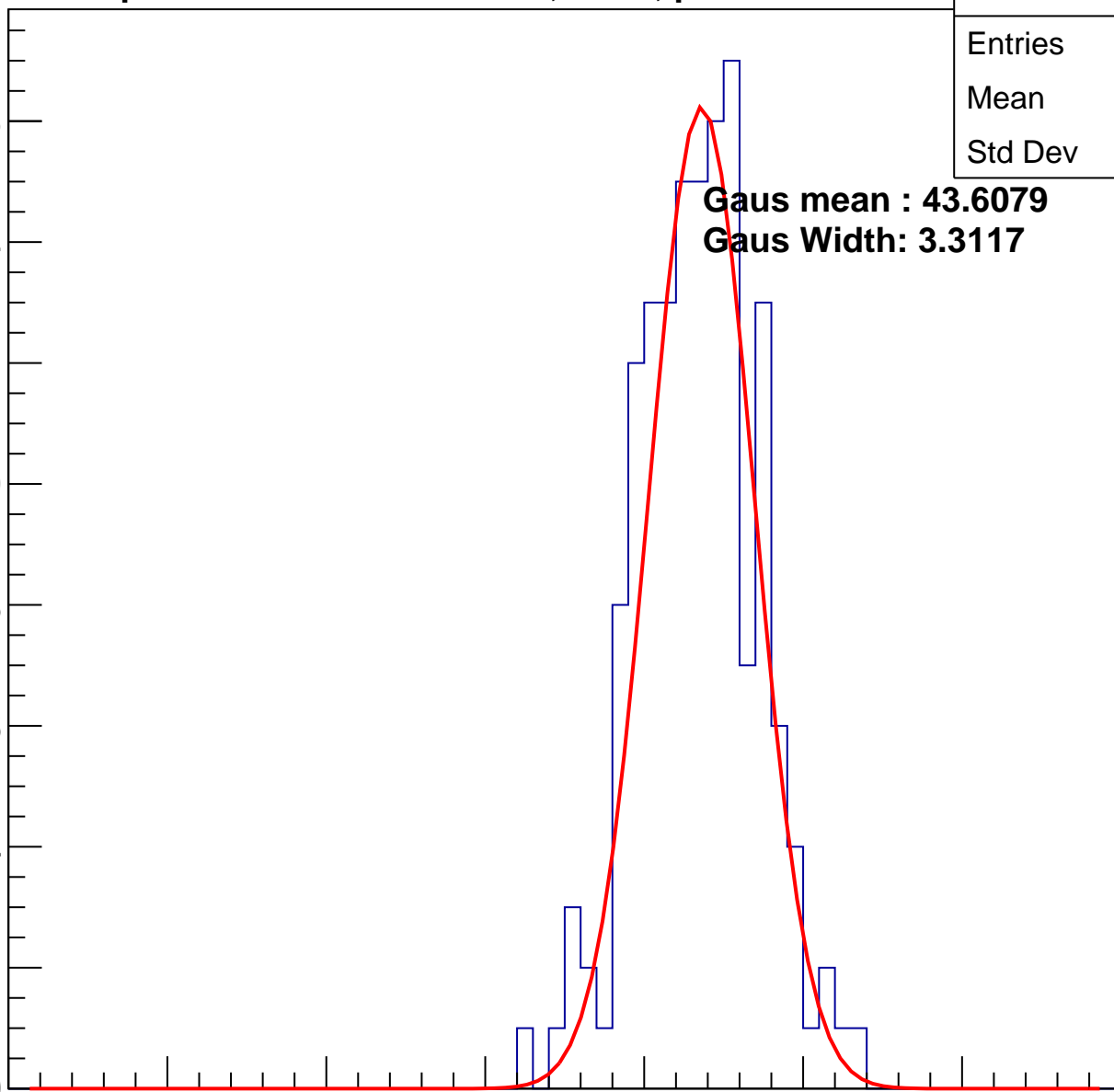
40

50

60

70

ampl



B0L101S, U18-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

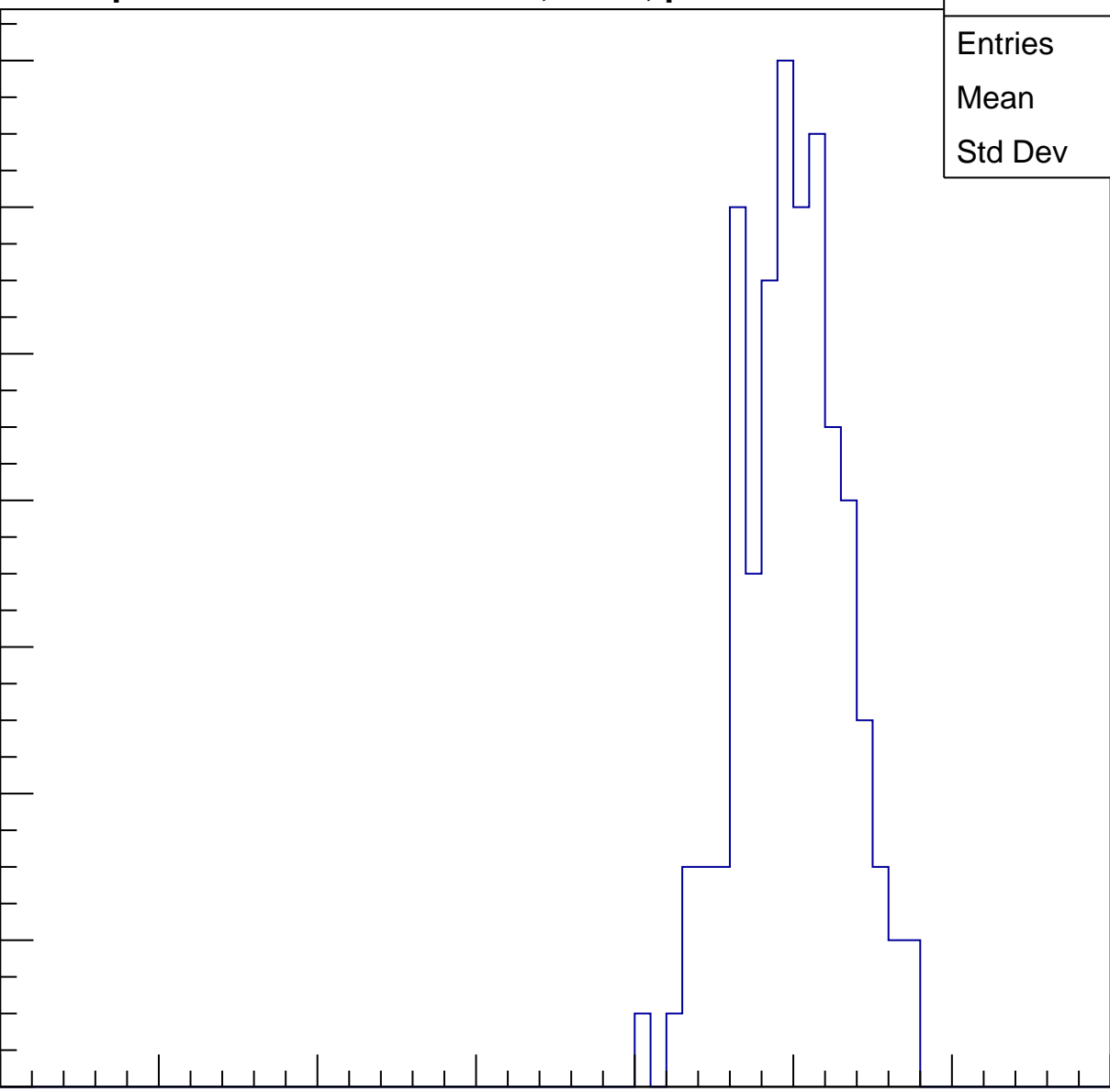
Entries	109
Mean	49.44
Std Dev	3.344

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

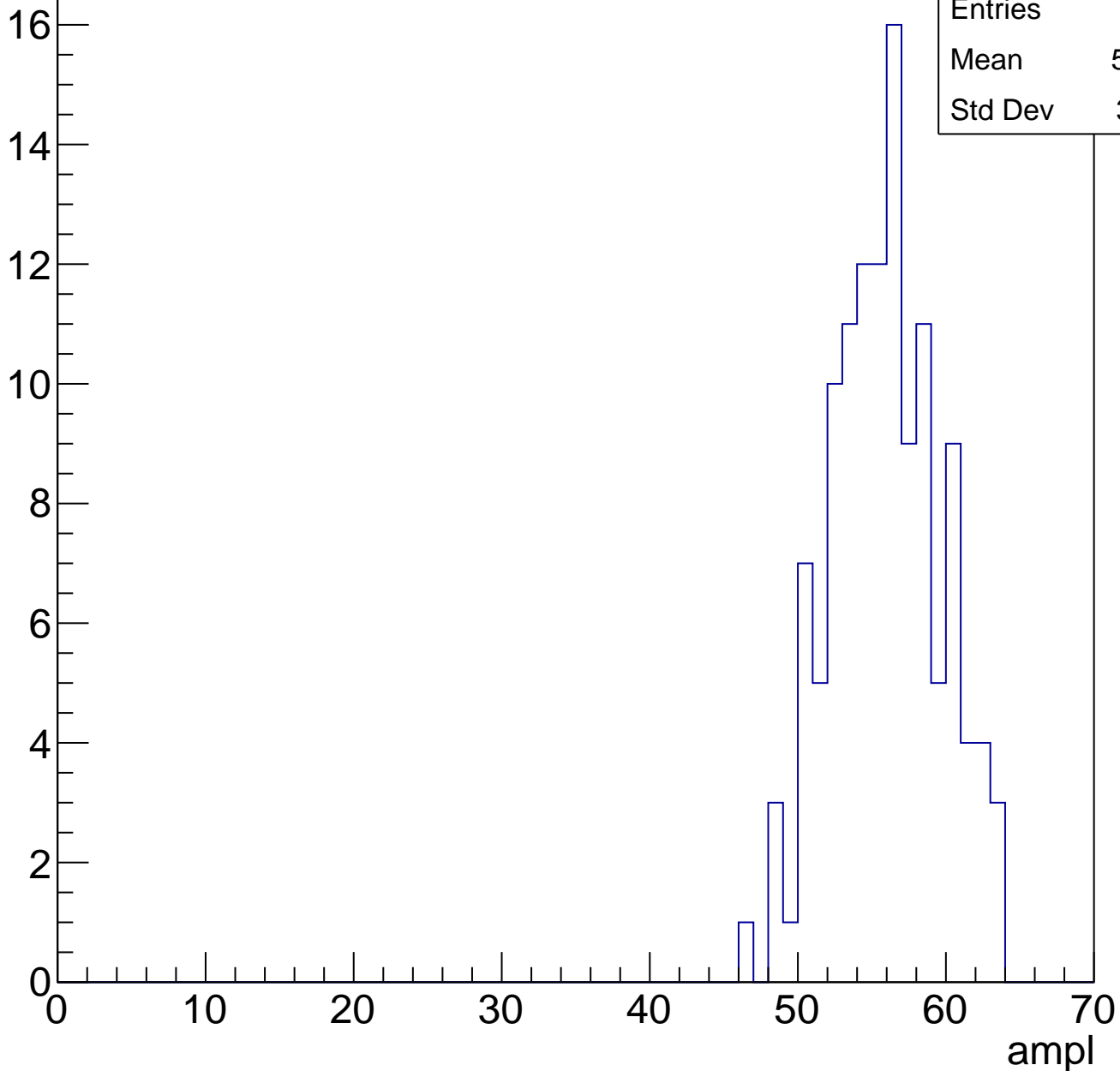
ampl



B0L101S, U18-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch40, adc5

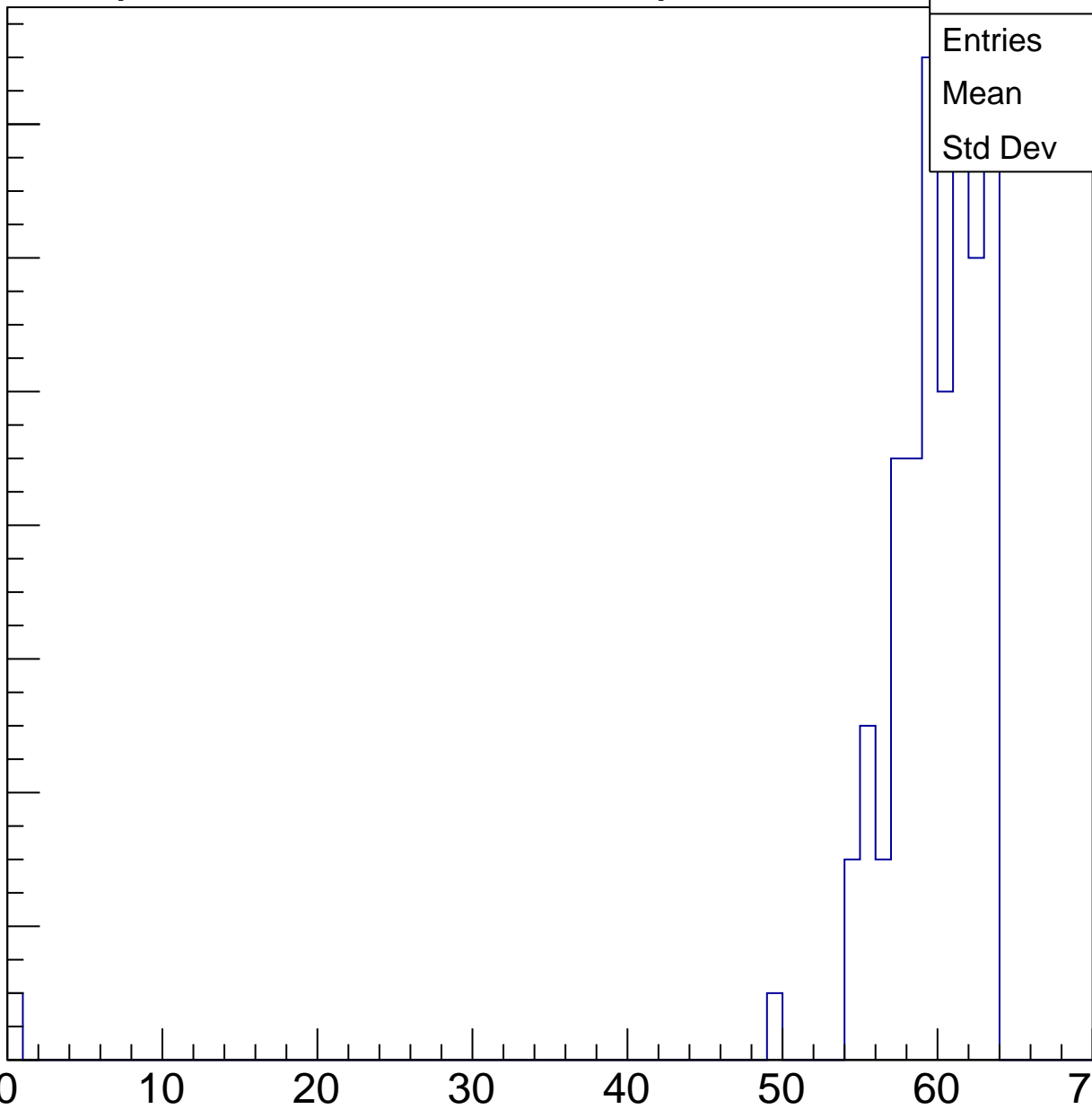
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	58.92
Std Dev	6.588

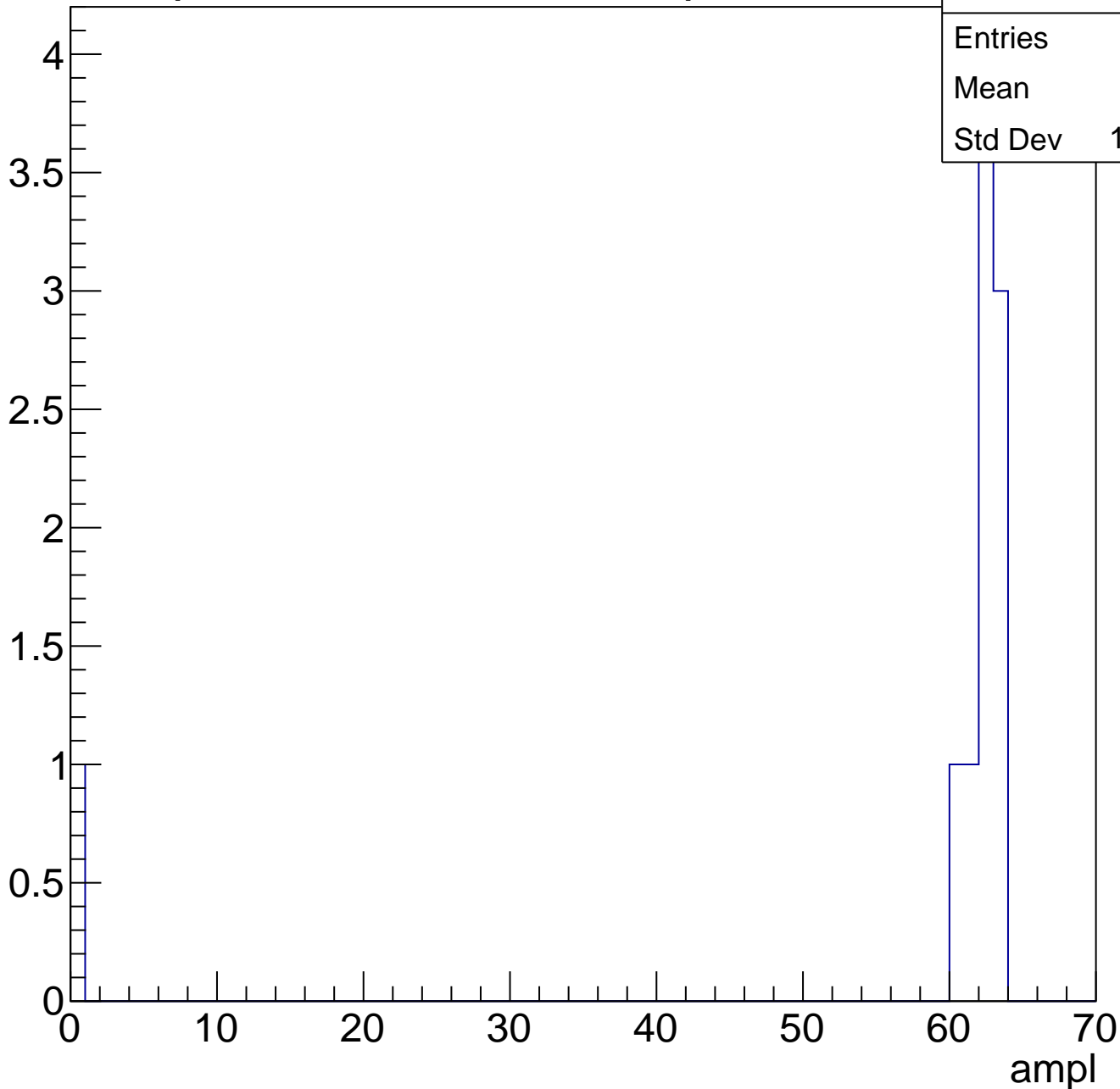
ampl



B0L101S, U18-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

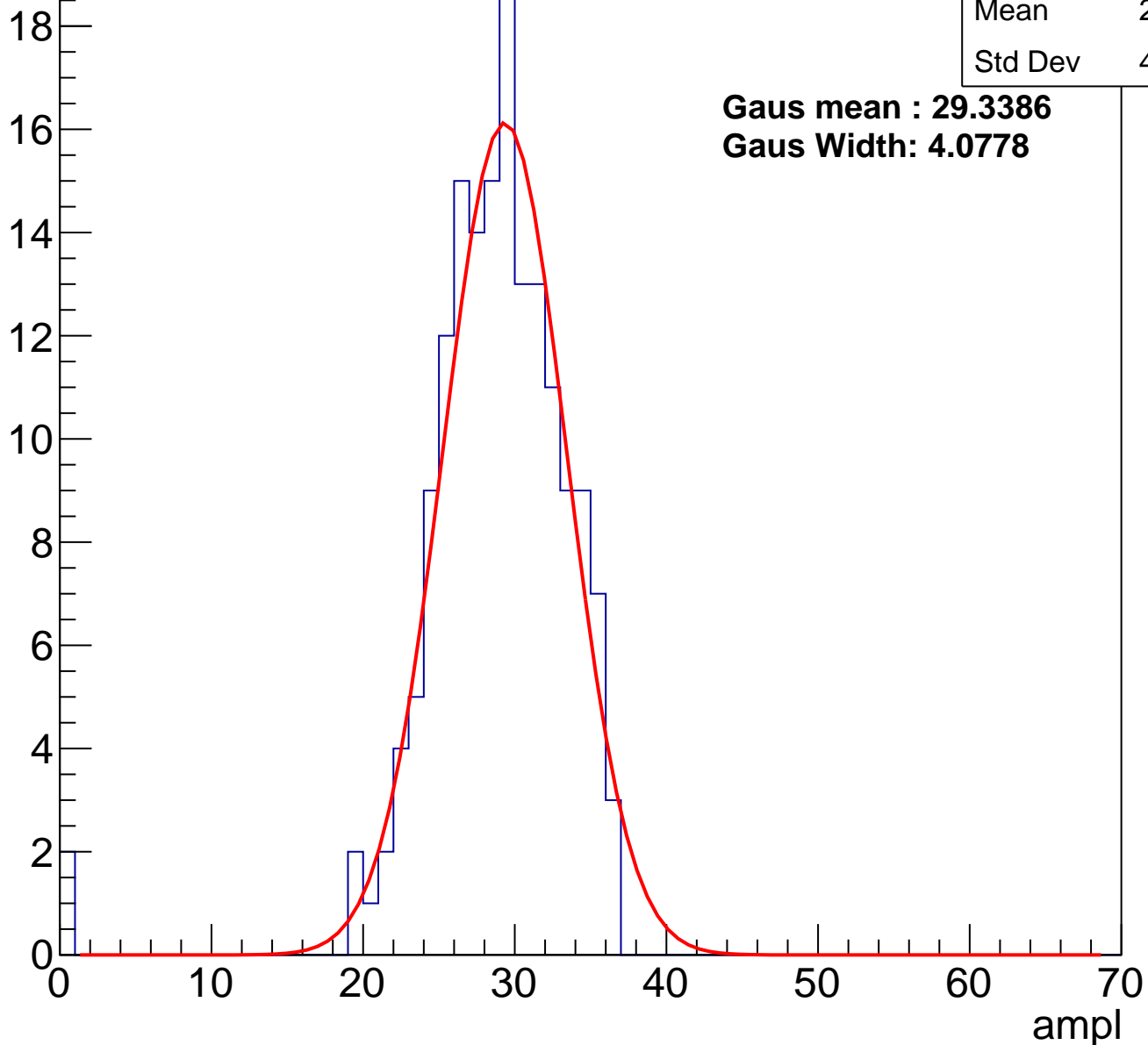


Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch41, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

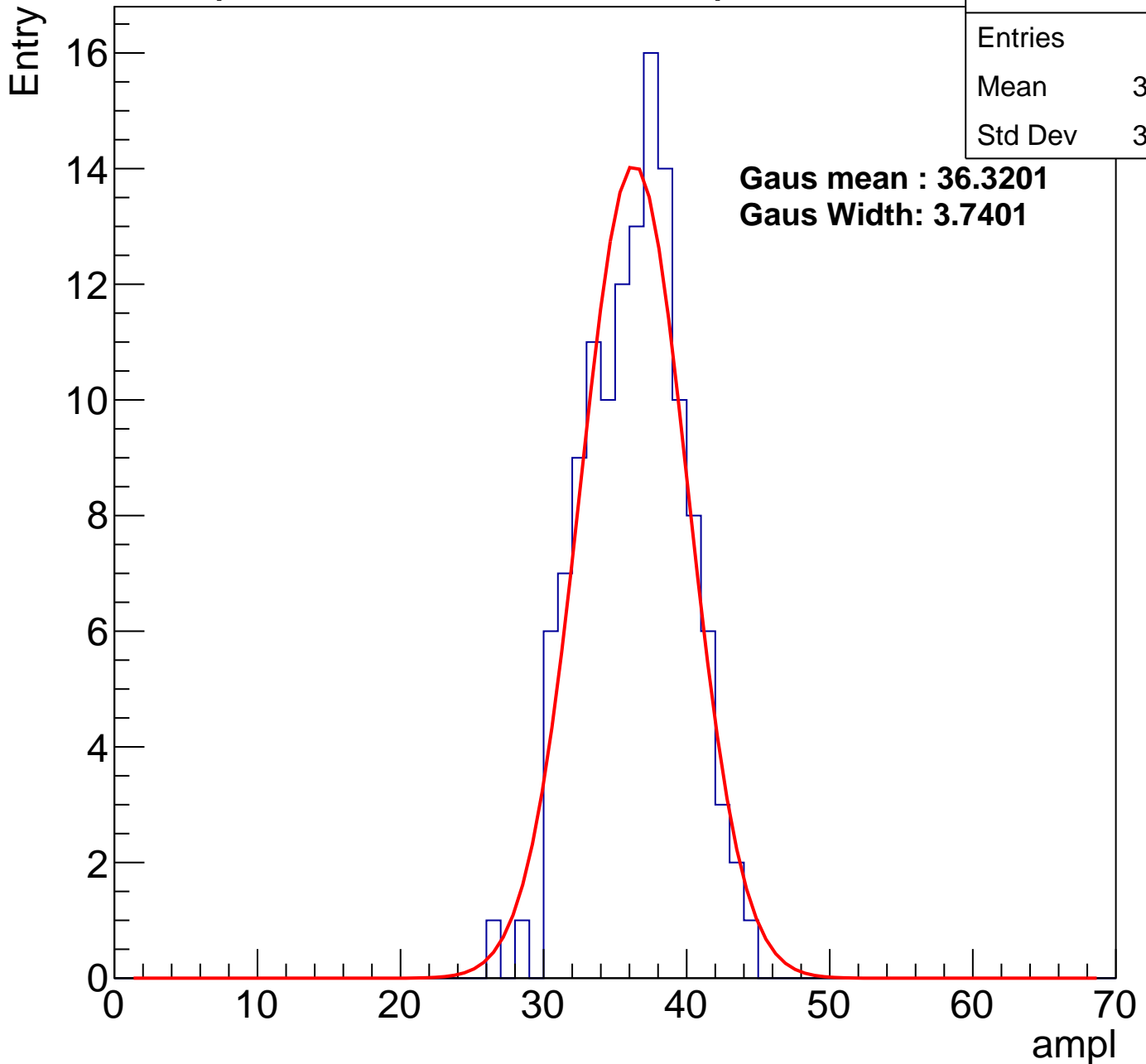


B0L101S, U18-ch41, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	35.89
Std Dev	3.462

Gaus mean : 36.3201
Gaus Width: 3.7401



B0L101S, U18-ch41, adc2

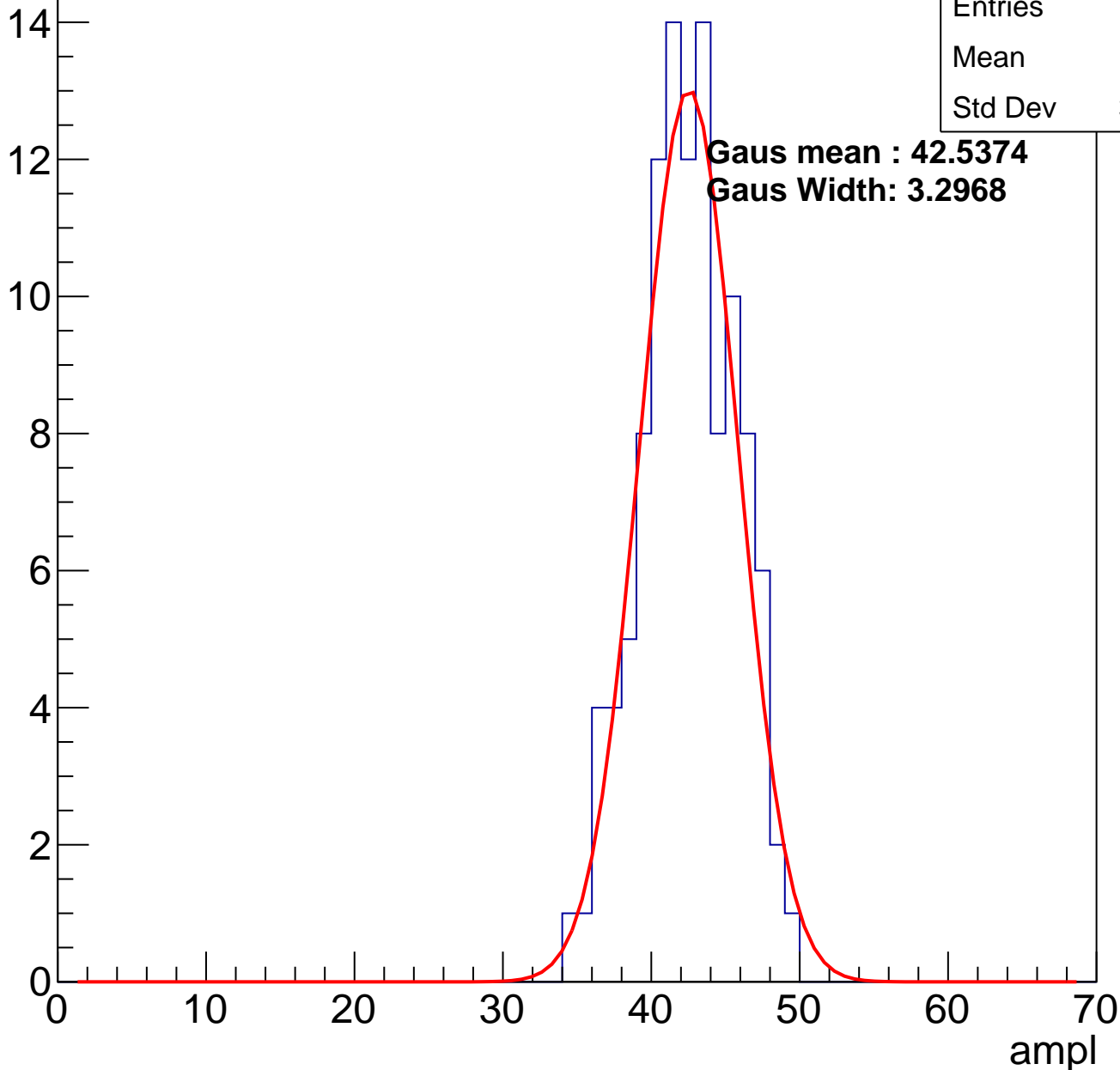
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	110
Mean	42
Std Dev	3.171

Gaus mean : 42.5374

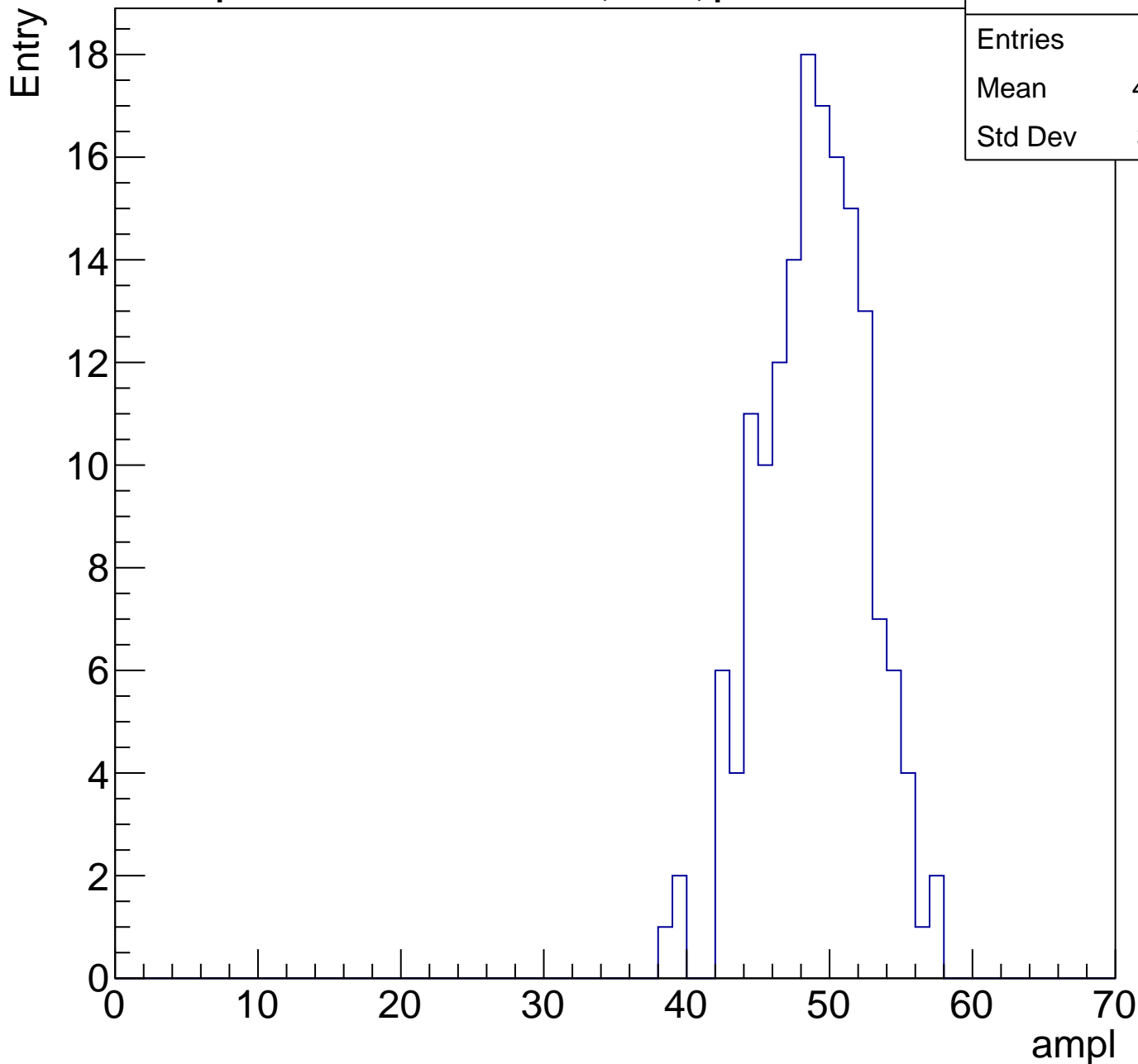
Gaus Width: 3.2968



B0L101S, U18-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

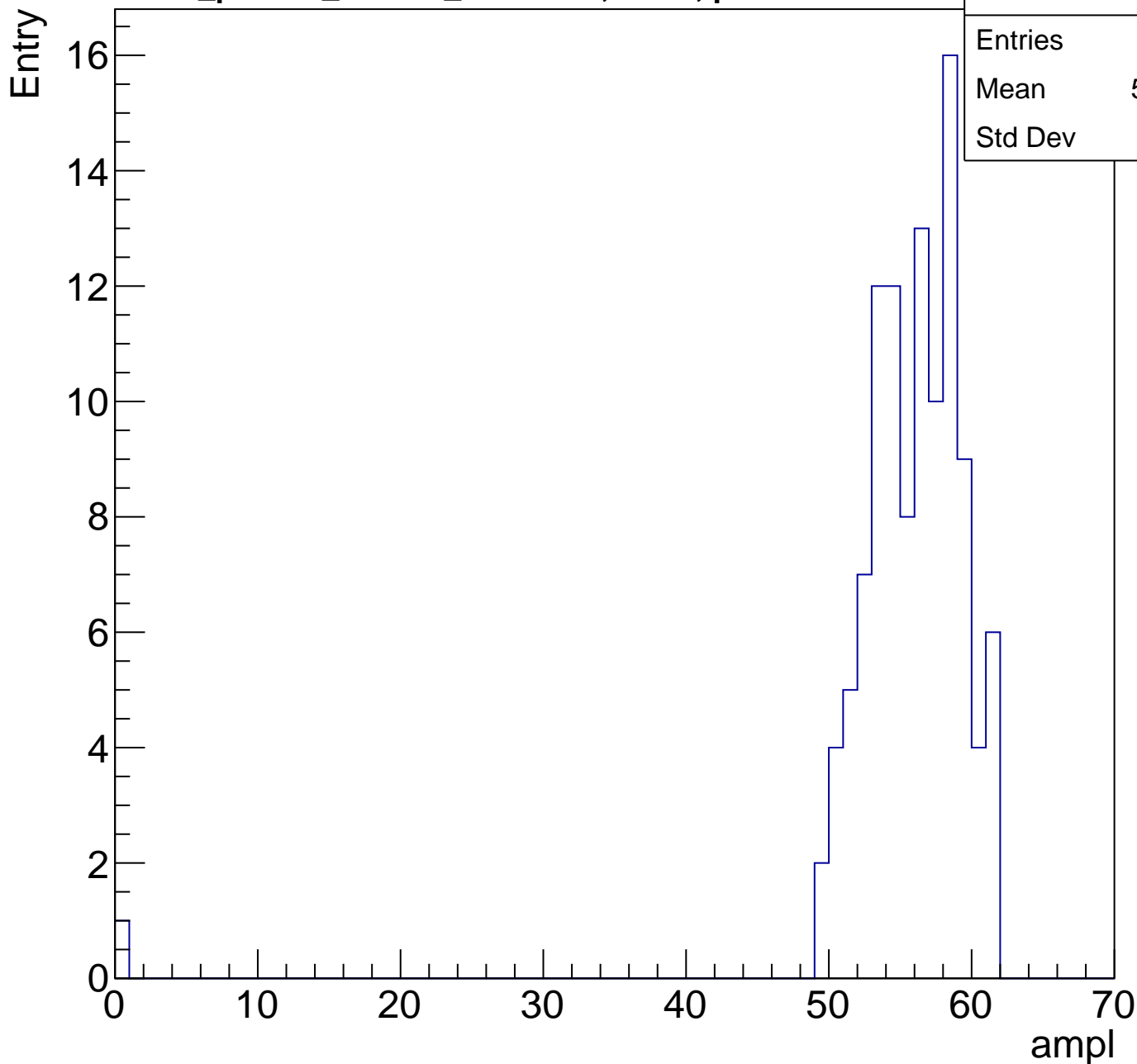
Entries	159
Mean	48.47
Std Dev	3.641



B0L101S, U18-ch41, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.08
Std Dev	6.1



B0L101S, U18-ch41, adc5

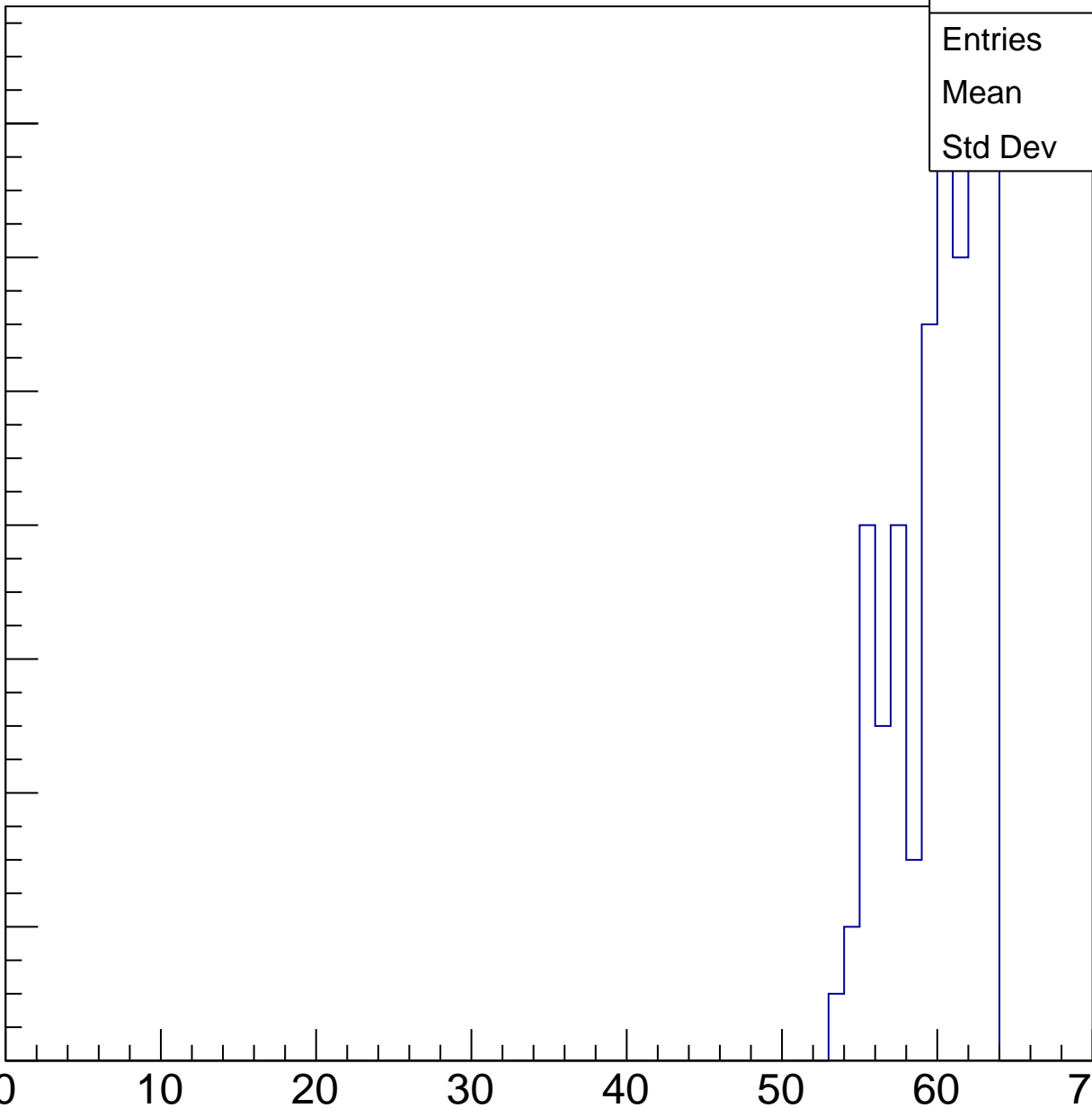
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	59.62
Std Dev	2.716

ampl

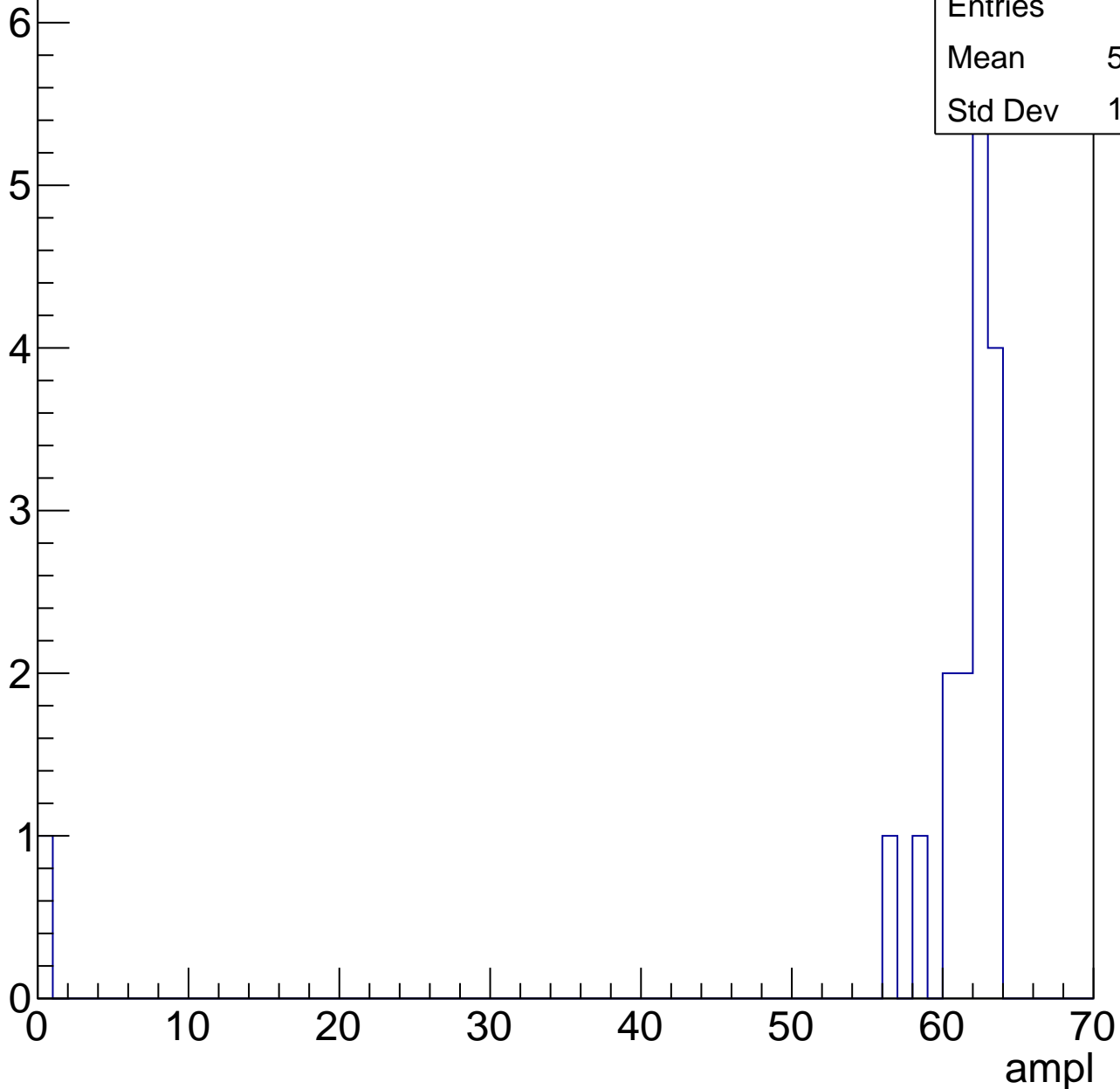


B0L101S, U18-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

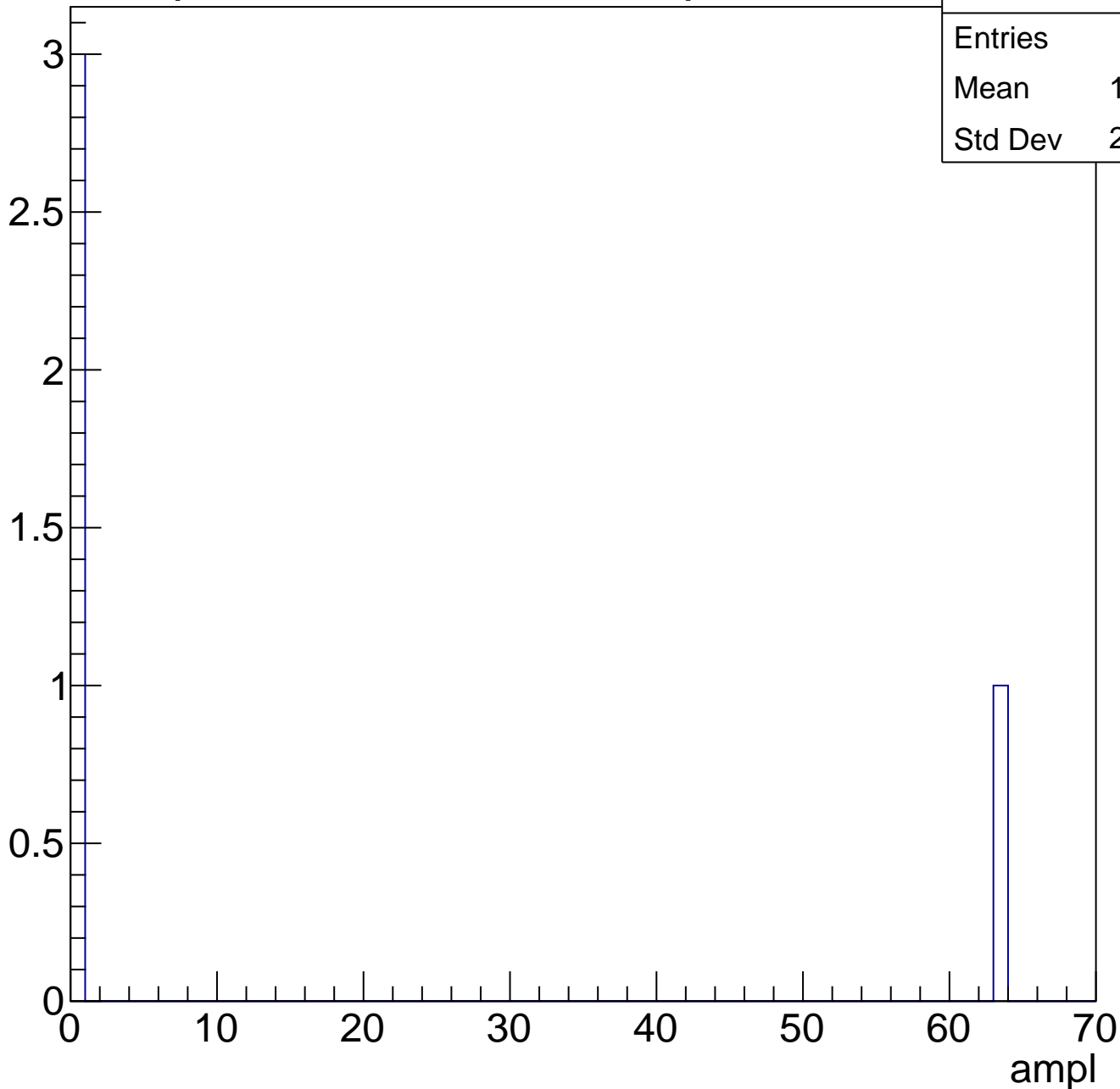
Entries	17
Mean	57.65
Std Dev	14.53



B0L101S, U18-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



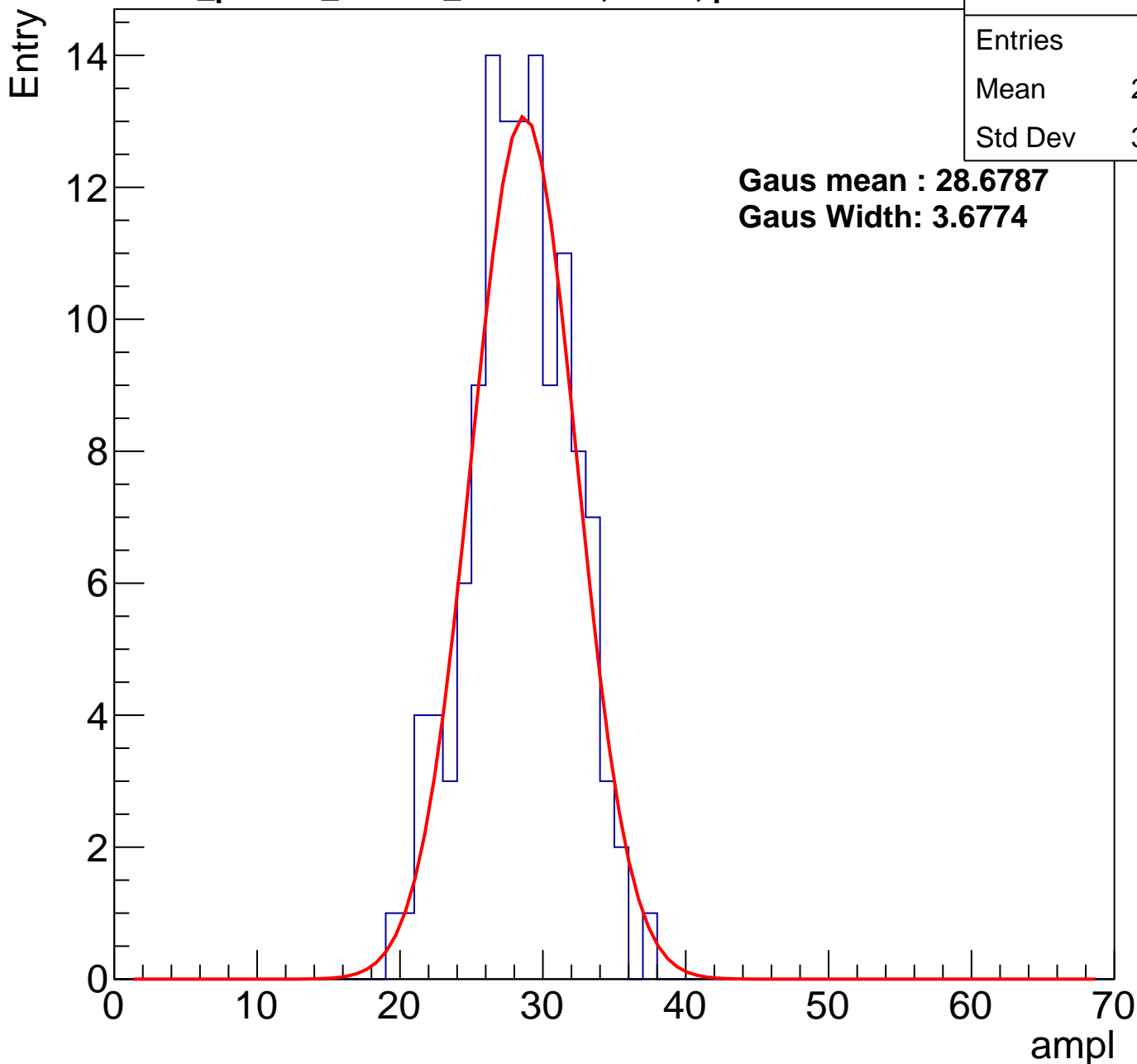
B0L101S, U18-ch42, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	27.98
Std Dev	3.544

Gaus mean : 28.6787

Gaus Width: 3.6774



B0L101S, U18-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	35.2
Std Dev	3.745

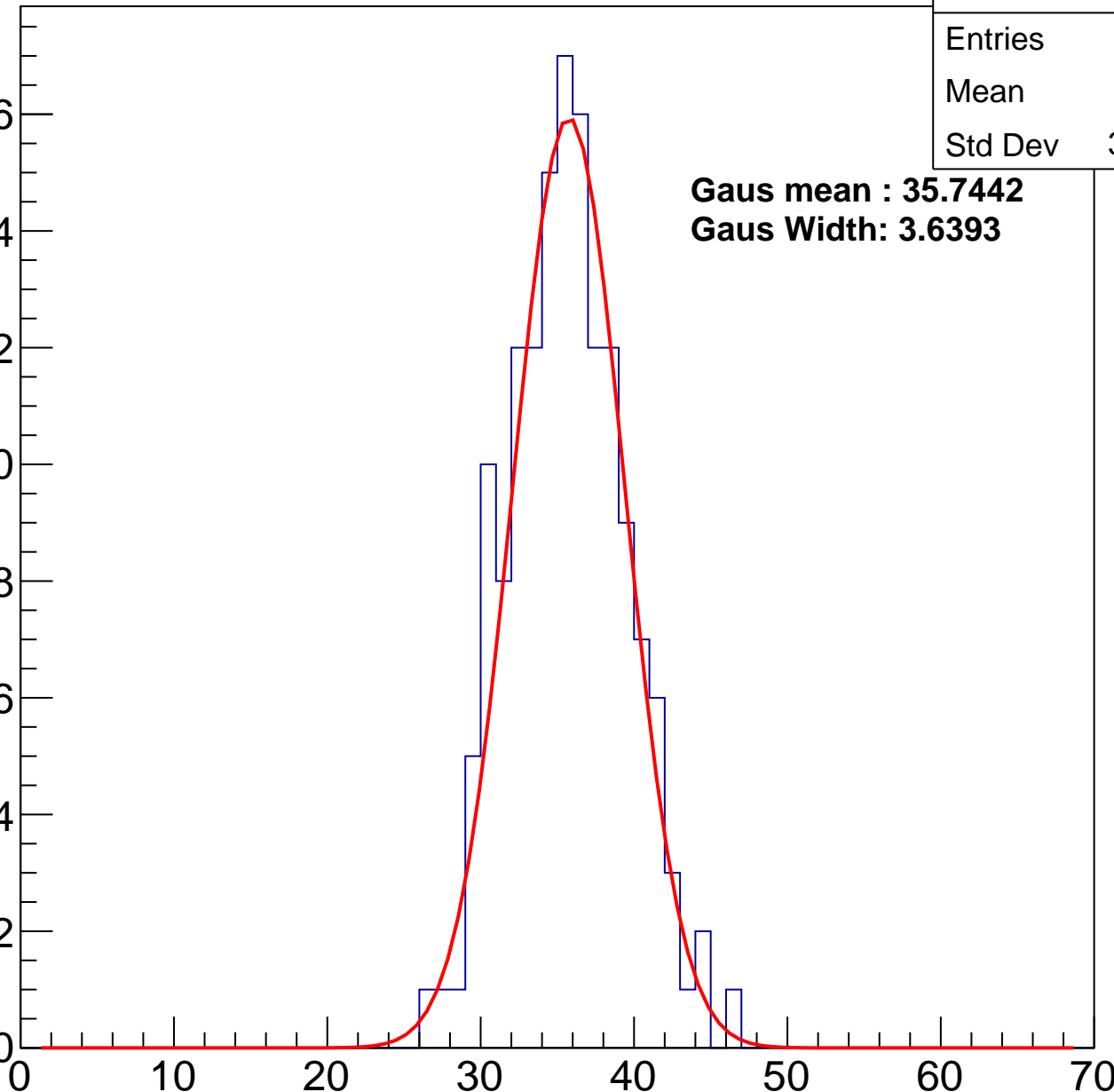
Gaus mean : 35.7442

Gaus Width: 3.6393

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch42, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

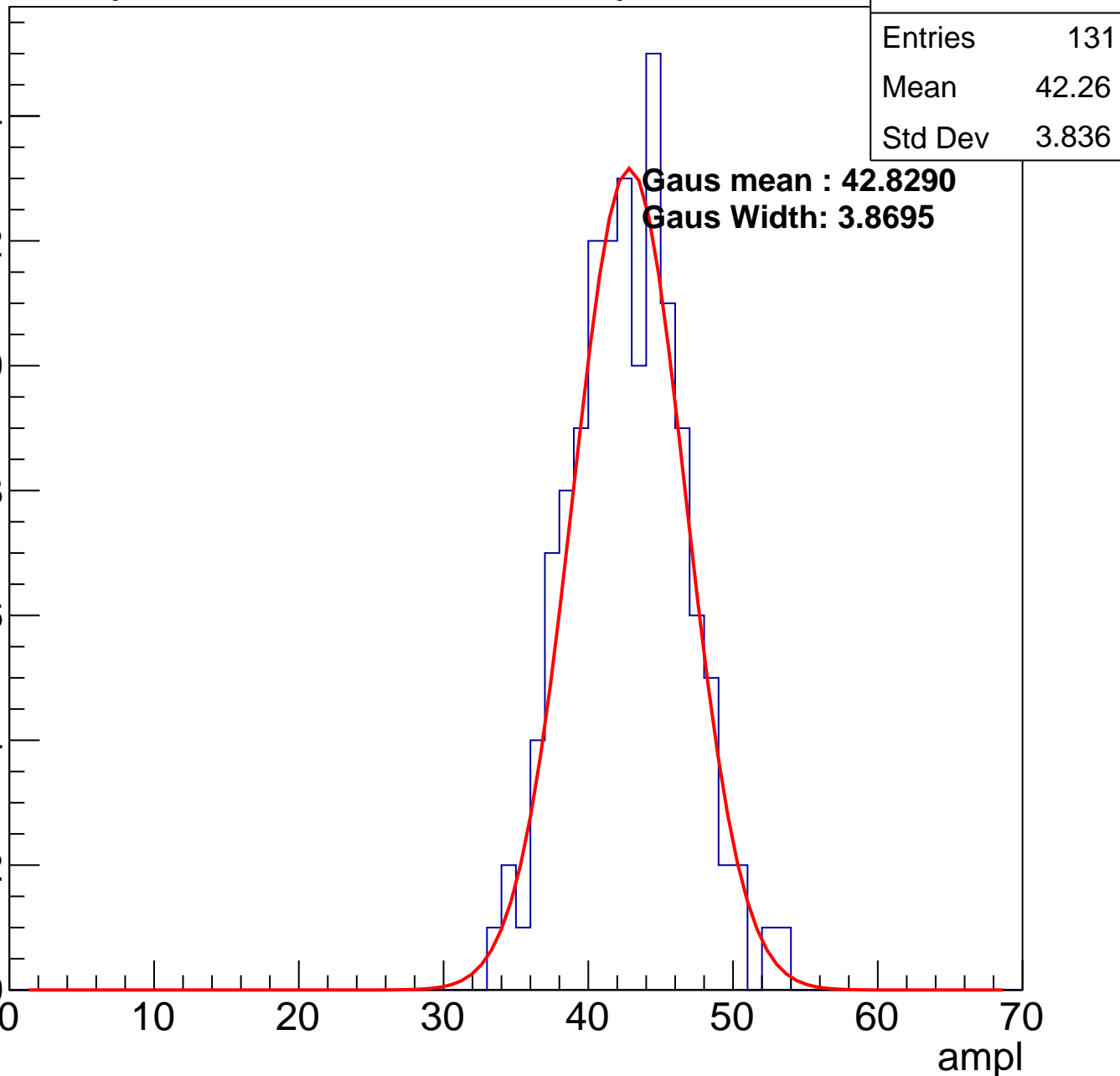
60

ampl

Entries	131
Mean	42.26
Std Dev	3.836

Gaus mean : 42.8290

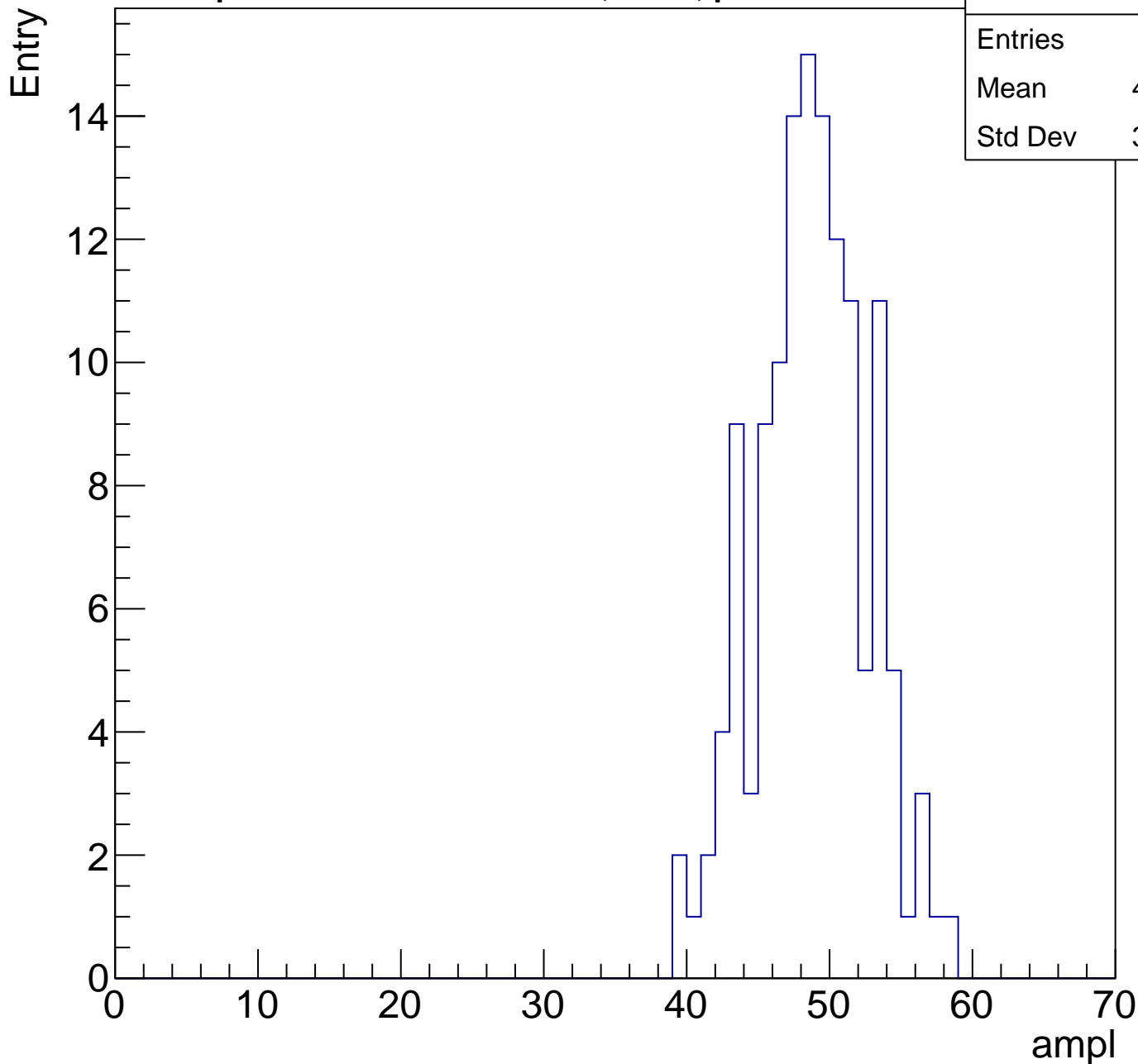
Gaus Width: 3.8695



B0L101S, U18-ch42, adc3

calib_packv5_042523_0143.root, FC#1, port C1

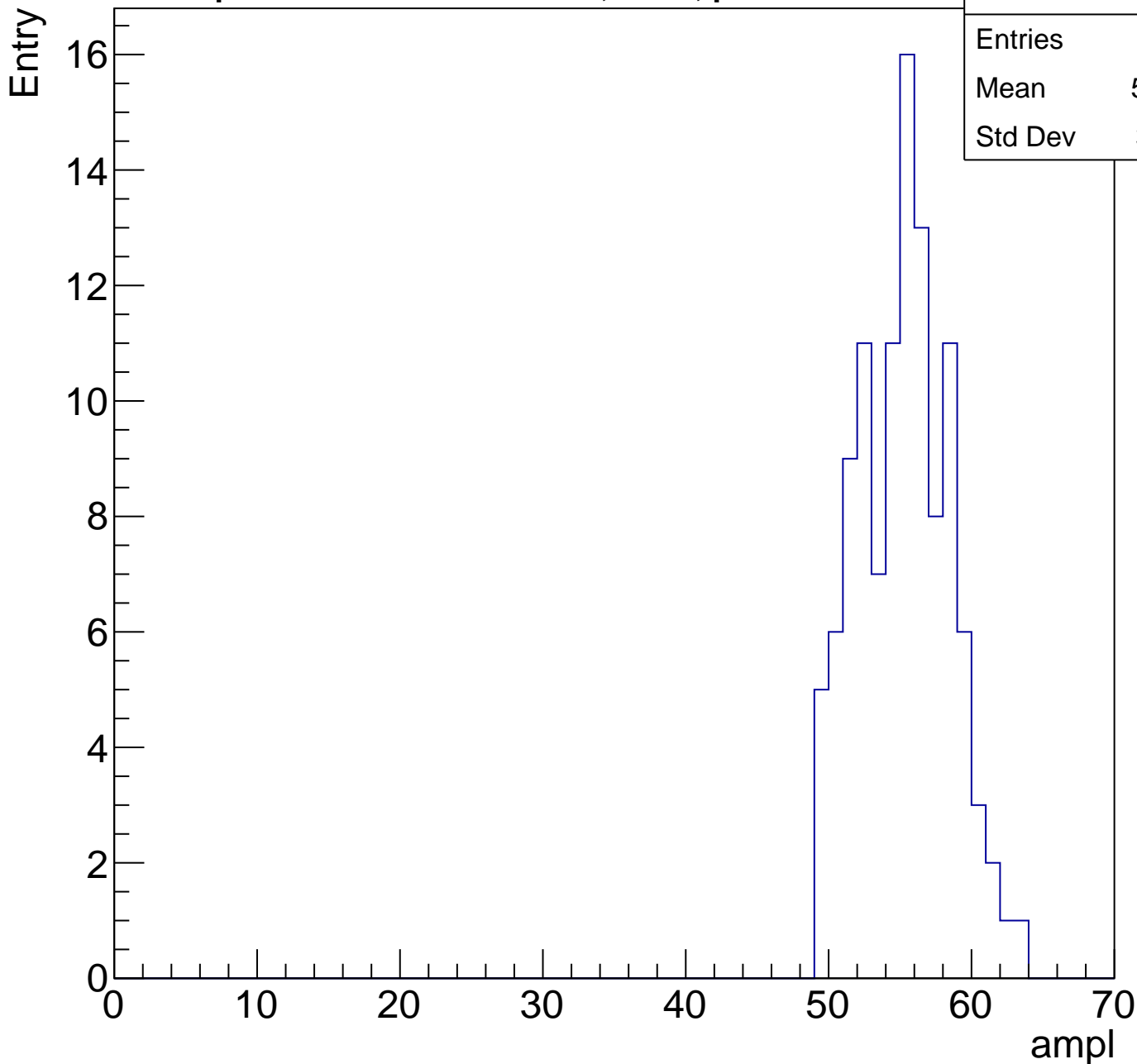
Entries	133
Mean	48.33
Std Dev	3.858



B0L101S, U18-ch42, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	54.76
Std Dev	3.171



B0L101S, U18-ch42, adc5

calib_packv5_042523_0143.root, FC#1, port C1

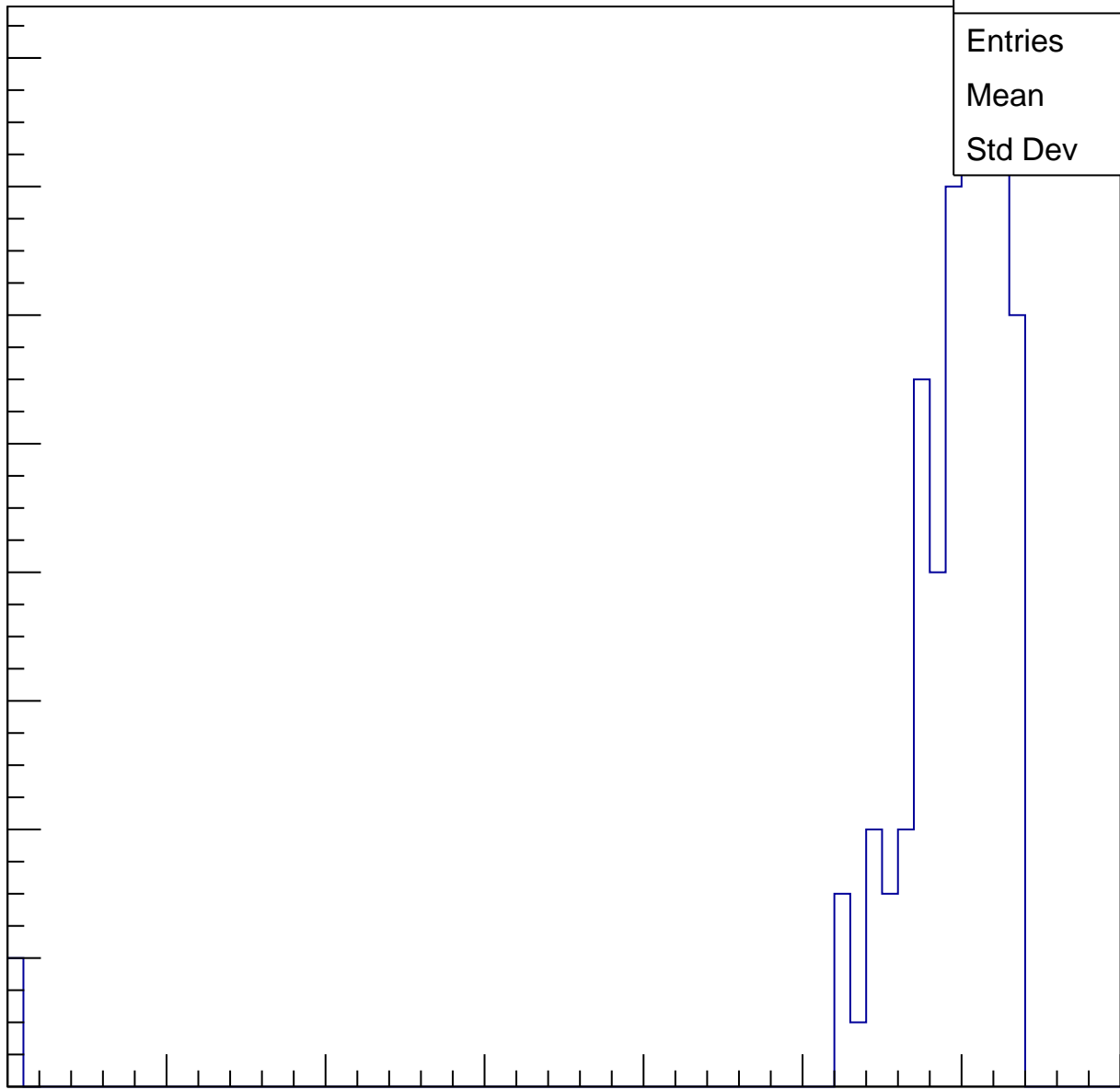
Entries	109
Mean	58.3
Std Dev	8.428

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

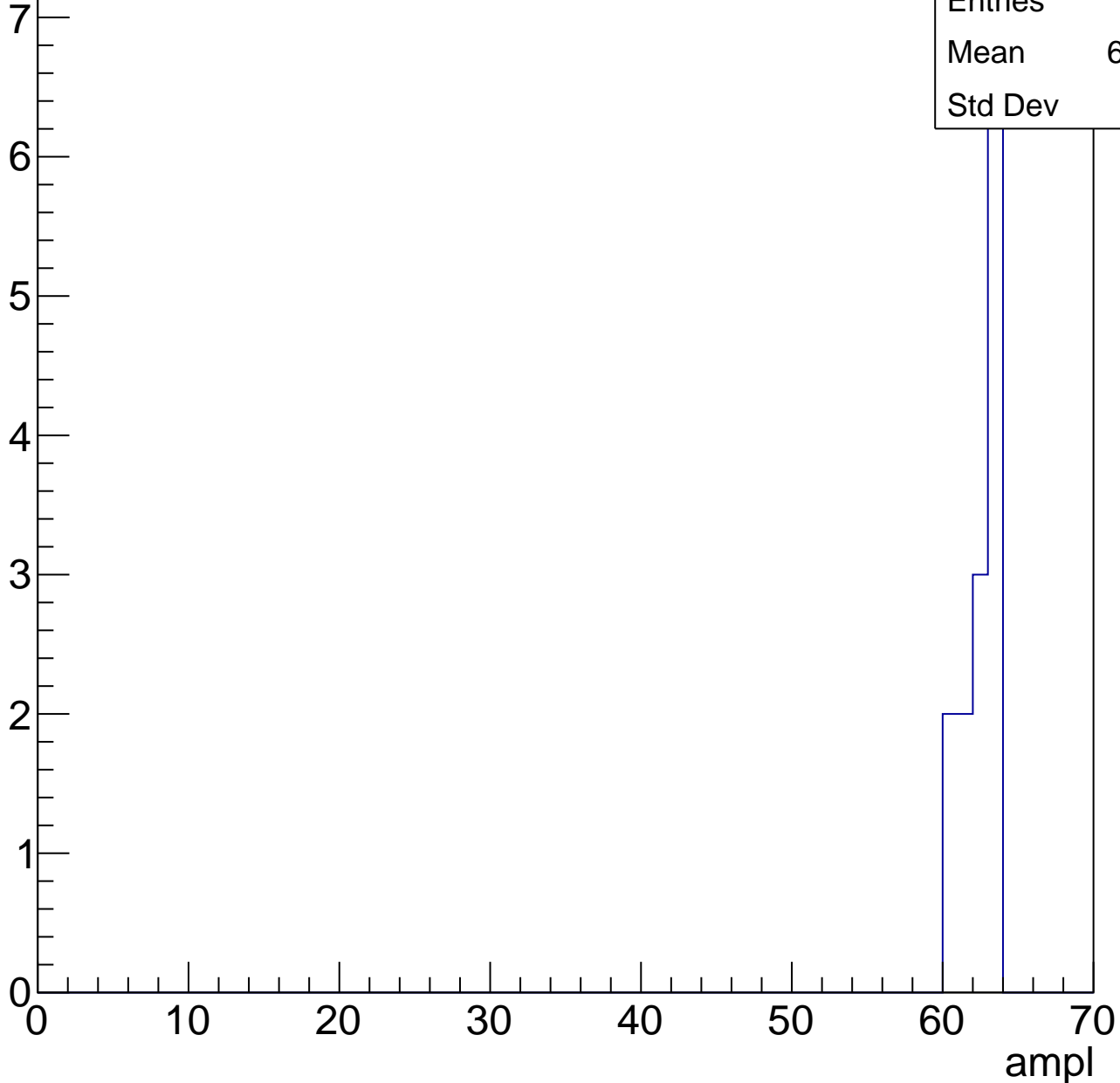


B0L101S, U18-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	62.07
Std Dev	1.1



B0L101S, U18-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch43, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	29.65
Std Dev	4.569

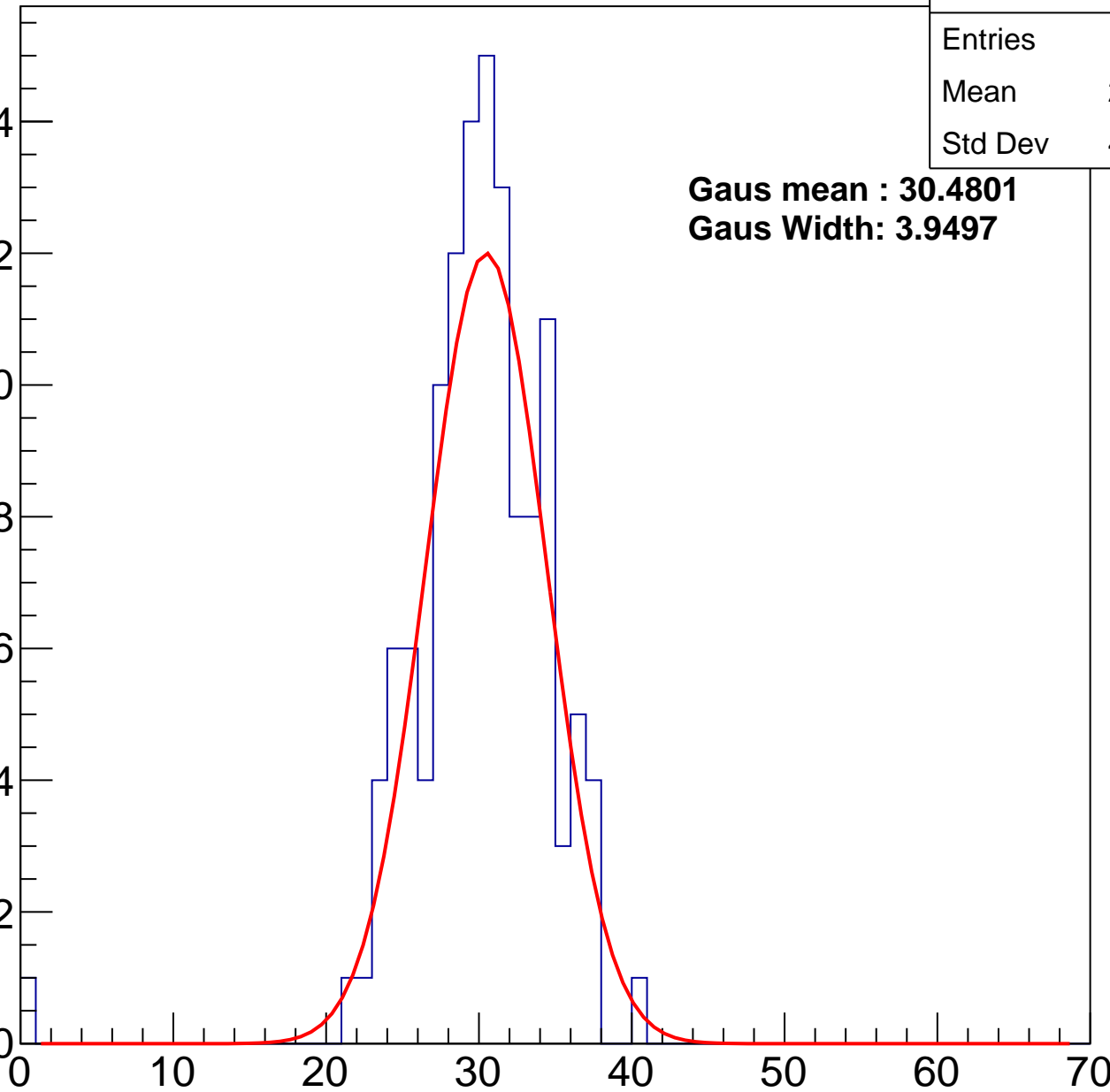
Gaus mean : 30.4801

Gaus Width: 3.9497

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch43, adc1

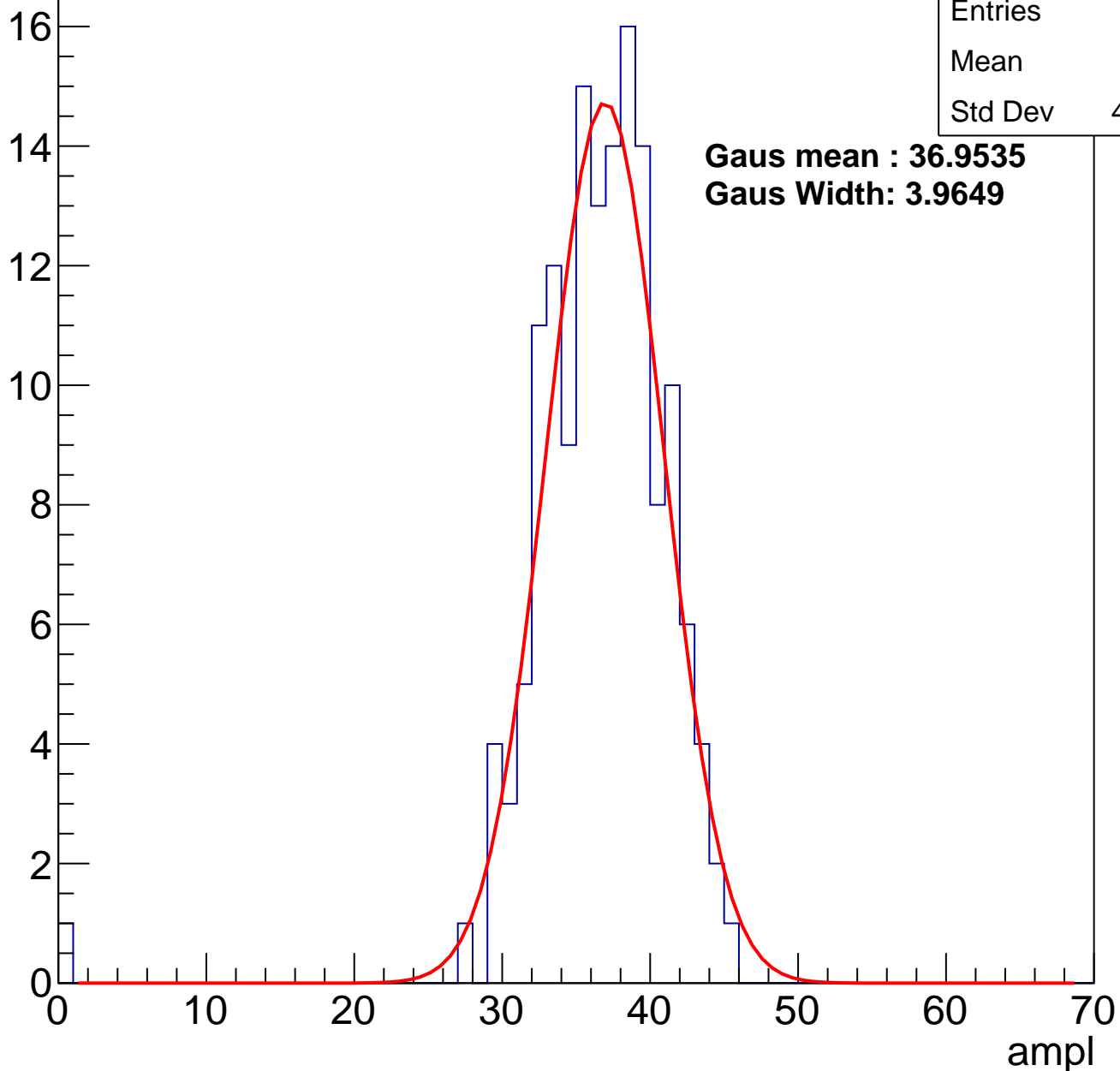
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	36.2
Std Dev	4.733

Gaus mean : 36.9535

Gaus Width: 3.9649

Entry



B0L101S, U18-ch43, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

127

Mean

43.57

Std Dev

3.634

Gaus mean : 43.9010

Gaus Width: 3.6230

0

10

20

30

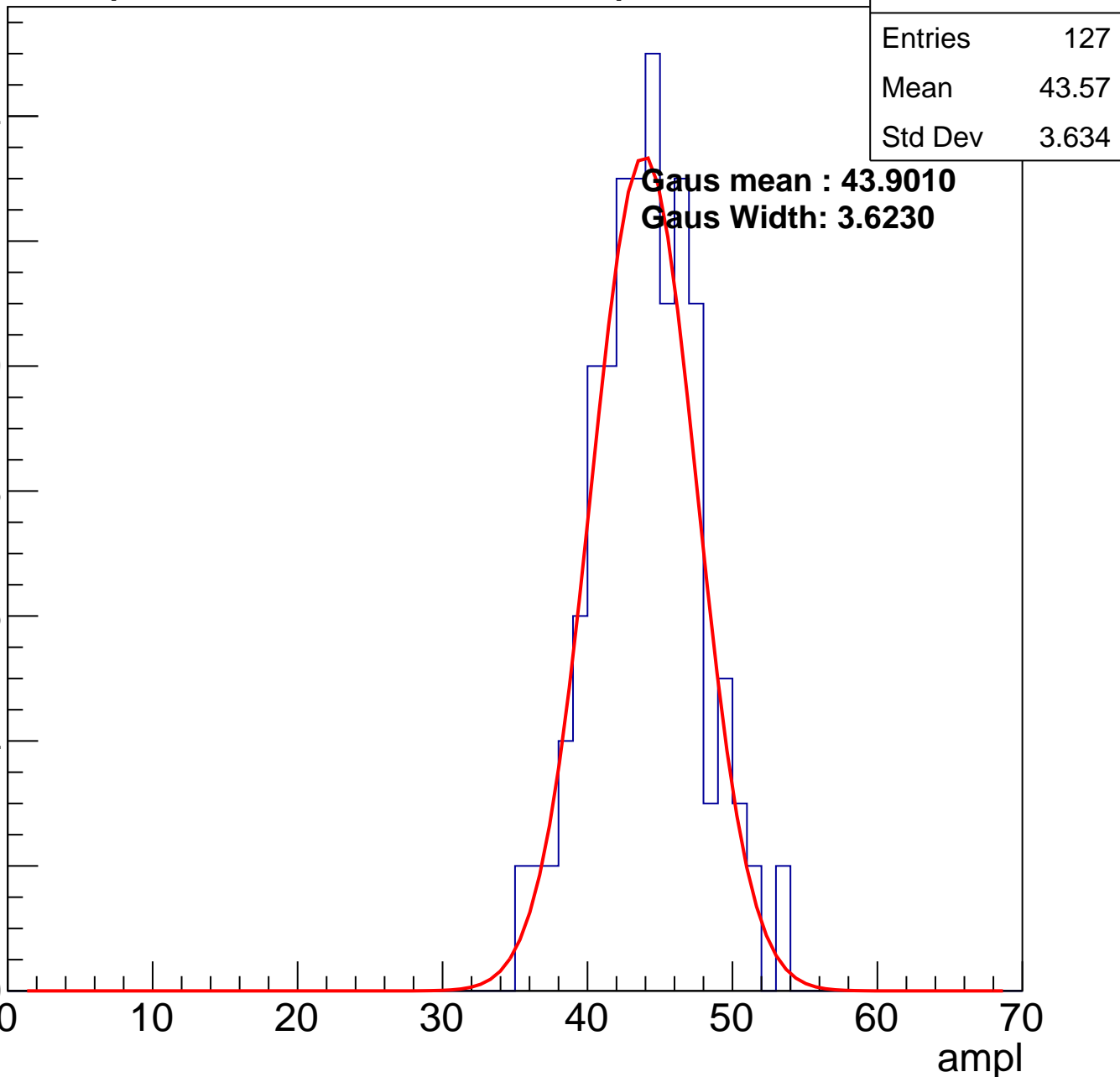
40

50

60

70

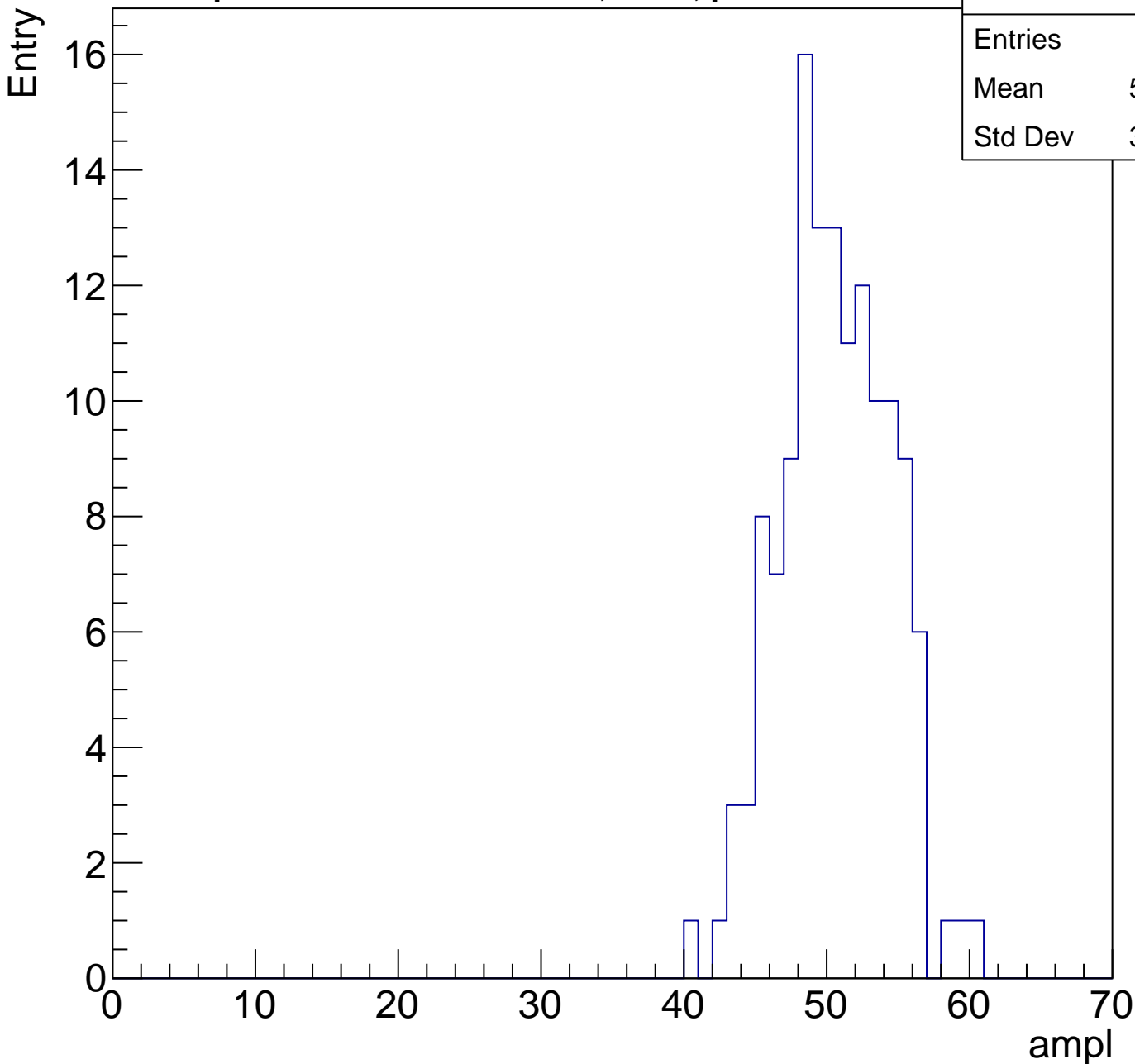
ampl



B0L101S, U18-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	50.12
Std Dev	3.734

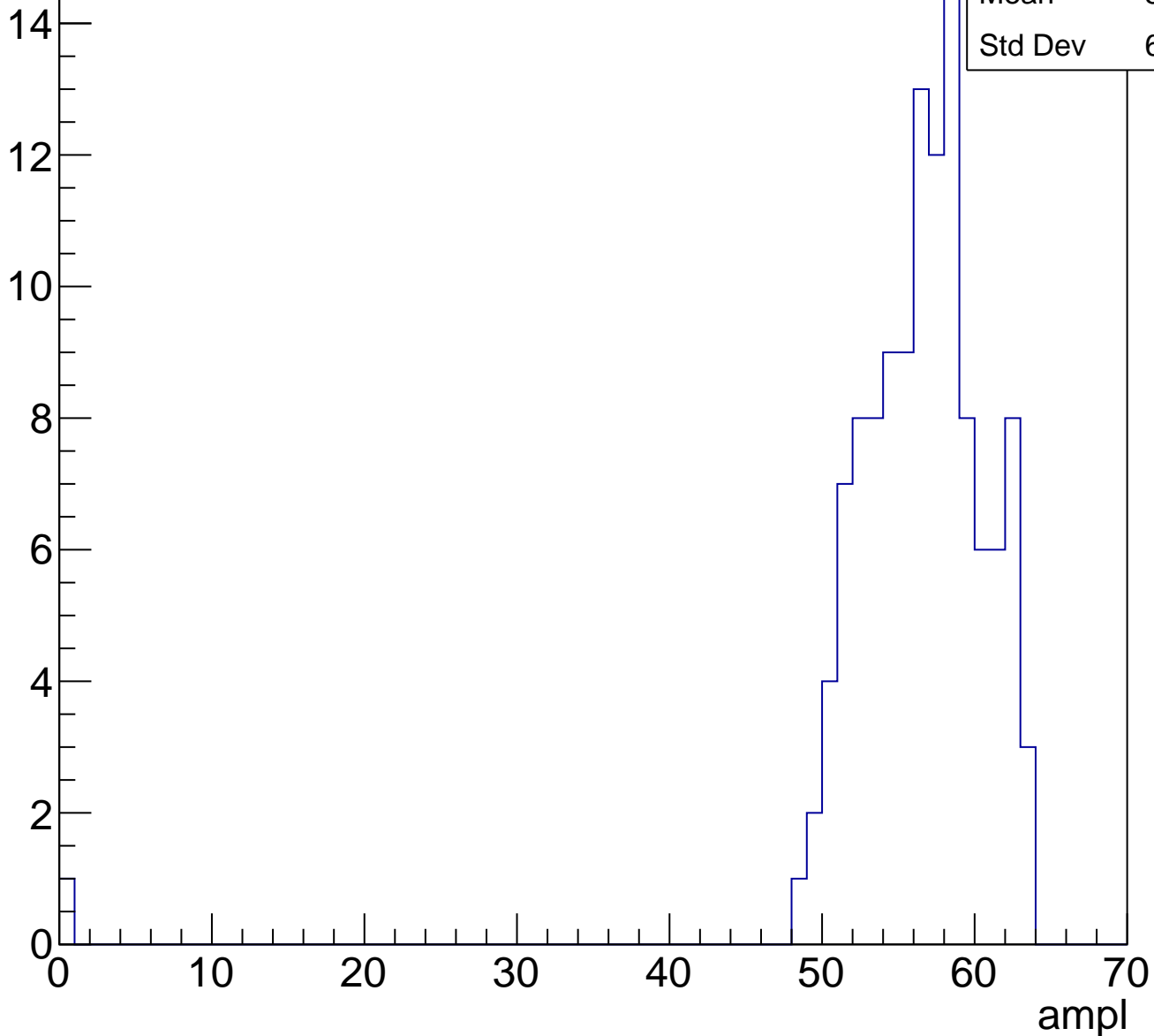


B0L101S, U18-ch43, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.74
Std Dev	6.244

Entry



B0L101S, U18-ch43, adc5

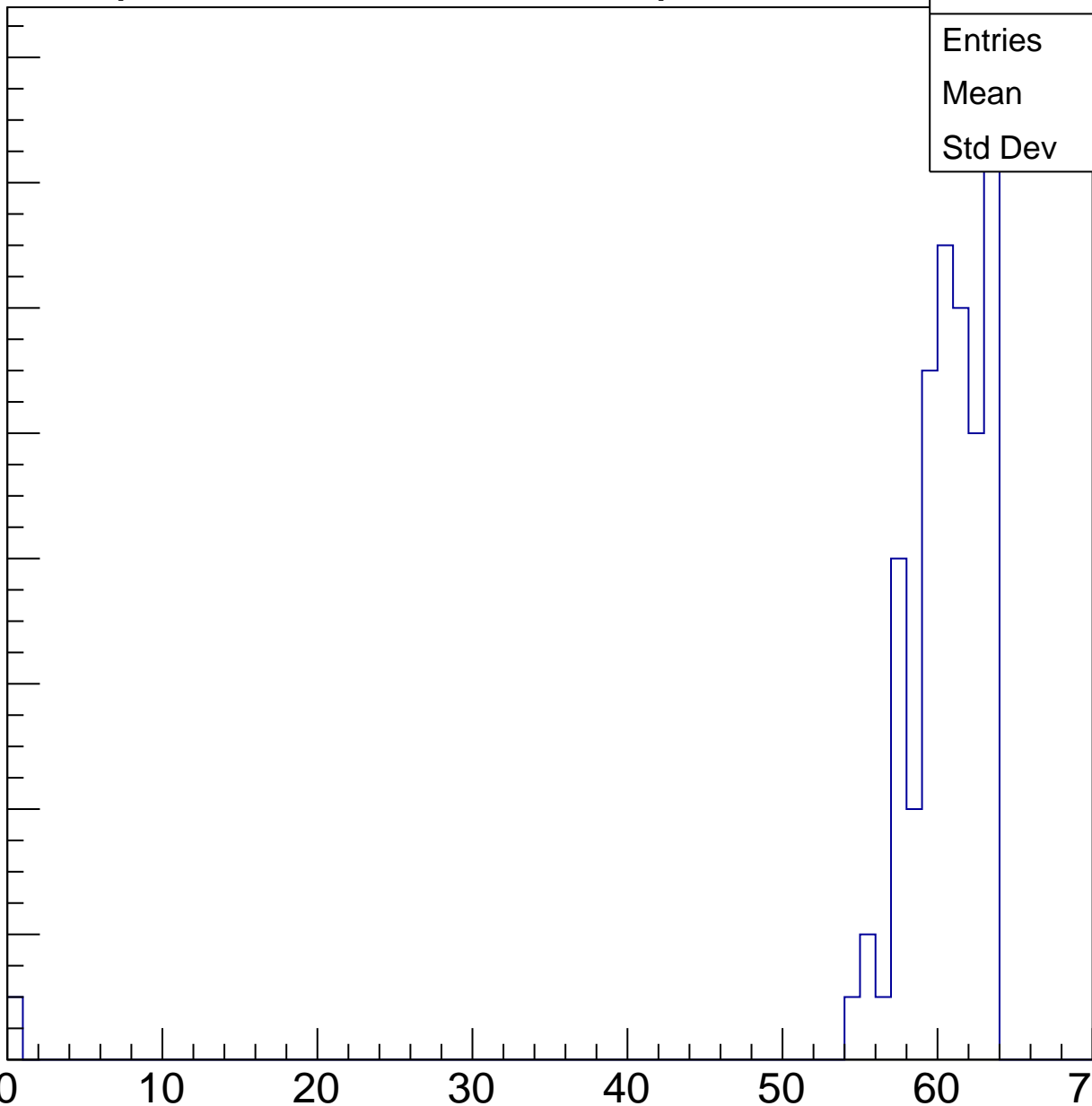
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.46
Std Dev	7.092

ampl



B0L101S, U18-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L101S, U18-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch44, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	28.49
Std Dev	4.926

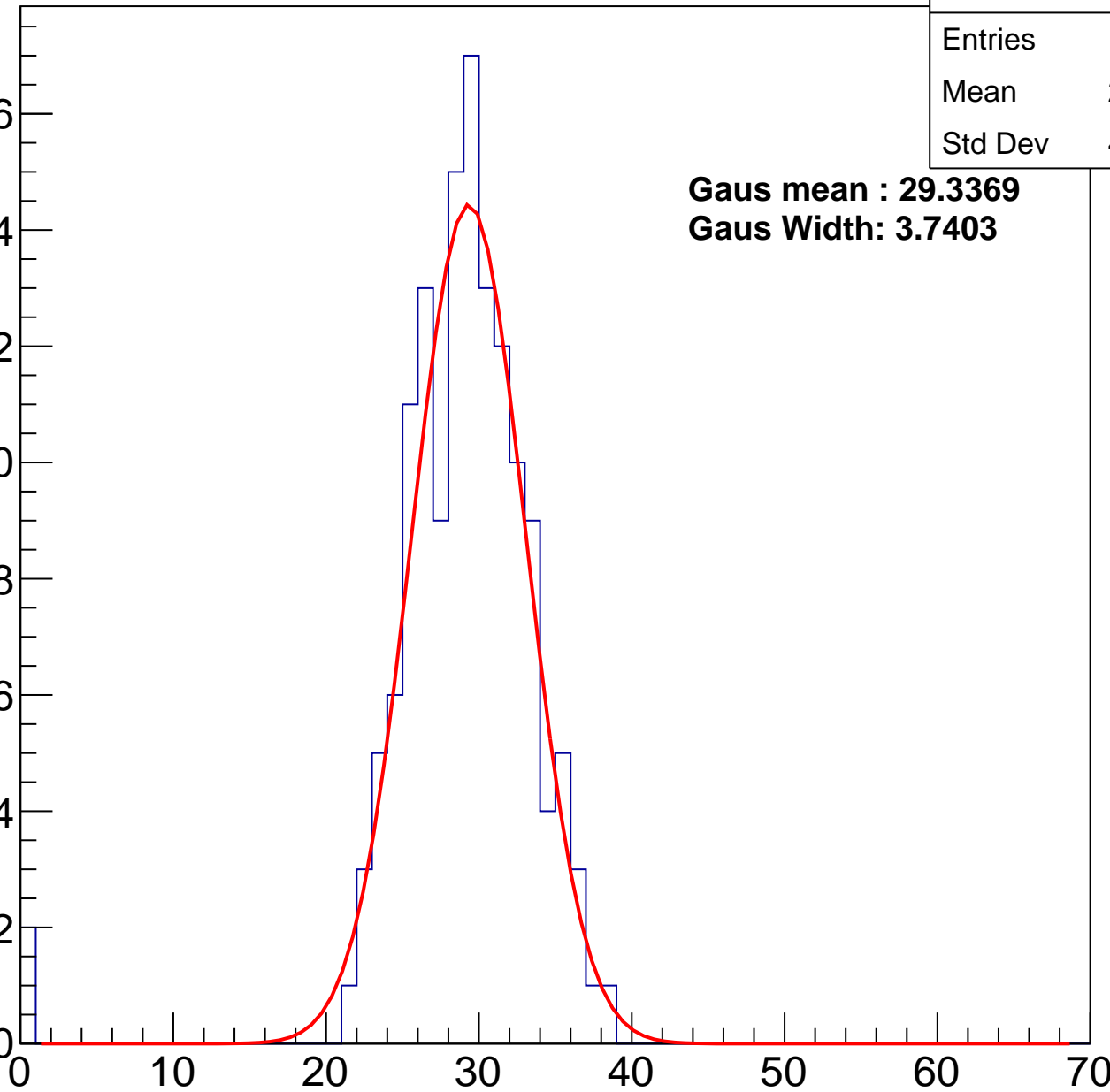
Gaus mean : 29.3369

Gaus Width: 3.7403

Entry

16
14
12
10
8
6
4
2
0

ampl



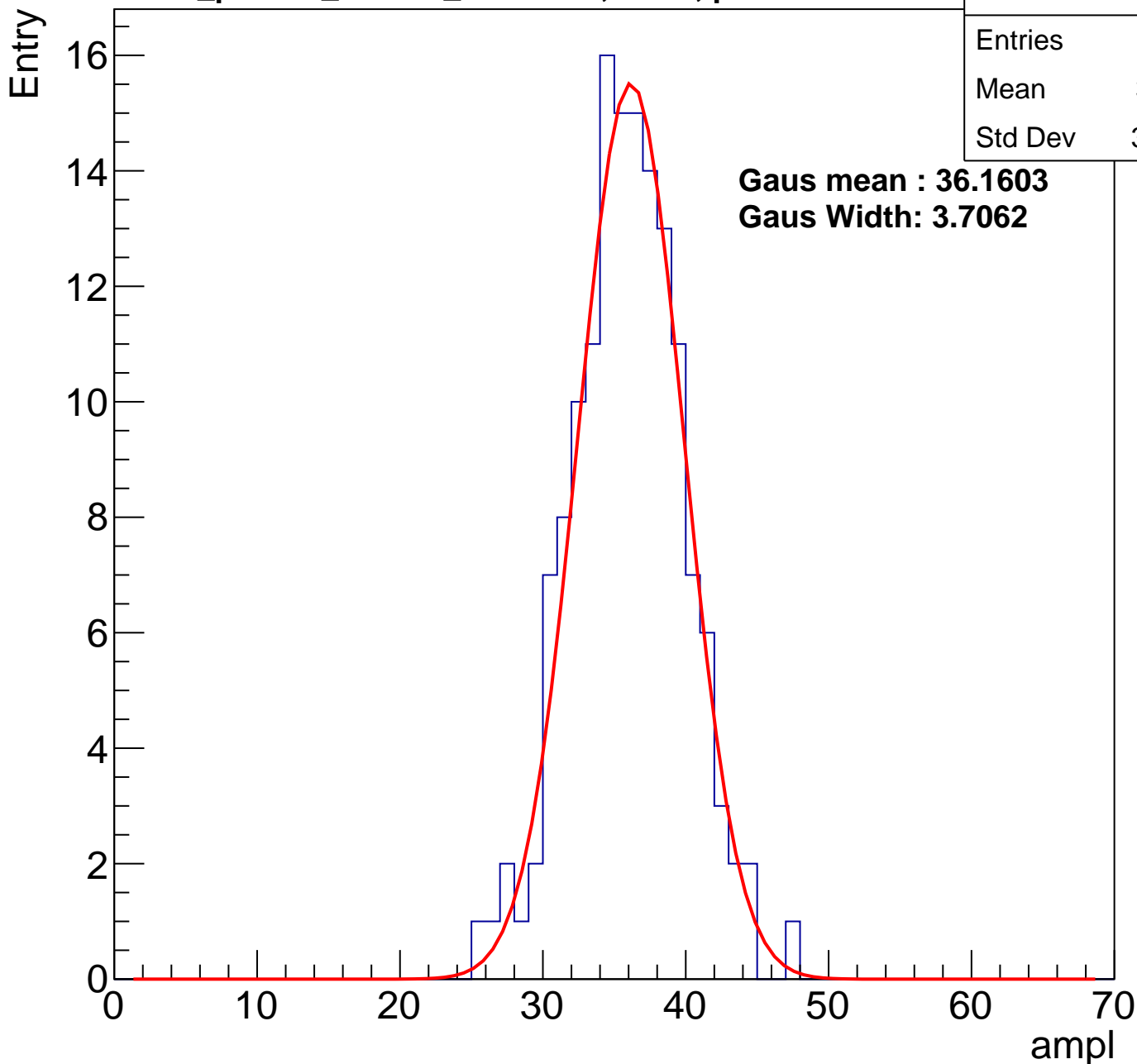
B0L101S, U18-ch44, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	35.51
Std Dev	3.839

Gaus mean : 36.1603

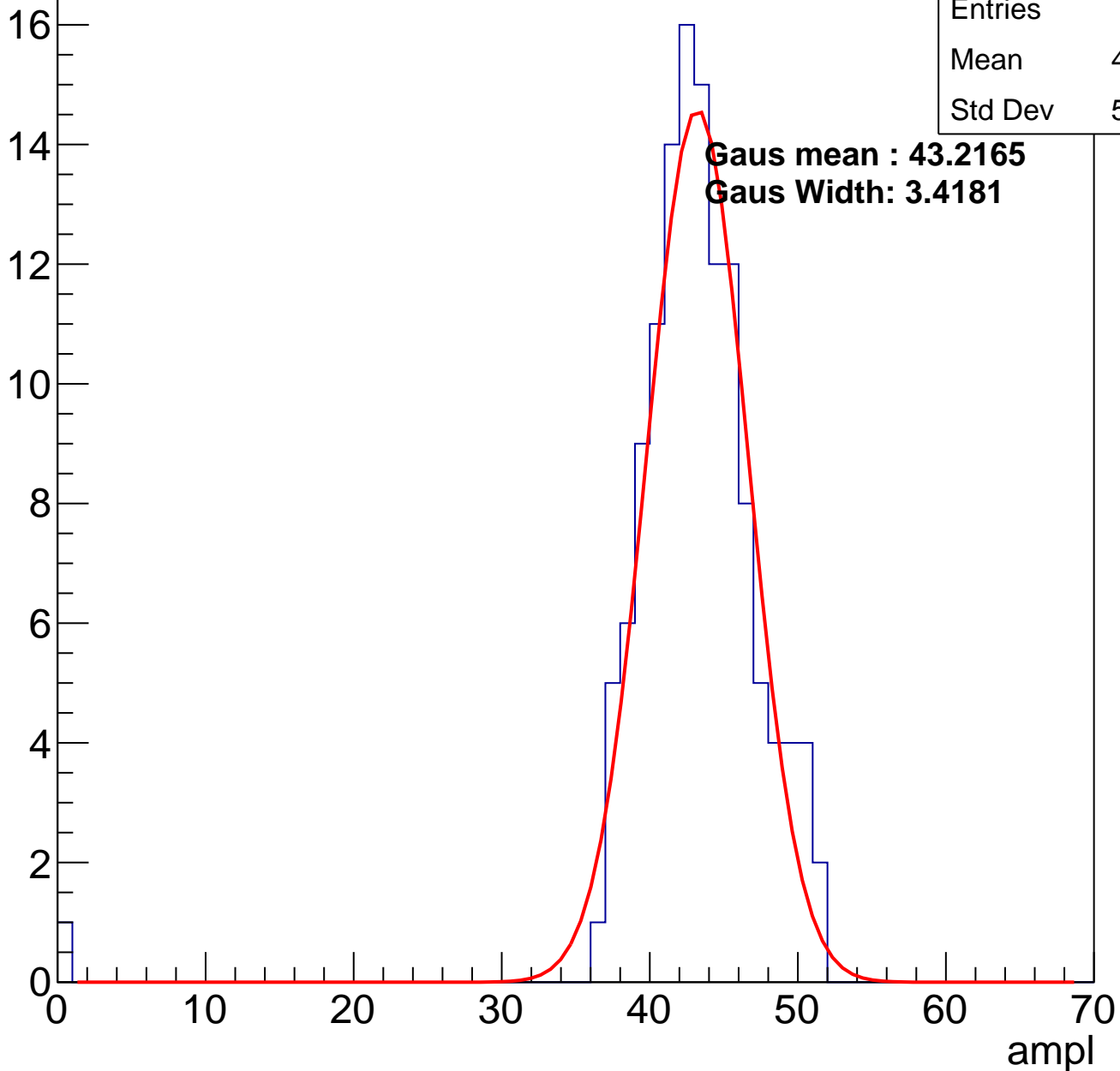
Gaus Width: 3.7062



B0L101S, U18-ch44, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

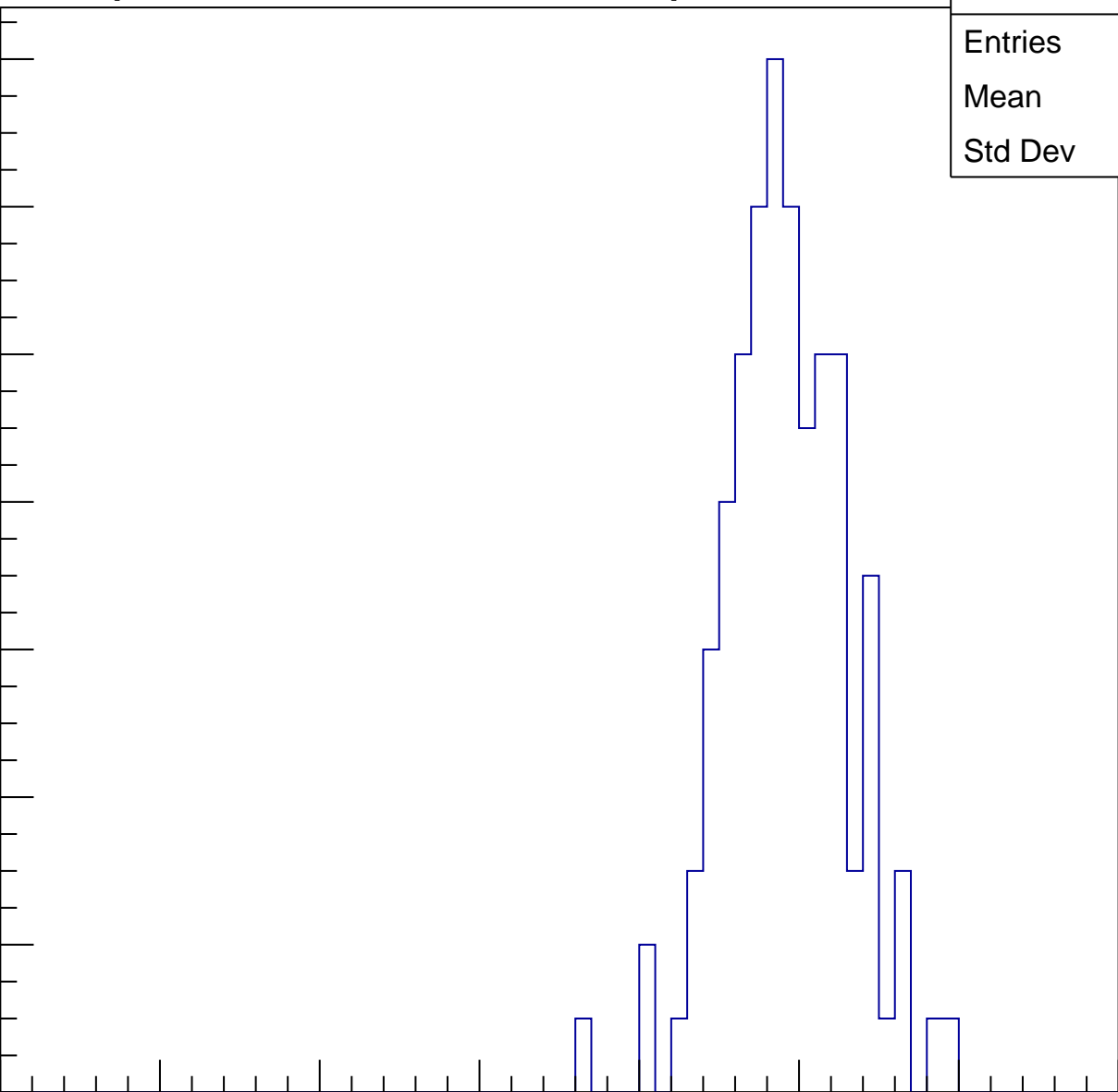
Entries	114
Mean	48.7
Std Dev	3.786

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

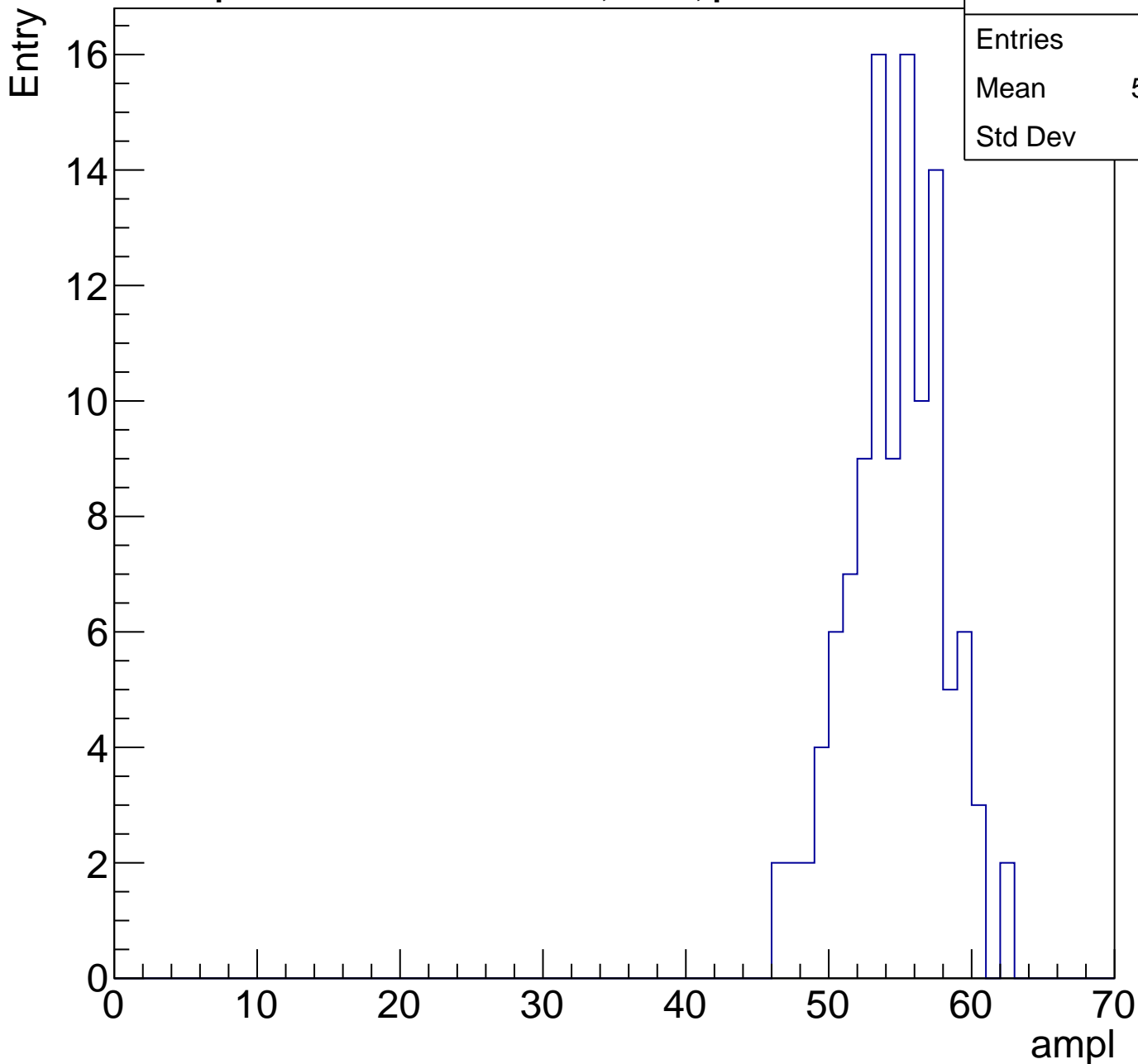
ampl



B0L101S, U18-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

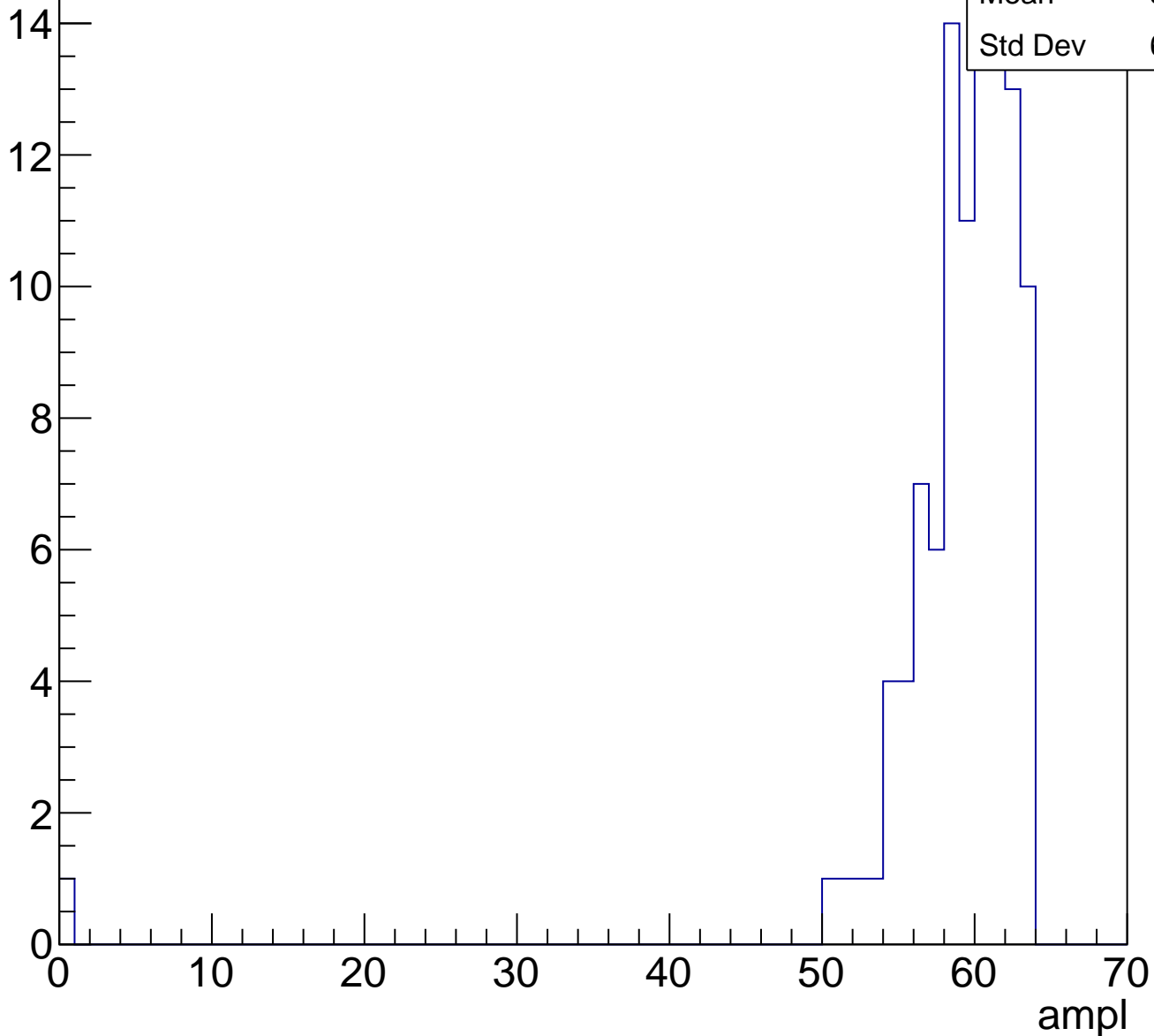
Entries	113
Mean	54.19
Std Dev	3.33



B0L101S, U18-ch44, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	26
Mean	59.38
Std Dev	11.94

Entry

10

8

6

4

2

0

0

10

20

30

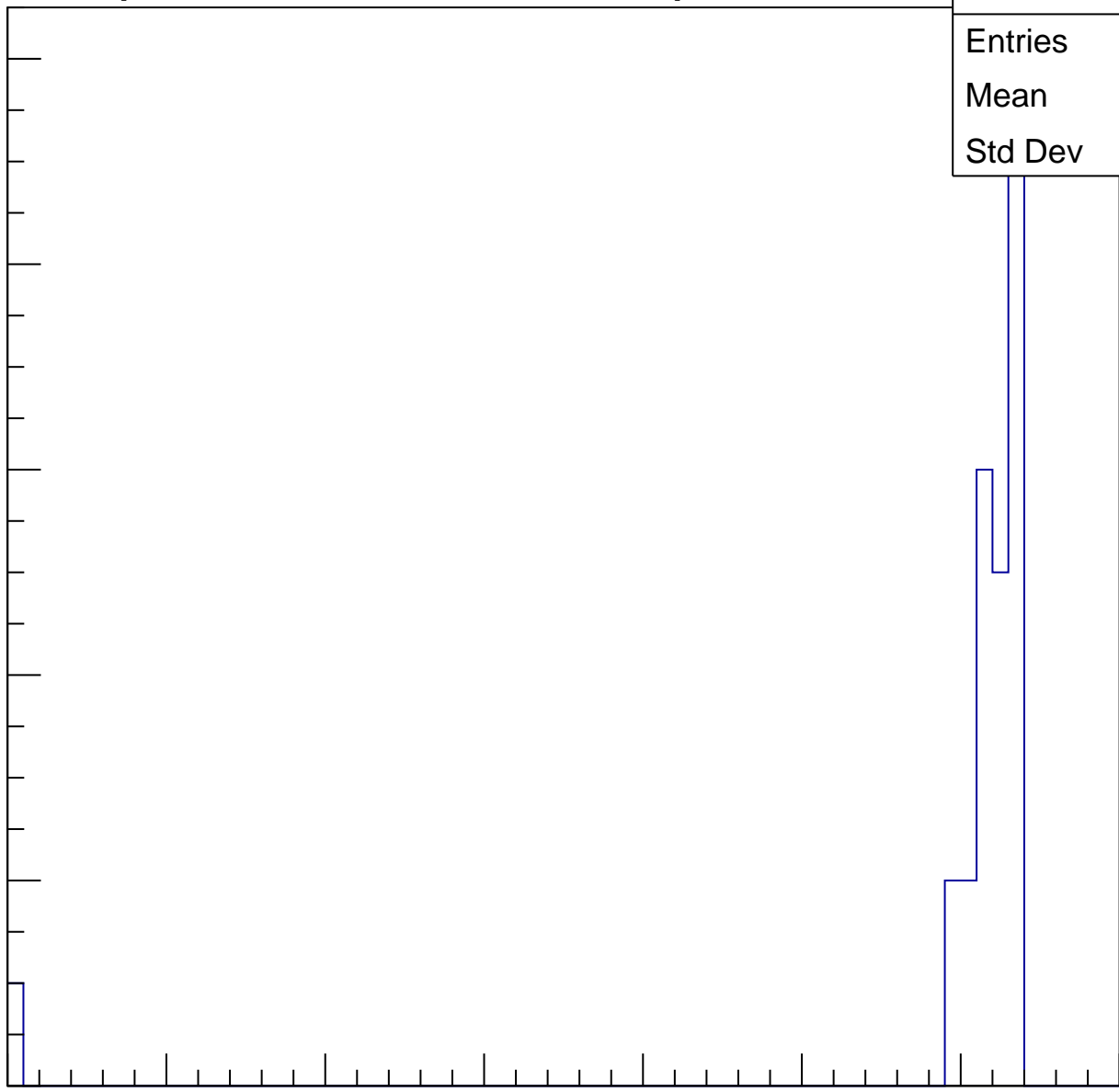
40

50

60

70

ampl



B0L101S, U18-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

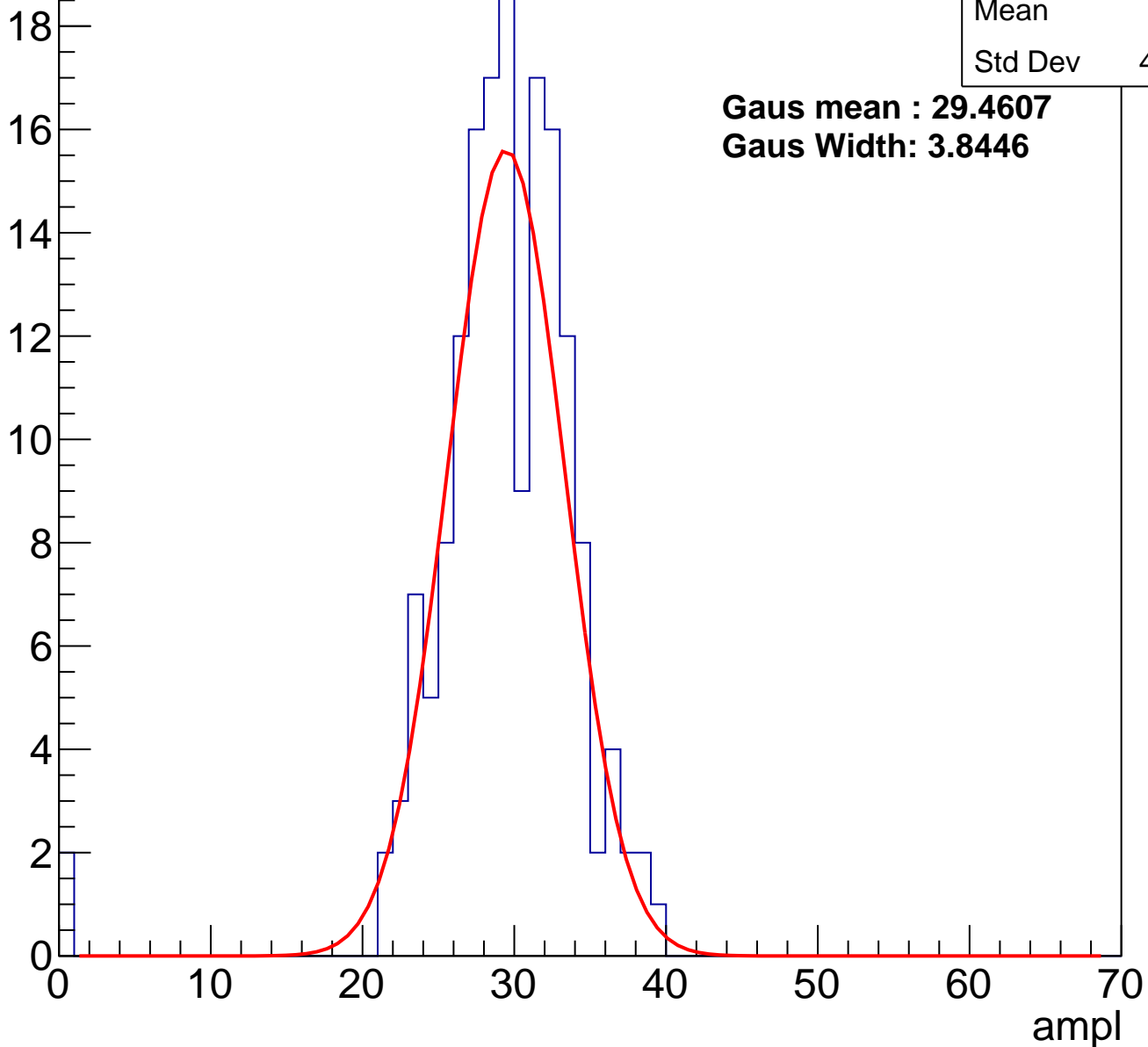


Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch45, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch45, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

136

Mean

36.71

Std Dev

3.445

Gaus mean : 37.2527

Gaus Width: 3.3378

0

10

20

30

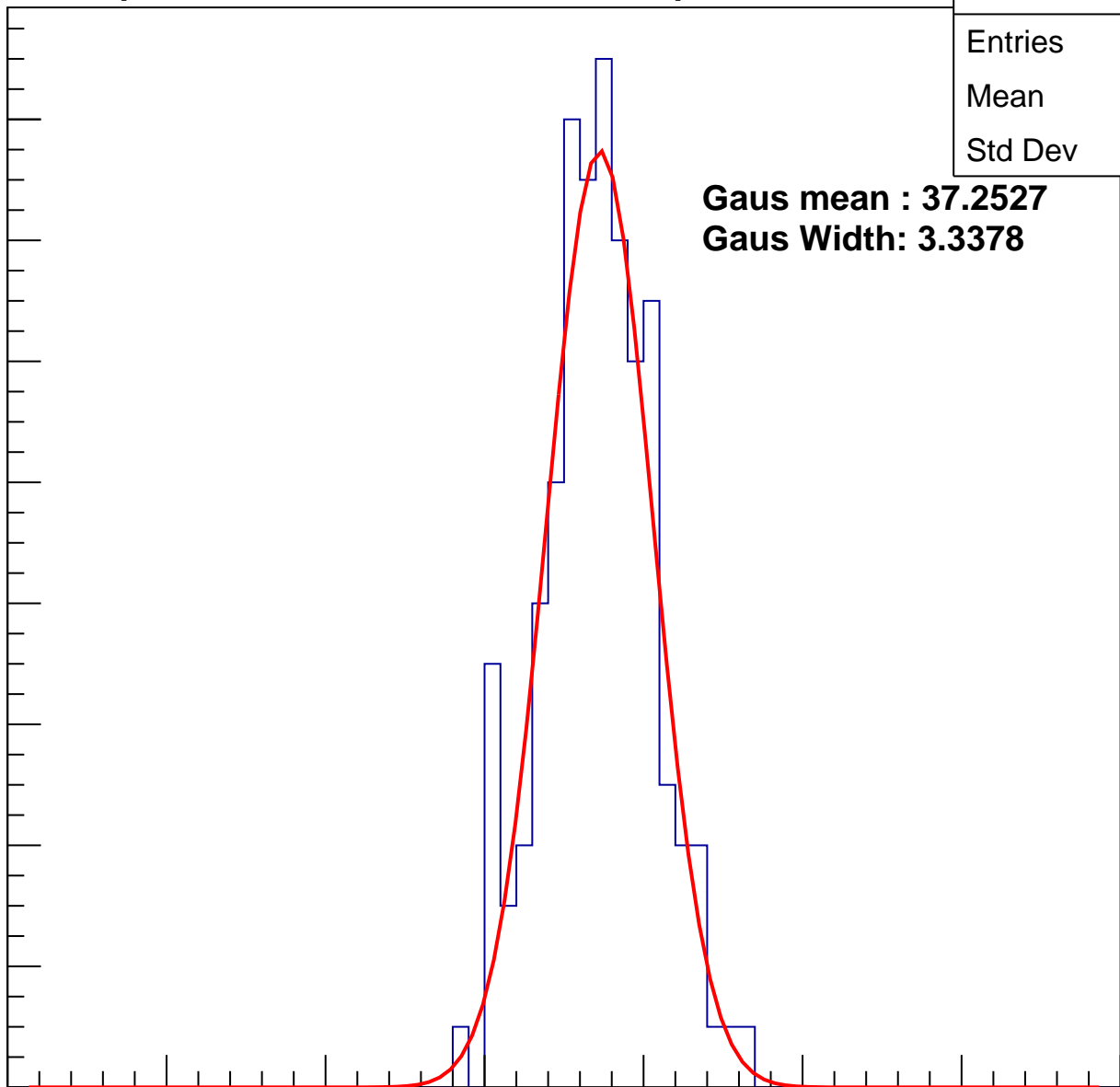
40

50

60

70

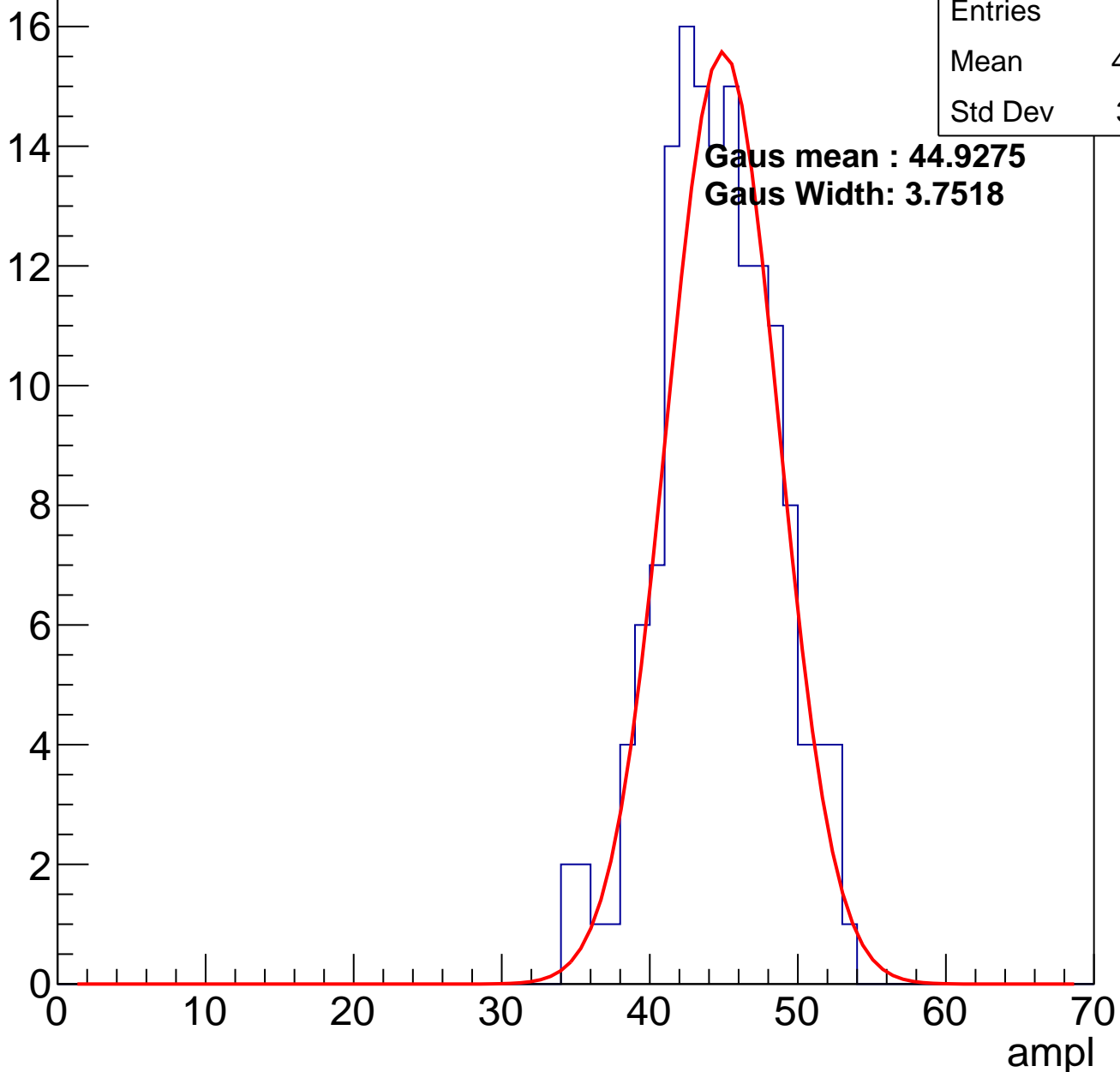
ampl



B0L101S, U18-ch45, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	49.81
Std Dev	3.477

Entry

10

8

6

4

2

0

0

10

20

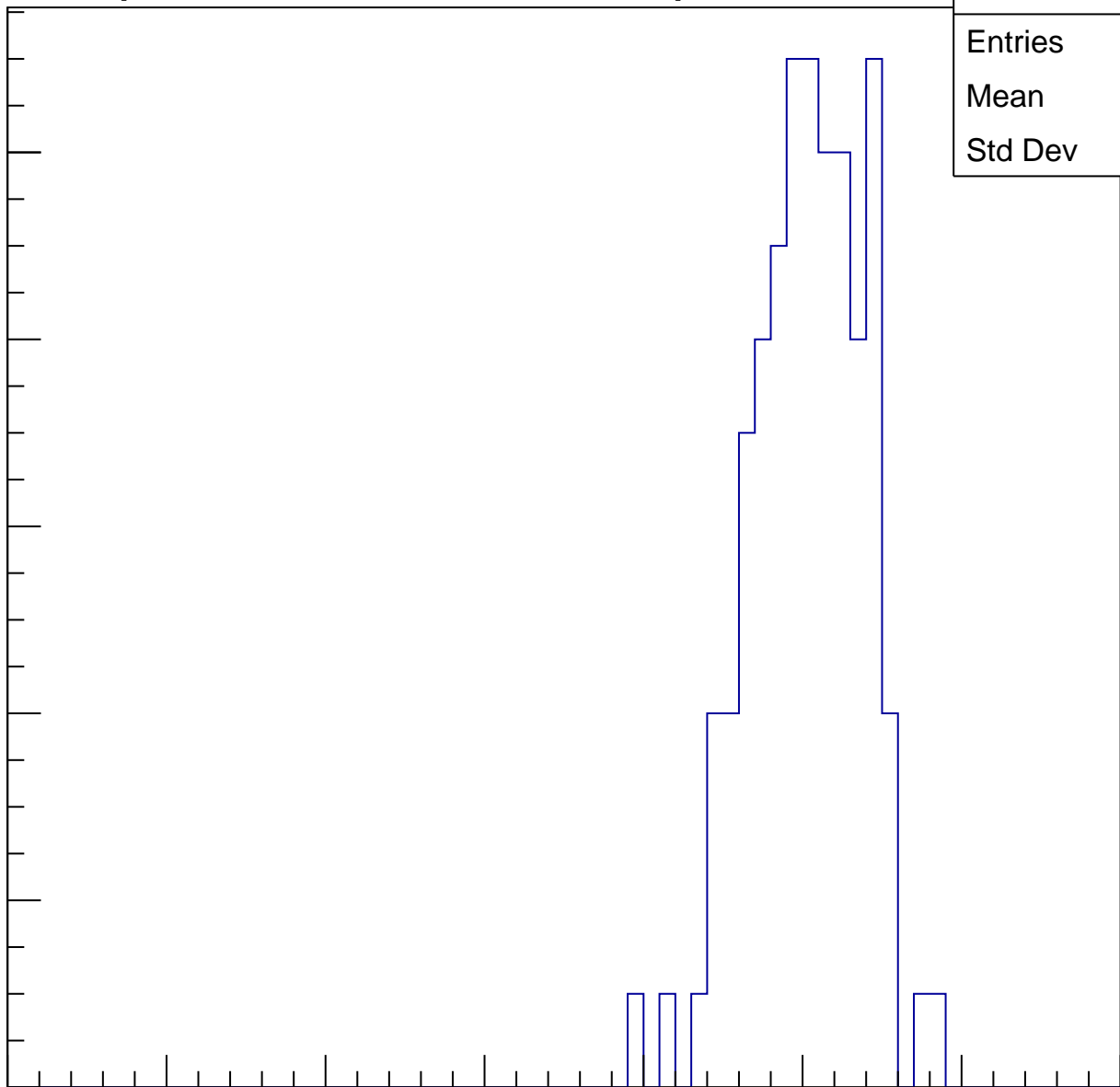
30

40

50

60

ampl



B0L101S, U18-ch45, adc4

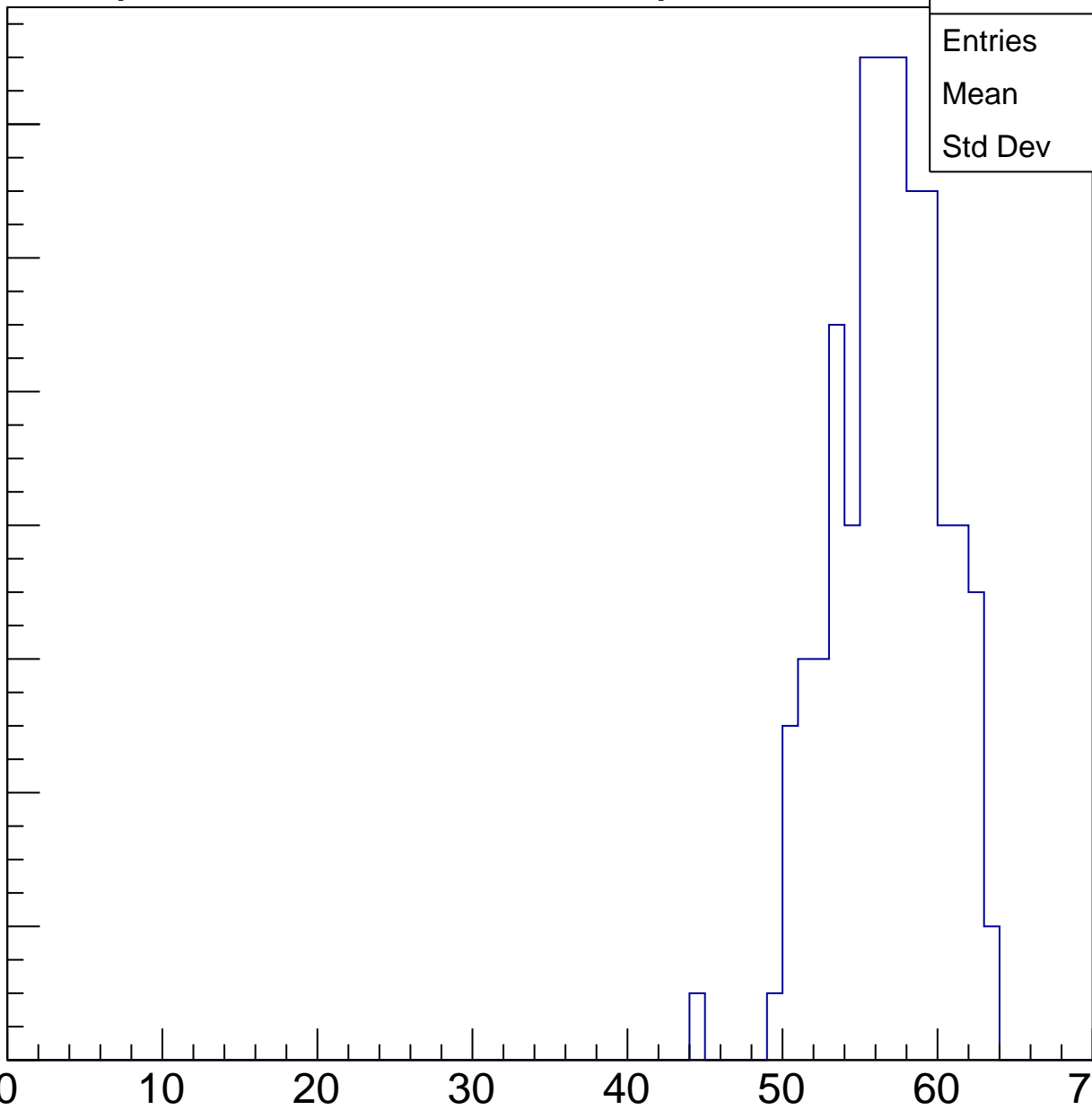
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	134
Mean	56.31
Std Dev	3.471

ampl



B0L101S, U18-ch45, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	69
Mean	59.26
Std Dev	7.554

ampl

0

10

20

30

40

50

60

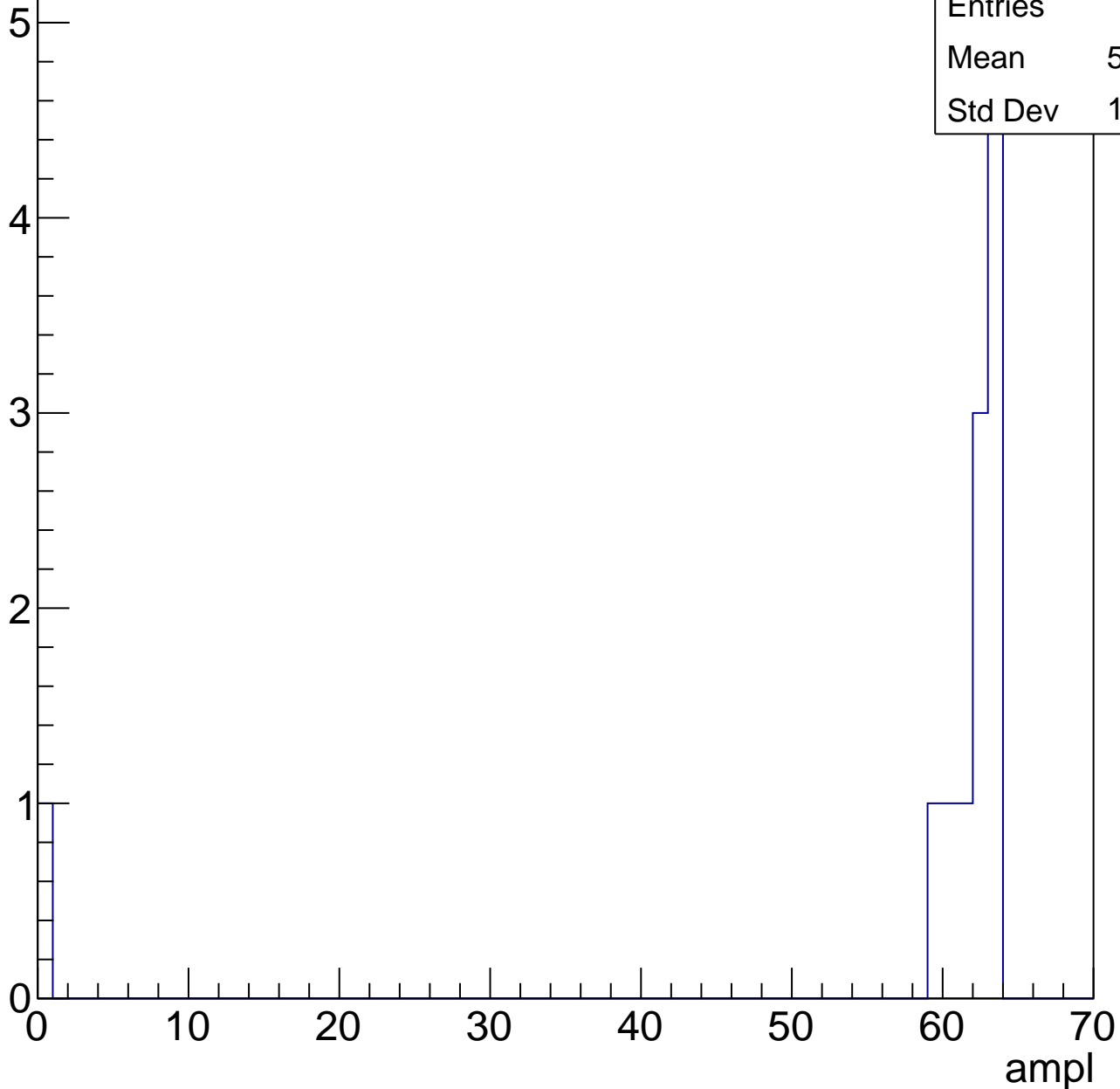
70

B0L101S, U18-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.75
Std Dev	17.16



B0L101S, U18-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch46, adc0

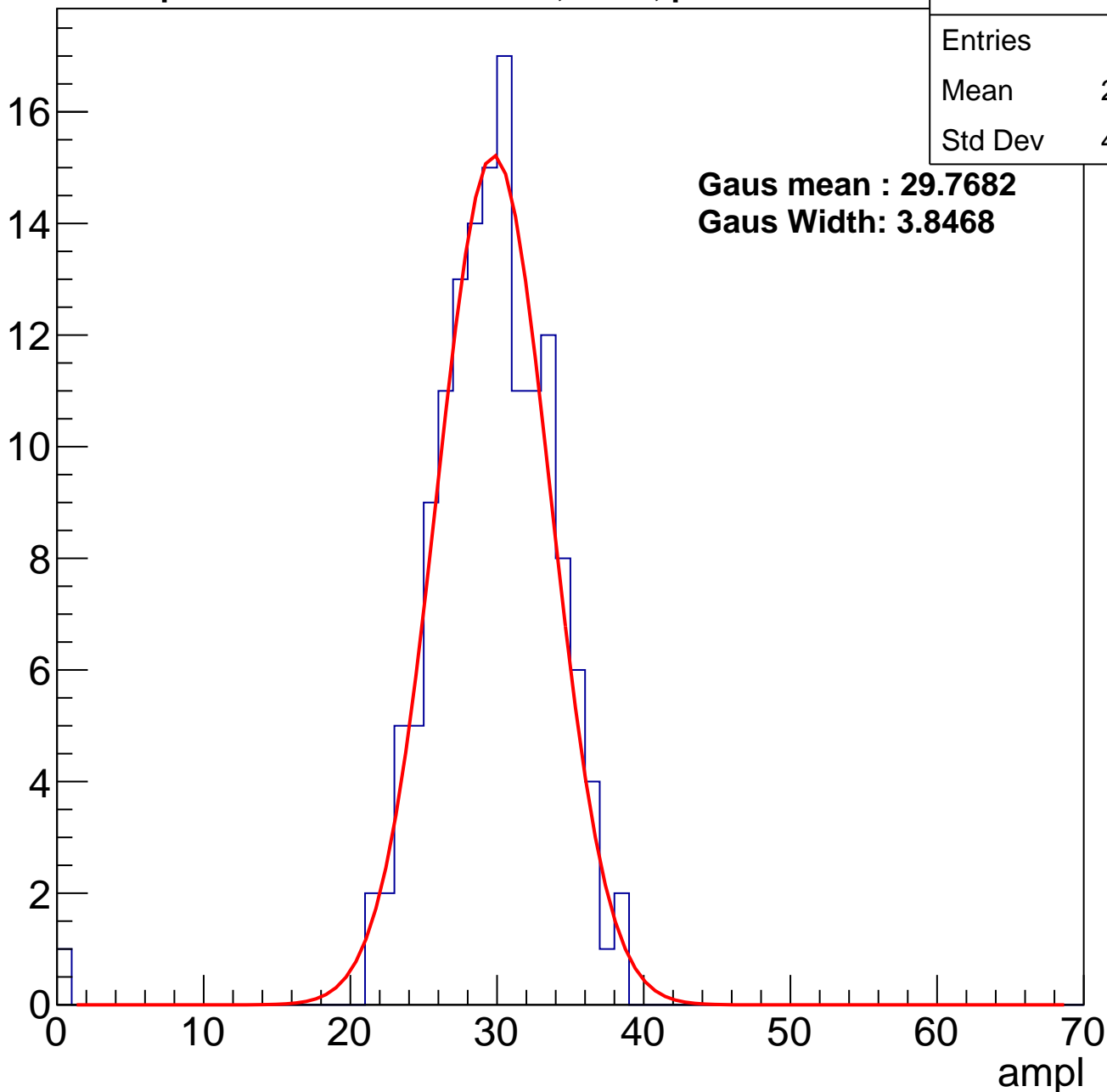
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	29.18
Std Dev	4.386

Gaus mean : 29.7682

Gaus Width: 3.8468

Entry

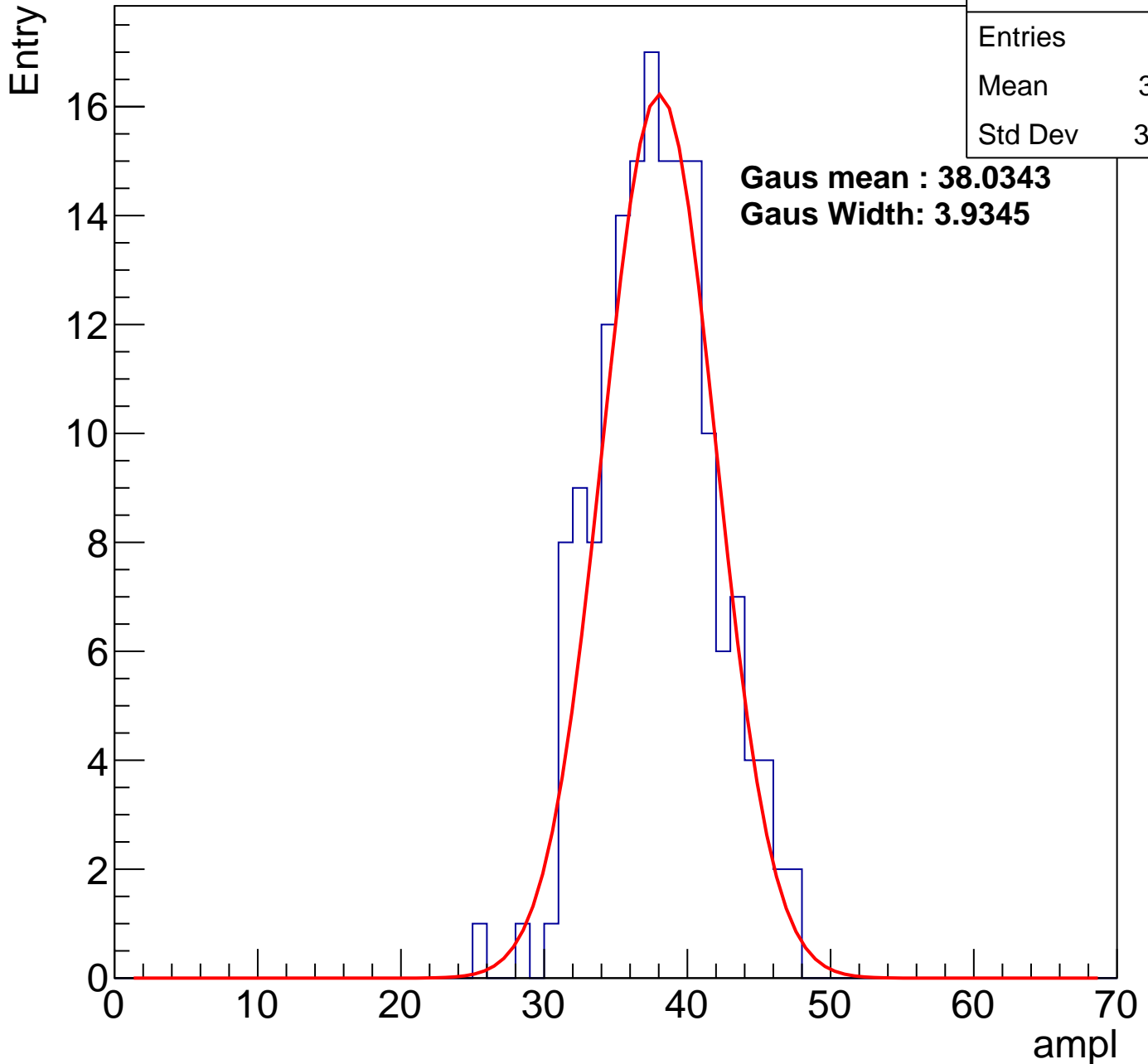


B0L101S, U18-ch46, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	37.41
Std Dev	3.997

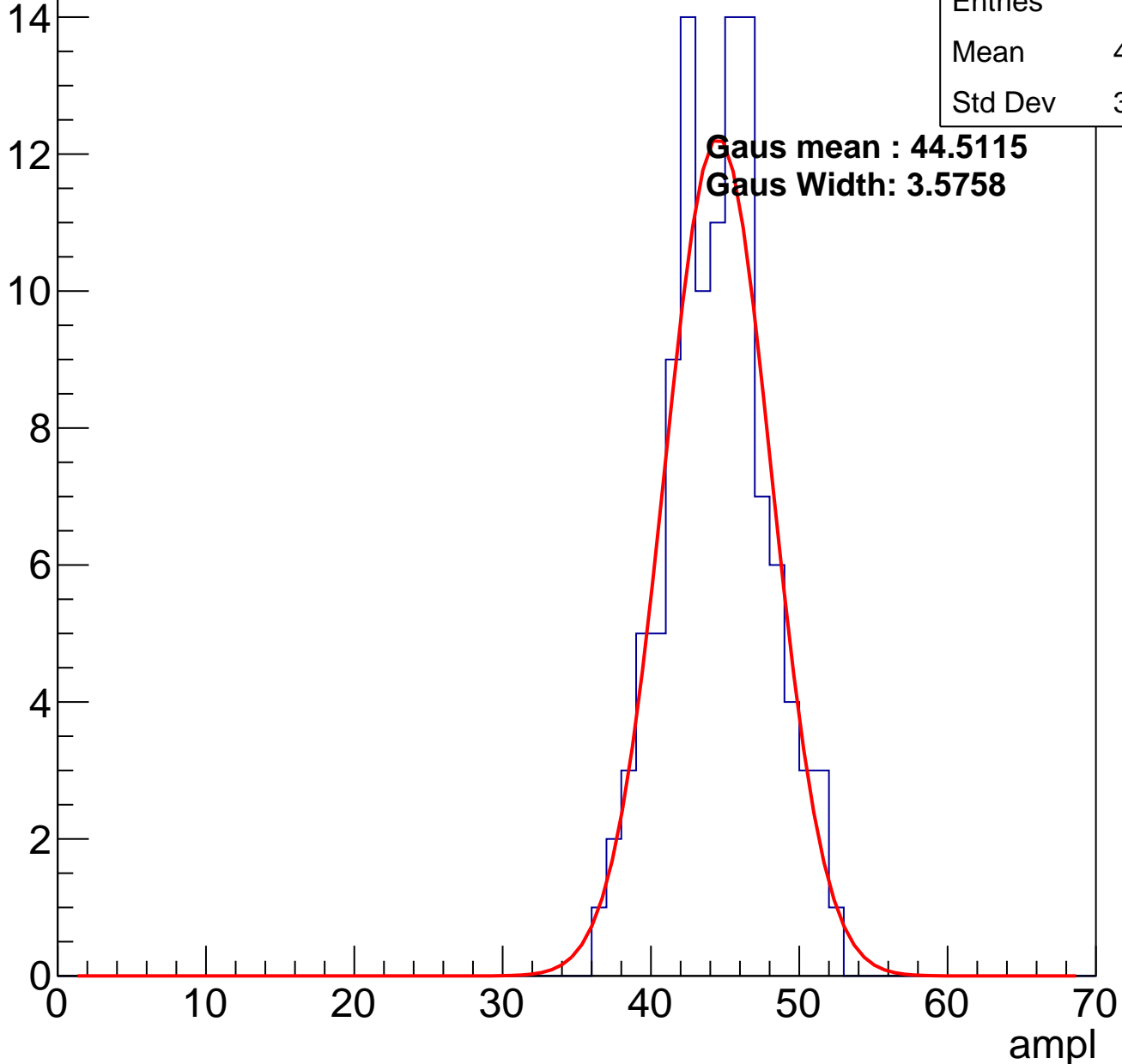
Gaus mean : 38.0343
Gaus Width: 3.9345



B0L101S, U18-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

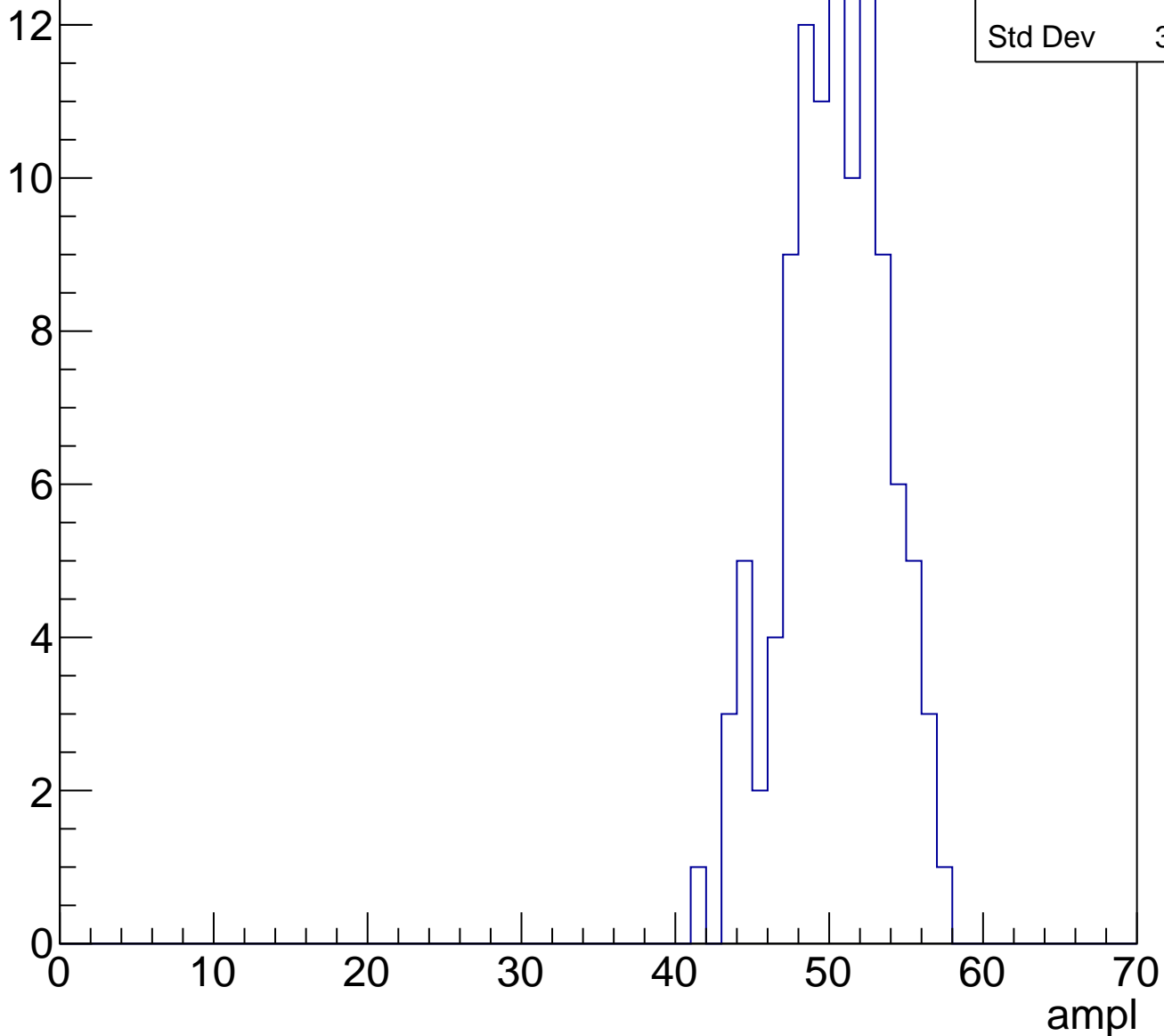


B0L101S, U18-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	49.9
Std Dev	3.323

Entry



B0L101S, U18-ch46, adc4

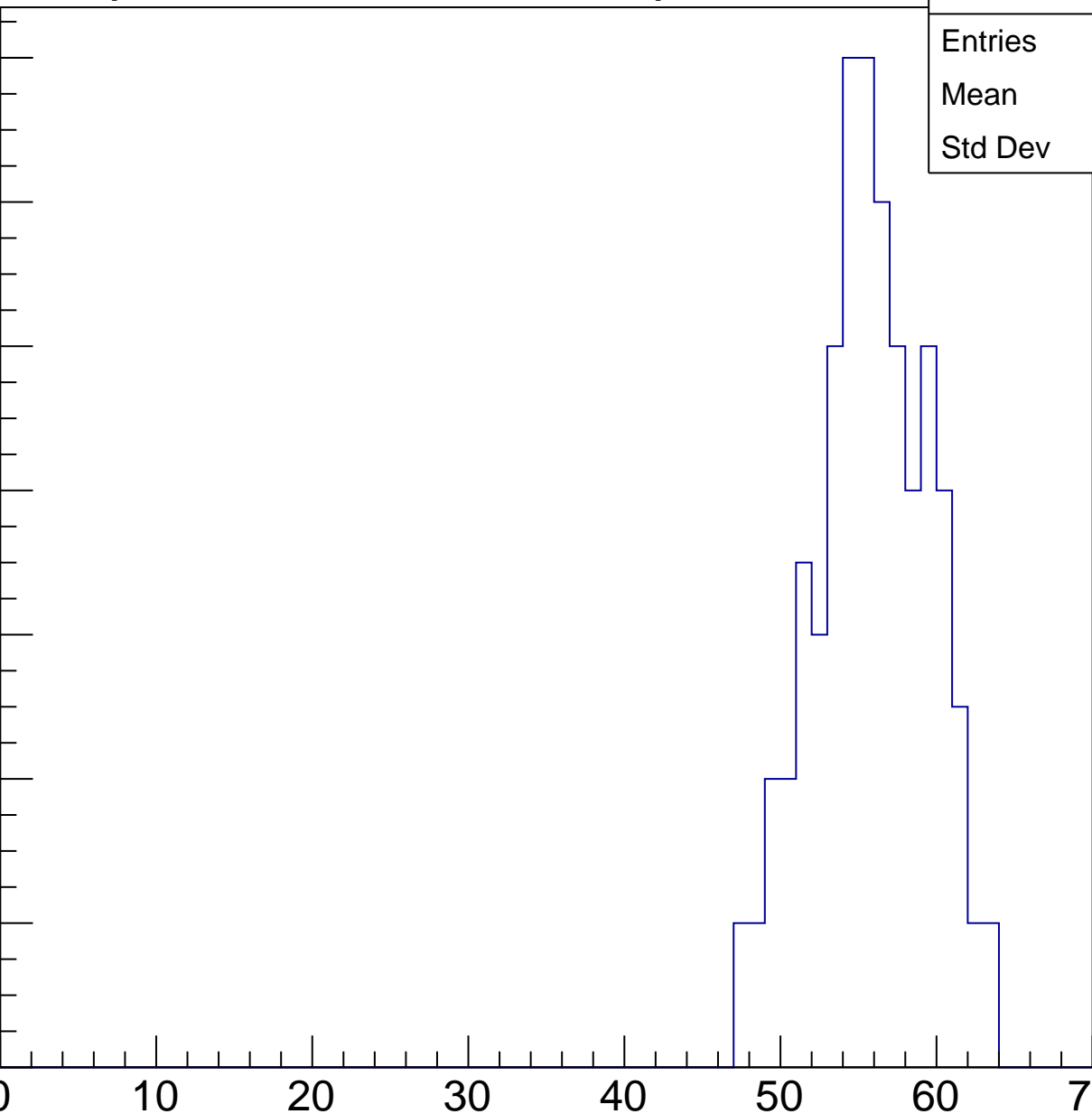
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.35
Std Dev	3.612

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch46, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

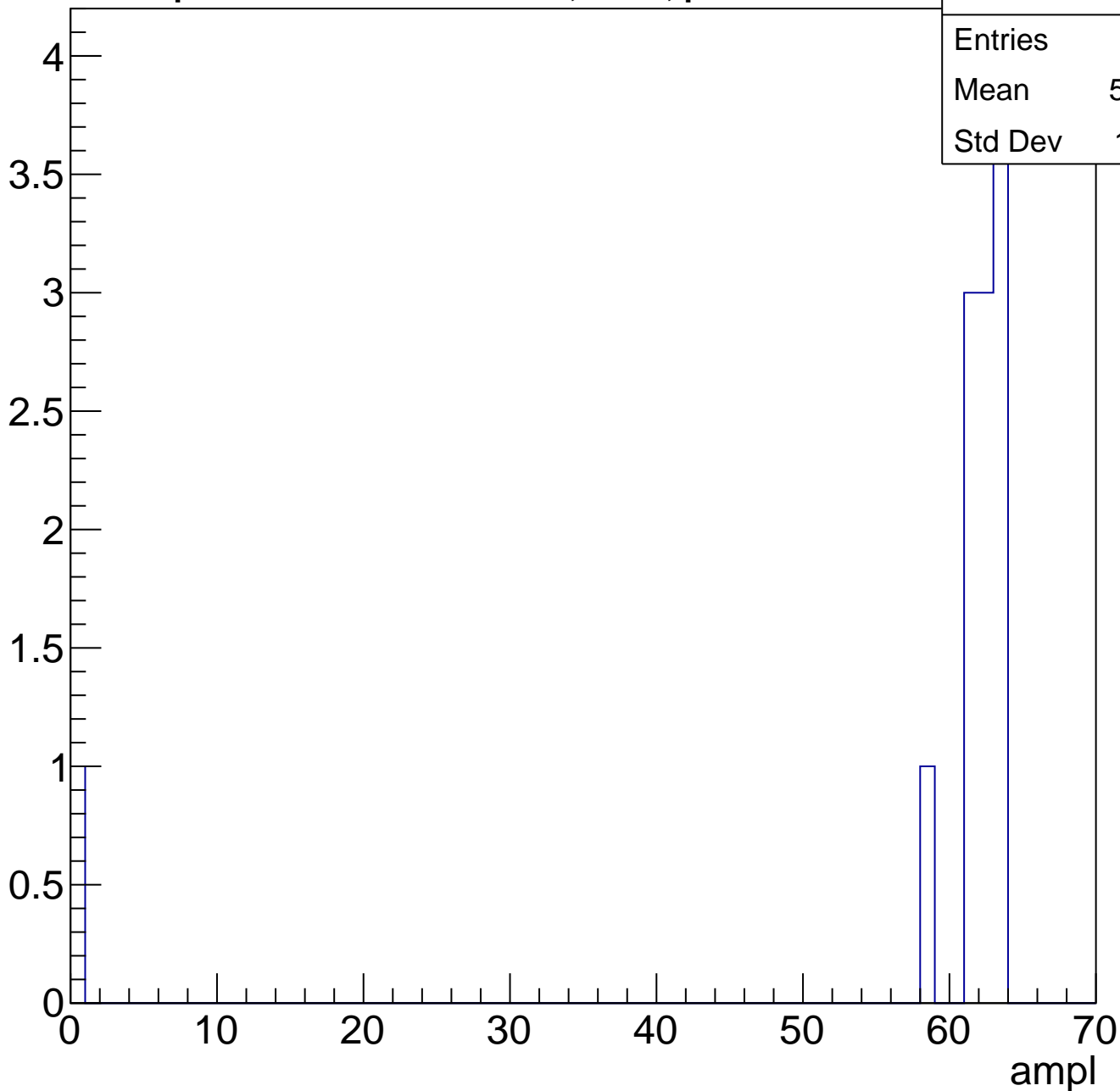
ampl

Entries	92
Mean	59.71
Std Dev	2.721

B0L101S, U18-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

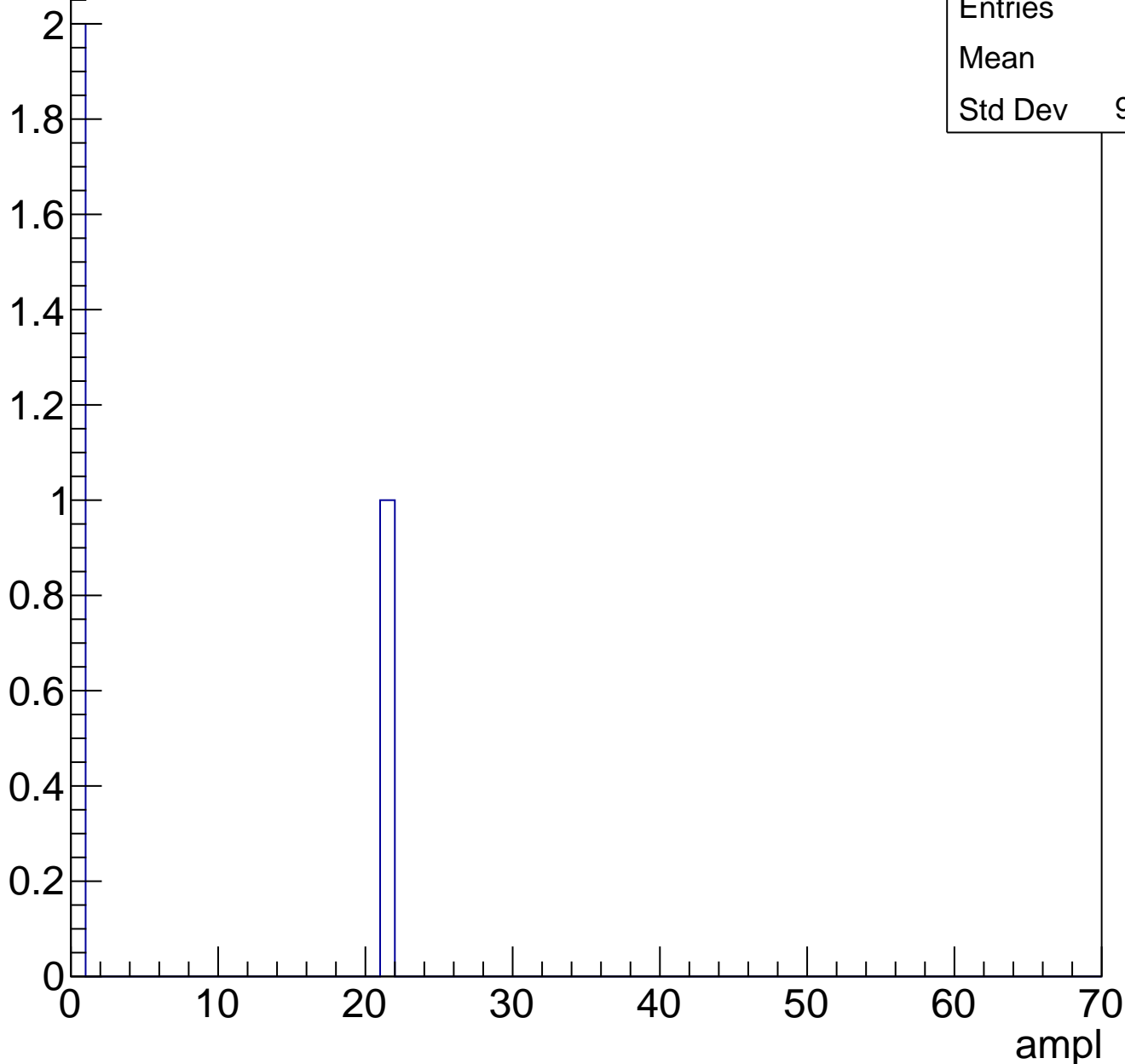
Entry



B0L101S, U18-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L101S, U18-ch47, adc0

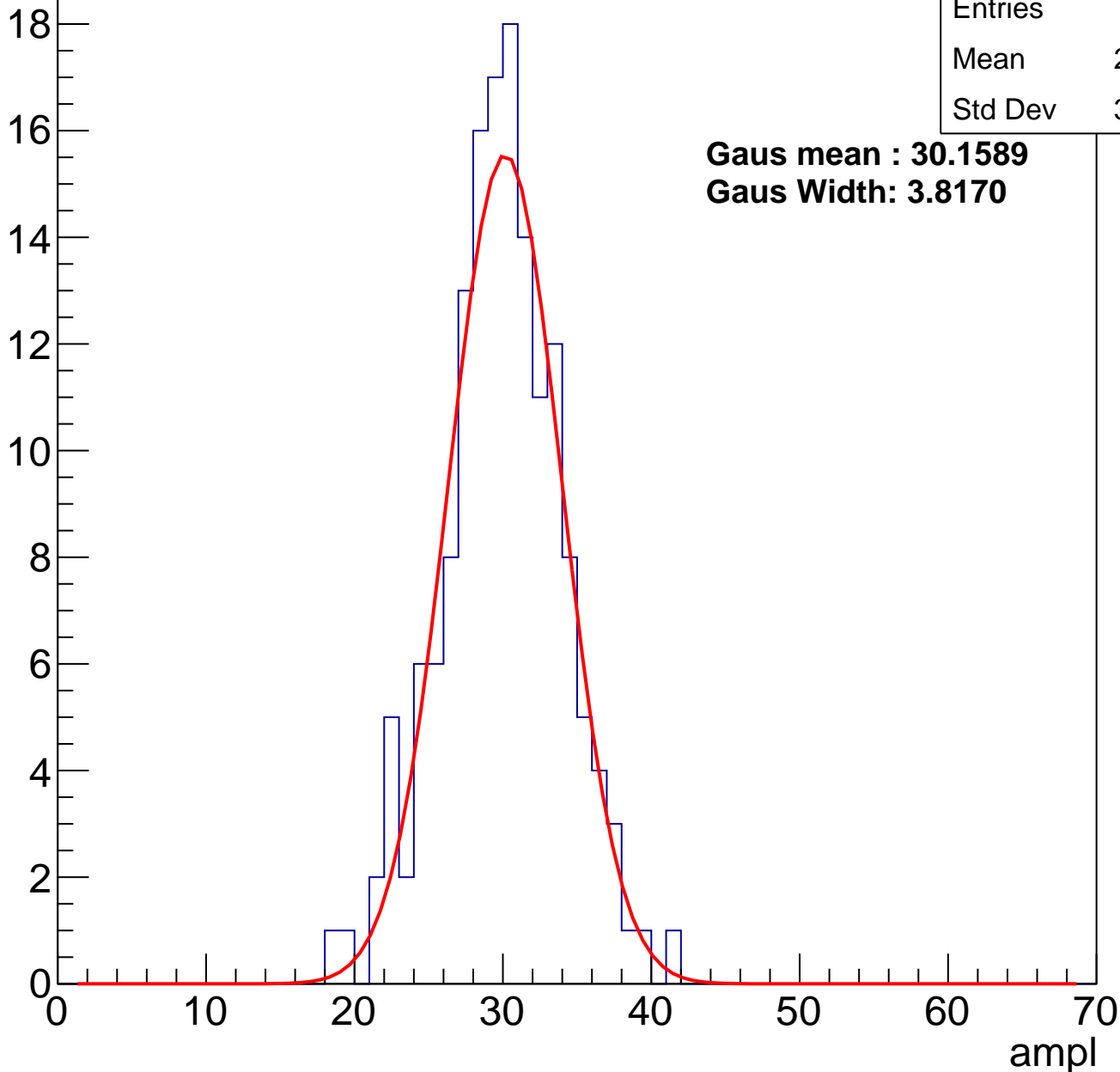
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	29.49
Std Dev	3.994

Gaus mean : 30.1589

Gaus Width: 3.8170

Entry



B0L101S, U18-ch47, adc1

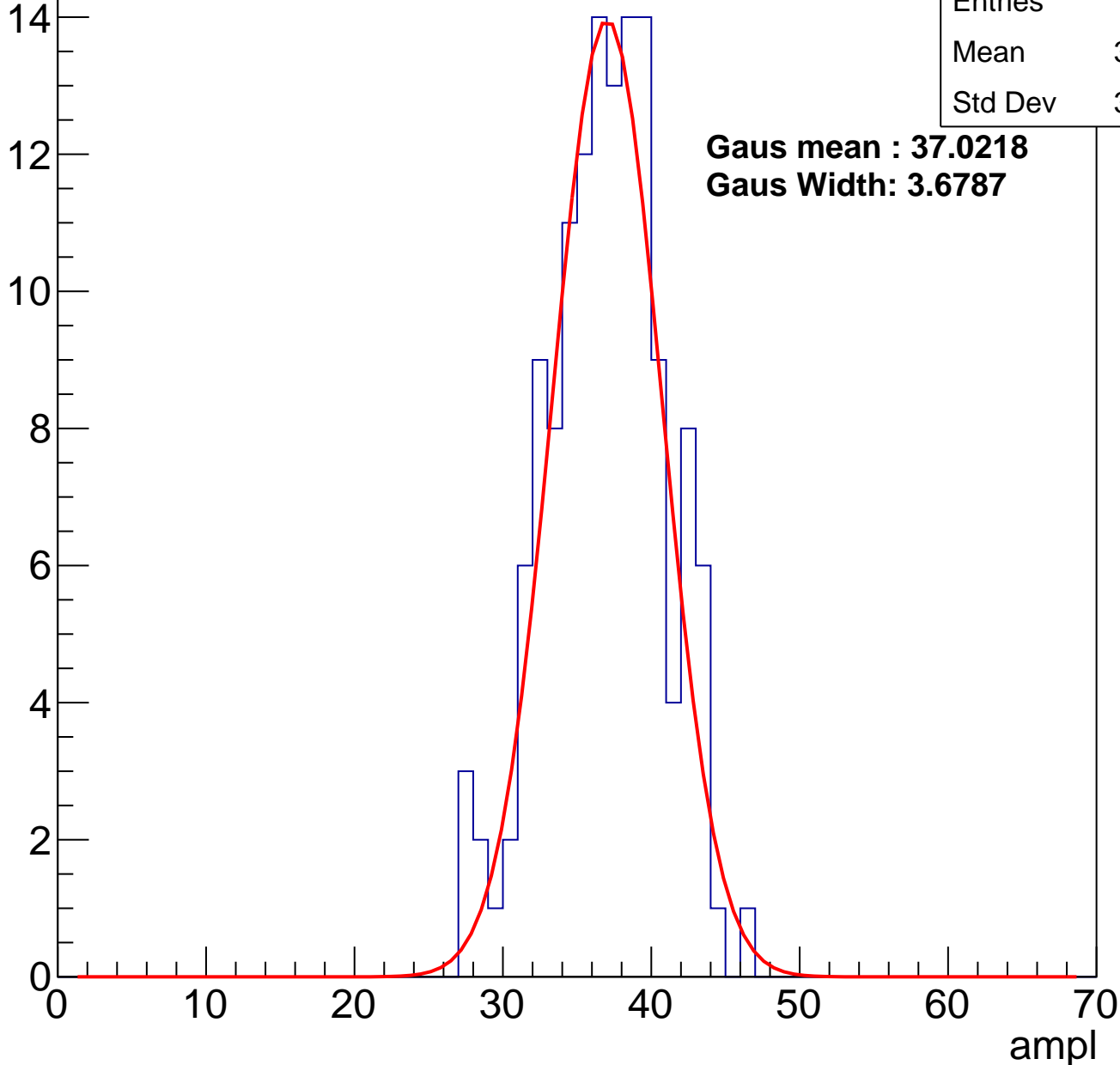
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	36.44
Std Dev	3.875

Gaus mean : 37.0218

Gaus Width: 3.6787

Entry



B0L101S, U18-ch47, adc2

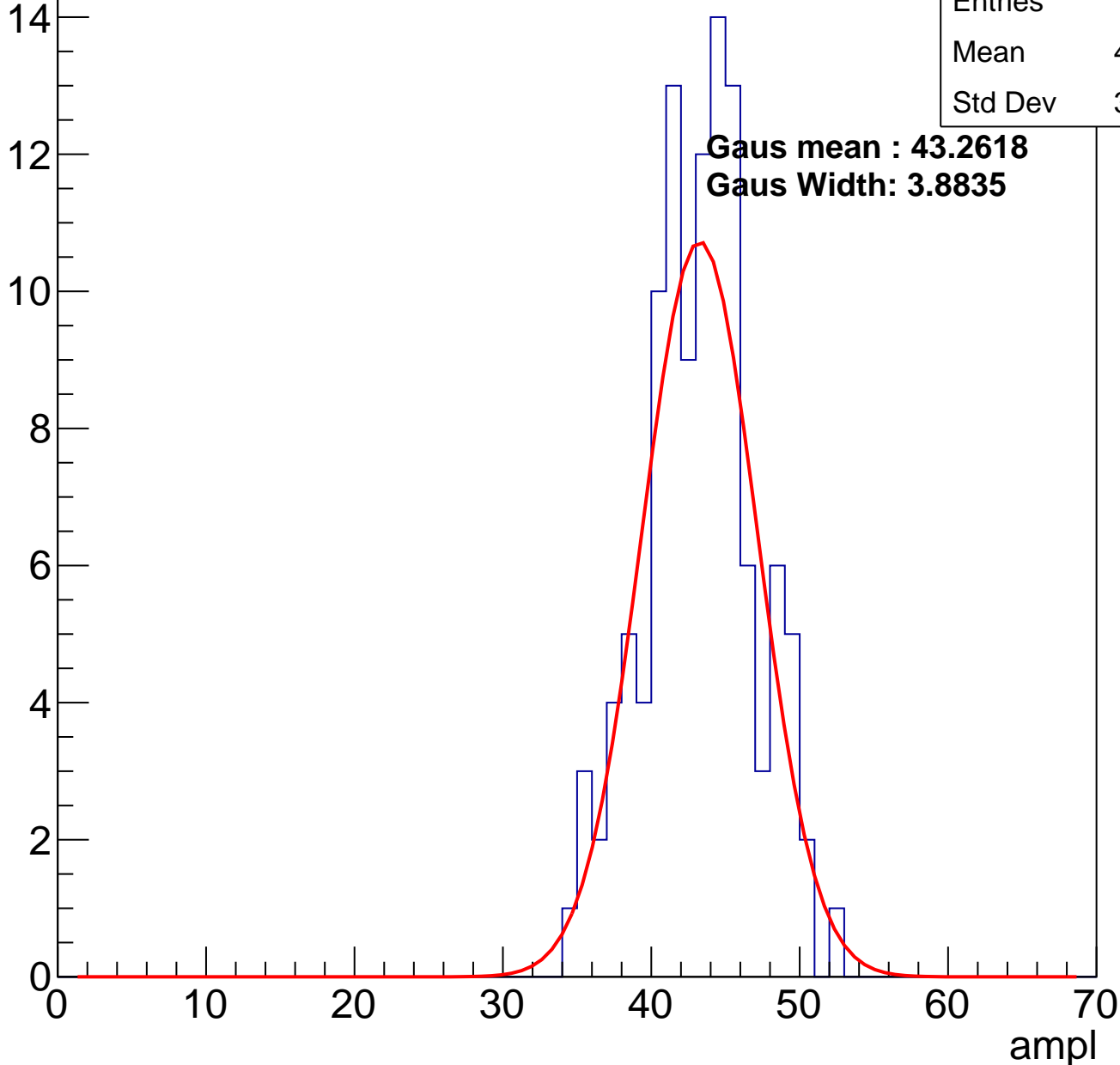
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	42.79
Std Dev	3.676

Gaus mean : 43.2618

Gaus Width: 3.8835

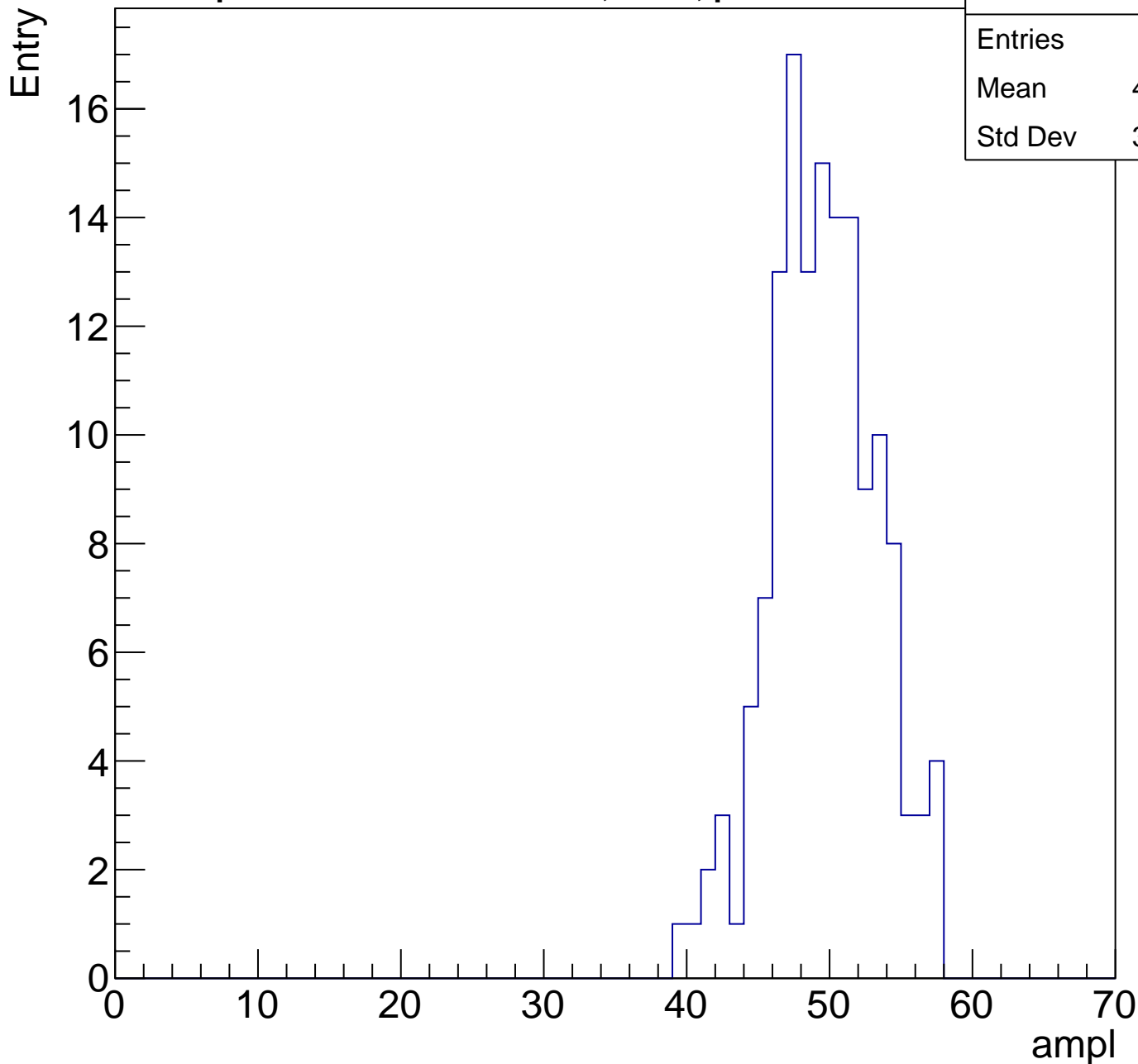
Entry



B0L101S, U18-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

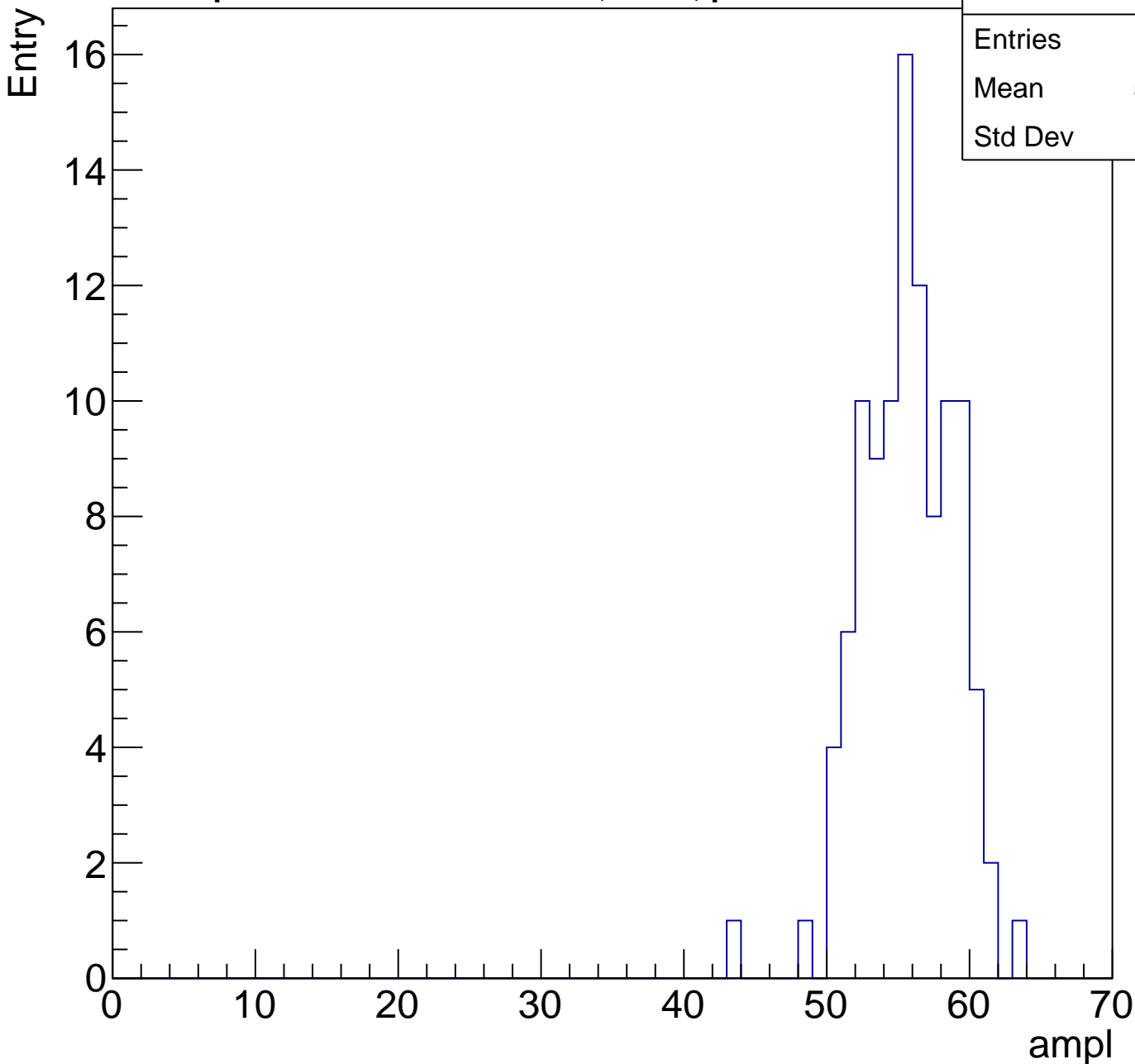
Entries	143
Mean	49.13
Std Dev	3.689



B0L101S, U18-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	55.21
Std Dev	3.2



B0L101S, U18-ch47, adc5

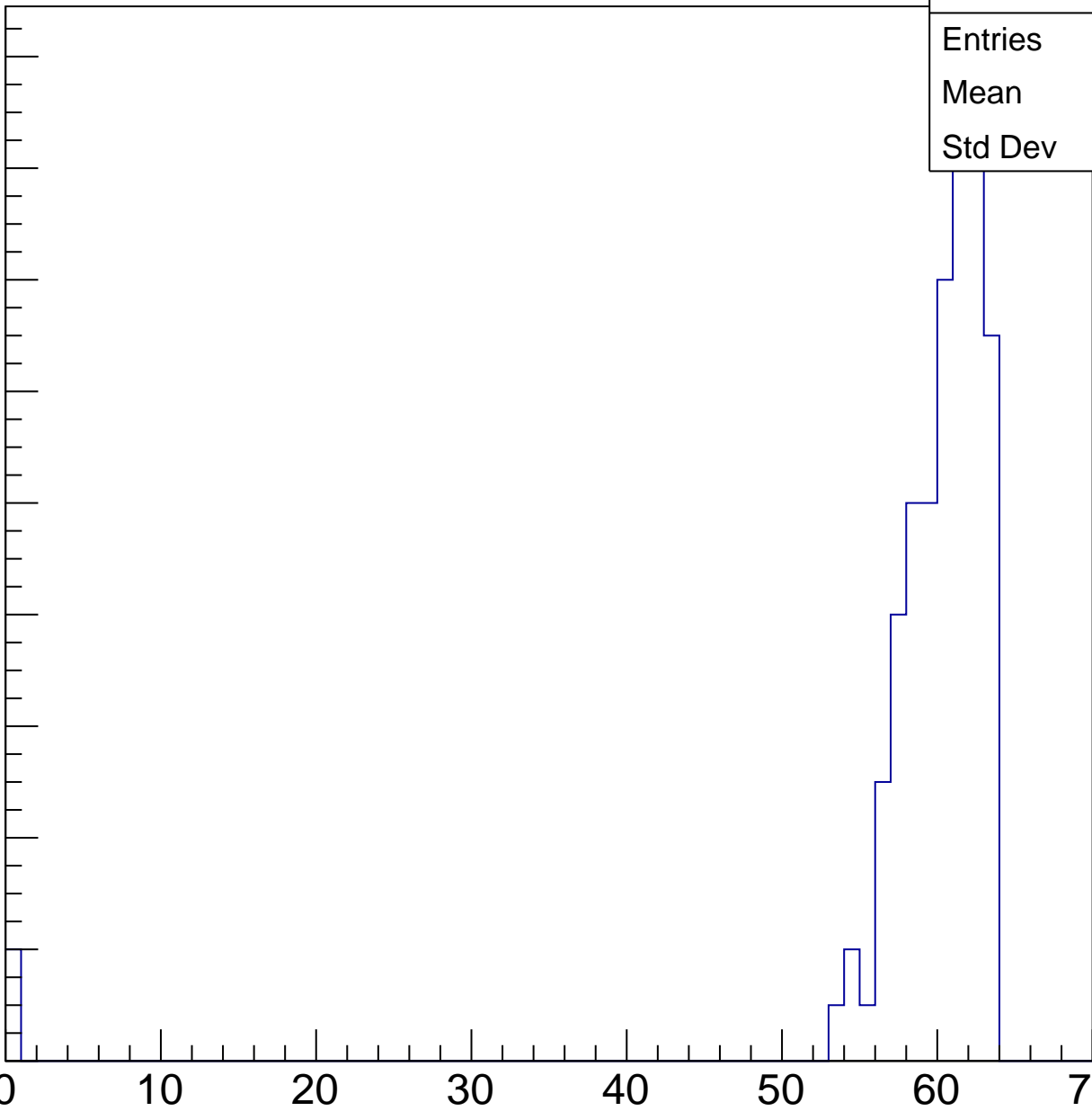
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.75
Std Dev	8.673

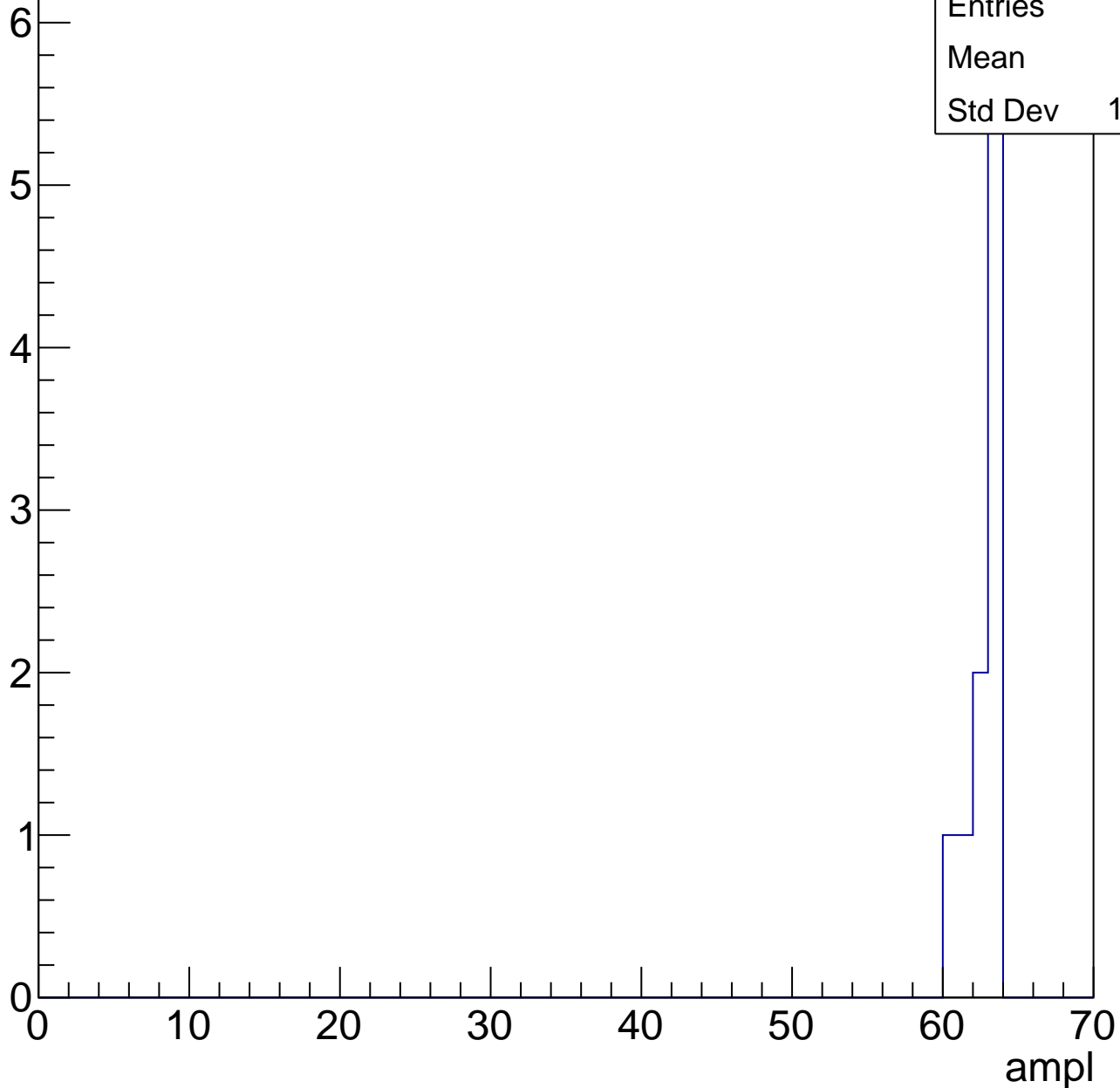
ampl



B0L101S, U18-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch48, adc0

calib_packv5_042523_0143.root, FC#1, port C1

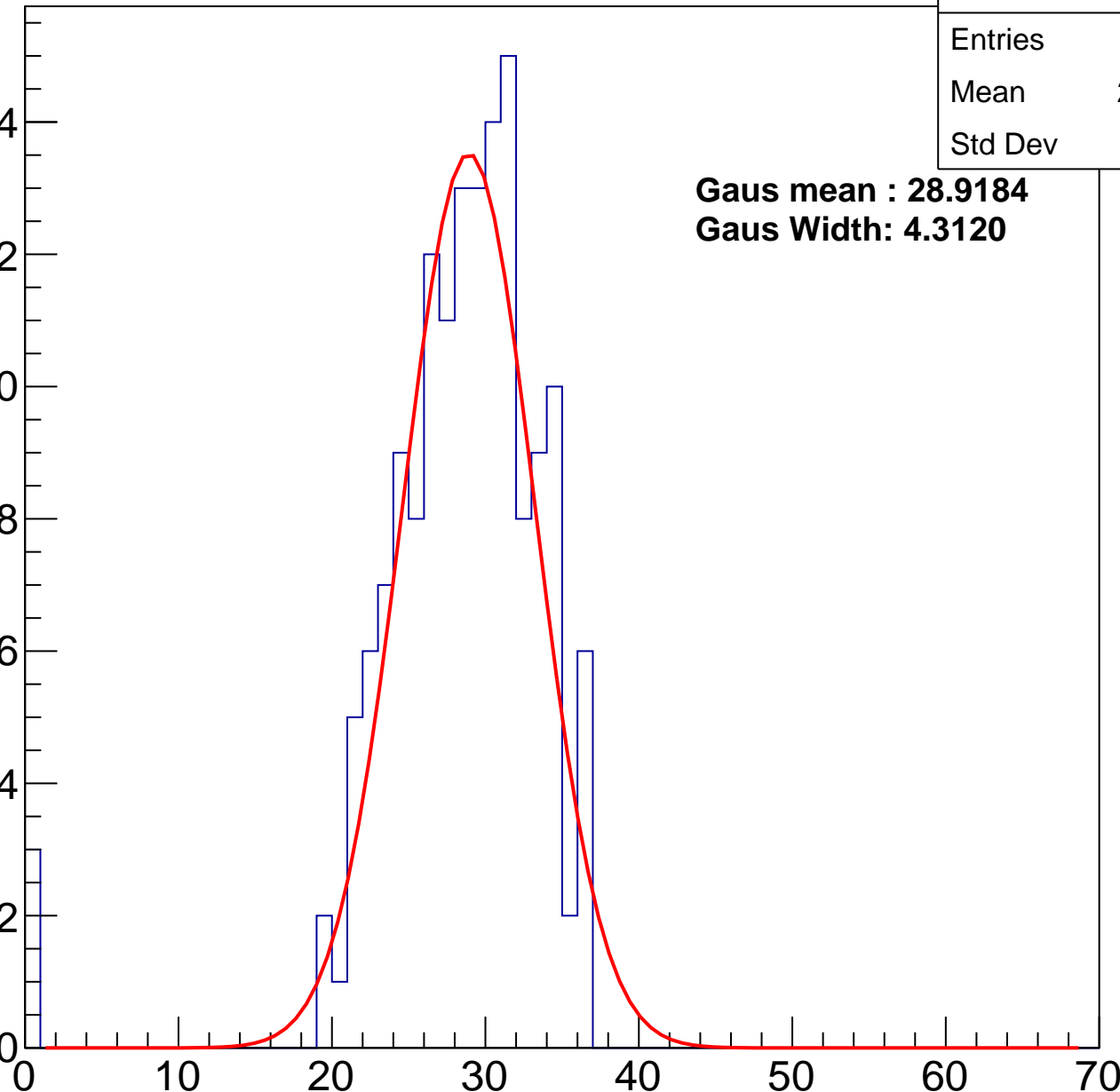
Entry

14
12
10
8
6
4
2
0

Entries	154
Mean	27.83
Std Dev	5.631

Gaus mean : 28.9184

Gaus Width: 4.3120



ampl

B0L101S, U18-ch48, adc1

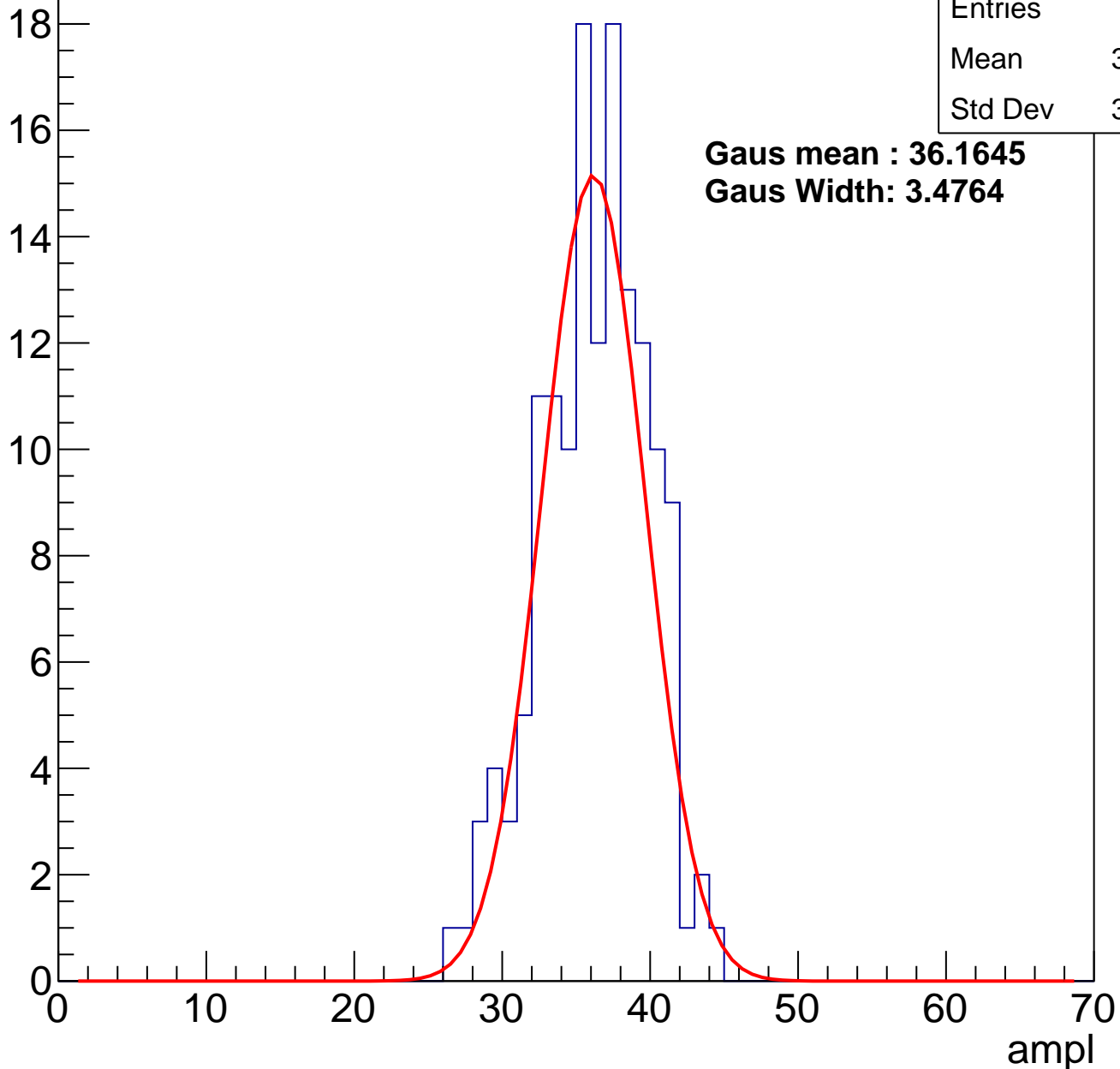
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	35.75
Std Dev	3.608

Gaus mean : 36.1645

Gaus Width: 3.4764

Entry



B0L101S, U18-ch48, adc2

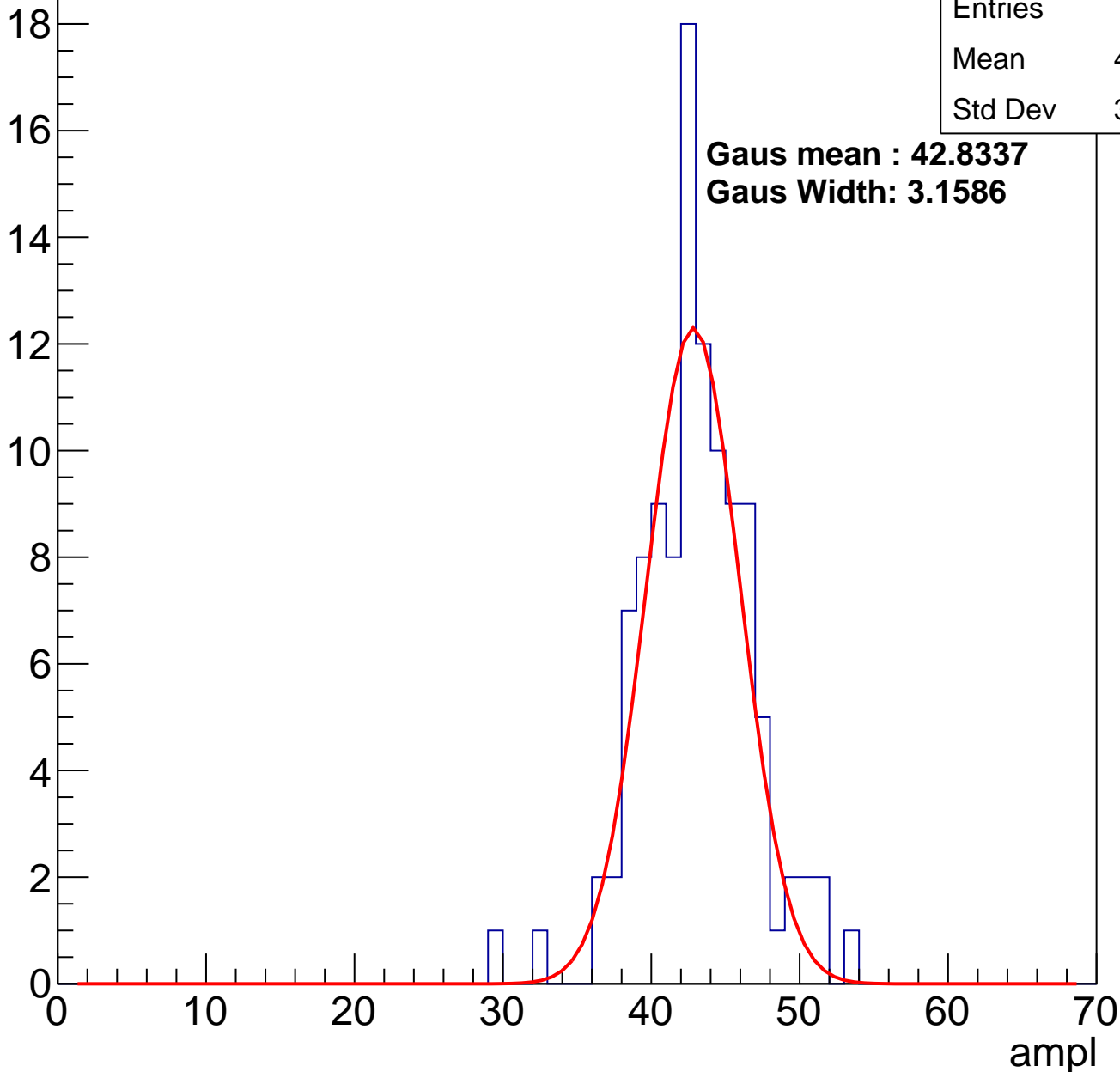
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	109
Mean	42.57
Std Dev	3.747

Gaus mean : 42.8337

Gaus Width: 3.1586

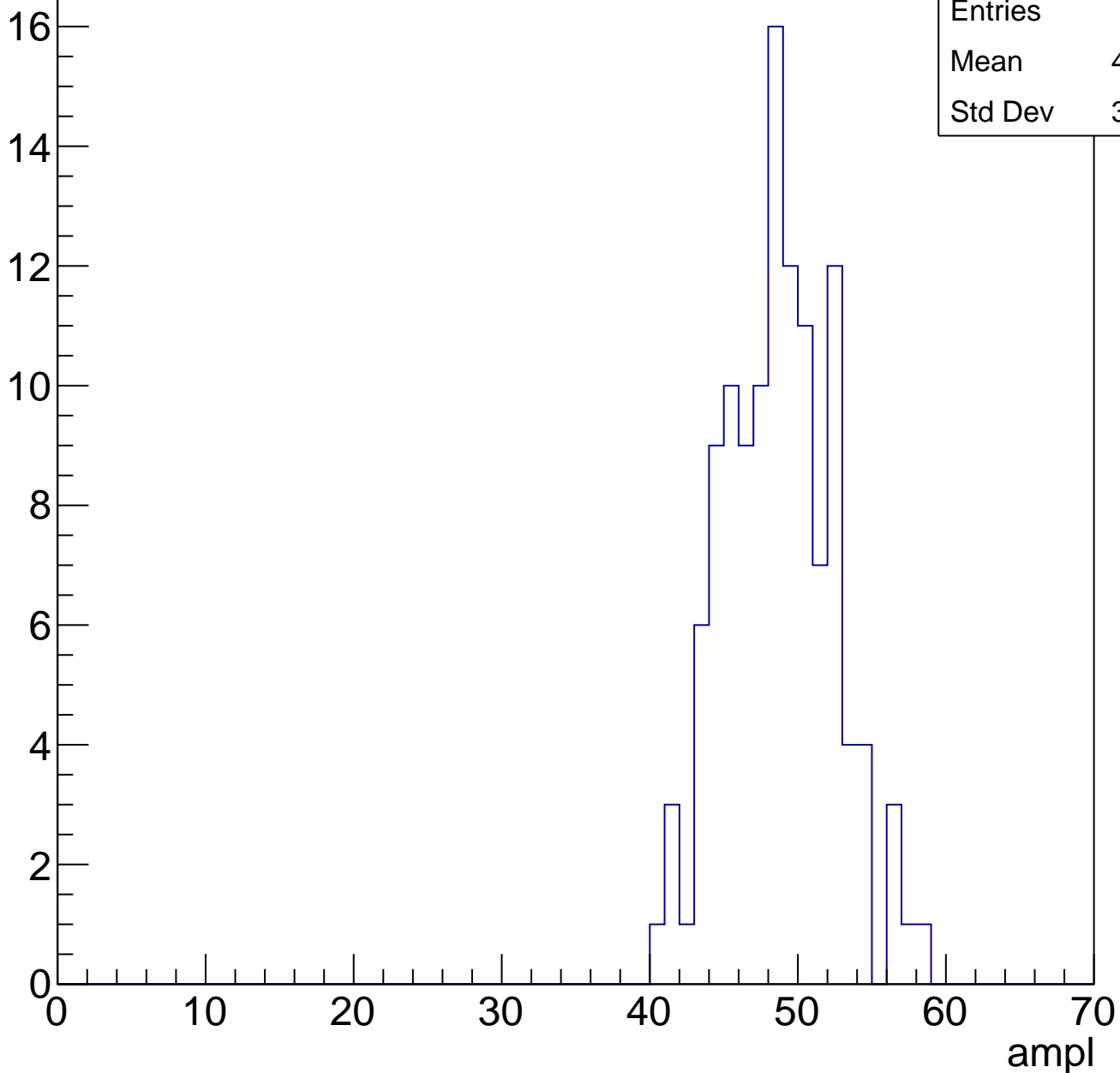


B0L101S, U18-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	48.26
Std Dev	3.648

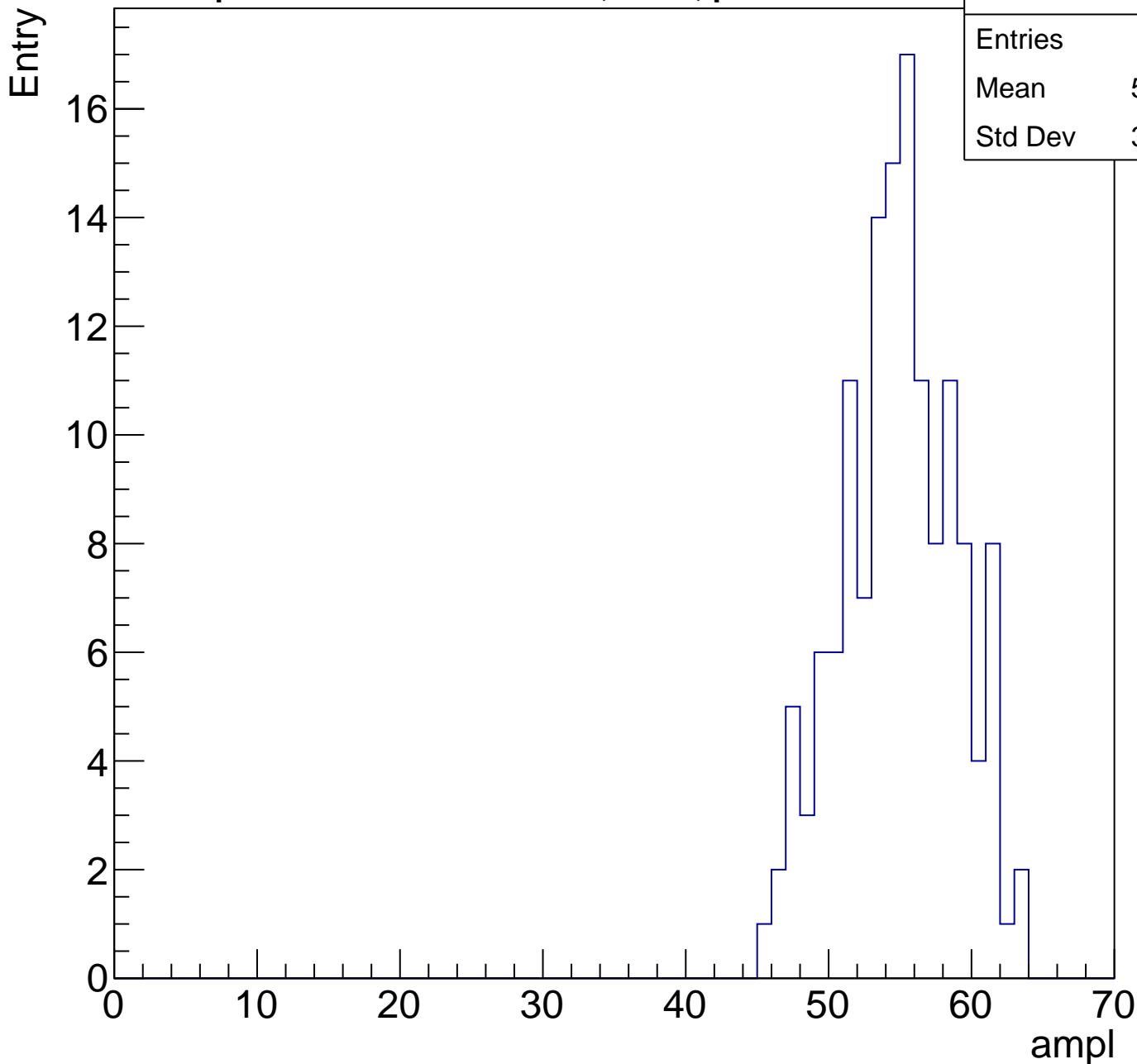
Entry



B0L101S, U18-ch48, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	54.43
Std Dev	3.959



B0L101S, U18-ch48, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	88
Mean	58.59
Std Dev	6.858

ampl

0

10

20

30

40

50

60

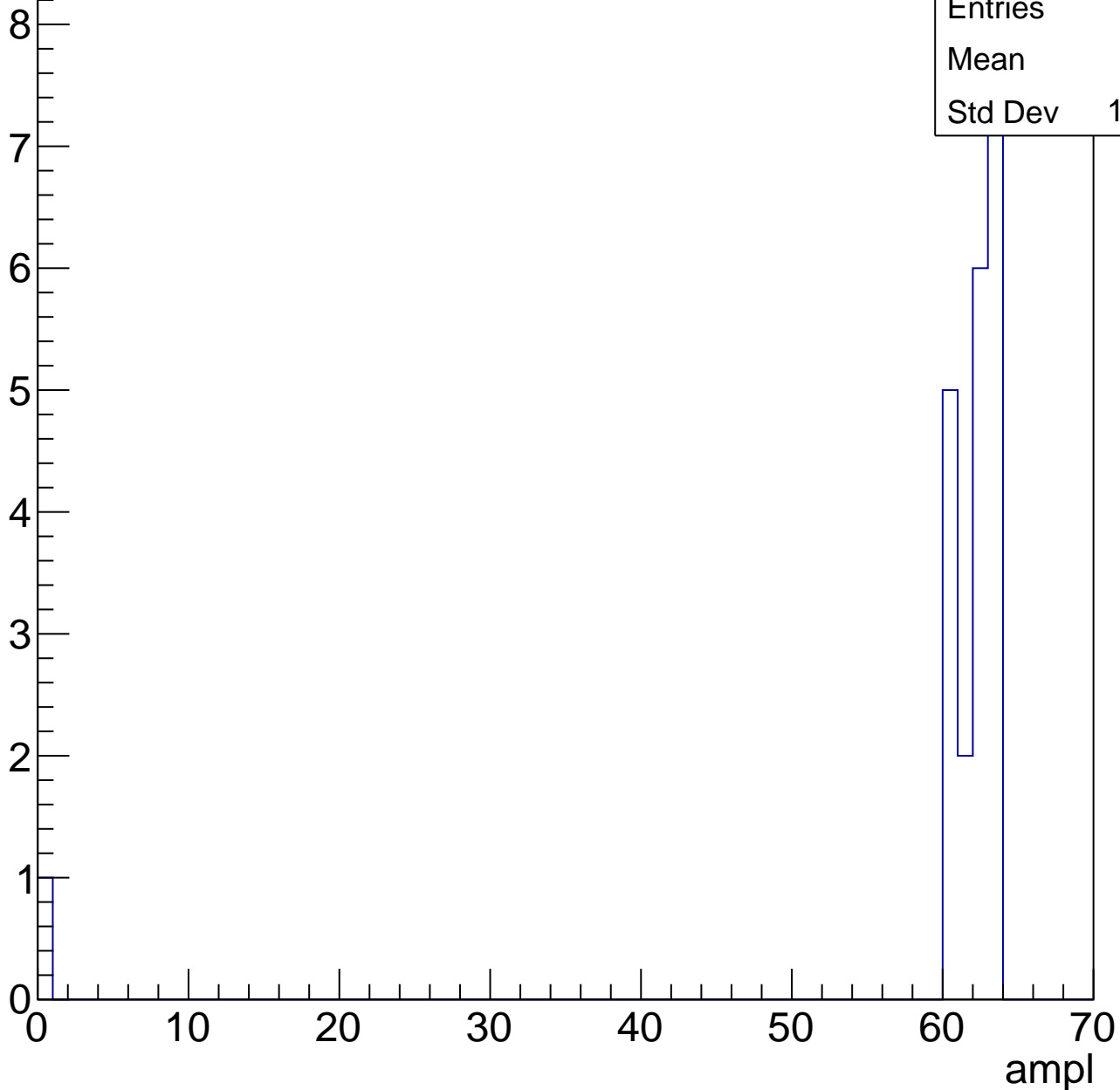
70

B0L101S, U18-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

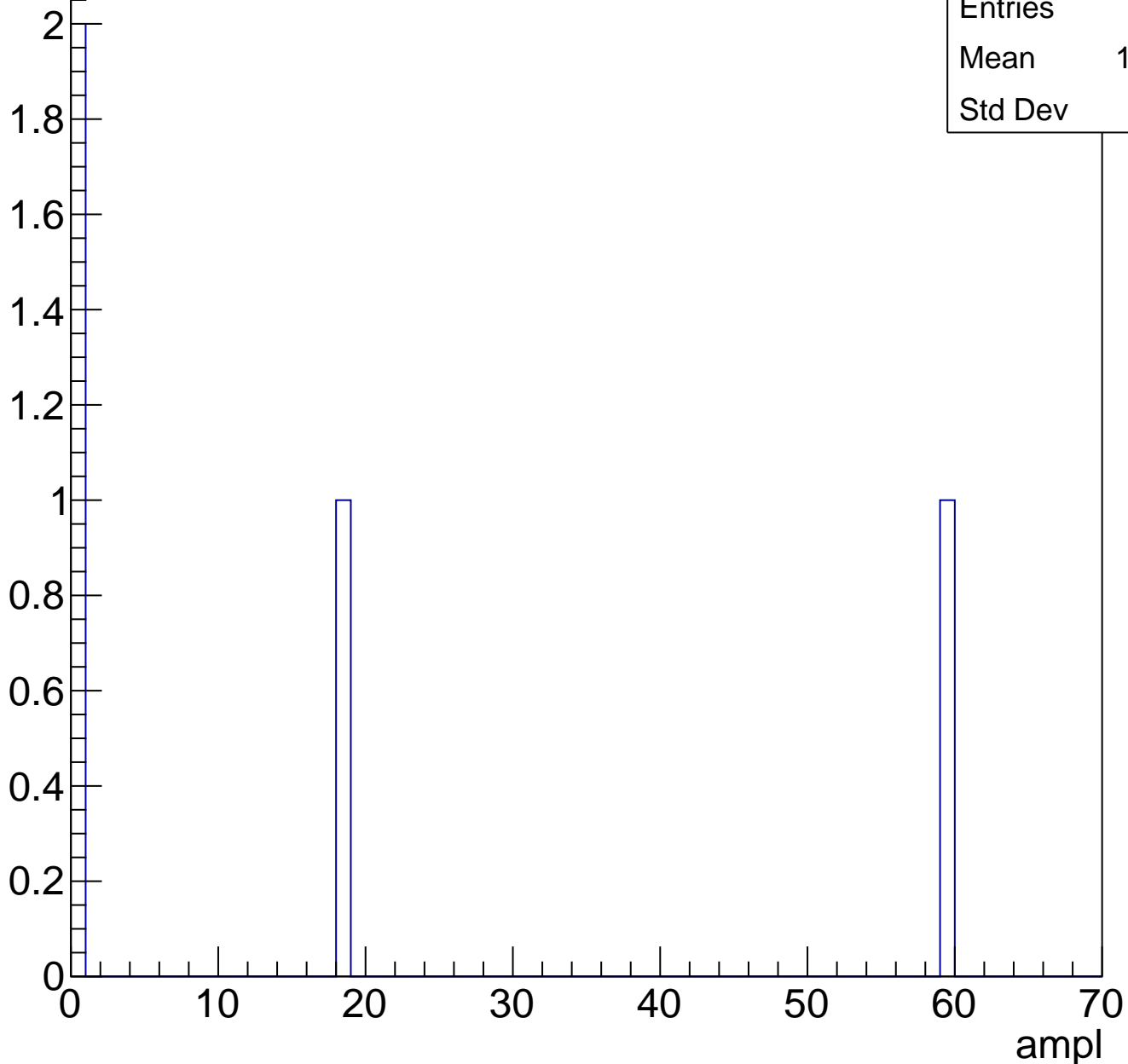
Entries	22
Mean	59
Std Dev	12.93



B0L101S, U18-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	19.25
Std Dev	24.1

B0L101S, U18-ch49, adc0

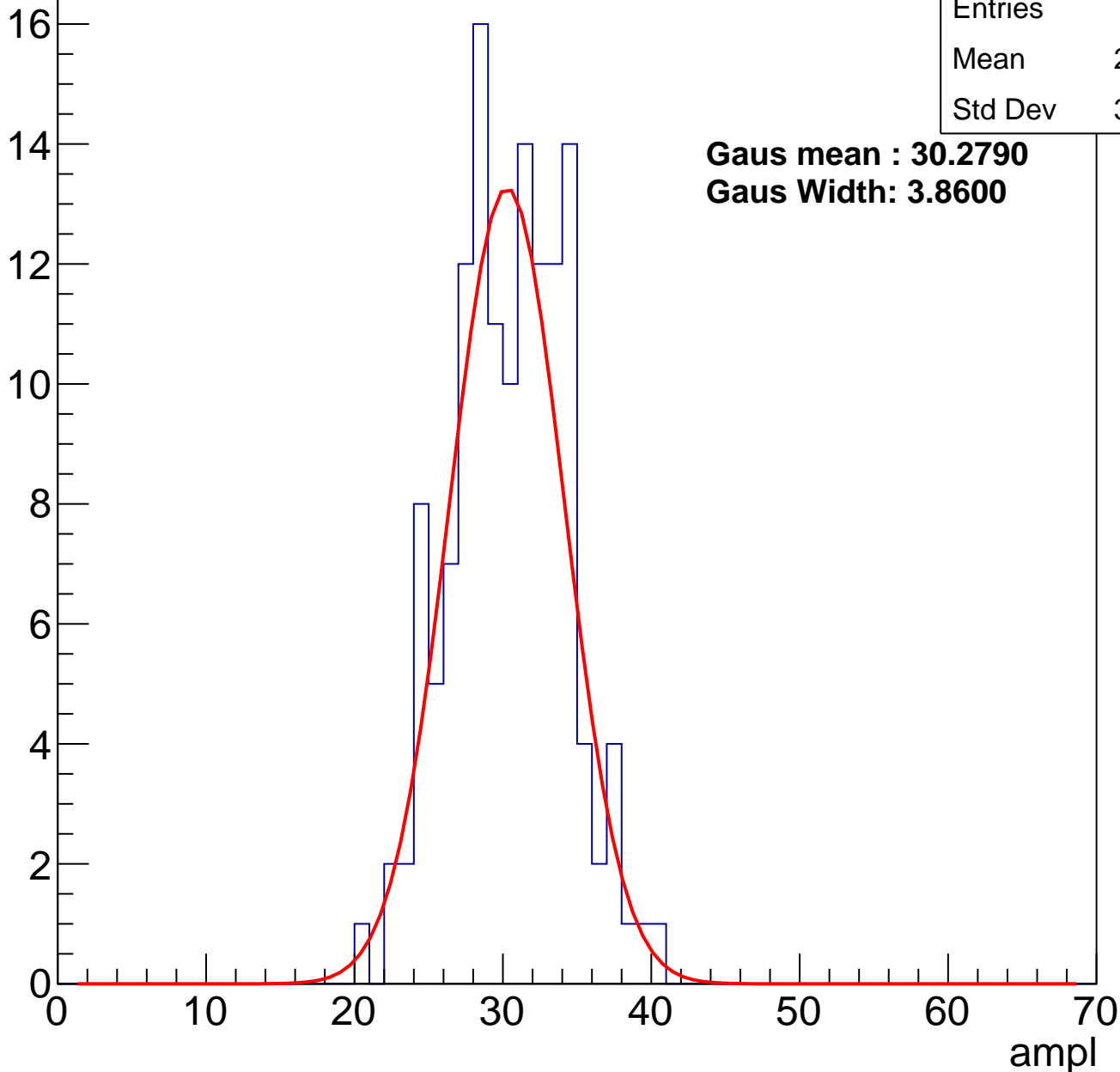
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	29.98
Std Dev	3.832

Gaus mean : 30.2790

Gaus Width: 3.8600

Entry



B0L101S, U18-ch49, adc1

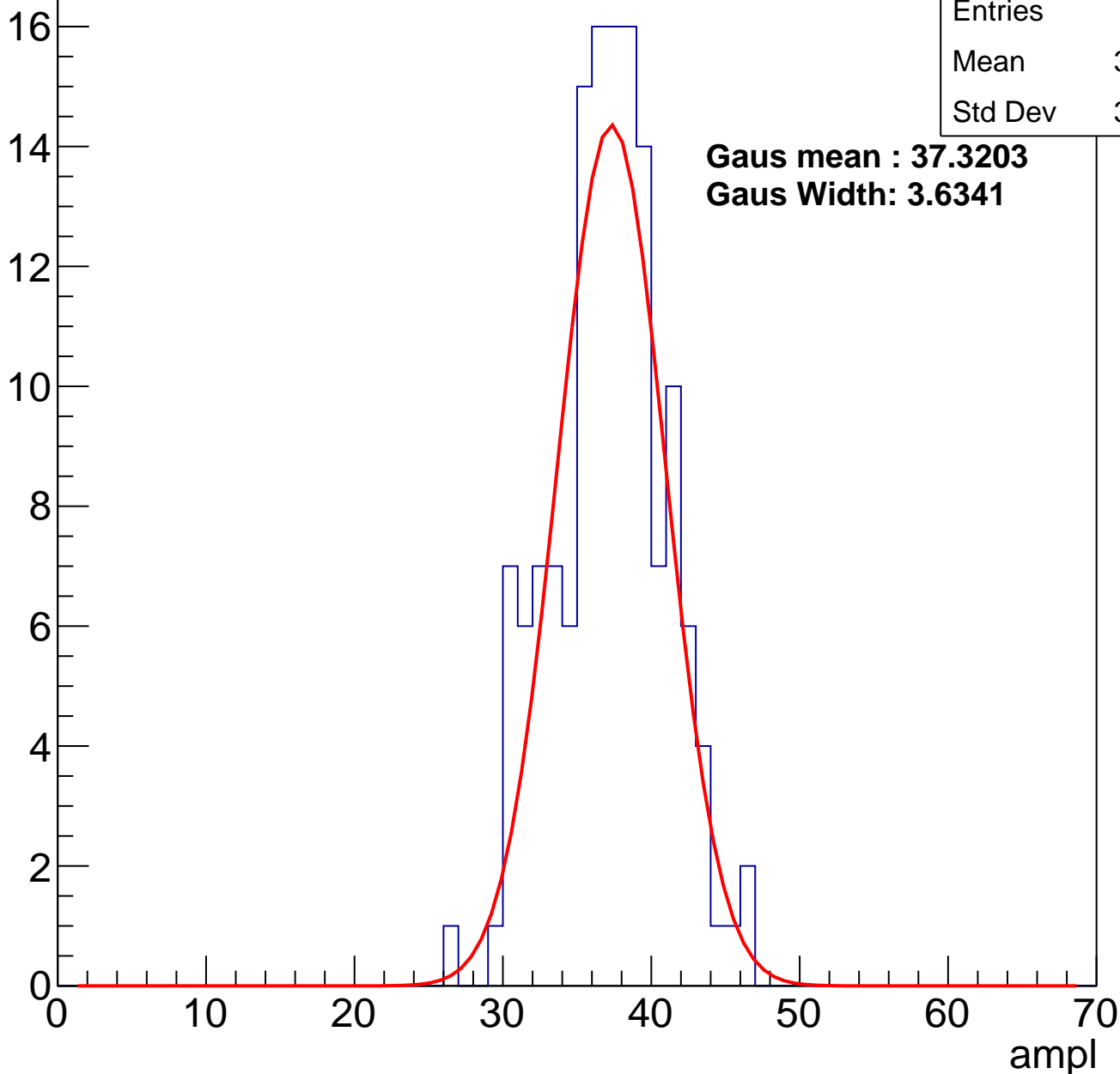
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	36.73
Std Dev	3.753

Gaus mean : 37.3203

Gaus Width: 3.6341

Entry



B0L101S, U18-ch49, adc2

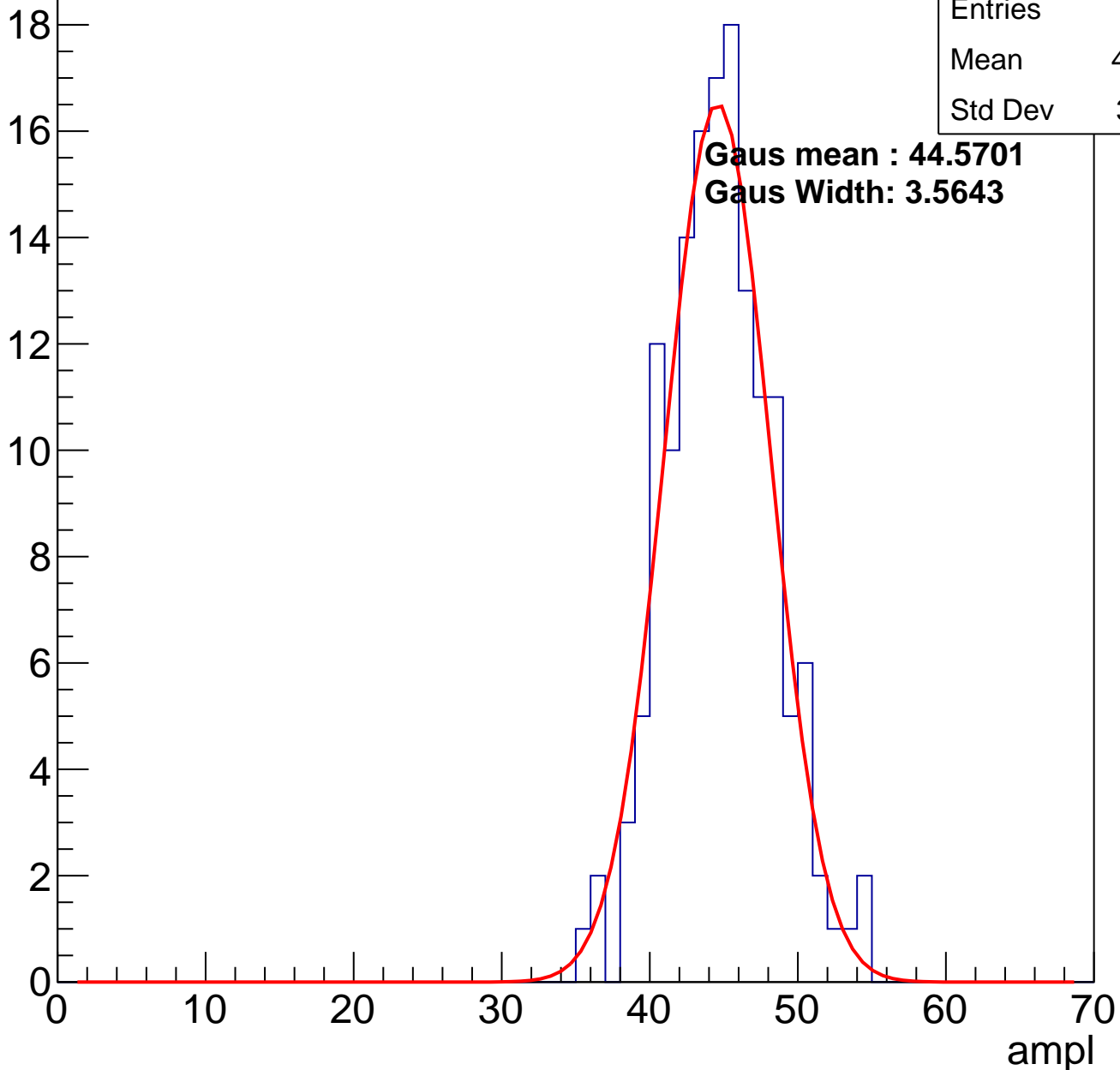
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	44.29
Std Dev	3.571

Entry

Gaus mean : 44.5701

Gaus Width: 3.5643



B0L101S, U18-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	85
Mean	50.16
Std Dev	2.857

Entry

12

10

8

6

4

2

0

0

10

20

30

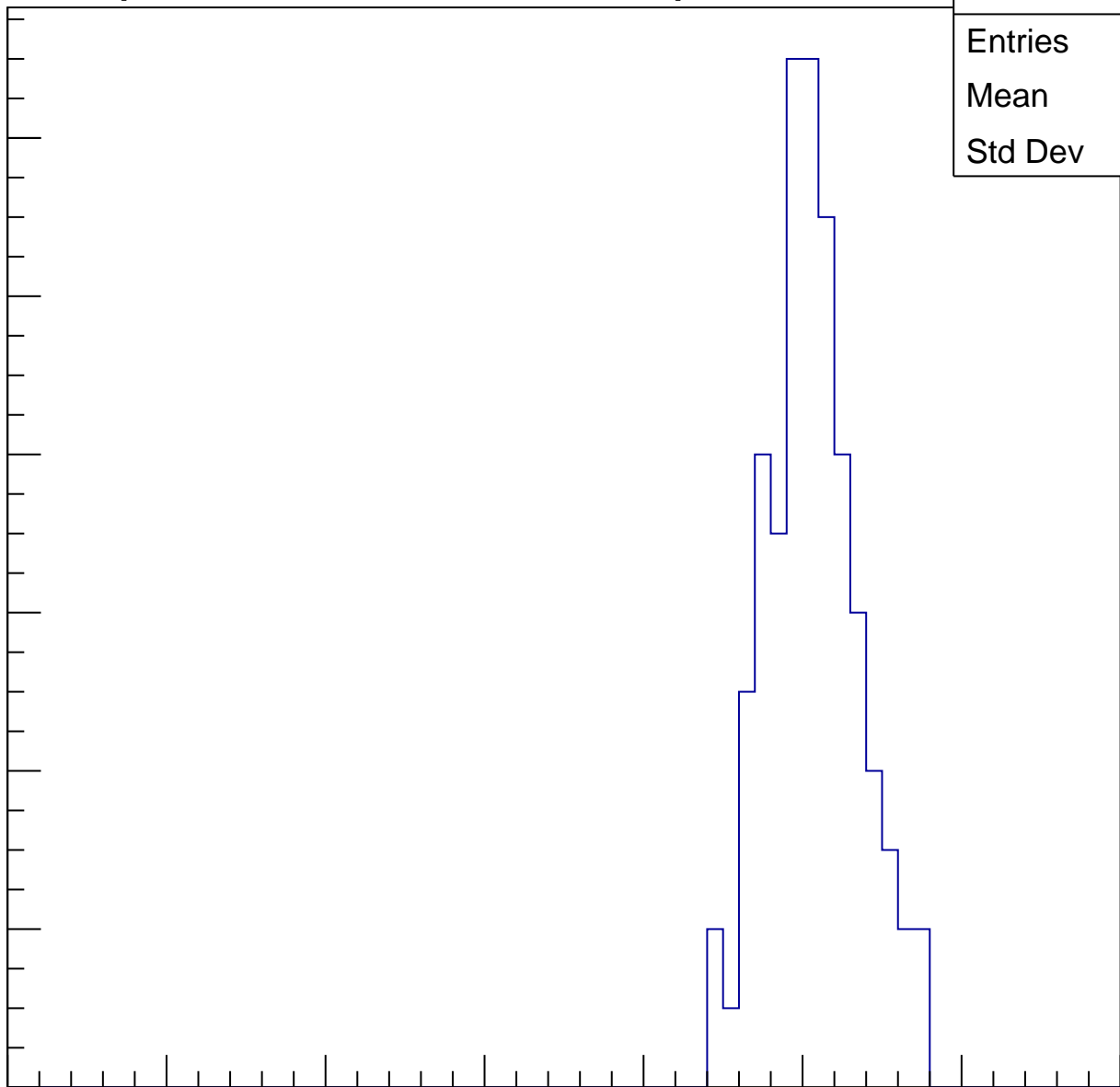
40

50

60

70

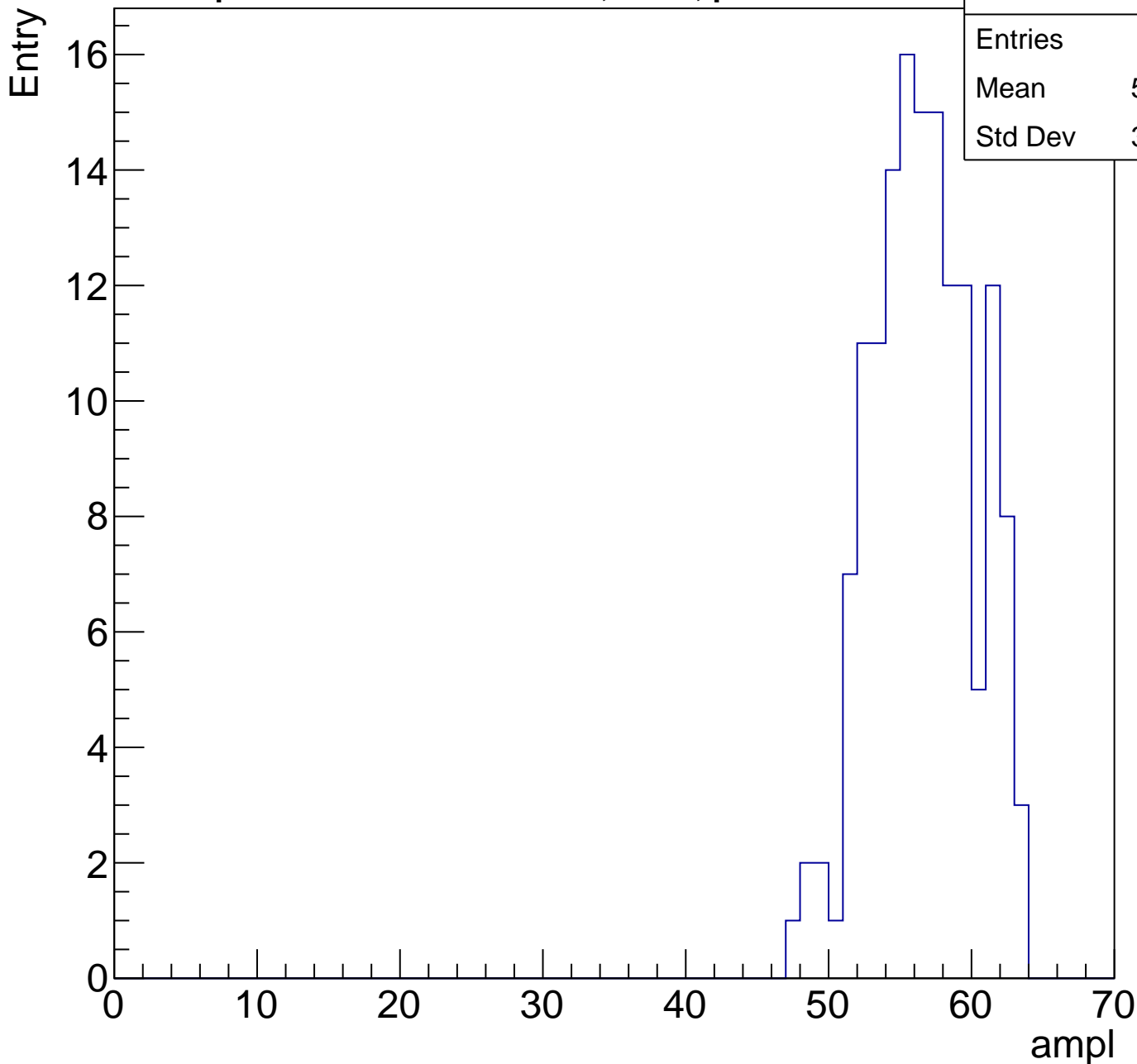
ampl



B0L101S, U18-ch49, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	56.16
Std Dev	3.545



B0L101S, U18-ch49, adc5

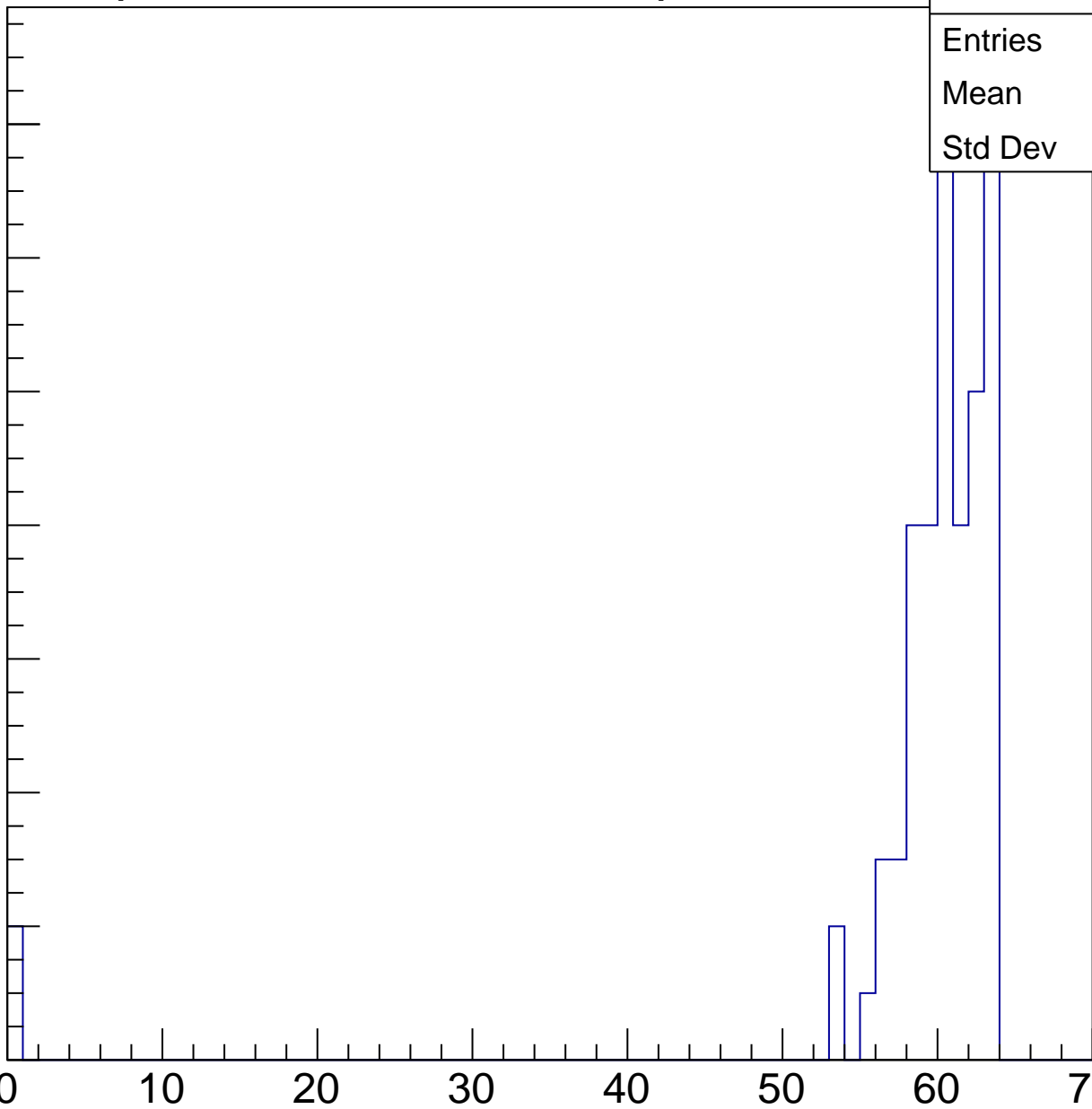
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	58.52
Std Dev	9.971

ampl



B0L101S, U18-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch50, adc0

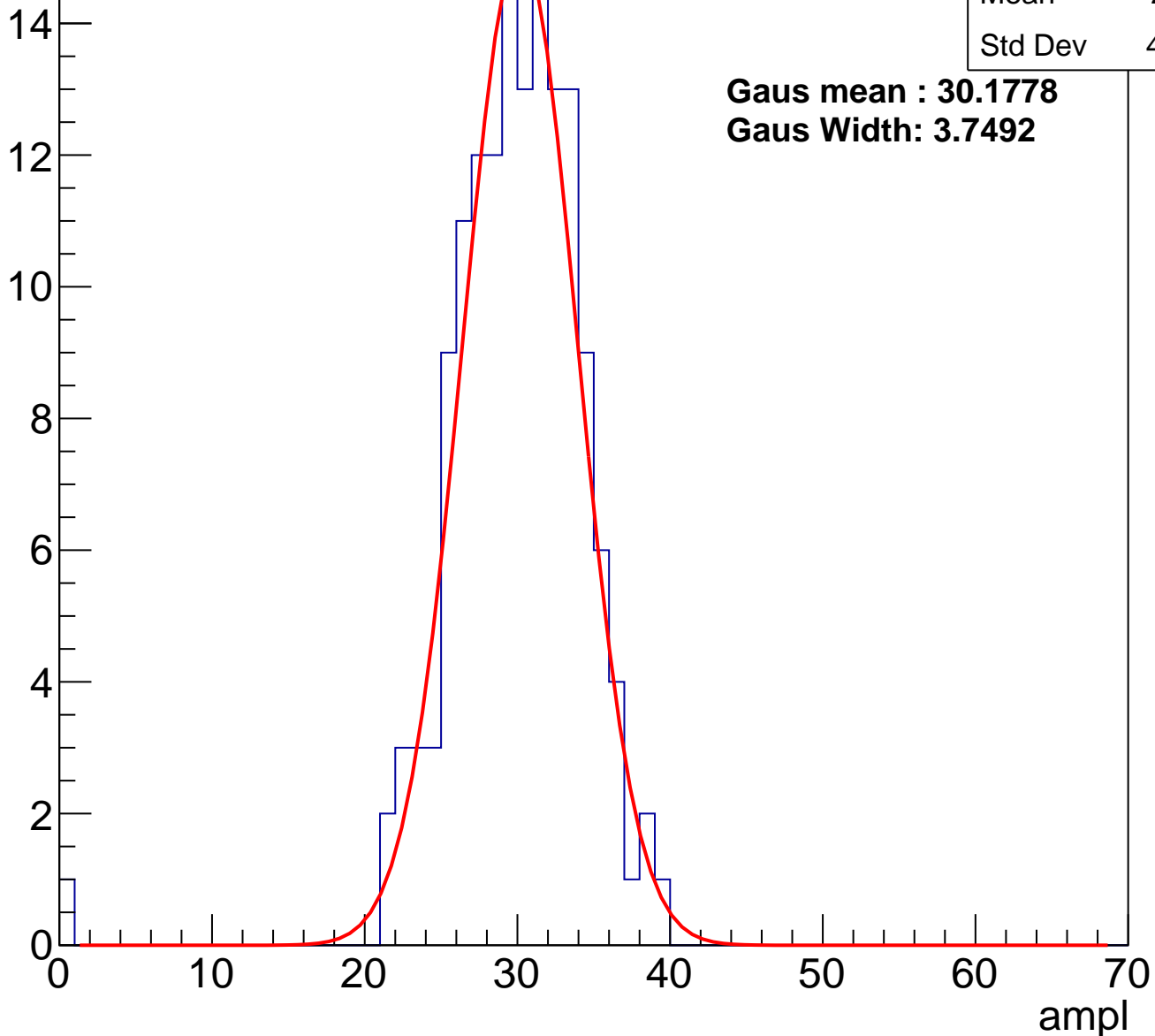
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	29.51
Std Dev	4.446

Gaus mean : 30.1778

Gaus Width: 3.7492

Entry



B0L101S, U18-ch50, adc1

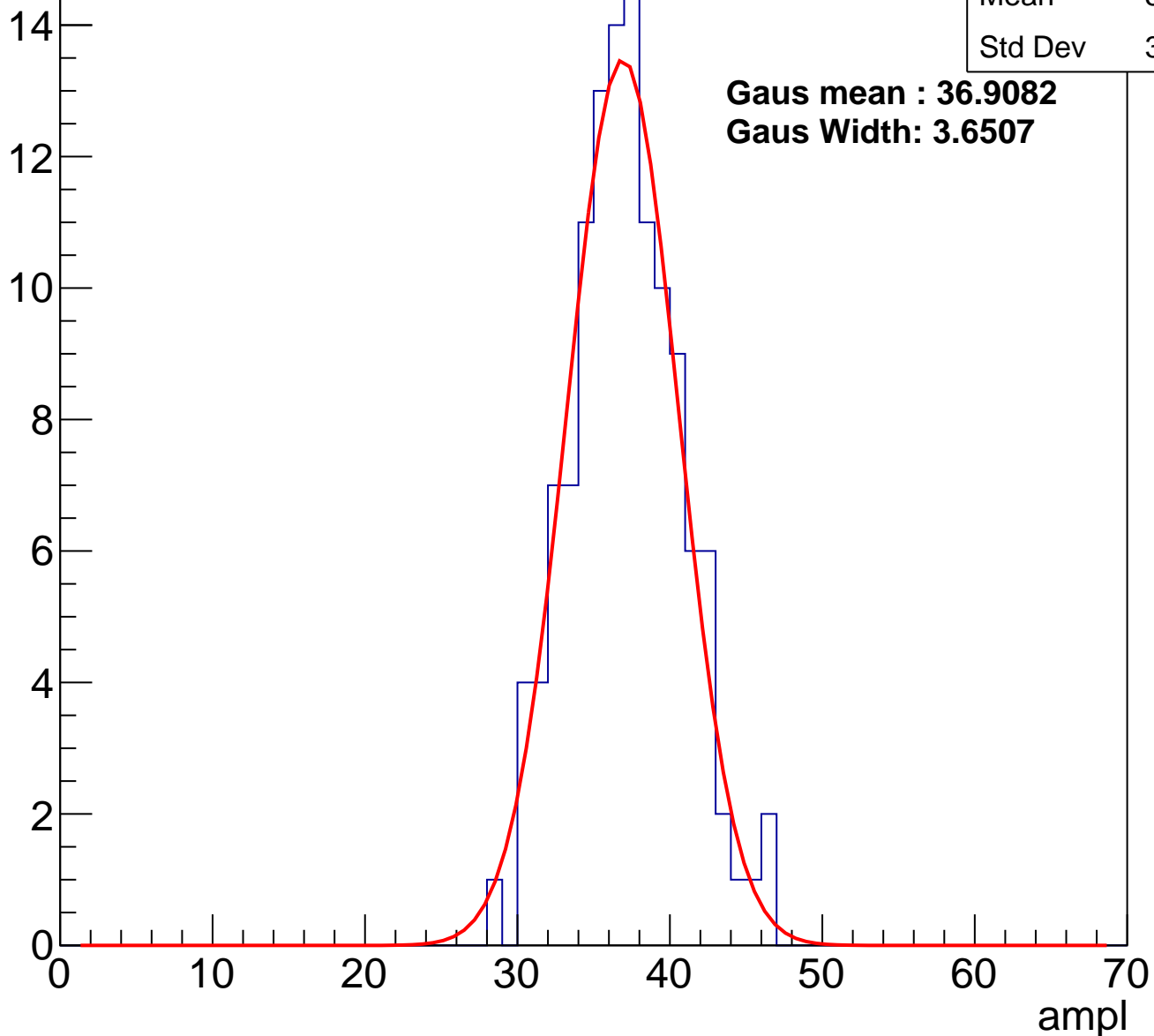
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.68
Std Dev	3.575

Gaus mean : 36.9082

Gaus Width: 3.6507

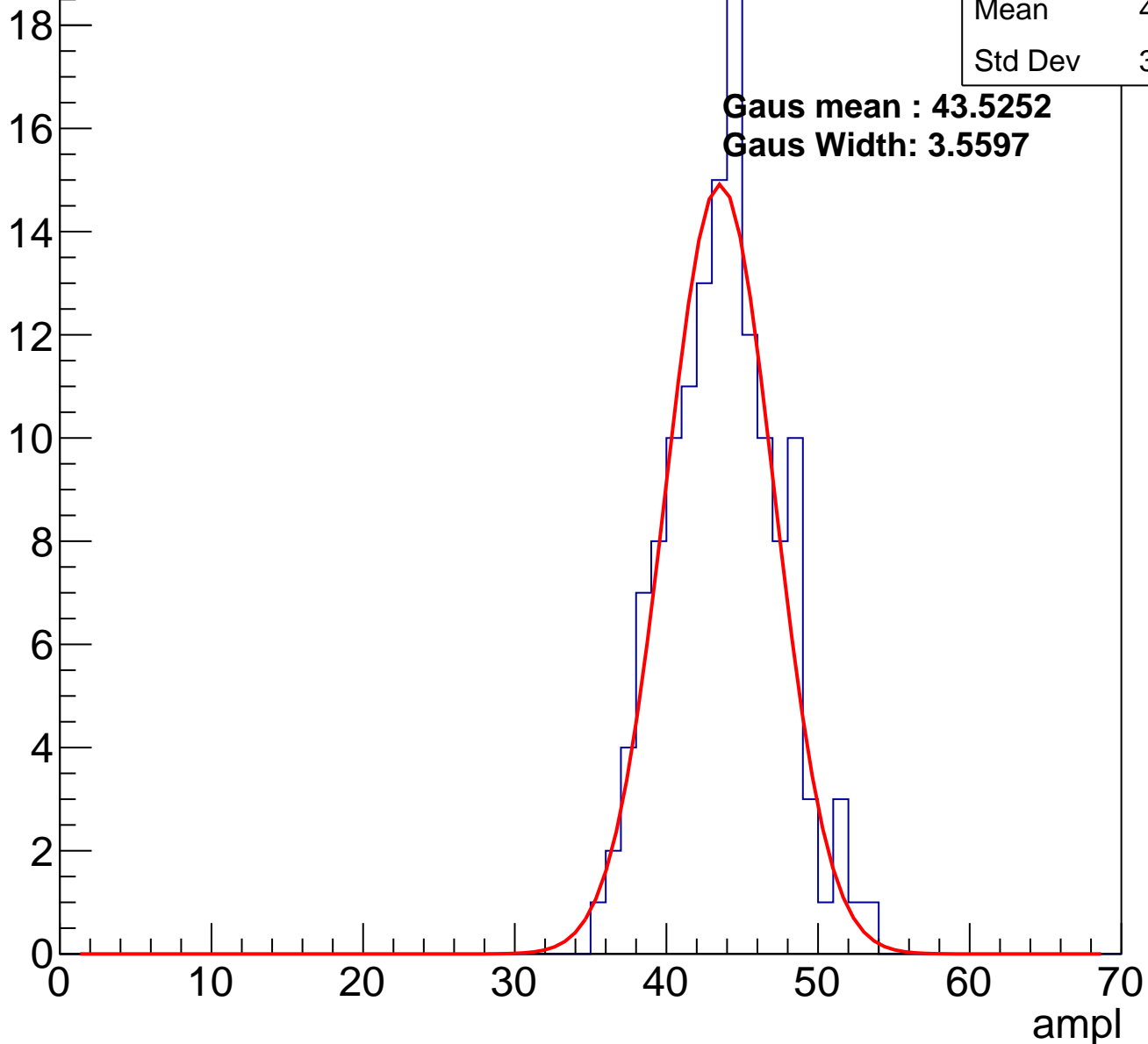
Entry



B0L101S, U18-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

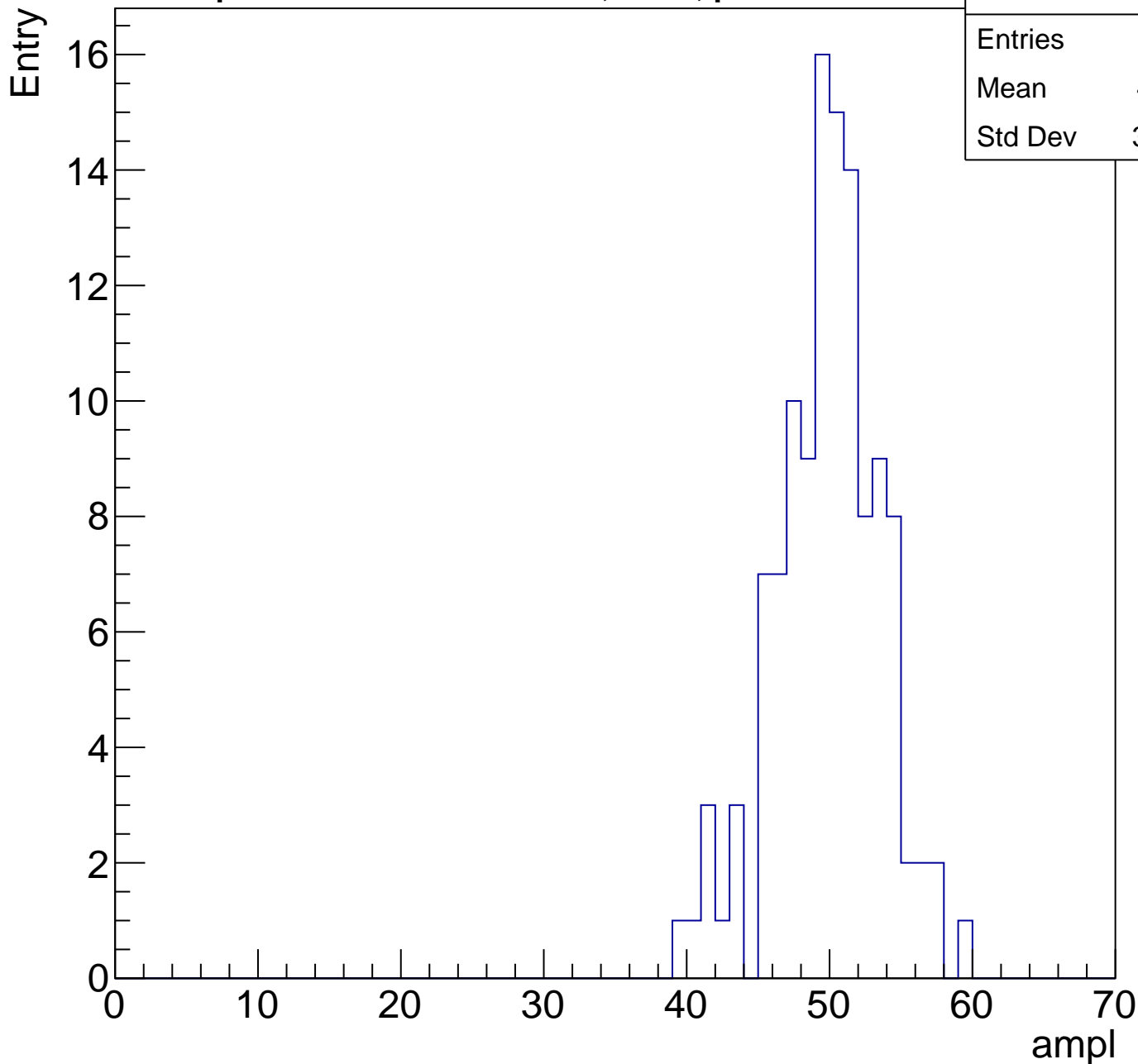
Entry



B0L101S, U18-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	49.41
Std Dev	3.688



B0L101S, U18-ch50, adc4

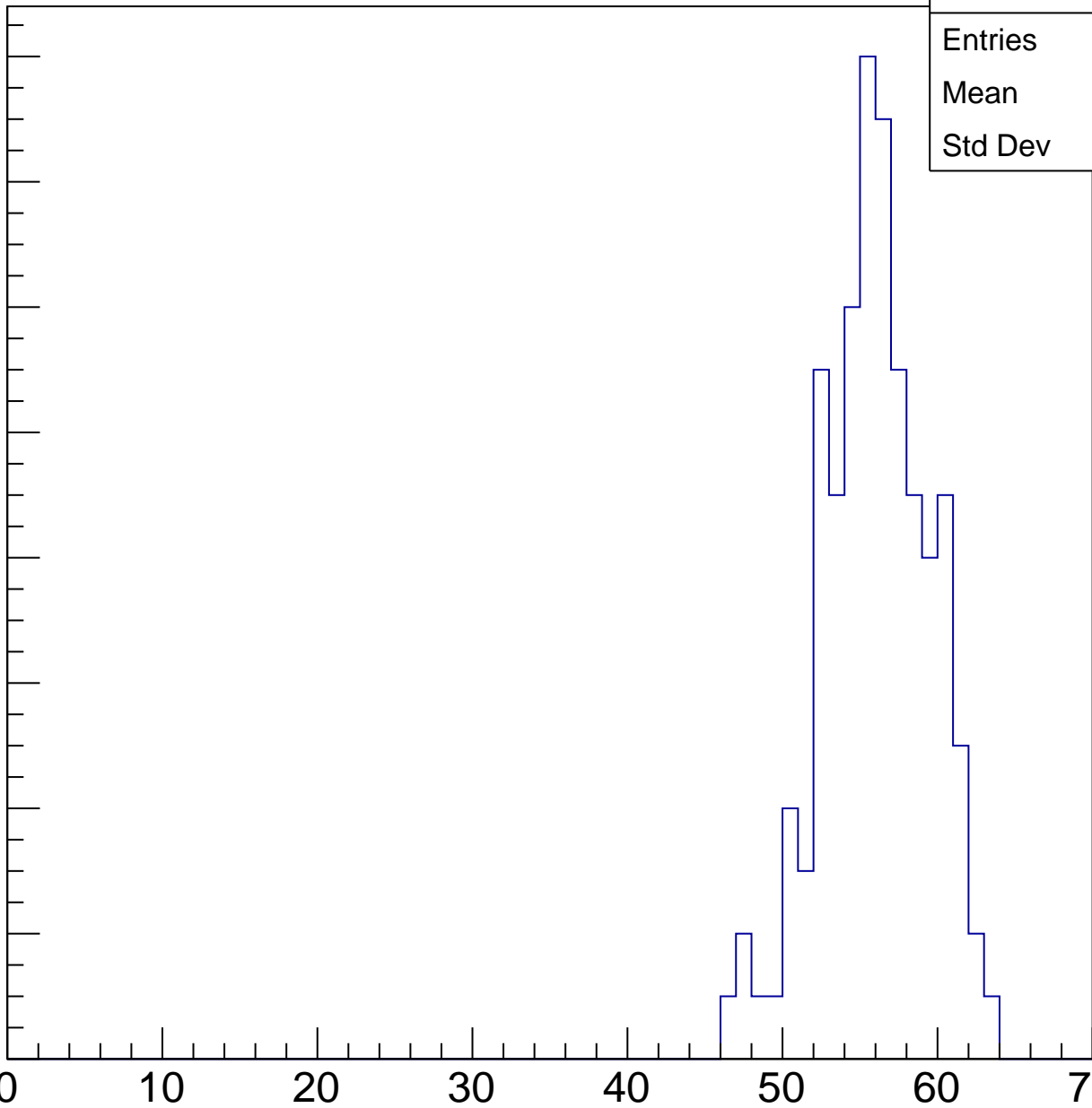
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	55.5
Std Dev	3.408

ampl



B0L101S, U18-ch50, adc5

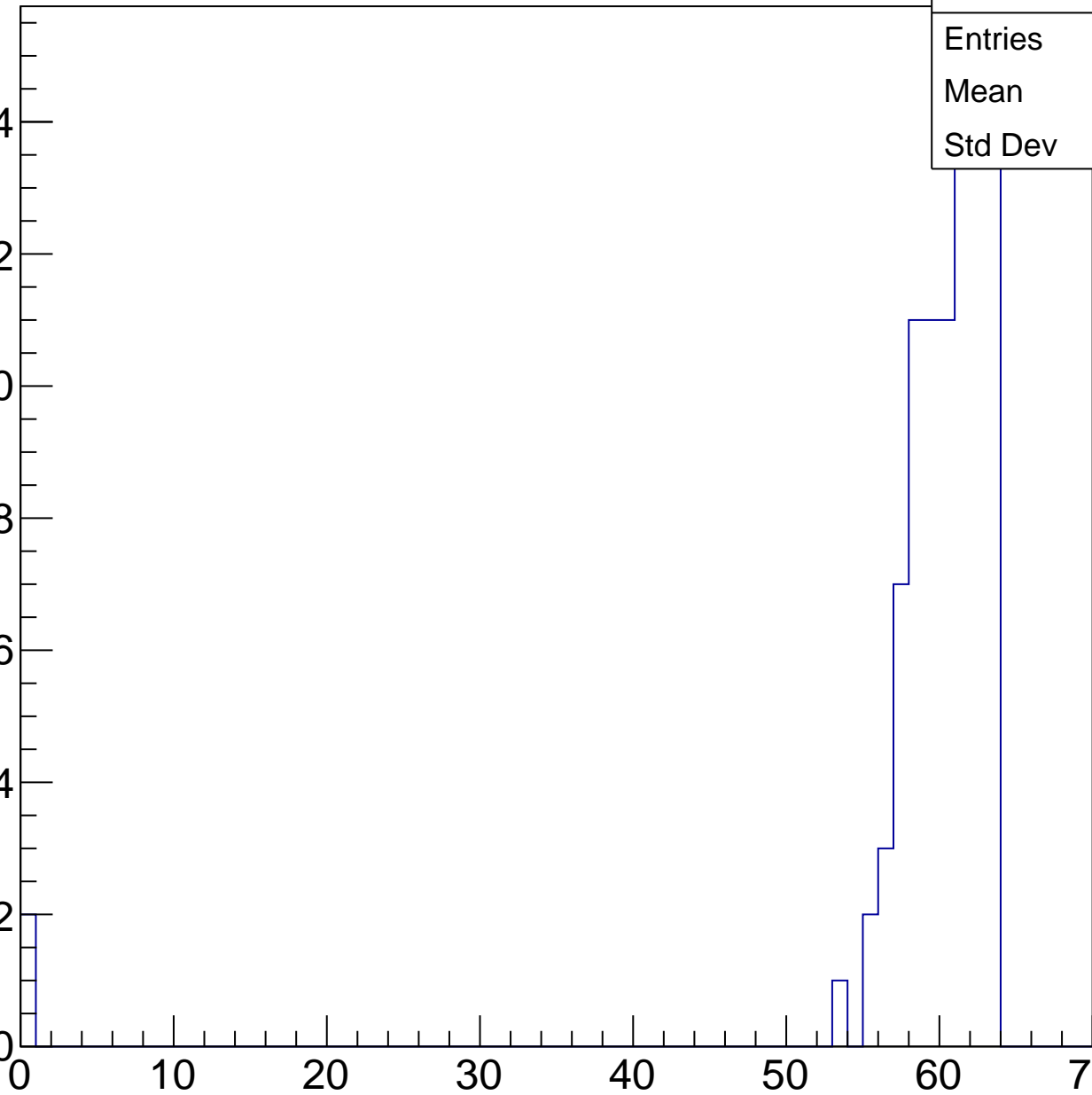
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.71
Std Dev	9.087

ampl

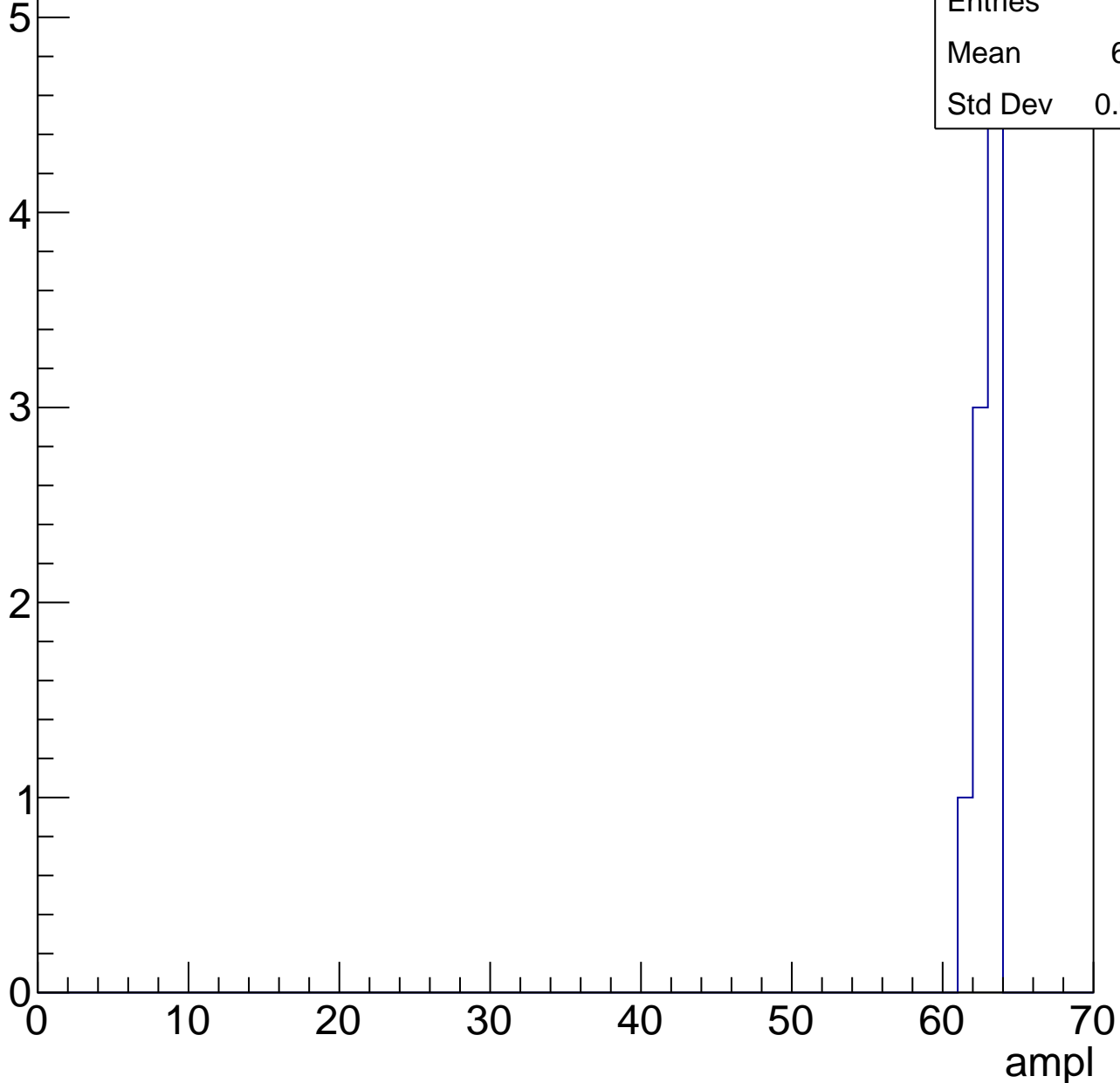


B0L101S, U18-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	62.44
Std Dev	0.6849



B0L101S, U18-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U18-ch51, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.56
Std Dev	4.186

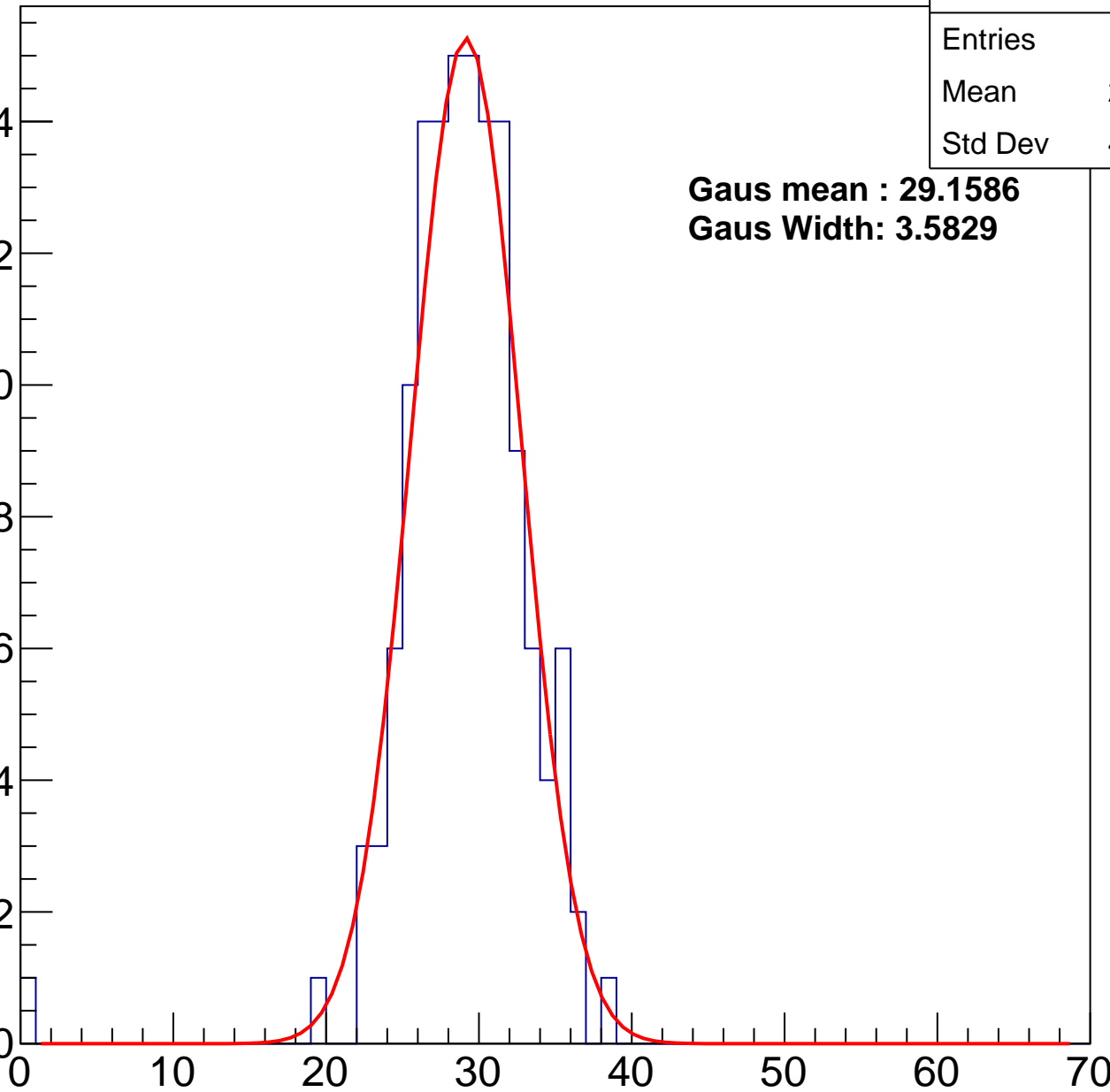
Gaus mean : 29.1586

Gaus Width: 3.5829

Entry

14
12
10
8
6
4
2
0

ampl



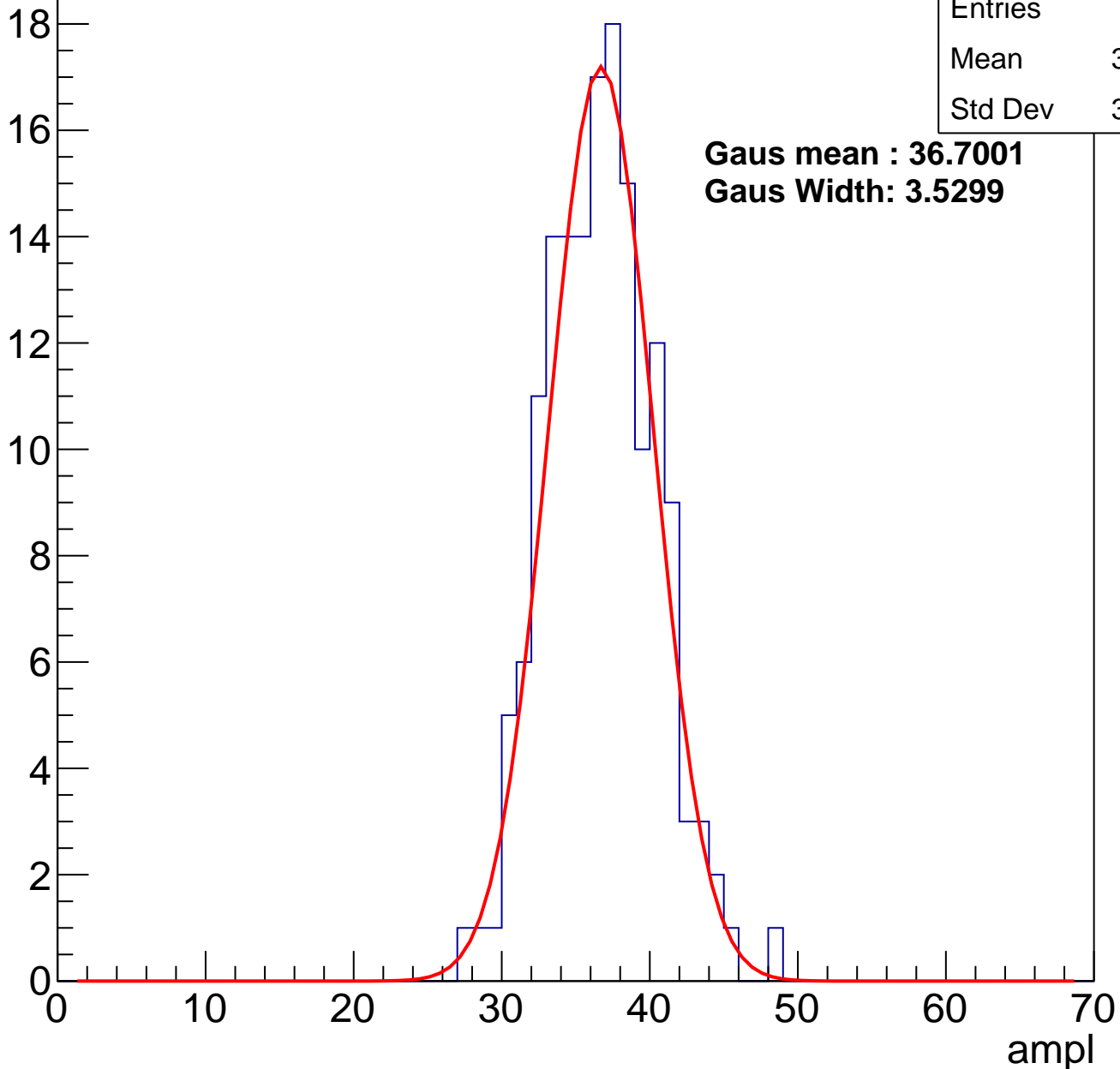
B0L101S, U18-ch51, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	36.22
Std Dev	3.629

Gaus mean : 36.7001
Gaus Width: 3.5299

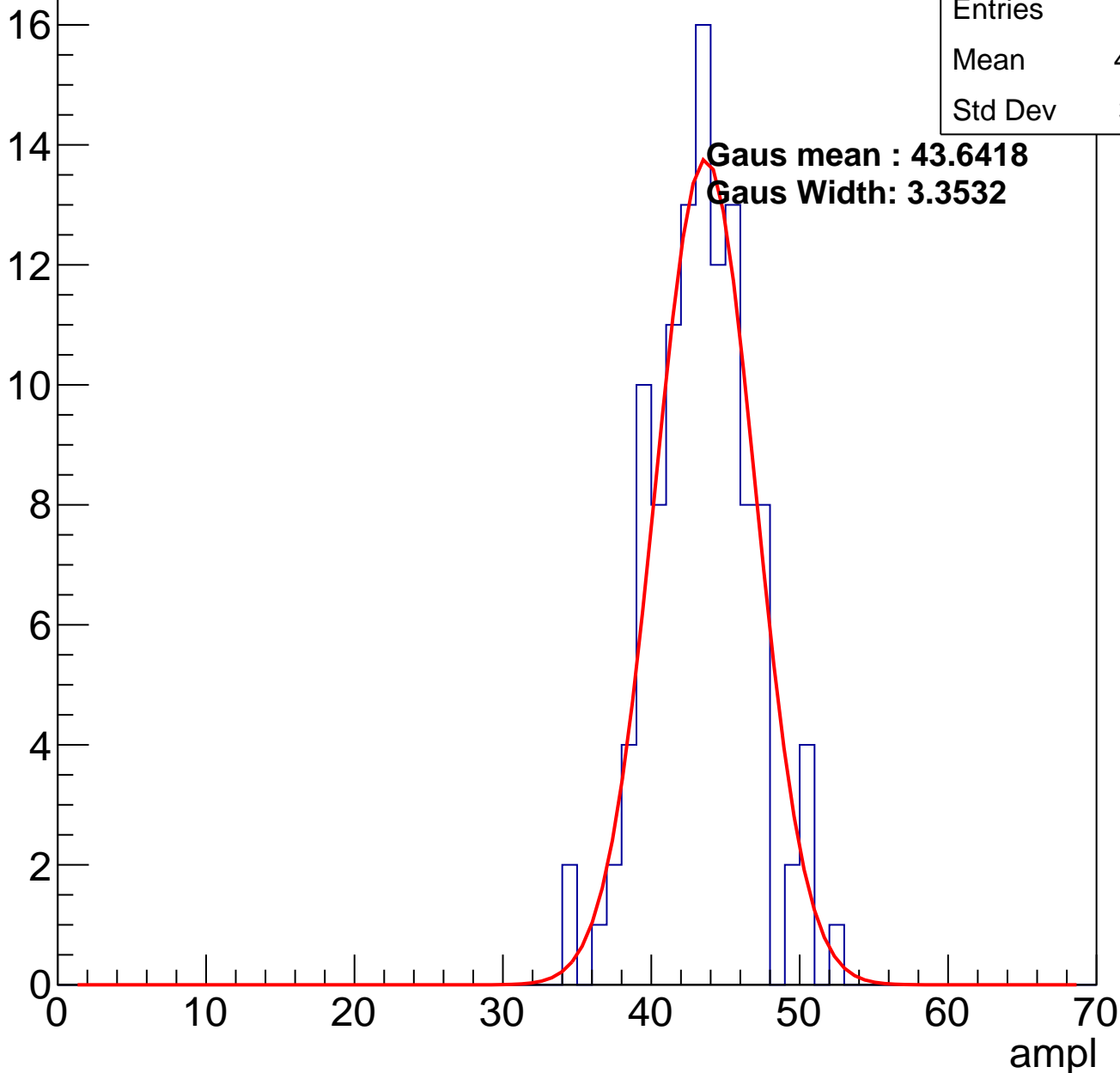
Entry



B0L101S, U18-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

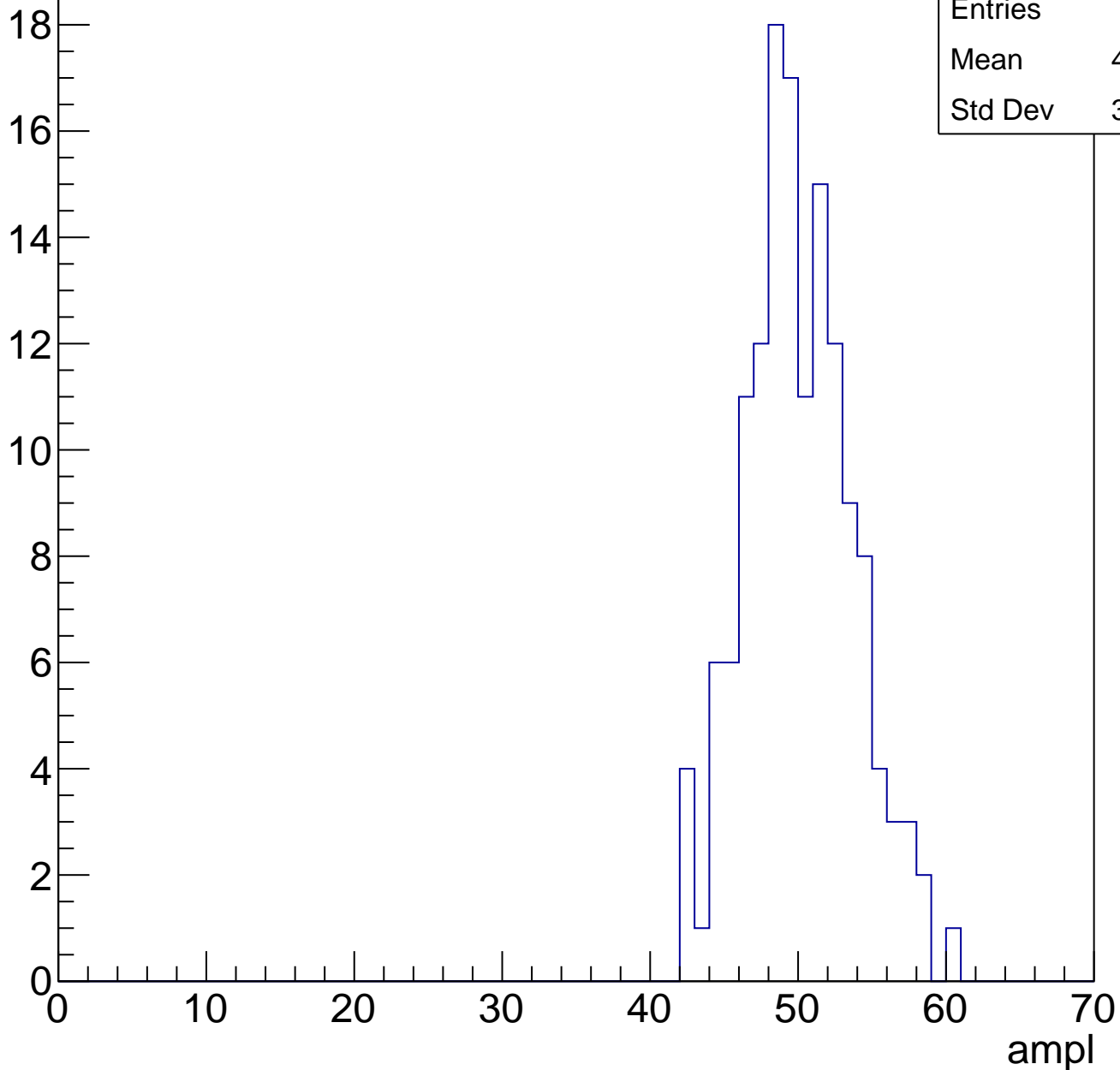


B0L101S, U18-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	49.62
Std Dev	3.624

Entry



B0L101S, U18-ch51, adc4

calib_packv5_042523_0143.root, FC#1, port C1

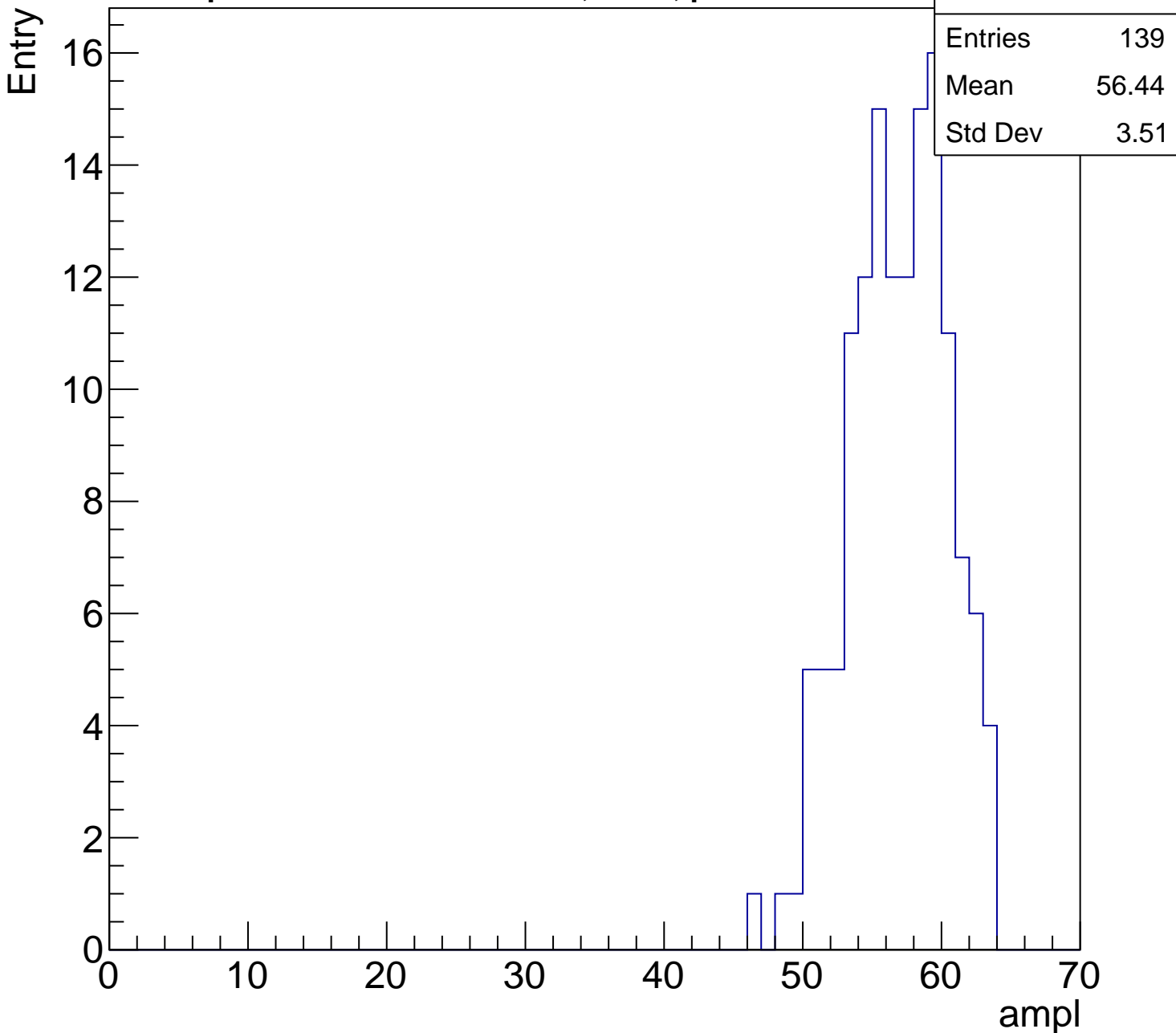
Entries	139
Mean	56.44
Std Dev	3.51

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch51, adc5

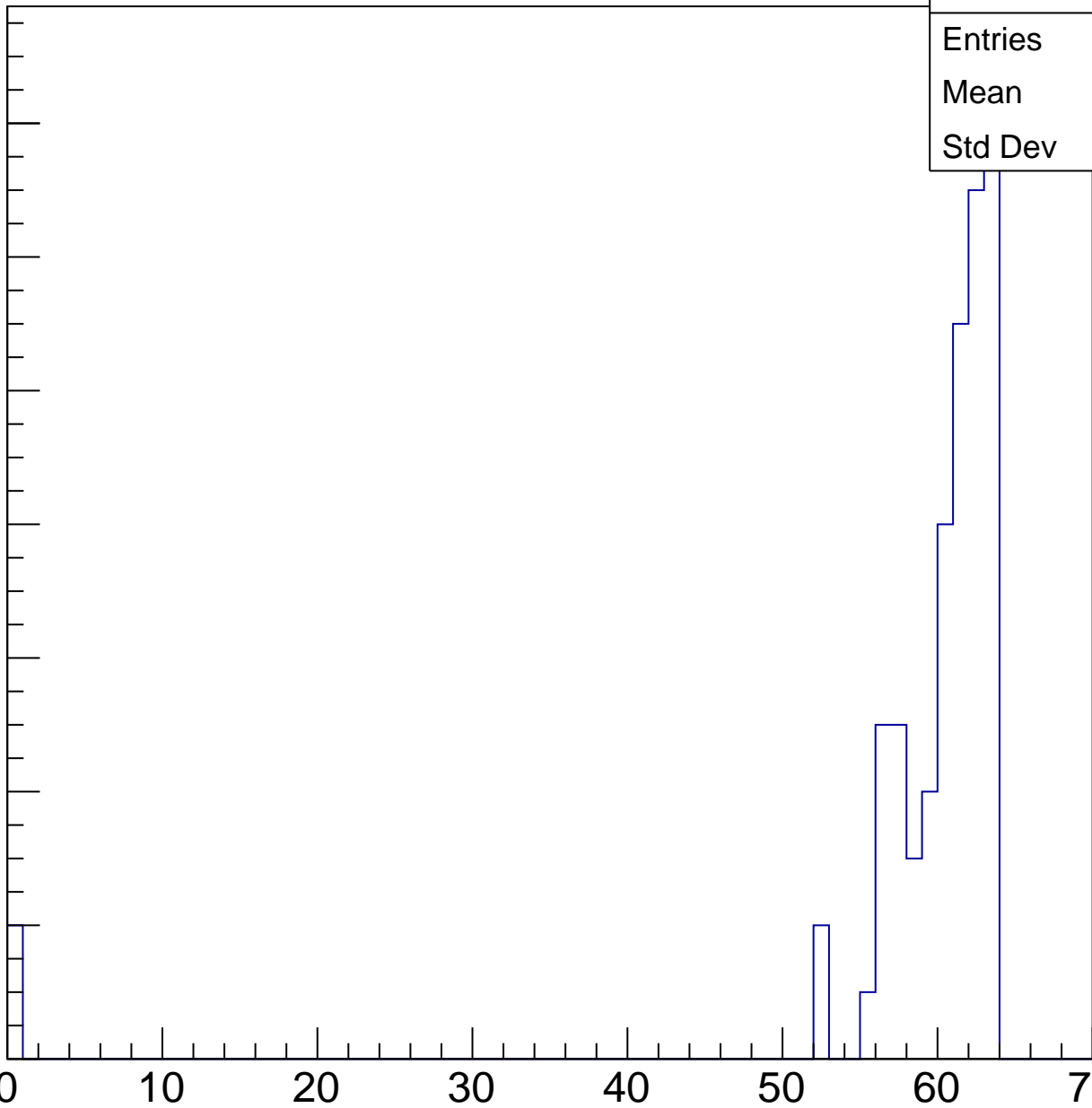
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	58.49
Std Dev	10.45

ampl



B0L101S, U18-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L101S, U18-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U18-ch52, adc0

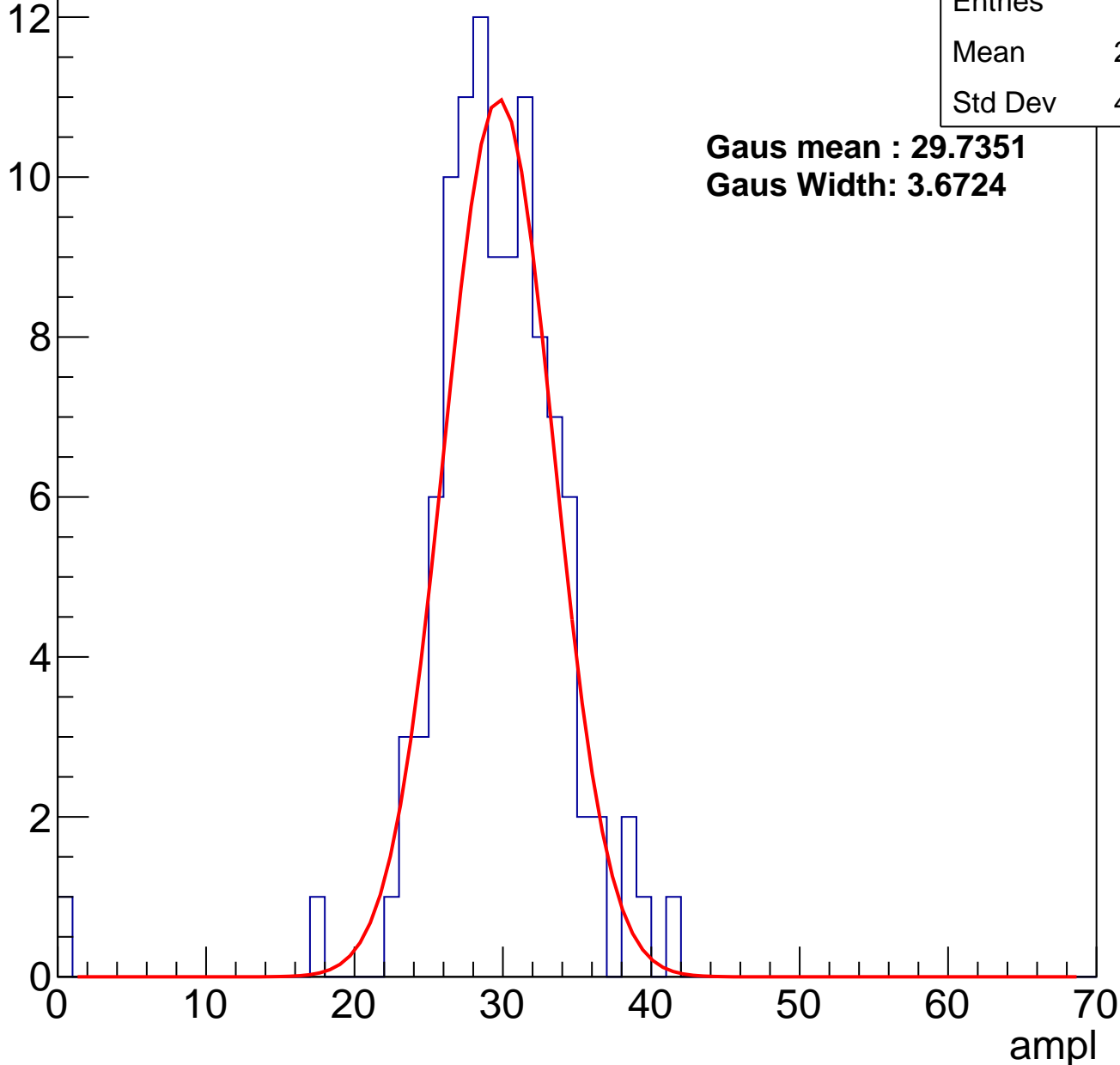
calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	29.09
Std Dev	4.779

Gaus mean : 29.7351

Gaus Width: 3.6724

Entry



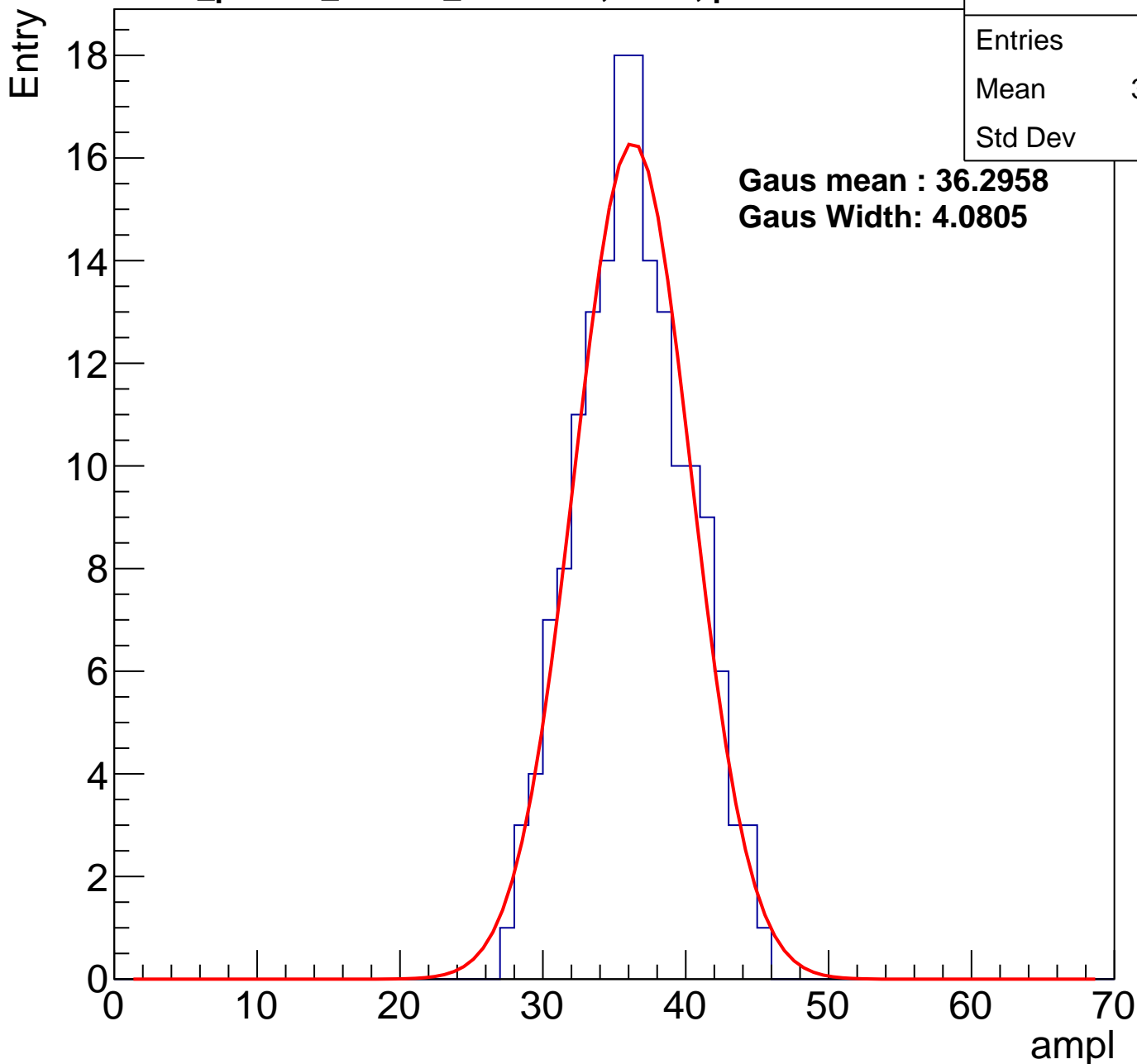
B0L101S, U18-ch52, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	35.84
Std Dev	3.85

Gaus mean : 36.2958

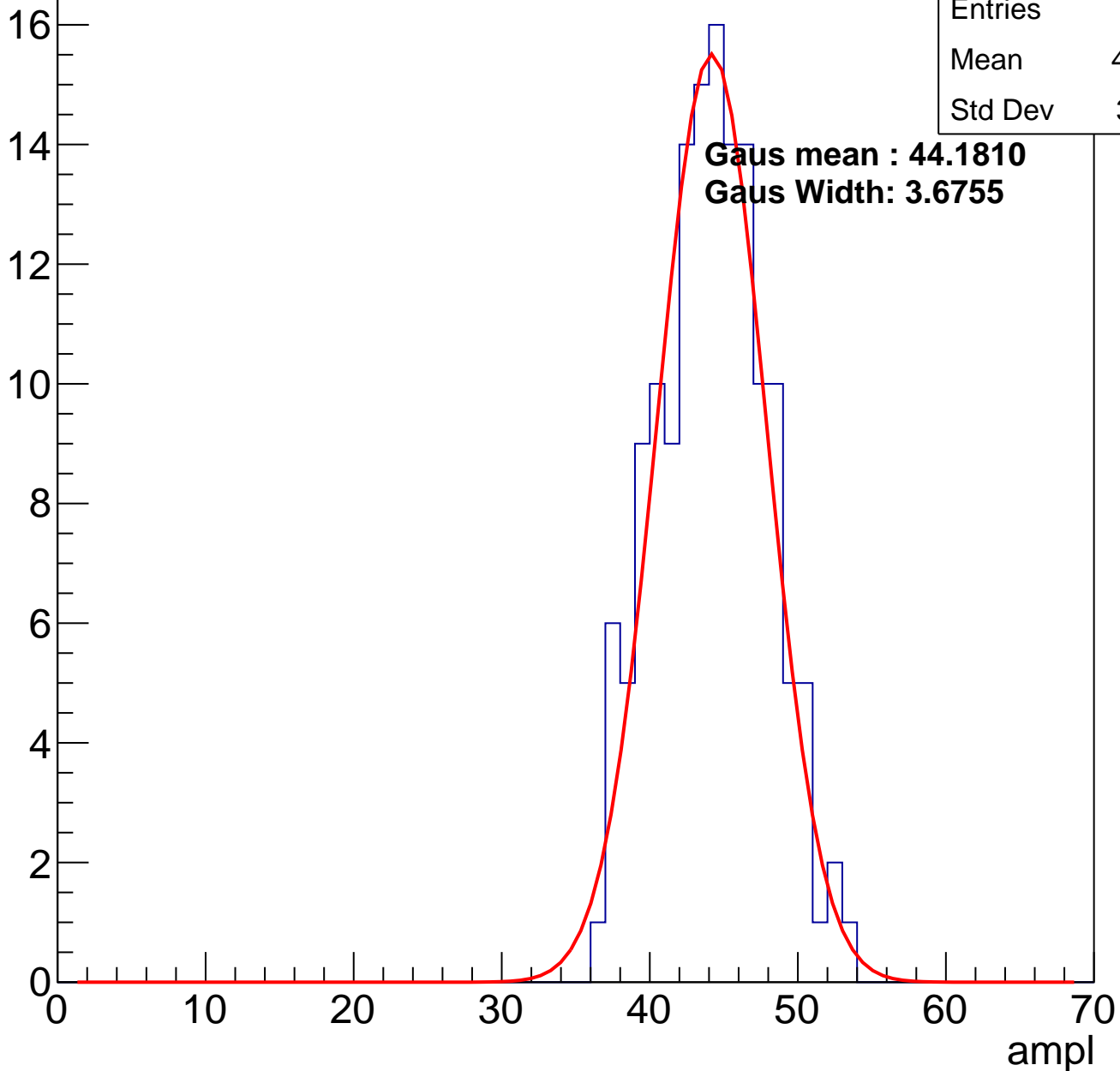
Gaus Width: 4.0805



B0L101S, U18-ch52, adc2

calib_packv5_042523_0143.root, FC#1, port C1

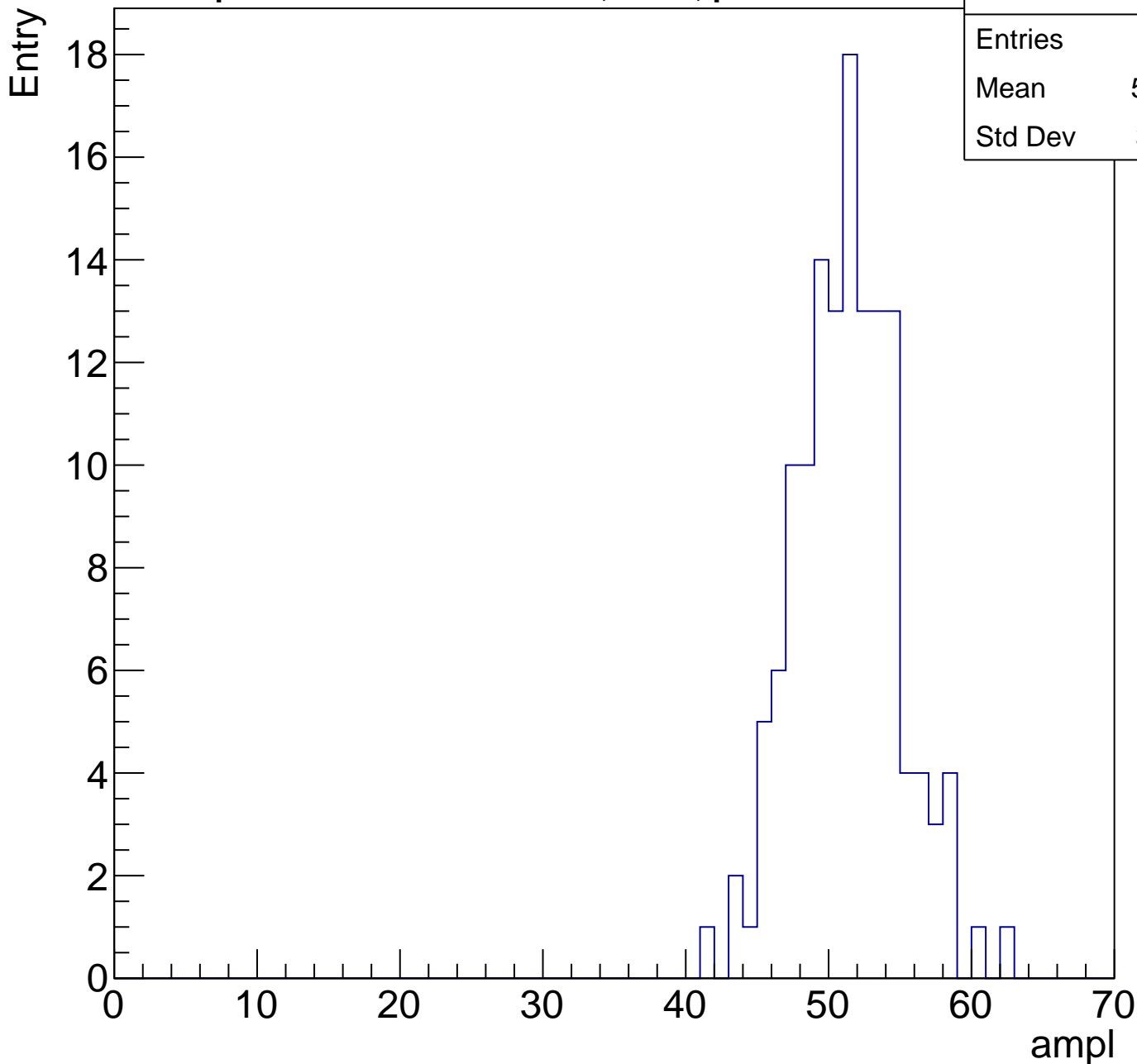
Entry



B0L101S, U18-ch52, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	50.82
Std Dev	3.611



B0L101S, U18-ch52, adc4

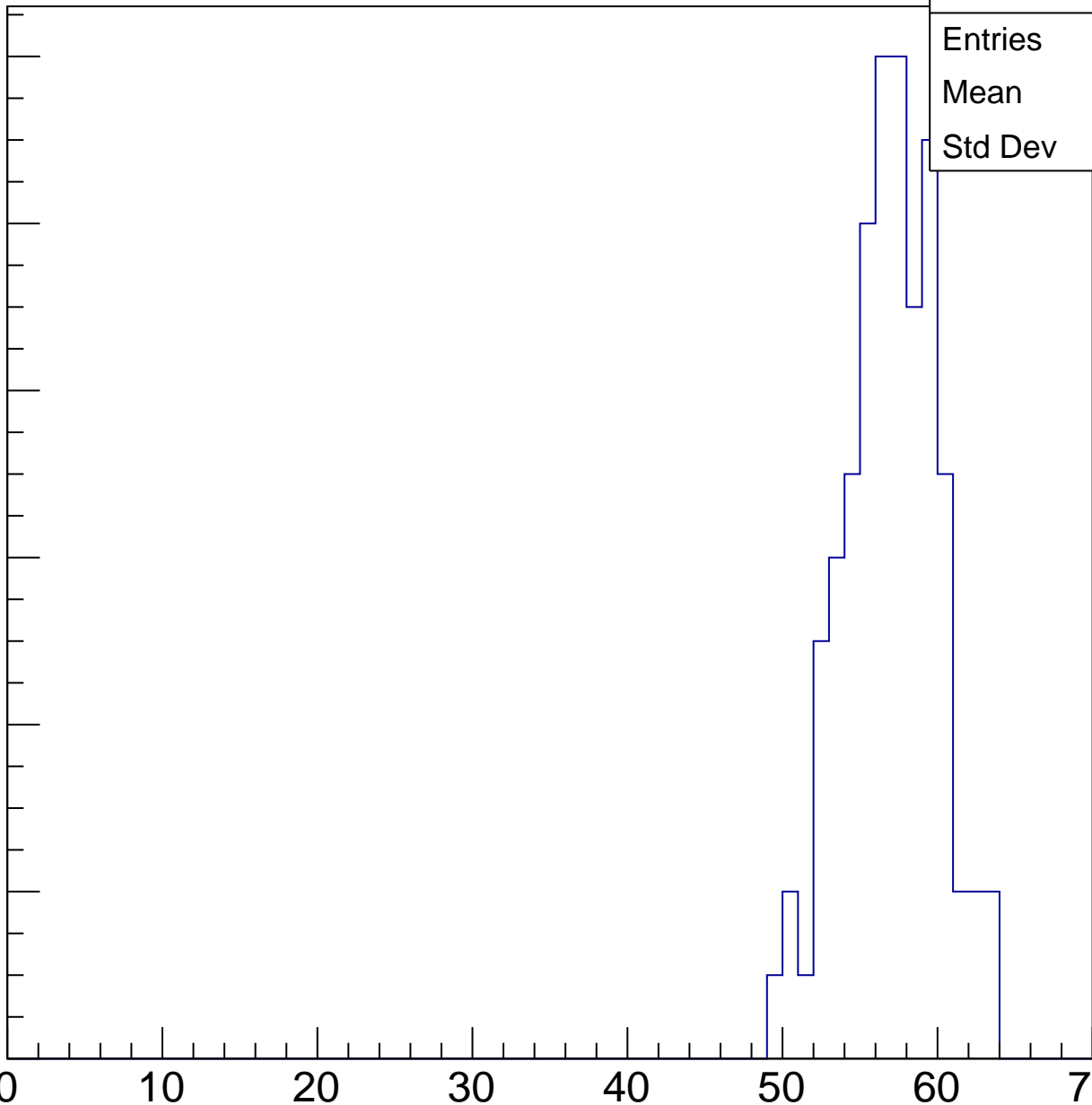
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	89
Mean	56.46
Std Dev	2.957

ampl



B0L101S, U18-ch52, adc5

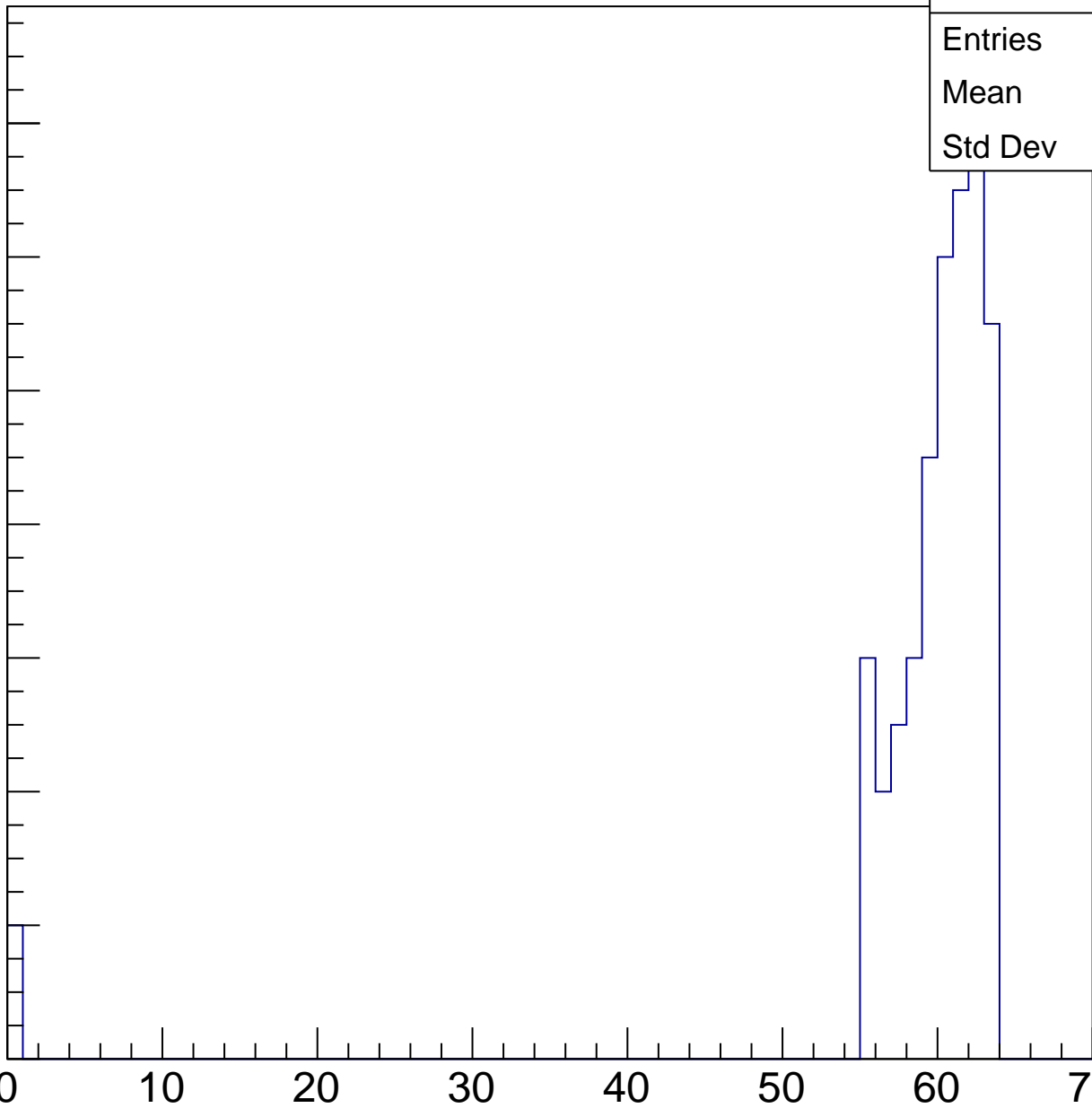
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	58.48
Std Dev	9.489

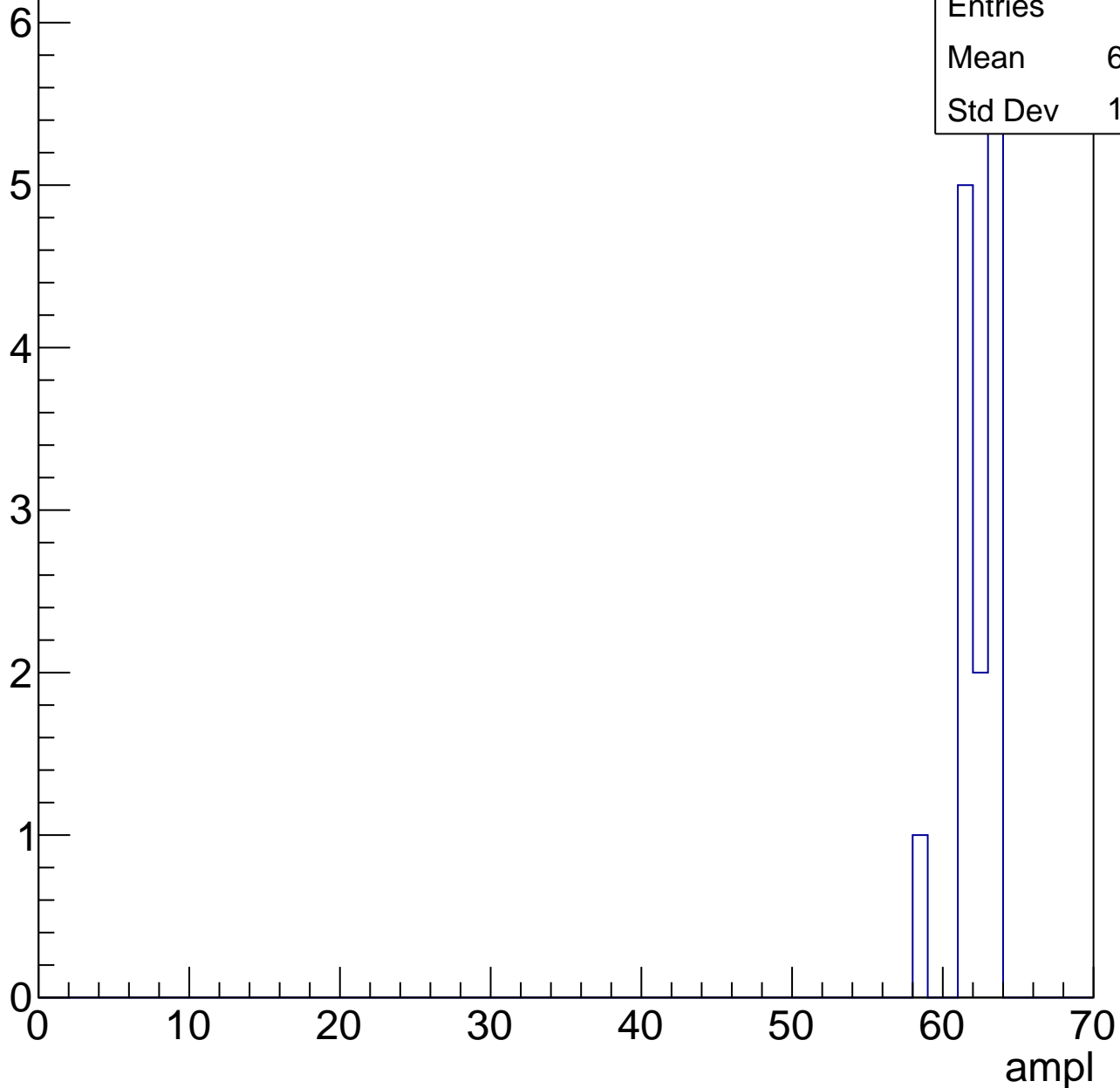
ampl



B0L101S, U18-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



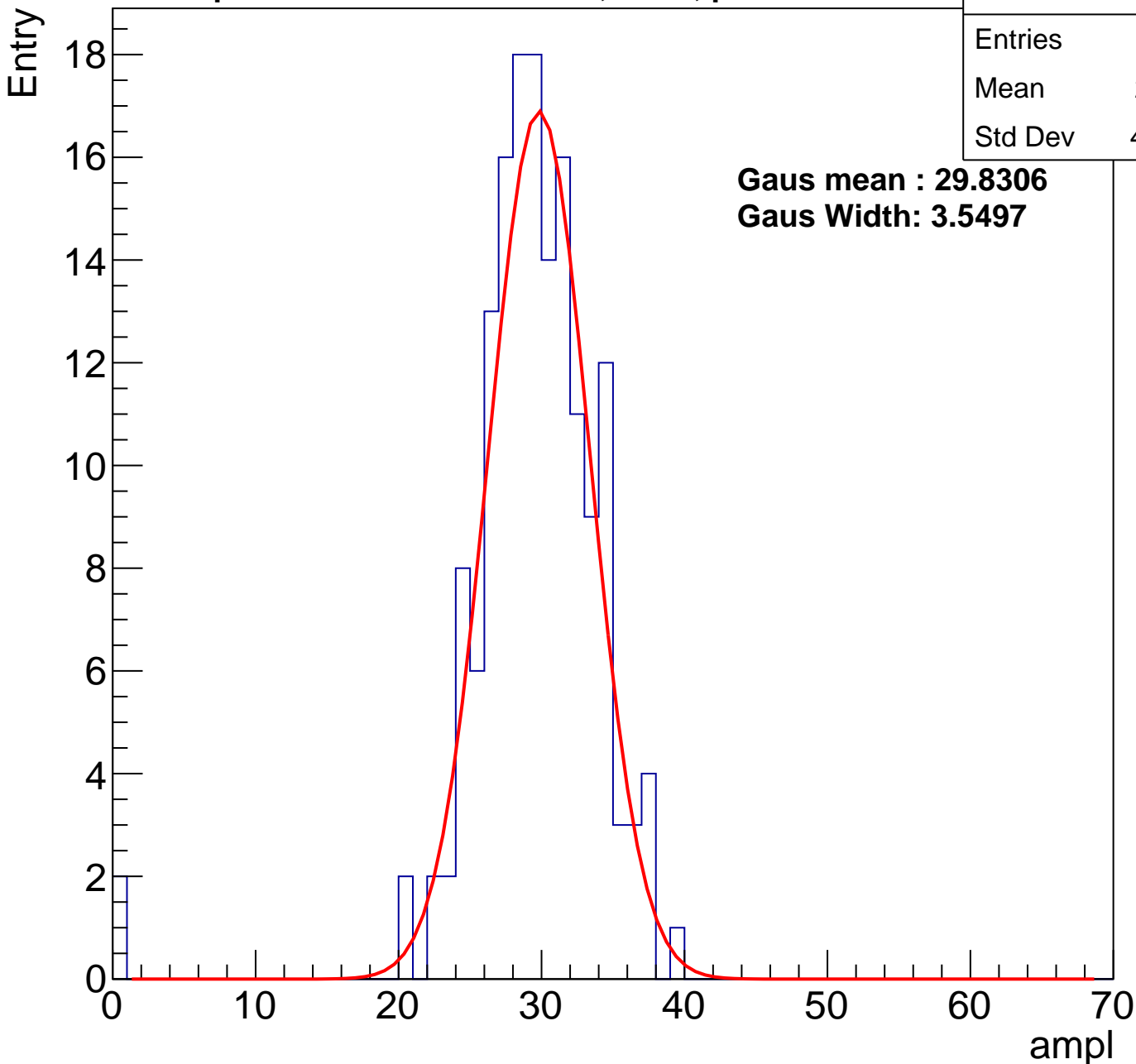
B0L101S, U18-ch53, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	29.01
Std Dev	4.835

Gaus mean : 29.8306

Gaus Width: 3.5497



B0L101S, U18-ch53, adc1

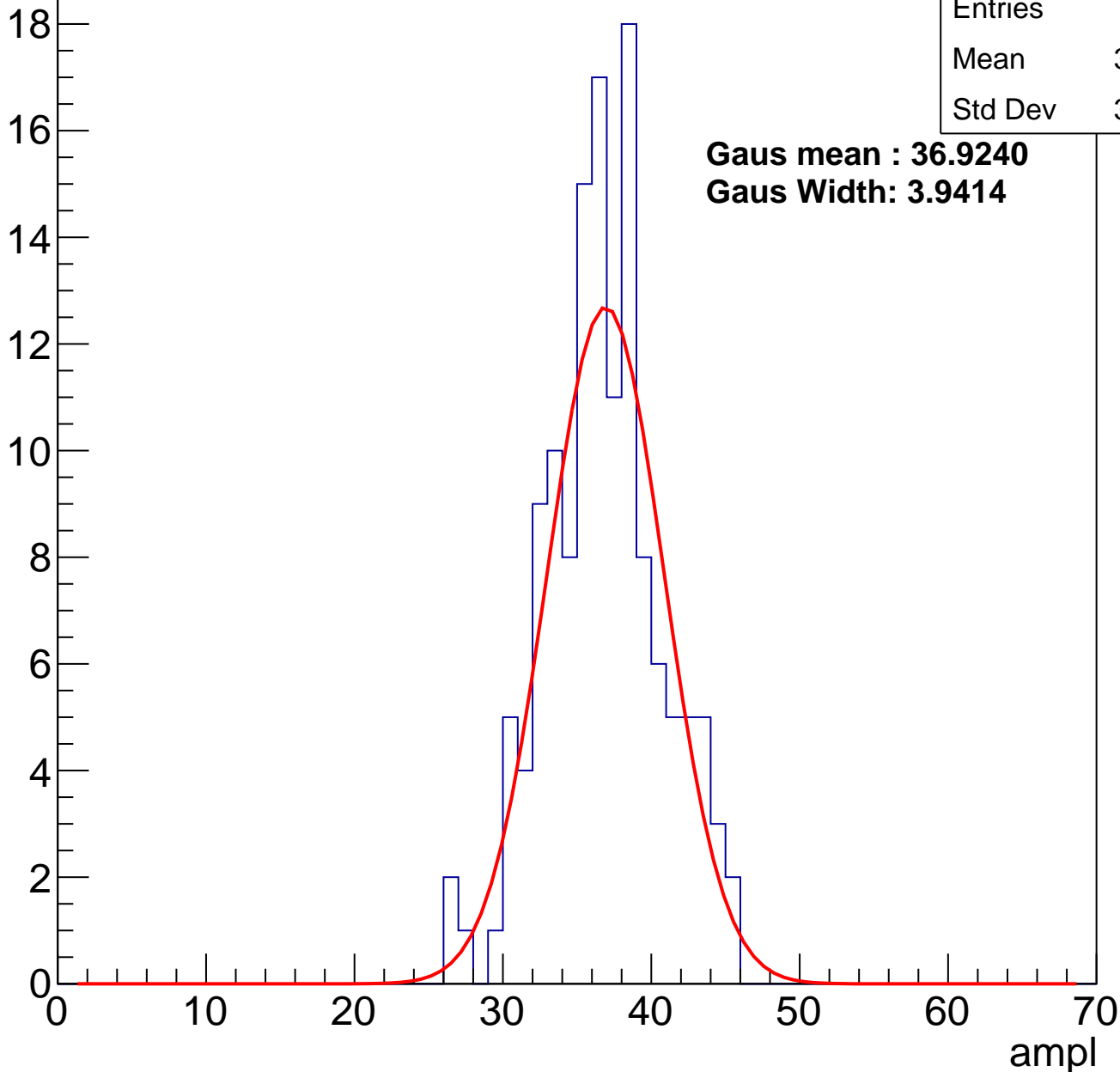
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	36.33
Std Dev	3.879

Gaus mean : 36.9240

Gaus Width: 3.9414

Entry



B0L101S, U18-ch53, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

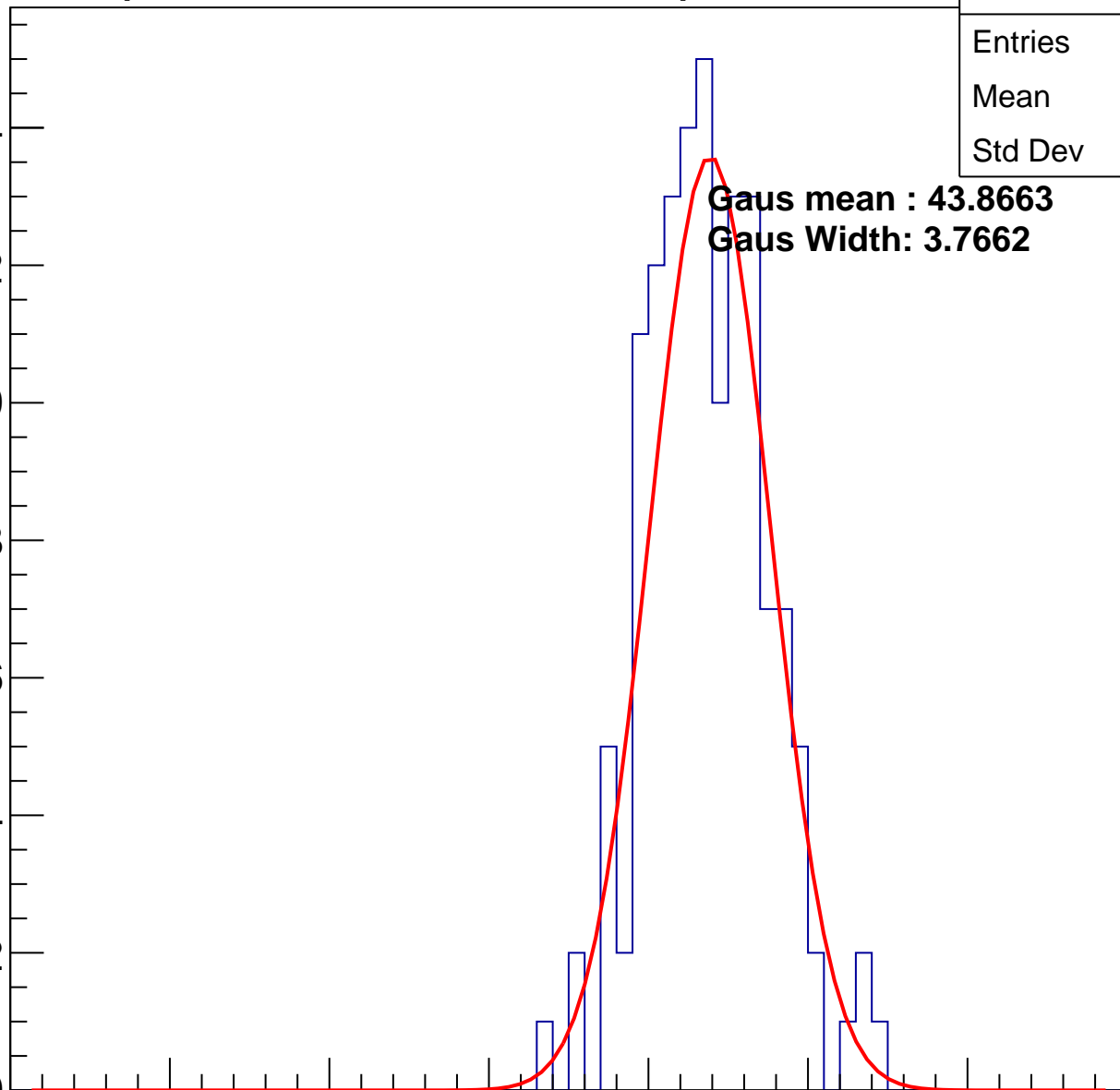
60

ampl

Entries	136
Mean	43.26
Std Dev	3.781

Gaus mean : 43.8663

Gaus Width: 3.7662



B0L101S, U18-ch53, adc3

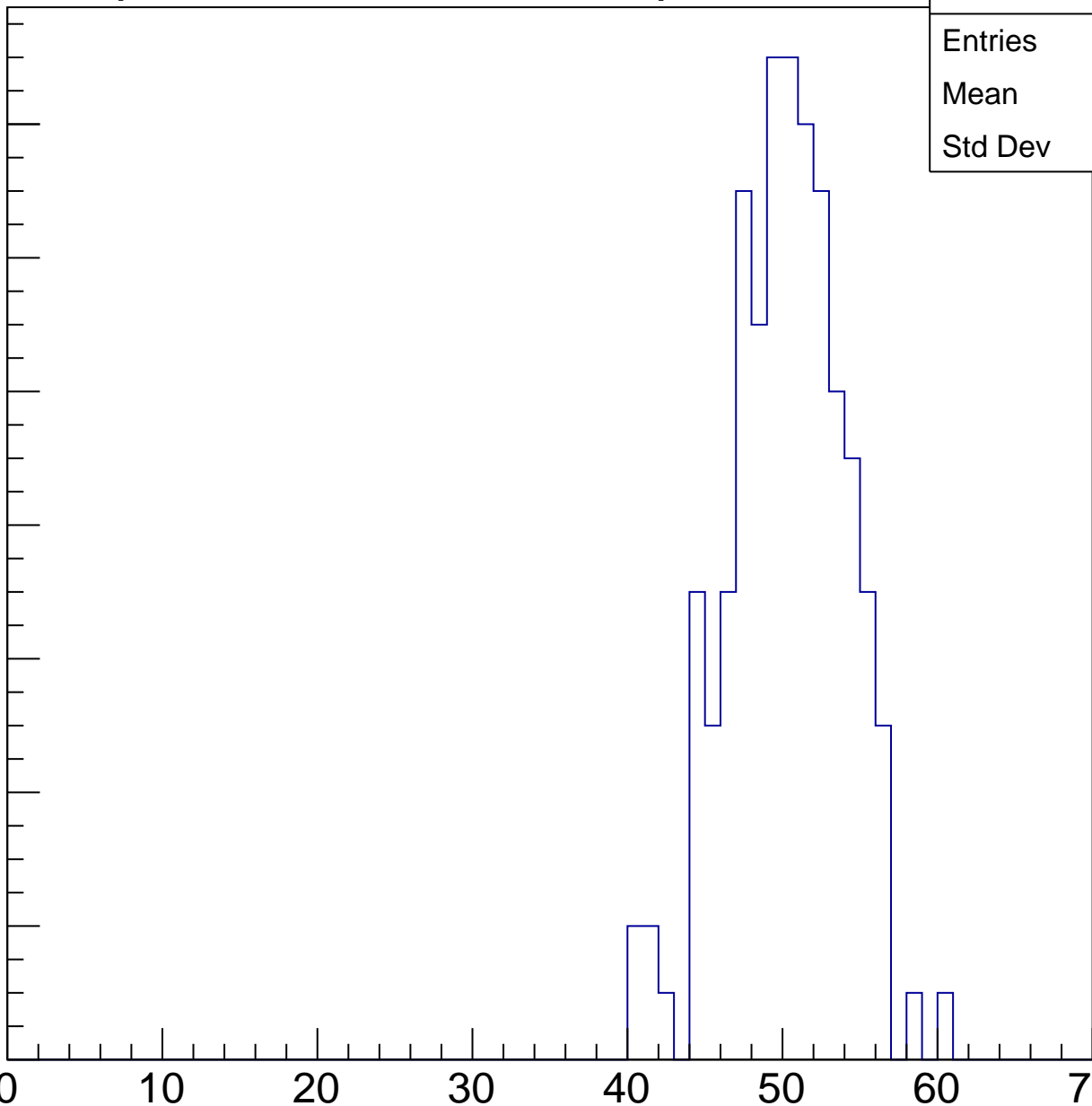
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	138
Mean	49.8
Std Dev	3.715

ampl



B0L101S, U18-ch53, adc4

calib_packv5_042523_0143.root, FC#1, port C1

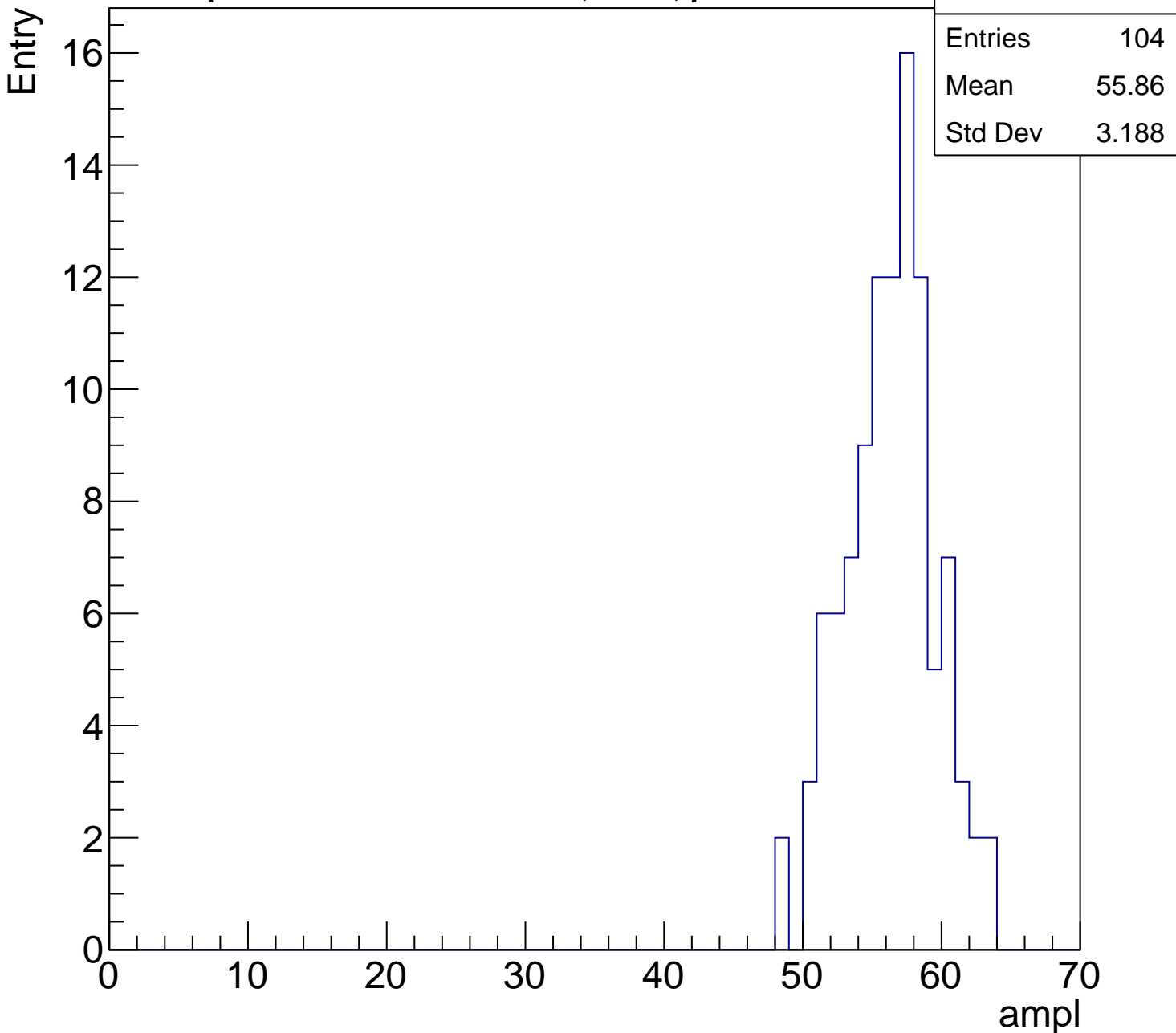
Entries	104
Mean	55.86
Std Dev	3.188

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch53, adc5

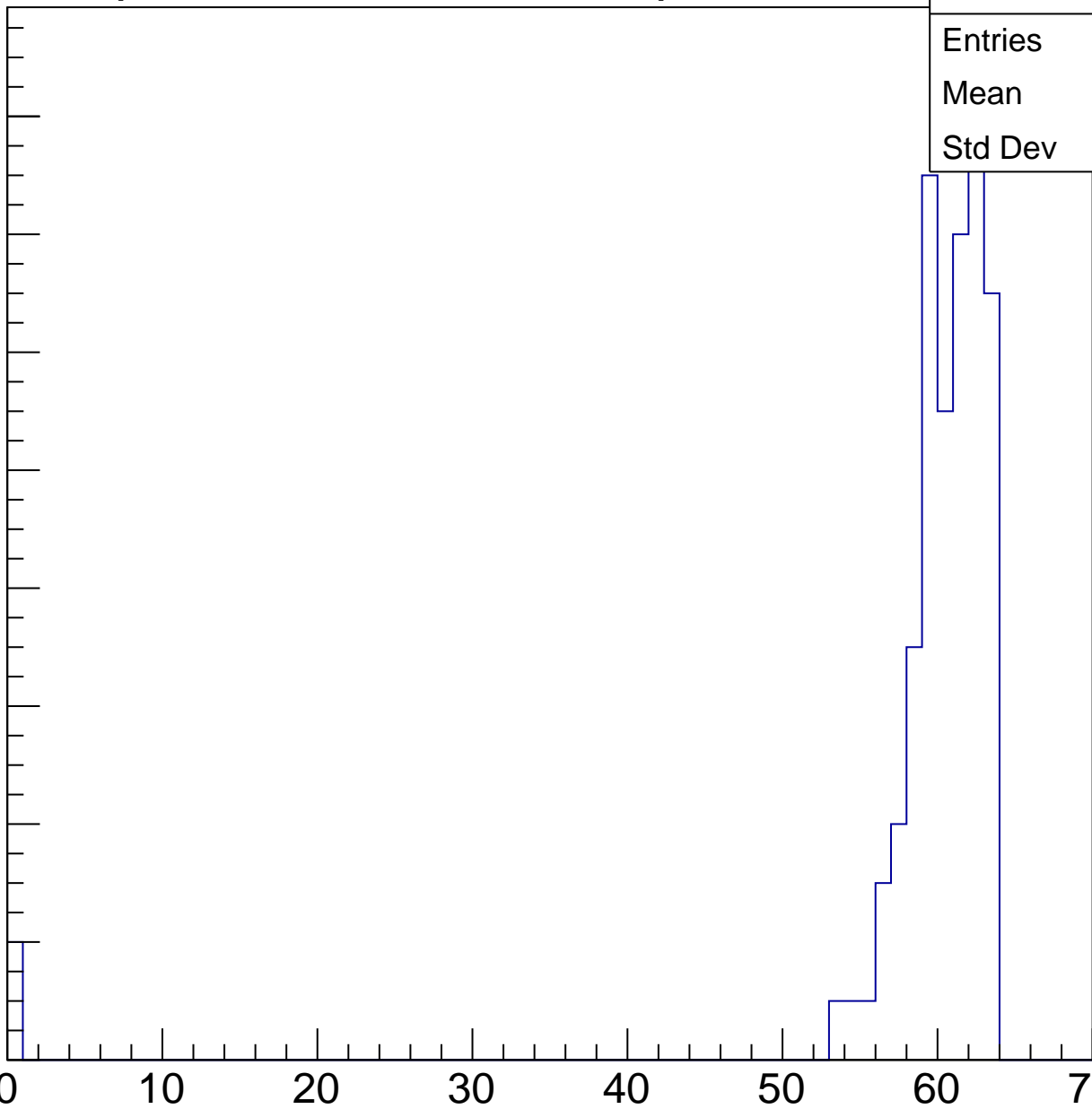
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	58.83
Std Dev	9.189

ampl

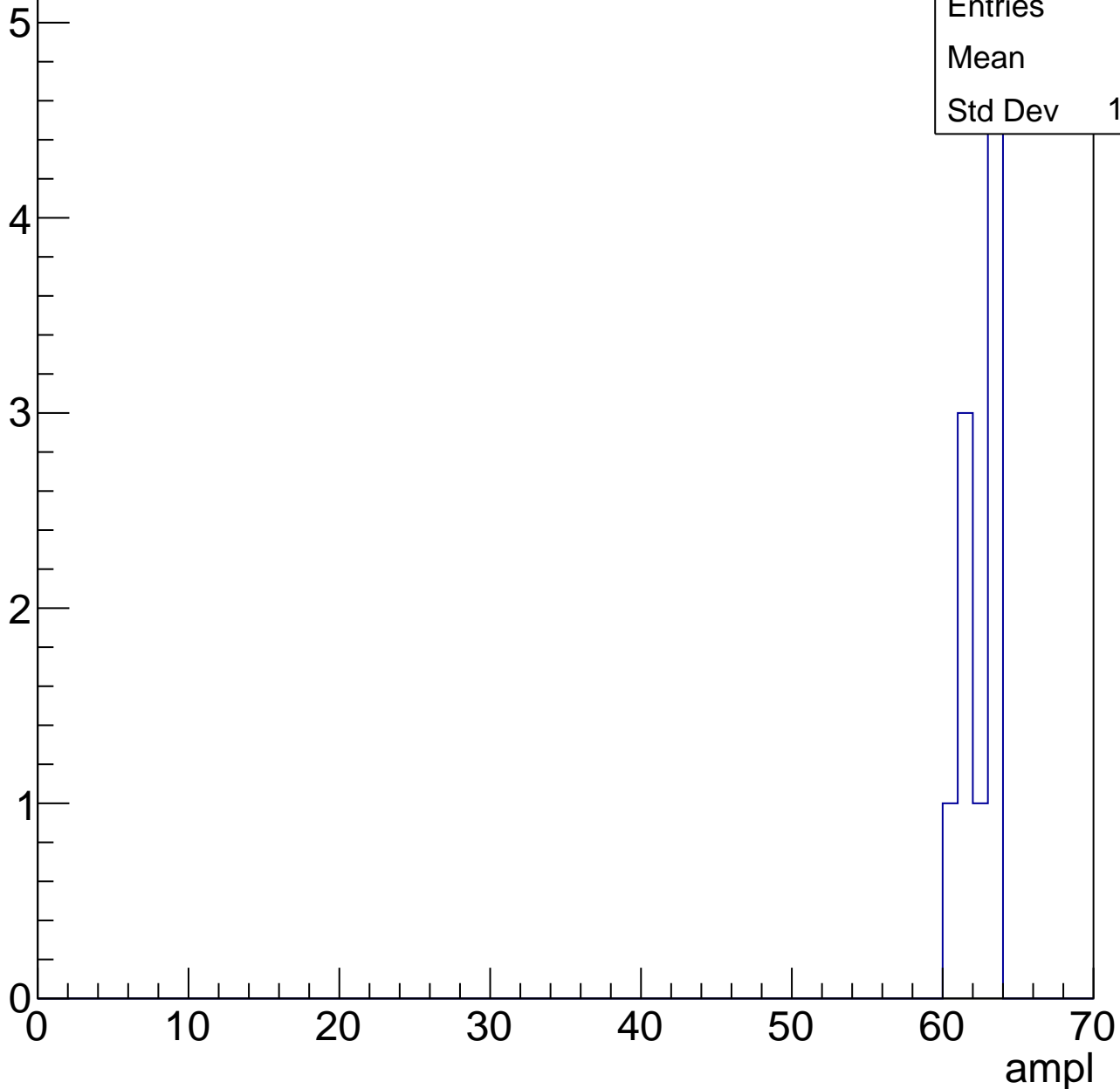


B0L101S, U18-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62
Std Dev	1.095



B0L101S, U18-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

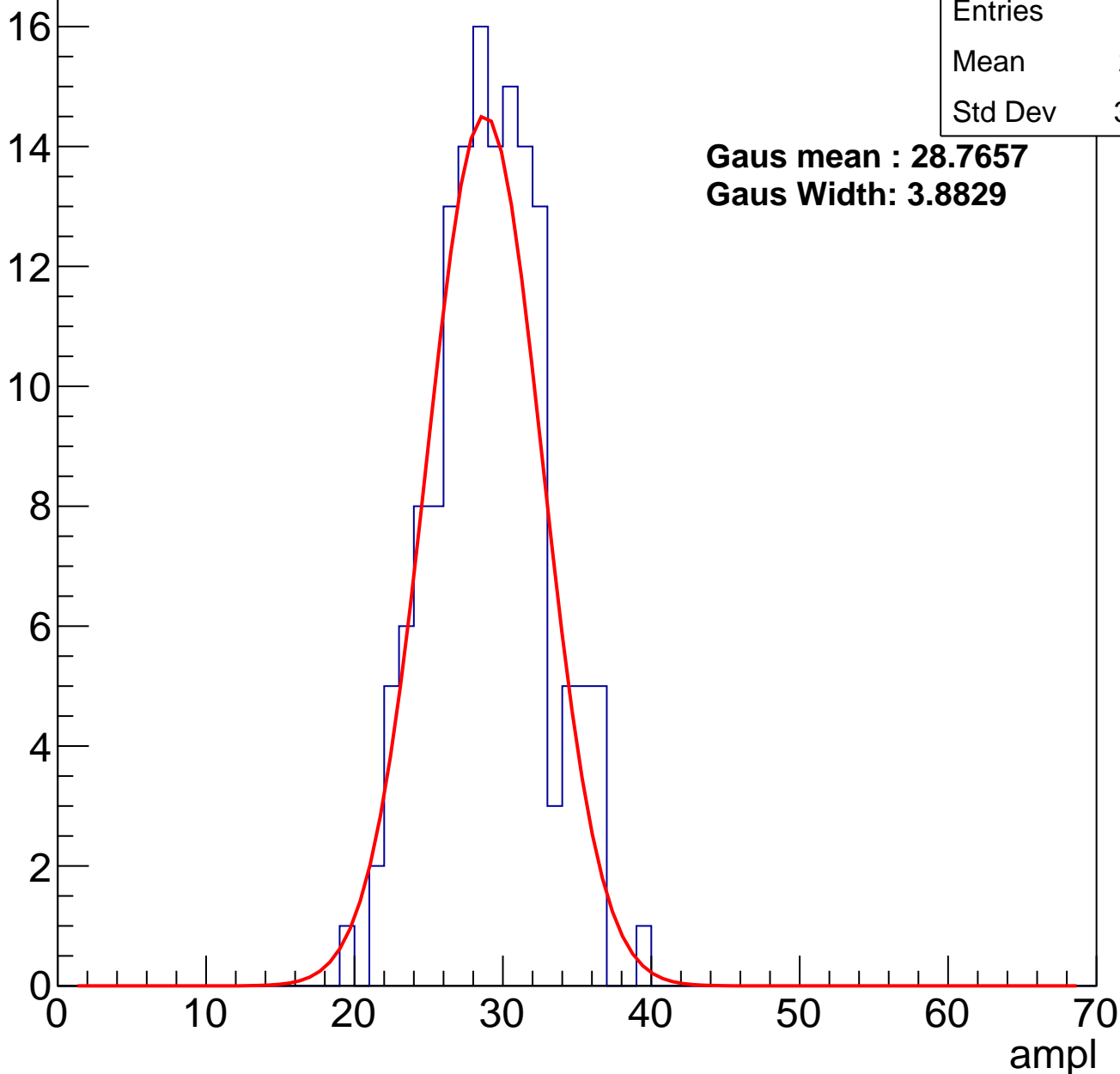


Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch54, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch54, adc1

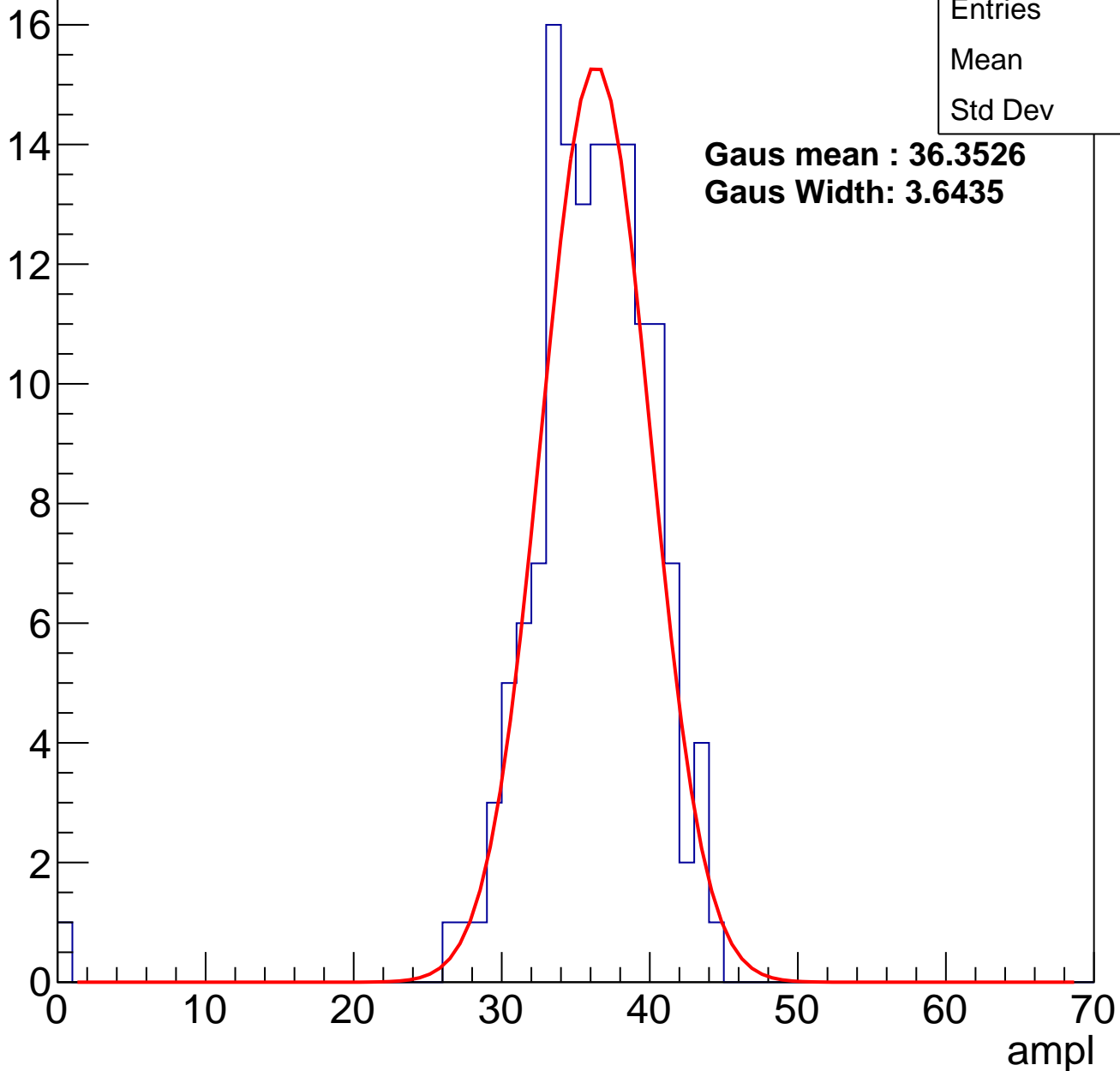
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	35.6
Std Dev	4.66

Gaus mean : 36.3526

Gaus Width: 3.6435

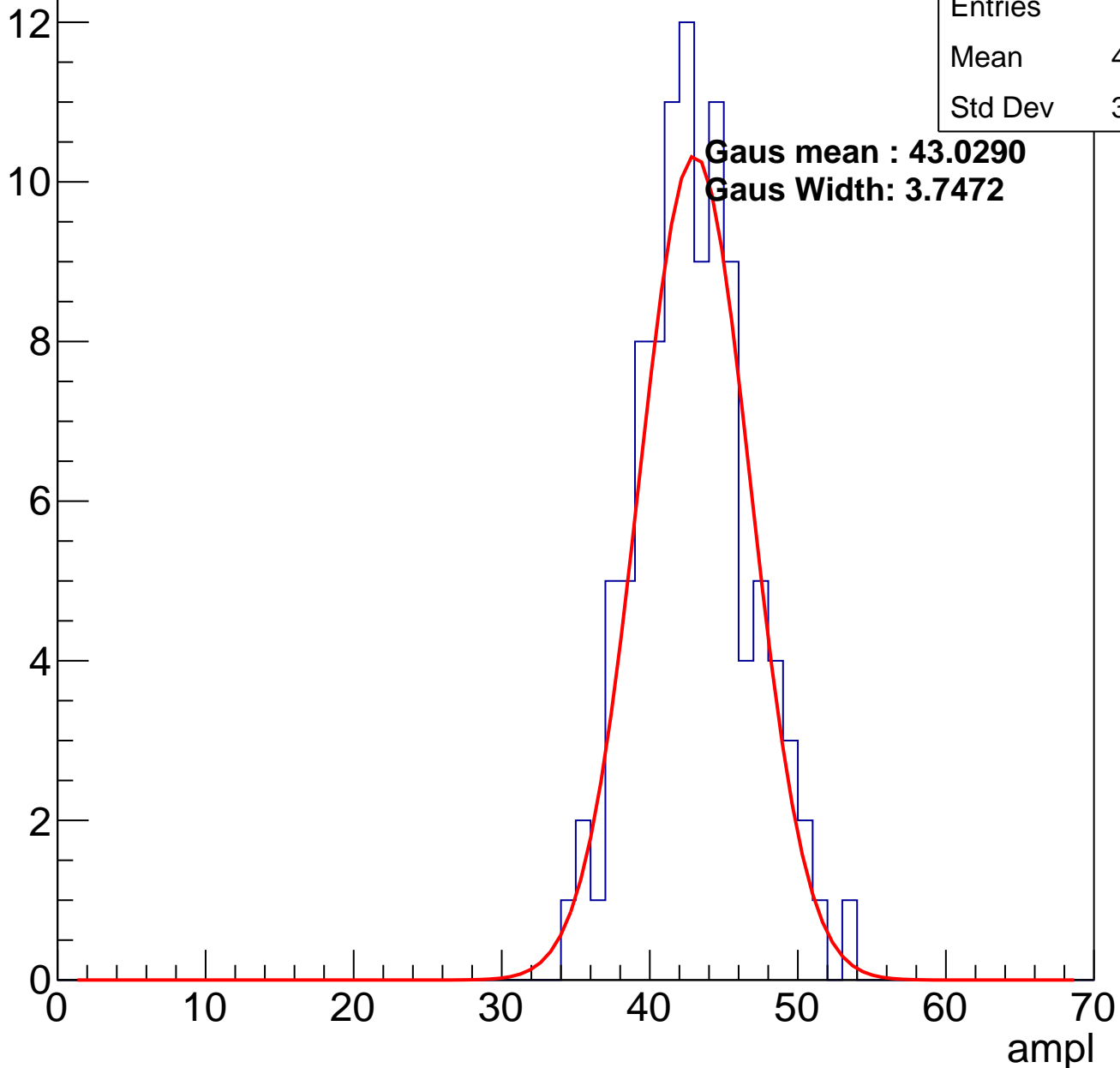
Entry



B0L101S, U18-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch54, adc3

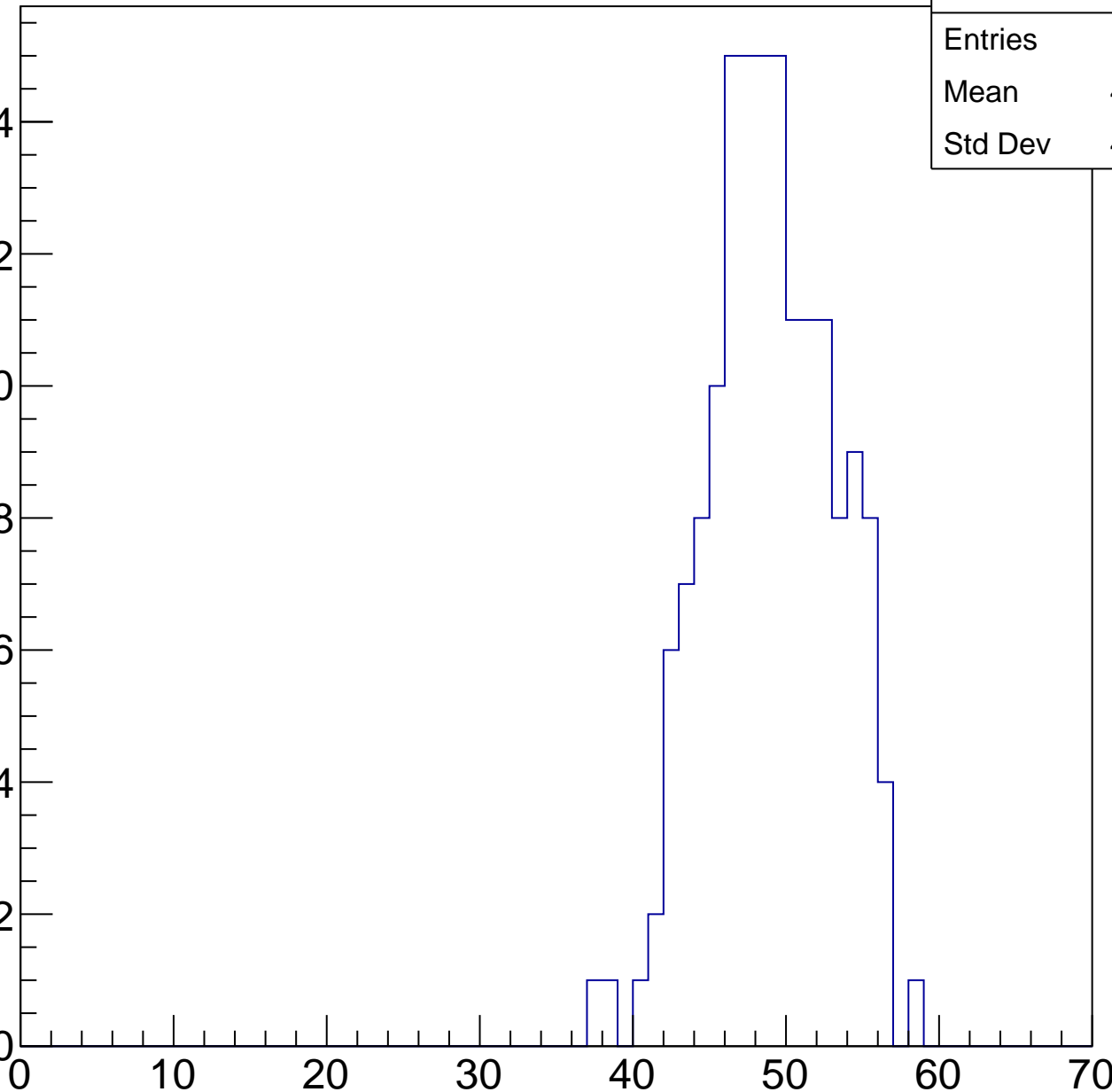
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	159
Mean	48.53
Std Dev	4.065

ampl

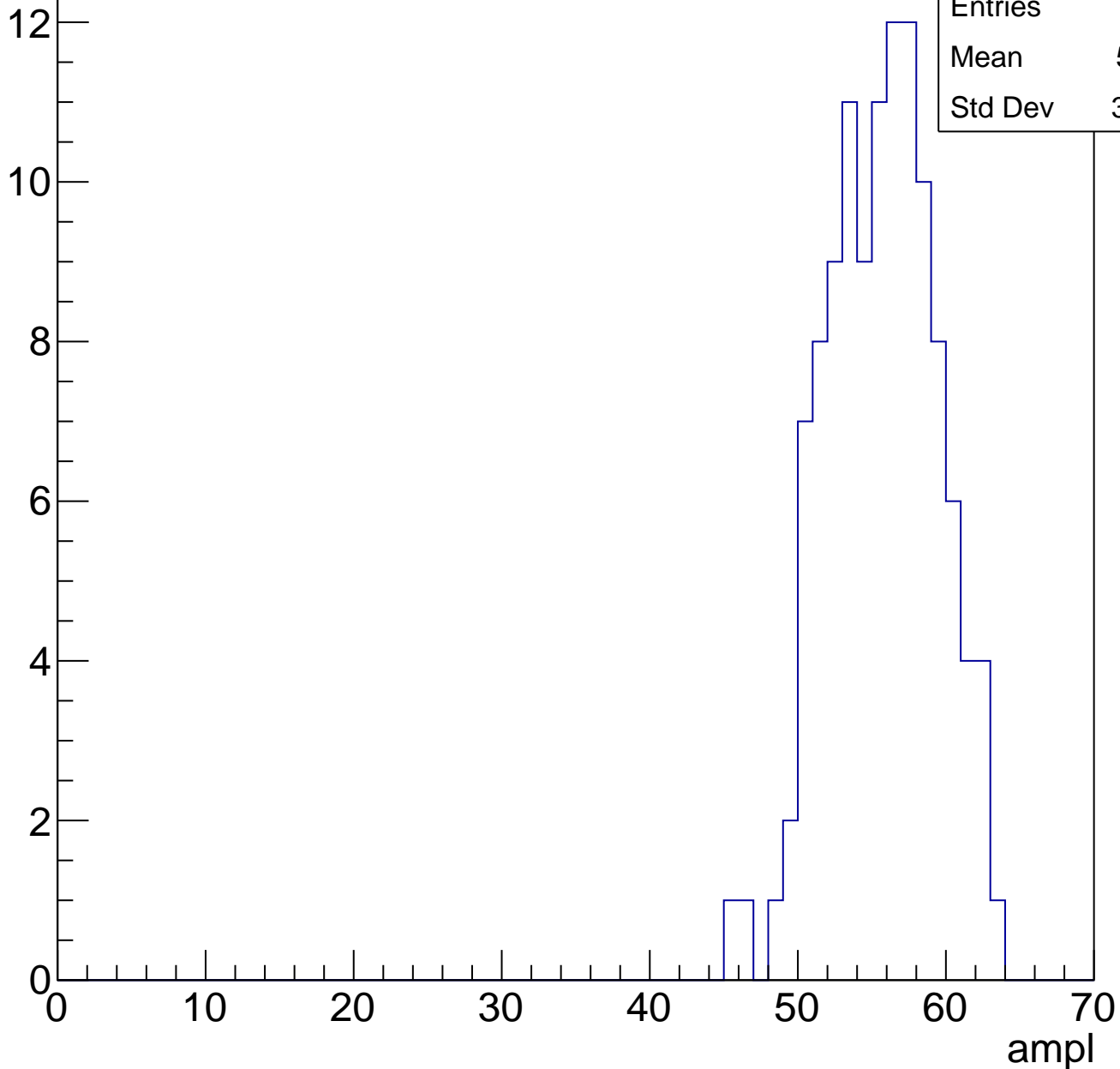


B0L101S, U18-ch54, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	55.21
Std Dev	3.658

Entry



B0L101S, U18-ch54, adc5

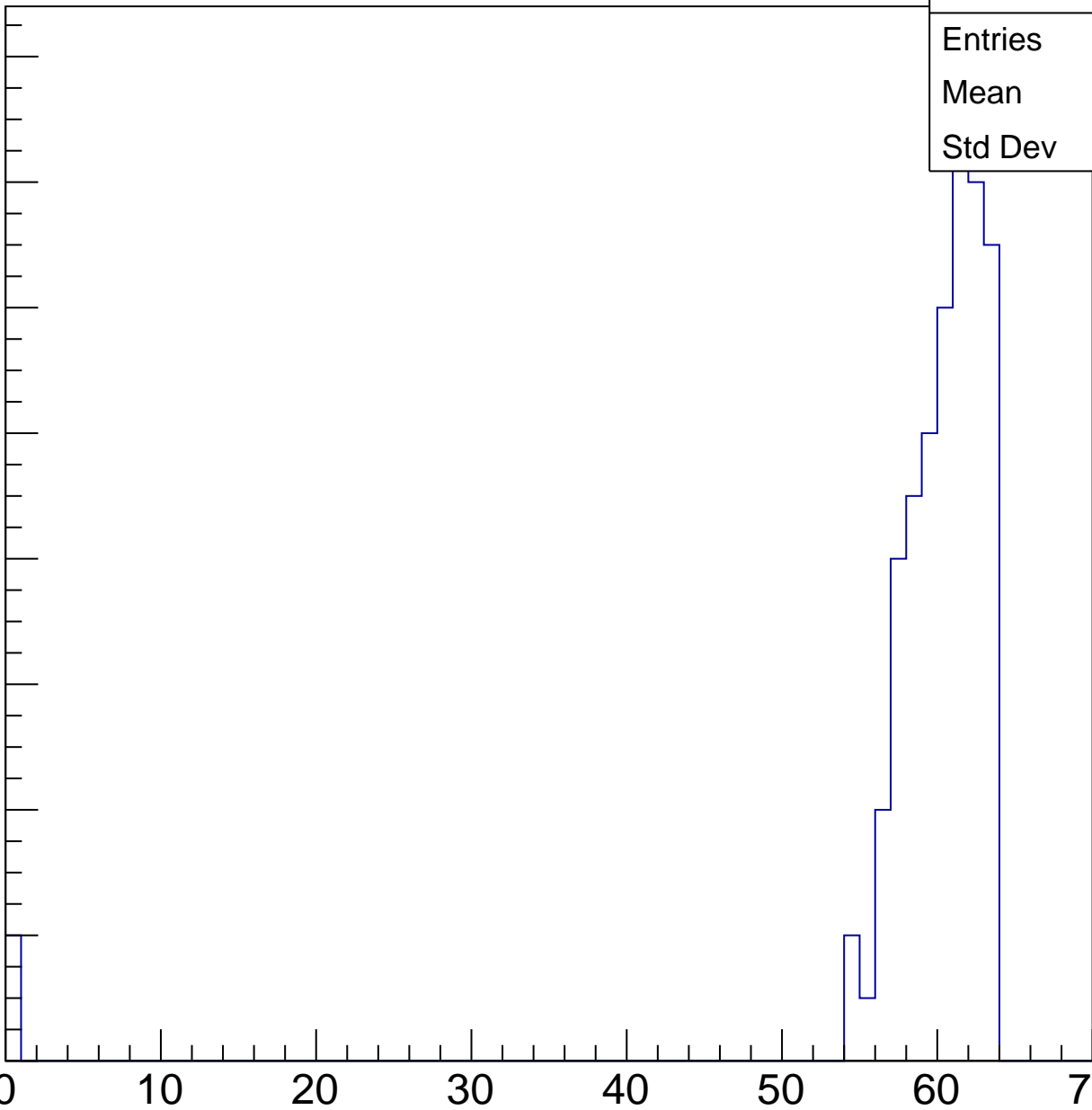
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.66
Std Dev	9.081

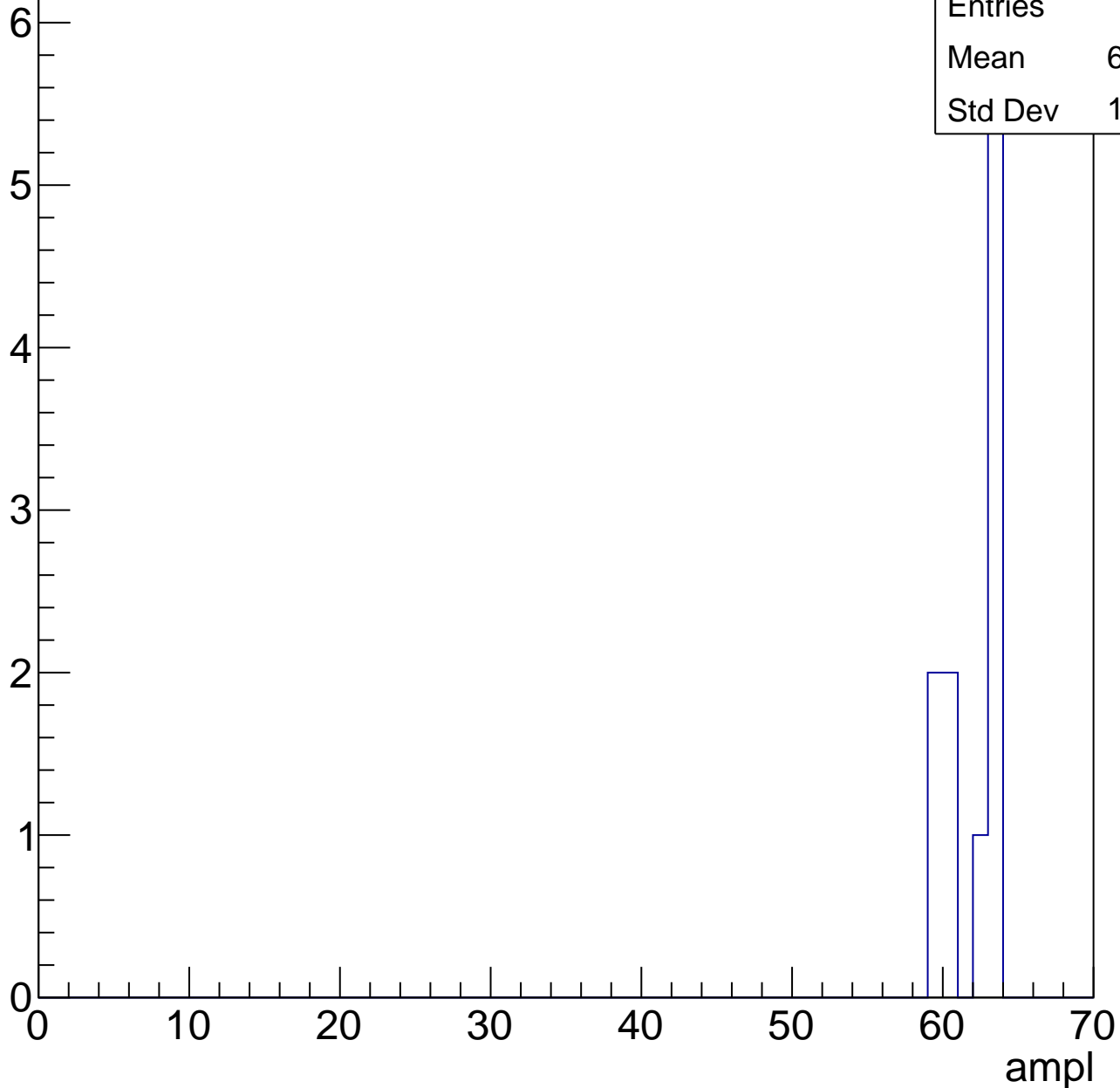
ampl



B0L101S, U18-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

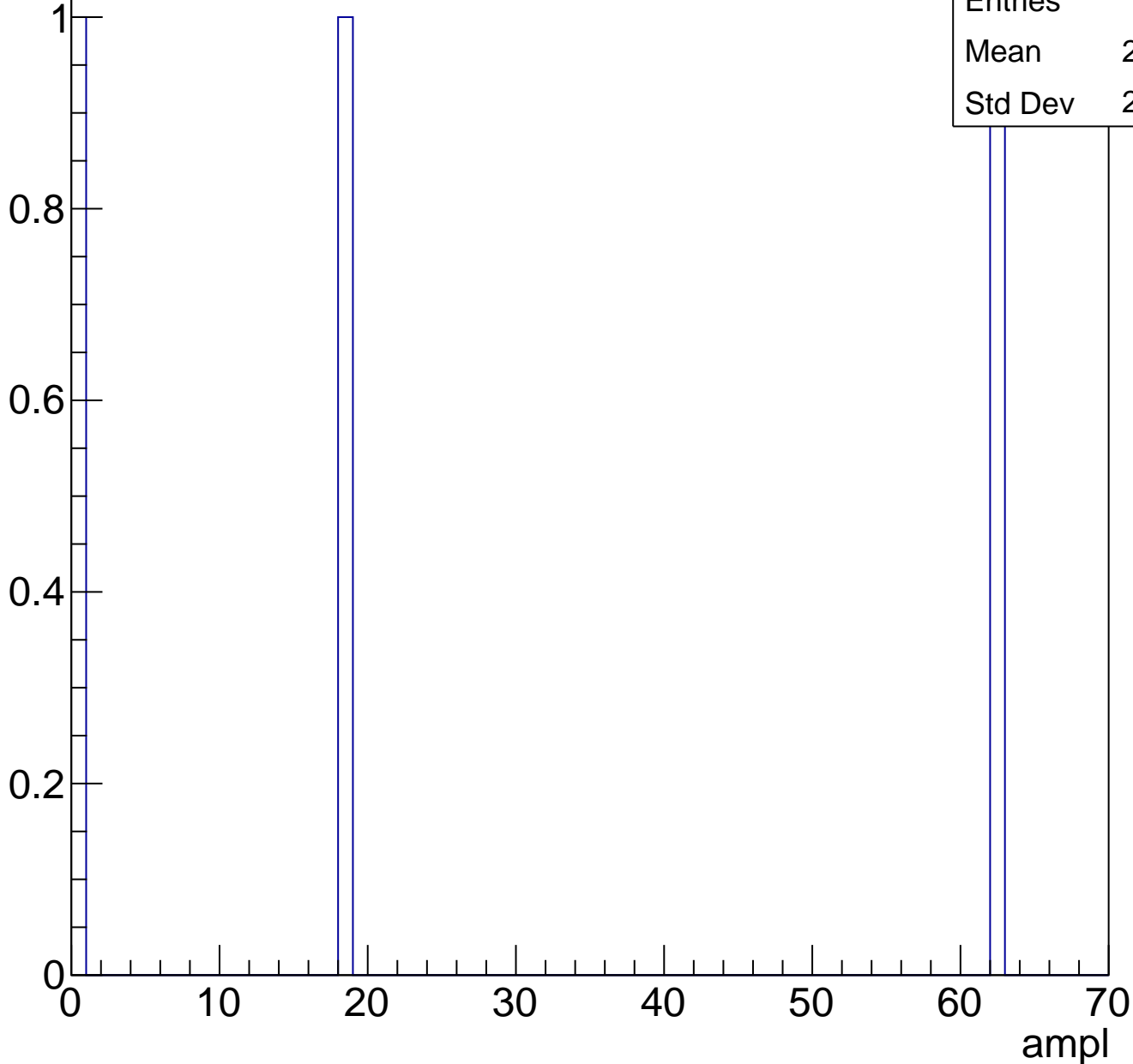
Entry



B0L101S, U18-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	26.67
Std Dev	26.04

B0L101S, U18-ch55, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	29.33
Std Dev	3.436

Gaus mean : 30.6511

Gaus Width: 3.1293

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch55, adc1

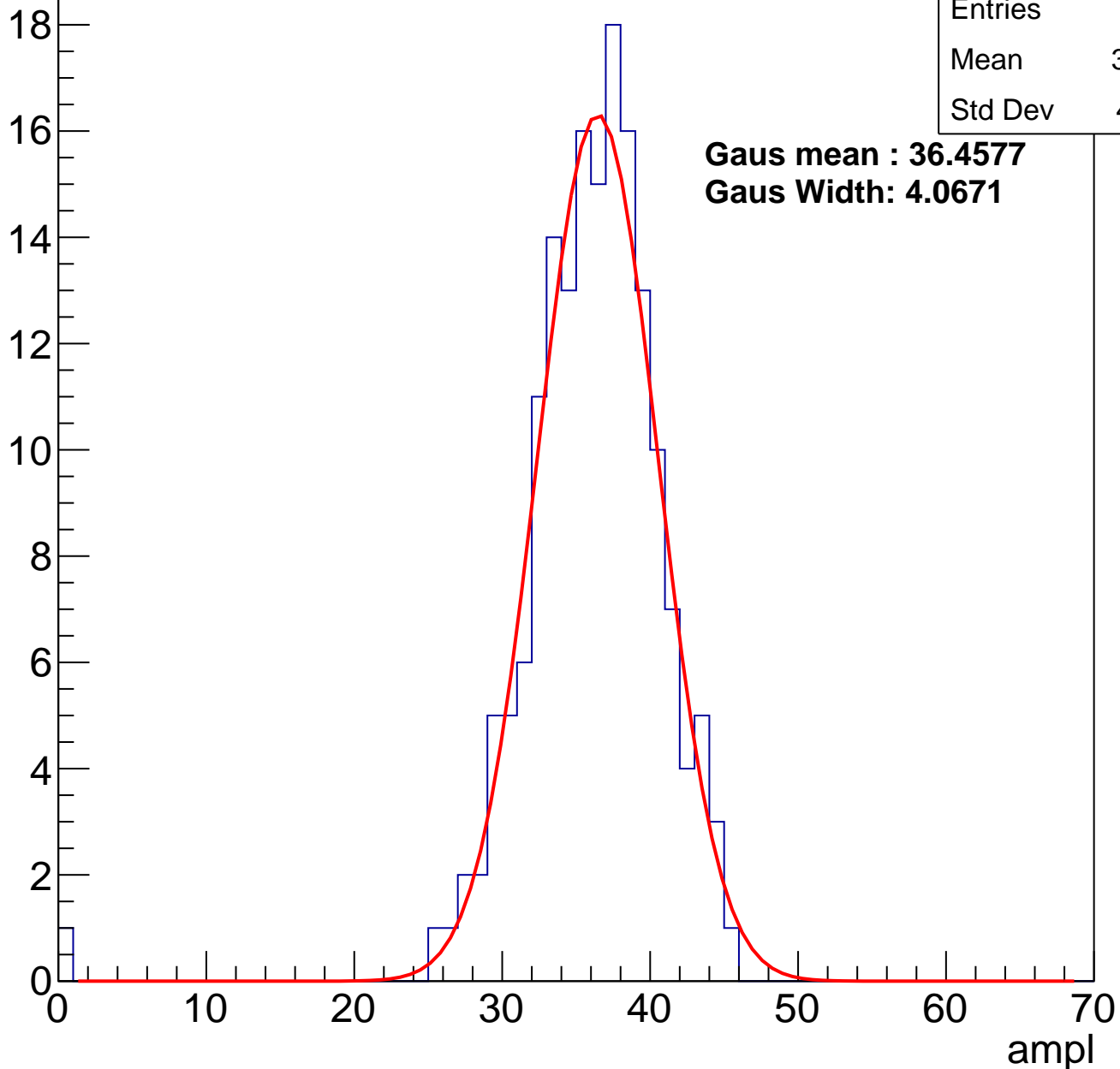
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	35.66
Std Dev	4.821

Gaus mean : 36.4577

Gaus Width: 4.0671

Entry



B0L101S, U18-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	100
Mean	42.65
Std Dev	2.851

Gaus mean : 43.0441

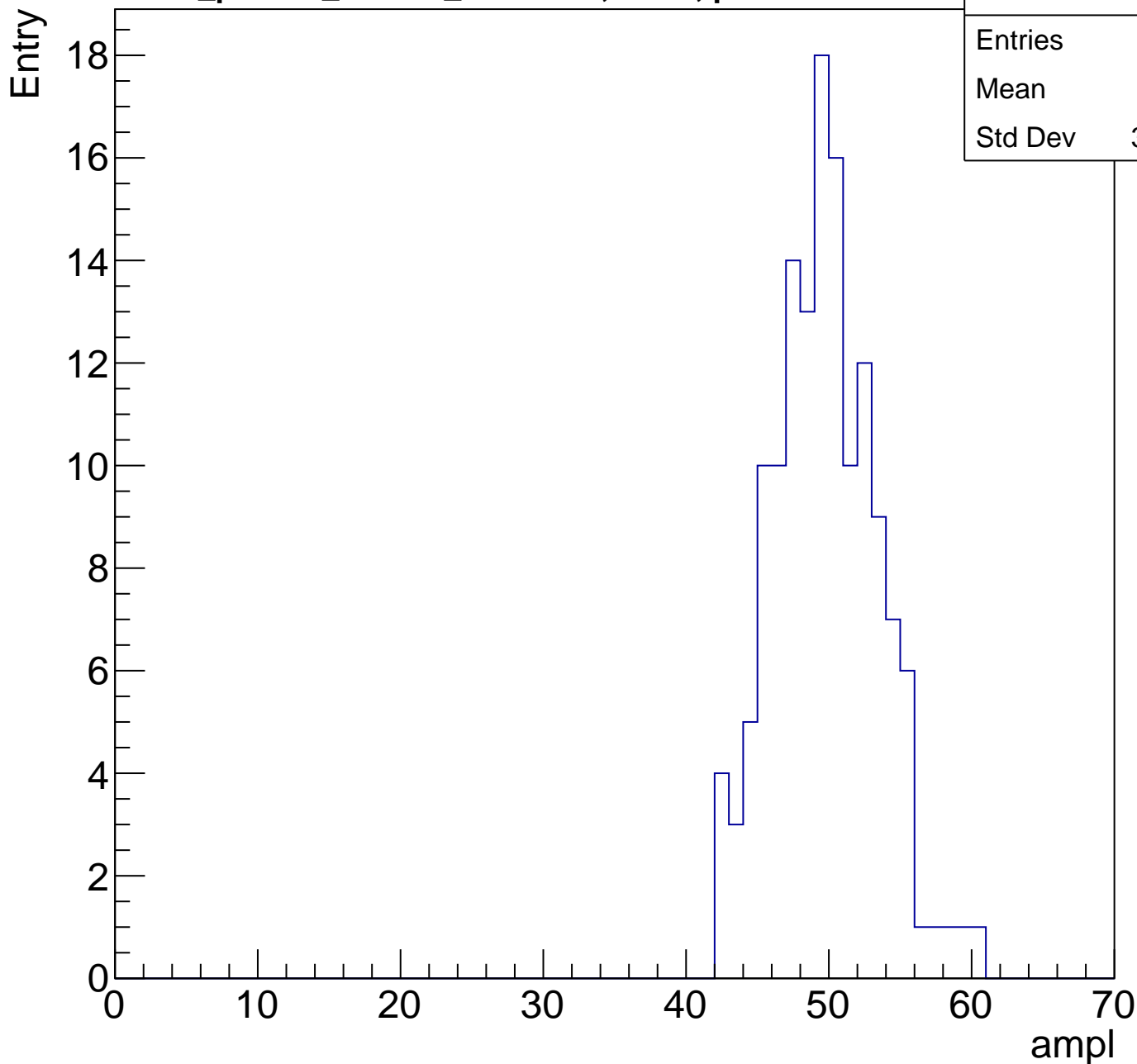
Gaus Width: 2.9591

0

B0L101S, U18-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	49.3
Std Dev	3.609



B0L101S, U18-ch55, adc4

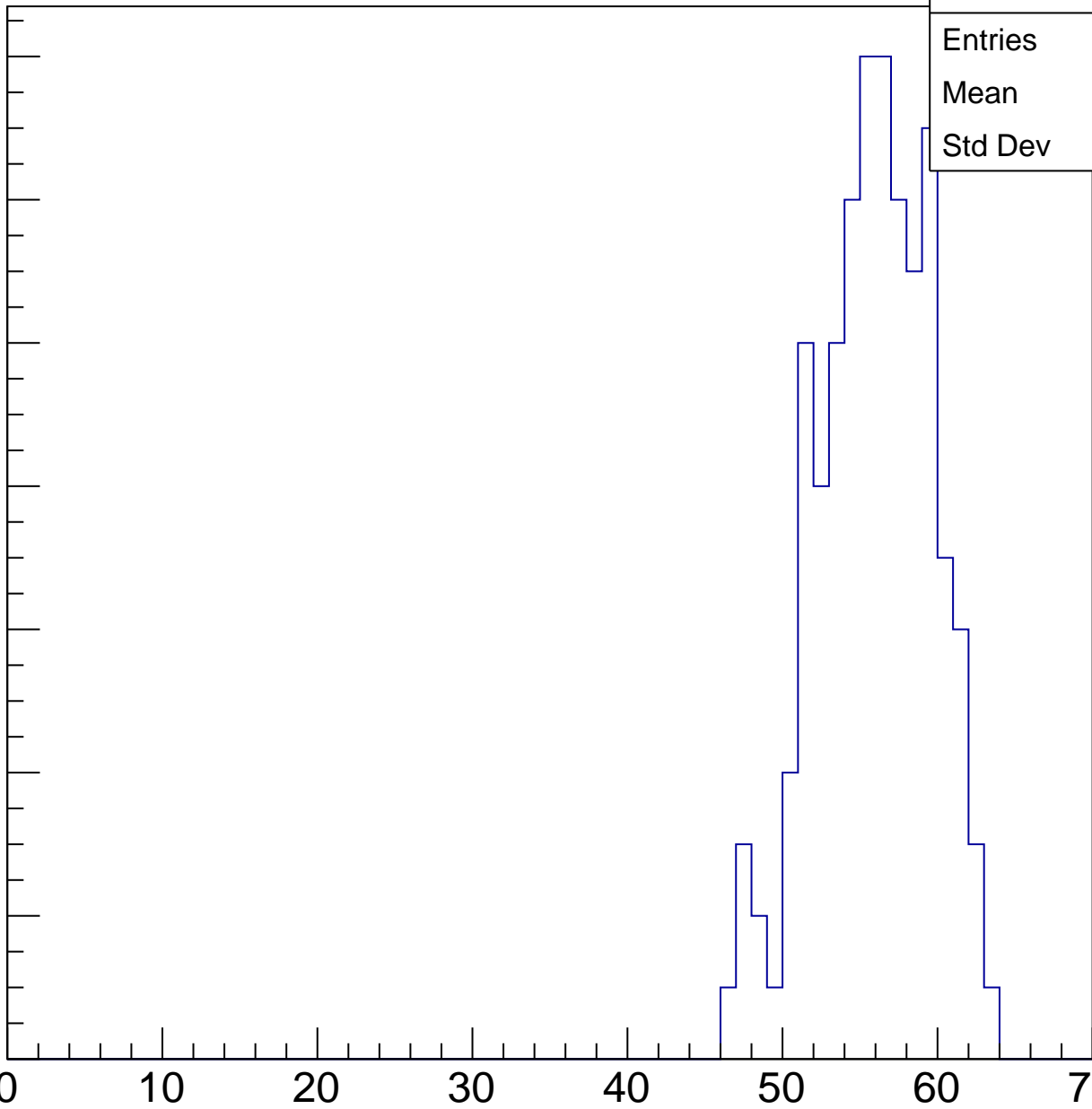
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	132
Mean	55.41
Std Dev	3.631

ampl

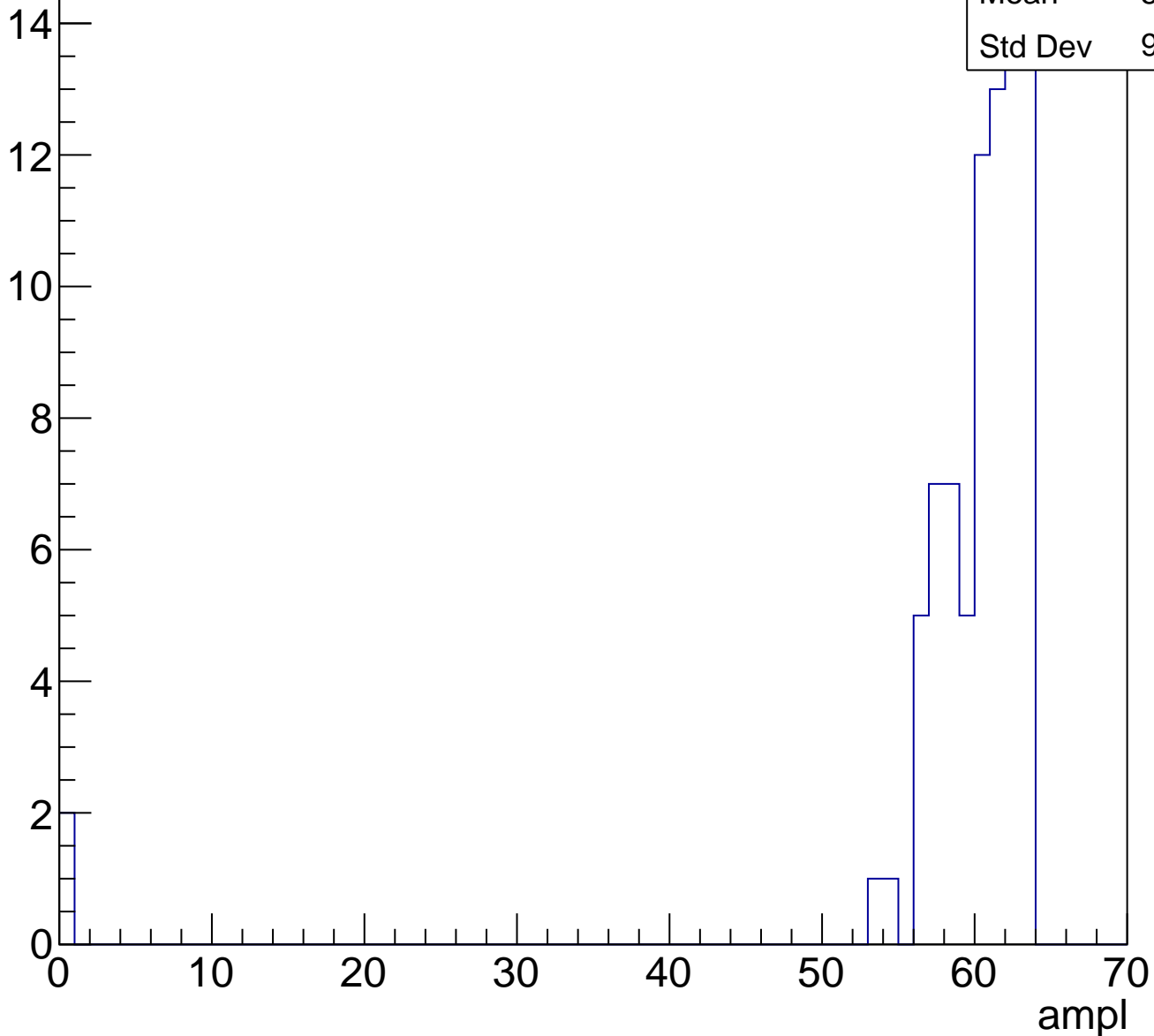


B0L101S, U18-ch55, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	82
Mean	58.68
Std Dev	9.575

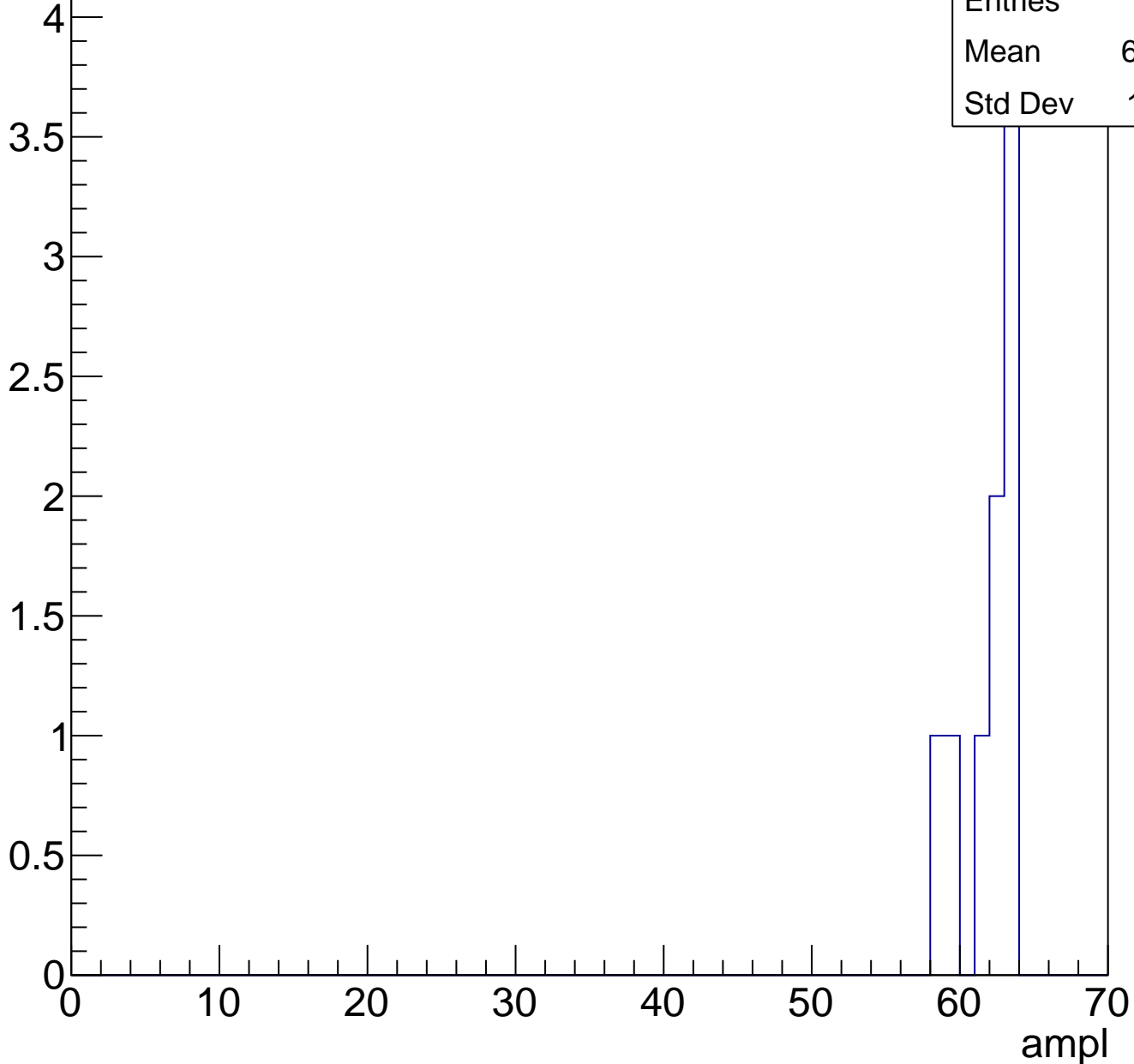
Entry



B0L101S, U18-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch56, adc0

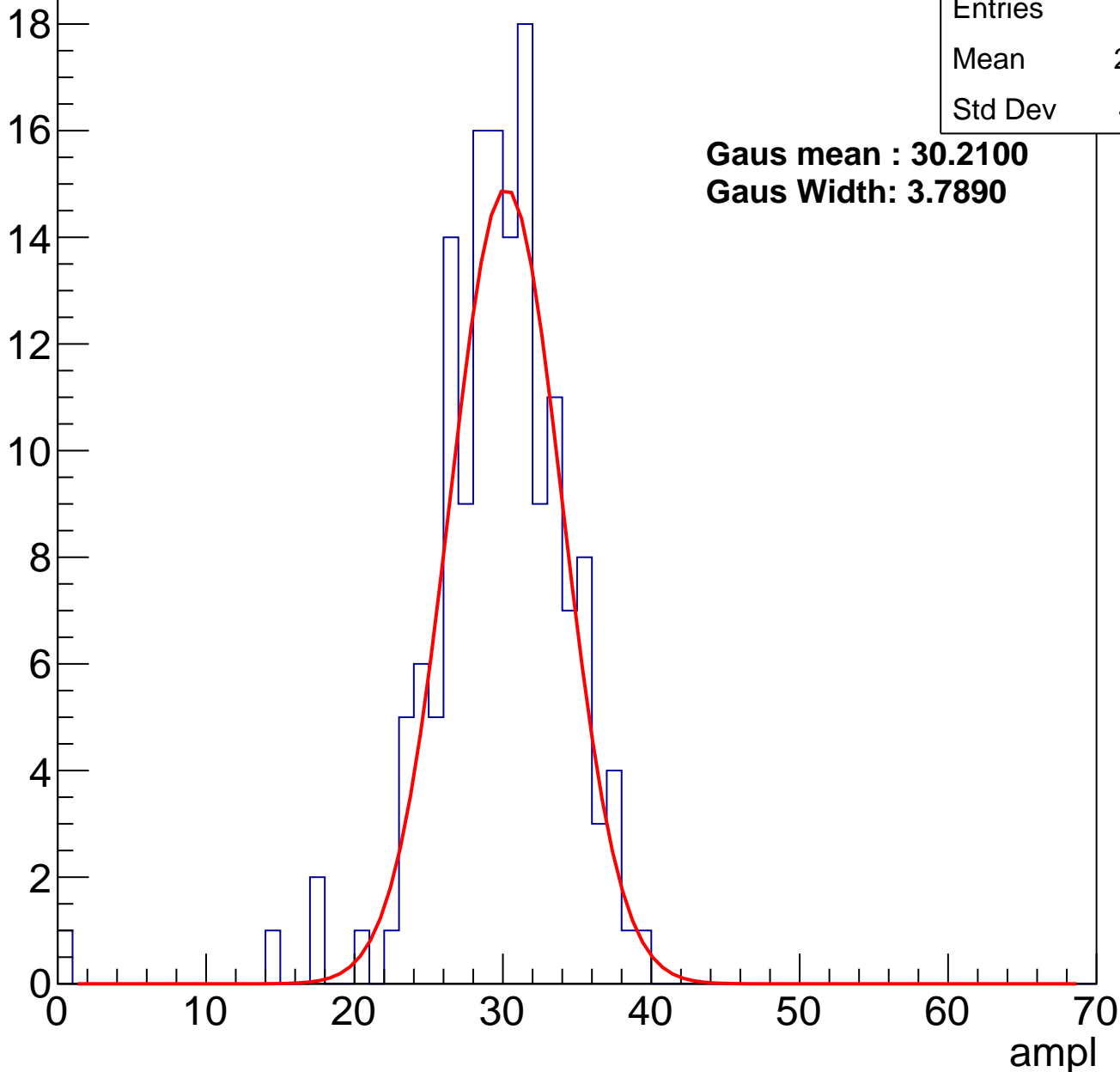
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	29.24
Std Dev	4.751

Gaus mean : 30.2100

Gaus Width: 3.7890

Entry



B0L101S, U18-ch56, adc1

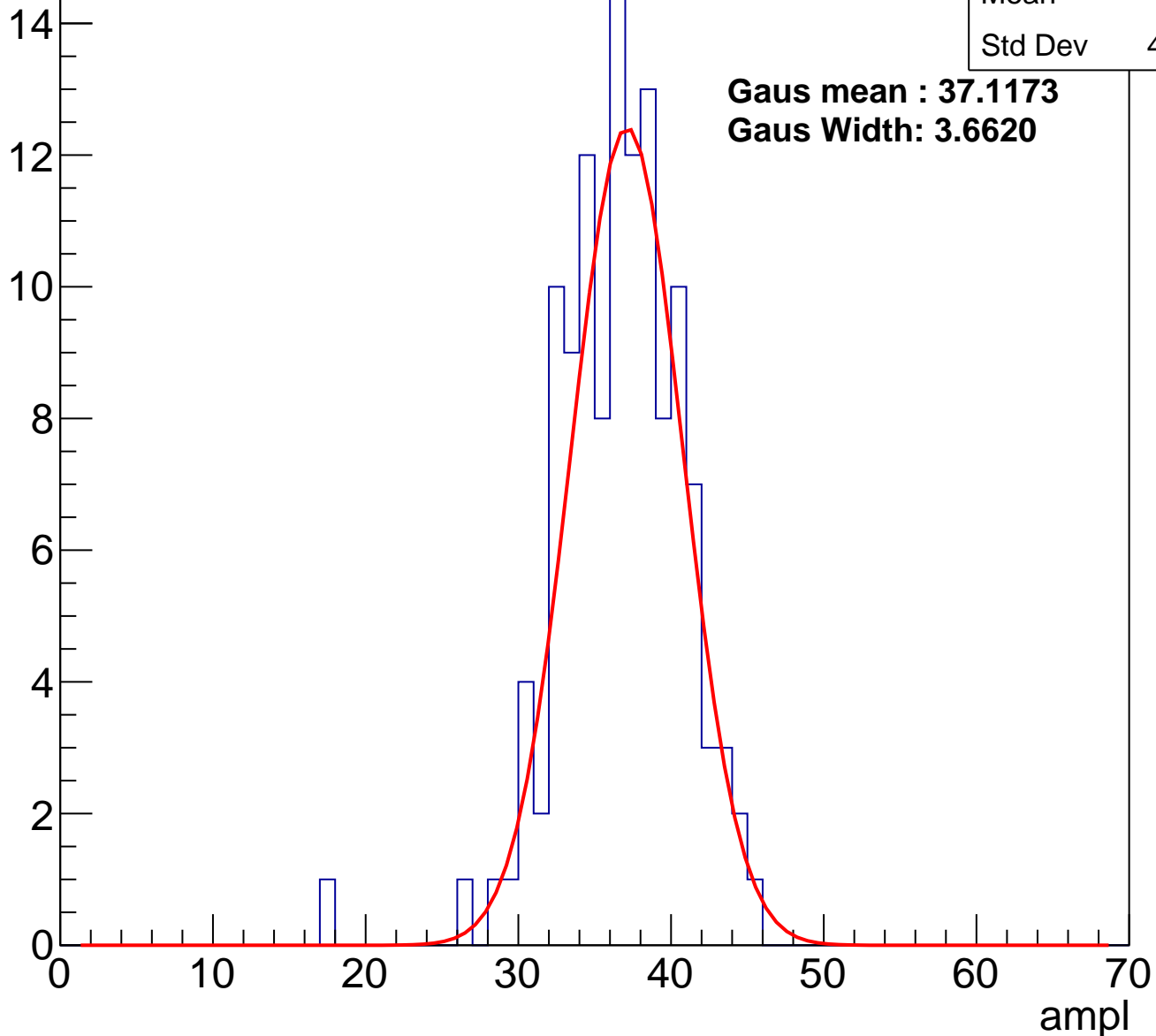
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.2
Std Dev	4.028

Gaus mean : 37.1173

Gaus Width: 3.6620

Entry



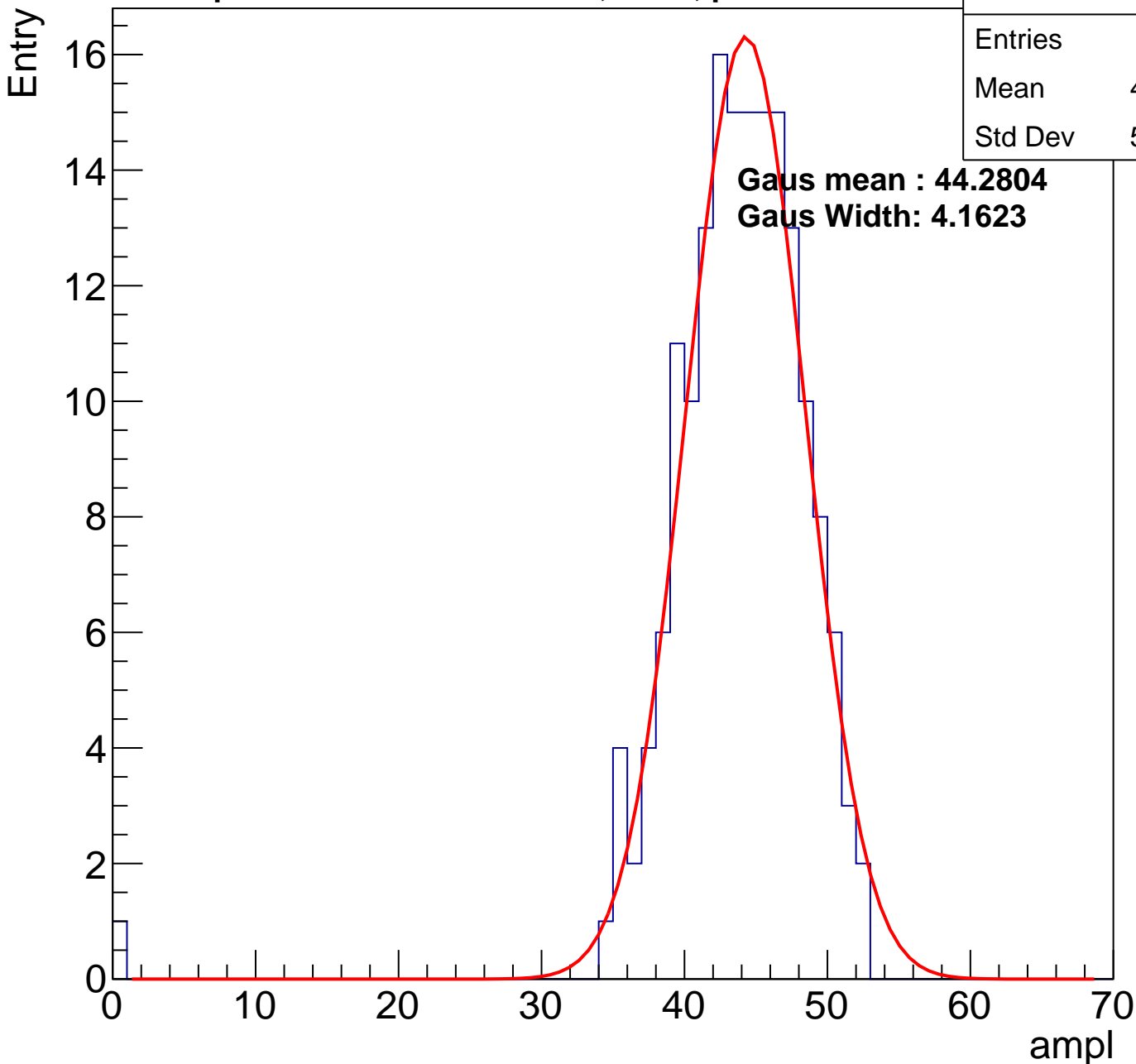
B0L101S, U18-ch56, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	43.33
Std Dev	5.144

Gaus mean : 44.2804

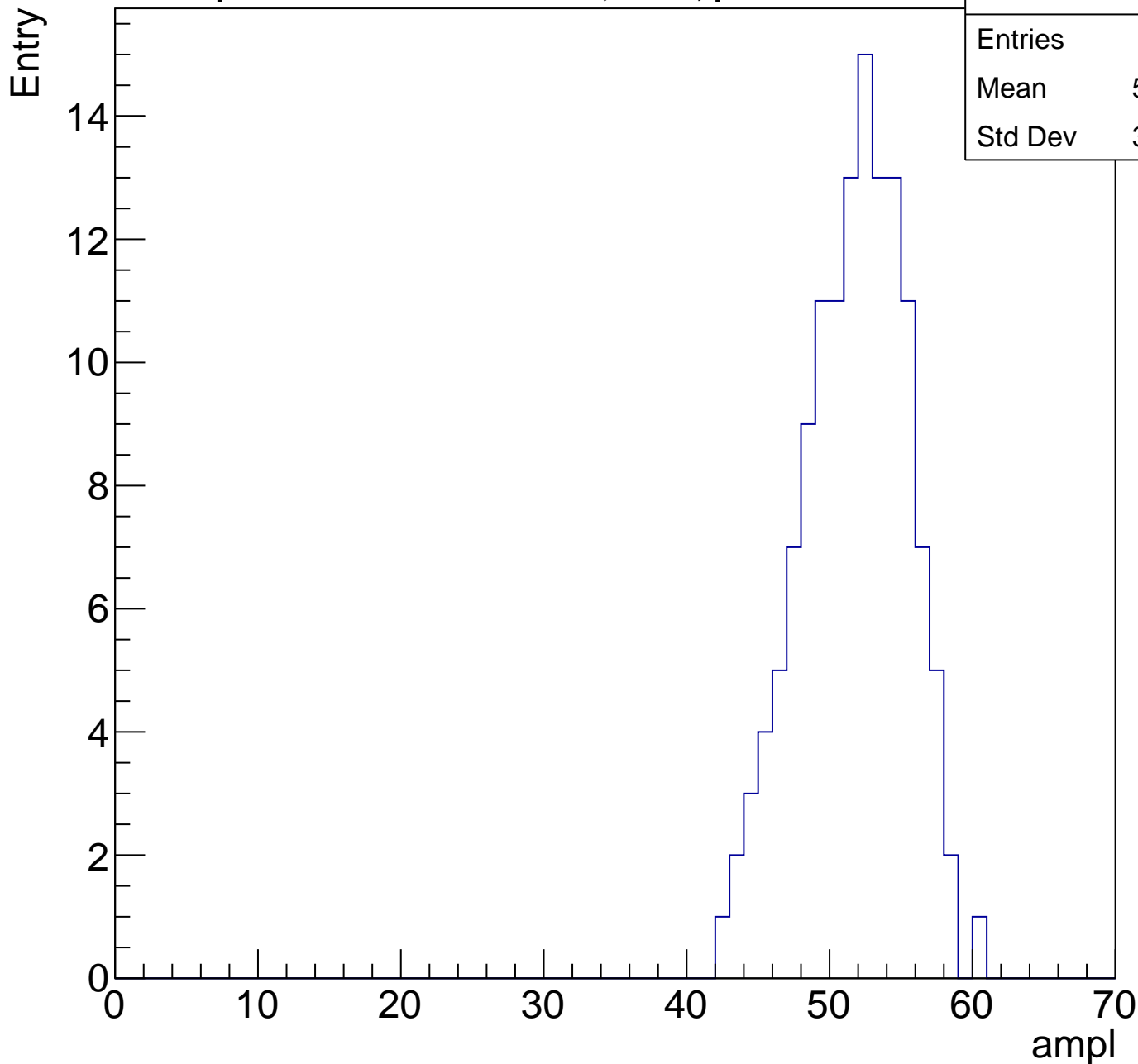
Gaus Width: 4.1623



B0L101S, U18-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

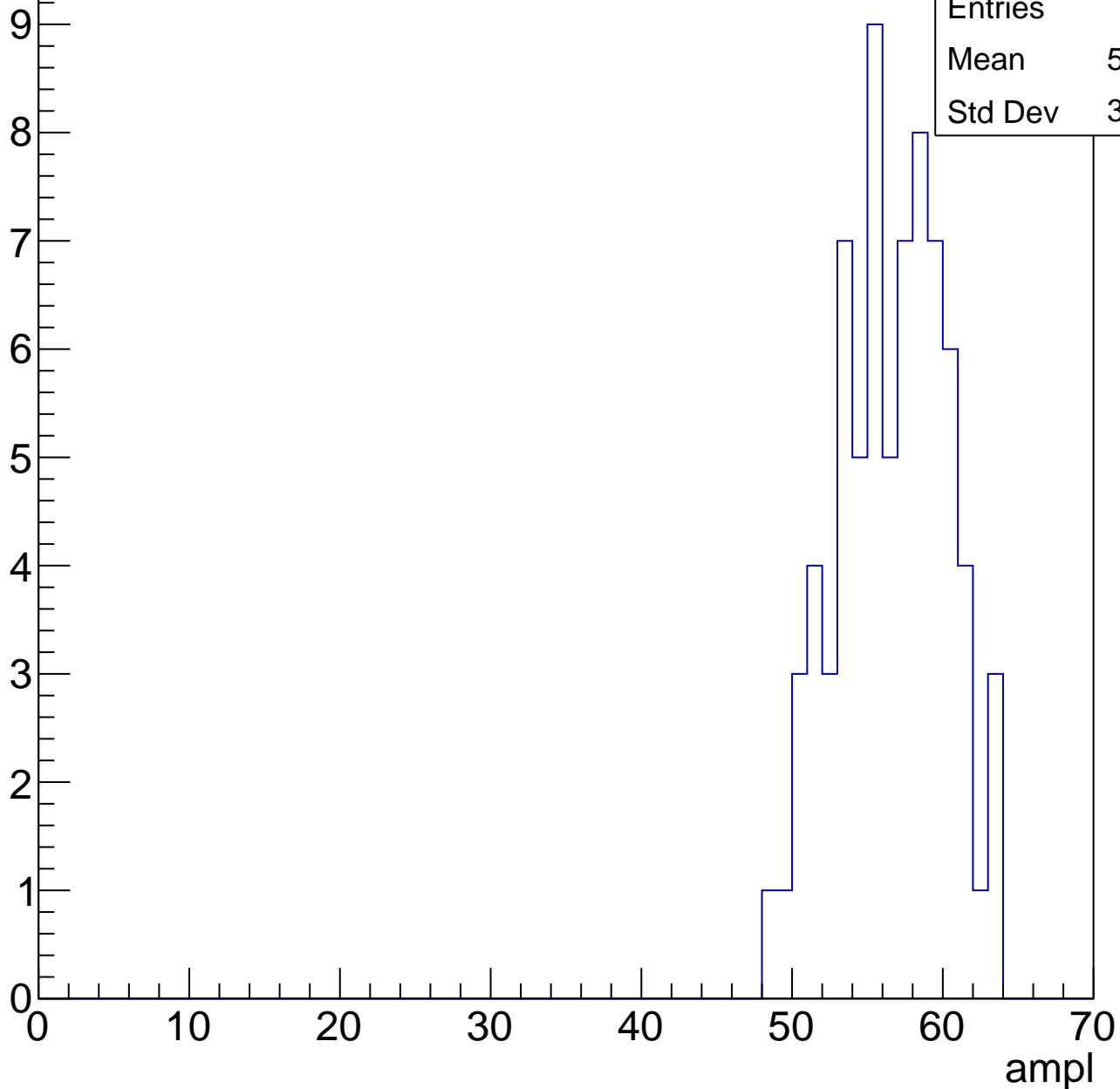
Entries	133
Mean	51.22
Std Dev	3.638



B0L101S, U18-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch56, adc5

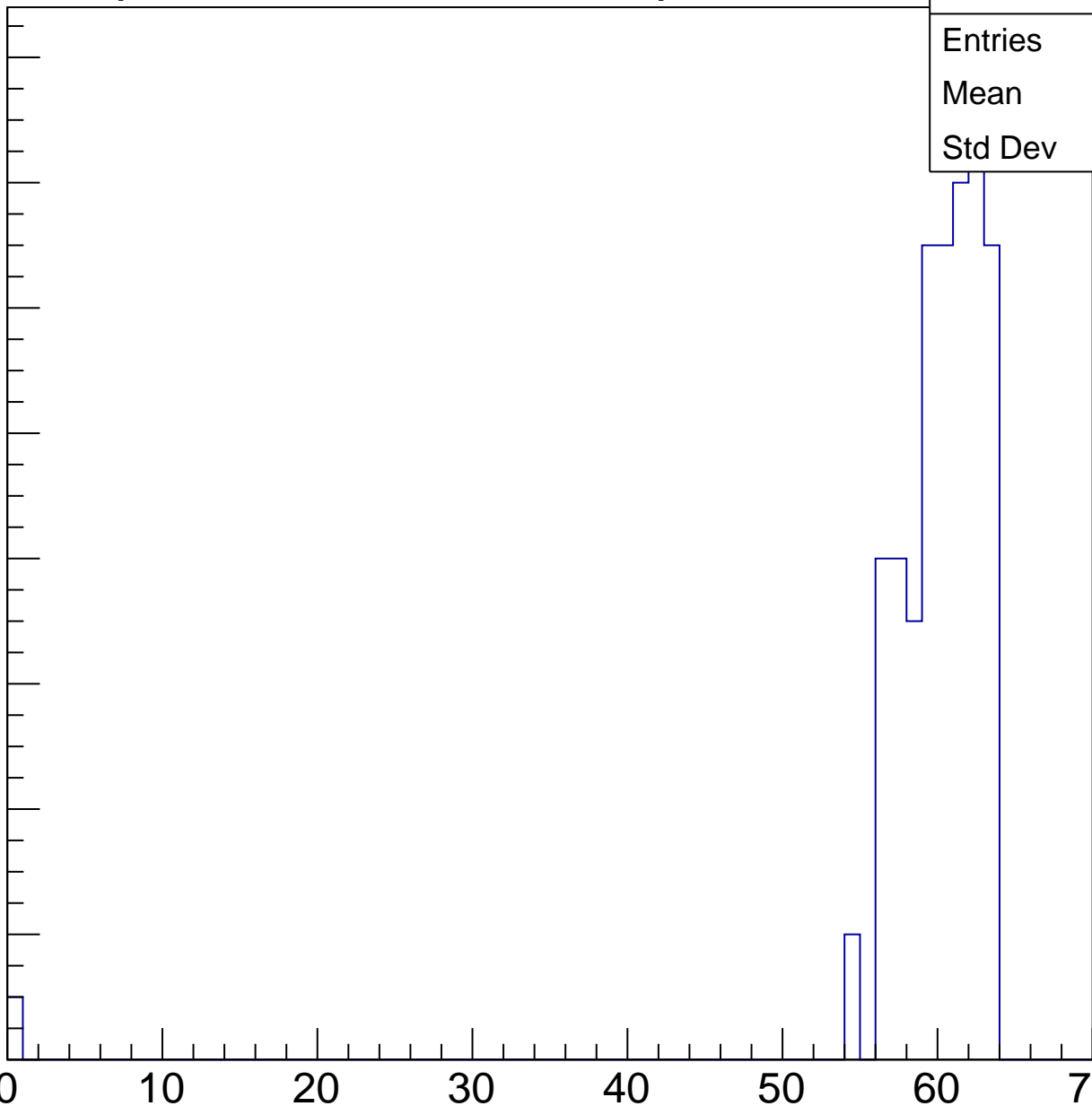
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.26
Std Dev	6.534

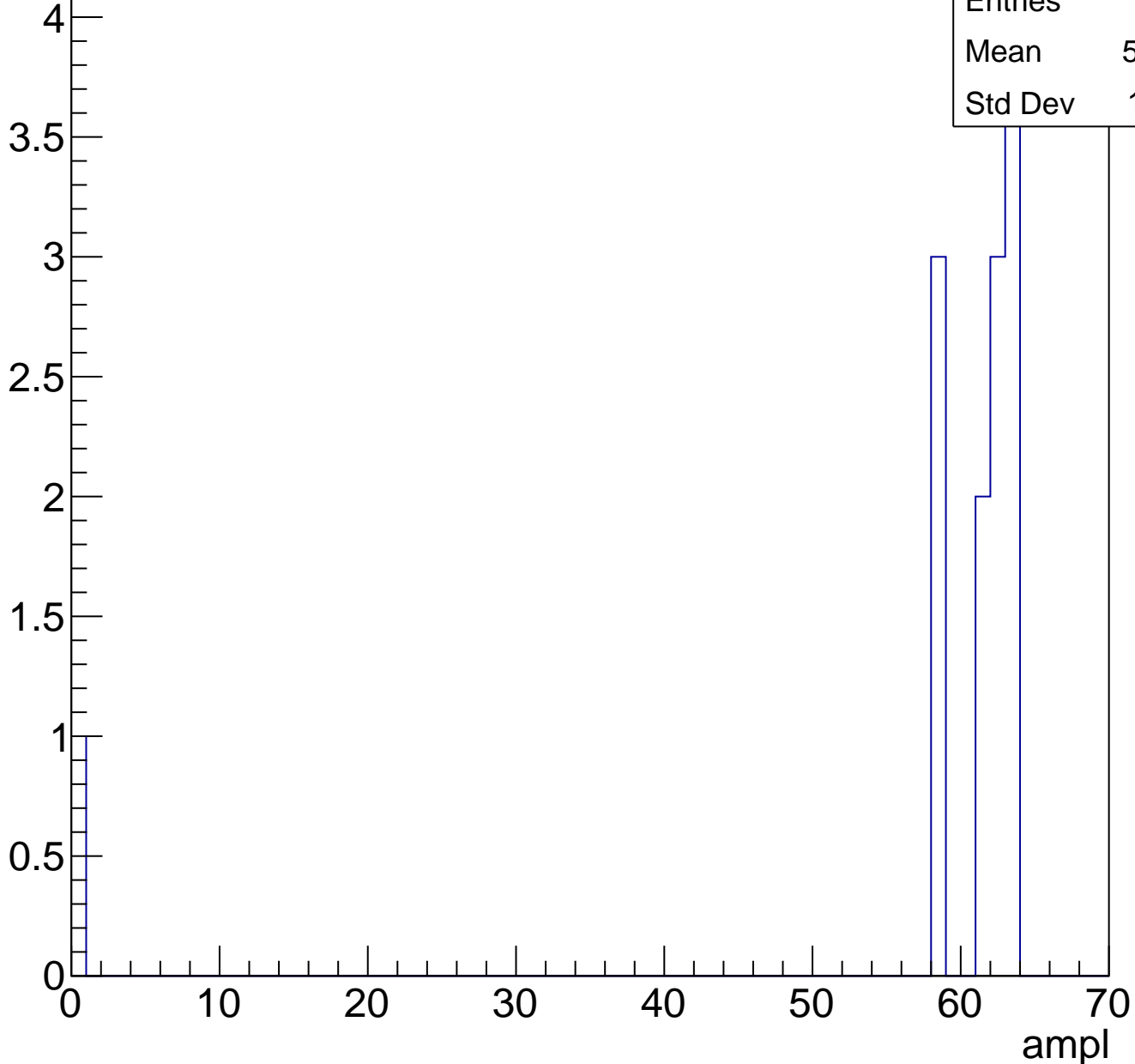
ampl



B0L101S, U18-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

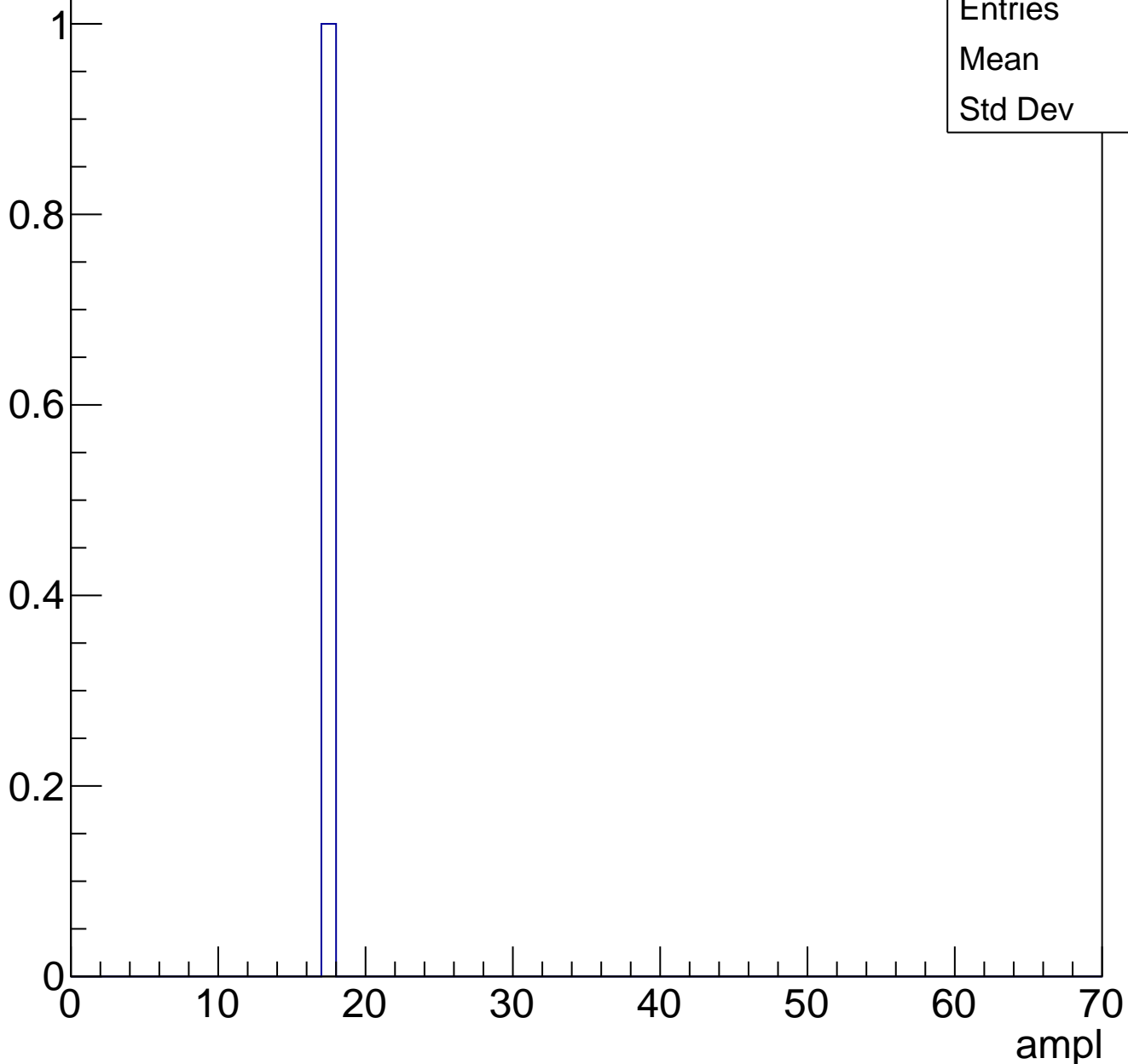
Entry



B0L101S, U18-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	17
Std Dev	0

B0L101S, U18-ch57, adc0

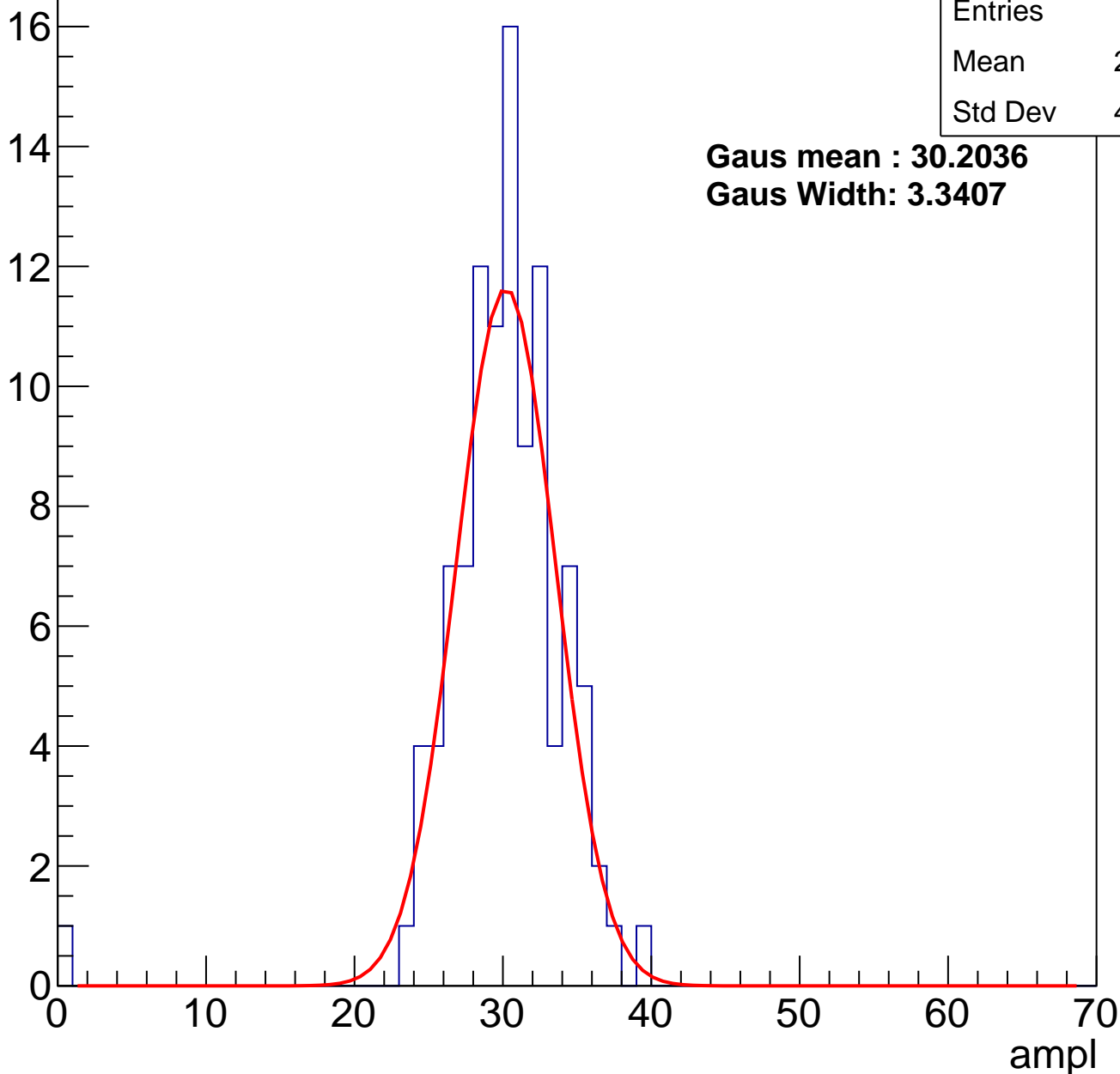
calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	29.62
Std Dev	4.324

Gaus mean : 30.2036

Gaus Width: 3.3407

Entry

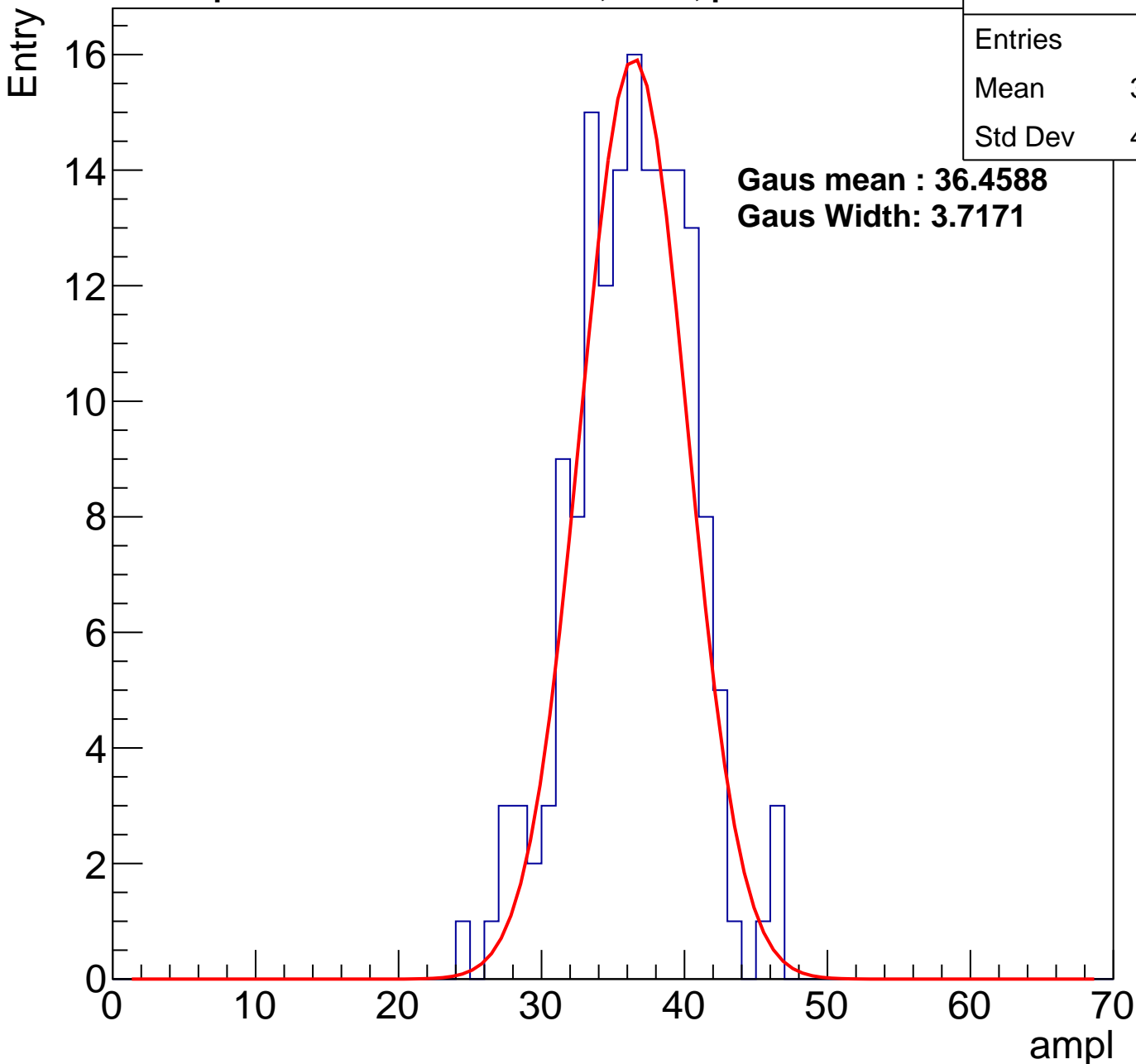


B0L101S, U18-ch57, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	35.92
Std Dev	4.068

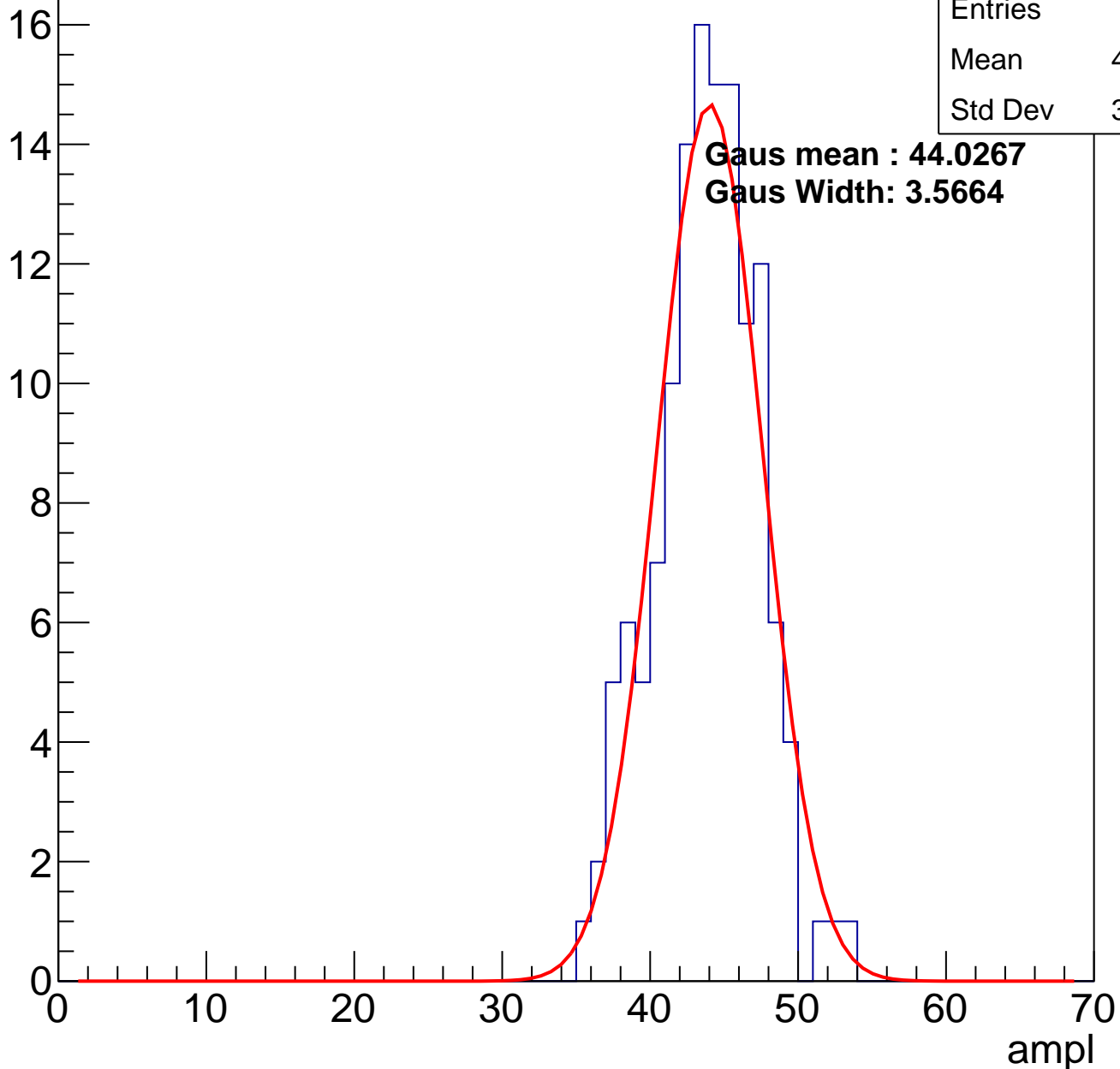
Gaus mean : 36.4588
Gaus Width: 3.7171



B0L101S, U18-ch57, adc2

calib_packv5_042523_0143.root, FC#1, port C1

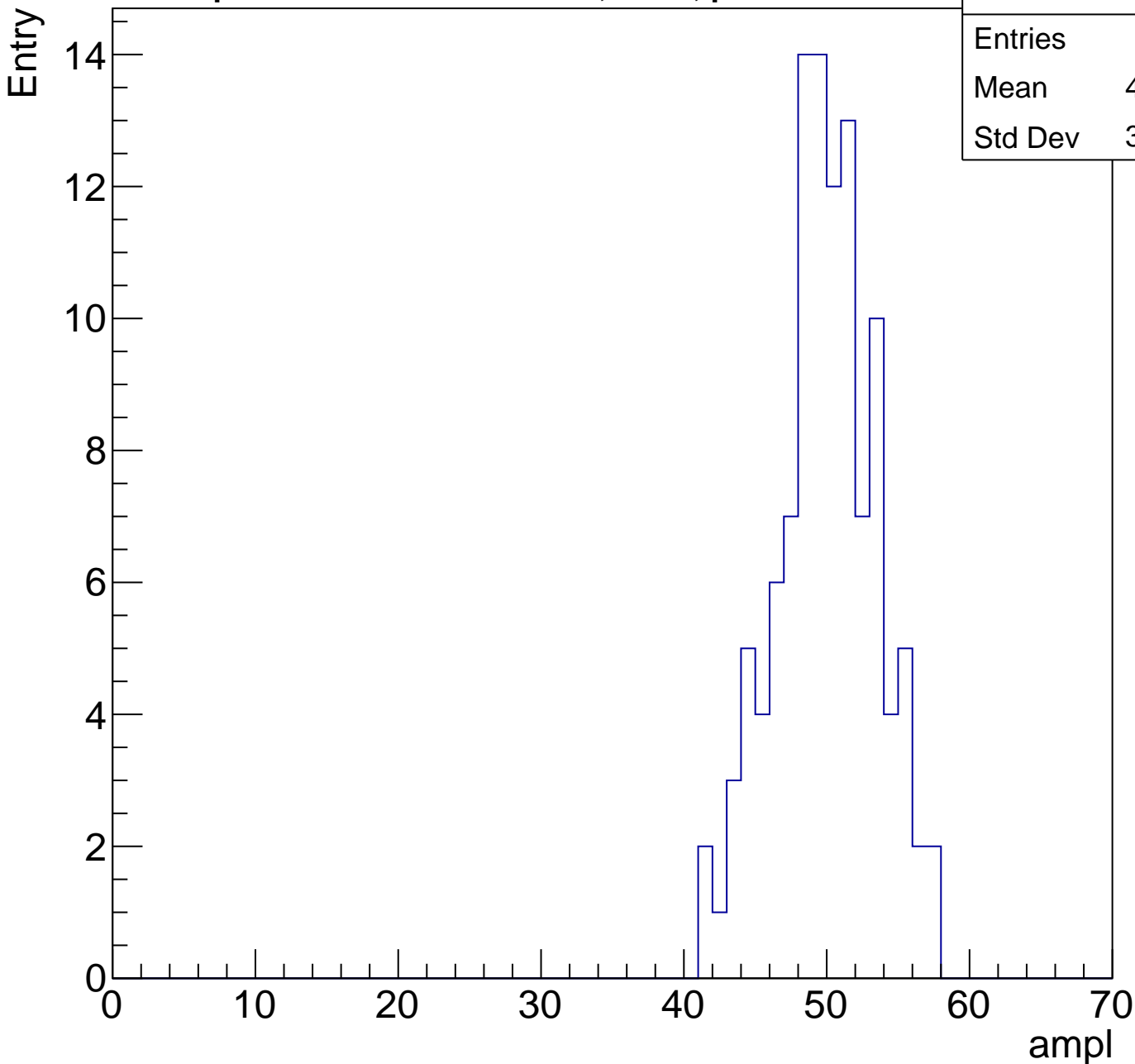
Entry



B0L101S, U18-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

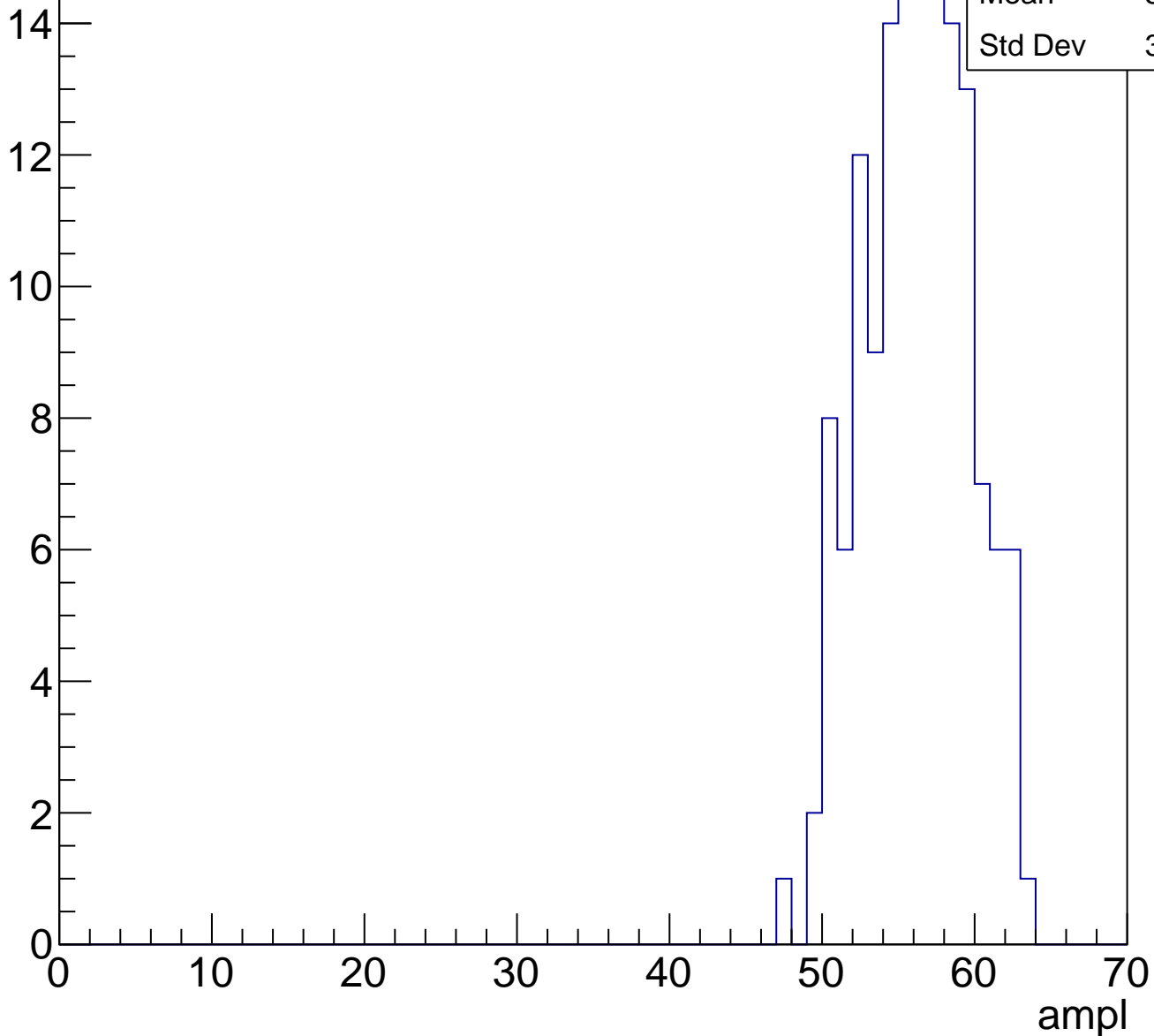
Entries	111
Mean	49.46
Std Dev	3.474



B0L101S, U18-ch57, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	144
Mean	55.75
Std Dev	3.402

B0L101S, U18-ch57, adc5

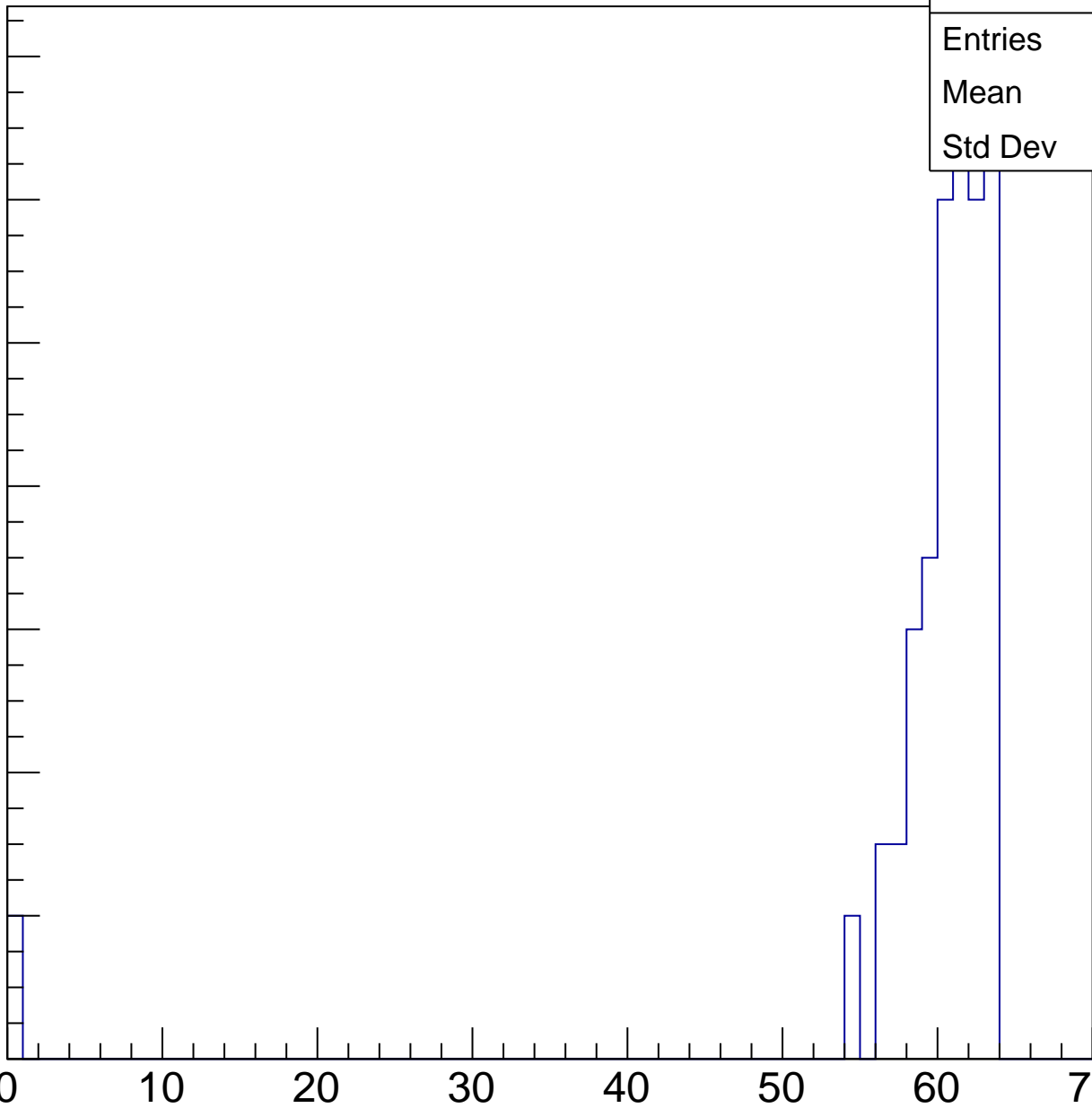
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	58.74
Std Dev	10.03

ampl

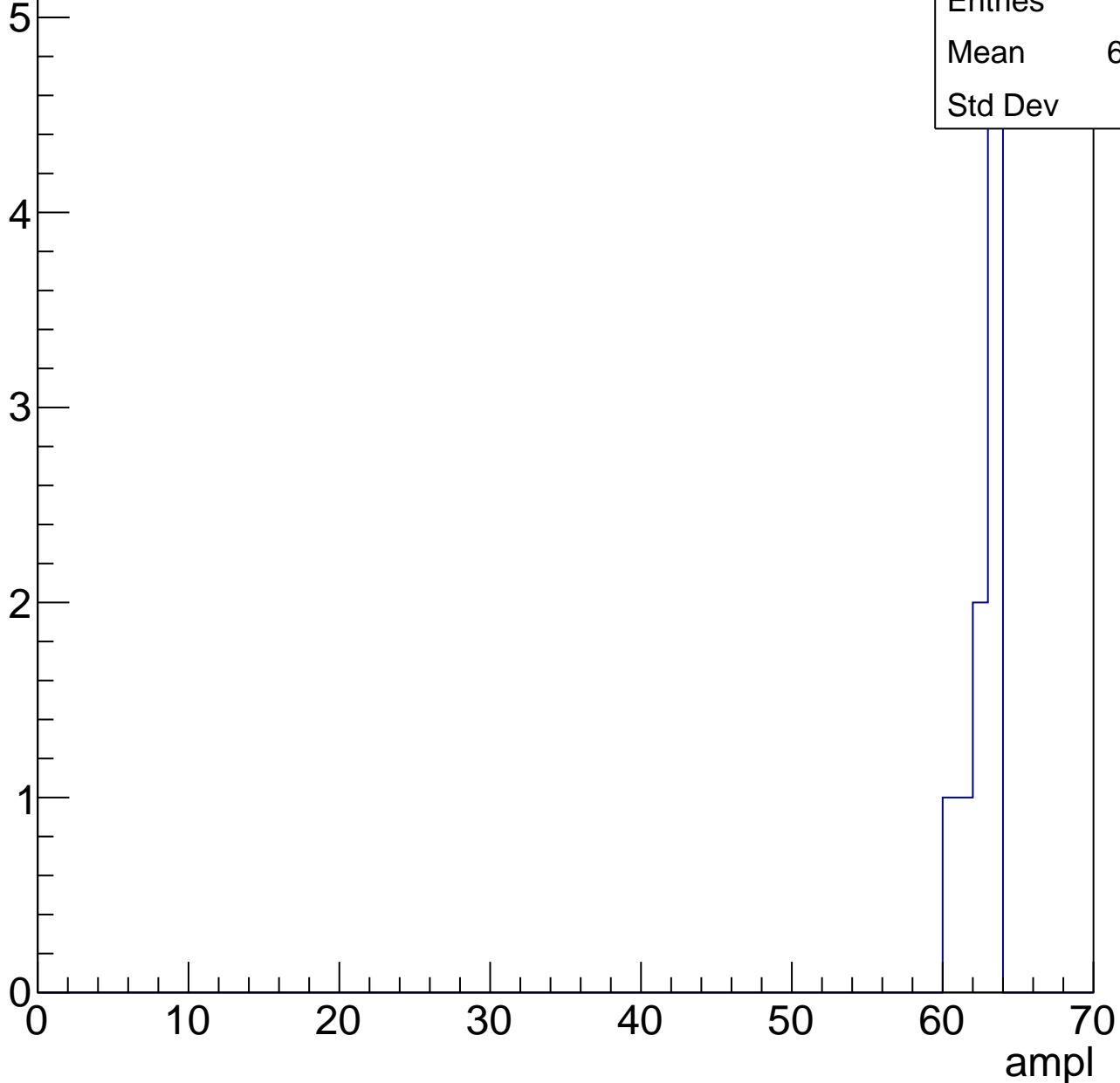


B0L101S, U18-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	62.22
Std Dev	1.03



B0L101S, U18-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch58, adc0

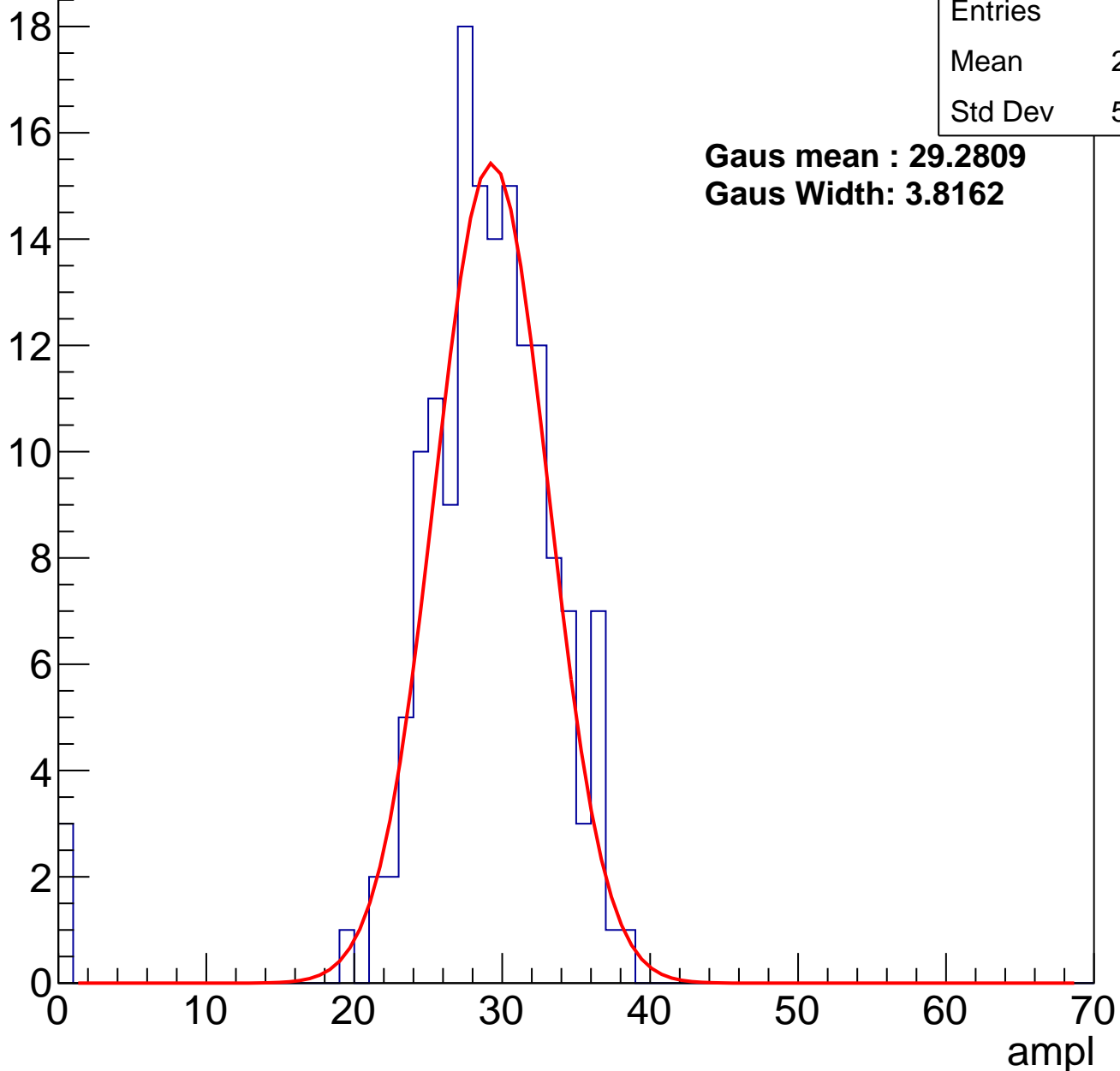
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	28.34
Std Dev	5.445

Gaus mean : 29.2809

Gaus Width: 3.8162

Entry

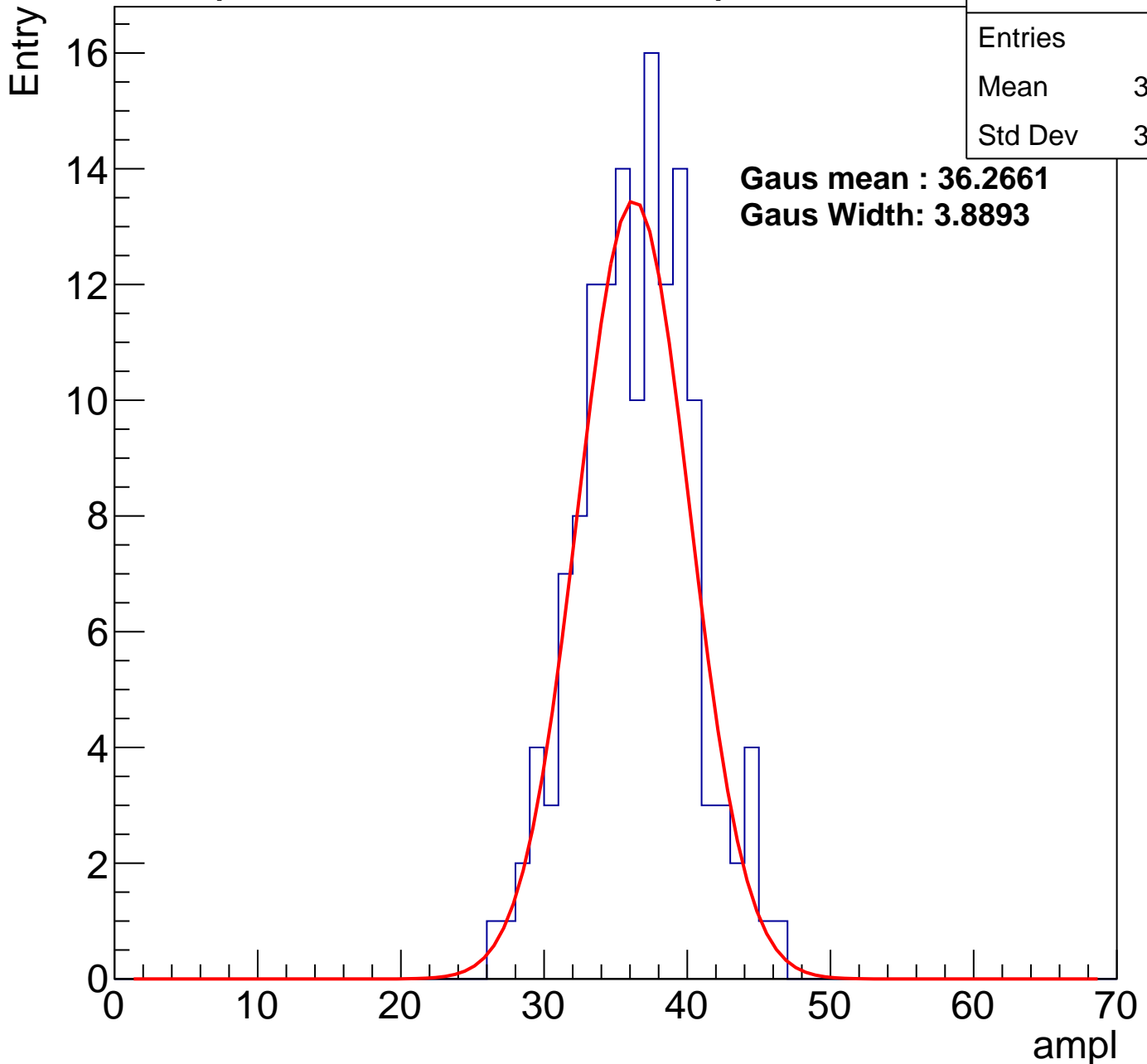


B0L101S, U18-ch58, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	35.99
Std Dev	3.922

Gaus mean : 36.2661
Gaus Width: 3.8893



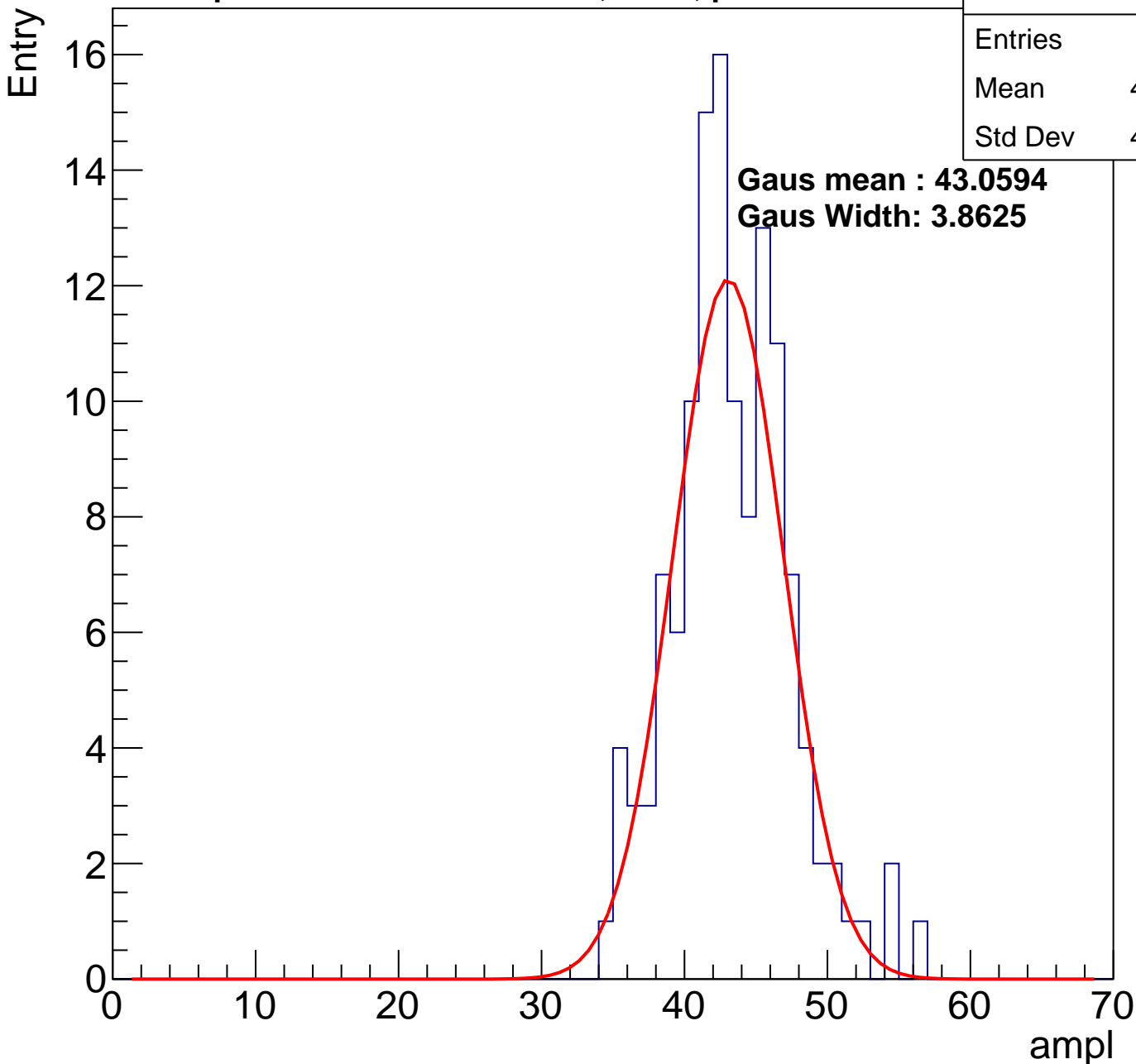
B0L101S, U18-ch58, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	42.83
Std Dev	4.088

Gaus mean : 43.0594

Gaus Width: 3.8625

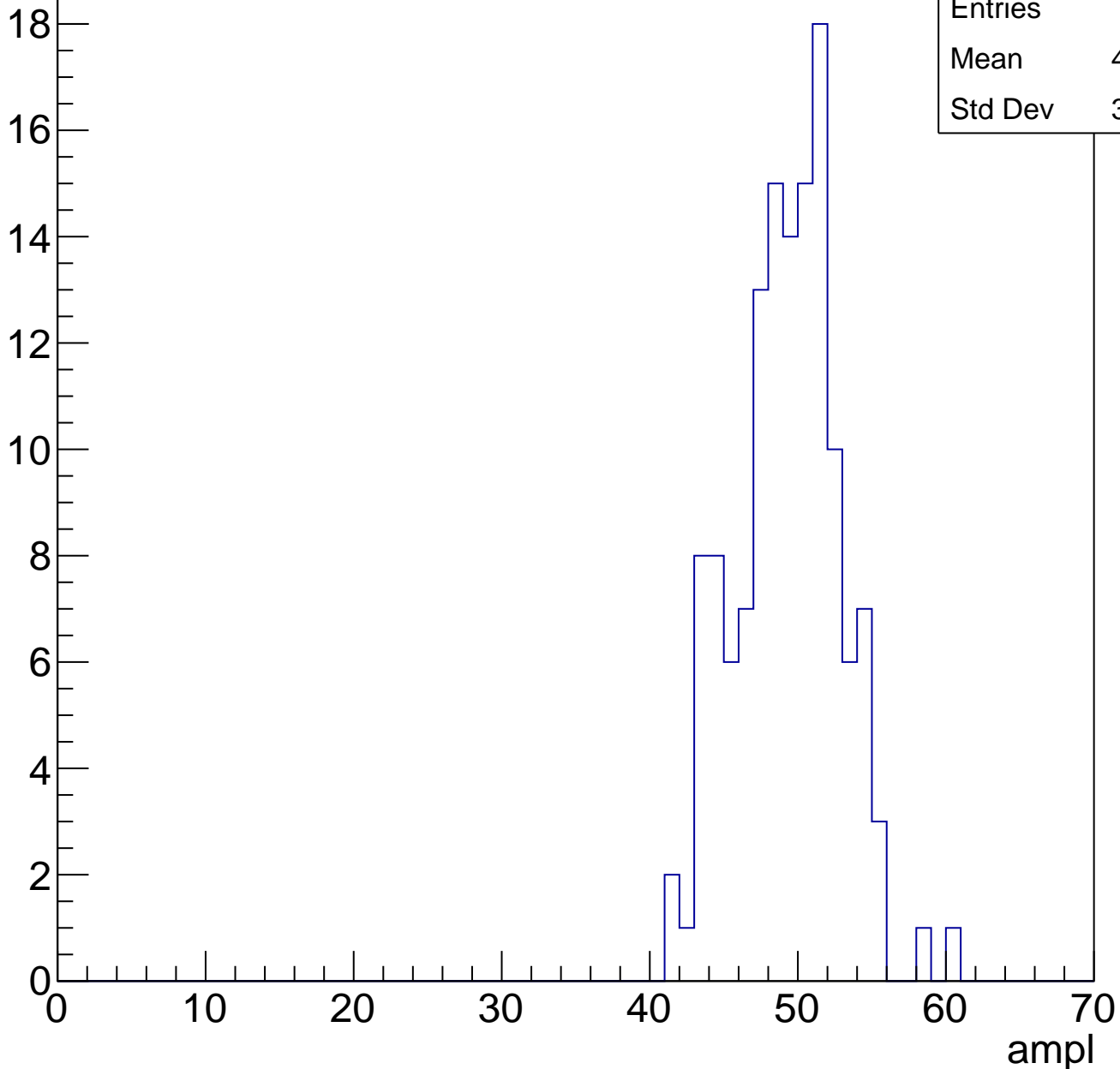


B0L101S, U18-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	48.86
Std Dev	3.492

Entry



B0L101S, U18-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	55.47
Std Dev	3.4

Entry

14
12
10
8
6
4
2
0

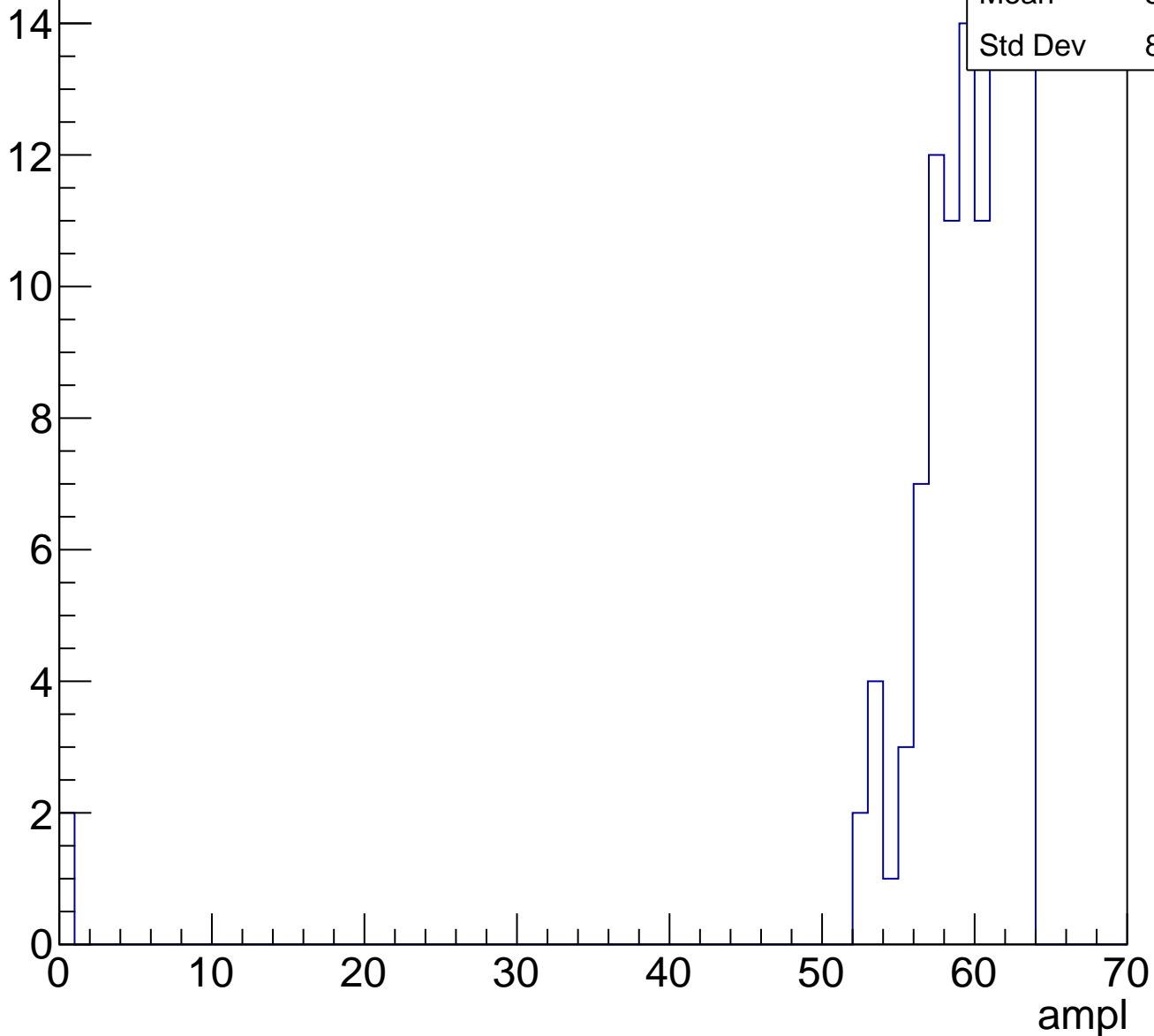
ampl

0 10 20 30 40 50 60 70

B0L101S, U18-ch58, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

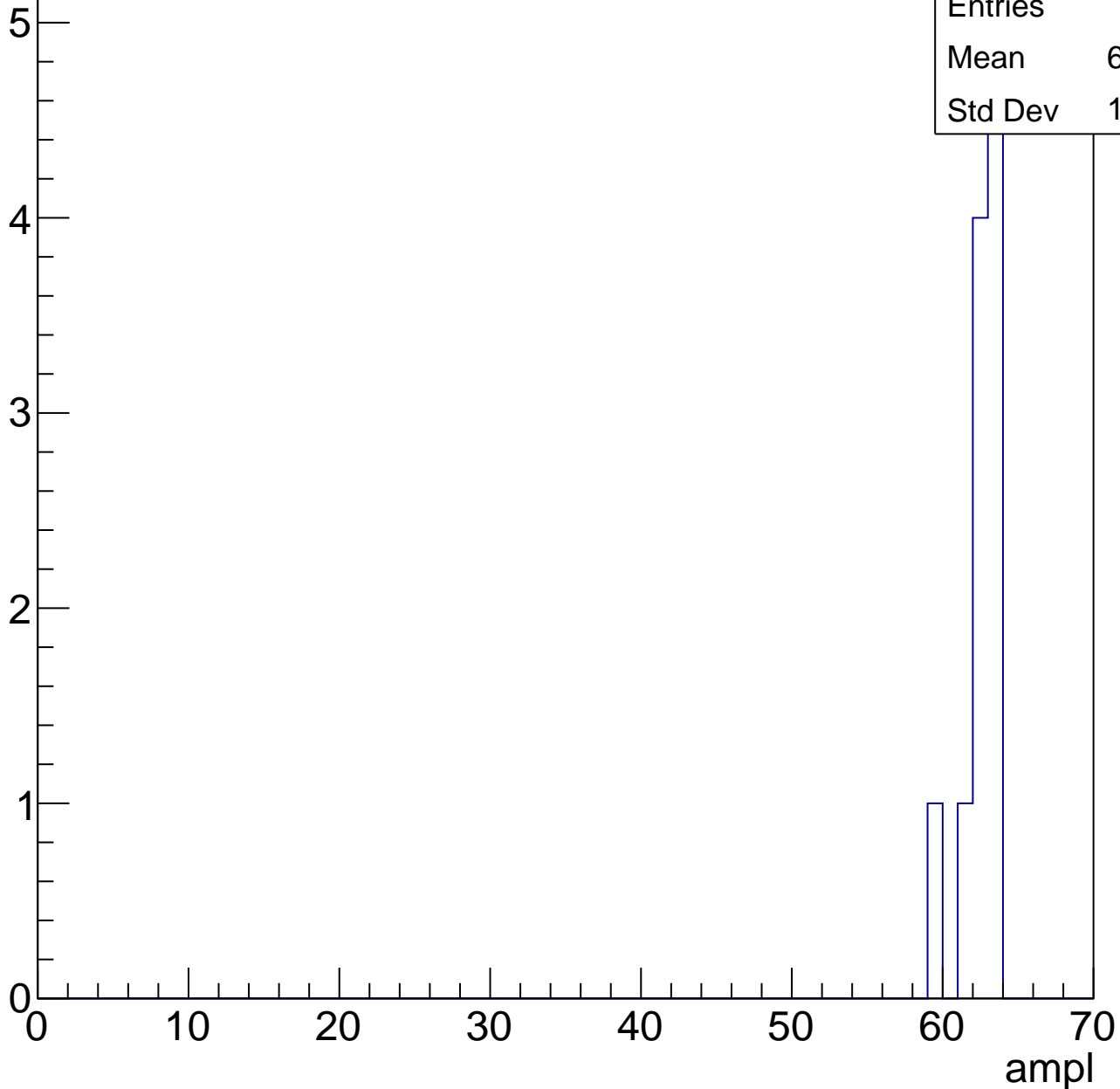


B0L101S, U18-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62.09
Std Dev	1.164



B0L101S, U18-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch59, adc0

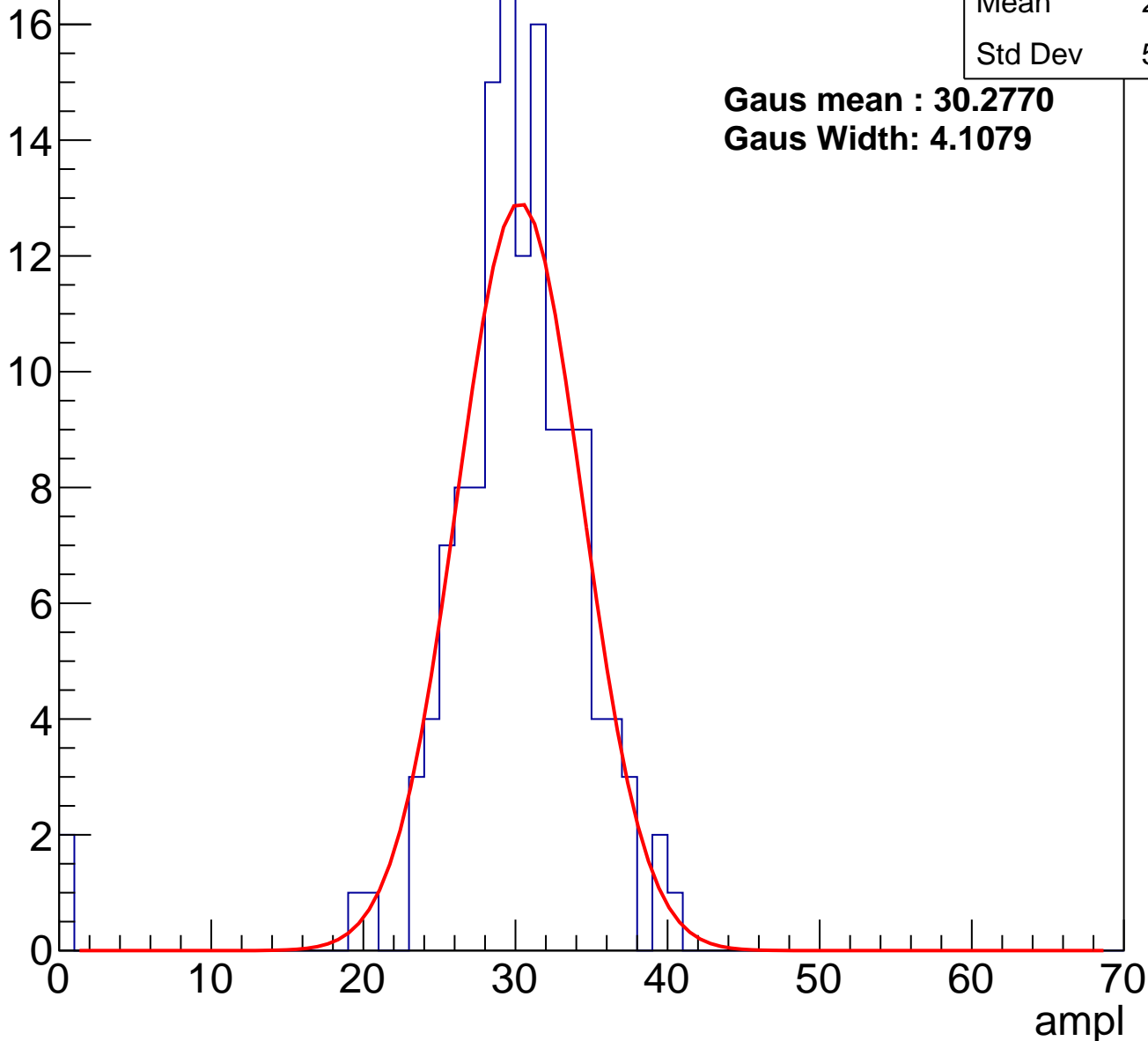
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	135
Mean	29.45
Std Dev	5.202

Gaus mean : 30.2770

Gaus Width: 4.1079



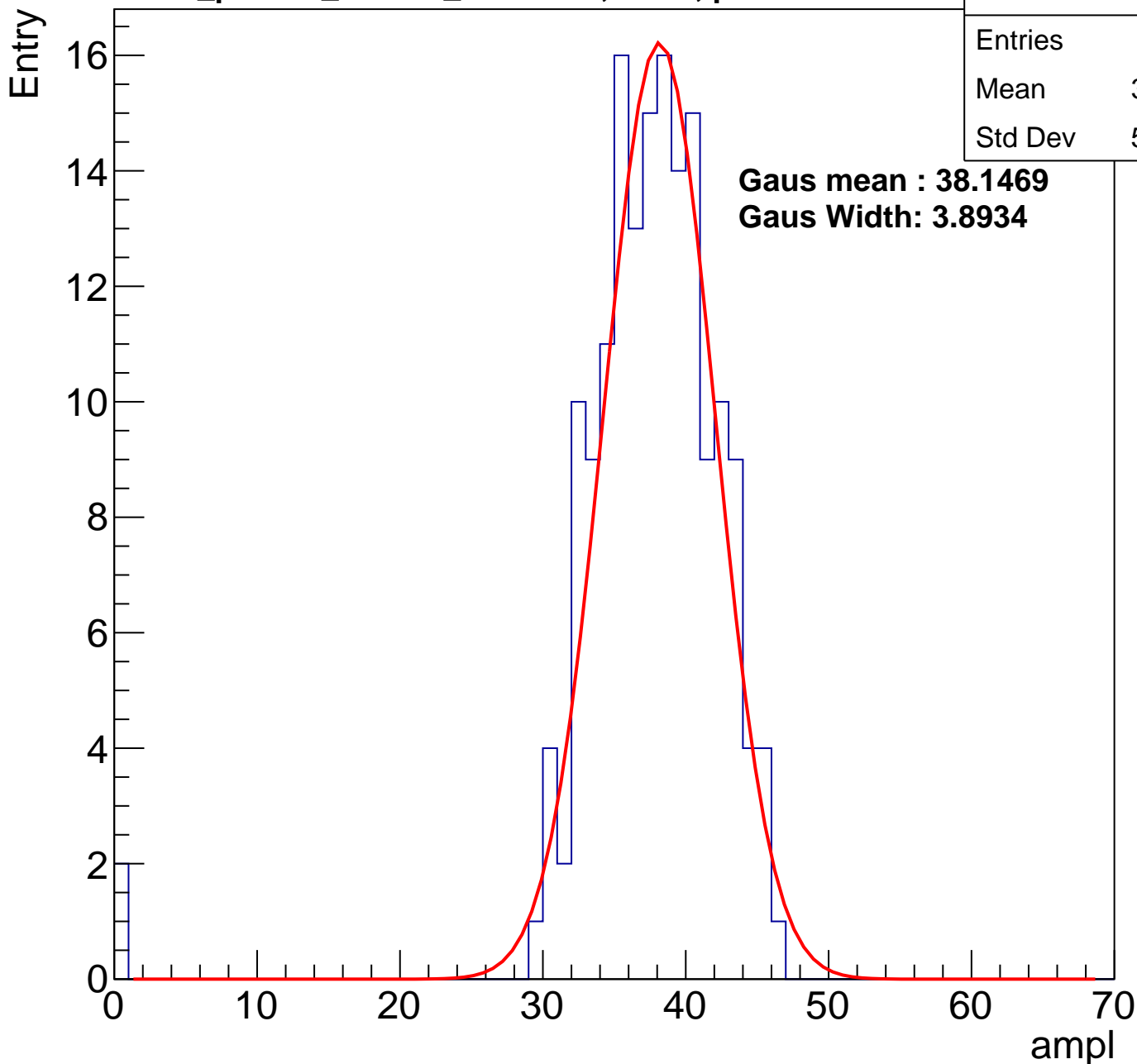
B0L101S, U18-ch59, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	37.07
Std Dev	5.558

Gaus mean : 38.1469

Gaus Width: 3.8934



B0L101S, U18-ch59, adc2

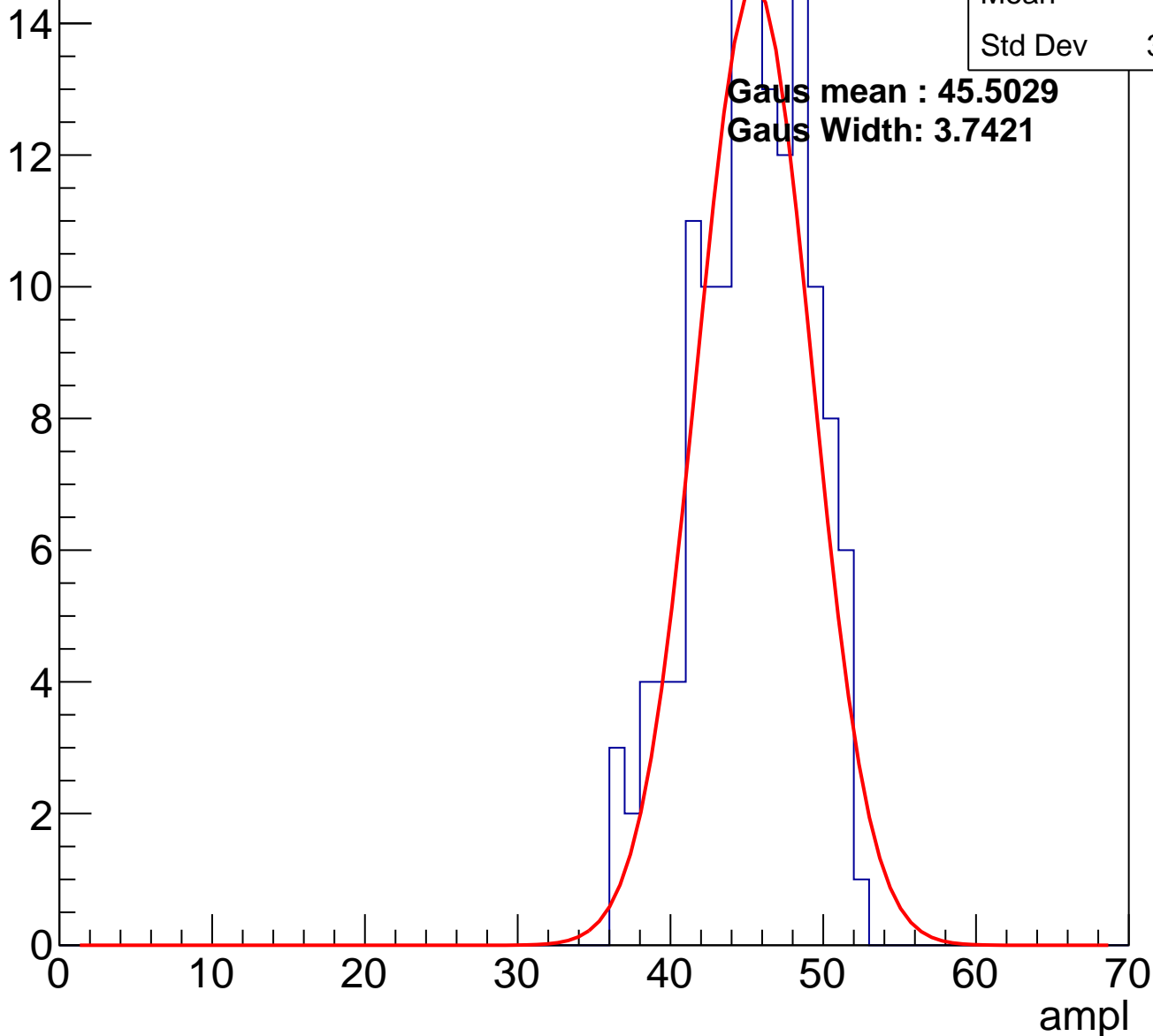
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	44.87
Std Dev	3.693

Gaus mean : 45.5029

Gaus Width: 3.7421

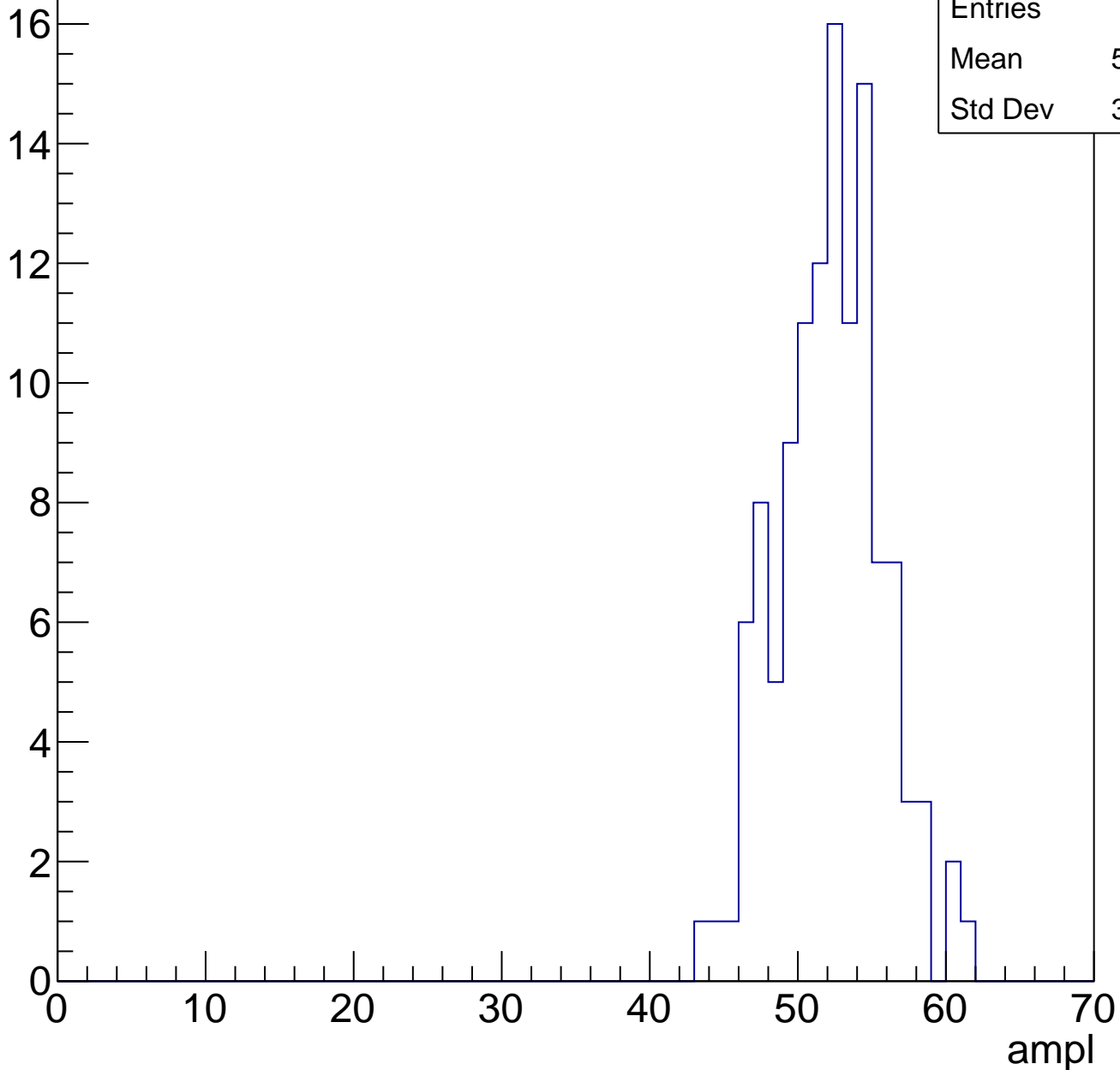
Entry



B0L101S, U18-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

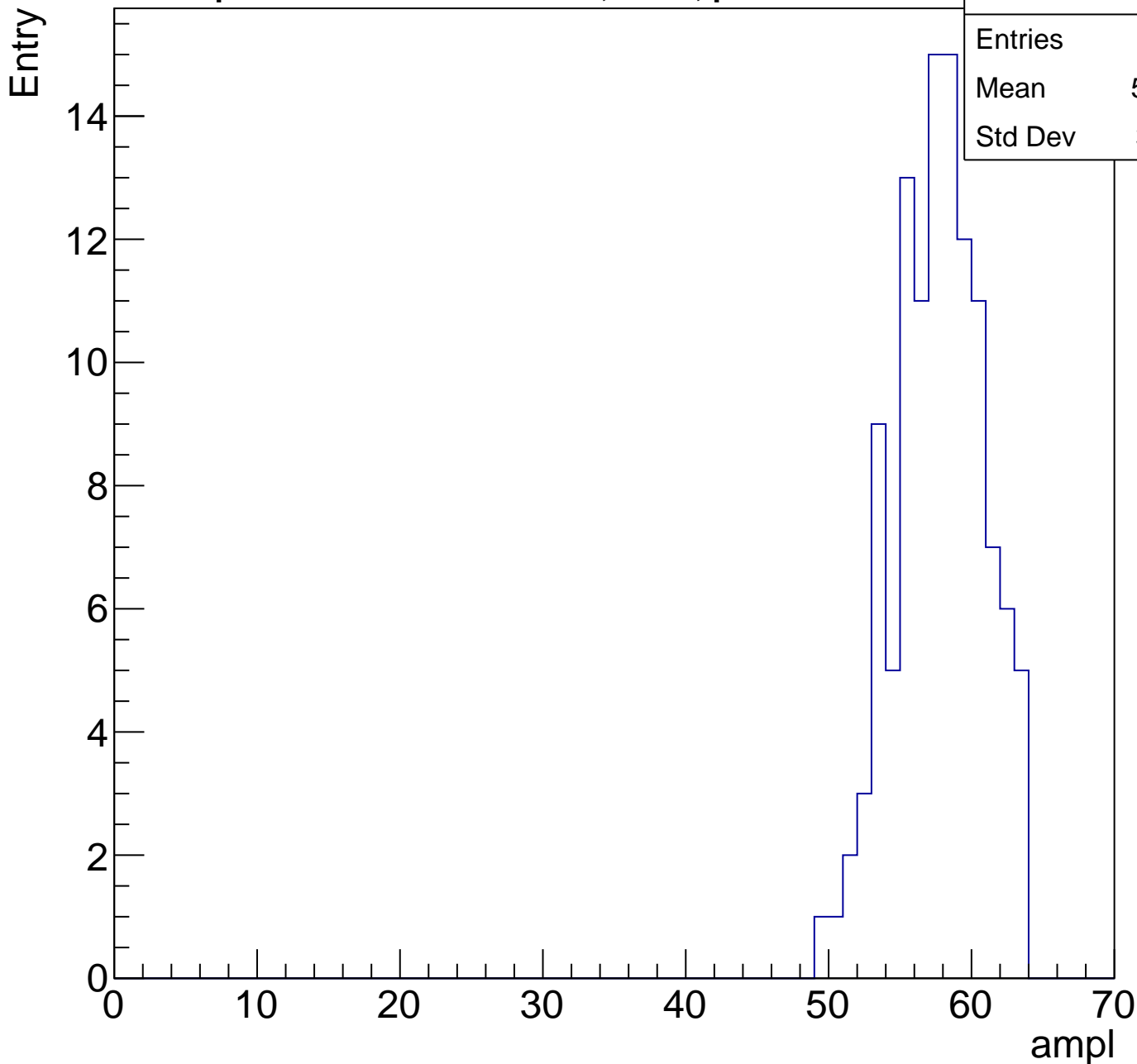
Entry



B0L101S, U18-ch59, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	57.26
Std Dev	3.091



B0L101S, U18-ch59, adc5

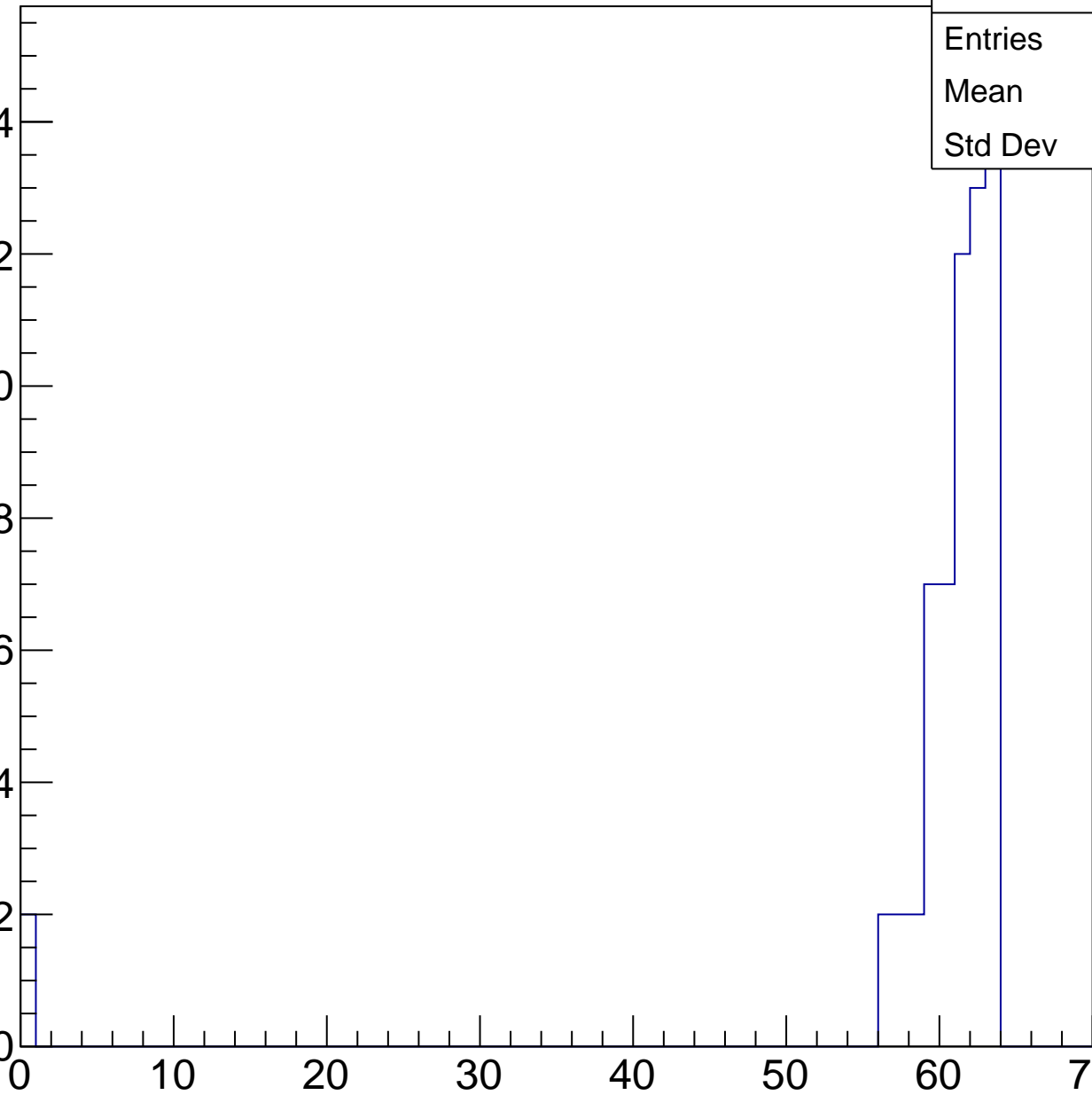
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	62
Mean	59
Std Dev	10.93

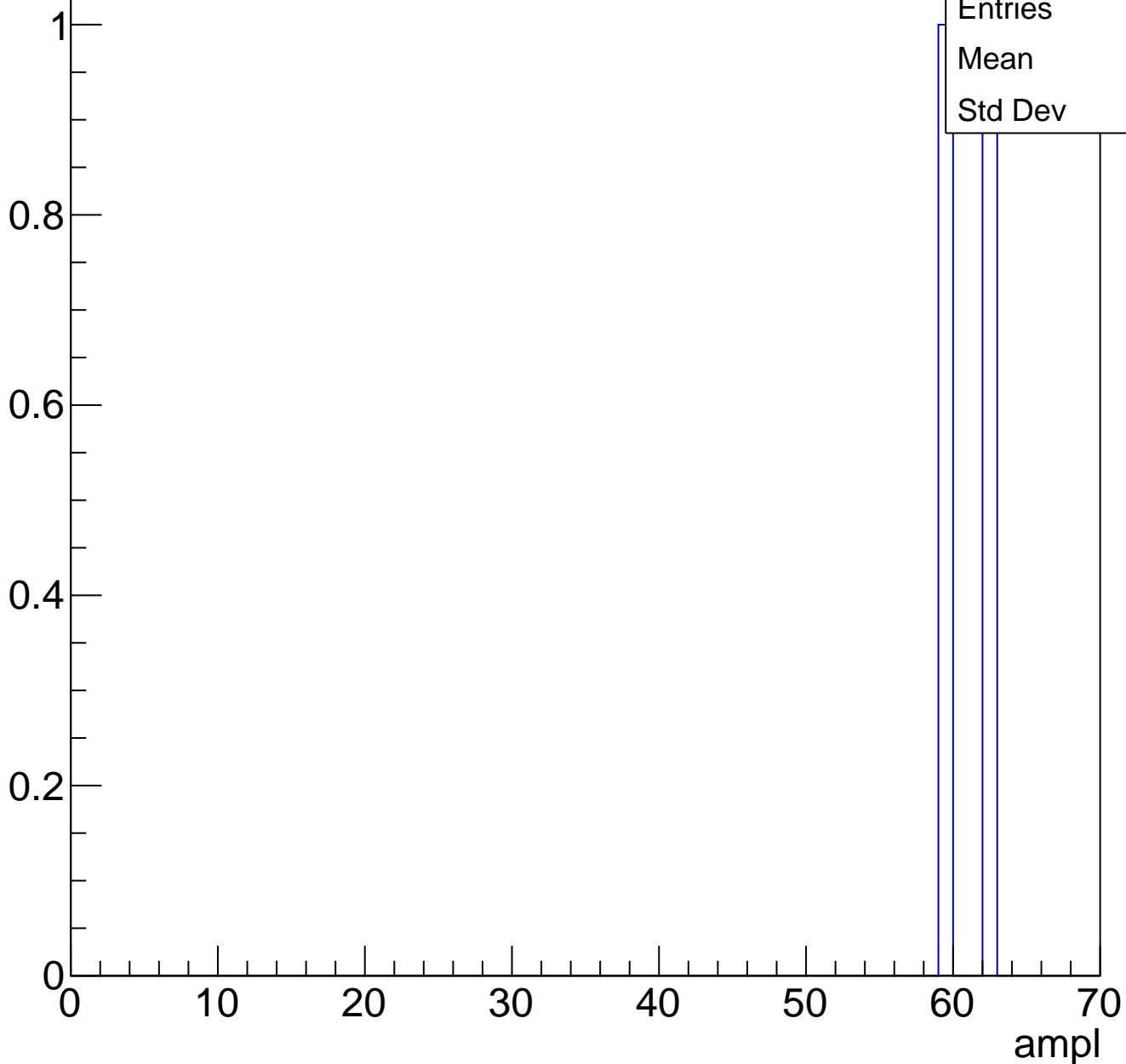
ampl



B0L101S, U18-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U18-ch60, adc0

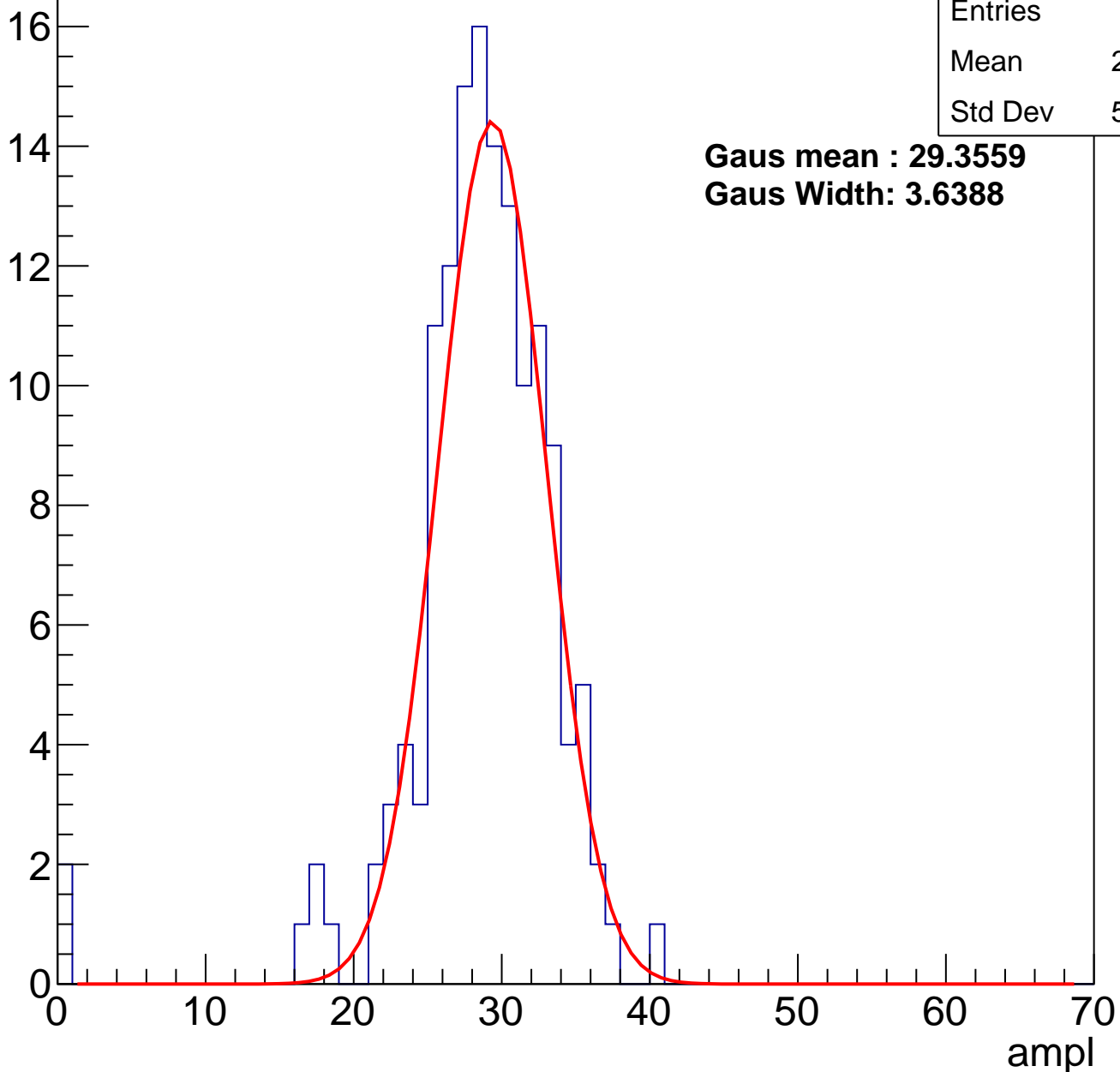
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	28.13
Std Dev	5.218

Gaus mean : 29.3559

Gaus Width: 3.6388

Entry



B0L101S, U18-ch60, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	34.87
Std Dev	3.677

Gaus mean : 35.3003

Gaus Width: 3.7093

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

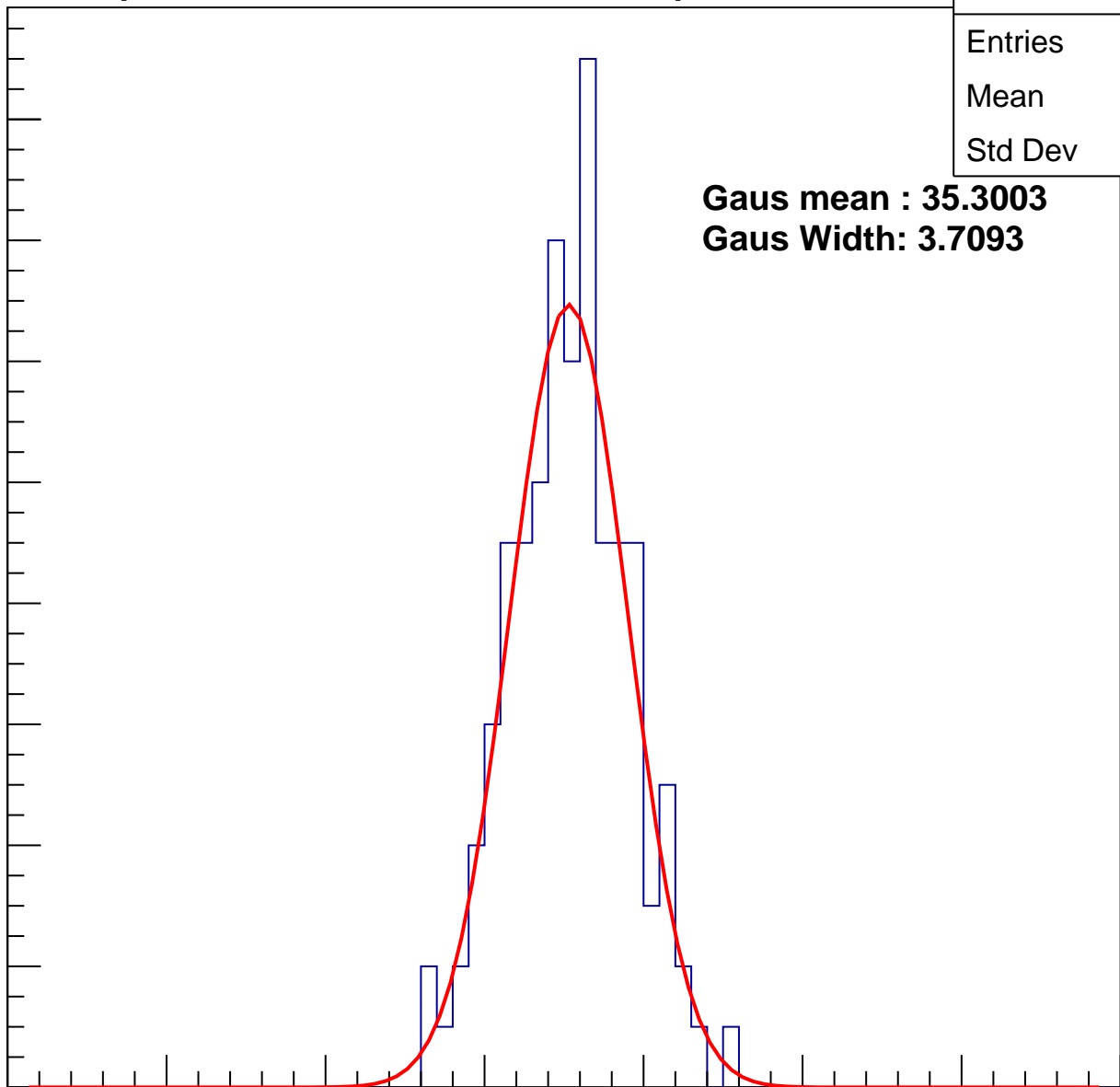
40

50

60

70

ampl



B0L101S, U18-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

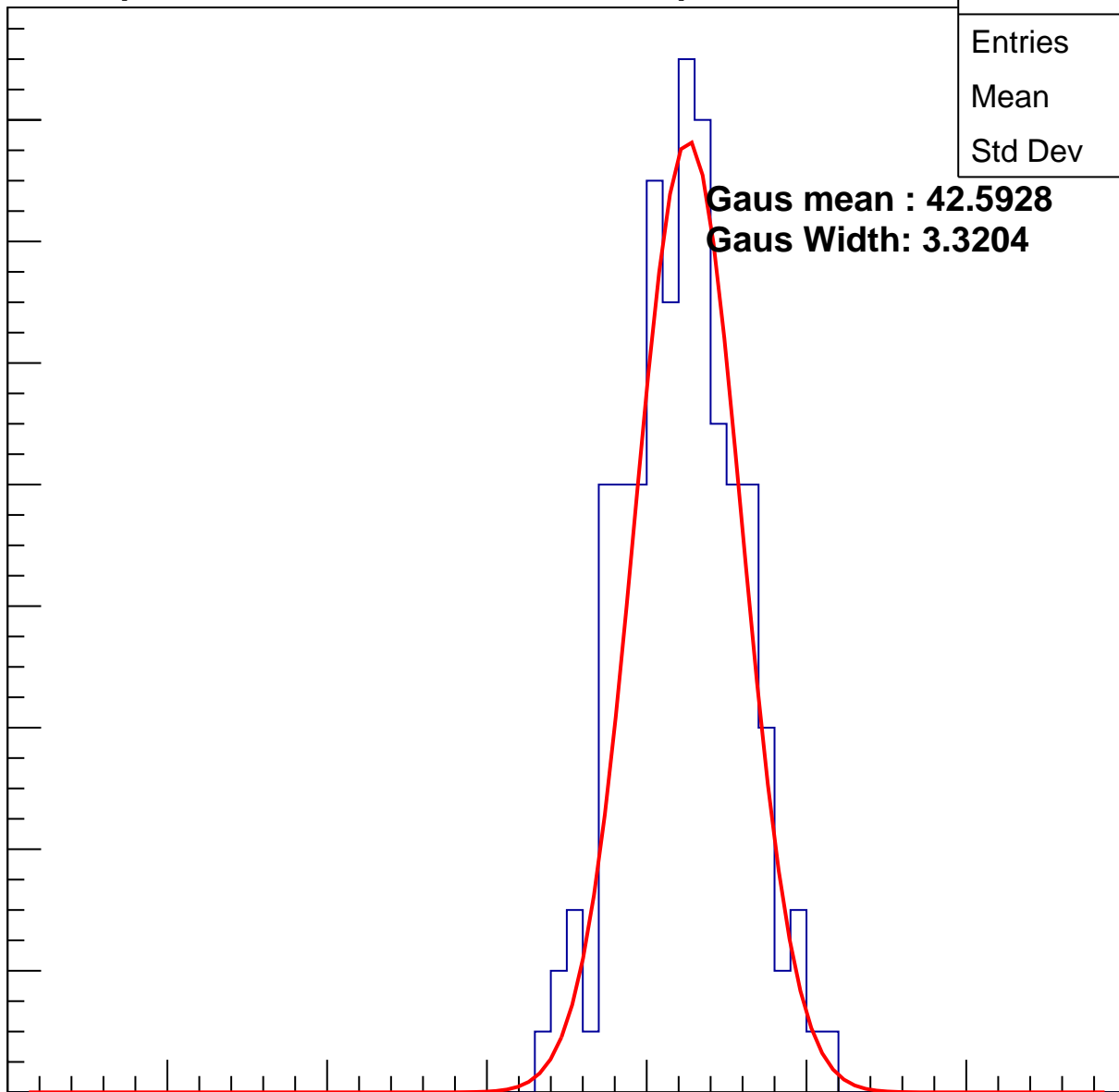
70

ampl

Entries	142
Mean	41.81
Std Dev	3.544

Gaus mean : 42.5928

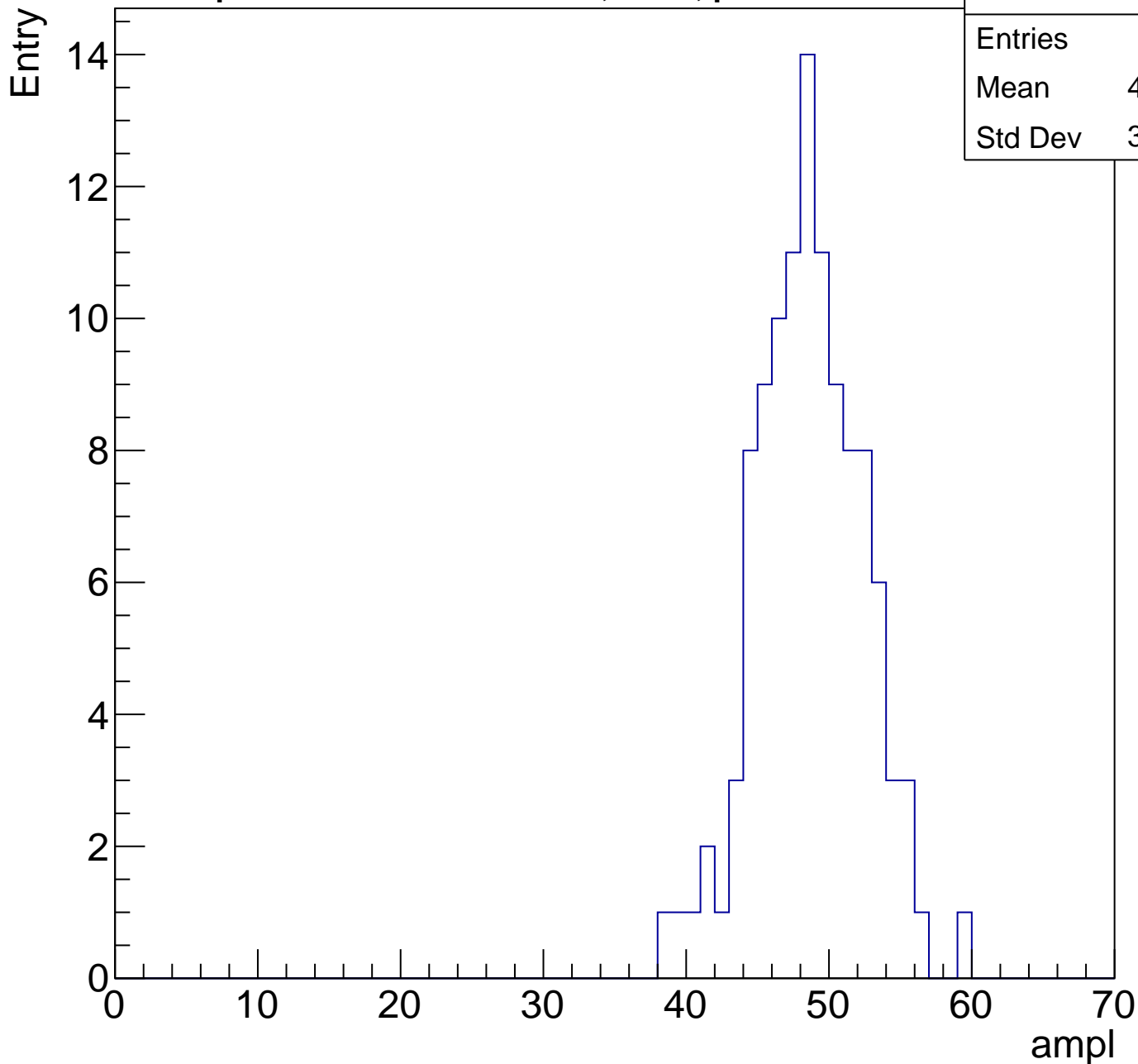
Gaus Width: 3.3204



B0L101S, U18-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

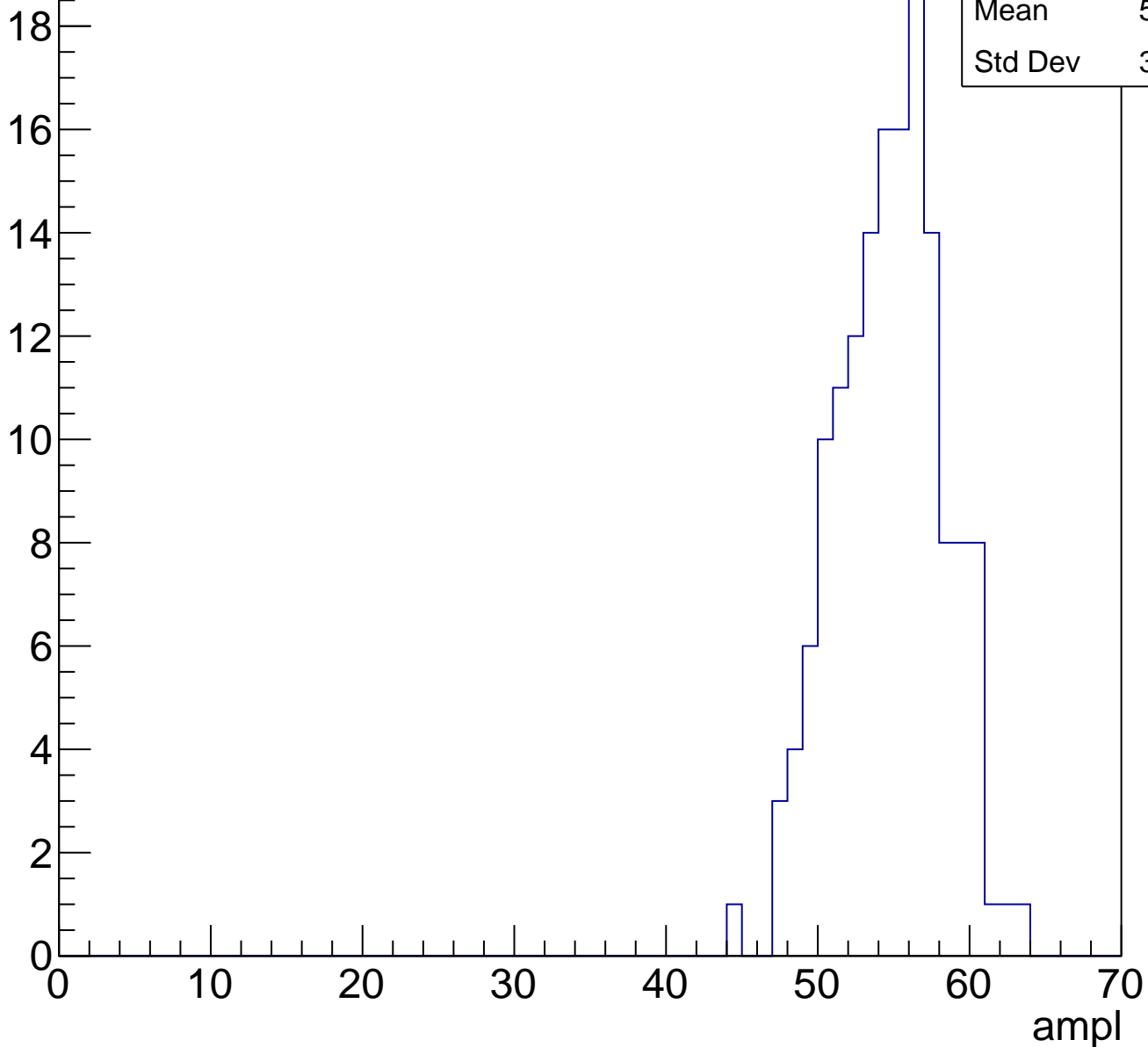
Entries	111
Mean	48.19
Std Dev	3.733



B0L101S, U18-ch60, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch60, adc5

calib_packv5_042523_0143.root, FC#1, port C1

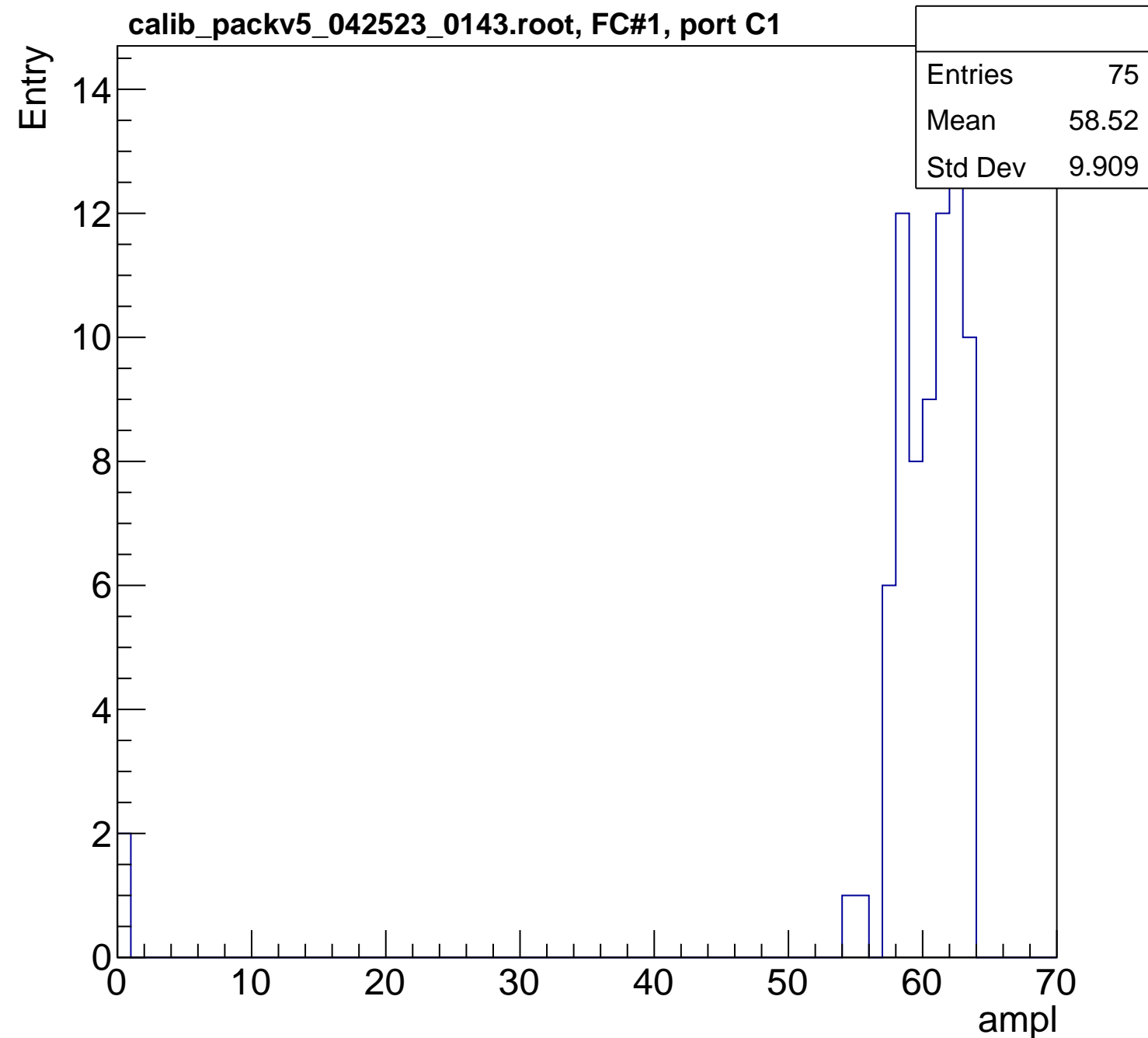
Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	58.52
Std Dev	9.909

ampl

0 10 20 30 40 50 60 70

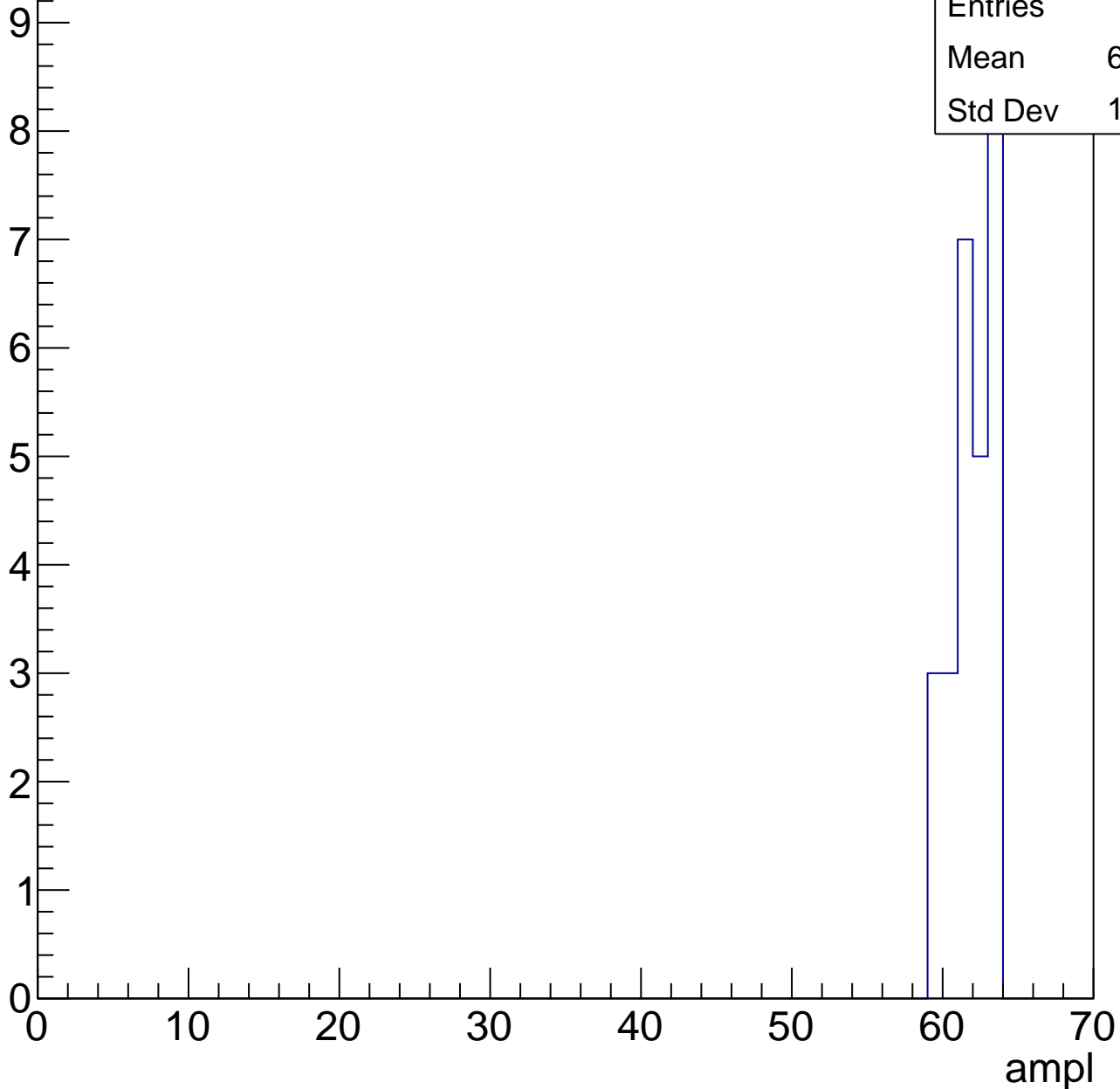


B0L101S, U18-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	27
Mean	61.52
Std Dev	1.344



B0L101S, U18-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

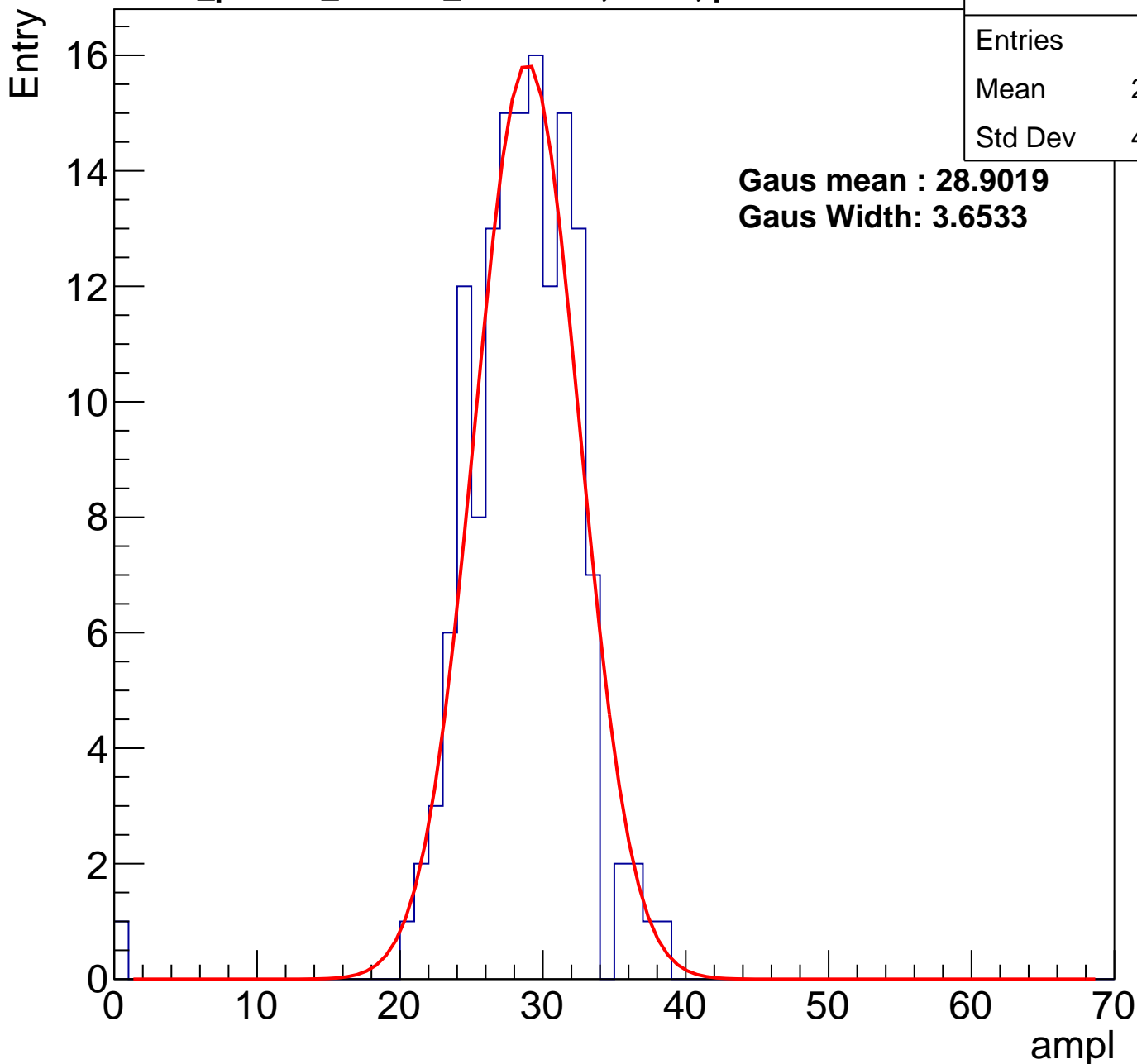
B0L101S, U18-ch61, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.07
Std Dev	4.172

Gaus mean : 28.9019

Gaus Width: 3.6533



B0L101S, U18-ch61, adc1

calib_packv5_042523_0143.root, FC#1, port C1

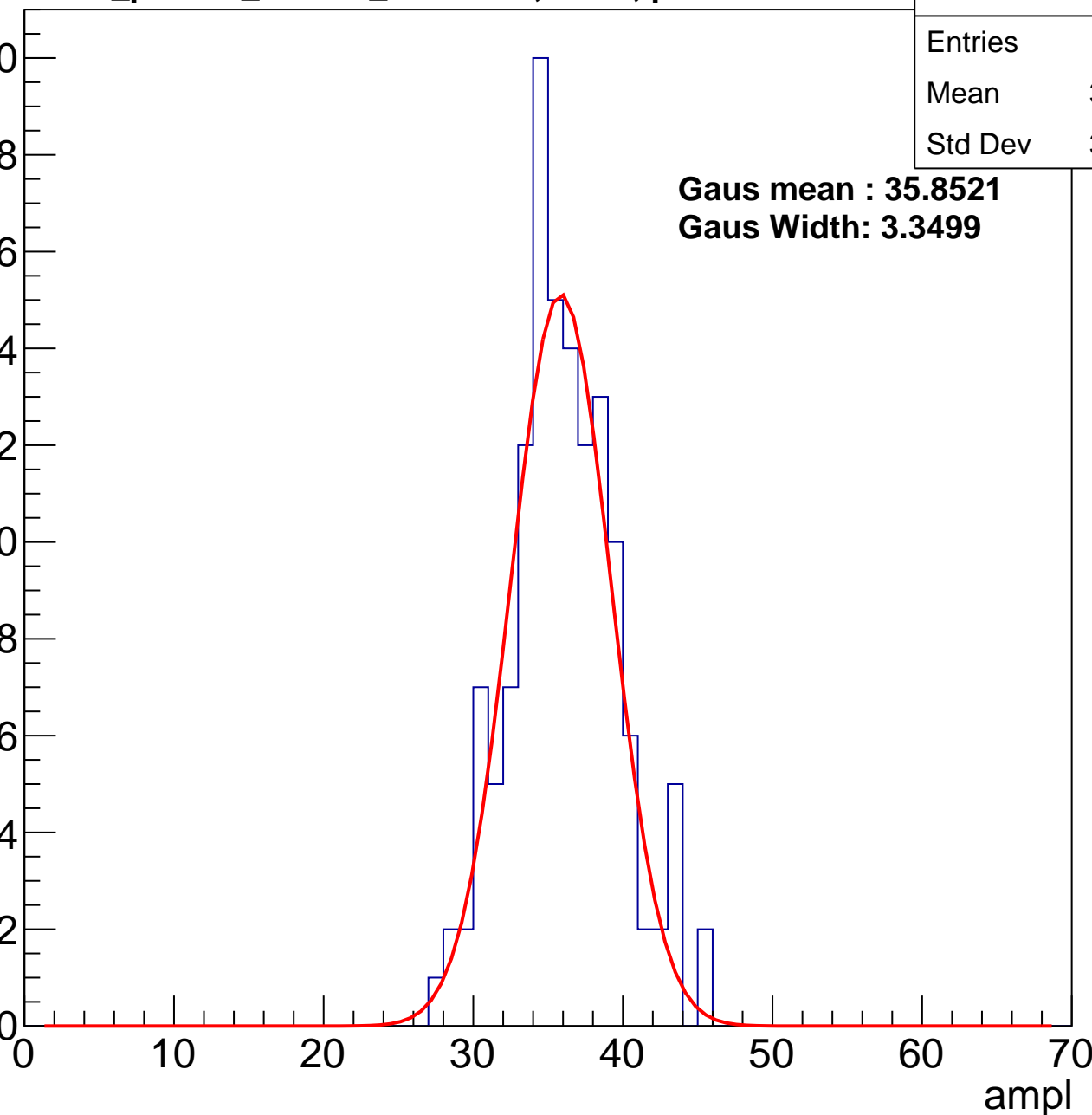
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	137
Mean	35.58
Std Dev	3.578

Gaus mean : 35.8521

Gaus Width: 3.3499



B0L101S, U18-ch61, adc2

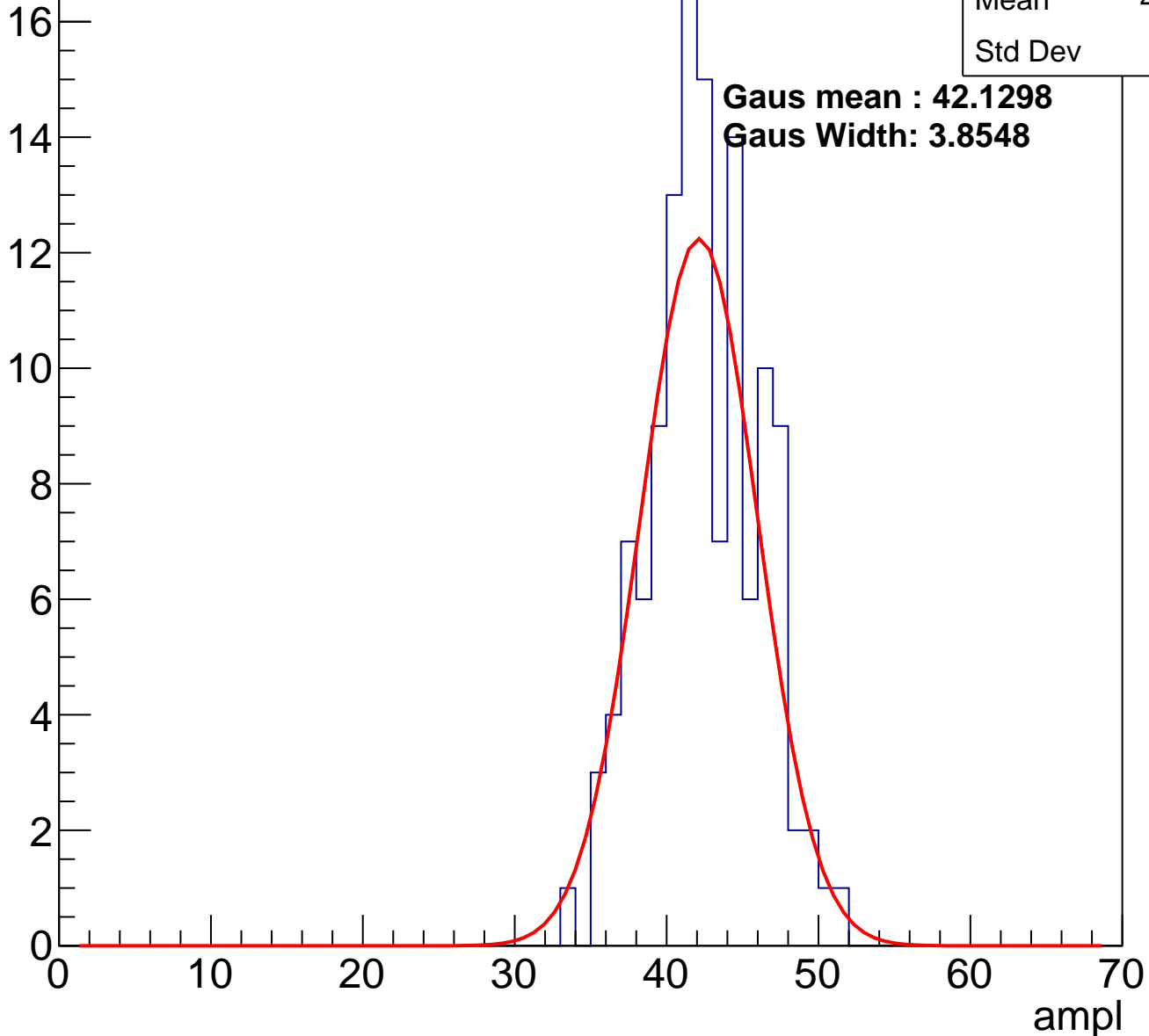
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	41.98
Std Dev	3.56

Gaus mean : 42.1298

Gaus Width: 3.8548

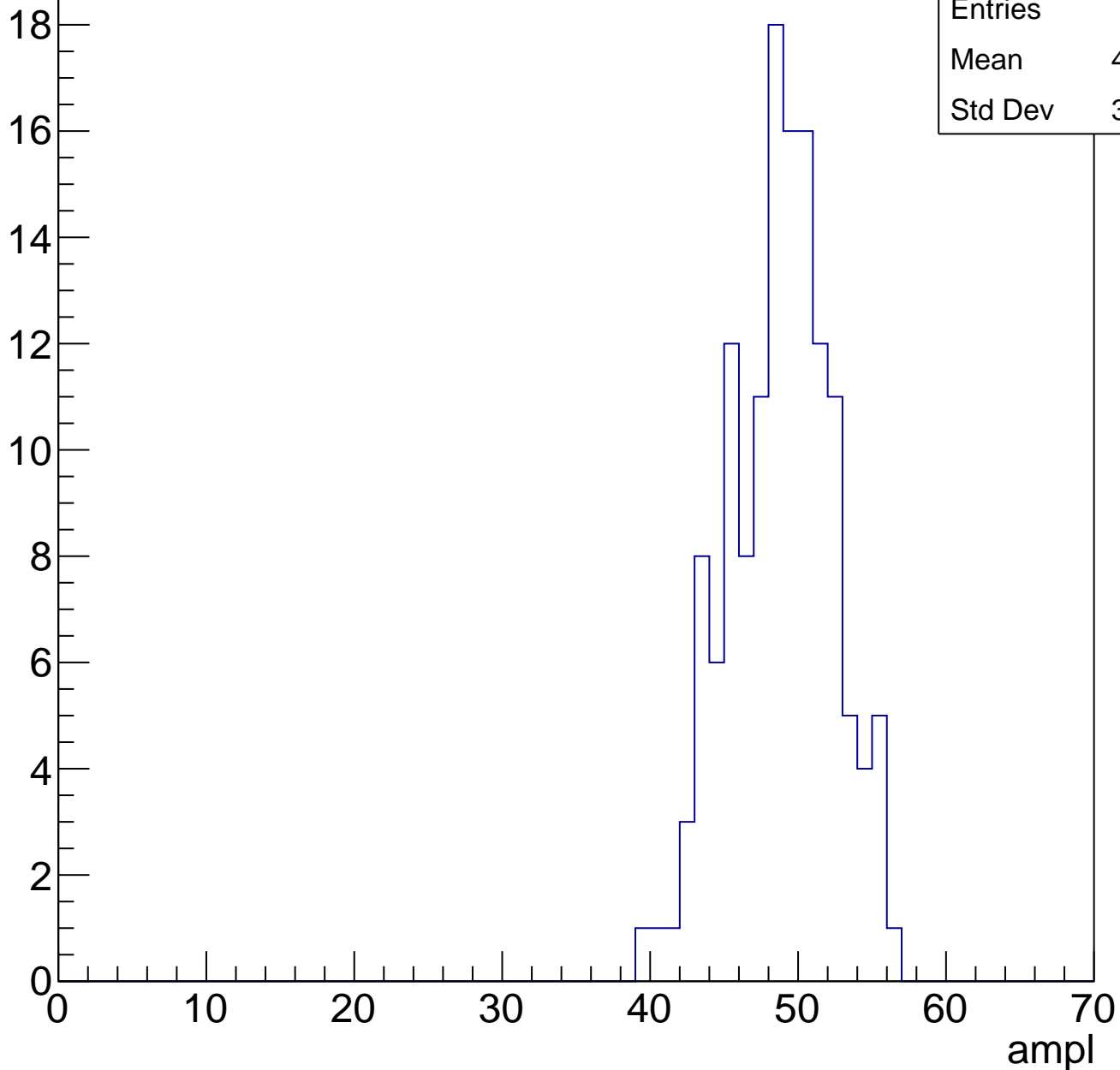
Entry



B0L101S, U18-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

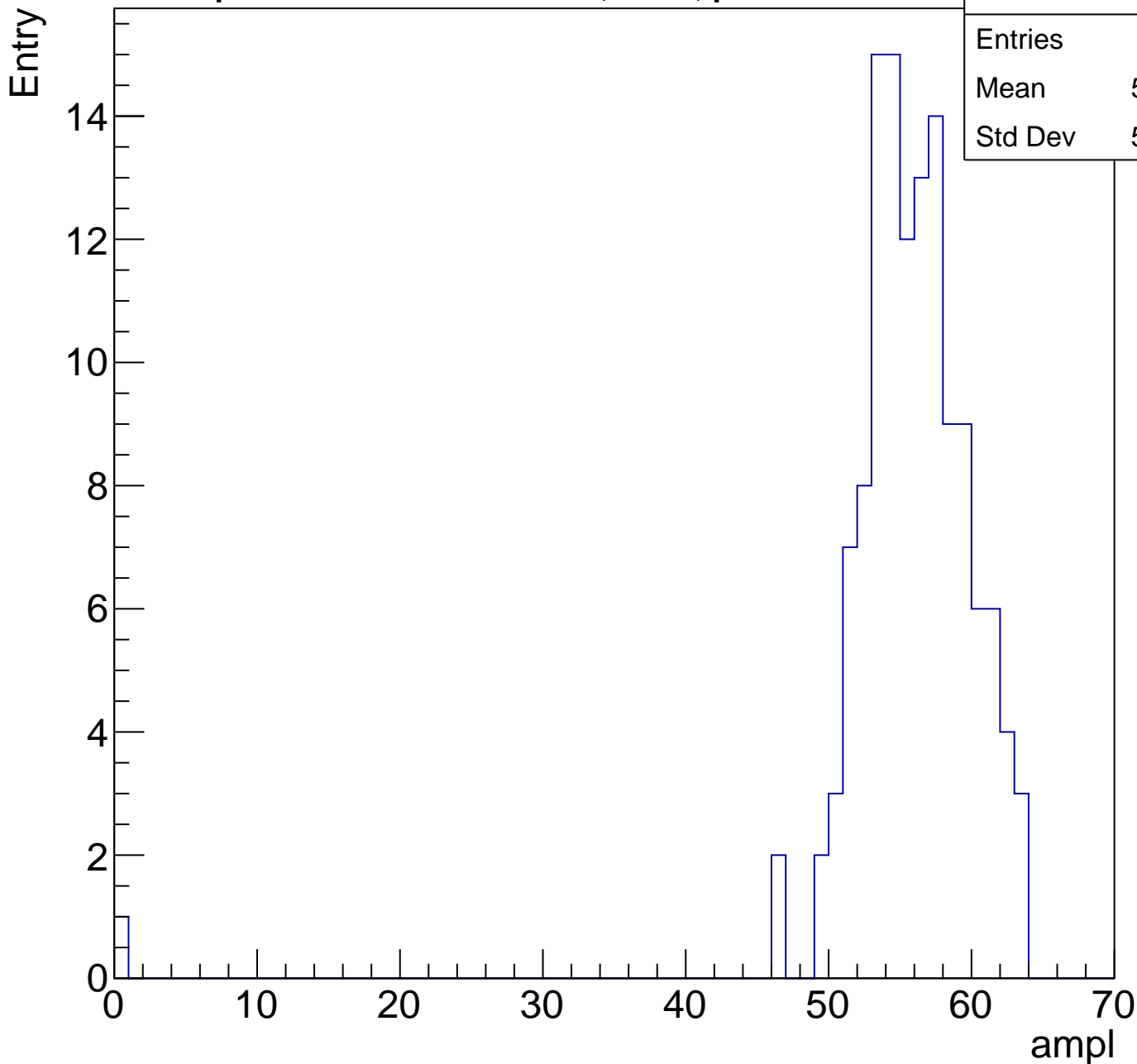
Entry



B0L101S, U18-ch61, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	55.19
Std Dev	5.995



B0L101S, U18-ch61, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

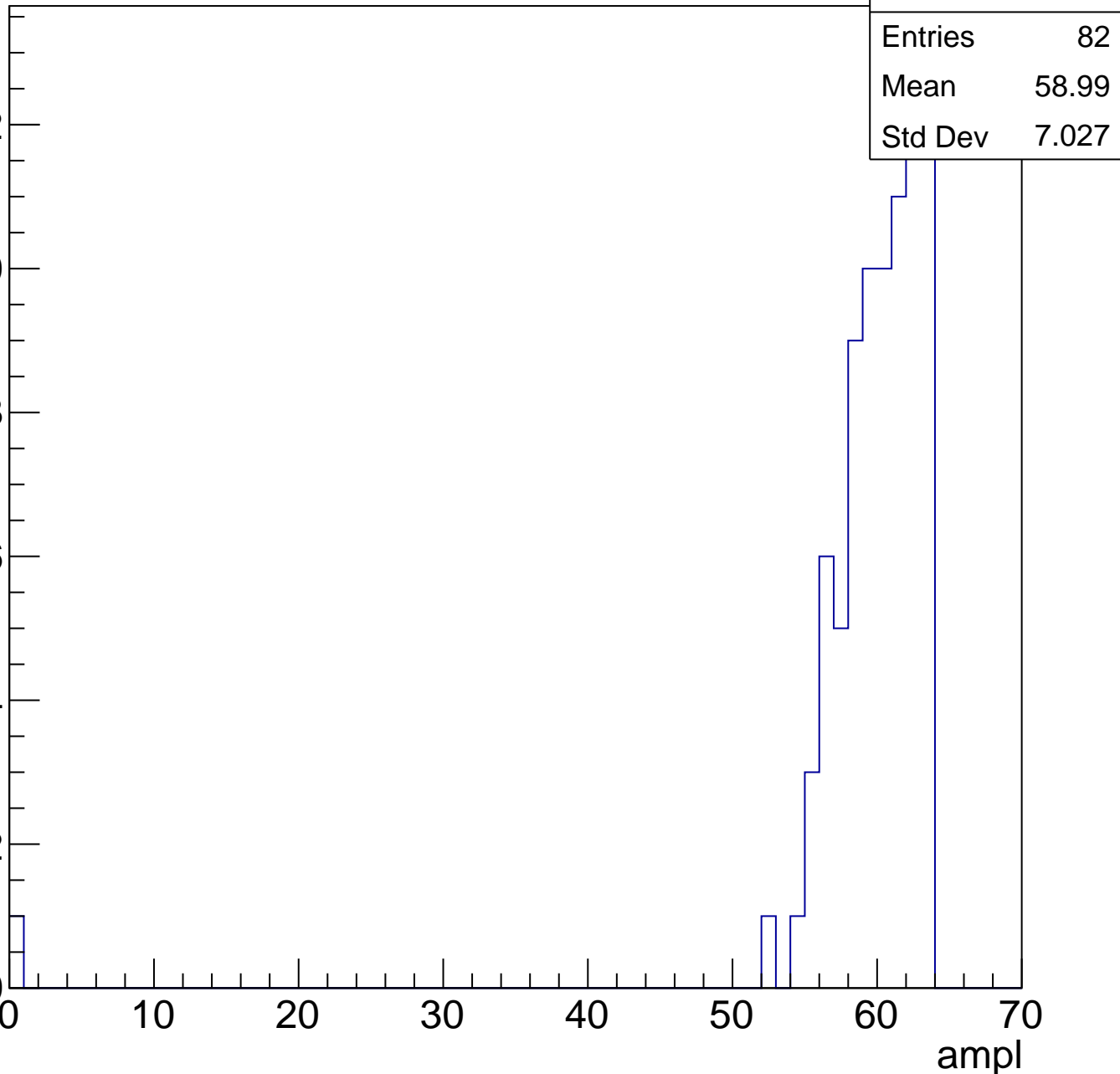
50

60

70

ampl

Entries	82
Mean	58.99
Std Dev	7.027

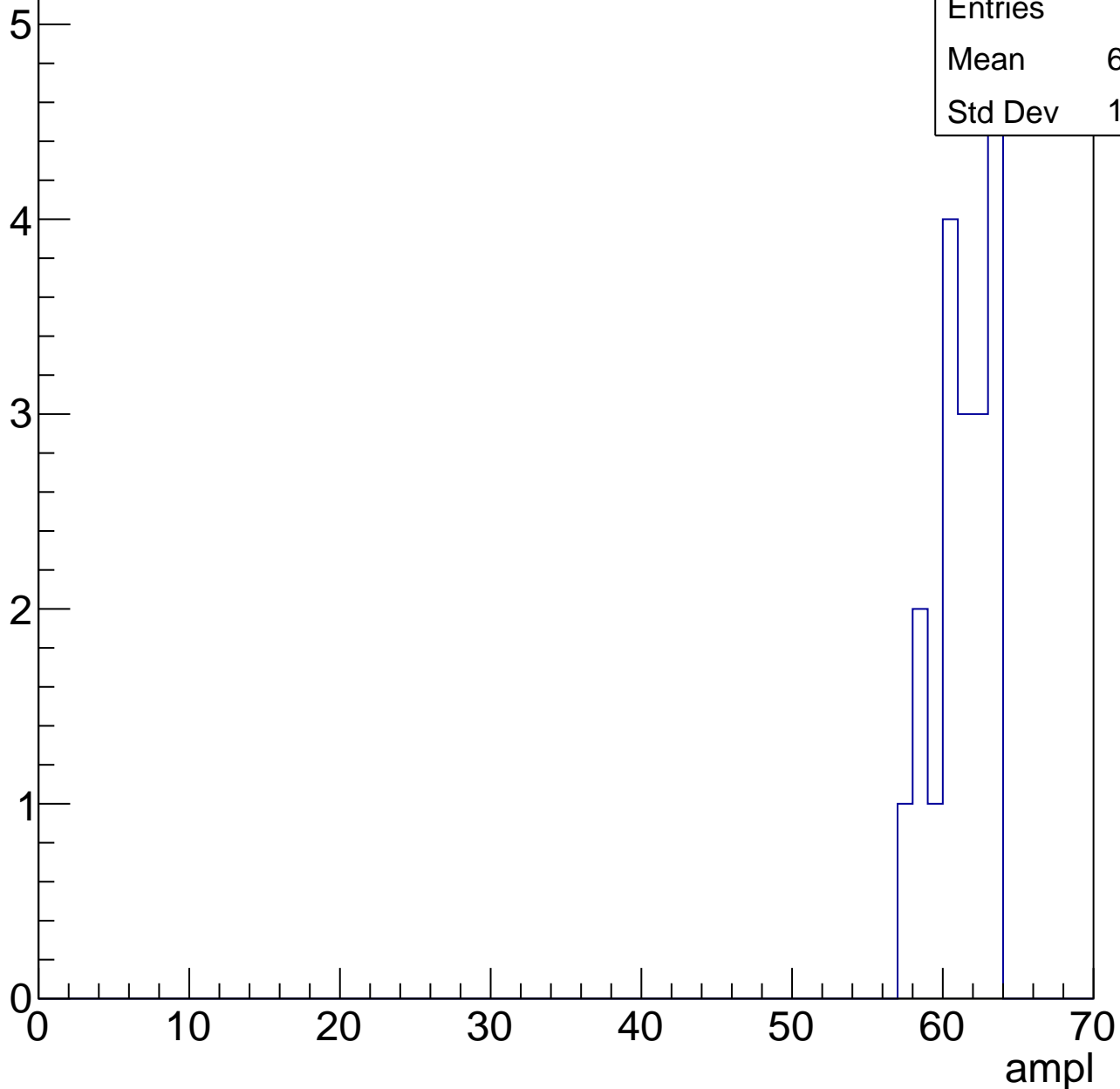


B0L101S, U18-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	60.84
Std Dev	1.843



B0L101S, U18-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

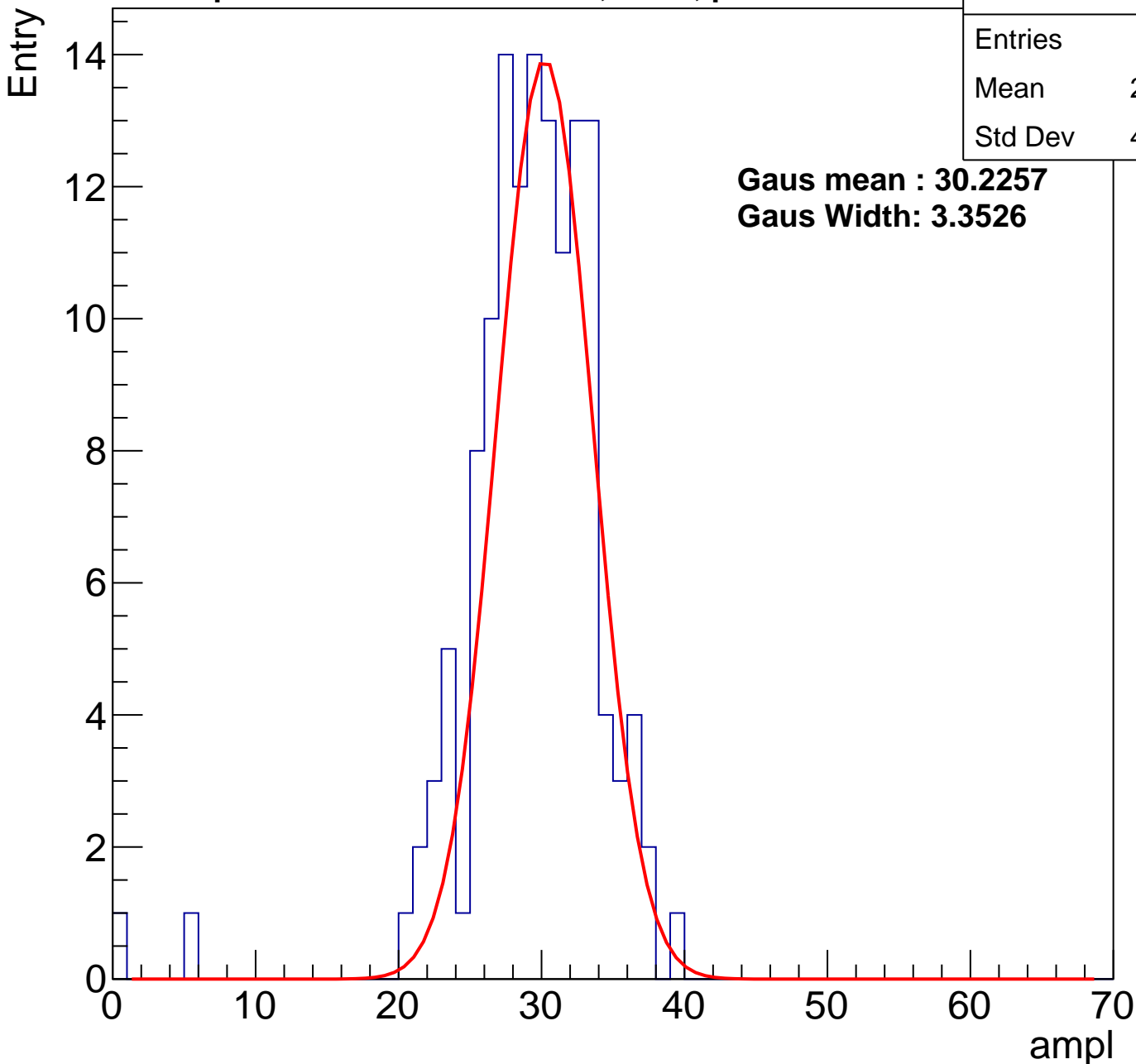


B0L101S, U18-ch62, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	28.87
Std Dev	4.908

Gaus mean : 30.2257
Gaus Width: 3.3526



B0L101S, U18-ch62, adc1

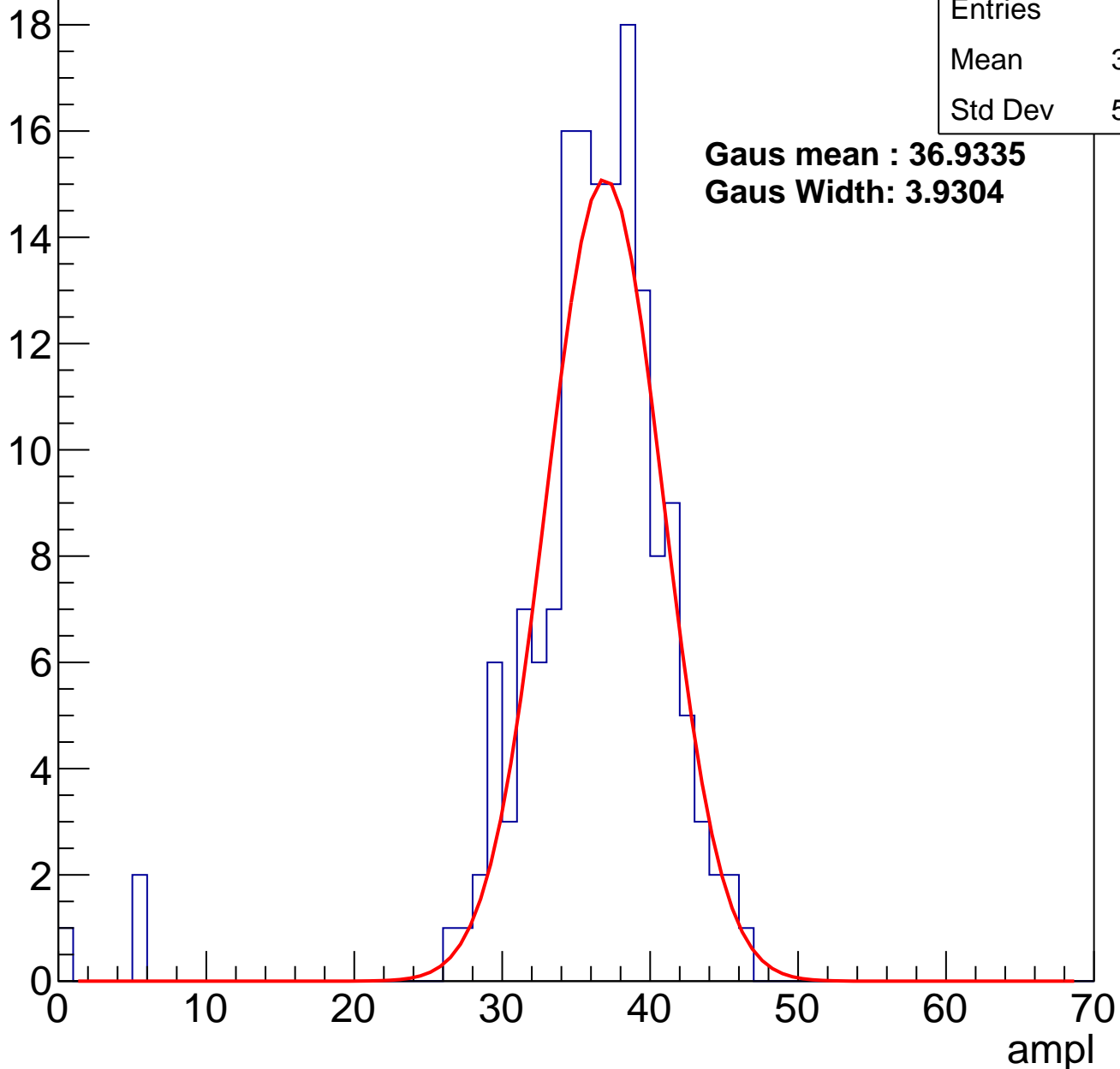
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	35.63
Std Dev	5.932

Gaus mean : 36.9335

Gaus Width: 3.9304

Entry



B0L101S, U18-ch62, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

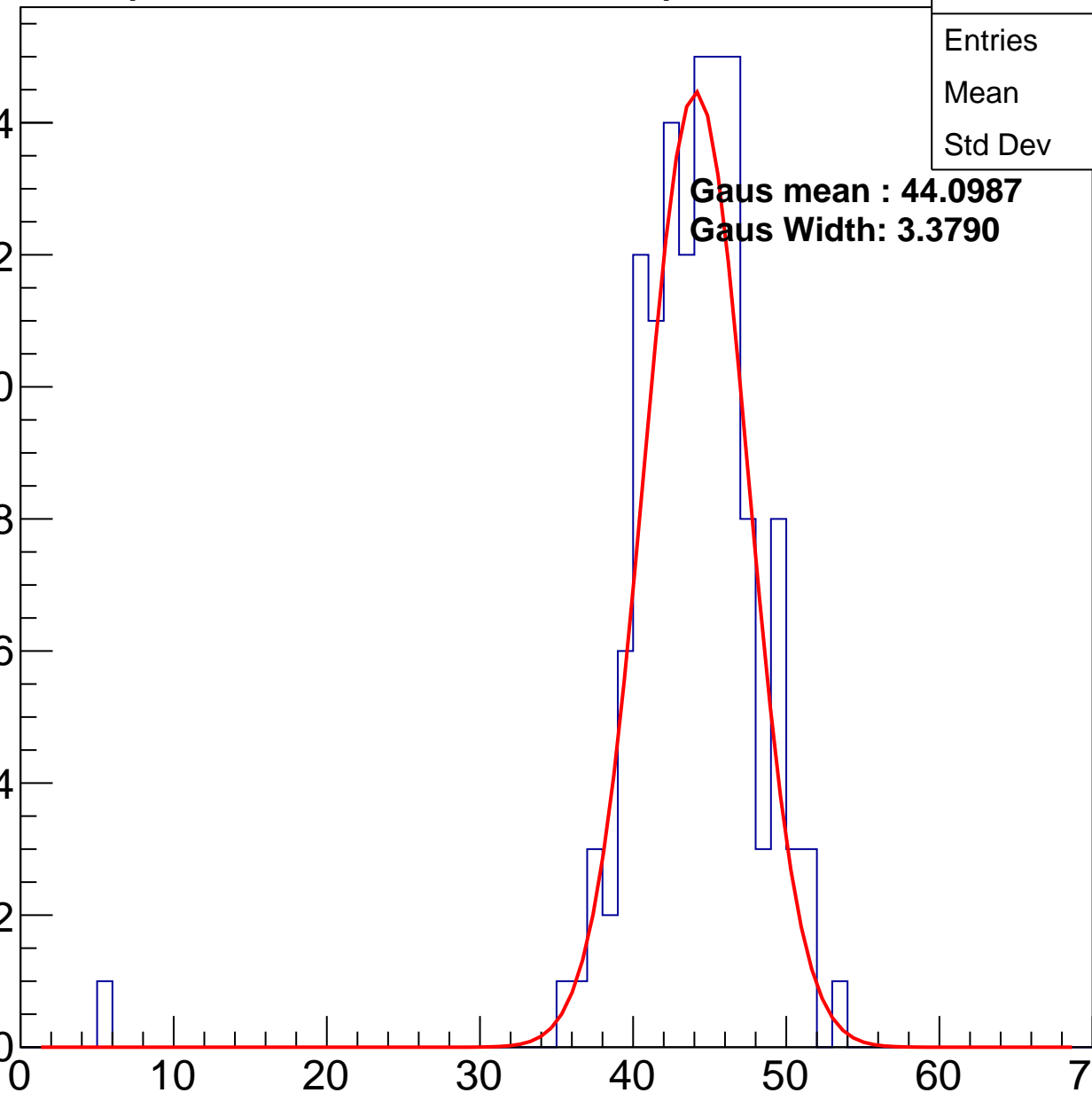
14
12
10
8
6
4
2
0

Entries	134
Mean	43.47
Std Dev	4.8

Gaus mean : 44.0987

Gaus Width: 3.3790

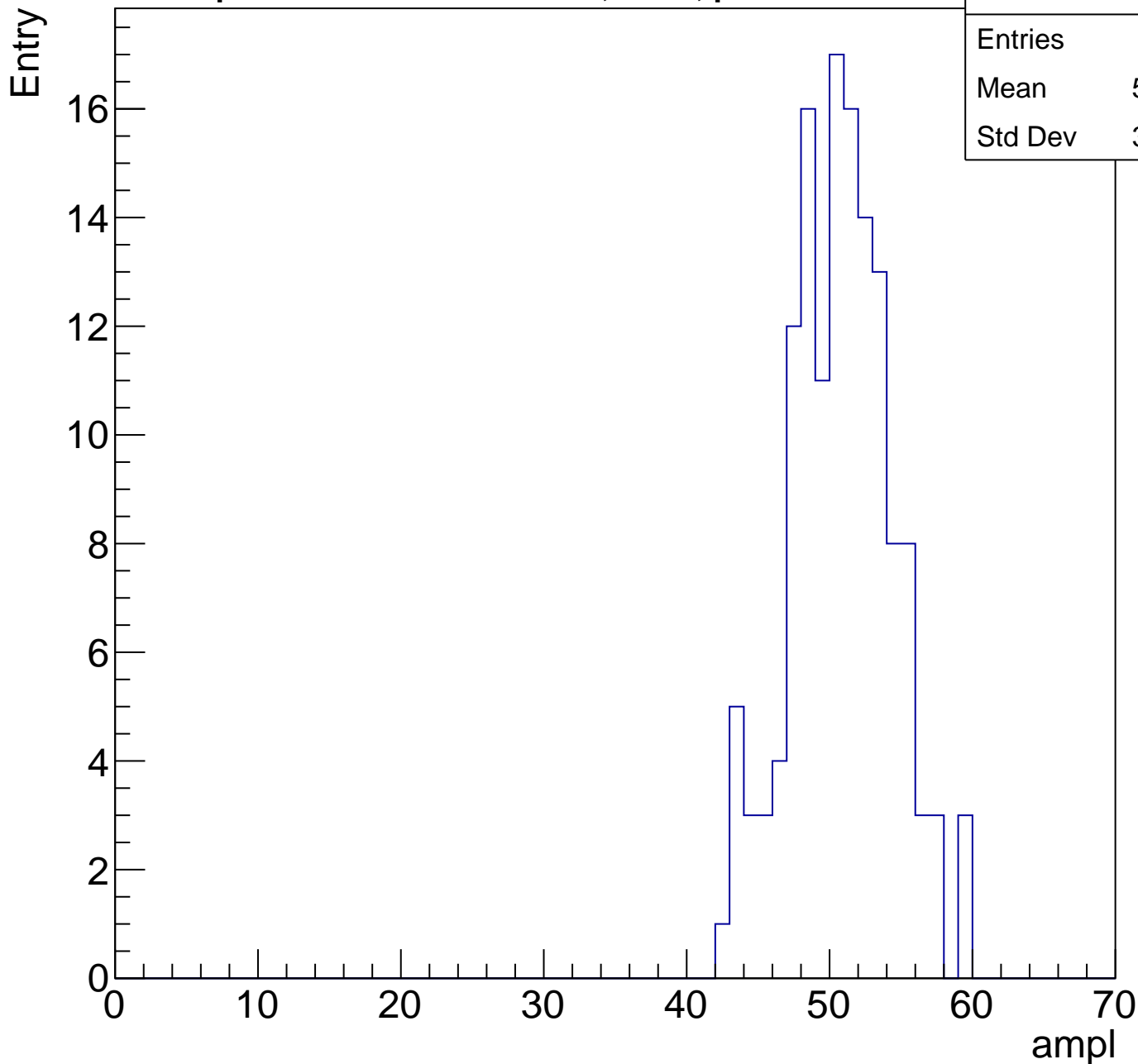
ampl



B0L101S, U18-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

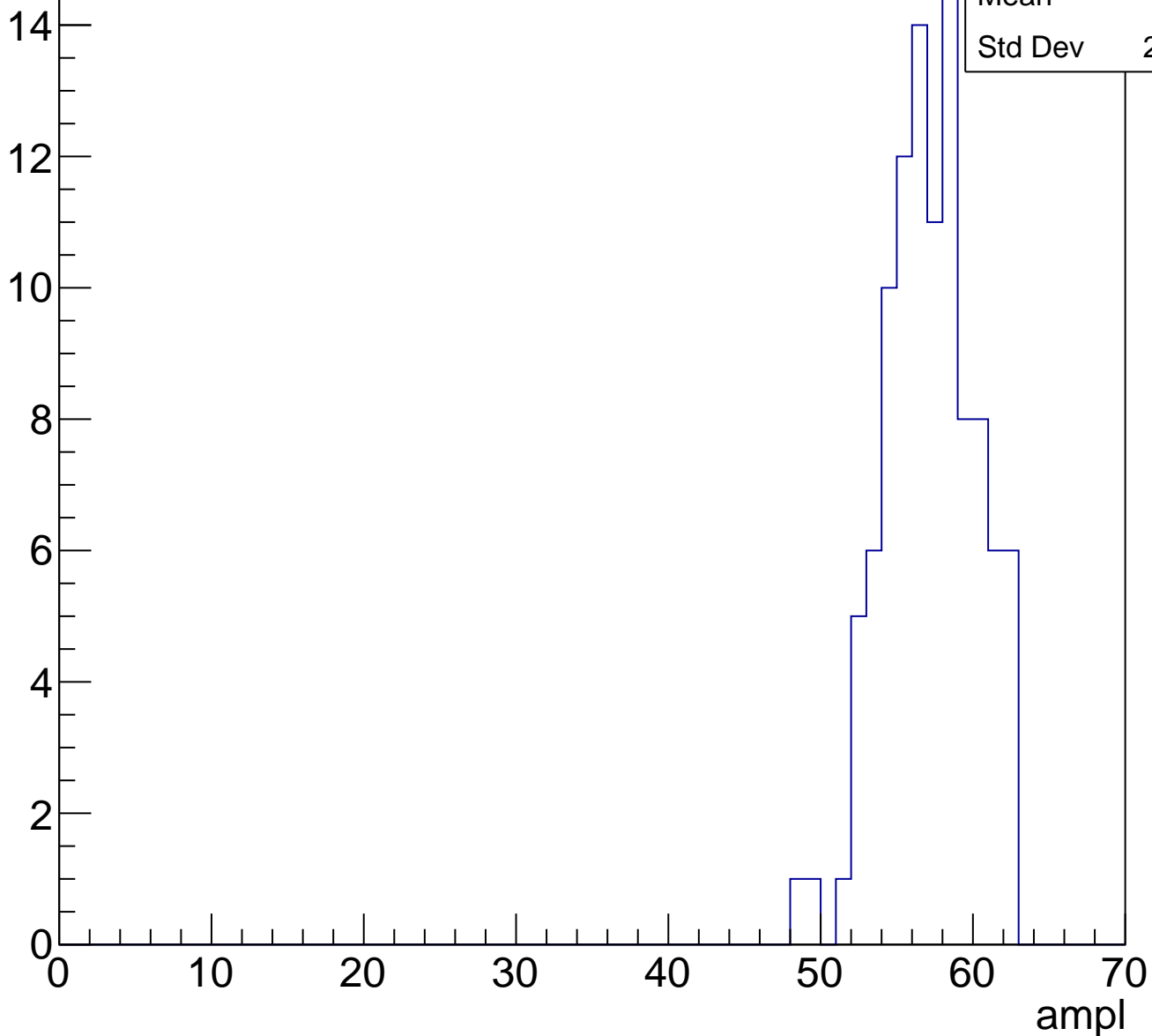
Entries	140
Mean	50.36
Std Dev	3.522



B0L101S, U18-ch62, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch62, adc5

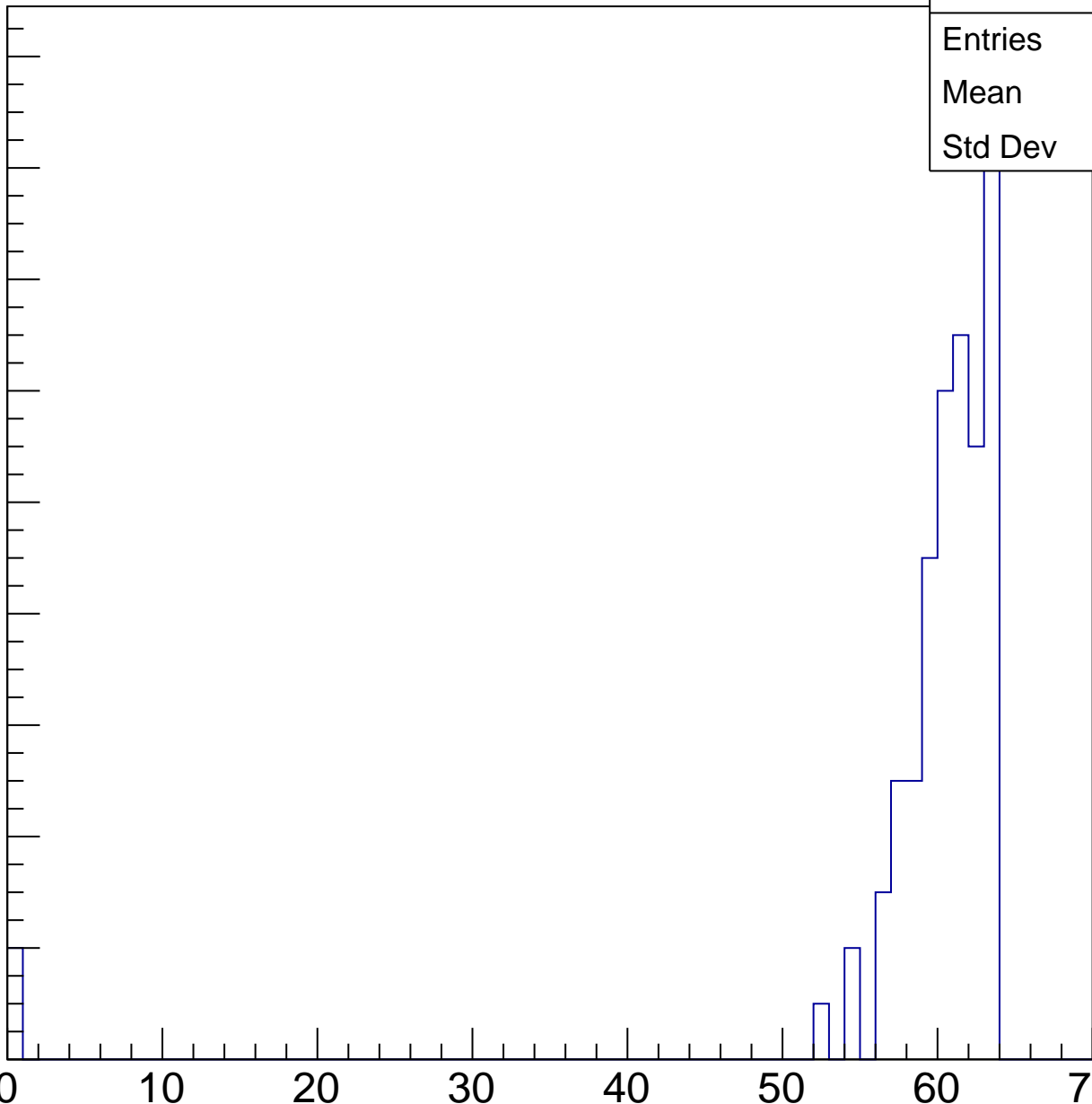
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.8
Std Dev	9.661

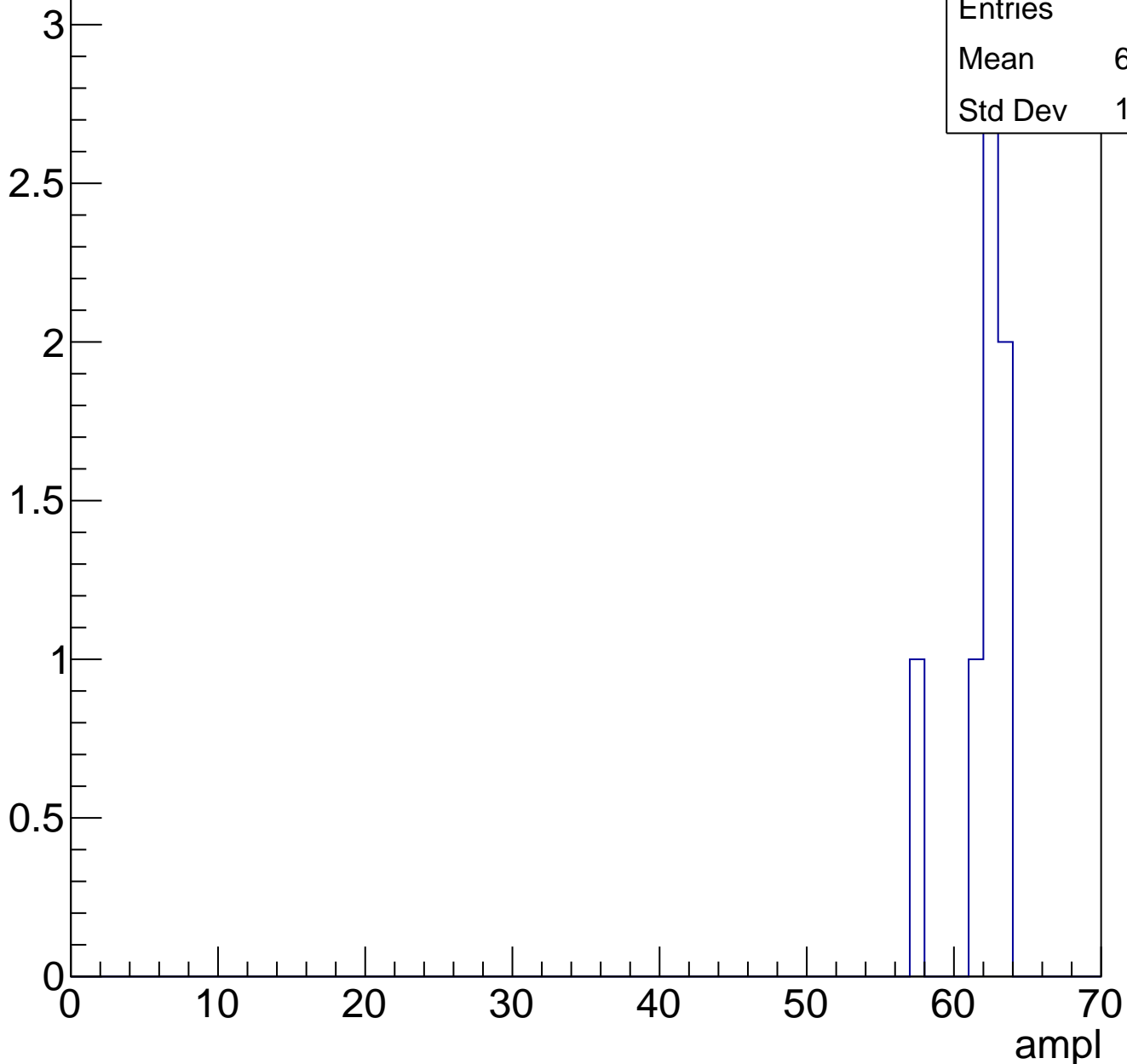
ampl



B0L101S, U18-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12.5
Std Dev	7.5

ampl

0 10 20 30 40 50 60 70

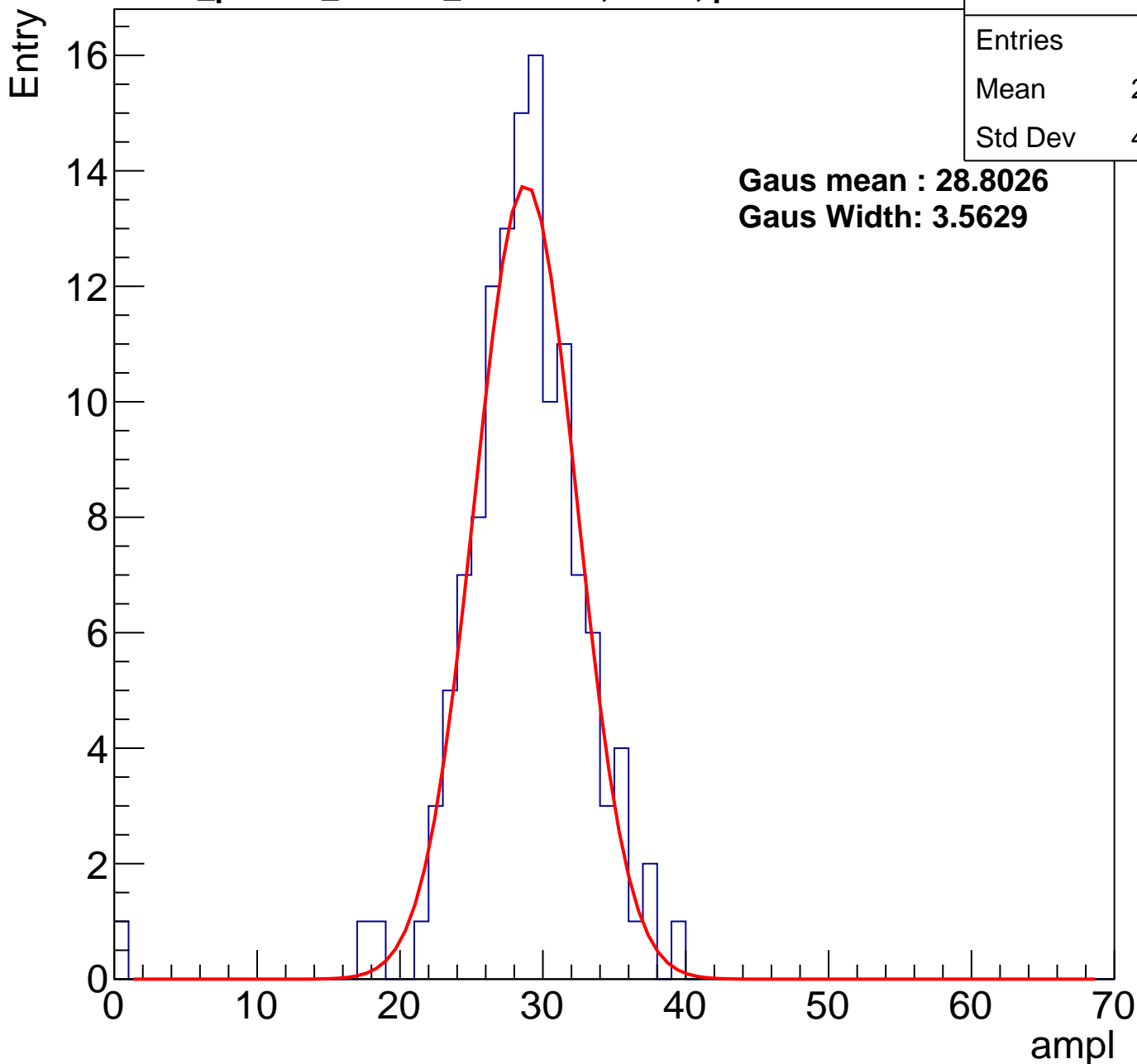
B0L101S, U18-ch63, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	28.17
Std Dev	4.504

Gaus mean : 28.8026

Gaus Width: 3.5629



B0L101S, U18-ch63, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	35.01
Std Dev	3.602

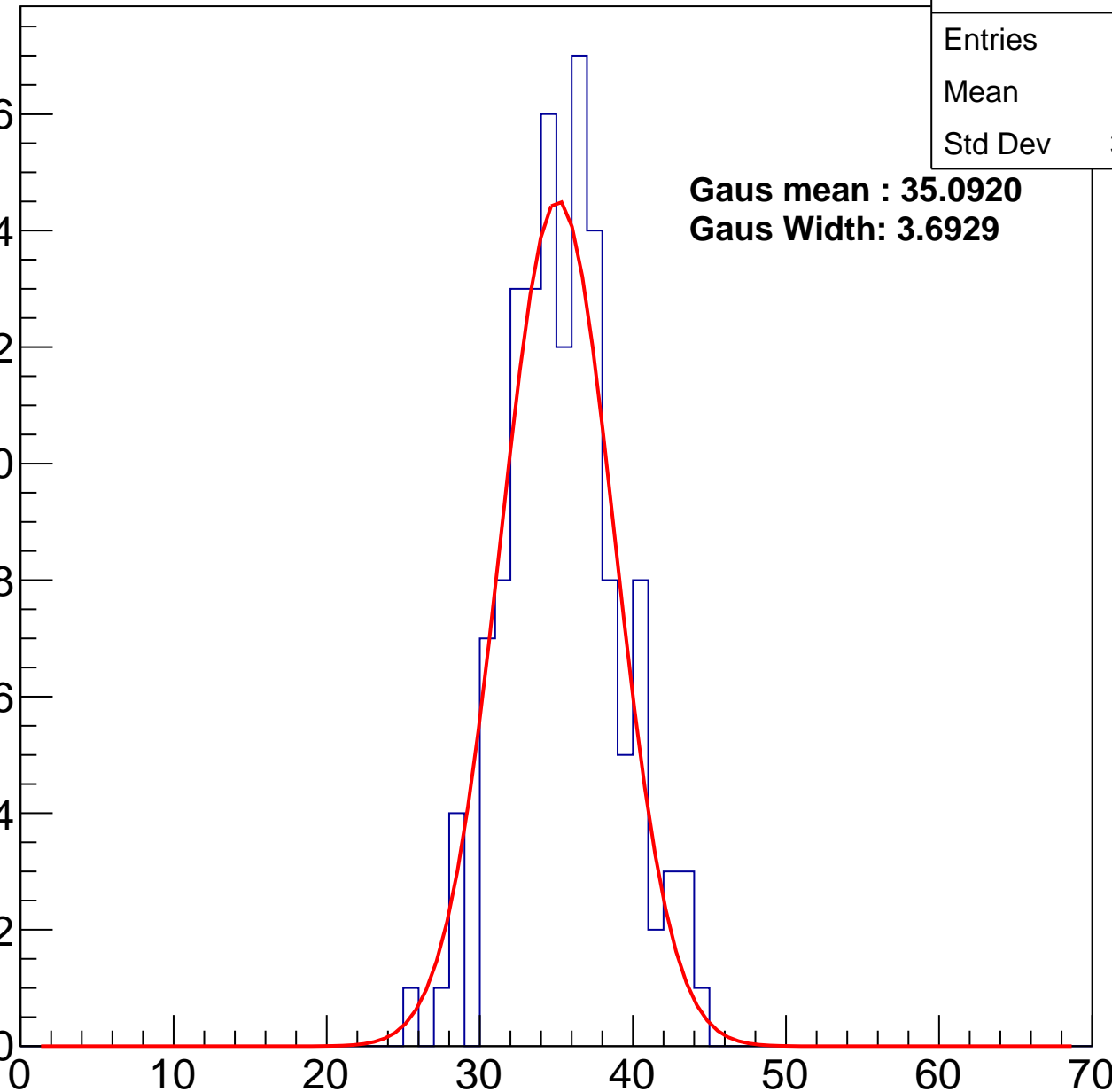
Gaus mean : 35.0920

Gaus Width: 3.6929

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch63, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	41.71
Std Dev	3.603

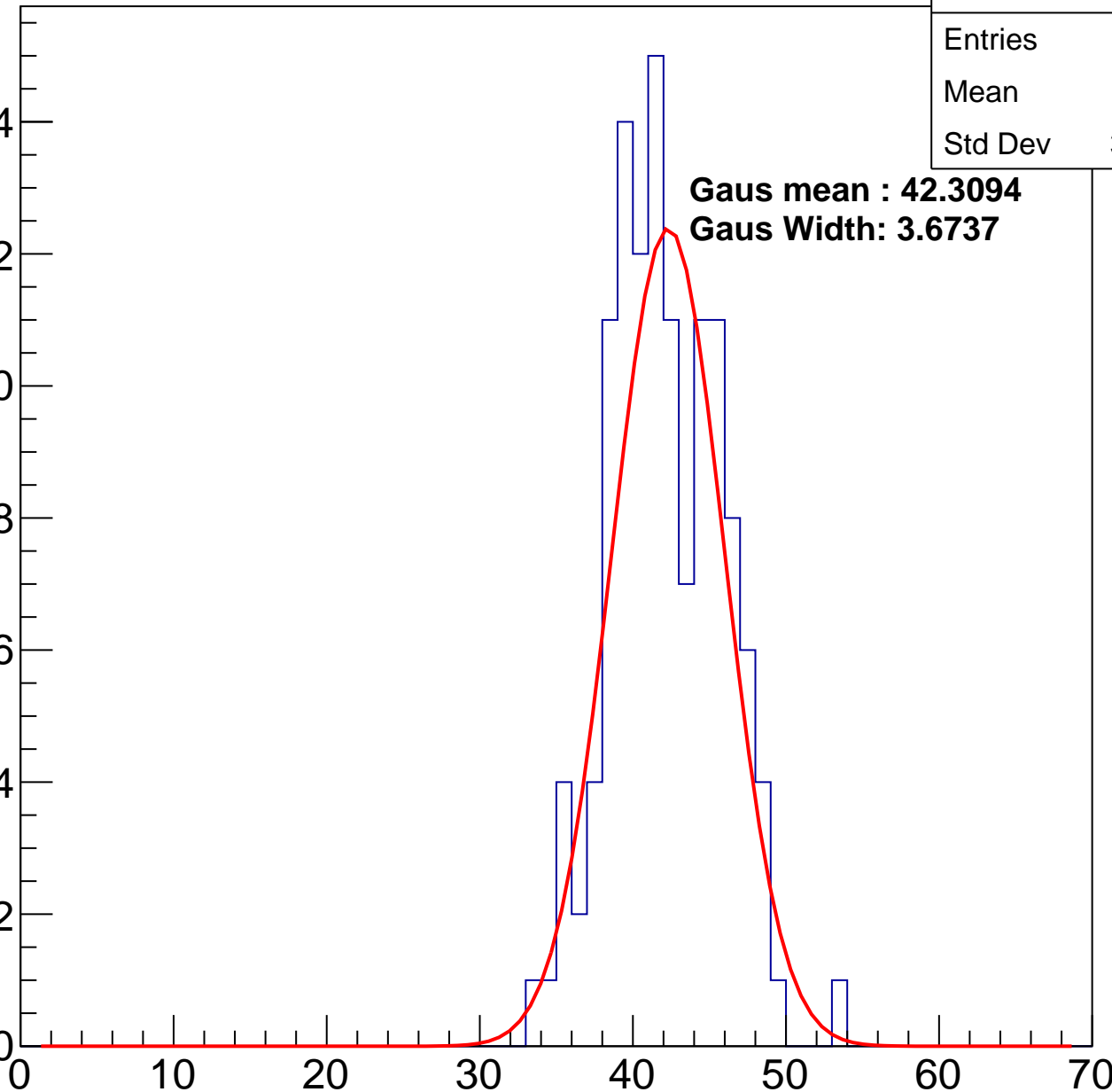
Gaus mean : 42.3094

Gaus Width: 3.6737

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

163

Mean

48.48

Std Dev

3.844

ampl

0

10

20

30

40

50

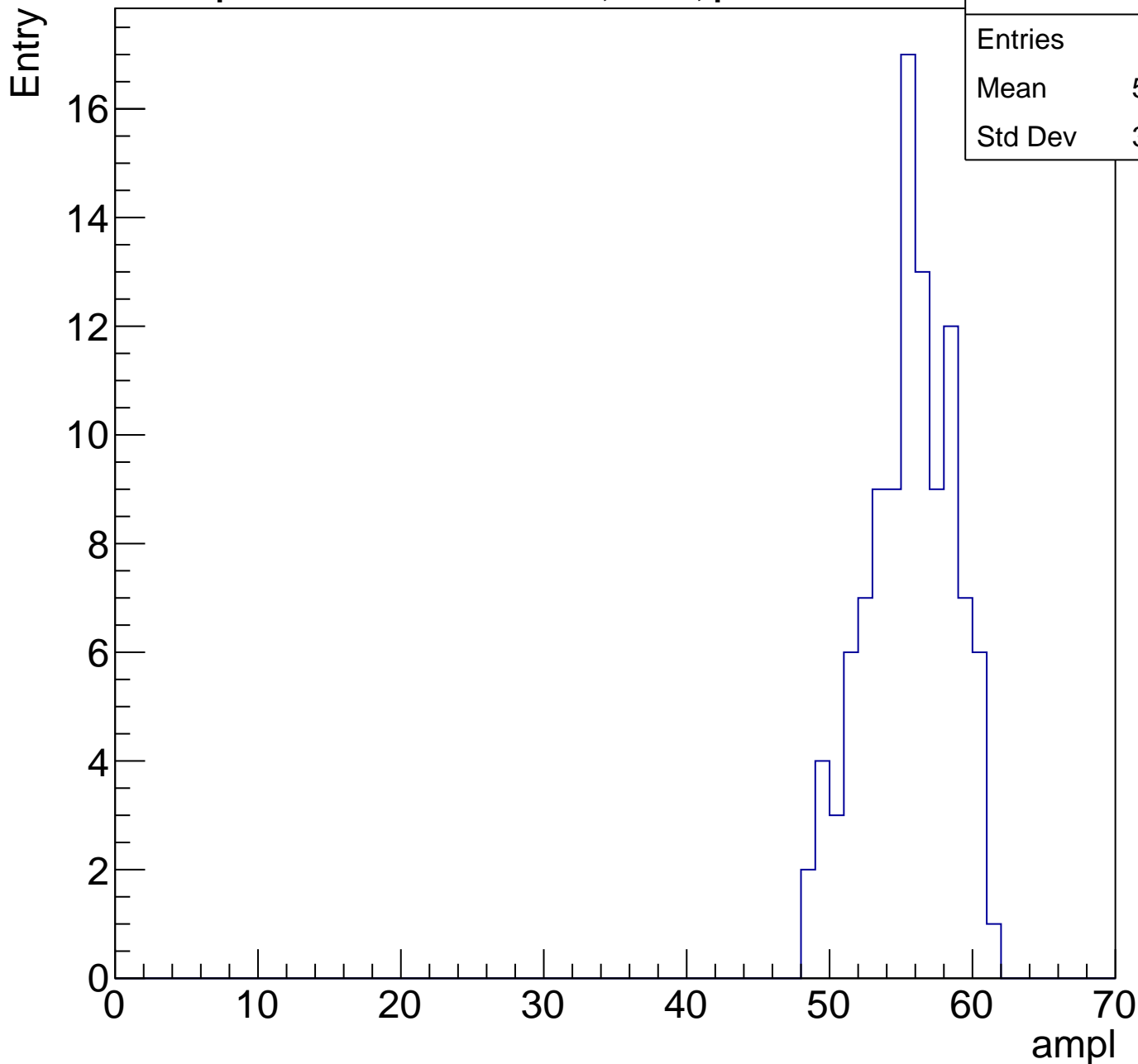
60

70

B0L101S, U18-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	55.06
Std Dev	3.048



B0L101S, U18-ch63, adc5

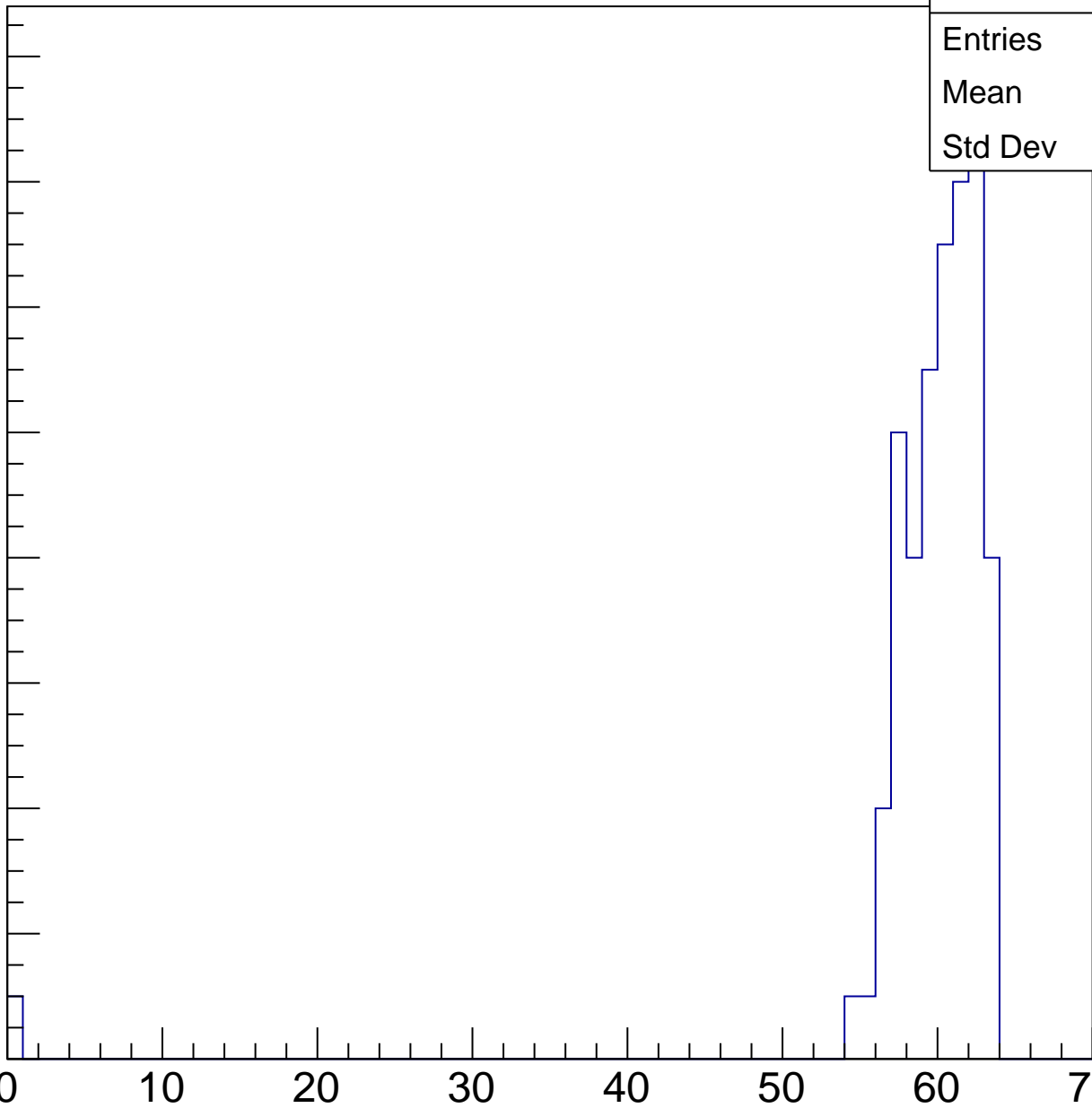
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.15
Std Dev	6.734

ampl



B0L101S, U18-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	25
Mean	59.48
Std Dev	12.21

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

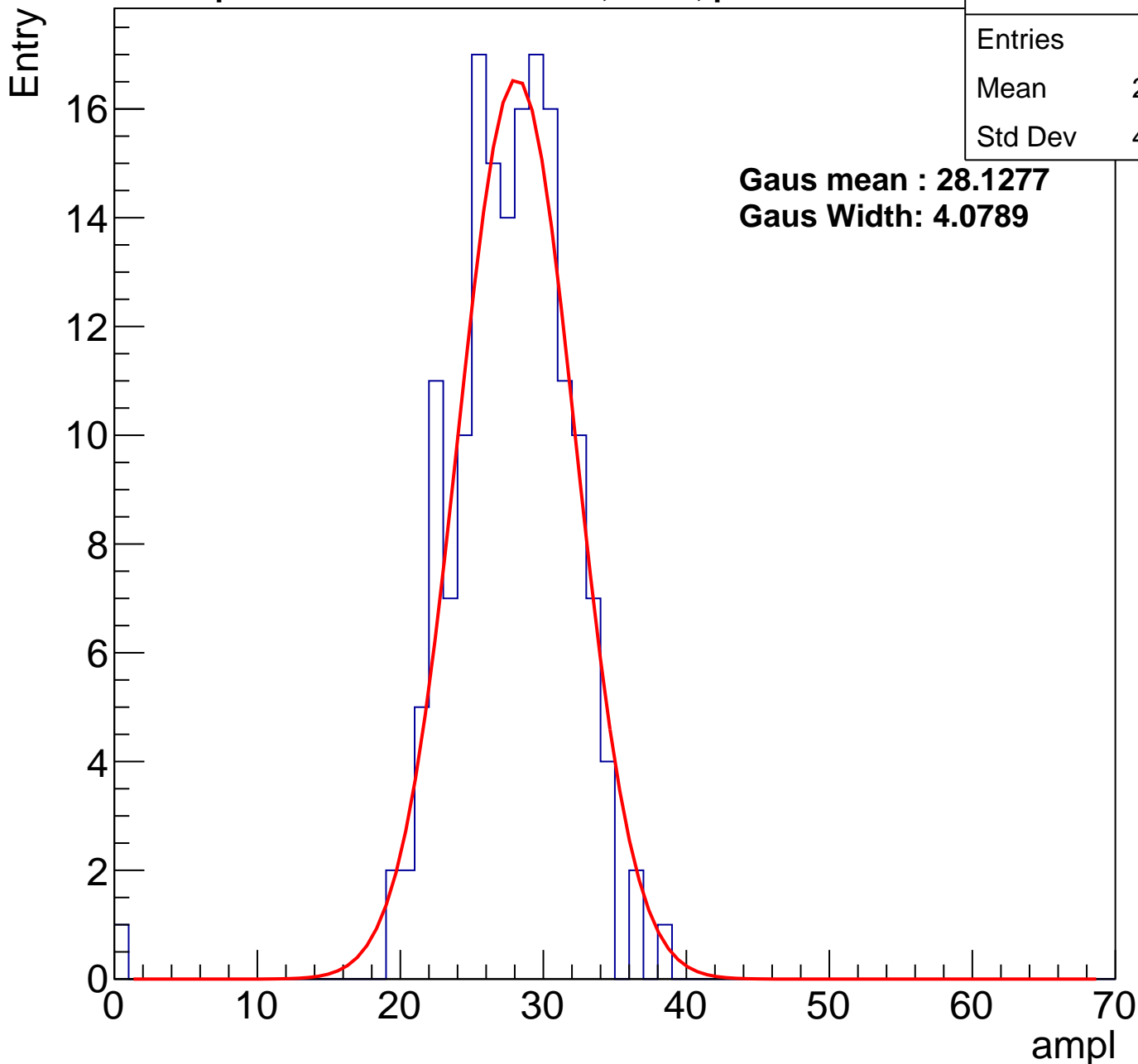
B0L101S, U18-ch64, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	27.25
Std Dev	4.274

Gaus mean : 28.1277

Gaus Width: 4.0789



B0L101S, U18-ch64, adc1

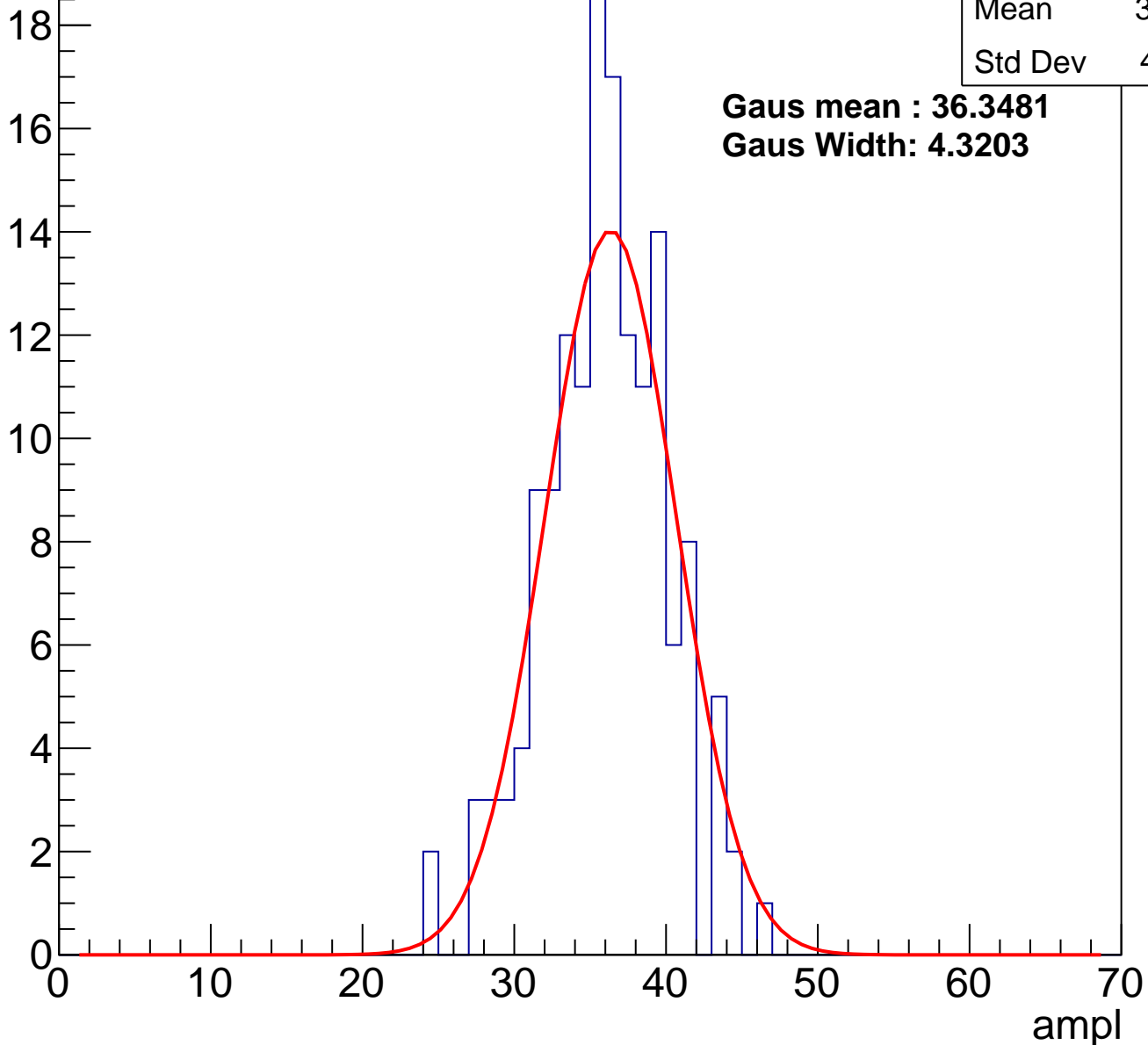
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	35.49
Std Dev	4.041

Gaus mean : 36.3481

Gaus Width: 4.3203

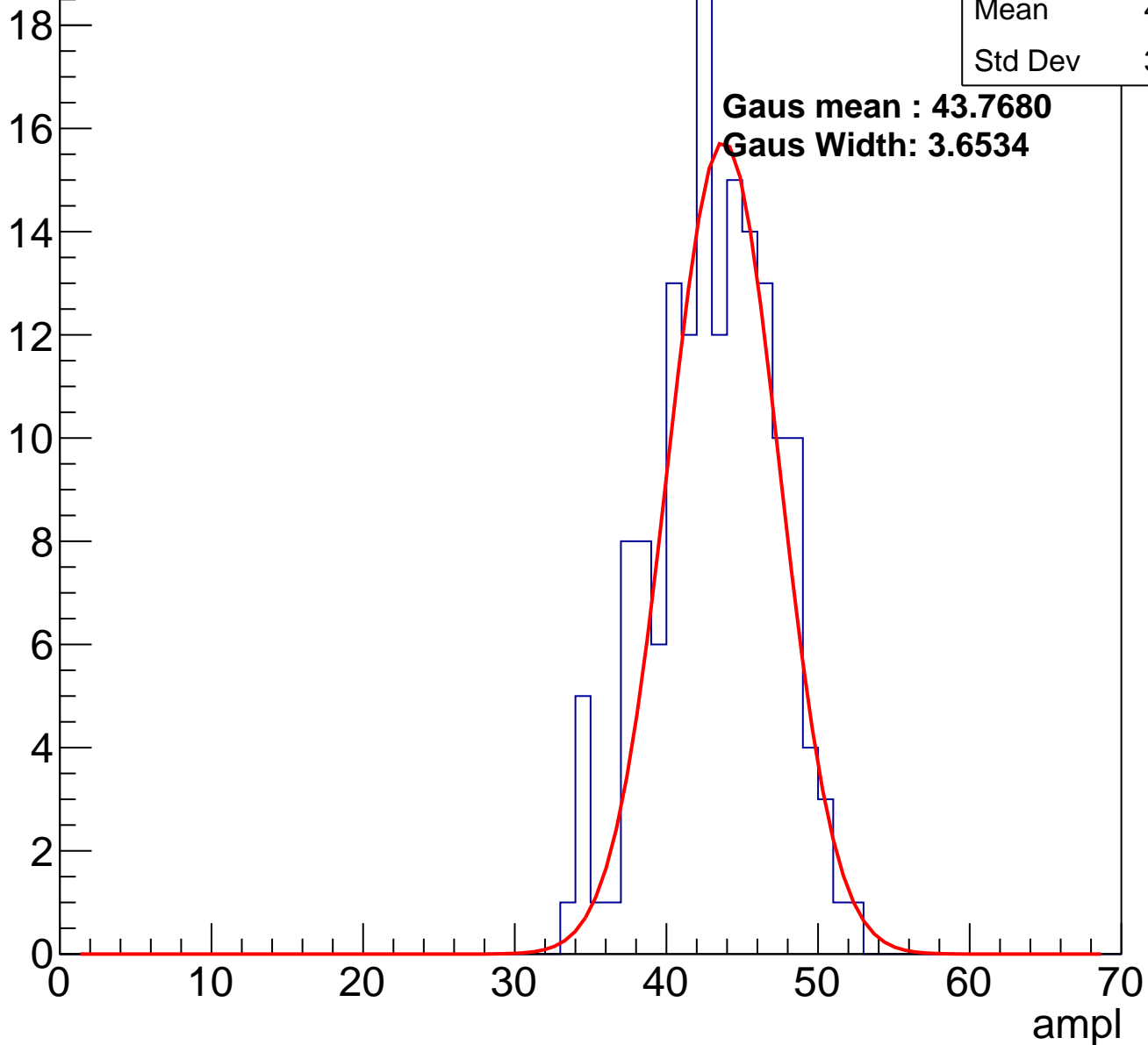
Entry



B0L101S, U18-ch64, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch64, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	49.51
Std Dev	3.413

Entry

12

10

8

6

4

2

0

0

10

20

30

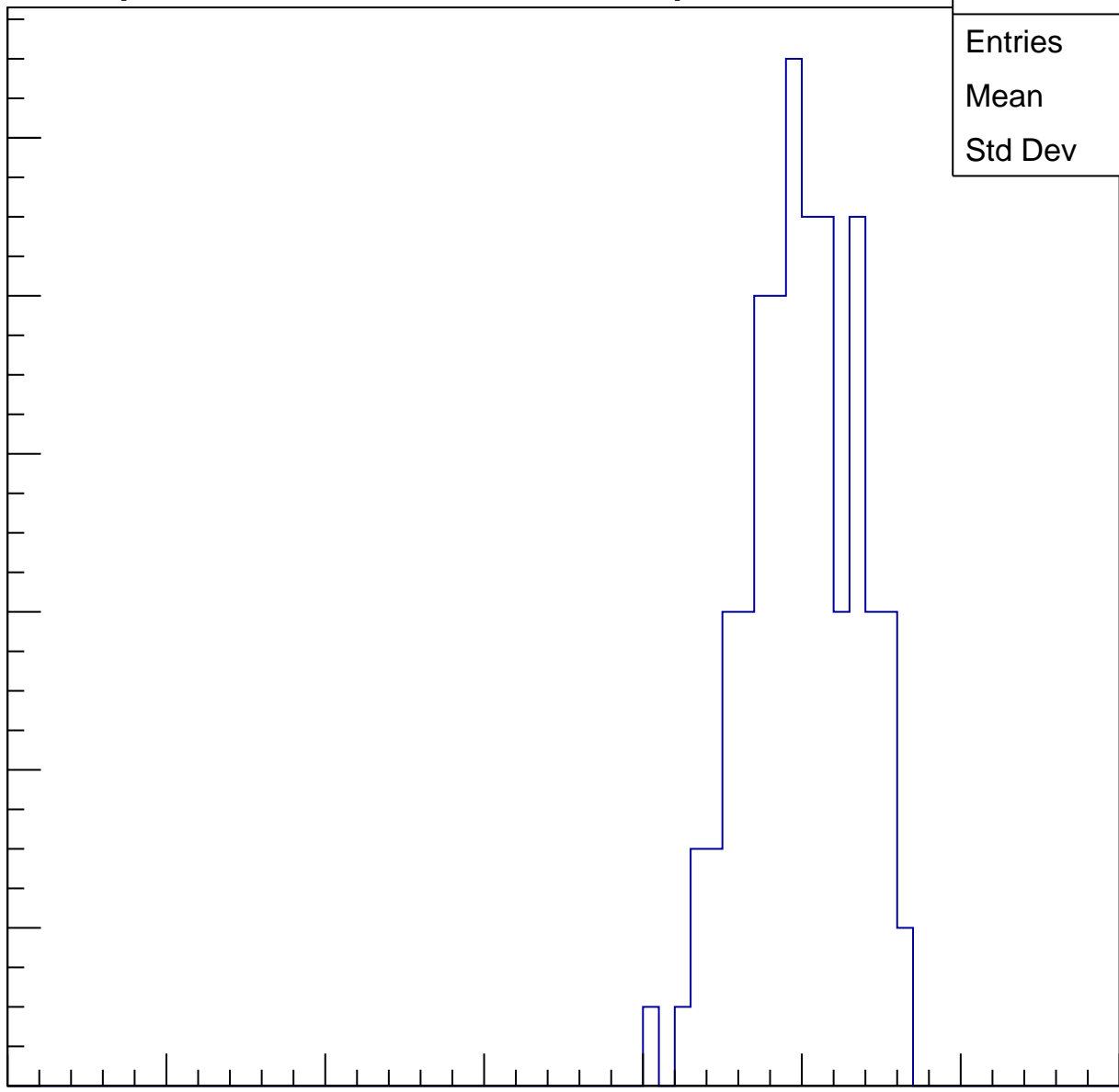
40

50

60

70

ampl



B0L101S, U18-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

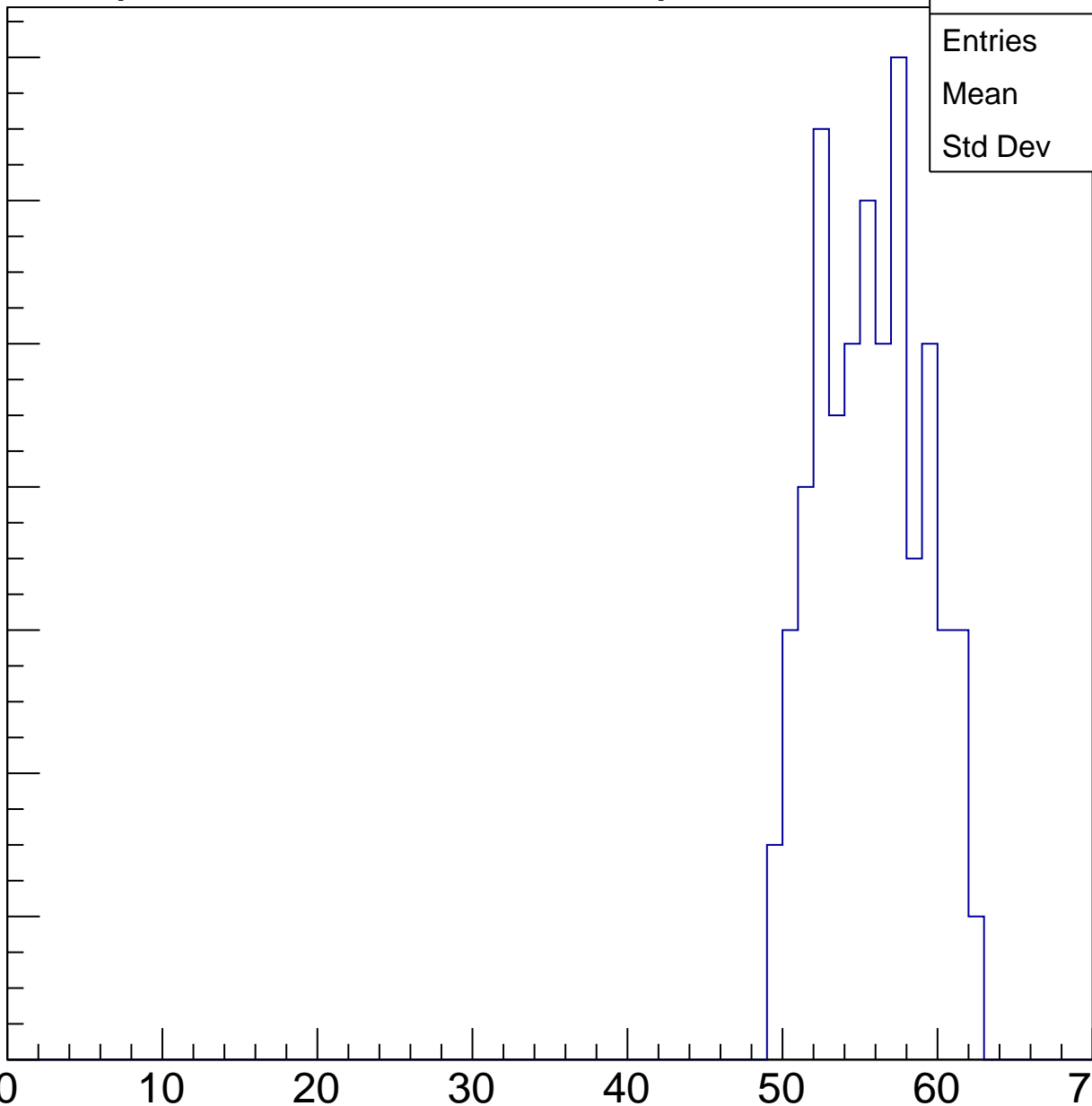
Entries	116
Mean	55.28
Std Dev	3.339

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch64, adc5

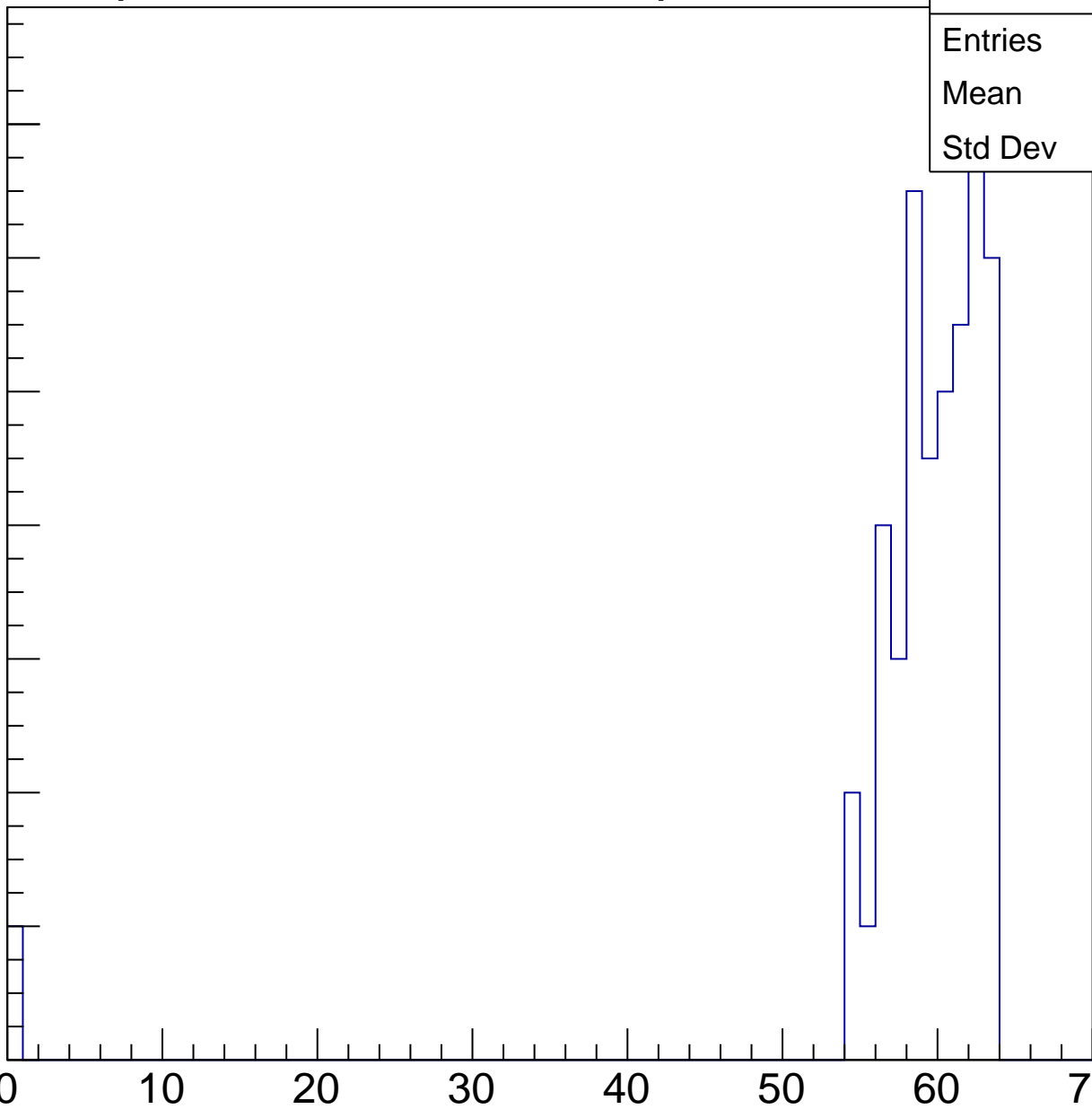
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	58.24
Std Dev	9.047

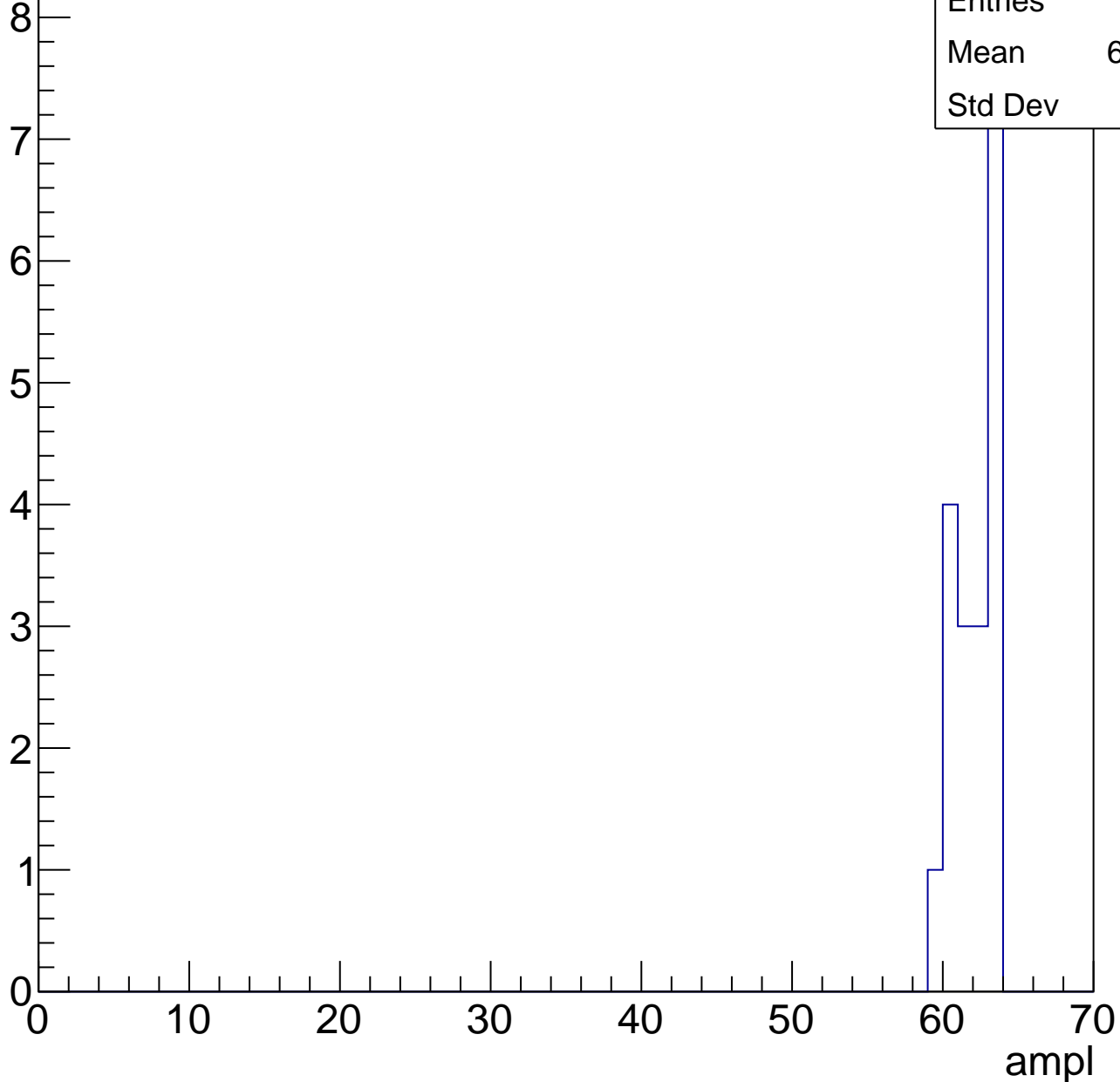
ampl



B0L101S, U18-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch65, adc0

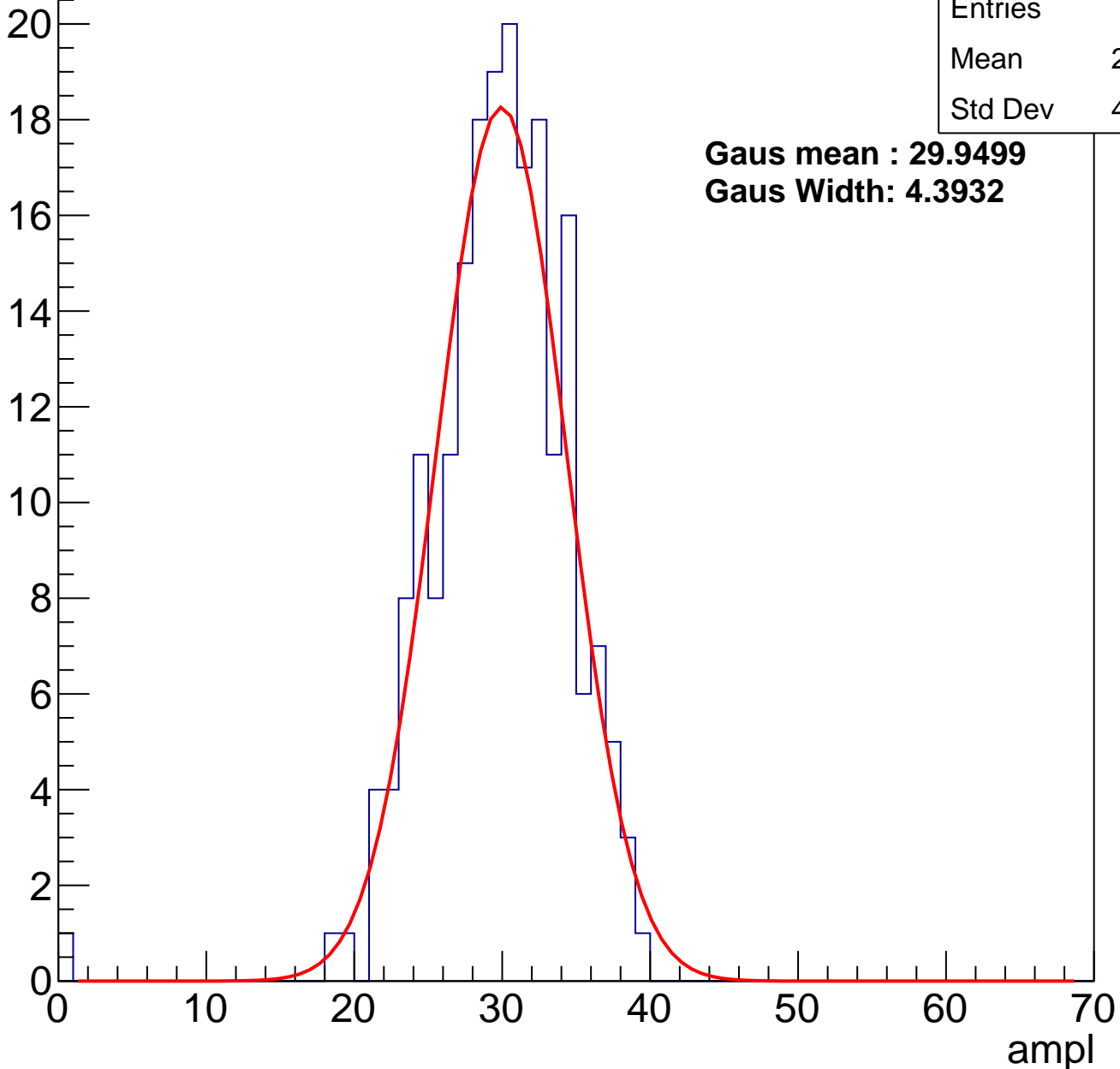
calib_packv5_042523_0143.root, FC#1, port C1

Entries	205
Mean	29.33
Std Dev	4.637

Gaus mean : 29.9499

Gaus Width: 4.3932

Entry



B0L101S, U18-ch65, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

99

Mean

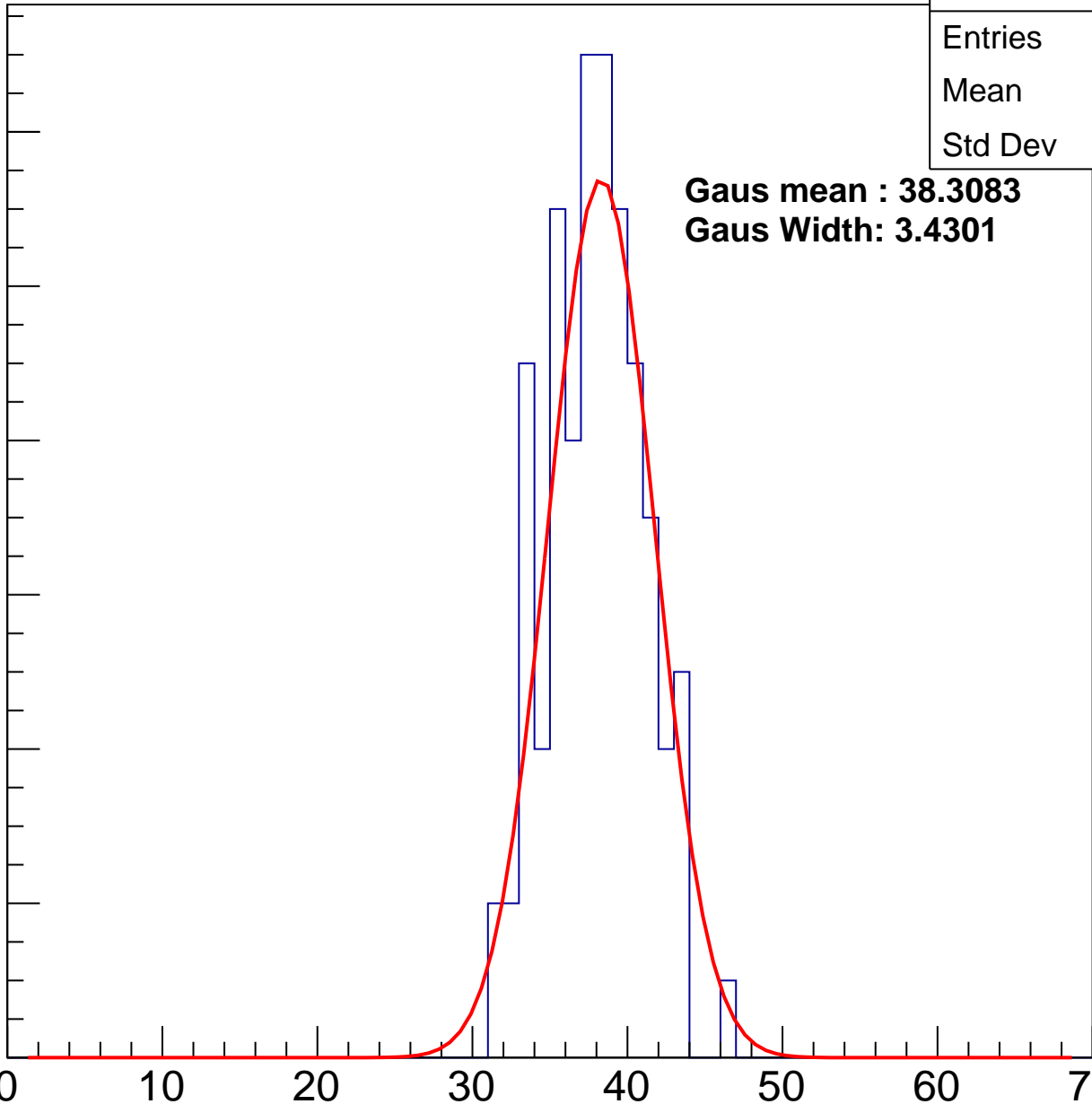
37.49

Std Dev

3.083

Gaus mean : 38.3083

Gaus Width: 3.4301



B0L101S, U18-ch65, adc2

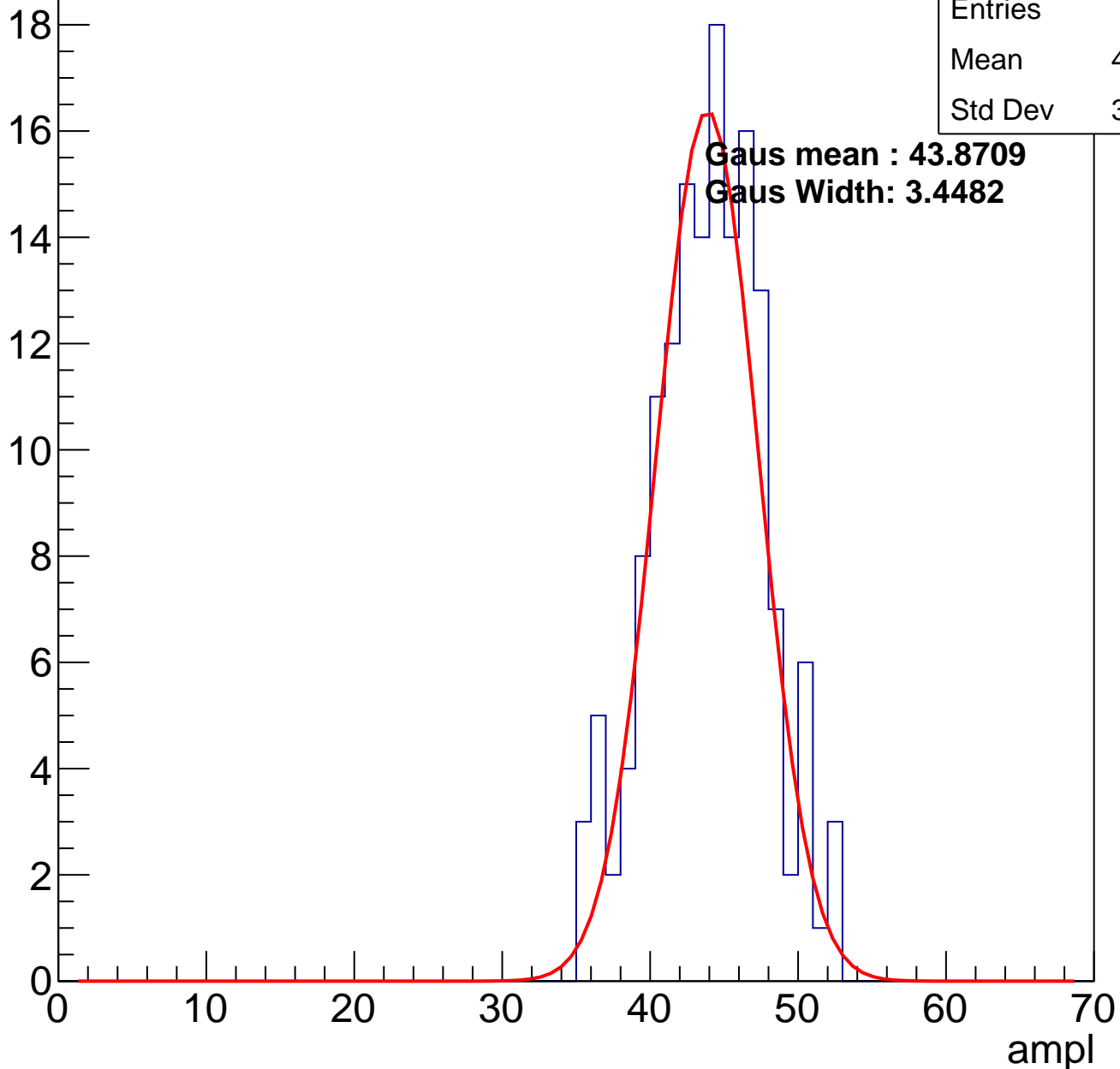
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	43.49
Std Dev	3.725

Gaus mean : 43.8709

Gaus Width: 3.4482

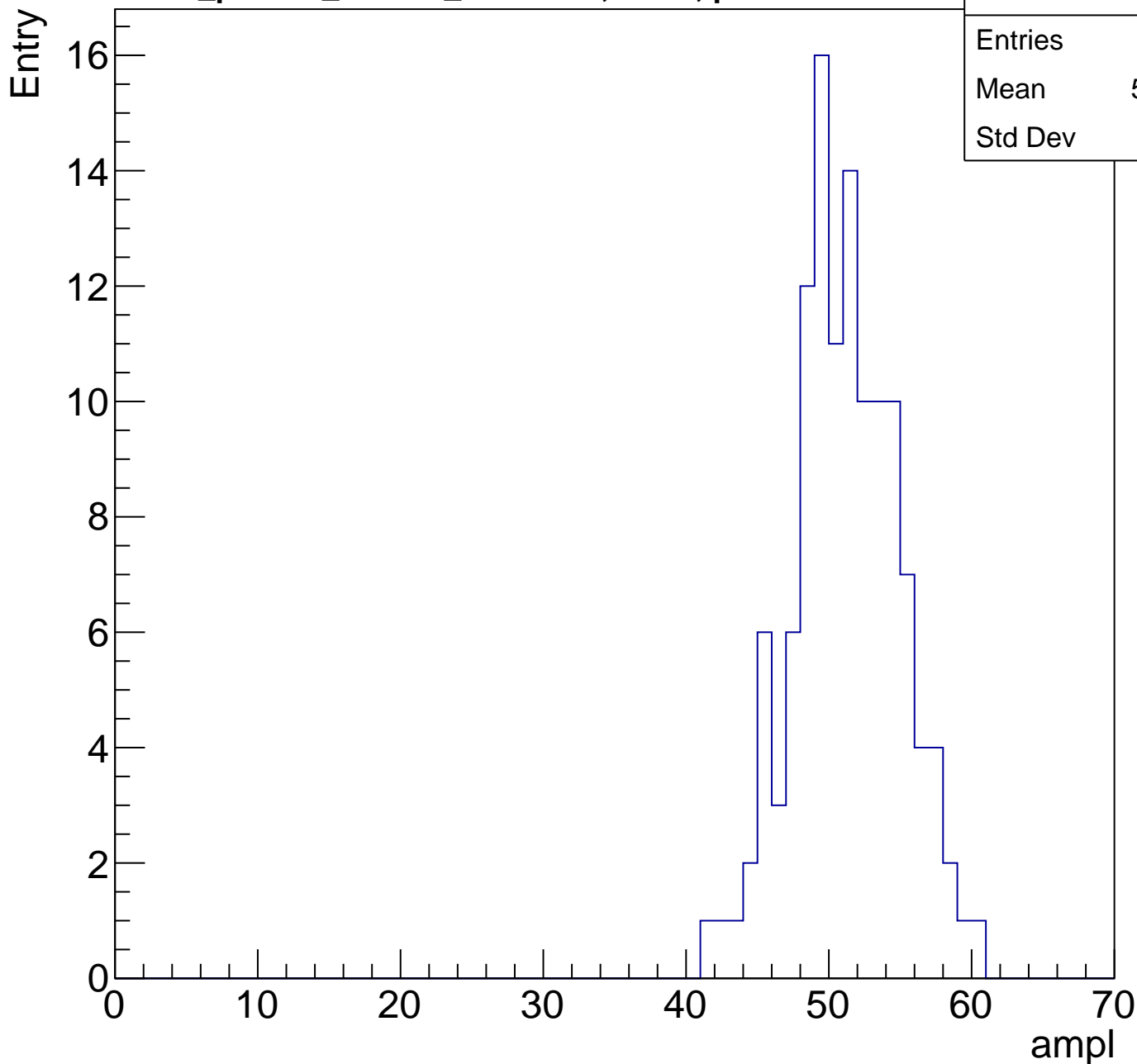
Entry



B0L101S, U18-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	50.74
Std Dev	3.67



B0L101S, U18-ch65, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	55.82
Std Dev	3.471

Entry

12

10

8

6

4

2

0

0

10

20

30

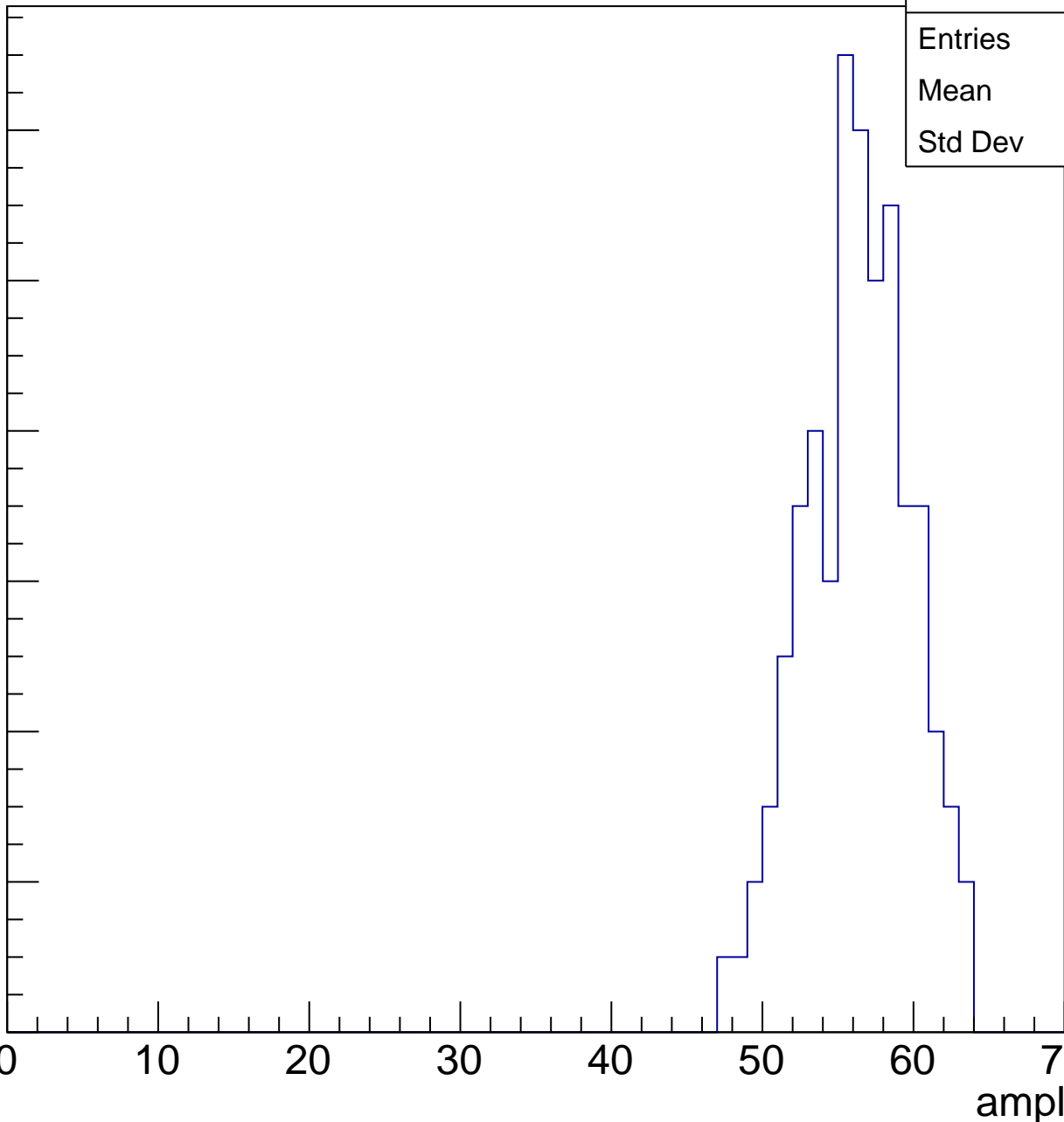
40

50

60

70

ampl



B0L101S, U18-ch65, adc5

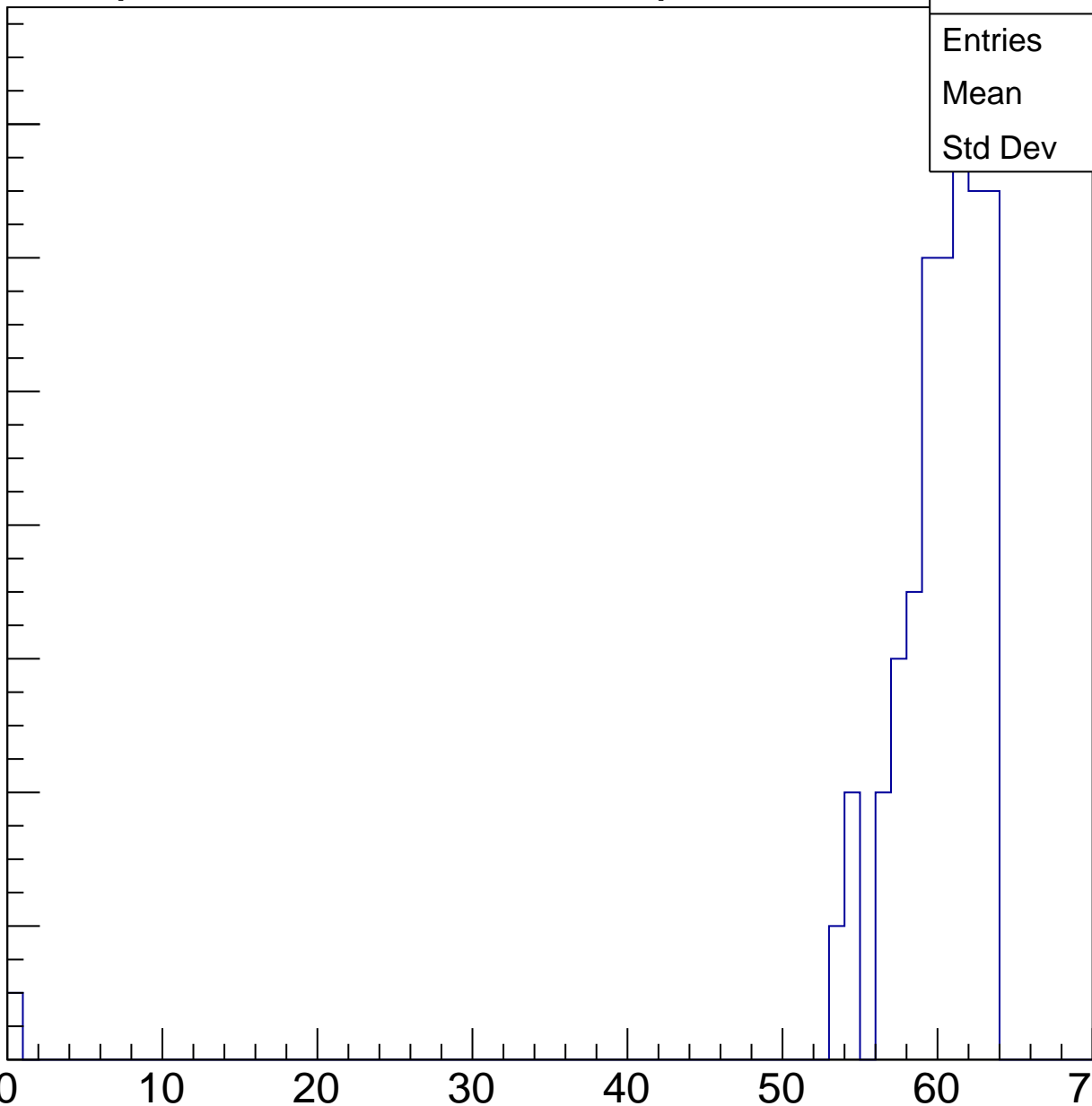
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	59.12
Std Dev	6.8

ampl

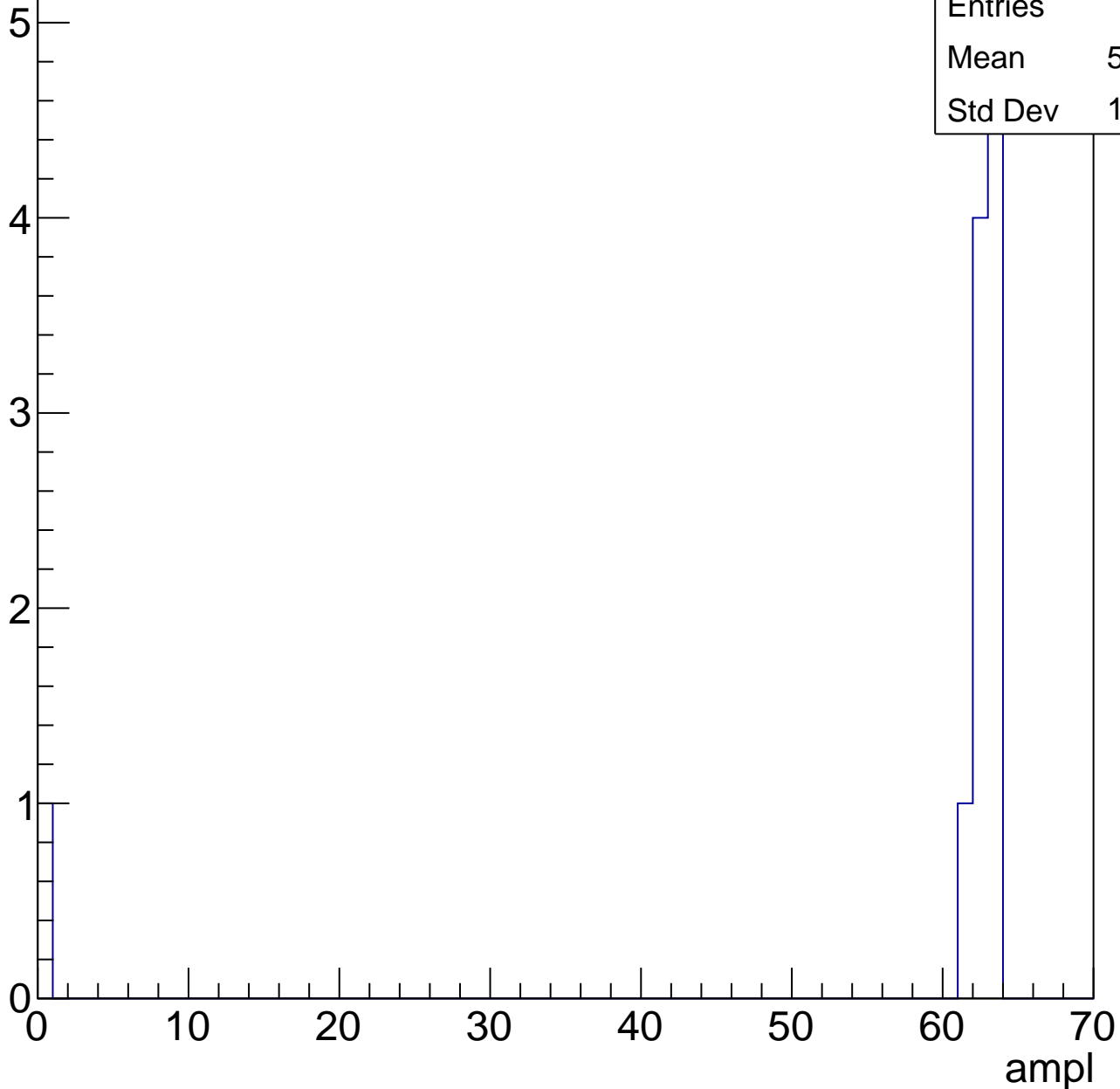


B0L101S, U18-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	56.73
Std Dev	17.95



B0L101S, U18-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U18-ch66, adc0

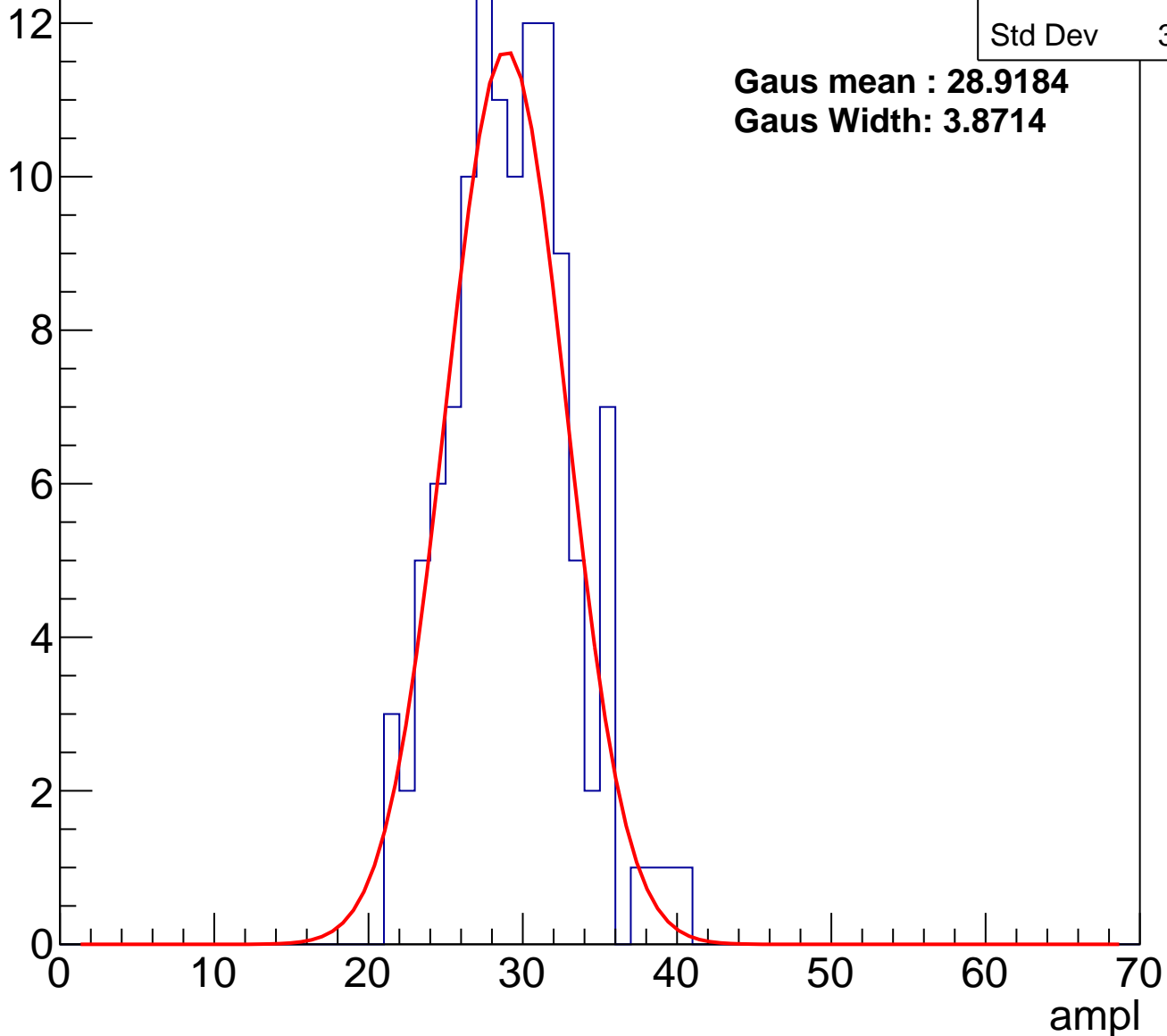
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	28.83
Std Dev	3.874

Gaus mean : 28.9184

Gaus Width: 3.8714

Entry



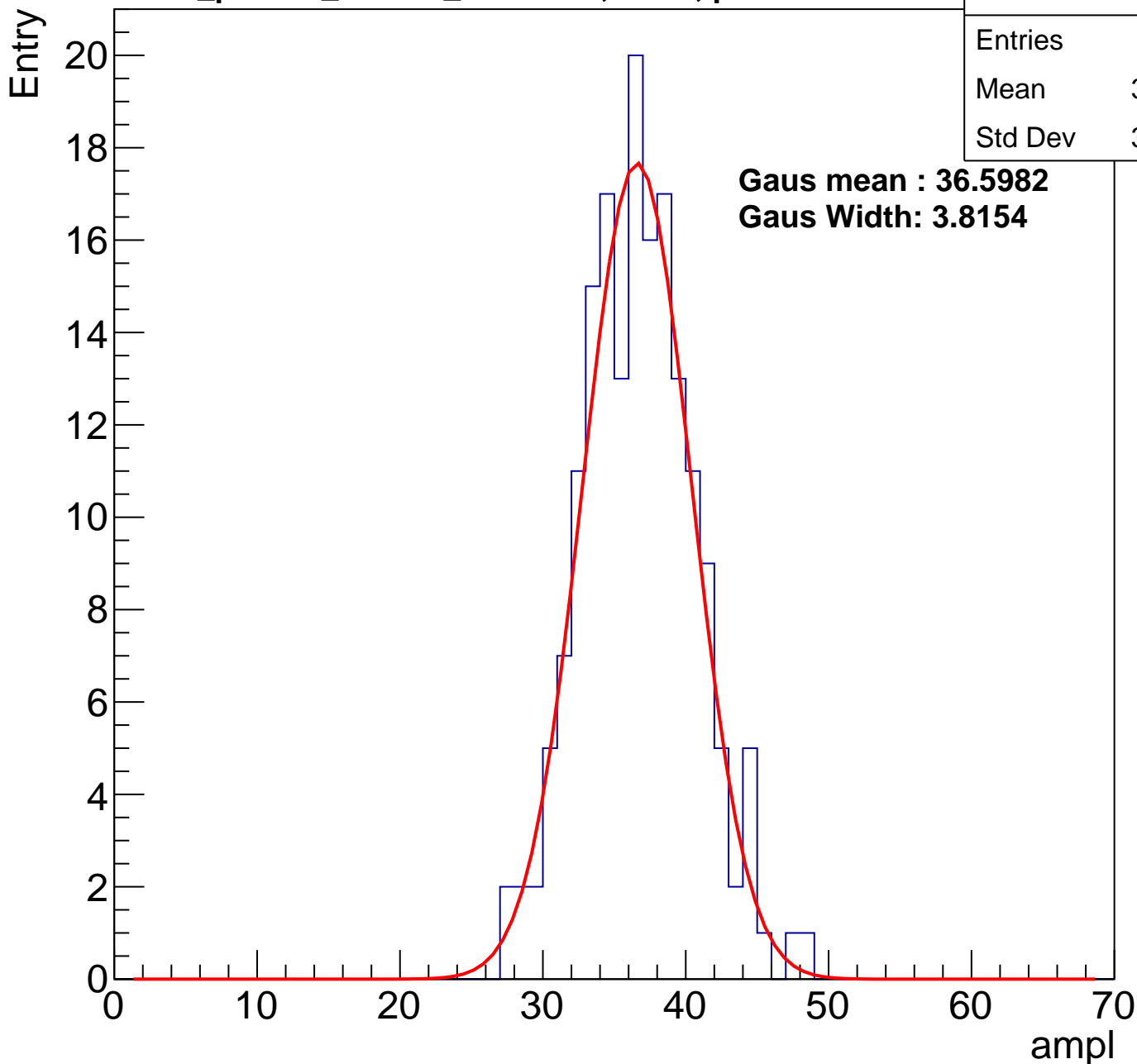
B0L101S, U18-ch66, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	36.26
Std Dev	3.906

Gaus mean : 36.5982

Gaus Width: 3.8154



B0L101S, U18-ch66, adc2

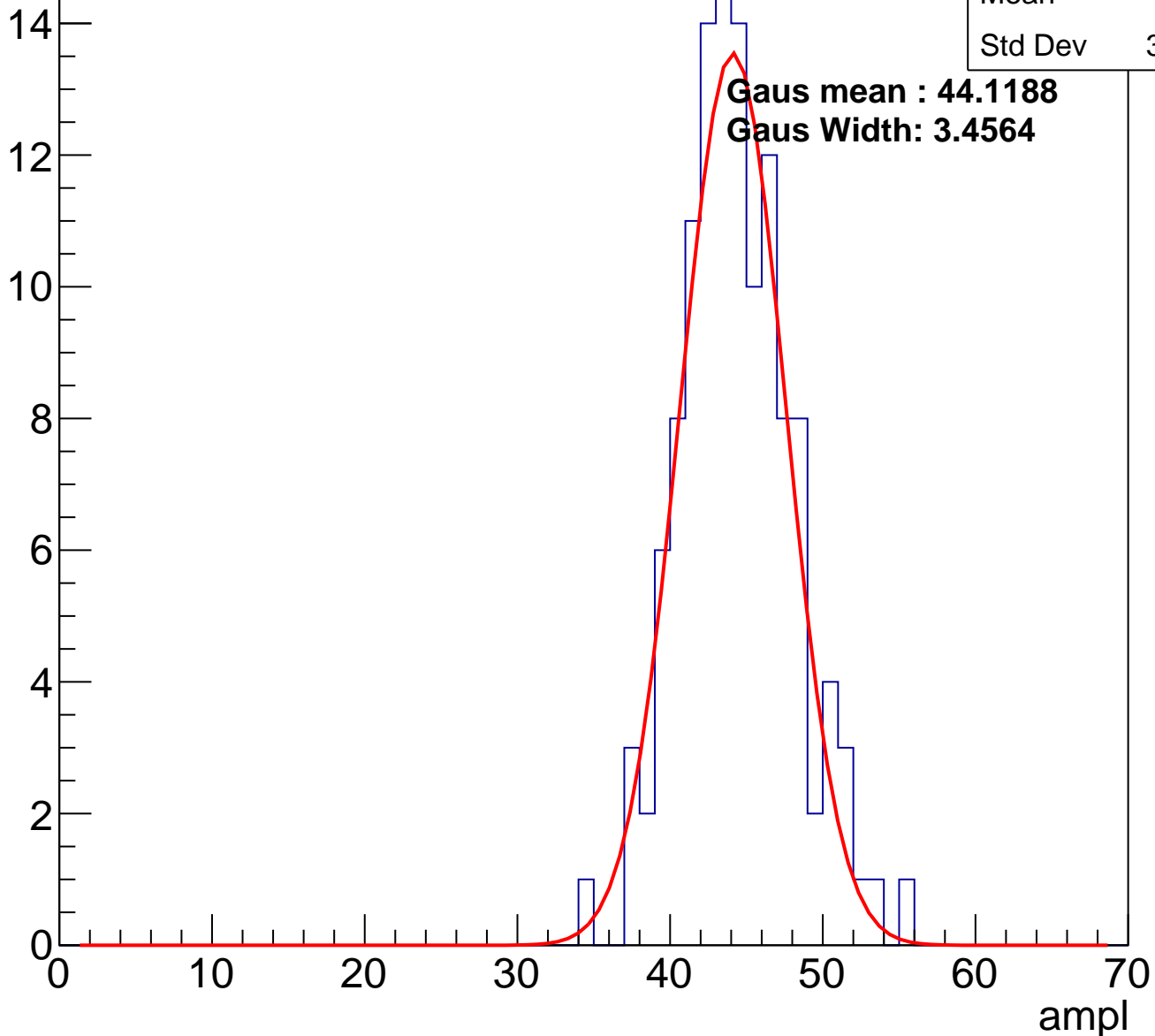
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	43.94
Std Dev	3.627

Gaus mean : 44.1188

Gaus Width: 3.4564

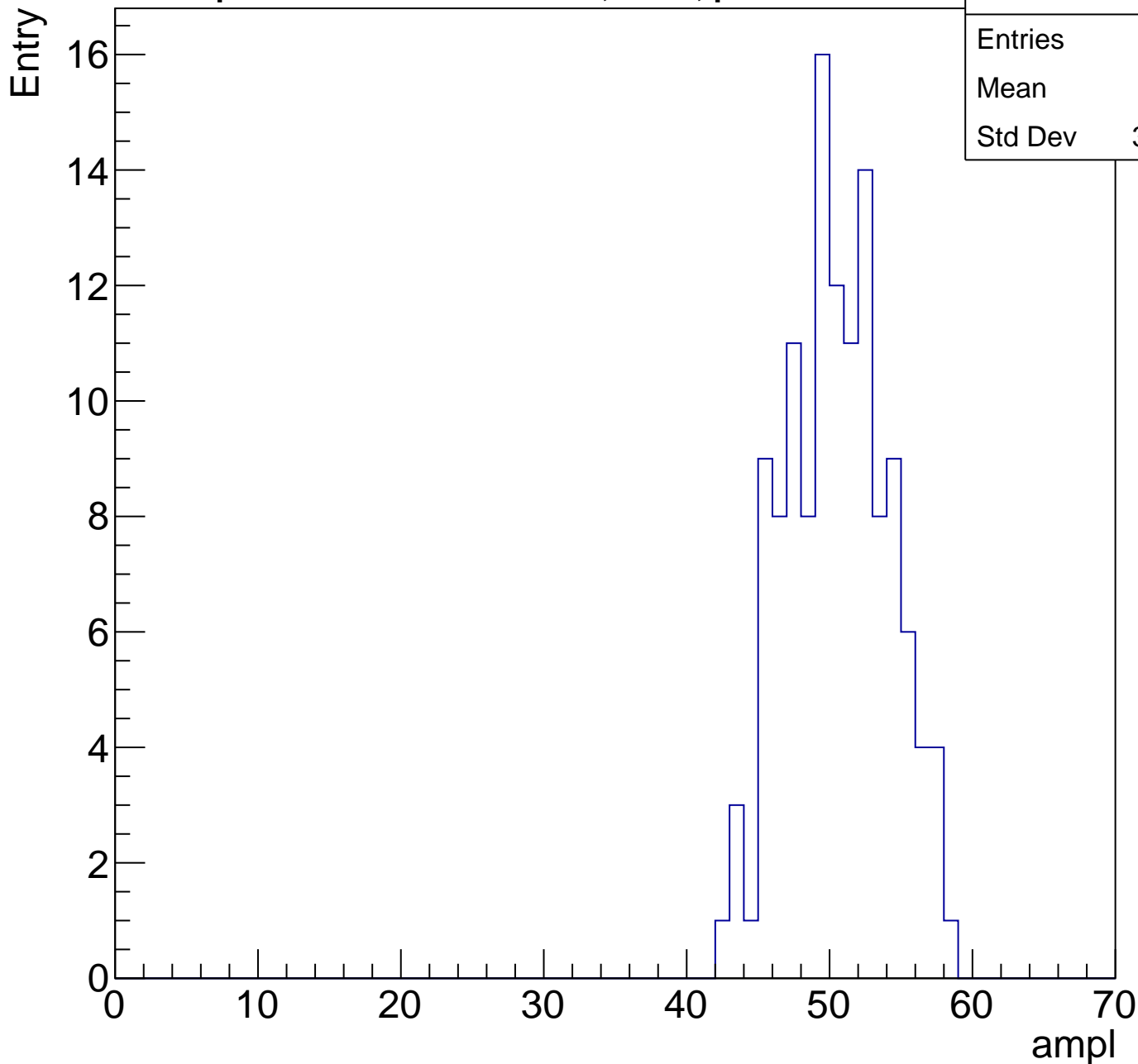
Entry



B0L101S, U18-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

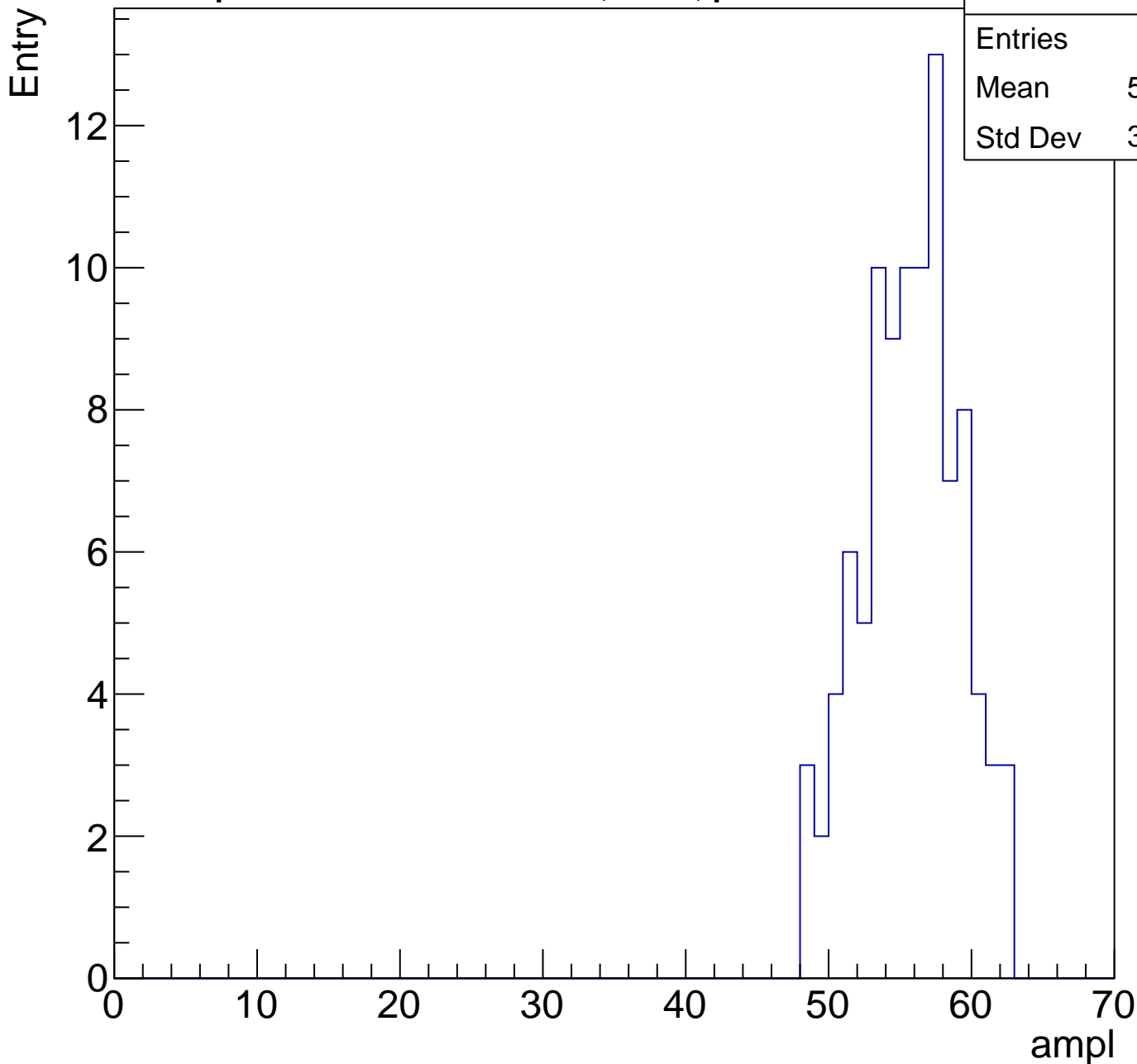
Entries	126
Mean	50.1
Std Dev	3.544



B0L101S, U18-ch66, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	55.28
Std Dev	3.382



B0L101S, U18-ch66, adc5

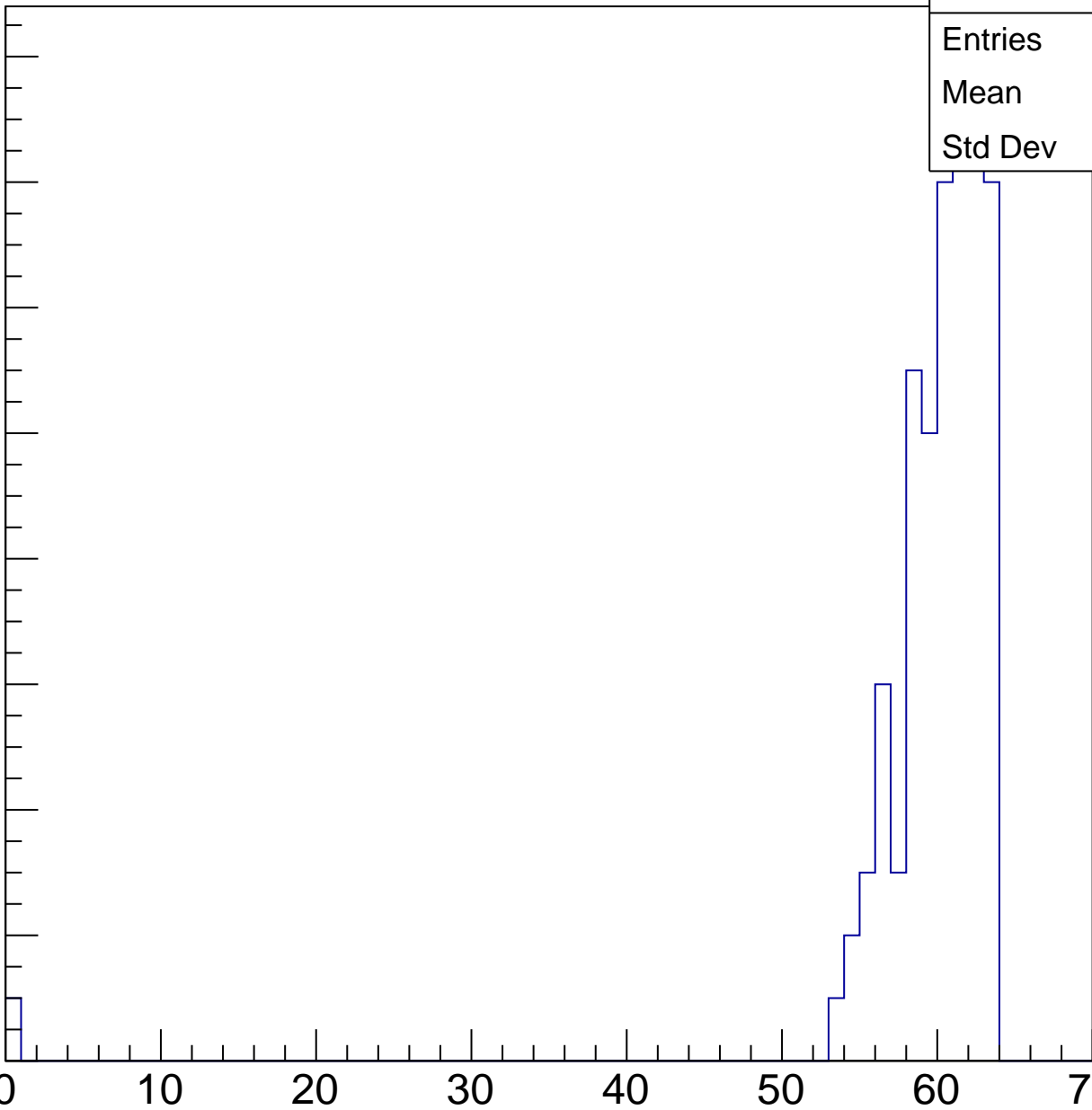
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.26
Std Dev	6.553

ampl

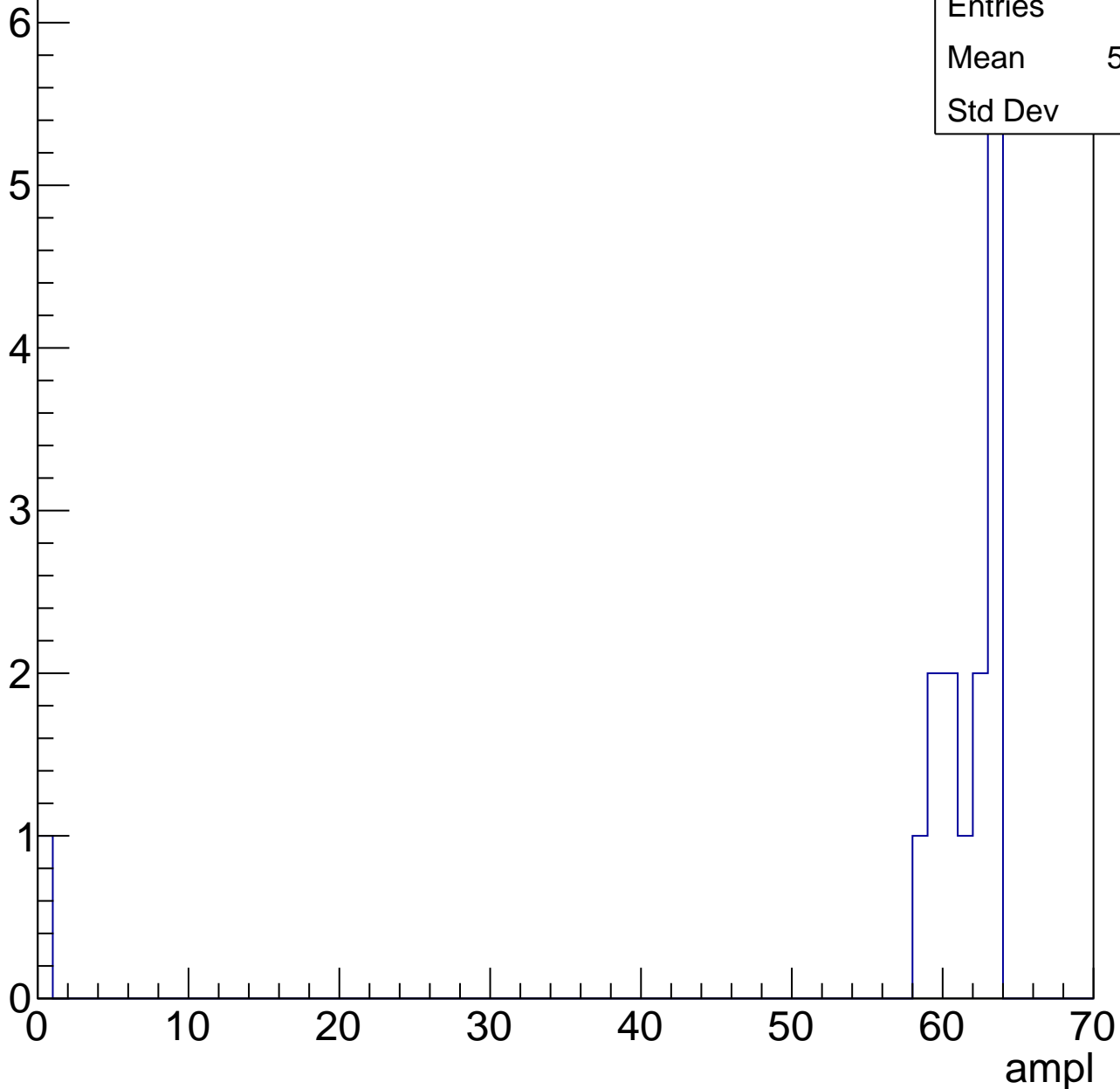


B0L101S, U18-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.27
Std Dev	15.4



B0L101S, U18-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

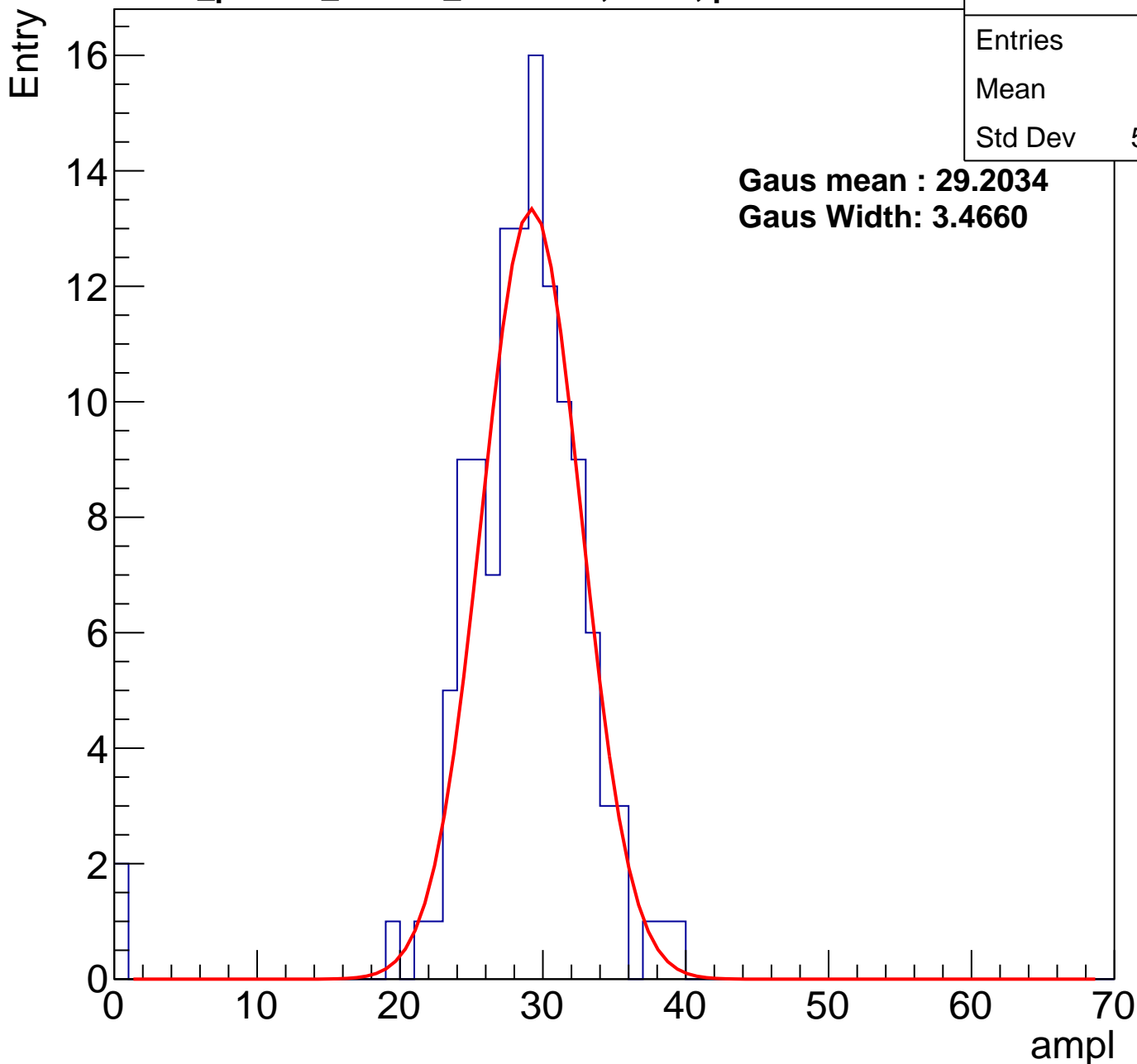
B0L101S, U18-ch67, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	28.1
Std Dev	5.044

Gaus mean : 29.2034

Gaus Width: 3.4660



B0L101S, U18-ch67, adc1

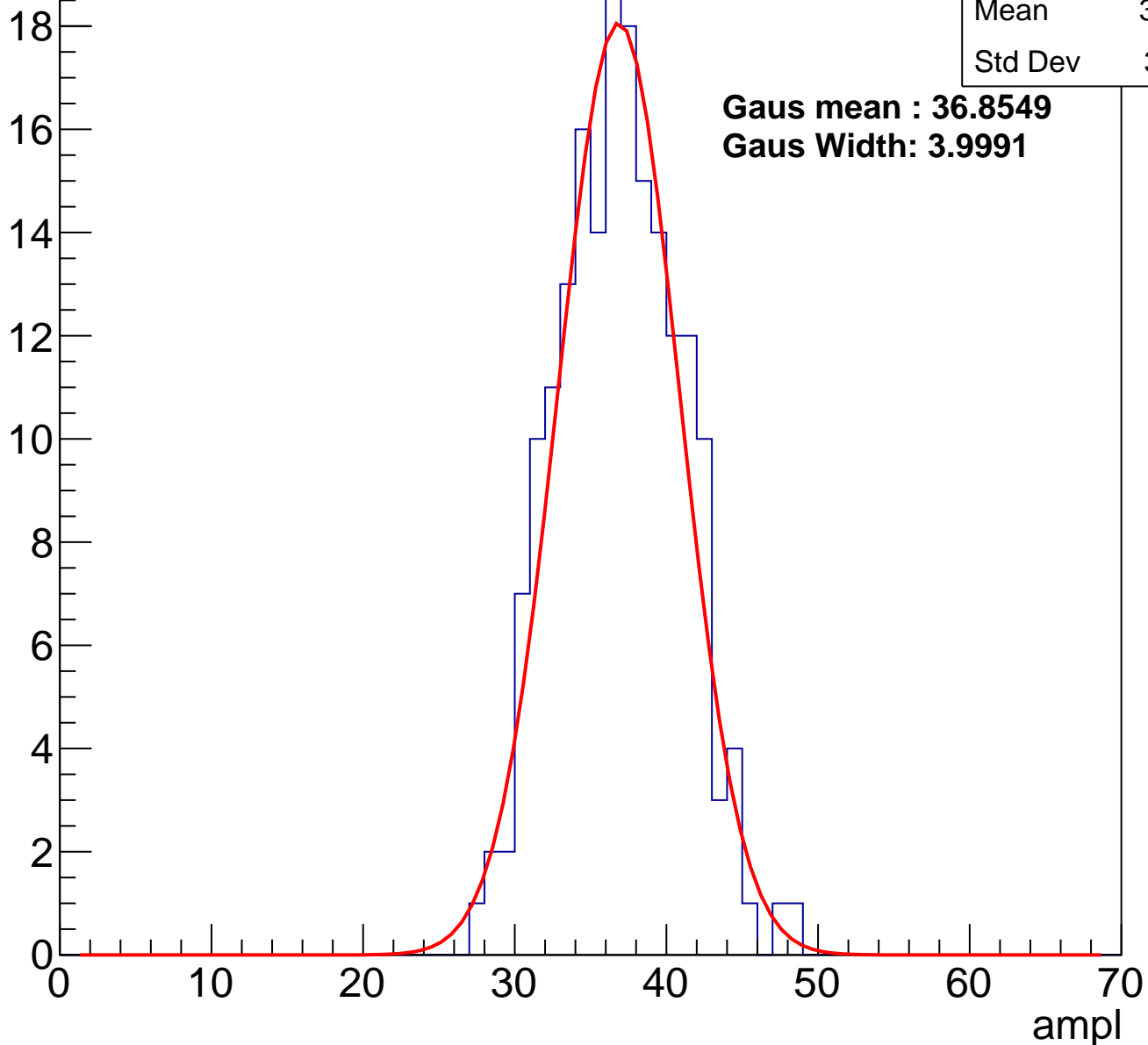
calib_packv5_042523_0143.root, FC#1, port C1

Entries	186
Mean	36.45
Std Dev	3.981

Gaus mean : 36.8549

Gaus Width: 3.9991

Entry



B0L101S, U18-ch67, adc2

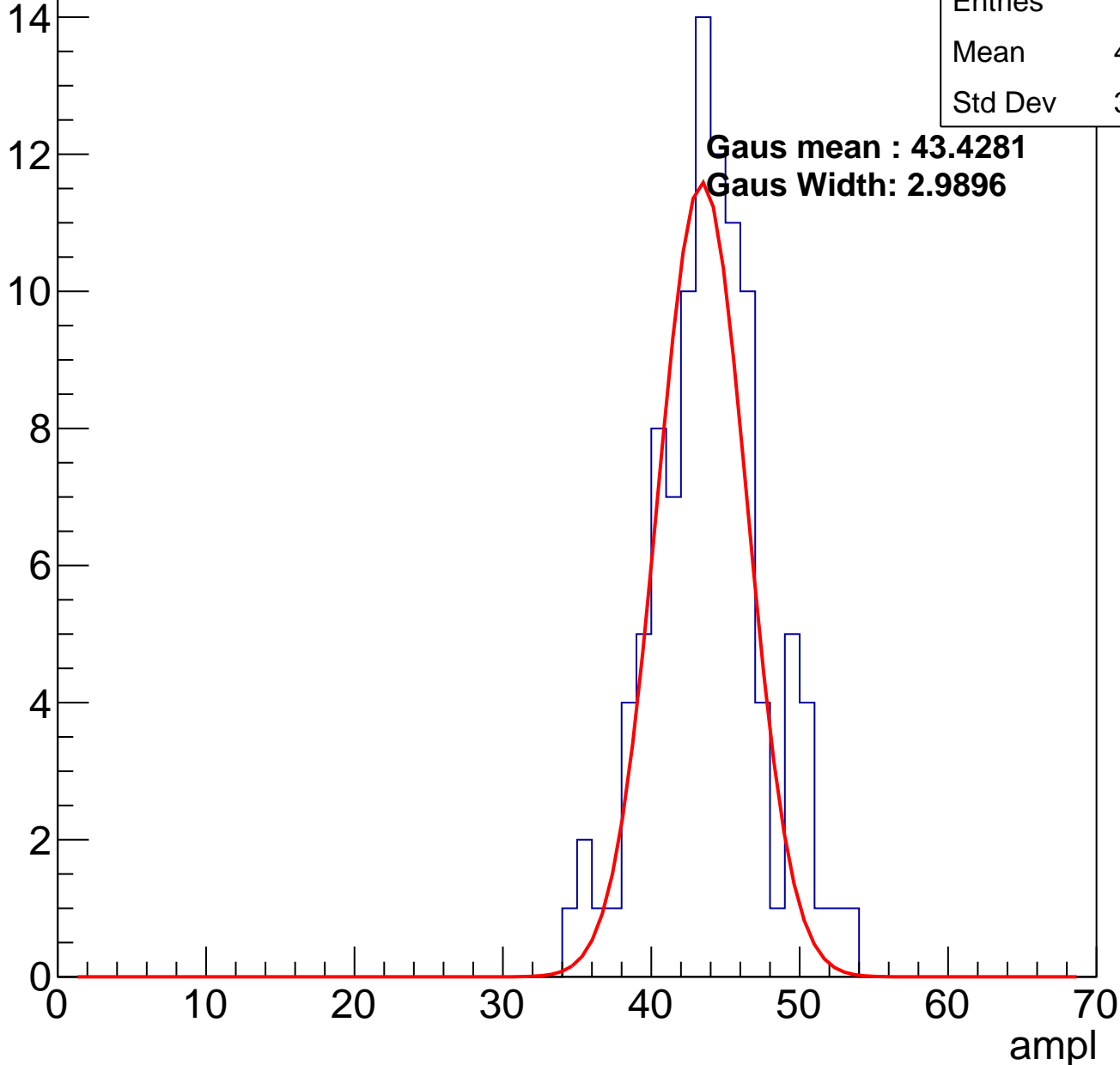
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	103
Mean	43.43
Std Dev	3.715

Gaus mean : 43.4281

Gaus Width: 2.9896

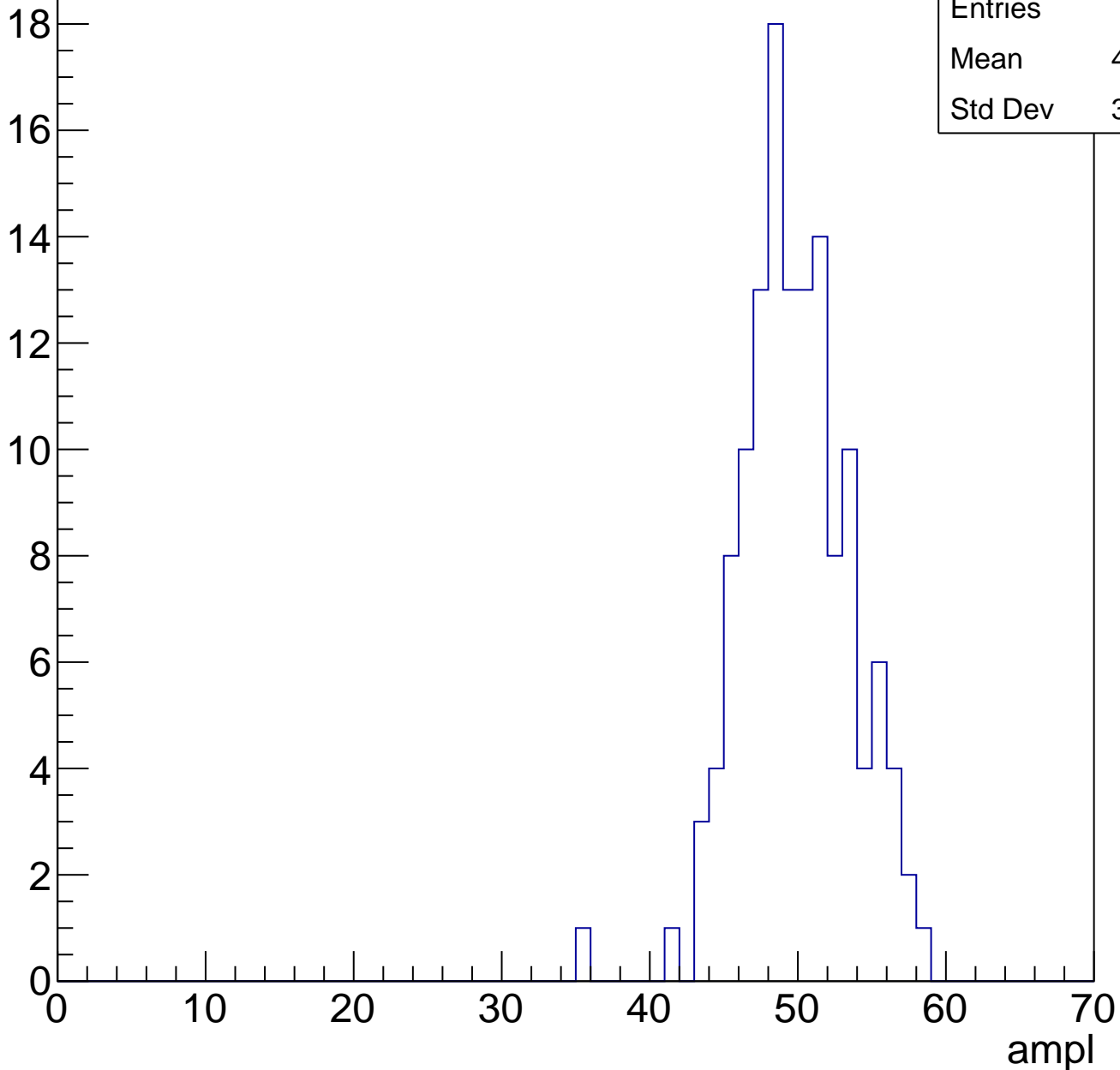


B0L101S, U18-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	49.36
Std Dev	3.638

Entry



B0L101S, U18-ch67, adc4

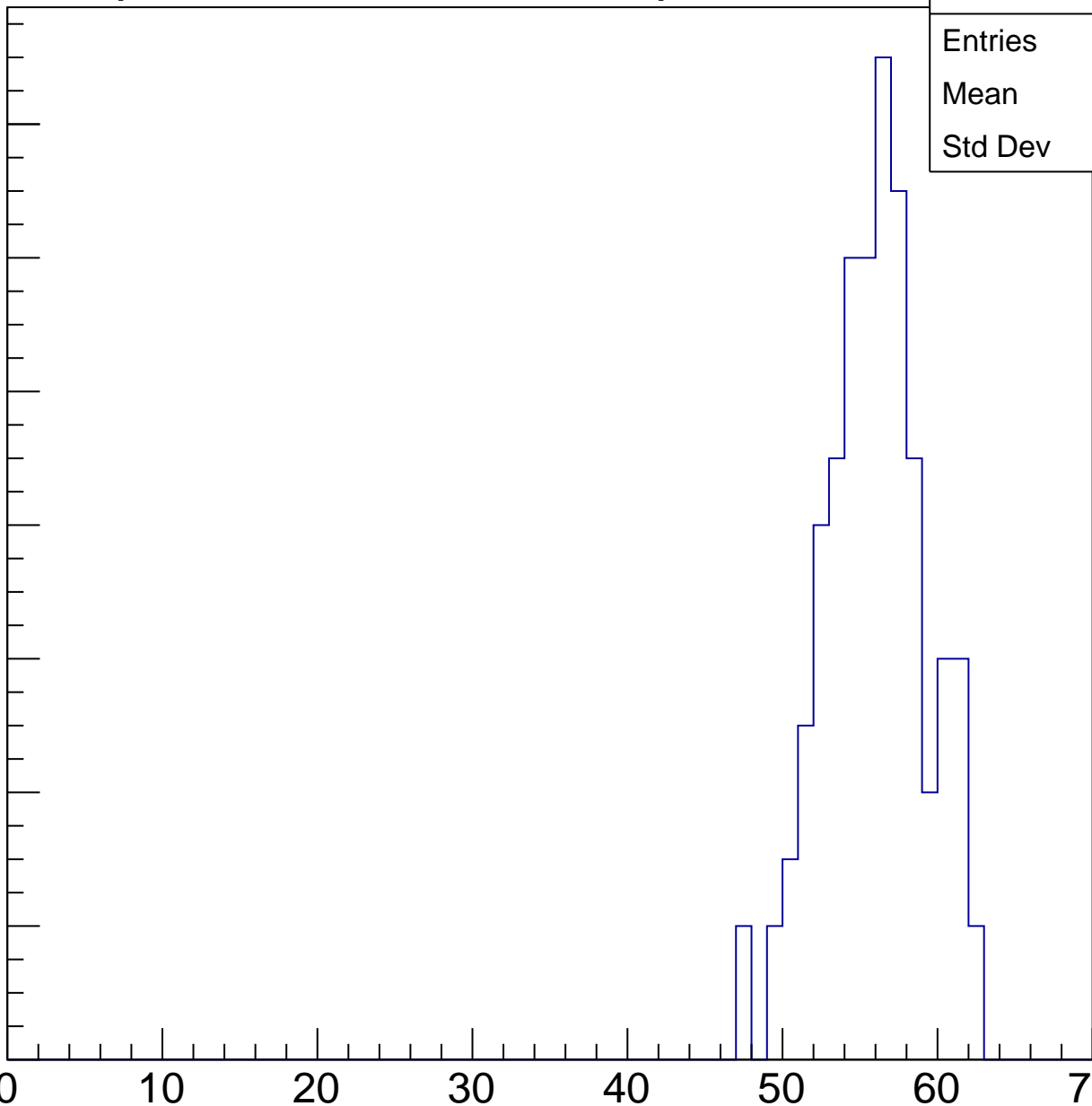
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	55.44
Std Dev	3.235

ampl



B0L101S, U18-ch67, adc5

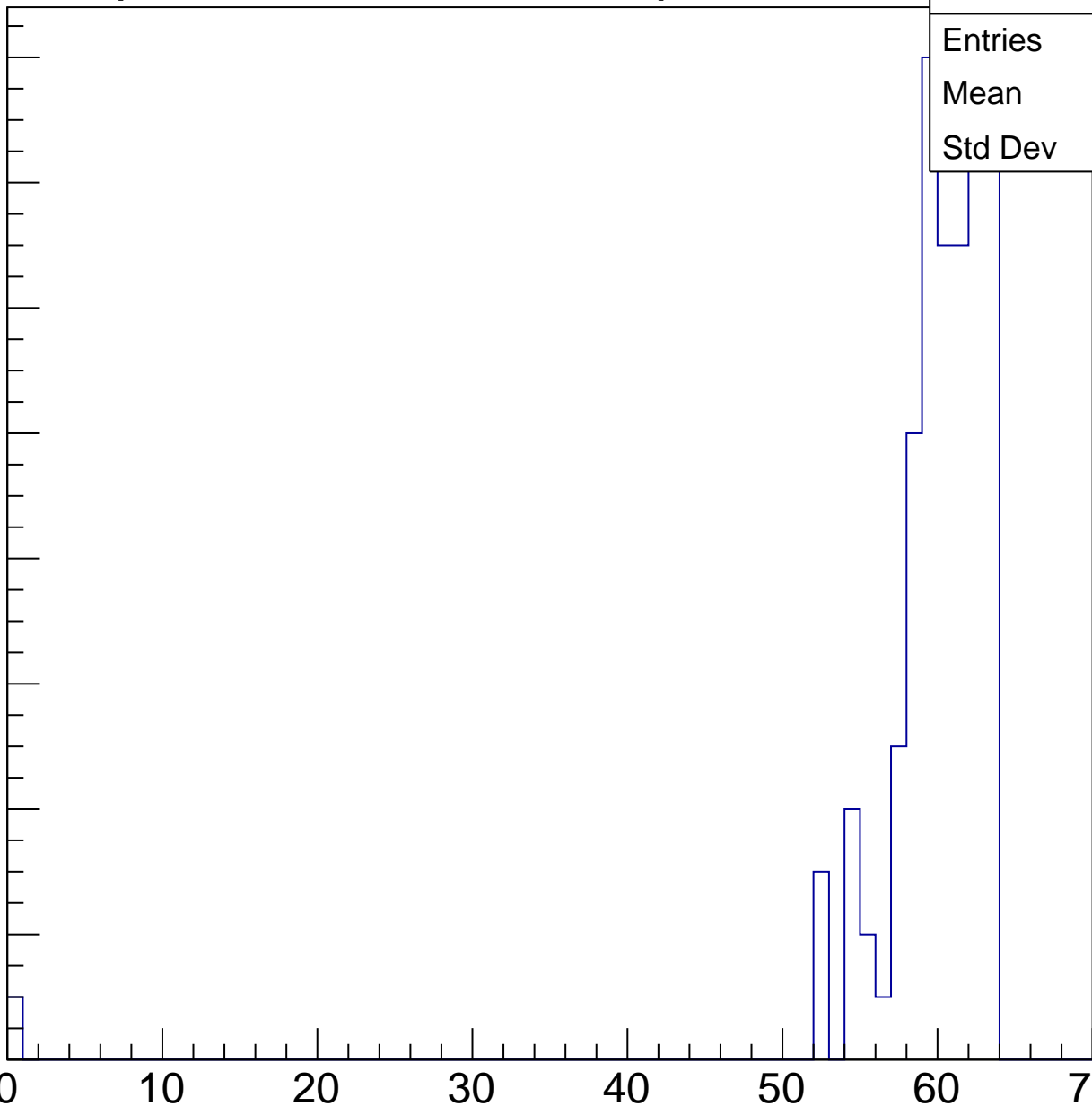
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.17
Std Dev	6.554

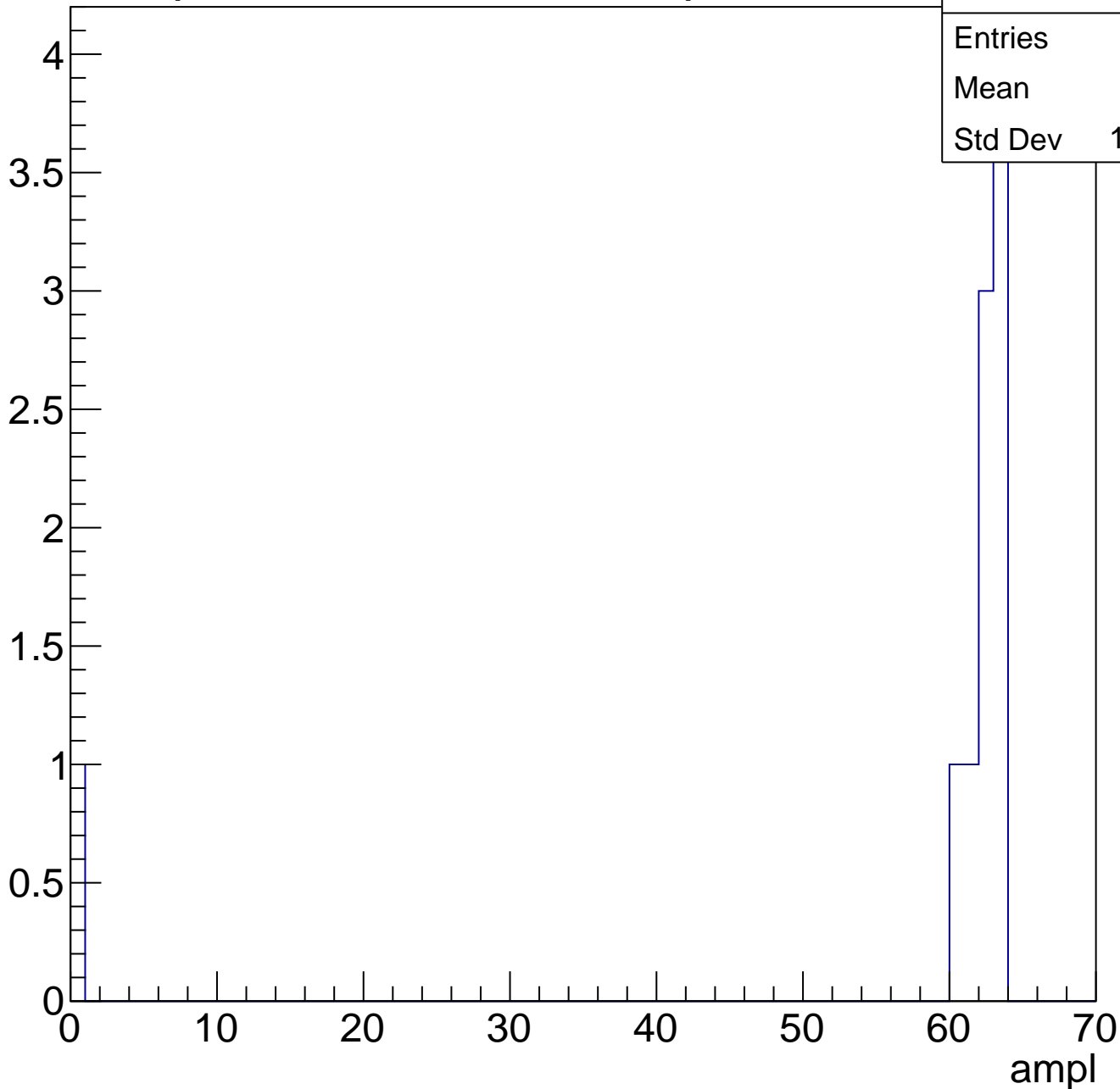
ampl



B0L101S, U18-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	10
Mean	55.9
Std Dev	18.66

B0L101S, U18-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U18-ch68, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.07
Std Dev	4.908

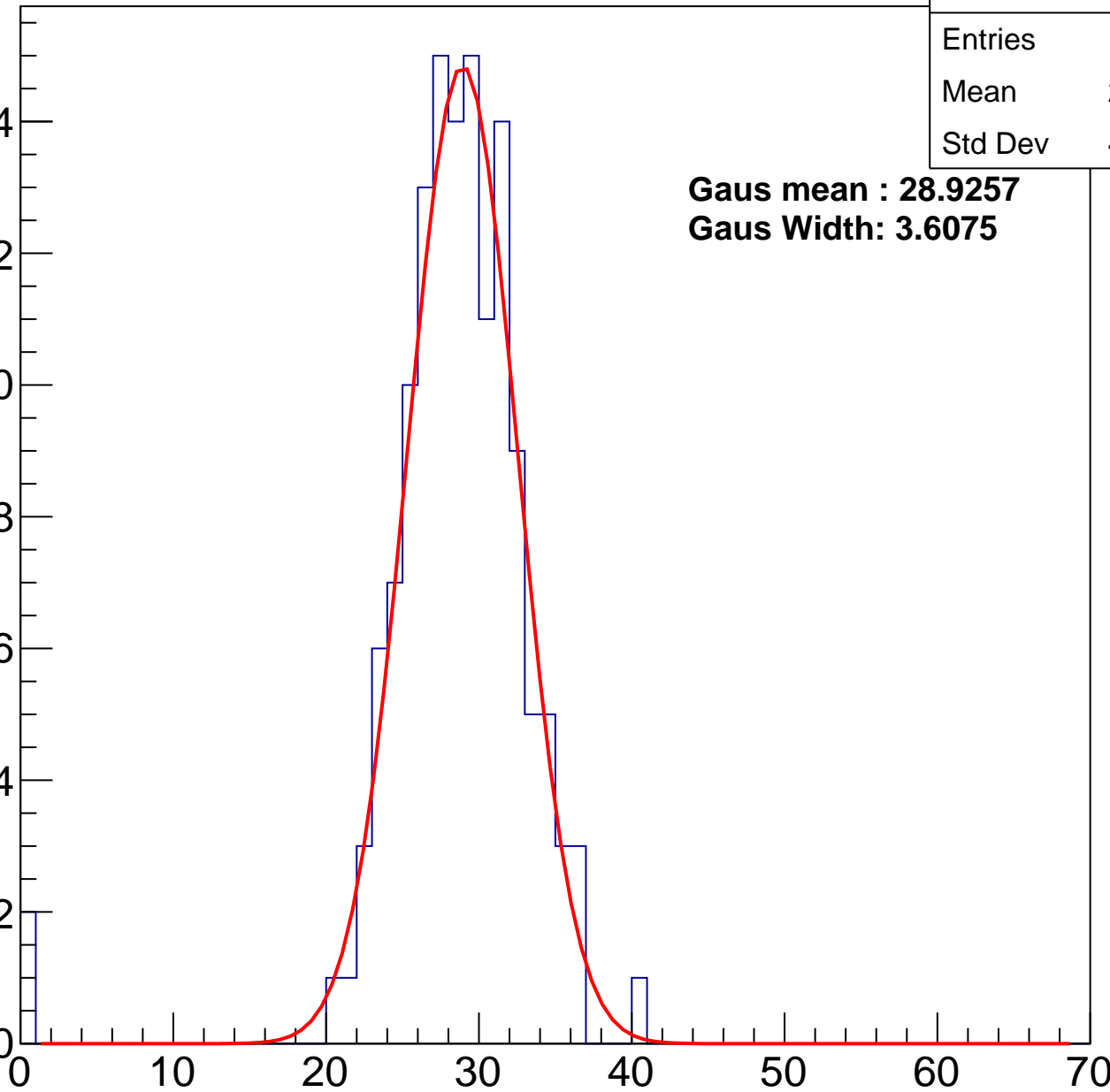
Gaus mean : 28.9257

Gaus Width: 3.6075

Entry

14
12
10
8
6
4
2
0

ampl



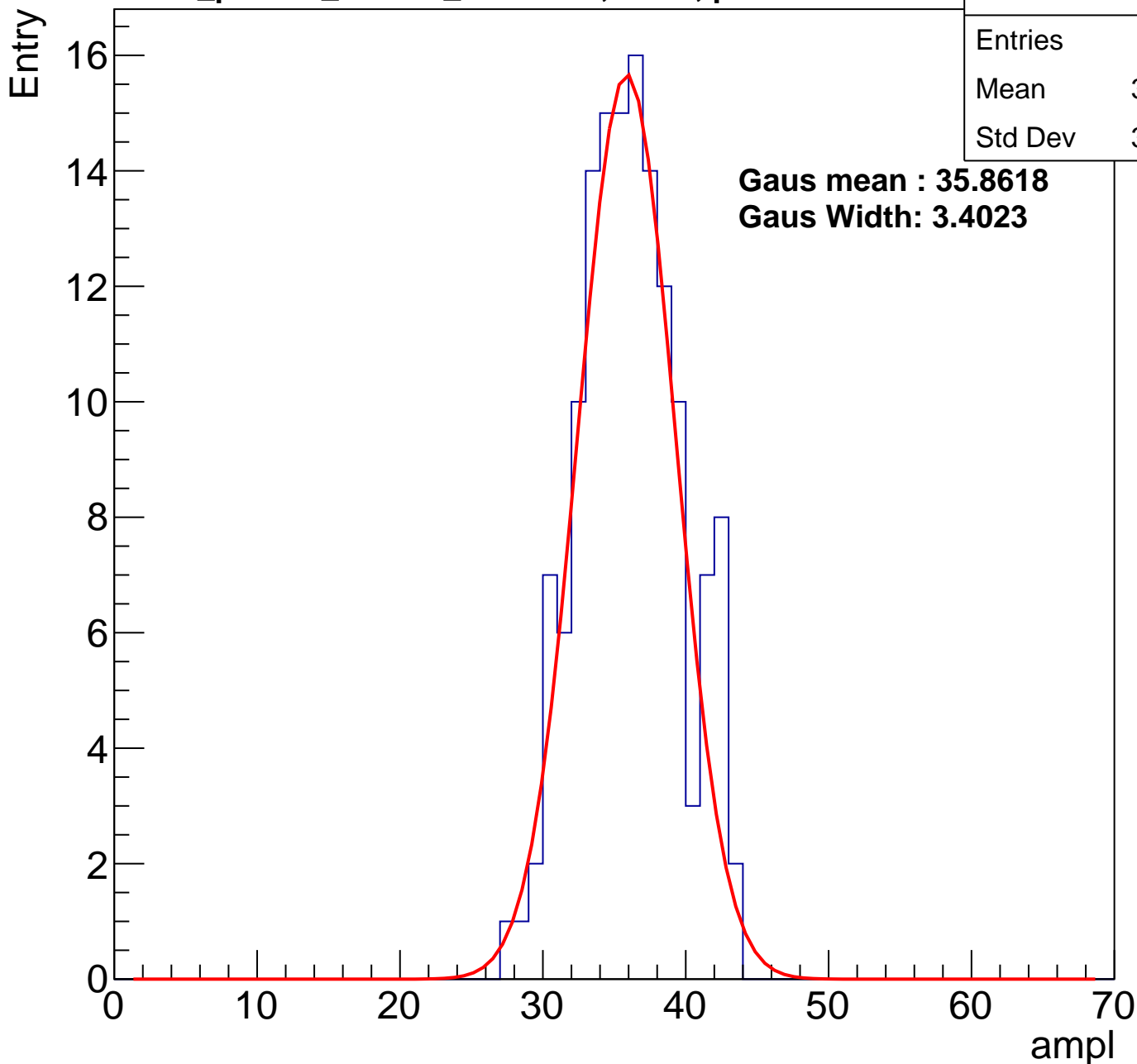
B0L101S, U18-ch68, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	35.63
Std Dev	3.496

Gaus mean : 35.8618

Gaus Width: 3.4023



B0L101S, U18-ch68, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	42.43
Std Dev	3.797

Gaus mean : 43.0915

Gaus Width: 3.6893

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

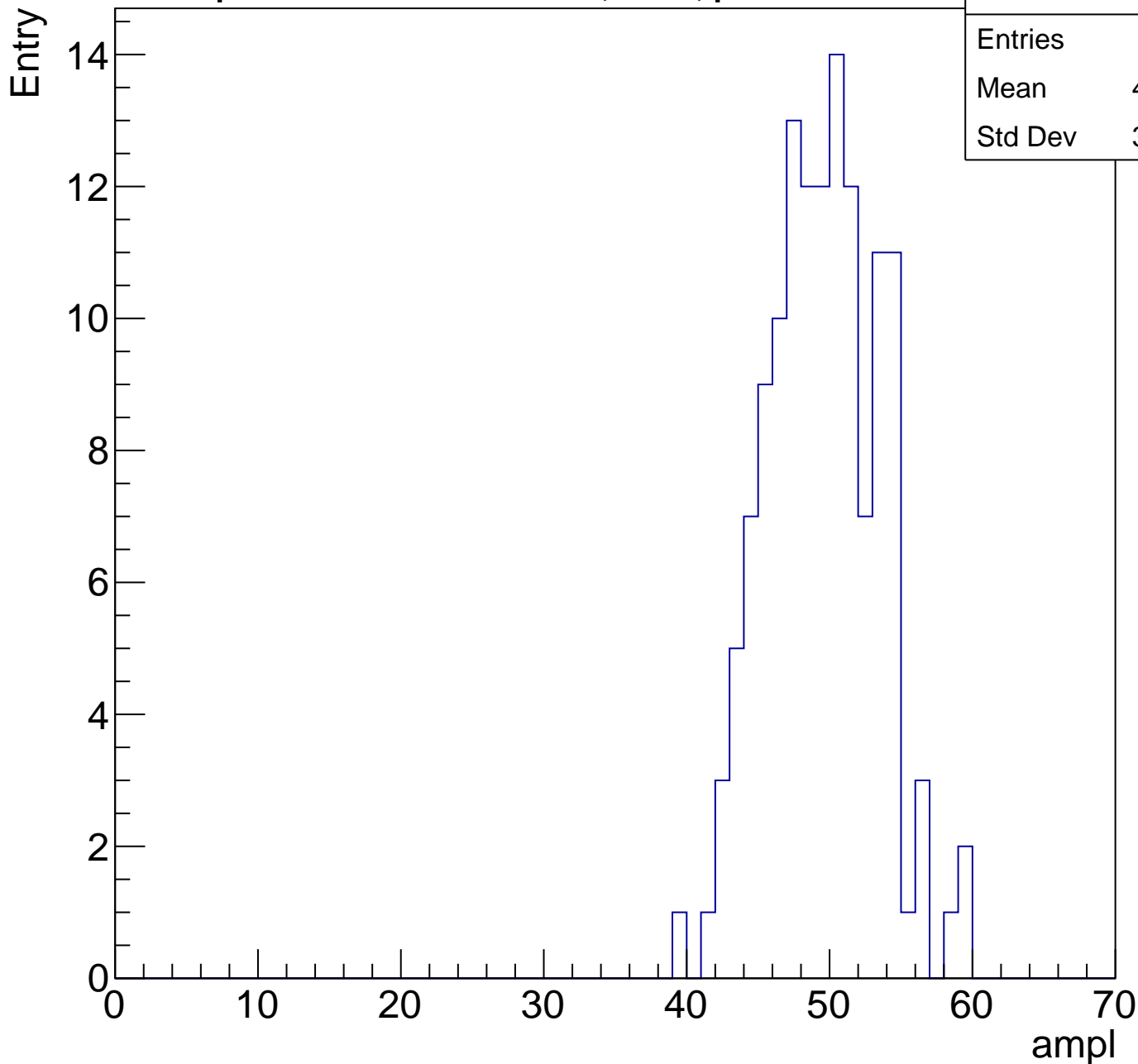
60

70

B0L101S, U18-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	49.04
Std Dev	3.853



B0L101S, U18-ch68, adc4

calib_packv5_042523_0143.root, FC#1, port C1

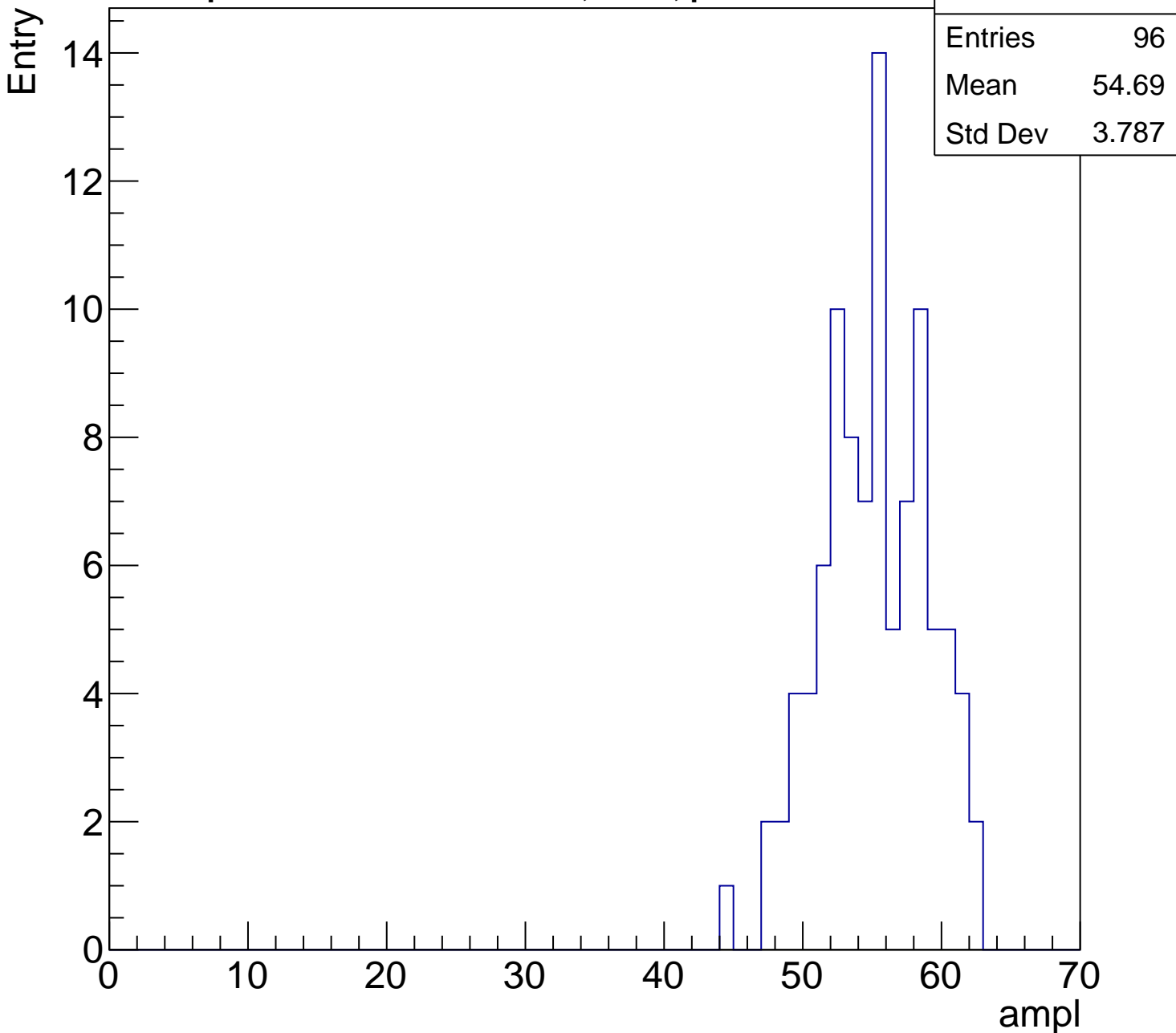
Entries	96
Mean	54.69
Std Dev	3.787

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch68, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	59.09
Std Dev	3.112

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

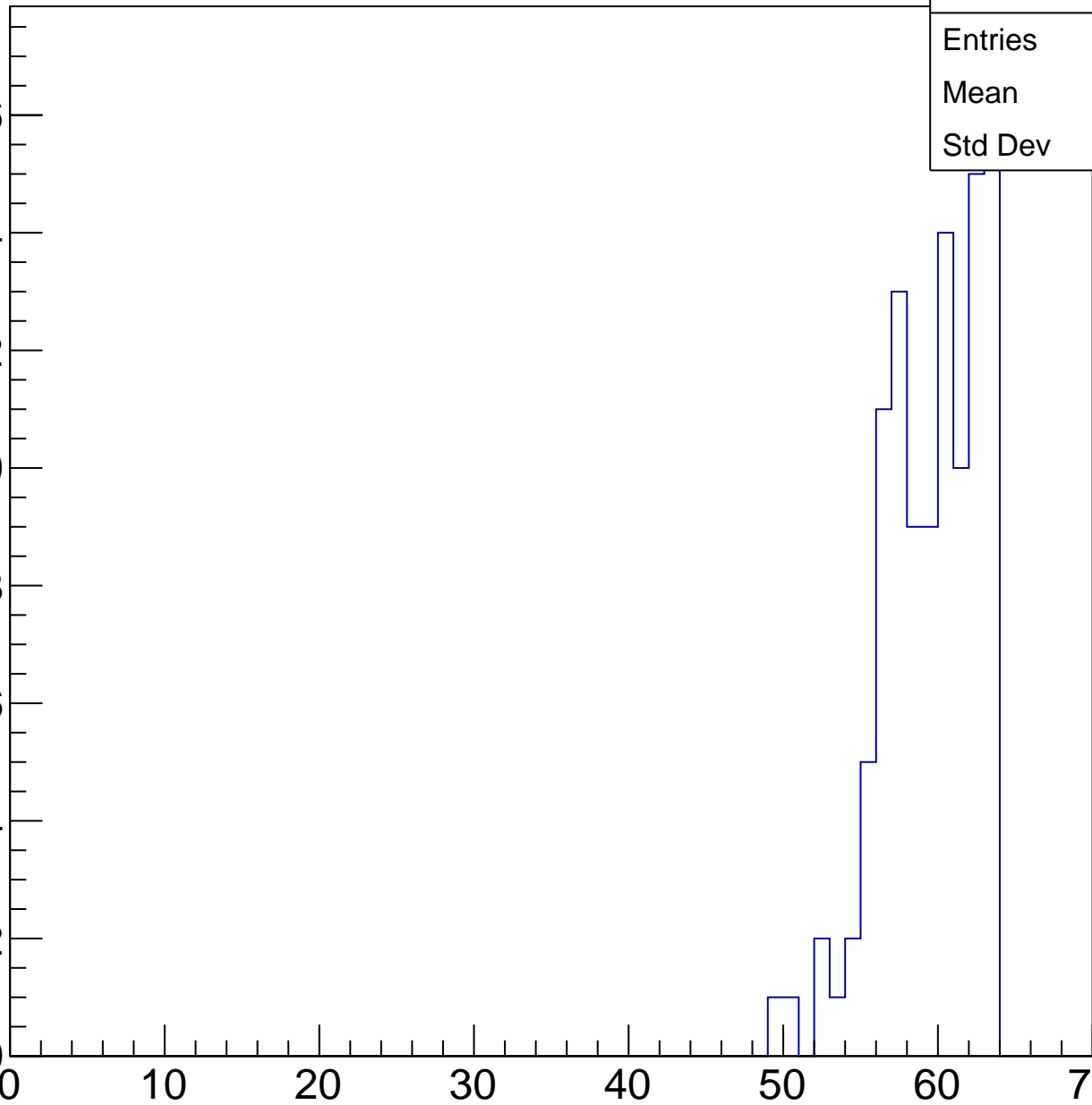
40

50

60

70

ampl

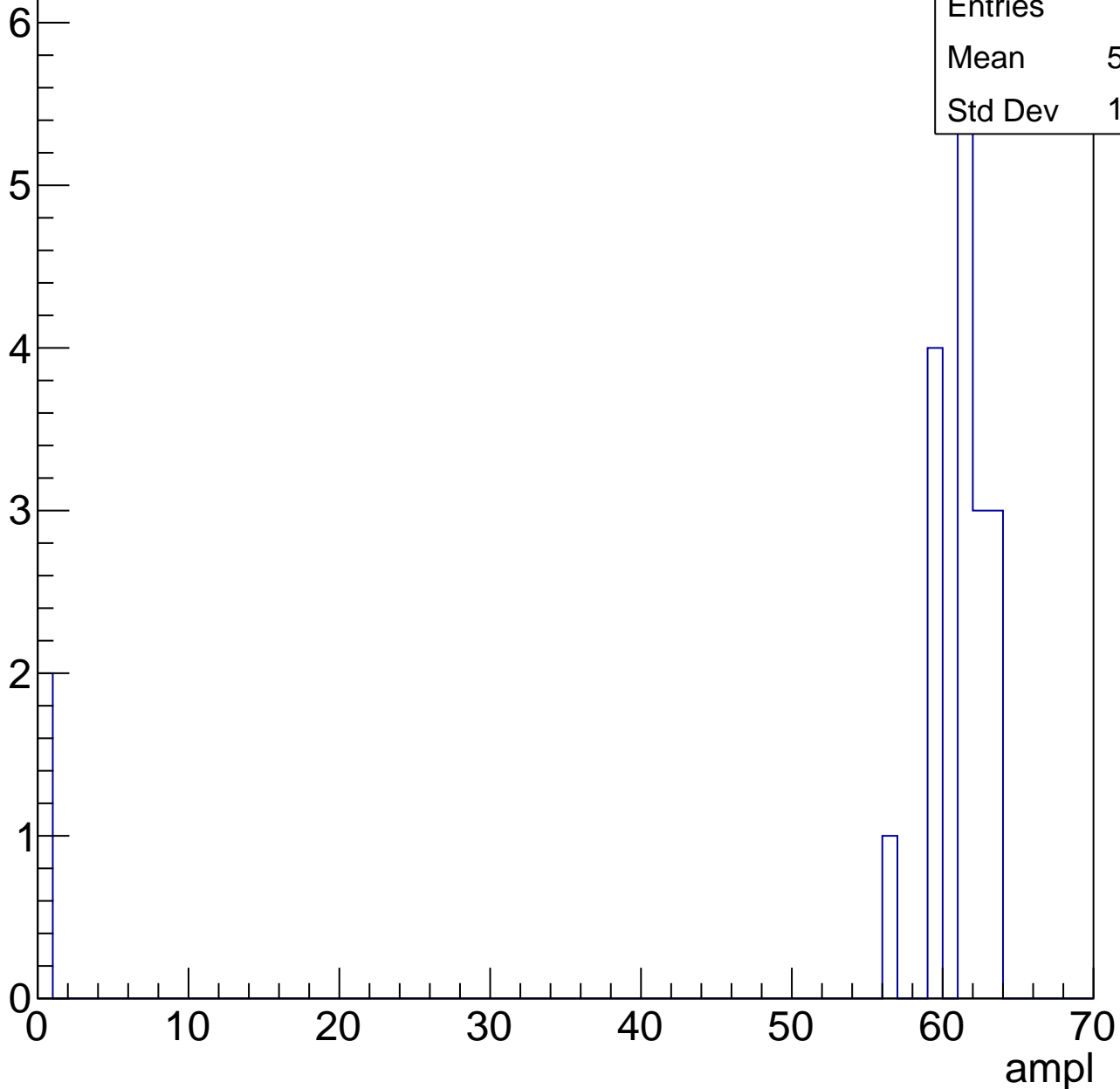


B0L101S, U18-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	54.37
Std Dev	18.73



B0L101S, U18-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch69, adc0

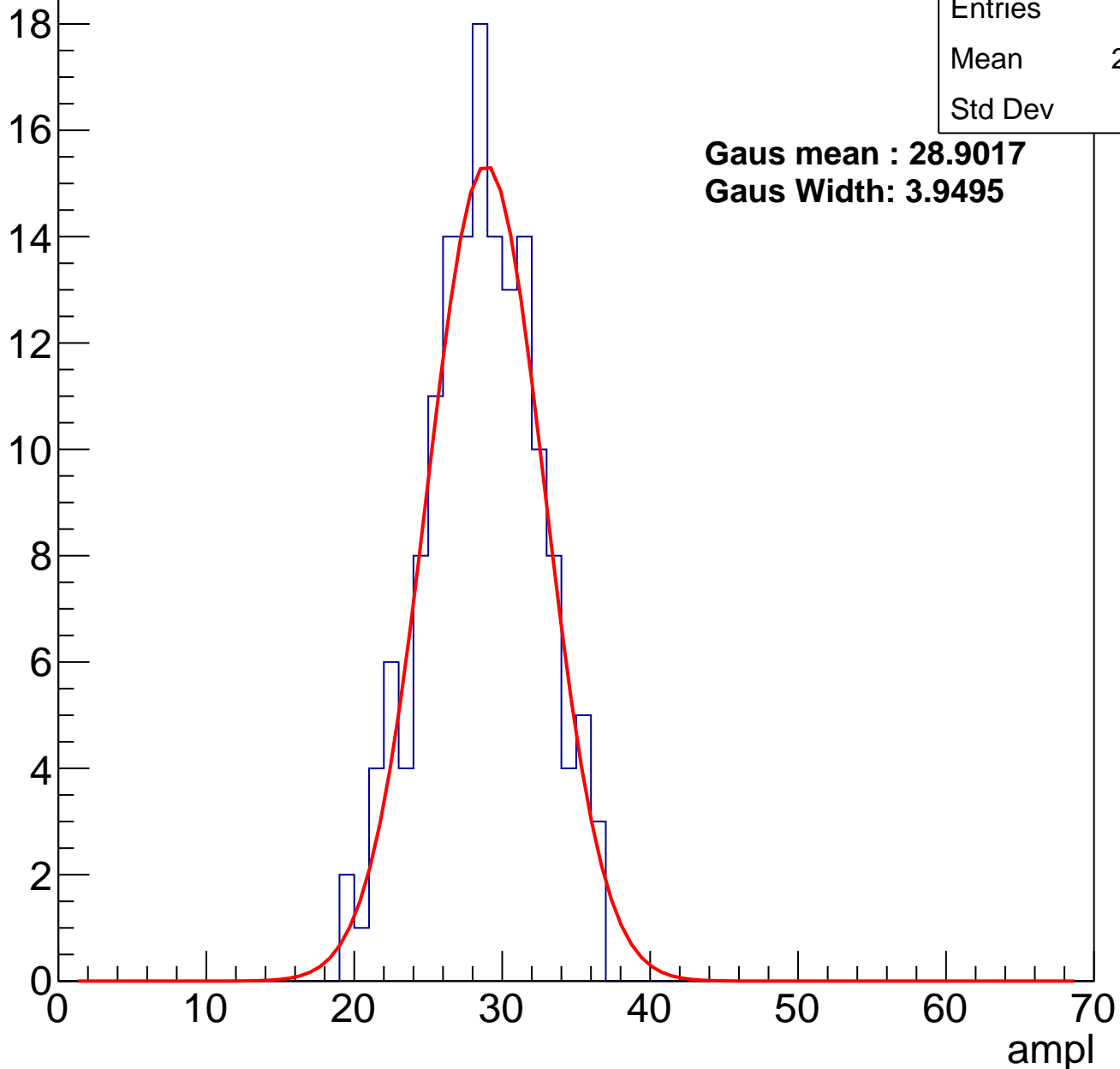
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.18
Std Dev	3.76

Gaus mean : 28.9017

Gaus Width: 3.9495

Entry



B0L101S, U18-ch69, adc1

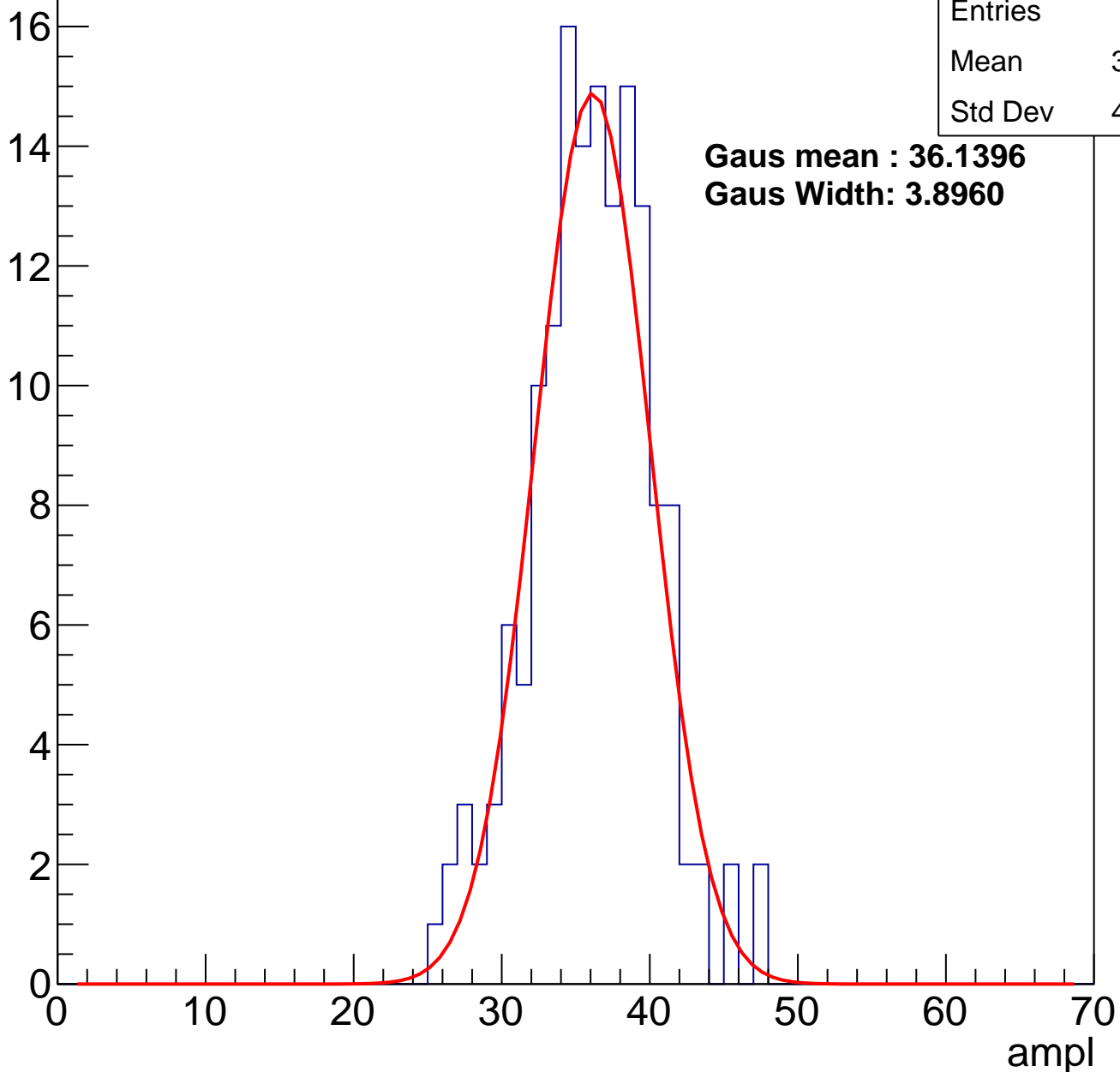
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	35.64
Std Dev	4.114

Gaus mean : 36.1396

Gaus Width: 3.8960

Entry



B0L101S, U18-ch69, adc2

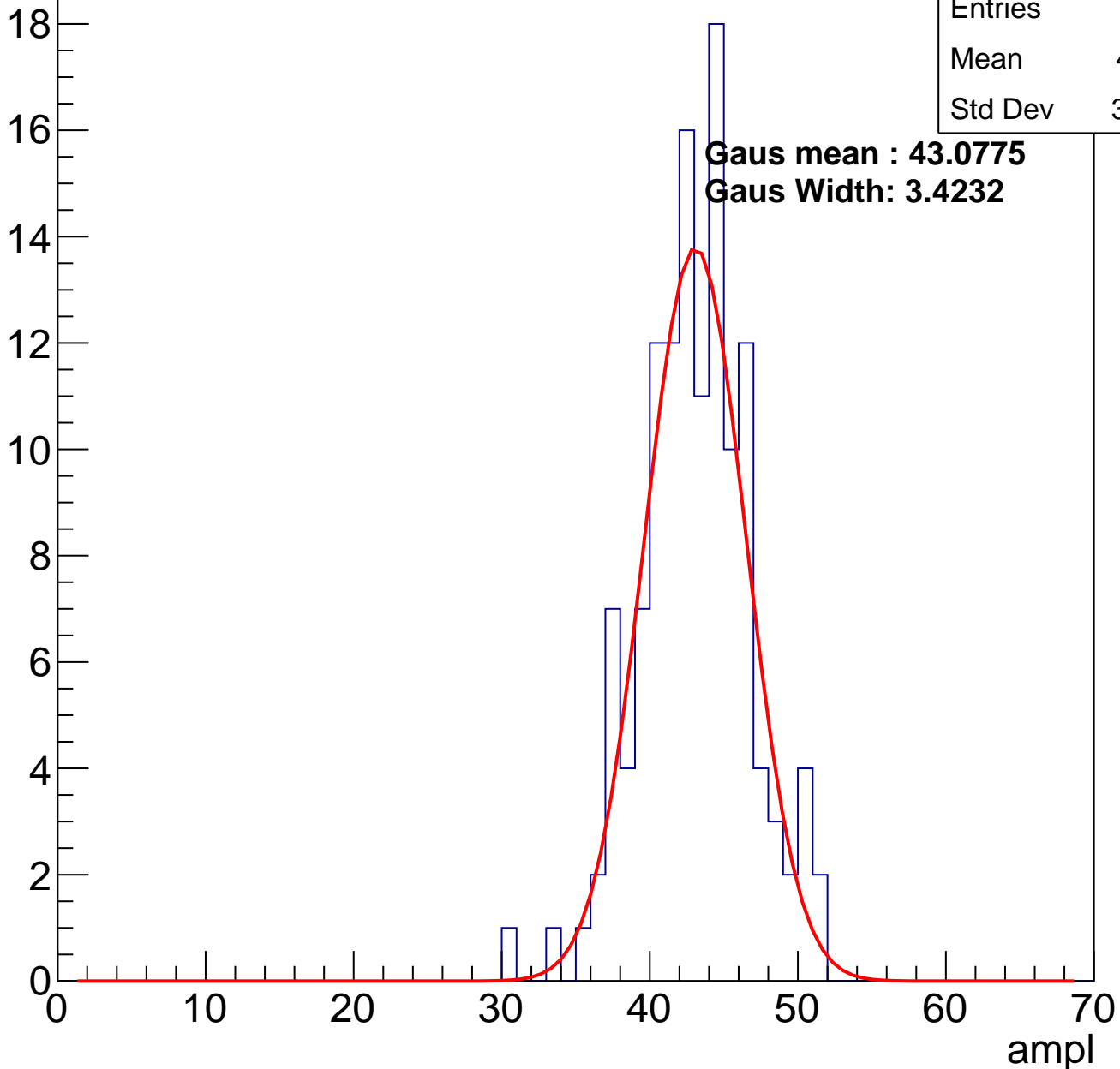
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	42.61
Std Dev	3.682

Gaus mean : 43.0775

Gaus Width: 3.4232

Entry

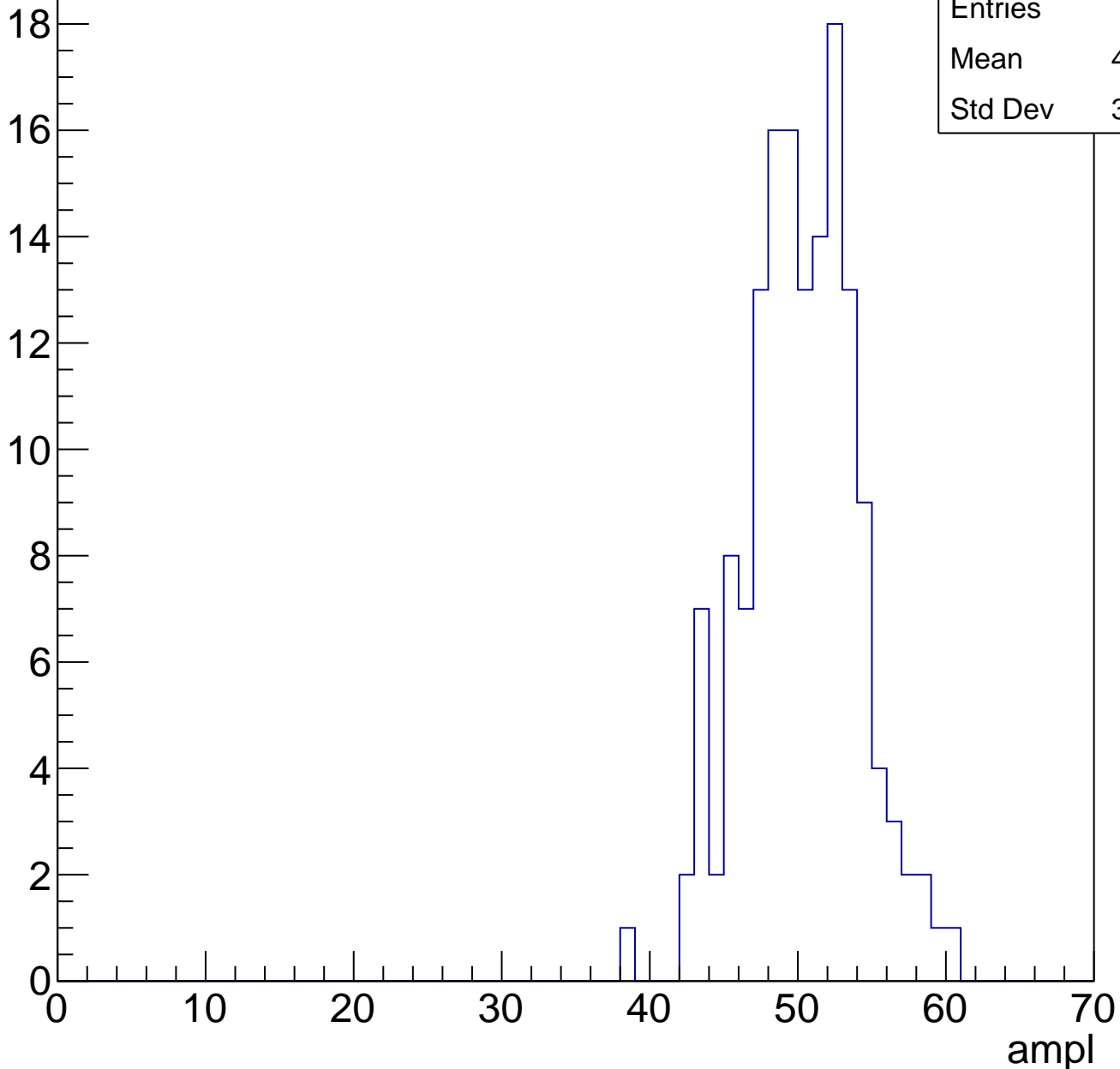


B0L101S, U18-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	49.79
Std Dev	3.762

Entry



B0L101S, U18-ch69, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	55.6
Std Dev	3.334

Entry

12

10

8

6

4

2

0

0

10

20

30

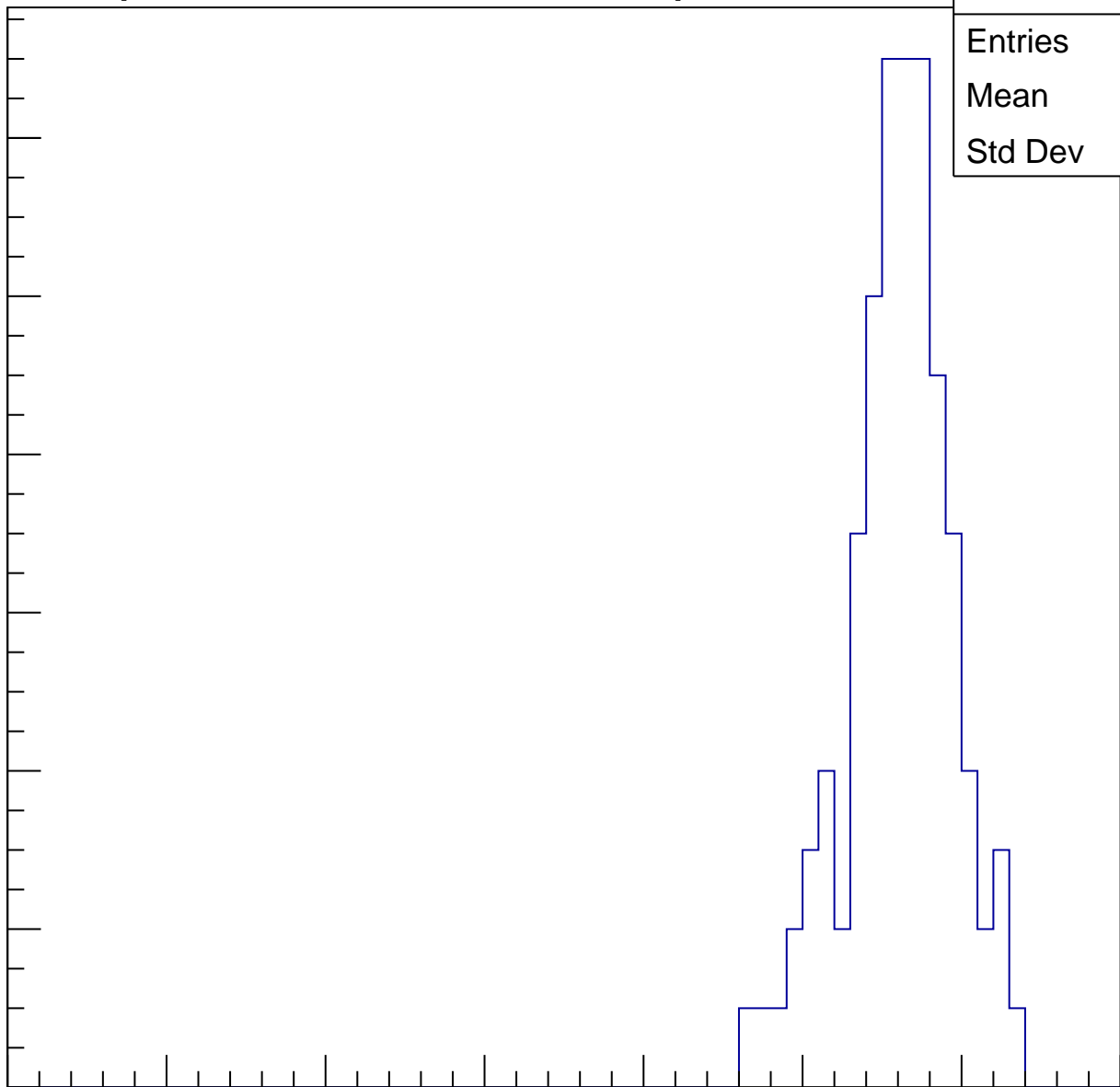
40

50

60

70

ampl



B0L101S, U18-ch69, adc5

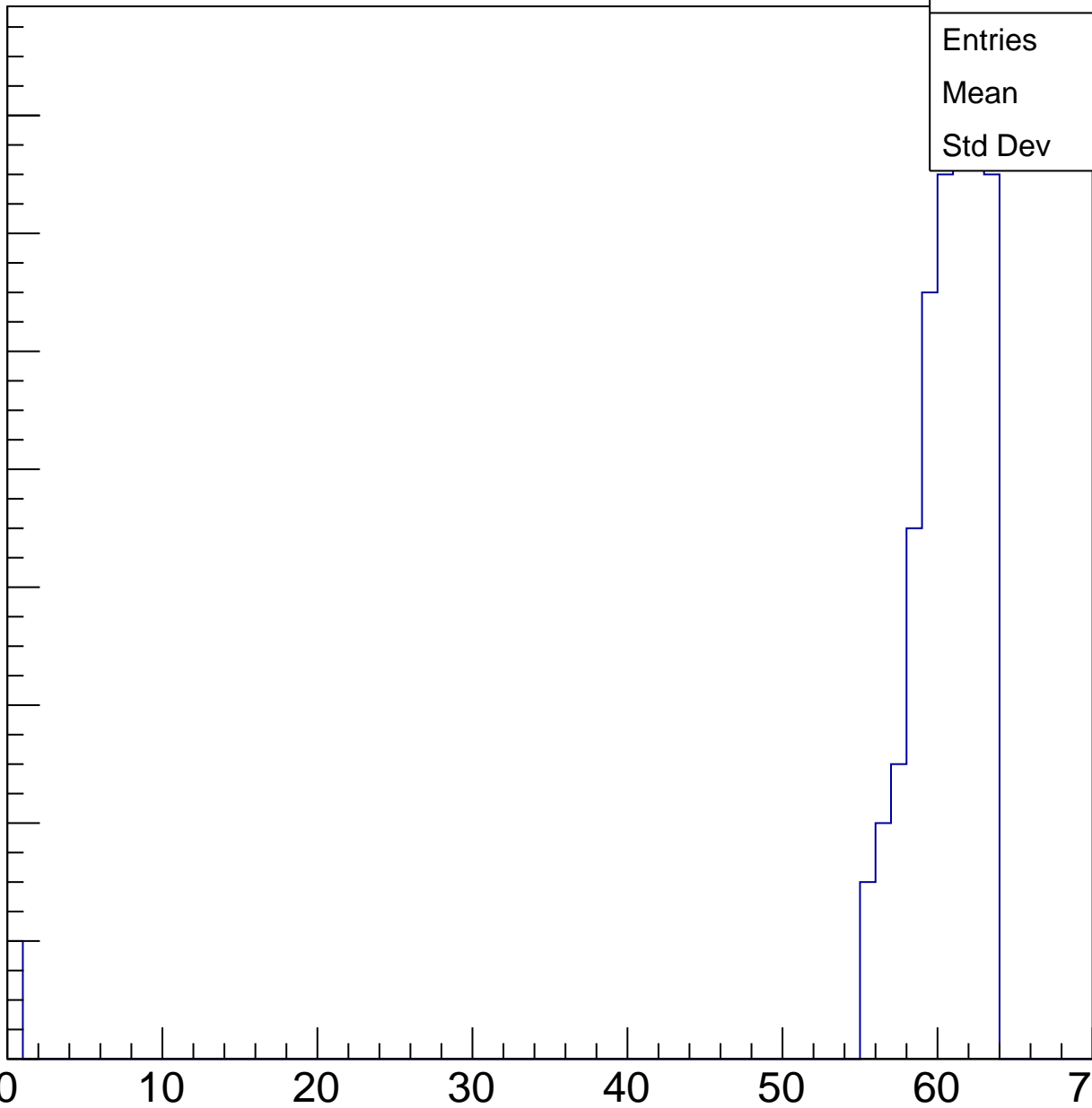
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	58.99
Std Dev	8.69

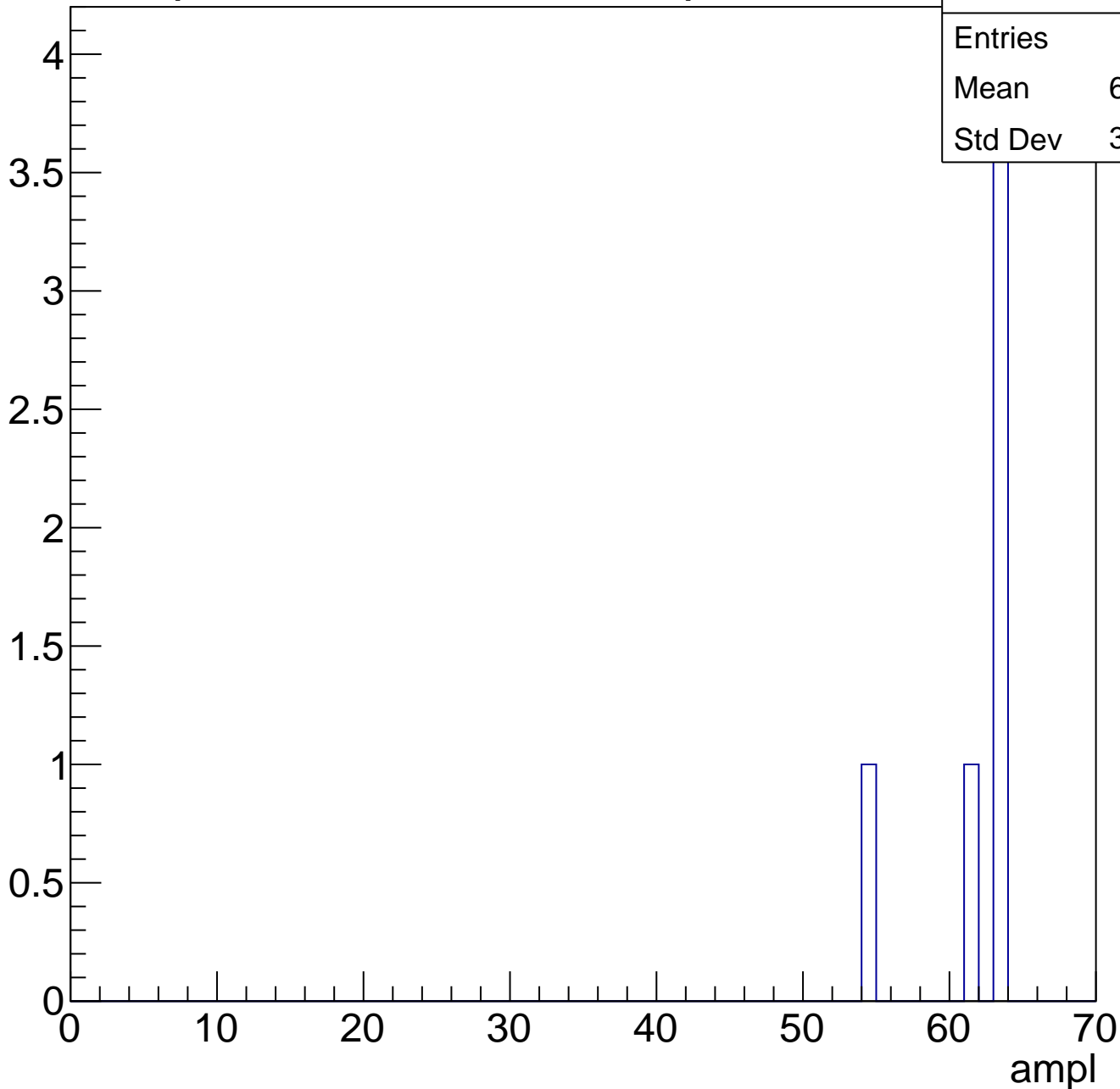
ampl



B0L101S, U18-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



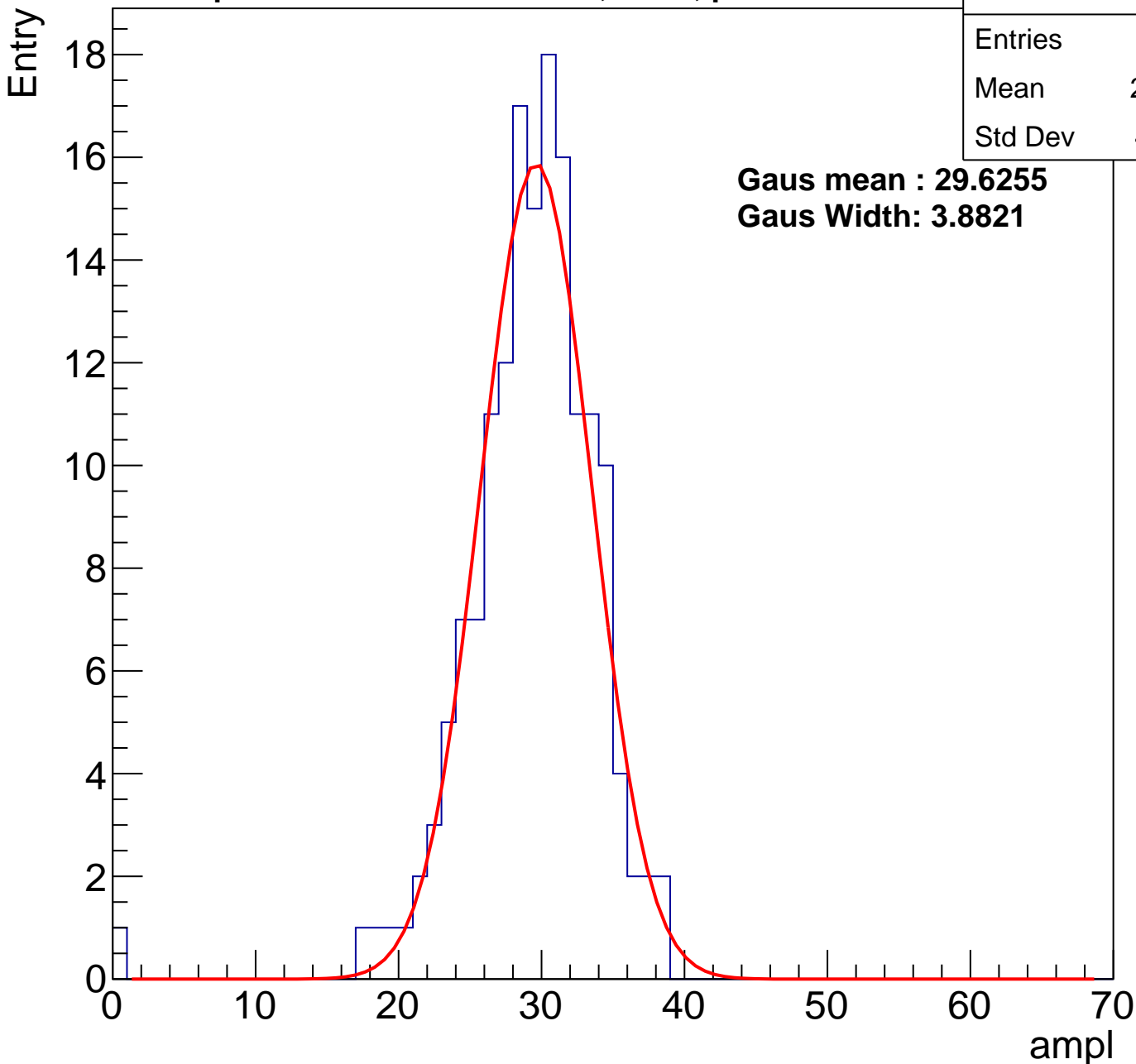
Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch70, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	28.84
Std Dev	4.561

Gaus mean : 29.6255
Gaus Width: 3.8821



B0L101S, U18-ch70, adc1

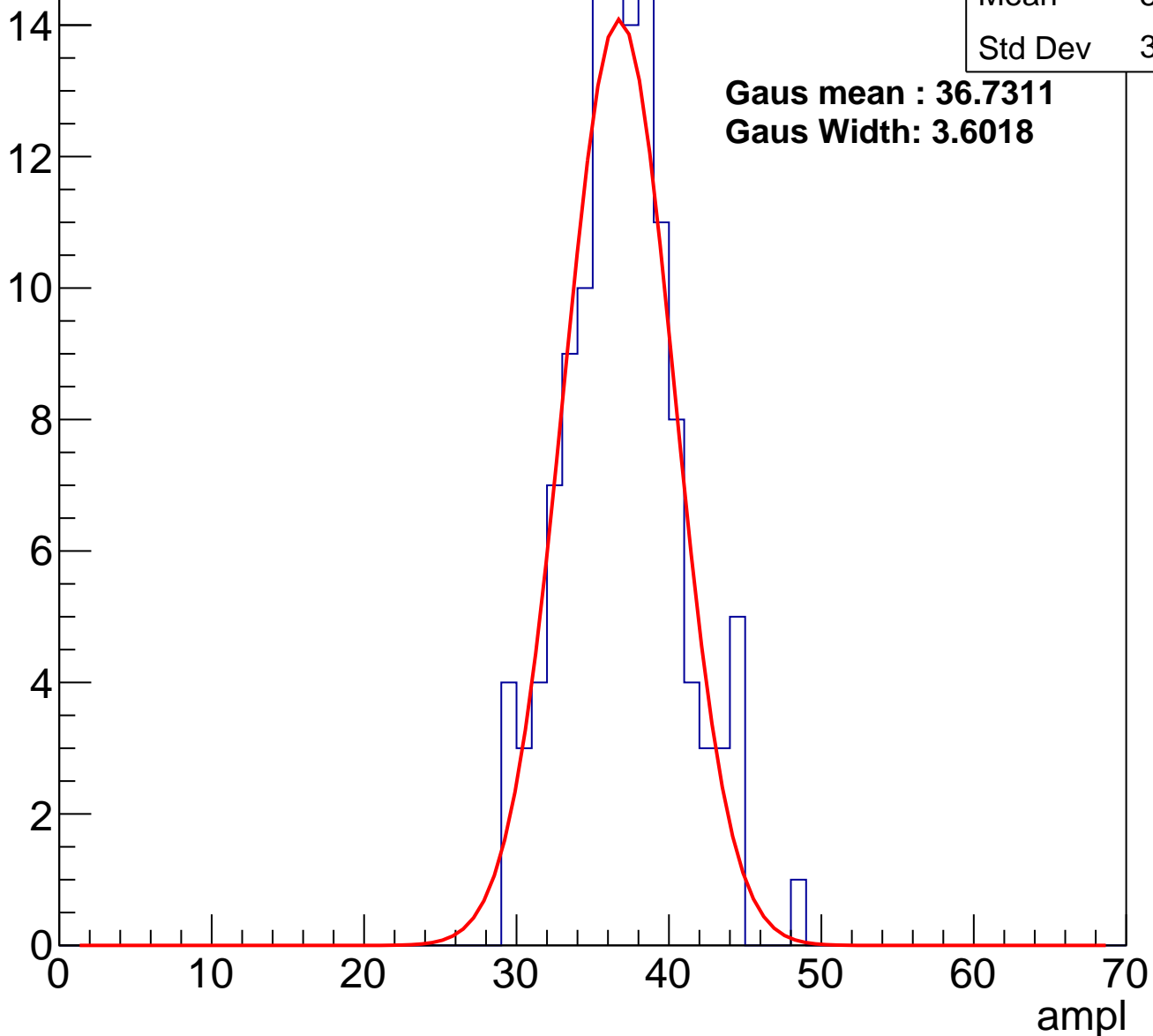
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.49
Std Dev	3.645

Gaus mean : 36.7311

Gaus Width: 3.6018

Entry



B0L101S, U18-ch70, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

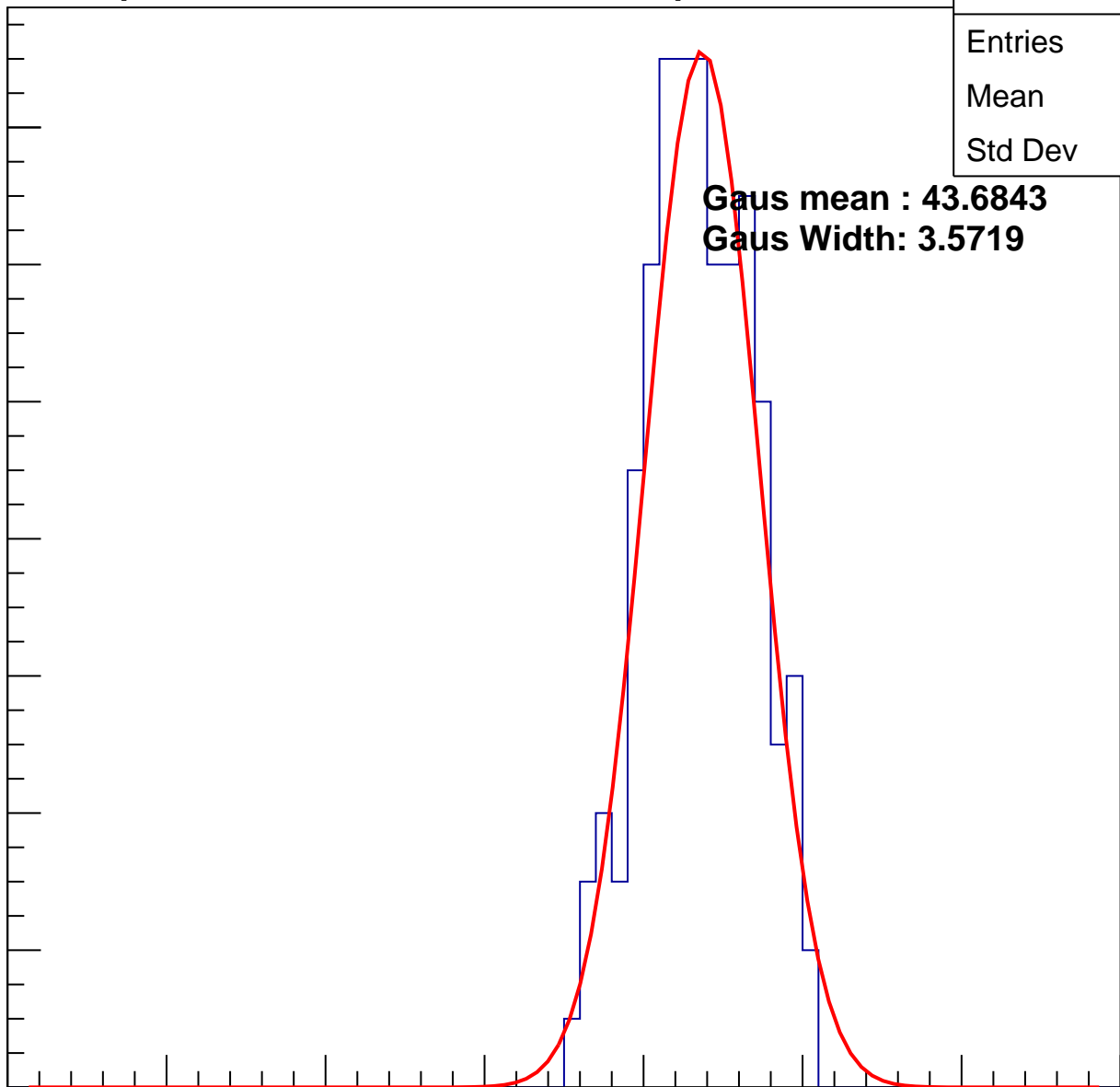
Entries	137
Mean	43.04
Std Dev	3.345

Gaus mean : 43.6843

Gaus Width: 3.5719

0 10 20 30 40 50 60 70

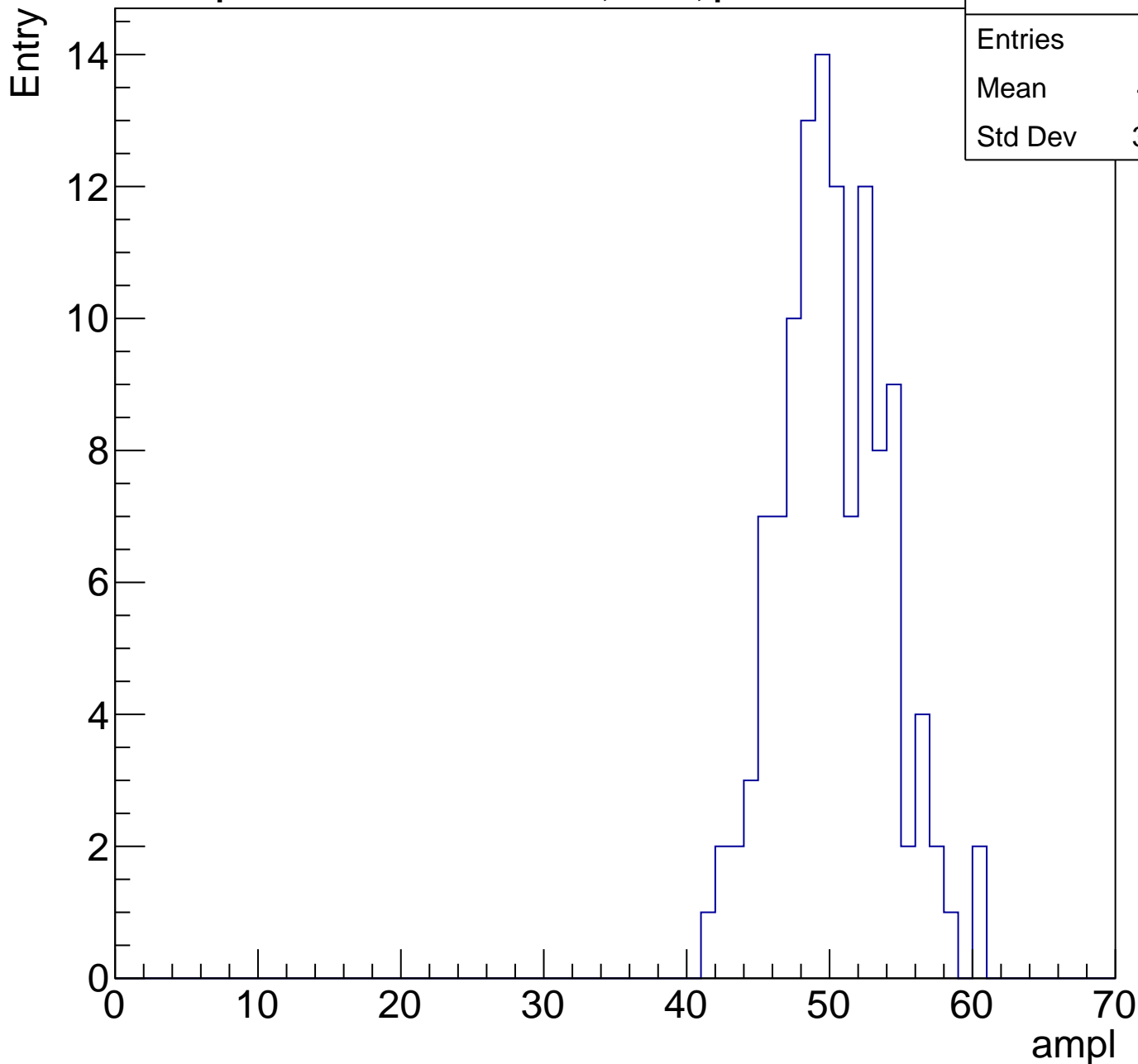
ampl



B0L101S, U18-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	49.81
Std Dev	3.774



B0L101S, U18-ch70, adc4

calib_packv5_042523_0143.root, FC#1, port C1

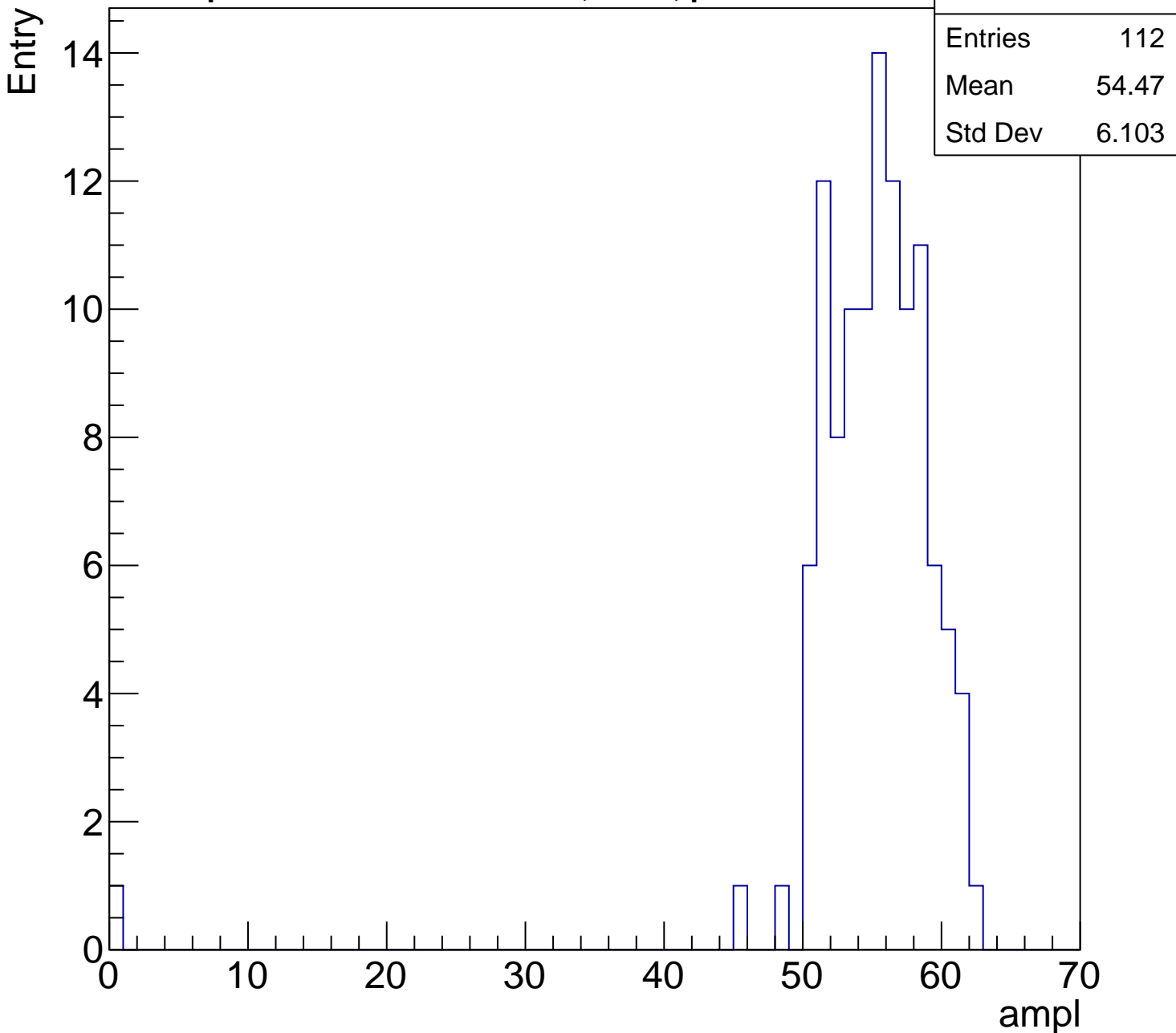
Entries	112
Mean	54.47
Std Dev	6.103

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

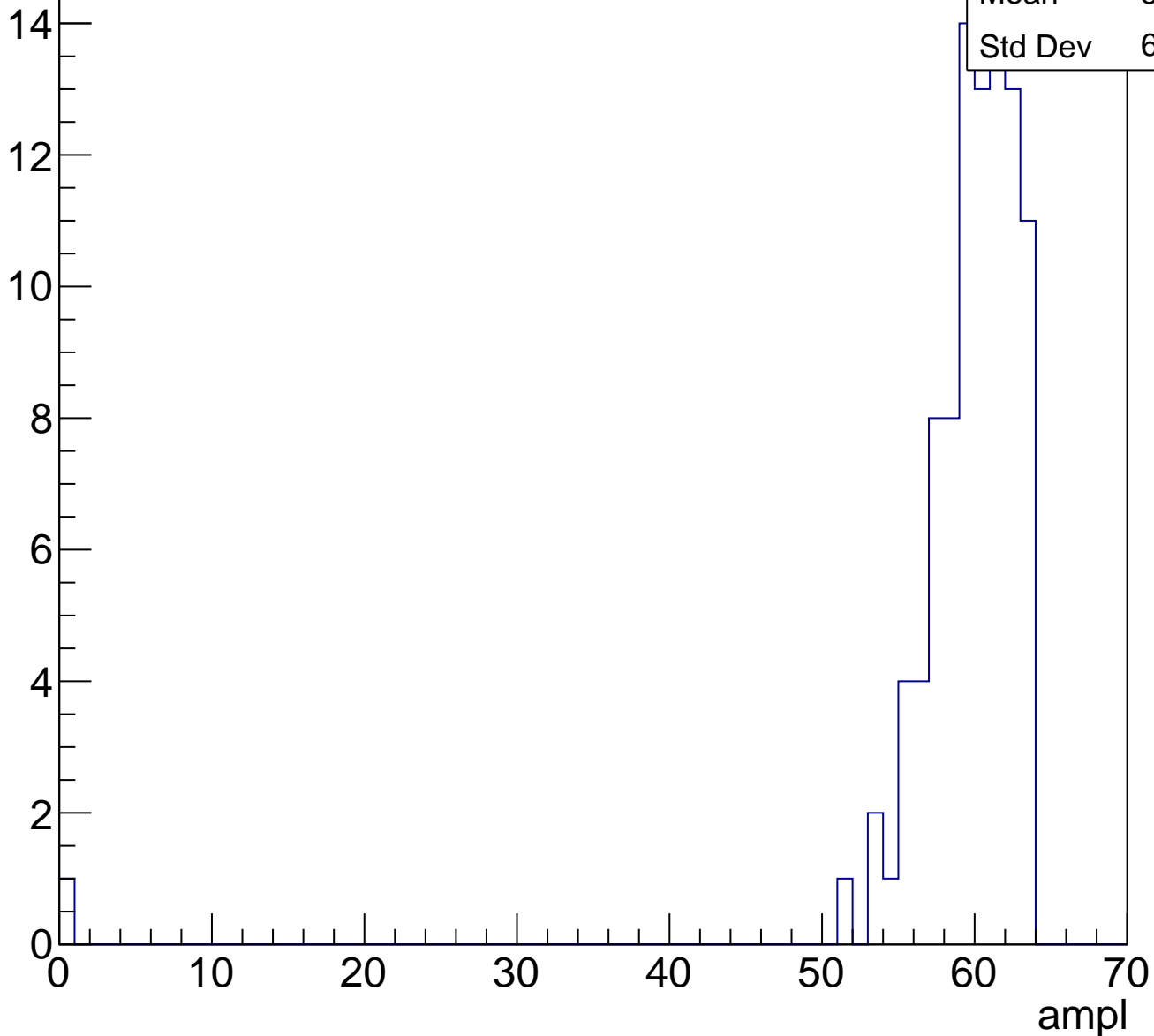
ampl



B0L101S, U18-ch70, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

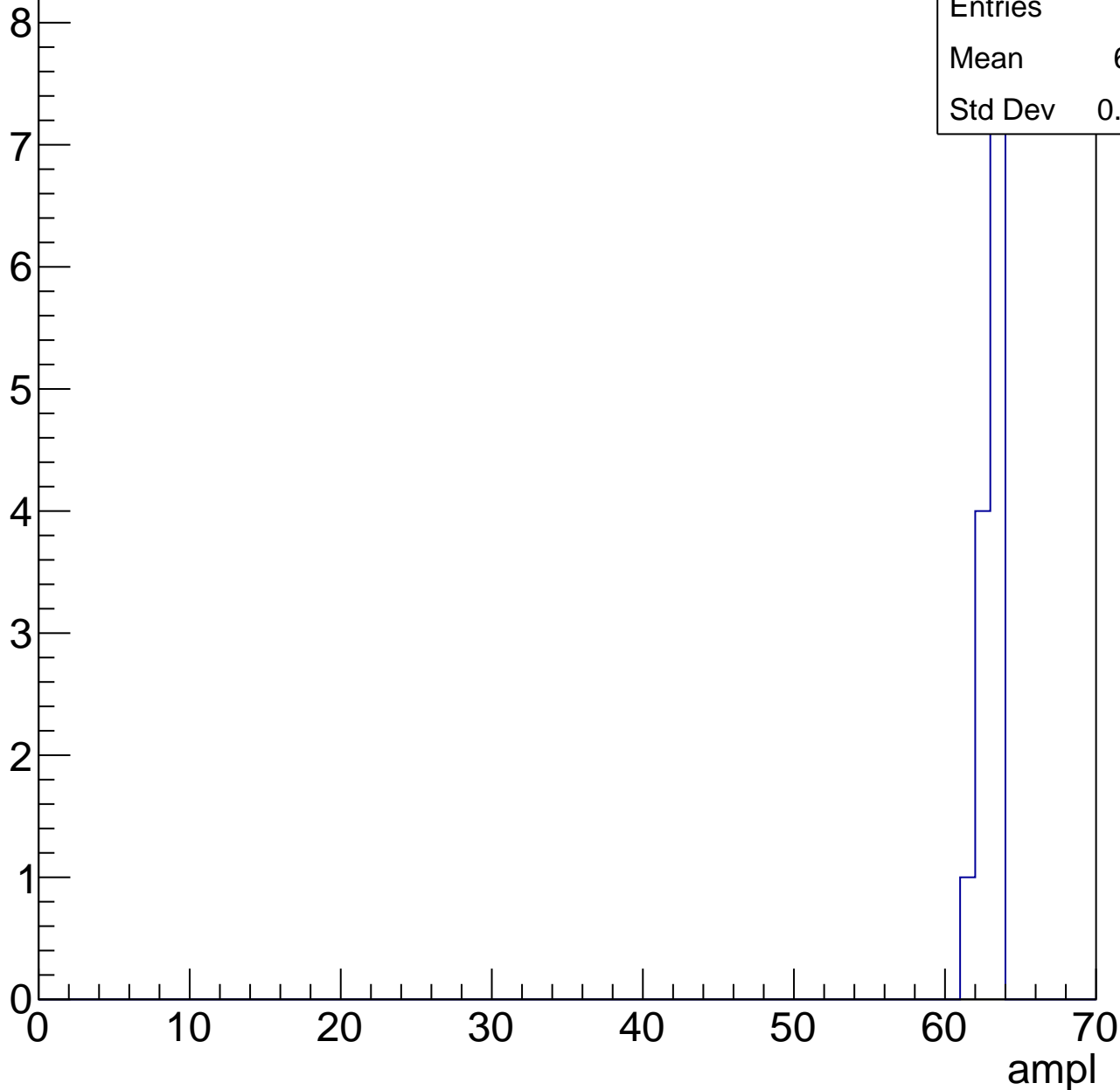


B0L101S, U18-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

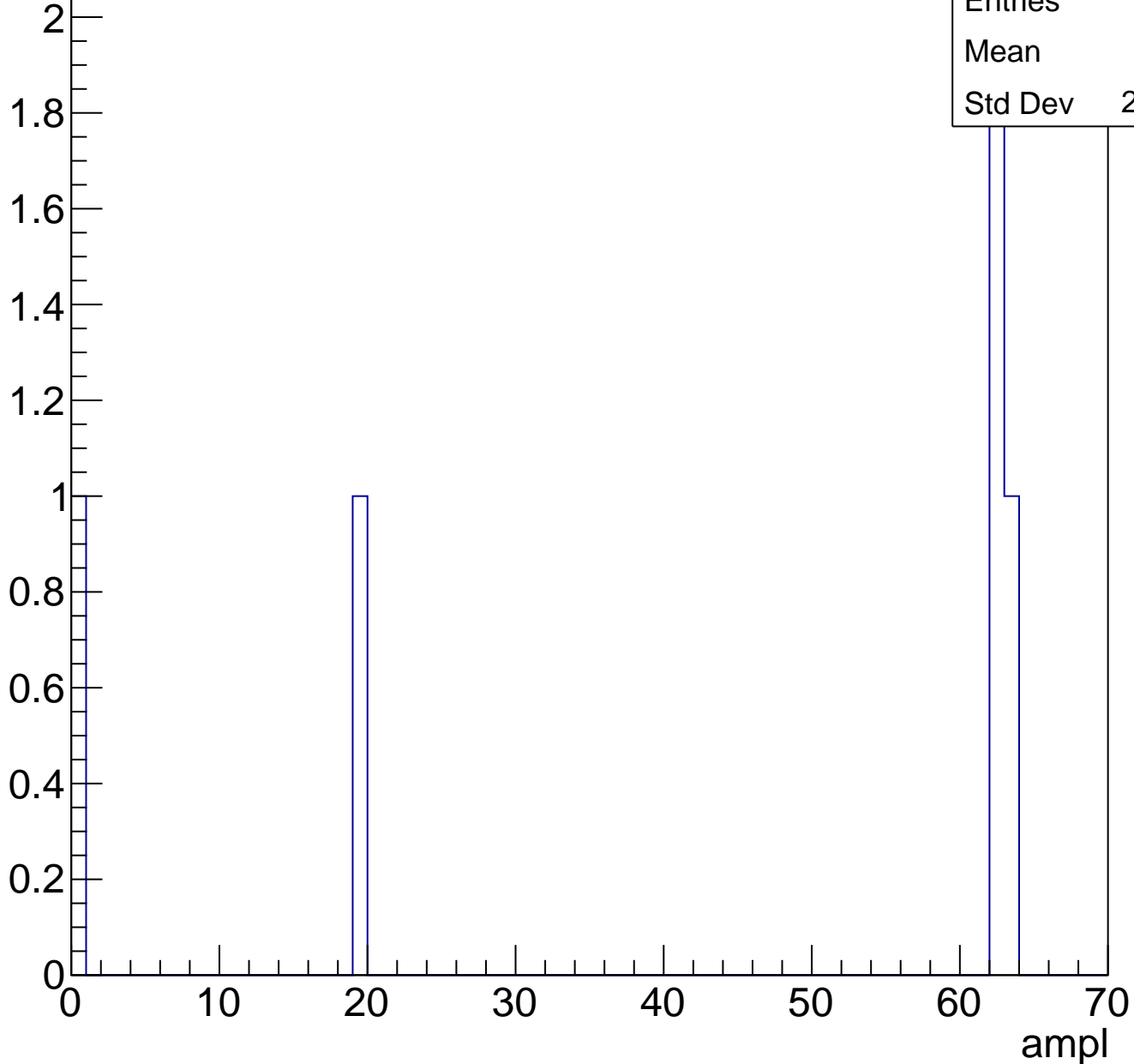
Entries	13
Mean	62.54
Std Dev	0.6343



B0L101S, U18-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	41.2
Std Dev	26.57

B0L101S, U18-ch71, adc0

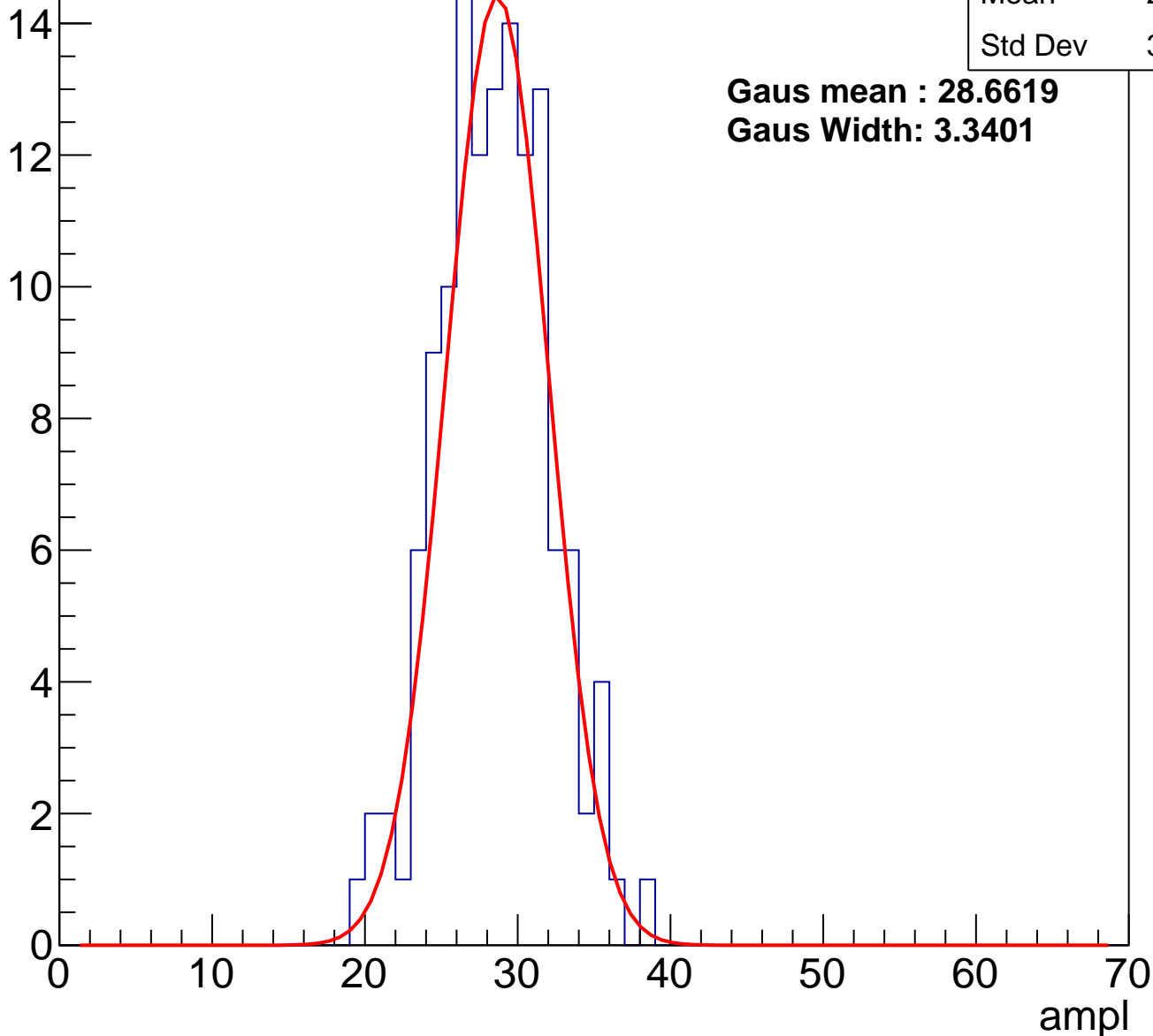
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	28.05
Std Dev	3.569

Gaus mean : 28.6619

Gaus Width: 3.3401

Entry



B0L101S, U18-ch71, adc1

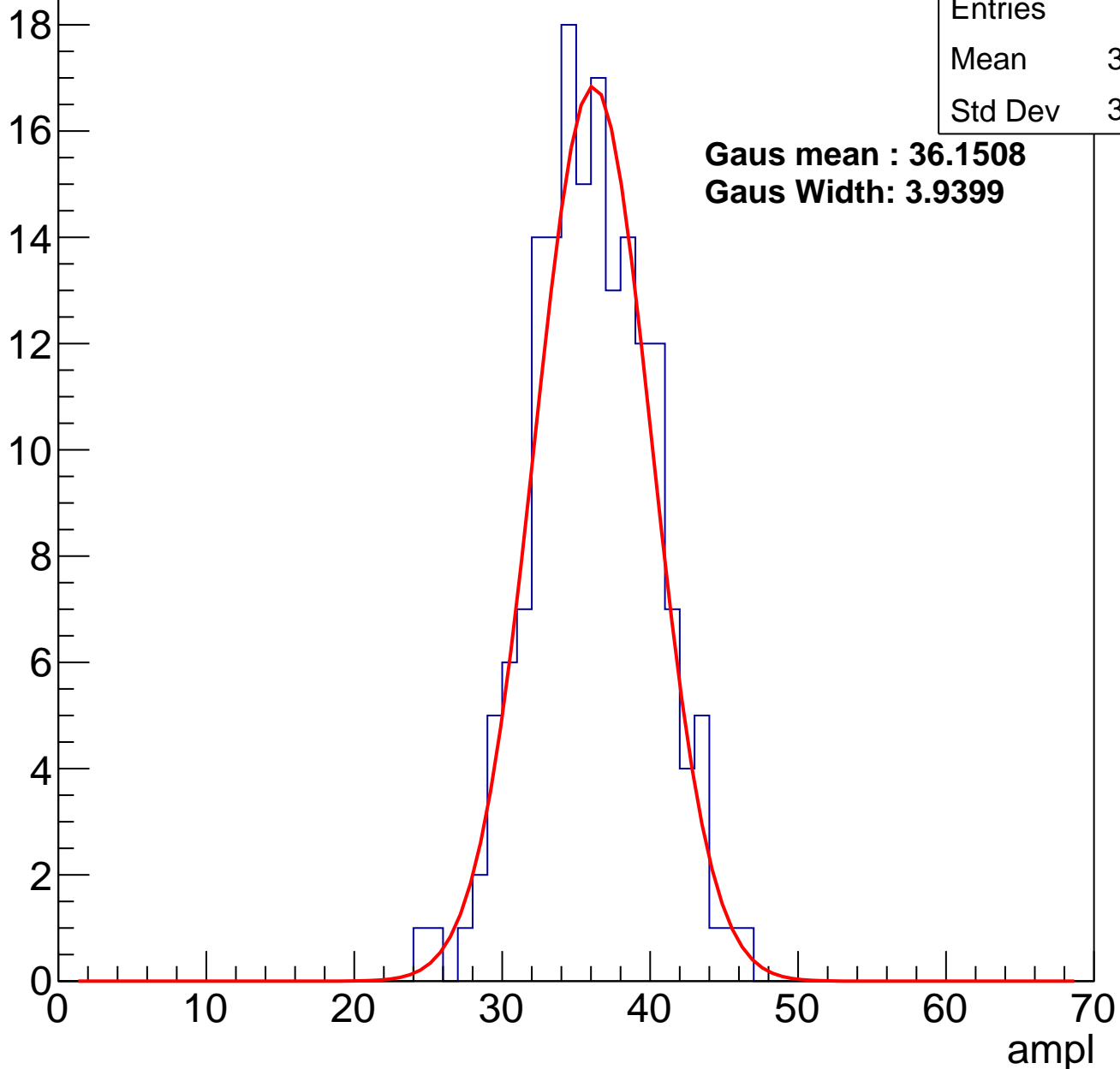
calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	35.67
Std Dev	3.978

Gaus mean : 36.1508

Gaus Width: 3.9399

Entry



B0L101S, U18-ch71, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

119

Mean

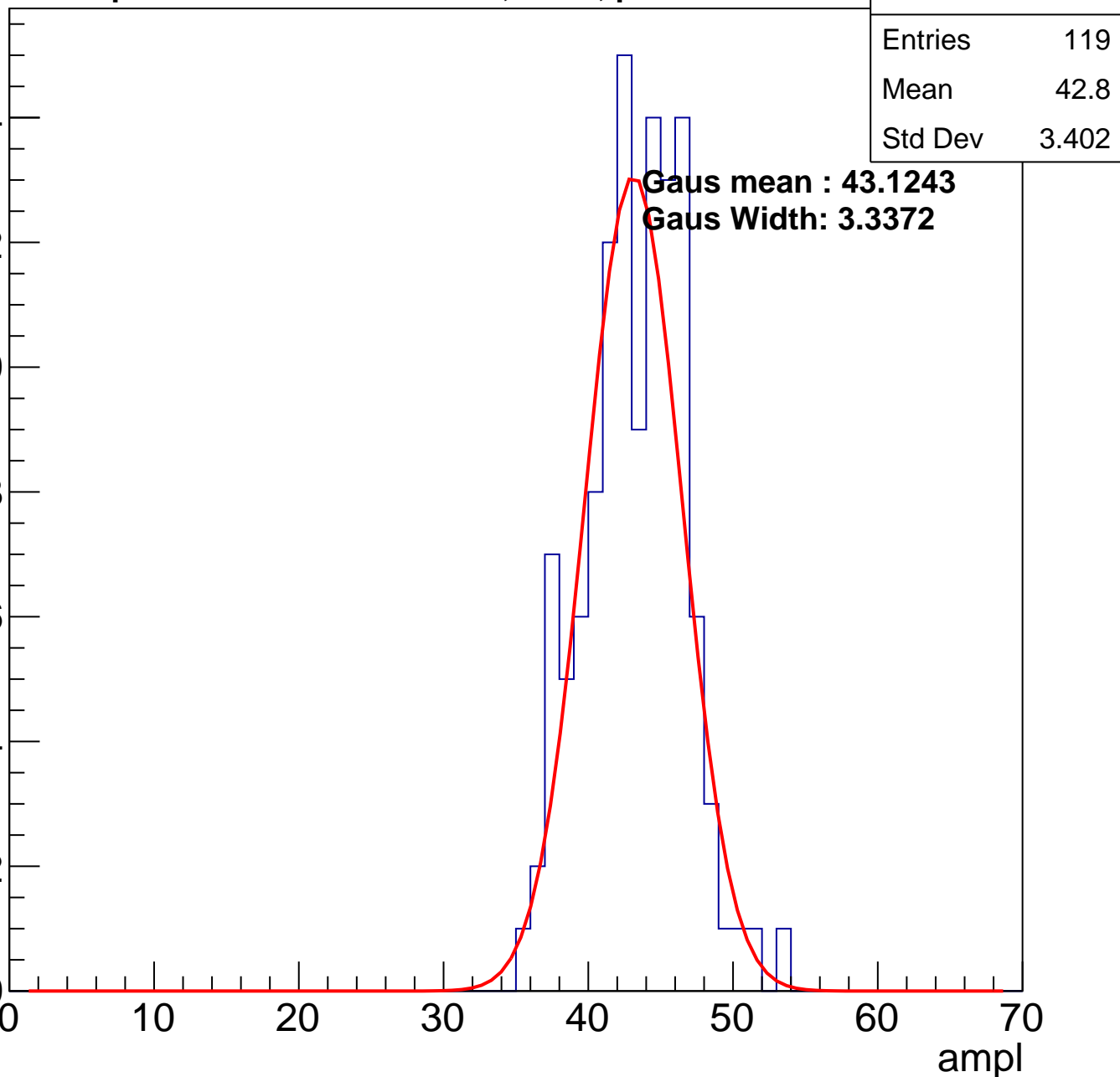
42.8

Std Dev

3.402

Gaus mean : 43.1243

Gaus Width: 3.3372

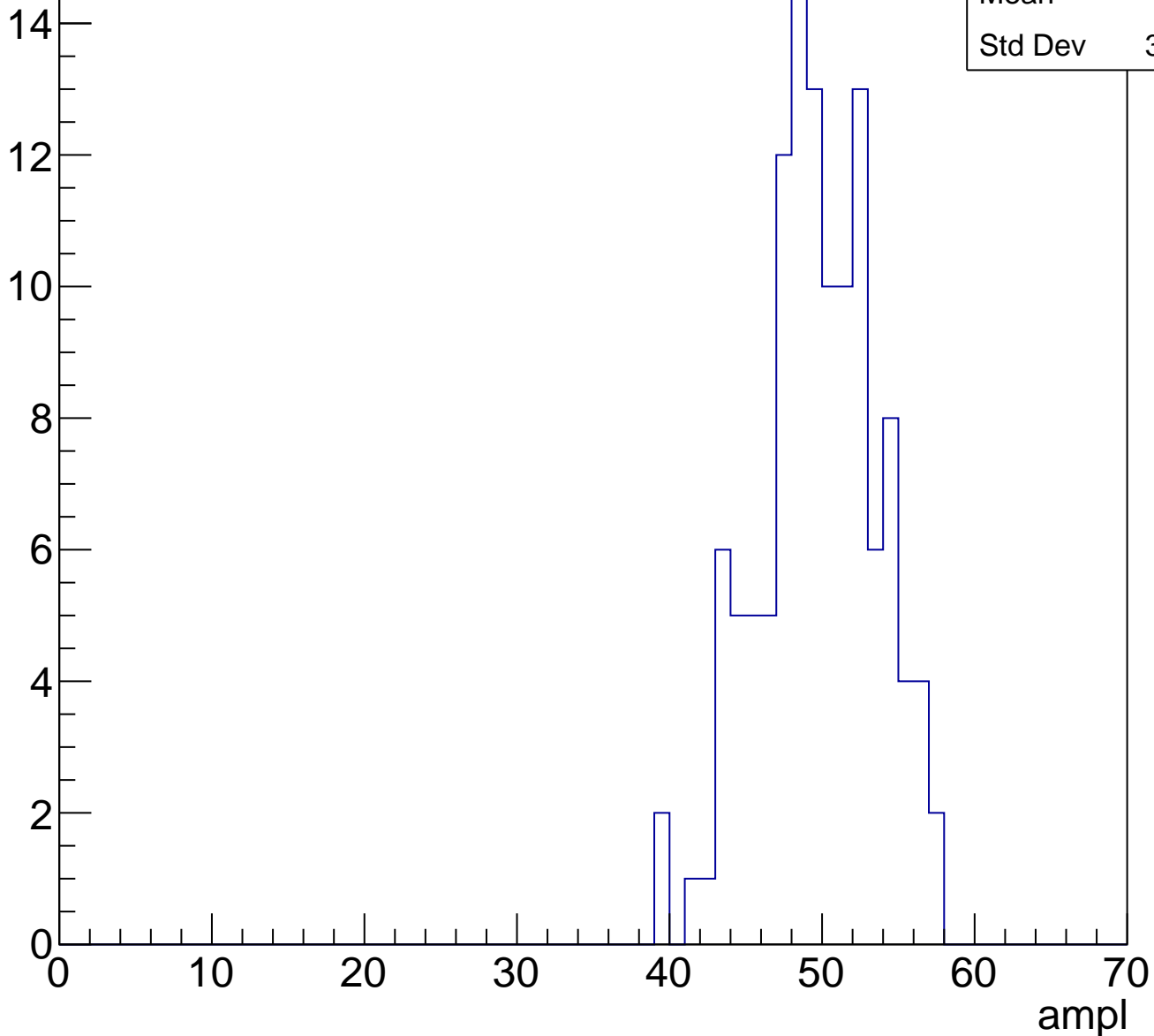


B0L101S, U18-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	49.25
Std Dev	3.804

Entry



B0L101S, U18-ch71, adc4

calib_packv5_042523_0143.root, FC#1, port C1

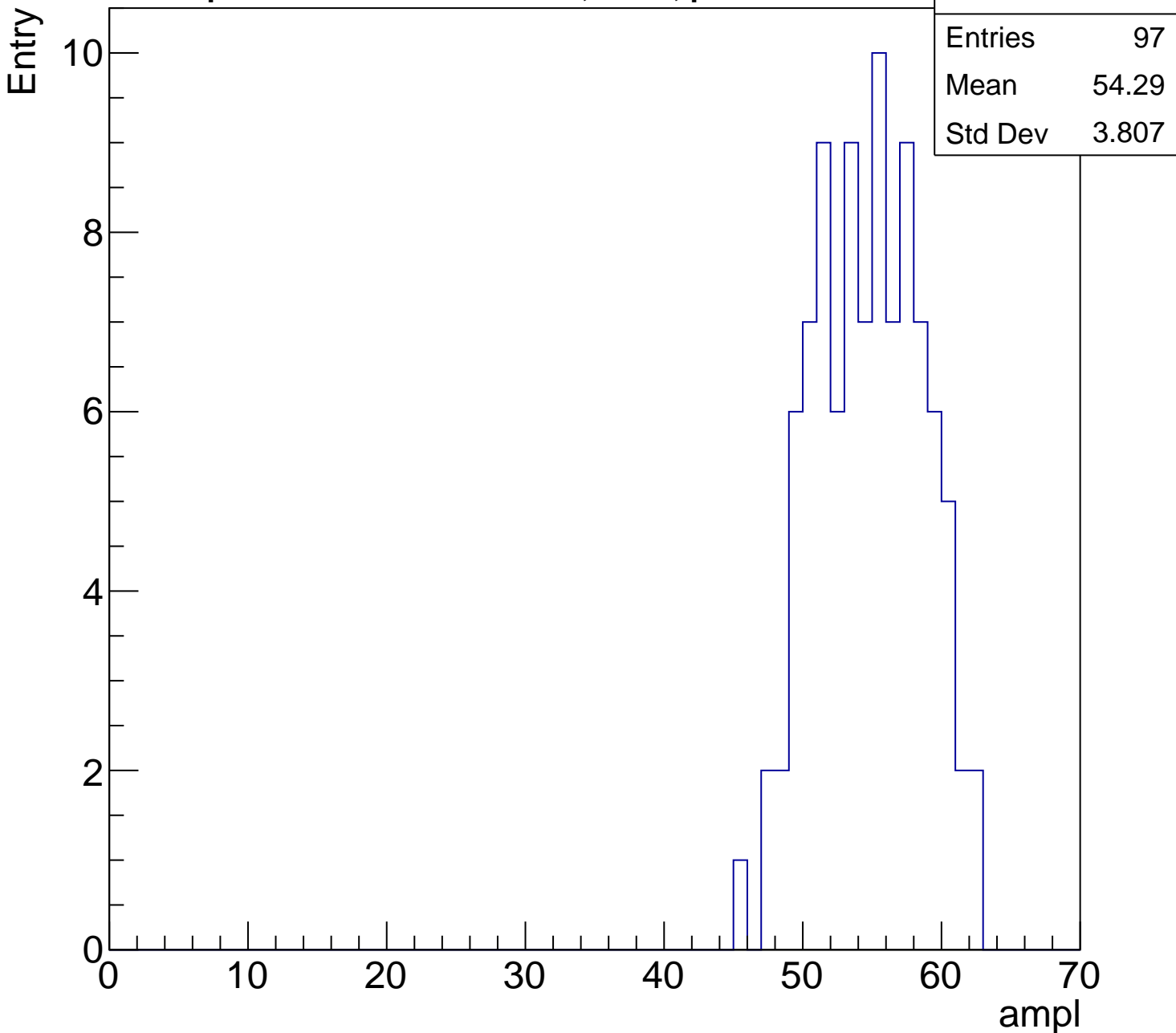
Entries	97
Mean	54.29
Std Dev	3.807

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch71, adc5

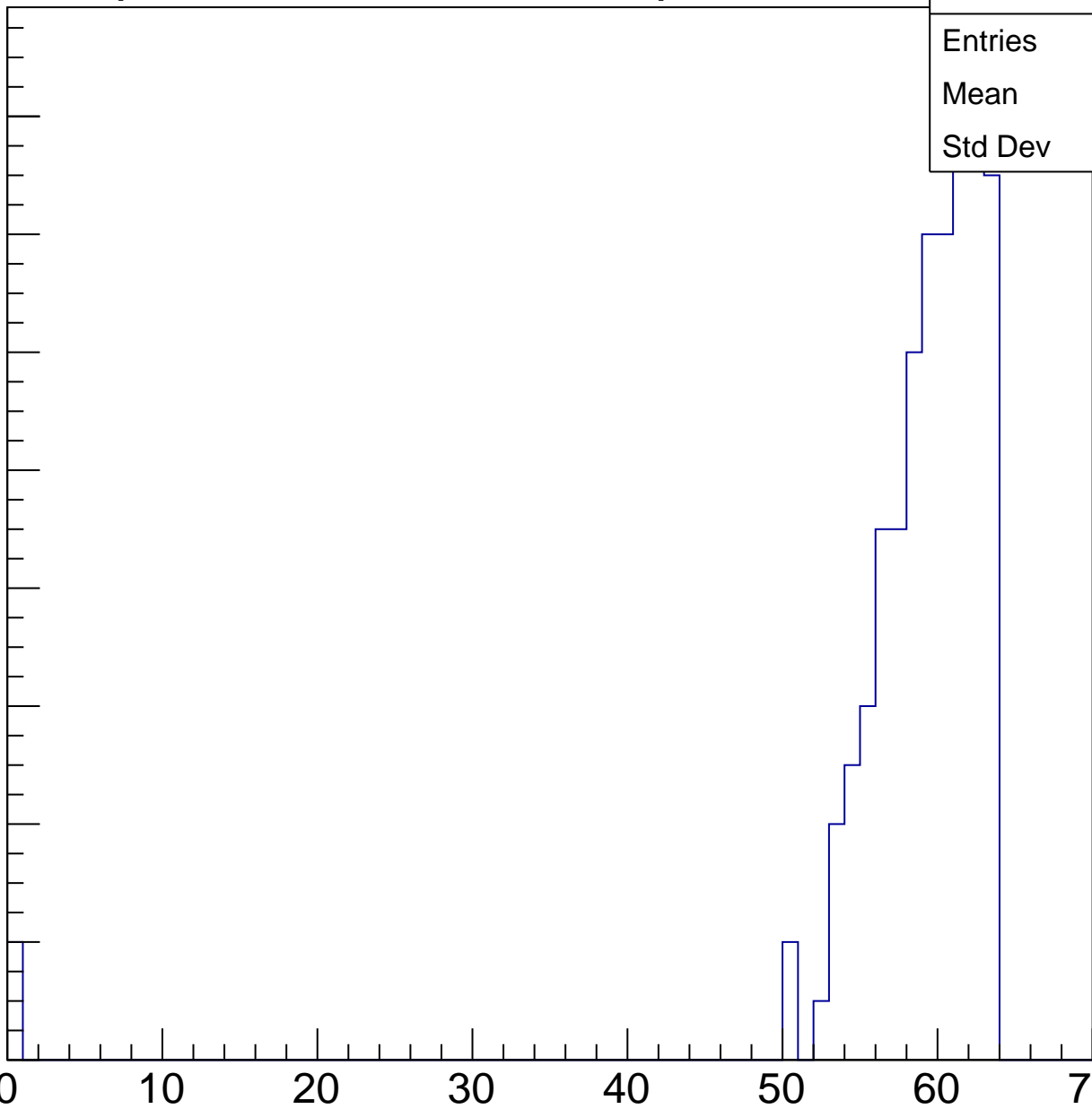
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	58.07
Std Dev	7.98

ampl

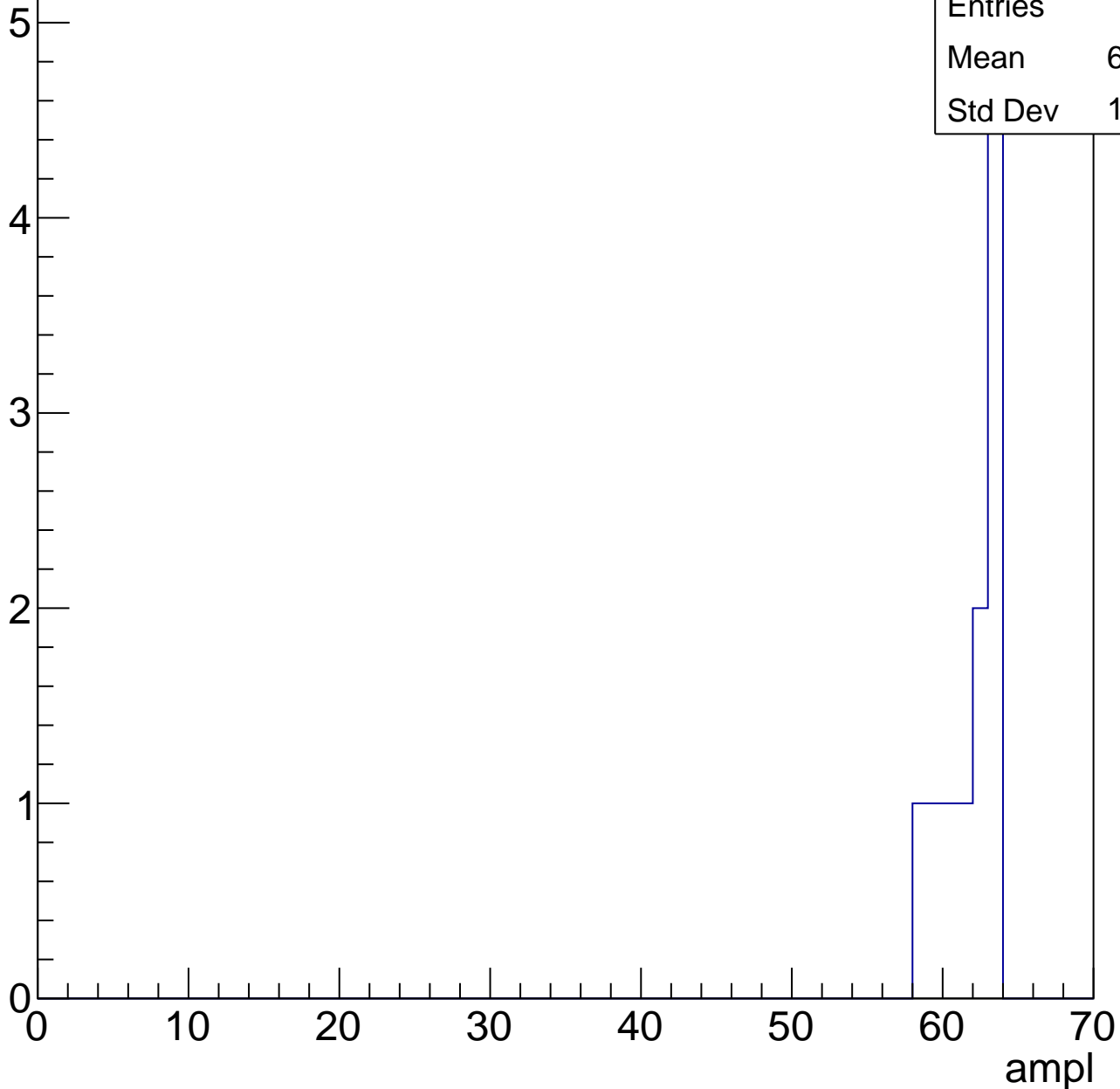


B0L101S, U18-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	61.55
Std Dev	1.725



B0L101S, U18-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch72, adc0

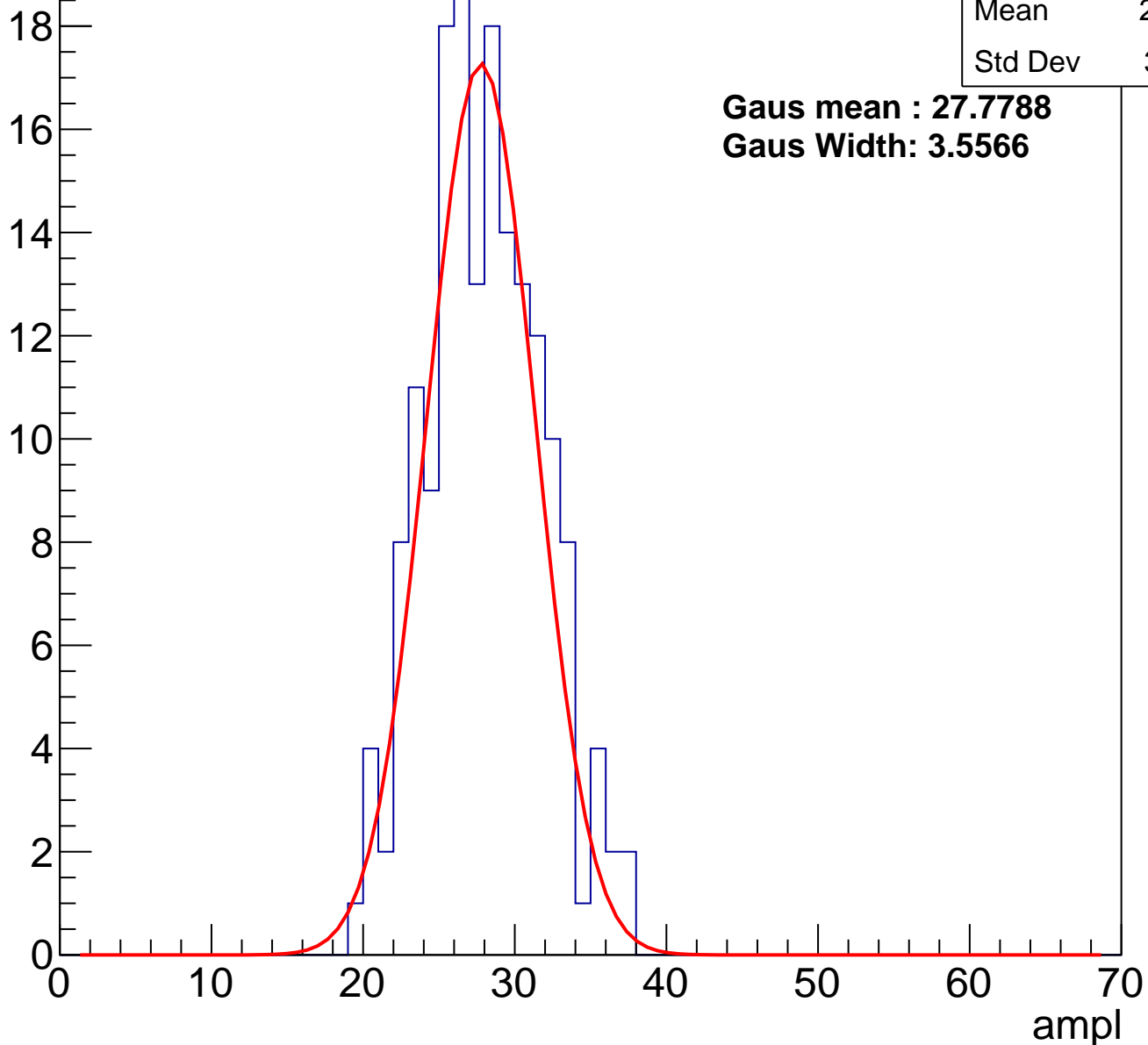
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	27.56
Std Dev	3.781

Gaus mean : 27.7788

Gaus Width: 3.5566

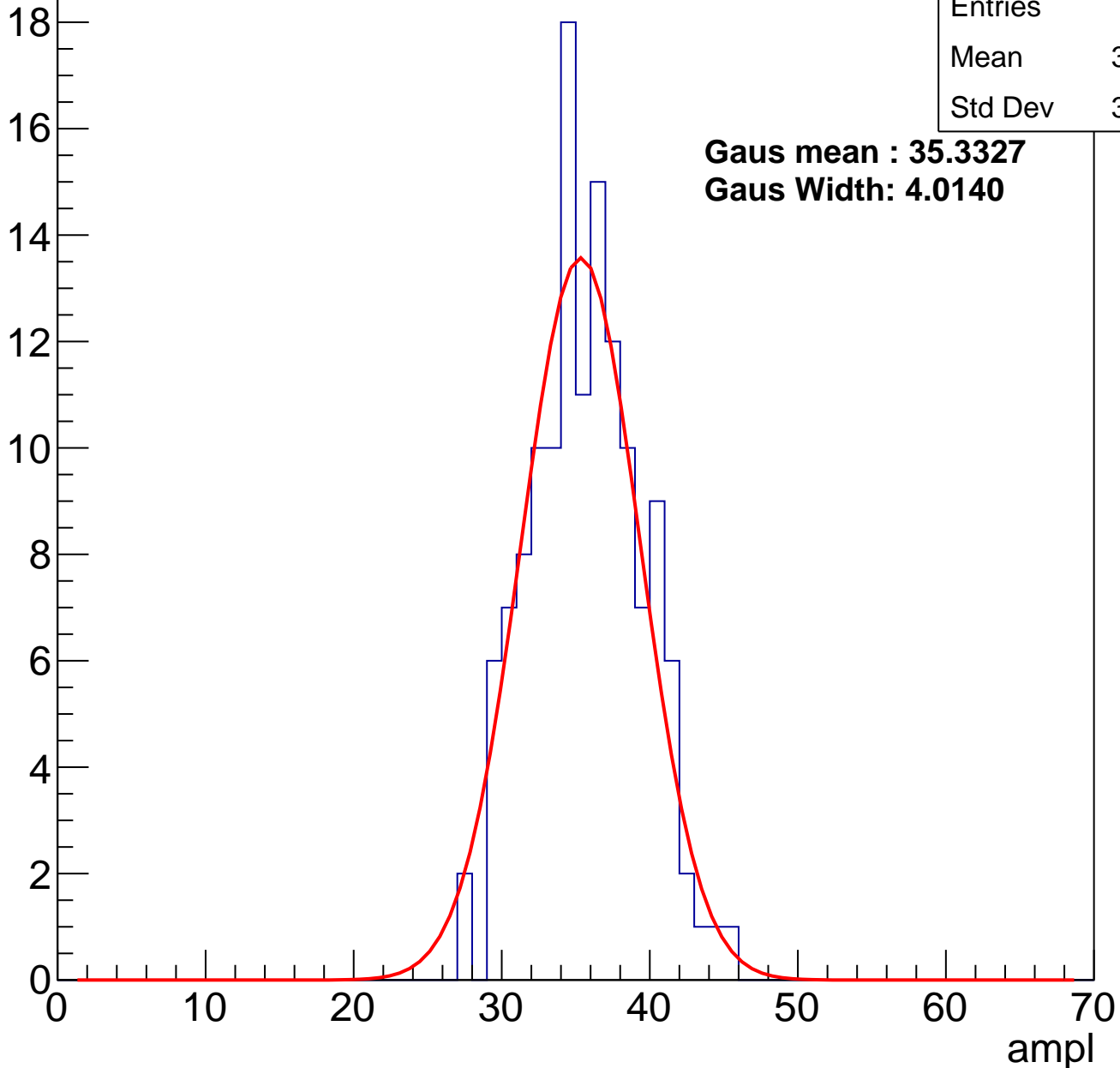
Entry



B0L101S, U18-ch72, adc1

calib_packv5_042523_0143.root, FC#1, port C1

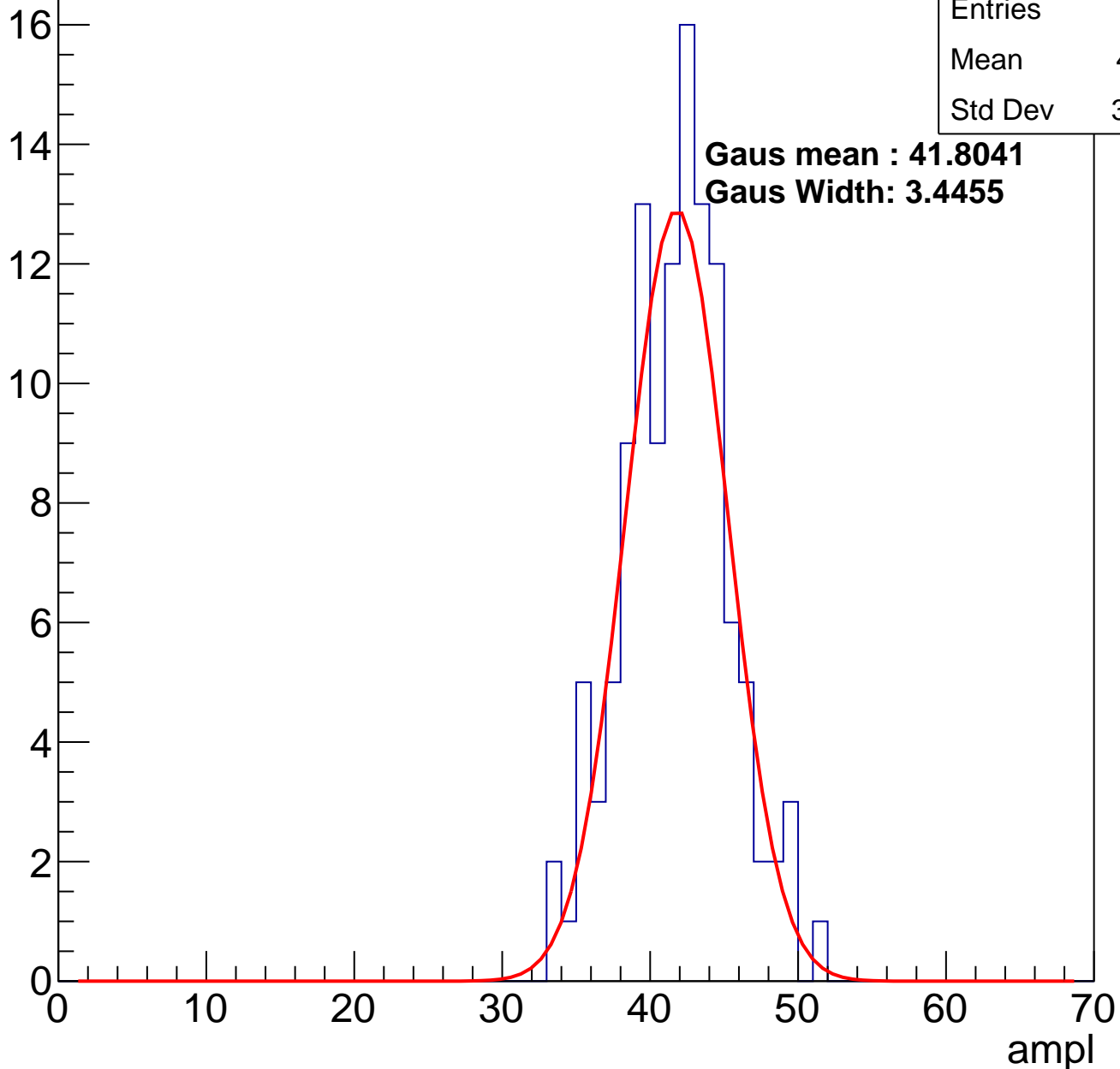
Entry



B0L101S, U18-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

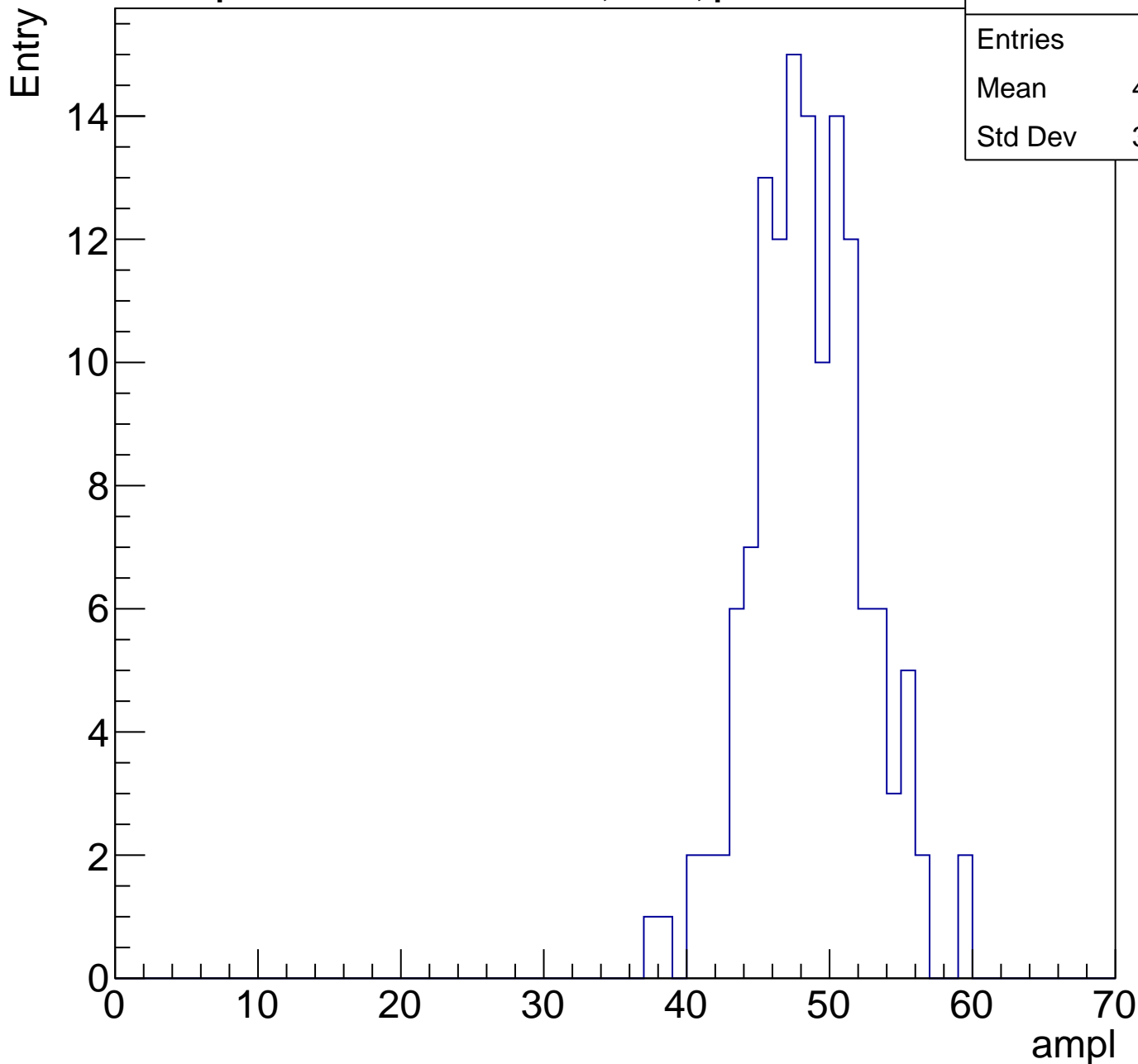
Entry



B0L101S, U18-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	48.15
Std Dev	3.945



B0L101S, U18-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

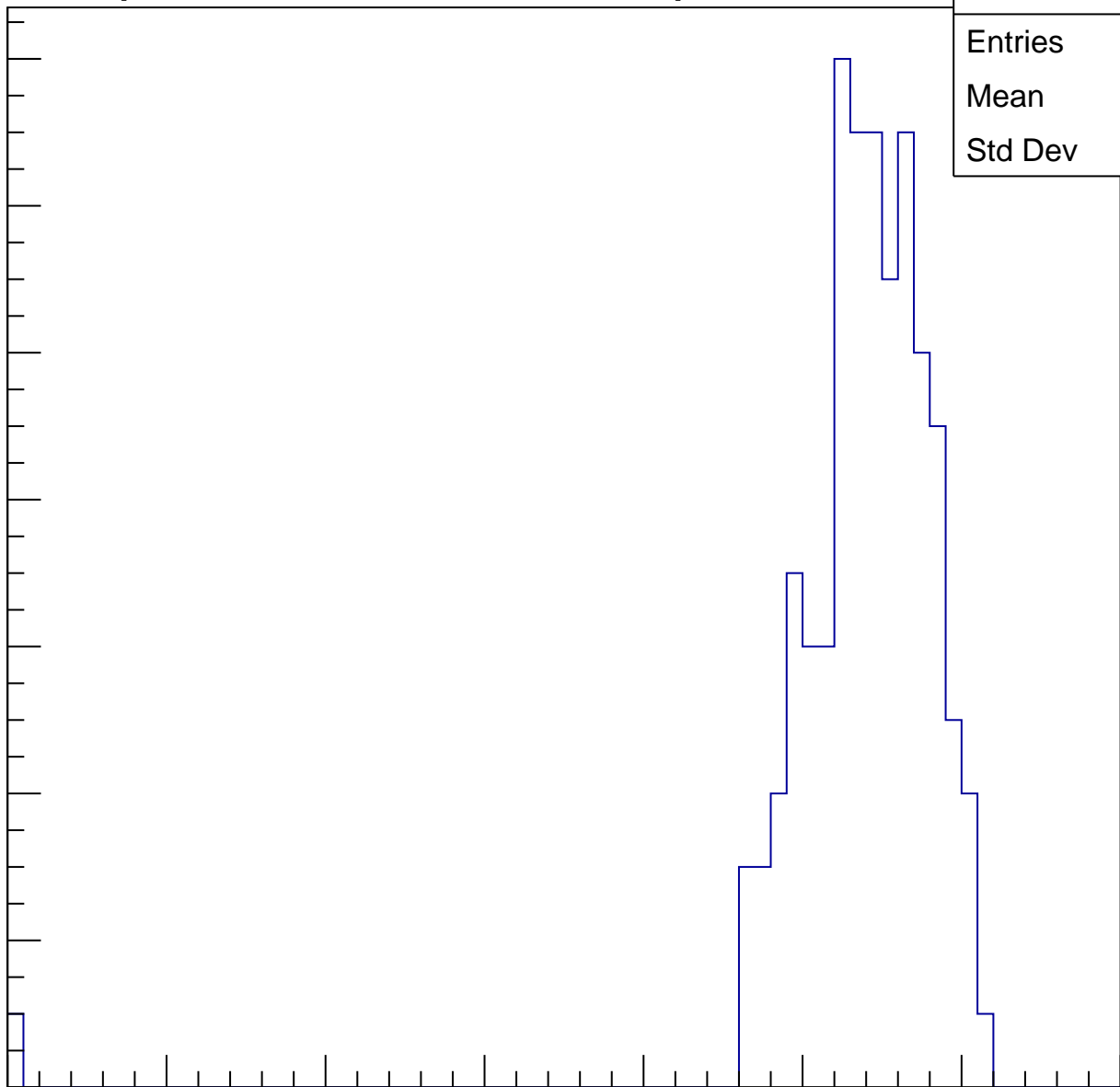
Entries	123
Mean	53.33
Std Dev	5.949

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch72, adc5

calib_packv5_042523_0143.root, FC#1, port C1

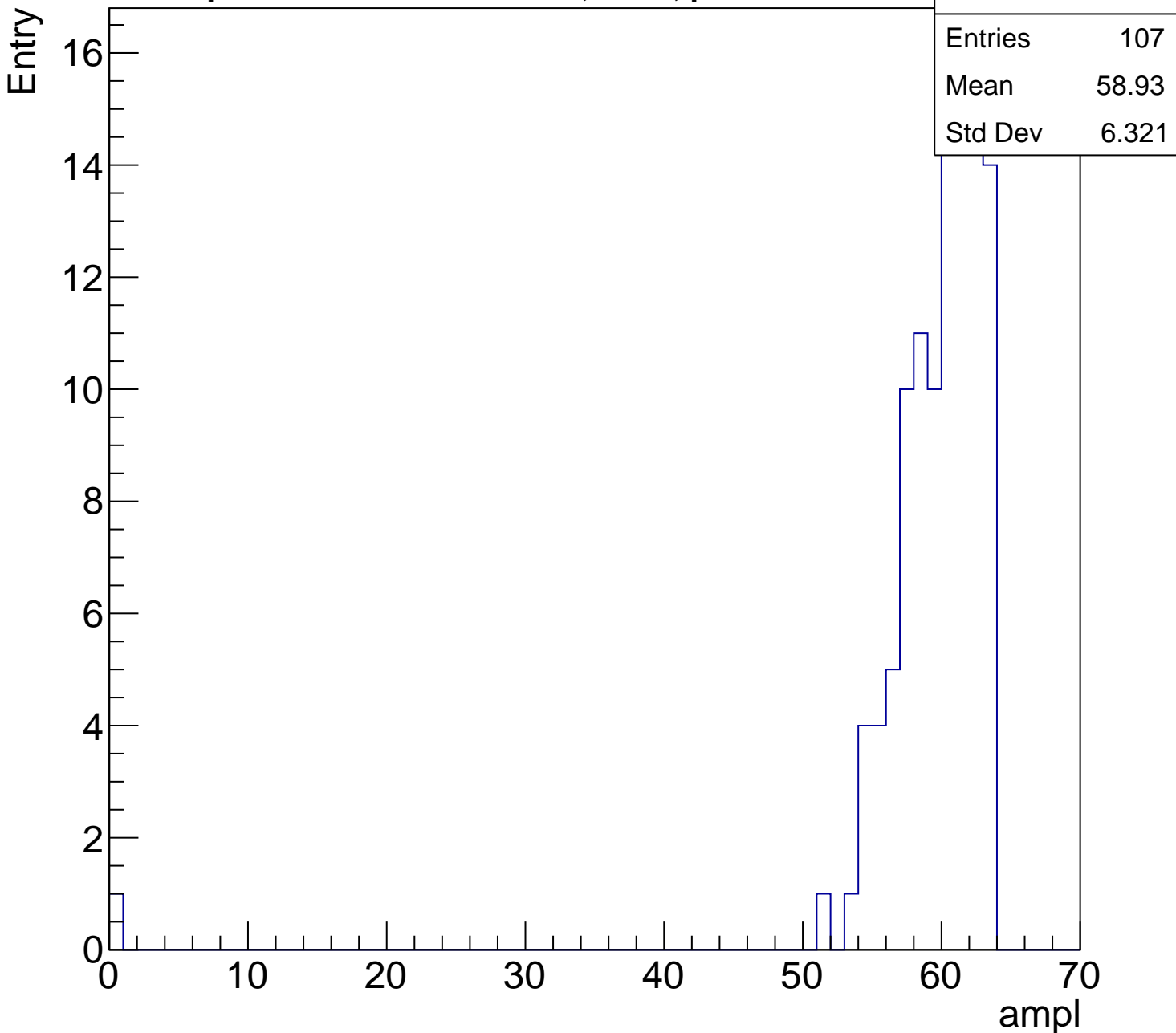
Entries	107
Mean	58.93
Std Dev	6.321

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

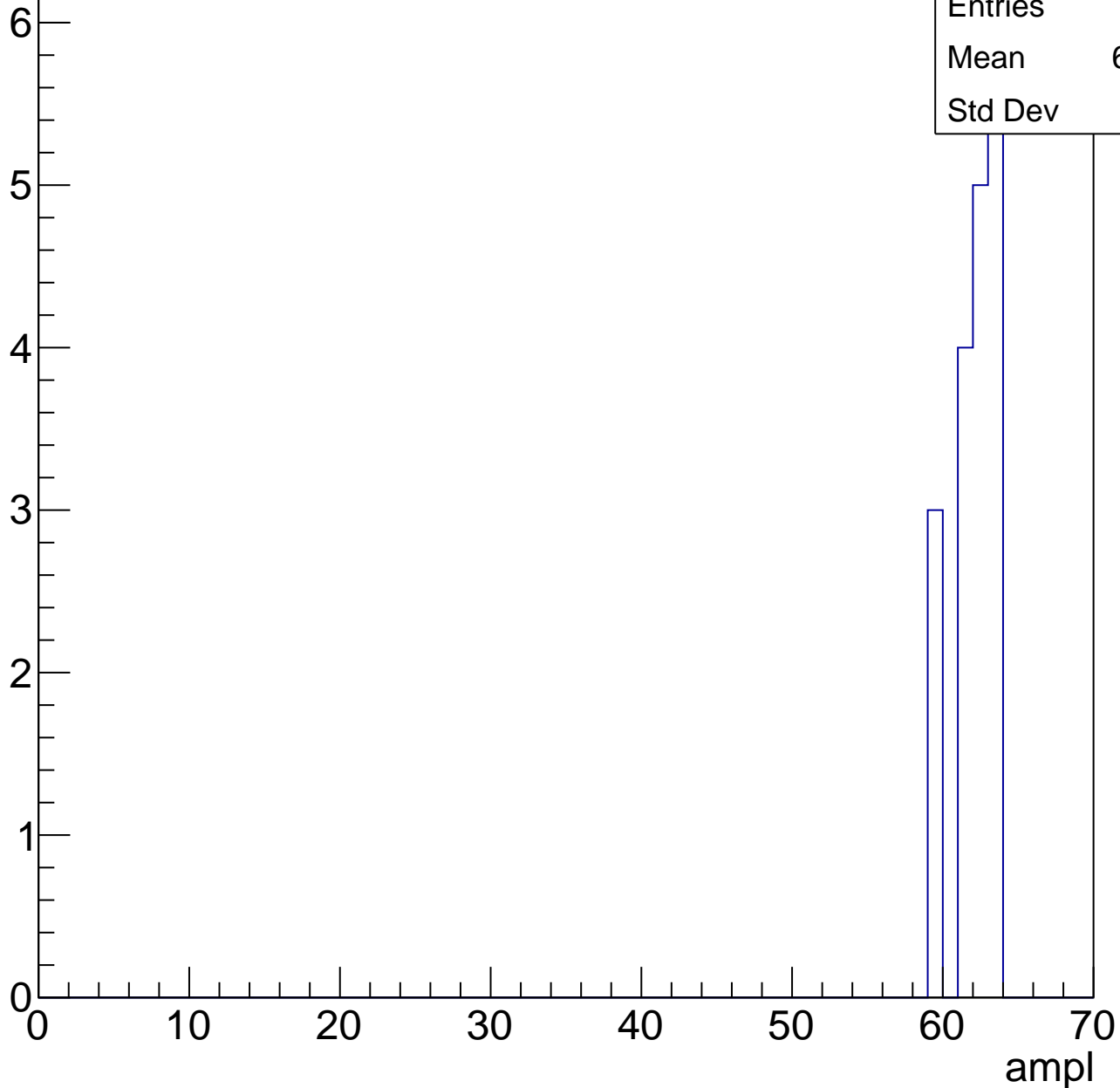
ampl



B0L101S, U18-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

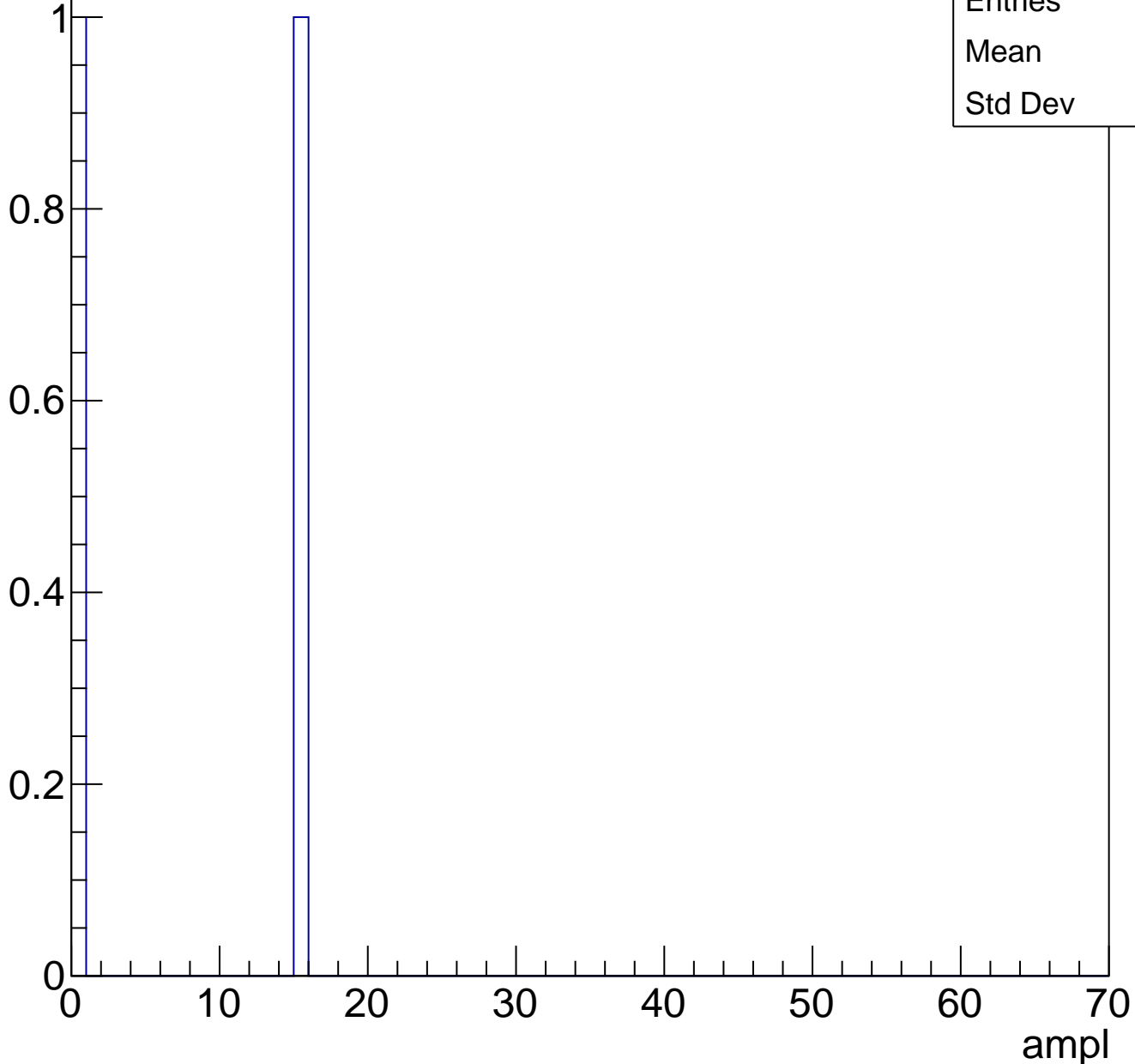
Entry



B0L101S, U18-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

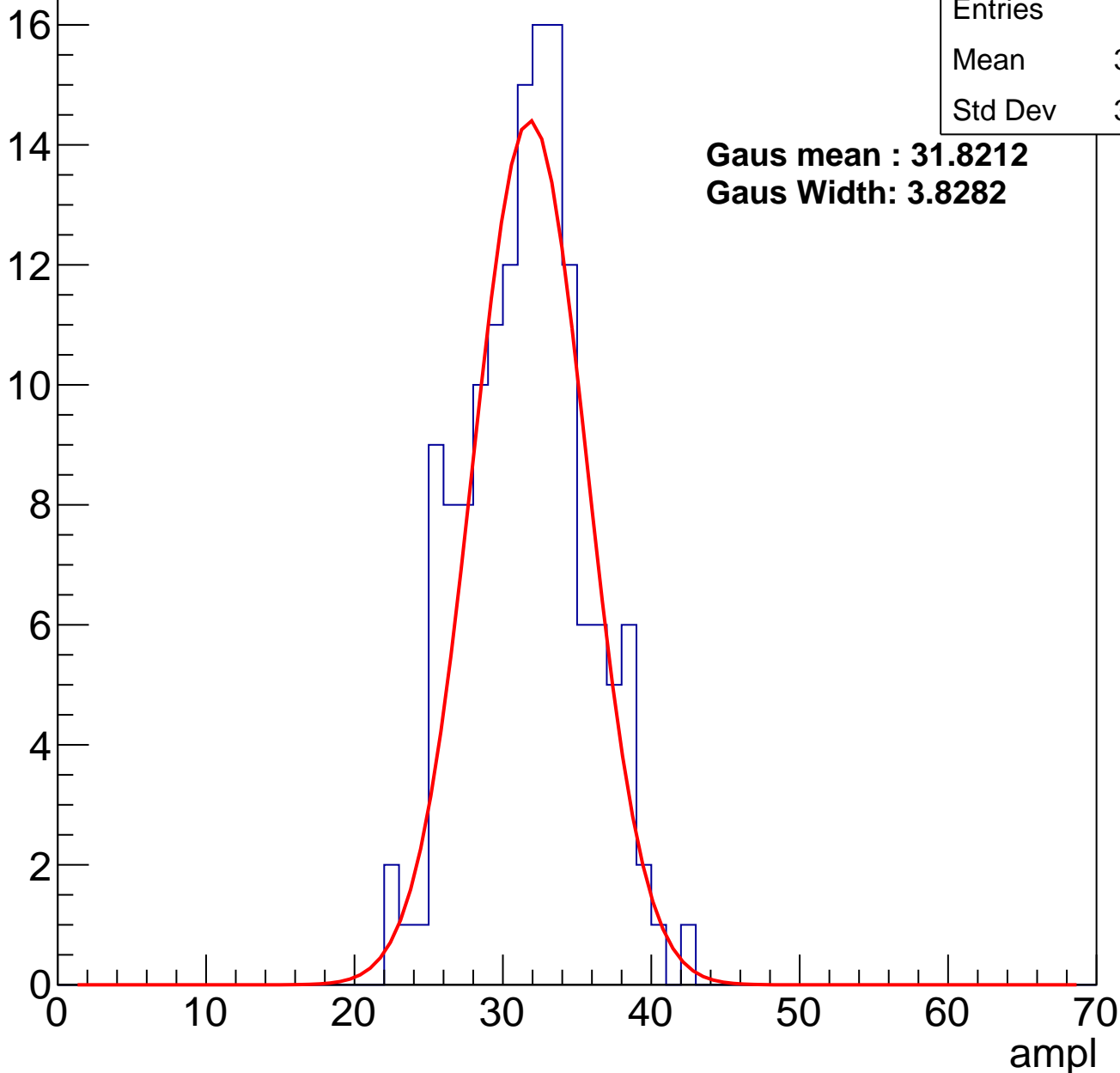


Entries	2
Mean	7.5
Std Dev	7.5

B0L101S, U18-ch73, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch73, adc1

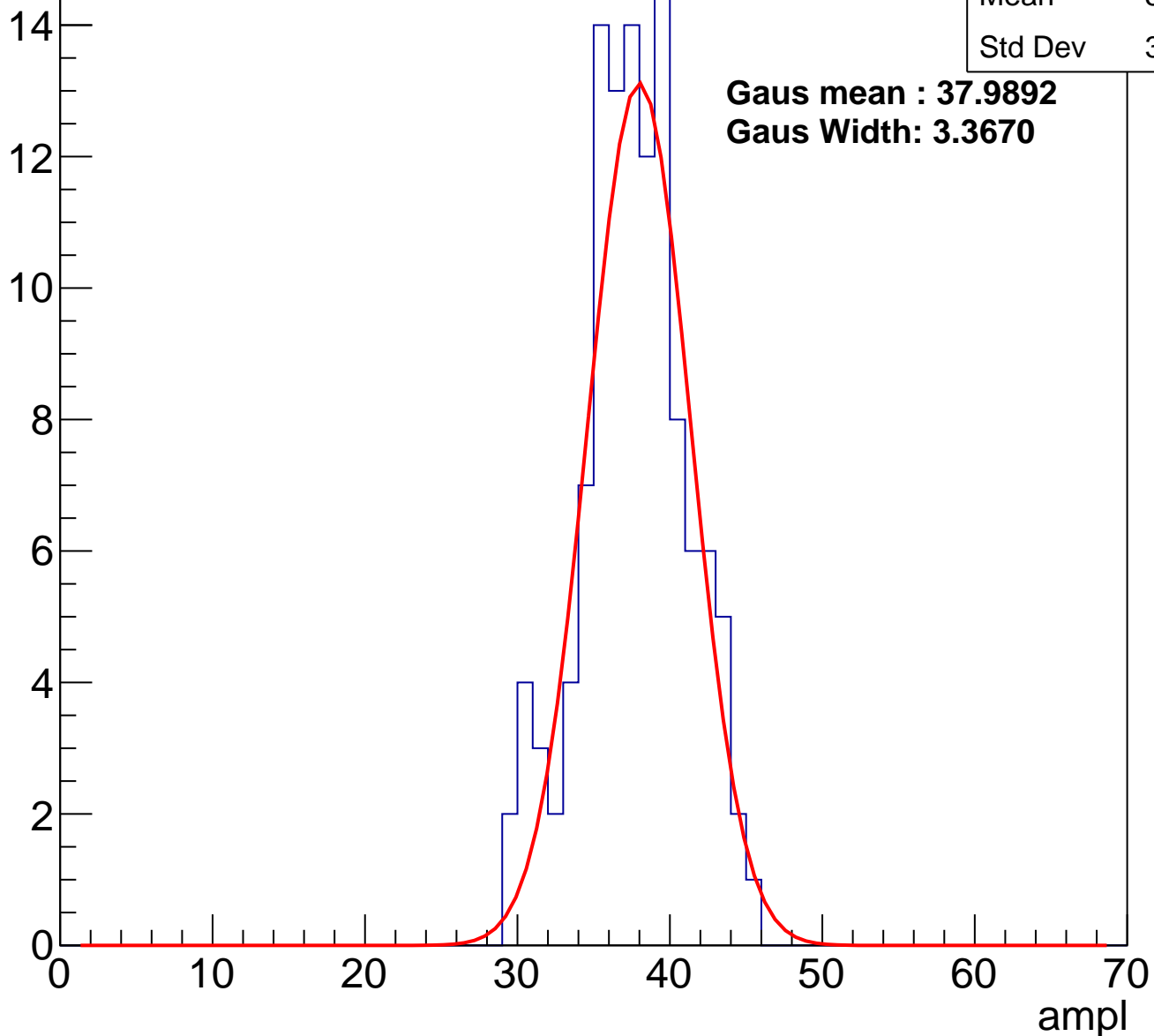
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	37.19
Std Dev	3.452

Gaus mean : 37.9892

Gaus Width: 3.3670

Entry



B0L101S, U18-ch73, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

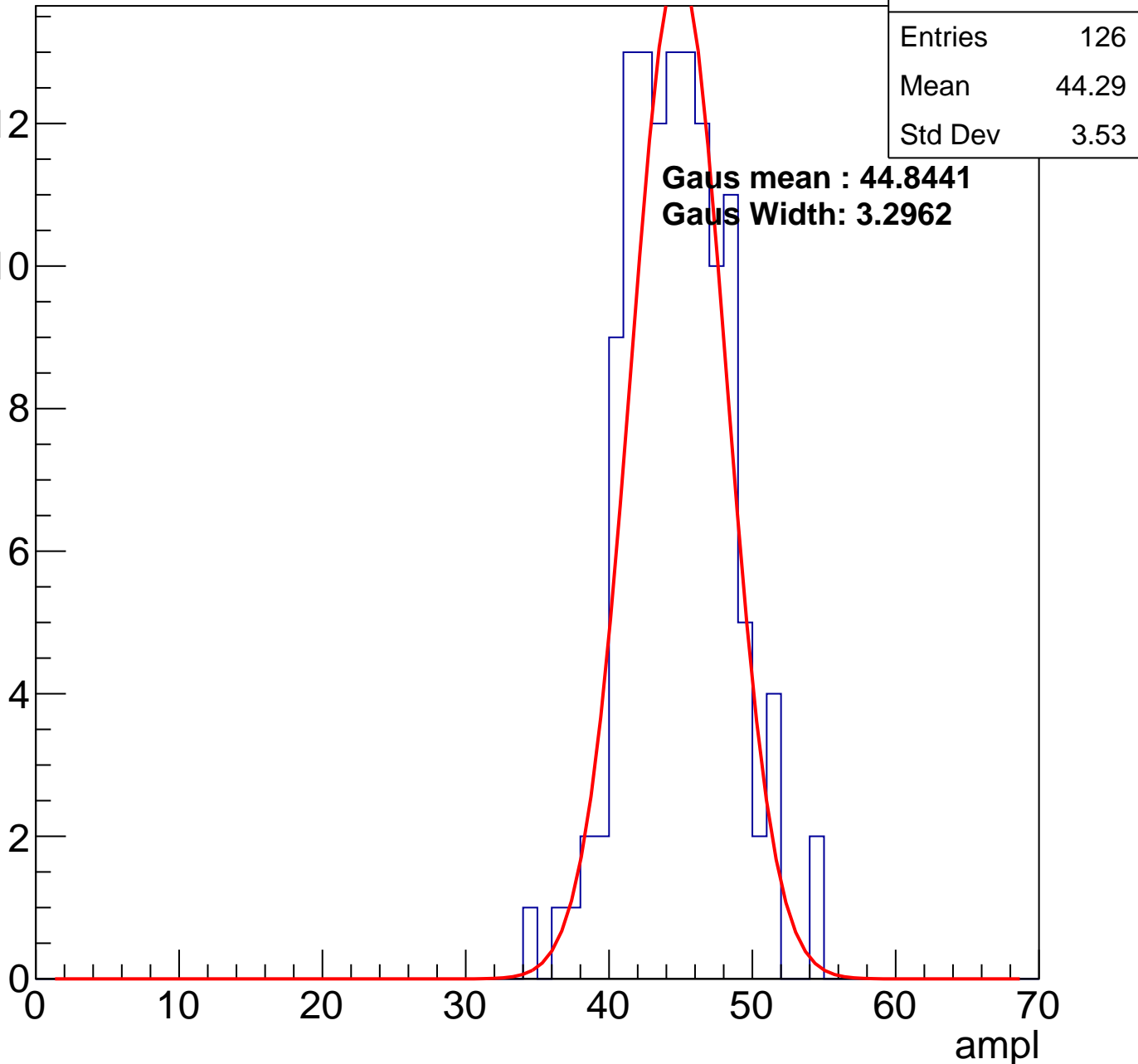
70

ampl

Entries	126
Mean	44.29
Std Dev	3.53

Gaus mean : 44.8441

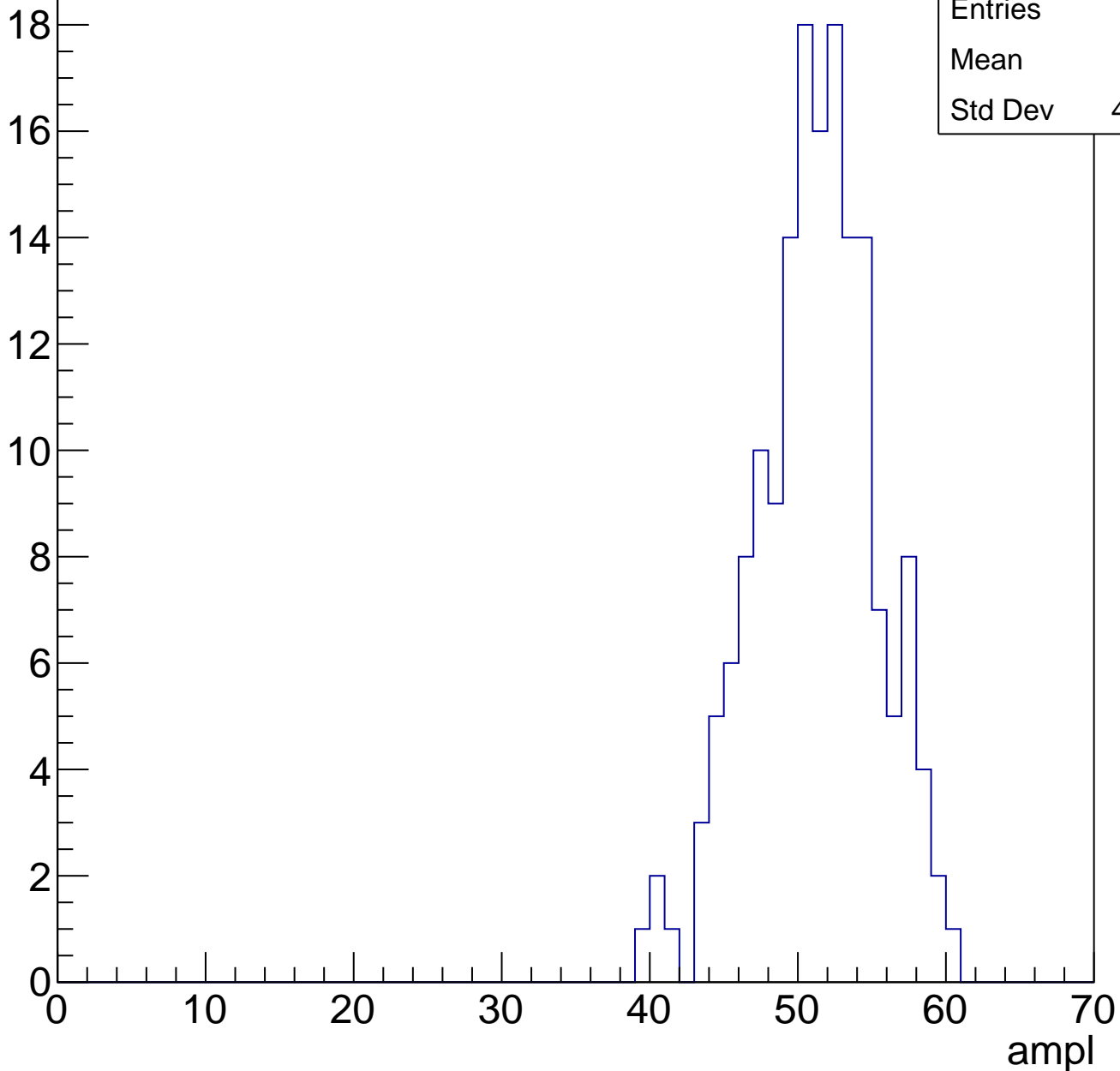
Gaus Width: 3.2962



B0L101S, U18-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	166
Mean	50.7
Std Dev	4.084

B0L101S, U18-ch73, adc4

calib_packv5_042523_0143.root, FC#1, port C1

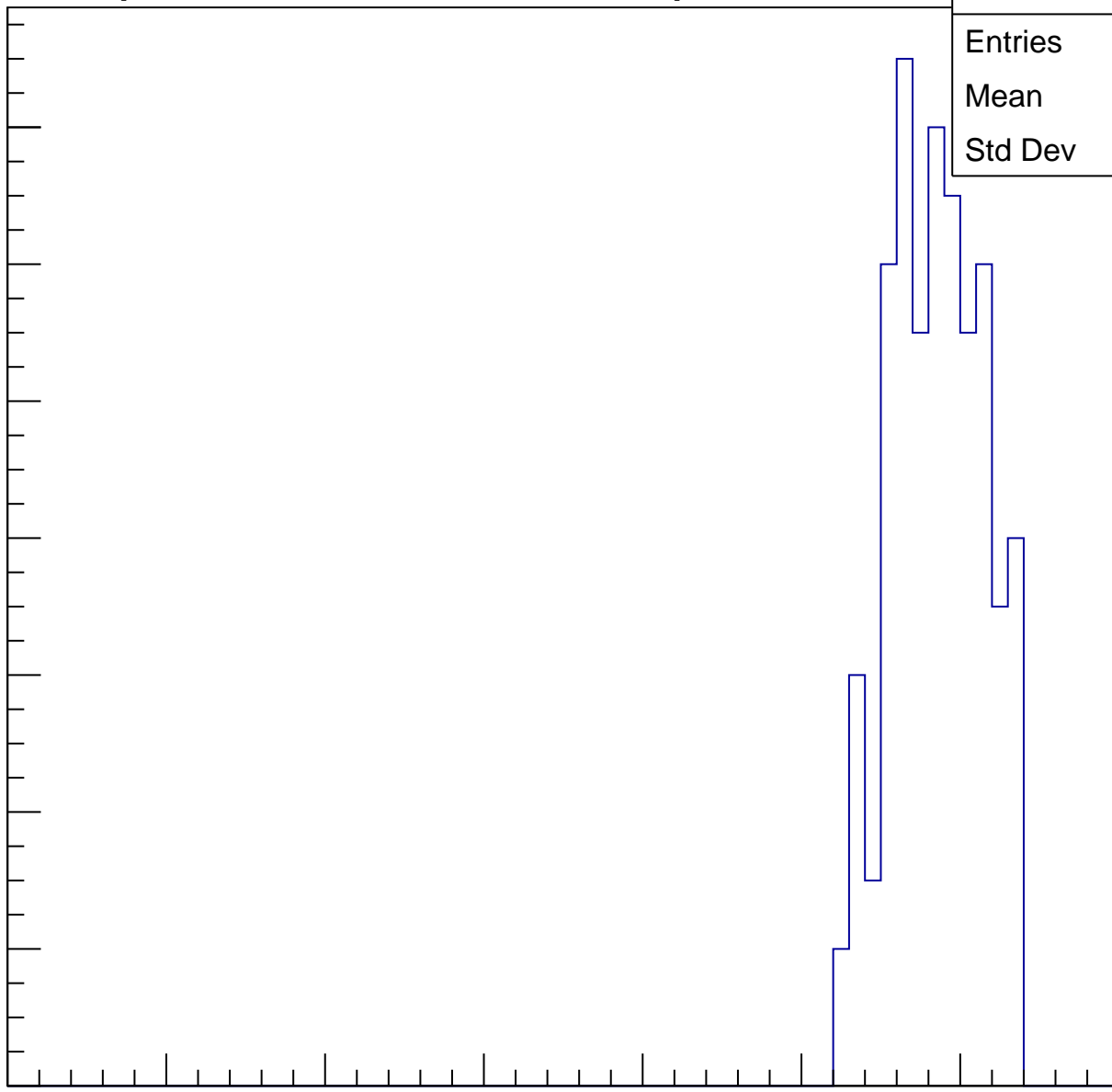
Entries	114
Mean	58.07
Std Dev	2.849

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch73, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	50
Mean	59.42
Std Dev	8.879

ampl

0

10

20

30

40

50

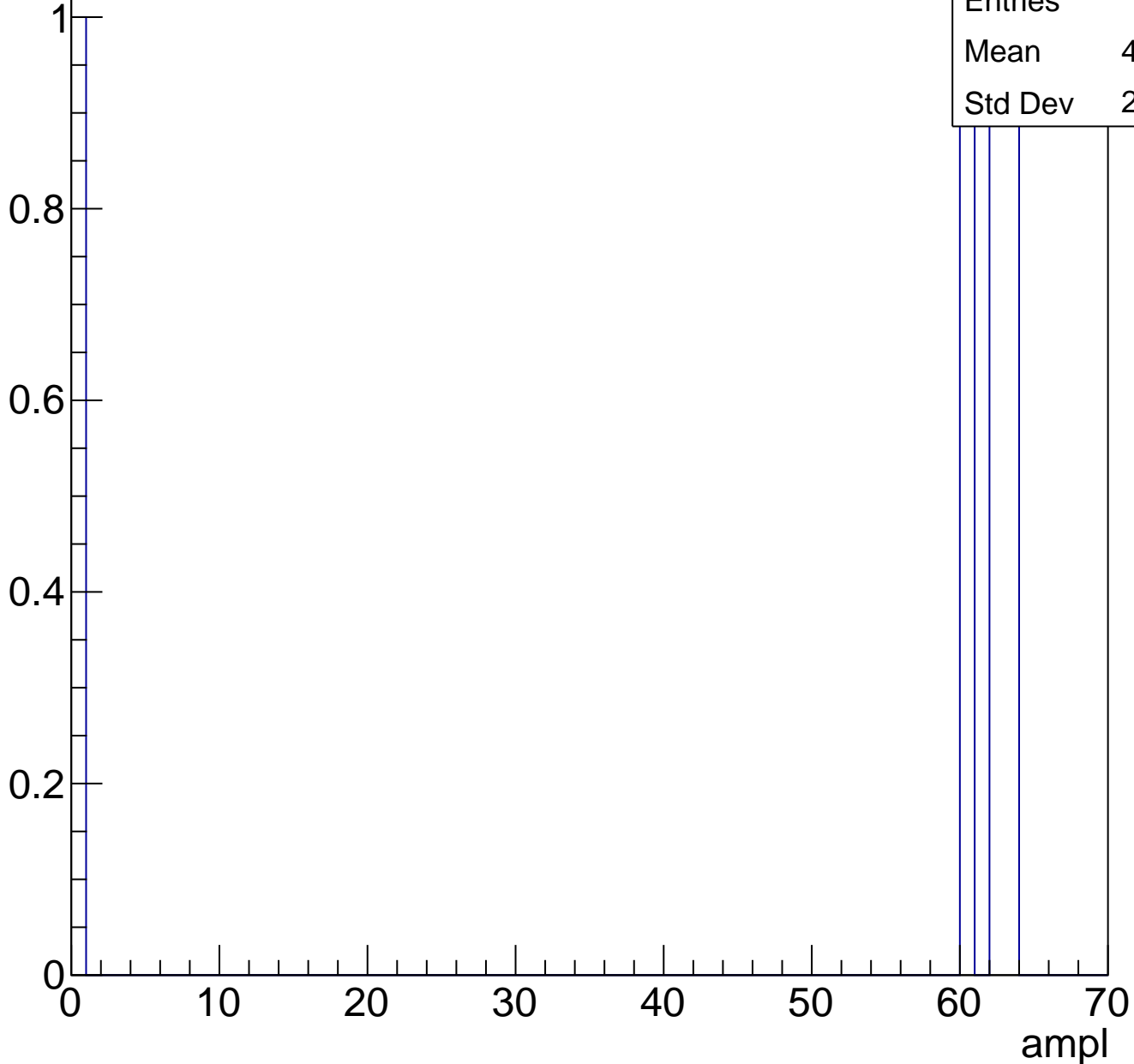
60

70

B0L101S, U18-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



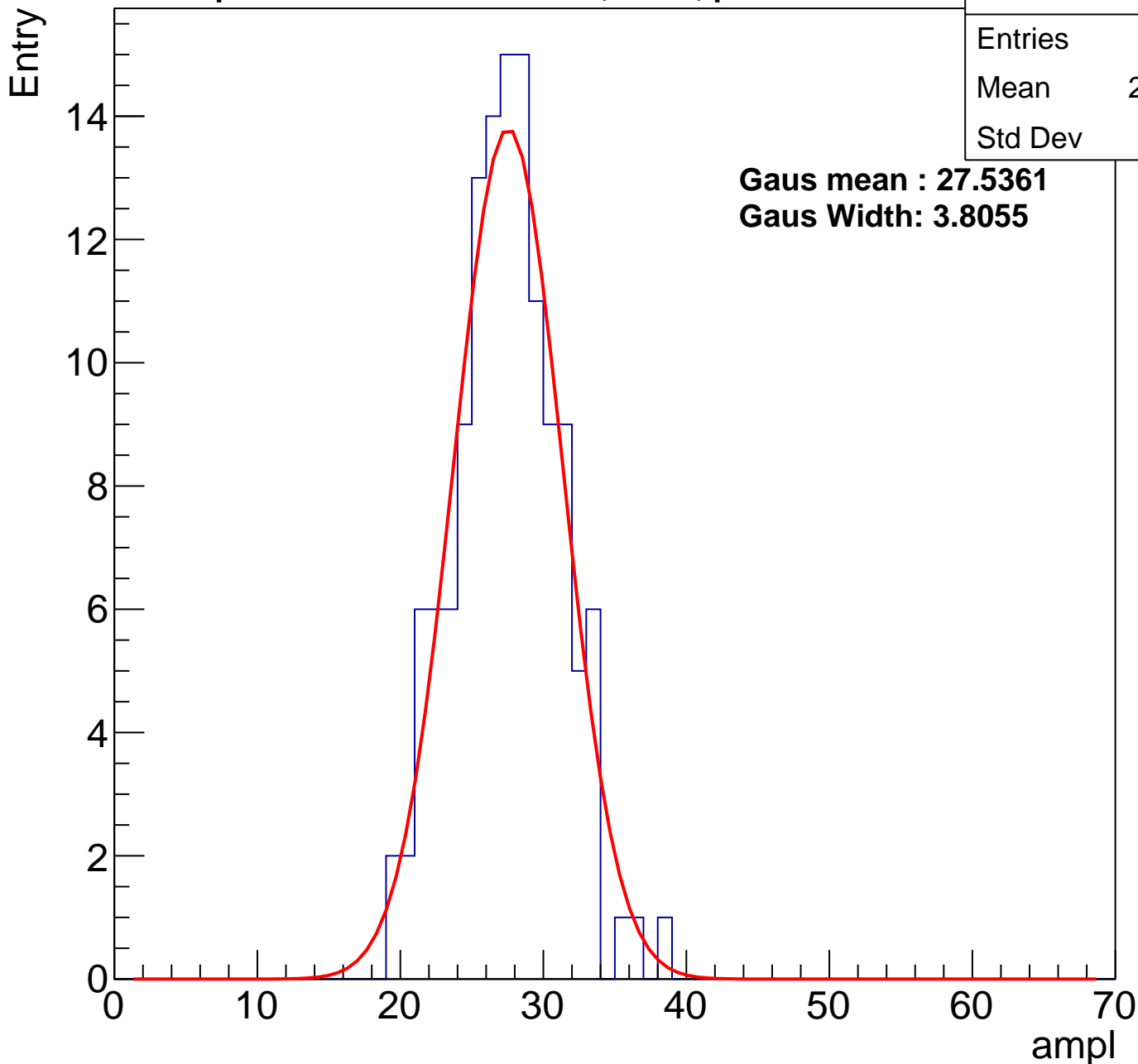
B0L101S, U18-ch74, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	27.02
Std Dev	3.63

Gaus mean : 27.5361

Gaus Width: 3.8055

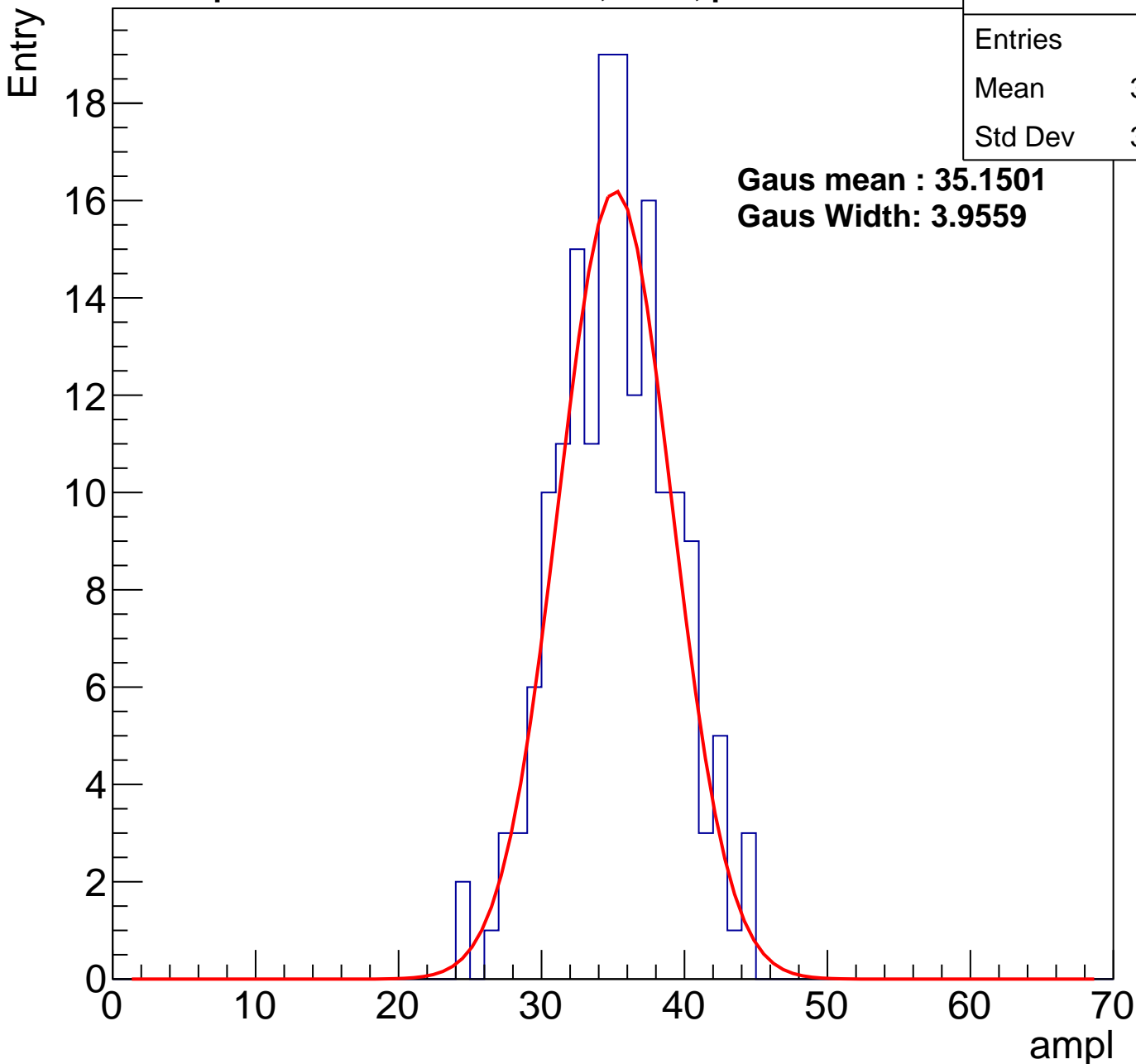


B0L101S, U18-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	34.73
Std Dev	3.998

Gaus mean : 35.1501
Gaus Width: 3.9559



B0L101S, U18-ch74, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	40.76
Std Dev	3.584

Gaus mean : 41.5146

Gaus Width: 3.4222

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

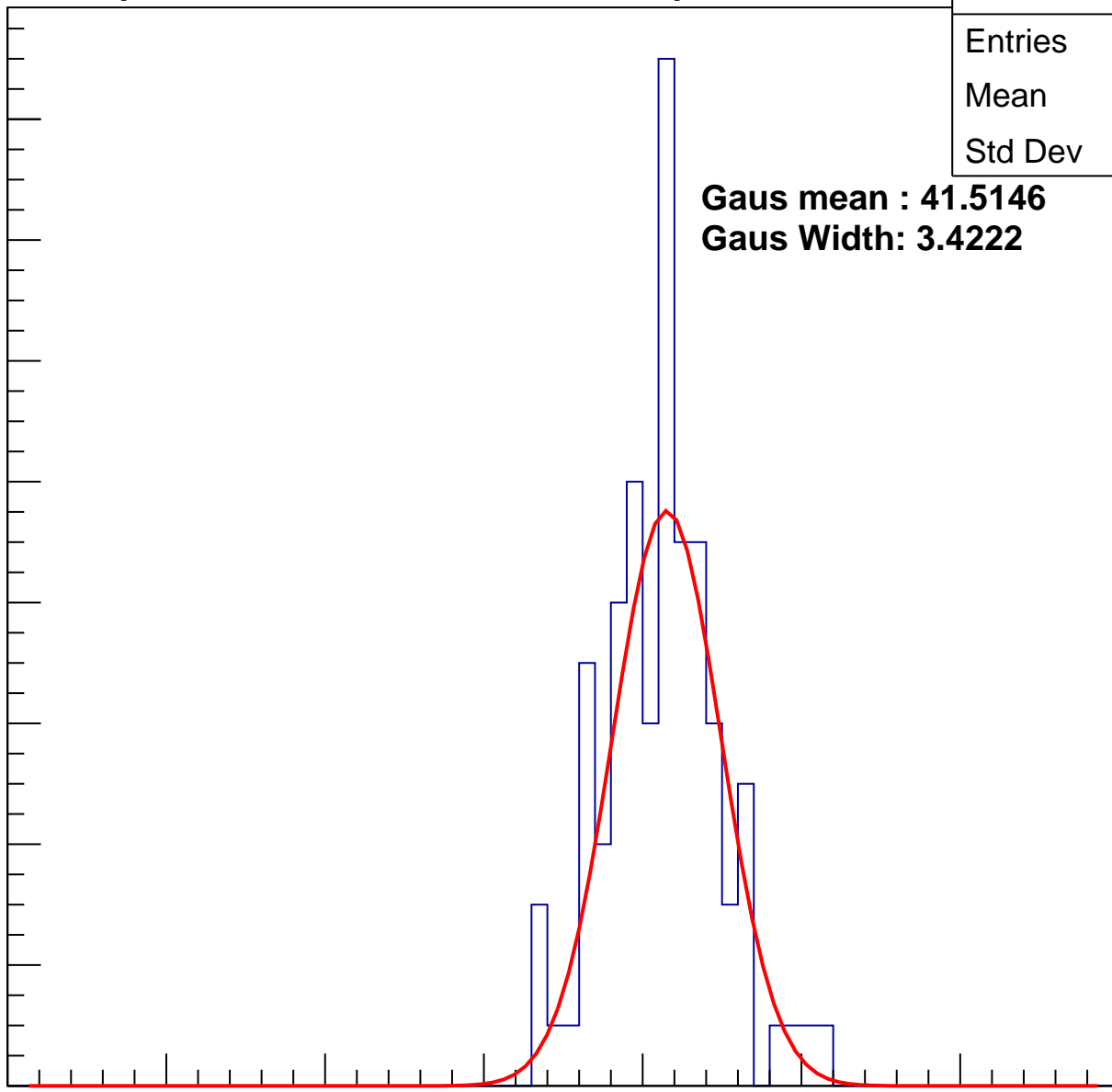
40

50

60

70

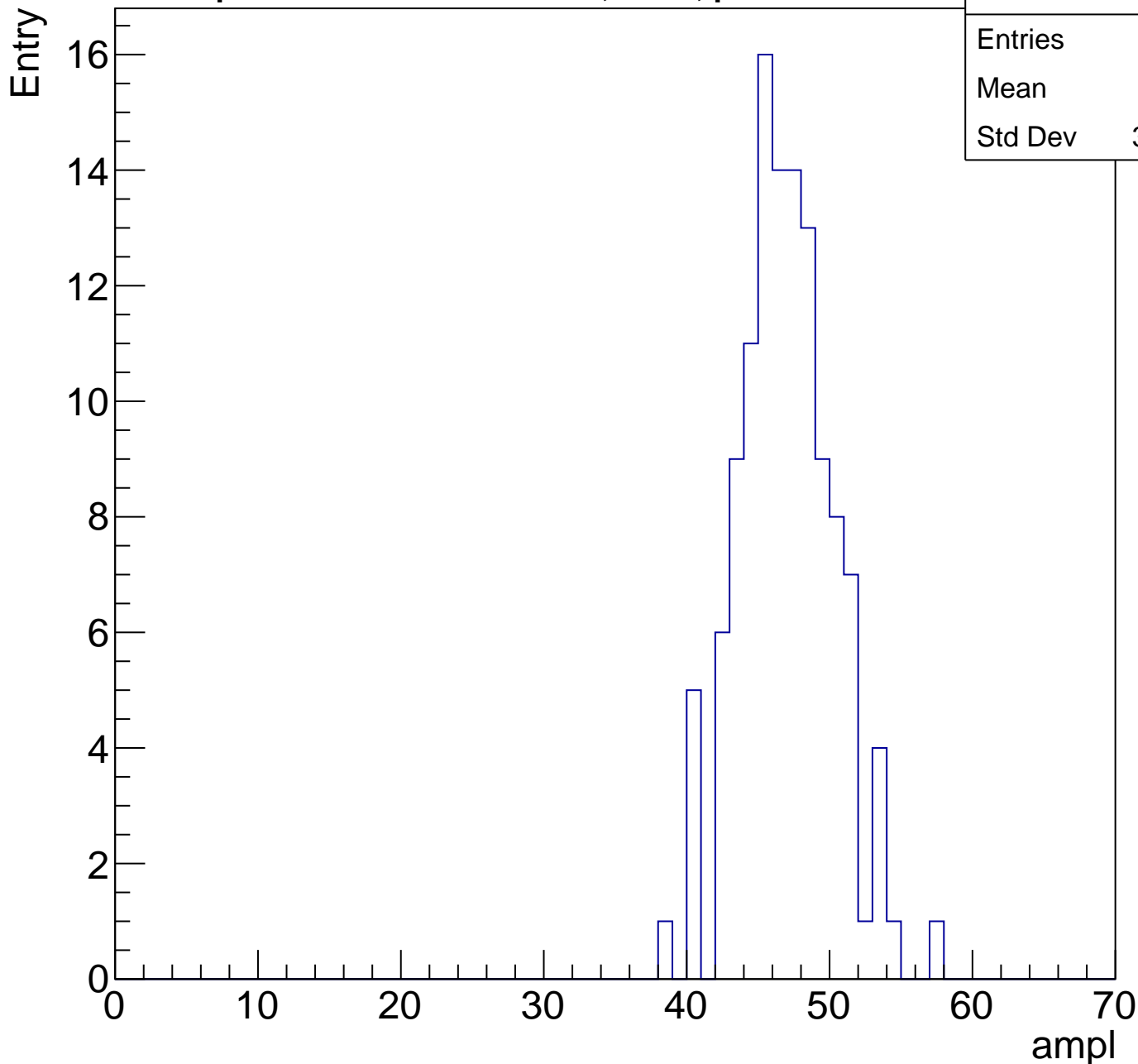
ampl



B0L101S, U18-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

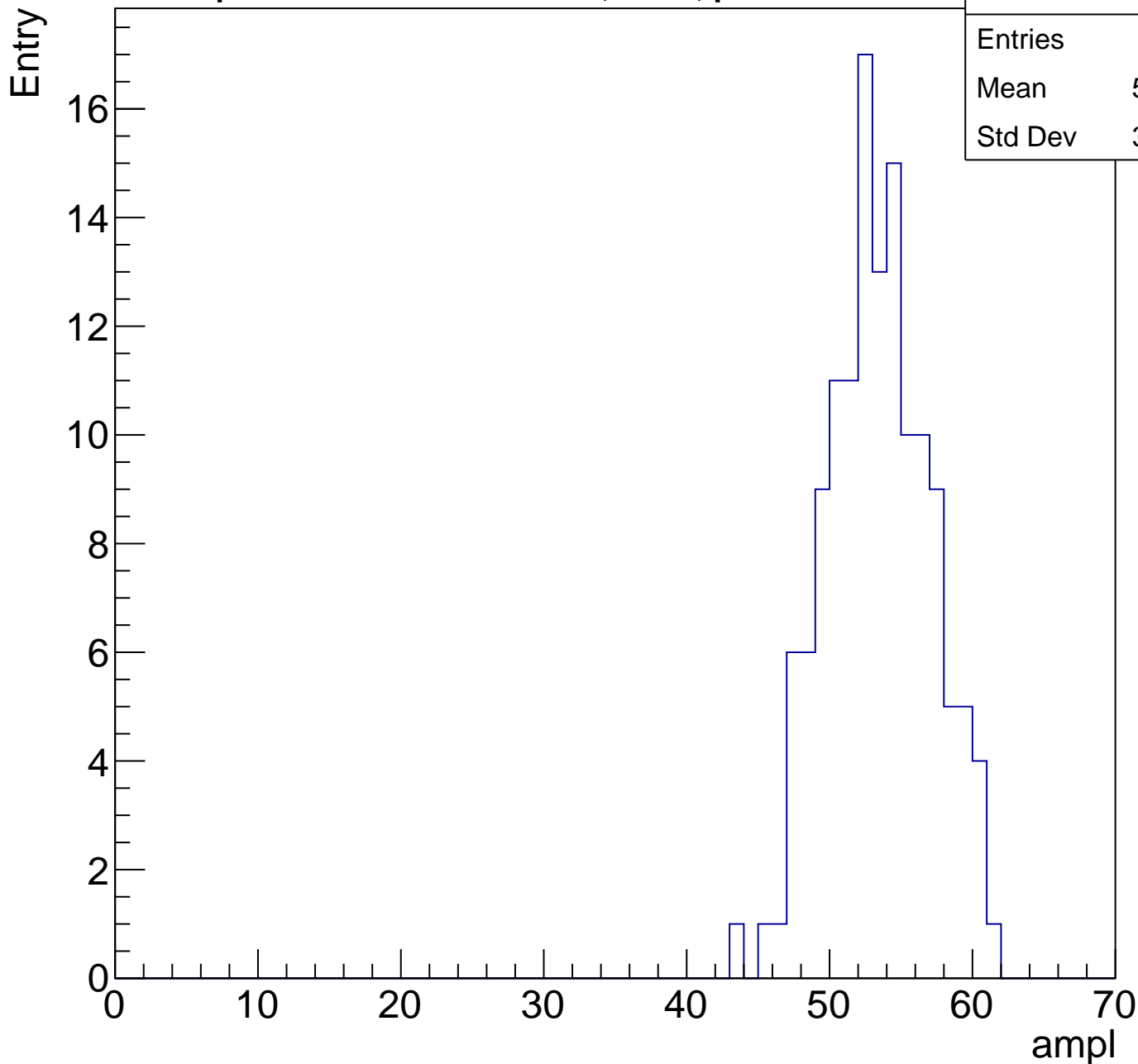
Entries	120
Mean	46.5
Std Dev	3.314



B0L101S, U18-ch74, adc4

calib_packv5_042523_0143.root, FC#1, port C1

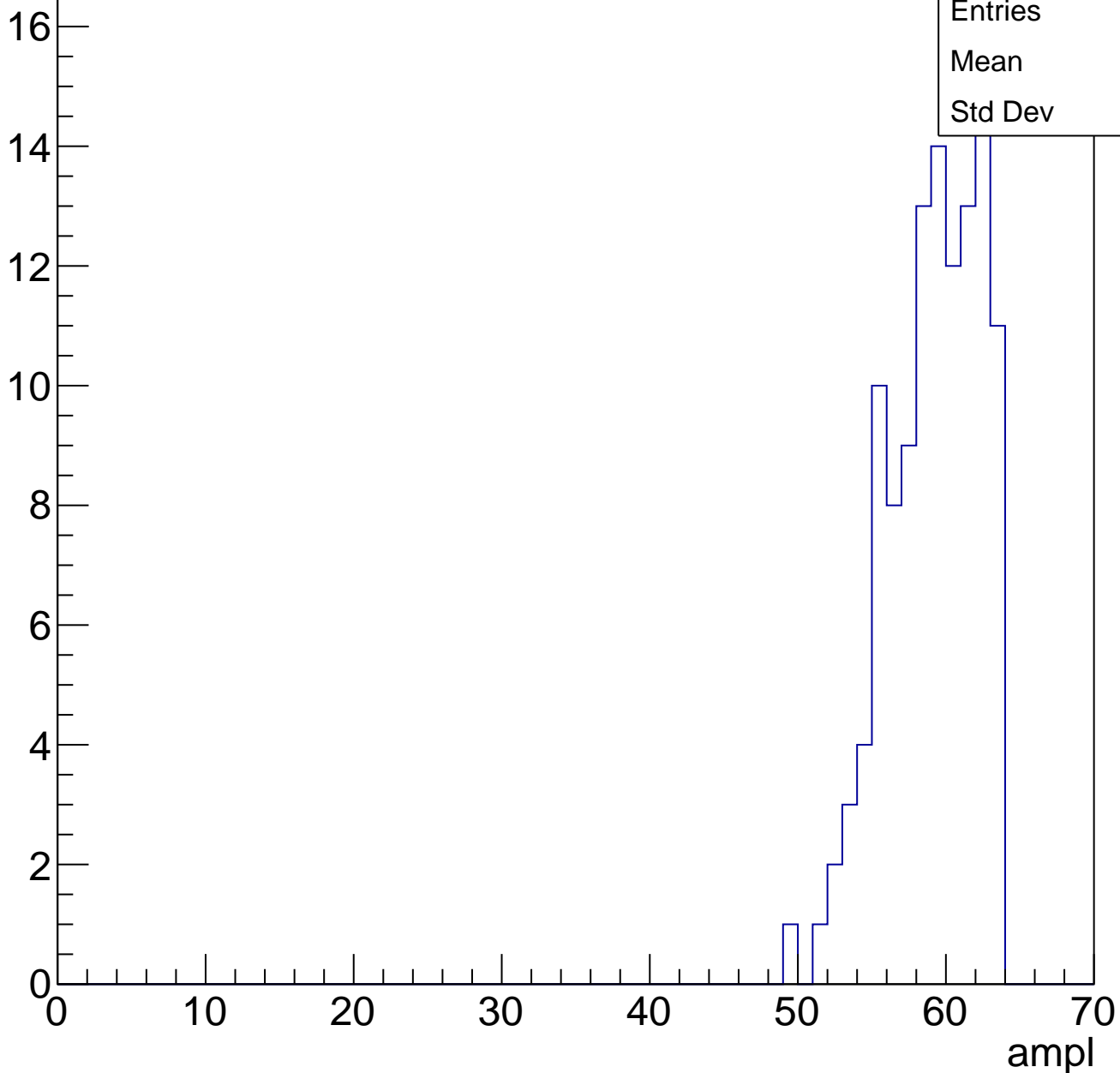
Entries	135
Mean	52.95
Std Dev	3.587



B0L101S, U18-ch74, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

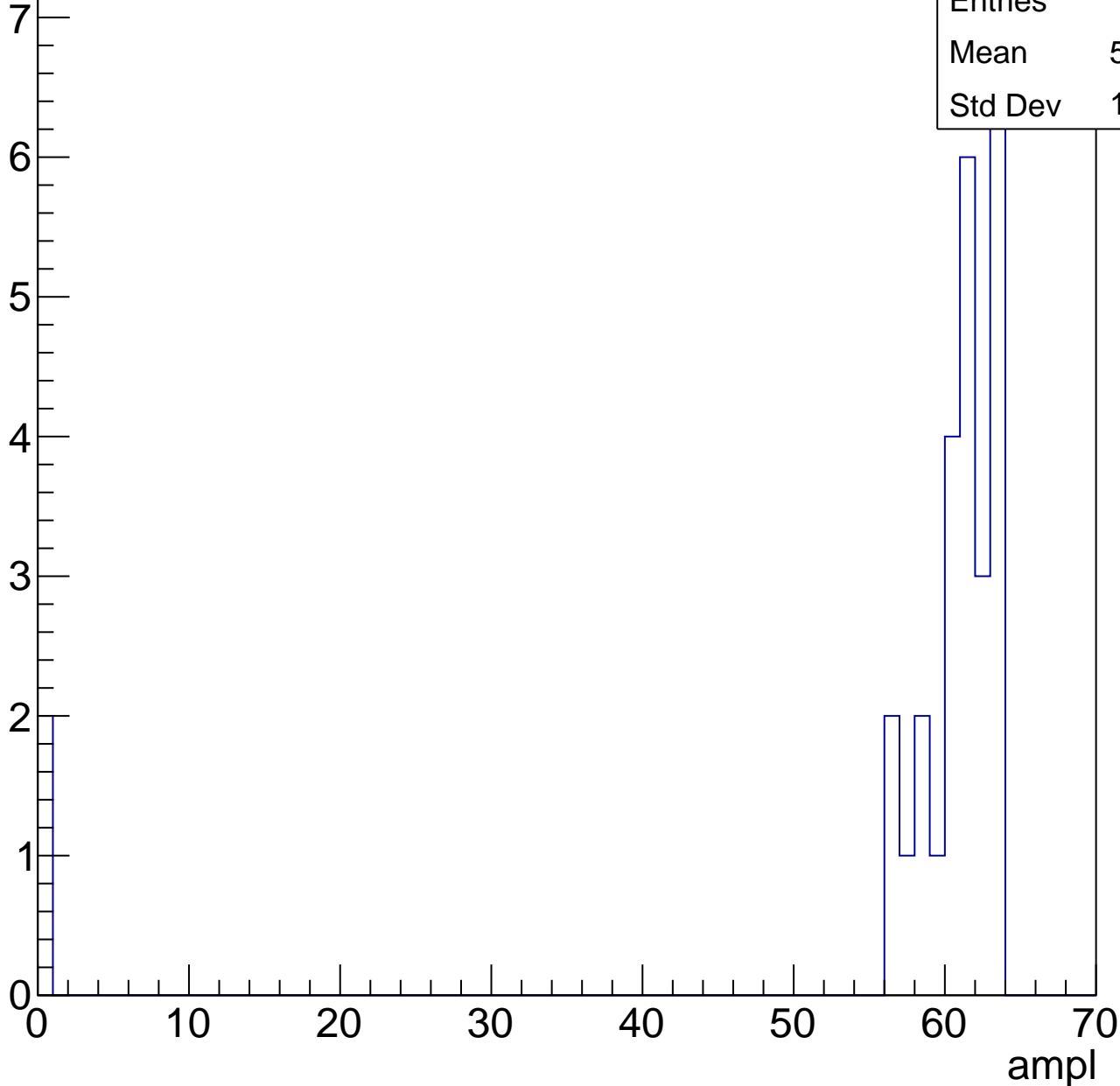


B0L101S, U18-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	28
Mean	56.32
Std Dev	15.76



B0L101S, U18-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch75, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	28.74
Std Dev	5.057

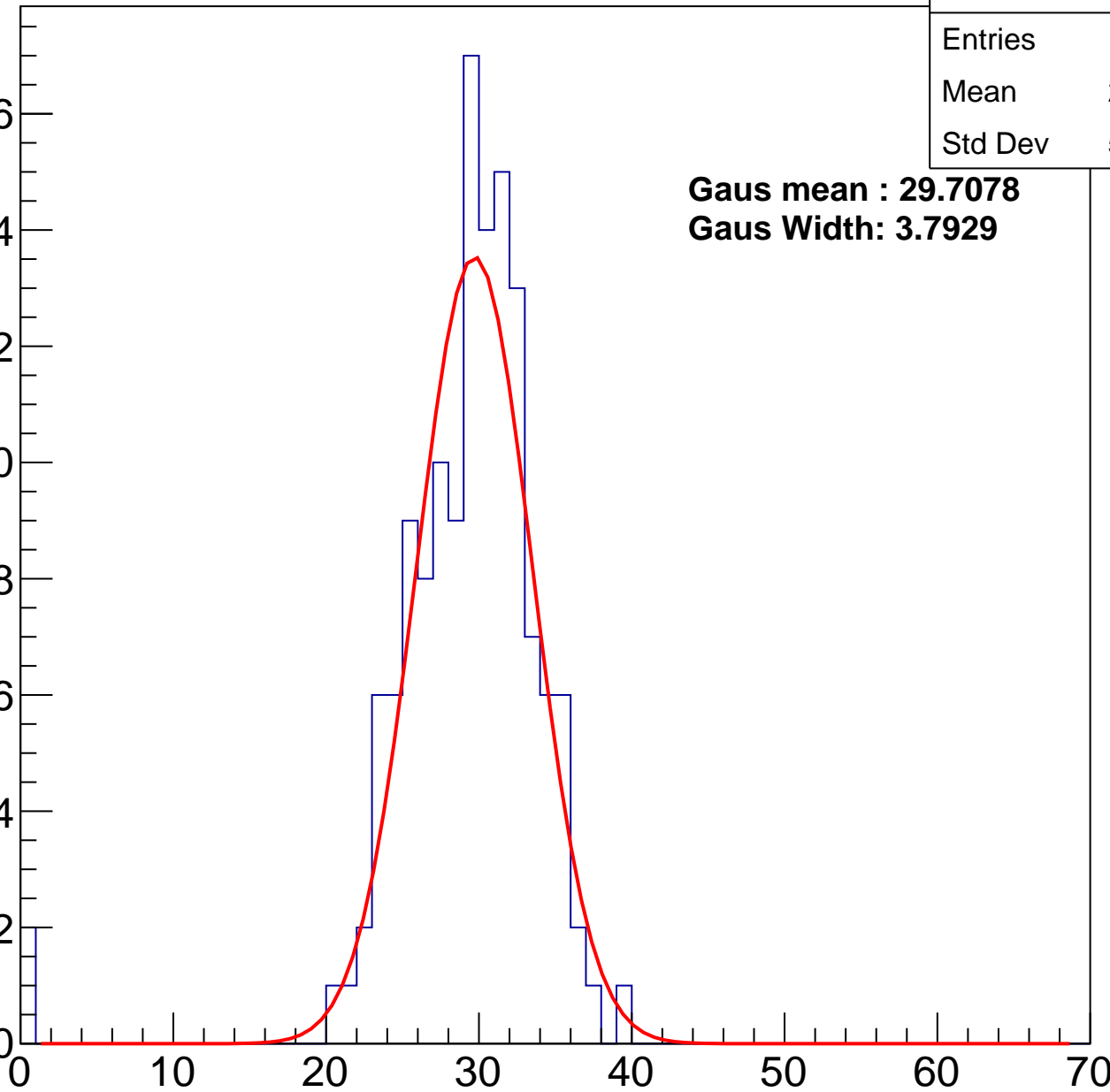
Gaus mean : 29.7078

Gaus Width: 3.7929

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch75, adc1

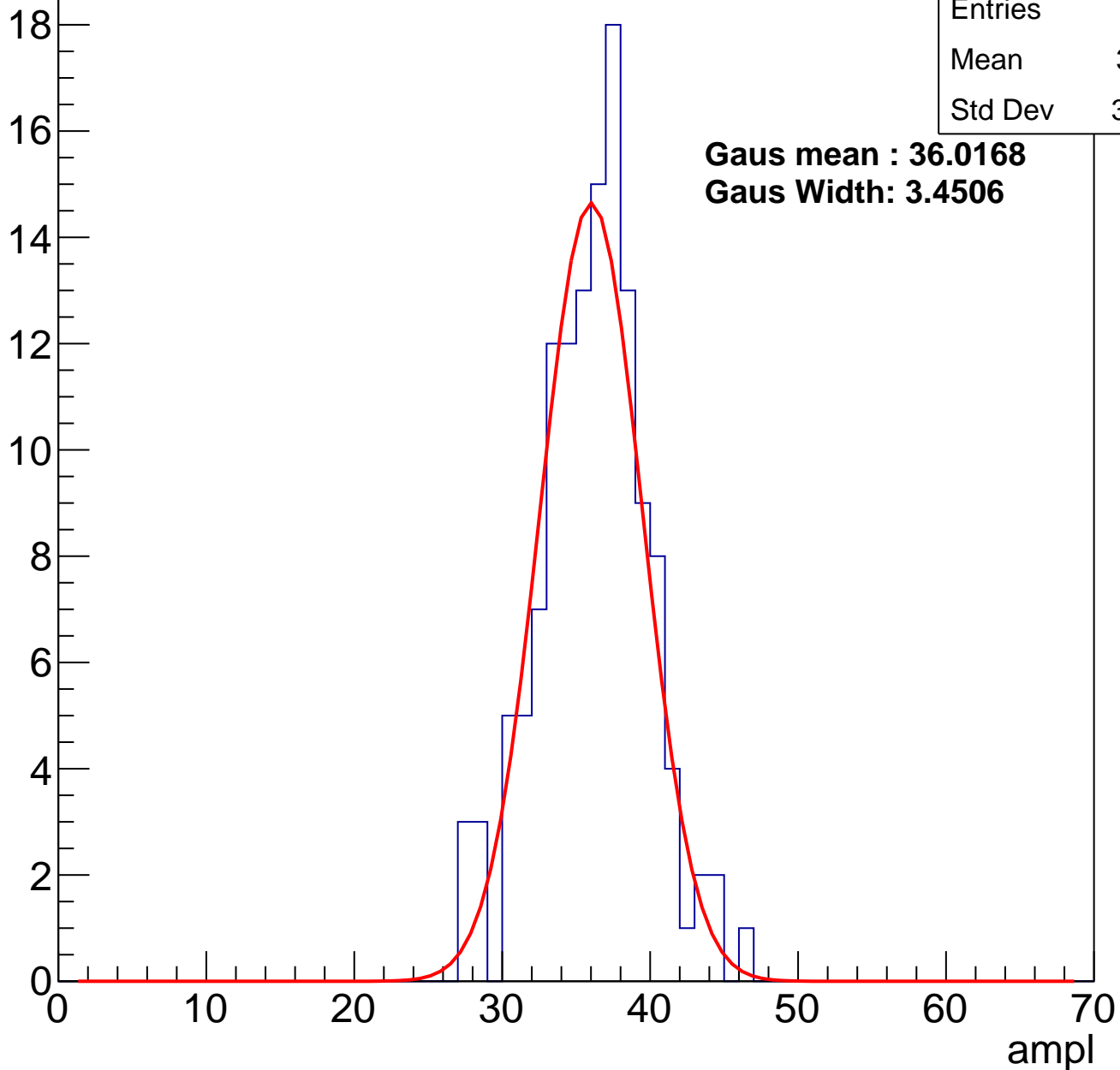
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	35.71
Std Dev	3.629

Gaus mean : 36.0168

Gaus Width: 3.4506

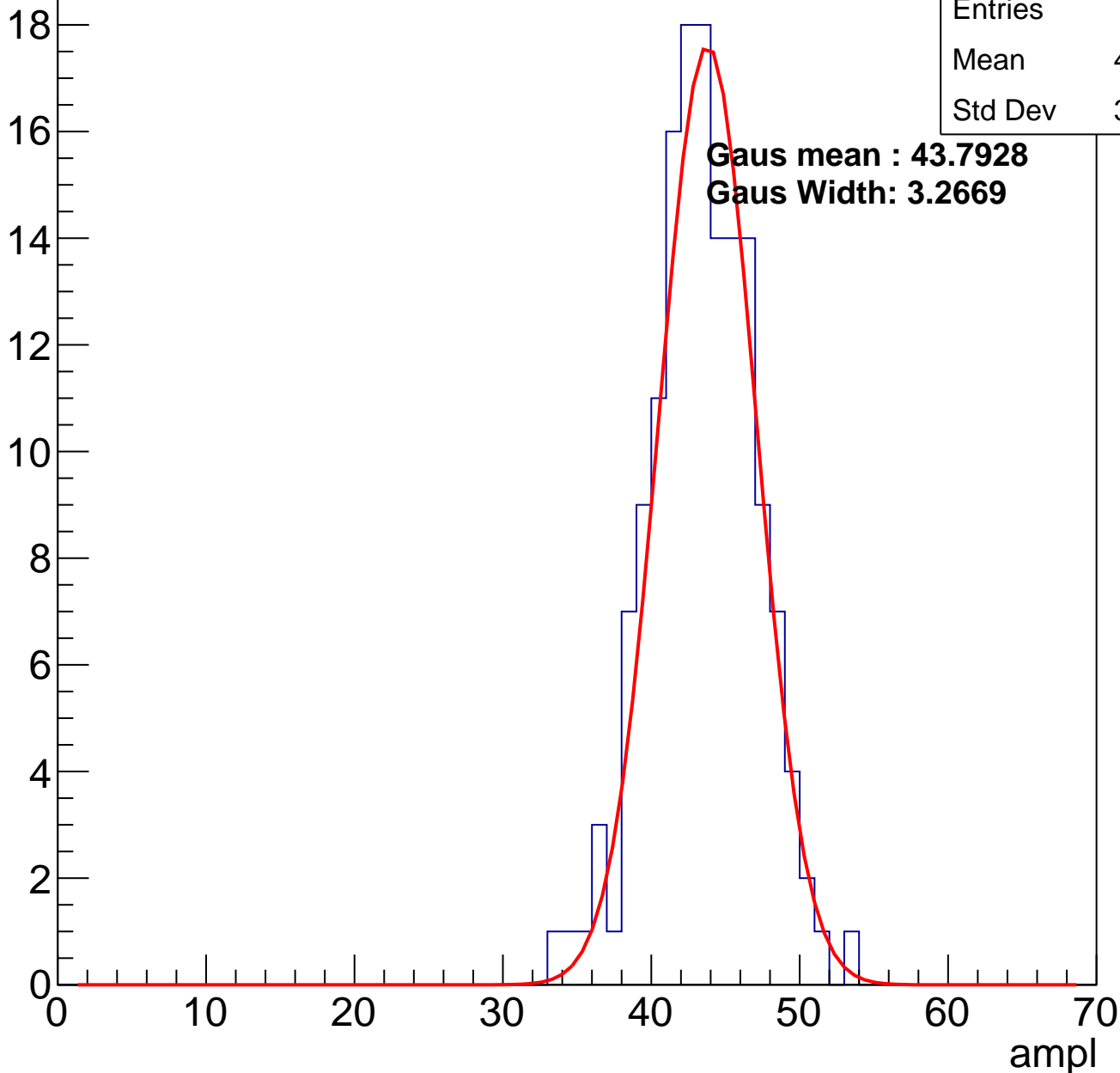
Entry



B0L101S, U18-ch75, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	152
Mean	43.02
Std Dev	3.488

B0L101S, U18-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

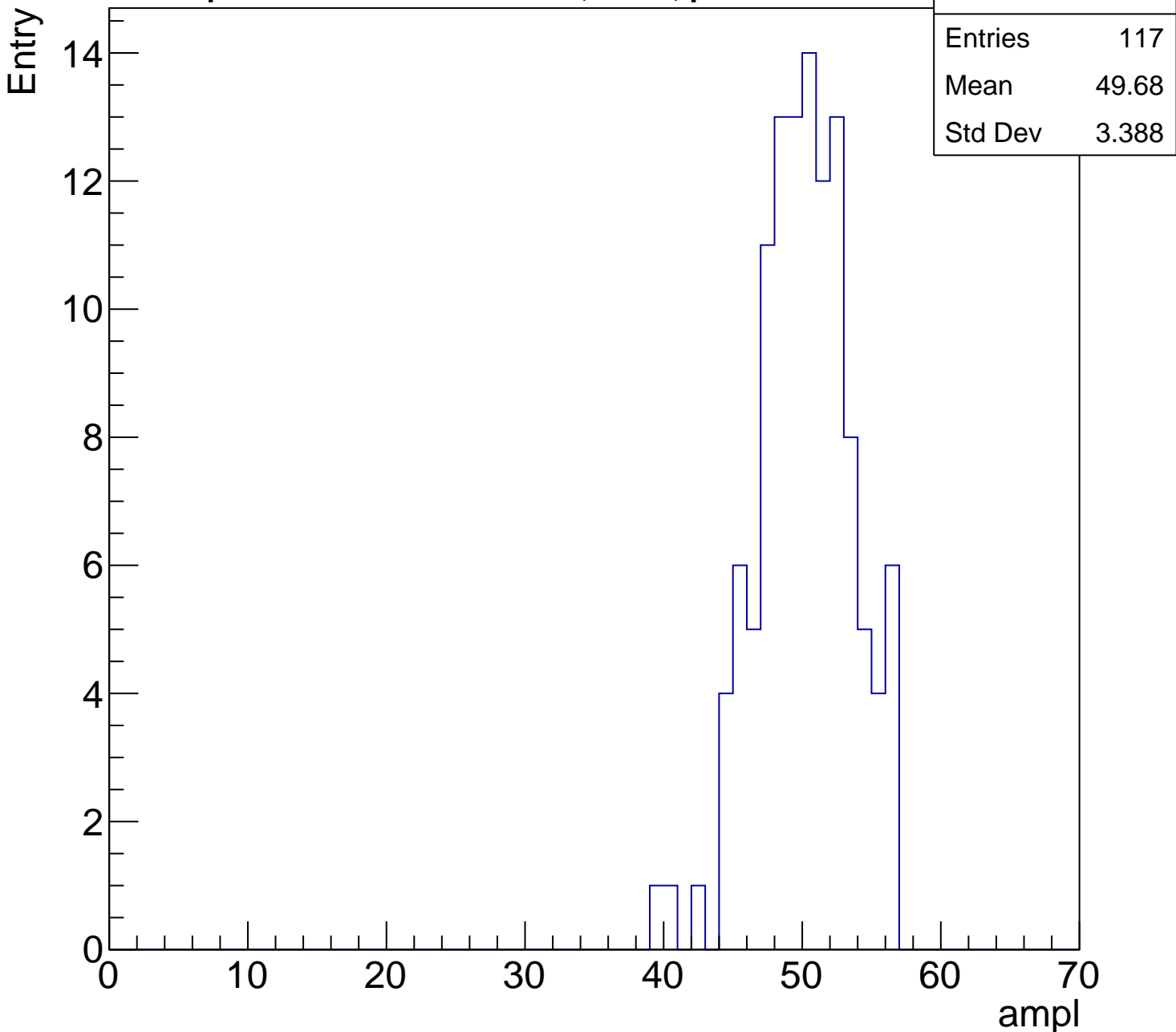
Entries	117
Mean	49.68
Std Dev	3.388

Entry

14
12
10
8
6
4
2
0

ampl

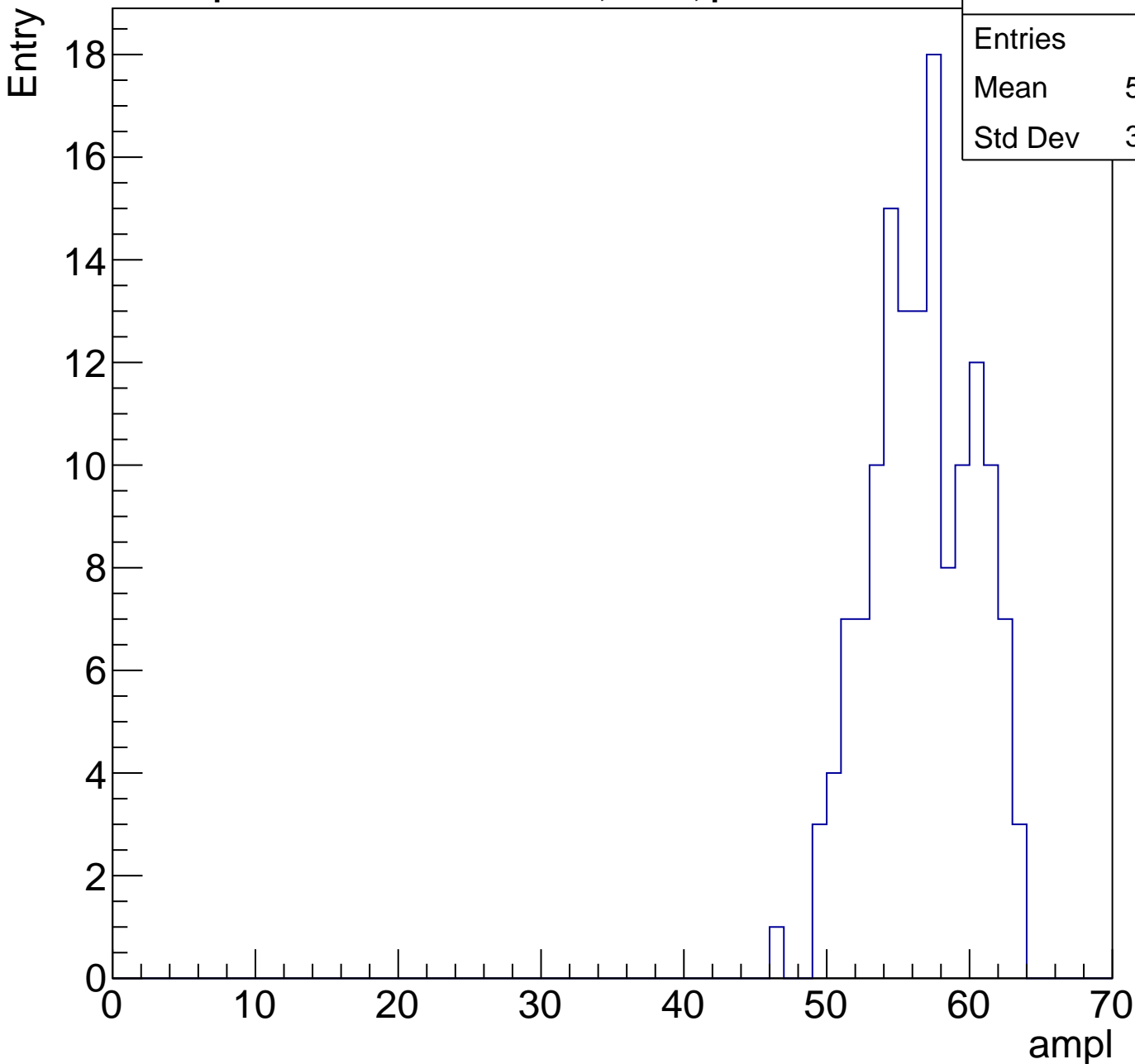
0 10 20 30 40 50 60 70



B0L101S, U18-ch75, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	56.24
Std Dev	3.587



B0L101S, U18-ch75, adc5

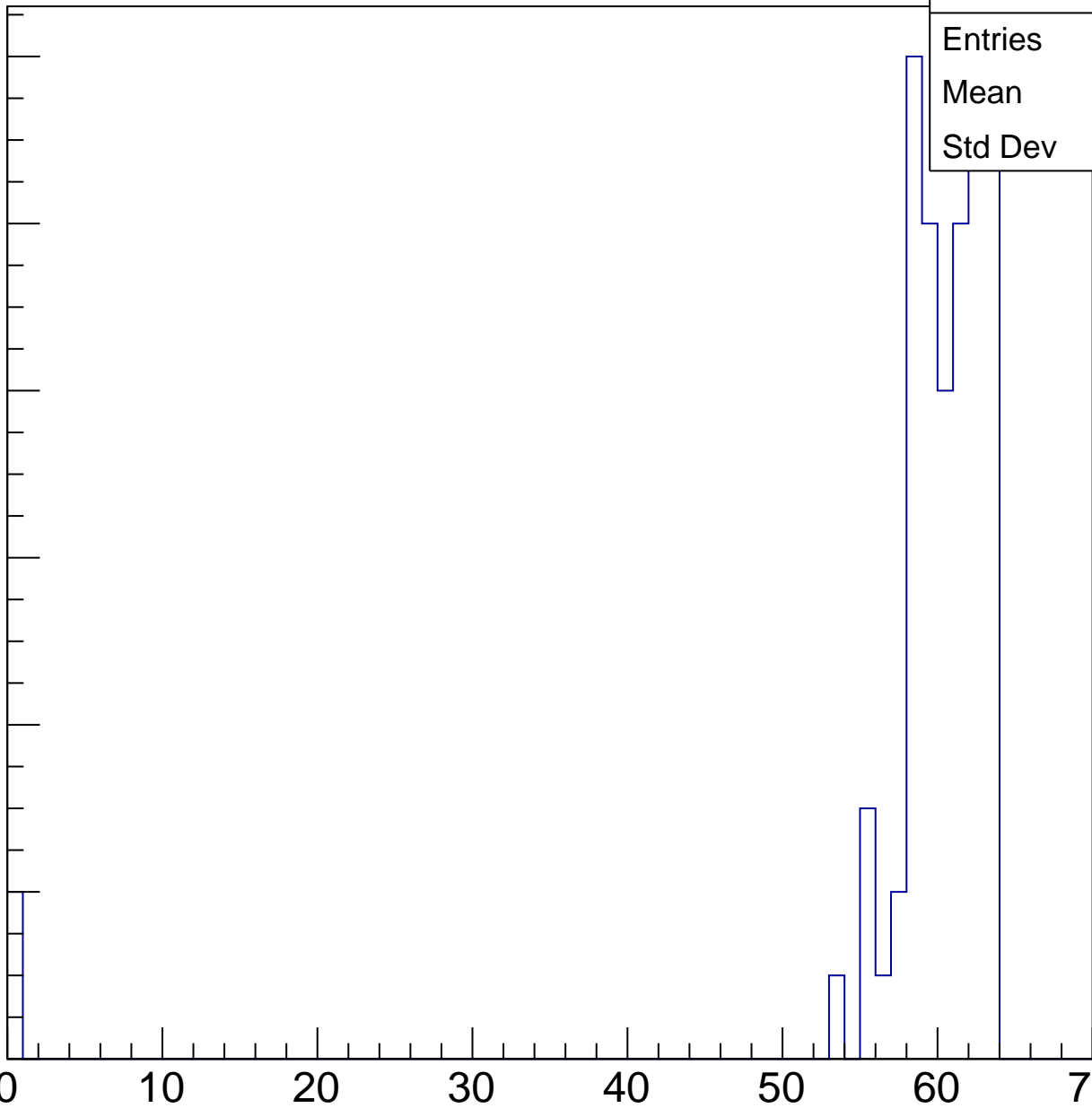
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	73
Mean	58.41
Std Dev	10.07

ampl

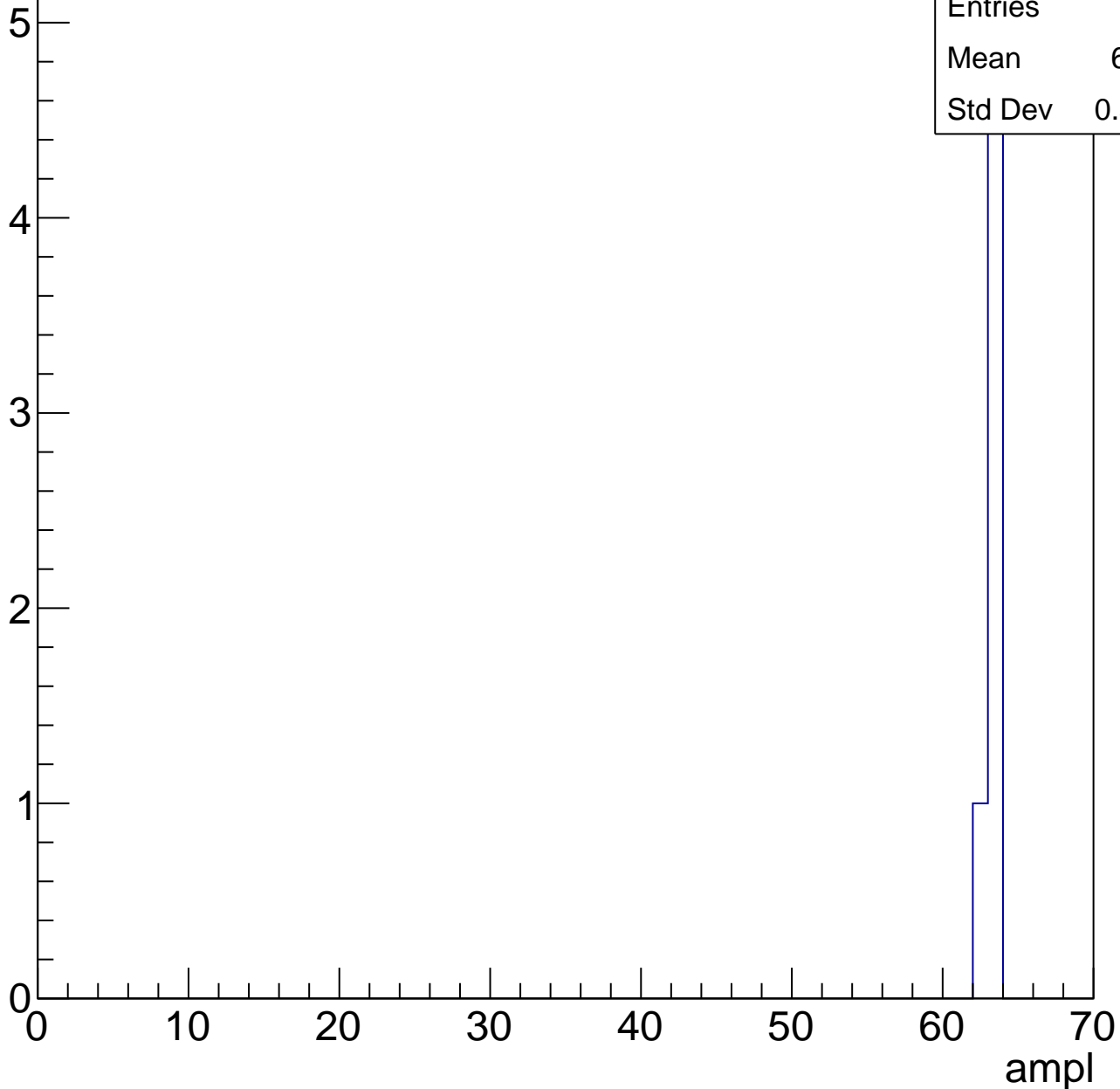


B0L101S, U18-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B0L101S, U18-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U18-ch76, adc0

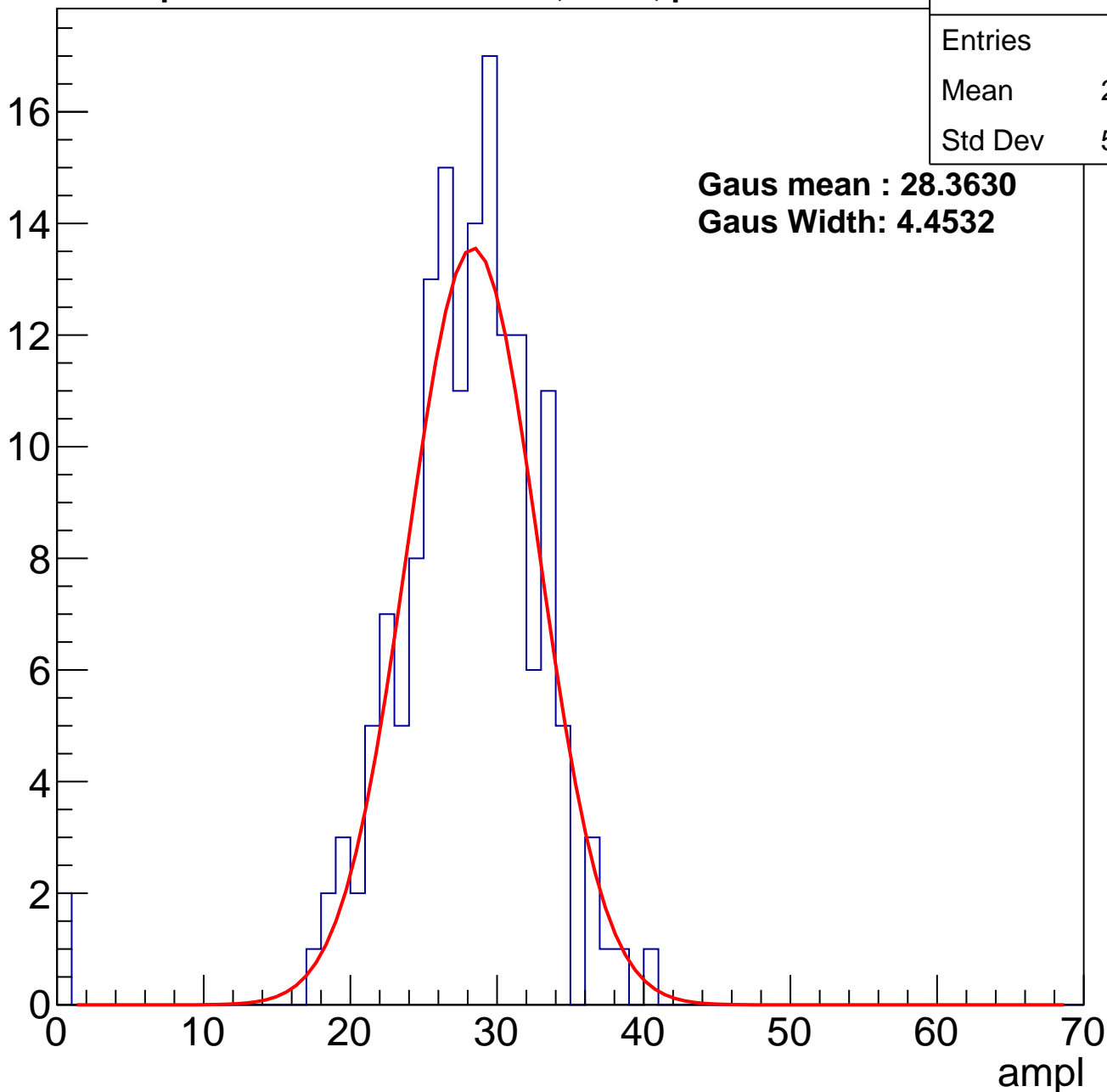
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	27.34
Std Dev	5.277

Gaus mean : 28.3630

Gaus Width: 4.4532

Entry



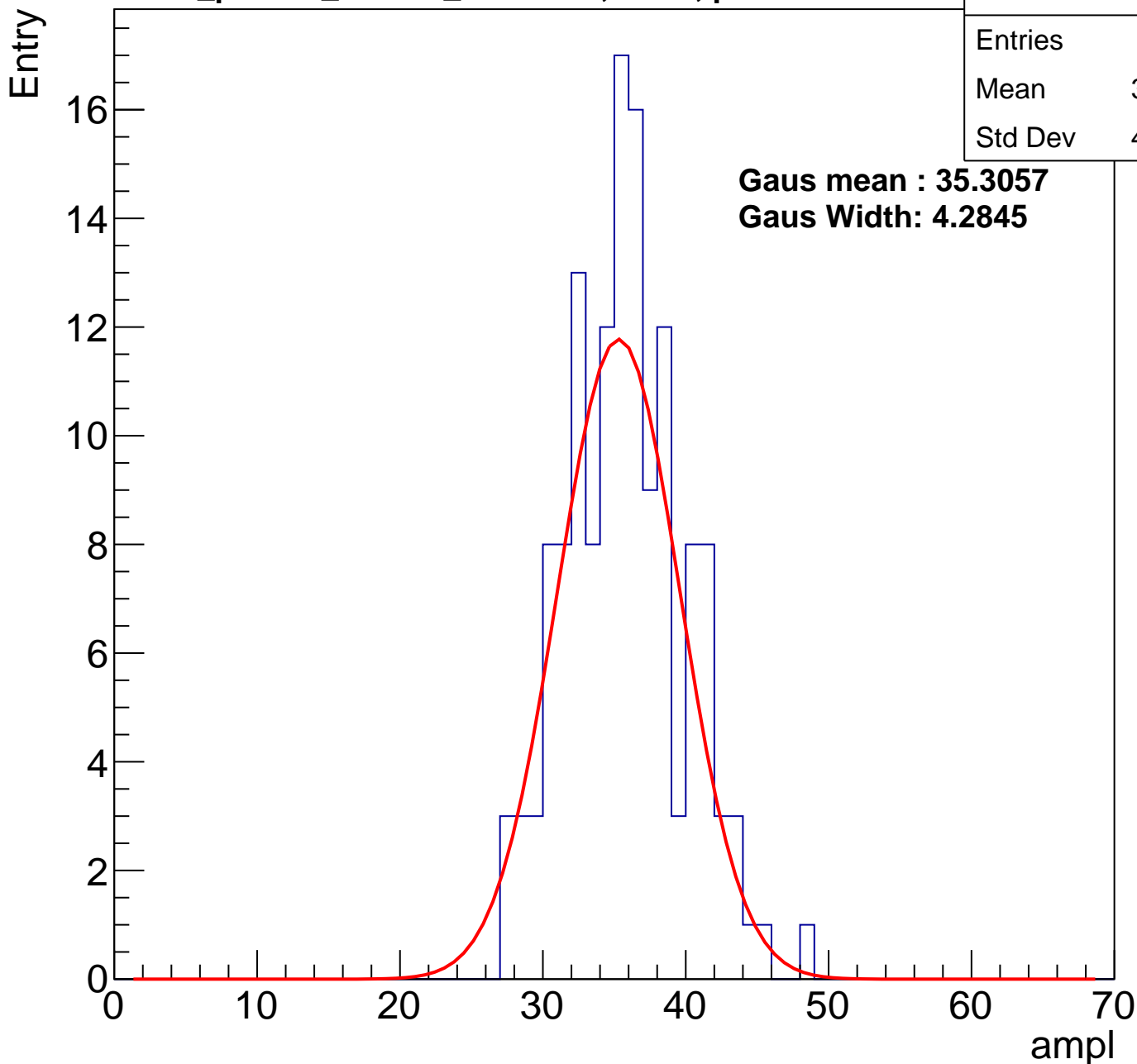
B0L101S, U18-ch76, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	35.32
Std Dev	4.059

Gaus mean : 35.3057

Gaus Width: 4.2845



B0L101S, U18-ch76, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

118

Mean

41.44

Std Dev

3.711

Gaus mean : 42.2312

Gaus Width: 3.3515

0

10

20

30

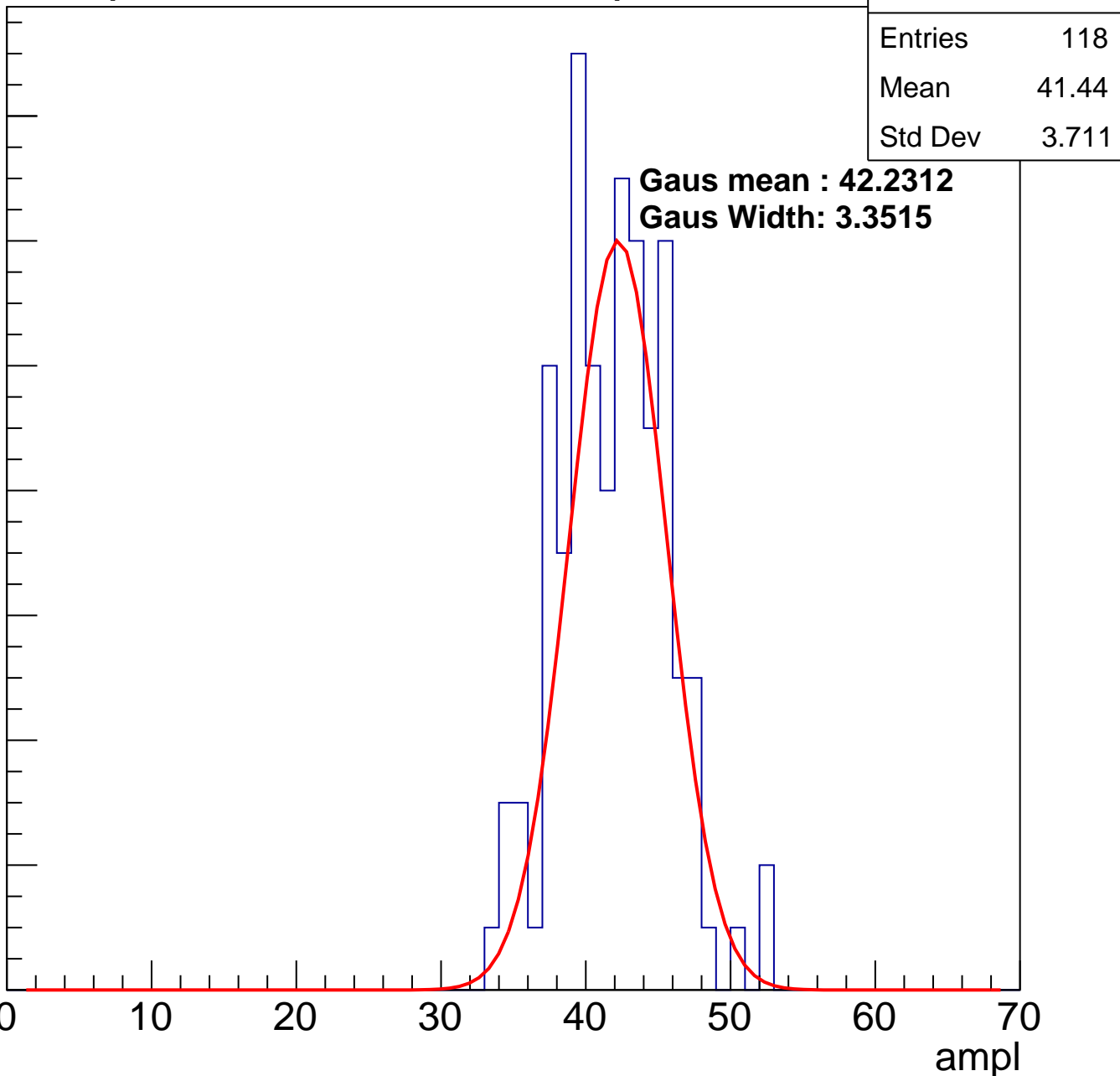
40

50

60

70

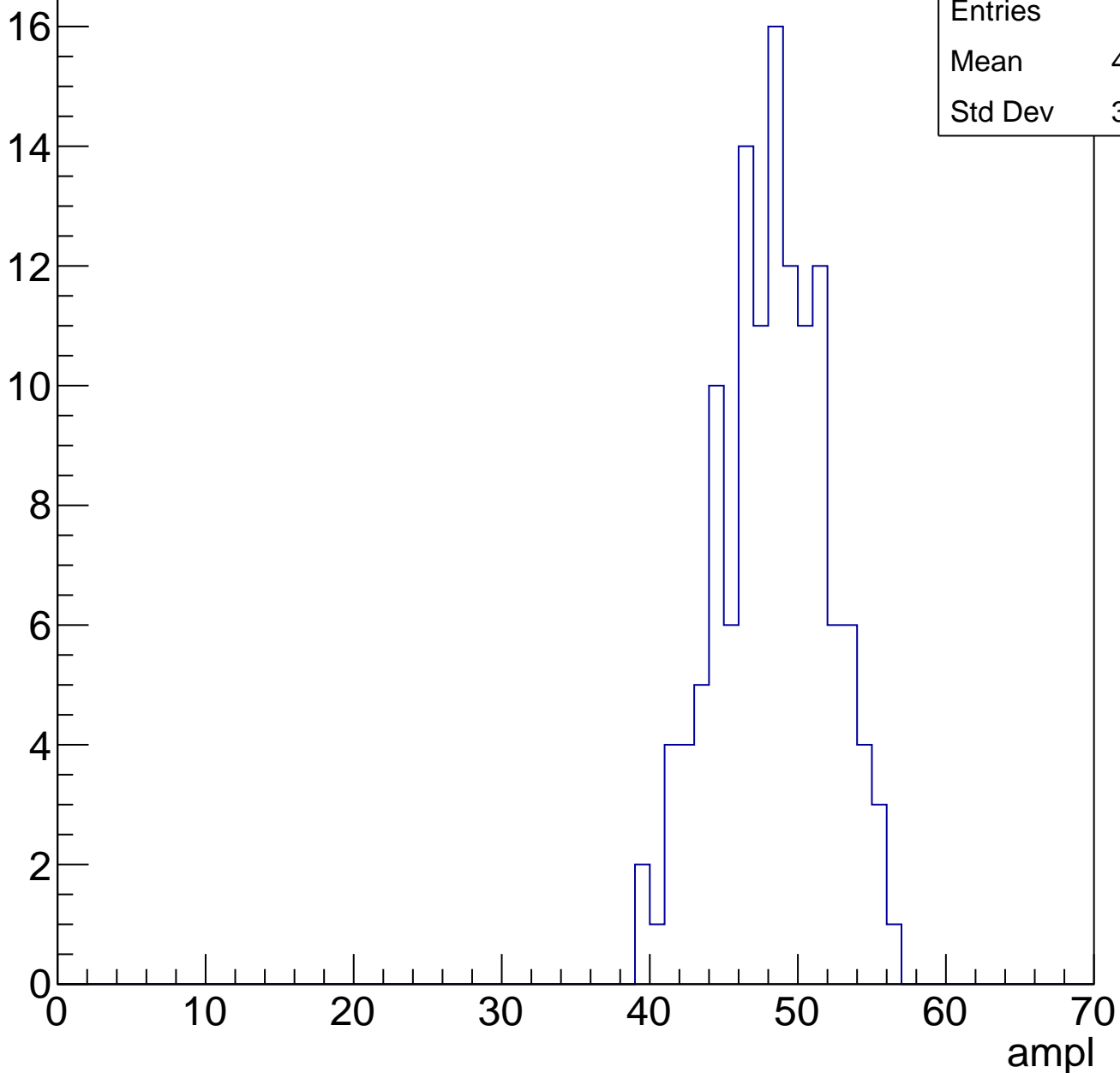
ampl



B0L101S, U18-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

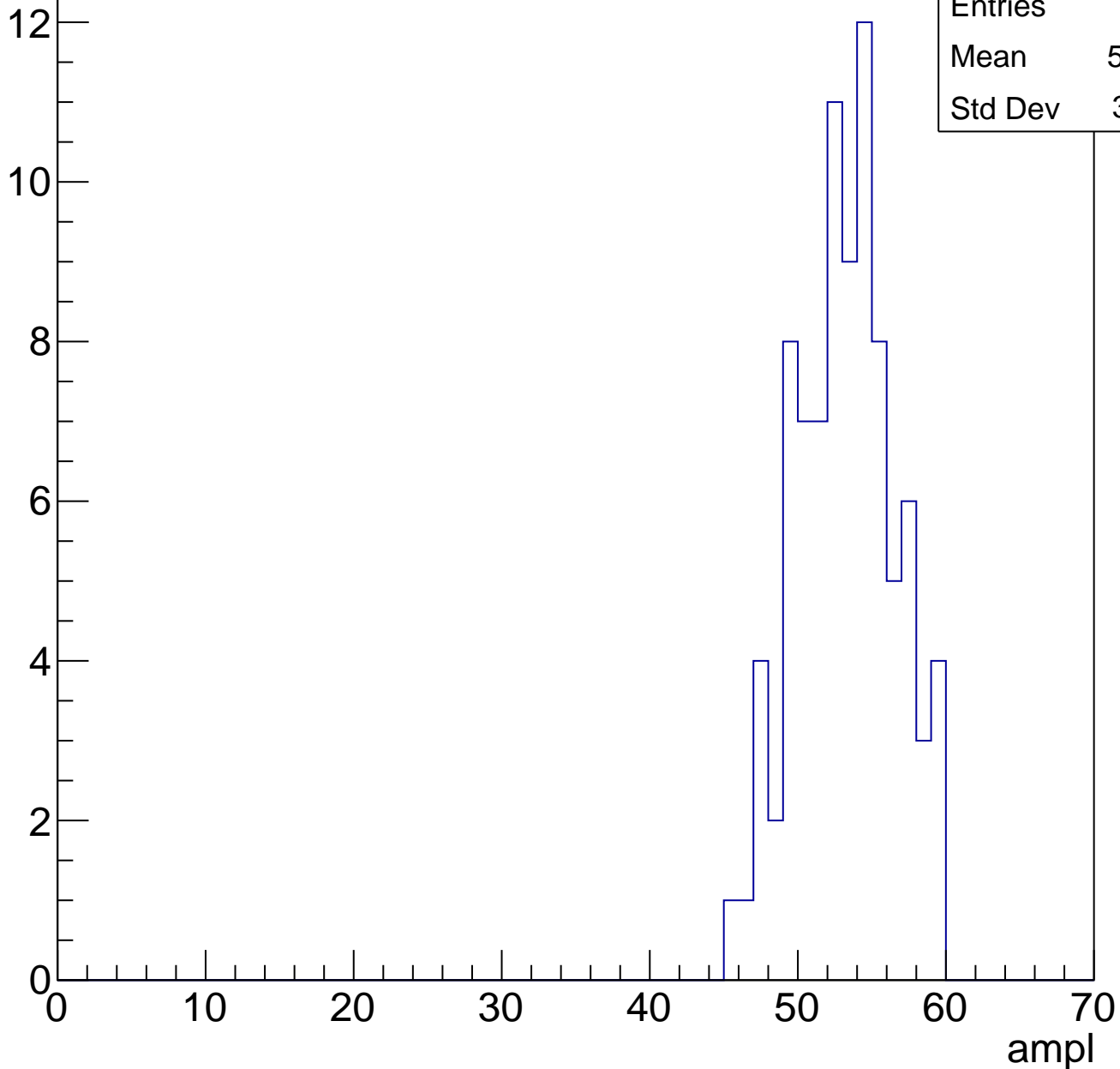
Entry



B0L101S, U18-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch76, adc5

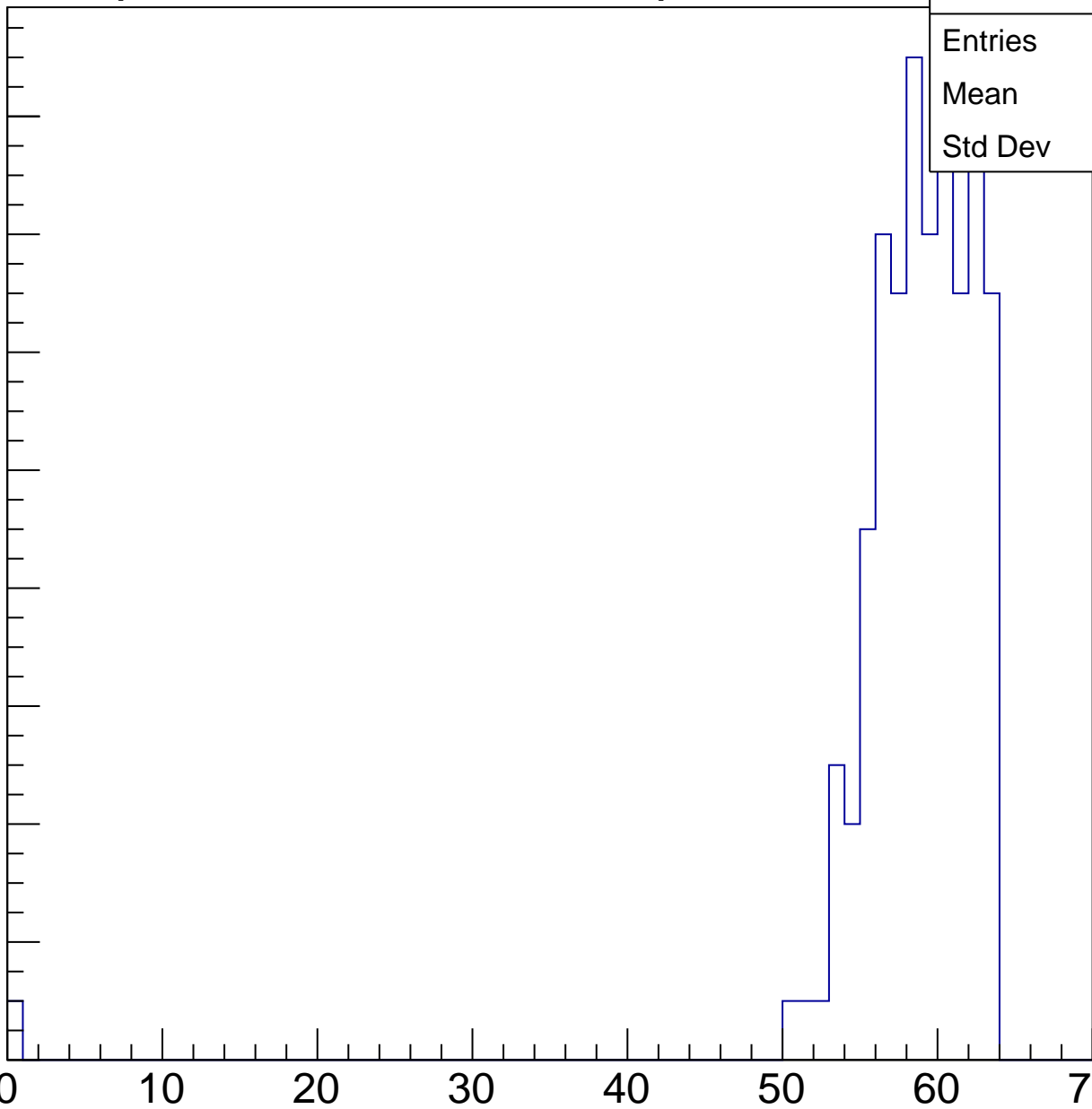
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	138
Mean	58.19
Std Dev	5.788

ampl

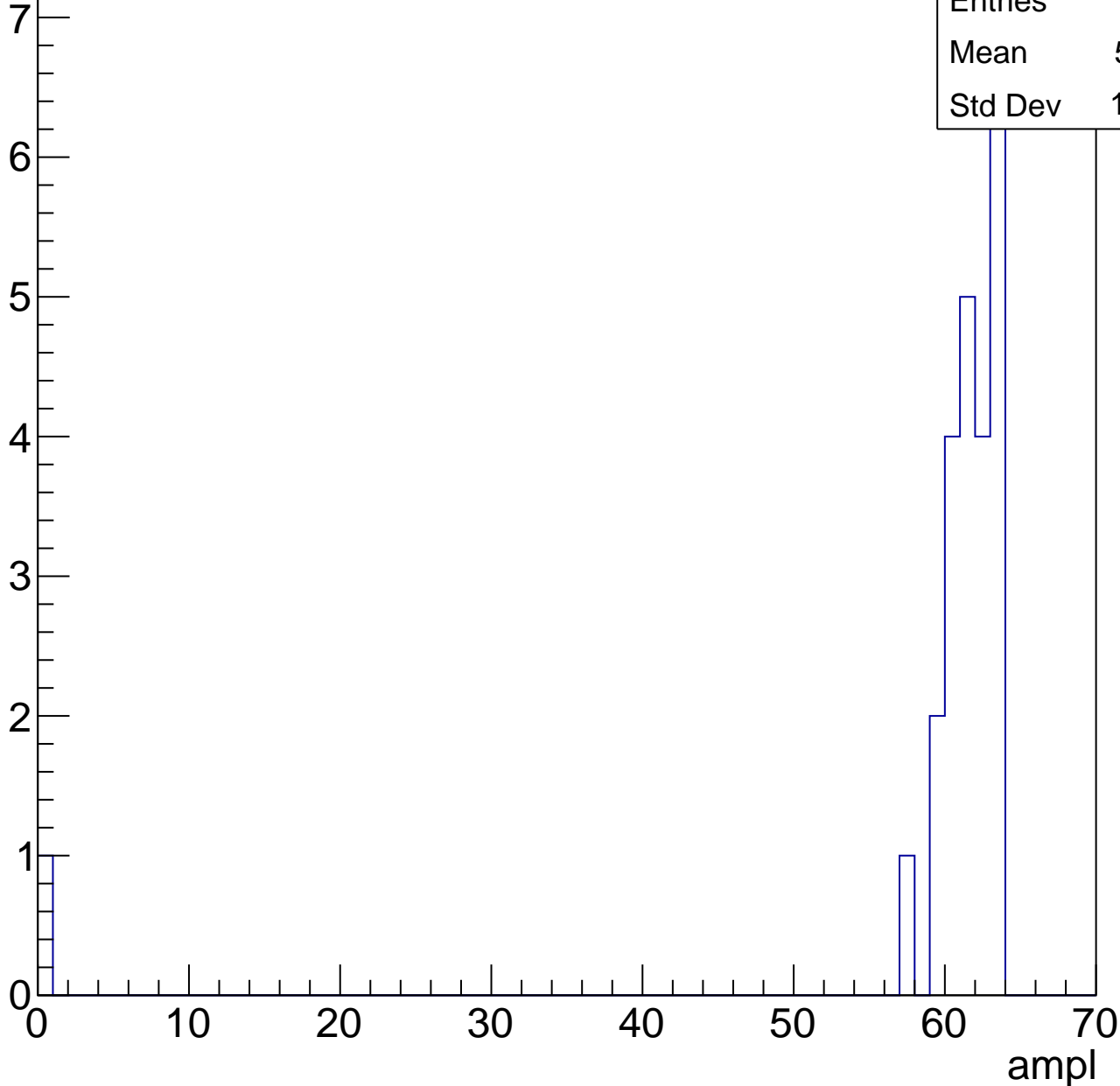


B0L101S, U18-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

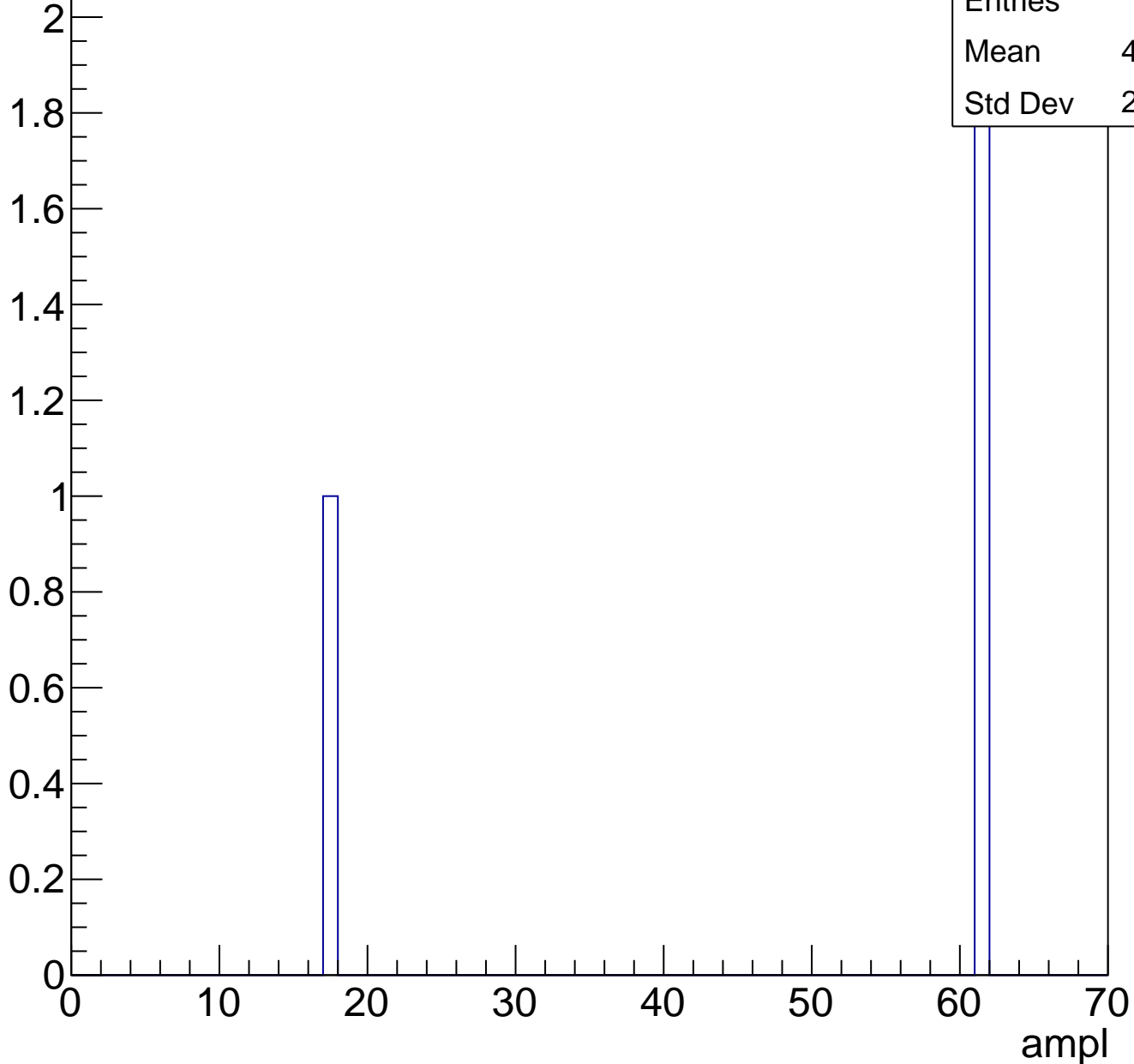
Entries	24
Mean	58.71
Std Dev	12.34



B0L101S, U18-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	46.33
Std Dev	20.74

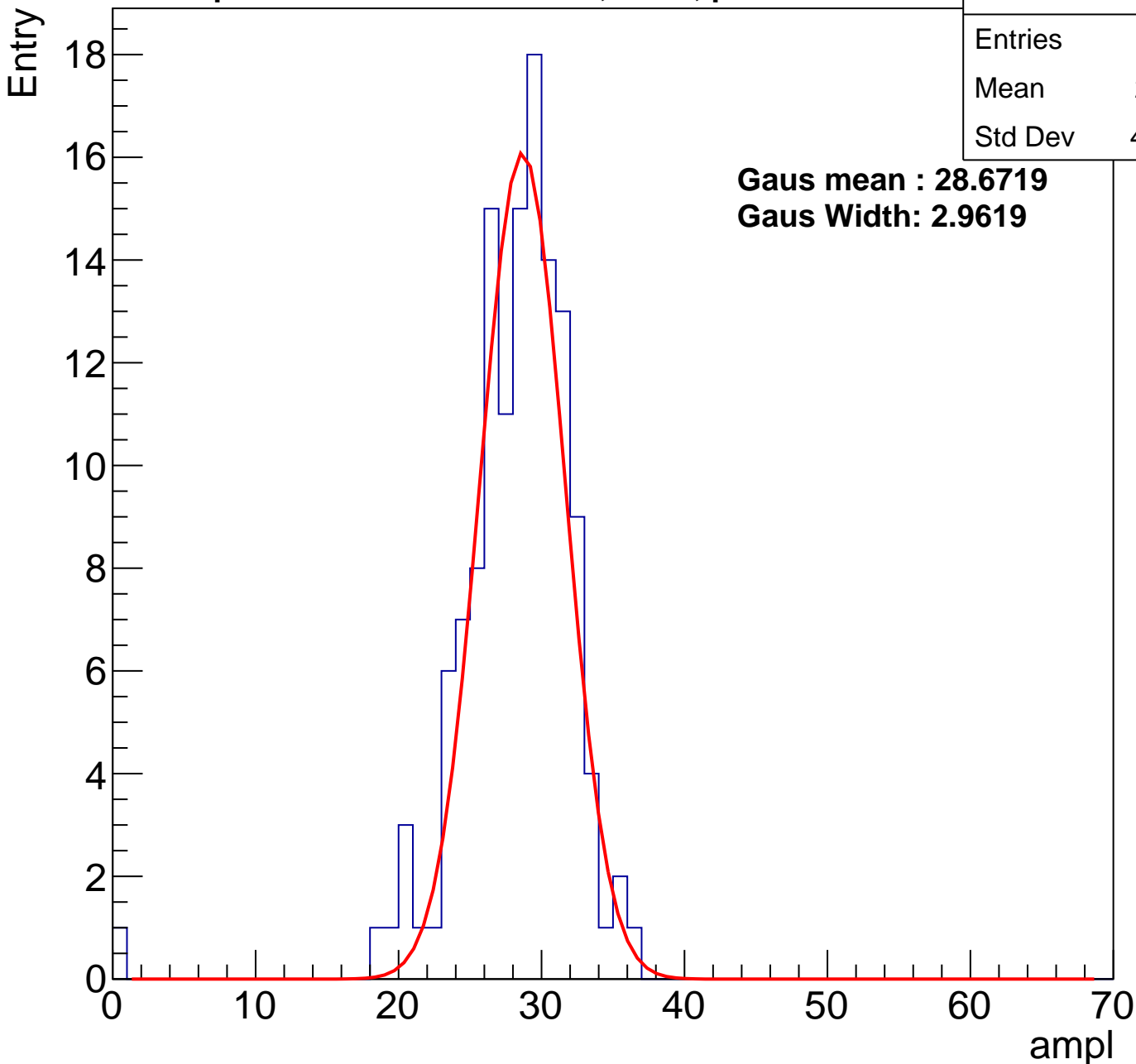
B0L101S, U18-ch77, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	27.71
Std Dev	4.146

Gaus mean : 28.6719

Gaus Width: 2.9619



B0L101S, U18-ch77, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	35.26
Std Dev	3.554

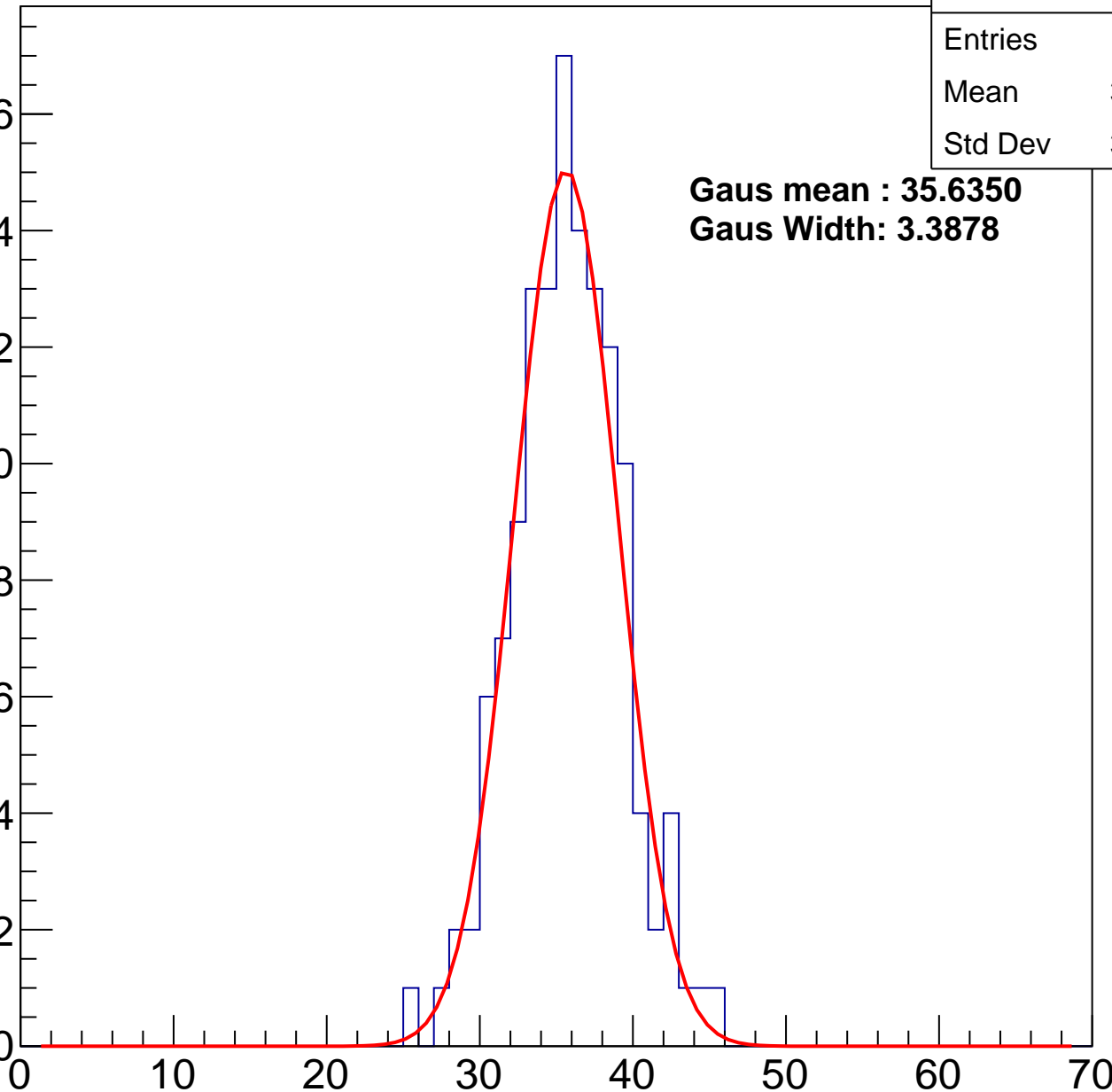
Gaus mean : 35.6350

Gaus Width: 3.3878

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch77, adc2

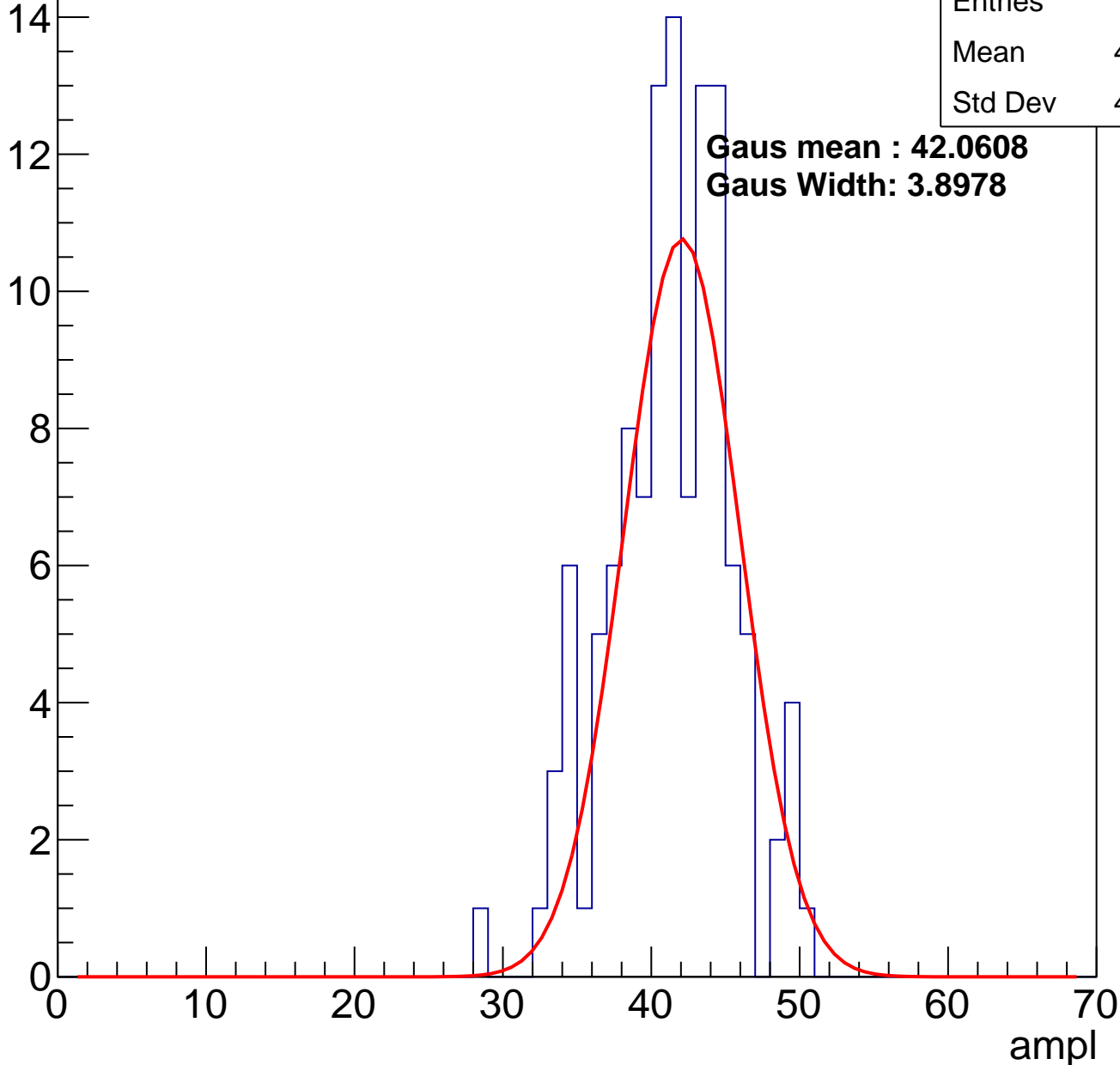
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	116
Mean	40.84
Std Dev	4.084

Gaus mean : 42.0608

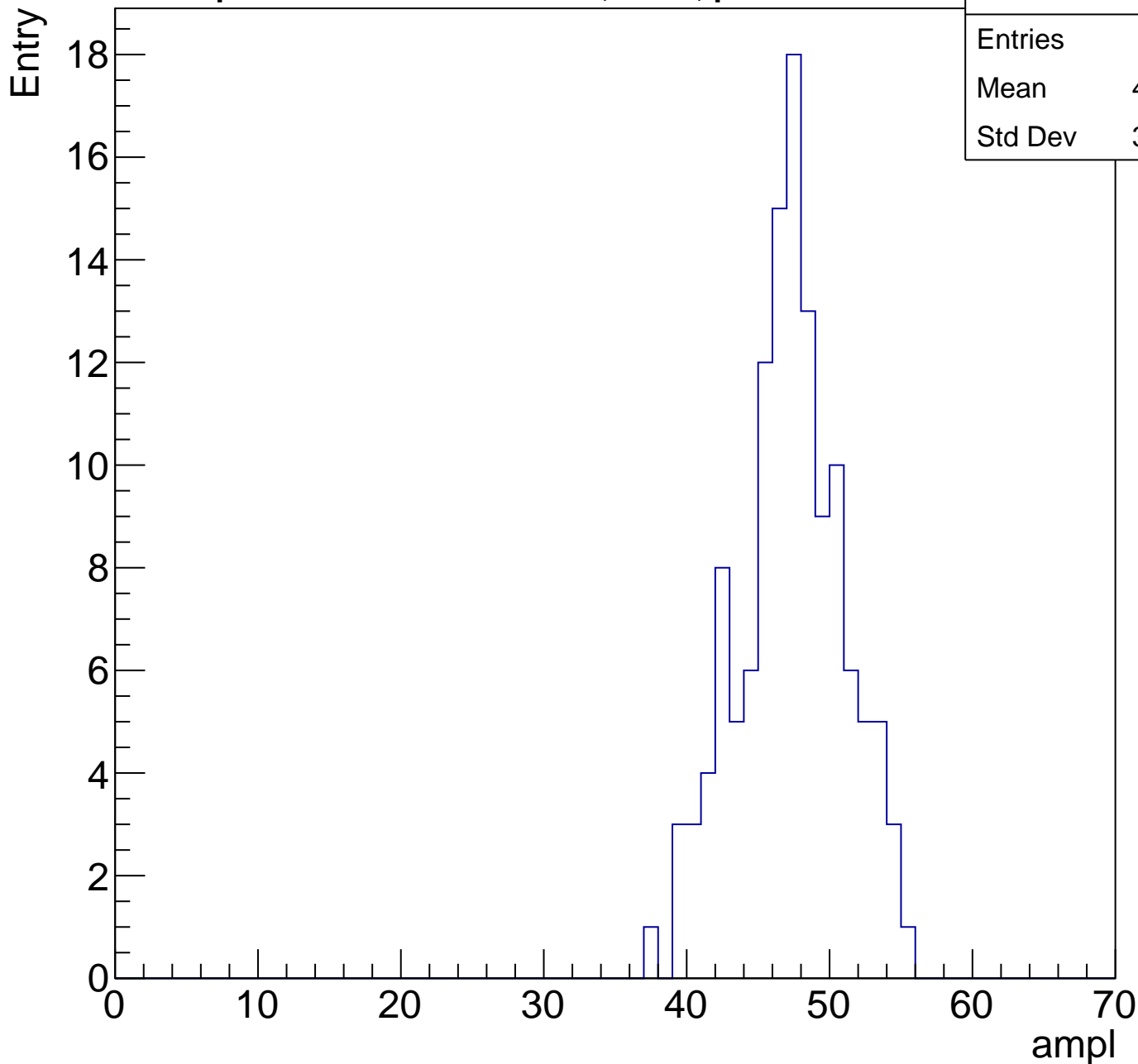
Gaus Width: 3.8978



B0L101S, U18-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

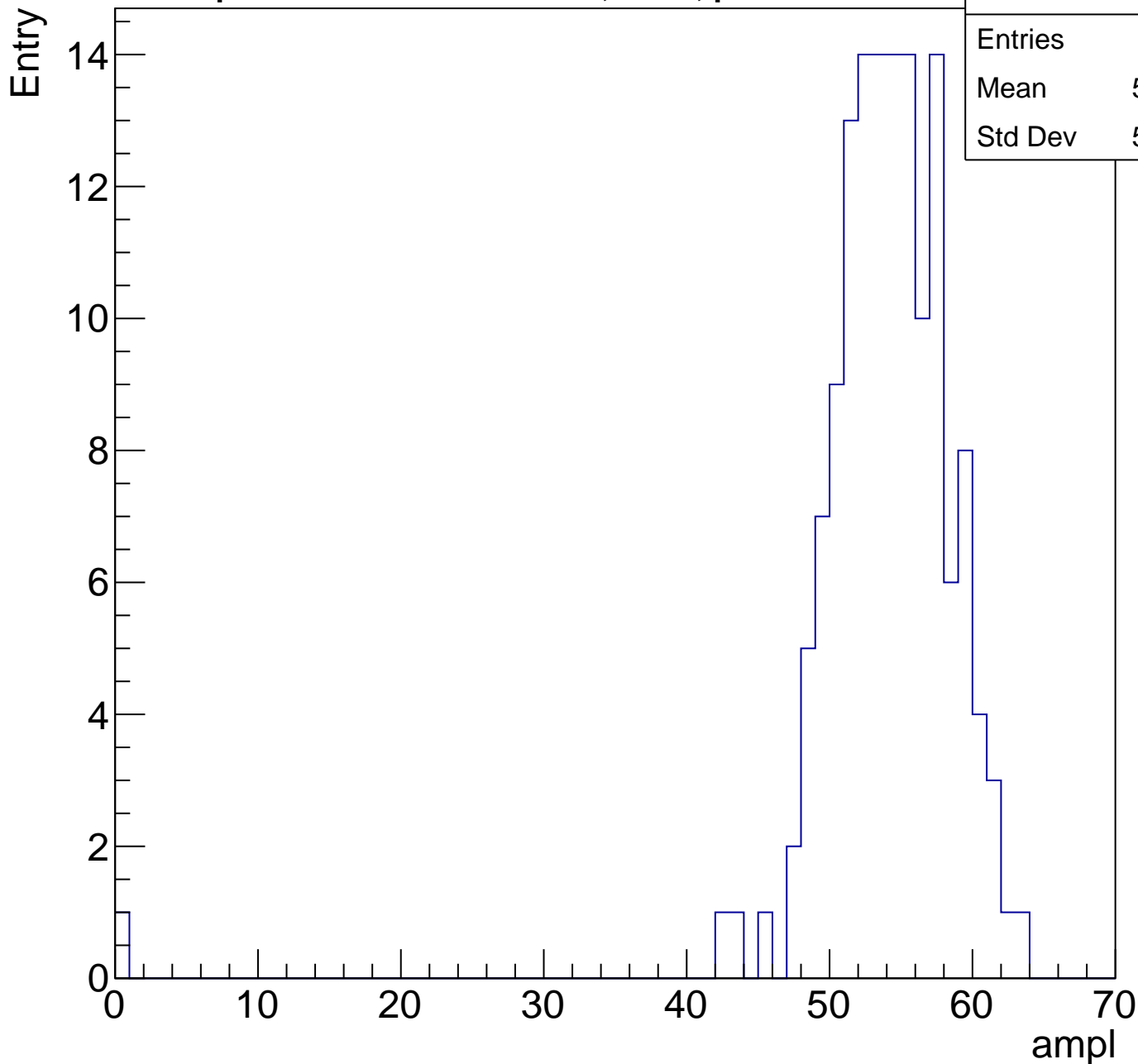
Entries	127
Mean	46.79
Std Dev	3.686



B0L101S, U18-ch77, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	53.44
Std Dev	5.862



B0L101S, U18-ch77, adc5

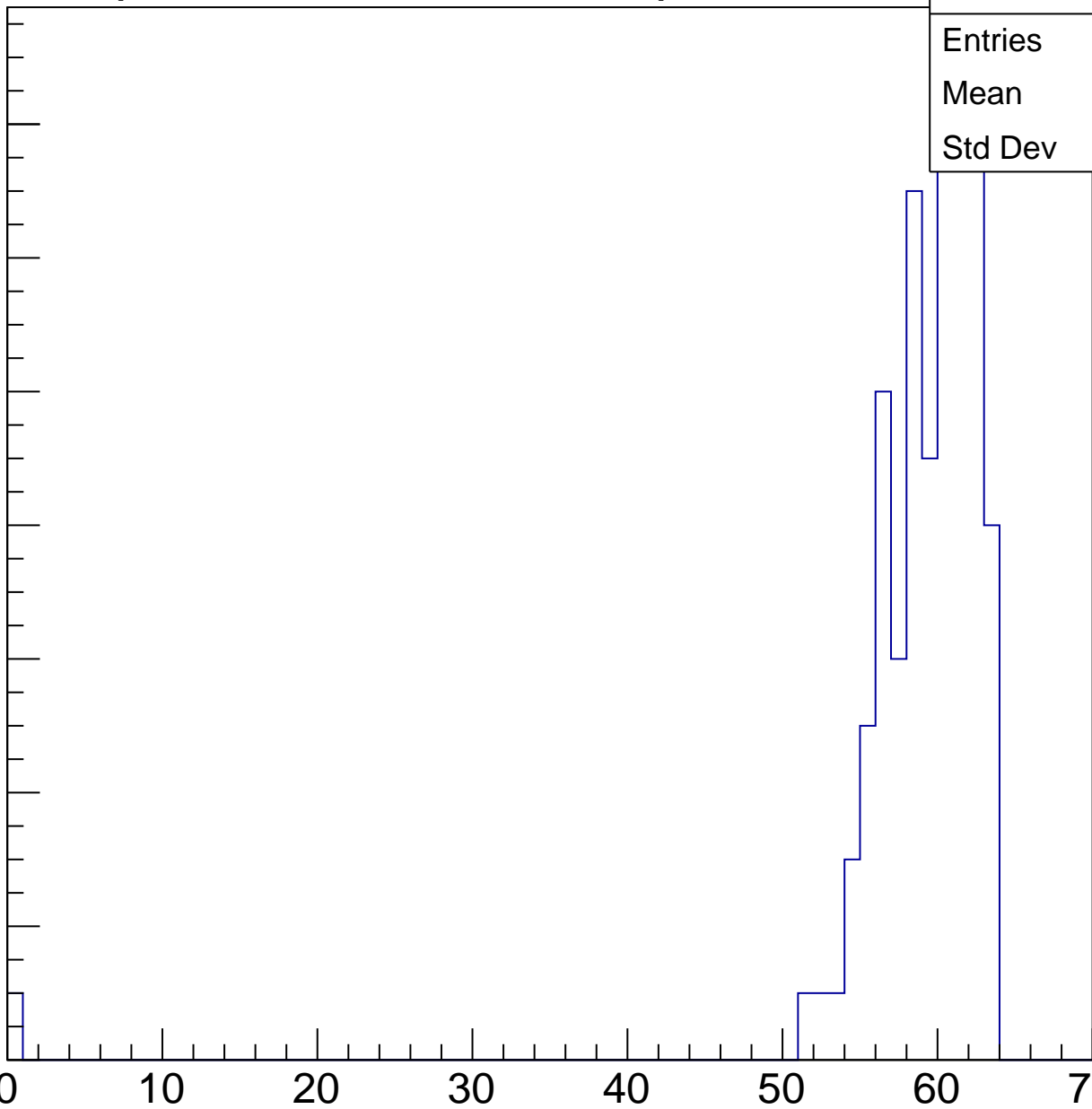
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	58.5
Std Dev	6.463

ampl

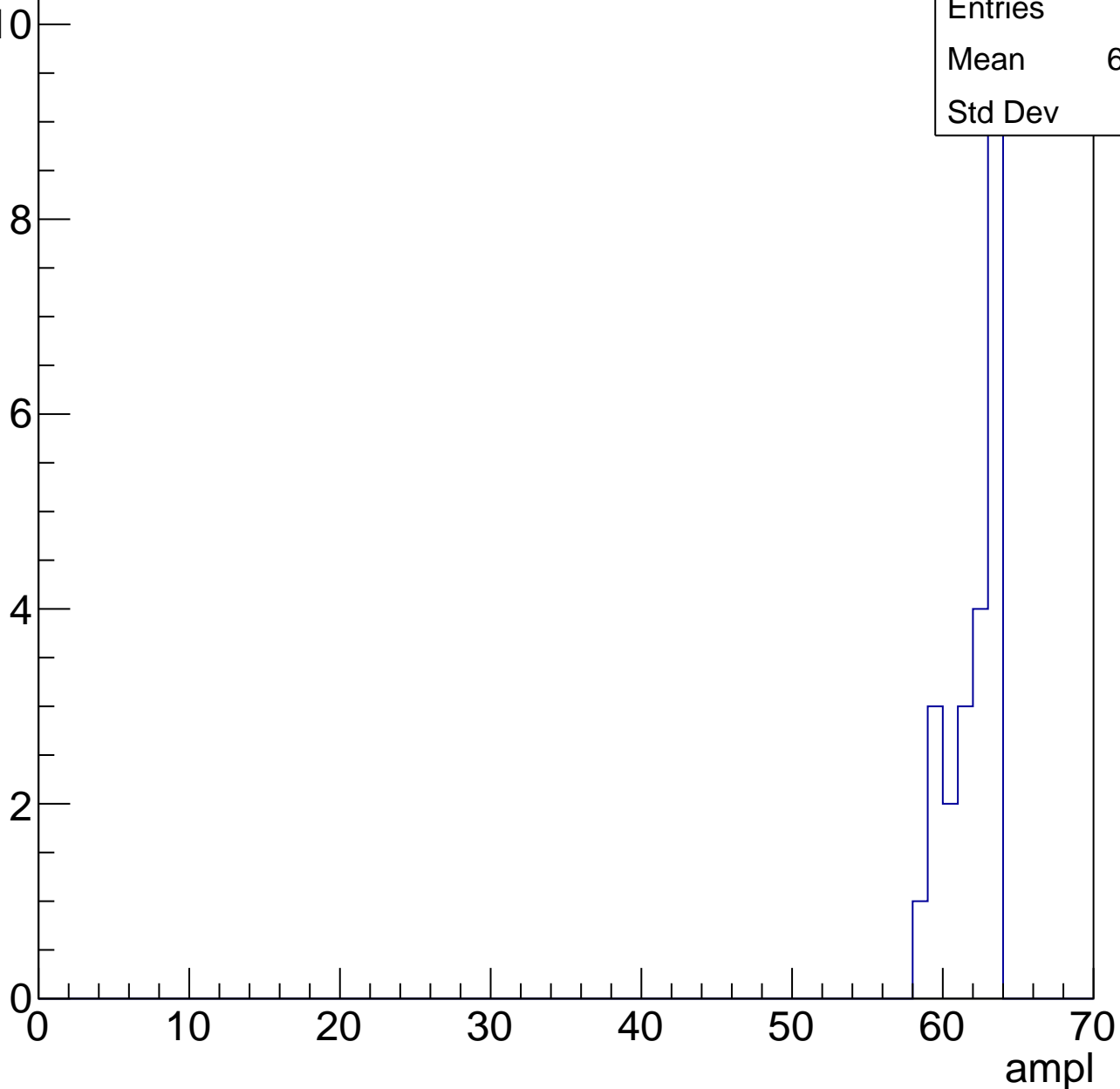


B0L101S, U18-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	23
Mean	61.57
Std Dev	1.61



B0L101S, U18-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch78, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	28.72
Std Dev	4.01

Gaus mean : 29.0860

Gaus Width: 3.3800

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

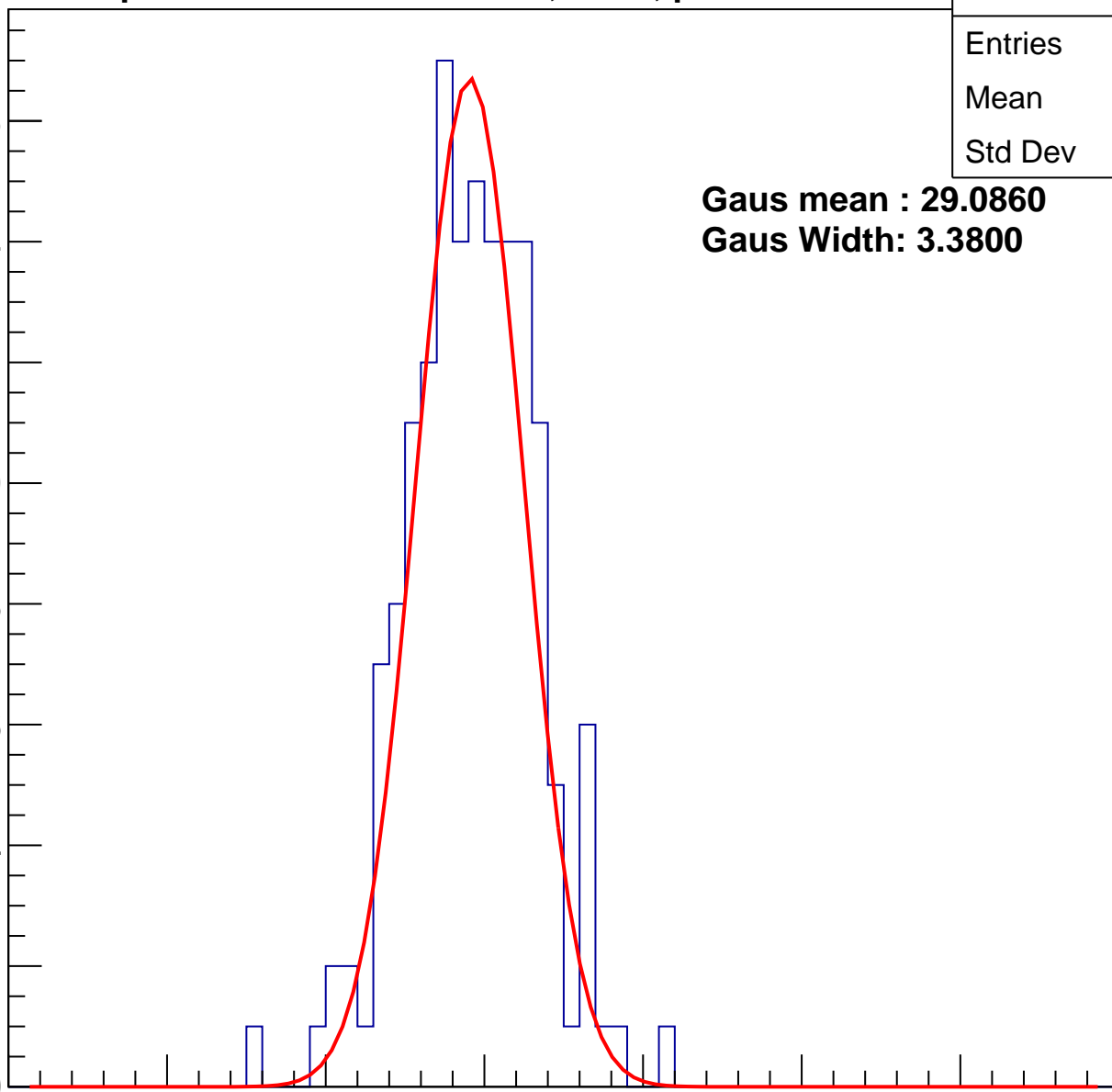
40

50

60

70

ampl



B0L101S, U18-ch78, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	35.94
Std Dev	4.998

Gaus mean : 36.7742

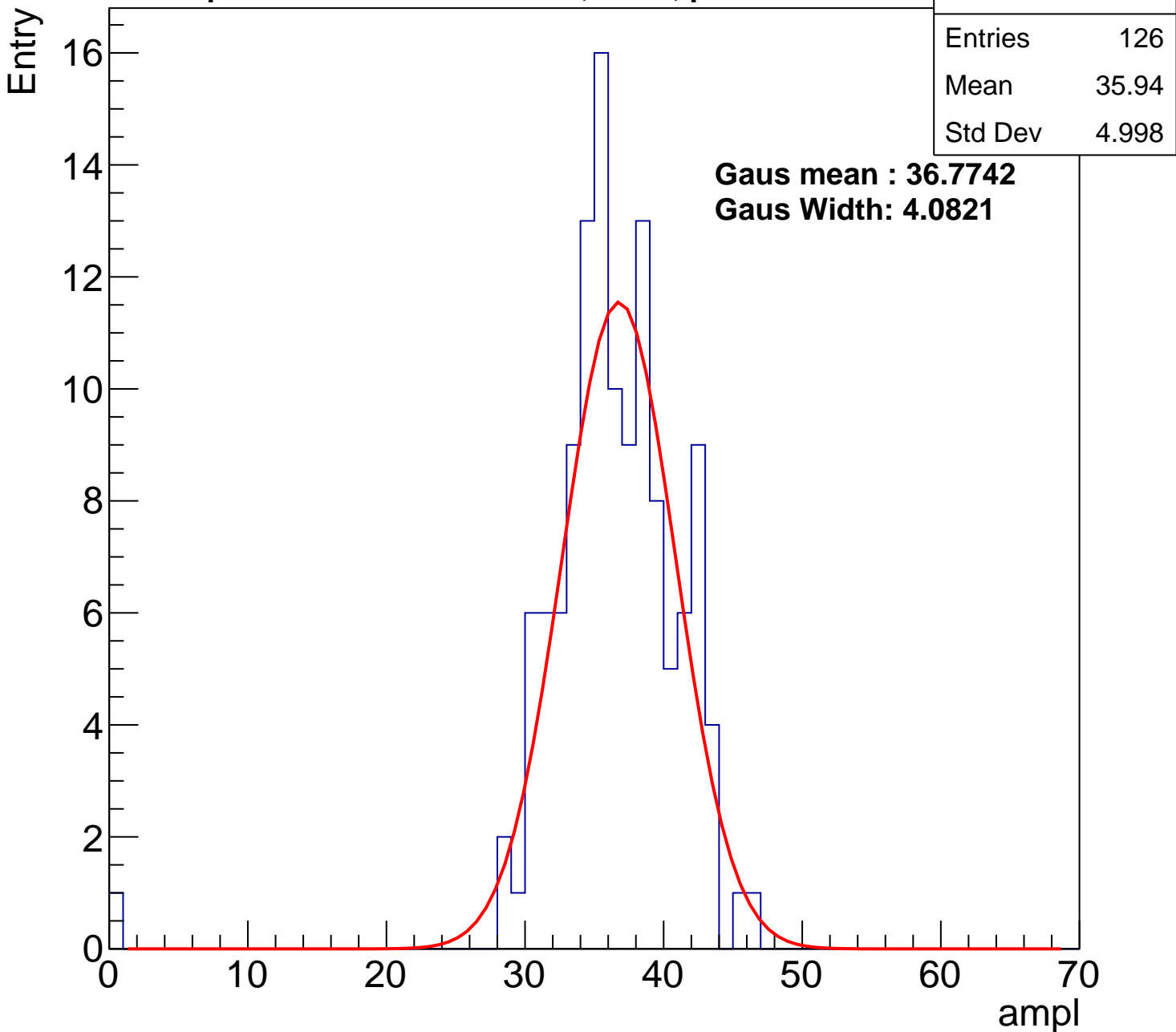
Gaus Width: 4.0821

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch78, adc2

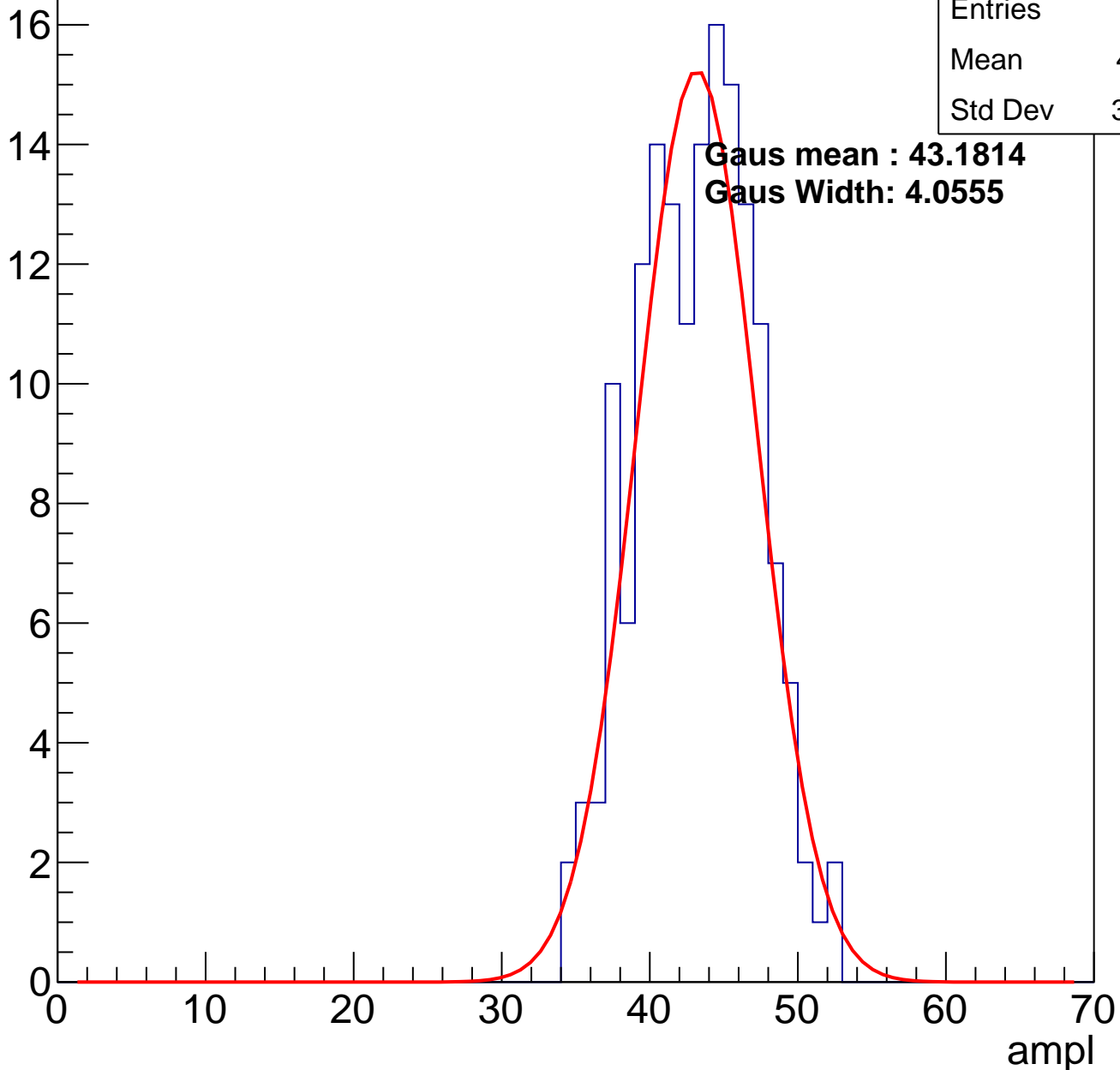
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	42.71
Std Dev	3.895

Gaus mean : 43.1814

Gaus Width: 4.0555

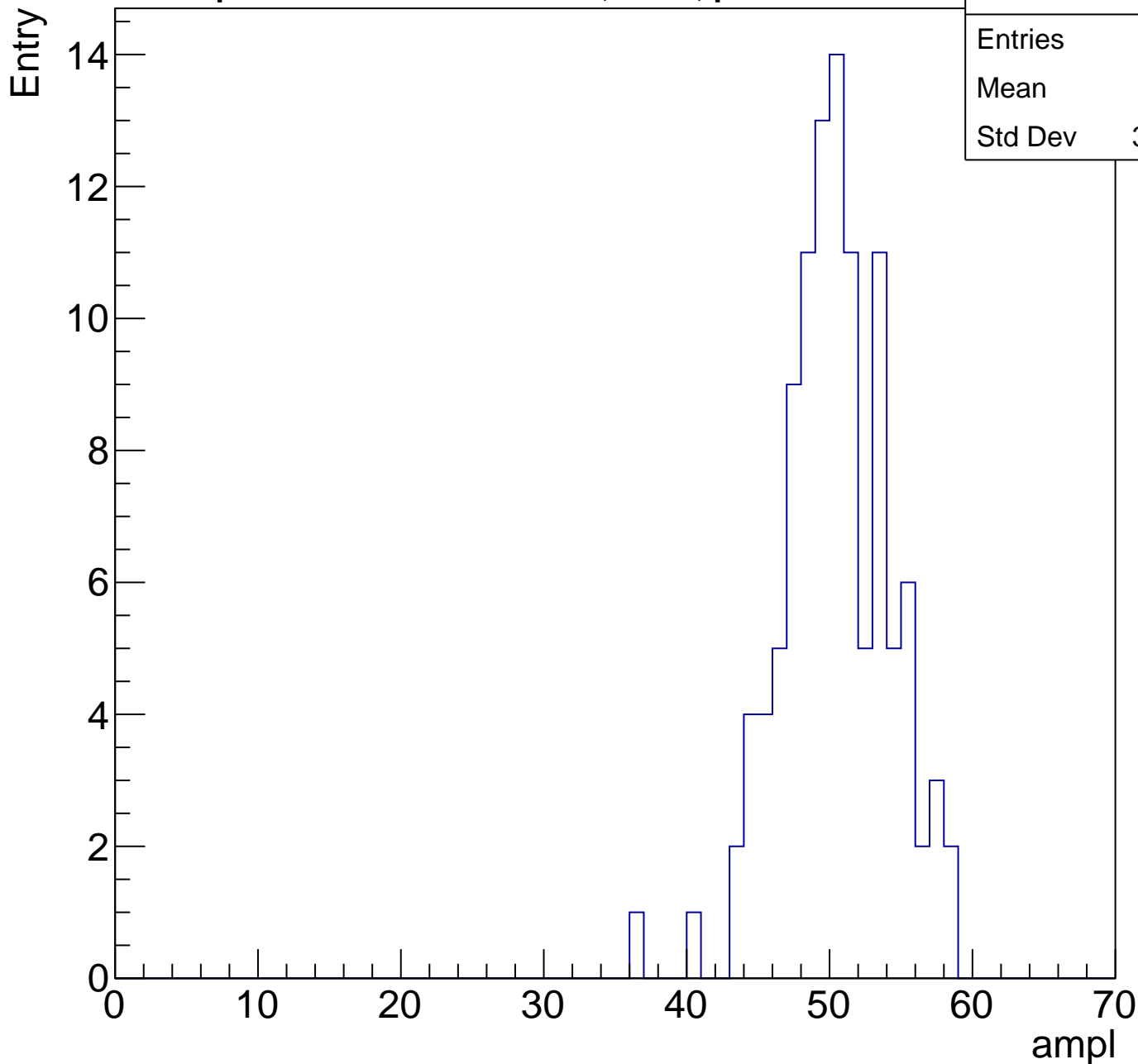
Entry



B0L101S, U18-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	49.9
Std Dev	3.785



B0L101S, U18-ch78, adc4

calib_packv5_042523_0143.root, FC#1, port C1

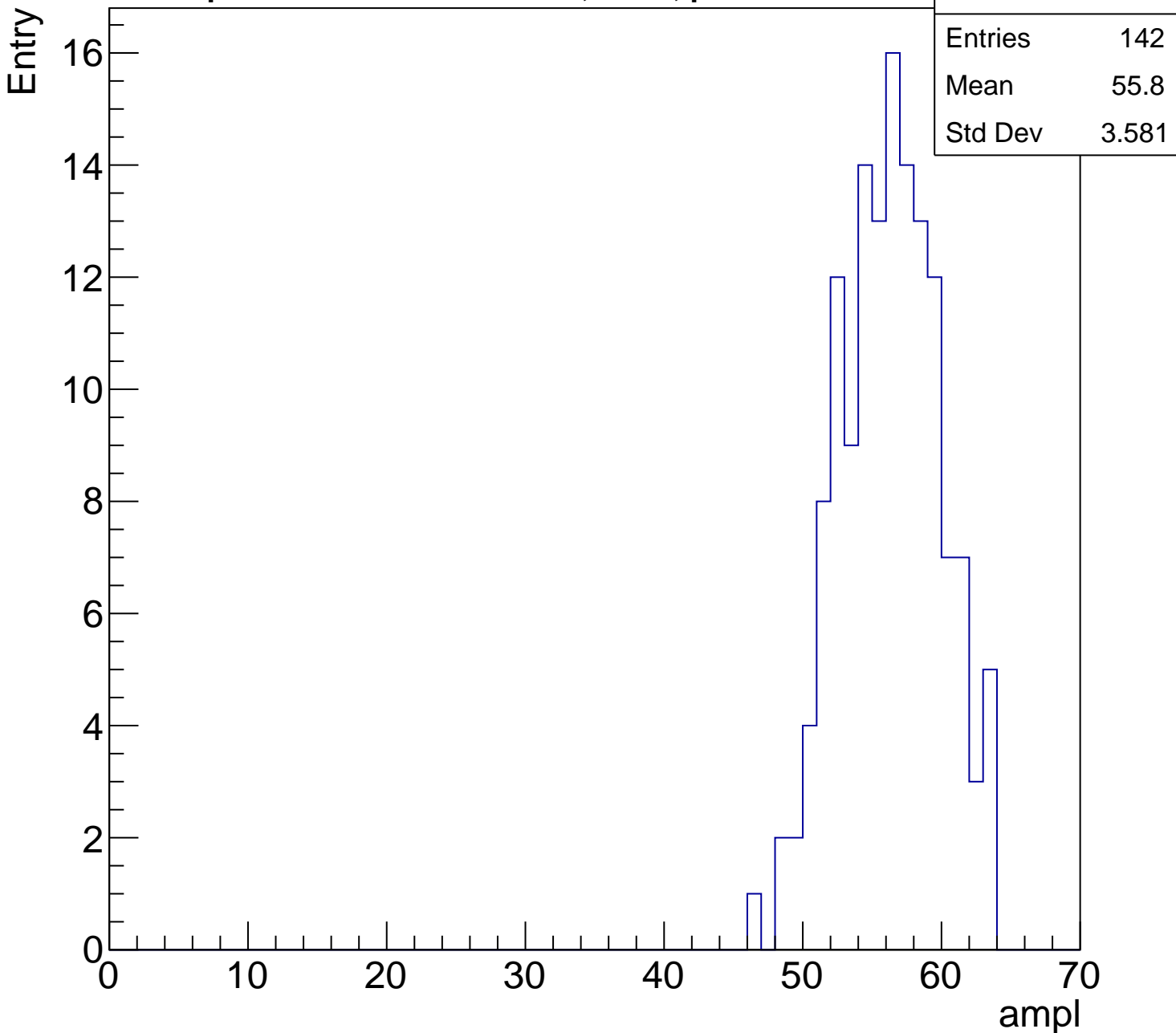
Entries	142
Mean	55.8
Std Dev	3.581

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch78, adc5

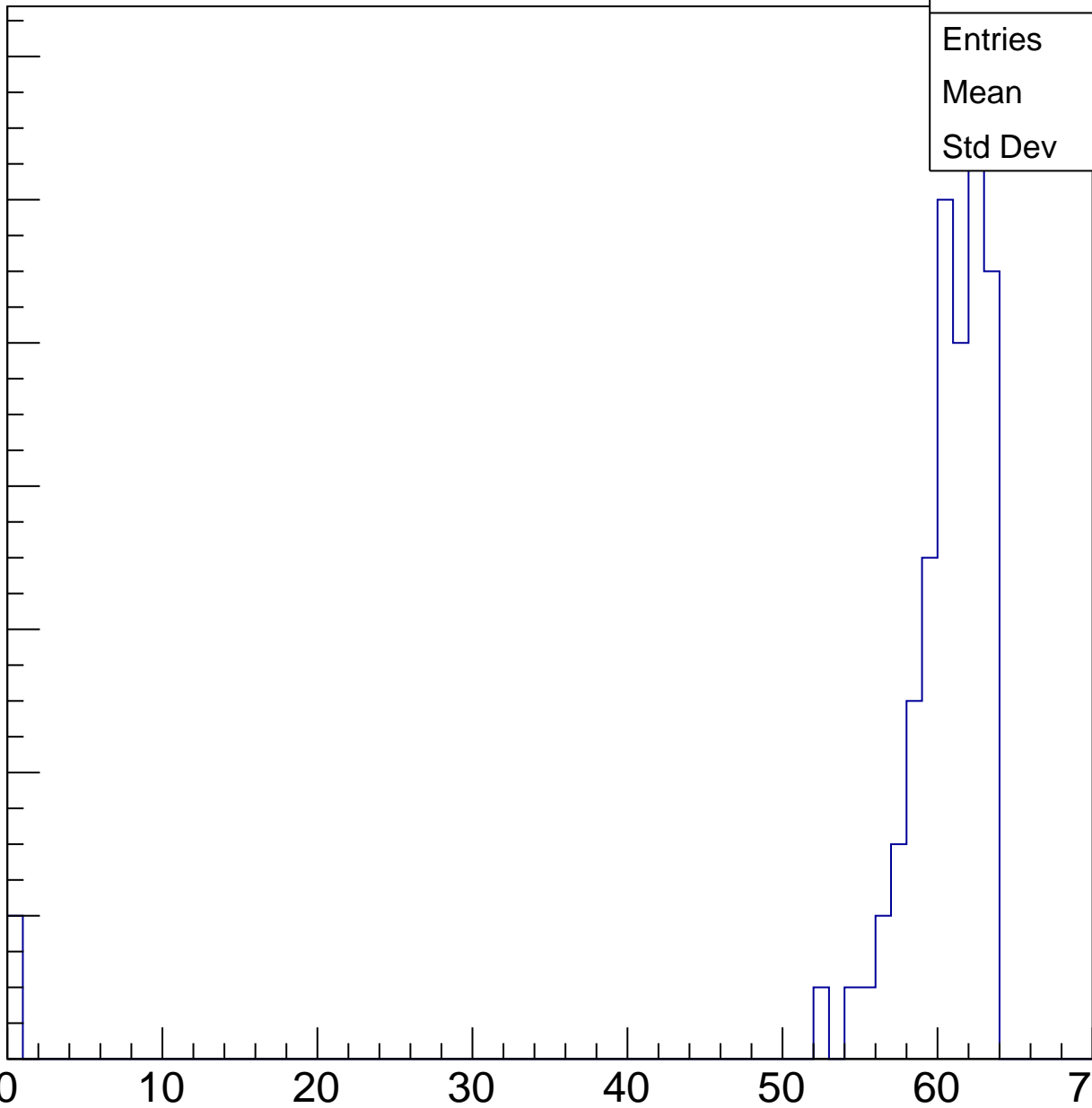
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	58.52
Std Dev	10.37

ampl



B0L101S, U18-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

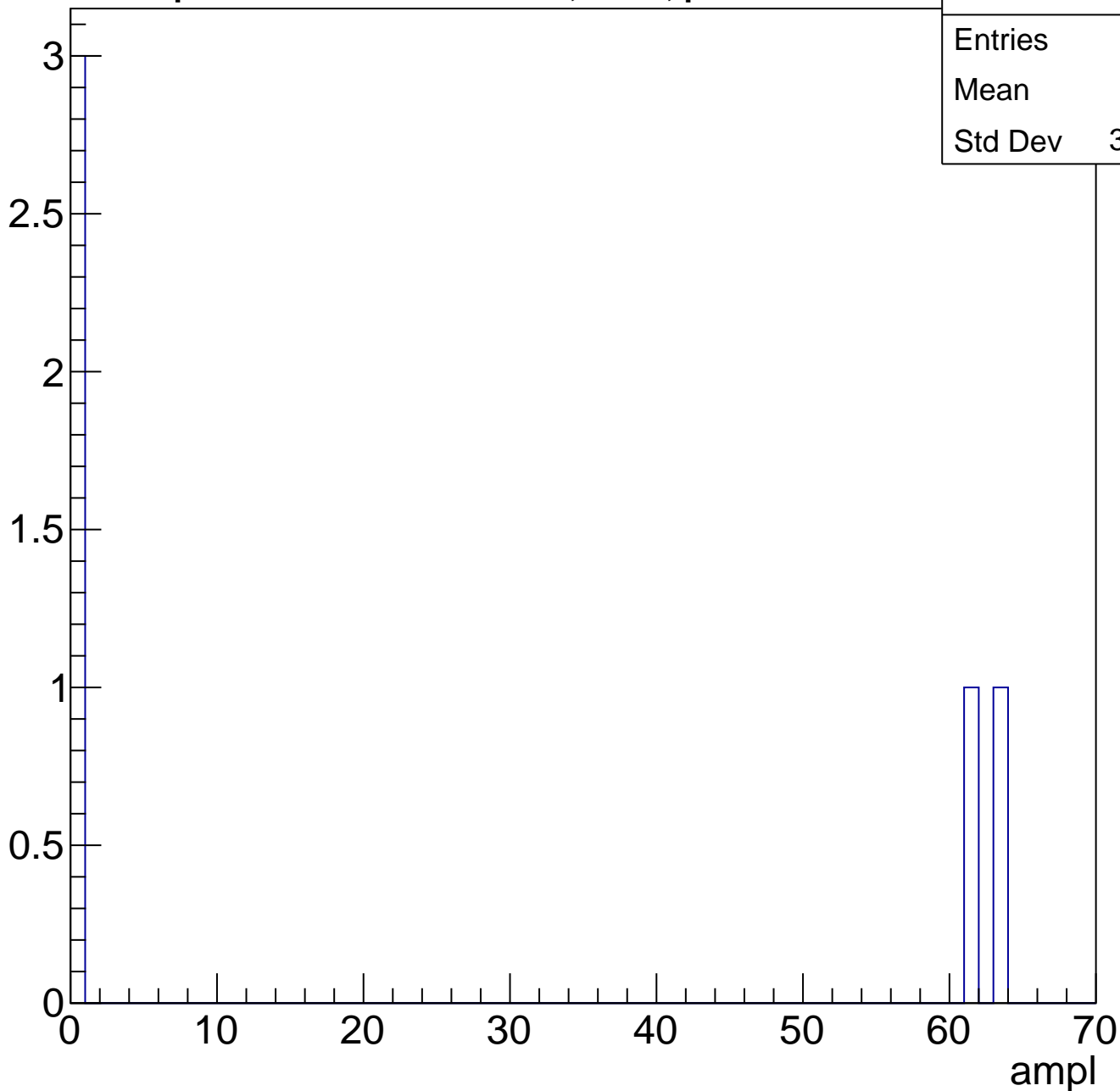
ampl

Entries	10
Mean	61.6
Std Dev	1.281

B0L101S, U18-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch79, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	28.22
Std Dev	5.928

Gaus mean : 29.7187

Gaus Width: 3.1474

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

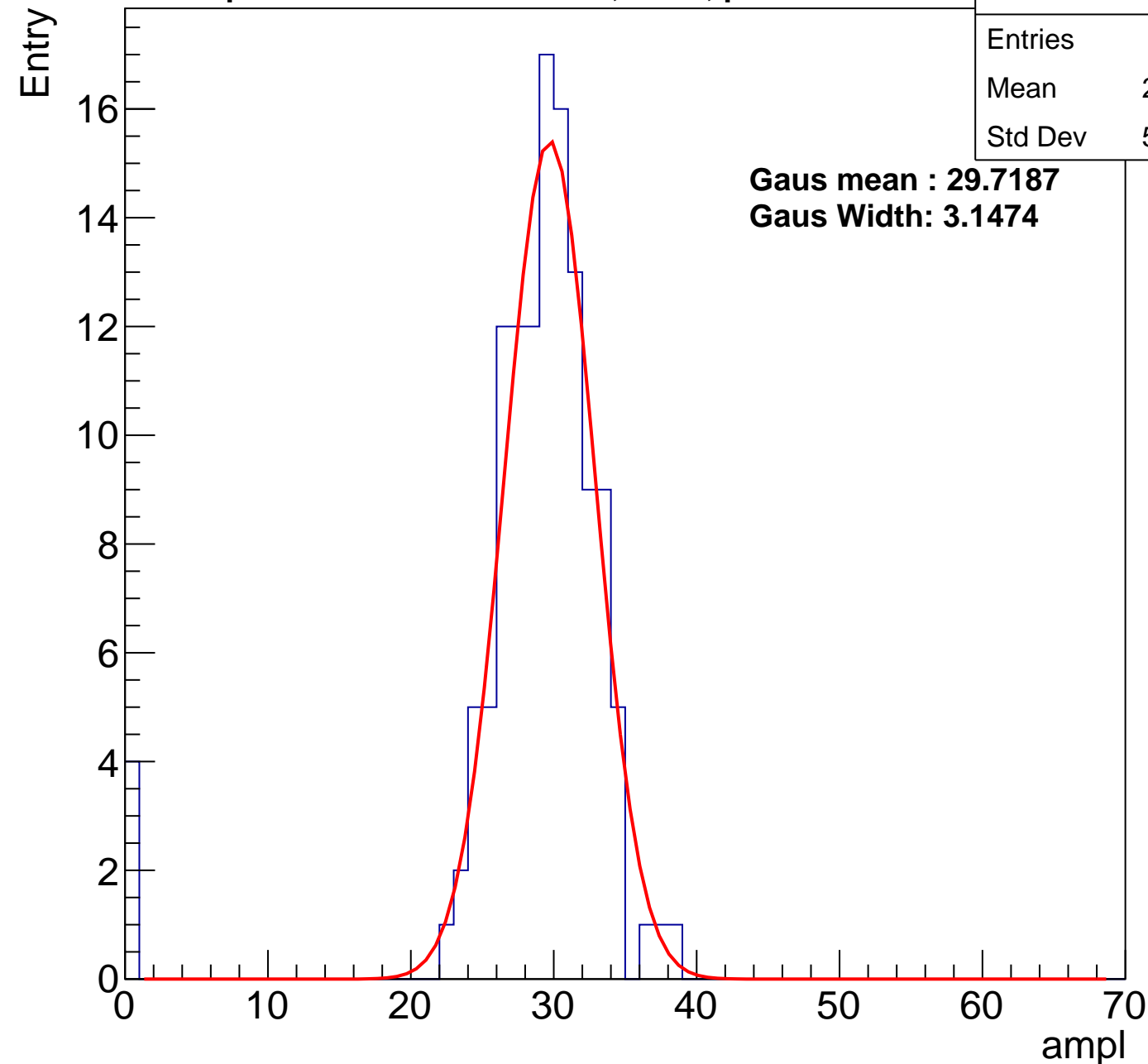
40

50

60

70

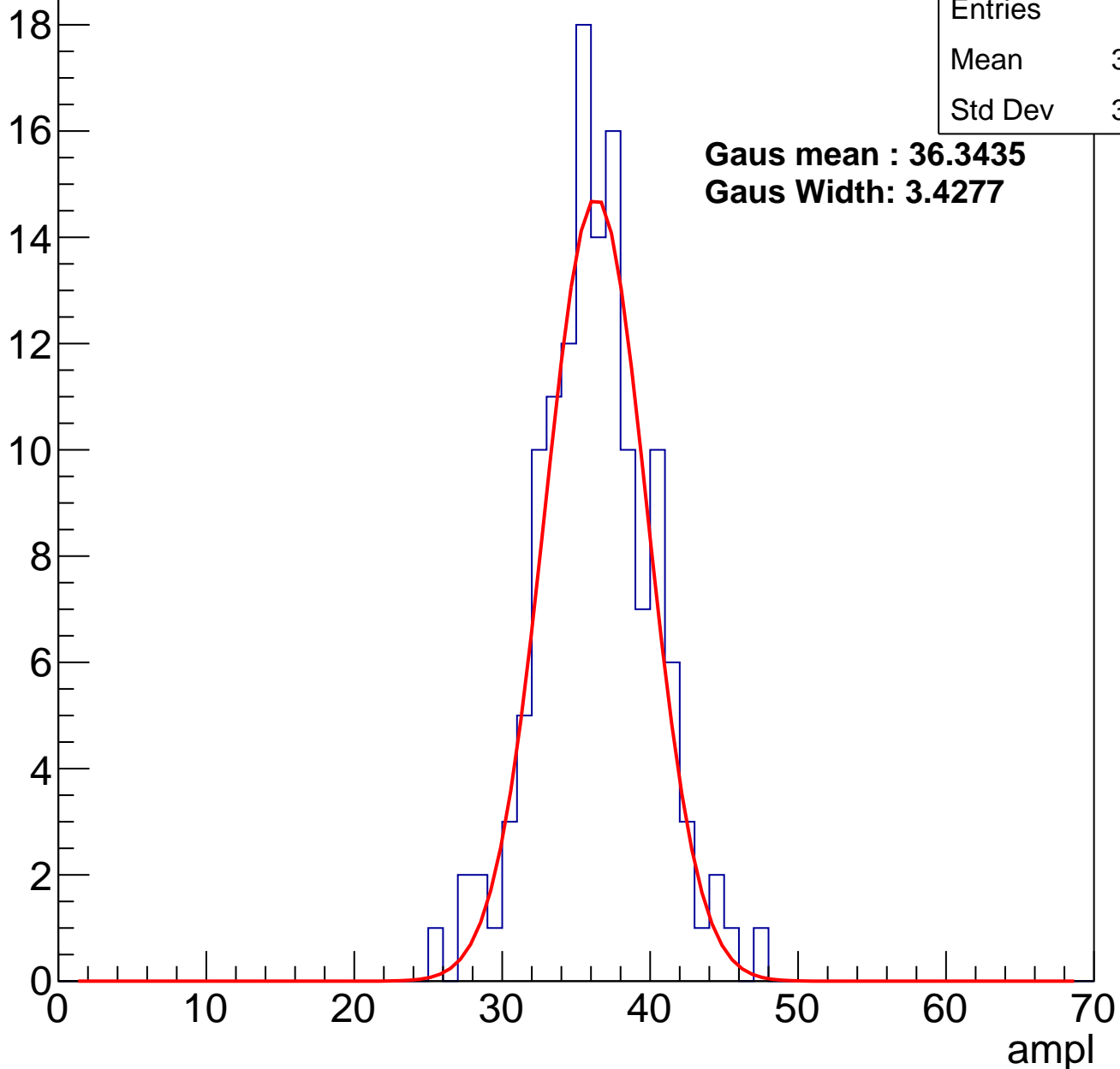
ampl



B0L101S, U18-ch79, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch79, adc2

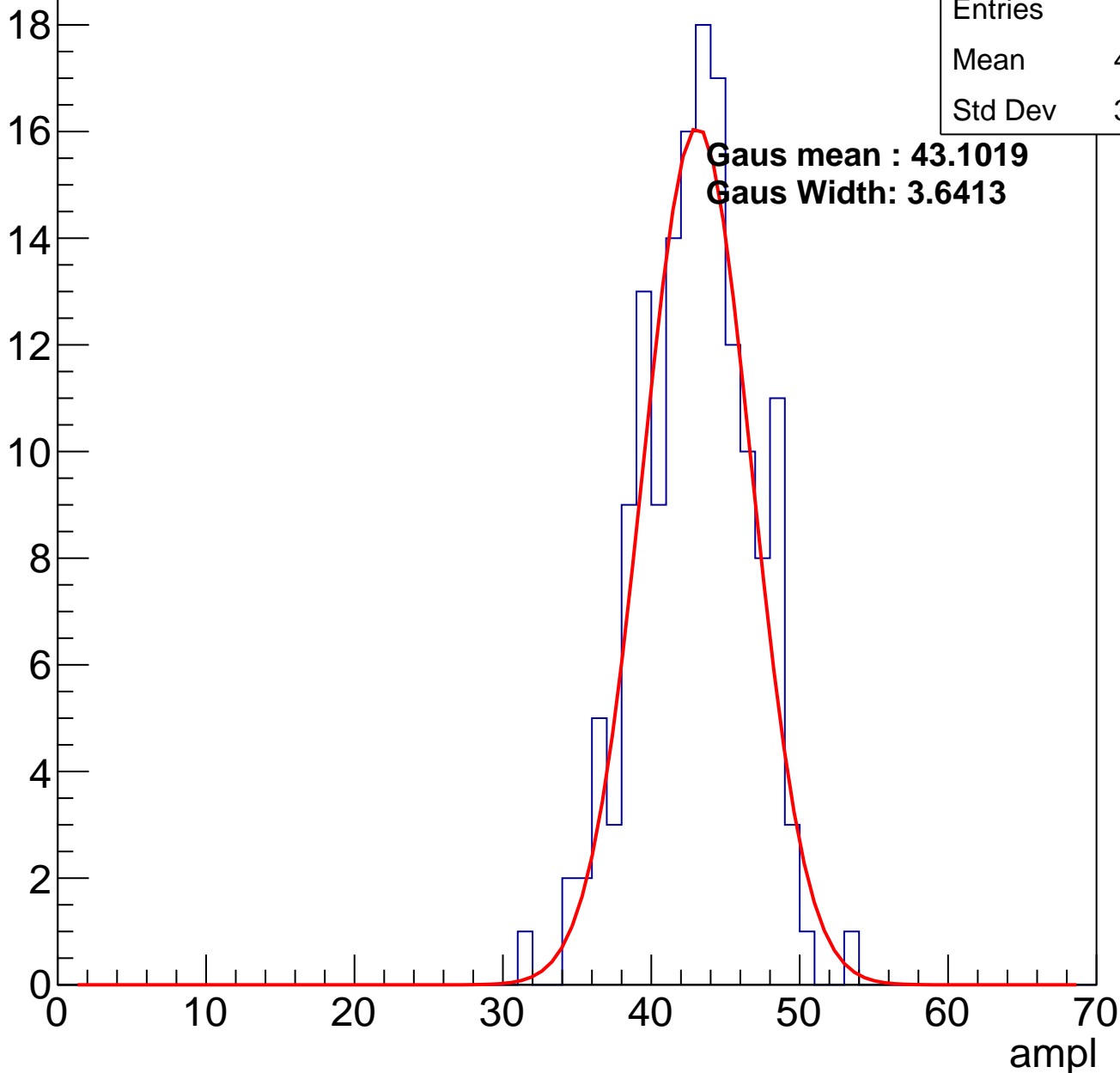
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	42.52
Std Dev	3.727

Gaus mean : 43.1019

Gaus Width: 3.6413

Entry



B0L101S, U18-ch79, adc3

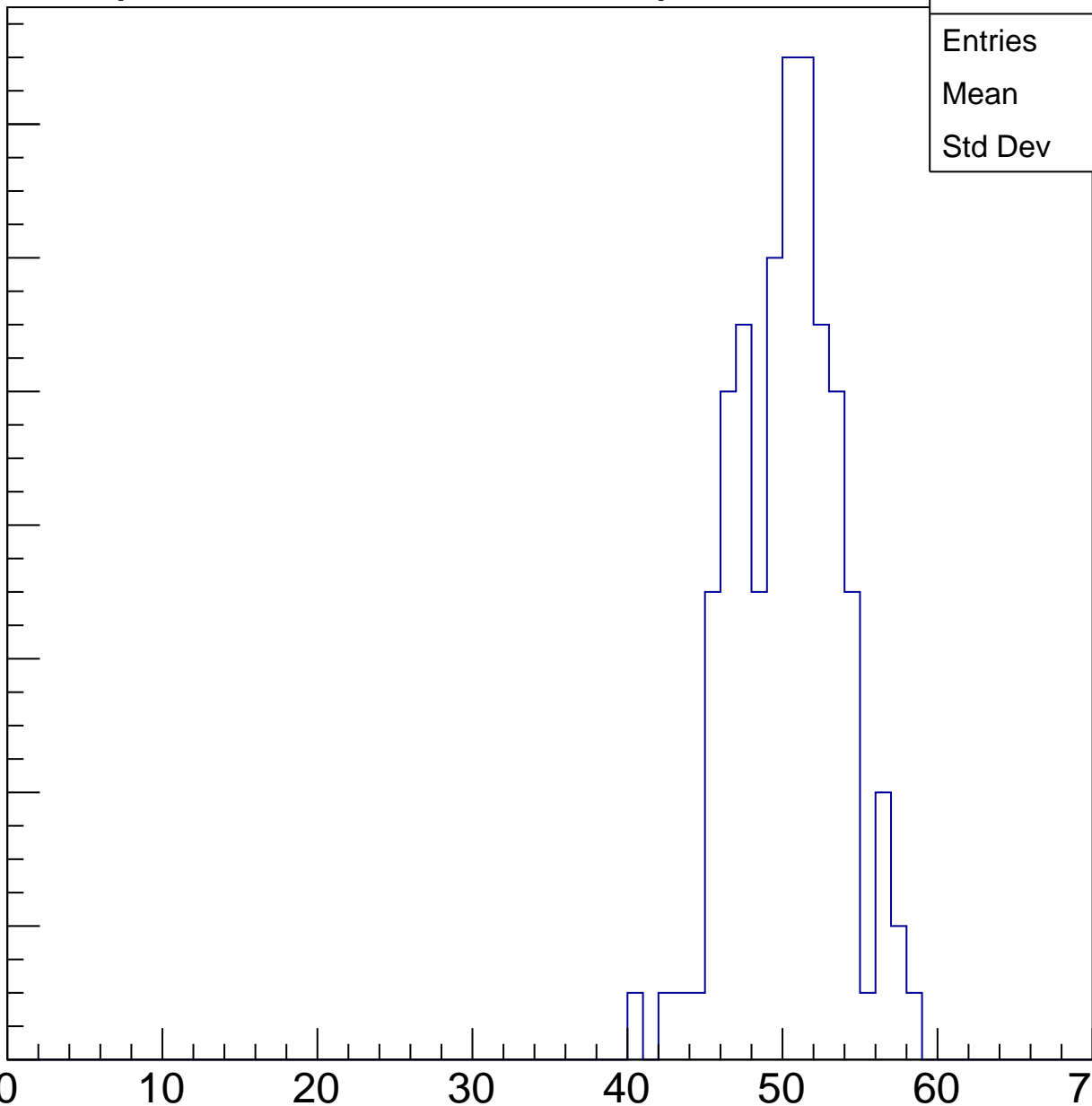
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	49.84
Std Dev	3.349

ampl



B0L101S, U18-ch79, adc4

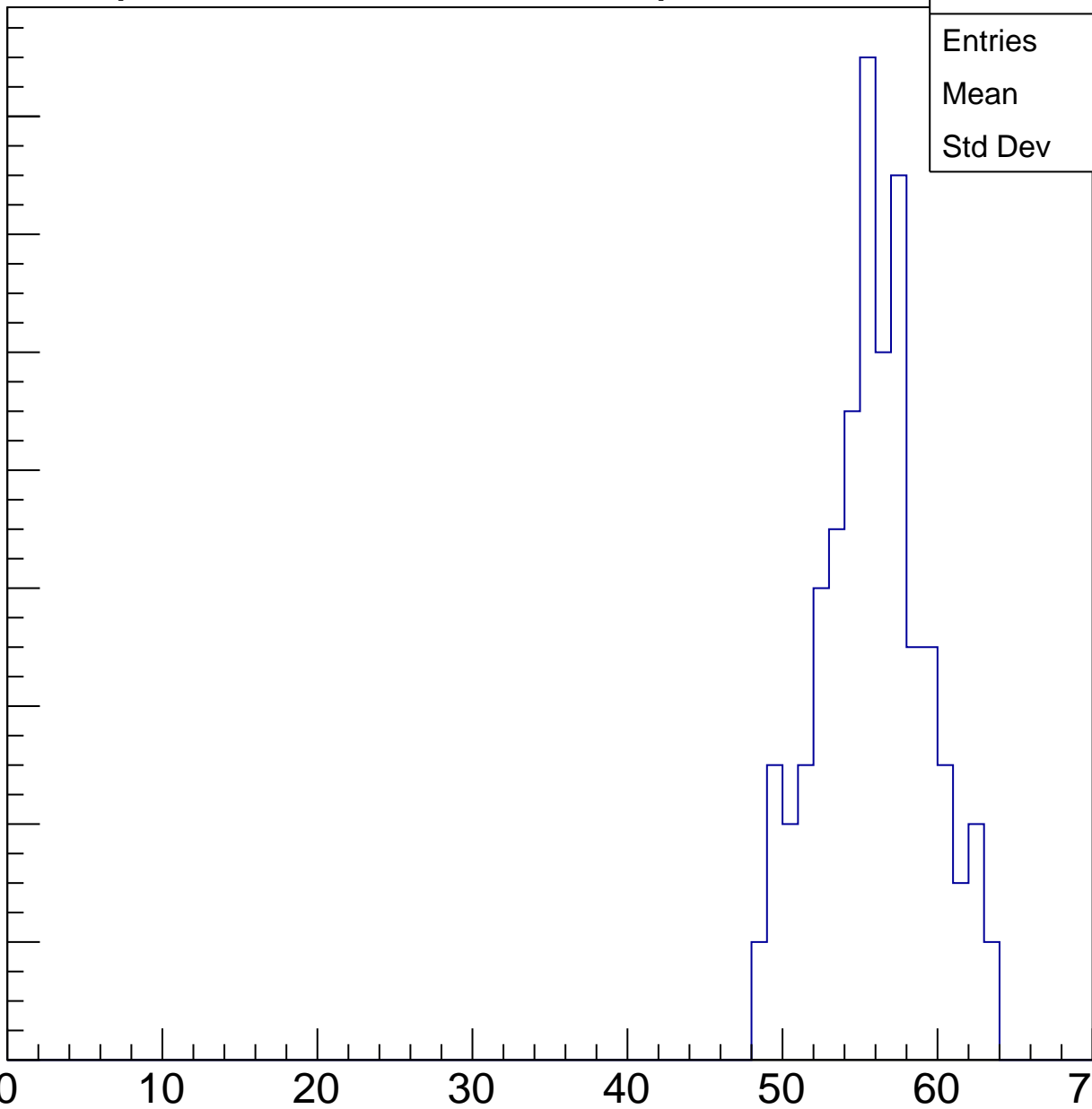
calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	55.35
Std Dev	3.432

Entry

16
14
12
10
8
6
4
2
0

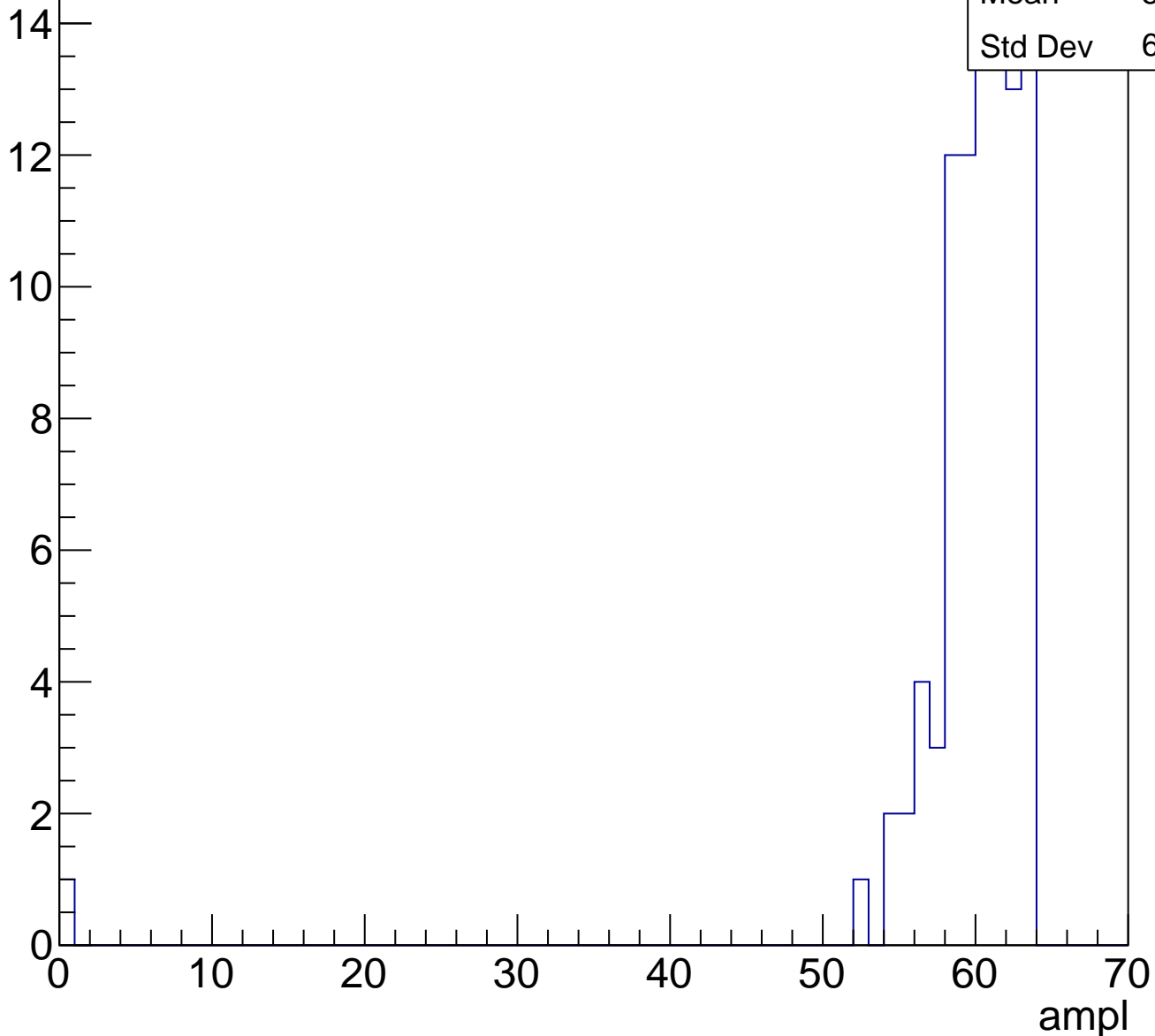
ampl



B0L101S, U18-ch79, adc5

calib_packv5_042523_0143.root, FC#1, port C1

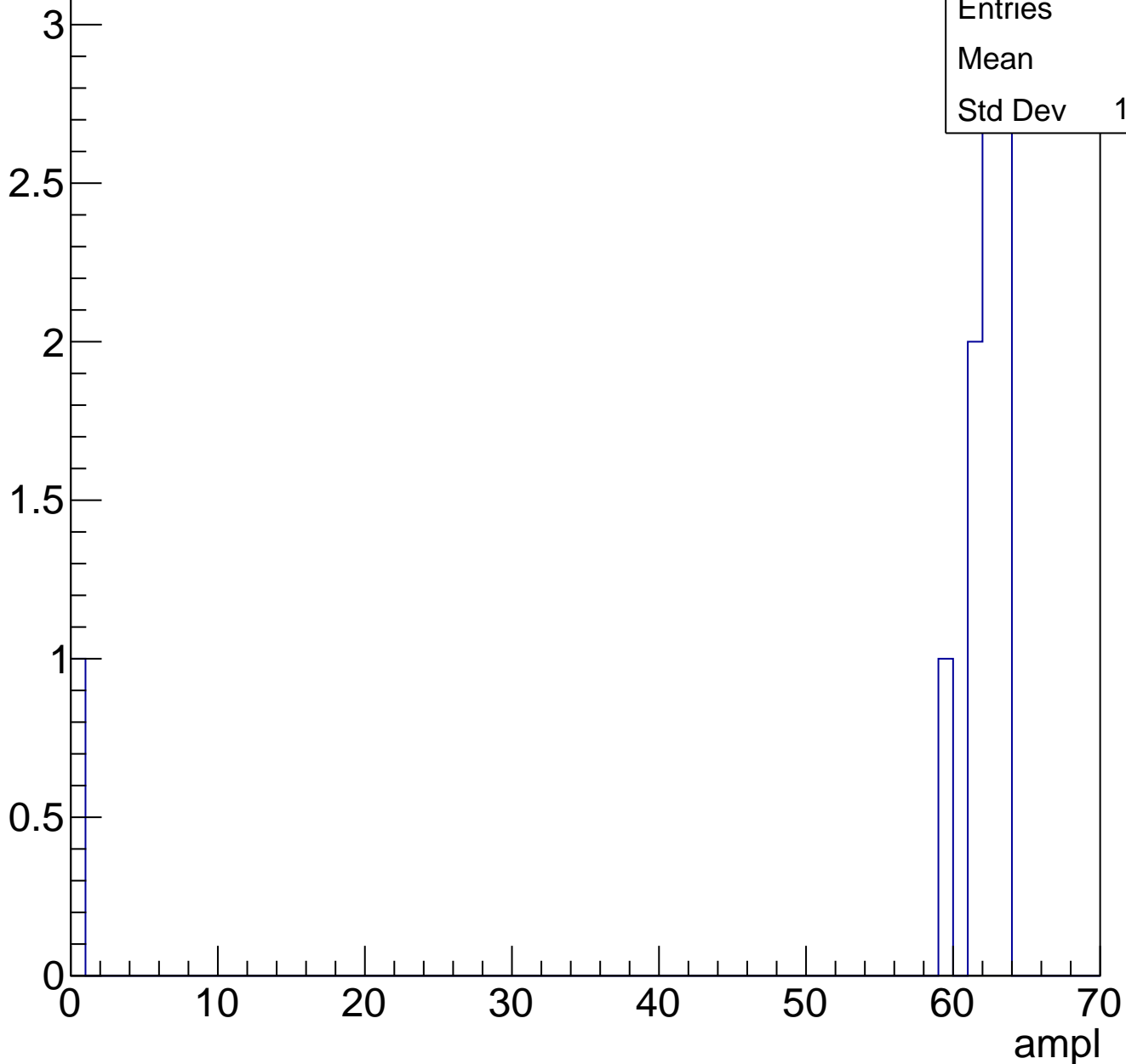
Entry



B0L101S, U18-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



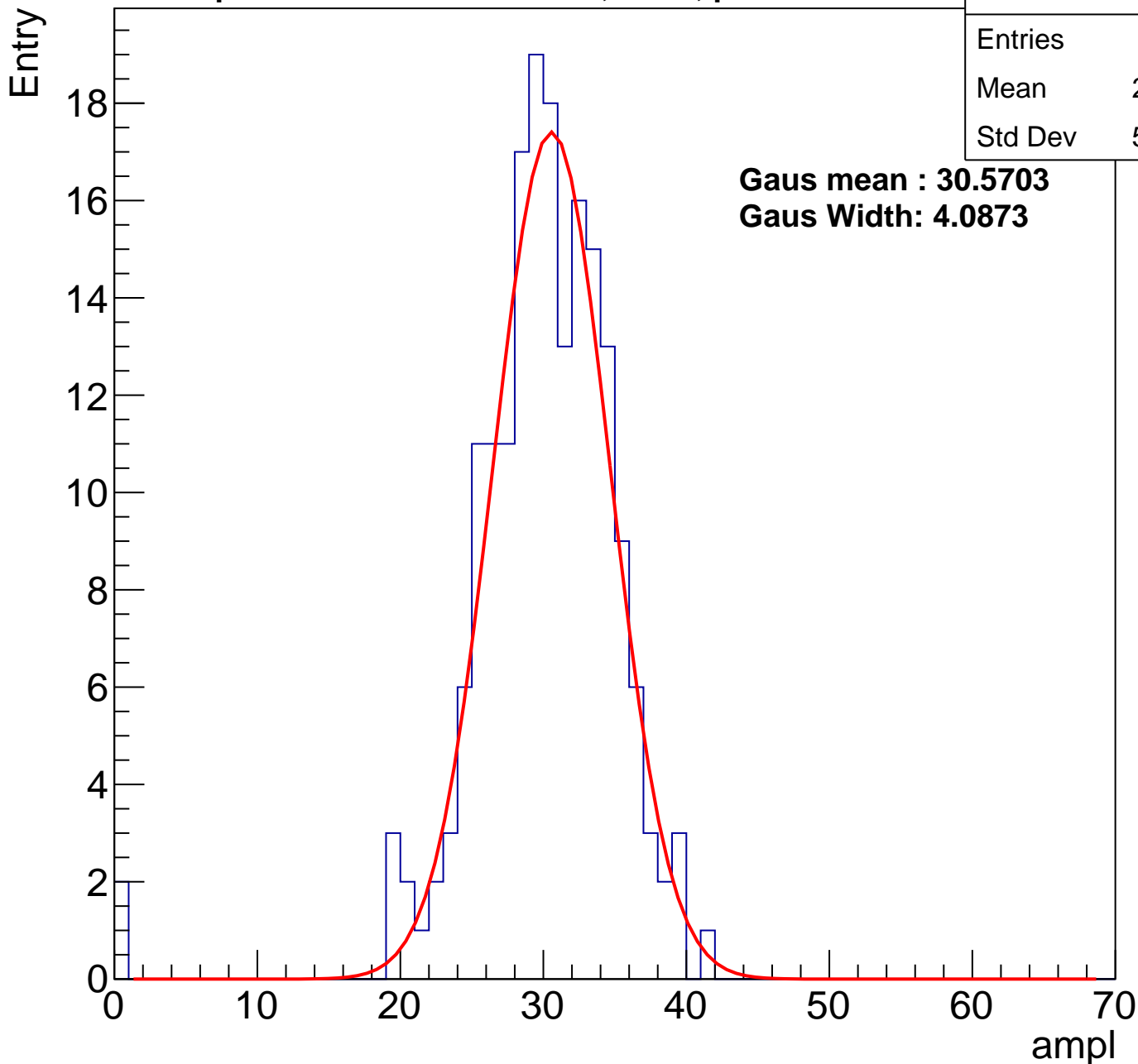
B0L101S, U18-ch80, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	29.56
Std Dev	5.189

Gaus mean : 30.5703

Gaus Width: 4.0873



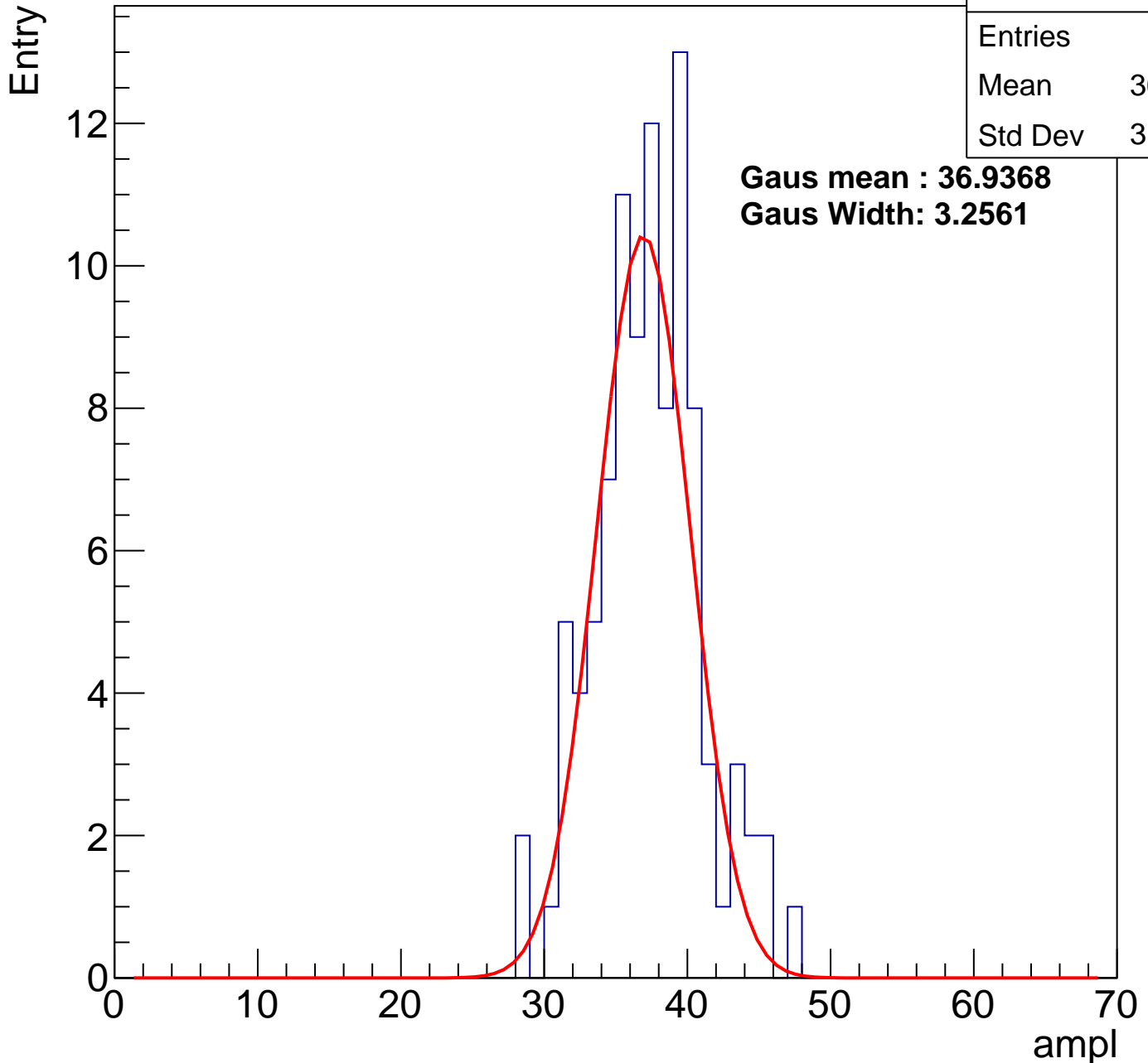
B0L101S, U18-ch80, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	36.86
Std Dev	3.694

Gaus mean : 36.9368

Gaus Width: 3.2561



B0L101S, U18-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

42.61

Std Dev

3.407

Gaus mean : 43.1525

Gaus Width: 3.5073

0

10

20

30

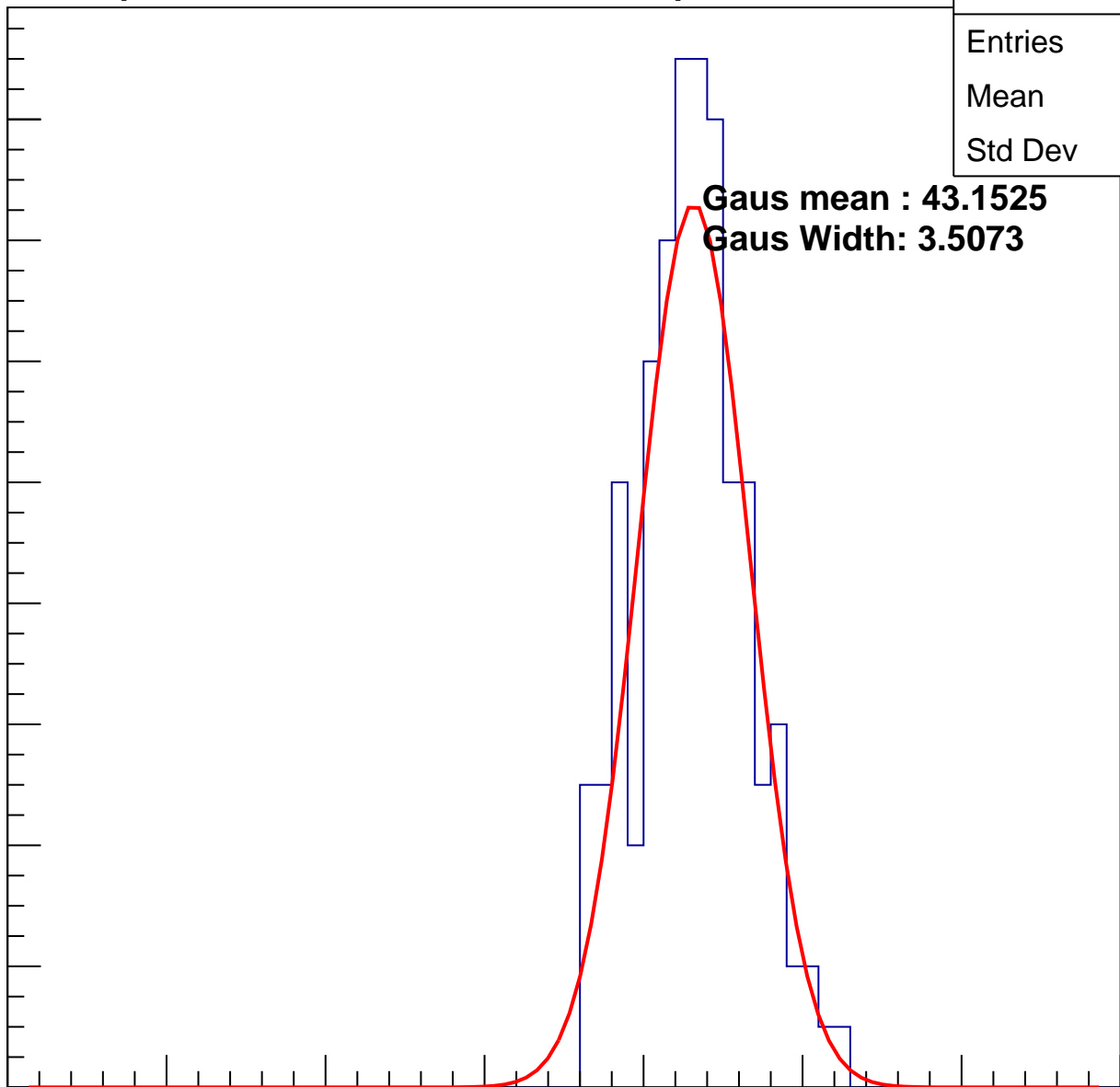
40

50

60

70

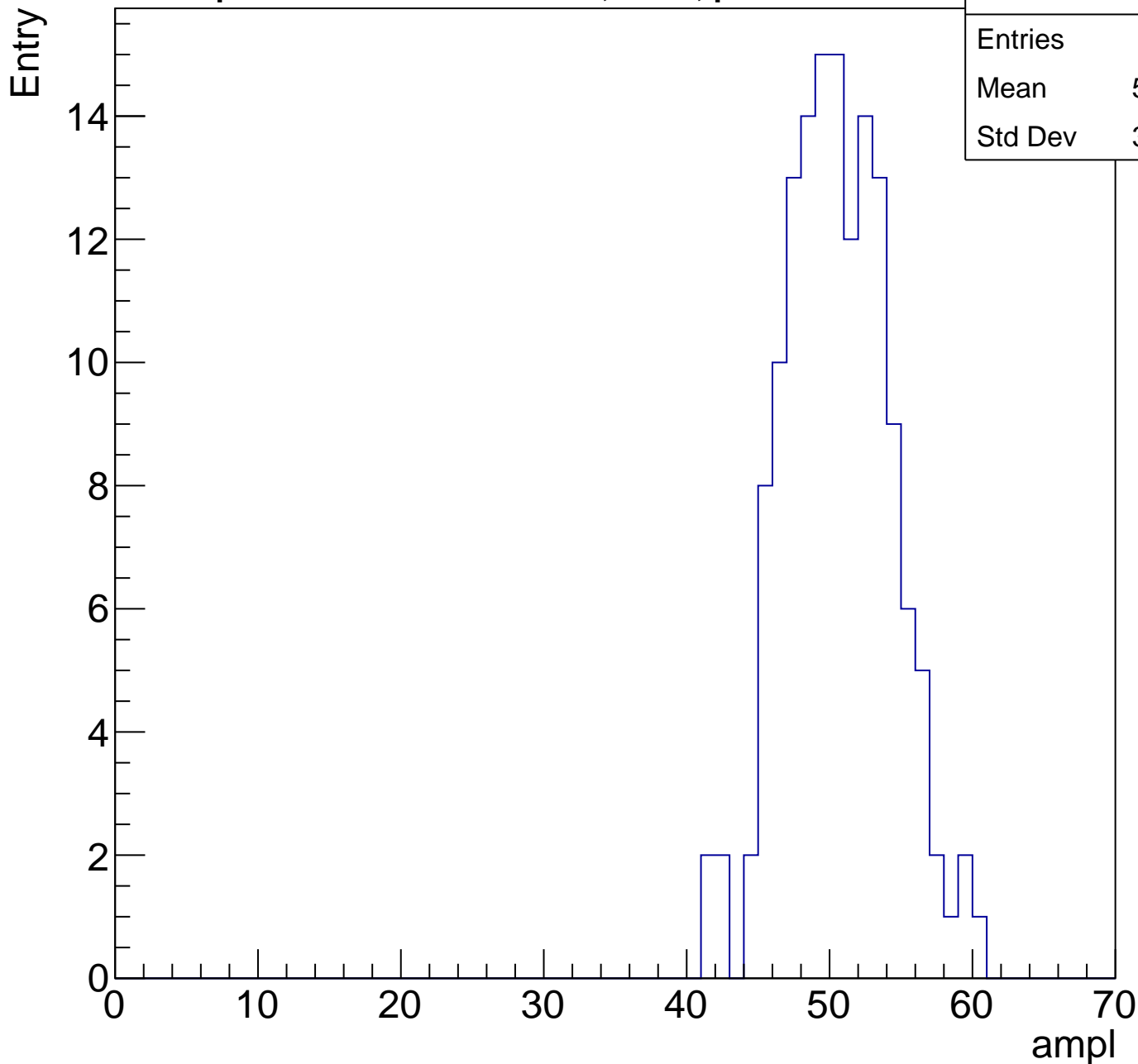
ampl



B0L101S, U18-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

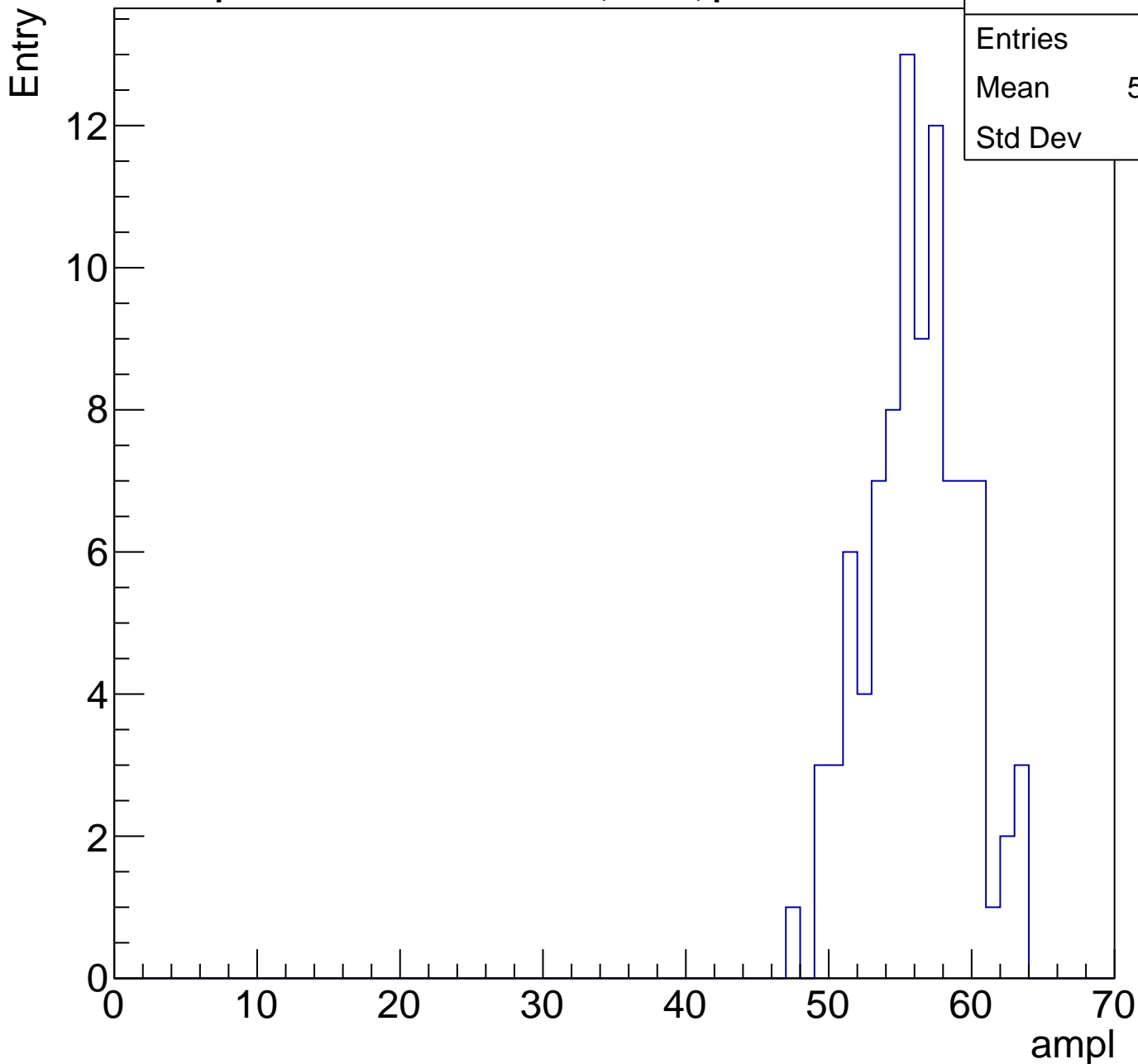
Entries	146
Mean	50.12
Std Dev	3.693



B0L101S, U18-ch80, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	55.67
Std Dev	3.45



B0L101S, U18-ch80, adc5

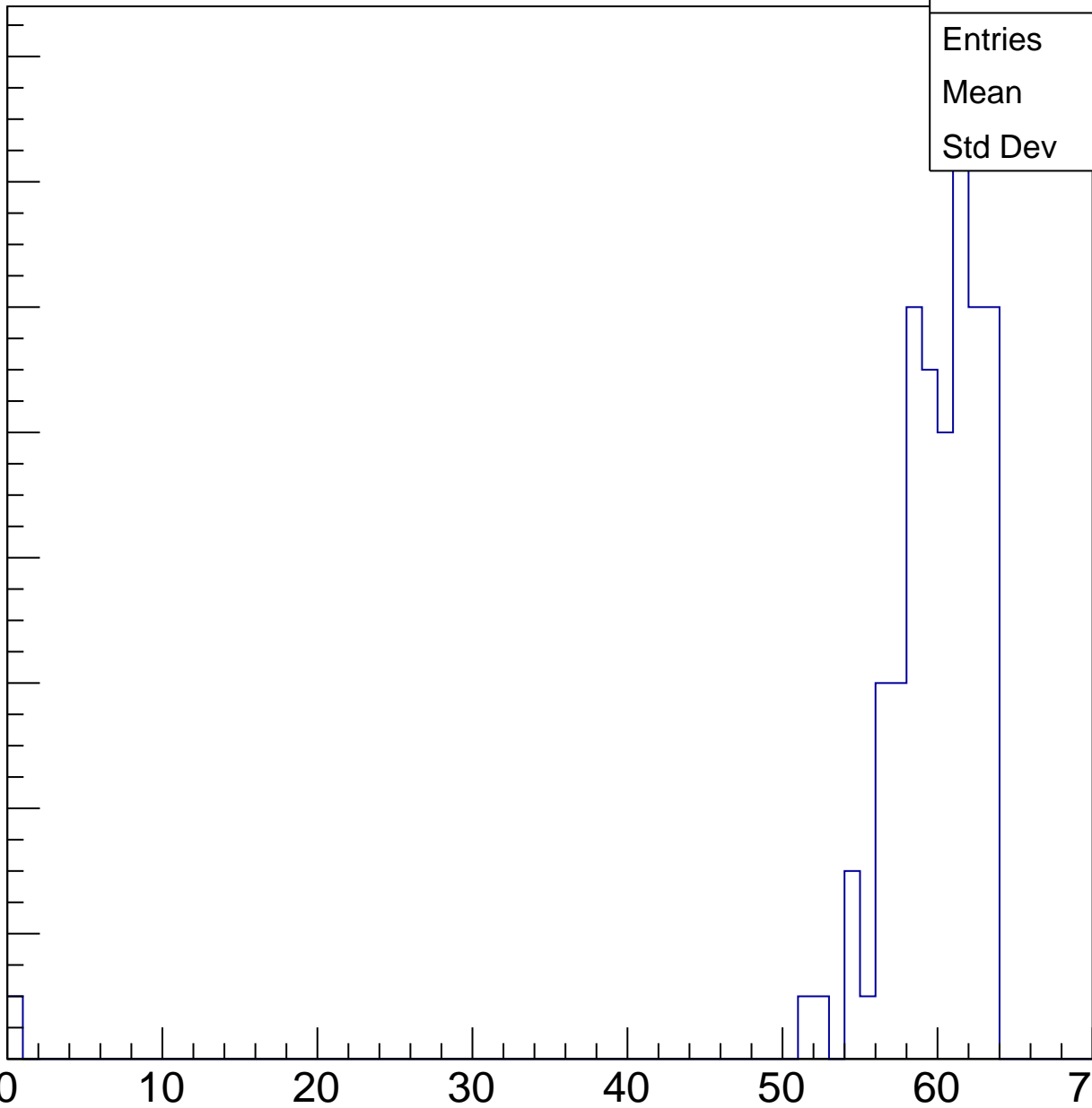
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.9
Std Dev	6.715

ampl

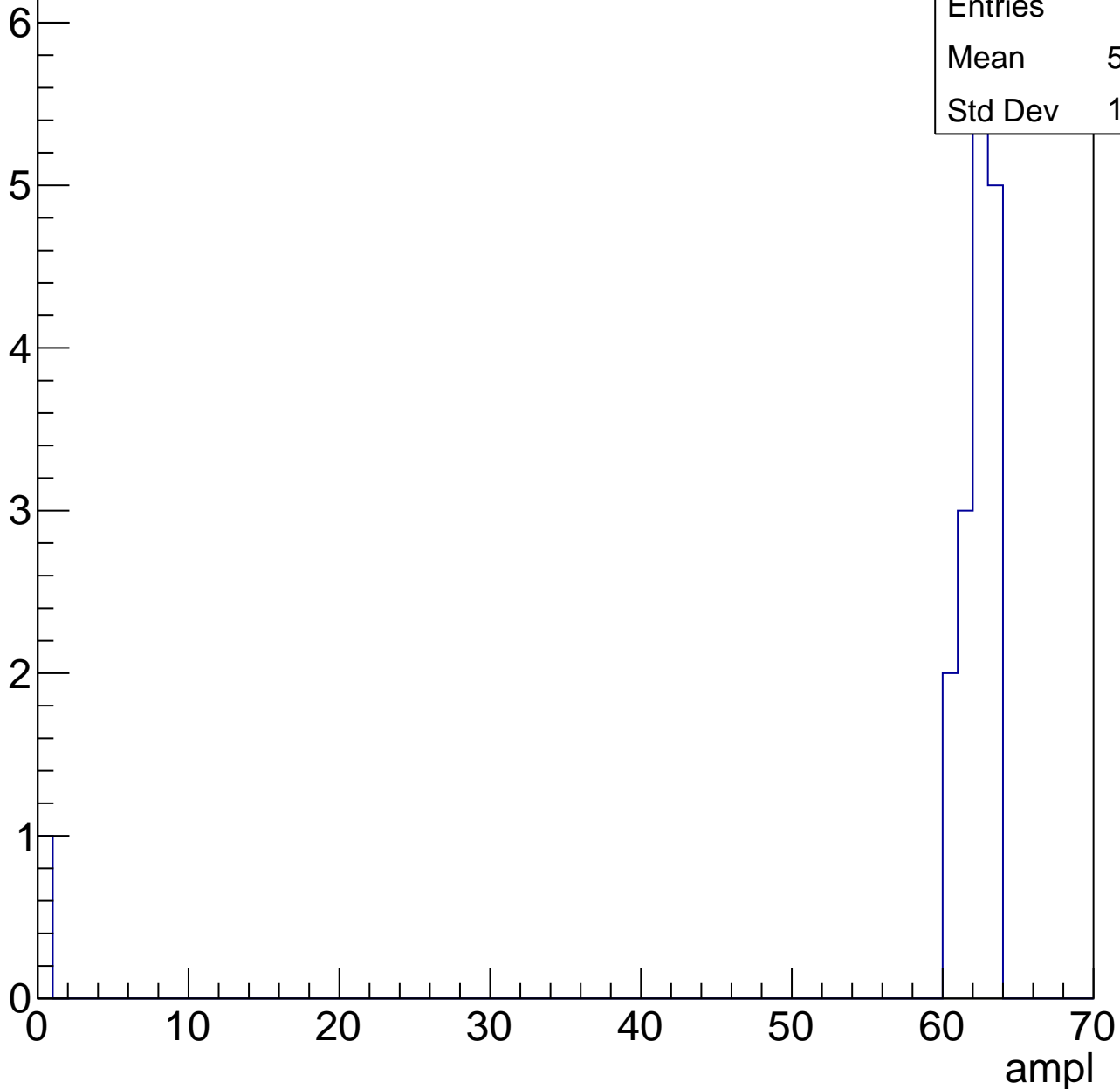


B0L101S, U18-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	58.24
Std Dev	14.59



B0L101S, U18-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

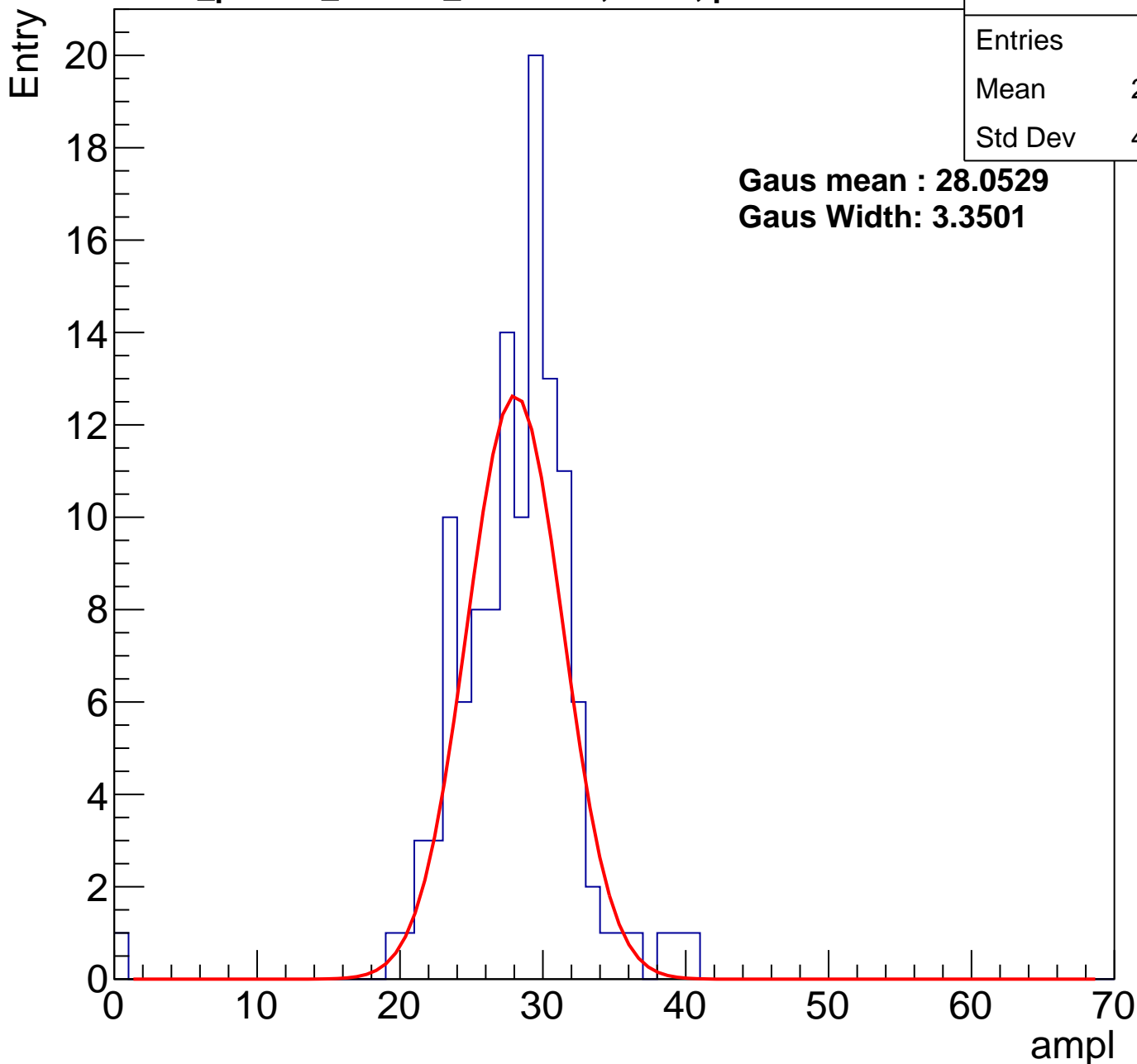
B0L101S, U18-ch81, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	27.63
Std Dev	4.474

Gaus mean : 28.0529

Gaus Width: 3.3501



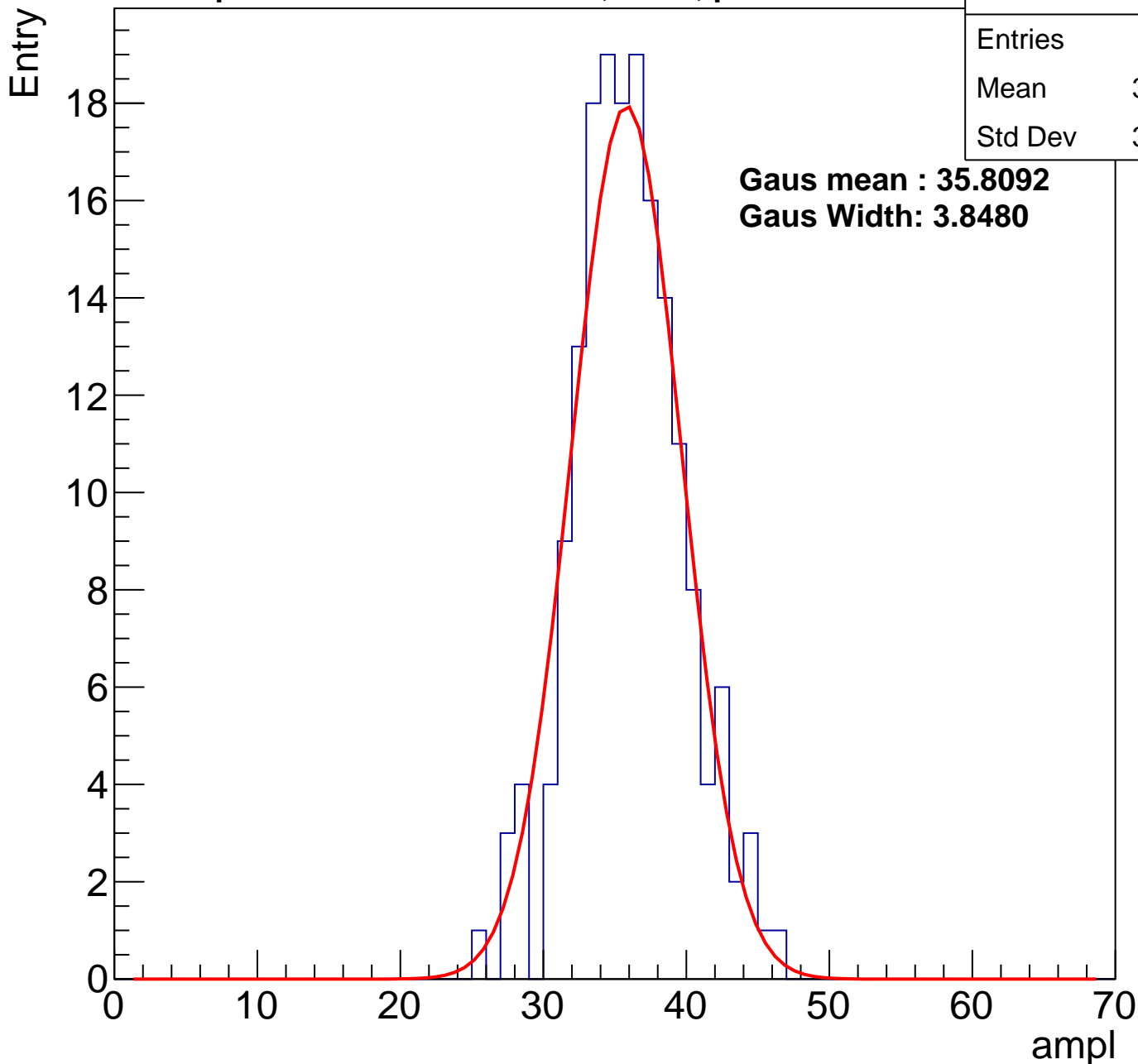
B0L101S, U18-ch81, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	35.55
Std Dev	3.793

Gaus mean : 35.8092

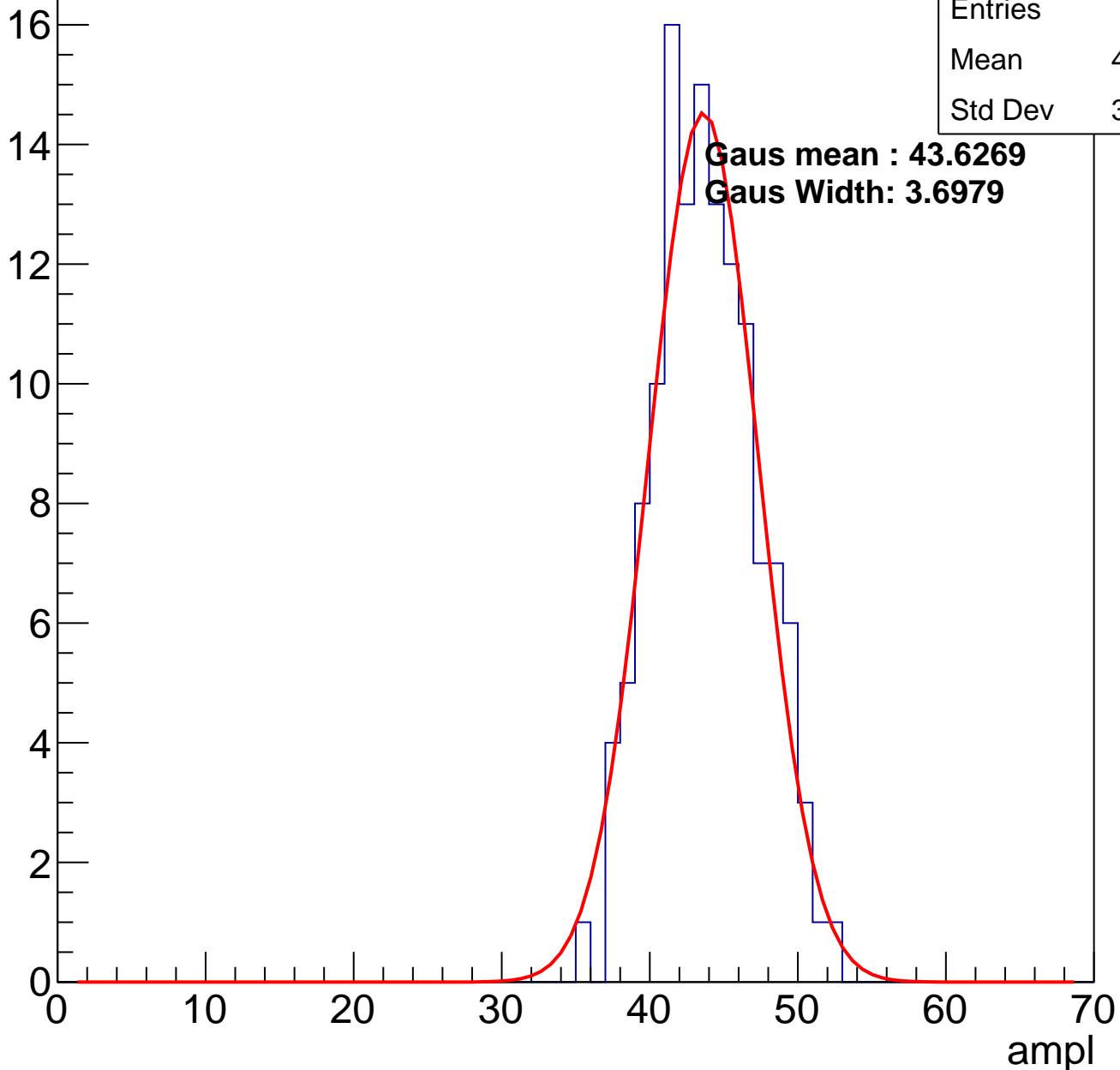
Gaus Width: 3.8480



B0L101S, U18-ch81, adc2

calib_packv5_042523_0143.root, FC#1, port C1

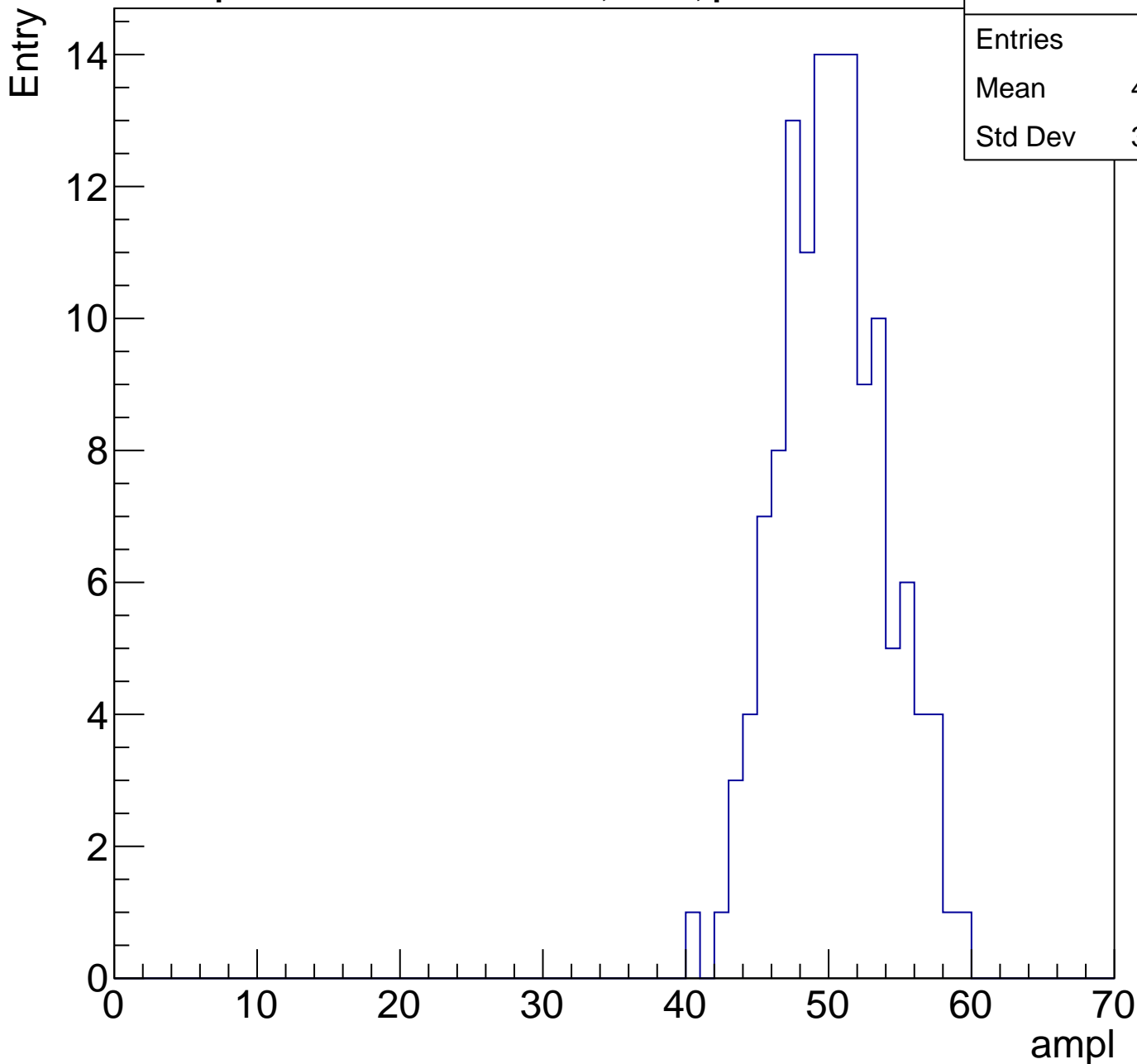
Entry



B0L101S, U18-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

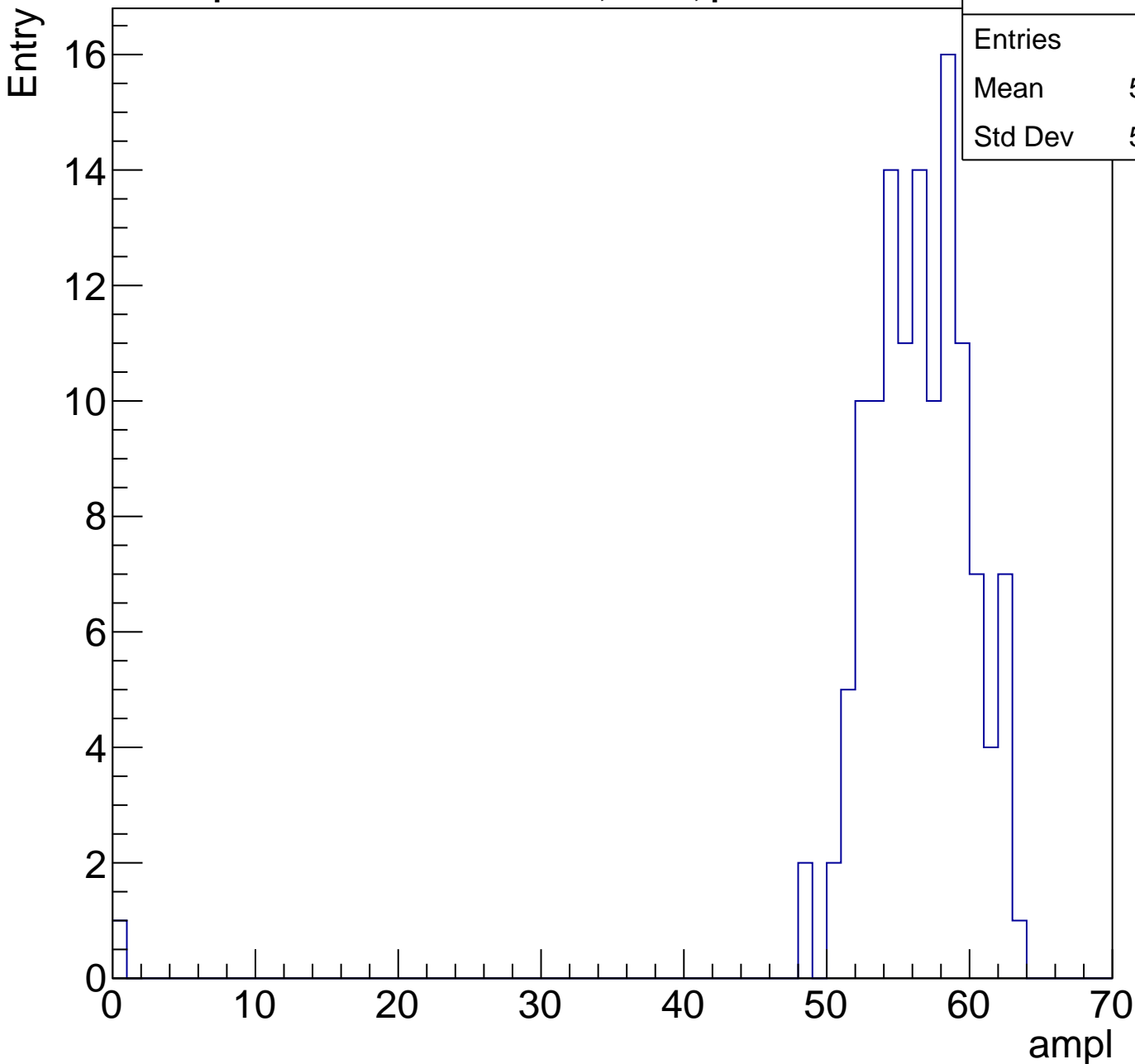
Entries	130
Mean	49.82
Std Dev	3.704



B0L101S, U18-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.63
Std Dev	5.965



B0L101S, U18-ch81, adc5

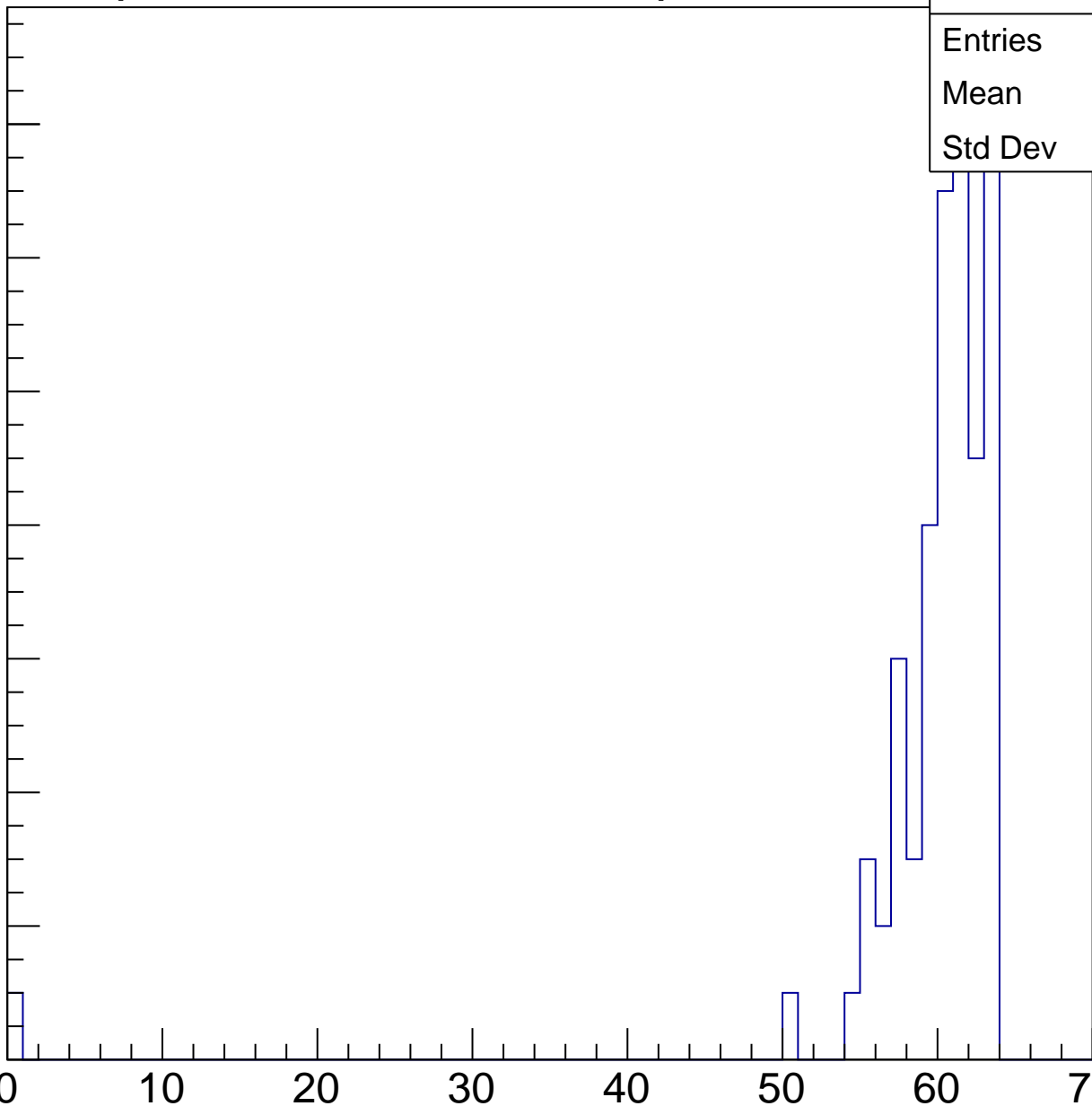
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	59.31
Std Dev	7.267

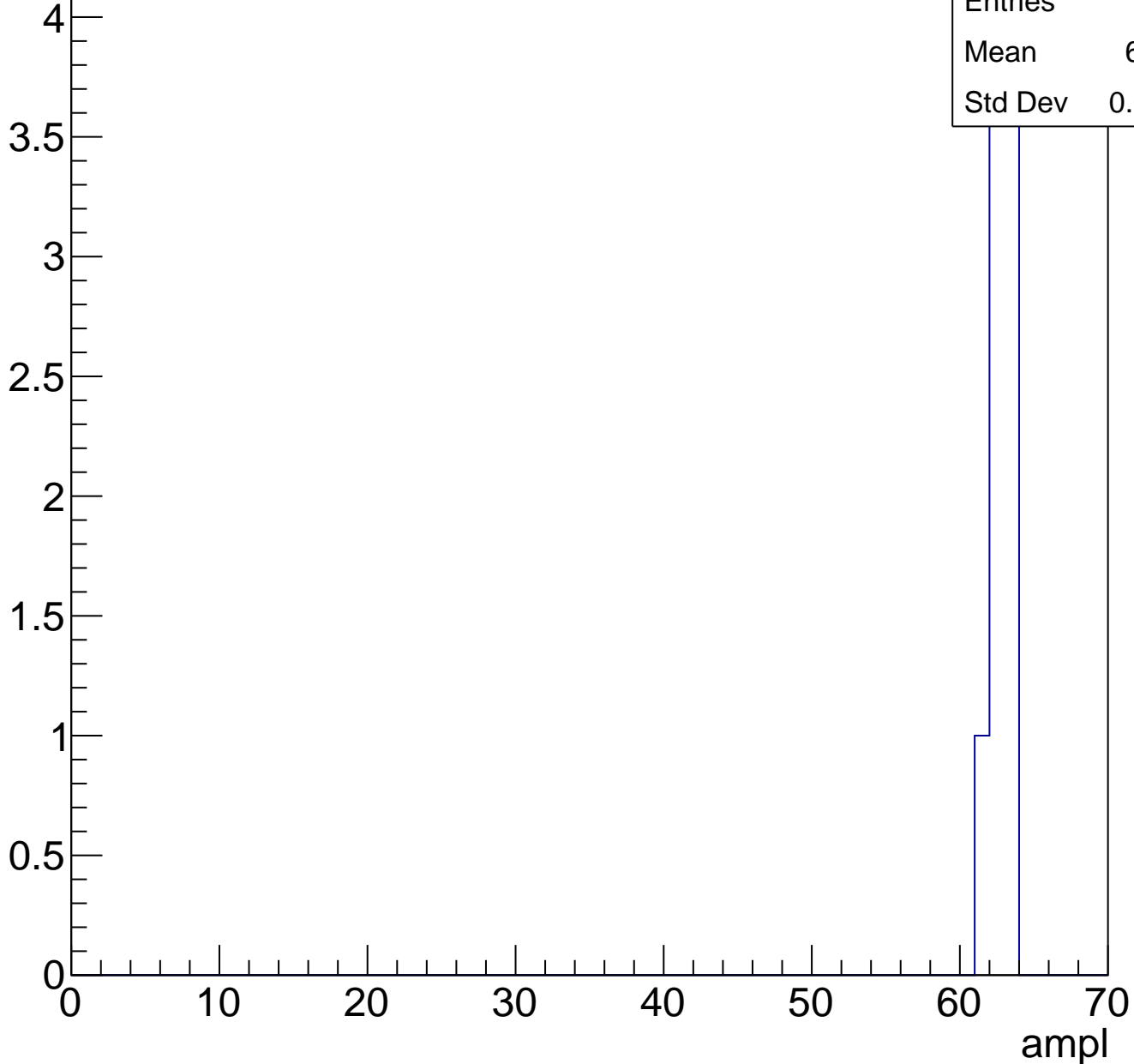
ampl



B0L101S, U18-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch82, adc0

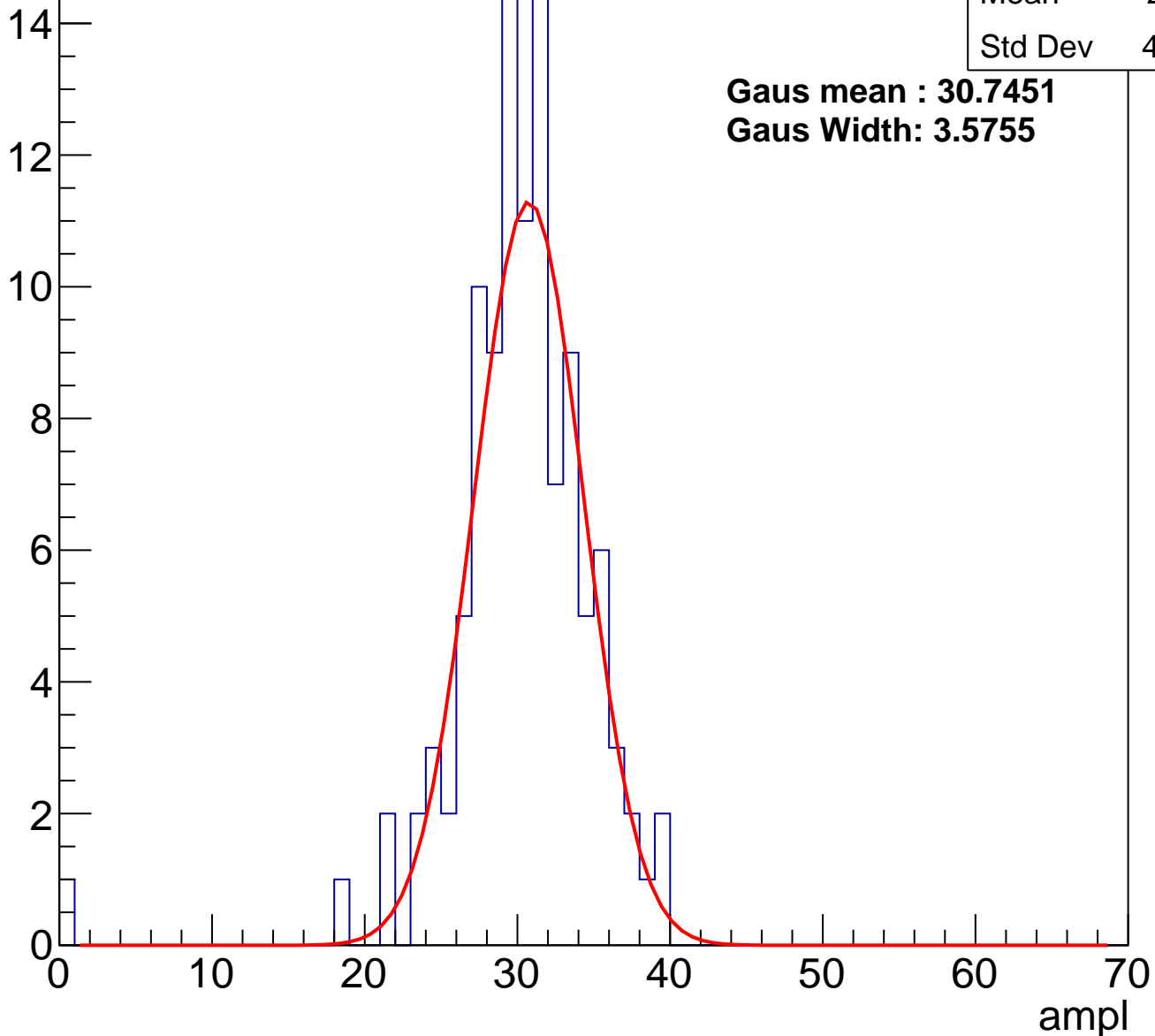
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	29.81
Std Dev	4.712

Gaus mean : 30.7451

Gaus Width: 3.5755

Entry



B0L101S, U18-ch82, adc1

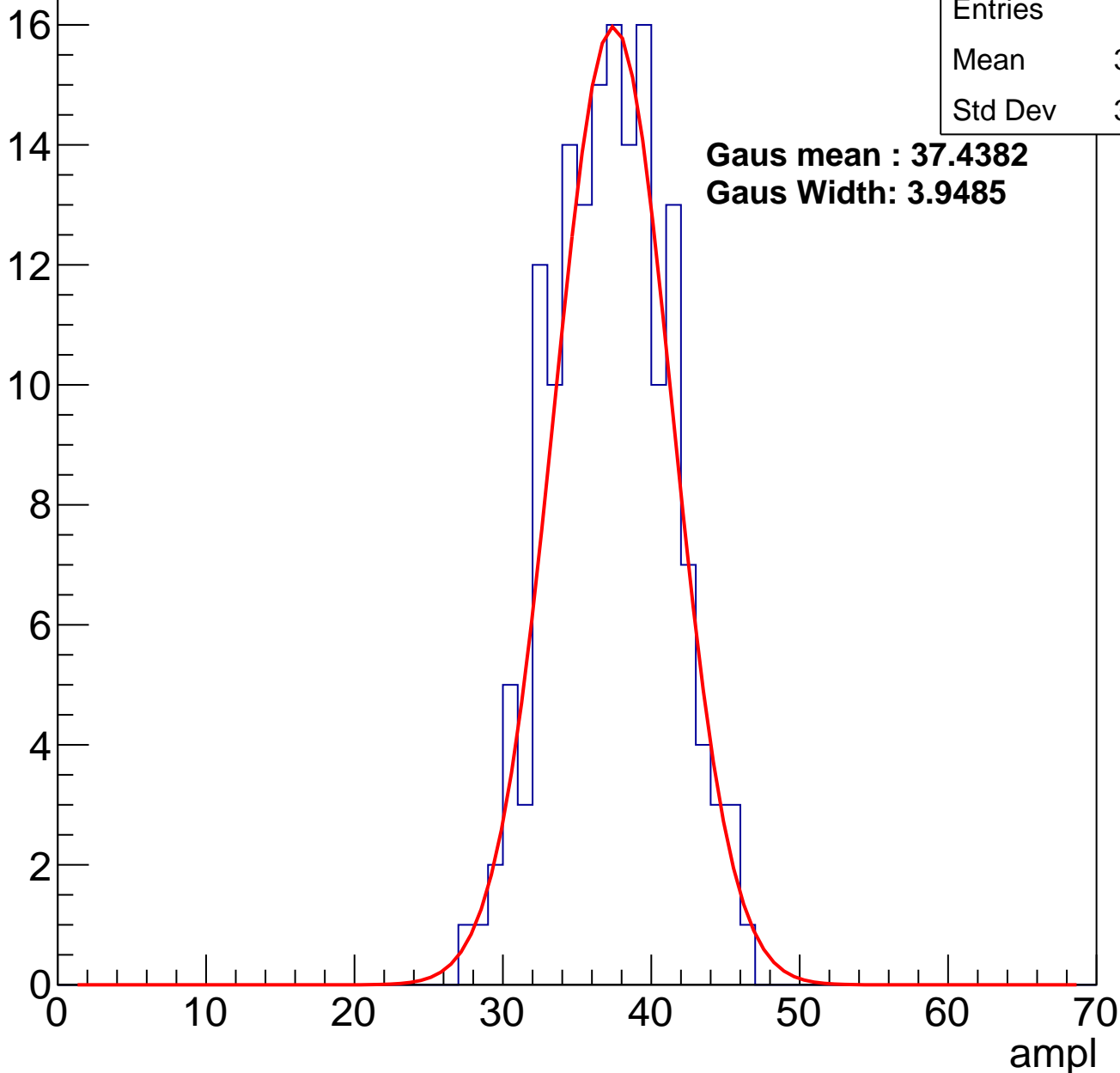
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	36.82
Std Dev	3.857

Gaus mean : 37.4382

Gaus Width: 3.9485

Entry



B0L101S, U18-ch82, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

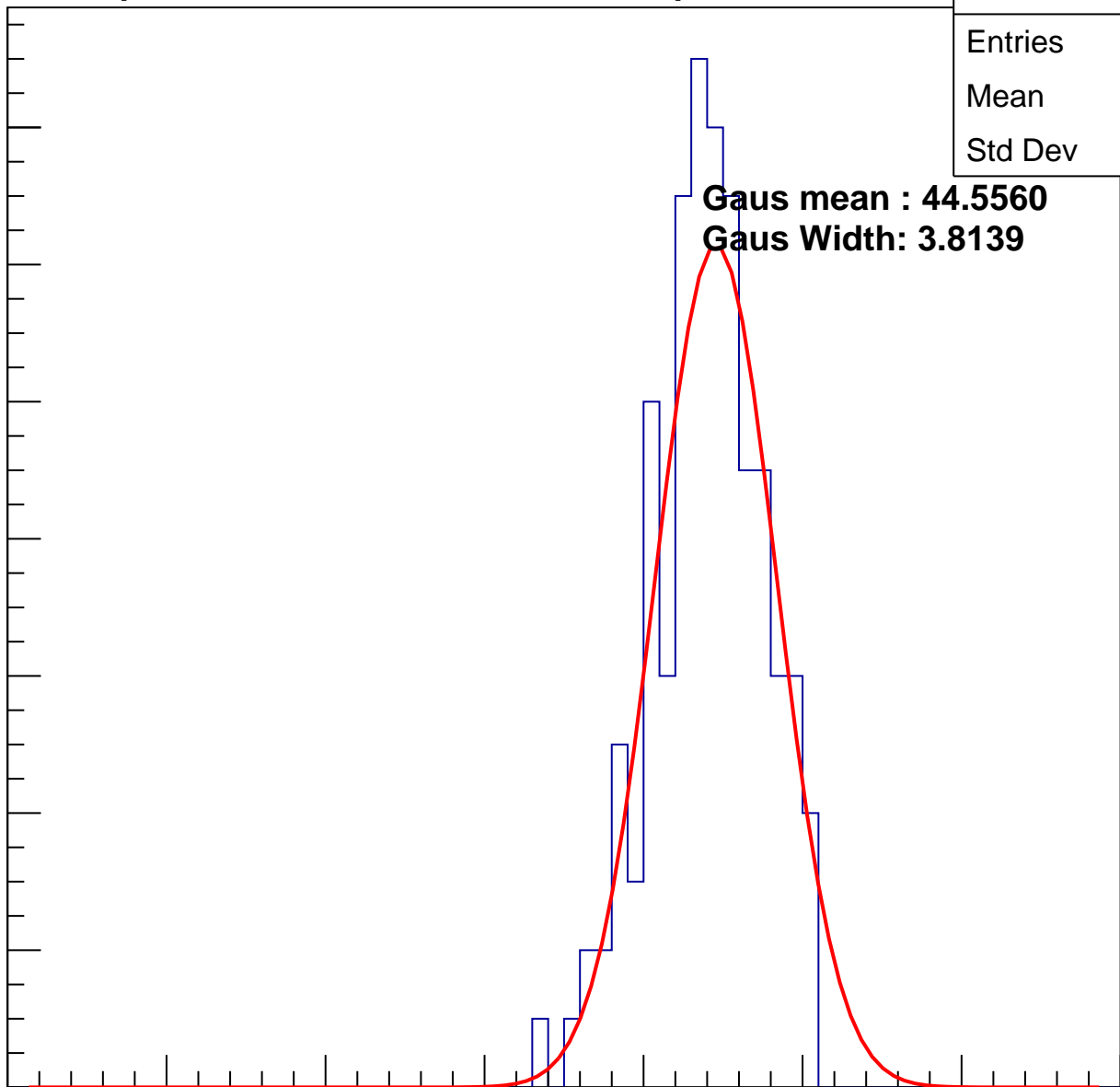
Entries	119
Mean	43.51
Std Dev	3.498

Gaus mean : 44.5560

Gaus Width: 3.8139

0 10 20 30 40 50 60 70

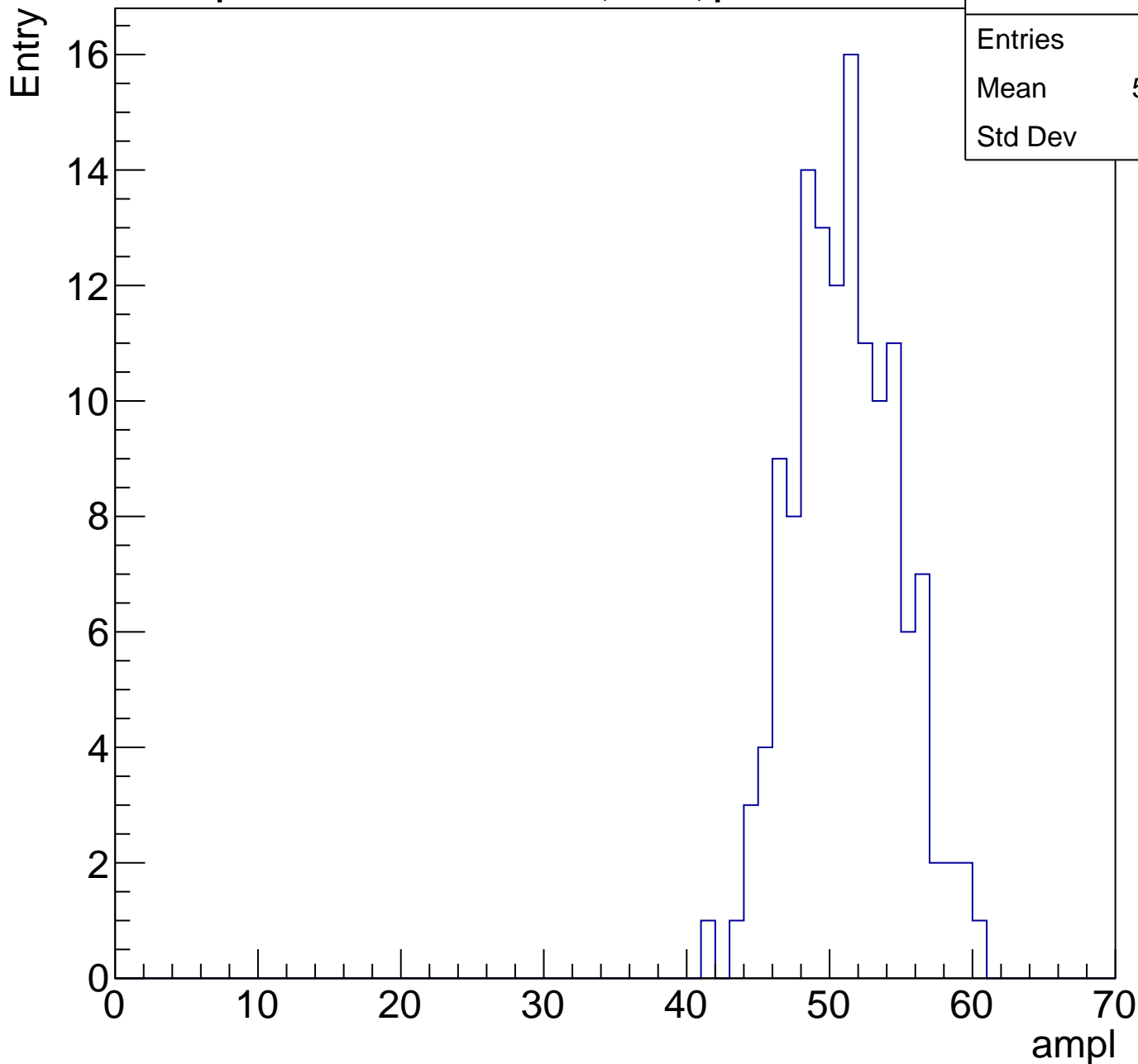
ampl



B0L101S, U18-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	50.65
Std Dev	3.66

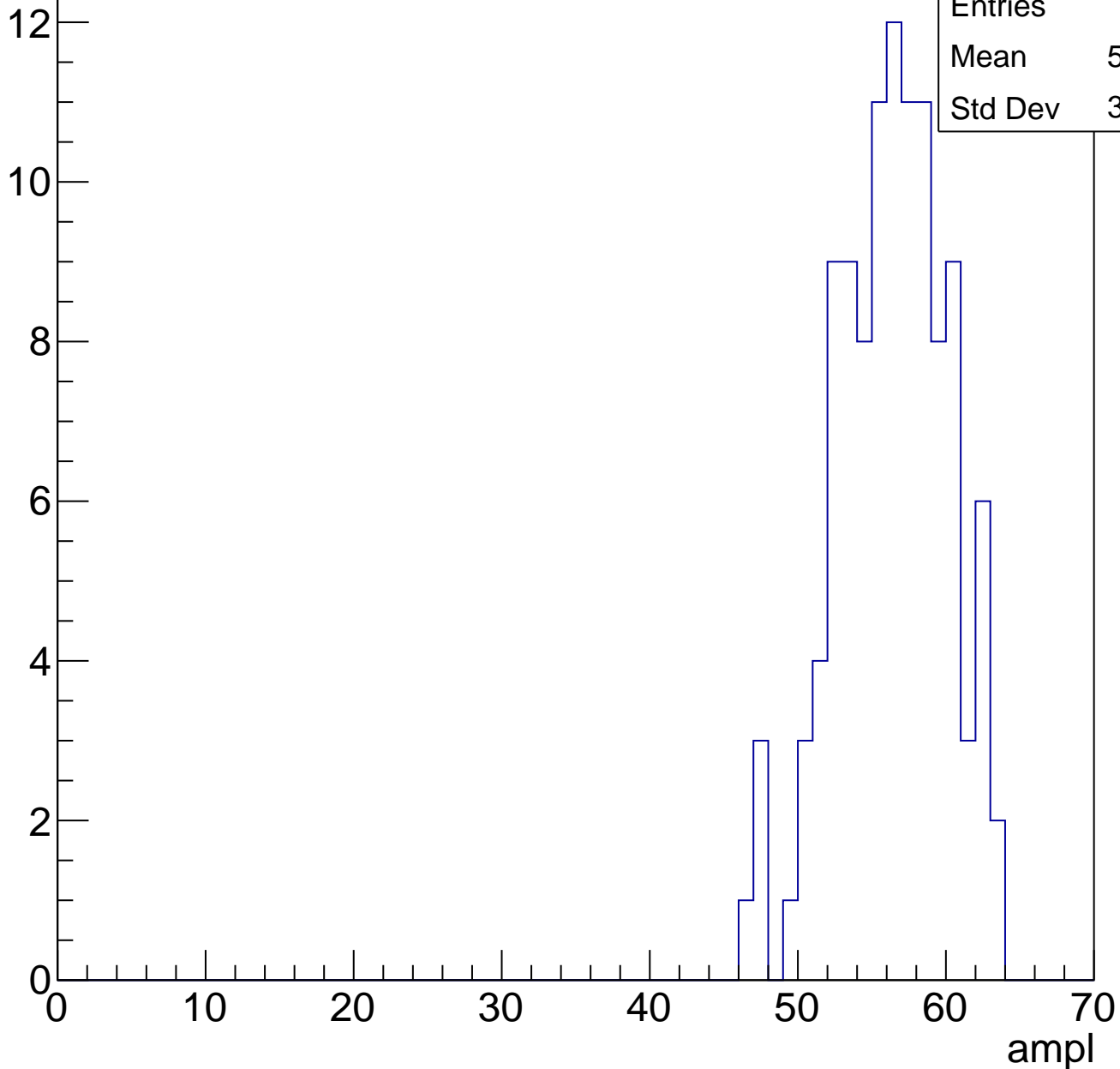


B0L101S, U18-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	55.87
Std Dev	3.708

Entry



B0L101S, U18-ch82, adc5

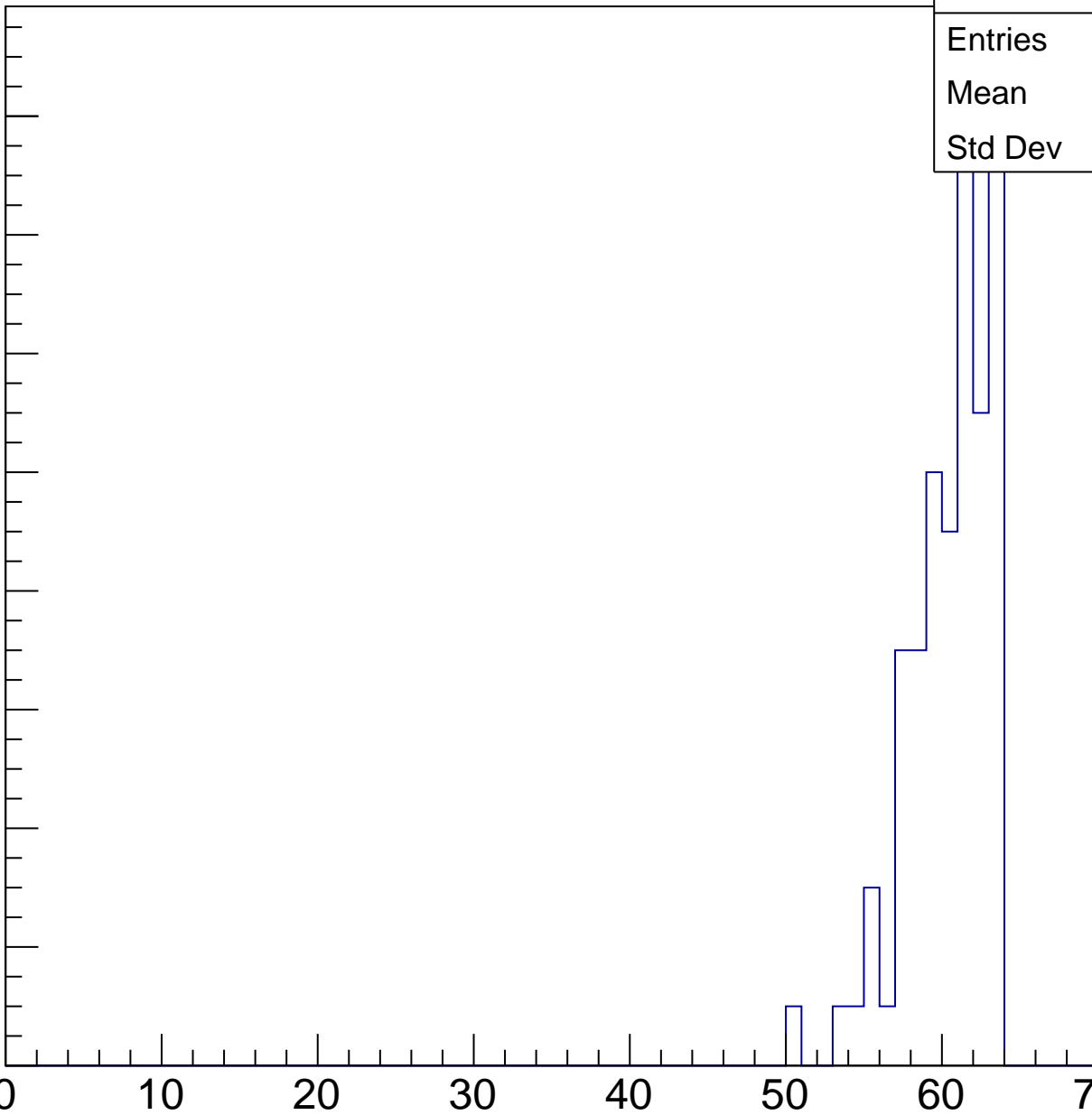
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	60.02
Std Dev	2.65

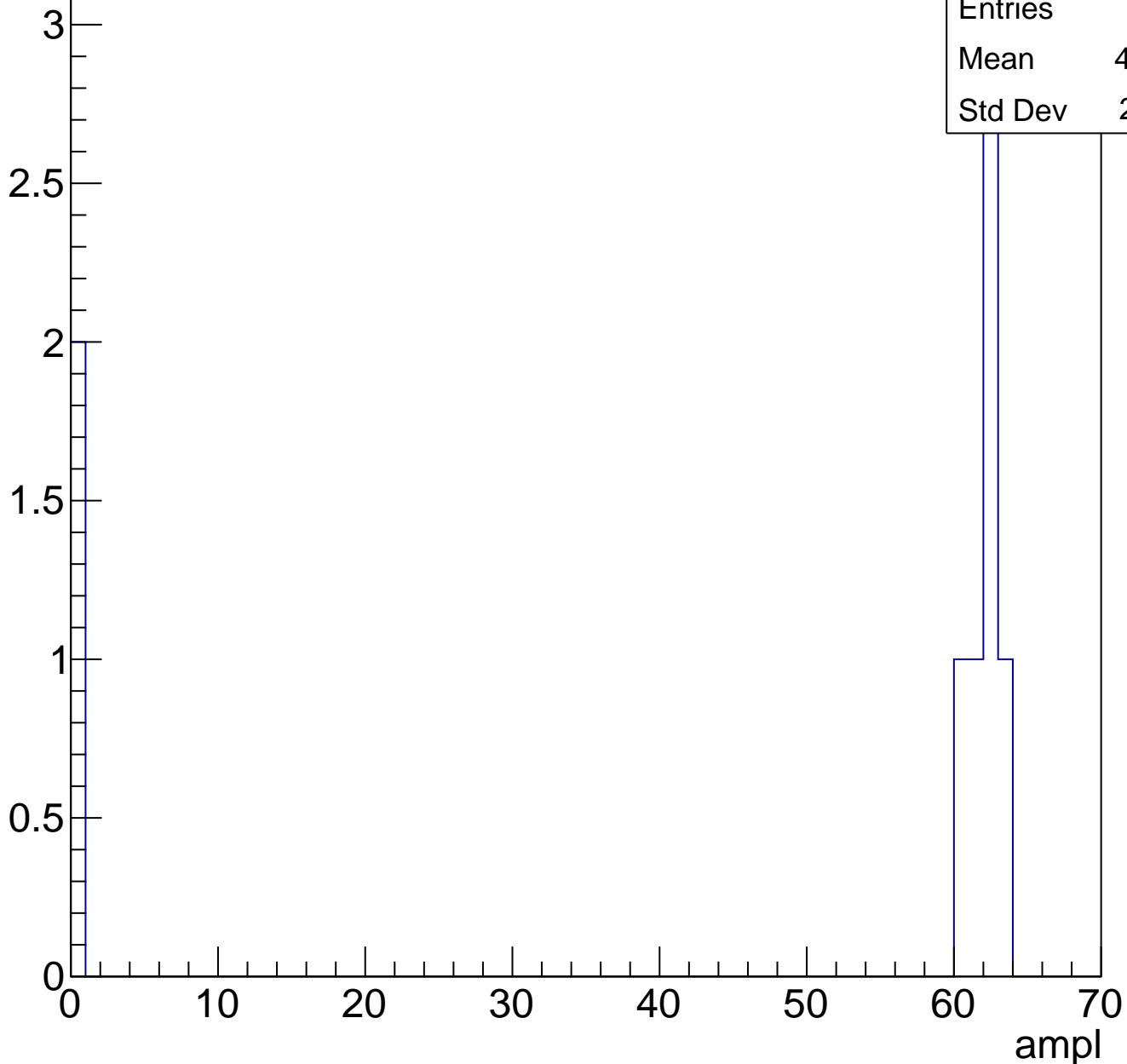
ampl



B0L101S, U18-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch83, adc0

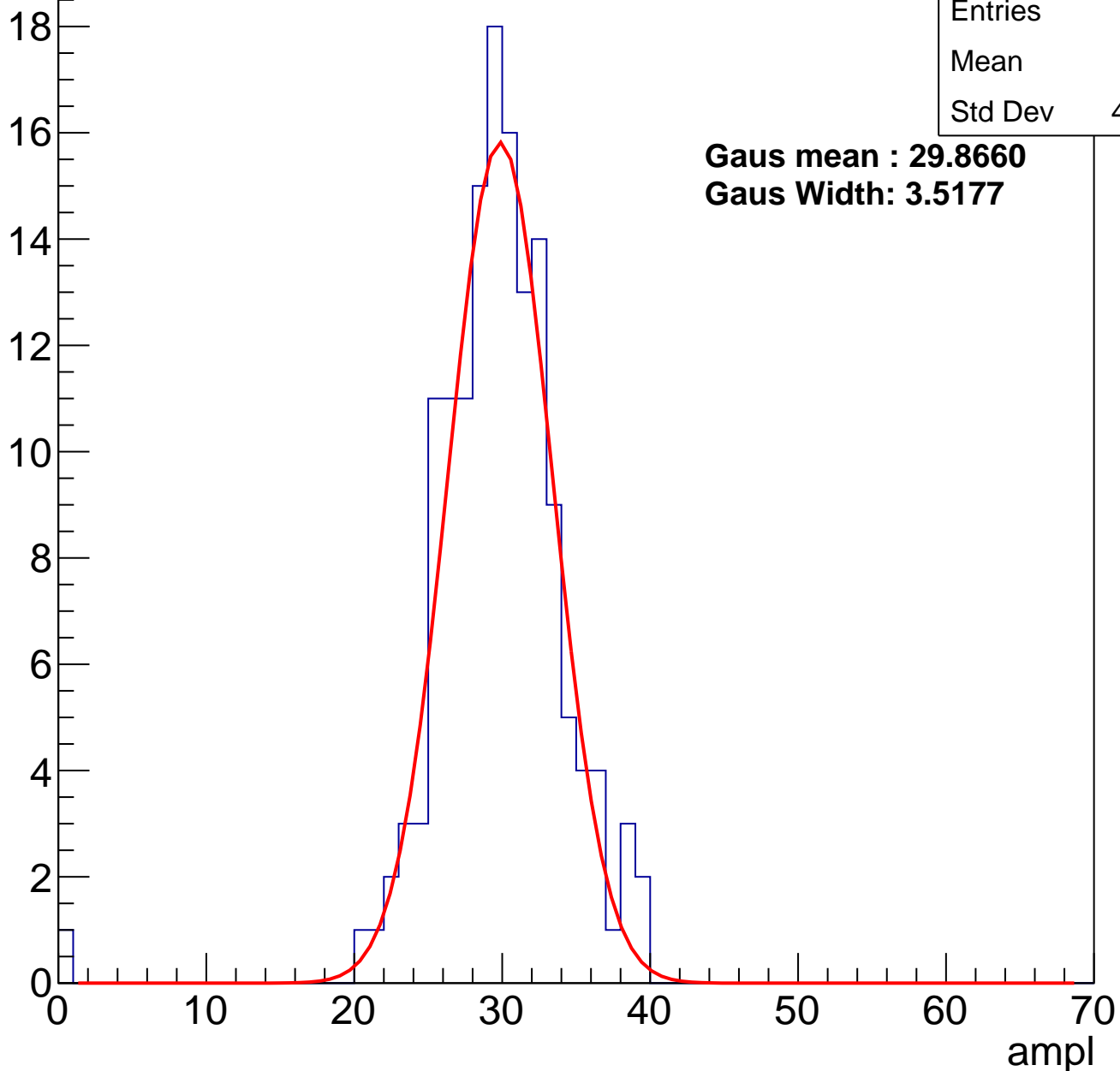
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	29.3
Std Dev	4.425

Gaus mean : 29.8660

Gaus Width: 3.5177

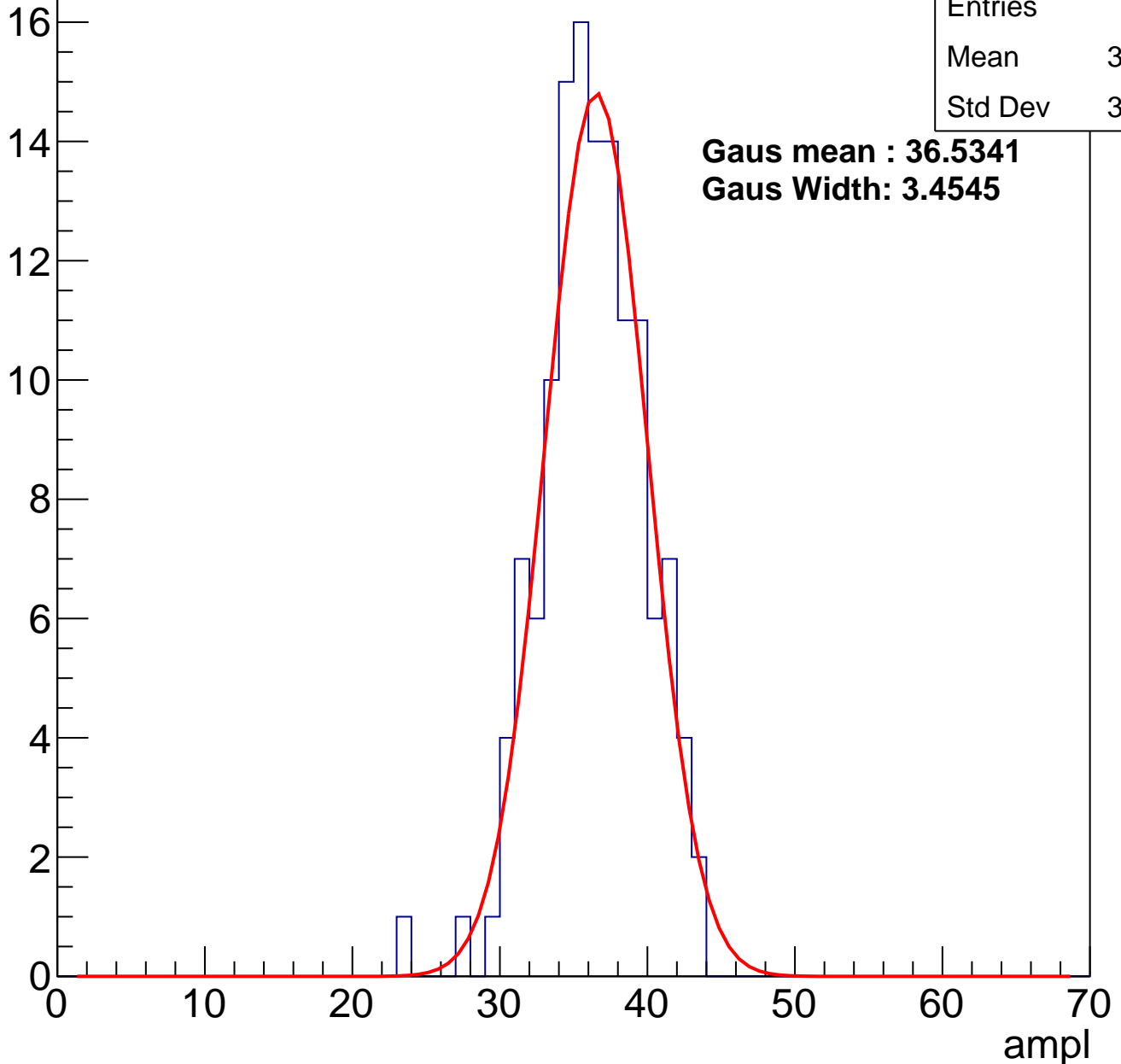
Entry



B0L101S, U18-ch83, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

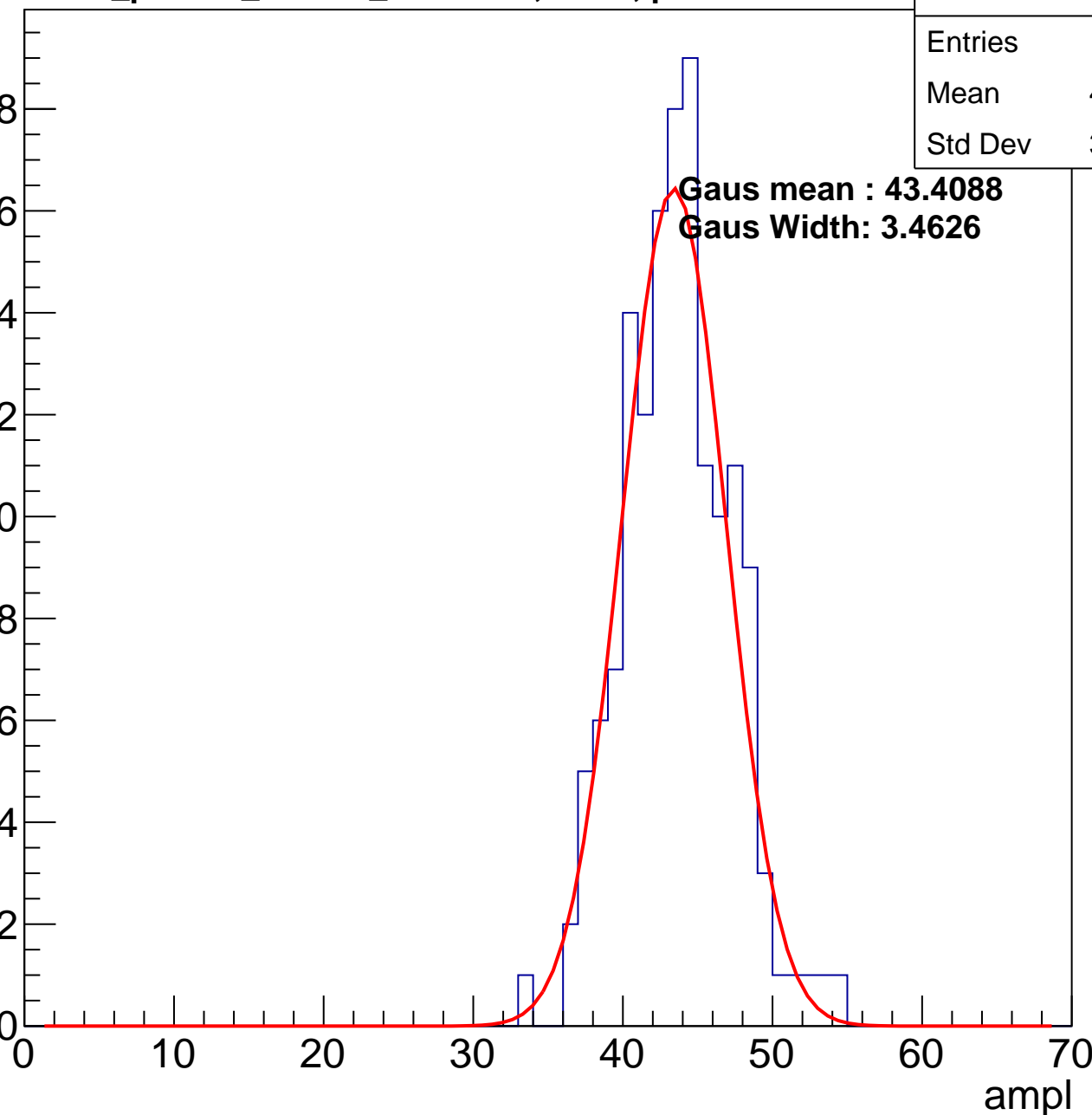
Entry

18
16
14
12
10
8
6
4
2
0

Entries	149
Mean	43.19
Std Dev	3.572

Gaus mean : 43.4088

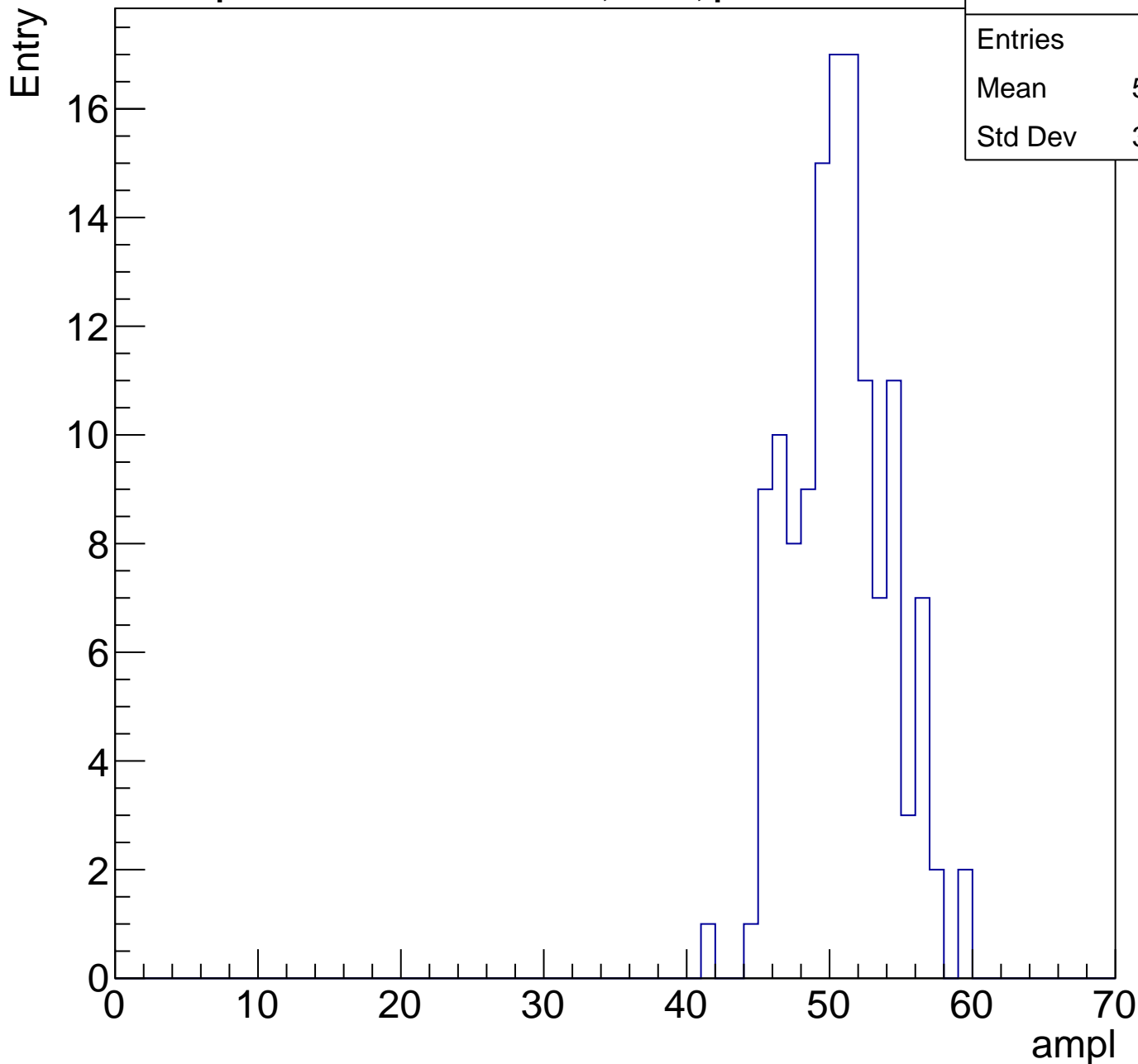
Gaus Width: 3.4626



B0L101S, U18-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	50.28
Std Dev	3.399

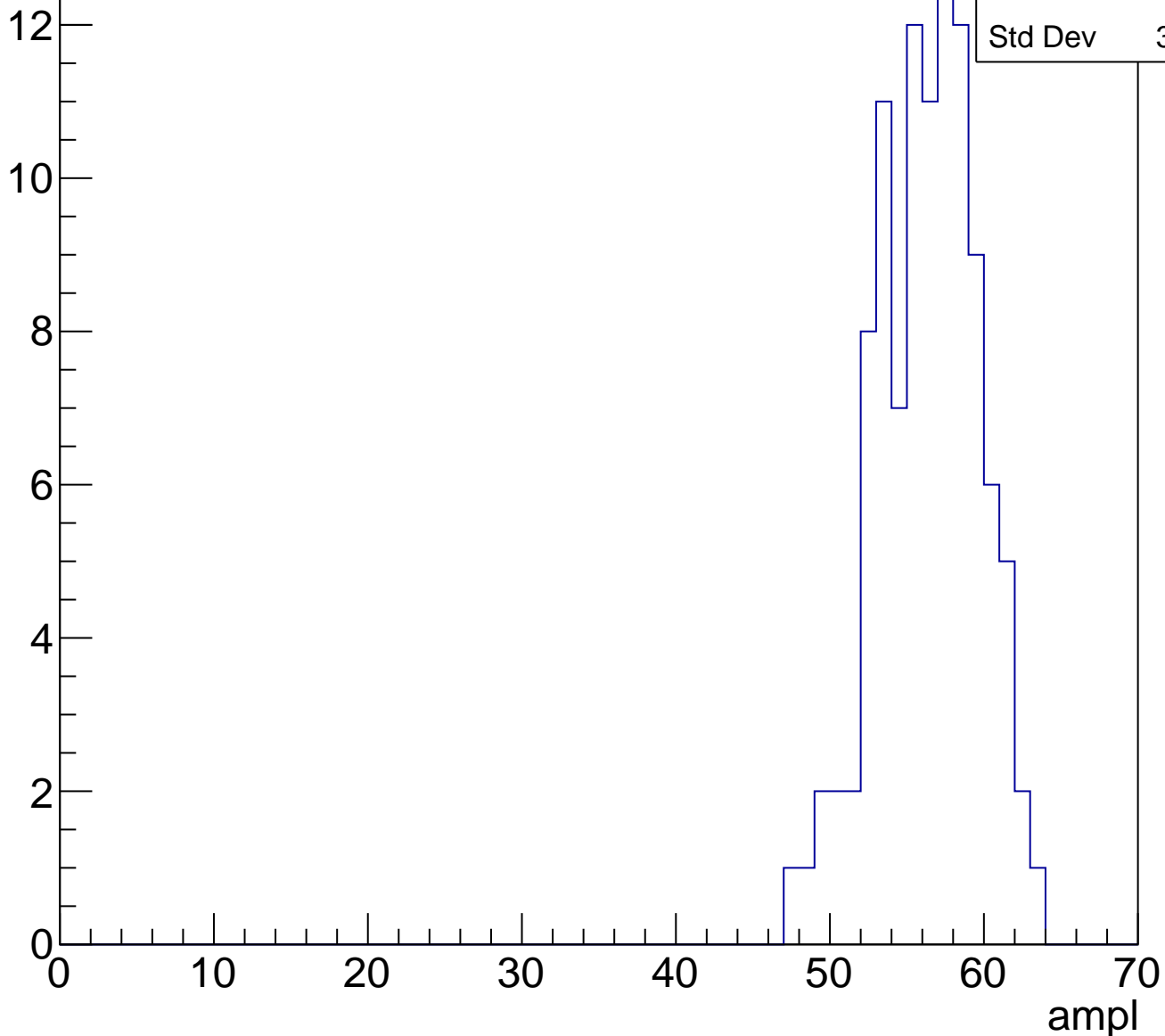


B0L101S, U18-ch83, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	55.89
Std Dev	3.258

Entry



B0L101S, U18-ch83, adc5

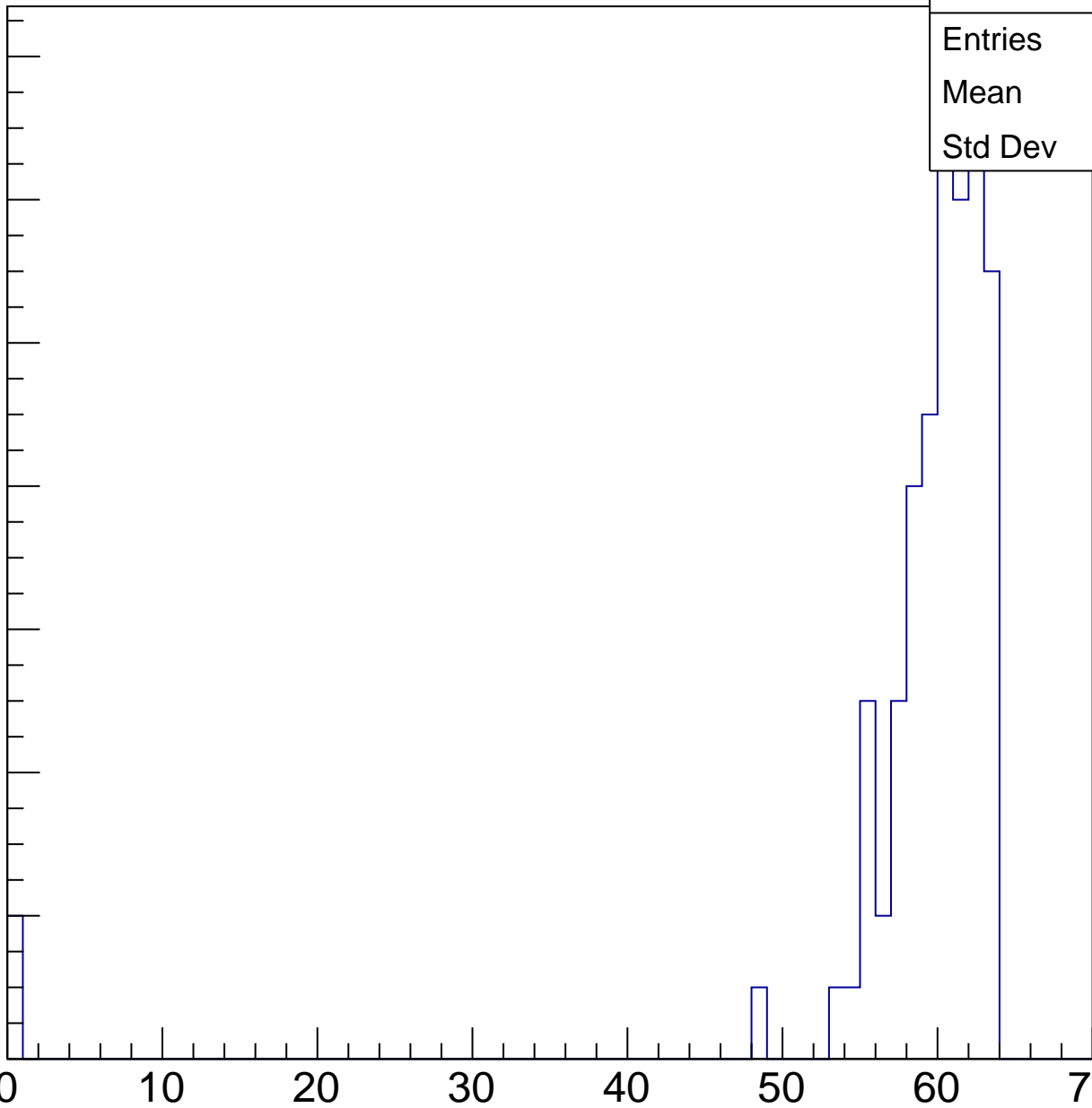
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	84
Mean	58.25
Std Dev	9.498

ampl

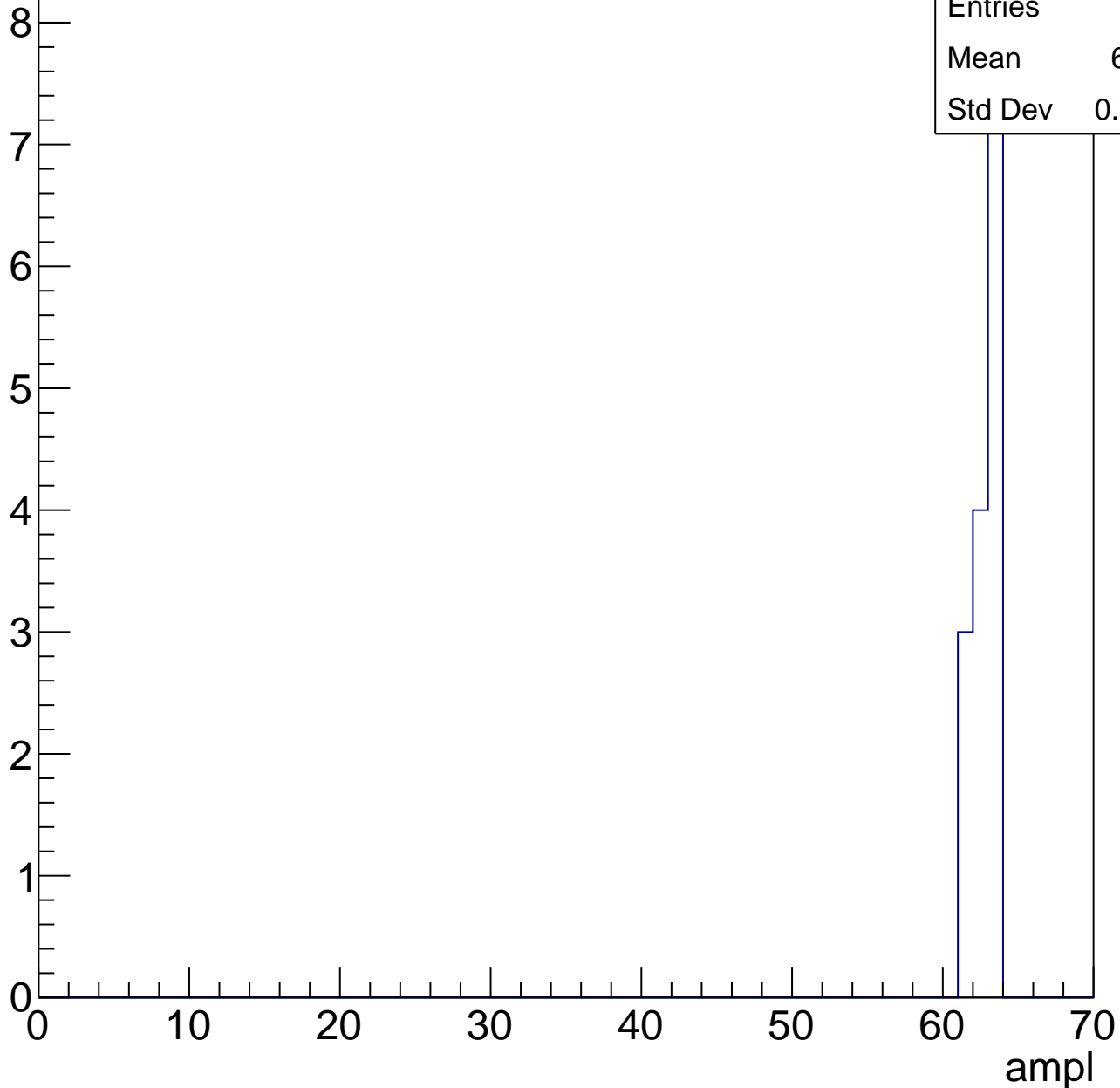


B0L101S, U18-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

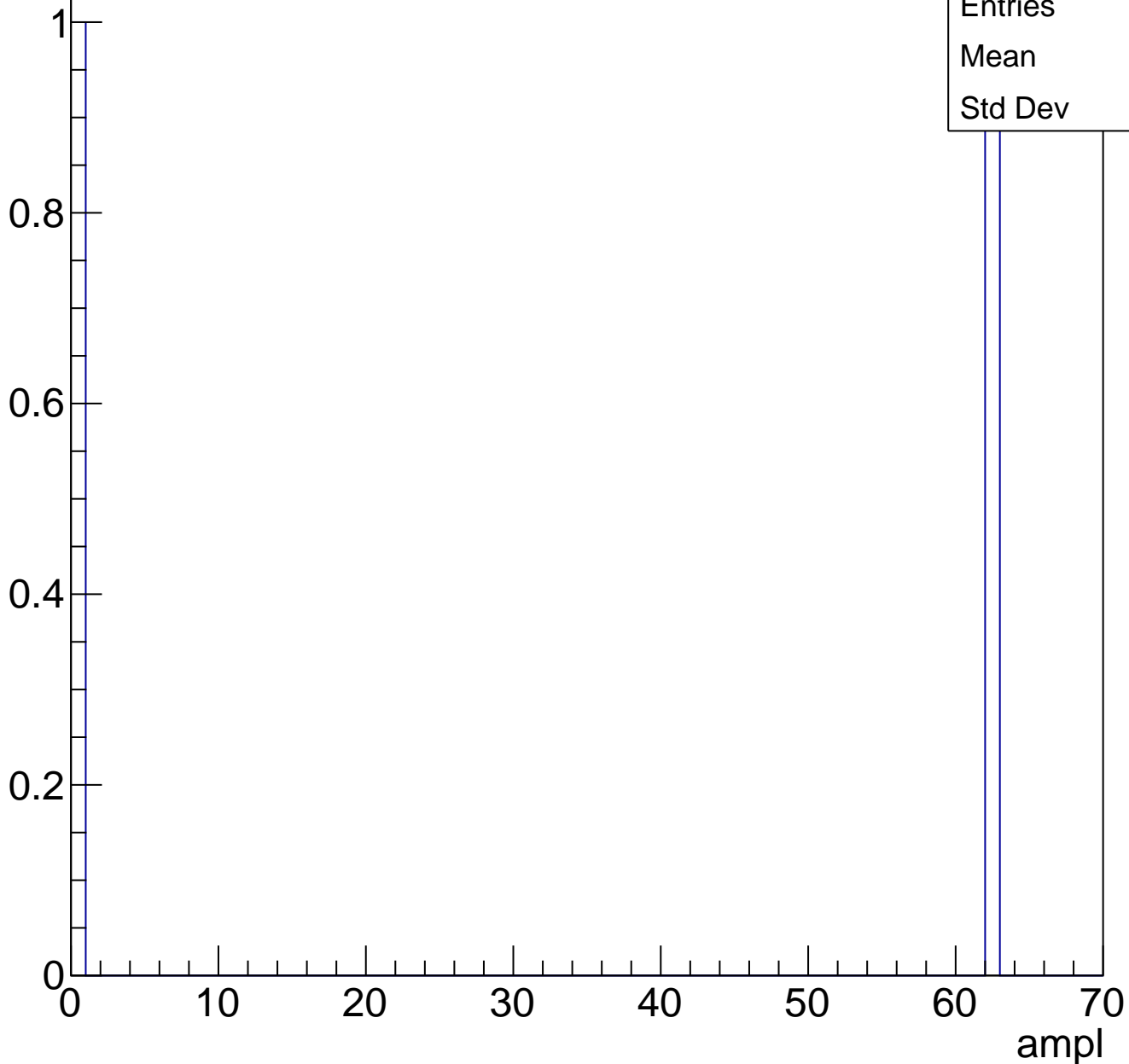
Entries	15
Mean	62.33
Std Dev	0.7888



B0L101S, U18-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch84, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	28.07
Std Dev	3.316

Gaus mean : 28.6140

Gaus Width: 3.3180

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

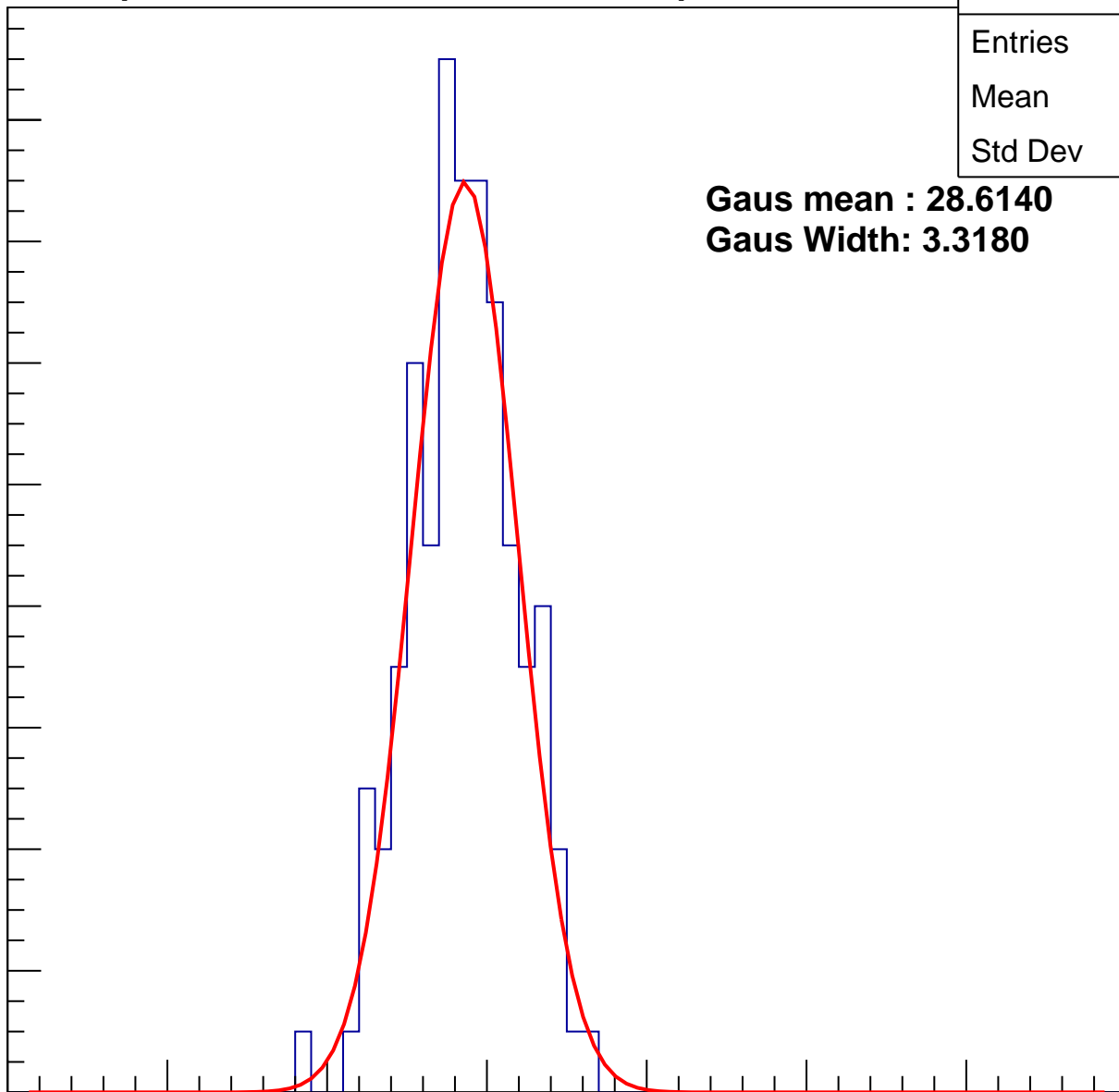
40

50

60

70

ampl



B0L101S, U18-ch84, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	34.93
Std Dev	3.546

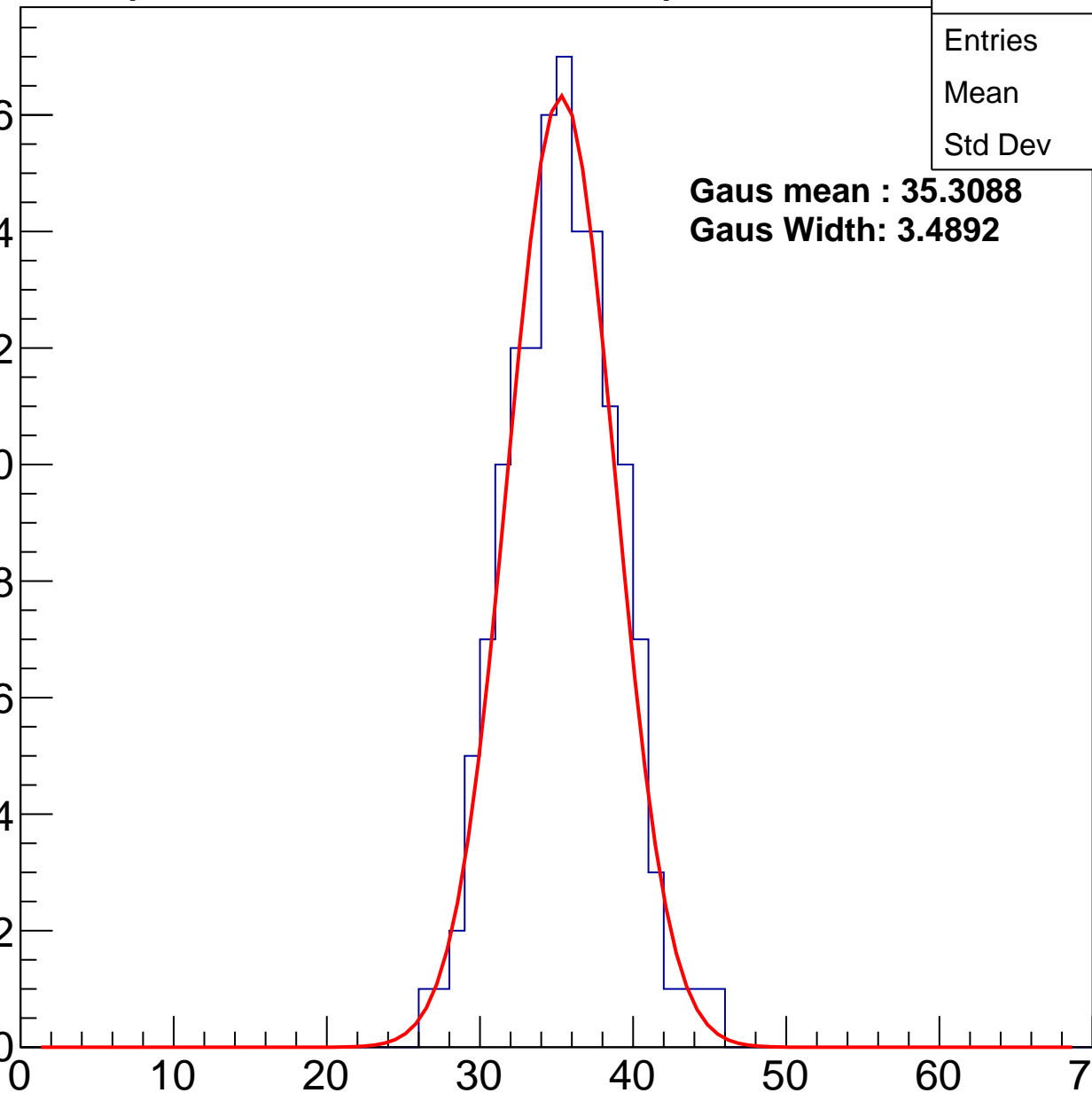
Gaus mean : 35.3088

Gaus Width: 3.4892

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch84, adc2

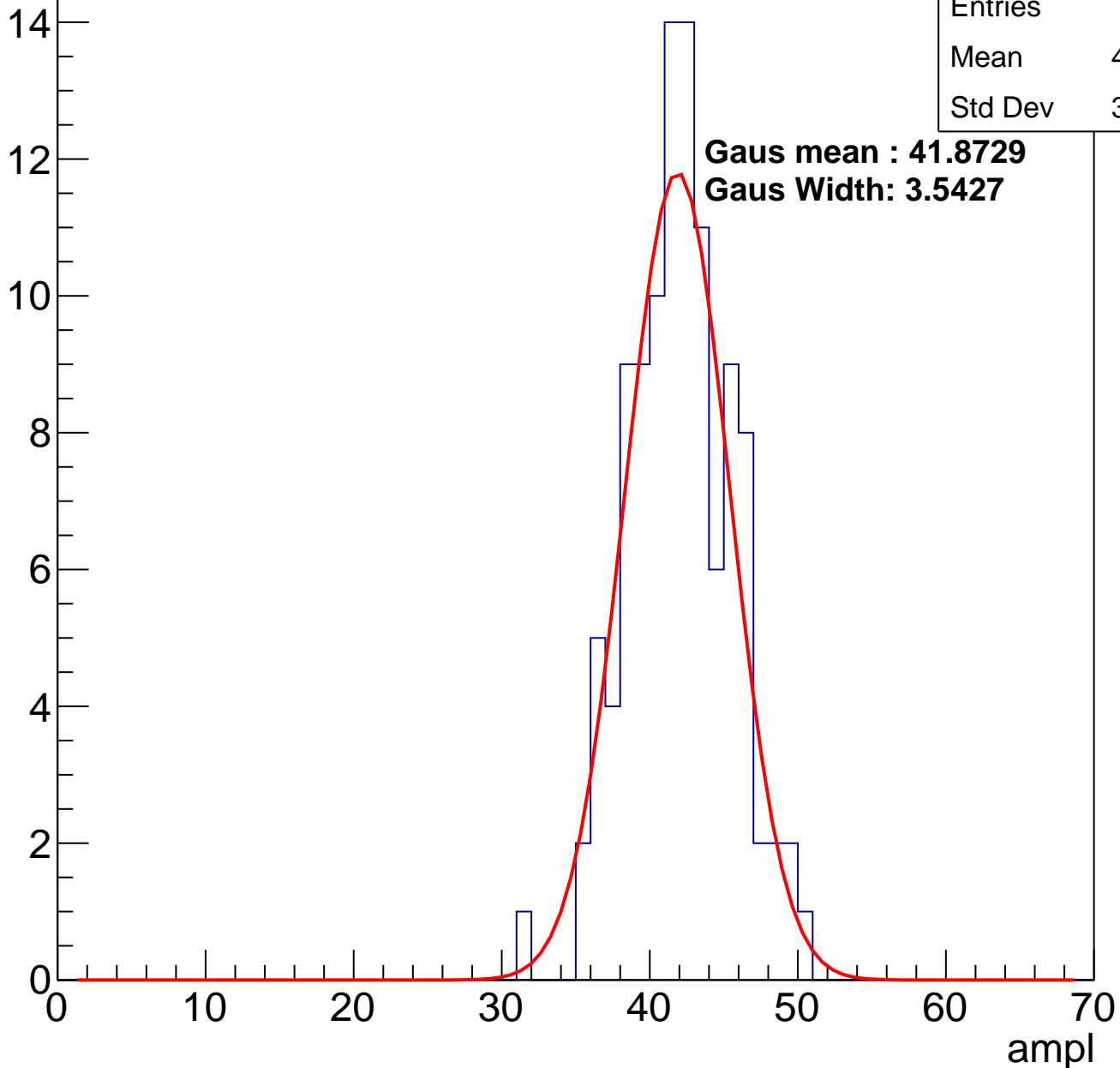
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	41.58
Std Dev	3.436

Gaus mean : 41.8729

Gaus Width: 3.5427

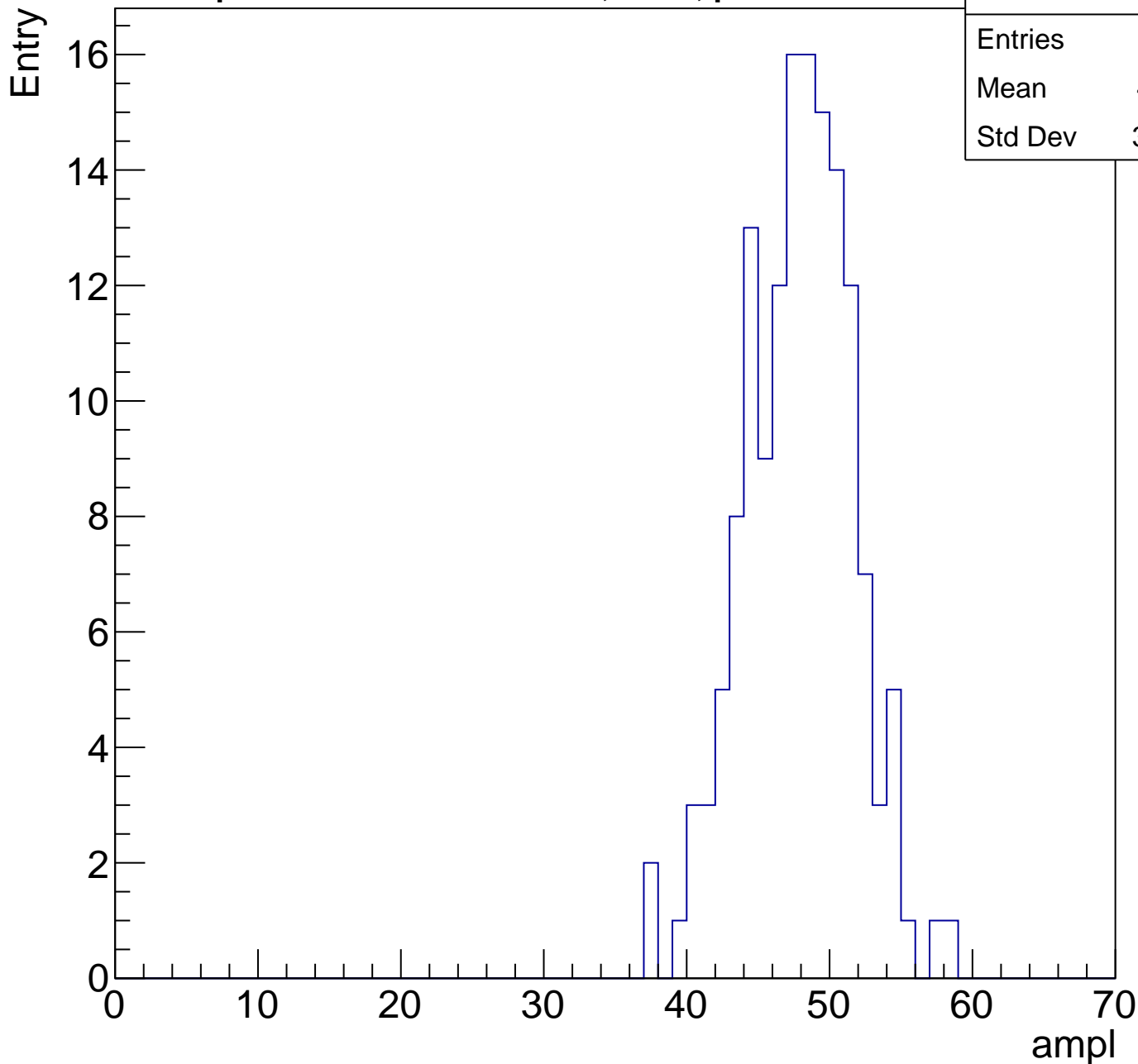
Entry



B0L101S, U18-ch84, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	47.41
Std Dev	3.797



B0L101S, U18-ch84, adc4

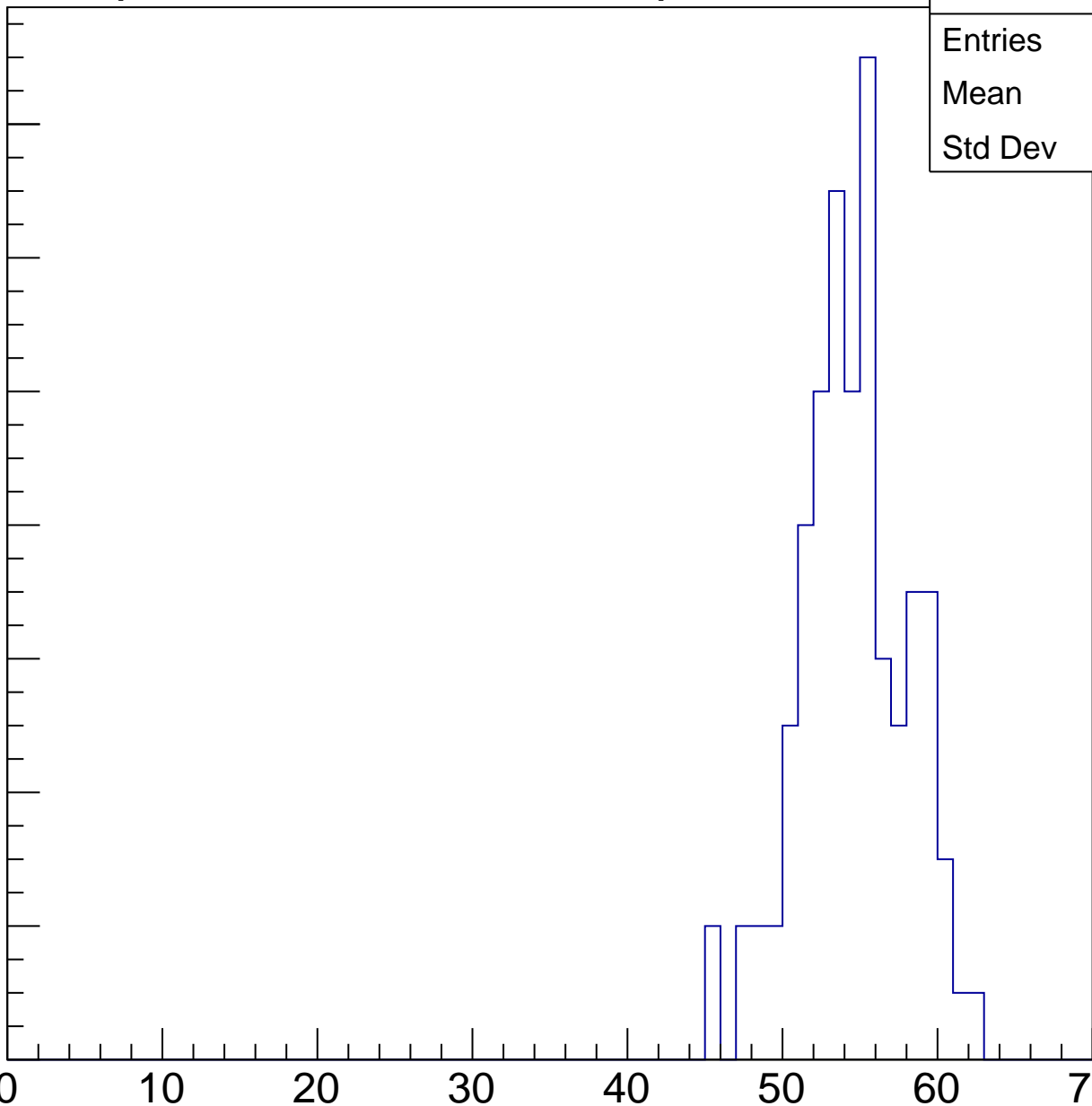
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	54.07
Std Dev	3.45

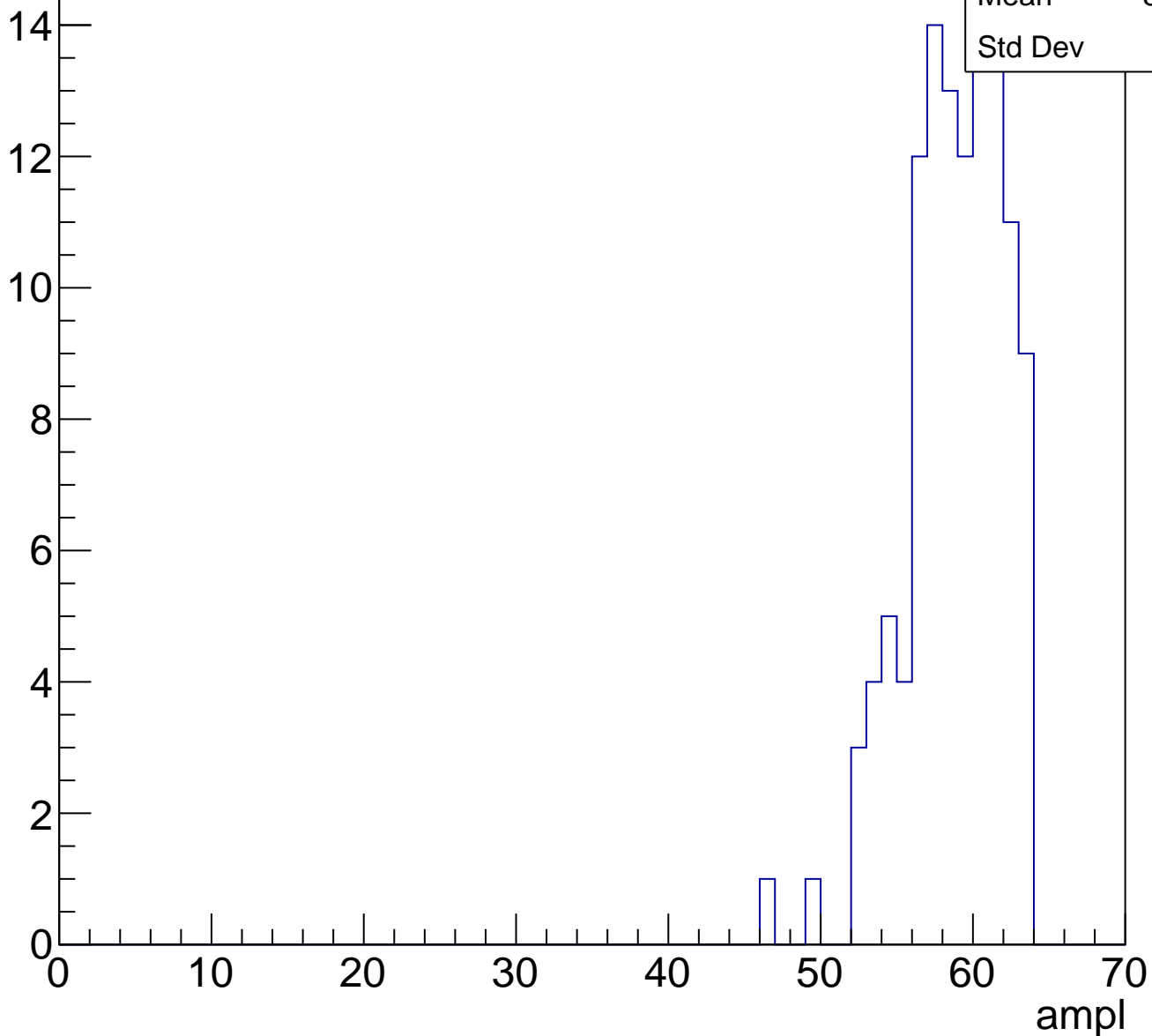
ampl



B0L101S, U18-ch84, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

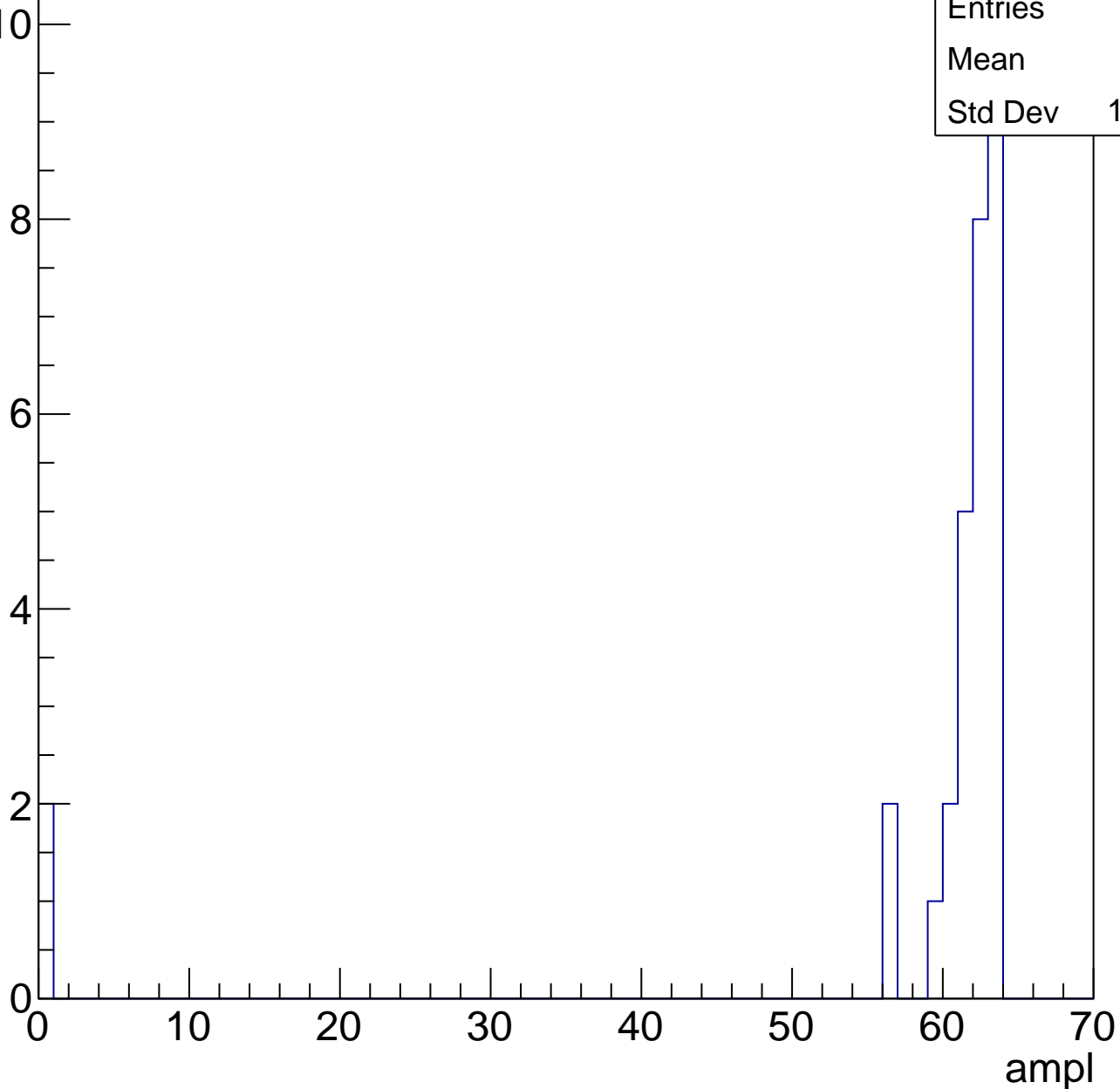


B0L101S, U18-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	30
Mean	57.4
Std Dev	15.45



B0L101S, U18-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch85, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	28.77
Std Dev	6.367

Gaus mean : 30.2374

Gaus Width: 4.0462

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch85, adc1

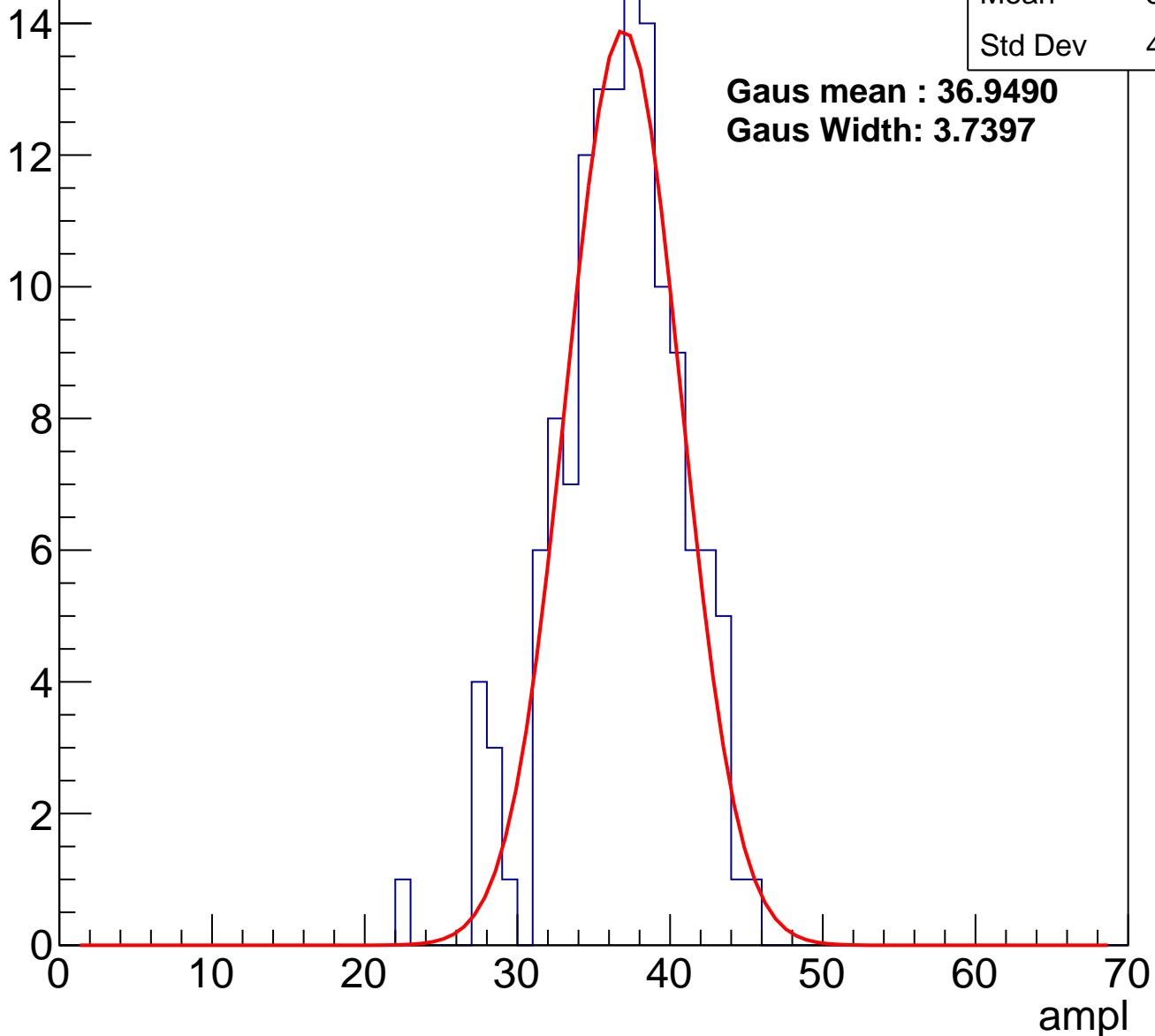
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	36.19
Std Dev	4.054

Gaus mean : 36.9490

Gaus Width: 3.7397

Entry



B0L101S, U18-ch85, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	43.17
Std Dev	3.75

Gaus mean : 43.7243

Gaus Width: 3.9468

Entry

12

10

8

6

4

2

0

0

10

20

30

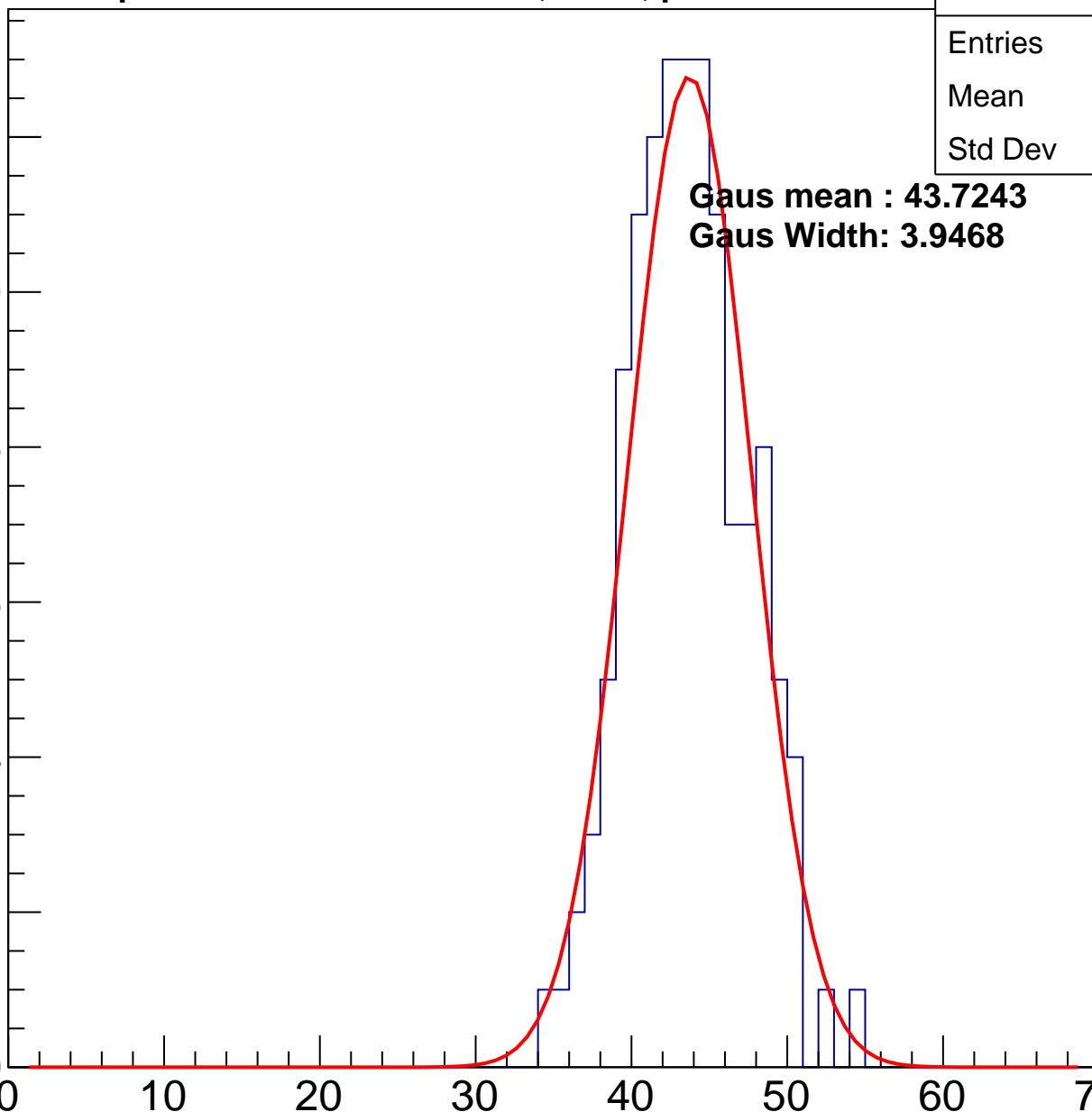
40

50

60

70

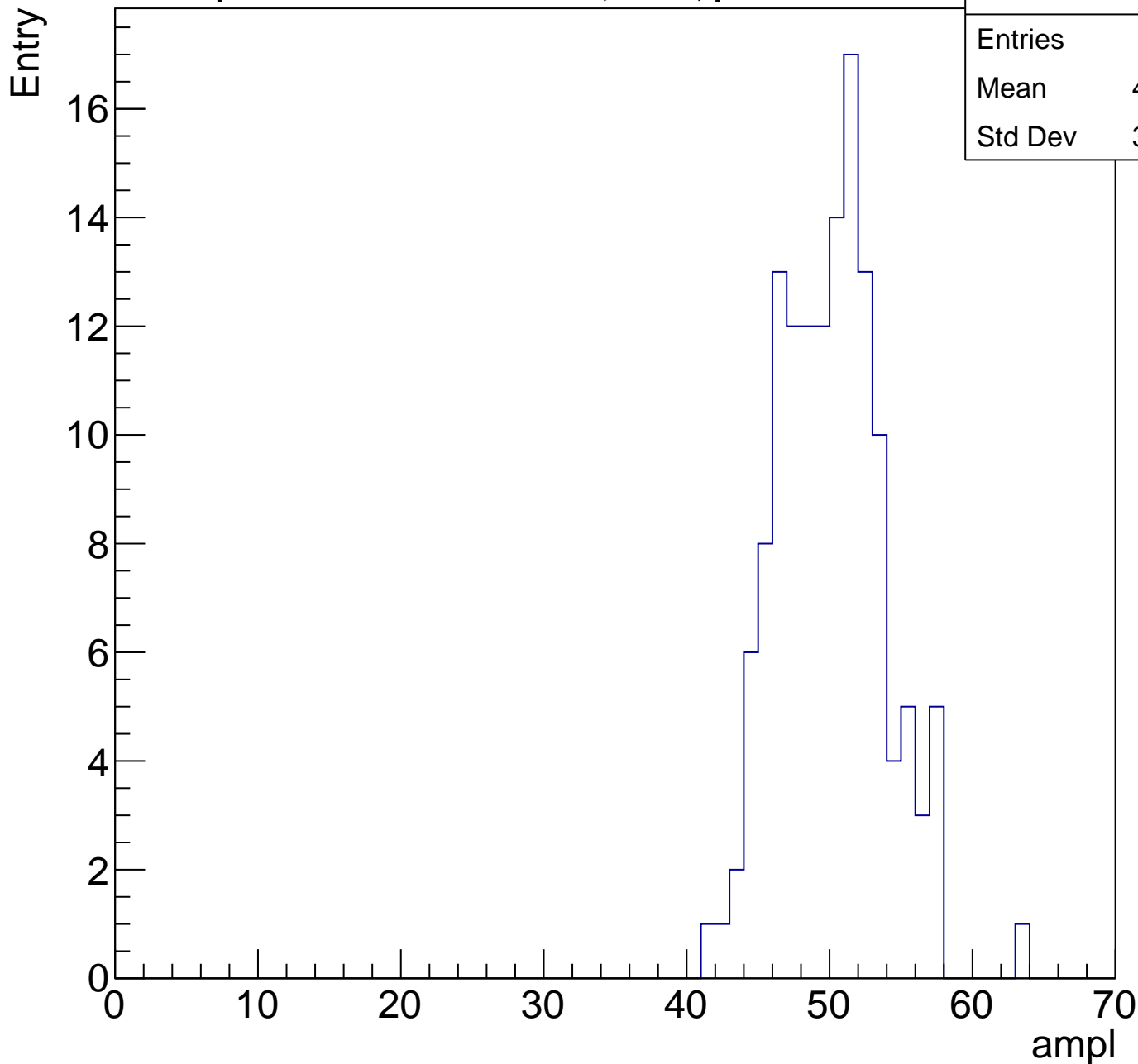
ampl



B0L101S, U18-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

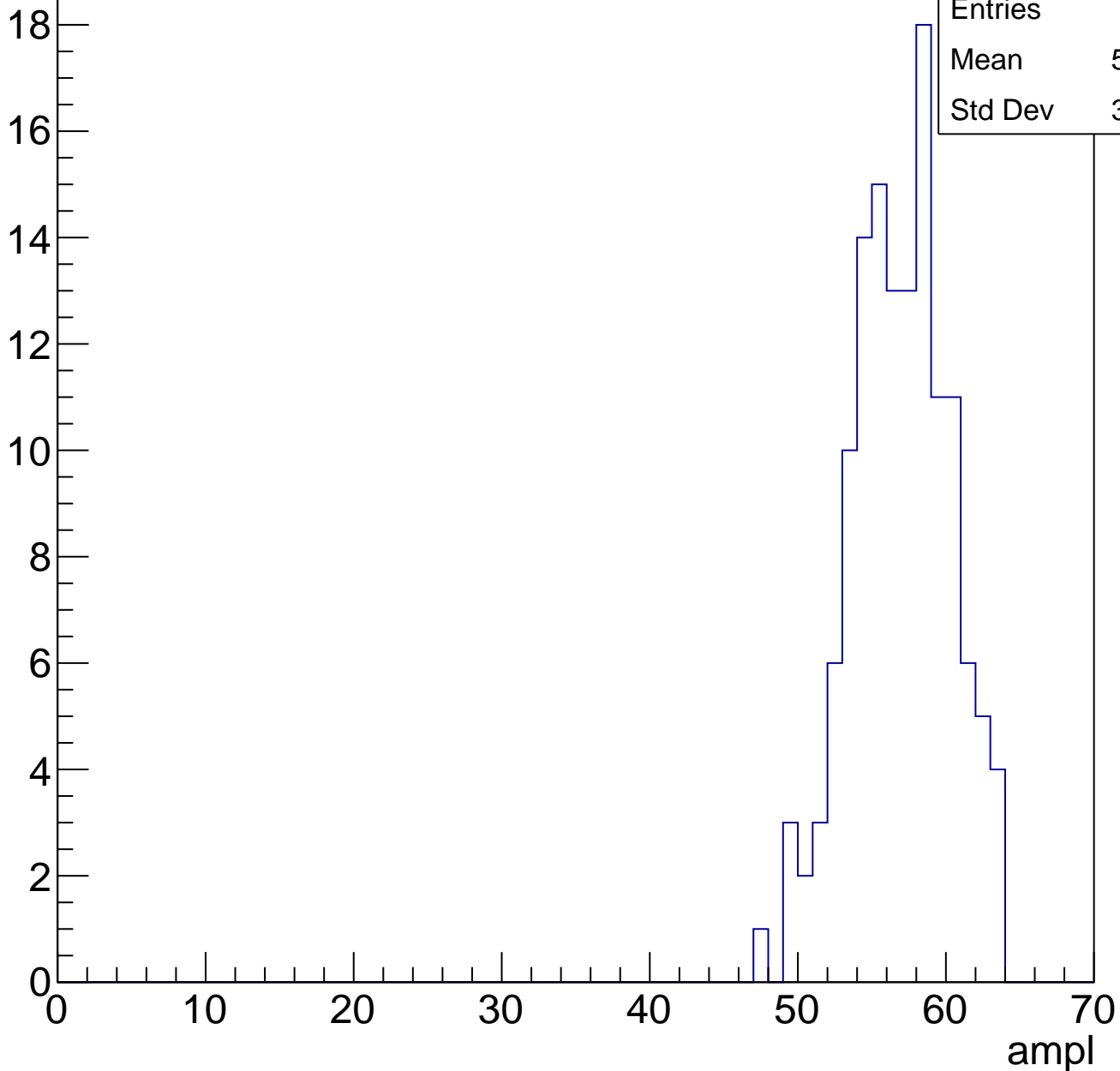
Entries	139
Mean	49.63
Std Dev	3.684



B0L101S, U18-ch85, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch85, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

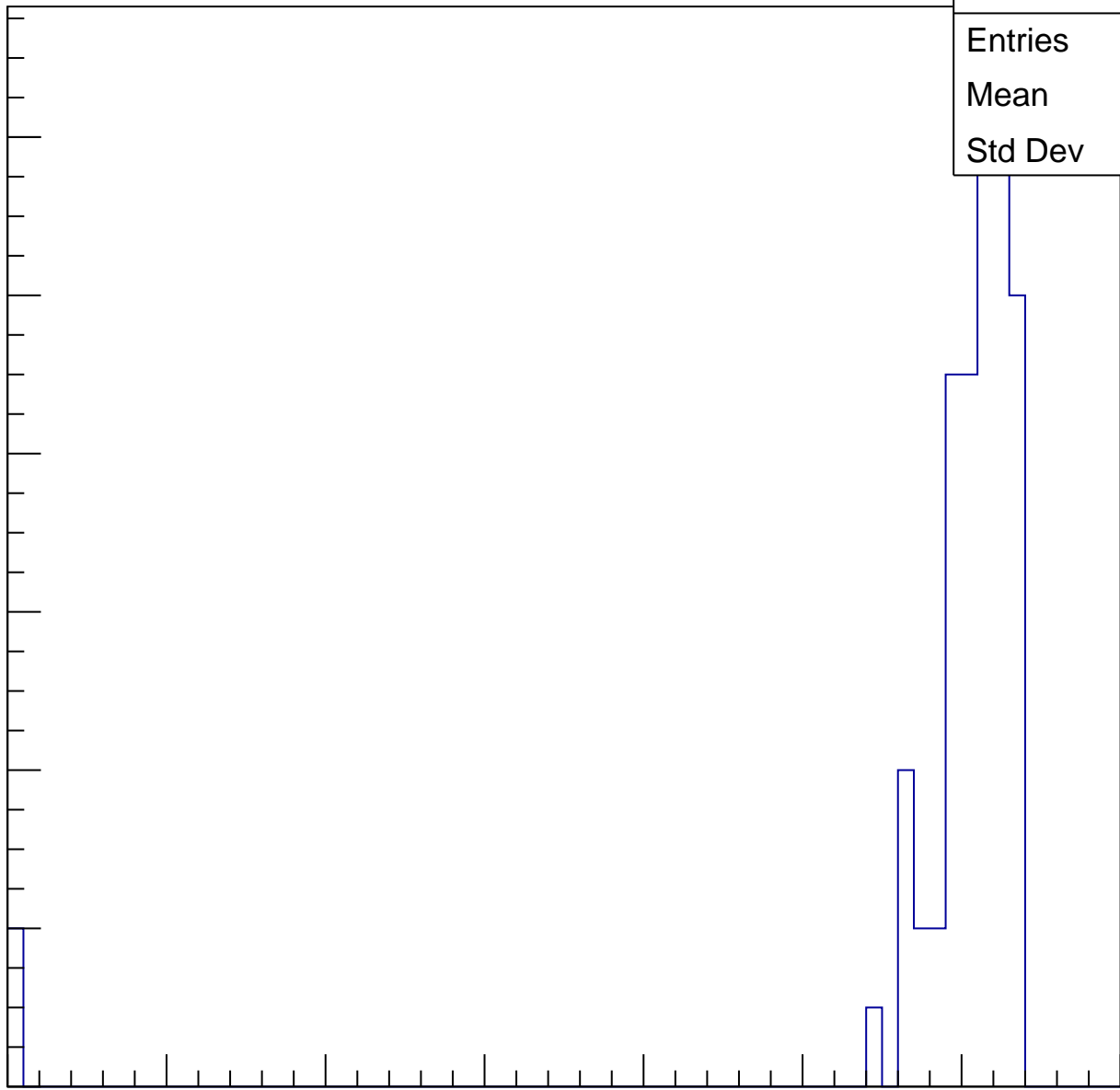
2

0

Entries	65
Mean	58.58
Std Dev	10.64

ampl

0 10 20 30 40 50 60 70

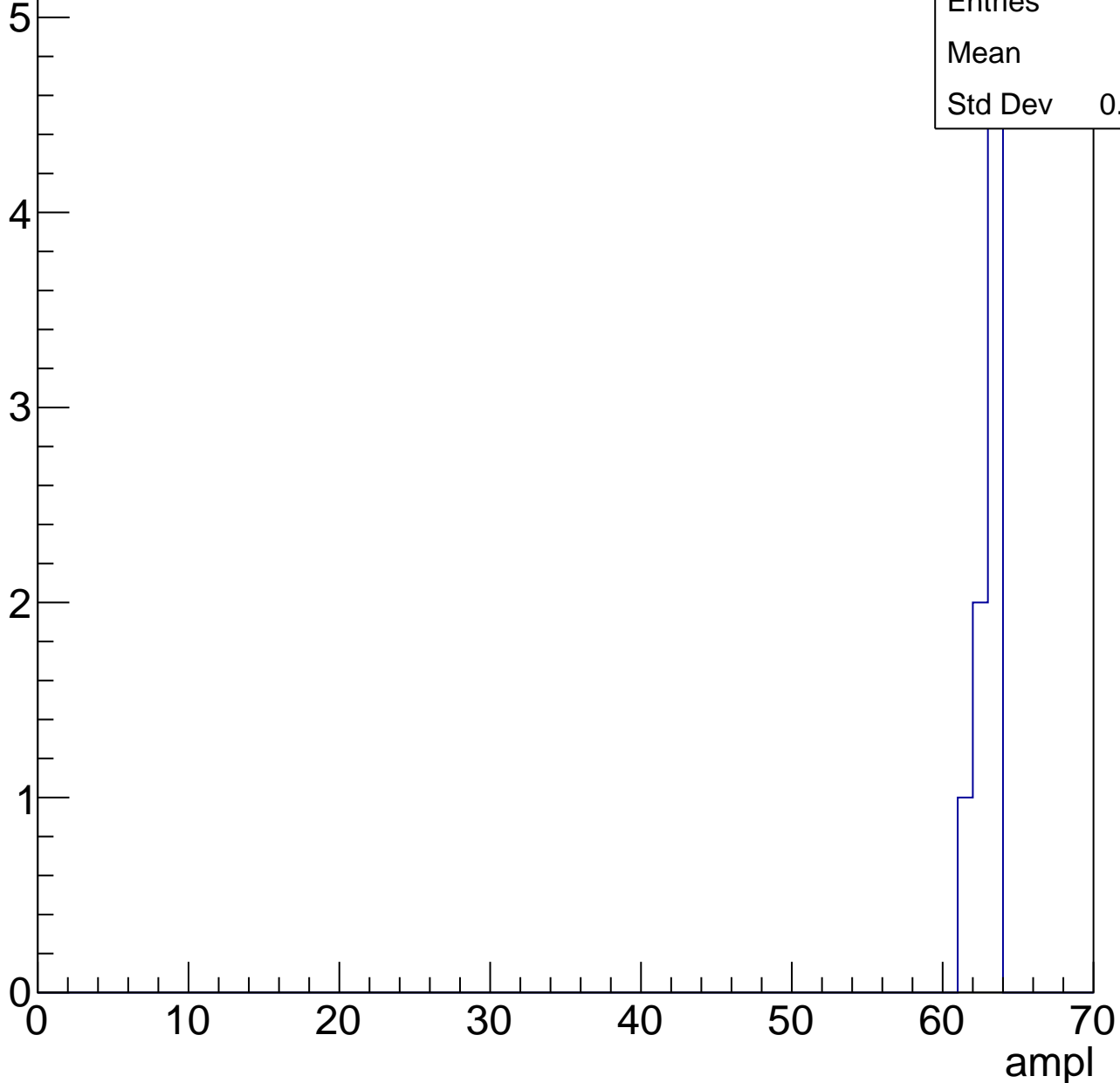


B0L101S, U18-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B0L101S, U18-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



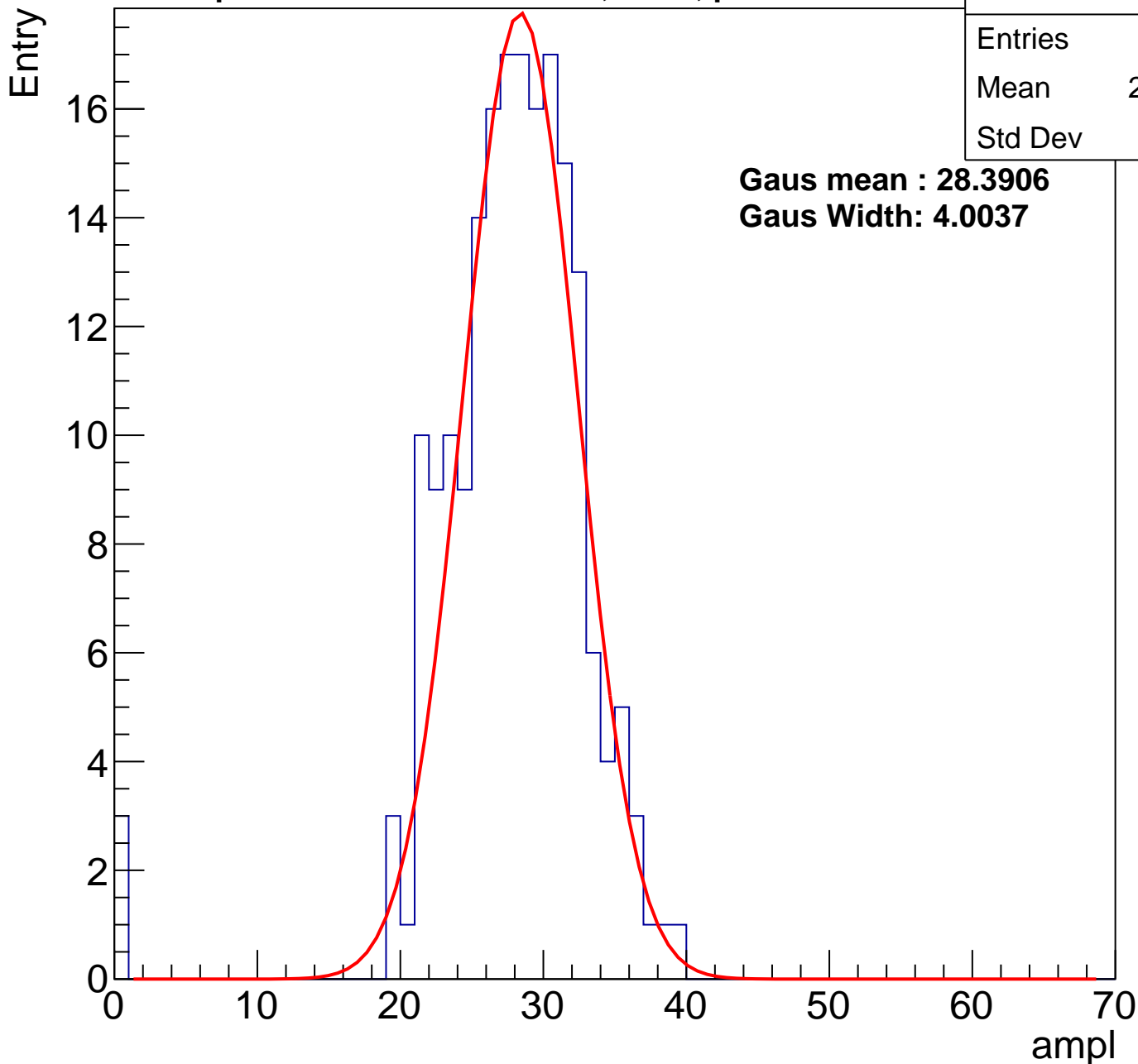
B0L101S, U18-ch86, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	191
Mean	27.32
Std Dev	5.34

Gaus mean : 28.3906

Gaus Width: 4.0037

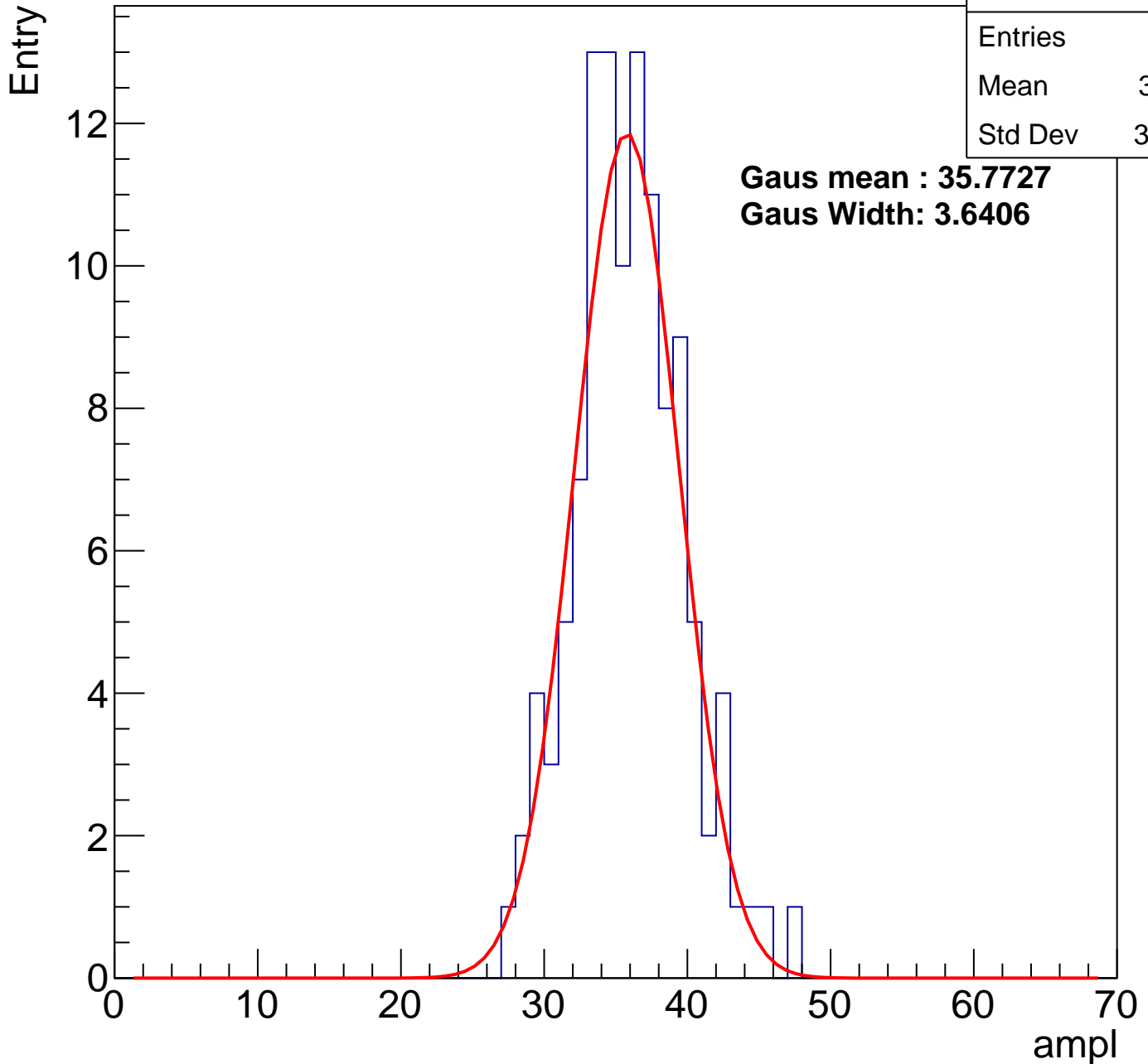


B0L101S, U18-ch86, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	35.51
Std Dev	3.763

Gaus mean : 35.7727
Gaus Width: 3.6406



B0L101S, U18-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	41.65
Std Dev	3.696

Gaus mean : 42.1499

Gaus Width: 3.8296

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

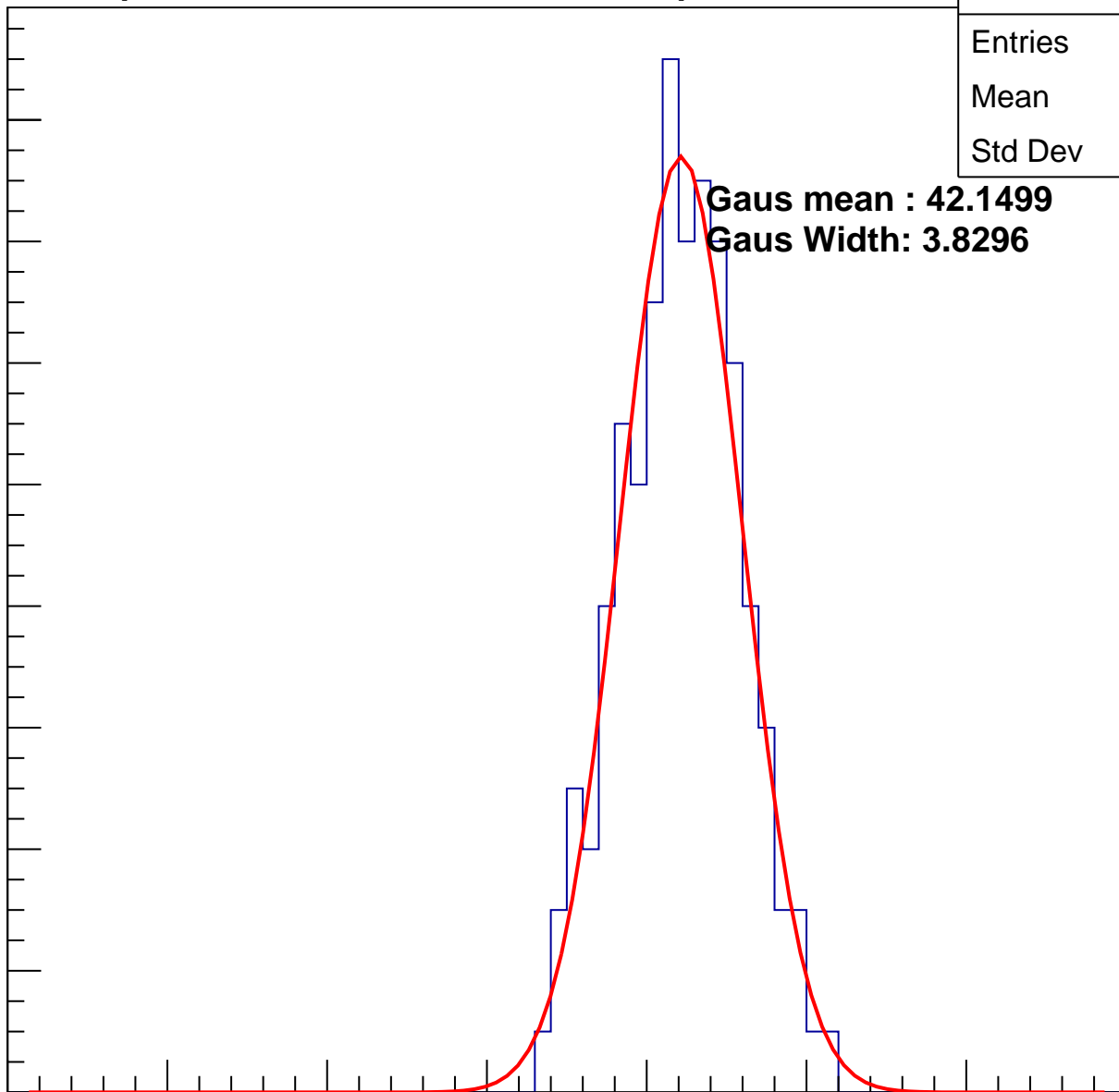
40

50

60

70

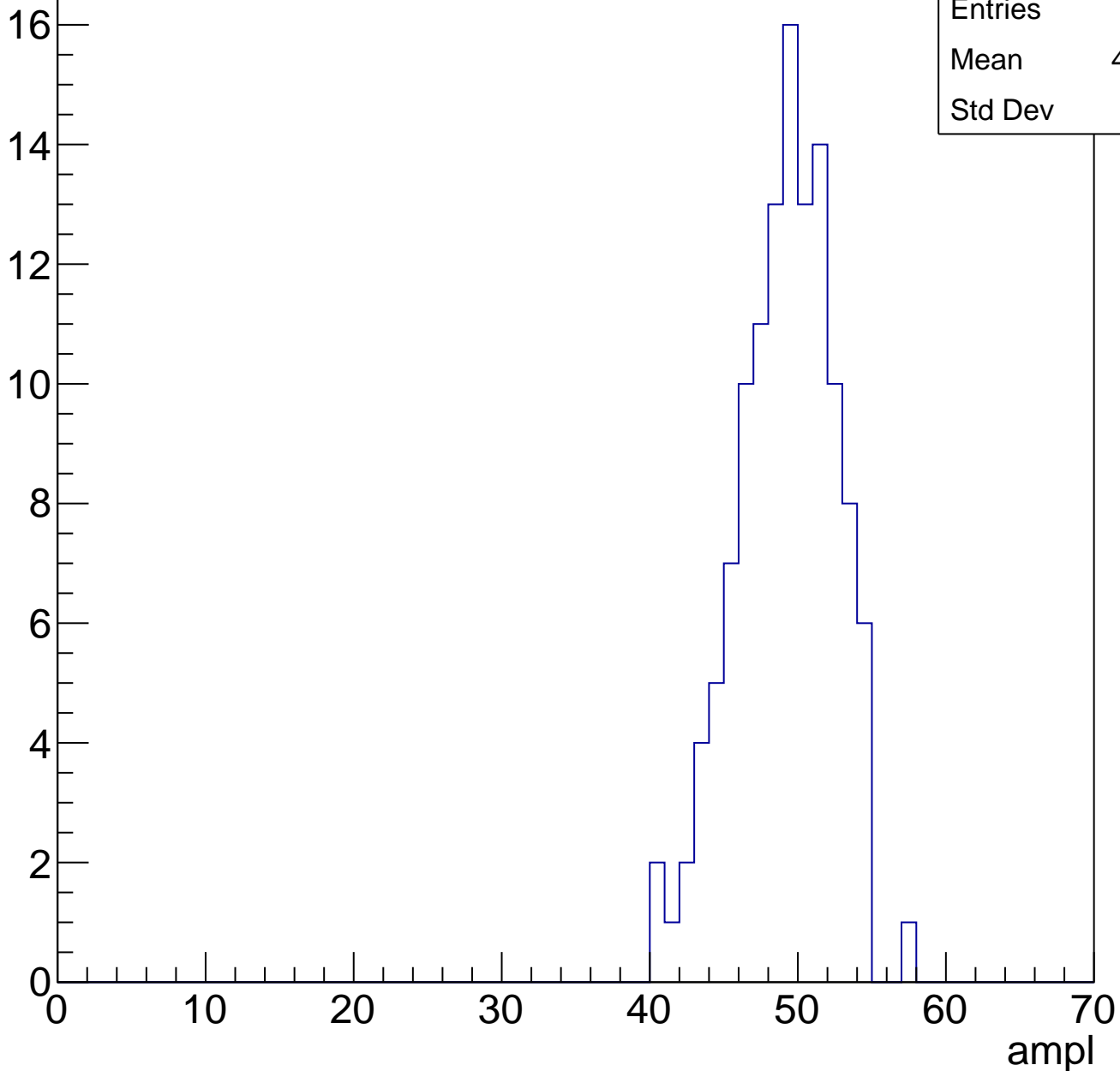
ampl



B0L101S, U18-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

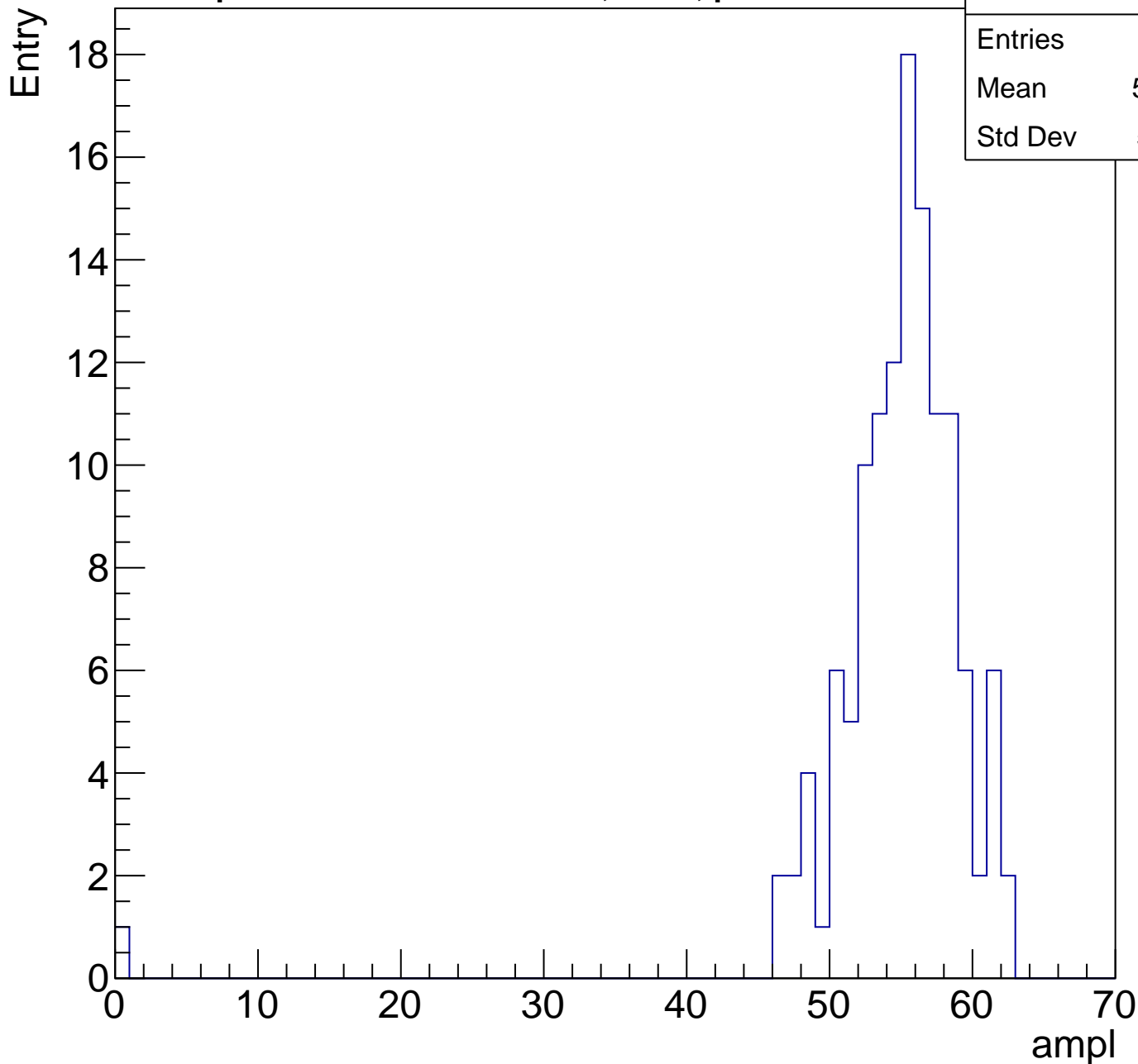
Entry



B0L101S, U18-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	54.34
Std Dev	5.991



B0L101S, U18-ch86, adc5

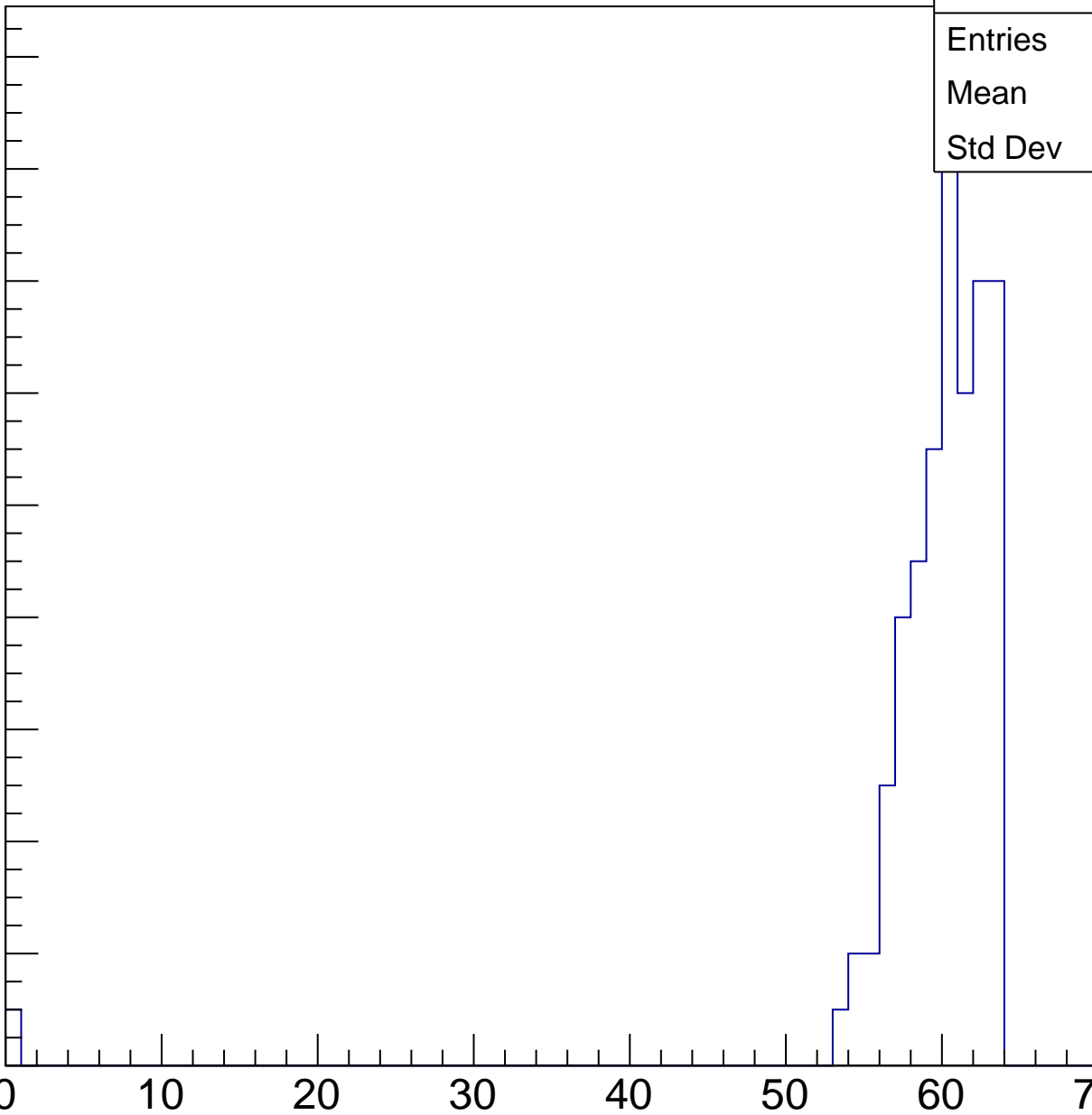
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	97
Mean	59.18
Std Dev	6.5

ampl

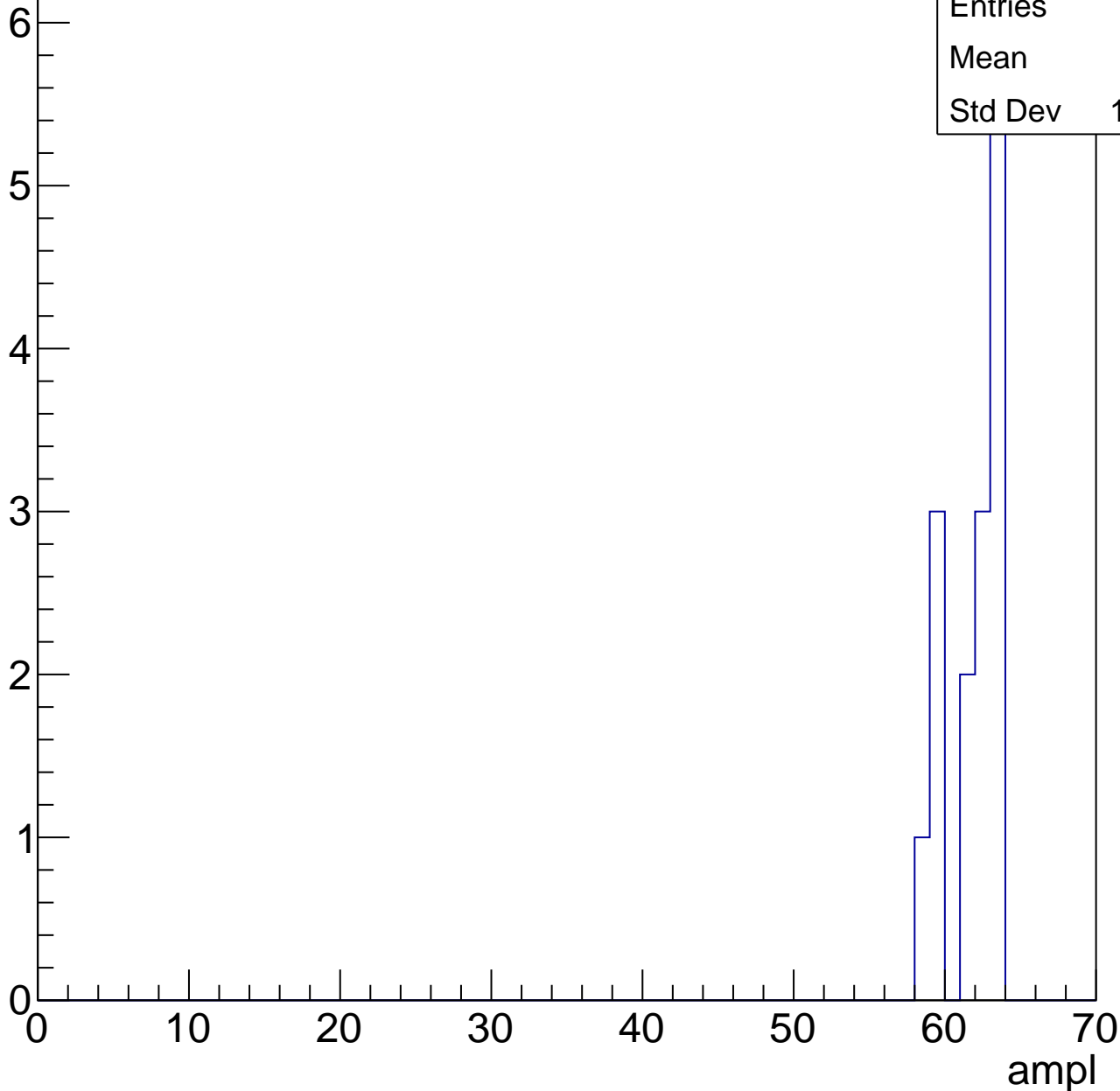


B0L101S, U18-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.4
Std Dev	1.744



B0L101S, U18-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch87, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.04
Std Dev	4.963

Gaus mean : 28.9821

Gaus Width: 3.6499

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

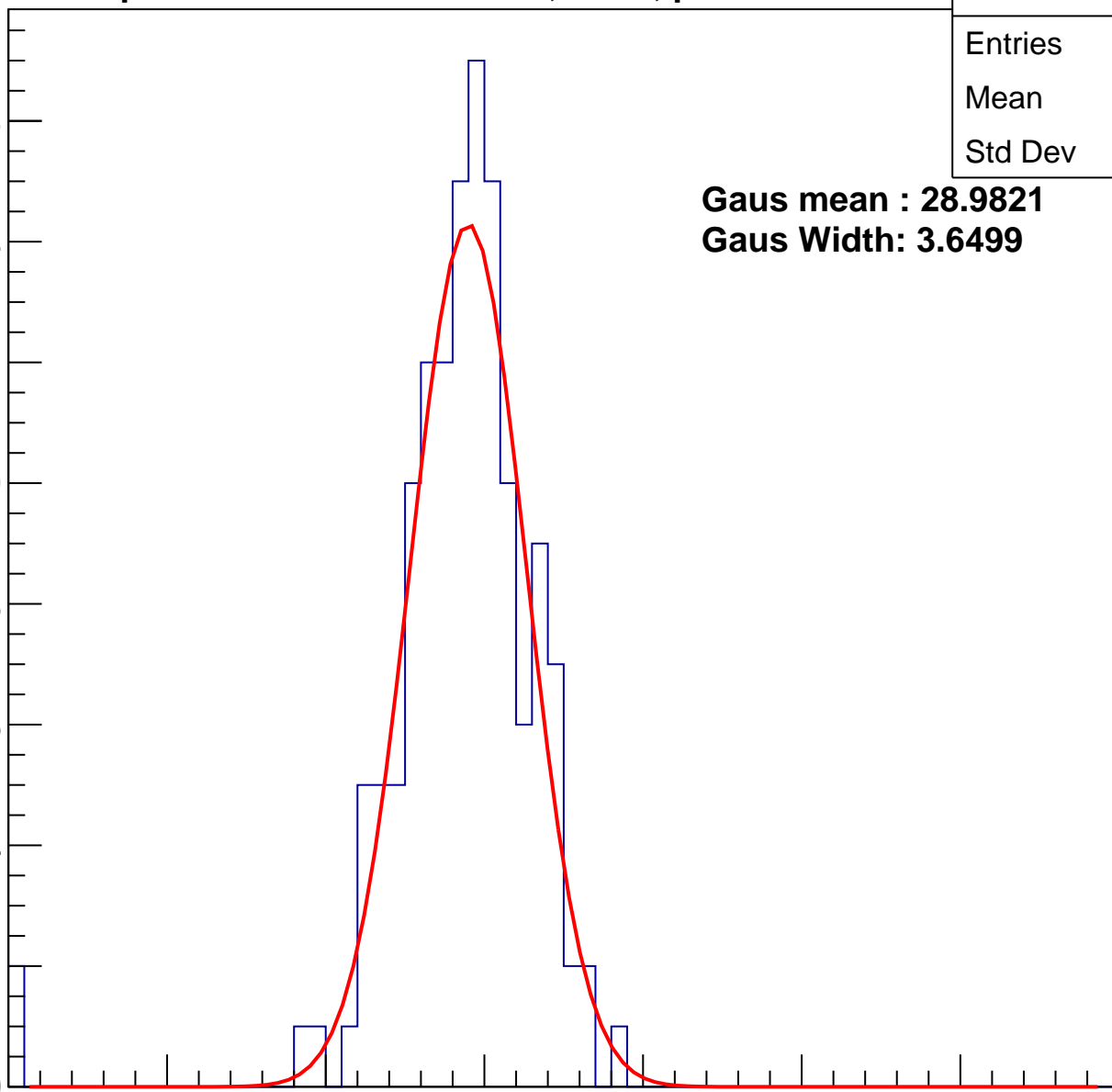
40

50

60

70

ampl



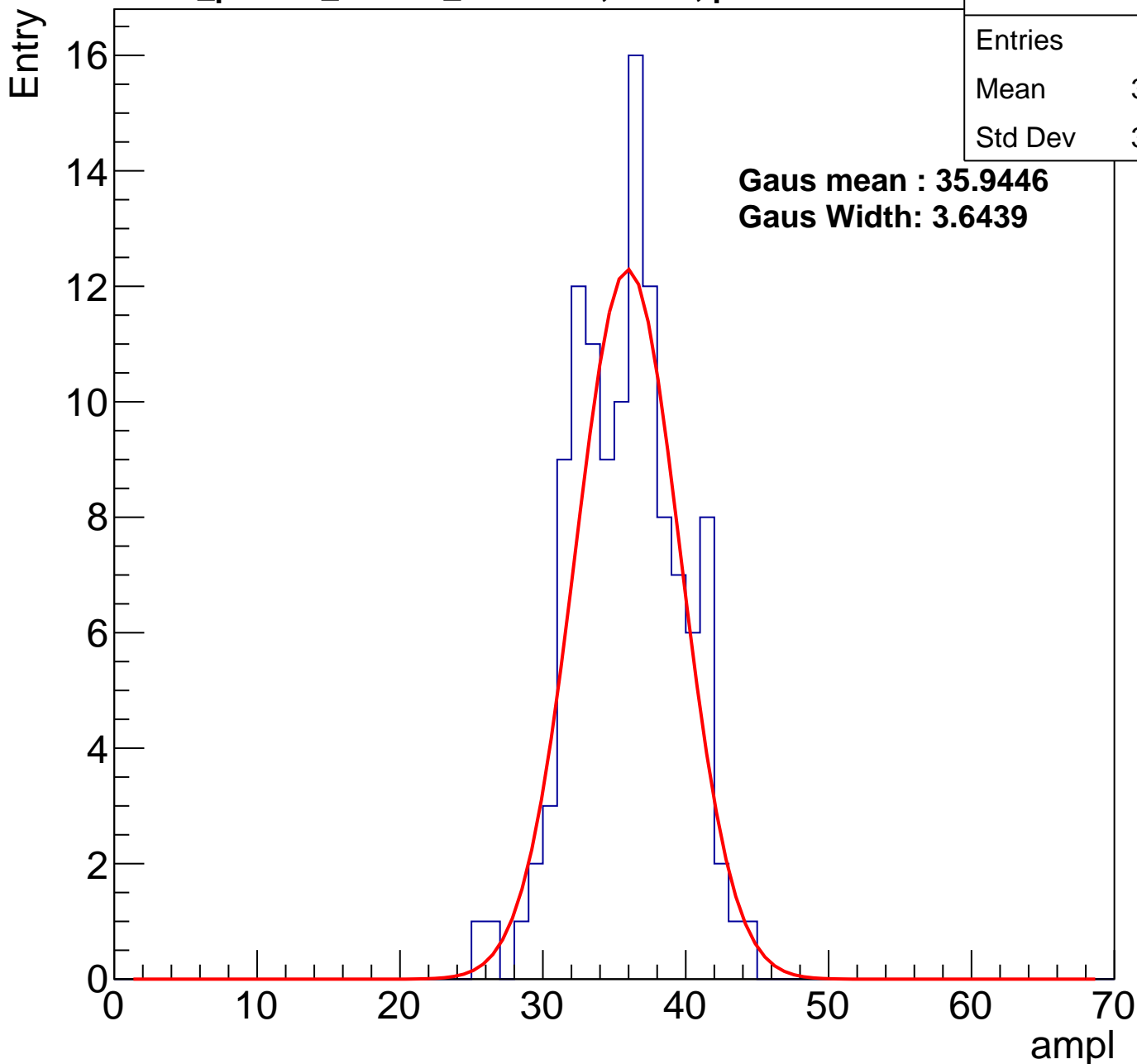
B0L101S, U18-ch87, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	35.38
Std Dev	3.645

Gaus mean : 35.9446

Gaus Width: 3.6439



B0L101S, U18-ch87, adc2

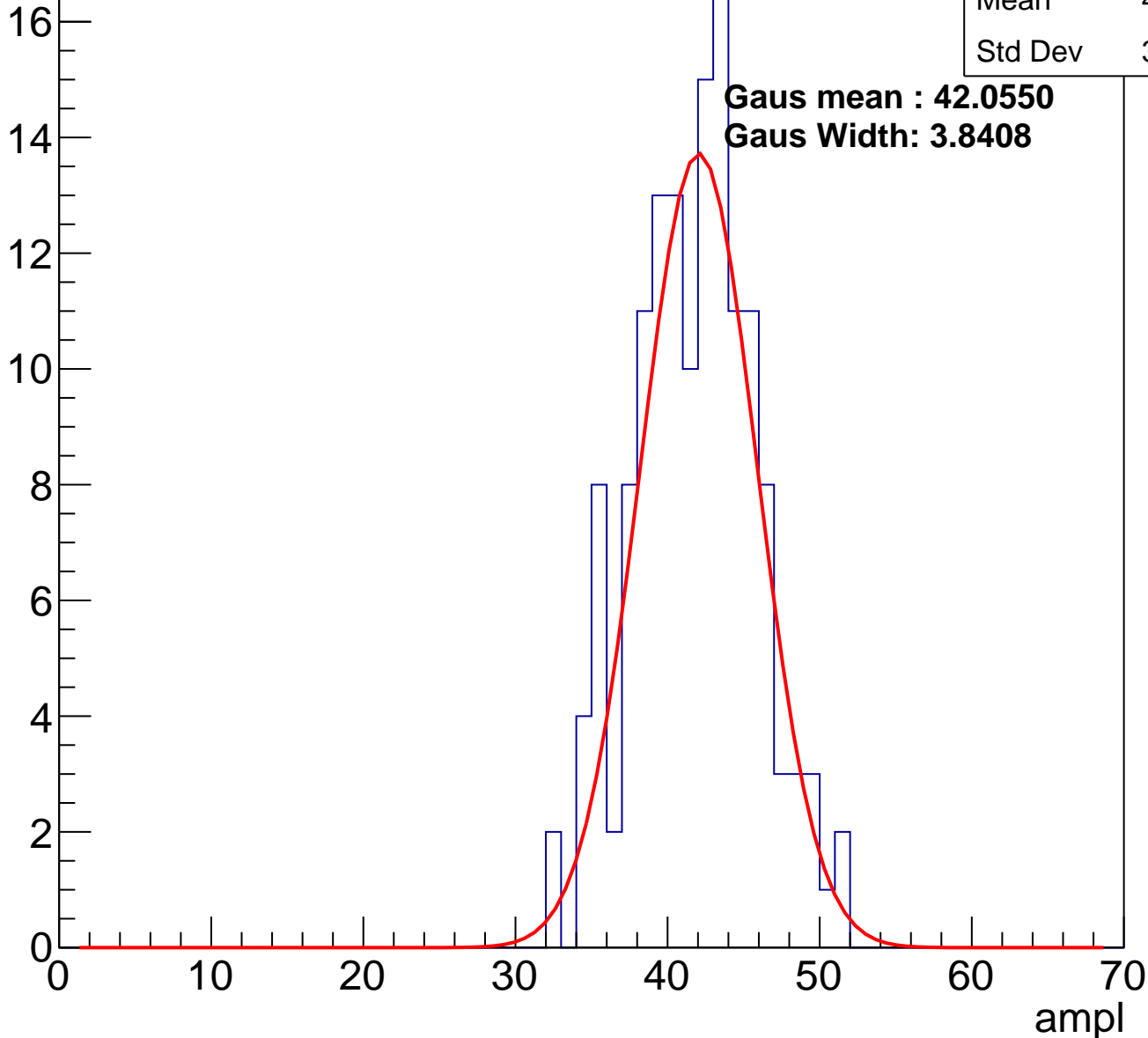
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	145
Mean	41.34
Std Dev	3.945

Gaus mean : 42.0550

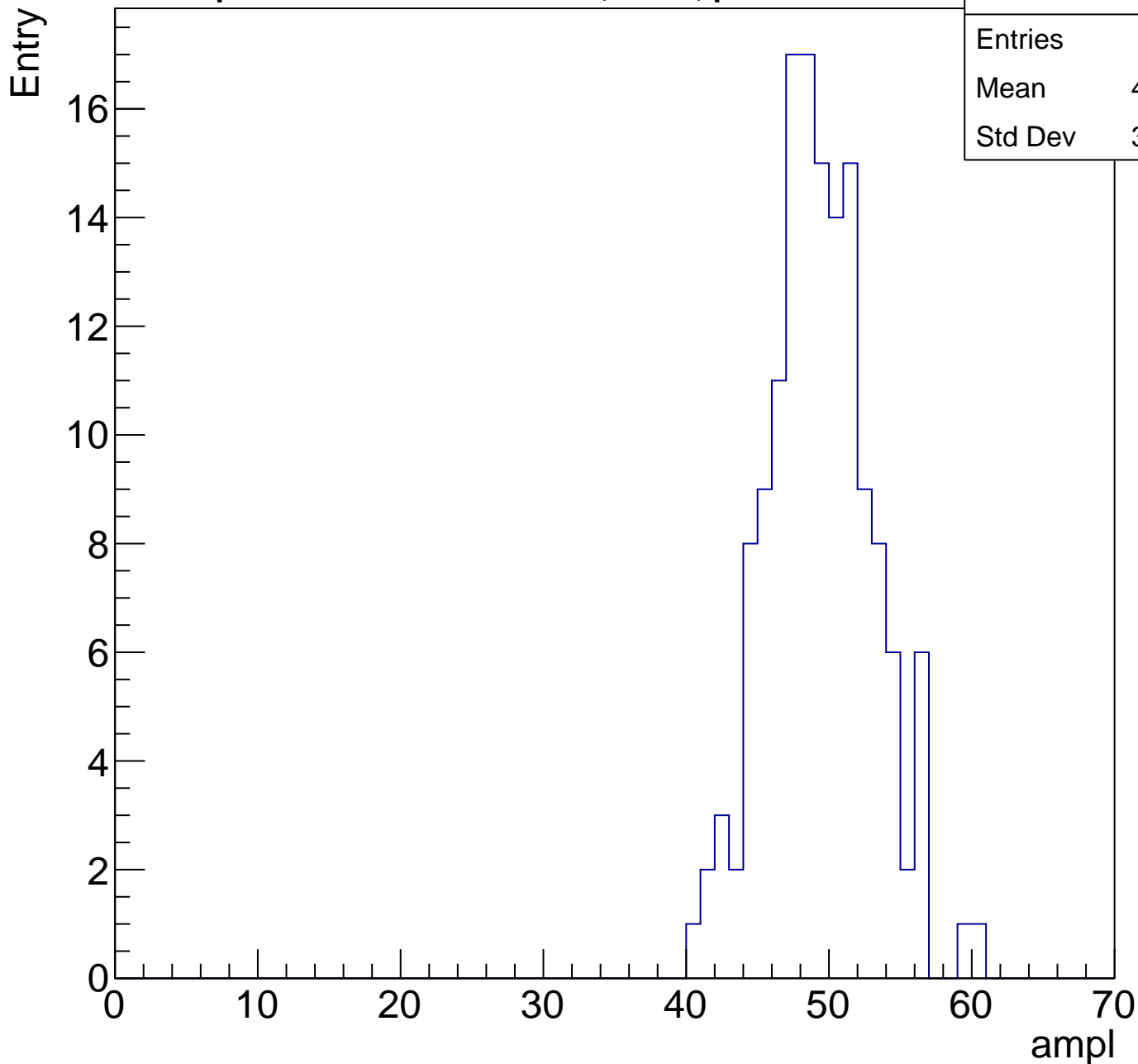
Gaus Width: 3.8408



B0L101S, U18-ch87, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	48.93
Std Dev	3.666



B0L101S, U18-ch87, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

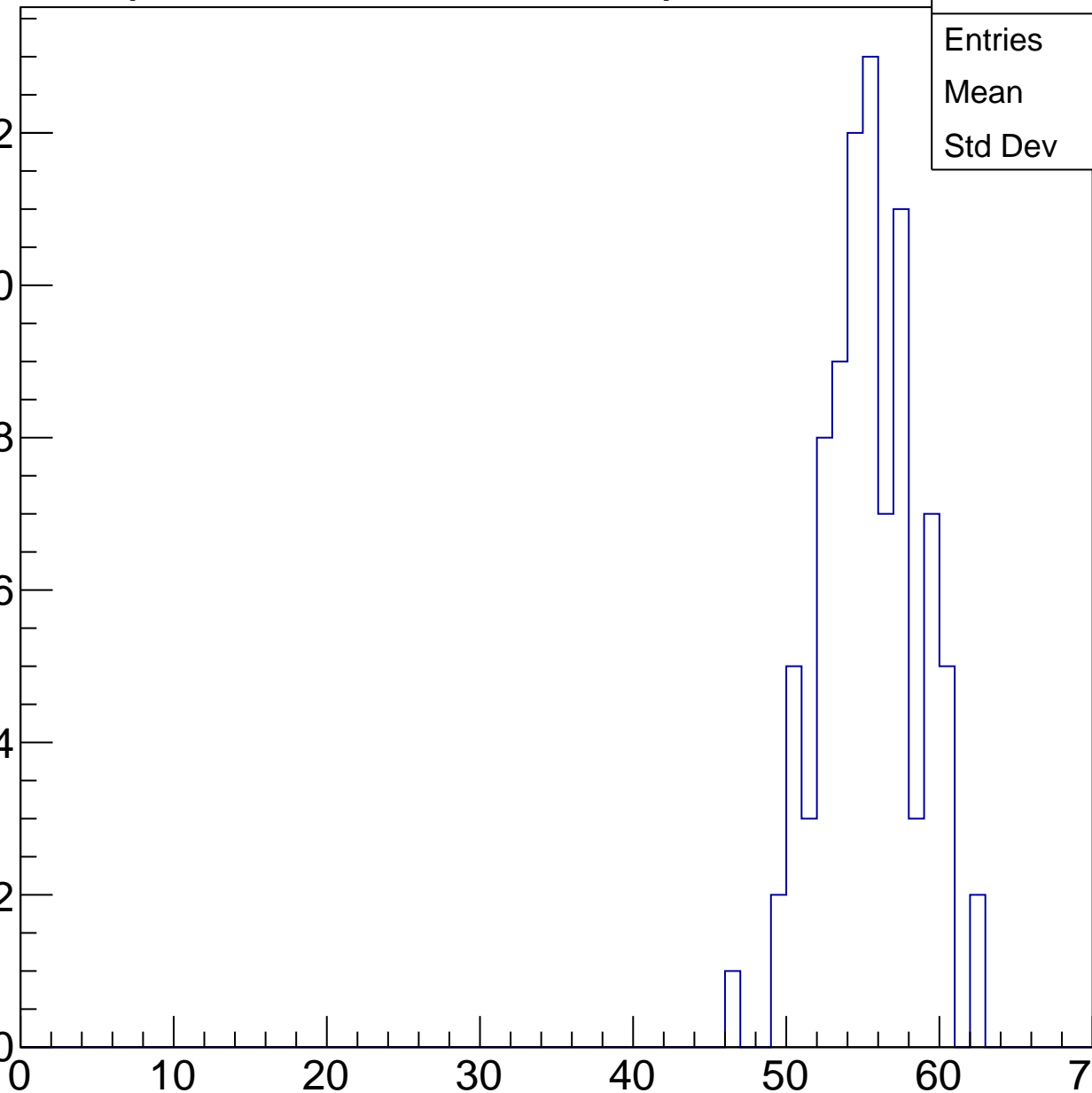
4

2

0

Entries	88
Mean	54.92
Std Dev	3.123

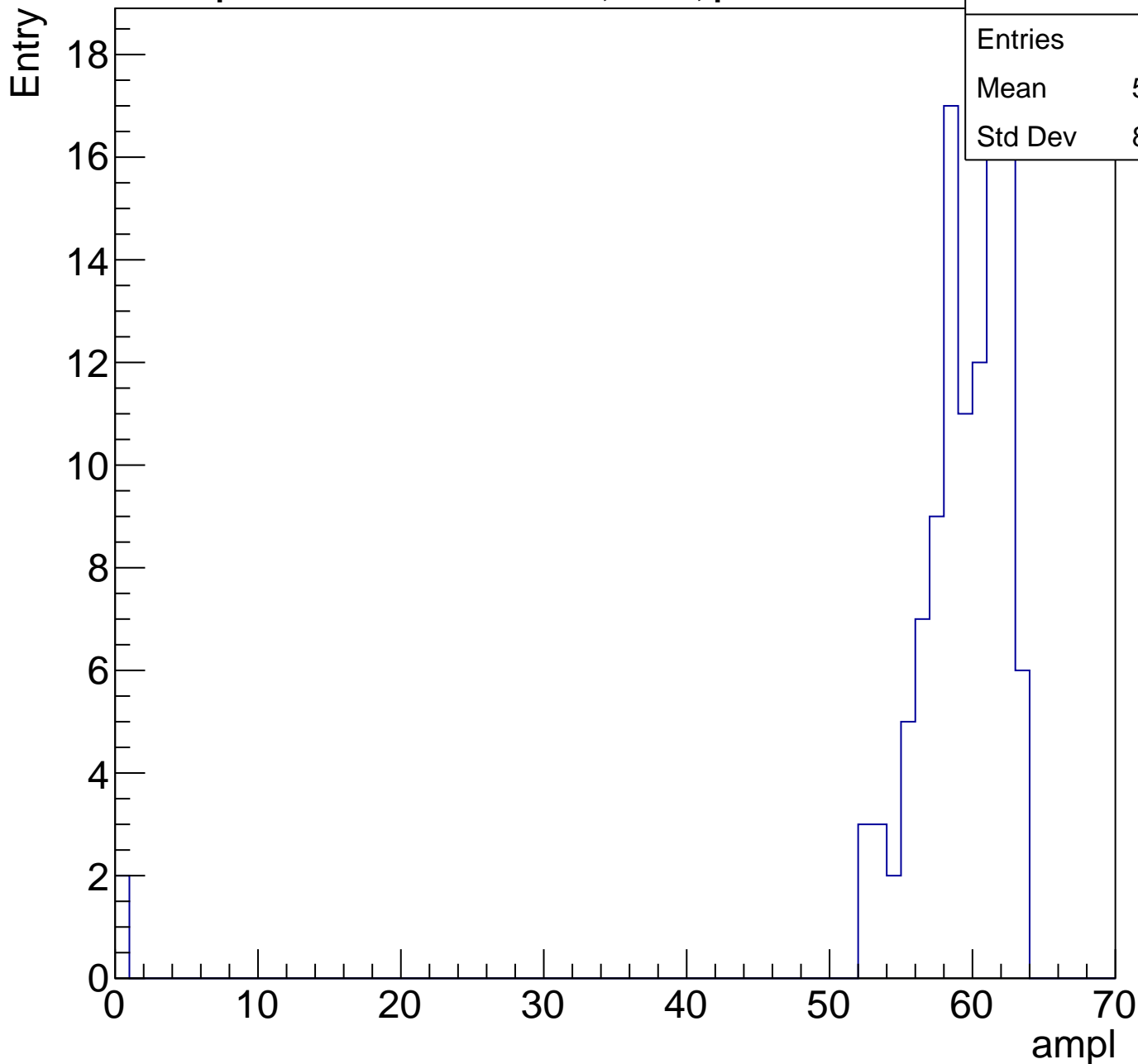
ampl



B0L101S, U18-ch87, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	57.93
Std Dev	8.274



B0L101S, U18-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	19
Mean	62.21
Std Dev	1.281

B0L101S, U18-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



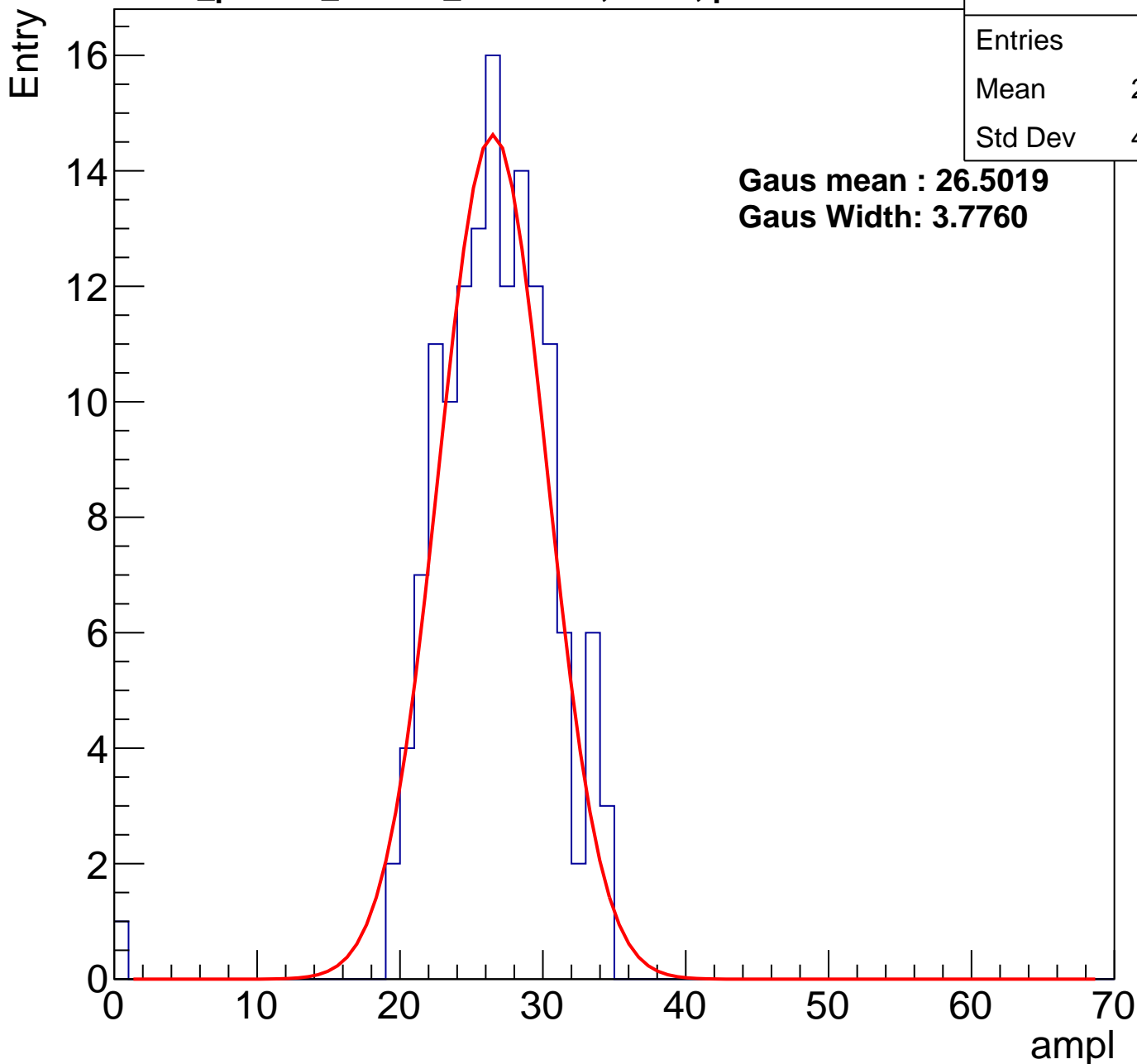
B0L101S, U18-ch88, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	26.13
Std Dev	4.177

Gaus mean : 26.5019

Gaus Width: 3.7760



B0L101S, U18-ch88, adc1

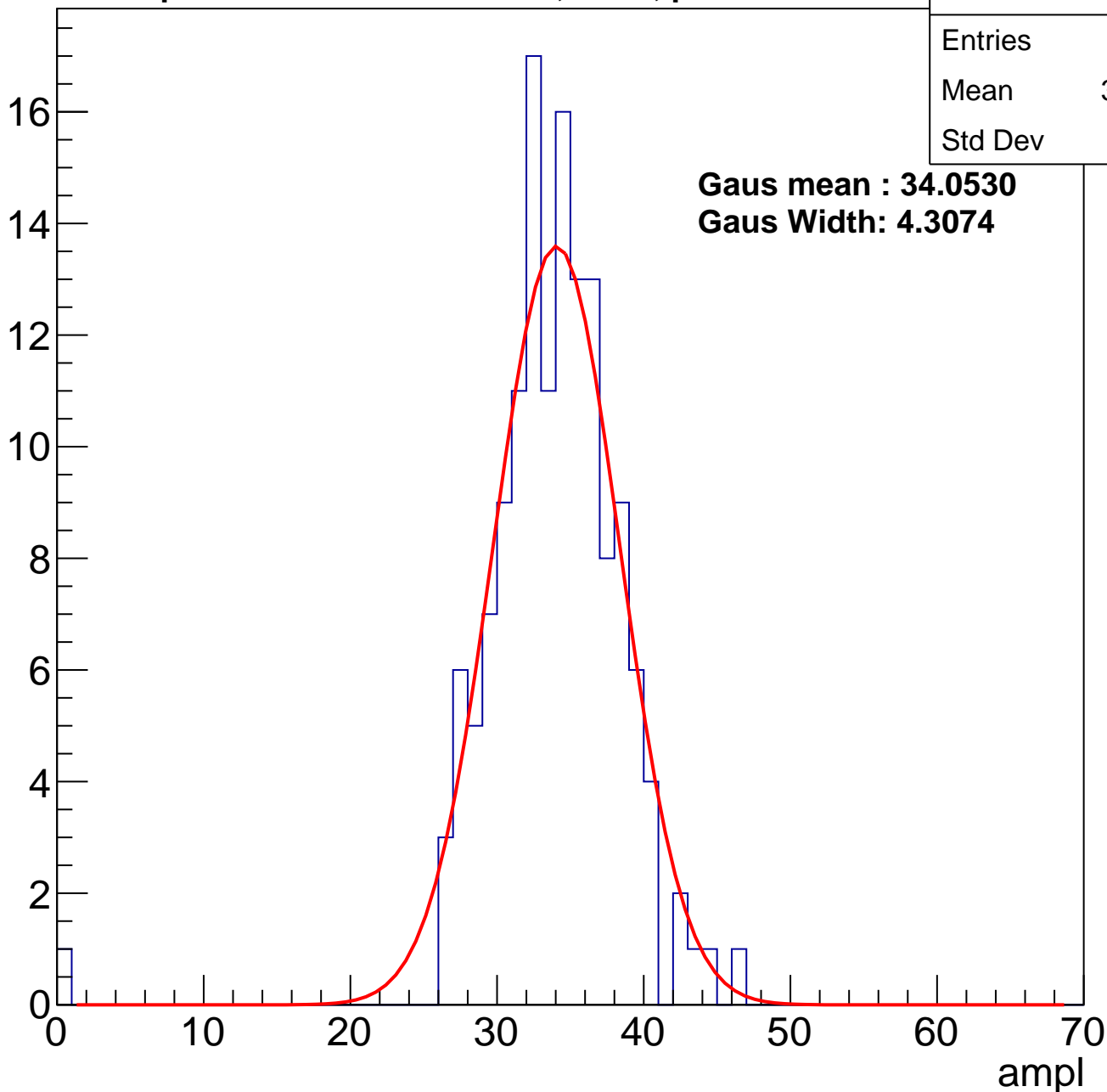
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	33.45
Std Dev	4.79

Gaus mean : 34.0530

Gaus Width: 4.3074

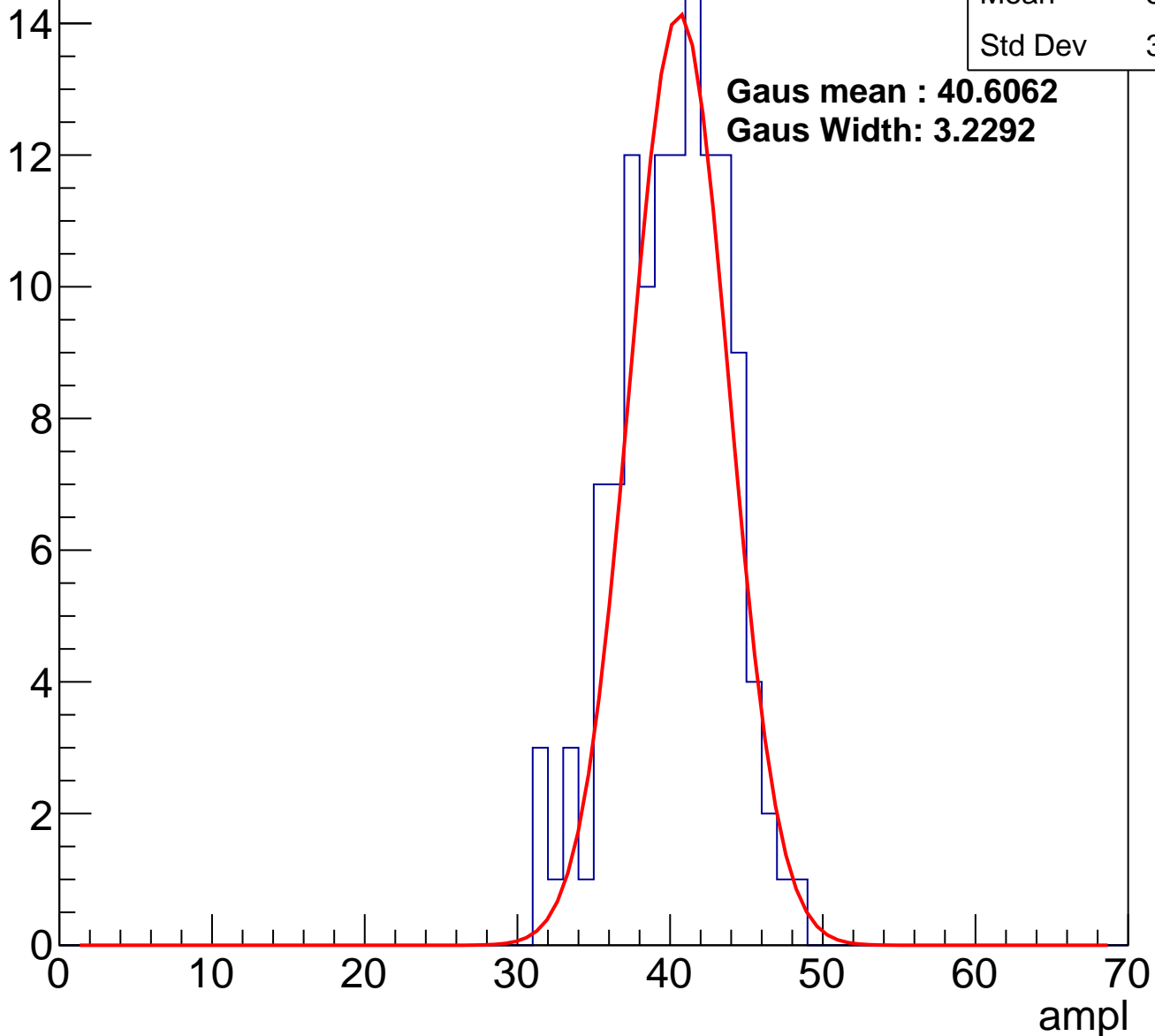
Entry



B0L101S, U18-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

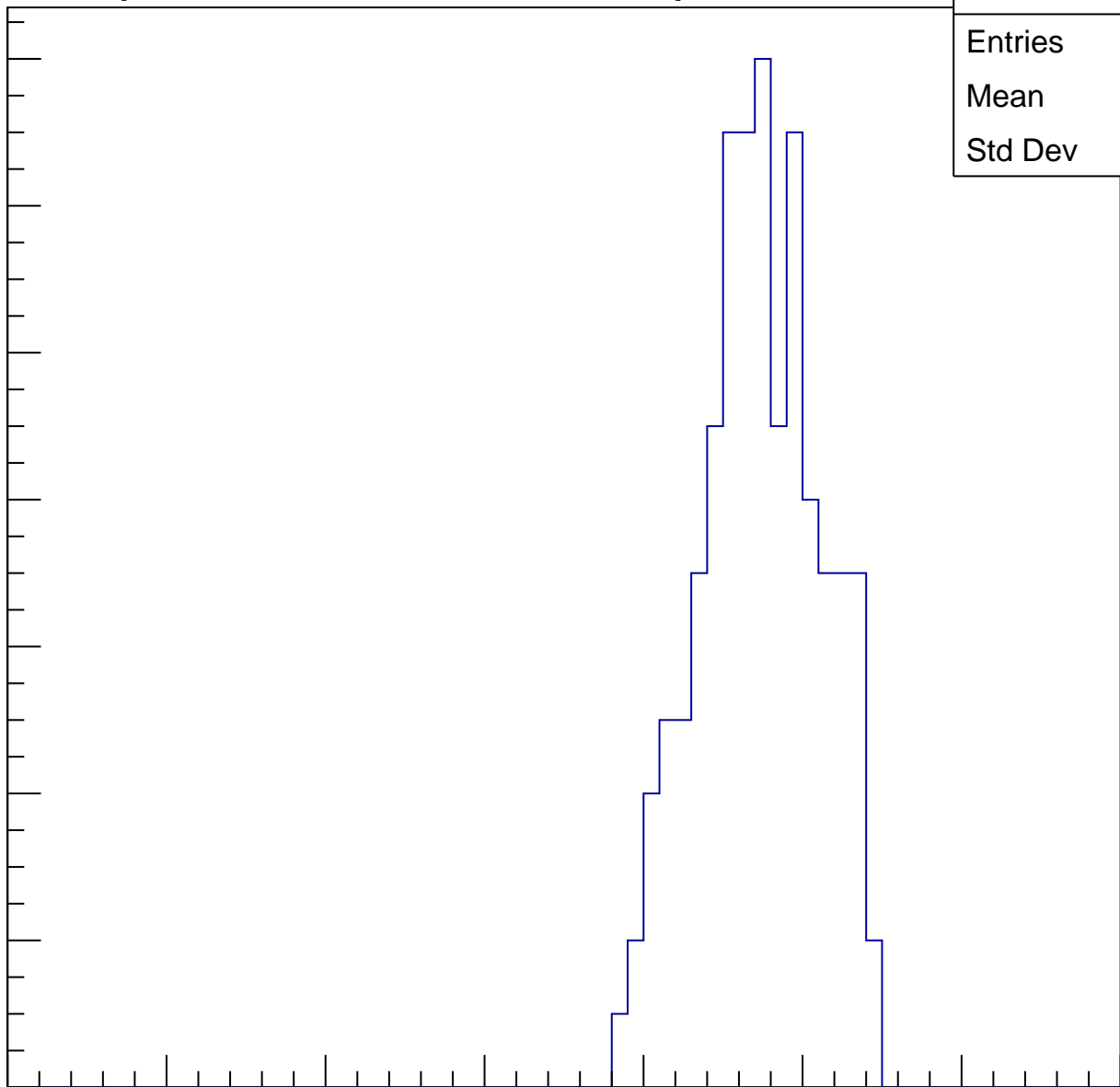
Entries	126
Mean	46.81
Std Dev	3.709

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

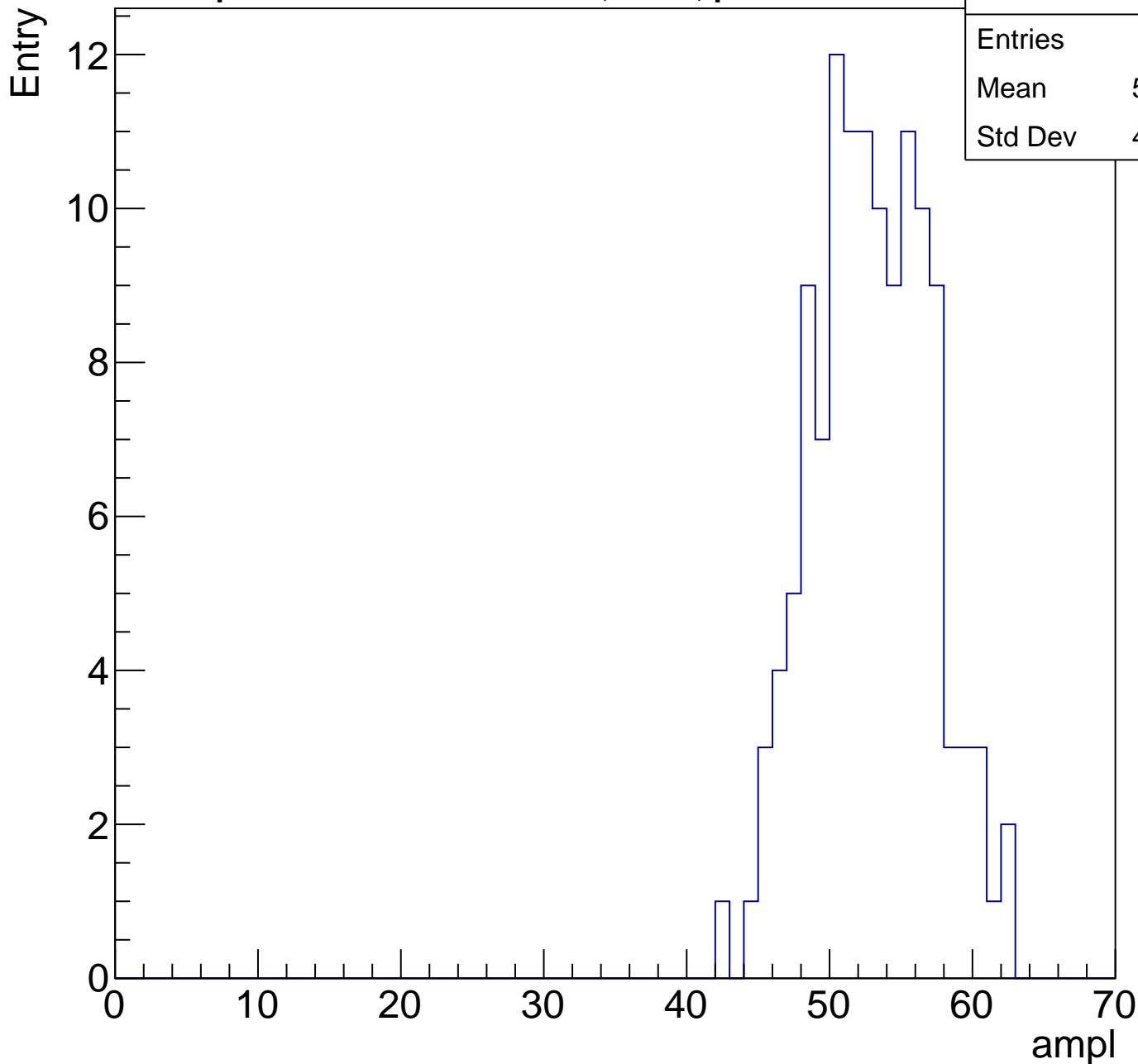
ampl



B0L101S, U18-ch88, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	52.46
Std Dev	4.076



B0L101S, U18-ch88, adc5

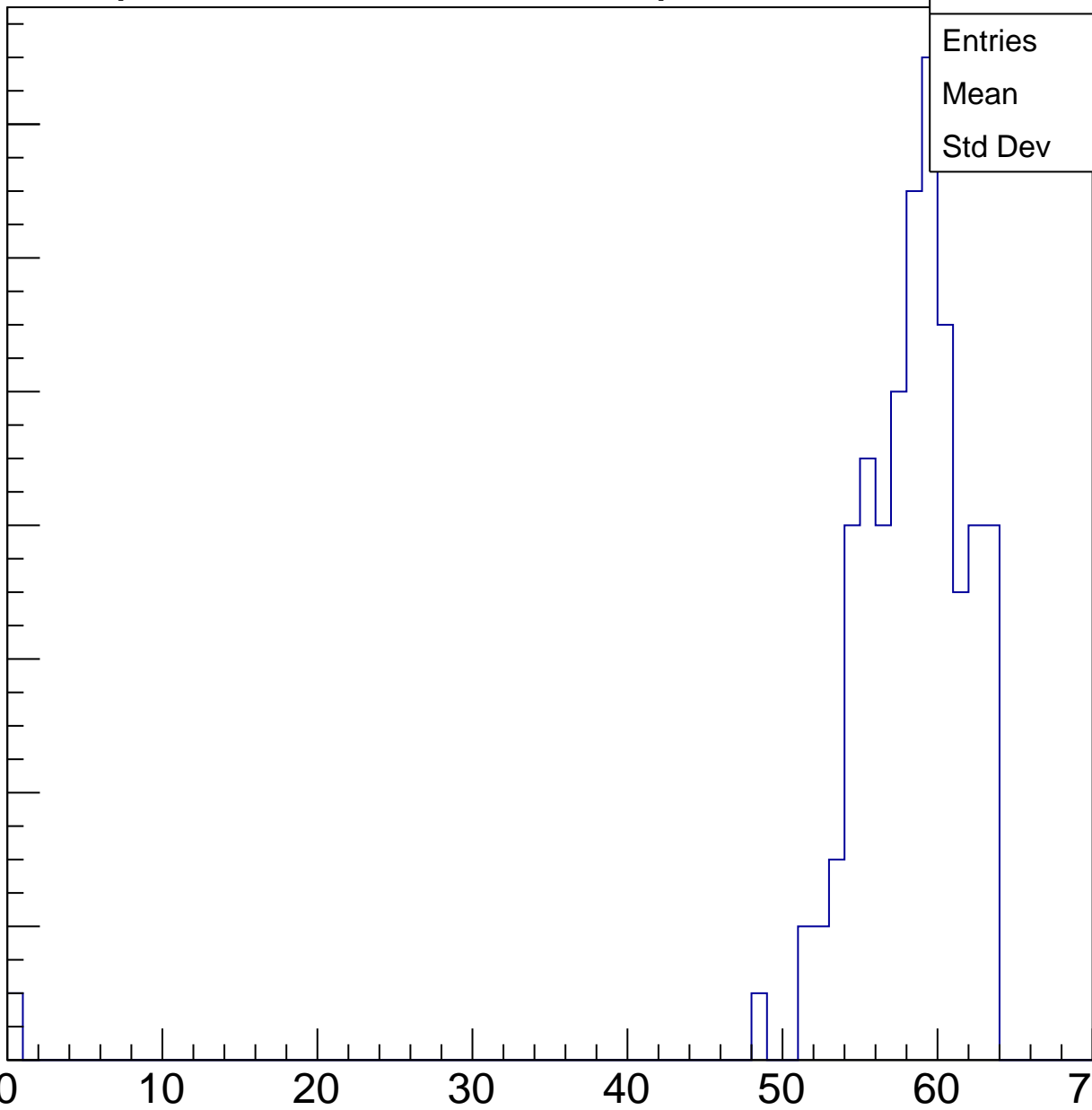
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	57.4
Std Dev	6.422

ampl



B0L101S, U18-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	46
Mean	60.74
Std Dev	2.161

B0L101S, U18-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

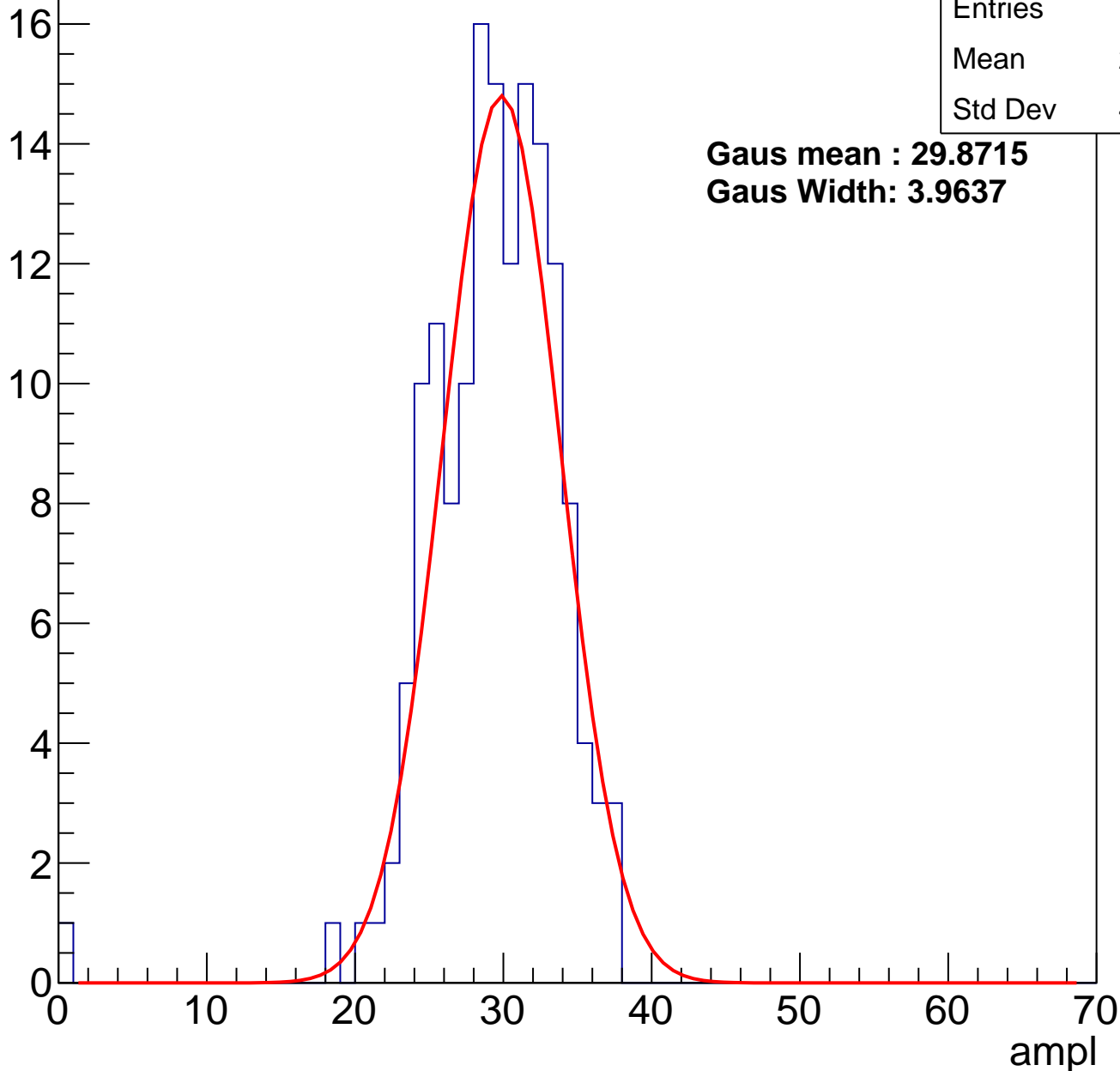
Entries	6
Mean	51.5
Std Dev	23.04

ampl

B0L101S, U18-ch89, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

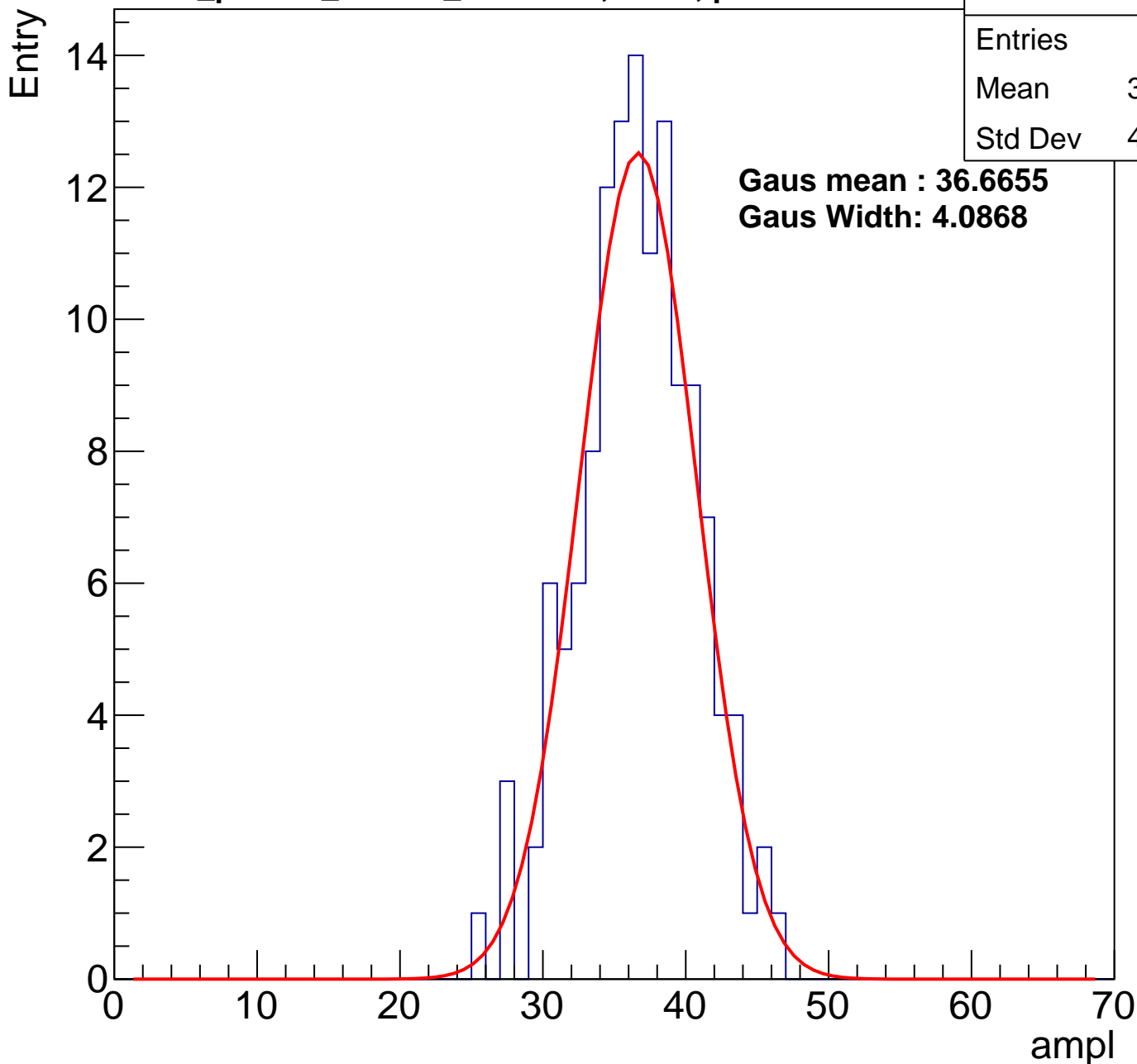


B0L101S, U18-ch89, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.19
Std Dev	4.057

Gaus mean : 36.6655
Gaus Width: 4.0868



B0L101S, U18-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

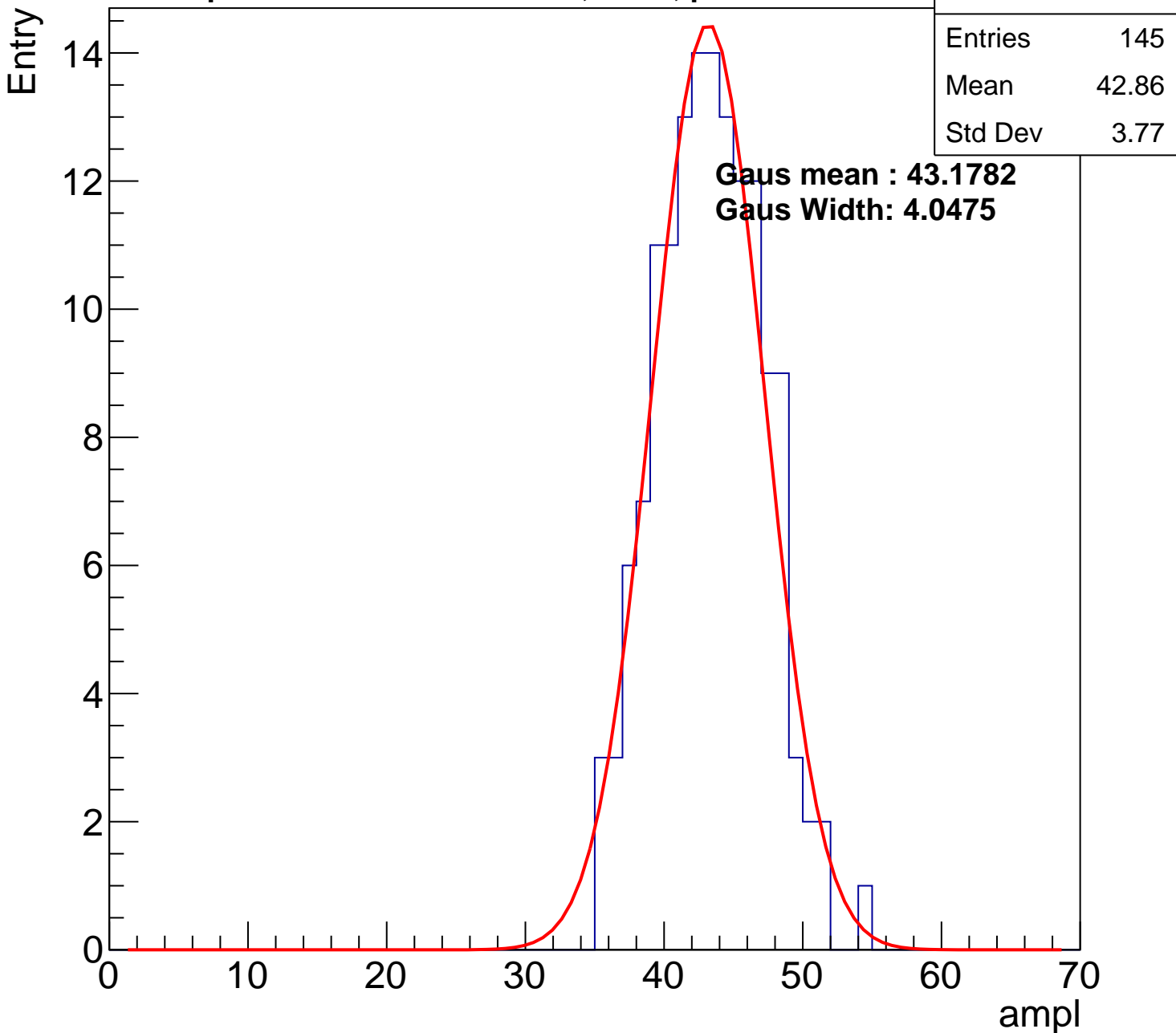
14
12
10
8
6
4
2
0

Entries	145
Mean	42.86
Std Dev	3.77

Gaus mean : 43.1782
Gaus Width: 4.0475

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch89, adc3

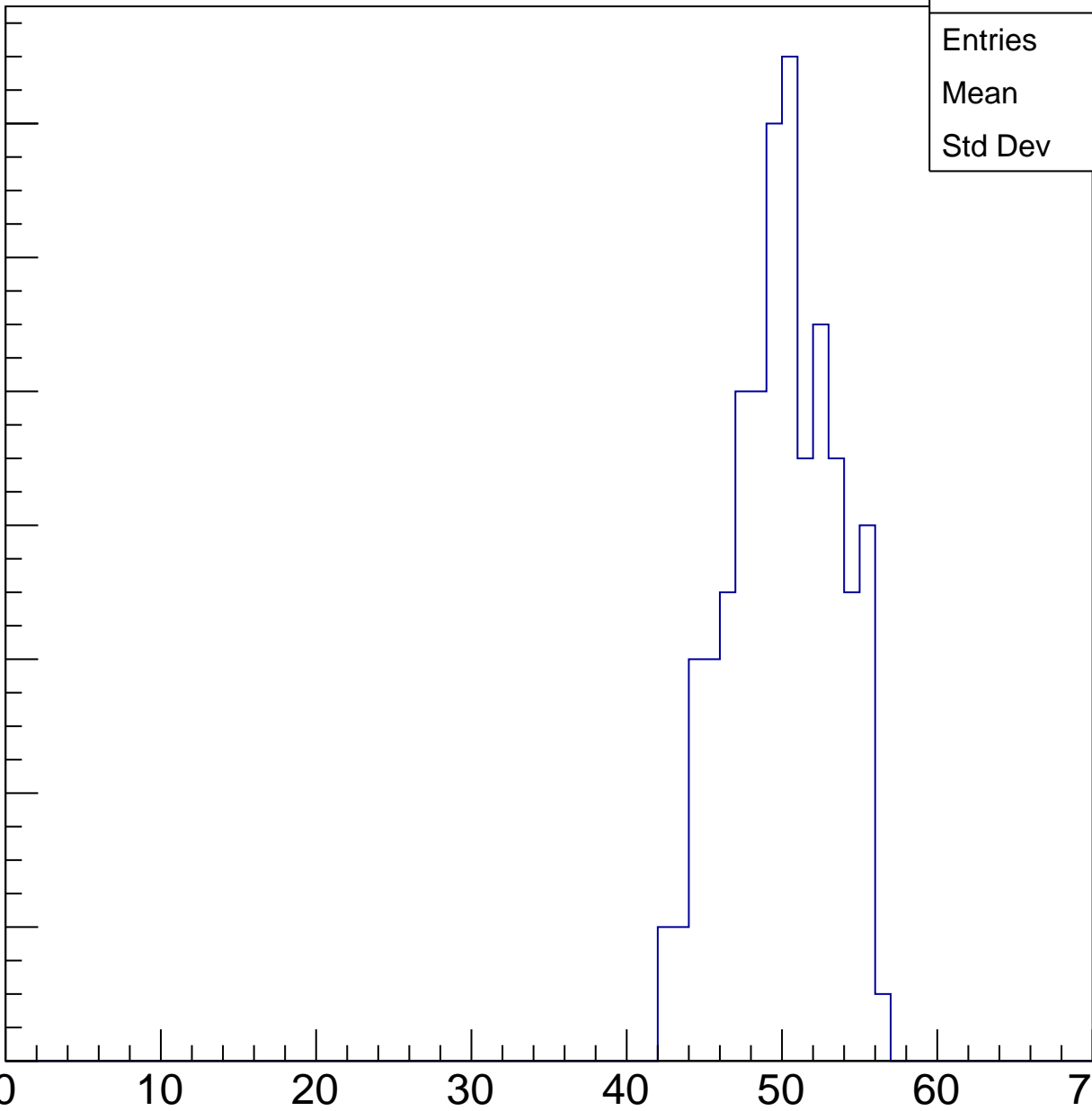
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	49.52
Std Dev	3.337

ampl

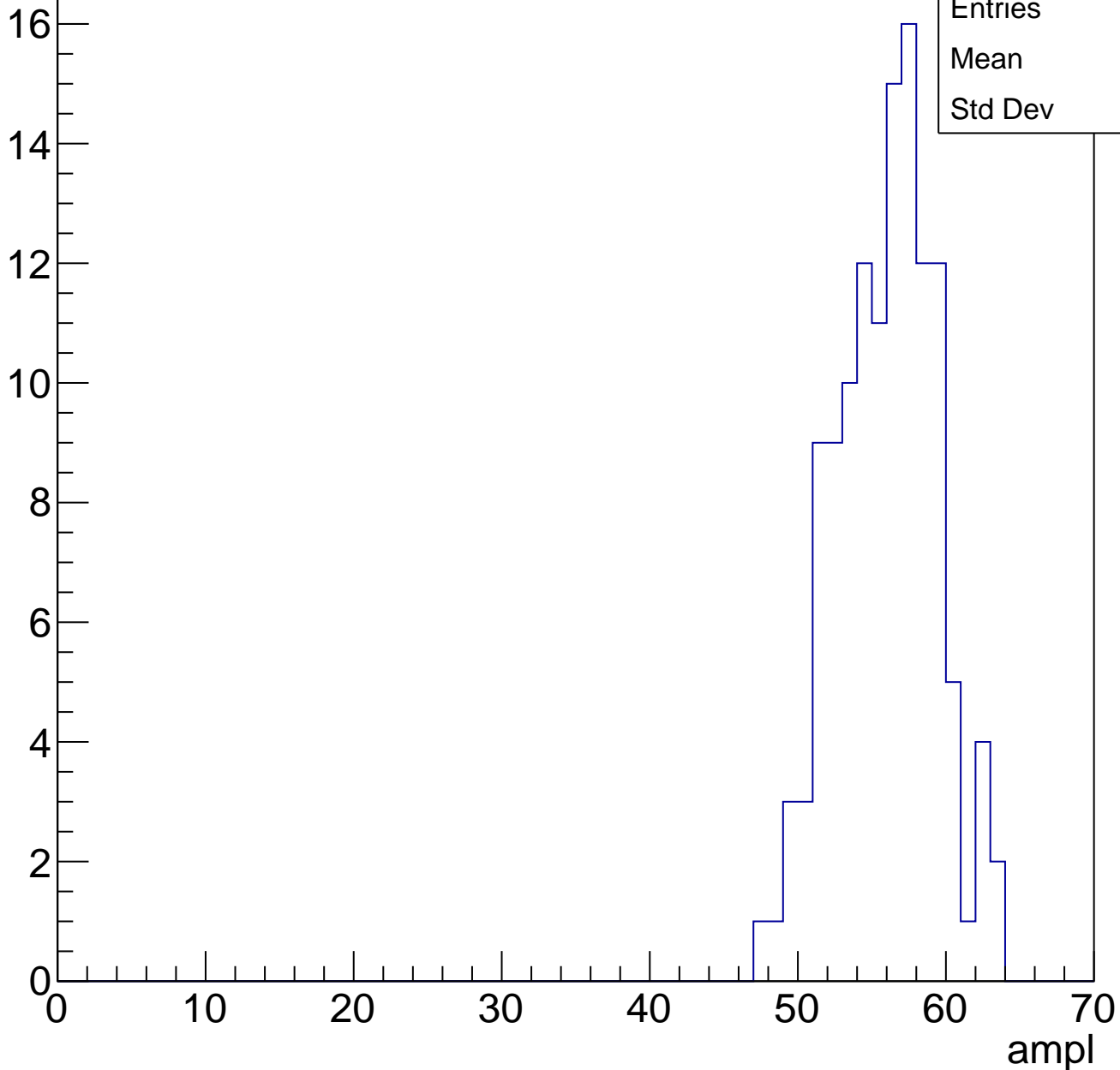


B0L101S, U18-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	55.5
Std Dev	3.34

Entry



B0L101S, U18-ch89, adc5

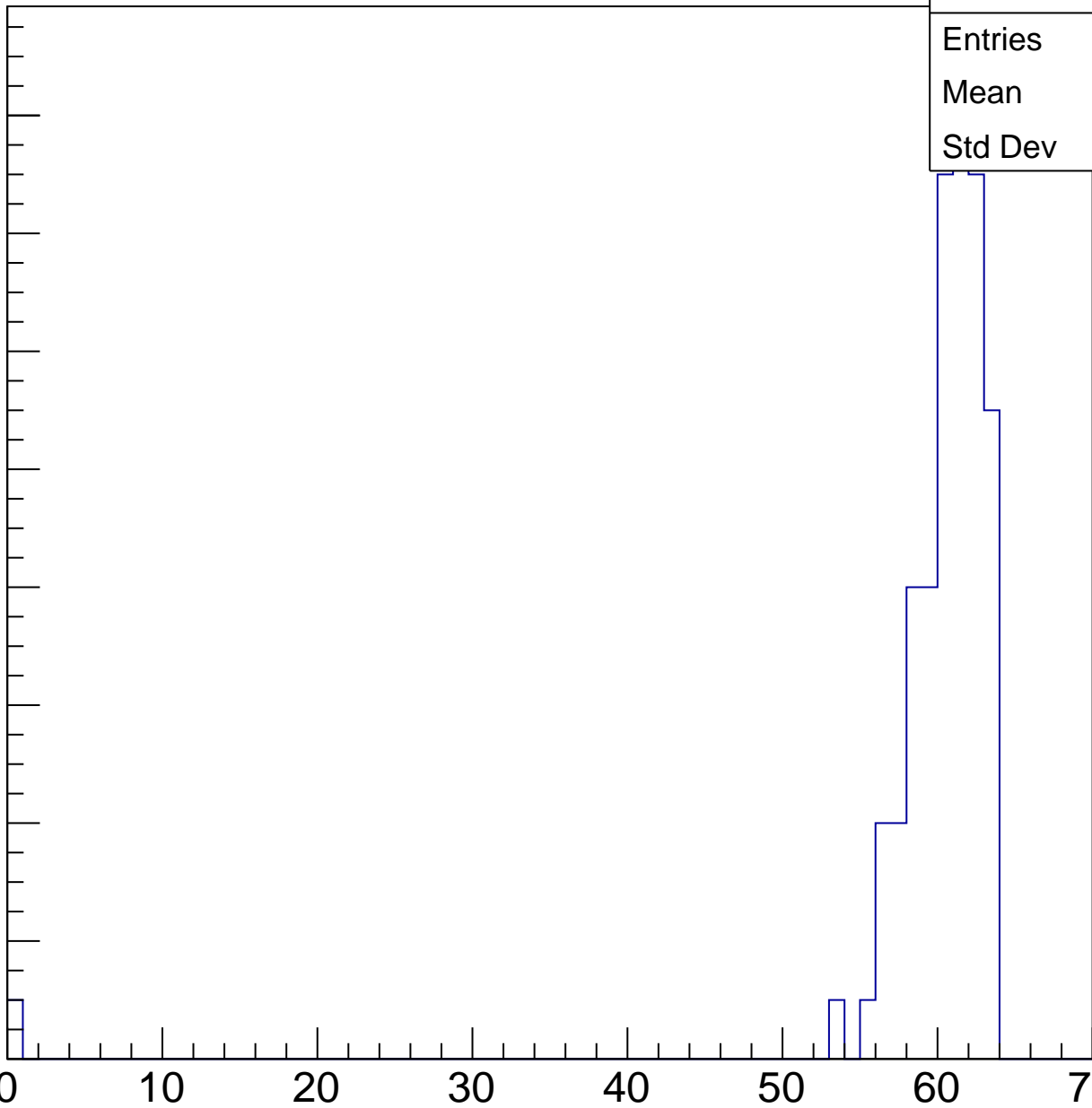
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.48
Std Dev	6.832

ampl

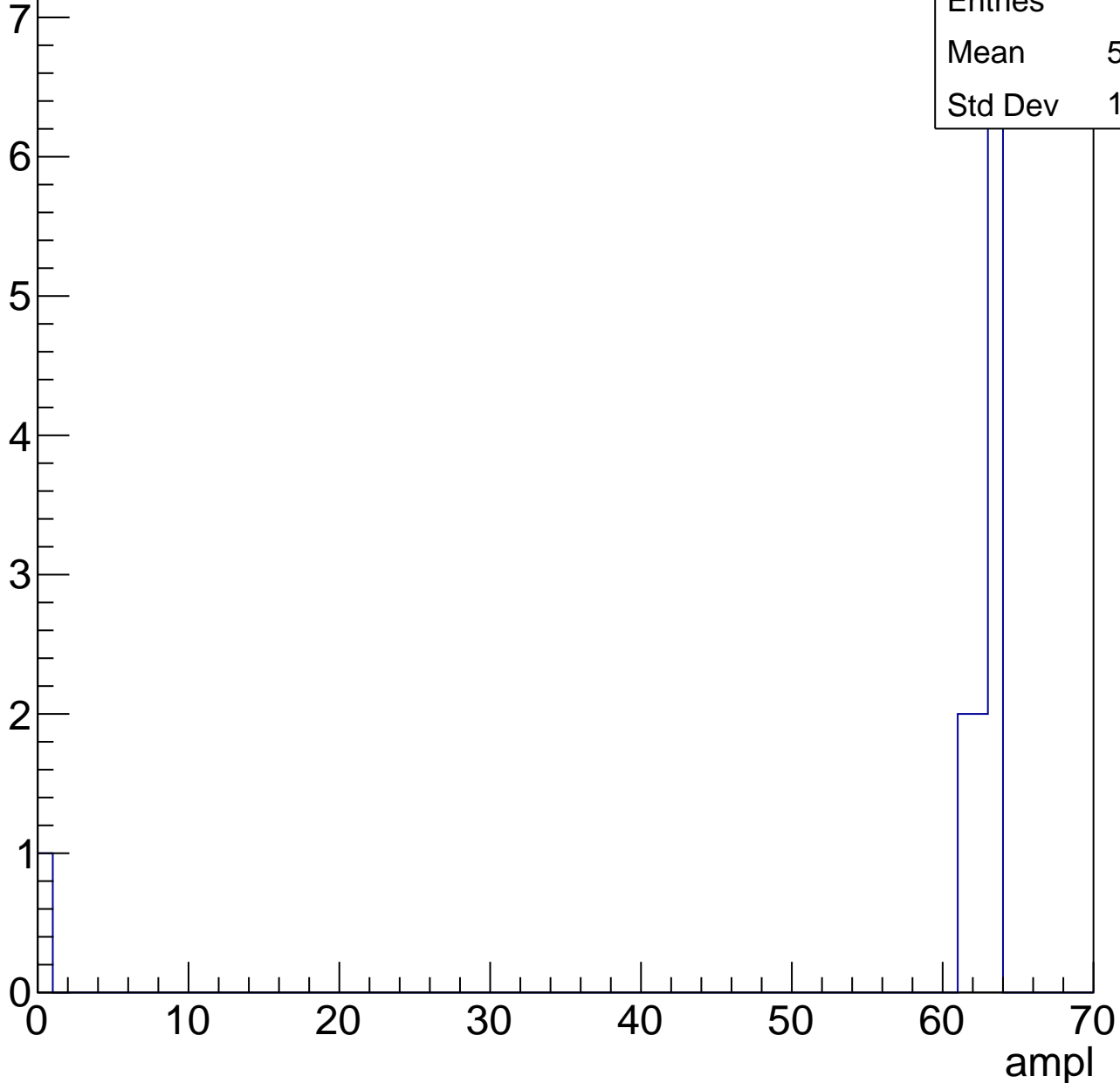


B0L101S, U18-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	57.25
Std Dev	17.28



B0L101S, U18-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch90, adc0

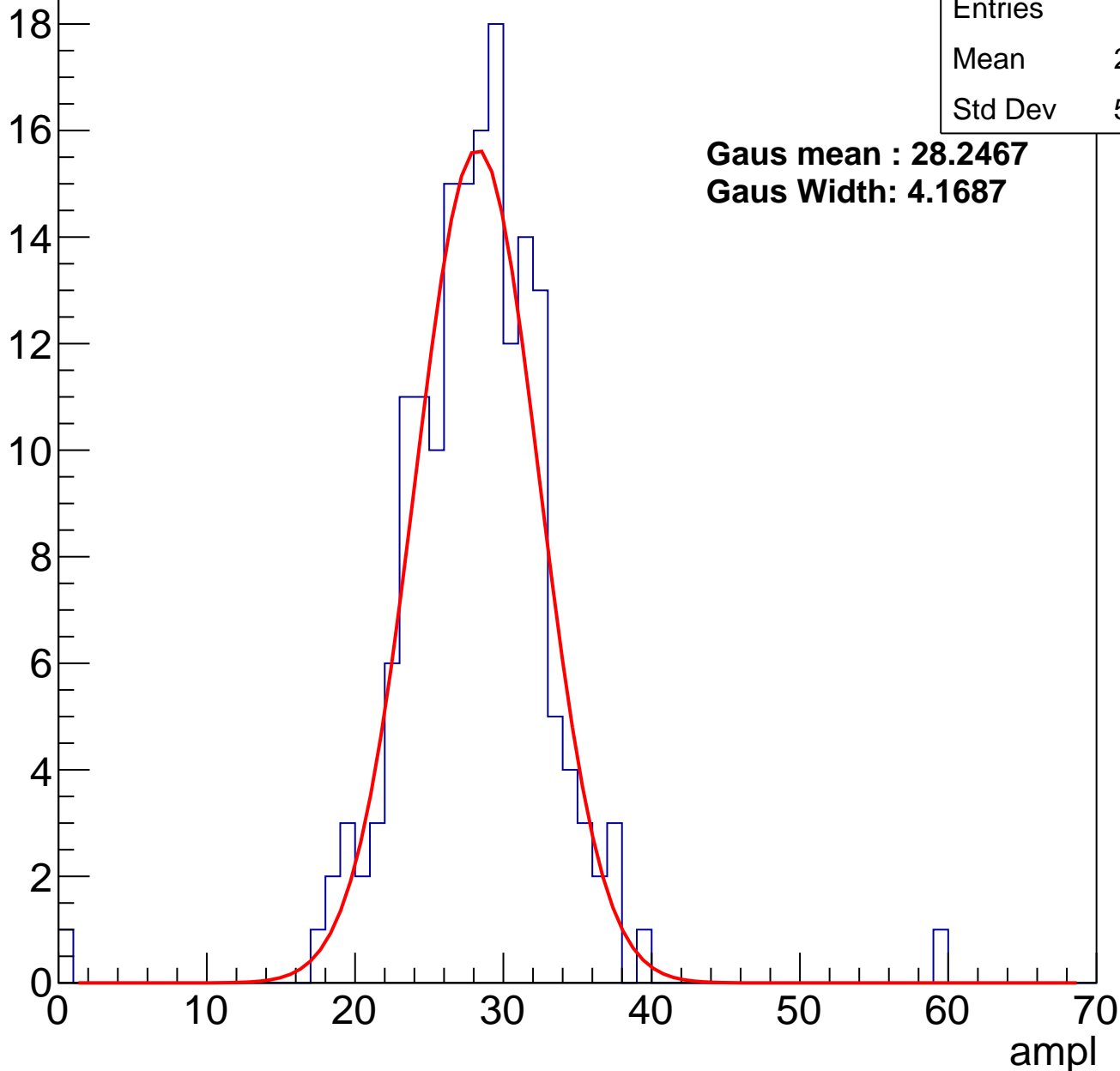
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	27.76
Std Dev	5.235

Gaus mean : 28.2467

Gaus Width: 4.1687

Entry



B0L101S, U18-ch90, adc1

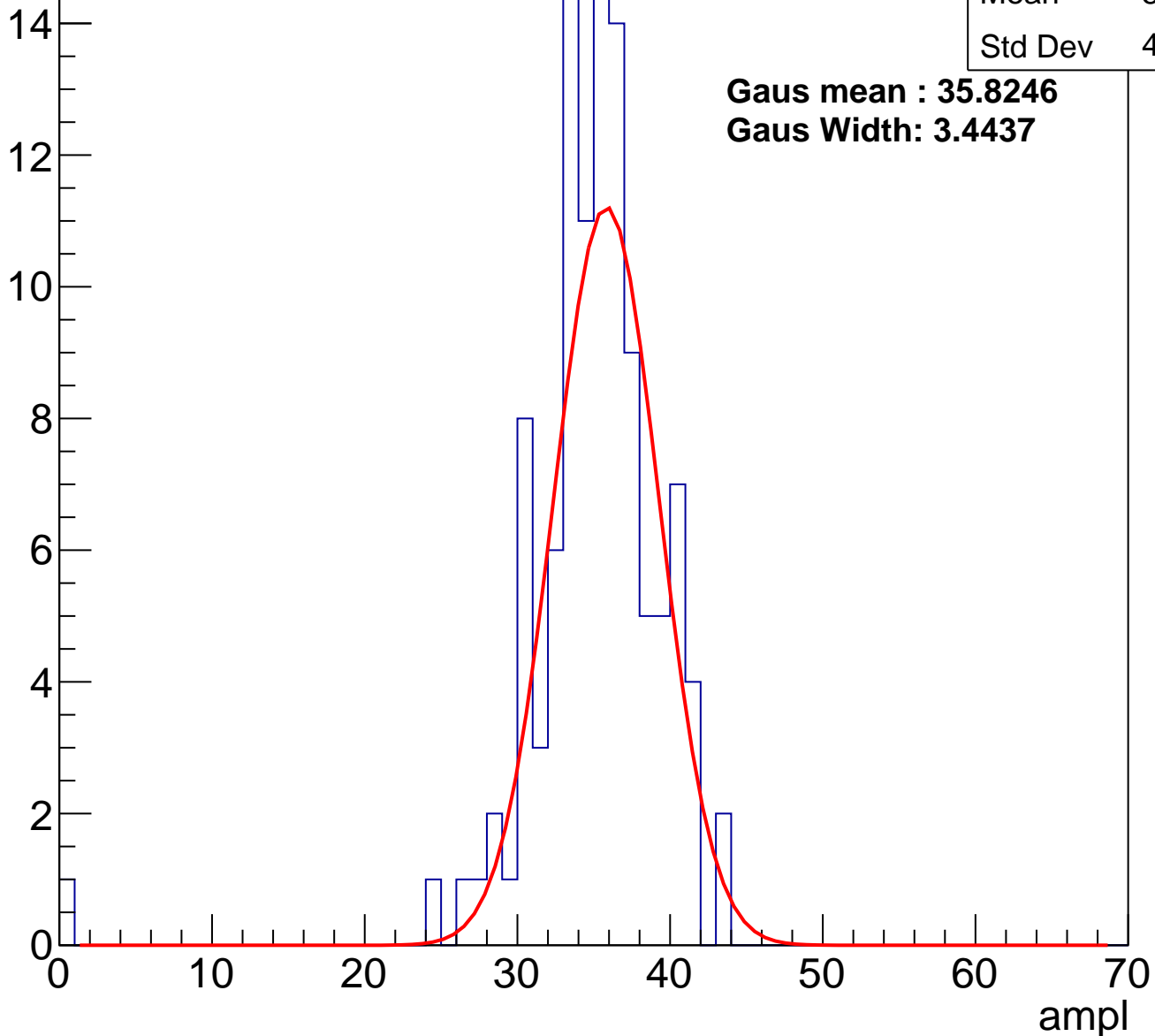
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	34.53
Std Dev	4.836

Gaus mean : 35.8246

Gaus Width: 3.4437

Entry



B0L101S, U18-ch90, adc2

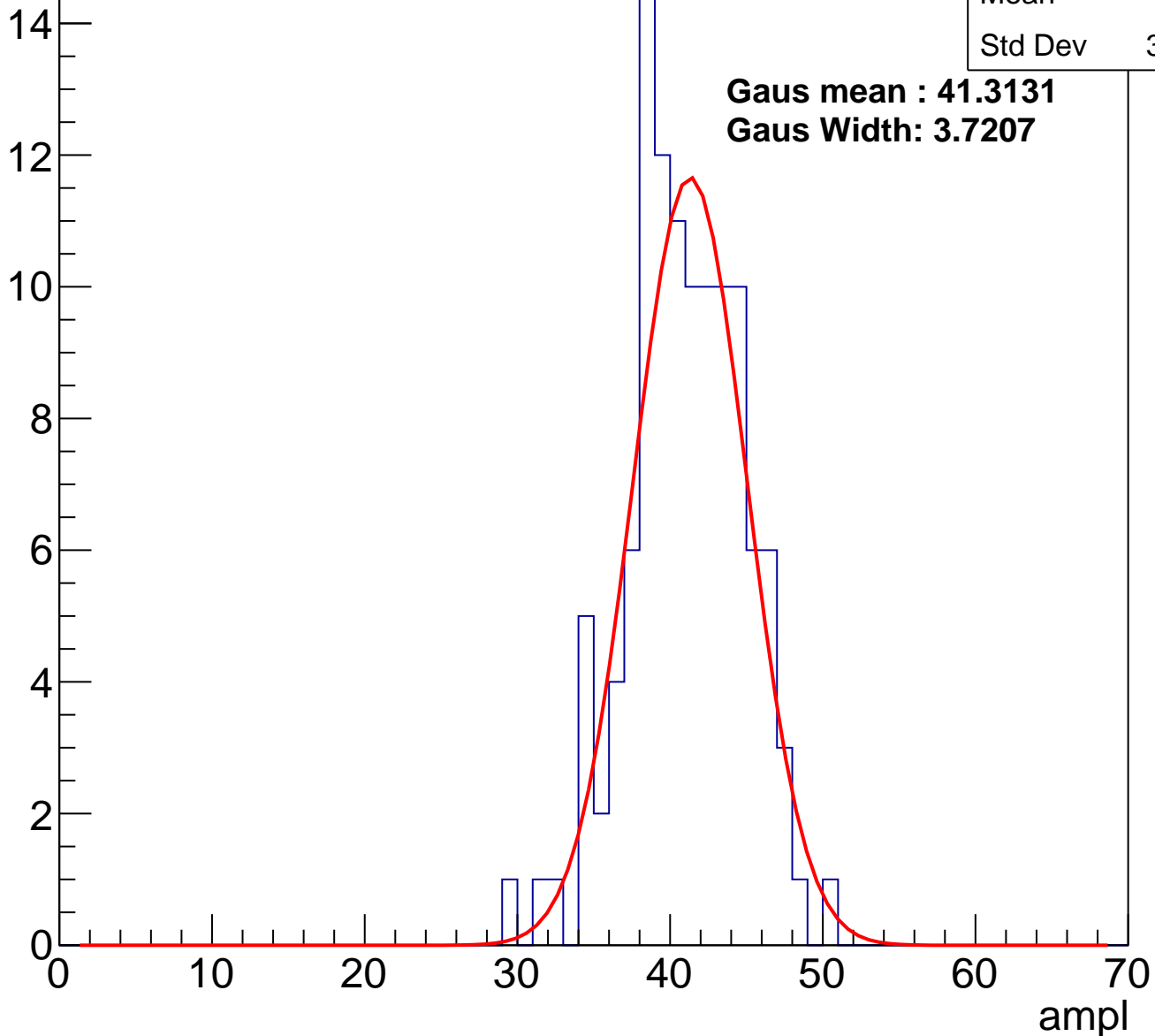
calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	40.53
Std Dev	3.766

Gaus mean : 41.3131

Gaus Width: 3.7207

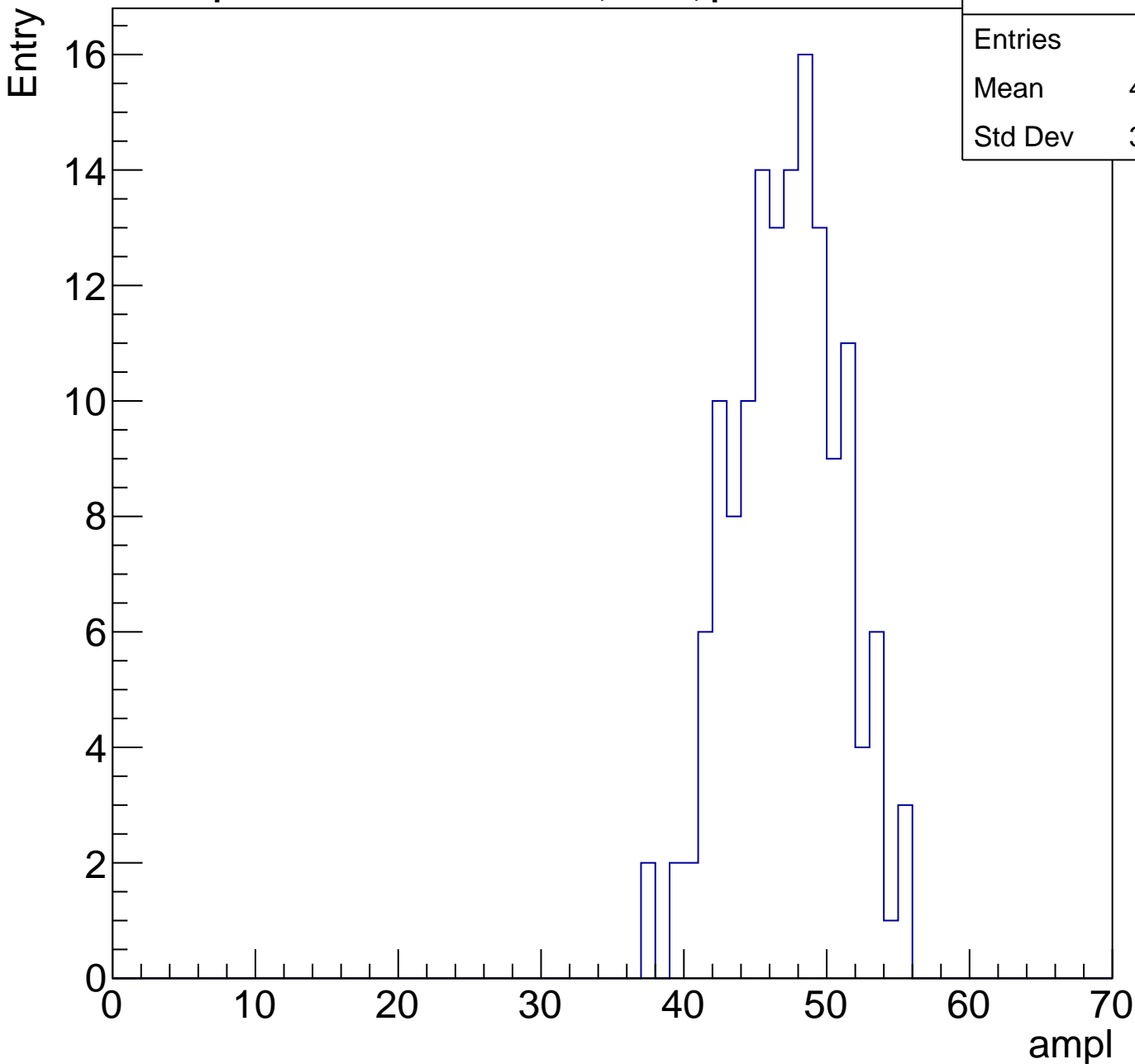
Entry



B0L101S, U18-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

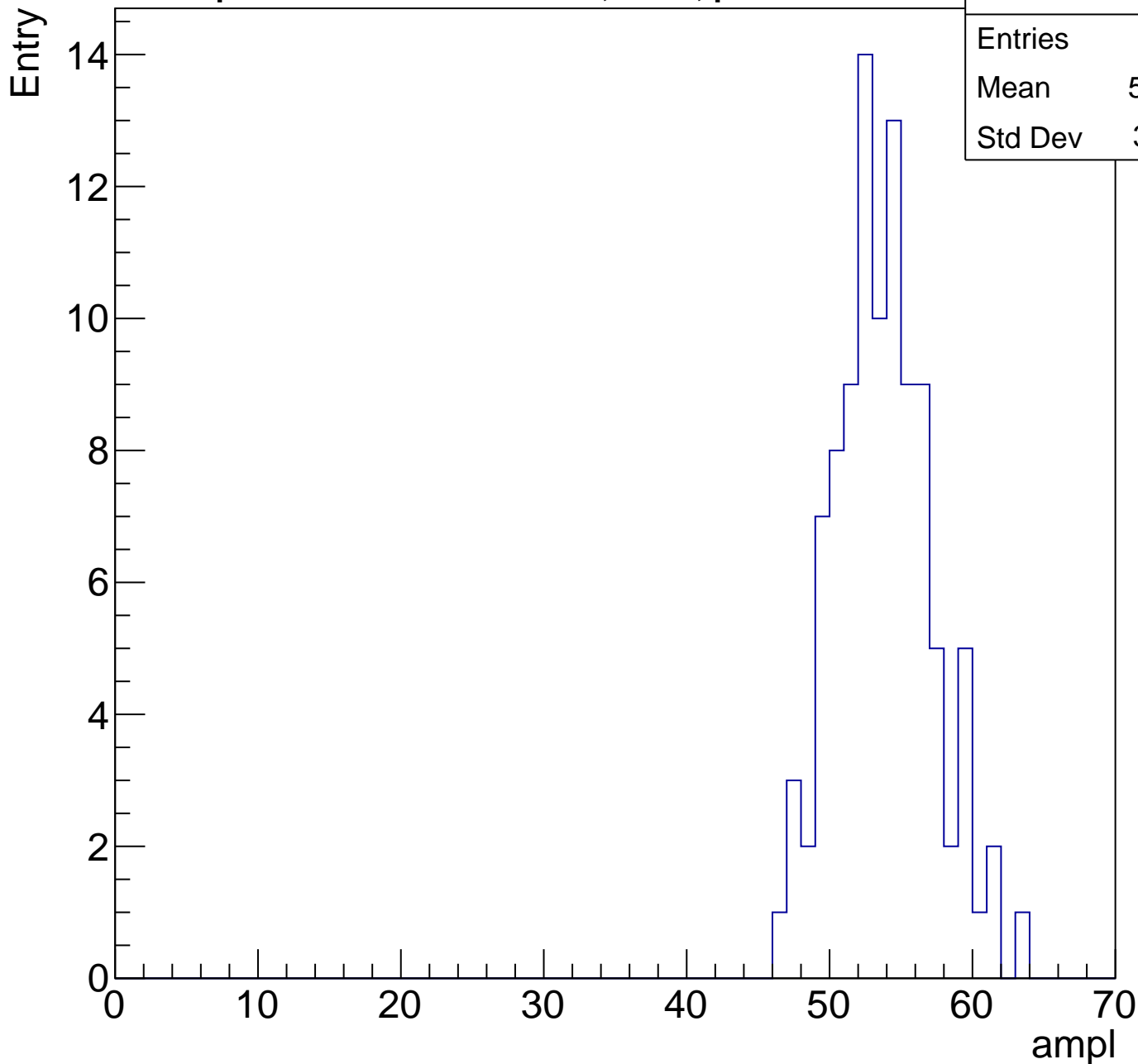
Entries	144
Mean	46.73
Std Dev	3.773



B0L101S, U18-ch90, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	53.32
Std Dev	3.371



B0L101S, U18-ch90, adc5

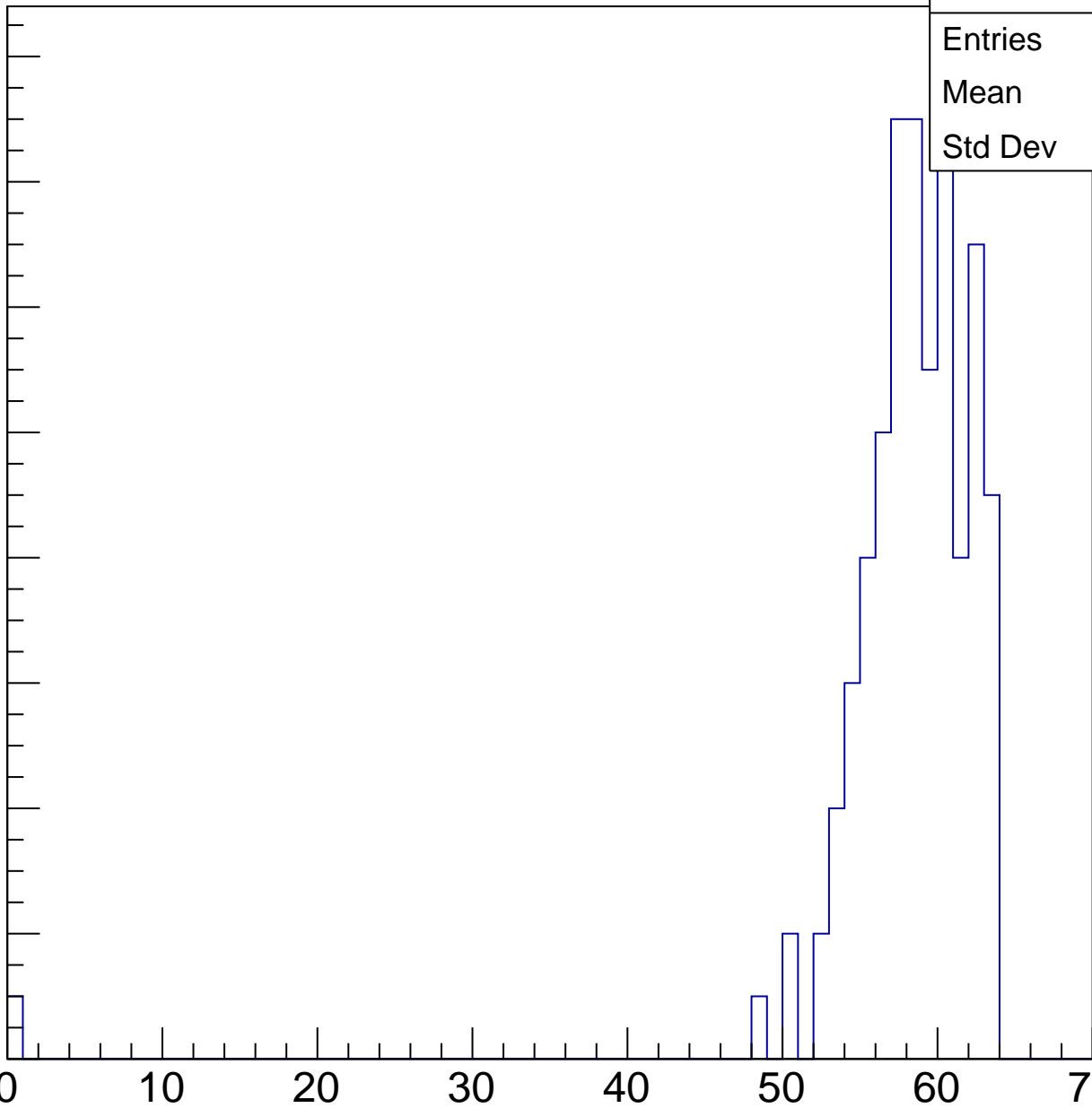
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	57.71
Std Dev	6.137

ampl

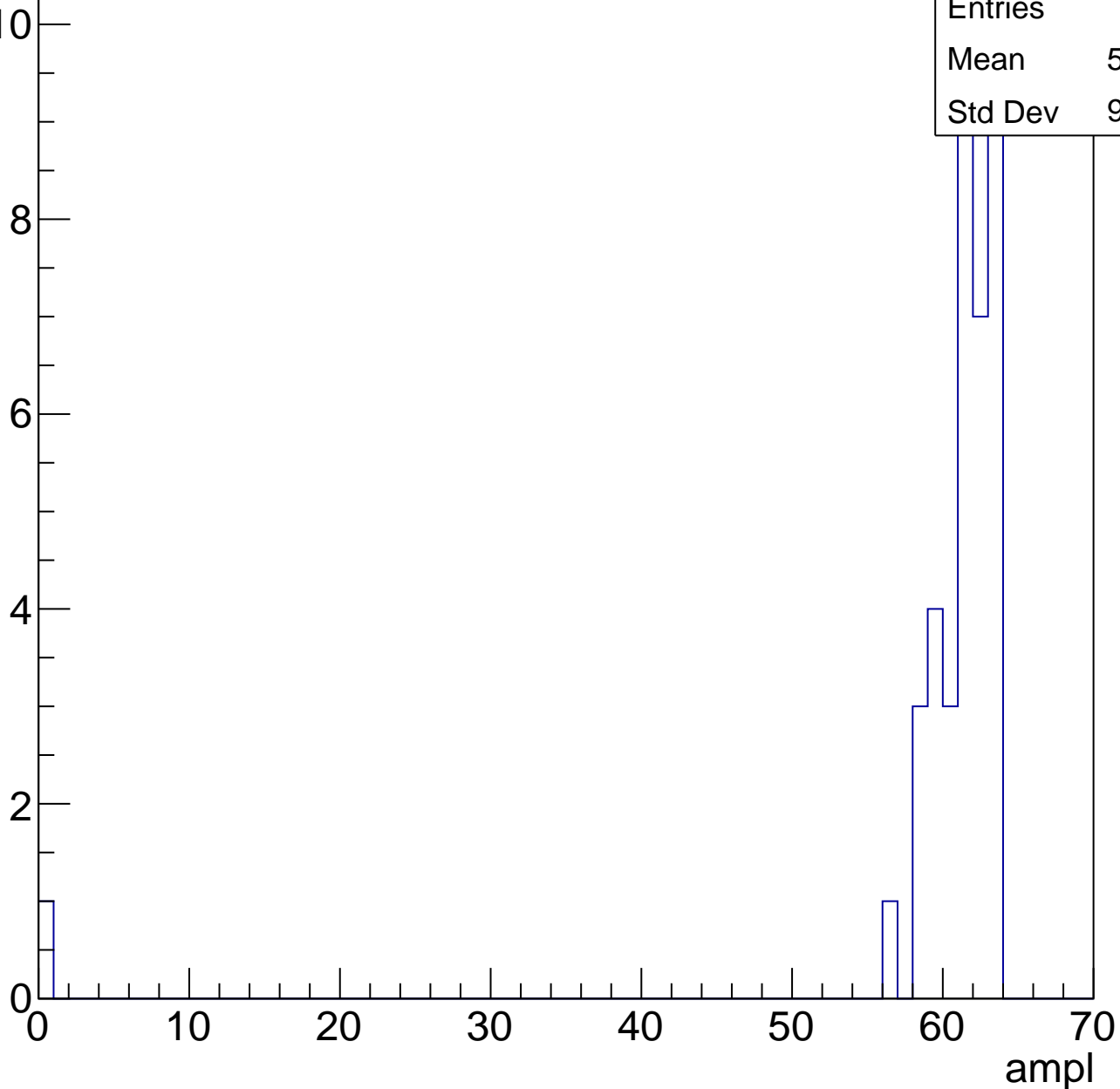


B0L101S, U18-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	38
Mean	59.39
Std Dev	9.917



B0L101S, U18-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L101S, U18-ch91, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	27.75
Std Dev	4.82

Gaus mean : 28.3902

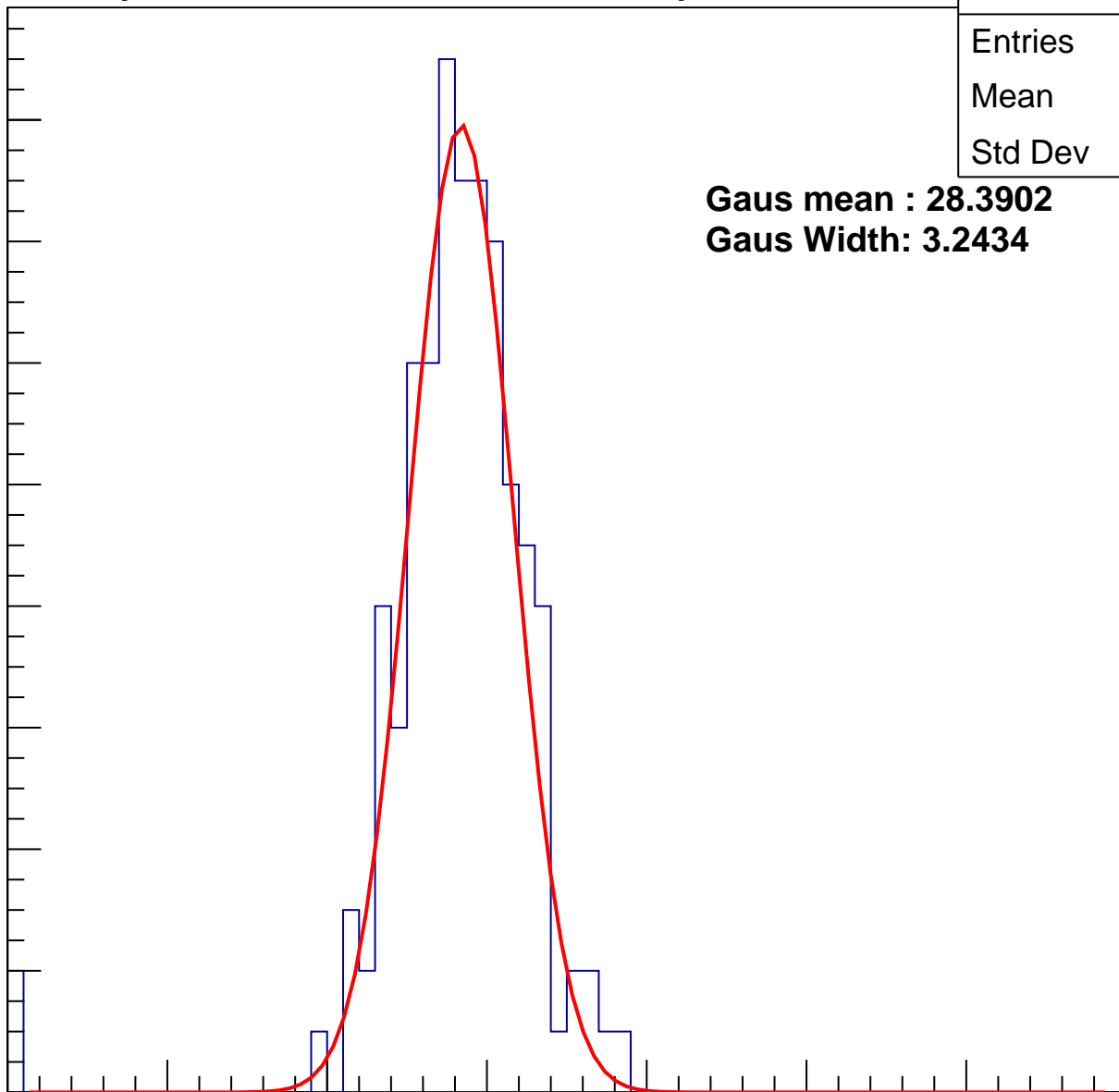
Gaus Width: 3.2434

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch91, adc1

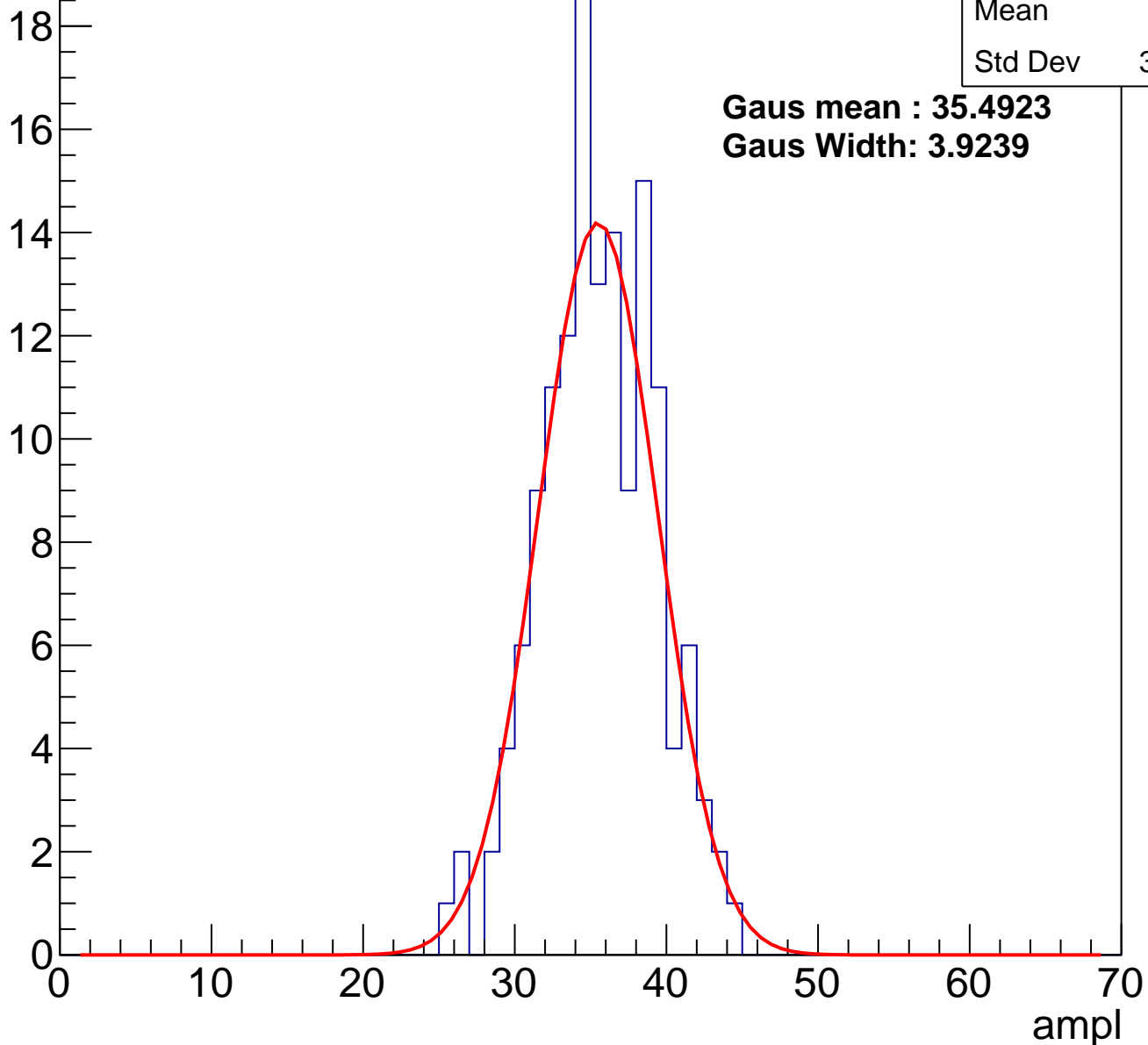
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	35.1
Std Dev	3.723

Gaus mean : 35.4923

Gaus Width: 3.9239

Entry



B0L101S, U18-ch91, adc2

calib_packv5_042523_0143.root, FC#1, port C1

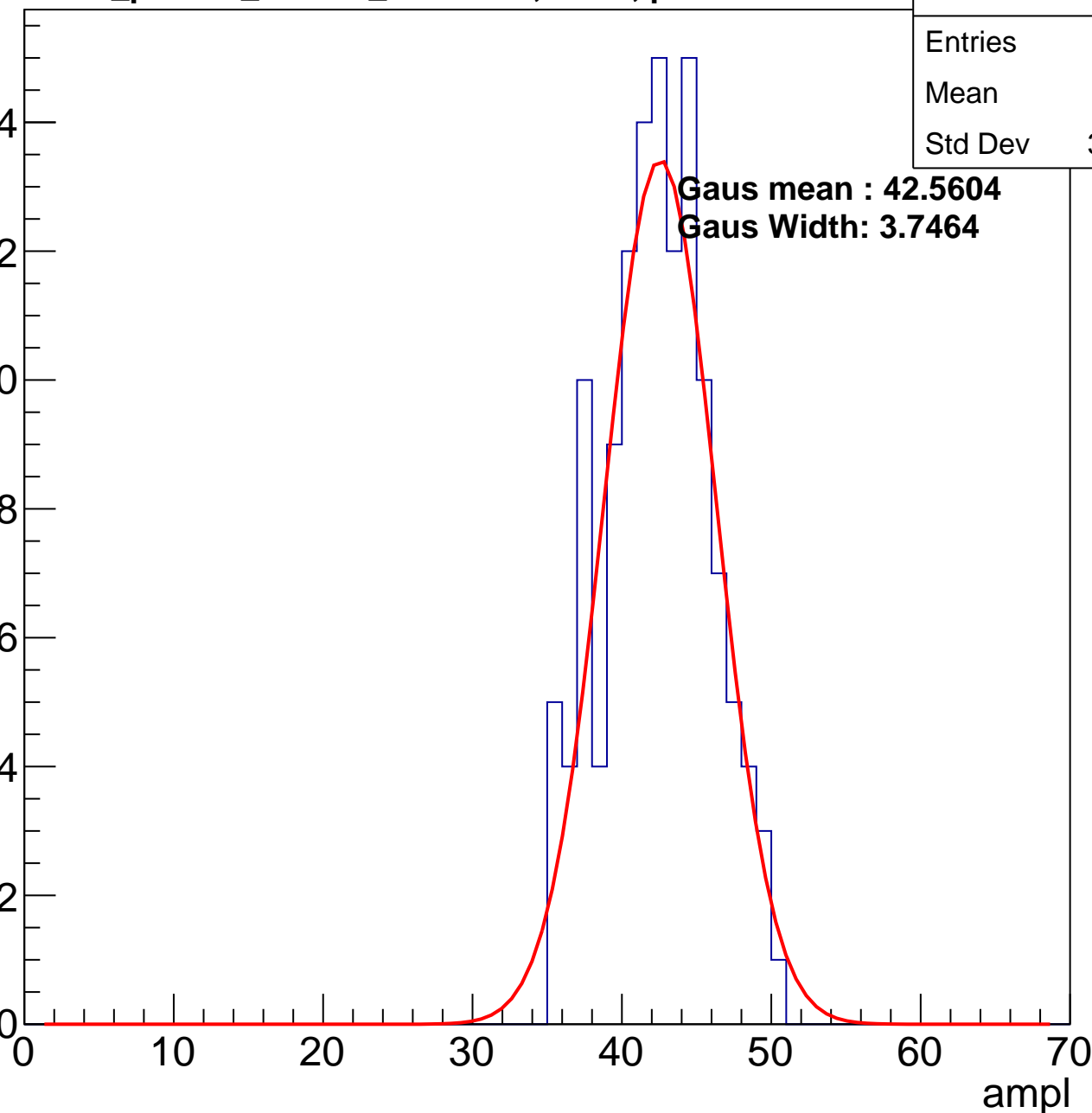
Entry

14
12
10
8
6
4
2
0

Entries	130
Mean	41.91
Std Dev	3.507

Gaus mean : 42.5604

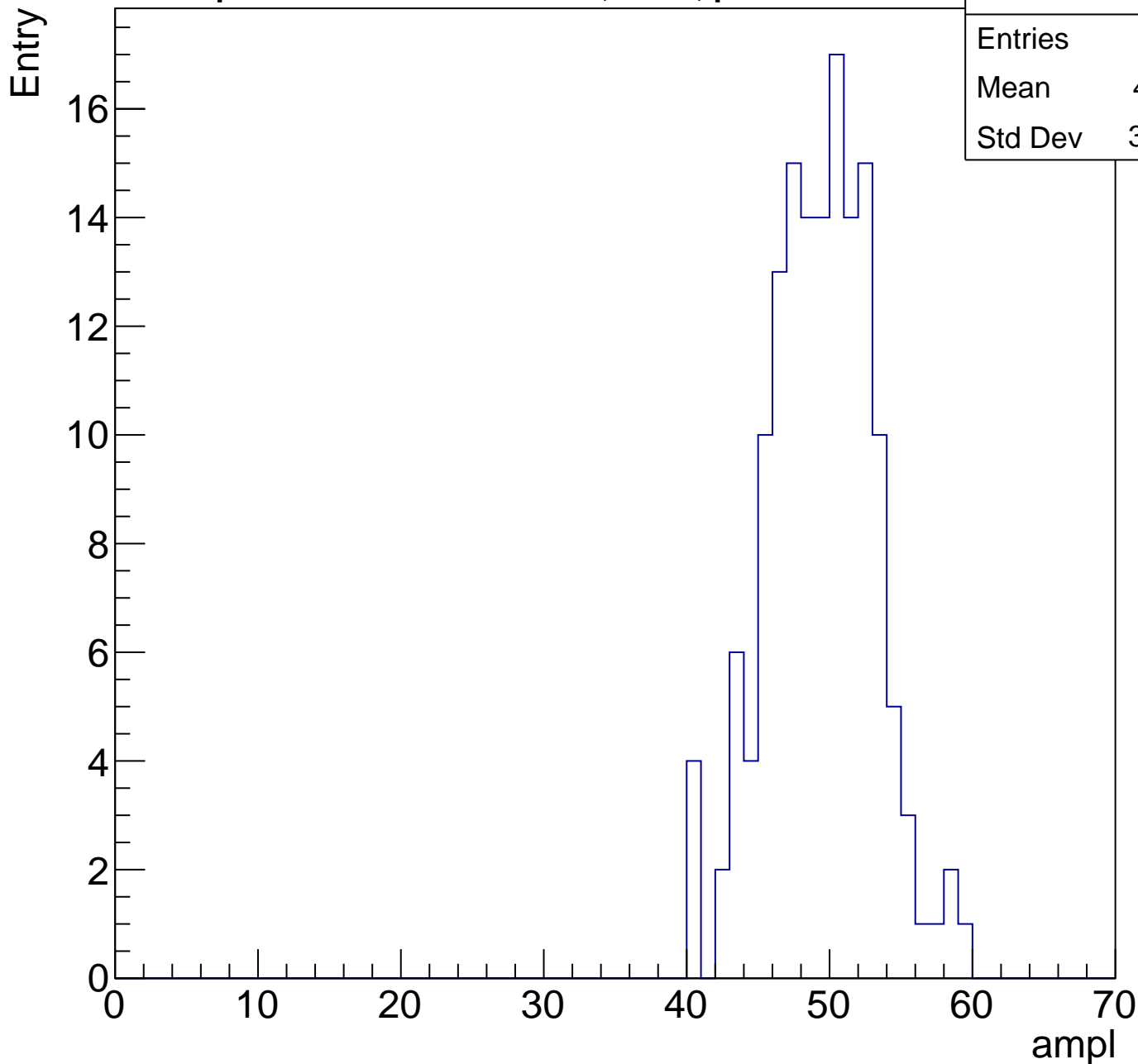
Gaus Width: 3.7464



B0L101S, U18-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	48.91
Std Dev	3.697



B0L101S, U18-ch91, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

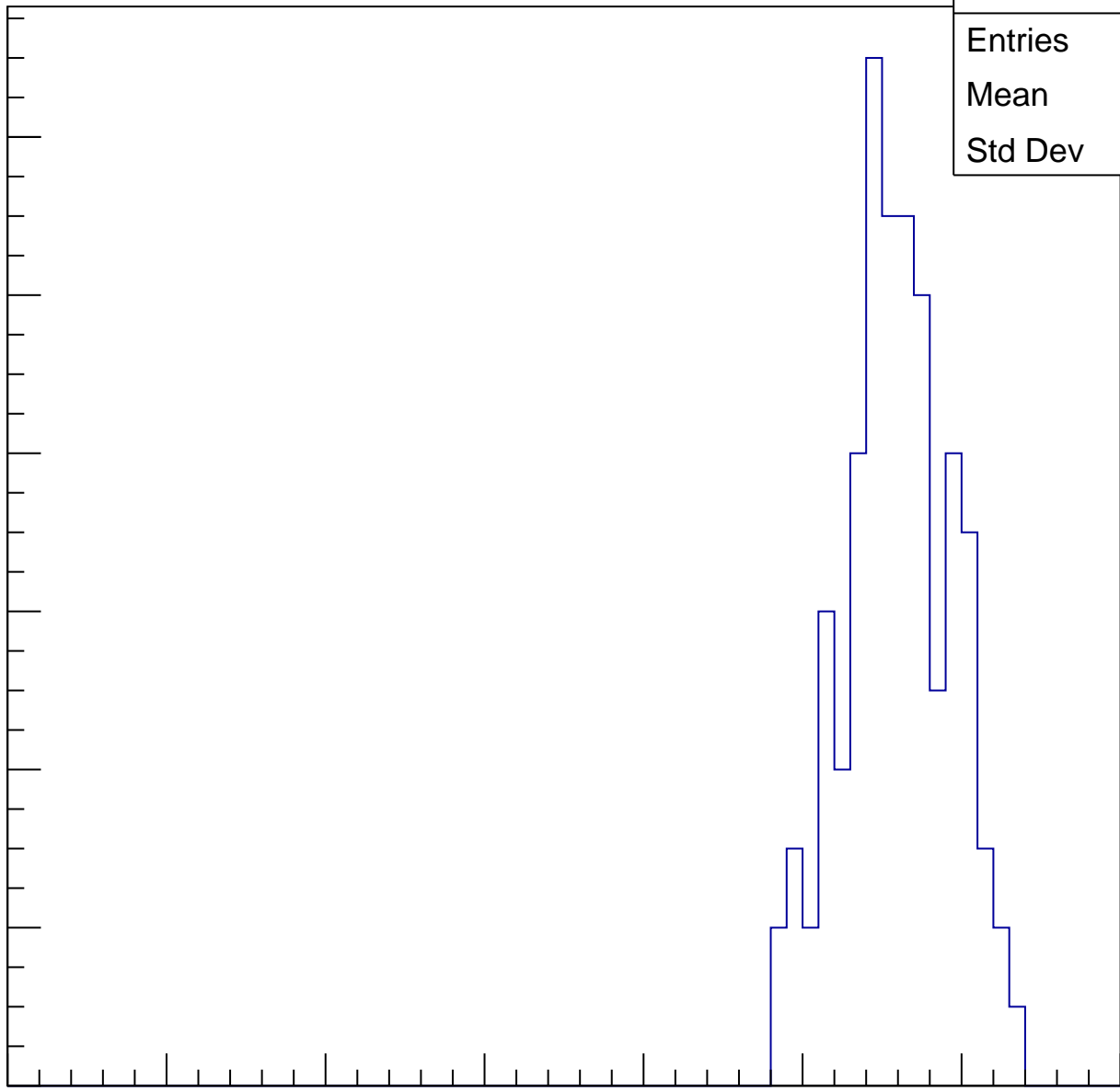
2

0

Entries	96
Mean	55.48
Std Dev	3.354

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch91, adc5

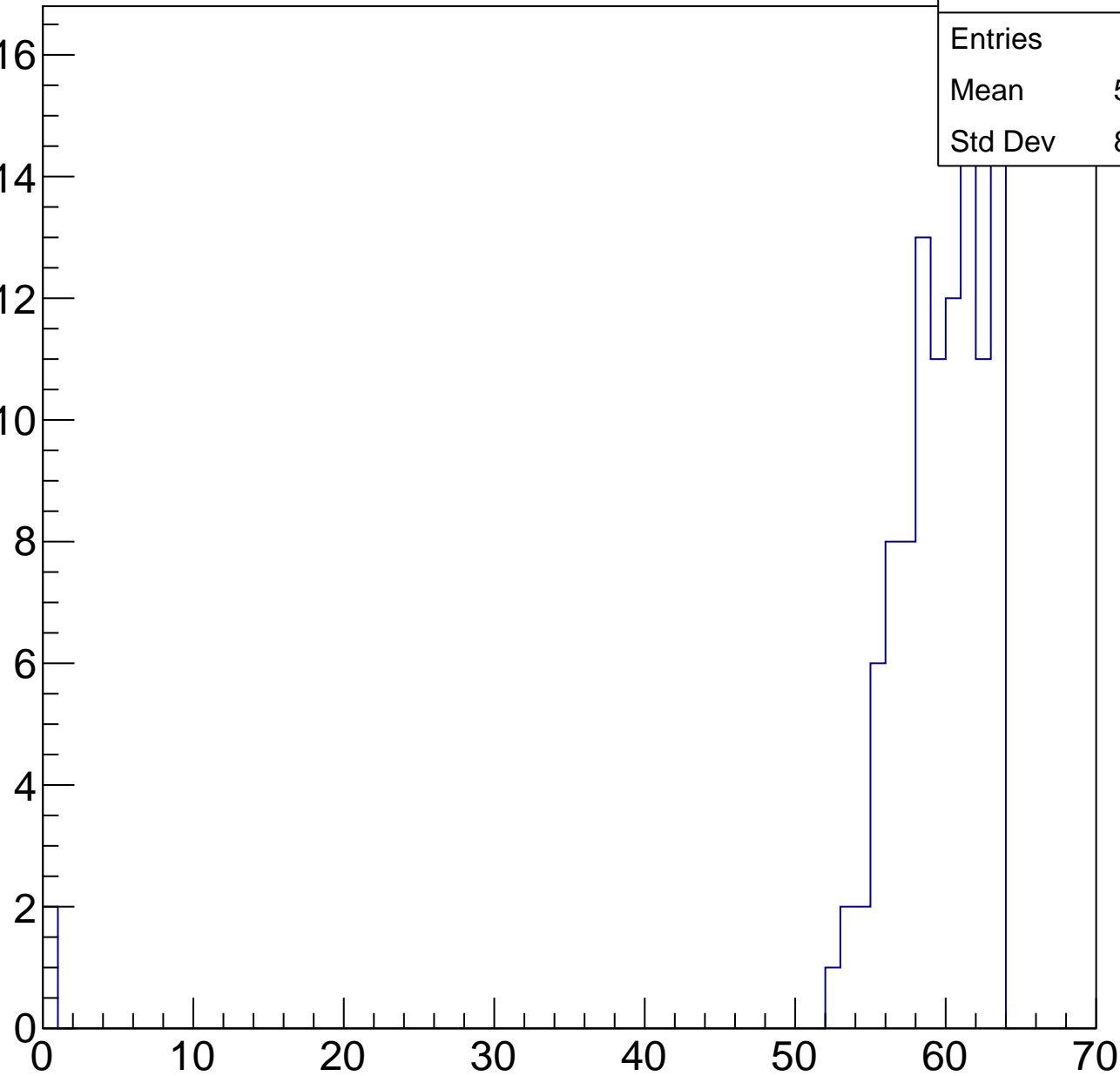
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.19
Std Dev	8.478

ampl

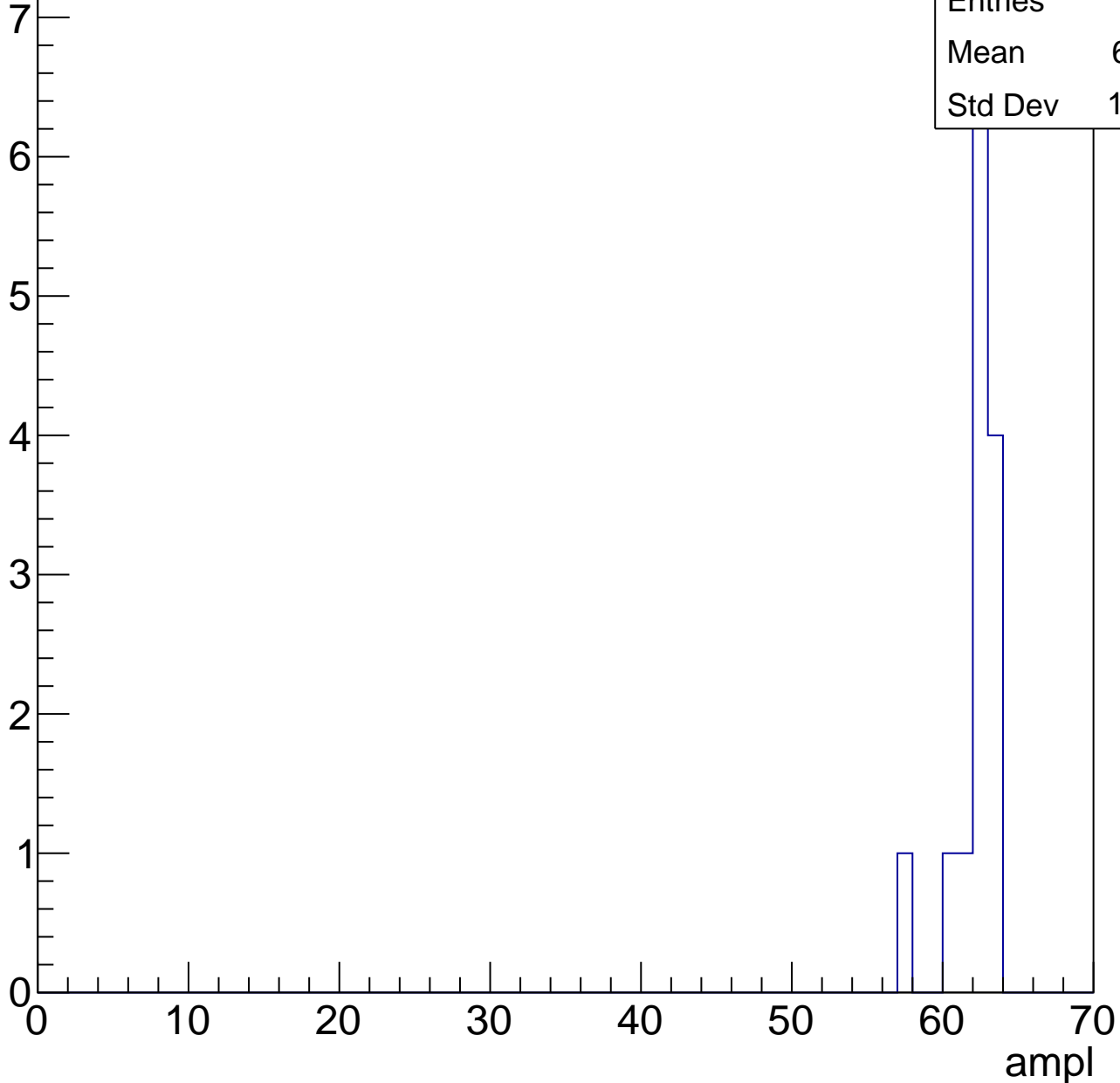


B0L101S, U18-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	61.71
Std Dev	1.532



B0L101S, U18-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U18-ch92, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	28.3
Std Dev	4.518

Gaus mean : 29.2362

Gaus Width: 3.4139

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

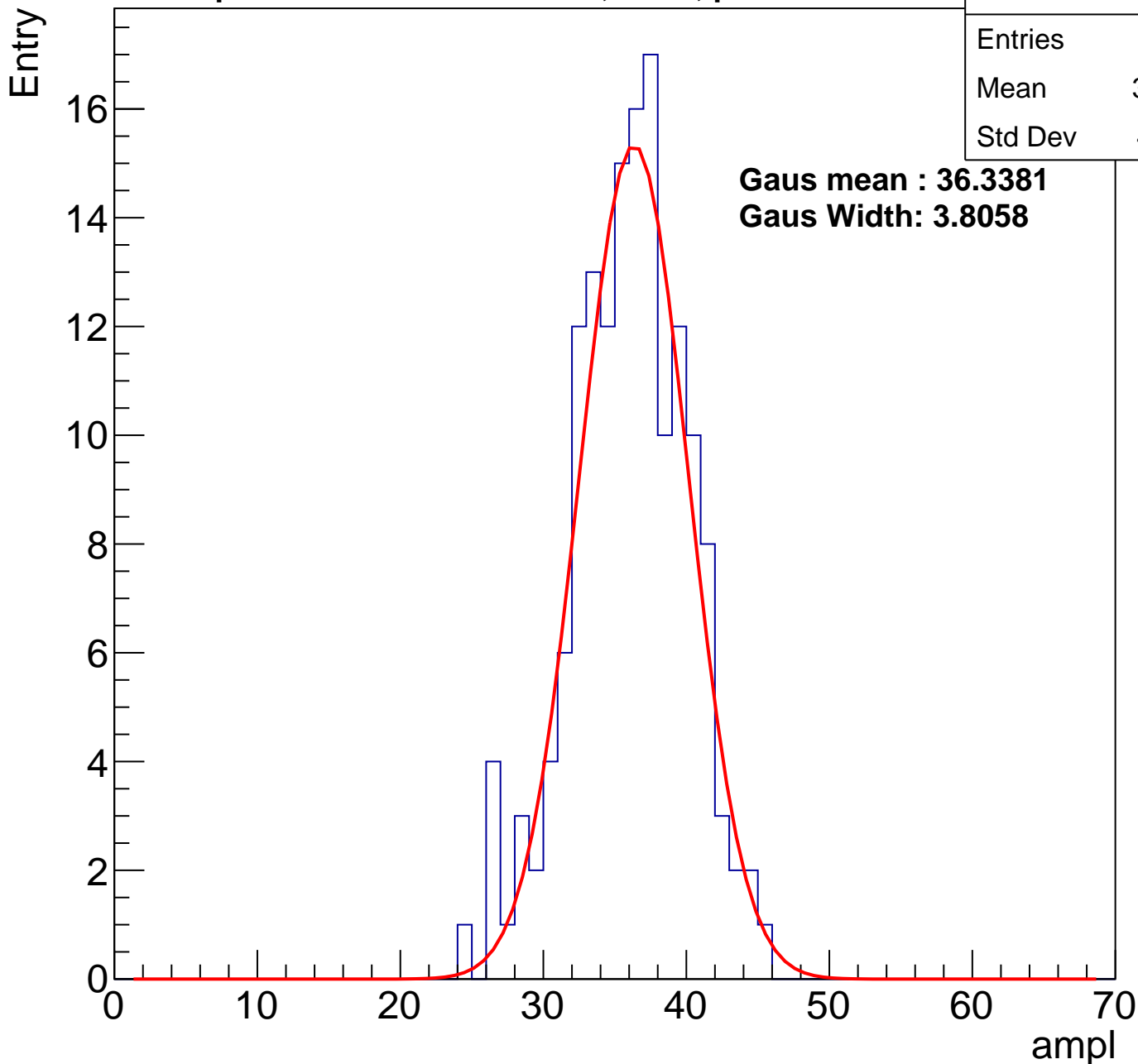
70

B0L101S, U18-ch92, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	35.55
Std Dev	4.021

Gaus mean : 36.3381
Gaus Width: 3.8058



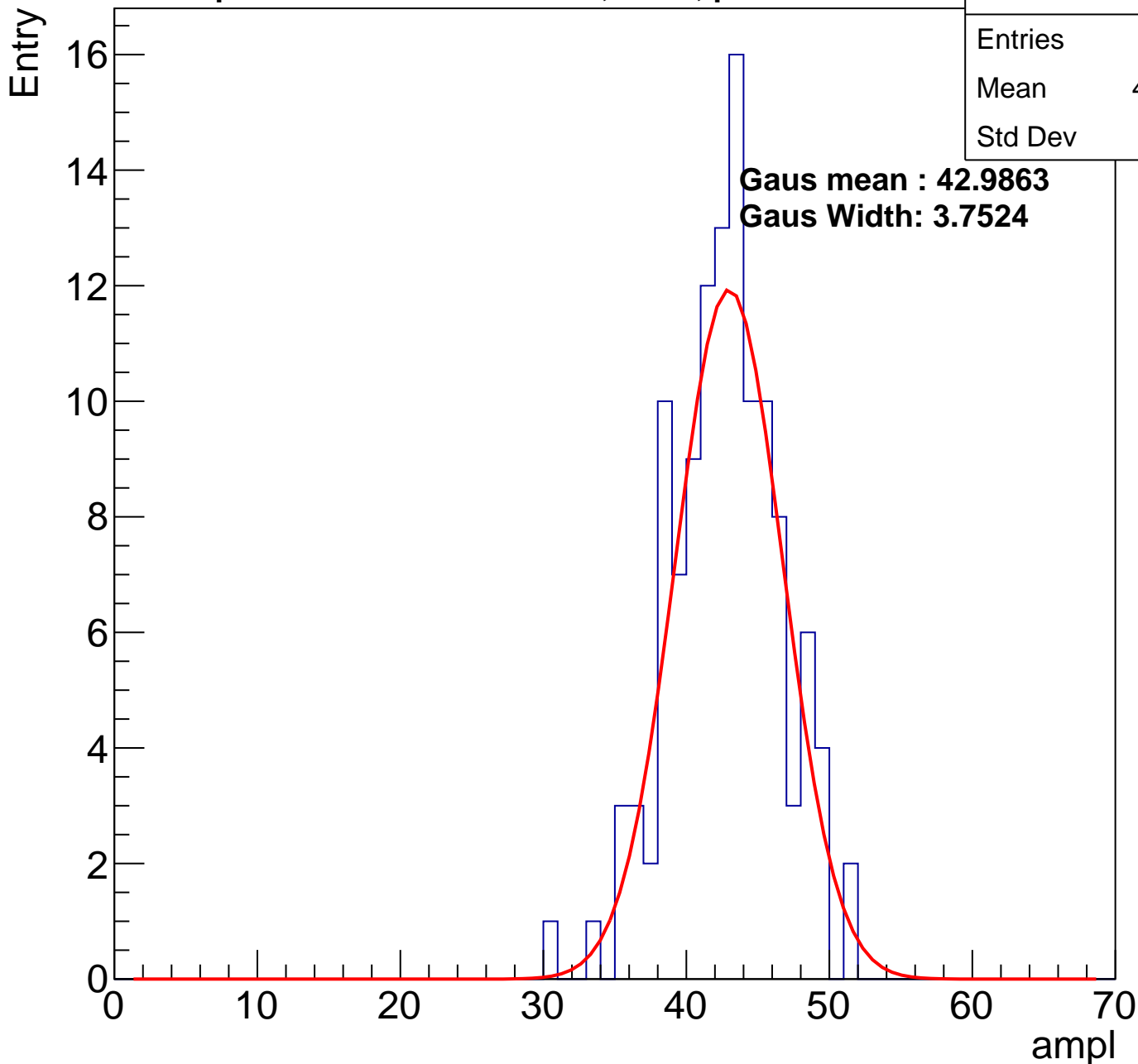
B0L101S, U18-ch92, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	42.28
Std Dev	3.78

Gaus mean : 42.9863

Gaus Width: 3.7524



B0L101S, U18-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

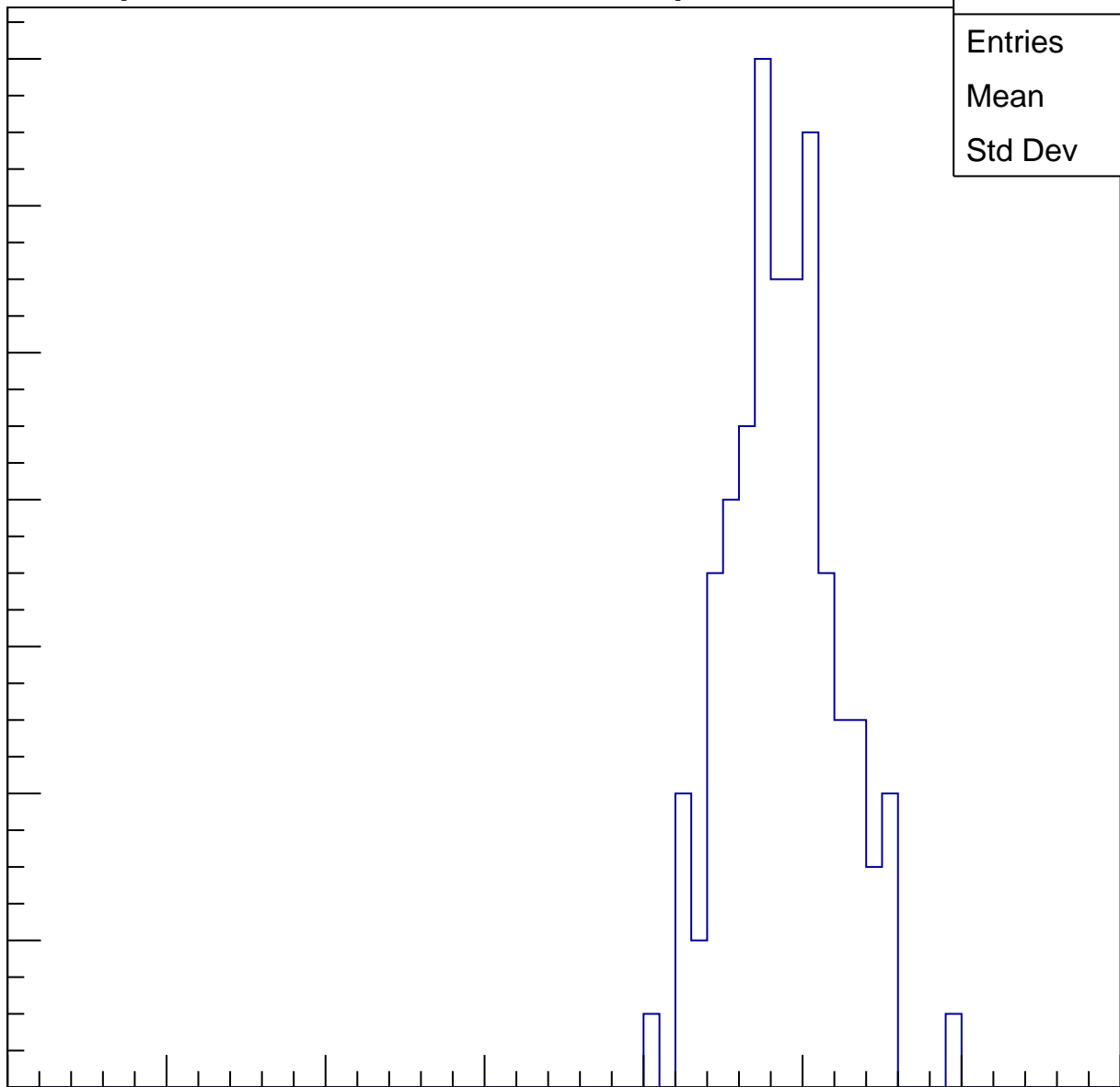
Entries	105
Mean	48.32
Std Dev	3.432

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

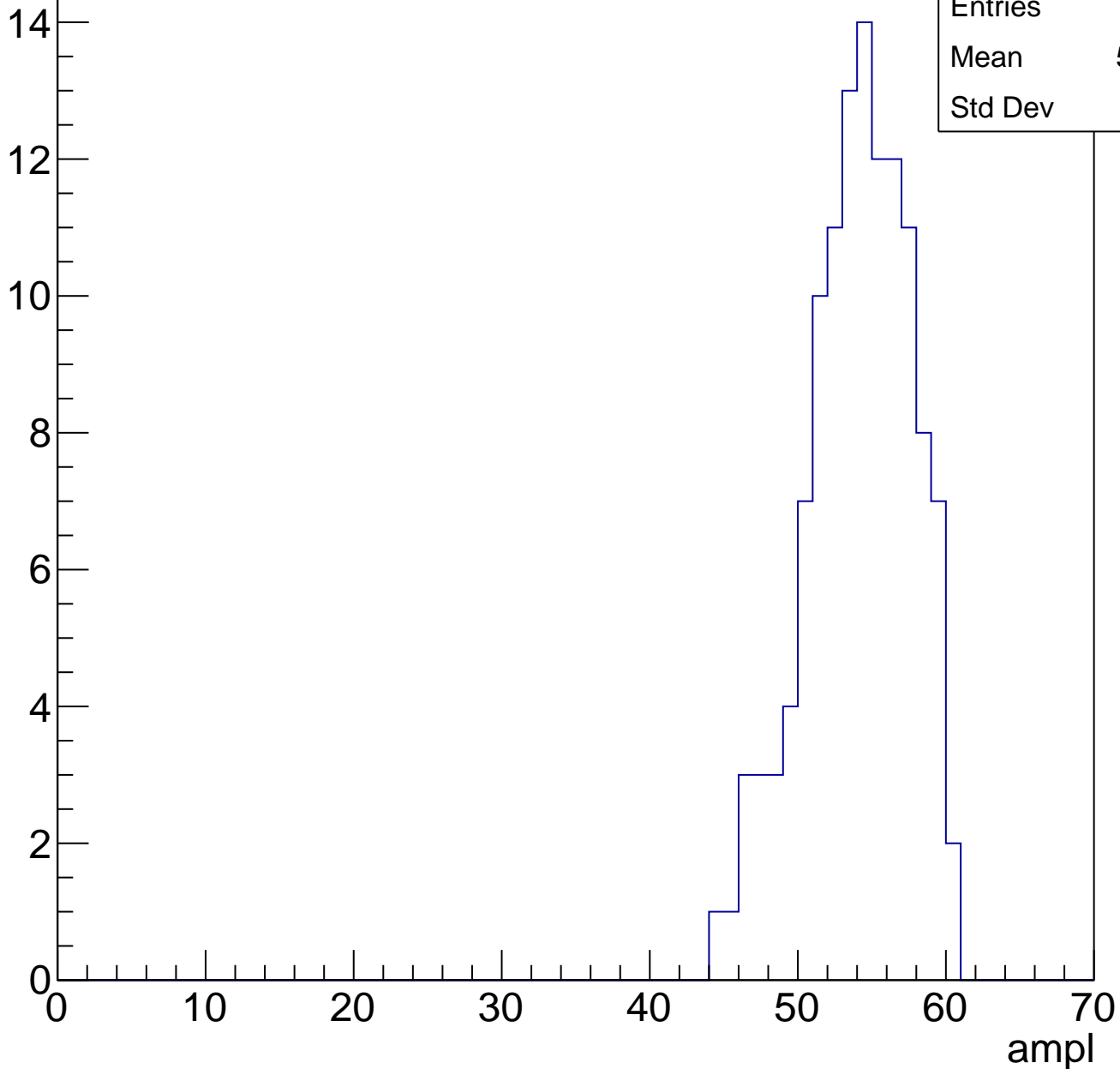
ampl



B0L101S, U18-ch92, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch92, adc5

calib_packv5_042523_0143.root, FC#1, port C1

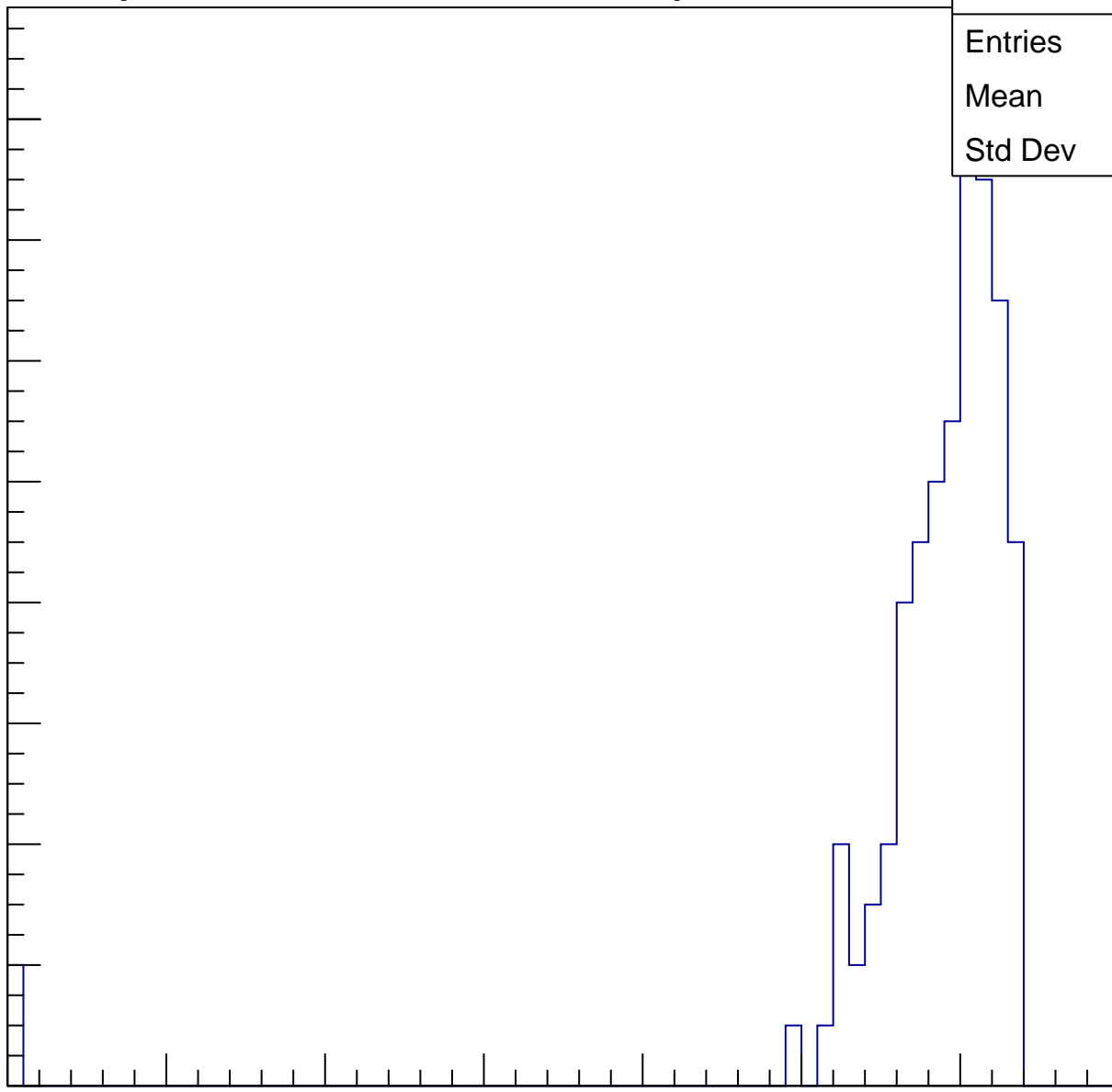
Entries	109
Mean	57.74
Std Dev	8.468

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	25
Mean	61.68
Std Dev	1.516

Entry

10

8

6

4

2

0

0

10

20

30

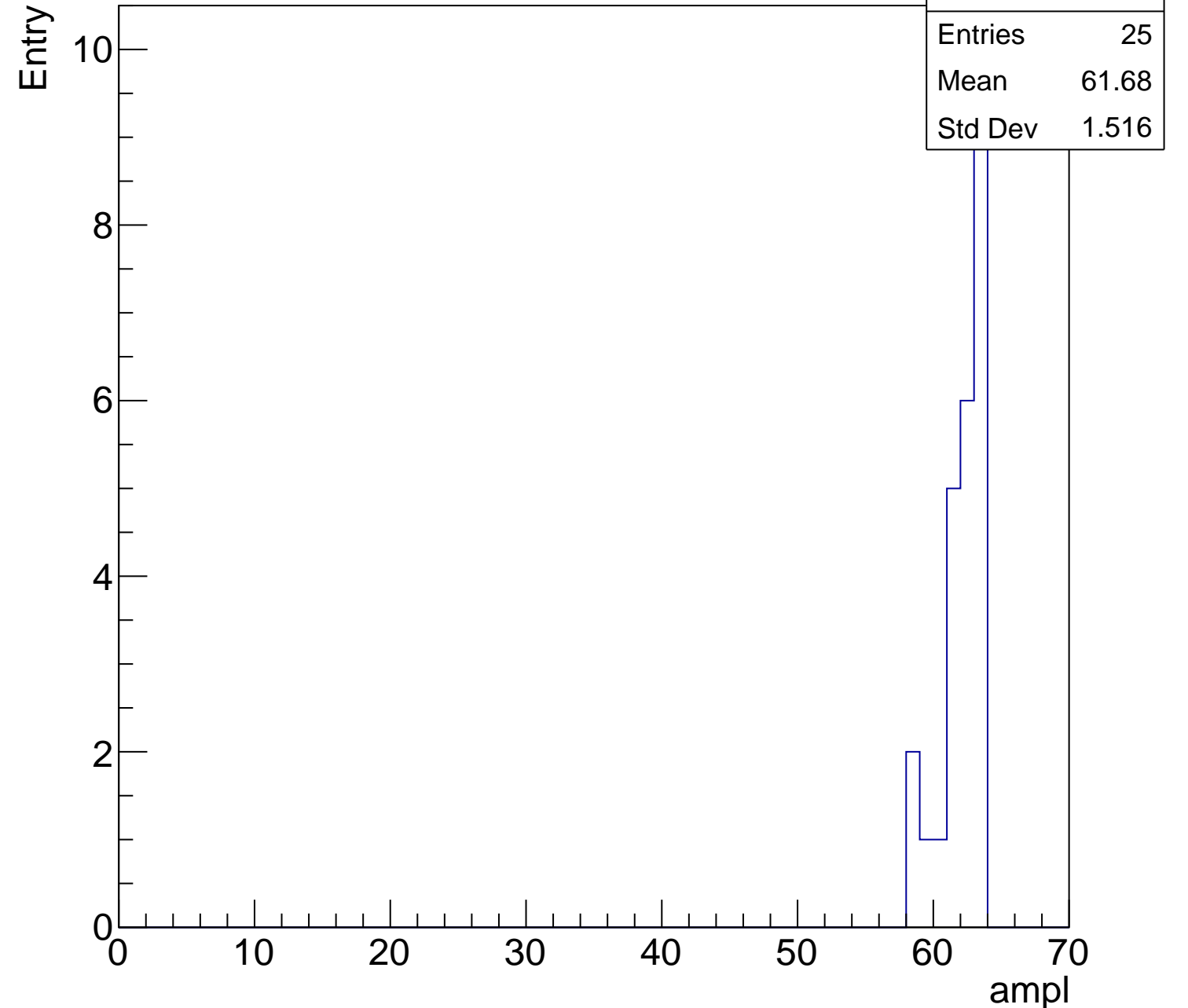
40

50

60

ampl

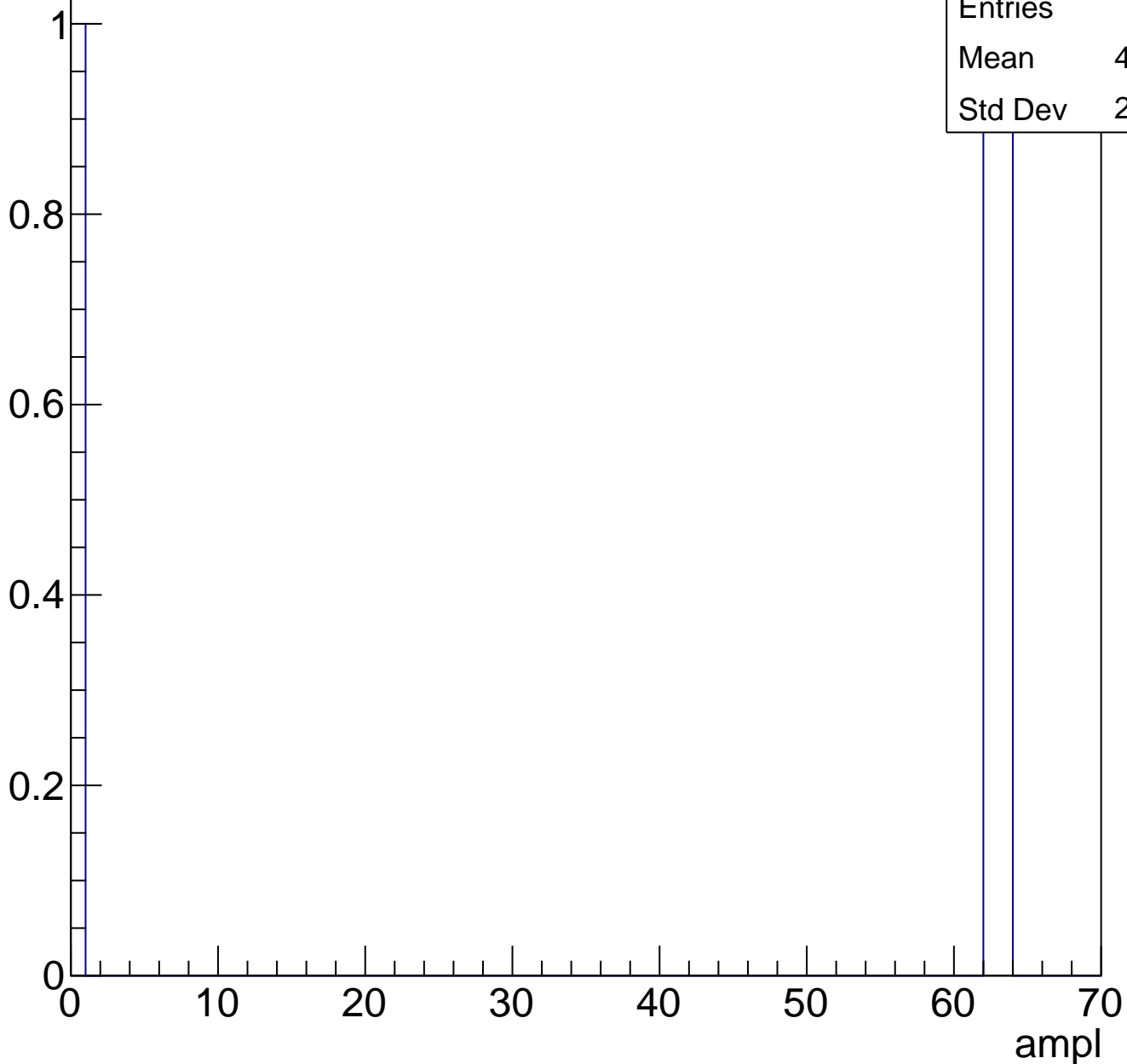
70



B0L101S, U18-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch93, adc0

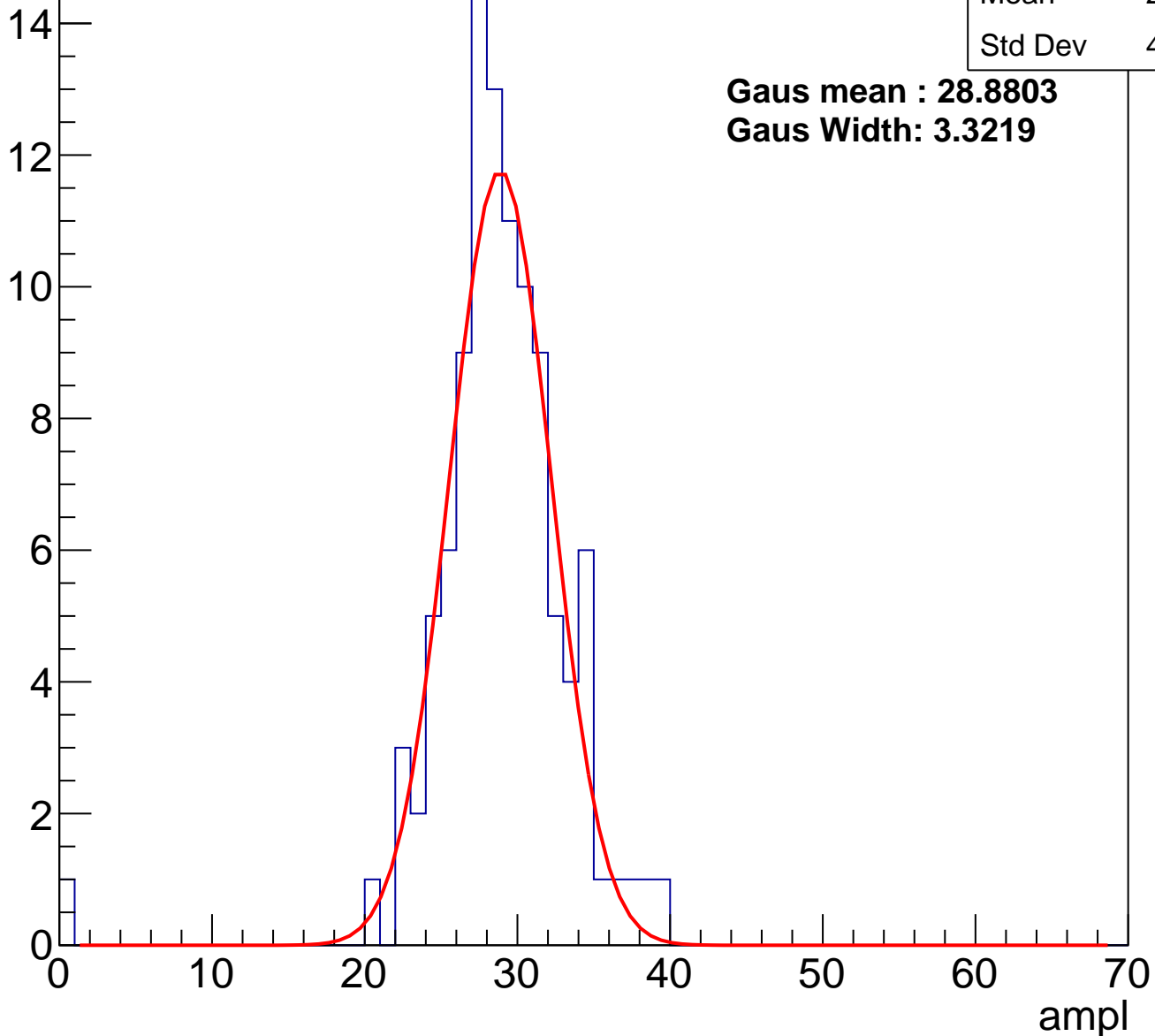
calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	28.42
Std Dev	4.489

Gaus mean : 28.8803

Gaus Width: 3.3219

Entry



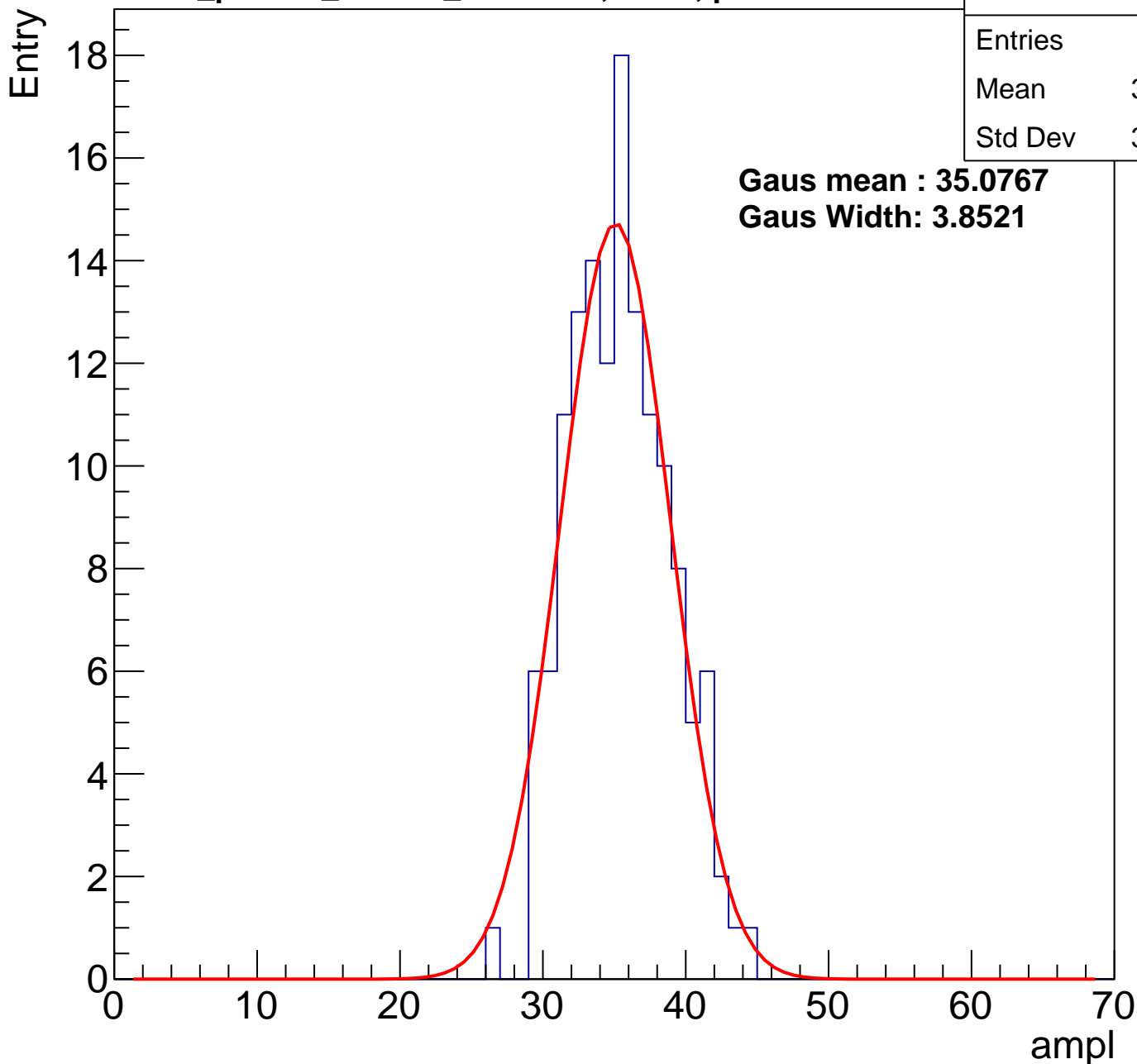
B0L101S, U18-ch93, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	34.93
Std Dev	3.475

Gaus mean : 35.0767

Gaus Width: 3.8521



B0L101S, U18-ch93, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	42.03
Std Dev	3.875

Gaus mean : 42.6438

Gaus Width: 3.6758

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

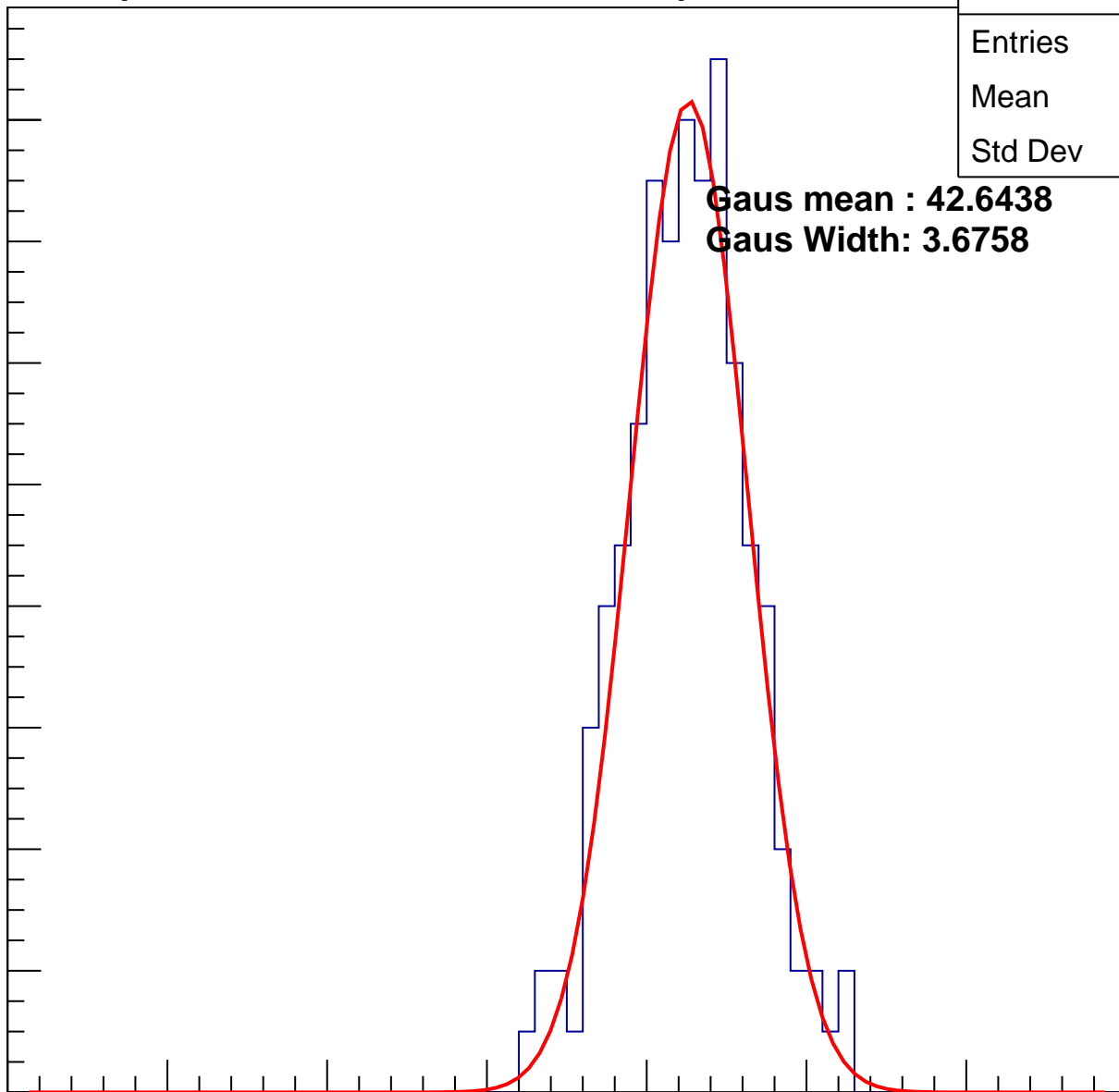
40

50

60

70

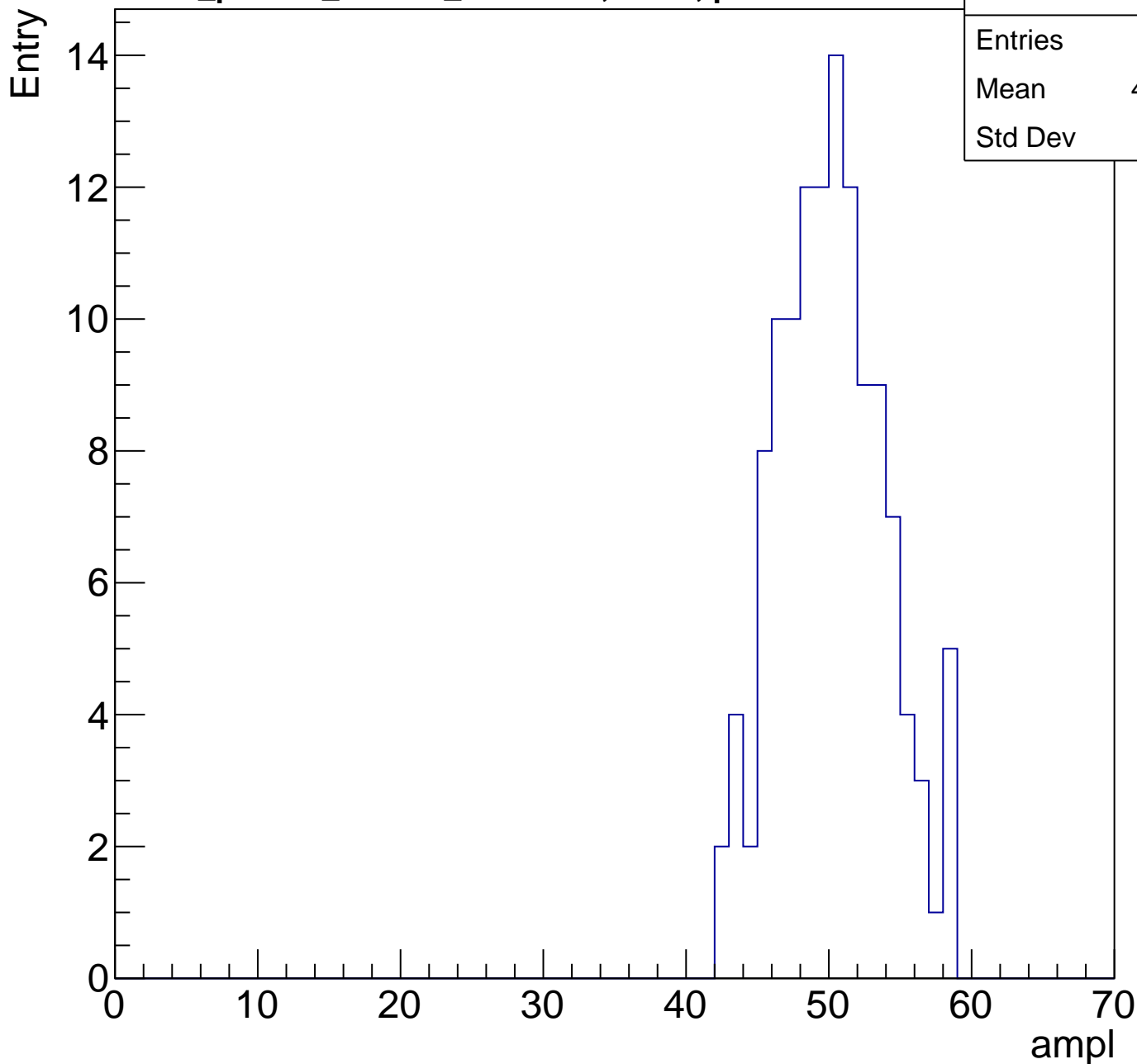
ampl



B0L101S, U18-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

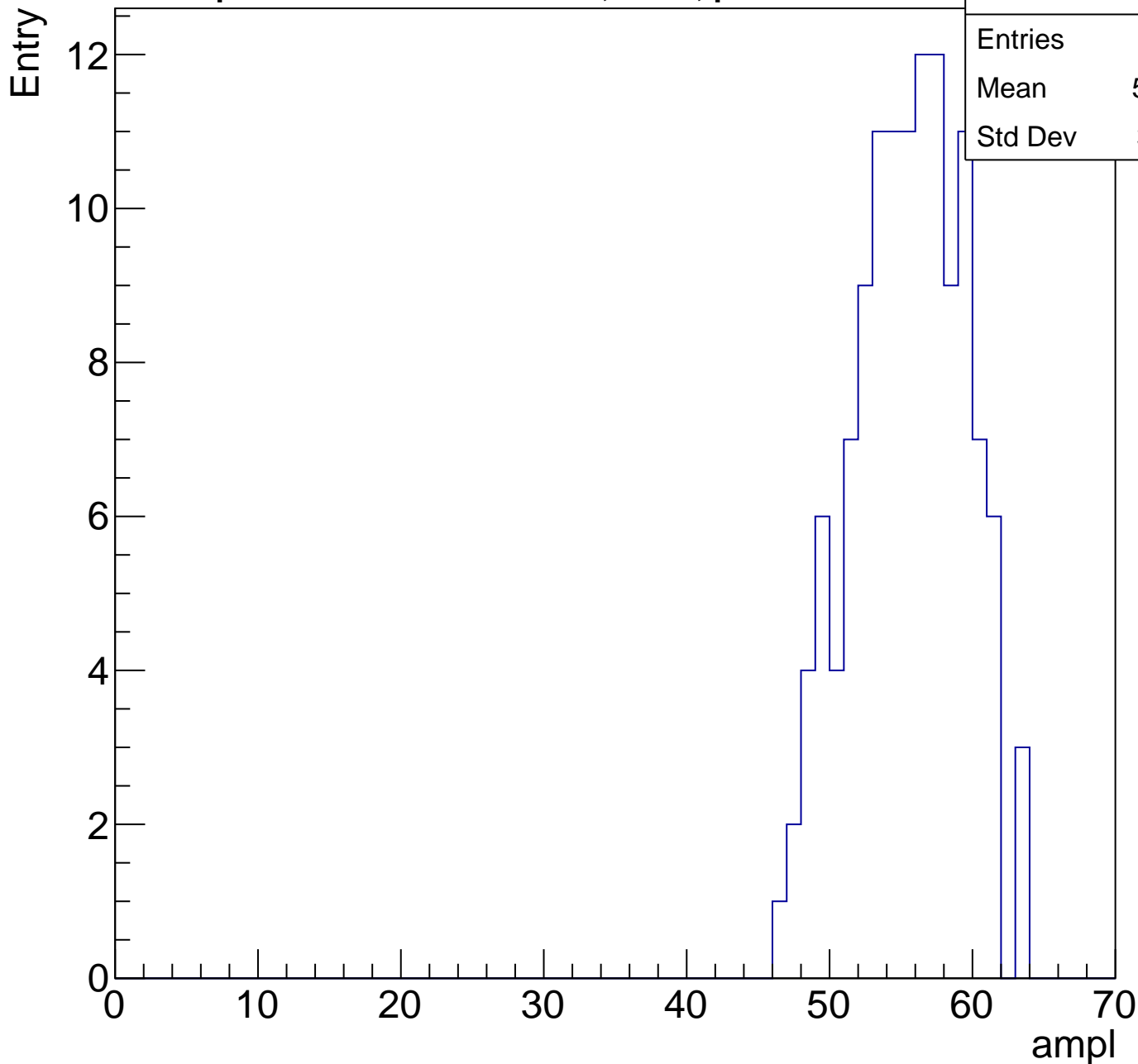
Entries	124
Mean	49.74
Std Dev	3.72



B0L101S, U18-ch93, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	55.04
Std Dev	3.851



B0L101S, U18-ch93, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	81
Mean	58.15
Std Dev	9.579

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

0

2

4

6

8

10

12

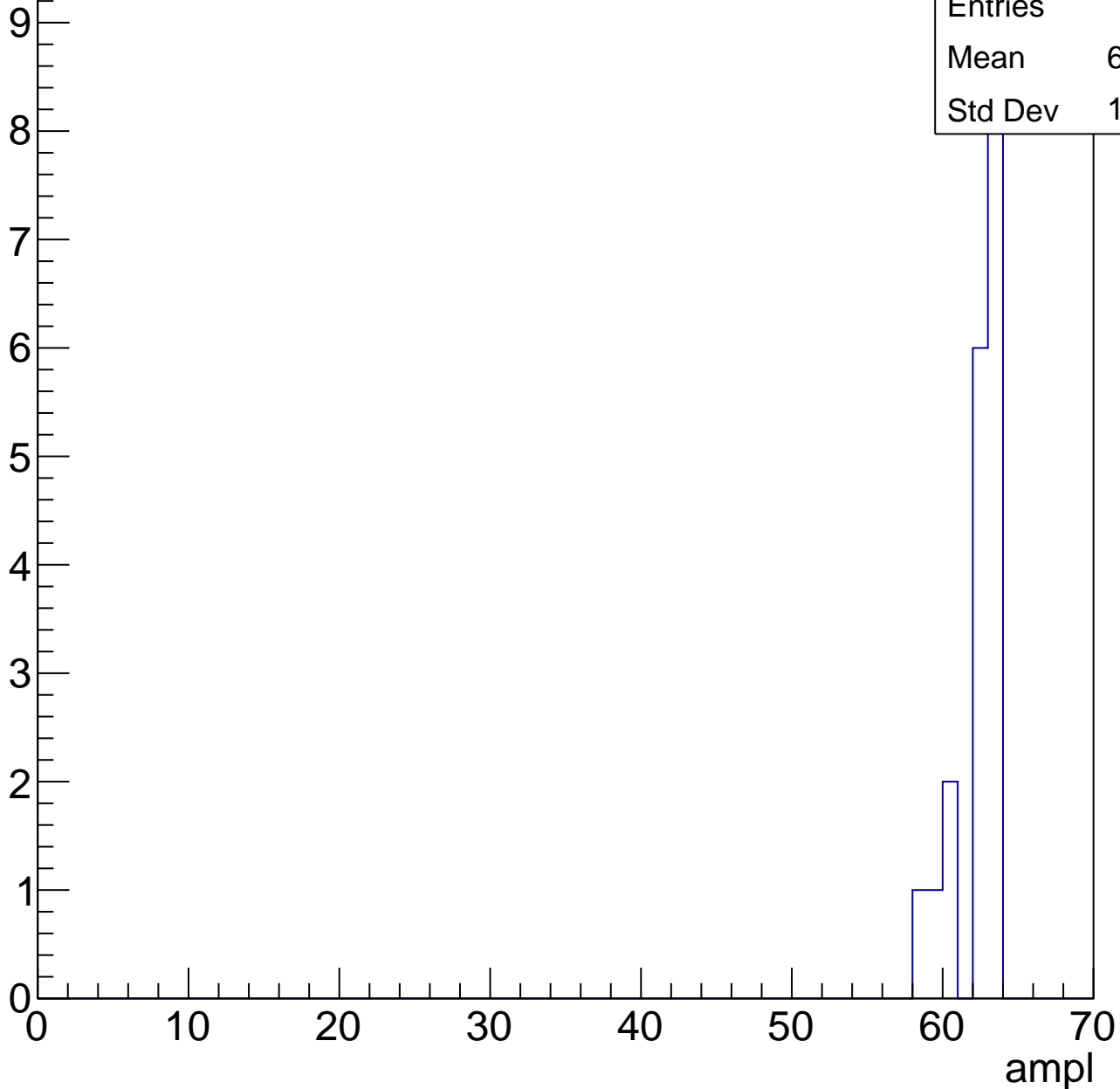
14

B0L101S, U18-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	61.89
Std Dev	1.483



B0L101S, U18-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



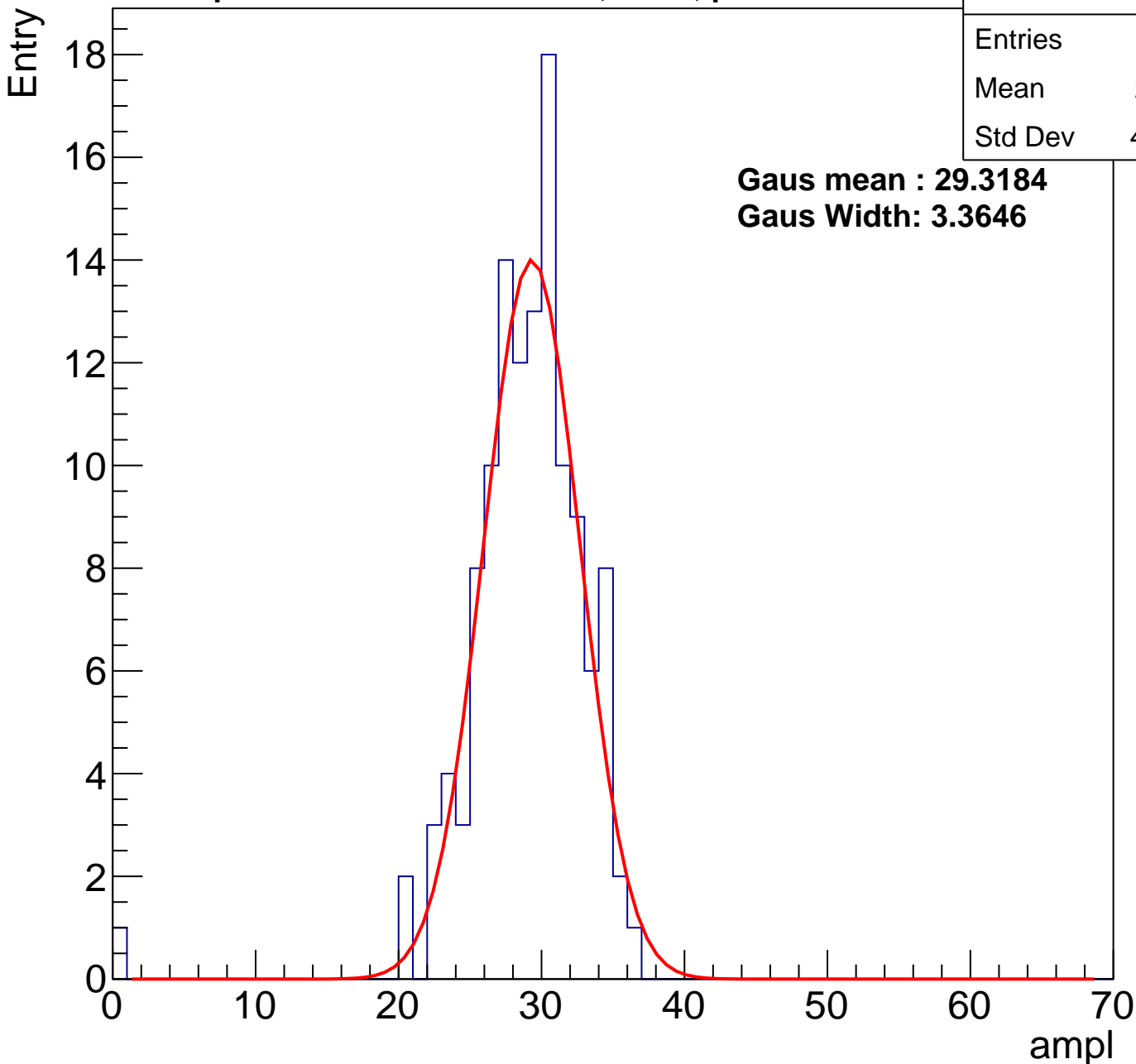
B0L101S, U18-ch94, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	28.51
Std Dev	4.194

Gaus mean : 29.3184

Gaus Width: 3.3646



B0L101S, U18-ch94, adc1

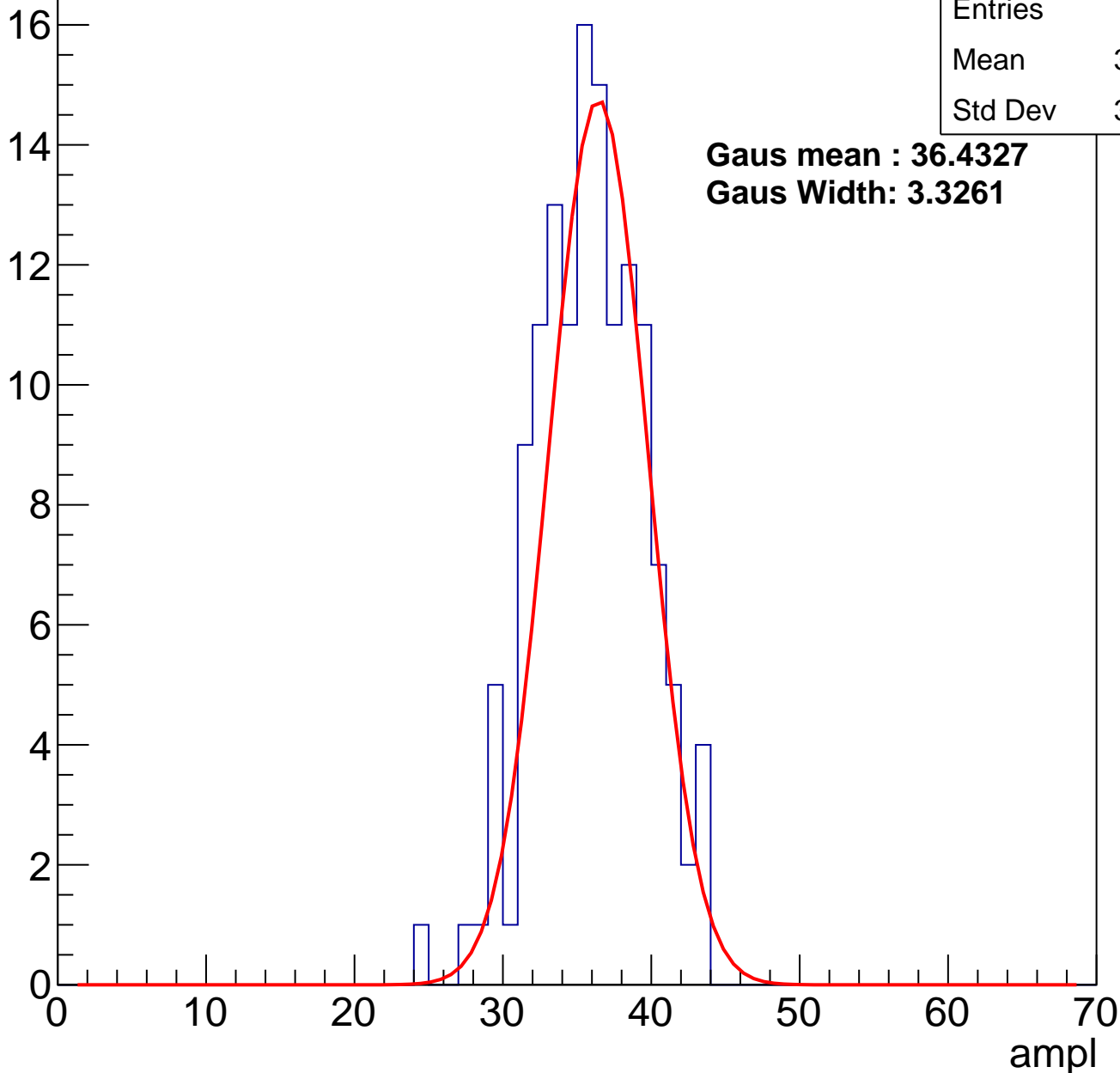
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	35.45
Std Dev	3.607

Gaus mean : 36.4327

Gaus Width: 3.3261

Entry



B0L101S, U18-ch94, adc2

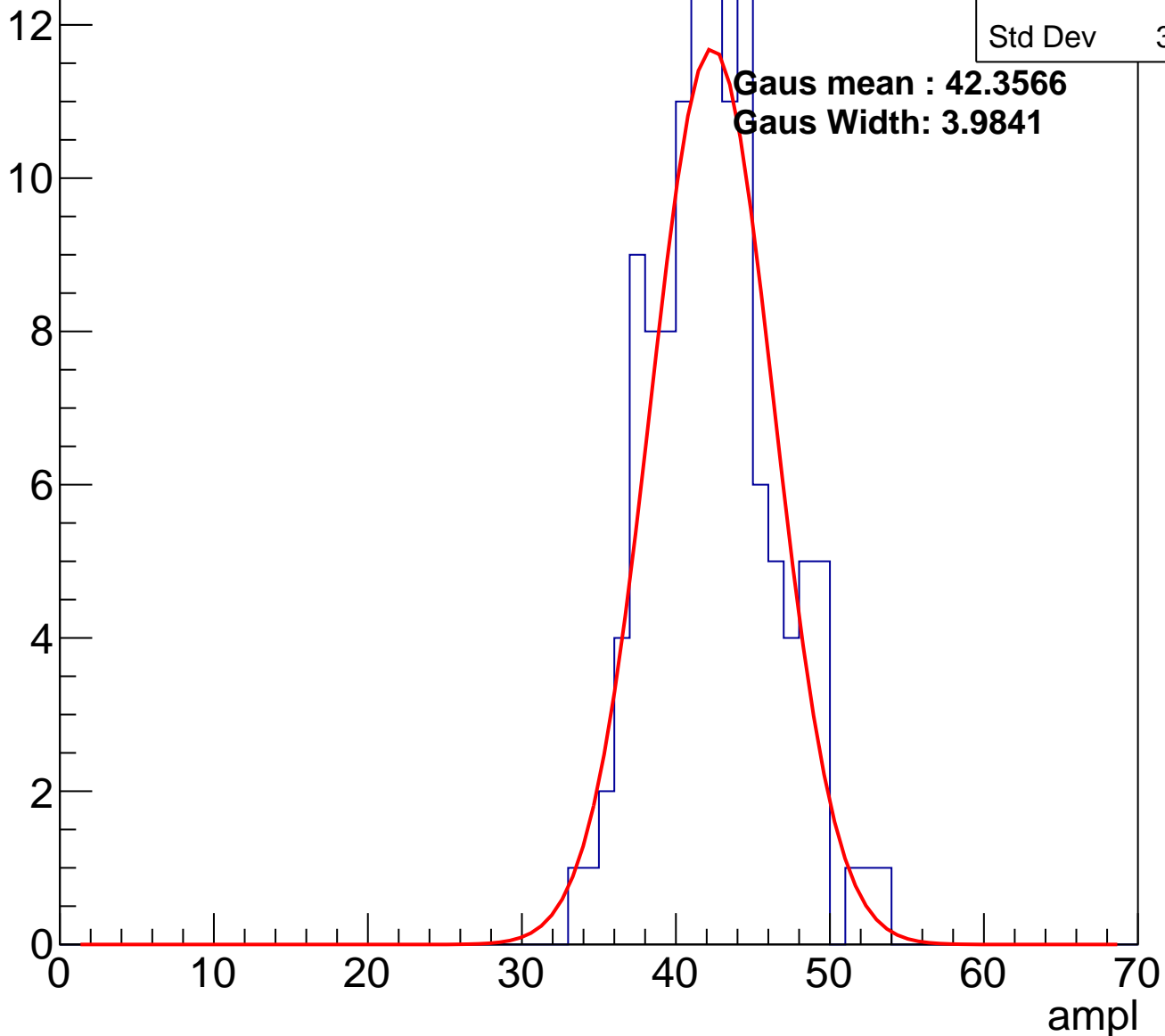
calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	41.99
Std Dev	3.939

Gaus mean : 42.3566

Gaus Width: 3.9841

Entry



B0L101S, U18-ch94, adc3

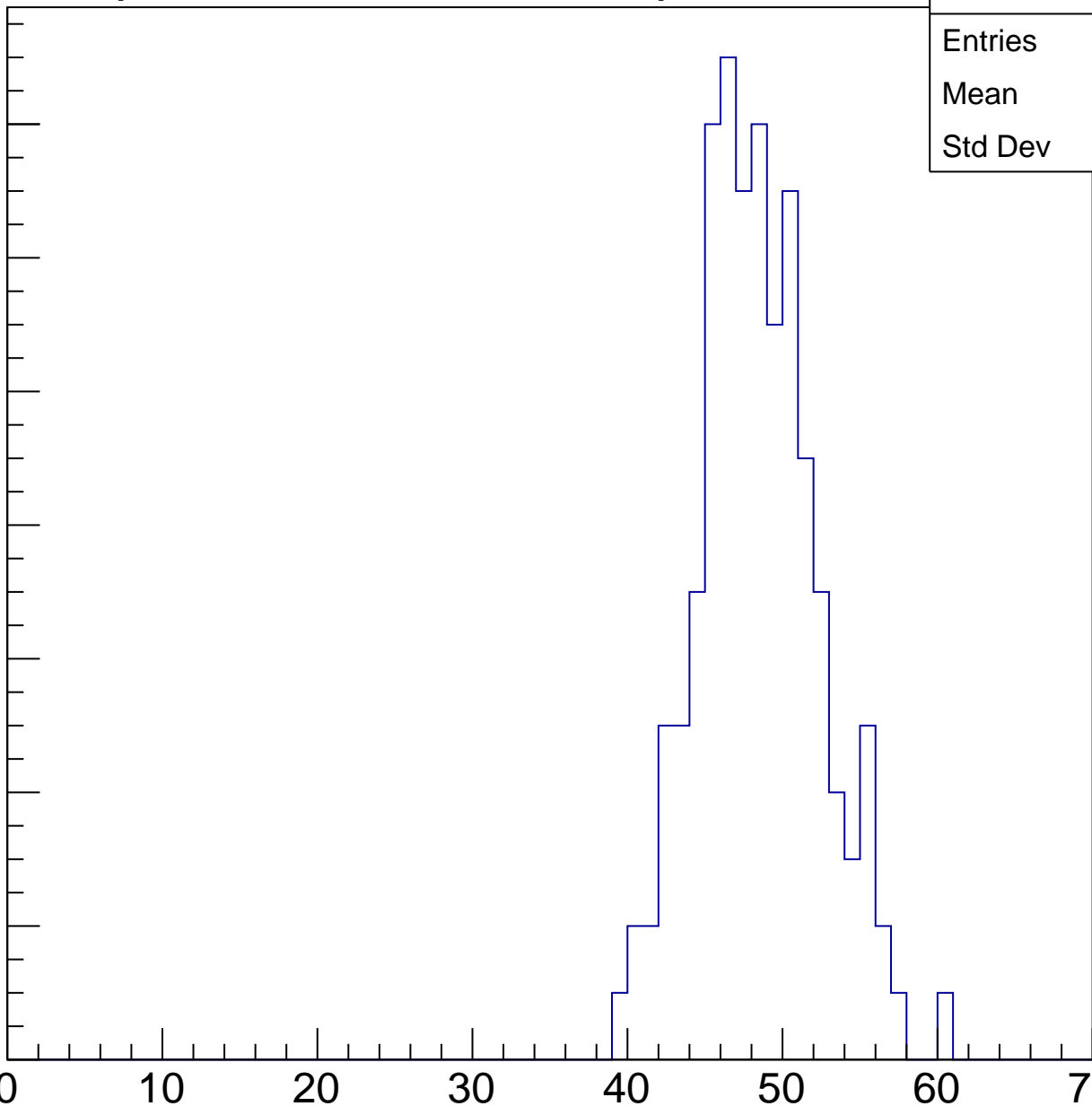
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	134
Mean	47.96
Std Dev	3.849

ampl



B0L101S, U18-ch94, adc4

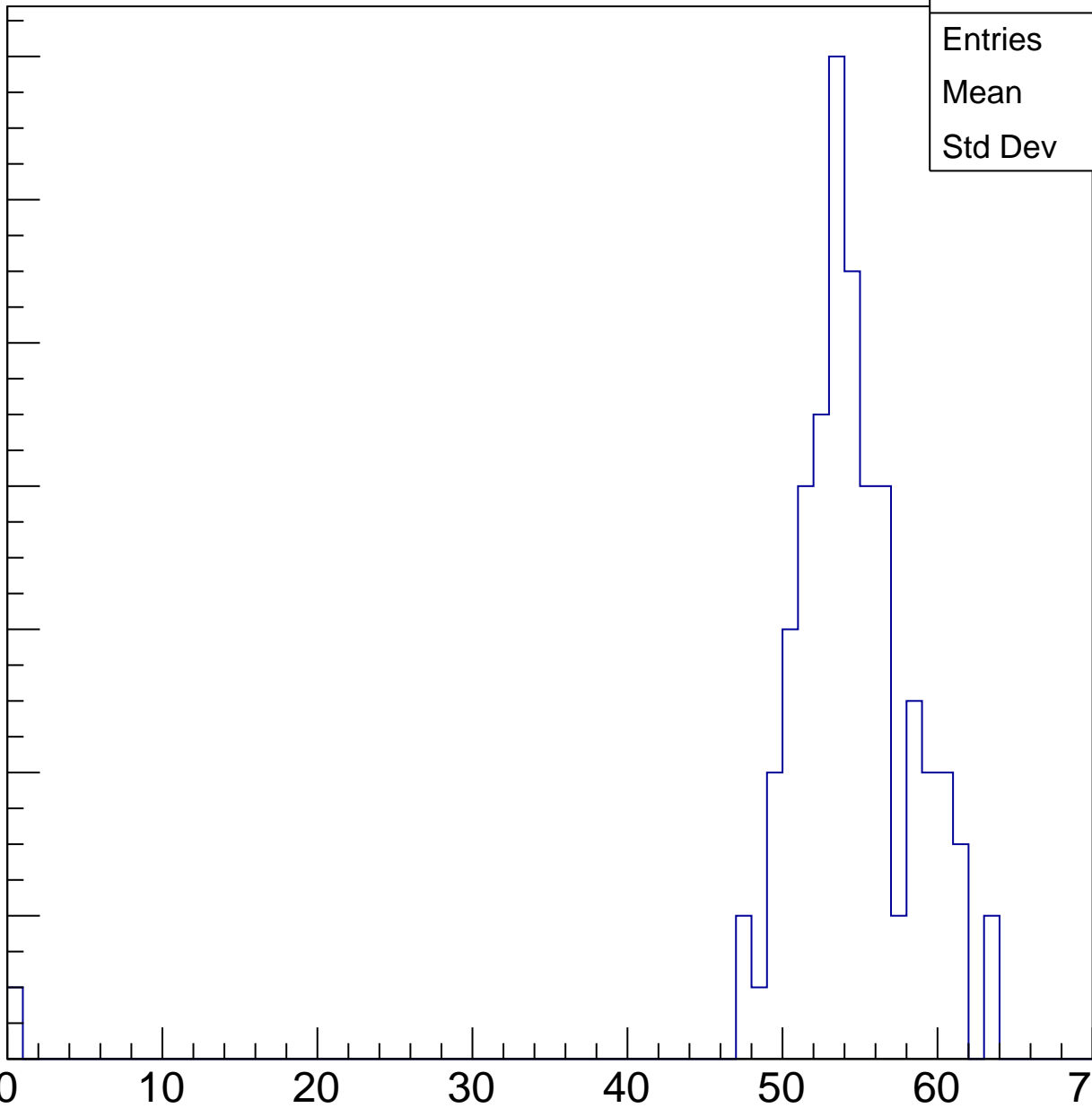
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	53.55
Std Dev	6.627

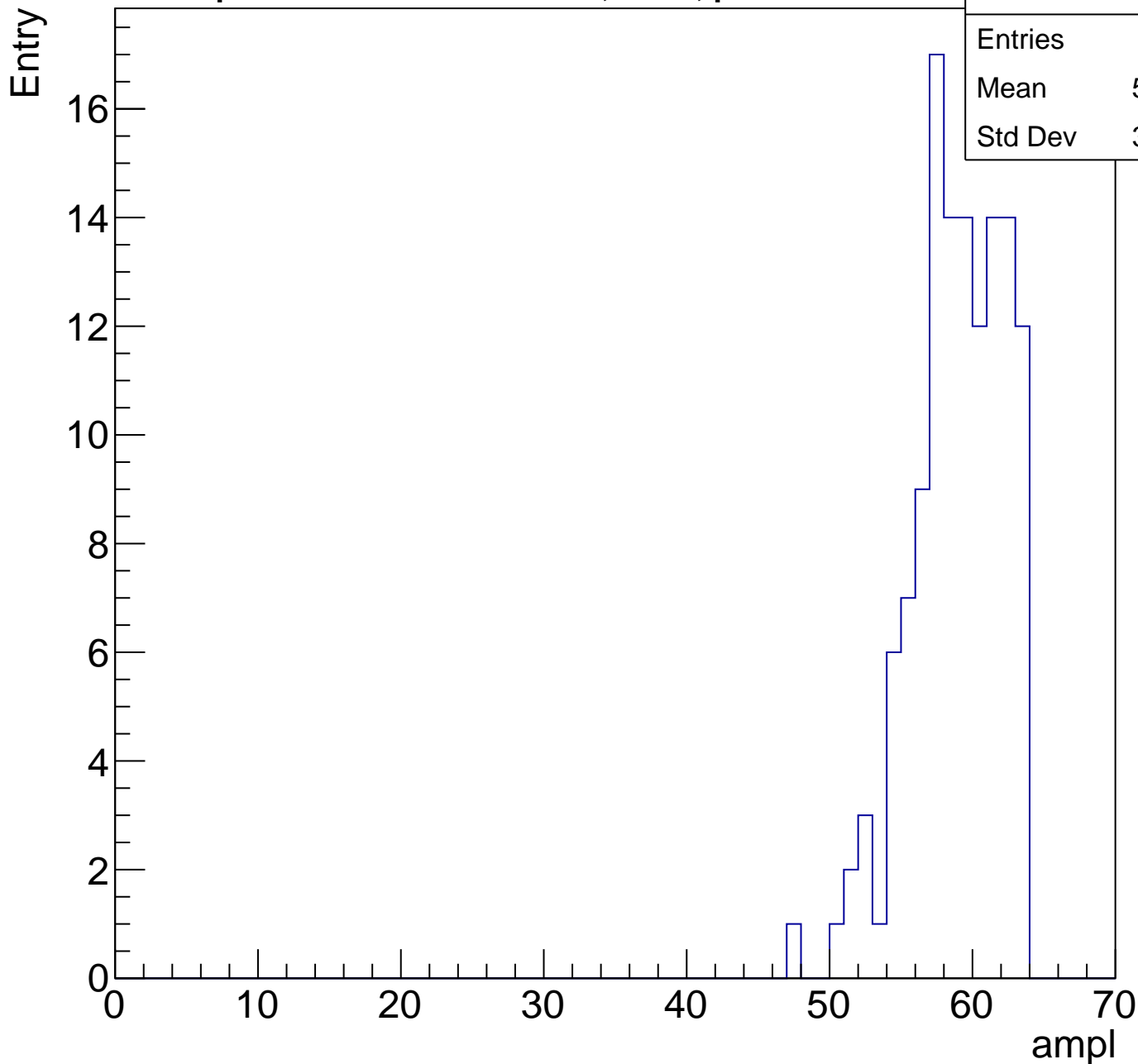
ampl



B0L101S, U18-ch94, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	58.47
Std Dev	3.214

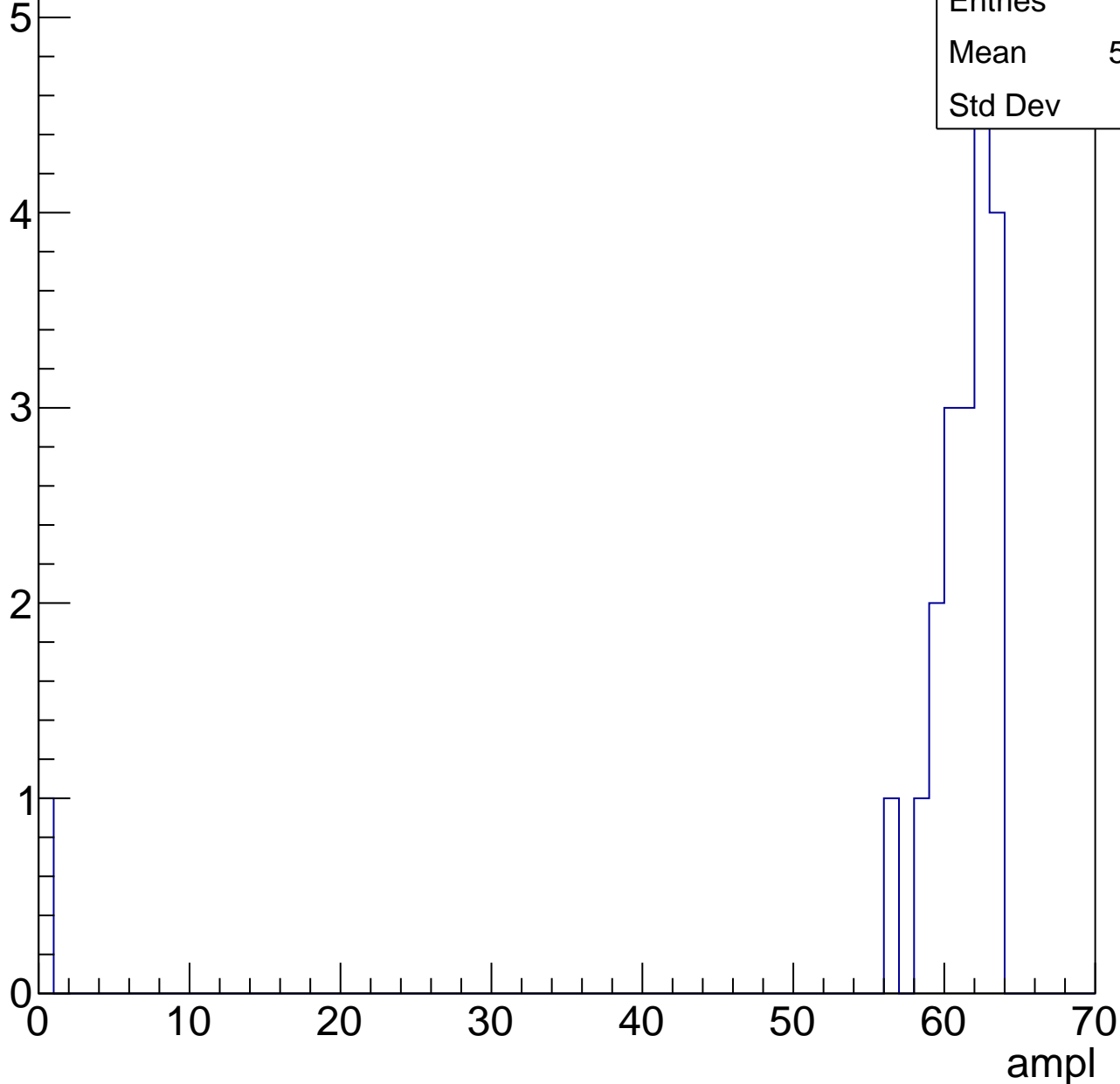


B0L101S, U18-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	57.85
Std Dev	13.4



B0L101S, U18-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch95, adc0

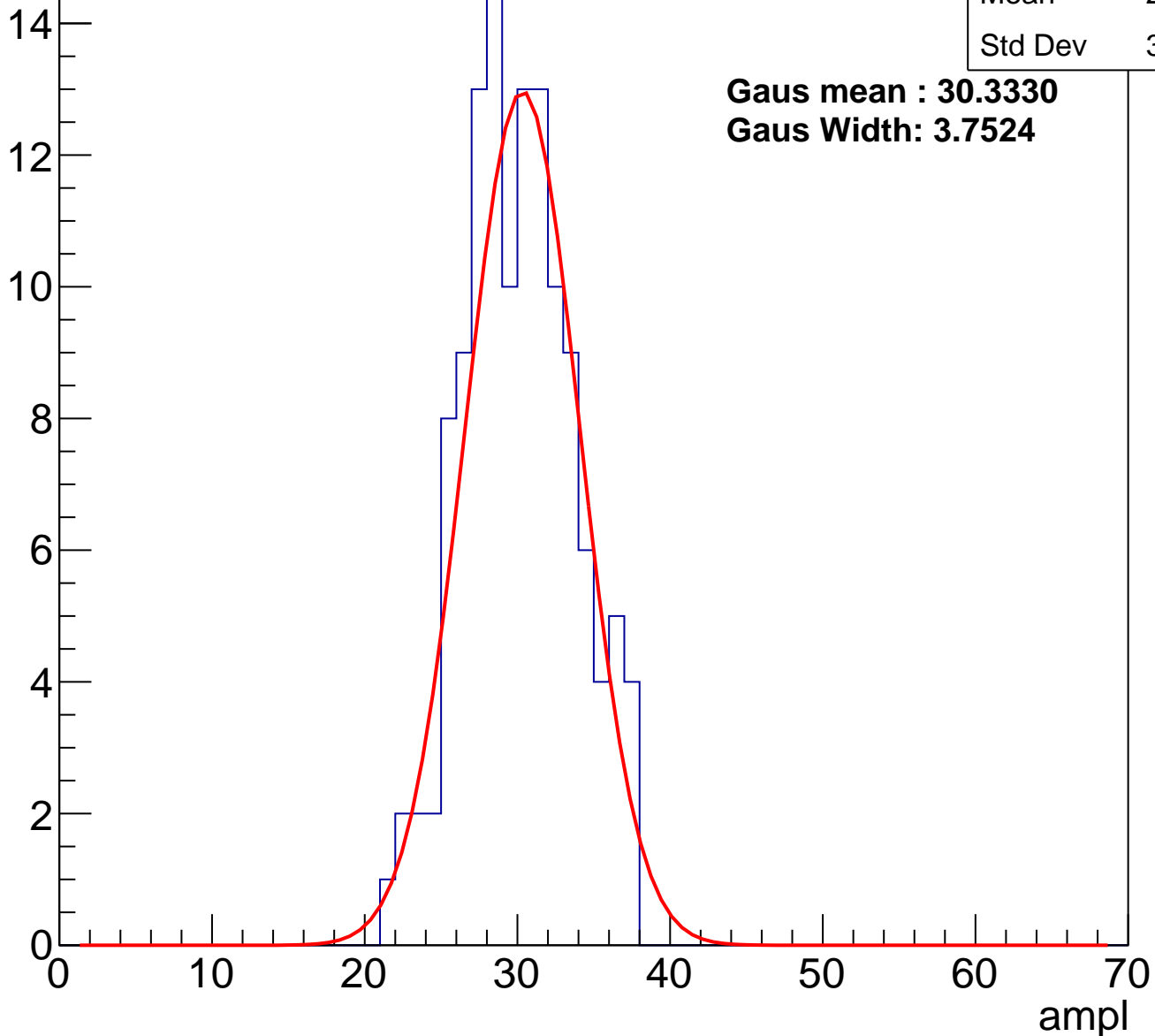
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.65
Std Dev	3.564

Gaus mean : 30.3330

Gaus Width: 3.7524

Entry



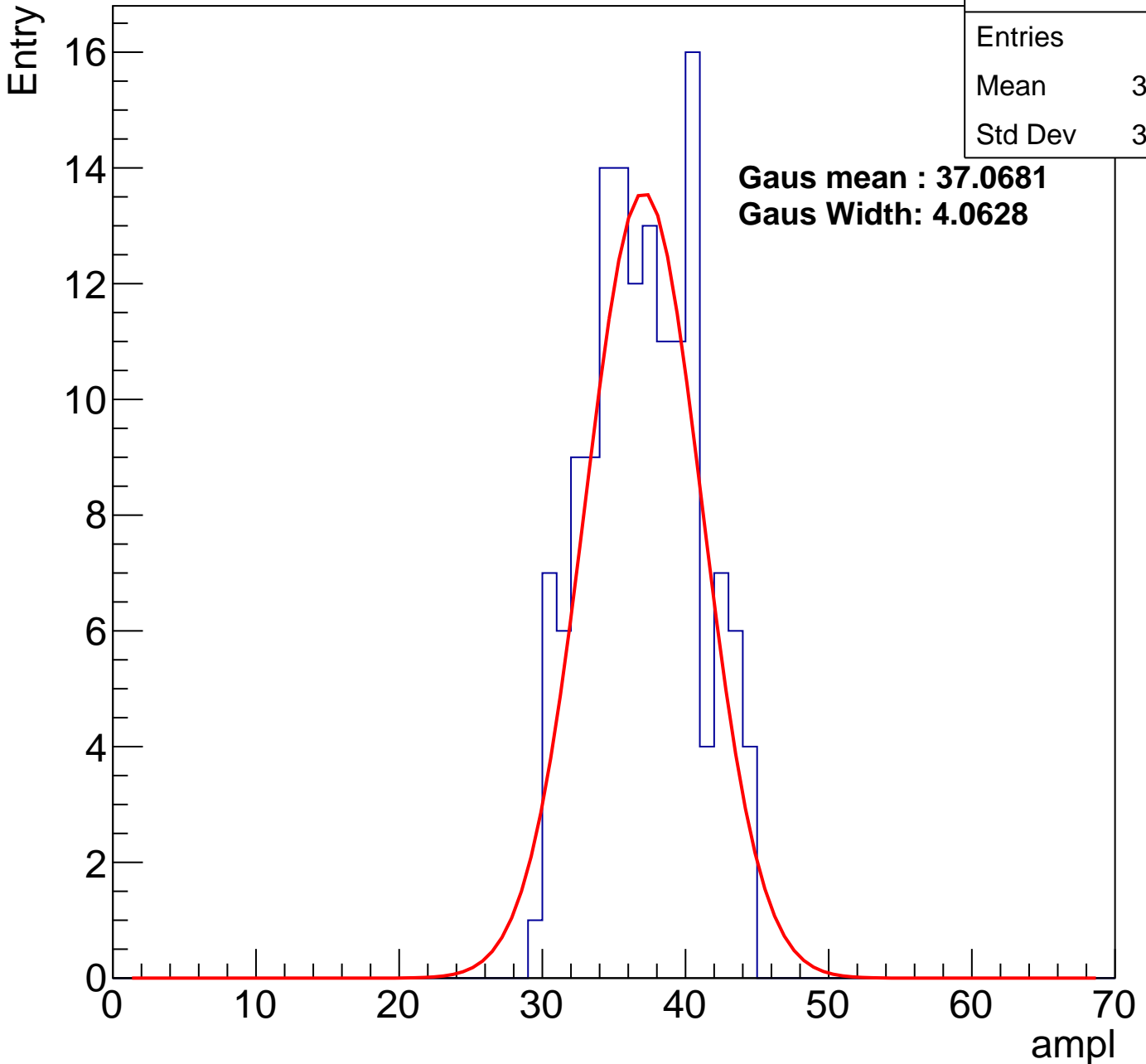
B0L101S, U18-ch95, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	36.58
Std Dev	3.737

Gaus mean : 37.0681

Gaus Width: 4.0628



B0L101S, U18-ch95, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

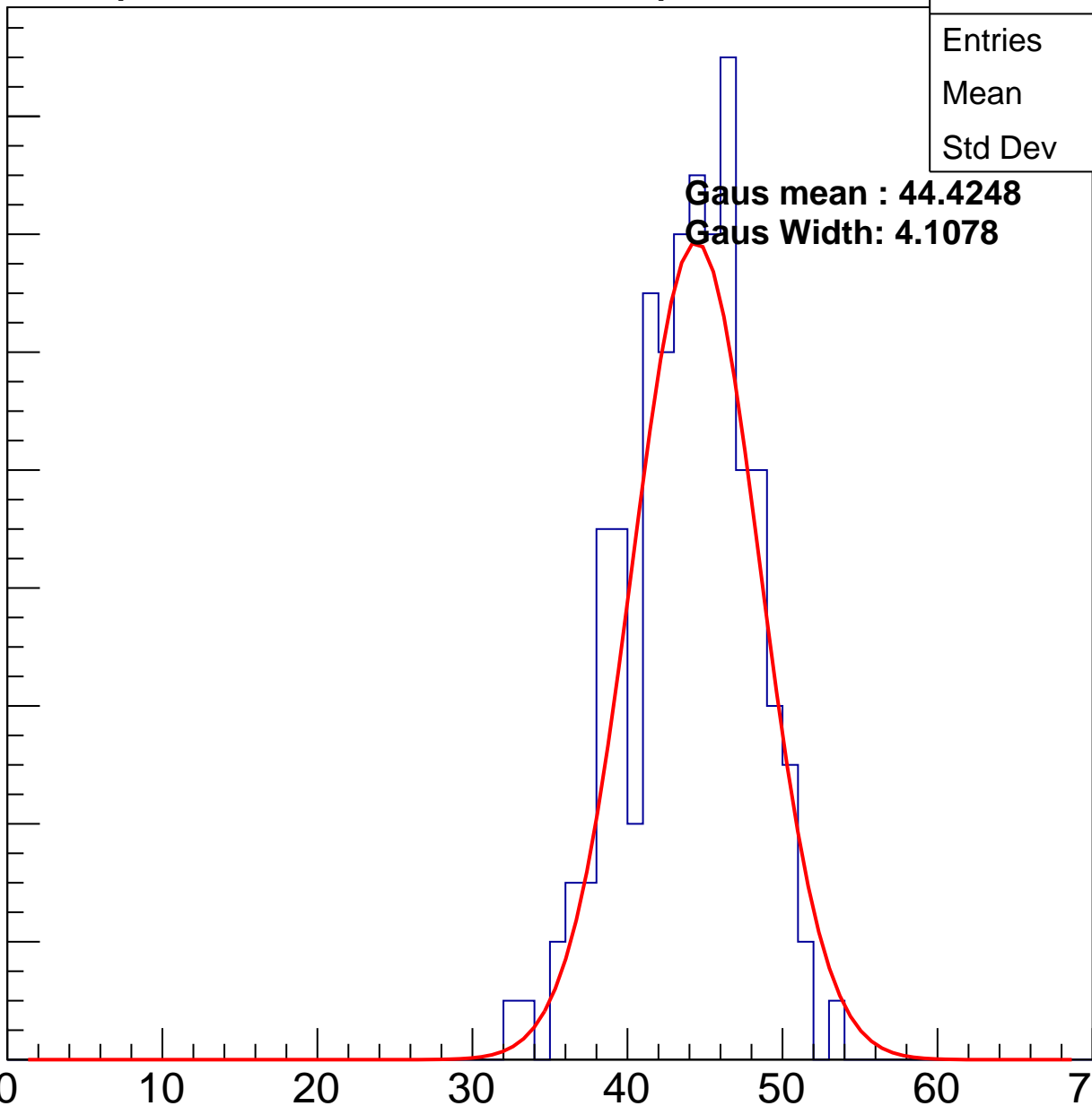
16
14
12
10
8
6
4
2
0

Entries	151
Mean	43.49
Std Dev	3.953

Gaus mean : 44.4248

Gaus Width: 4.1078

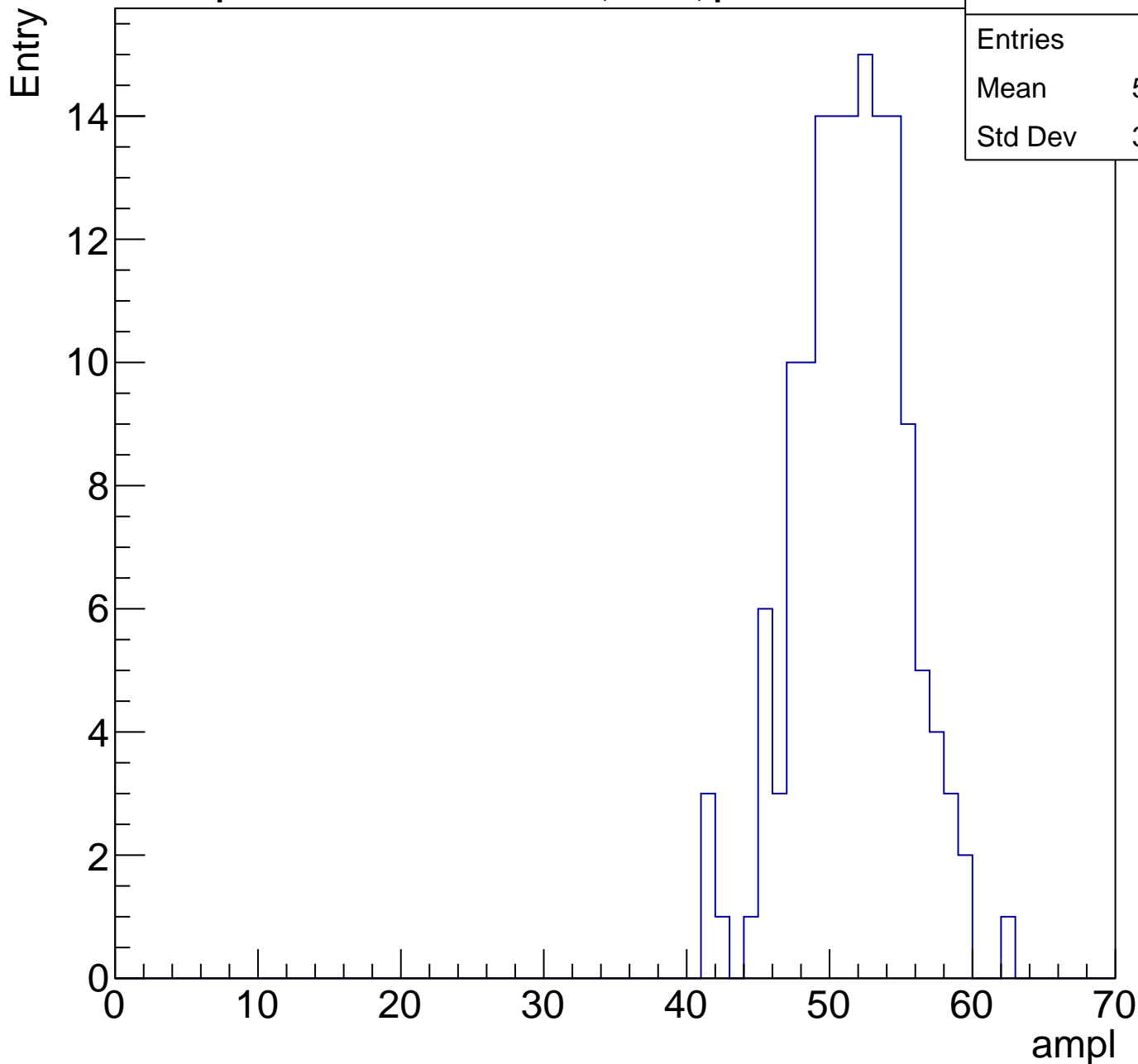
0 10 20 30 40 50 60 70
ampl



B0L101S, U18-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	51.06
Std Dev	3.783



B0L101S, U18-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

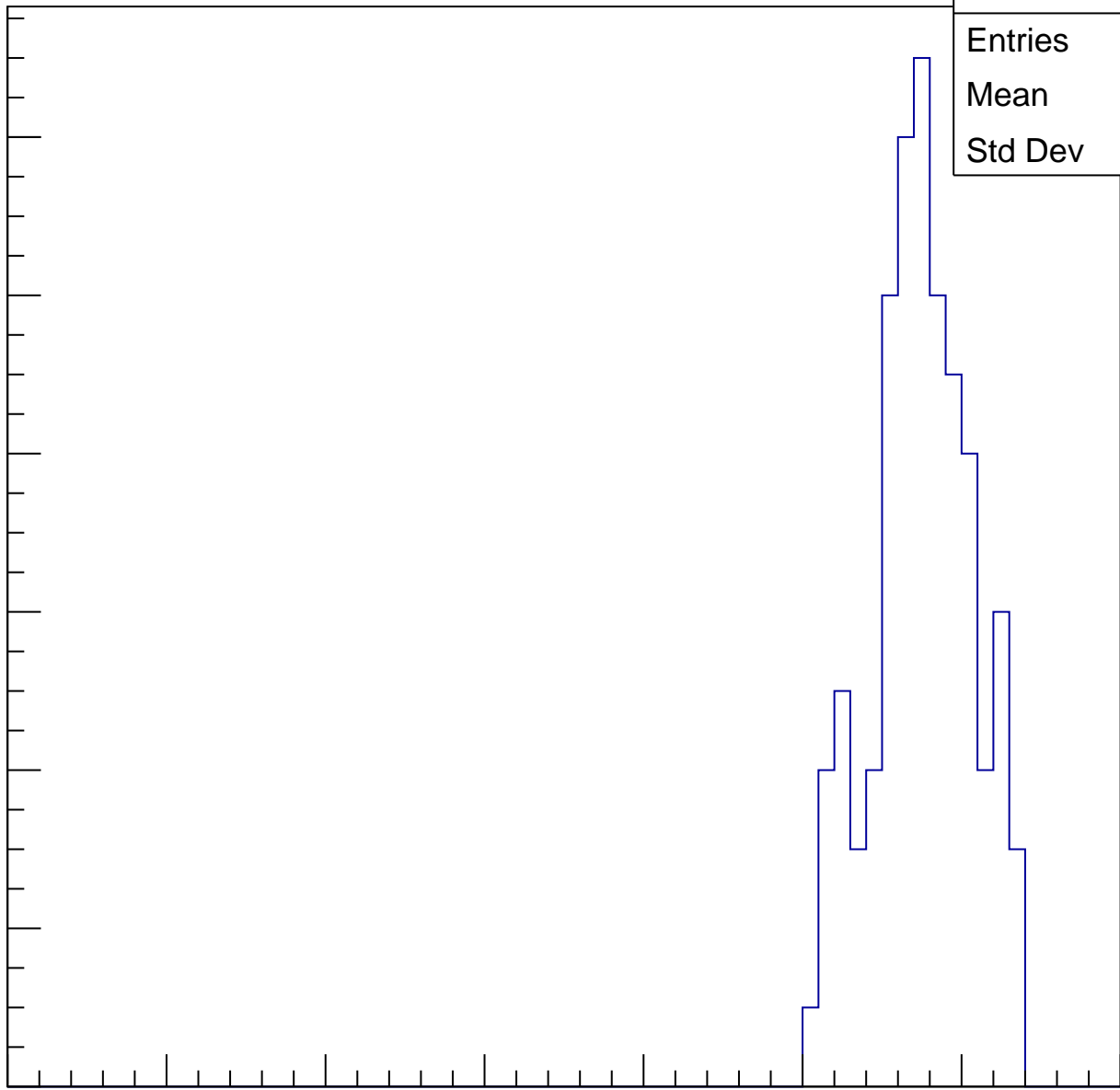
2

0

Entries	92
Mean	57.04
Std Dev	3.117

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch95, adc5

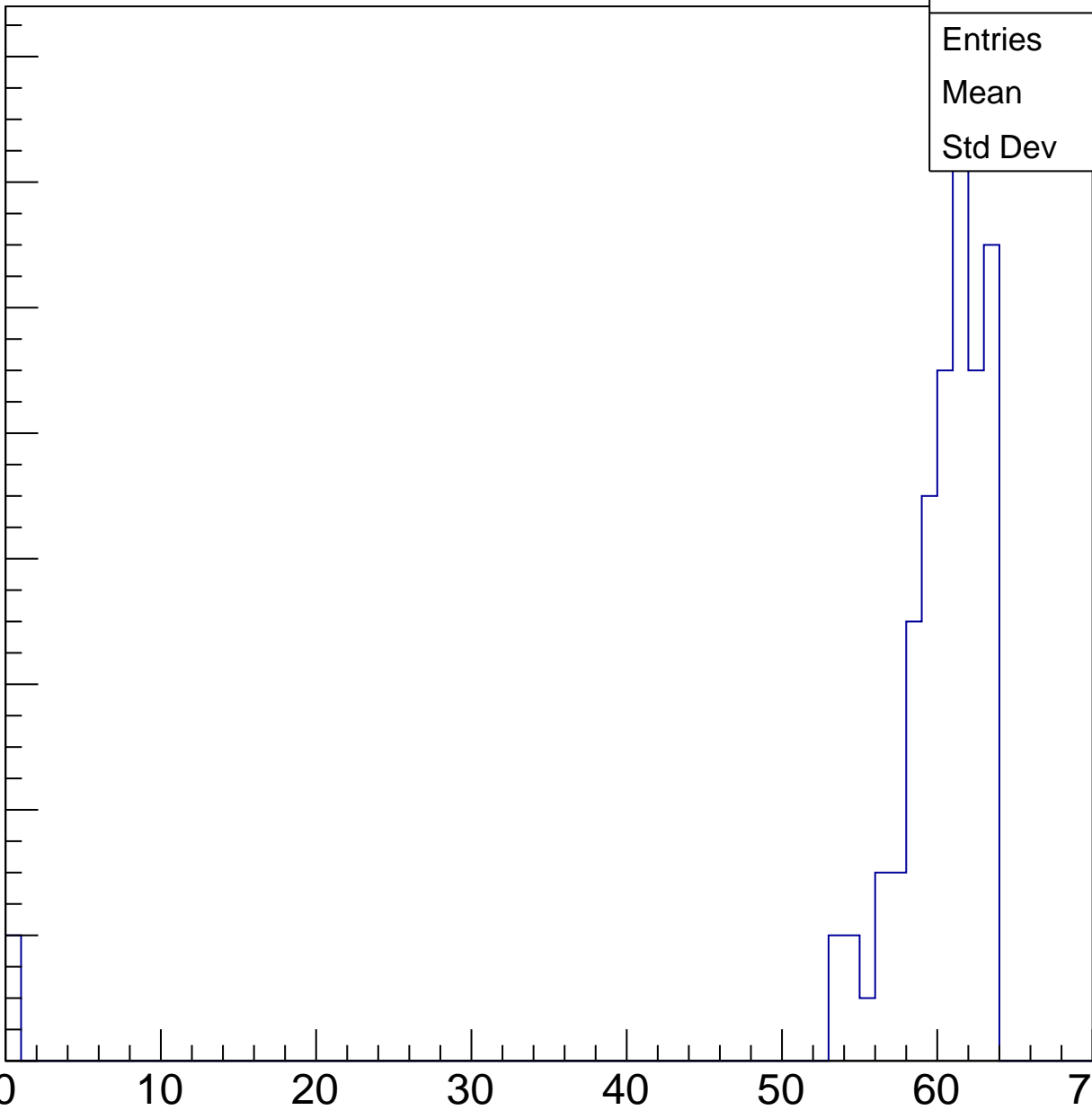
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	58.52
Std Dev	9.689

ampl



B0L101S, U18-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

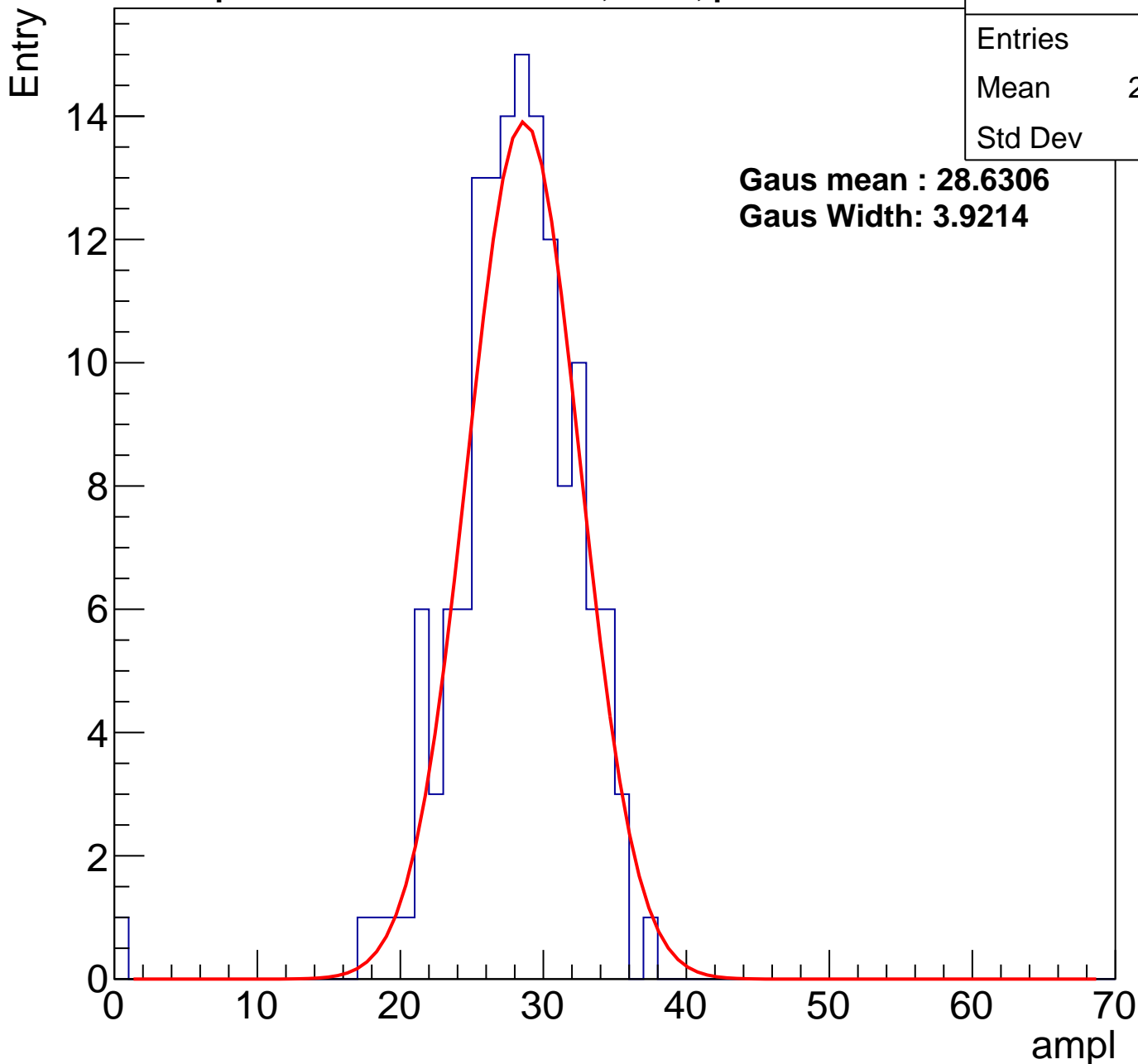
B0L101S, U18-ch96, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	27.57
Std Dev	4.49

Gaus mean : 28.6306

Gaus Width: 3.9214



B0L101S, U18-ch96, adc1

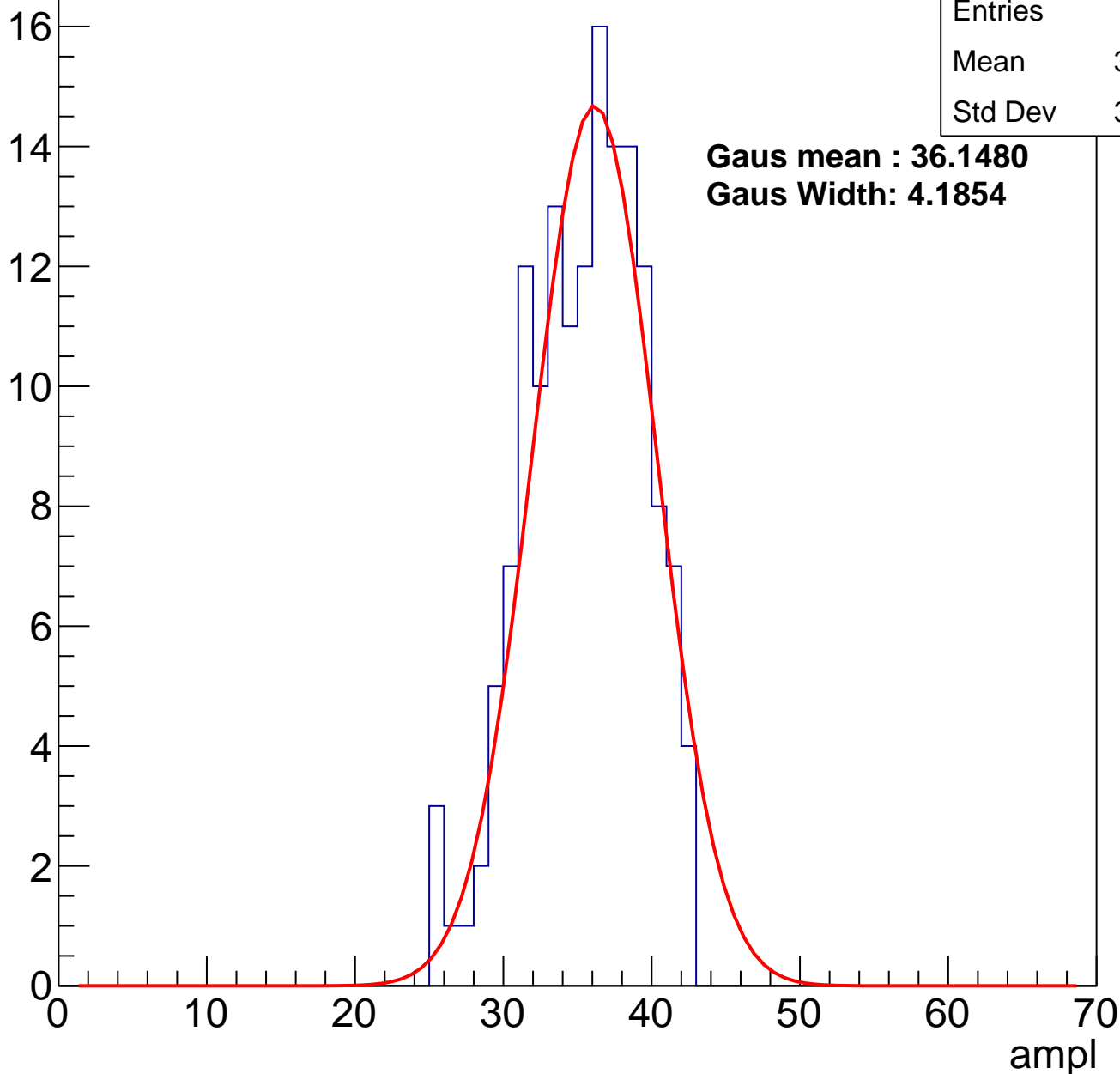
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	35.02
Std Dev	3.874

Gaus mean : 36.1480

Gaus Width: 4.1854

Entry



B0L101S, U18-ch96, adc2

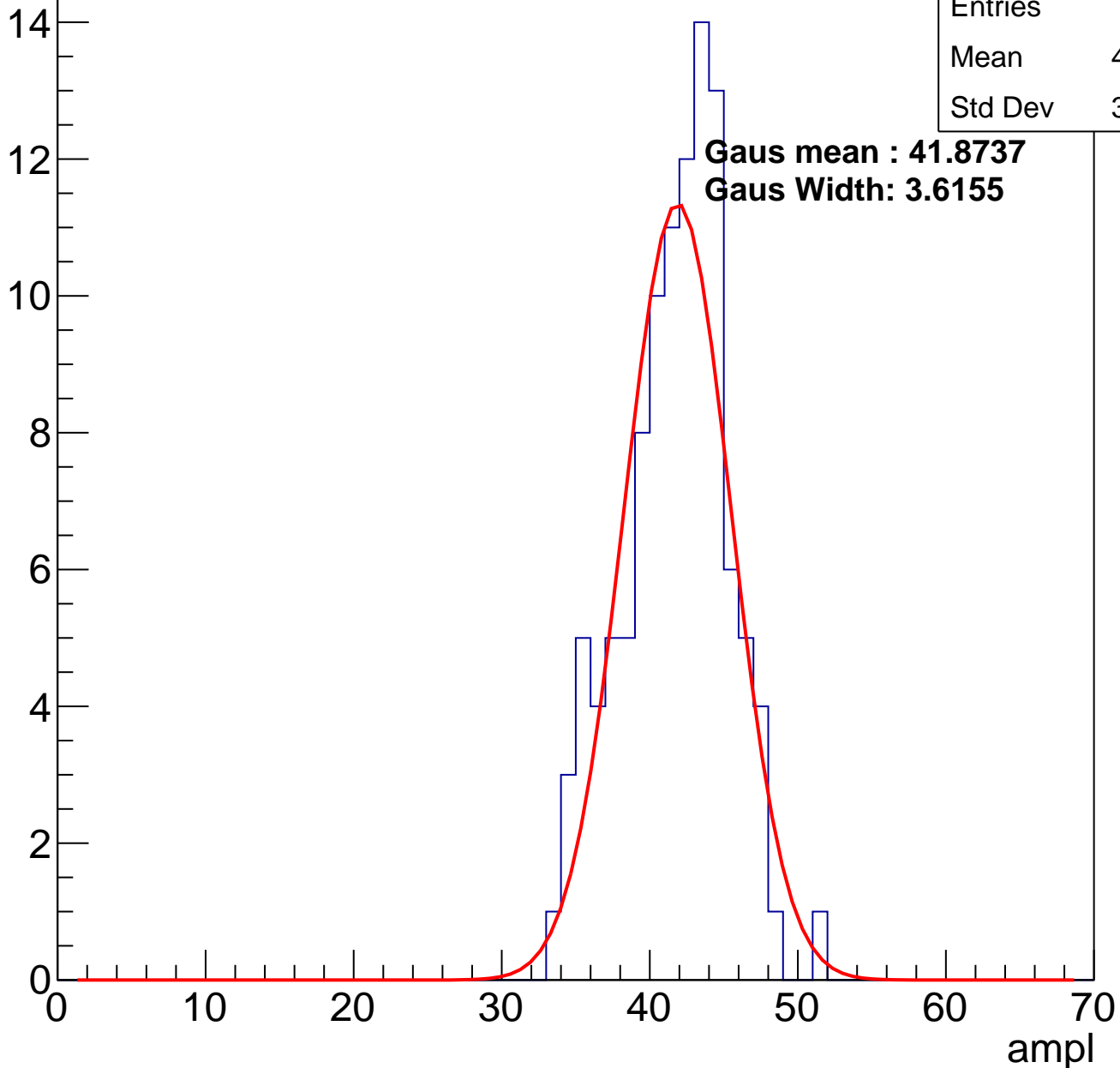
calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	41.27
Std Dev	3.535

Gaus mean : 41.8737

Gaus Width: 3.6155

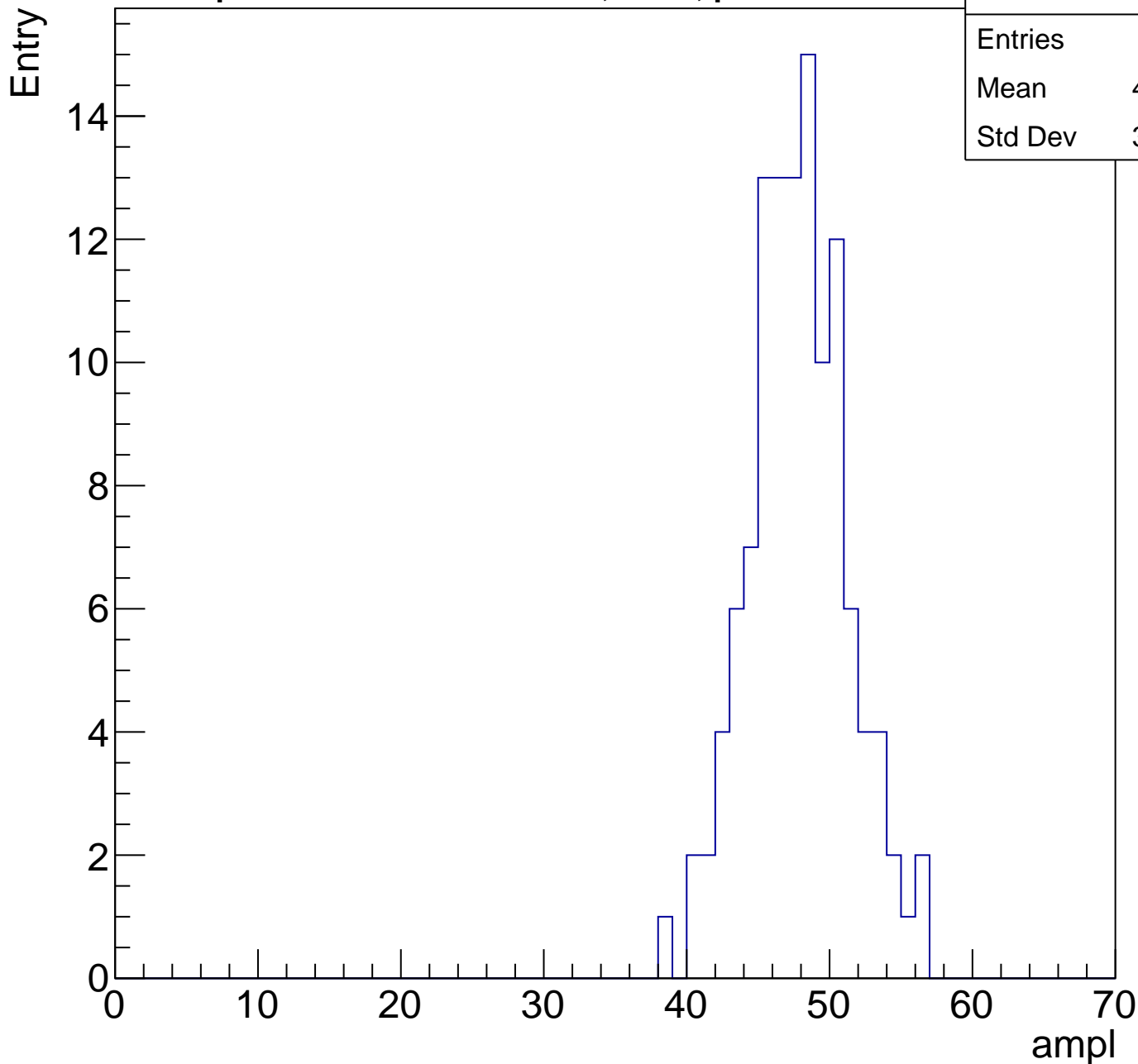
Entry



B0L101S, U18-ch96, adc3

calib_packv5_042523_0143.root, FC#1, port C1

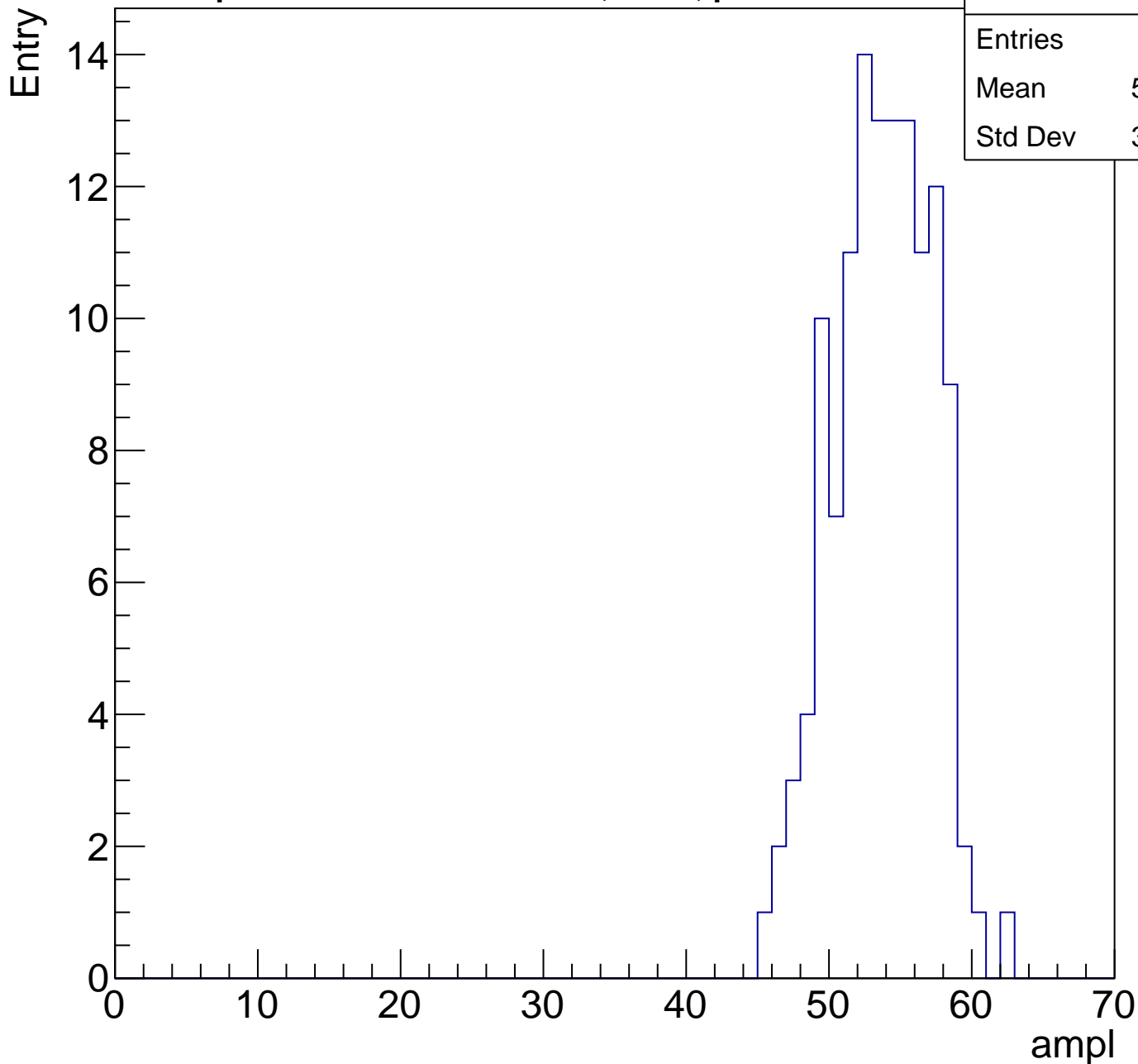
Entries	117
Mean	47.34
Std Dev	3.445



B0L101S, U18-ch96, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	53.28
Std Dev	3.348



B0L101S, U18-ch96, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	58.44
Std Dev	6.149

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

30

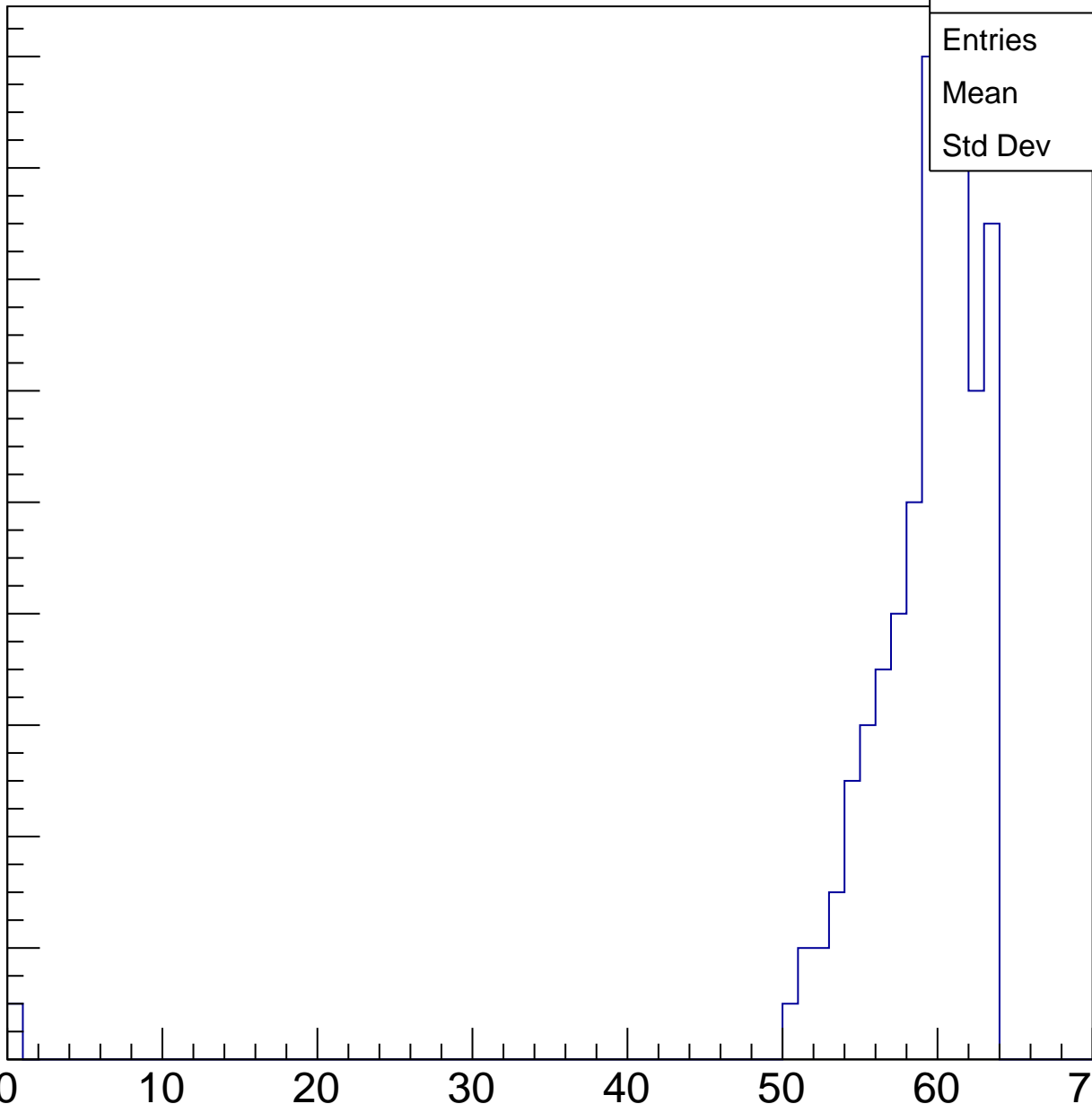
40

50

60

70

ampl

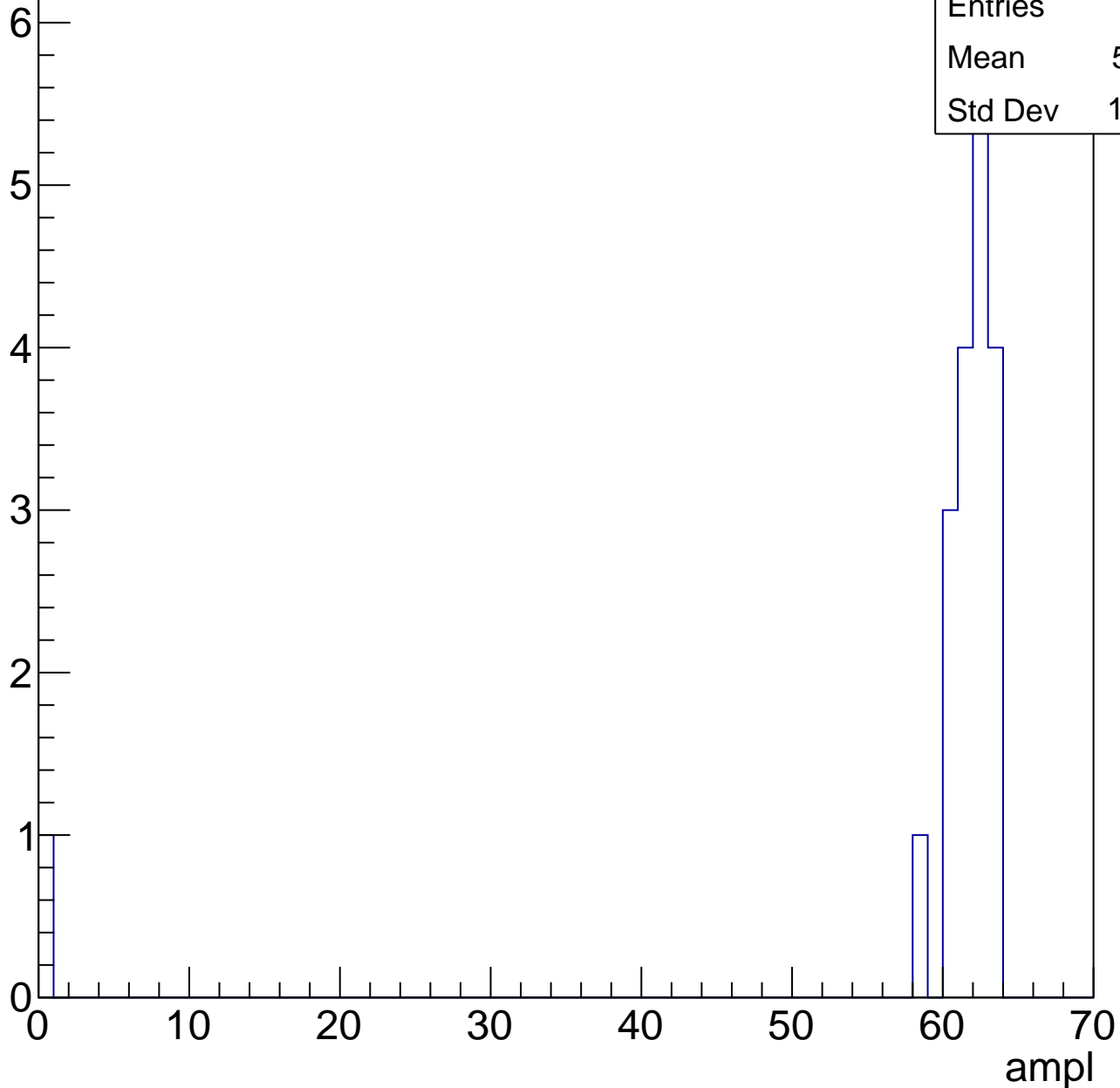


B0L101S, U18-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	58.21
Std Dev	13.78



B0L101S, U18-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



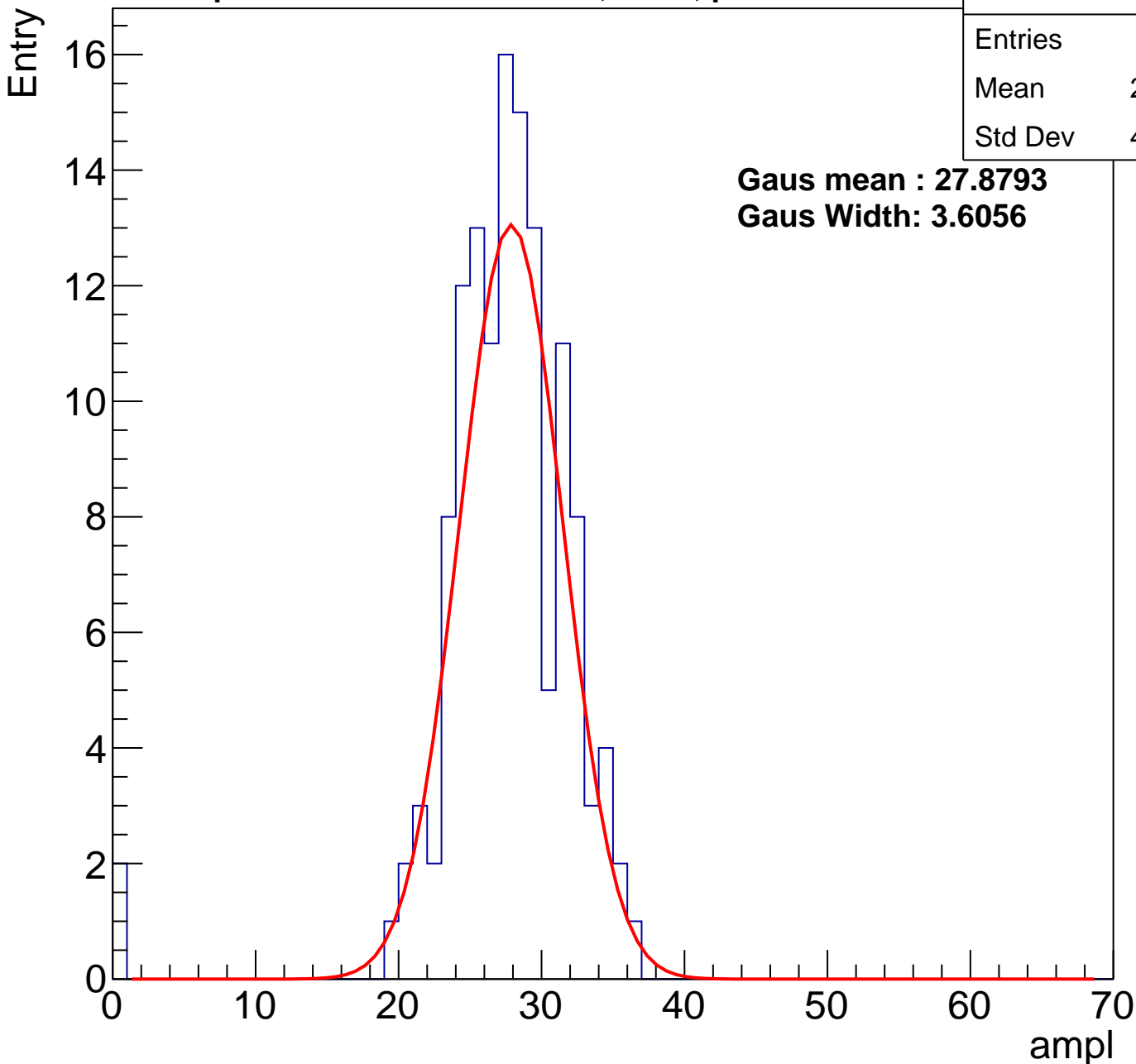
B0L101S, U18-ch97, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	27.02
Std Dev	4.829

Gaus mean : 27.8793

Gaus Width: 3.6056



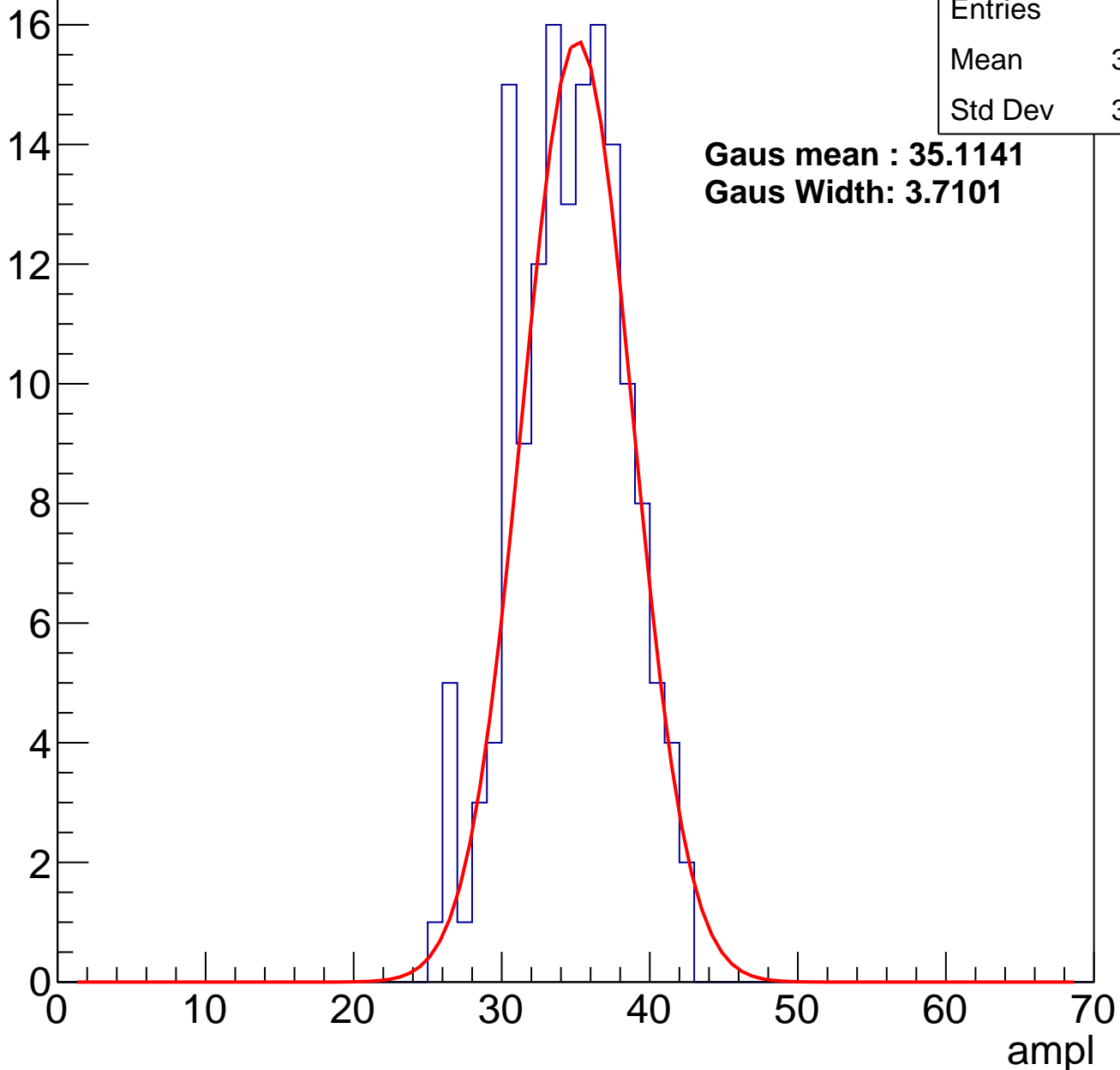
B0L101S, U18-ch97, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	34.14
Std Dev	3.713

Gaus mean : 35.1141
Gaus Width: 3.7101

Entry



B0L101S, U18-ch97, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	41.92
Std Dev	3.747

Gaus mean : 42.4113

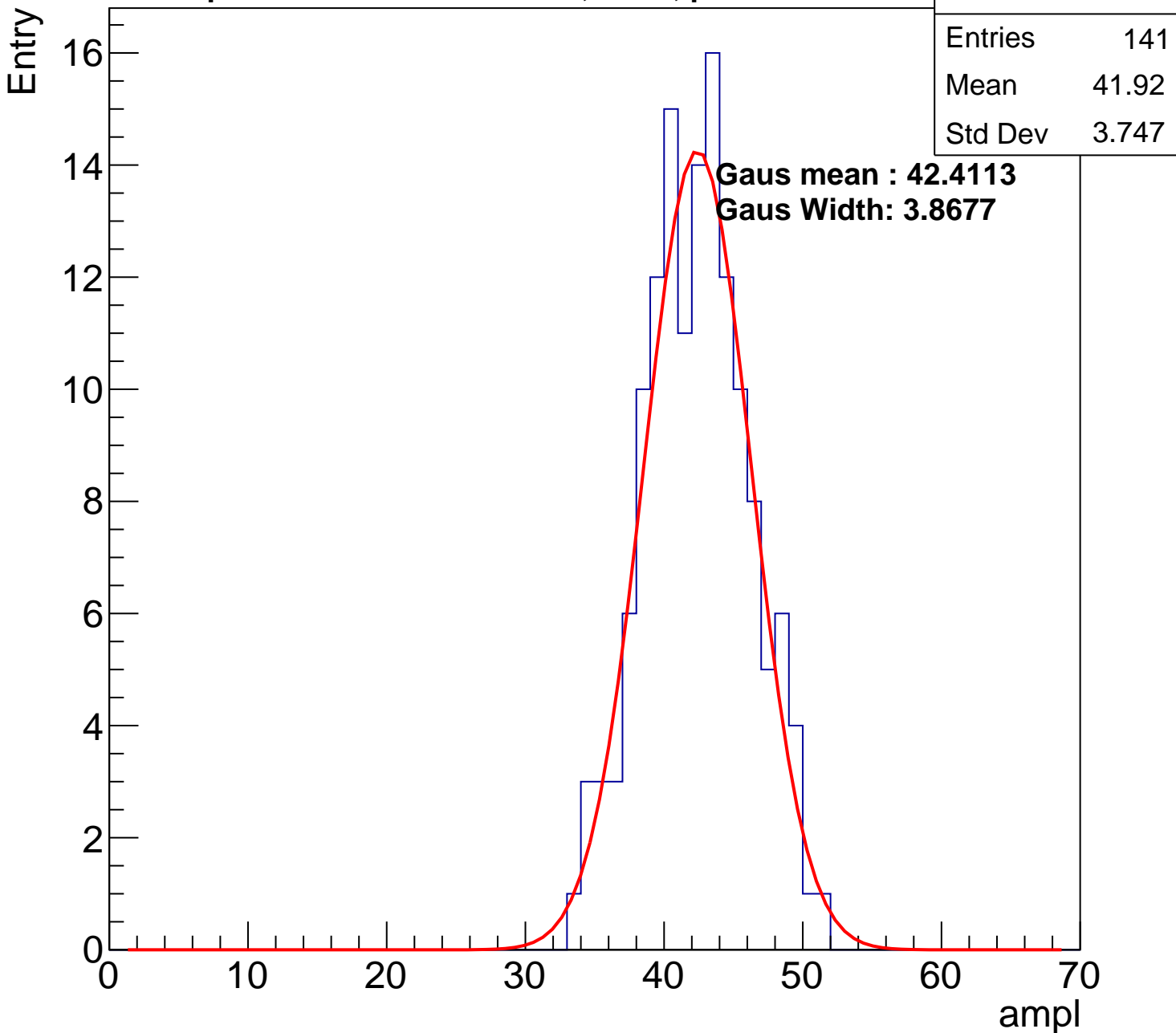
Gaus Width: 3.8677

Entry

16
14
12
10
8
6
4
2
0

ampl

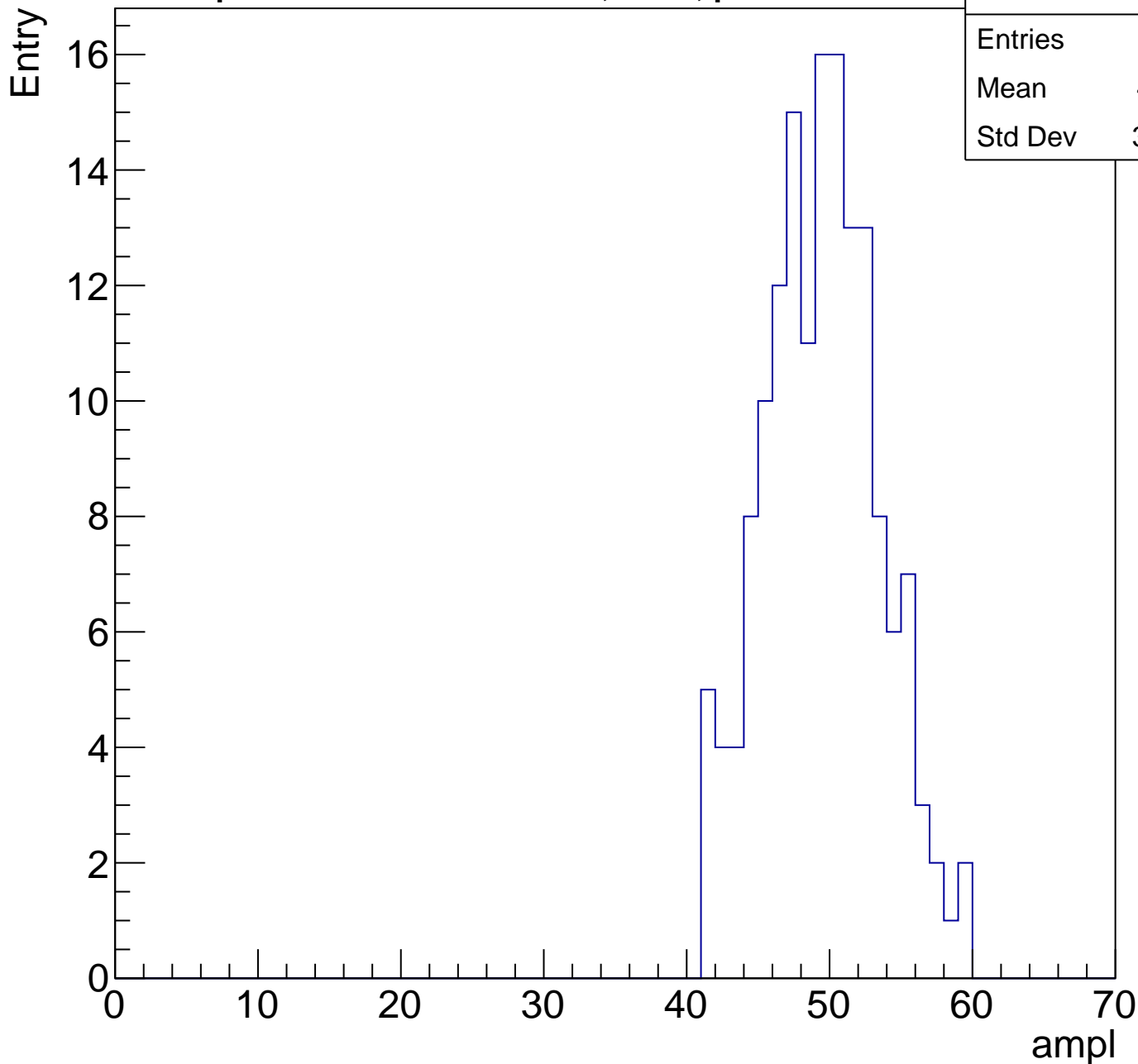
0 10 20 30 40 50 60 70



B0L101S, U18-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	49.01
Std Dev	3.979

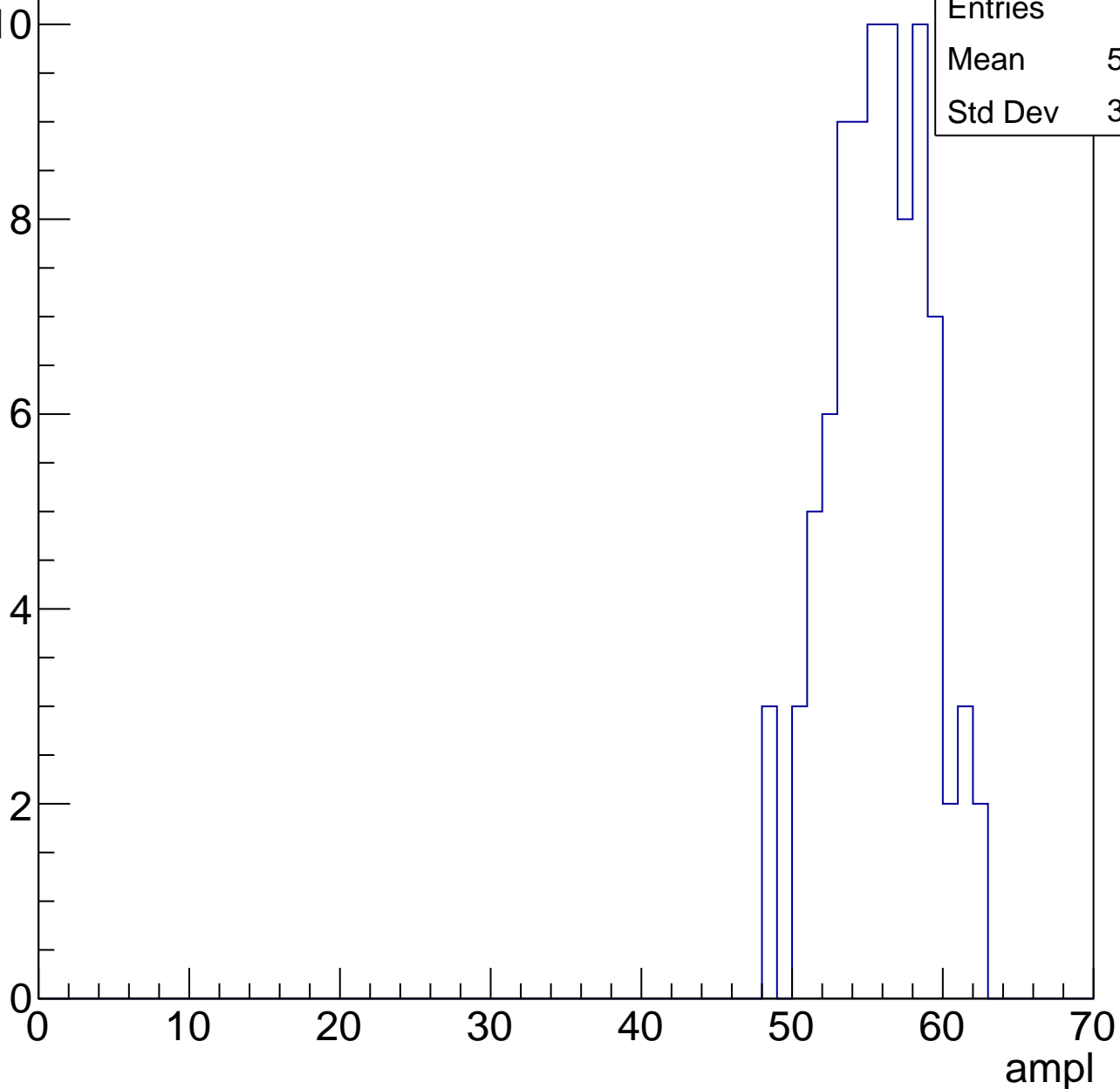


B0L101S, U18-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	87
Mean	55.29
Std Dev	3.205



B0L101S, U18-ch97, adc5

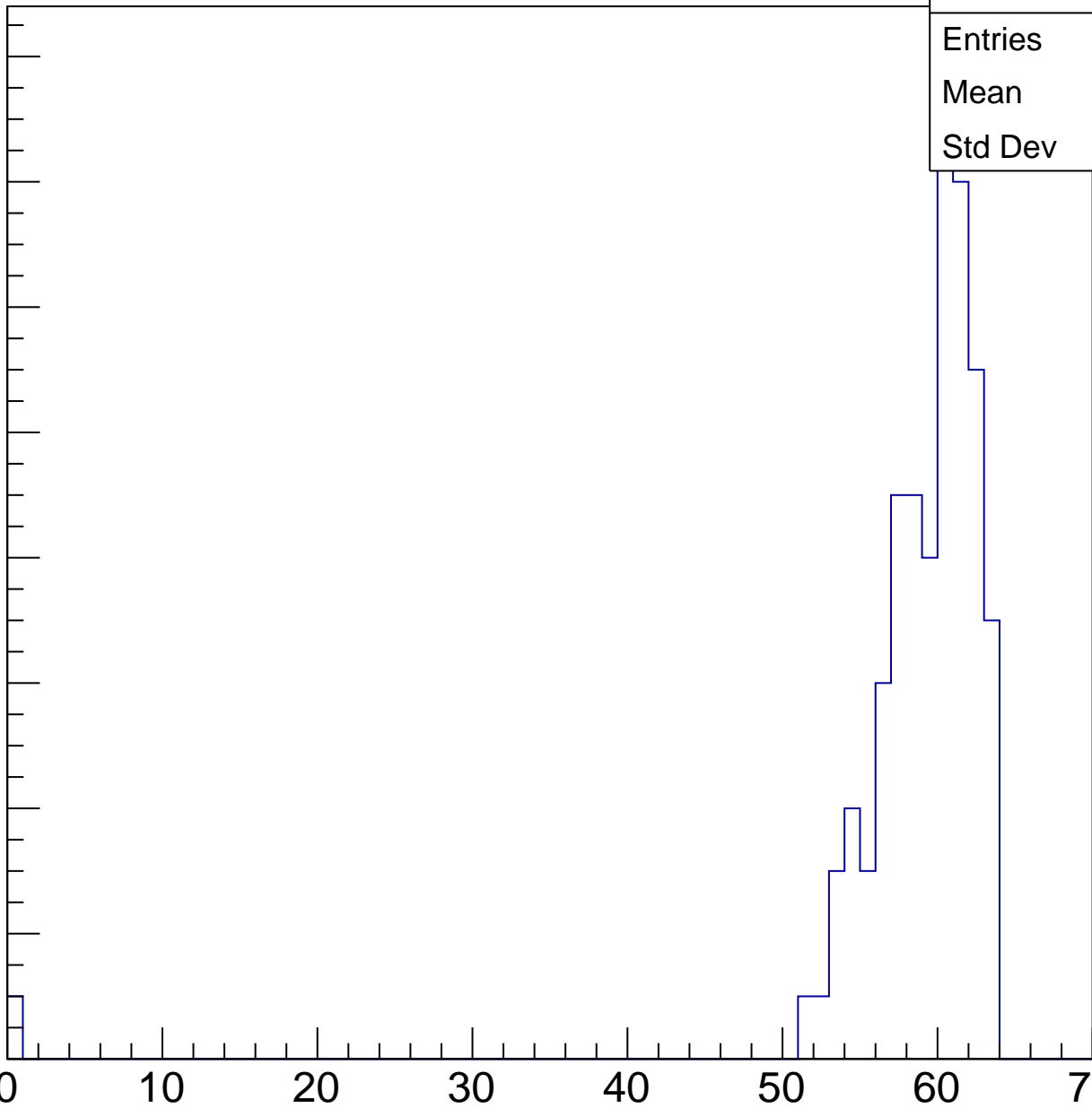
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.31
Std Dev	6.711

ampl



B0L101S, U18-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	32
Mean	59.38
Std Dev	10.9

Entry

12

10

8

6

4

2

0

0

10

20

30

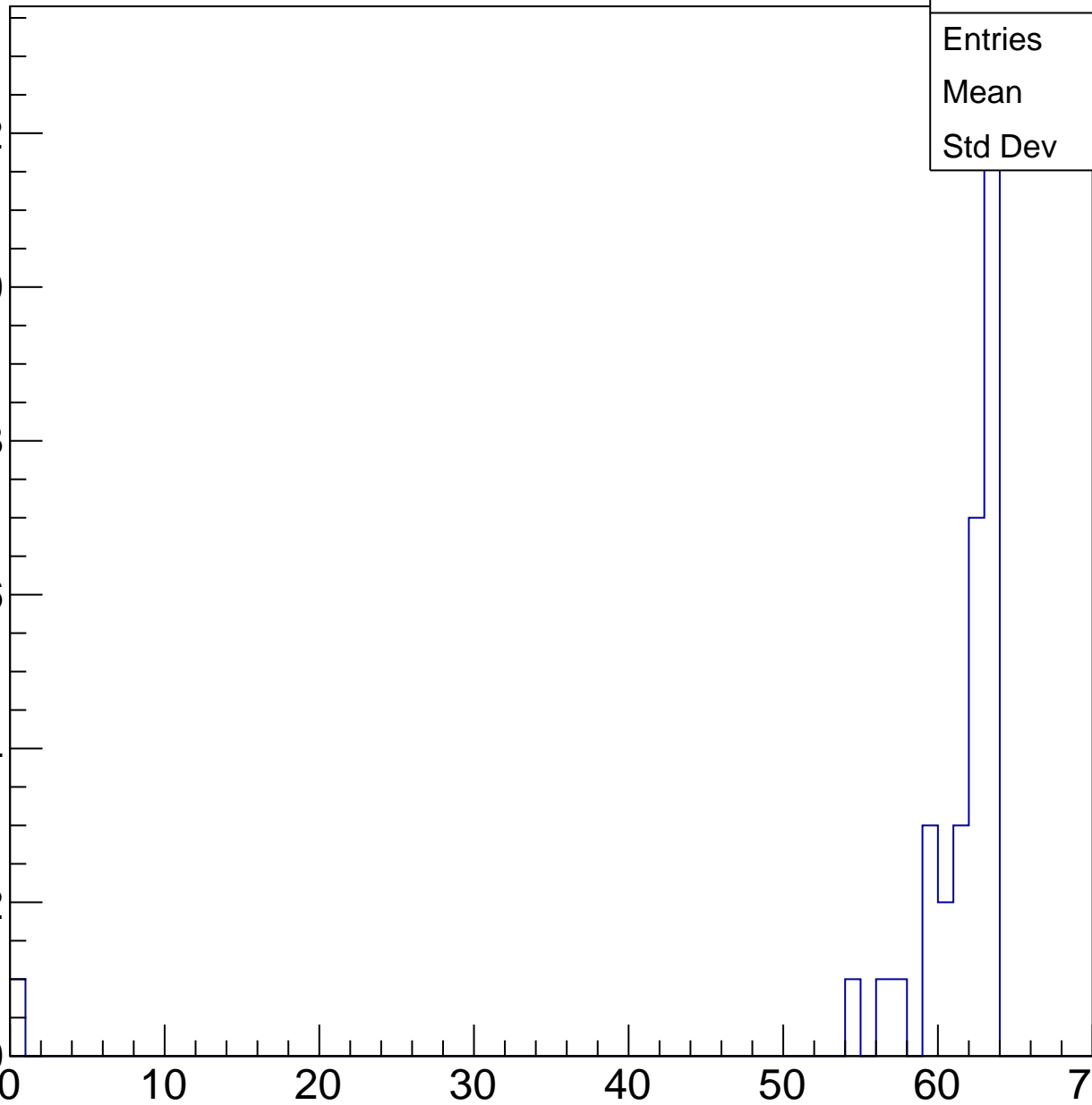
40

50

60

70

ampl



B0L101S, U18-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch98, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.29
Std Dev	5.187

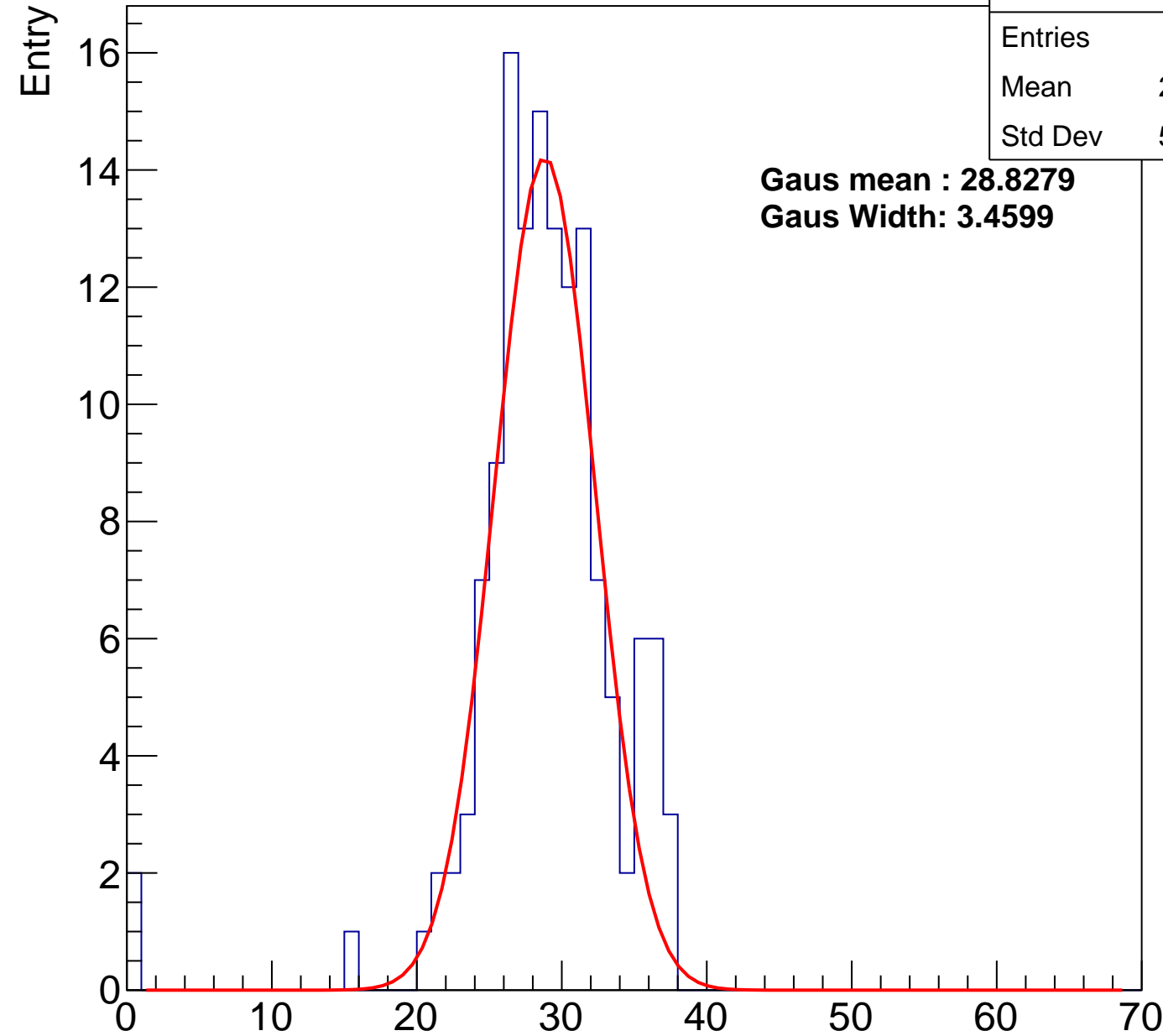
Gaus mean : 28.8279

Gaus Width: 3.4599

Entry

16
14
12
10
8
6
4
2
0

ampl



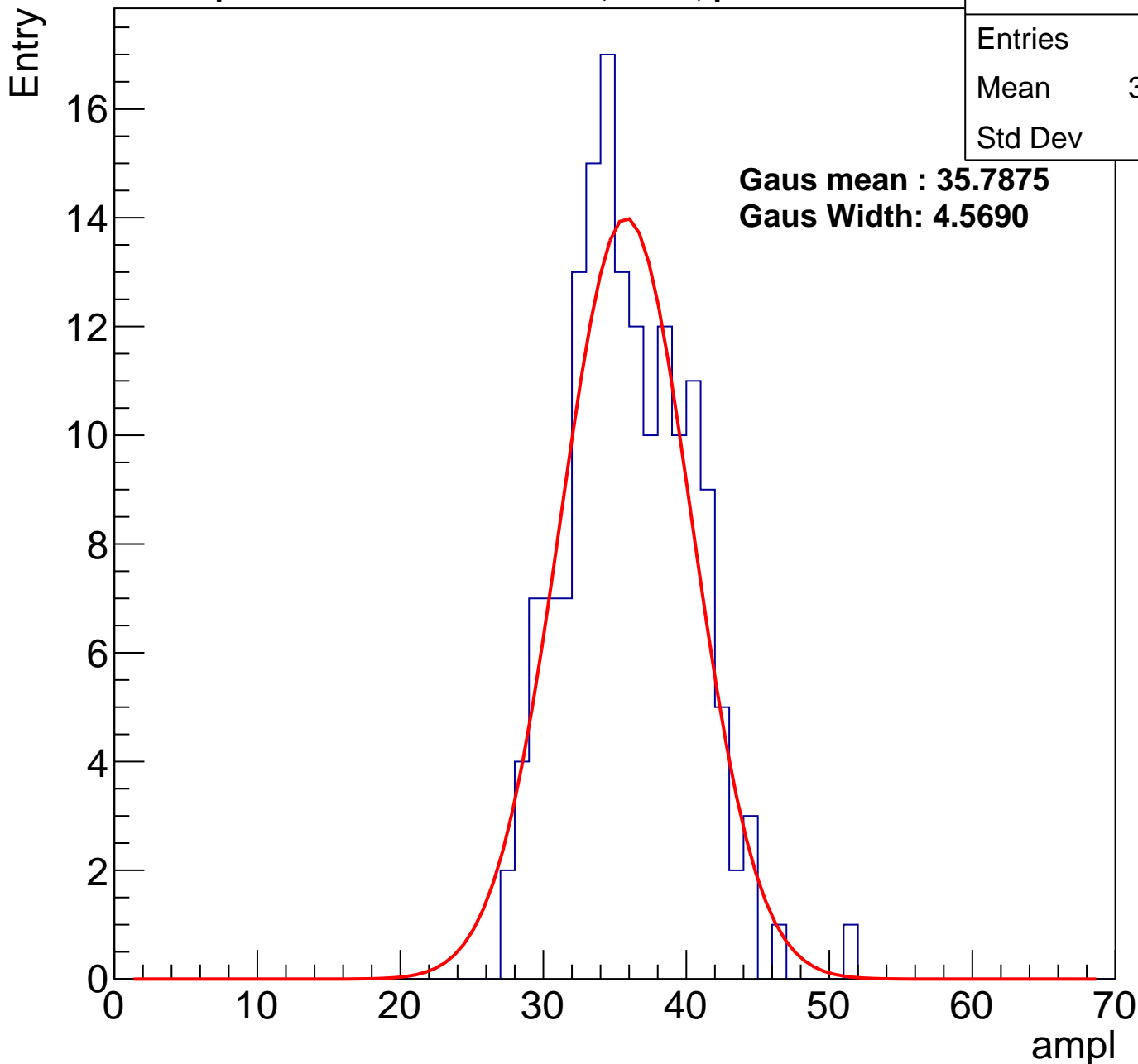
B0L101S, U18-ch98, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	35.54
Std Dev	4.26

Gaus mean : 35.7875

Gaus Width: 4.5690



B0L101S, U18-ch98, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

124

Mean

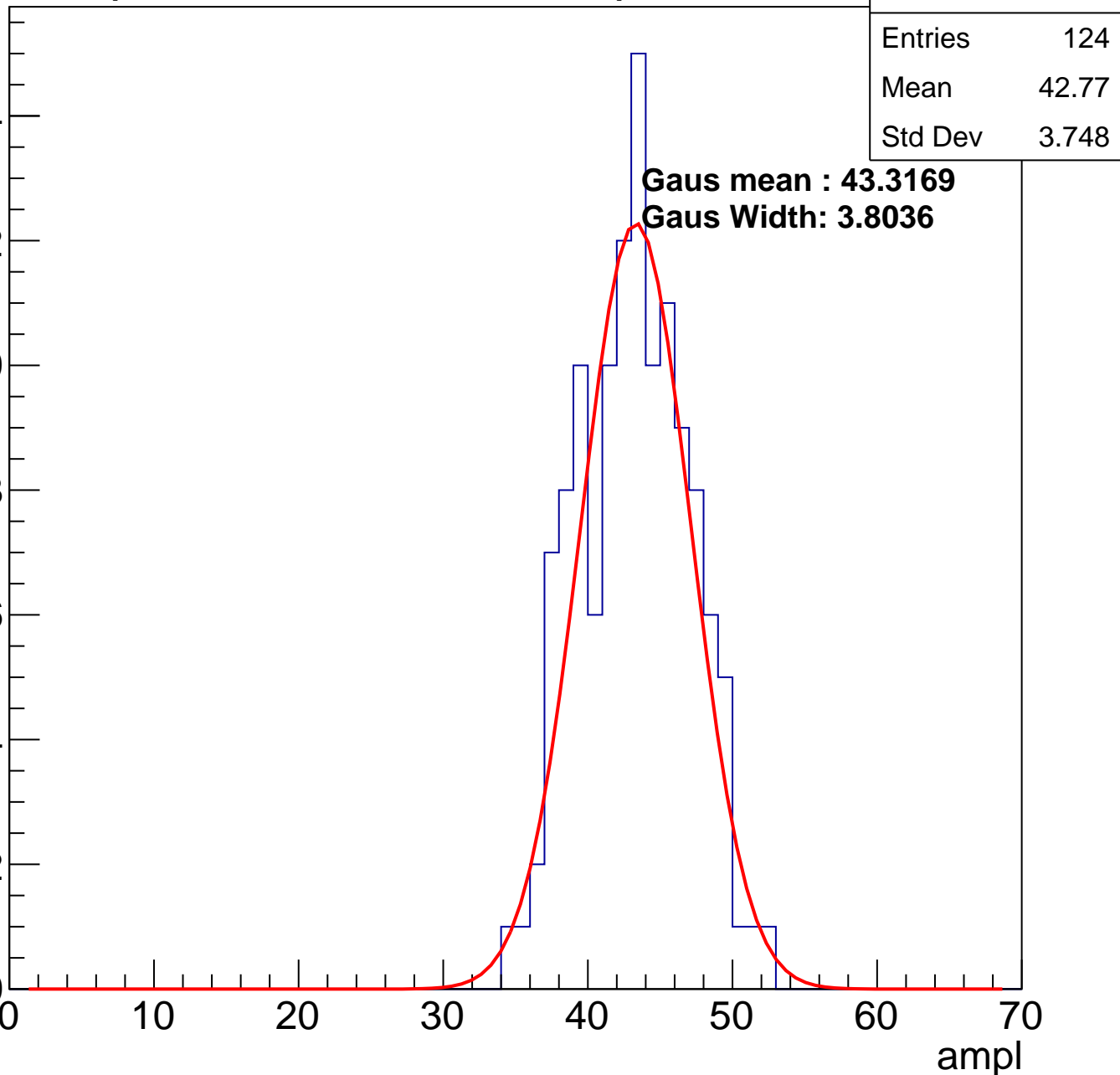
42.77

Std Dev

3.748

Gaus mean : 43.3169

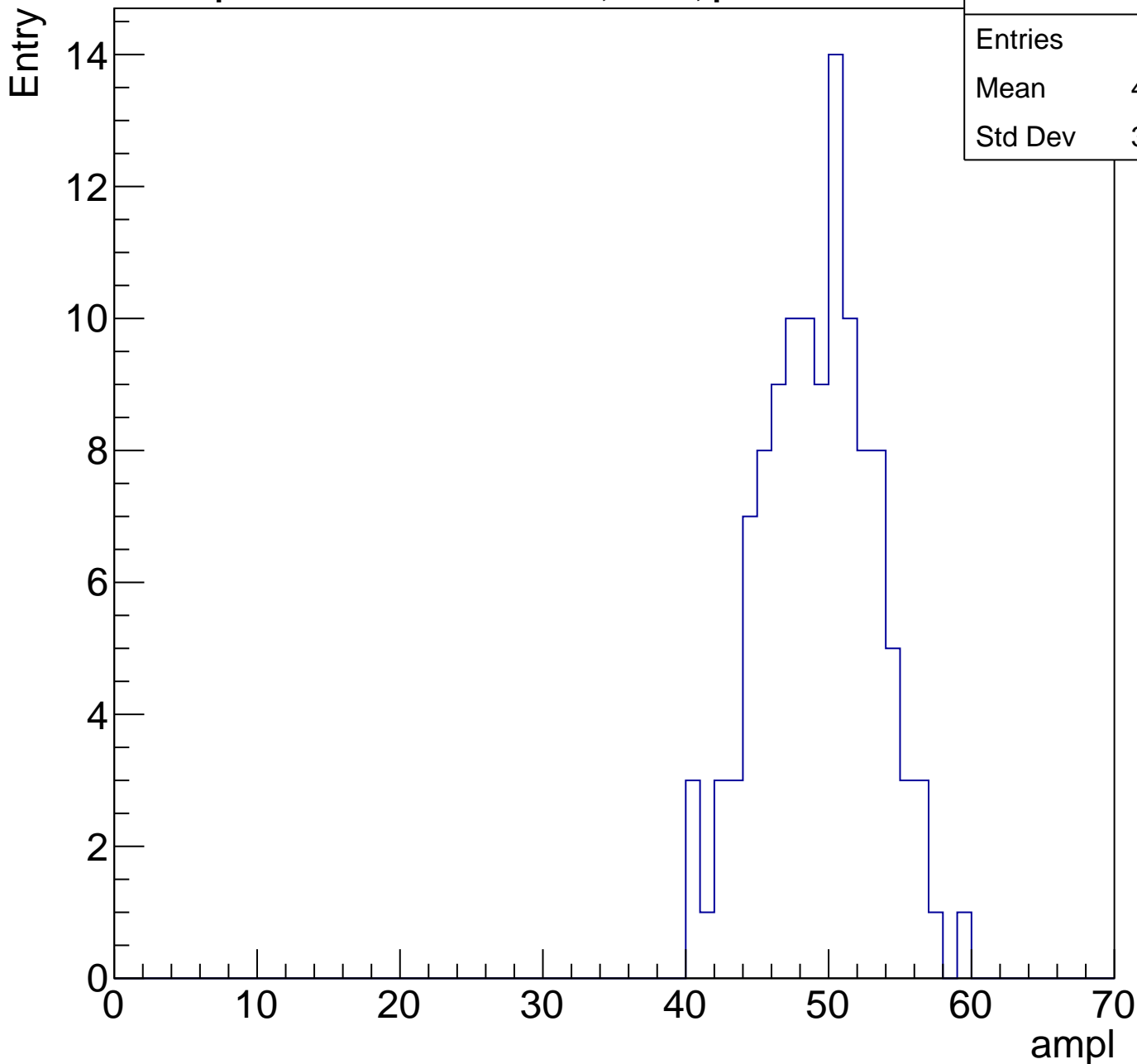
Gaus Width: 3.8036



B0L101S, U18-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	48.78
Std Dev	3.926



B0L101S, U18-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	53.97
Std Dev	6.158

Entry

14
12
10
8
6
4
2
0

0

10

20

30

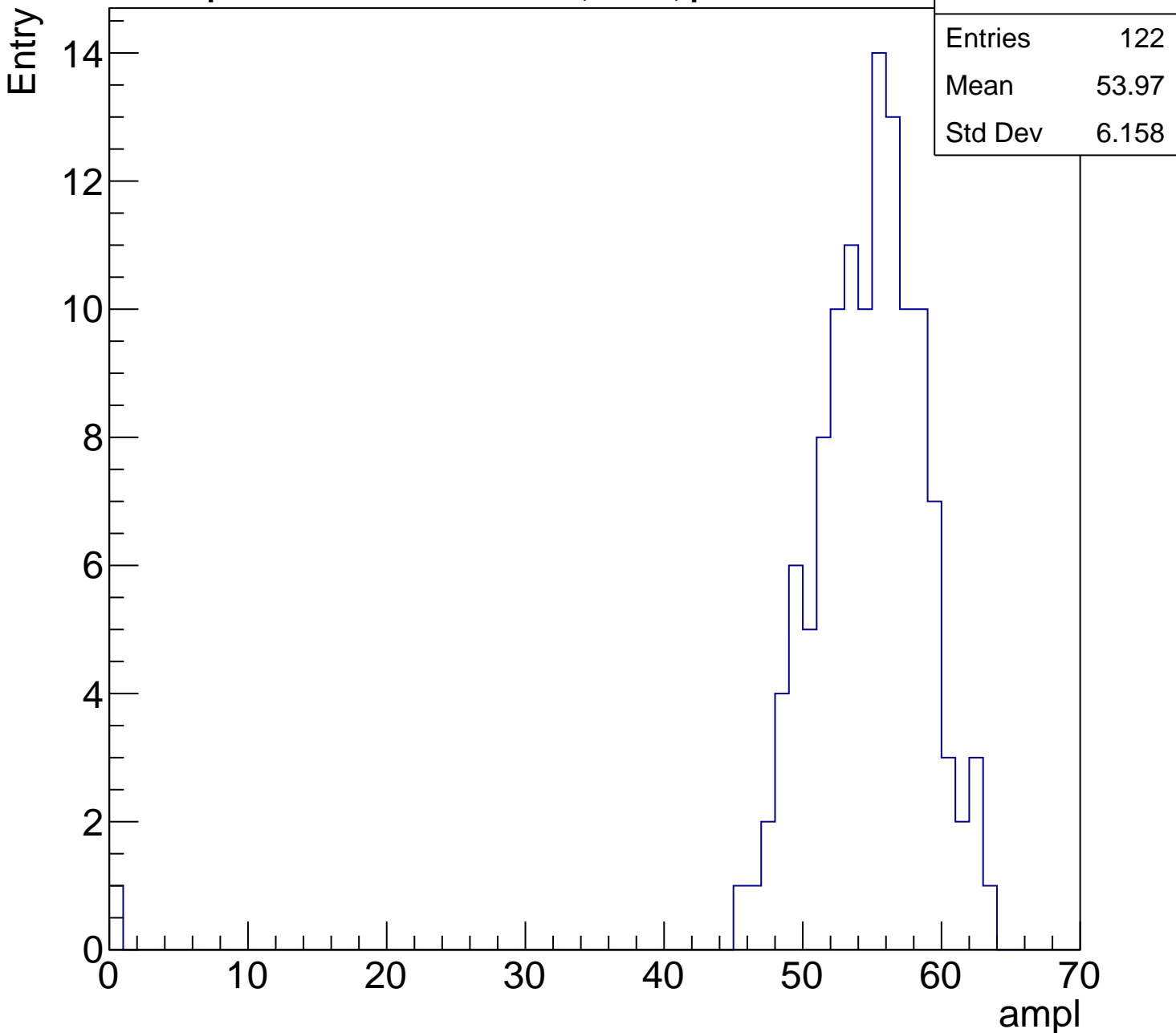
40

50

60

70

ampl



B0L101S, U18-ch98, adc5

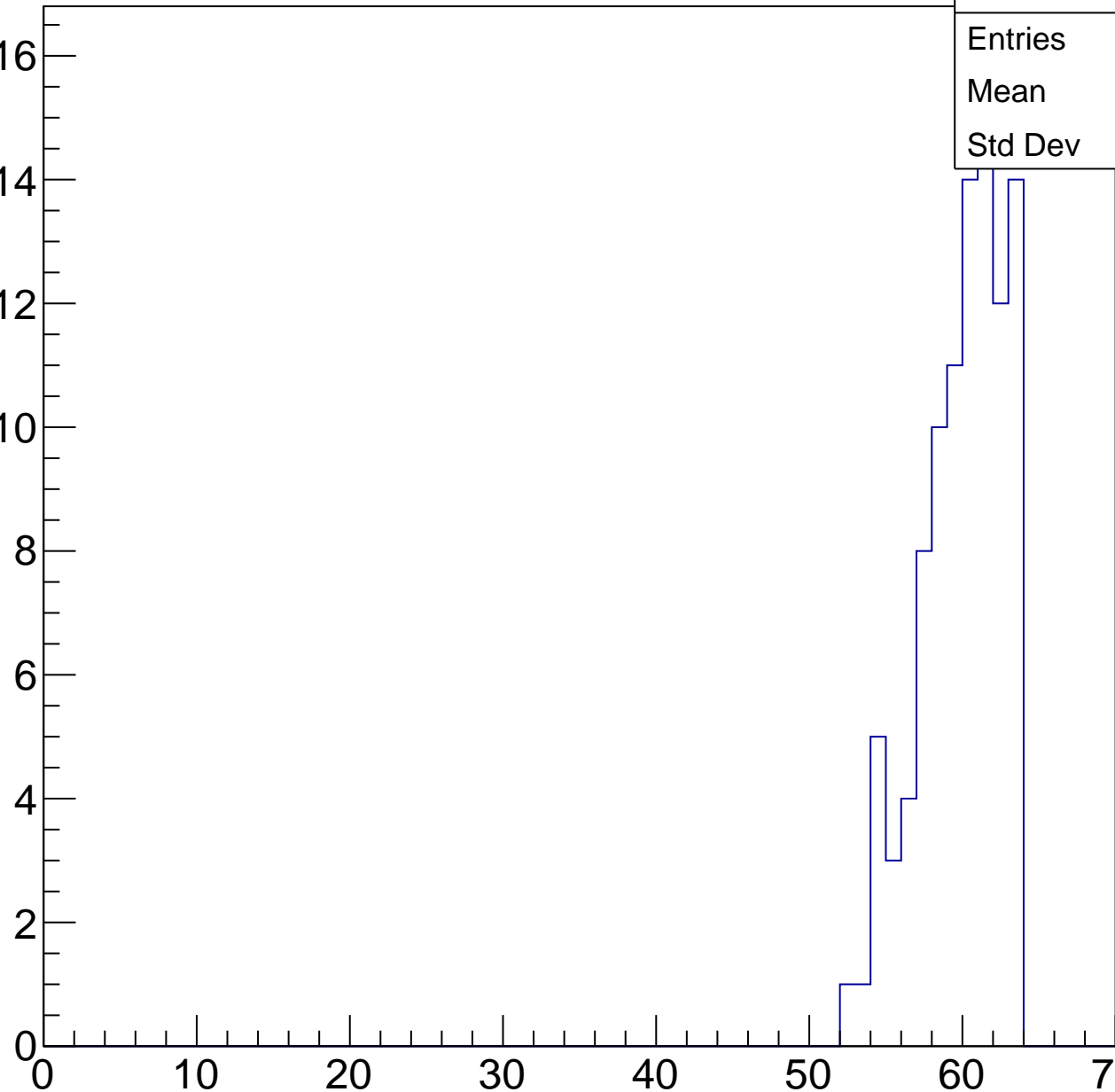
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.51
Std Dev	2.698

ampl

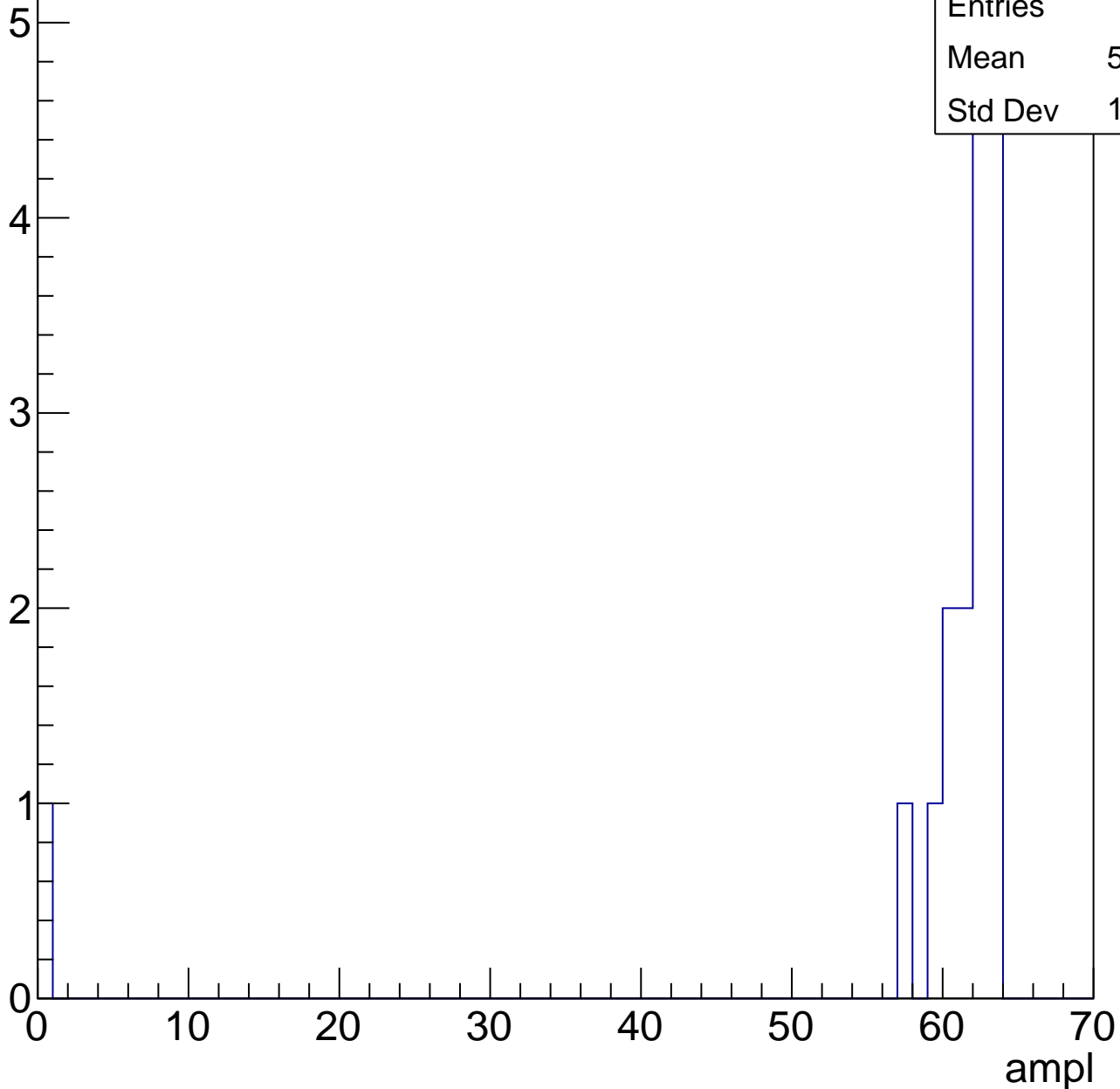


B0L101S, U18-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

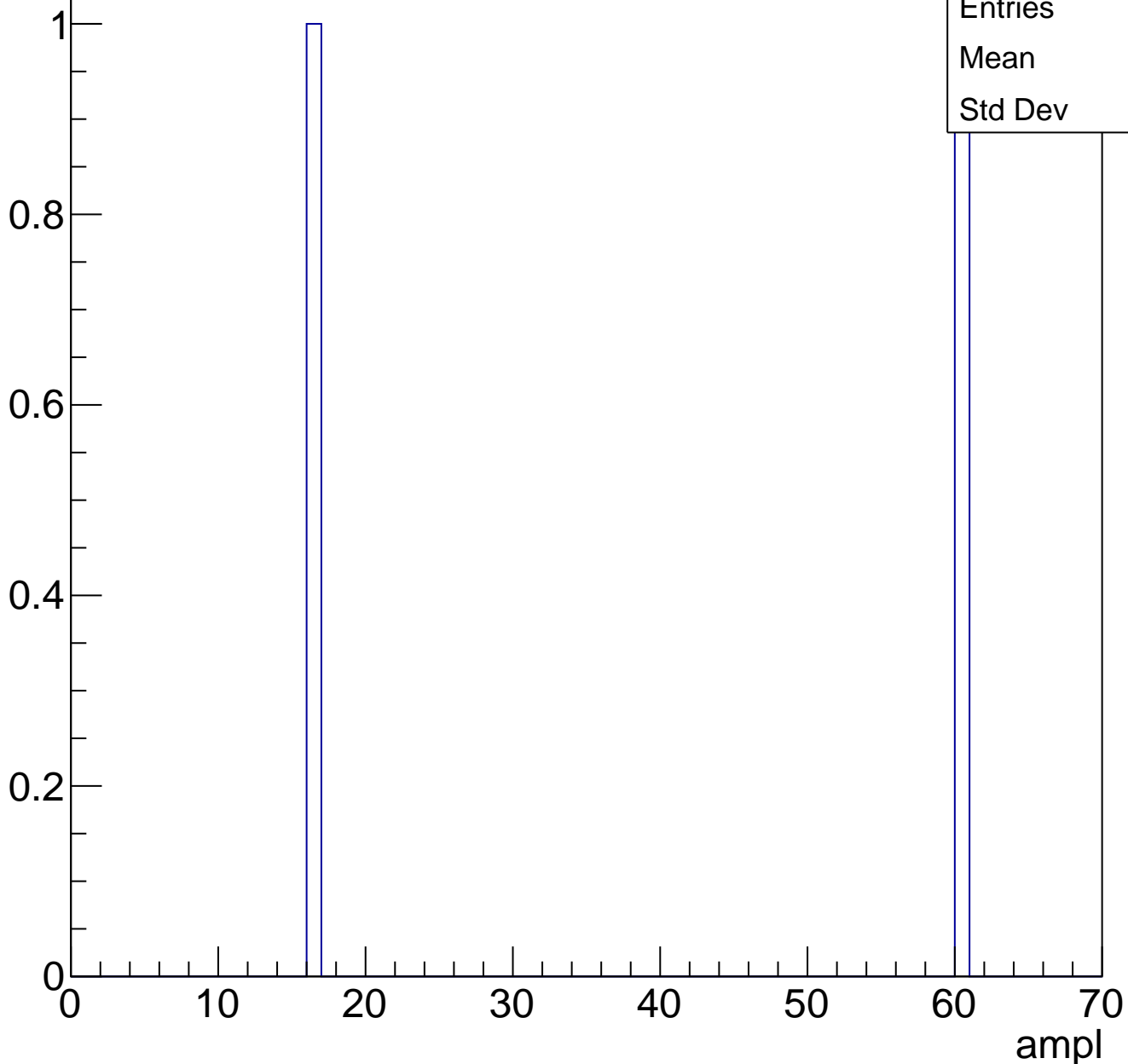
Entries	17
Mean	57.82
Std Dev	14.55



B0L101S, U18-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	38
Std Dev	22

B0L101S, U18-ch99, adc0

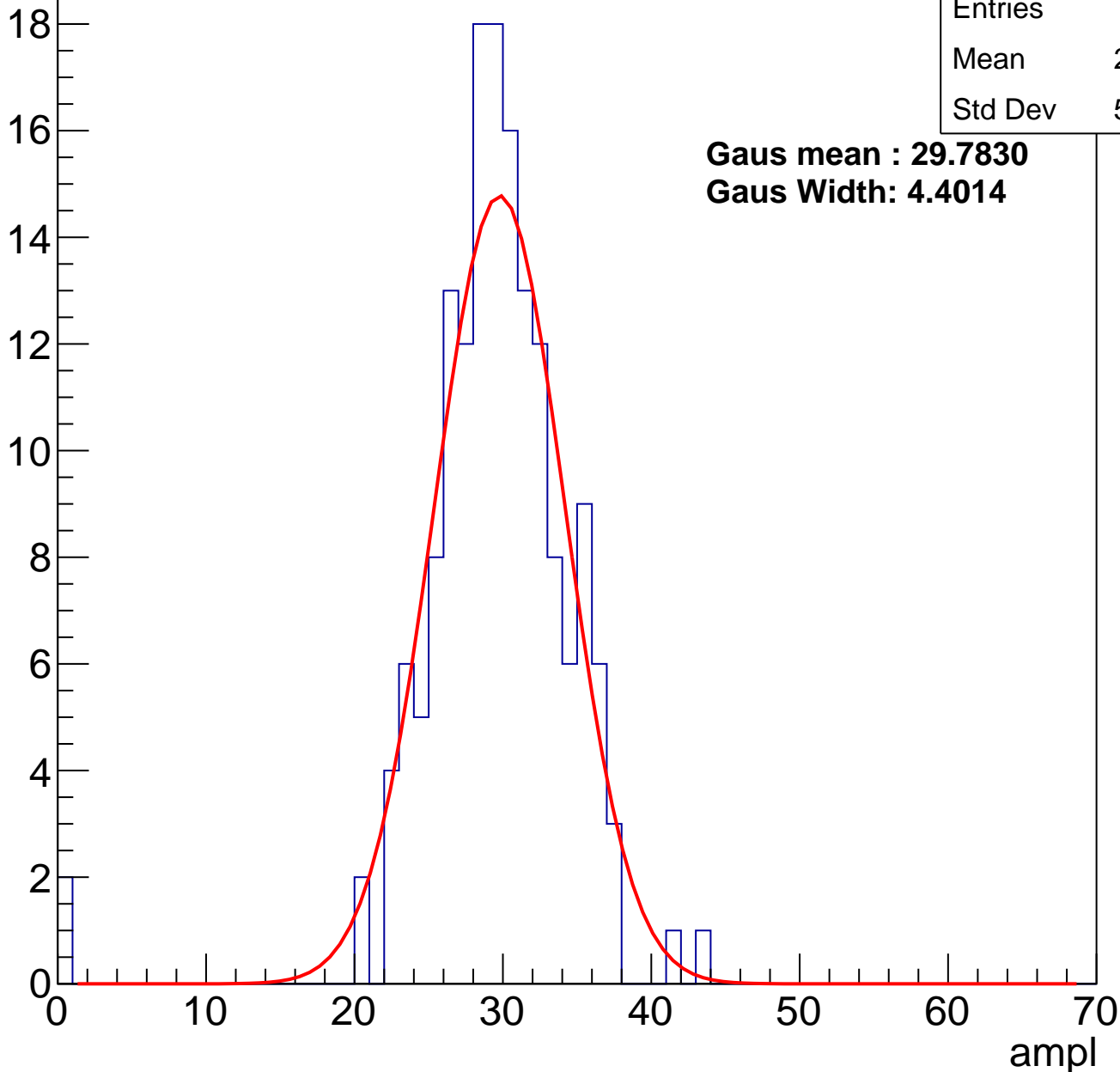
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.05
Std Dev	5.134

Gaus mean : 29.7830

Gaus Width: 4.4014

Entry



B0L101S, U18-ch99, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	37.23
Std Dev	3.937

Gaus mean : 37.5554

Gaus Width: 3.7111

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

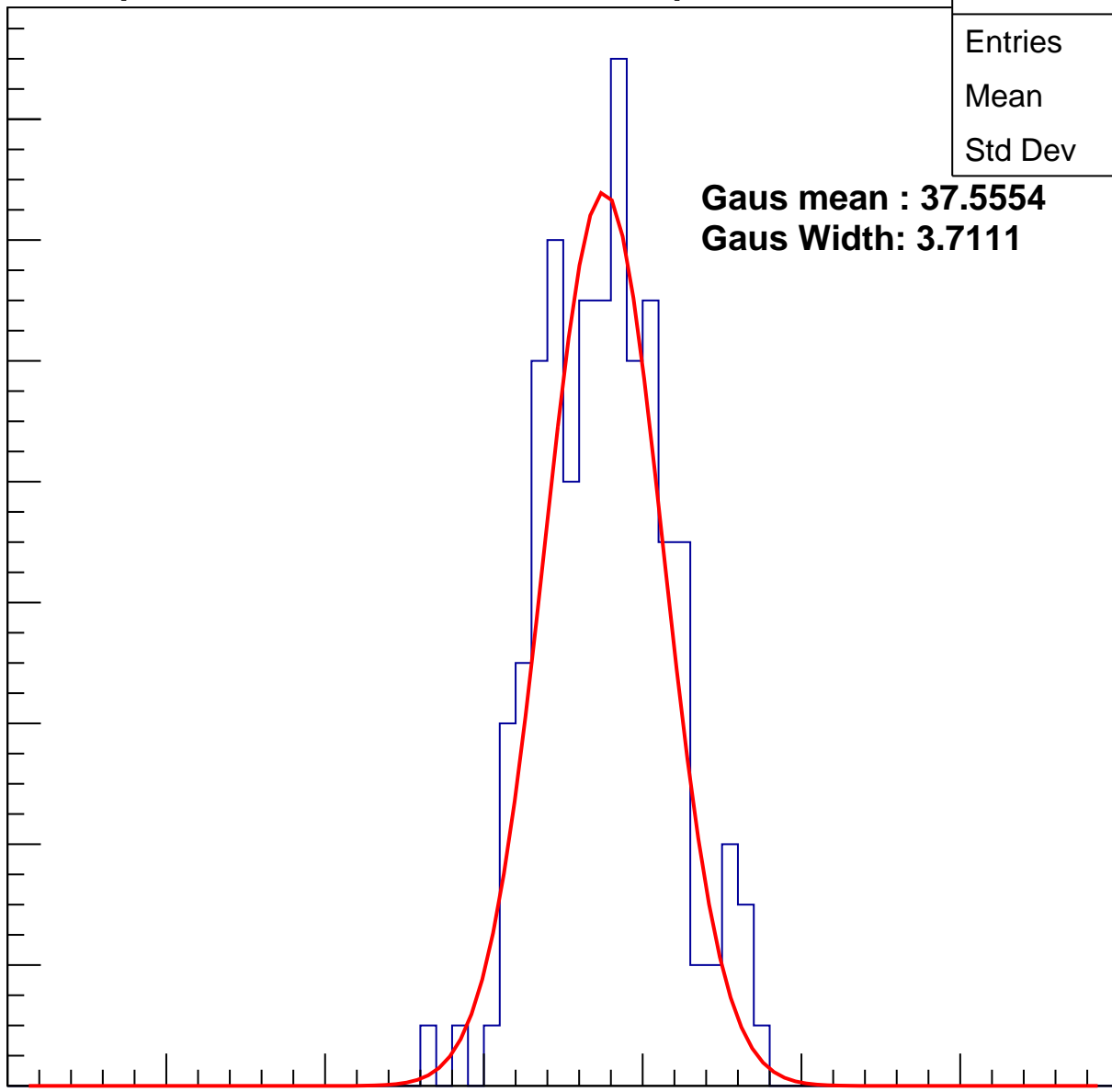
40

50

60

70

ampl



B0L101S, U18-ch99, adc2

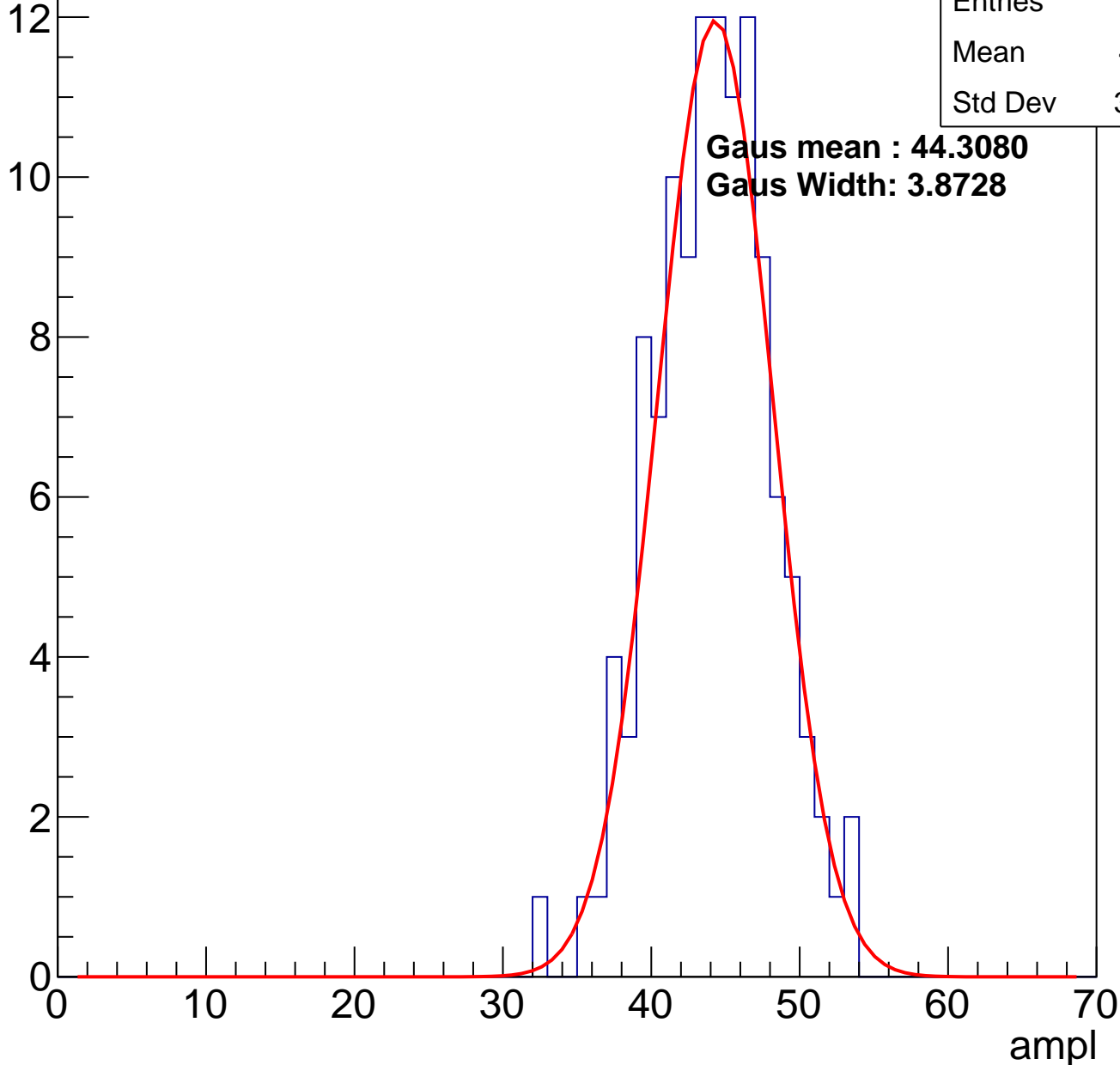
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	43.71
Std Dev	3.918

Gaus mean : 44.3080

Gaus Width: 3.8728

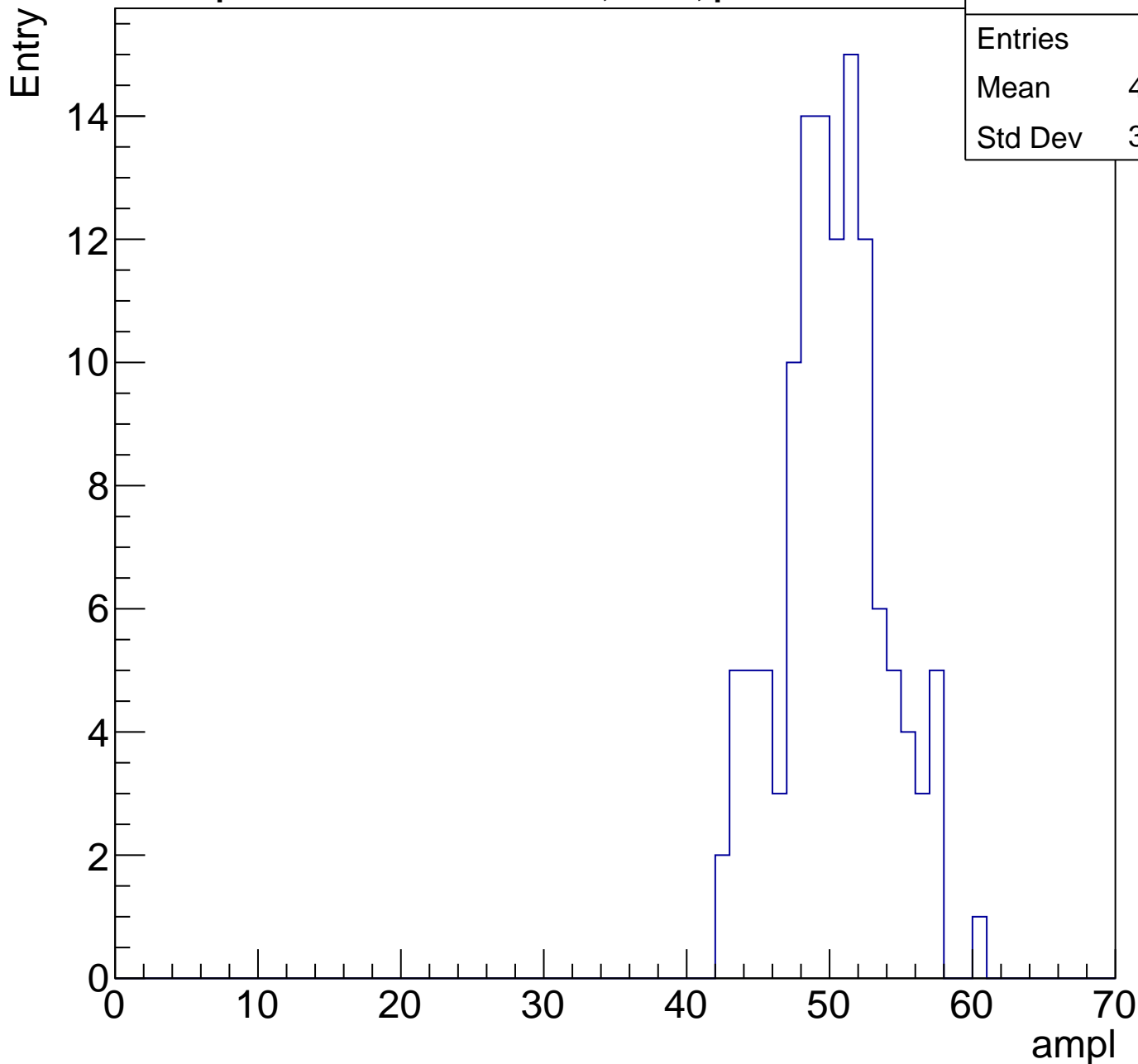
Entry



B0L101S, U18-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

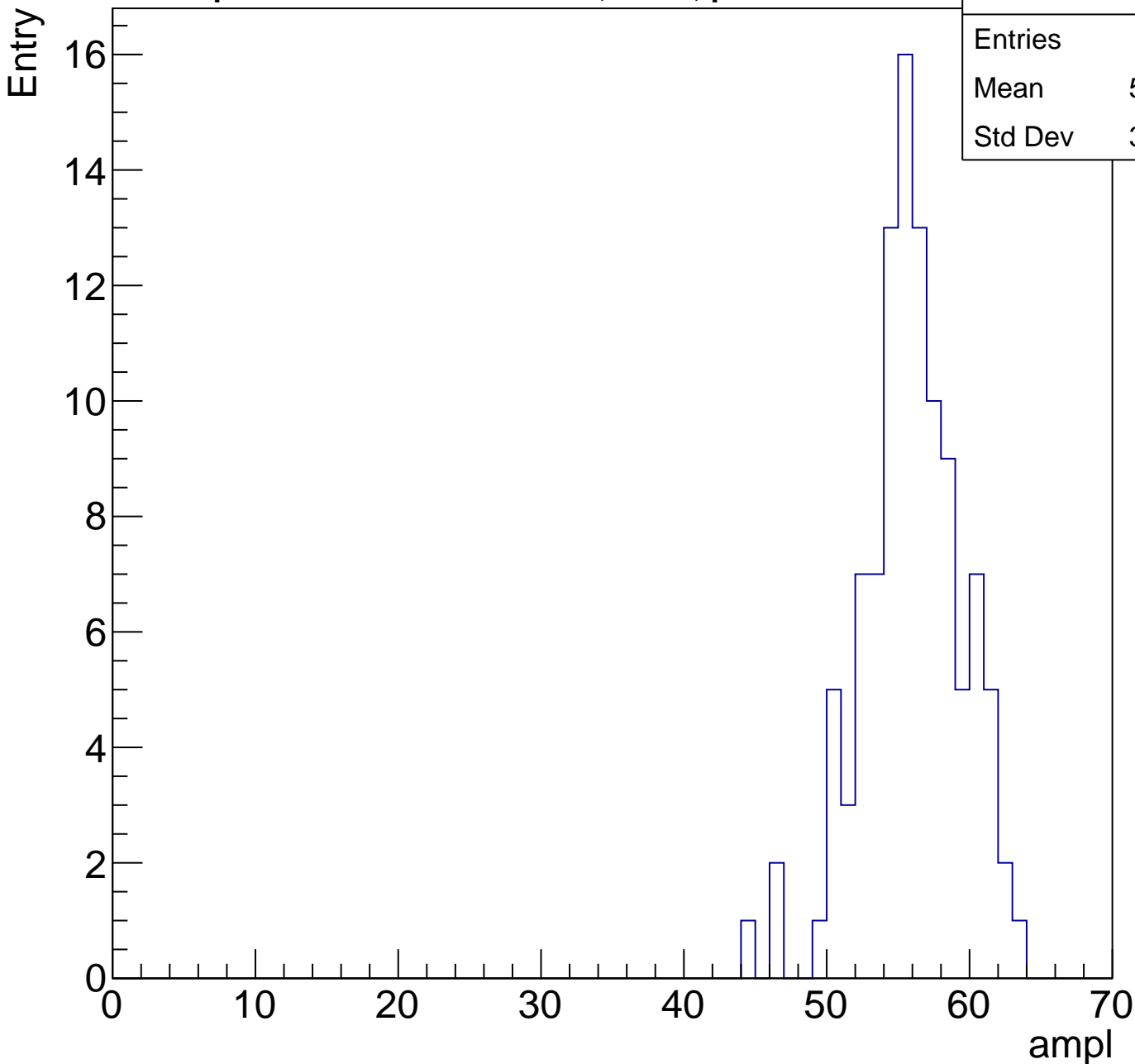
Entries	121
Mean	49.75
Std Dev	3.669



B0L101S, U18-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	55.44
Std Dev	3.494



B0L101S, U18-ch99, adc5

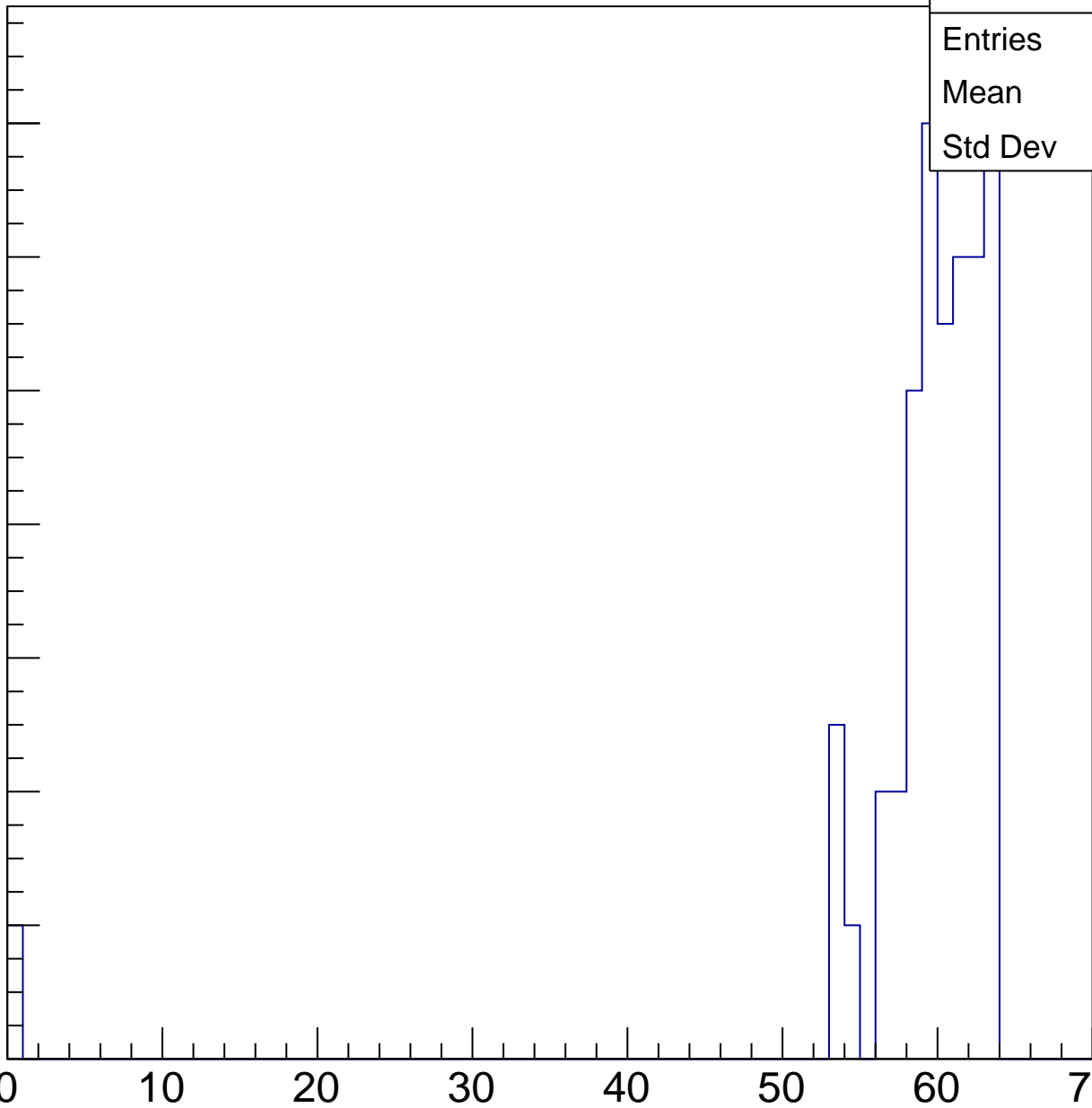
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.37
Std Dev	9.154

ampl

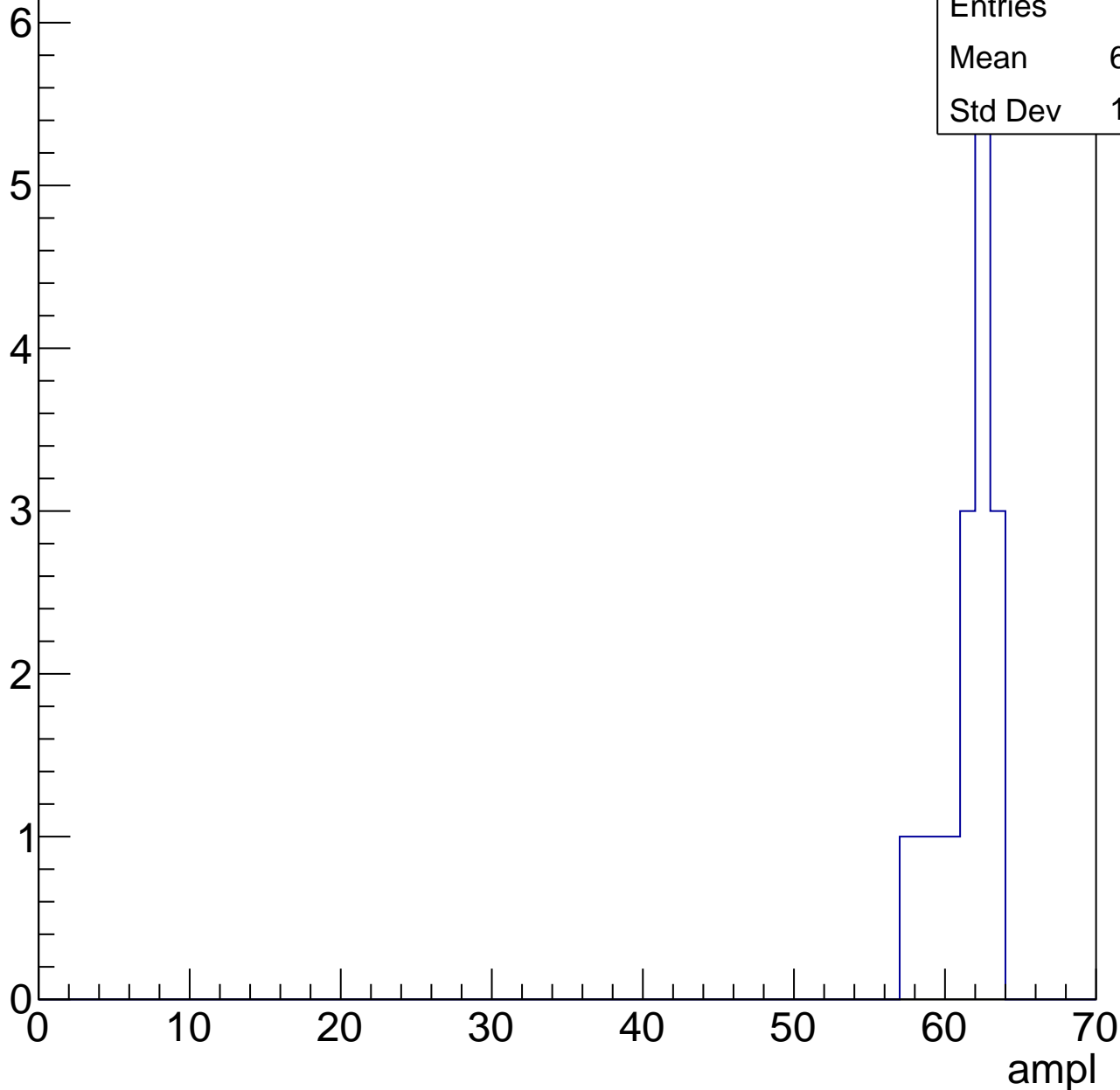


B0L101S, U18-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.12
Std Dev	1.728



B0L101S, U18-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch100, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	26.89
Std Dev	3.914

Gaus mean : 27.2659

Gaus Width: 3.7839

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch100, adc1

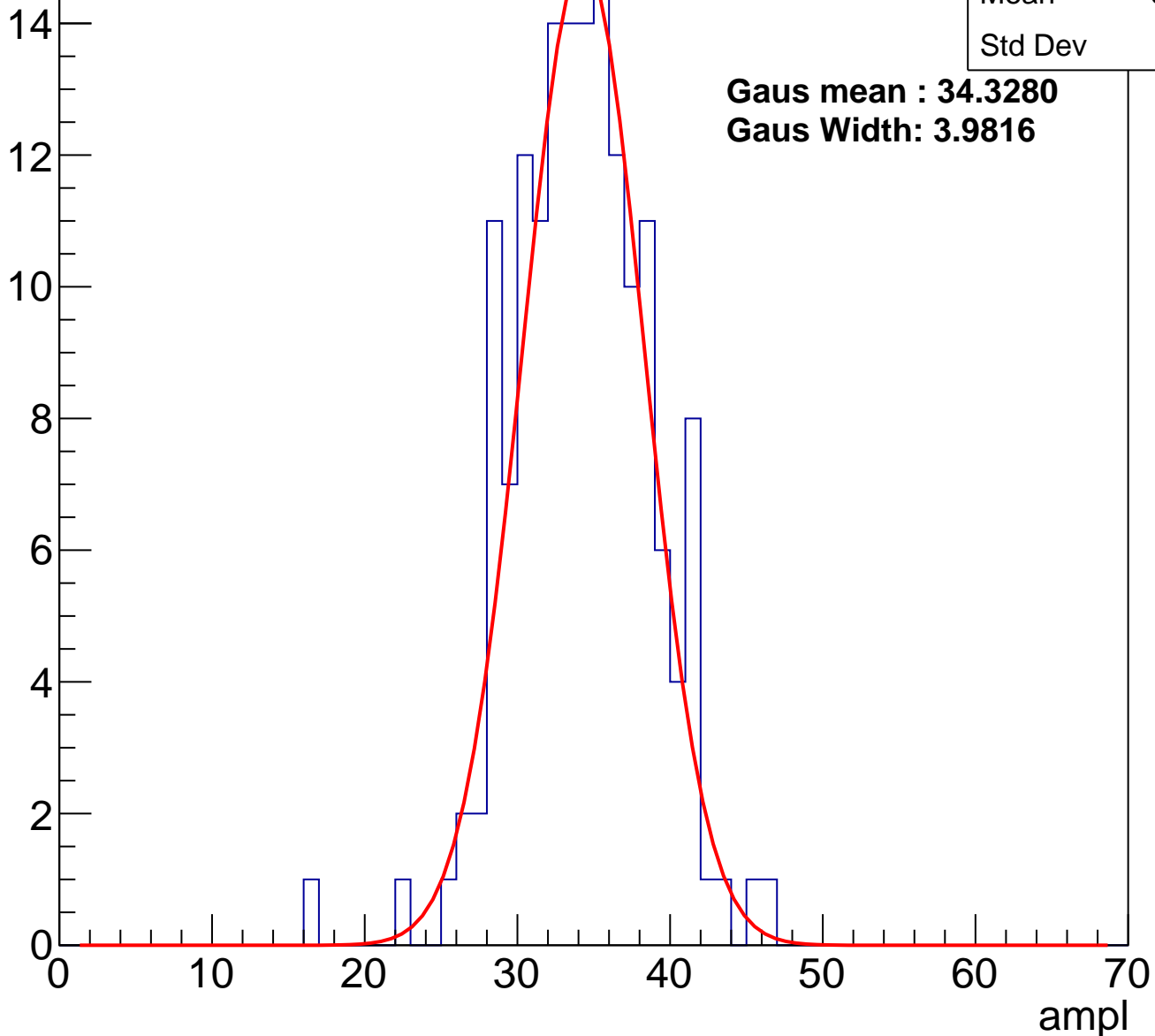
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	33.81
Std Dev	4.42

Gaus mean : 34.3280

Gaus Width: 3.9816

Entry



B0L101S, U18-ch100, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

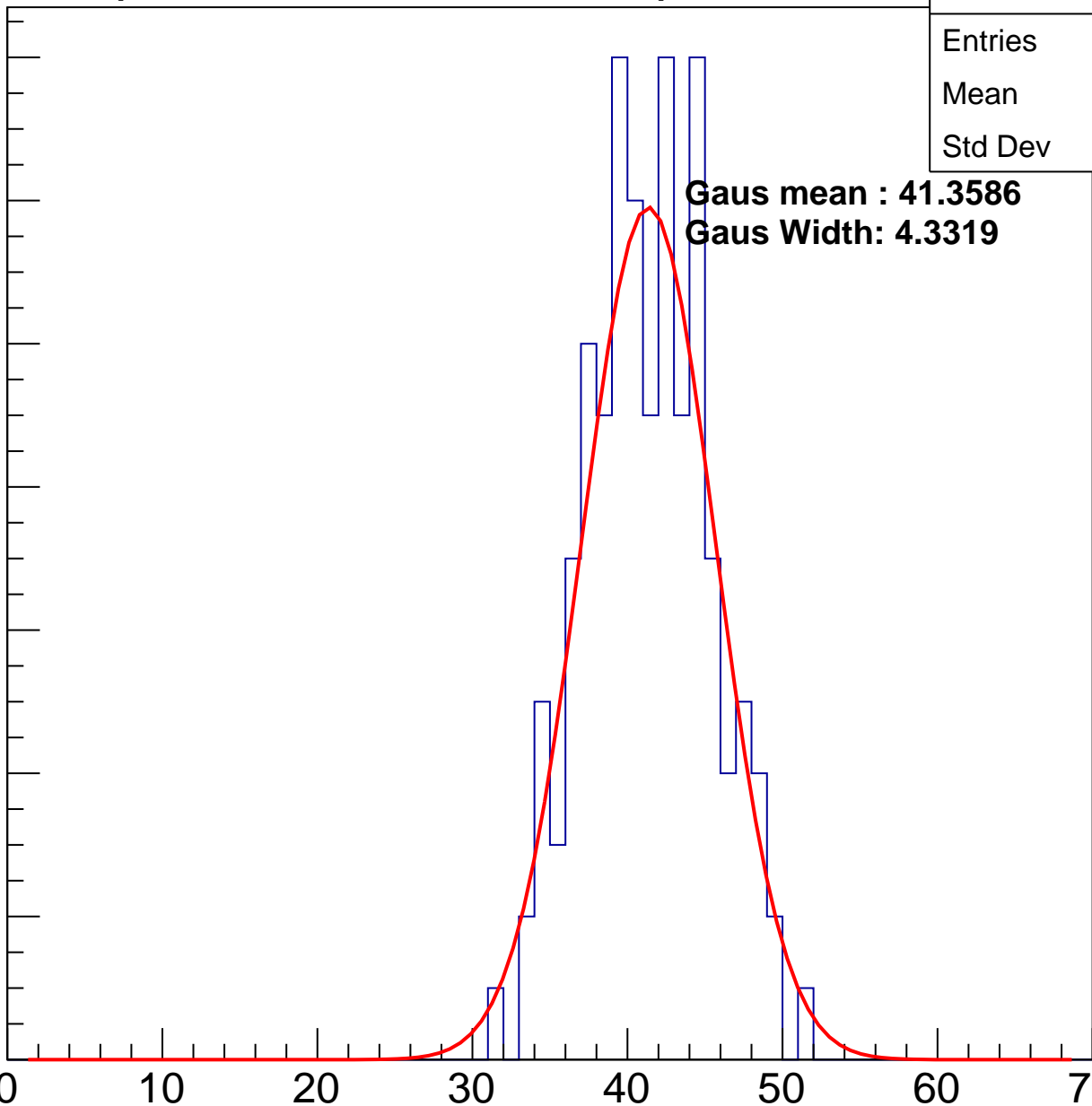
14
12
10
8
6
4
2
0

Entries	132
Mean	40.89
Std Dev	3.946

Gaus mean : 41.3586

Gaus Width: 4.3319

ampl



B0L101S, U18-ch100, adc3

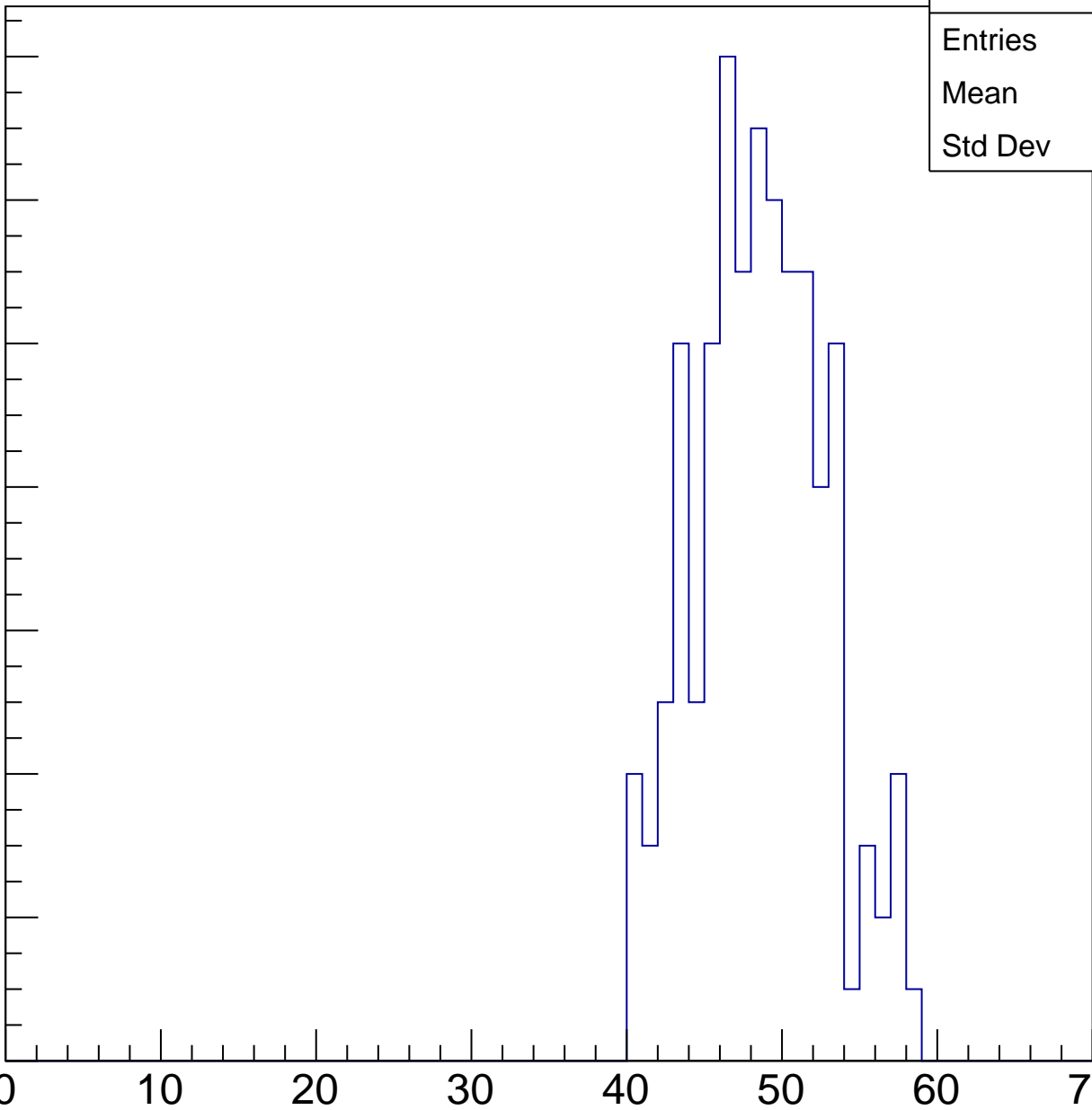
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	138
Mean	48.12
Std Dev	4.114

ampl



B0L101S, U18-ch100, adc4

calib_packv5_042523_0143.root, FC#1, port C1

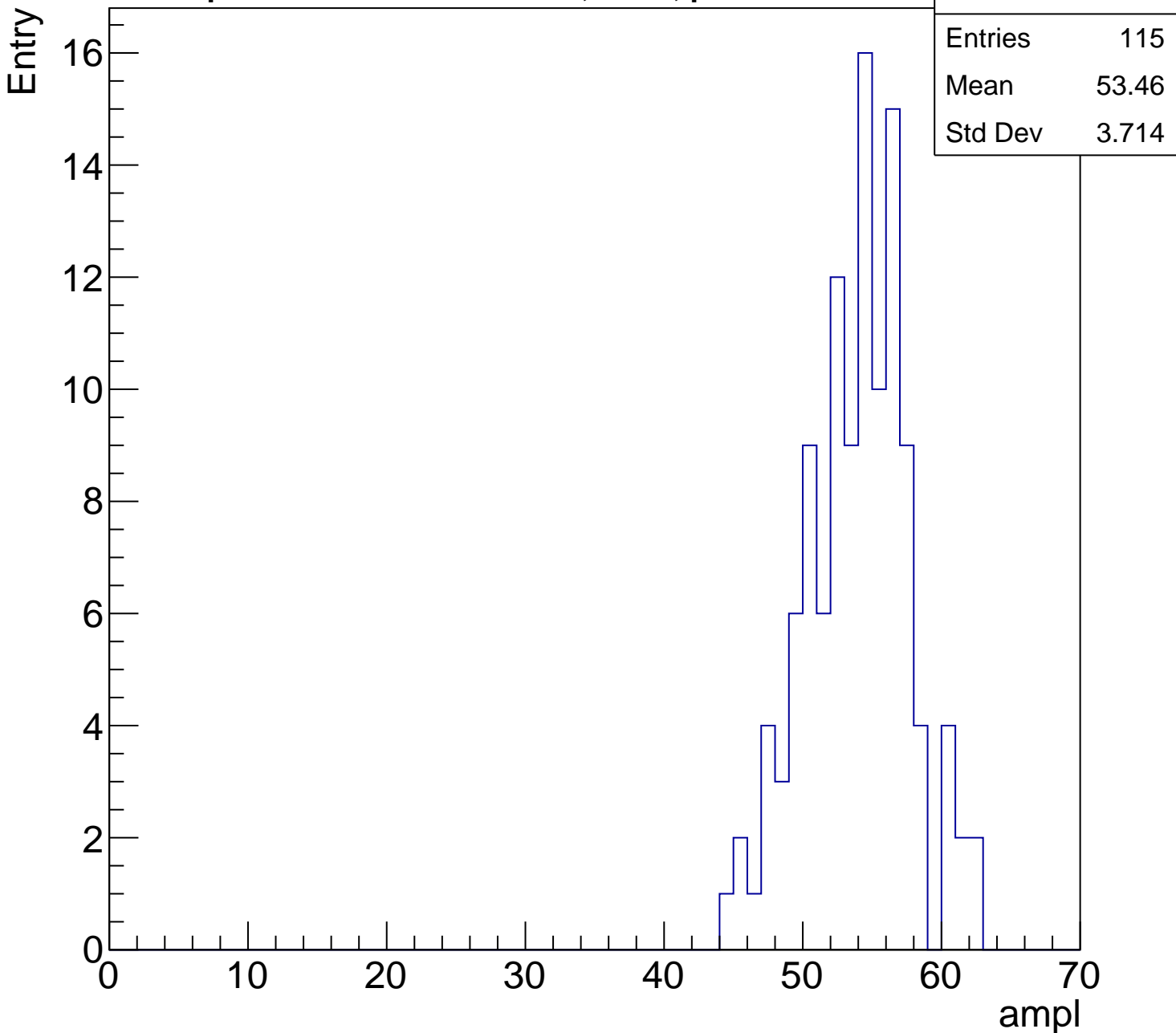
Entries	115
Mean	53.46
Std Dev	3.714

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

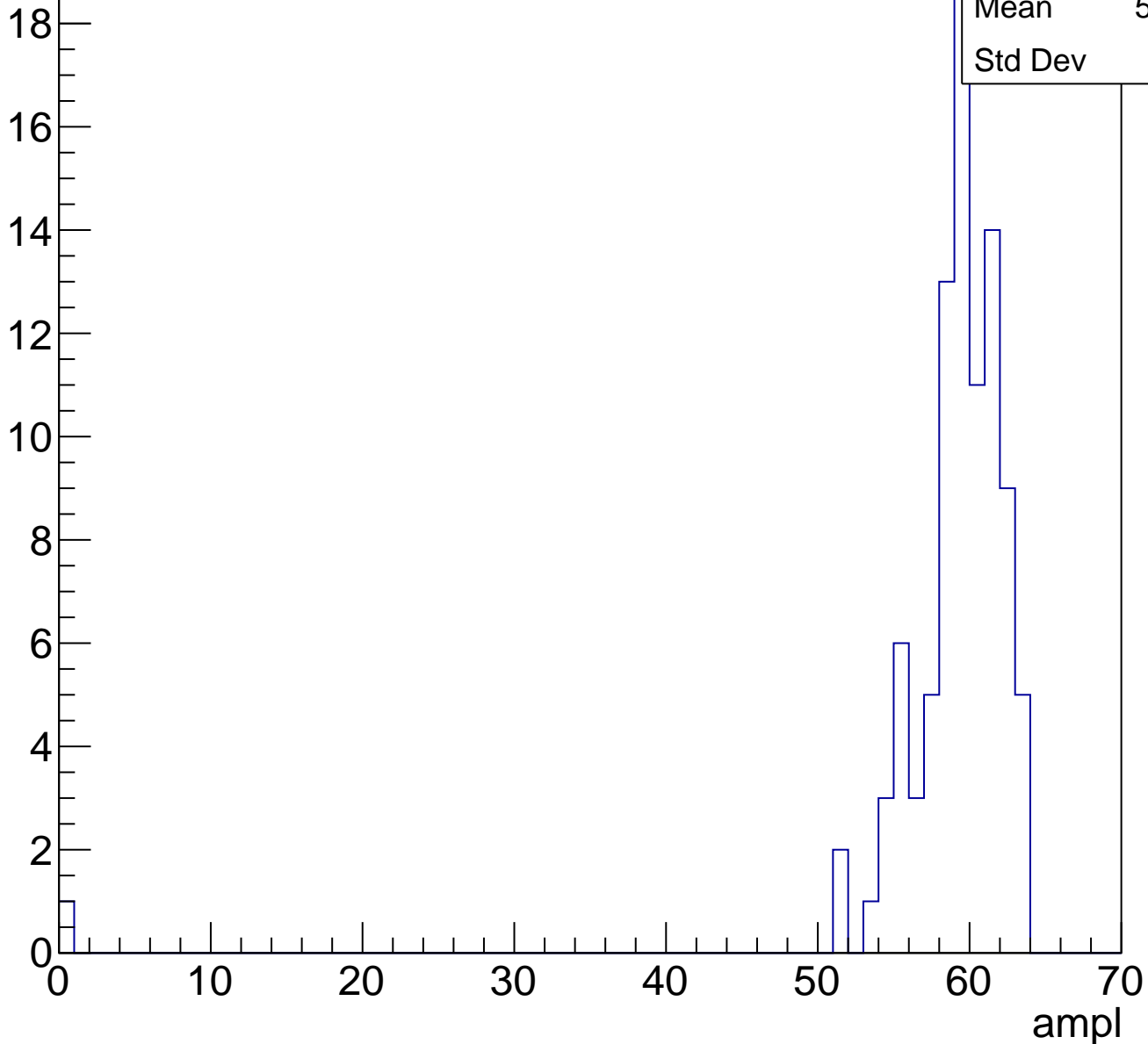
ampl



B0L101S, U18-ch100, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch100, adc6

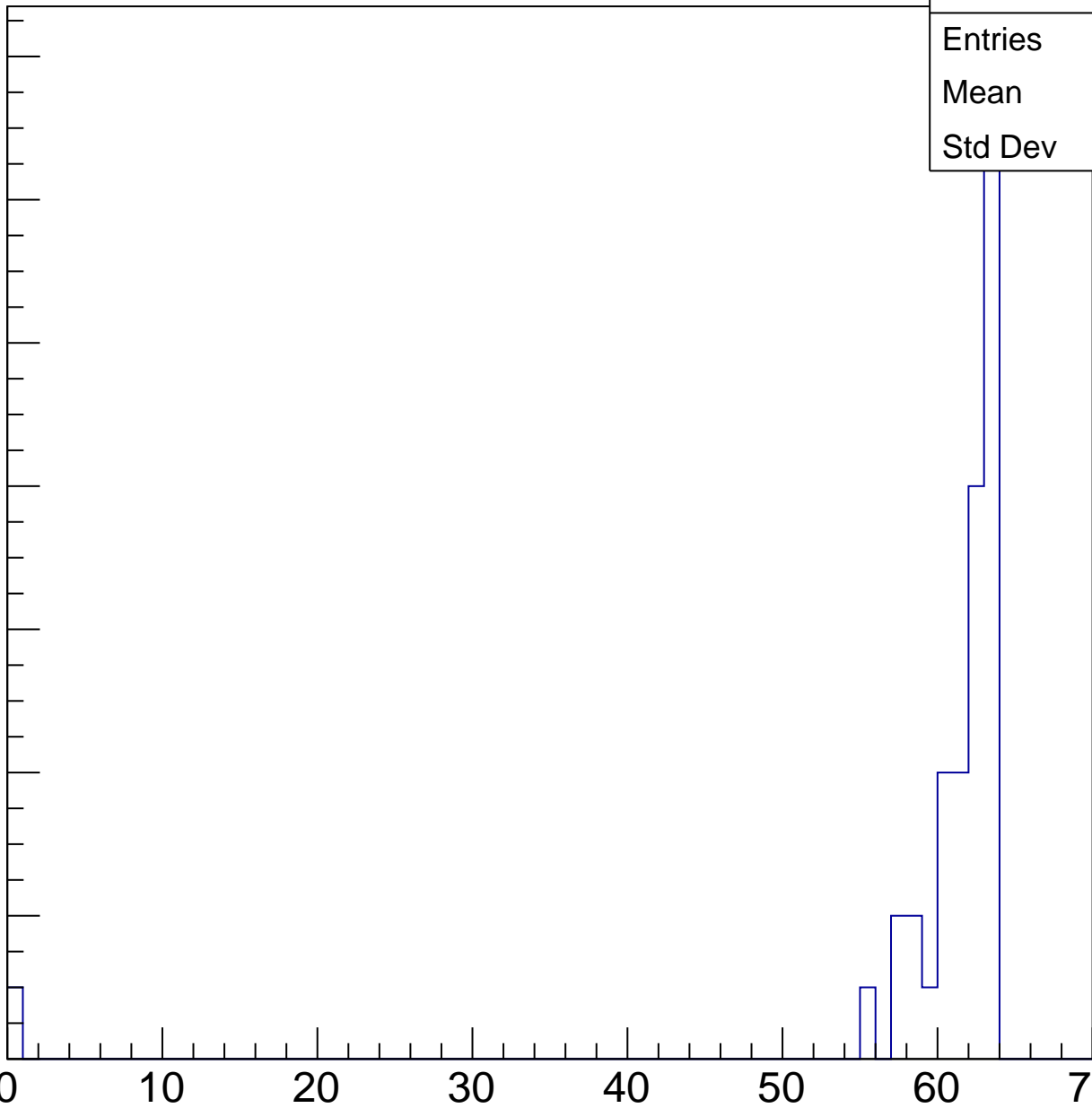
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	37
Mean	59.62
Std Dev	10.15

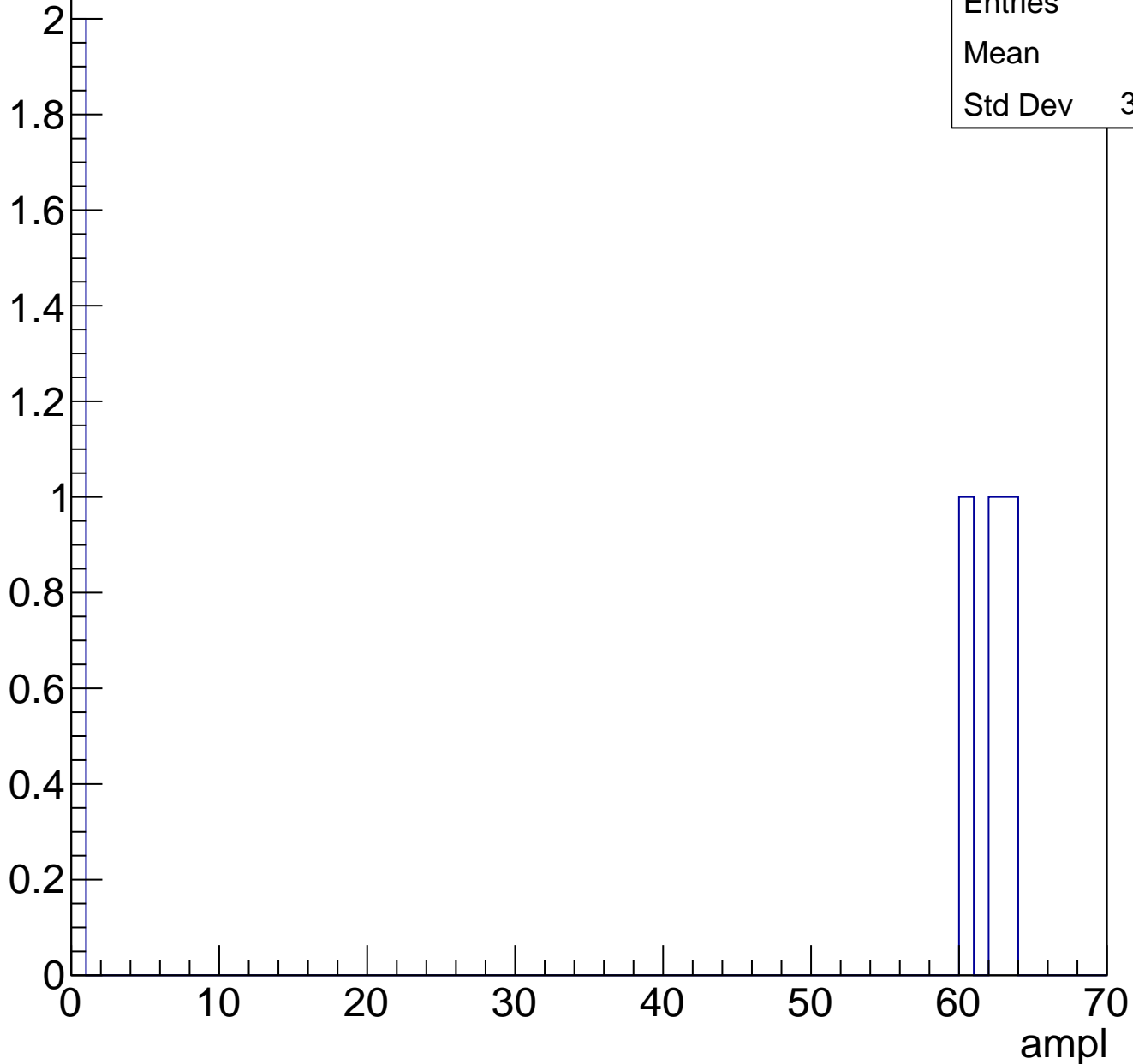
ampl



B0L101S, U18-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	37
Std Dev	30.23

B0L101S, U18-ch101, adc0

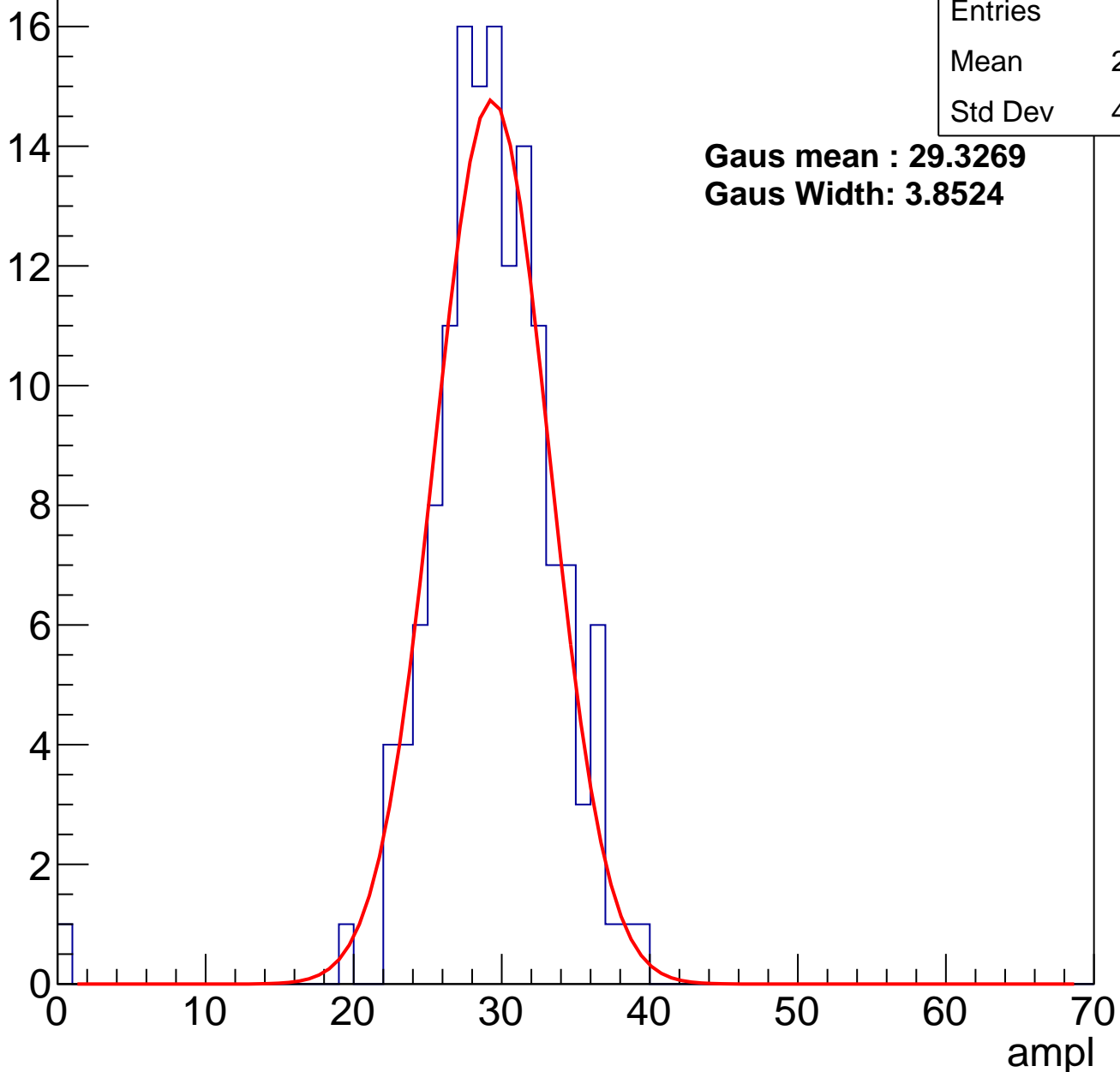
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.93
Std Dev	4.424

Gaus mean : 29.3269

Gaus Width: 3.8524

Entry



B0L101S, U18-ch101, adc1

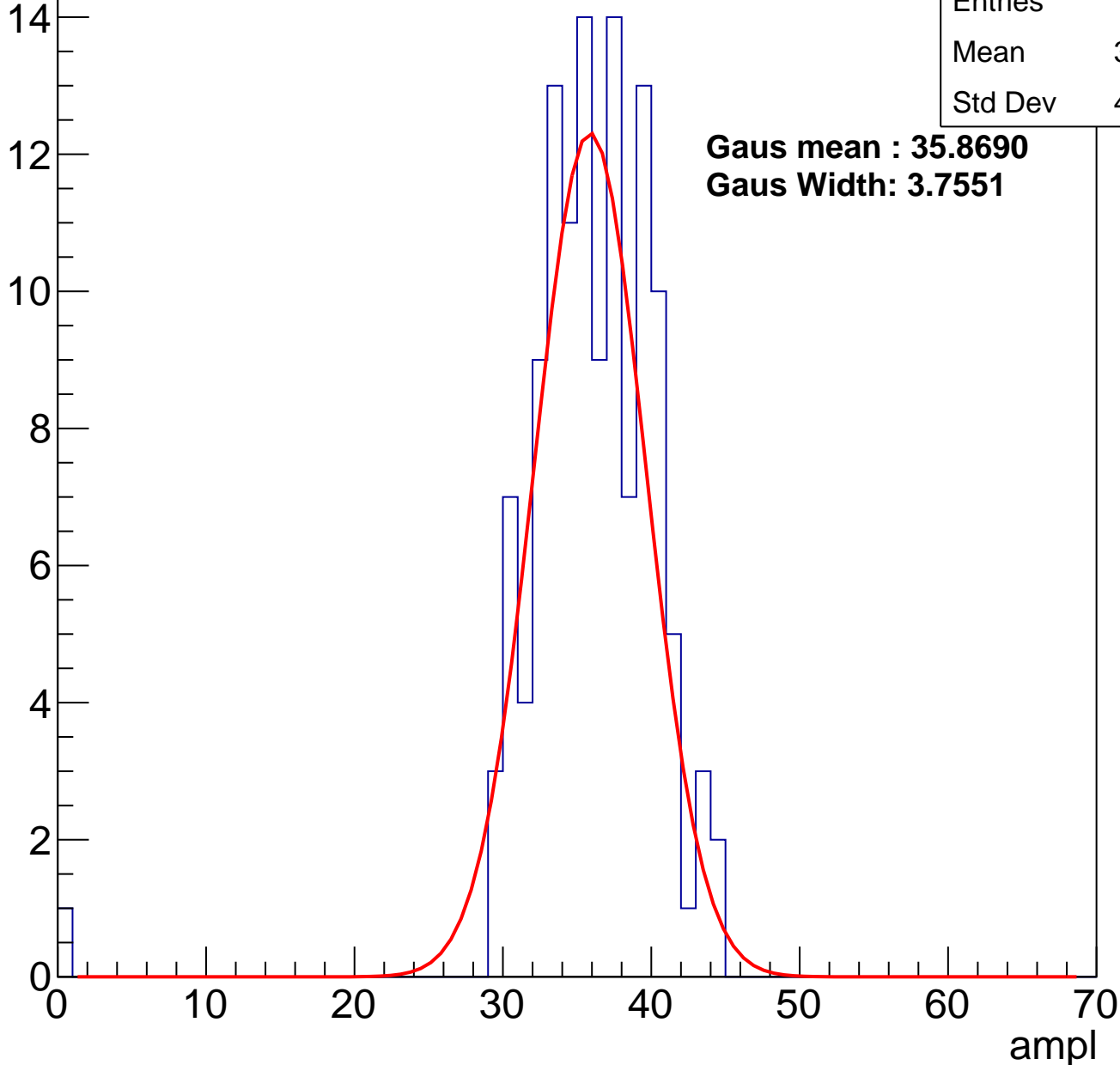
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	35.56
Std Dev	4.752

Gaus mean : 35.8690

Gaus Width: 3.7551

Entry



B0L101S, U18-ch101, adc2

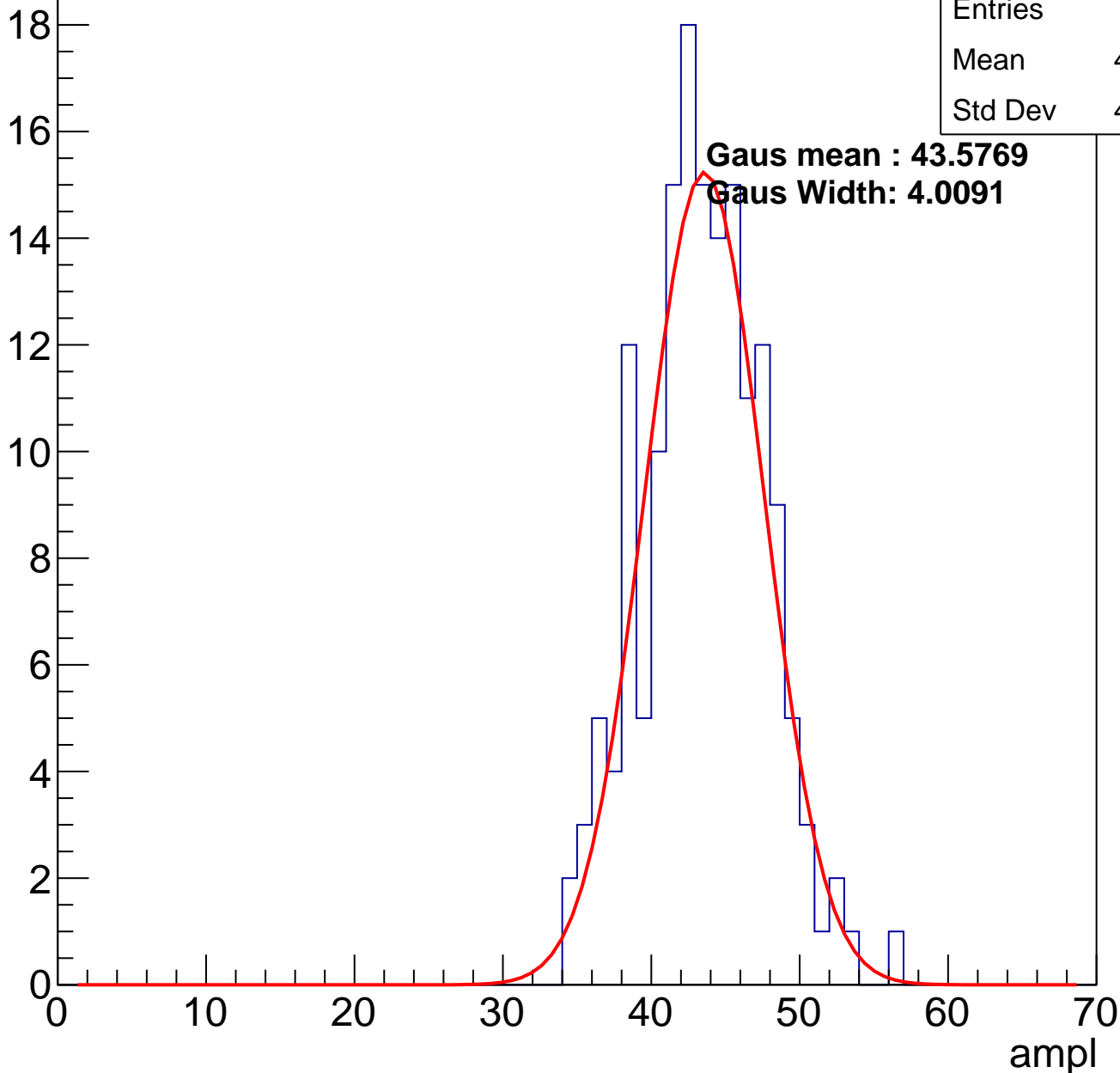
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	163
Mean	43.07
Std Dev	4.072

Gaus mean : 43.5769

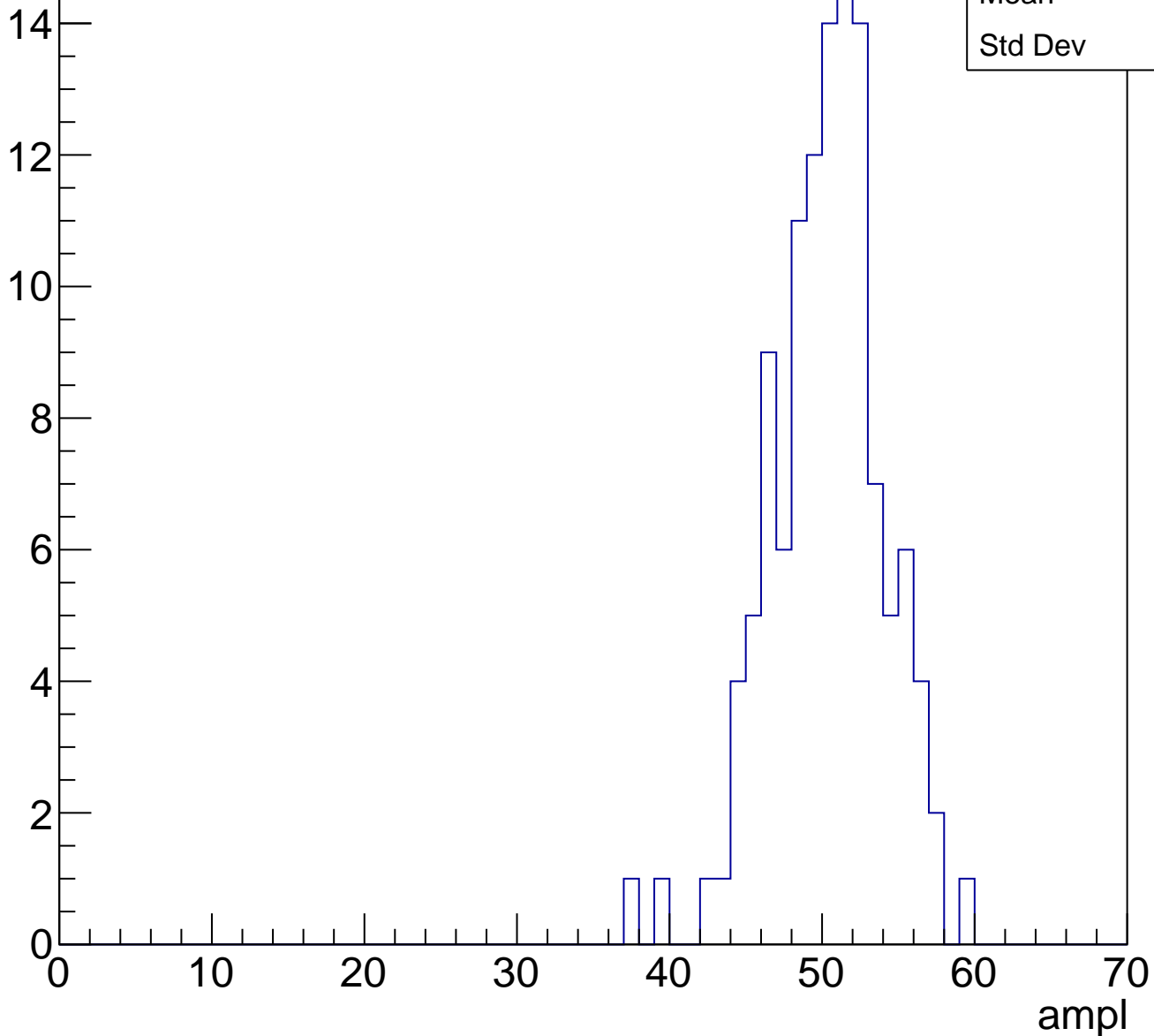
Gaus Width: 4.0091



B0L101S, U18-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

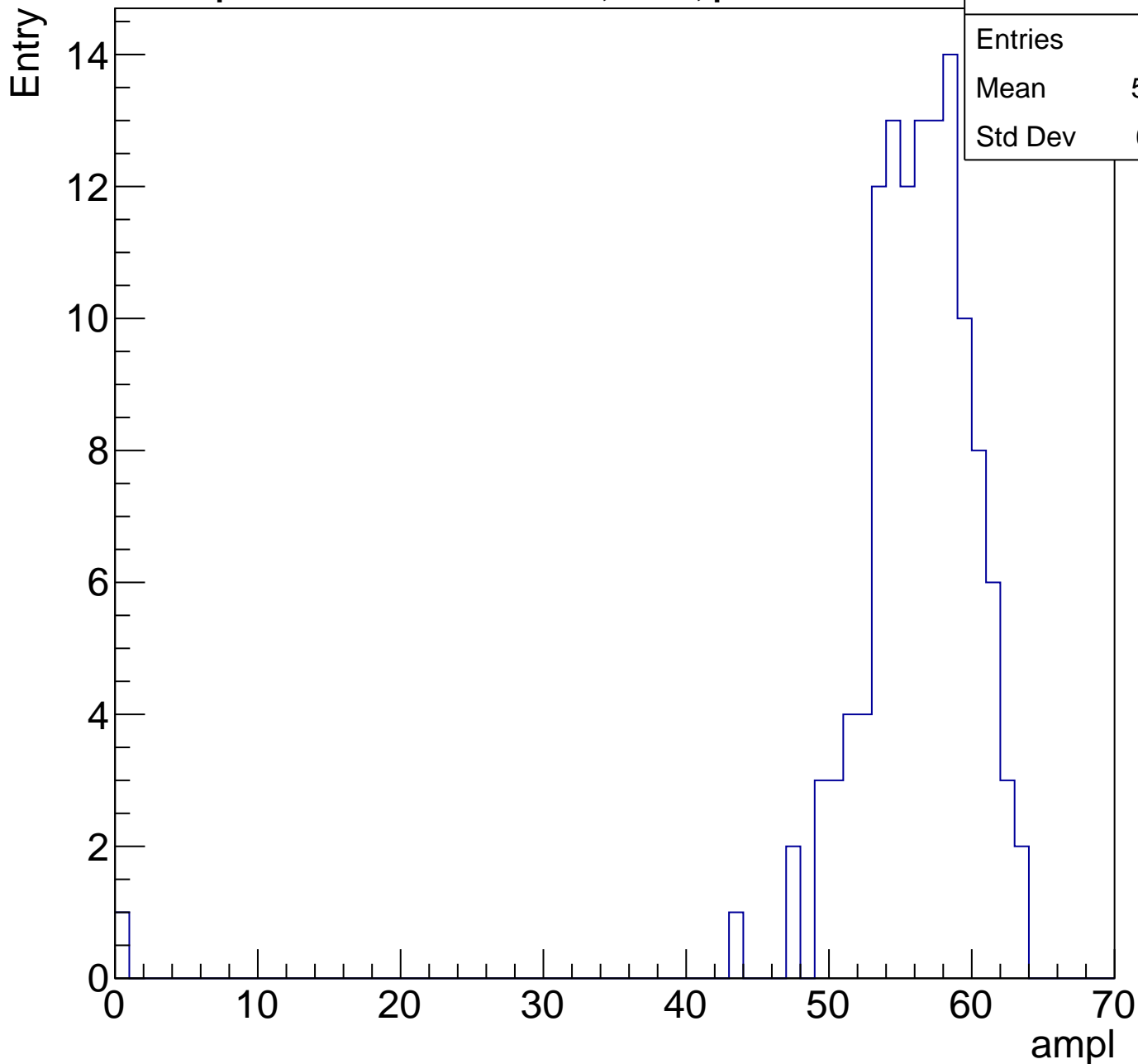
Entry



B0L101S, U18-ch101, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	55.43
Std Dev	6.131



B0L101S, U18-ch101, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

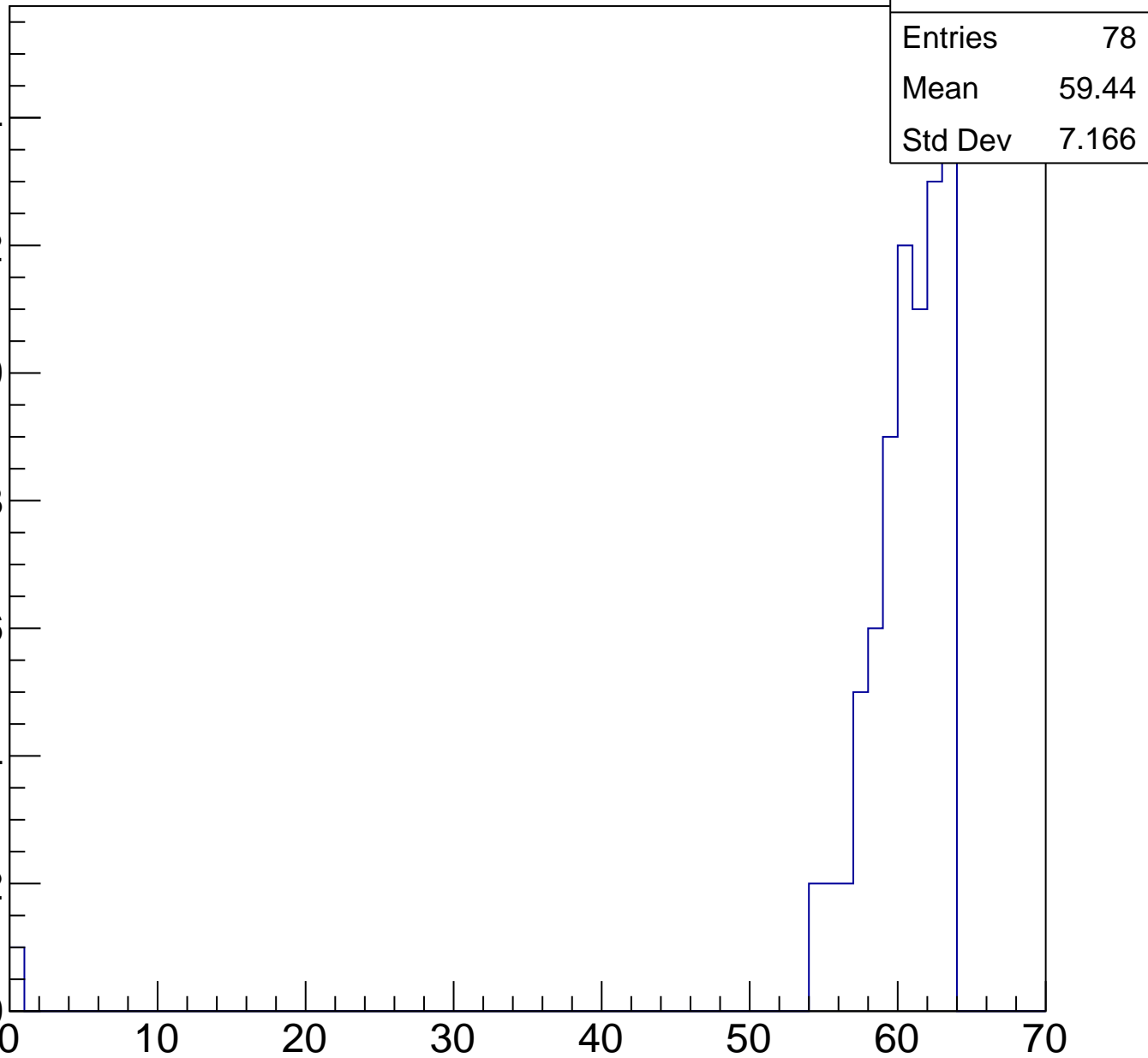
4

2

0

Entries	78
Mean	59.44
Std Dev	7.166

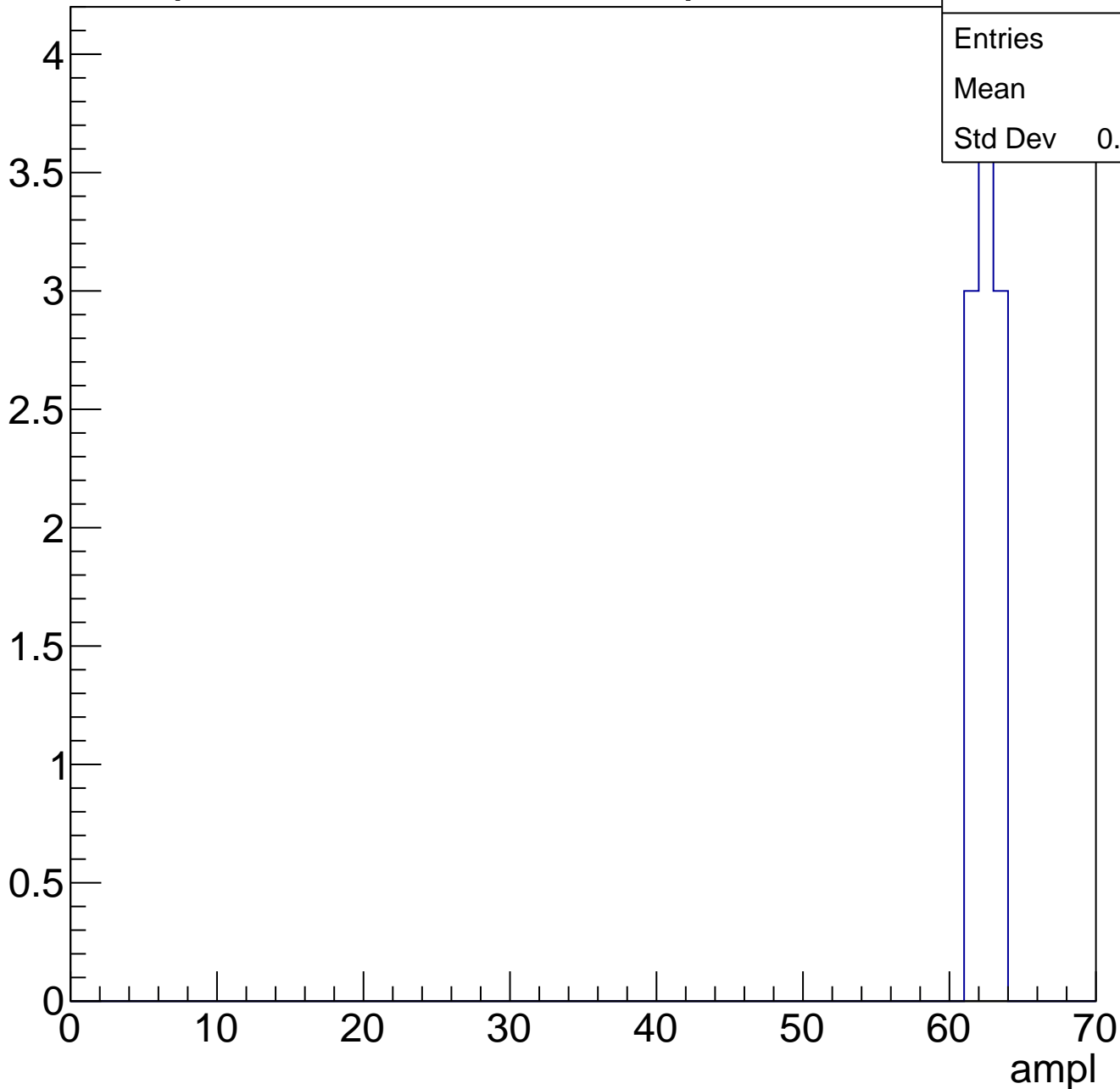
ampl



B0L101S, U18-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

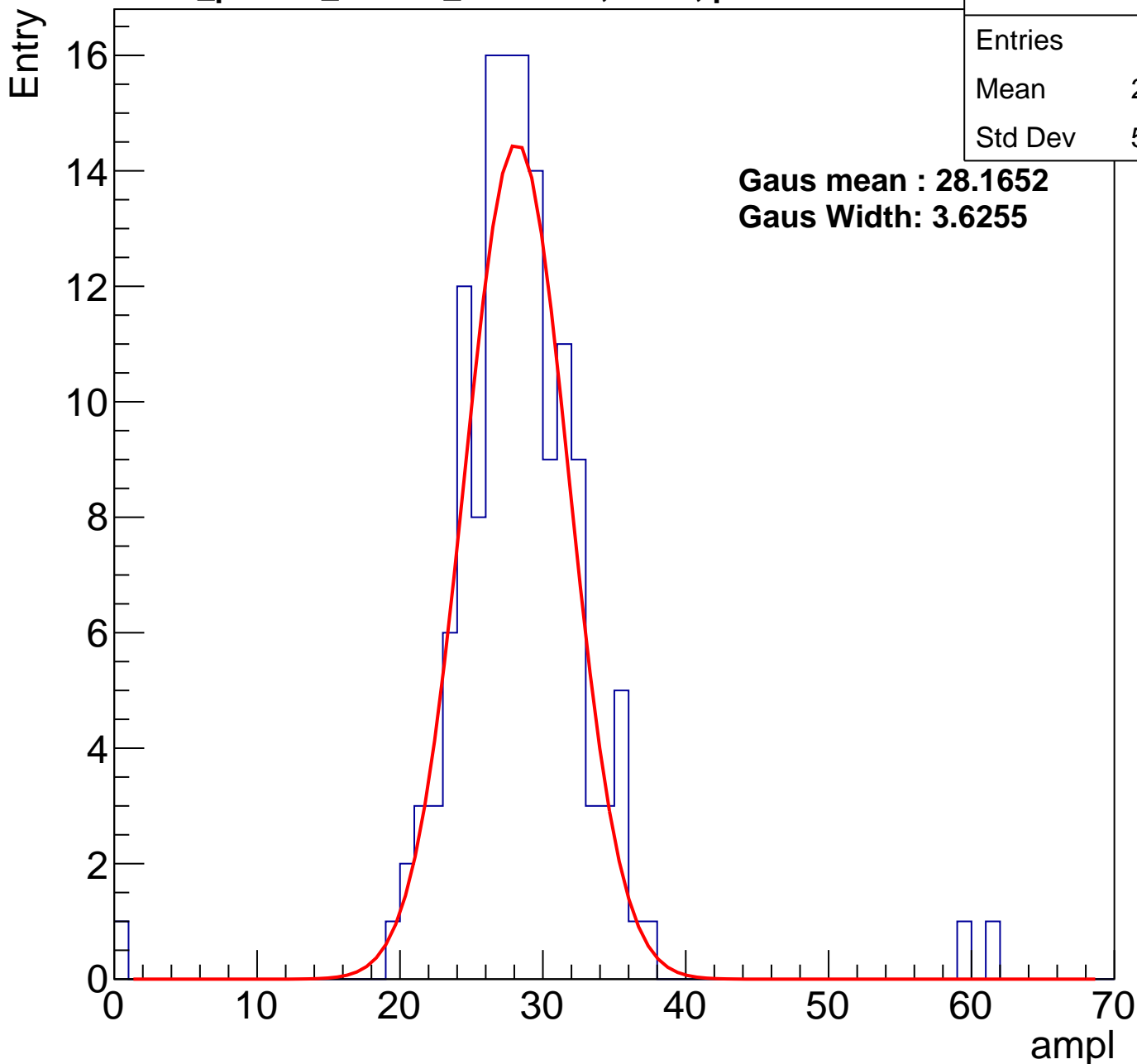
B0L101S, U18-ch102, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	28.06
Std Dev	5.727

Gaus mean : 28.1652

Gaus Width: 3.6255



B0L101S, U18-ch102, adc1

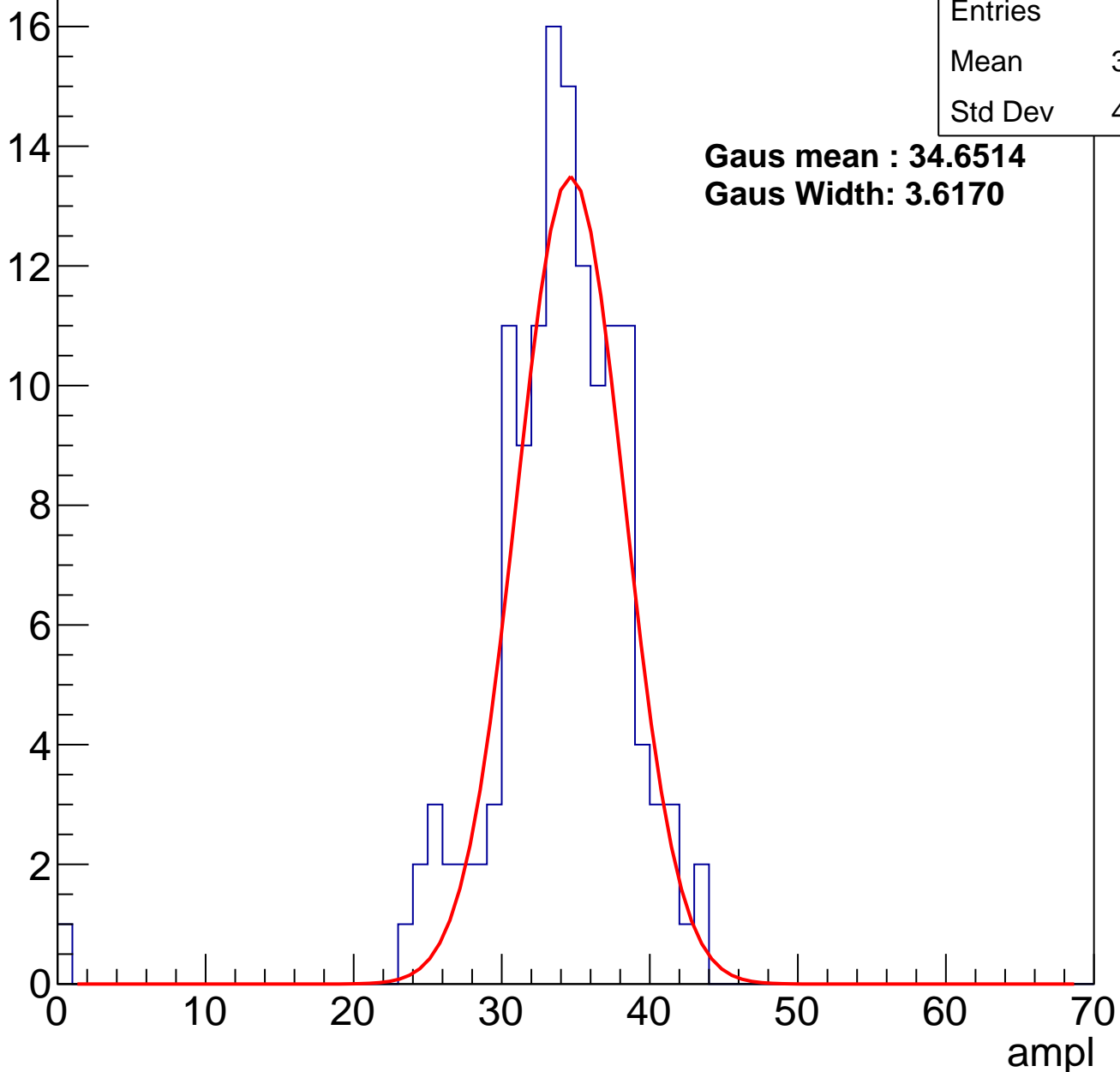
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	33.53
Std Dev	4.935

Gaus mean : 34.6514

Gaus Width: 3.6170

Entry



B0L101S, U18-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

154

Mean

41.47

Std Dev

3.766

Gaus mean : 41.9707

Gaus Width: 3.6278

0

10

20

30

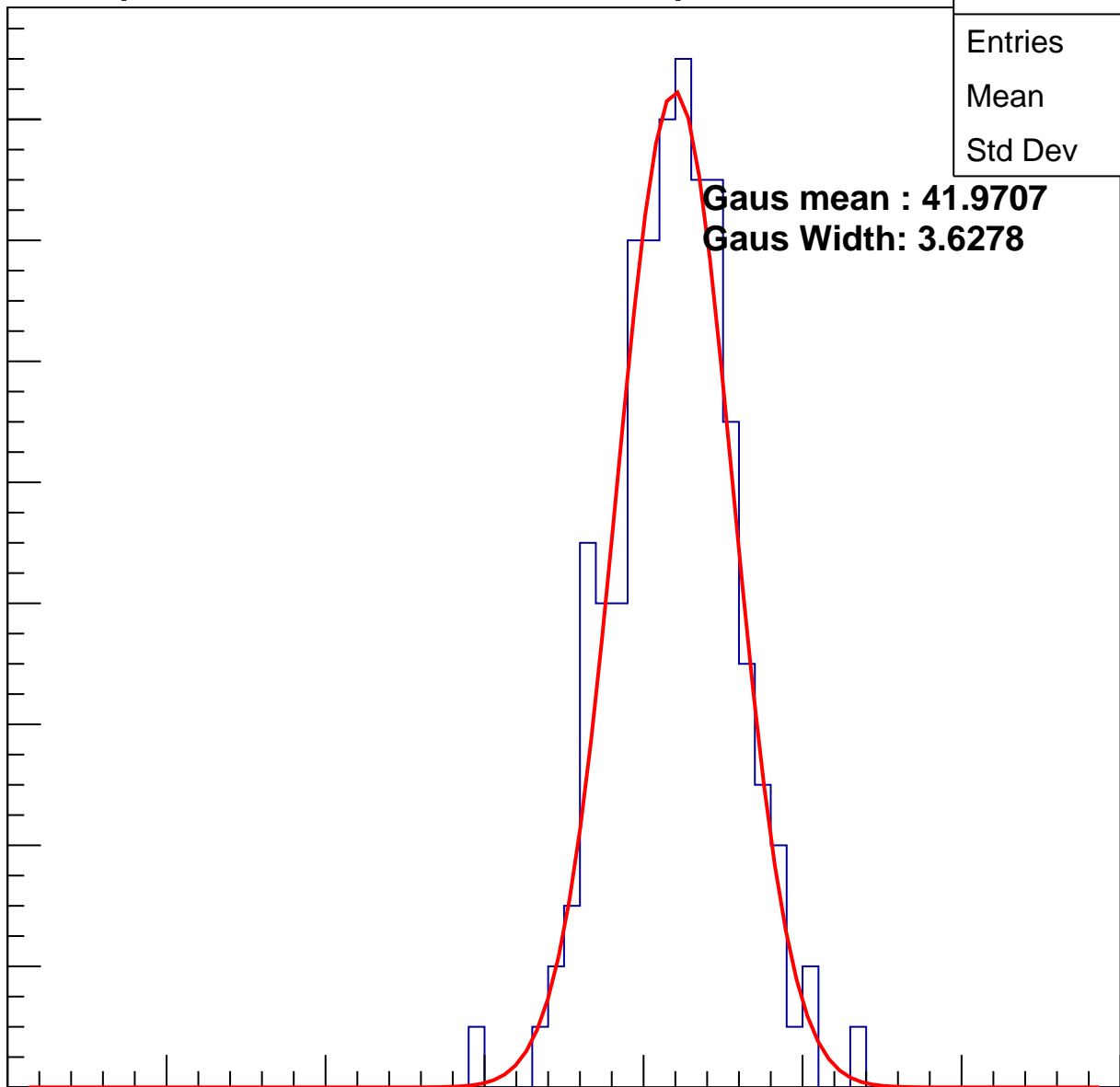
40

50

60

70

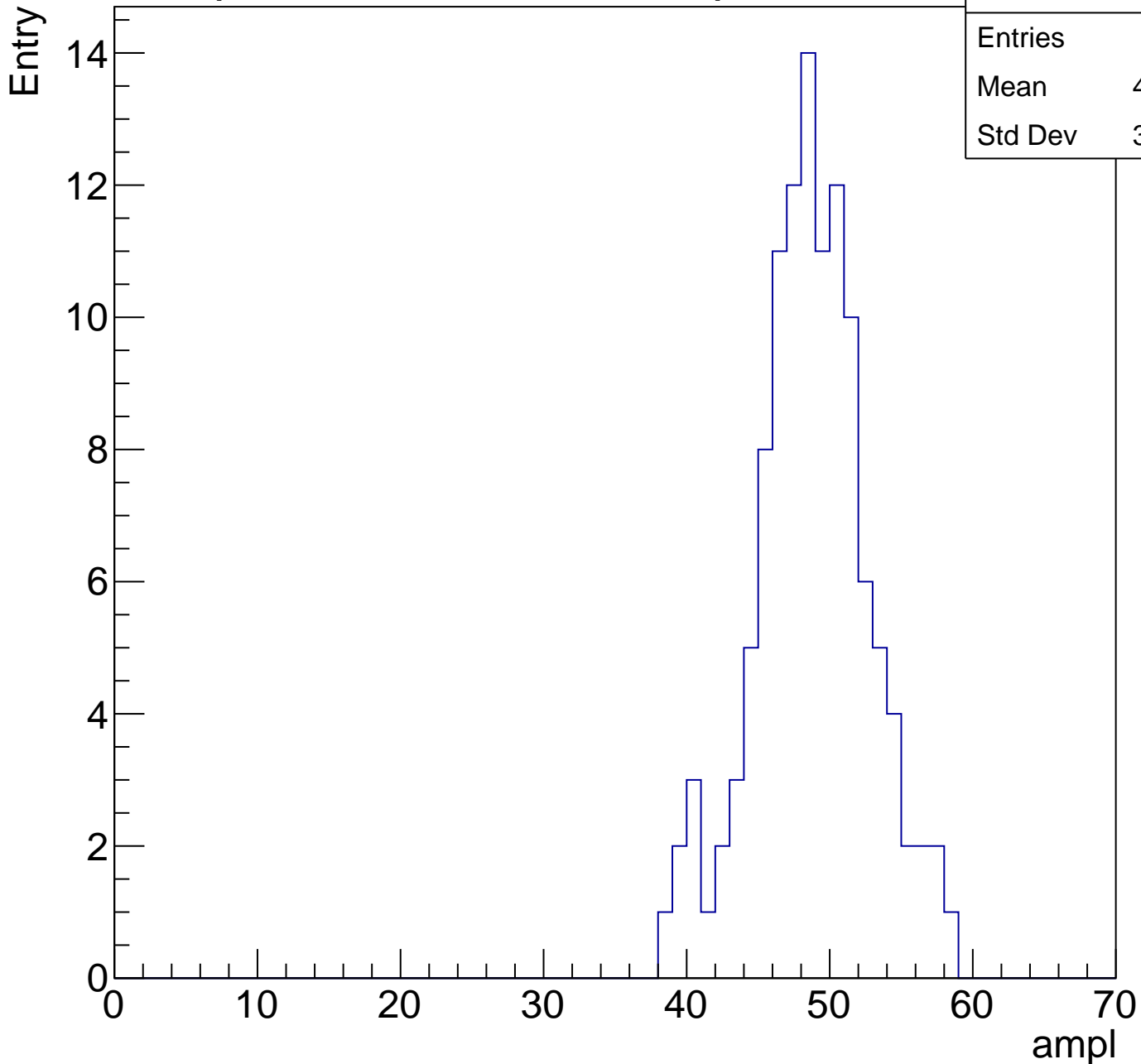
ampl



B0L101S, U18-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	48.27
Std Dev	3.978



B0L101S, U18-ch102, adc4

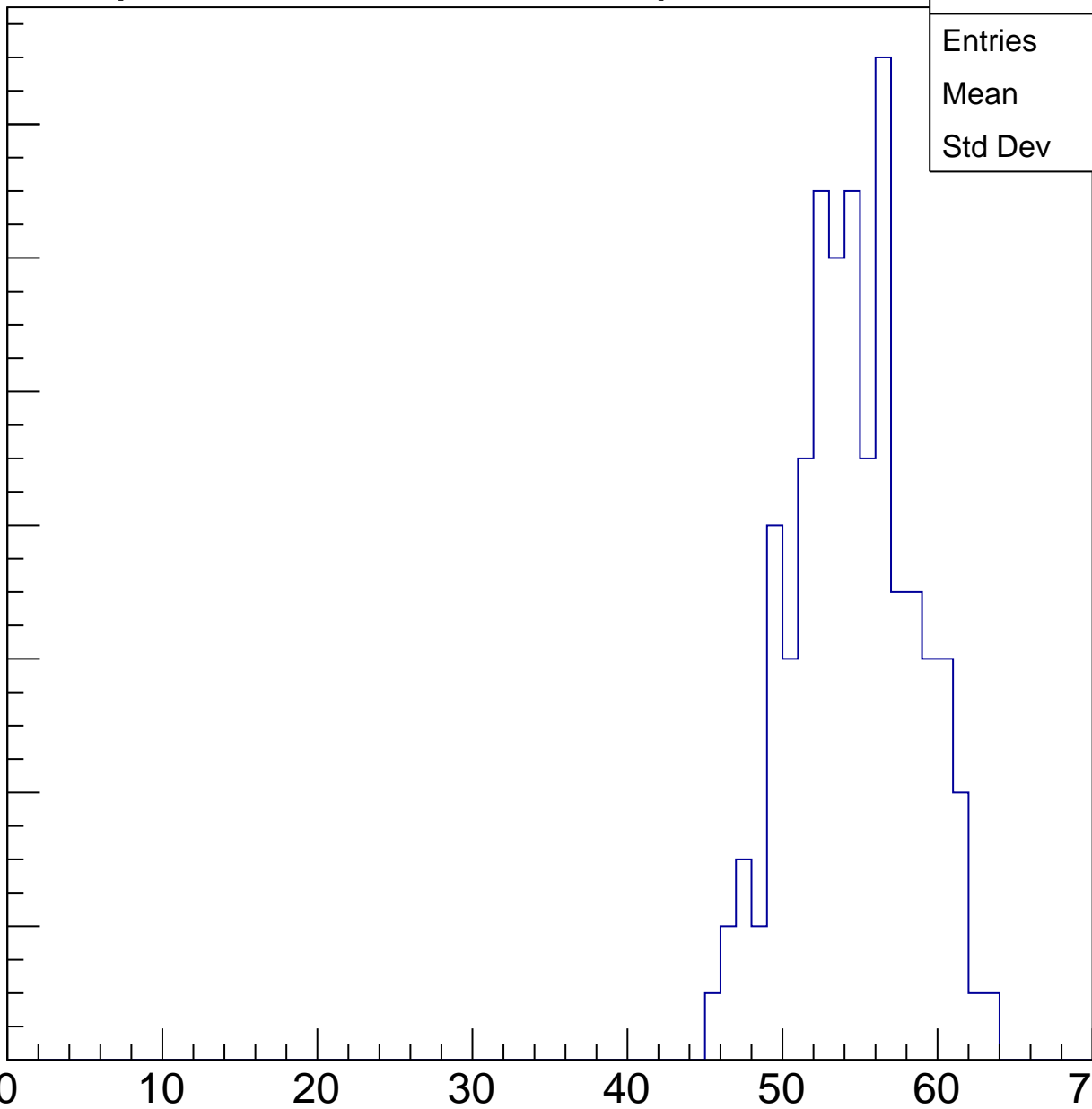
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	125
Mean	54.1
Std Dev	3.815

ampl



B0L101S, U18-ch102, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	91
Mean	58.82
Std Dev	2.858

B0L101S, U18-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

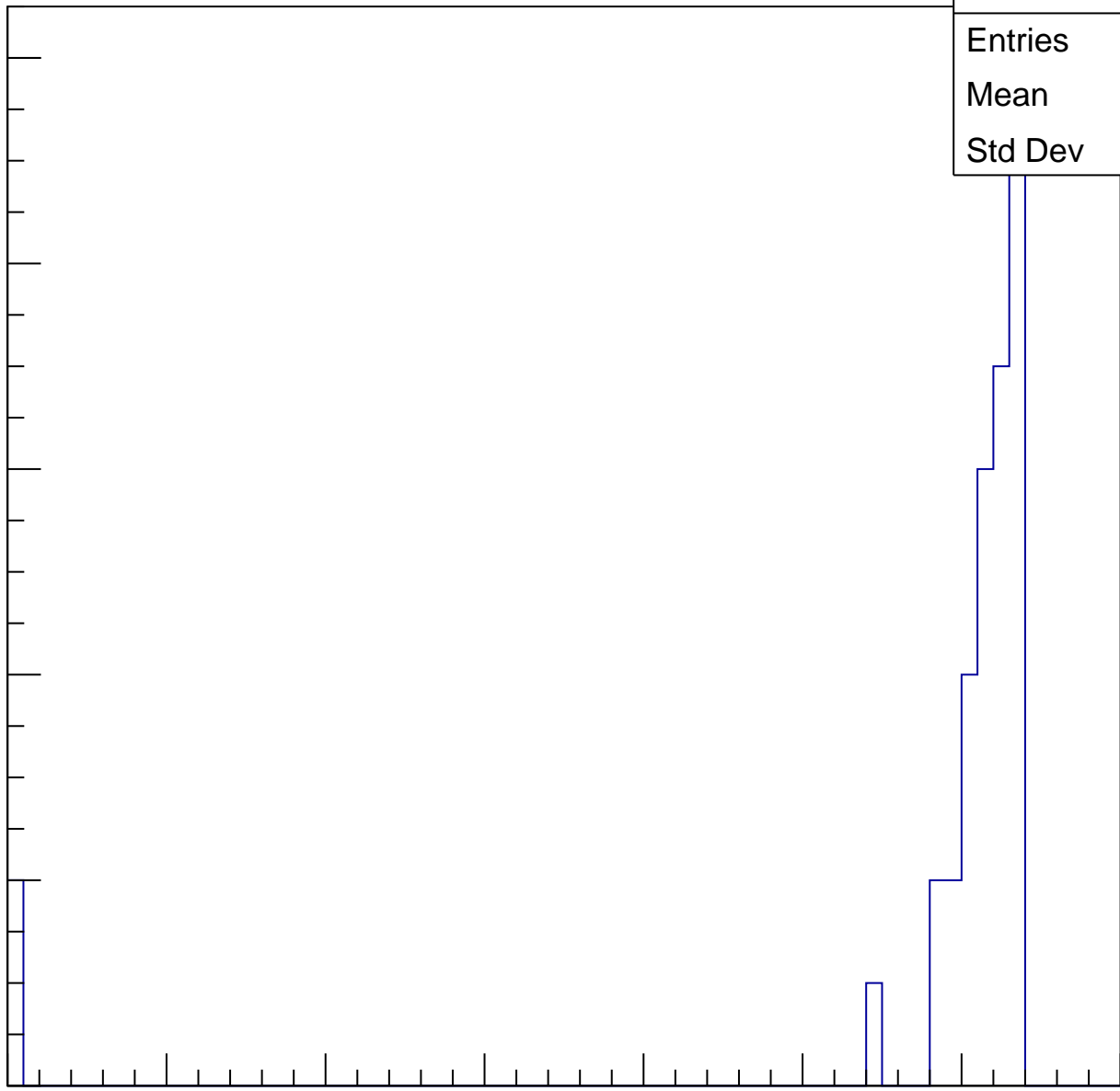
Entries	34
Mean	57.59
Std Dev	14.52

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch103, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	27.84
Std Dev	4.436

Gaus mean : 28.5188

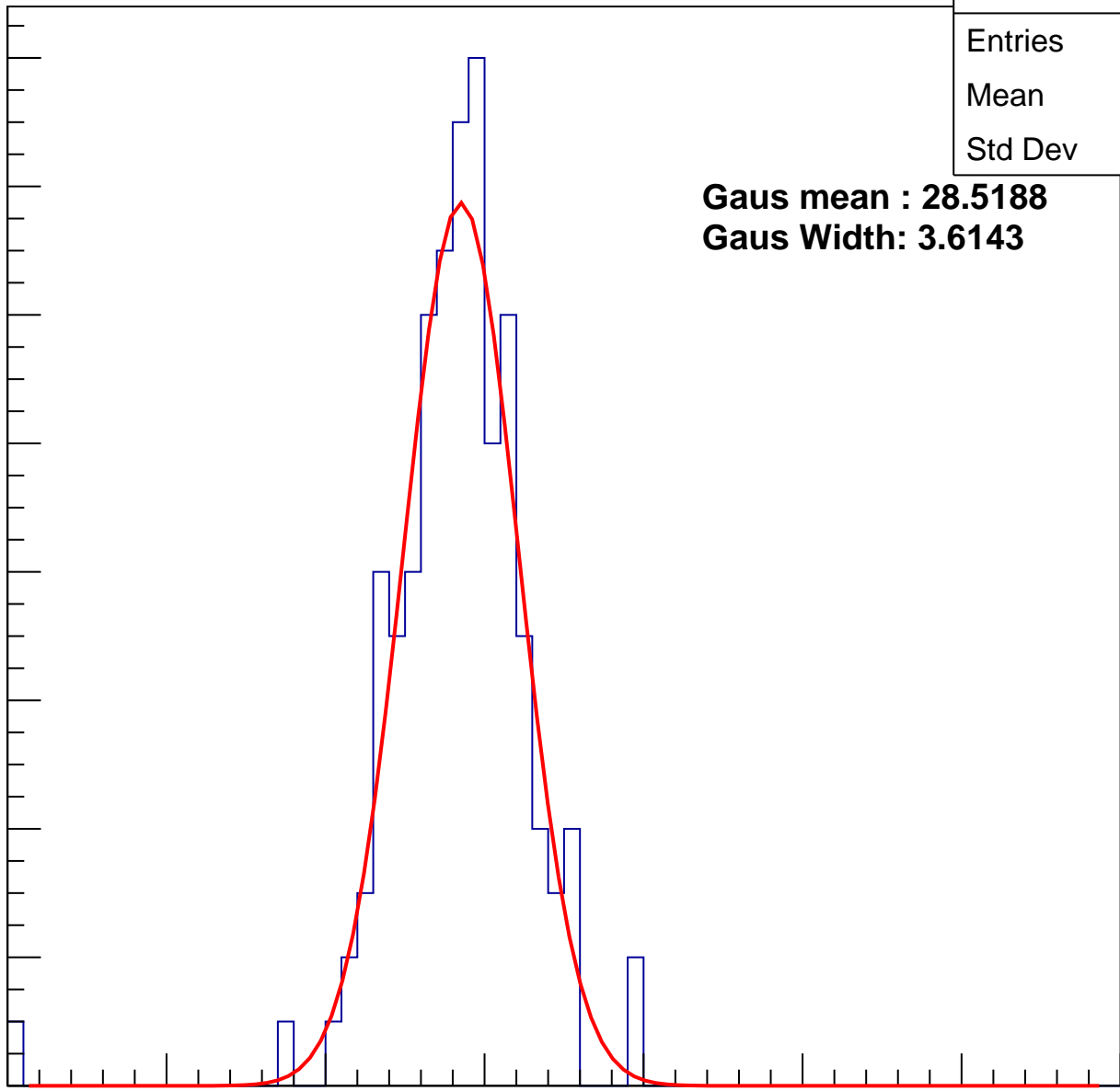
Gaus Width: 3.6143

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



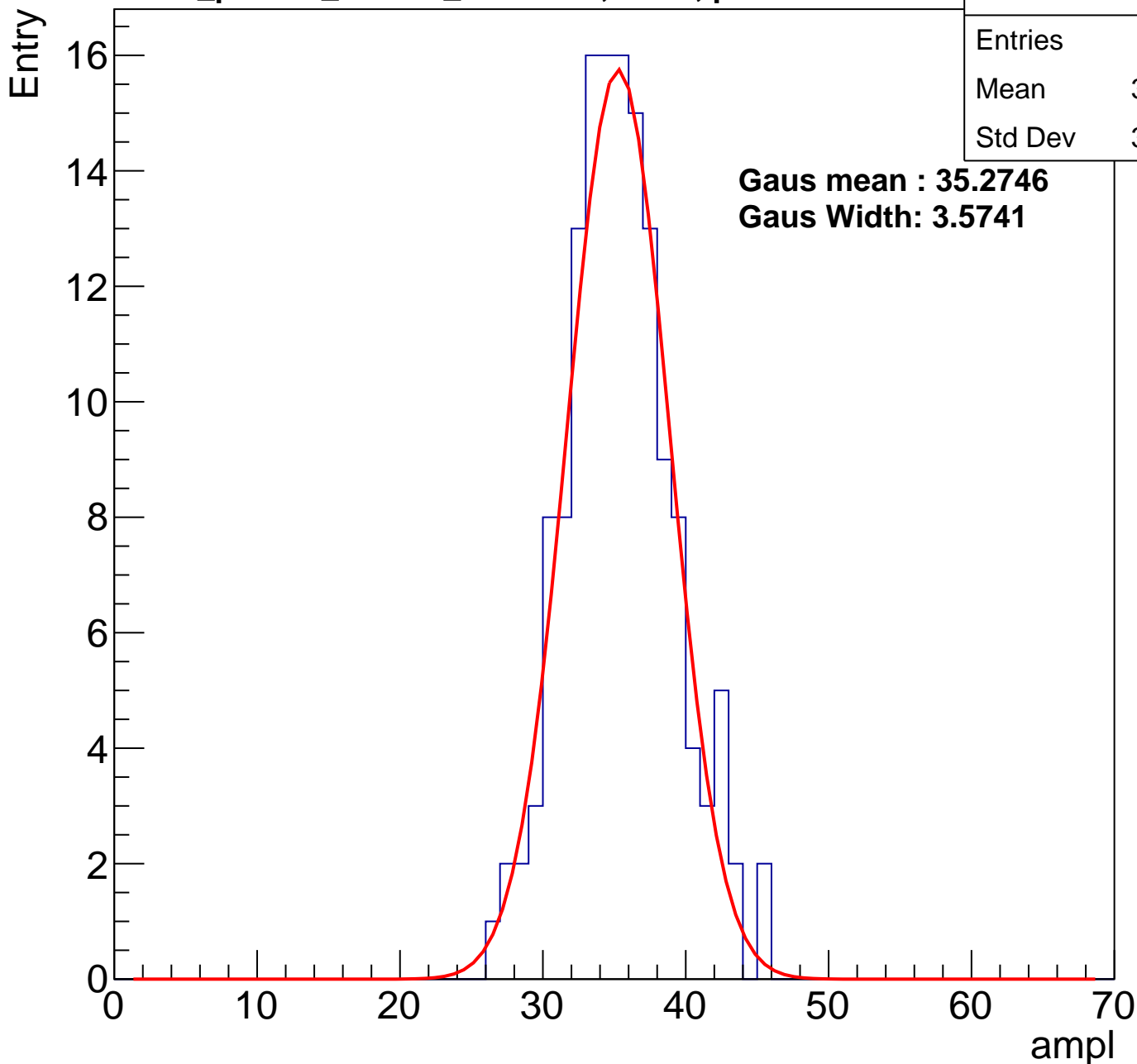
B0L101S, U18-ch103, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	34.95
Std Dev	3.698

Gaus mean : 35.2746

Gaus Width: 3.5741



B0L101S, U18-ch103, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

145

Mean

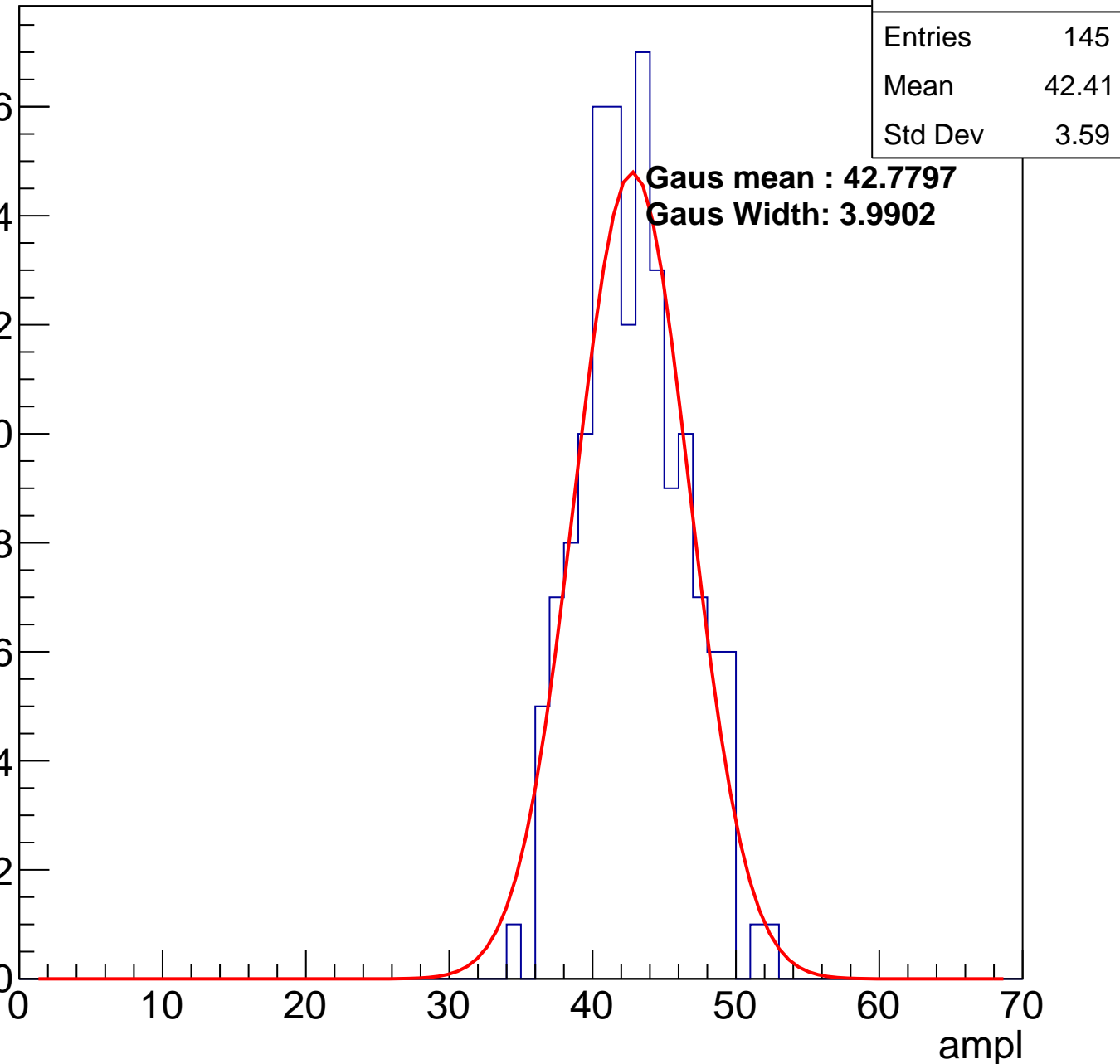
42.41

Std Dev

3.59

Gaus mean : 42.7797

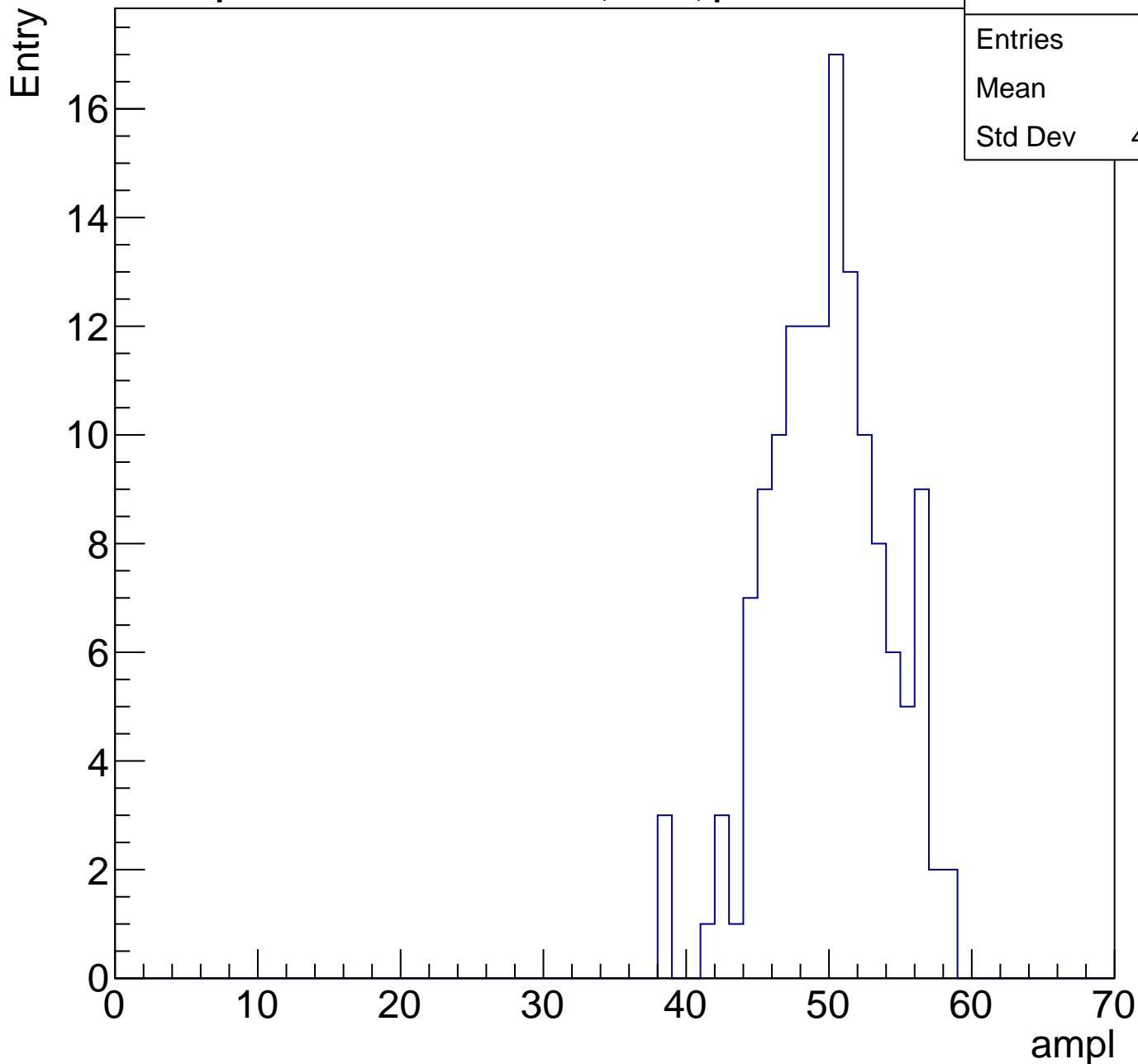
Gaus Width: 3.9902



B0L101S, U18-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

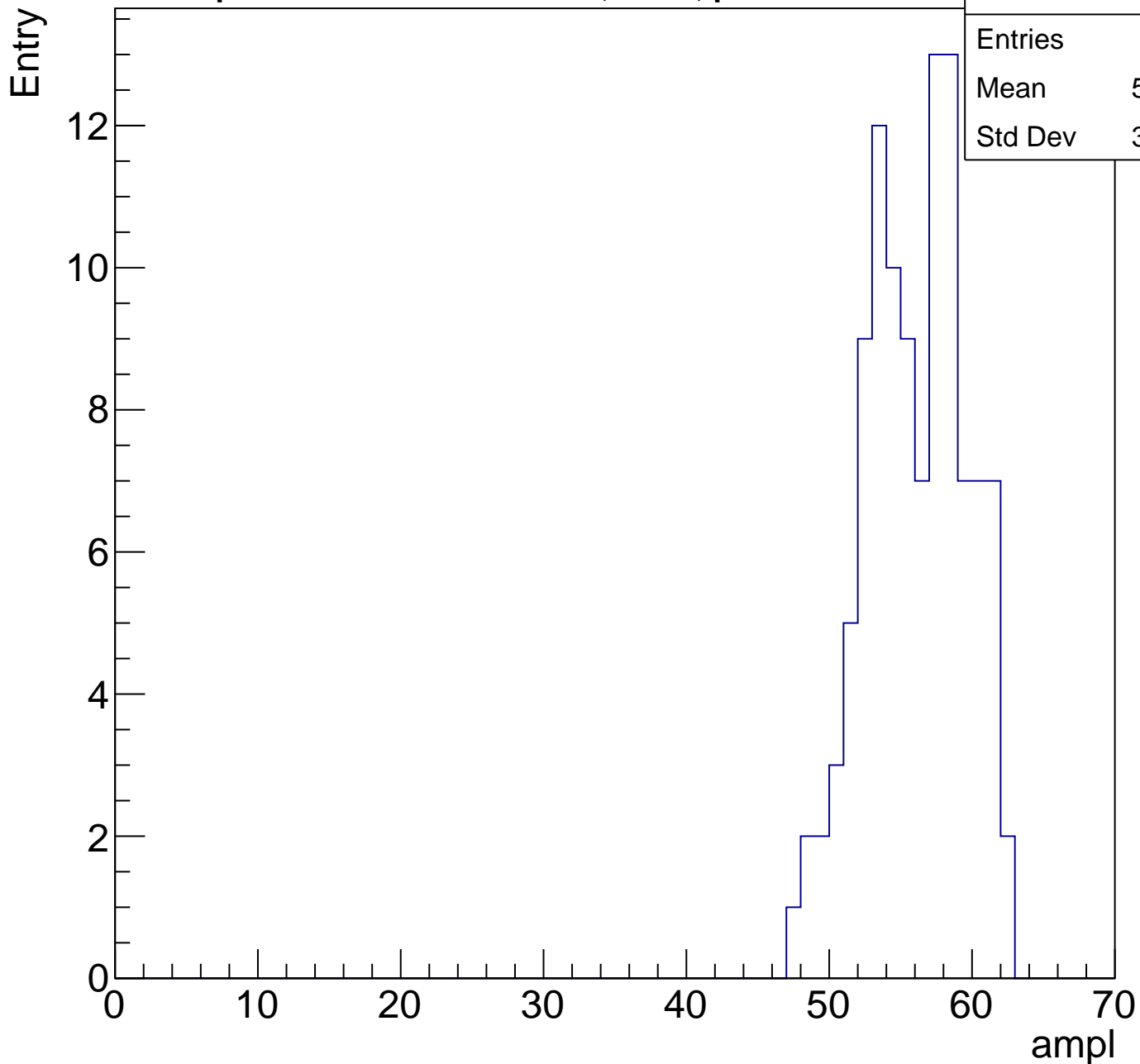
Entries	142
Mean	49.4
Std Dev	4.115



B0L101S, U18-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.56
Std Dev	3.463



B0L101S, U18-ch103, adc5

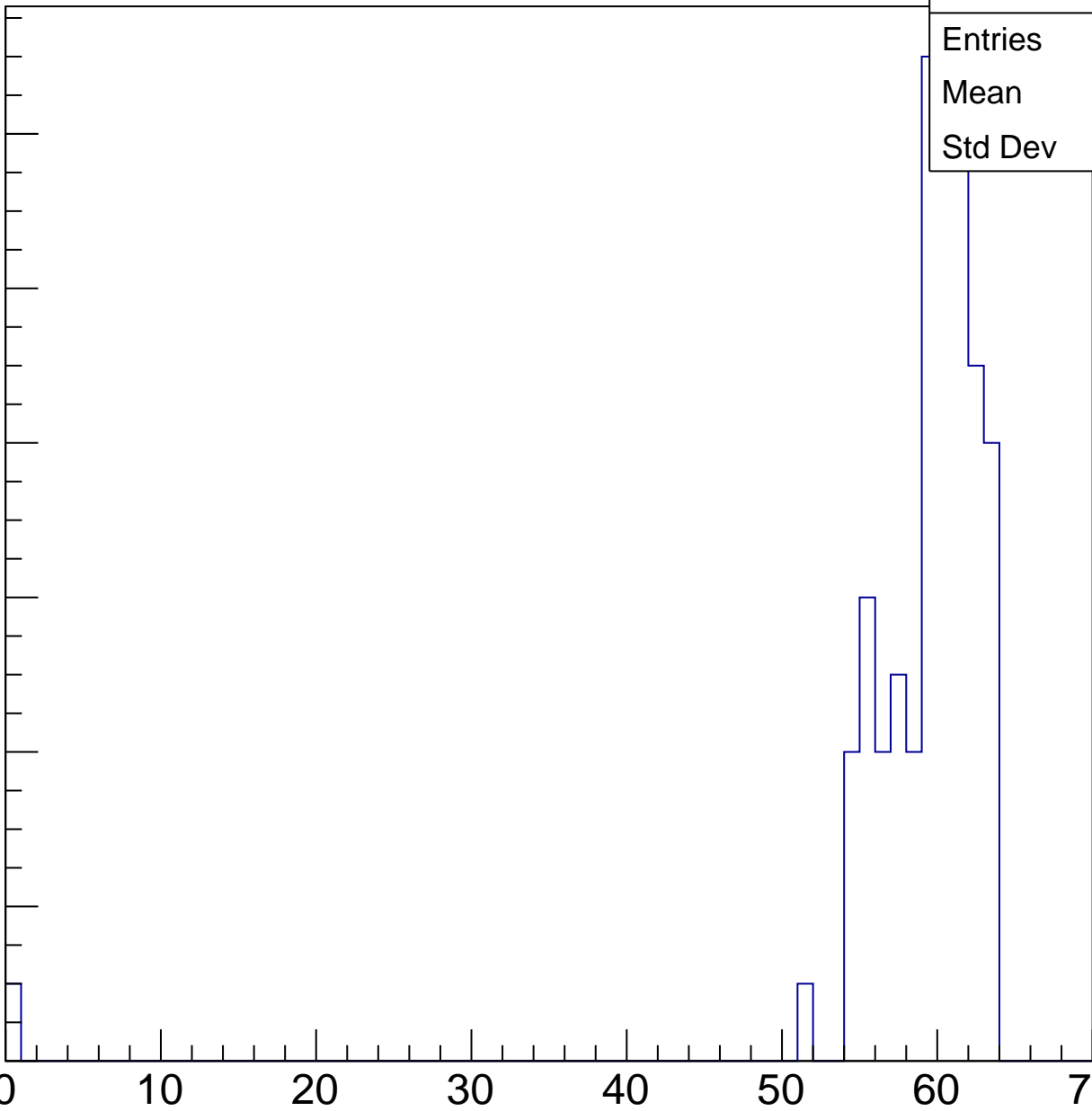
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	79
Mean	58.47
Std Dev	7.158

ampl



B0L101S, U18-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	24
Mean	59.58
Std Dev	12.48

Entry

10

8

6

4

2

0

0

10

20

30

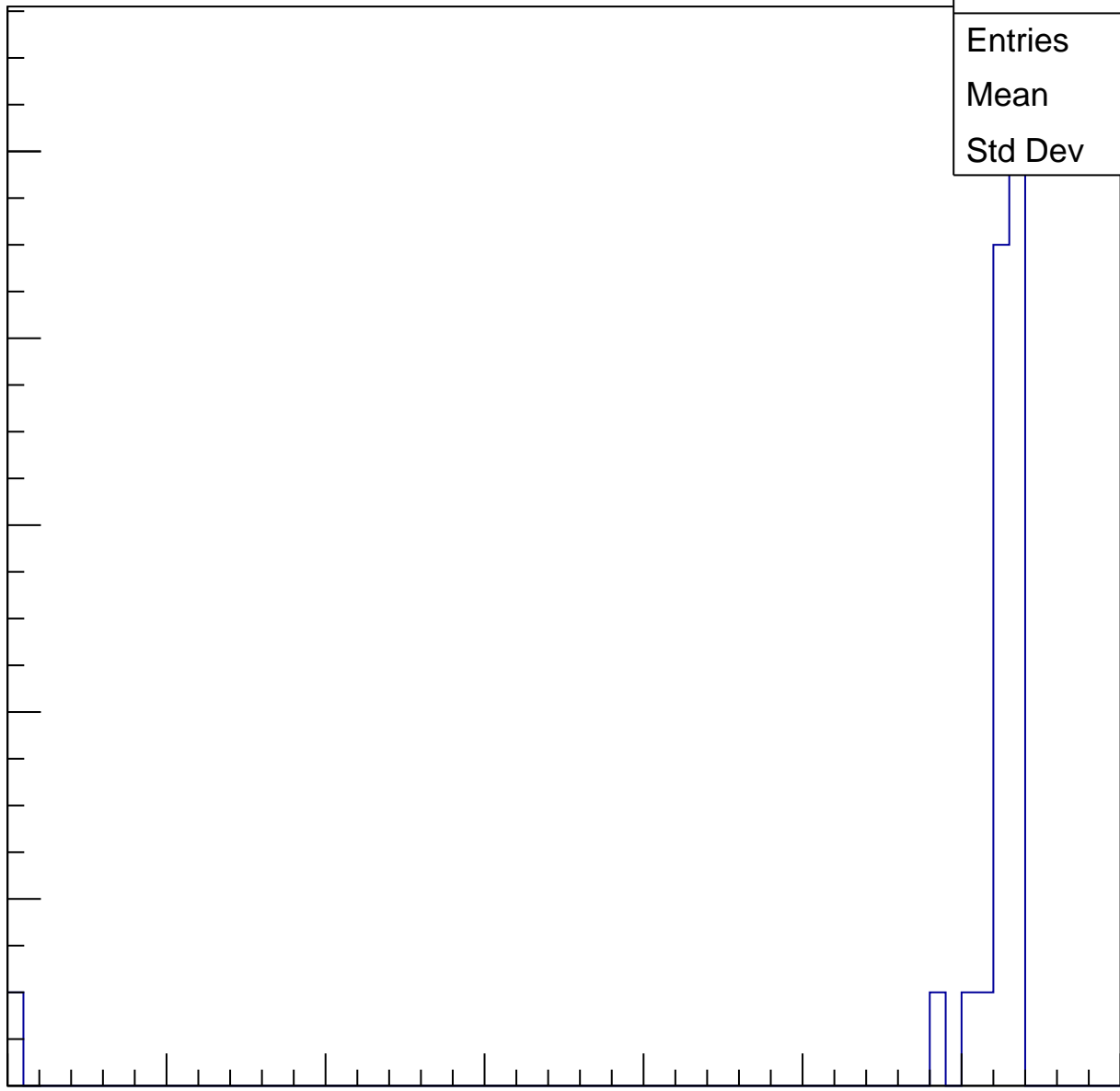
40

50

60

70

ampl



B0L101S, U18-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch104, adc0

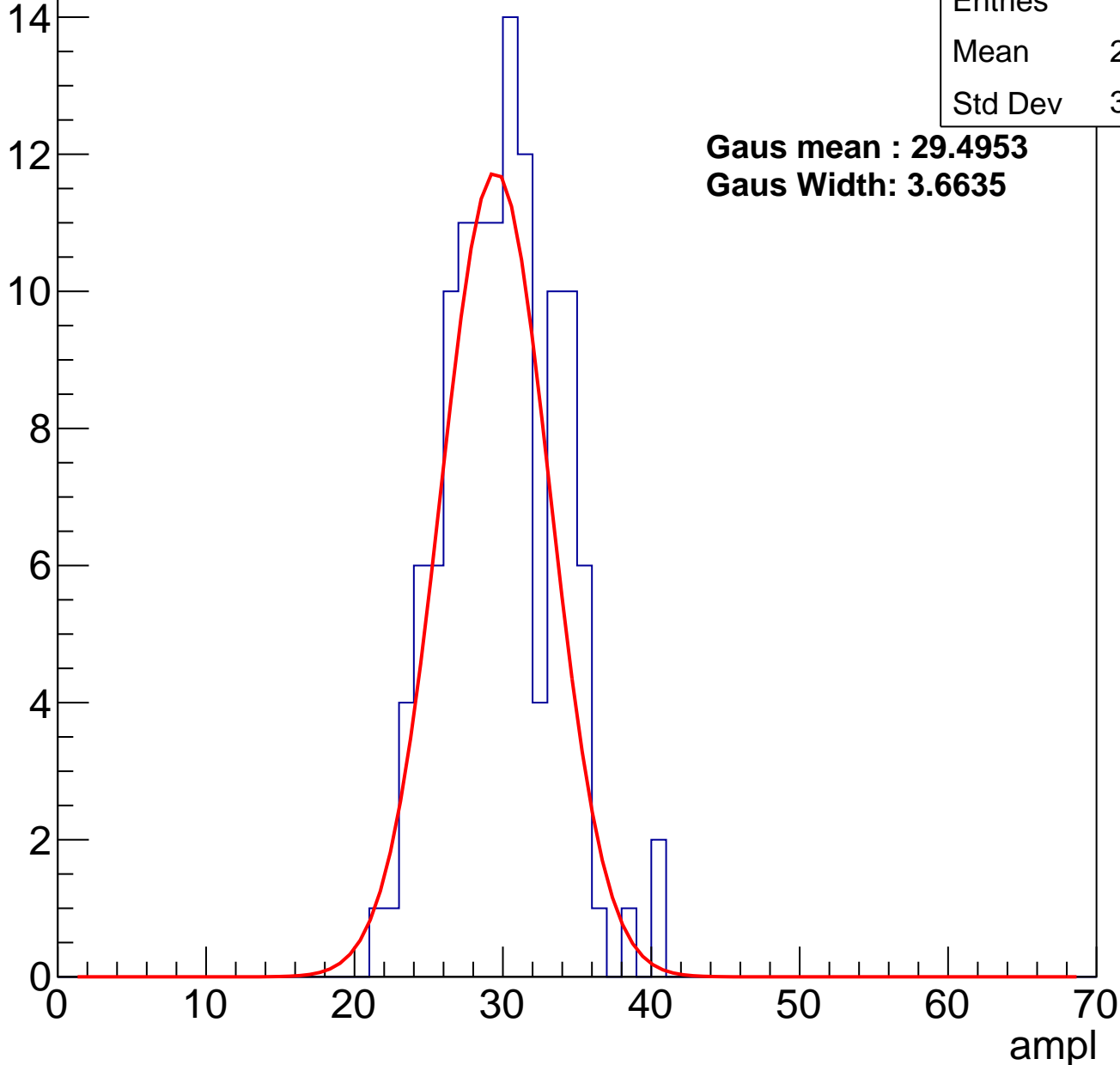
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	29.48
Std Dev	3.766

Gaus mean : 29.4953

Gaus Width: 3.6635

Entry



B0L101S, U18-ch104, adc1

calib_packv5_042523_0143.root, FC#1, port C1

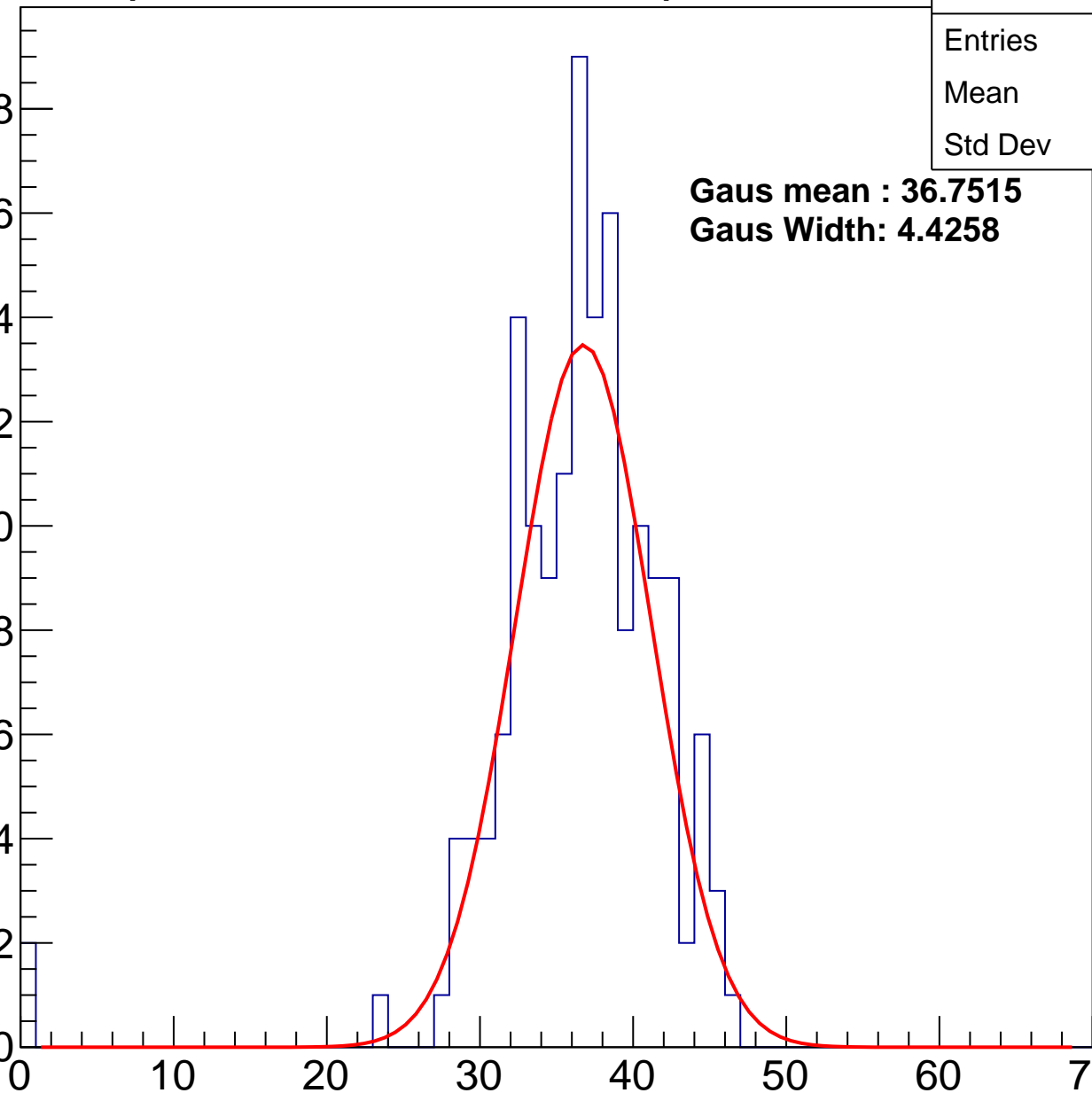
Entry

18
16
14
12
10
8
6
4
2
0

Entries	163
Mean	35.91
Std Dev	5.878

Gaus mean : 36.7515
Gaus Width: 4.4258

ampl



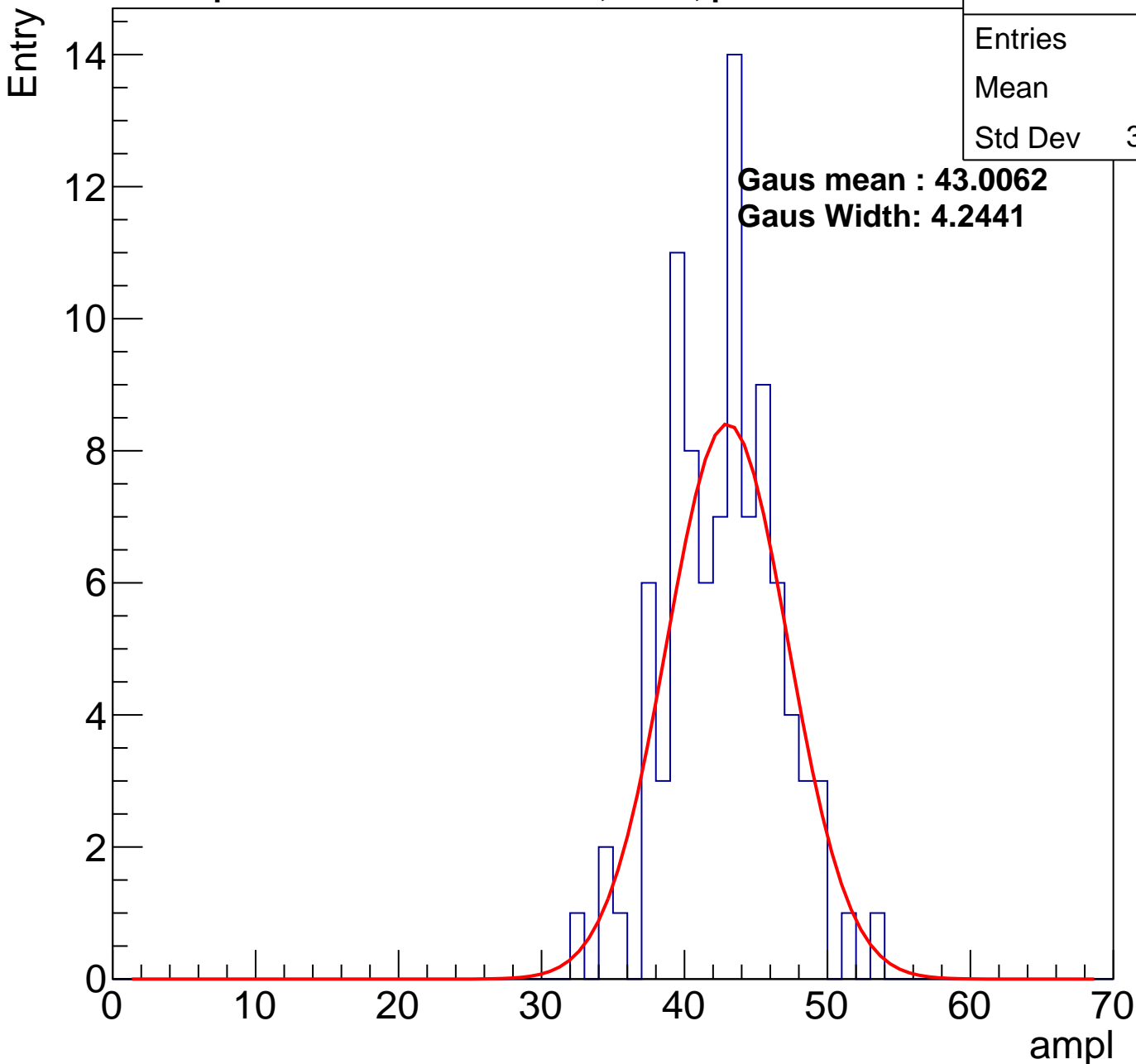
B0L101S, U18-ch104, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	42.3
Std Dev	3.852

Gaus mean : 43.0062

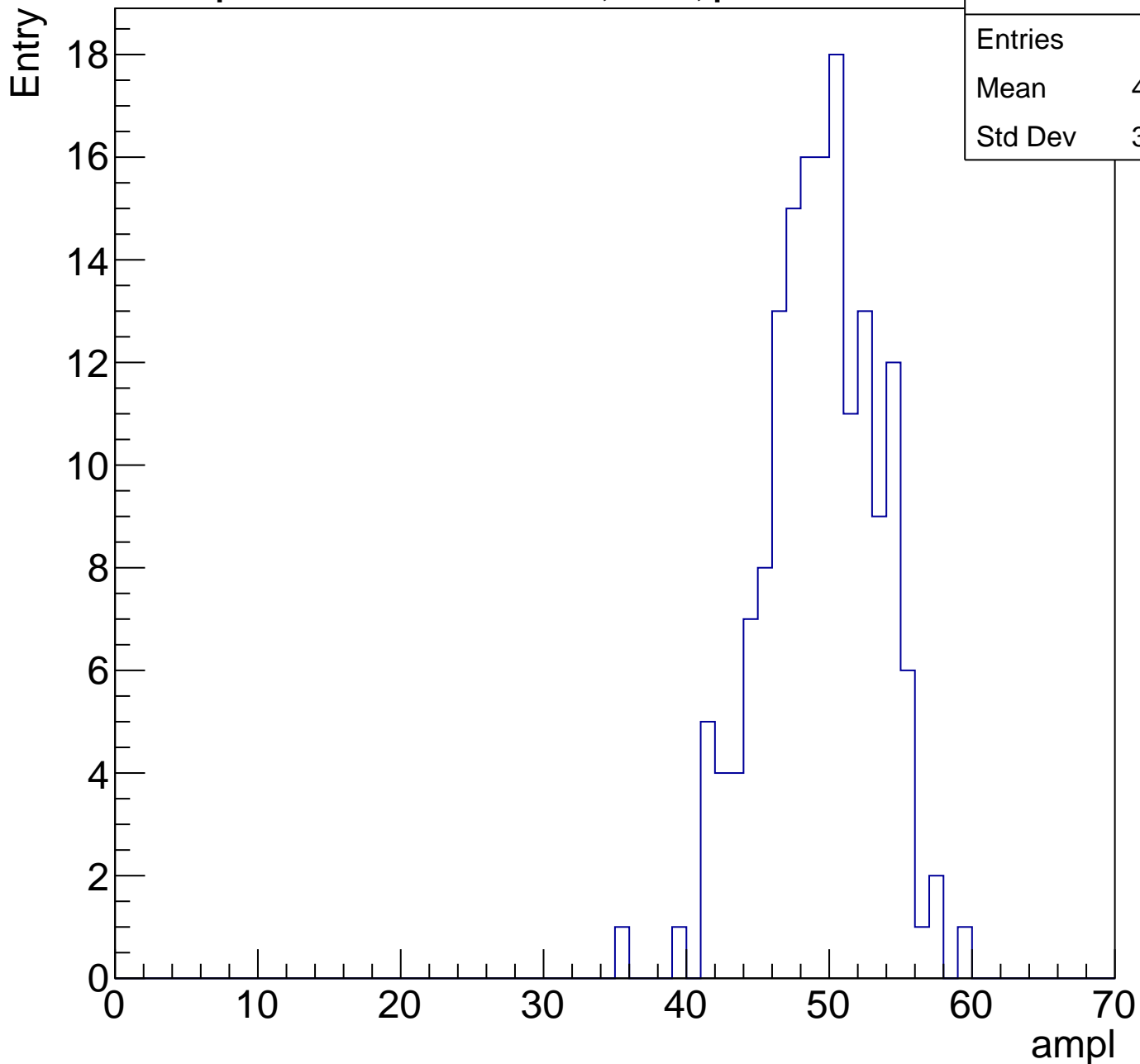
Gaus Width: 4.2441



B0L101S, U18-ch104, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	48.85
Std Dev	3.966



B0L101S, U18-ch104, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

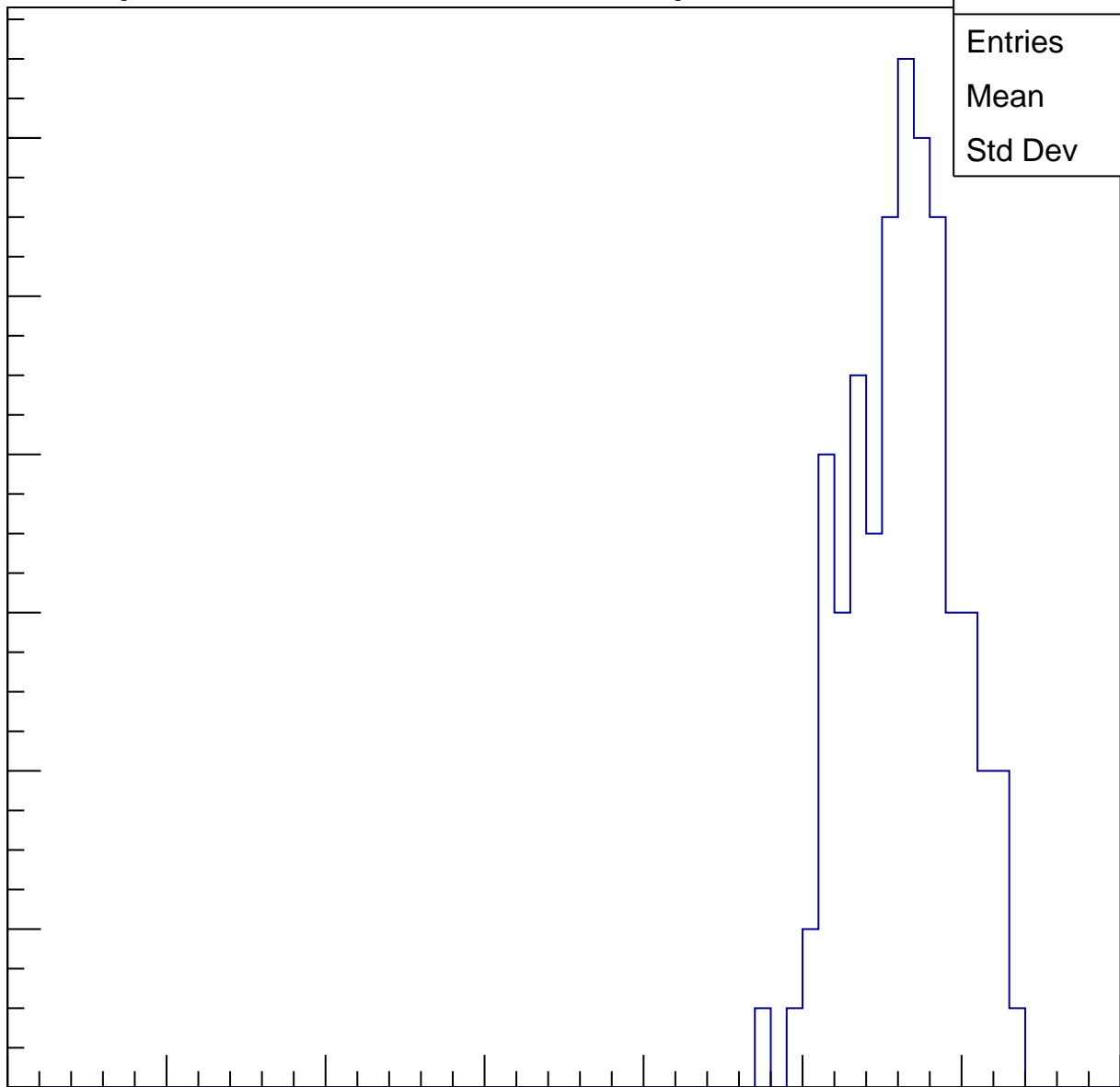
50

60

70

ampl

Entries	102
Mean	55.83
Std Dev	3.305



B0L101S, U18-ch104, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries	94
Mean	58.35
Std Dev	9.056

ampl

0

10

20

30

40

50

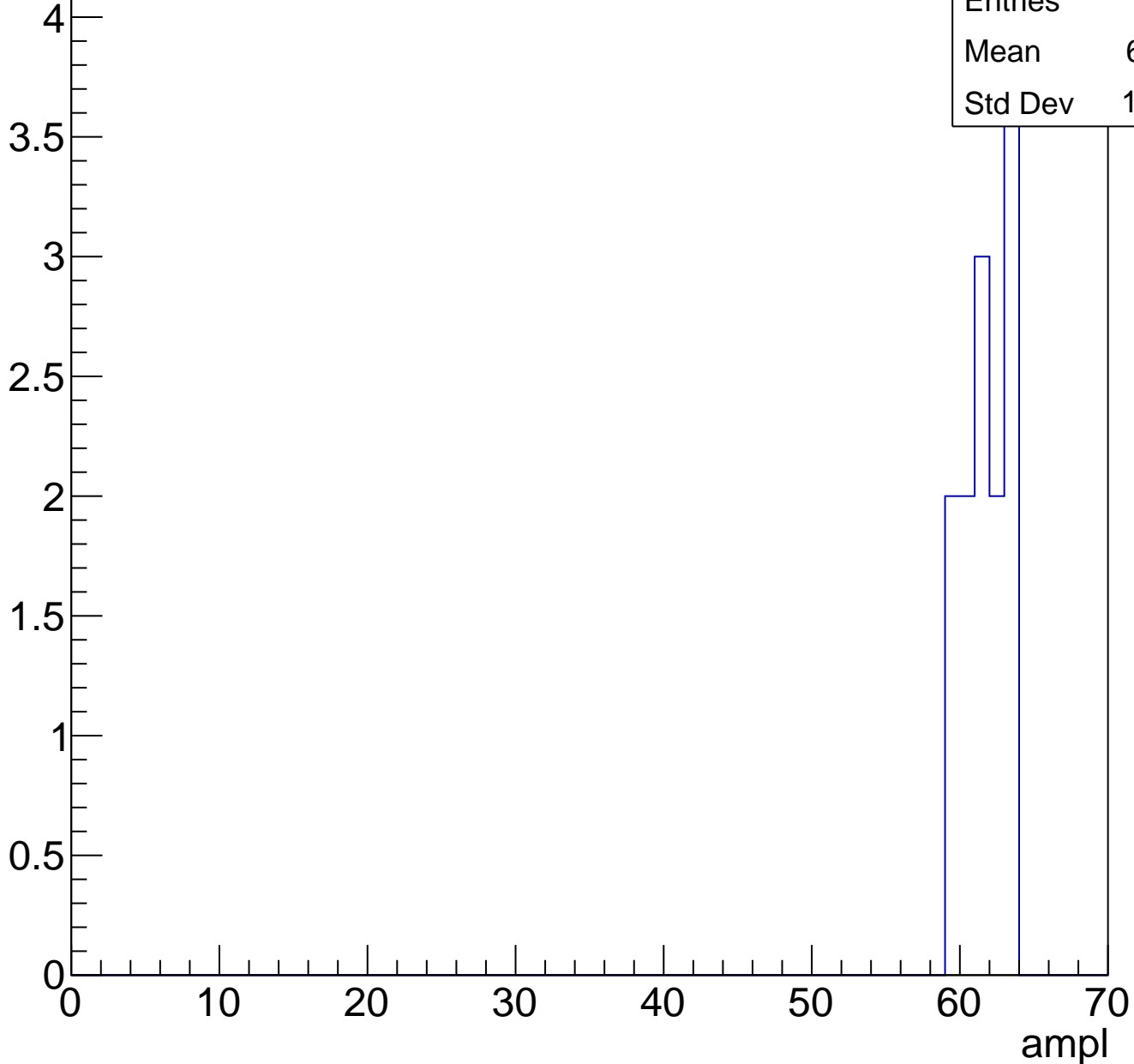
60

70

B0L101S, U18-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch105, adc0

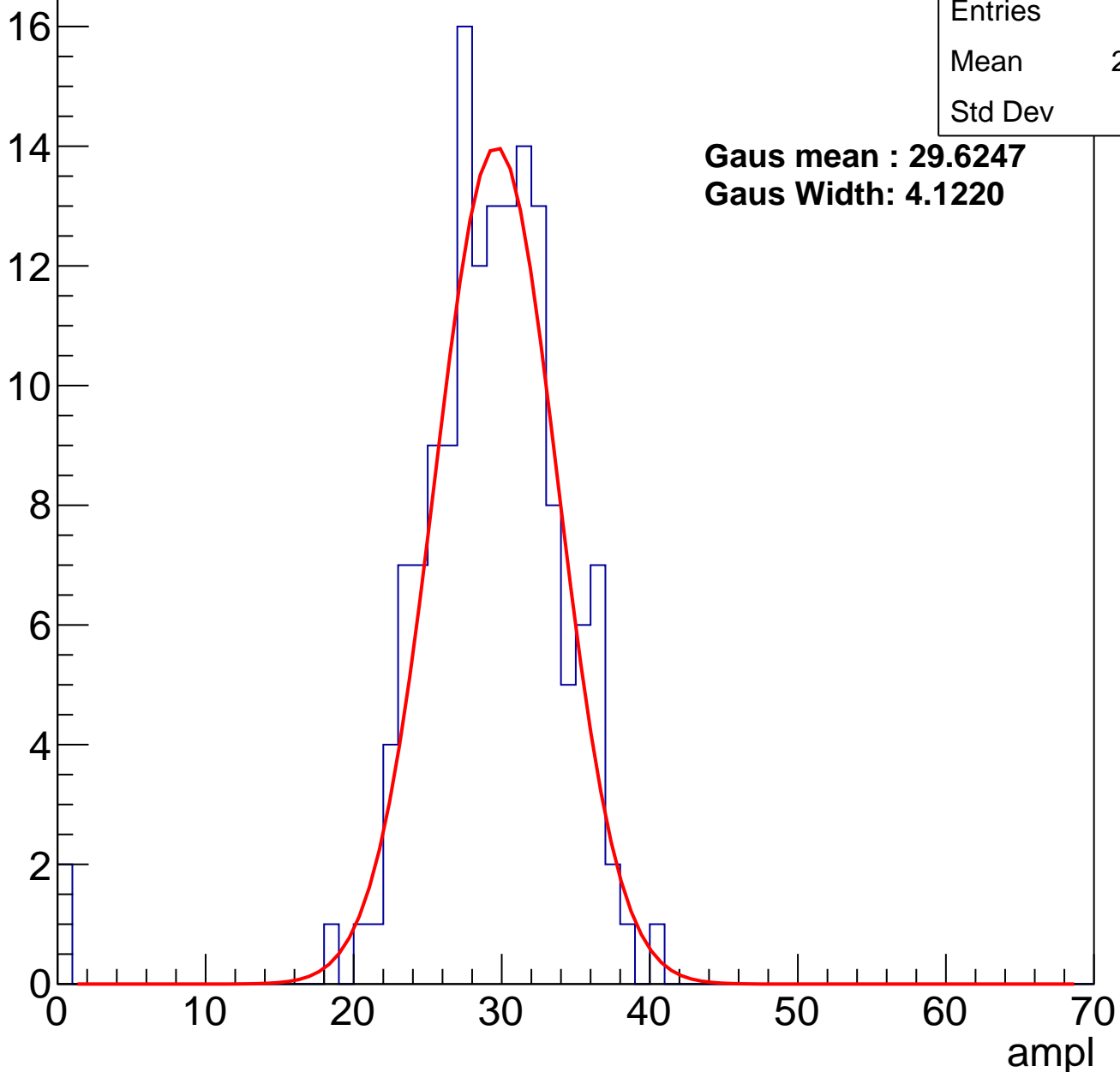
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.74
Std Dev	5.25

Gaus mean : 29.6247

Gaus Width: 4.1220

Entry



B0L101S, U18-ch105, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	36.46
Std Dev	3.832

Gaus mean : 36.8462

Gaus Width: 3.8642

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

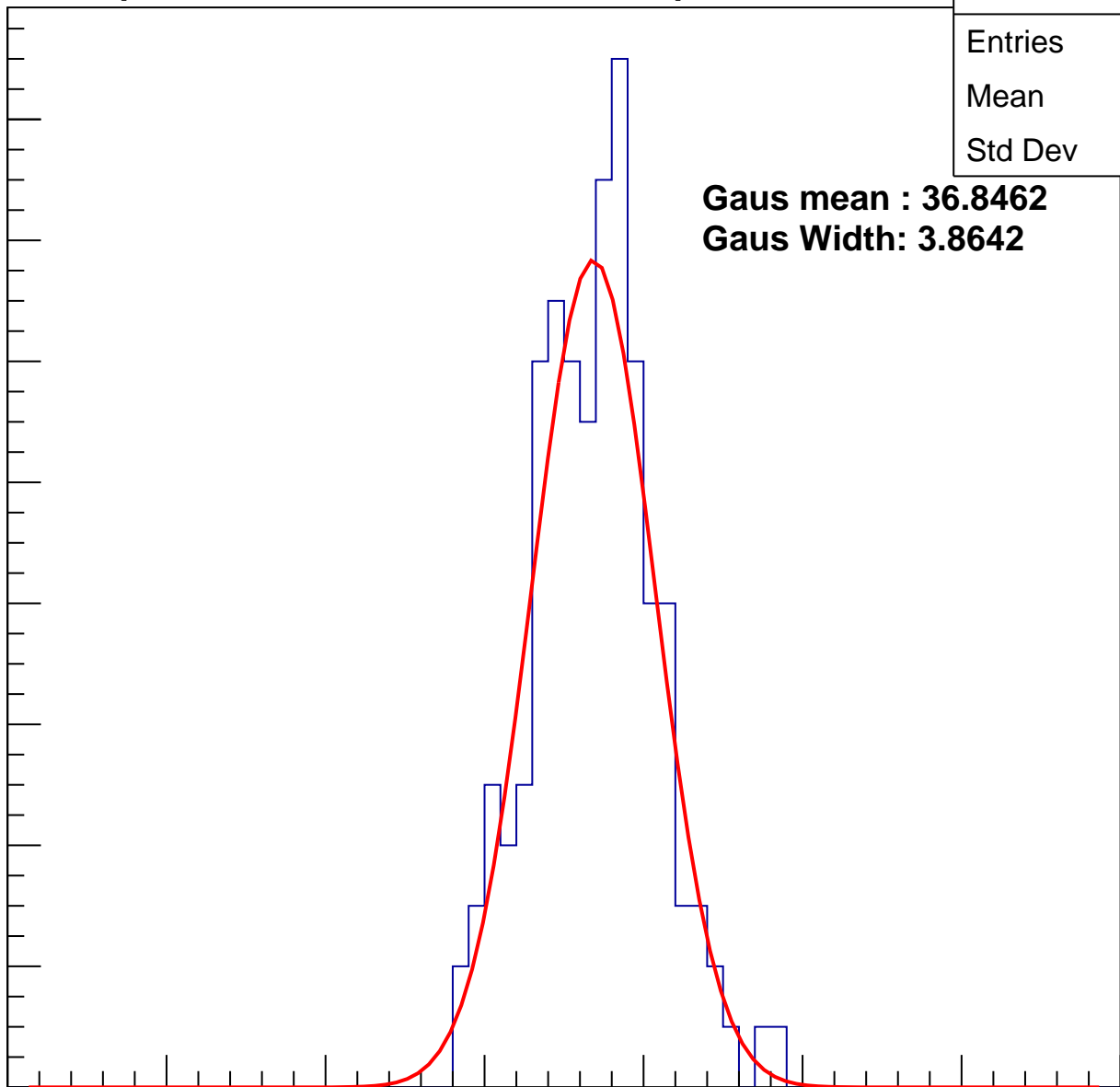
40

50

60

70

ampl



B0L101S, U18-ch105, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	42.06
Std Dev	3.51

Gaus mean : 43.2670

Gaus Width: 2.6583

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

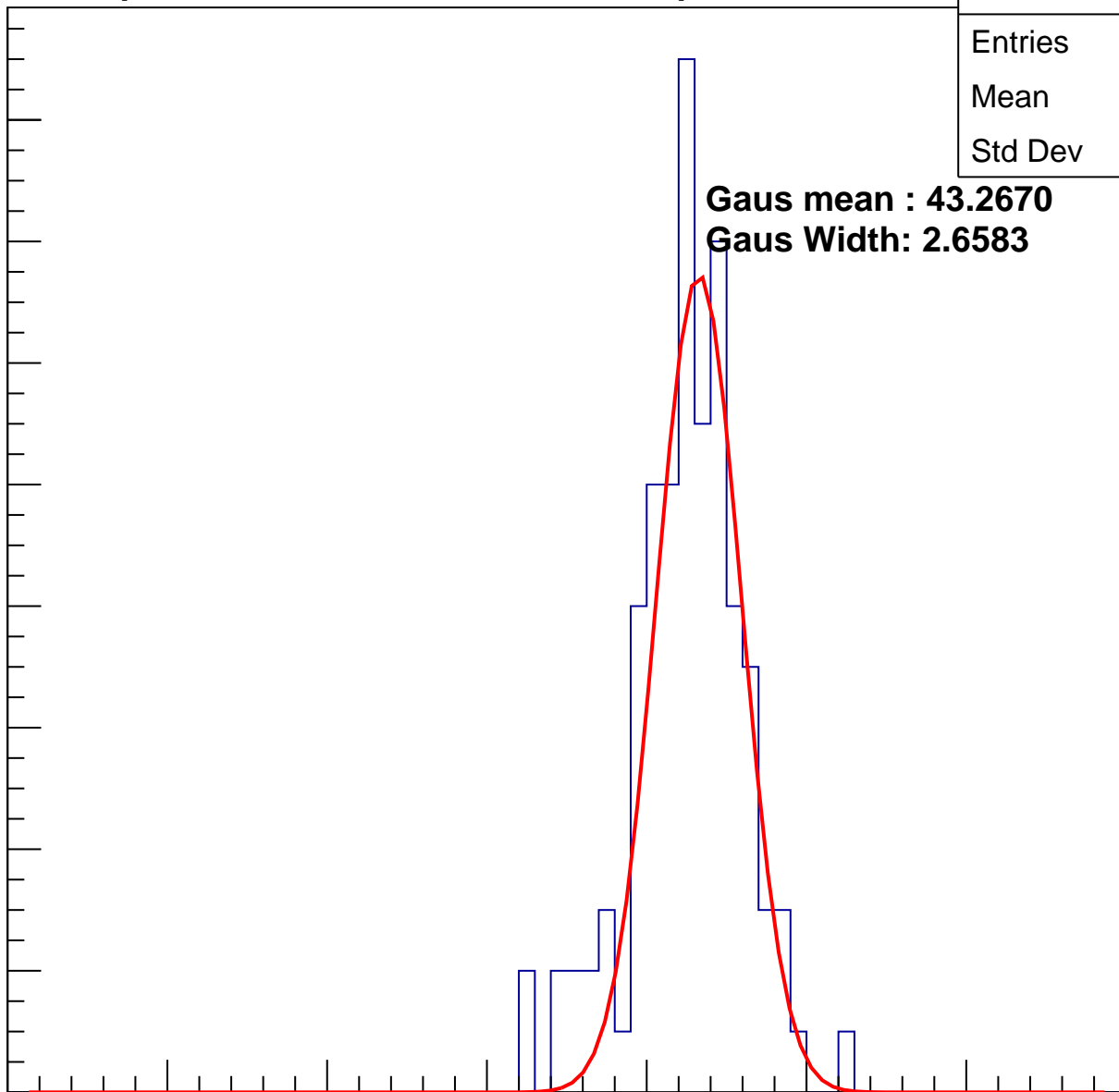
40

50

60

70

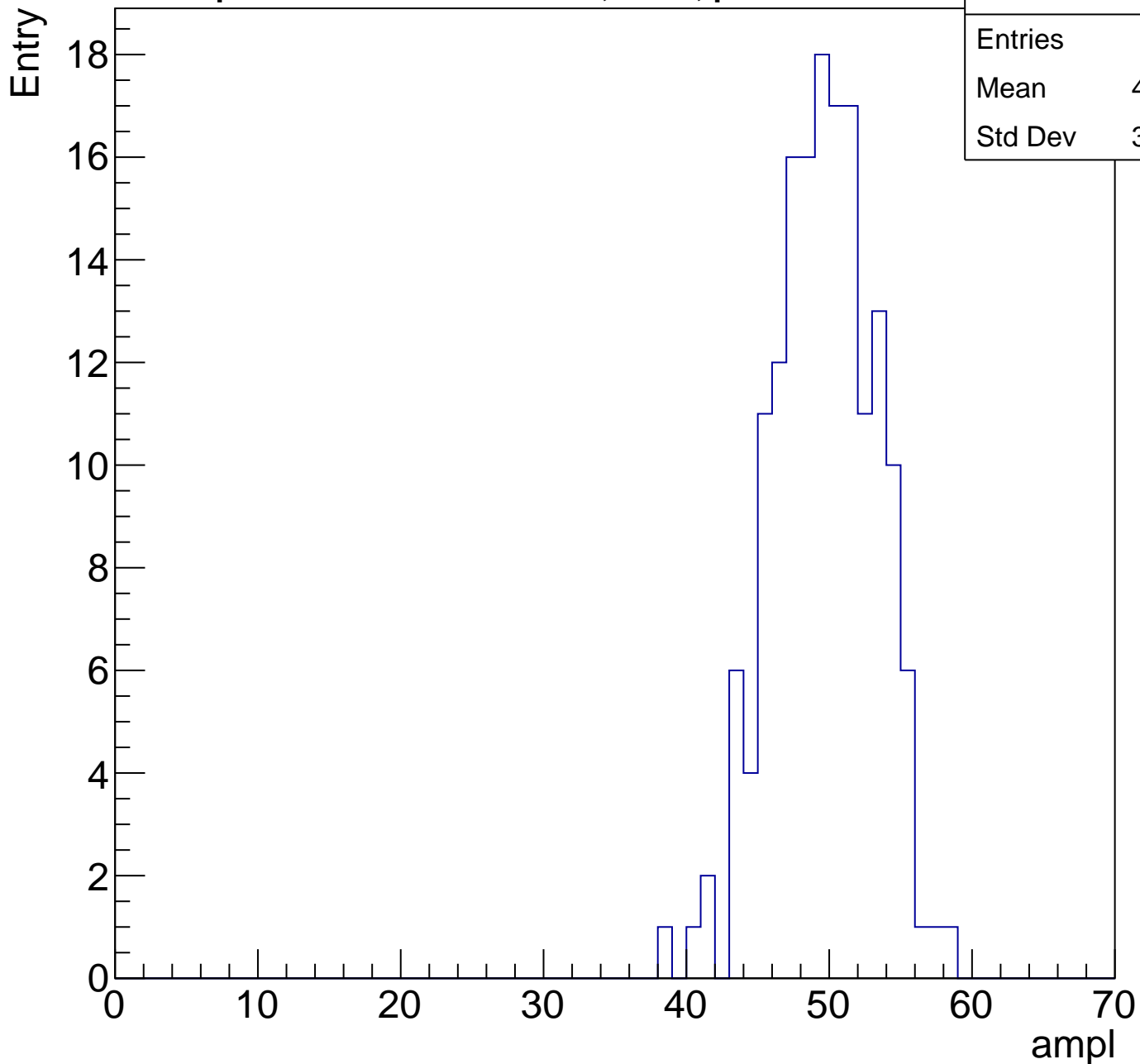
ampl



B0L101S, U18-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	49.16
Std Dev	3.542



B0L101S, U18-ch105, adc4

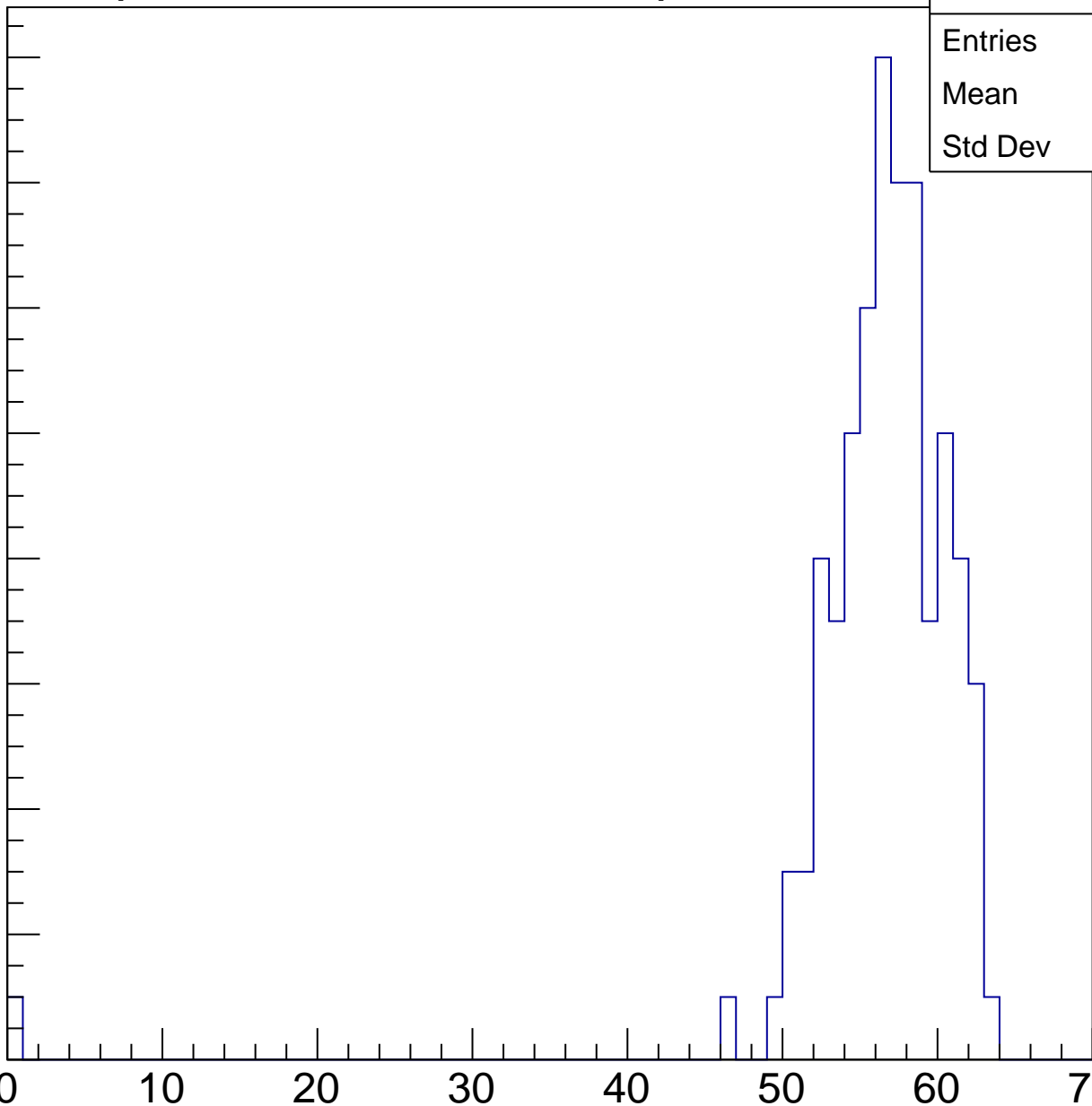
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	55.96
Std Dev	6.052

ampl



B0L101S, U18-ch105, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

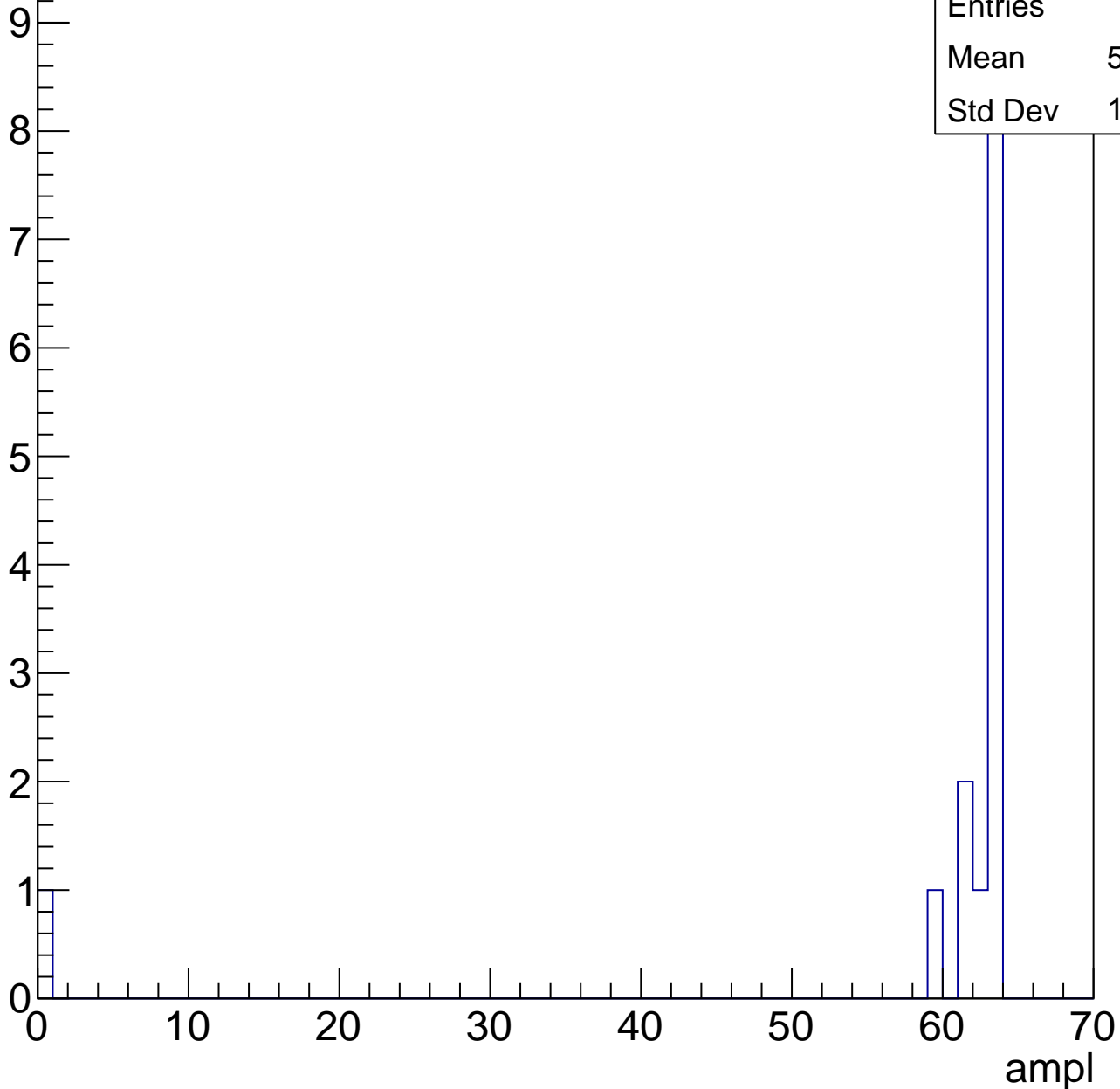
Entries	69
Mean	60.07
Std Dev	2.162

B0L101S, U18-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

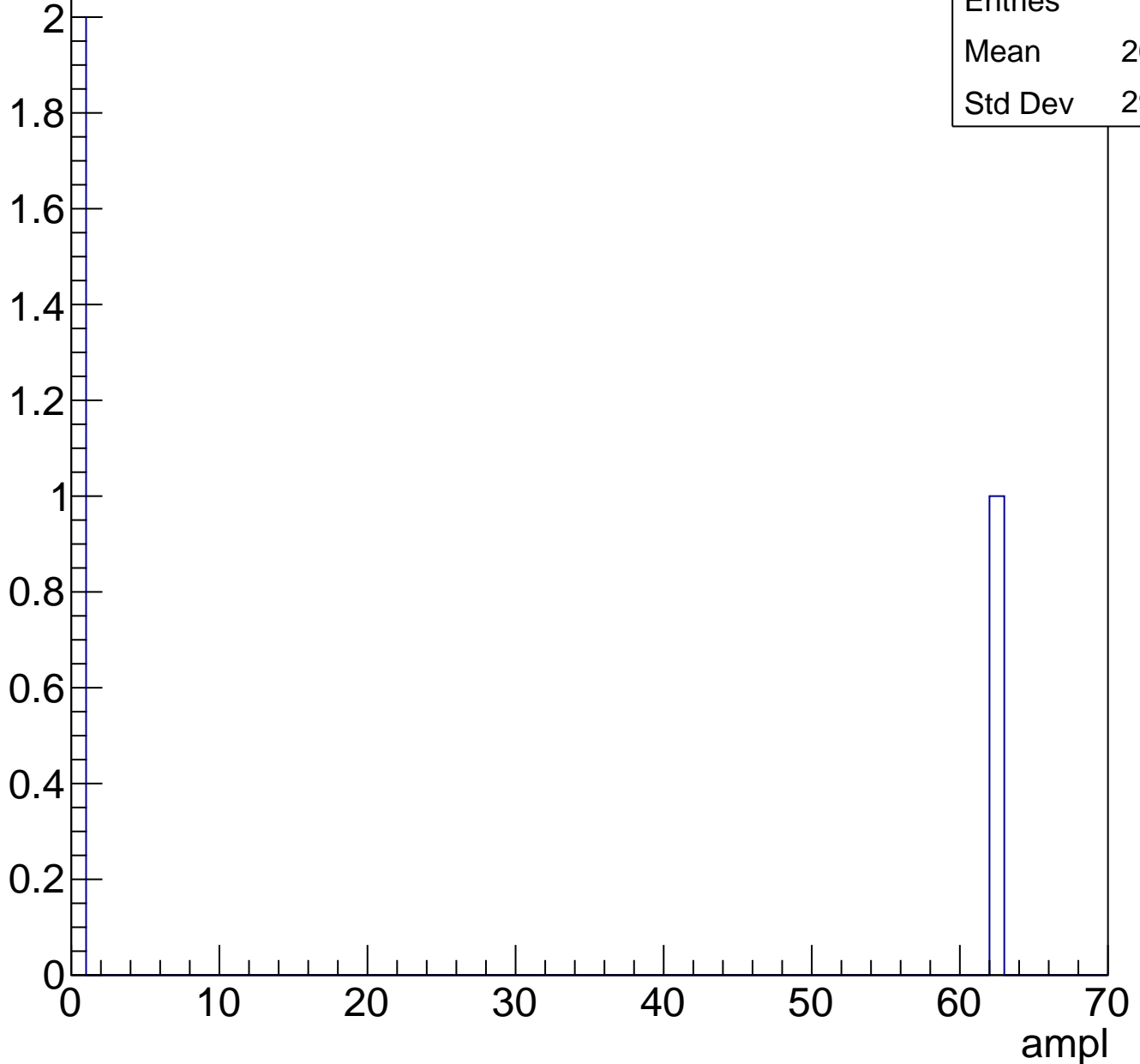
Entries	14
Mean	57.86
Std Dev	16.09



B0L101S, U18-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch106, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	29.12
Std Dev	4.003

Gaus mean : 29.5172

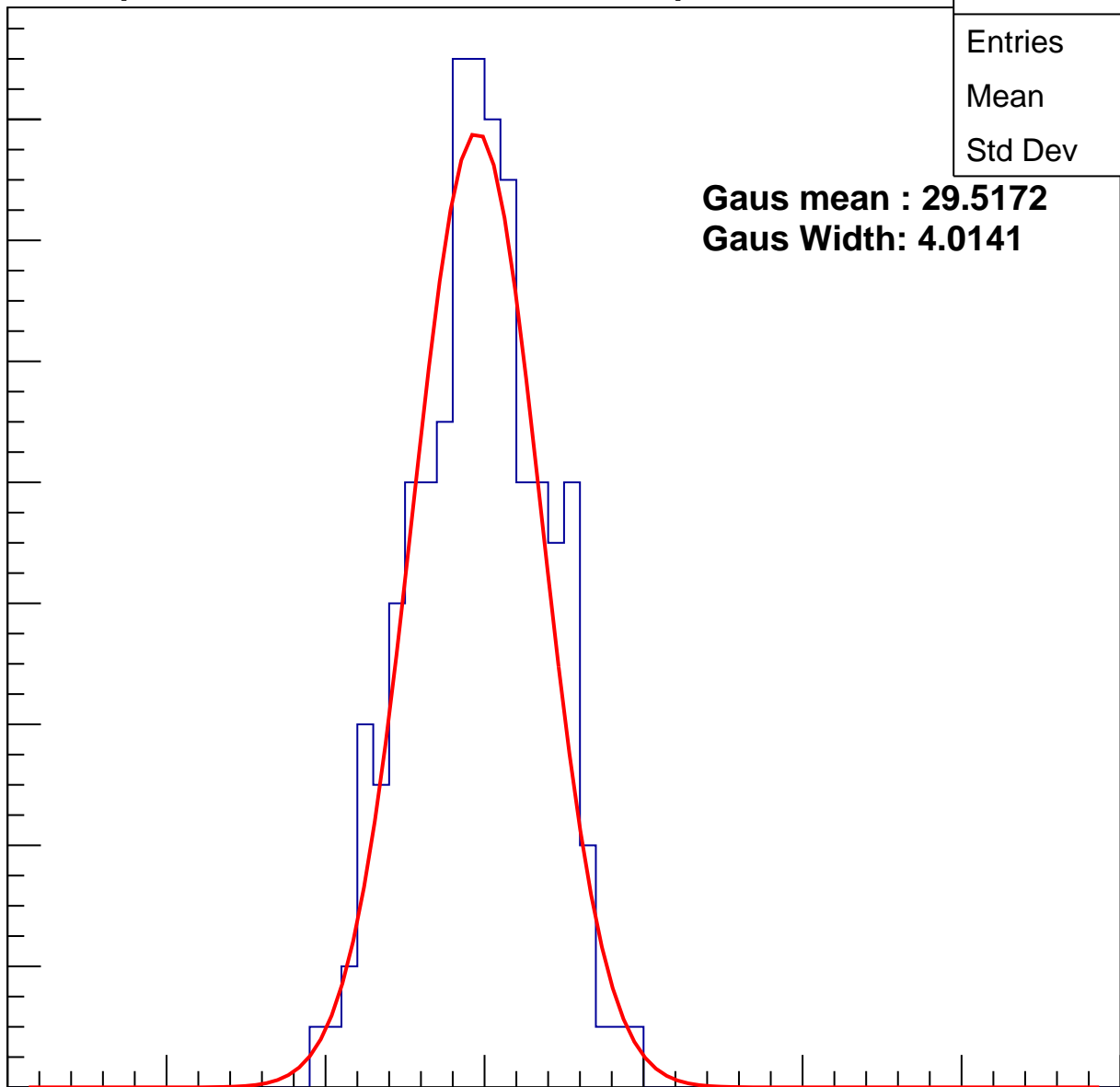
Gaus Width: 4.0141

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



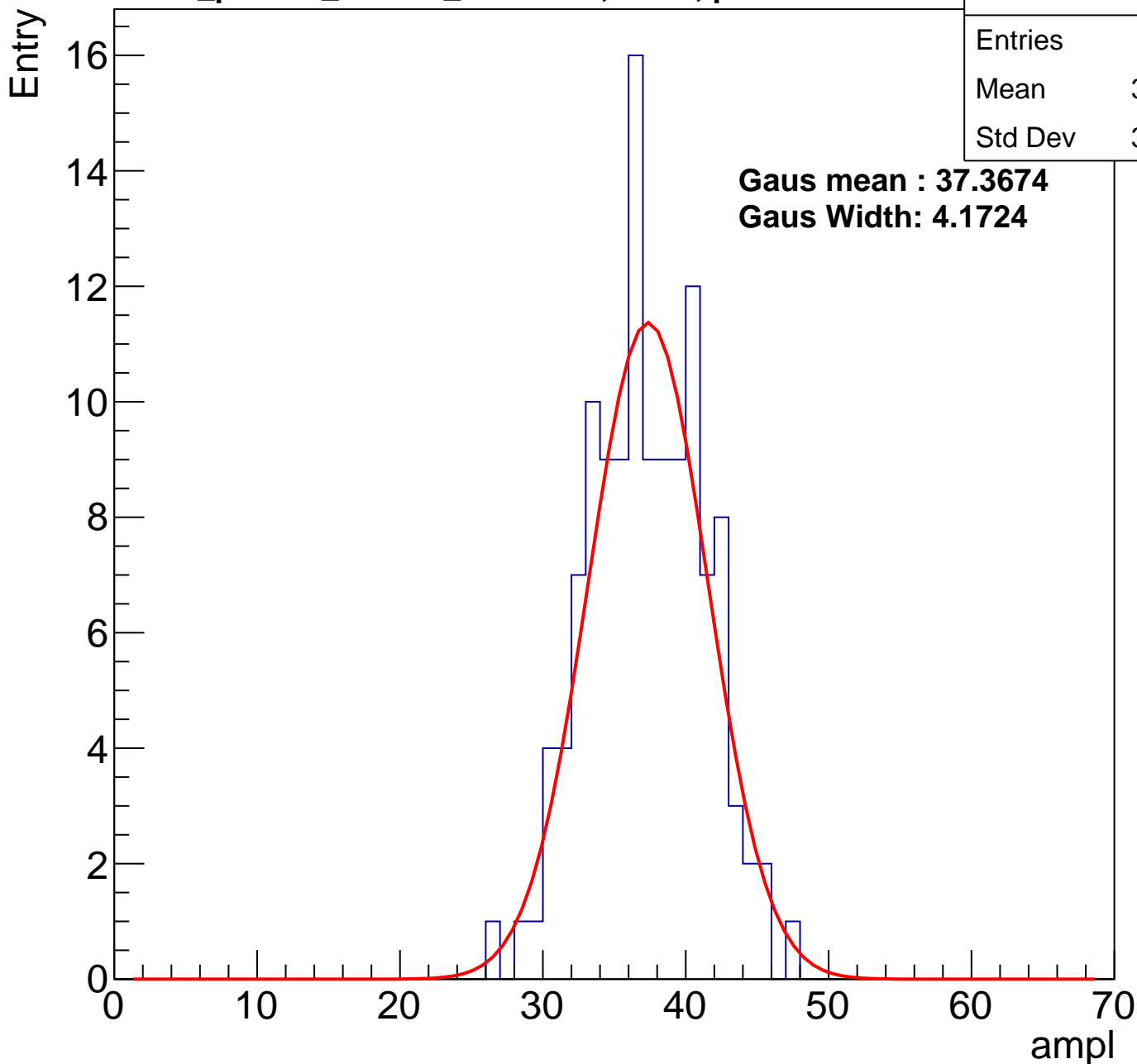
B0L101S, U18-ch106, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.78
Std Dev	3.983

Gaus mean : 37.3674

Gaus Width: 4.1724



B0L101S, U18-ch106, adc2

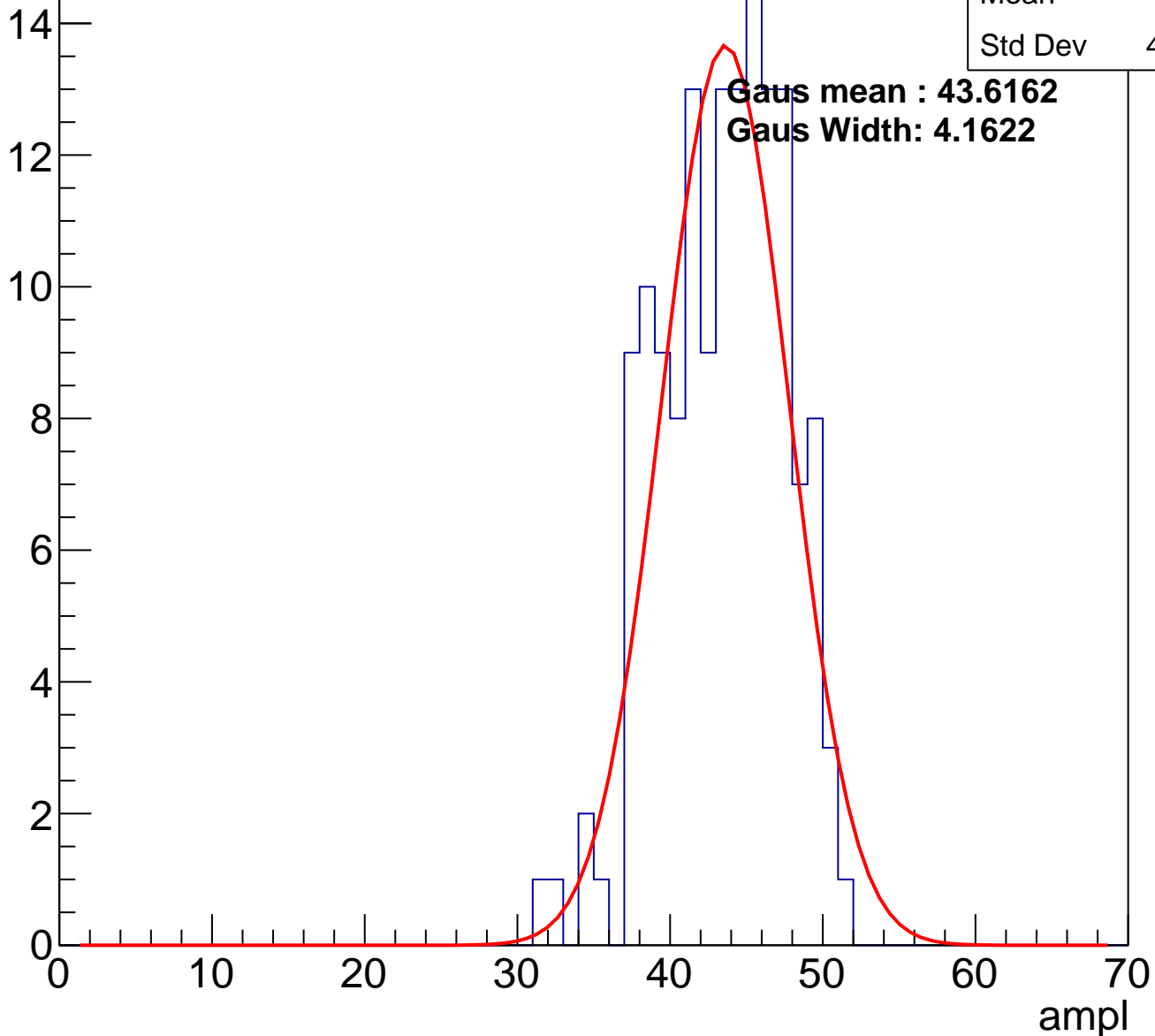
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	42.99
Std Dev	4.025

Gaus mean : 43.6162

Gaus Width: 4.1622

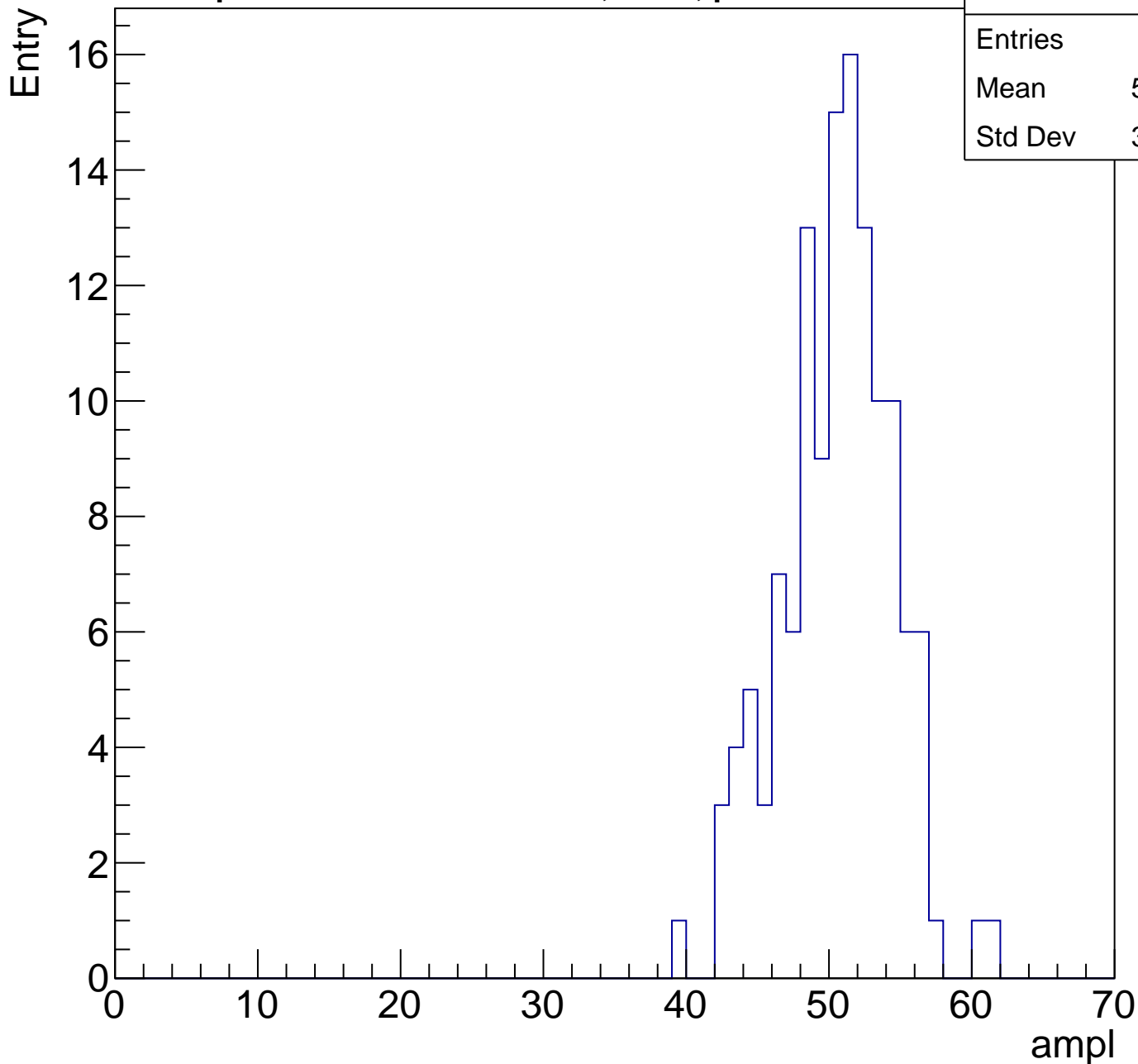
Entry



B0L101S, U18-ch106, adc3

calib_packv5_042523_0143.root, FC#1, port C1

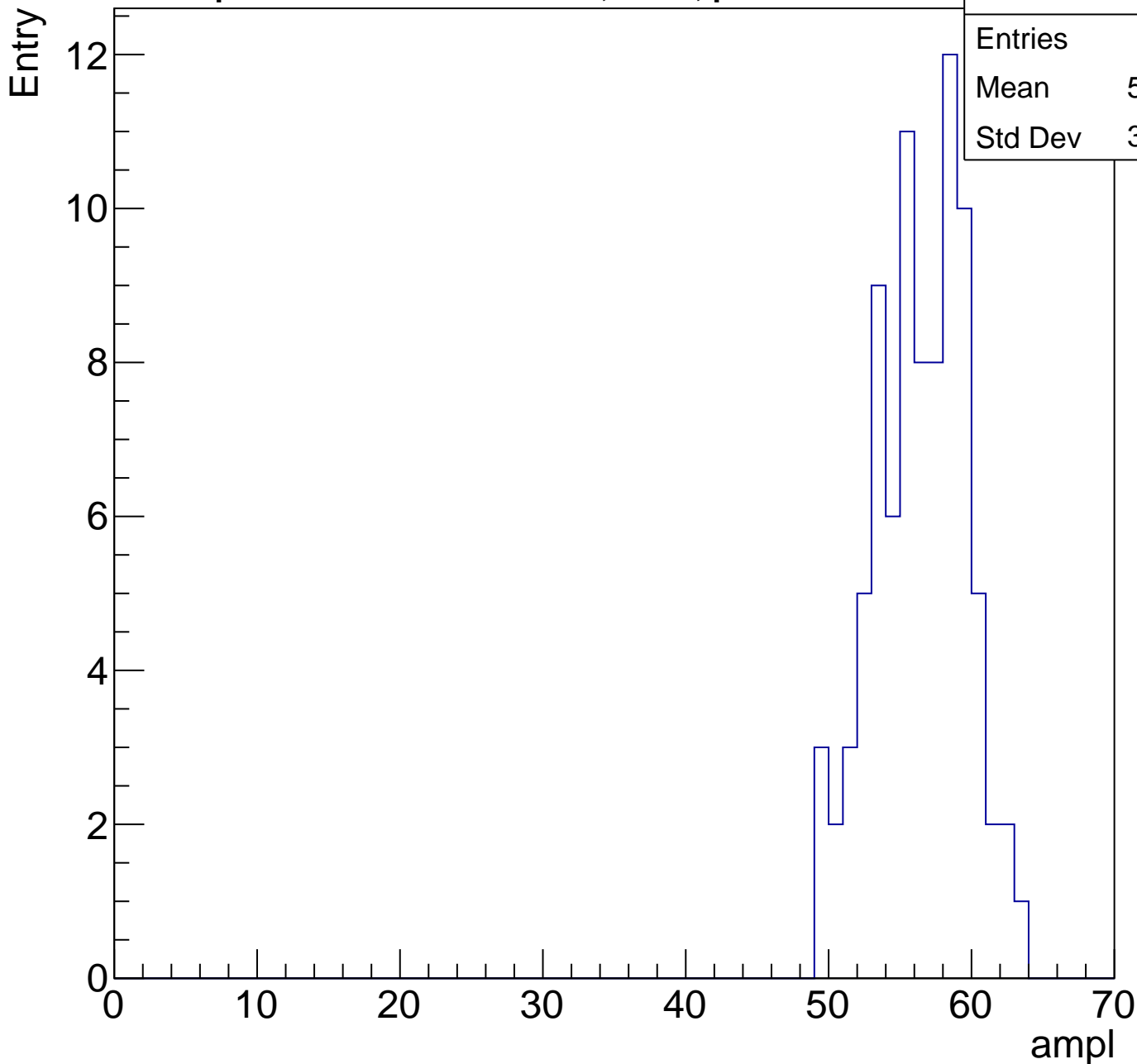
Entries	130
Mean	50.13
Std Dev	3.866



B0L101S, U18-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	87
Mean	55.92
Std Dev	3.192



B0L101S, U18-ch106, adc5

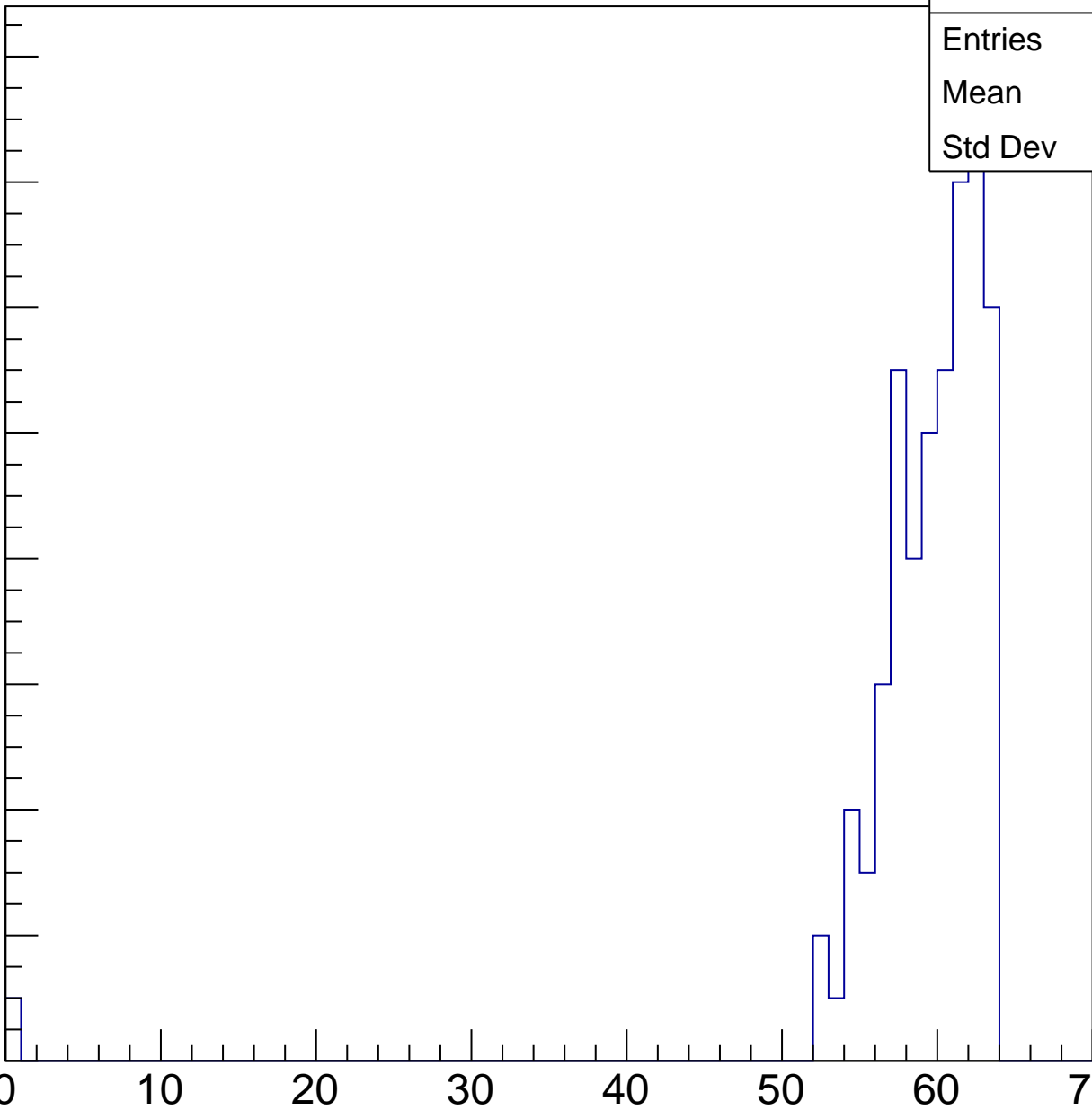
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	58.76
Std Dev	6.565

ampl

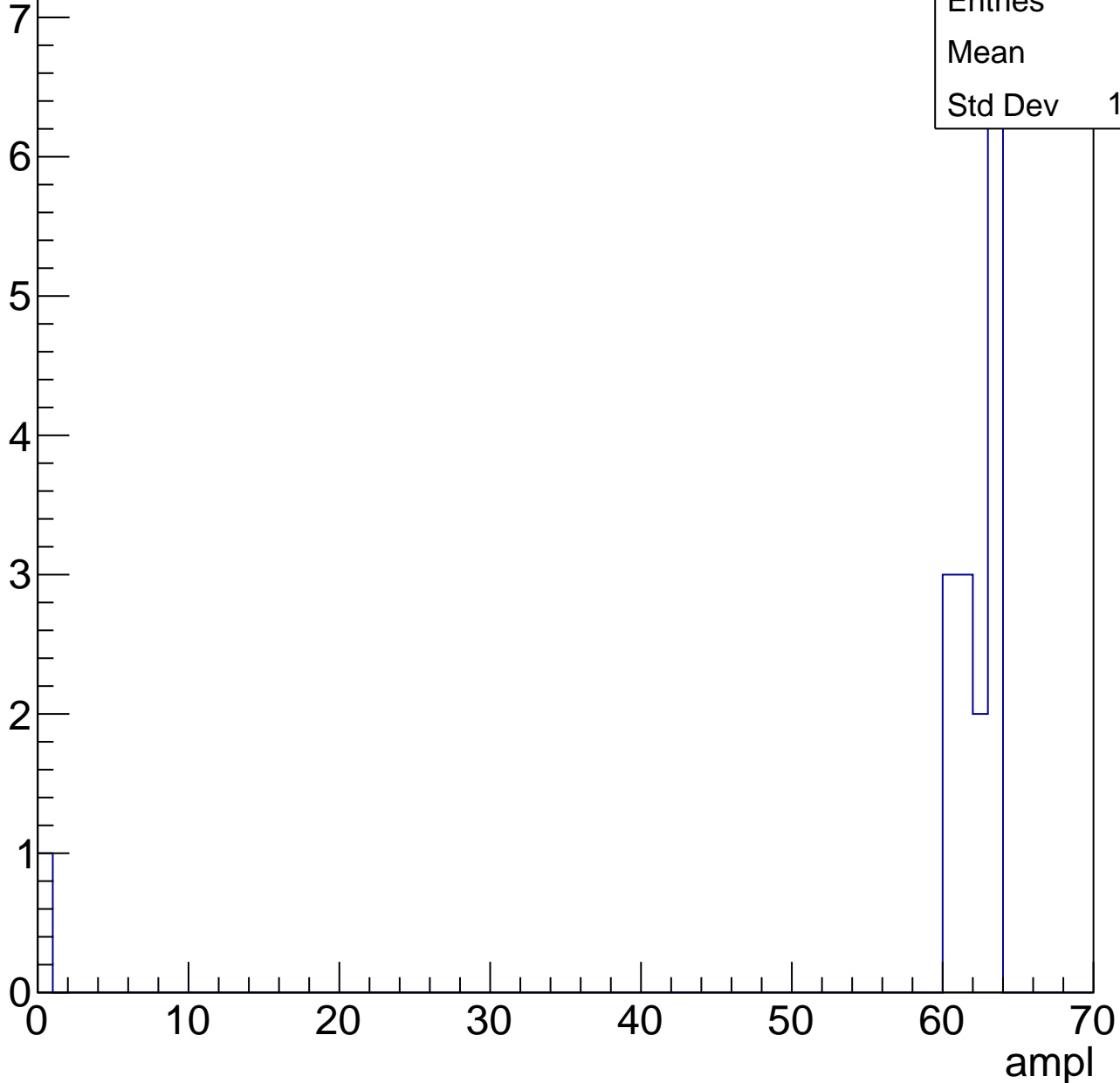


B0L101S, U18-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	58
Std Dev	15.02



B0L101S, U18-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch107, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

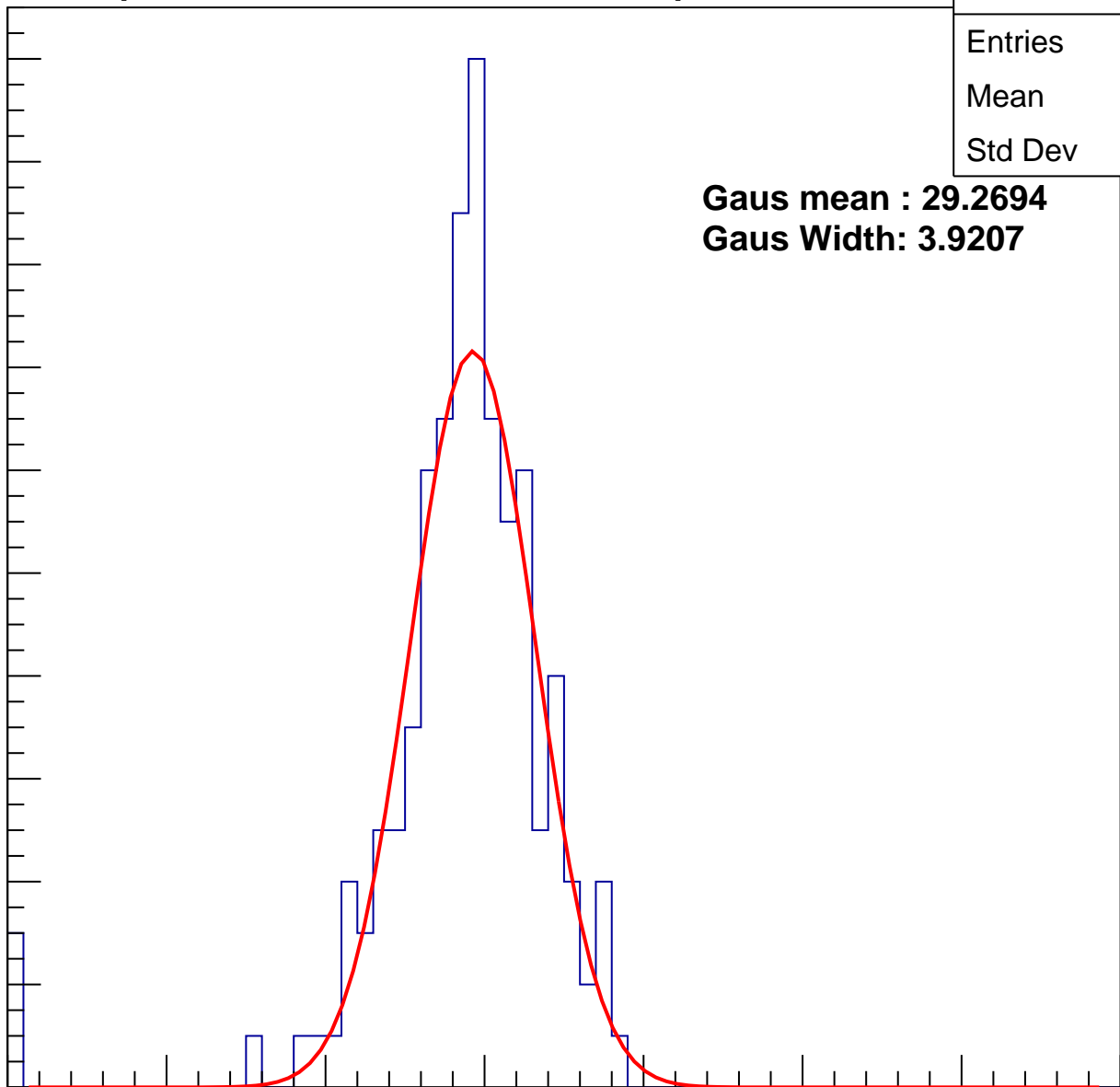
Entries	153
Mean	28.11
Std Dev	5.671

Gaus mean : 29.2694

Gaus Width: 3.9207

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch107, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	35.54
Std Dev	4.81

Gaus mean : 36.1182

Gaus Width: 3.9538

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch107, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	42.21
Std Dev	4.137

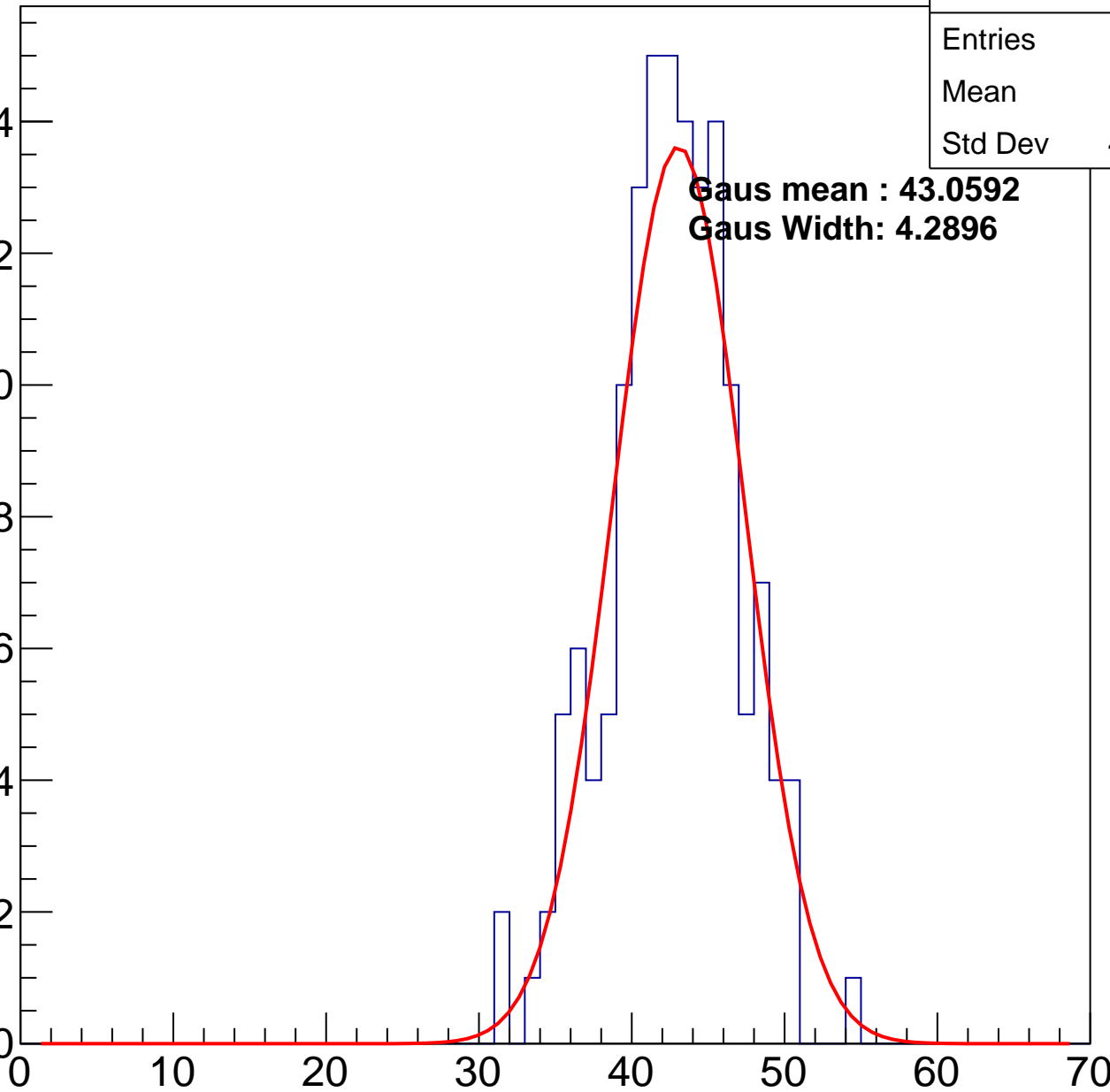
Gaus mean : 43.0592

Gaus Width: 4.2896

Entry

14
12
10
8
6
4
2
0

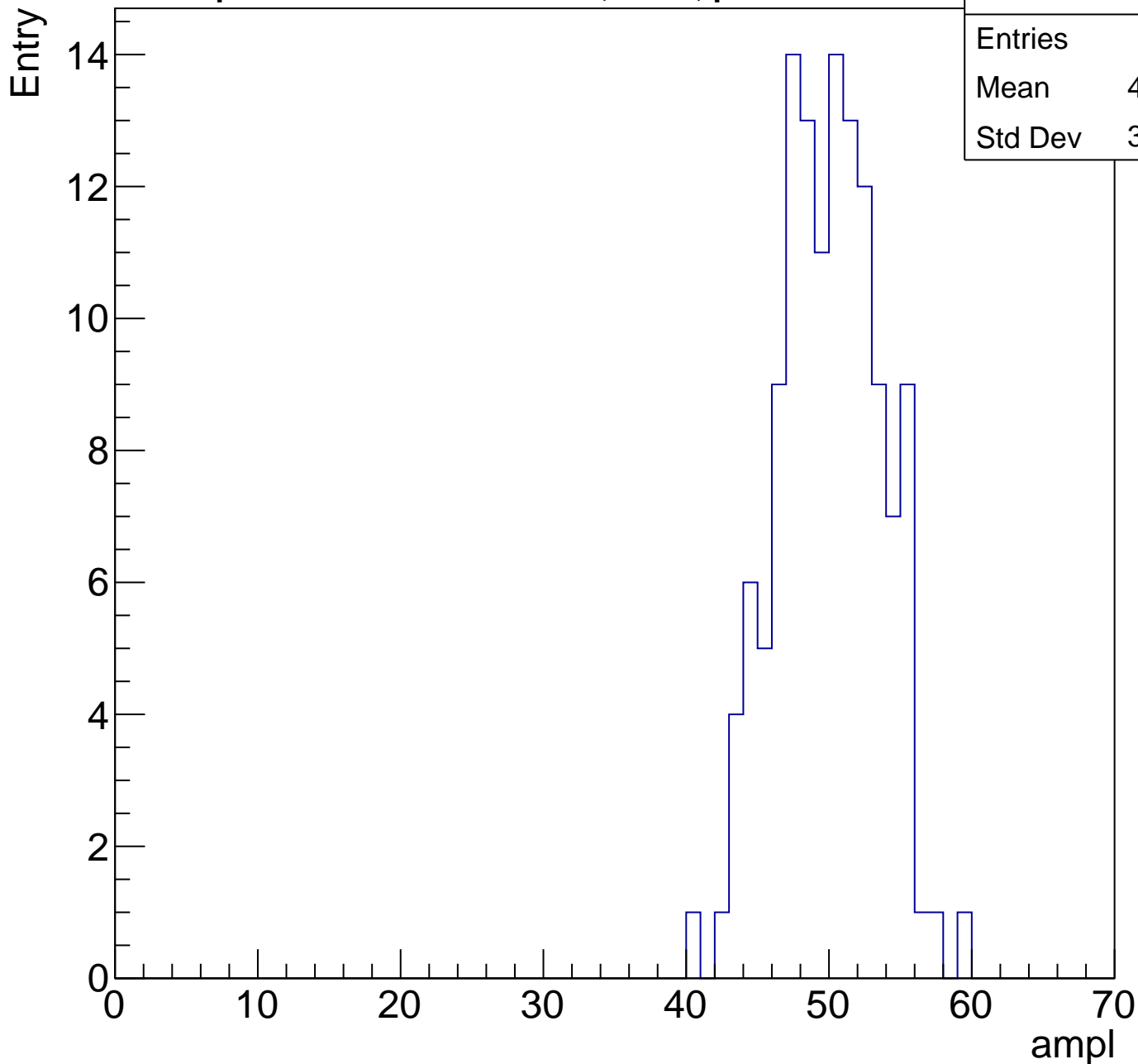
ampl



B0L101S, U18-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

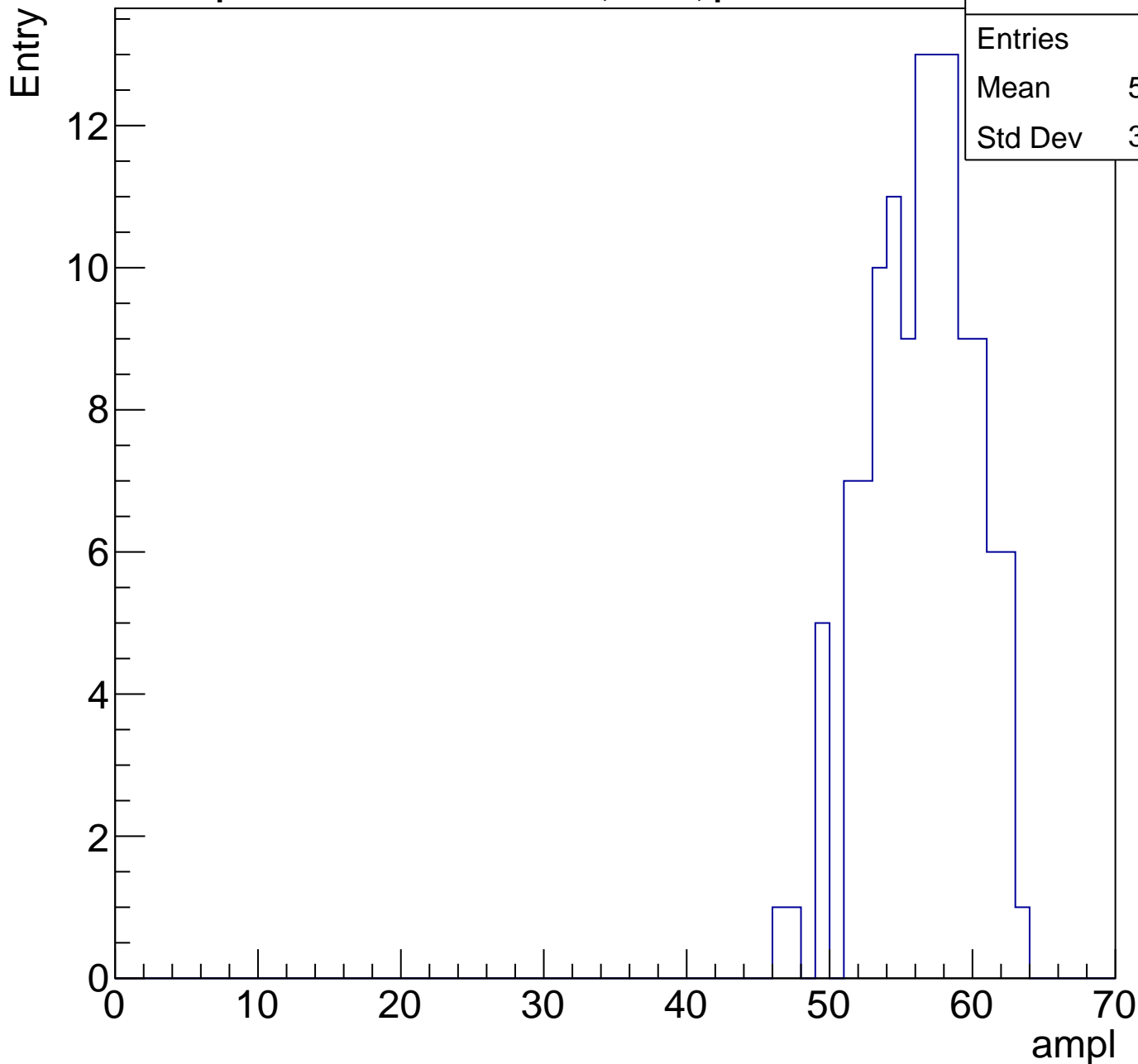
Entries	131
Mean	49.52
Std Dev	3.548



B0L101S, U18-ch107, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	55.98
Std Dev	3.592



B0L101S, U18-ch107, adc5

calib_packv5_042523_0143.root, FC#1, port C1

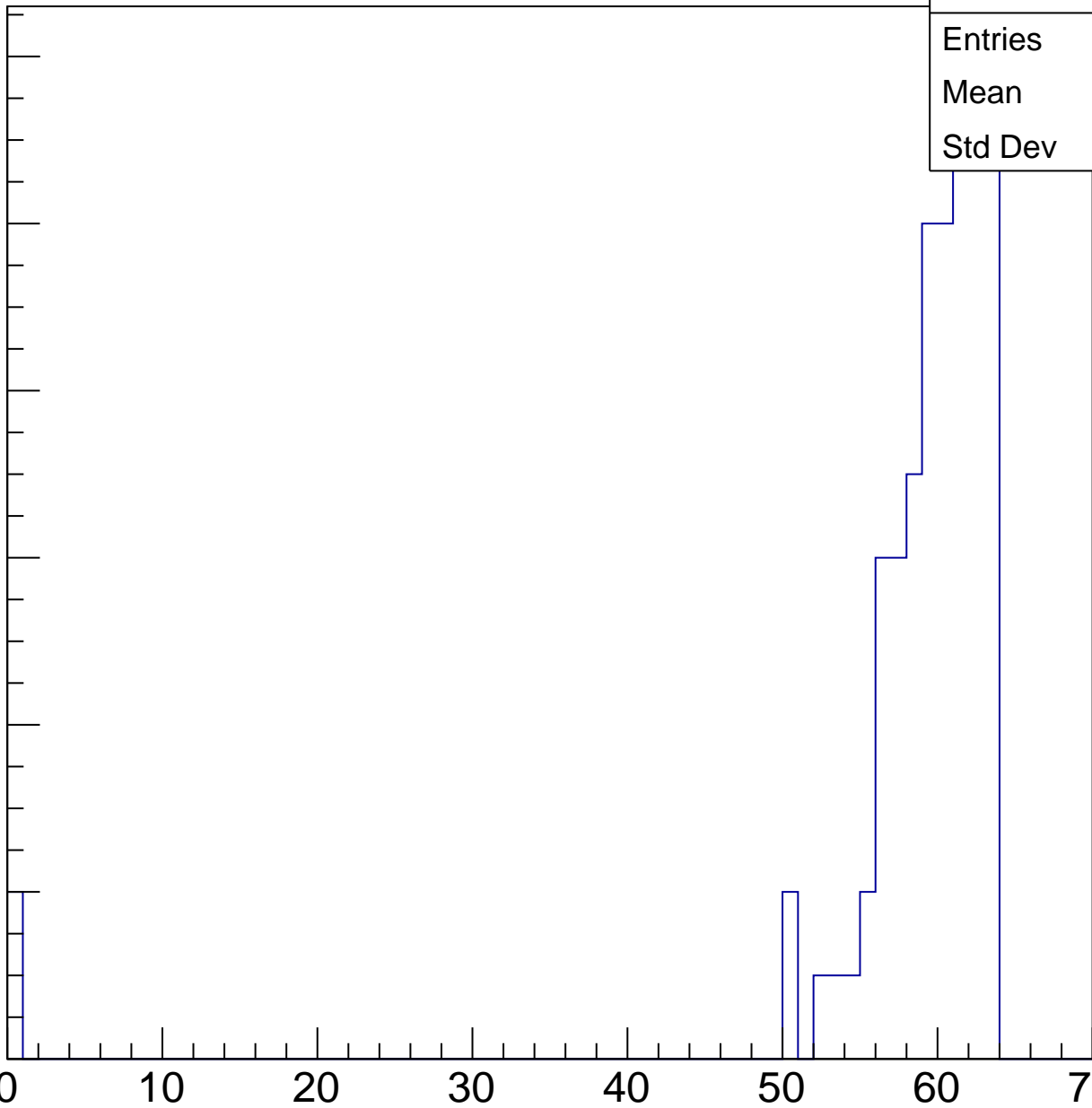
Entries	82
Mean	57.93
Std Dev	9.621

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

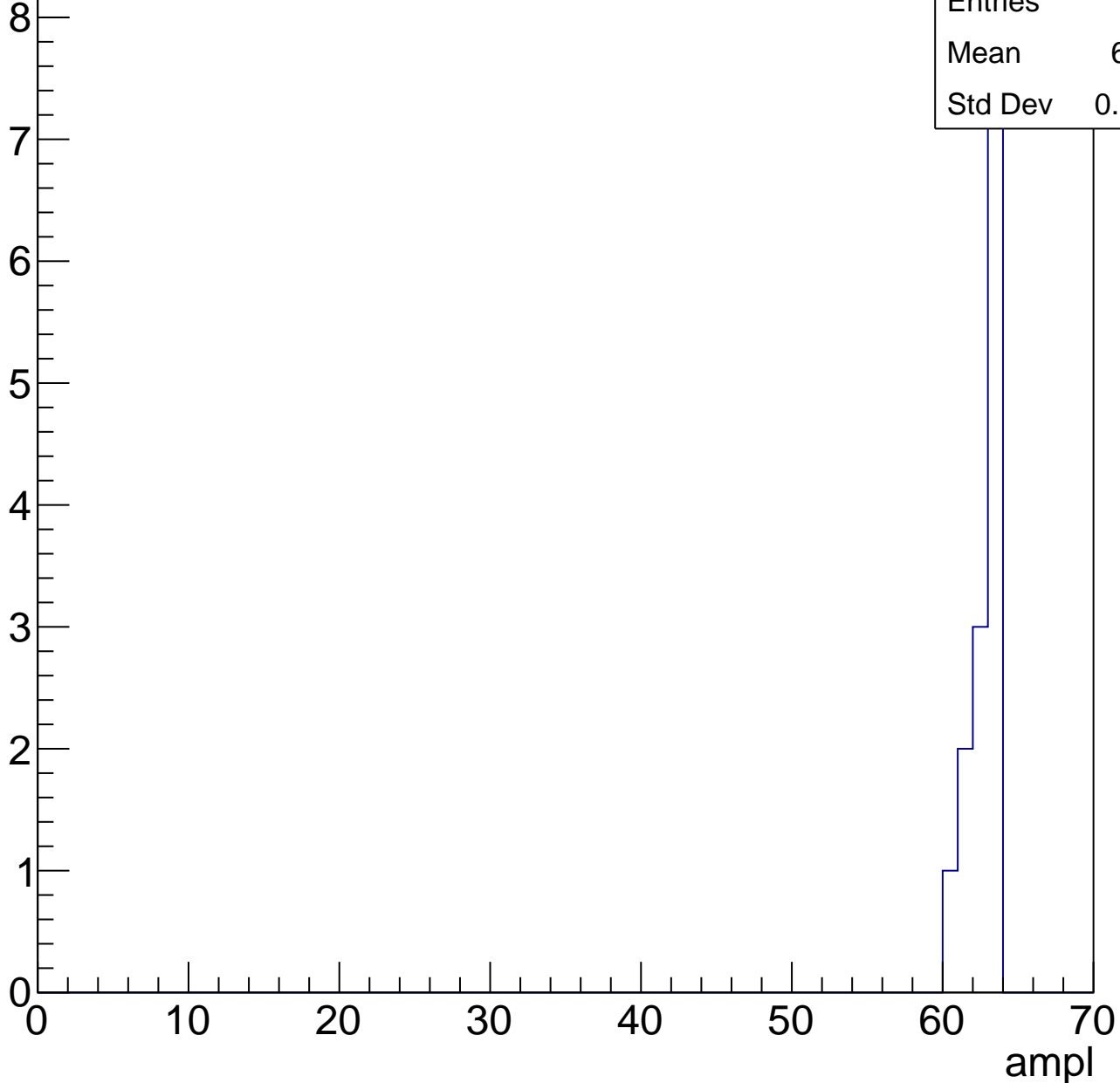


B0L101S, U18-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	62.29
Std Dev	0.9583



B0L101S, U18-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch108, adc0

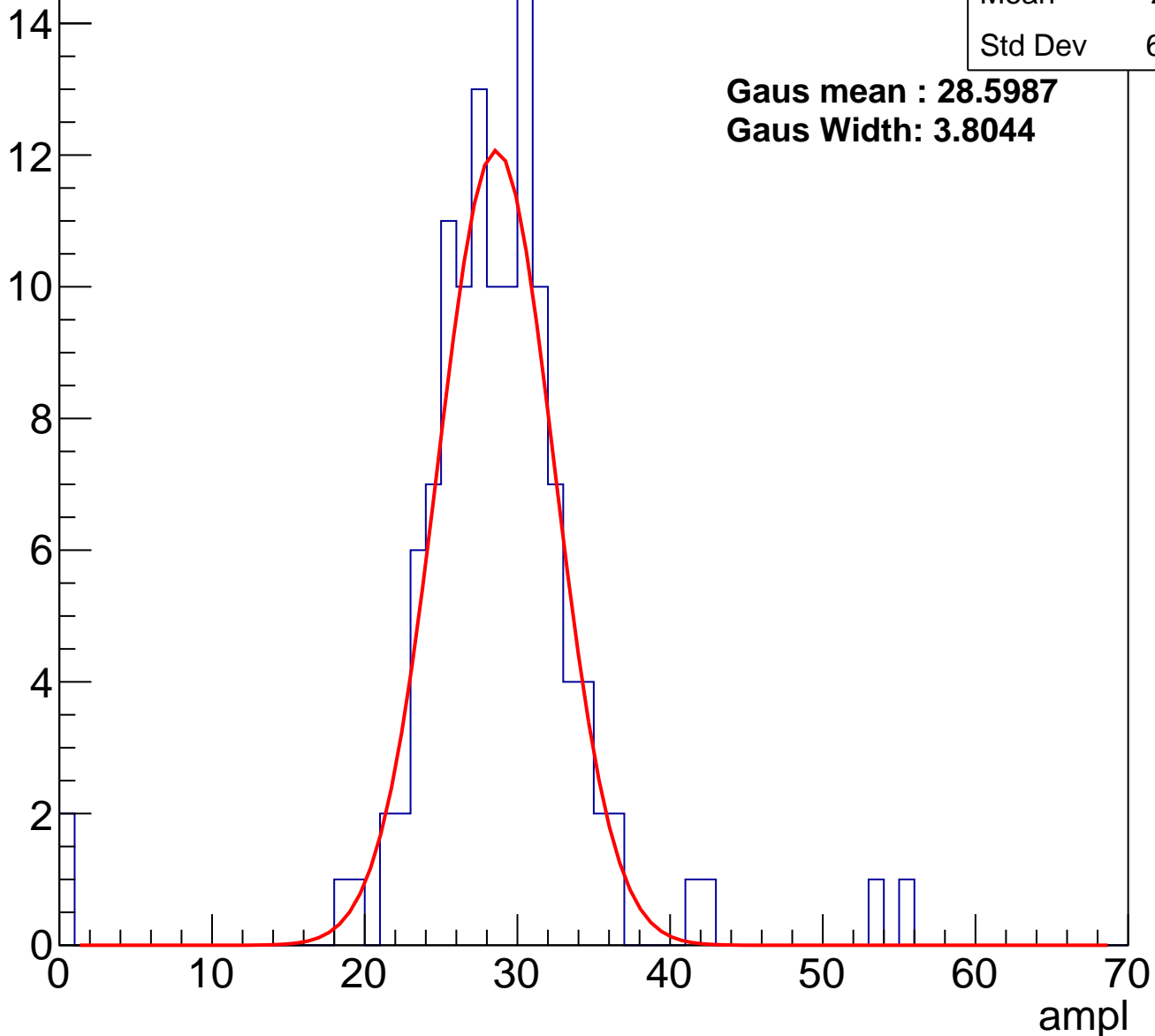
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	28.21
Std Dev	6.239

Gaus mean : 28.5987

Gaus Width: 3.8044

Entry



B0L101S, U18-ch108, adc1

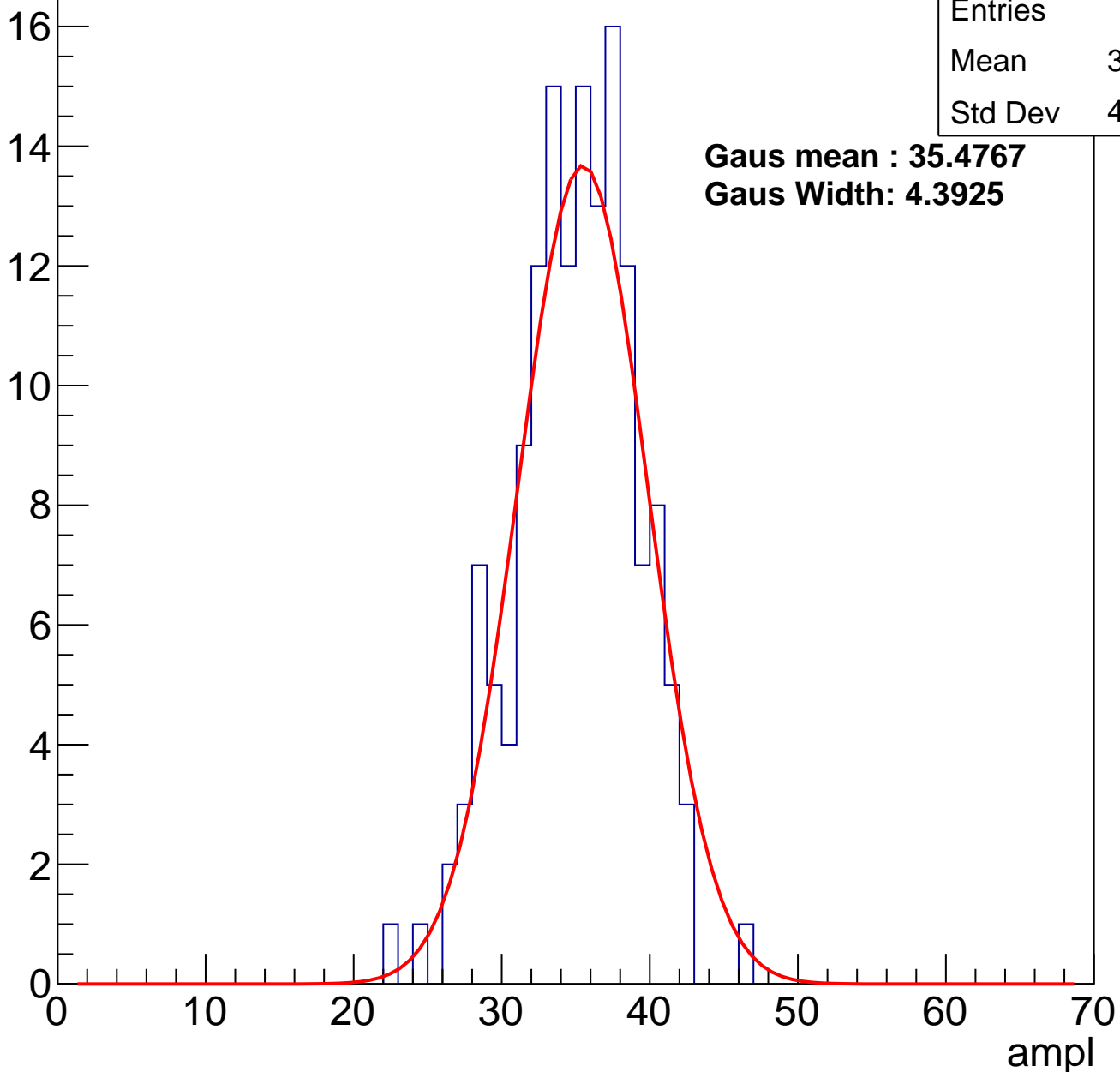
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	34.55
Std Dev	4.073

Gaus mean : 35.4767

Gaus Width: 4.3925

Entry



B0L101S, U18-ch108, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

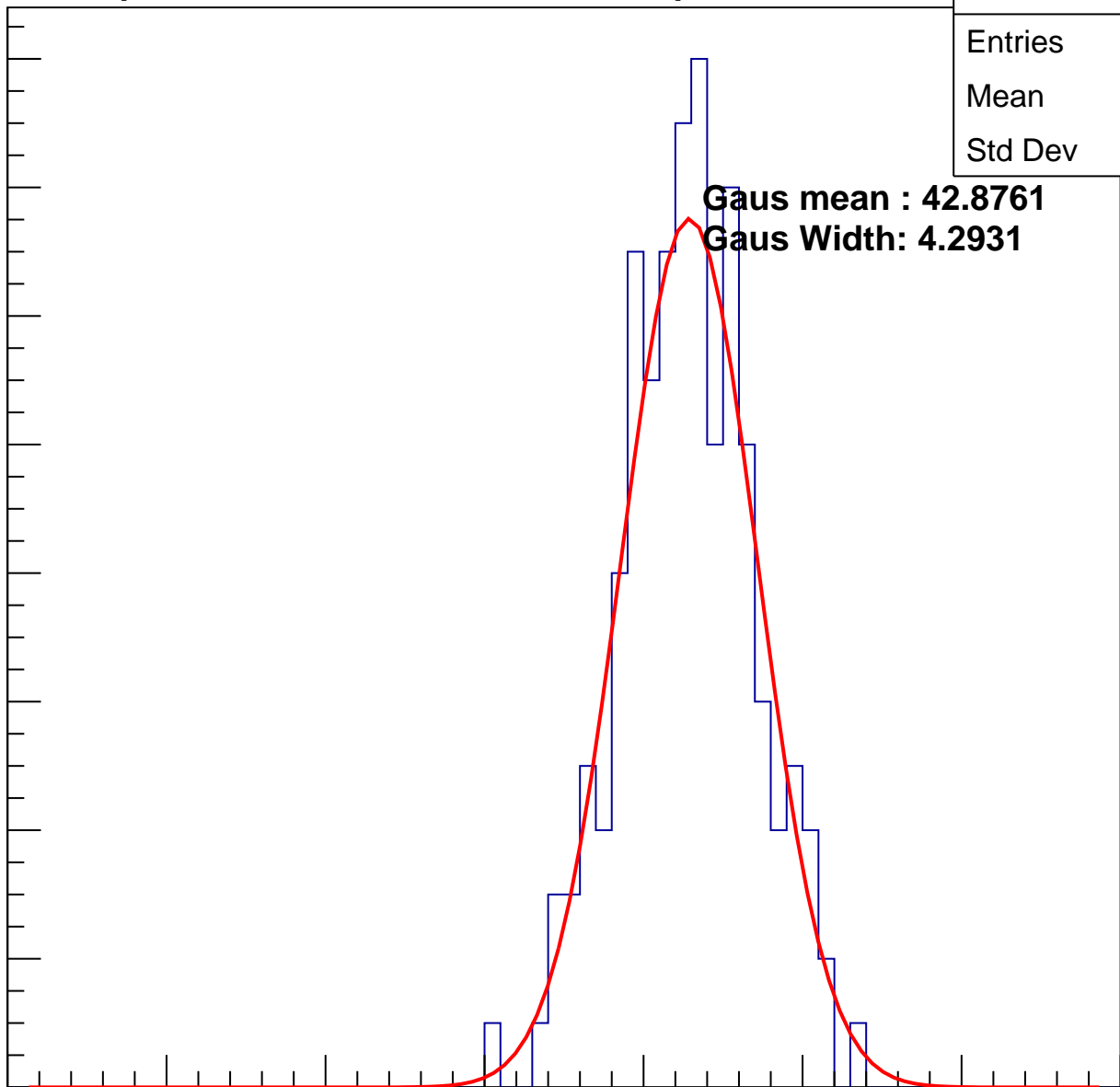
Entries	149
Mean	42.31
Std Dev	4.138

Gaus mean : 42.8761

Gaus Width: 4.2931

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch108, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	48.36
Std Dev	3.575

Entry

12

10

8

6

4

2

0

0

10

20

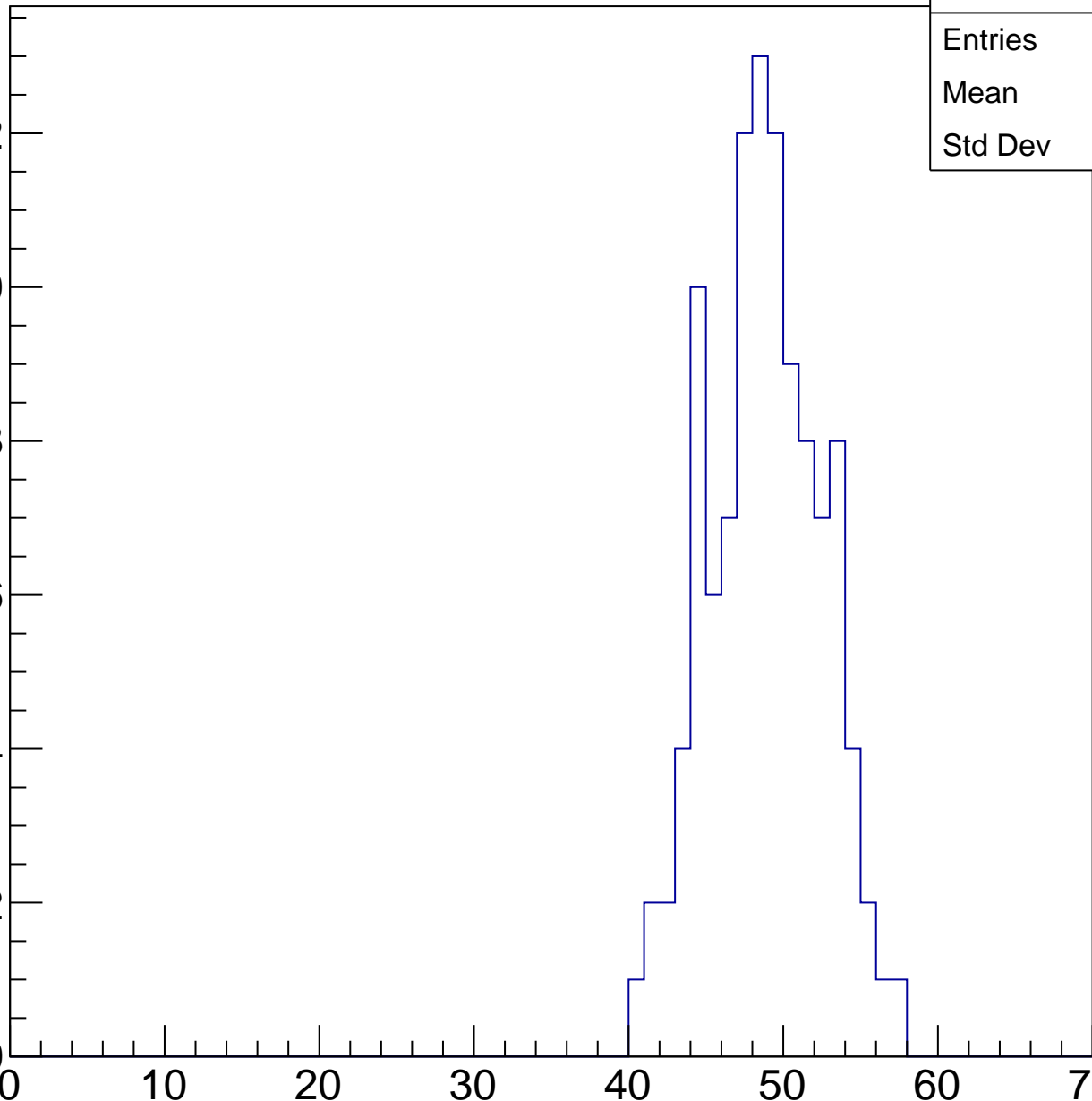
30

40

50

60

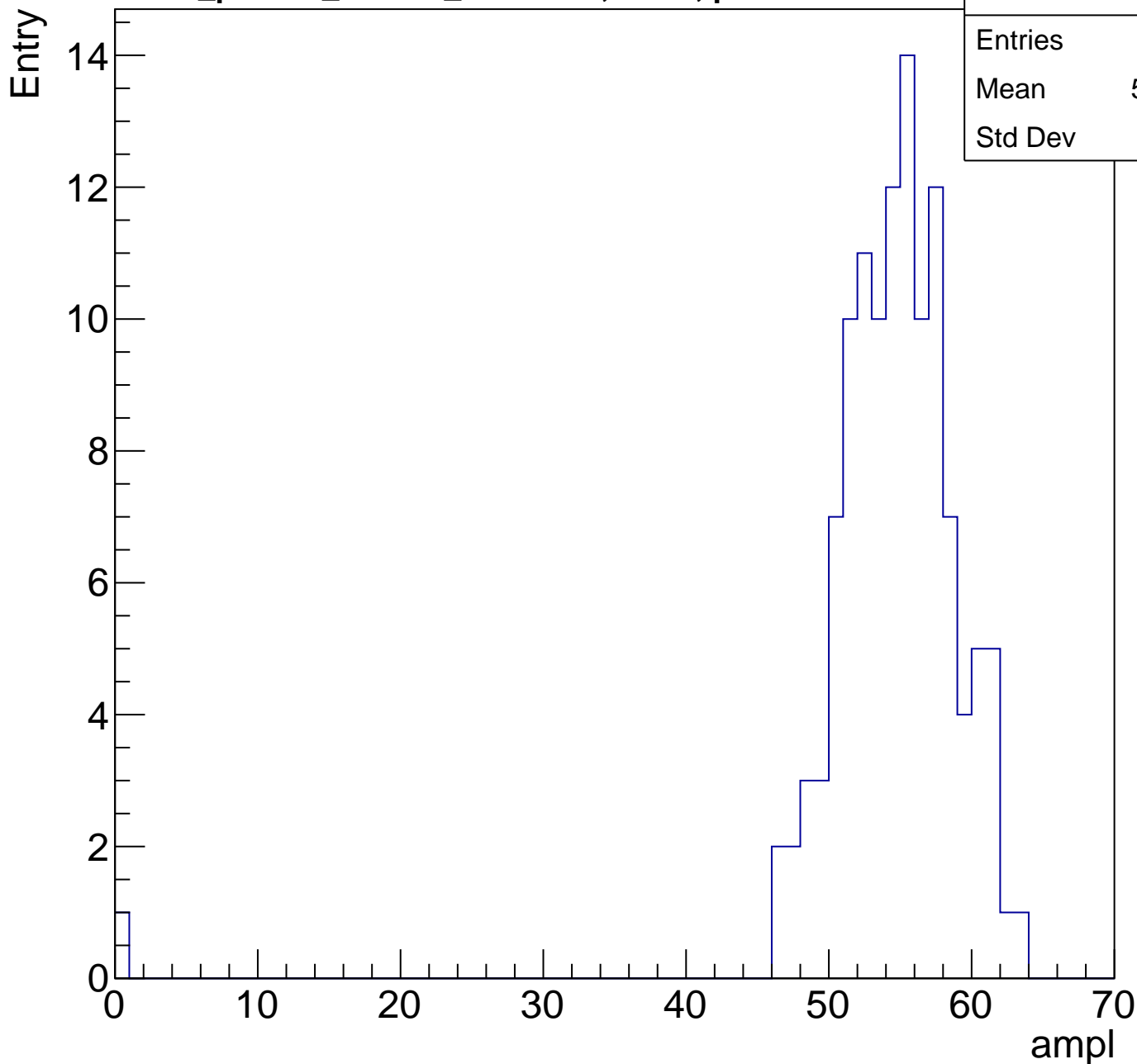
ampl



B0L101S, U18-ch108, adc4

calib_packv5_042523_0143.root, FC#1, port C1

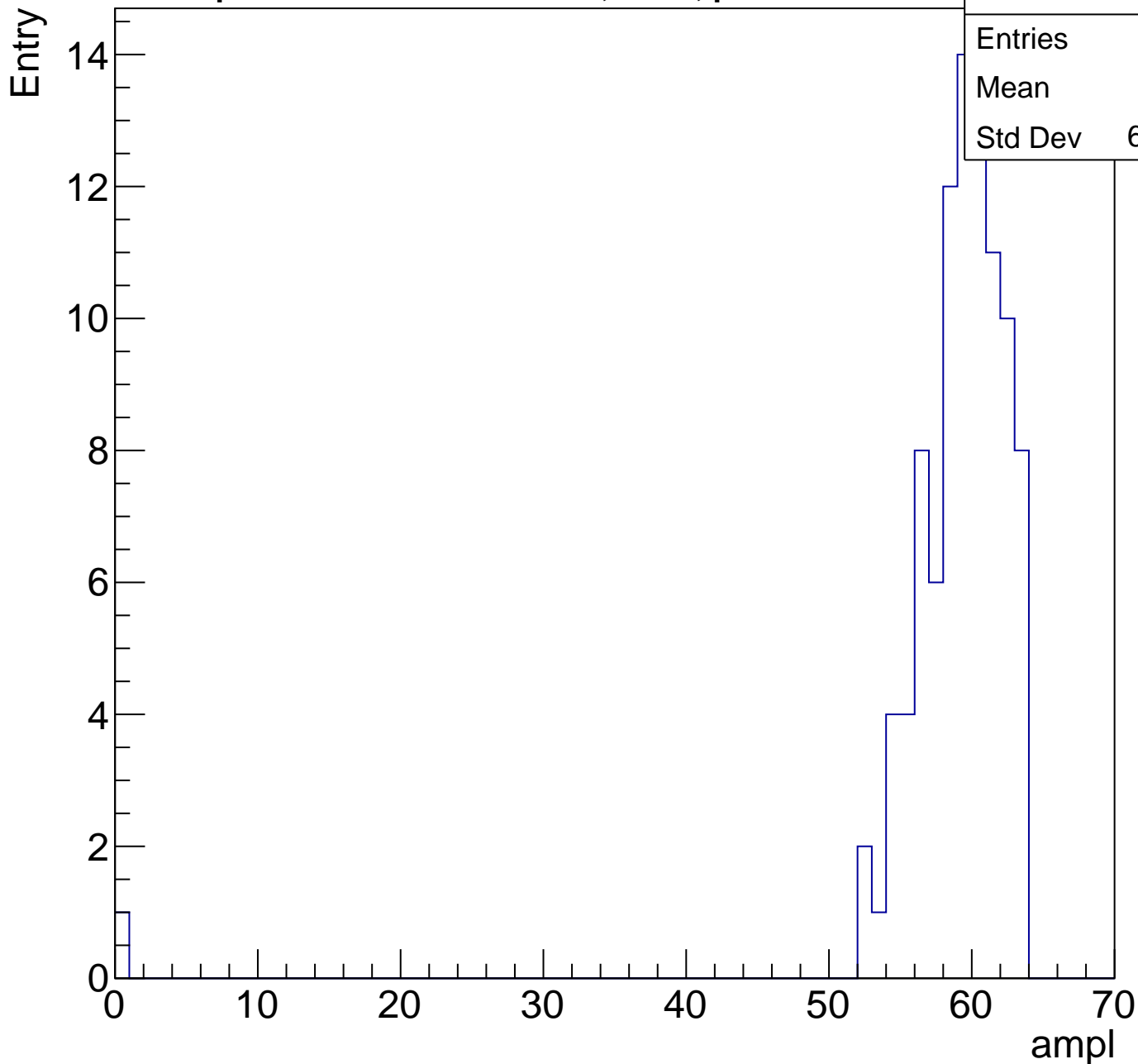
Entries	120
Mean	53.94
Std Dev	6.14



B0L101S, U18-ch108, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	58.3
Std Dev	6.617

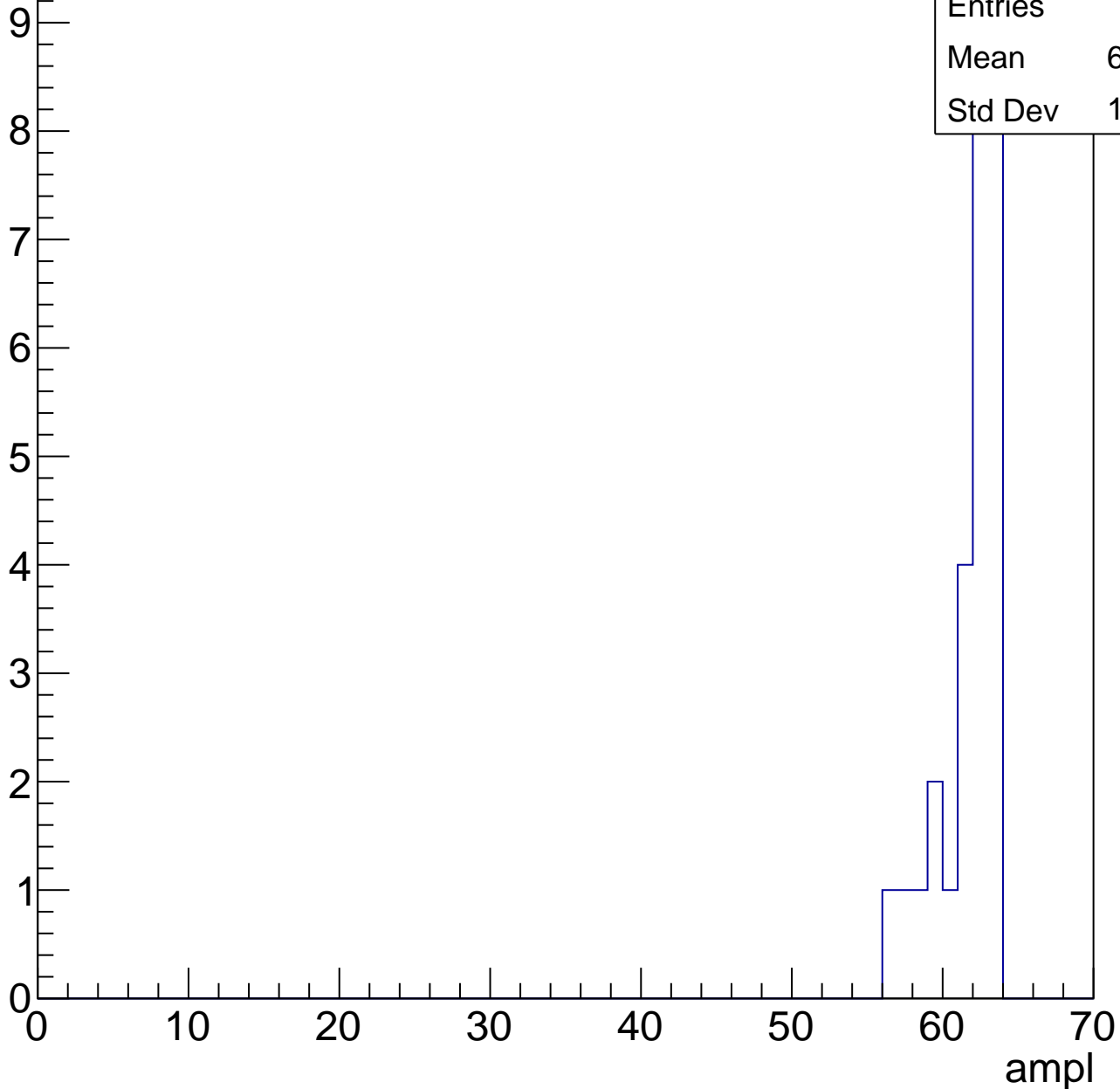


B0L101S, U18-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

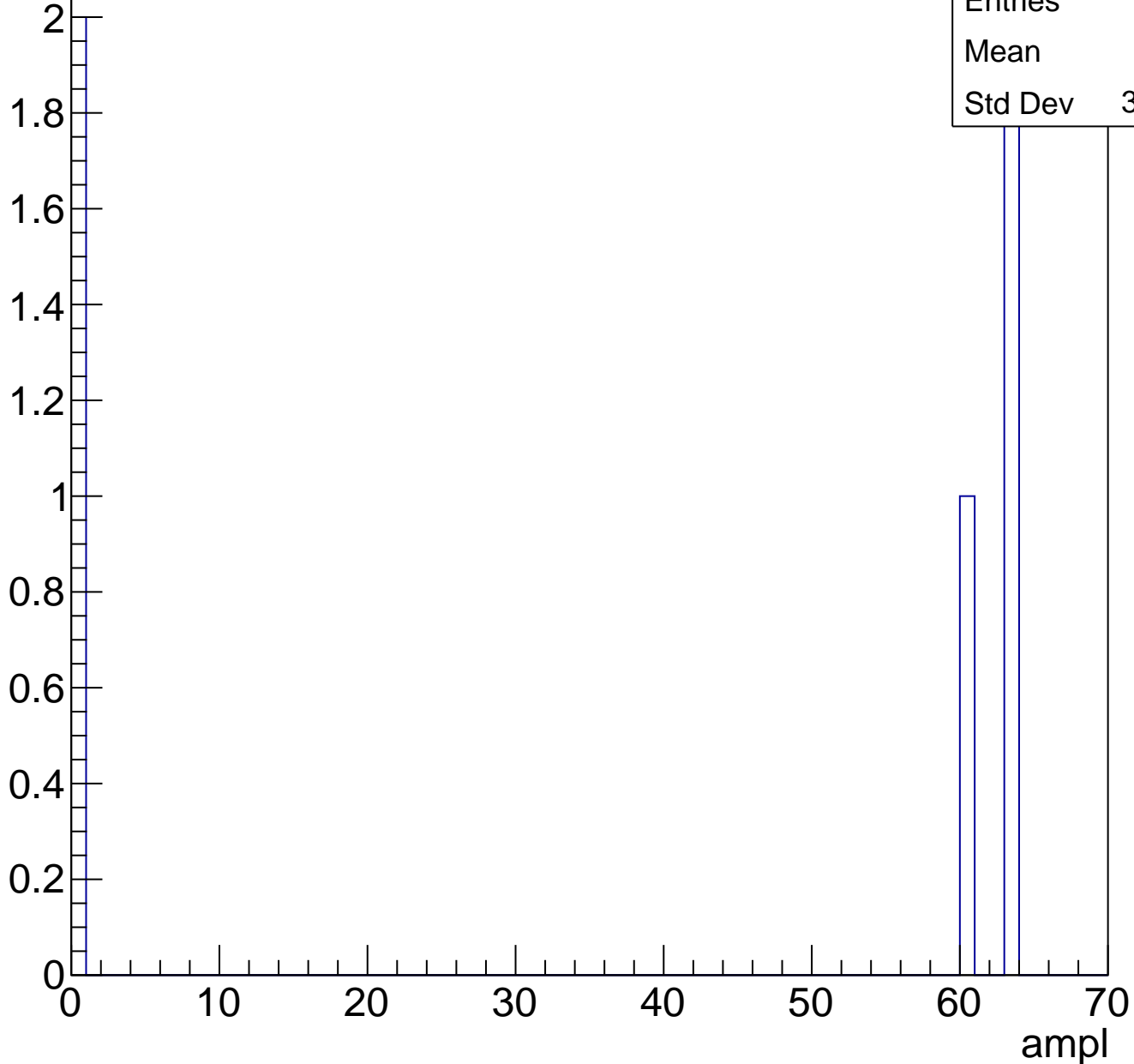
Entries	28
Mean	61.36
Std Dev	1.894



B0L101S, U18-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch109, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.95
Std Dev	5.057

Gaus mean : 30.8796

Gaus Width: 3.1771

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

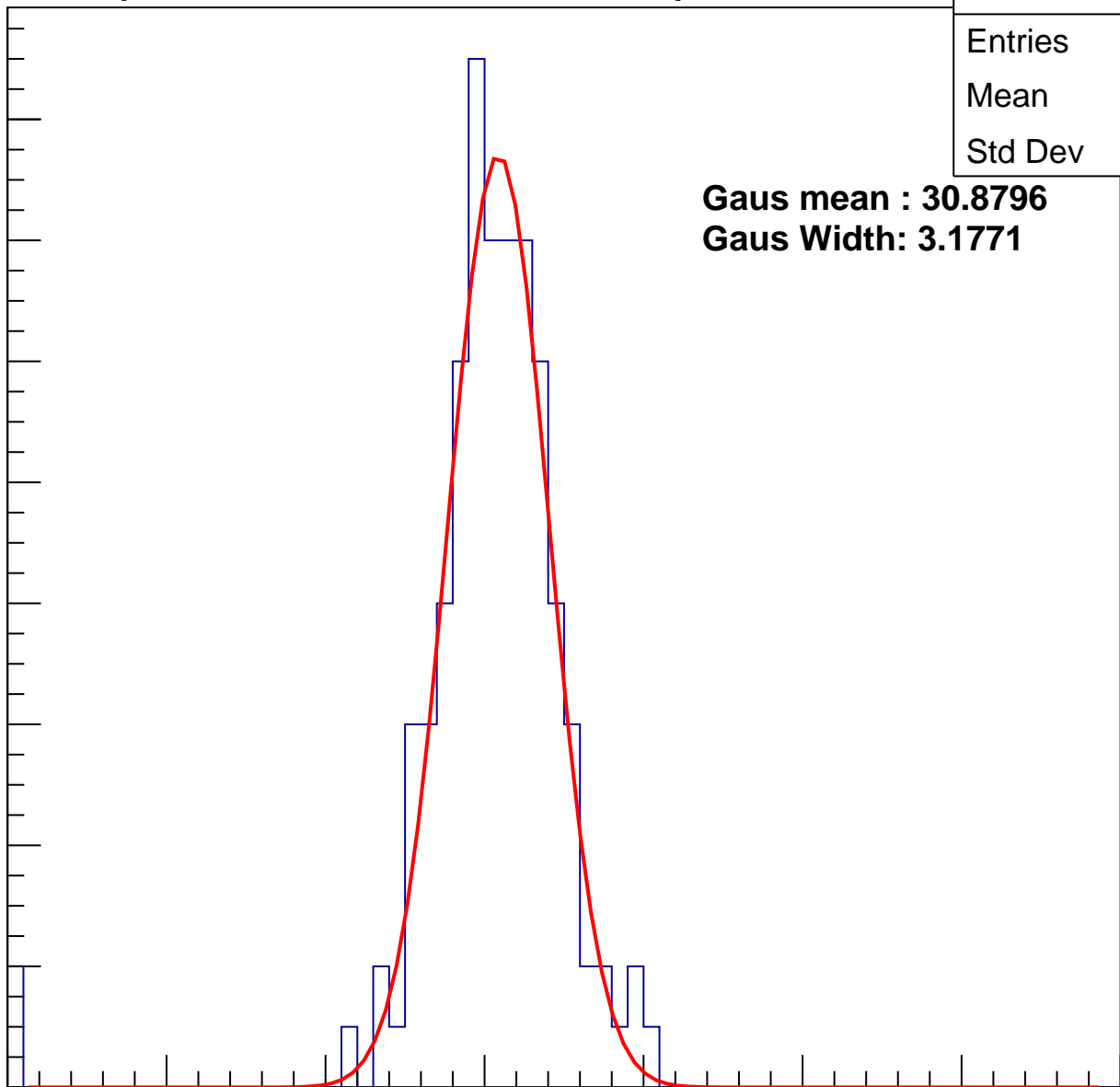
40

50

60

70

ampl



B0L101S, U18-ch109, adc1

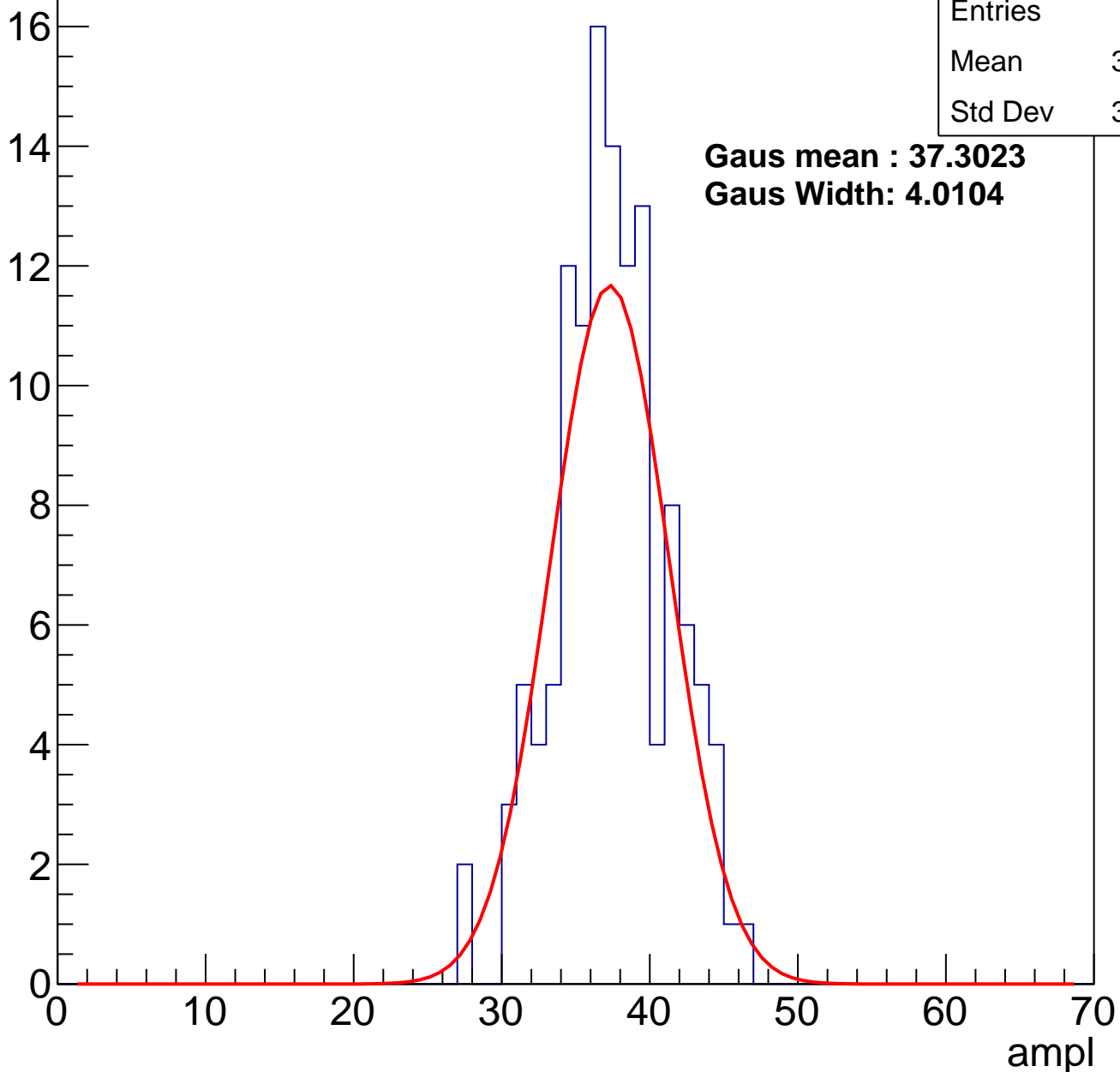
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	37.02
Std Dev	3.752

Gaus mean : 37.3023

Gaus Width: 4.0104

Entry



B0L101S, U18-ch109, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

138

Mean

43.1

Std Dev

3.832

Gaus mean : 43.6226

Gaus Width: 4.0194

0

10

20

30

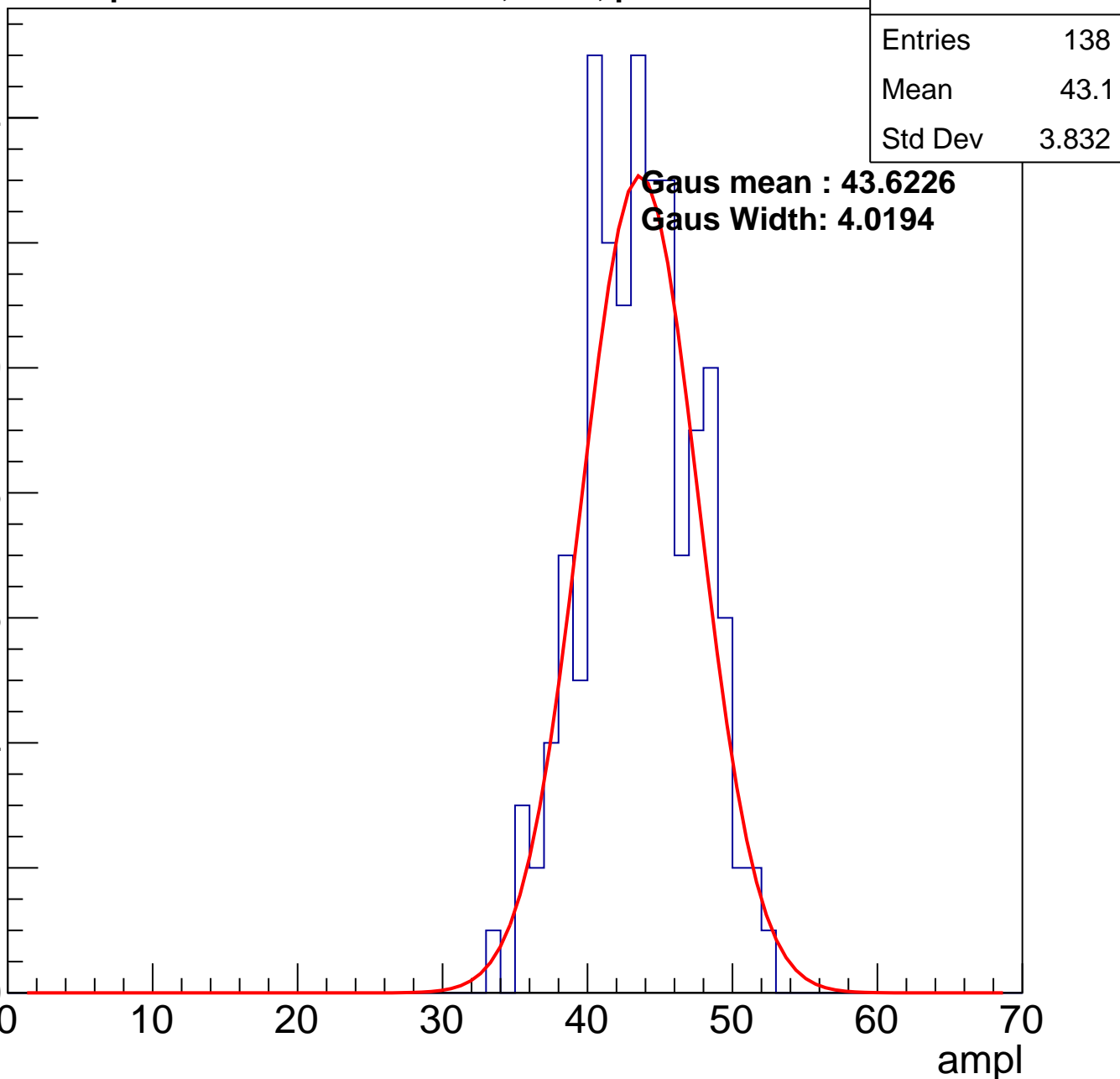
40

50

60

70

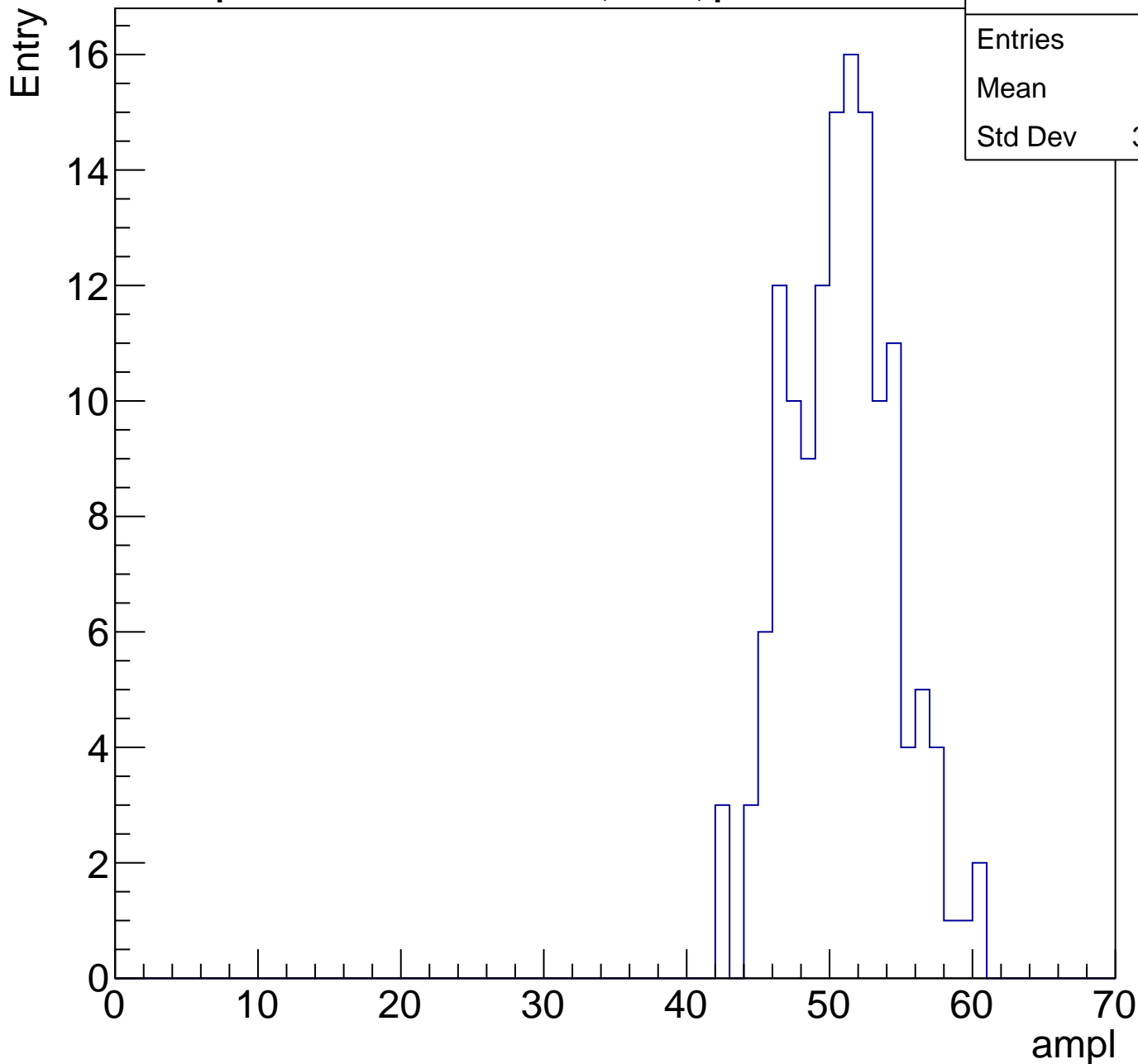
ampl



B0L101S, U18-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	50.4
Std Dev	3.718



B0L101S, U18-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

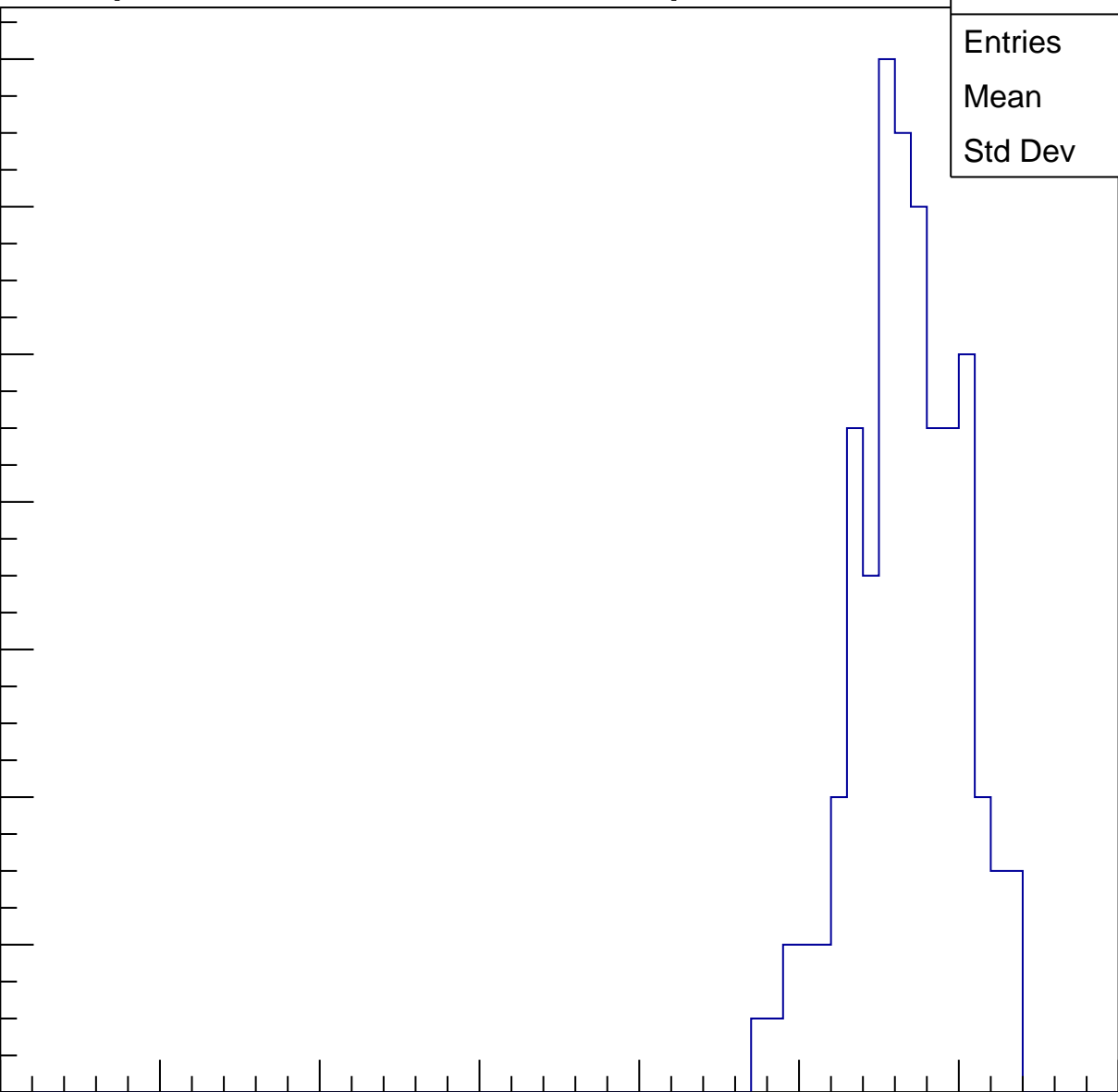
Entries	105
Mean	56.3
Std Dev	3.361

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch109, adc5

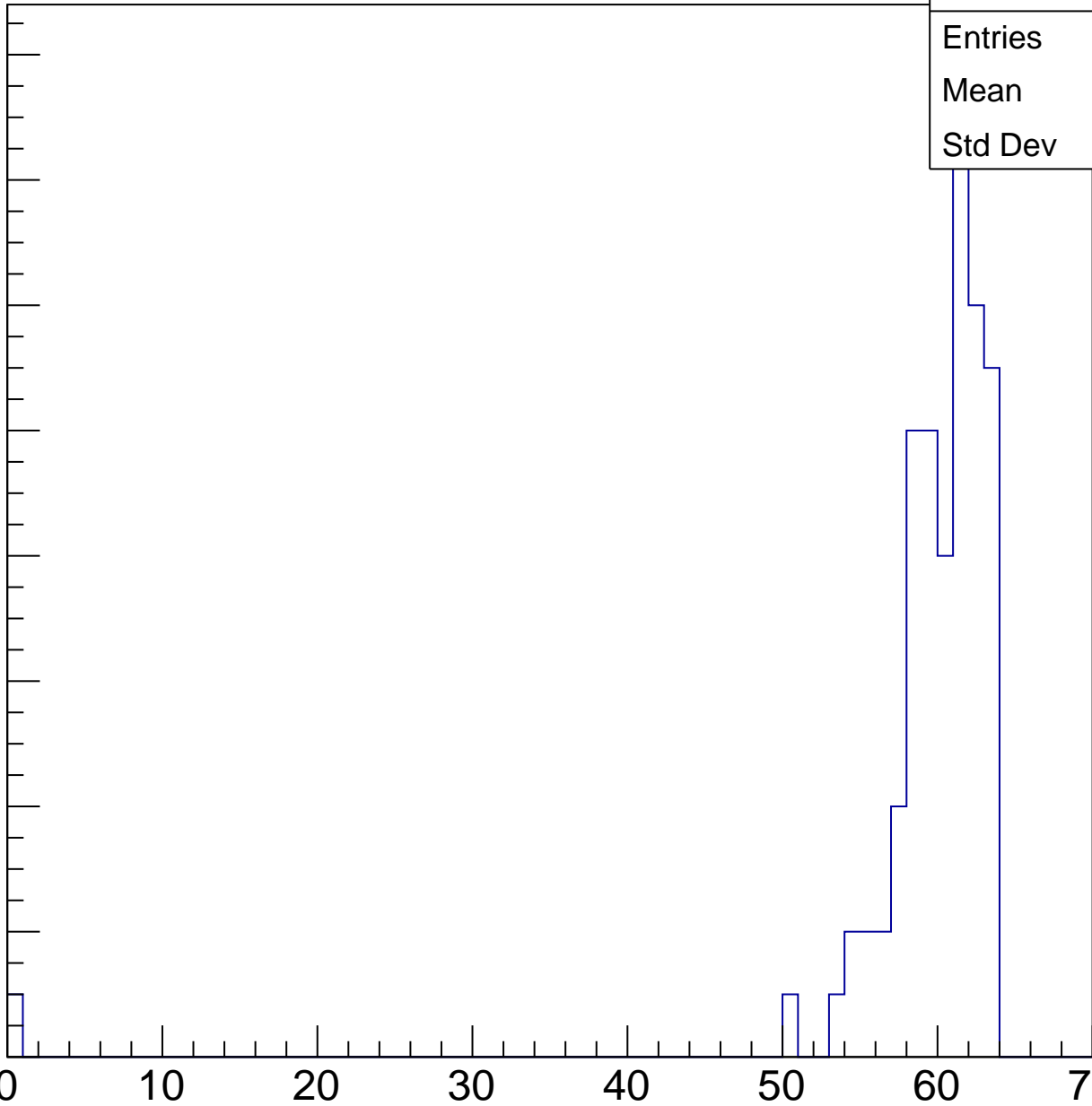
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.05
Std Dev	7.141

ampl

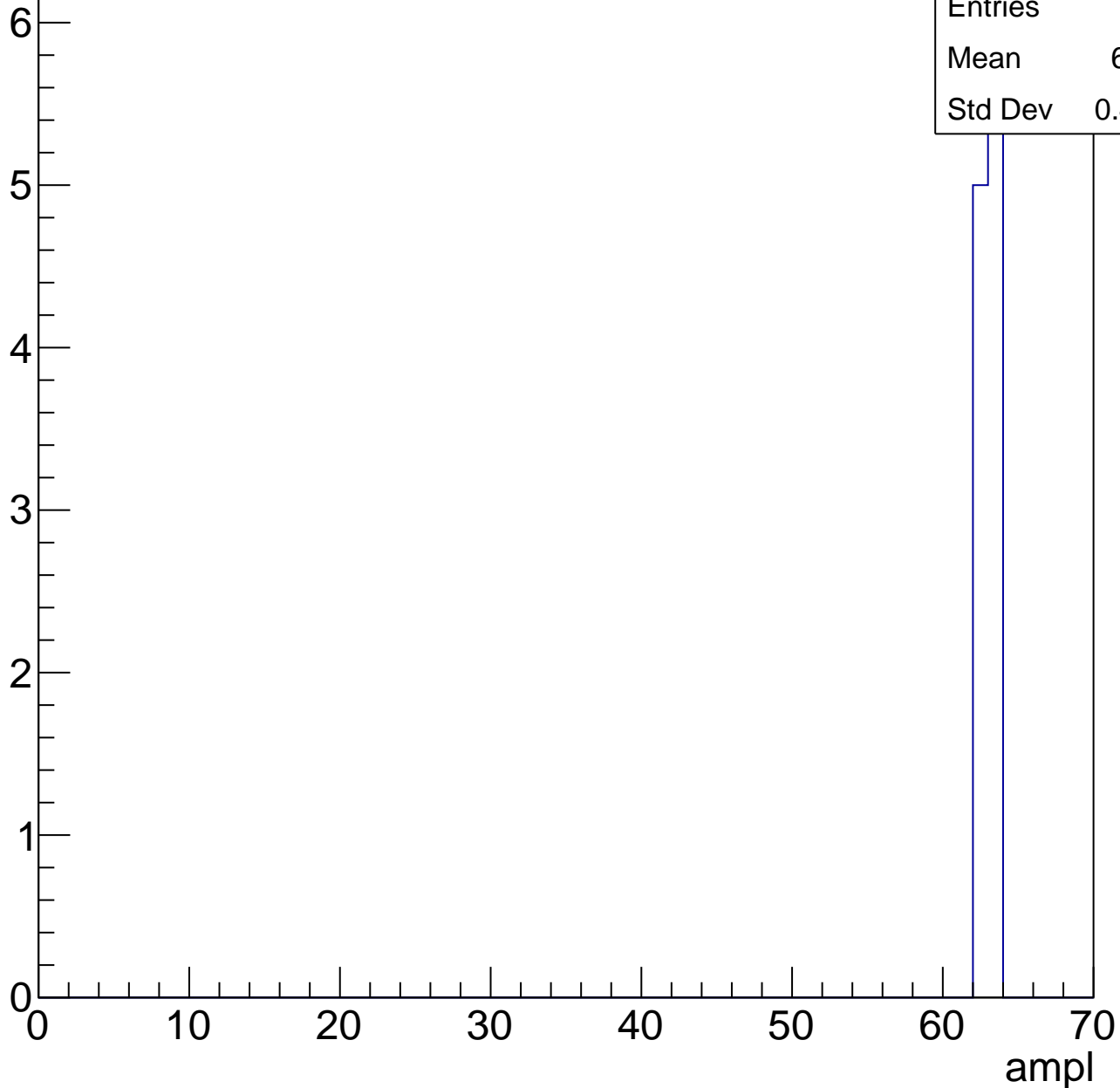


B0L101S, U18-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62.55
Std Dev	0.4979



B0L101S, U18-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

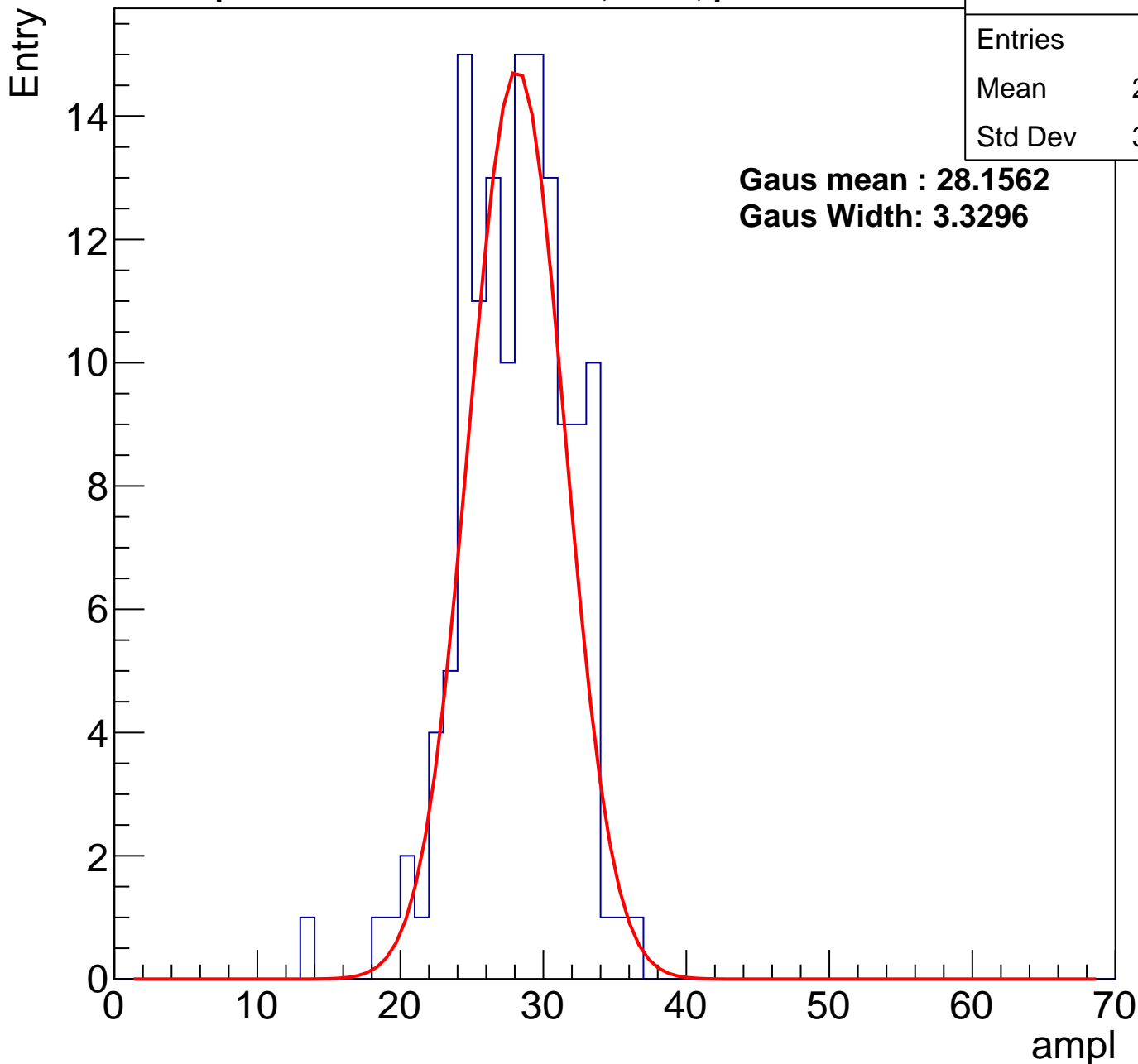
B0L101S, U18-ch110, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	27.57
Std Dev	3.728

Gaus mean : 28.1562

Gaus Width: 3.3296



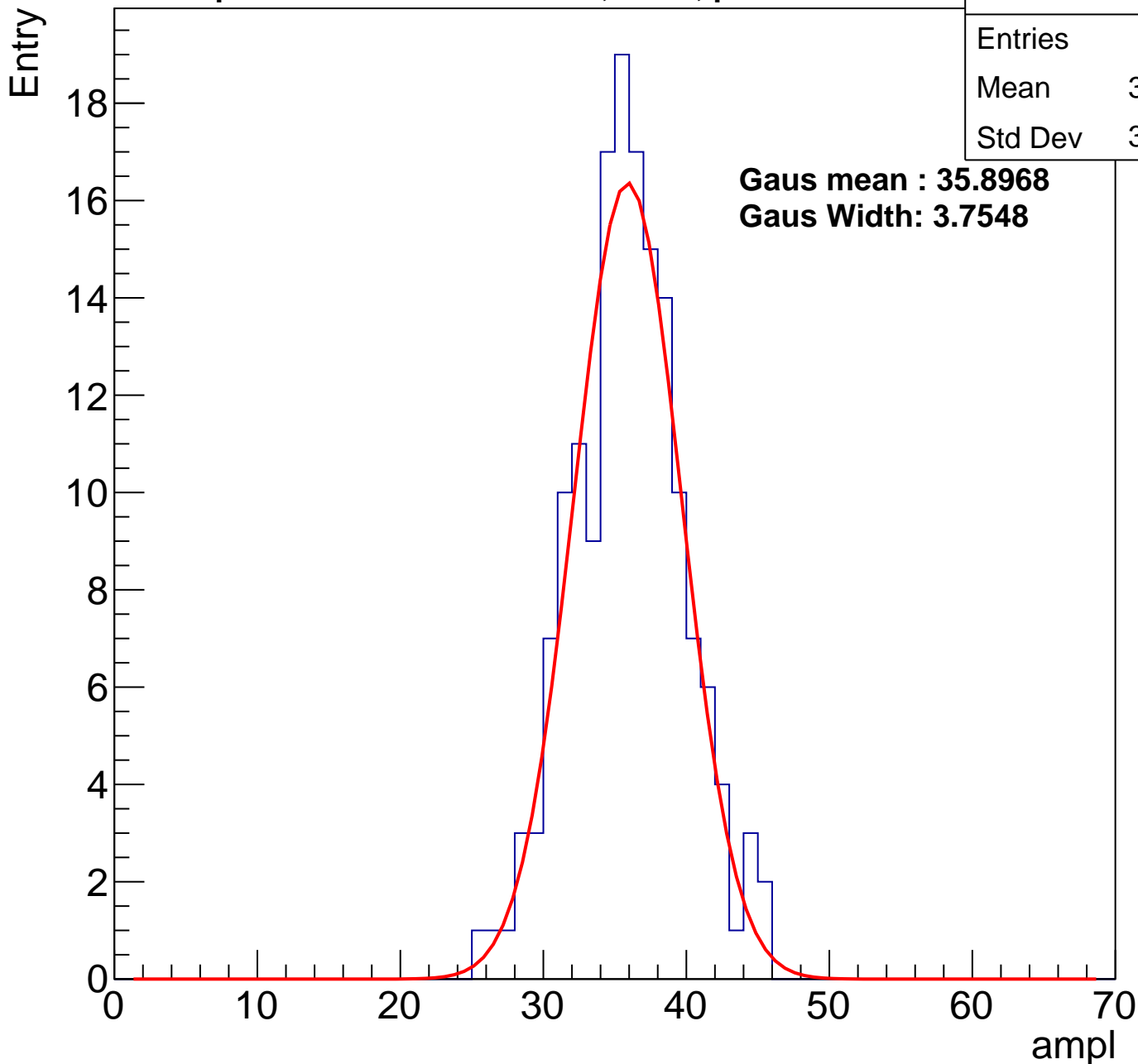
B0L101S, U18-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	35.46
Std Dev	3.869

Gaus mean : 35.8968

Gaus Width: 3.7548



B0L101S, U18-ch110, adc2

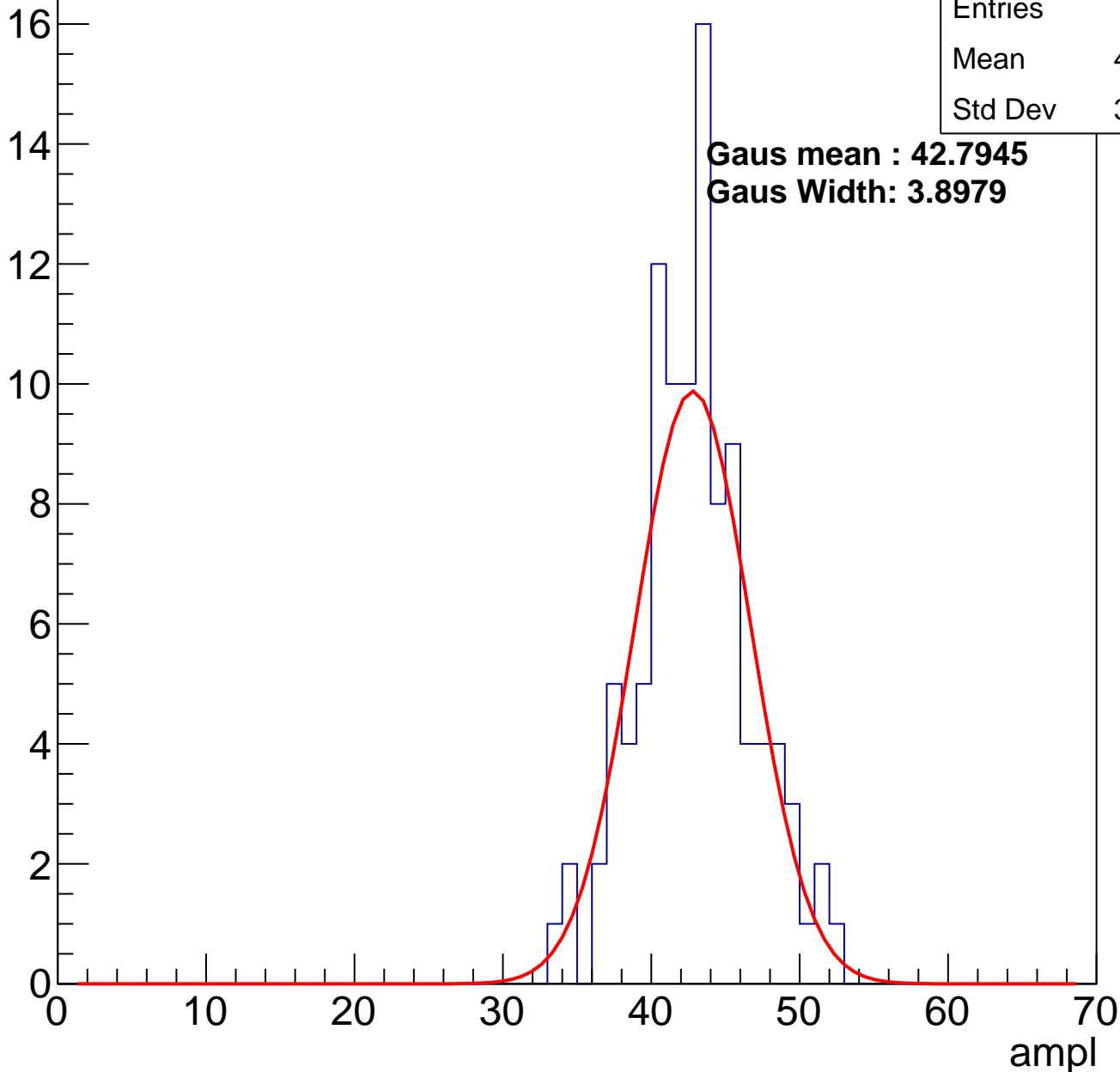
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	42.48
Std Dev	3.765

Gaus mean : 42.7945

Gaus Width: 3.8979

Entry



B0L101S, U18-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

157

Mean

48.17

Std Dev

4.313

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch110, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	54.52
Std Dev	3.577

Entry

12

10

8

6

4

2

0

0

10

20

30

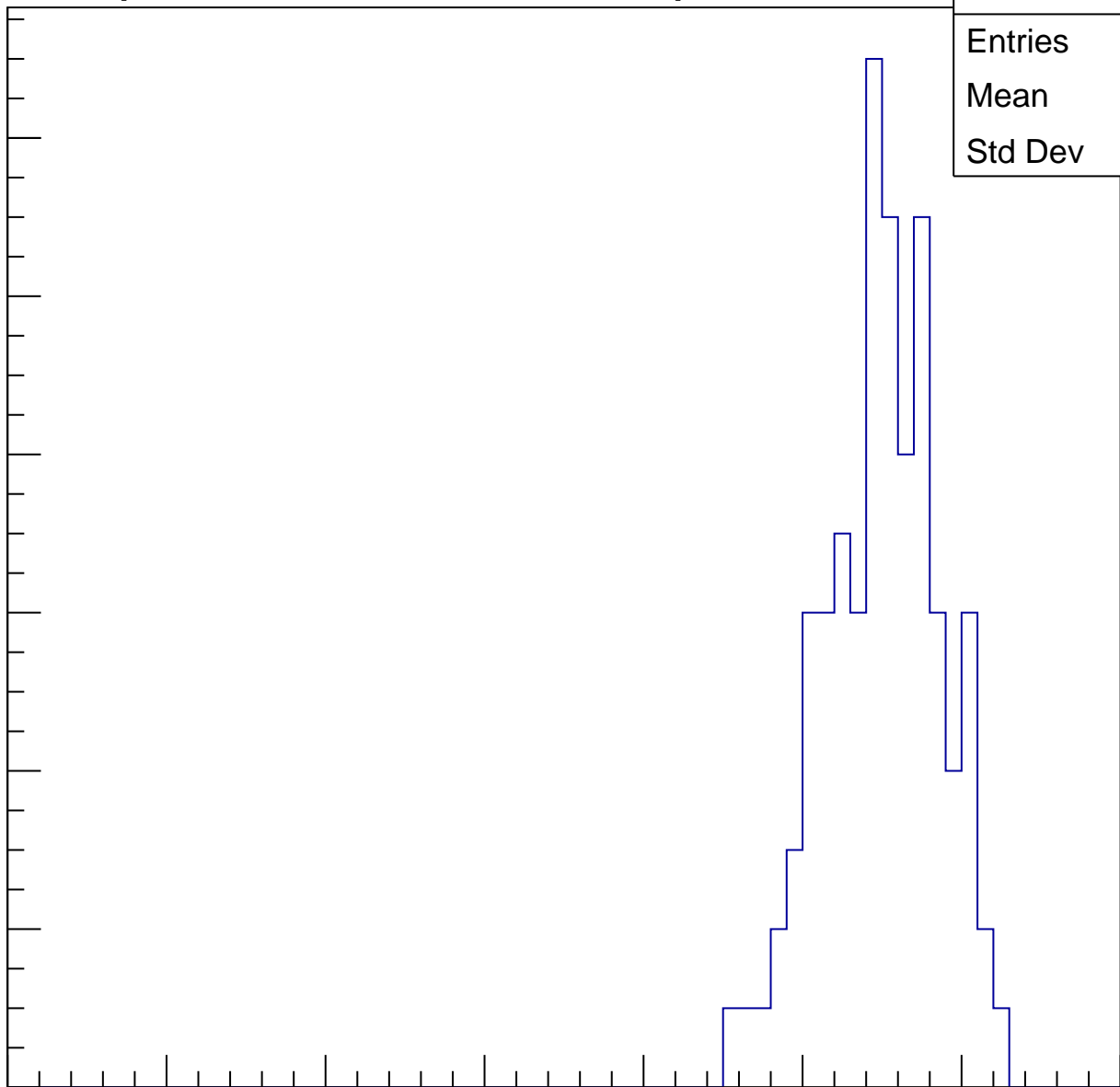
40

50

60

70

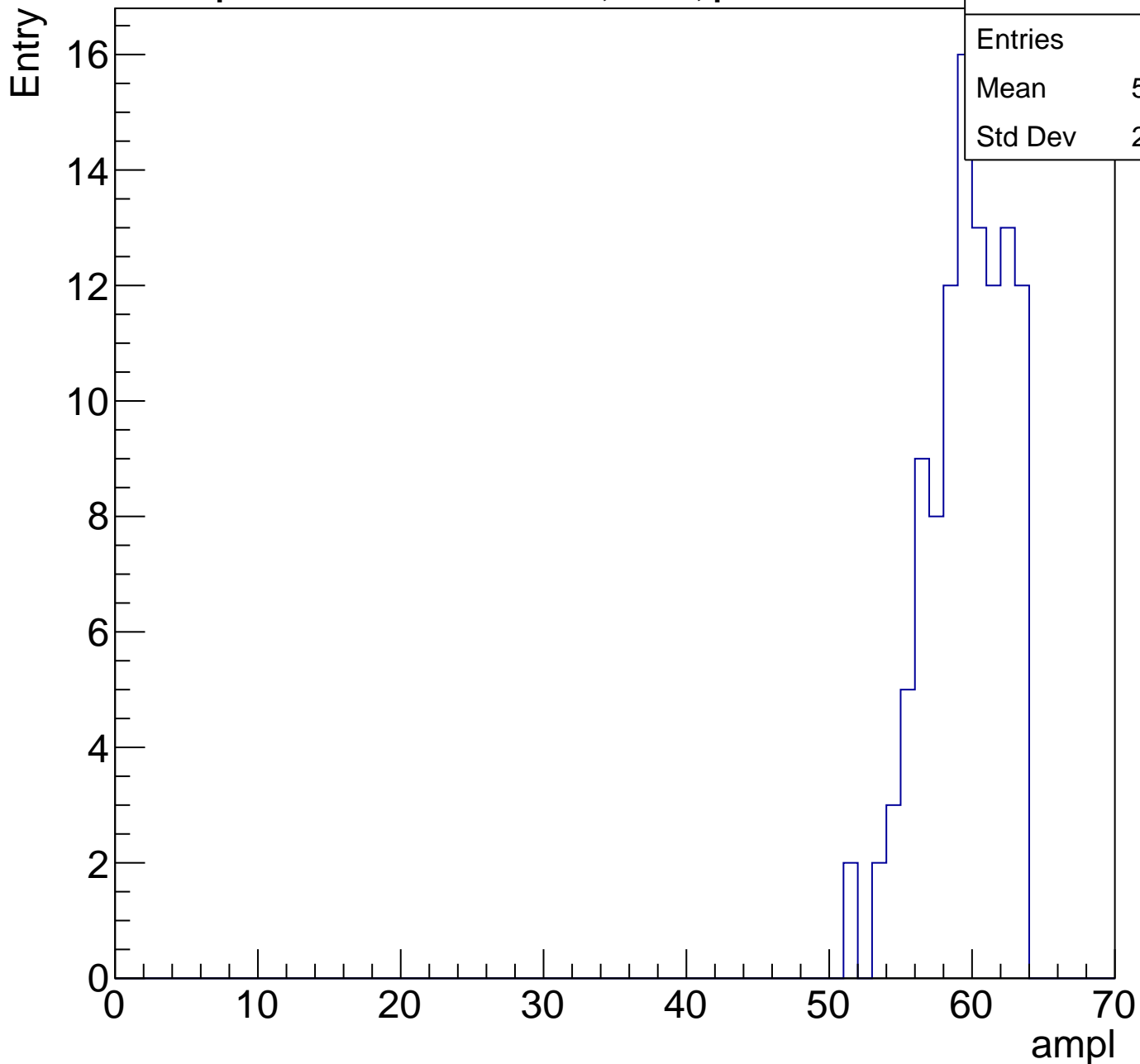
ampl



B0L101S, U18-ch110, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	59.06
Std Dev	2.818

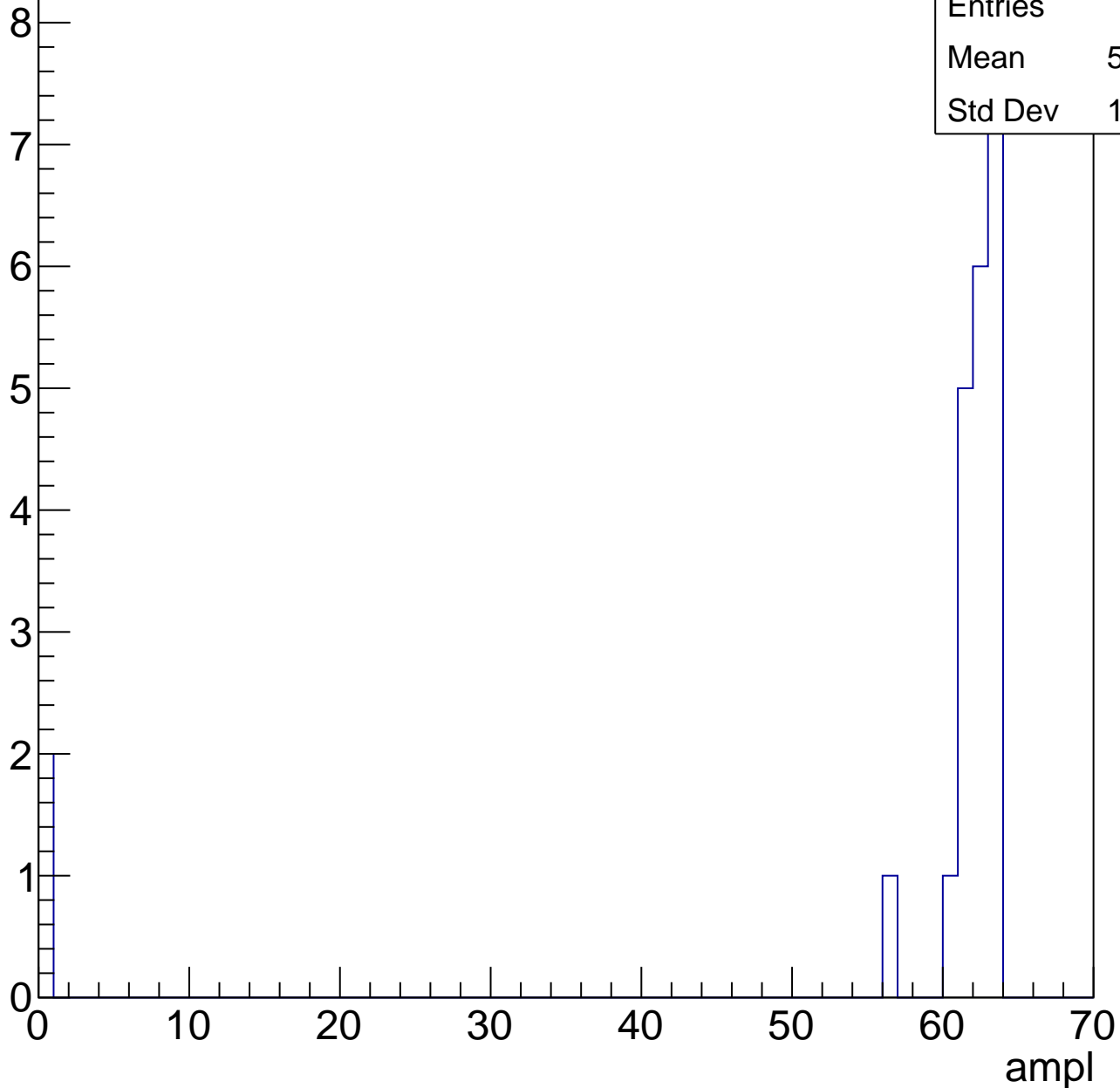


B0L101S, U18-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

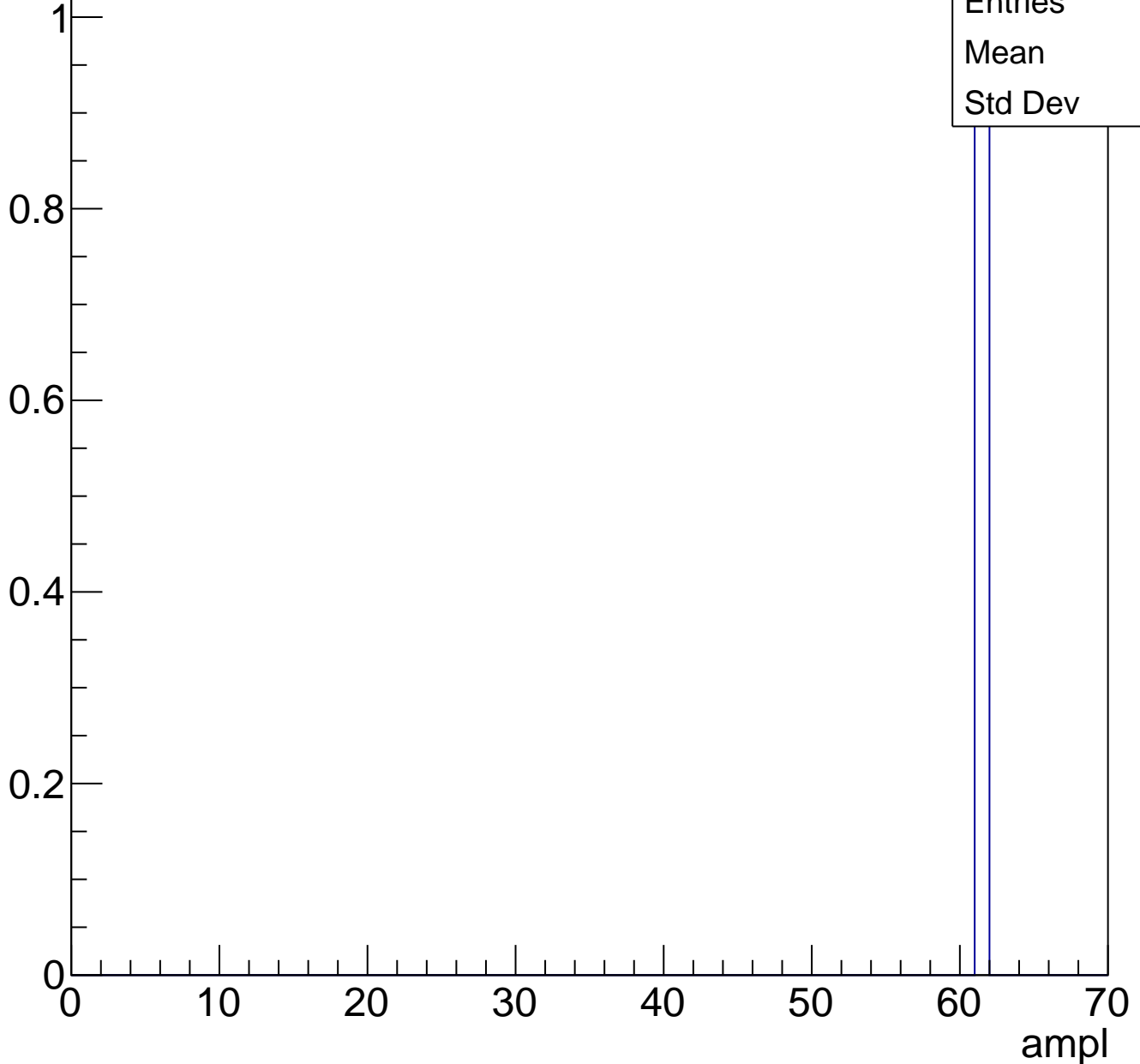
Entries	23
Mean	56.39
Std Dev	17.47



B0L101S, U18-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch111, adc0

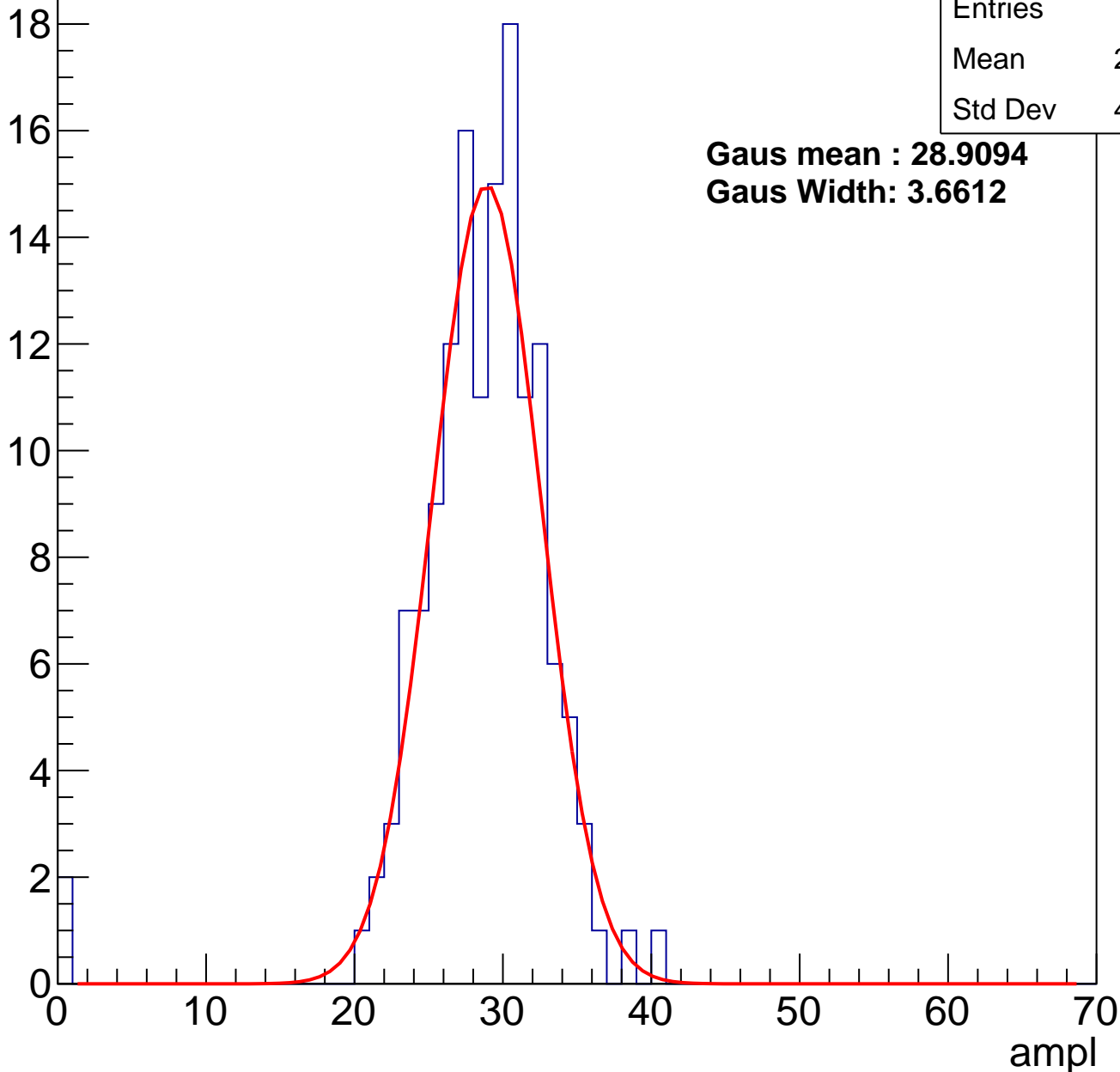
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	28.12
Std Dev	4.898

Gaus mean : 28.9094

Gaus Width: 3.6612

Entry



B0L101S, U18-ch111, adc1

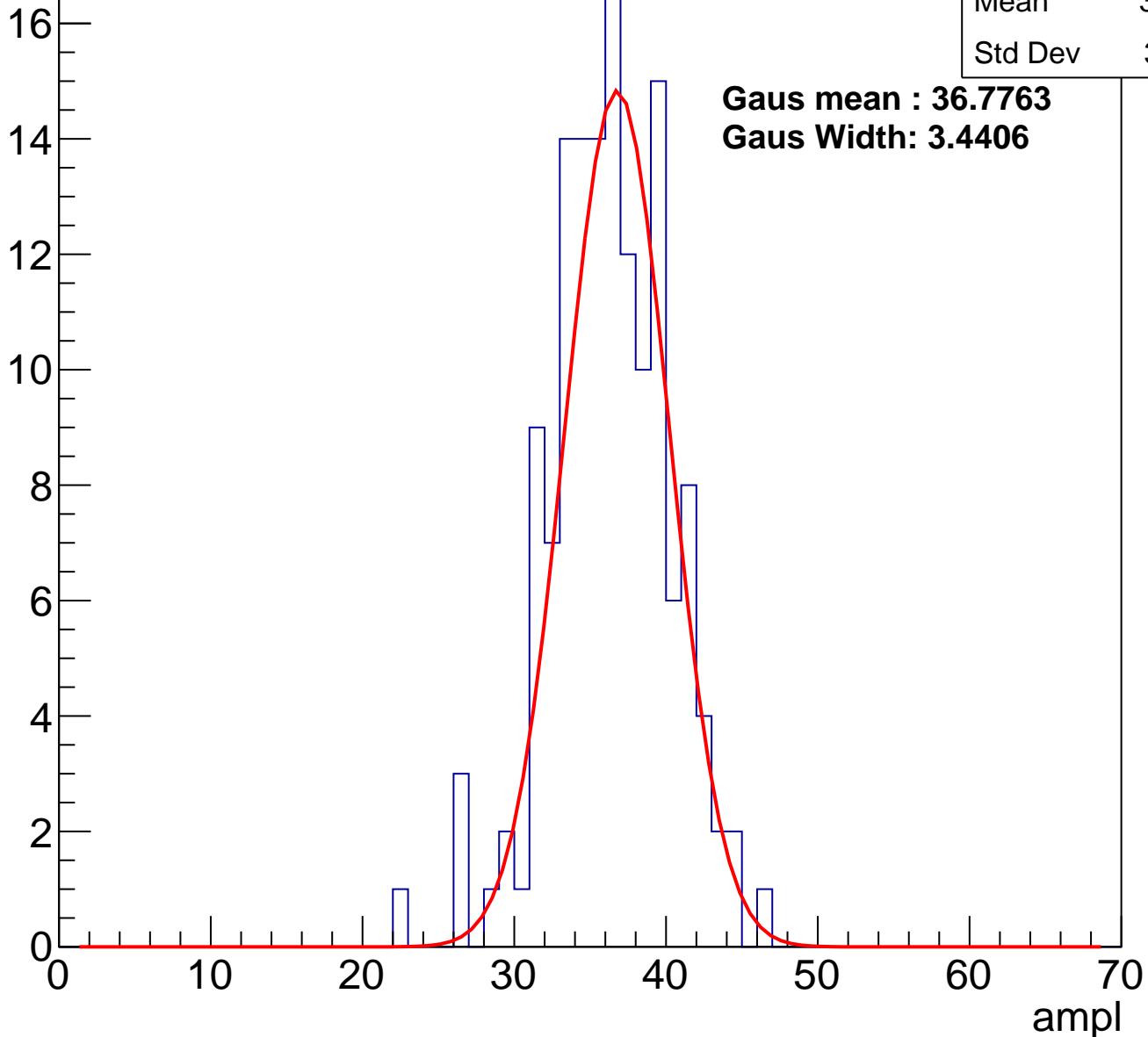
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	35.83
Std Dev	3.901

Gaus mean : 36.7763

Gaus Width: 3.4406

Entry



B0L101S, U18-ch111, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

122

Mean

42.13

Std Dev

3.68

Gaus mean : 42.7888

Gaus Width: 3.6829

0

10

20

30

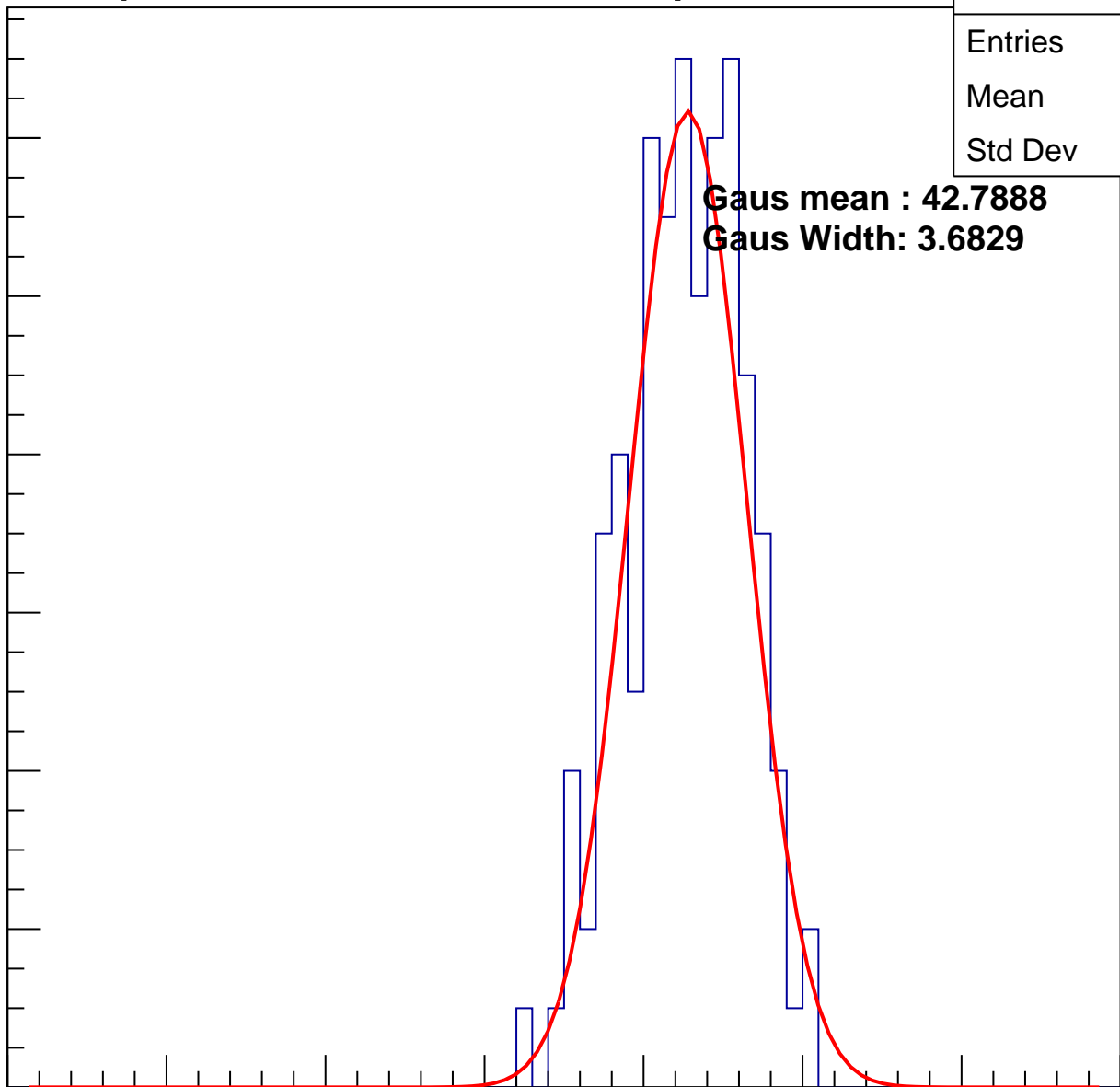
40

50

60

70

ampl



B0L101S, U18-ch111, adc3

calib_packv5_042523_0143.root, FC#1, port C1

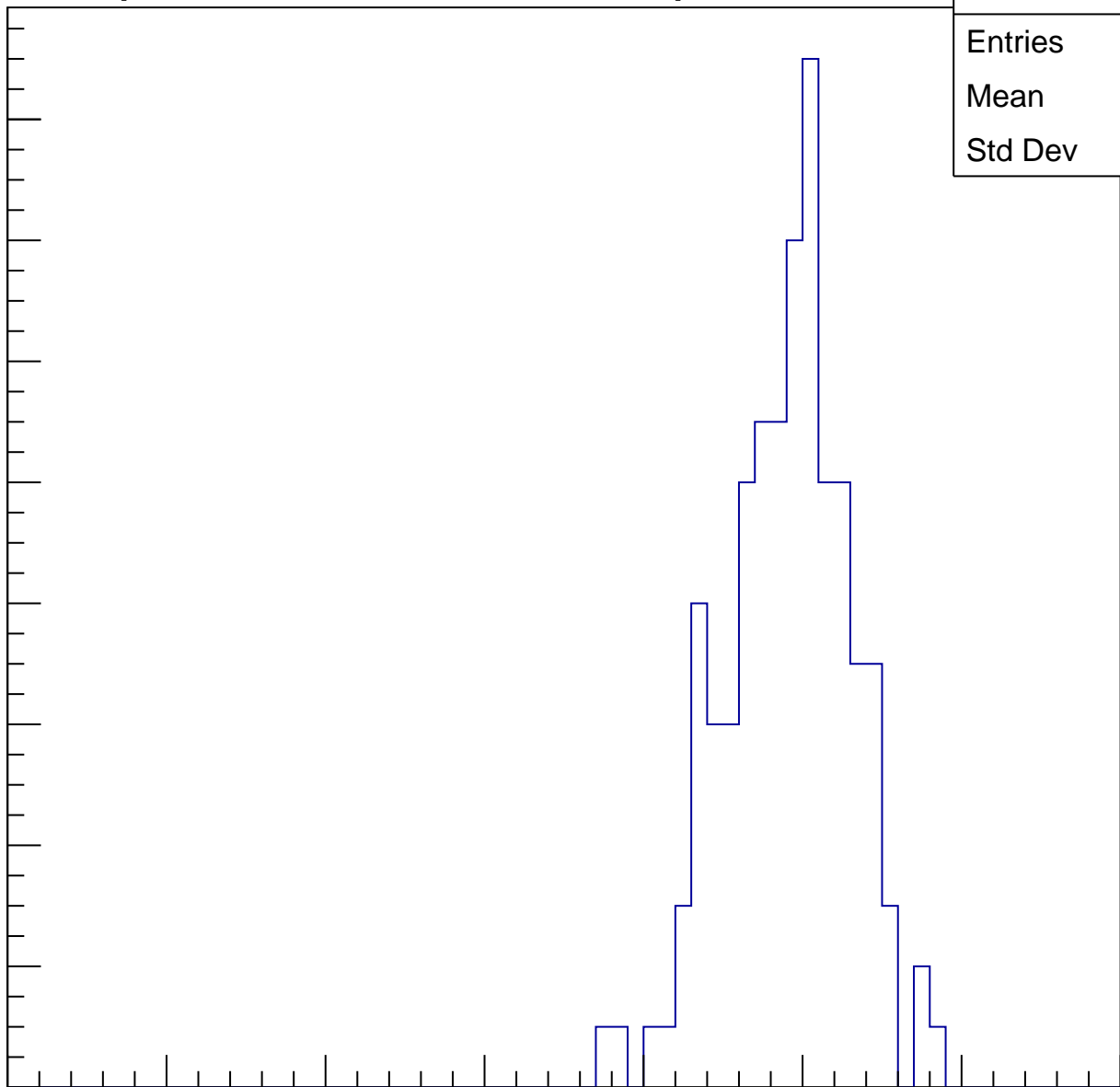
Entries	130
Mean	48.59
Std Dev	3.888

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

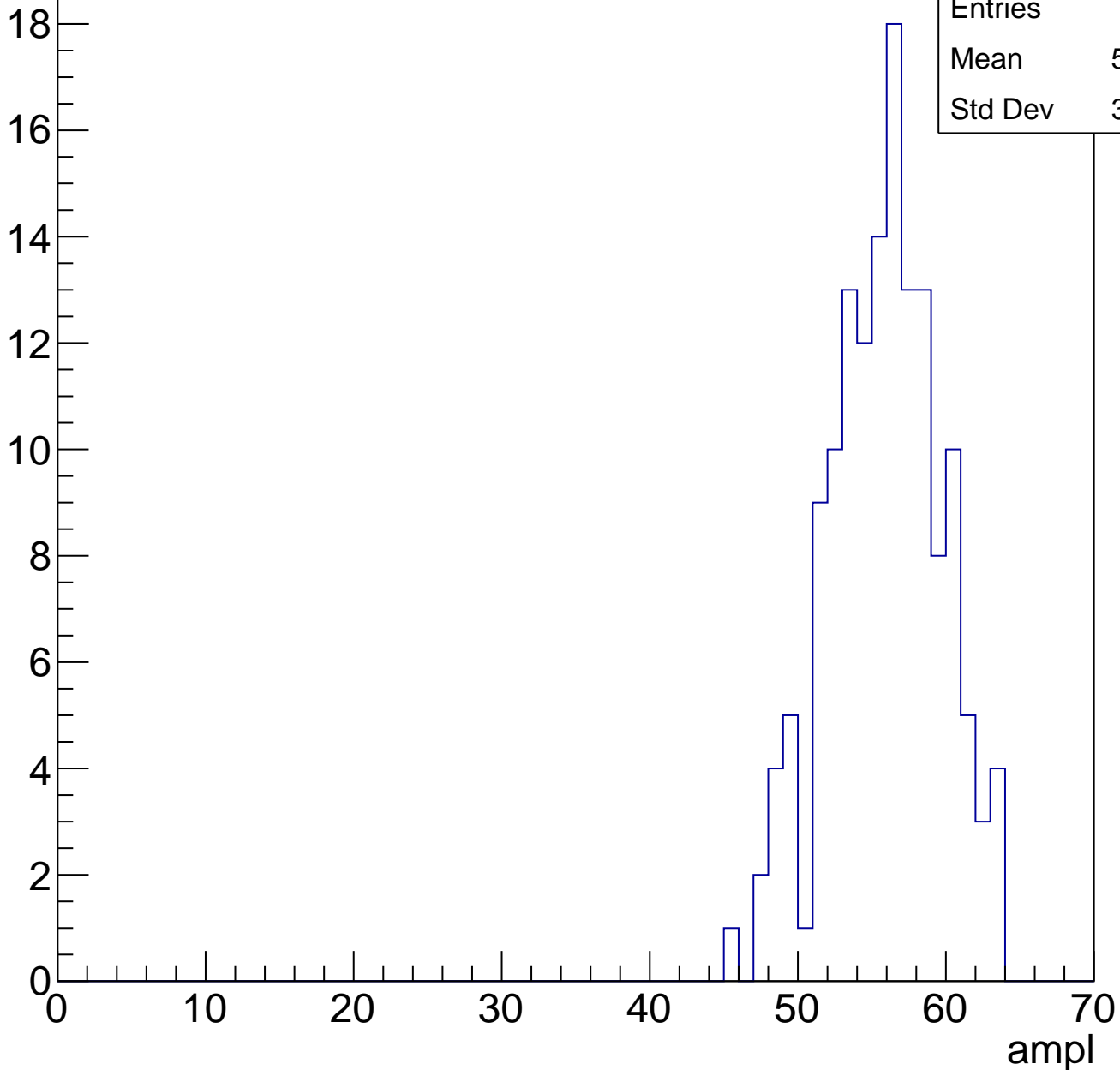
ampl



B0L101S, U18-ch111, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	145
Mean	55.38
Std Dev	3.764

B0L101S, U18-ch111, adc5

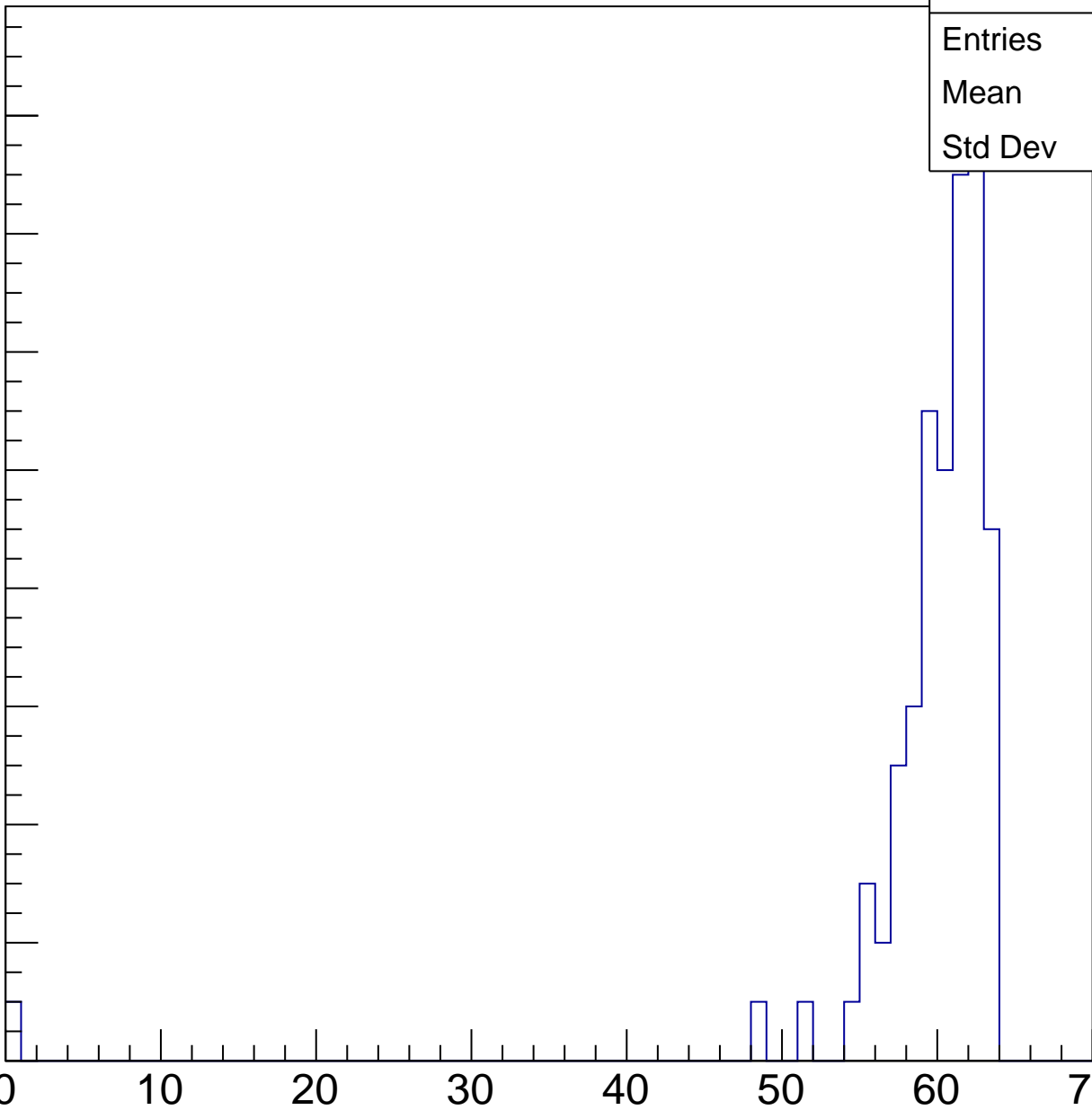
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	59.12
Std Dev	7.113

ampl

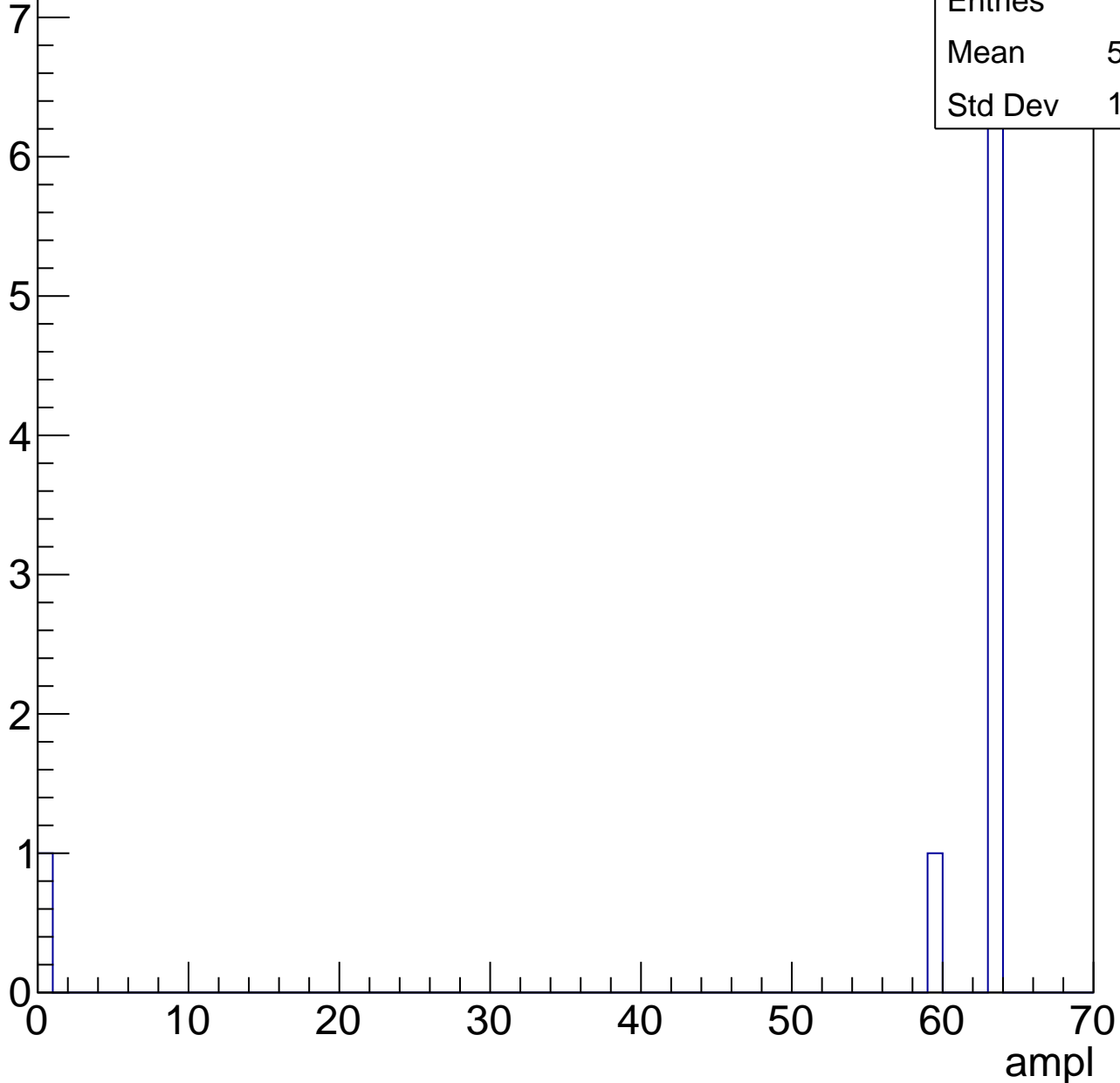


B0L101S, U18-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	55.56
Std Dev	19.68



B0L101S, U18-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

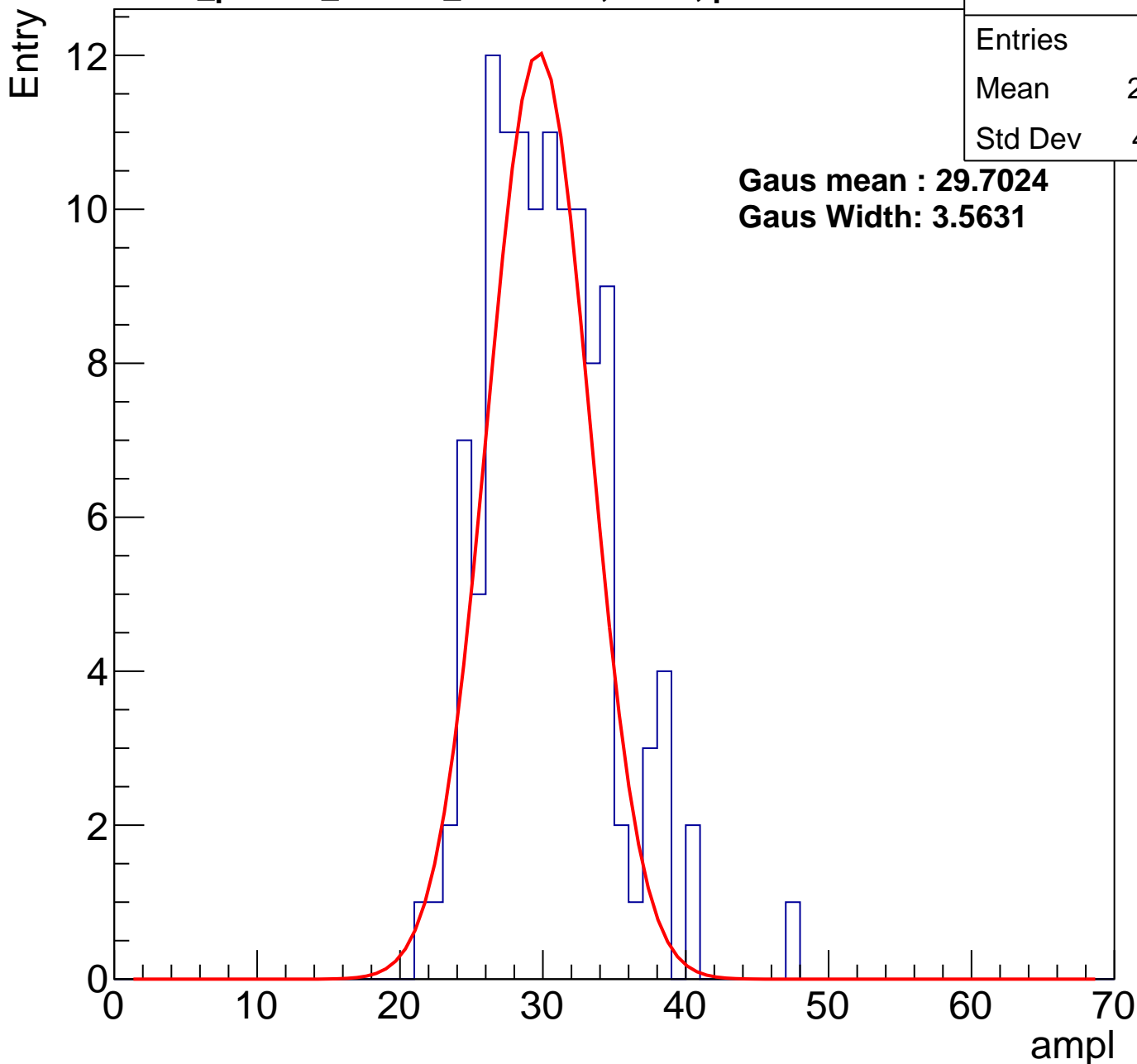
B0L101S, U18-ch112, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	29.88
Std Dev	4.271

Gaus mean : 29.7024

Gaus Width: 3.5631



B0L101S, U18-ch112, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	36.3
Std Dev	4.235

Gaus mean : 36.8602

Gaus Width: 4.2012

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

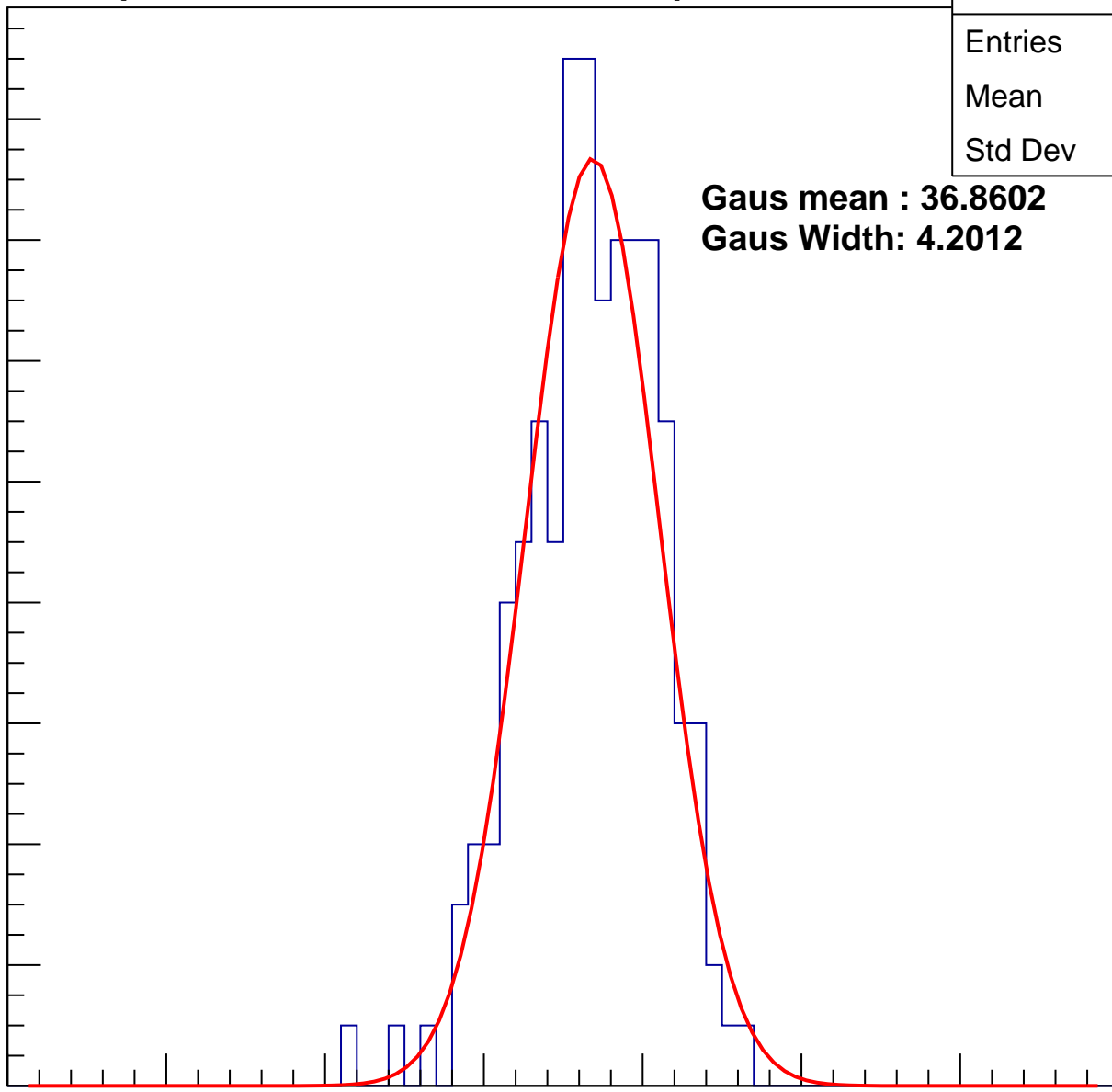
40

50

60

70

ampl



B0L101S, U18-ch112, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

84

Mean

43.1

Std Dev

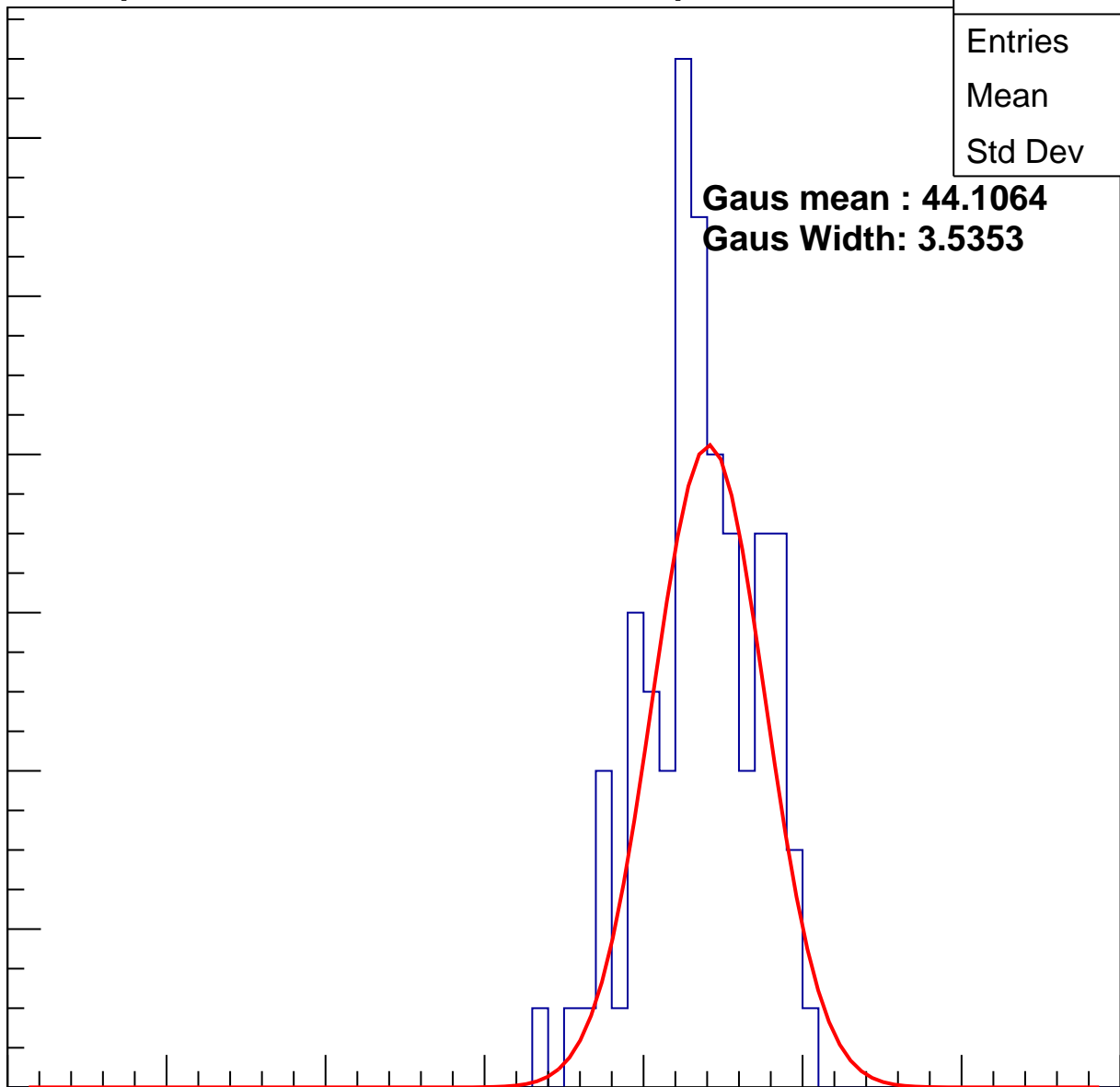
3.564

Gaus mean : 44.1064

Gaus Width: 3.5353

0 10 20 30 40 50 60 70

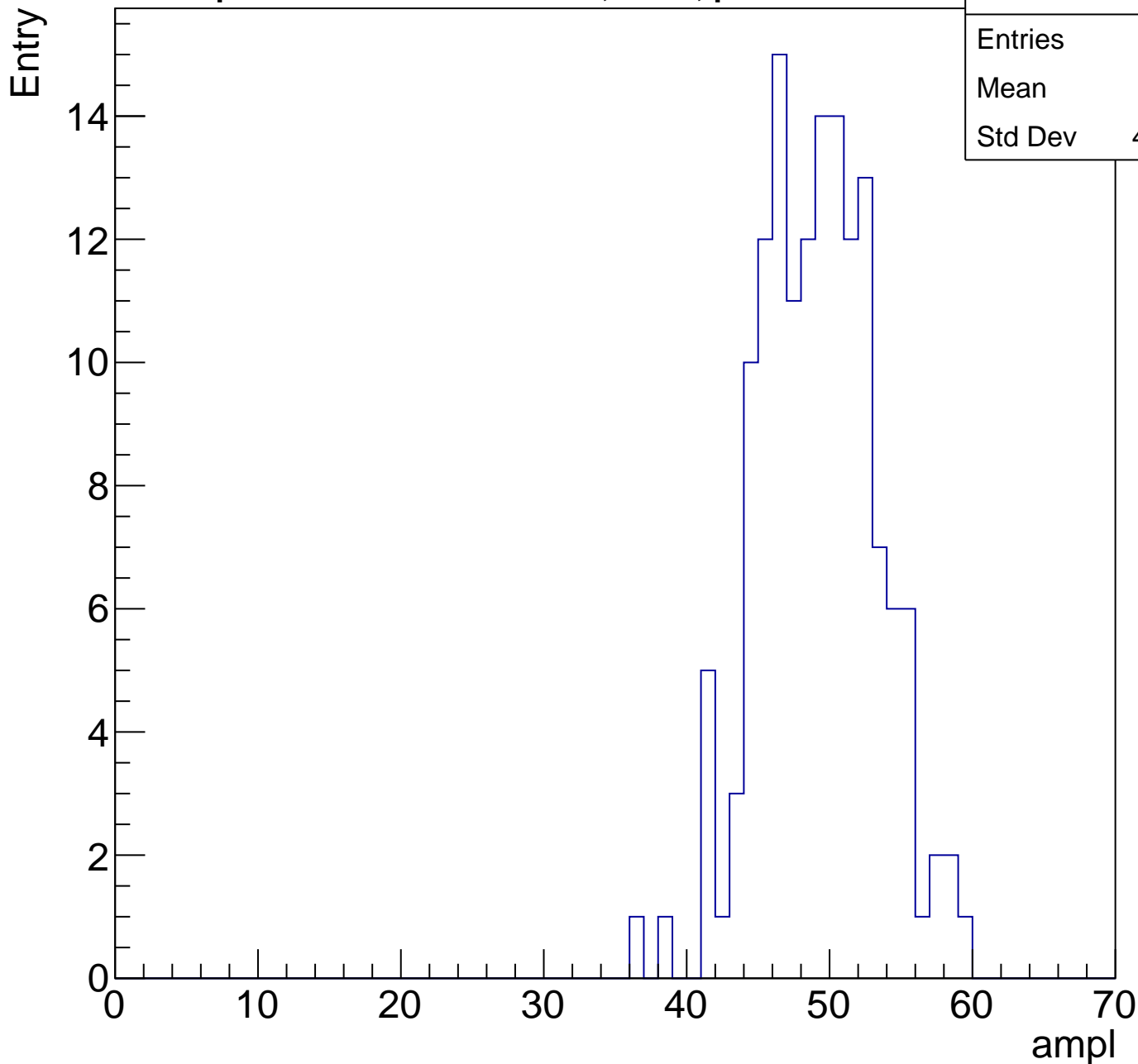
ampl



B0L101S, U18-ch112, adc3

calib_packv5_042523_0143.root, FC#1, port C1

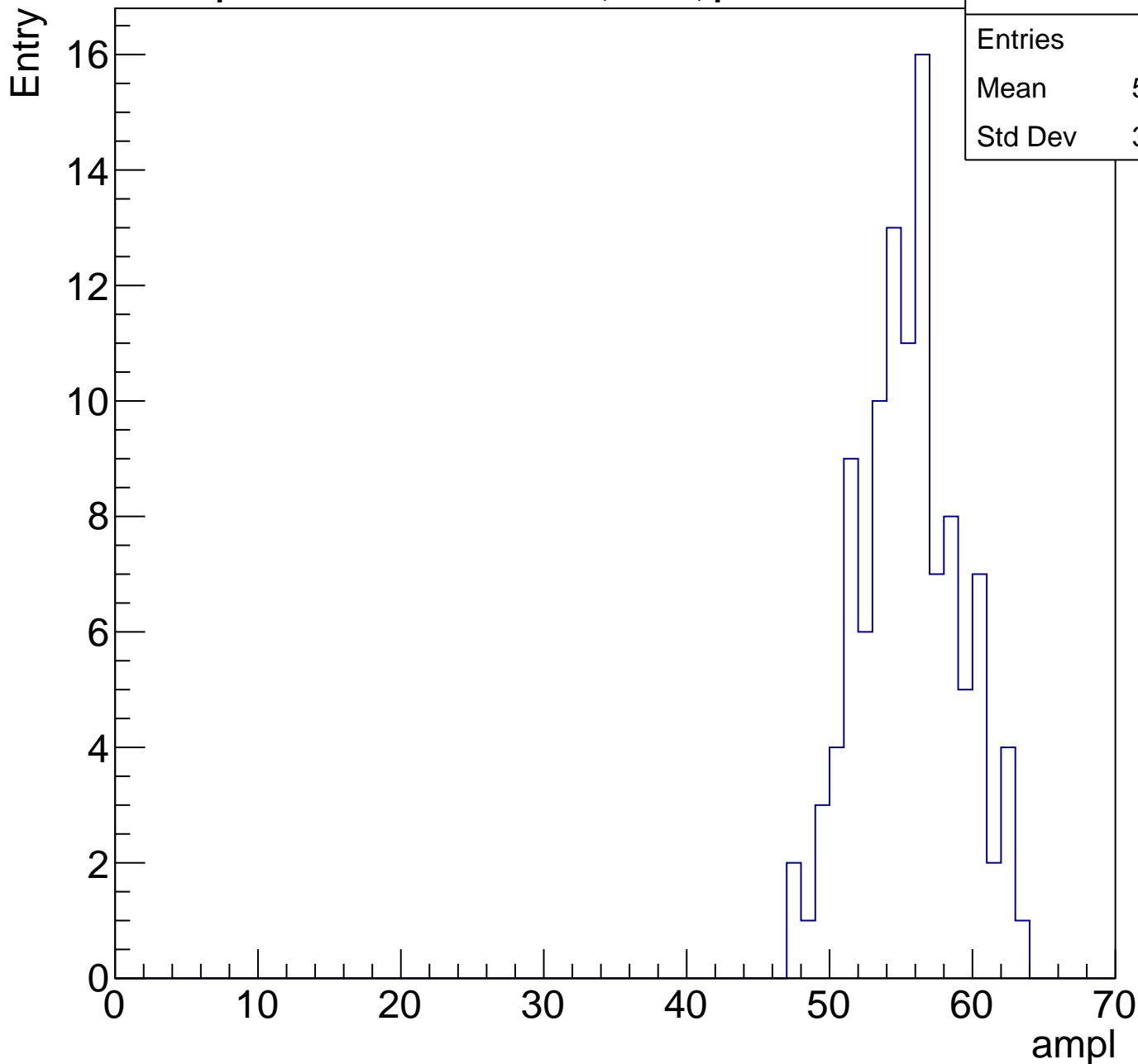
Entries	149
Mean	48.7
Std Dev	4.082



B0L101S, U18-ch112, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.08
Std Dev	3.504



B0L101S, U18-ch112, adc5

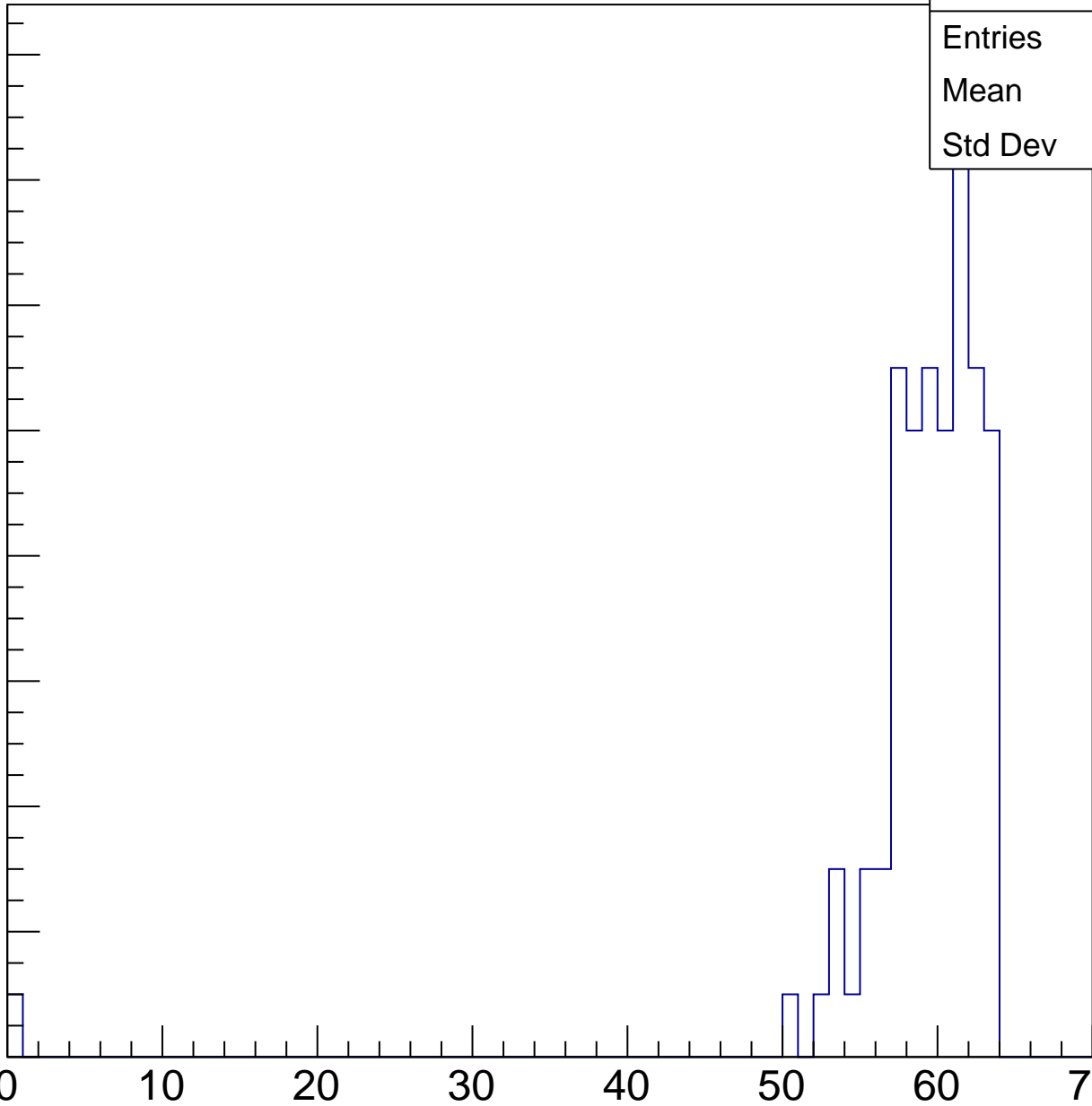
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.61
Std Dev	6.75

ampl

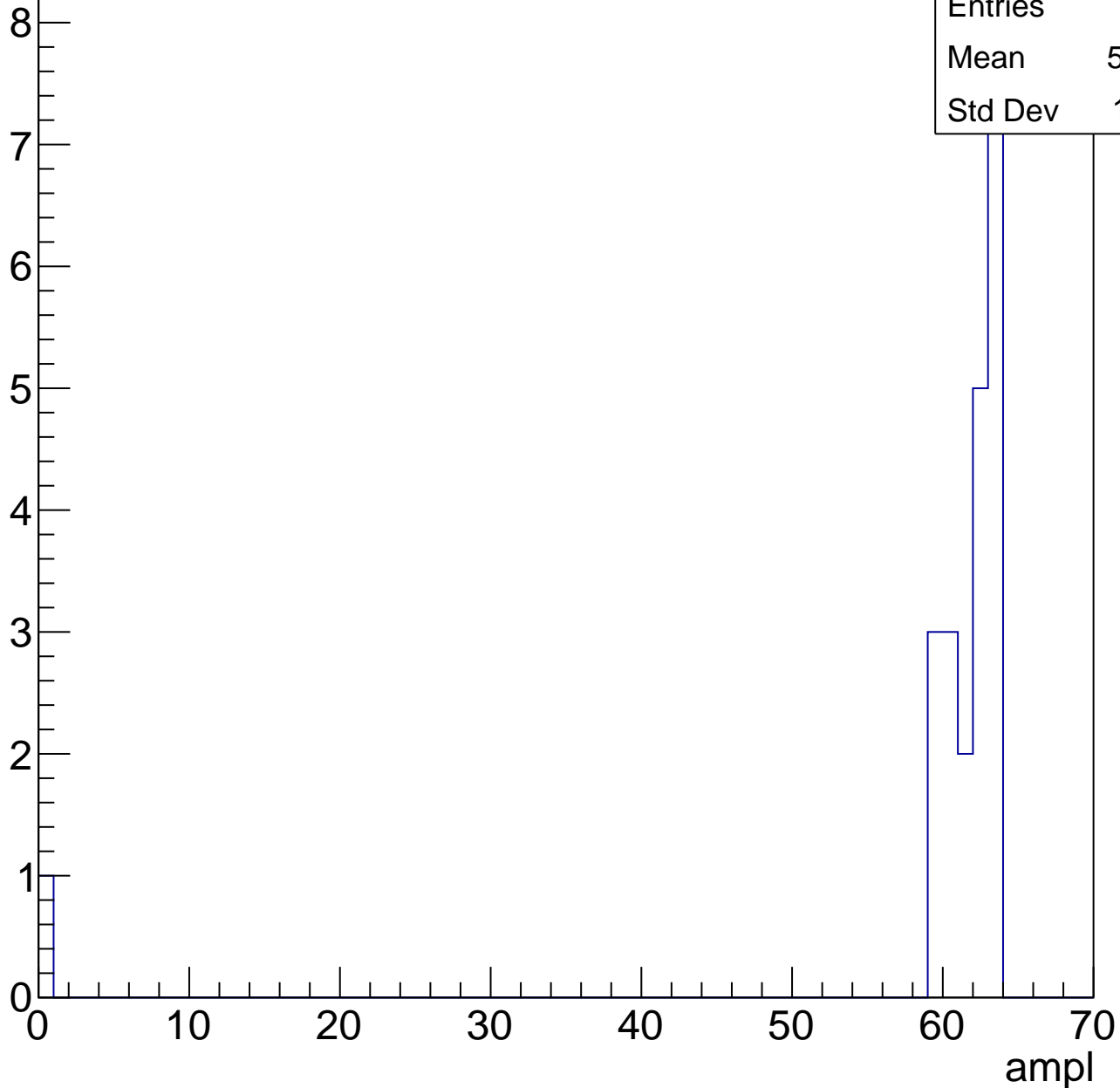


B0L101S, U18-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

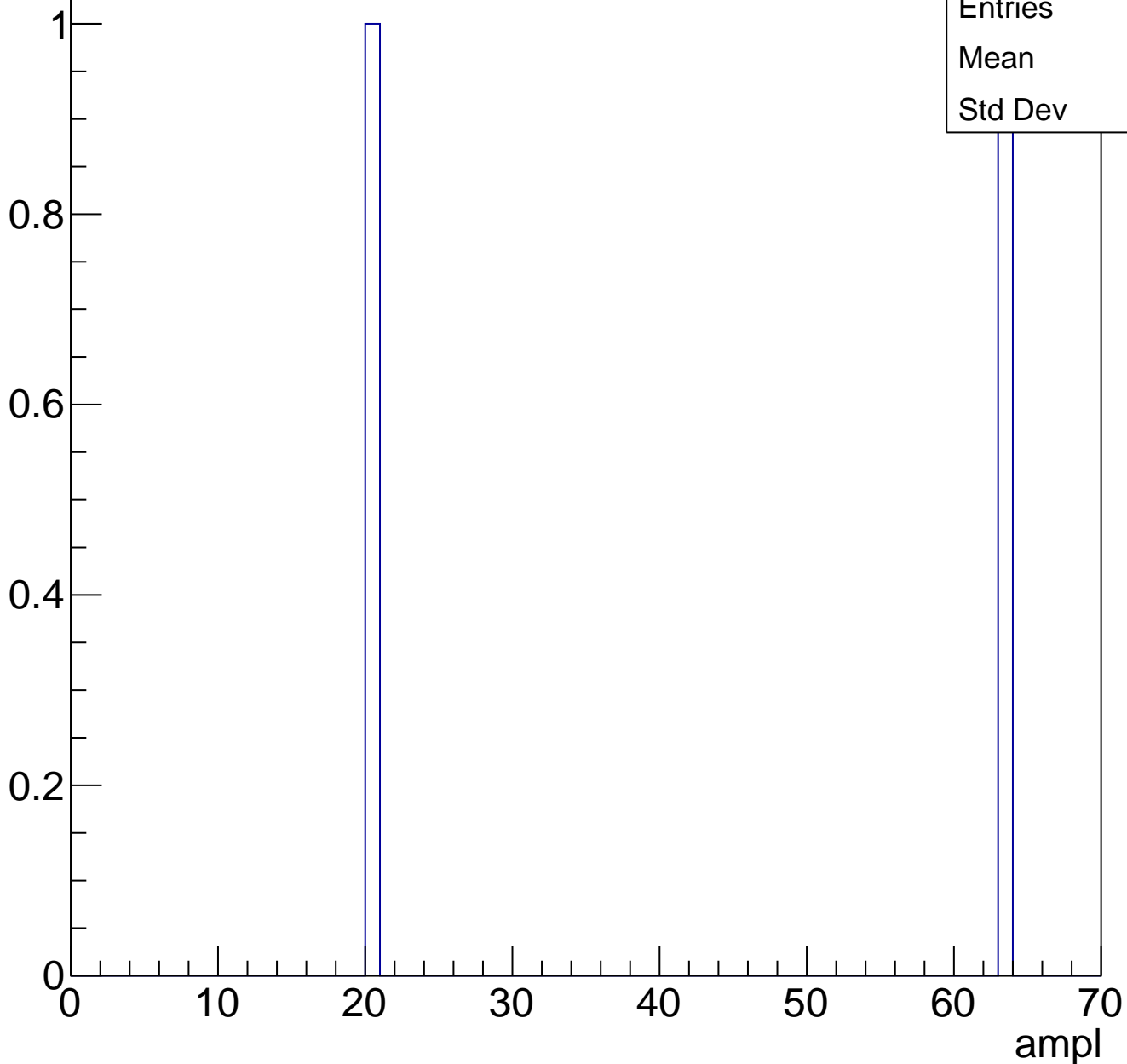
Entries	22
Mean	58.77
Std Dev	12.91



B0L101S, U18-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch113, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	29.85
Std Dev	3.649

Gaus mean : 30.3791

Gaus Width: 3.7412

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

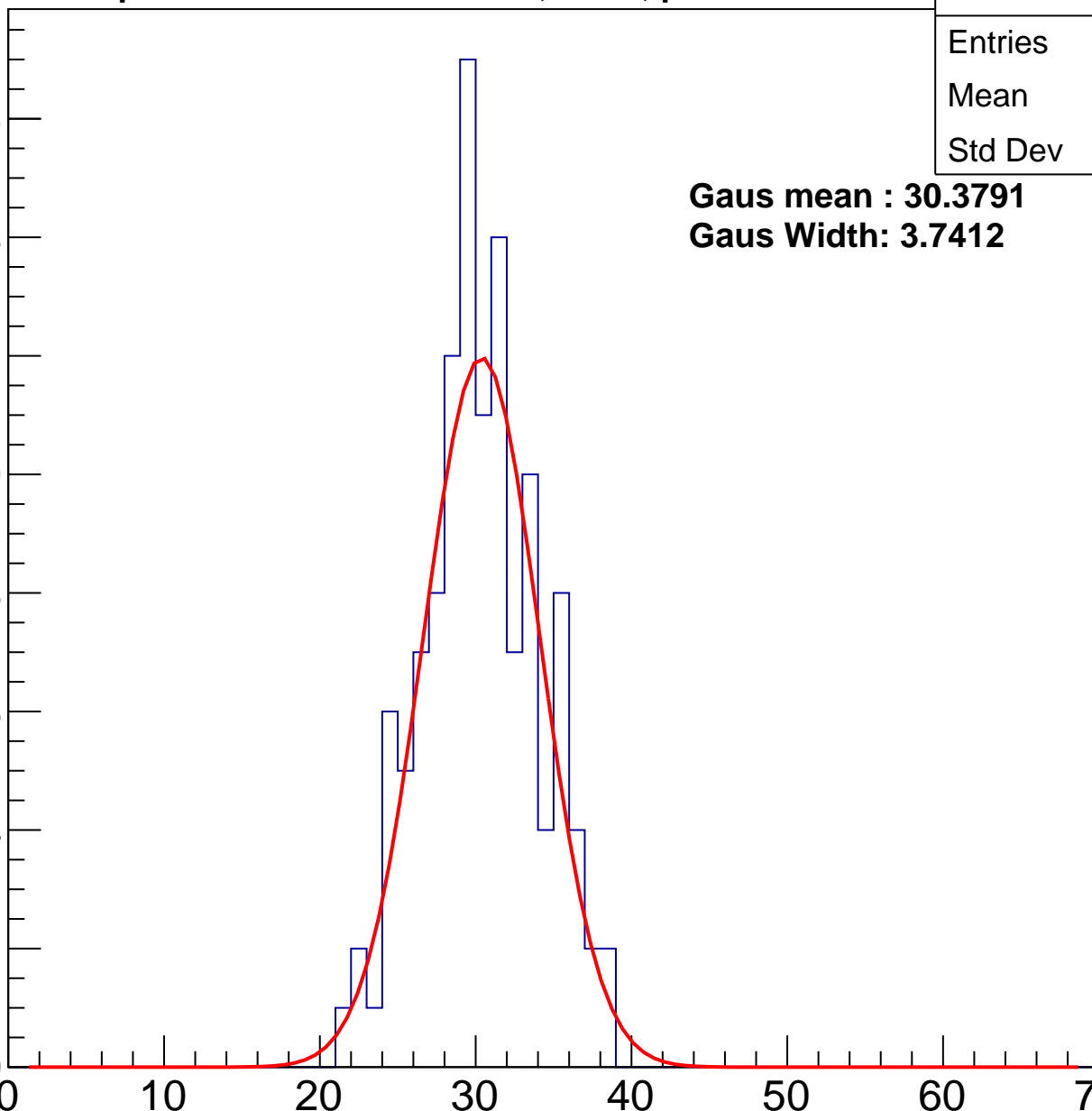
40

50

60

70

ampl



B0L101S, U18-ch113, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	37.27
Std Dev	4.508

Gaus mean : 38.0378

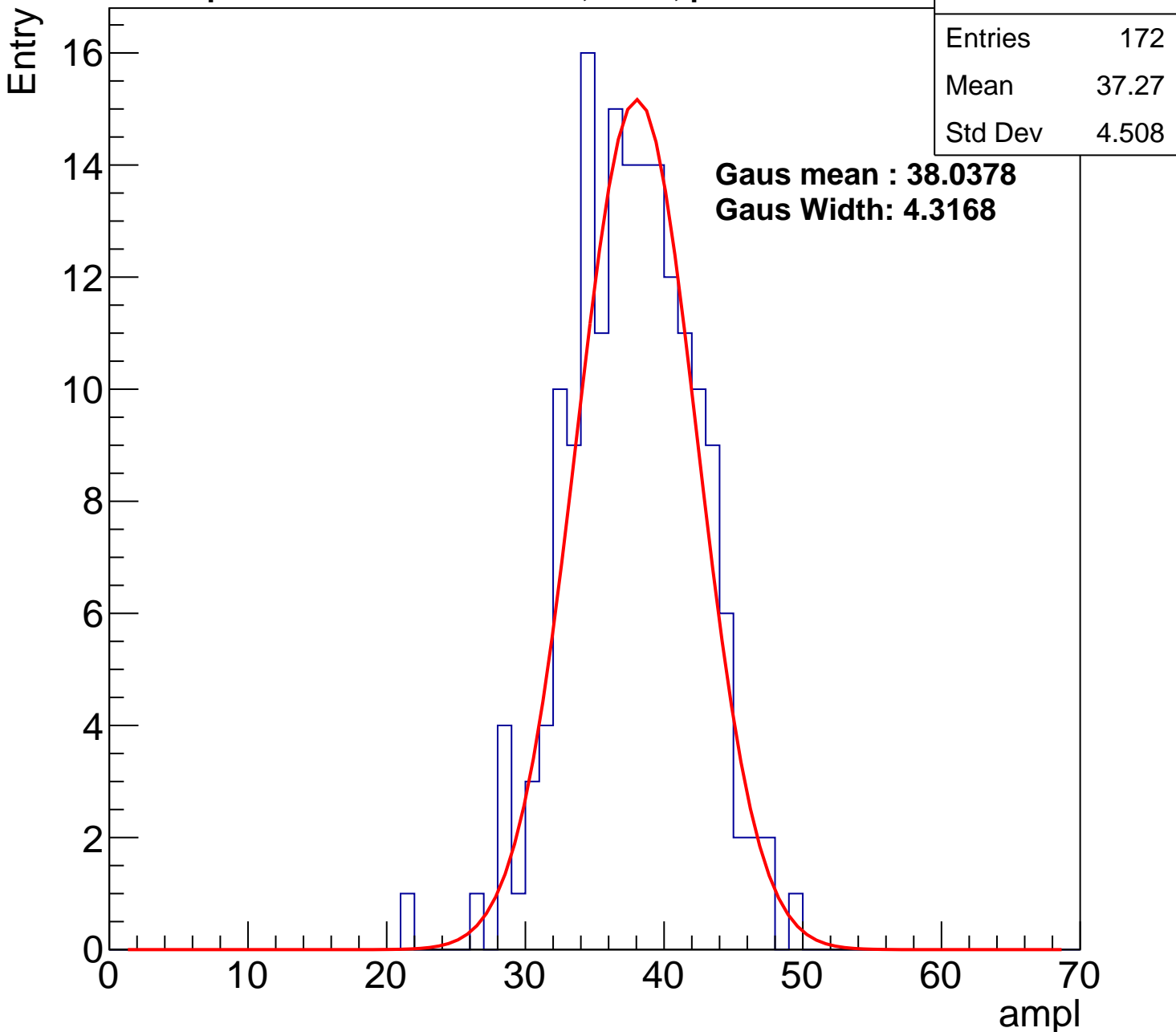
Gaus Width: 4.3168

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U18-ch113, adc2

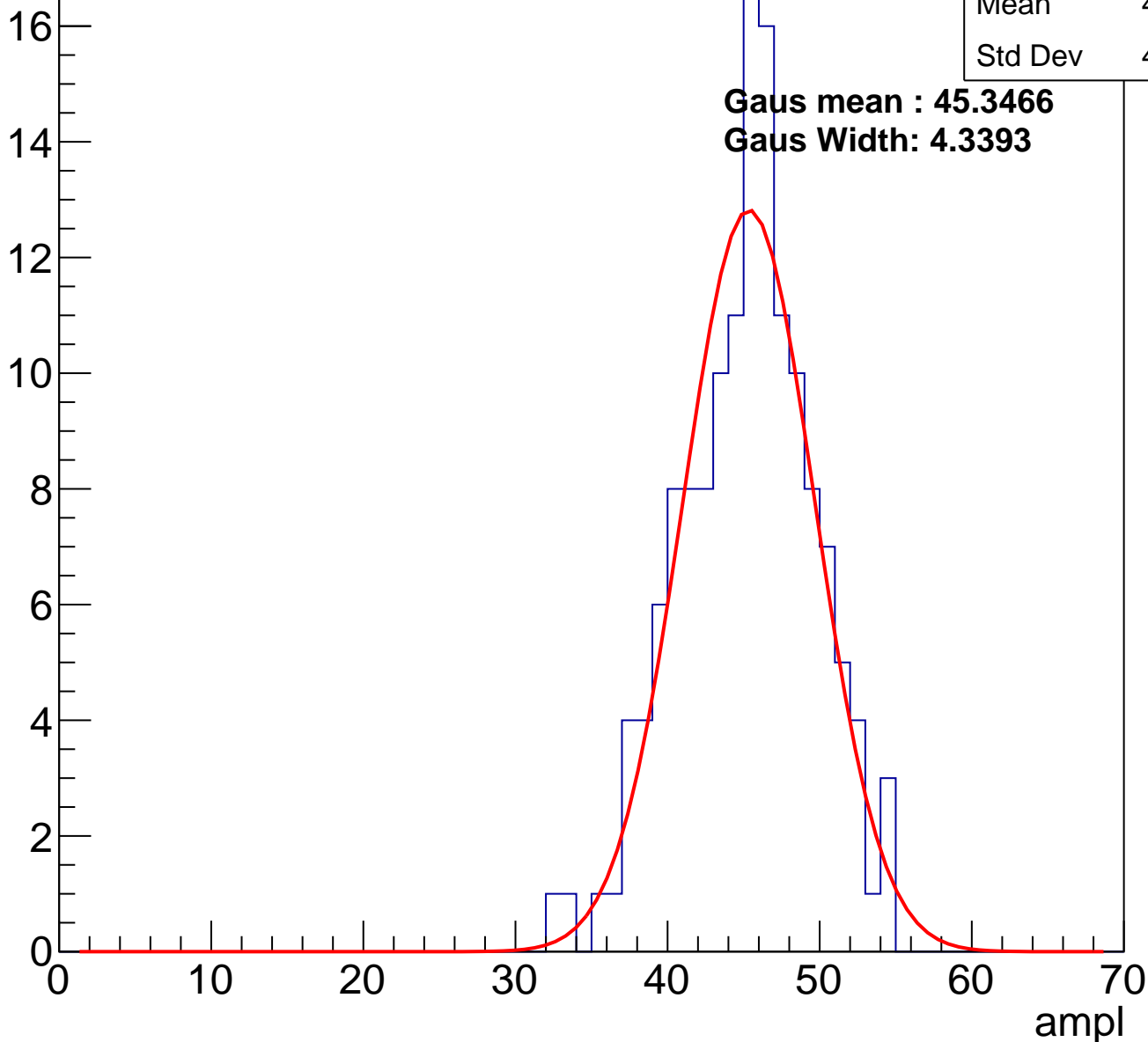
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	145
Mean	44.73
Std Dev	4.346

Gaus mean : 45.3466

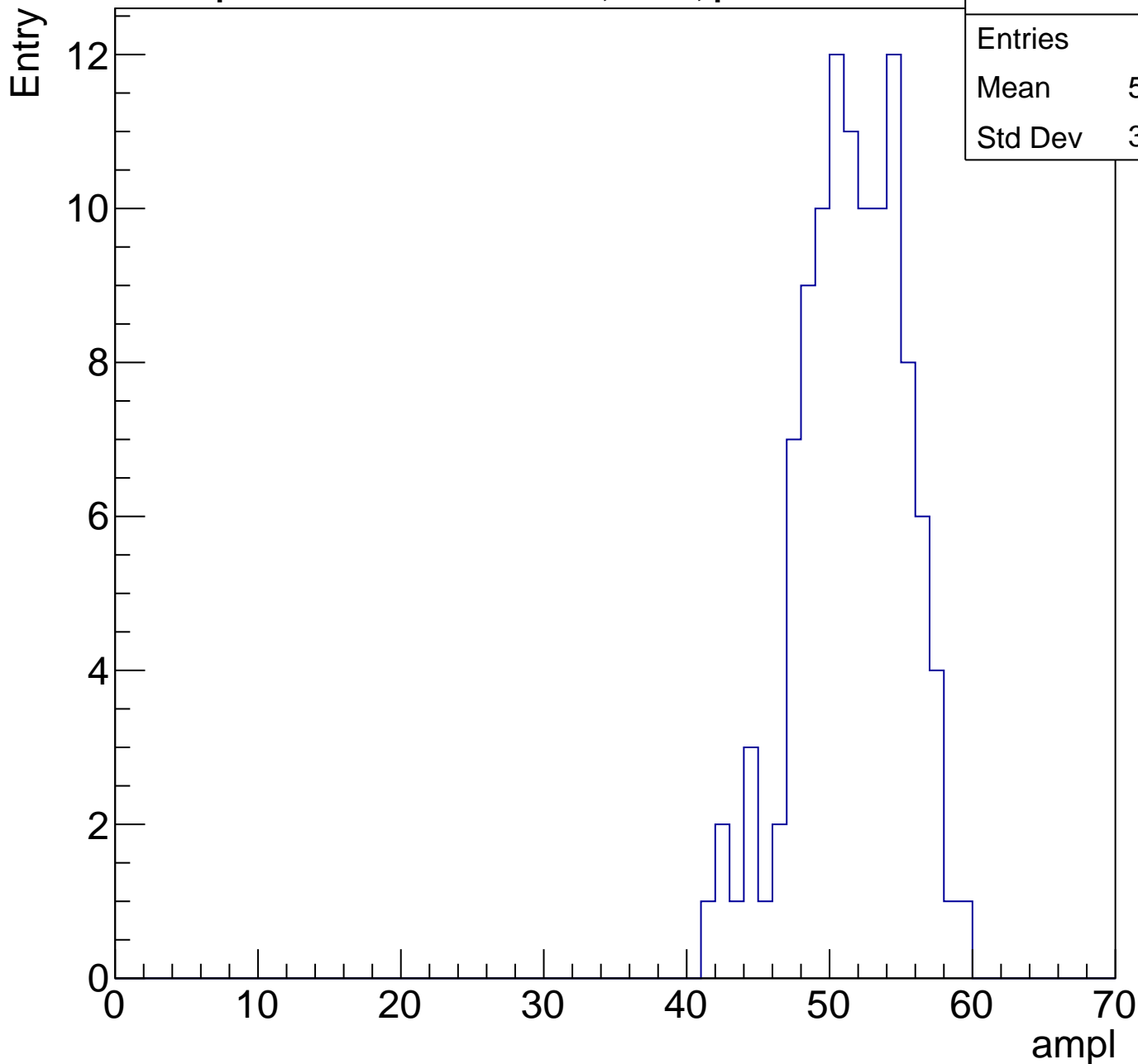
Gaus Width: 4.3393



B0L101S, U18-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

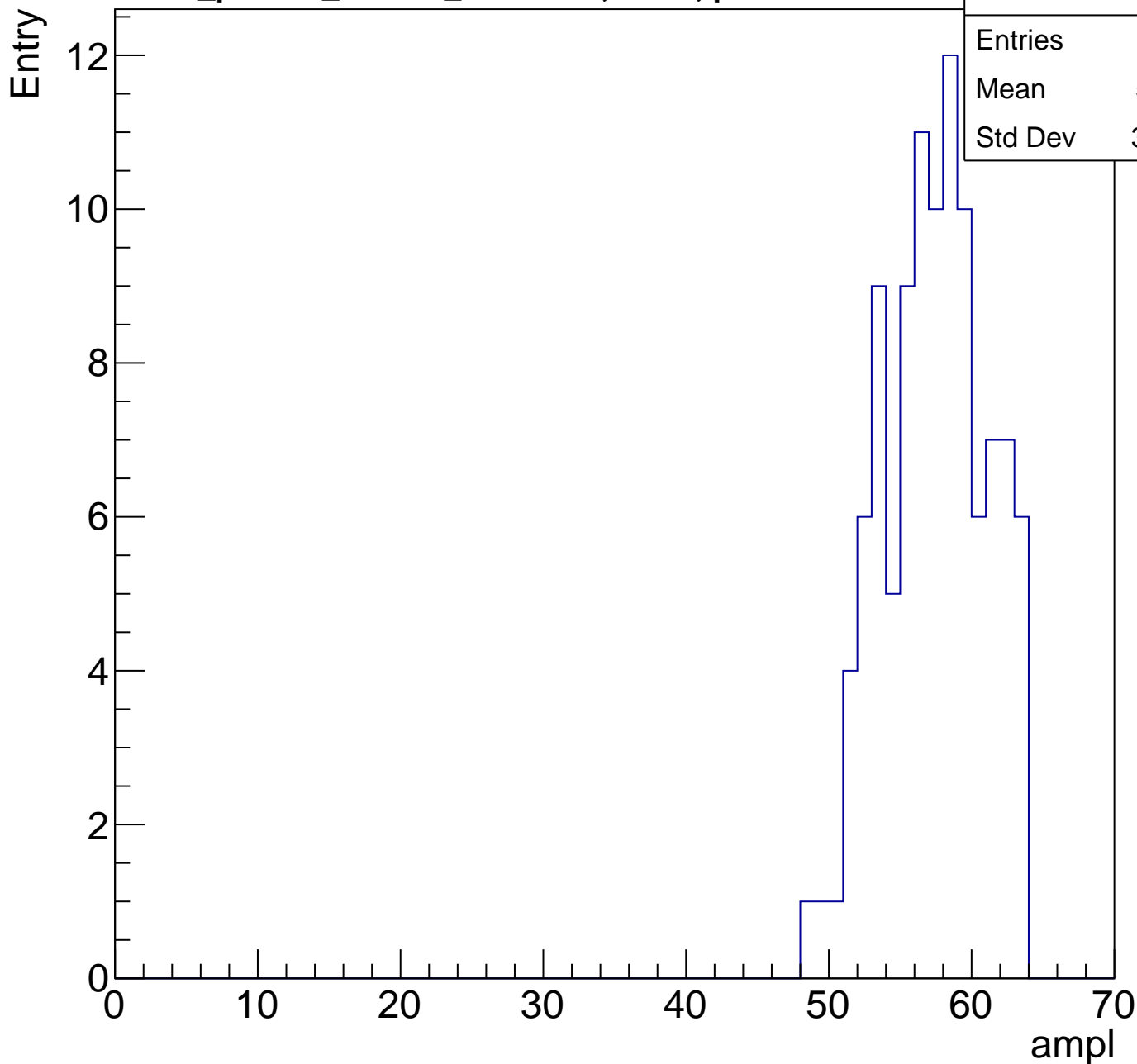
Entries	111
Mean	51.06
Std Dev	3.672



B0L101S, U18-ch113, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	56.91
Std Dev	3.562



B0L101S, U18-ch113, adc5

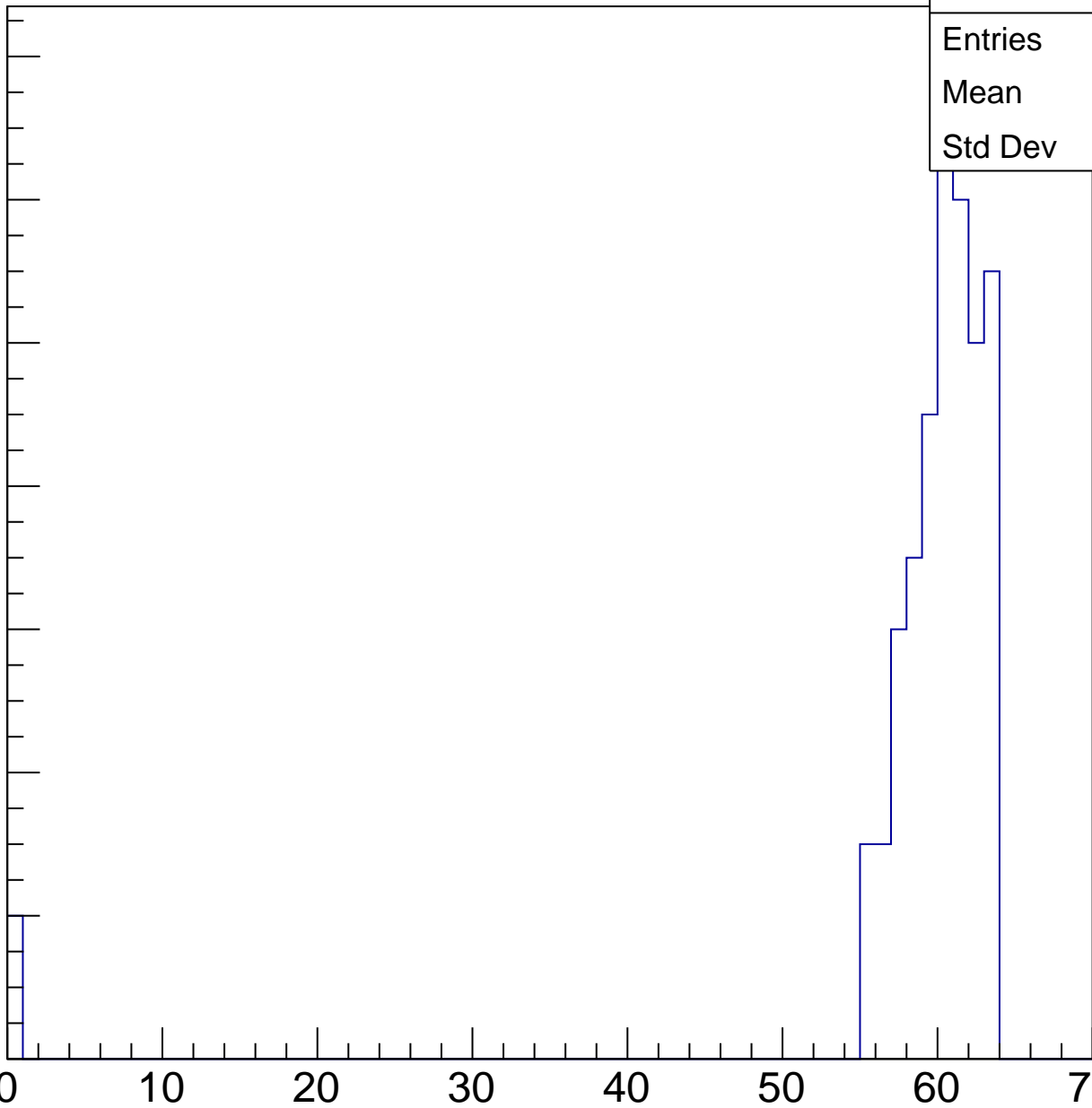
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	58.4
Std Dev	9.782

ampl



B0L101S, U18-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

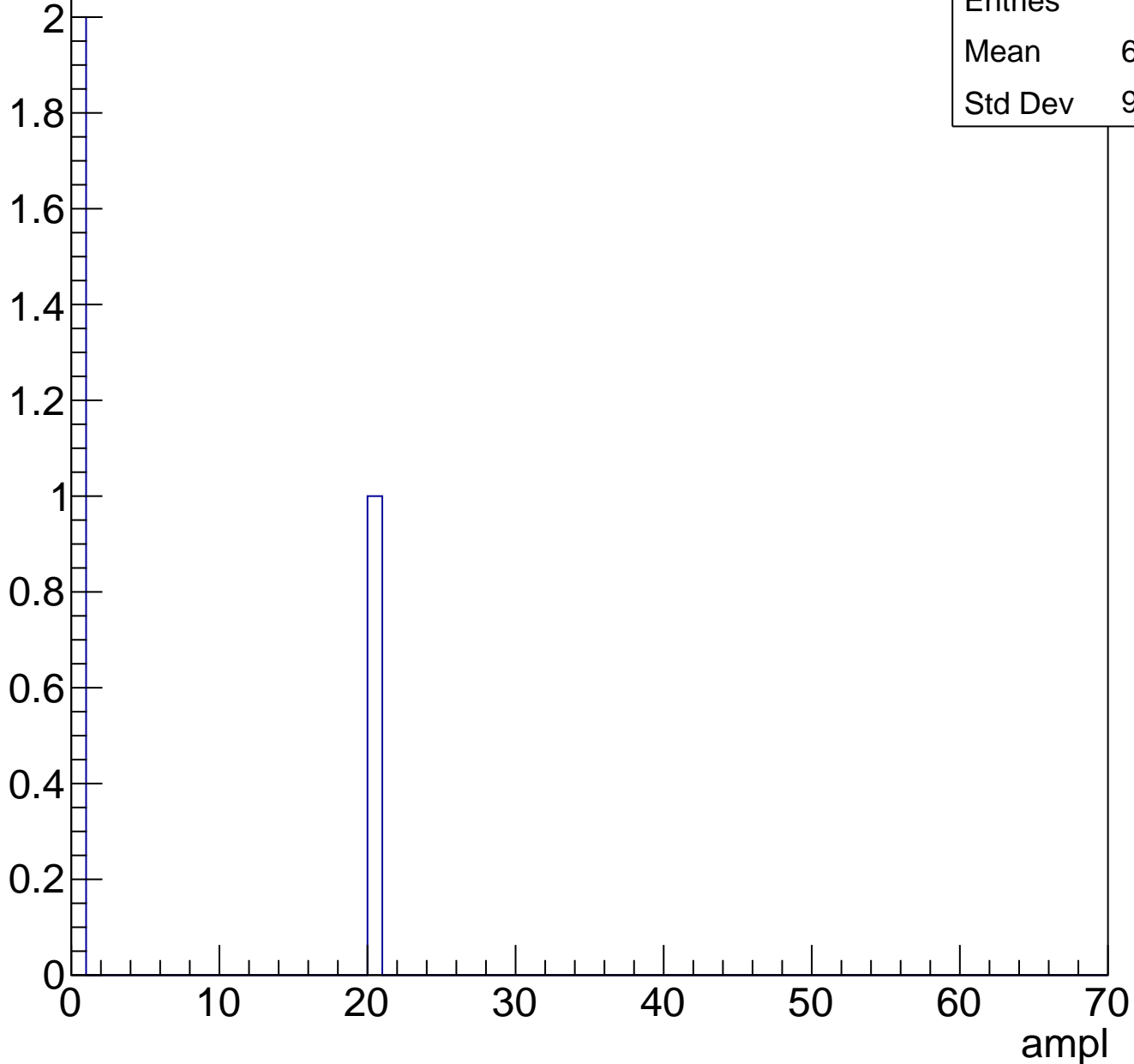
Entry



B0L101S, U18-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L101S, U18-ch114, adc0

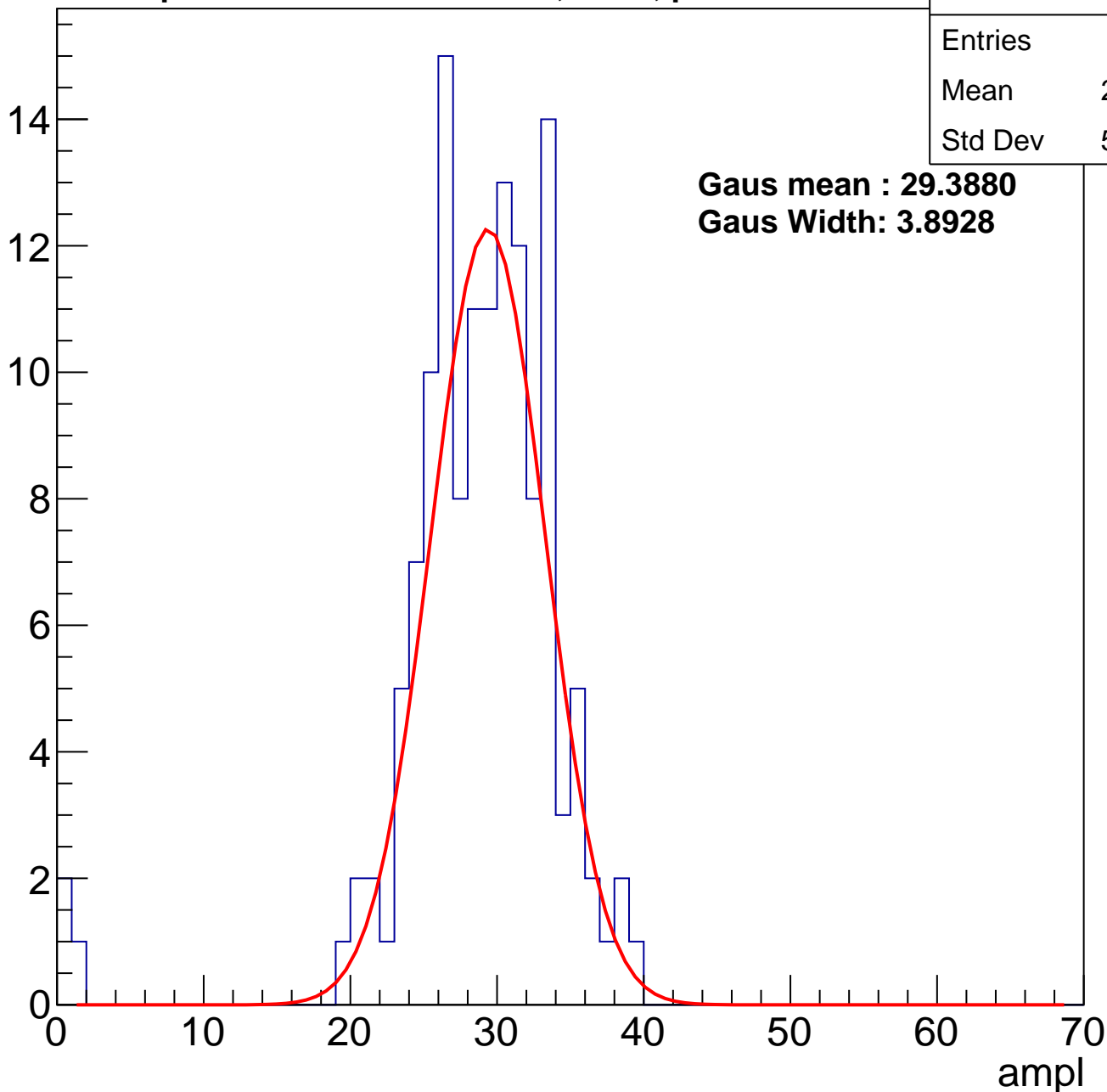
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	28.26
Std Dev	5.768

Gaus mean : 29.3880

Gaus Width: 3.8928

Entry



B0L101S, U18-ch114, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	35.82
Std Dev	4.071

Gaus mean : 35.9494

Gaus Width: 3.6789

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U18-ch114, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

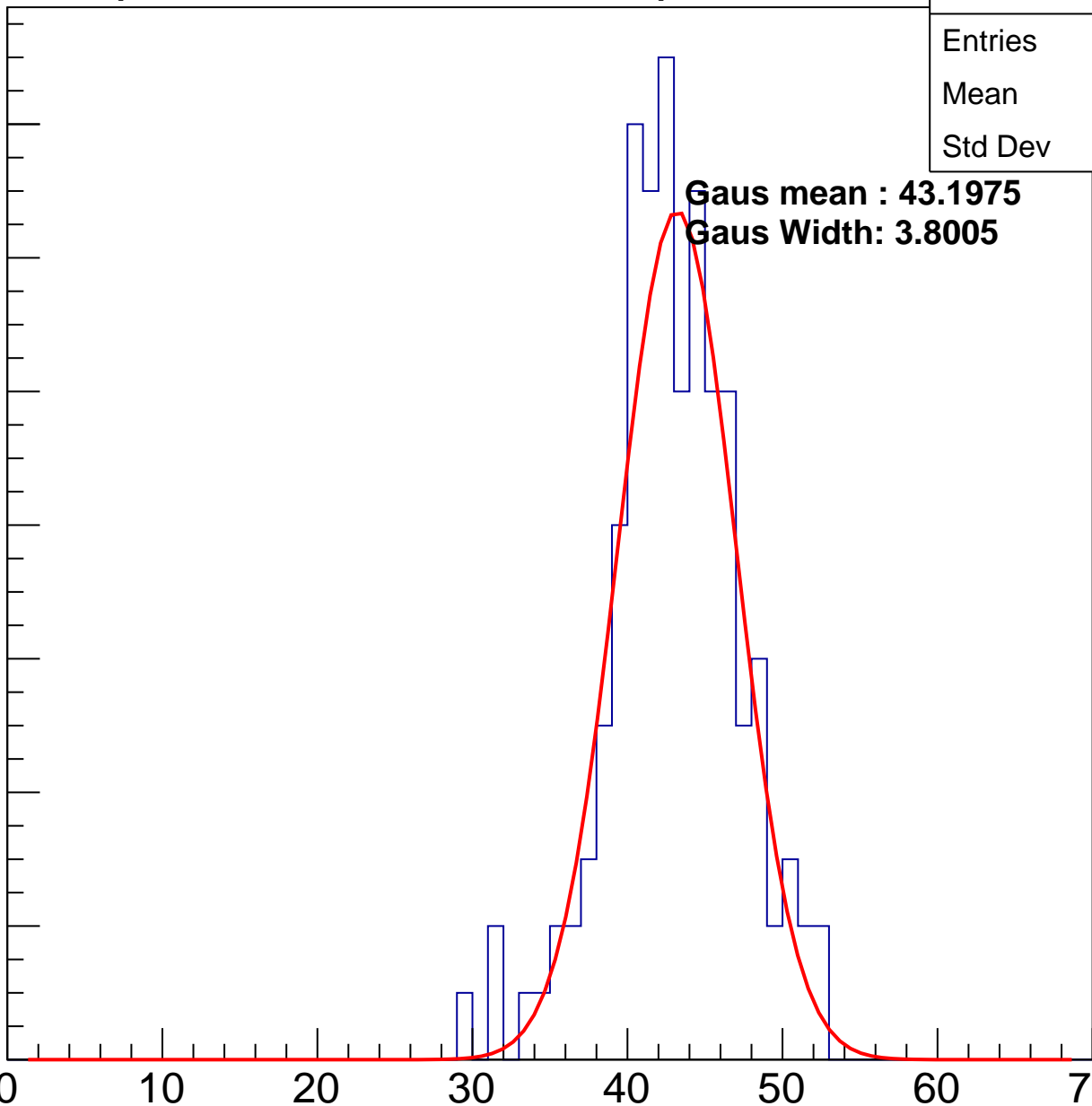
14
12
10
8
6
4
2
0

Entries	130
Mean	42.5
Std Dev	4.201

Gaus mean : 43.1975

Gaus Width: 3.8005

ampl



B0L101S, U18-ch114, adc3

calib_packv5_042523_0143.root, FC#1, port C1

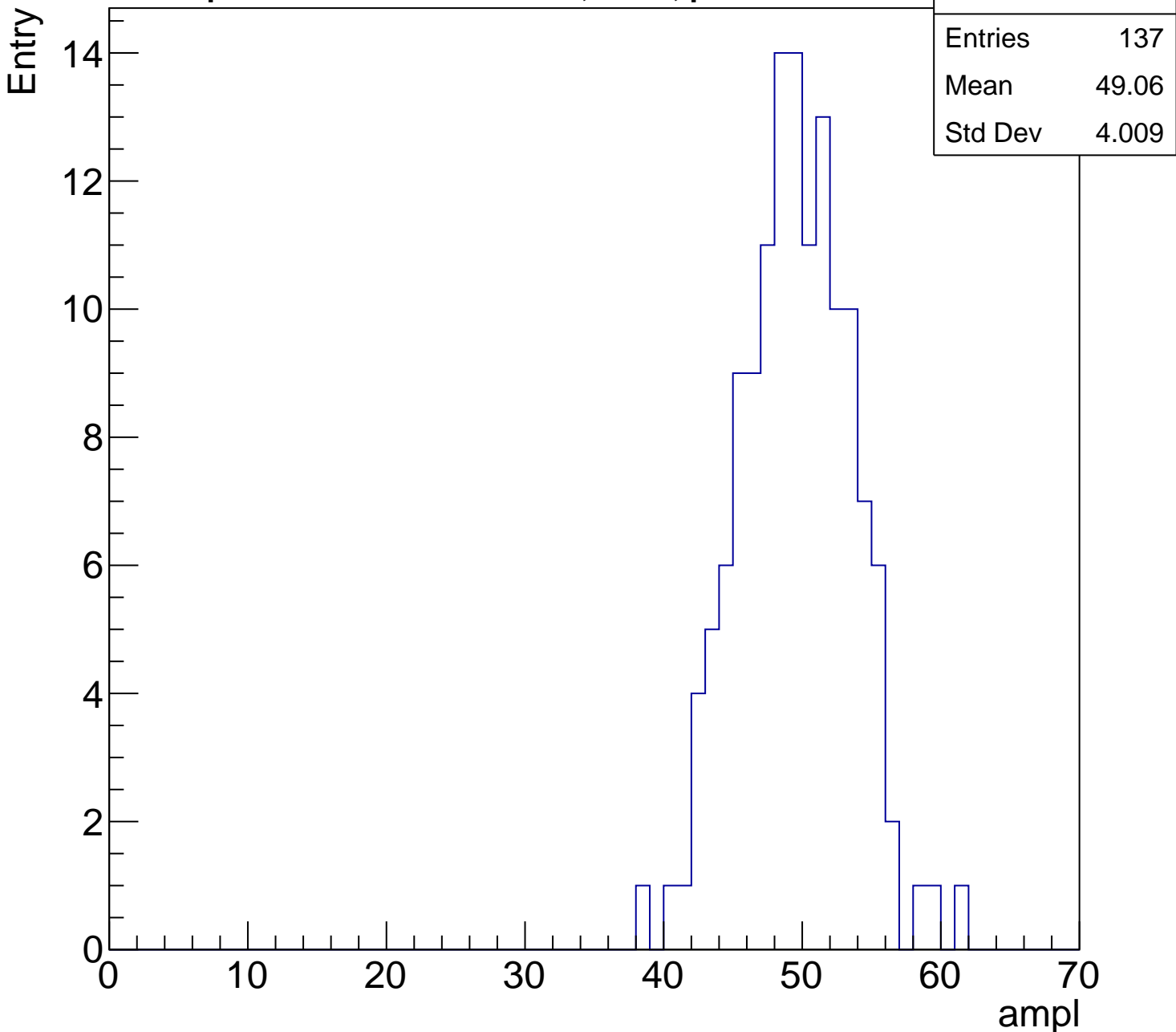
Entry

14
12
10
8
6
4
2
0

Entries	137
Mean	49.06
Std Dev	4.009

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch114, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	55.17
Std Dev	3.798

Entry

12

10

8

6

4

2

0

0

10

20

30

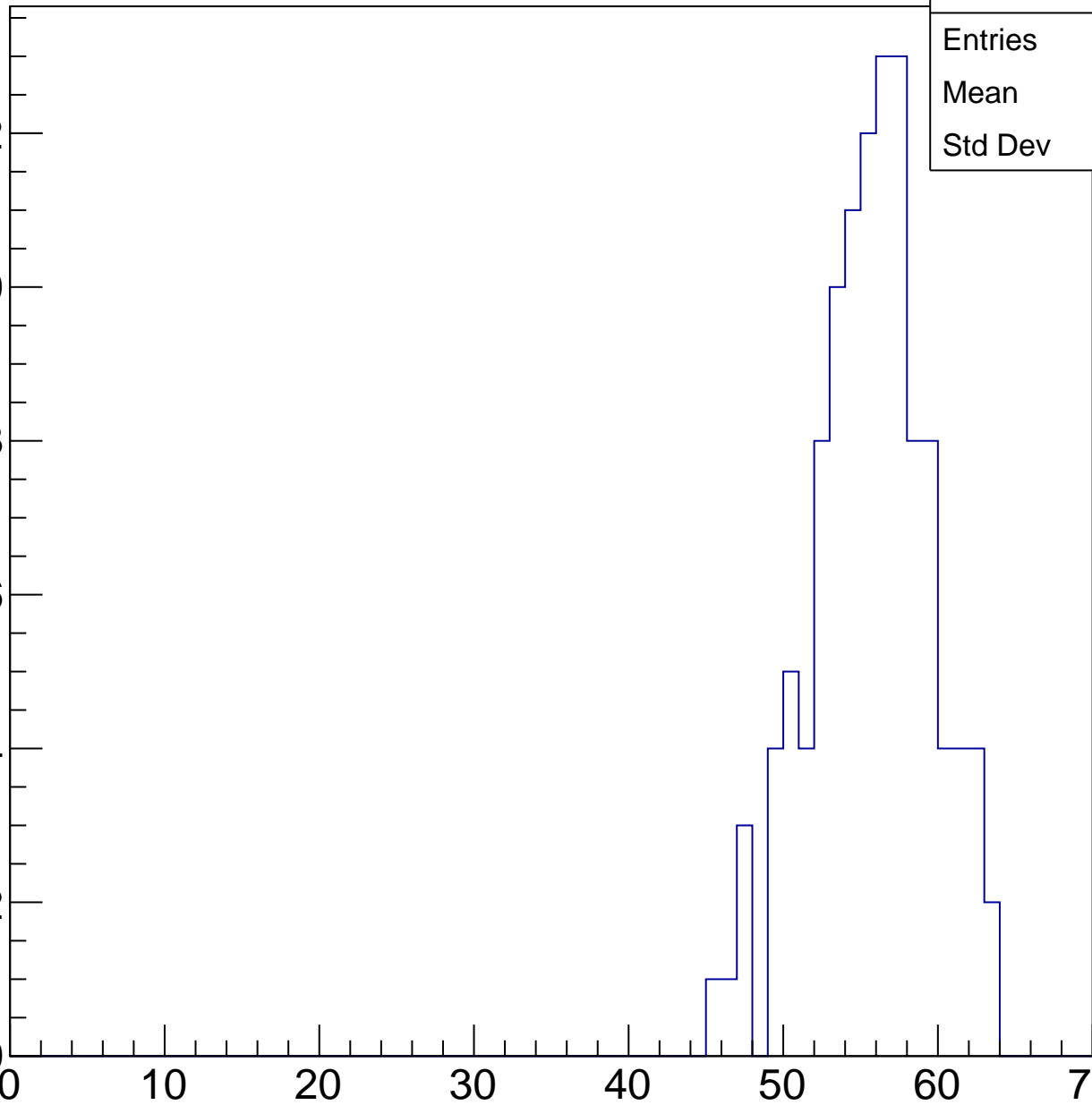
40

50

60

70

ampl



B0L101S, U18-ch114, adc5

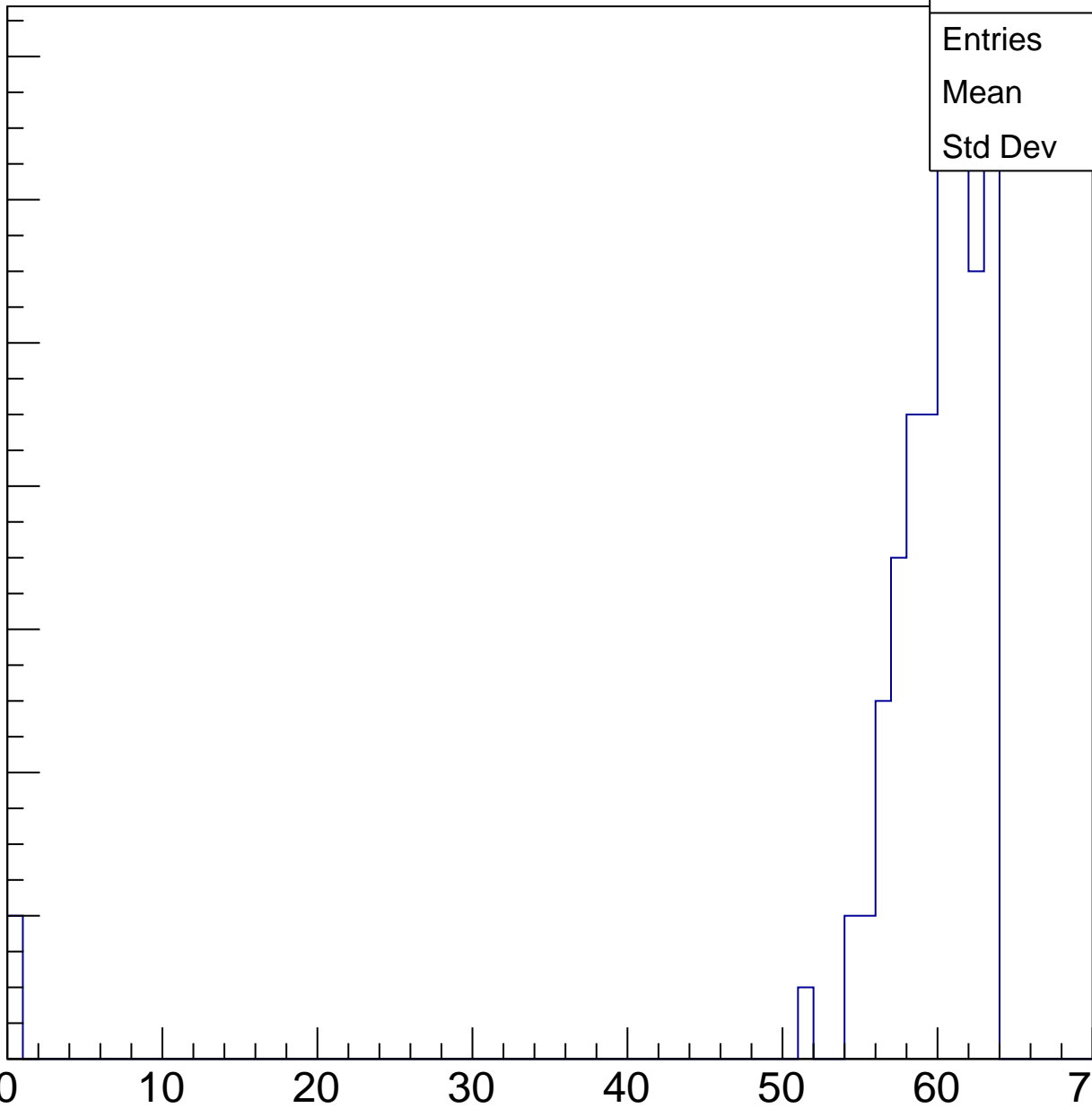
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.43
Std Dev	9.162

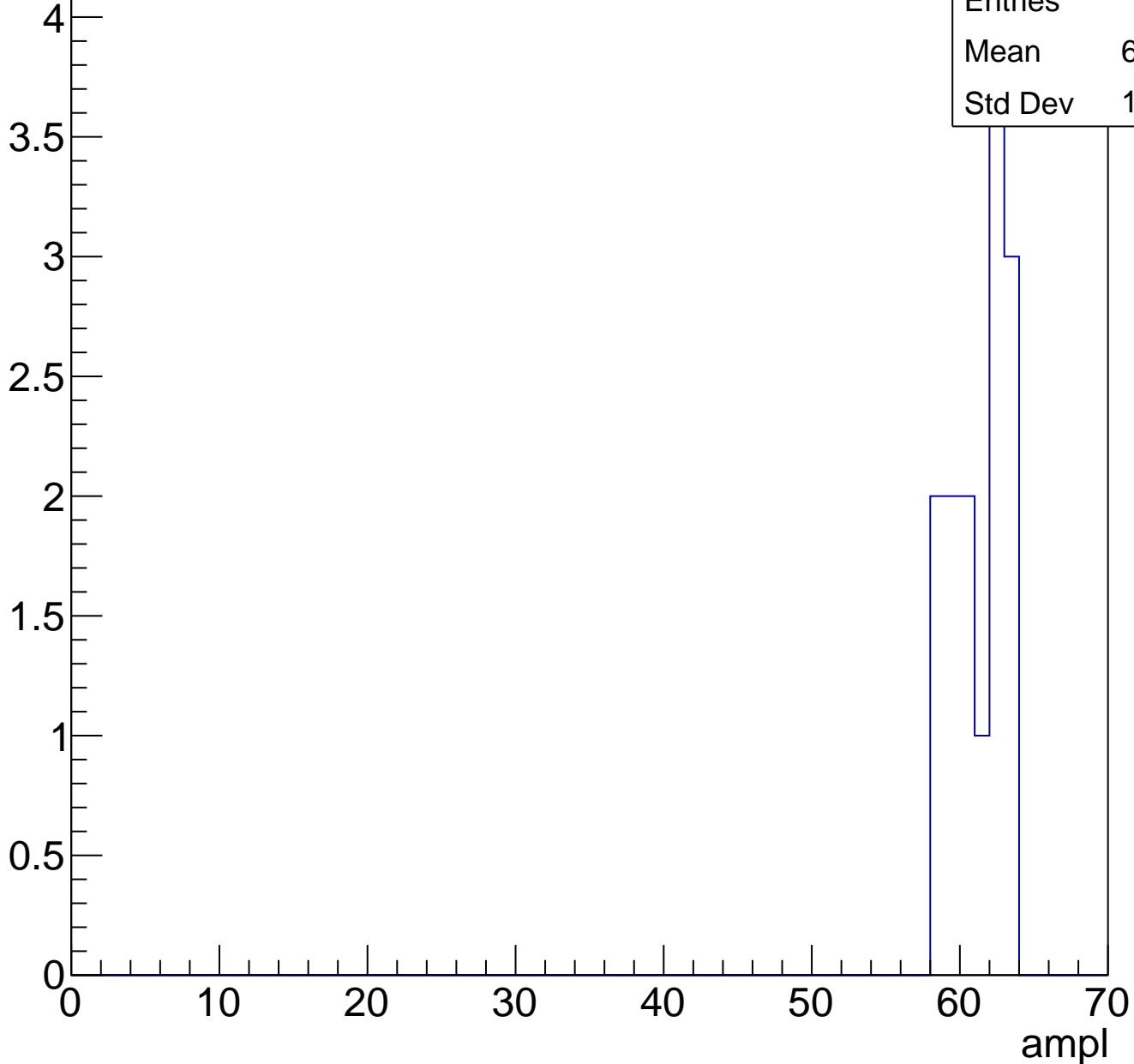
ampl



B0L101S, U18-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

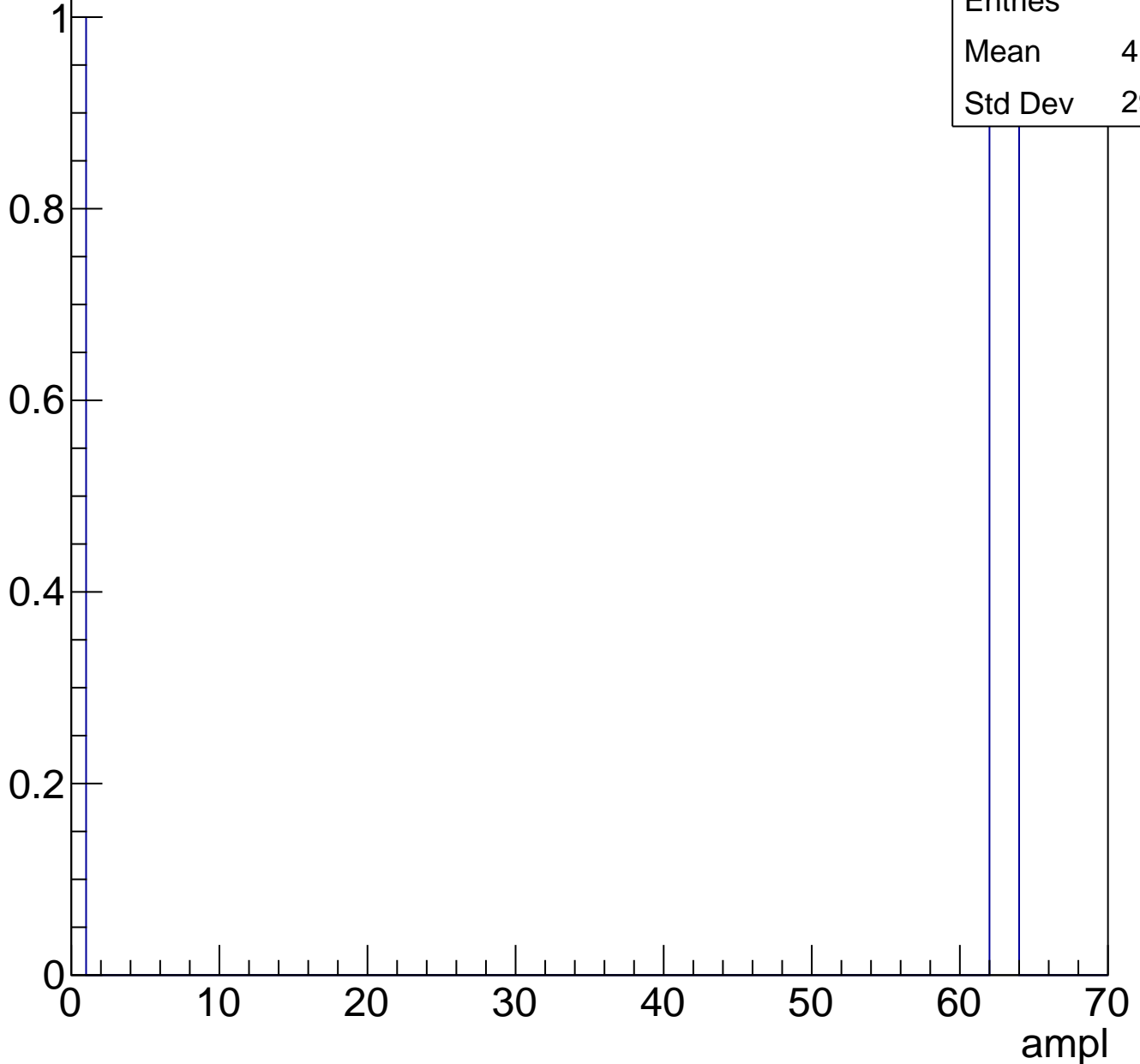
Entry



B0L101S, U18-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch115, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	29.18
Std Dev	4.459

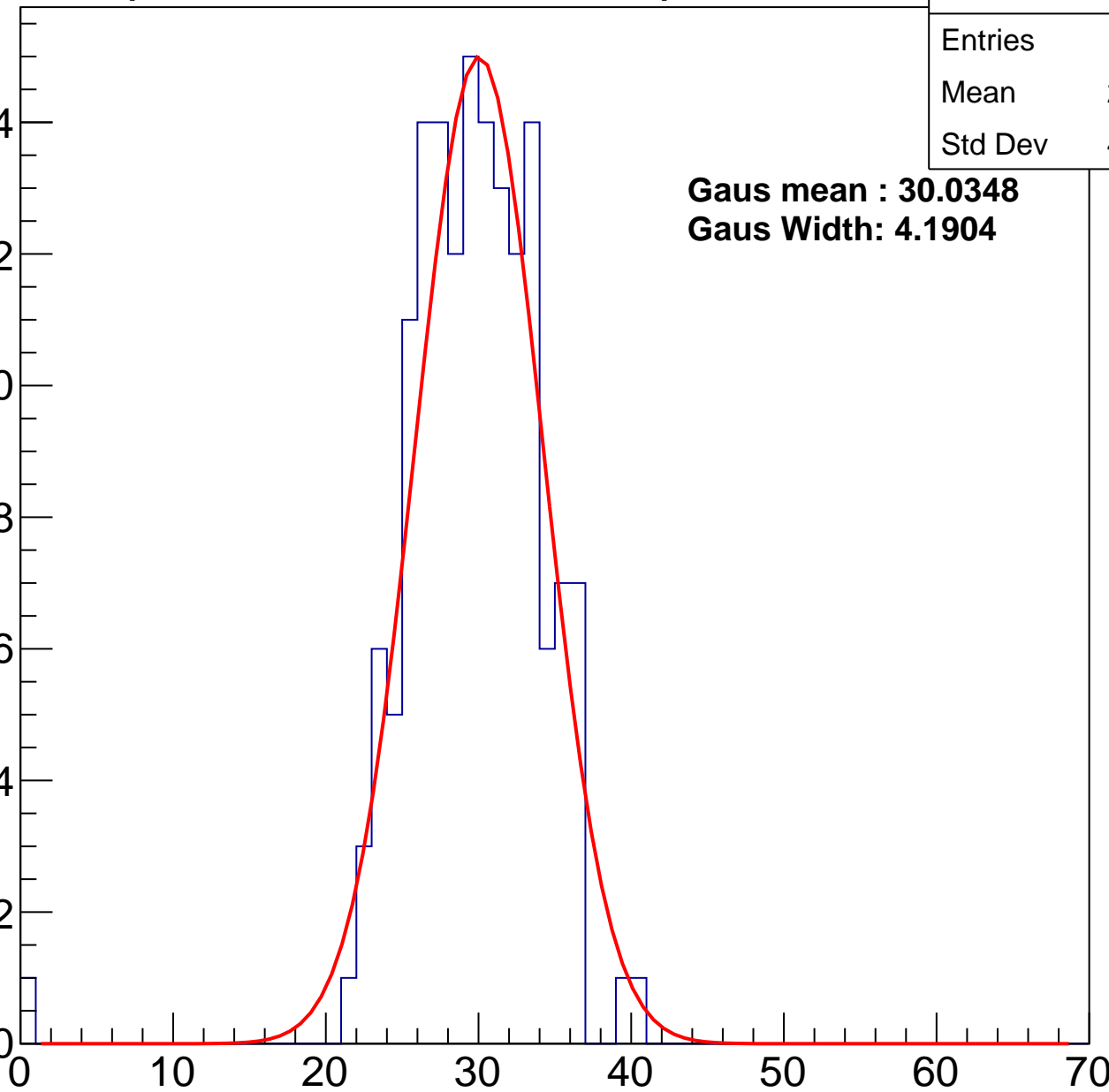
Gaus mean : 30.0348

Gaus Width: 4.1904

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U18-ch115, adc1

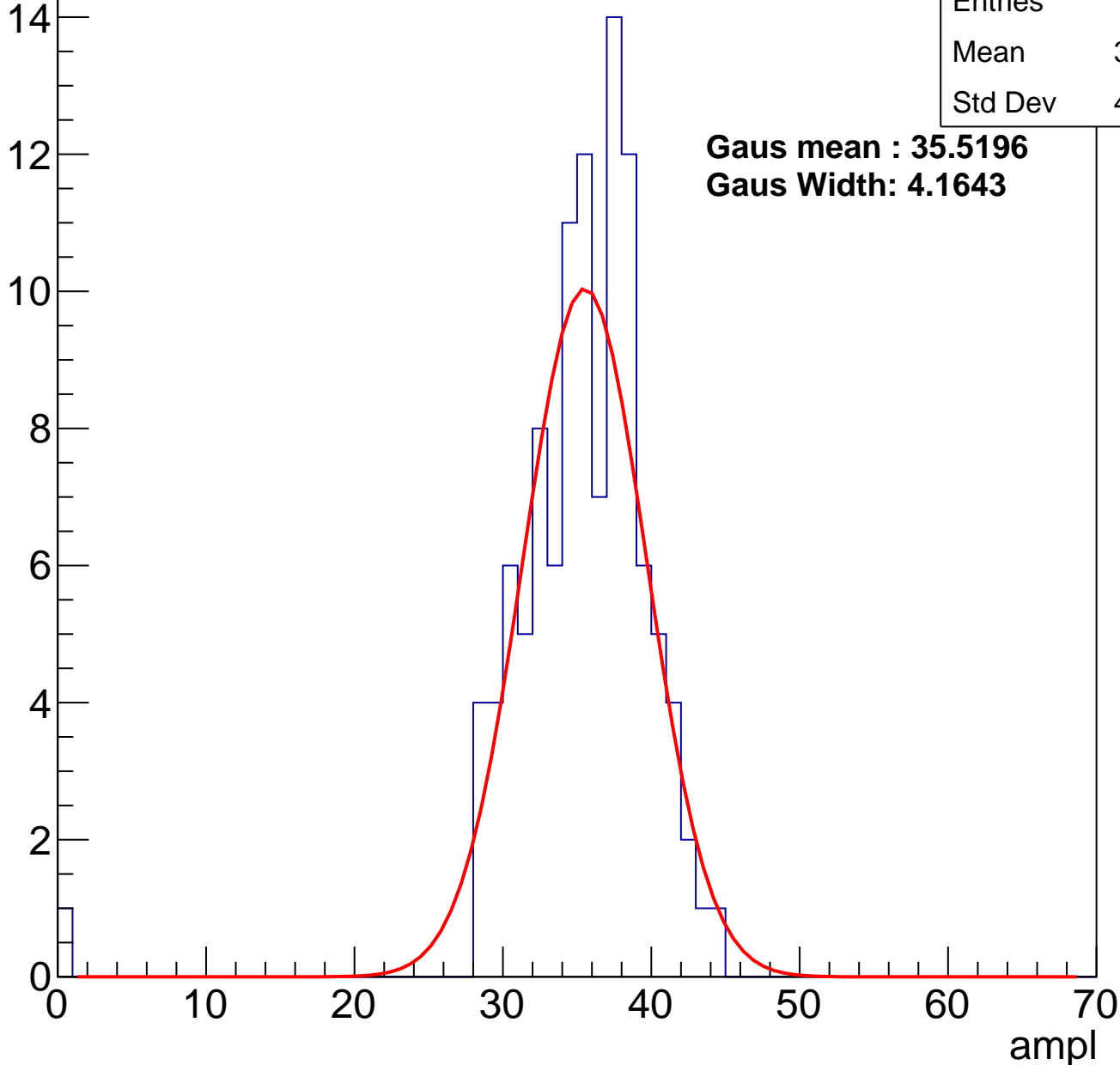
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	34.92
Std Dev	4.959

Gaus mean : 35.5196

Gaus Width: 4.1643

Entry



B0L101S, U18-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	42.03
Std Dev	3.687

Gaus mean : 42.5248

Gaus Width: 3.6653

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

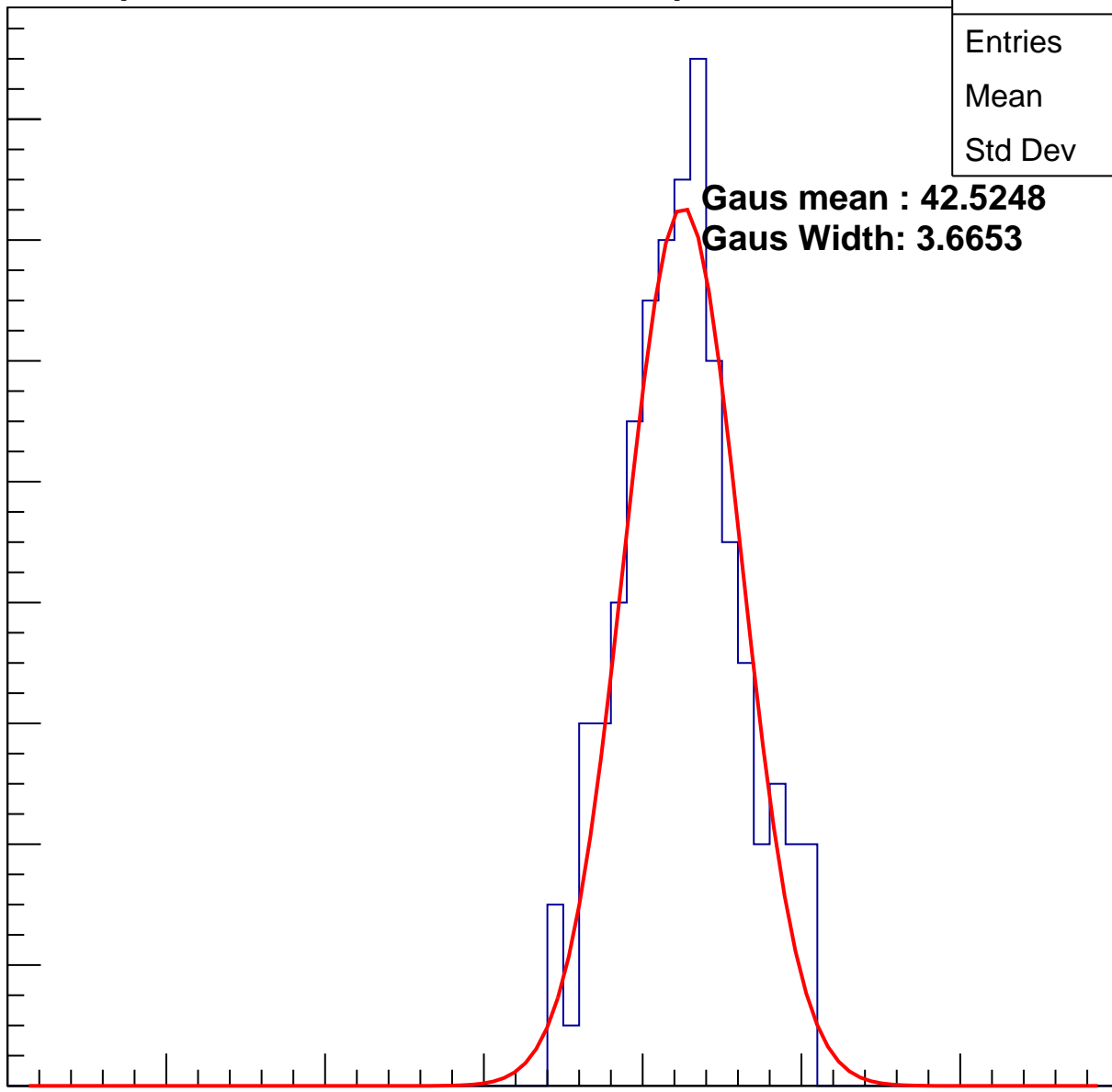
40

50

60

70

ampl

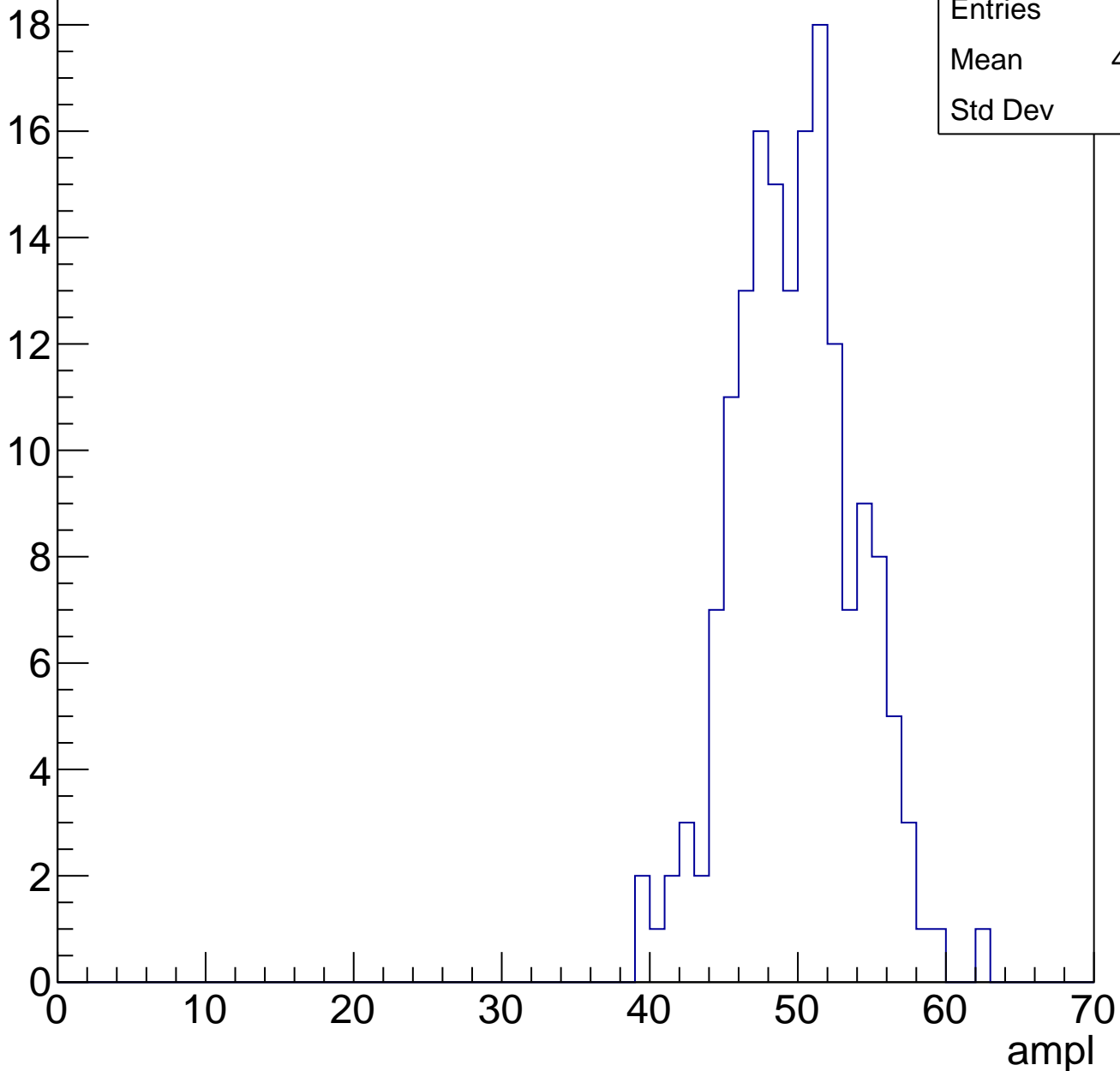


B0L101S, U18-ch115, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	49.34
Std Dev	4.08

Entry



B0L101S, U18-ch115, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	56.21
Std Dev	3.226

Entry

10

8

6

4

2

0

0

10

20

30

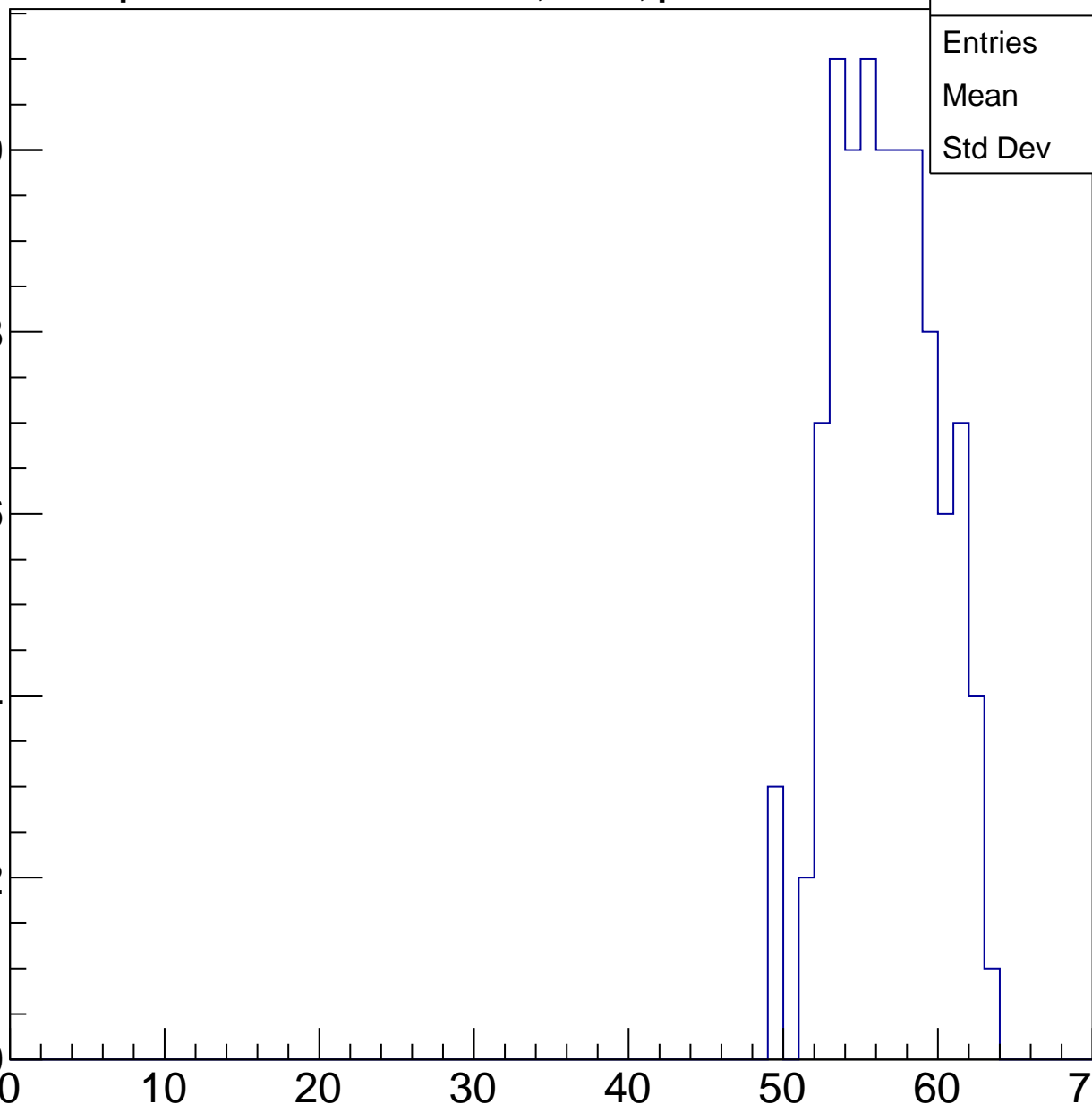
40

50

60

70

ampl



B0L101S, U18-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

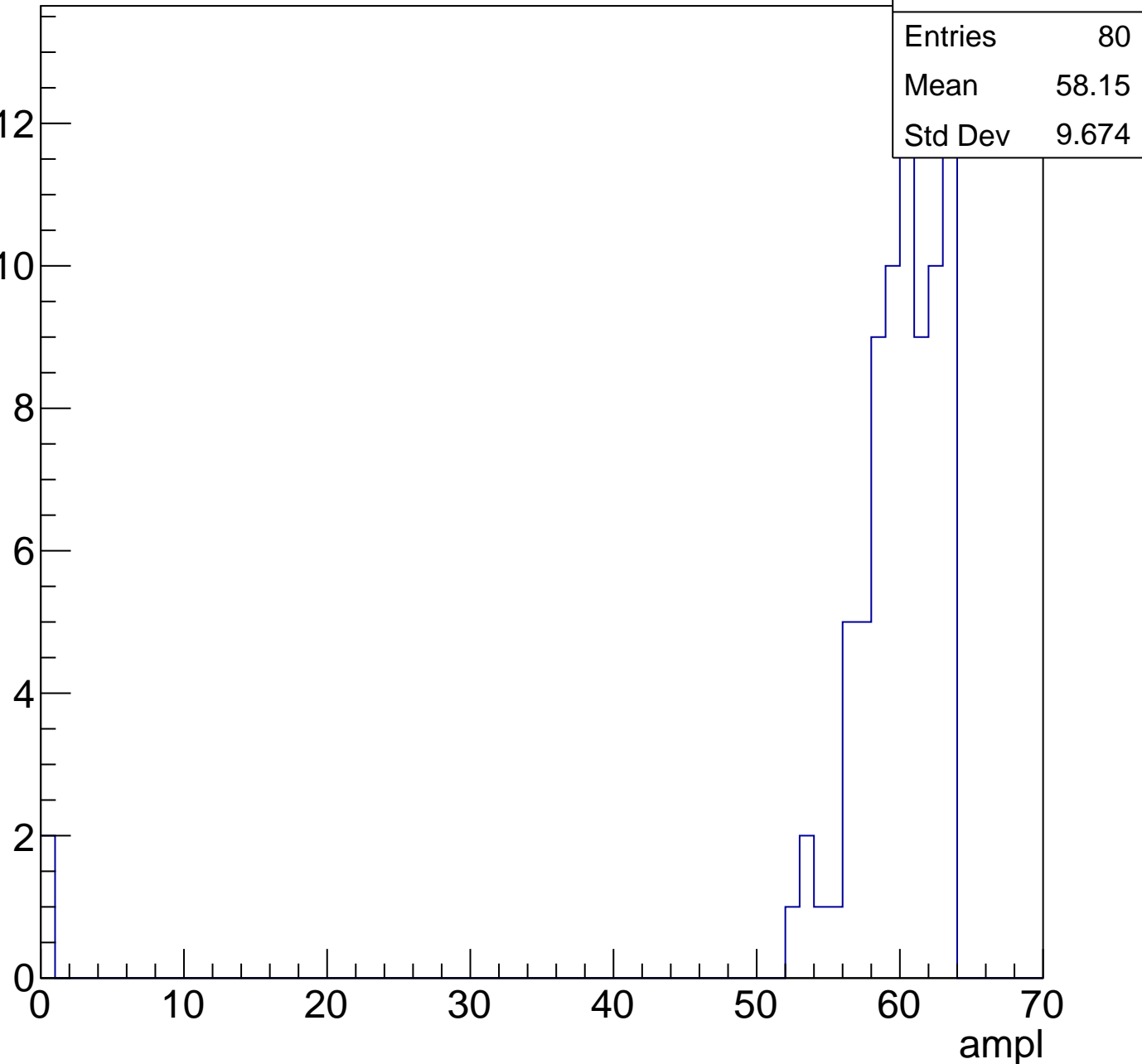
50

60

70

ampl

Entries	80
Mean	58.15
Std Dev	9.674

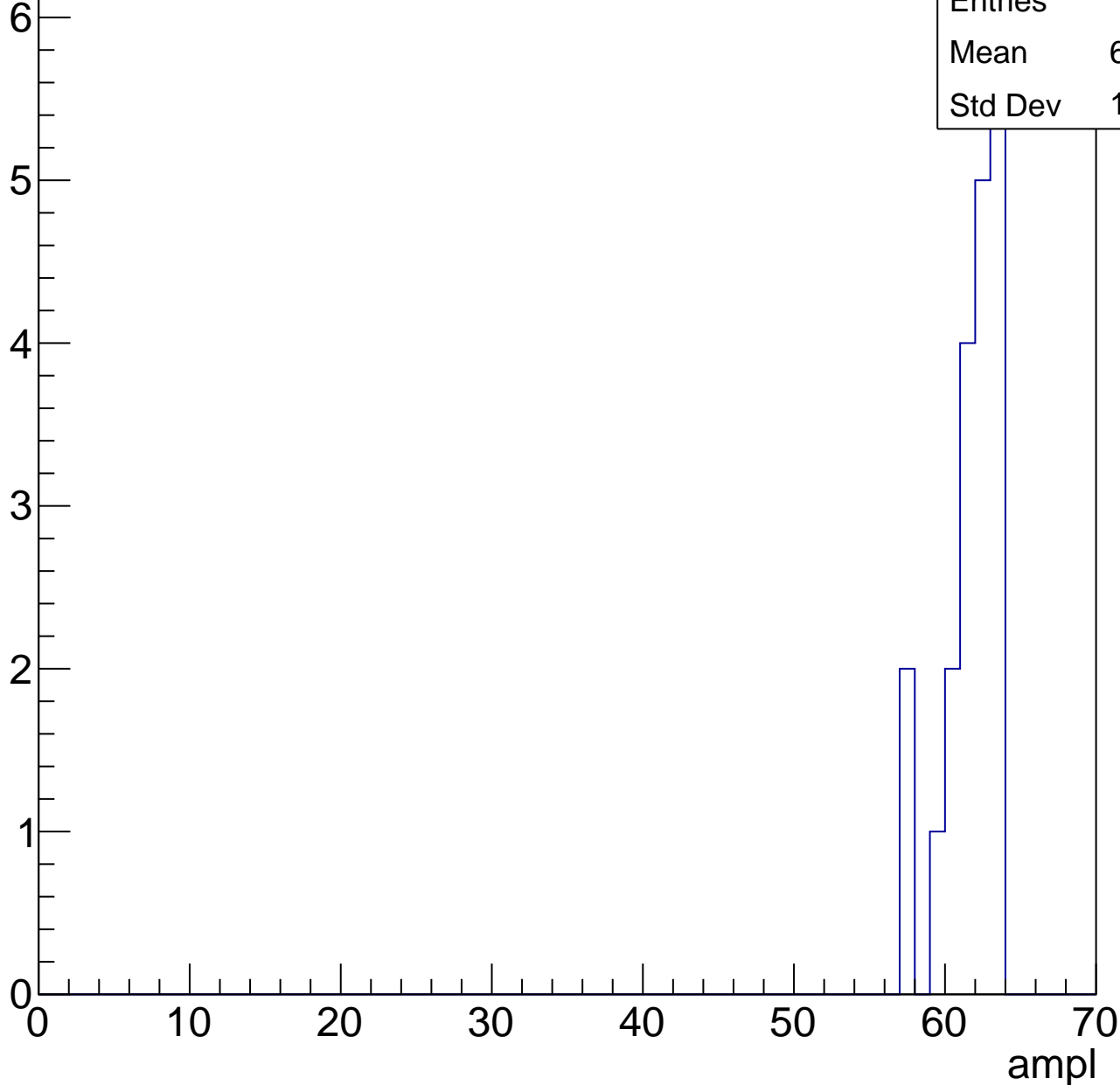


B0L101S, U18-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

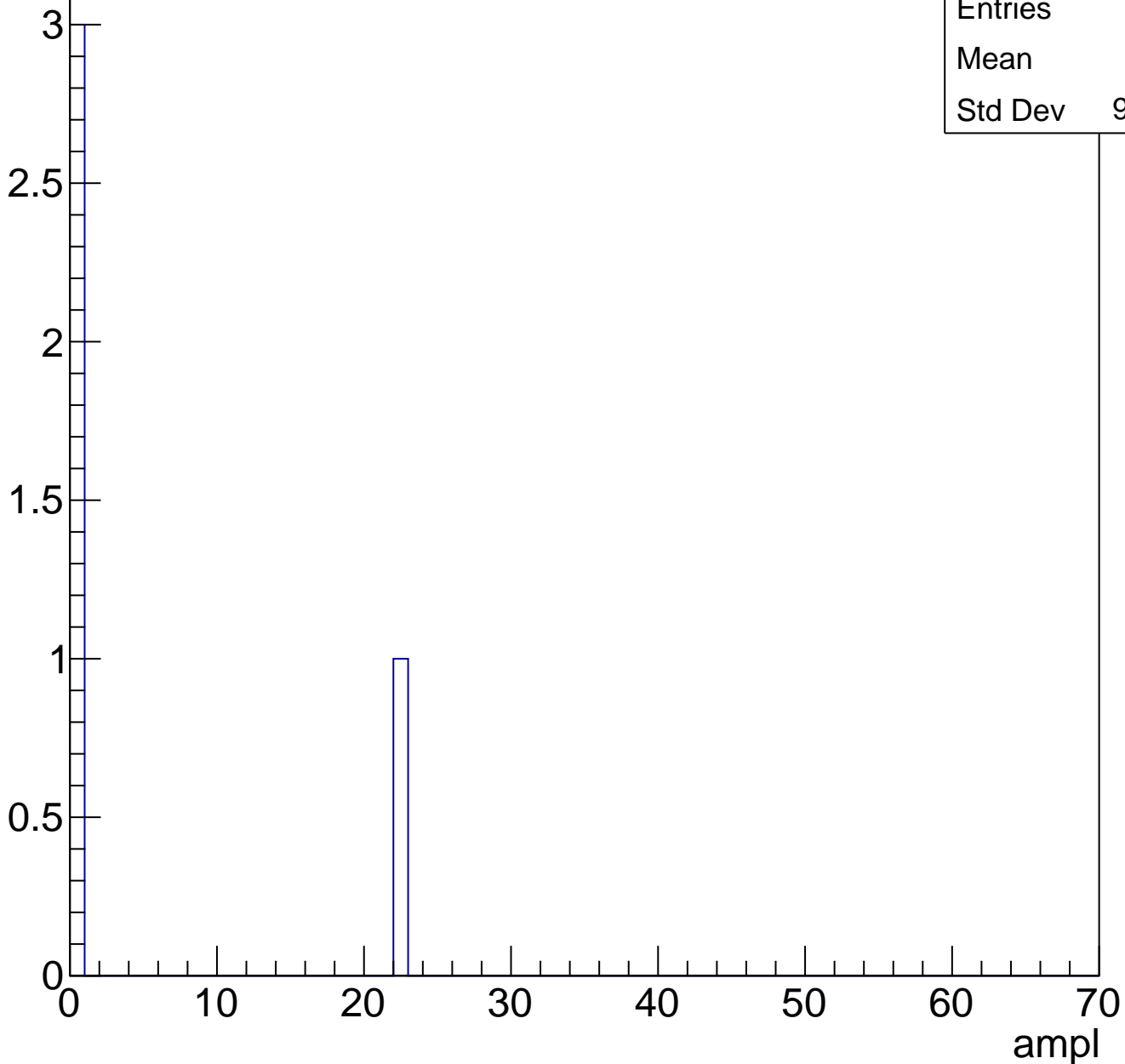
Entries	20
Mean	61.25
Std Dev	1.813



B0L101S, U18-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch116, adc0

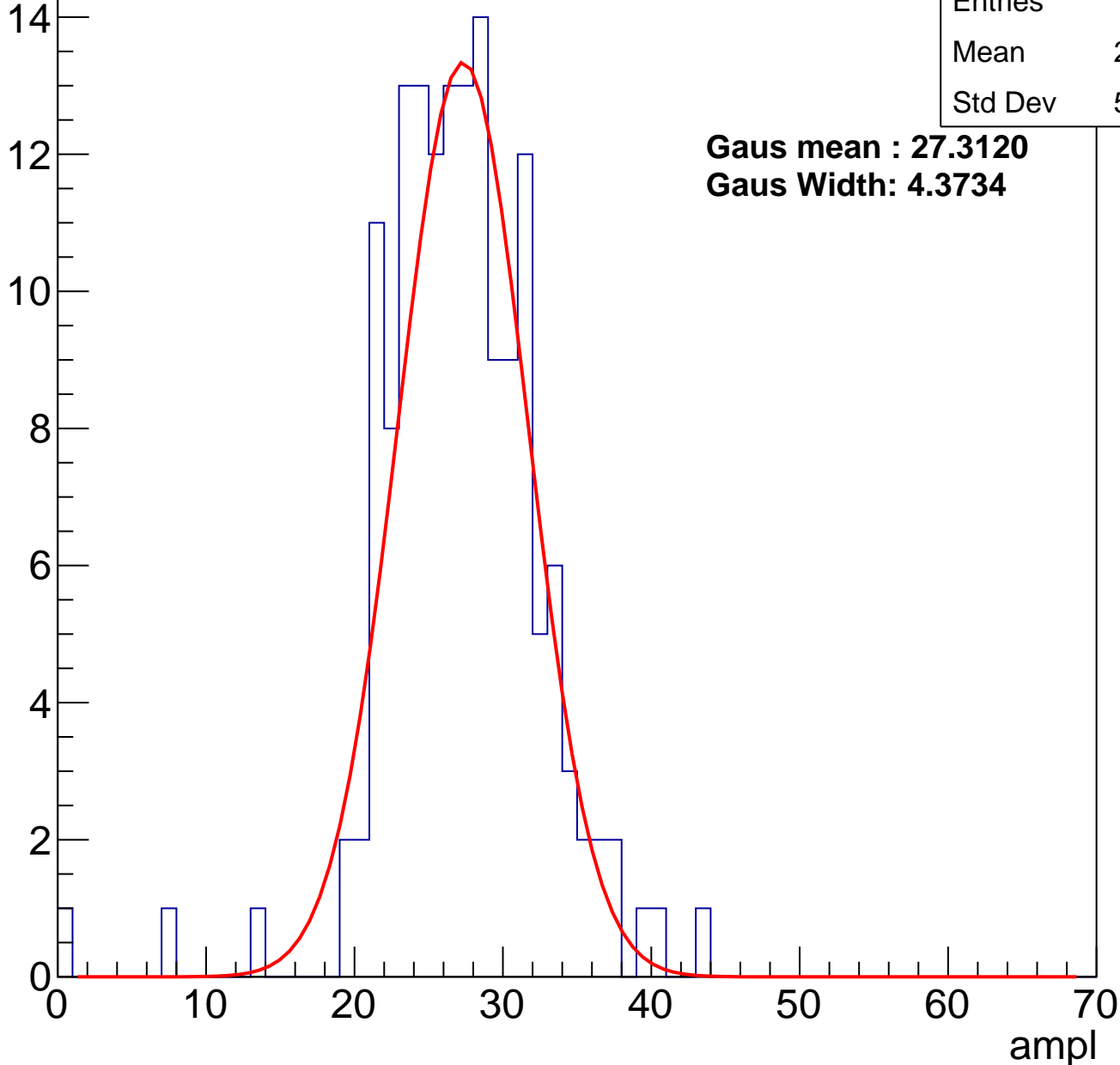
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	26.74
Std Dev	5.289

Gaus mean : 27.3120

Gaus Width: 4.3734

Entry

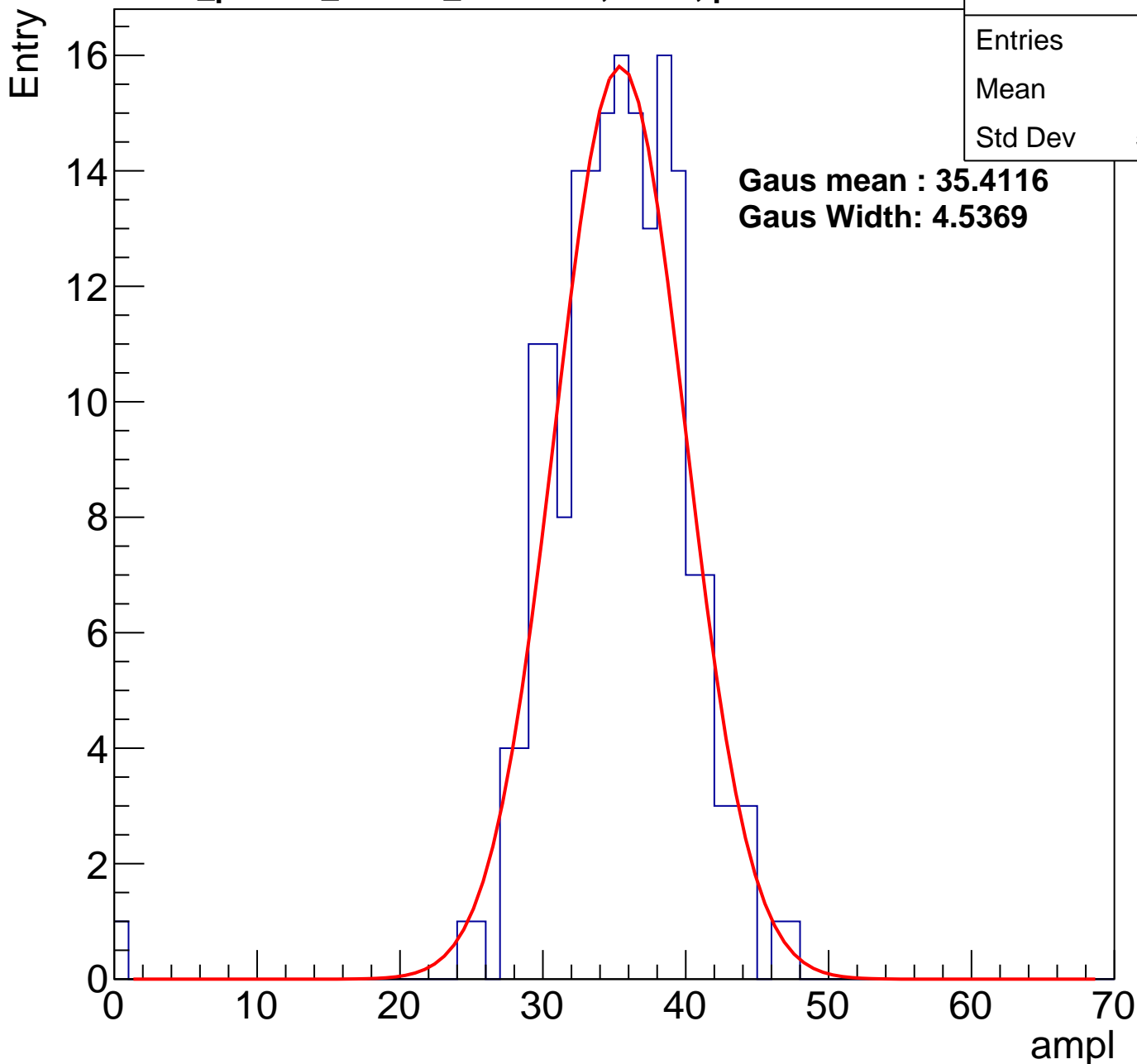


B0L101S, U18-ch116, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	183
Mean	34.8
Std Dev	5.011

Gaus mean : 35.4116
Gaus Width: 4.5369



B0L101S, U18-ch116, adc2

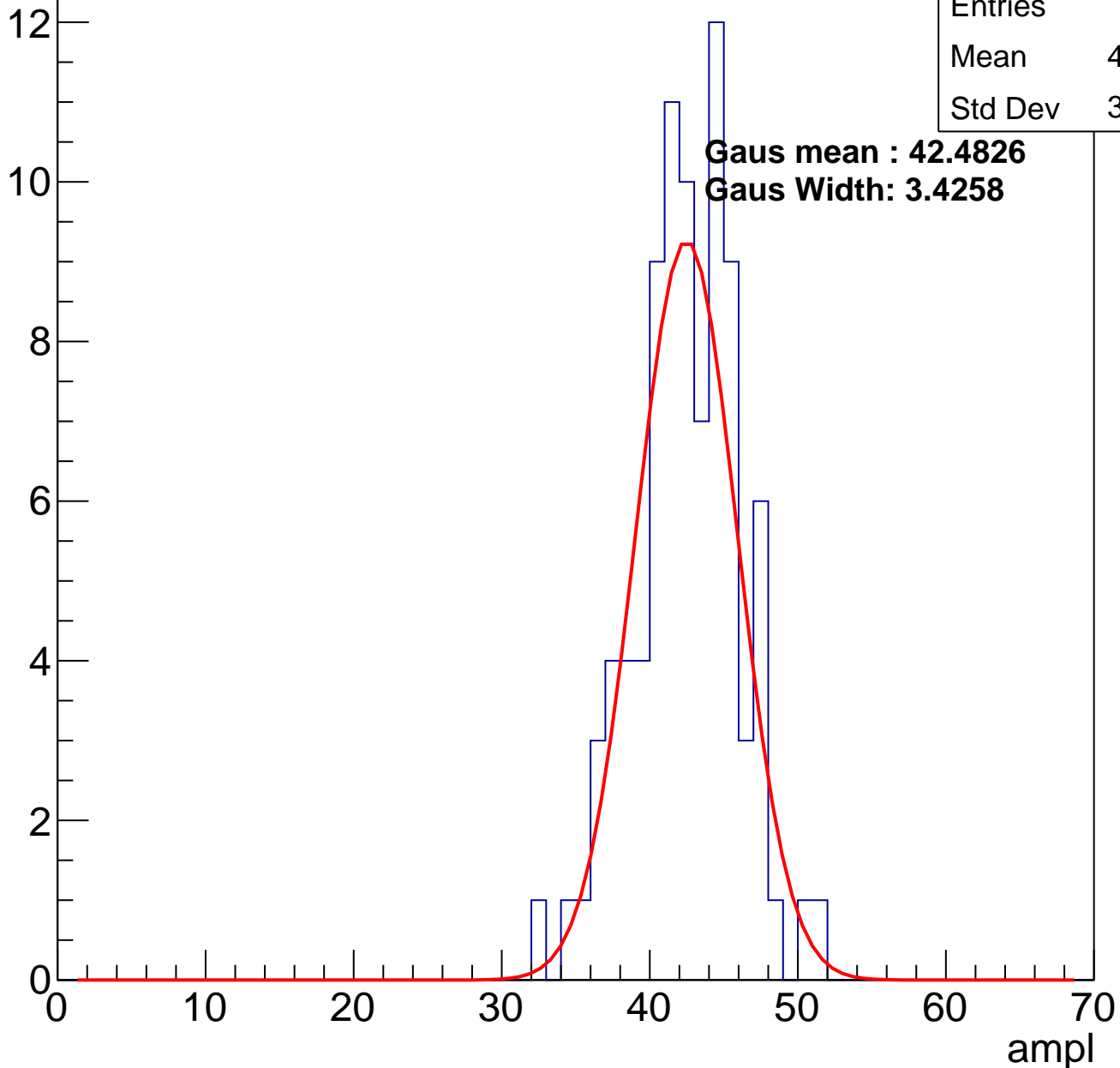
calib_packv5_042523_0143.root, FC#1, port C1

Entries	88
Mean	42.03
Std Dev	3.508

Gaus mean : 42.4826

Gaus Width: 3.4258

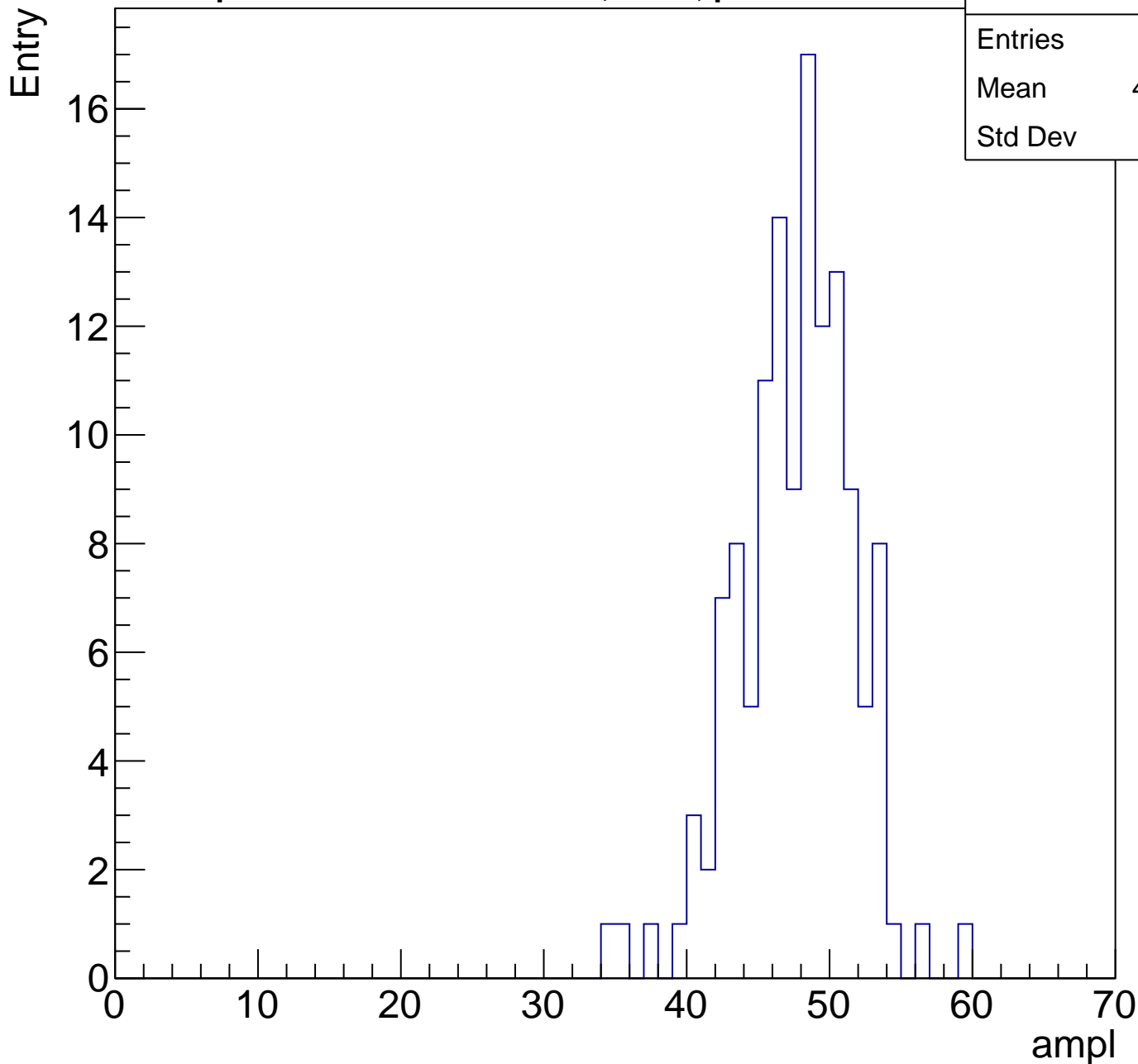
Entry



B0L101S, U18-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

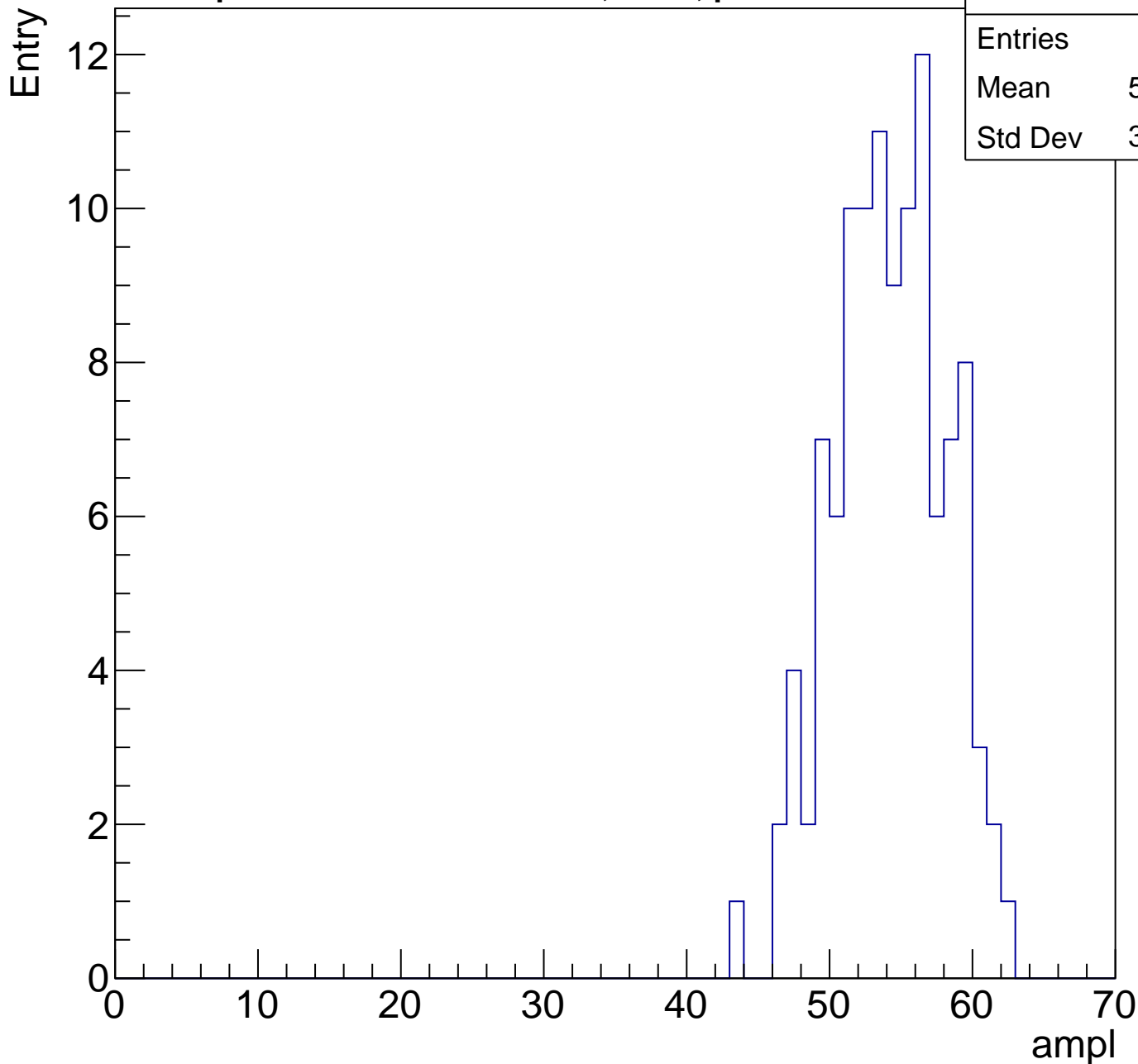
Entries	130
Mean	47.18
Std Dev	4.03



B0L101S, U18-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	53.76
Std Dev	3.816

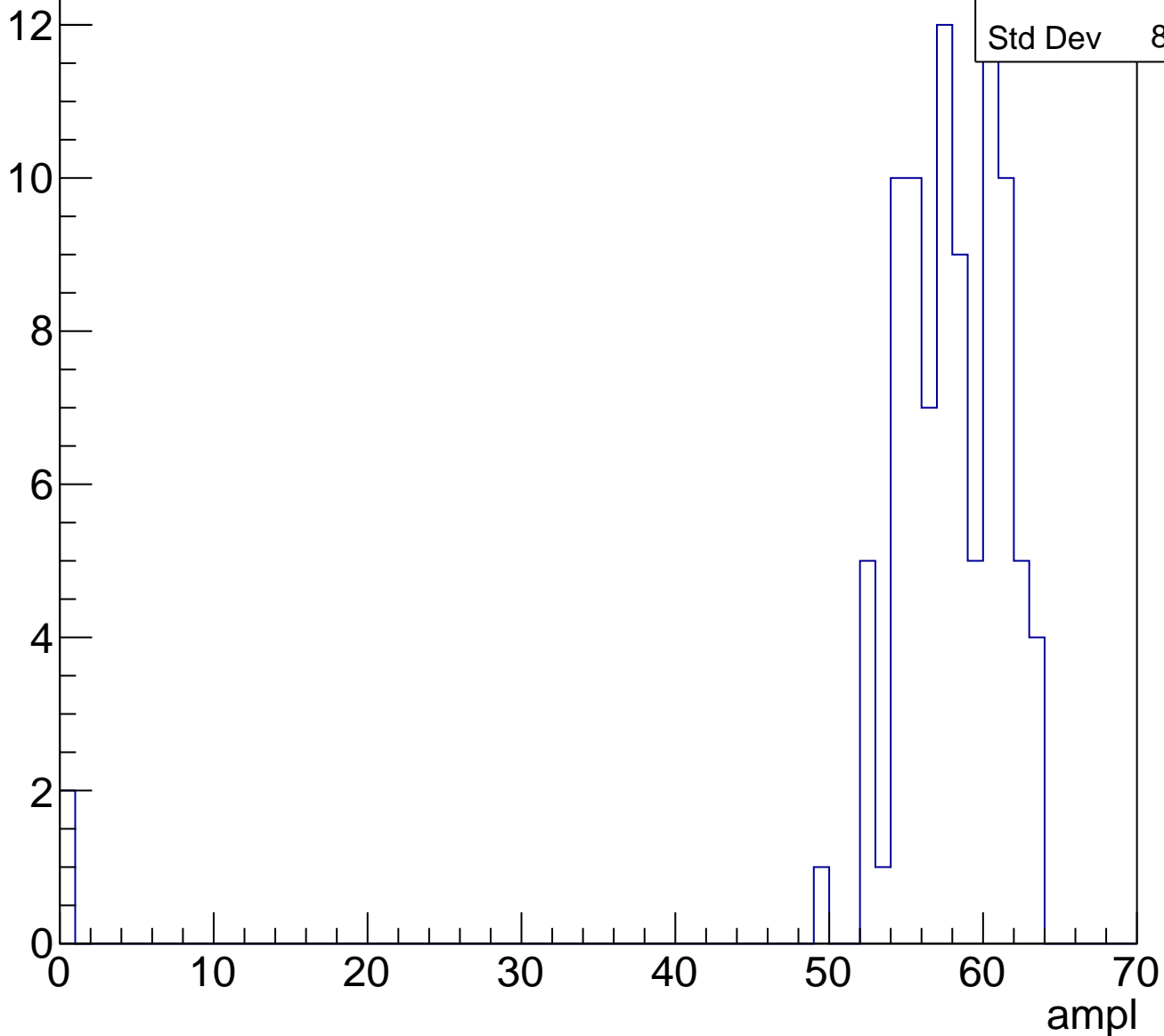


B0L101S, U18-ch116, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	56.35
Std Dev	8.858

Entry



B0L101S, U18-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	48
Mean	61
Std Dev	1.803

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

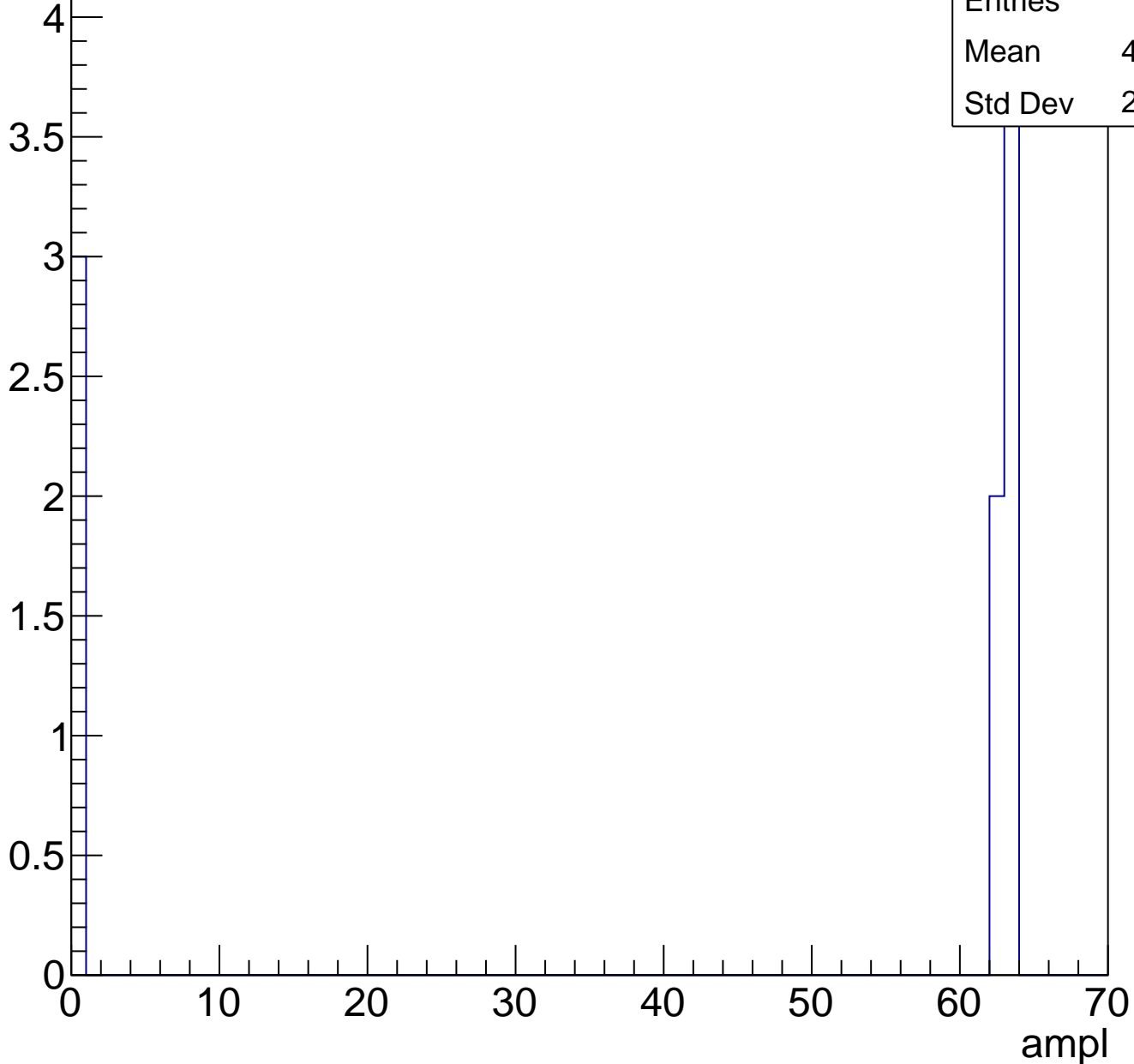
ampl

70

B0L101S, U18-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch117, adc0

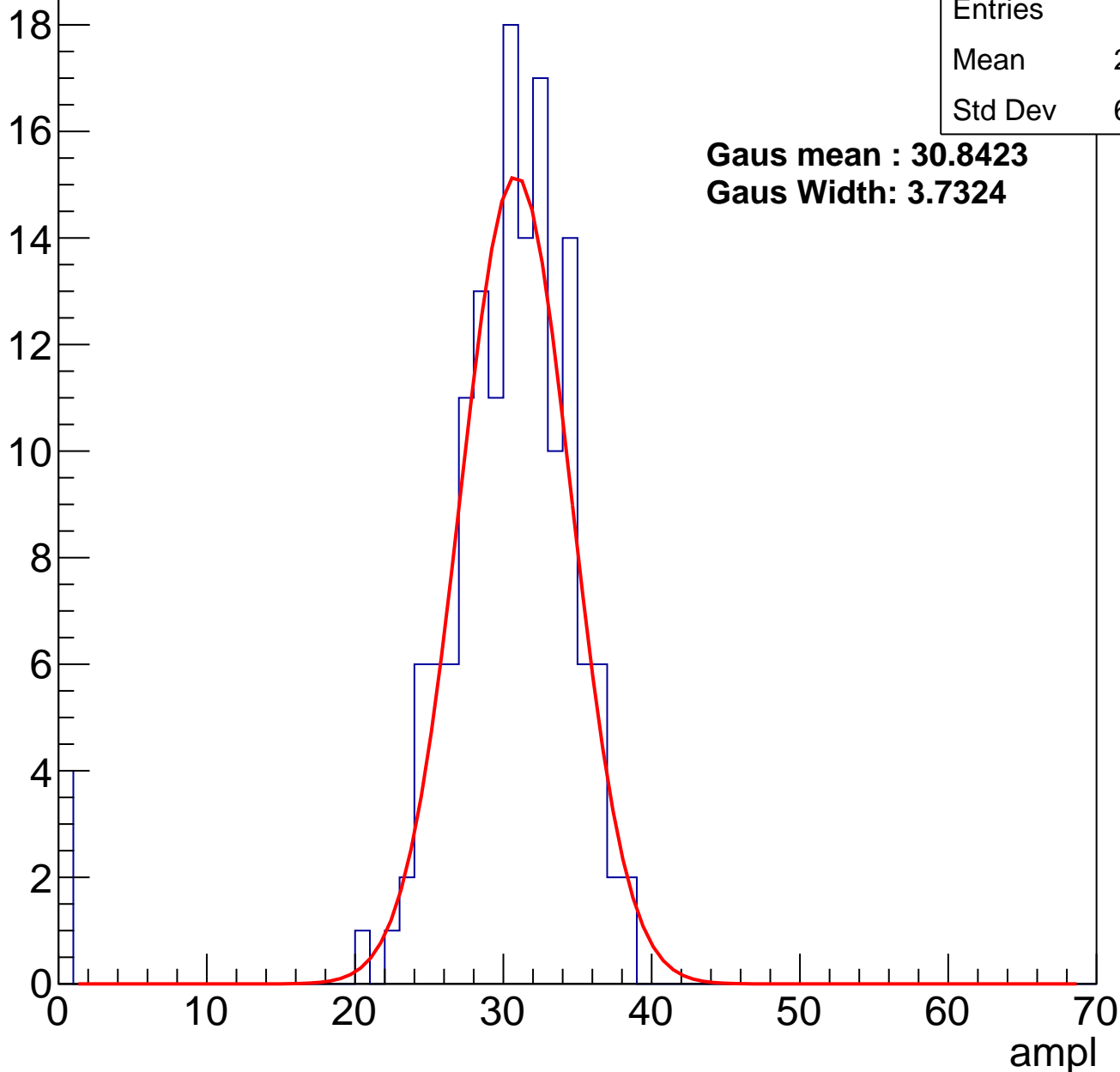
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	29.45
Std Dev	6.016

Gaus mean : 30.8423

Gaus Width: 3.7324

Entry



B0L101S, U18-ch117, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	37.08
Std Dev	3.714

Gaus mean : 37.6665

Gaus Width: 3.5271

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

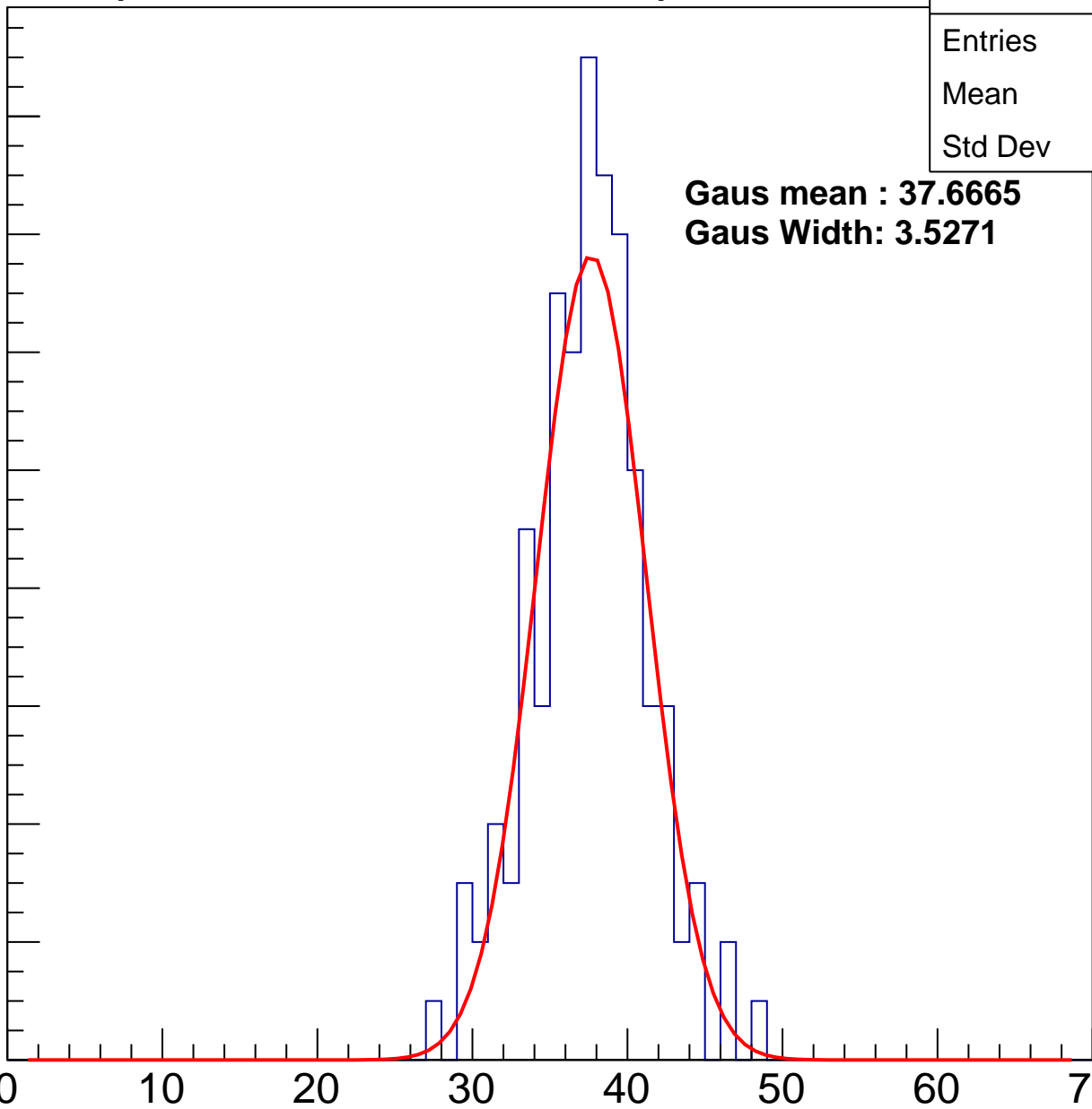
40

50

60

70

ampl



B0L101S, U18-ch117, adc2

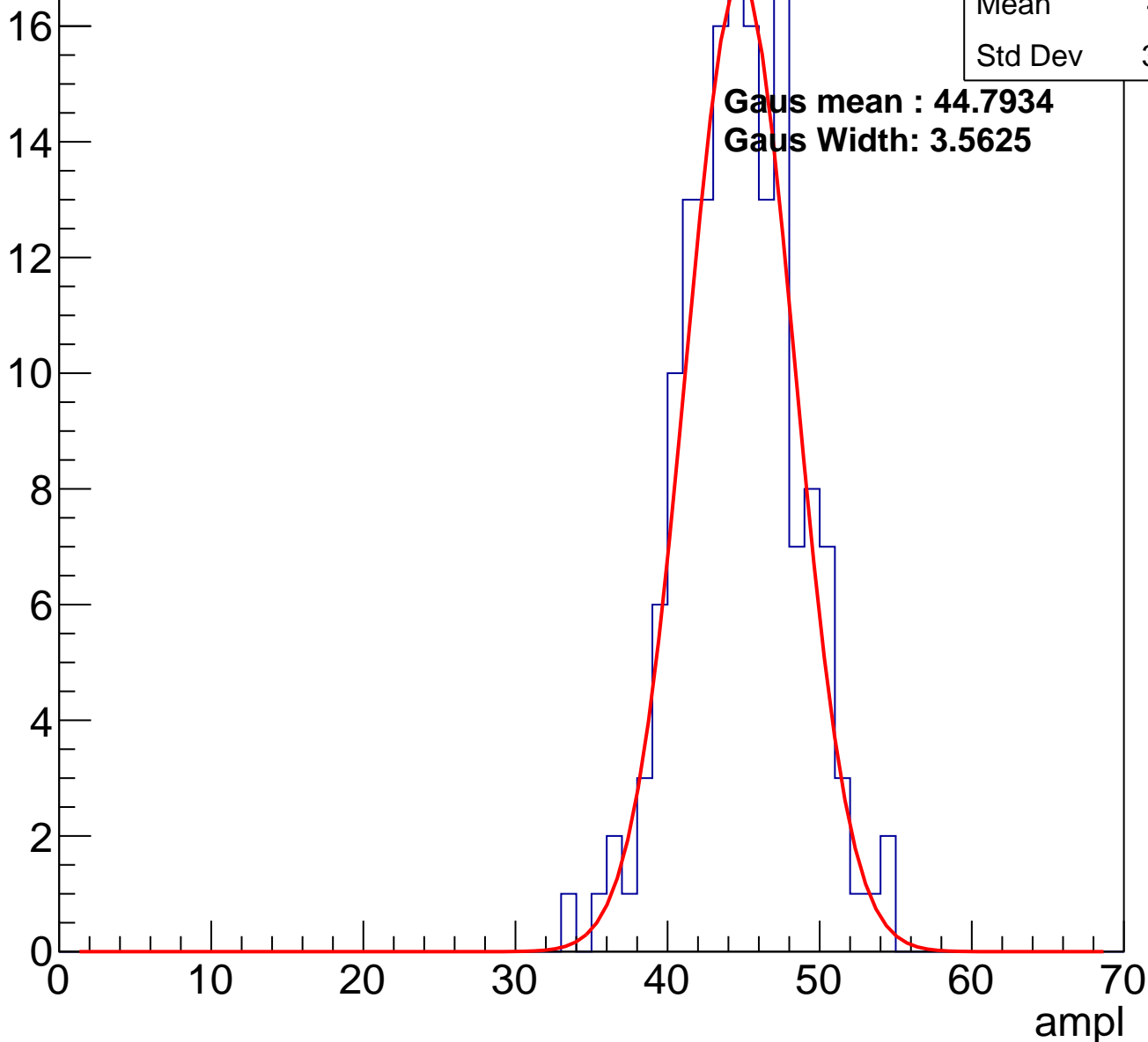
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	158
Mean	44.31
Std Dev	3.768

Gaus mean : 44.7934

Gaus Width: 3.5625



B0L101S, U18-ch117, adc3

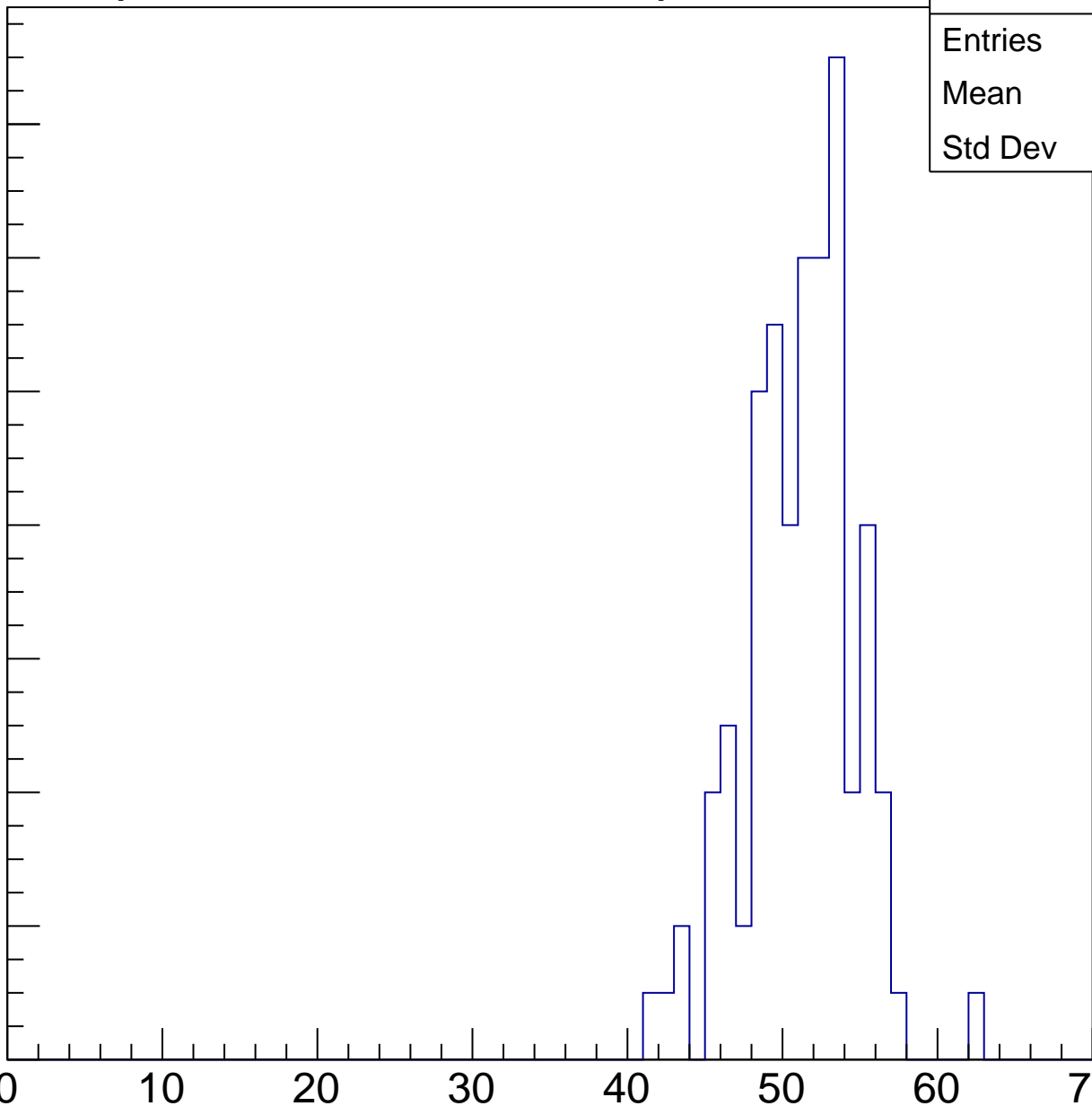
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	50.71
Std Dev	3.499

ampl

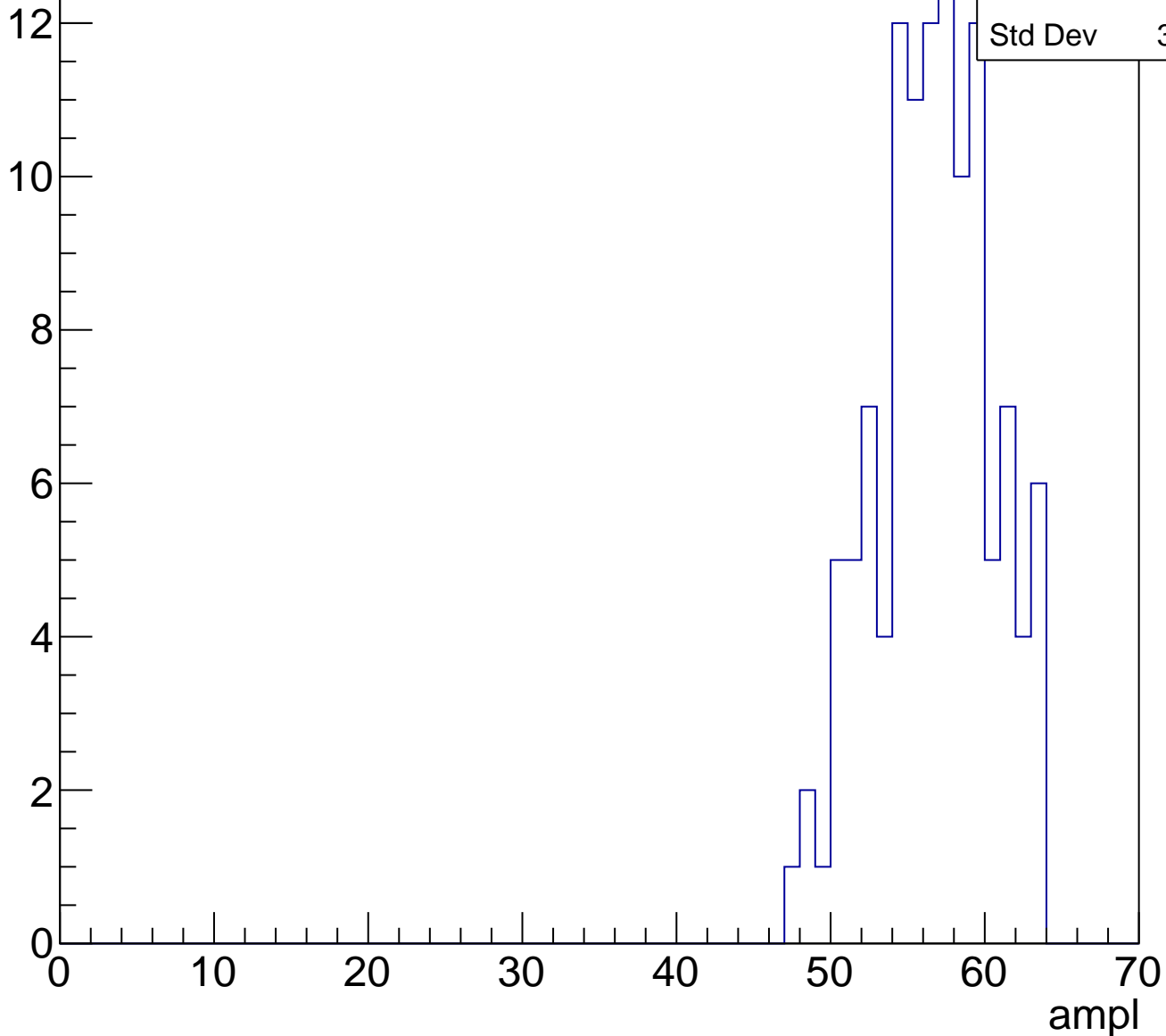


B0L101S, U18-ch117, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	56.24
Std Dev	3.718

Entry



B0L101S, U18-ch117, adc5

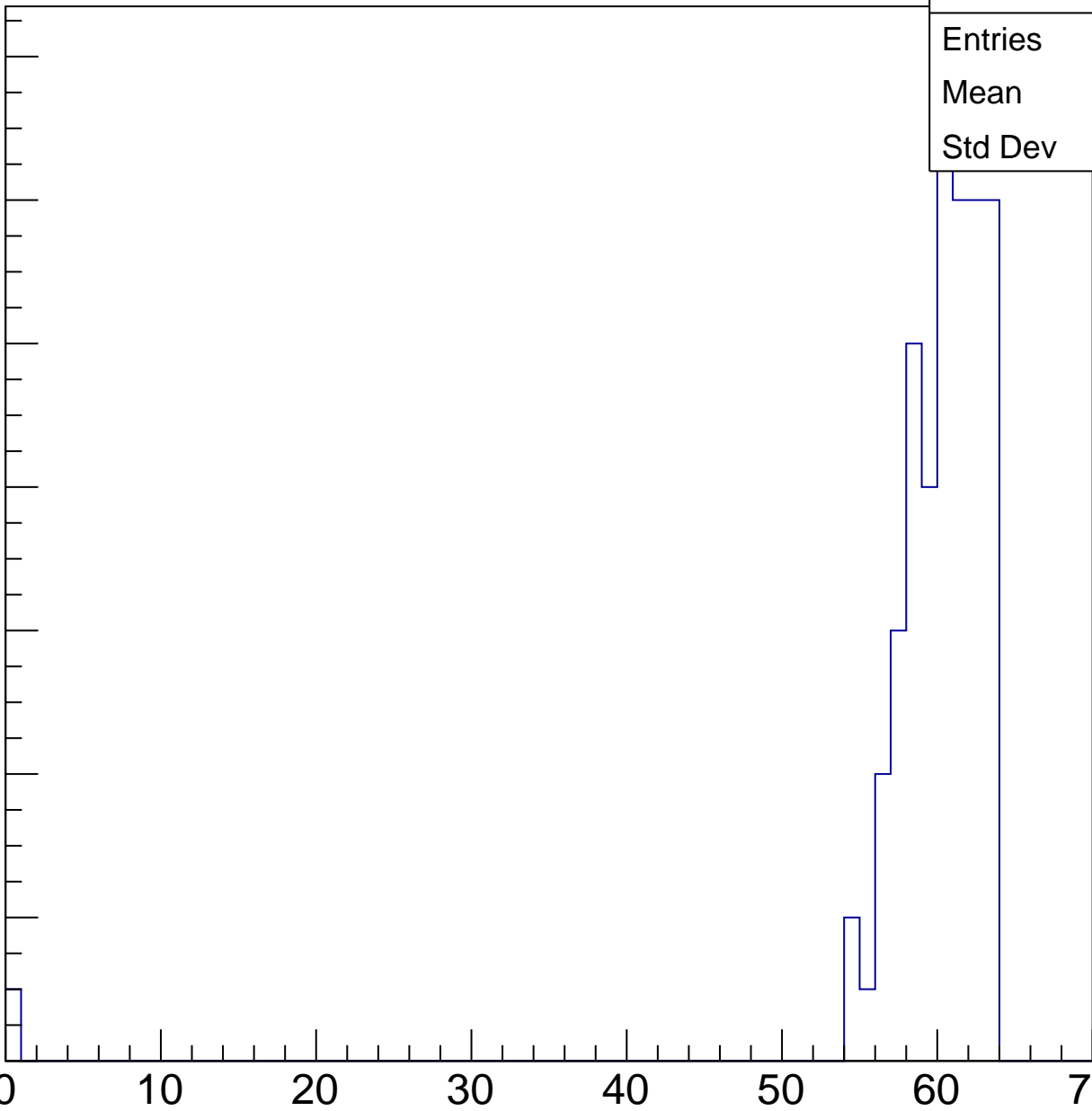
calib_packv5_042523_0143.root, FC#1, port C1

Entries	82
Mean	59.18
Std Dev	6.964

Entry

14
12
10
8
6
4
2
0

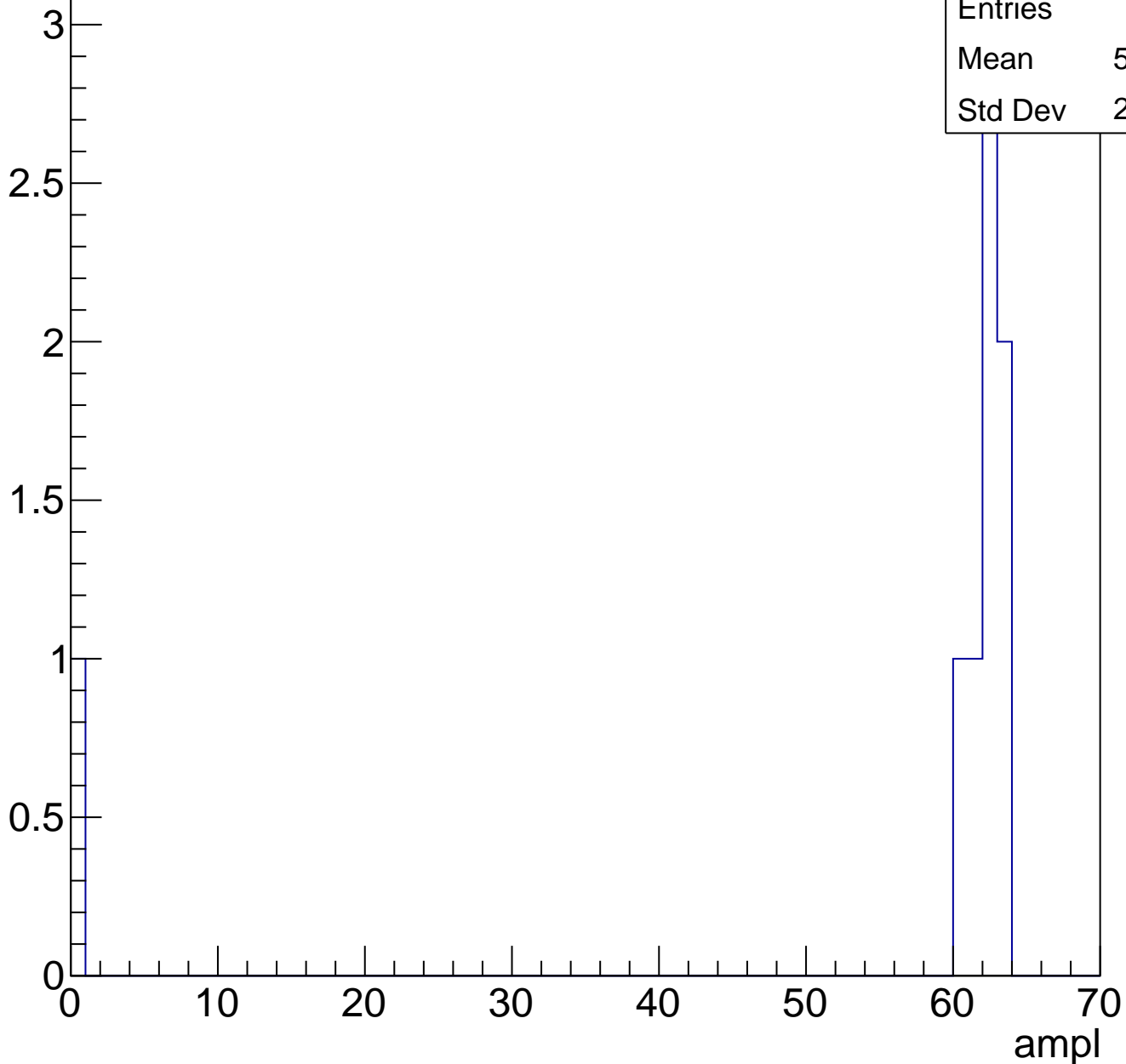
ampl



B0L101S, U18-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

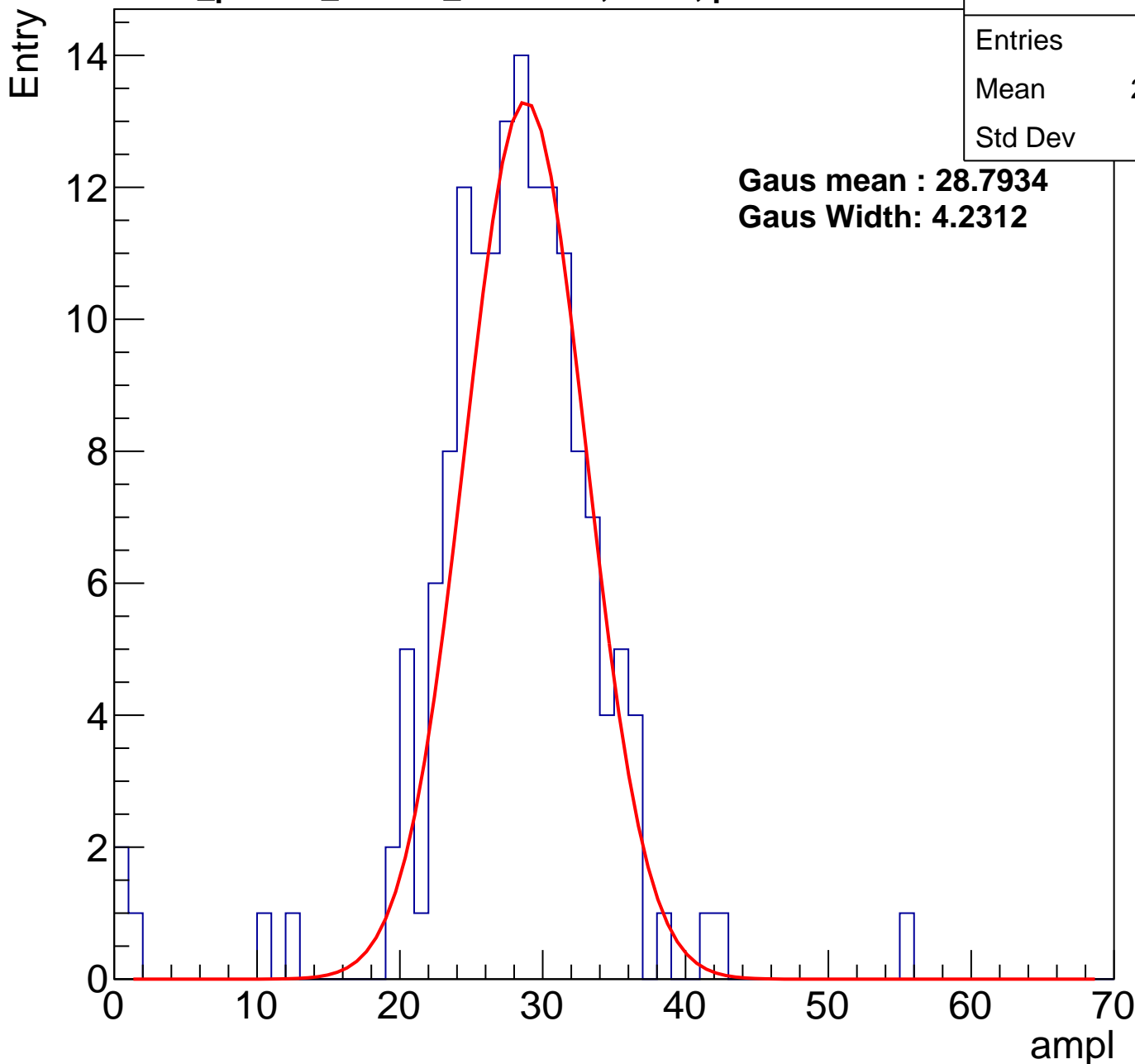
B0L101S, U18-ch118, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	27.46
Std Dev	6.44

Gaus mean : 28.7934

Gaus Width: 4.2312



B0L101S, U18-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	35.09
Std Dev	4.485

Gaus mean : 36.2519

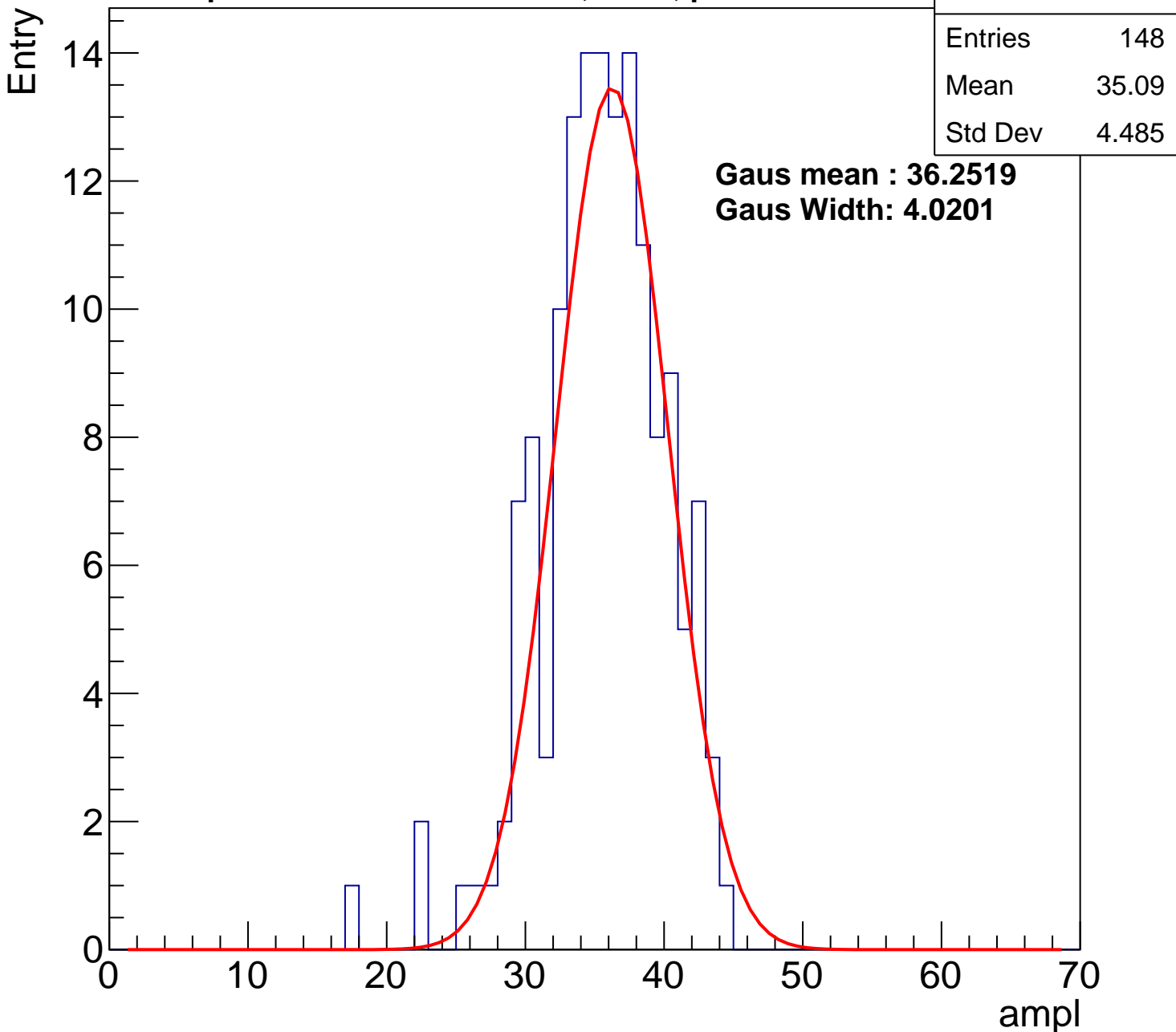
Gaus Width: 4.0201

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U18-ch118, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

114

Mean

42.08

Std Dev

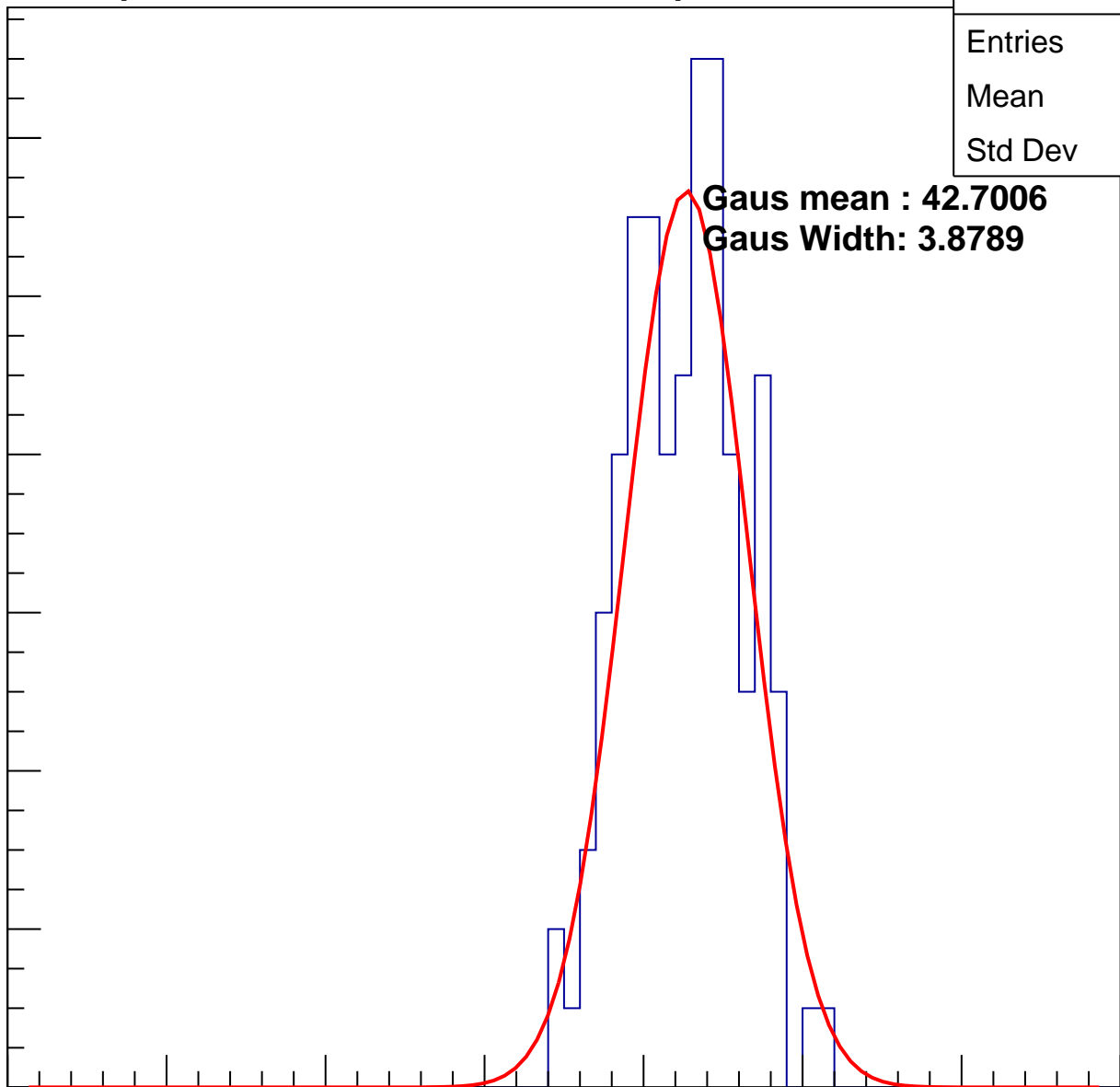
3.603

Gaus mean : 42.7006

Gaus Width: 3.8789

0 10 20 30 40 50 60 70

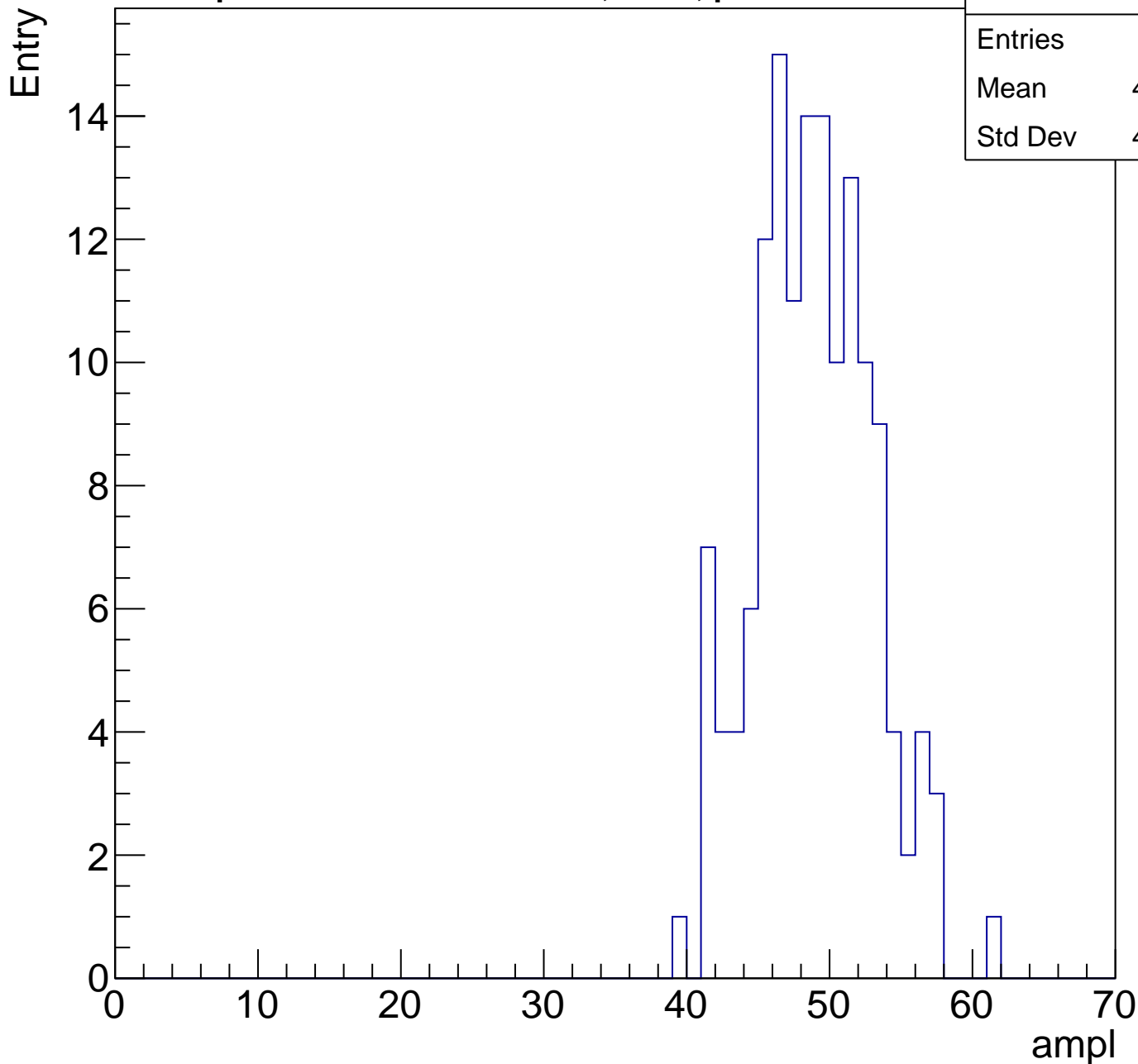
ampl



B0L101S, U18-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

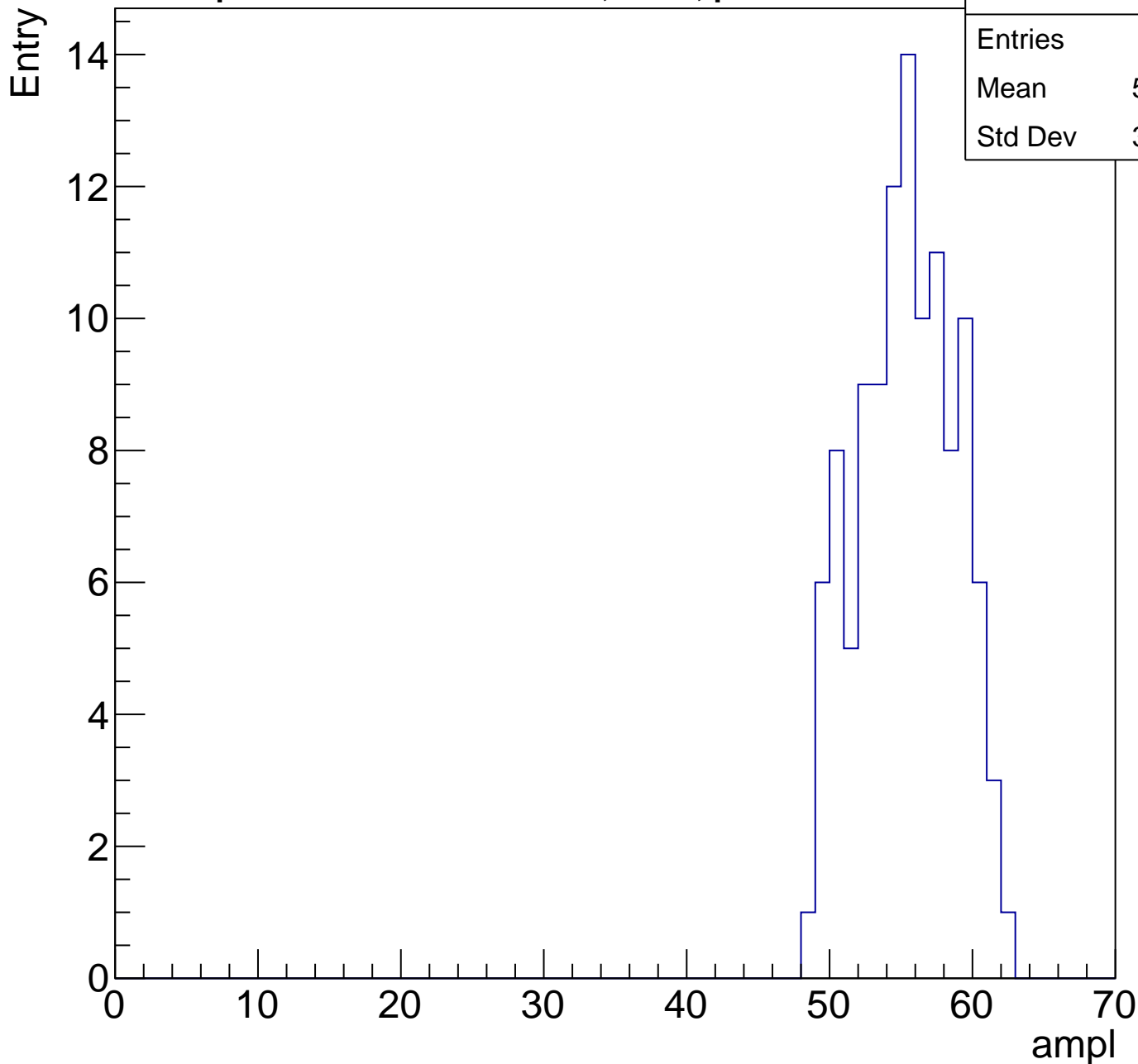
Entries	144
Mean	48.45
Std Dev	4.065



B0L101S, U18-ch118, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	54.92
Std Dev	3.348



B0L101S, U18-ch118, adc5

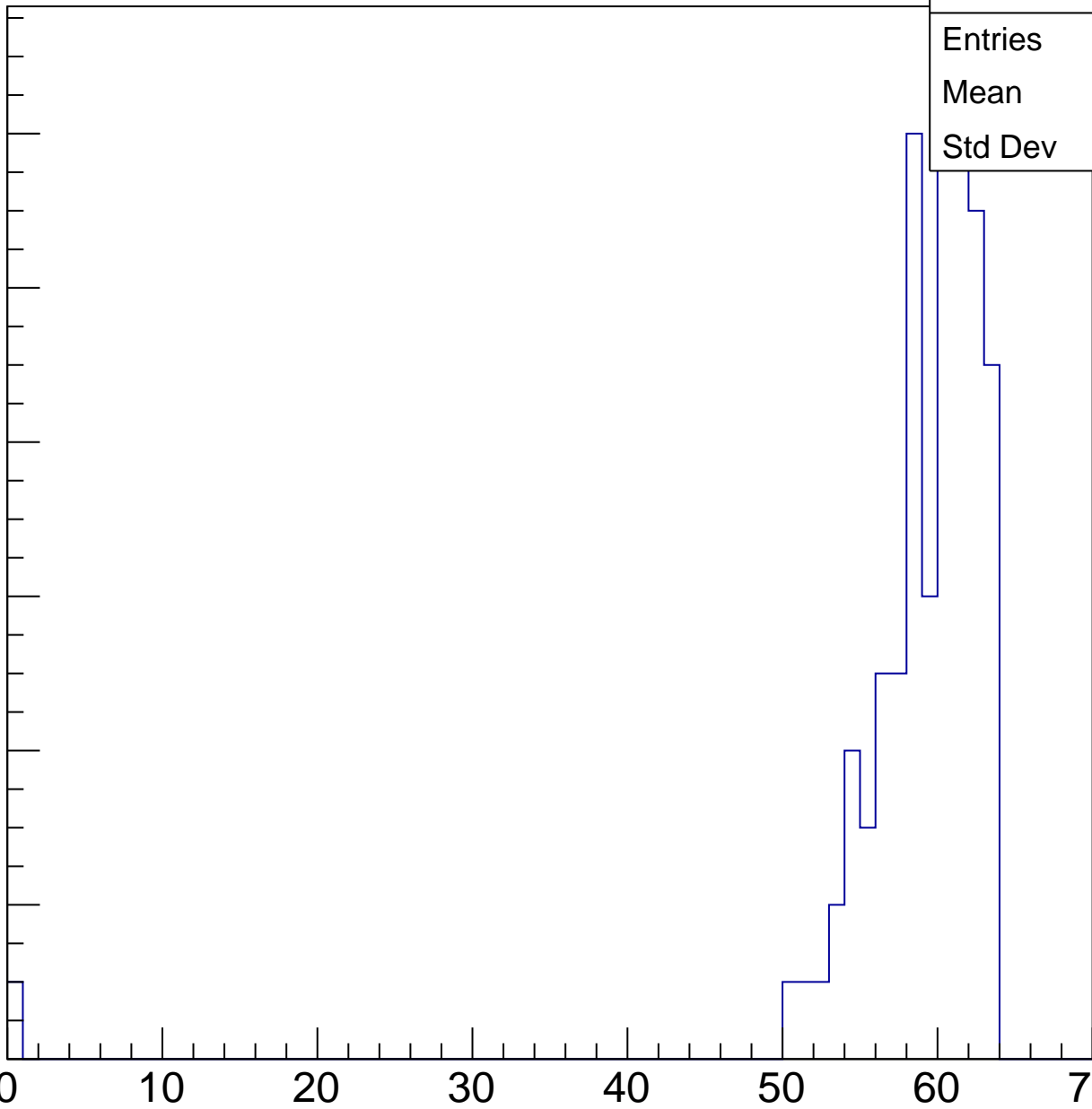
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	86
Mean	58.33
Std Dev	7.019

ampl

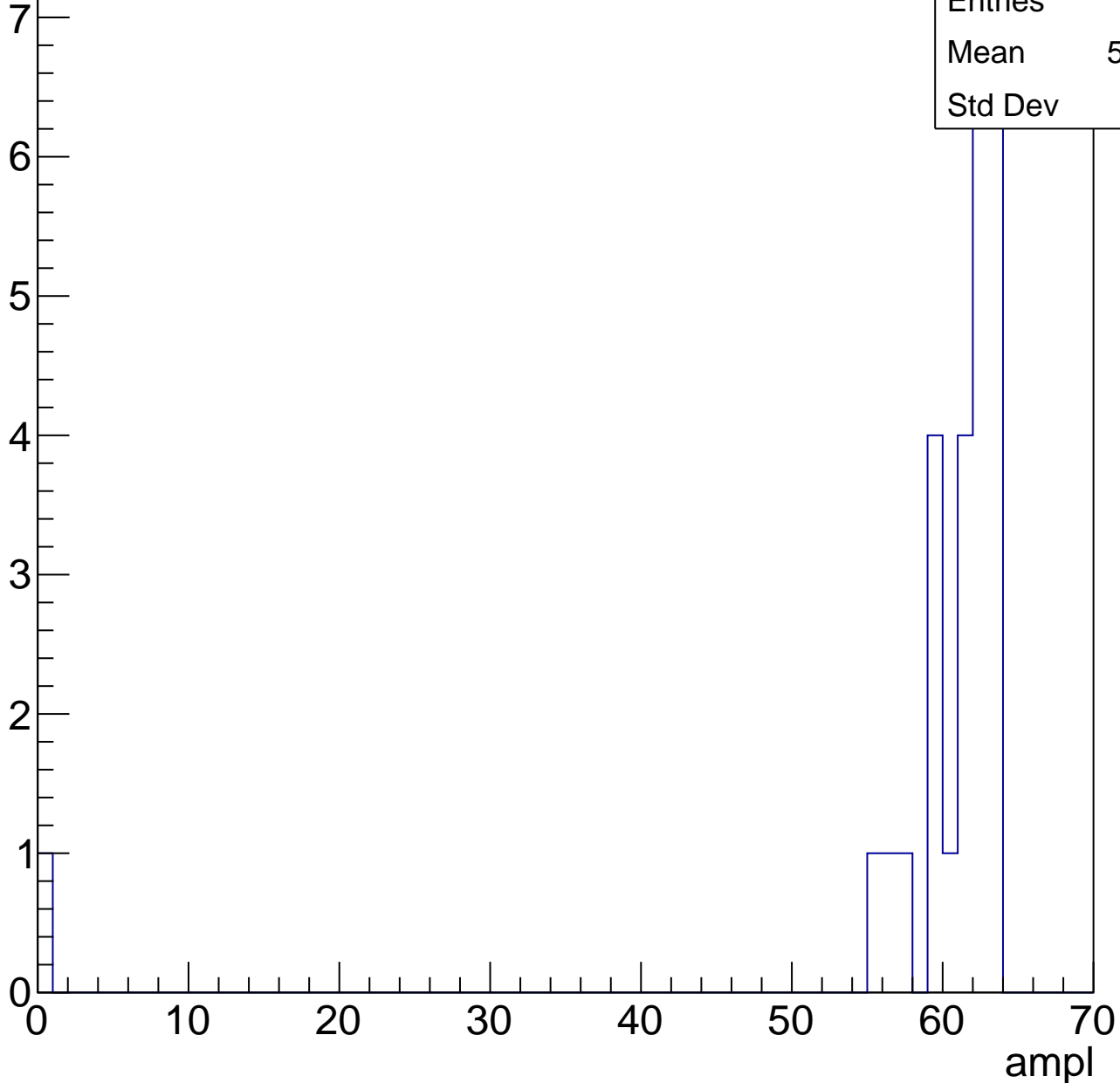


B0L101S, U18-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

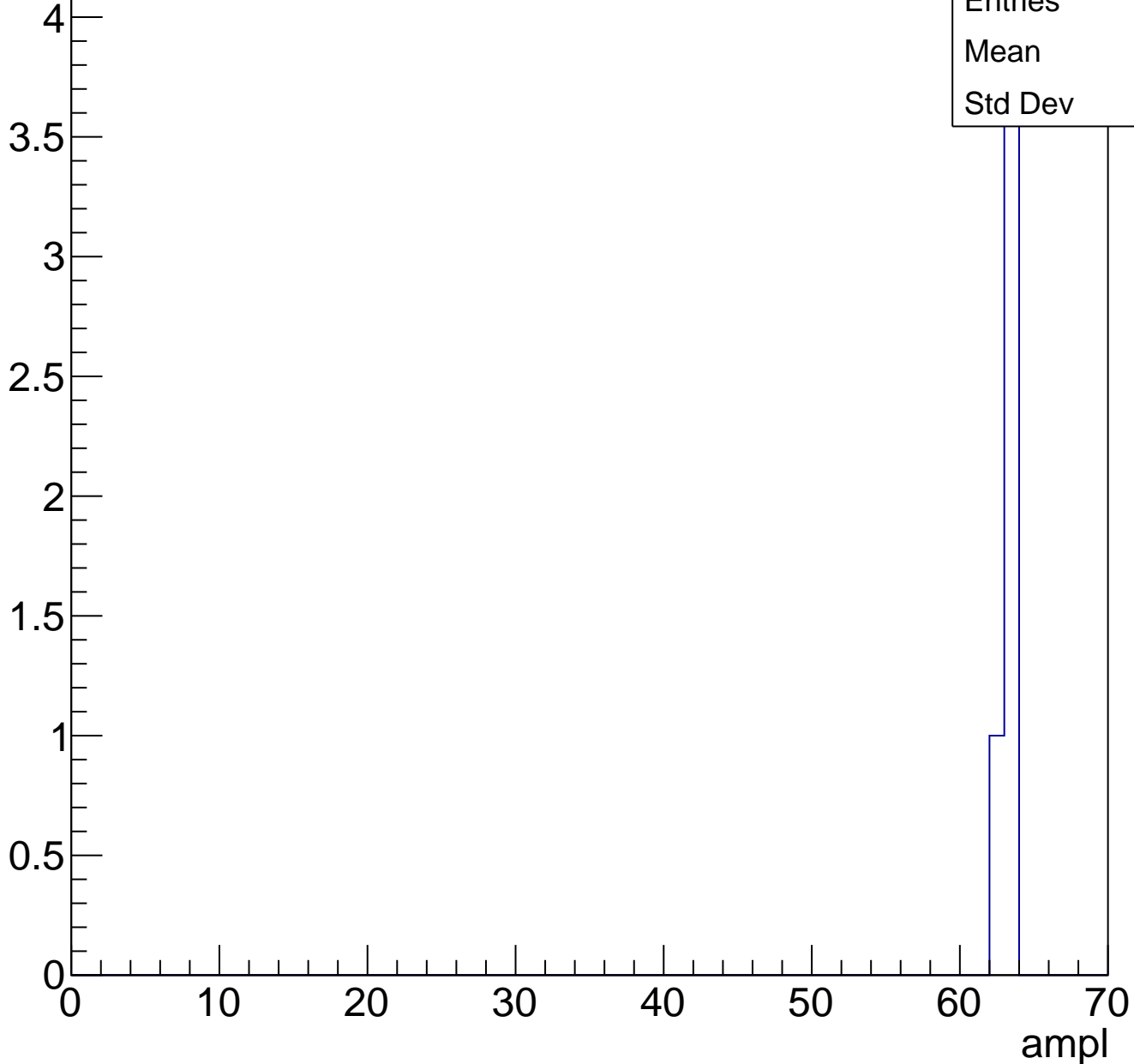
Entries	27
Mean	58.63
Std Dev	11.7



B0L101S, U18-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch119, adc0

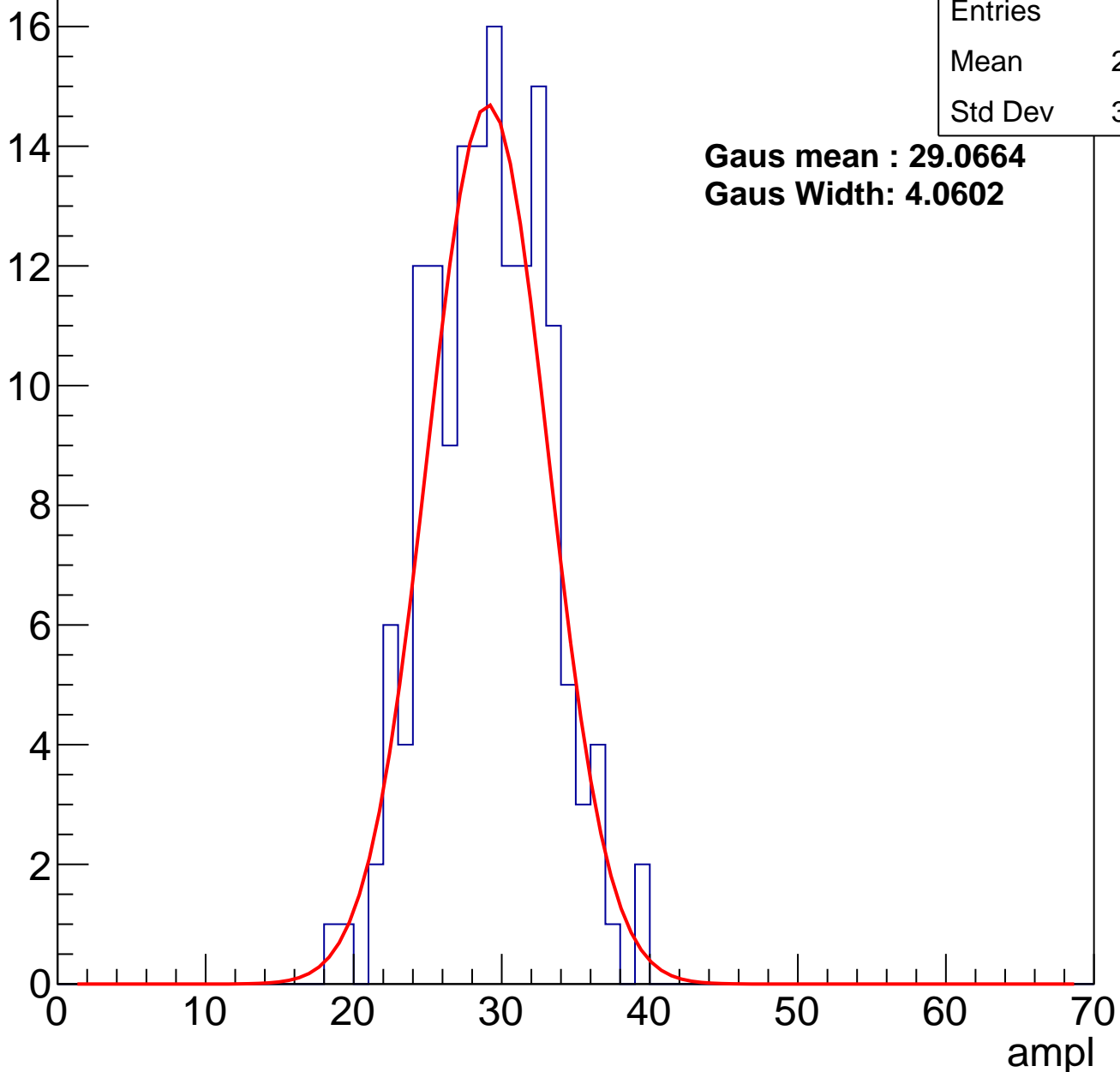
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	28.64
Std Dev	3.982

Gaus mean : 29.0664

Gaus Width: 4.0602

Entry



B0L101S, U18-ch119, adc1

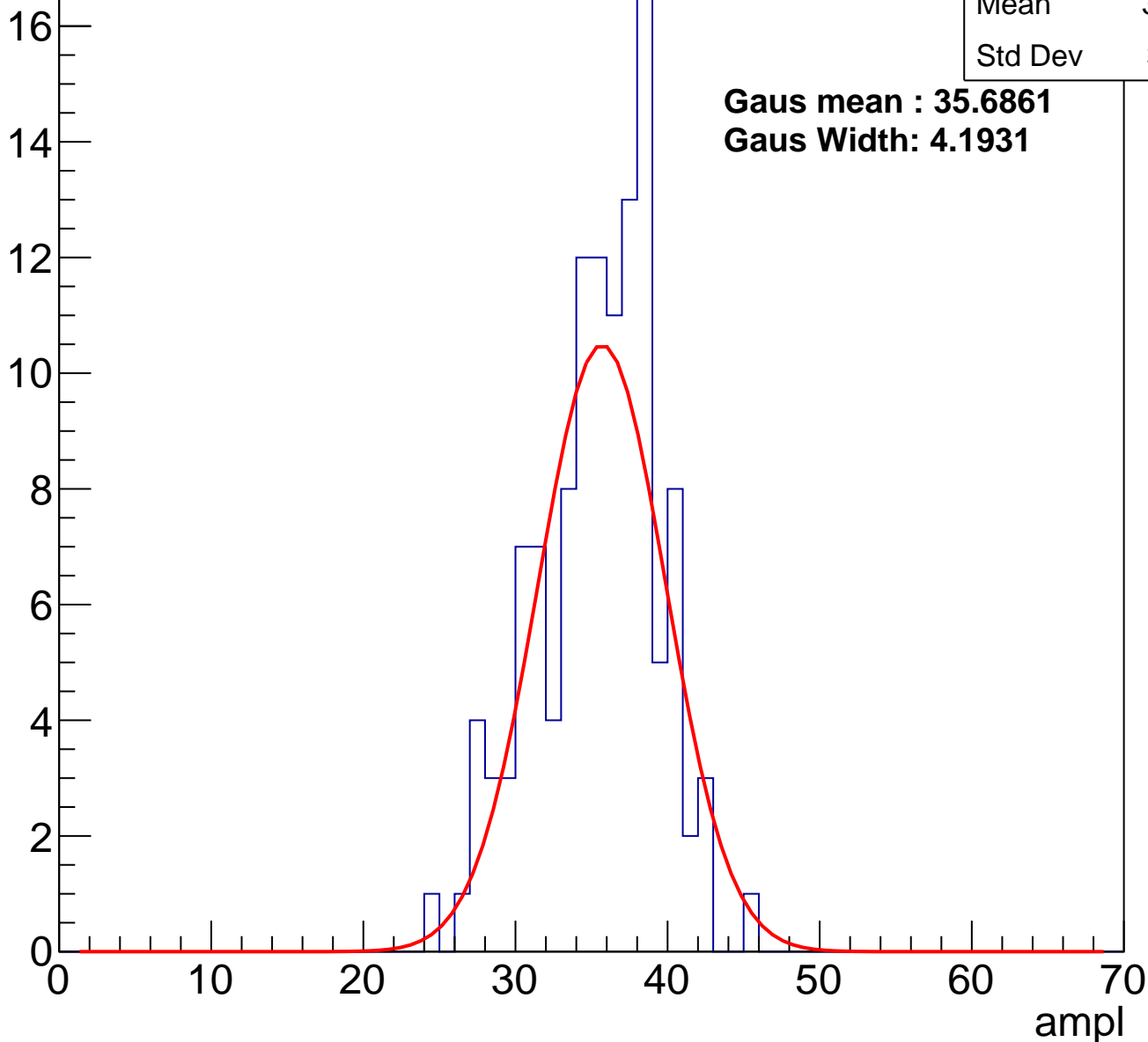
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	122
Mean	34.98
Std Dev	3.941

Gaus mean : 35.6861

Gaus Width: 4.1931



B0L101S, U18-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	41.76
Std Dev	3.852

Entry

14

12

10

8

6

4

2

0

Gaus mean : 43.0336

Gaus Width: 3.7494

0

10

20

30

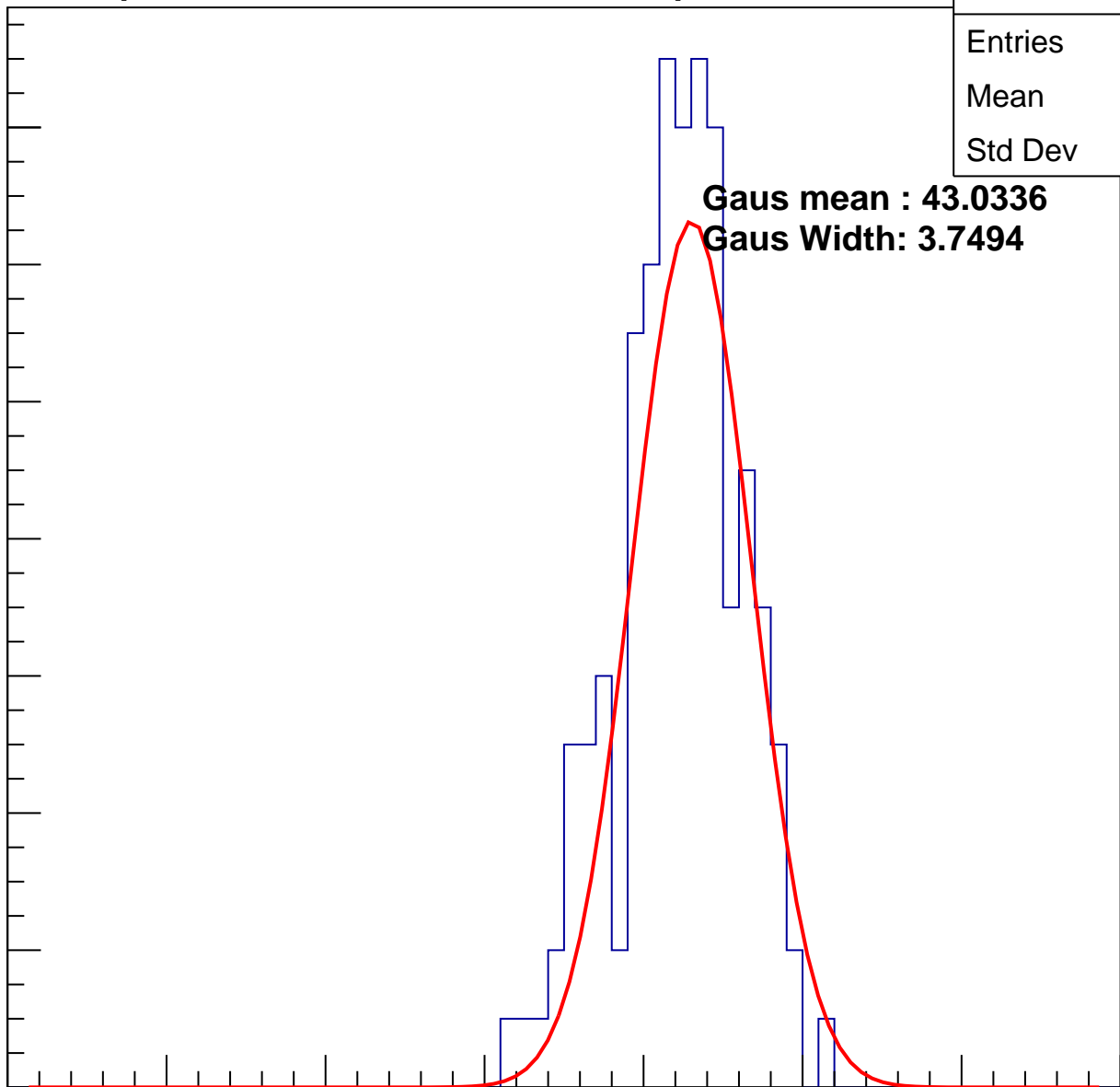
40

50

60

70

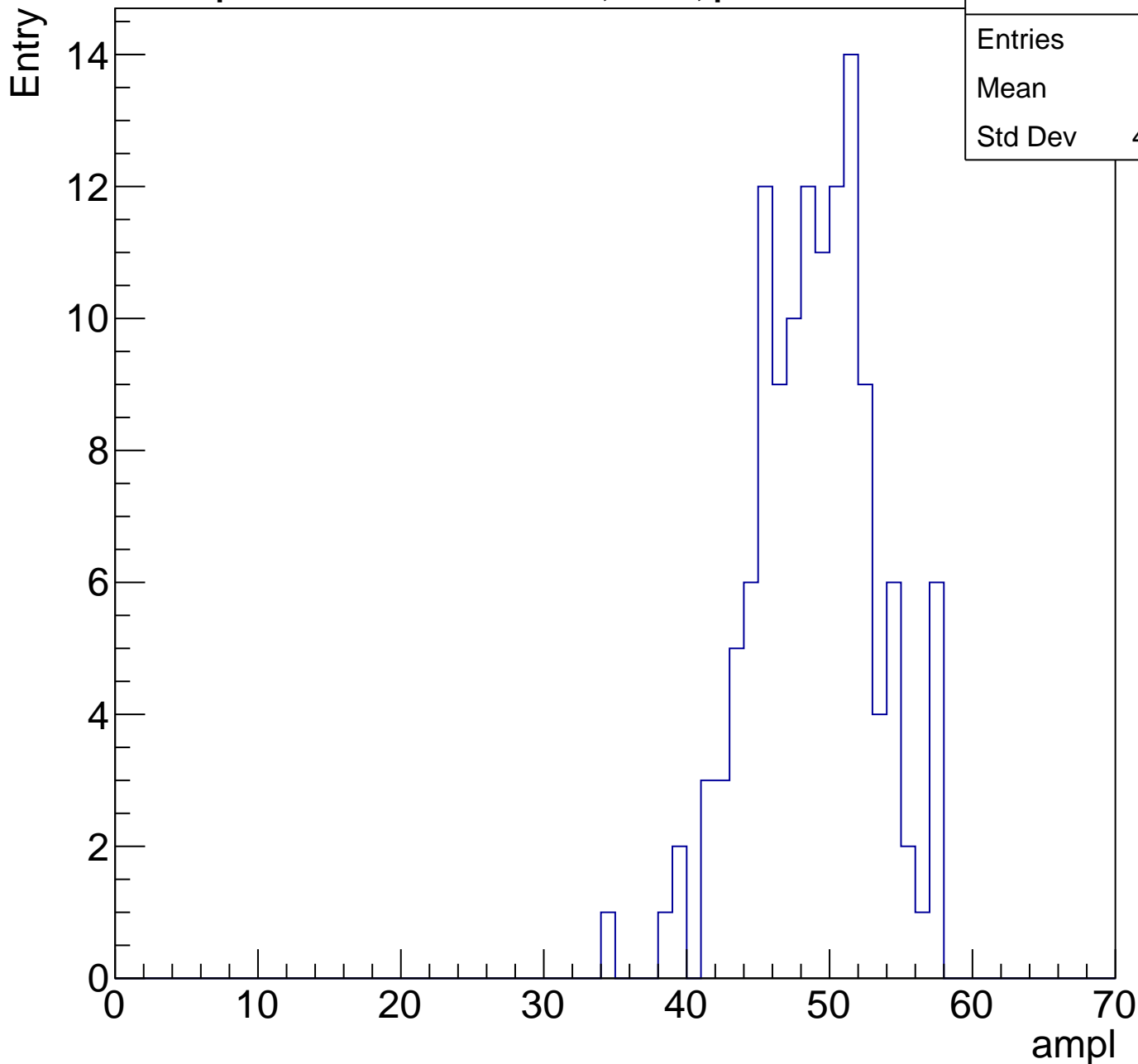
ampl



B0L101S, U18-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

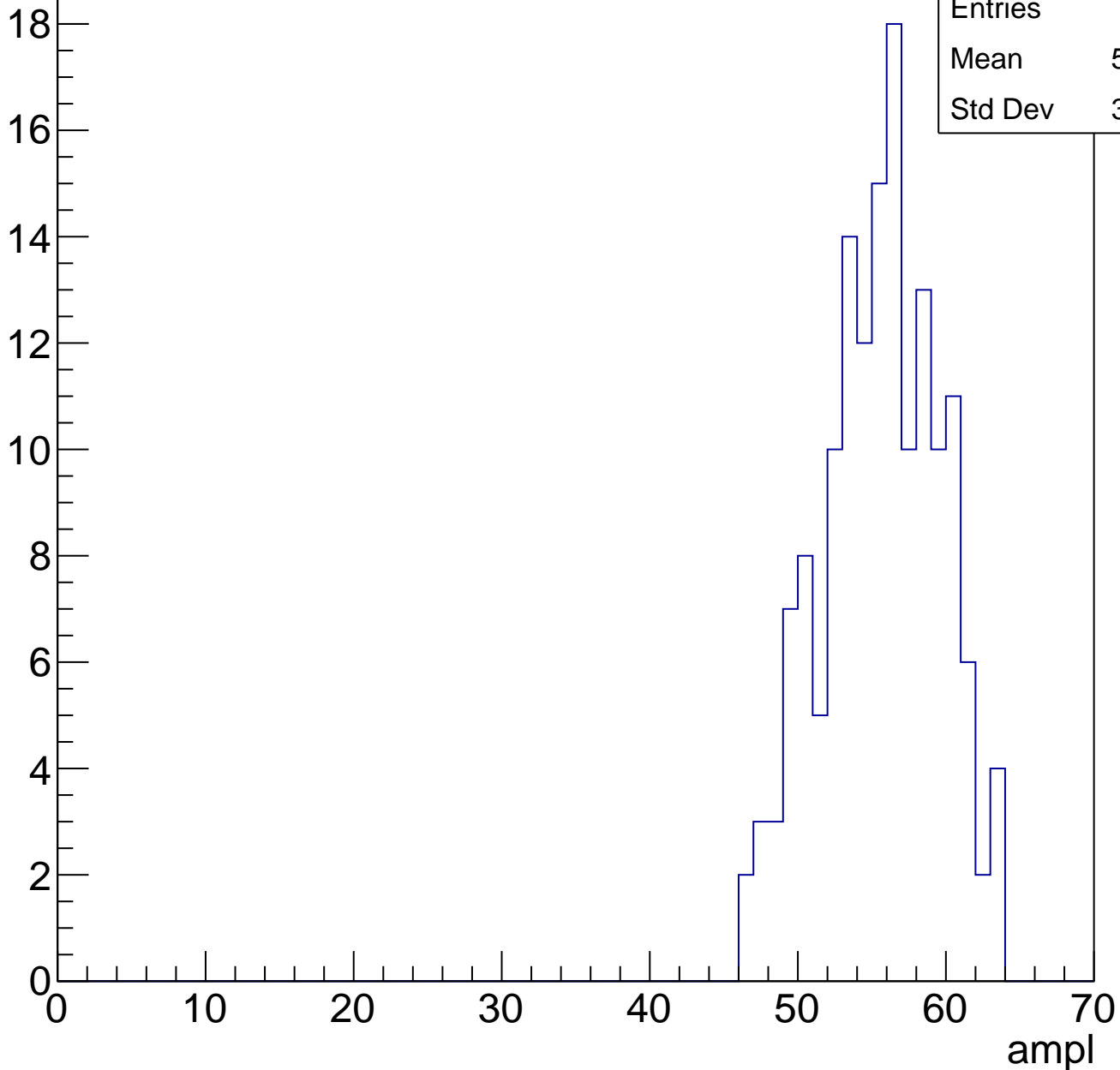
Entries	129
Mean	48.4
Std Dev	4.278



B0L101S, U18-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch119, adc5

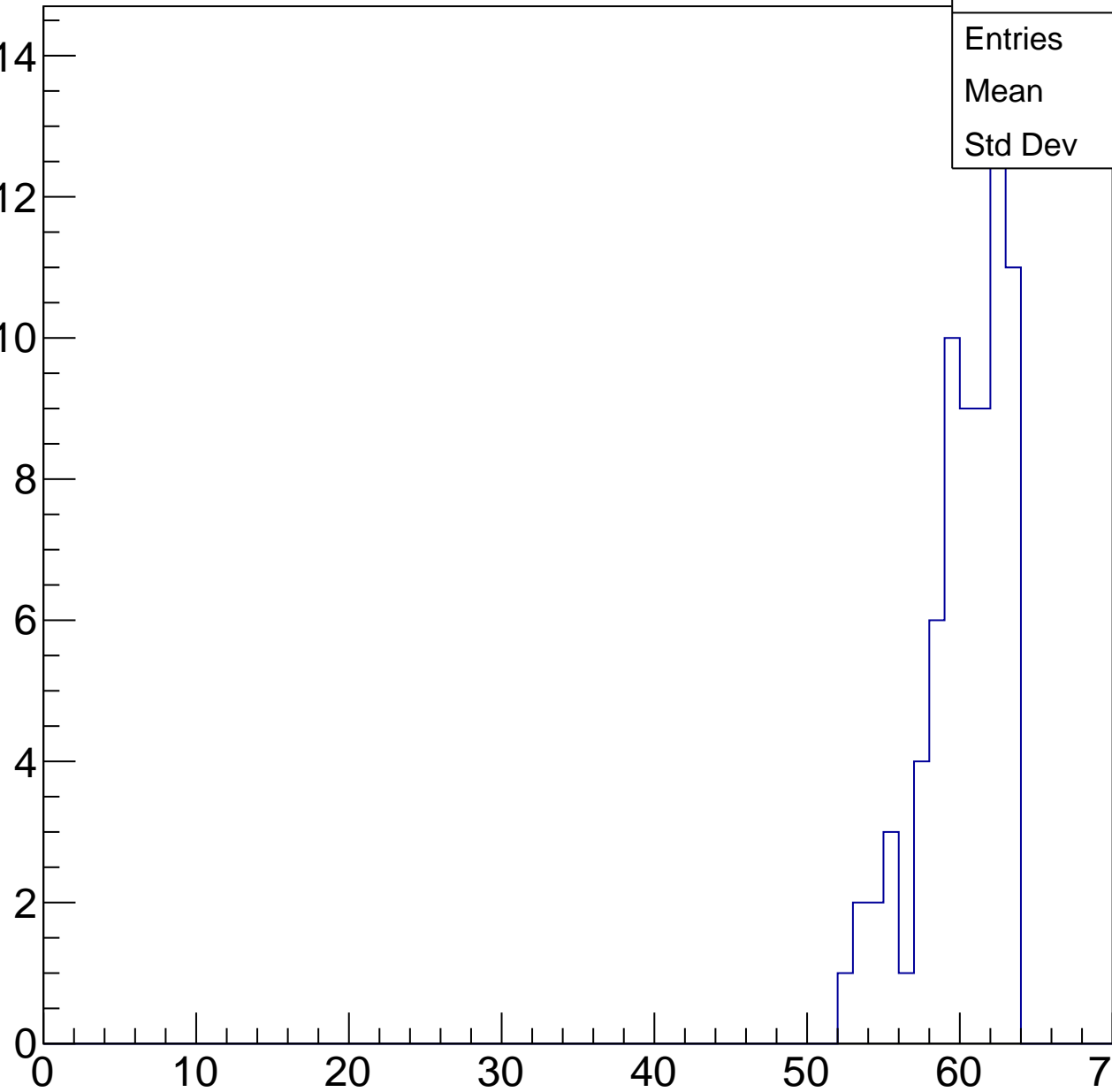
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.76
Std Dev	2.776

ampl

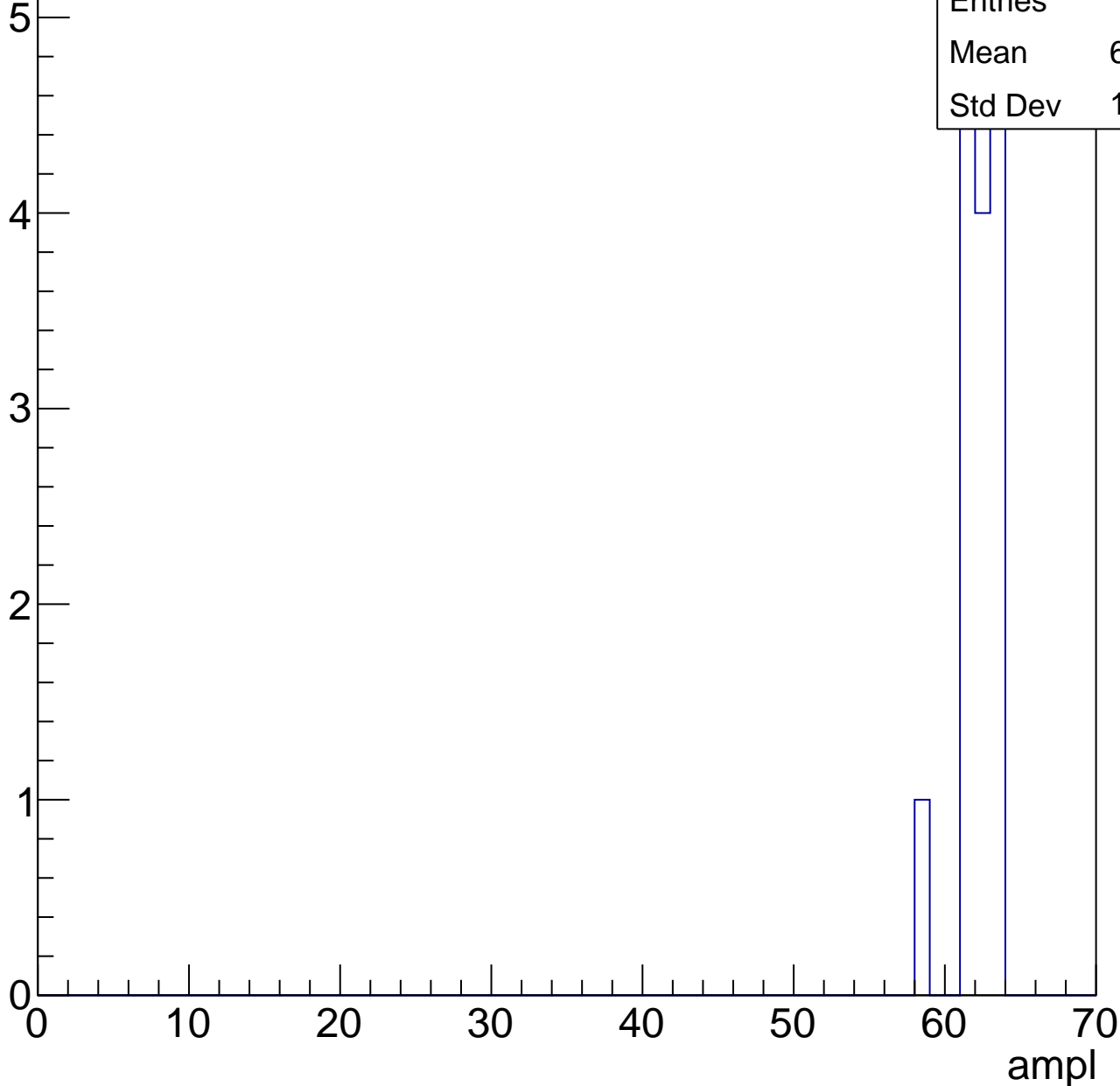


B0L101S, U18-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.73
Std Dev	1.289

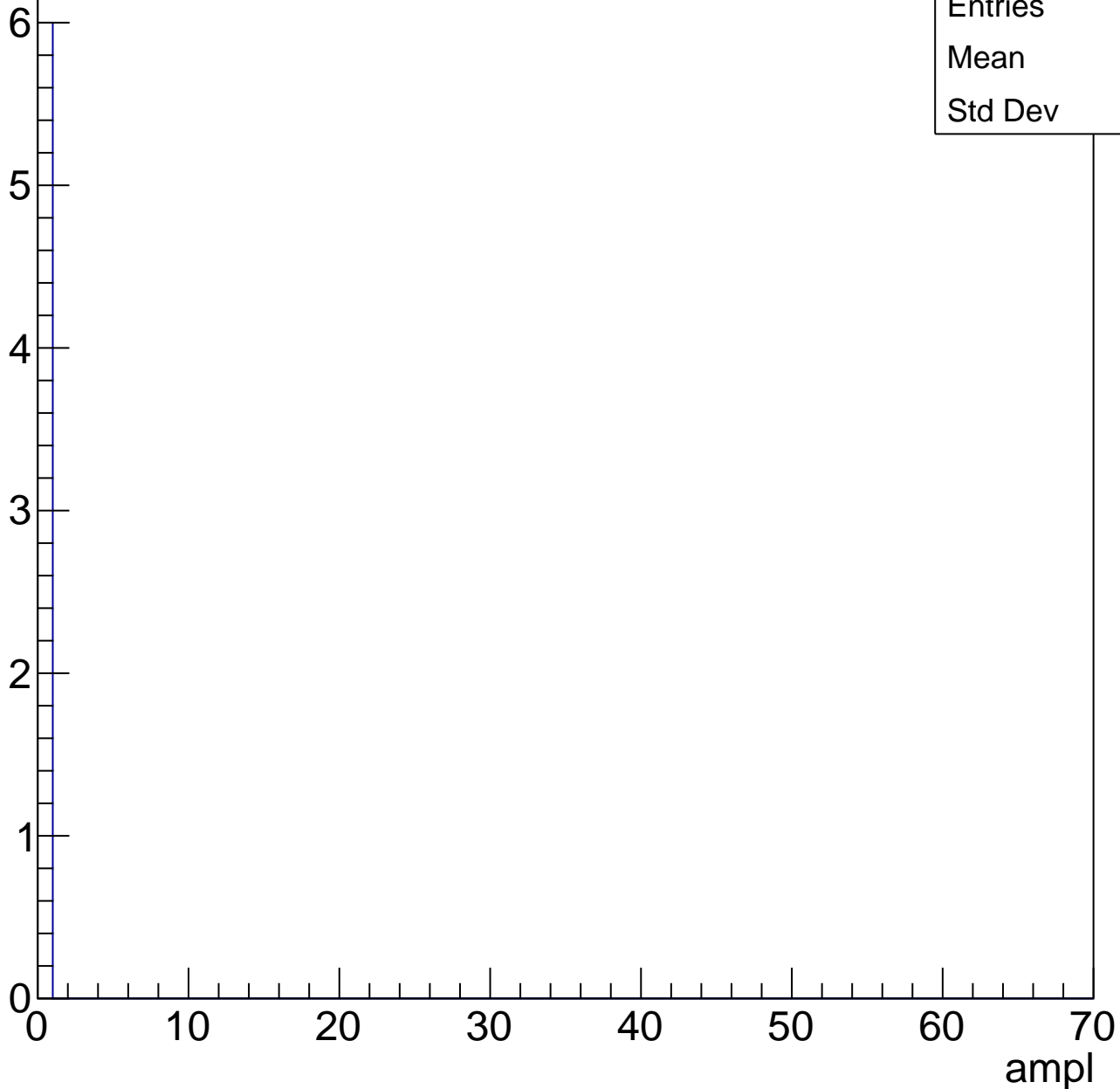


B0L101S, U18-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	6
Mean	0
Std Dev	0



B0L101S, U18-ch120, adc0

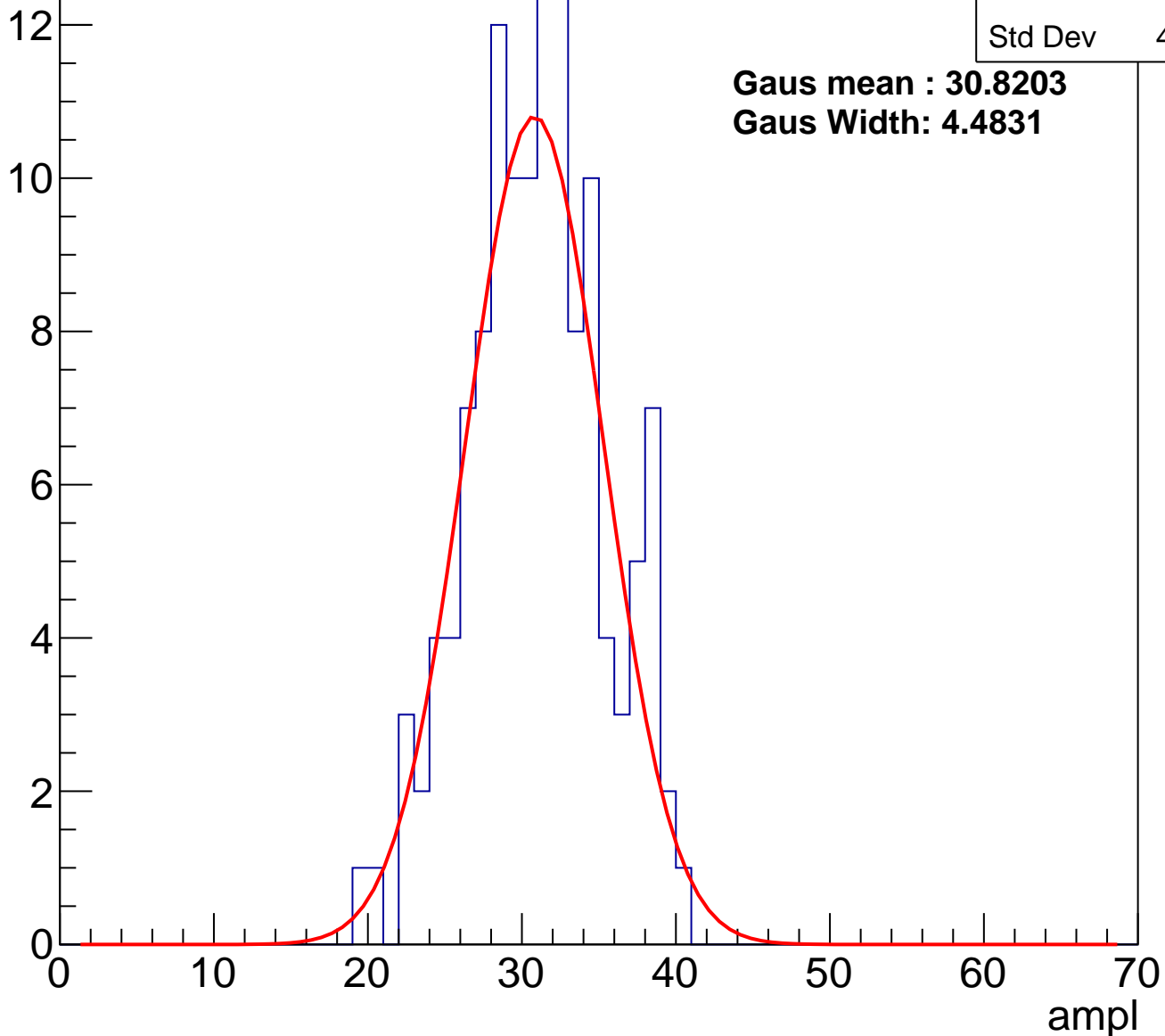
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	30.55
Std Dev	4.346

Gaus mean : 30.8203

Gaus Width: 4.4831

Entry



B0L101S, U18-ch120, adc1

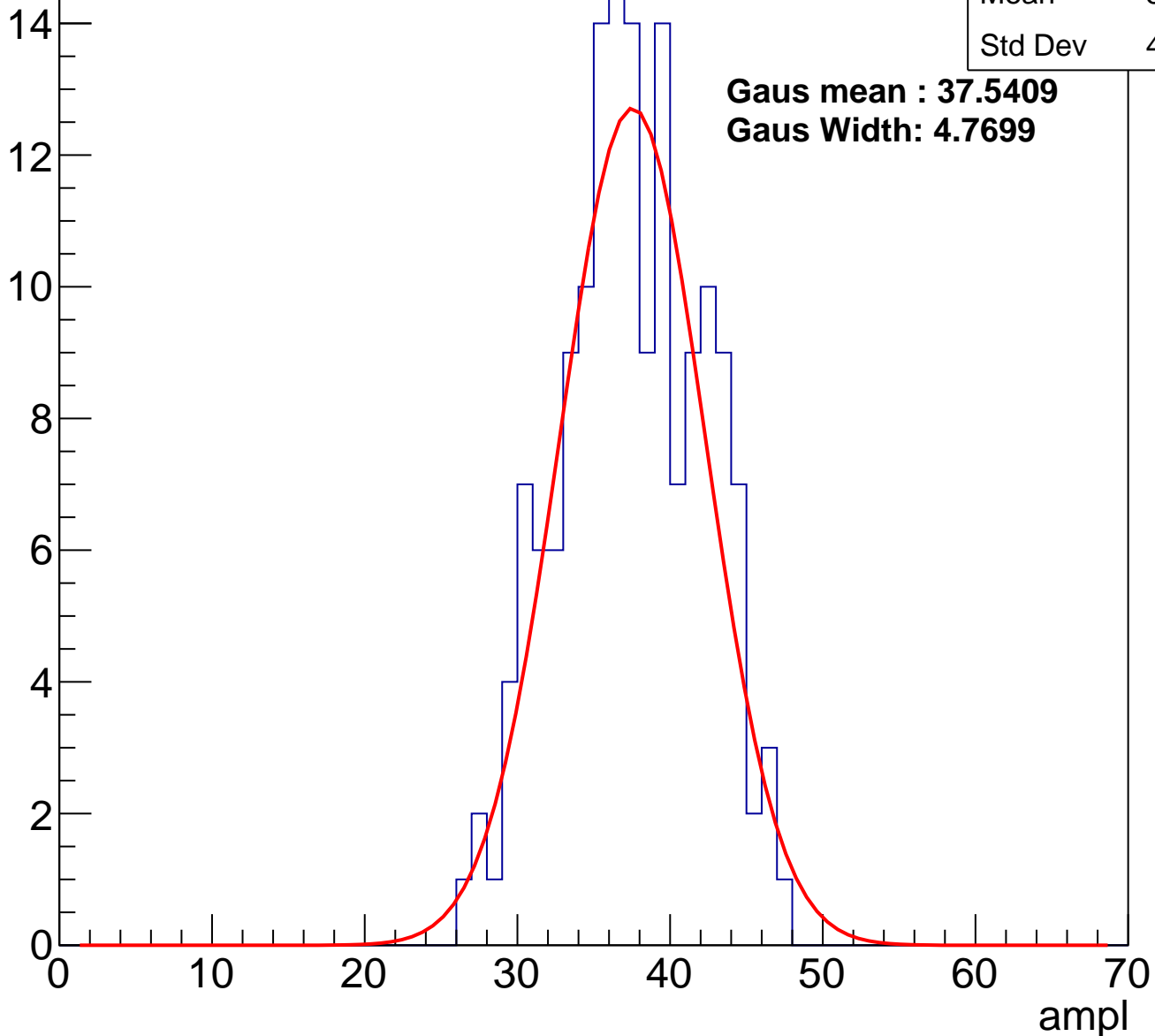
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	37.02
Std Dev	4.563

Gaus mean : 37.5409

Gaus Width: 4.7699

Entry



B0L101S, U18-ch120, adc2

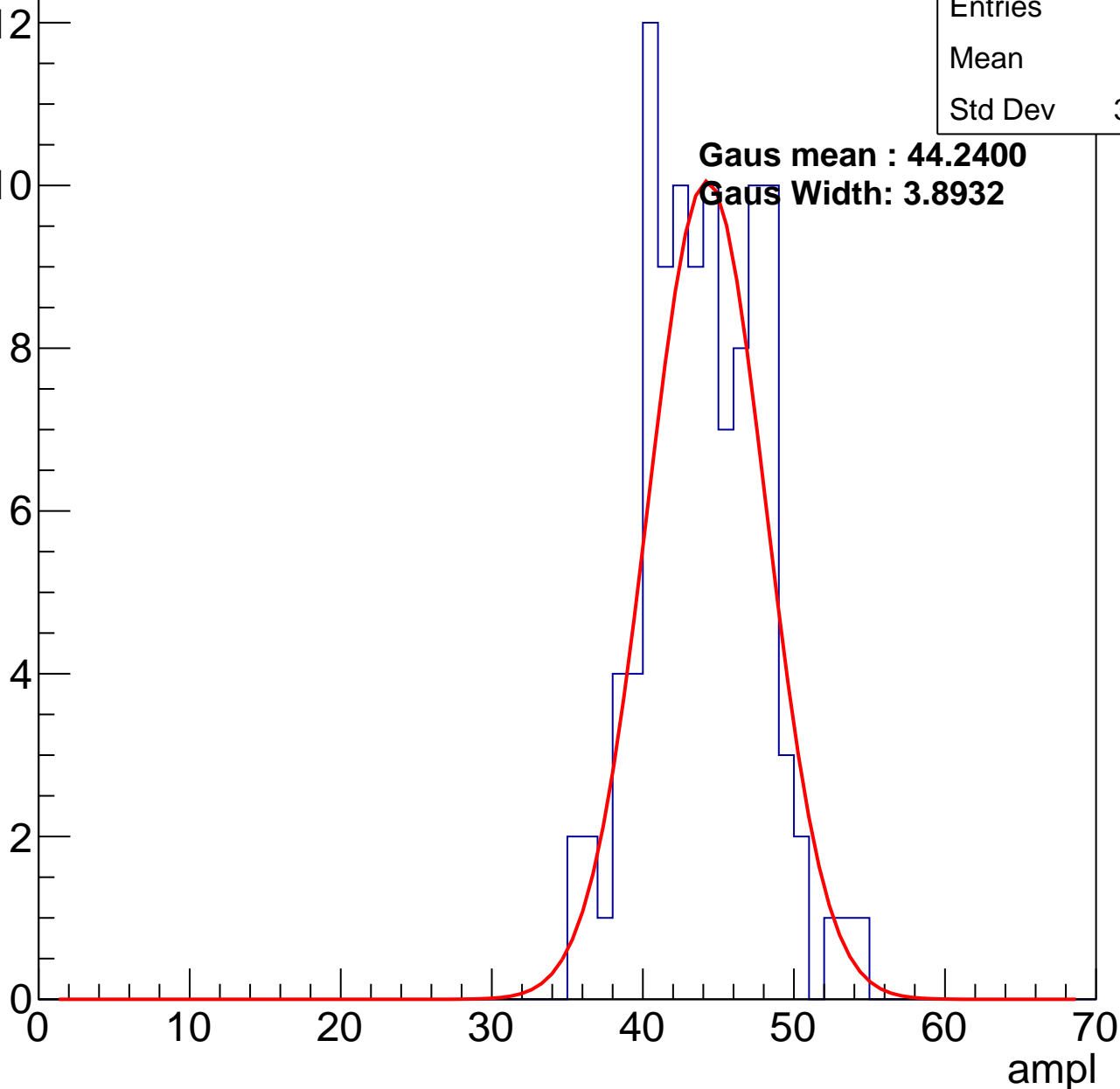
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	106
Mean	43.6
Std Dev	3.848

Gaus mean : 44.2400

Gaus Width: 3.8932



B0L101S, U18-ch120, adc3

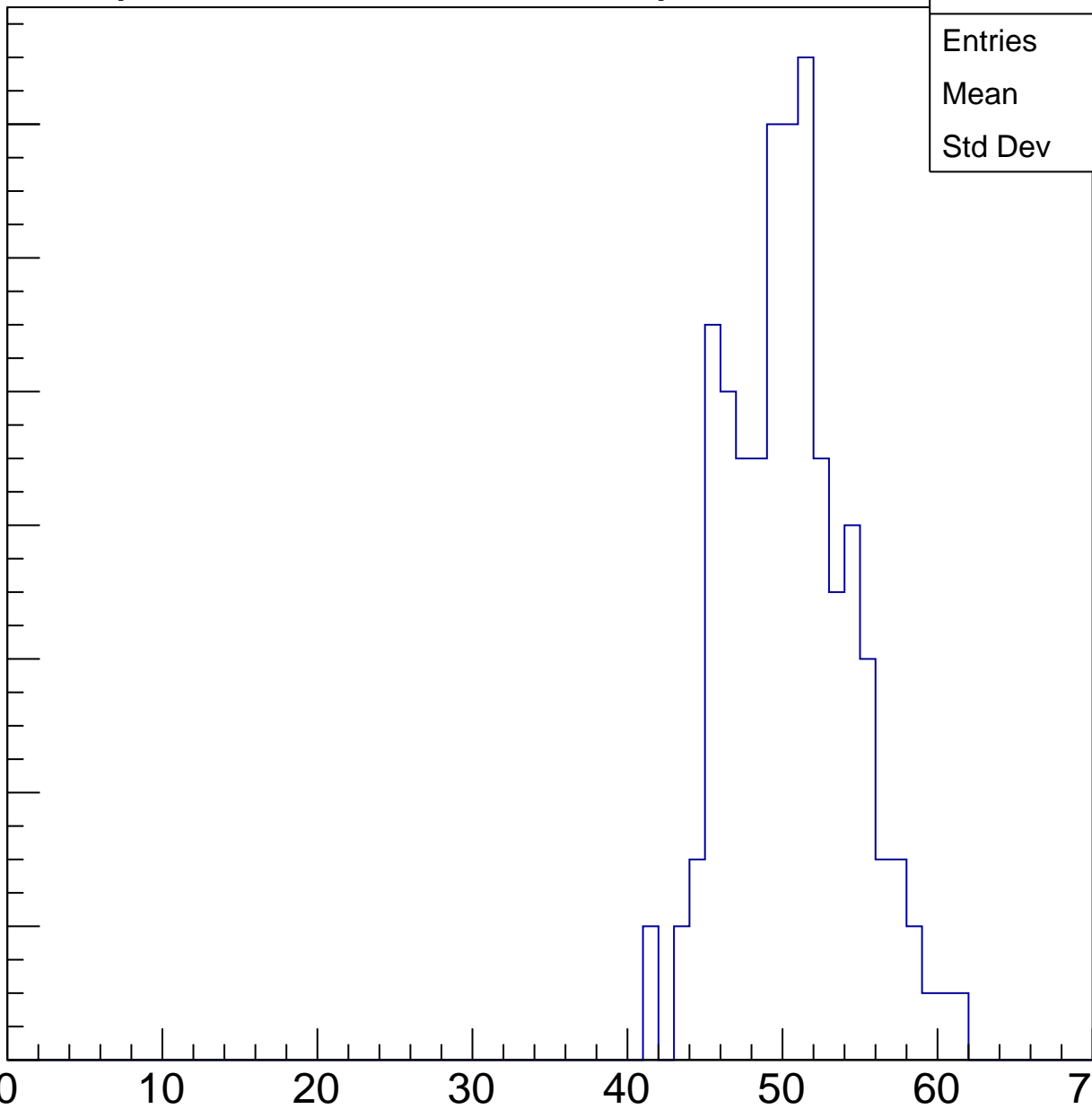
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	130
Mean	49.98
Std Dev	3.933

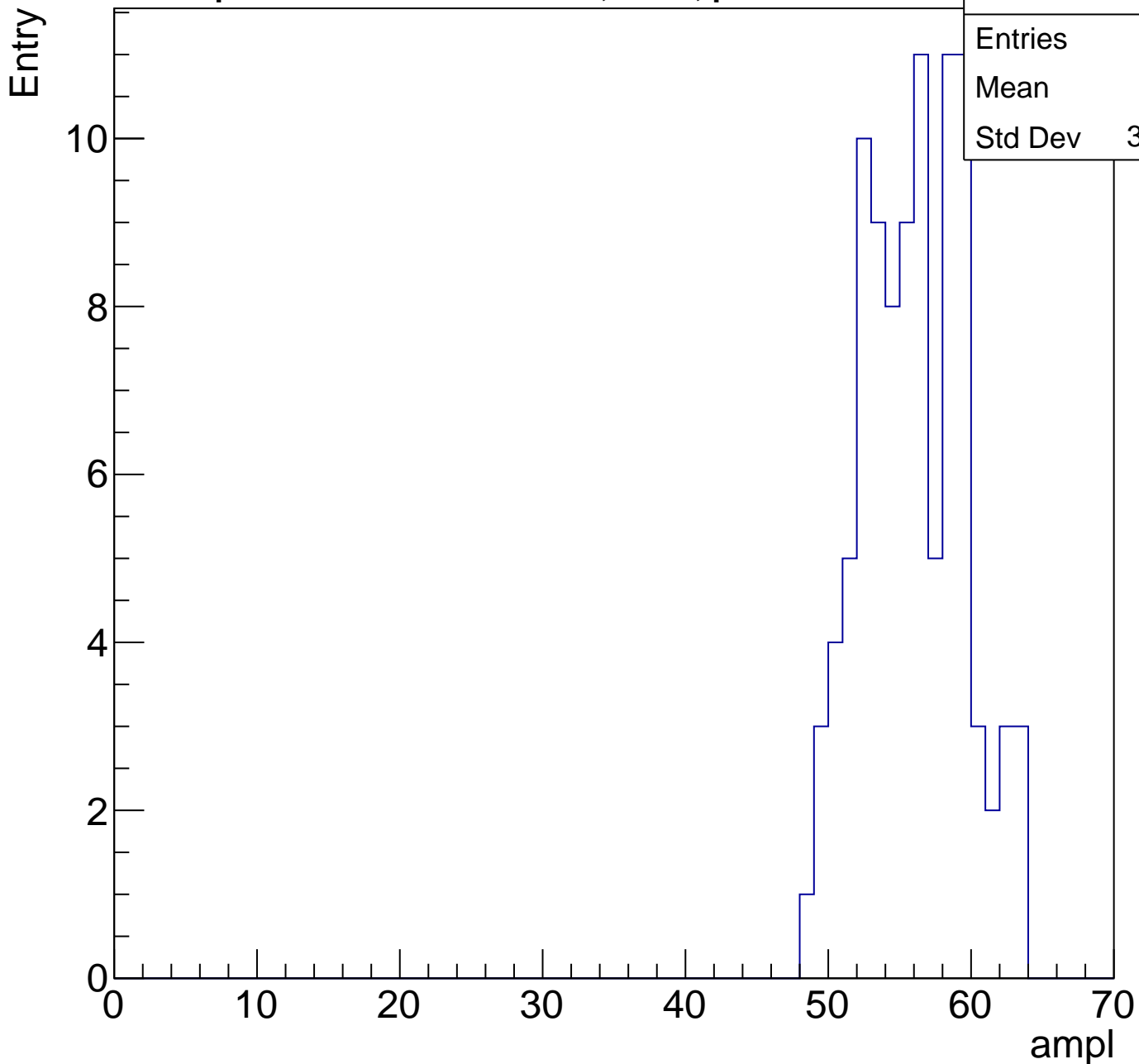
ampl



B0L101S, U18-ch120, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	98
Mean	55.5
Std Dev	3.555



B0L101S, U18-ch120, adc5

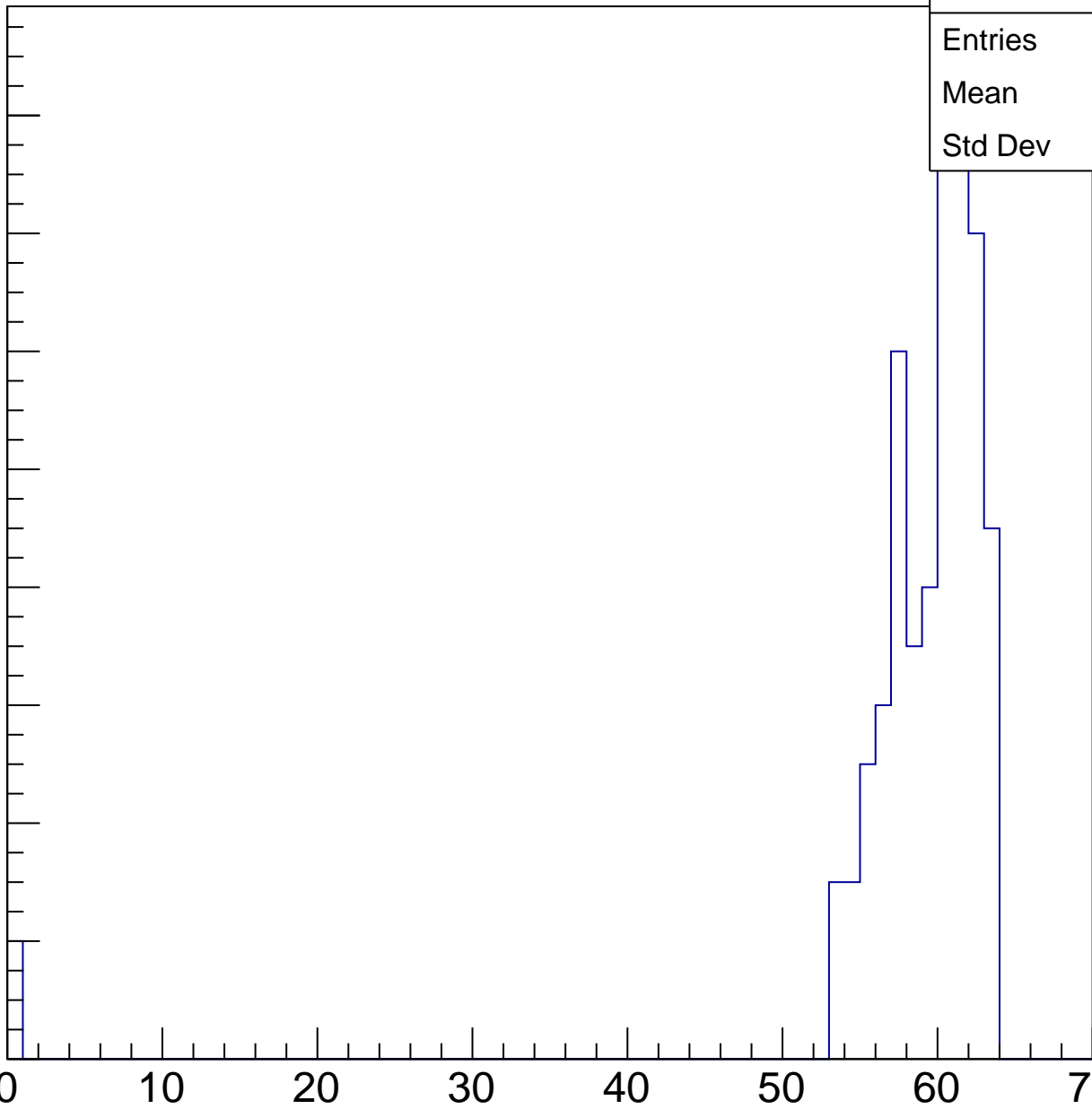
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.1
Std Dev	8.635

ampl

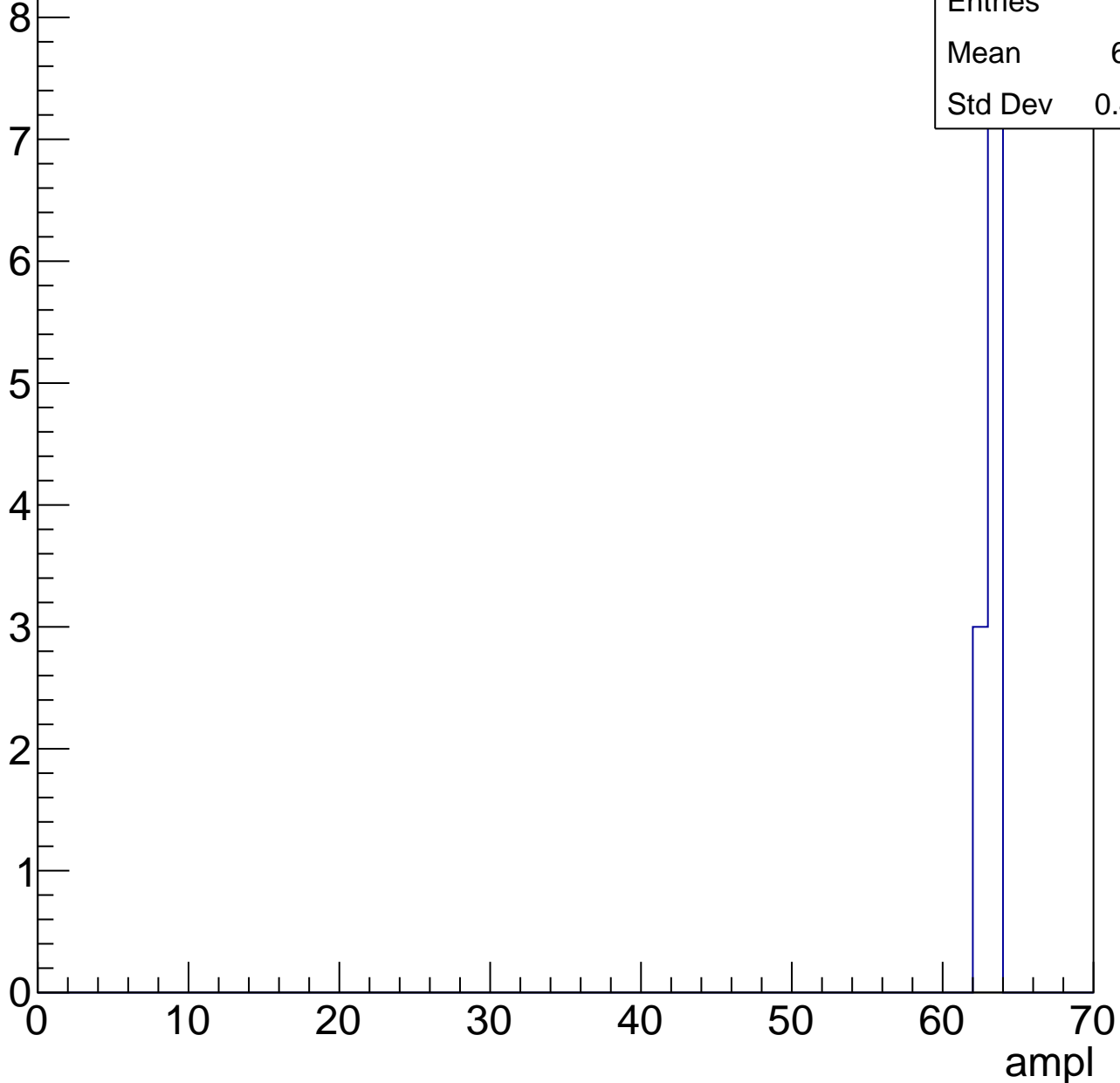


B0L101S, U18-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62.73
Std Dev	0.4454



B0L101S, U18-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch121, adc0

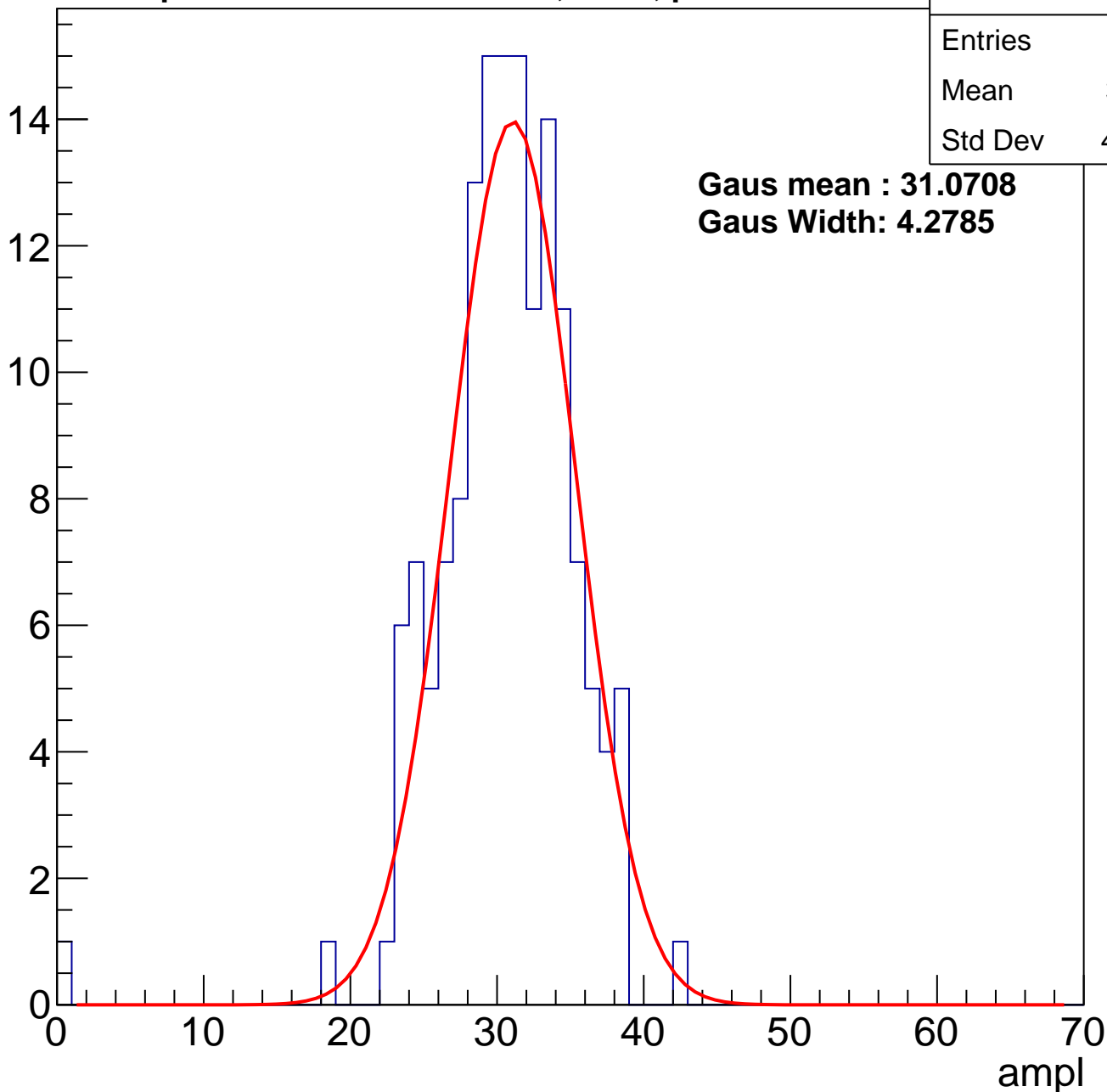
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	30.11
Std Dev	4.733

Gaus mean : 31.0708

Gaus Width: 4.2785

Entry



B0L101S, U18-ch121, adc1

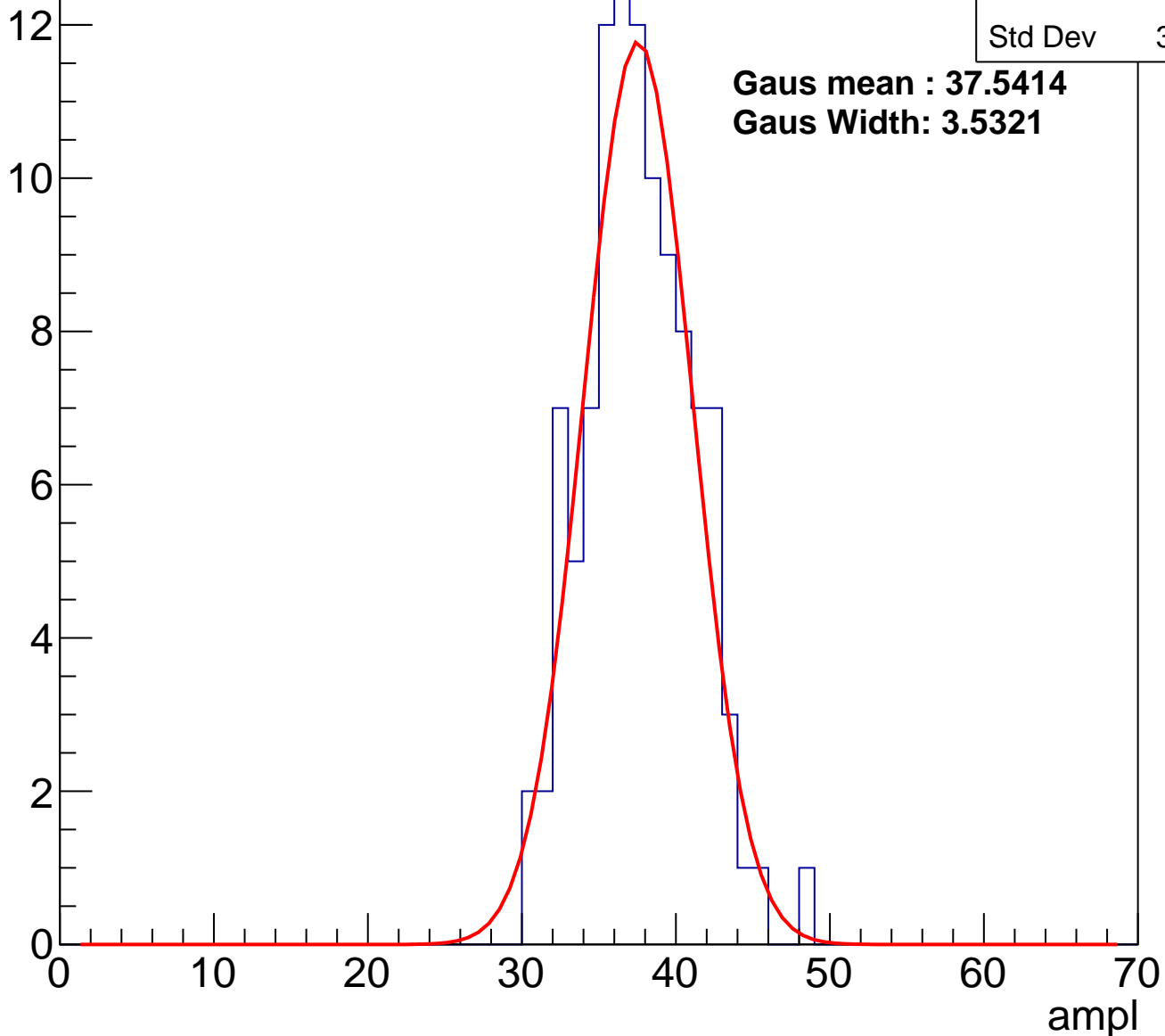
calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	37.19
Std Dev	3.475

Gaus mean : 37.5414

Gaus Width: 3.5321

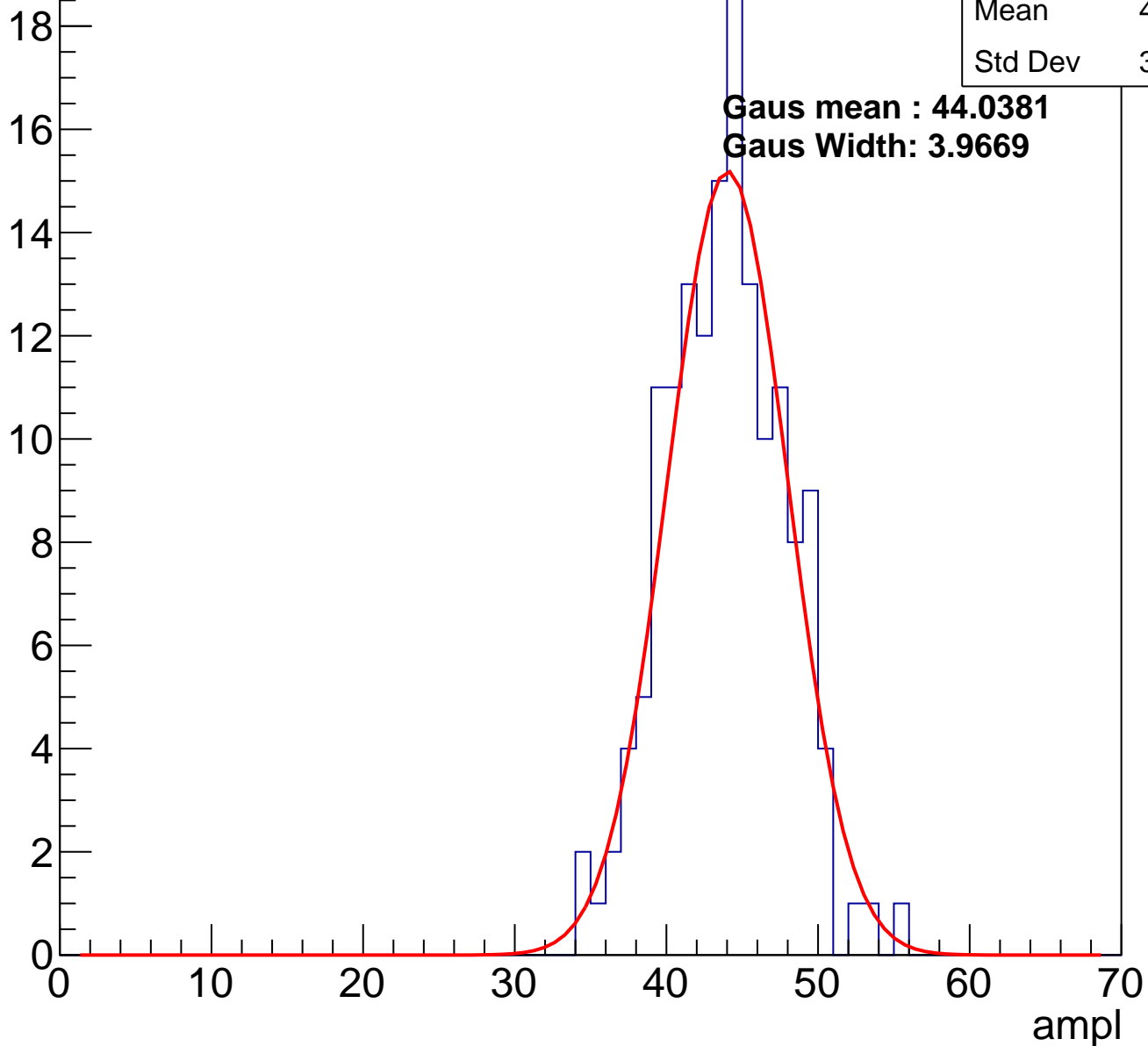
Entry



B0L101S, U18-ch121, adc2

calib_packv5_042523_0143.root, FC#1, port C1

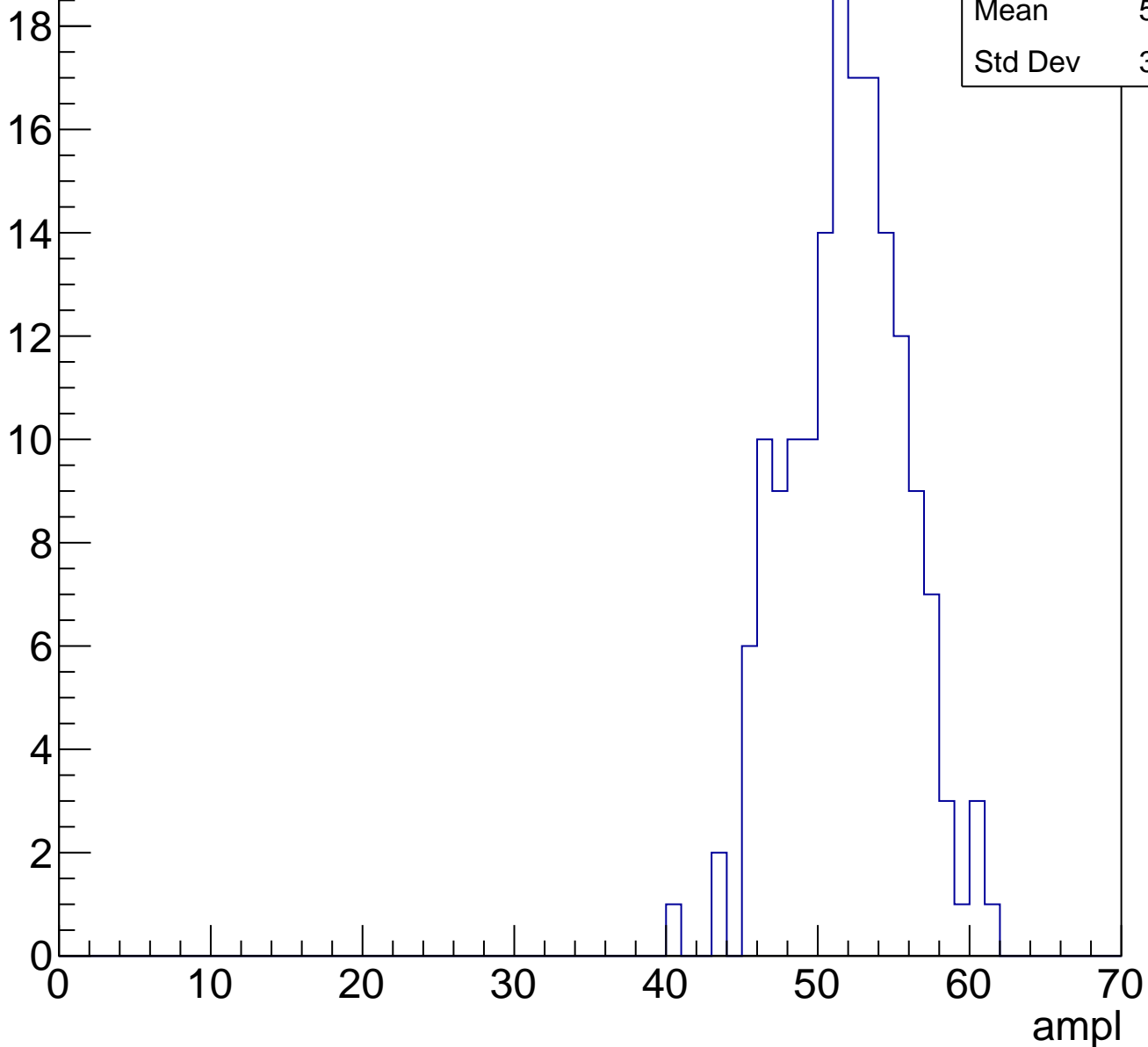
Entry



B0L101S, U18-ch121, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch121, adc4

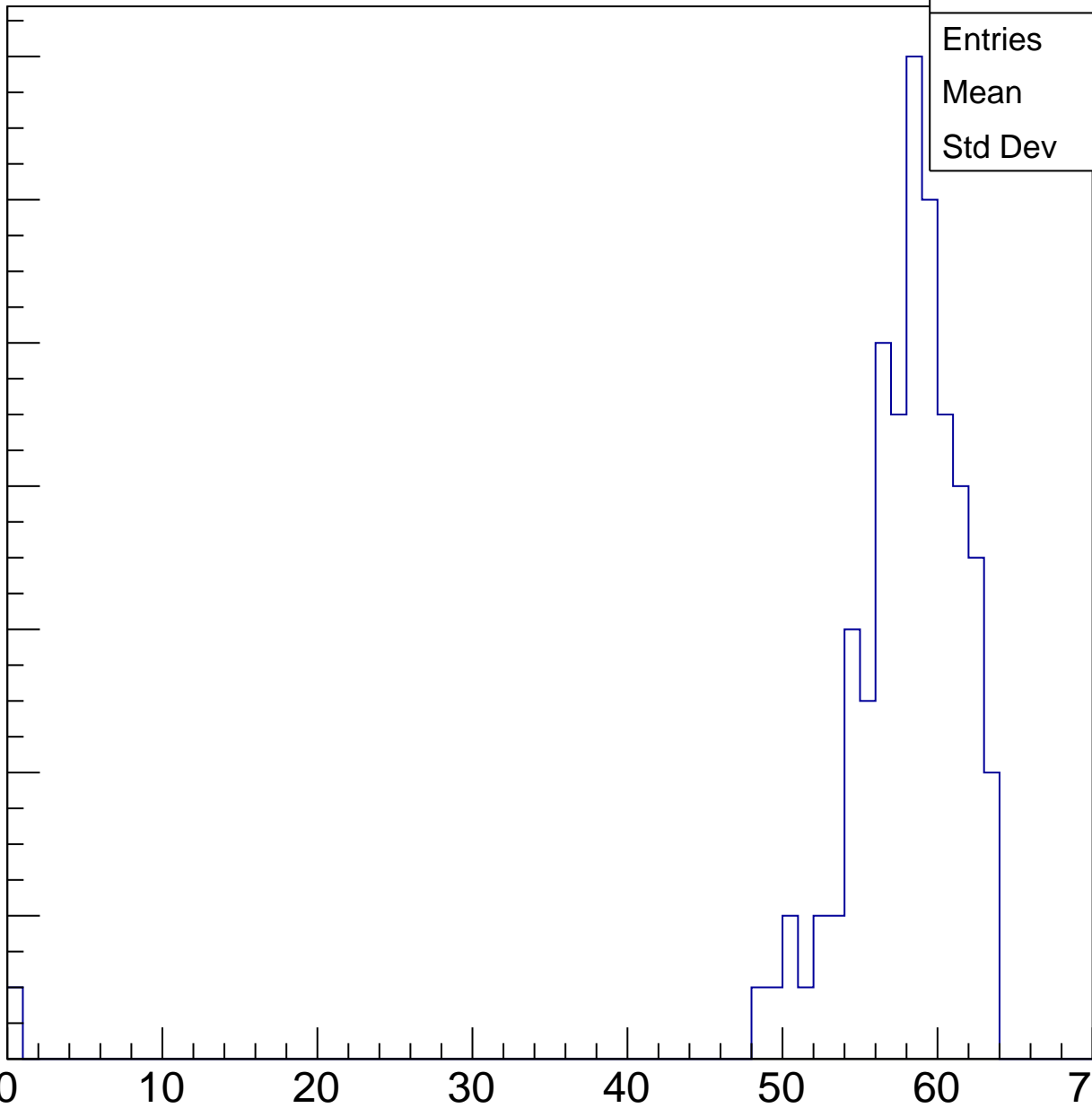
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	57.06
Std Dev	6.751

ampl



B0L101S, U18-ch121, adc5

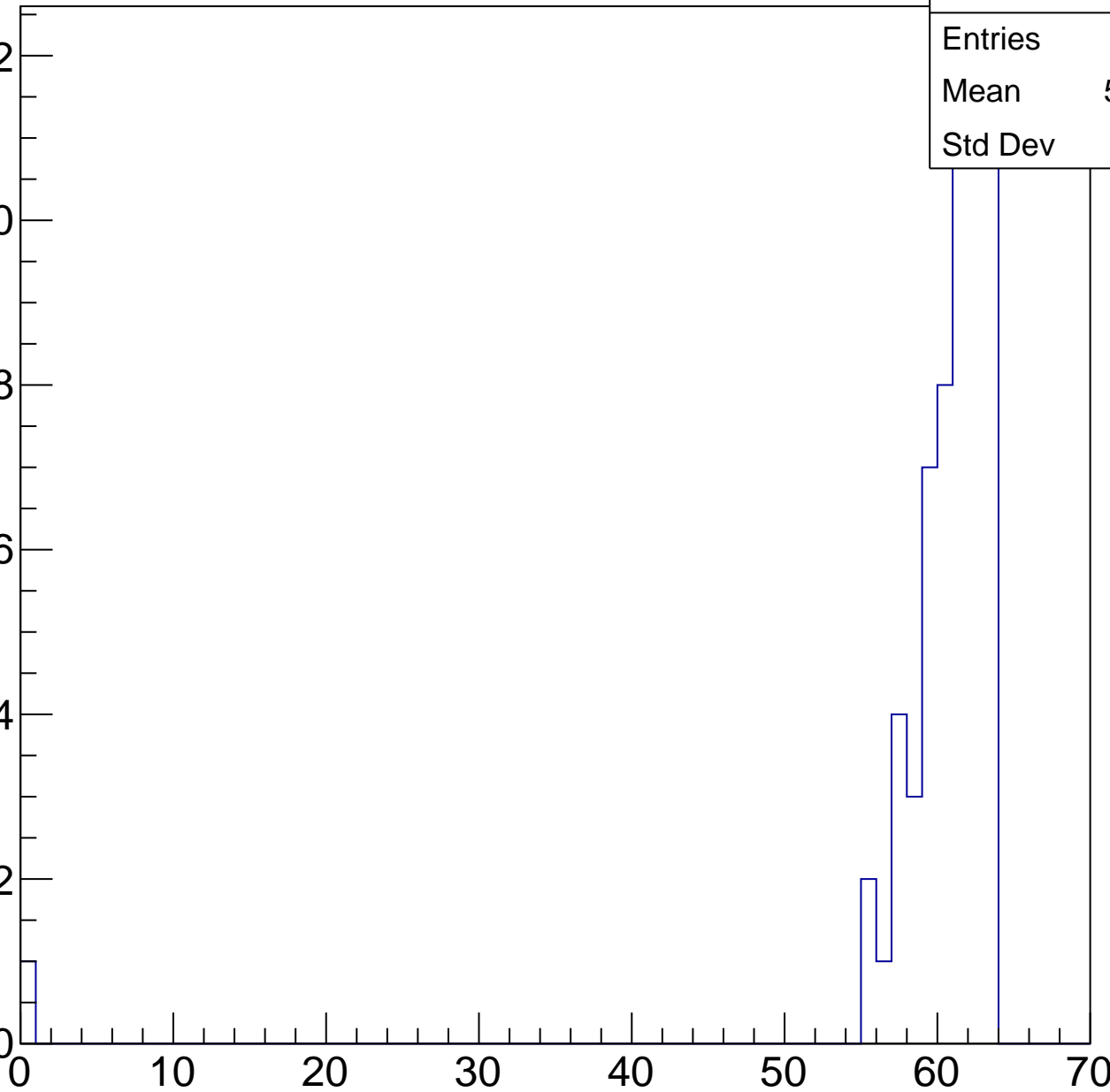
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	61
Mean	59.54
Std Dev	7.97

ampl



B0L101S, U18-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

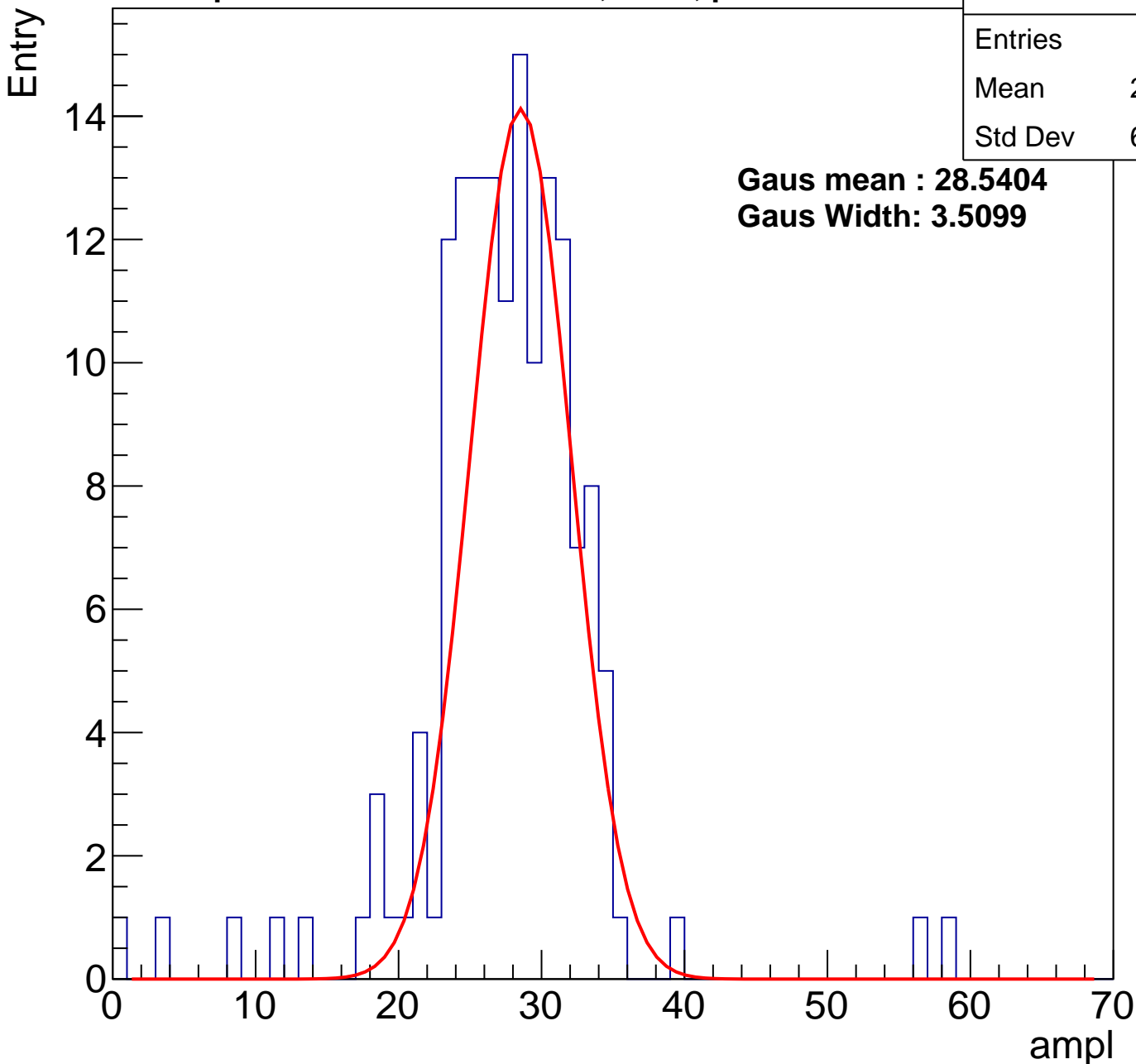
B0L101S, U18-ch122, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	27.09
Std Dev	6.386

Gaus mean : 28.5404

Gaus Width: 3.5099



B0L101S, U18-ch122, adc1

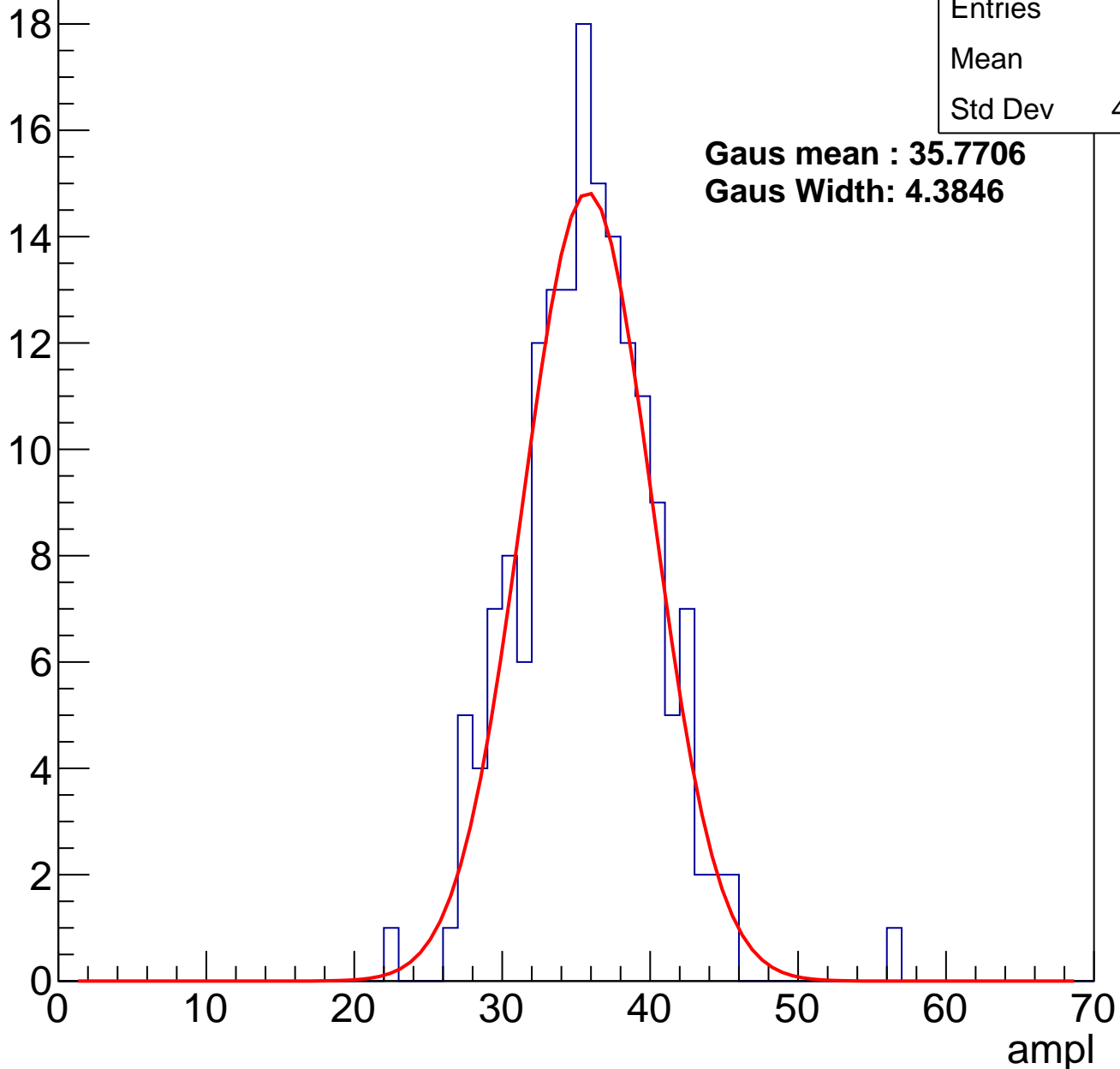
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	35.3
Std Dev	4.568

Gaus mean : 35.7706

Gaus Width: 4.3846

Entry



B0L101S, U18-ch122, adc2

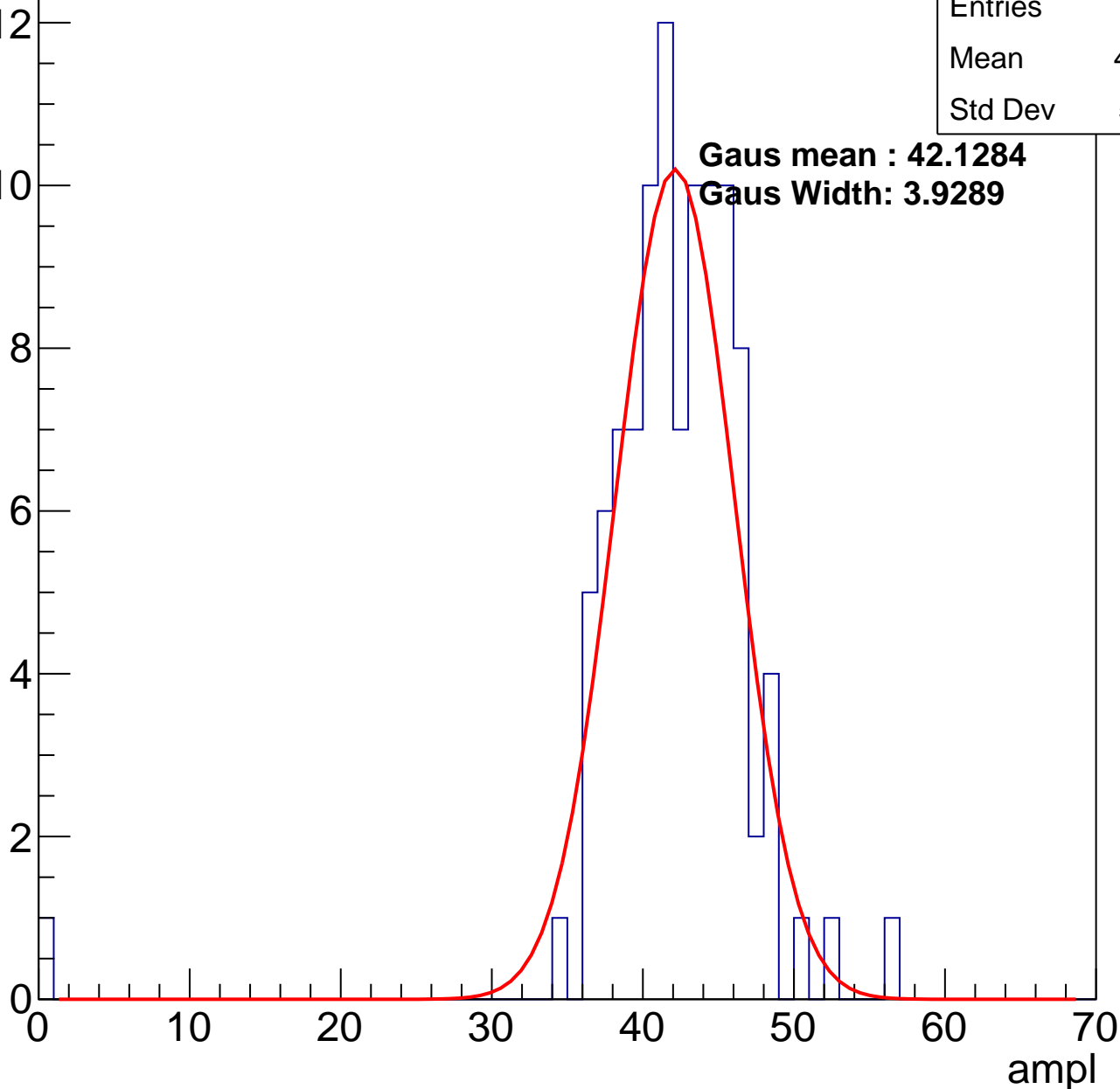
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	103
Mean	41.68
Std Dev	5.571

Gaus mean : 42.1284

Gaus Width: 3.9289



B0L101S, U18-ch122, adc3

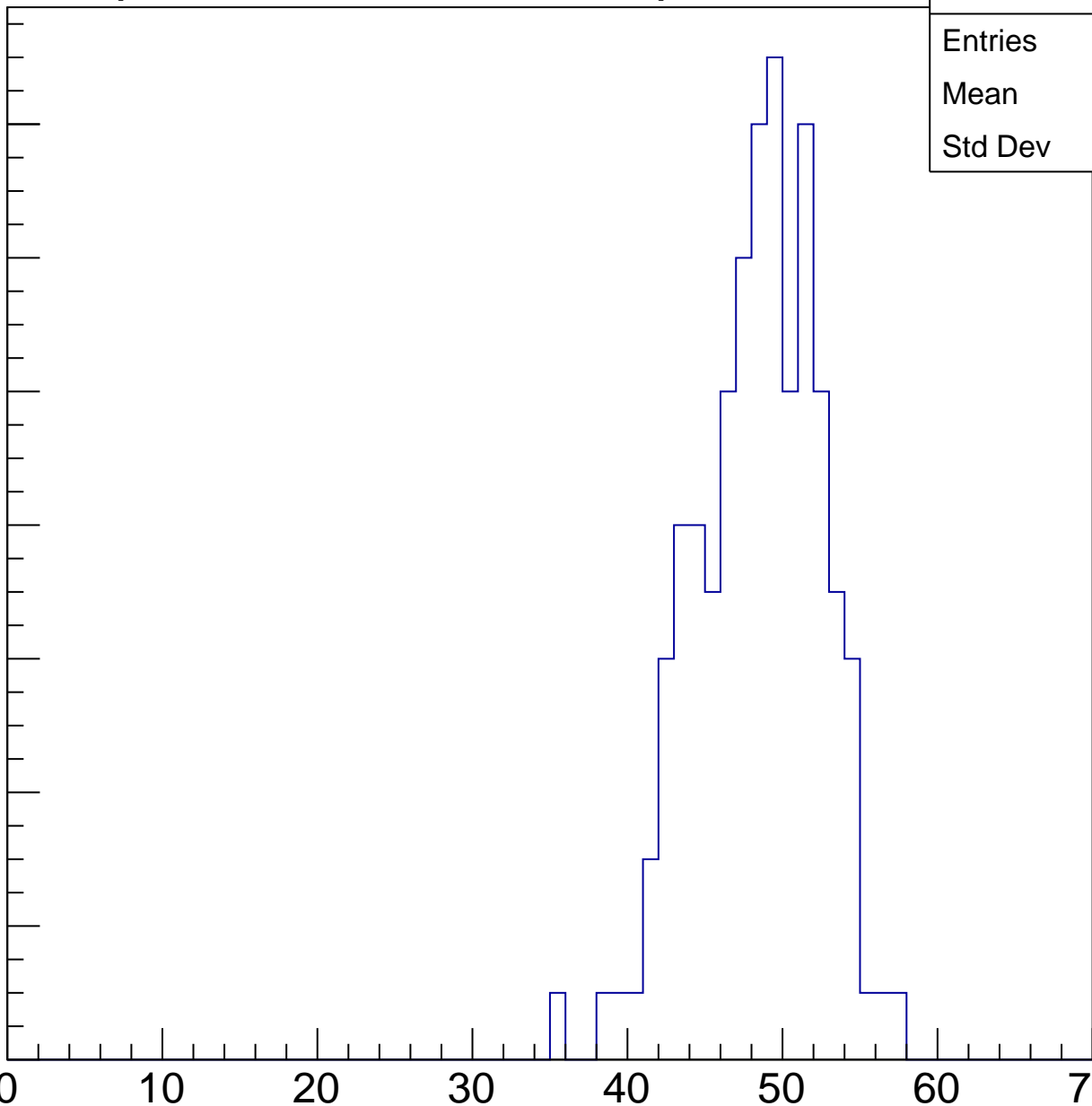
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	137
Mean	47.93
Std Dev	3.957

ampl



B0L101S, U18-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	54.18
Std Dev	4.187

Entry

10

8

6

4

2

0

0

10

20

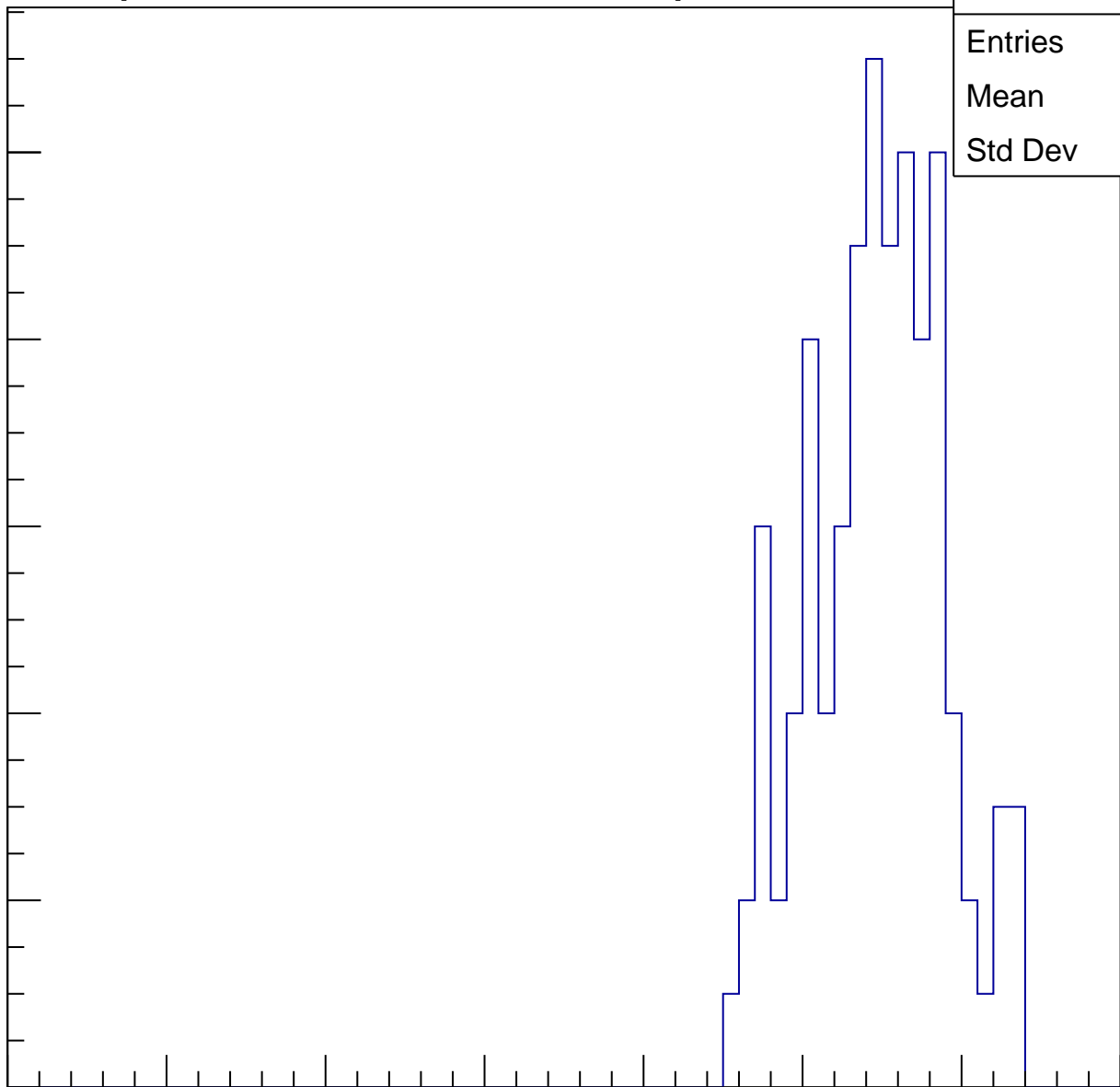
30

40

50

60

ampl



B0L101S, U18-ch122, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	57.54
Std Dev	8.008

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

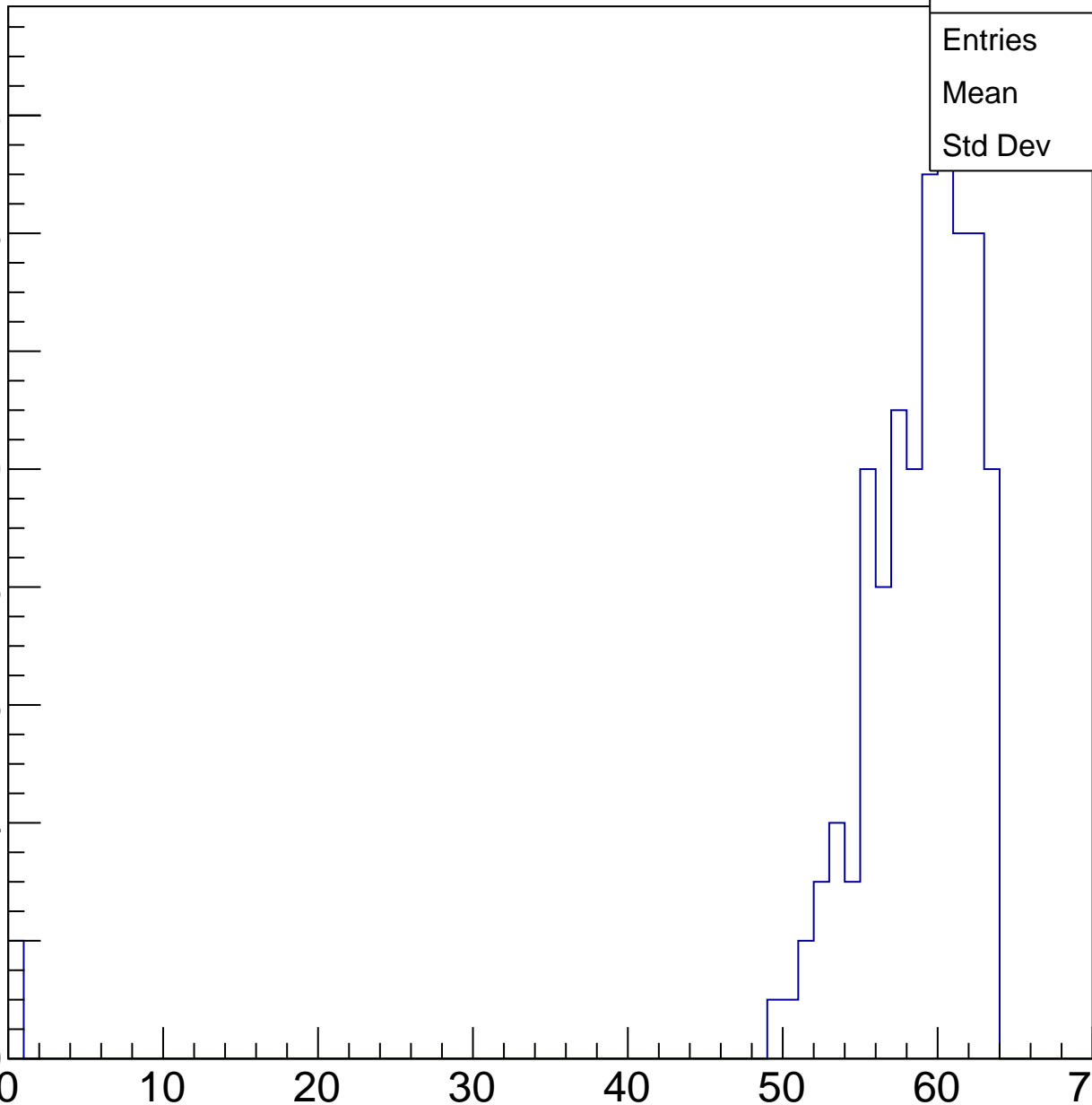
40

50

60

70

ampl

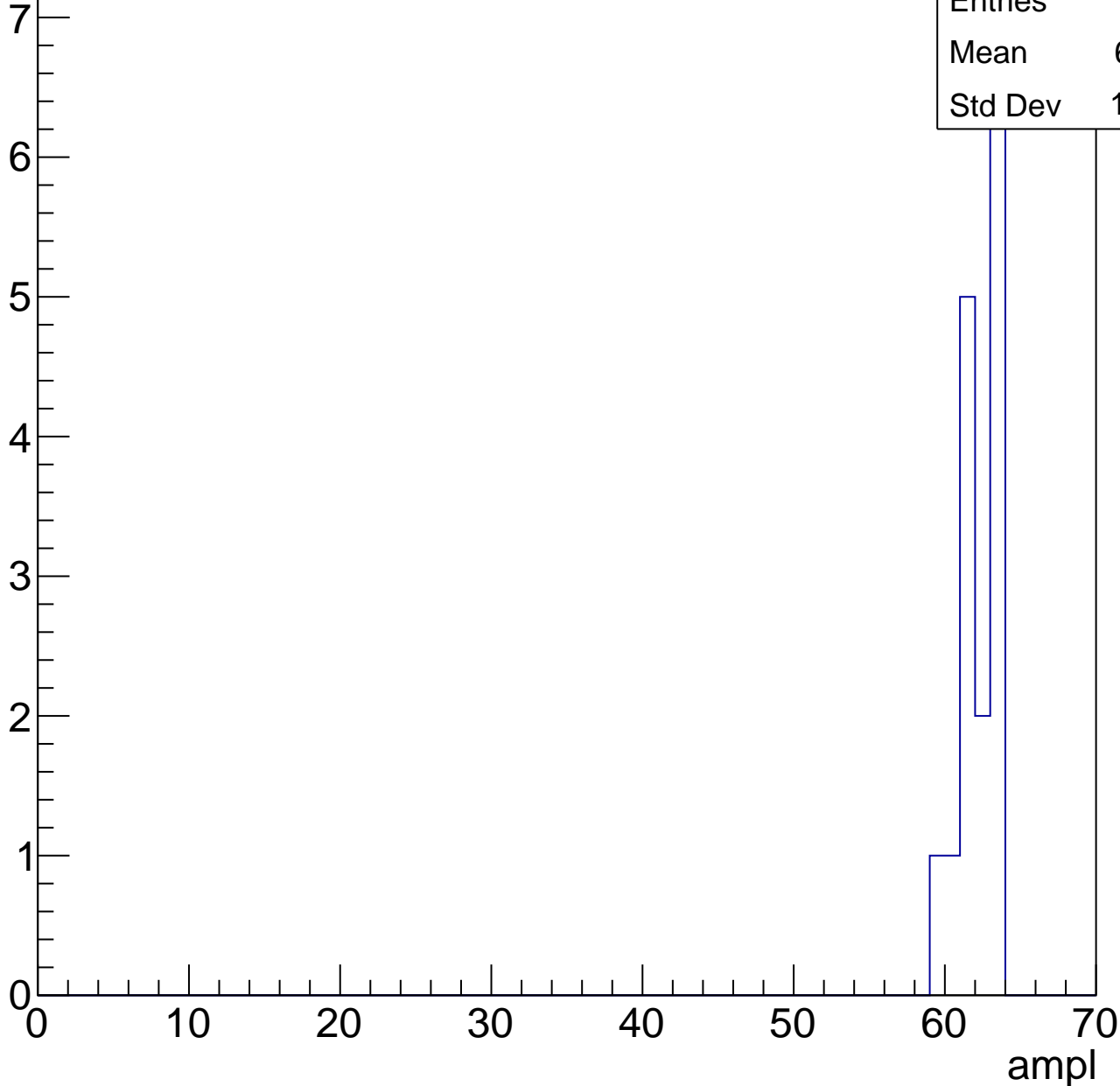


B0L101S, U18-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.81
Std Dev	1.236



B0L101S, U18-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch123, adc0

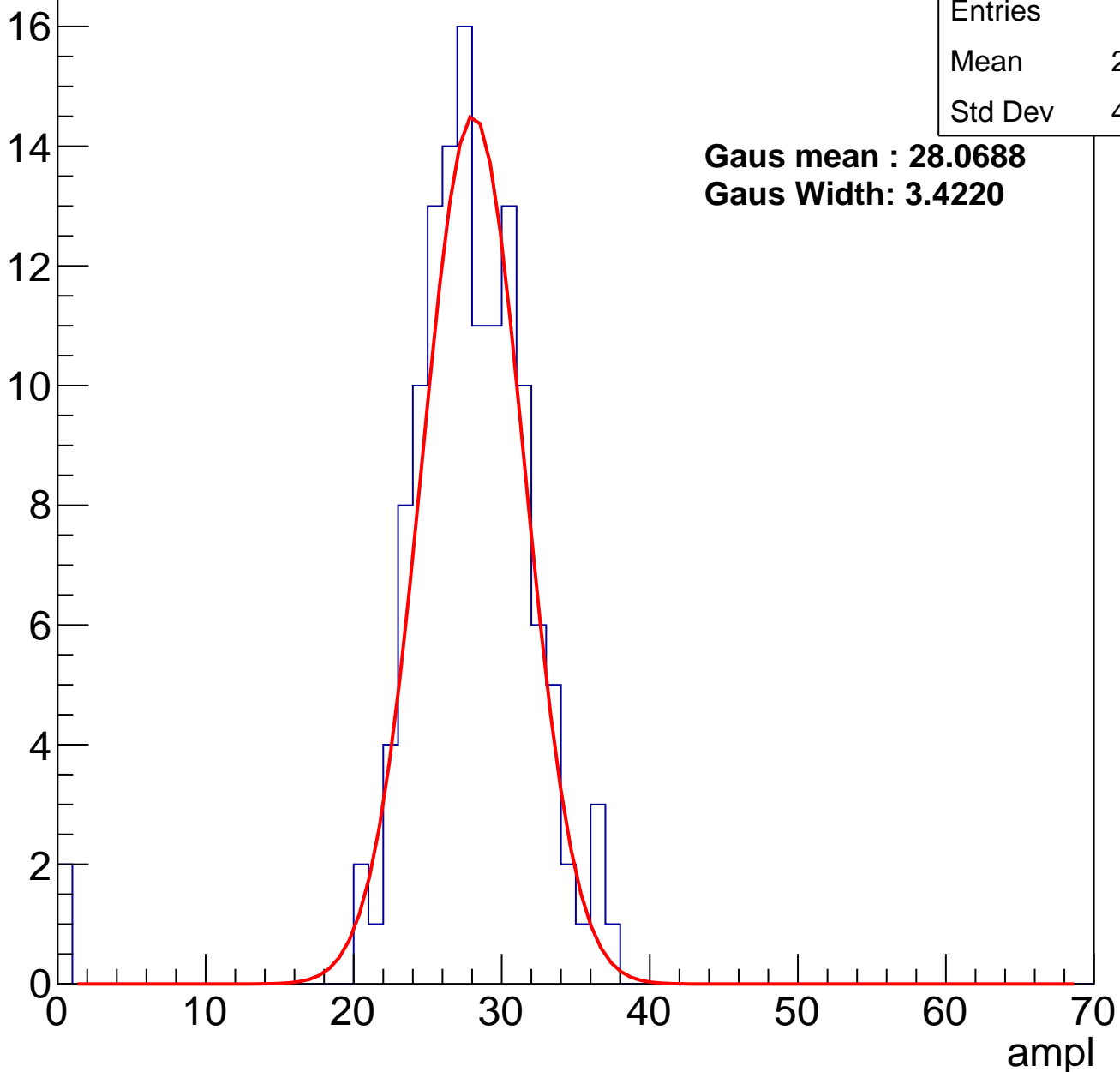
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	27.26
Std Dev	4.854

Gaus mean : 28.0688

Gaus Width: 3.4220

Entry



B0L101S, U18-ch123, adc1

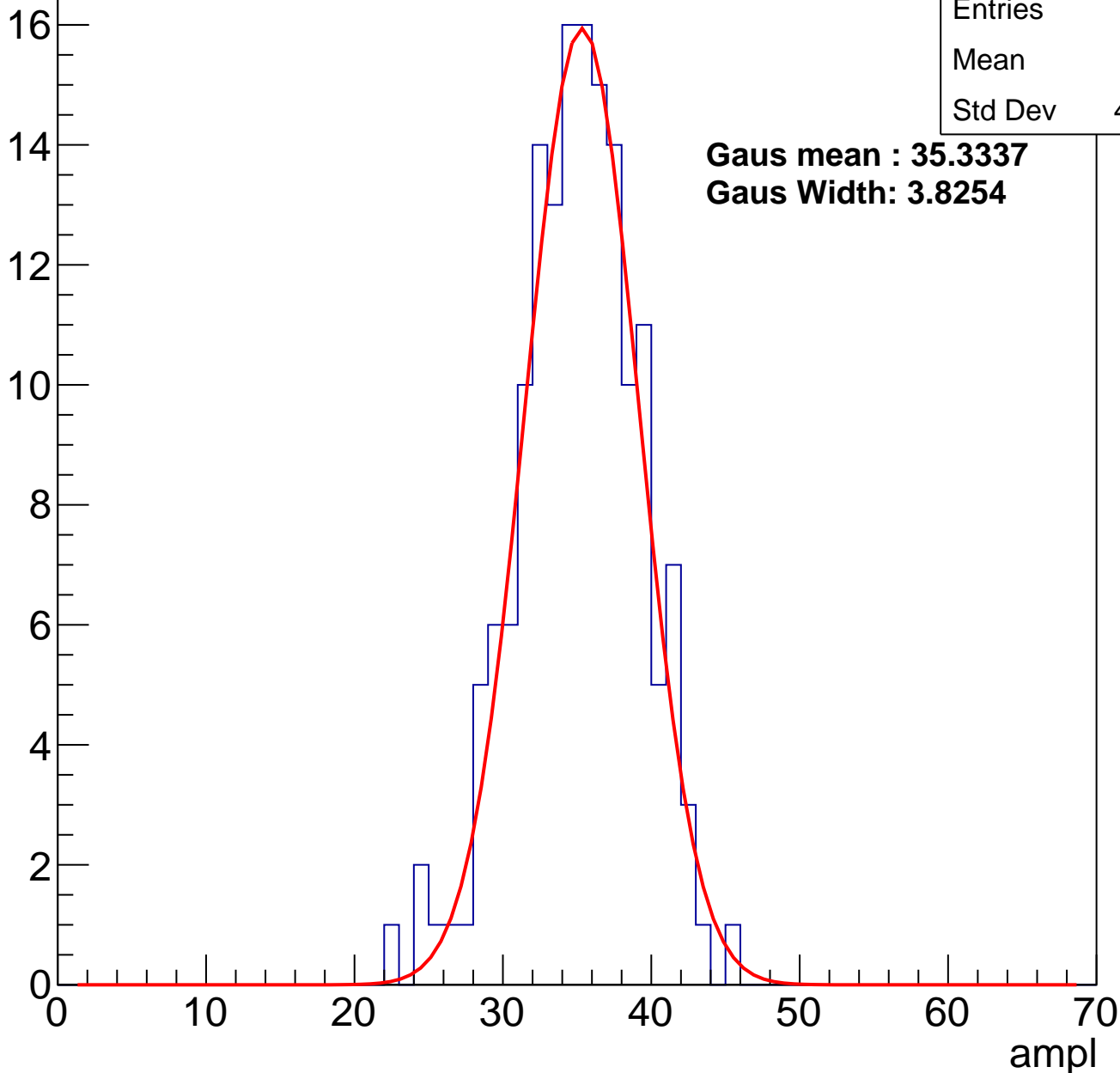
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	34.6
Std Dev	4.059

Gaus mean : 35.3337

Gaus Width: 3.8254

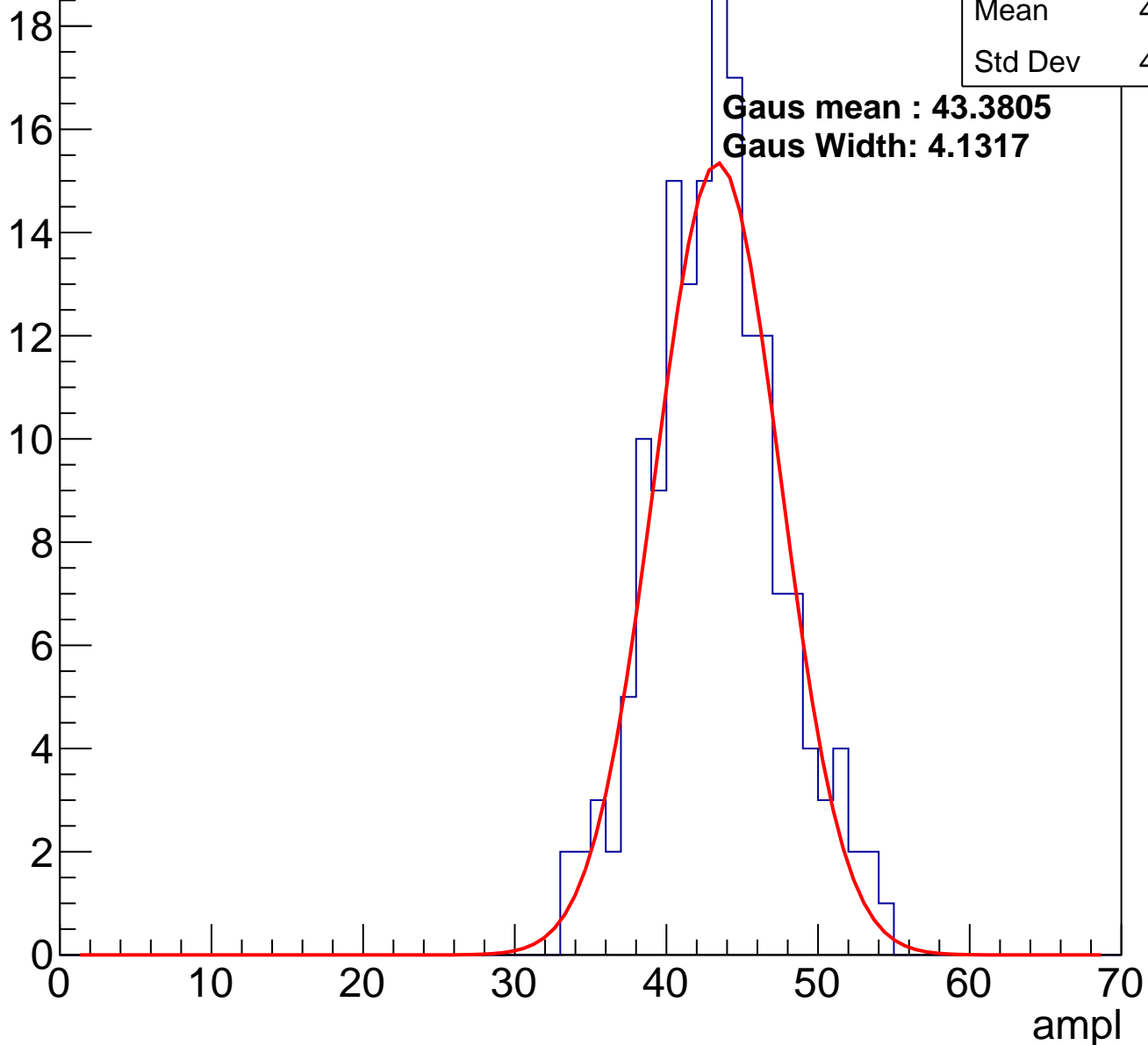
Entry



B0L101S, U18-ch123, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch123, adc3

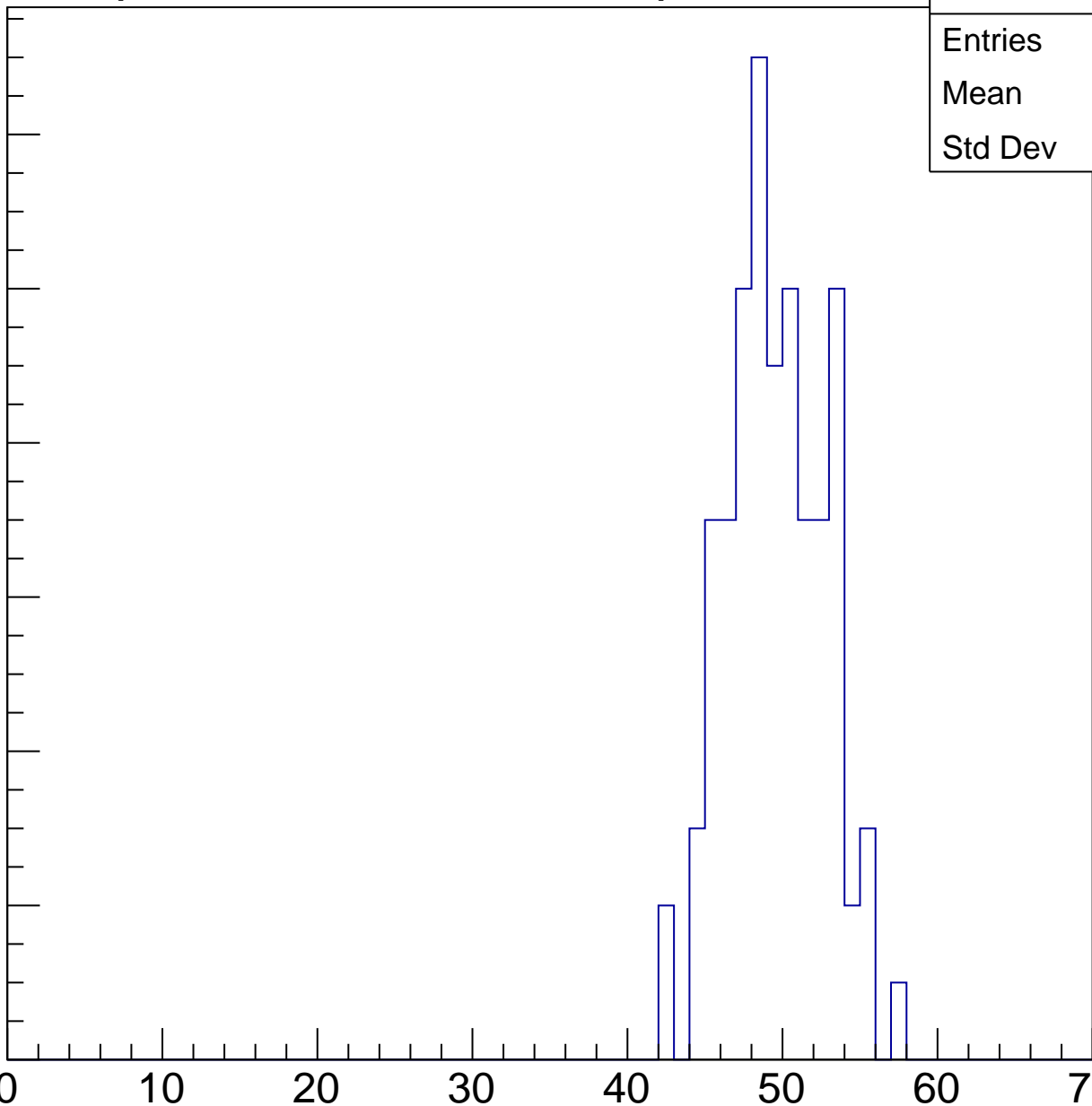
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	91
Mean	49.11
Std Dev	3.115

ampl



B0L101S, U18-ch123, adc4

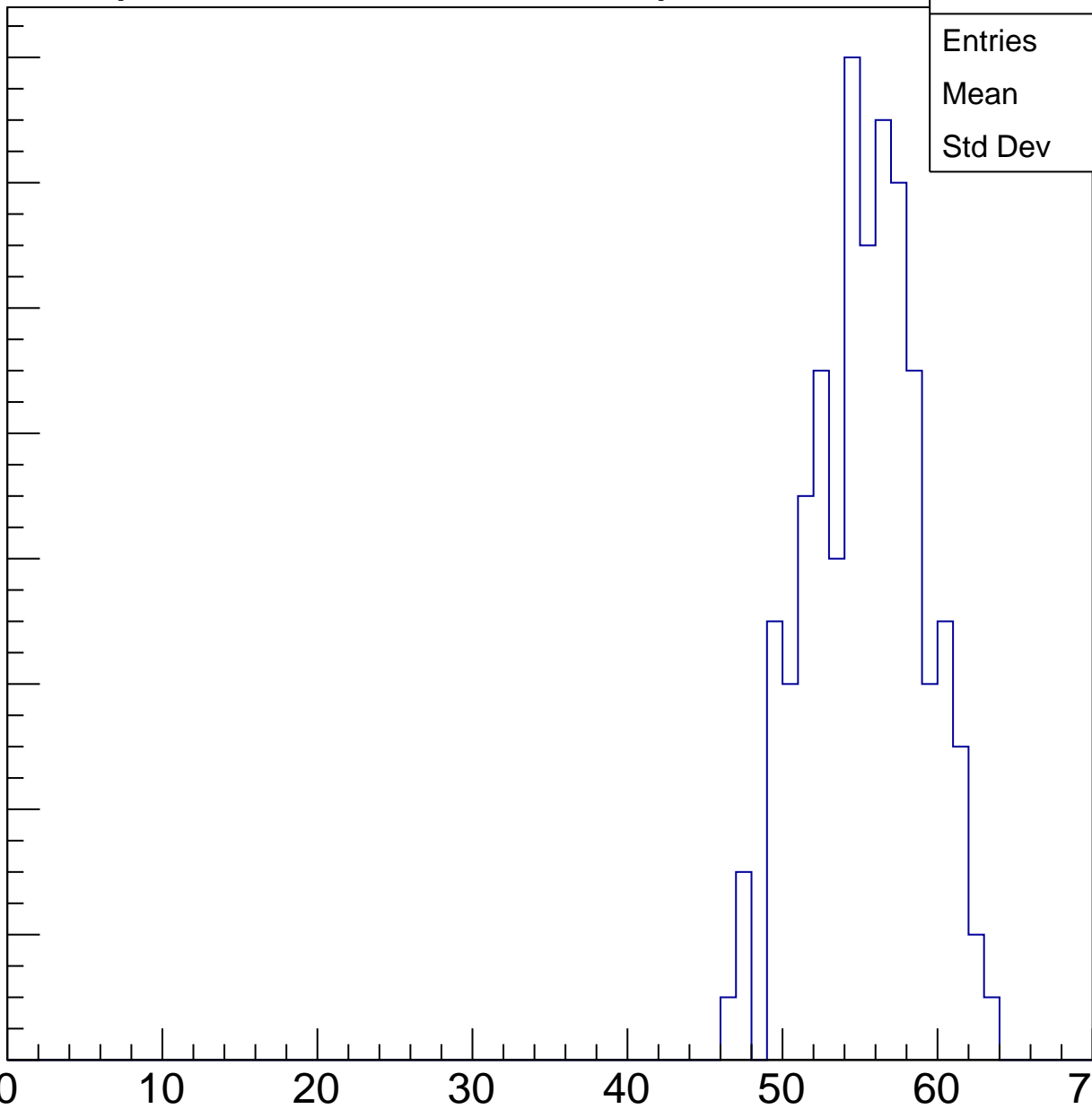
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	54.86
Std Dev	3.597

ampl



B0L101S, U18-ch123, adc5

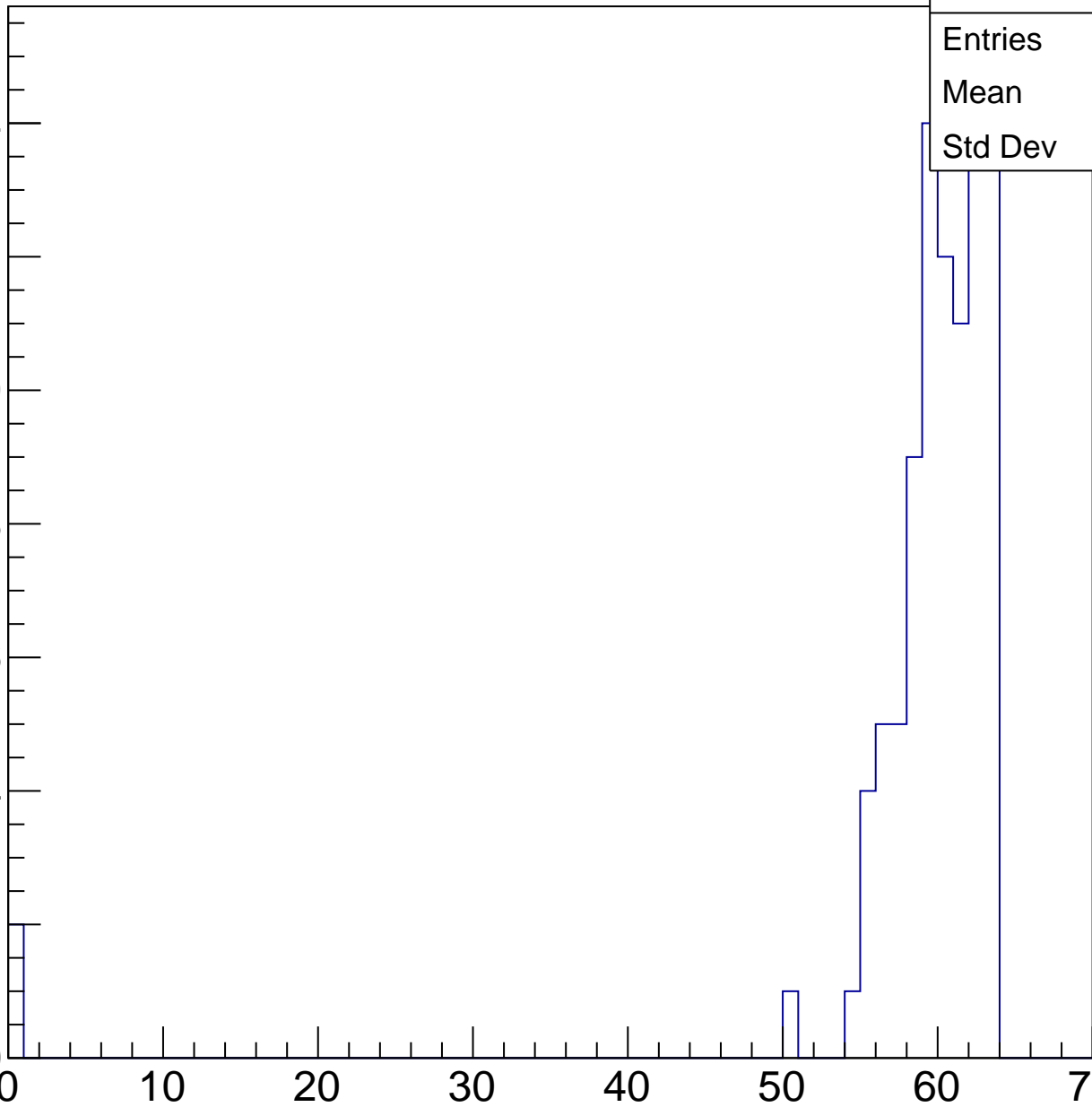
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	58.54
Std Dev	9.002

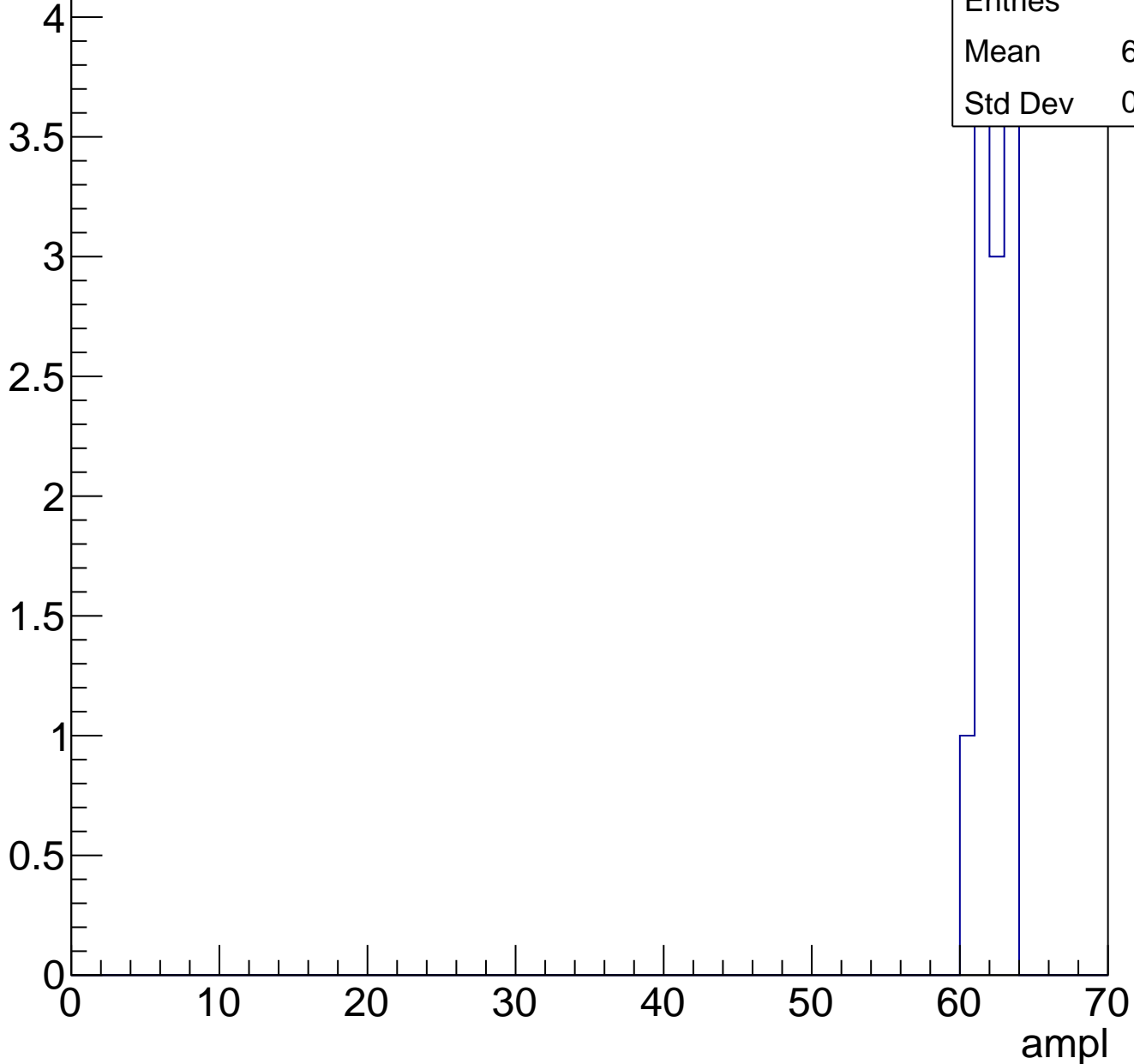
ampl



B0L101S, U18-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L101S, U18-ch124, adc0

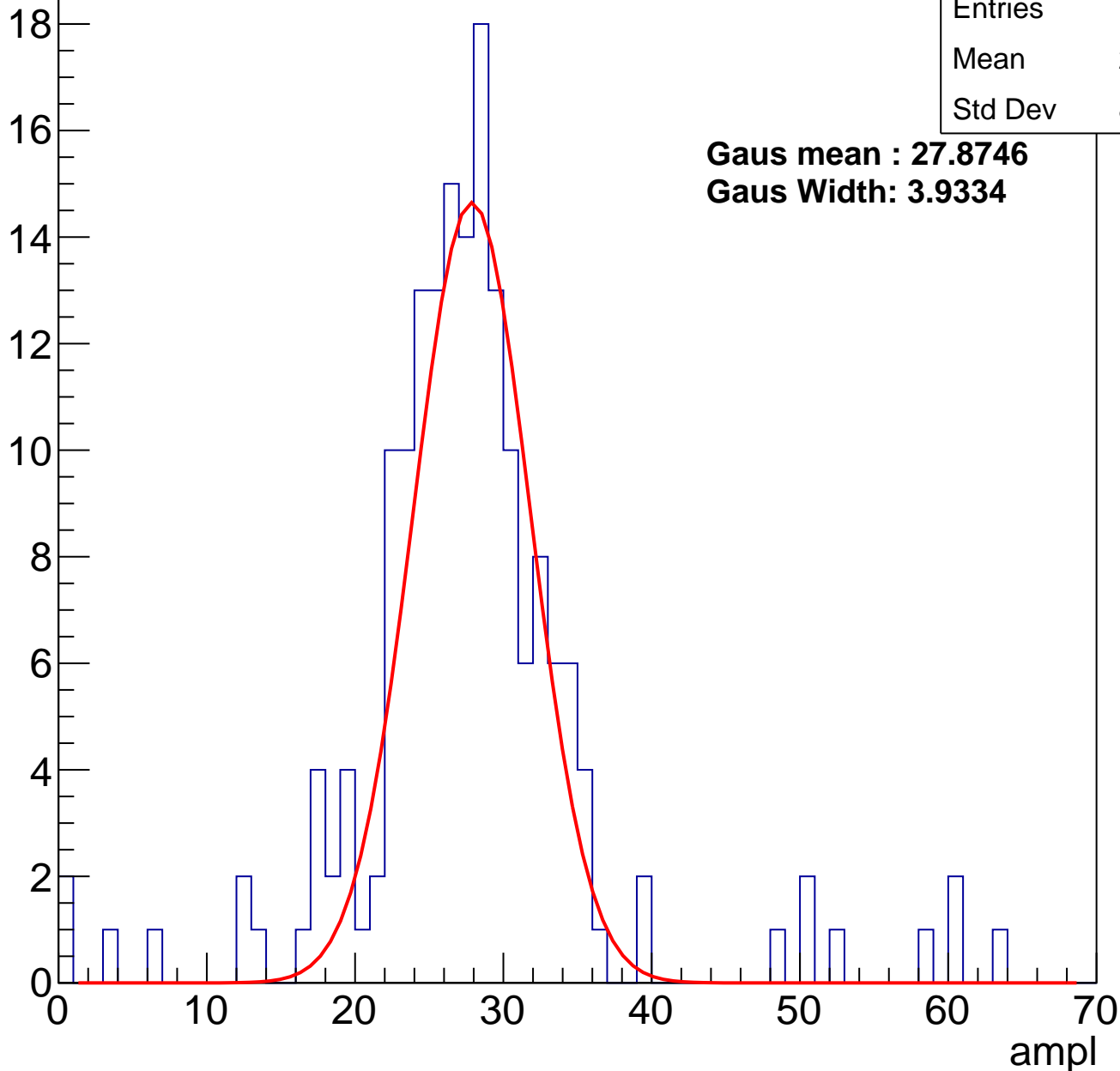
calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	27.41
Std Dev	8.511

Gaus mean : 27.8746

Gaus Width: 3.9334

Entry



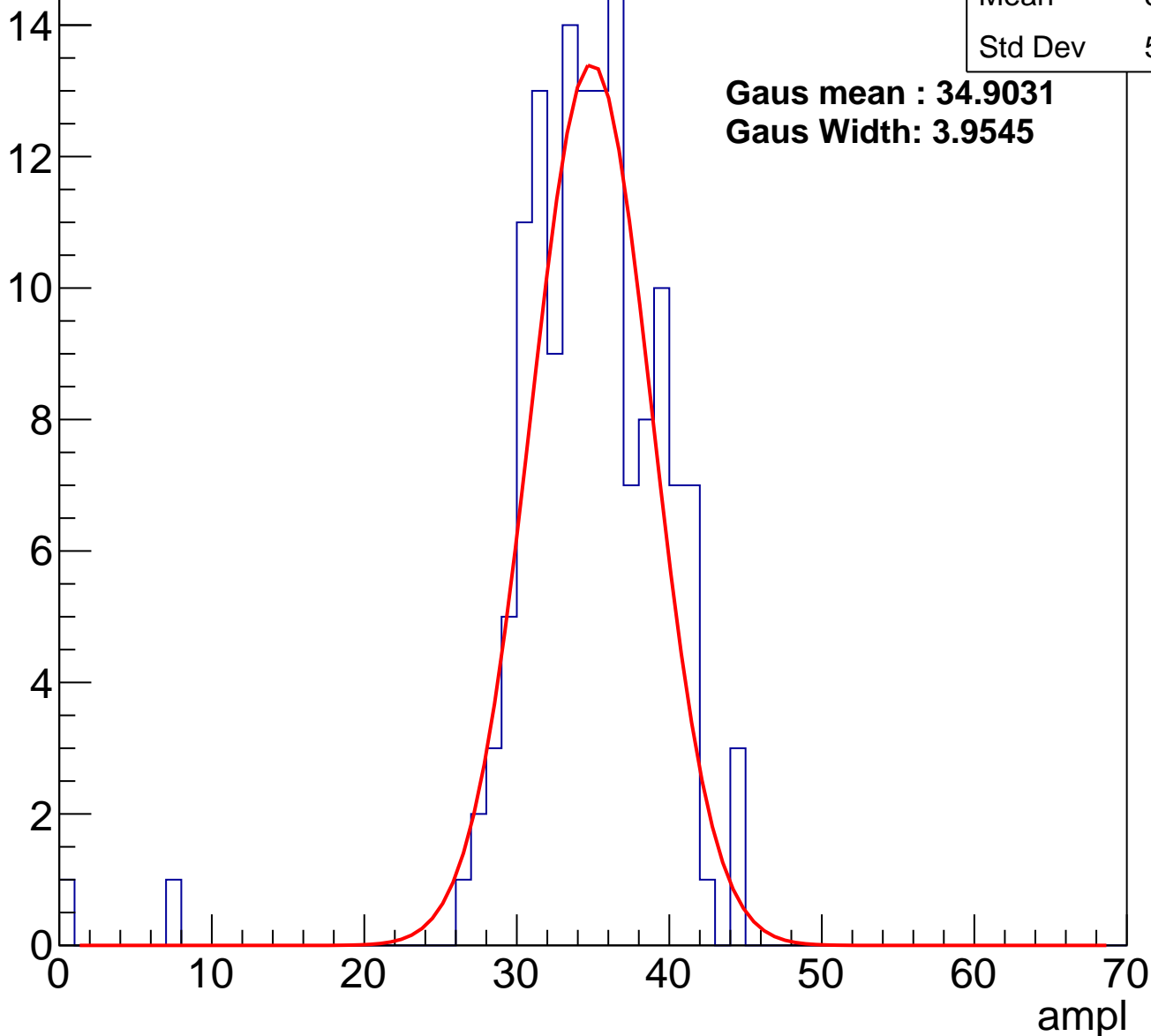
B0L101S, U18-ch124, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	34.24
Std Dev	5.328

Gaus mean : 34.9031
Gaus Width: 3.9545

Entry



B0L101S, U18-ch124, adc2

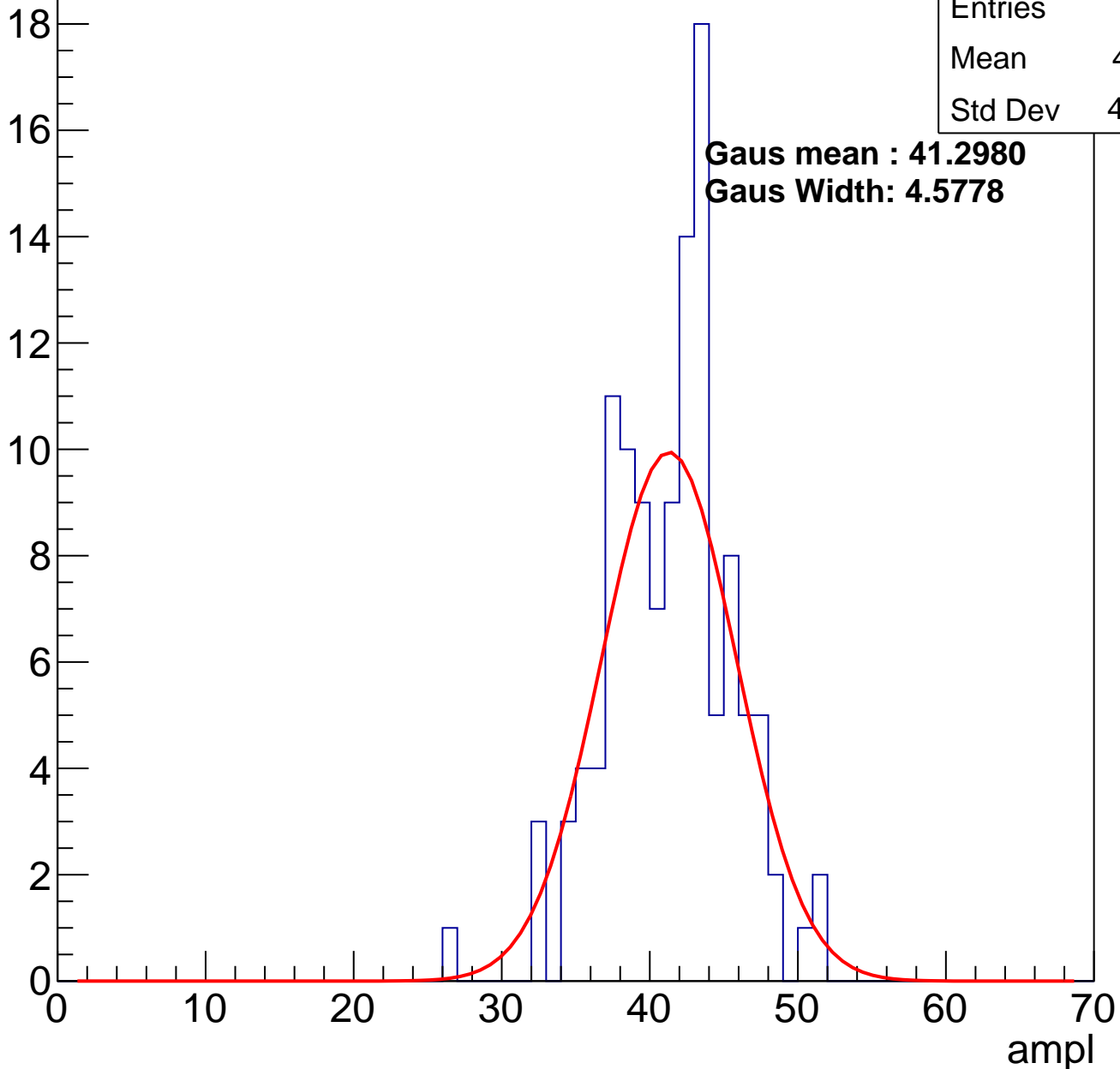
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	40.91
Std Dev	4.176

Gaus mean : 41.2980

Gaus Width: 4.5778

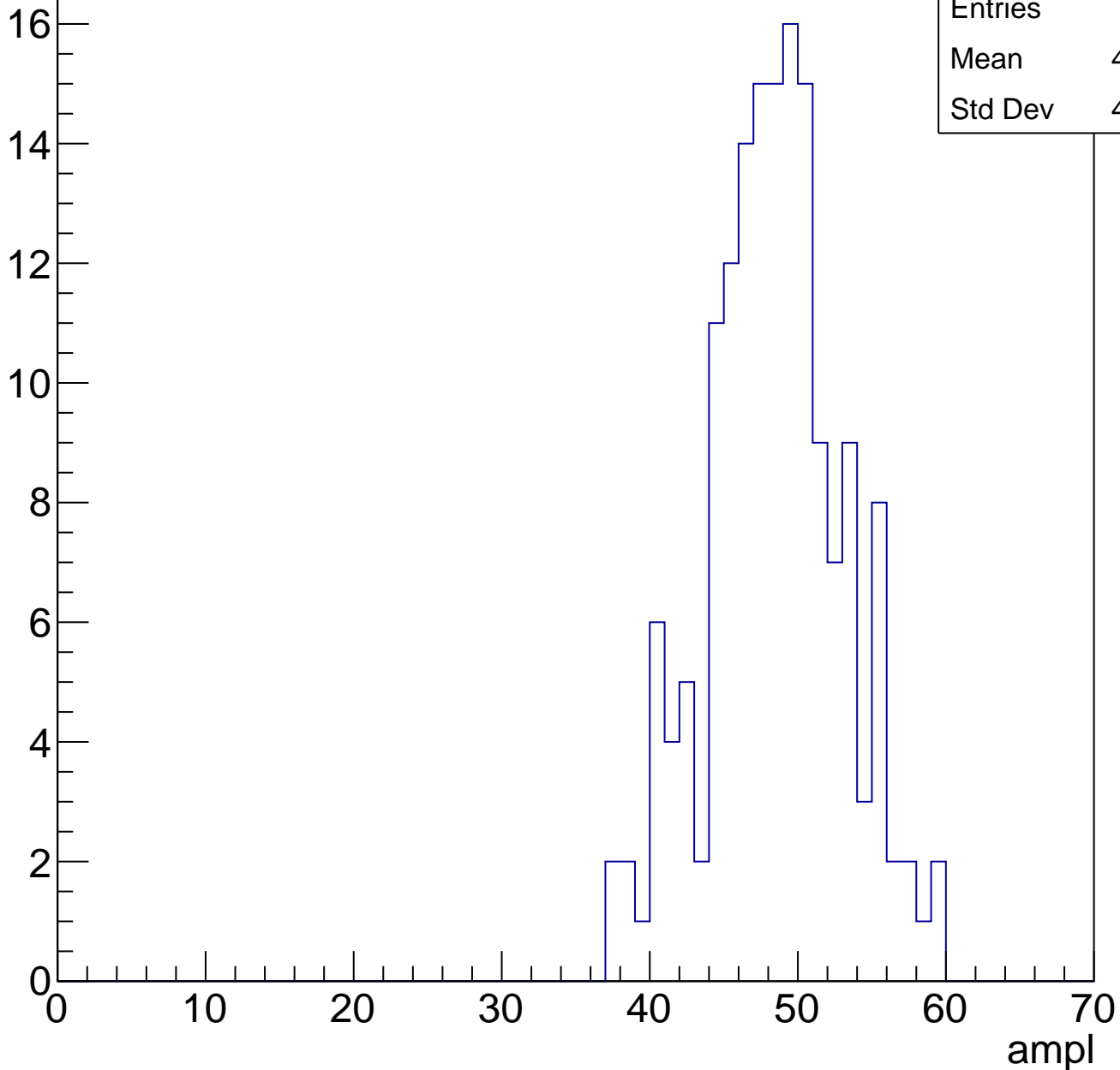
Entry



B0L101S, U18-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

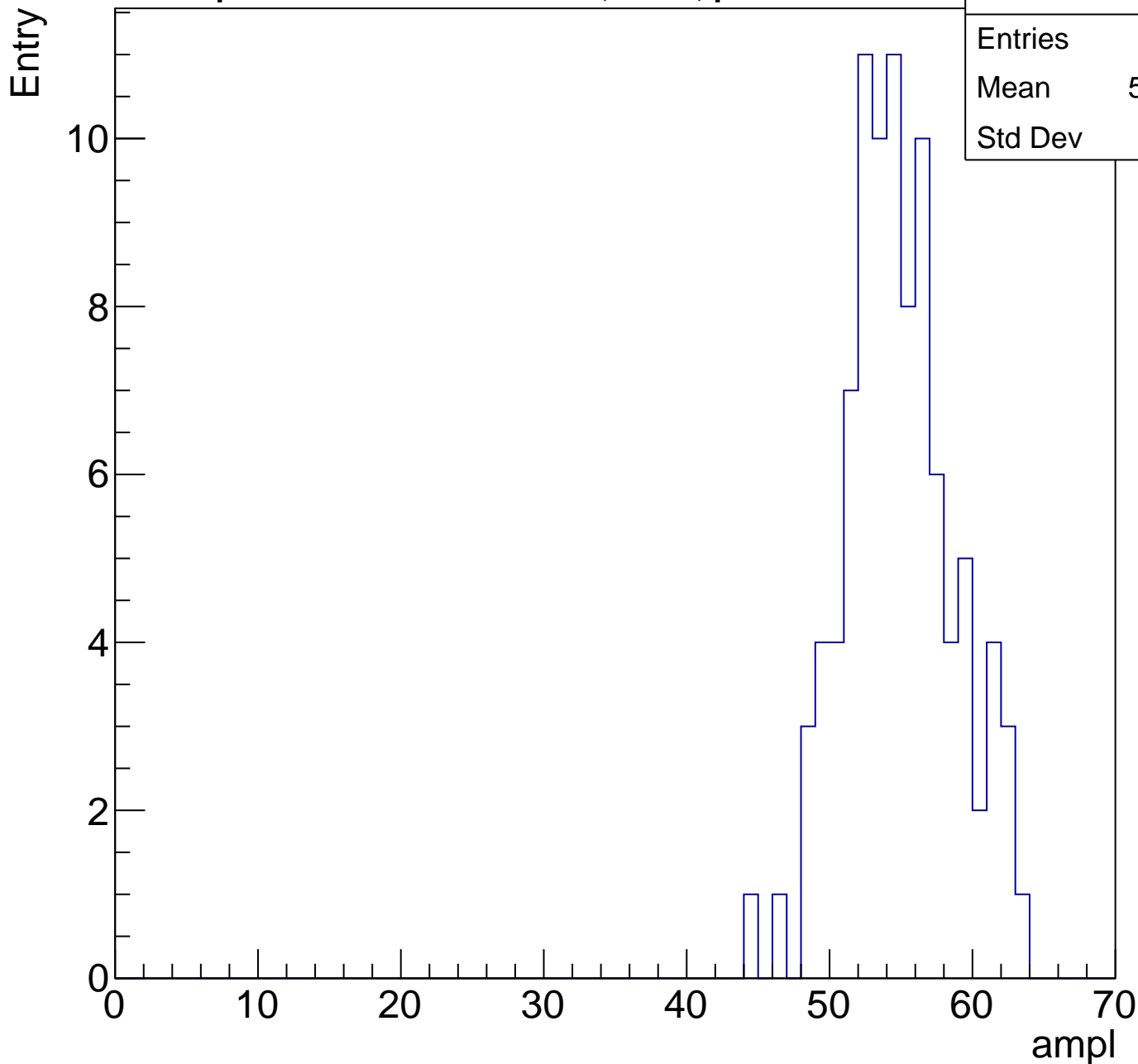
Entry



B0L101S, U18-ch124, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	54.37
Std Dev	3.8



B0L101S, U18-ch124, adc5

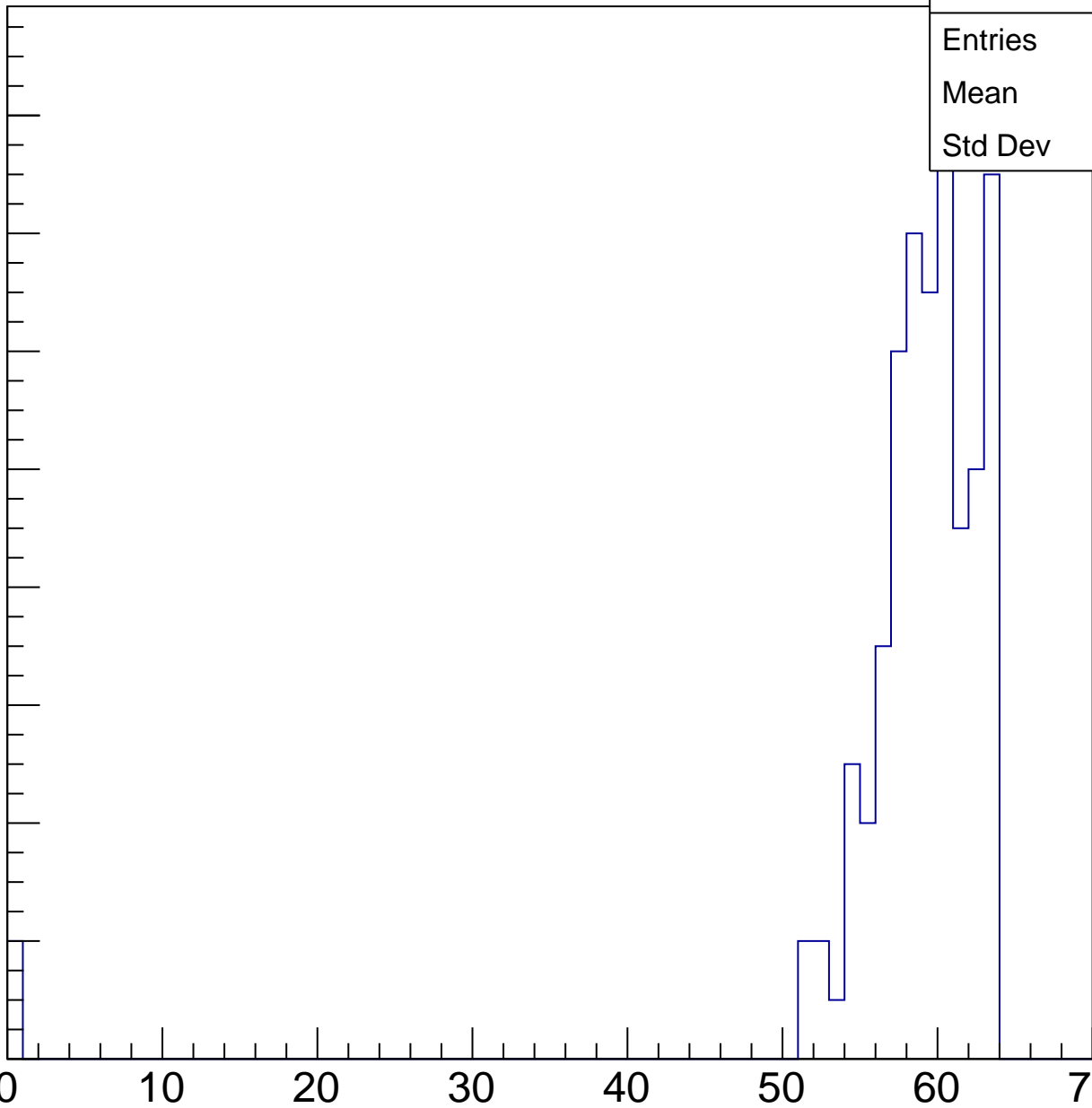
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	113
Mean	57.86
Std Dev	8.294

ampl

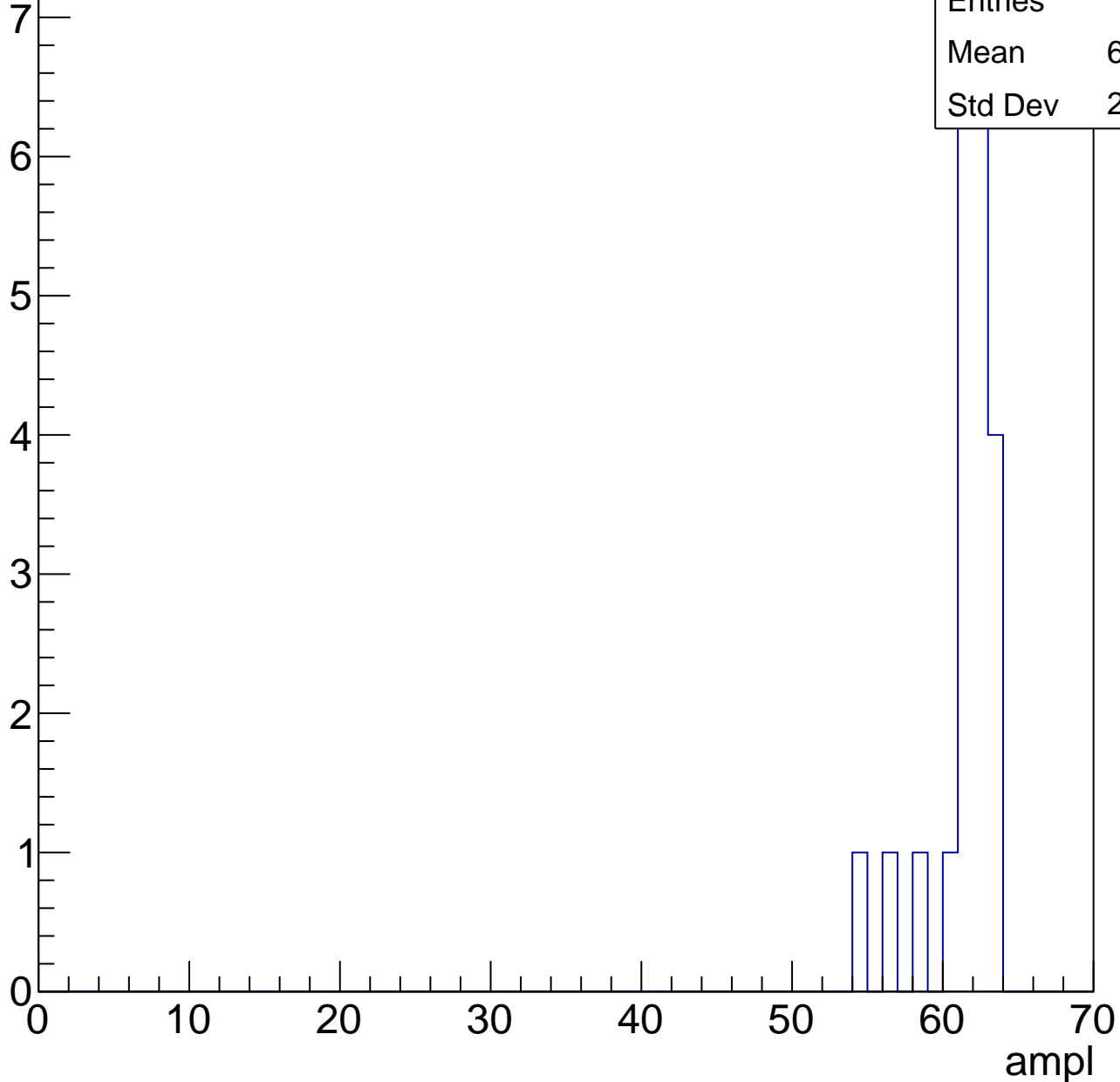


B0L101S, U18-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	22
Mean	60.95
Std Dev	2.205



B0L101S, U18-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U18-ch125, adc0

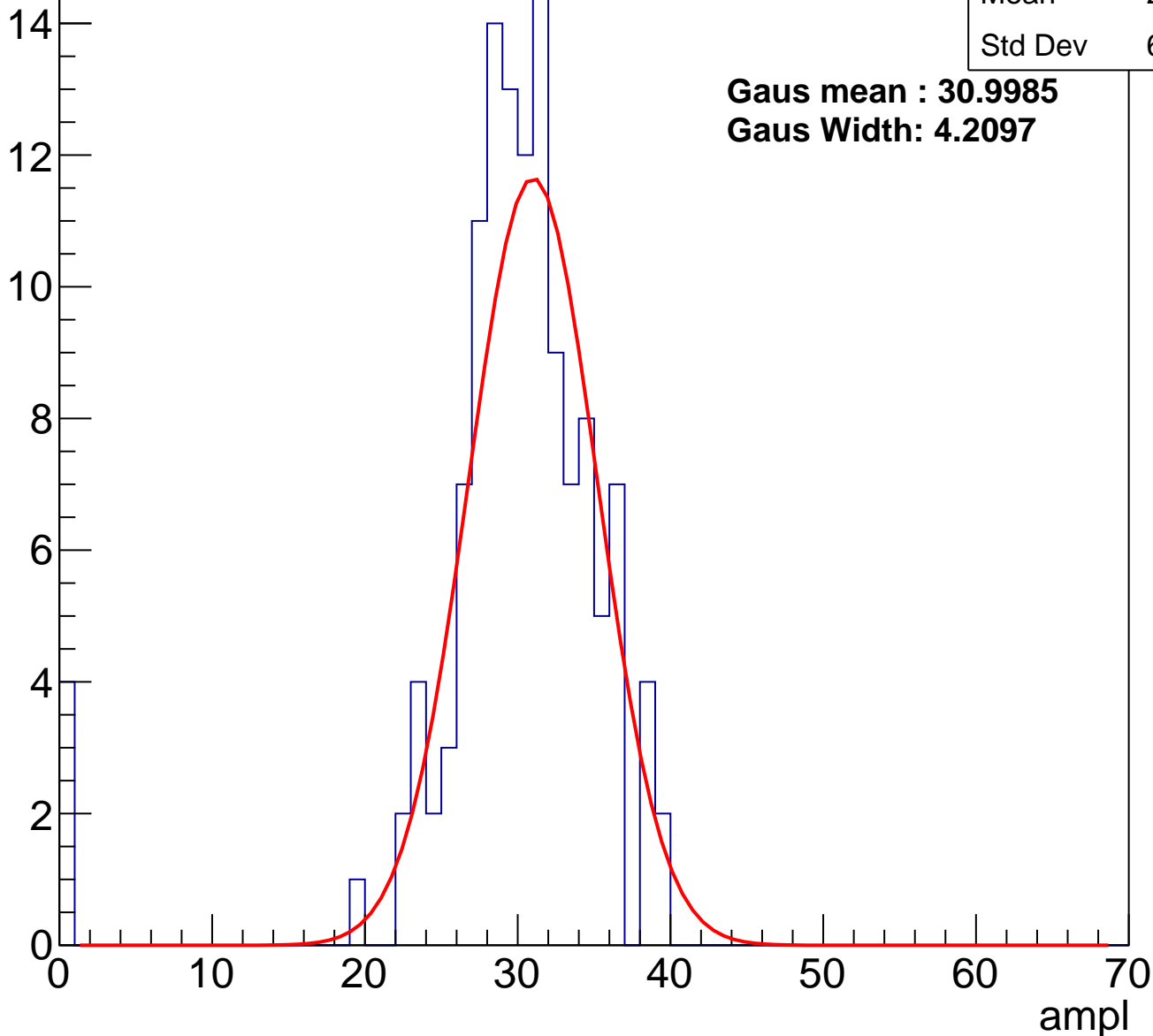
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	29.22
Std Dev	6.463

Gaus mean : 30.9985

Gaus Width: 4.2097

Entry



B0L101S, U18-ch125, adc1

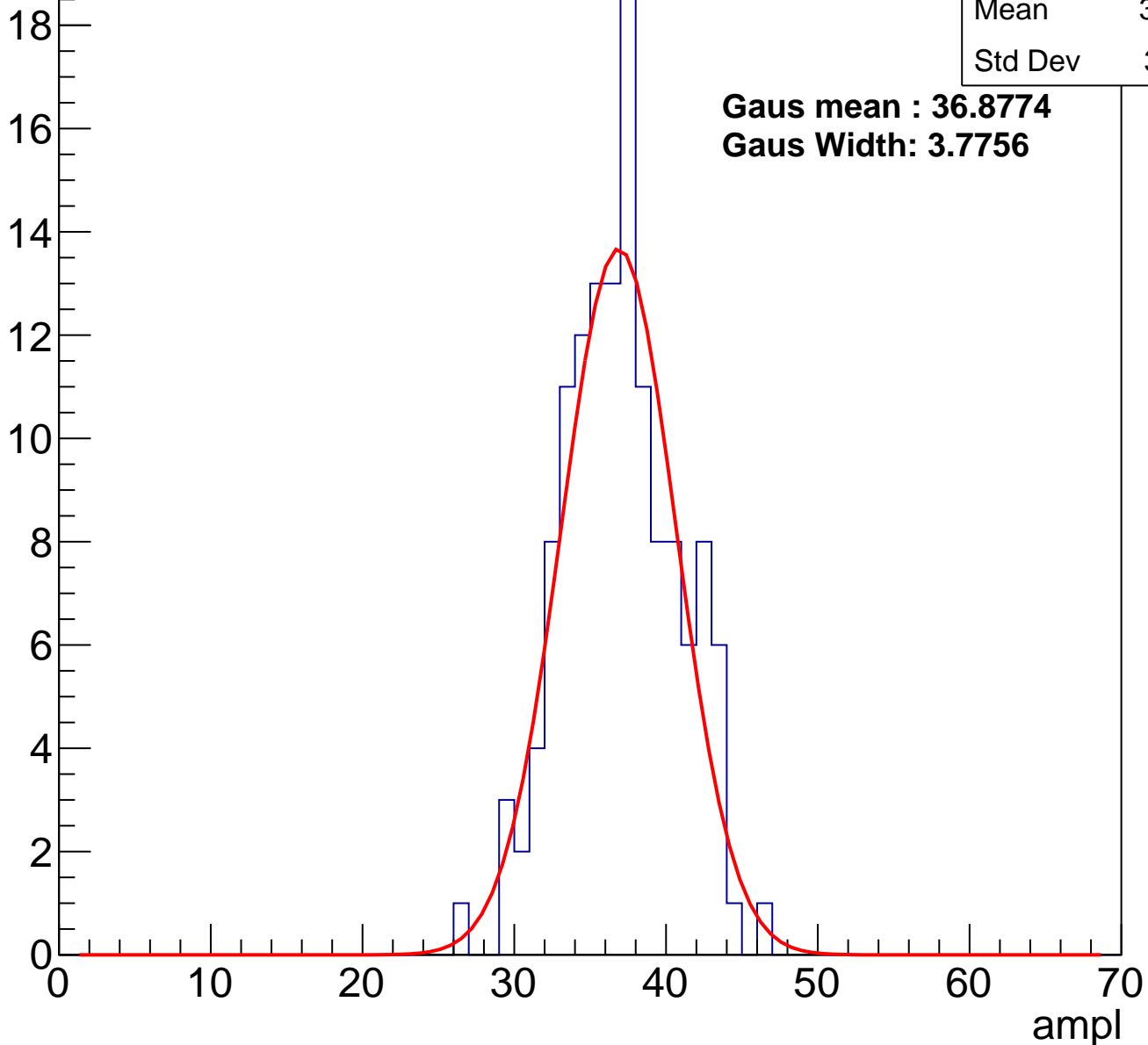
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	36.52
Std Dev	3.681

Gaus mean : 36.8774

Gaus Width: 3.7756

Entry



B0L101S, U18-ch125, adc2

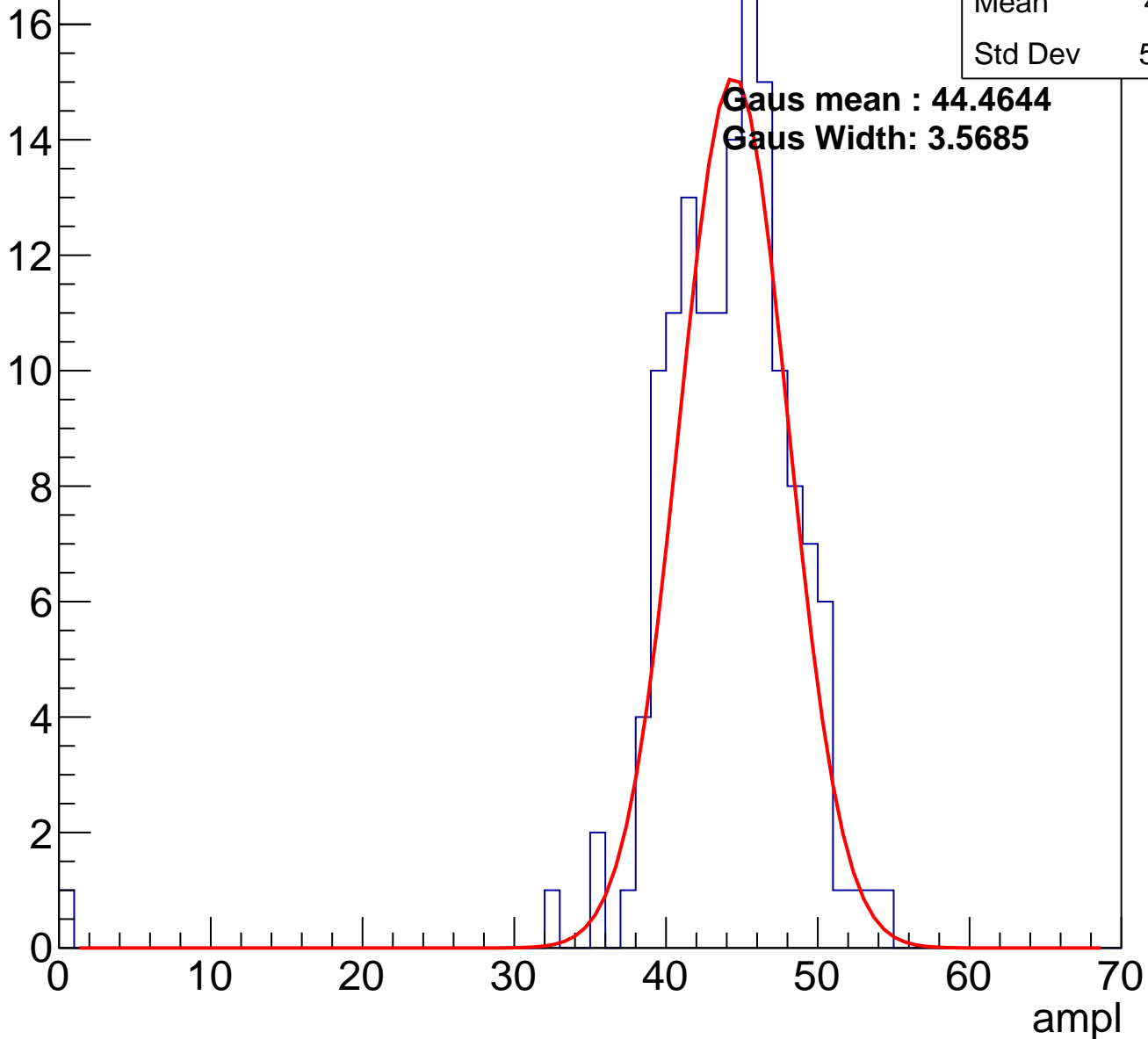
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	146
Mean	43.61
Std Dev	5.228

Gaus mean : 44.4644

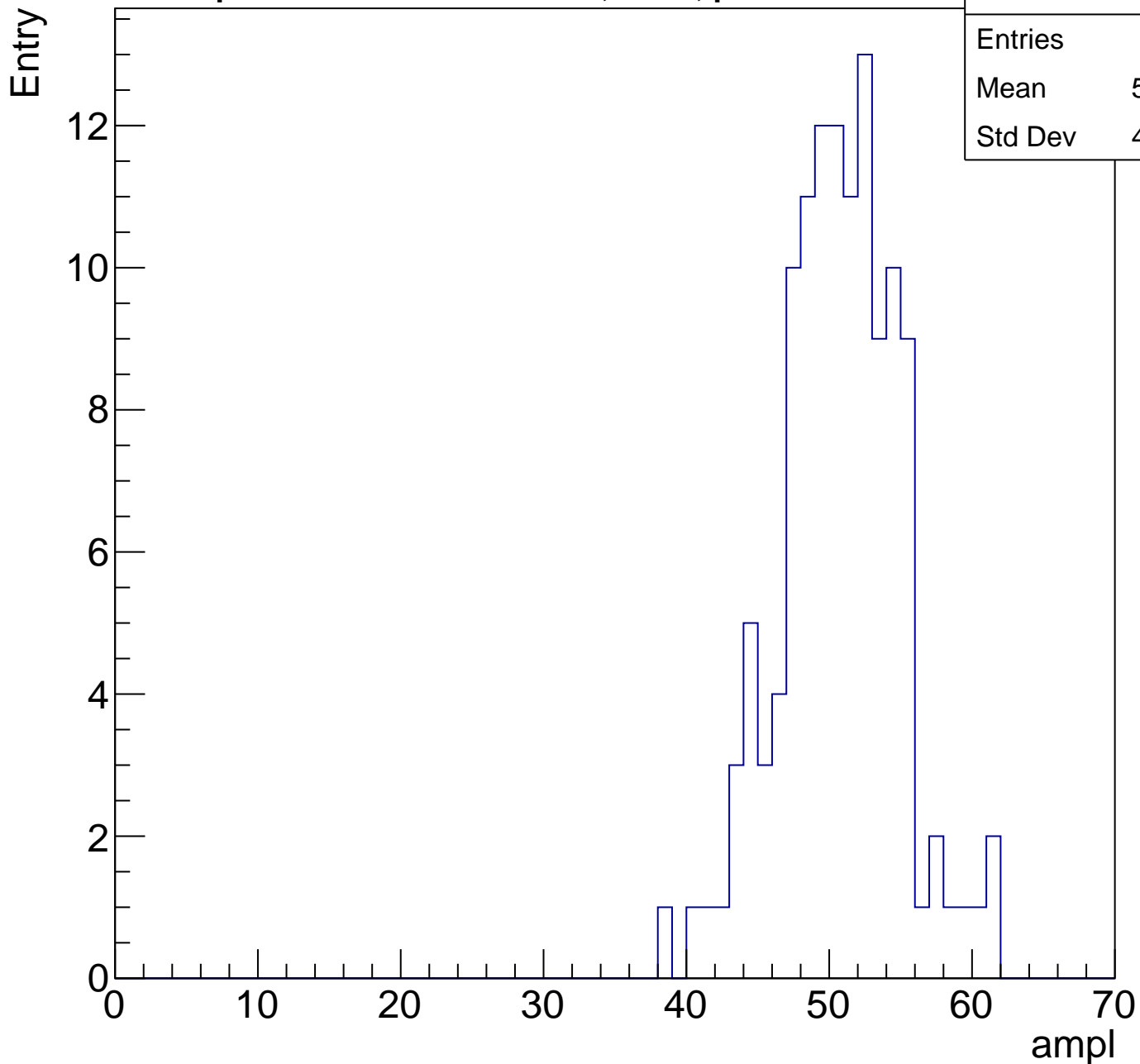
Gaus Width: 3.5685



B0L101S, U18-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	50.27
Std Dev	4.156



B0L101S, U18-ch125, adc4

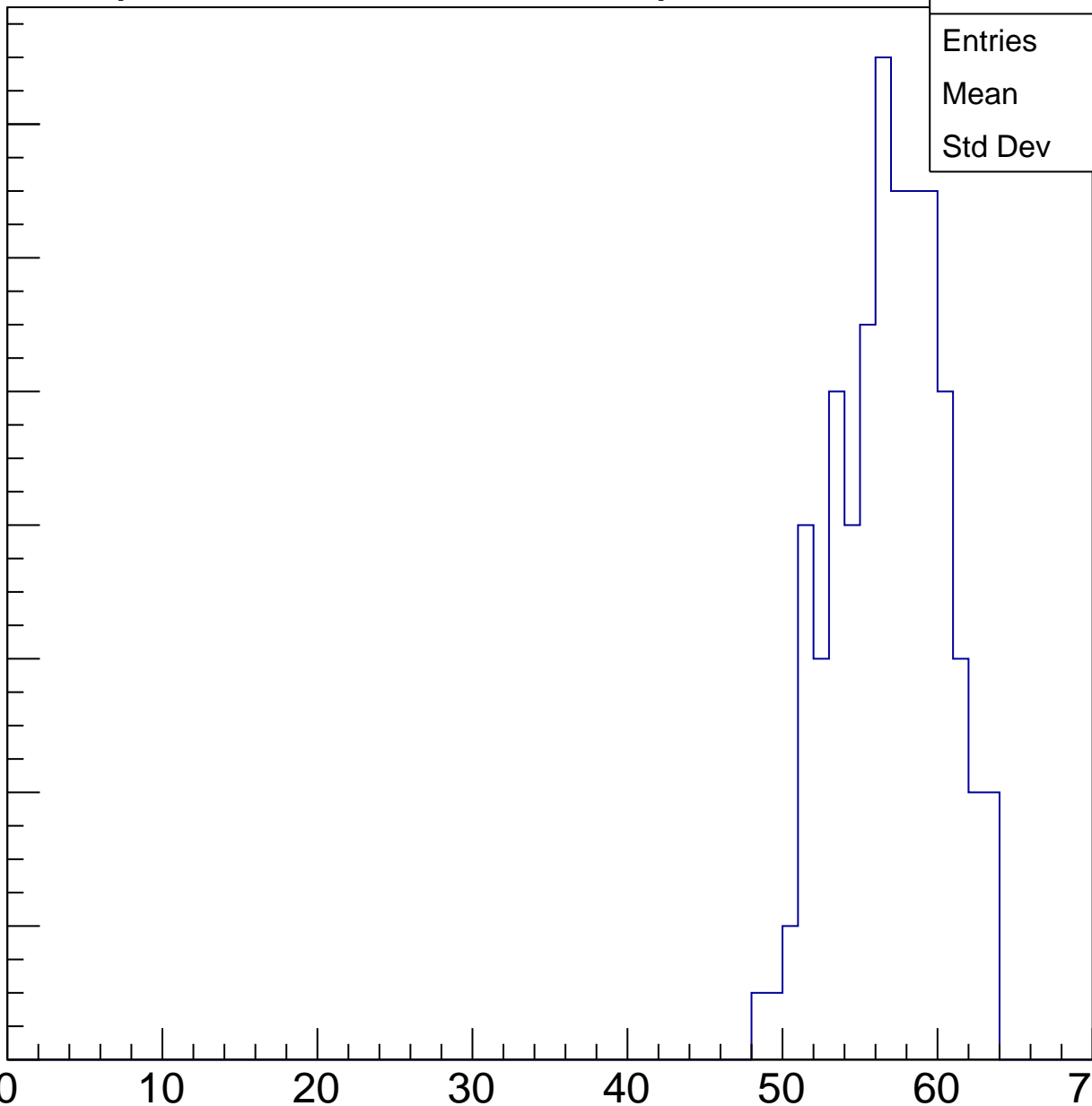
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	125
Mean	56.42
Std Dev	3.376

ampl



B0L101S, U18-ch125, adc5

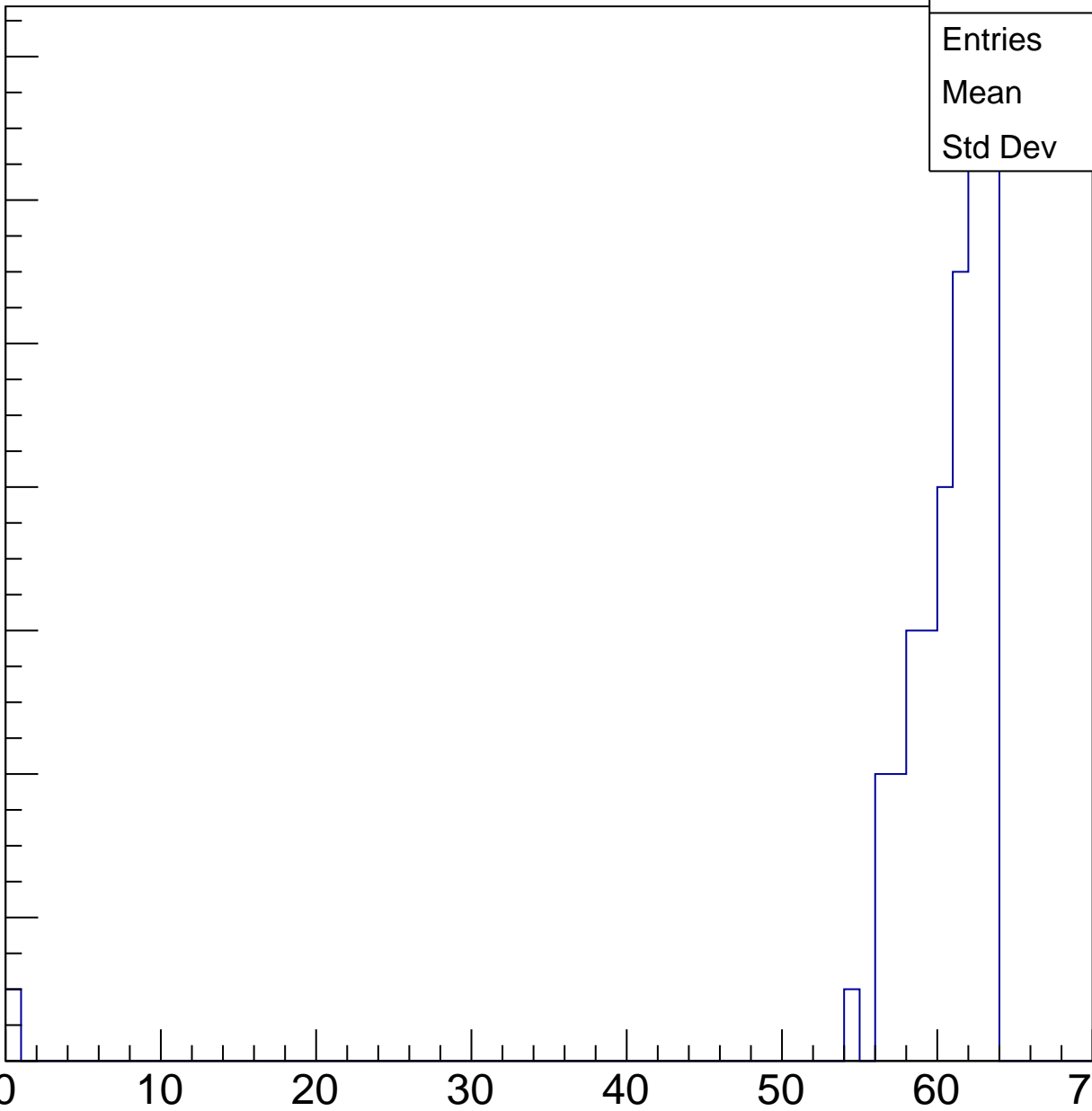
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	59.51
Std Dev	7.611

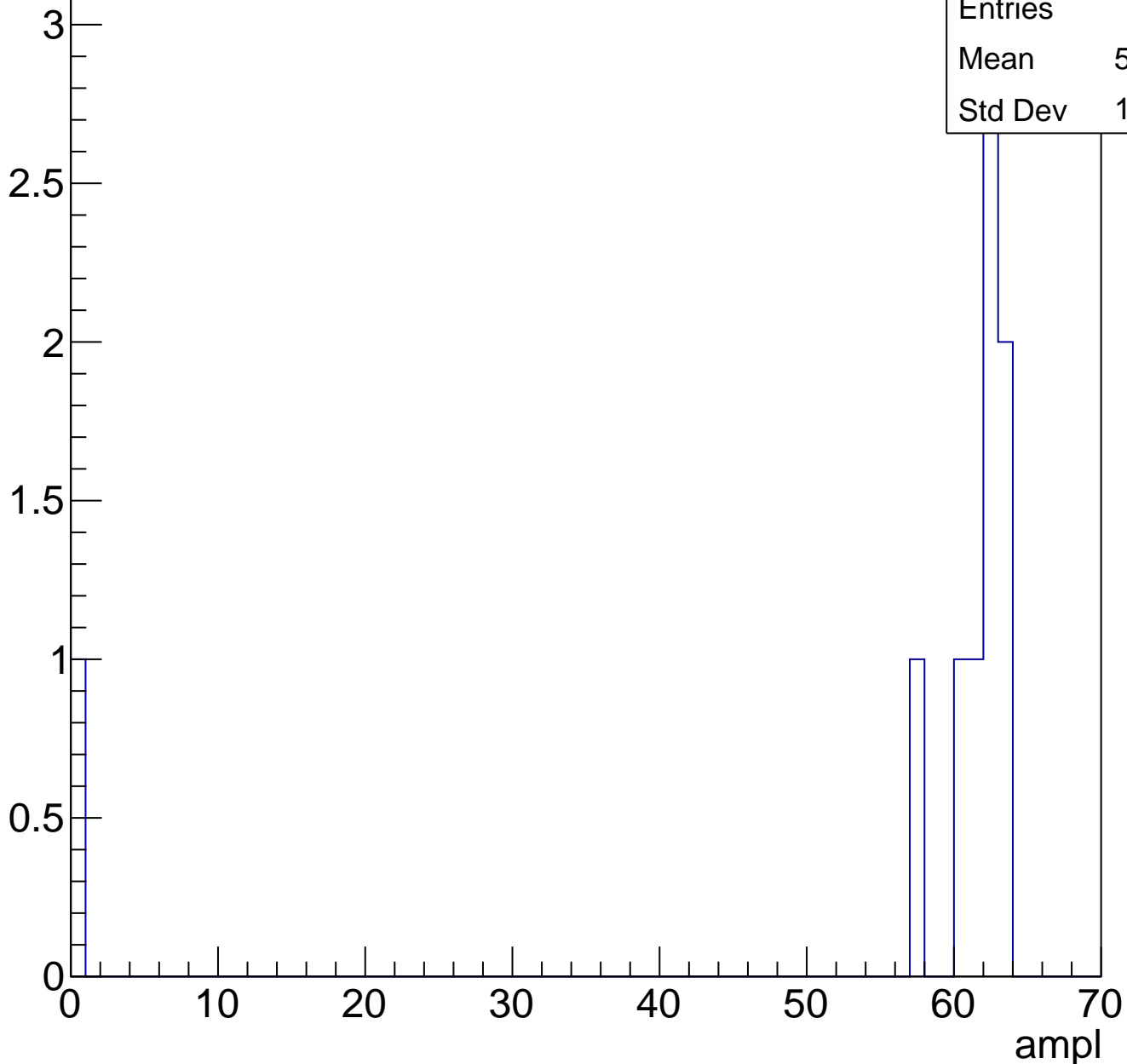
ampl



B0L101S, U18-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

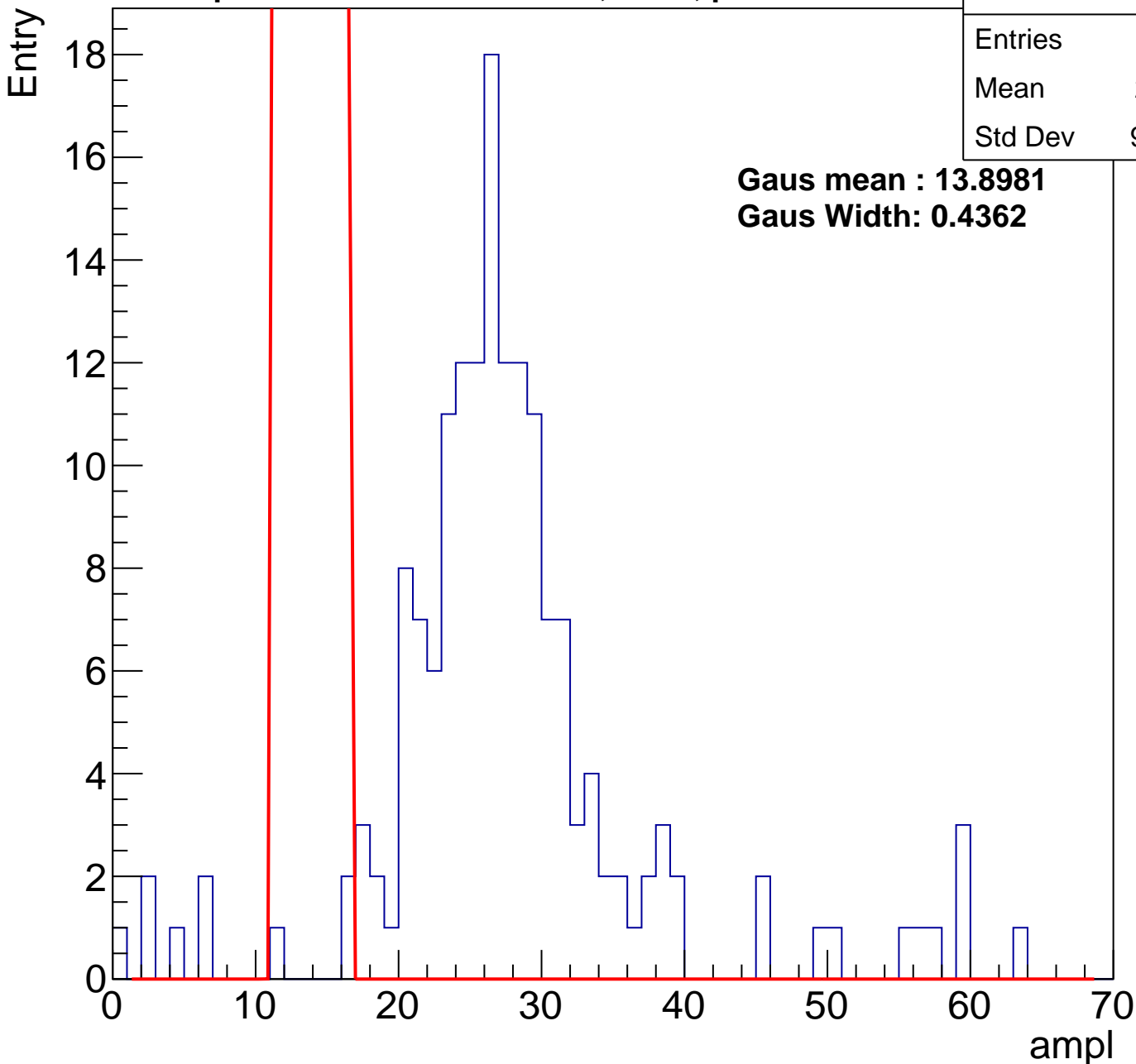
B0L101S, U18-ch126, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	27.31
Std Dev	9.668

Gaus mean : 13.8981

Gaus Width: 0.4362



B0L101S, U18-ch126, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	140
Mean	33.54
Std Dev	4.696

Gaus mean : 33.8882

Gaus Width: 3.9341

0

B0L101S, U18-ch126, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

125

Mean

39.89

Std Dev

3.962

Gaus mean : 40.6913

Gaus Width: 4.1595

0

10

20

30

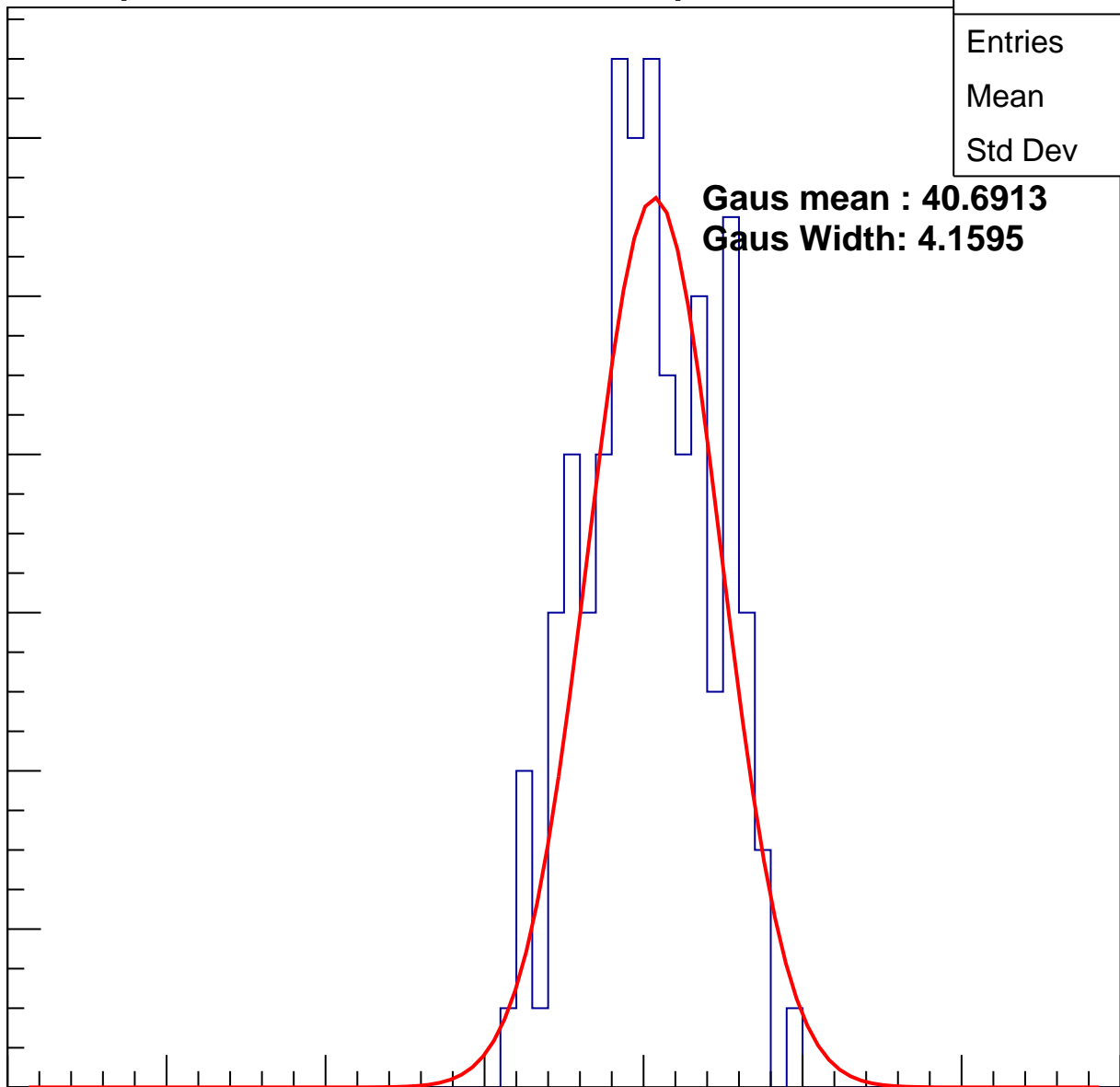
40

50

60

70

ampl



B0L101S, U18-ch126, adc3

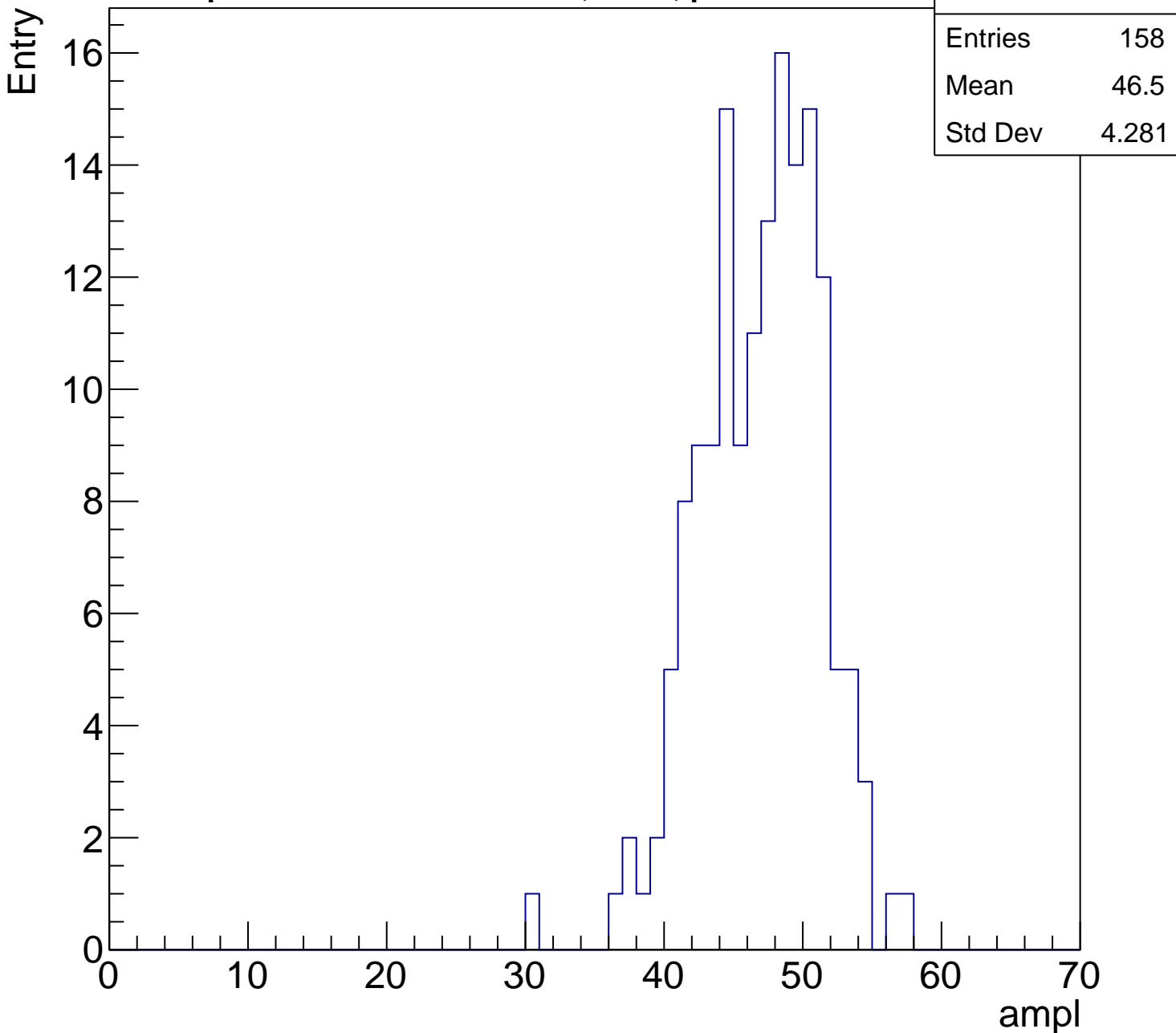
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	158
Mean	46.5
Std Dev	4.281

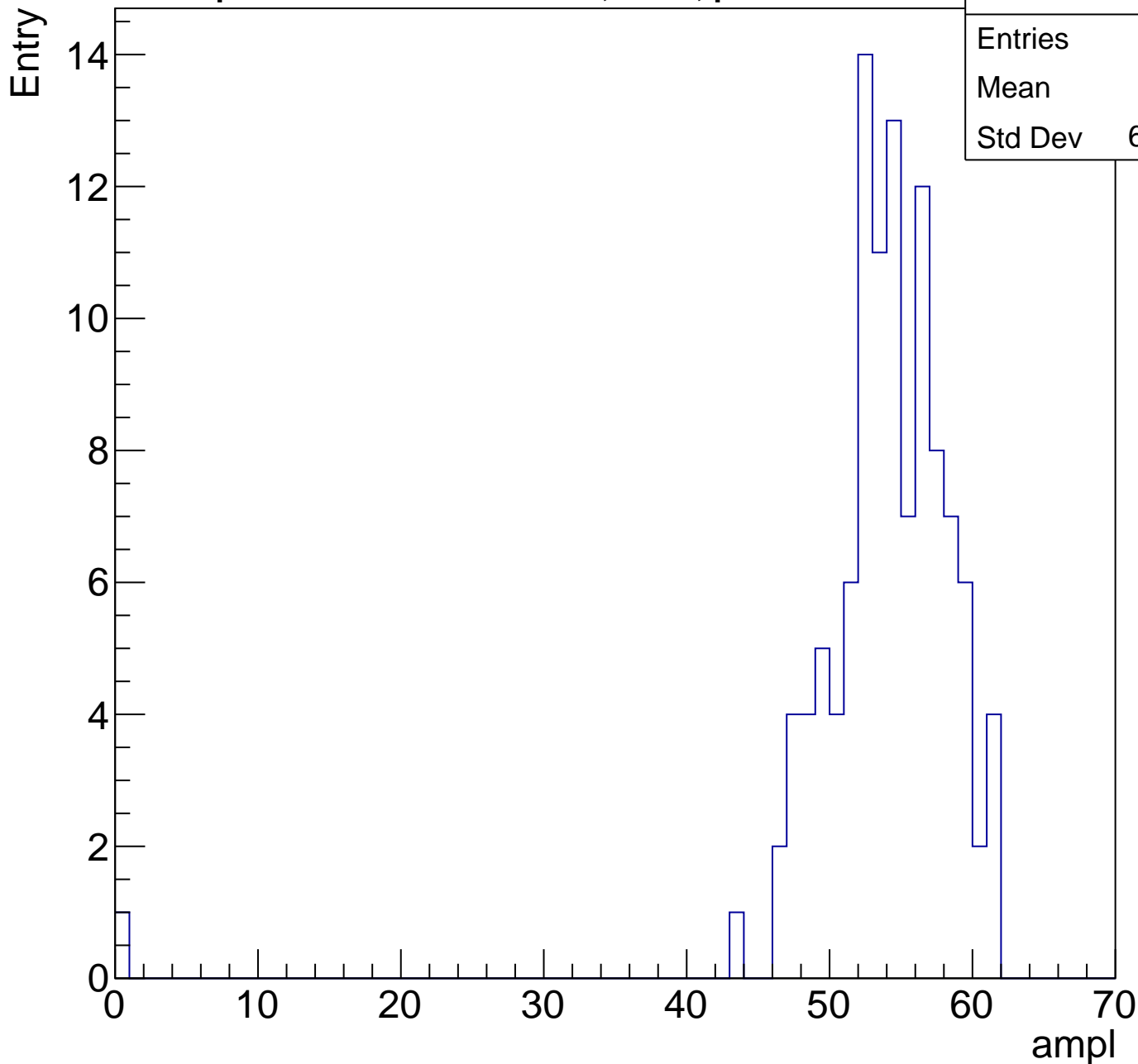
ampl



B0L101S, U18-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	53.3
Std Dev	6.308



B0L101S, U18-ch126, adc5

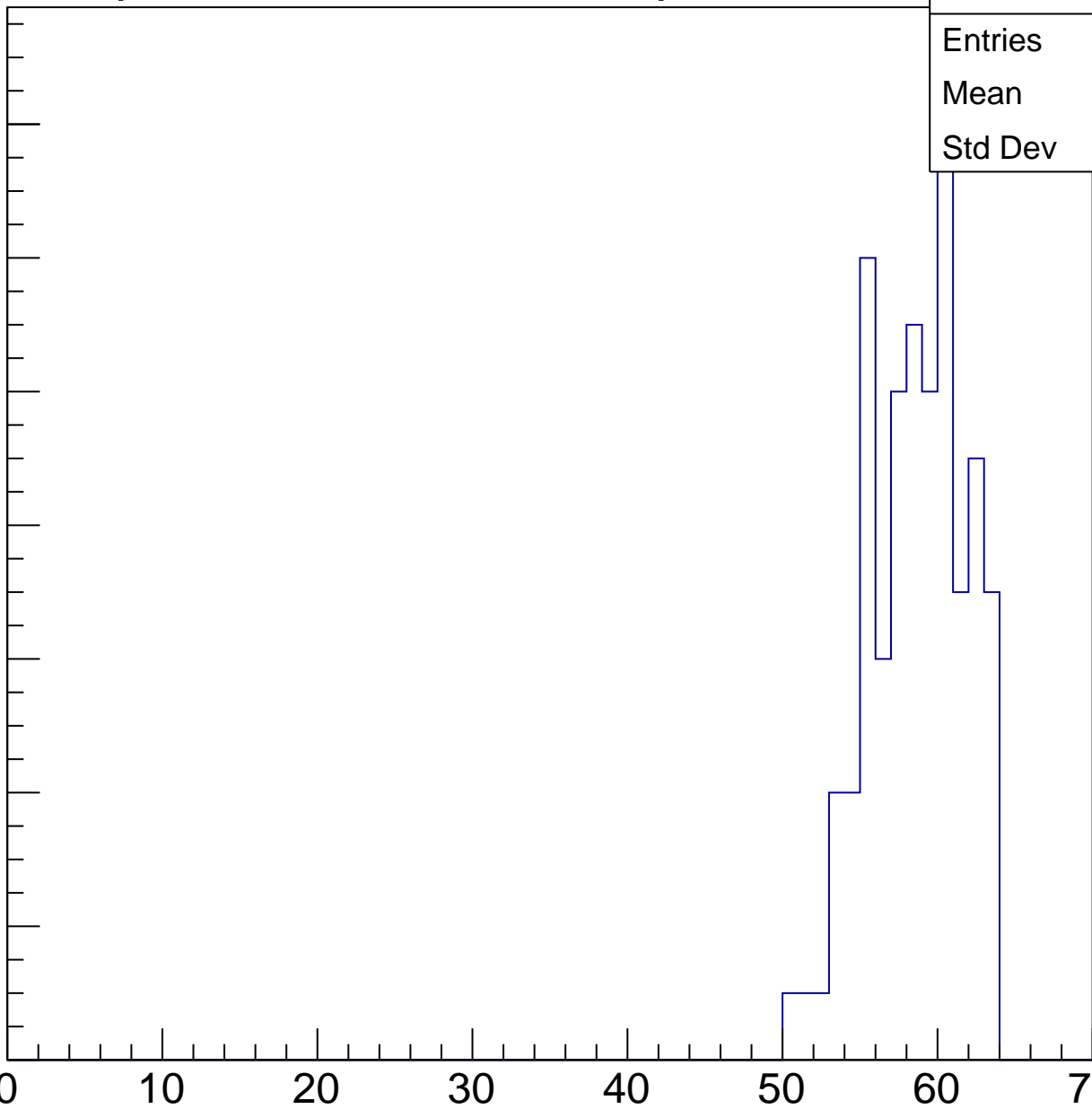
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	58.17
Std Dev	3.037

ampl



B0L101S, U18-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

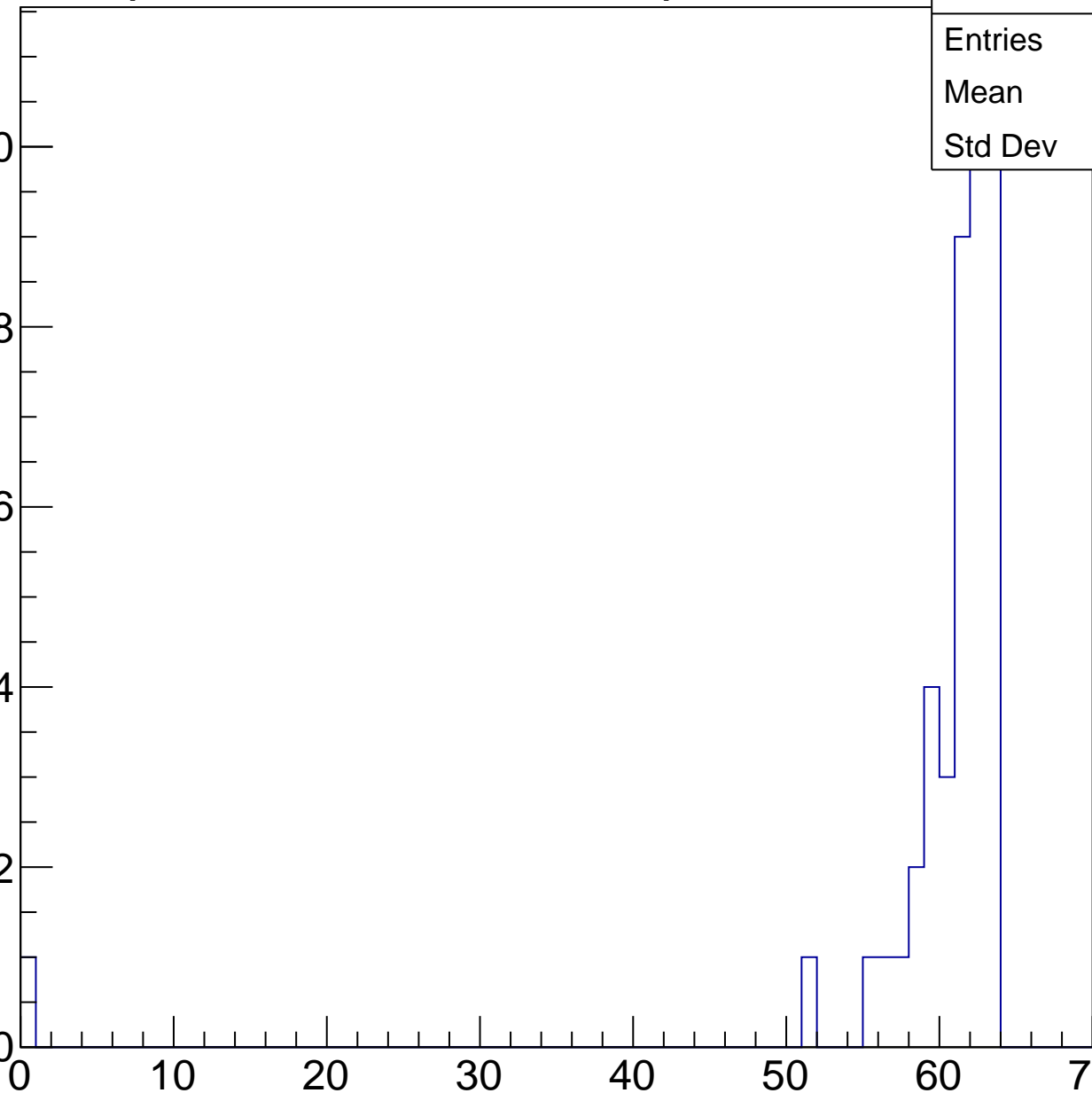
4

2

0

Entries	45
Mean	59.44
Std Dev	9.287

ampl



B0L101S, U18-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



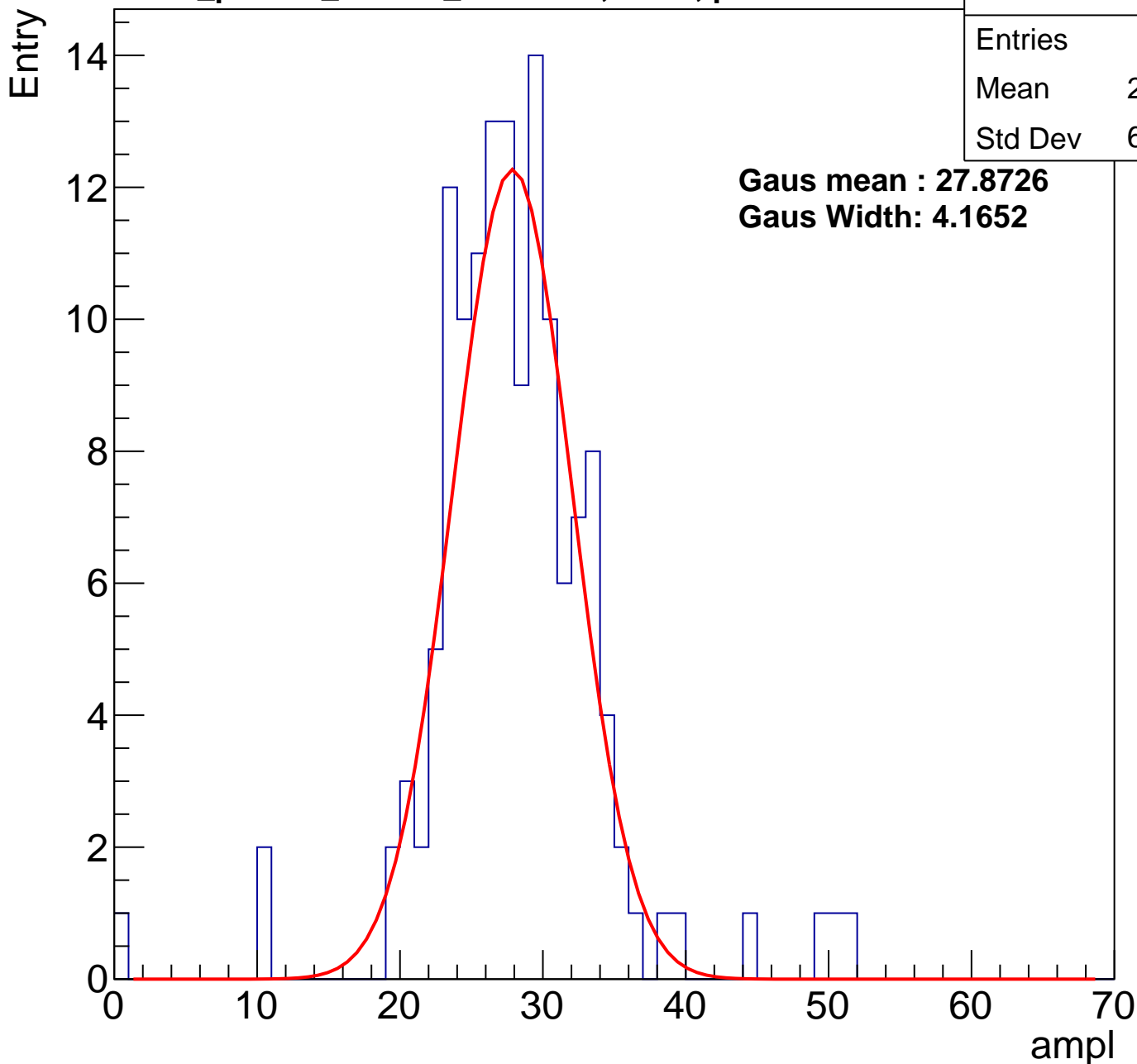
B0L101S, U18-ch127, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	27.62
Std Dev	6.145

Gaus mean : 27.8726

Gaus Width: 4.1652



B0L101S, U18-ch127, adc1

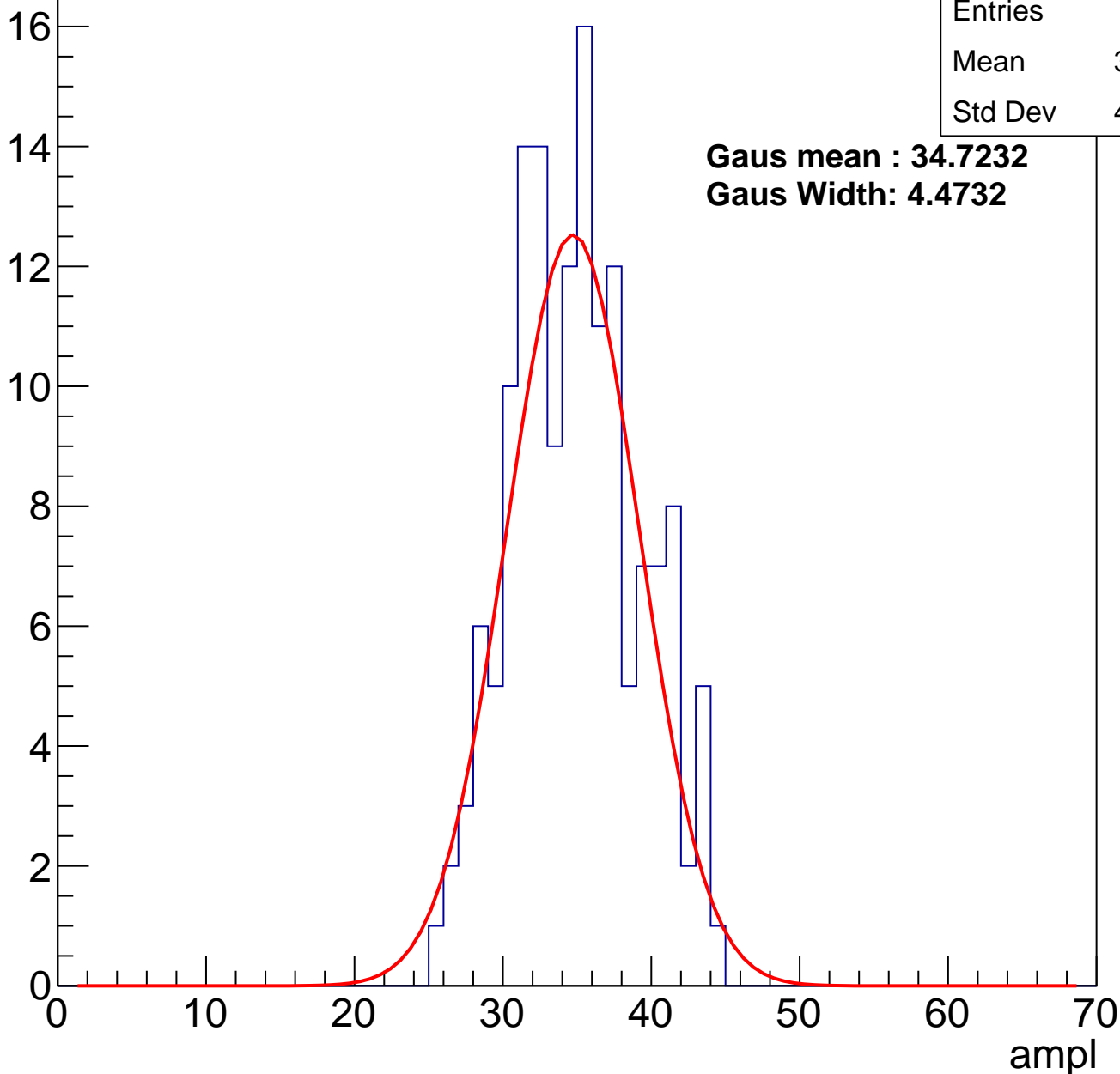
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	34.48
Std Dev	4.253

Gaus mean : 34.7232

Gaus Width: 4.4732

Entry



B0L101S, U18-ch127, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	41.54
Std Dev	3.904

Gaus mean : 42.3820

Gaus Width: 4.3700

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

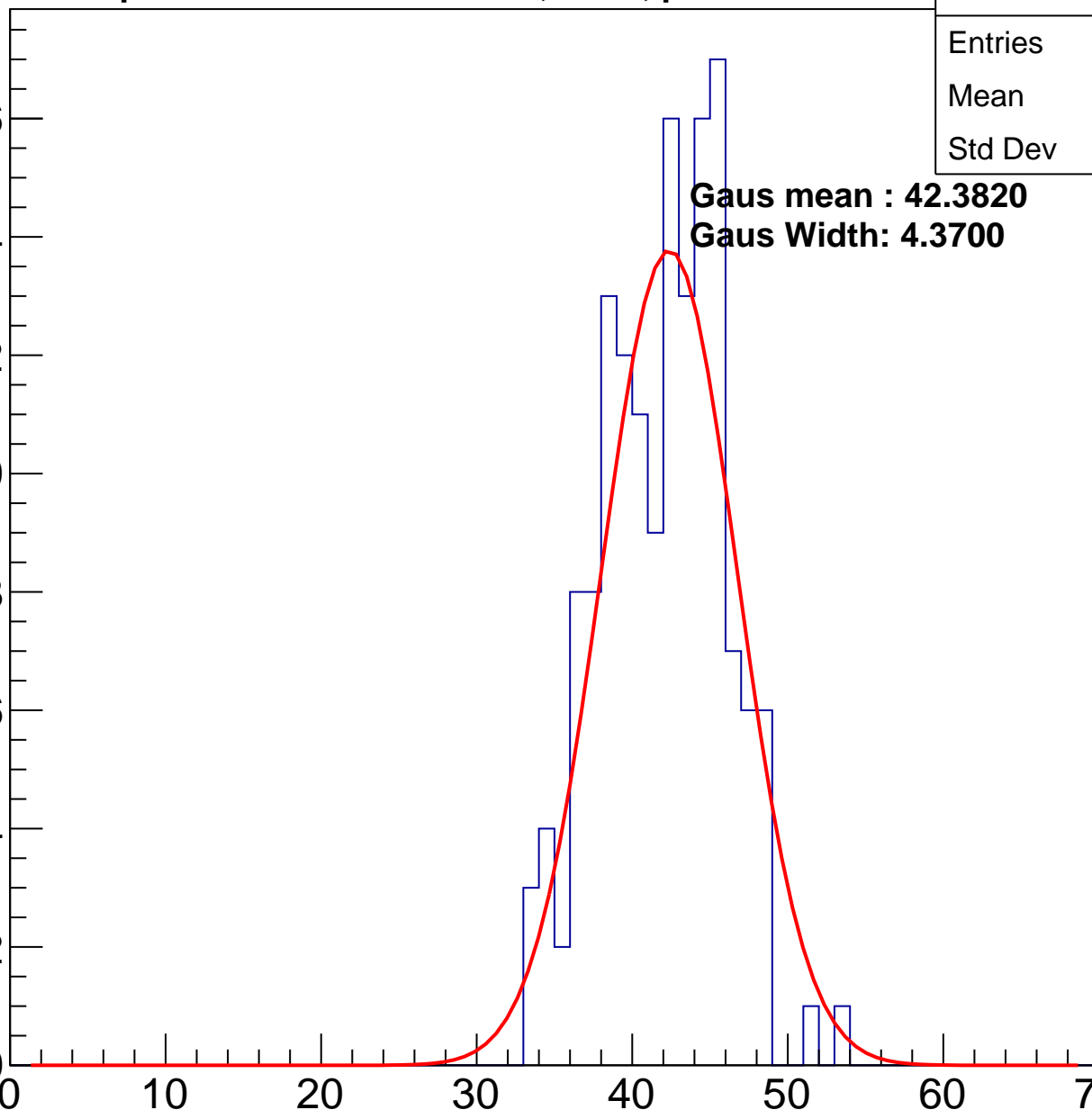
40

50

60

70

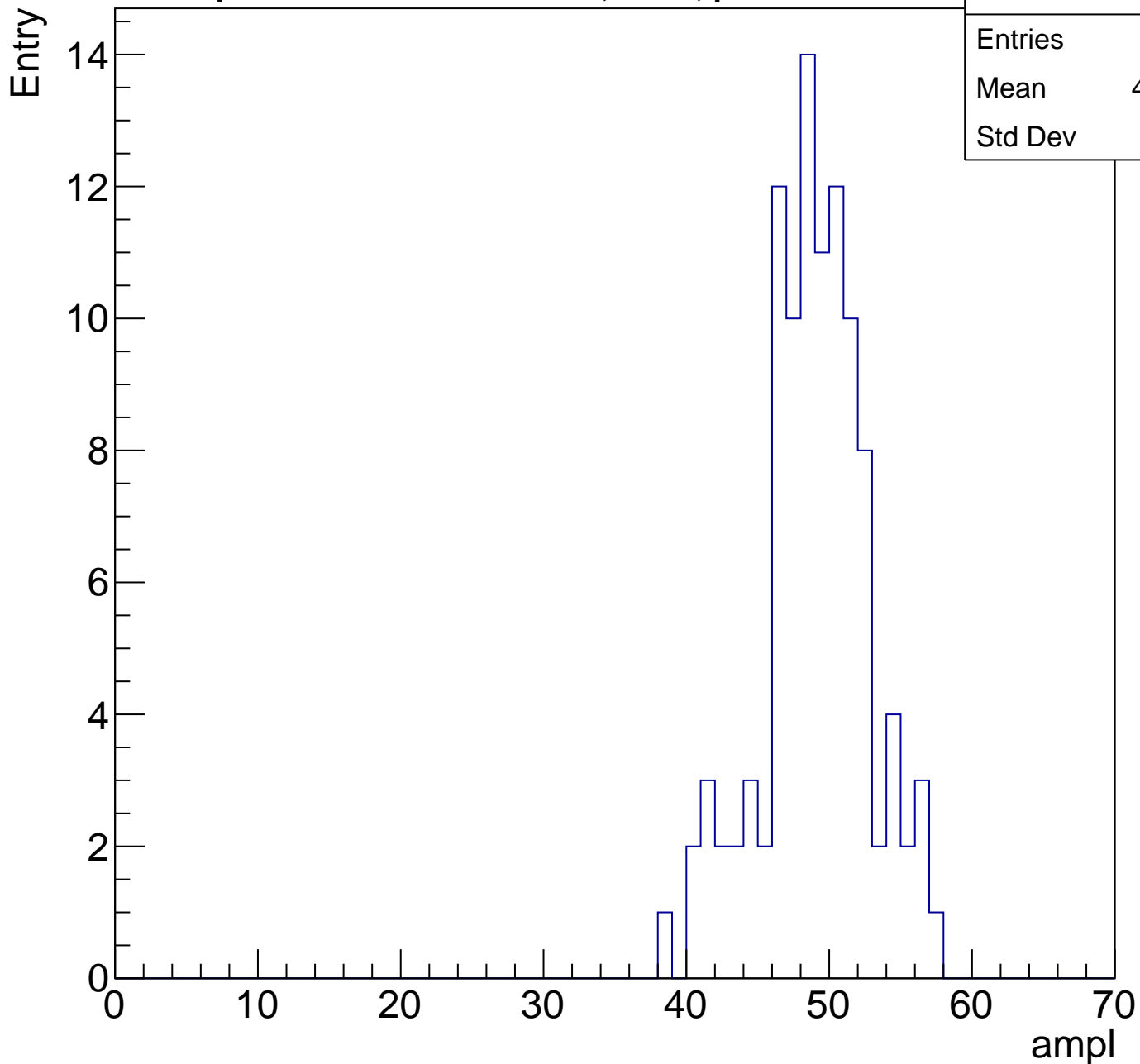
ampl



B0L101S, U18-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

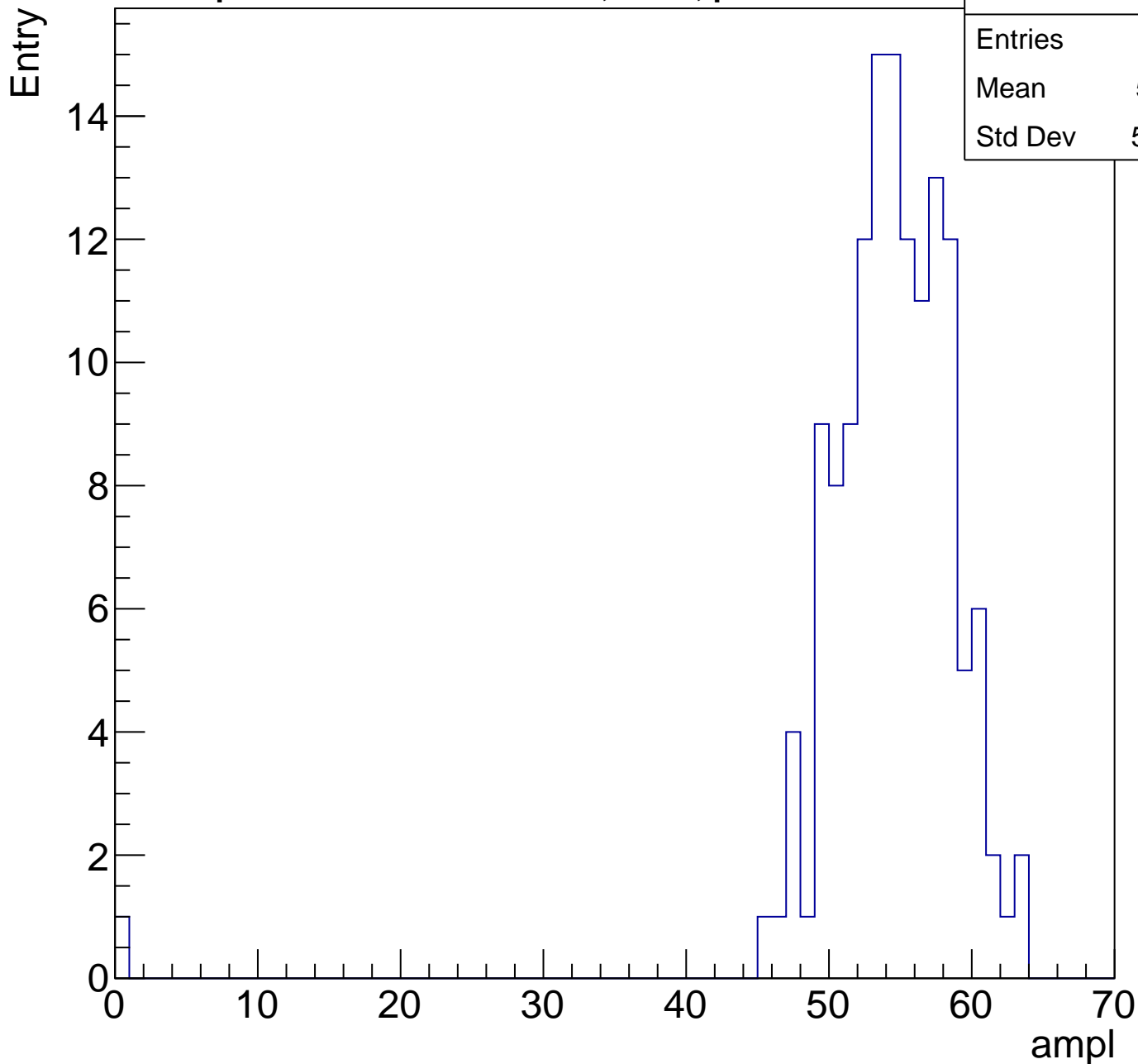
Entries	104
Mean	48.55
Std Dev	3.71



B0L101S, U18-ch127, adc4

calib_packv5_042523_0143.root, FC#1, port C1

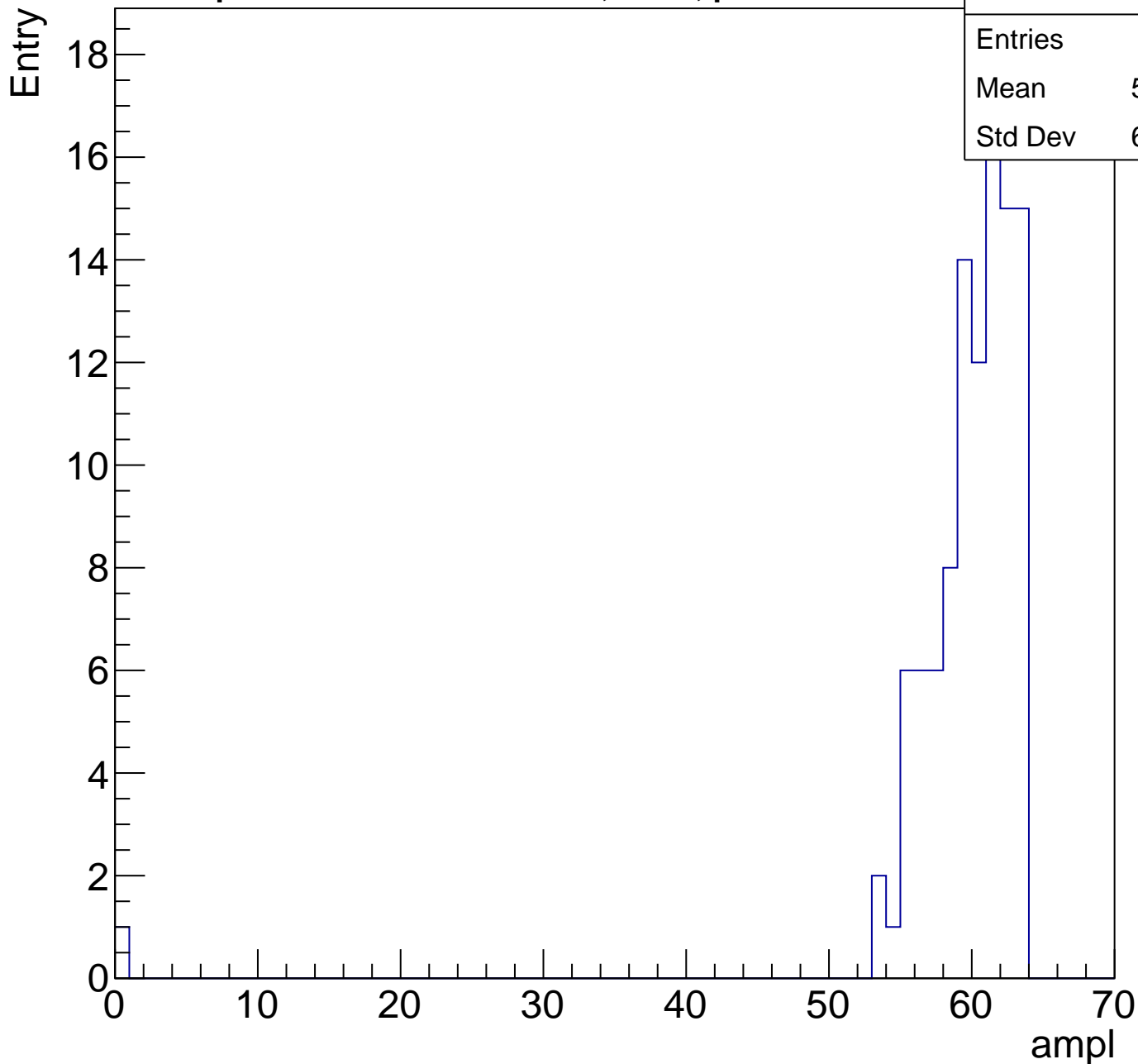
Entries	140
Mean	53.81
Std Dev	5.865



B0L101S, U18-ch127, adc5

calib_packv5_042523_0143.root, FC#1, port C1

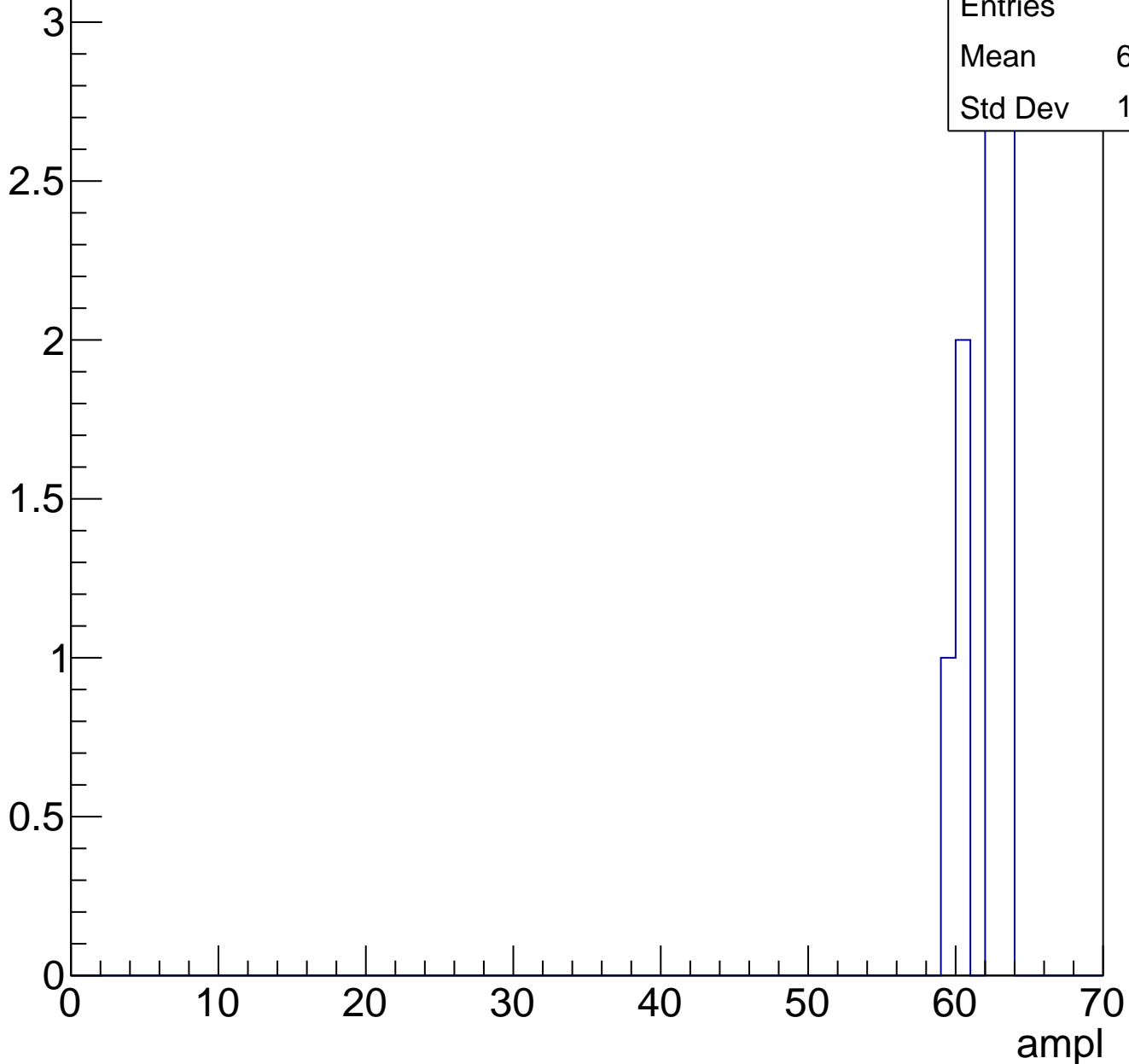
Entries	104
Mean	59.14
Std Dev	6.368



B0L101S, U18-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

