

B0L102S, U11-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.26
Std Dev	12.17

Turn on : 24.2959

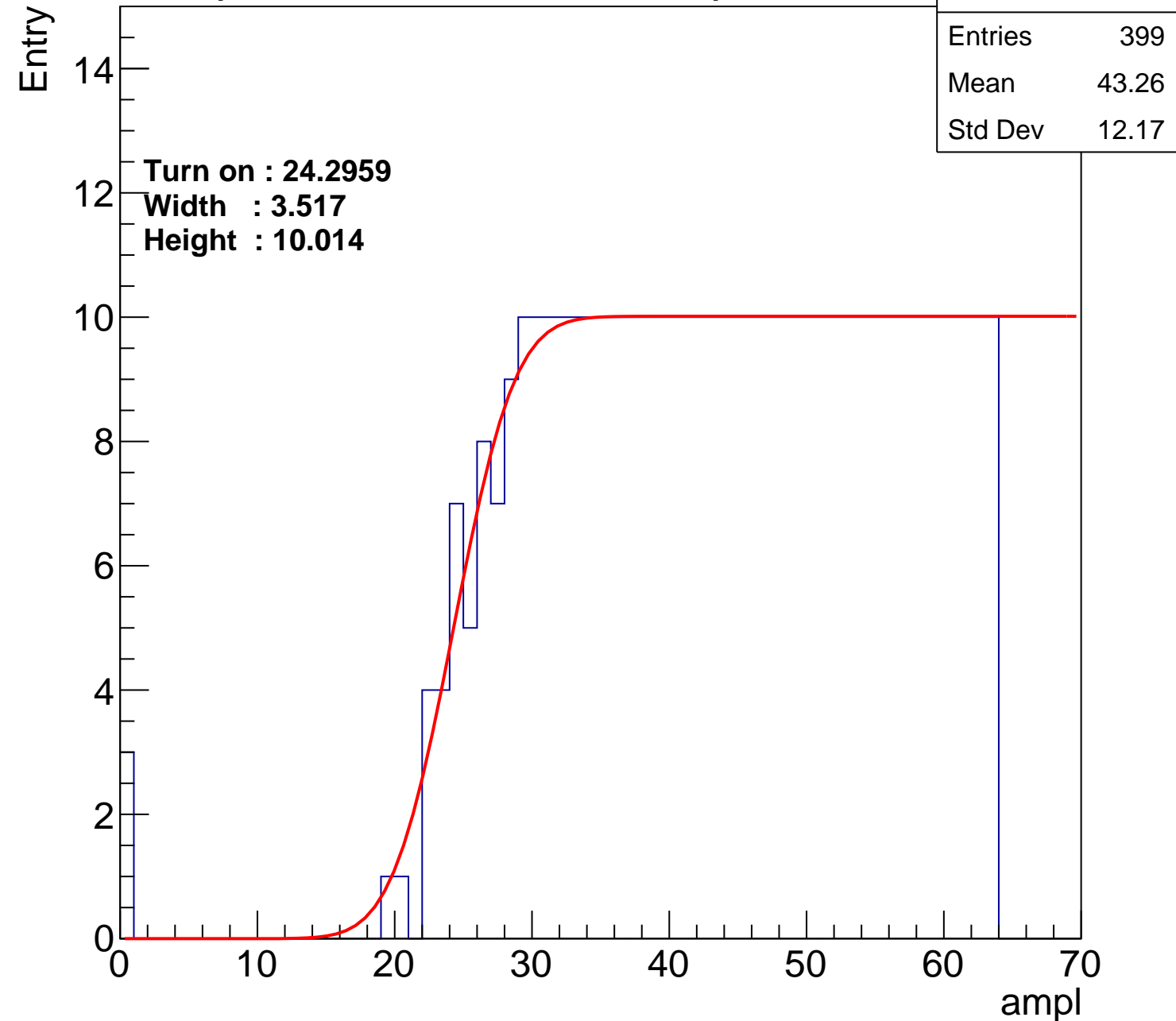
Width : 3.517

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	351
Mean	45.69
Std Dev	10.79

Turn on : 29.5685

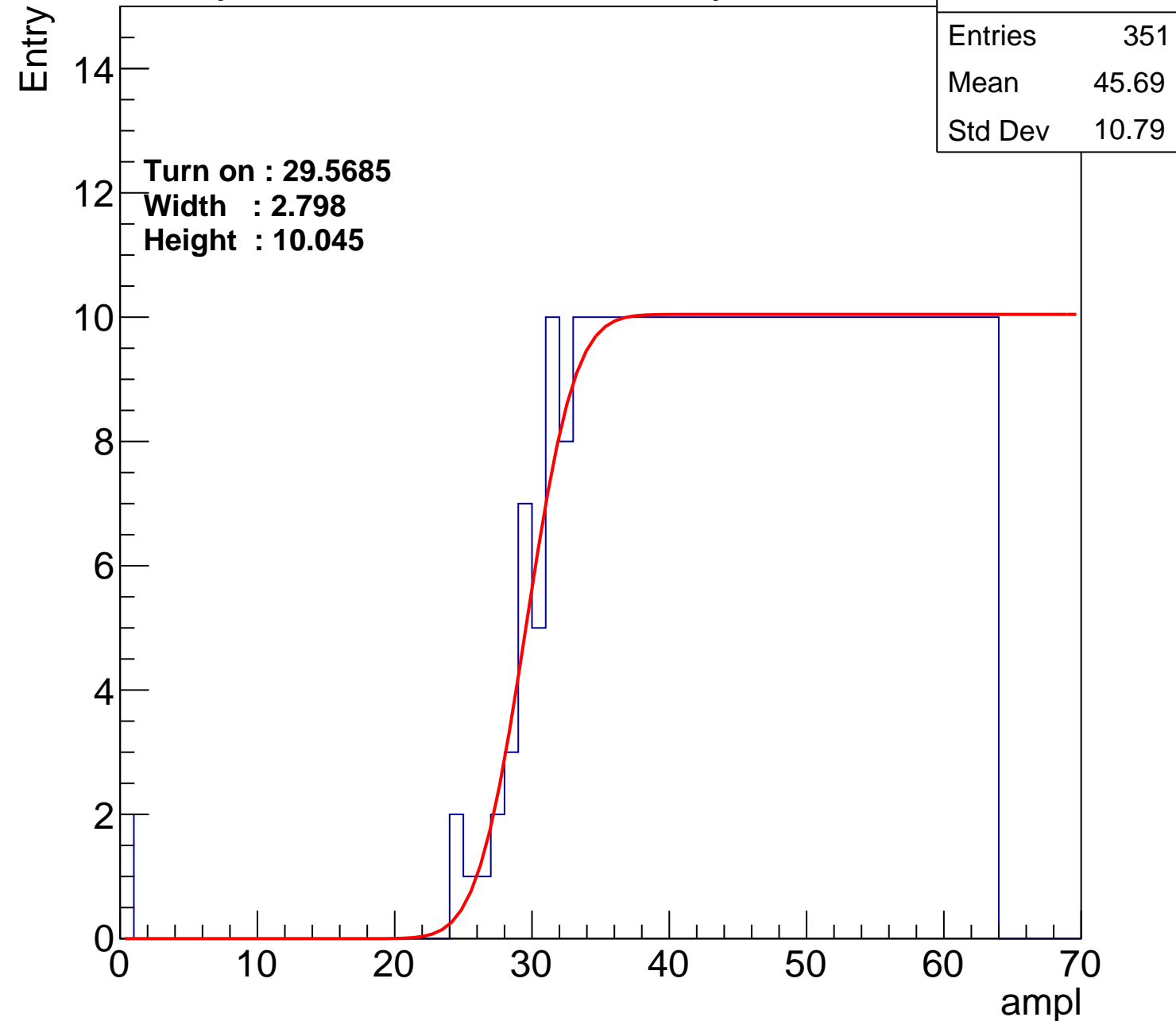
Width : 2.798

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.81
Std Dev	11.95

Turn on : 26.2222

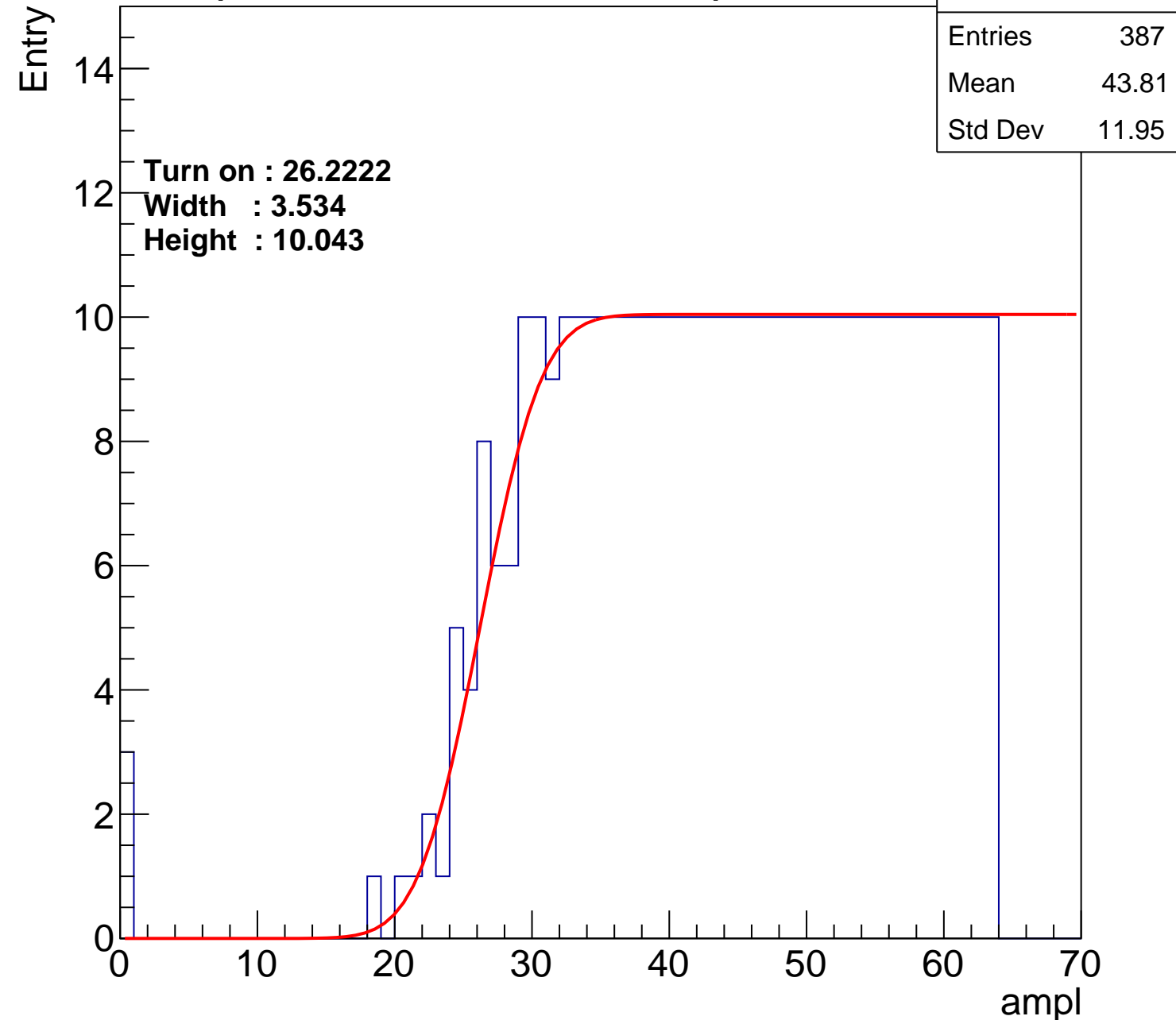
Width : 3.534

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 26.7808

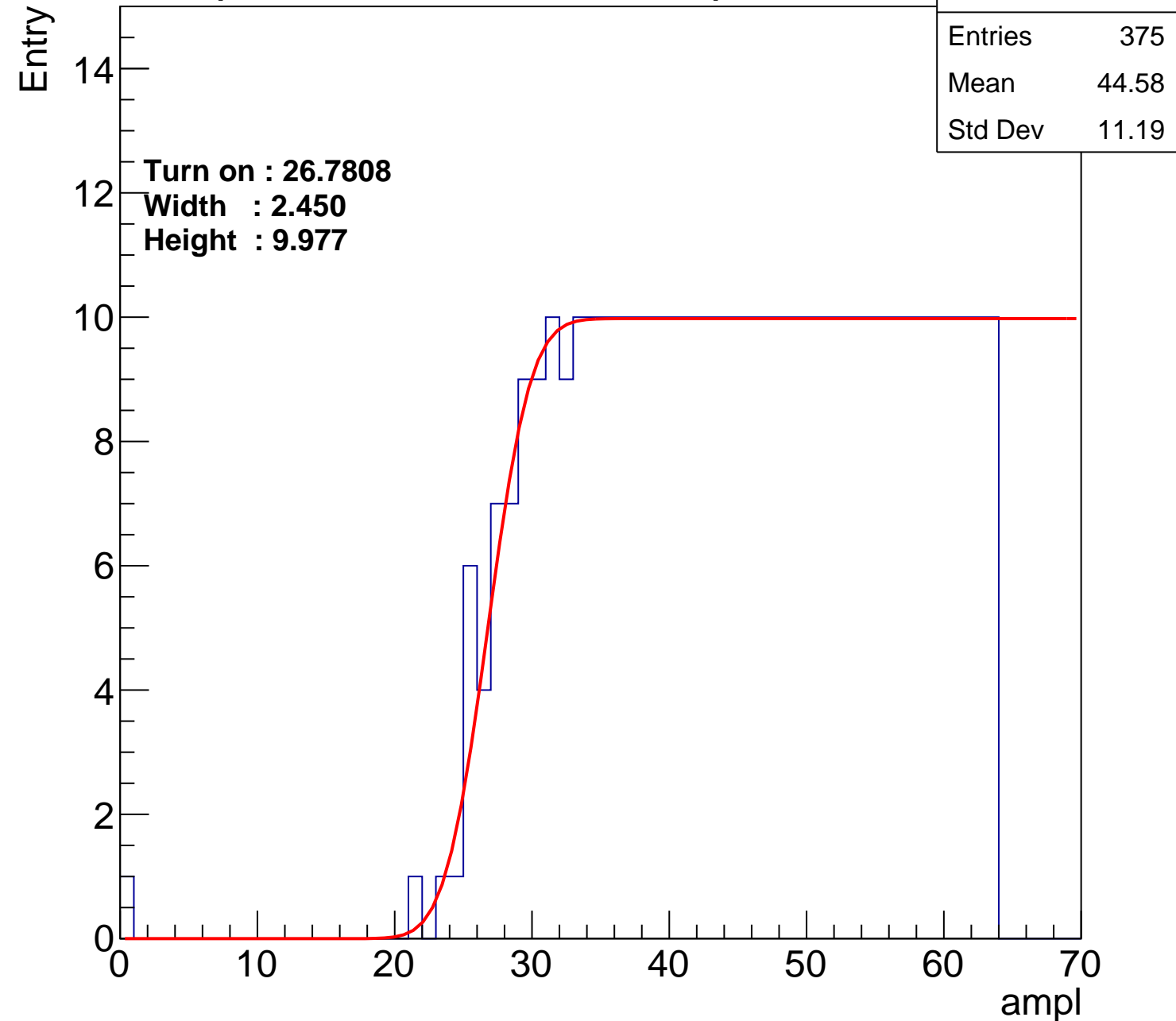
Width : 2.450

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch4

calib_packv5_042523_0143.root, FC#12, port B1

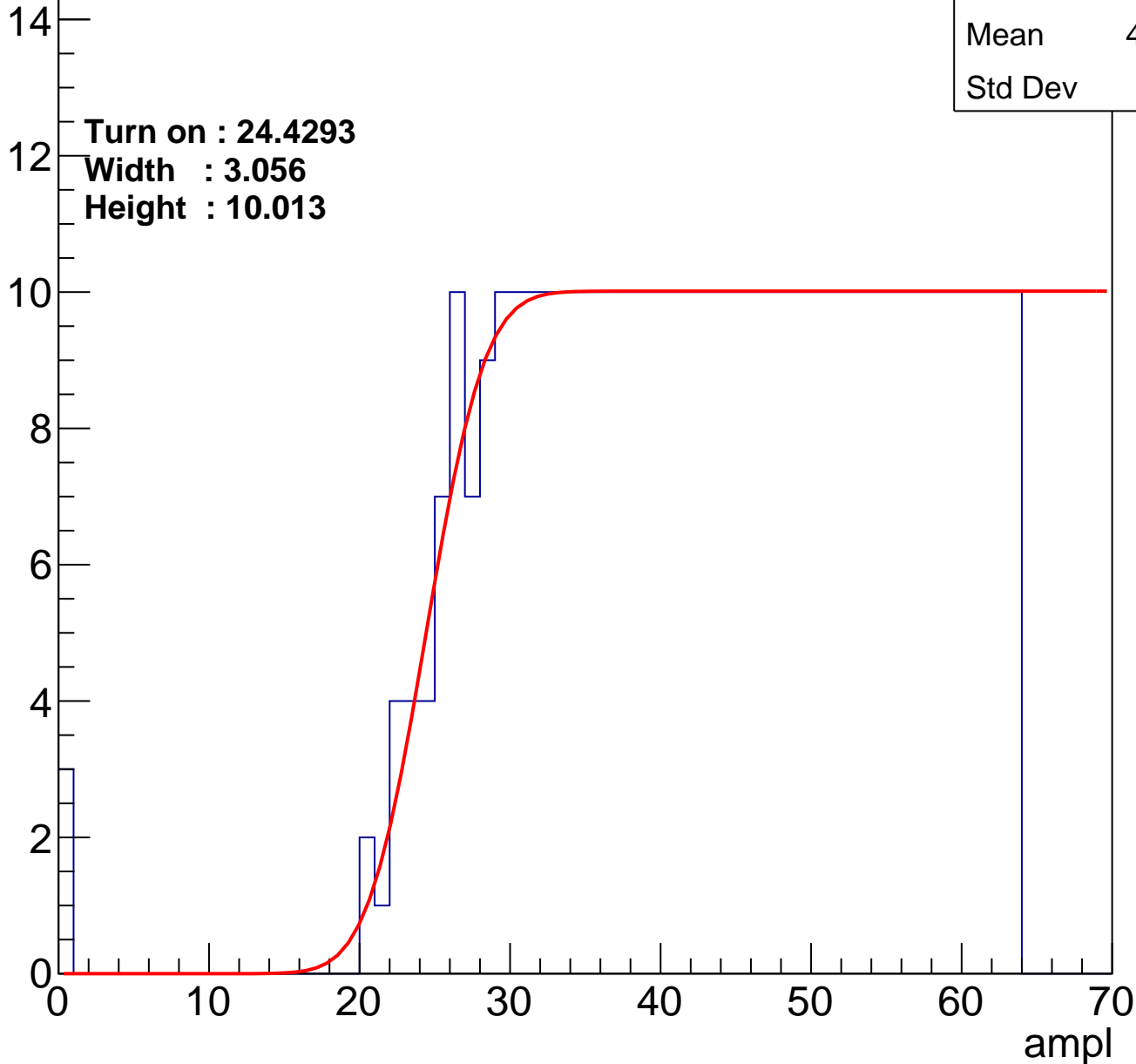
Entry

Entries	401
Mean	43.17
Std Dev	12.2

Turn on : 24.4293

Width : 3.056

Height : 10.013



B0L102S, U11-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.99
Std Dev	11.64

Turn on : 25.7003

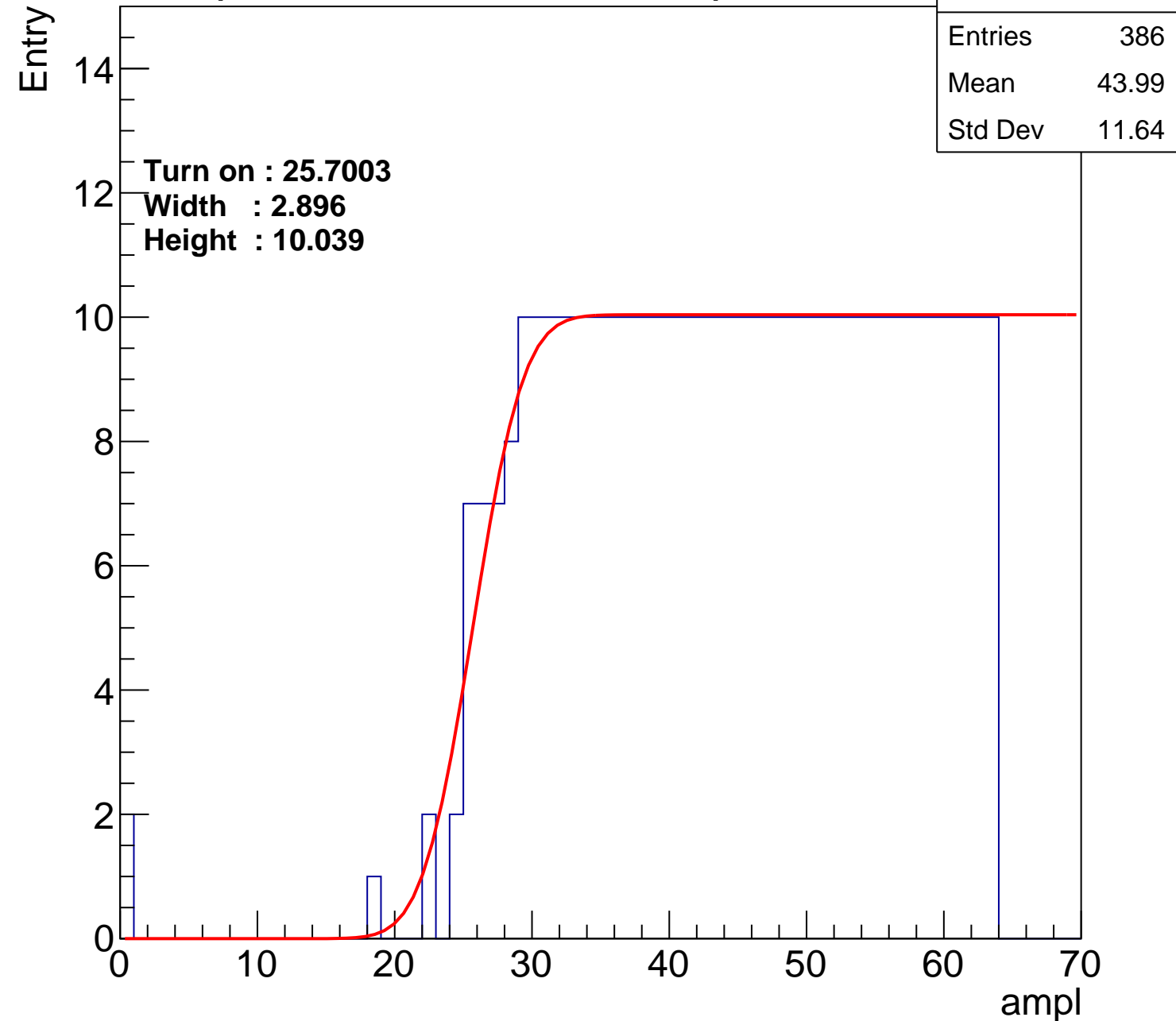
Width : 2.896

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch6

calib_packv5_042523_0143.root, FC#12, port B1

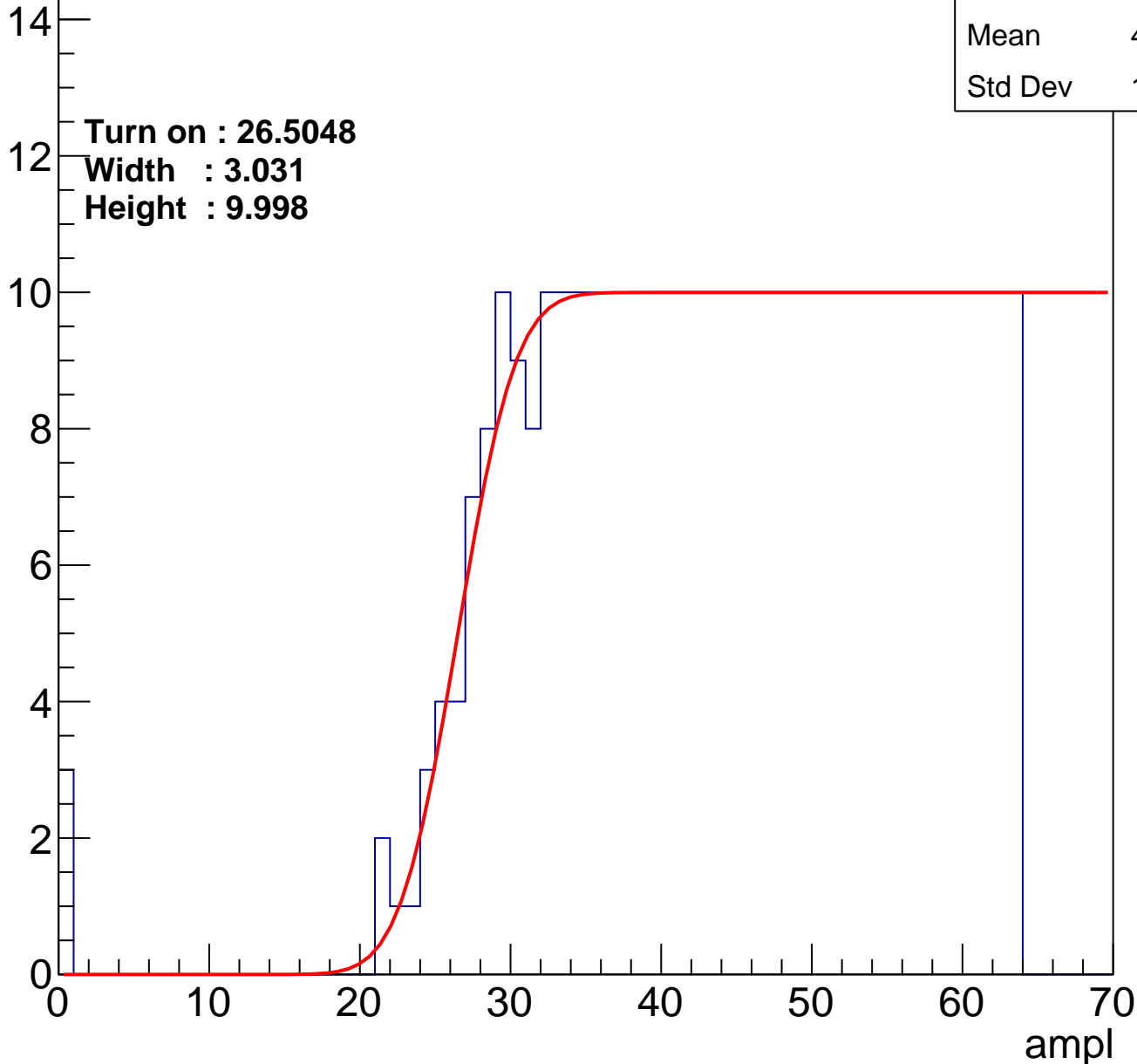
Entry

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.5048

Width : 3.031

Height : 9.998



B0L102S, U11-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.52
Std Dev	11.42

Turn on : 27.4191

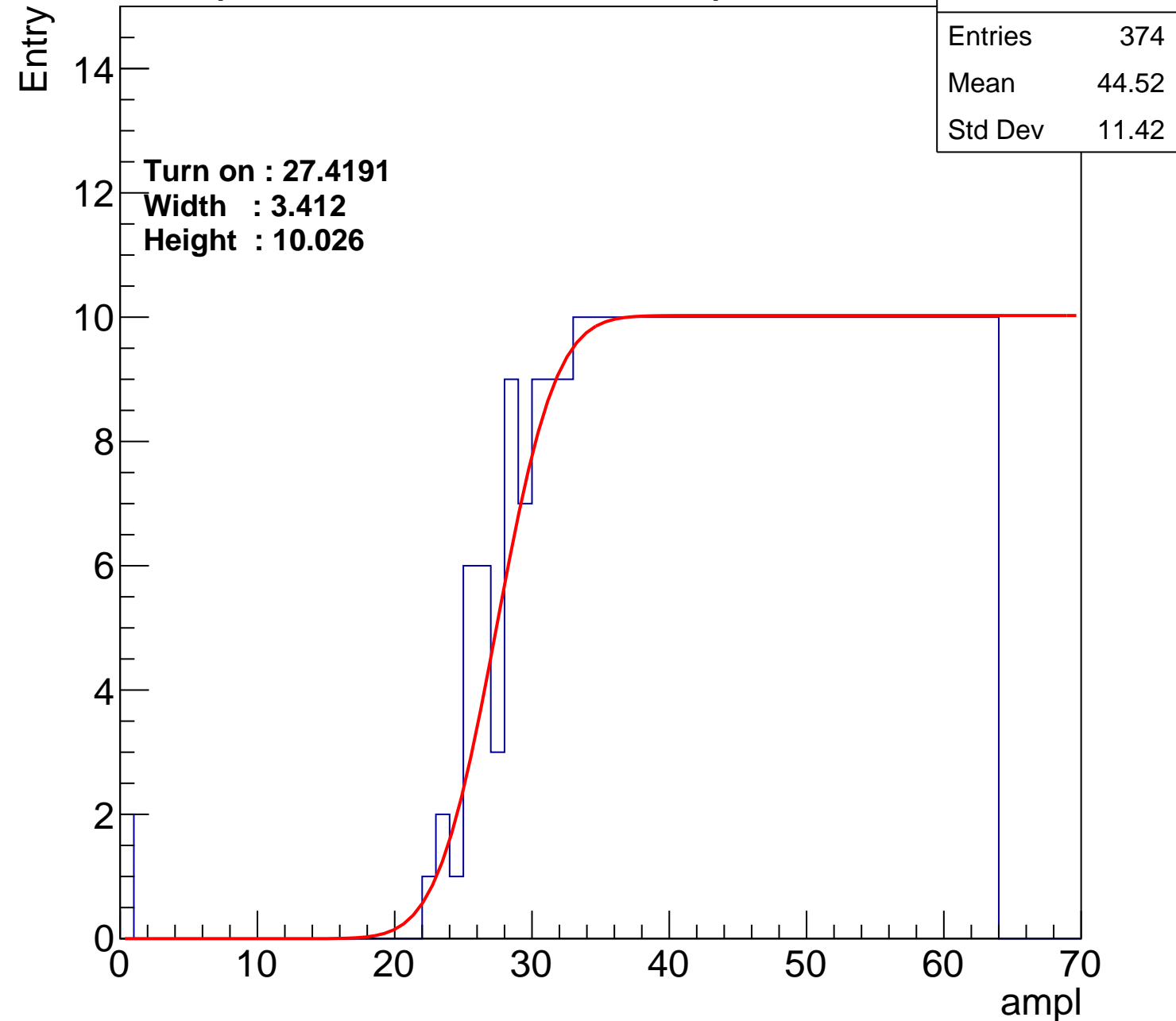
Width : 3.412

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.52
Std Dev	11.24

Turn on : 26.8489

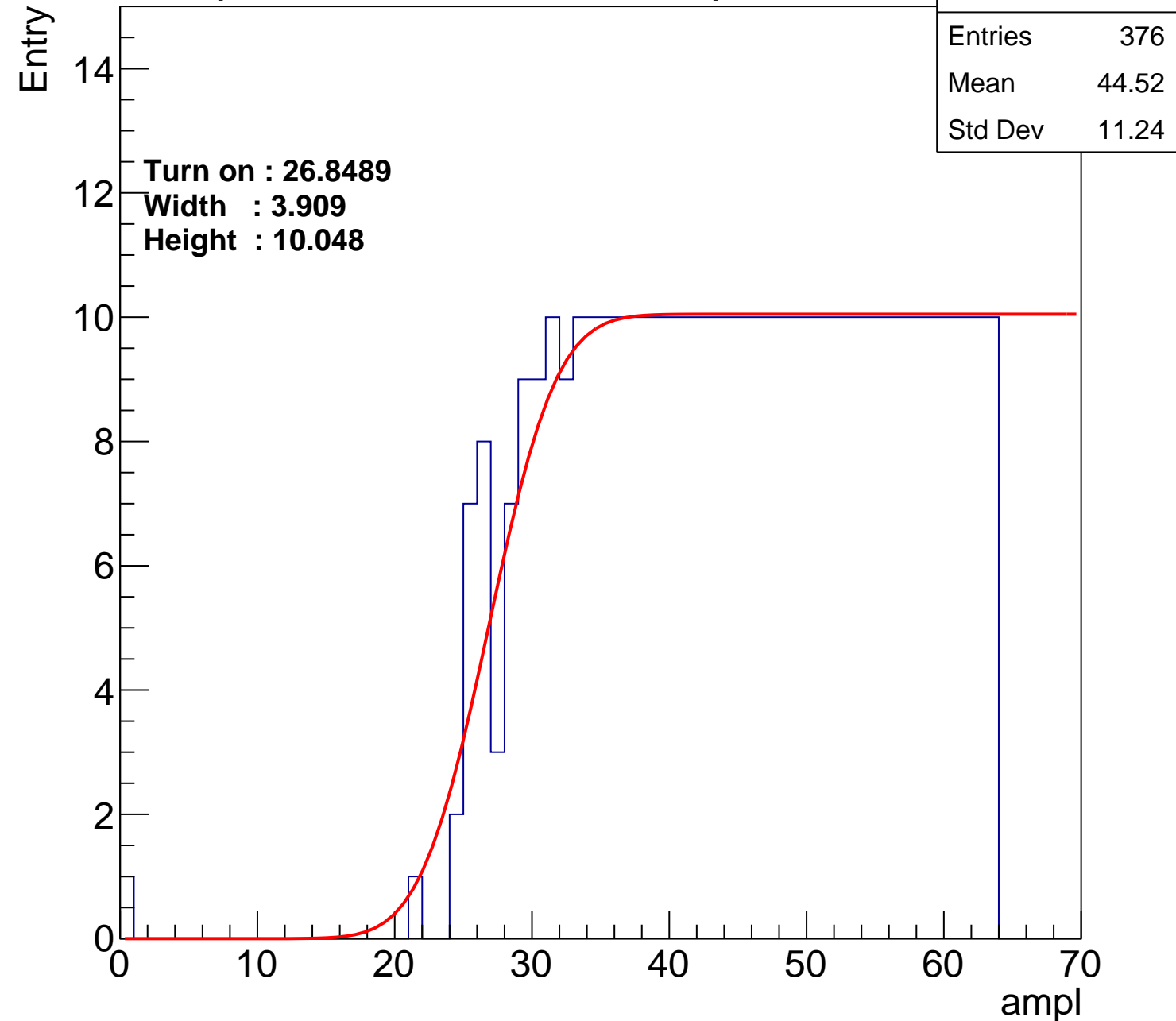
Width : 3.909

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	350
Mean	45.77
Std Dev	10.58

Turn on : 29.1992

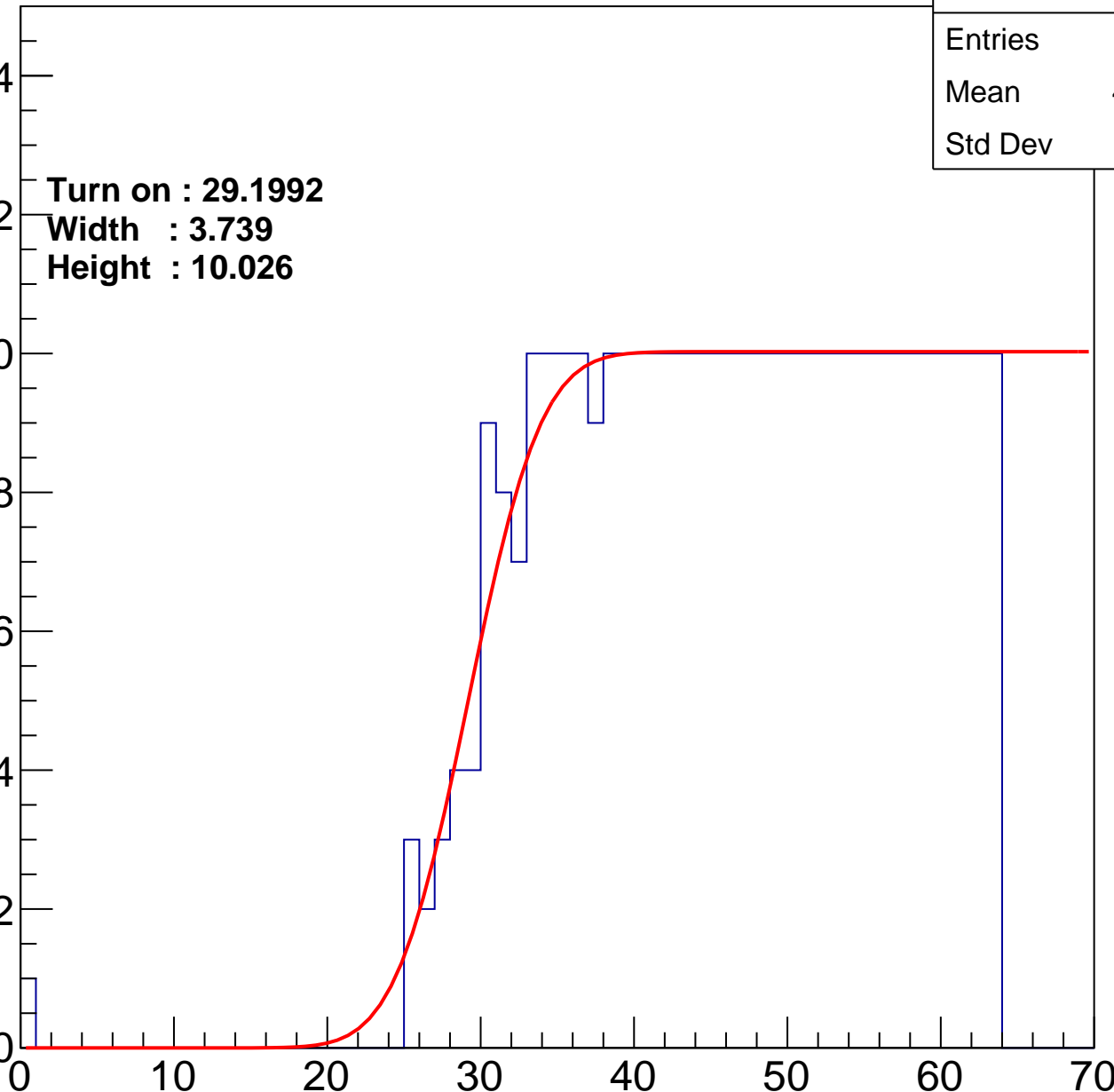
Width : 3.739

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.38
Std Dev	11.49

Turn on : 26.6422

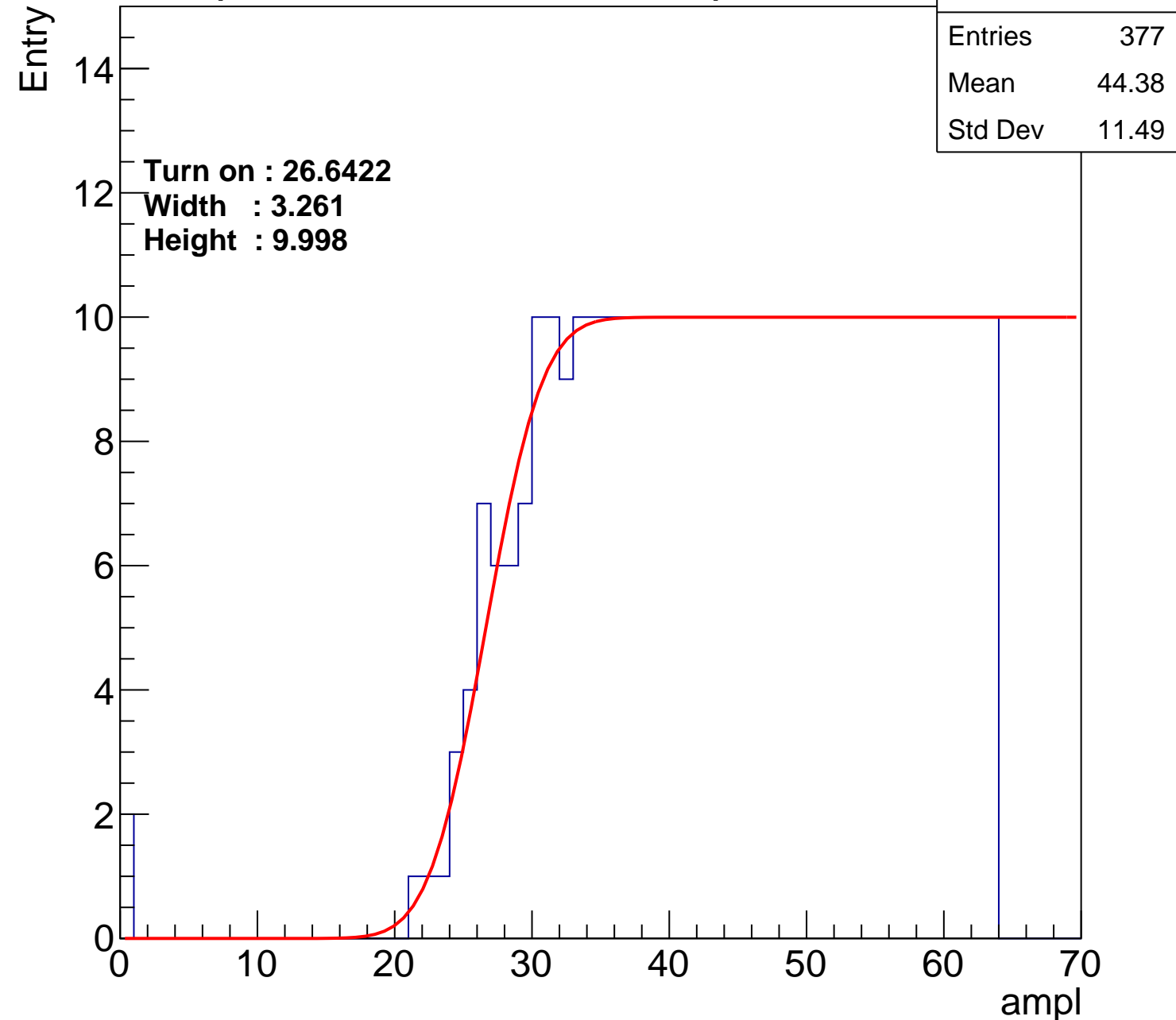
Width : 3.261

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 25.4188

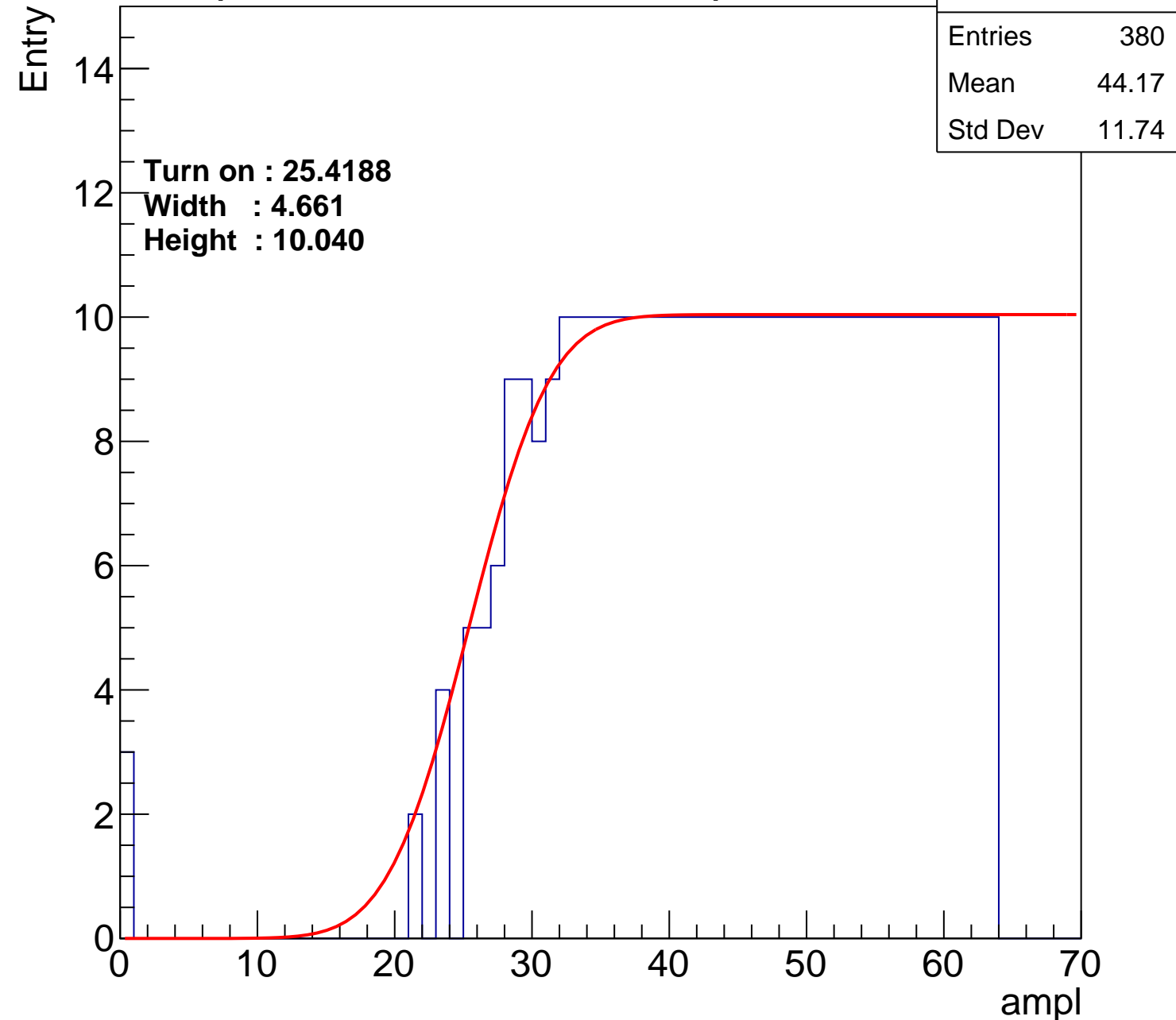
Width : 4.661

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch12

calib_packv5_042523_0143.root, FC#12, port B1

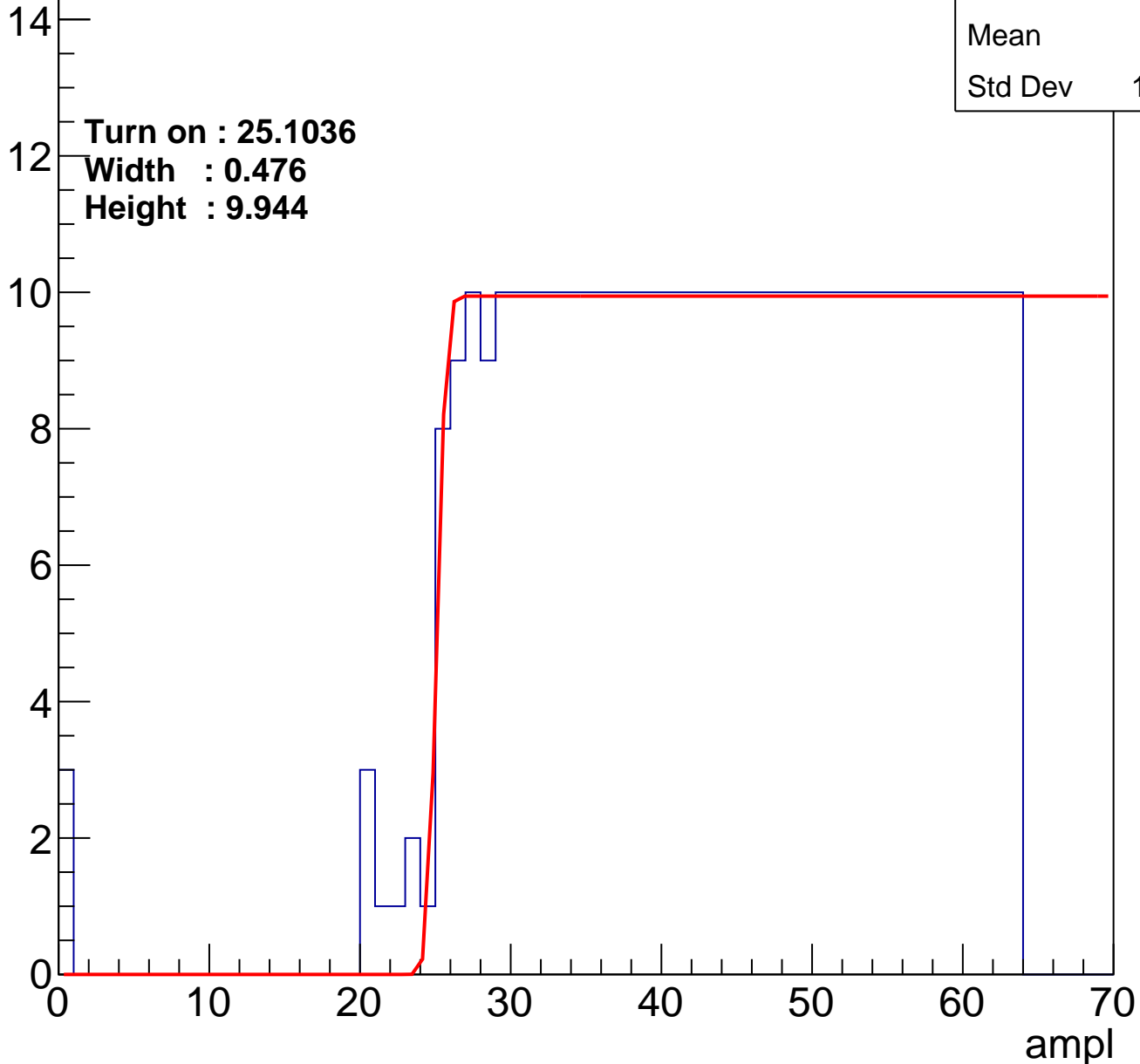
Entries	397
Mean	43.4
Std Dev	12.06

Turn on : 25.1036

Width : 0.476

Height : 9.944

Entry



B0L102S, U11-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.2
Std Dev	11.23

Turn on : 29.9486

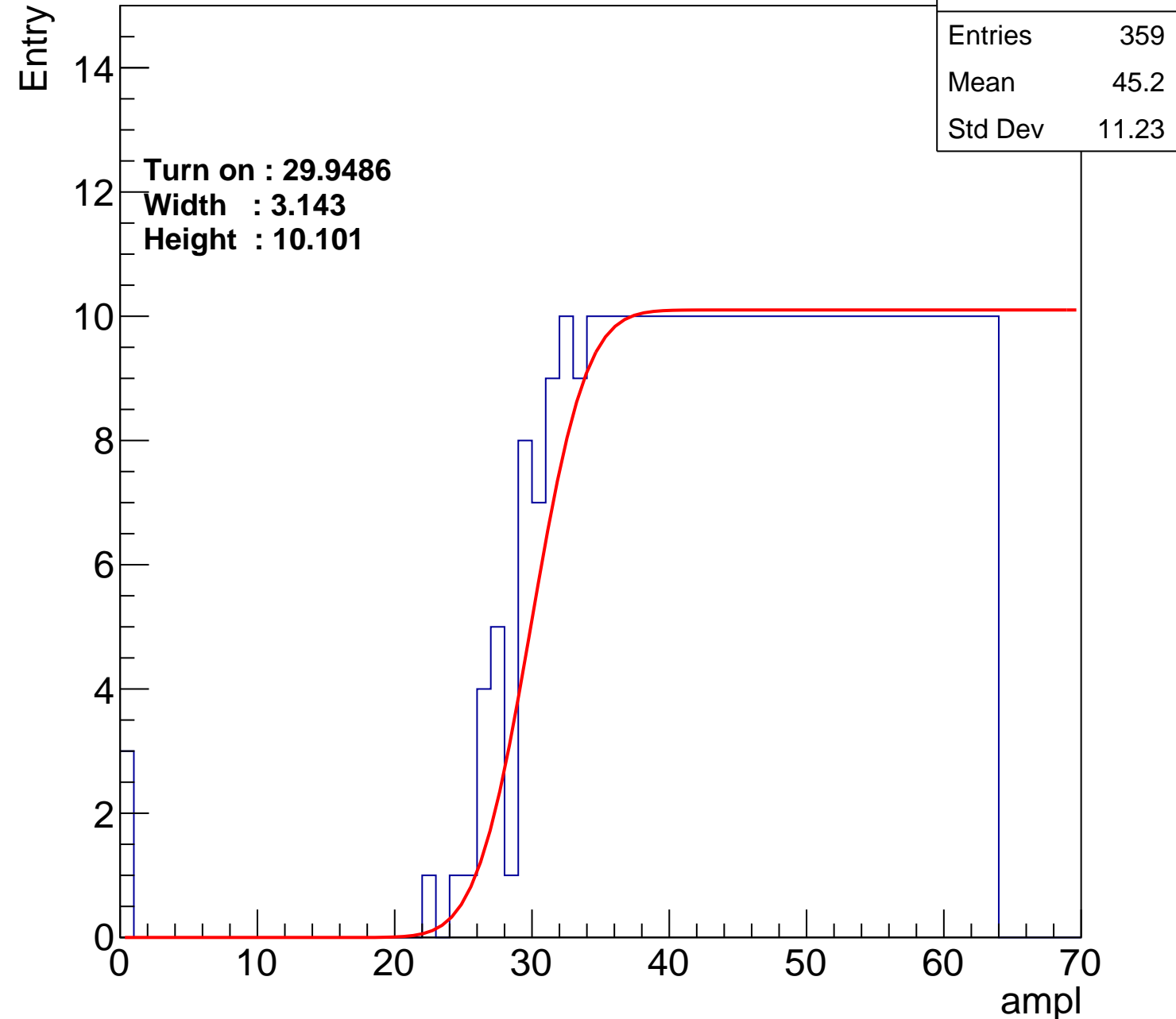
Width : 3.143

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.09
Std Dev	11.88

Turn on : 26.5613

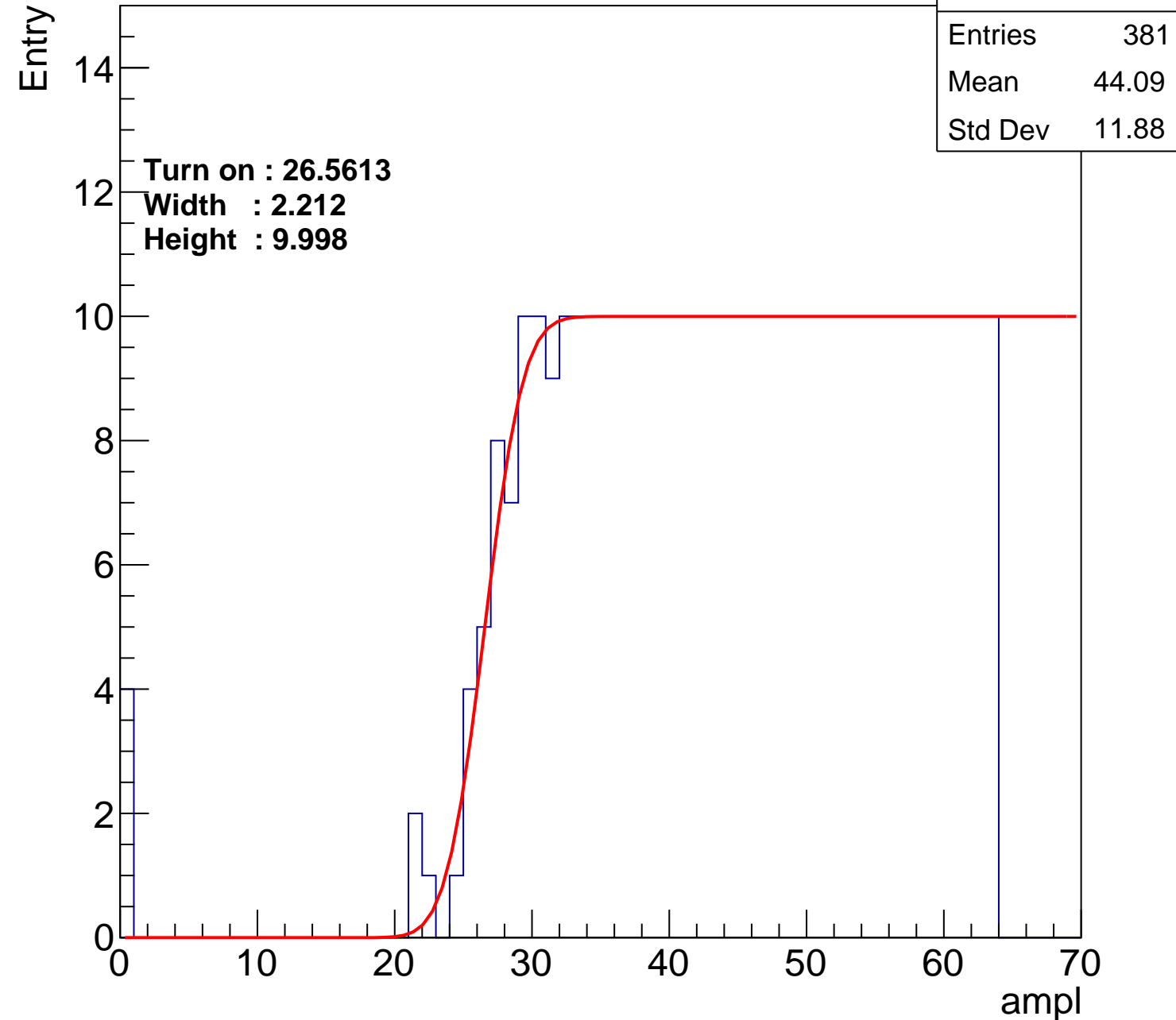
Width : 2.212

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 26.3275

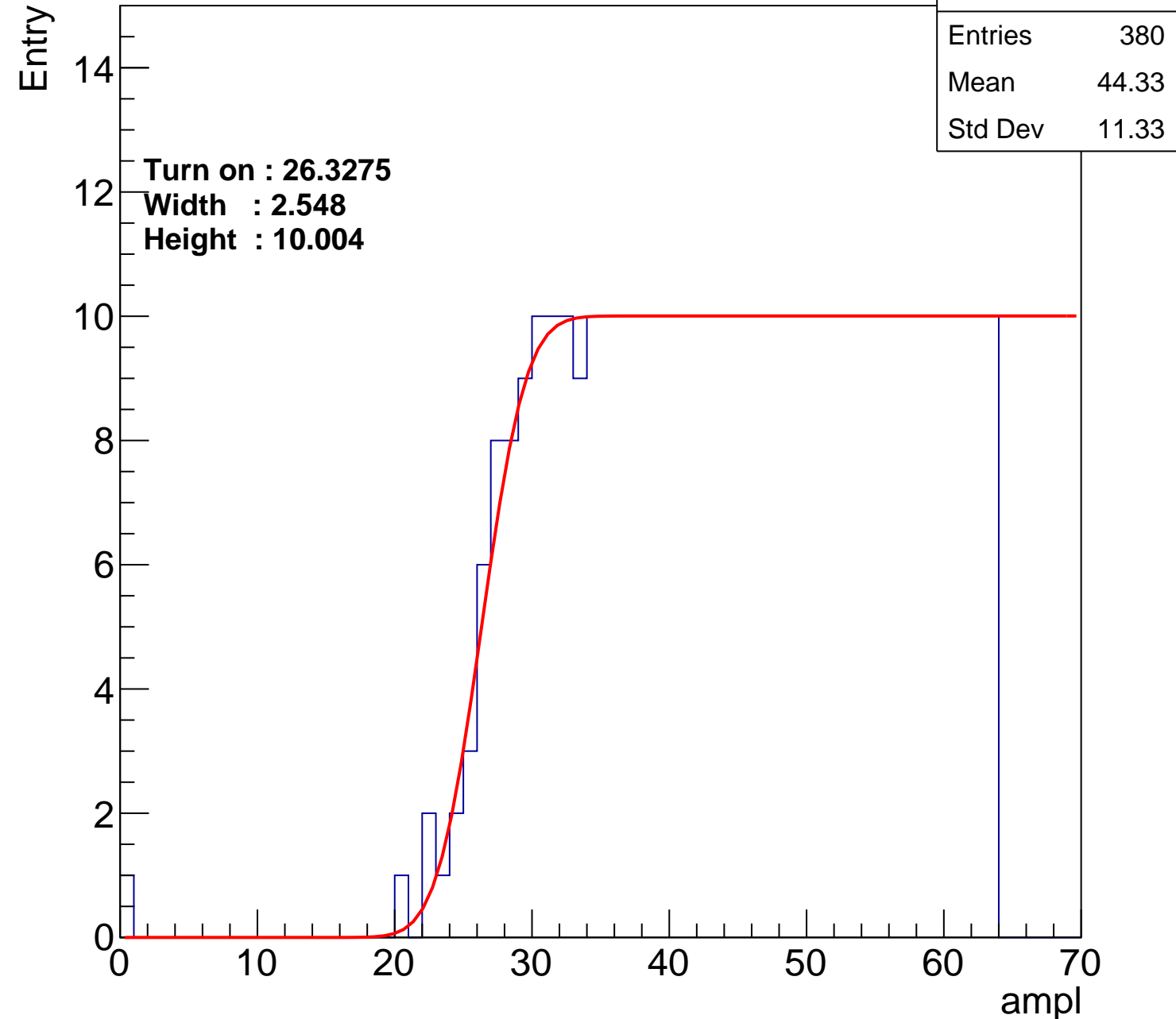
Width : 2.548

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 26.3835

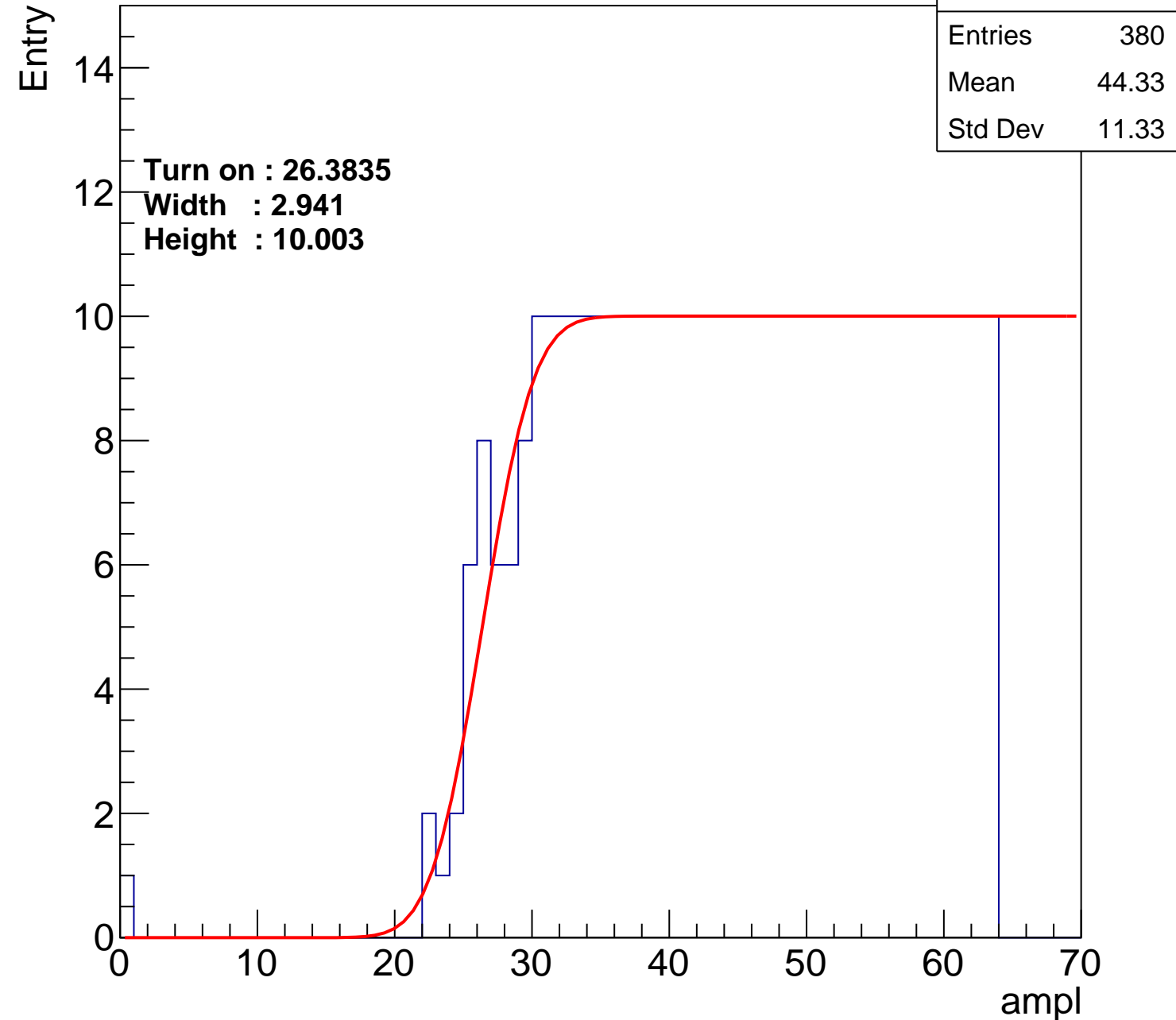
Width : 2.941

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.49
Std Dev	11.23

Turn on : 26.0737

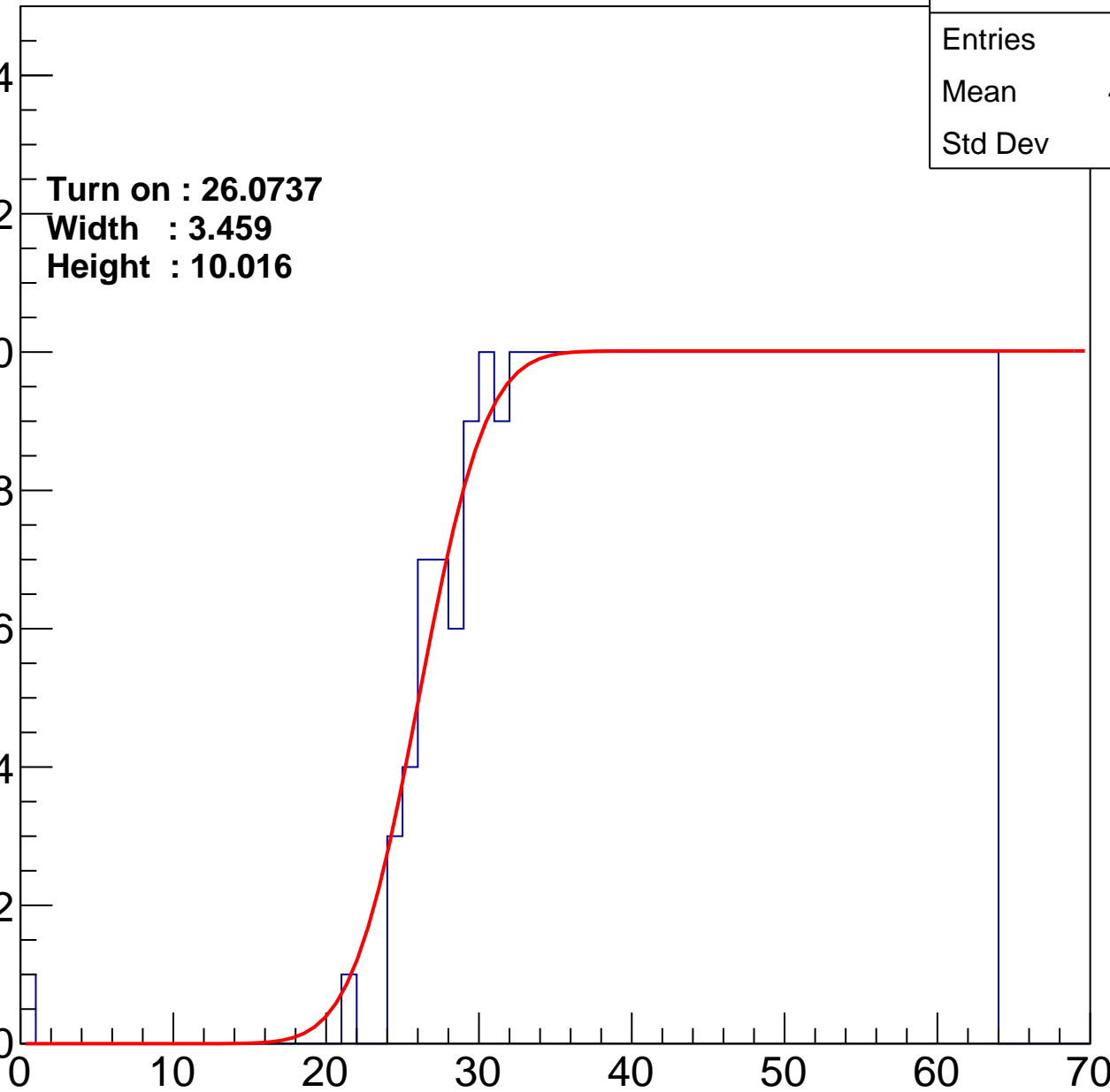
Width : 3.459

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.27
Std Dev	12.05

Turn on : 24.6525

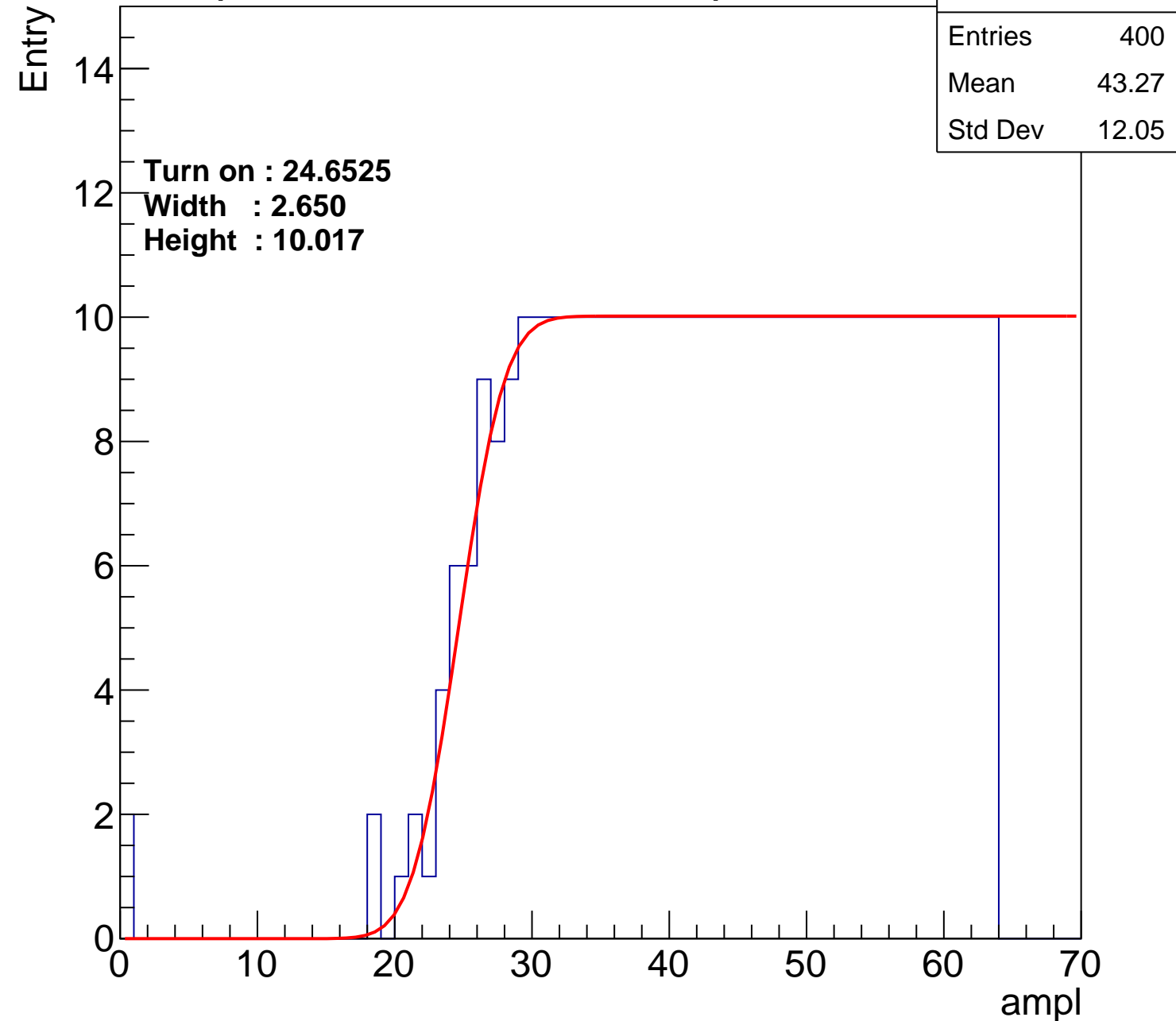
Width : 2.650

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 26.4601
Width : 3.198
Height : 10.029



B0L102S, U11-ch20

calib_packv5_042523_0143.root, FC#12, port B1

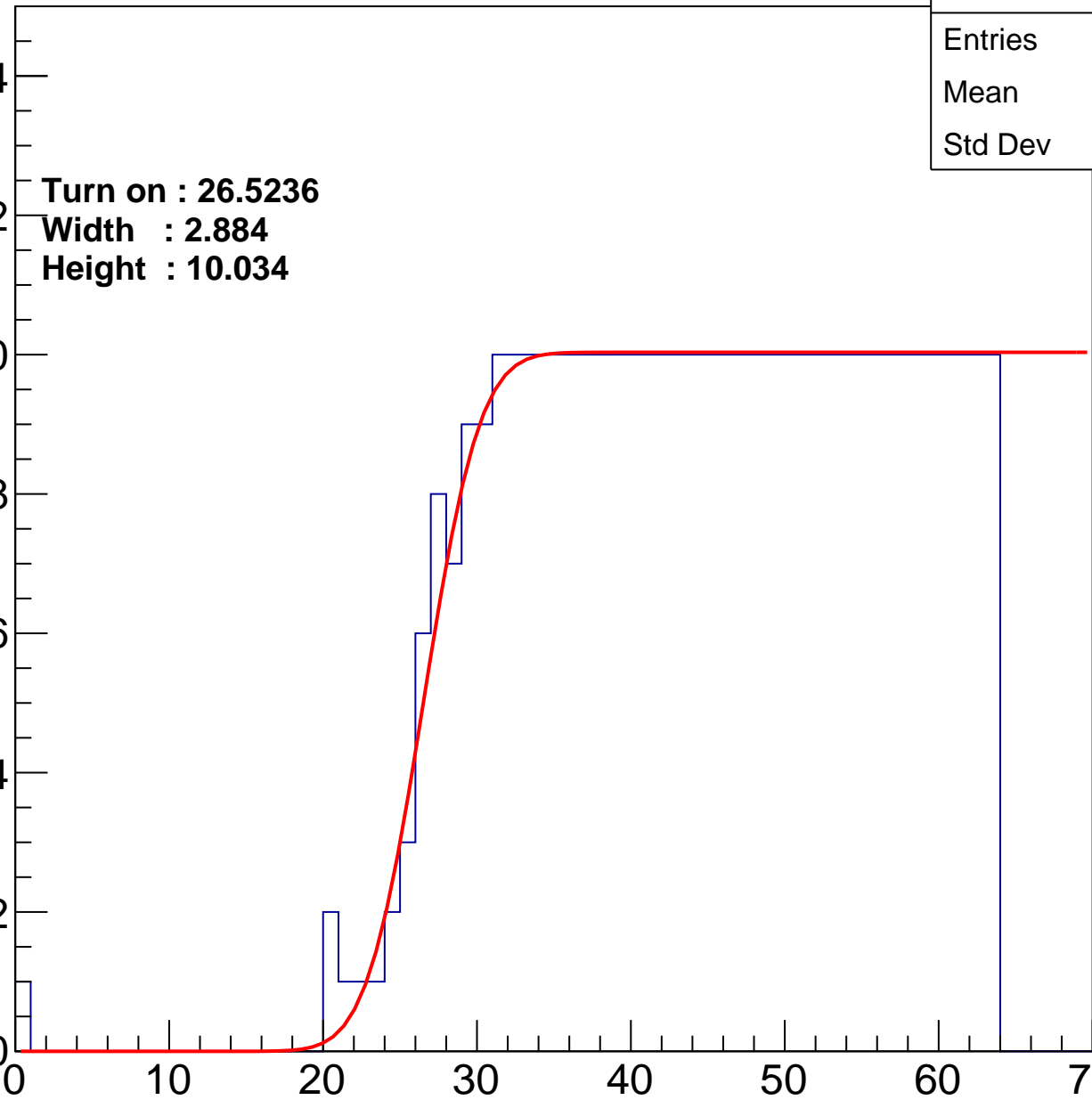
Entry

14
12
10
8
6
4
2
0

Turn on : 26.5236
Width : 2.884
Height : 10.034

Entries	380
Mean	44.31
Std Dev	11.37

ampl



B0L102S, U11-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.05
Std Dev	11.35

Turn on : 28.2563

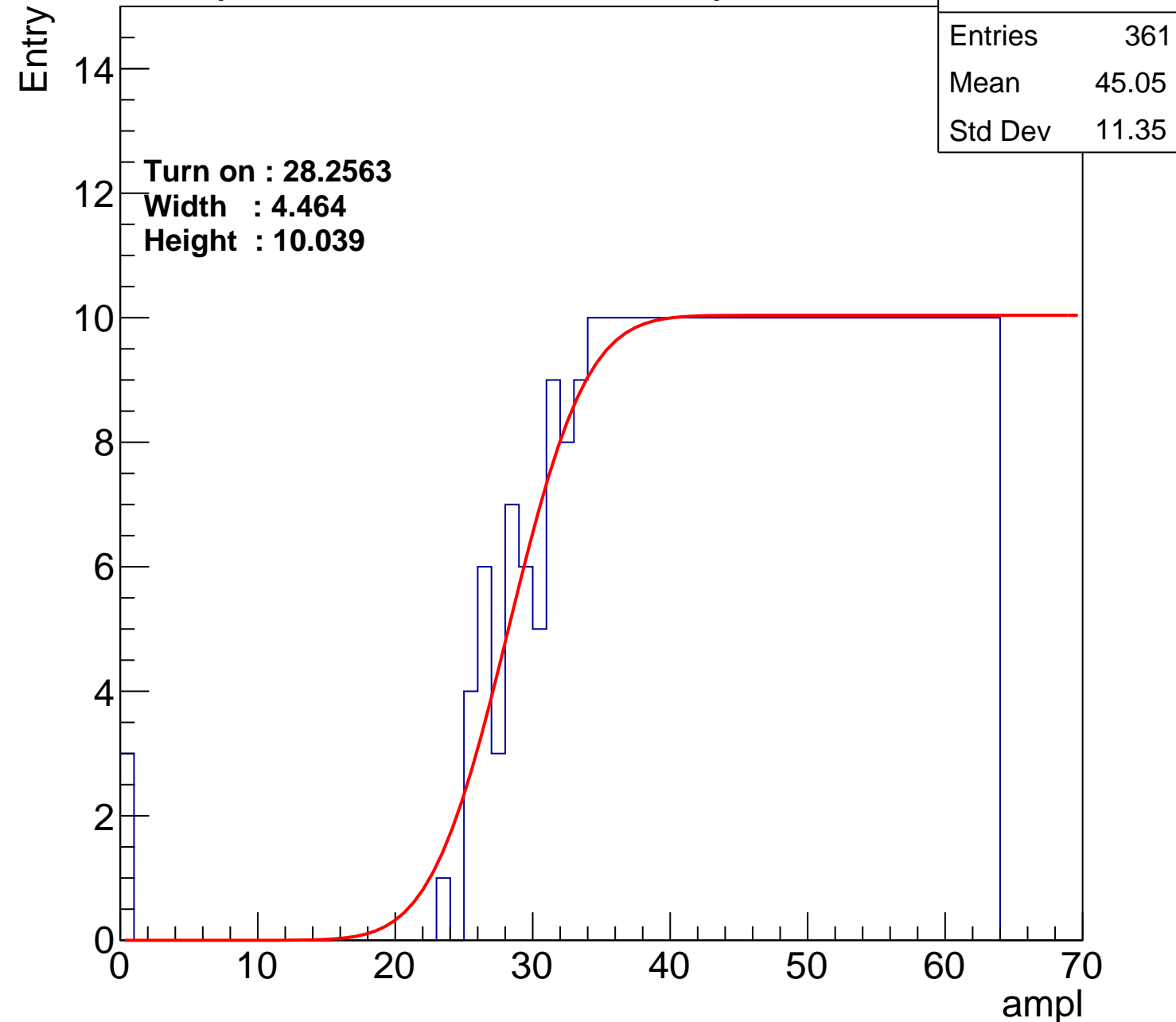
Width : 4.464

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.56
Std Dev	11.5

Turn on : 27.4306

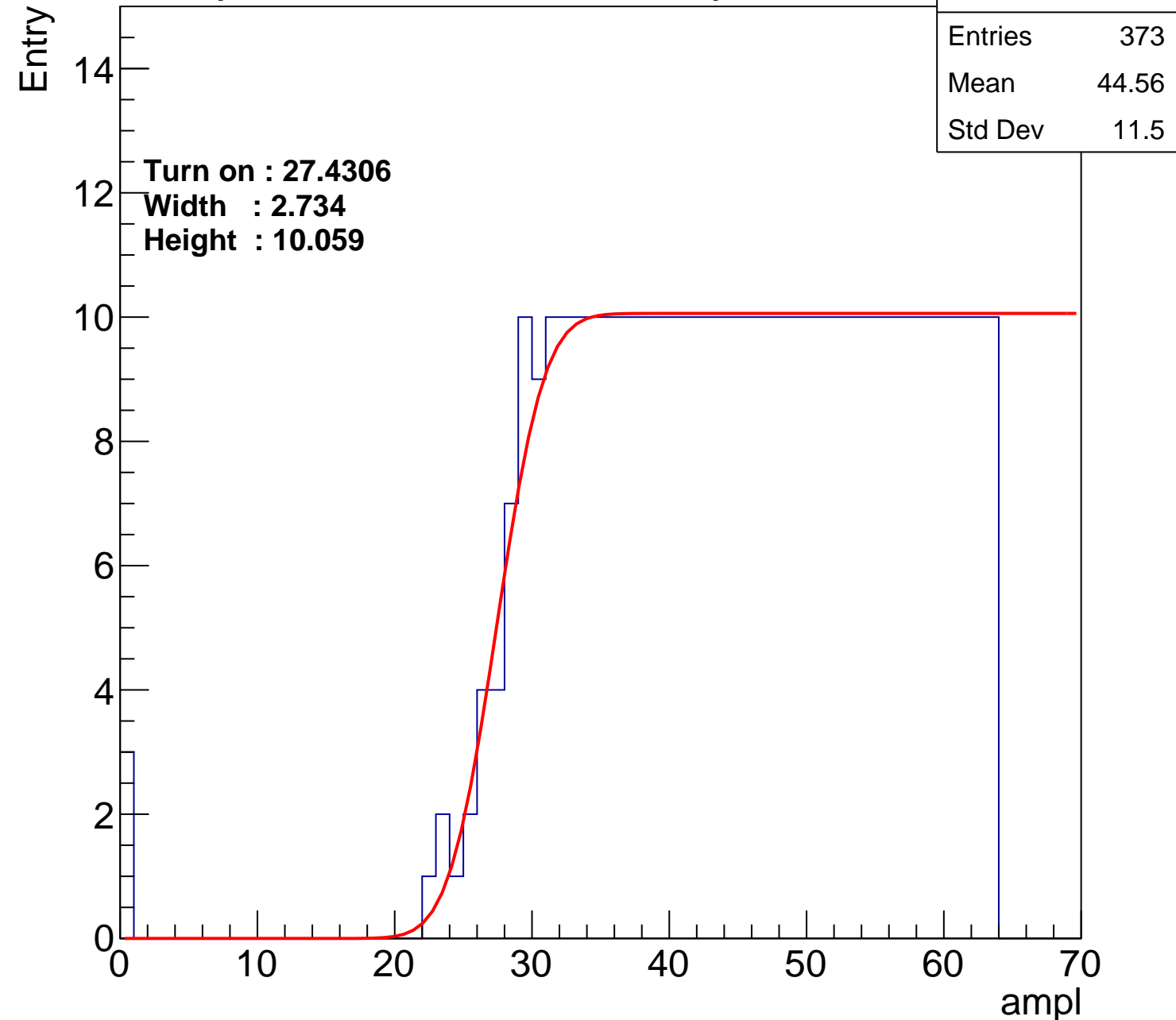
Width : 2.734

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch23

calib_packv5_042523_0143.root, FC#12, port B1

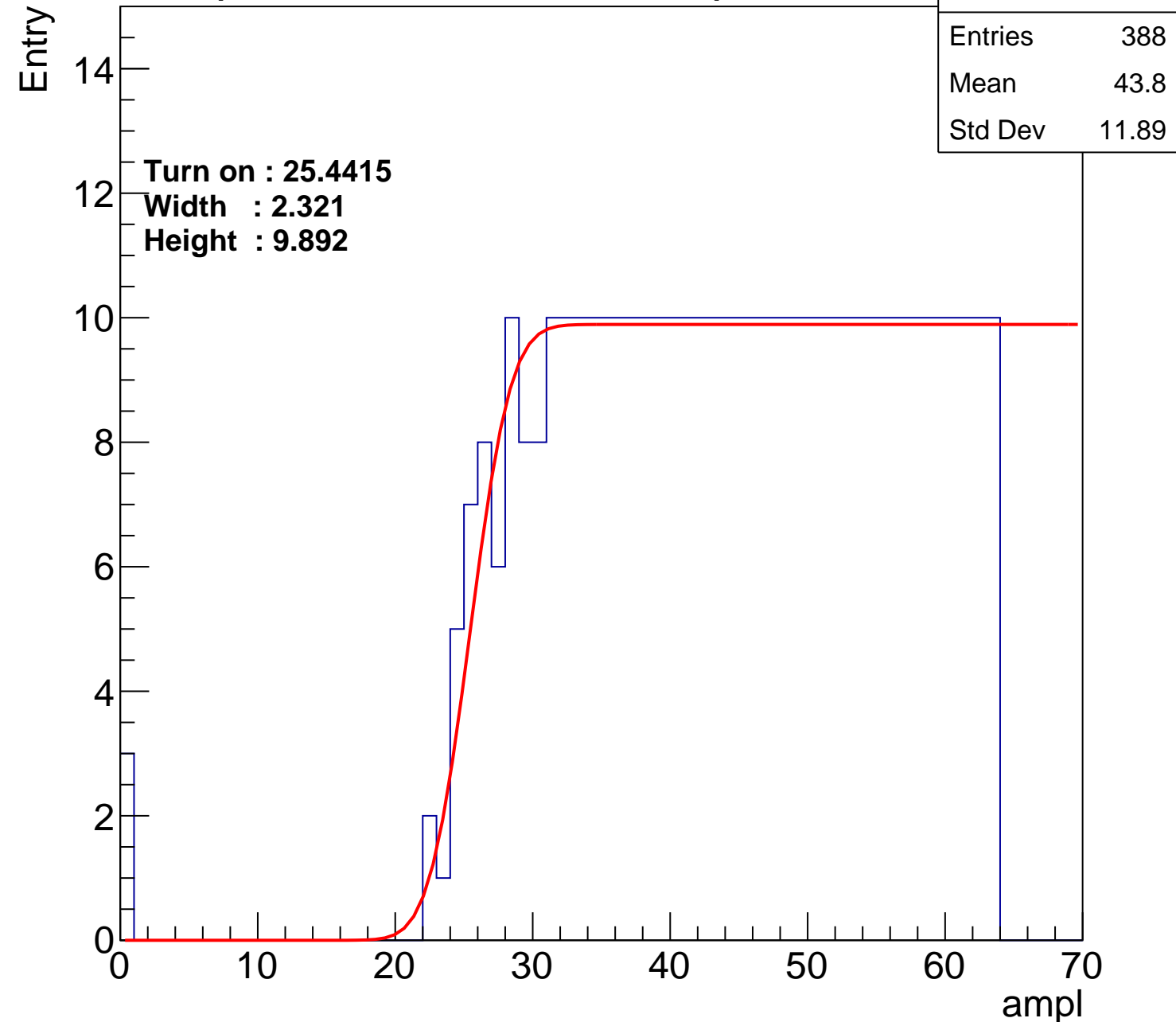
Entry

14
12
10
8
6
4
2
0

Turn on : 25.4415
Width : 2.321
Height : 9.892

Entries	388
Mean	43.8
Std Dev	11.89

ampl



B0L102S, U11-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.4933

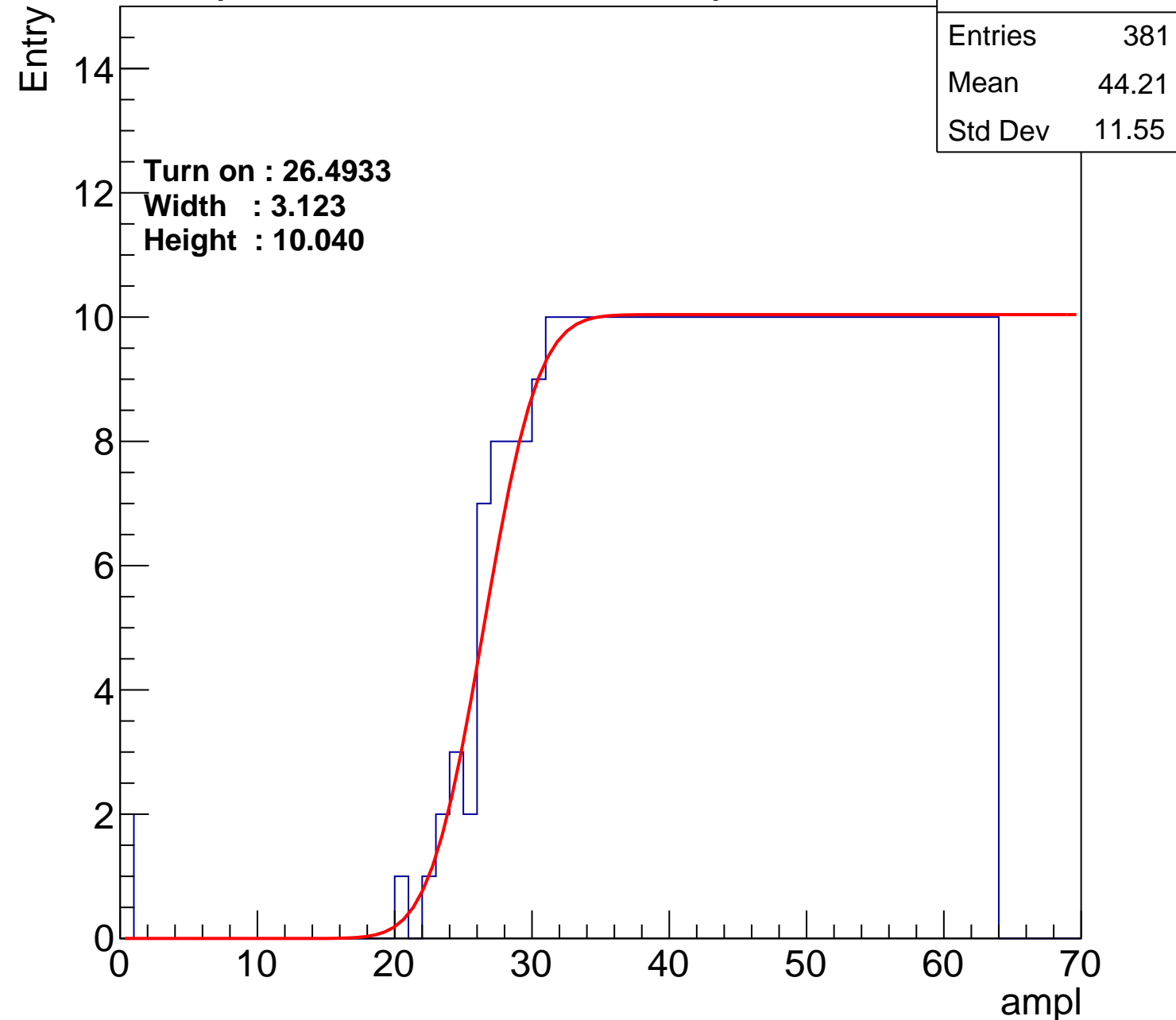
Width : 3.123

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch25

calib_packv5_042523_0143.root, FC#12, port B1

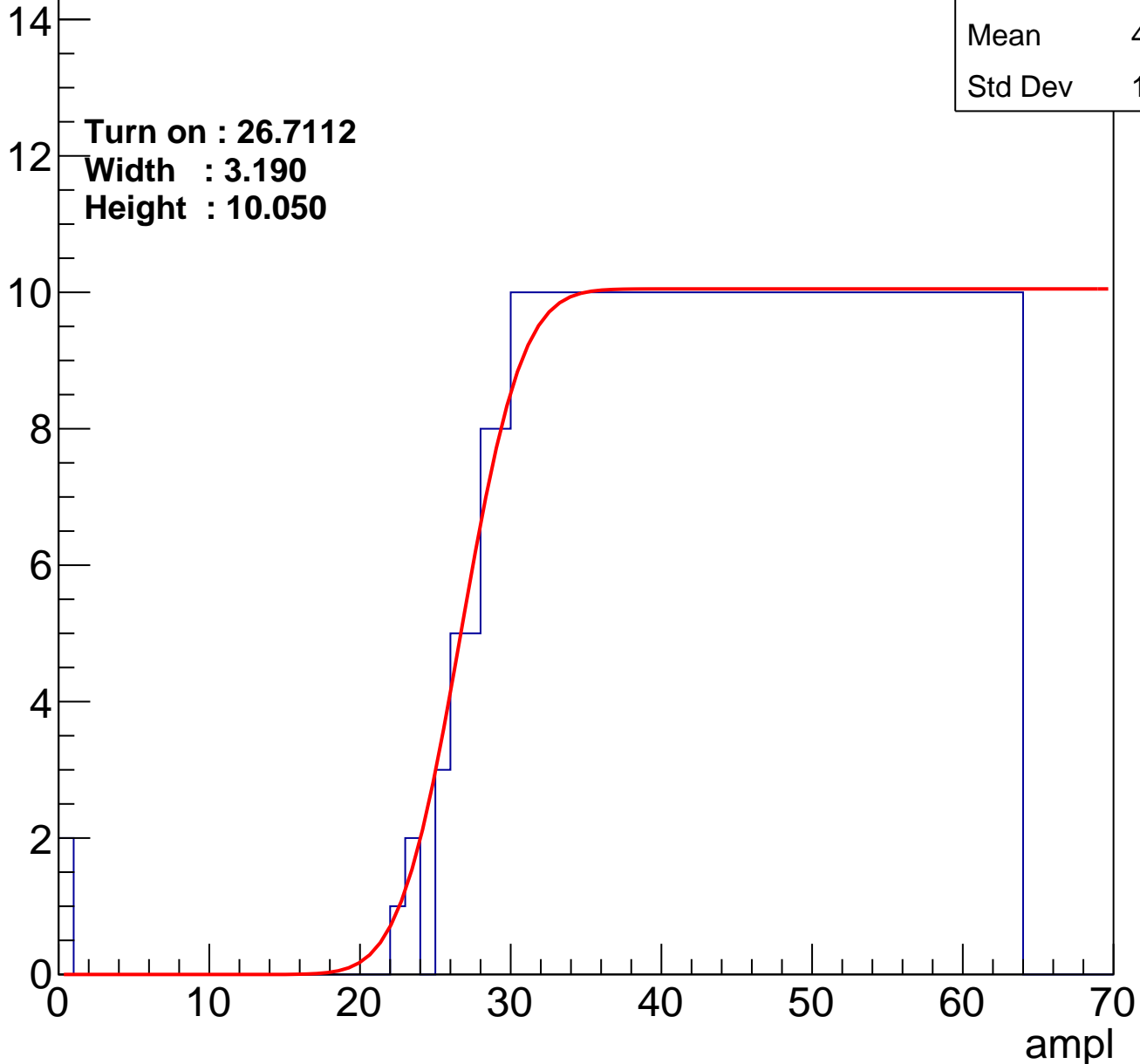
Entries	374
Mean	44.58
Std Dev	11.32

Turn on : 26.7112

Width : 3.190

Height : 10.050

Entry



B0L102S, U11-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	362
Mean	45.19
Std Dev	10.9

Turn on : 27.7871

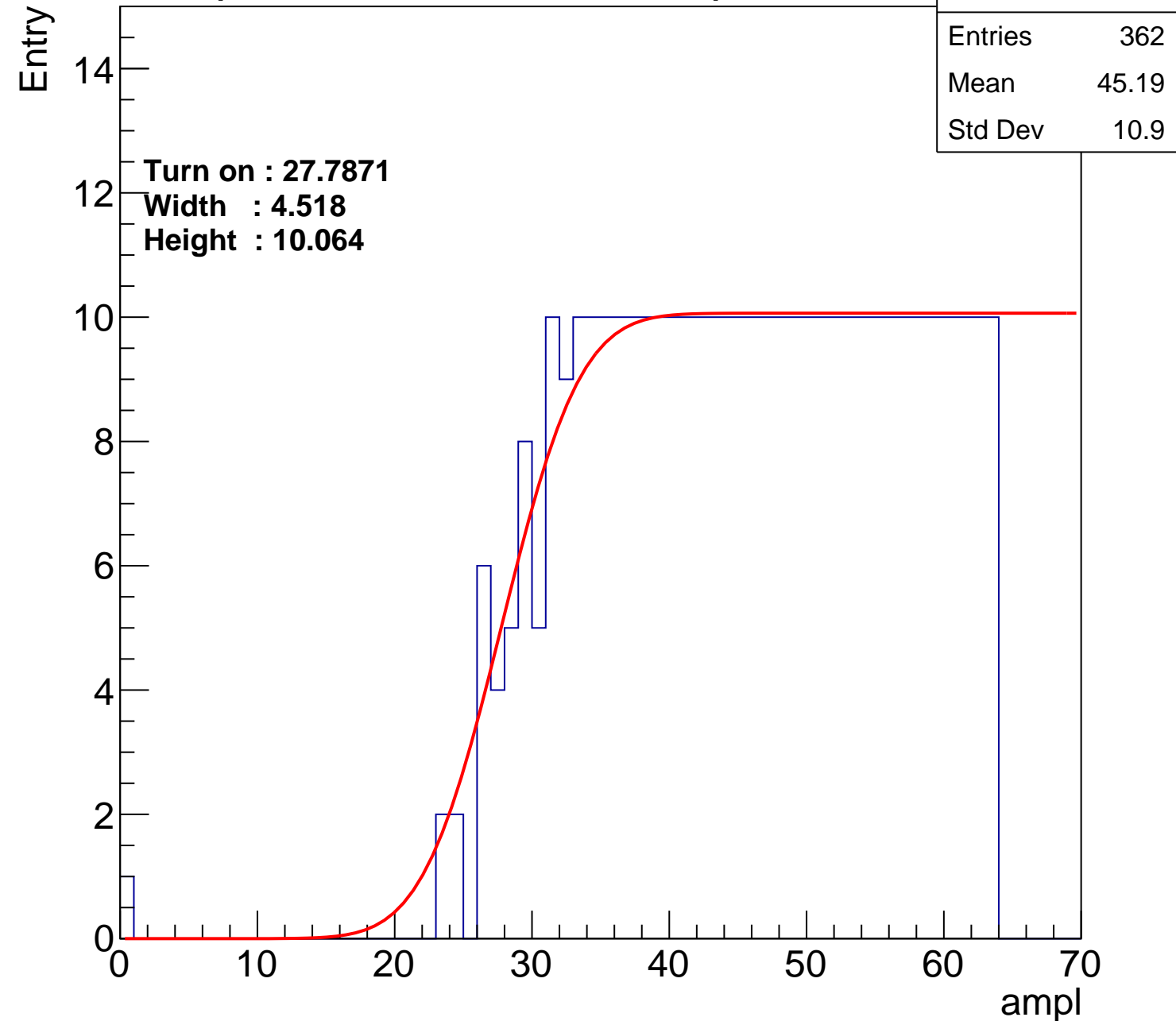
Width : 4.518

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch27

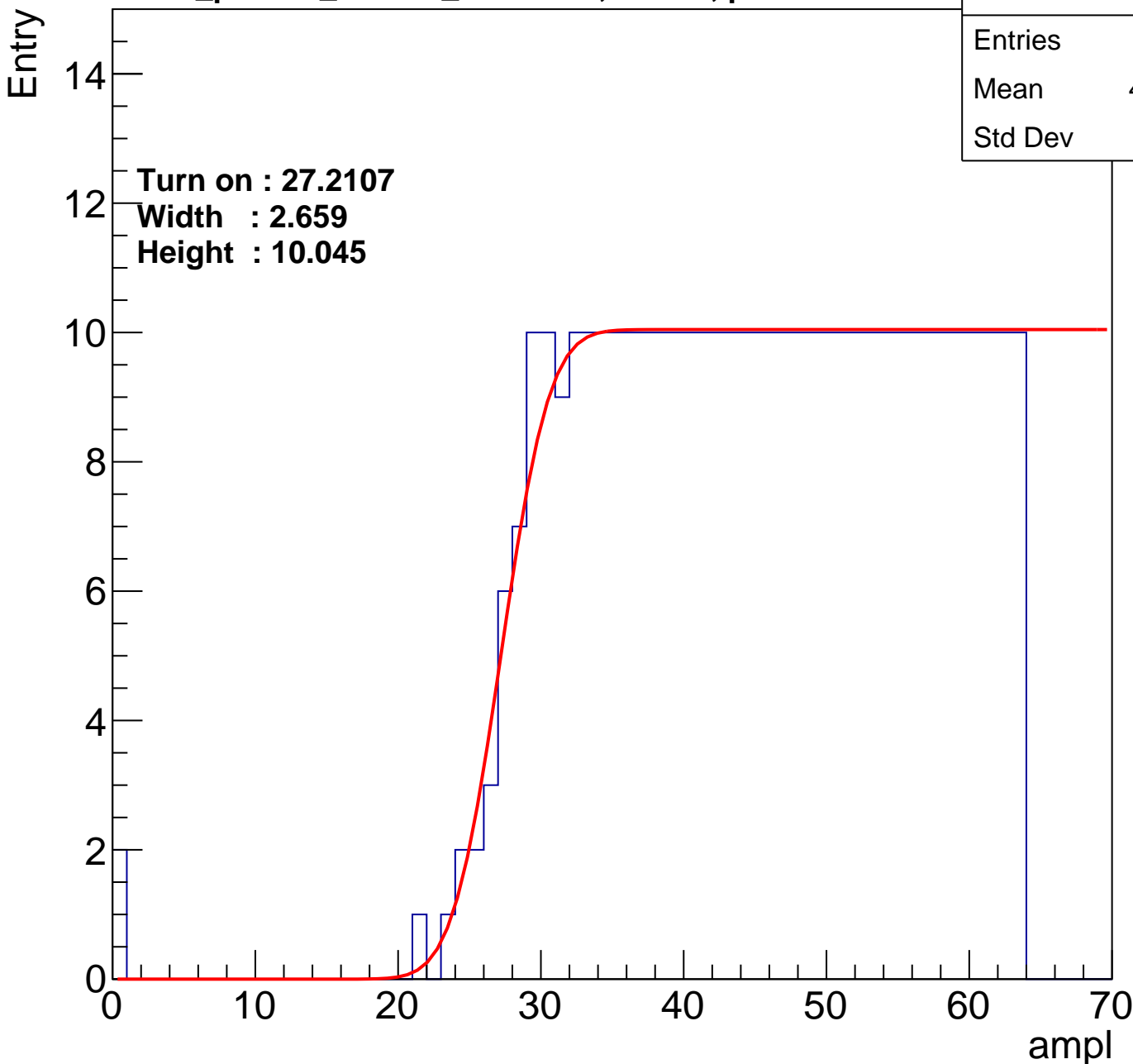
calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.2107

Width : 2.659

Height : 10.045



B0L102S, U11-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.44
Std Dev	11.33

Turn on : 27.1272

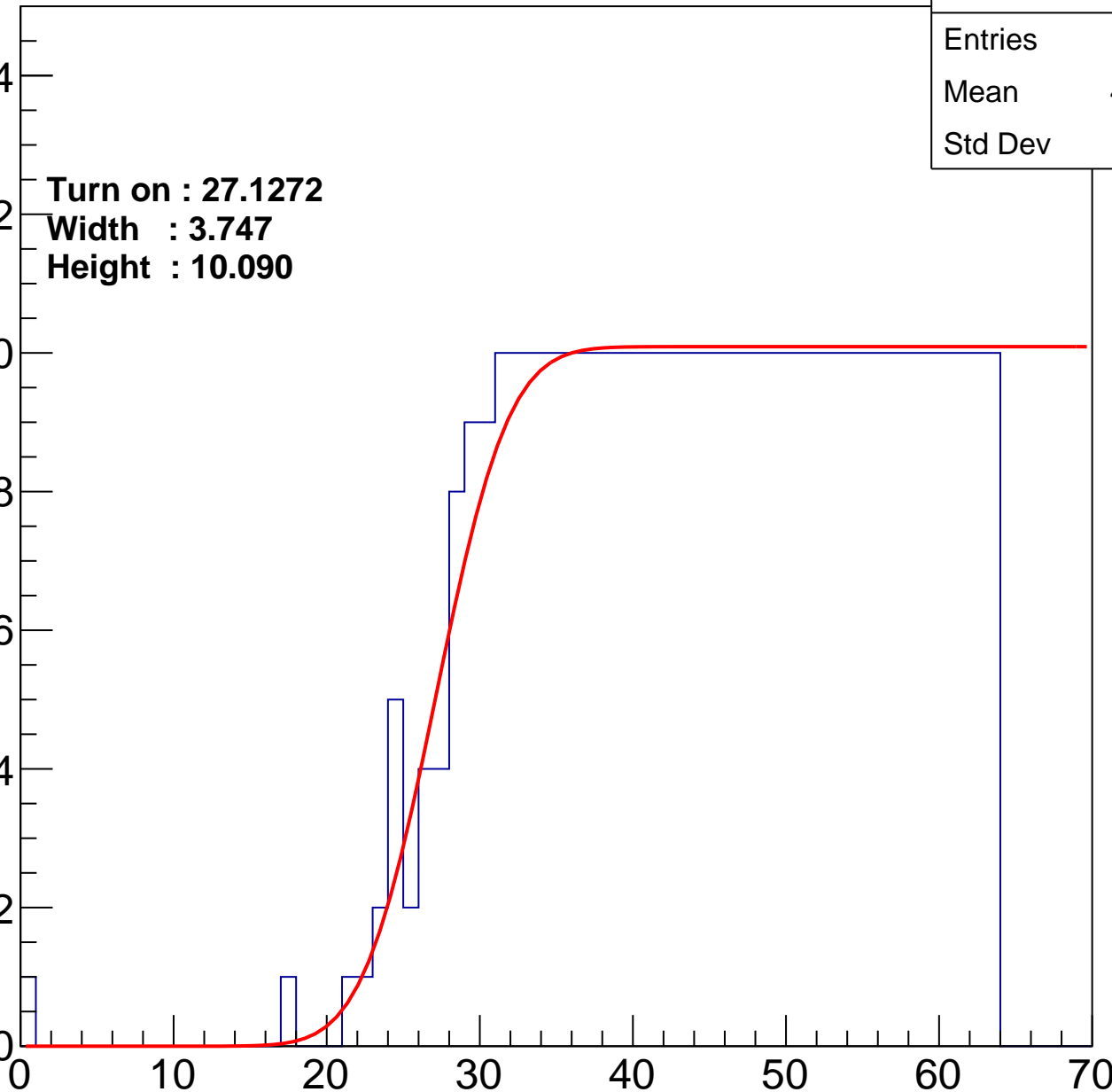
Width : 3.747

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.36
Std Dev	11.65

Turn on : 27.0670

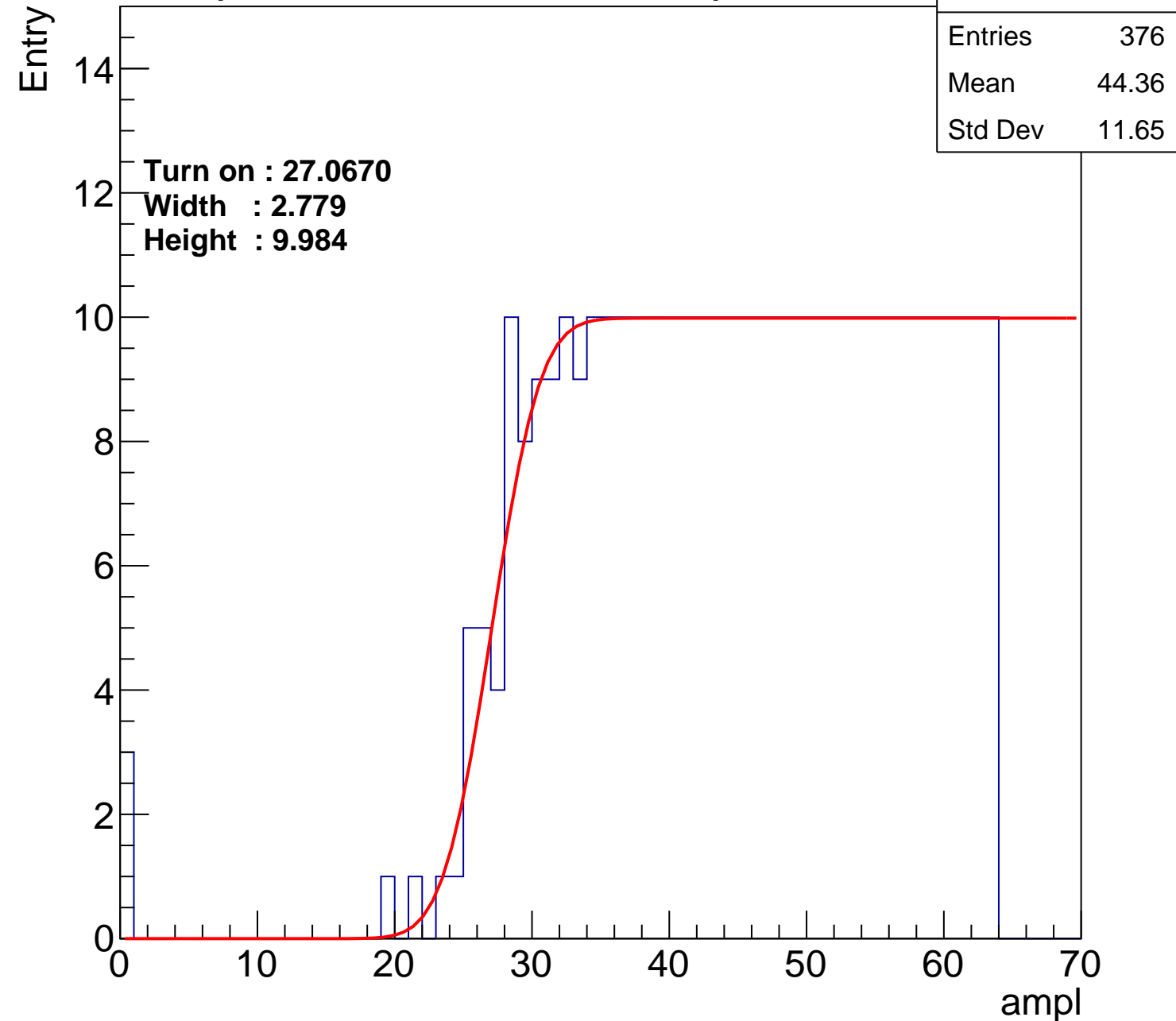
Width : 2.779

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch30

calib_packv5_042523_0143.root, FC#12, port B1

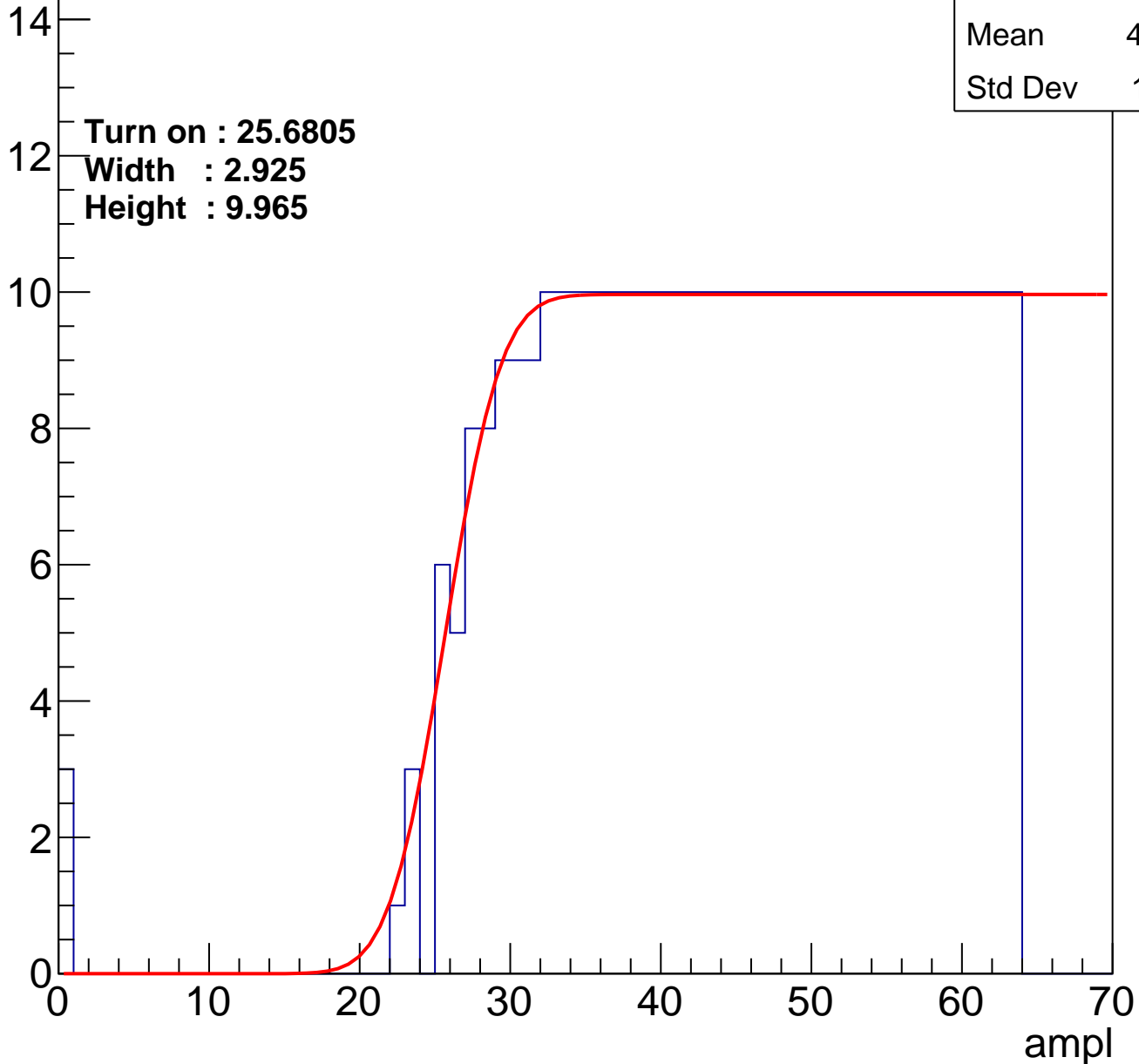
Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 25.6805

Width : 2.925

Height : 9.965

Entry



B0L102S, U11-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.17
Std Dev	11.86

Turn on : 26.7198

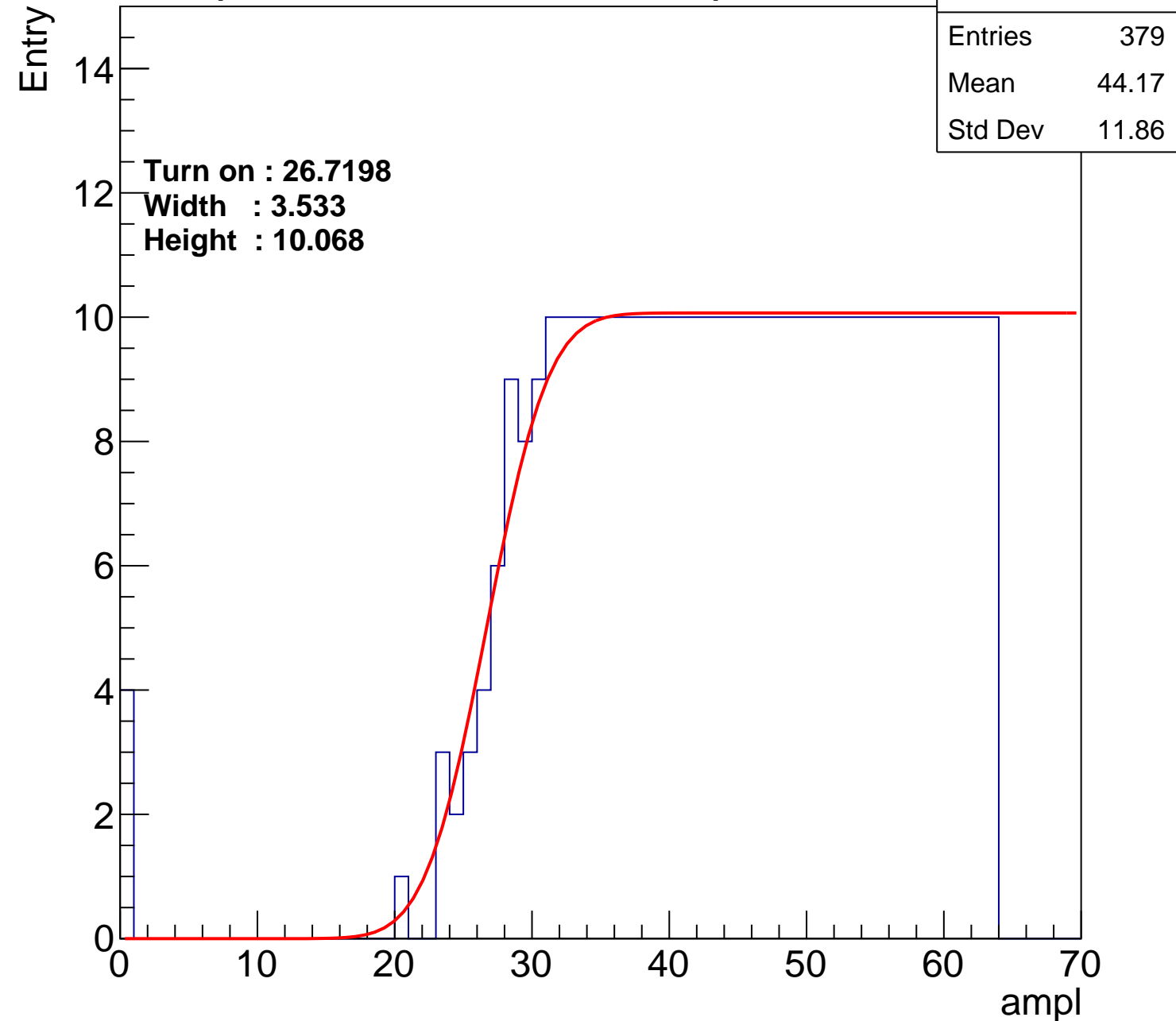
Width : 3.533

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch32

calib_packv5_042523_0143.root, FC#12, port B1

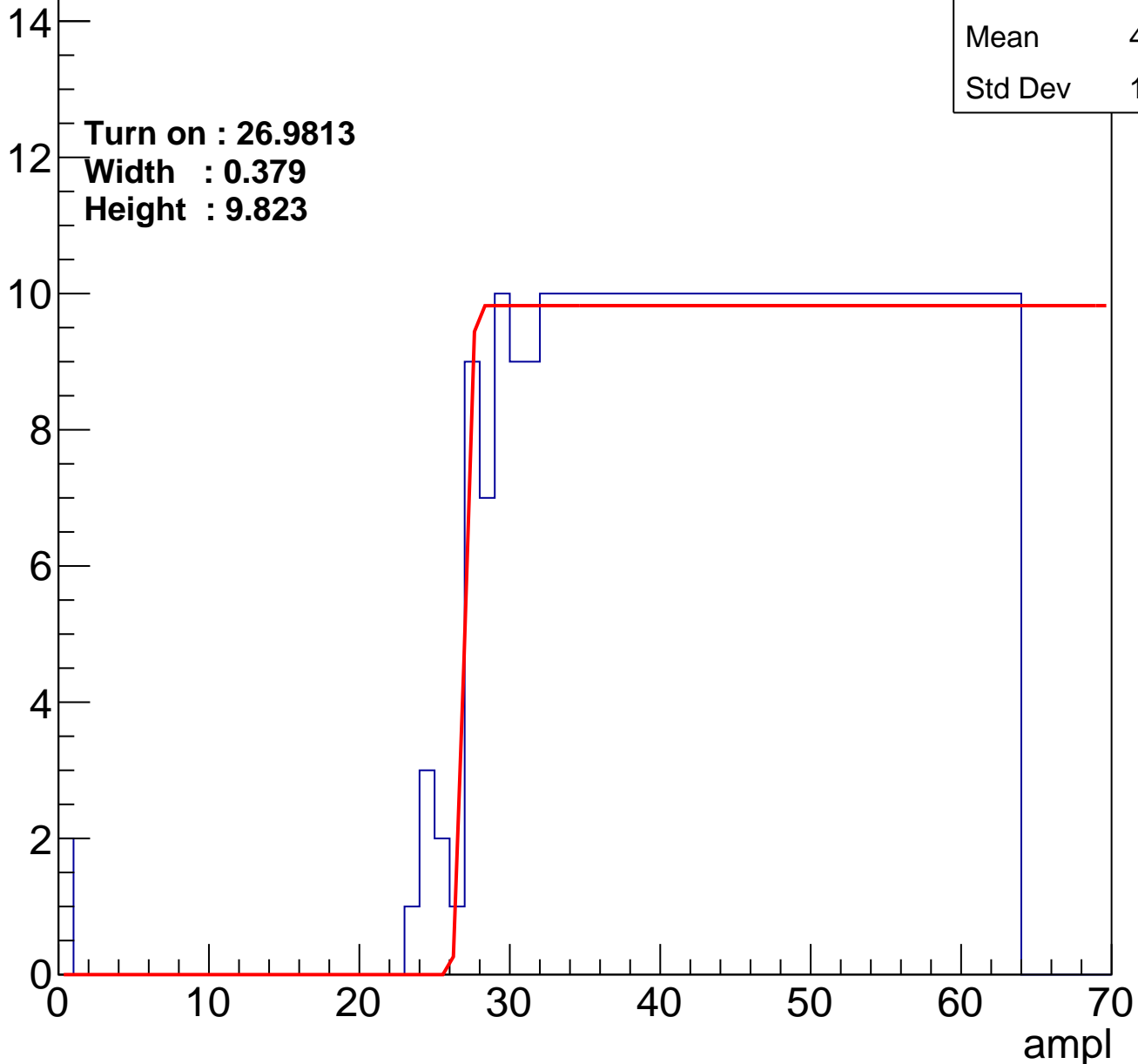
Entries	373
Mean	44.64
Std Dev	11.29

Turn on : 26.9813

Width : 0.379

Height : 9.823

Entry



B0L102S, U11-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.1235

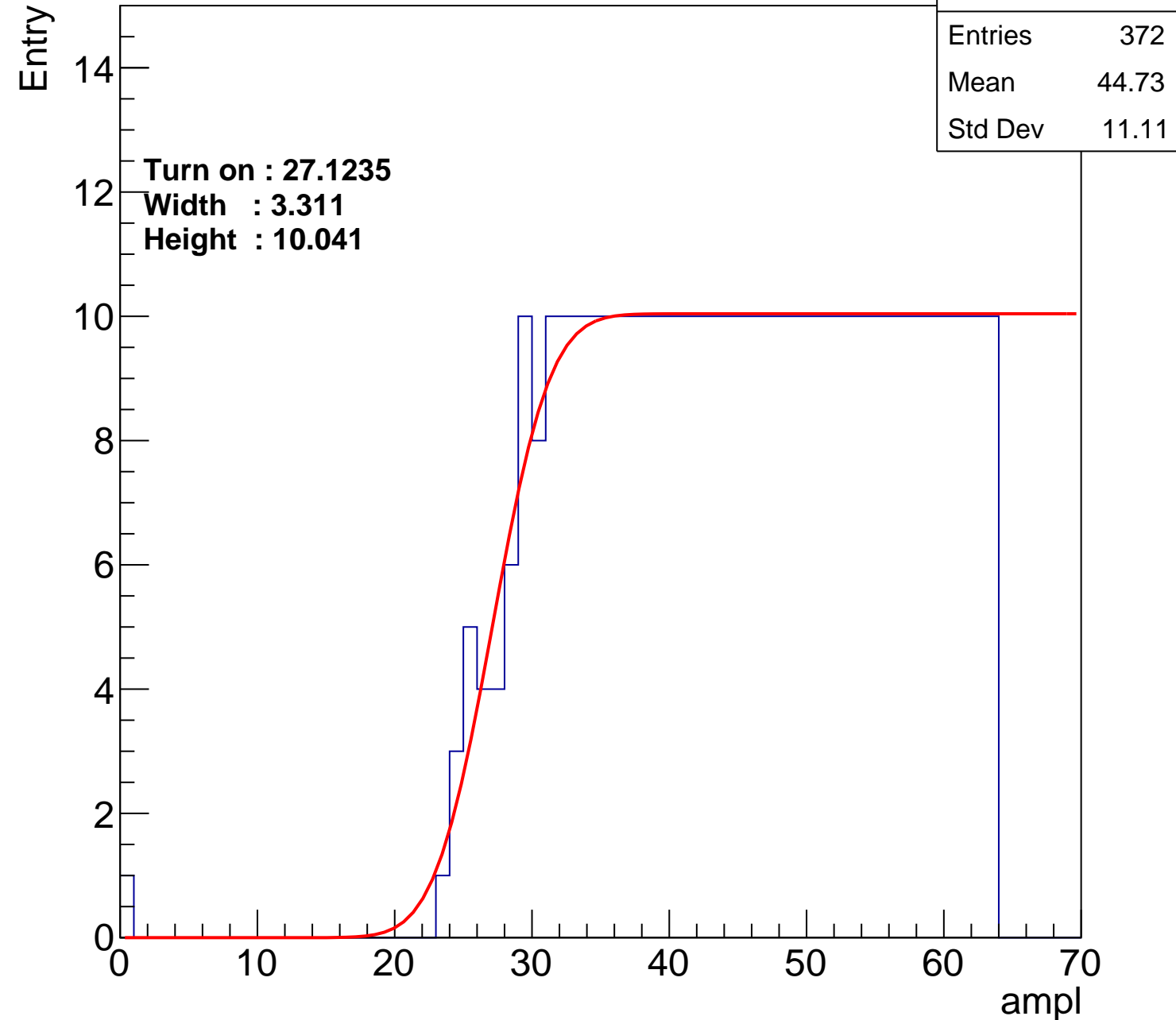
Width : 3.311

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.69
Std Dev	11.85

Turn on : 26.2250

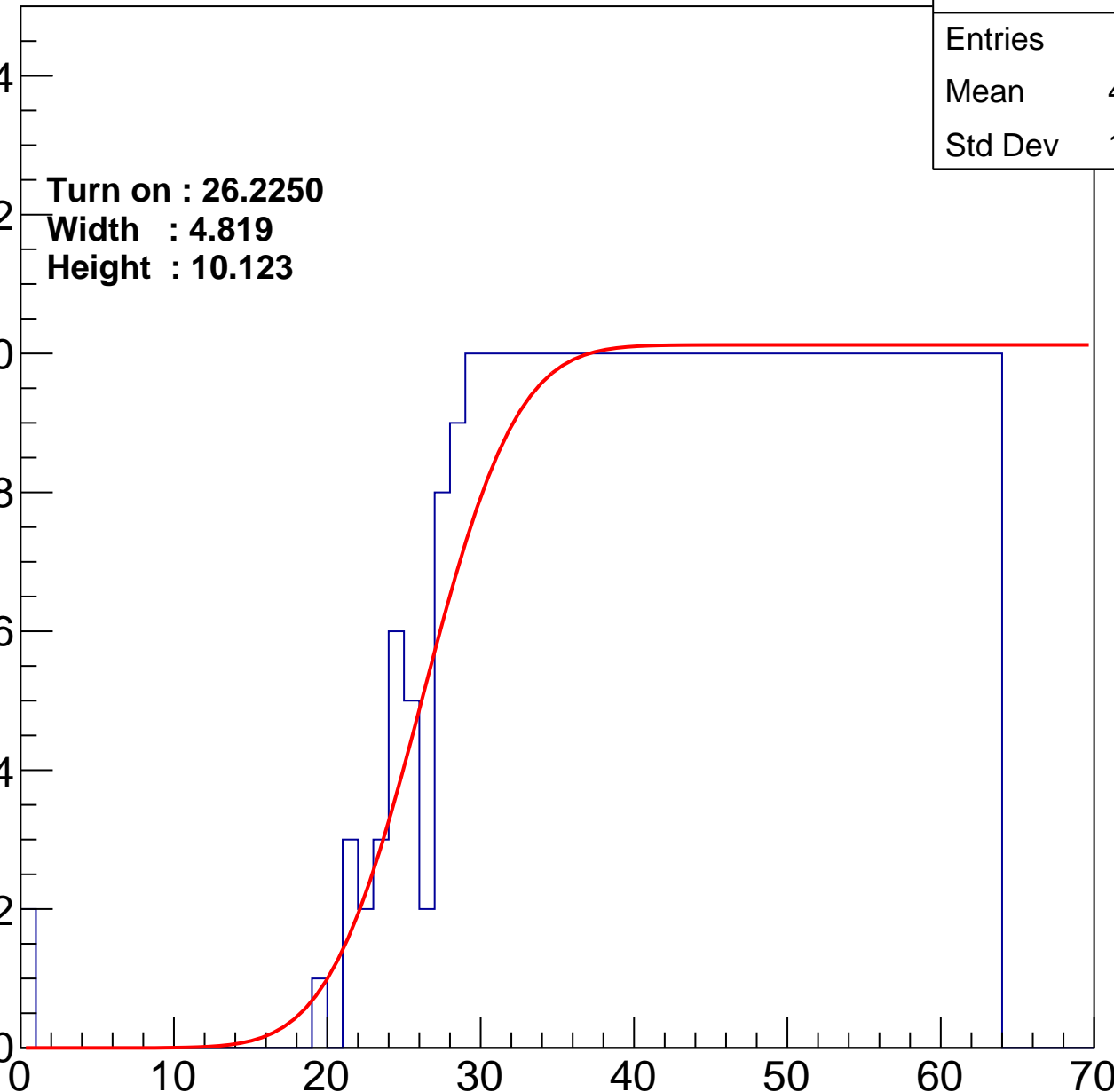
Width : 4.819

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.65
Std Dev	11.51

Turn on : 27.5667

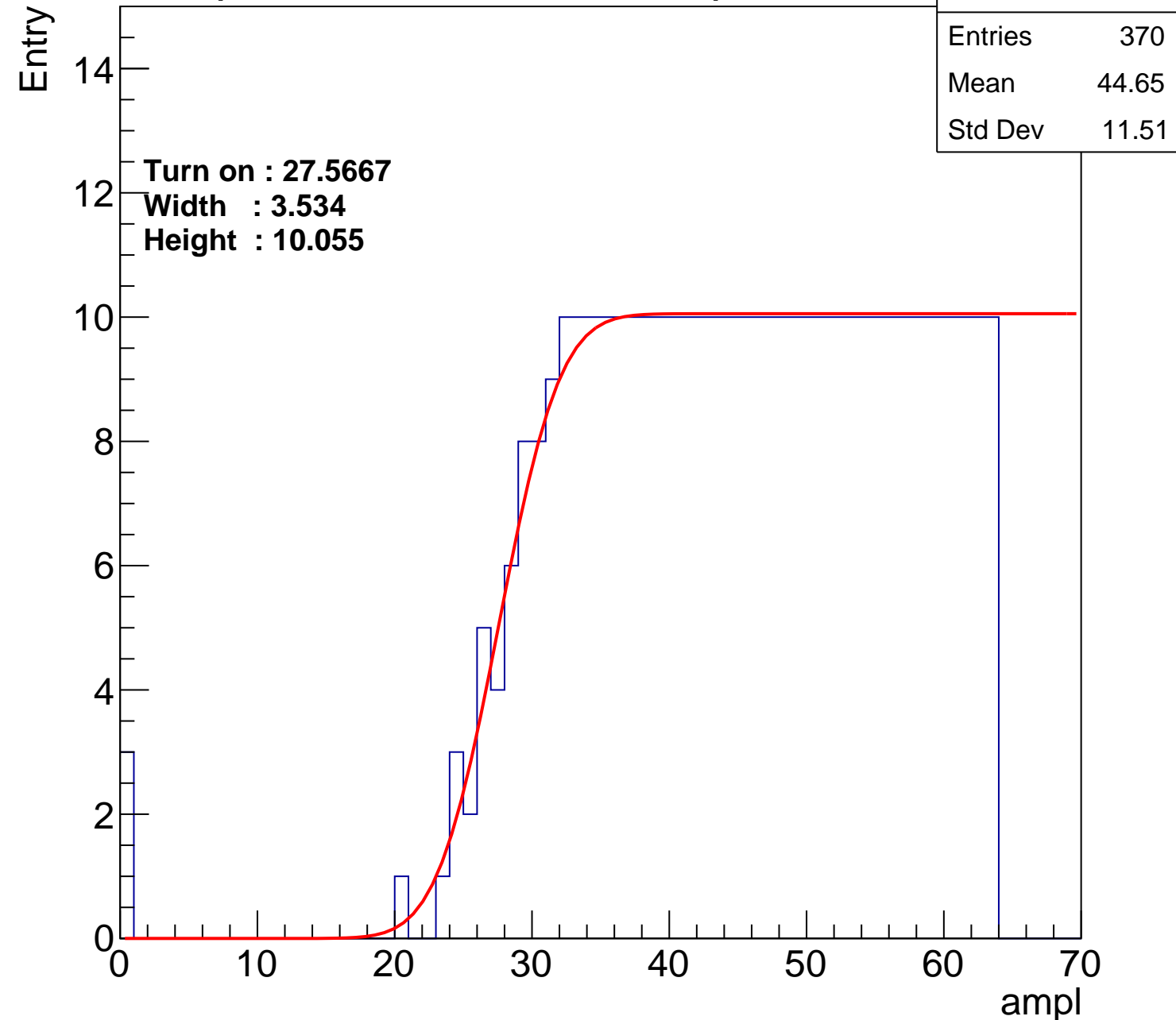
Width : 3.534

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.78
Std Dev	11.65

Turn on : 25.7254

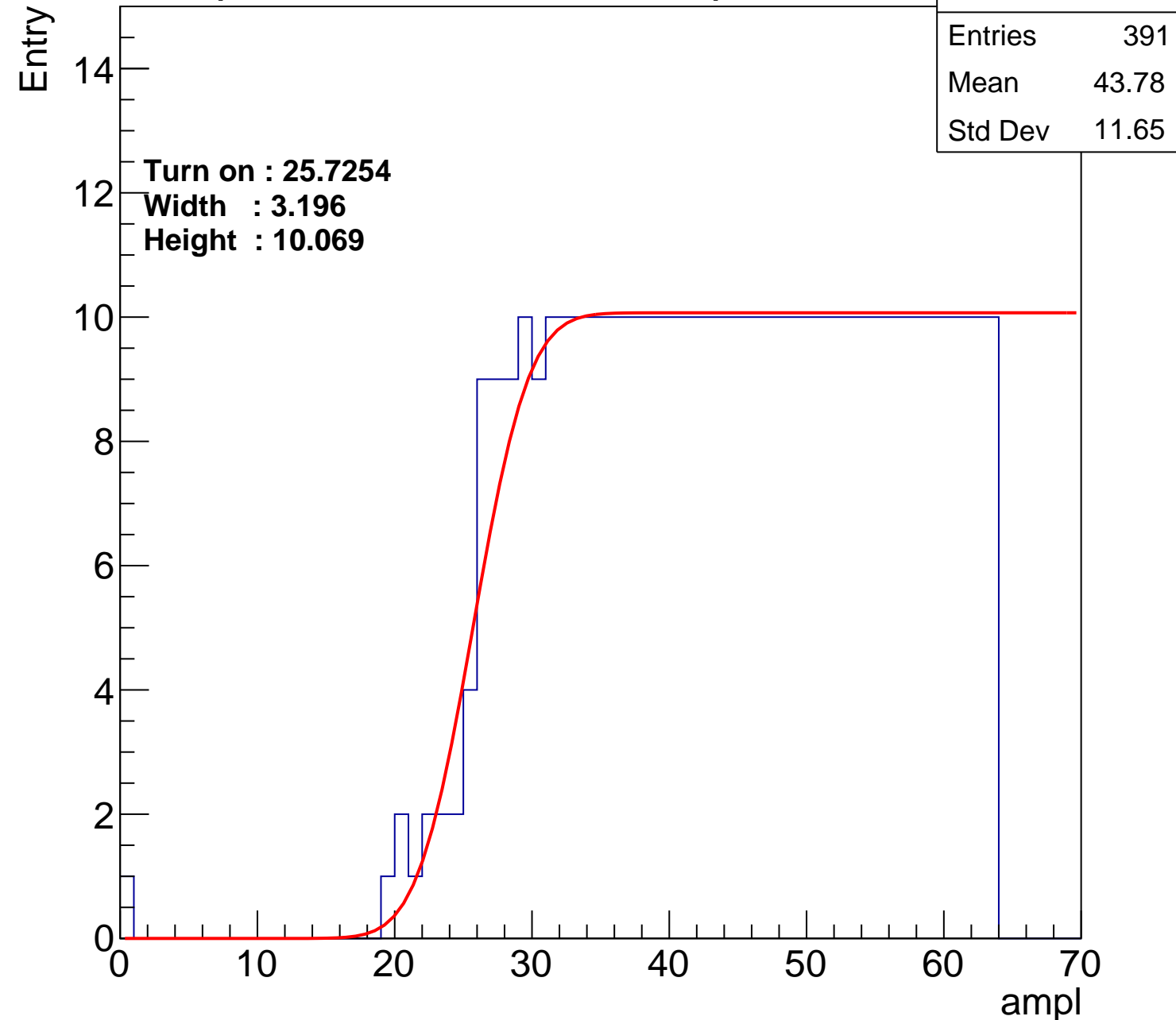
Width : 3.196

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch37

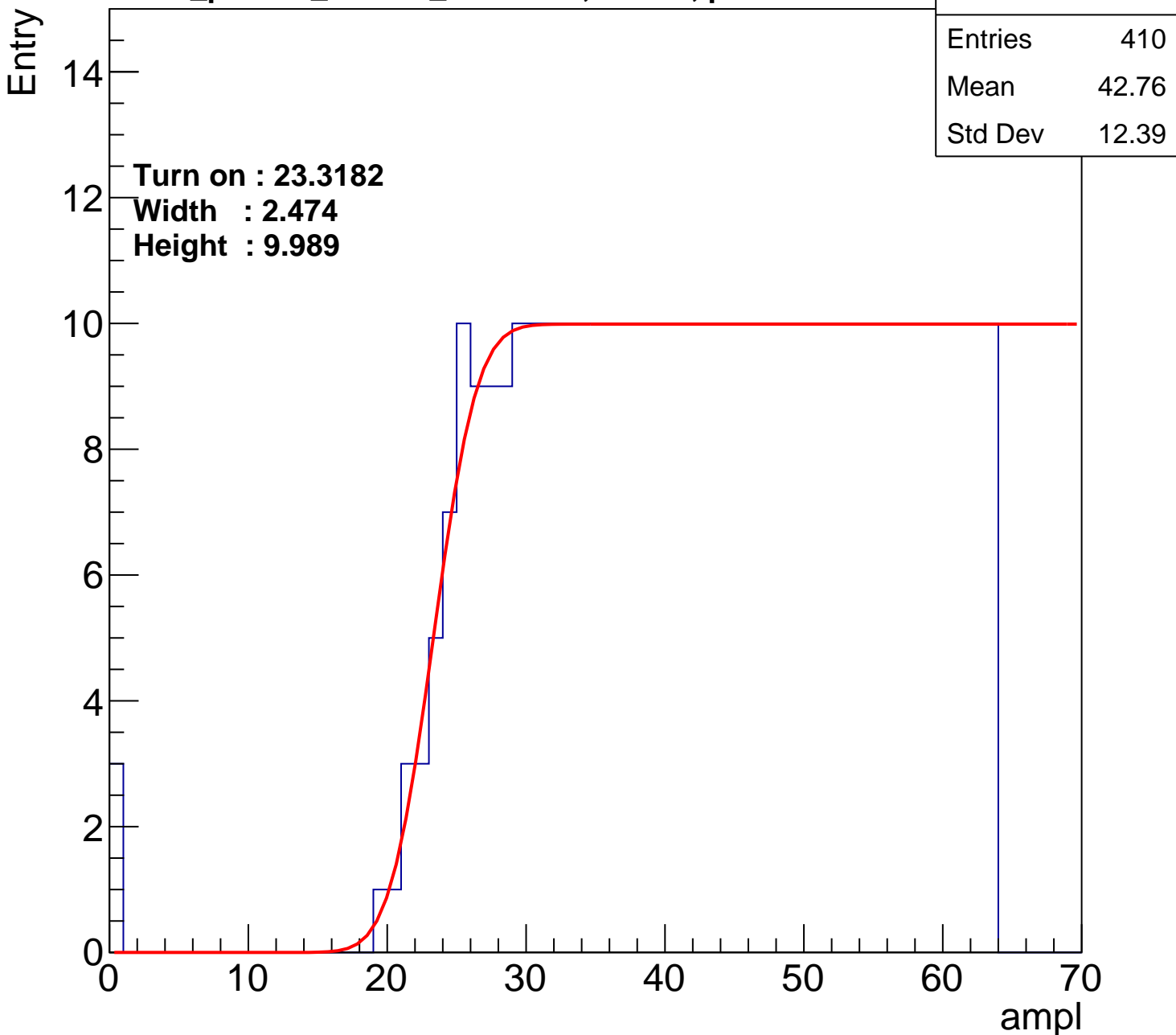
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 23.3182

Width : 2.474

Height : 9.989

Entries	410
Mean	42.76
Std Dev	12.39



B0L102S, U11-ch38

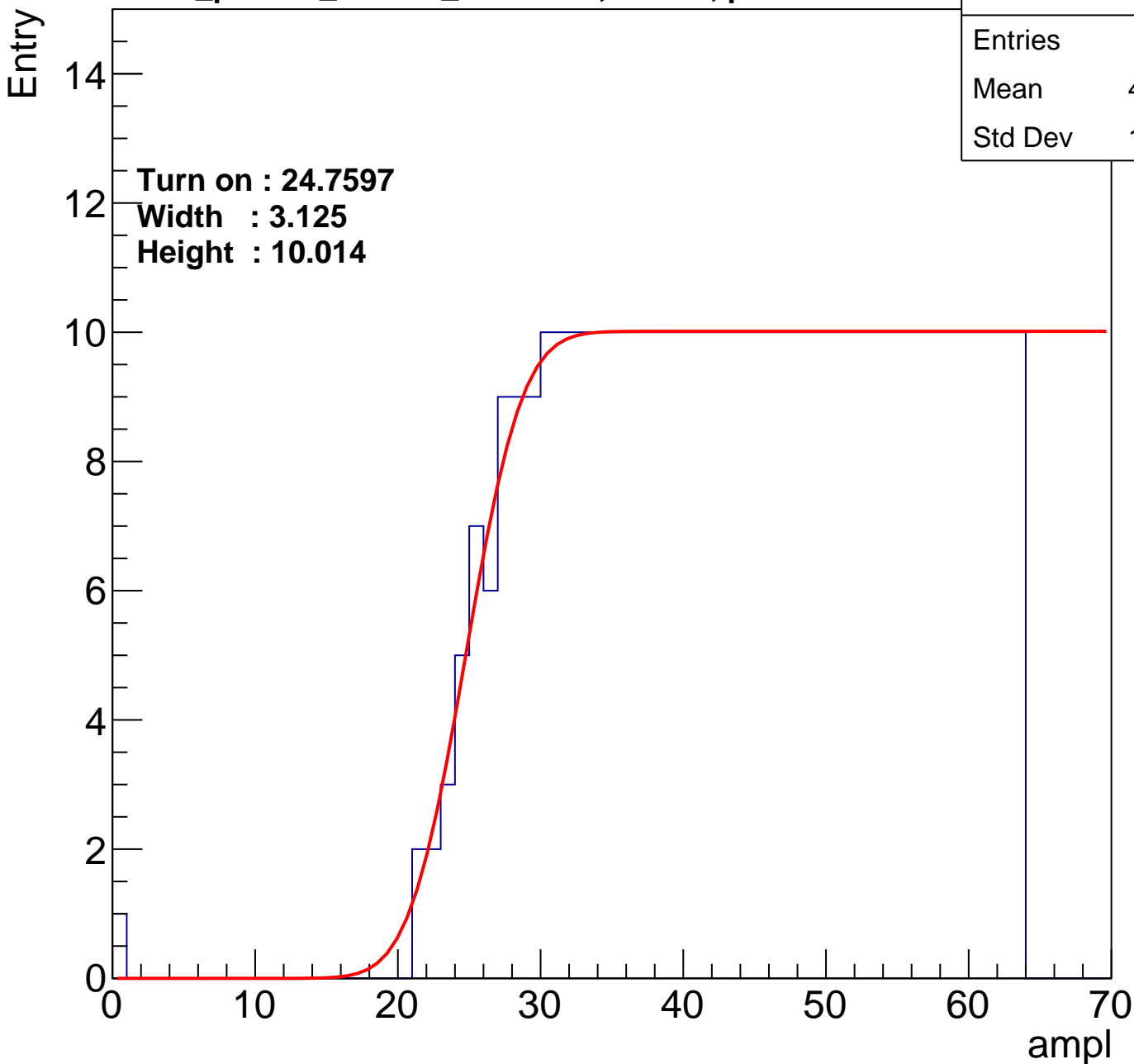
calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.69
Std Dev	11.67

Turn on : 24.7597

Width : 3.125

Height : 10.014



B0L102S, U11-ch39

calib_packv5_042523_0143.root, FC#12, port B1

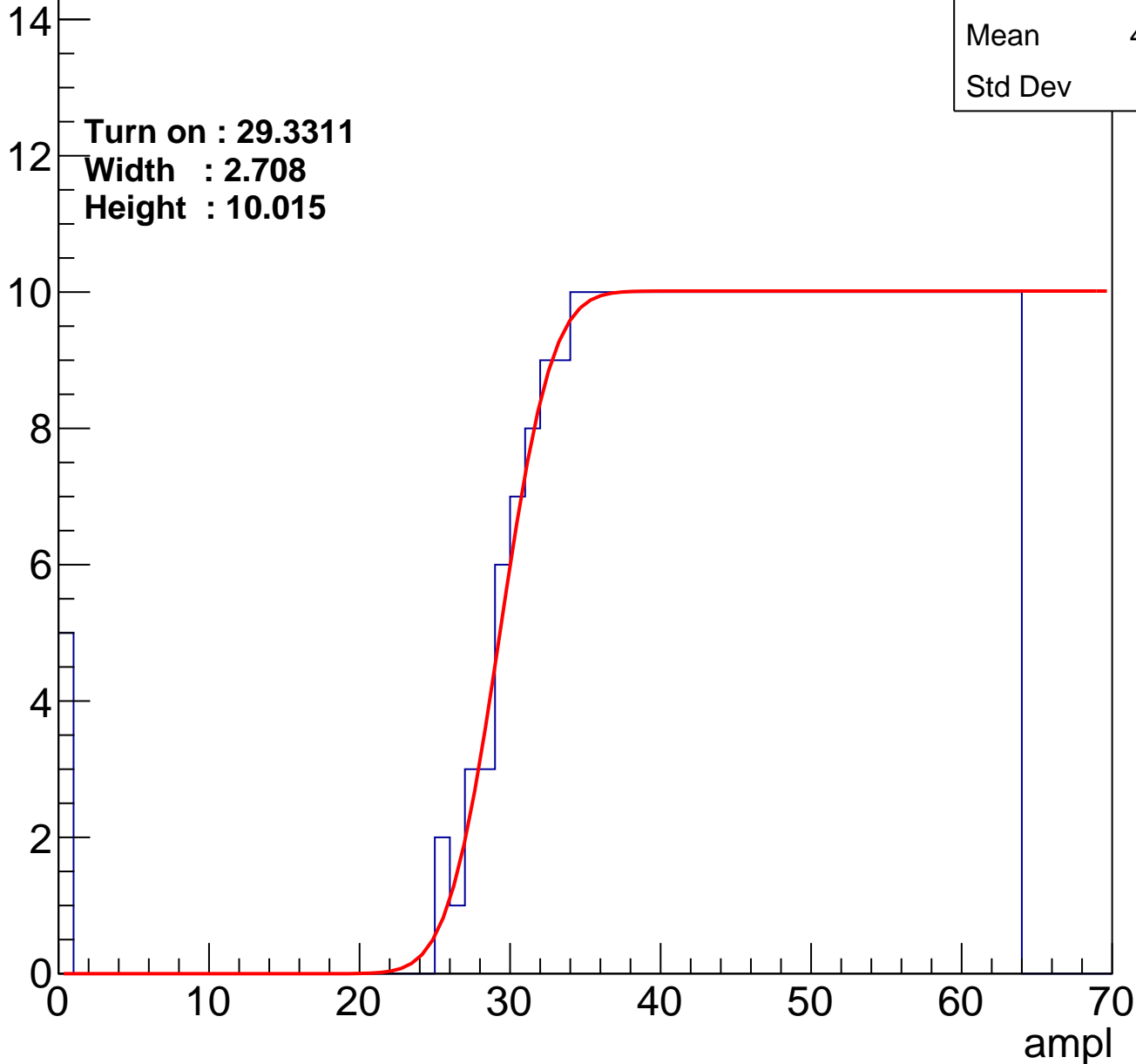
Entries	353
Mean	45.35
Std Dev	11.51

Turn on : 29.3311

Width : 2.708

Height : 10.015

Entry



B0L102S, U11-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.33
Std Dev	12.55

Turn on : 24.7458

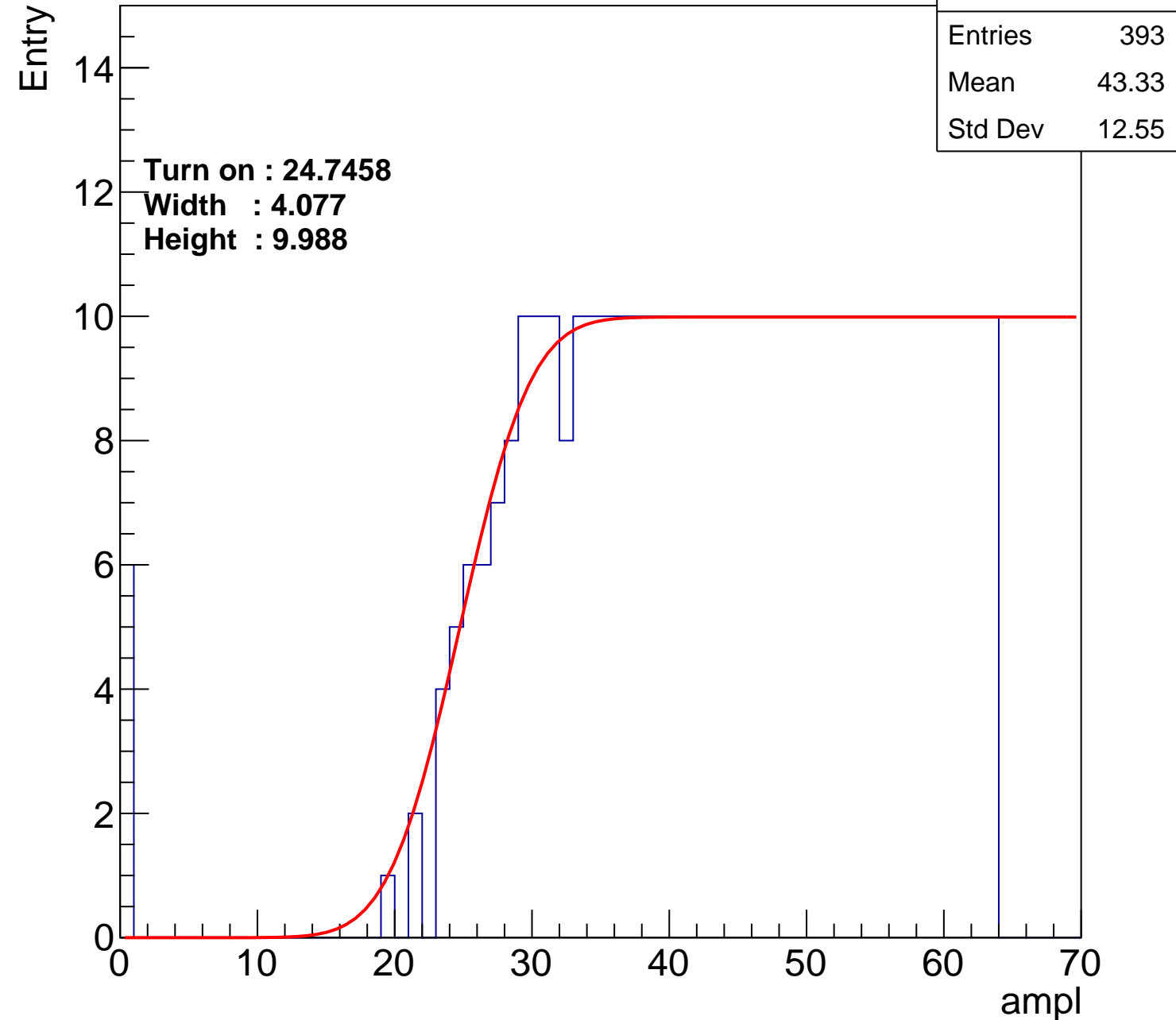
Width : 4.077

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.08
Std Dev	11.91

Turn on : 26.7821

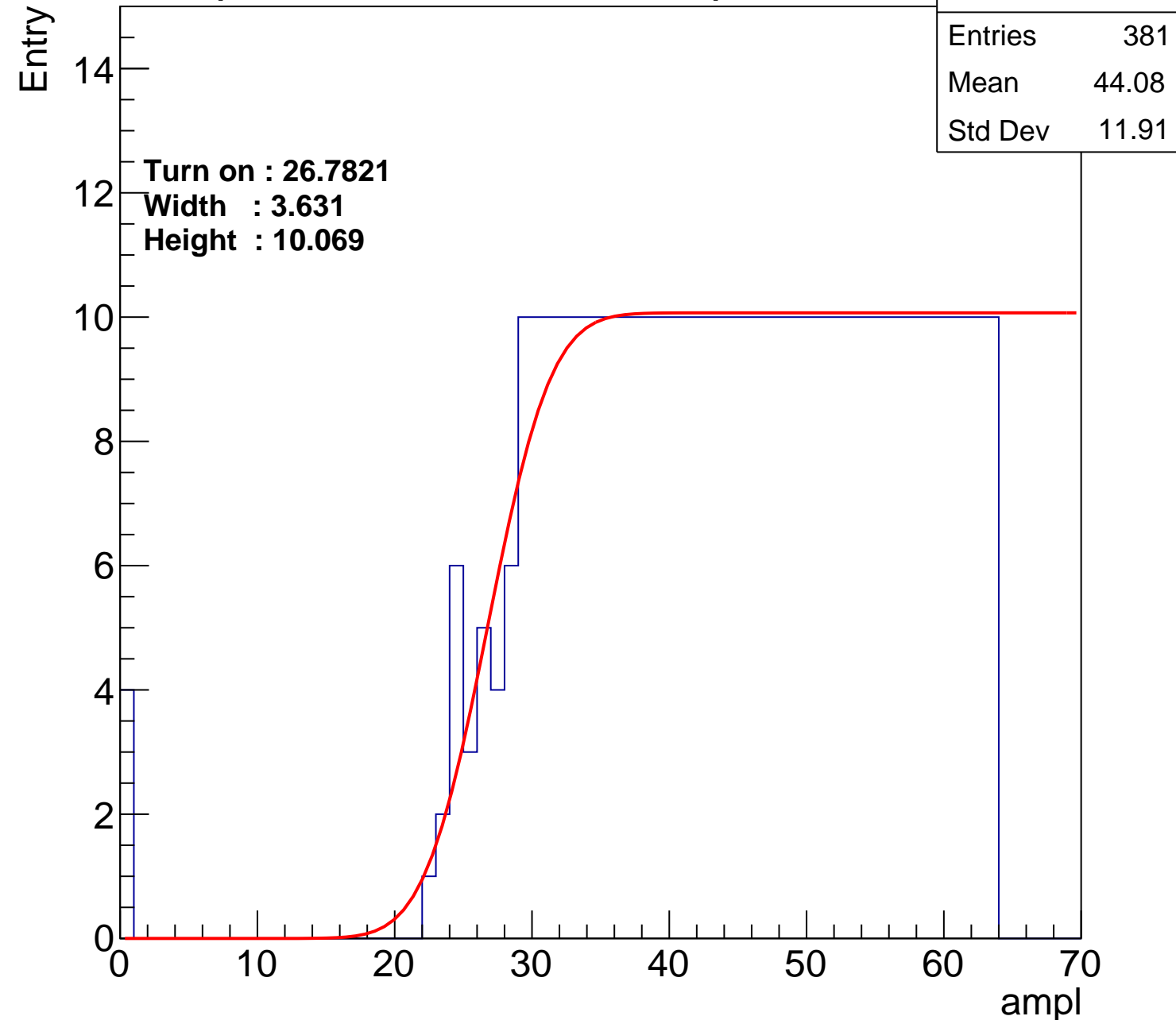
Width : 3.631

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.81
Std Dev	11.83

Turn on : 25.9115

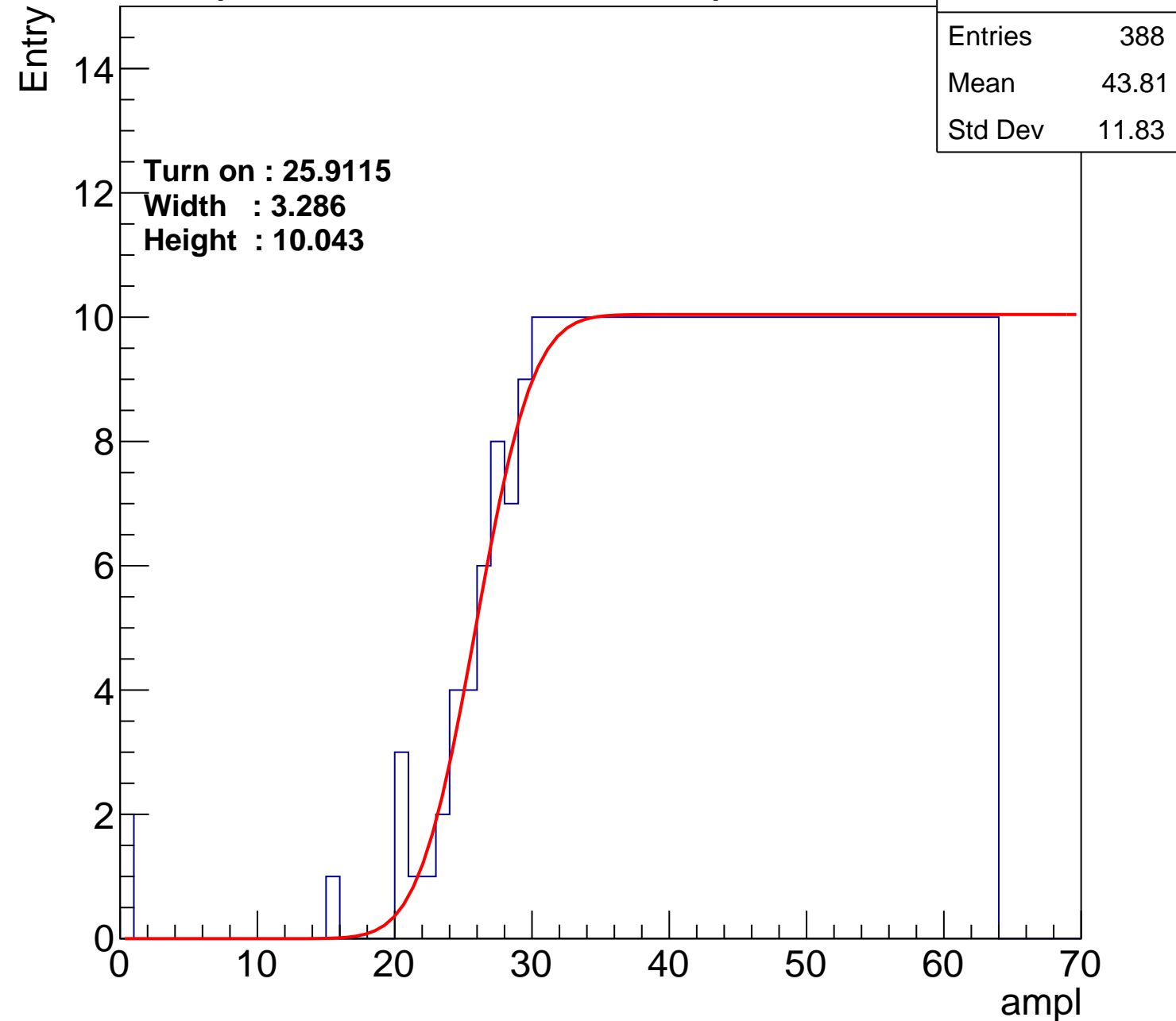
Width : 3.286

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.32
Std Dev	11.53

Turn on : 26.5741

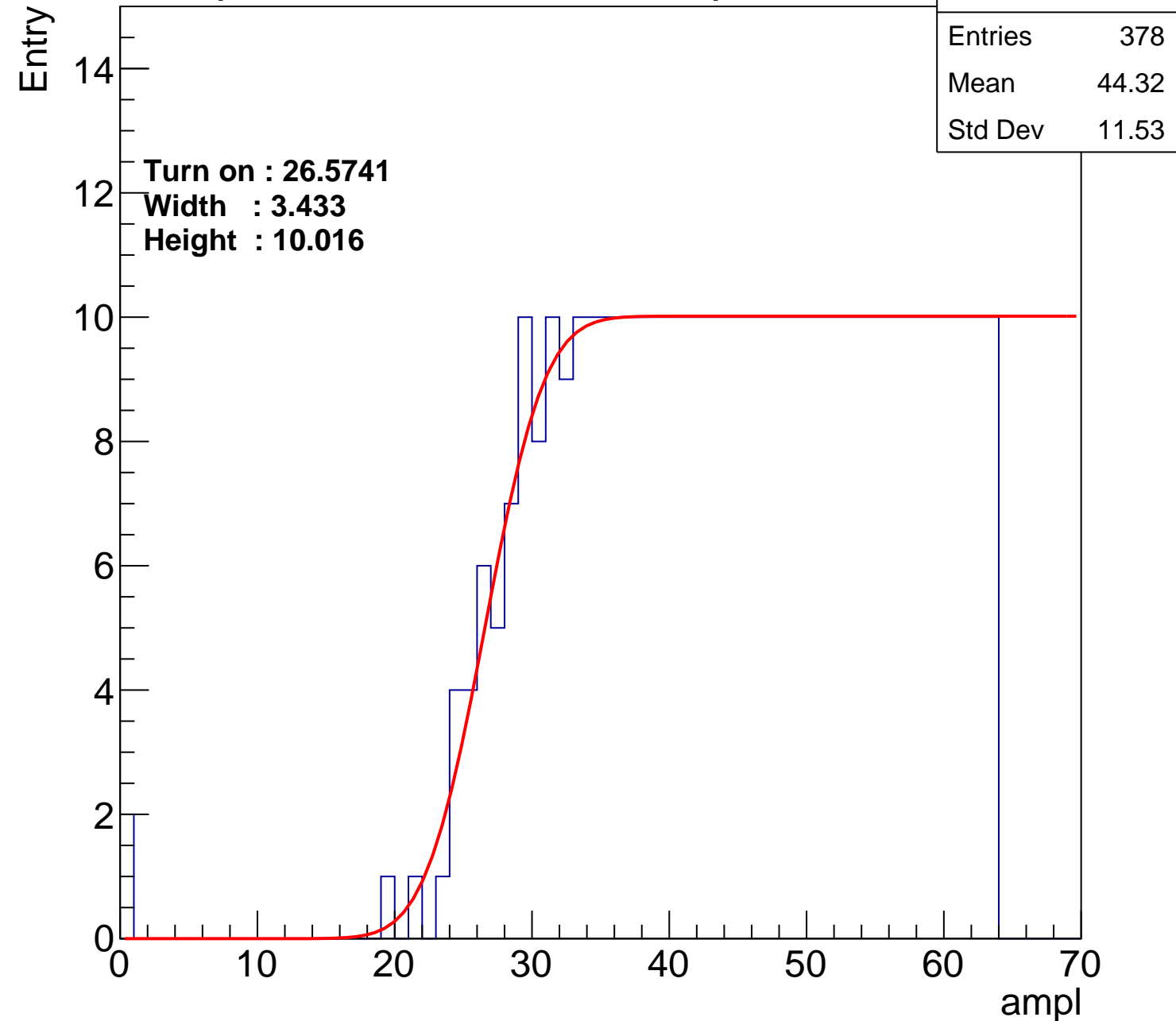
Width : 3.433

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl

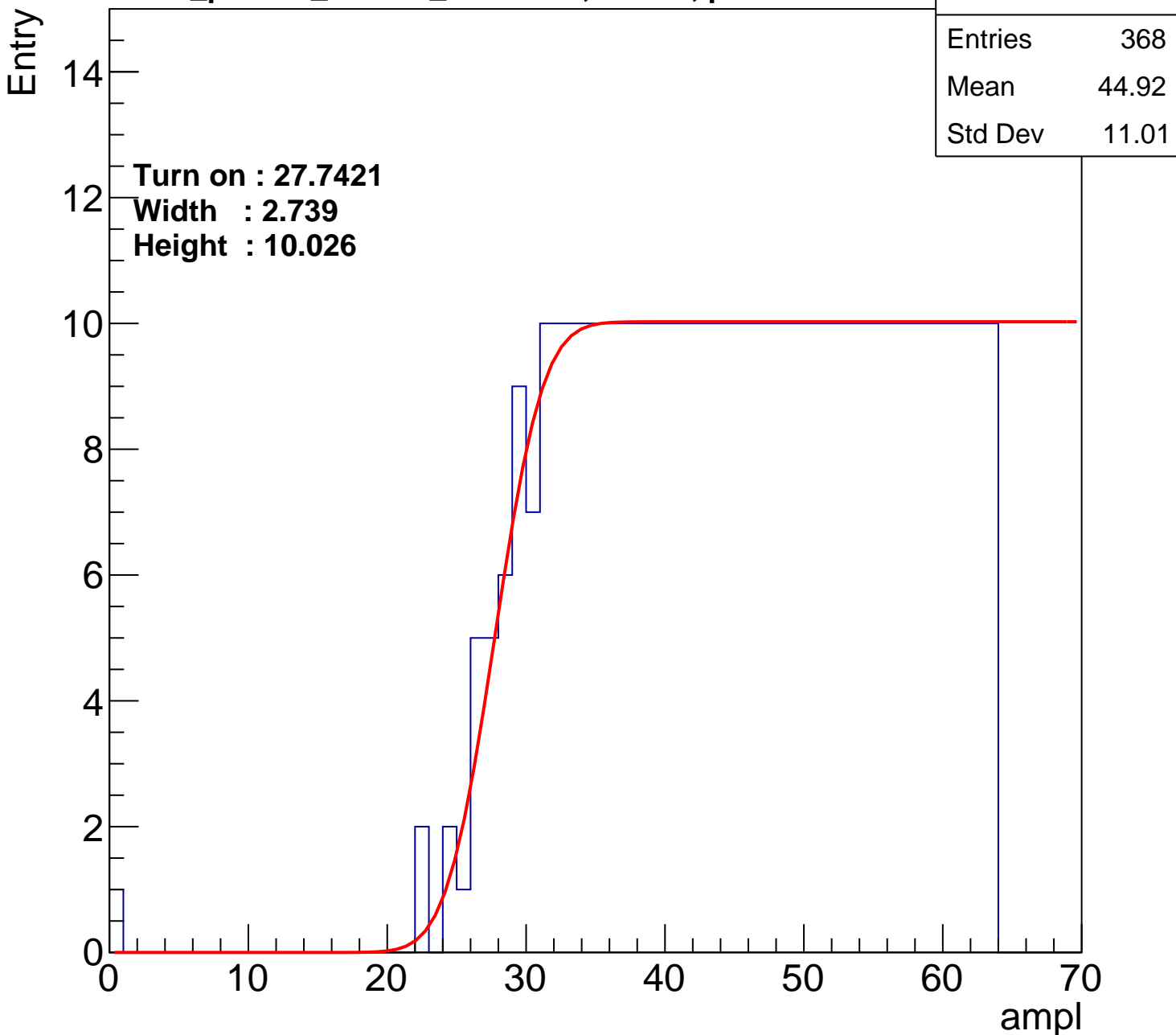


calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.92
Std Dev	11.01

Height : 10.026



B0L102S, U11-ch45

calib_packv5_042523_0143.root, FC#12, port B1

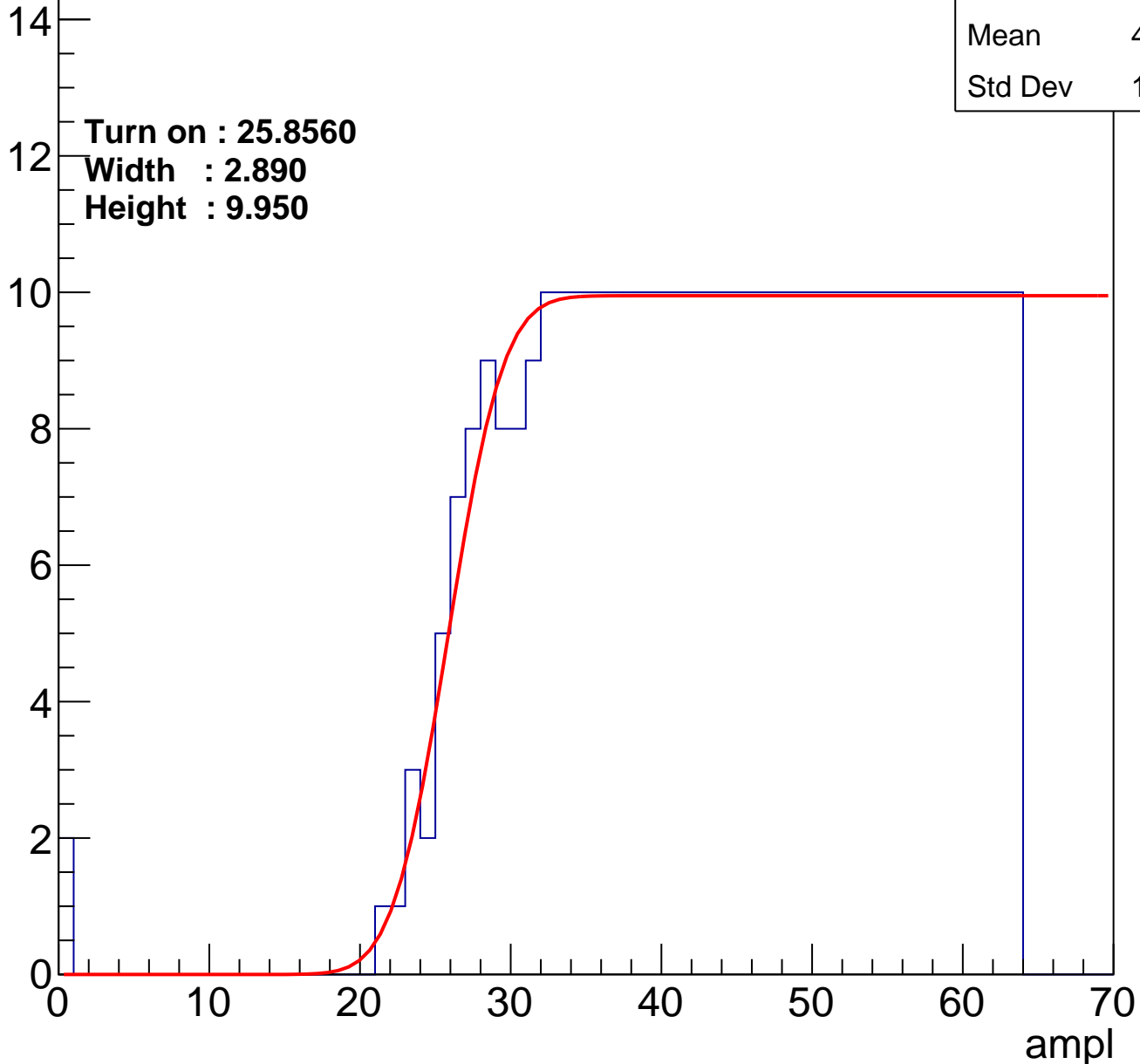
Entries	383
Mean	44.09
Std Dev	11.63

Turn on : 25.8560

Width : 2.890

Height : 9.950

Entry



B0L102S, U11-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.6352

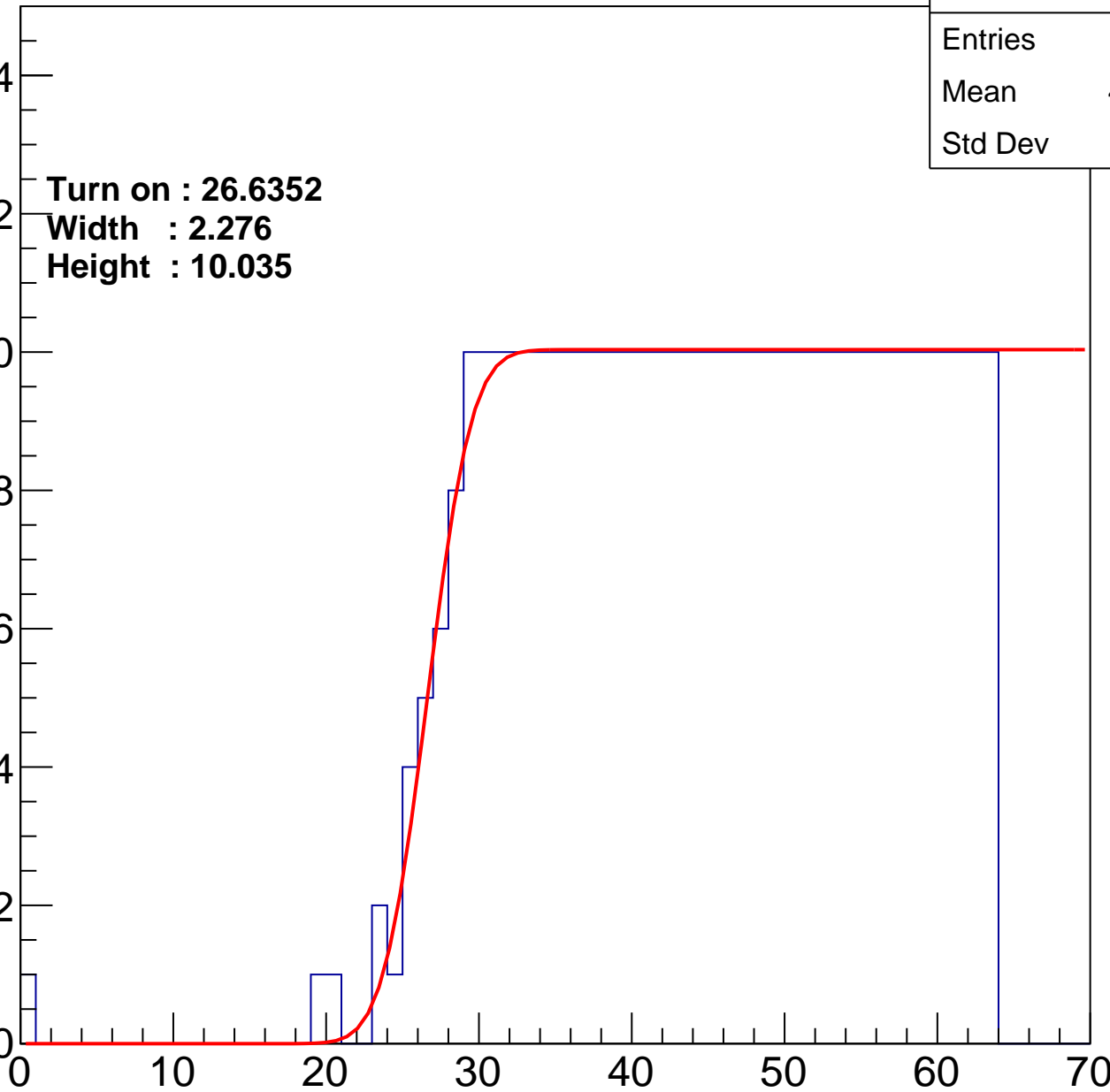
Width : 2.276

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.8335

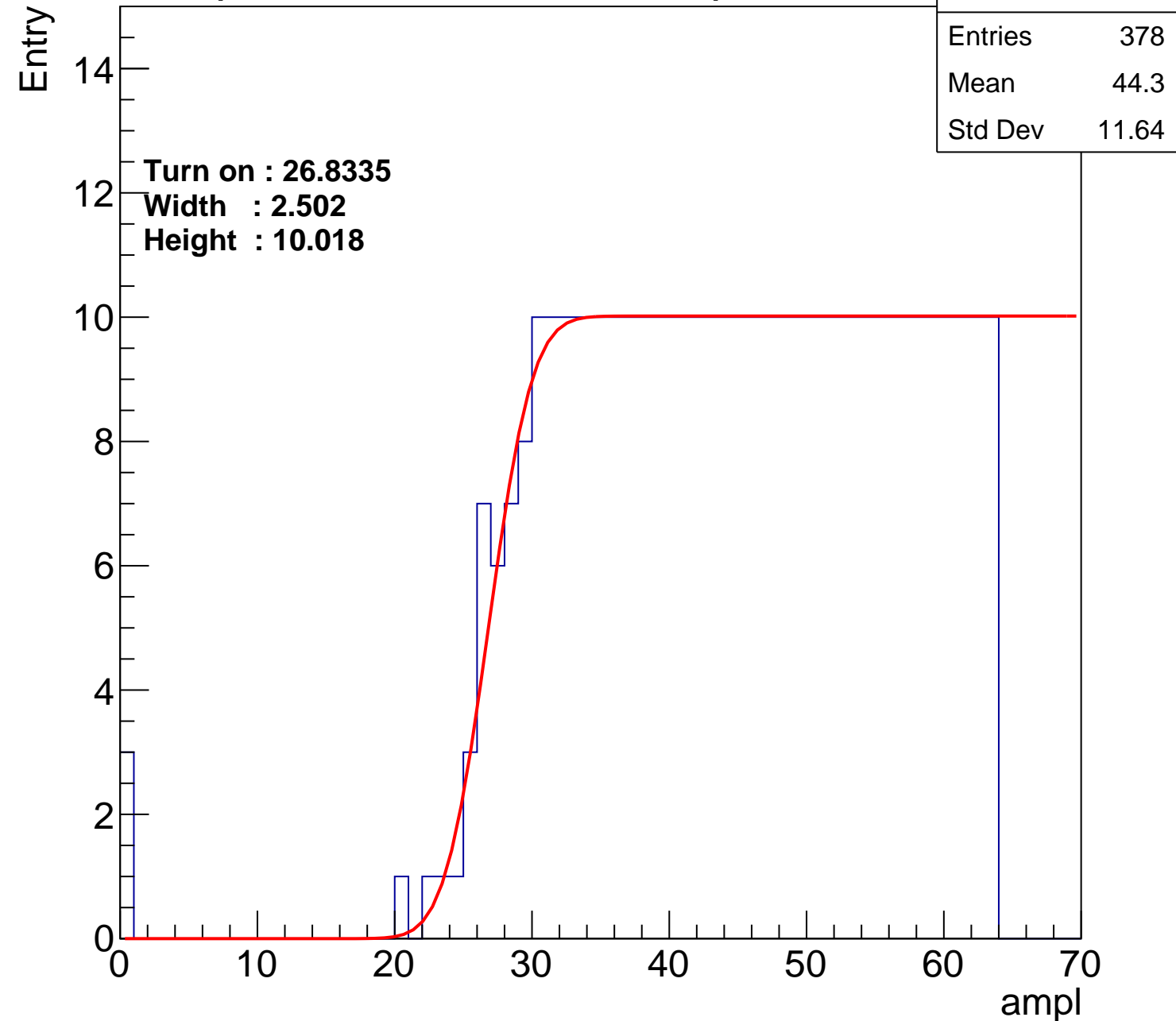
Width : 2.502

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.74
Std Dev	11.98

Turn on : 25.7005

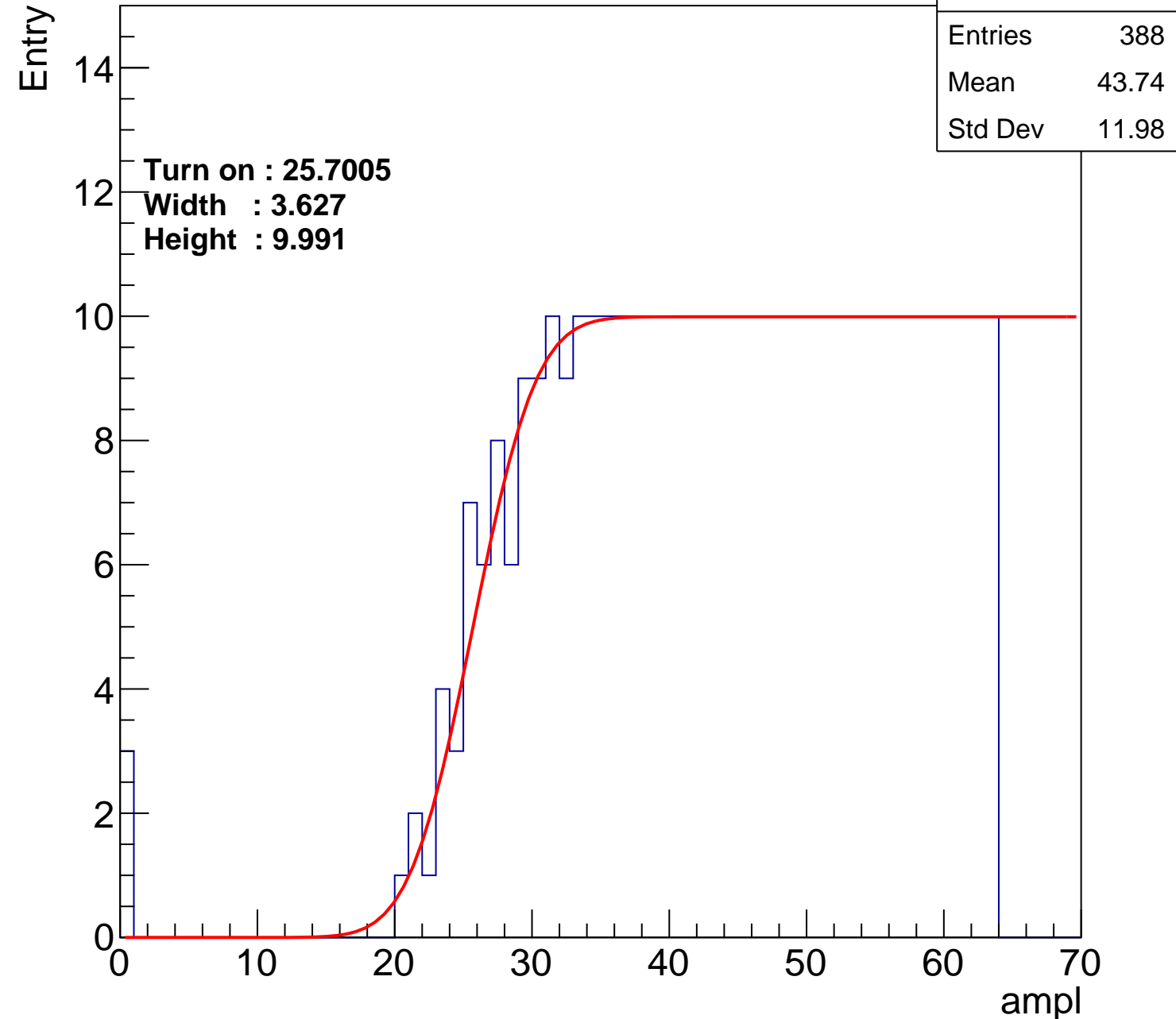
Width : 3.627

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.41
Std Dev	11.57

Turn on : 27.2248

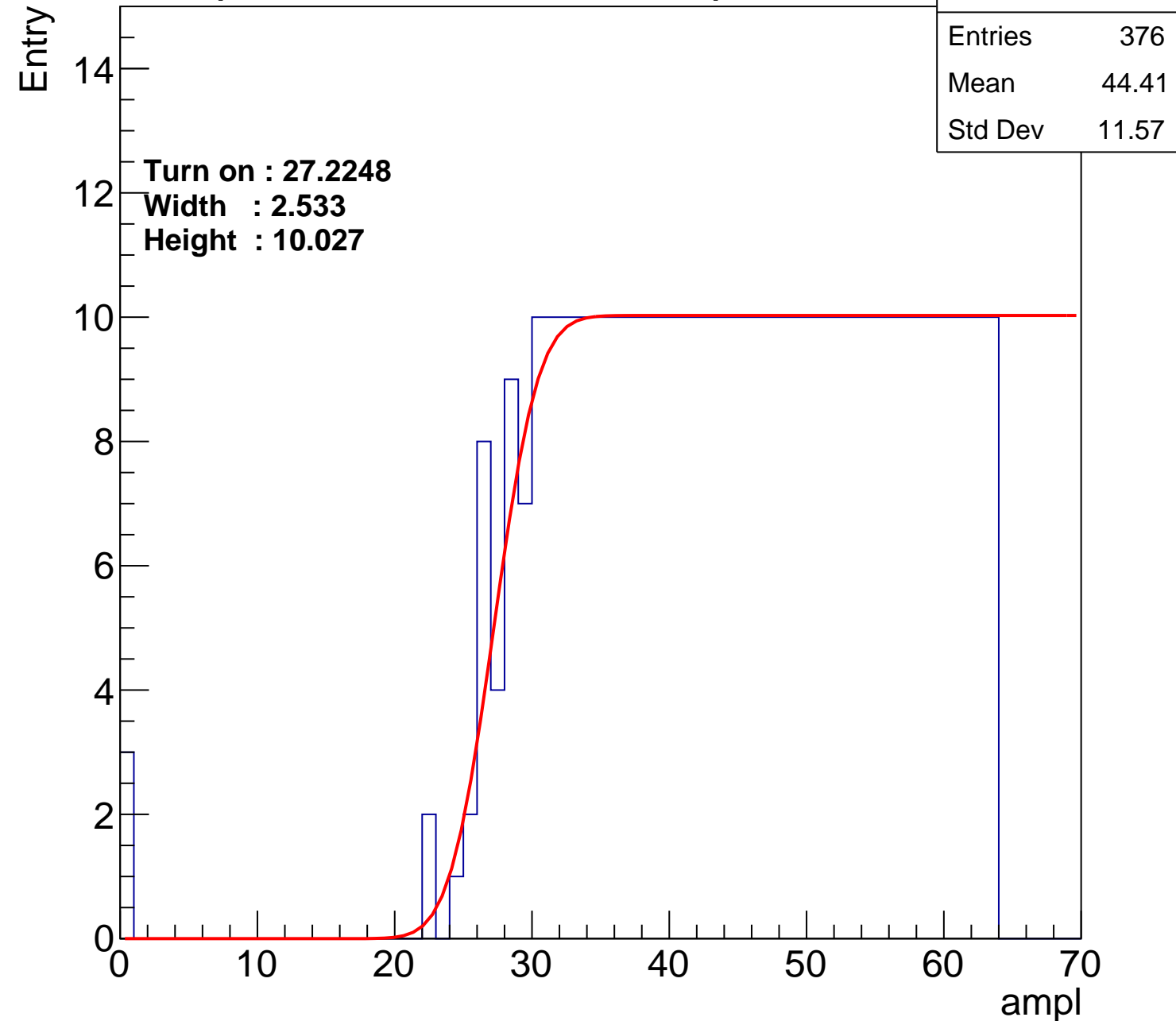
Width : 2.533

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.58
Std Dev	11.52

Turn on : 27.3425

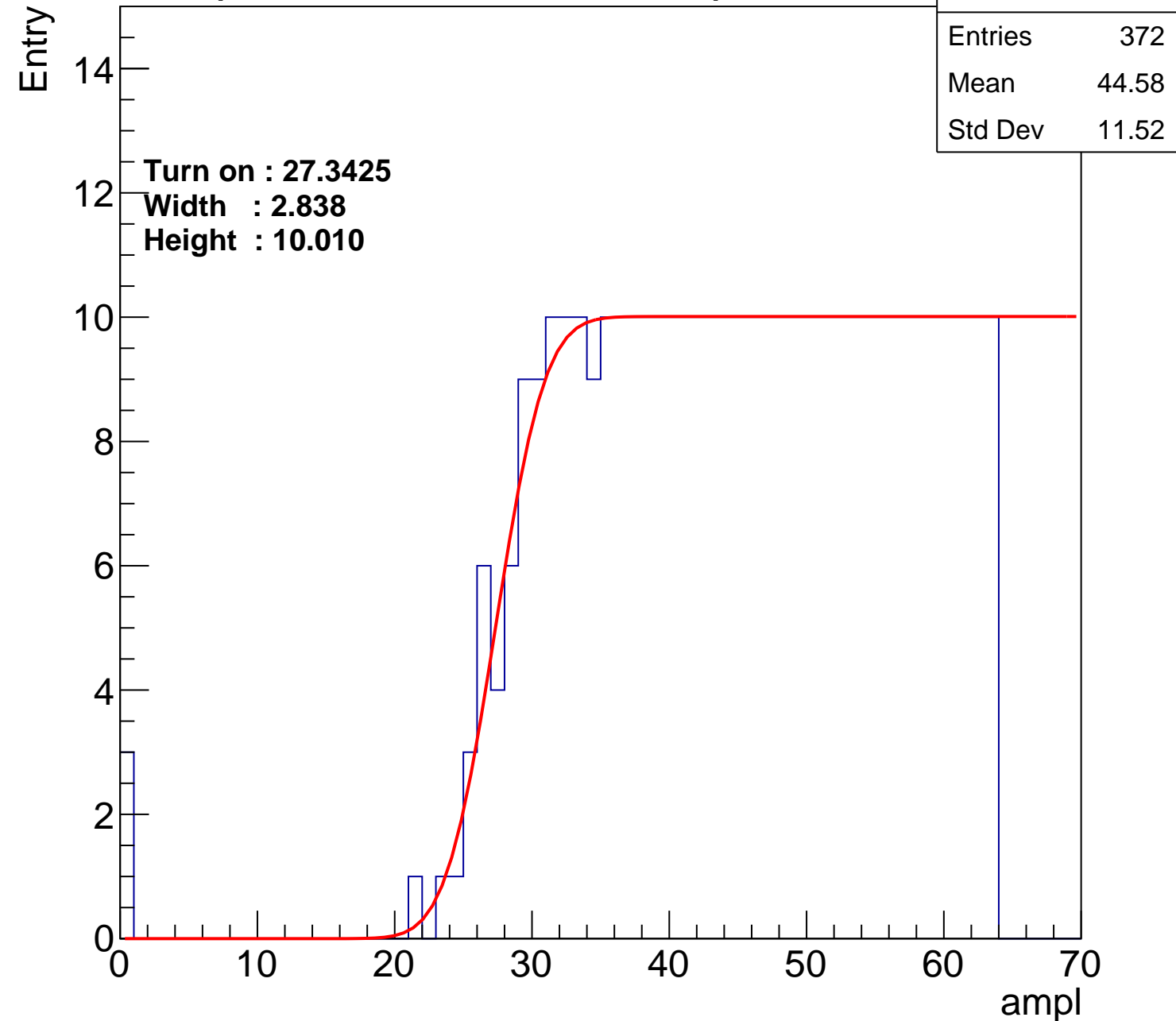
Width : 2.838

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.54
Std Dev	12.06

Turn on : 25.7082

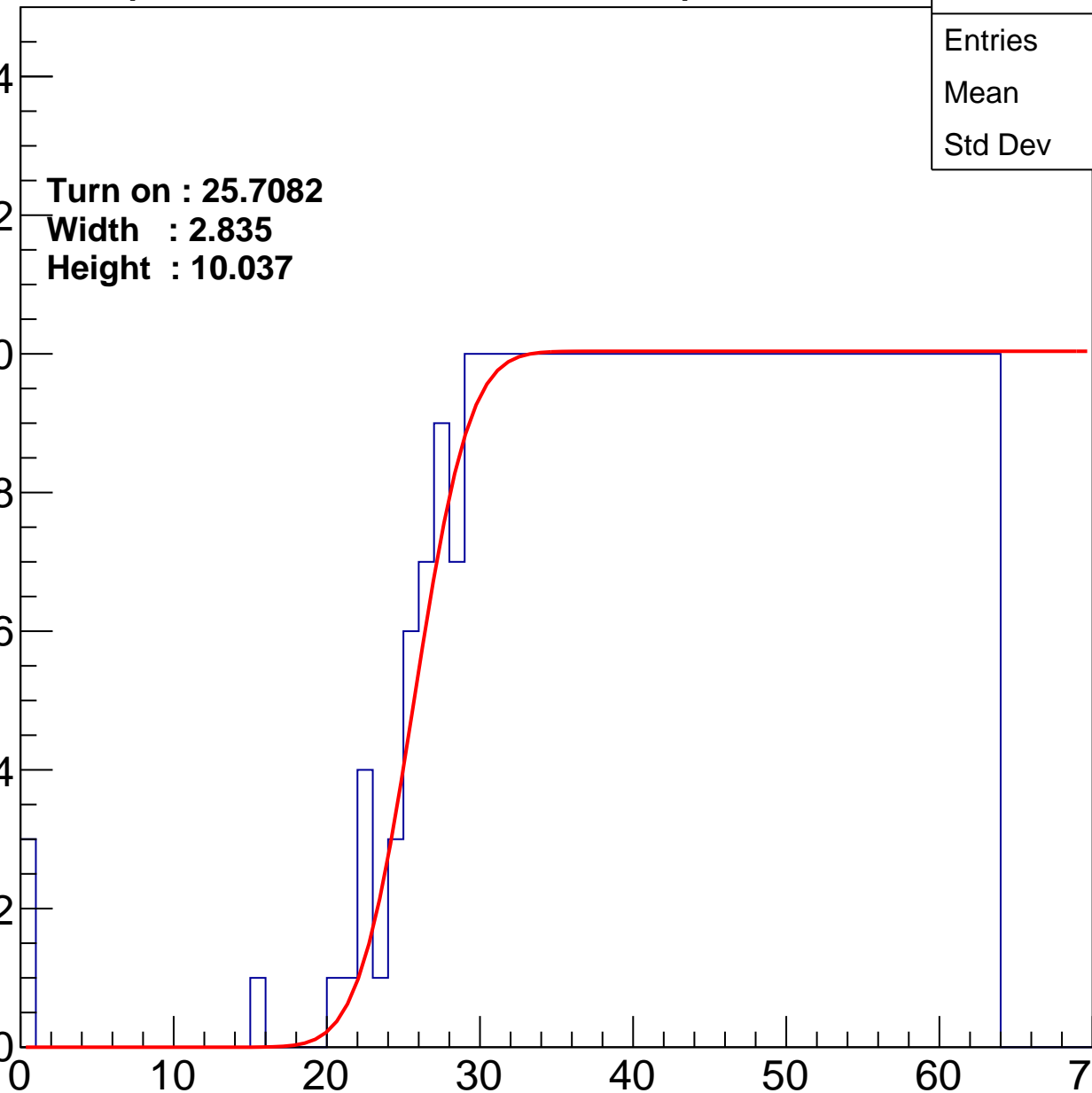
Width : 2.835

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.7948

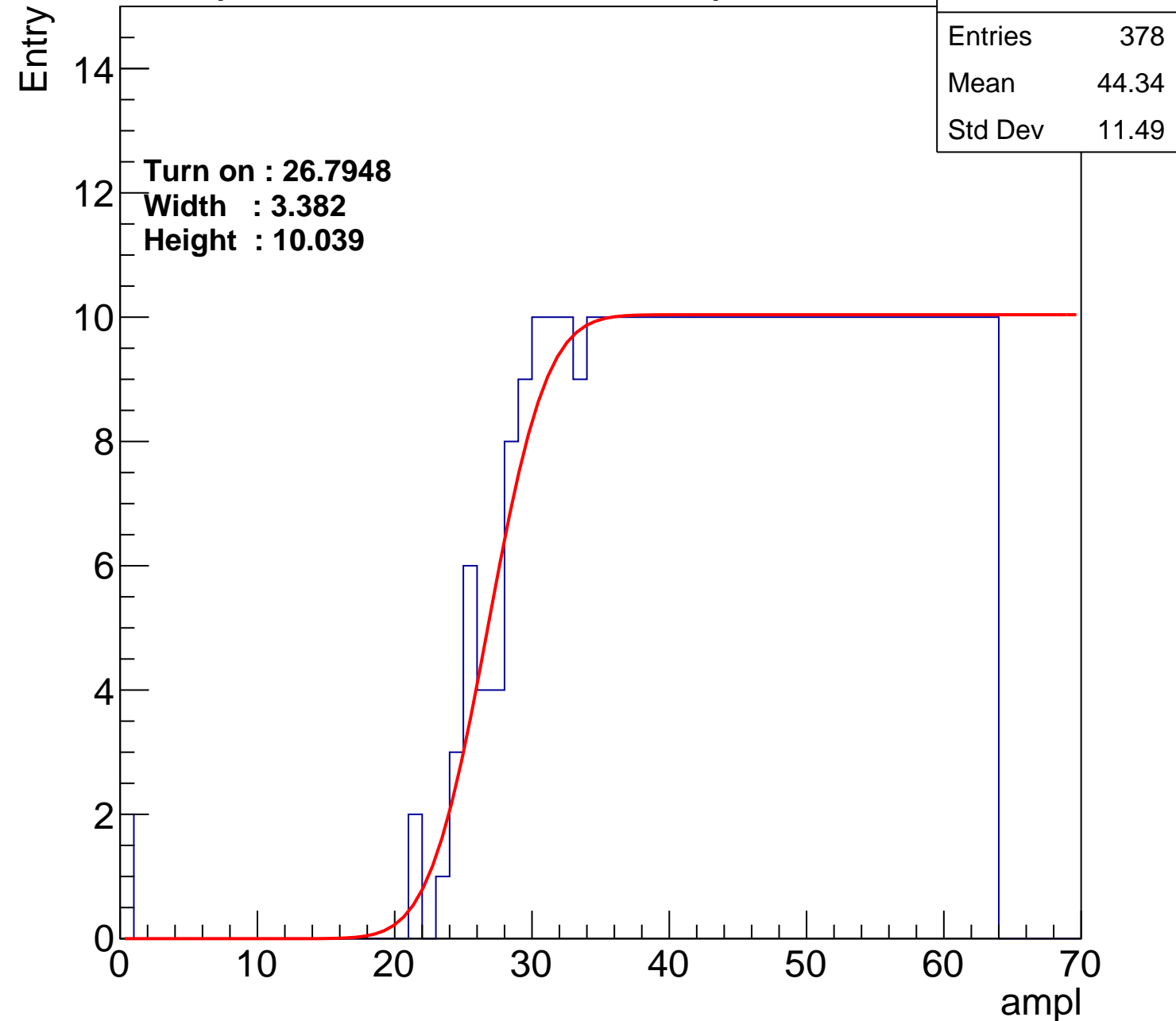
Width : 3.382

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch53

calib_packv5_042523_0143.root, FC#12, port B1

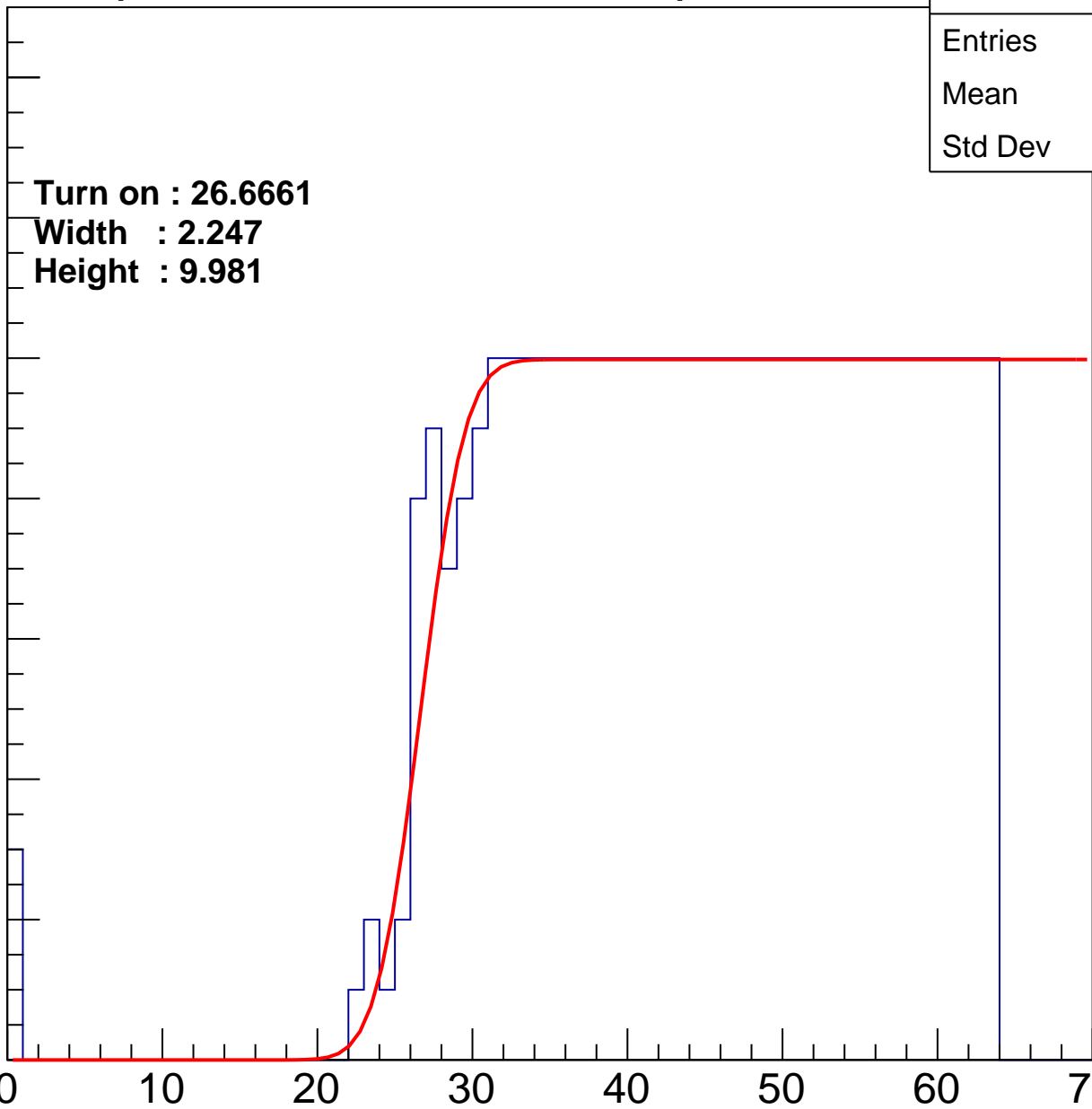
Entry

14
12
10
8
6
4
2
0

Turn on : 26.6661
Width : 2.247
Height : 9.981

Entries	380
Mean	44.21
Std Dev	11.67

ampl



B0L102S, U11-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.25
Std Dev	12.08

Turn on : 24.1170

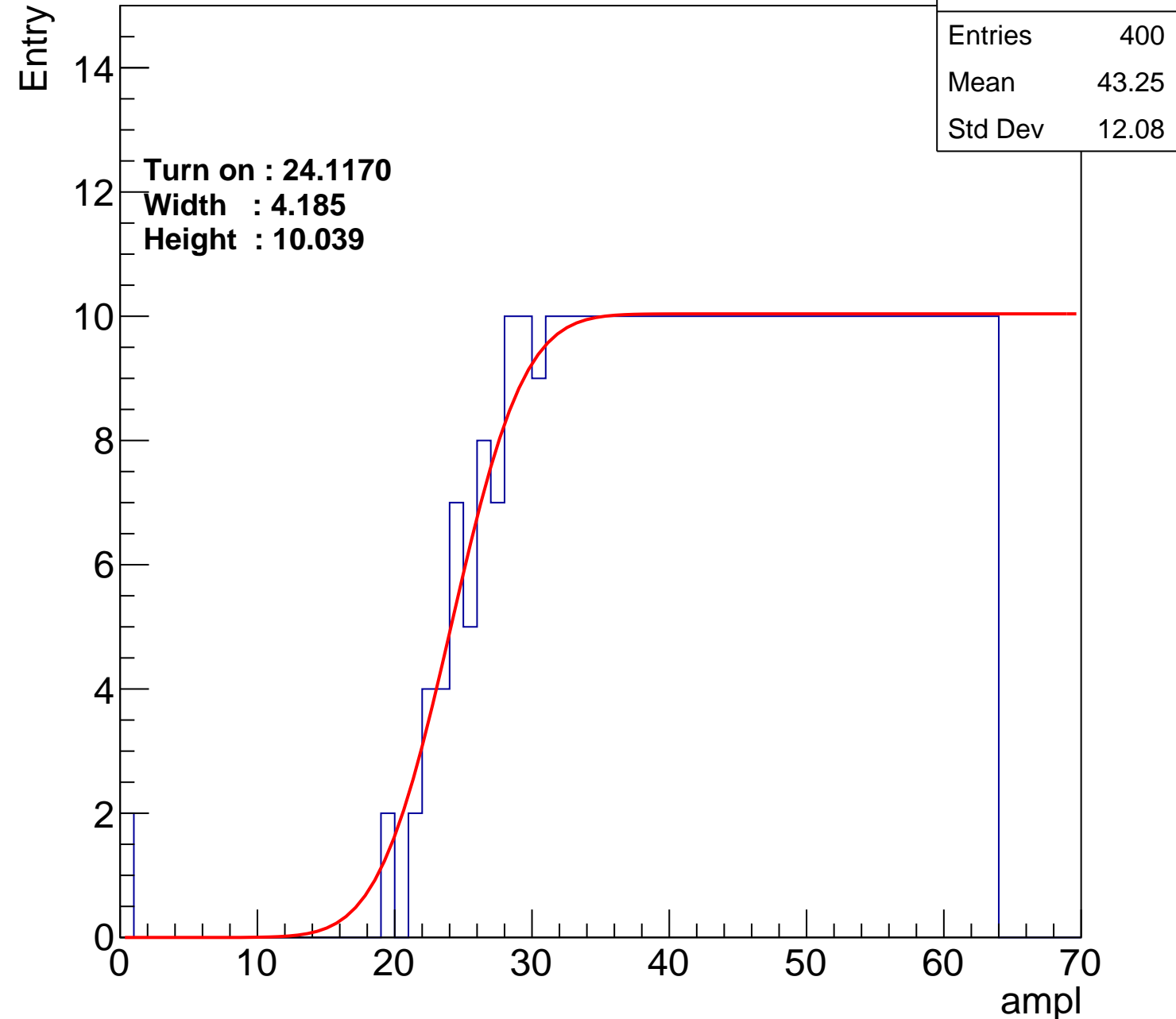
Width : 4.185

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch55

calib_packv5_042523_0143.root, FC#12, port B1

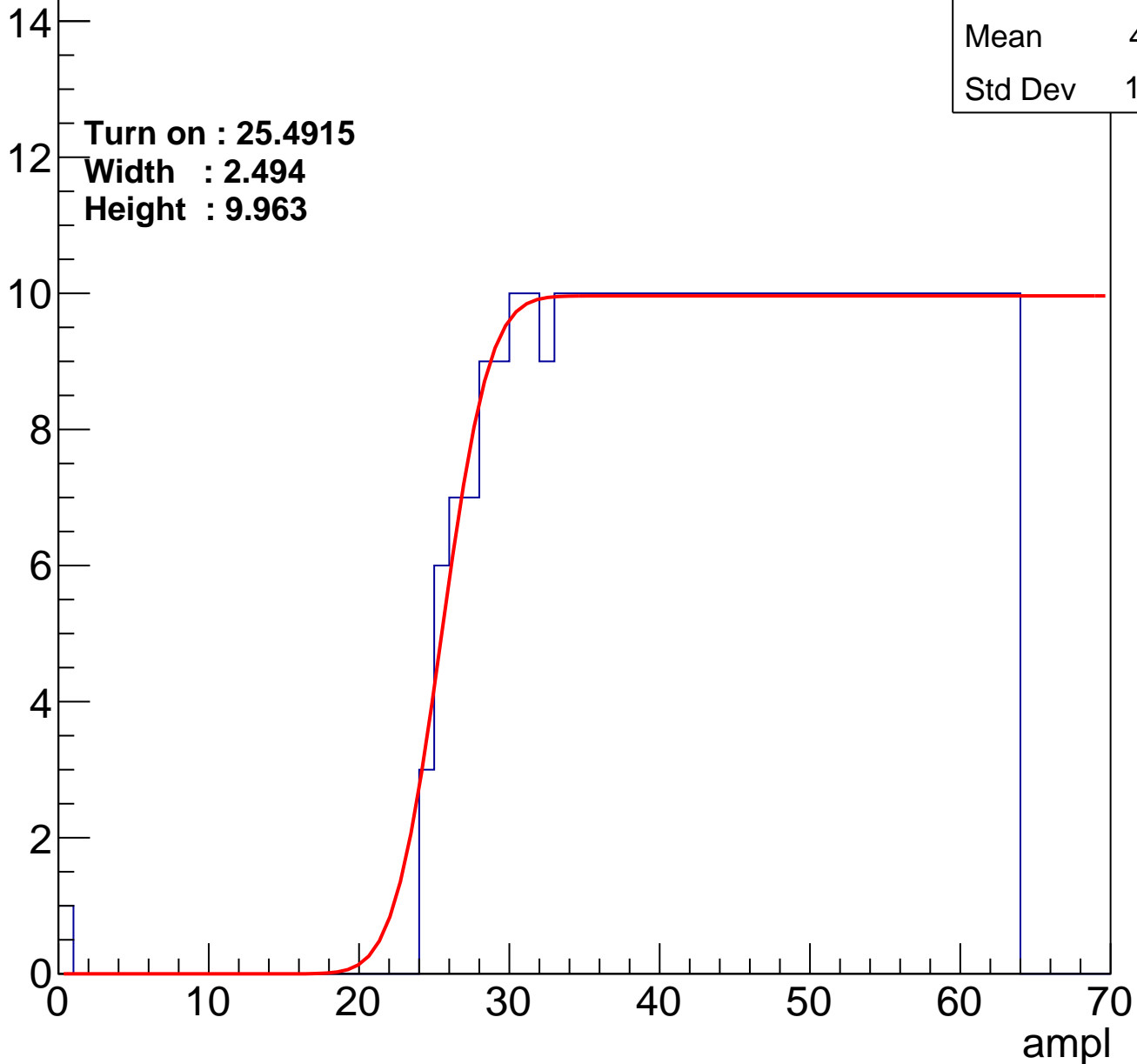
Entries	381
Mean	44.31
Std Dev	11.29

Turn on : 25.4915

Width : 2.494

Height : 9.963

Entry



B0L102S, U11-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.6918

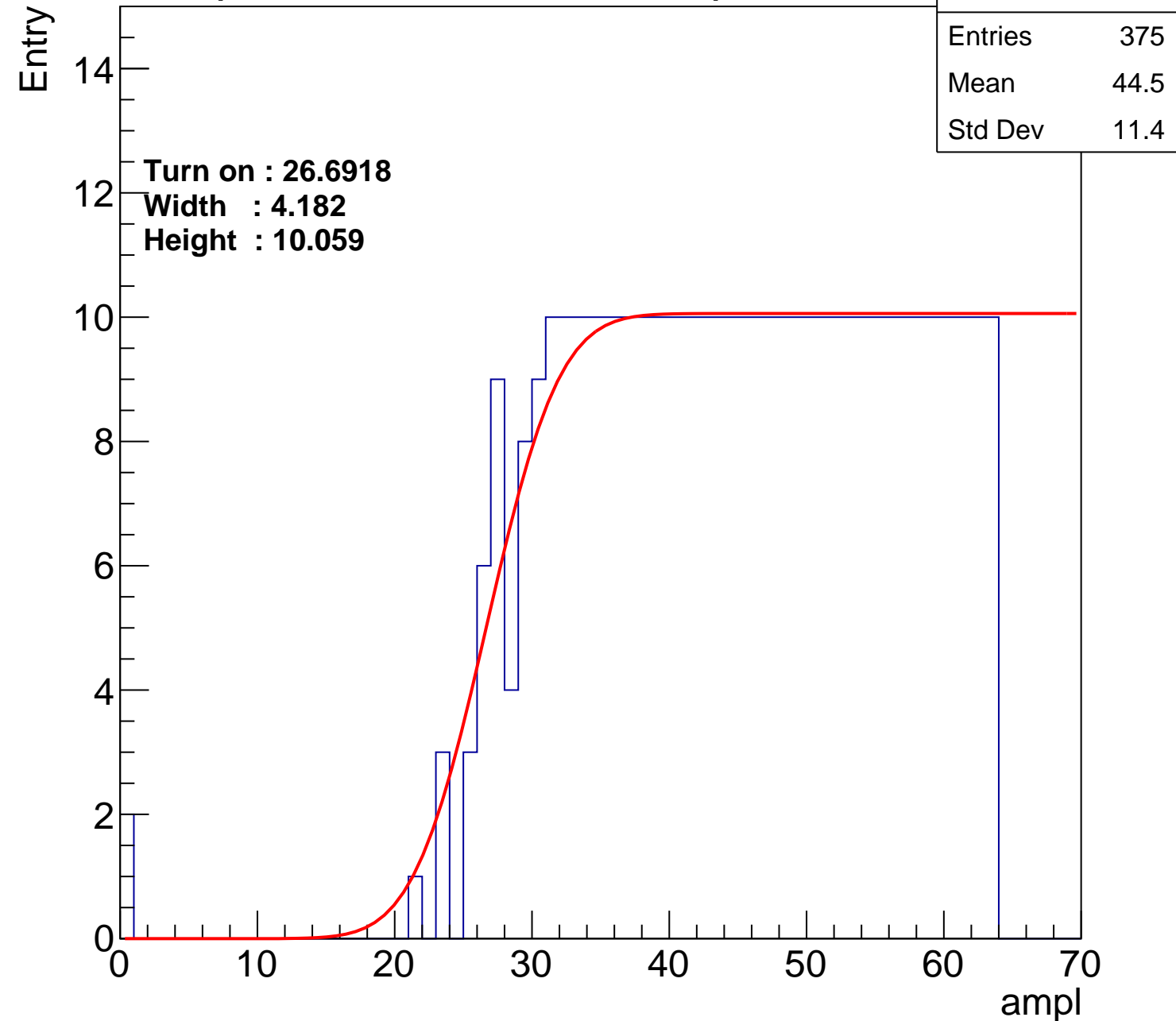
Width : 4.182

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.78
Std Dev	11.71

Turn on : 25.2581

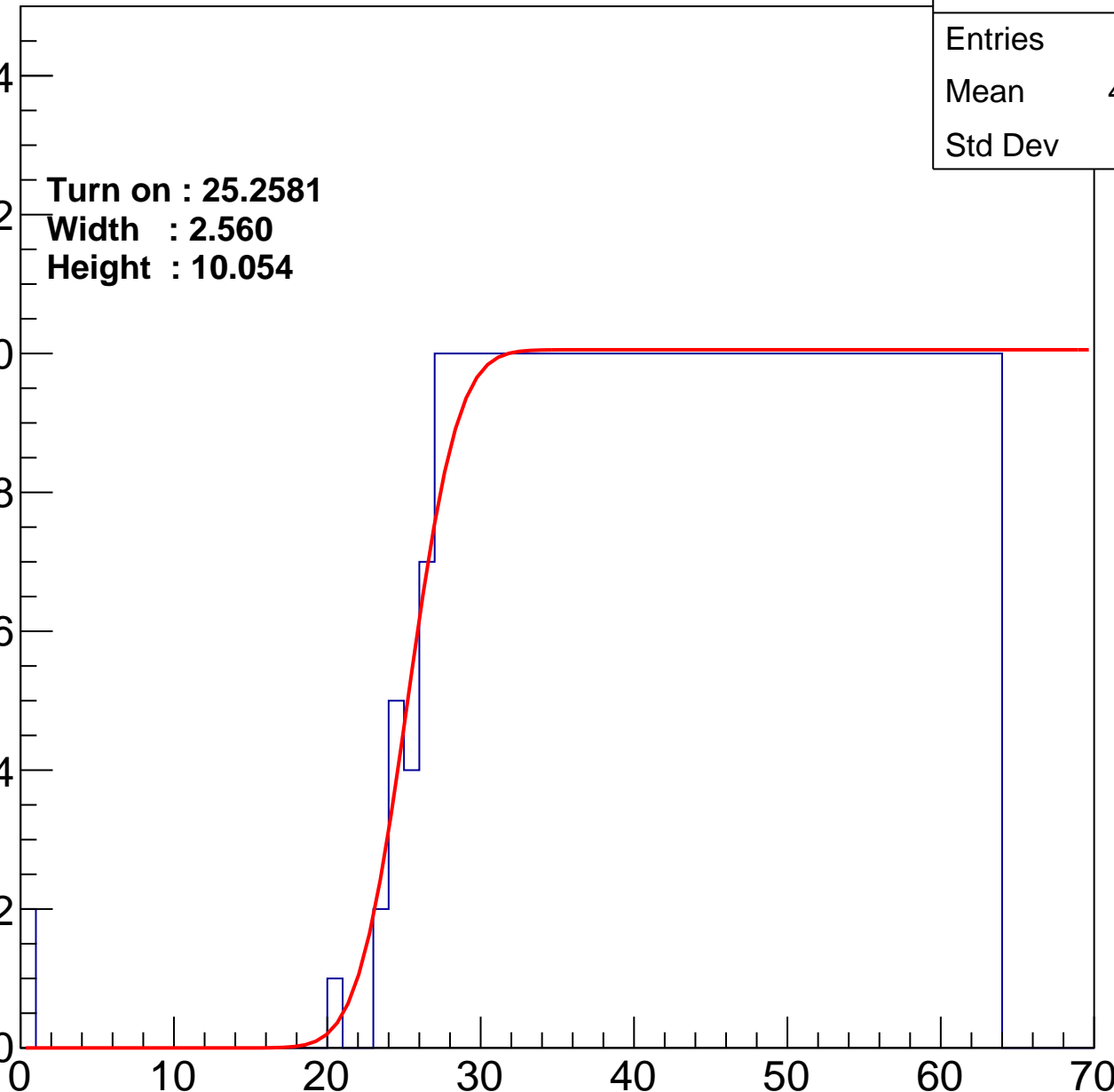
Width : 2.560

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.65
Std Dev	12.11

Turn on : 25.7343

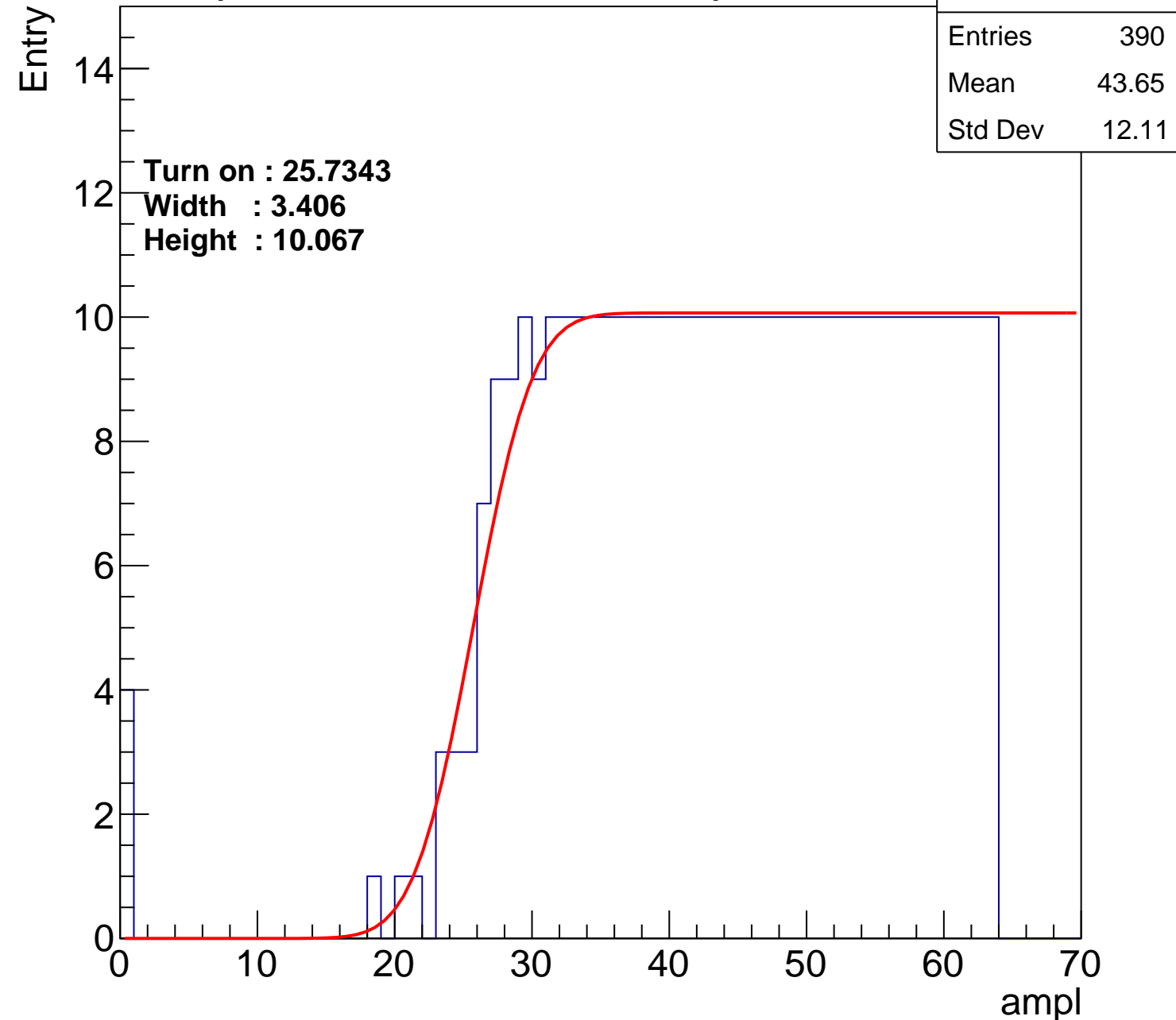
Width : 3.406

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.52
Std Dev	11.61

Turn on : 27.3436

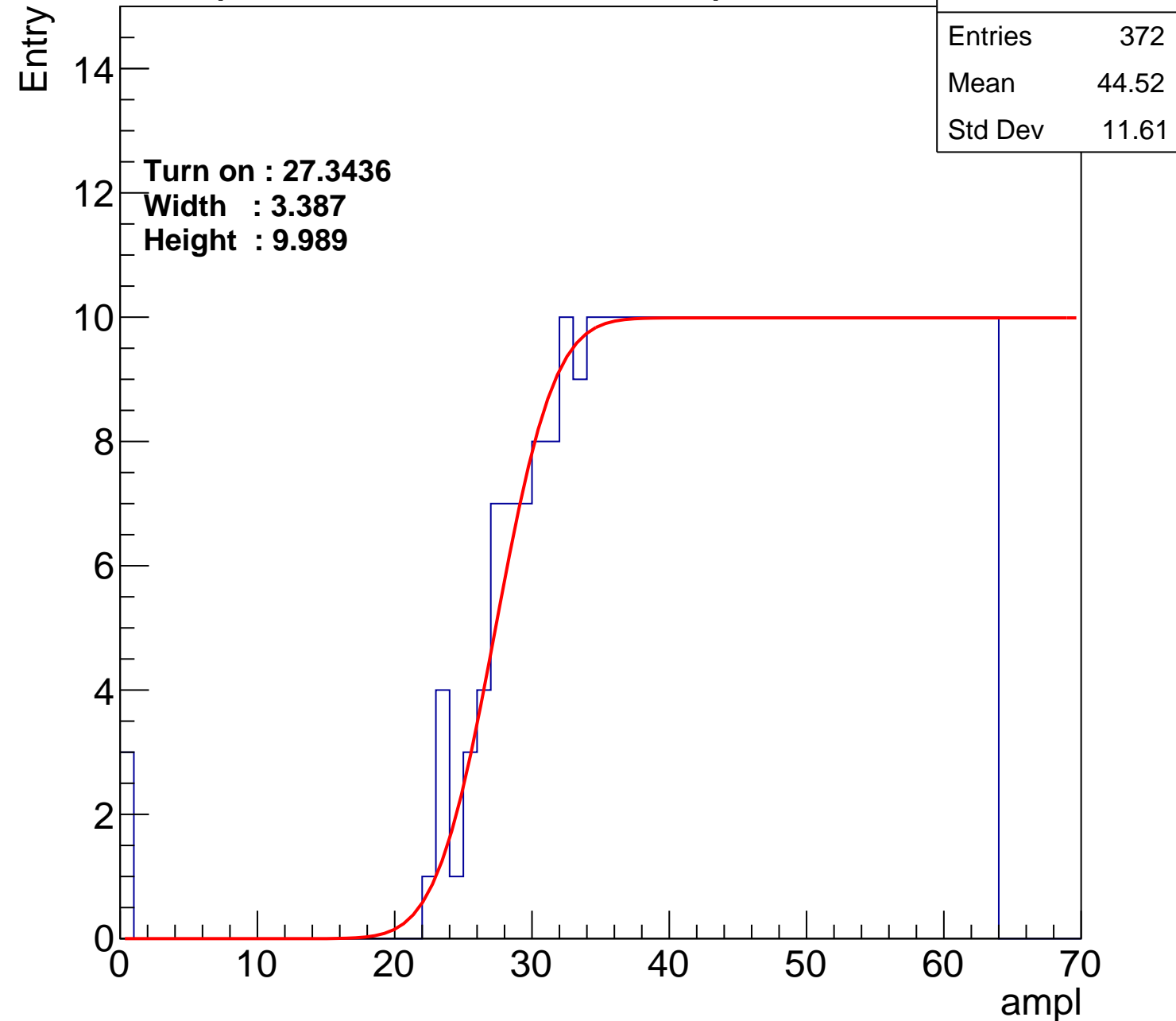
Width : 3.387

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.7414
Width : 3.392
Height : 9.958



B0L102S, U11-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.48
Std Dev	11.25

Turn on : 27.0242

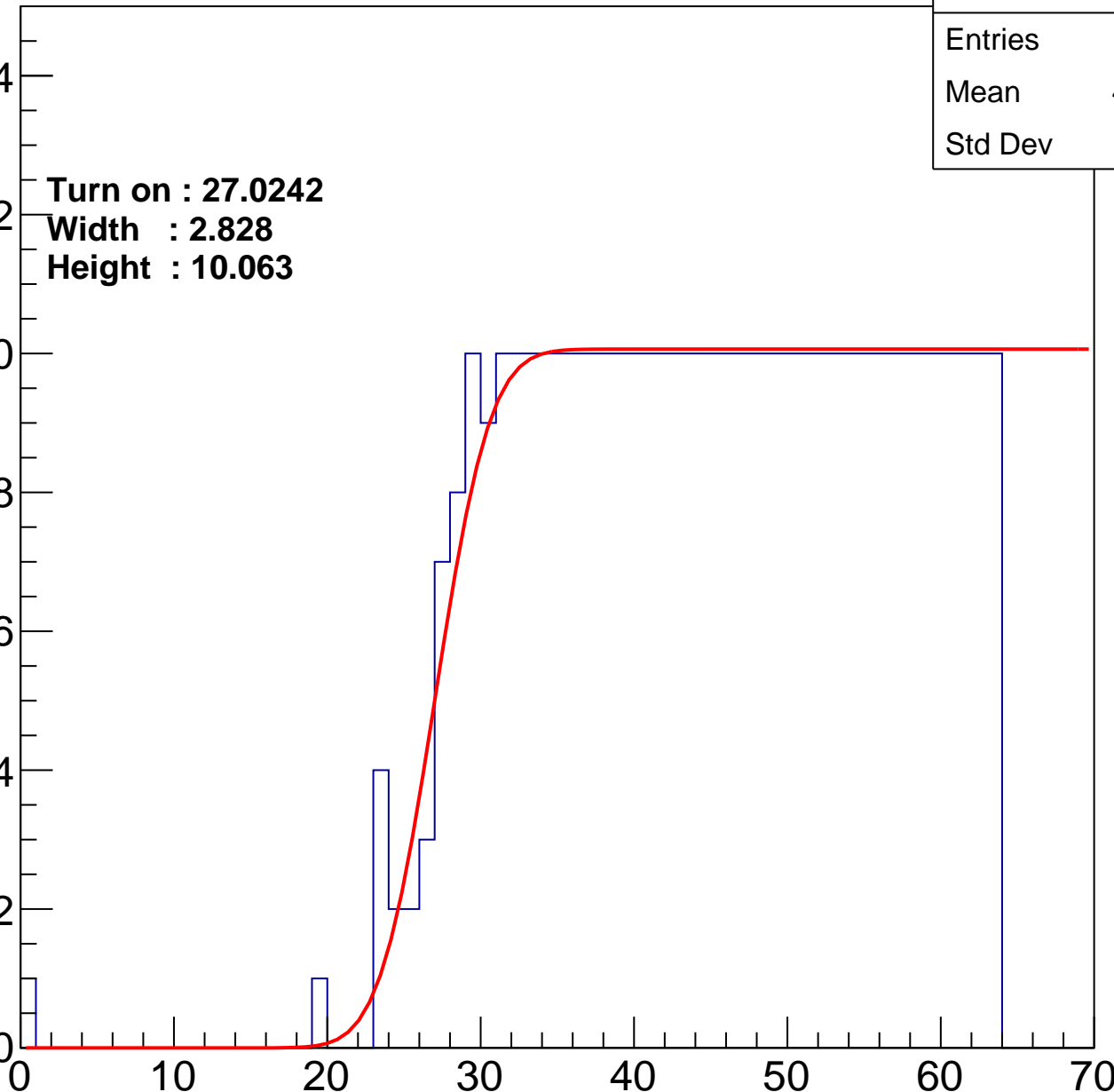
Width : 2.828

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.62
Std Dev	11.96

Turn on : 25.3715

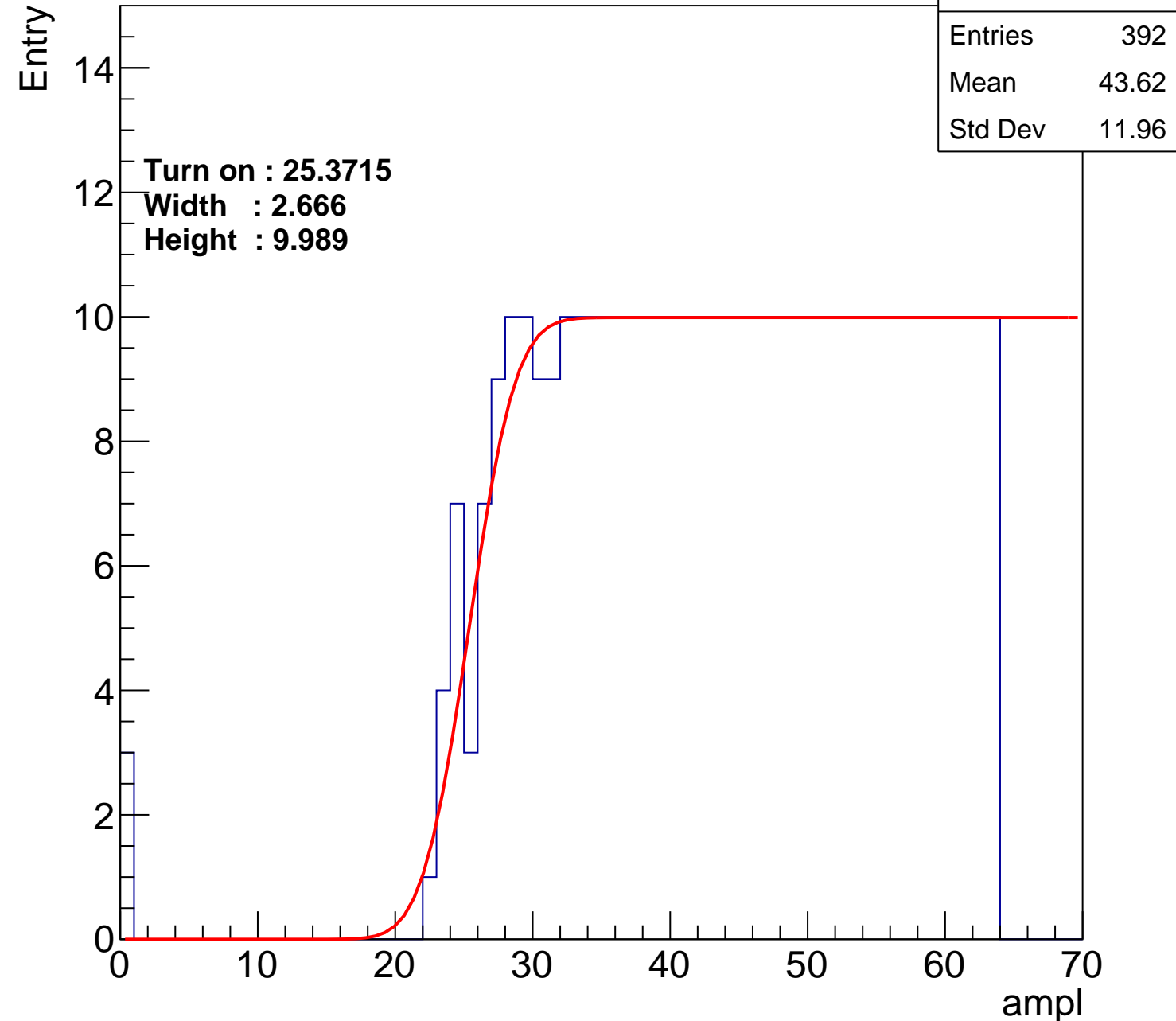
Width : 2.666

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.1275

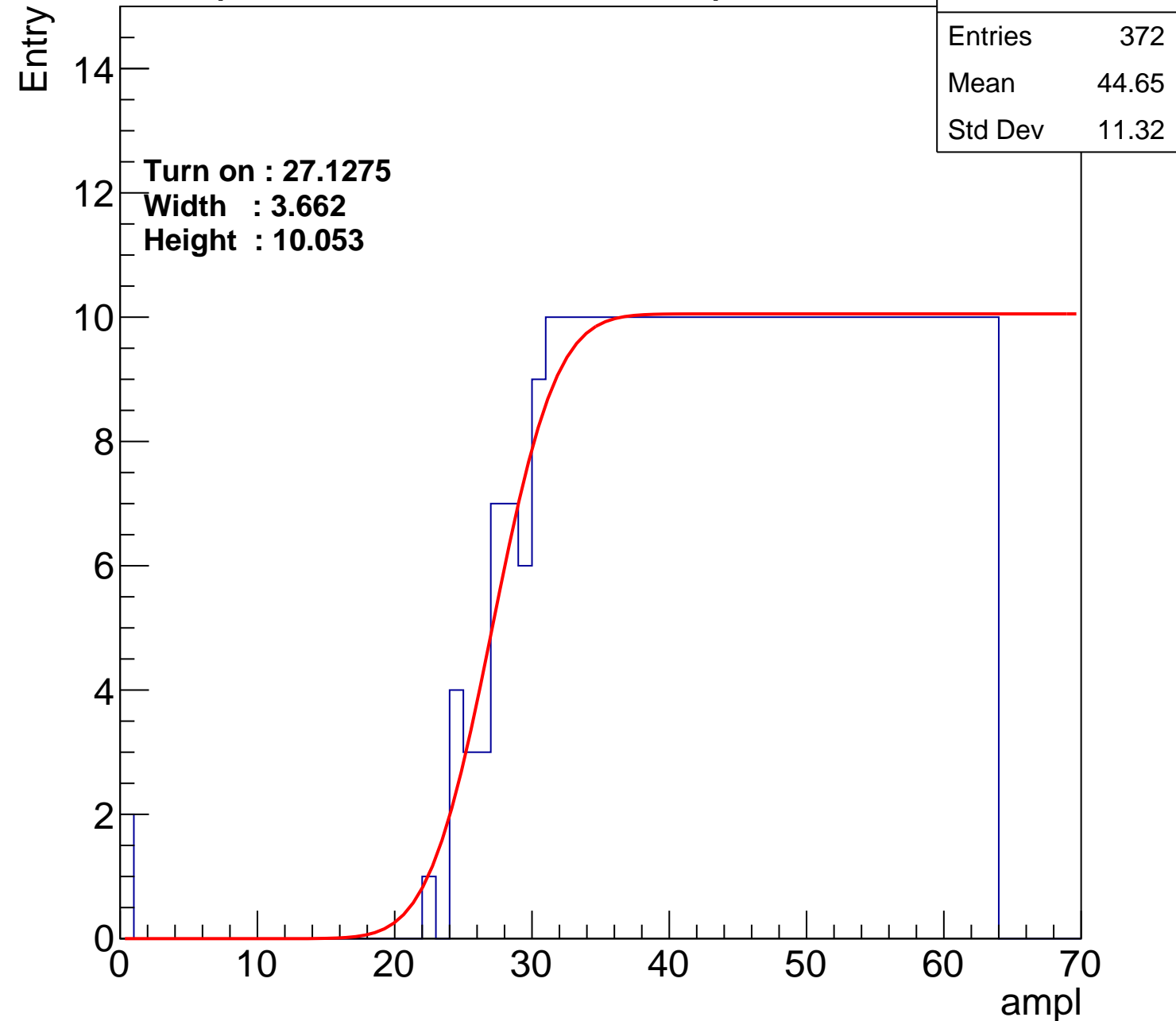
Width : 3.662

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 27.8295

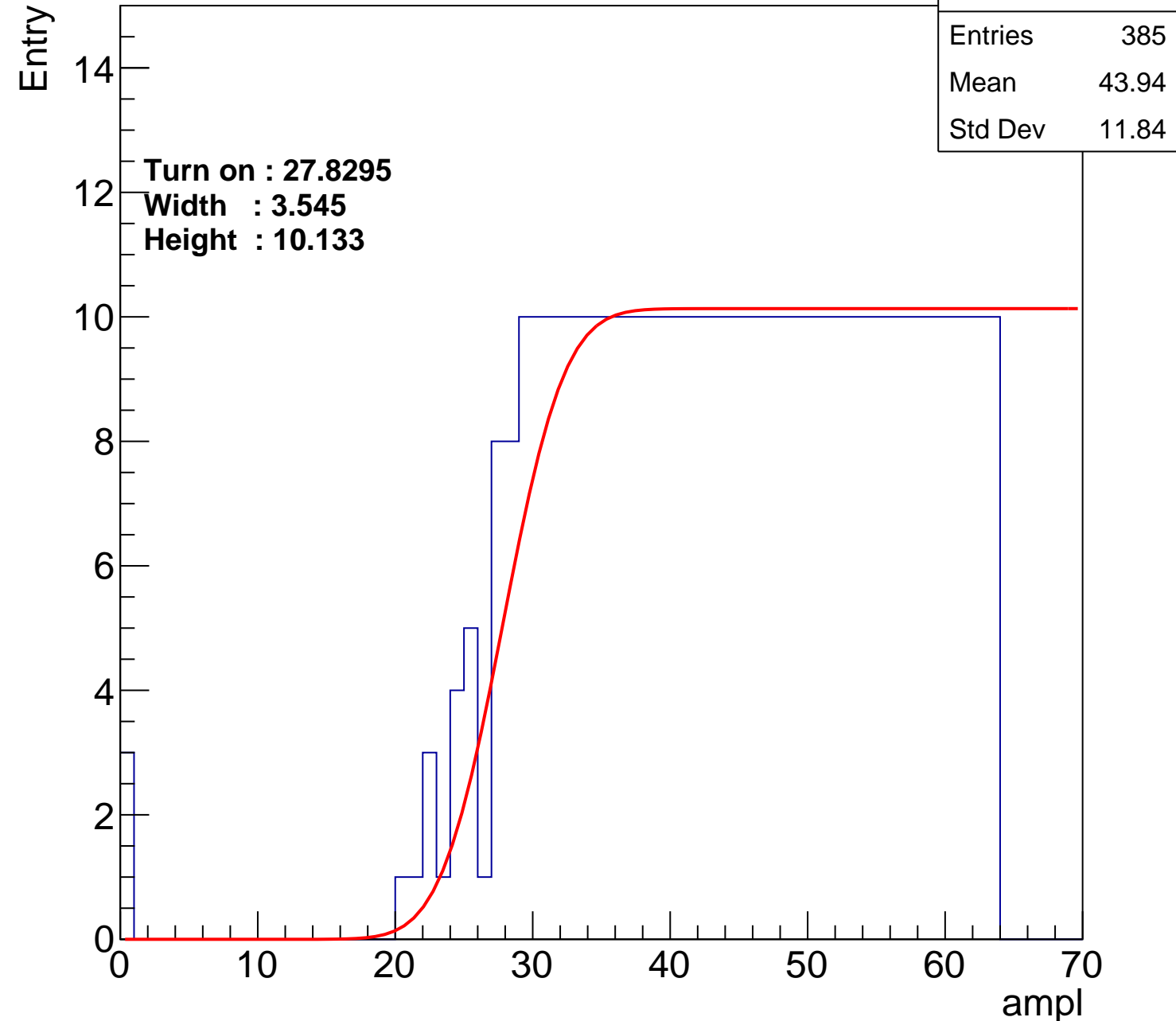
Width : 3.545

Height : 10.133

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.42
Std Dev	11.41

Turn on : 26.6460

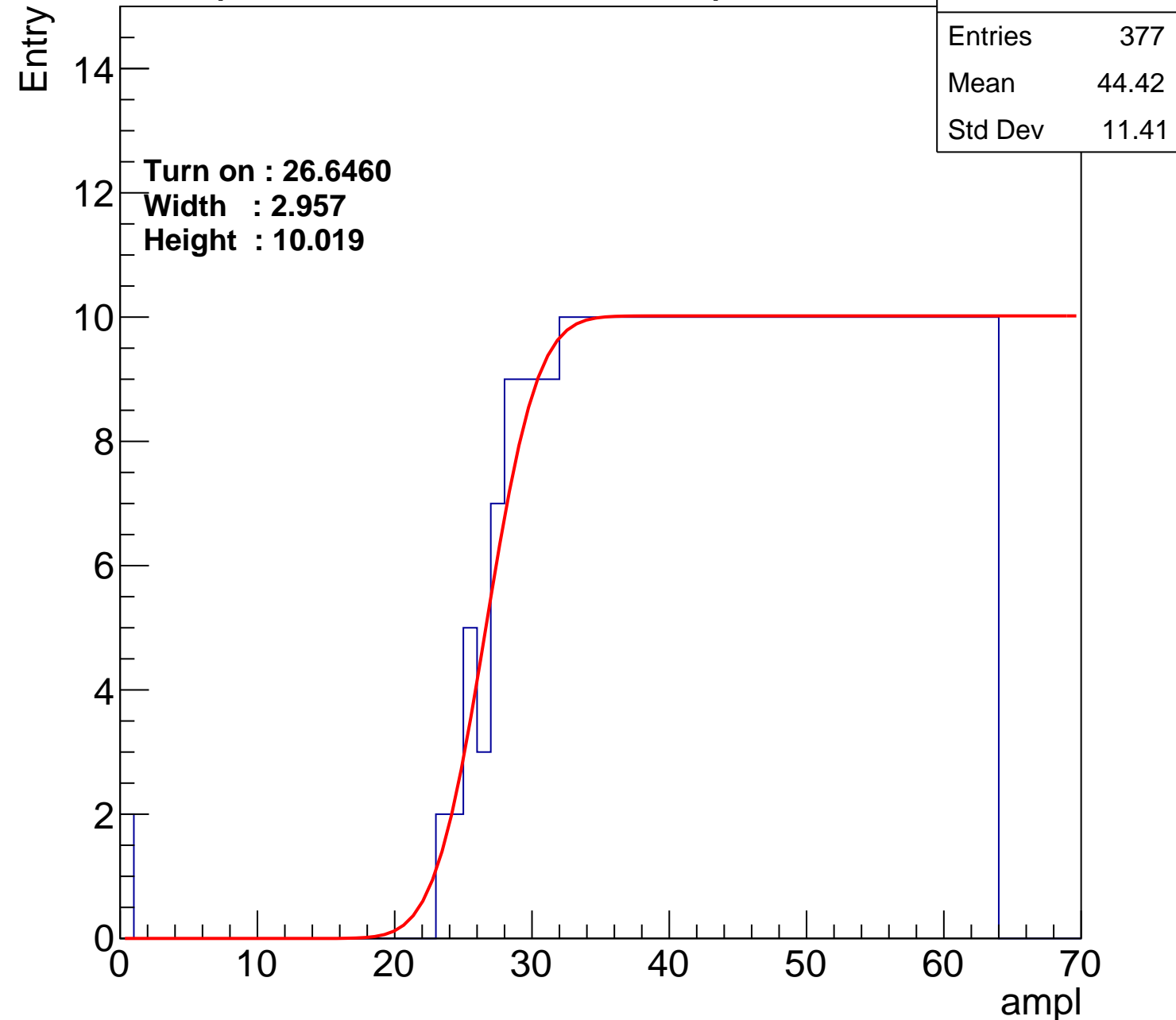
Width : 2.957

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.45
Std Dev	11.97

Turn on : 24.7048

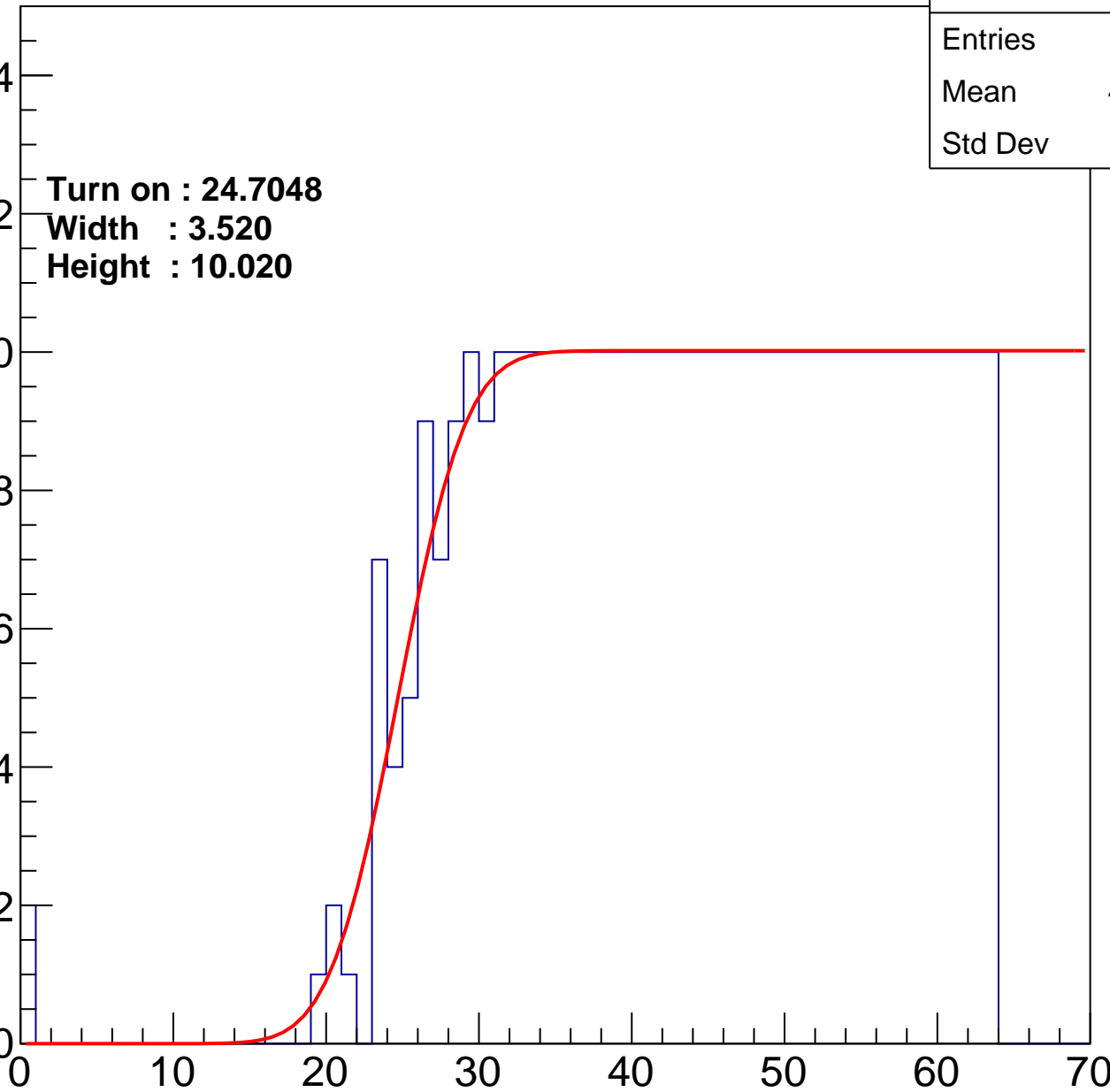
Width : 3.520

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.96
Std Dev	11.87

Turn on : 26.0760

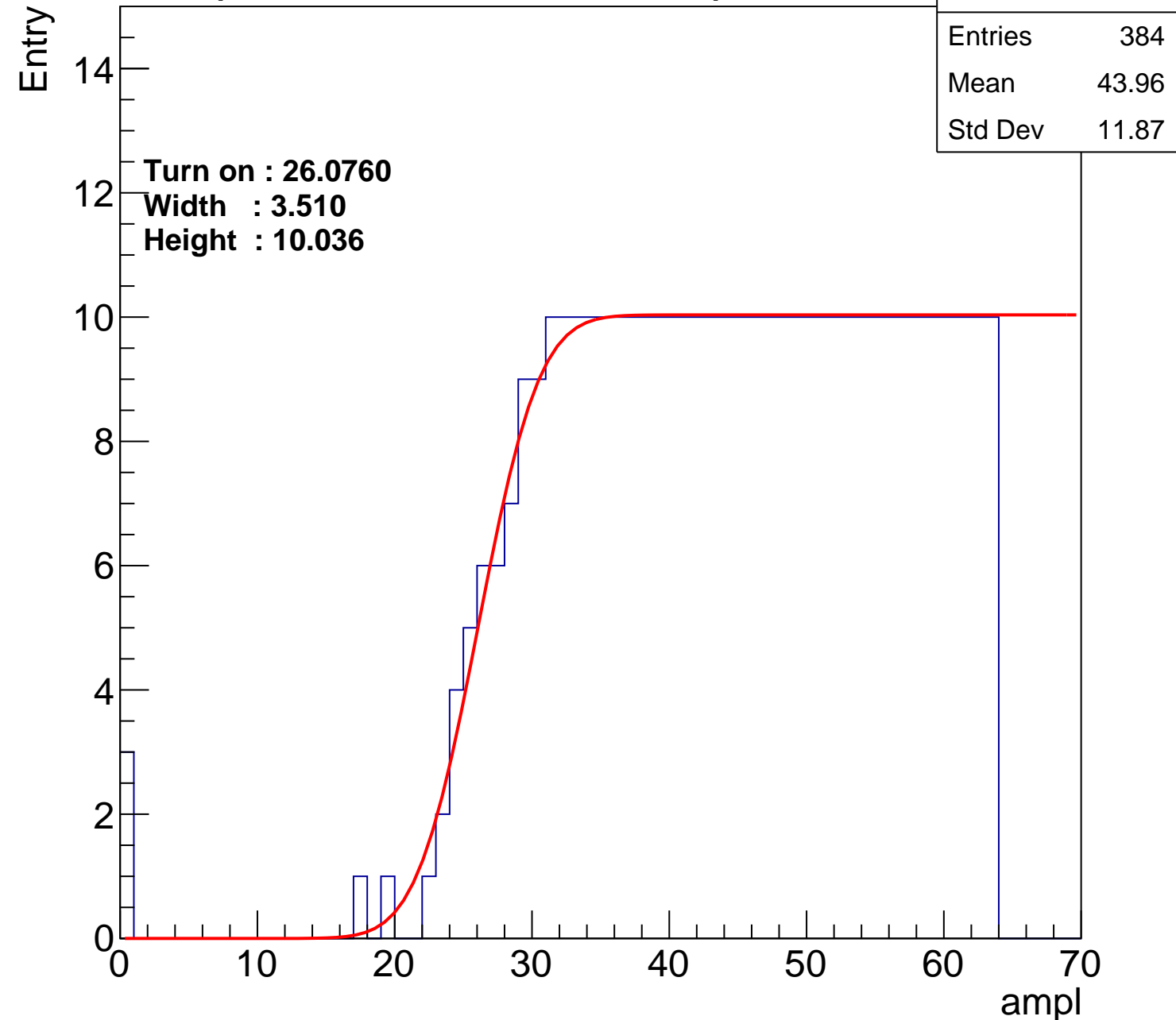
Width : 3.510

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.41
Std Dev	12.32

Turn on : 25.3238

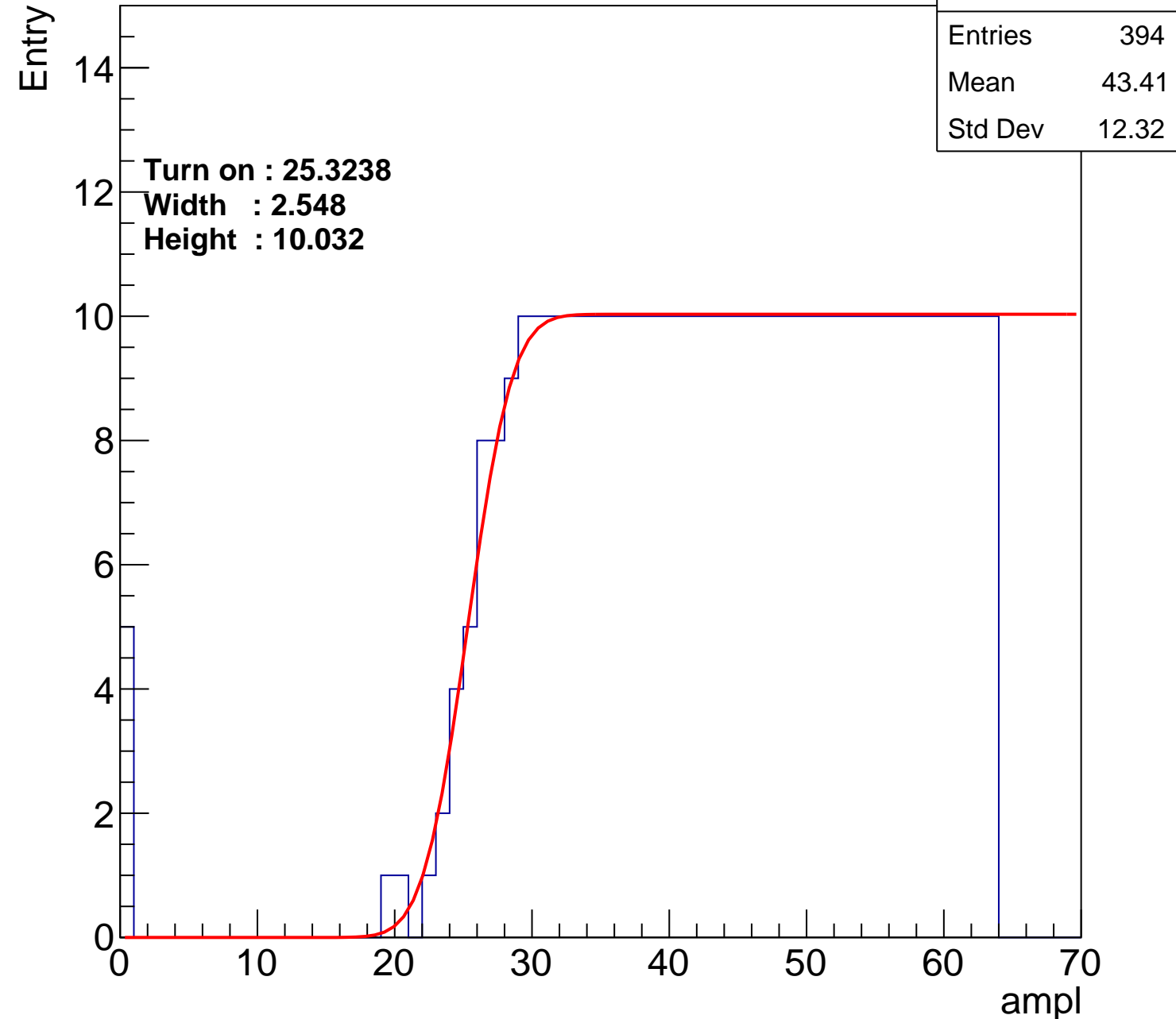
Width : 2.548

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.3125

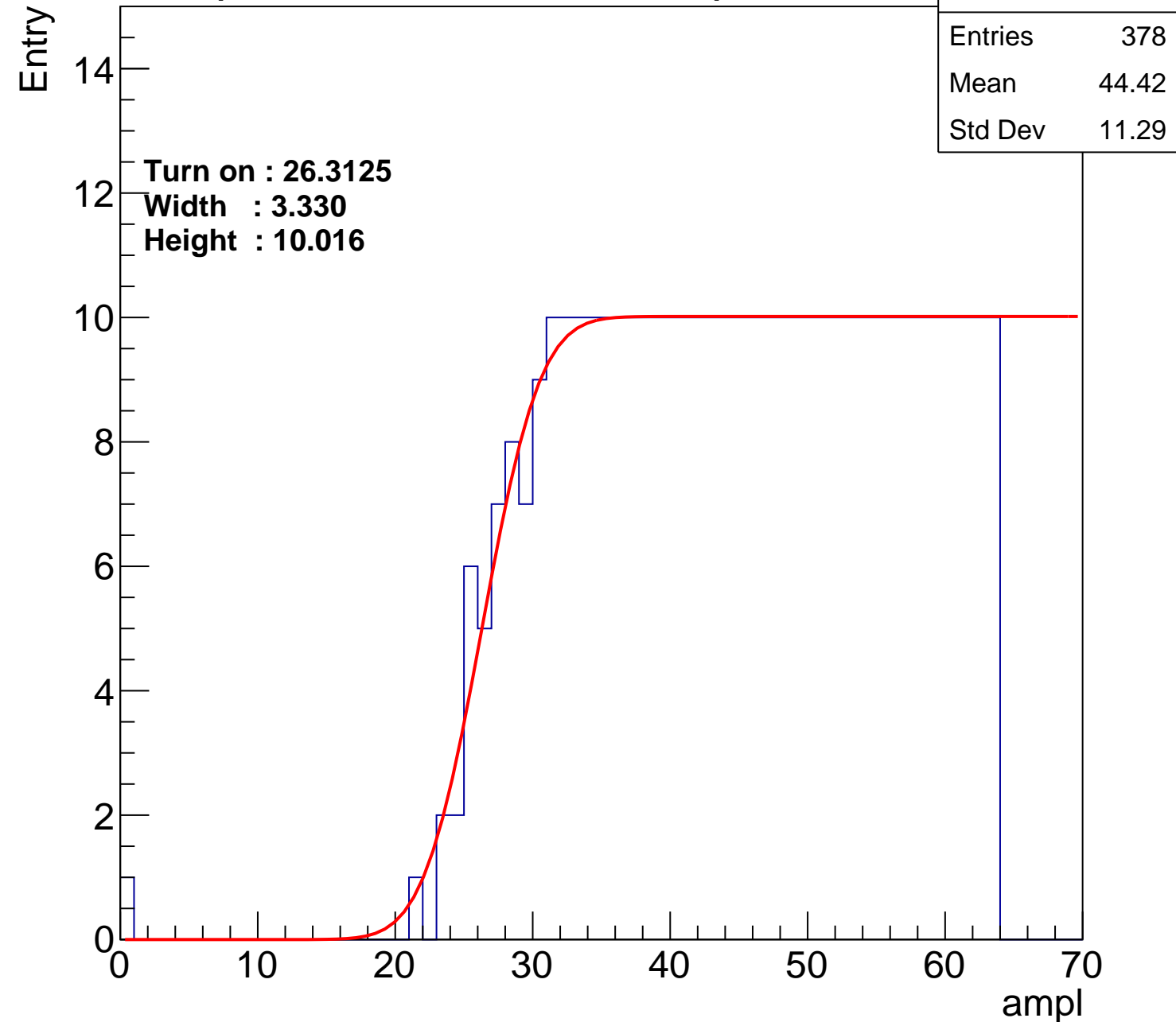
Width : 3.330

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.81
Std Dev	12.01

Turn on : 25.5372

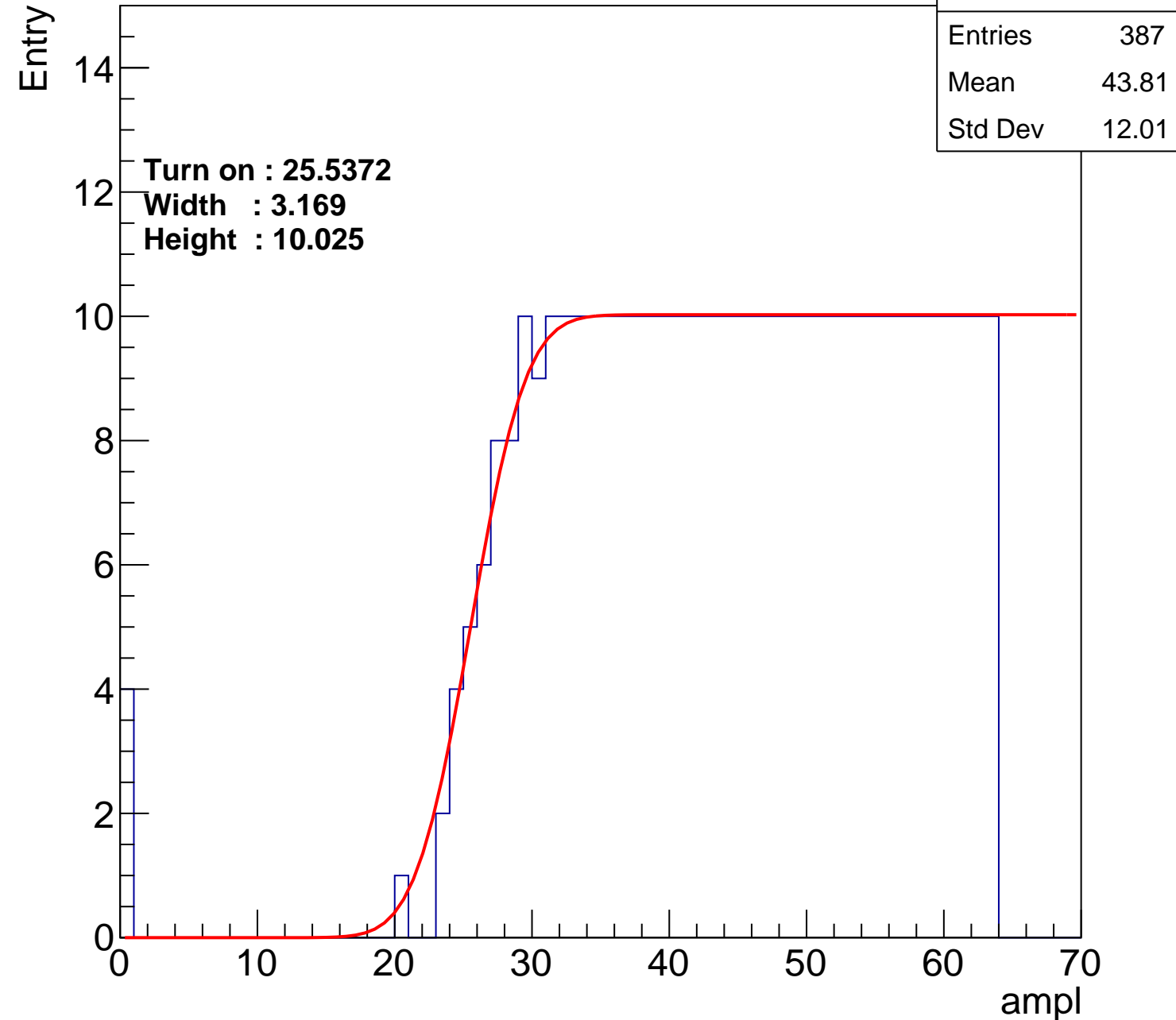
Width : 3.169

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.03
Std Dev	11.01

Turn on : 28.1448

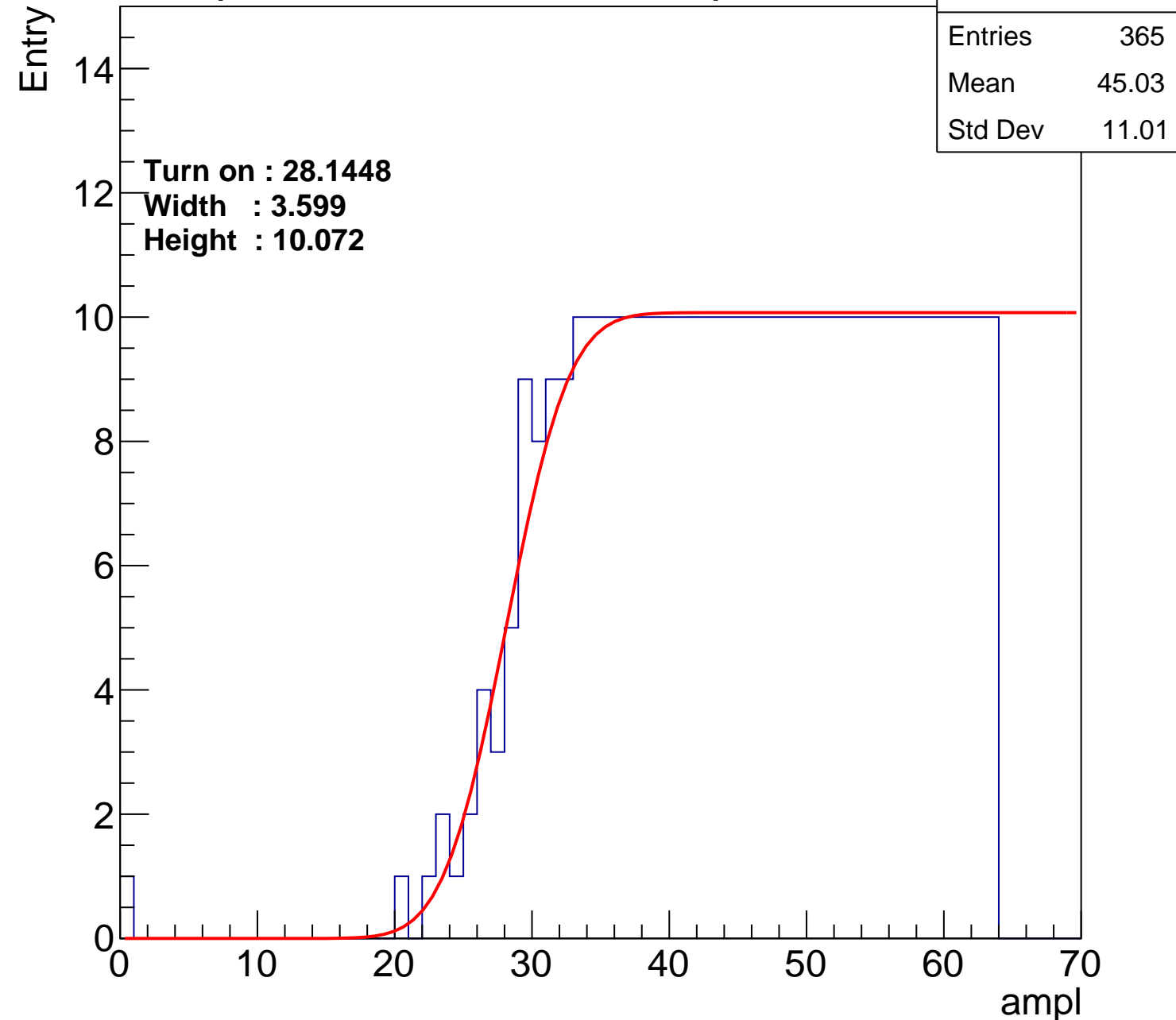
Width : 3.599

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.76
Std Dev	11.68

Turn on : 25.1665

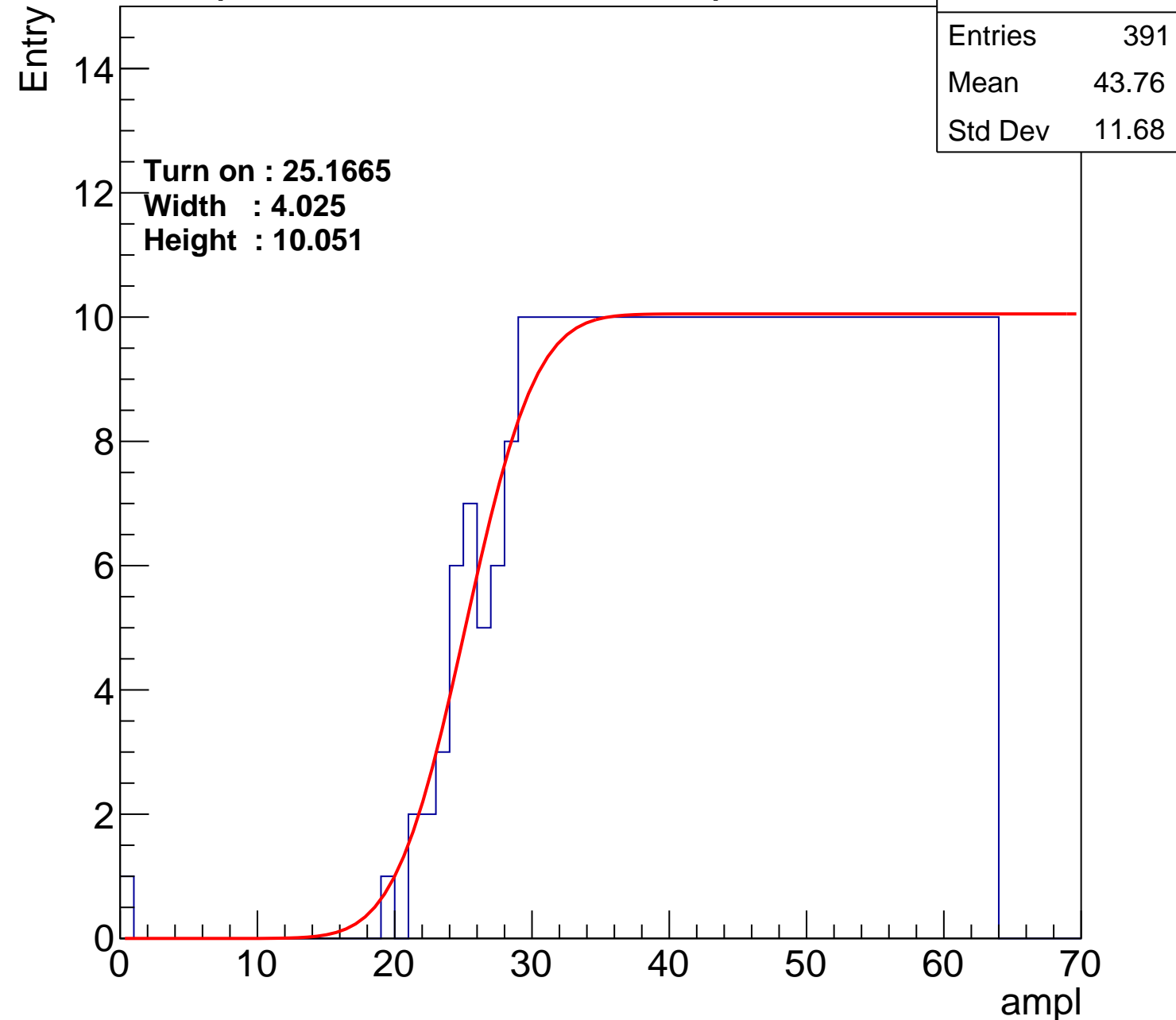
Width : 4.025

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.86
Std Dev	11.25

Turn on : 27.7136

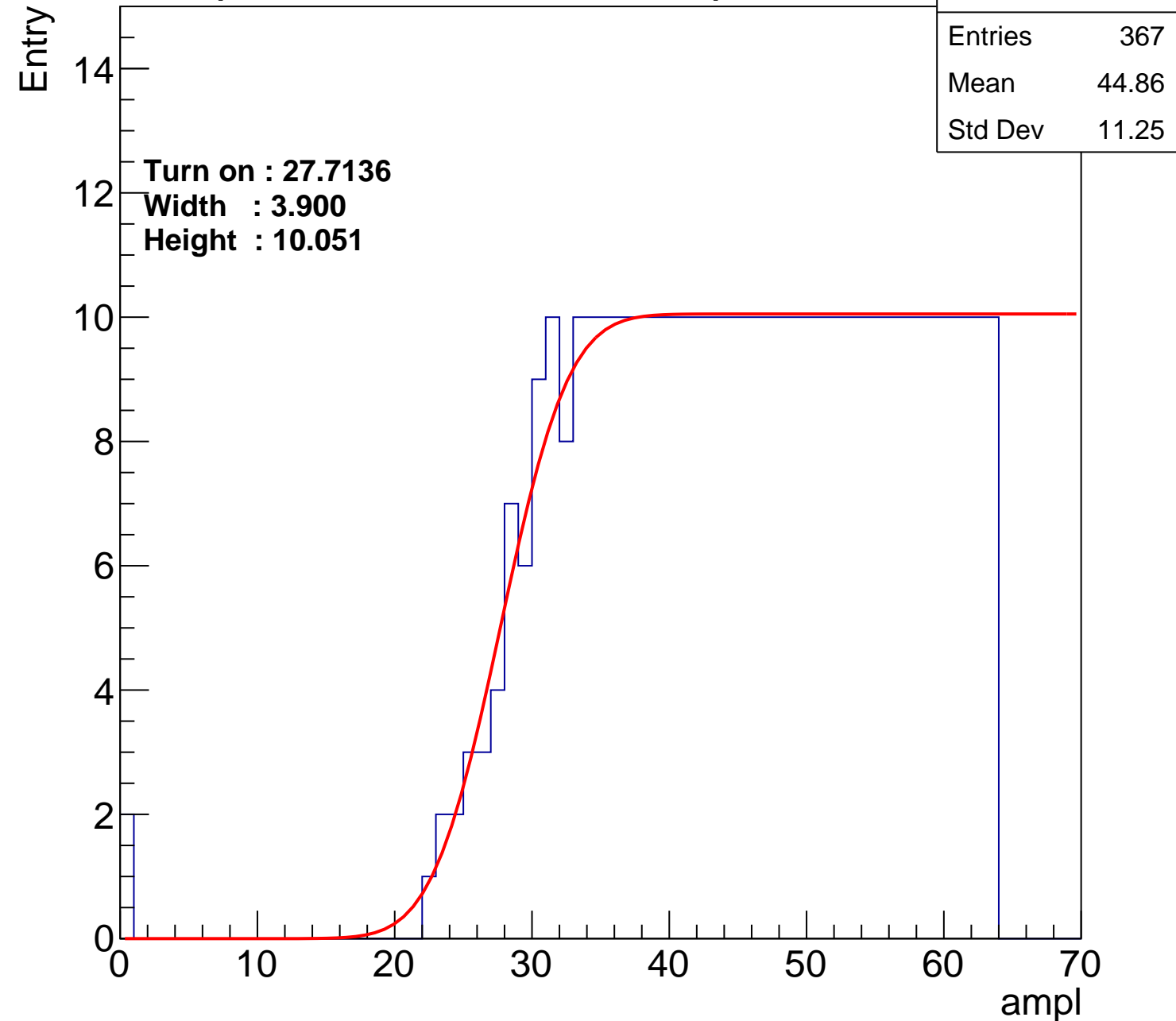
Width : 3.900

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.36
Std Dev	12.01

Turn on : 25.4067

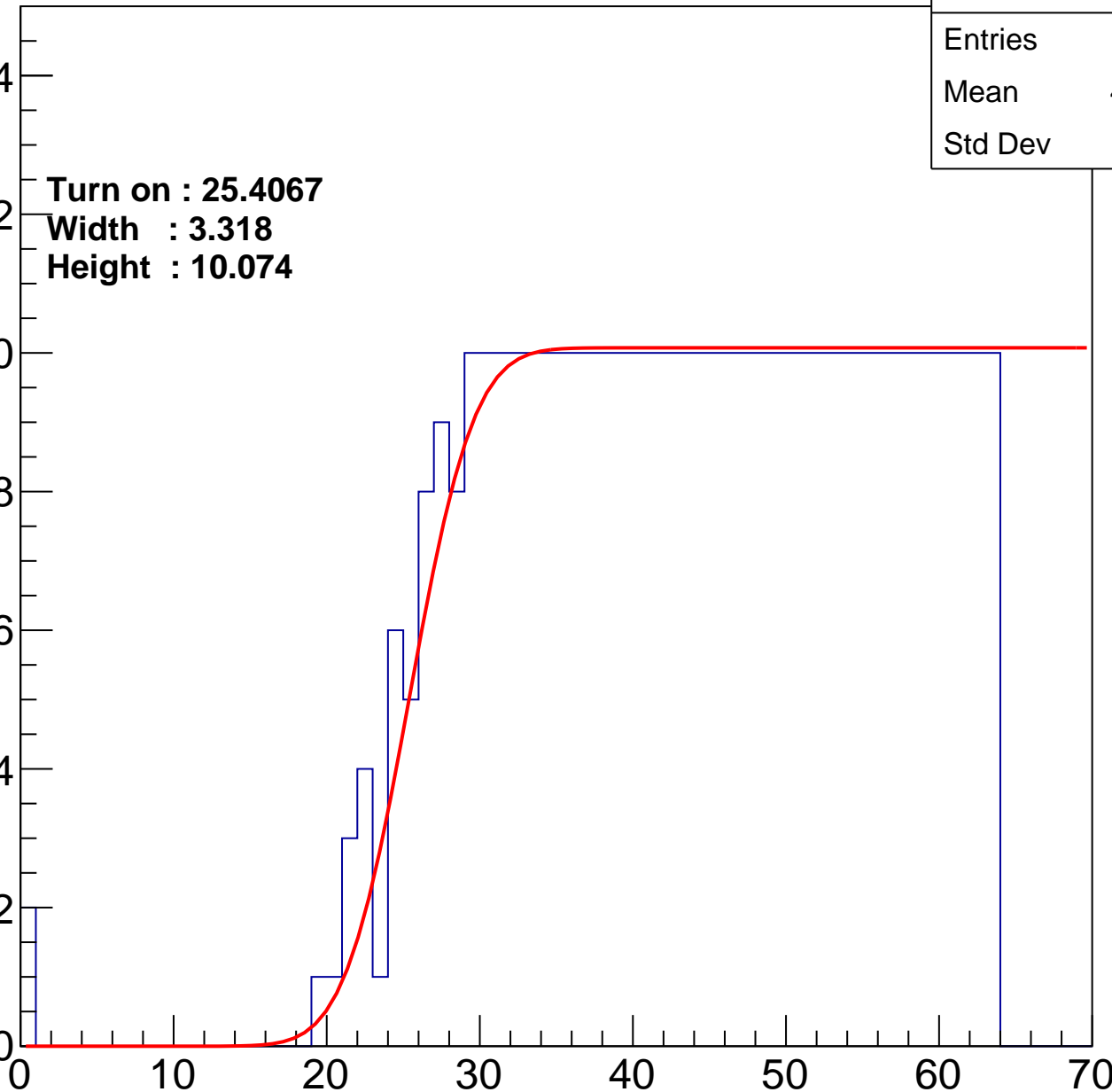
Width : 3.318

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.04
Std Dev	11.83

Turn on : 25.9269

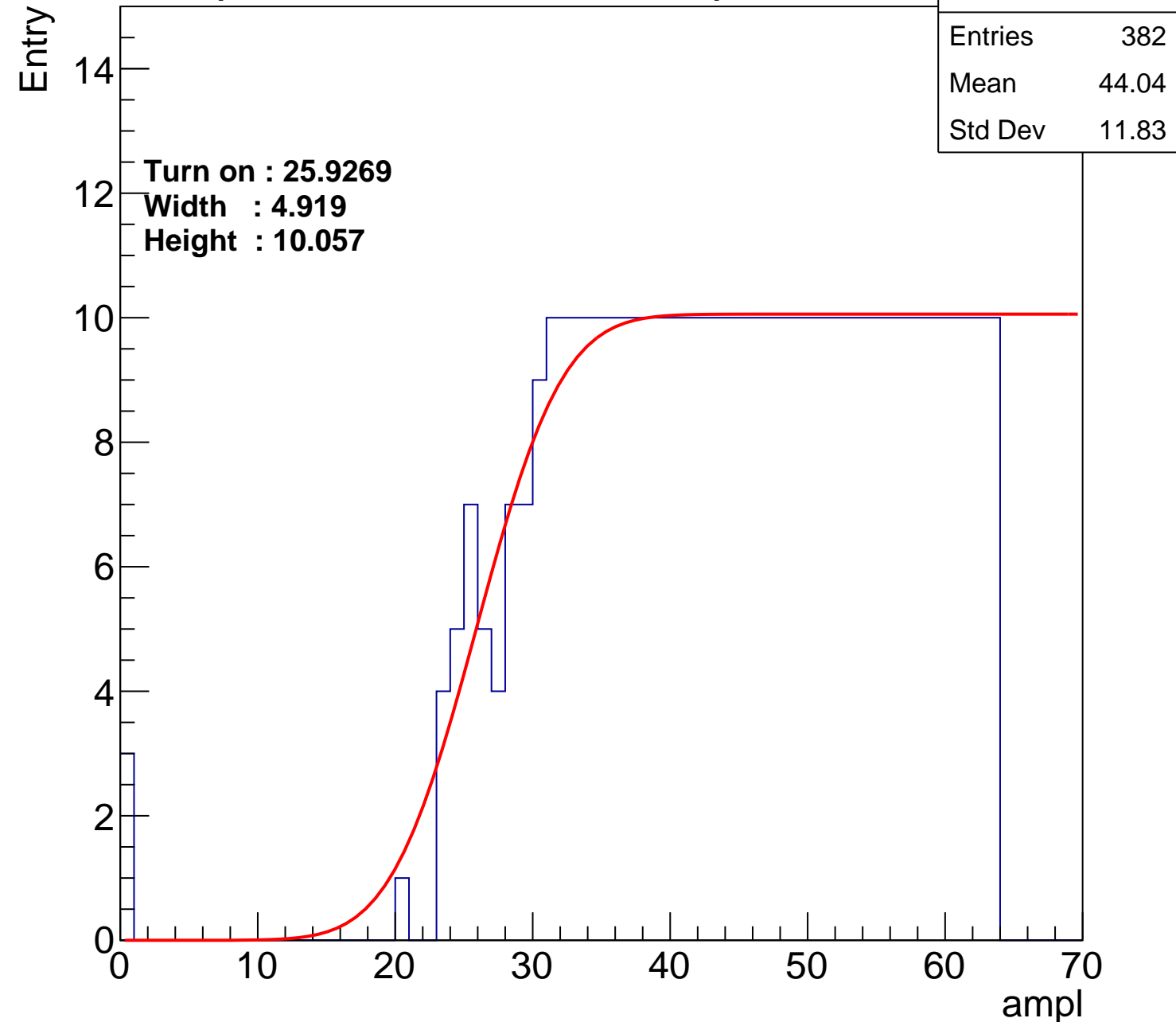
Width : 4.919

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.26
Std Dev	12.06

Turn on : 24.7731

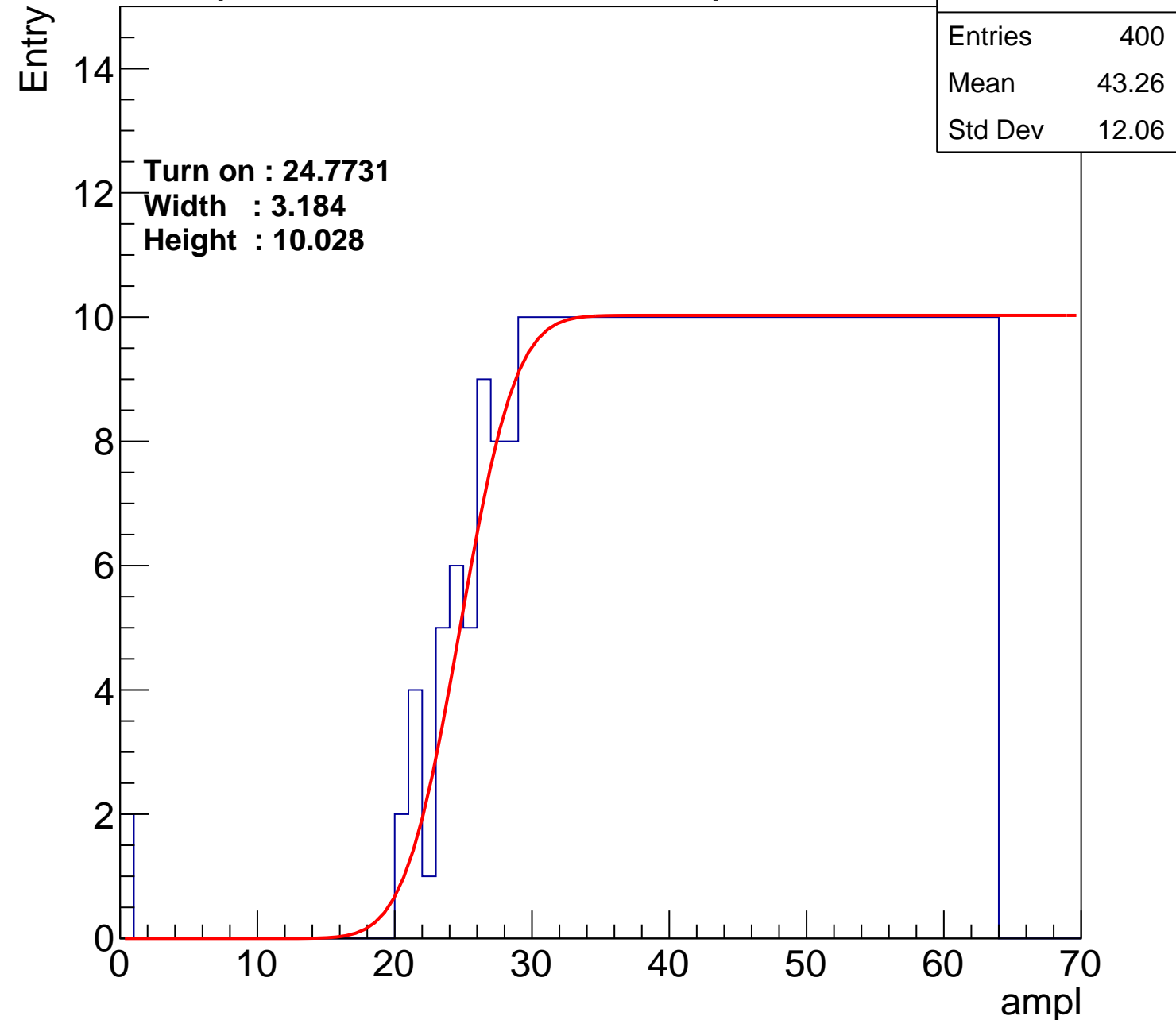
Width : 3.184

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.31
Std Dev	11.43

Turn on : 26.6621

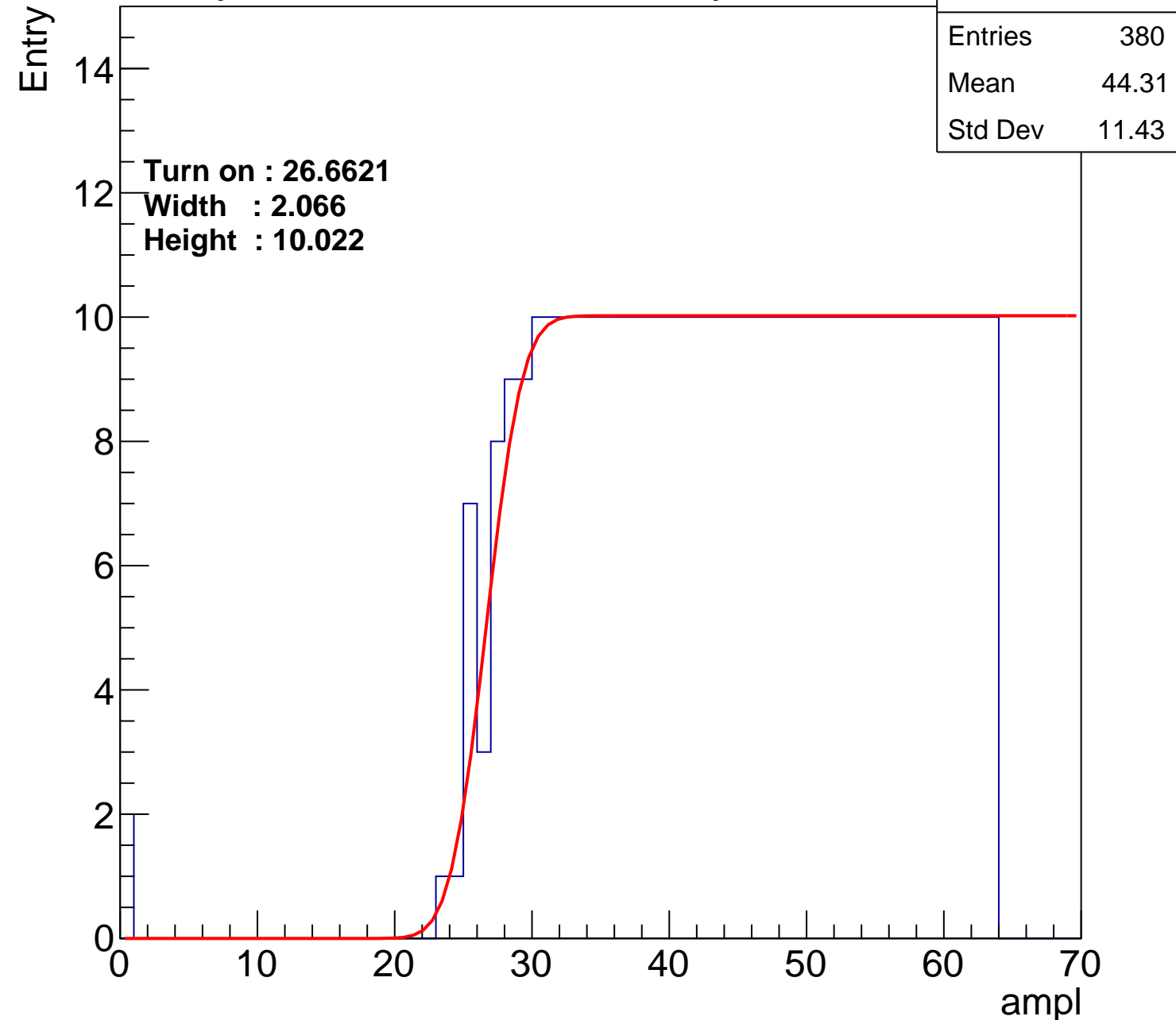
Width : 2.066

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.97
Std Dev	11.84

Turn on : 26.0253

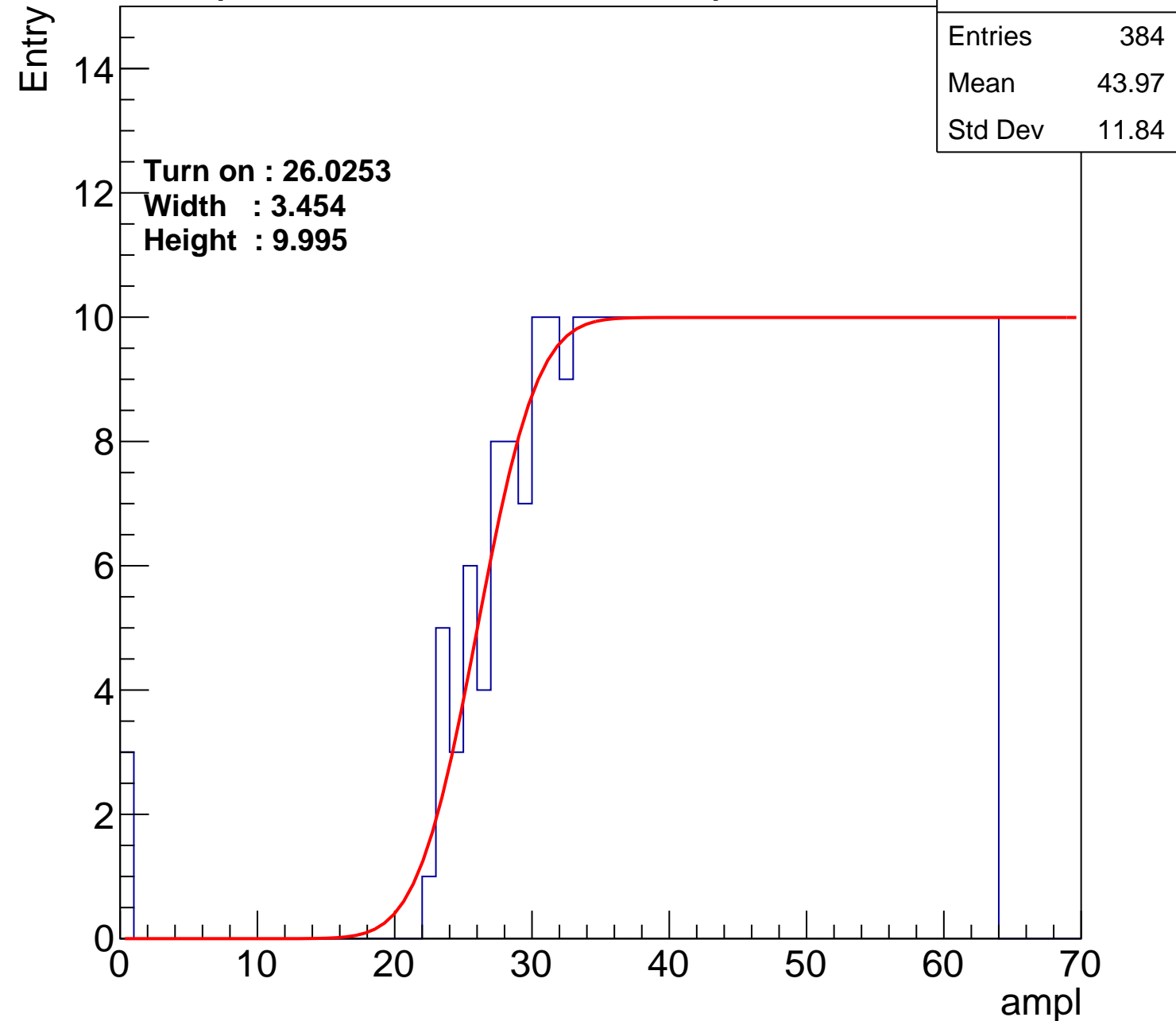
Width : 3.454

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.96
Std Dev	11

Turn on : 27.5611

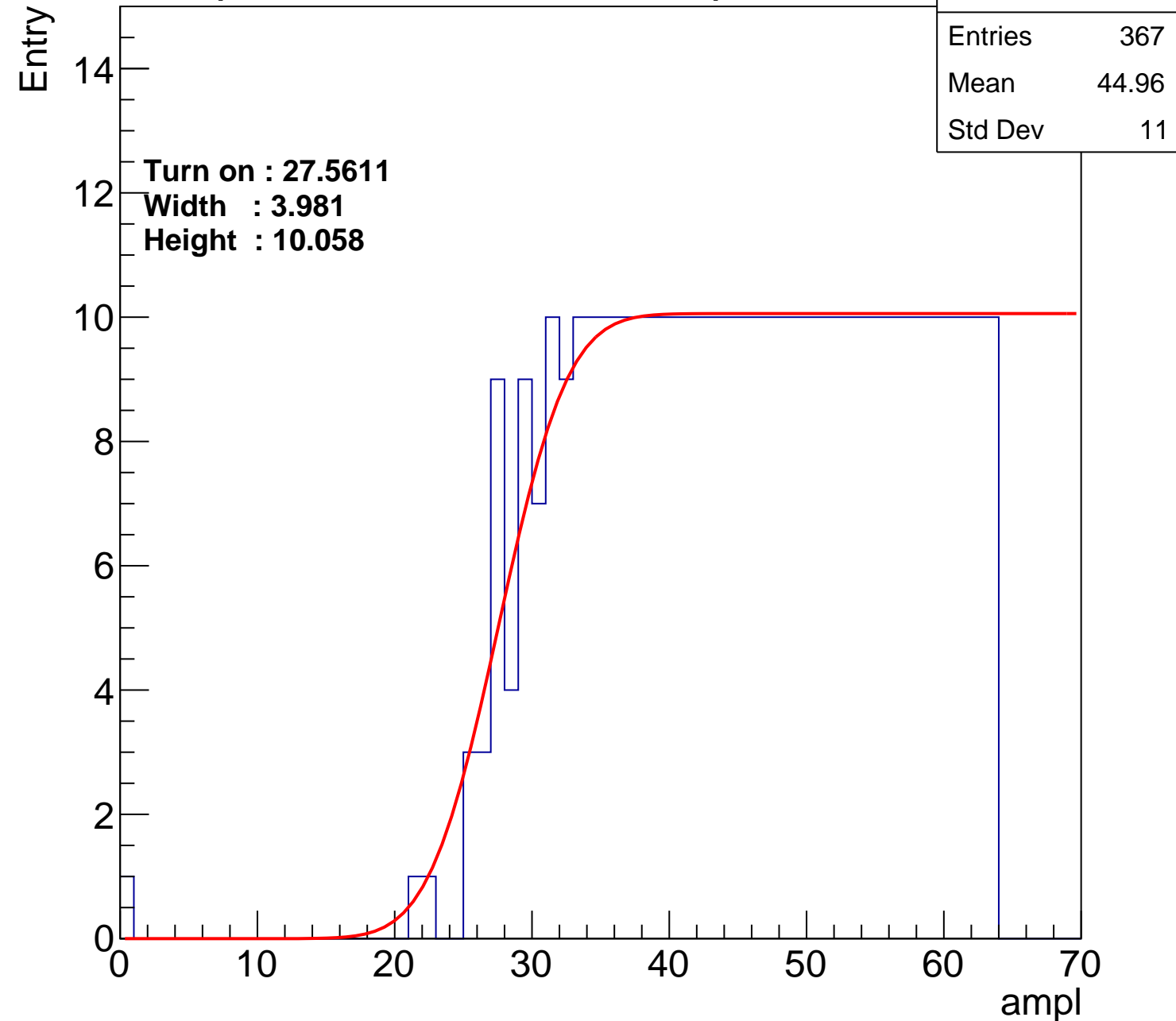
Width : 3.981

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.81
Std Dev	11.93

Turn on : 25.9997

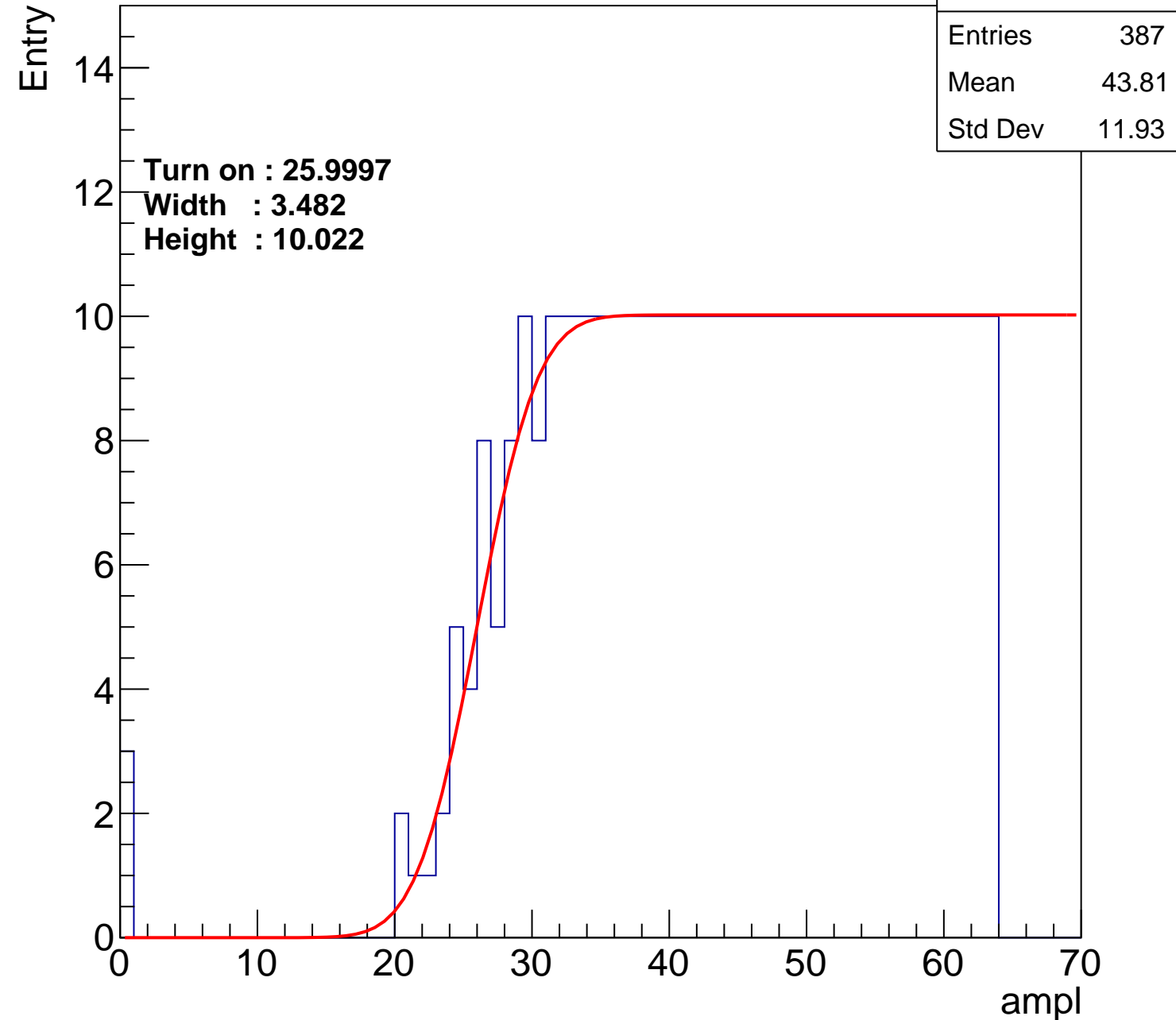
Width : 3.482

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	44.04
Std Dev	11.57

Turn on : 25.5724

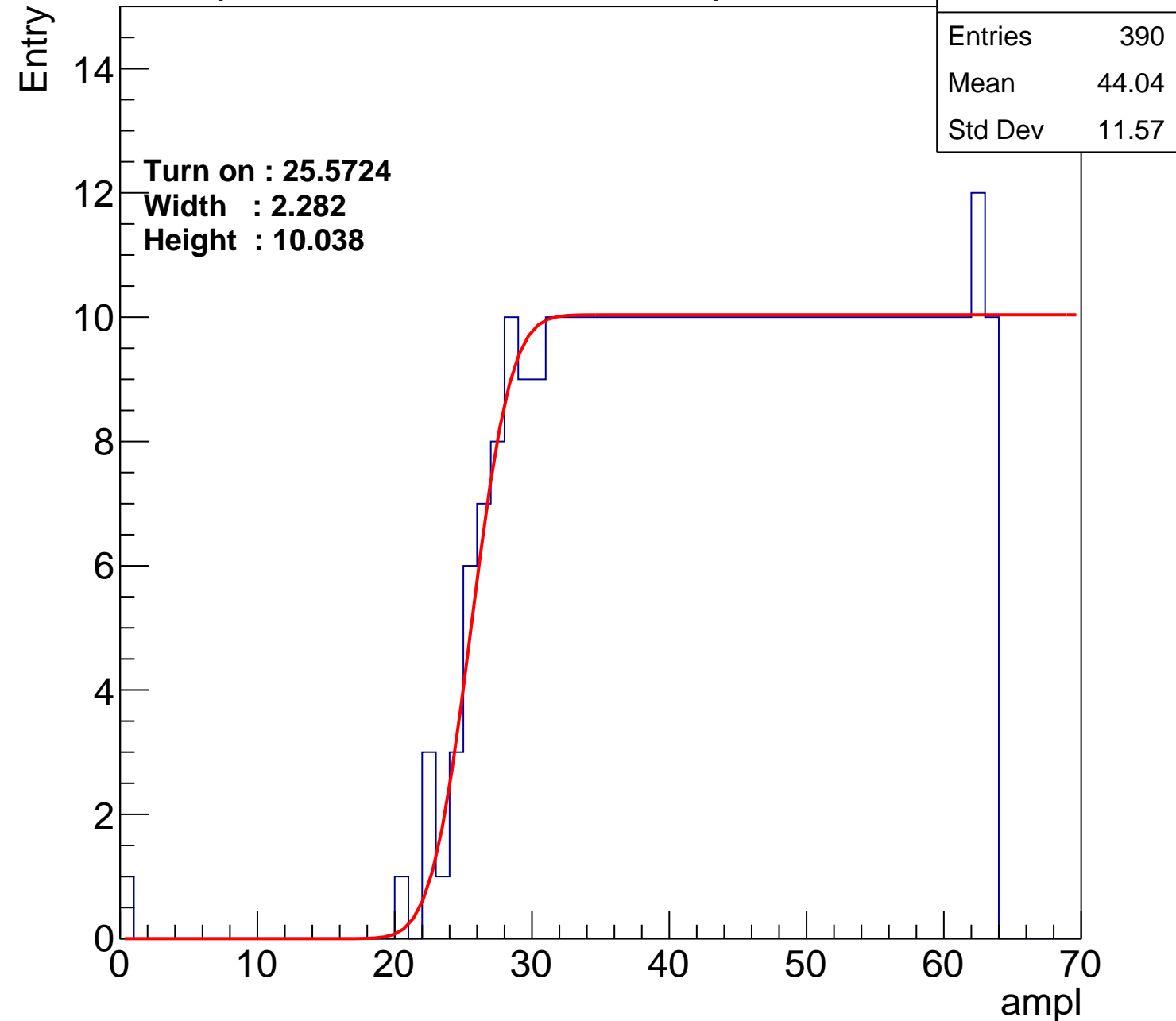
Width : 2.282

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.02
Std Dev	11.81

Turn on : 26.3356

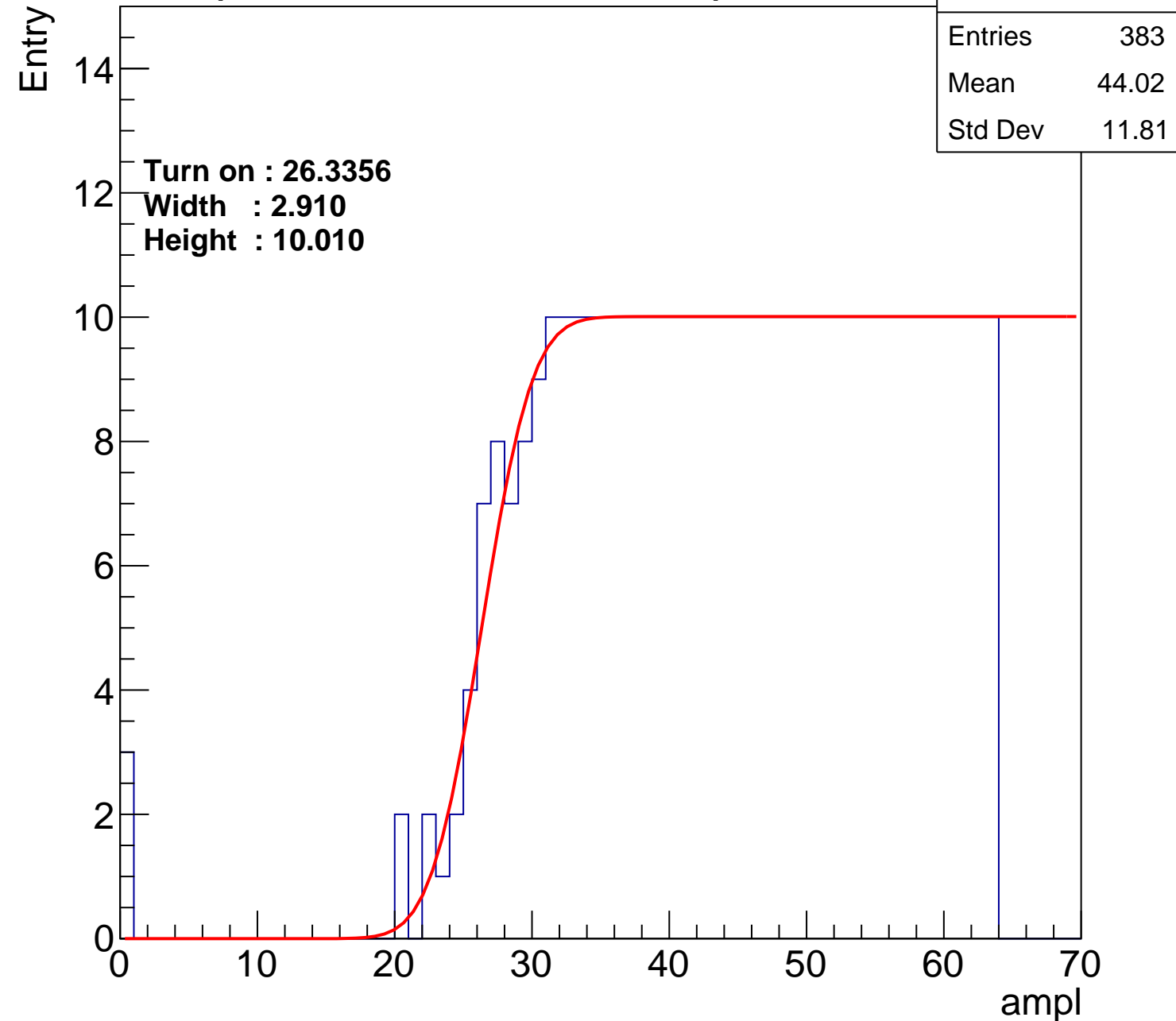
Width : 2.910

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 27.0646

Width : 3.353

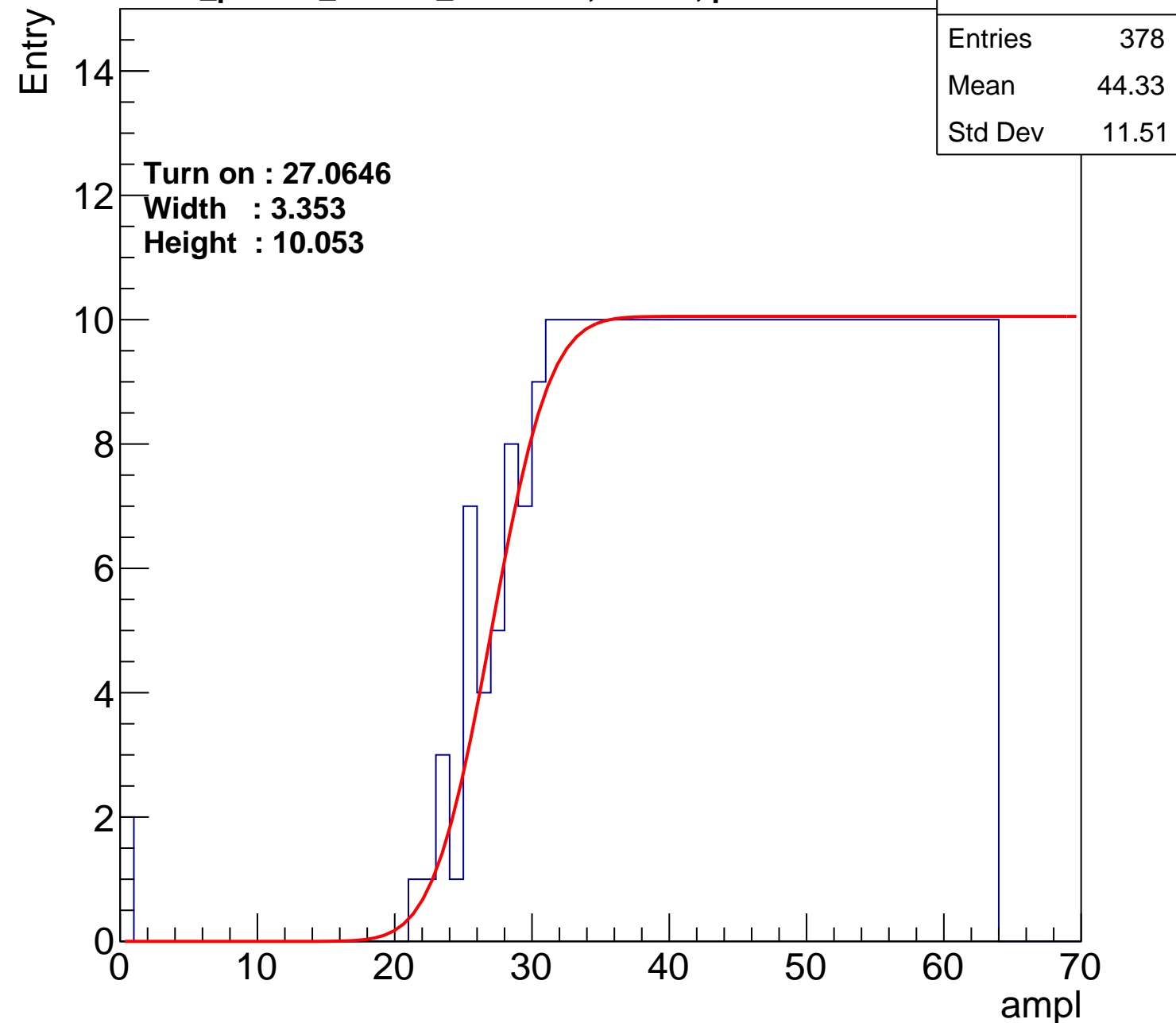
Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U11-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.15
Std Dev	12.42

Turn on : 25.0001

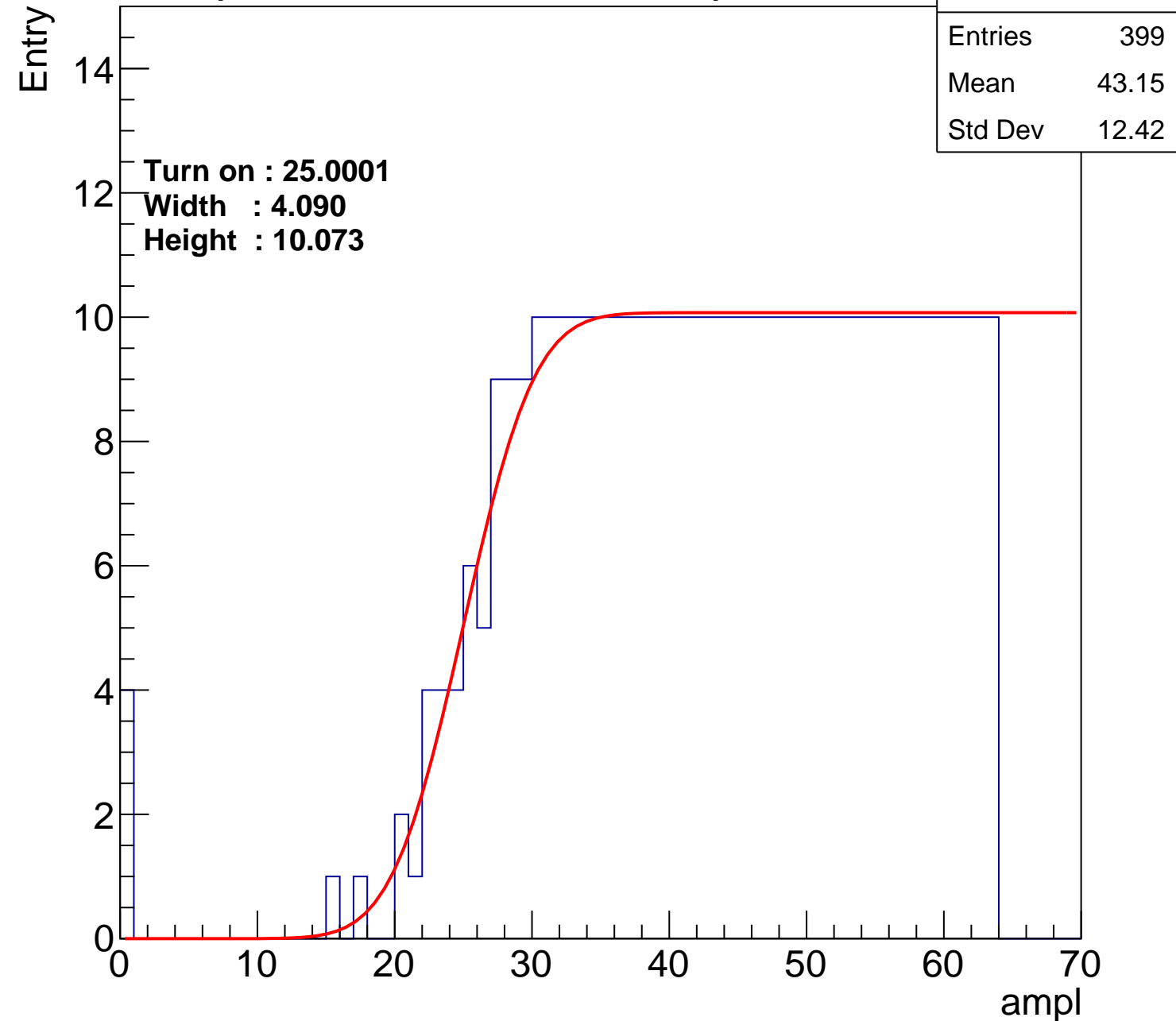
Width : 4.090

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.06
Std Dev	12.05

Turn on : 26.5674

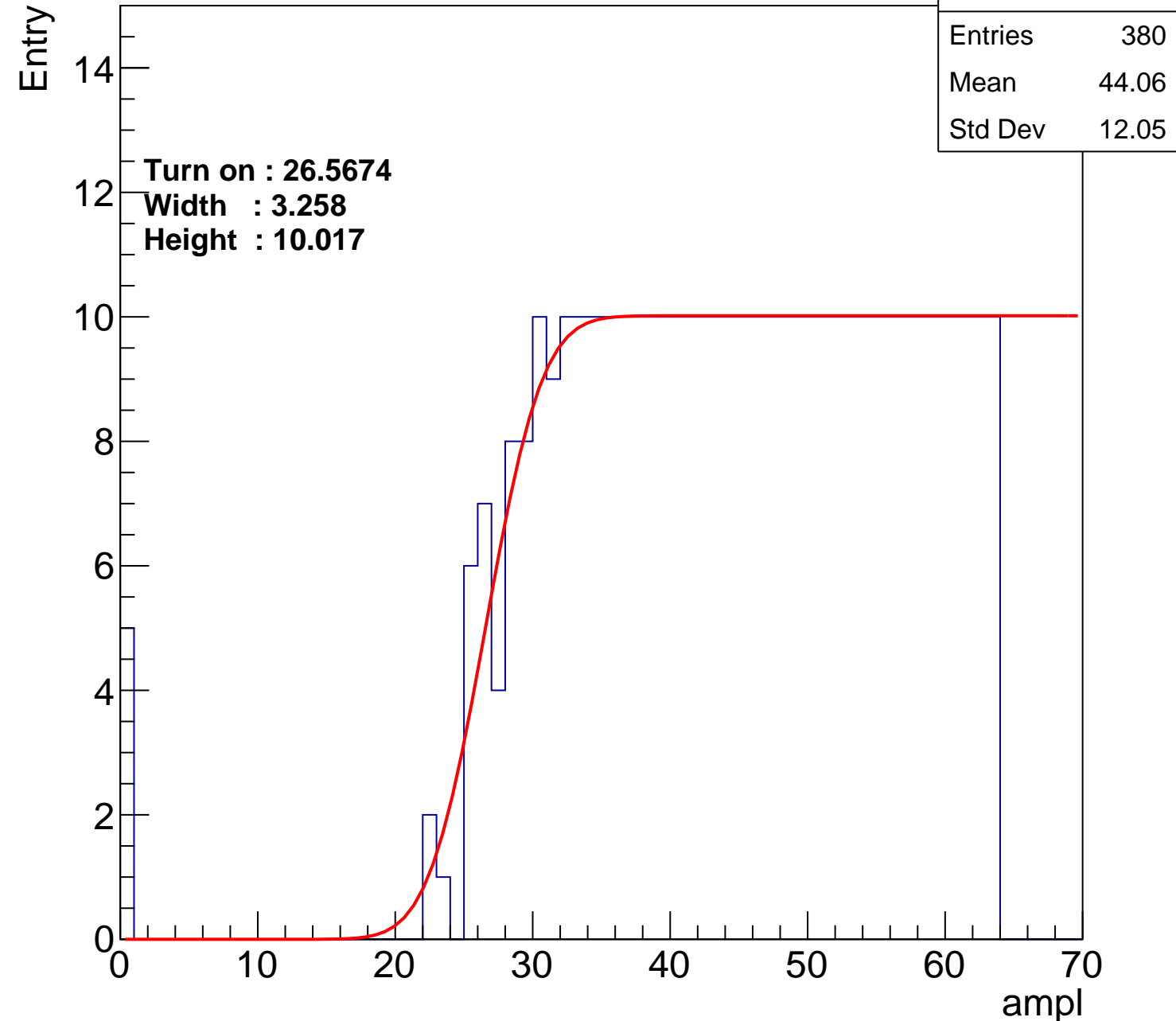
Width : 3.258

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.15
Std Dev	11.64

Turn on : 26.1616

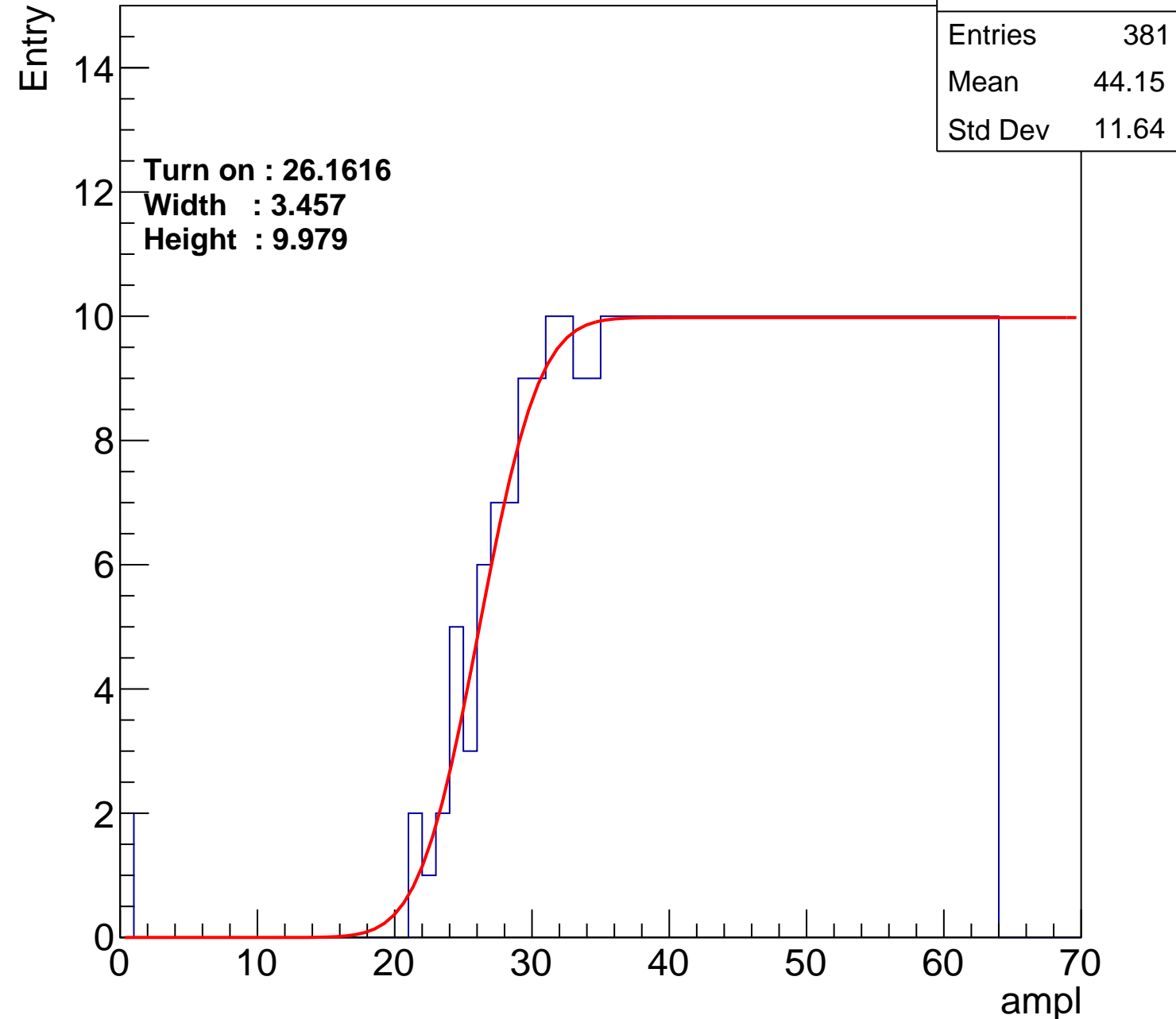
Width : 3.457

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	357
Mean	45.45
Std Dev	10.74

Turn on : 28.2394

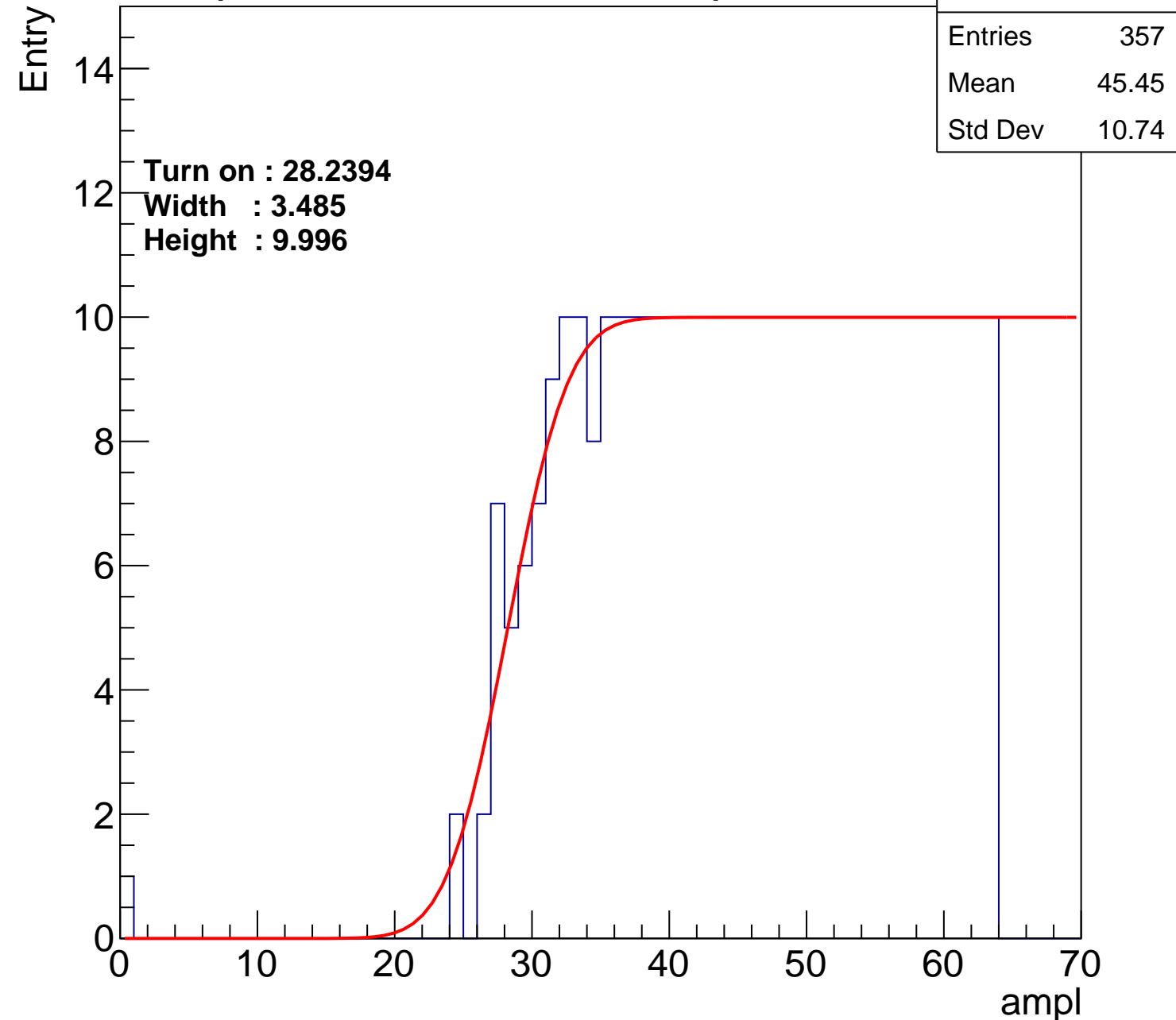
Width : 3.485

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.01
Std Dev	12.01

Turn on : 26.3724

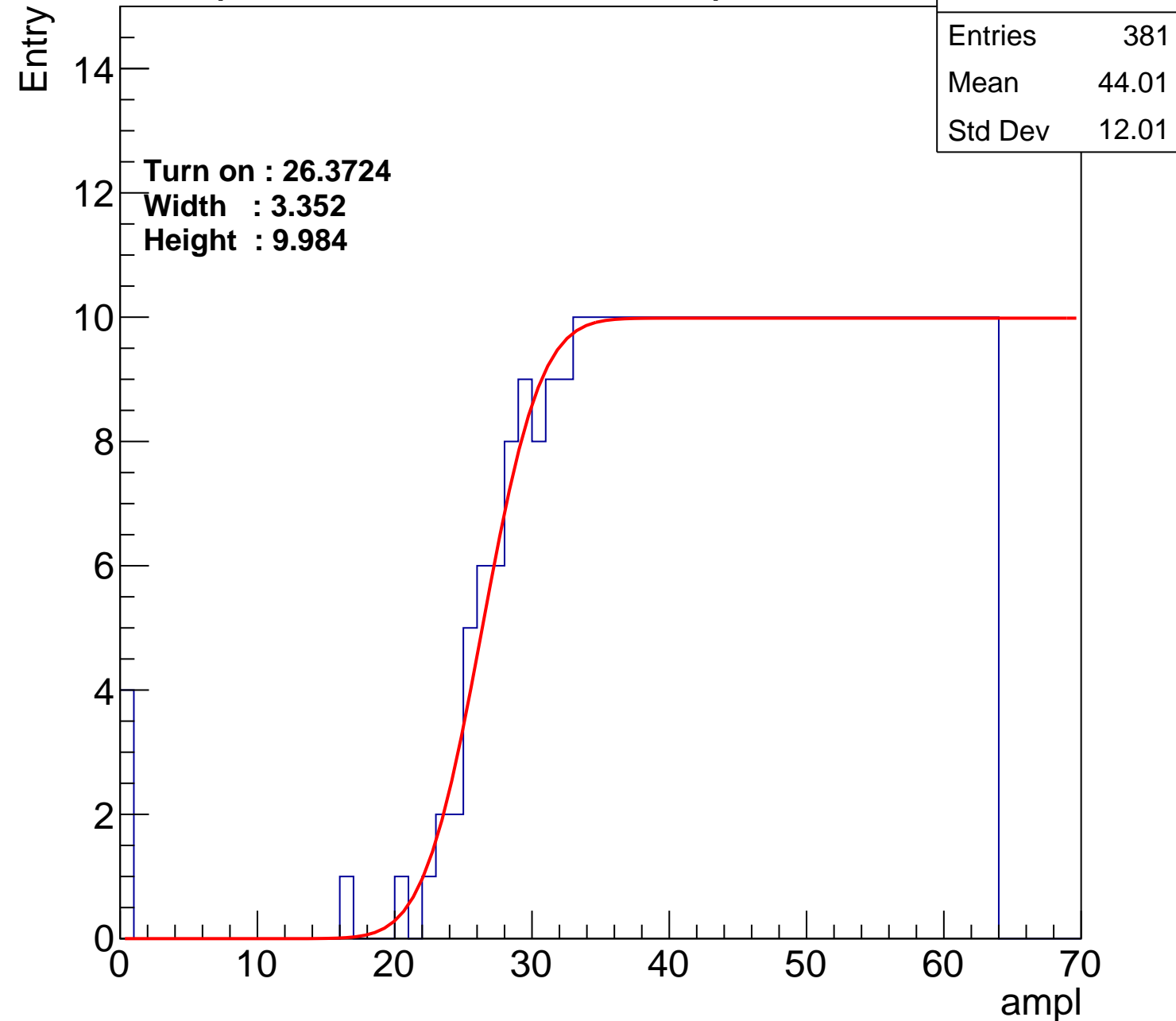
Width : 3.352

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44
Std Dev	11.81

Turn on : 26.5807

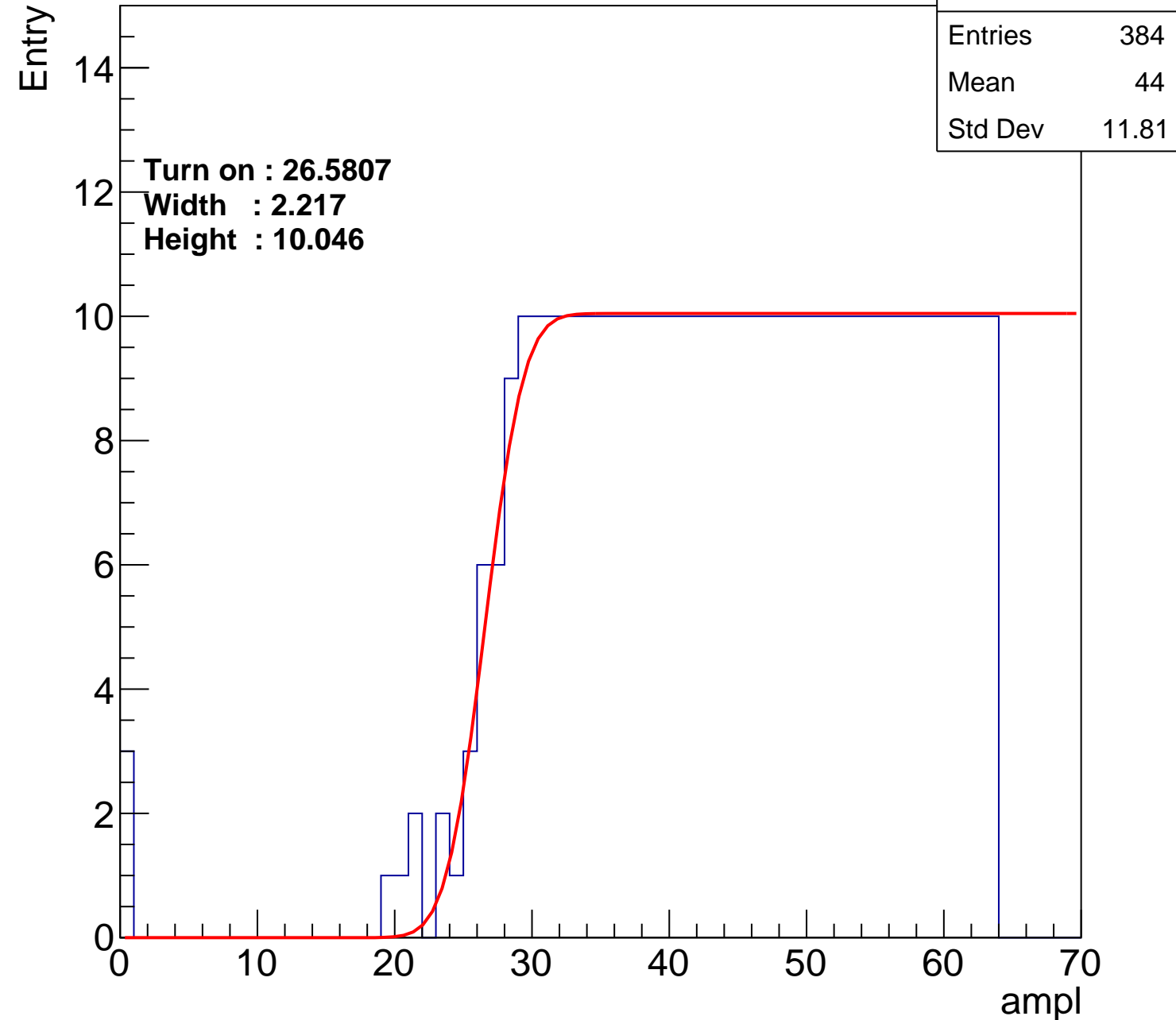
Width : 2.217

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.08
Std Dev	12.4

Turn on : 25.0851

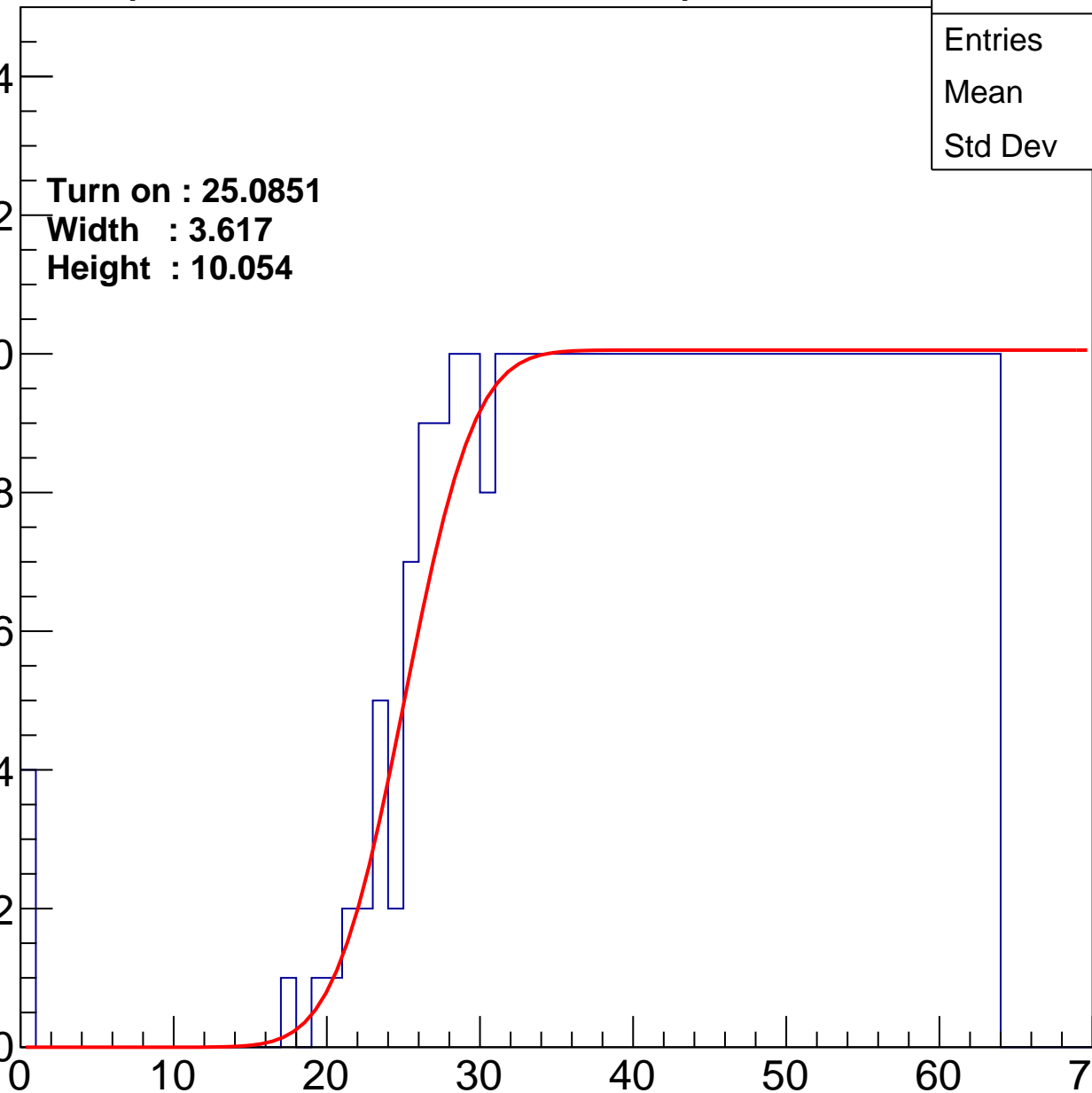
Width : 3.617

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.12
Std Dev	10.96

Turn on : 29.8385

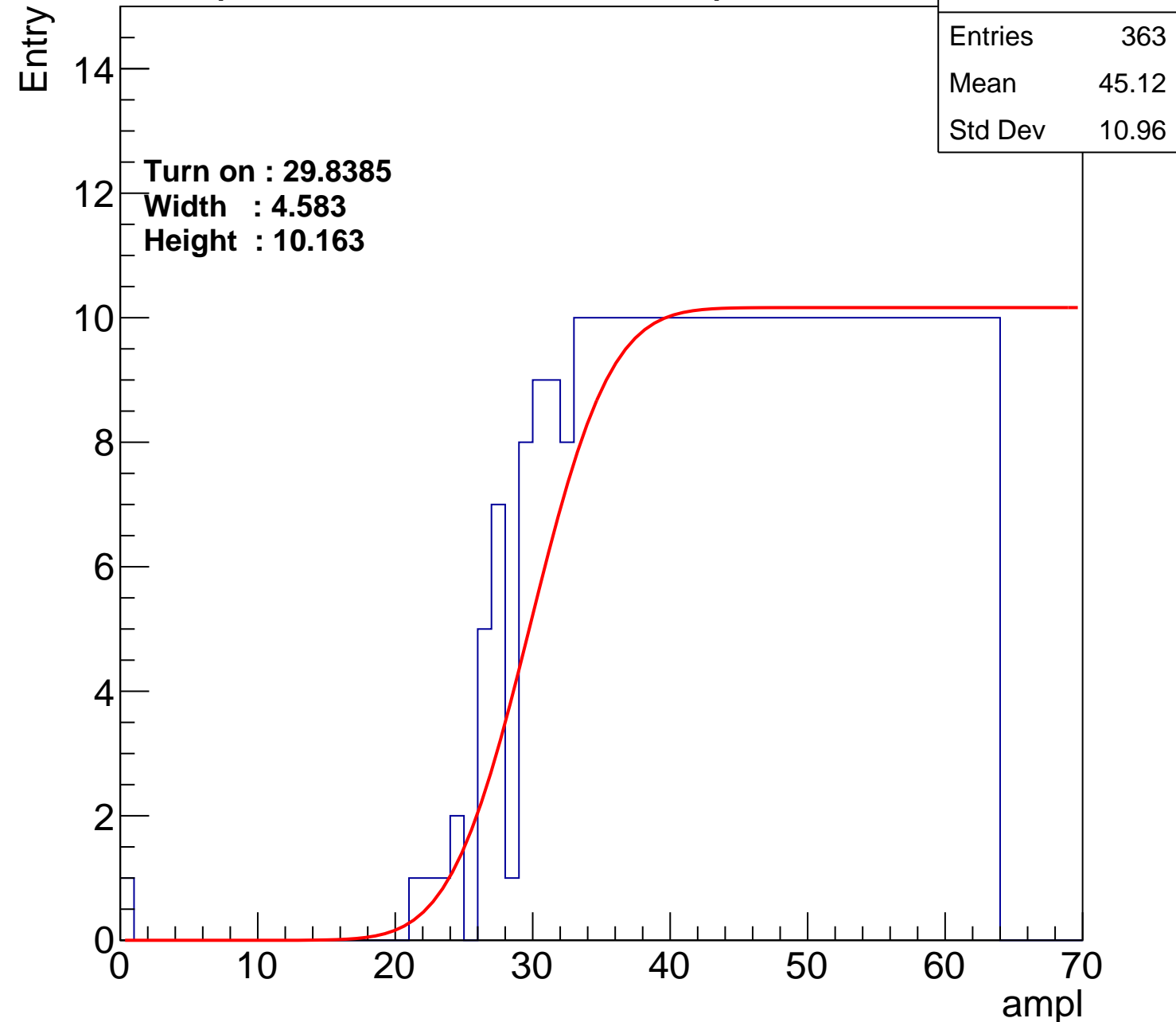
Width : 4.583

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.76
Std Dev	12.11

Turn on : 26.3282

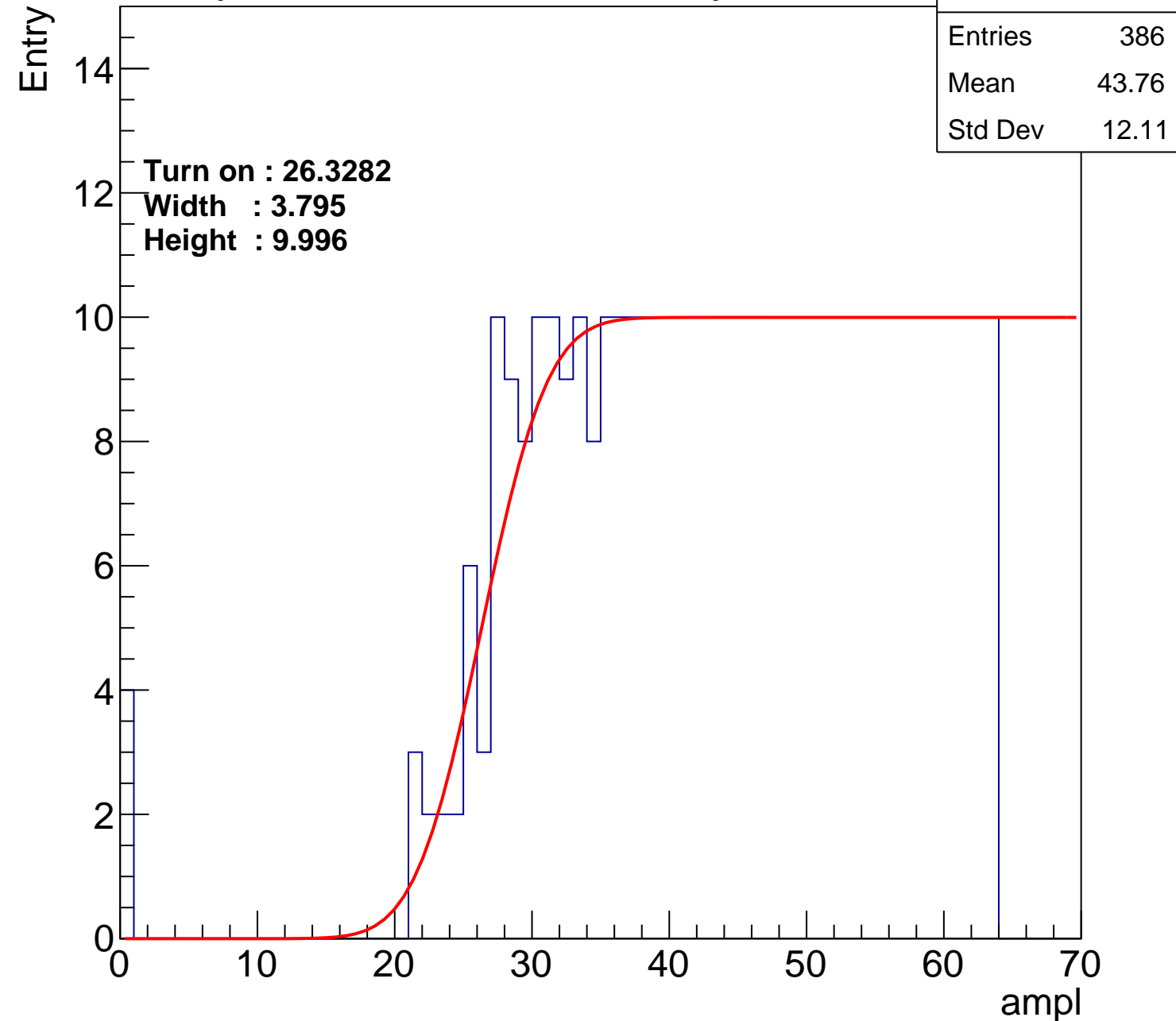
Width : 3.795

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.28
Std Dev	11.79

Turn on : 27.0110

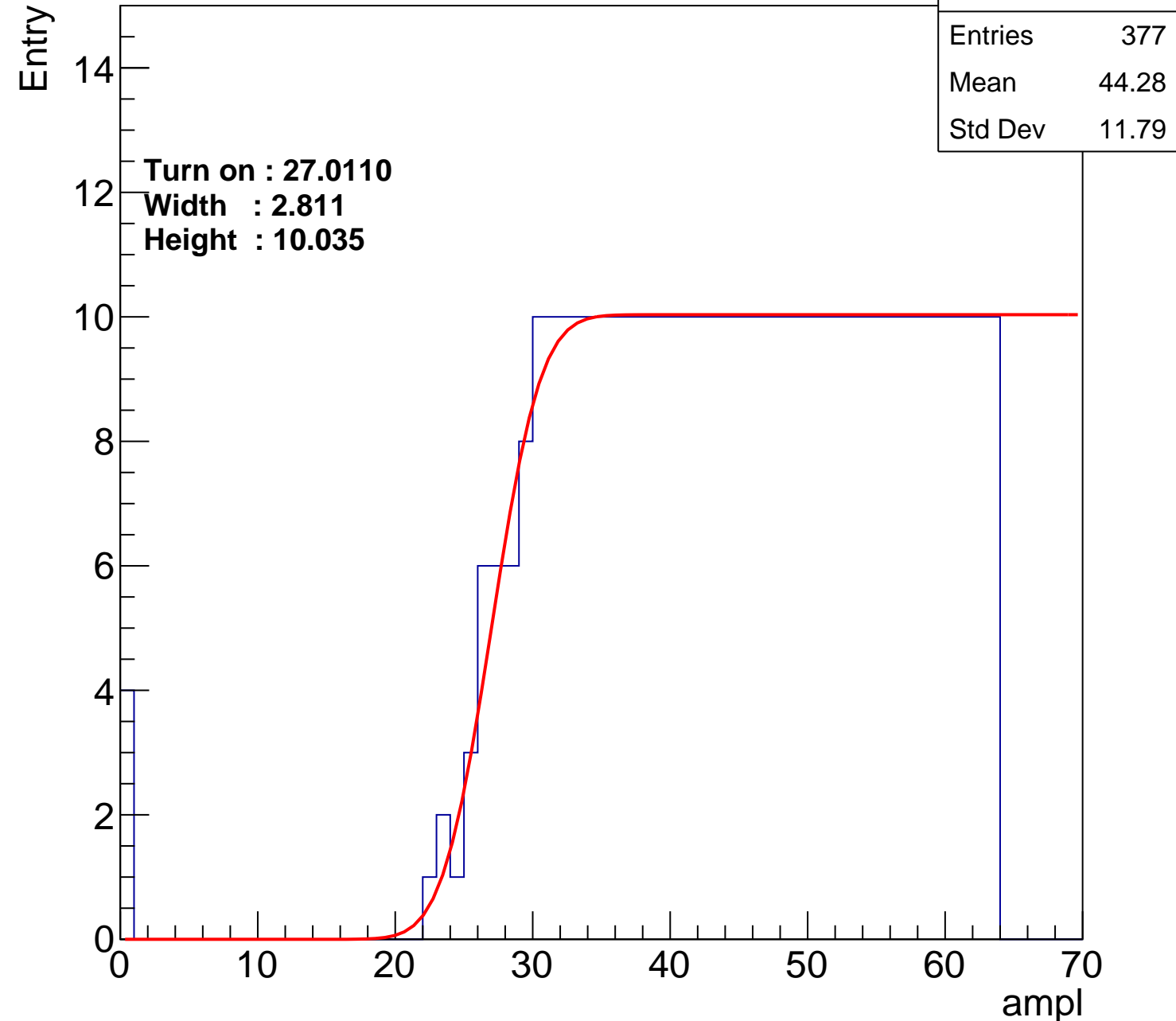
Width : 2.811

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch94

calib_packv5_042523_0143.root, FC#12, port B1

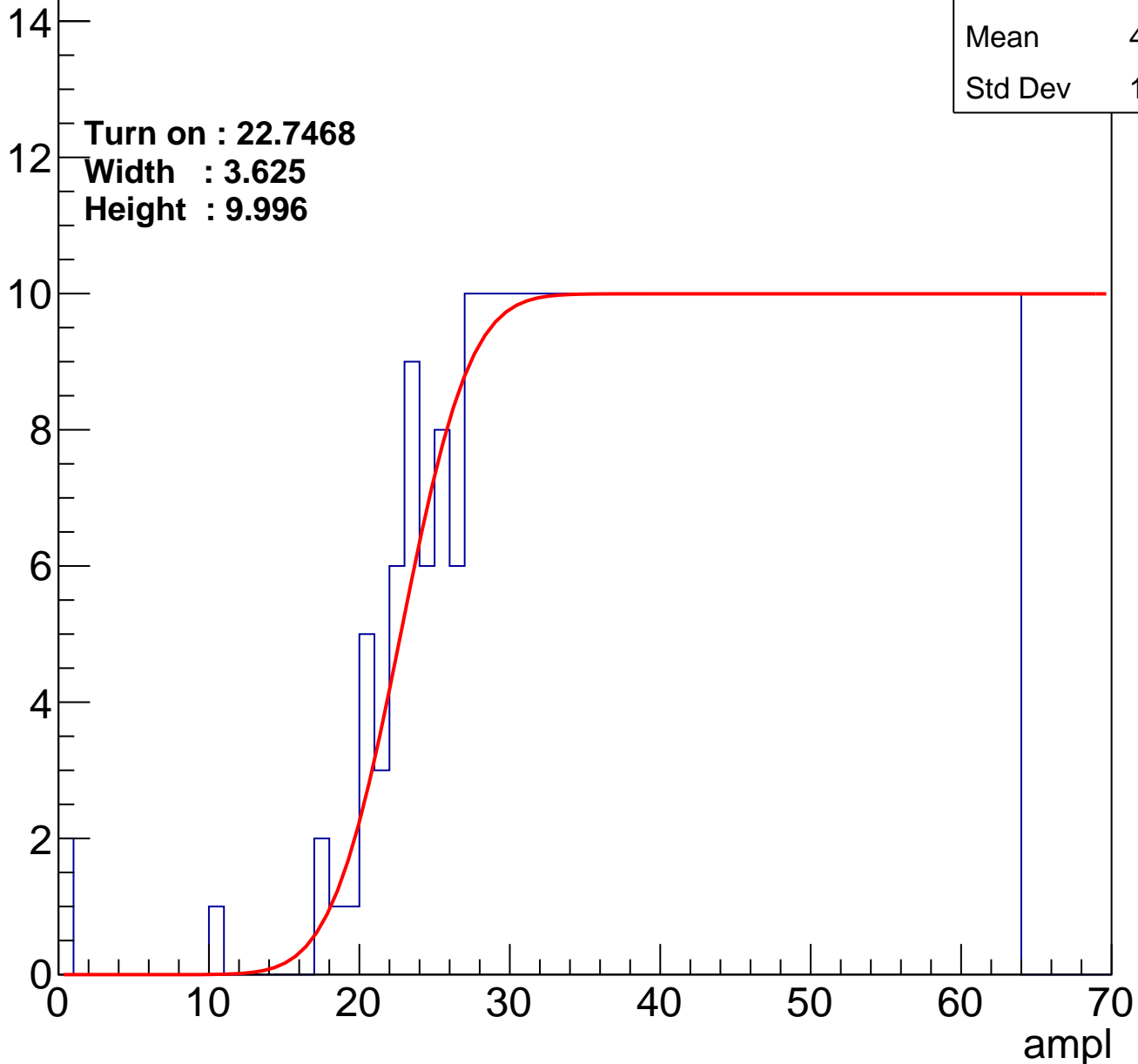
Entries	420
Mean	42.22
Std Dev	12.68

Turn on : 22.7468

Width : 3.625

Height : 9.996

Entry



B0L102S, U11-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.04
Std Dev	10.98

Turn on : 28.4607

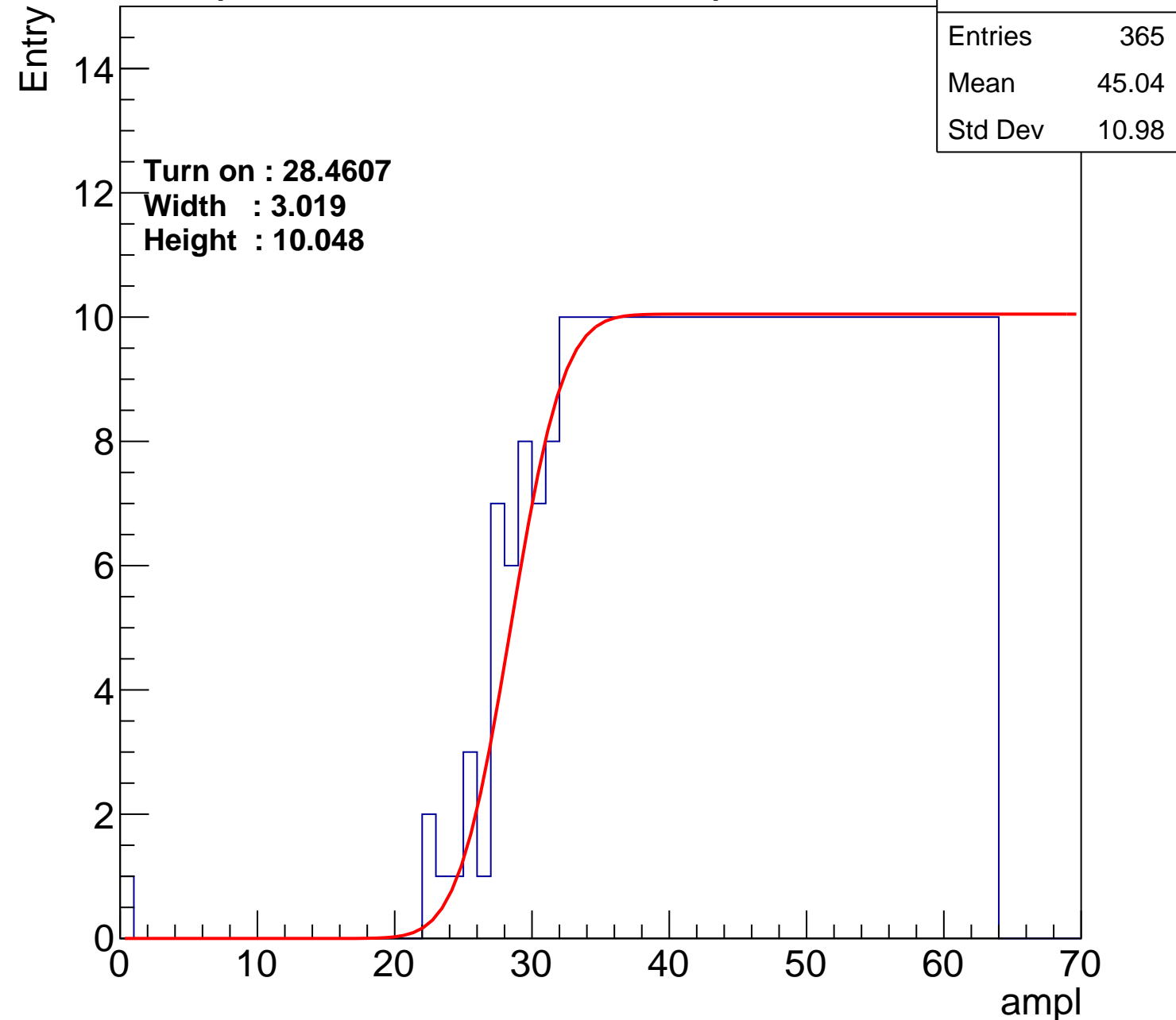
Width : 3.019

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch96

calib_packv5_042523_0143.root, FC#12, port B1

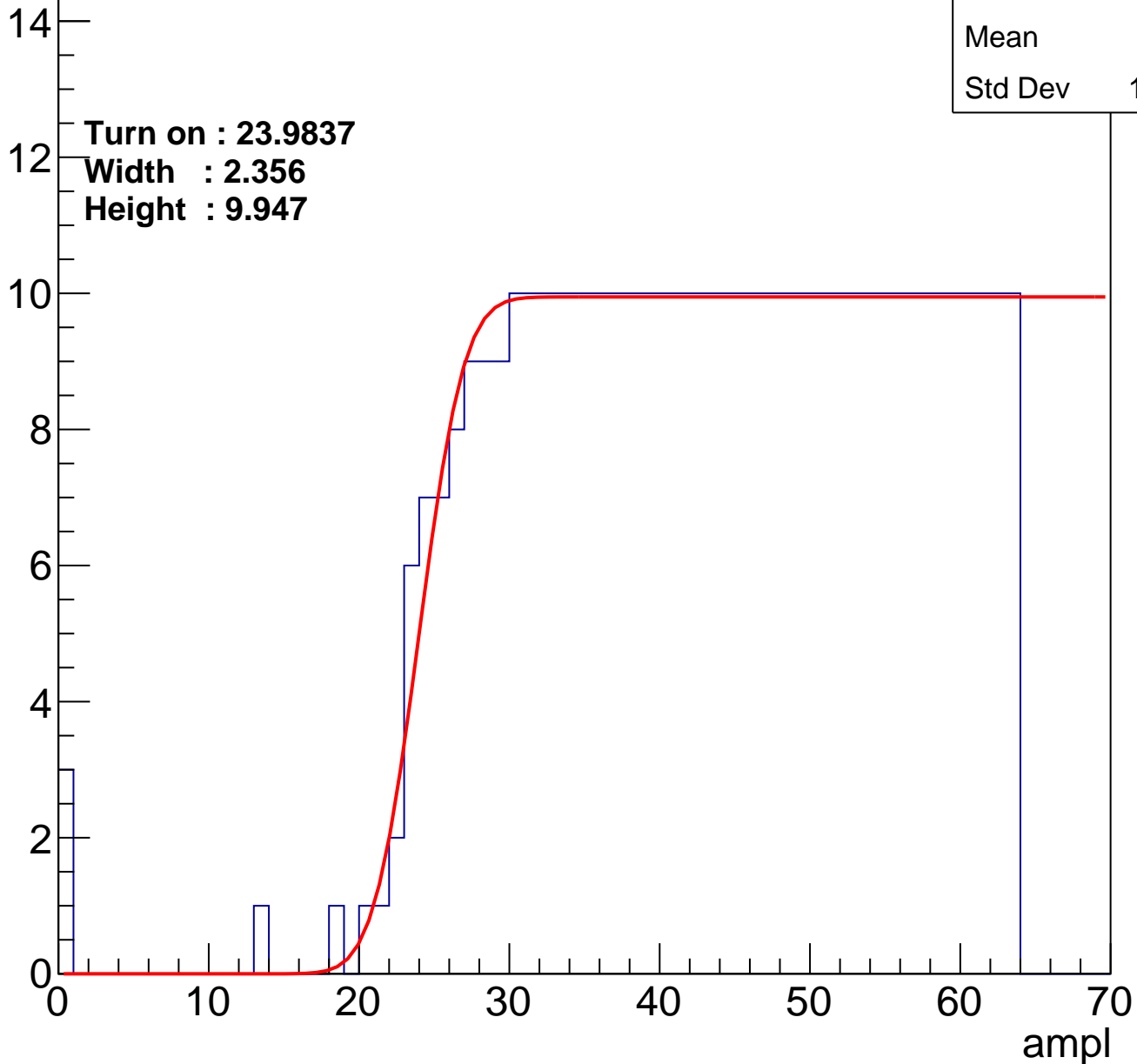
Entries	404
Mean	43
Std Dev	12.33

Turn on : 23.9837

Width : 2.356

Height : 9.947

Entry



B0L102S, U11-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.43
Std Dev	11.5

Turn on : 26.6450

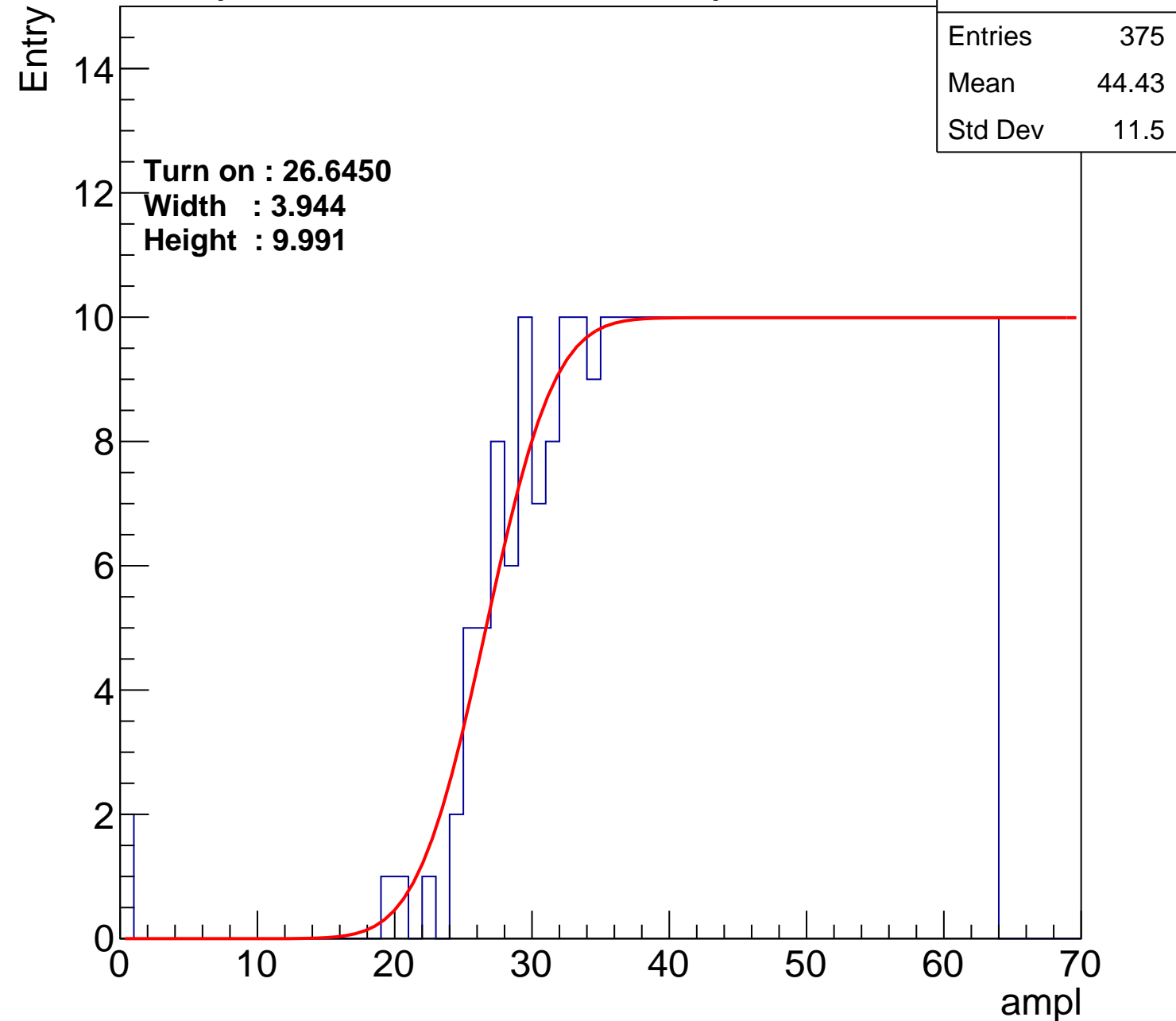
Width : 3.944

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.57
Std Dev	12.11

Turn on : 27.1177

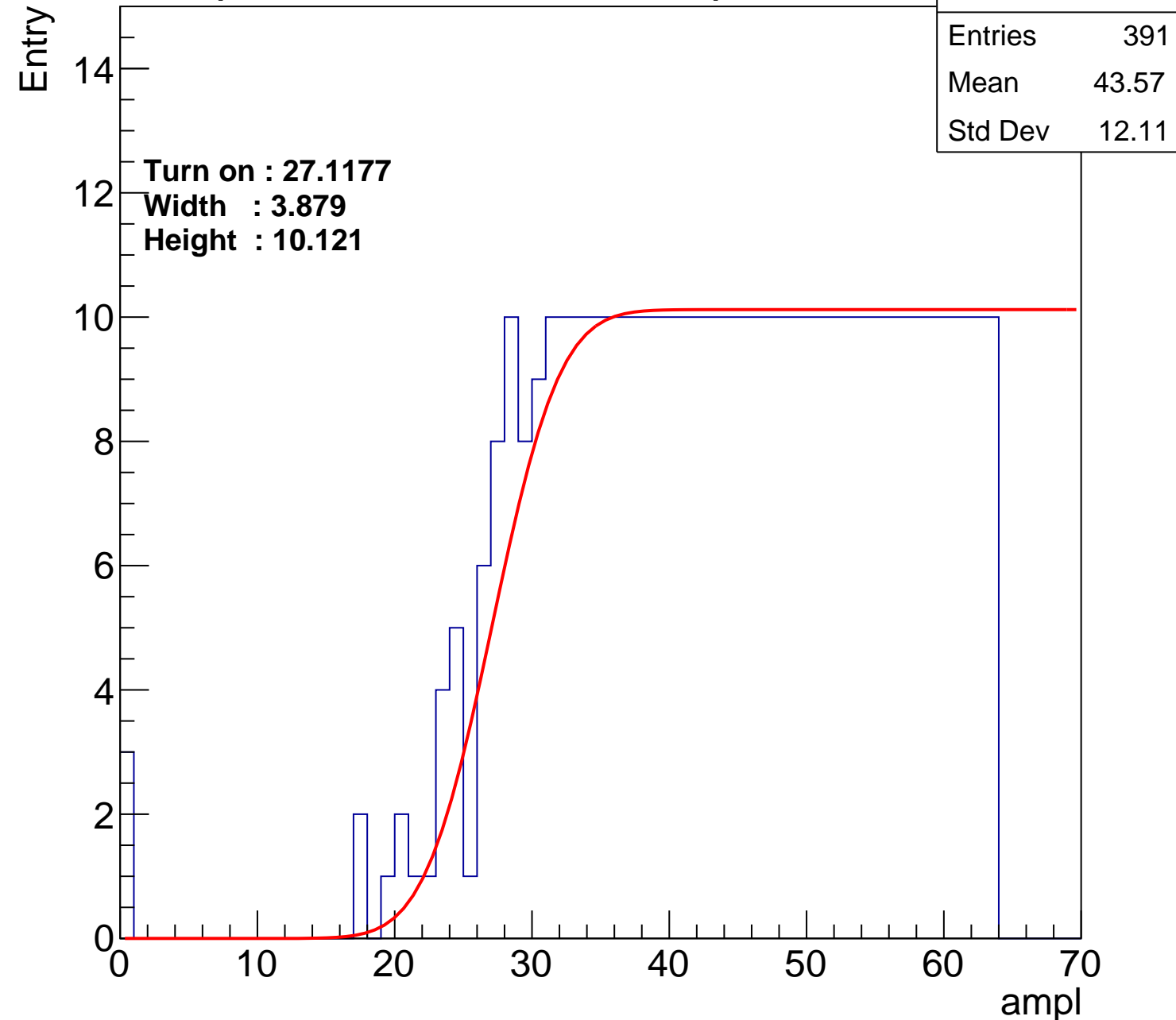
Width : 3.879

Height : 10.121

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.82
Std Dev	11.62

Turn on : 24.7968

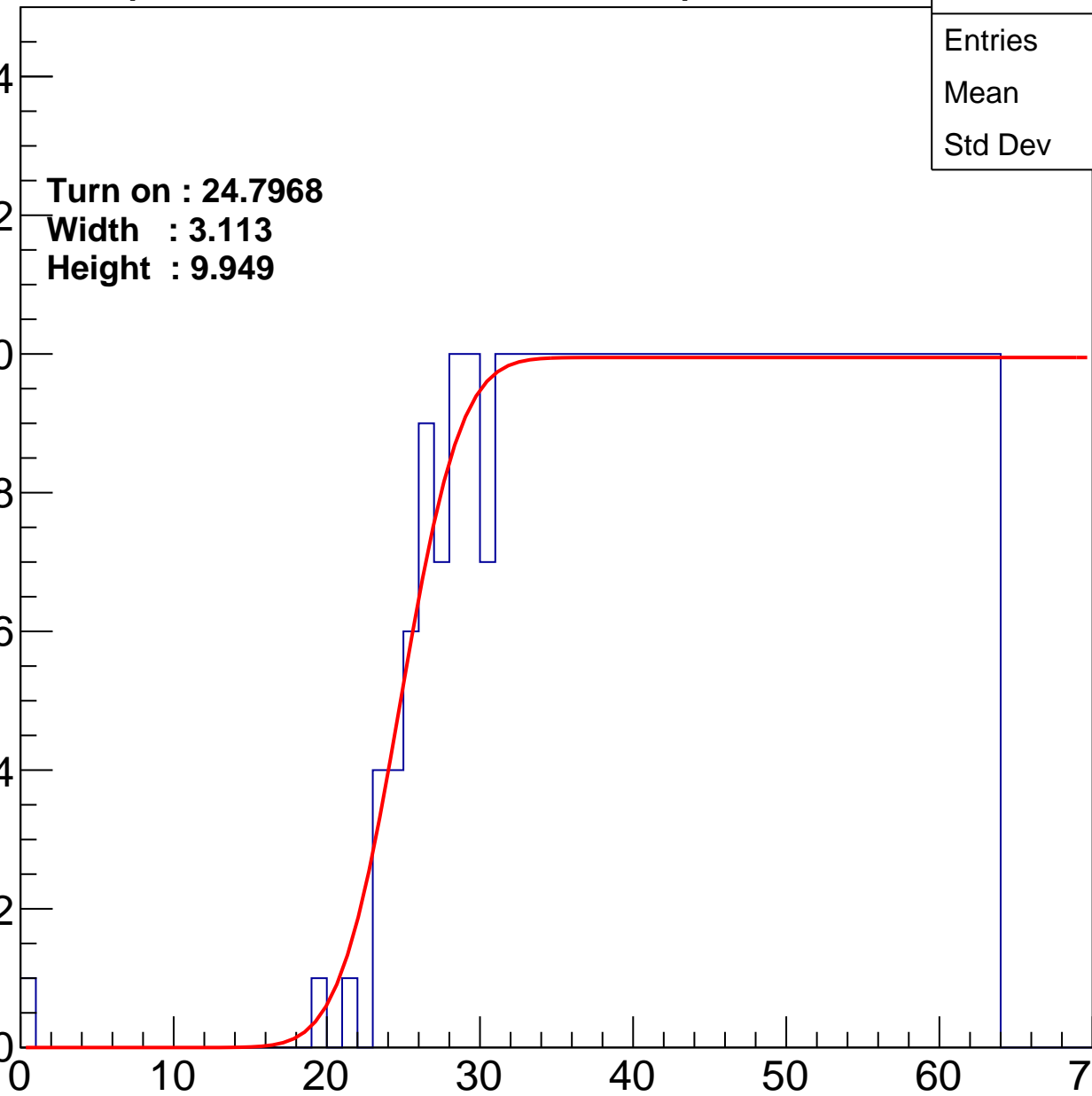
Width : 3.113

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.15
Std Dev	11.65

Turn on : 26.4598

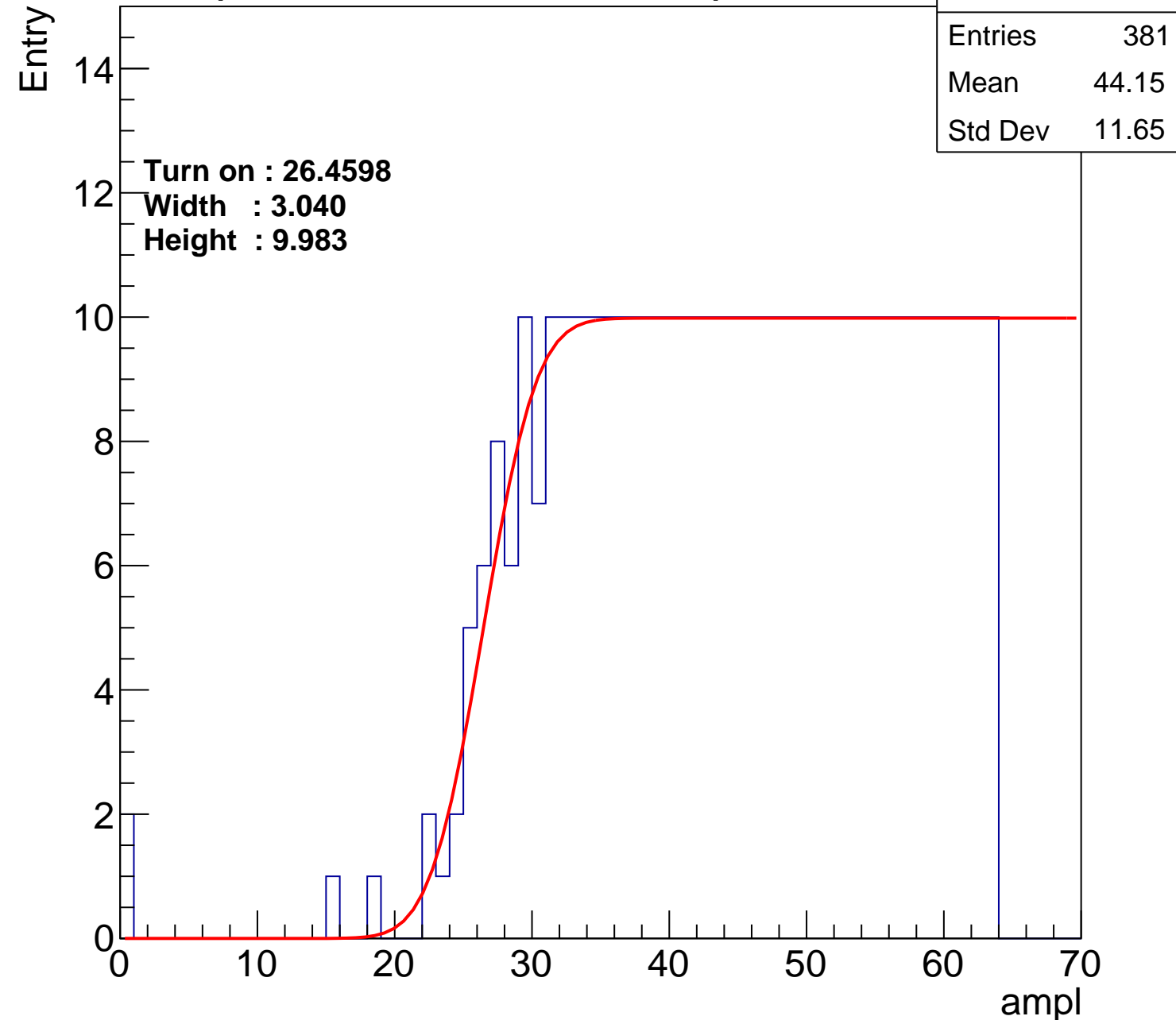
Width : 3.040

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.24
Std Dev	11.43

Turn on : 26.5306

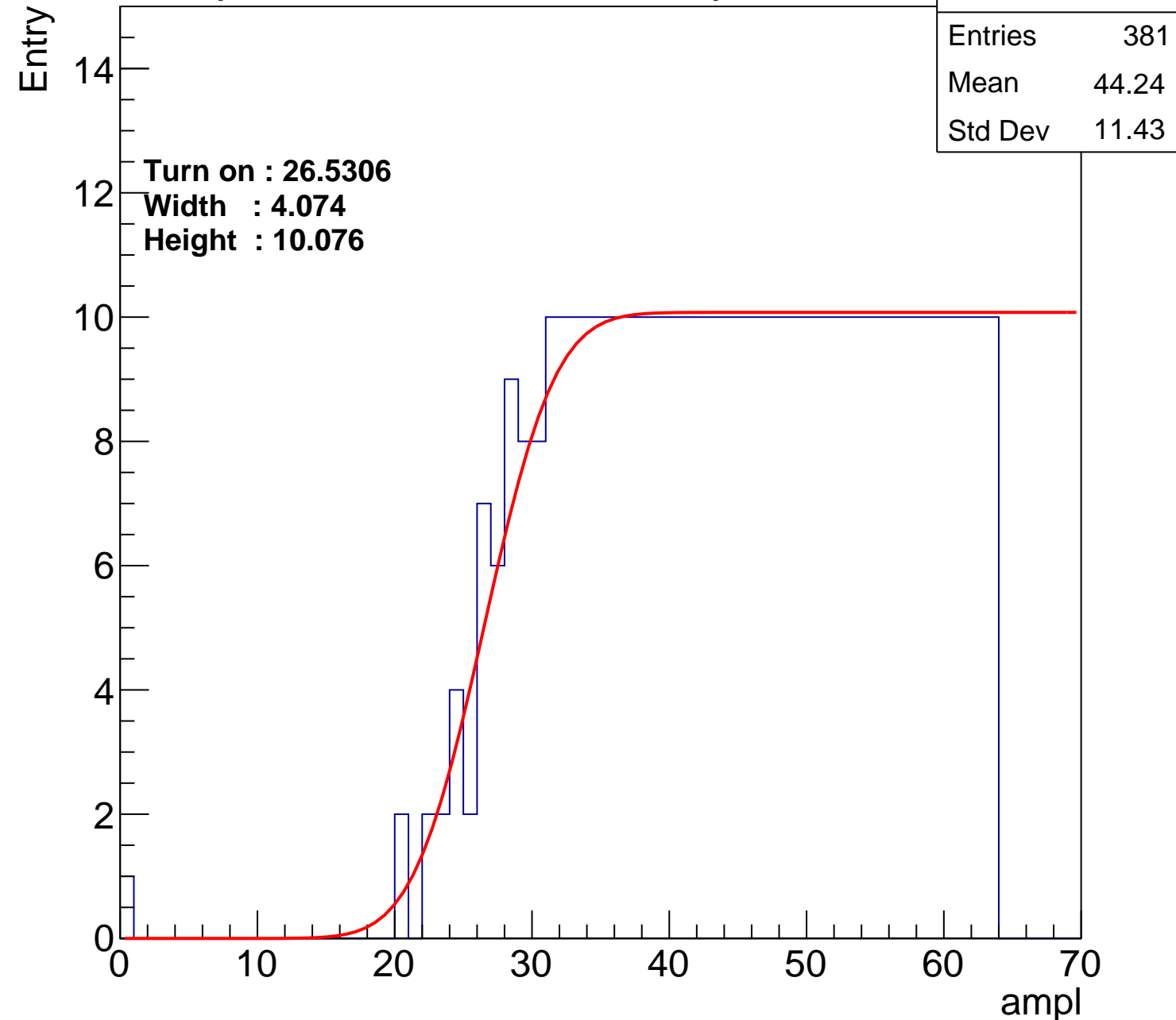
Width : 4.074

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	43.09
Std Dev	12.17

Turn on : 24.1155

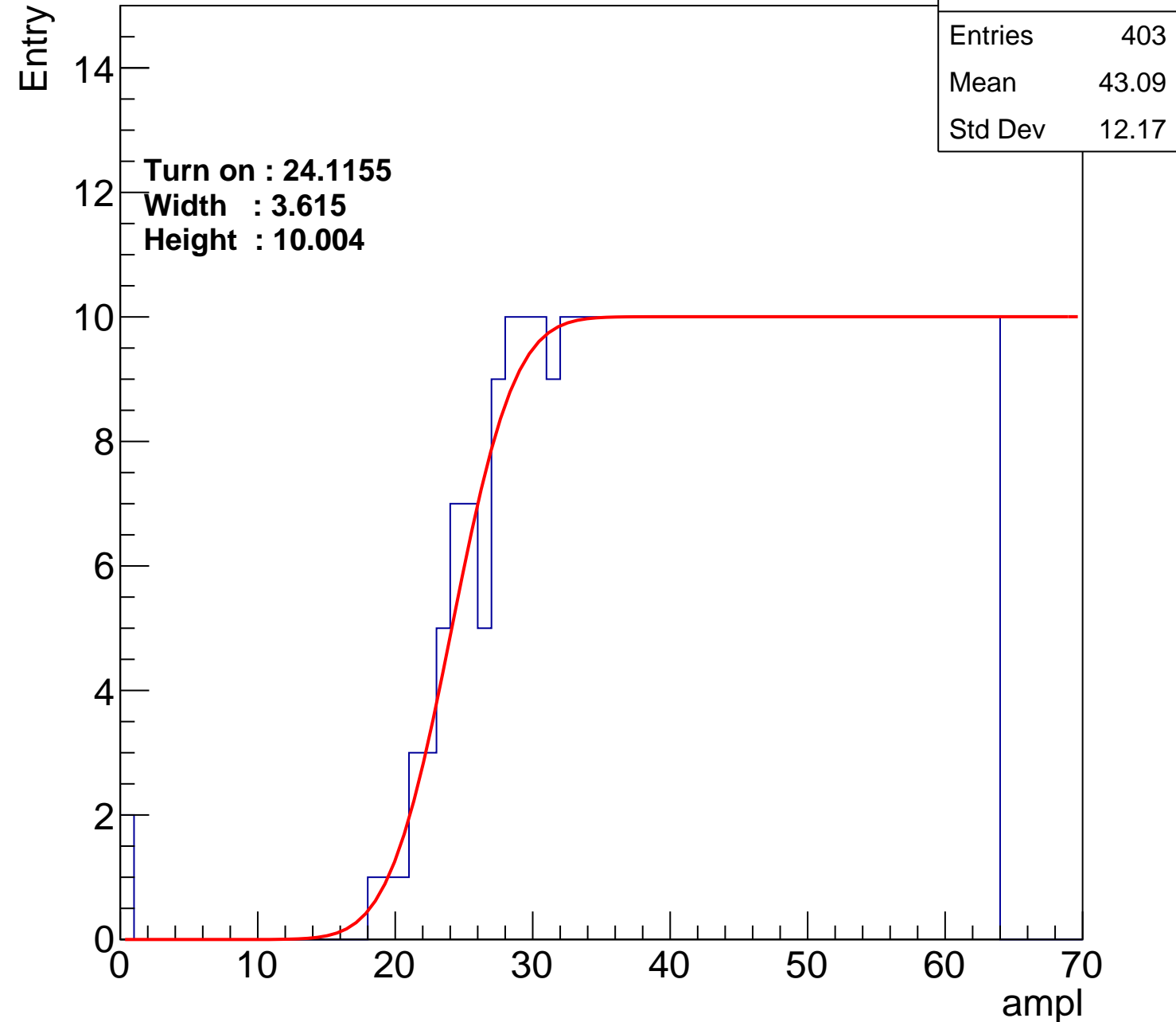
Width : 3.615

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.12
Std Dev	11.76

Turn on : 25.9569

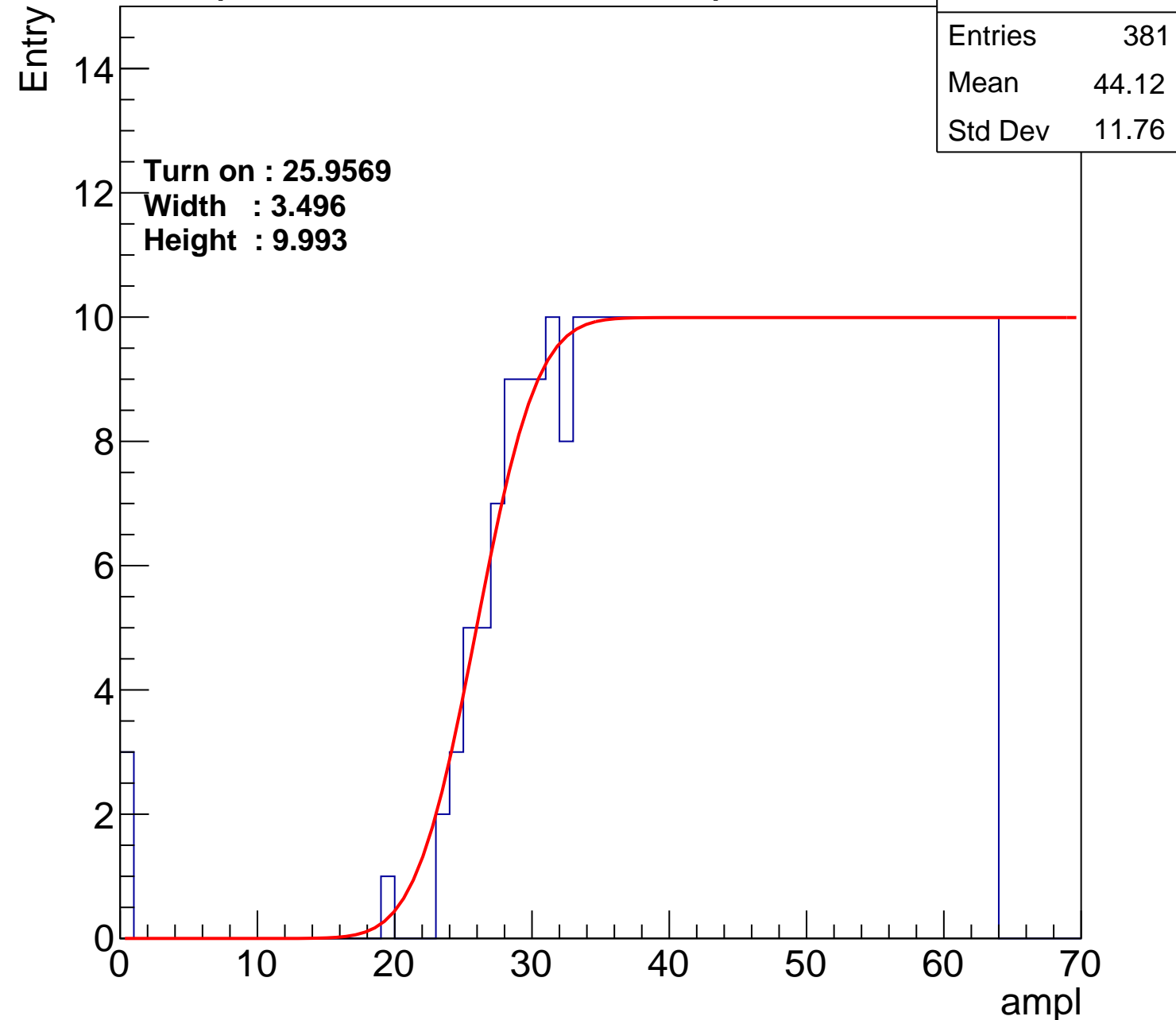
Width : 3.496

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.01
Std Dev	11.02

Turn on : 27.4803

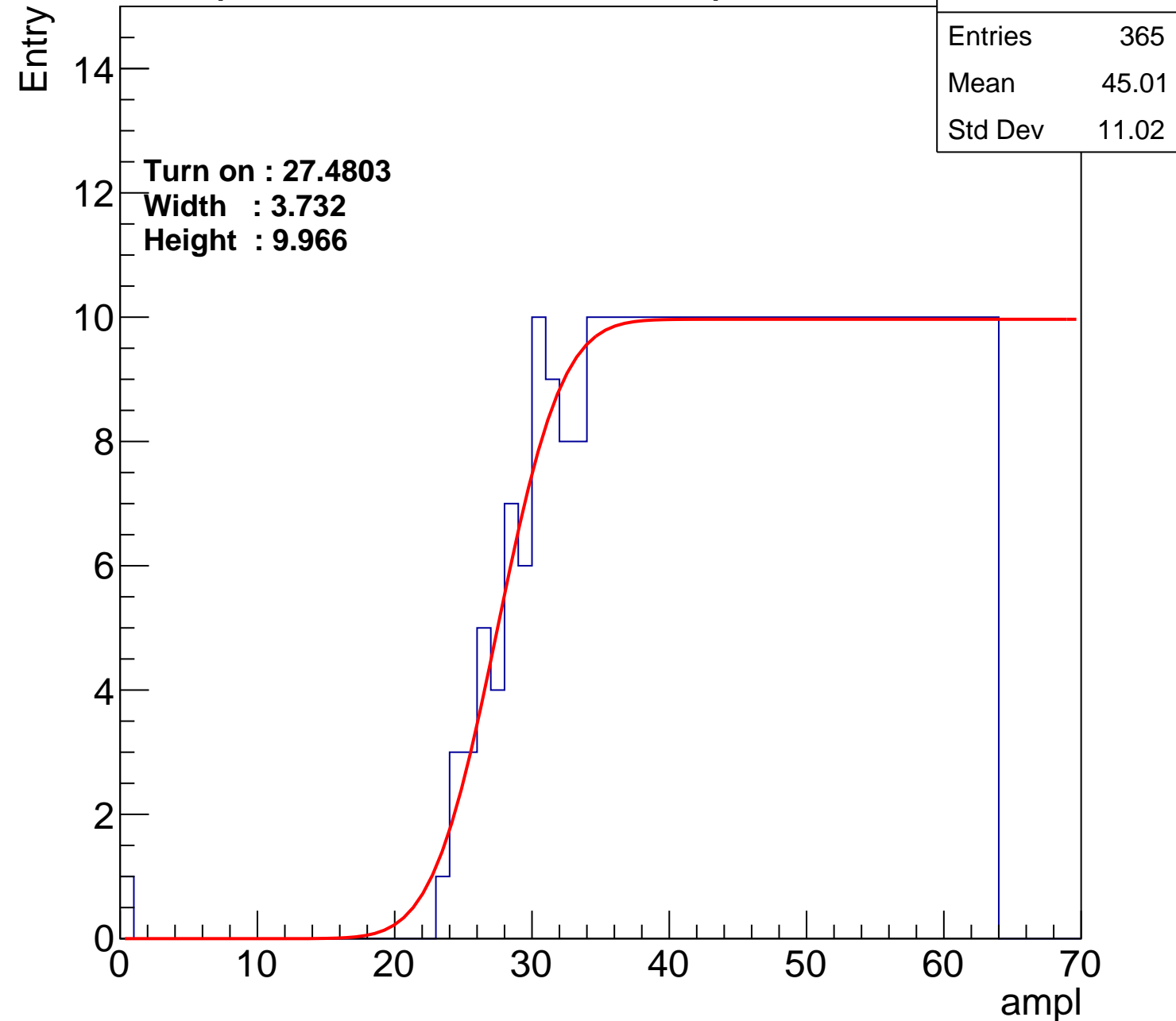
Width : 3.732

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.91
Std Dev	11.82

Turn on : 25.4897

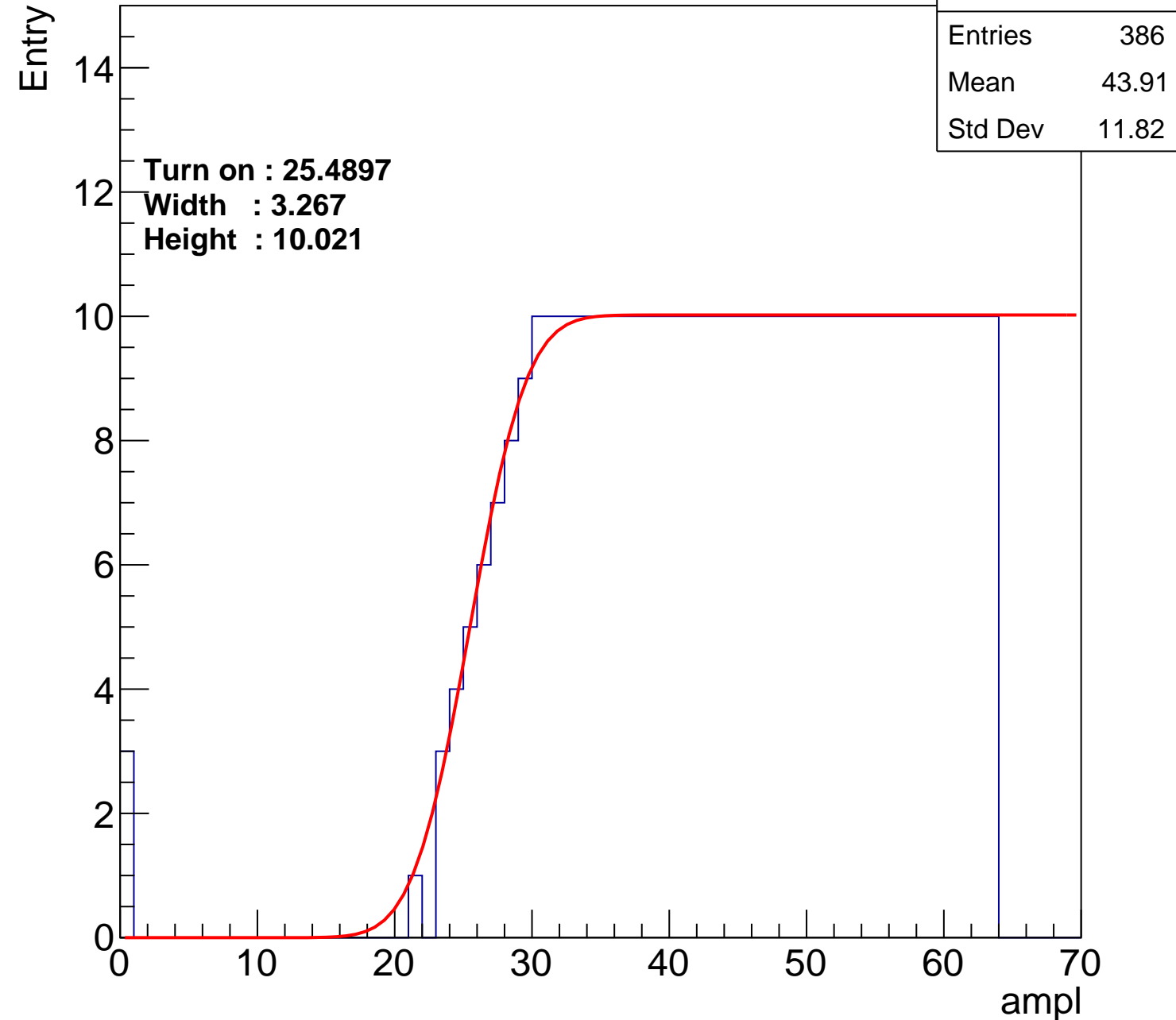
Width : 3.267

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.54
Std Dev	11.57

Turn on : 27.4945

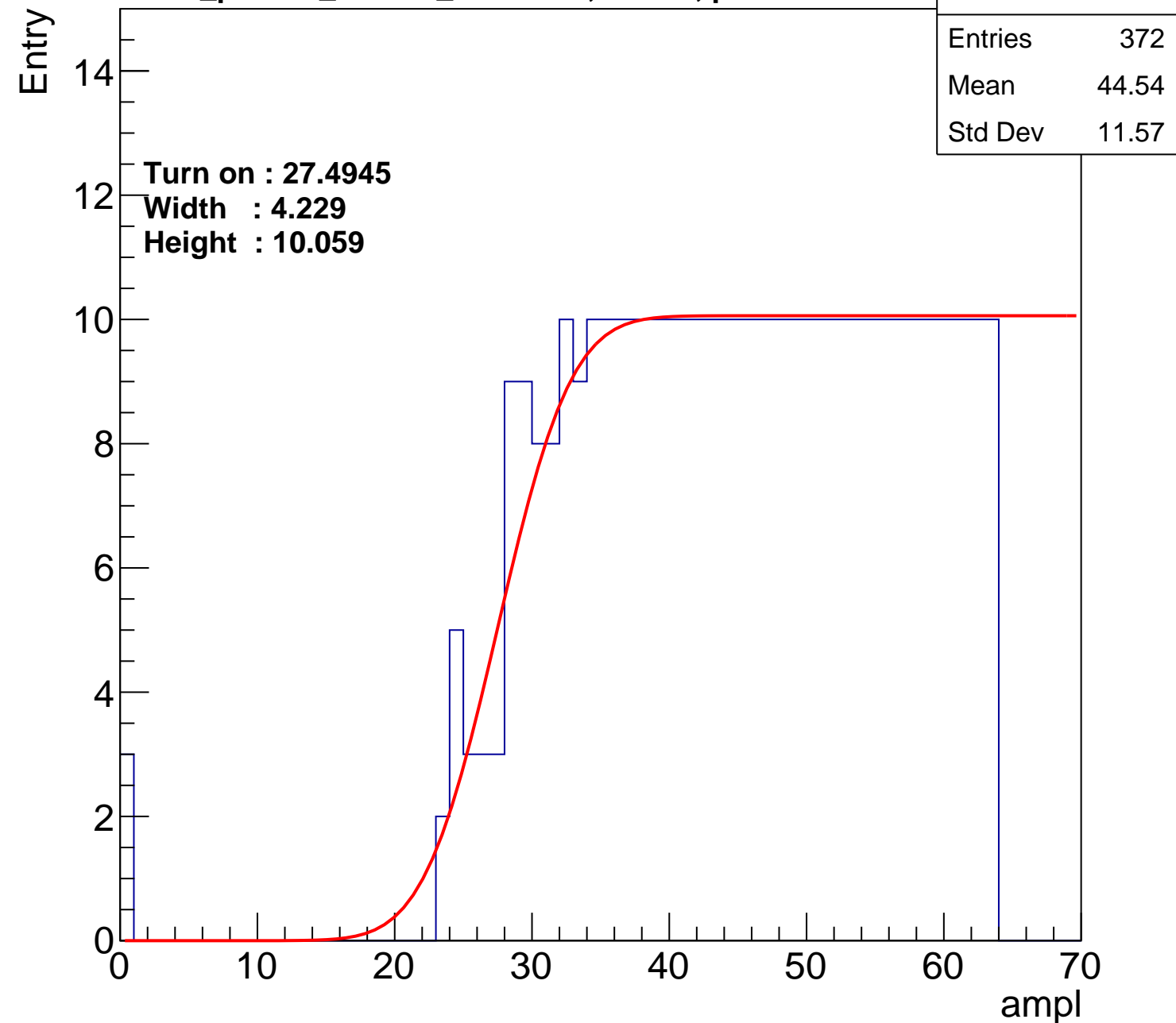
Width : 4.229

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.38
Std Dev	11.3

Turn on : 26.6191

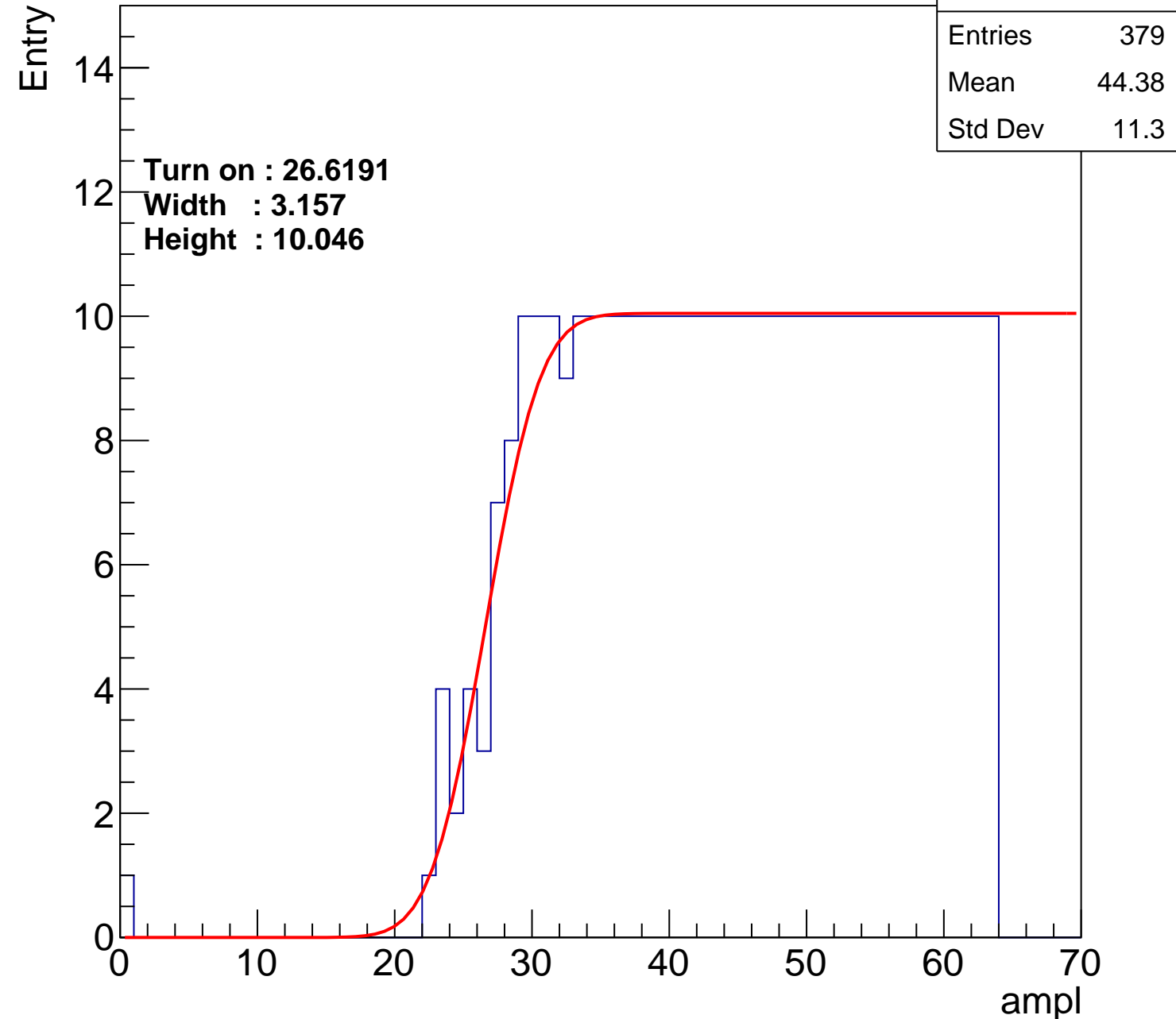
Width : 3.157

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.06
Std Dev	12.66

Turn on : 25.4184

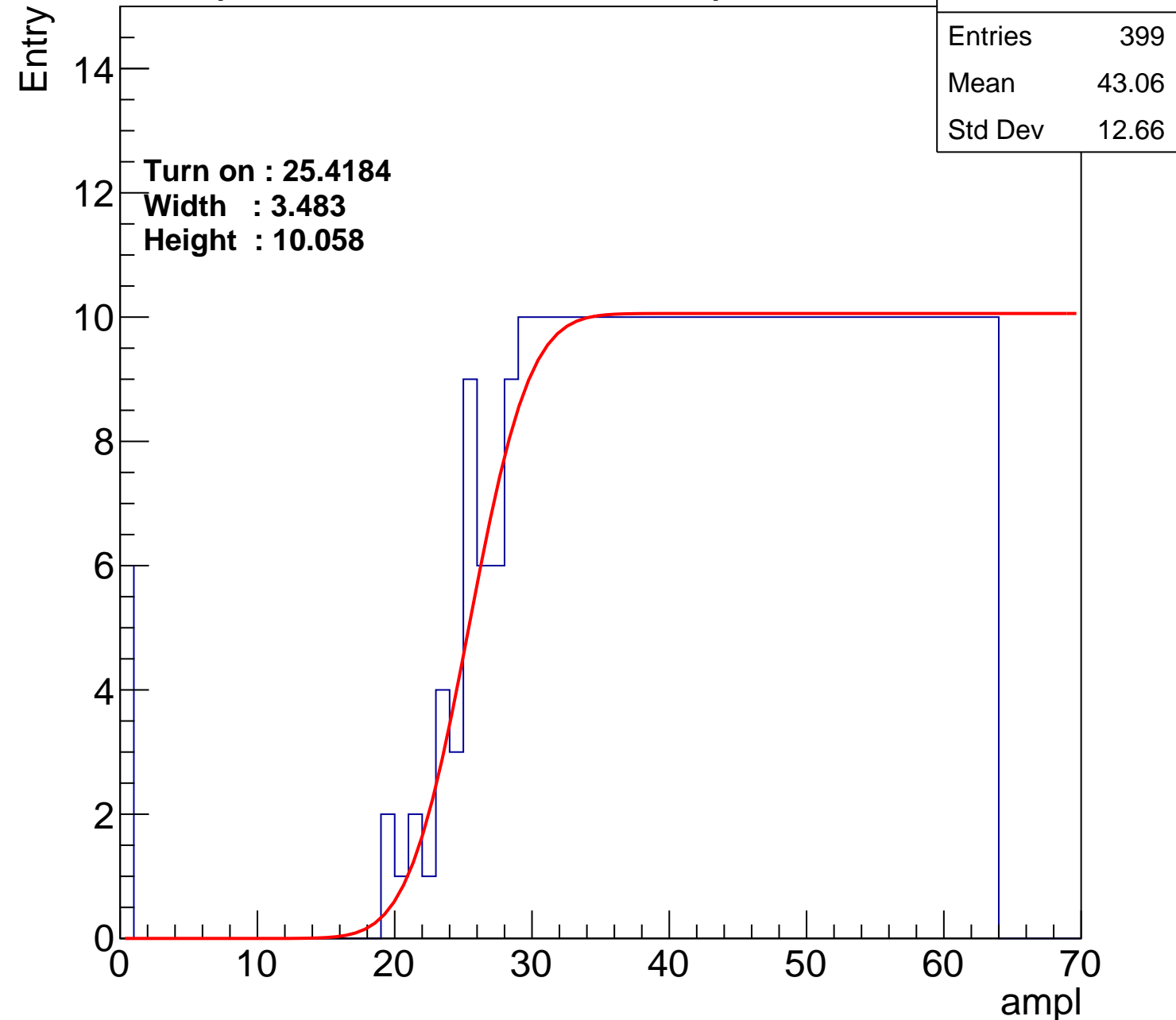
Width : 3.483

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.4772

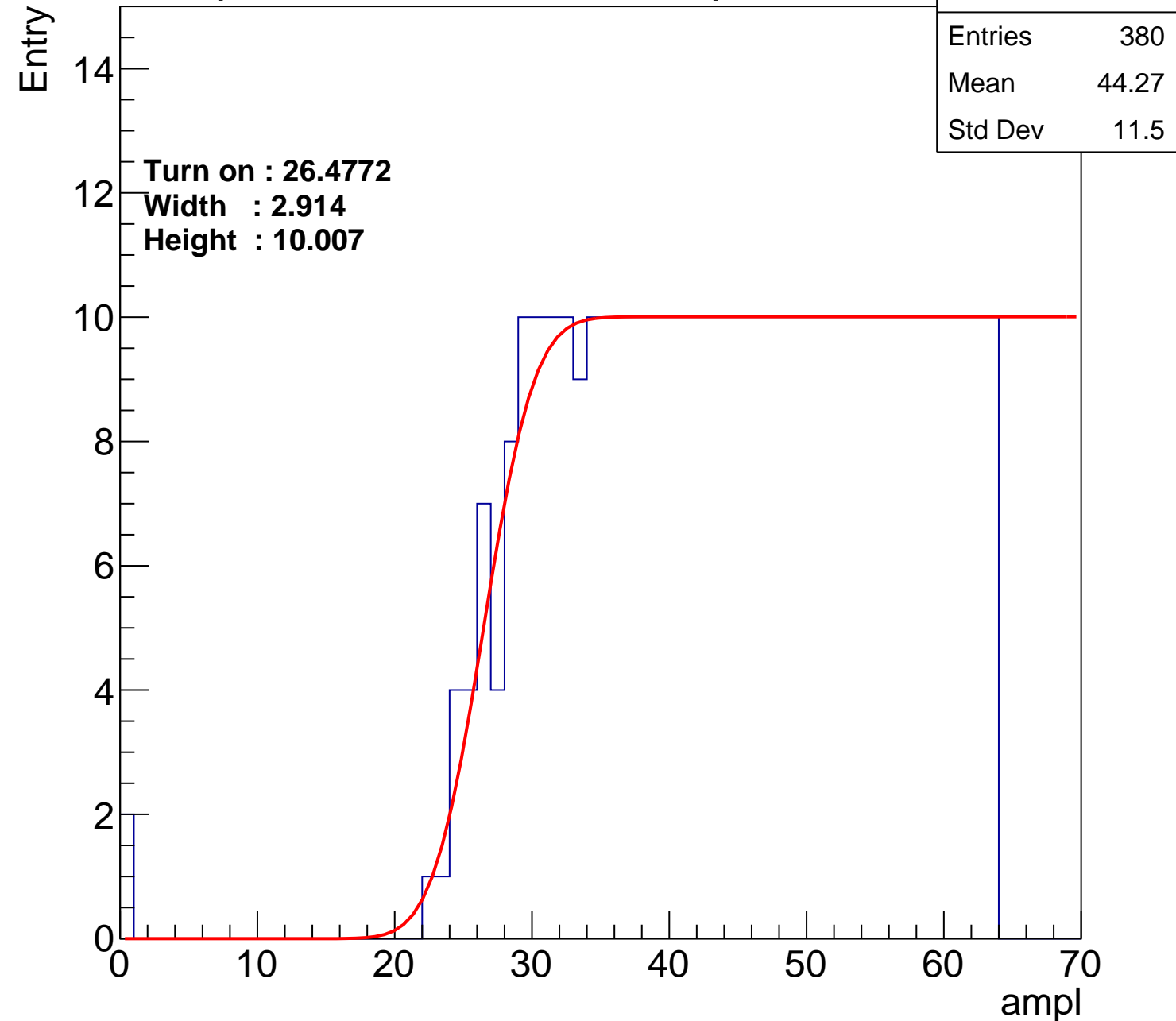
Width : 2.914

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.26
Std Dev	12.45

Turn on : 24.3677

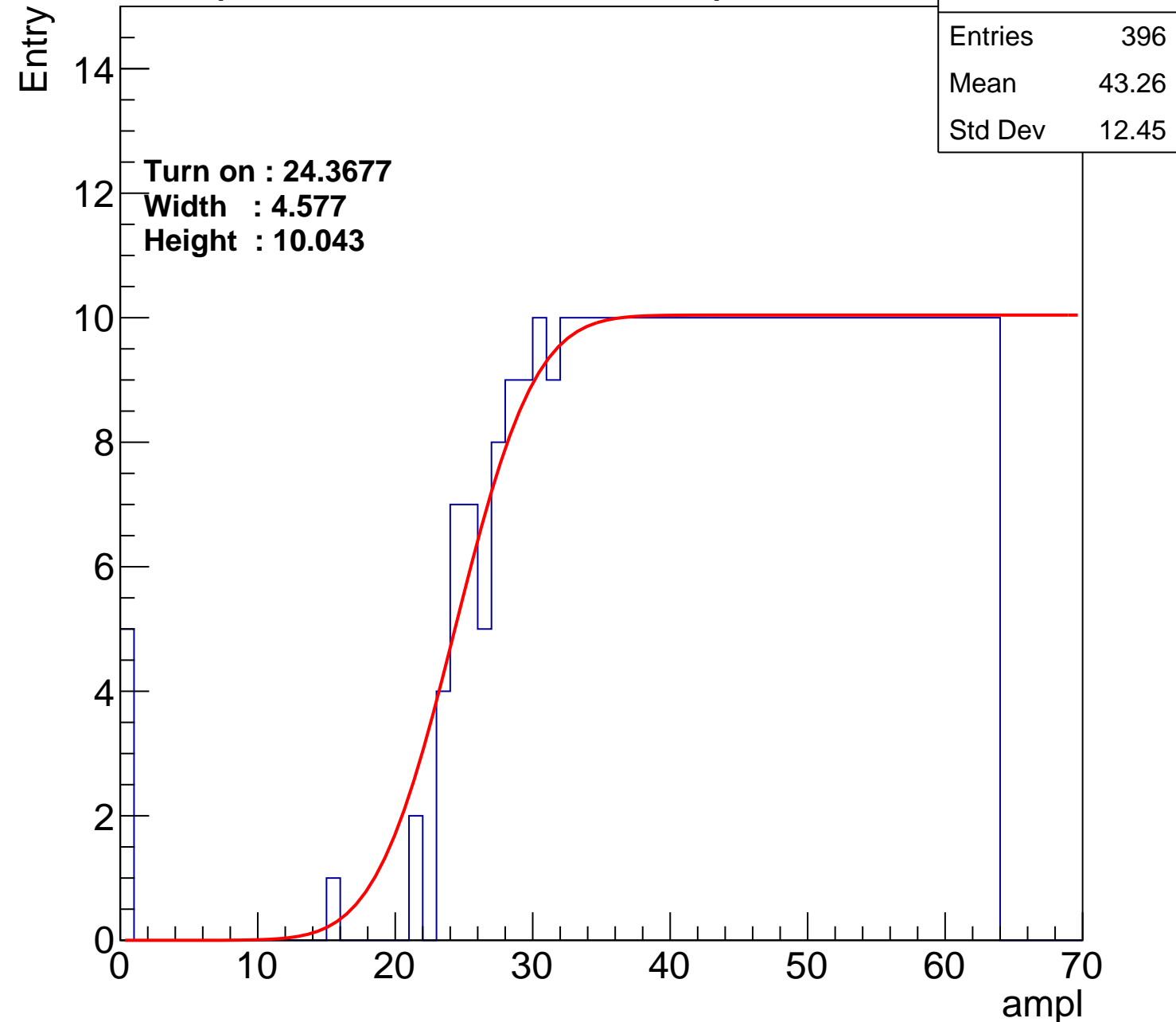
Width : 4.577

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch111

calib_packv5_042523_0143.root, FC#12, port B1

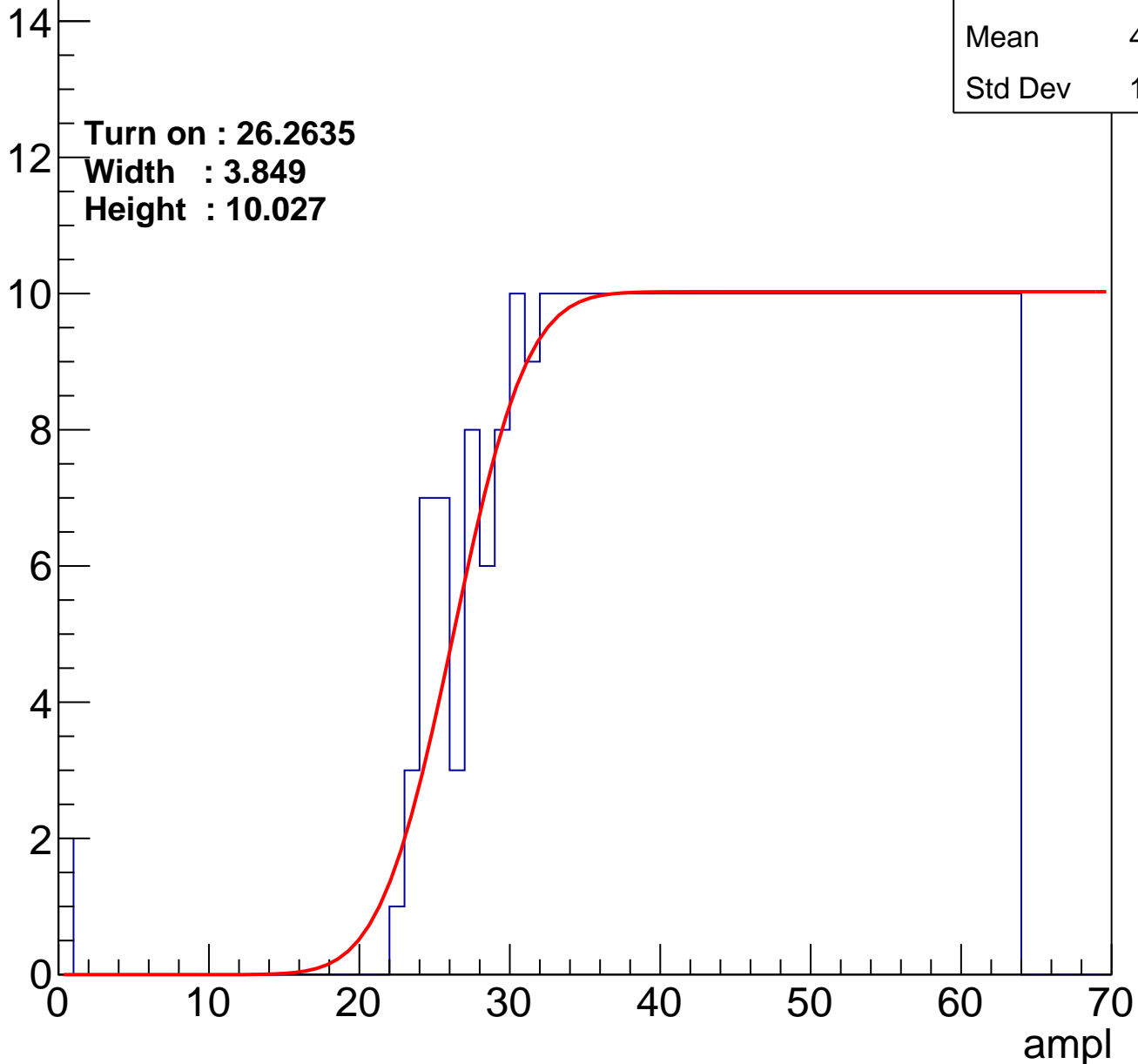
Entries	384
Mean	44.03
Std Dev	11.67

Turn on : 26.2635

Width : 3.849

Height : 10.027

Entry



B0L102S, U11-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 24.0200

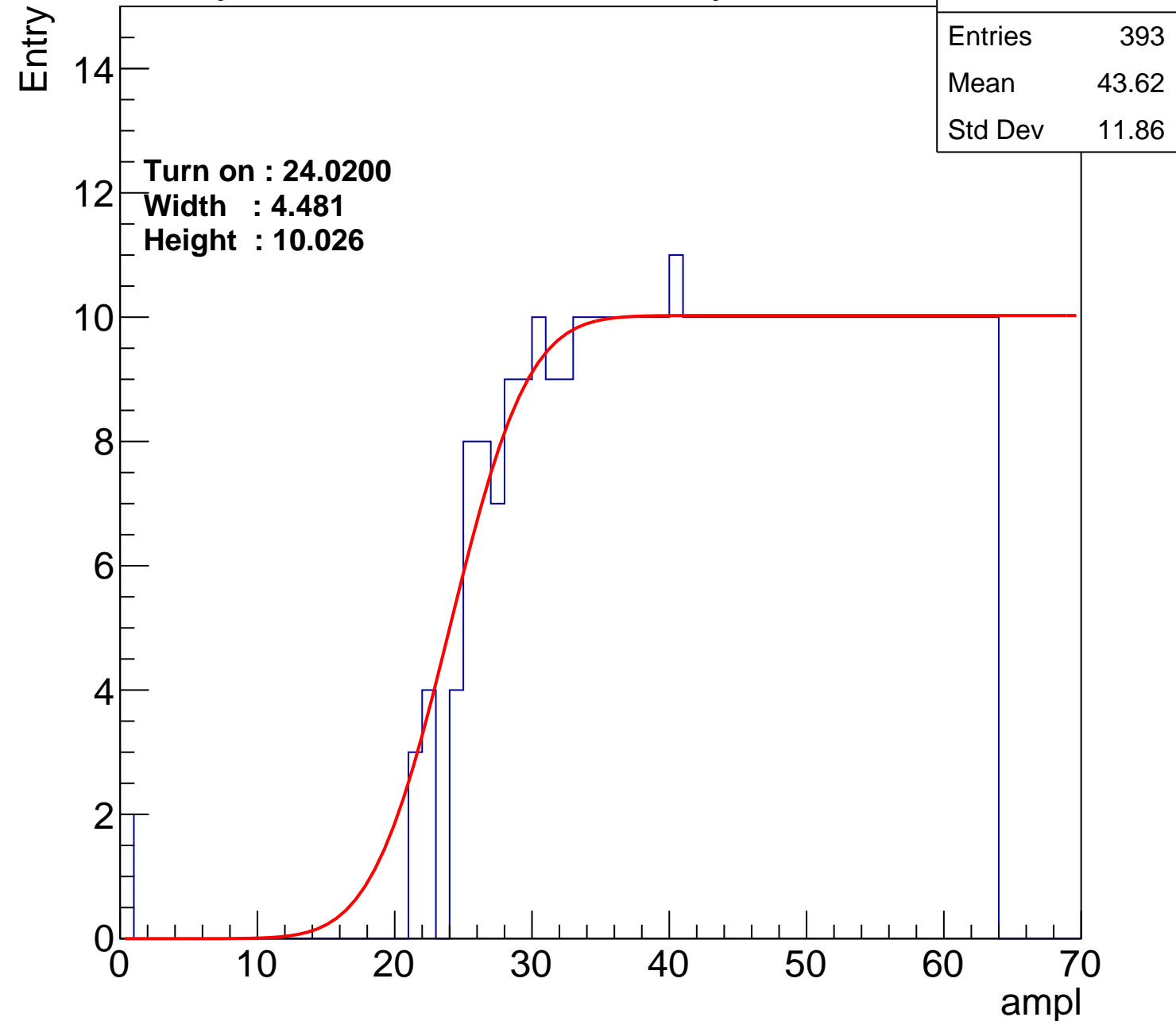
Width : 4.481

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.67
Std Dev	11.35

Turn on : 26.6412

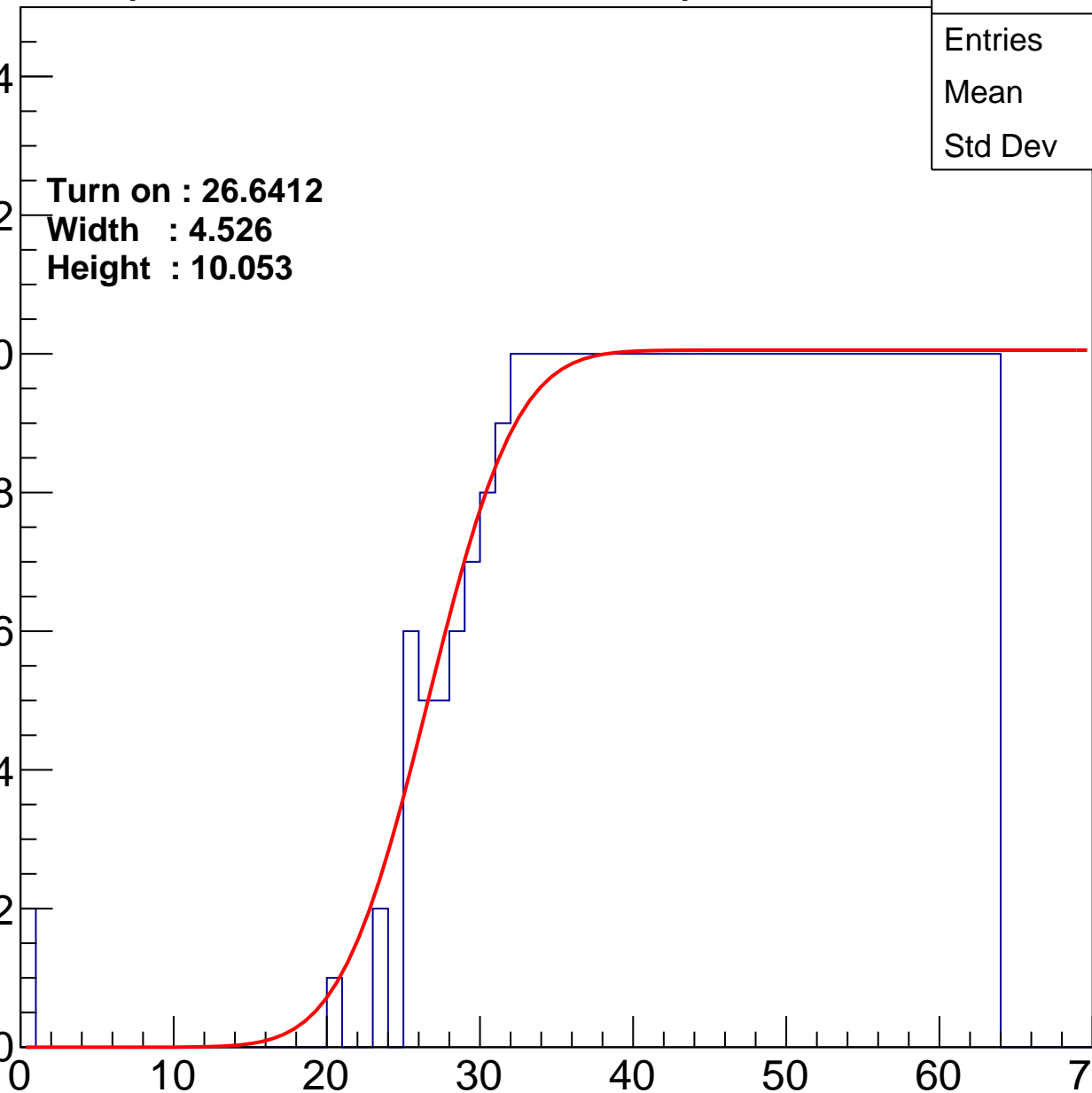
Width : 4.526

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.58
Std Dev	12.24

Turn on : 26.2390

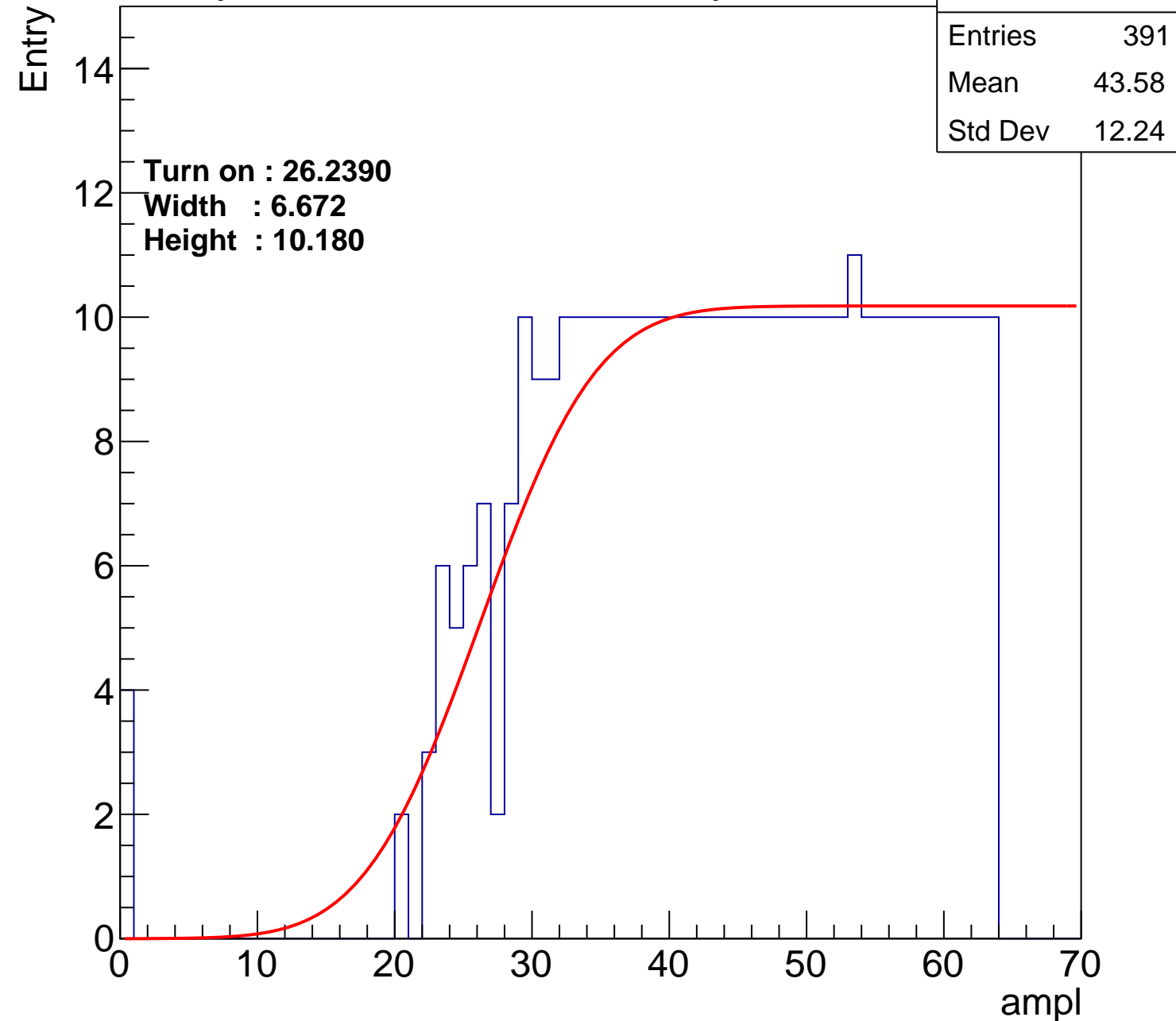
Width : 6.672

Height : 10.180

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.14
Std Dev	11.6

Turn on : 26.5049

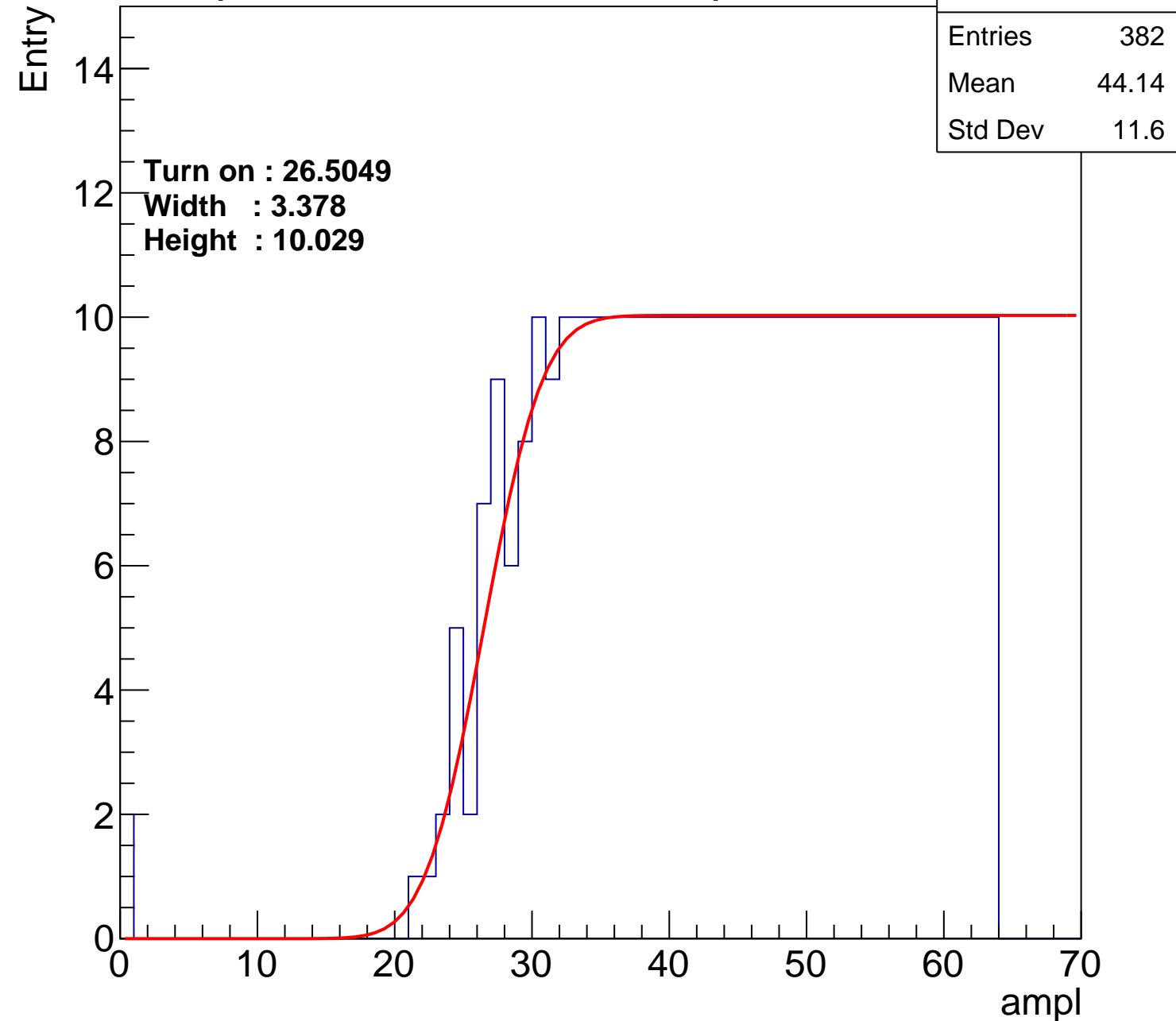
Width : 3.378

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch116

calib_packv5_042523_0143.root, FC#12, port B1

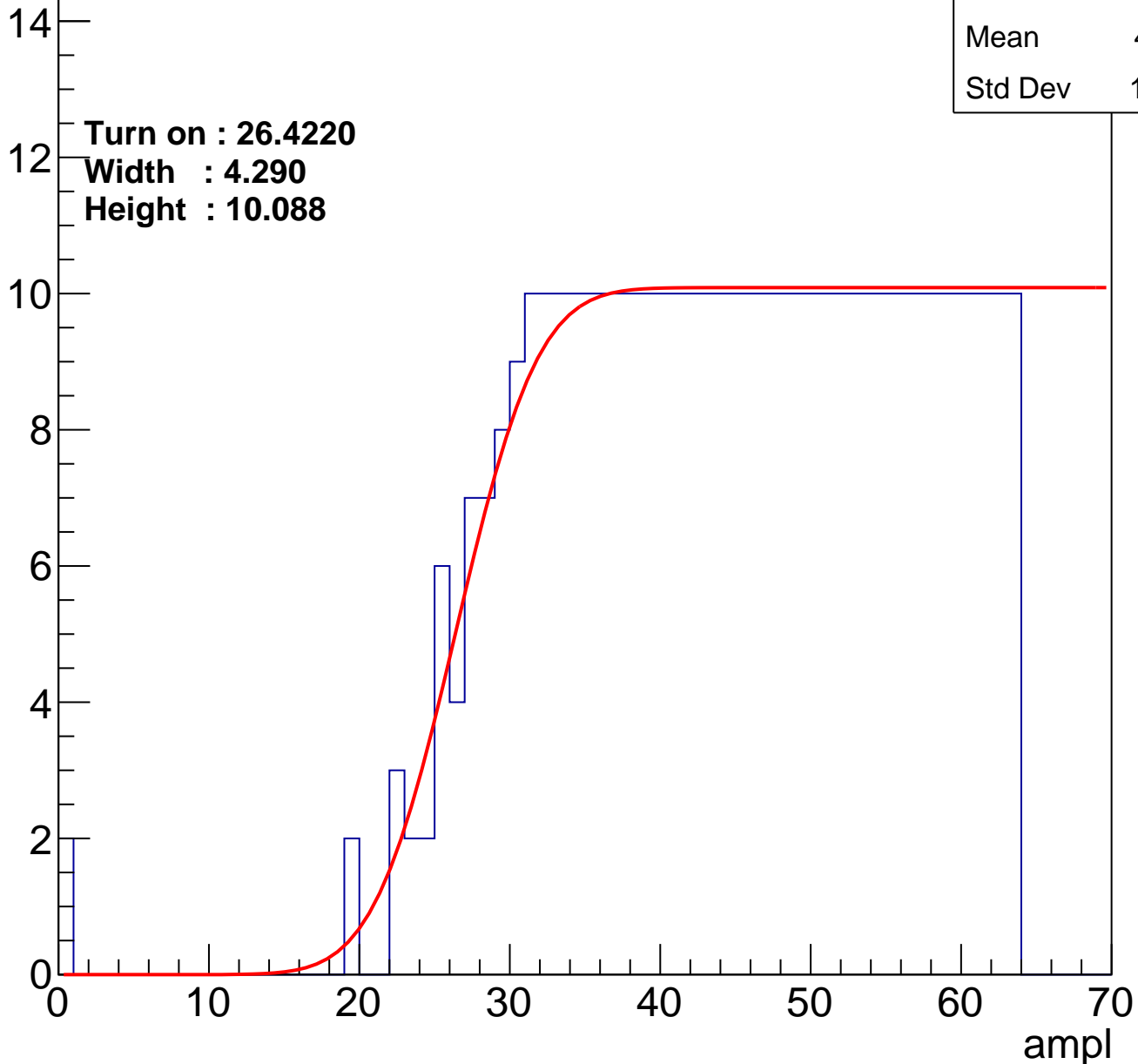
Entries	382
Mean	44.11
Std Dev	11.67

Turn on : 26.4220

Width : 4.290

Height : 10.088

Entry



B0L102S, U11-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.95
Std Dev	12.24

Turn on : 23.8294

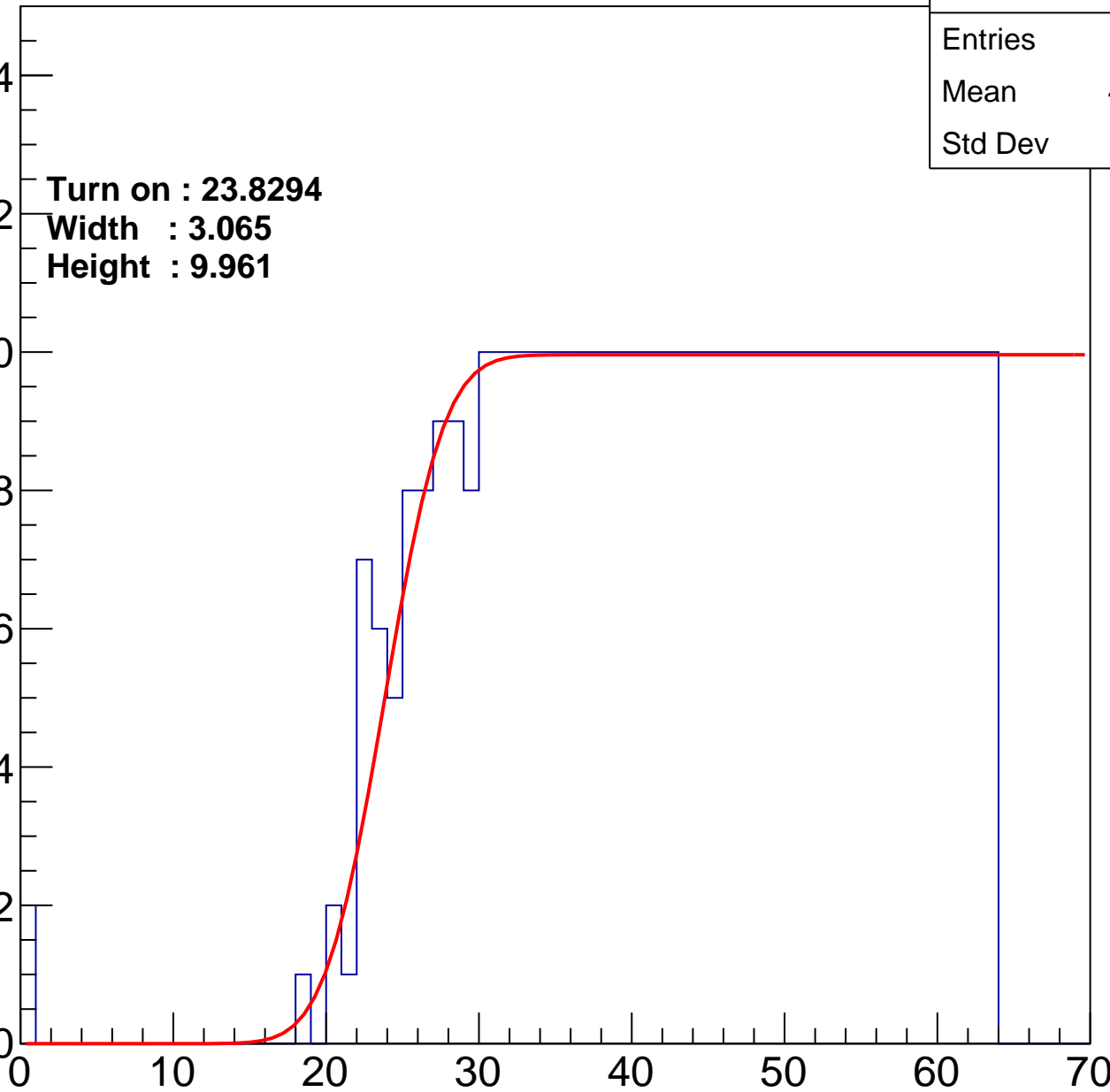
Width : 3.065

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.59
Std Dev	11.54

Turn on : 27.4192

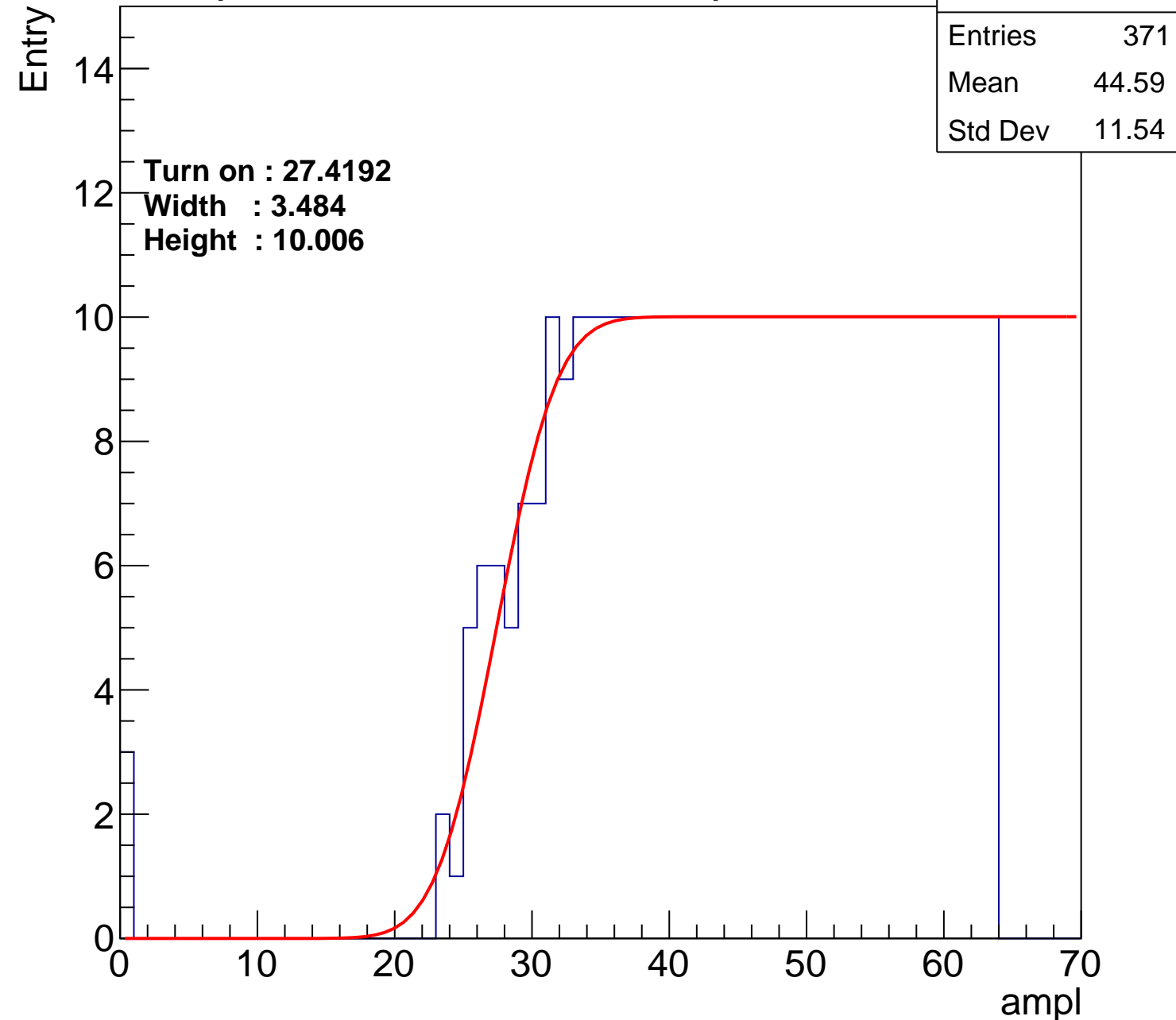
Width : 3.484

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch119

calib_packv5_042523_0143.root, FC#12, port B1

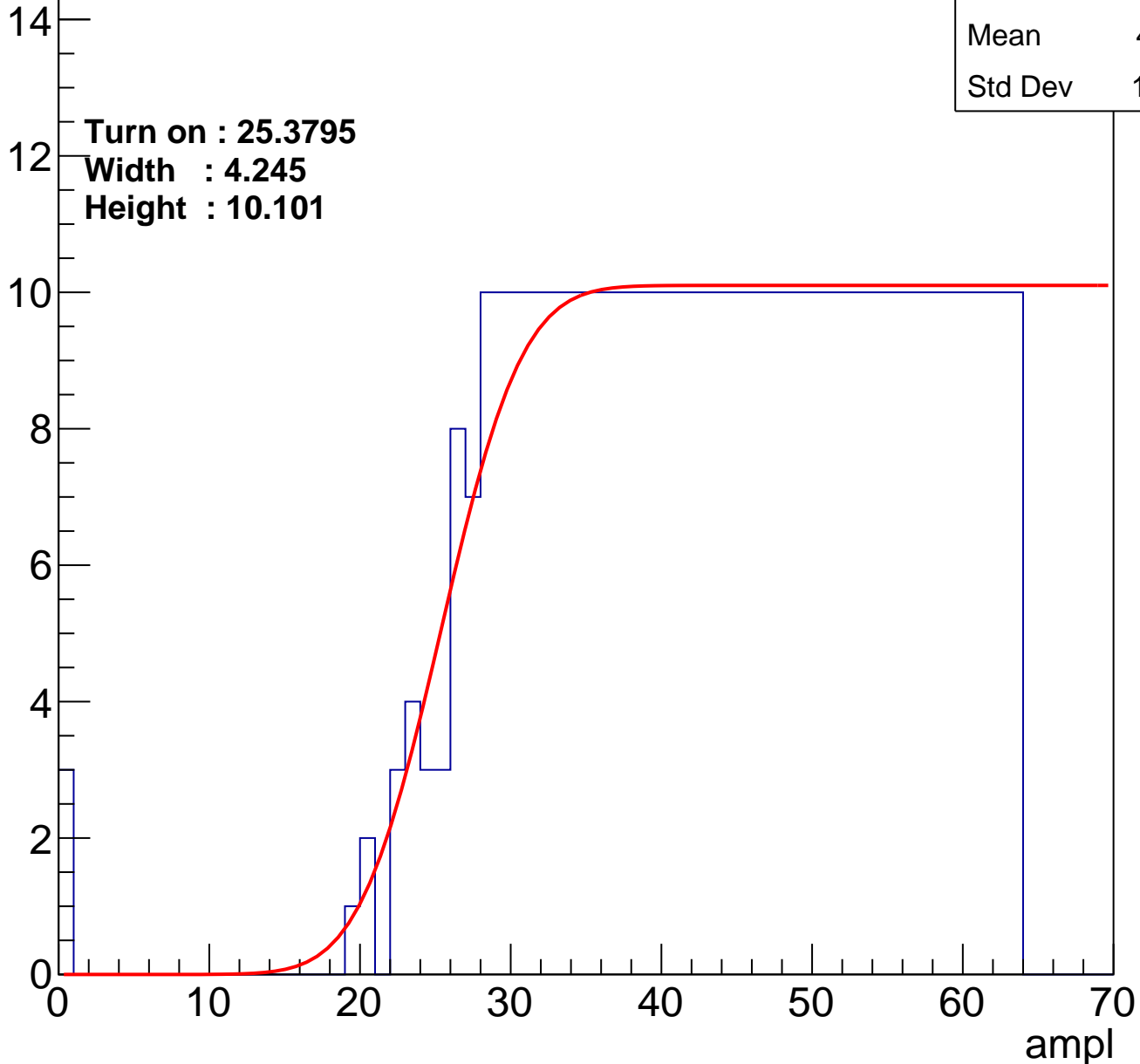
Entries	394
Mean	43.51
Std Dev	12.06

Turn on : 25.3795

Width : 4.245

Height : 10.101

Entry



B0L102S, U11-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.24
Std Dev	11.62

Turn on : 26.8651

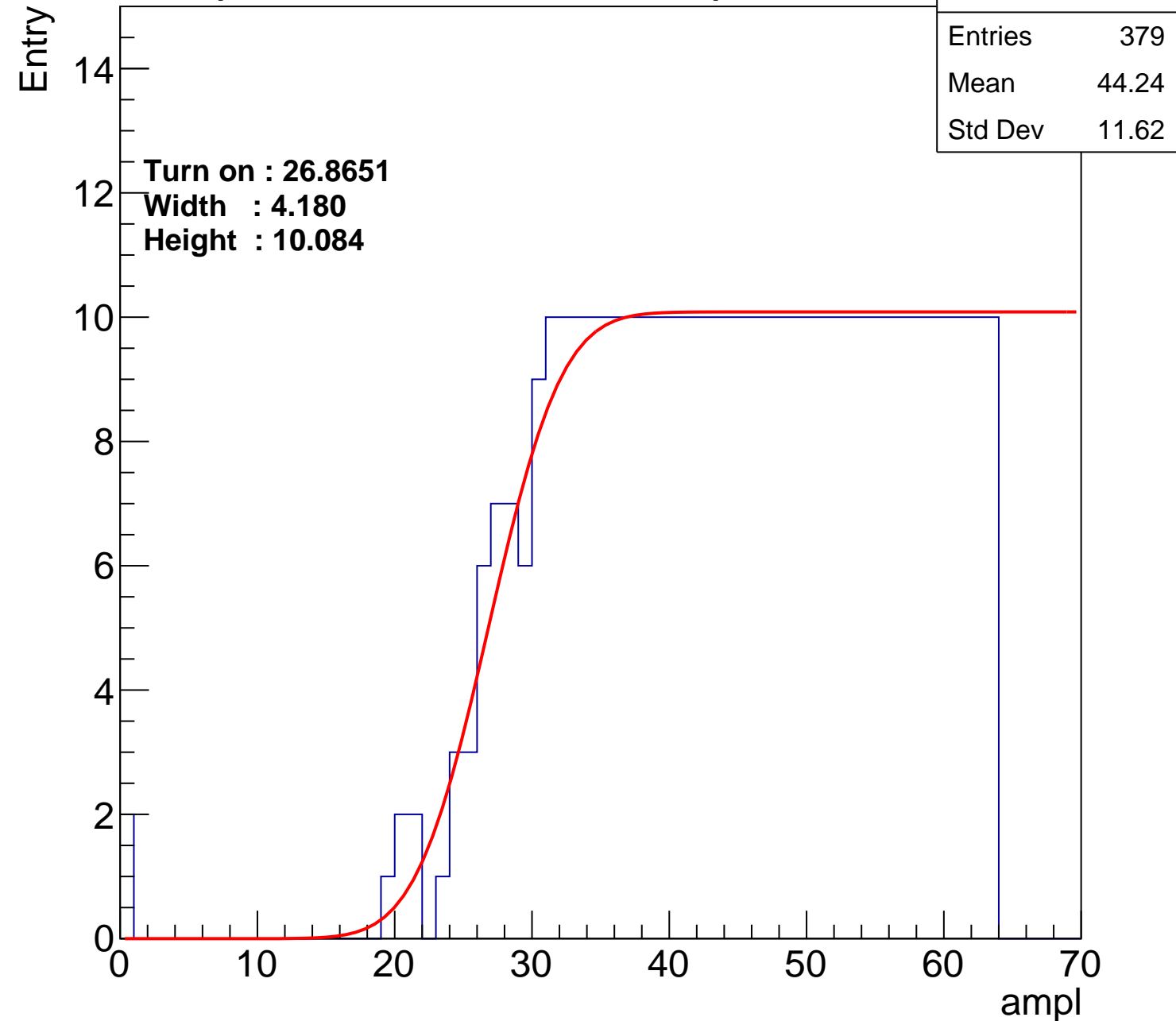
Width : 4.180

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch121

calib_packv5_042523_0143.root, FC#12, port B1

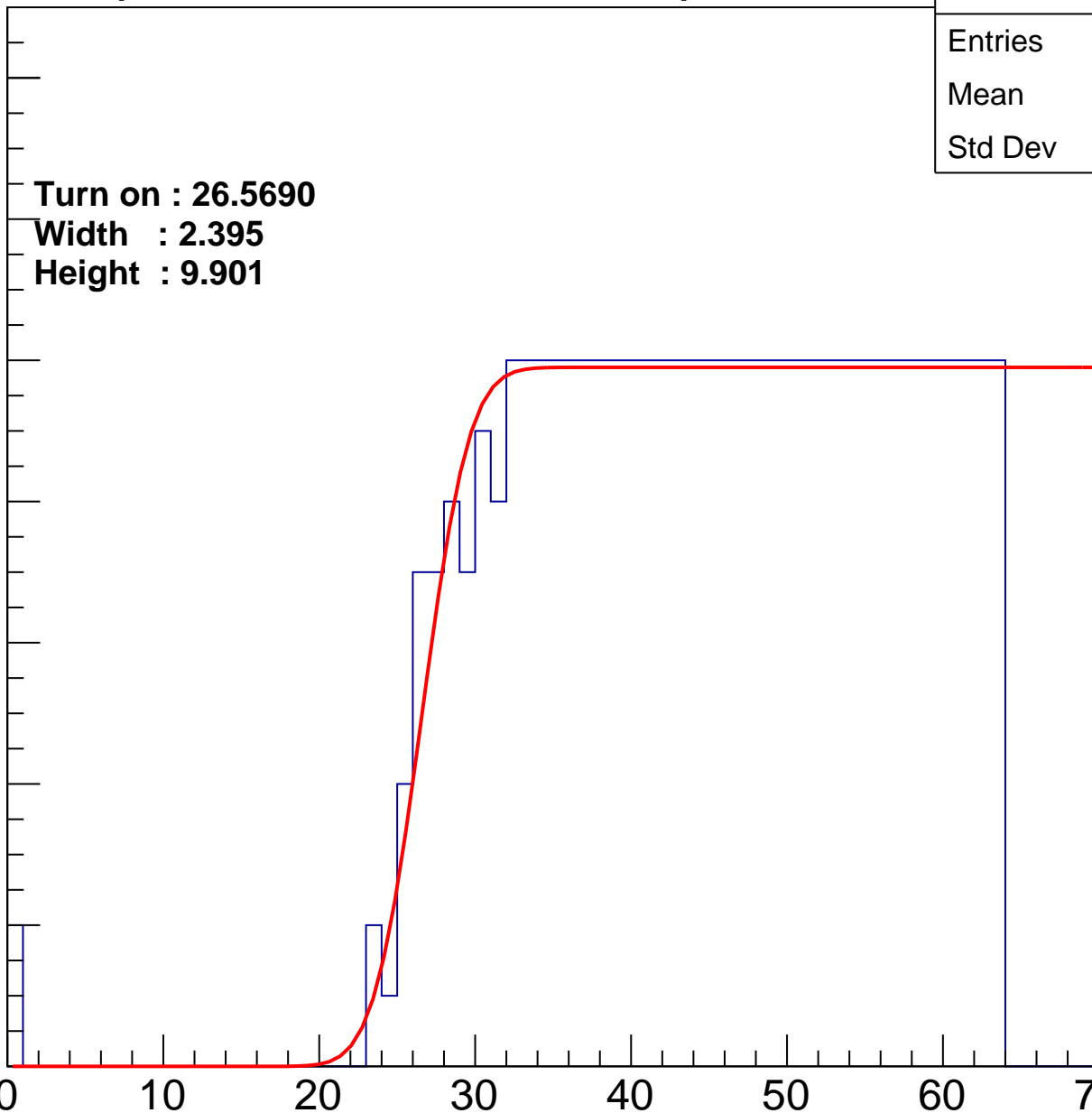
Entry

14
12
10
8
6
4
2
0

Turn on : 26.5690
Width : 2.395
Height : 9.901

Entries	375
Mean	44.5
Std Dev	11.4

ampl



B0L102S, U11-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.76
Std Dev	11.96

Turn on : 25.3532

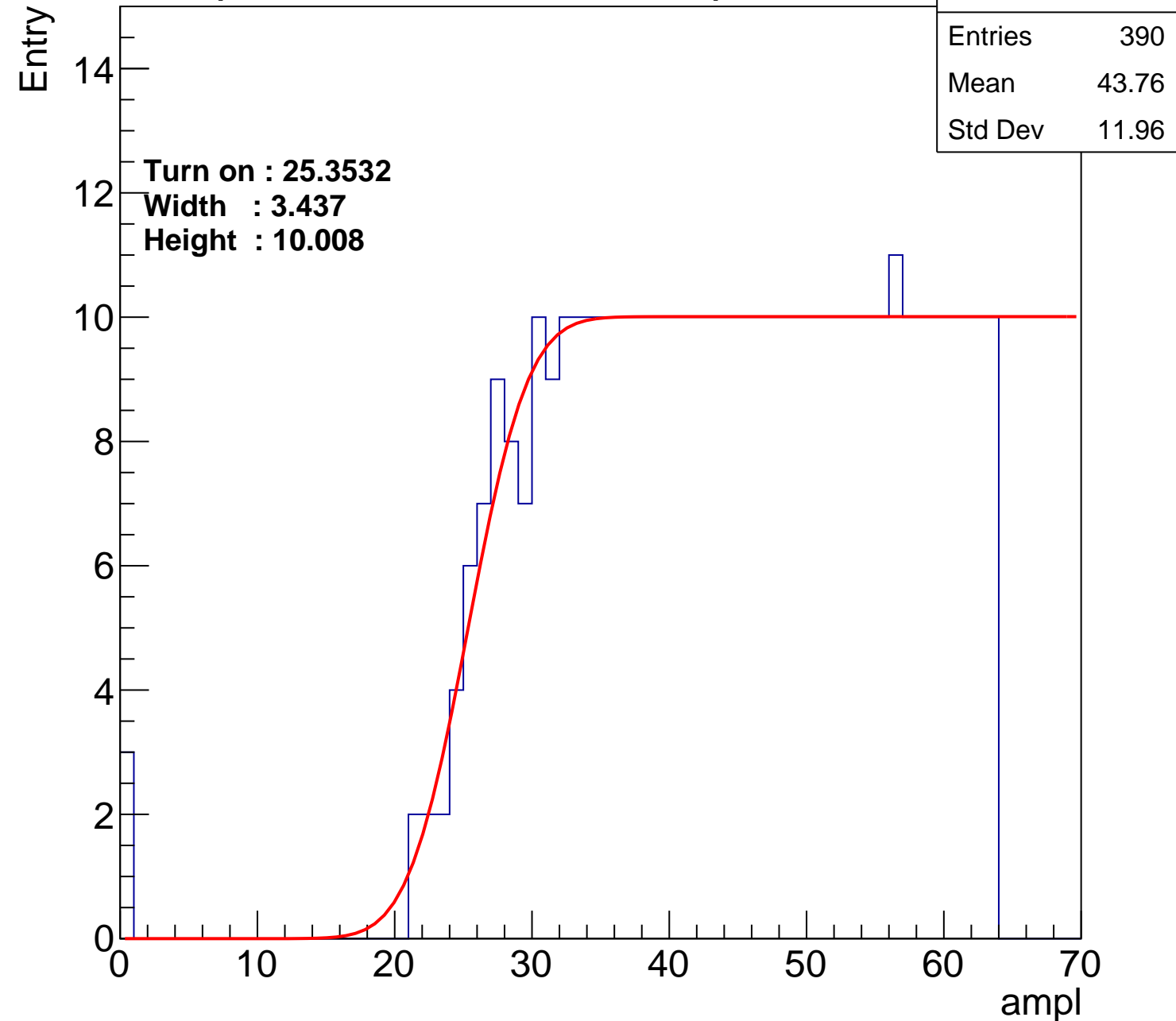
Width : 3.437

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.16
Std Dev	11.75

Turn on : 26.6561

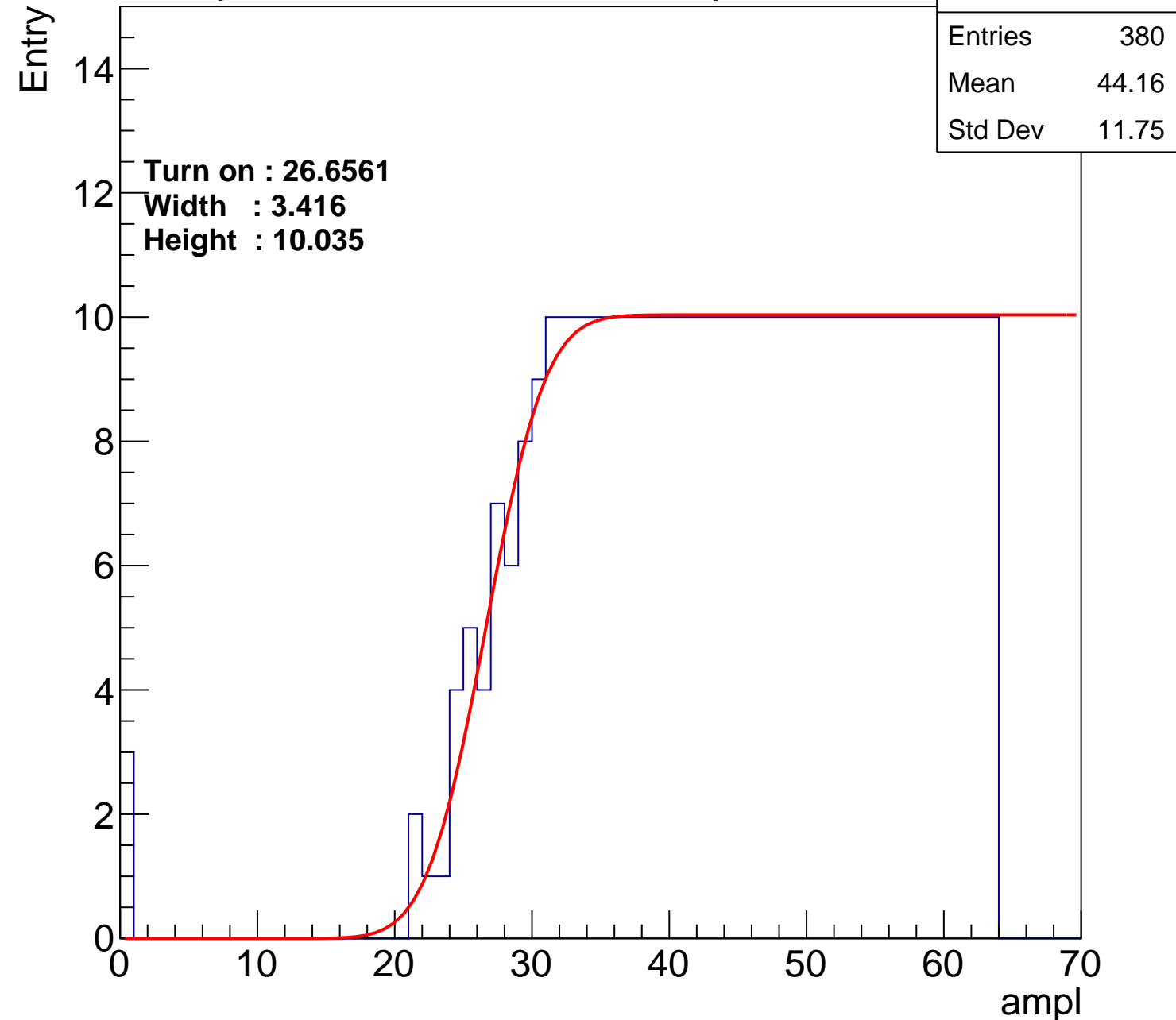
Width : 3.416

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	42.91
Std Dev	12.61

Turn on : 24.4668

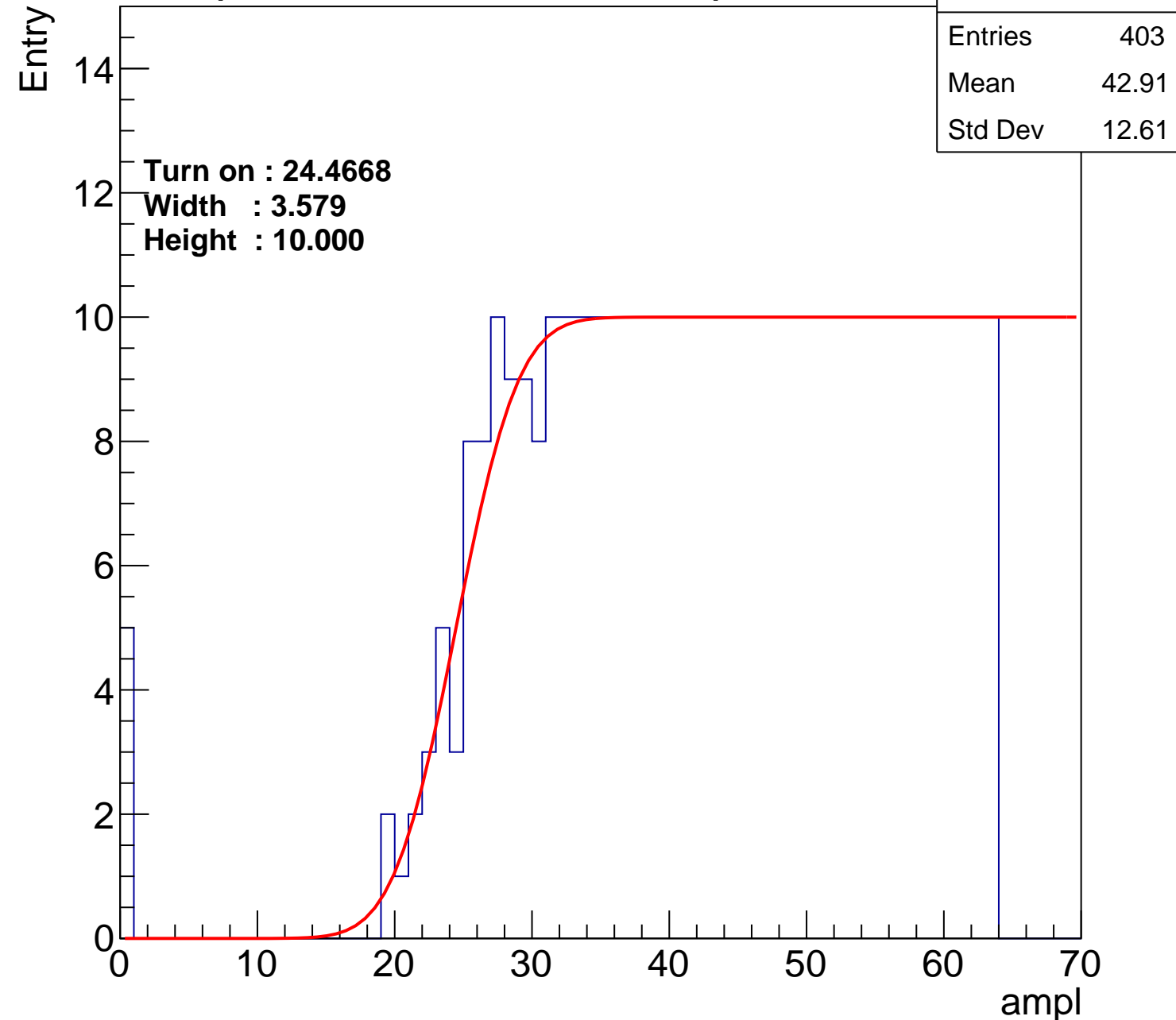
Width : 3.579

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.69
Std Dev	11.96

Turn on : 25.8381

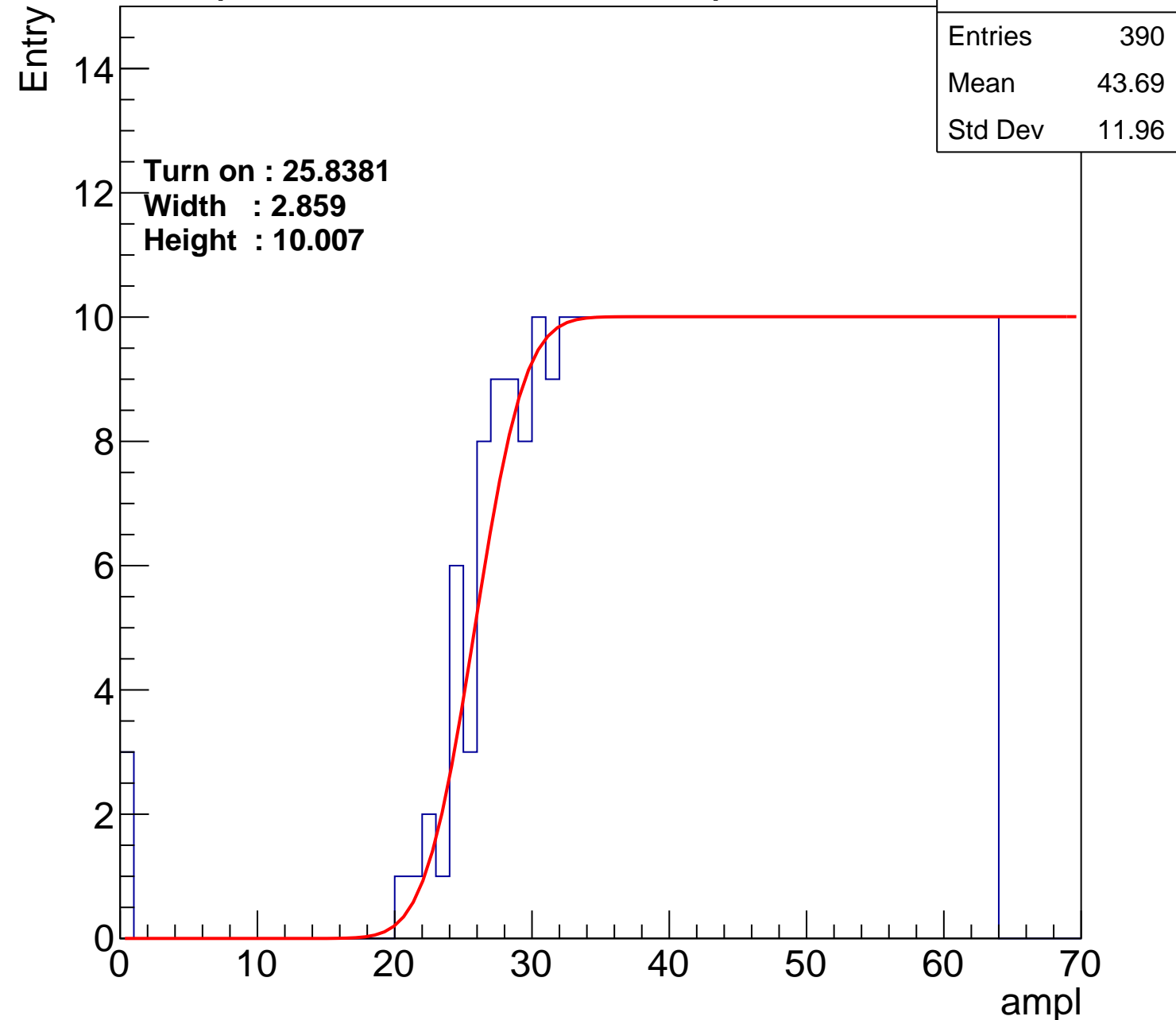
Width : 2.859

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.39
Std Dev	12.34

Turn on : 25.7093

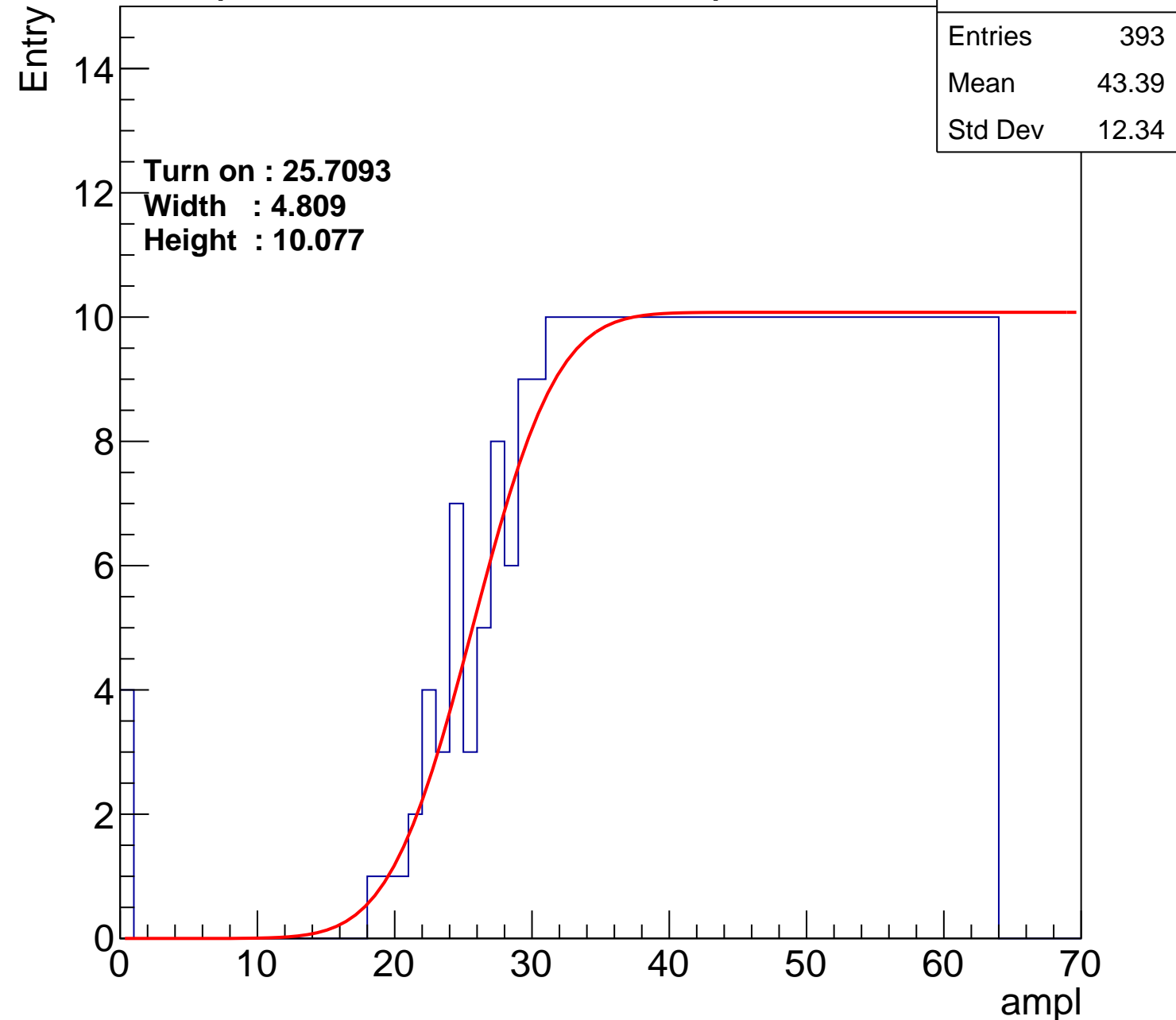
Width : 4.809

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.11
Std Dev	12.42

Turn on : 25.1463

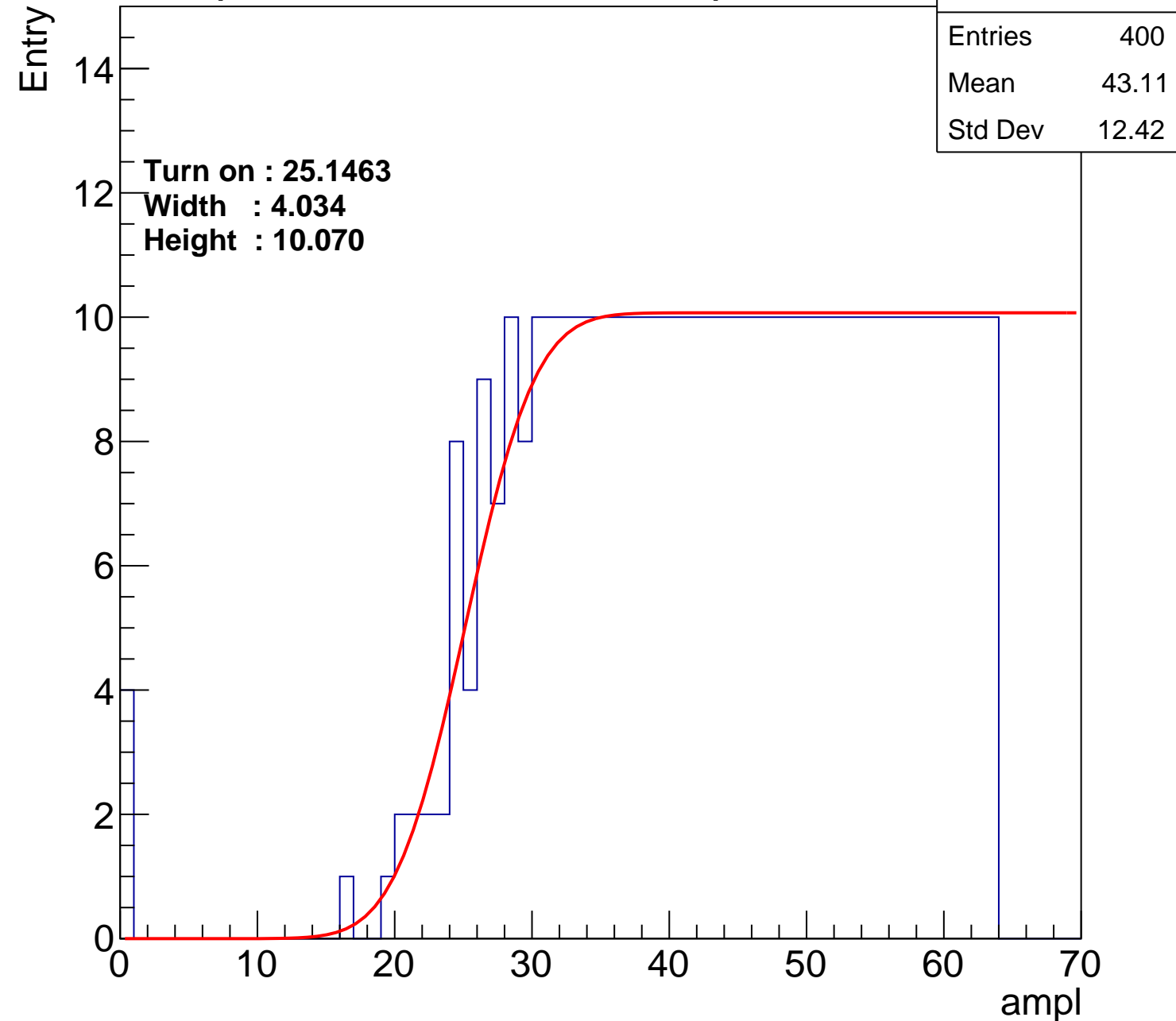
Width : 4.034

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U11-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.11
Std Dev	12.42

Turn on : 25.1463

Width : 4.034

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl

