

B0L101S, U21-ch0, adc0

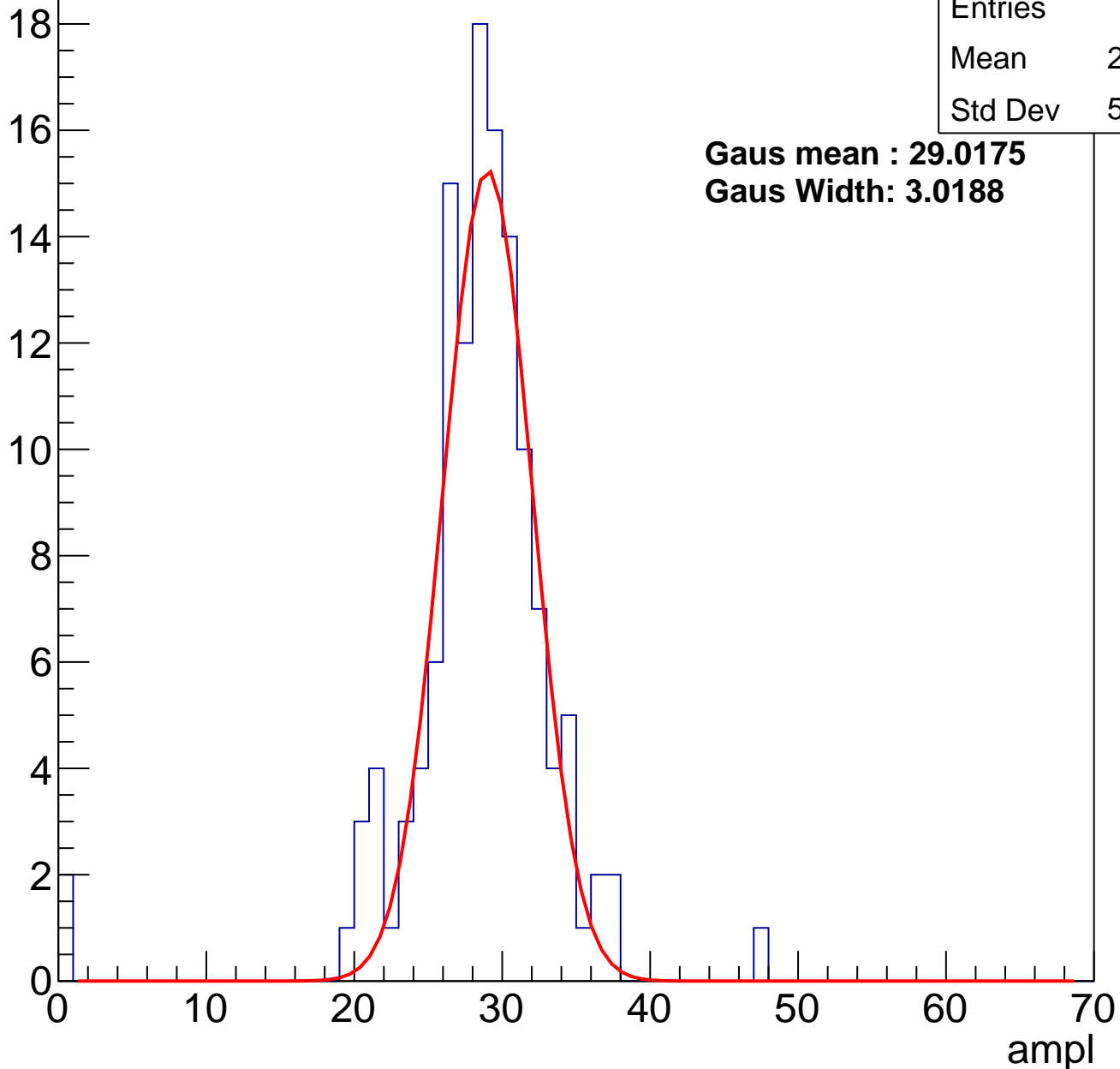
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	27.98
Std Dev	5.236

Gaus mean : 29.0175

Gaus Width: 3.0188

Entry



B0L101S, U21-ch0, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	35.36
Std Dev	3.77

Gaus mean : 36.1893

Gaus Width: 3.7506

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

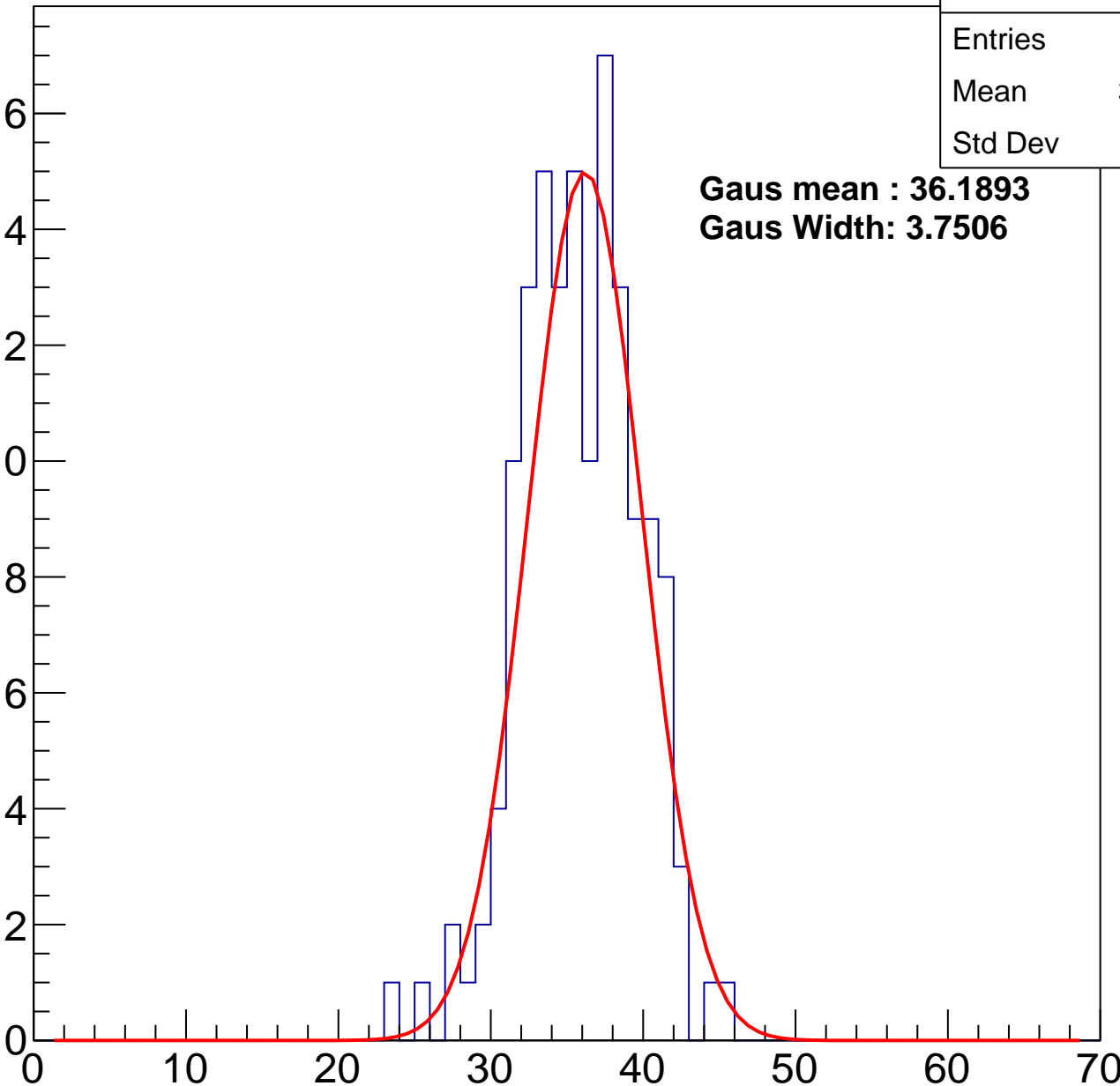
40

50

60

70

ampl



B0L101S, U21-ch0, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	41.85
Std Dev	3.605

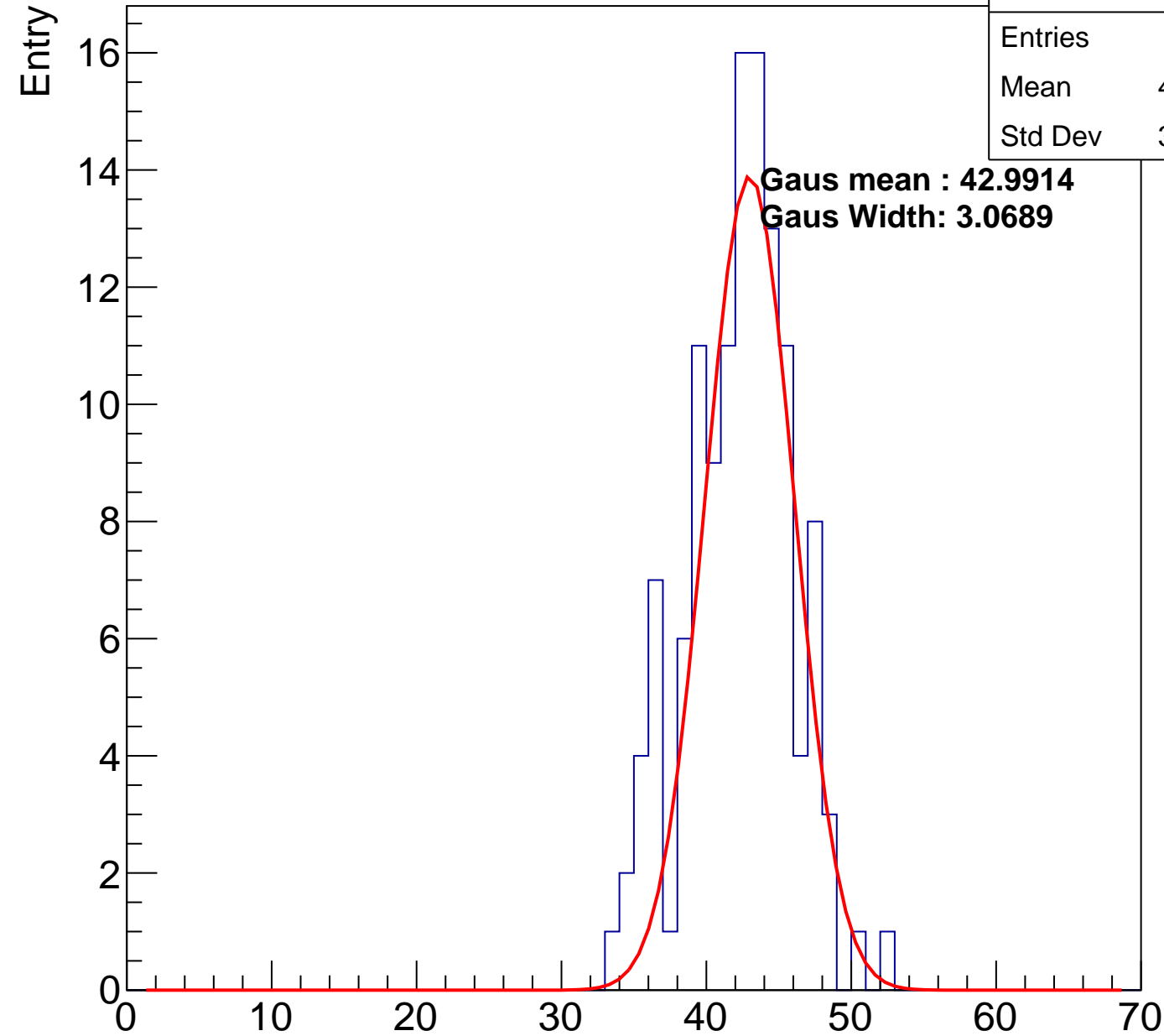
Gaus mean : 42.9914

Gaus Width: 3.0689

Entry

16
14
12
10
8
6
4
2
0

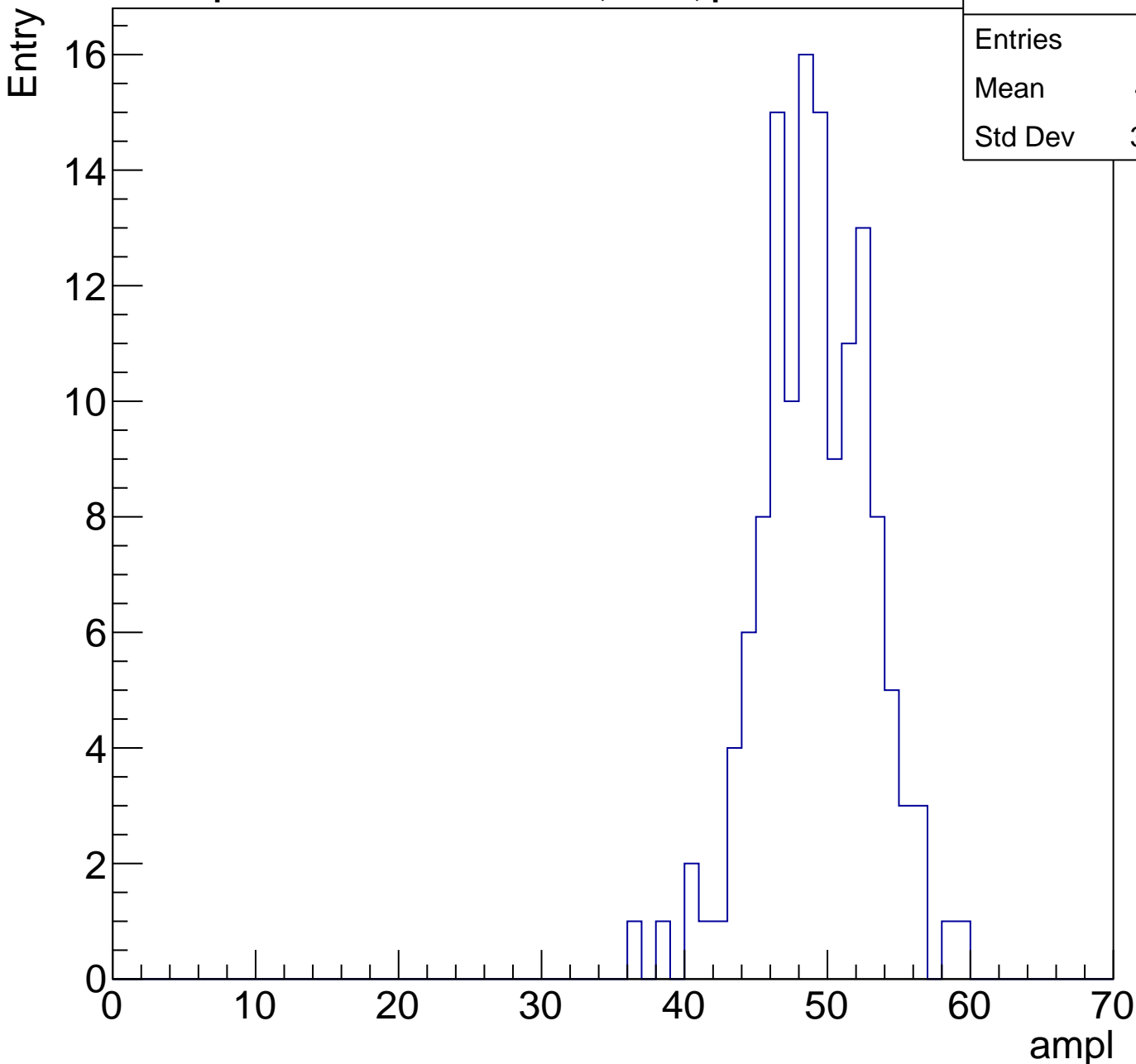
ampl



B0L101S, U21-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

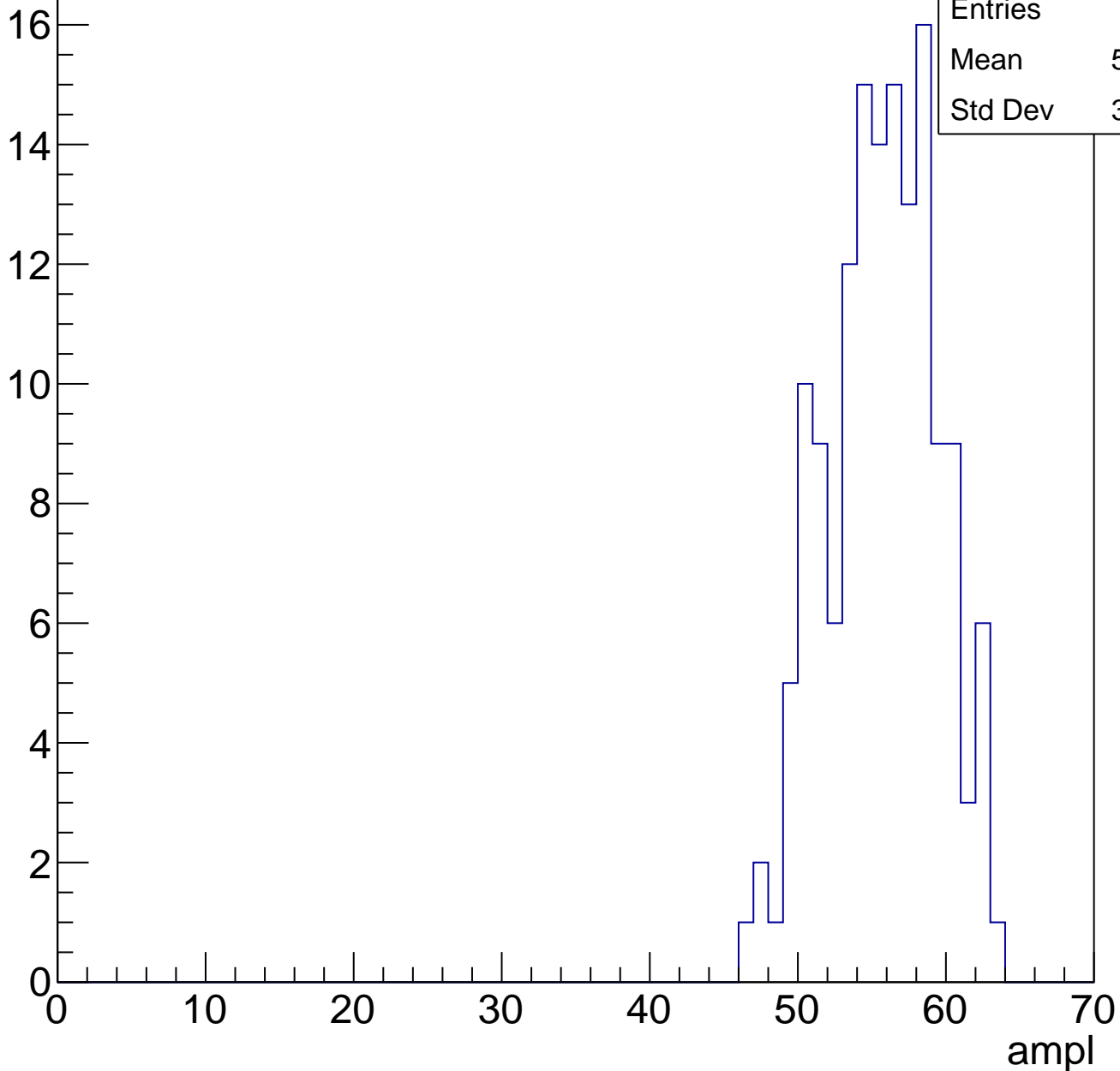
Entries	134
Mean	48.71
Std Dev	3.888



B0L101S, U21-ch0, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch0, adc5

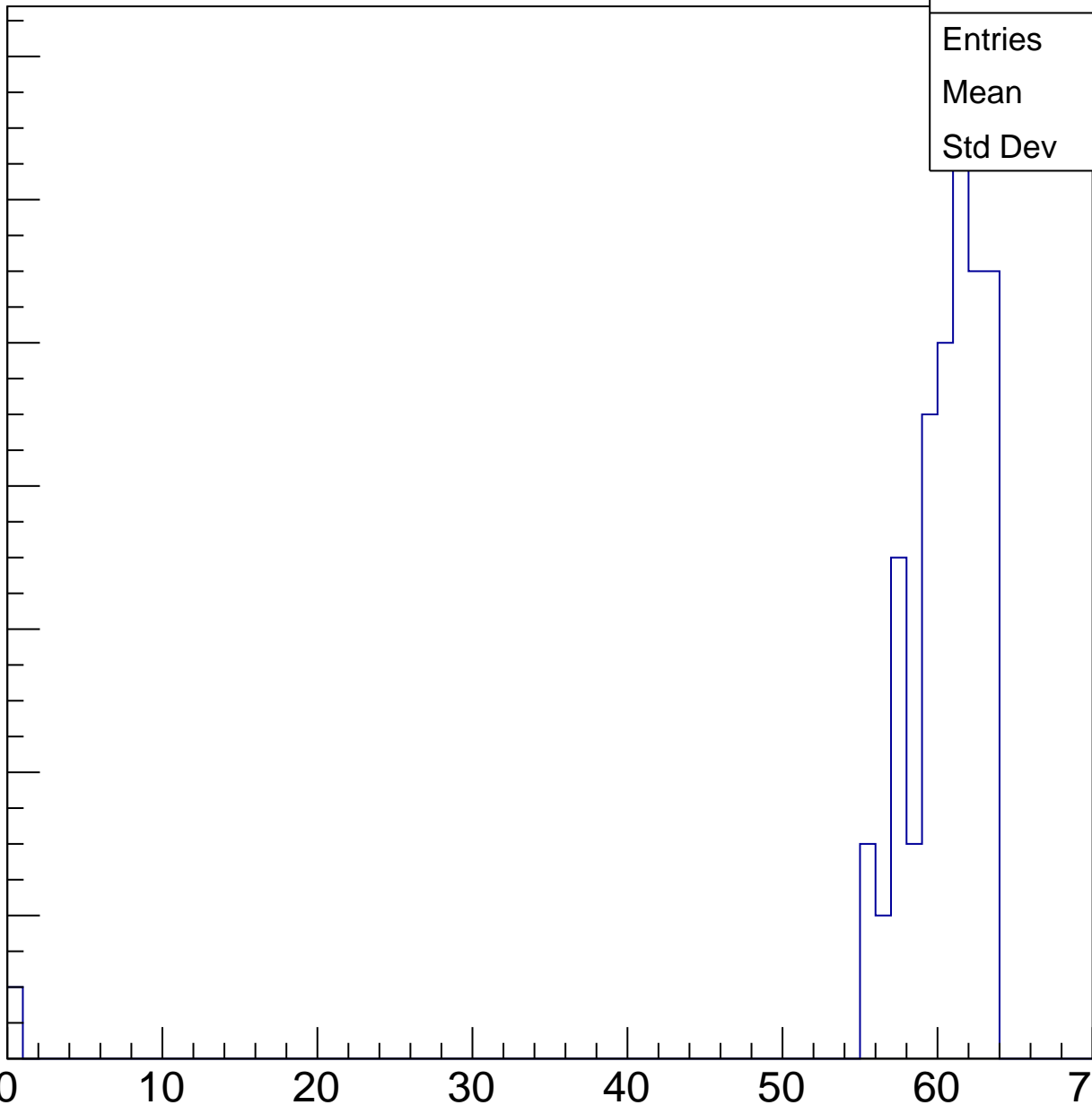
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	59.3
Std Dev	7.424

ampl

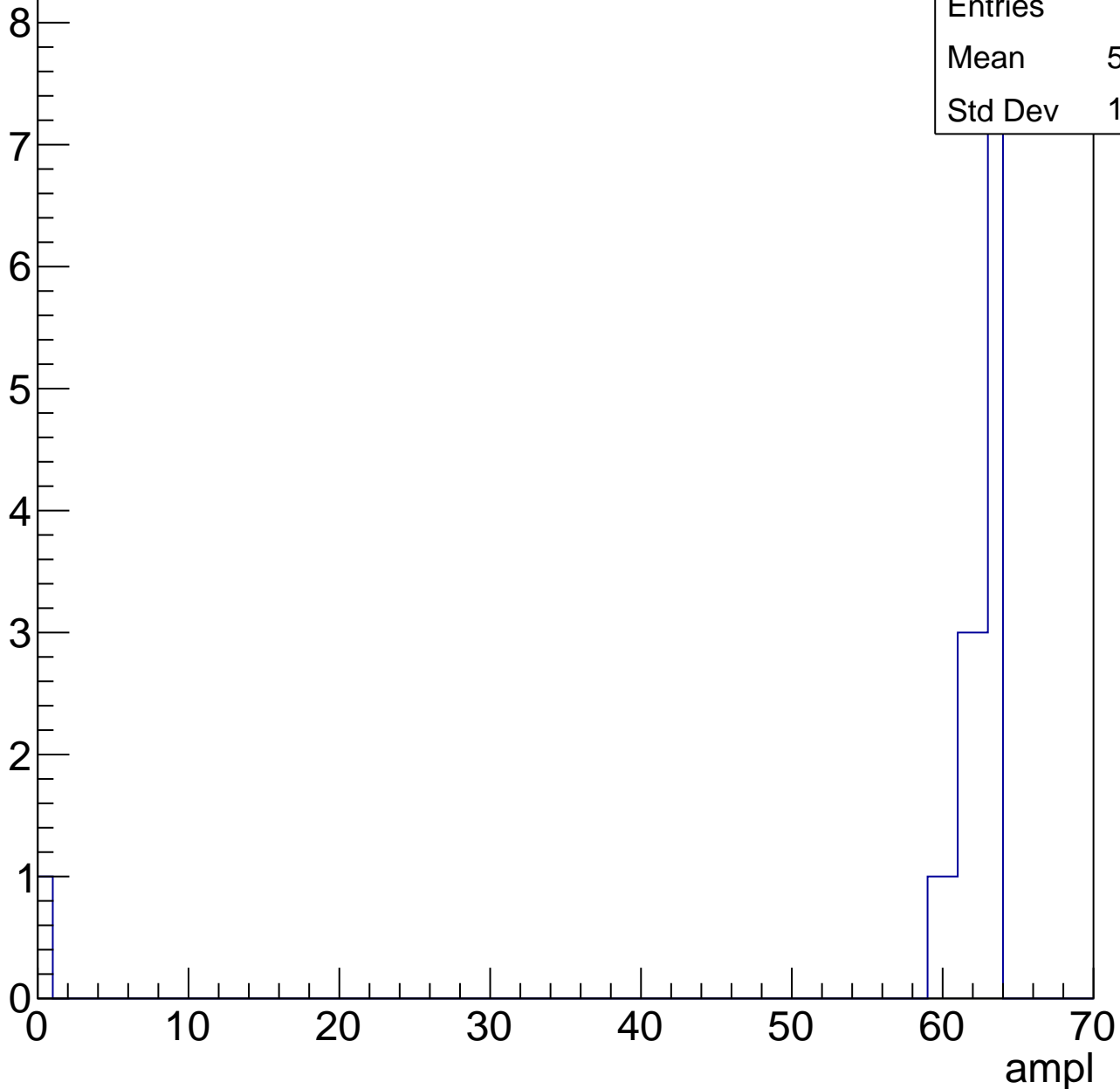


B0L101S, U21-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	58.35
Std Dev	14.64



B0L101S, U21-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

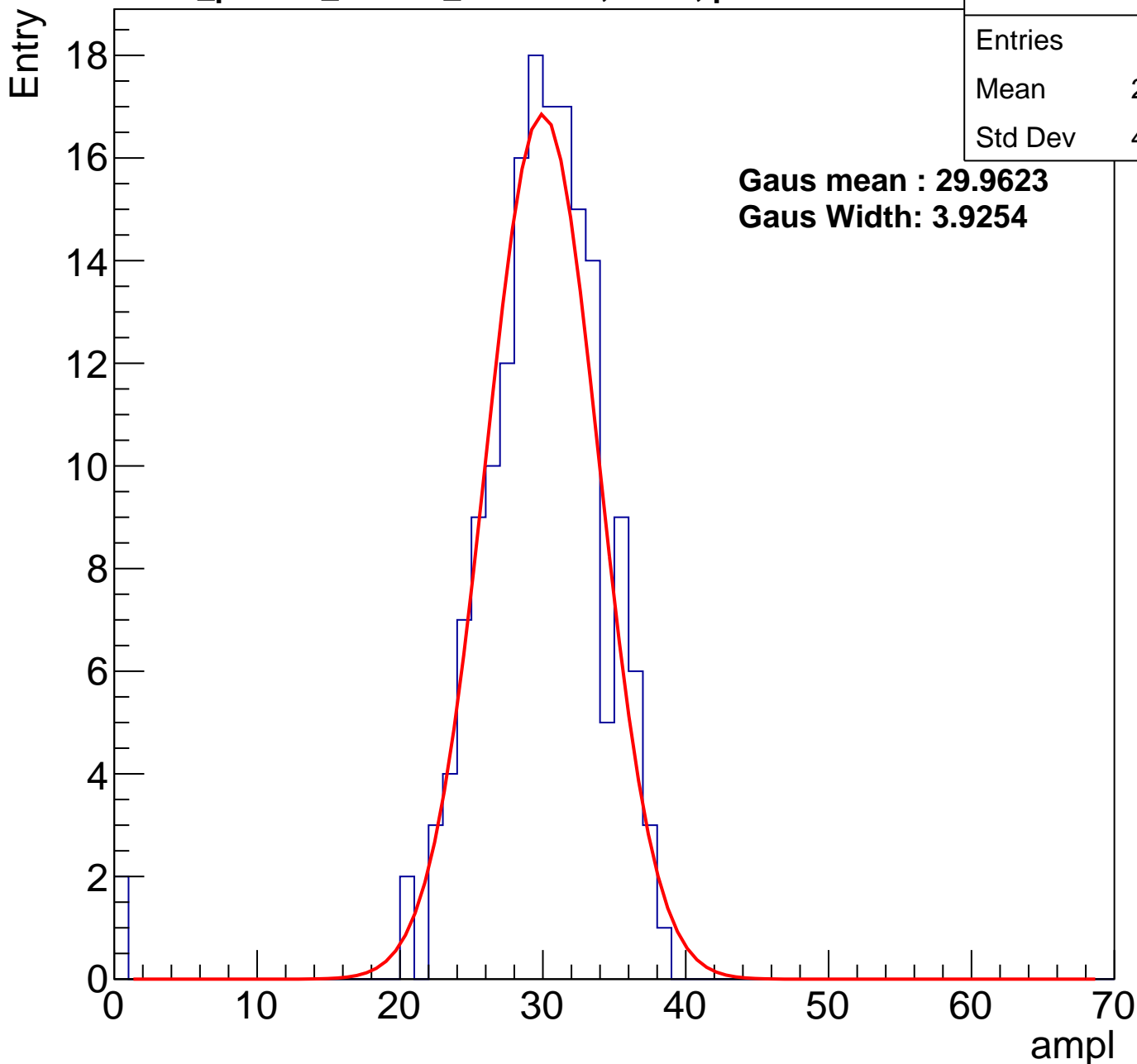
B0L101S, U21-ch1, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	29.26
Std Dev	4.885

Gaus mean : 29.9623

Gaus Width: 3.9254



B0L101S, U21-ch1, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

123

Mean

36.93

Std Dev

3.248

Gaus mean : 37.4008

Gaus Width: 3.2766

0

10

20

30

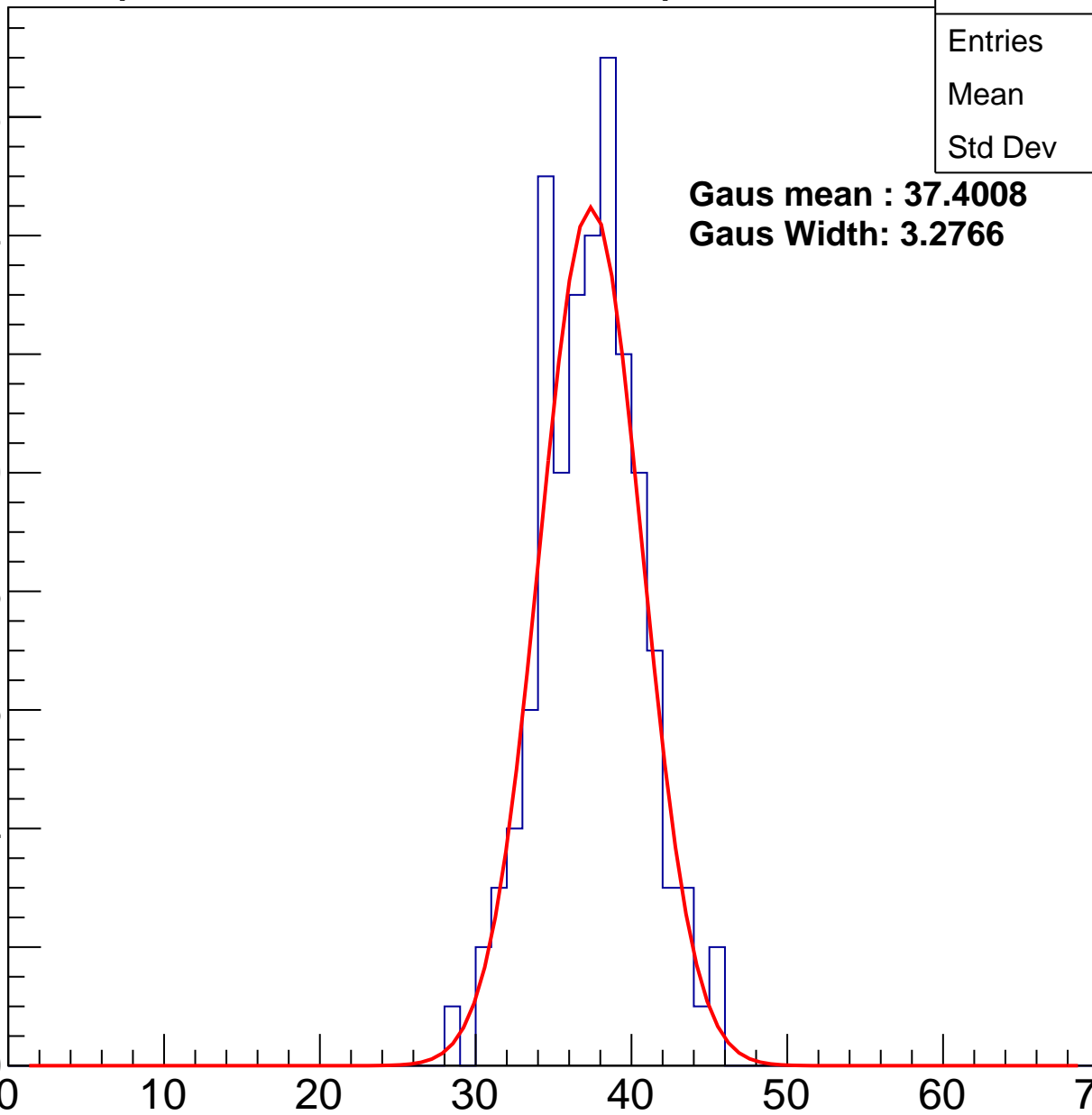
40

50

60

70

ampl



B0L101S, U21-ch1, adc2

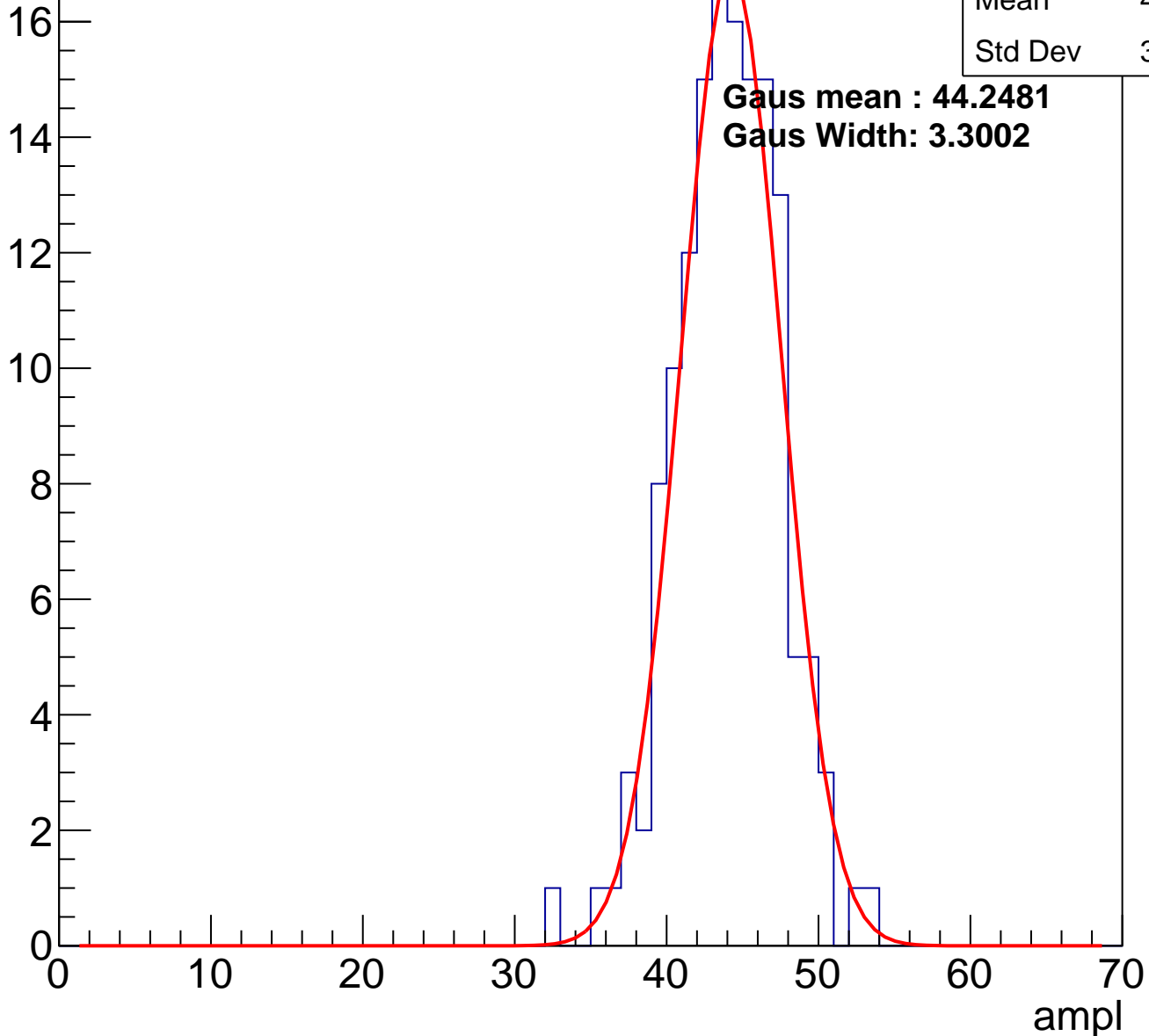
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	144
Mean	43.58
Std Dev	3.413

Gaus mean : 44.2481

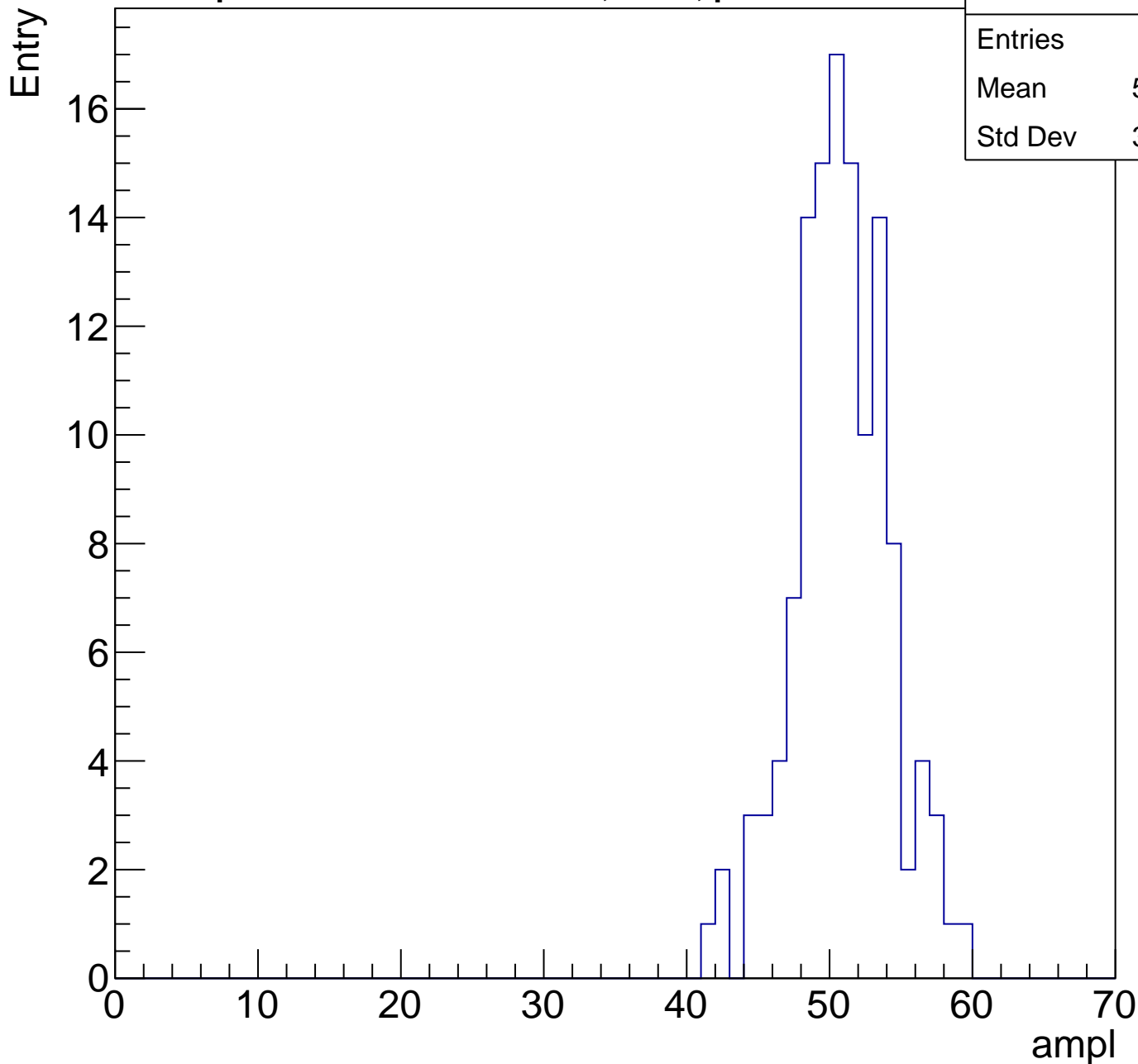
Gaus Width: 3.3002



B0L101S, U21-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	50.35
Std Dev	3.324



B0L101S, U21-ch1, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

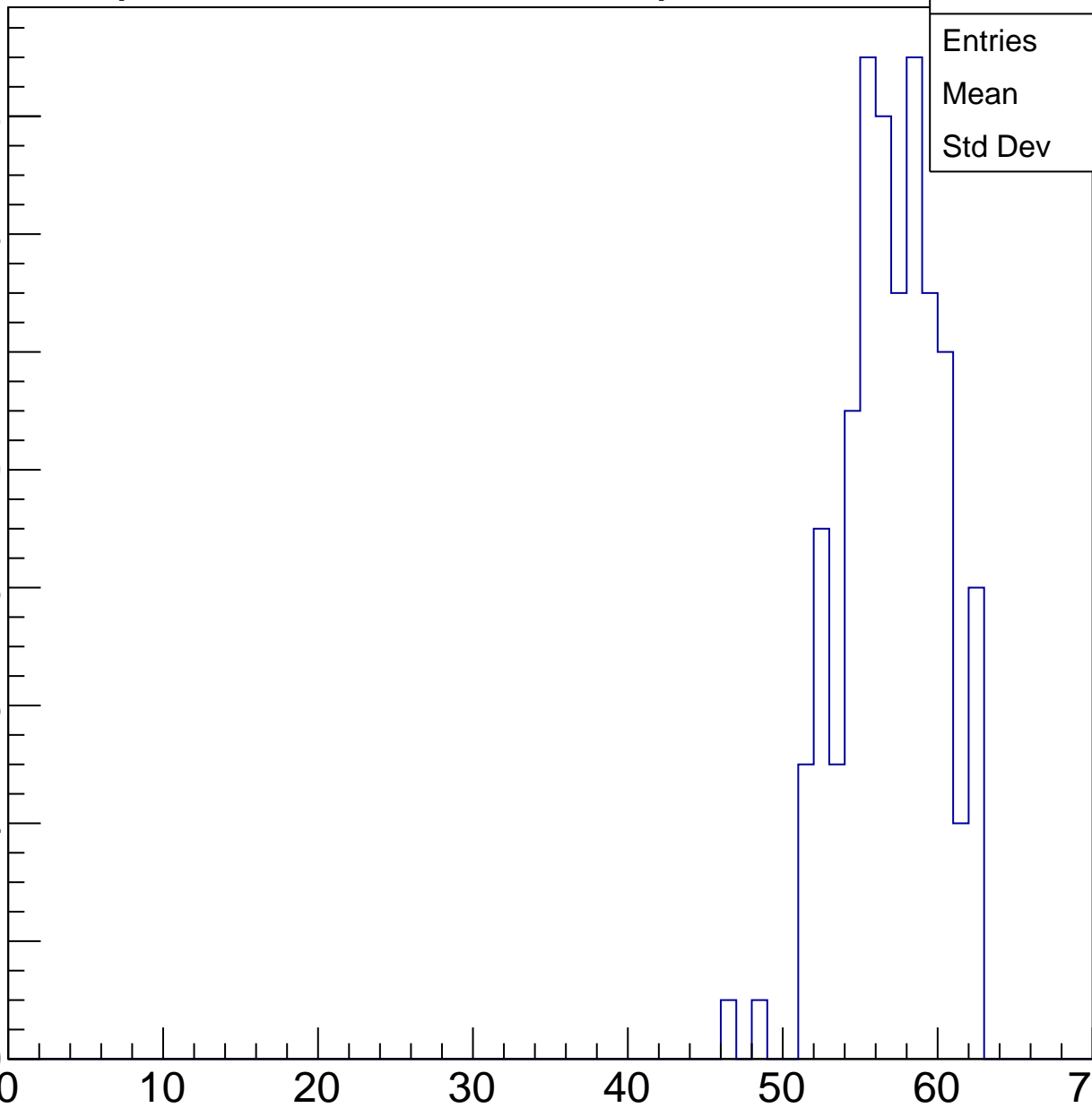
Mean

56.52

Std Dev

3.115

ampl



B0L101S, U21-ch1, adc5

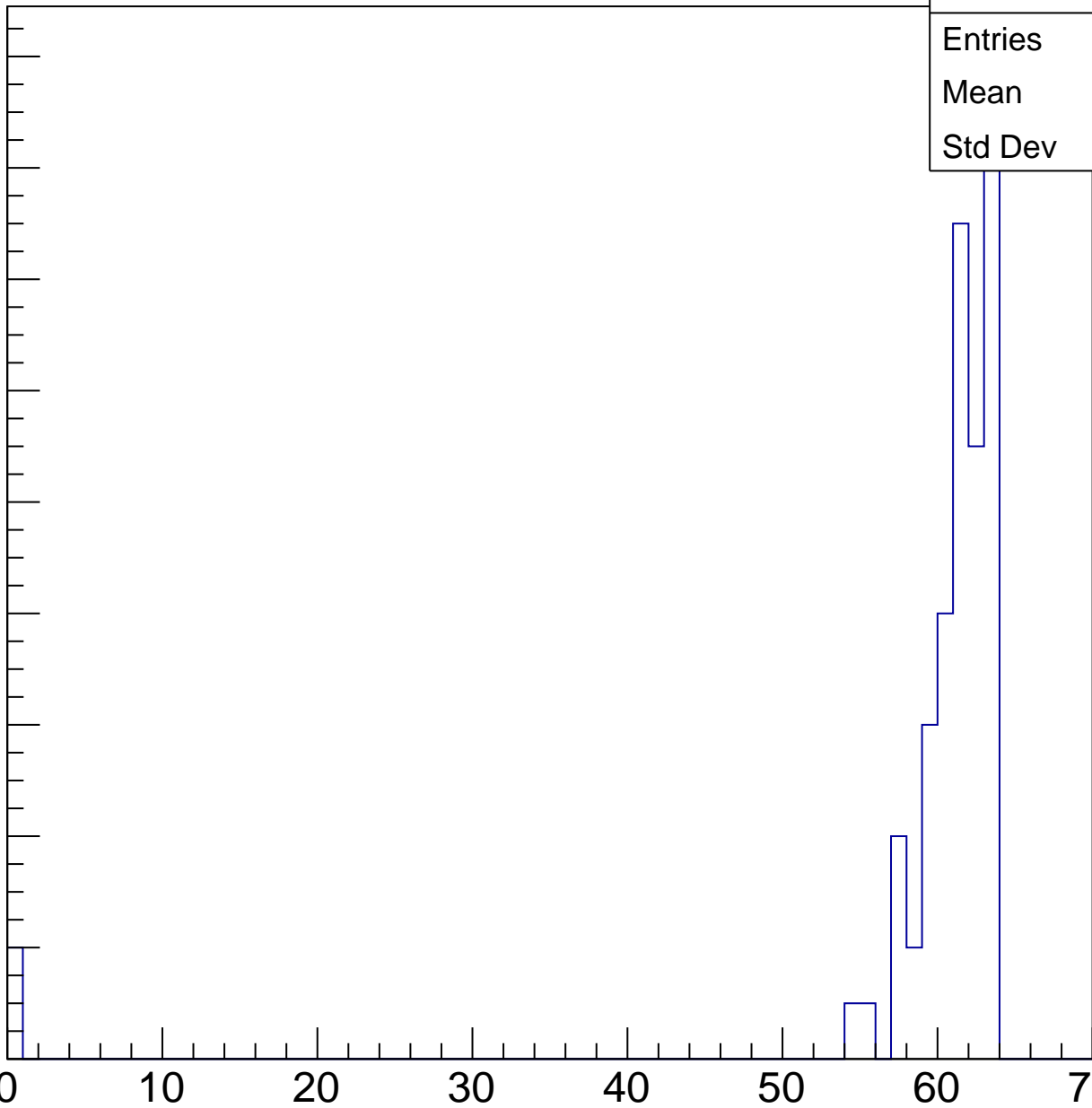
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	68
Mean	59.09
Std Dev	10.48

ampl



B0L101S, U21-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

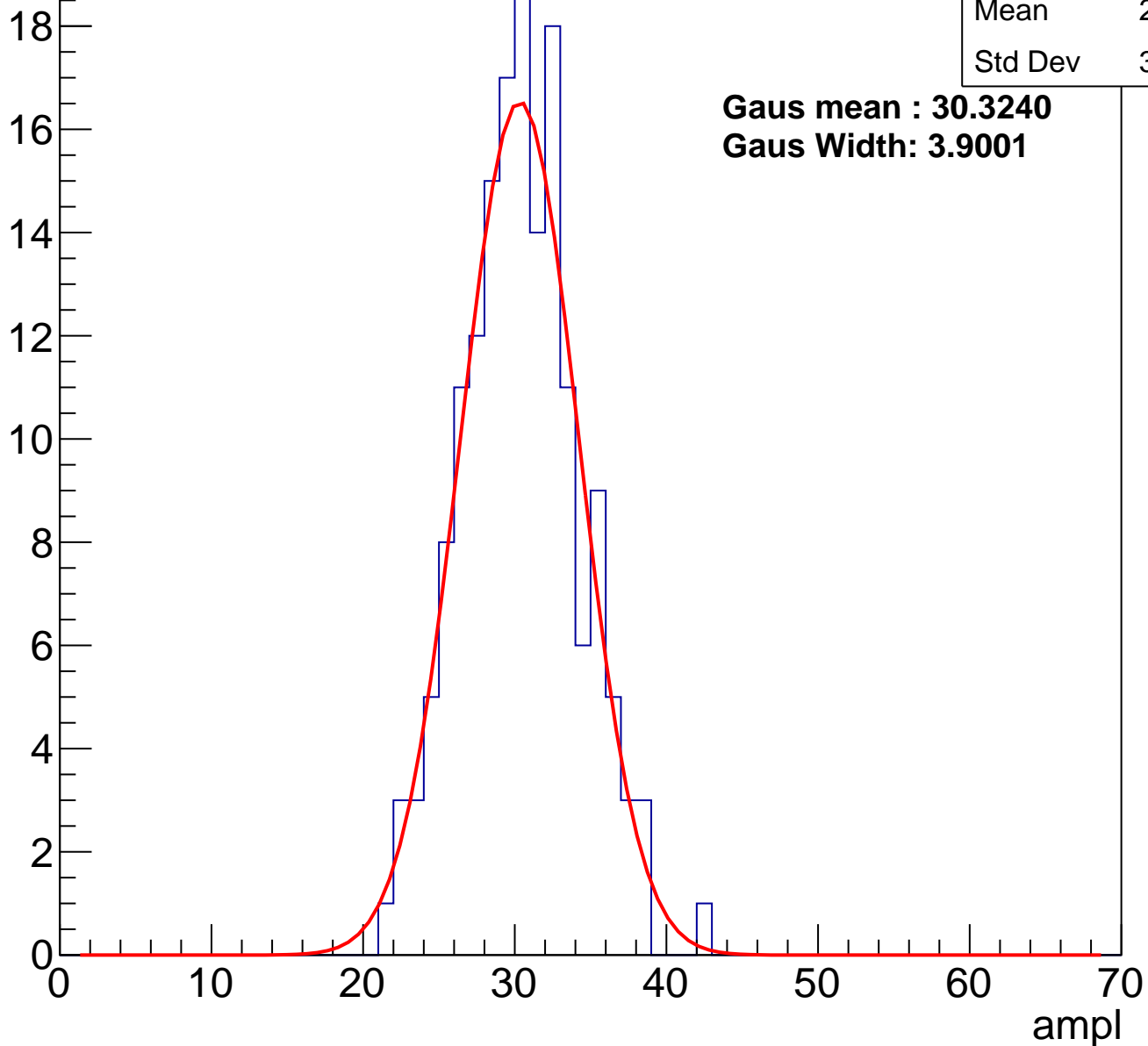
Entry



B0L101S, U21-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch2, adc1

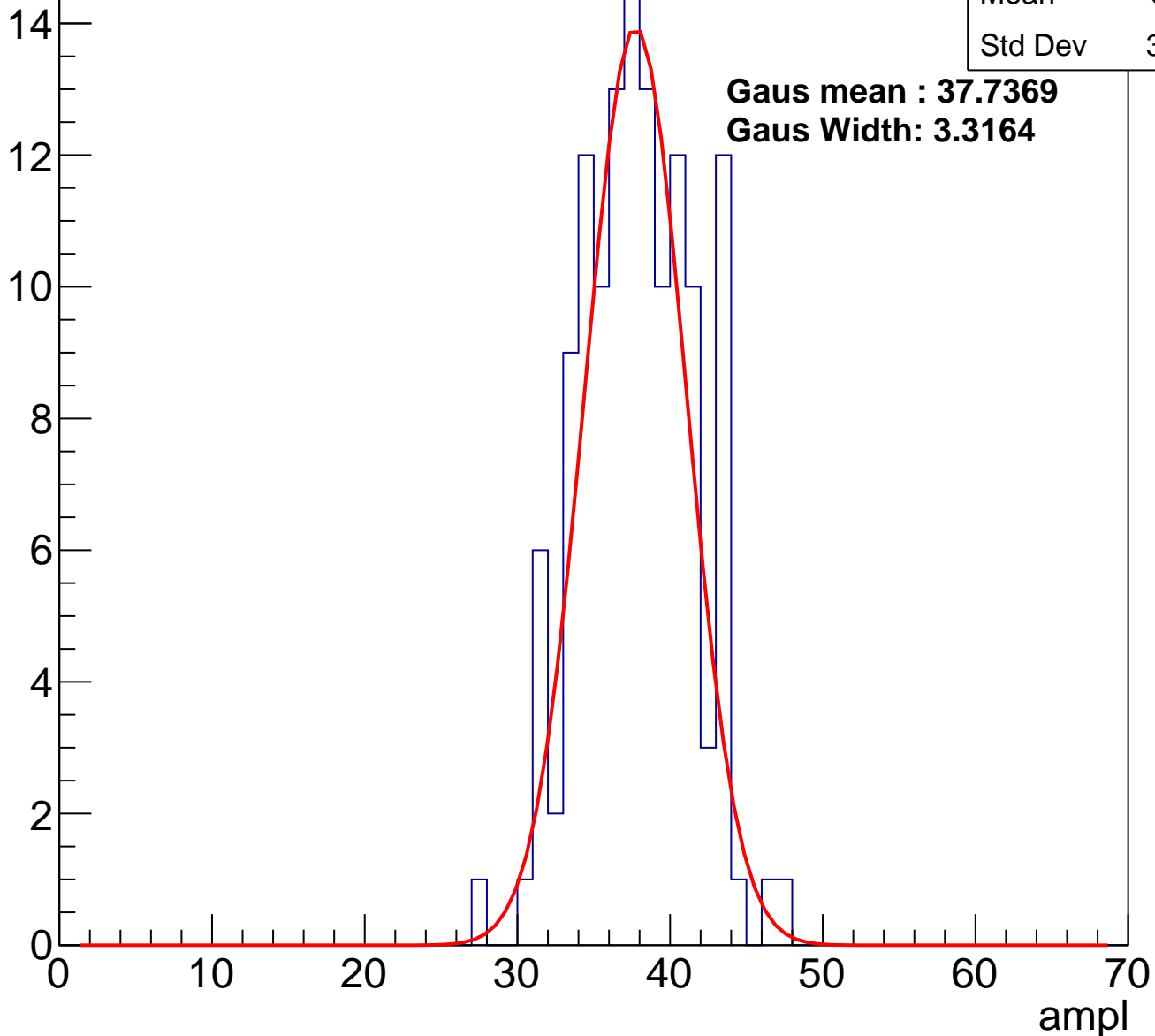
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	37.41
Std Dev	3.647

Gaus mean : 37.7369

Gaus Width: 3.3164

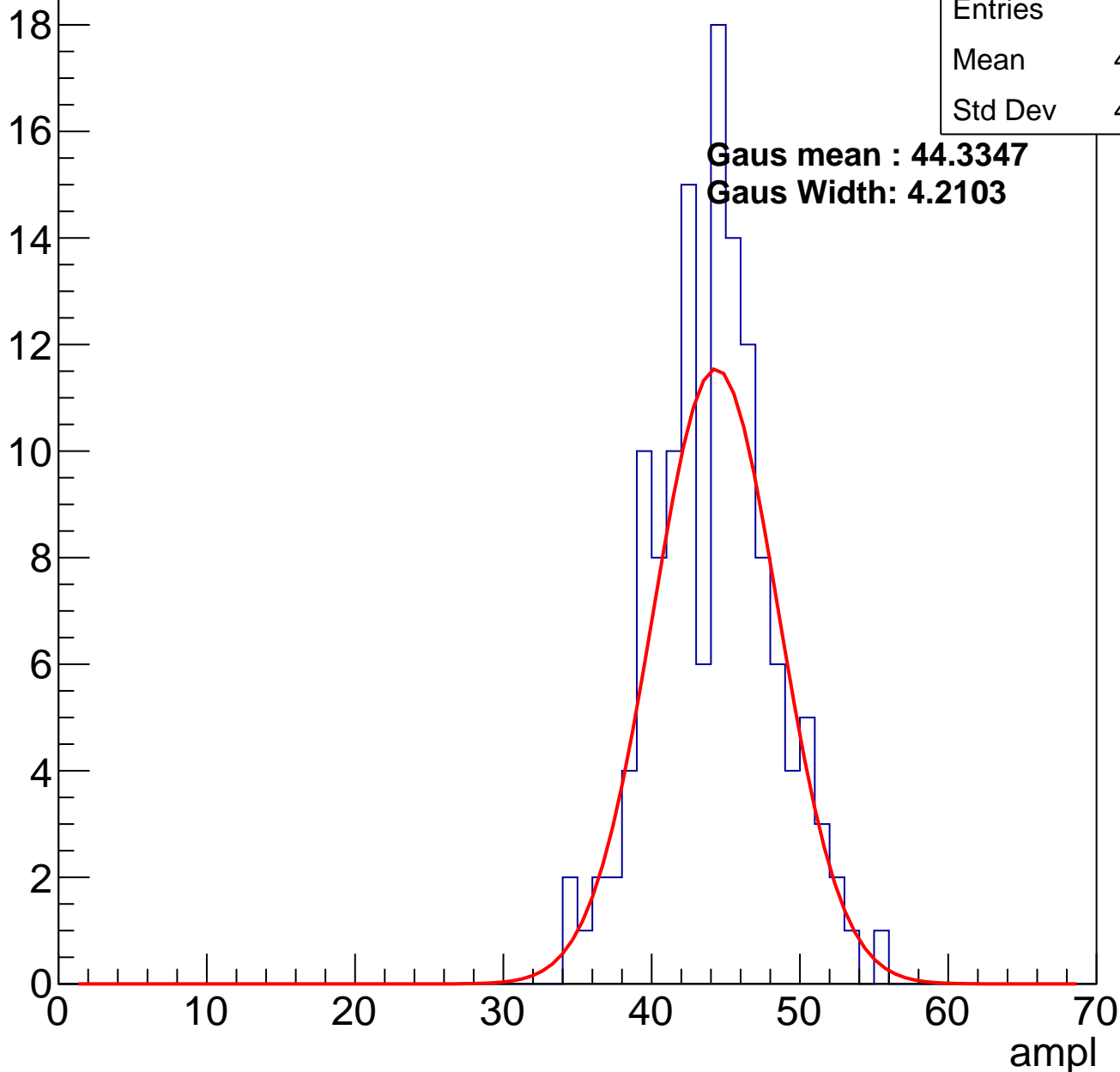
Entry



B0L101S, U21-ch2, adc2

calib_packv5_042523_0143.root, FC#1, port C1

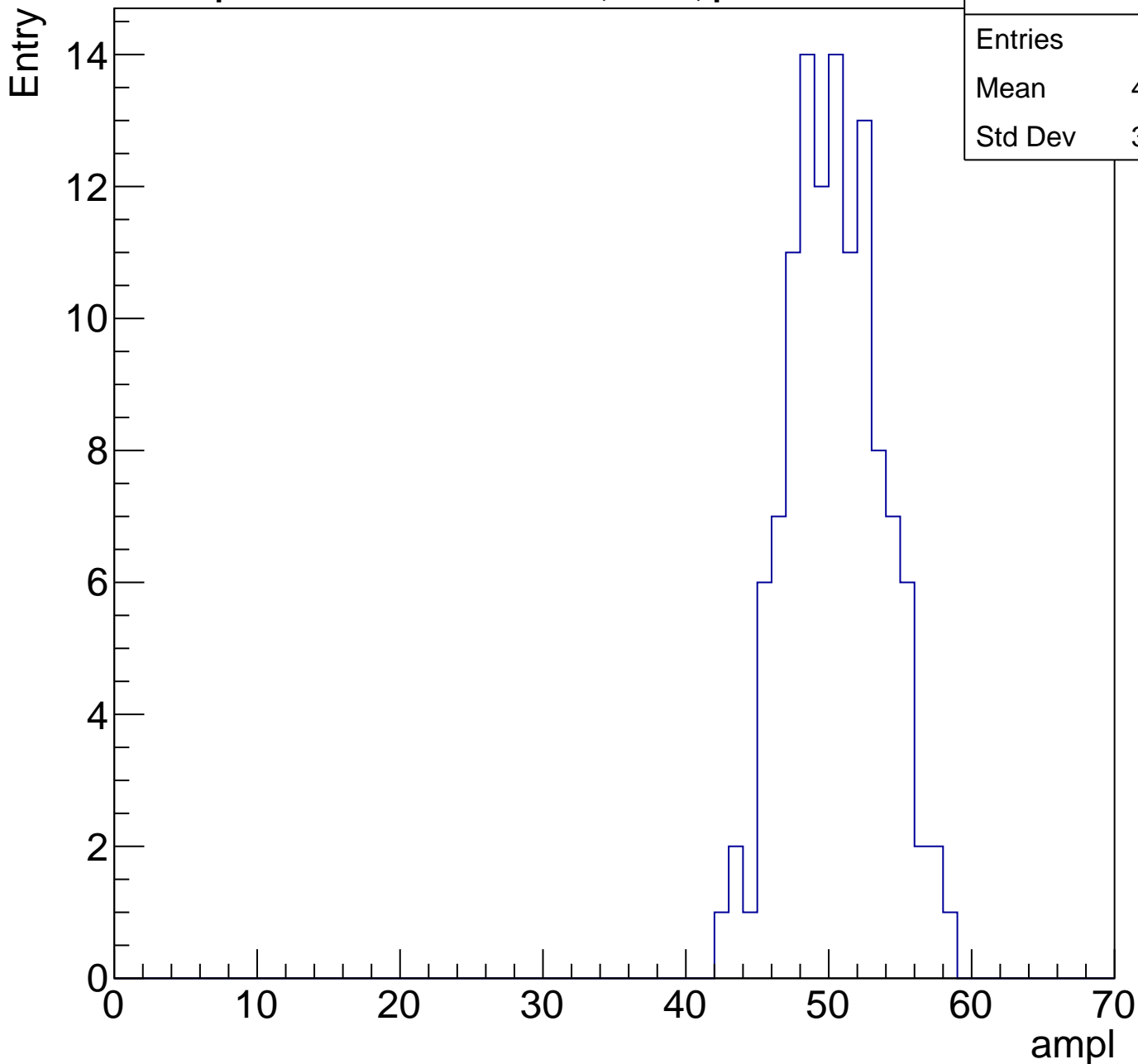
Entry



B0L101S, U21-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

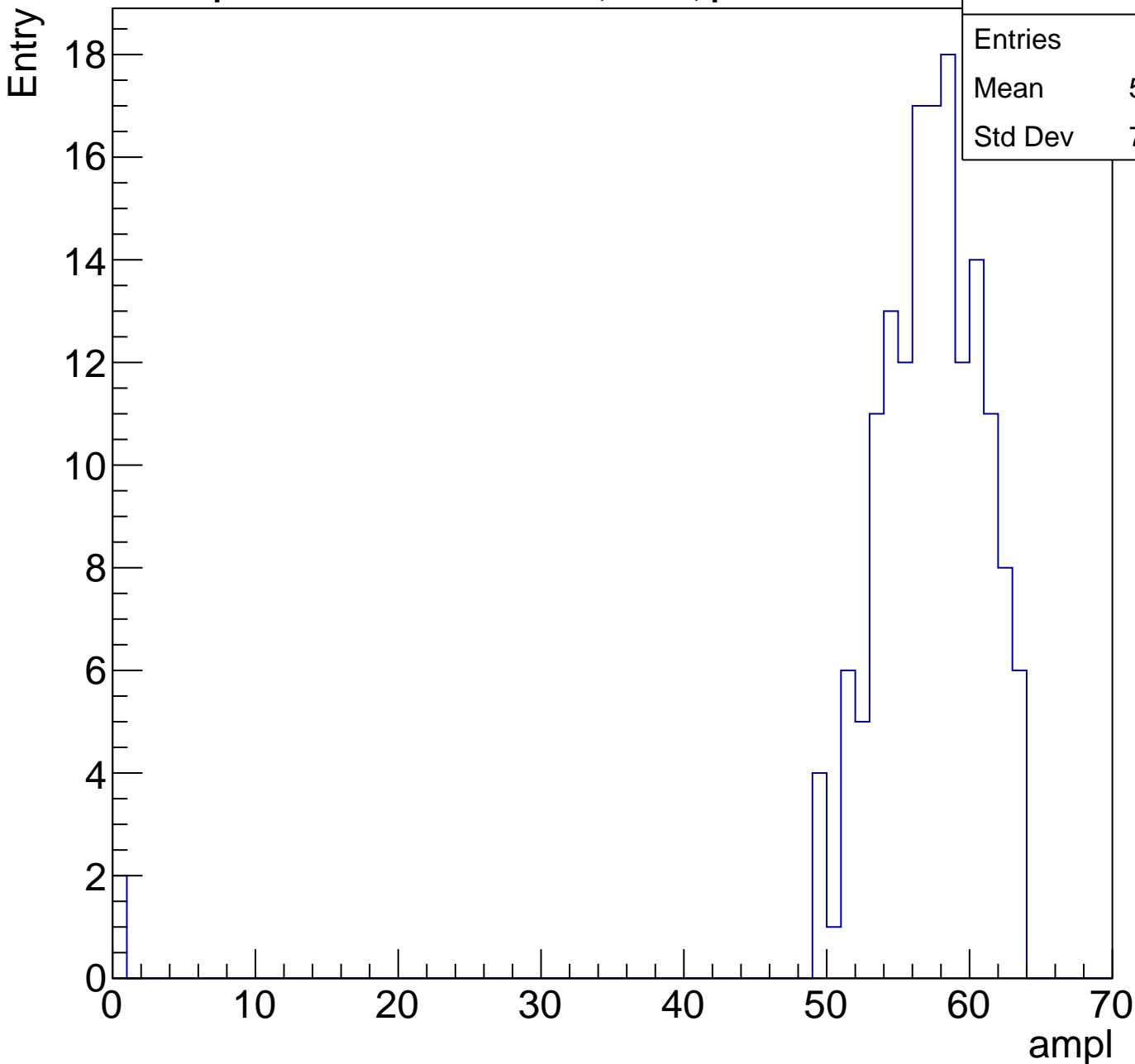
Entries	118
Mean	49.95
Std Dev	3.265



B0L101S, U21-ch2, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	56.15
Std Dev	7.214



B0L101S, U21-ch2, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

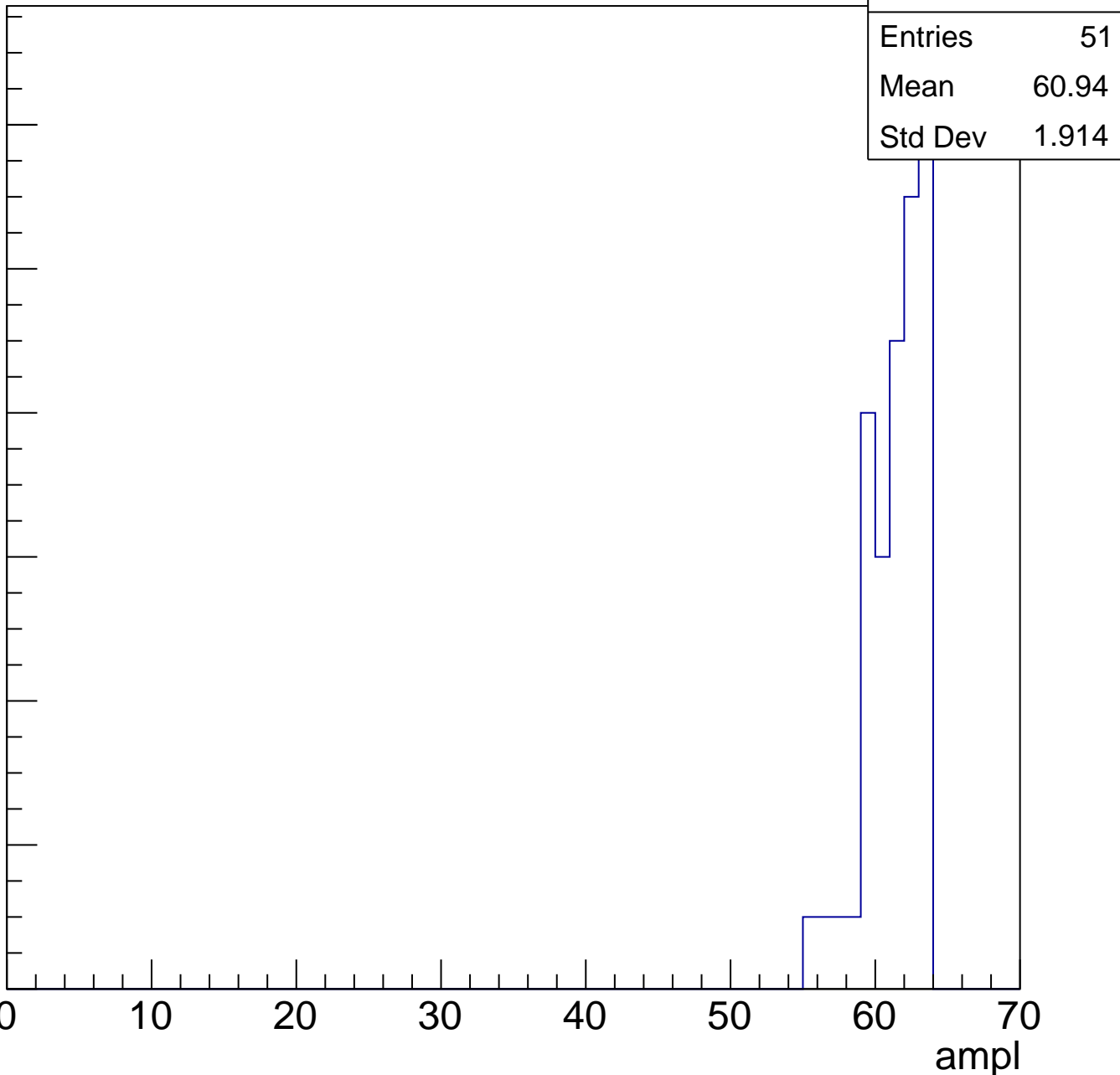
40

50

60

ampl

Entries	51
Mean	60.94
Std Dev	1.914



B0L101S, U21-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L101S, U21-ch3, adc0

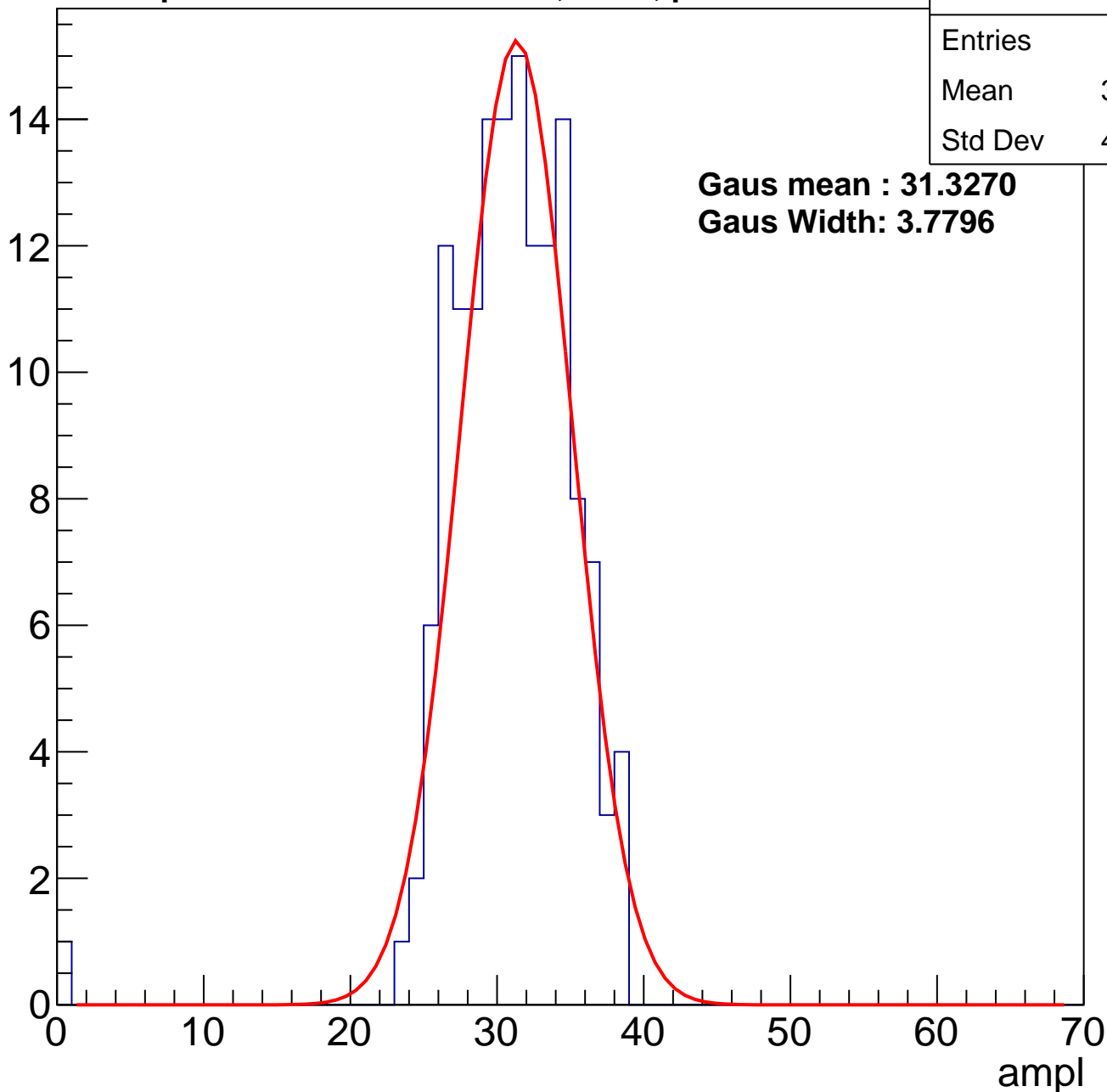
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	30.48
Std Dev	4.315

Gaus mean : 31.3270

Gaus Width: 3.7796

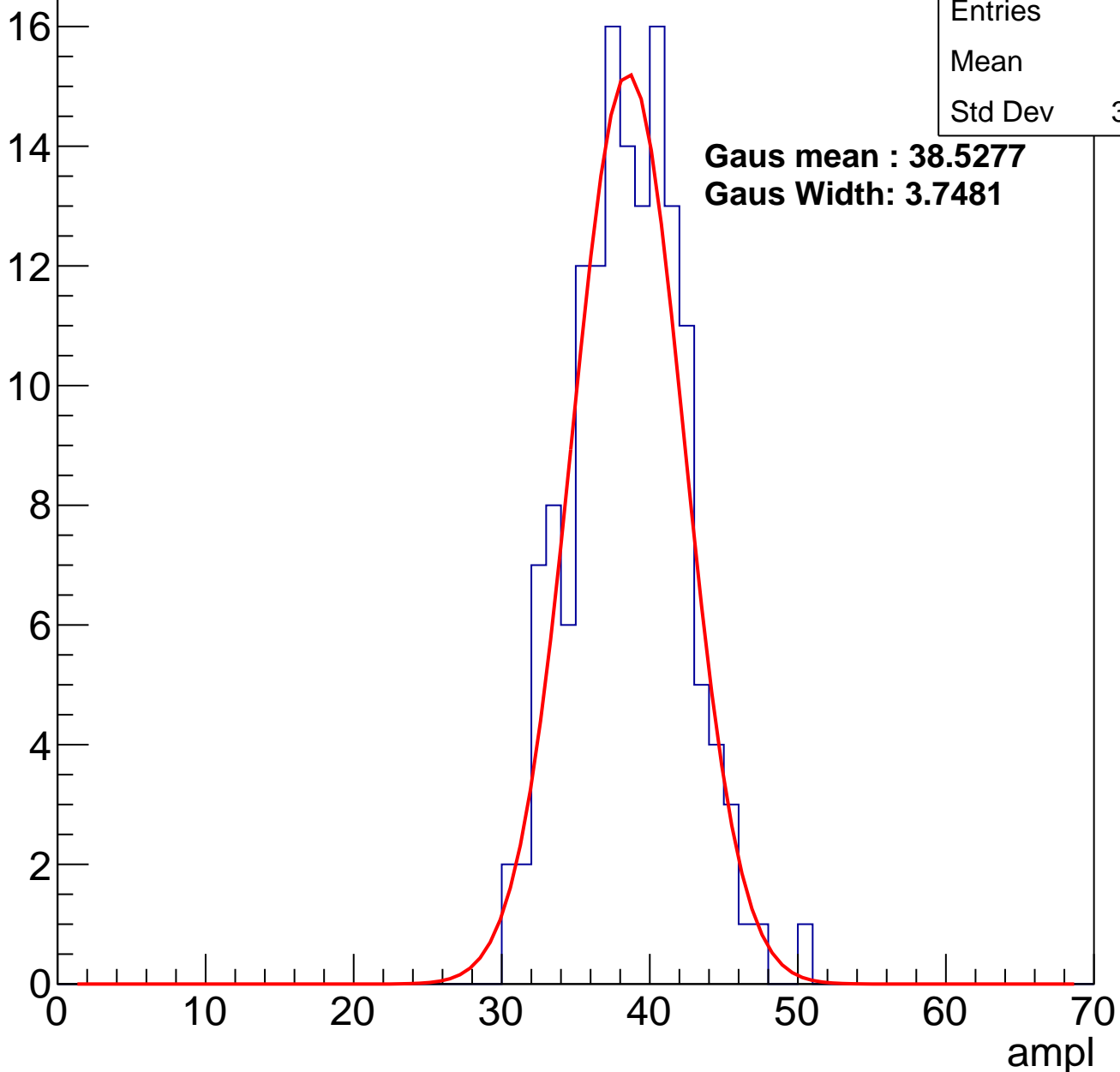
Entry



B0L101S, U21-ch3, adc1

calib_packv5_042523_0143.root, FC#1, port C1

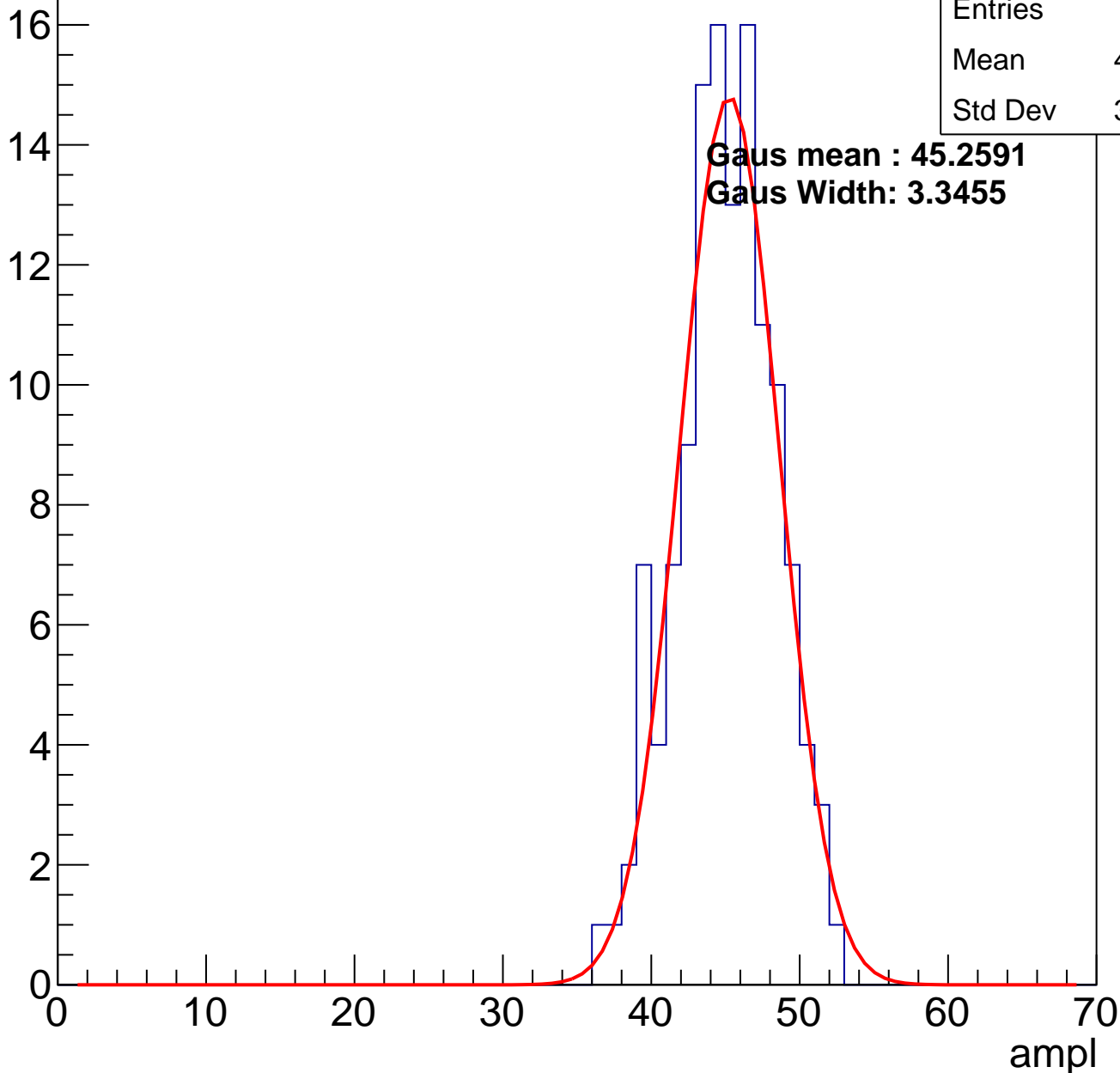
Entry



B0L101S, U21-ch3, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch3, adc3

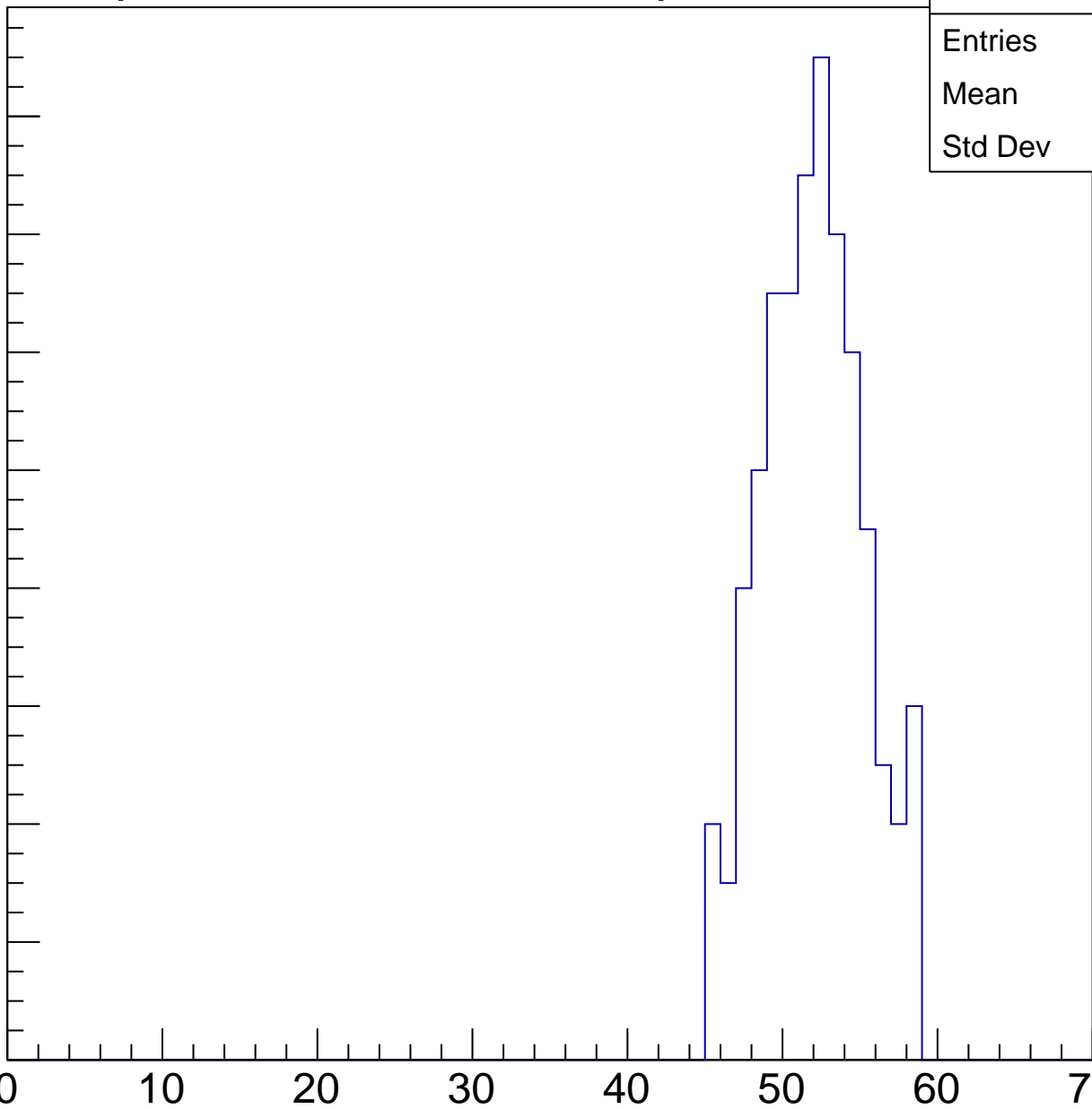
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	51.51
Std Dev	3.192

ampl



B0L101S, U21-ch3, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

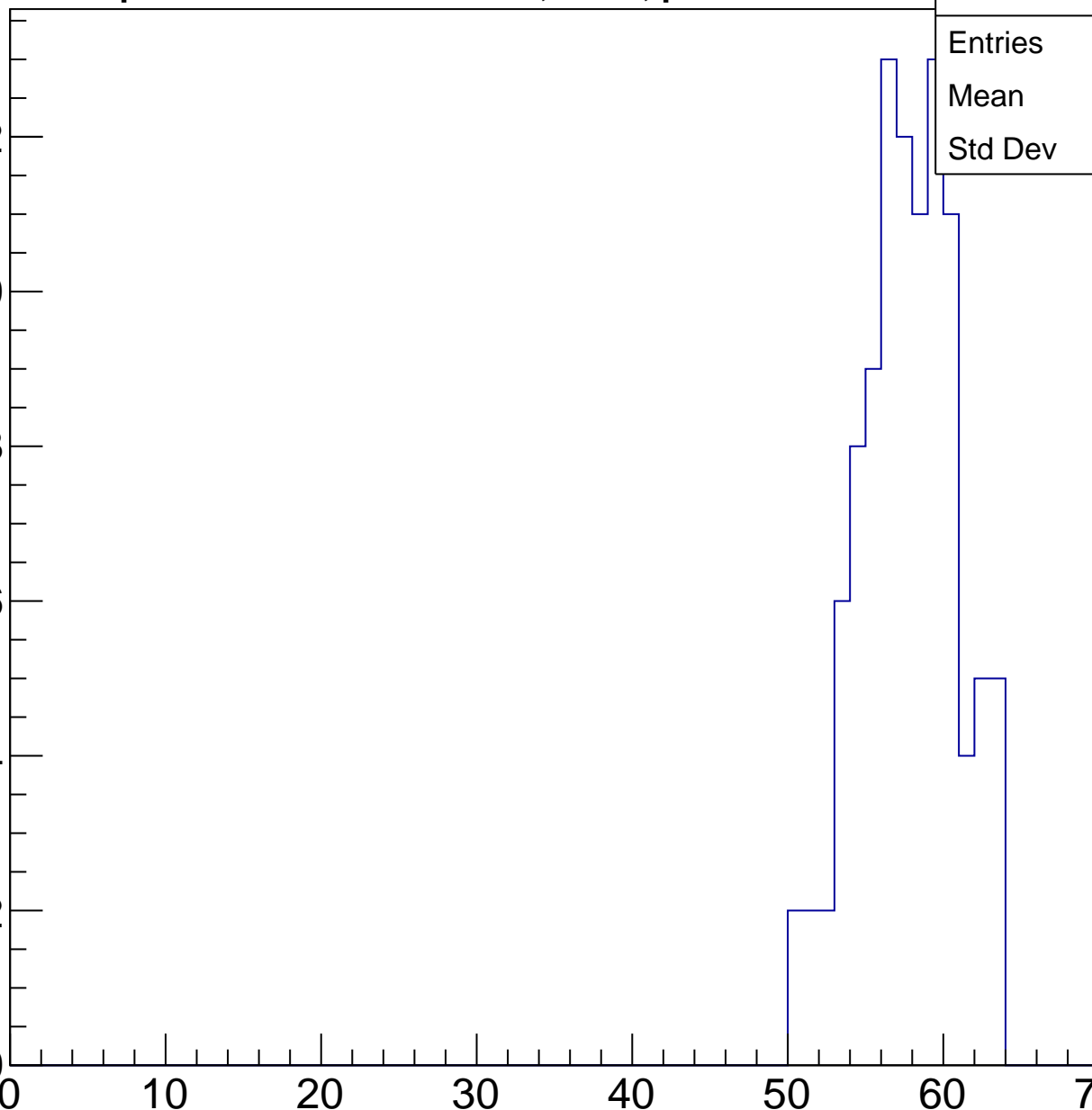
50

60

70

ampl

Entries	103
Mean	57.25
Std Dev	3.055



B0L101S, U21-ch3, adc5

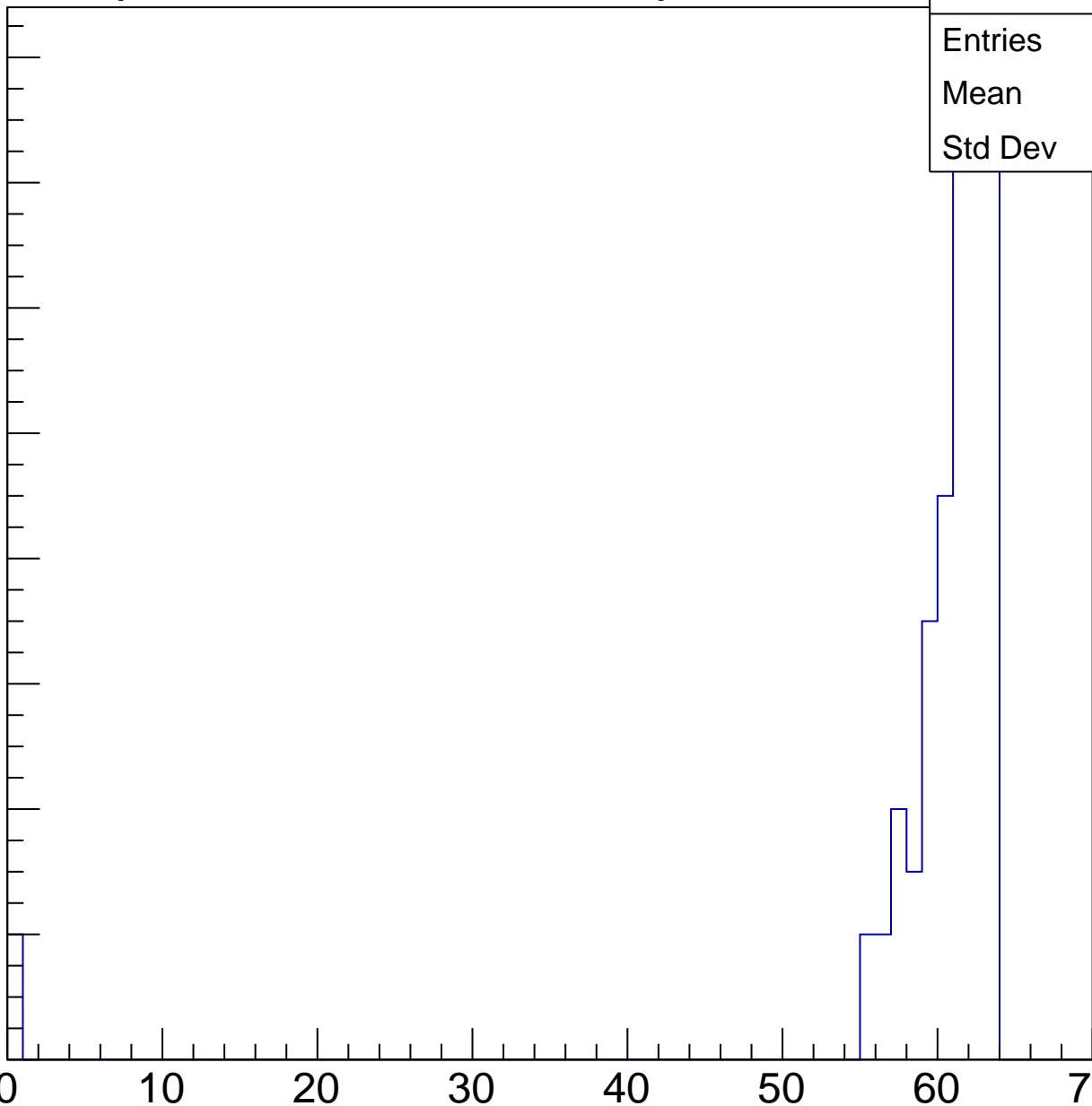
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	59.04
Std Dev	9.986

ampl



B0L101S, U21-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch4, adc0

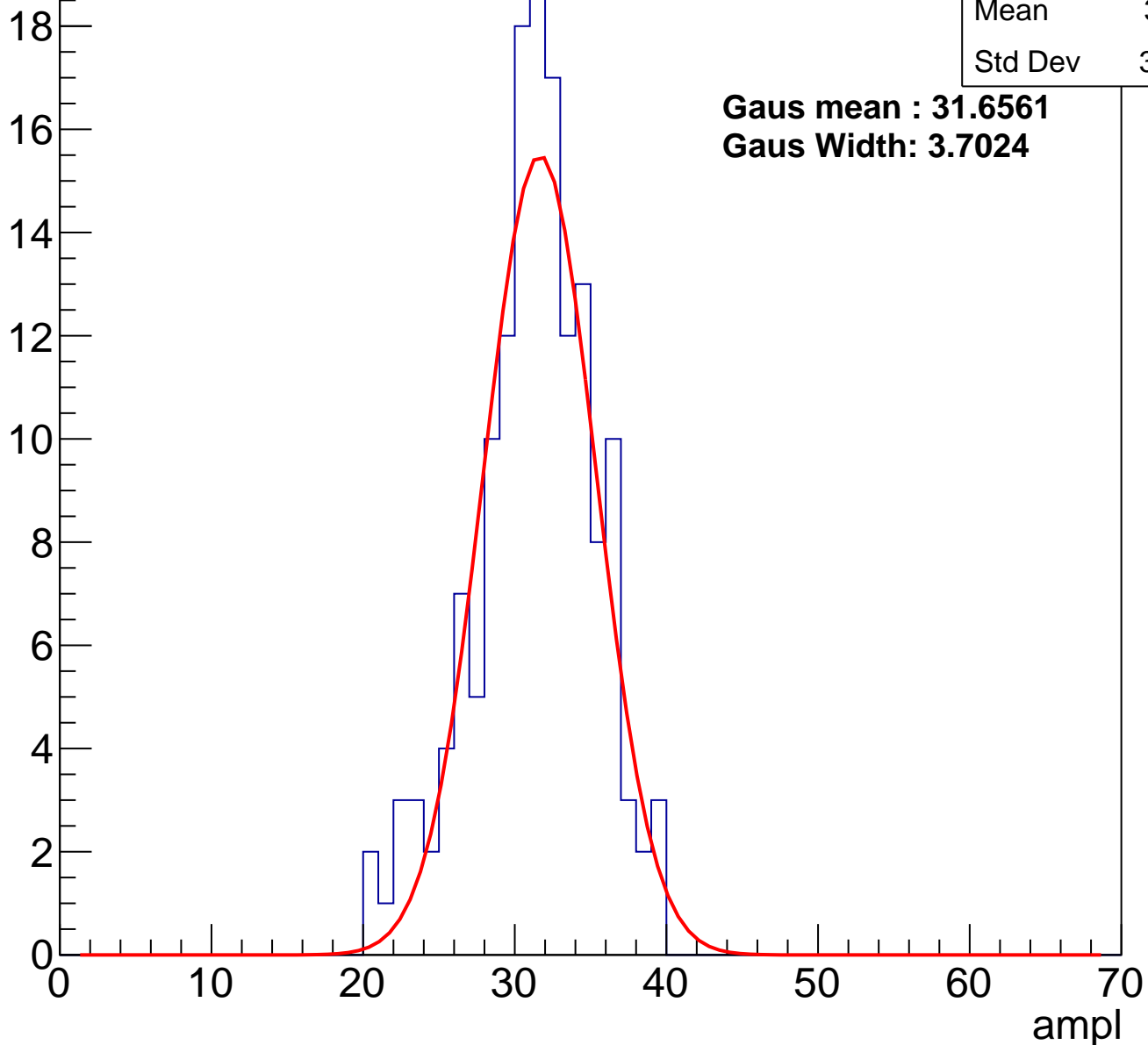
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	30.81
Std Dev	3.933

Gaus mean : 31.6561

Gaus Width: 3.7024

Entry



B0L101S, U21-ch4, adc1

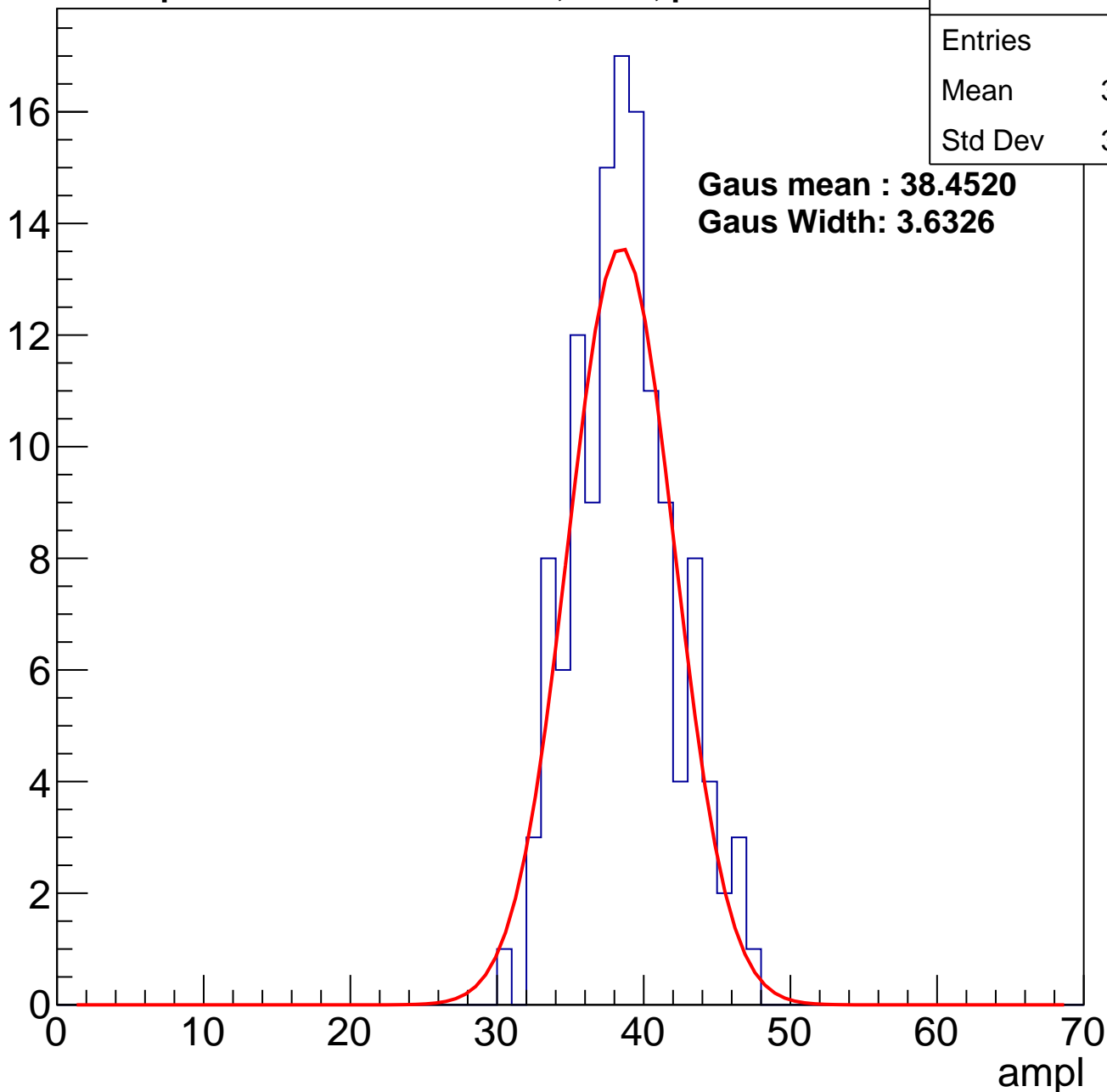
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	38.26
Std Dev	3.454

Gaus mean : 38.4520

Gaus Width: 3.6326

Entry



B0L101S, U21-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

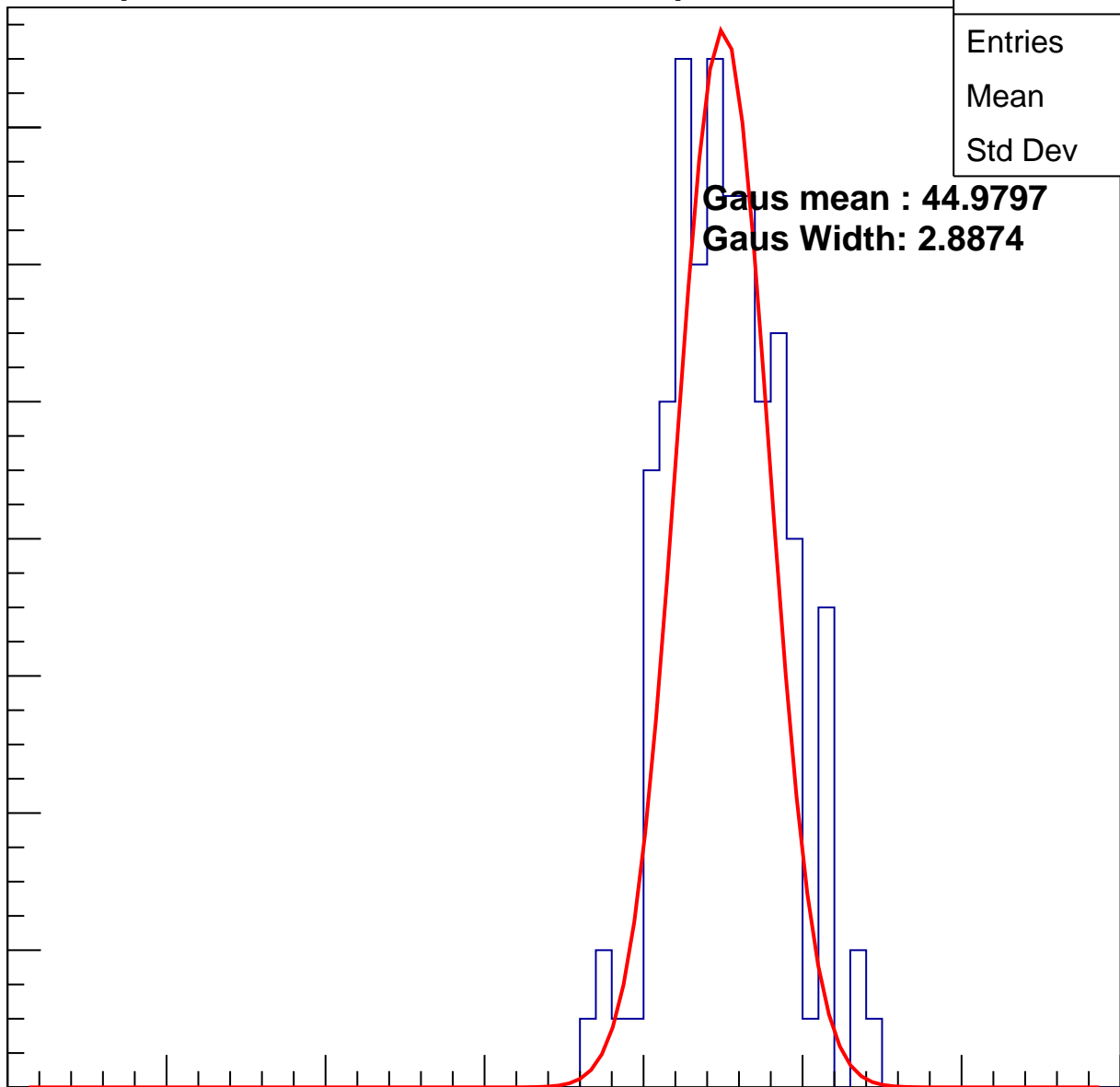
Entries	132
Mean	44.72
Std Dev	3.517

Gaus mean : 44.9797

Gaus Width: 2.8874

0 10 20 30 40 50 60 70

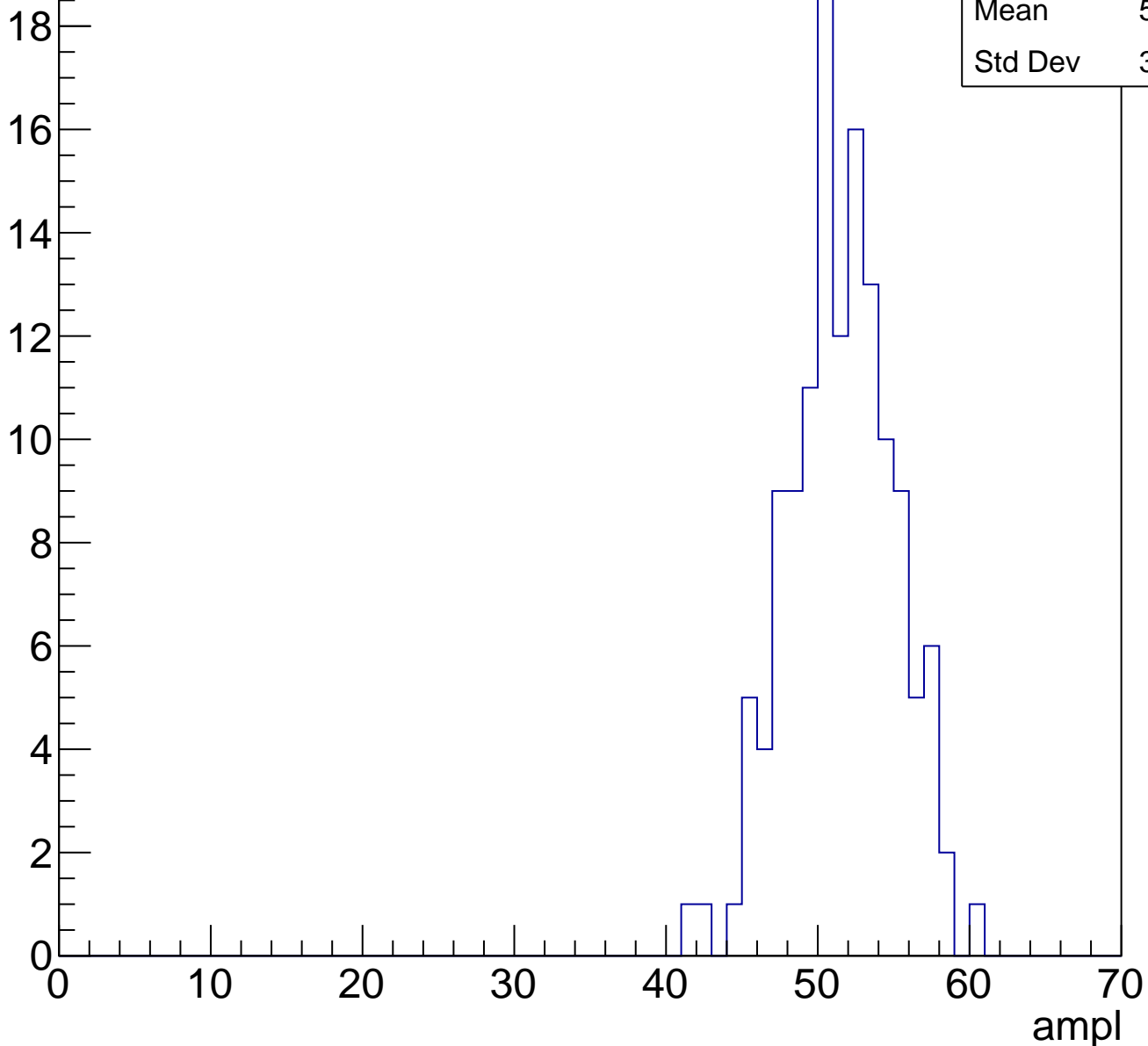
ampl



B0L101S, U21-ch4, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	134
Mean	51.09
Std Dev	3.476

B0L101S, U21-ch4, adc4

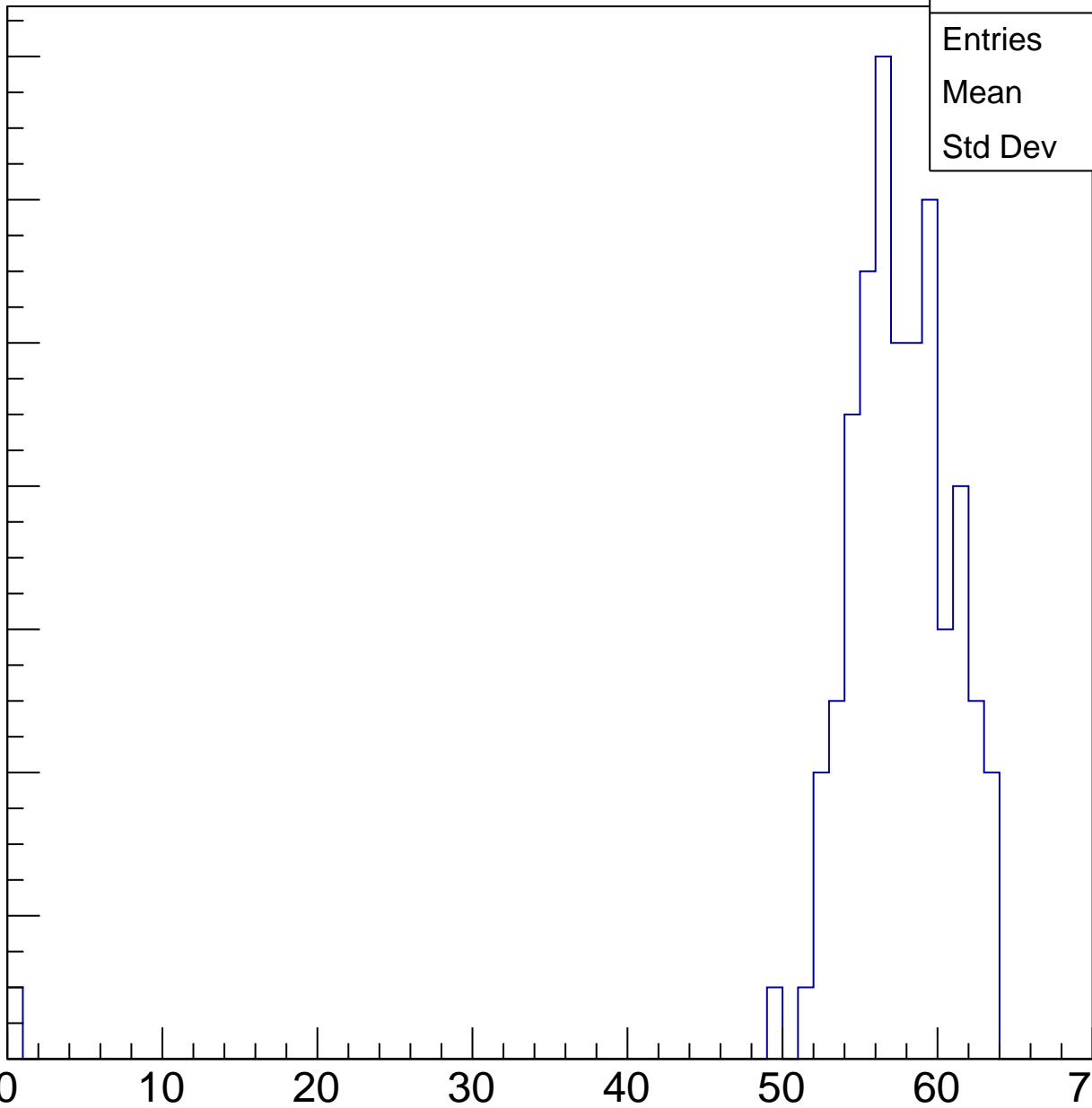
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	56.59
Std Dev	6.418

ampl



B0L101S, U21-ch4, adc5

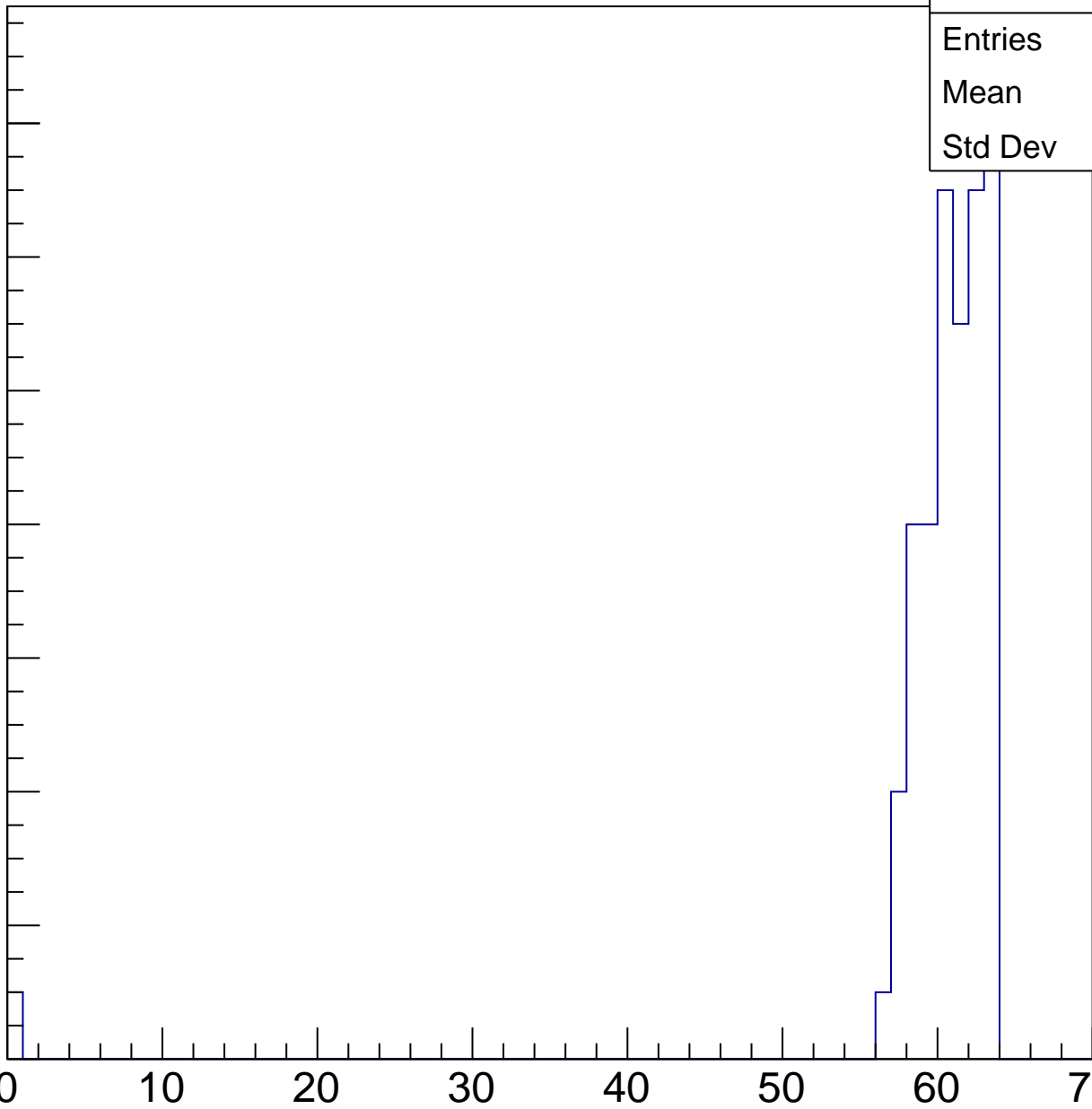
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	59.76
Std Dev	7.246

ampl



B0L101S, U21-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch5, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	30.27
Std Dev	3.65

Gaus mean : 30.9473

Gaus Width: 3.4175

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

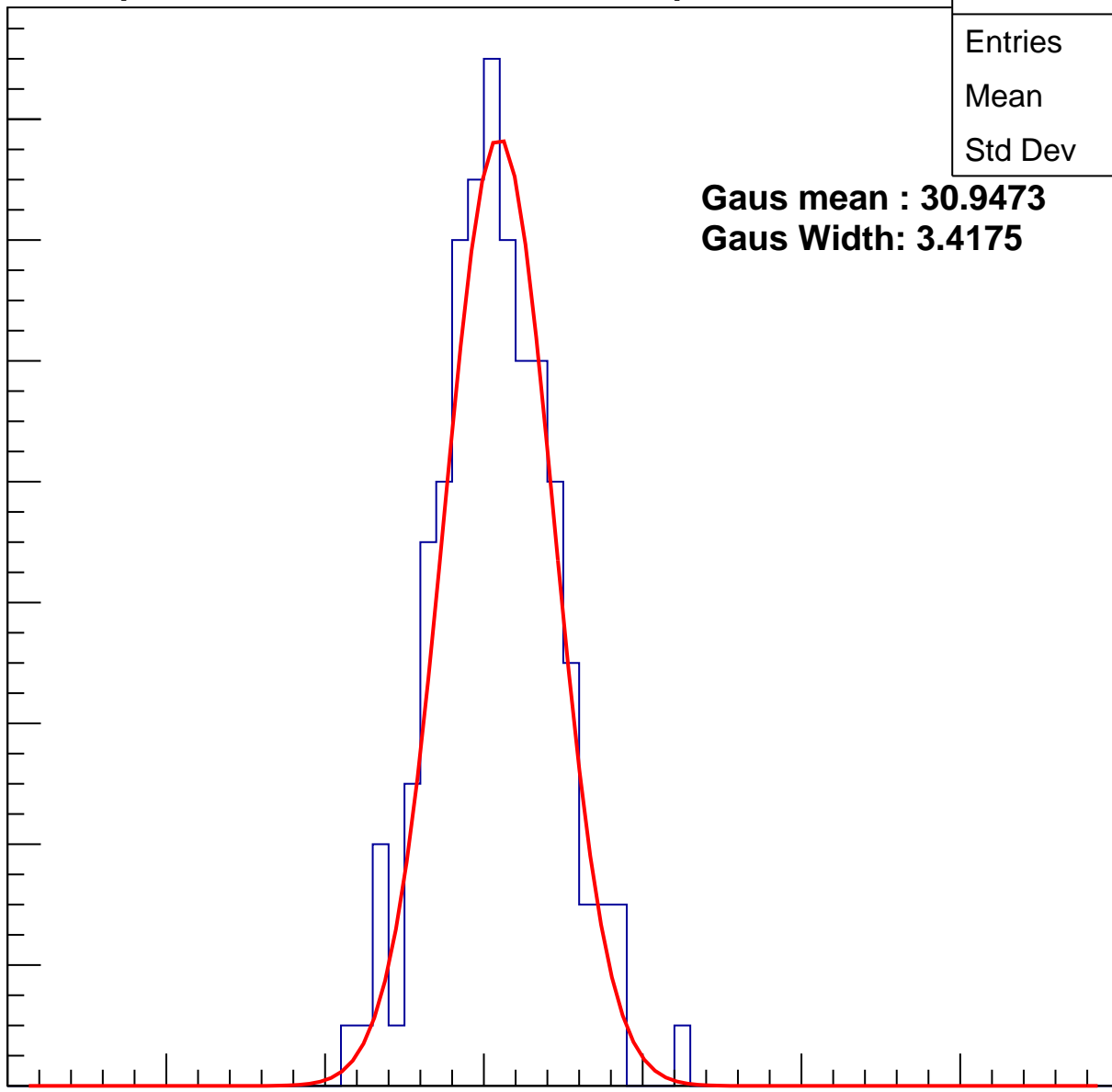
40

50

60

70

ampl



B0L101S, U21-ch5, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

159

Mean

37.99

Std Dev

3.721

Gaus mean : 38.4552

Gaus Width: 3.8791

0

10

20

30

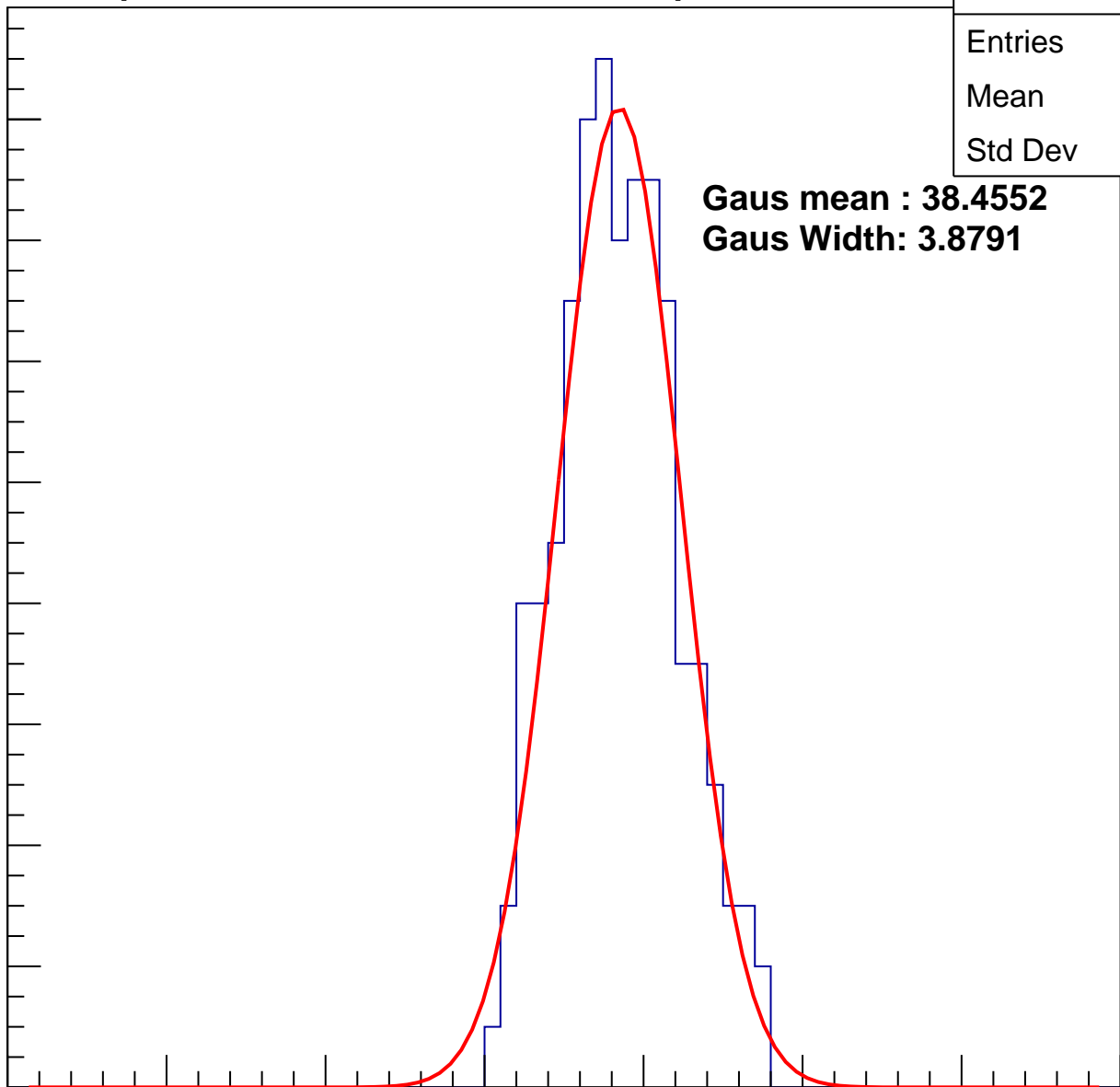
40

50

60

70

ampl



B0L101S, U21-ch5, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	109
Mean	44.34
Std Dev	3.243

Gaus mean : 44.7379

Gaus Width: 3.5188

0

2

4

6

8

10

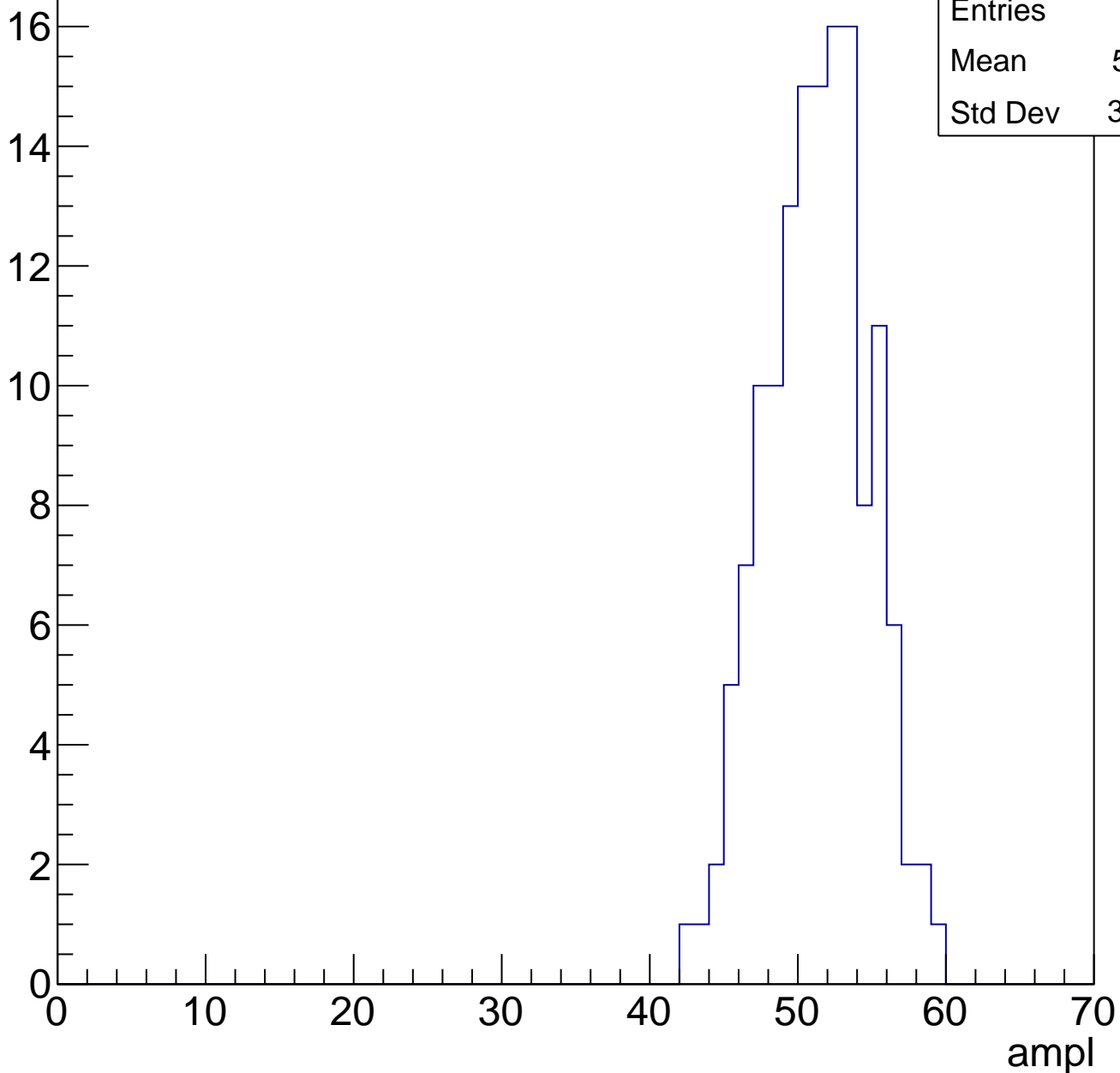
12

B0L101S, U21-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	50.81
Std Dev	3.392

Entry



B0L101S, U21-ch5, adc4

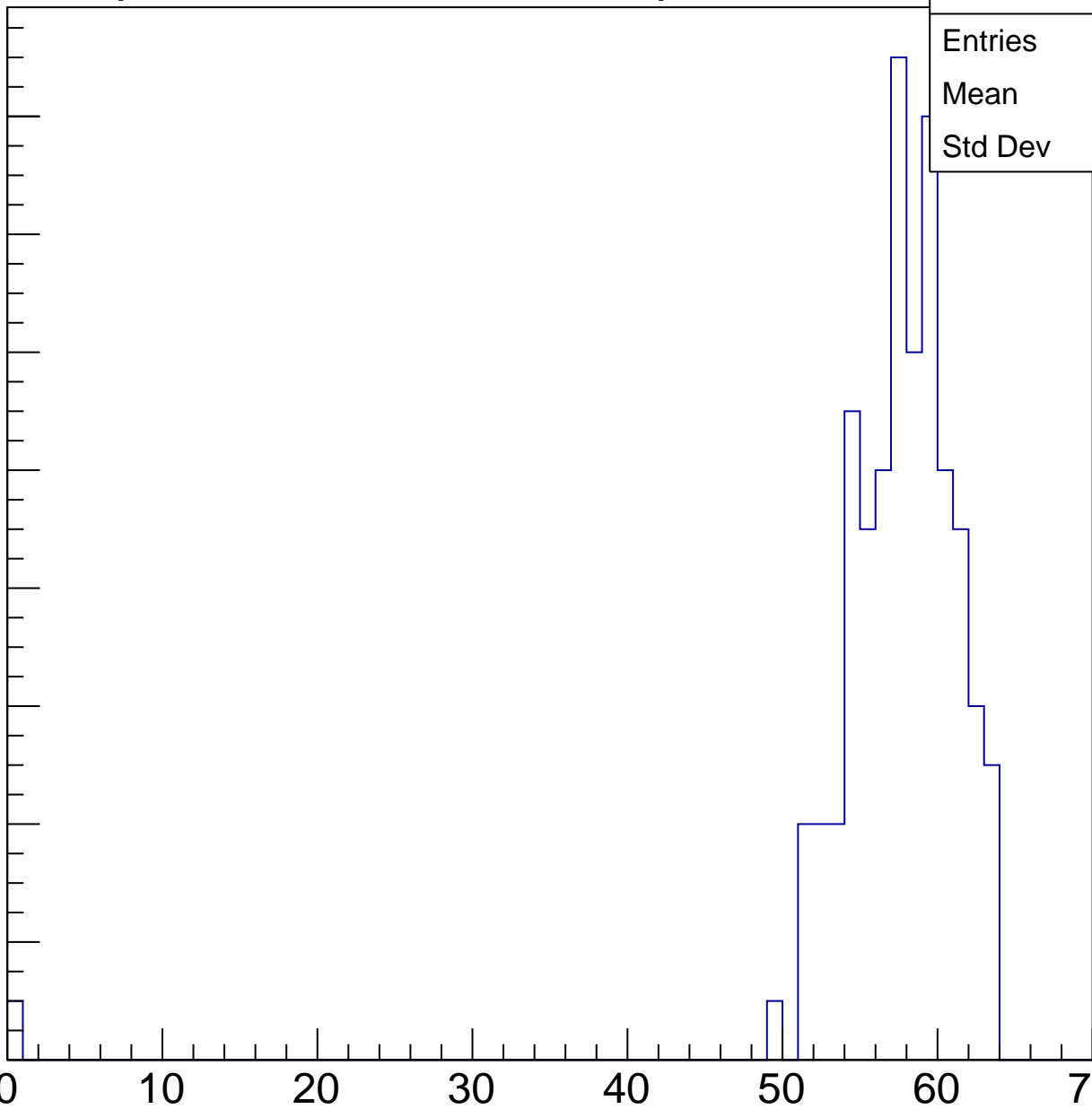
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	56.87
Std Dev	6.083

ampl



B0L101S, U21-ch5, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	61
Mean	59.59
Std Dev	8.001

ampl

0

10

20

30

40

50

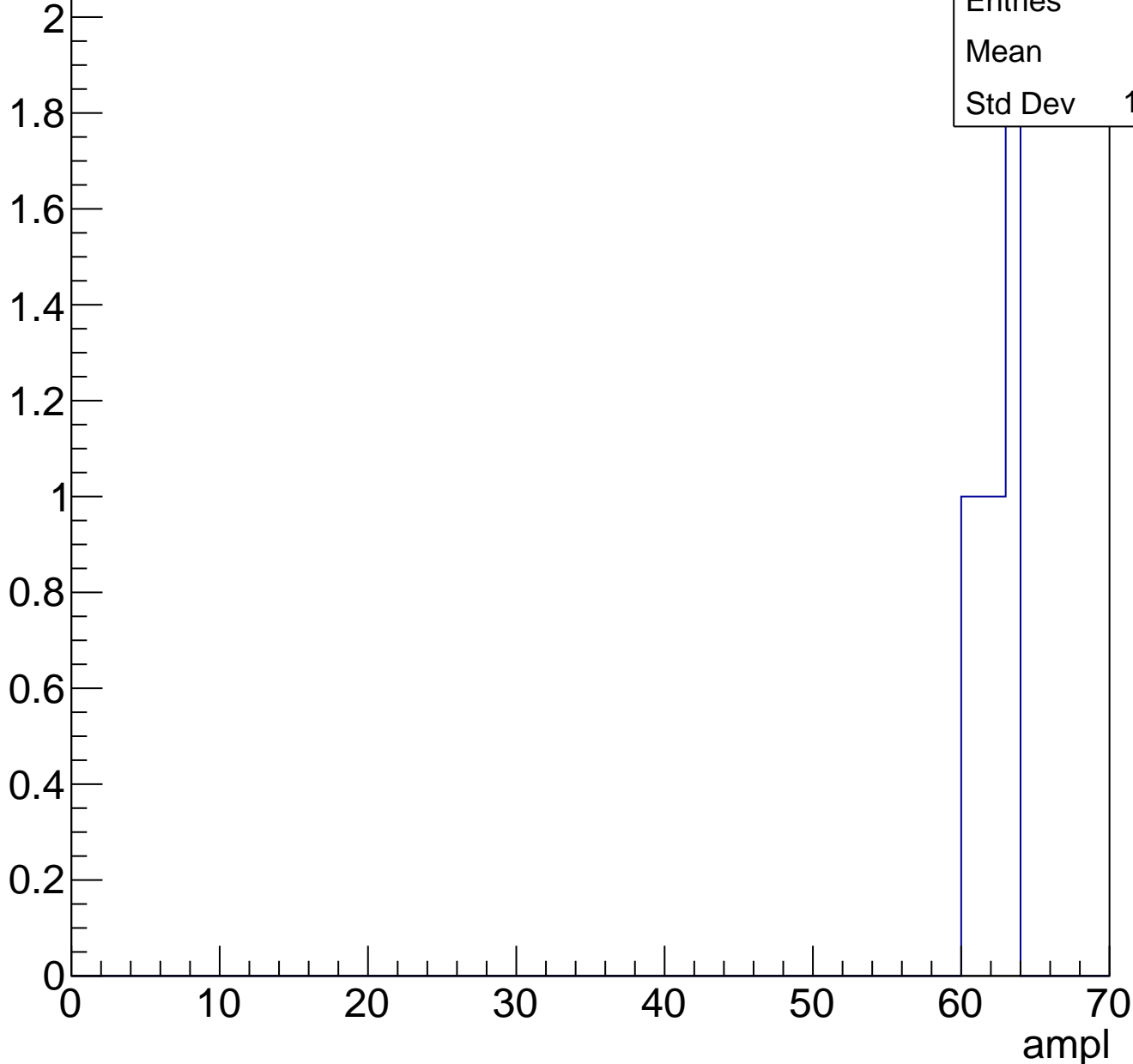
60

70

B0L101S, U21-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch6, adc0

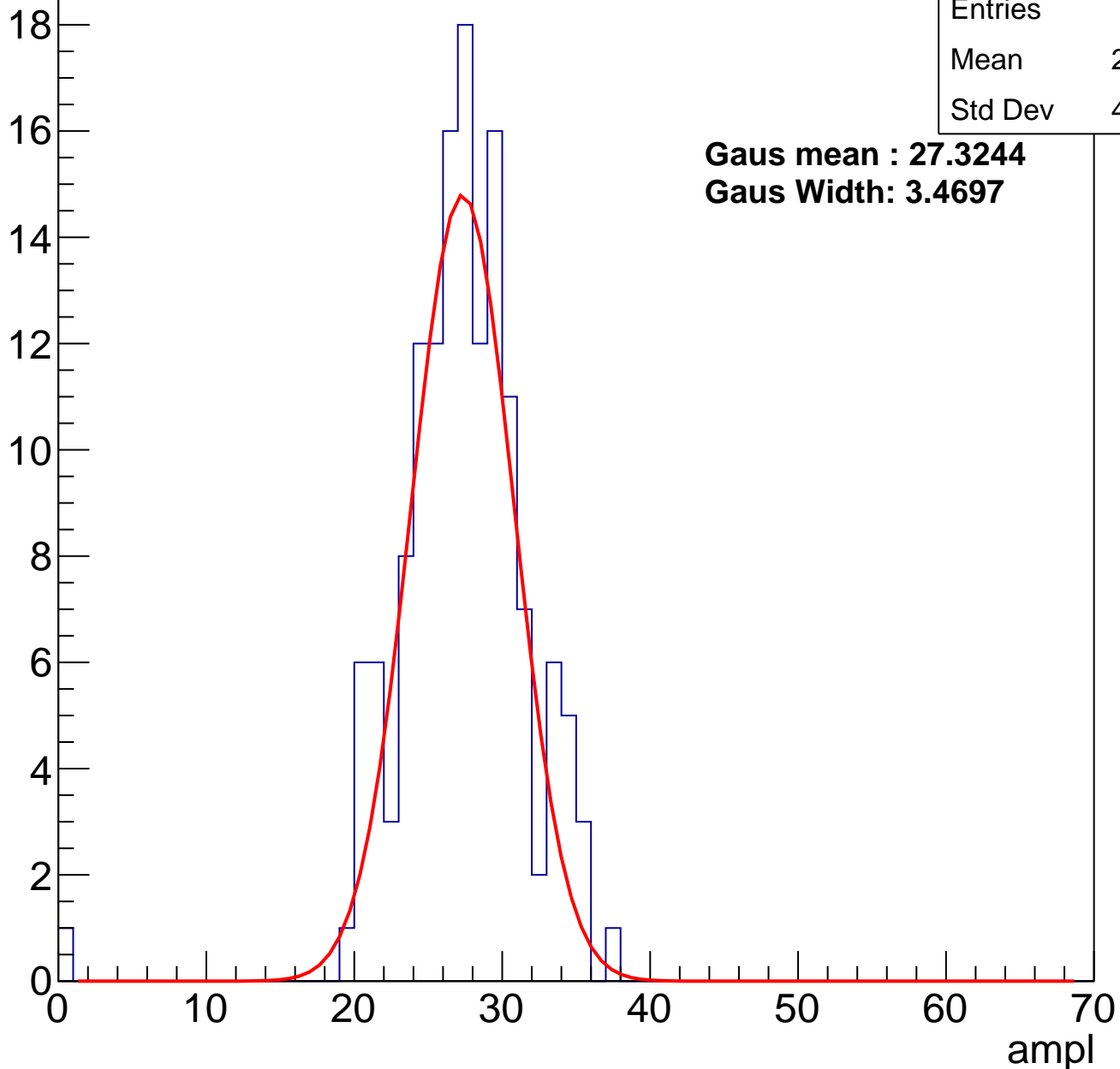
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	26.89
Std Dev	4.353

Gaus mean : 27.3244

Gaus Width: 3.4697

Entry



B0L101S, U21-ch6, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	34.12
Std Dev	3.406

Gaus mean : 34.5196

Gaus Width: 3.7942

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

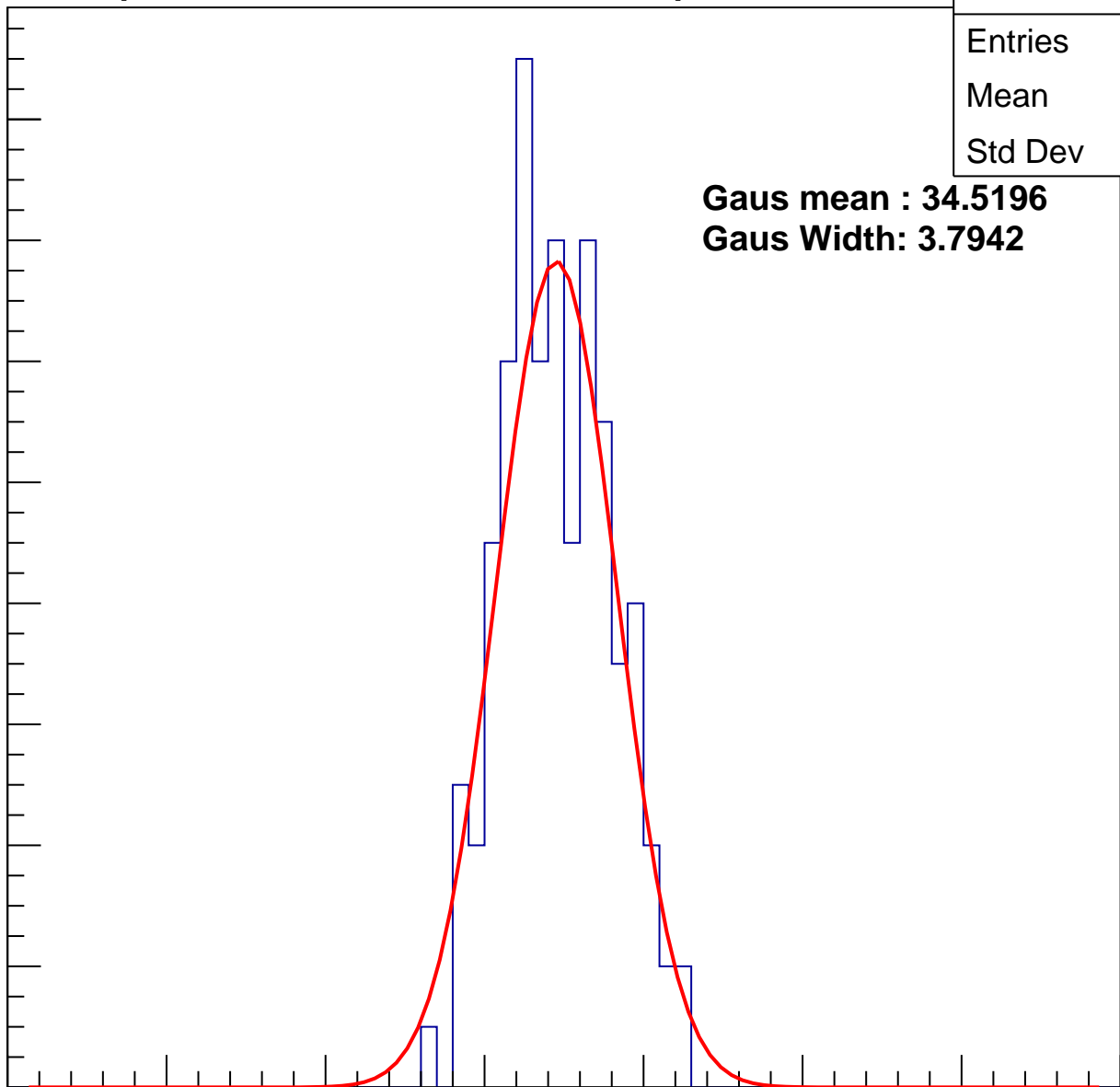
40

50

60

70

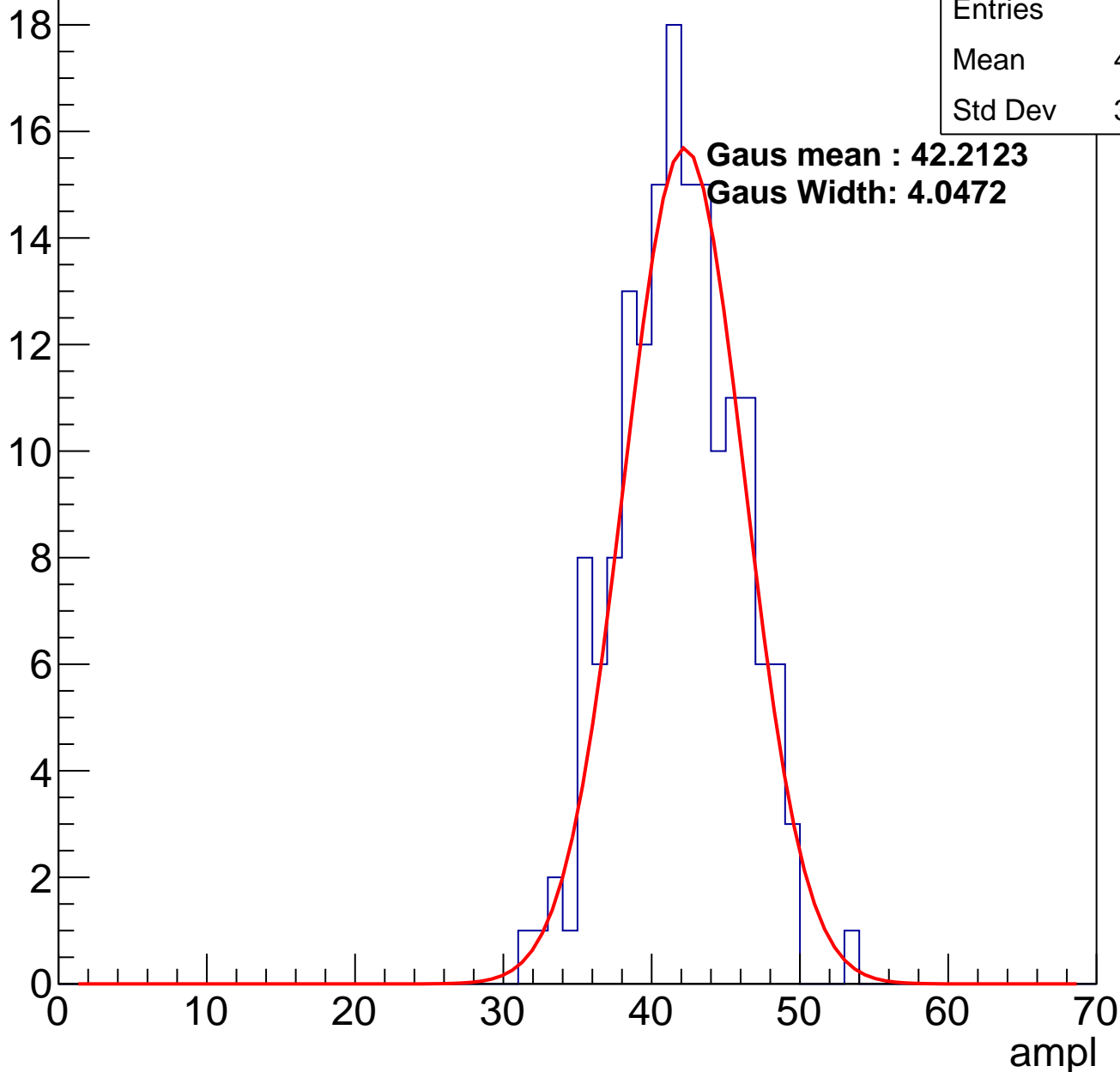
ampl



B0L101S, U21-ch6, adc2

calib_packv5_042523_0143.root, FC#1, port C1

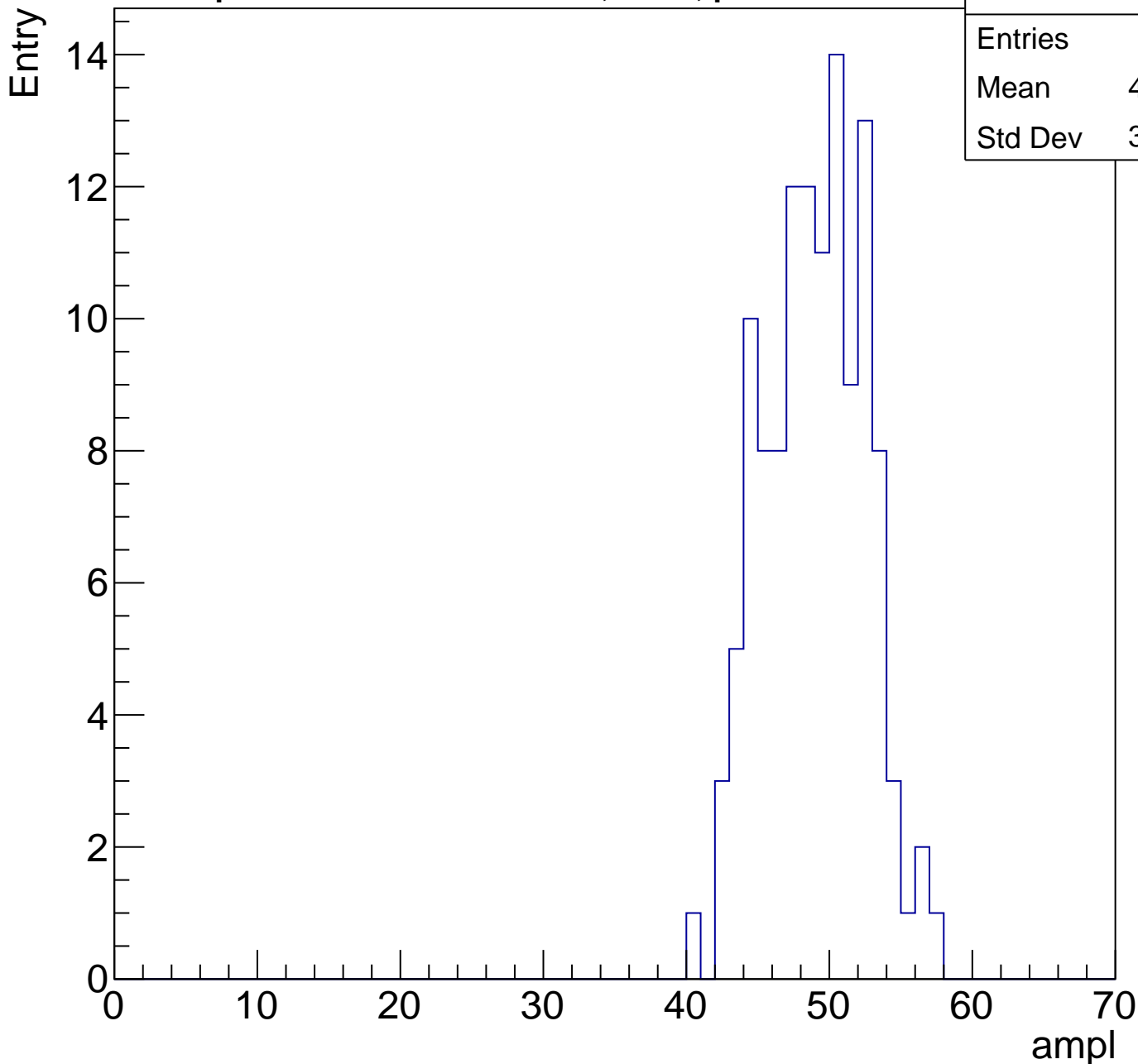
Entry



B0L101S, U21-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	48.54
Std Dev	3.474



B0L101S, U21-ch6, adc4

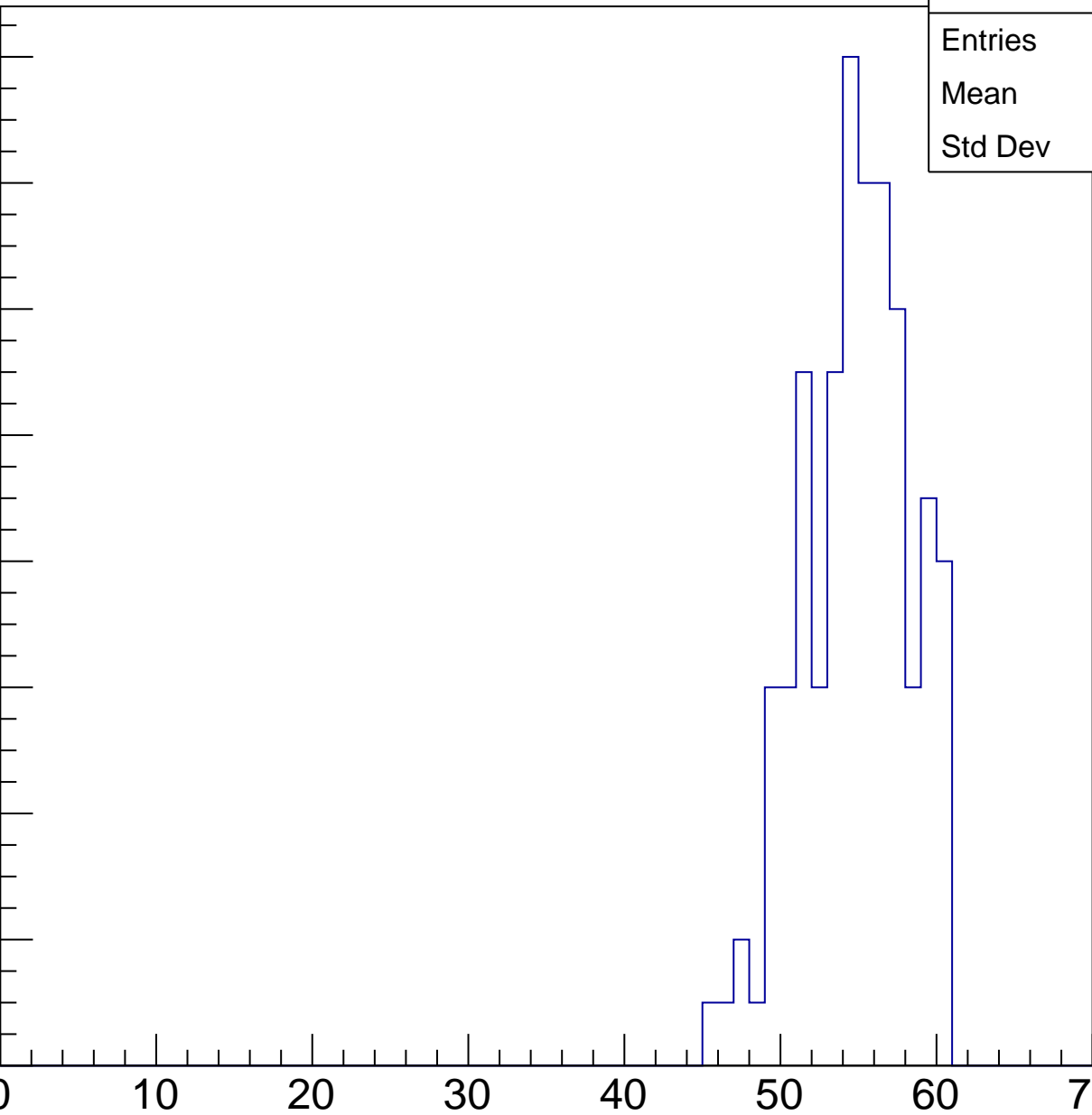
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	54.39
Std Dev	3.393

ampl



B0L101S, U21-ch6, adc5

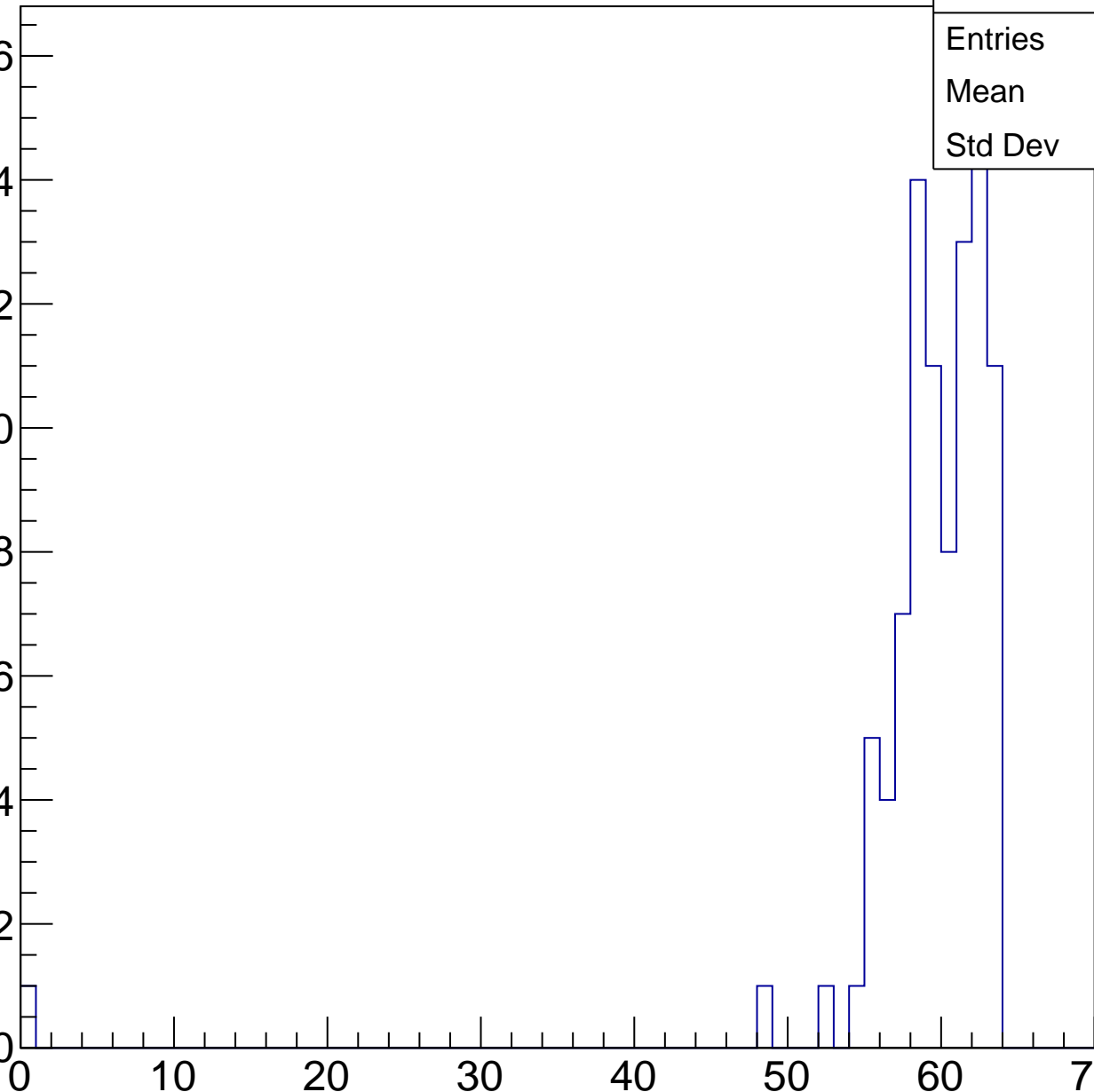
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.83
Std Dev	6.729

ampl

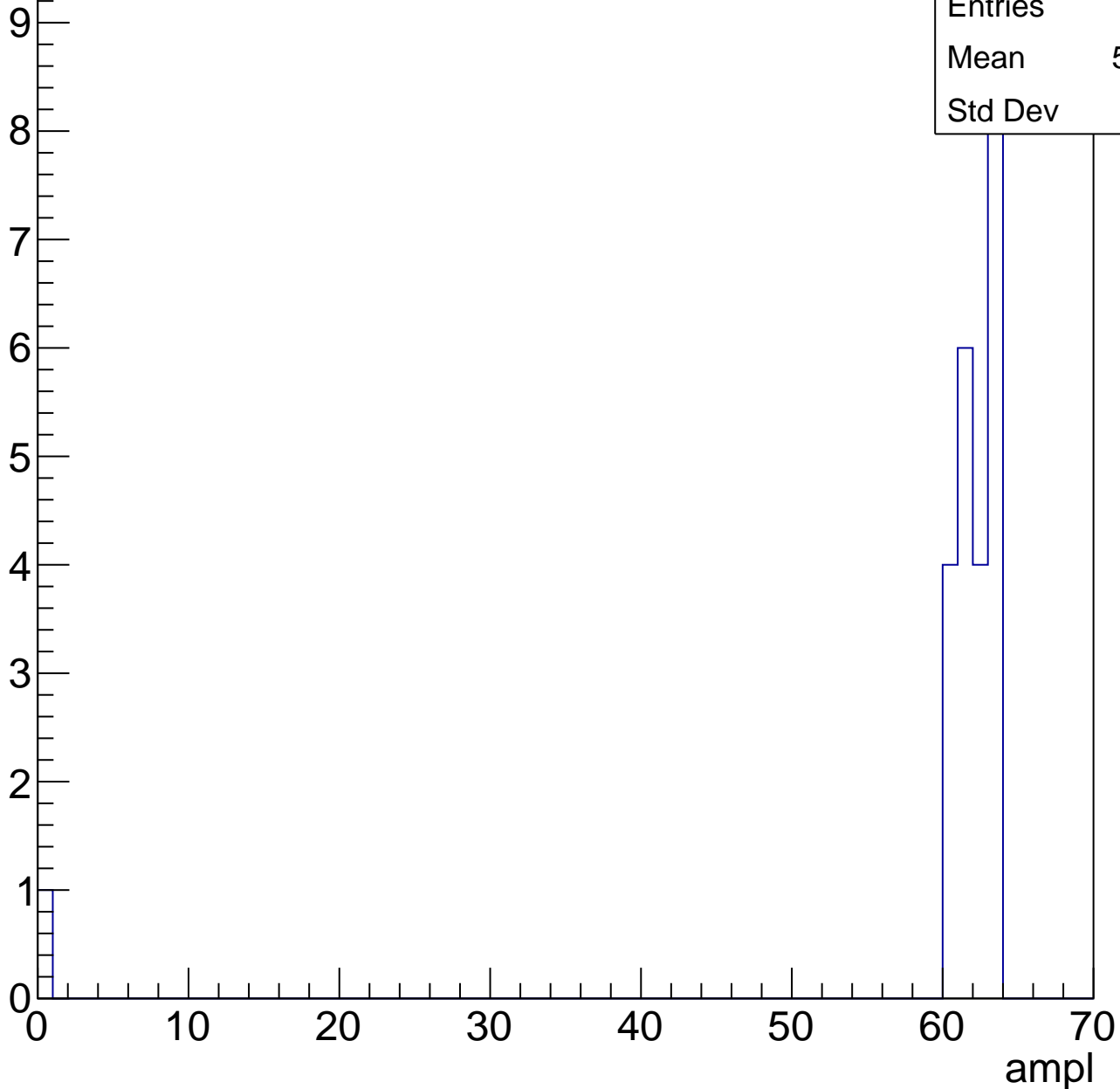


B0L101S, U21-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	24
Mean	59.21
Std Dev	12.4



B0L101S, U21-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch7, adc0

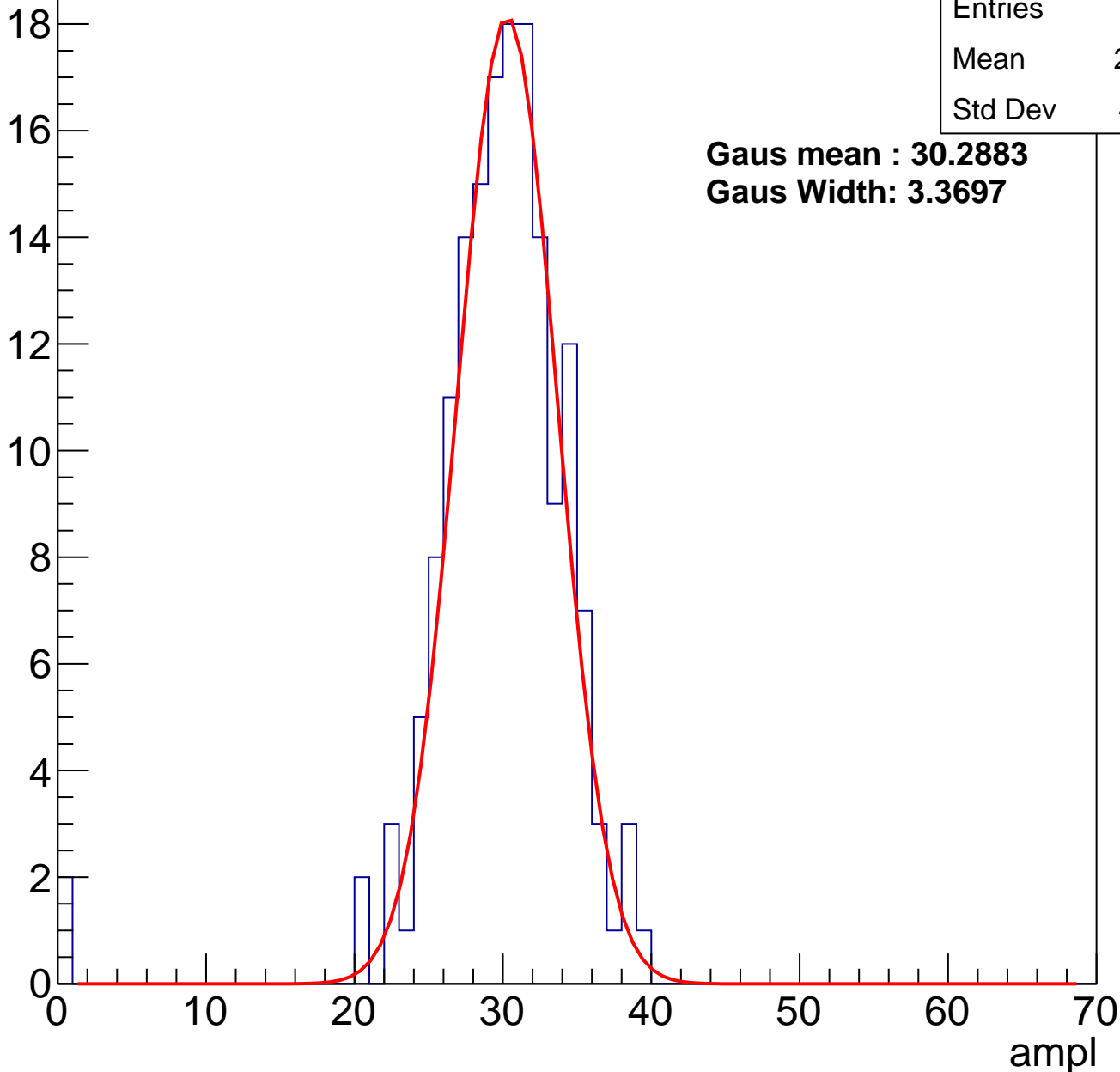
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	29.39
Std Dev	4.871

Gaus mean : 30.2883

Gaus Width: 3.3697

Entry



B0L101S, U21-ch7, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	37.02
Std Dev	3.126

Gaus mean : 37.4426

Gaus Width: 3.2428

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

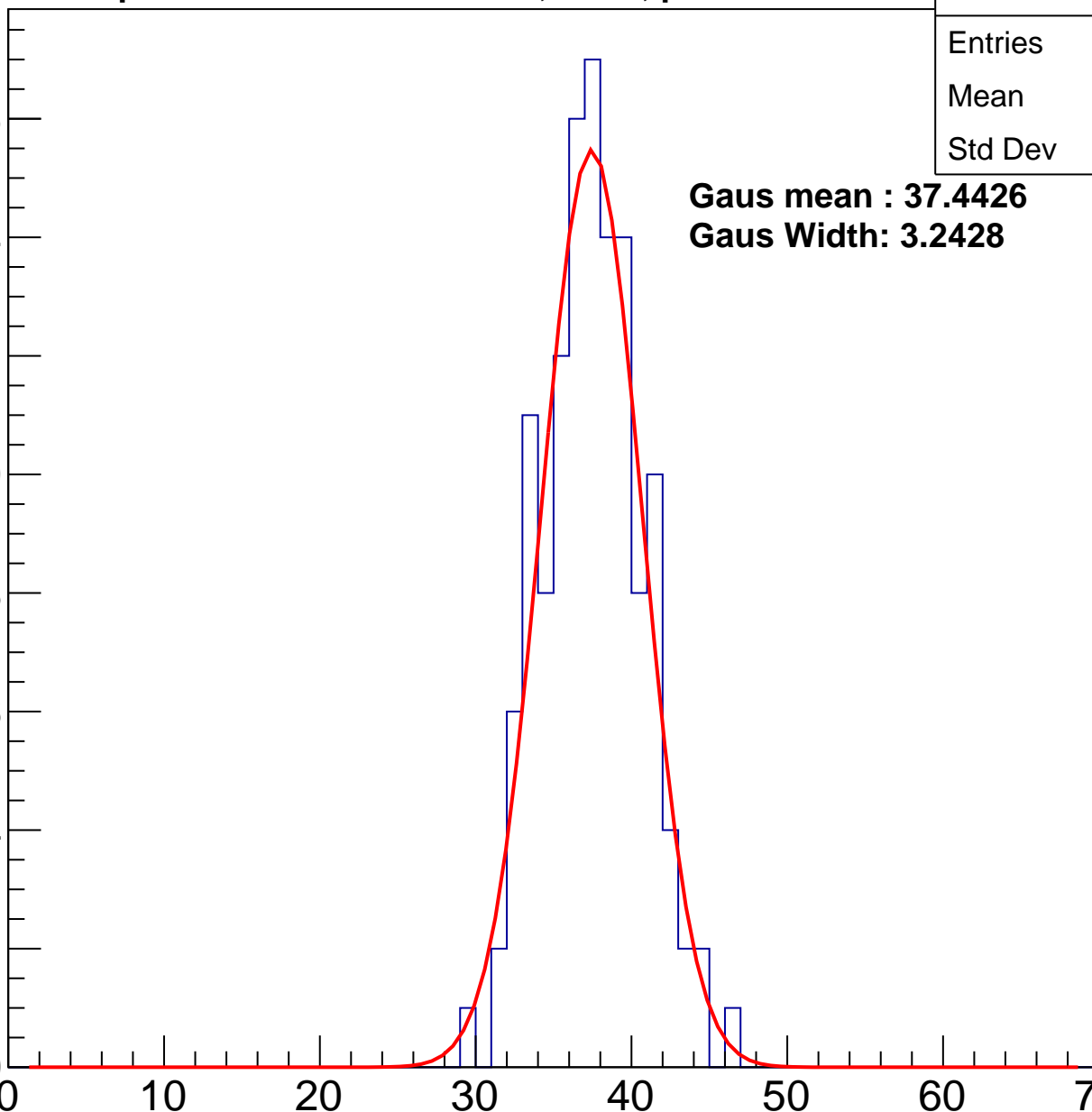
40

50

60

70

ampl



B0L101S, U21-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

153

Mean

44.24

Std Dev

3.502

Gaus mean : 44.7463

Gaus Width: 3.5749

0

10

20

30

40

50

60

ampl

70

B0L101S, U21-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

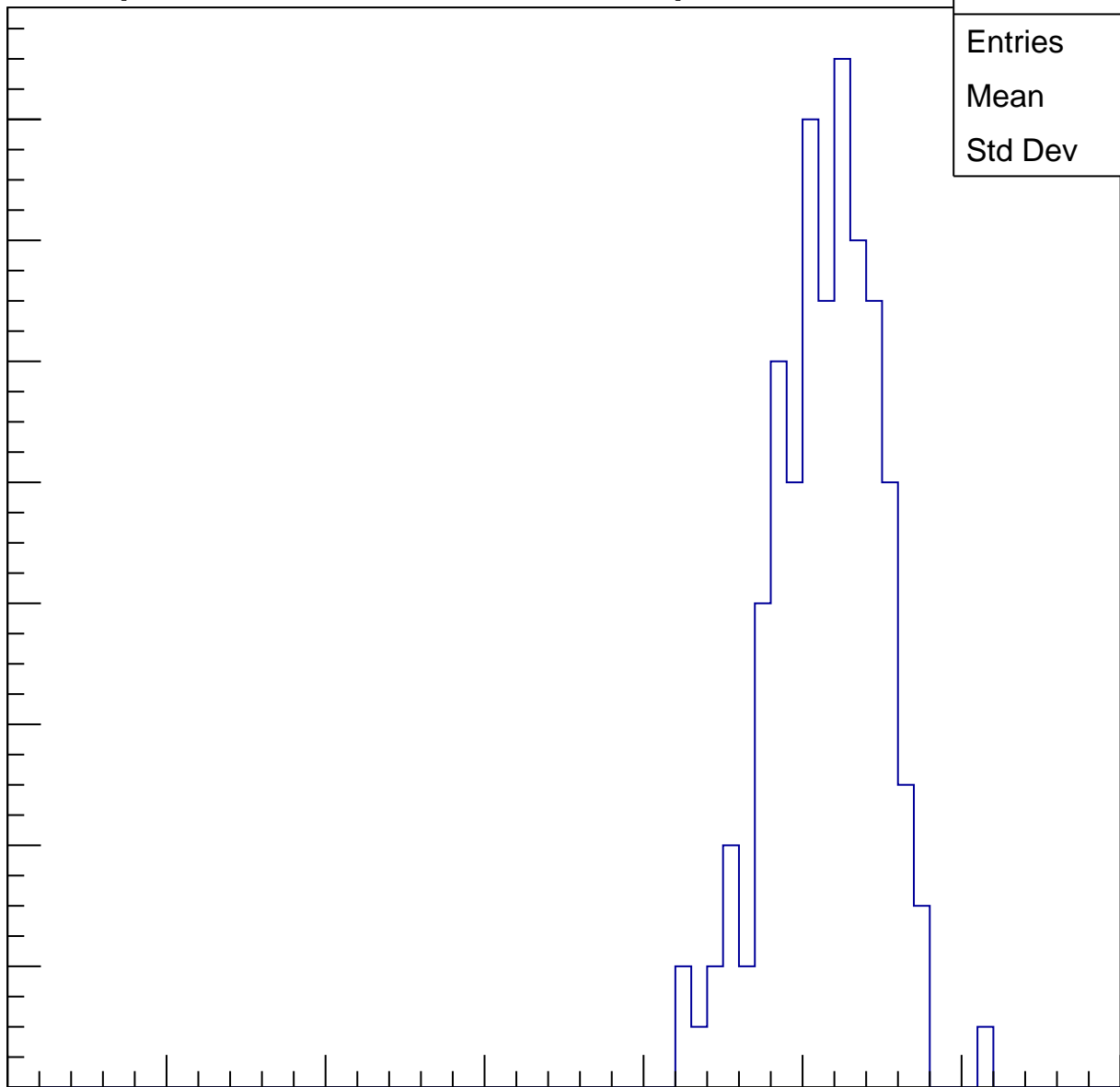
Entries	133
Mean	50.99
Std Dev	3.35

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch7, adc4

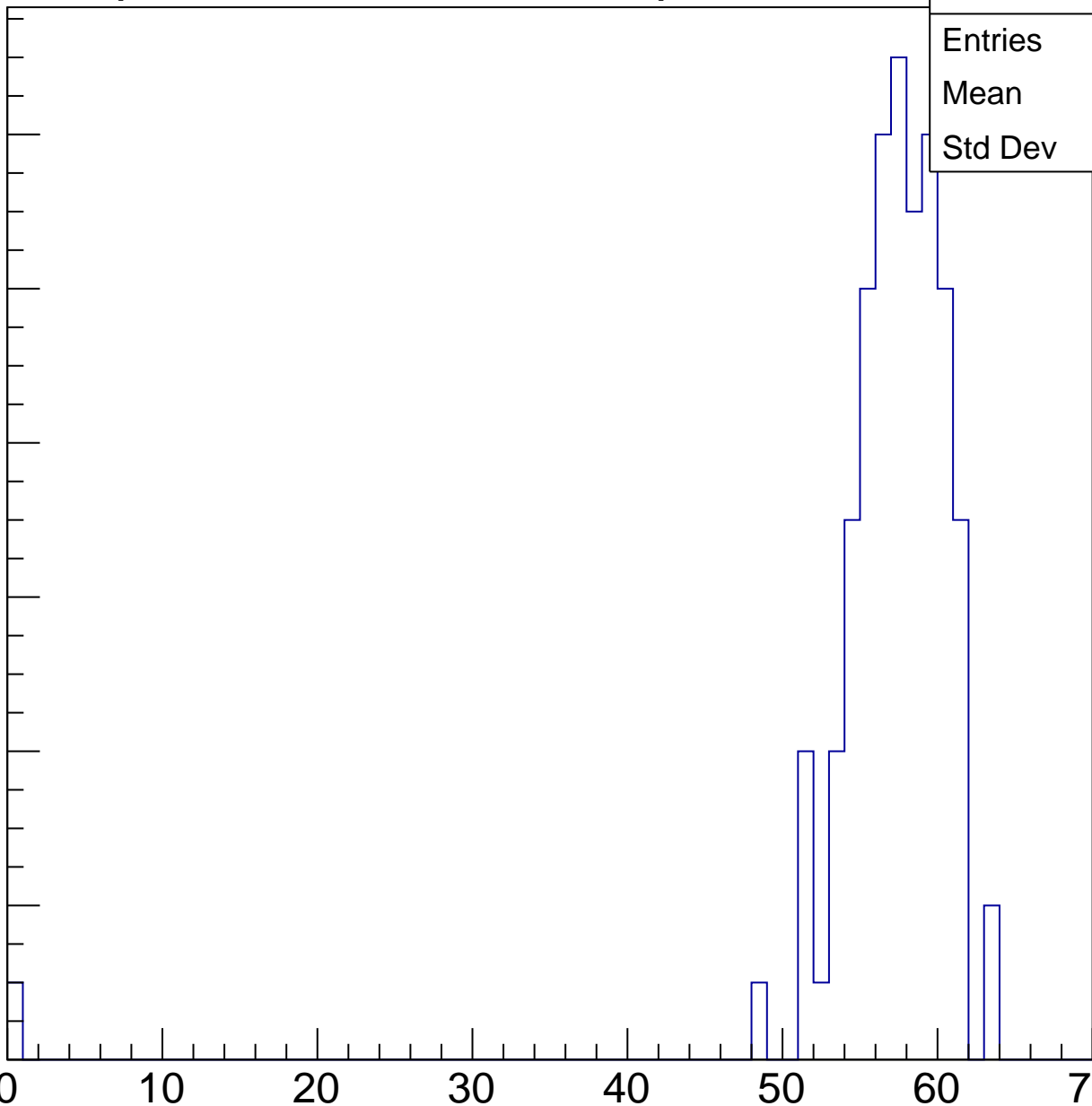
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	95
Mean	56.38
Std Dev	6.468

ampl



B0L101S, U21-ch7, adc5

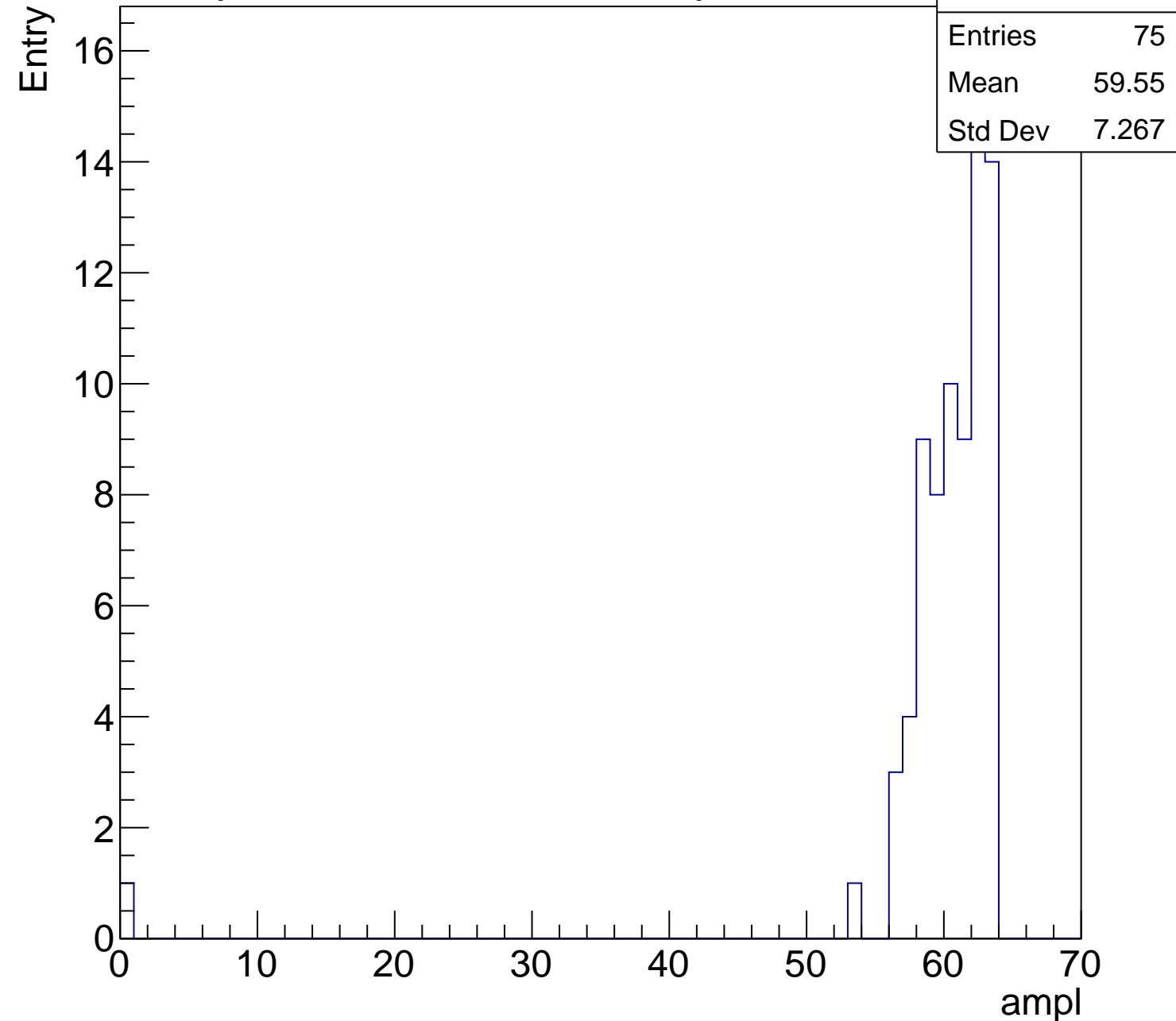
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	59.55
Std Dev	7.267

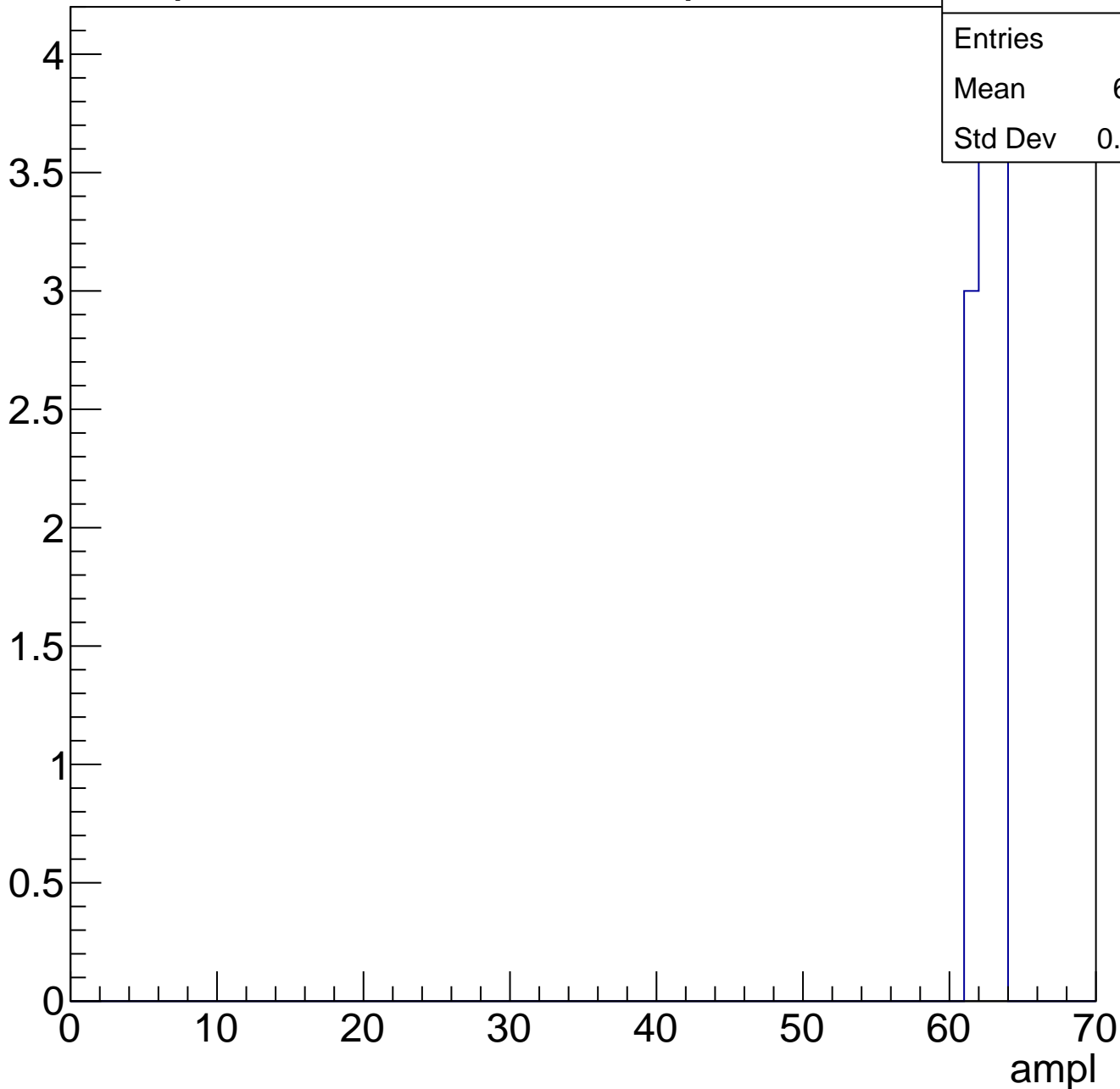
ampl



B0L101S, U21-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch8, adc0

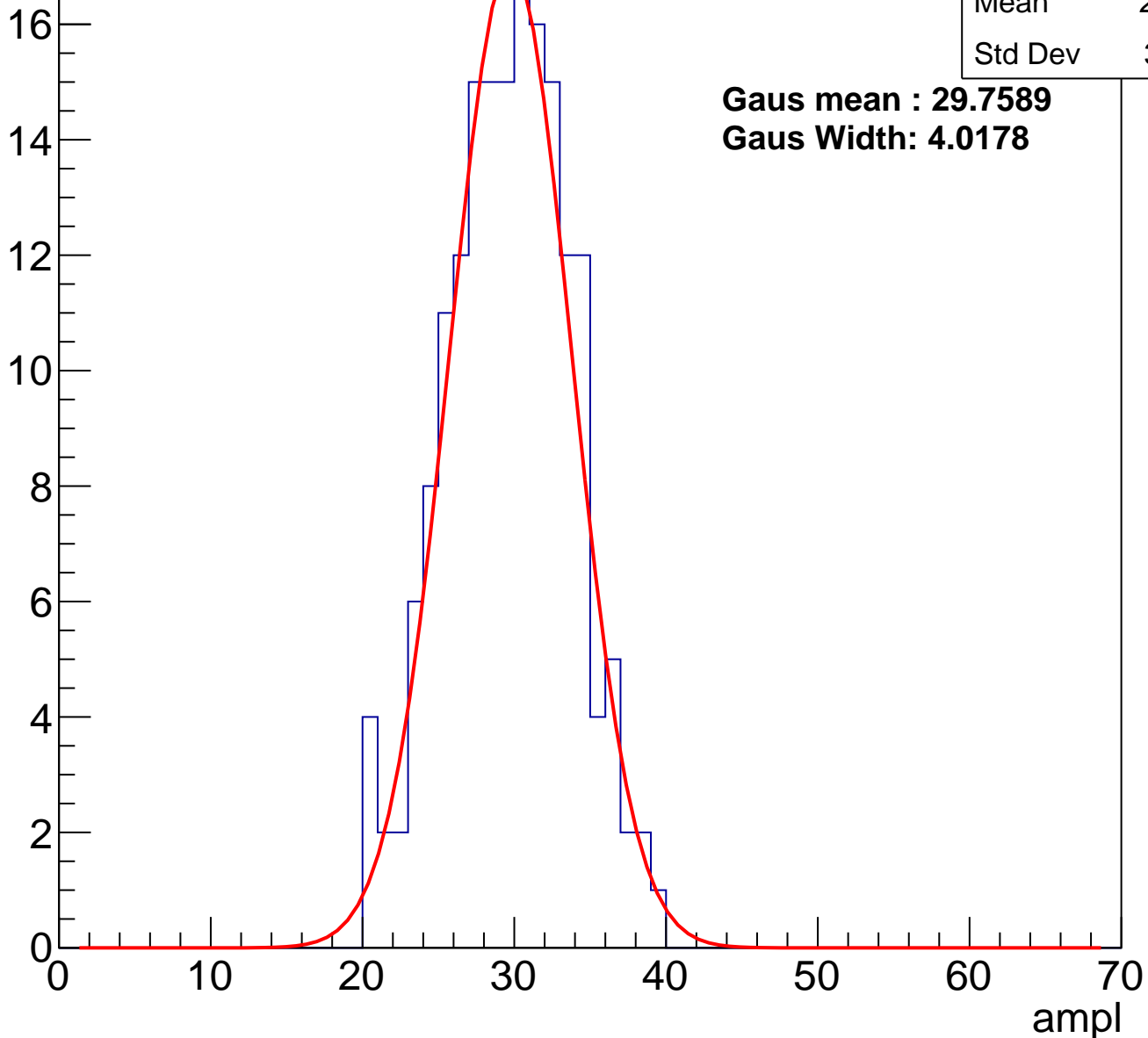
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	176
Mean	29.22
Std Dev	3.991

Gaus mean : 29.7589

Gaus Width: 4.0178



B0L101S, U21-ch8, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	37.13
Std Dev	3.568

Gaus mean : 37.5255

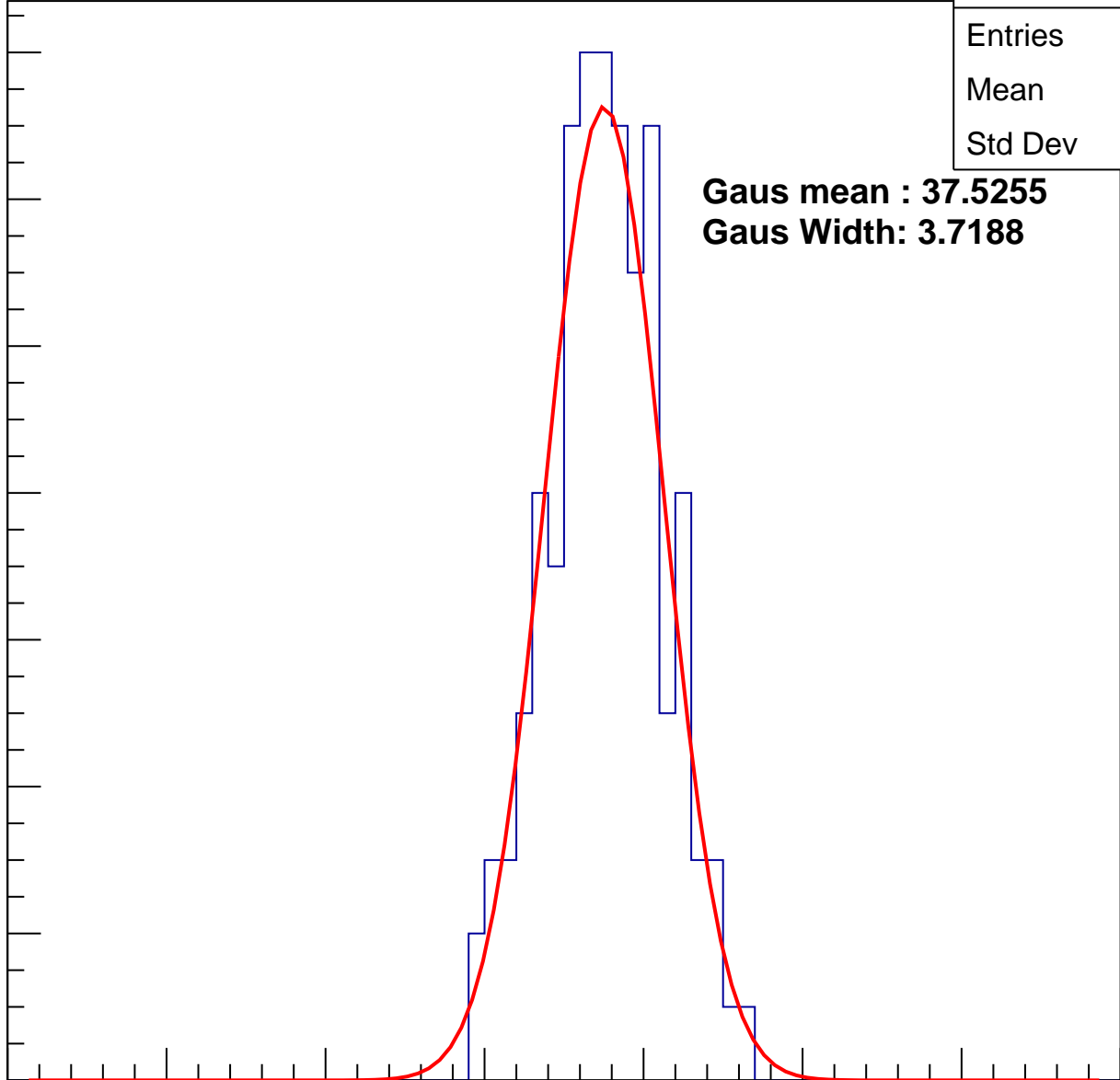
Gaus Width: 3.7188

Entry

14
12
10
8
6
4
2
0

ampl

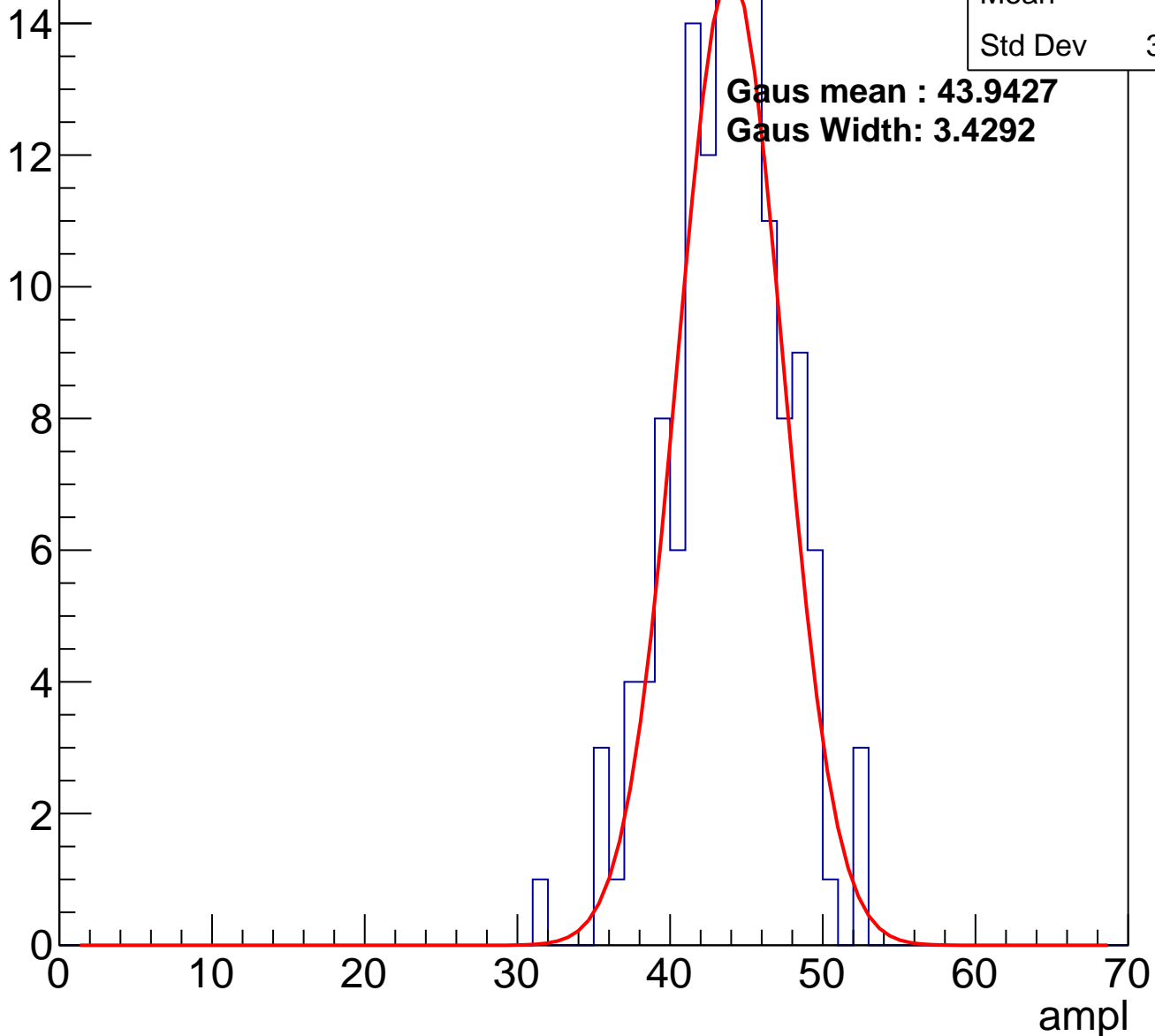
0 10 20 30 40 50 60 70



B0L101S, U21-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

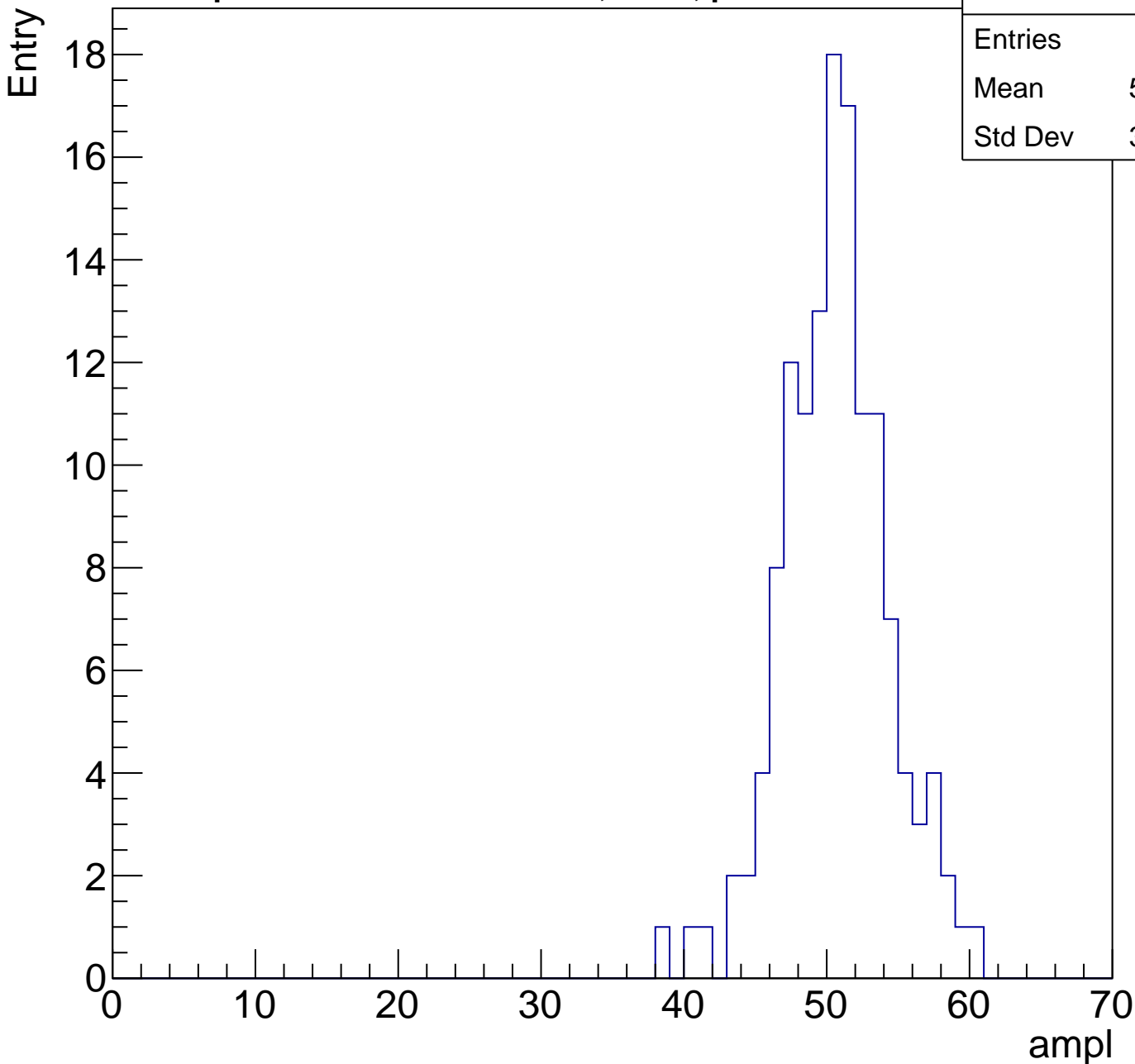


Entries	136
Mean	43.35
Std Dev	3.735

B0L101S, U21-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	50.16
Std Dev	3.735



B0L101S, U21-ch8, adc4

calib_packv5_042523_0143.root, FC#1, port C1

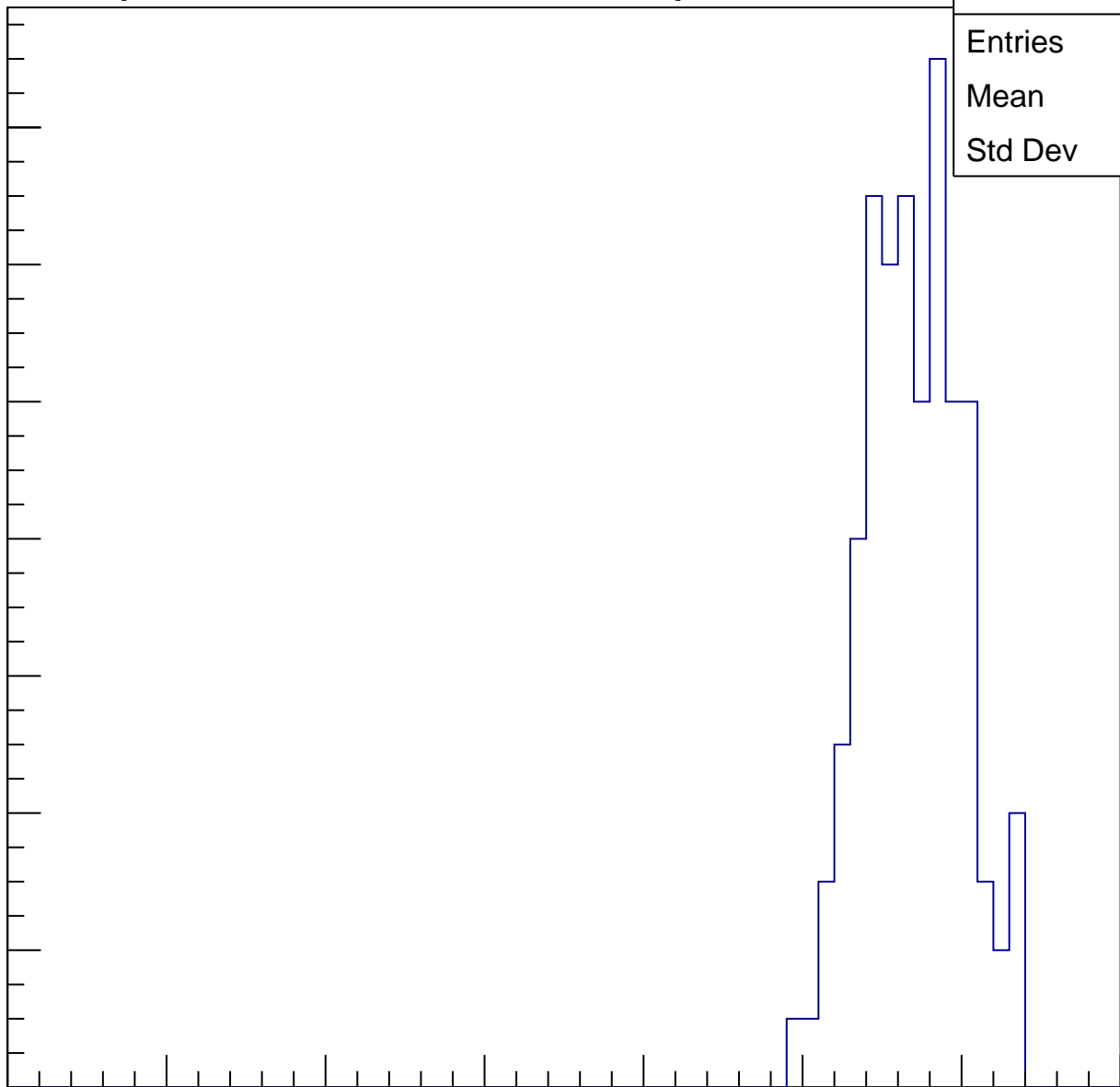
Entries	110
Mean	56.5
Std Dev	3.032

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

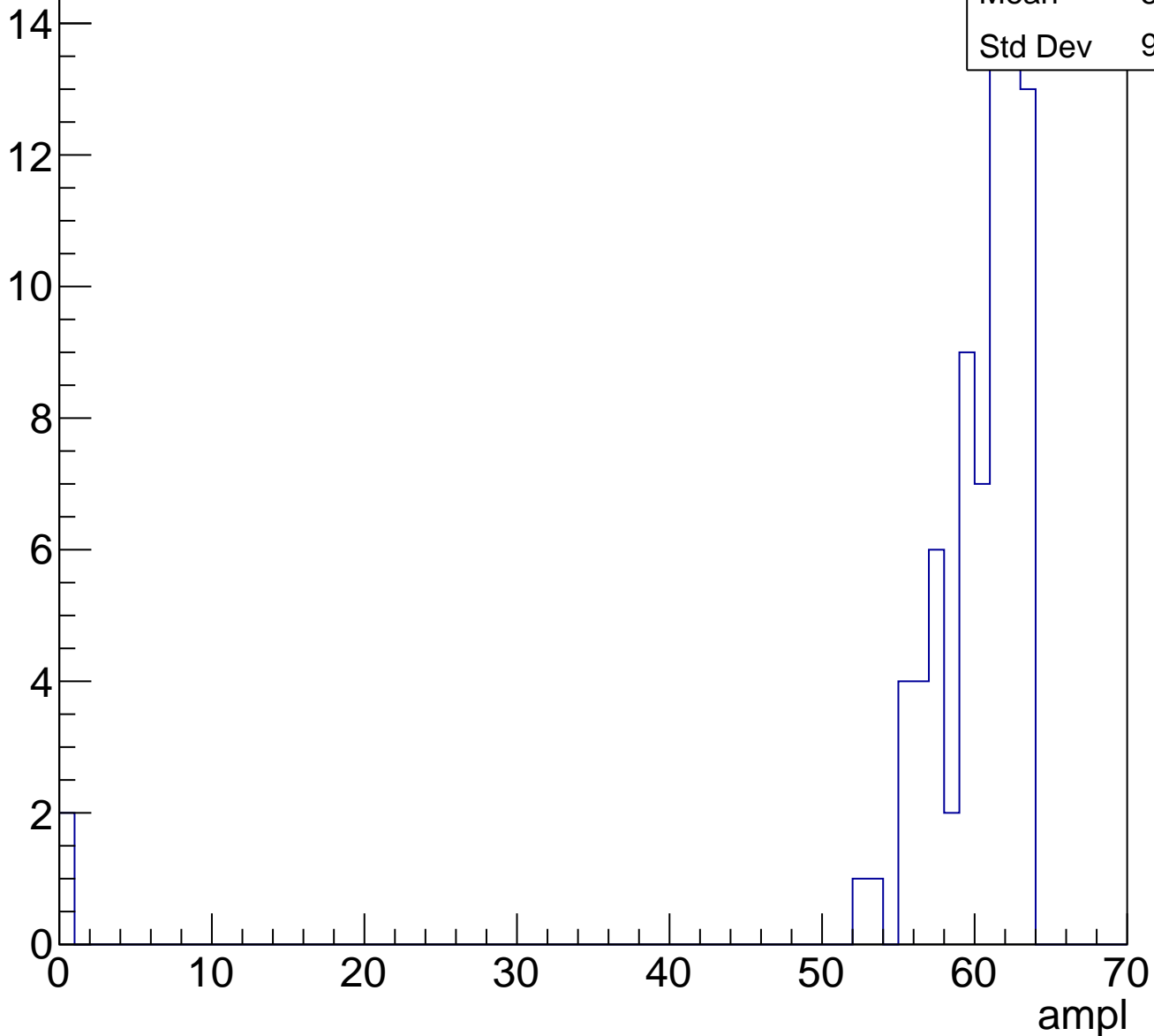
ampl



B0L101S, U21-ch8, adc5

calib_packv5_042523_0143.root, FC#1, port C1

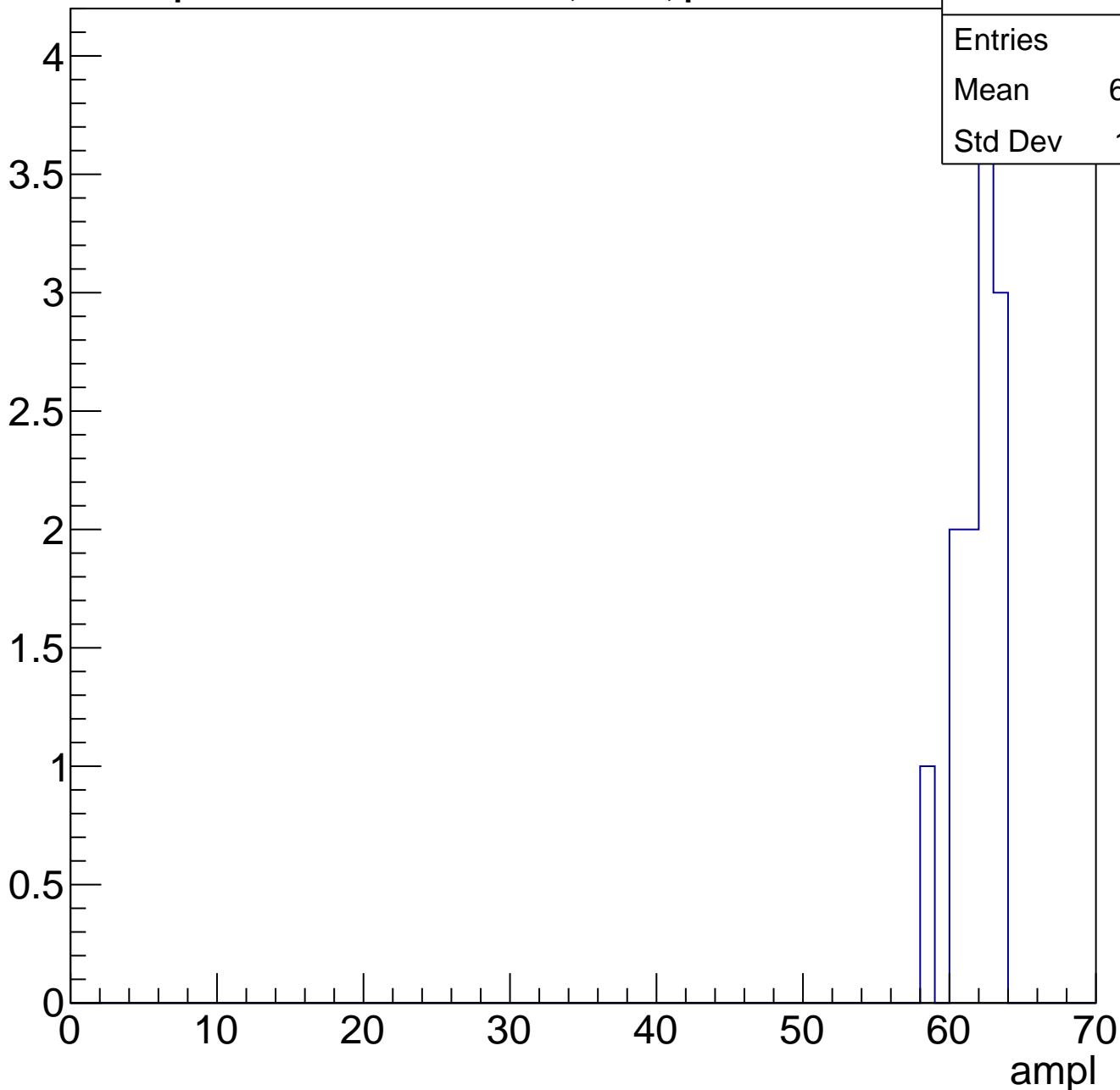
Entry



B0L101S, U21-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch9, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	29.77
Std Dev	4.309

Gaus mean : 30.6168

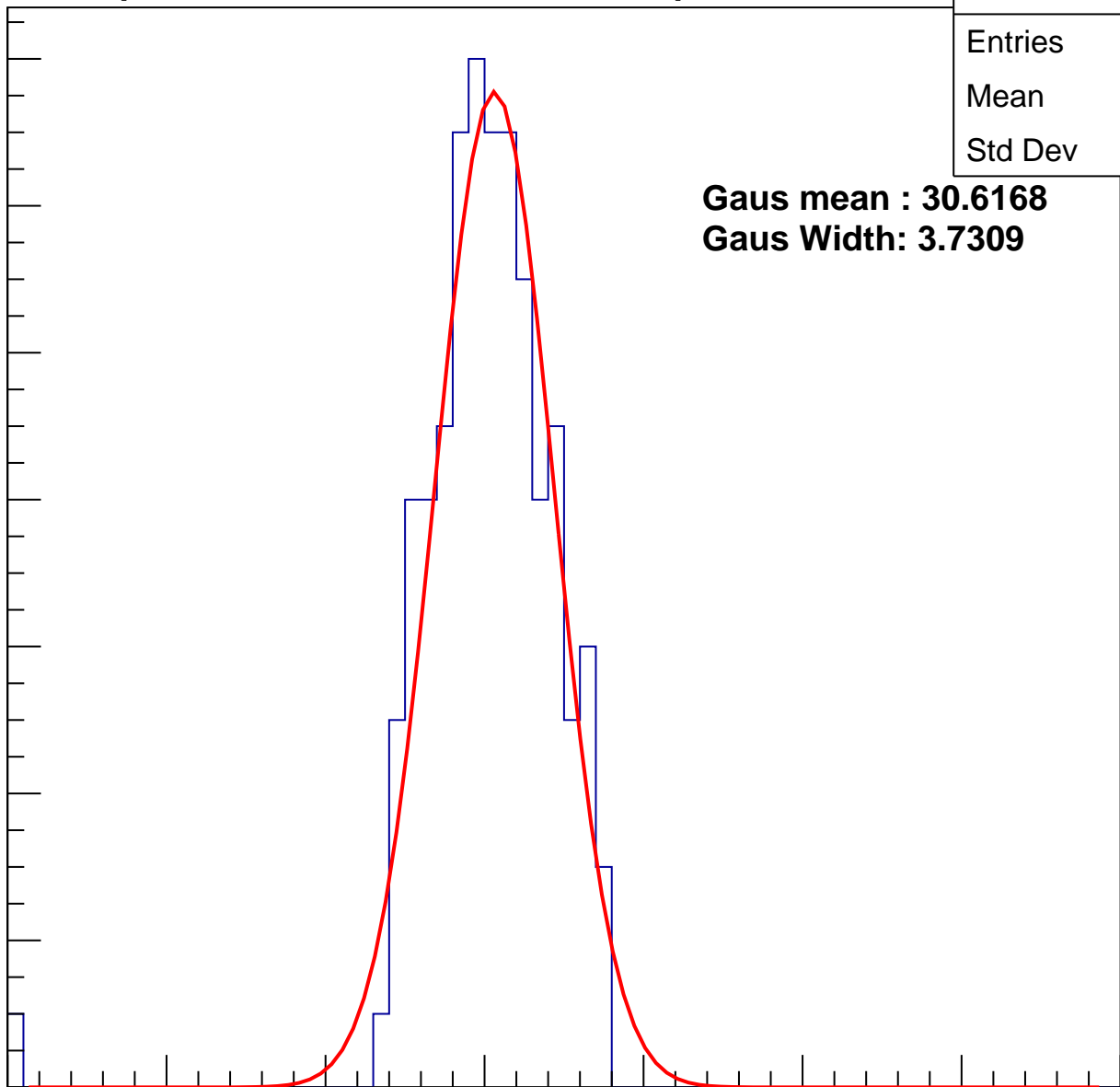
Gaus Width: 3.7309

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch9, adc1

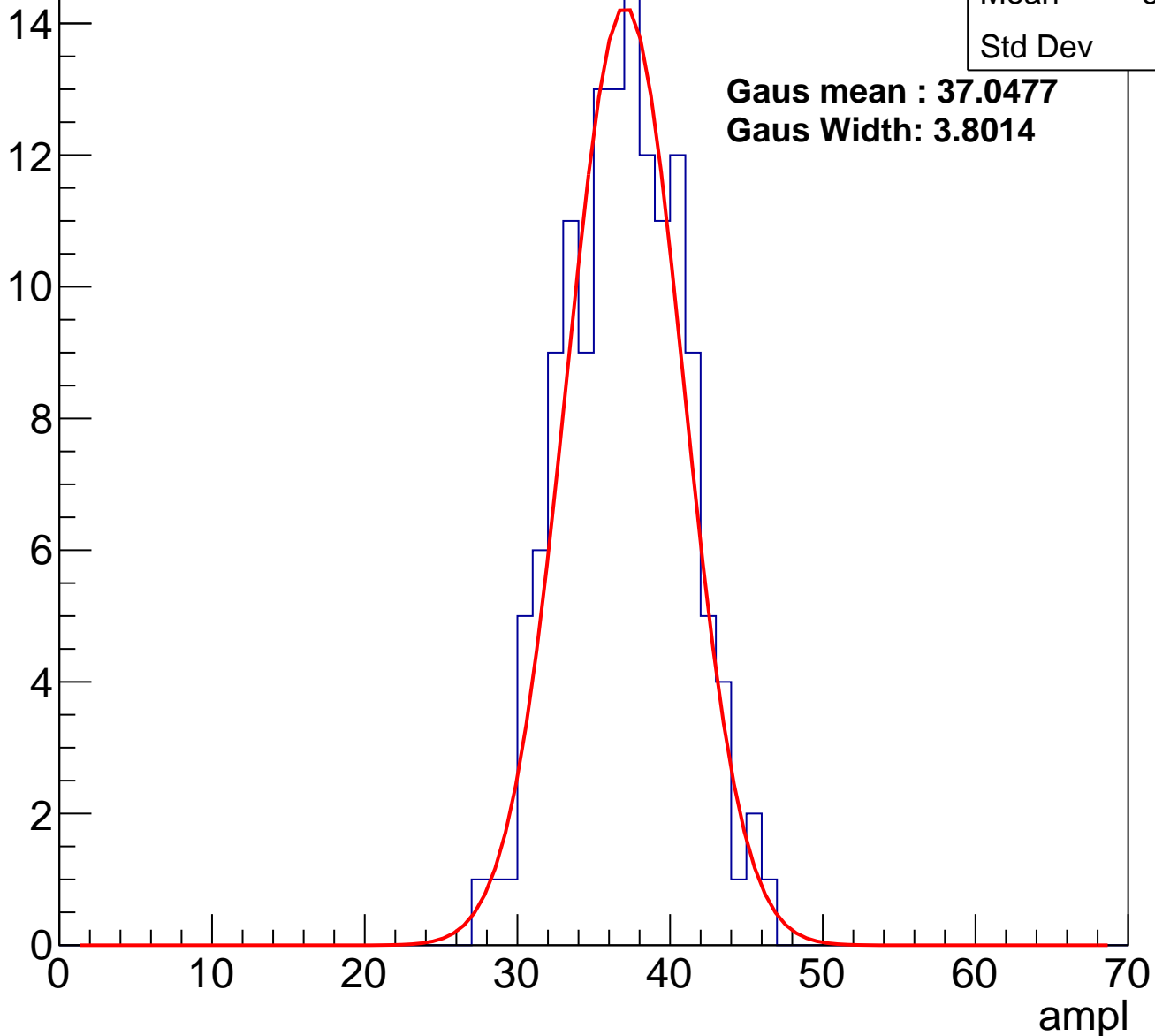
calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.53
Std Dev	3.82

Gaus mean : 37.0477

Gaus Width: 3.8014

Entry



B0L101S, U21-ch9, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

154

Mean

43.53

Std Dev

3.768

Gaus mean : 44.4095

Gaus Width: 3.7439

0

10

20

30

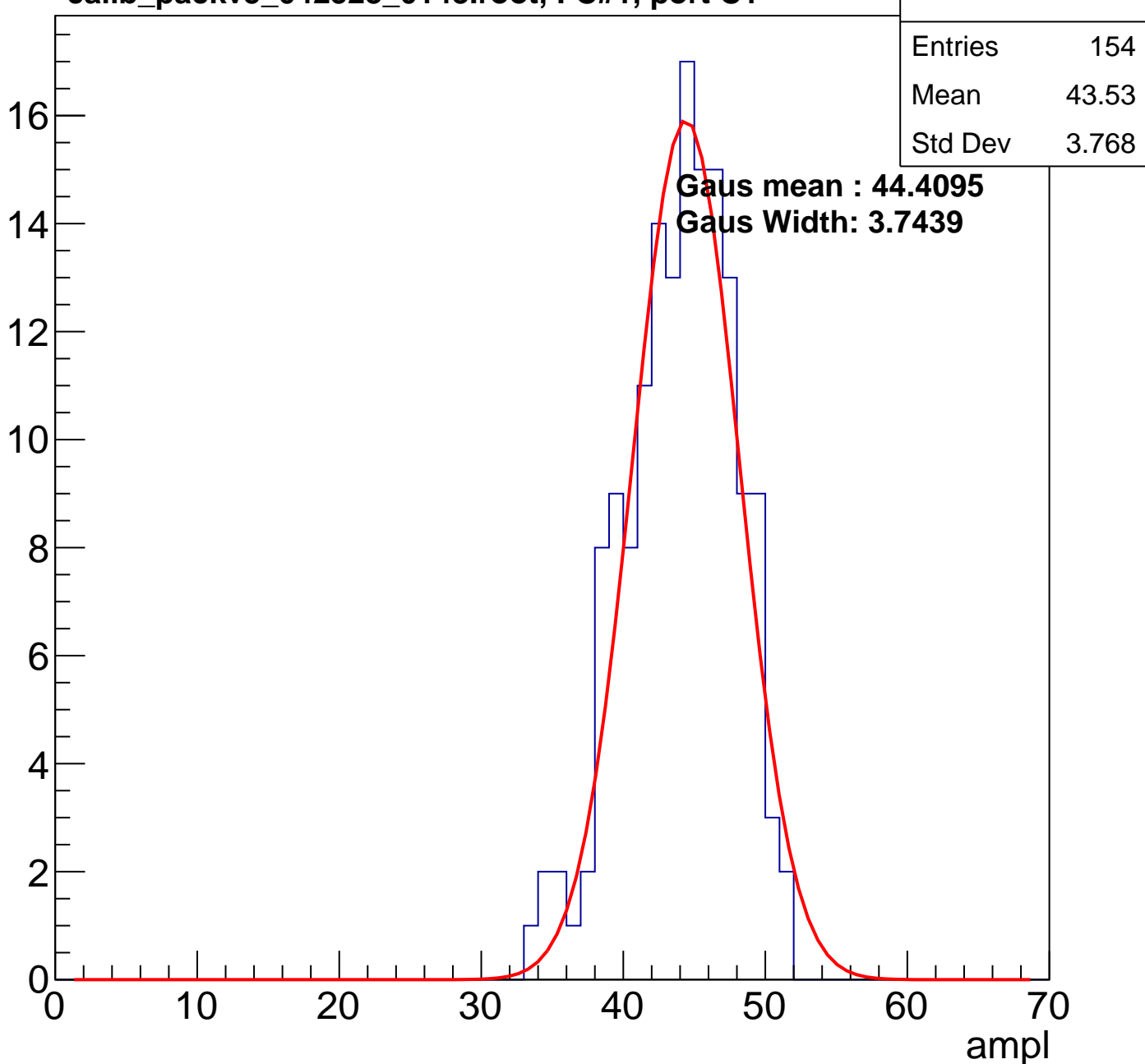
40

50

60

70

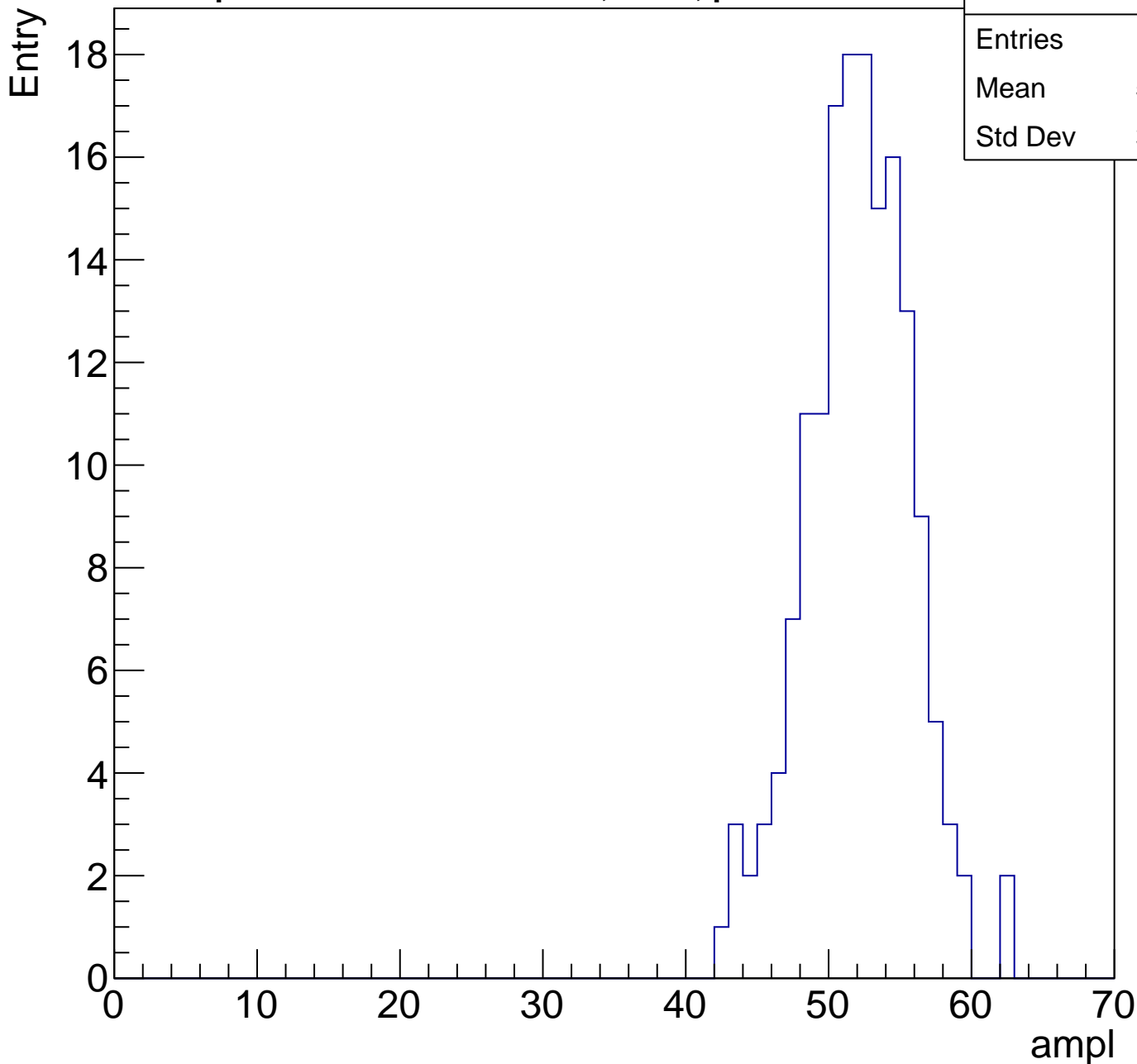
ampl



B0L101S, U21-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	51.61
Std Dev	3.661



B0L101S, U21-ch9, adc4

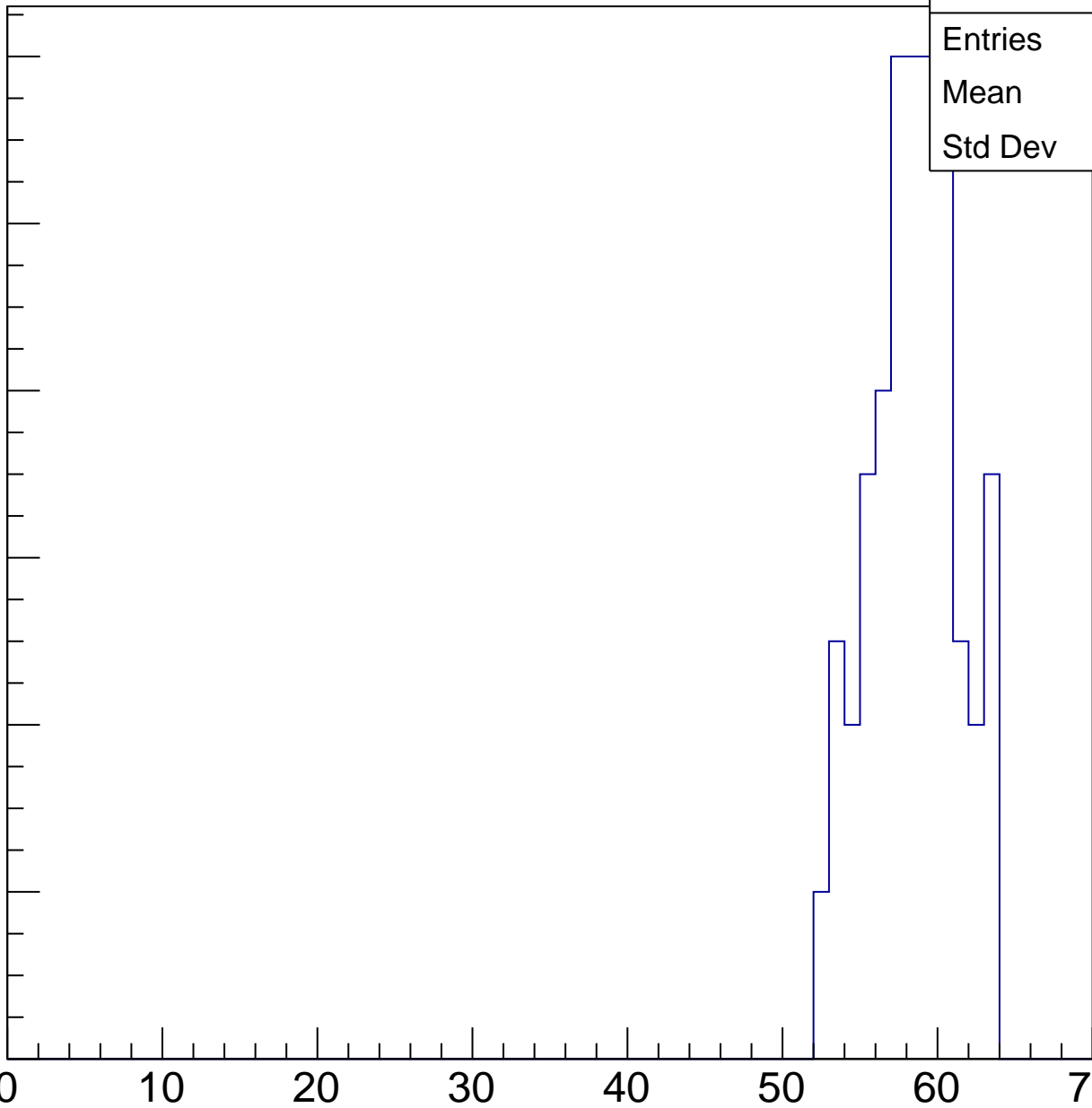
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	89
Mean	57.98
Std Dev	2.82

ampl



B0L101S, U21-ch9, adc5

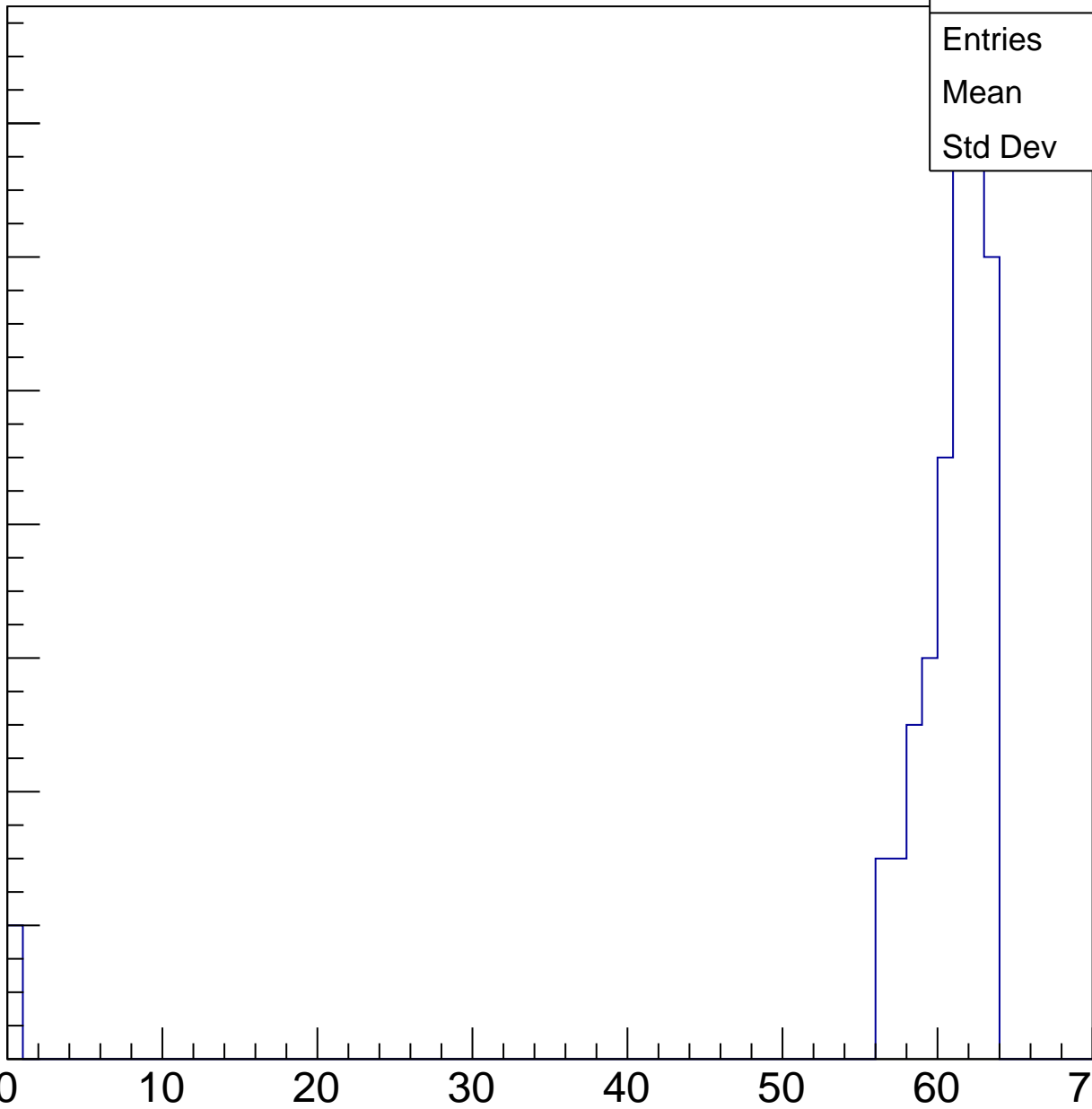
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	58.87
Std Dev	10.35

ampl



B0L101S, U21-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L101S, U21-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L101S, U21-ch10, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	29.9
Std Dev	3.313

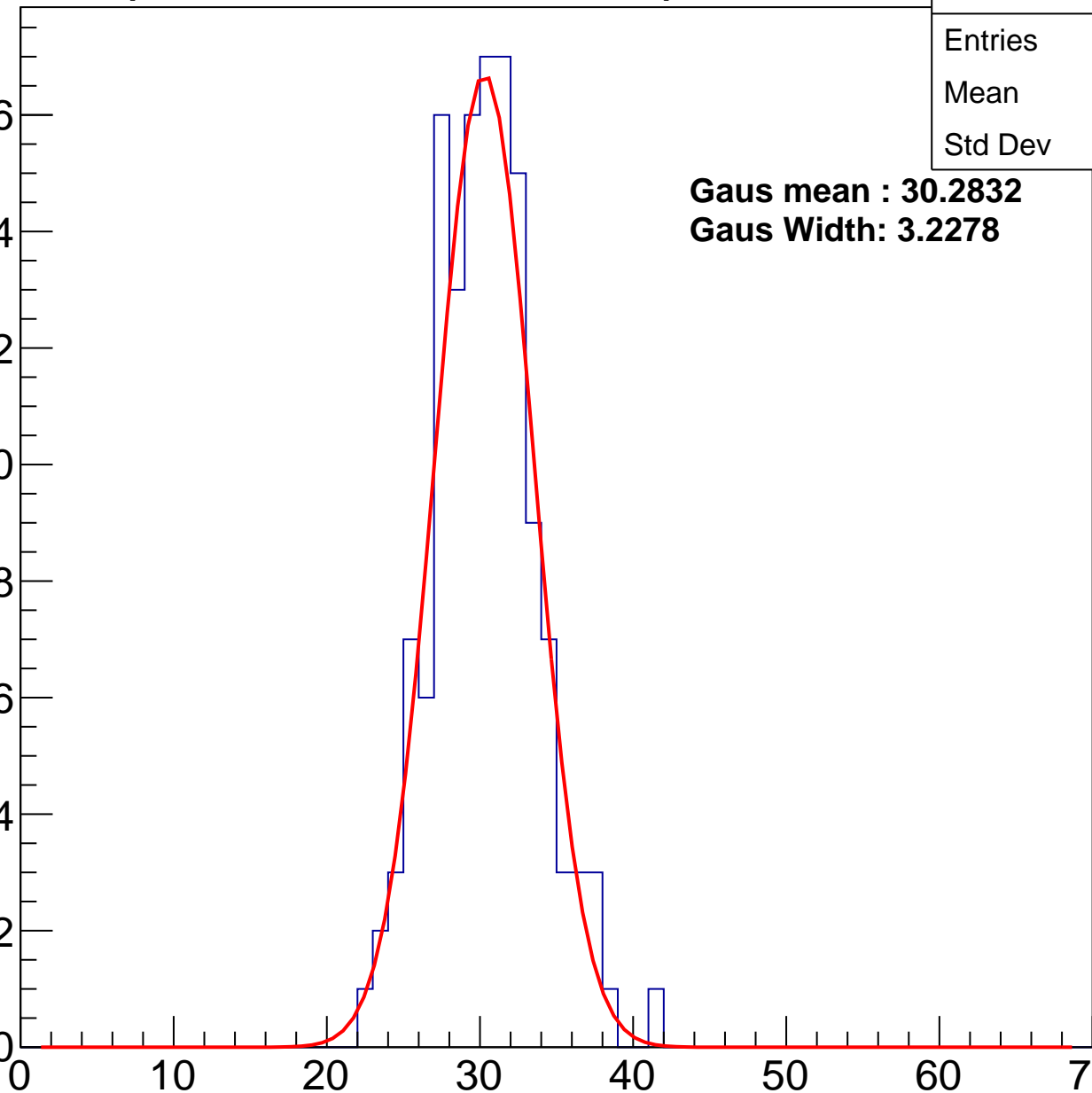
Gaus mean : 30.2832

Gaus Width: 3.2278

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	37.17
Std Dev	3.558

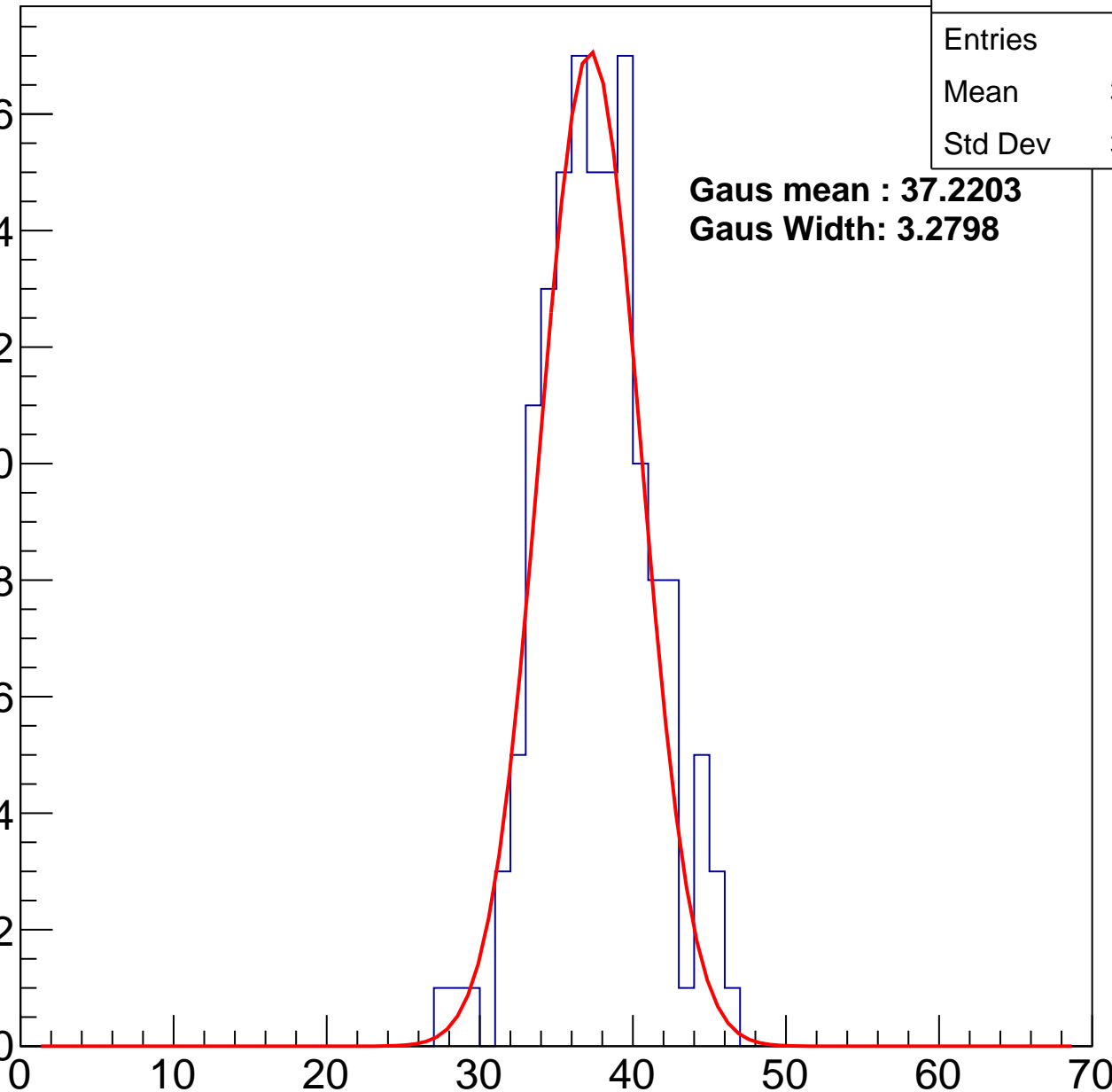
Gaus mean : 37.2203

Gaus Width: 3.2798

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch10, adc2

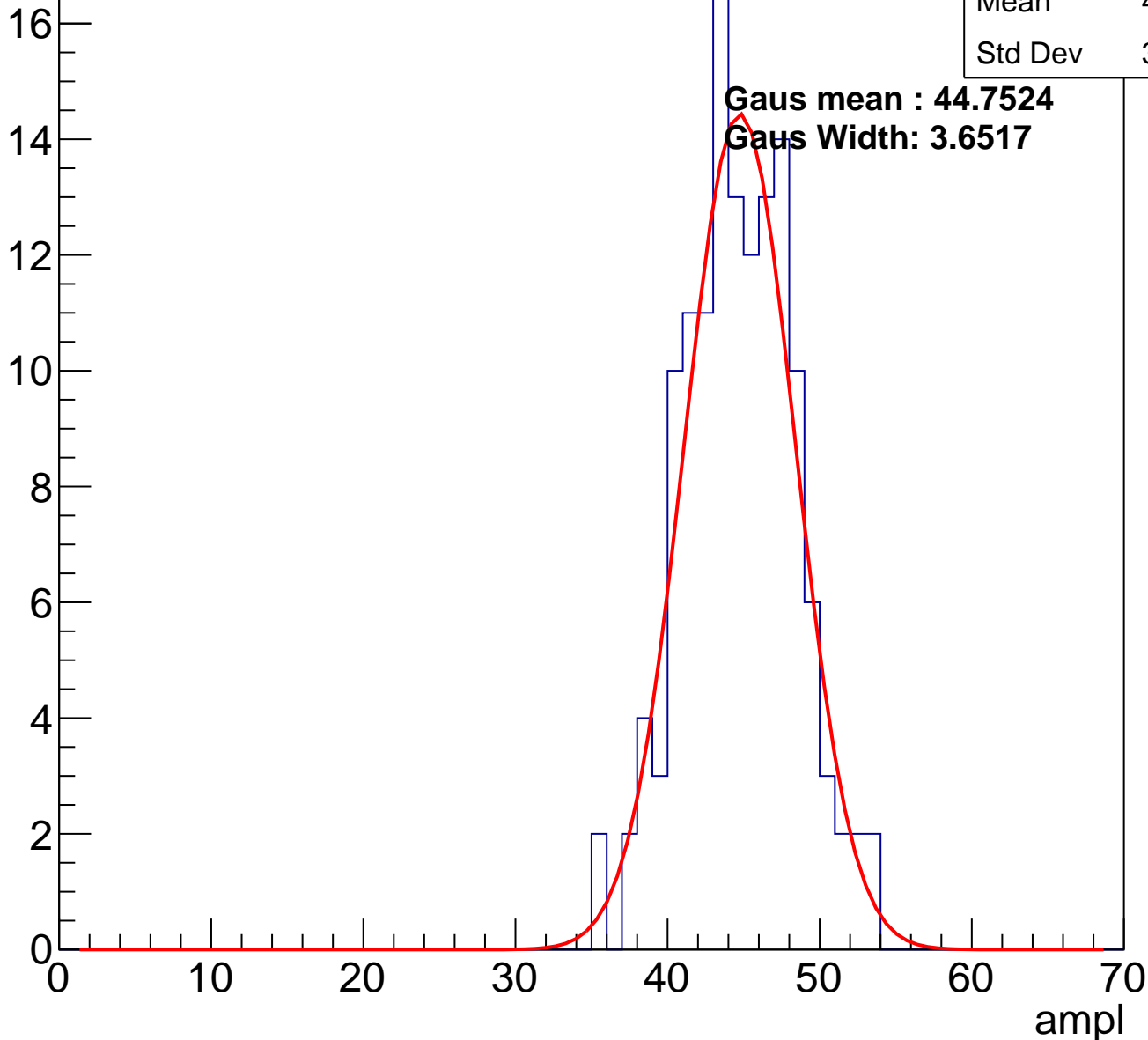
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	137
Mean	44.24
Std Dev	3.612

Gaus mean : 44.7524

Gaus Width: 3.6517



B0L101S, U21-ch10, adc3

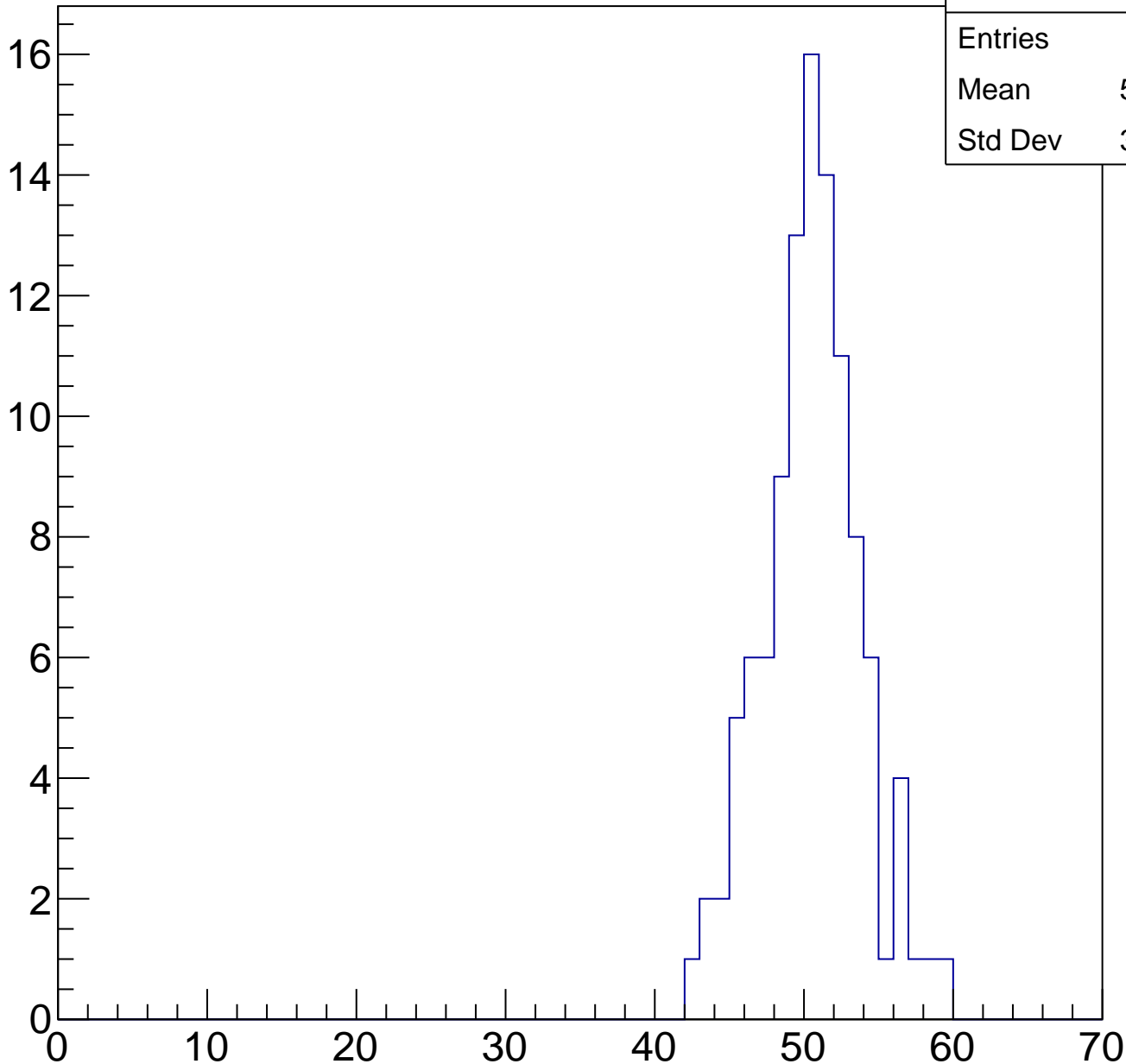
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	50.05
Std Dev	3.274

ampl



B0L101S, U21-ch10, adc4

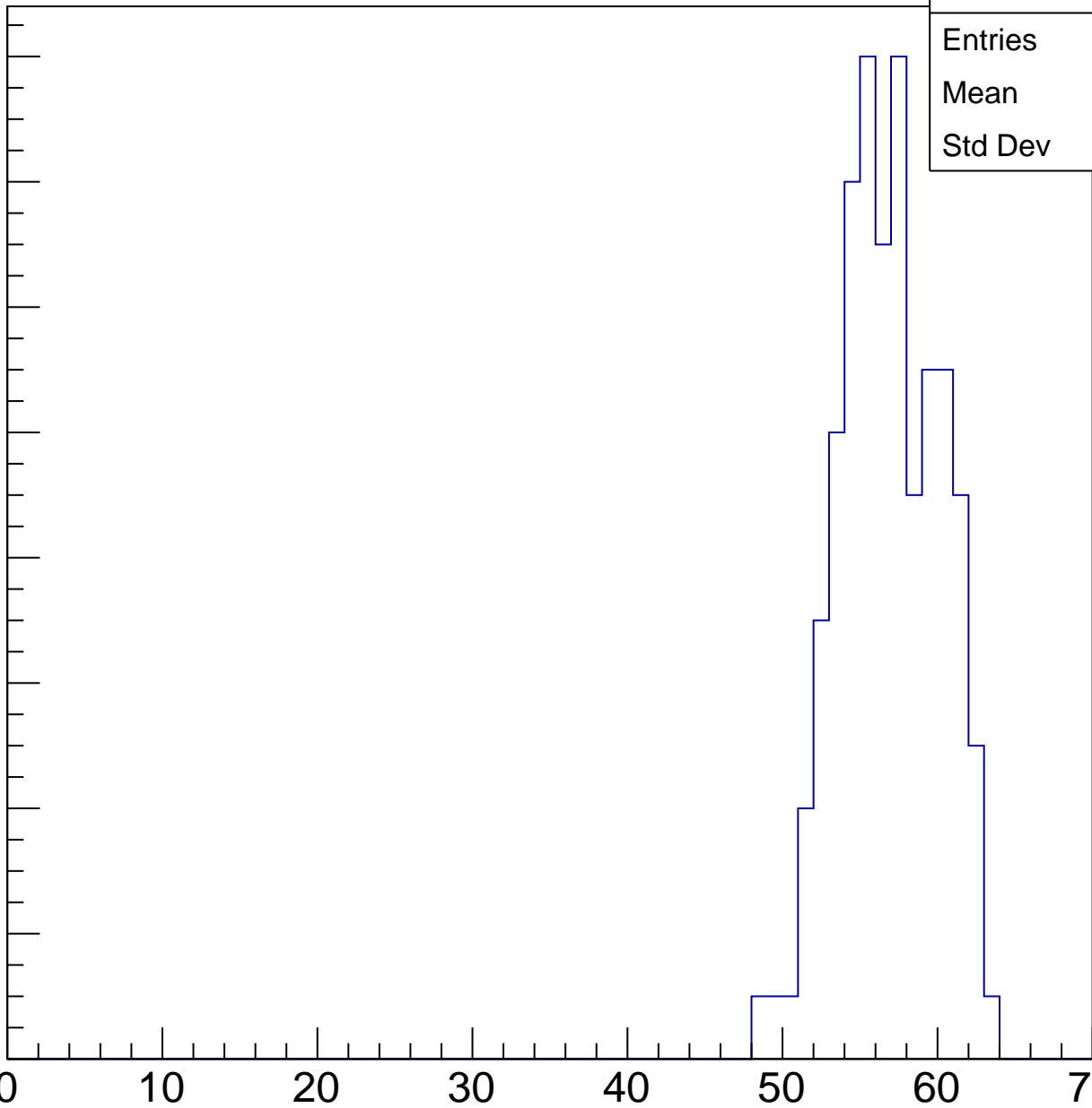
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	56.39
Std Dev	3.156

ampl



B0L101S, U21-ch10, adc5

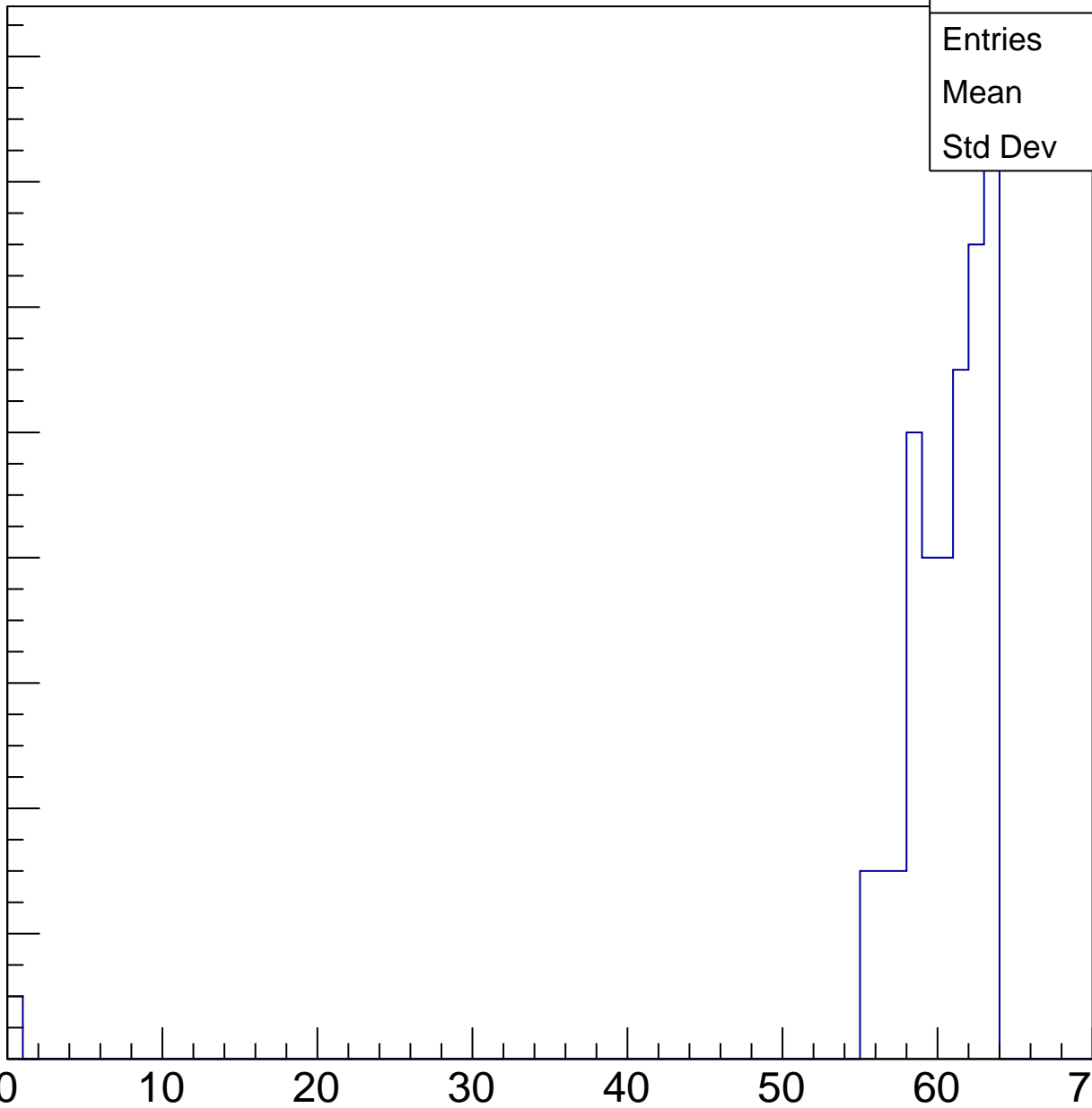
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.49
Std Dev	7.241

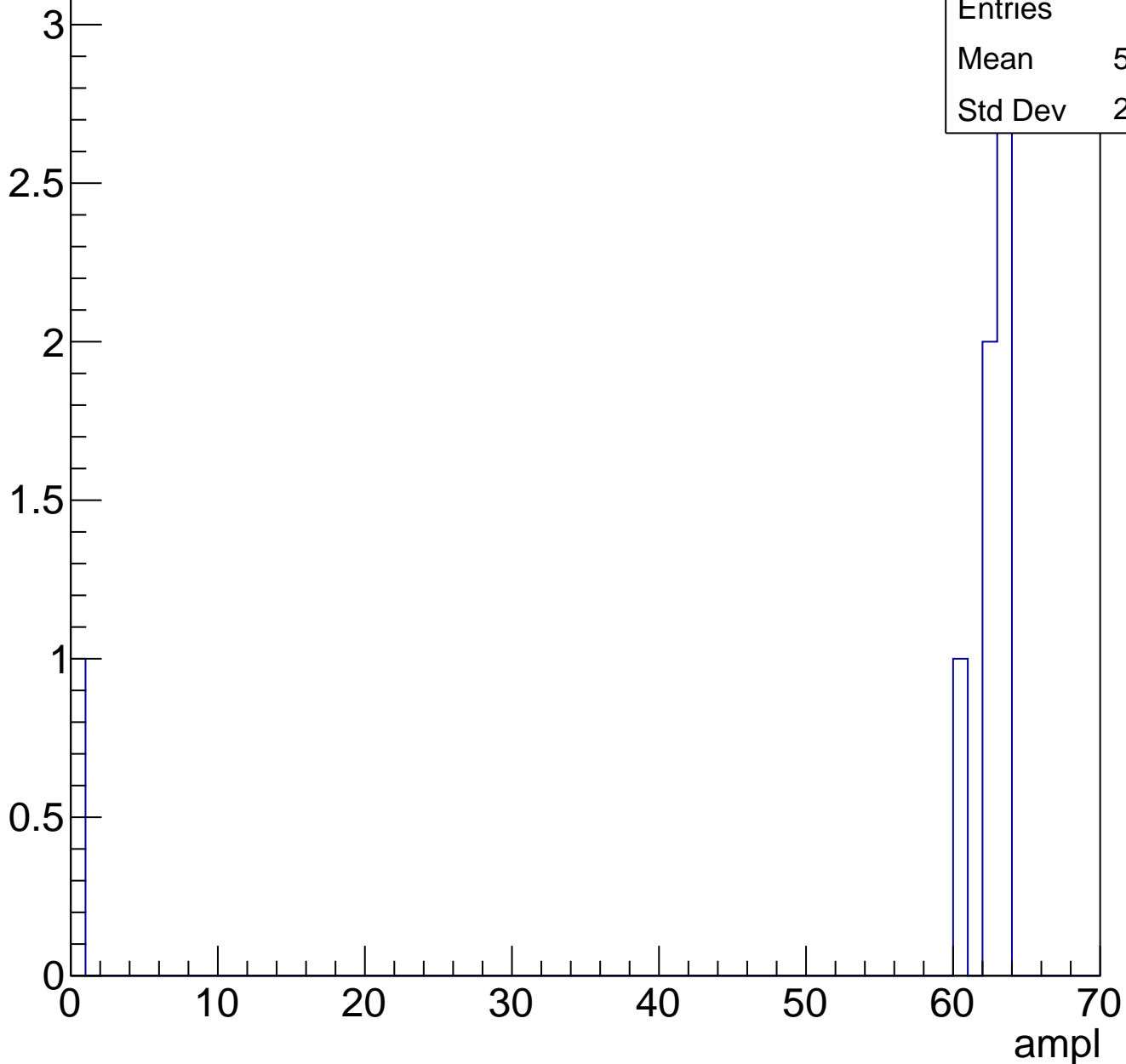
ampl



B0L101S, U21-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch11, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	30.87
Std Dev	4.323

Gaus mean : 31.9428

Gaus Width: 3.0020

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

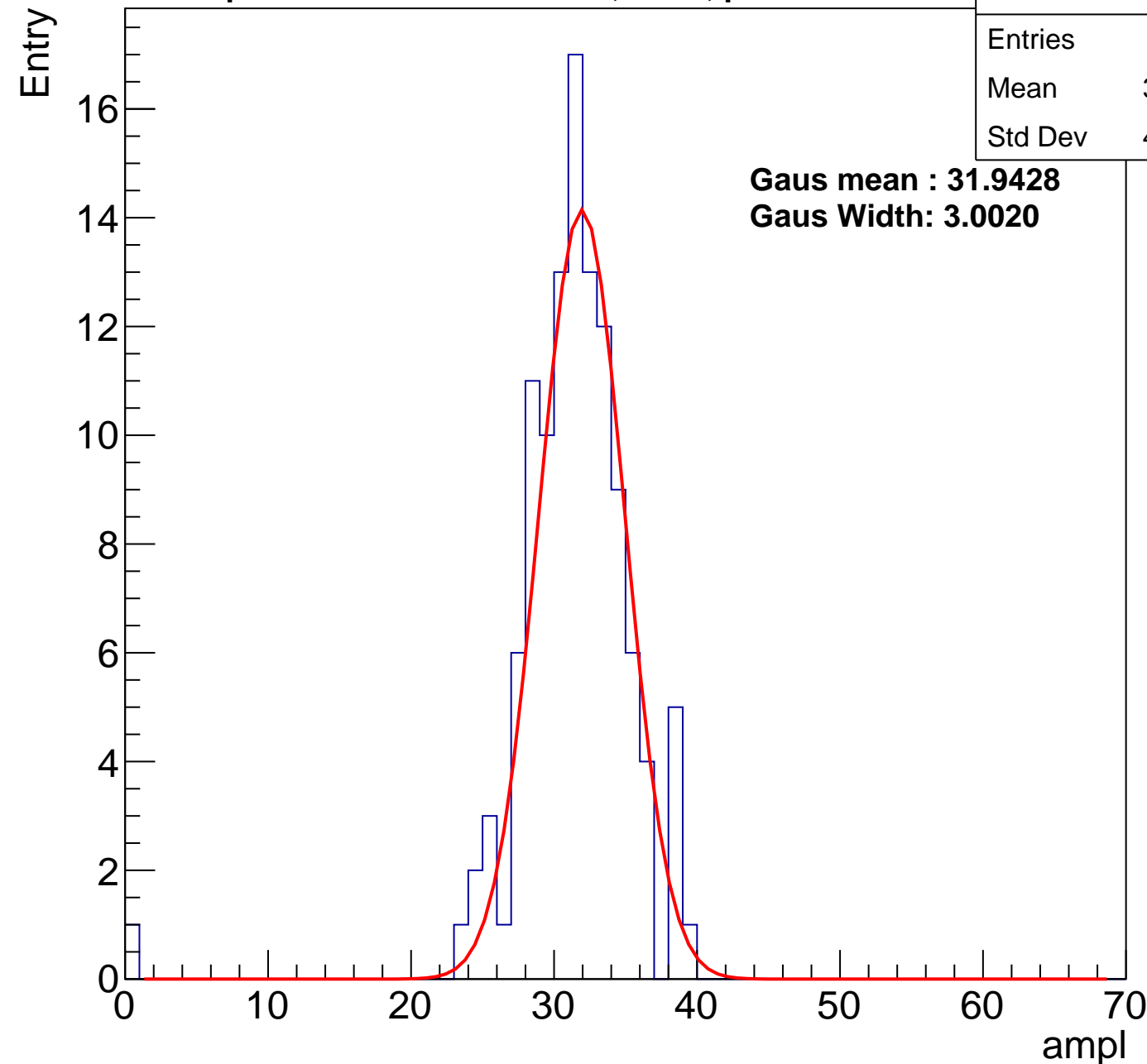
40

50

60

70

ampl



B0L101S, U21-ch11, adc1

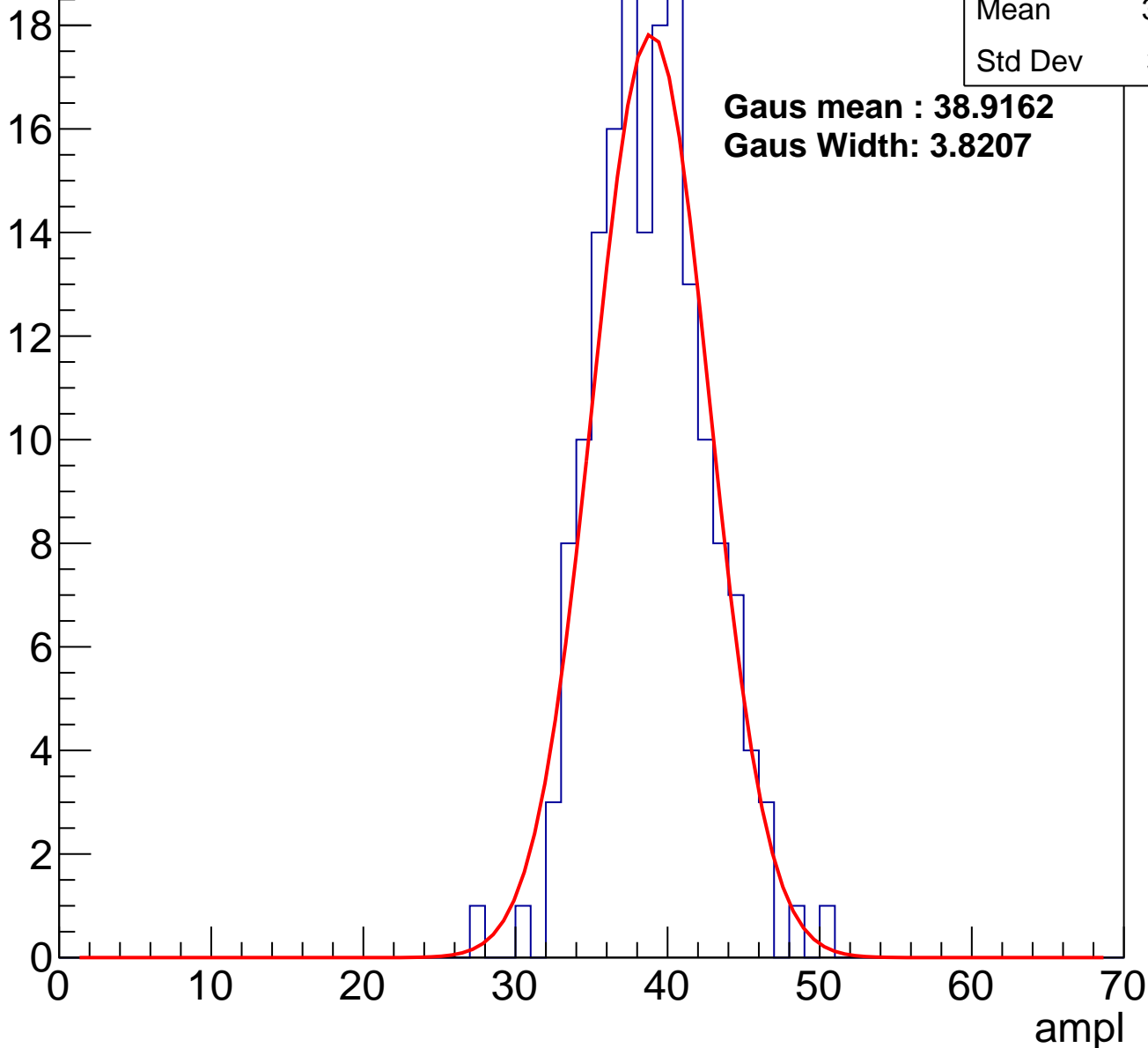
calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	38.48
Std Dev	3.661

Gaus mean : 38.9162

Gaus Width: 3.8207

Entry



B0L101S, U21-ch11, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

46.09

Std Dev

3.363

Gaus mean : 46.6044

Gaus Width: 3.1785

0

10

20

30

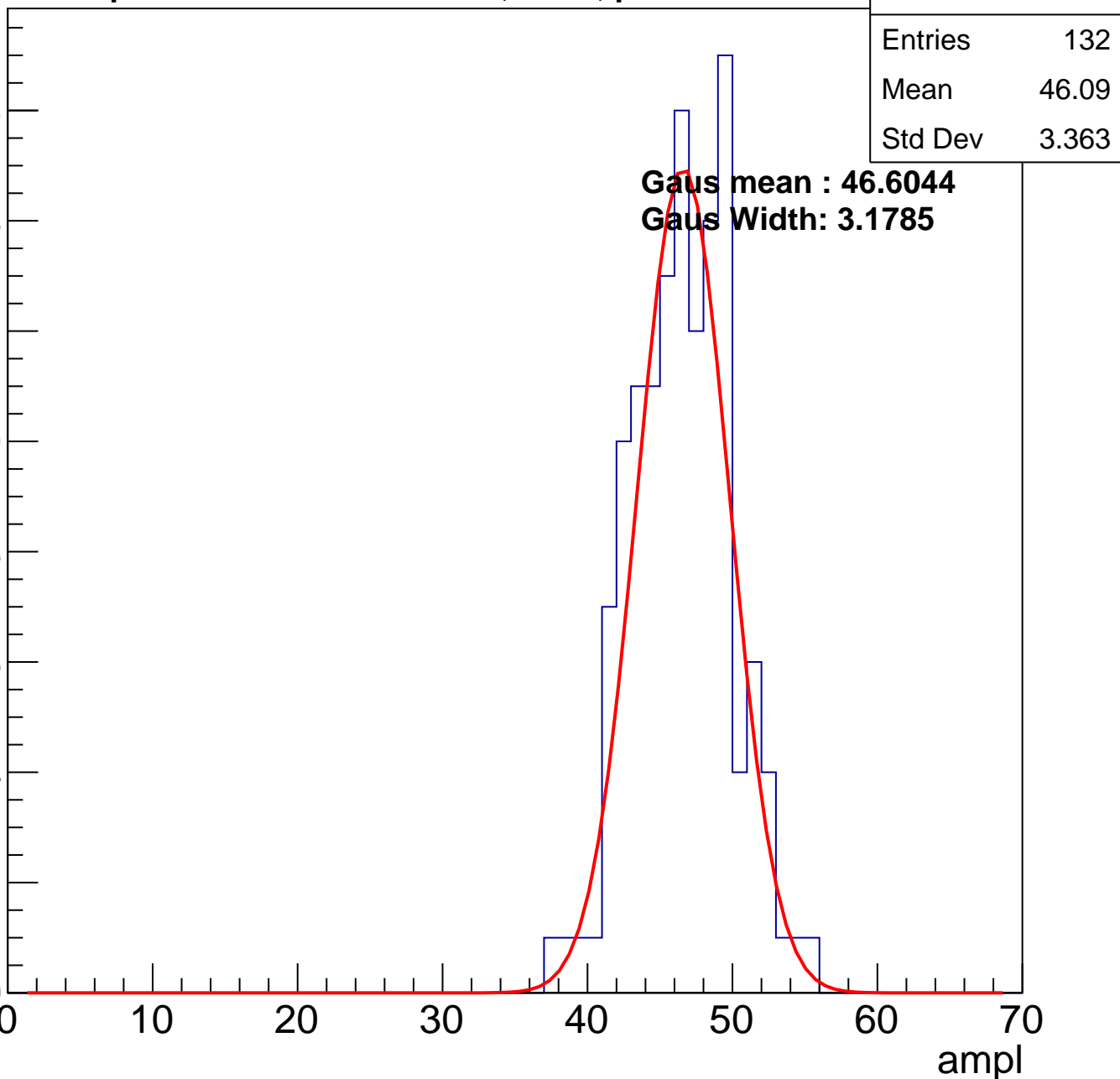
40

50

60

70

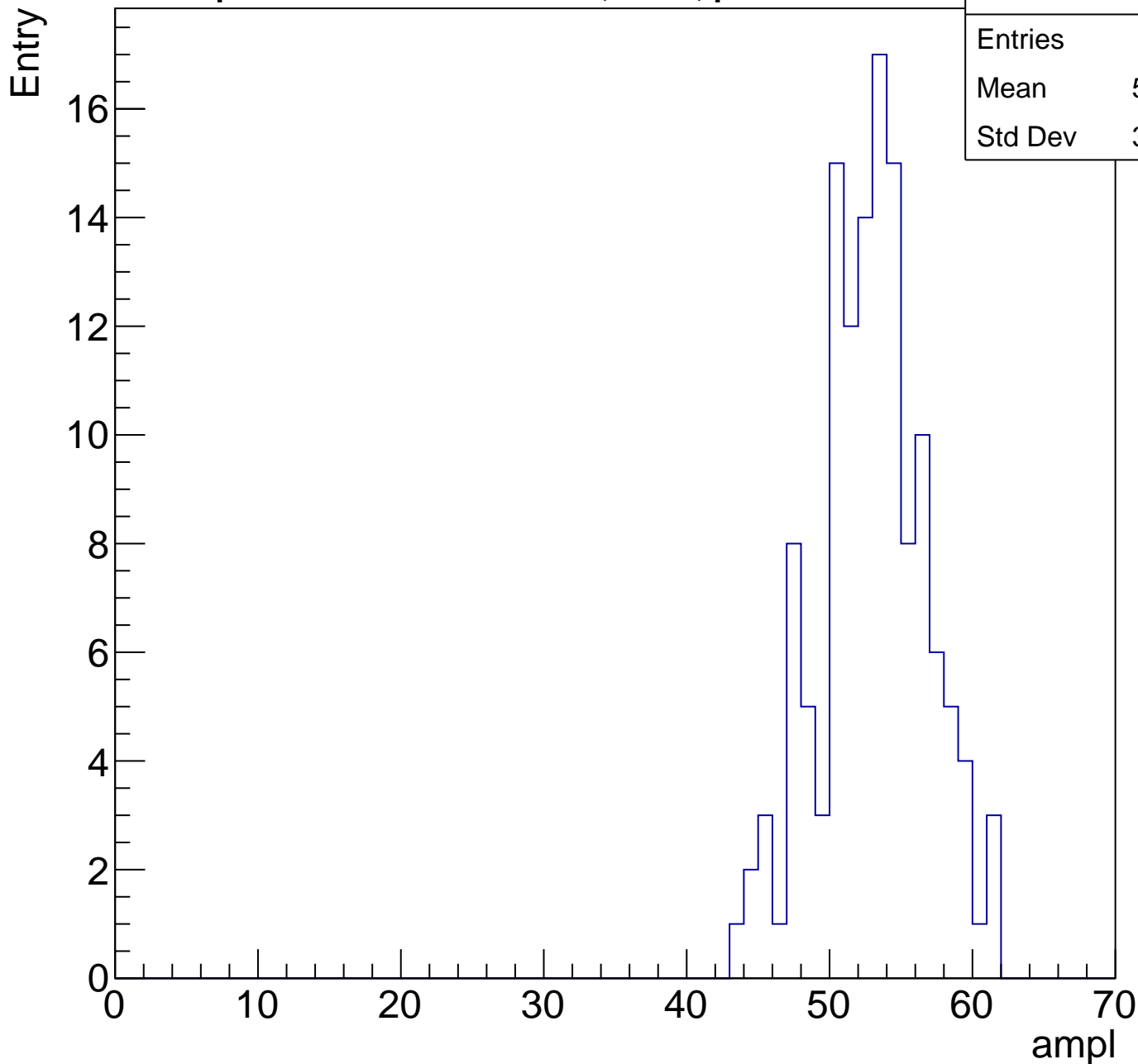
ampl



B0L101S, U21-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	52.53
Std Dev	3.755



B0L101S, U21-ch11, adc4

calib_packv5_042523_0143.root, FC#1, port C1

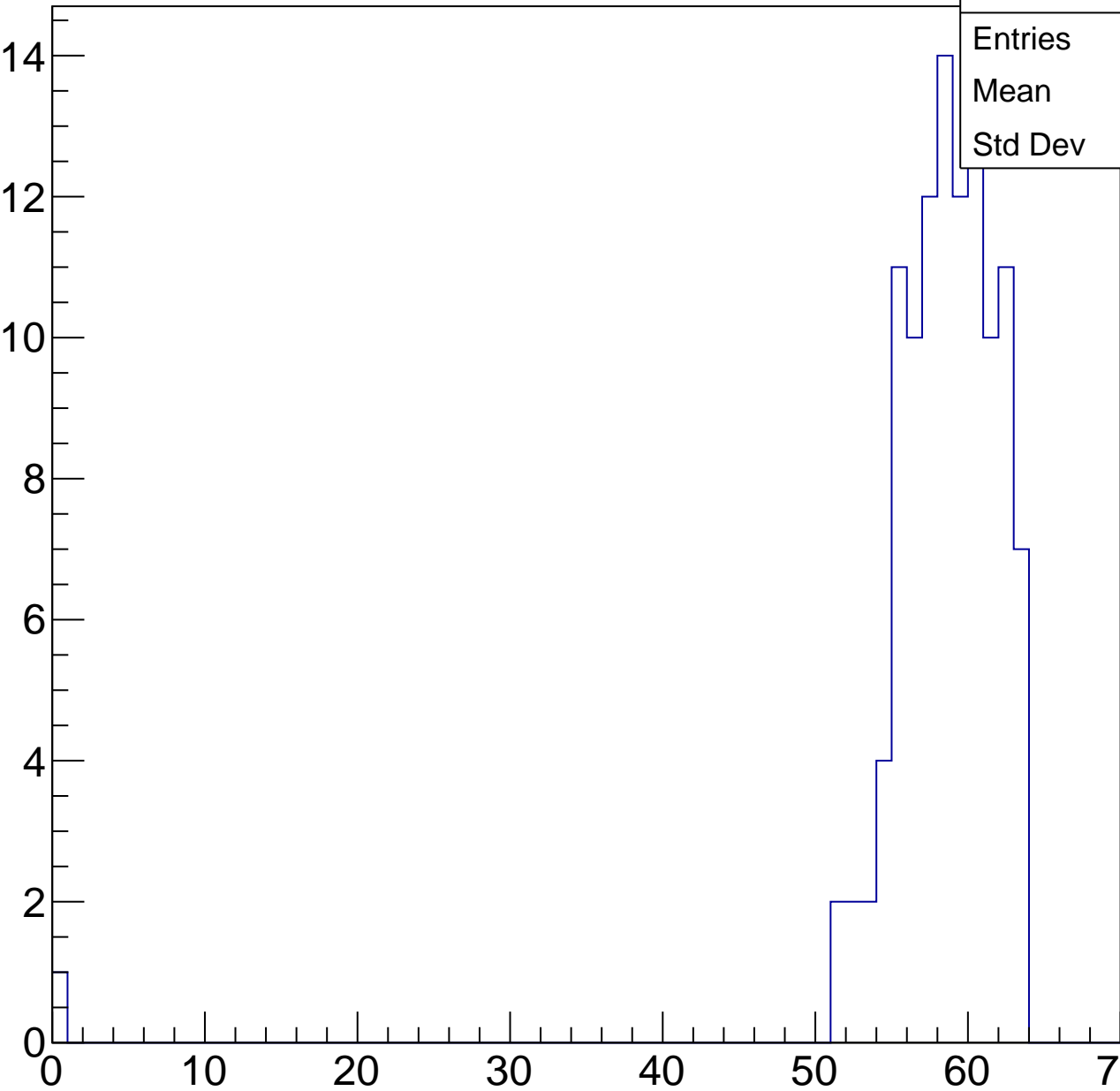
Entries	111
Mean	57.75
Std Dev	6.222

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch11, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	41
Mean	59.66
Std Dev	9.58

0

B0L101S, U21-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

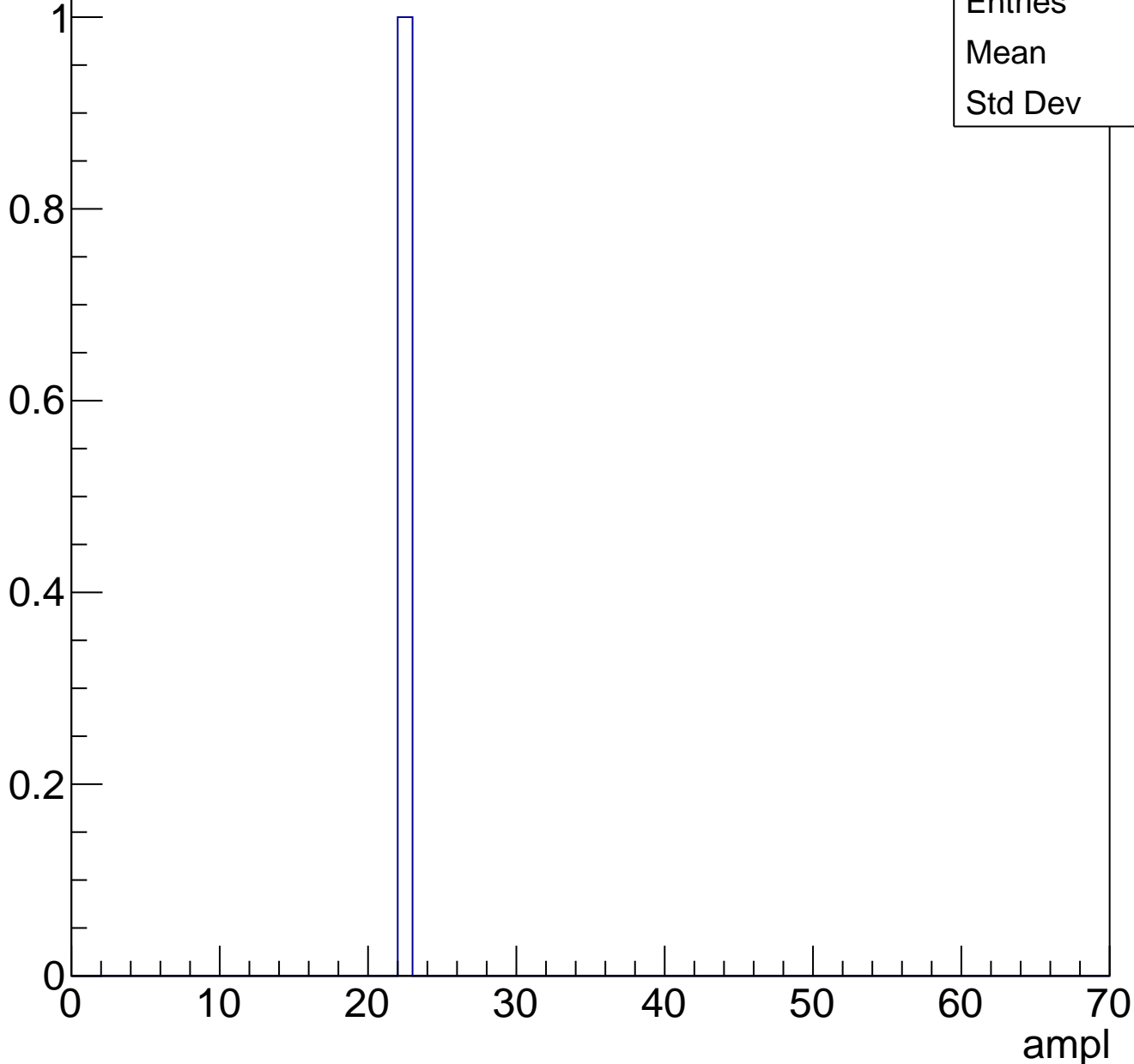
Entries	2
Mean	63
Std Dev	0

ampl

B0L101S, U21-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

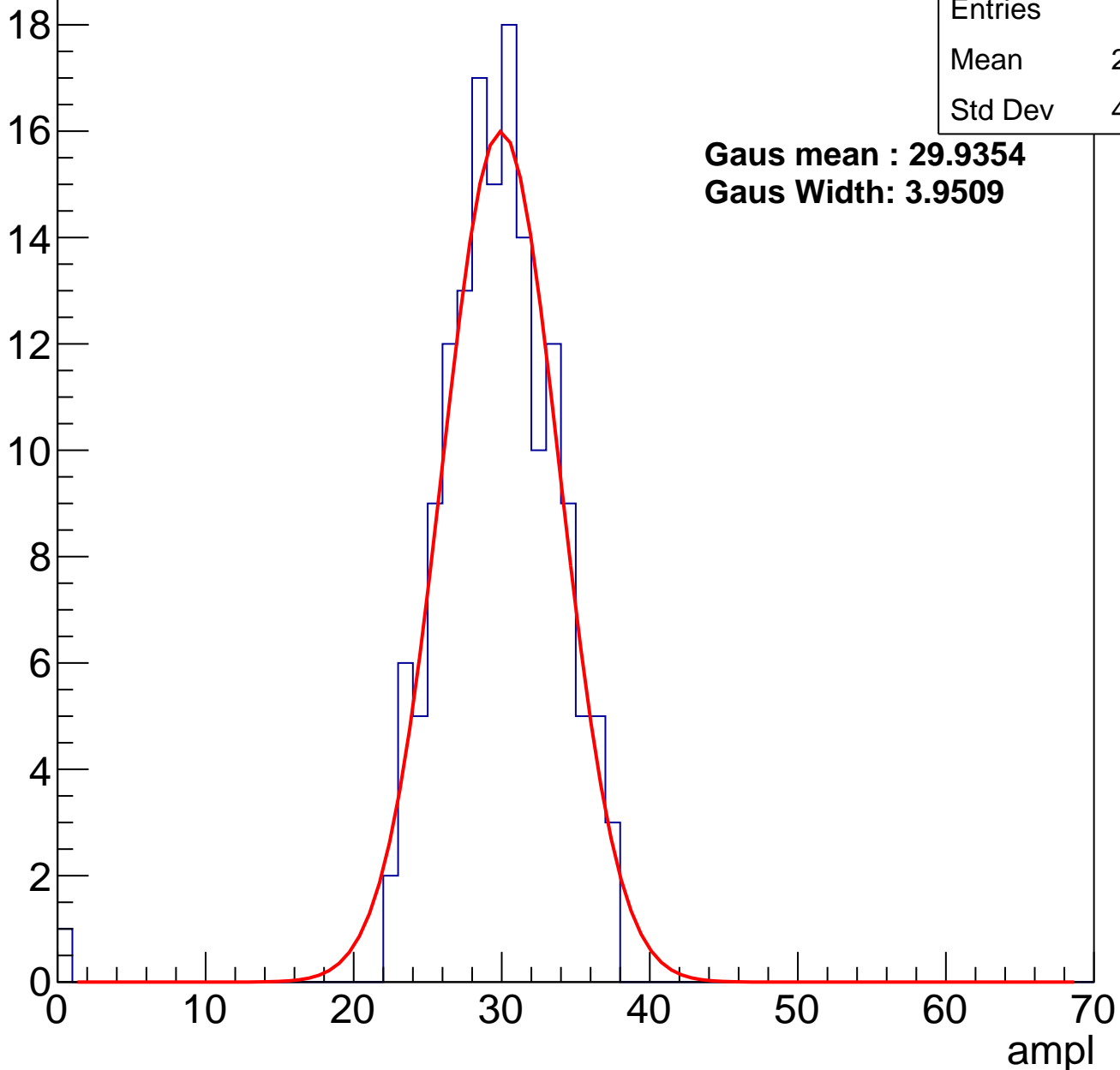
Entry



B0L101S, U21-ch12, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch12, adc1

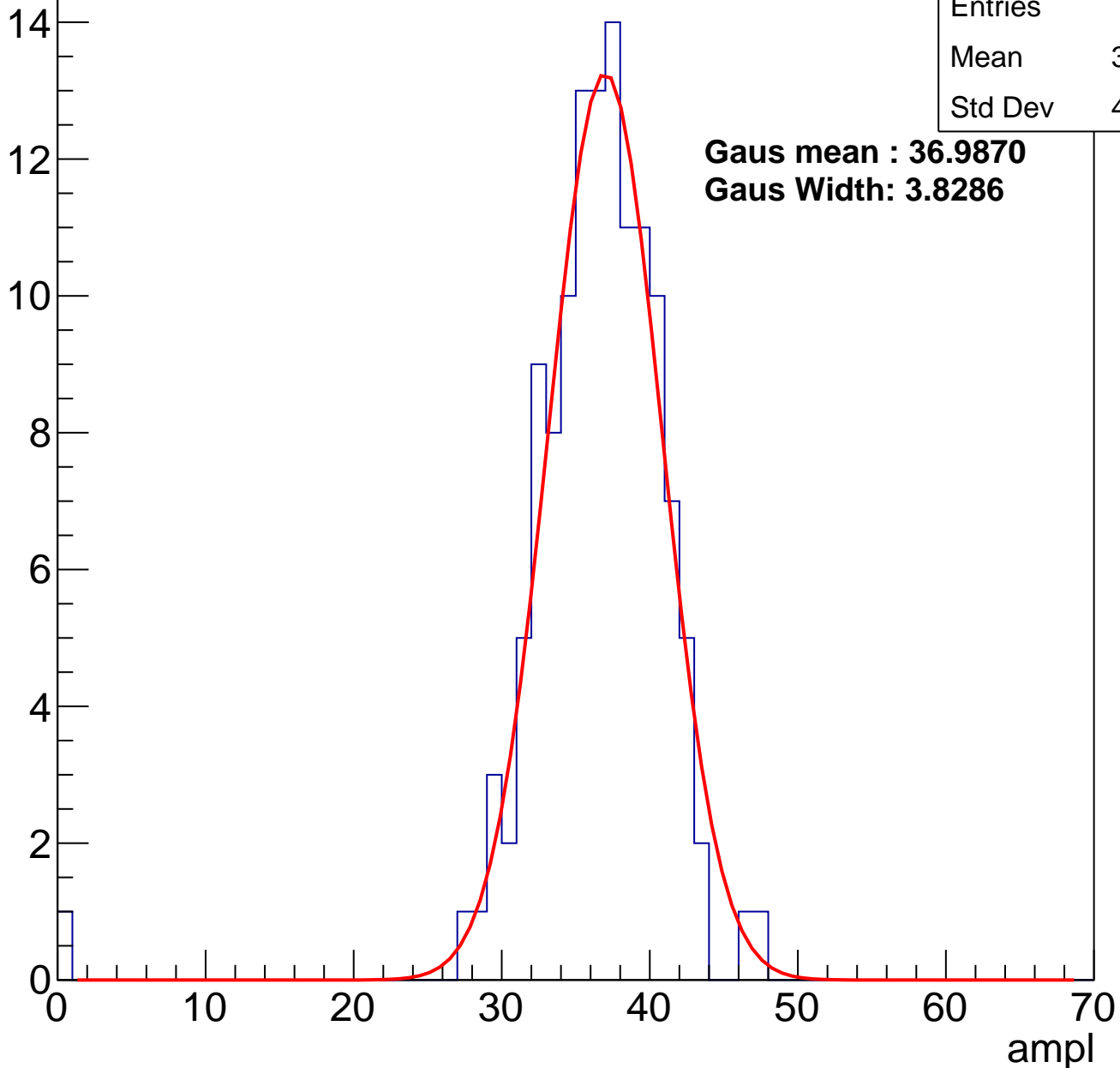
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	36.04
Std Dev	4.877

Gaus mean : 36.9870

Gaus Width: 3.8286

Entry



B0L101S, U21-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

158

Mean

43.63

Std Dev

3.977

Gaus mean : 44.0986

Gaus Width: 4.0918

0

10

20

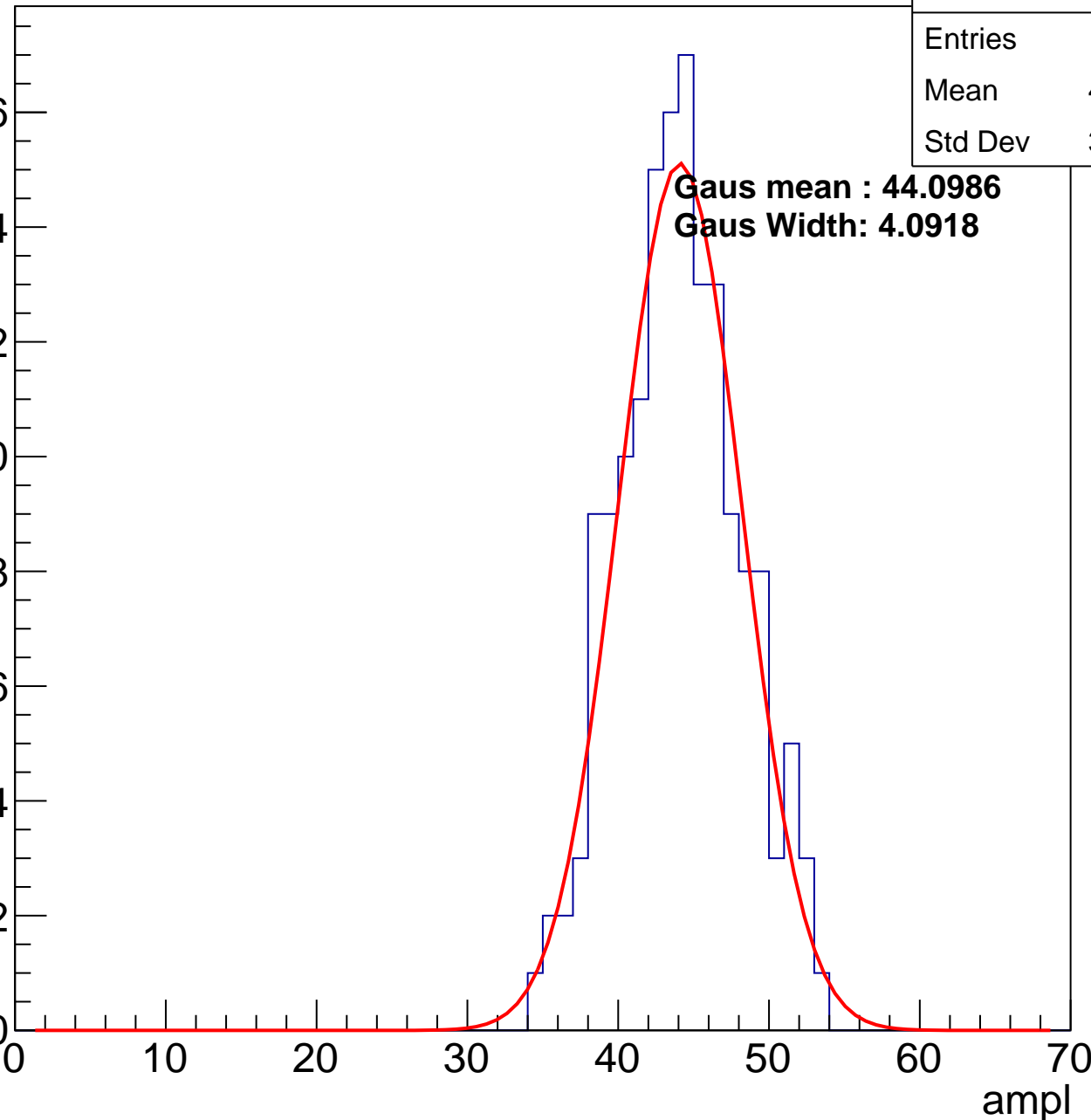
30

40

50

60

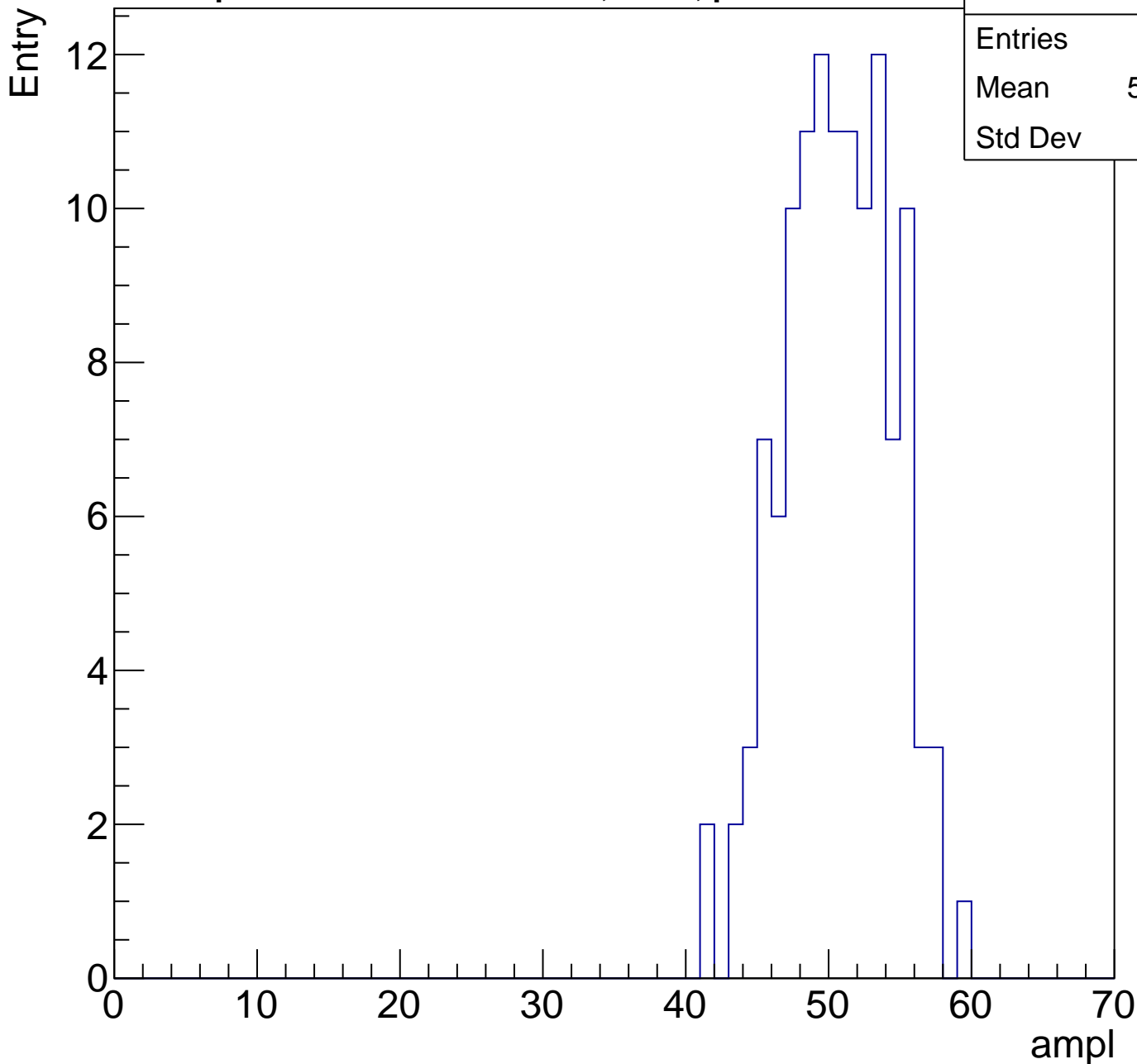
ampl



B0L101S, U21-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	50.17
Std Dev	3.68



B0L101S, U21-ch12, adc4

calib_packv5_042523_0143.root, FC#1, port C1

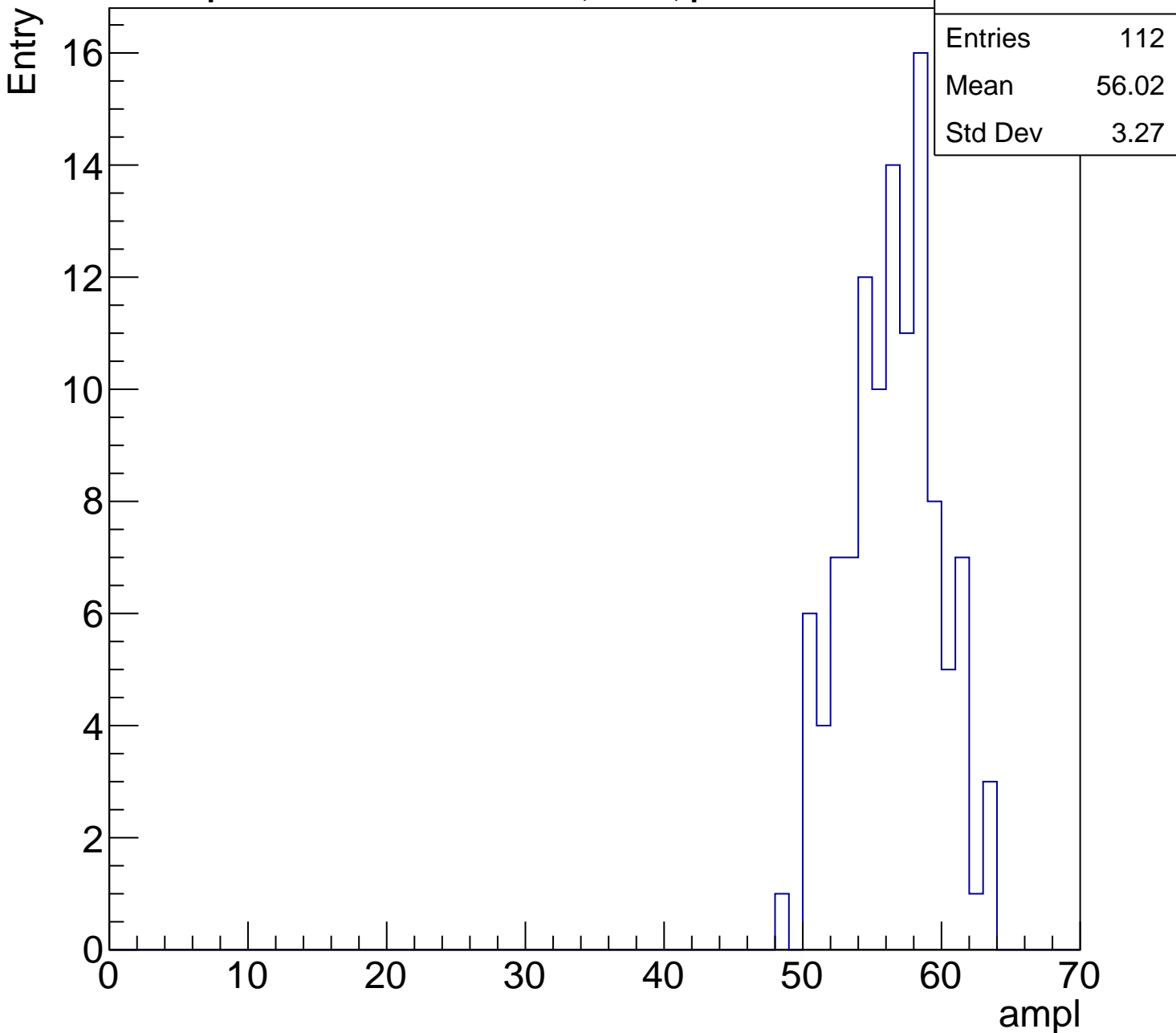
Entries	112
Mean	56.02
Std Dev	3.27

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch12, adc5

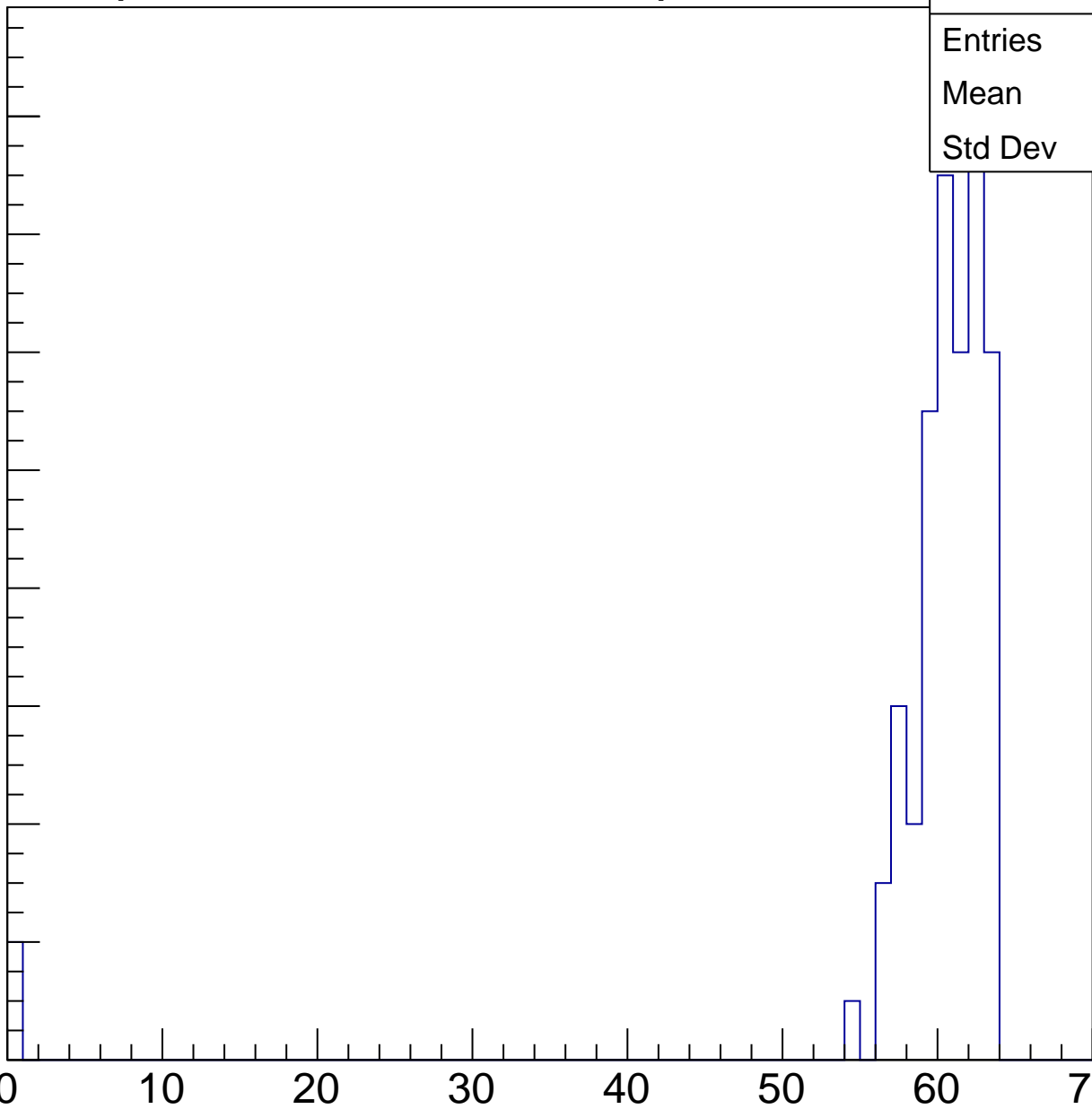
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.88
Std Dev	9.473

ampl

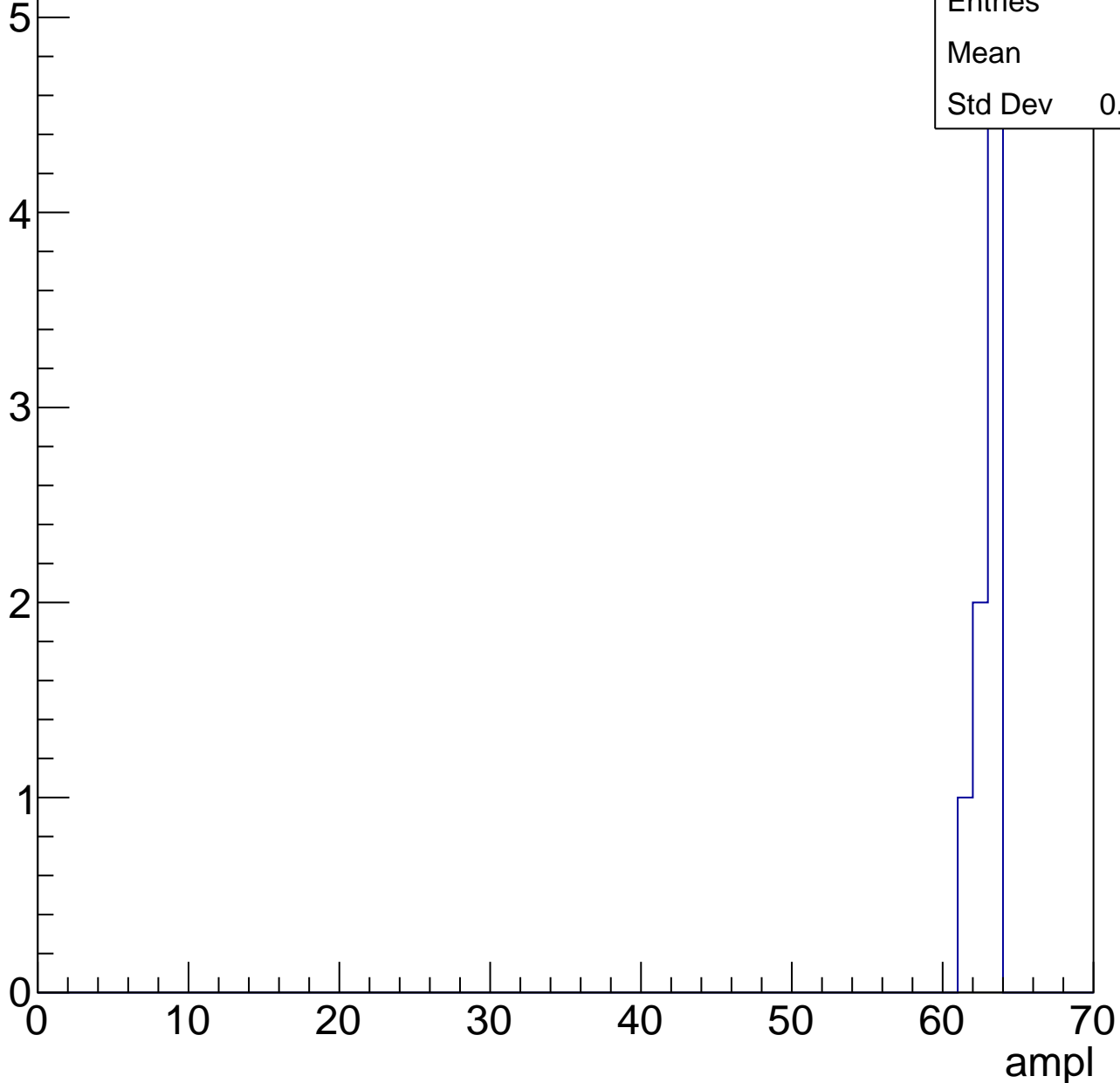


B0L101S, U21-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

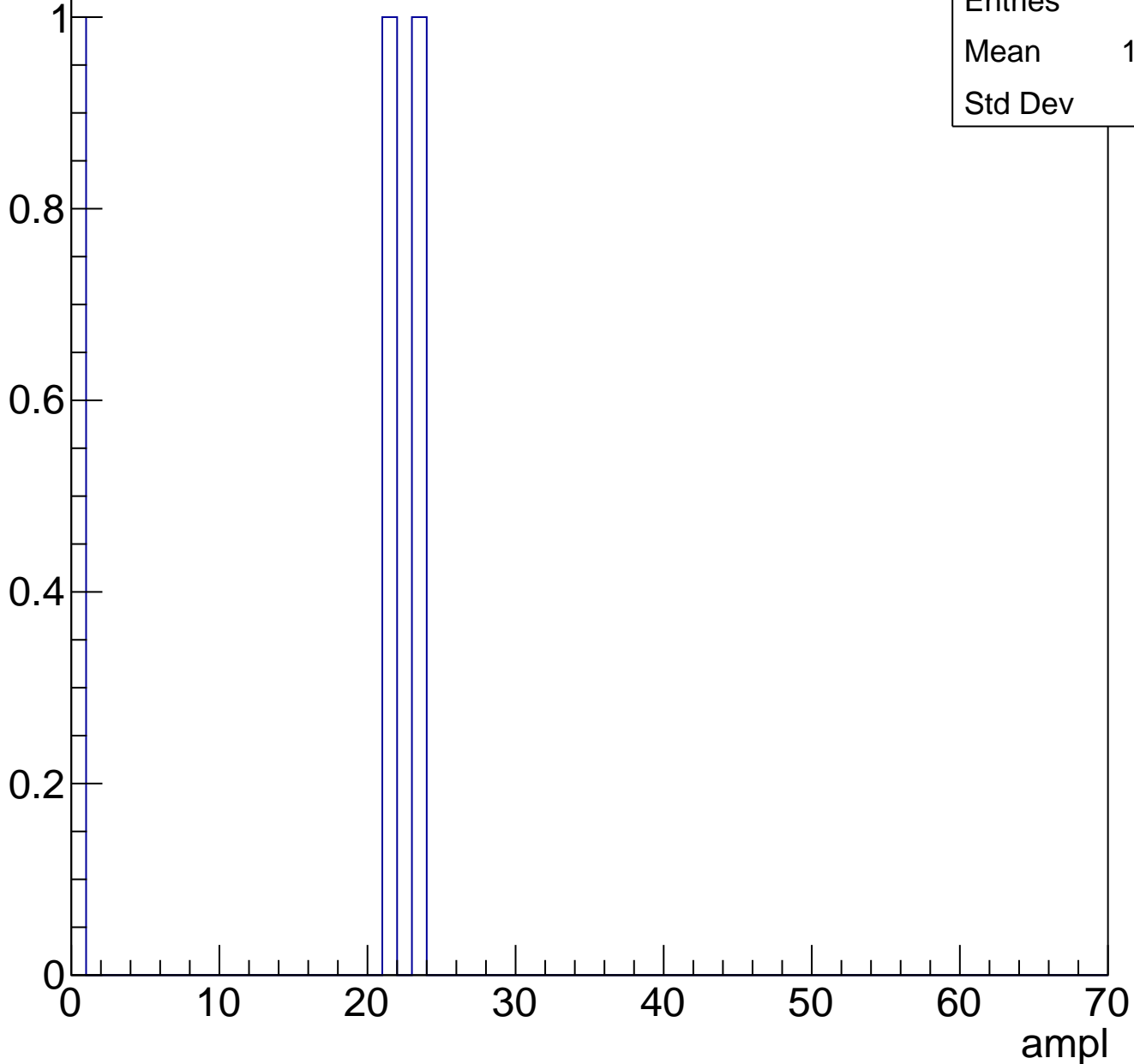
Entries	8
Mean	62.5
Std Dev	0.7071



B0L101S, U21-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



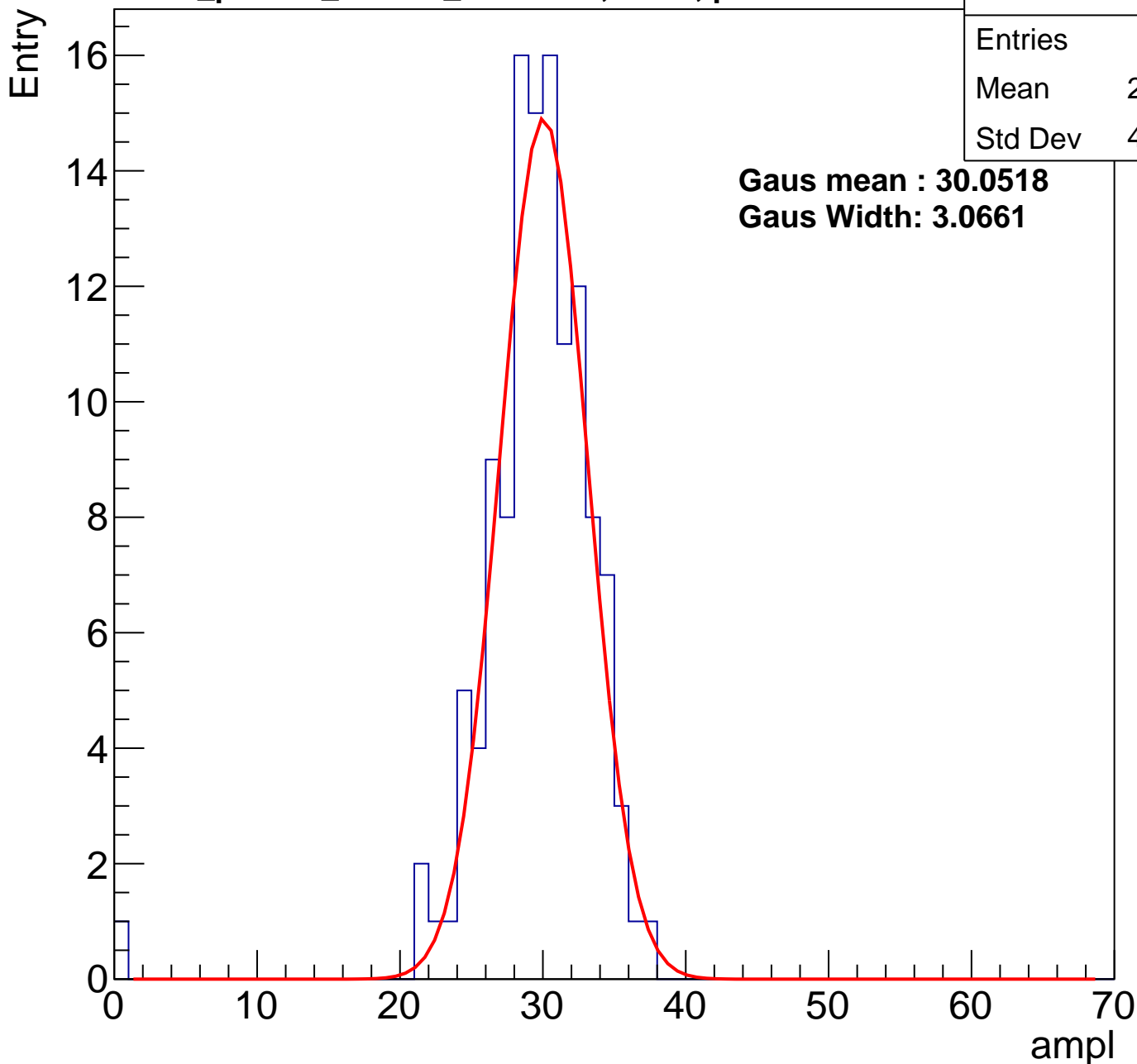
B0L101S, U21-ch13, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	29.13
Std Dev	4.128

Gaus mean : 30.0518

Gaus Width: 3.0661



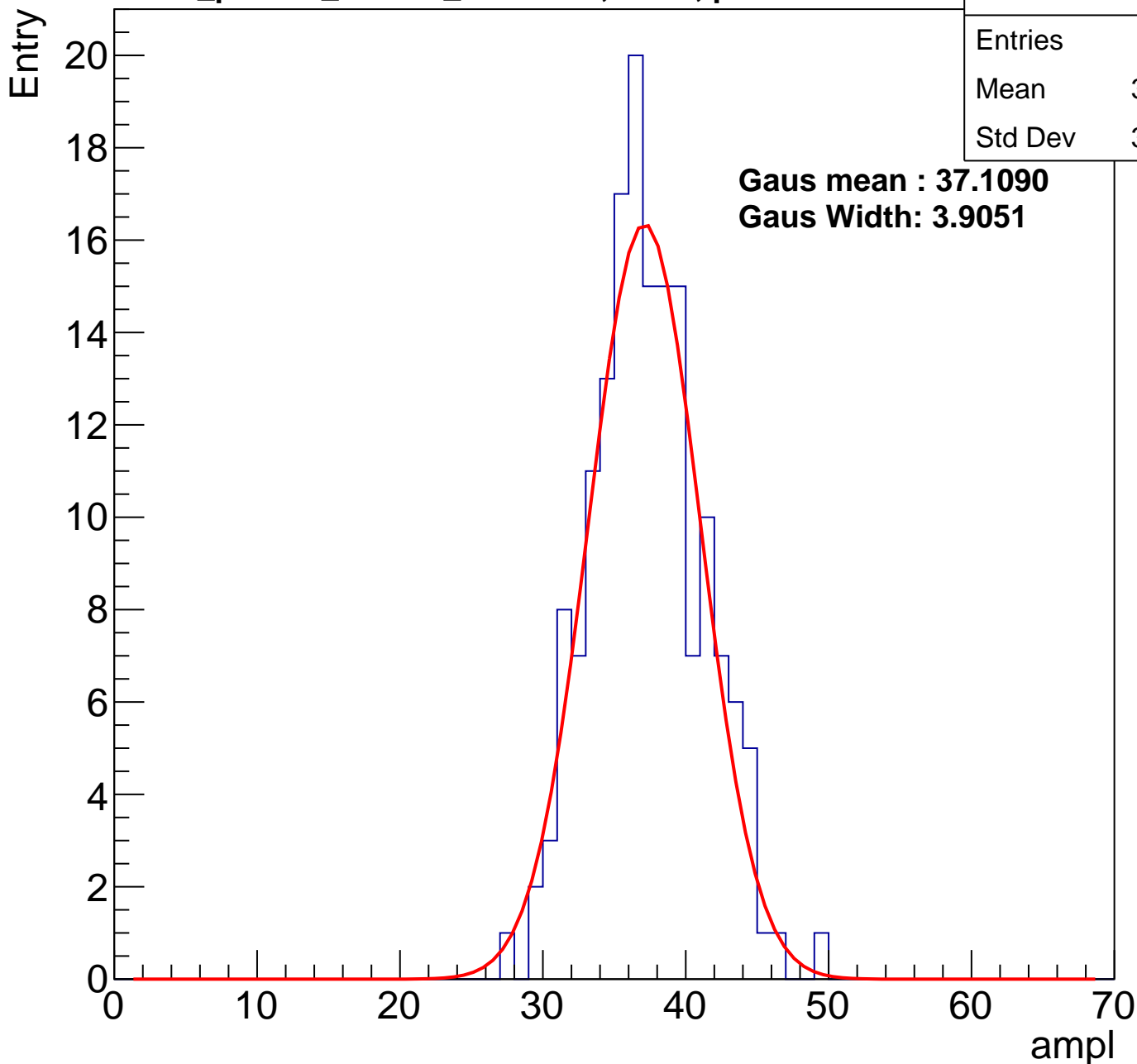
B0L101S, U21-ch13, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	36.84
Std Dev	3.844

Gaus mean : 37.1090

Gaus Width: 3.9051



B0L101S, U21-ch13, adc2

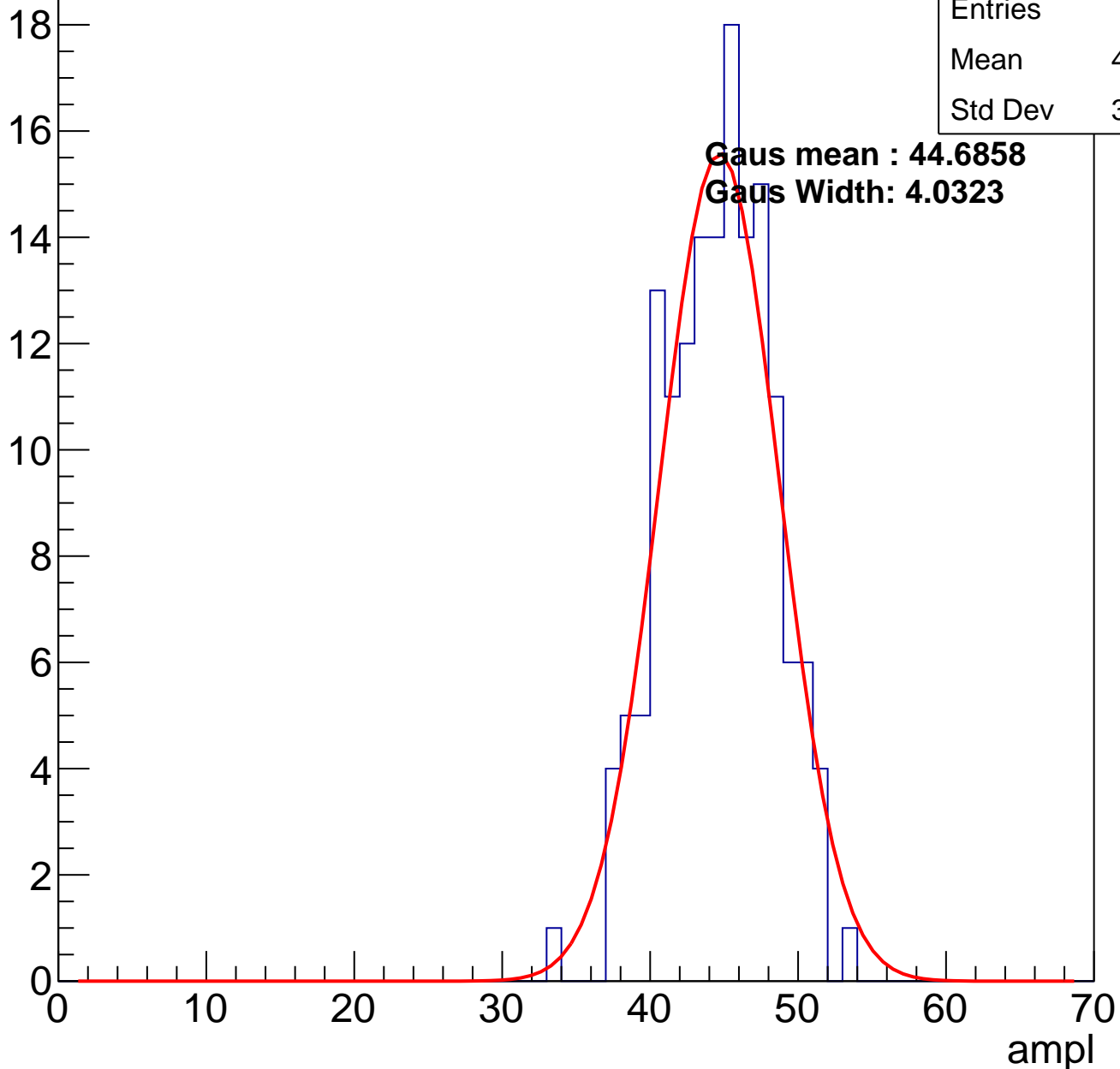
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	44.14
Std Dev	3.618

Gaus mean : 44.6858

Gaus Width: 4.0323

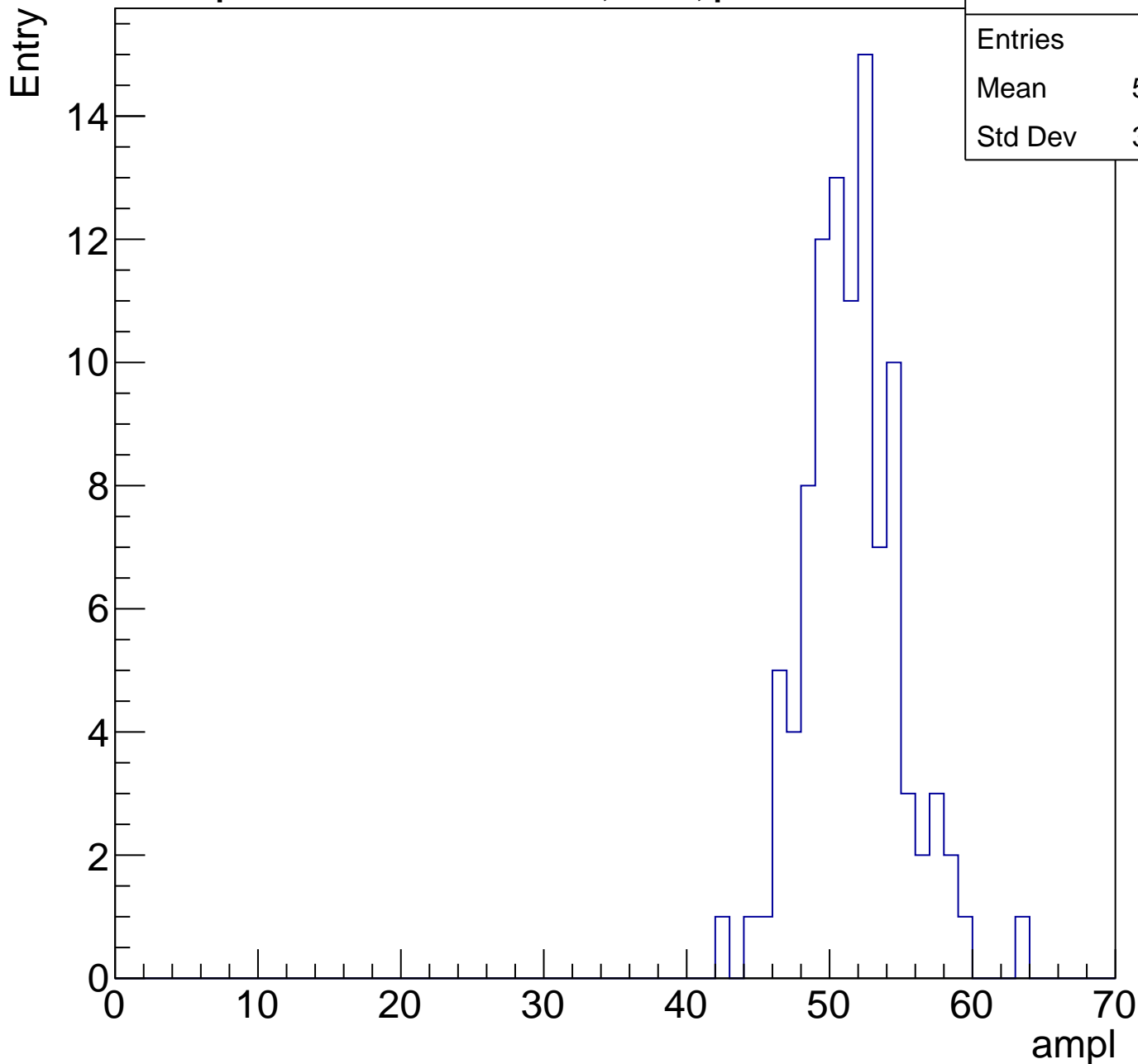
Entry



B0L101S, U21-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	51.09
Std Dev	3.365



B0L101S, U21-ch13, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

145

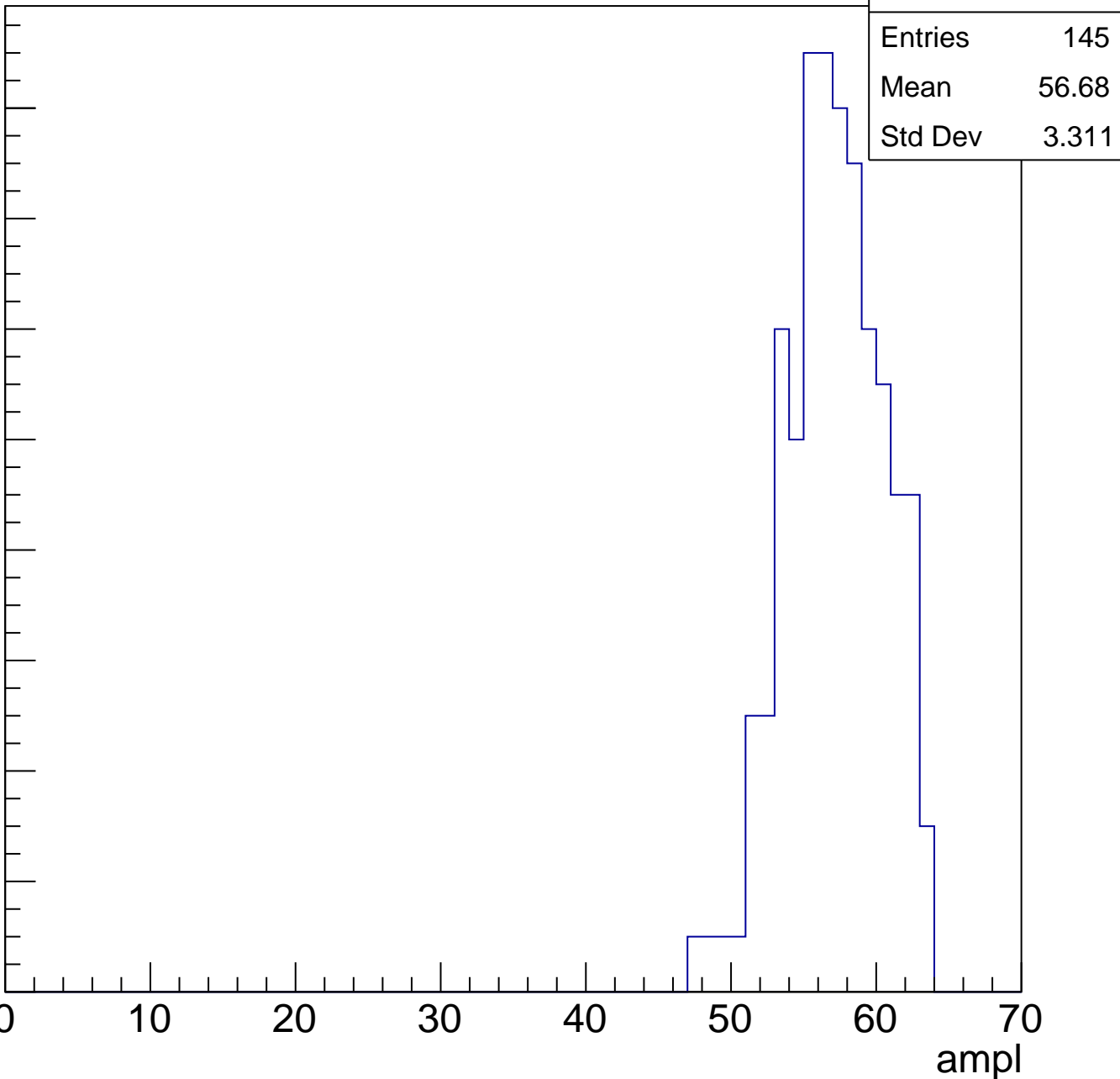
Mean

56.68

Std Dev

3.311

ampl



B0L101S, U21-ch13, adc5

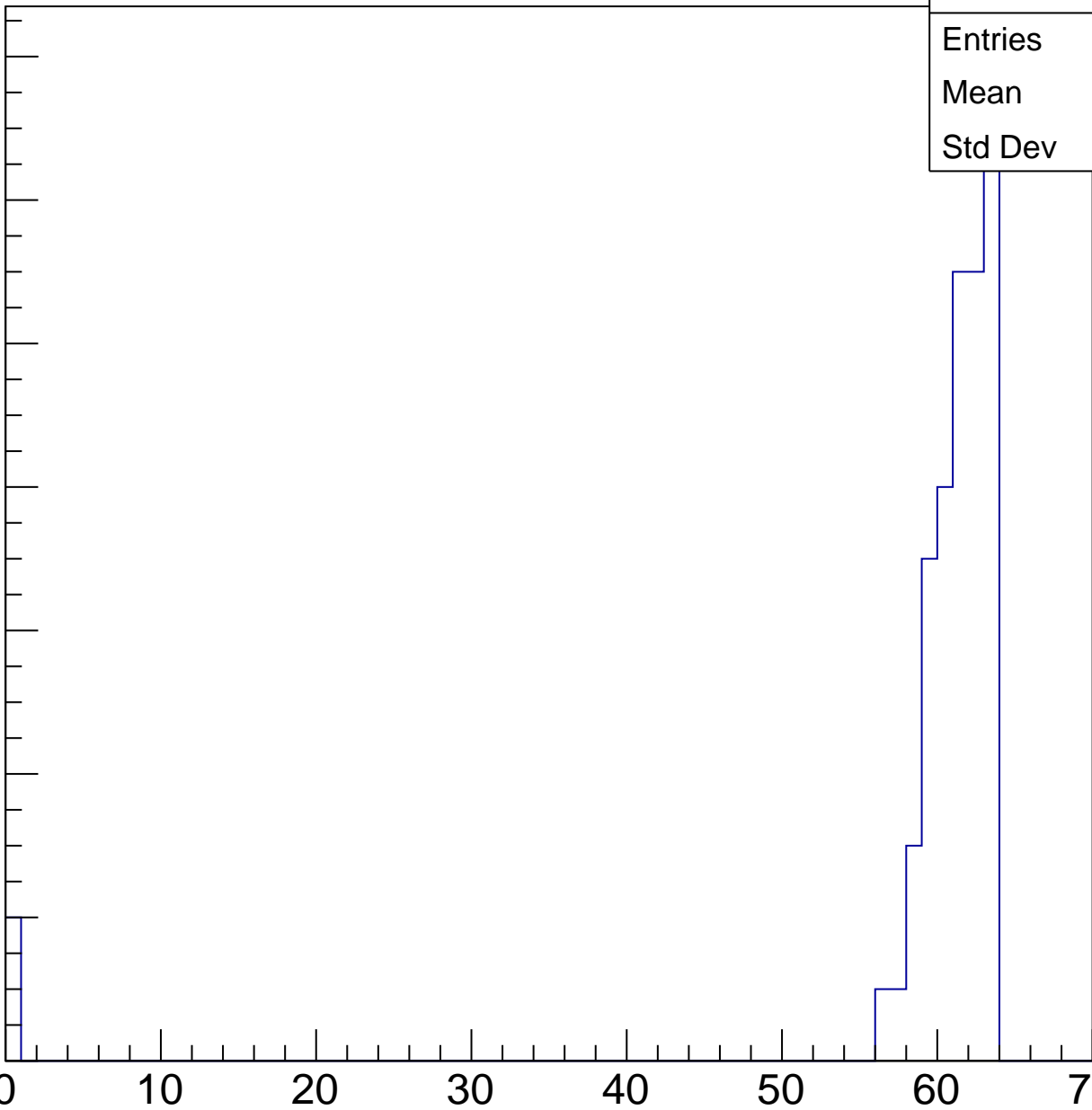
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	58.88
Std Dev	11.26

ampl



B0L101S, U21-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch14, adc0

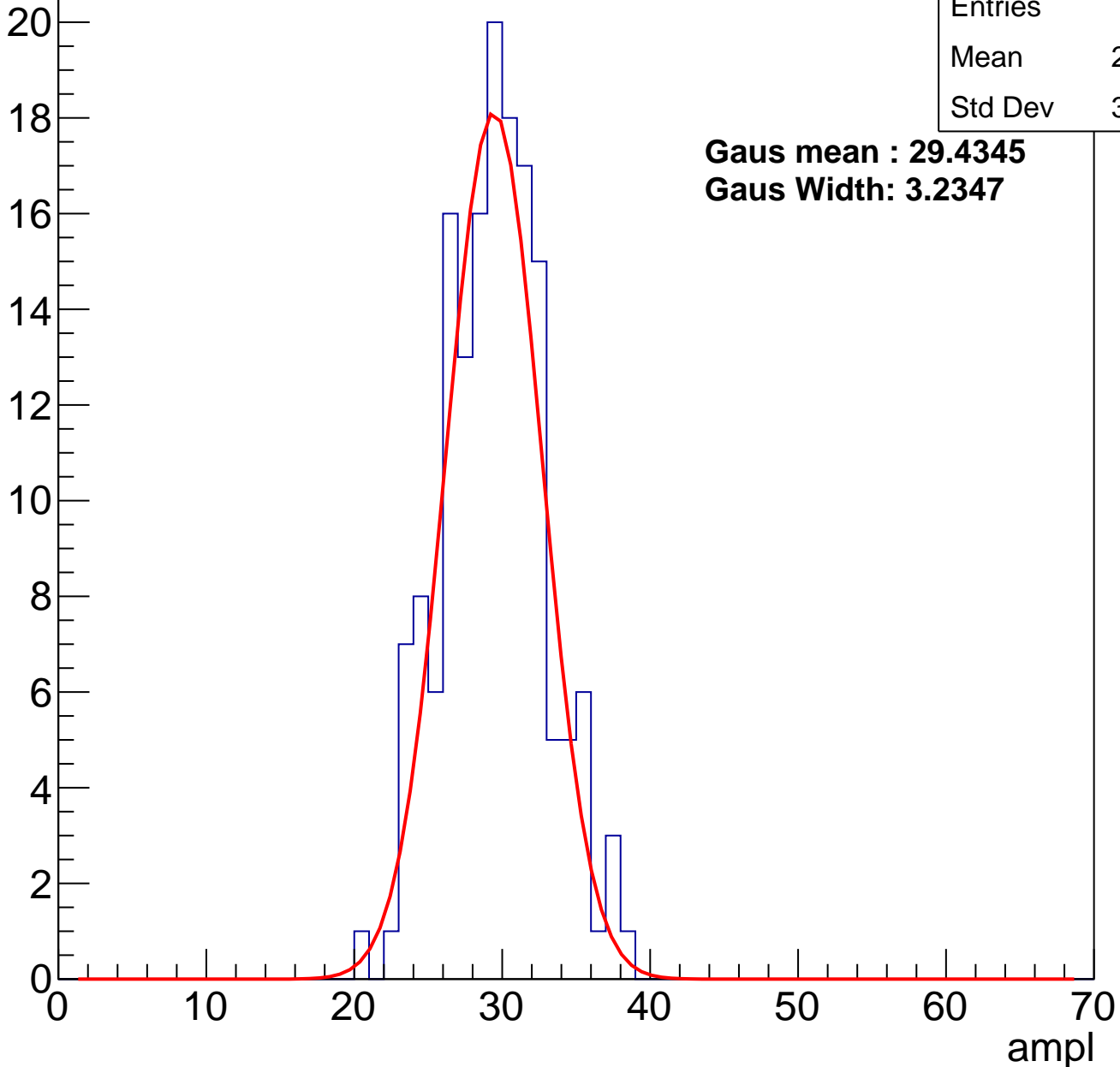
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	29.04
Std Dev	3.415

Gaus mean : 29.4345

Gaus Width: 3.2347

Entry



B0L101S, U21-ch14, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	36.9
Std Dev	3.248

Gaus mean : 37.1578

Gaus Width: 3.0353

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

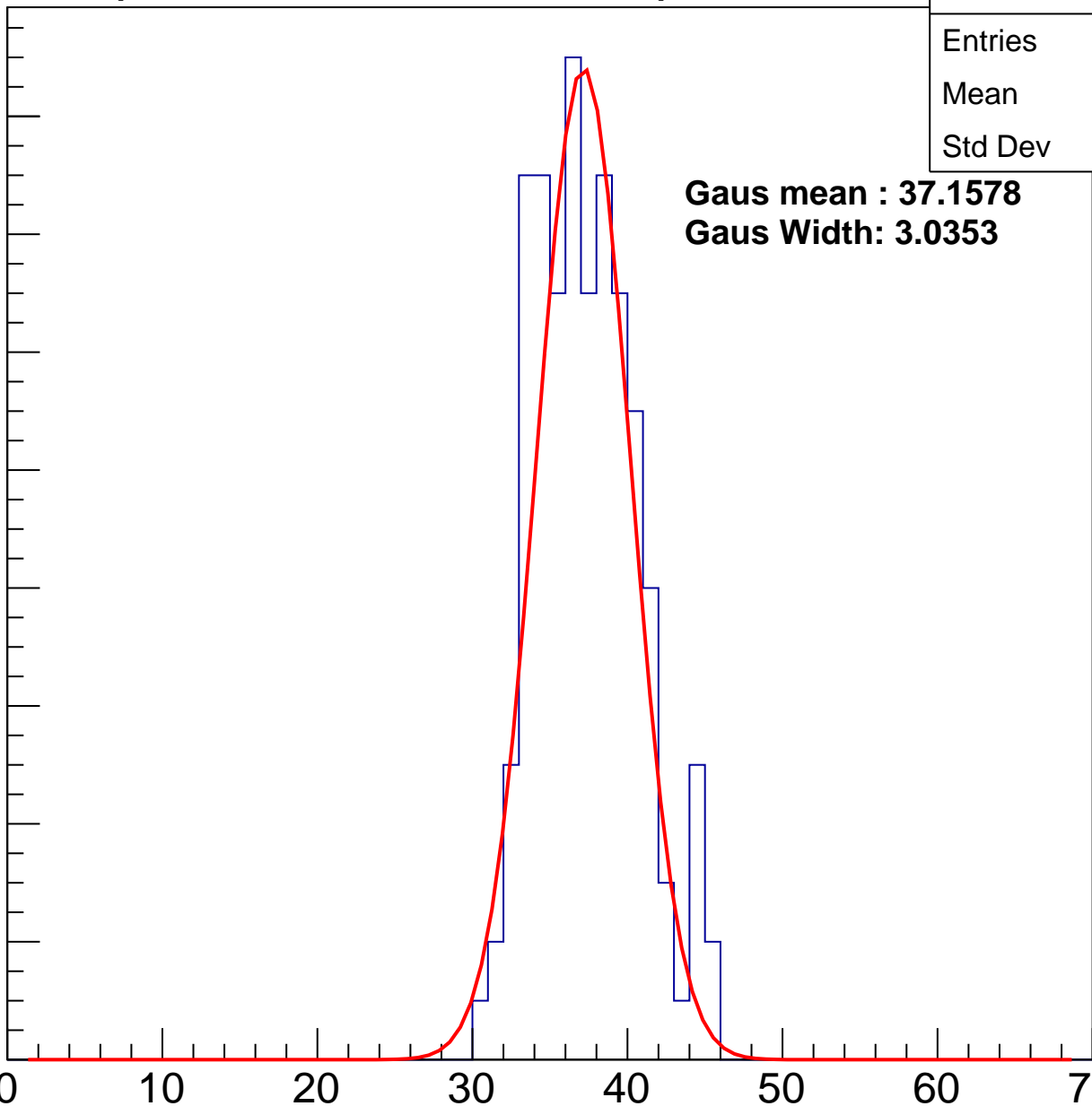
40

50

60

70

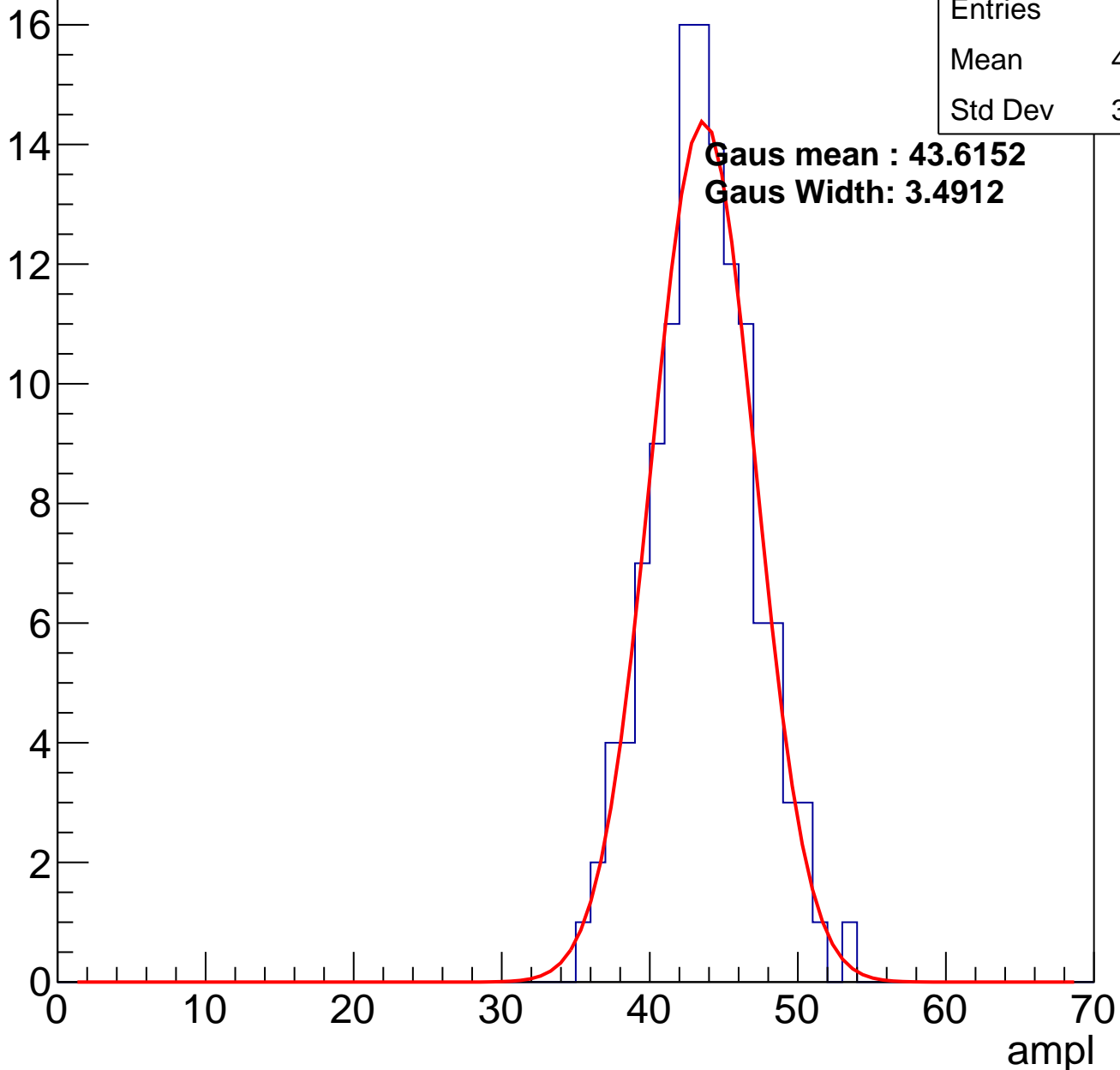
ampl



B0L101S, U21-ch14, adc2

calib_packv5_042523_0143.root, FC#1, port C1

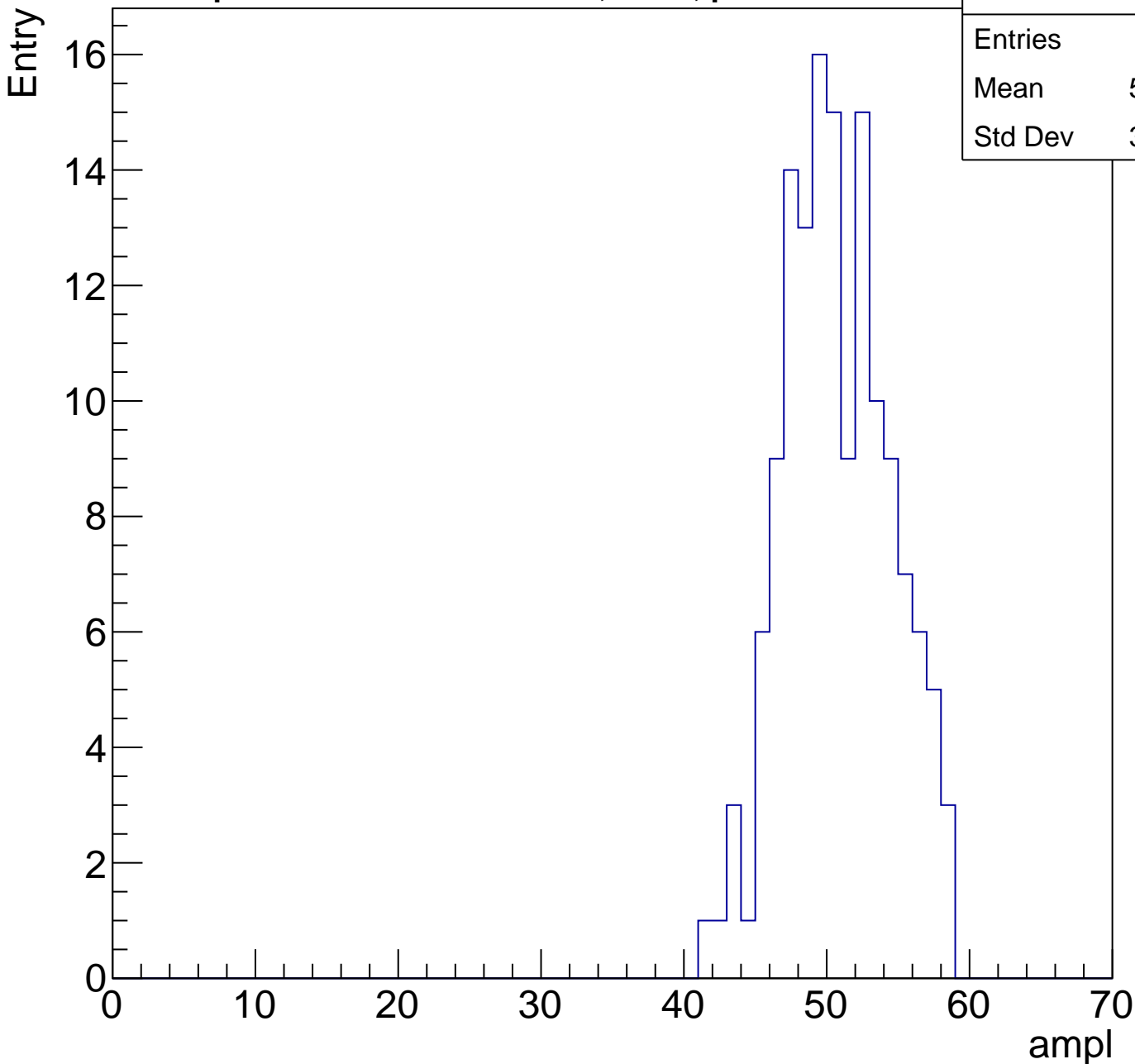
Entry



B0L101S, U21-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	50.29
Std Dev	3.679



B0L101S, U21-ch14, adc4

calib_packv5_042523_0143.root, FC#1, port C1

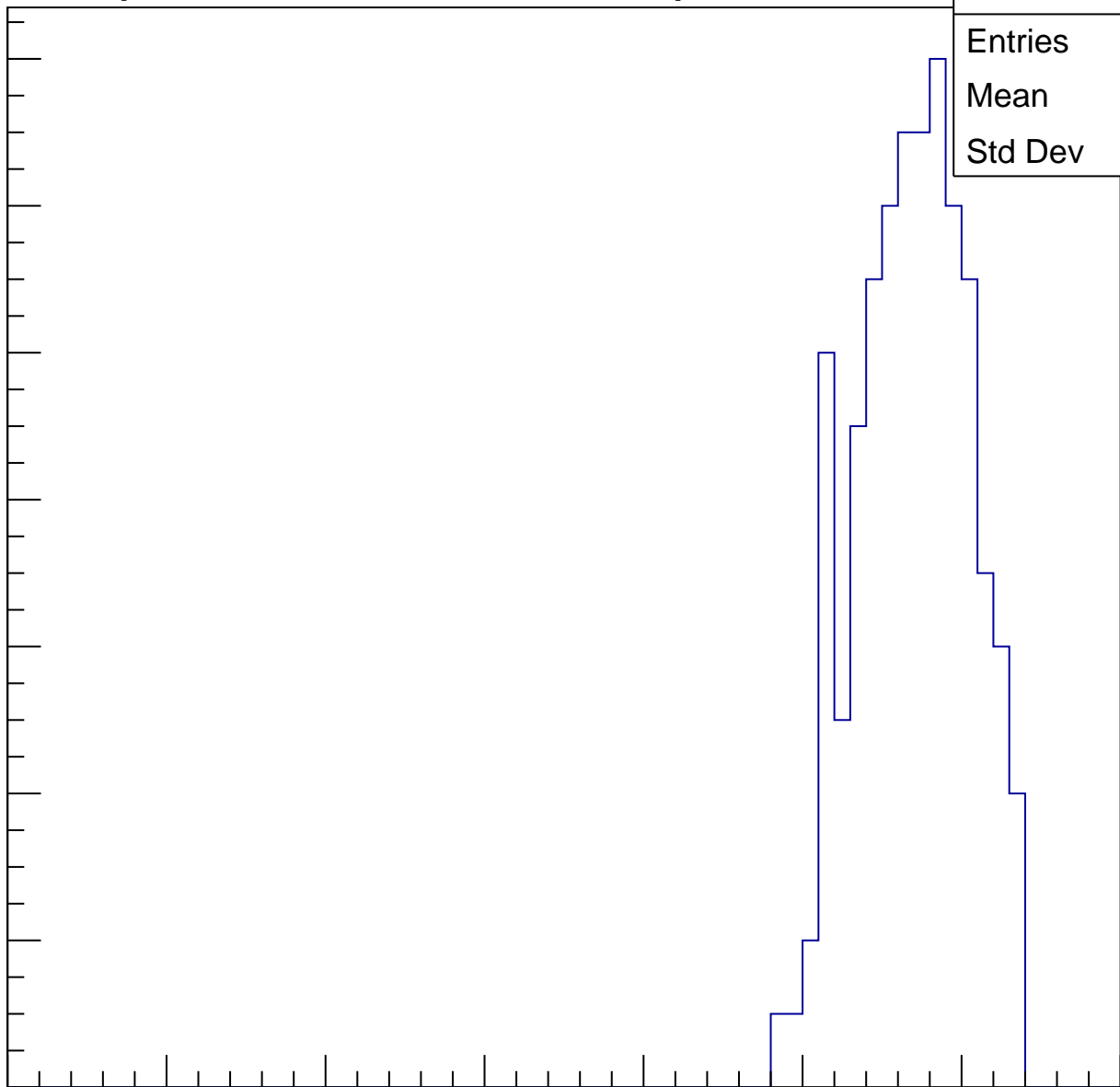
Entries	131
Mean	56.47
Std Dev	3.454

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch14, adc5

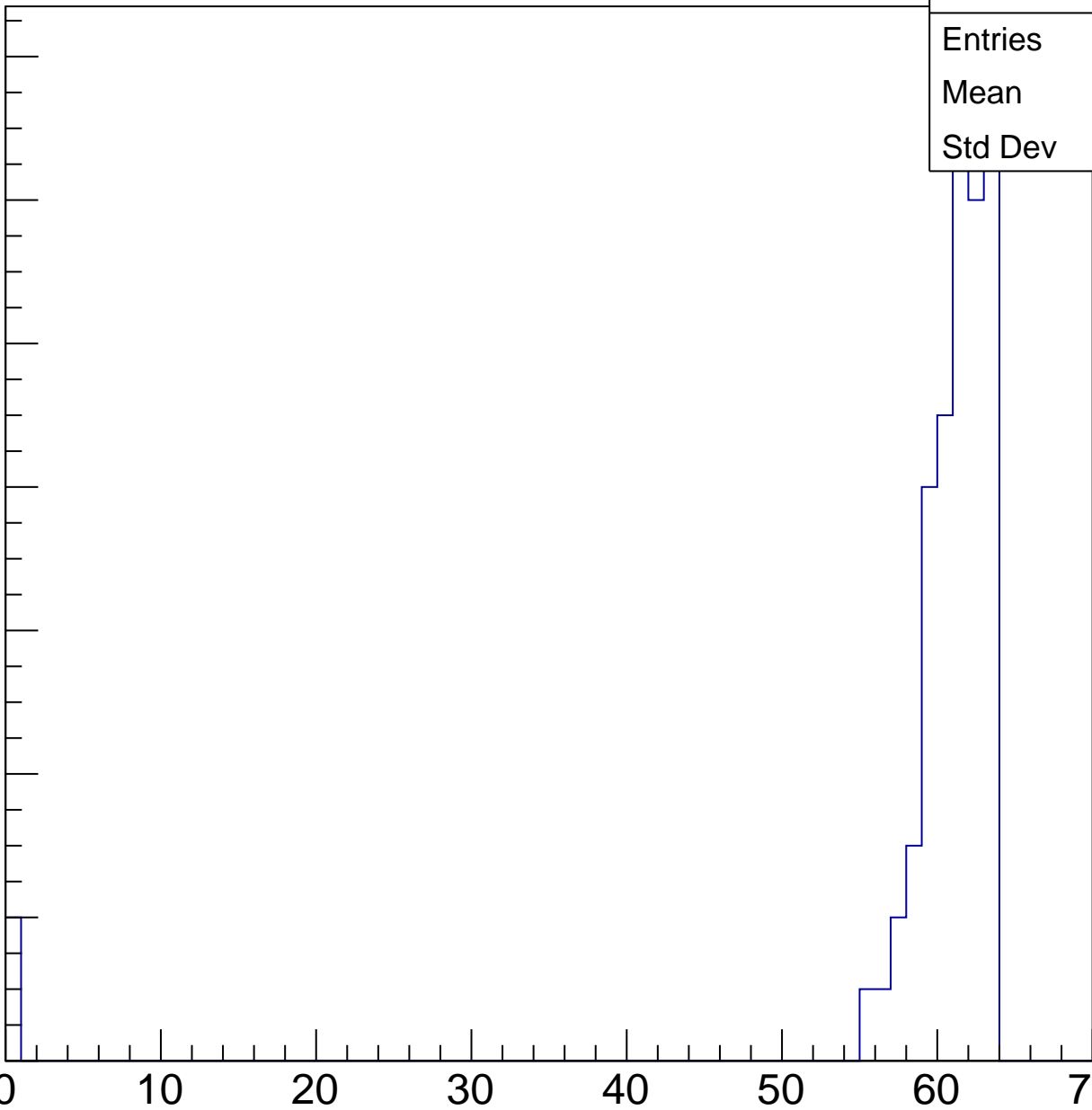
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.92
Std Dev	10.66

ampl



B0L101S, U21-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L101S, U21-ch15, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	30.29
Std Dev	3.374

Gaus mean : 30.6310

Gaus Width: 3.1257

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch15, adc1

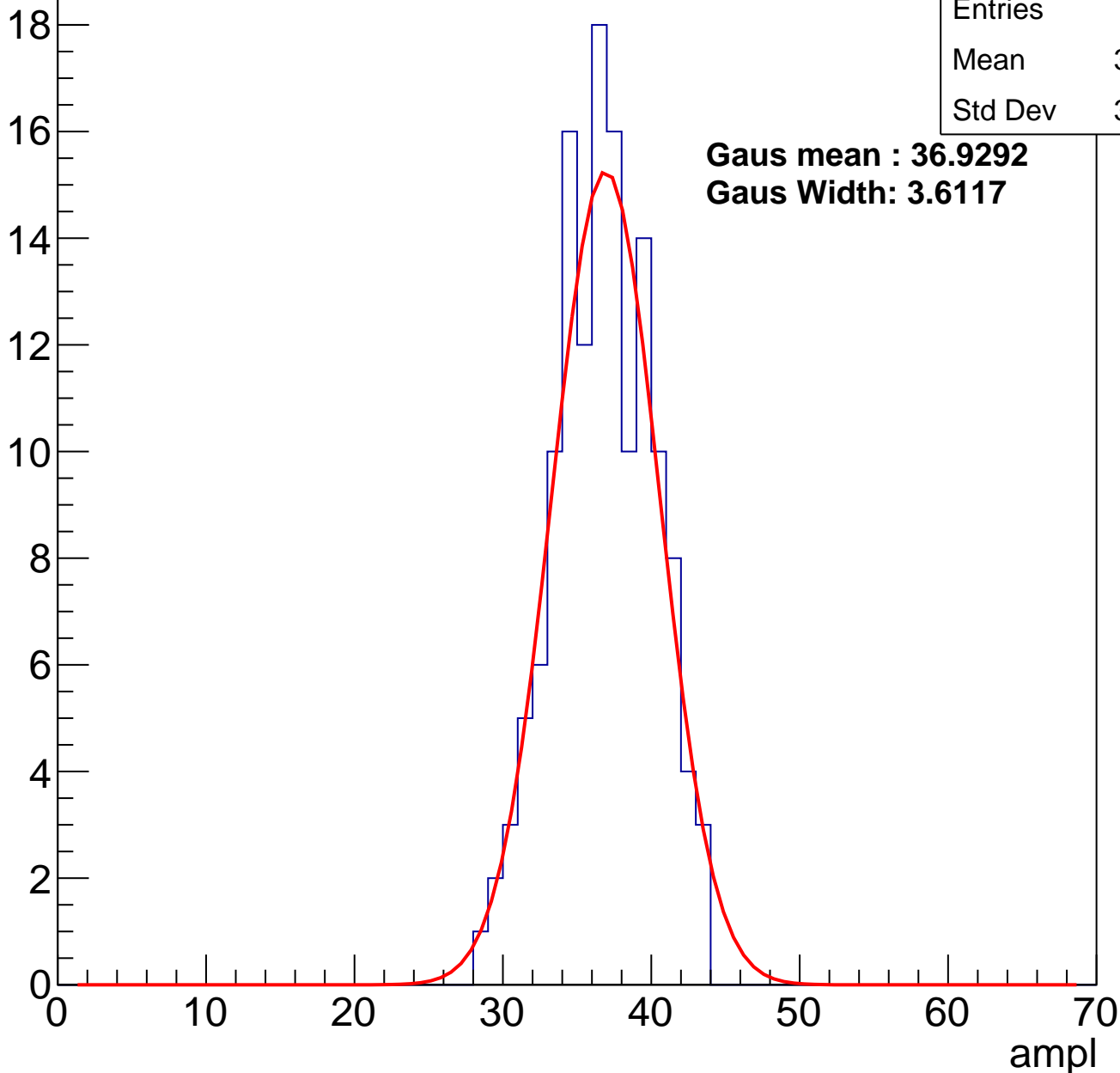
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	36.29
Std Dev	3.273

Gaus mean : 36.9292

Gaus Width: 3.6117

Entry



B0L101S, U21-ch15, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries 121

Mean 43.31

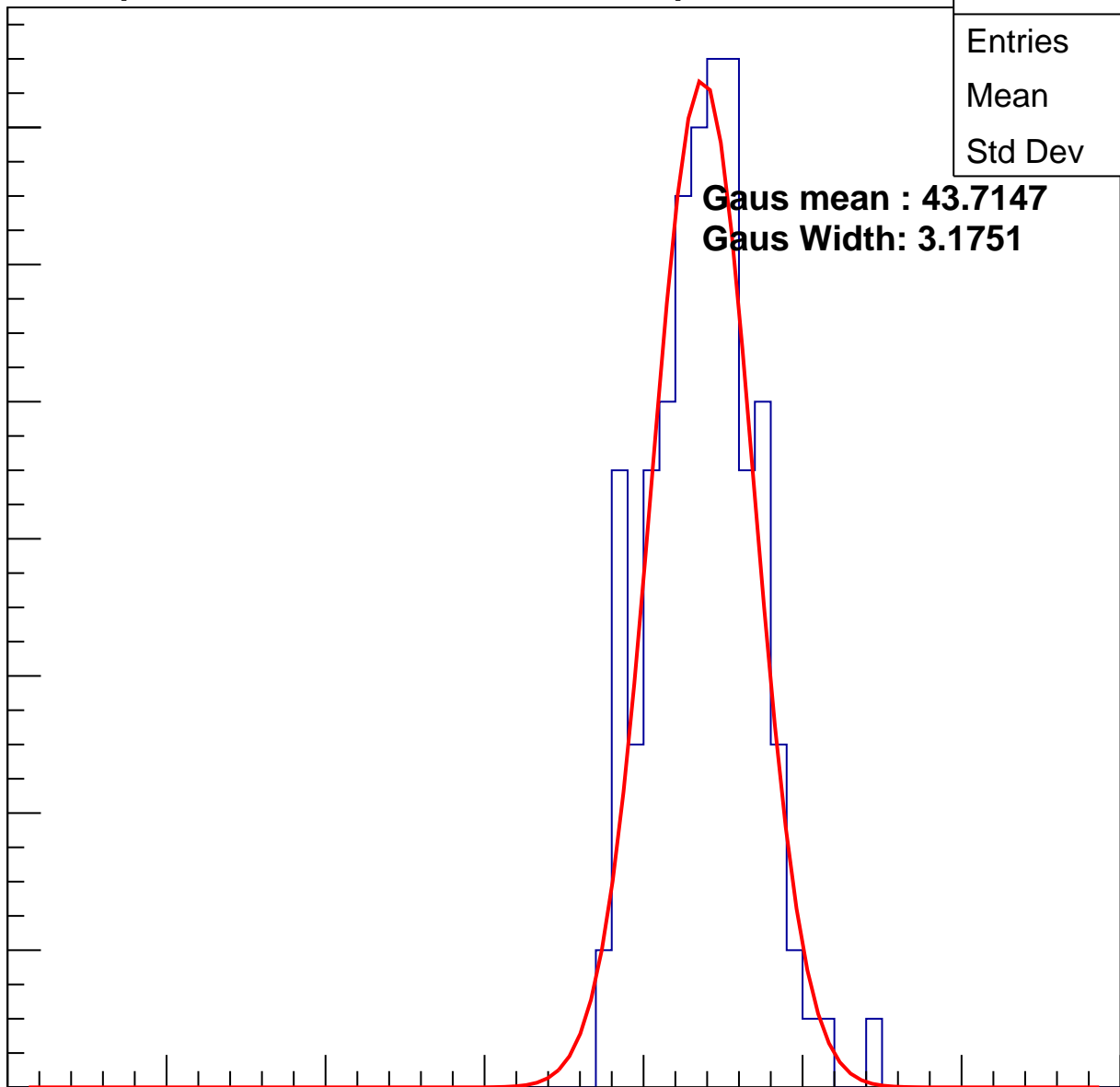
Std Dev 3.209

Gaus mean : 43.7147

Gaus Width: 3.1751

0 10 20 30 40 50 60 70

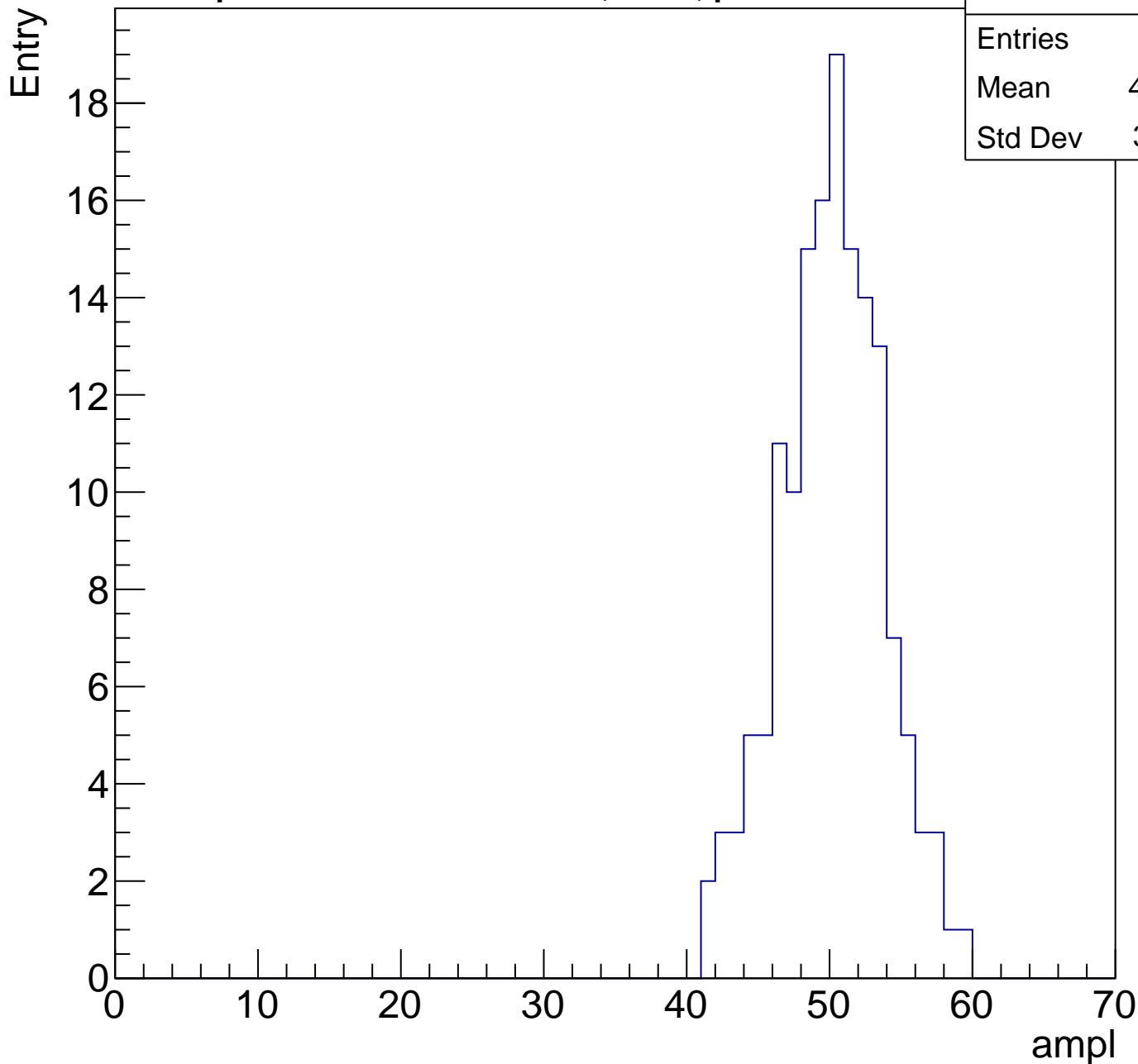
ampl



B0L101S, U21-ch15, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	49.69
Std Dev	3.601



B0L101S, U21-ch15, adc4

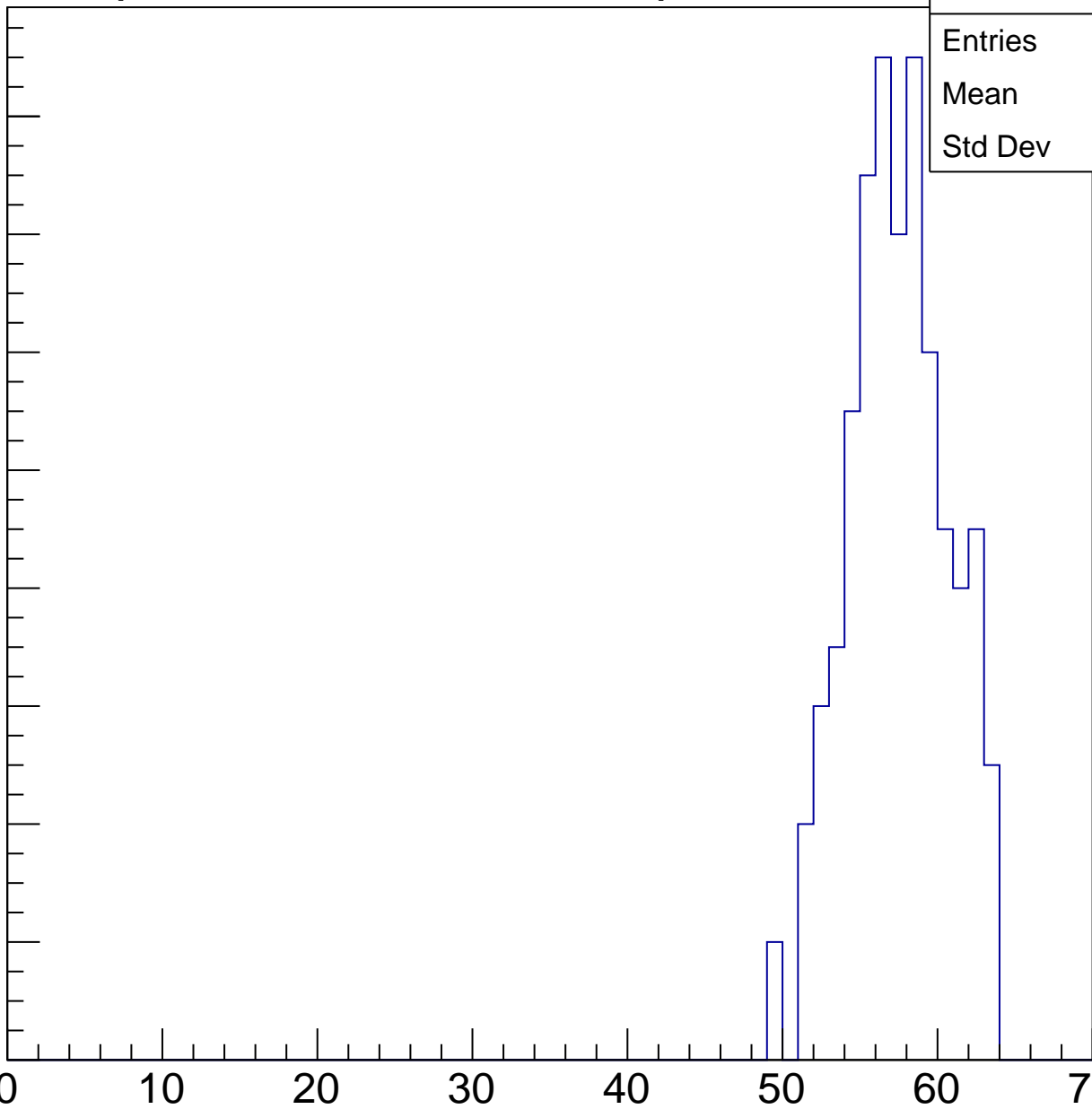
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	56.98
Std Dev	3.196

ampl



B0L101S, U21-ch15, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

58

Mean

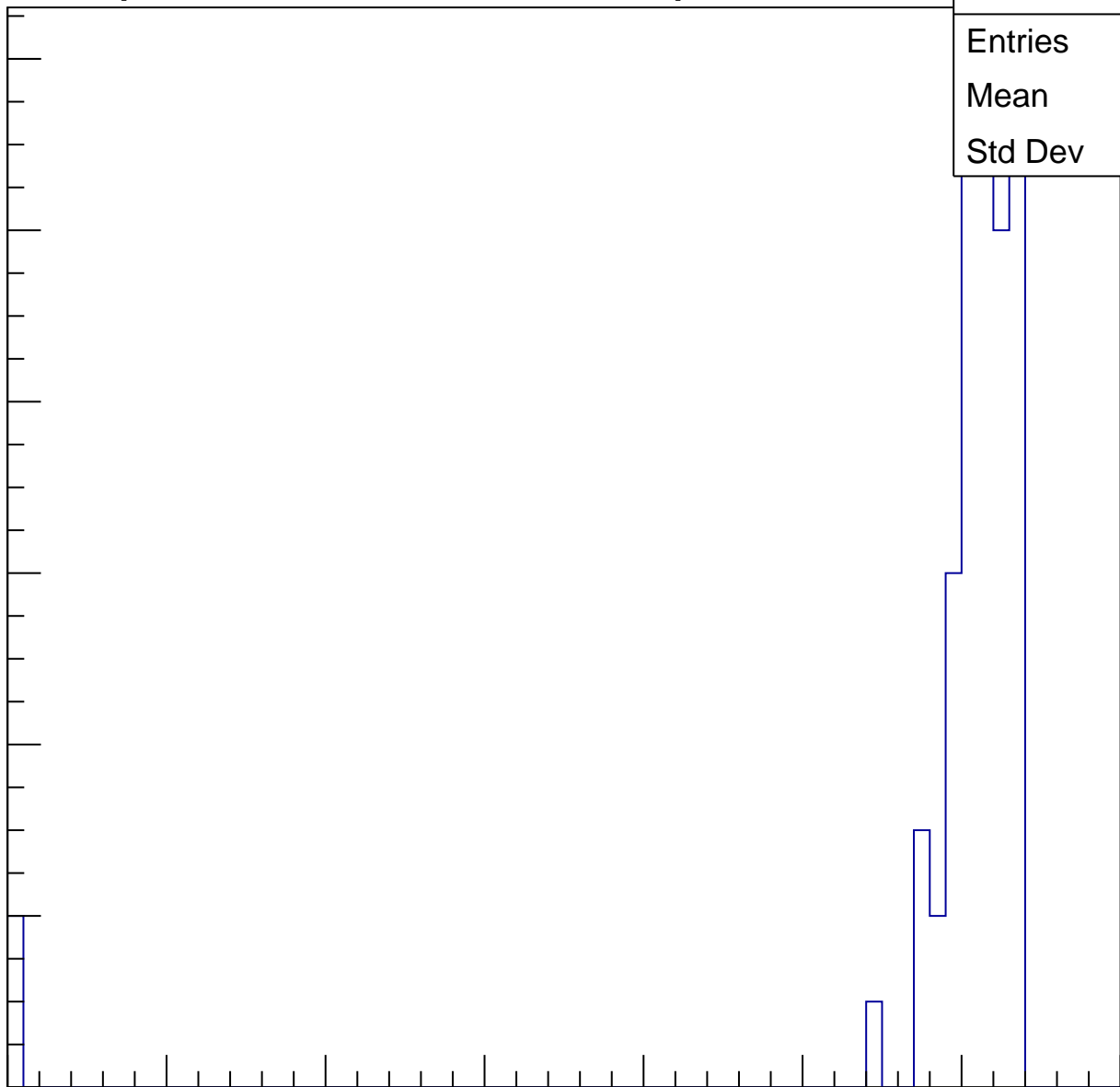
58.62

Std Dev

11.23

ampl

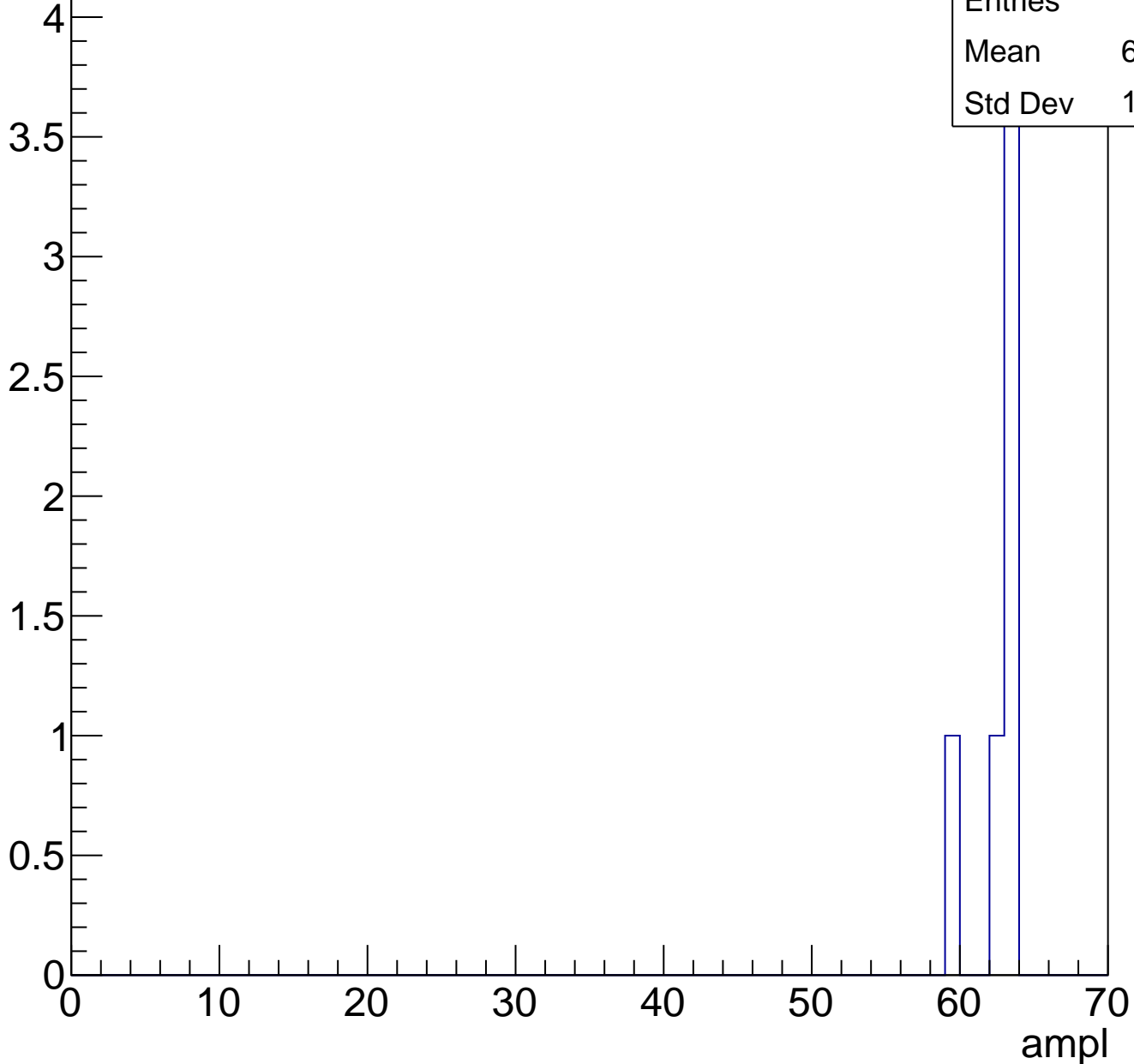
0 10 20 30 40 50 60 70



B0L101S, U21-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



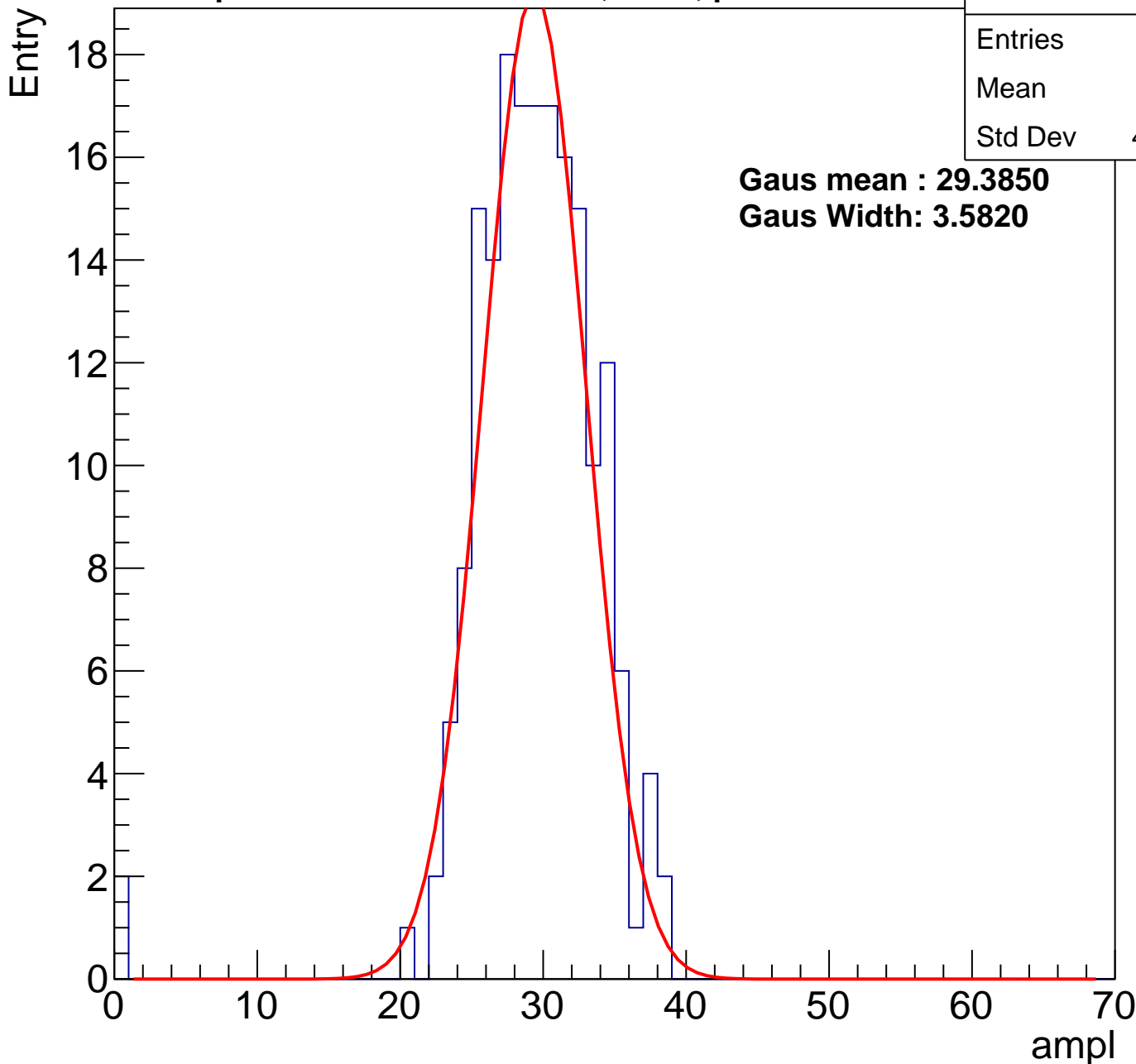
B0L101S, U21-ch16, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	28.9
Std Dev	4.719

Gaus mean : 29.3850

Gaus Width: 3.5820



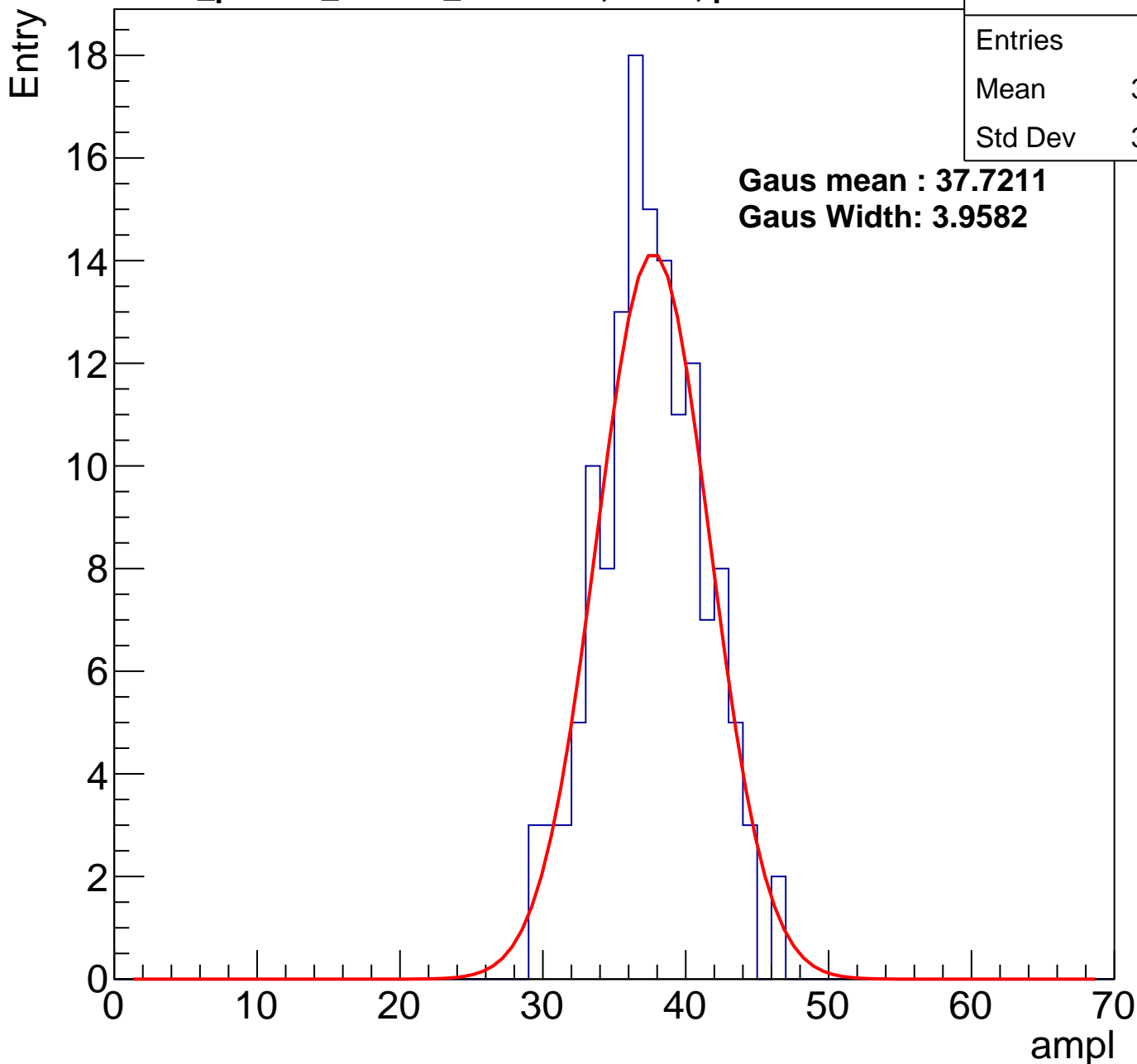
B0L101S, U21-ch16, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	37.09
Std Dev	3.625

Gaus mean : 37.7211

Gaus Width: 3.9582



B0L101S, U21-ch16, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	43.43
Std Dev	3.511

Gaus mean : 43.2877

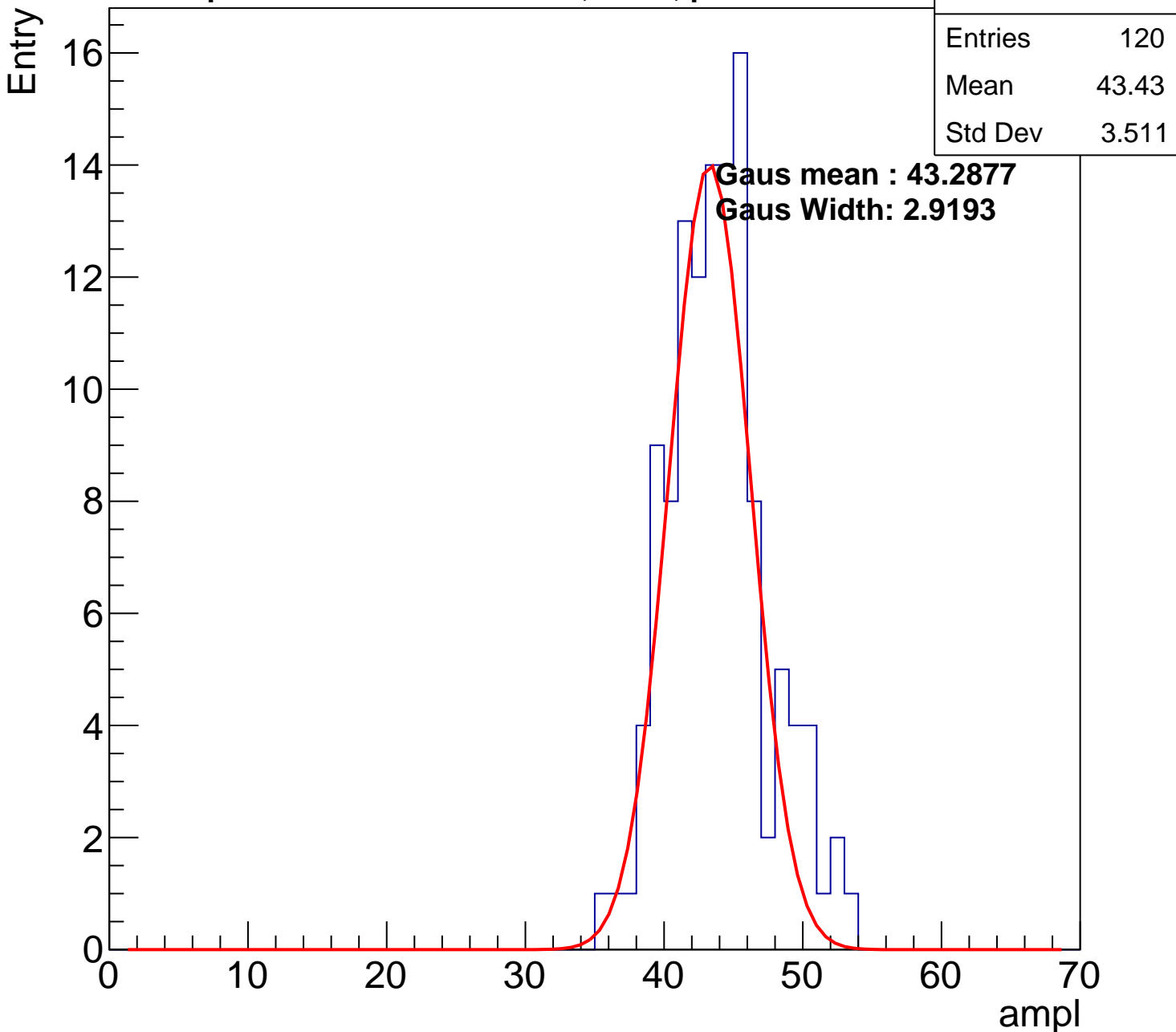
Gaus Width: 2.9193

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	49.58
Std Dev	3.057

Entry

16

14

12

10

8

6

4

2

0

0

10

20

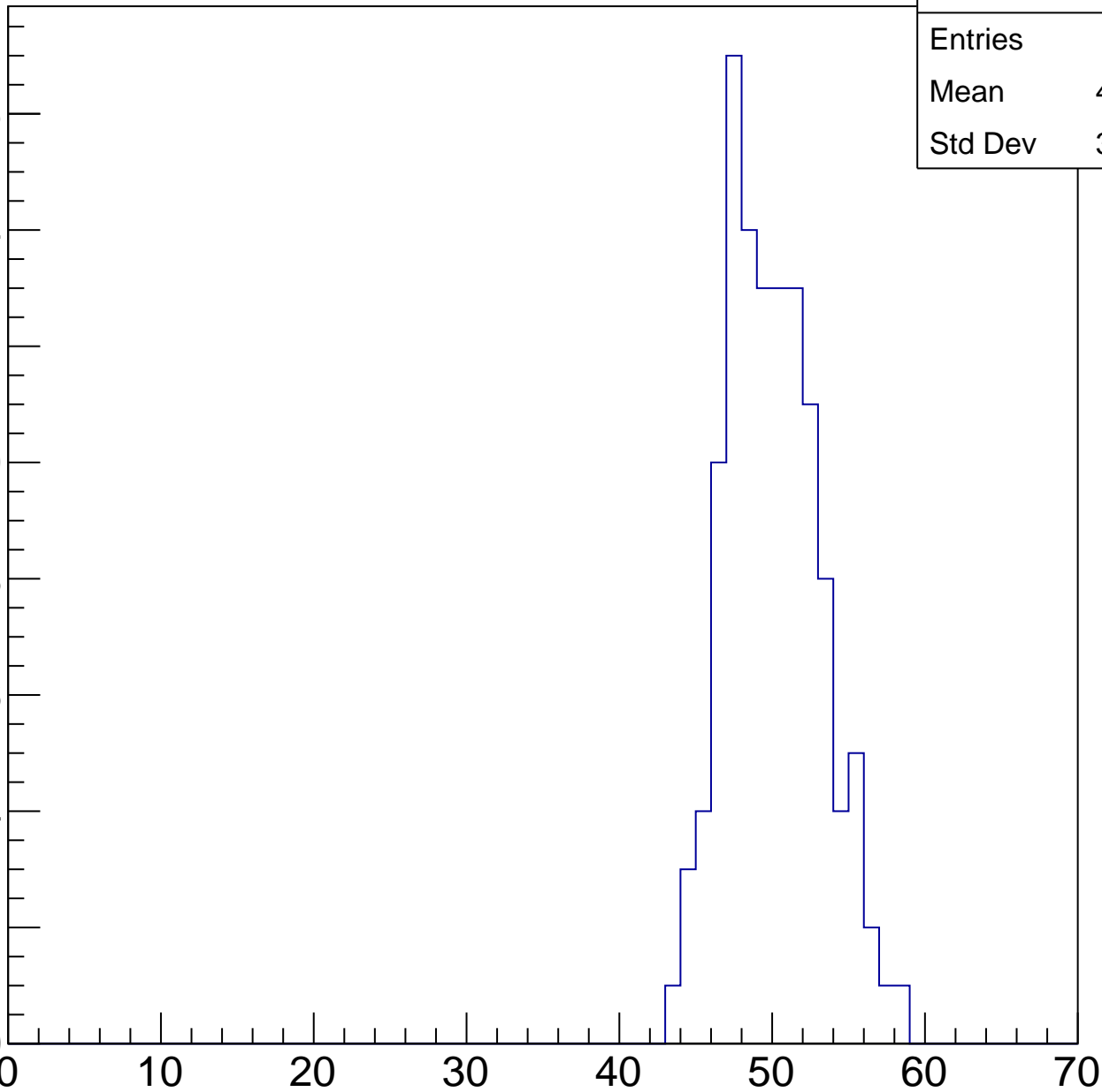
30

40

50

60

ampl



B0L101S, U21-ch16, adc4

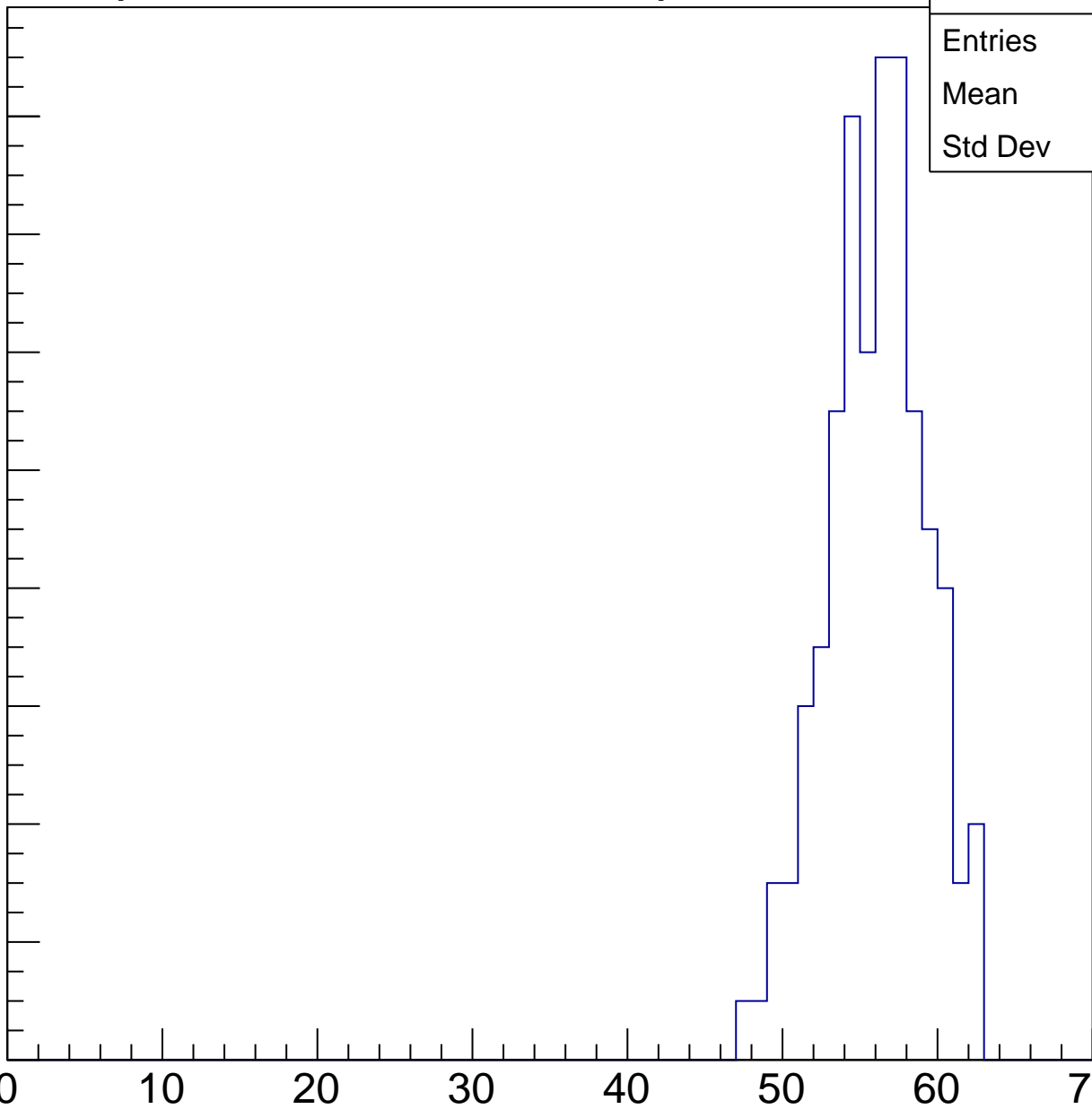
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	55.58
Std Dev	3.179

ampl



B0L101S, U21-ch16, adc5

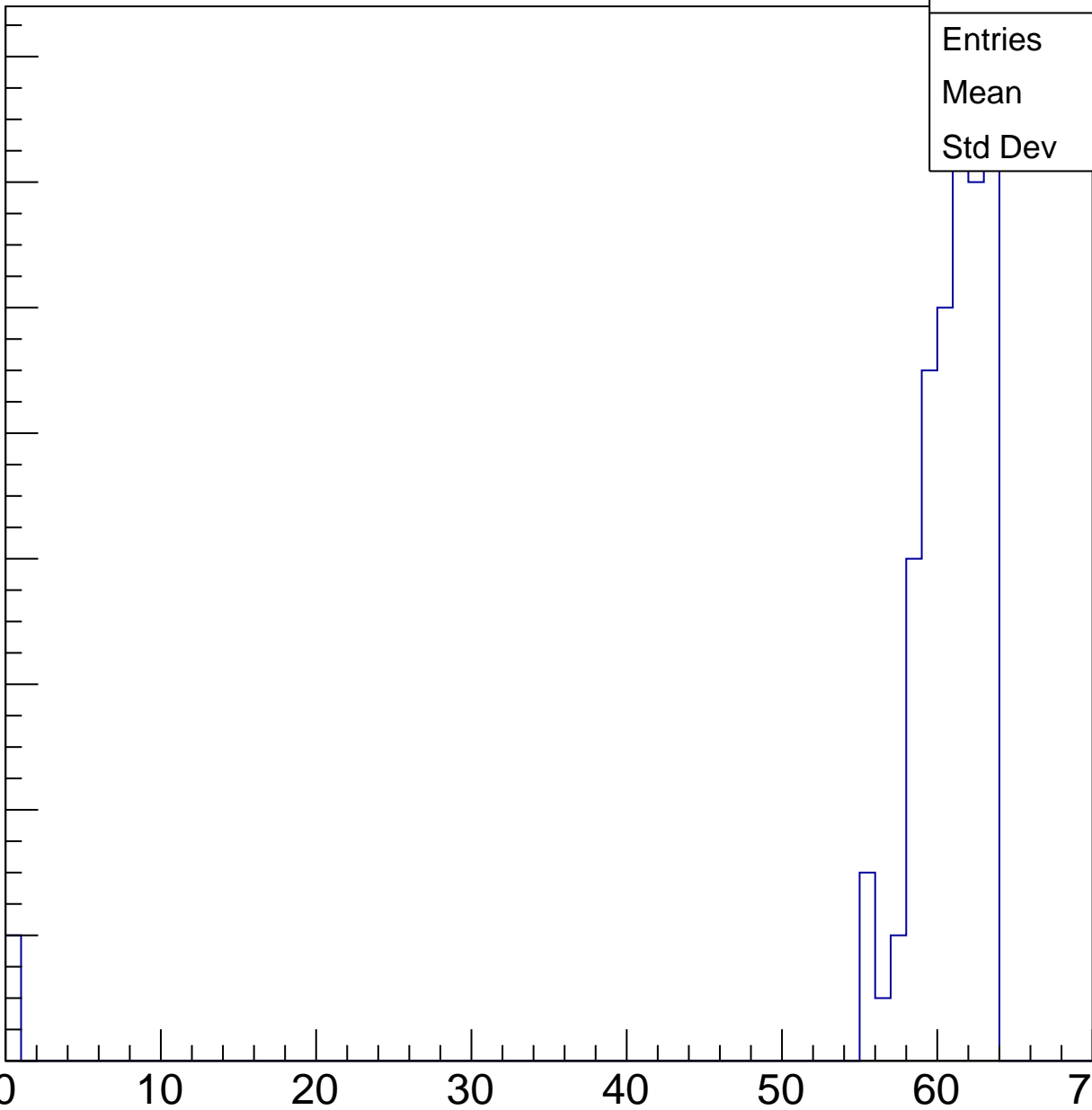
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.04
Std Dev	9.442

ampl



B0L101S, U21-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



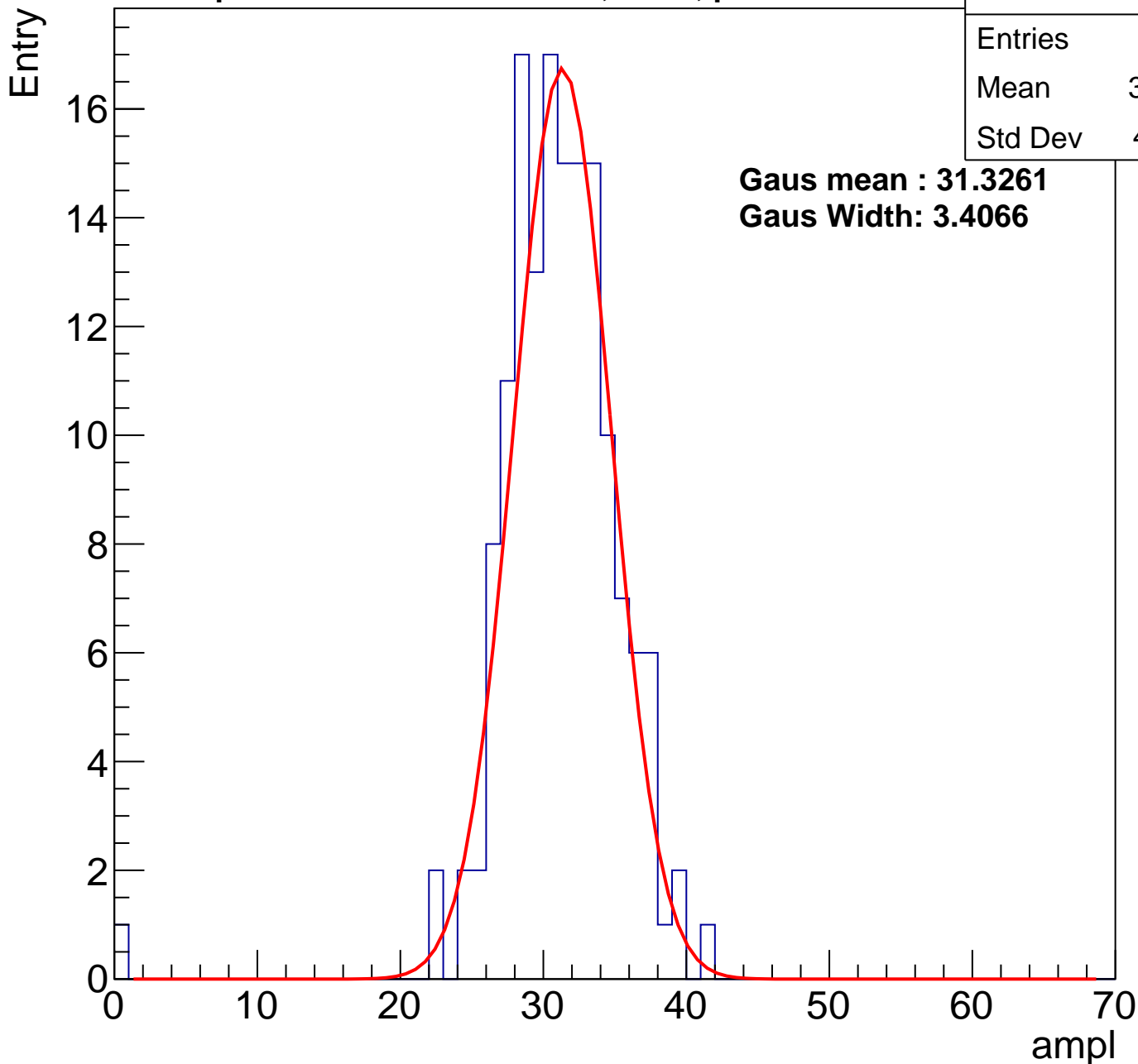
B0L101S, U21-ch17, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	30.66
Std Dev	4.301

Gaus mean : 31.3261

Gaus Width: 3.4066



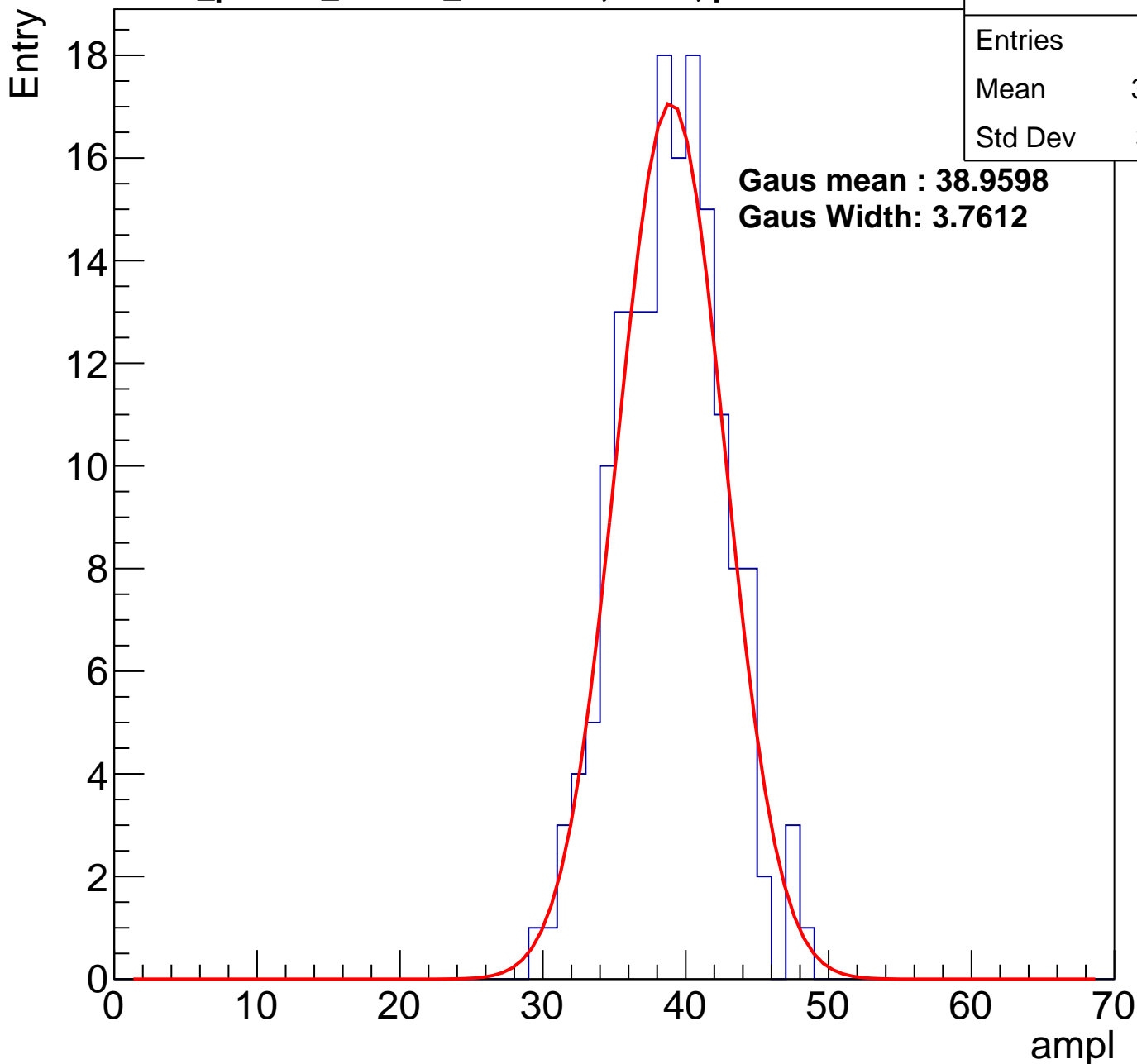
B0L101S, U21-ch17, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	38.46
Std Dev	3.671

Gaus mean : 38.9598

Gaus Width: 3.7612



B0L101S, U21-ch17, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

140

Mean

46.14

Std Dev

3.551

Gaus mean : 46.4964

Gaus Width: 3.3391

0

10

20

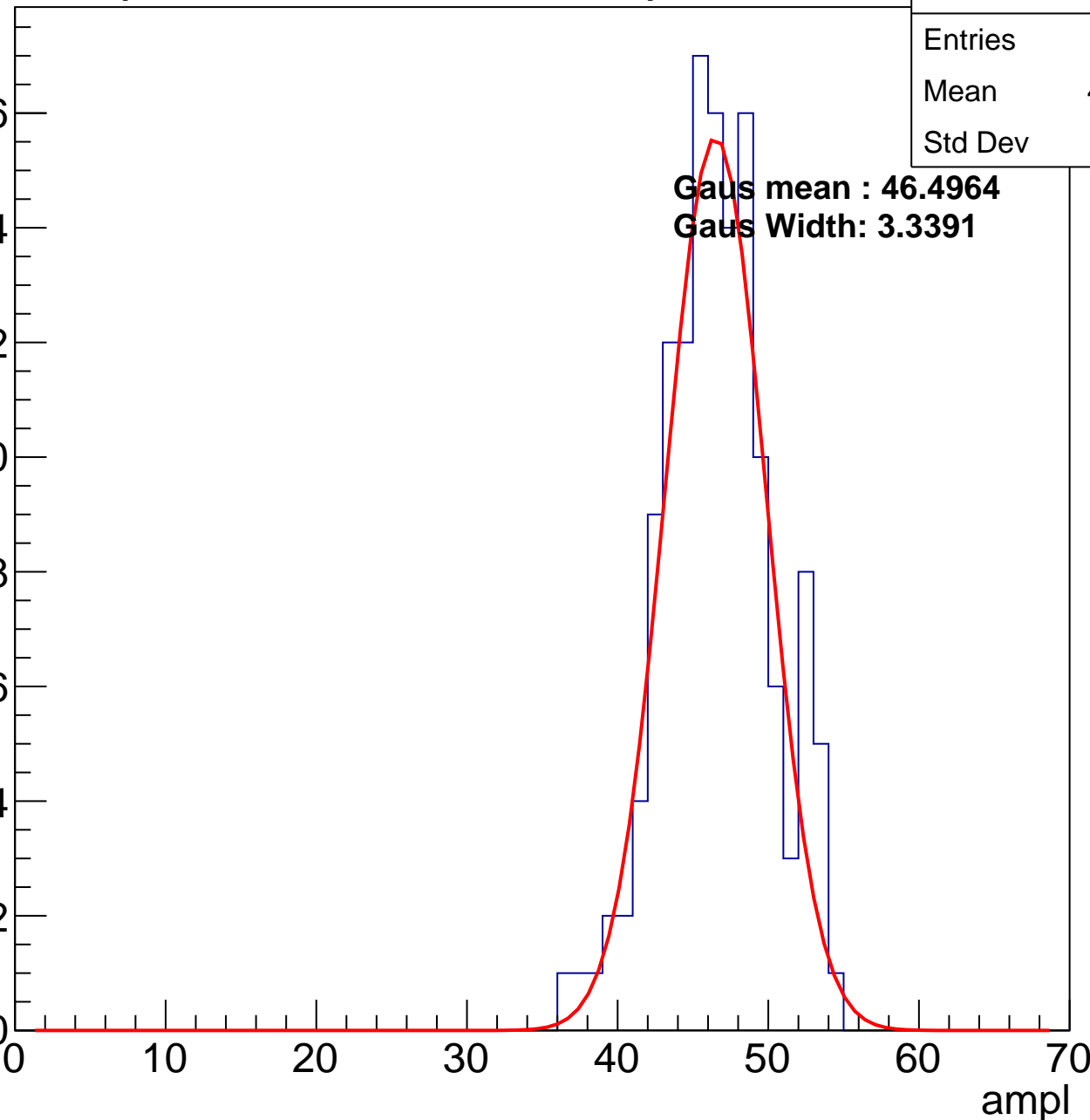
30

40

50

60

ampl



B0L101S, U21-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

138

Mean

52.93

Std Dev

3.322

ampl

0

10

20

30

40

50

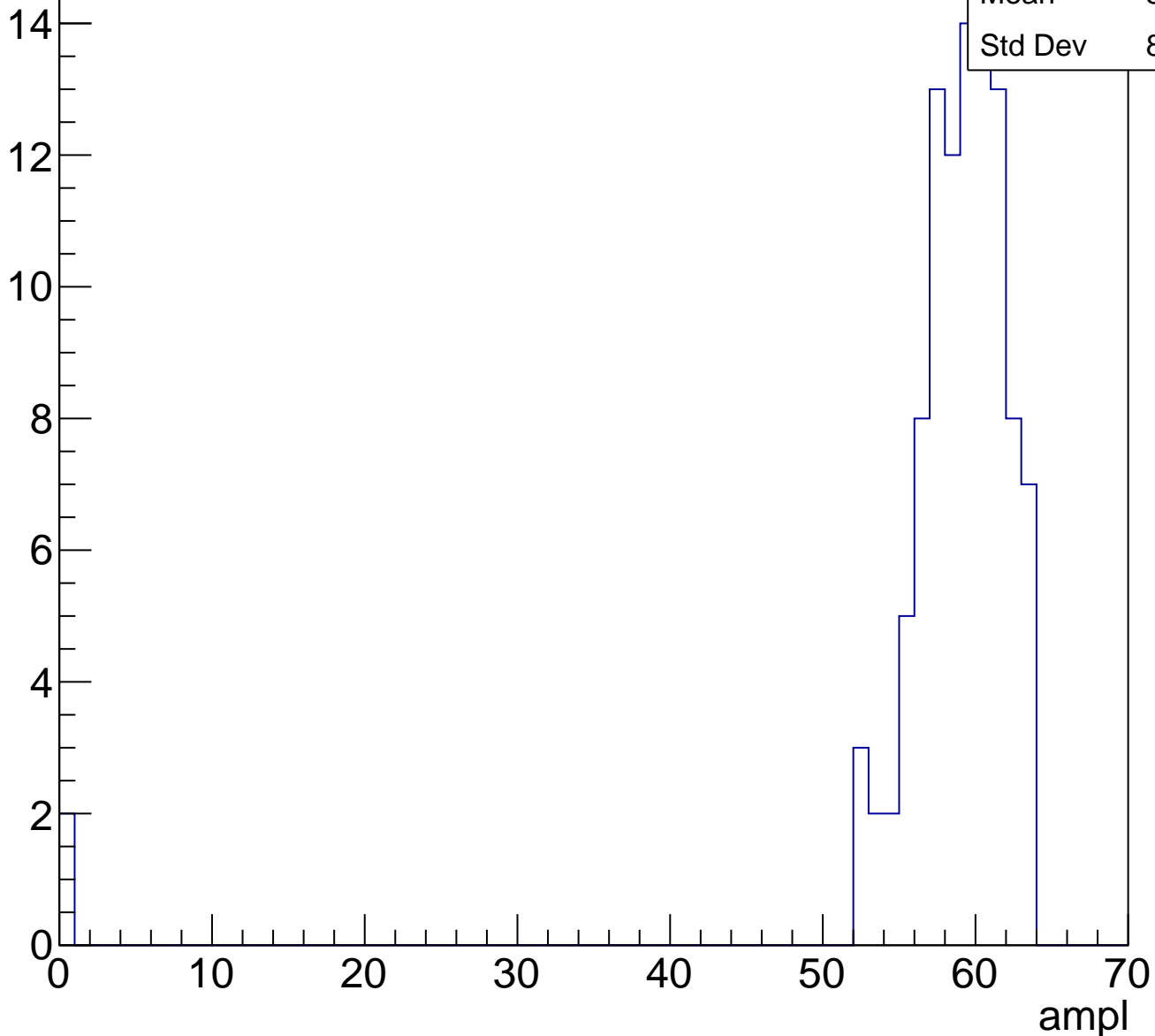
60

70

B0L101S, U21-ch17, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch17, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	38
Mean	61.74
Std Dev	1.25

B0L101S, U21-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

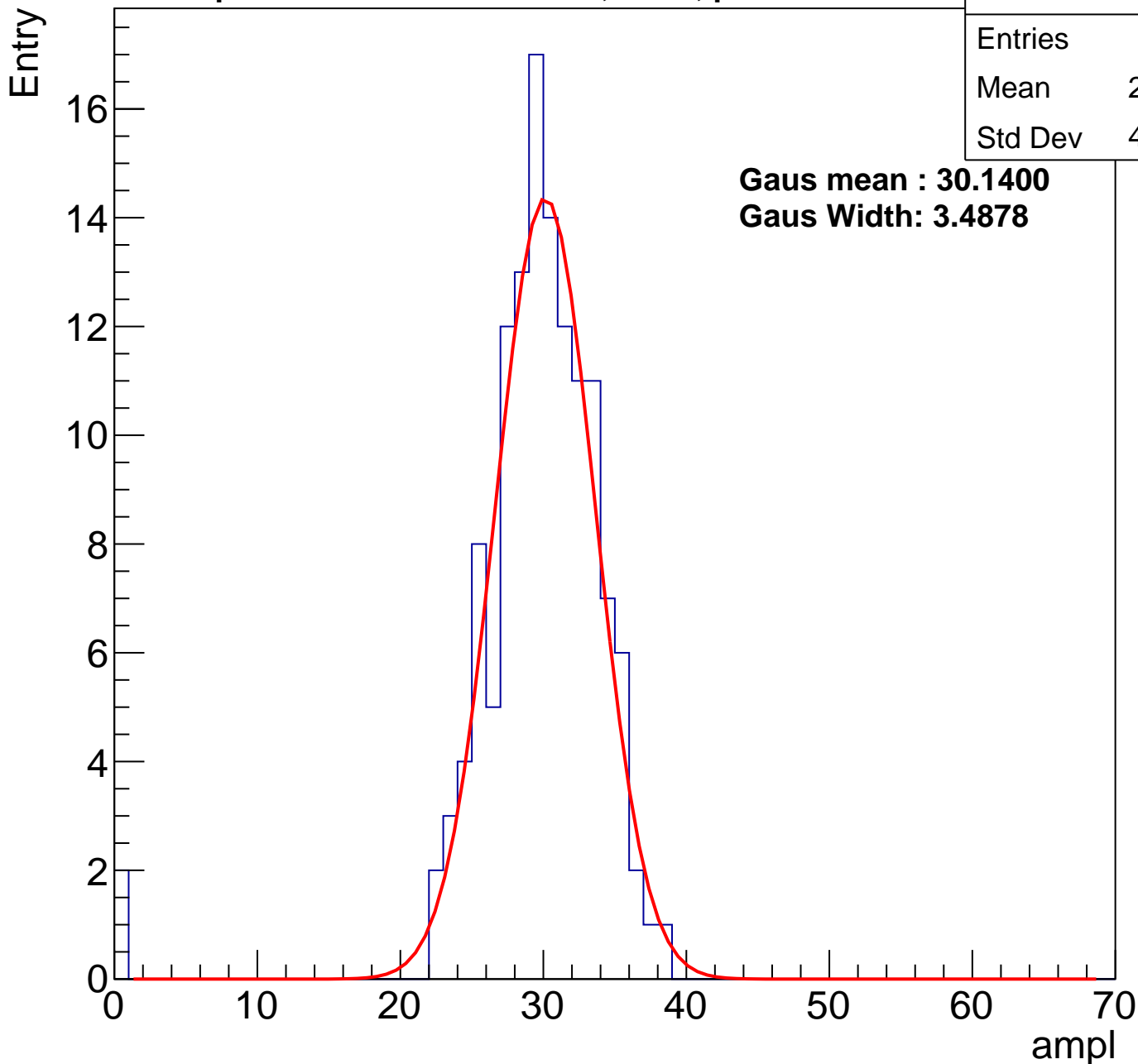
B0L101S, U21-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.18
Std Dev	4.934

Gaus mean : 30.1400

Gaus Width: 3.4878

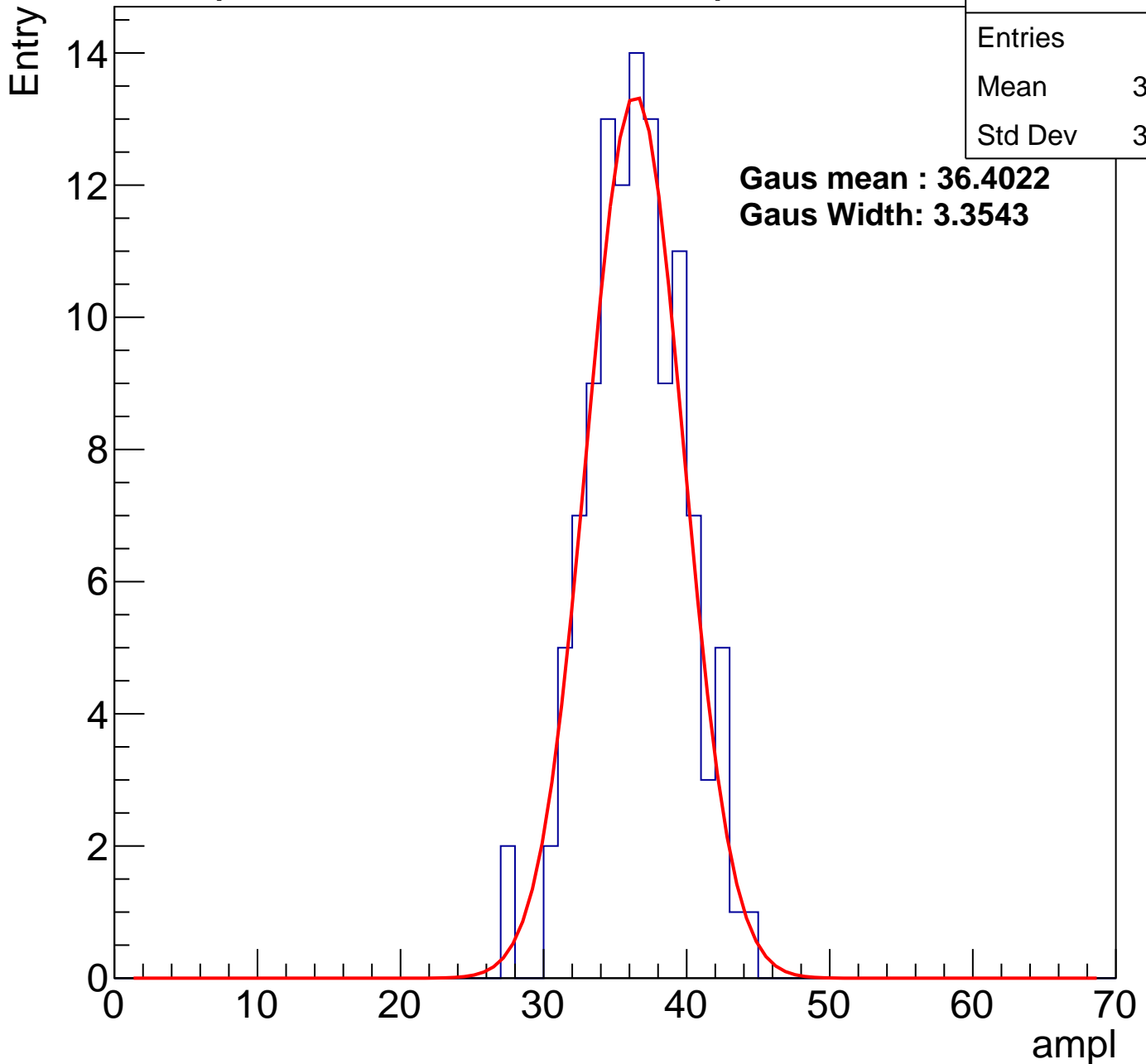


B0L101S, U21-ch18, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	36.04
Std Dev	3.303

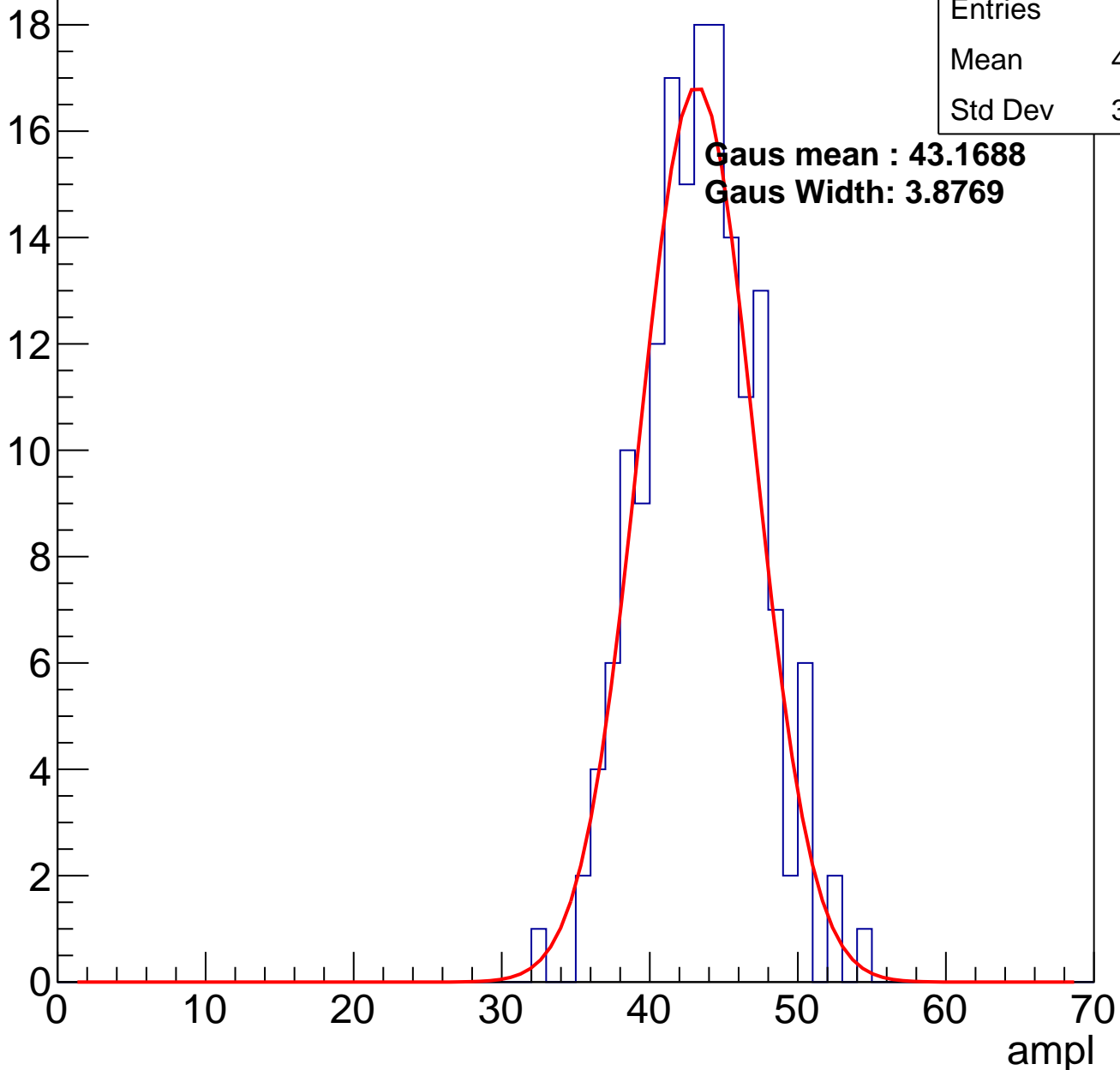
Gaus mean : 36.4022
Gaus Width: 3.3543



B0L101S, U21-ch18, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	50.59
Std Dev	3.391

Entry

16

14

12

10

8

6

4

2

0

0

10

20

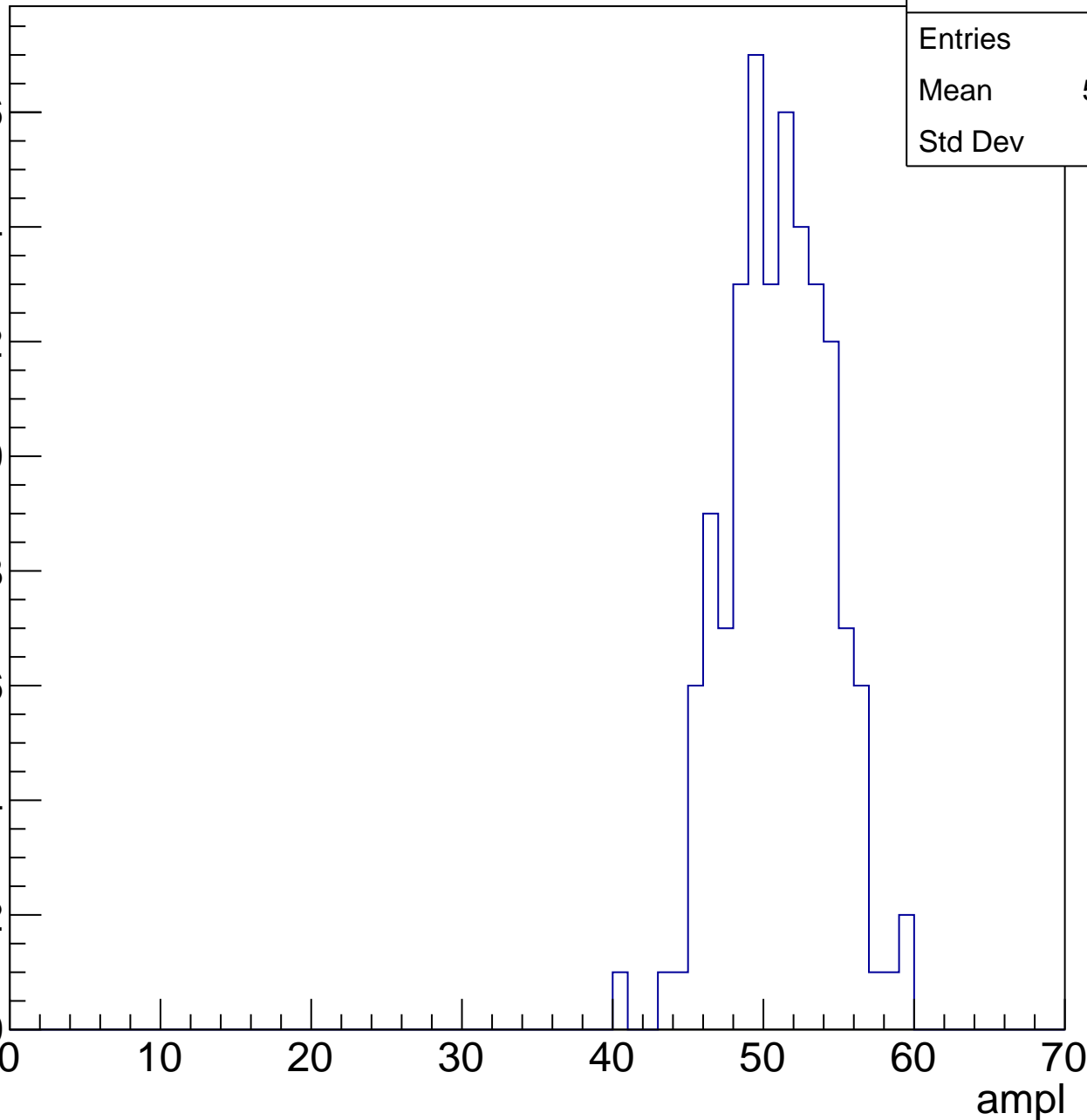
30

40

50

60

ampl

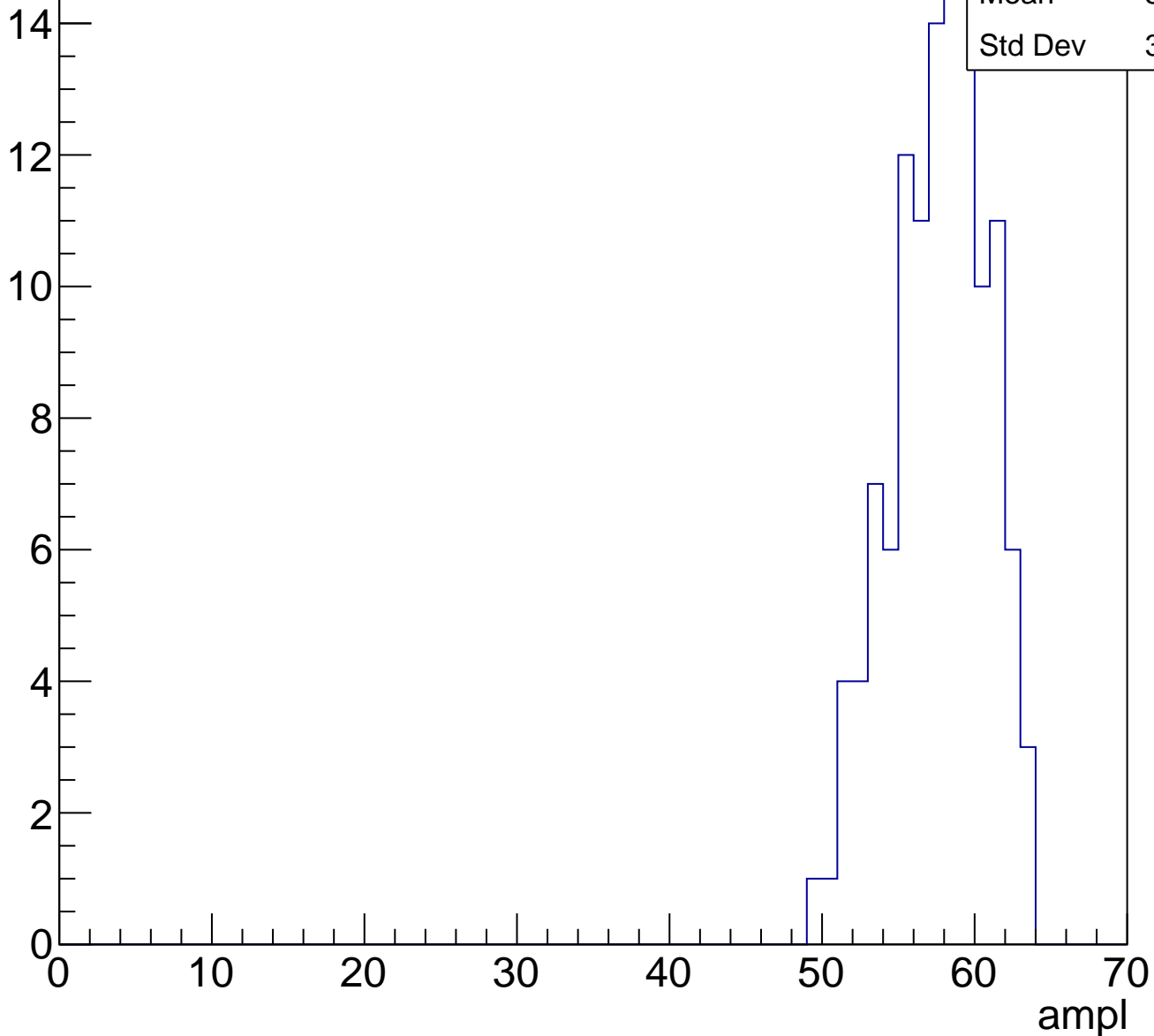


B0L101S, U21-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	57.23
Std Dev	3.129

Entry



B0L101S, U21-ch18, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

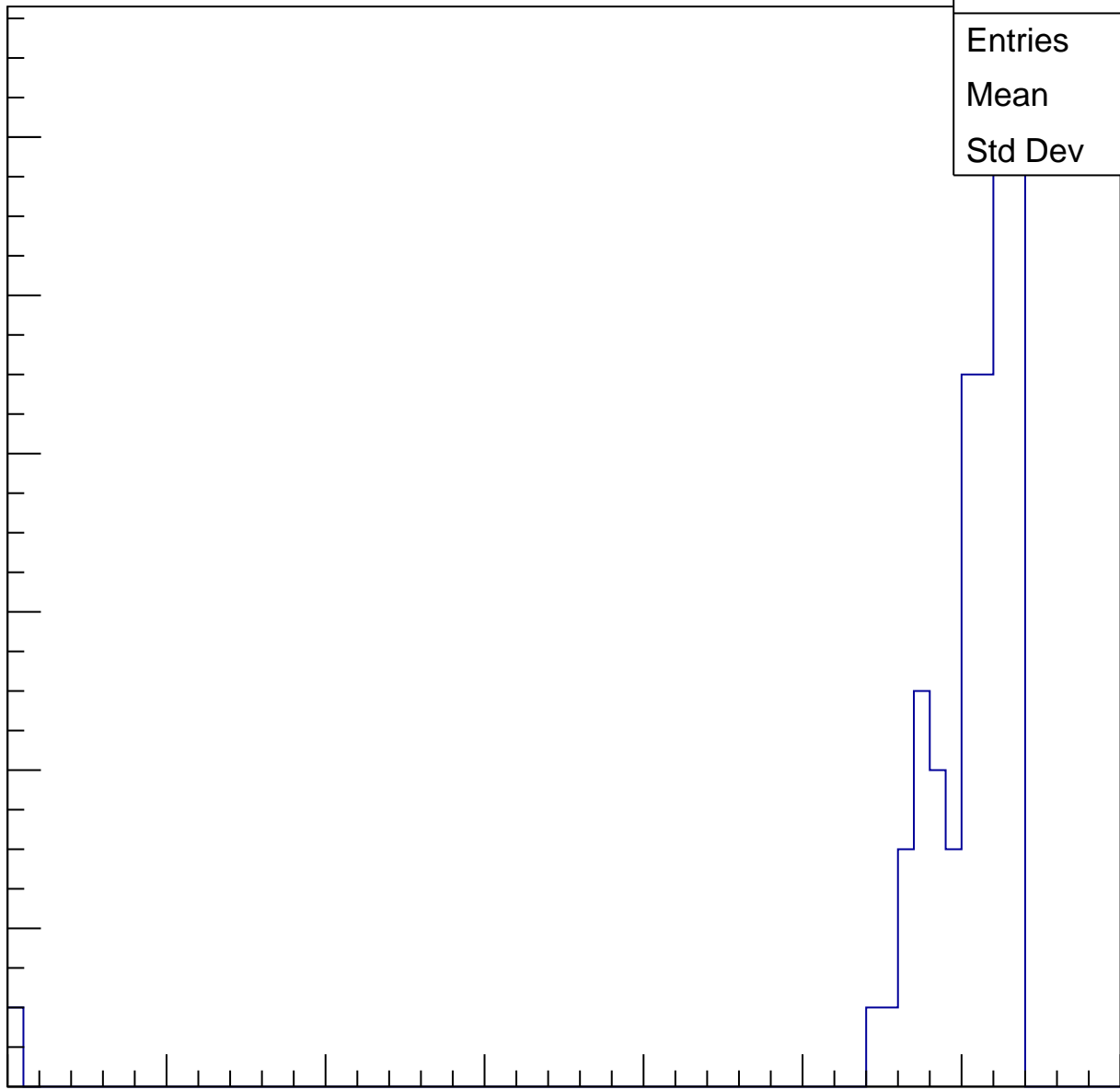
2

0

Entries	61
Mean	59.39
Std Dev	8.021

ampl

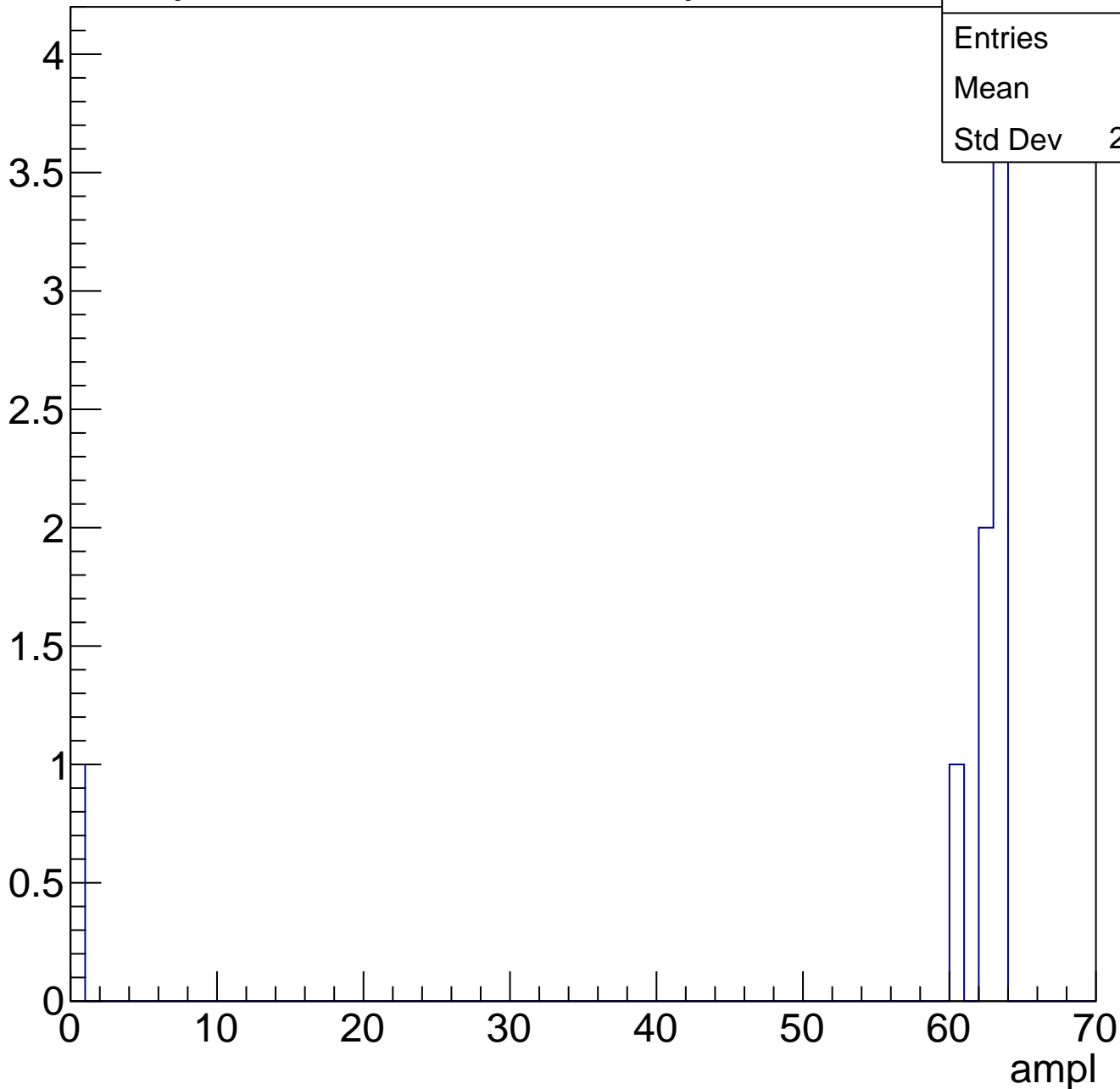
0 10 20 30 40 50 60 70



B0L101S, U21-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch19, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	30.34
Std Dev	3.371

Gaus mean : 30.8138

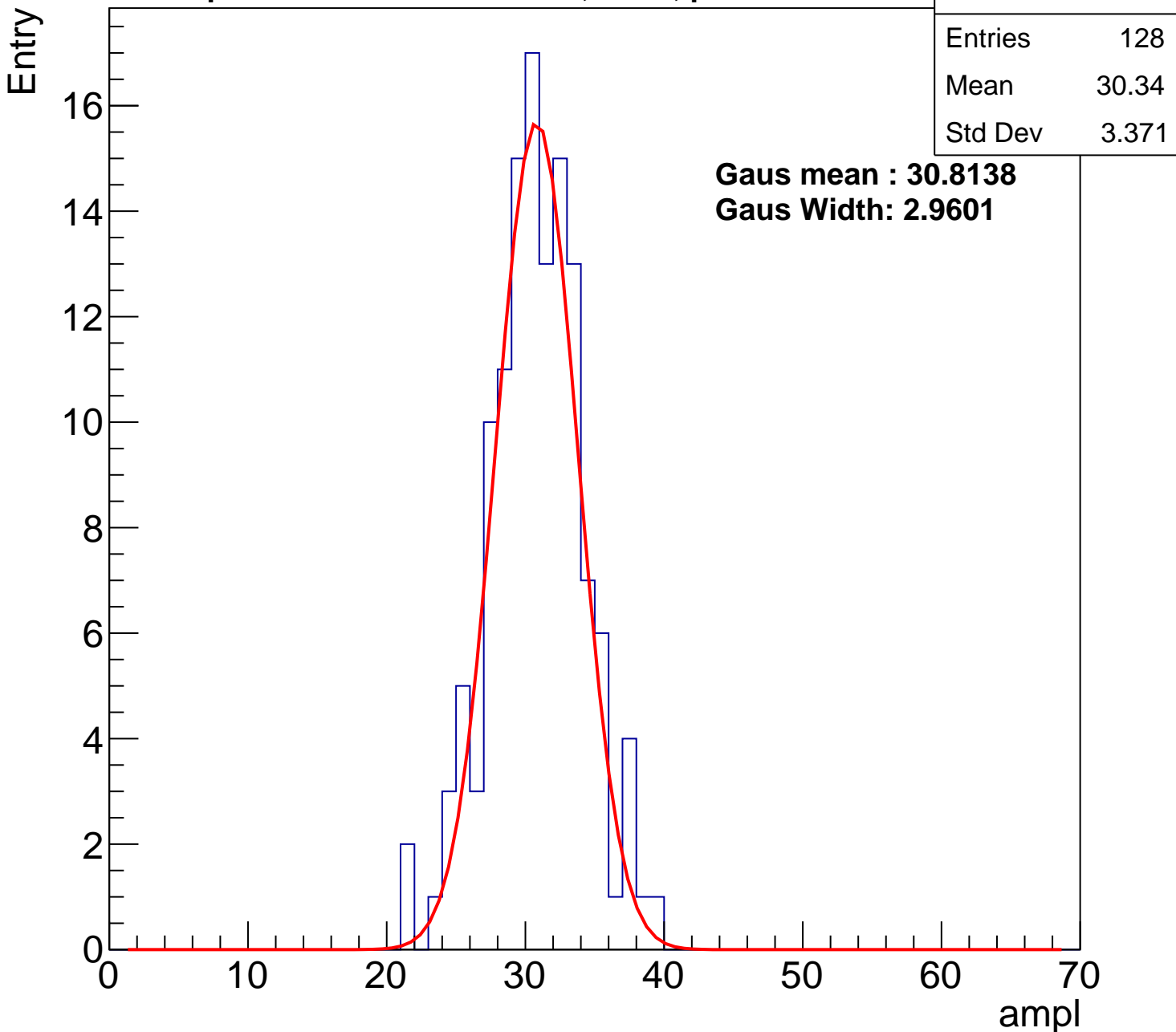
Gaus Width: 2.9601

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



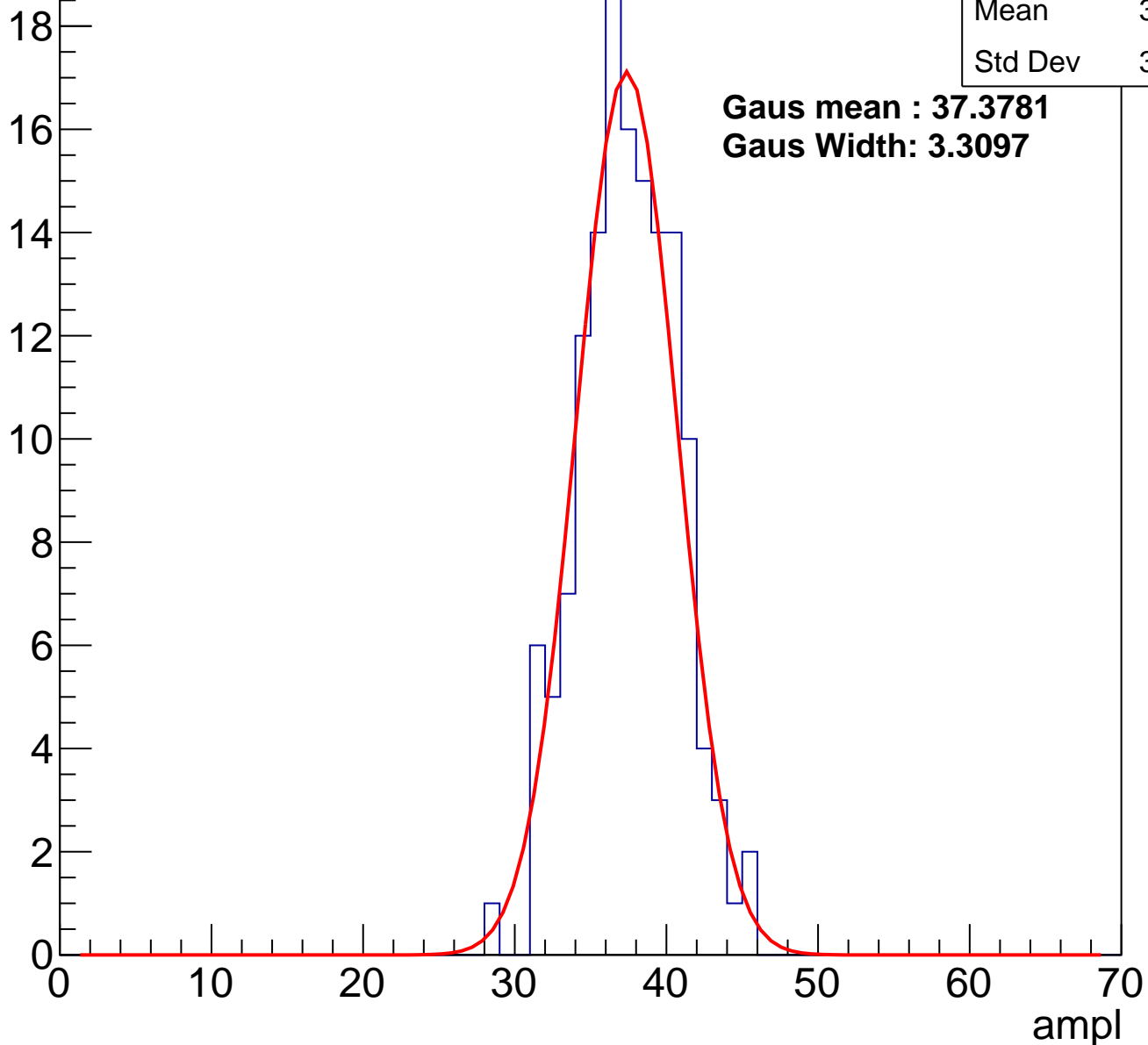
B0L101S, U21-ch19, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	37.03
Std Dev	3.194

Gaus mean : 37.3781
Gaus Width: 3.3097

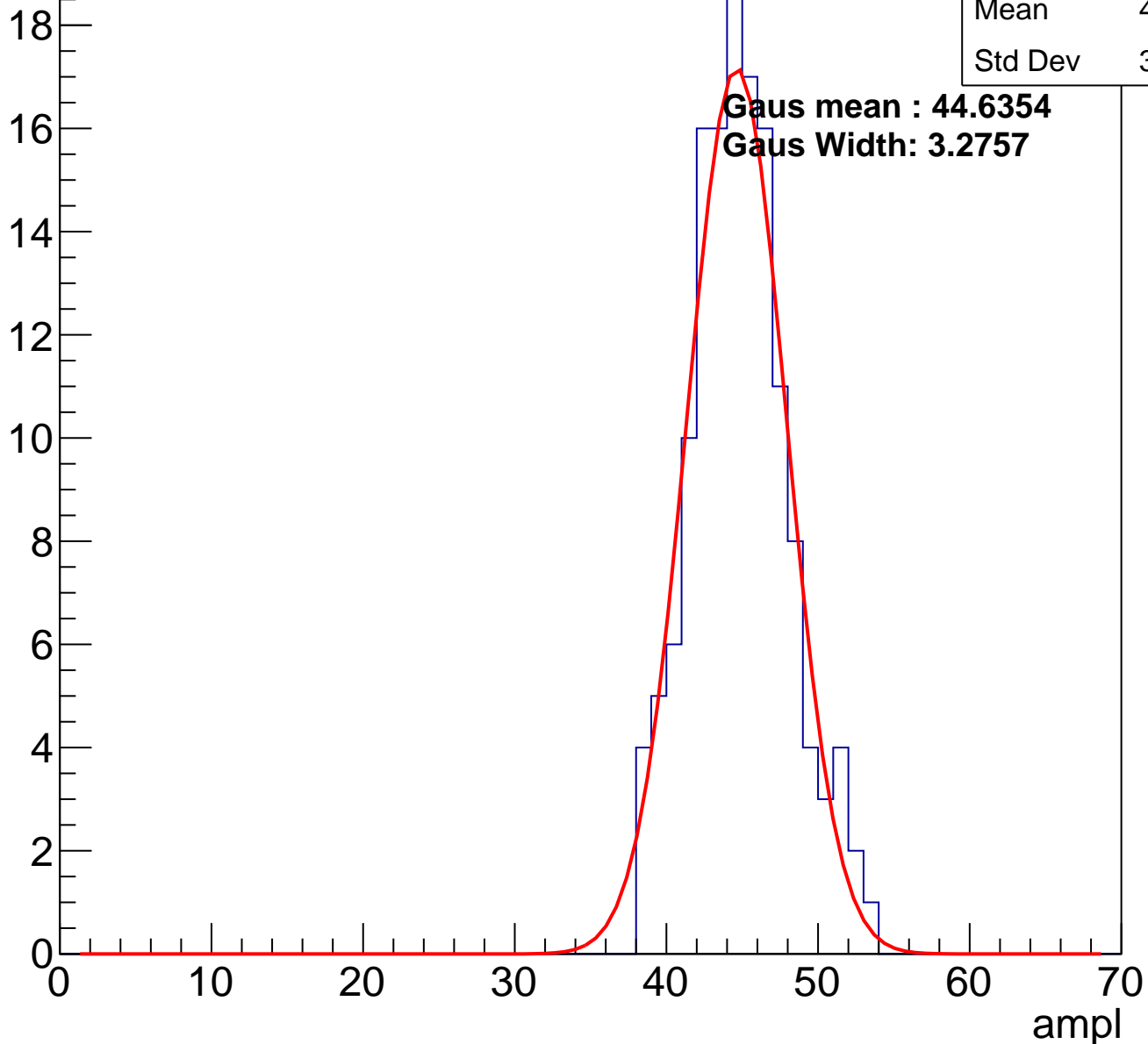
Entry



B0L101S, U21-ch19, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

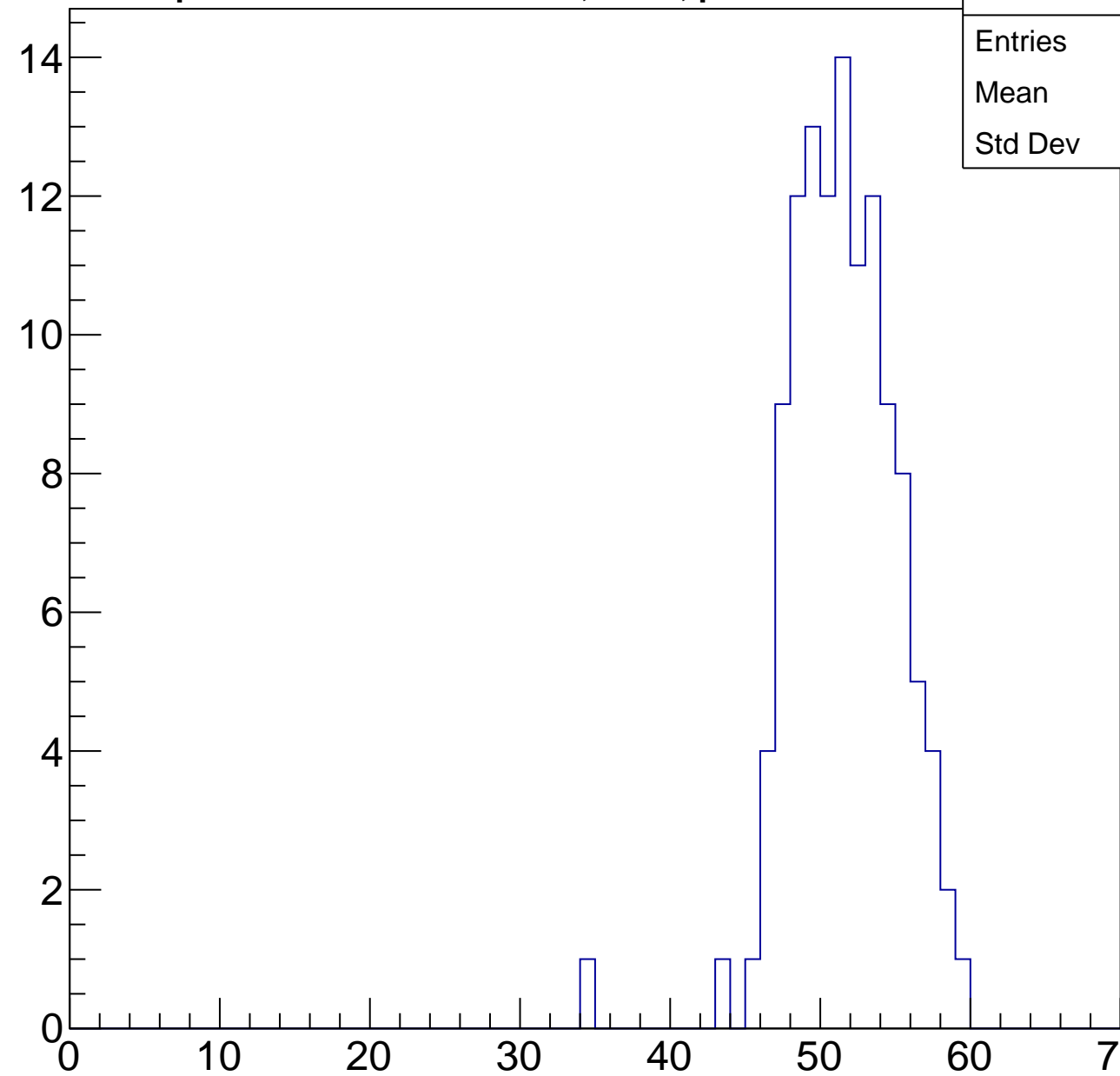
Entries	119
Mean	51.03
Std Dev	3.555

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch19, adc4

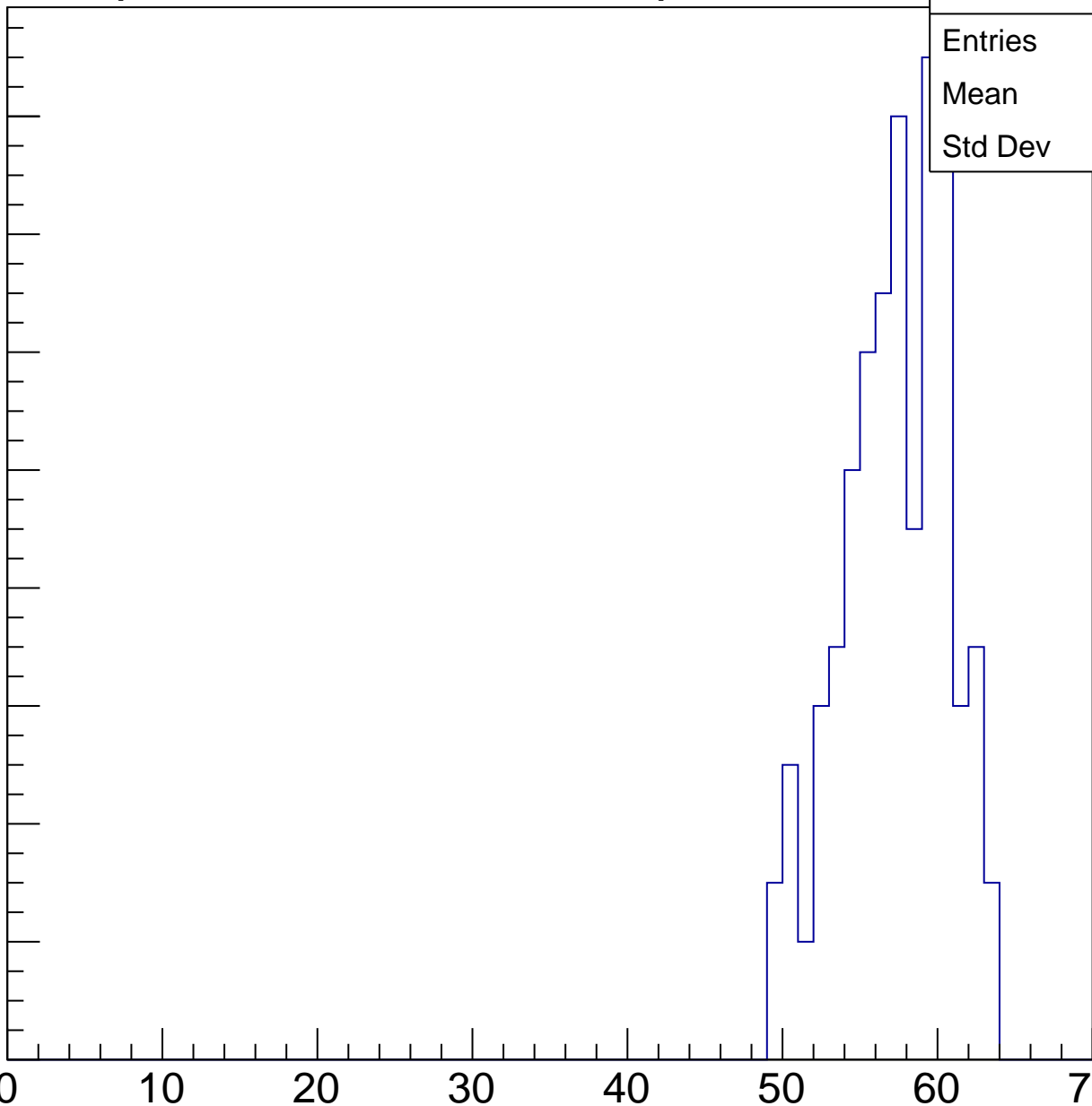
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	56.81
Std Dev	3.388

ampl



B0L101S, U21-ch19, adc5

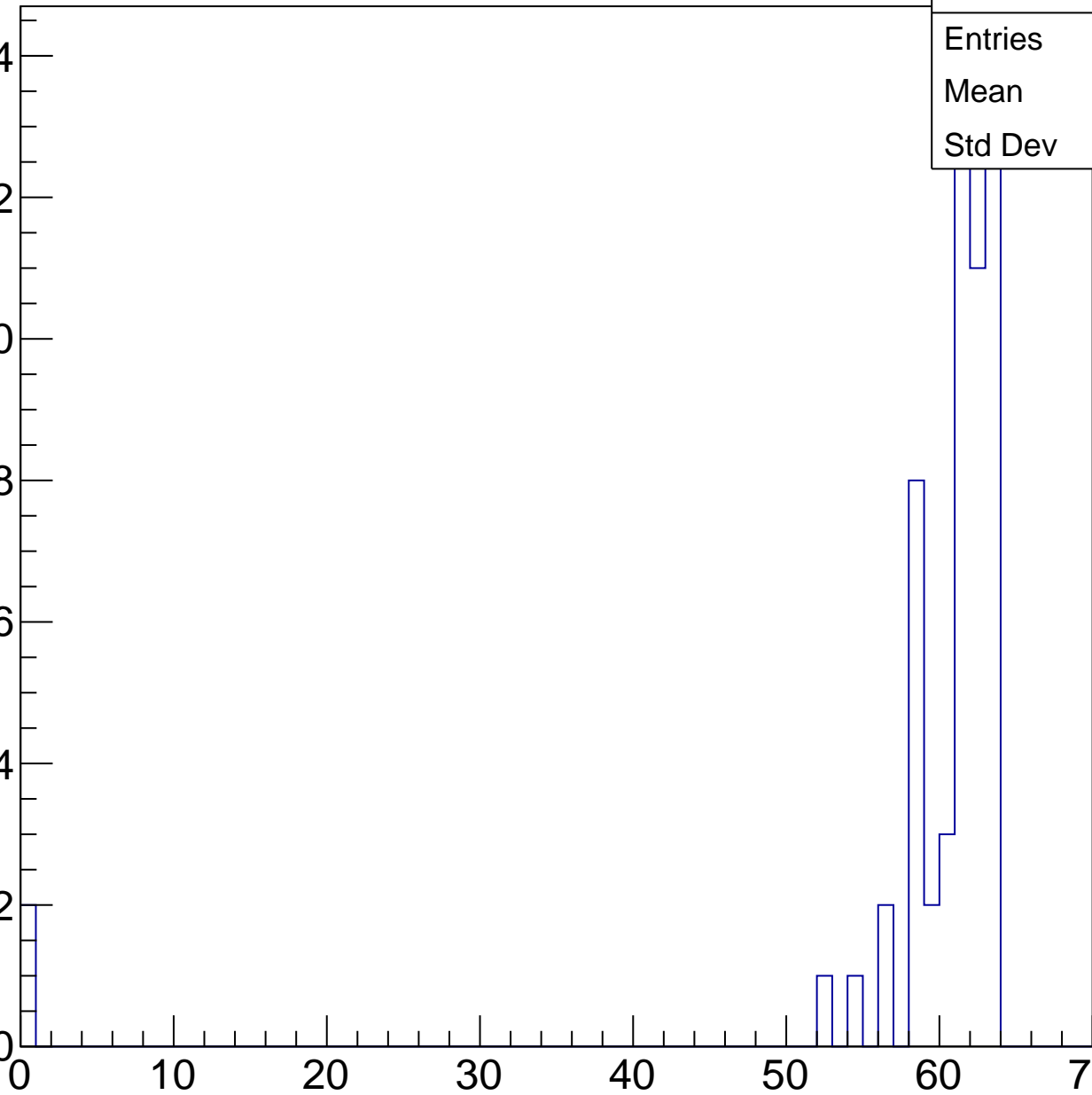
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	57
Mean	58.54
Std Dev	11.42

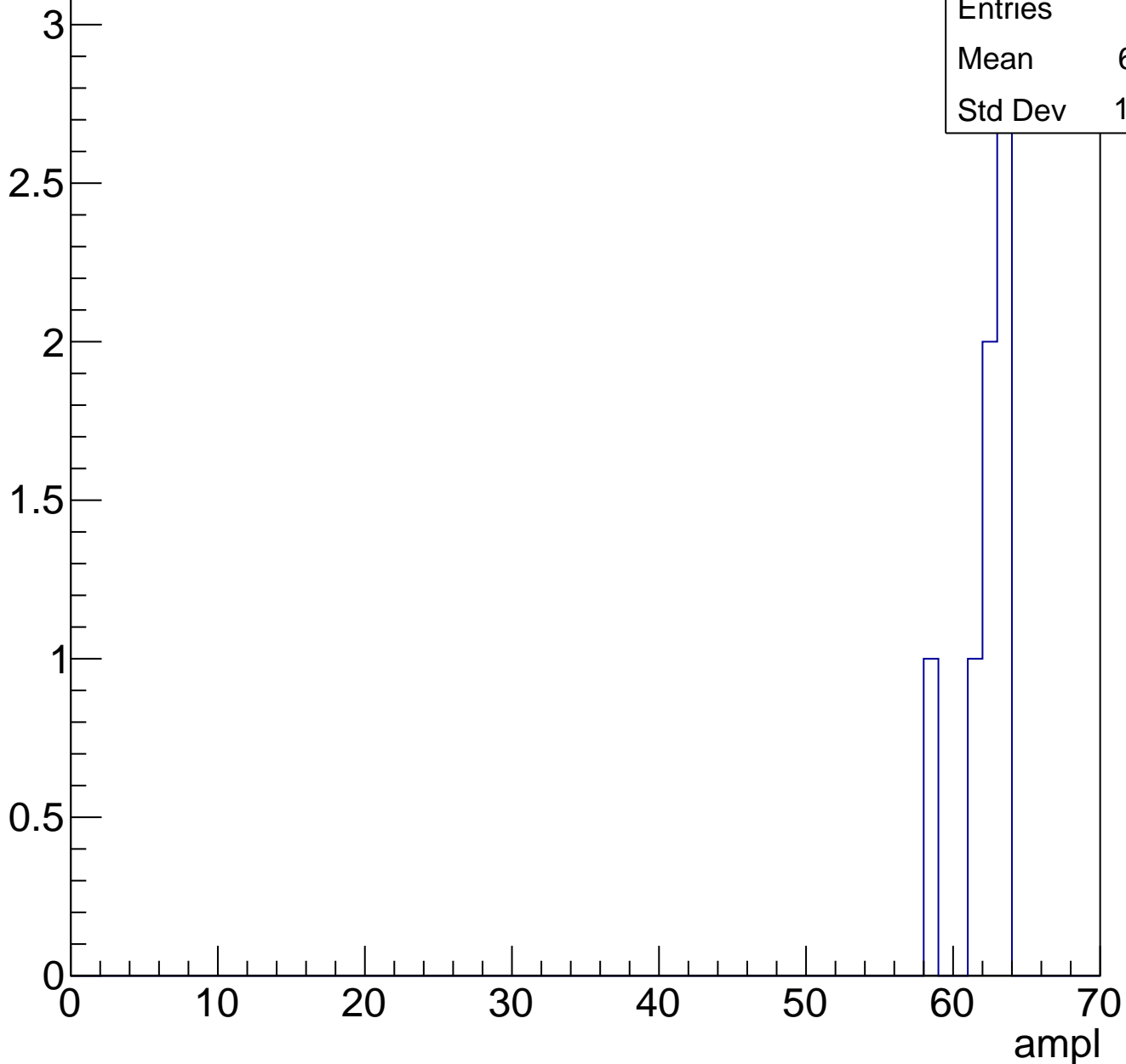
ampl



B0L101S, U21-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch20, adc0

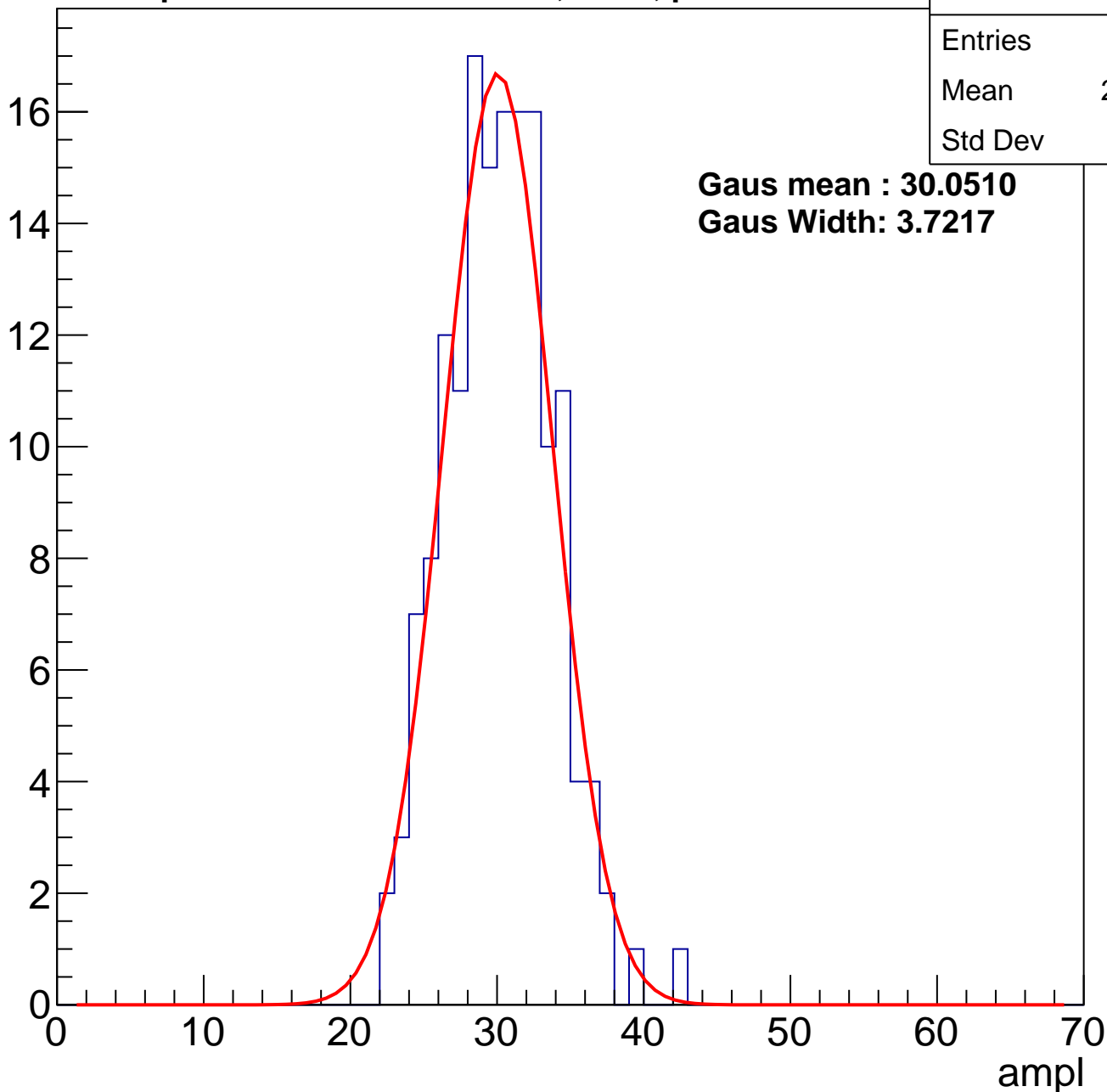
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	29.69
Std Dev	3.58

Gaus mean : 30.0510

Gaus Width: 3.7217

Entry



B0L101S, U21-ch20, adc1

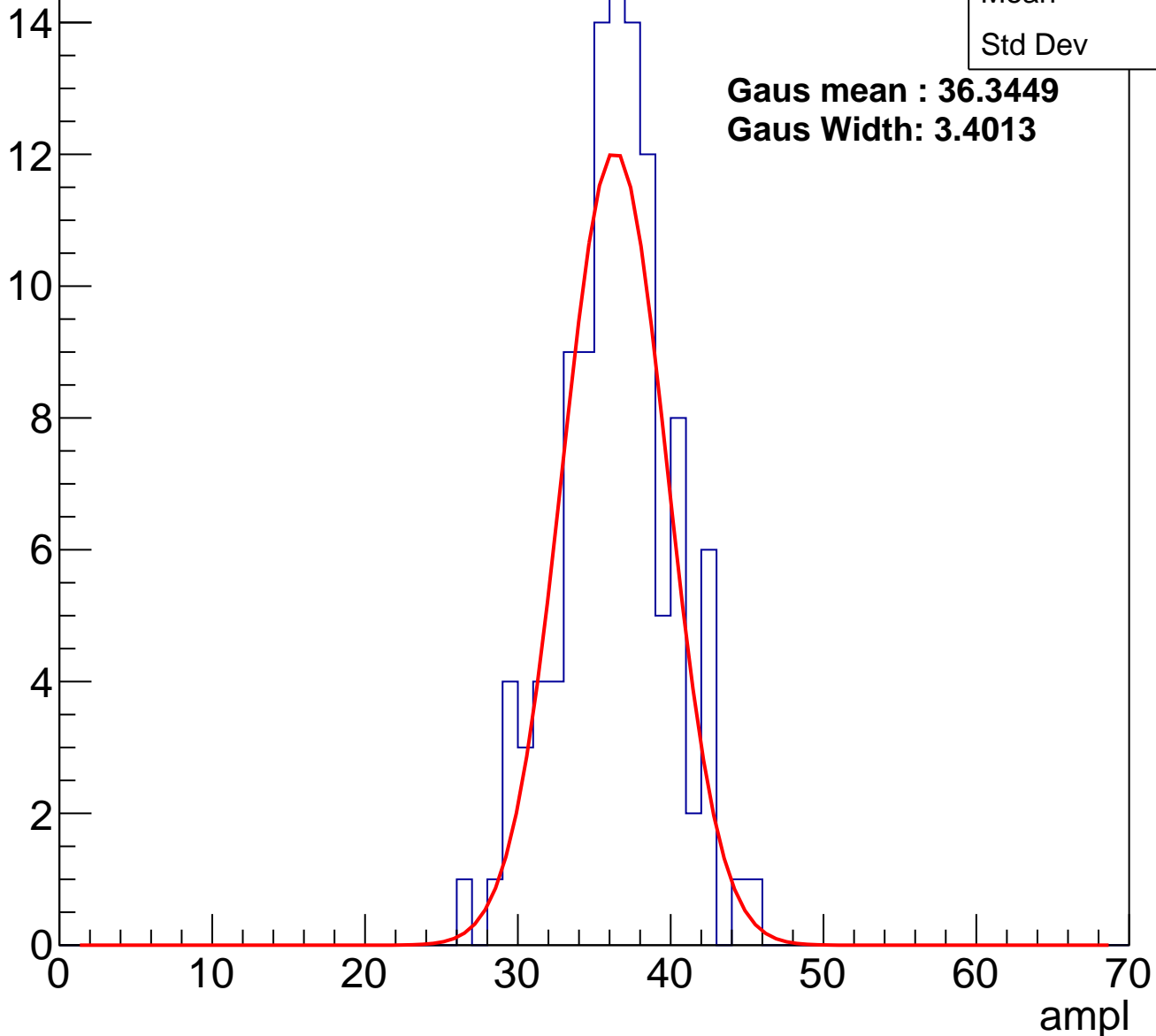
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	35.9
Std Dev	3.53

Gaus mean : 36.3449

Gaus Width: 3.4013

Entry



B0L101S, U21-ch20, adc2

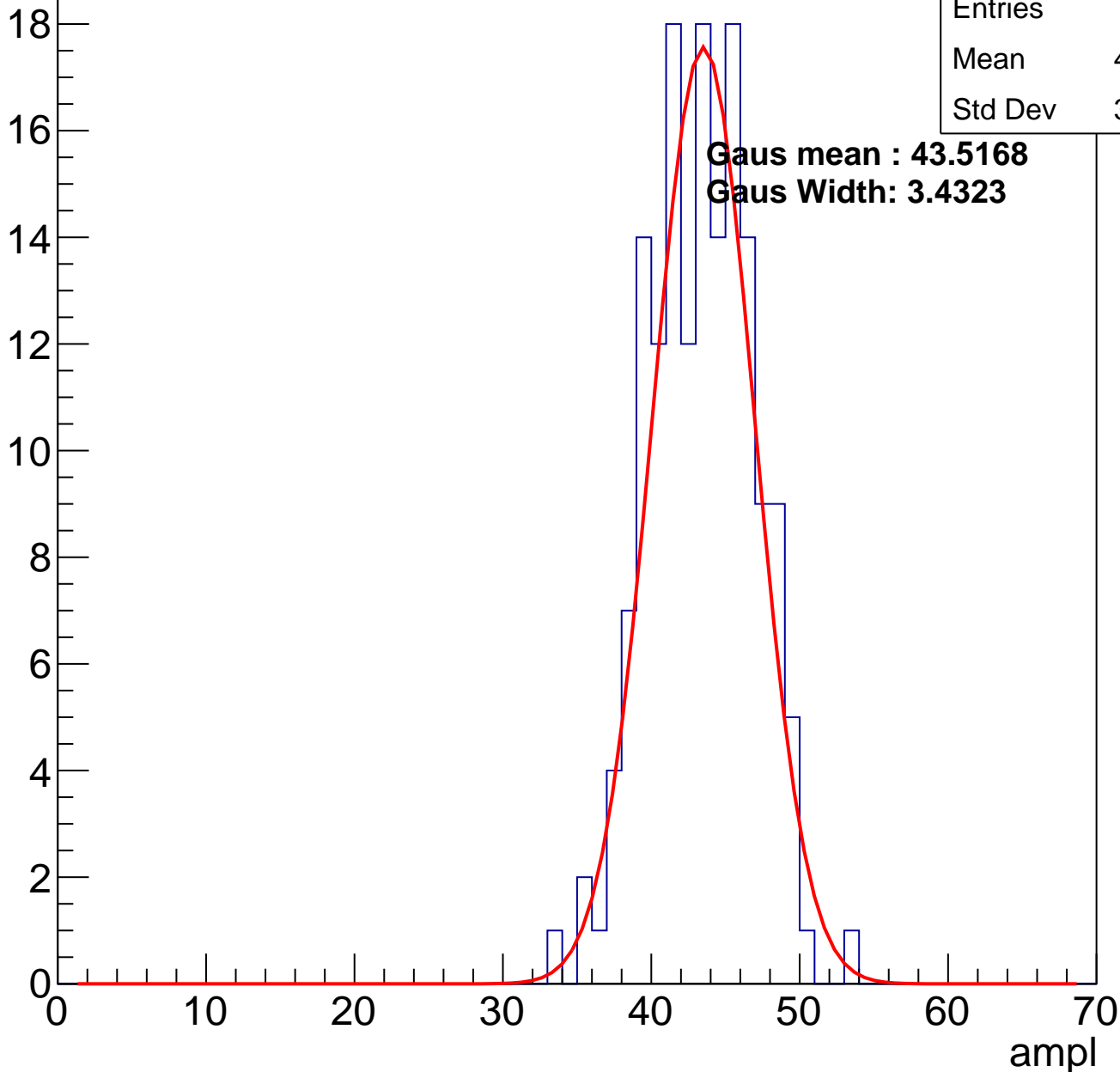
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	42.92
Std Dev	3.465

Gaus mean : 43.5168

Gaus Width: 3.4323

Entry

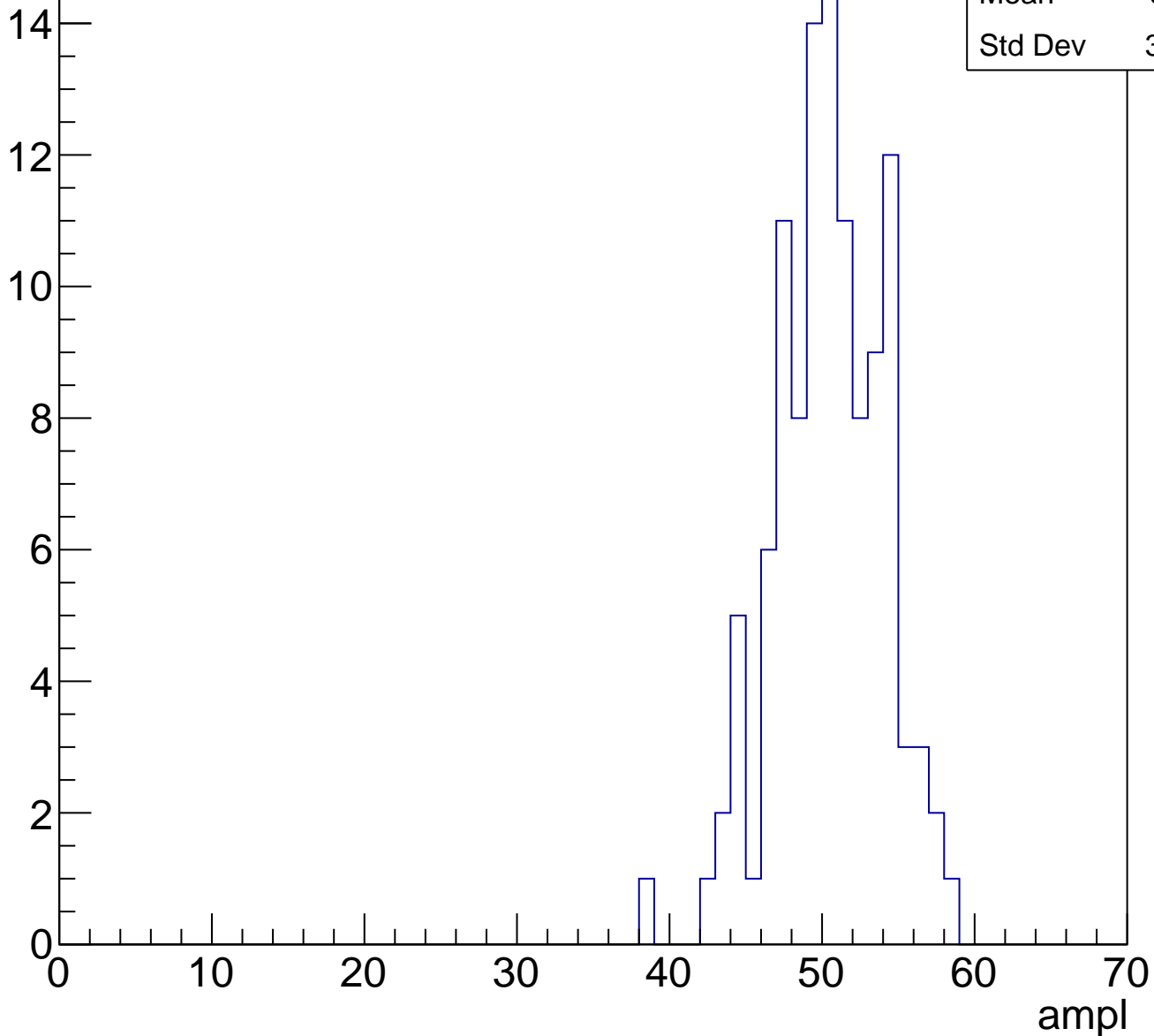


B0L101S, U21-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	50.01
Std Dev	3.536

Entry

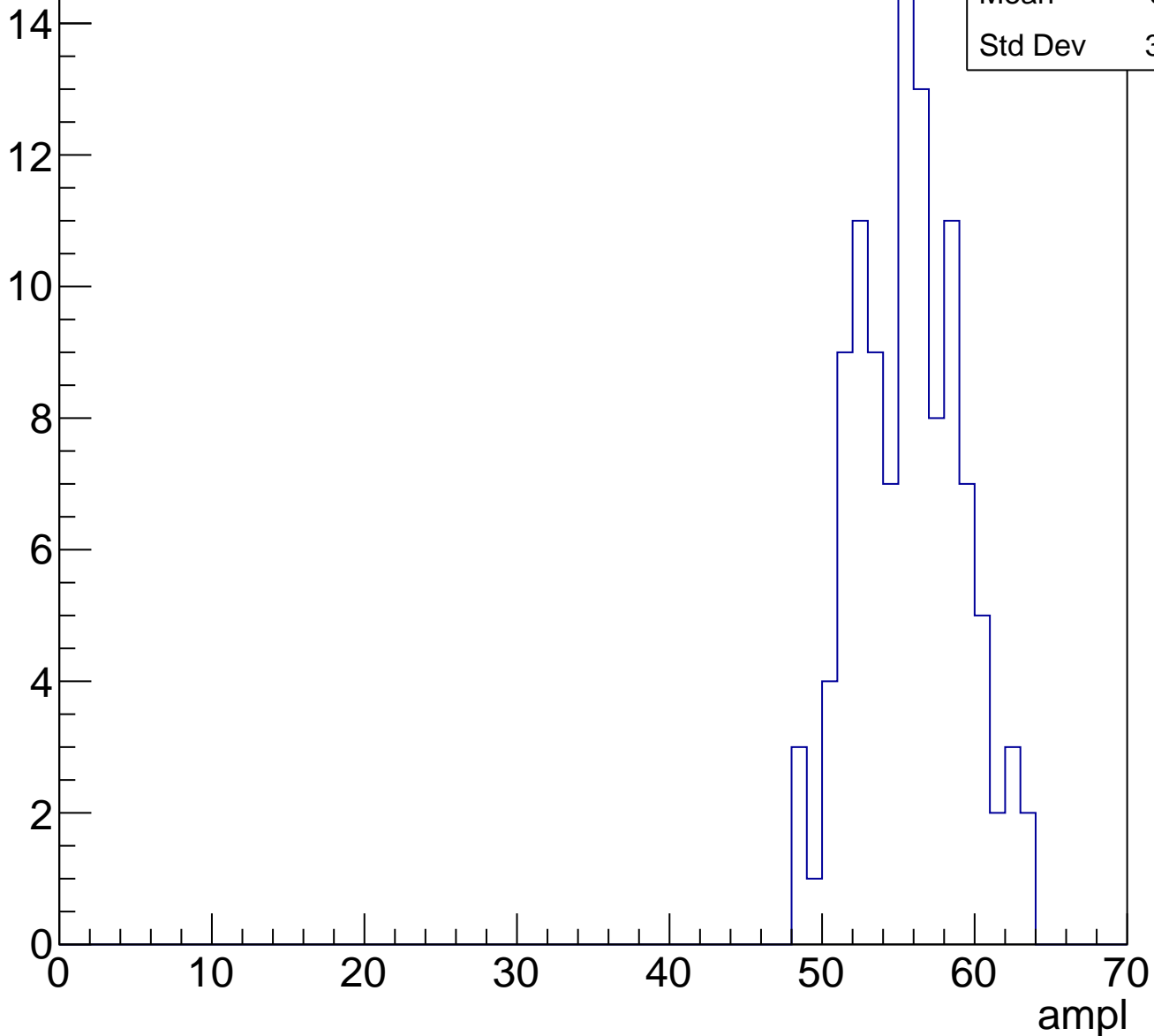


B0L101S, U21-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	55.21
Std Dev	3.427

Entry



B0L101S, U21-ch20, adc5

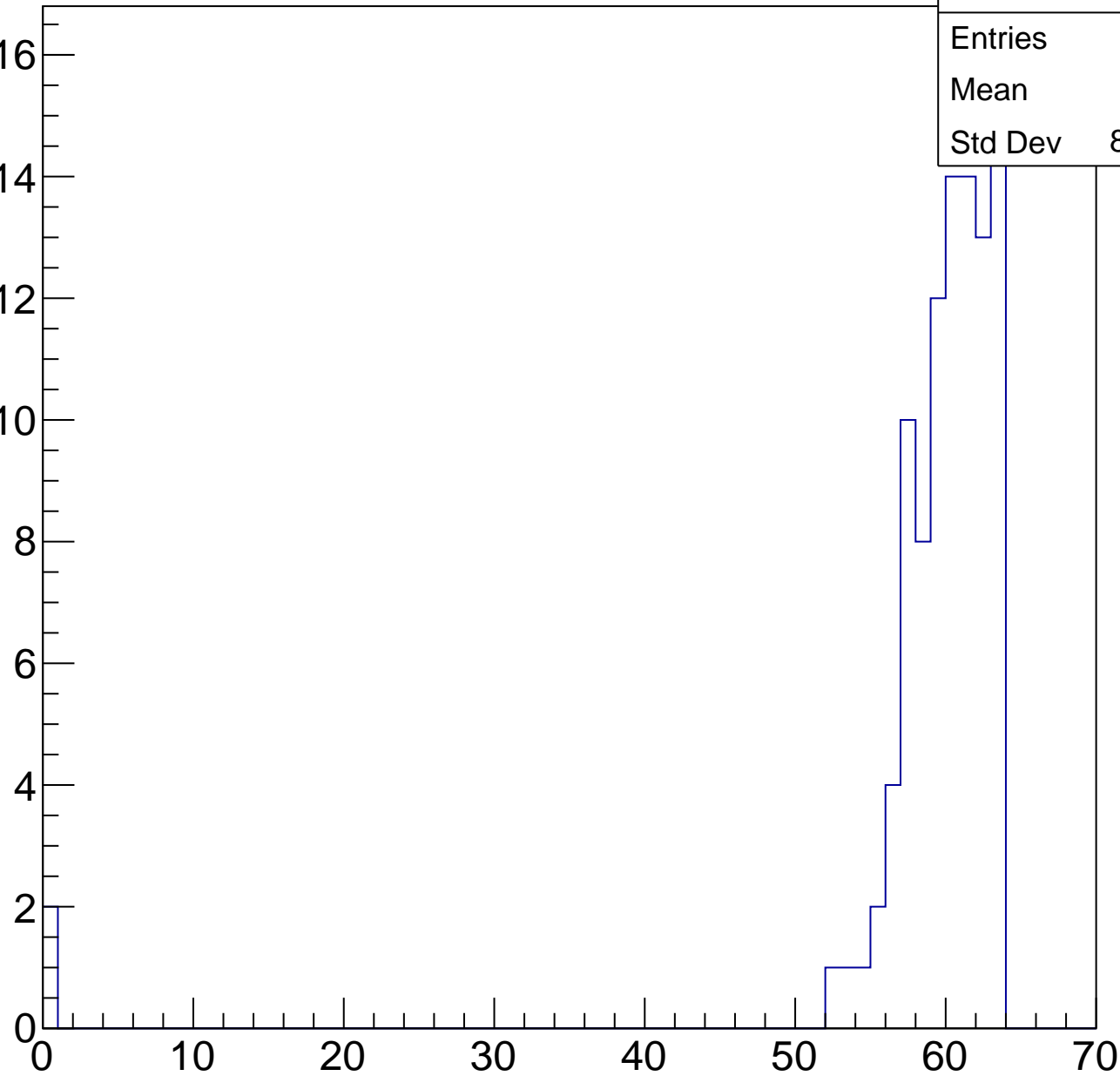
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.6
Std Dev	8.815

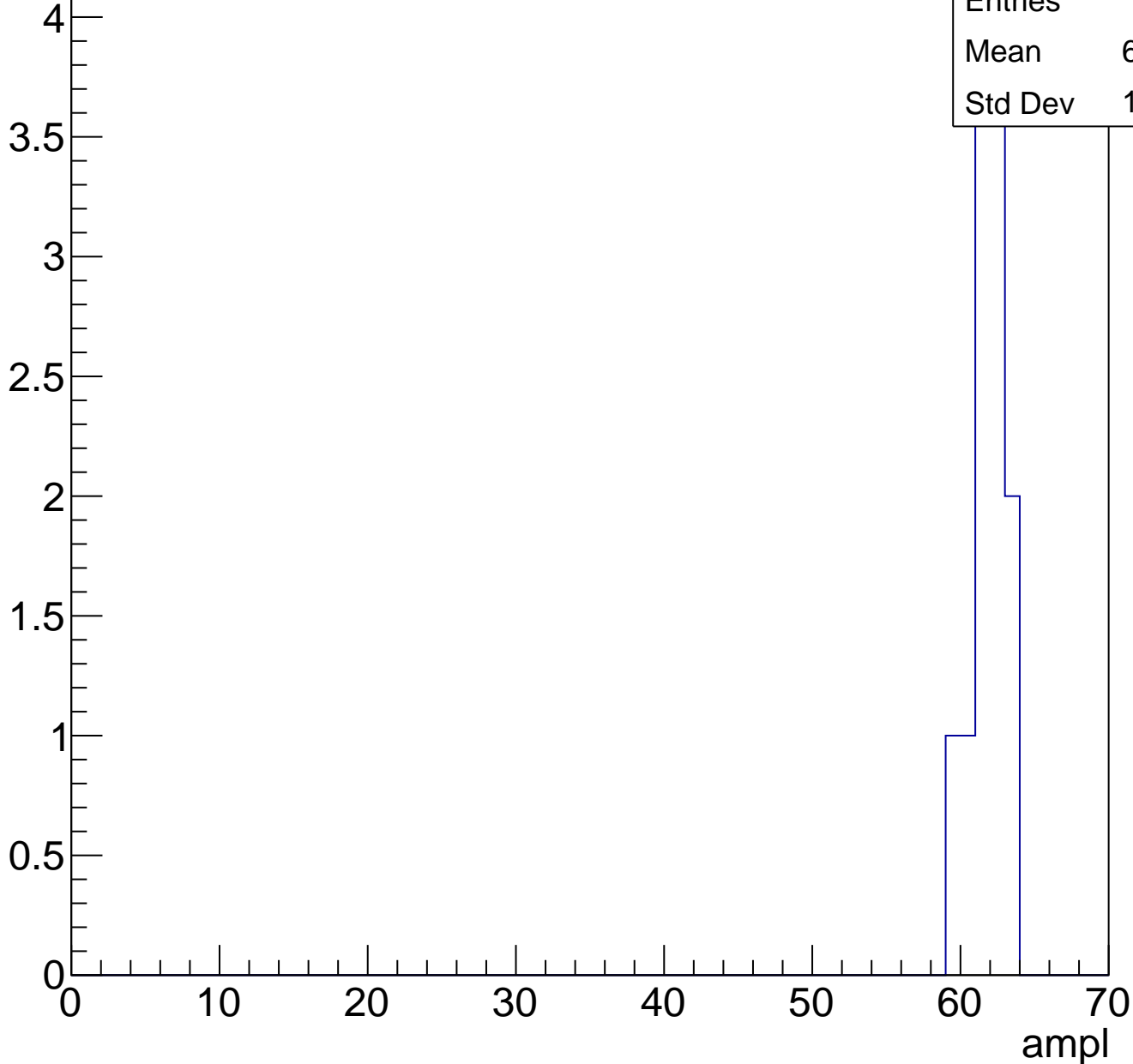
ampl



B0L101S, U21-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U21-ch21, adc0

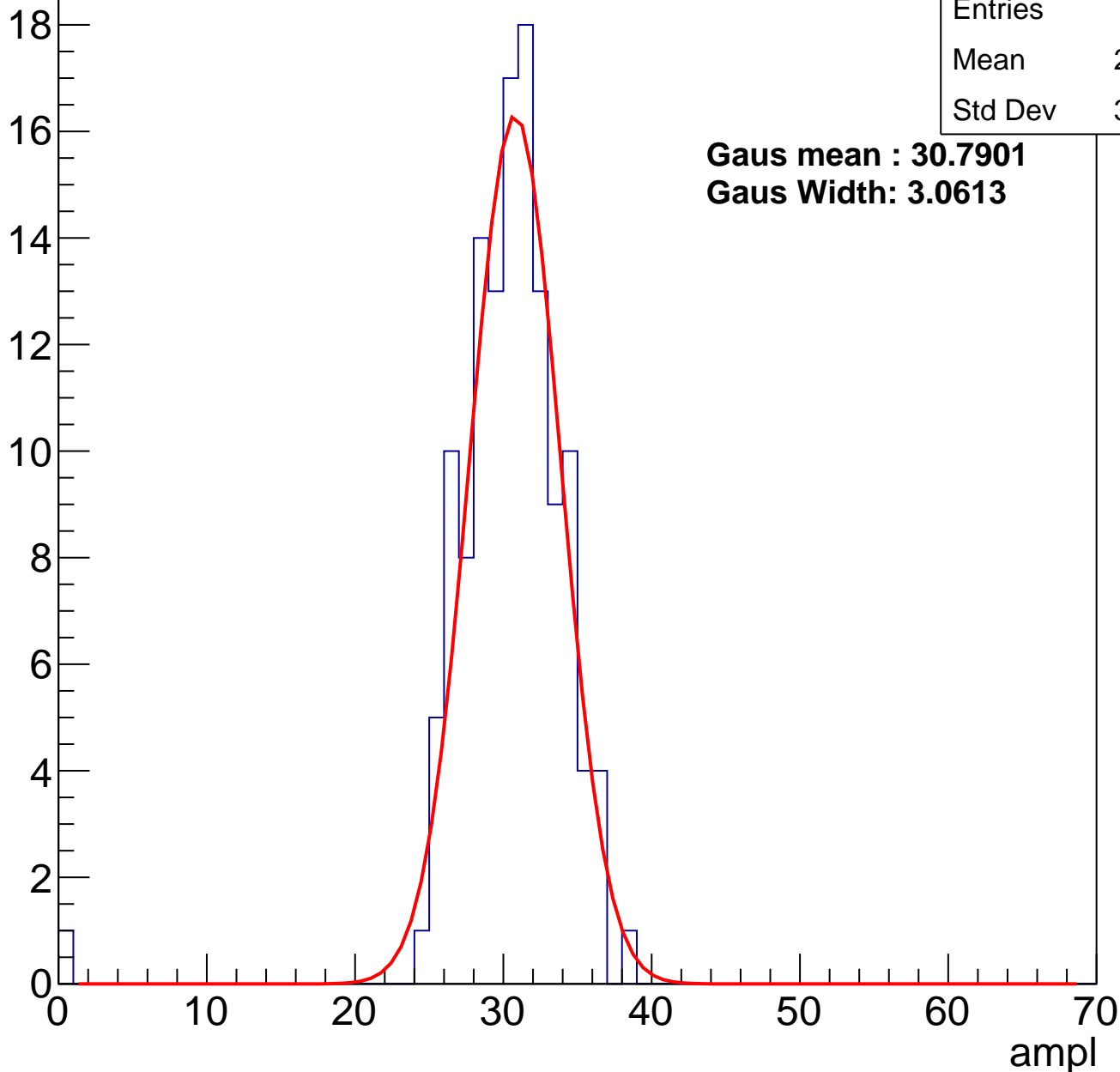
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	29.98
Std Dev	3.934

Gaus mean : 30.7901

Gaus Width: 3.0613

Entry

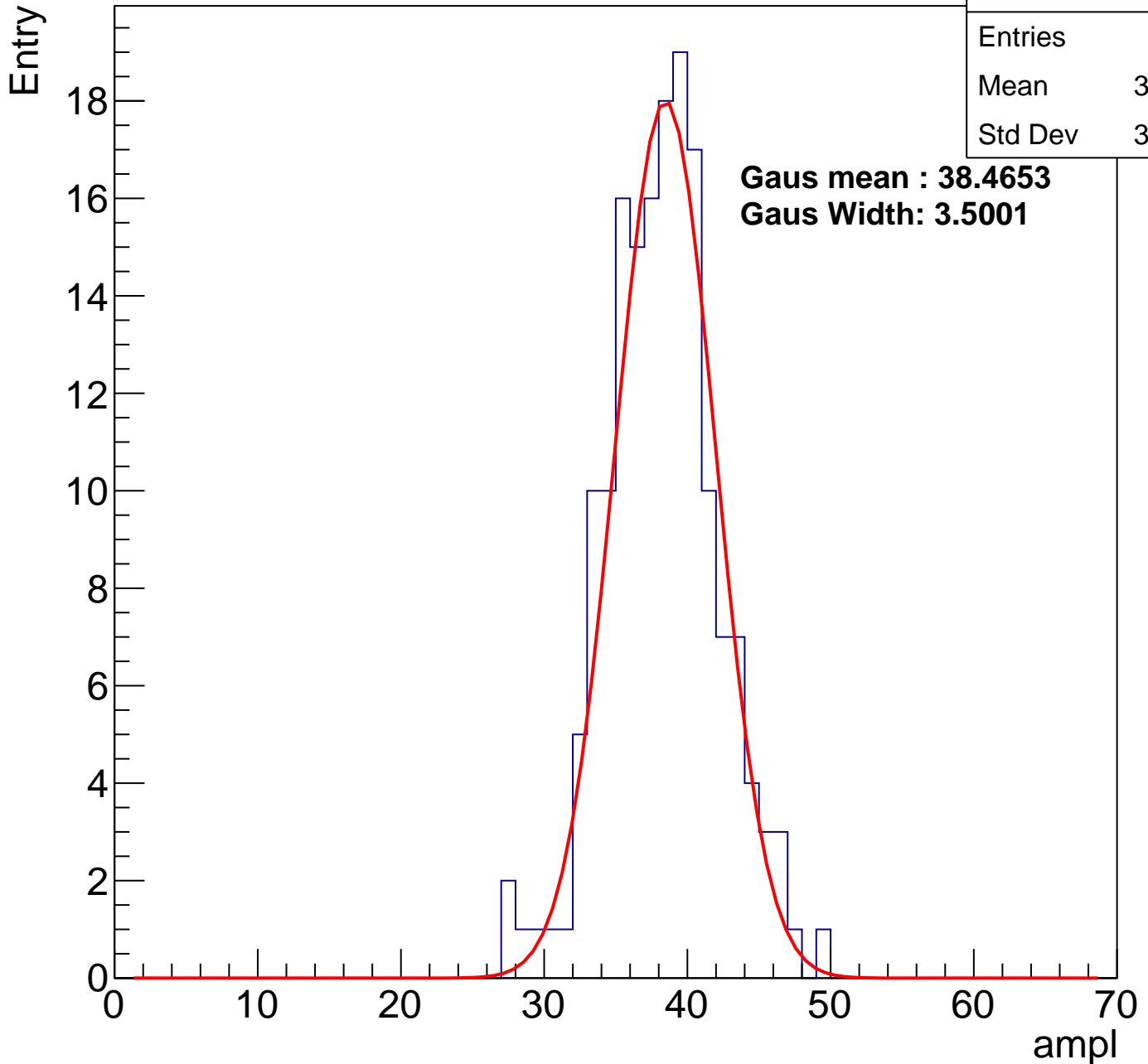


B0L101S, U21-ch21, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	37.79
Std Dev	3.848

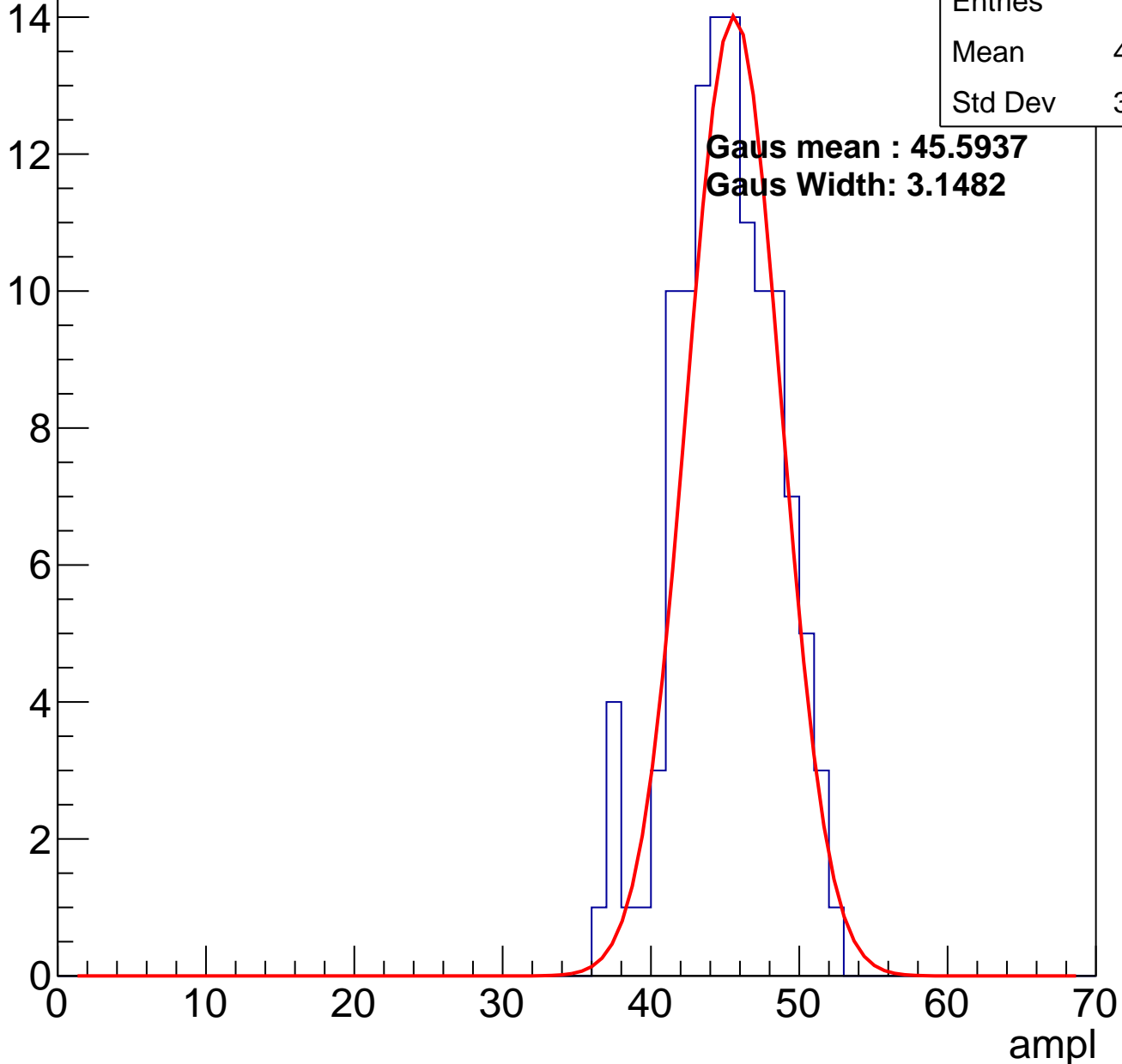
Gaus mean : 38.4653
Gaus Width: 3.5001



B0L101S, U21-ch21, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

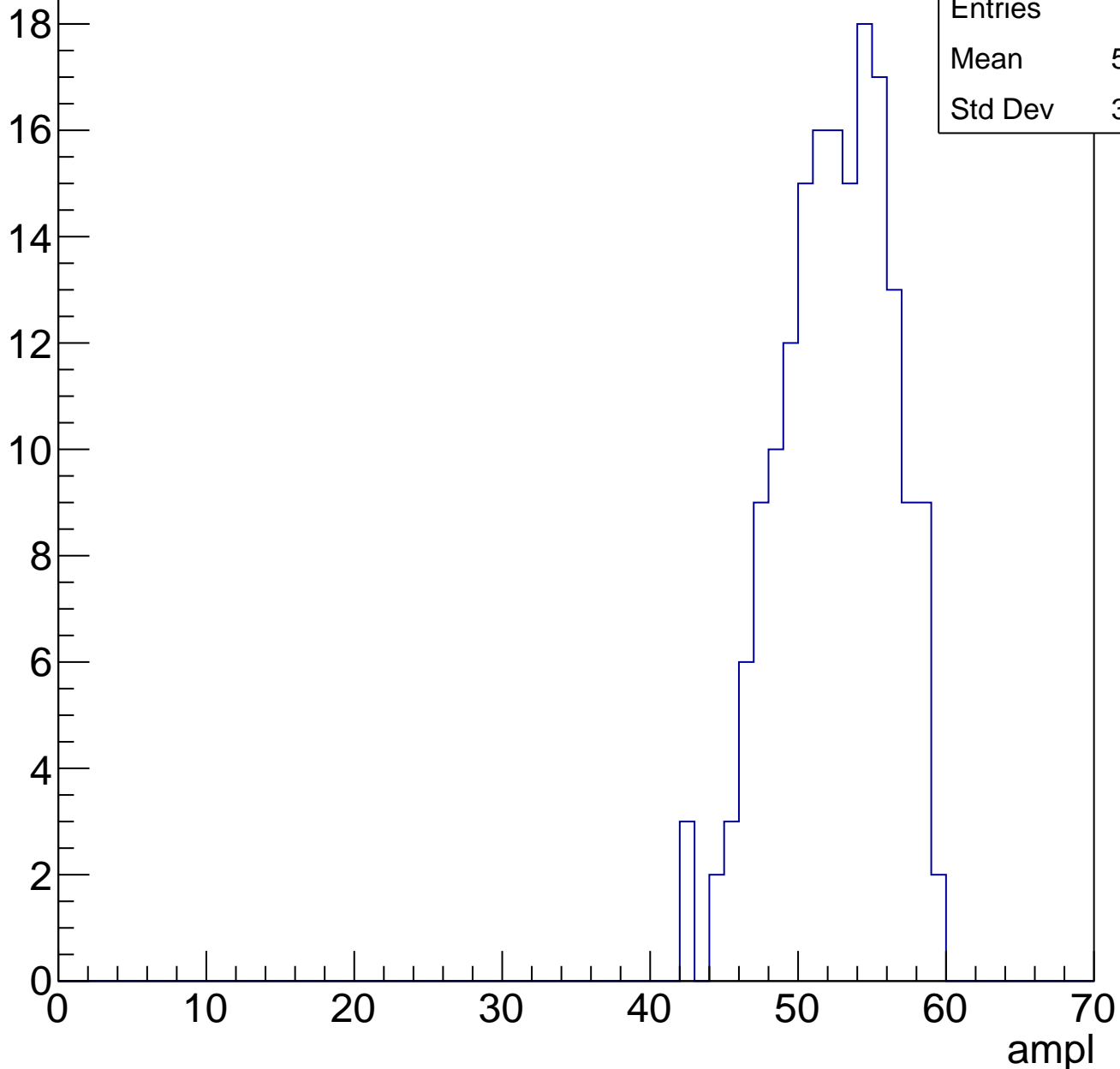


B0L101S, U21-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	51.98
Std Dev	3.745

Entry



B0L101S, U21-ch21, adc4

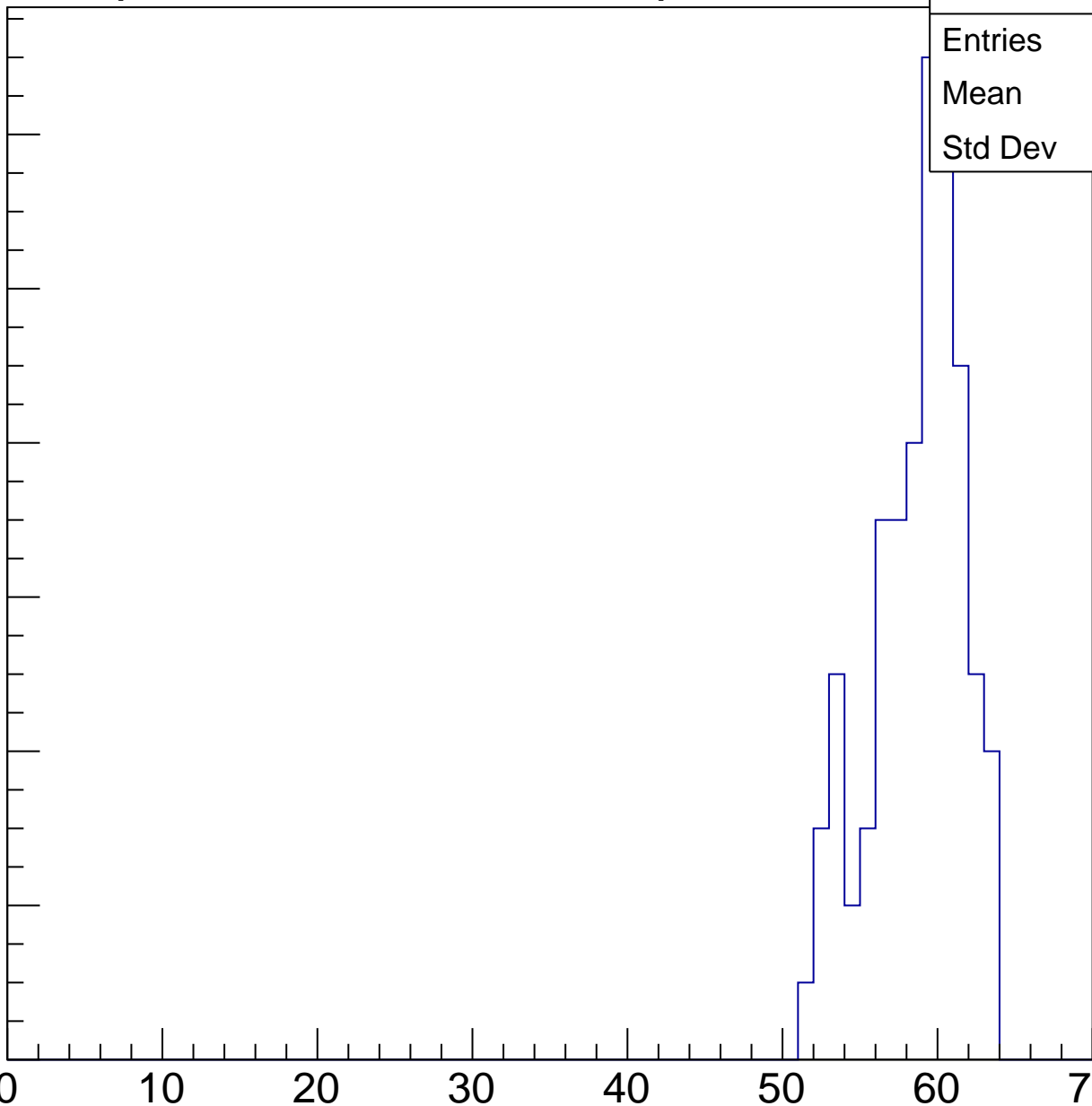
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	79
Mean	58.2
Std Dev	2.966

ampl



B0L101S, U21-ch21, adc5

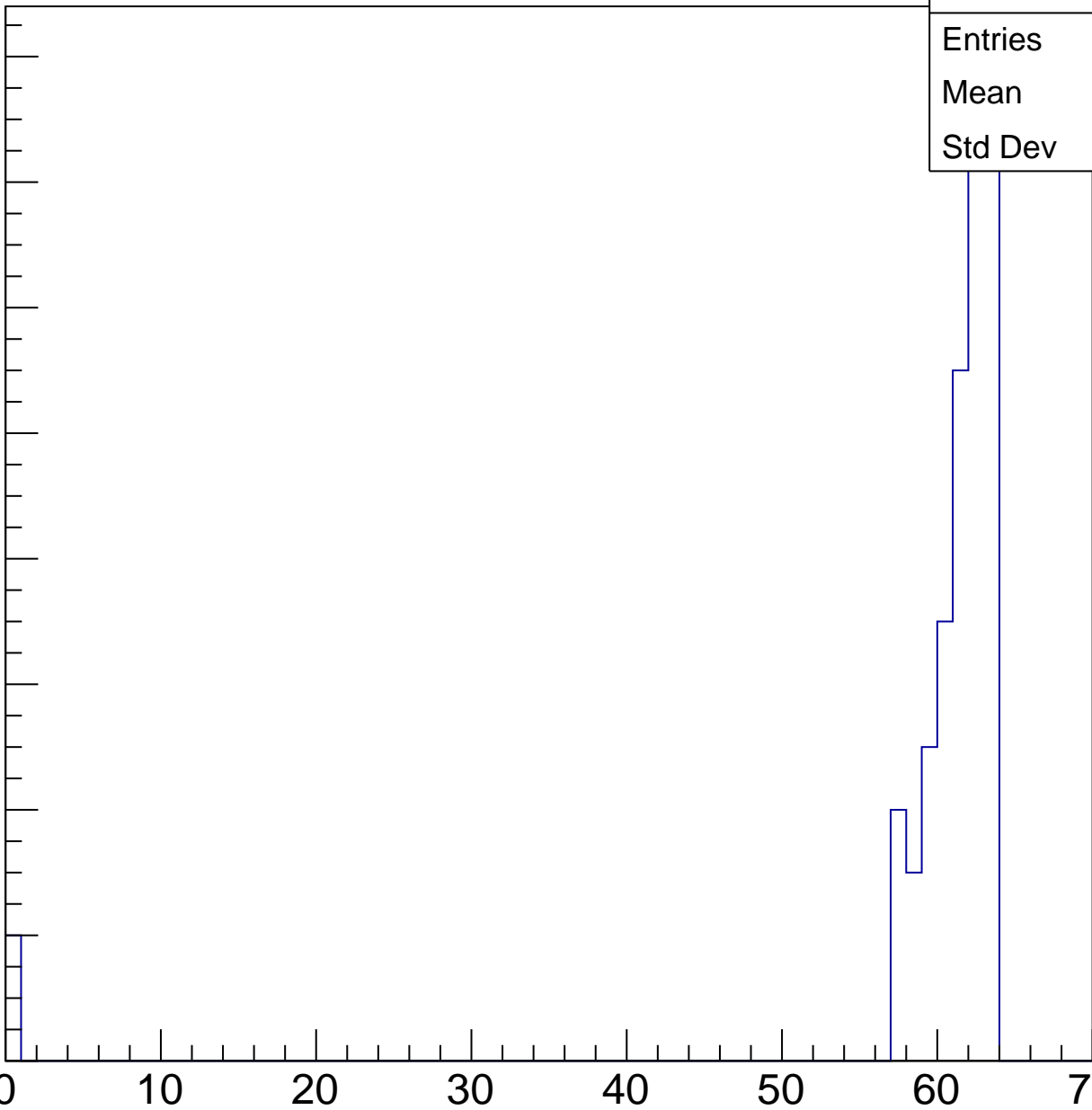
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	63
Mean	59.14
Std Dev	10.85

ampl



B0L101S, U21-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L101S, U21-ch22, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.45
Std Dev	3.647

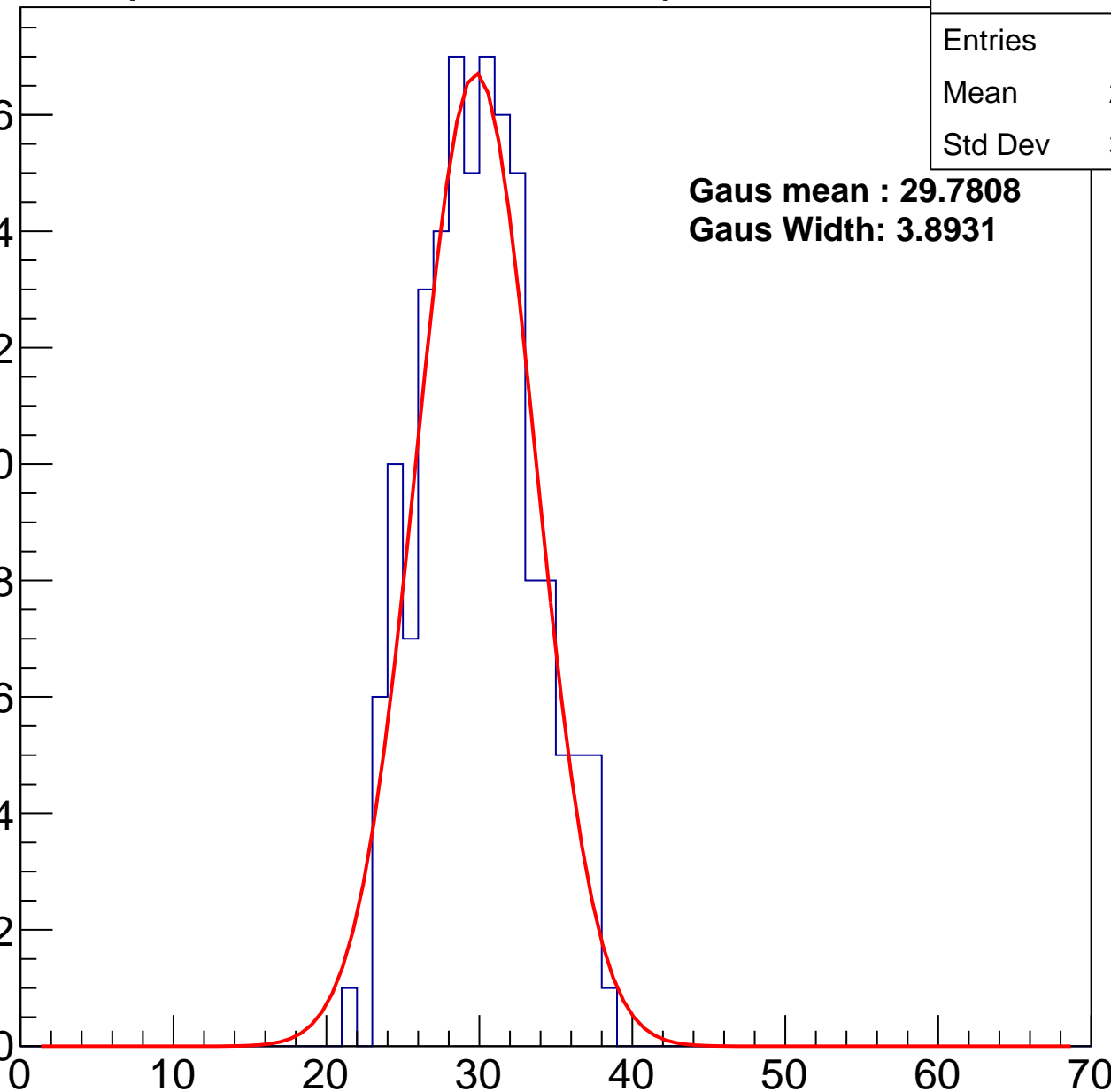
Gaus mean : 29.7808

Gaus Width: 3.8931

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch22, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

152

Mean

37.16

Std Dev

3.612

Gaus mean : 37.7022

Gaus Width: 3.5642

0

10

20

30

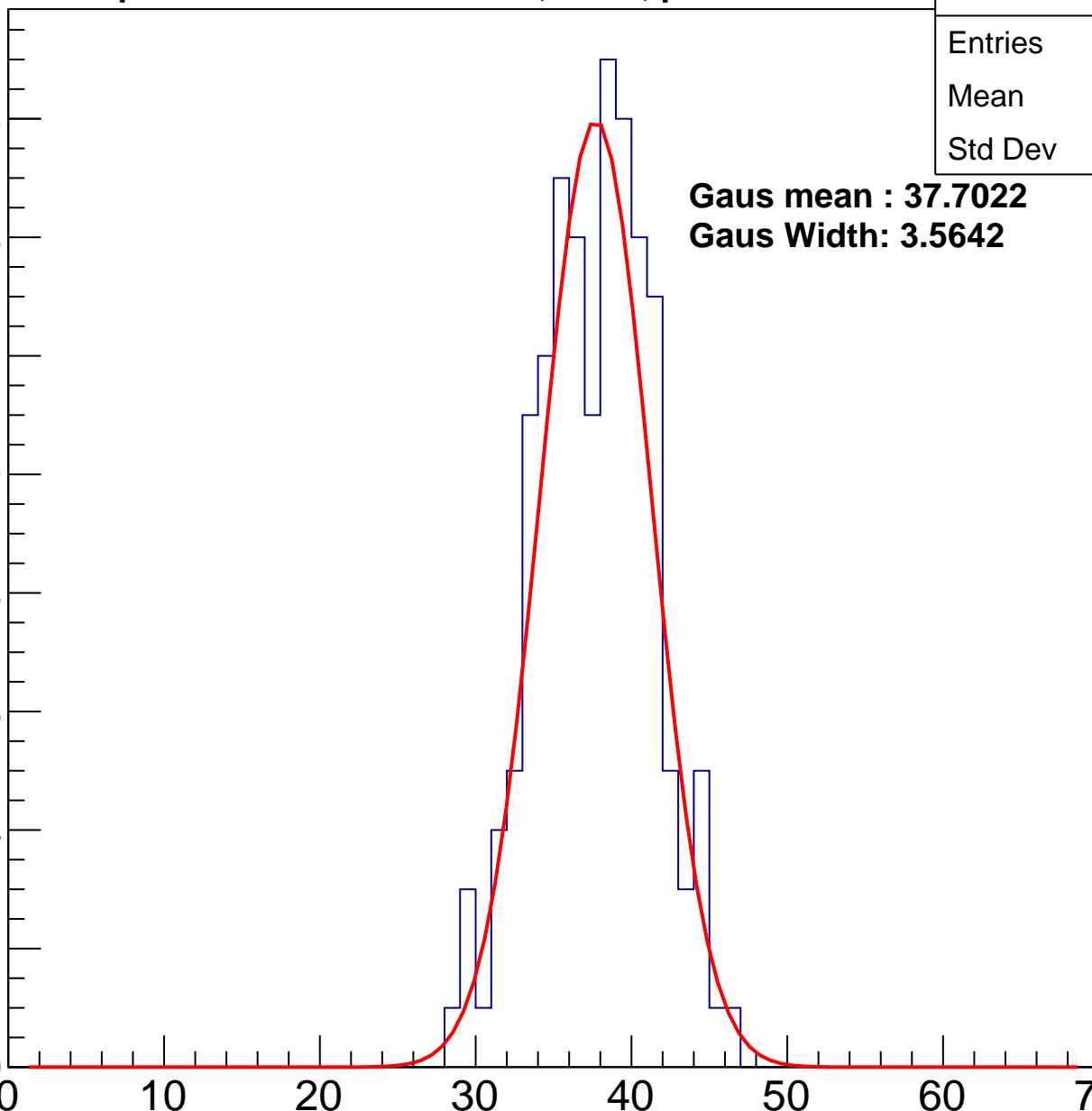
40

50

60

70

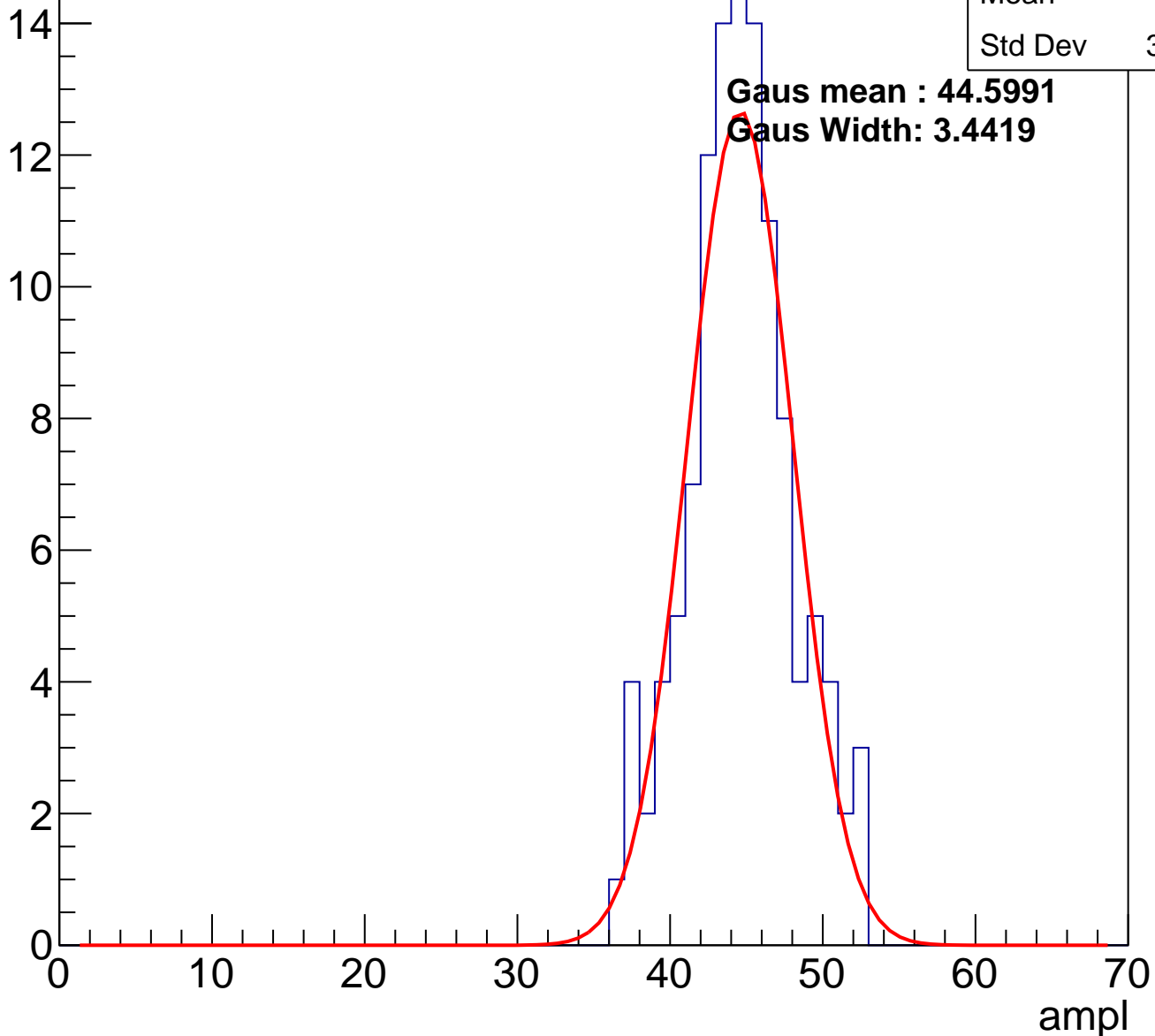
ampl



B0L101S, U21-ch22, adc2

calib_packv5_042523_0143.root, FC#1, port C1

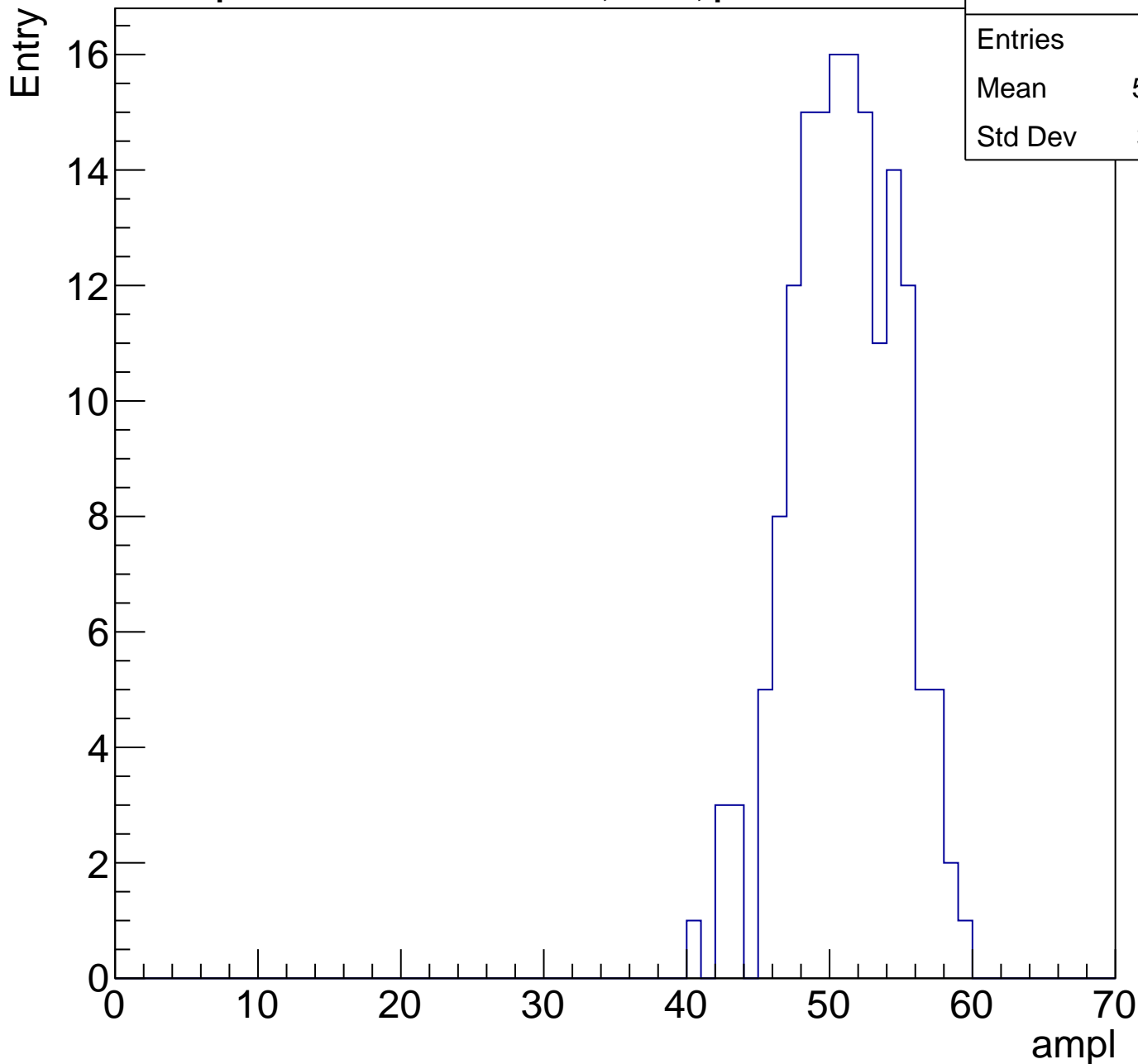
Entry



B0L101S, U21-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

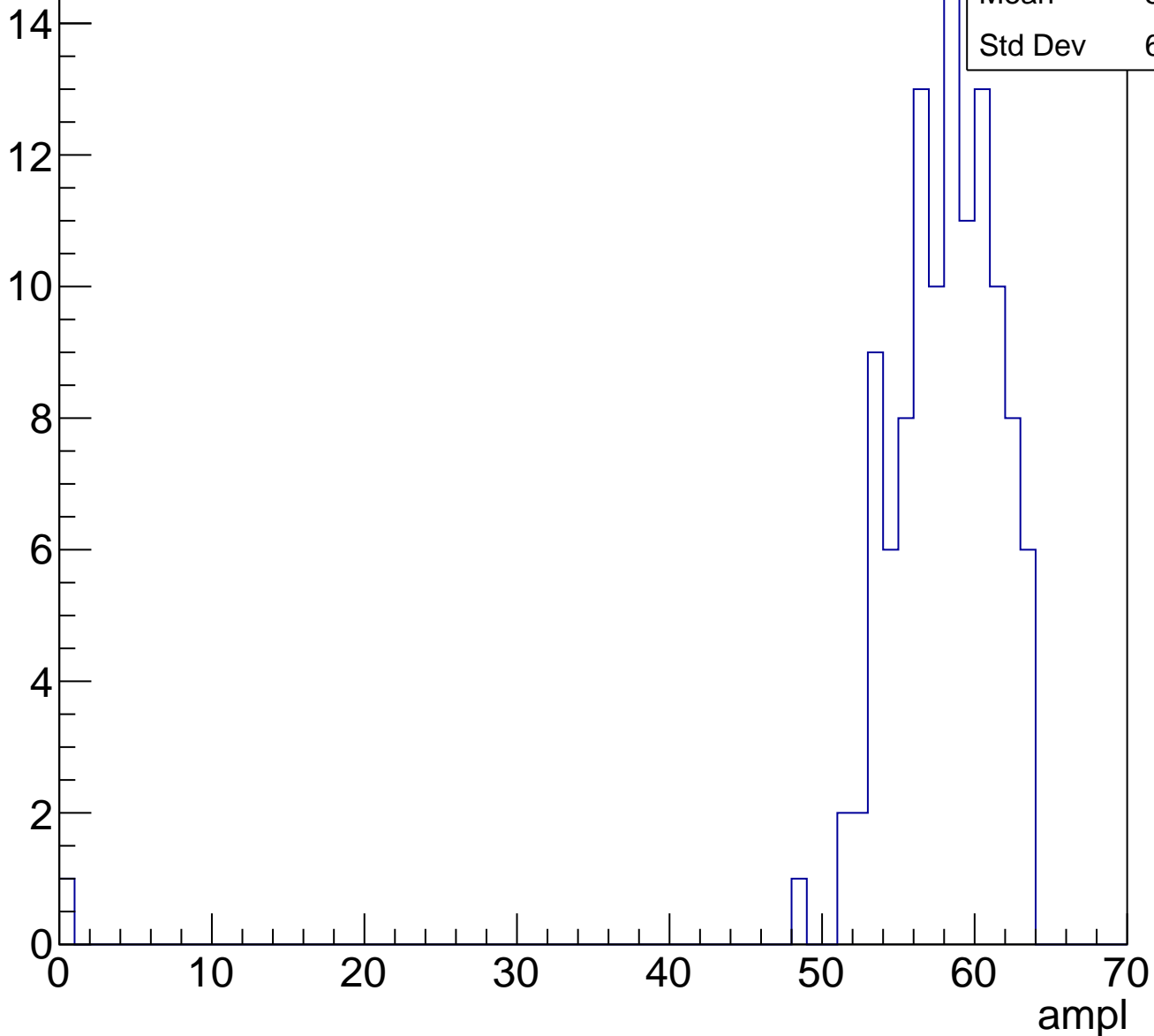
Entries	159
Mean	50.58
Std Dev	3.681



B0L101S, U21-ch22, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

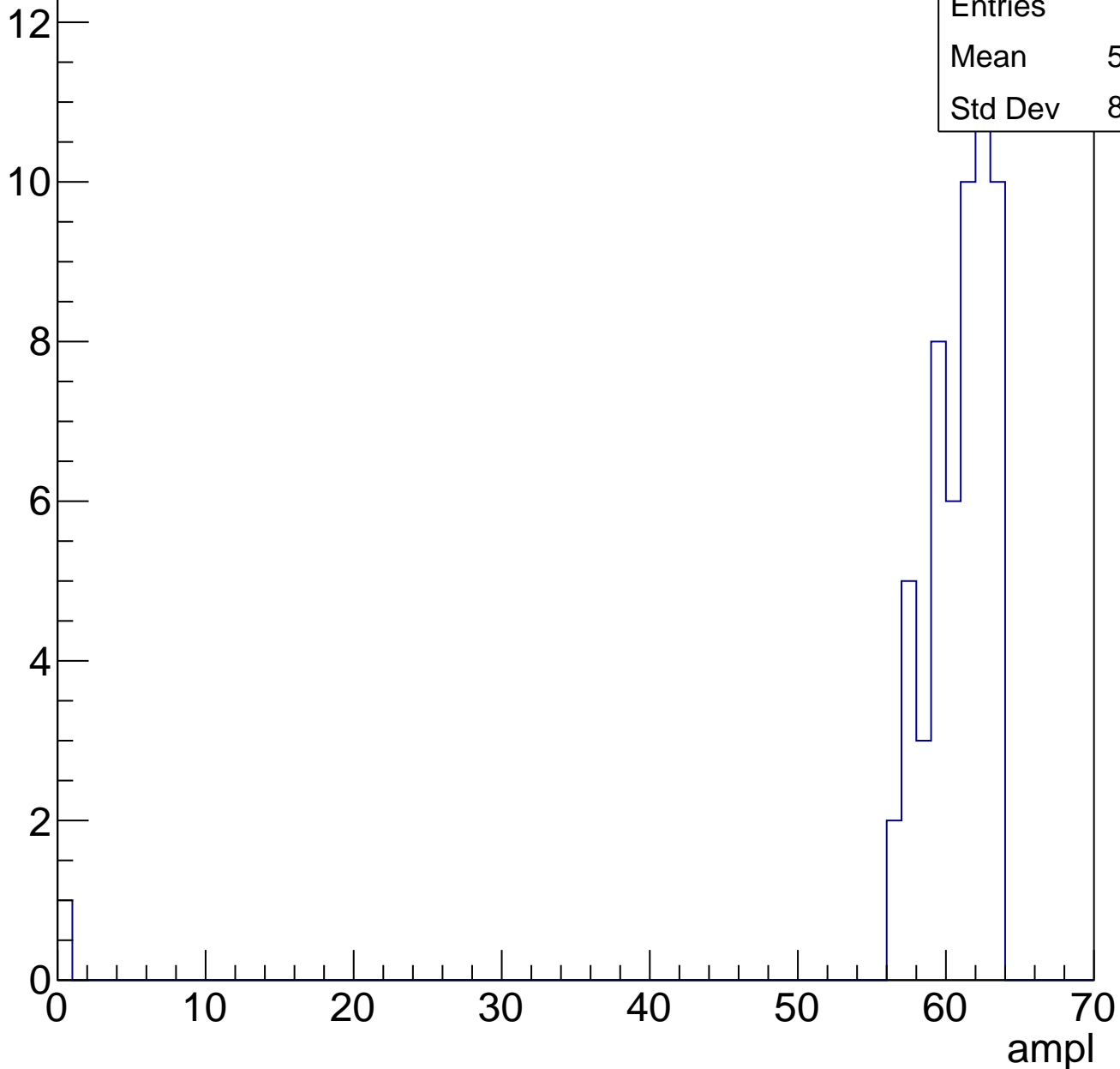


B0L101S, U21-ch22, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	57
Mean	59.42
Std Dev	8.193

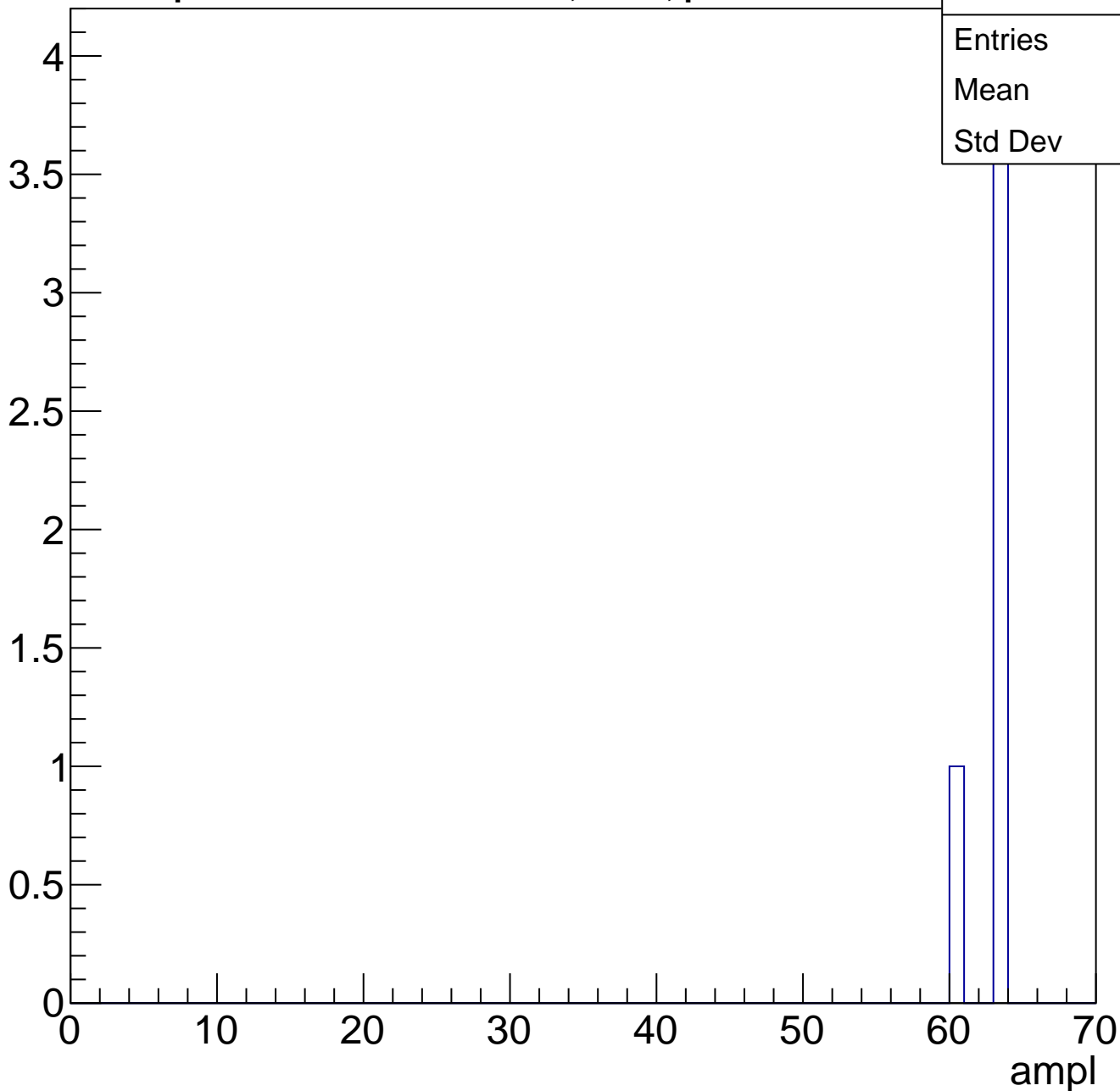
Entry



B0L101S, U21-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

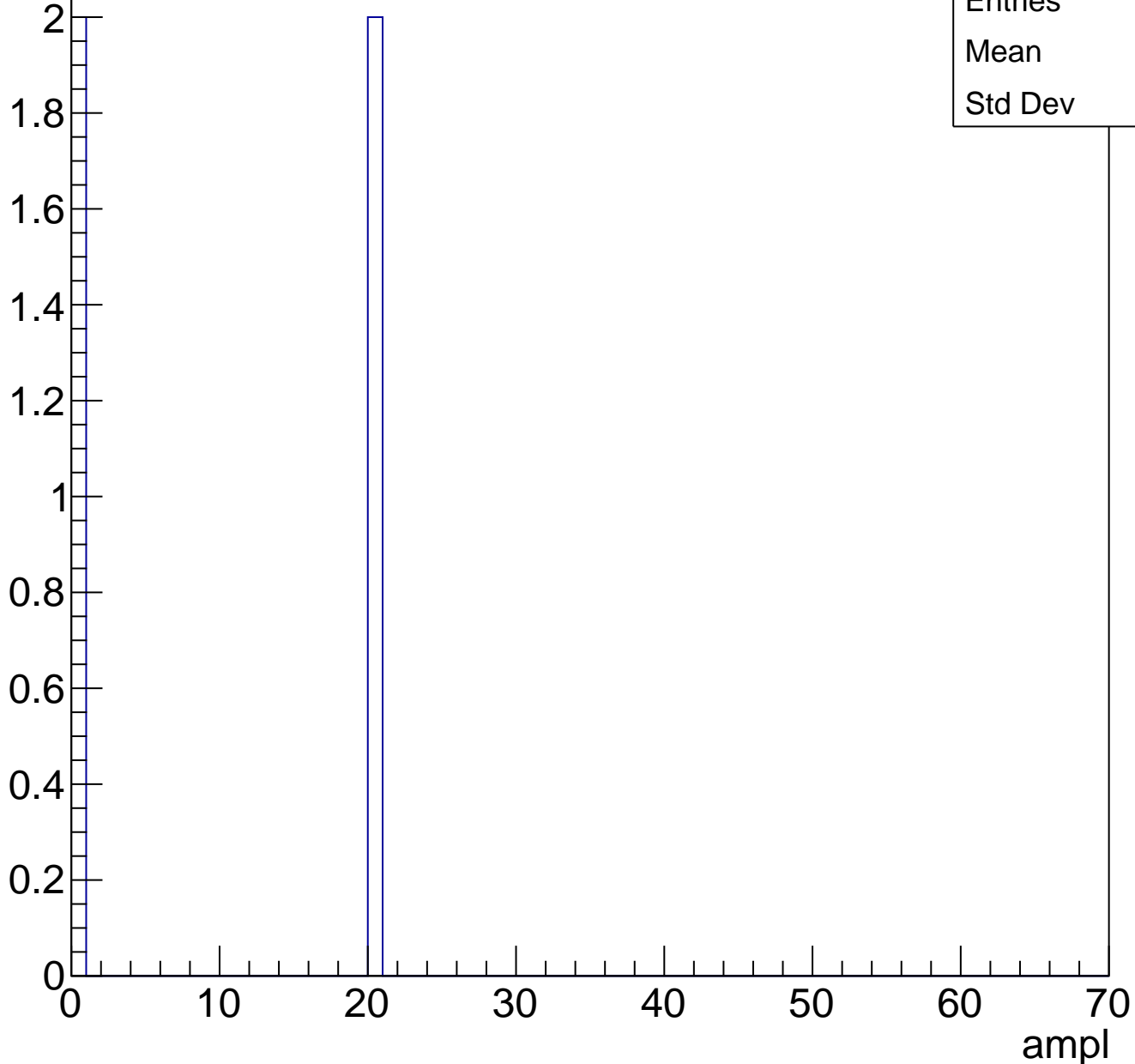
Entry



B0L101S, U21-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	10
Std Dev	10

B0L101S, U21-ch23, adc0

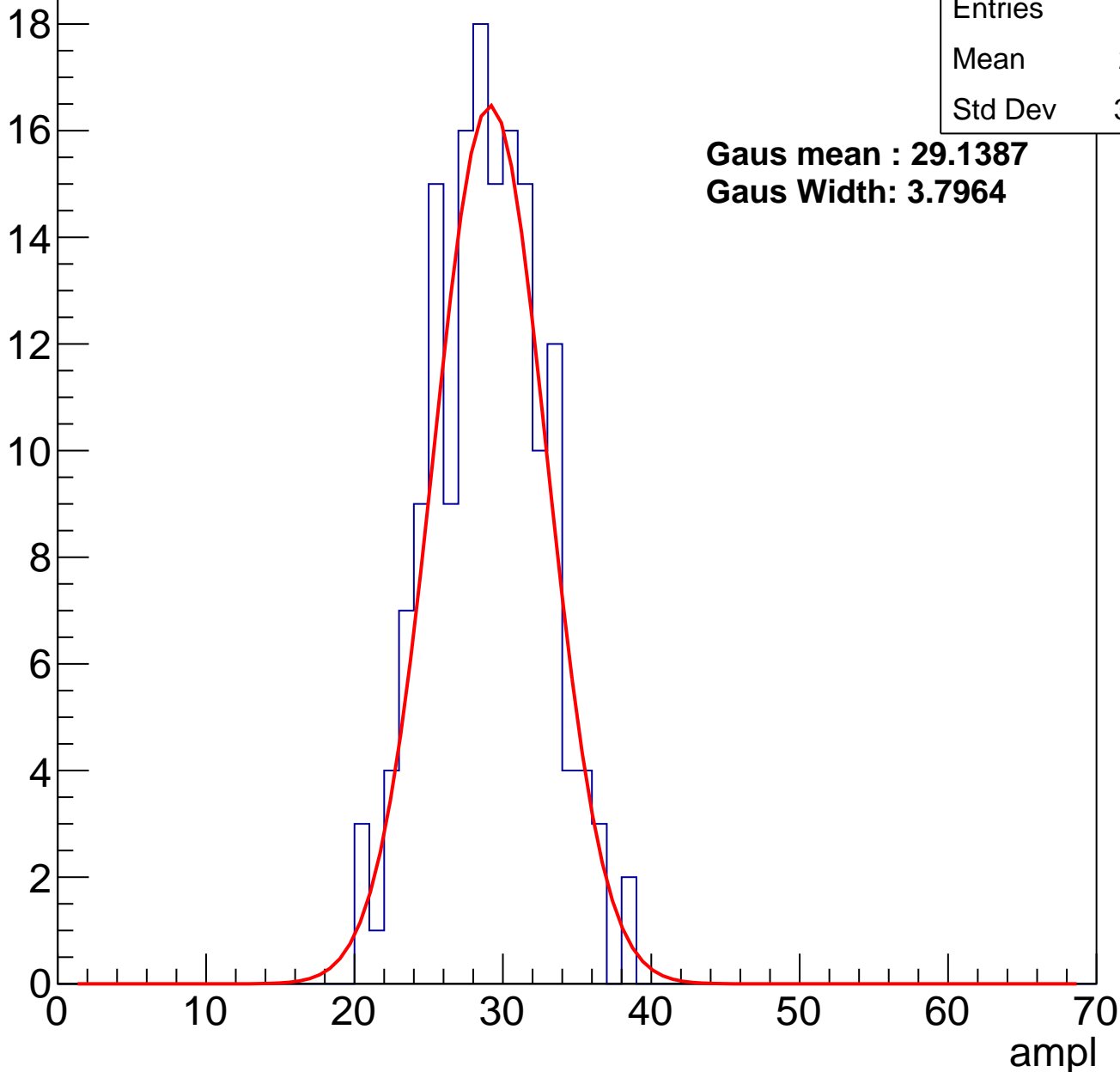
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	28.51
Std Dev	3.719

Gaus mean : 29.1387

Gaus Width: 3.7964

Entry



B0L101S, U21-ch23, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	35.71
Std Dev	3.363

Gaus mean : 36.4274

Gaus Width: 3.8129

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	42.19
Std Dev	3.951

Gaus mean : 42.5970

Gaus Width: 3.7375

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

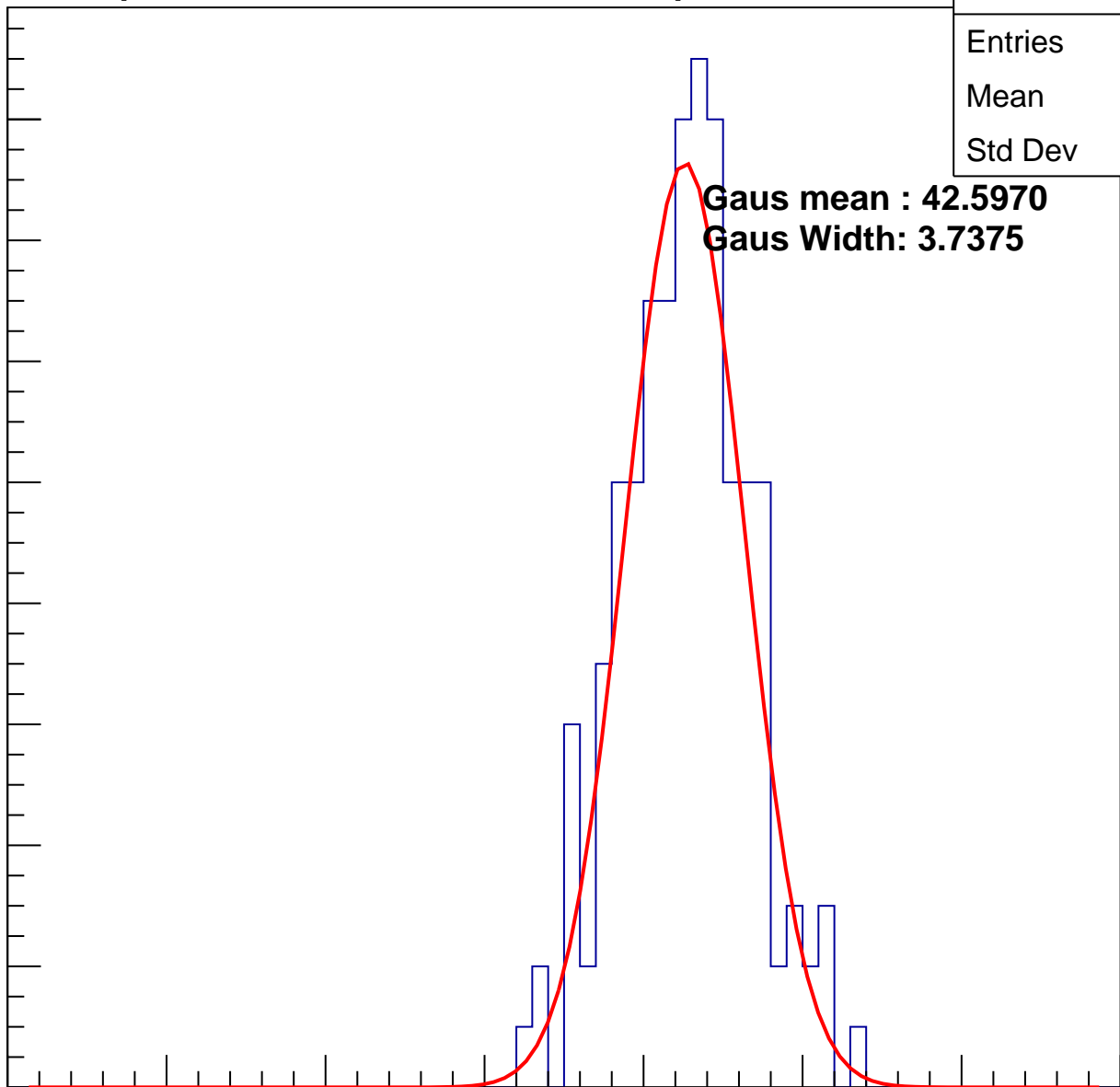
40

50

60

70

ampl

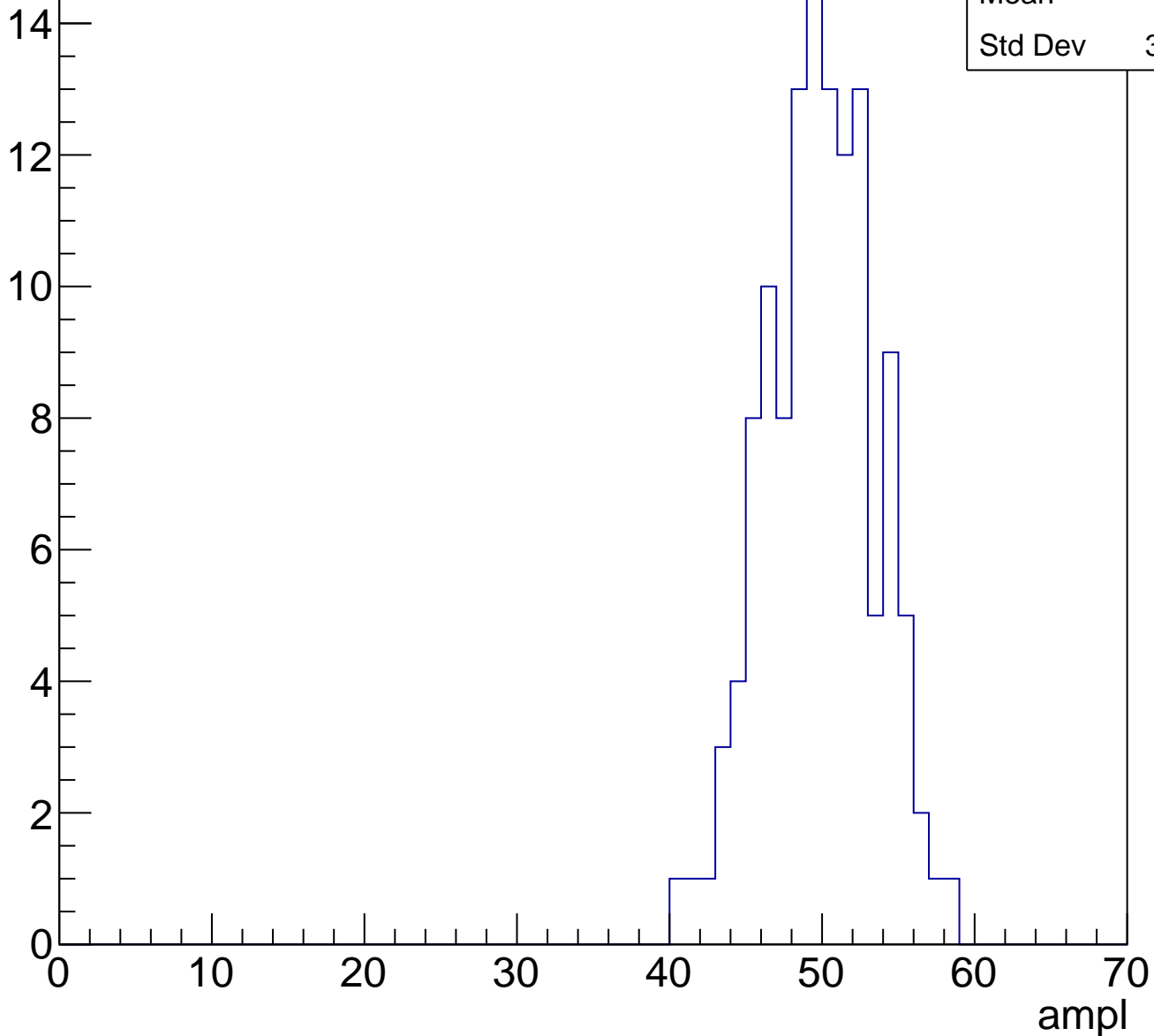


B0L101S, U21-ch23, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	49.39
Std Dev	3.523

Entry



B0L101S, U21-ch23, adc4

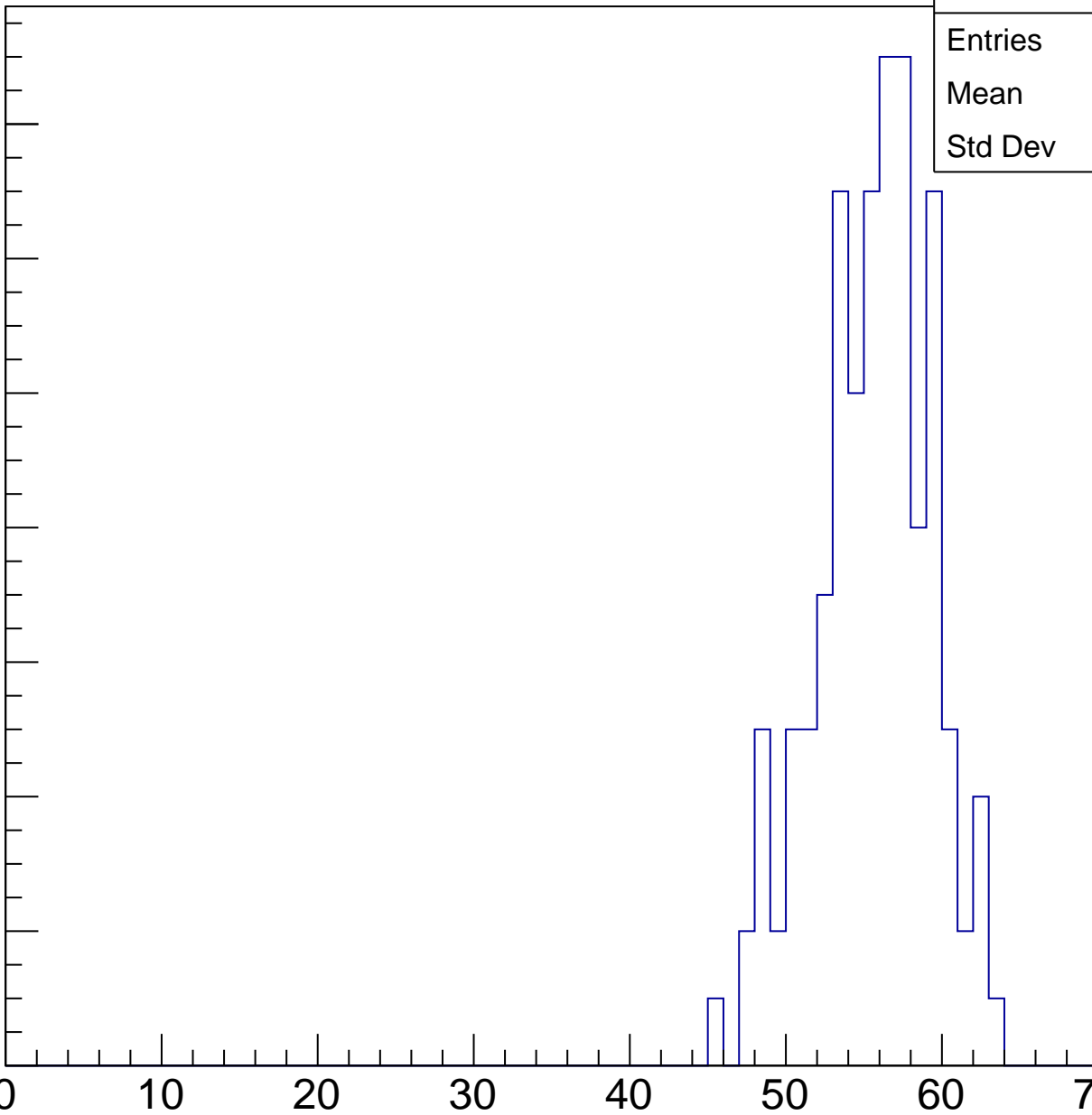
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	55.15
Std Dev	3.656

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch23, adc5

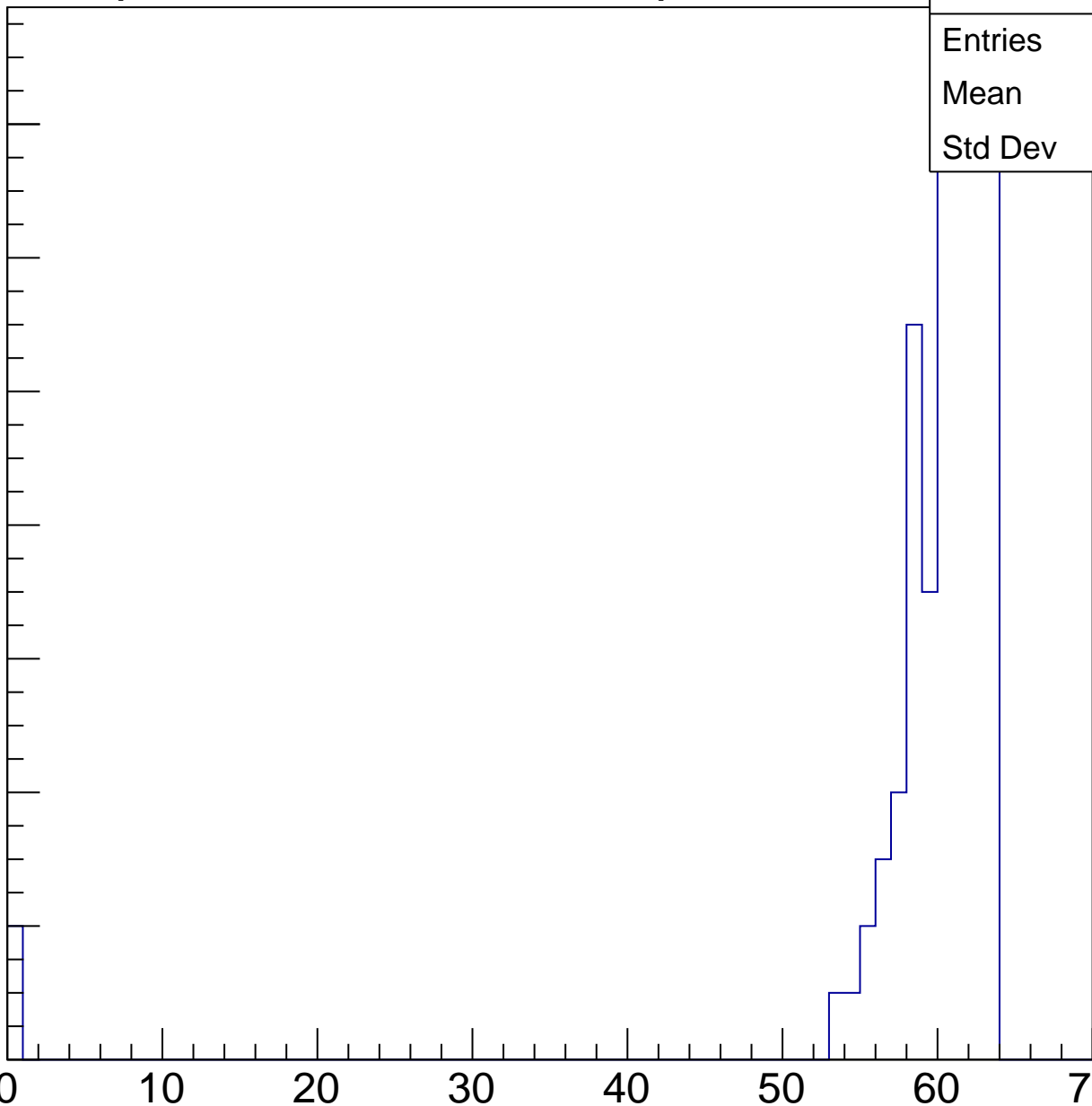
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	58.75
Std Dev	9.197

ampl

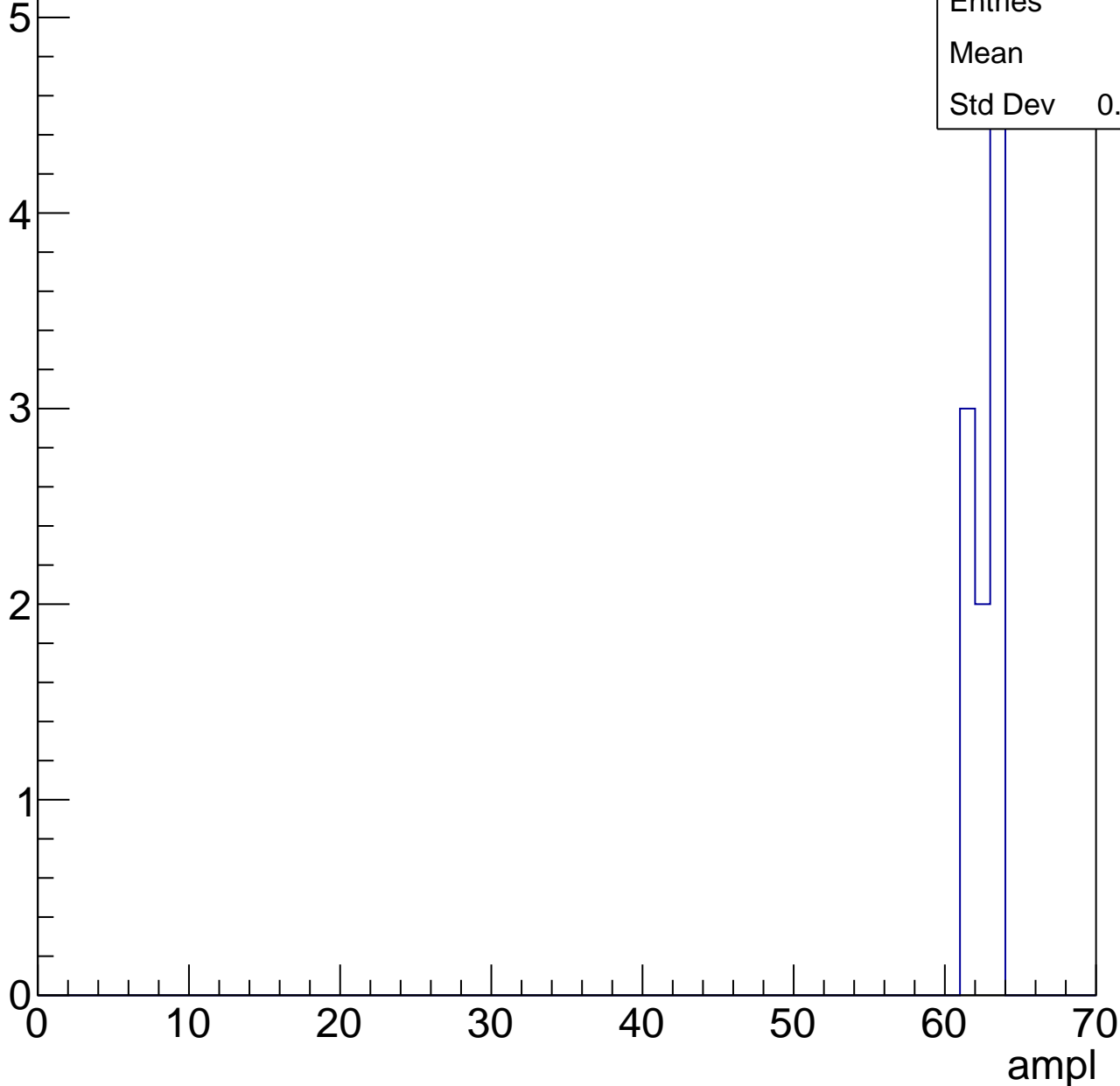


B0L101S, U21-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

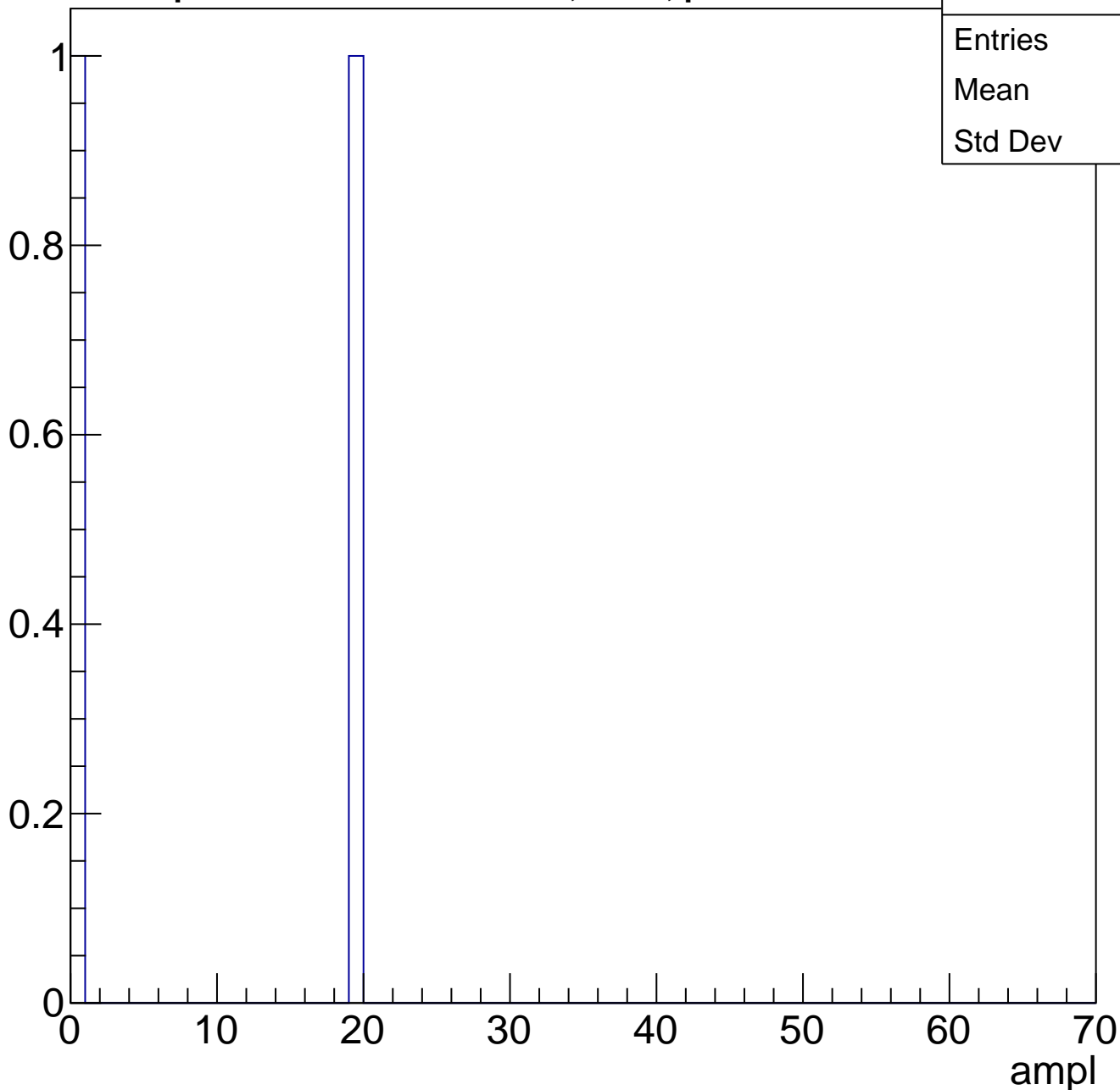
Entries	10
Mean	62.2
Std Dev	0.8718



B0L101S, U21-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

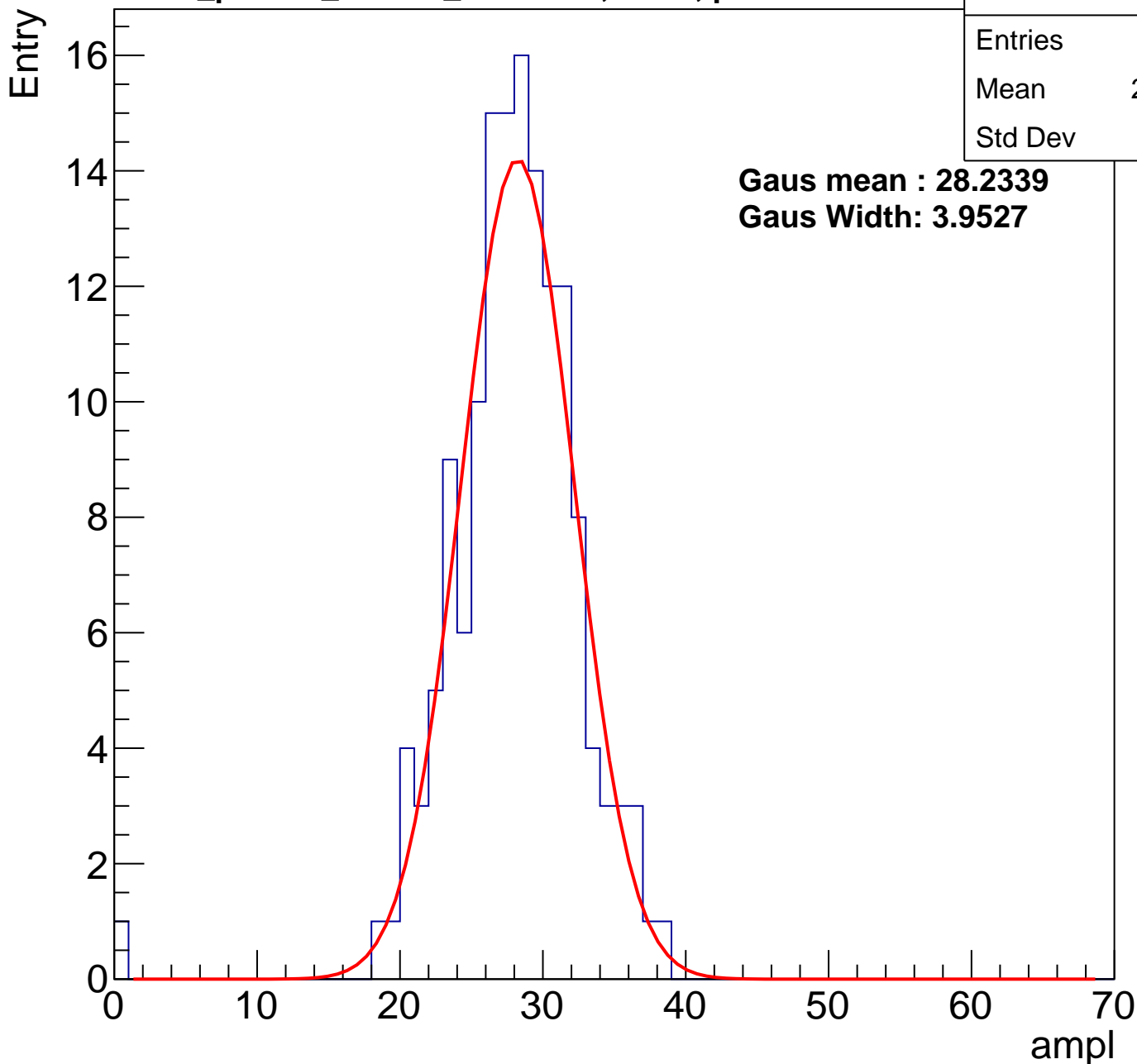
B0L101S, U21-ch24, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	27.55
Std Dev	4.54

Gaus mean : 28.2339

Gaus Width: 3.9527



B0L101S, U21-ch24, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	34.09
Std Dev	3.101

Gaus mean : 34.4793

Gaus Width: 3.0208

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch24, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

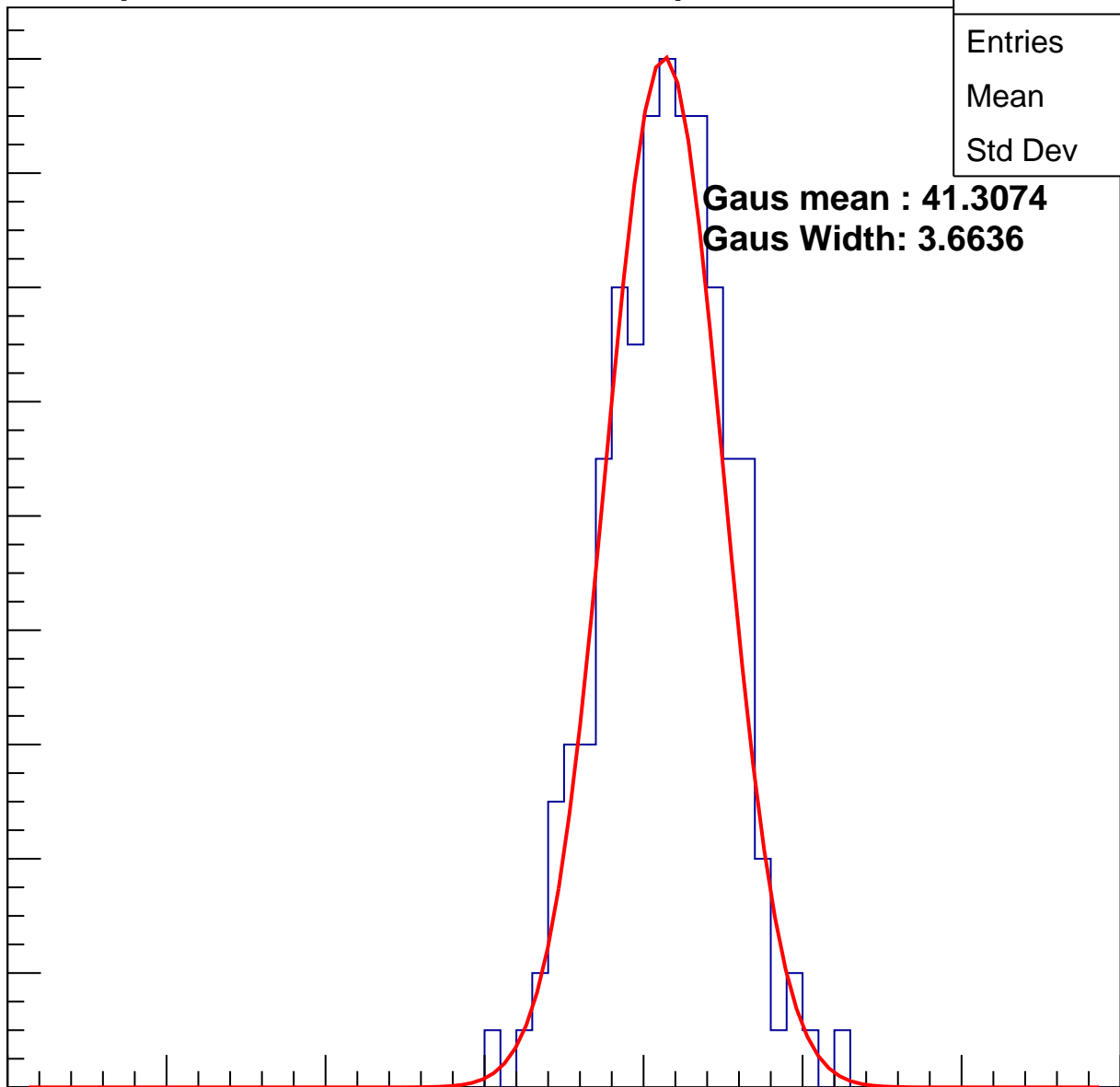
Entries	173
Mean	40.96
Std Dev	3.799

Gaus mean : 41.3074

Gaus Width: 3.6636

0 10 20 30 40 50 60 70

ampl

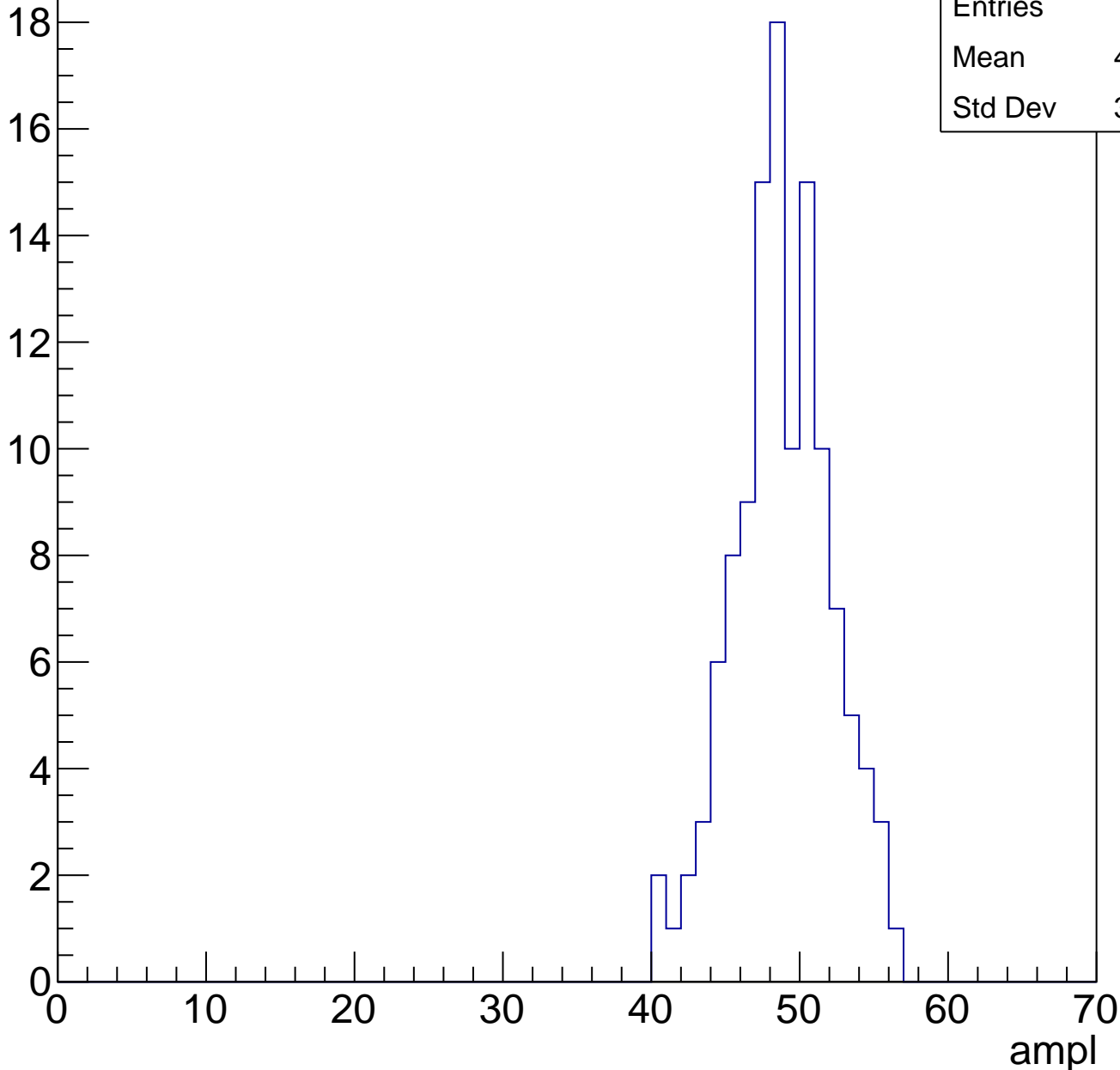


B0L101S, U21-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	48.38
Std Dev	3.277

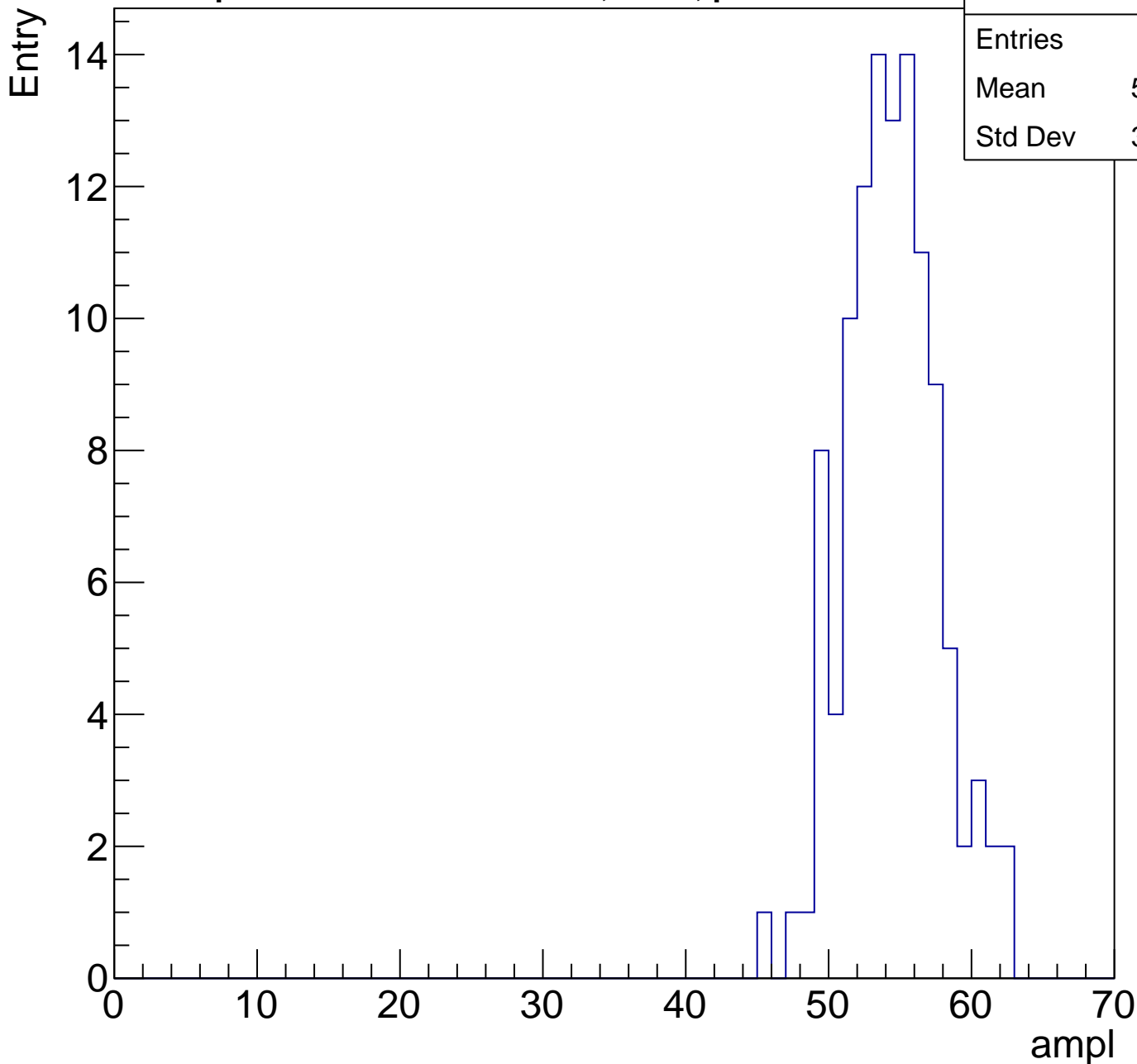
Entry



B0L101S, U21-ch24, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	53.96
Std Dev	3.252



B0L101S, U21-ch24, adc5

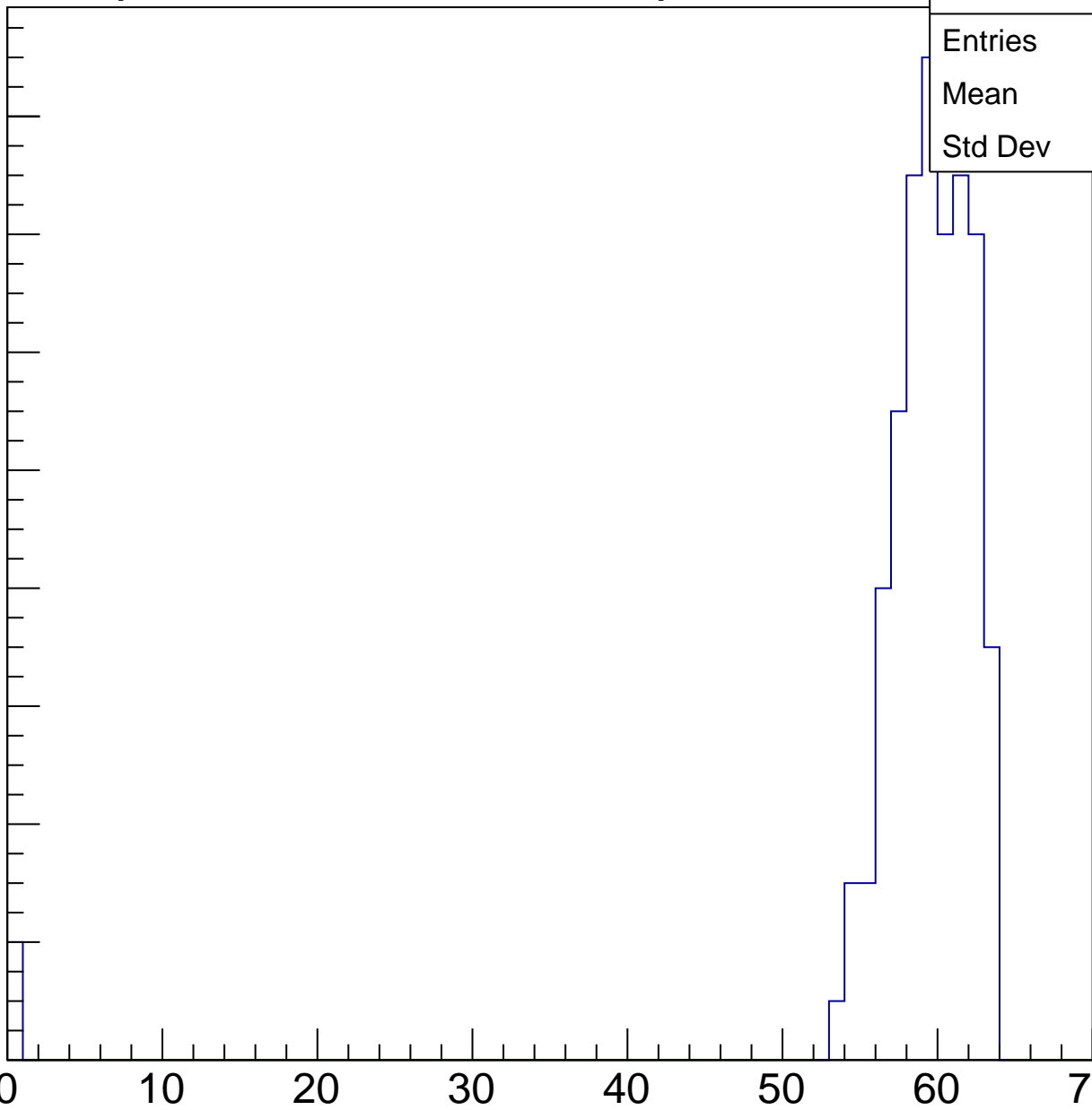
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.11
Std Dev	8.244

ampl



B0L101S, U21-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	22
Mean	61.95
Std Dev	1.26

B0L101S, U21-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



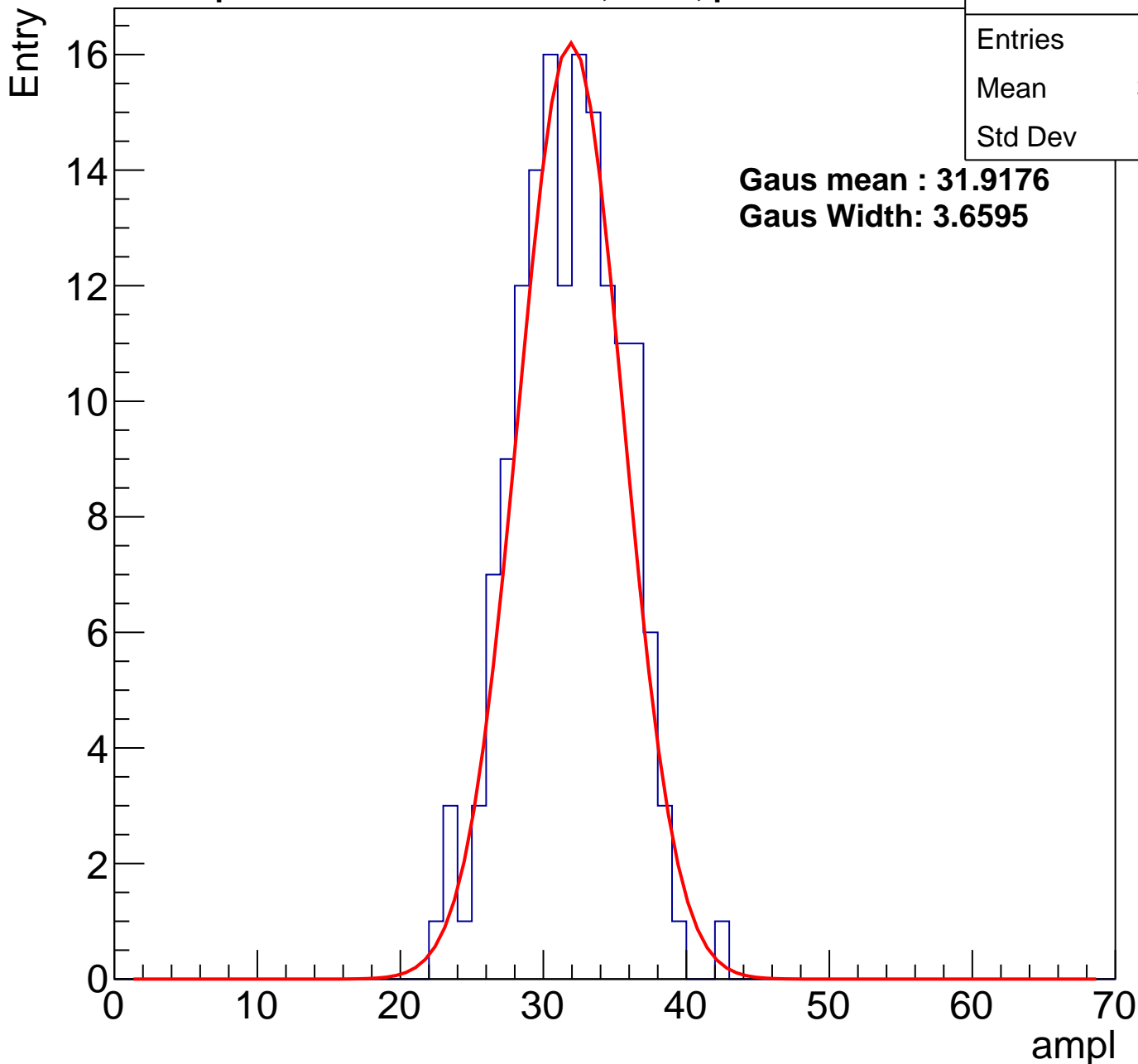
B0L101S, U21-ch25, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	31.31
Std Dev	3.69

Gaus mean : 31.9176

Gaus Width: 3.6595



B0L101S, U21-ch25, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

127

Mean

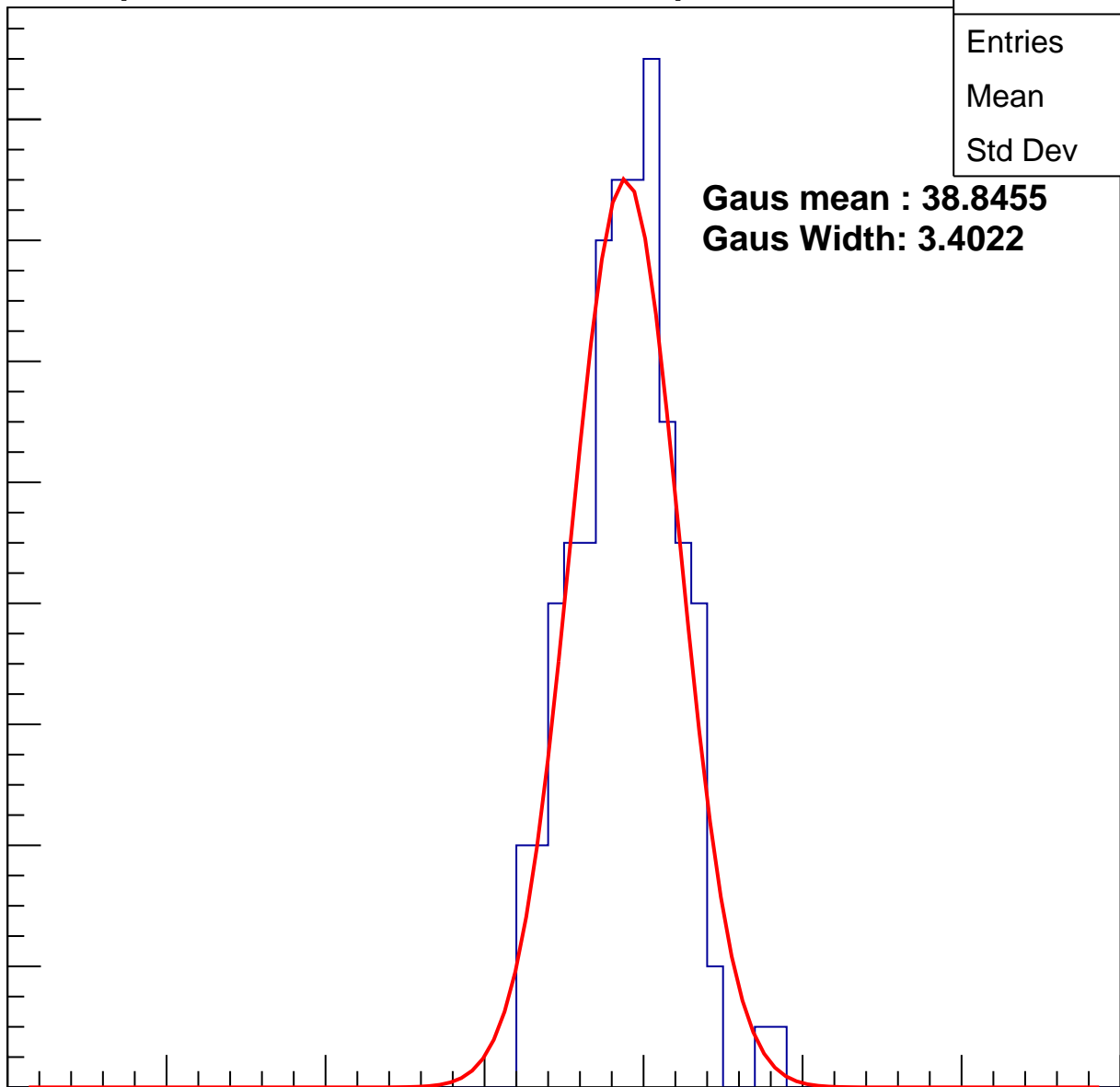
38.43

Std Dev

3.149

Gaus mean : 38.8455

Gaus Width: 3.4022



B0L101S, U21-ch25, adc2

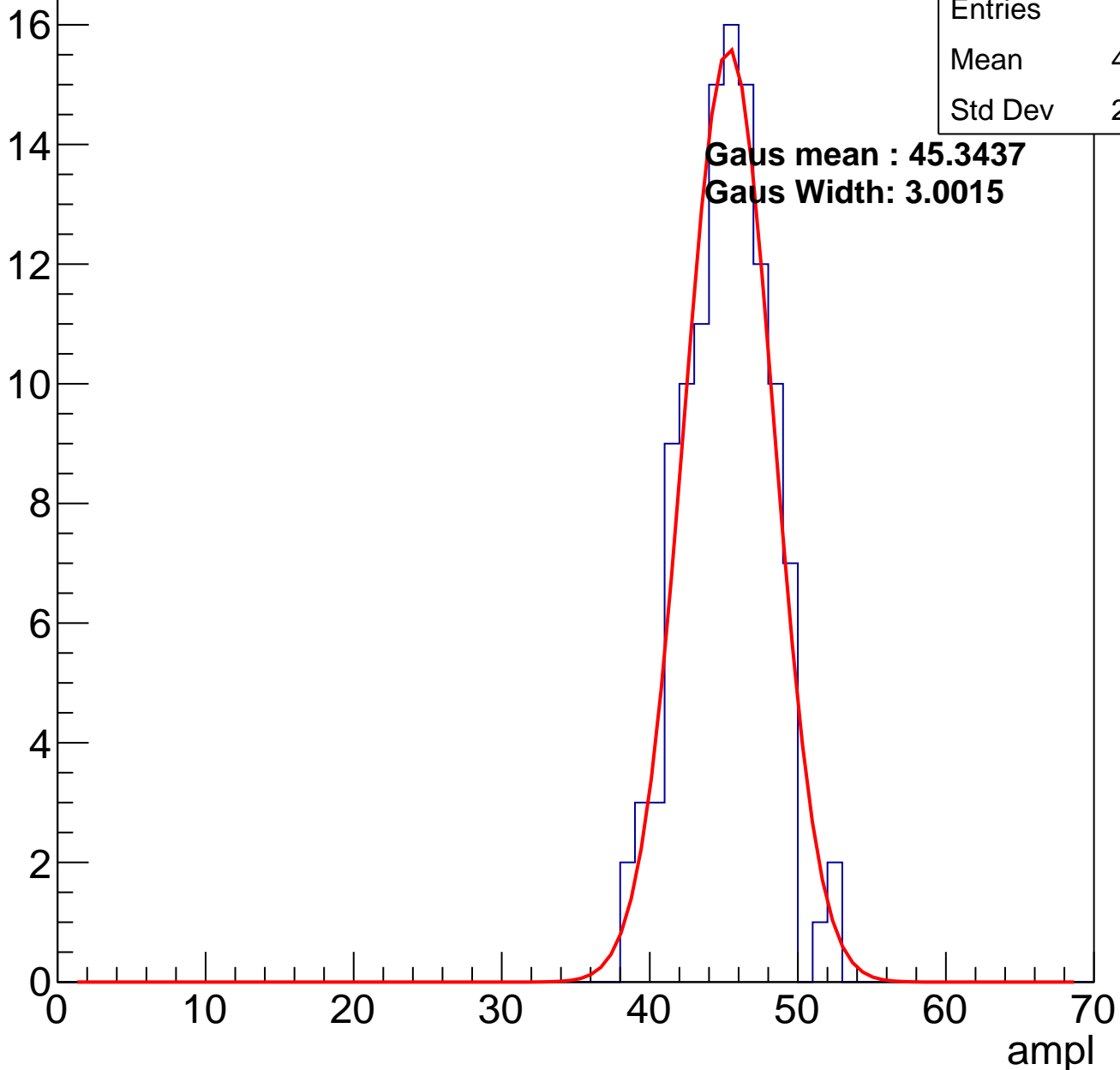
calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	44.72
Std Dev	2.882

Gaus mean : 45.3437

Gaus Width: 3.0015

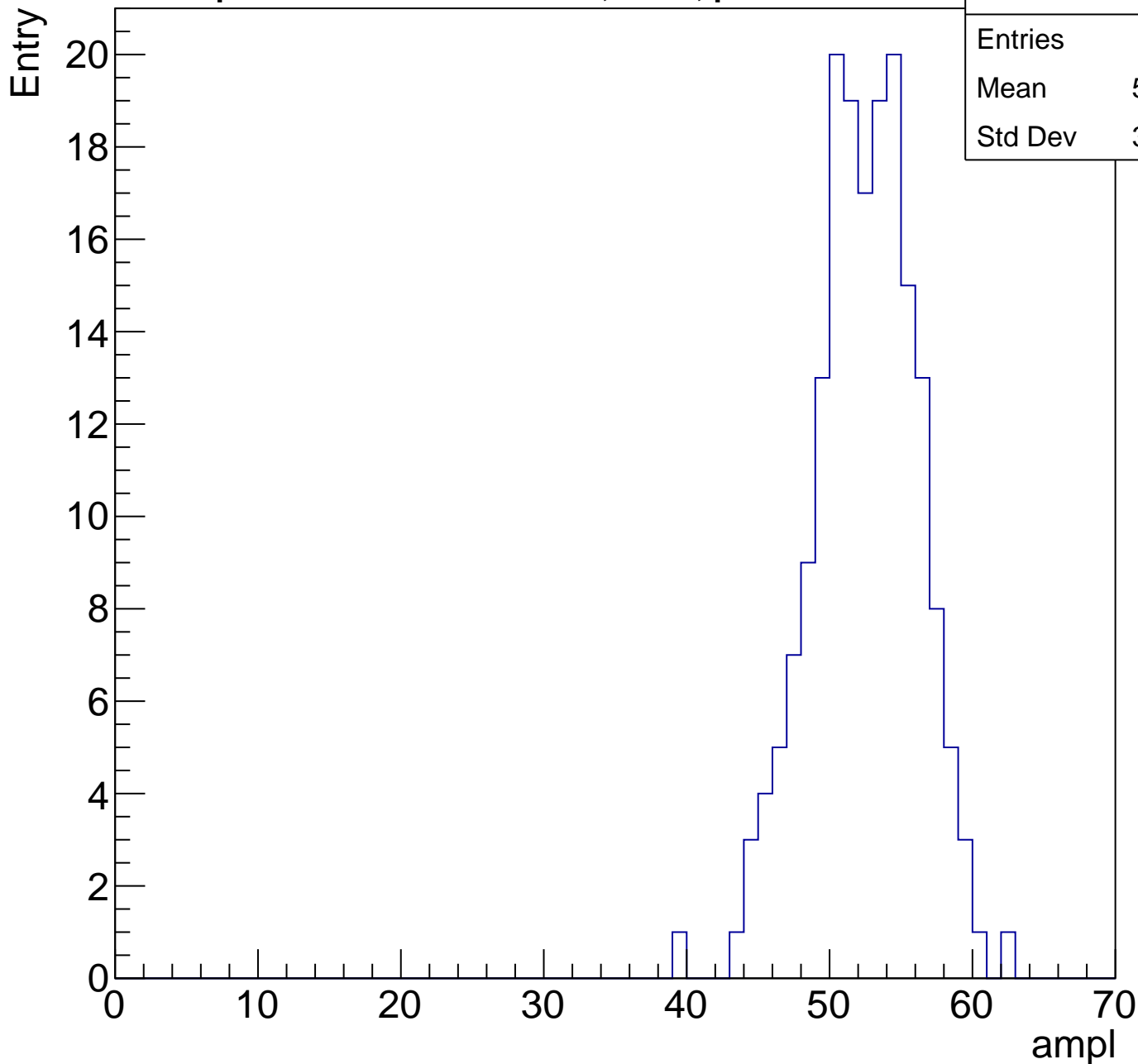
Entry



B0L101S, U21-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	184
Mean	51.96
Std Dev	3.675



B0L101S, U21-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

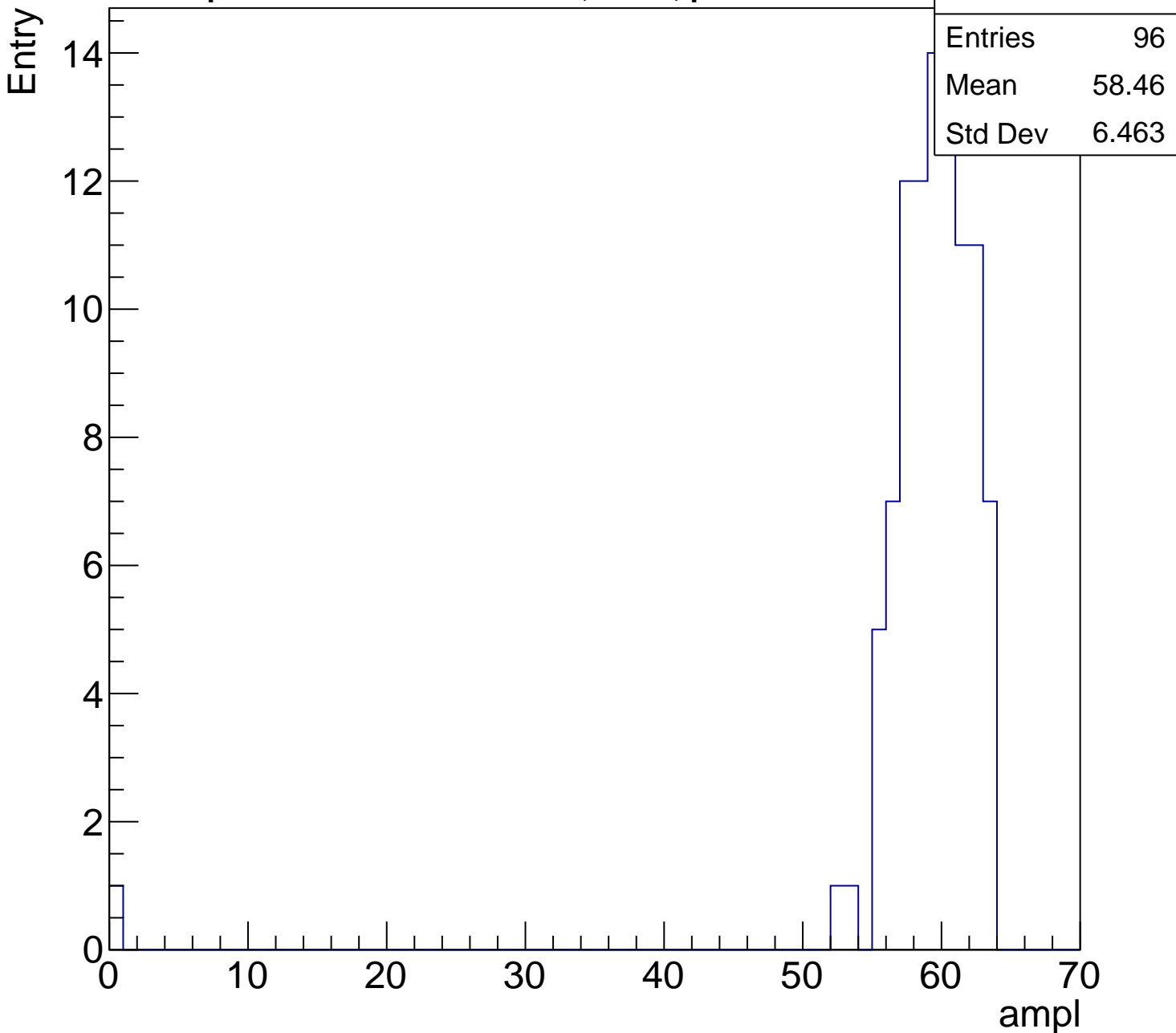
Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	58.46
Std Dev	6.463

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch25, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

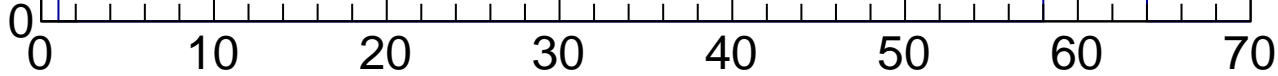
2

0

0 10 20 30 40 50 60 70

ampl

Entries	41
Mean	59.85
Std Dev	9.591



B0L101S, U21-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L101S, U21-ch26, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	30.71
Std Dev	3.833

Gaus mean : 31.1690

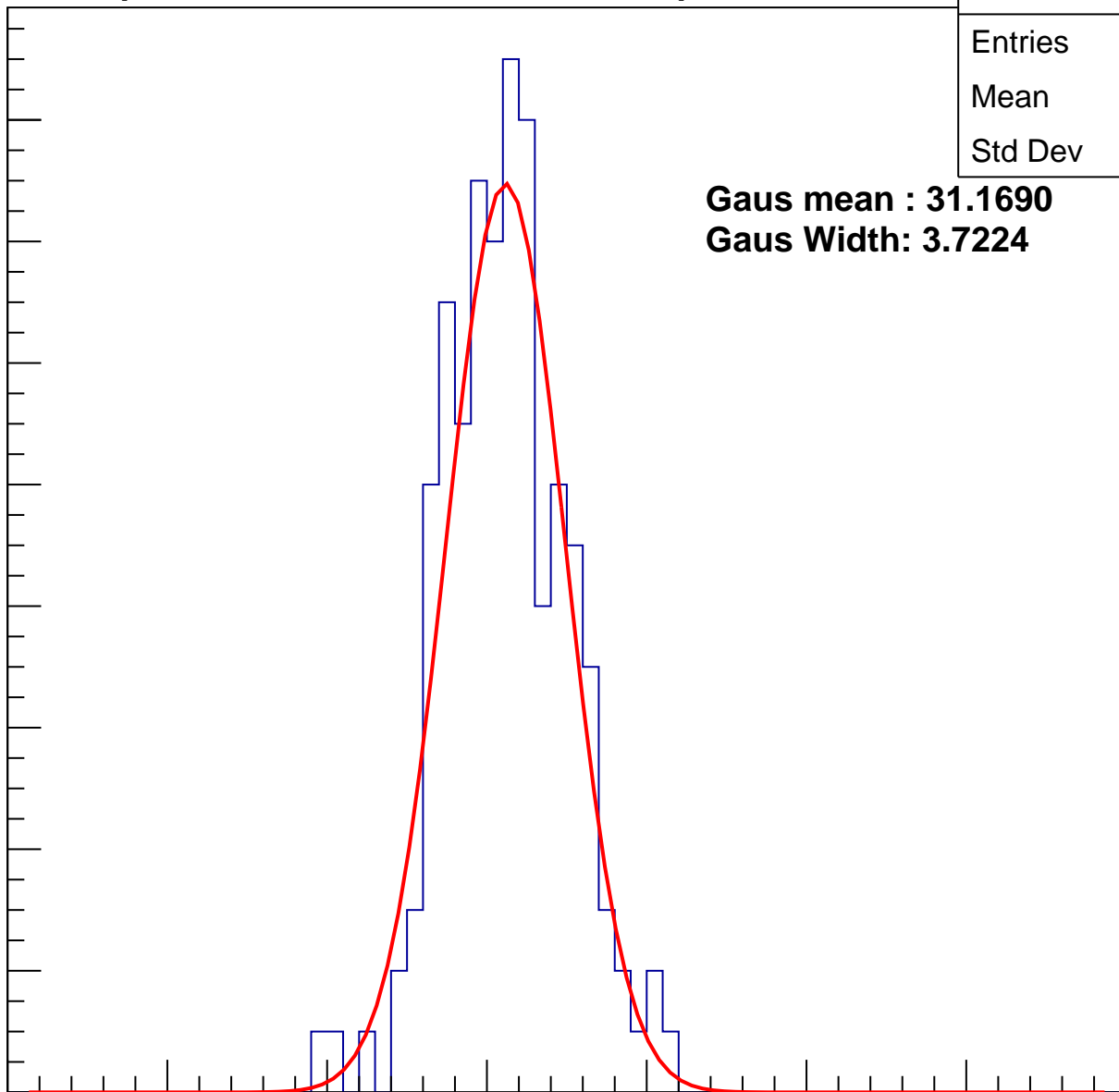
Gaus Width: 3.7224

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

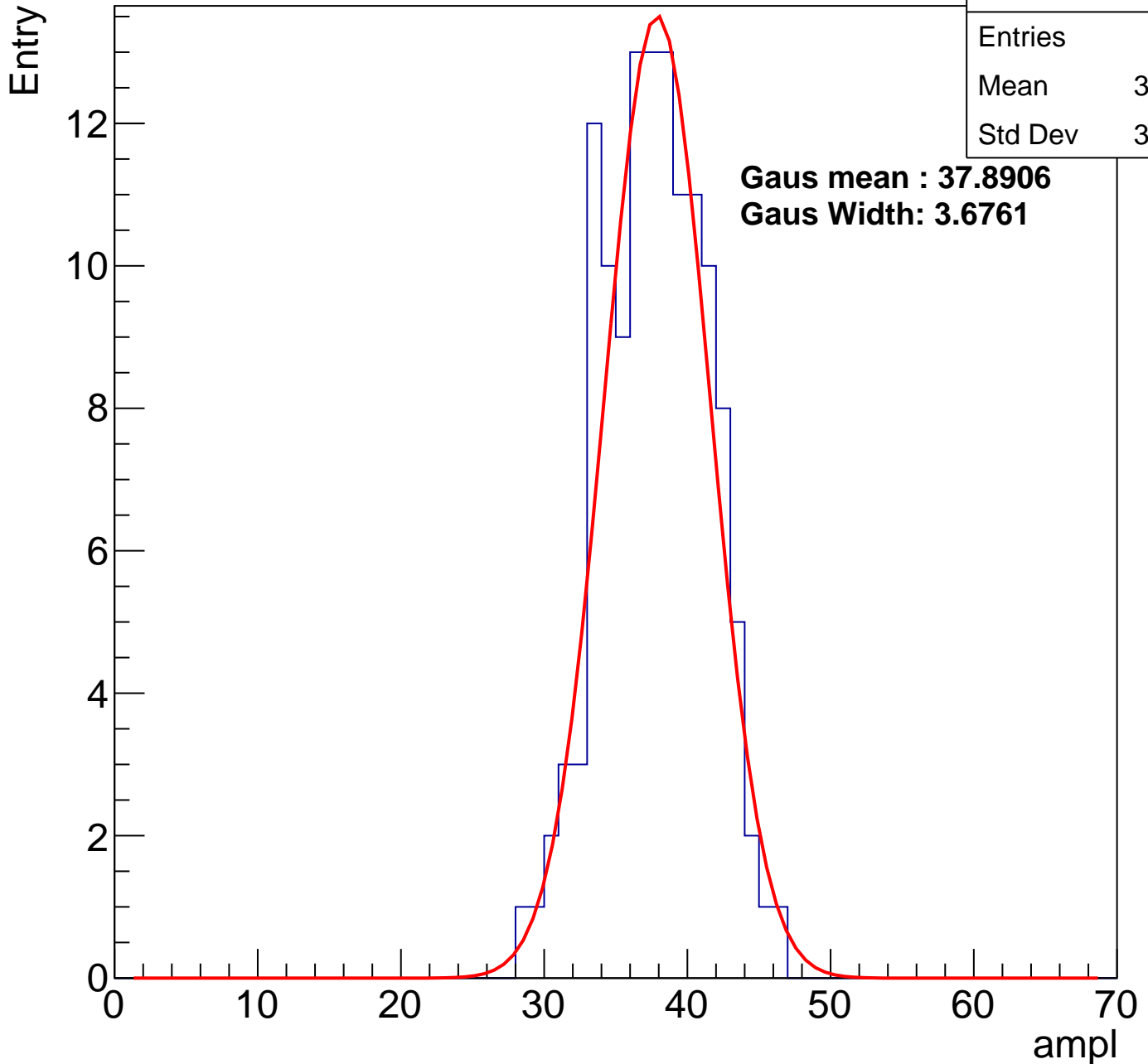


B0L101S, U21-ch26, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	37.28
Std Dev	3.615

Gaus mean : 37.8906
Gaus Width: 3.6761



B0L101S, U21-ch26, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	44.01
Std Dev	3.751

Gaus mean : 44.3495

Gaus Width: 4.0321

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

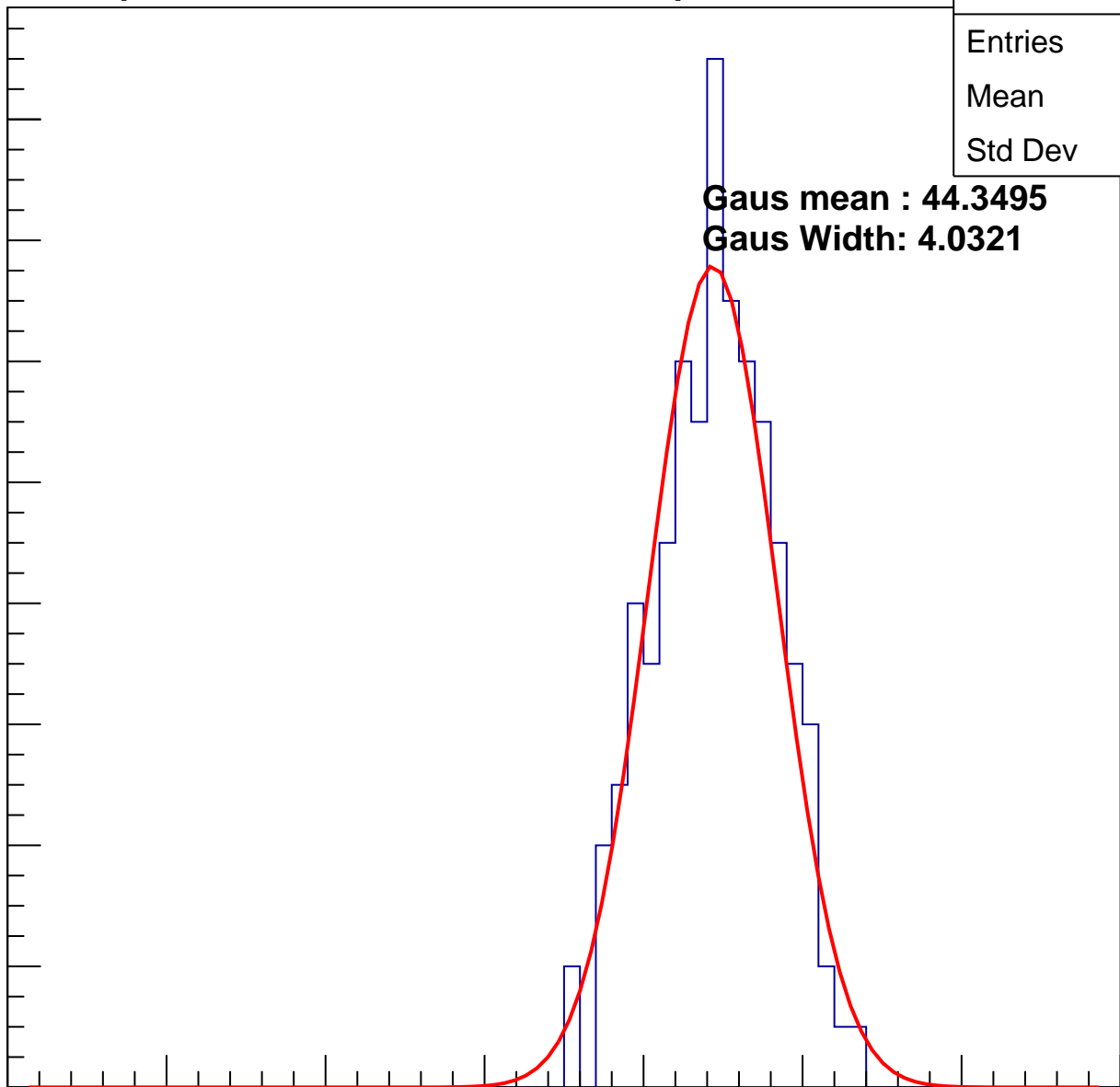
40

50

60

70

ampl



B0L101S, U21-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	51.12
Std Dev	3.926

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

40

50

60

ampl

4

1

6

7

9

11

13

15

17

19

21

23

25

27

29

31

33

35

37

39

41

43

45

47

49

51

53

55

57

59

61

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75

77

79

81

83

85

87

89

91

93

95

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101

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105

107

109

111

113

115

117

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125

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131

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553

555

557

559

561

563

565

567

569

571

573

575

577

579

581

583

585

587

589

591

593

595

597

599

601

603

605

607

609

611

613

615

617

619

621

623

625

B0L101S, U21-ch26, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	56.02
Std Dev	3.034

Entry

12

10

8

6

4

2

0

0

10

20

30

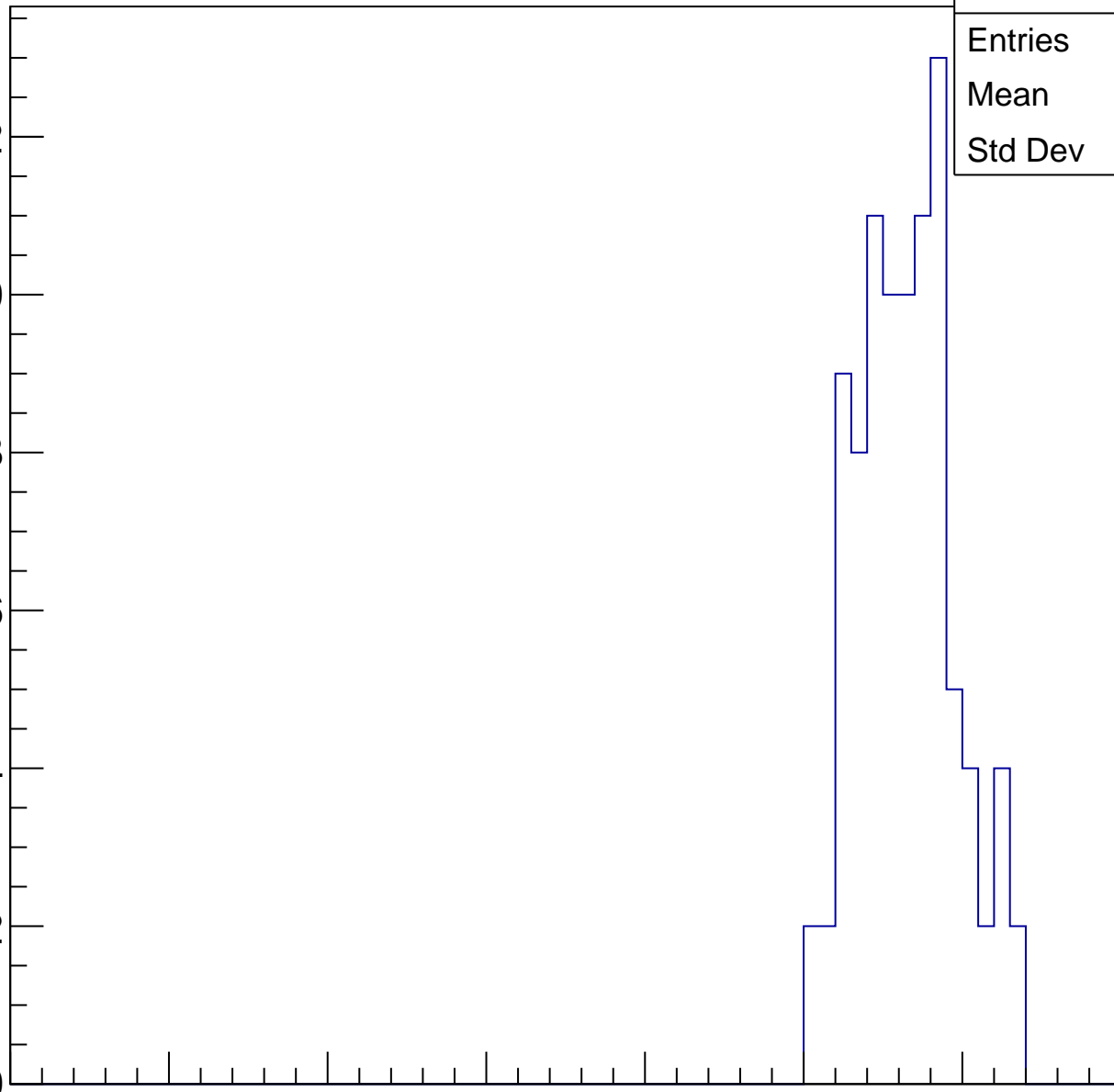
40

50

60

70

ampl



B0L101S, U21-ch26, adc5

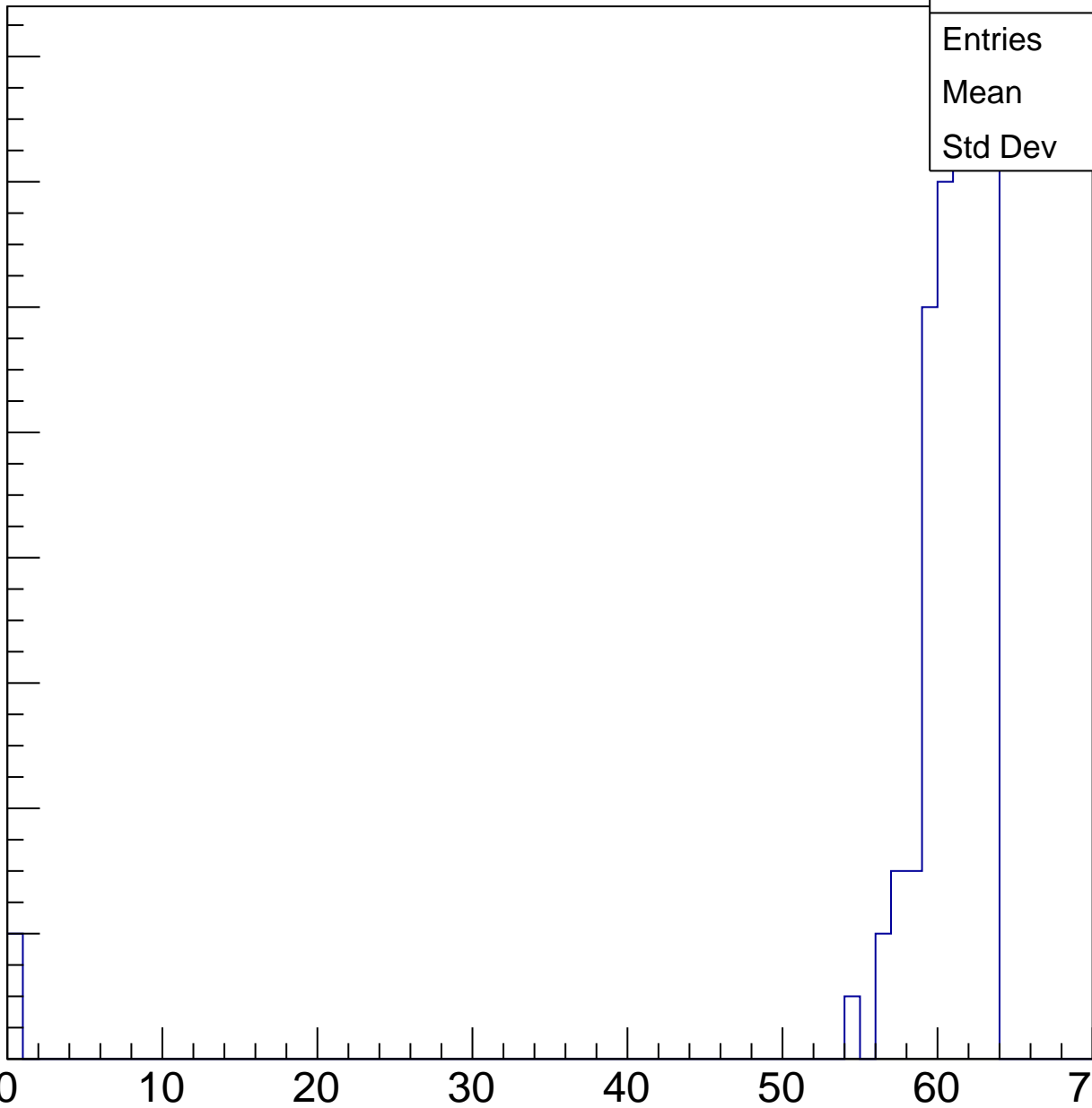
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.19
Std Dev	9.436

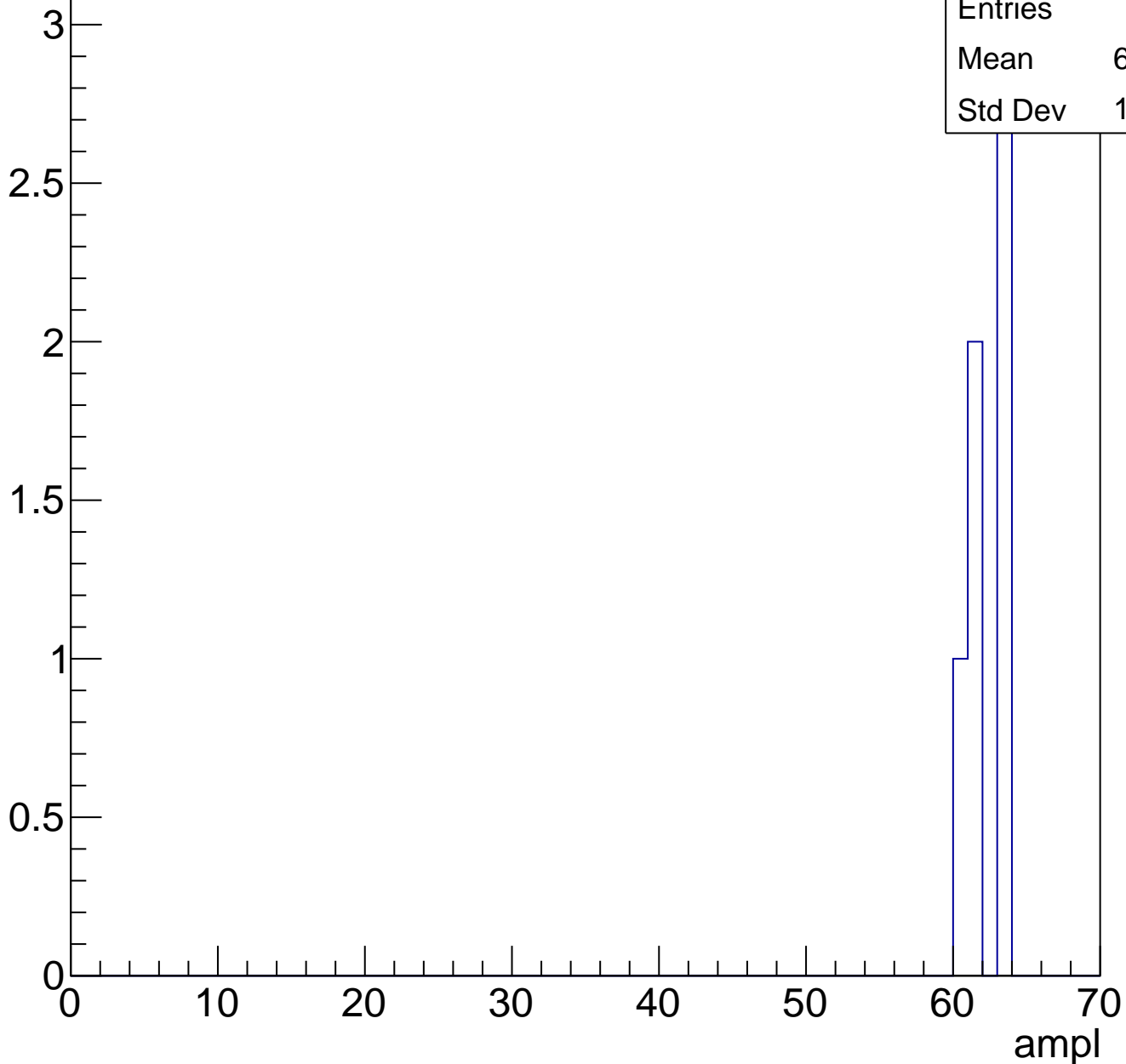
ampl



B0L101S, U21-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch27, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	30.12
Std Dev	3.958

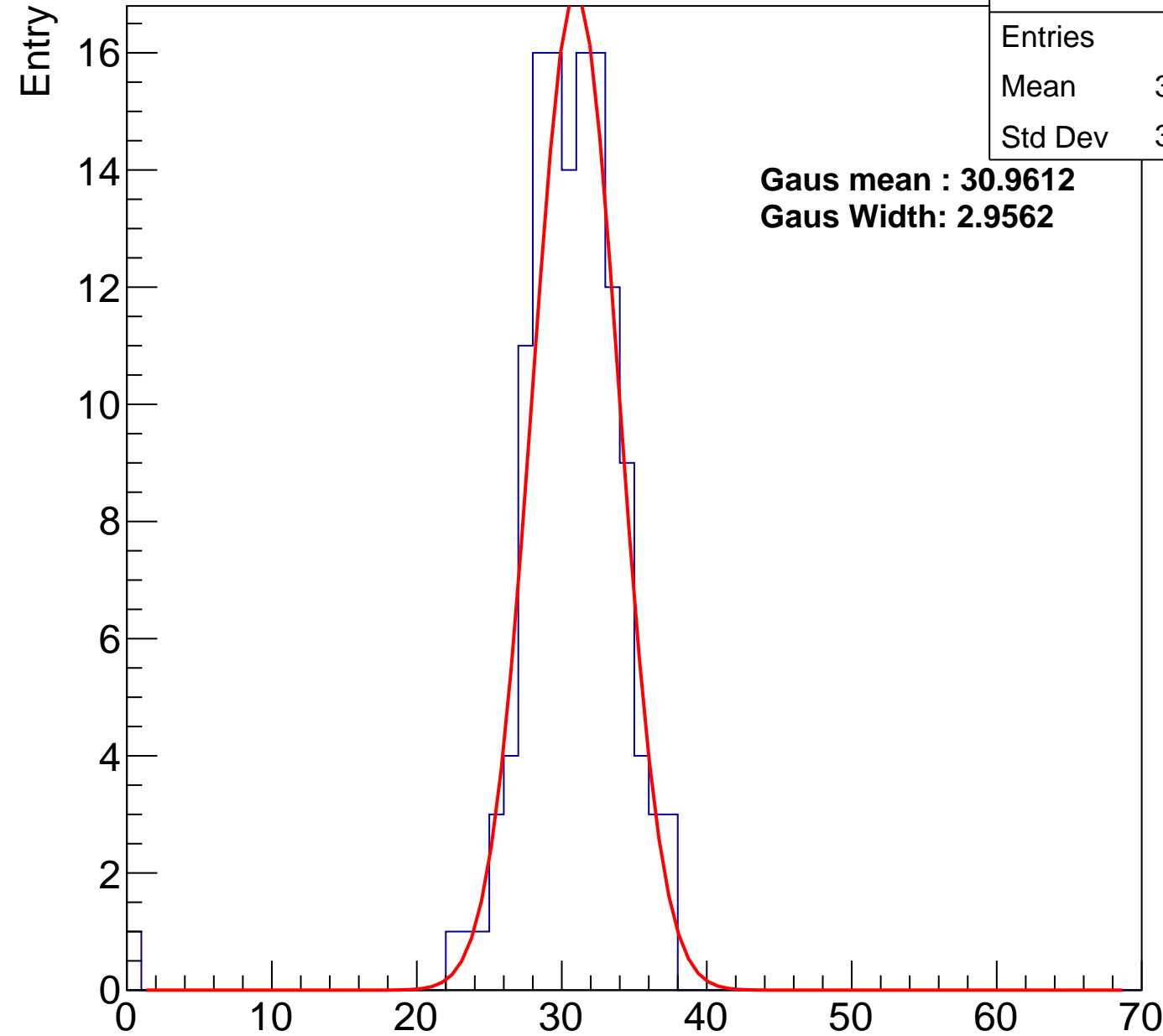
Gaus mean : 30.9612

Gaus Width: 2.9562

Entry

16
14
12
10
8
6
4
2
0

ampl

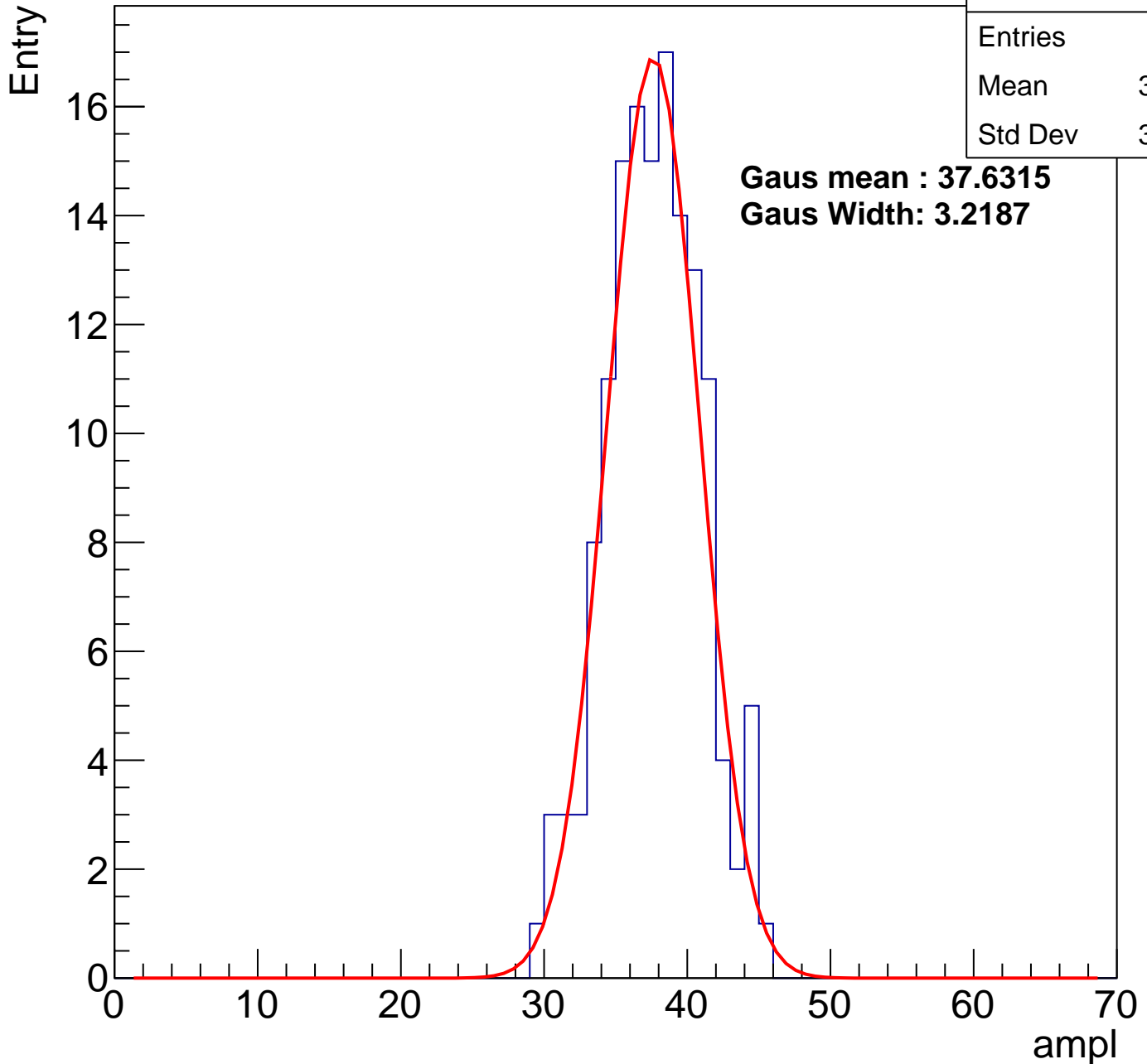


B0L101S, U21-ch27, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	37.21
Std Dev	3.291

Gaus mean : 37.6315
Gaus Width: 3.2187



B0L101S, U21-ch27, adc2

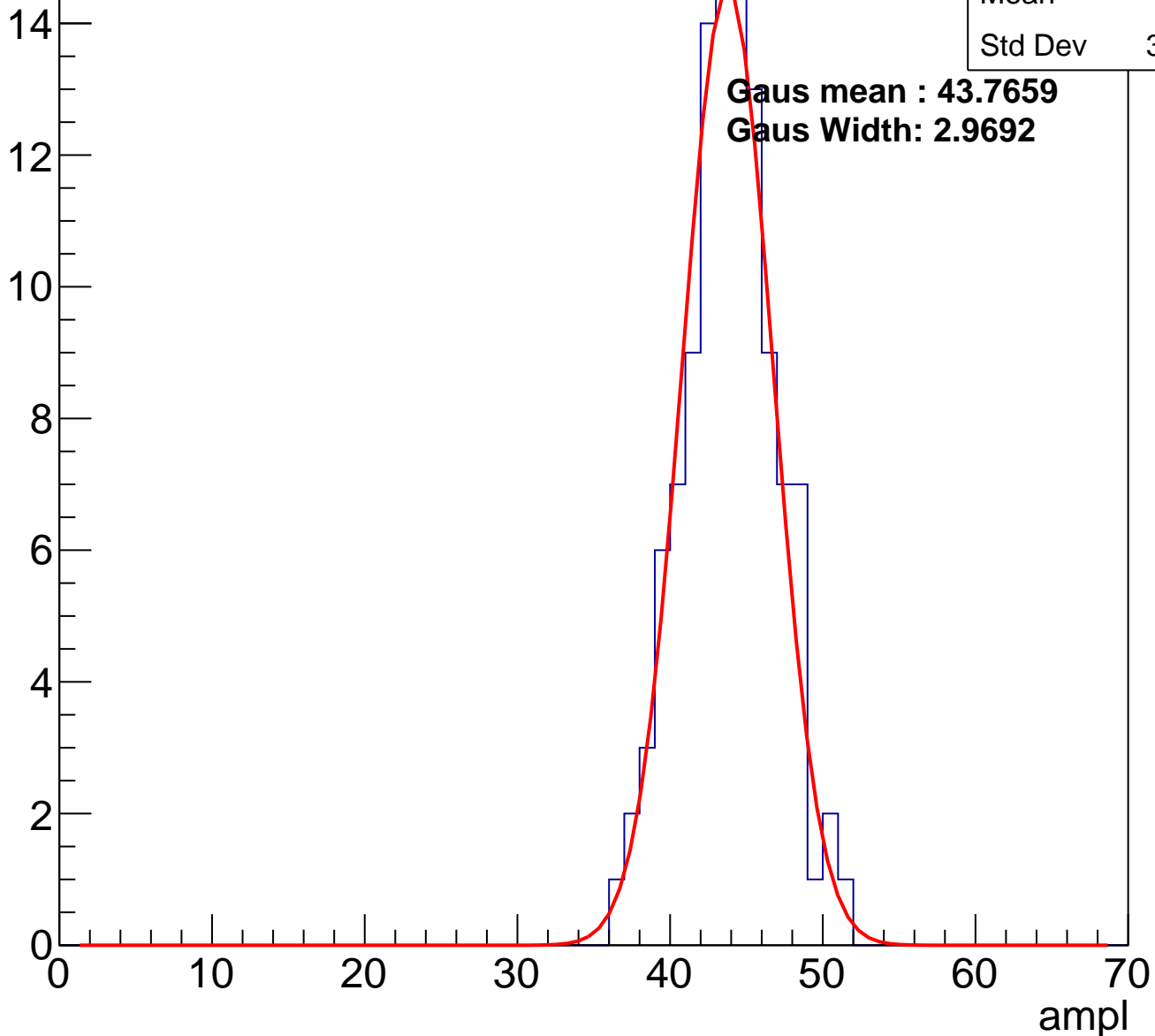
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	43.43
Std Dev	3.014

Gaus mean : 43.7659

Gaus Width: 2.9692

Entry

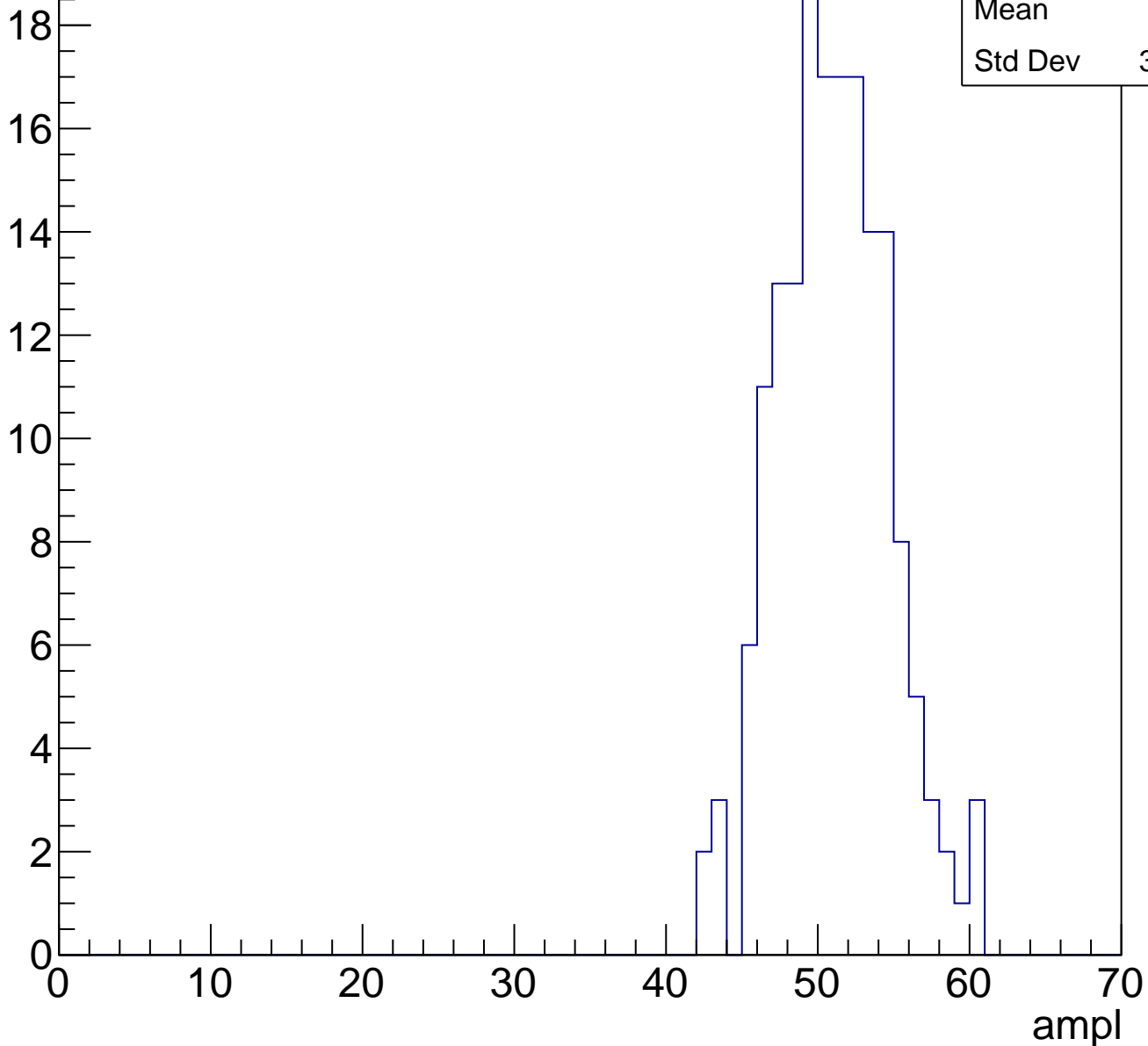


B0L101S, U21-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	50.6
Std Dev	3.619

Entry



B0L101S, U21-ch27, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

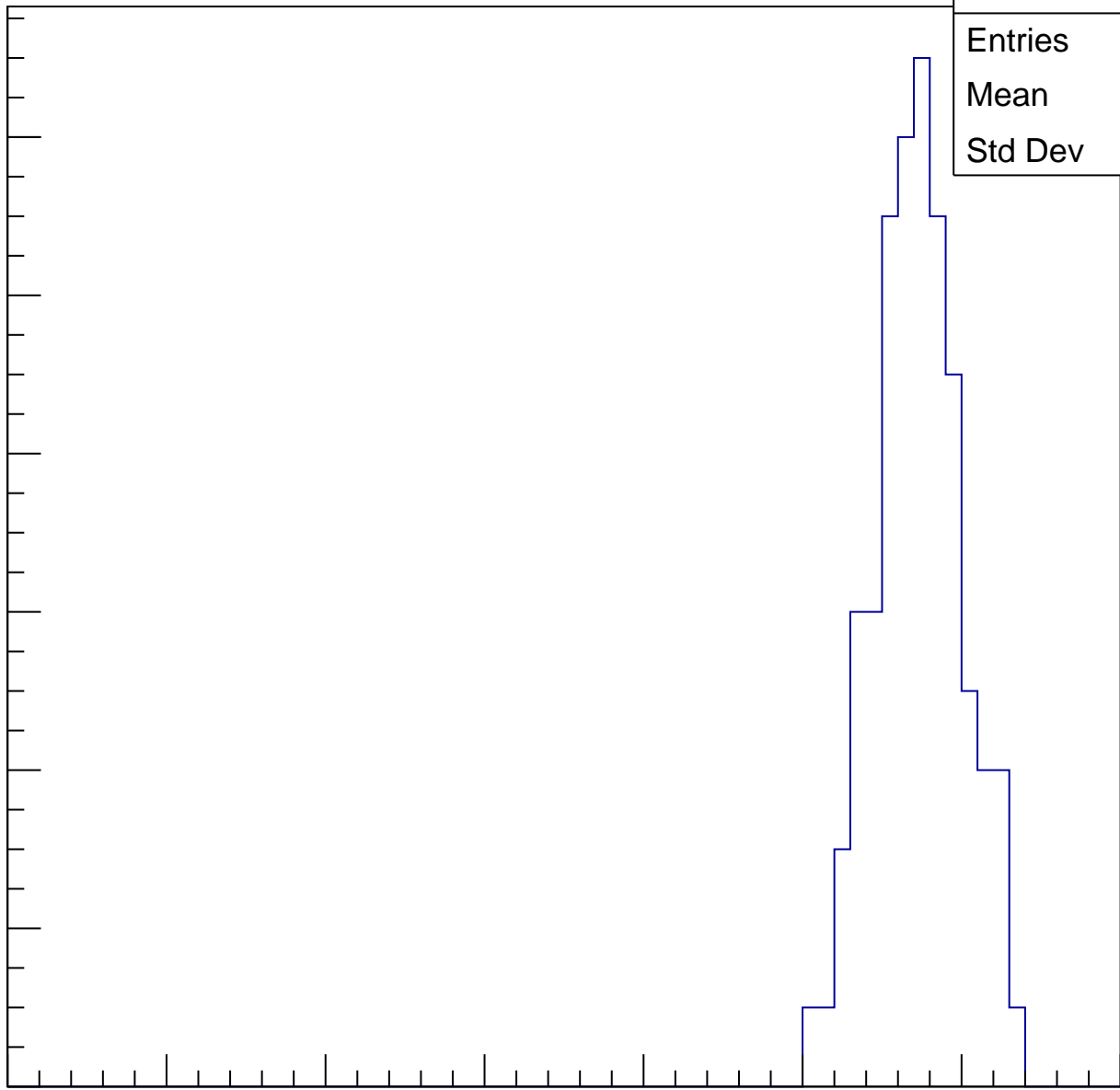
2

0

Entries	87
Mean	56.79
Std Dev	2.742

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch27, adc5

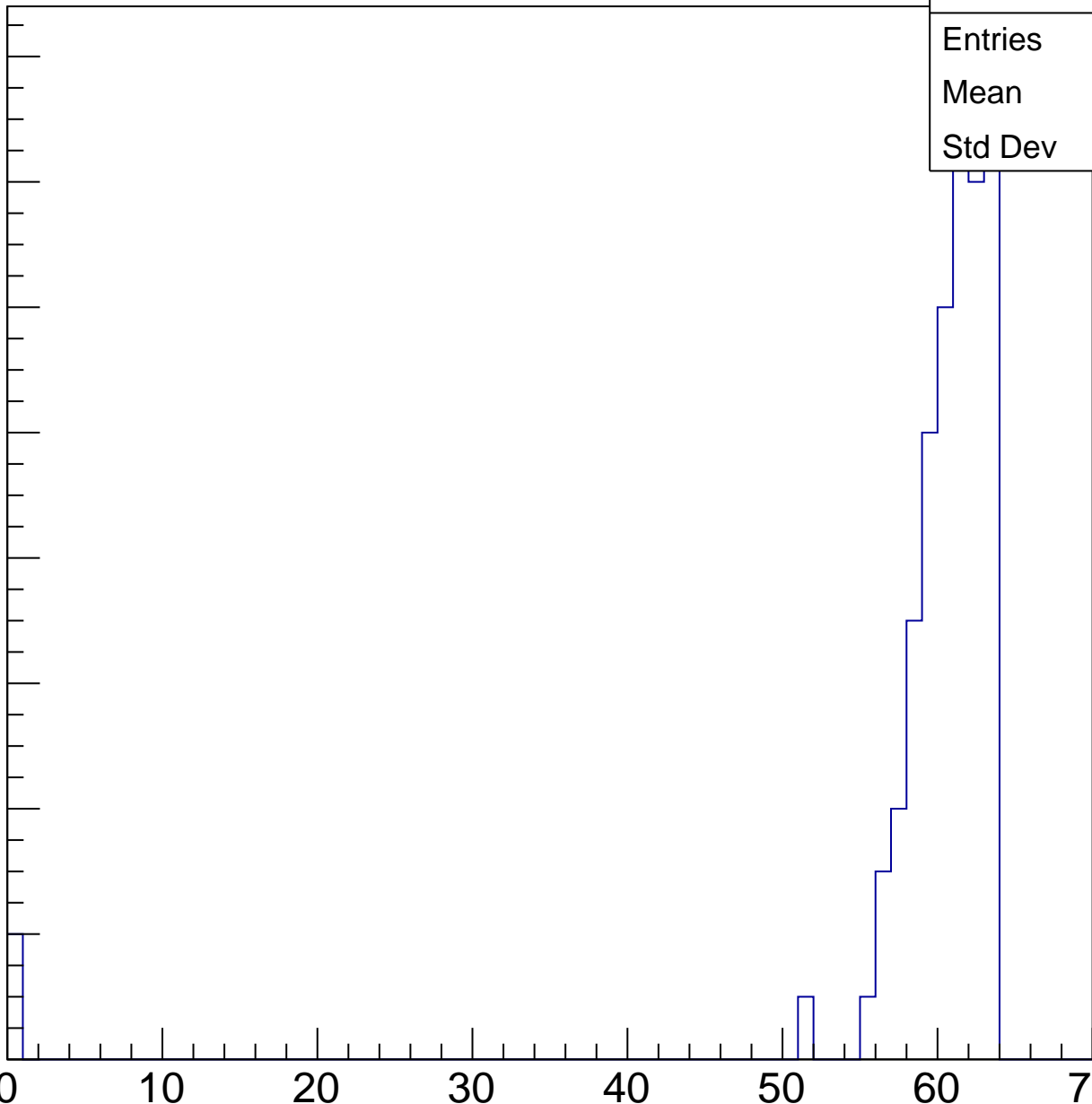
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.95
Std Dev	9.37

ampl



B0L101S, U21-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

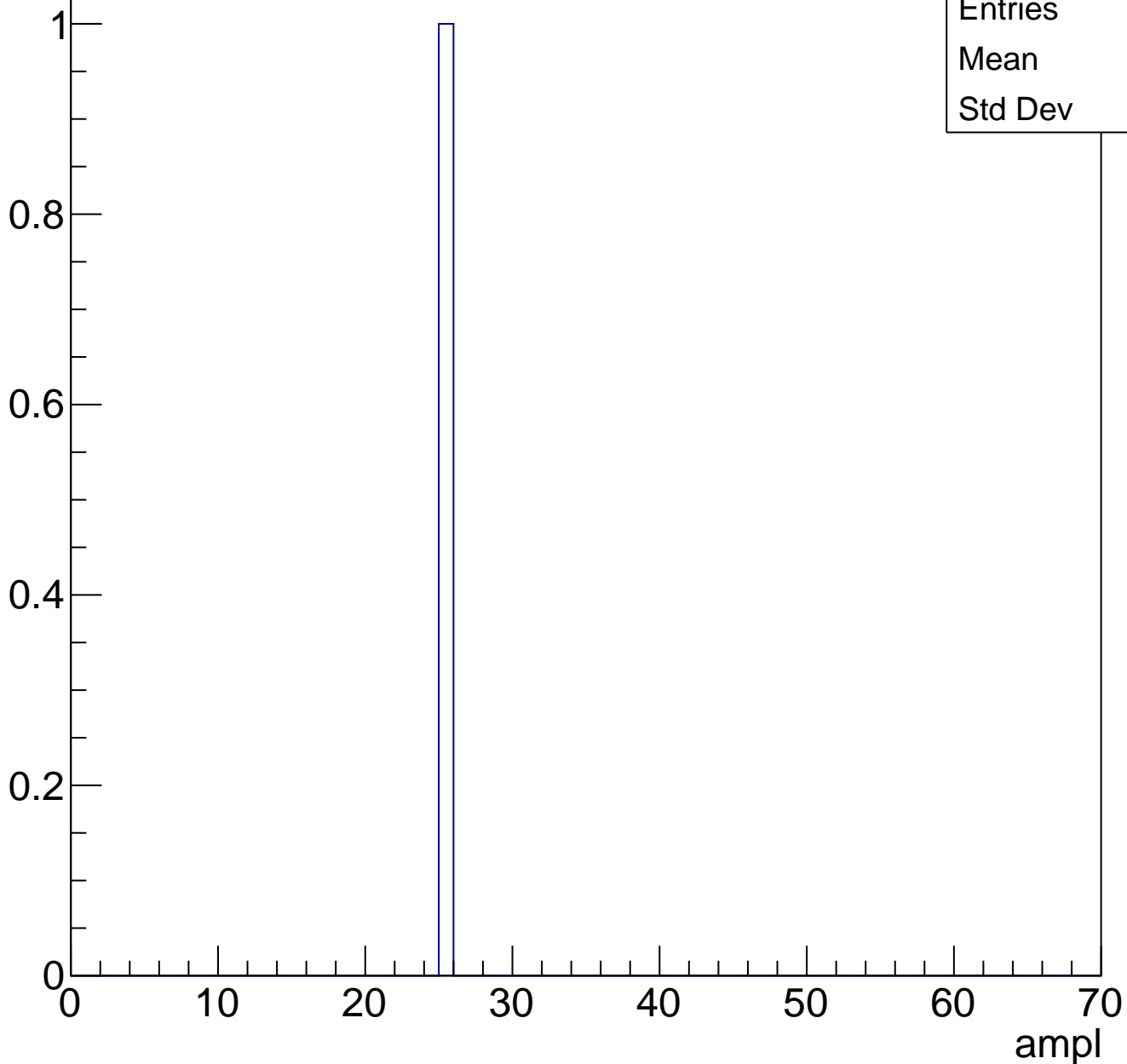
Entry



B0L101S, U21-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch28, adc0

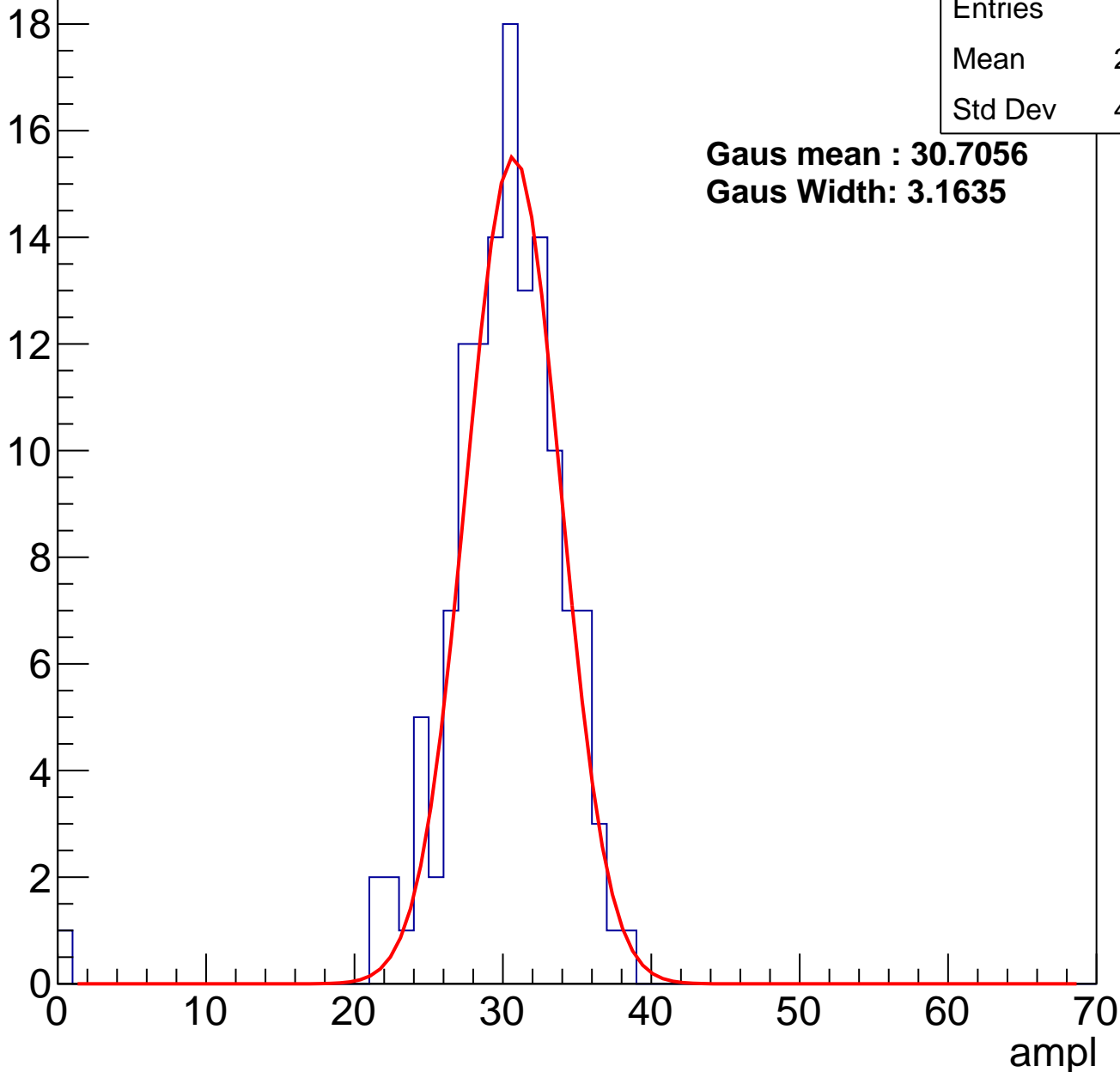
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	29.65
Std Dev	4.266

Gaus mean : 30.7056

Gaus Width: 3.1635

Entry



B0L101S, U21-ch28, adc1

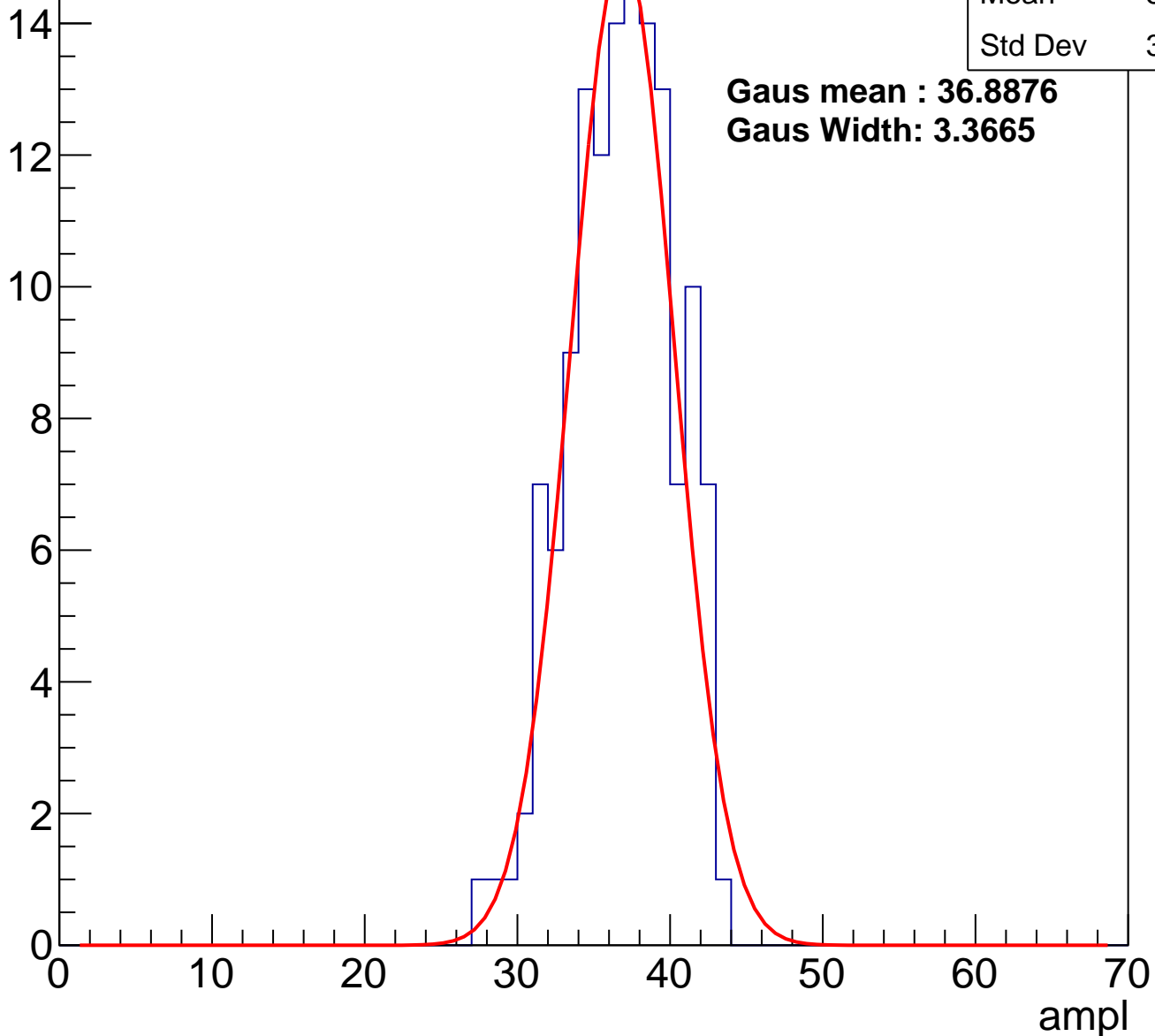
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	36.37
Std Dev	3.375

Gaus mean : 36.8876

Gaus Width: 3.3665

Entry



B0L101S, U21-ch28, adc2

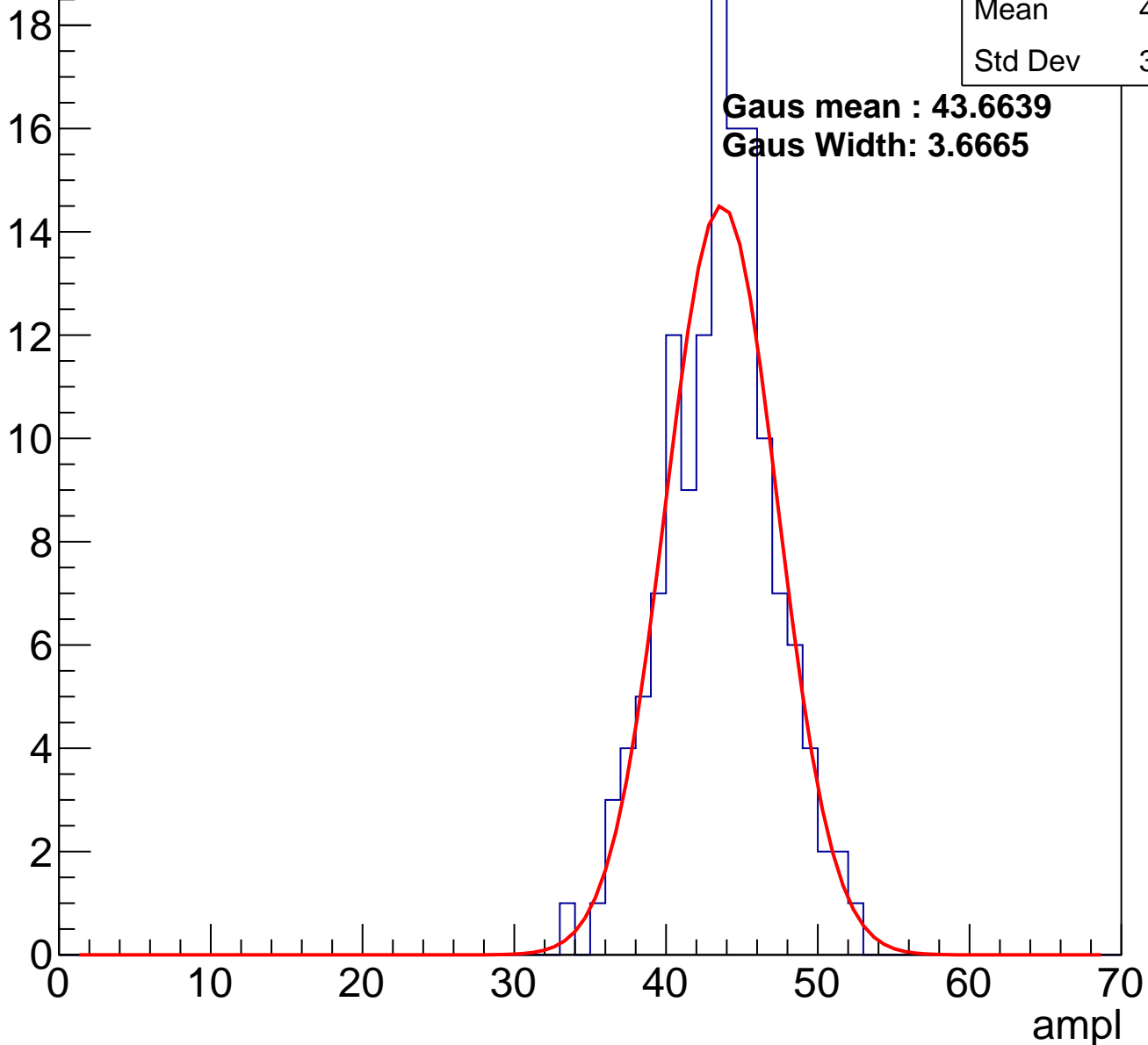
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	43.12
Std Dev	3.552

Gaus mean : 43.6639

Gaus Width: 3.6665

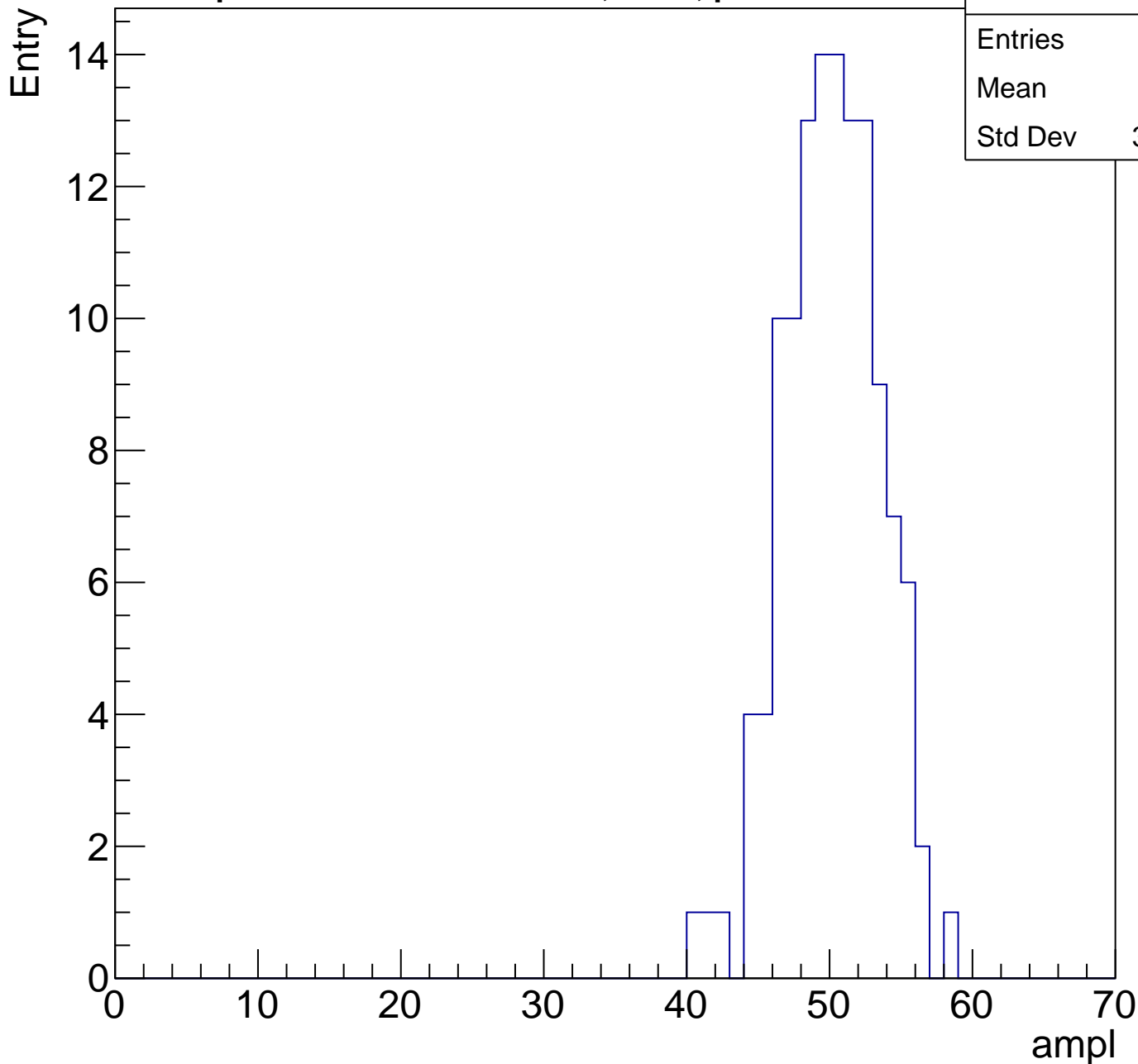
Entry



B0L101S, U21-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	49.7
Std Dev	3.298



B0L101S, U21-ch28, adc4

calib_packv5_042523_0143.root, FC#1, port C1

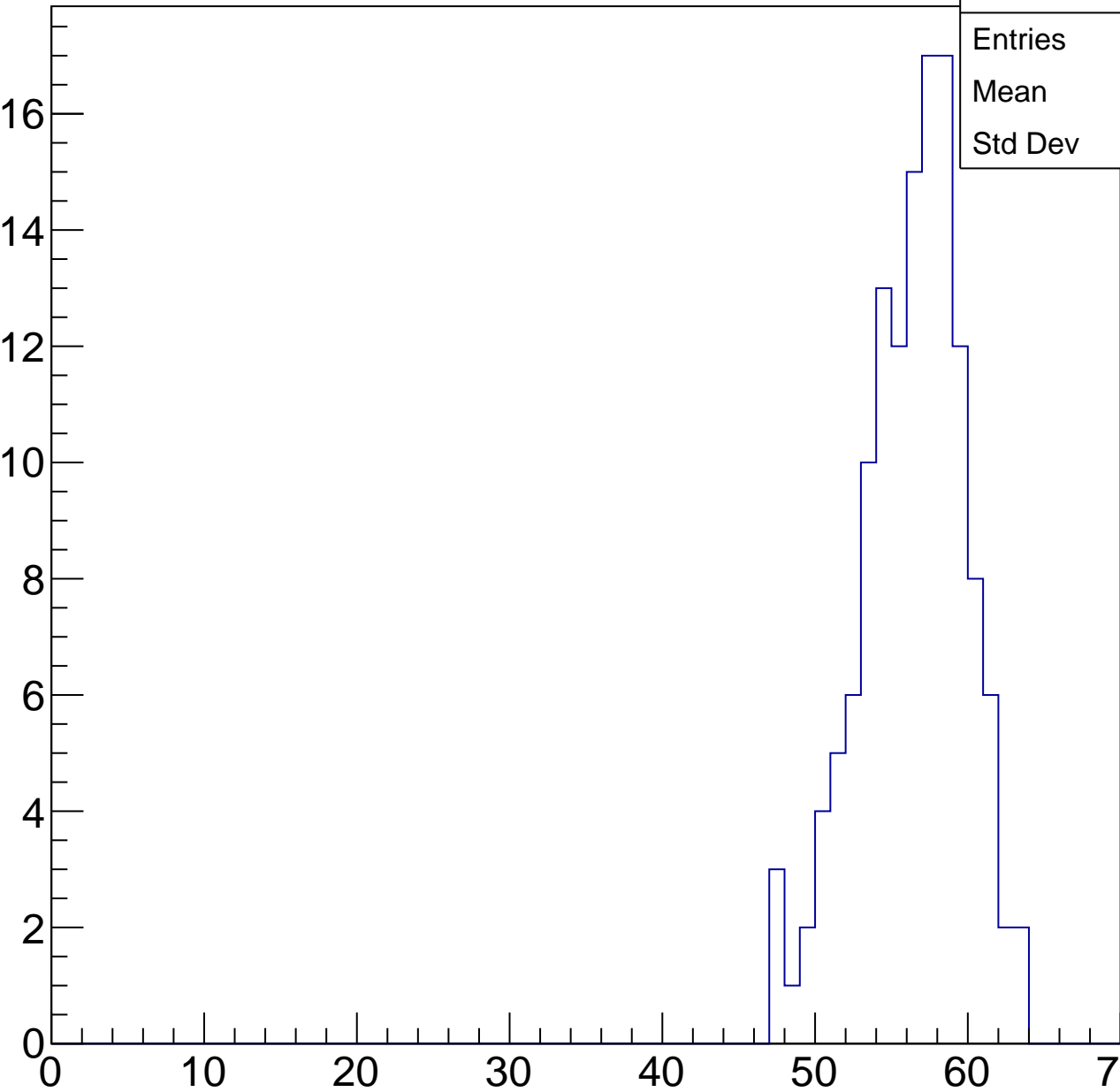
Entries	135
Mean	55.89
Std Dev	3.403

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch28, adc5

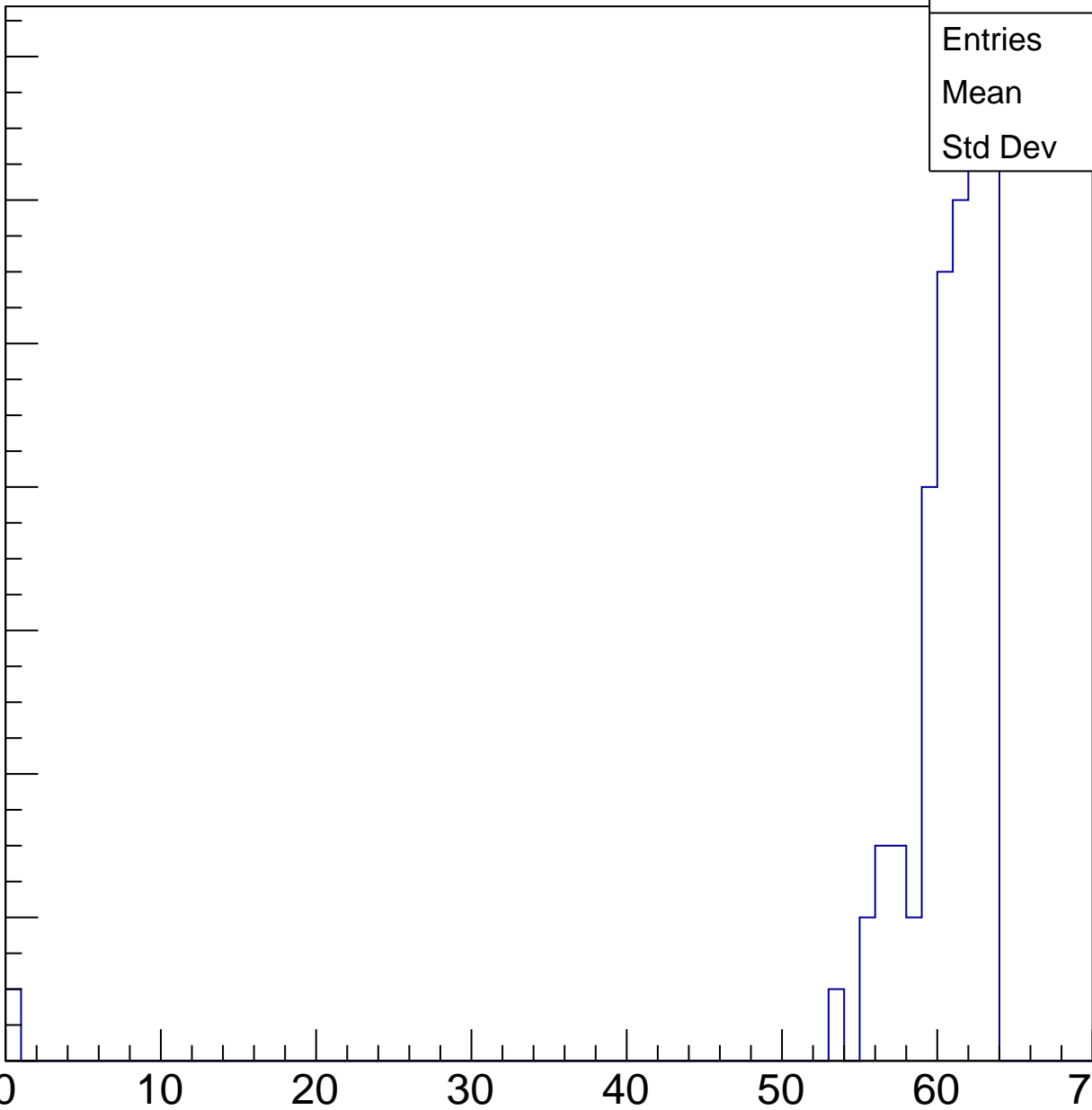
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	59.57
Std Dev	7.528

ampl

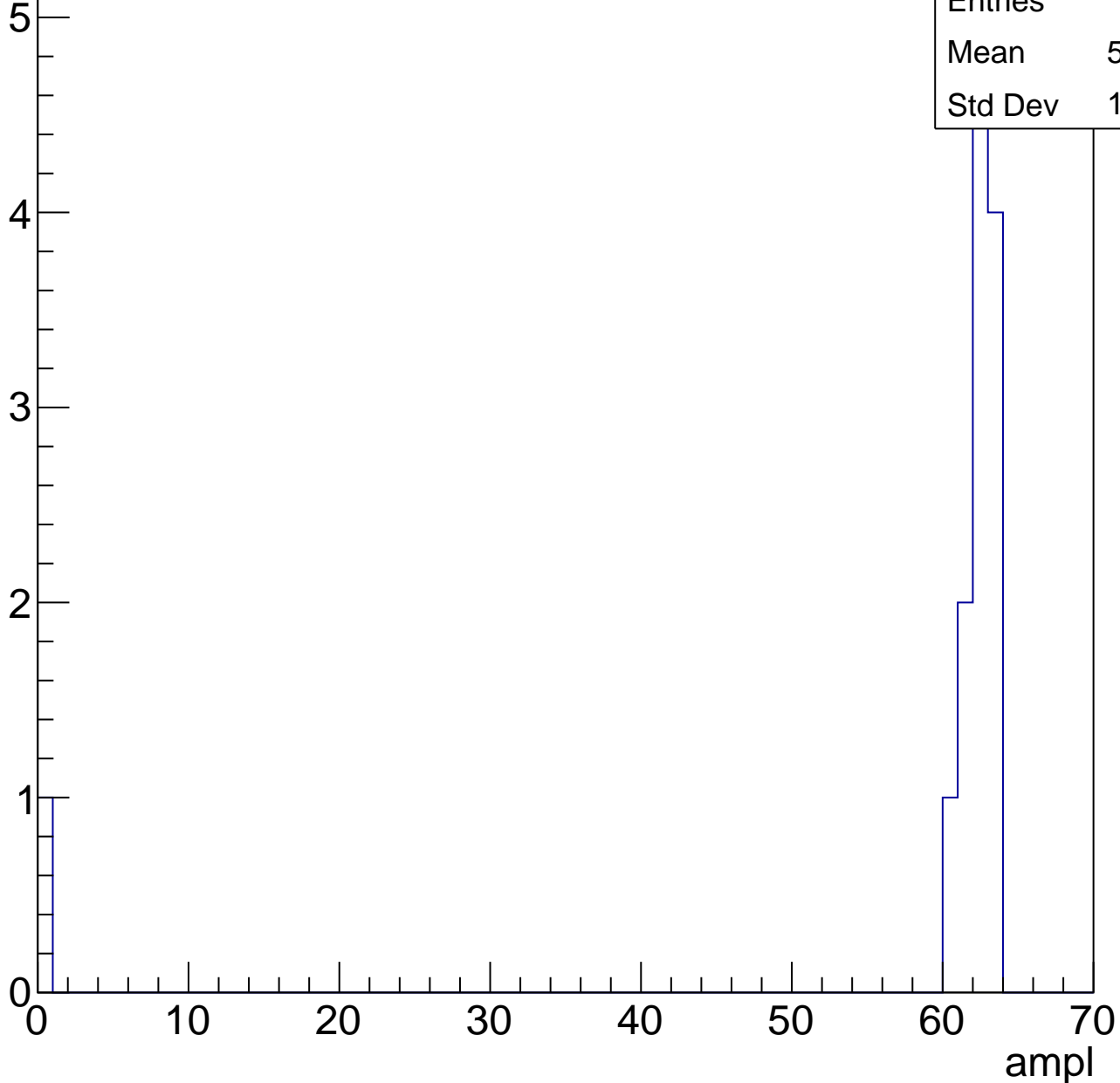


B0L101S, U21-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.23
Std Dev	16.54



B0L101S, U21-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch29, adc0

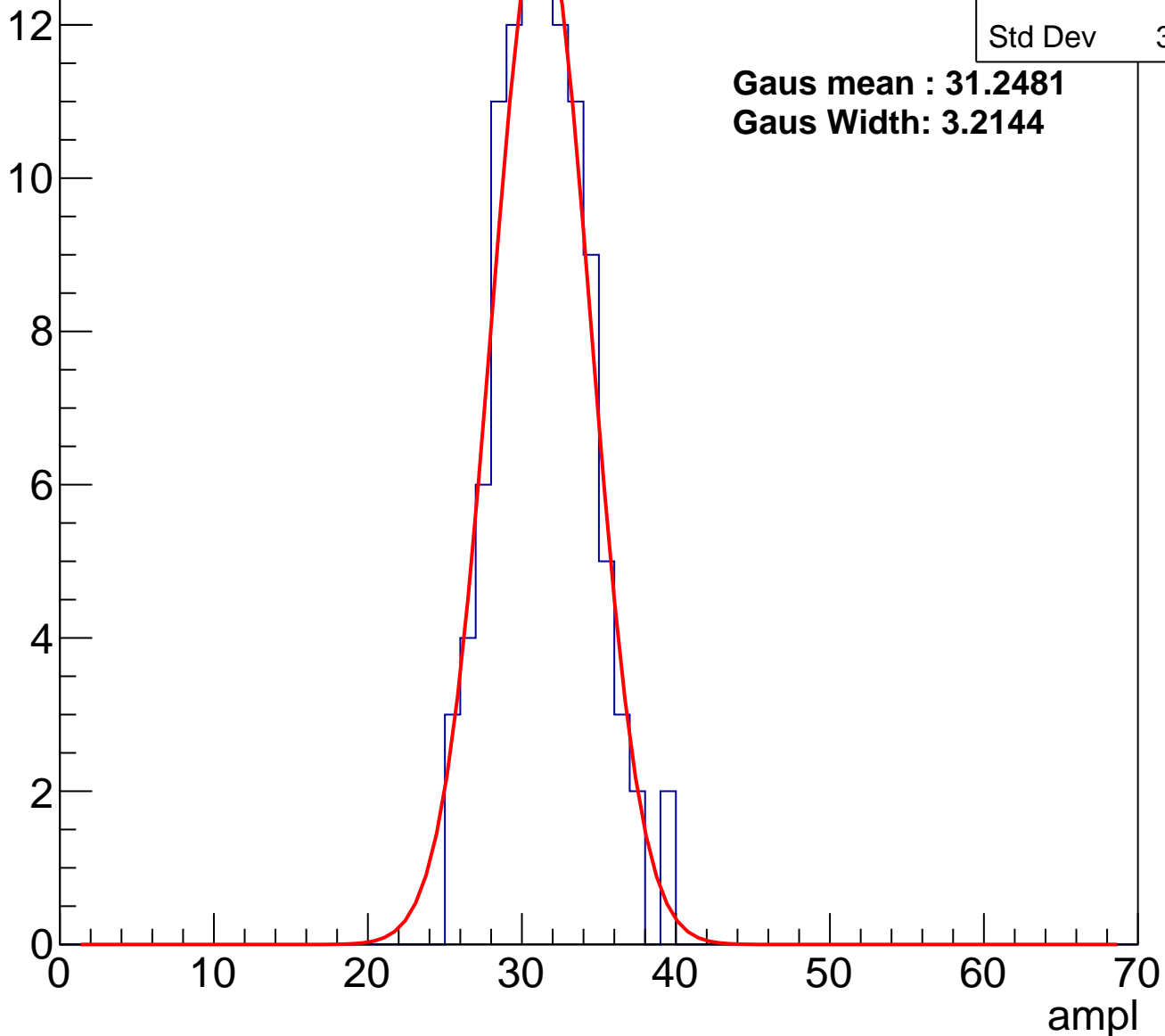
calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	30.92
Std Dev	3.002

Gaus mean : 31.2481

Gaus Width: 3.2144

Entry



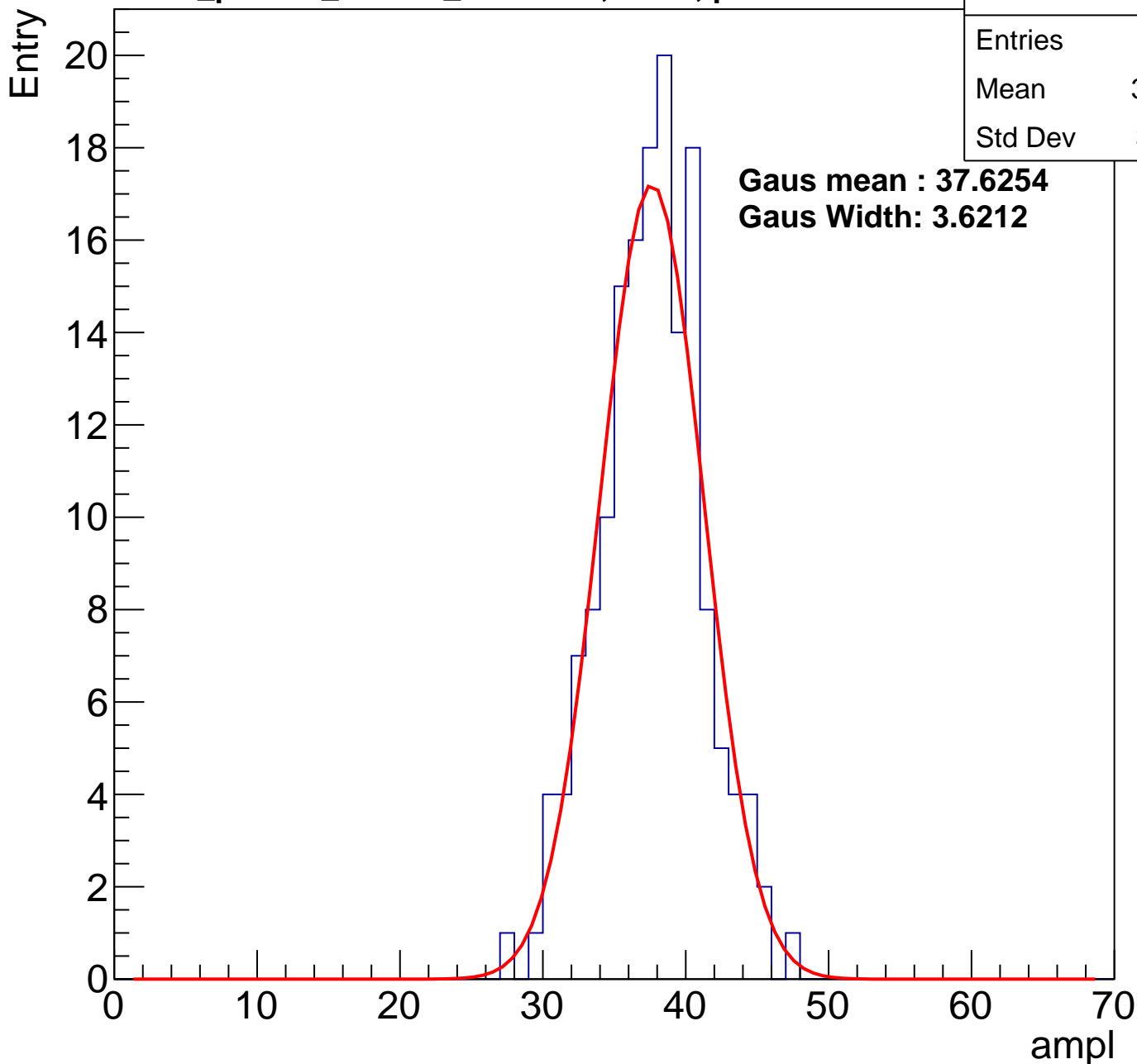
B0L101S, U21-ch29, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	37.15
Std Dev	3.561

Gaus mean : 37.6254

Gaus Width: 3.6212



B0L101S, U21-ch29, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

142

Mean

44.89

Std Dev

3.284

Gaus mean : 45.7250

Gaus Width: 3.3458

0

10

20

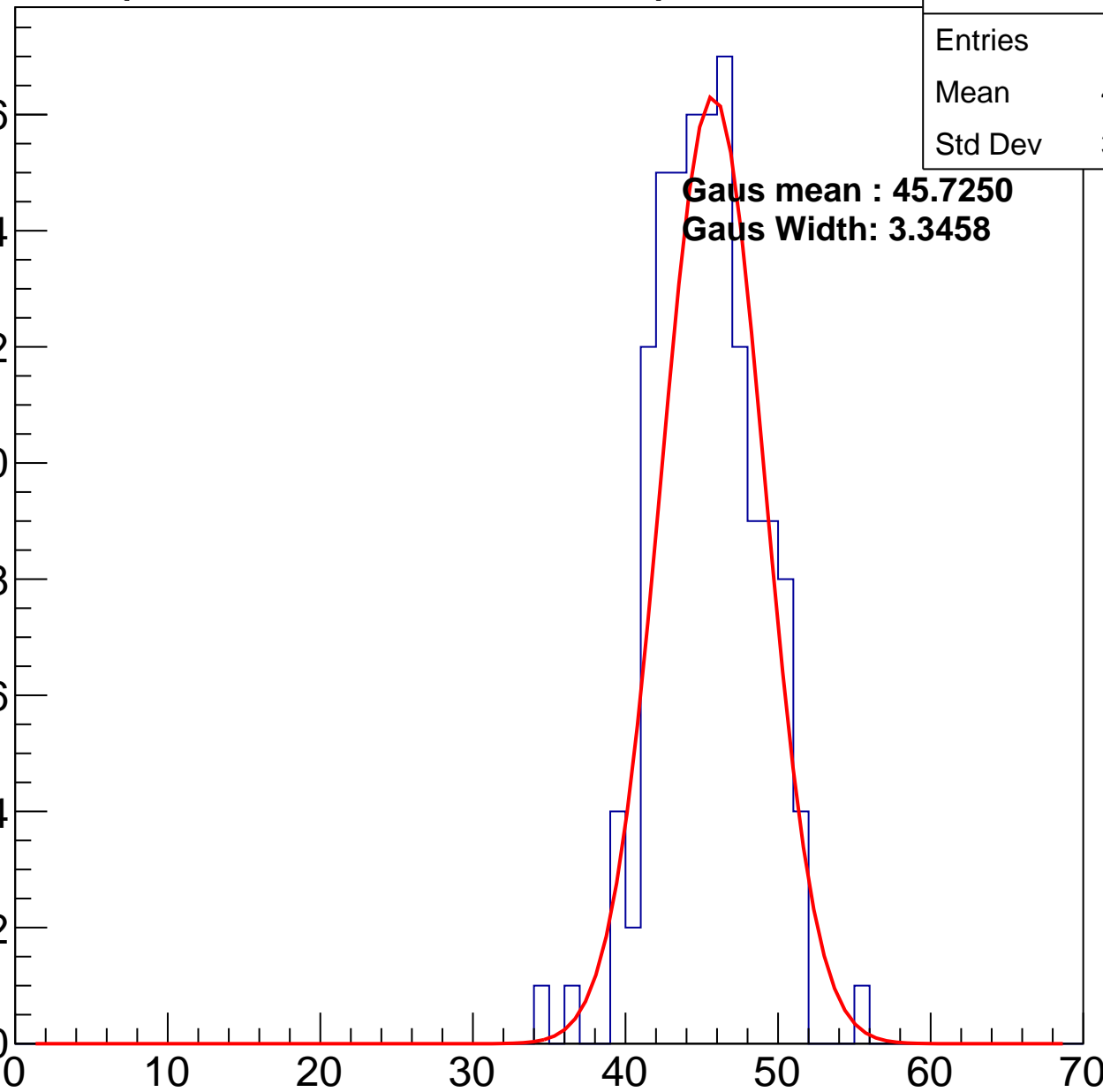
30

40

50

60

ampl



B0L101S, U21-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	50.78
Std Dev	2.993

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

1

1

2

3

6

10

8

10

12

14

9

5

3

2

2

0

B0L101S, U21-ch29, adc4

calib_packv5_042523_0143.root, FC#1, port C1

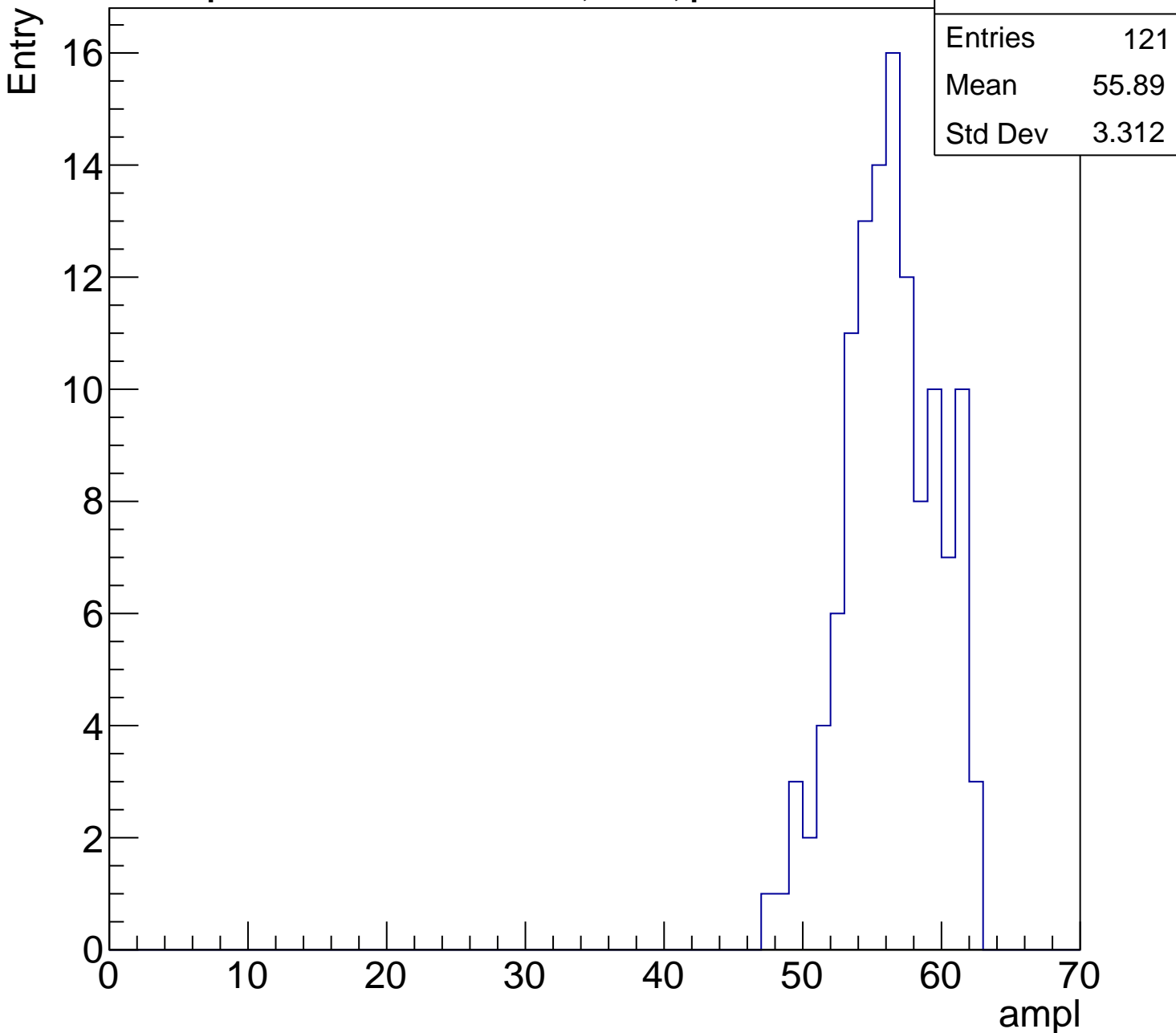
Entries	121
Mean	55.89
Std Dev	3.312

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch29, adc5

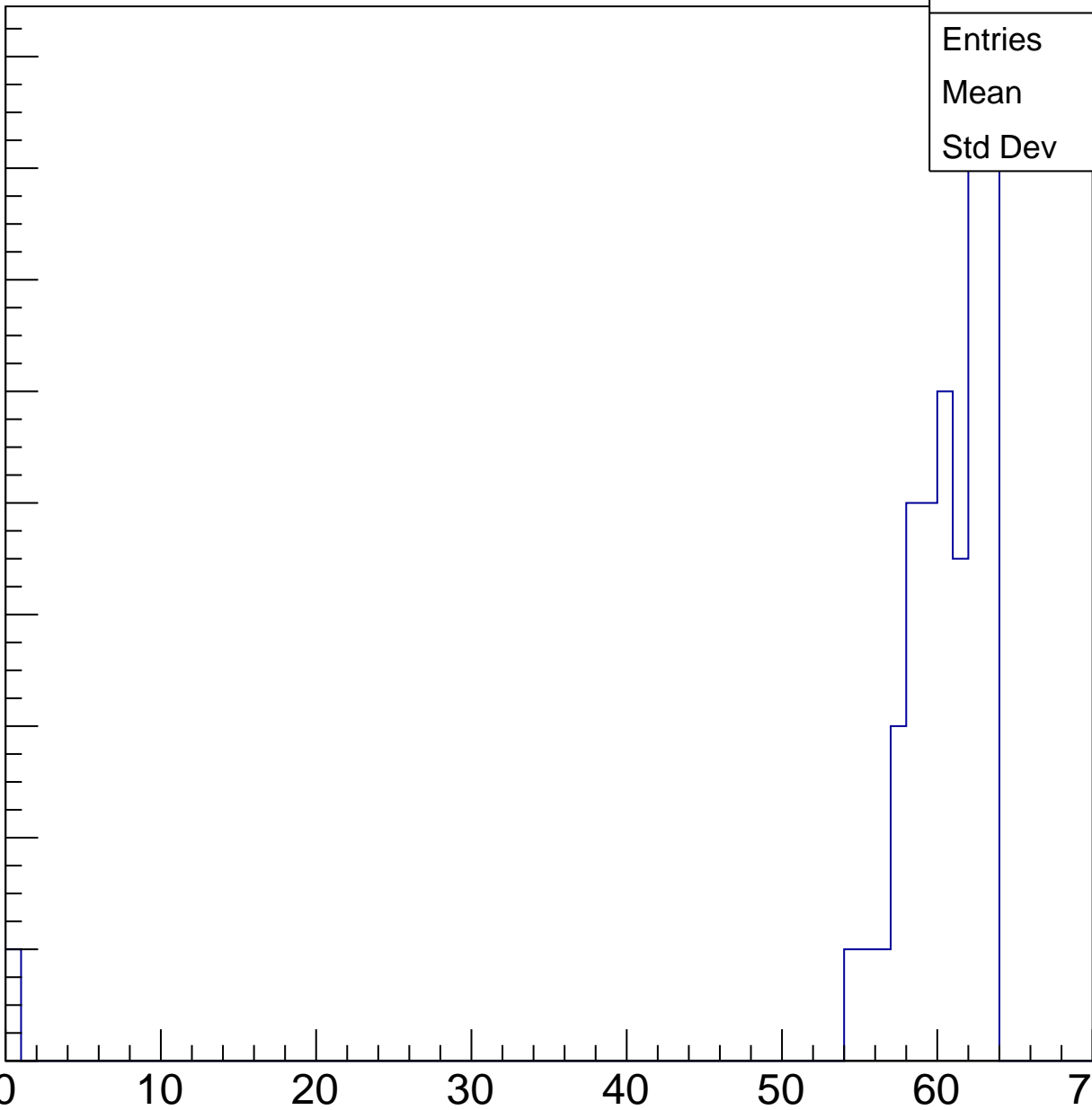
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	90
Mean	58.88
Std Dev	9.179

ampl



B0L101S, U21-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

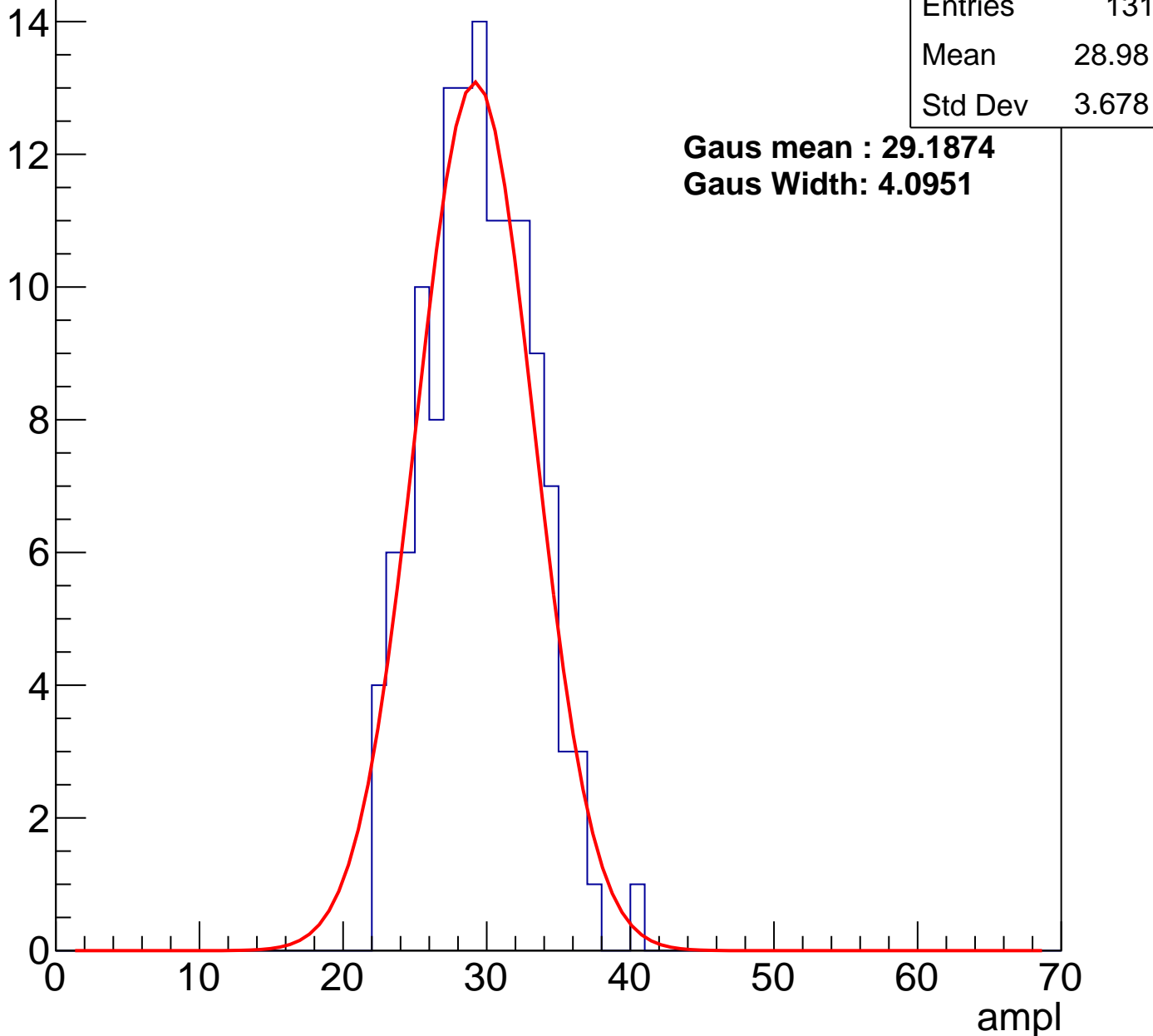
Entry



B0L101S, U21-ch30, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch30, adc1

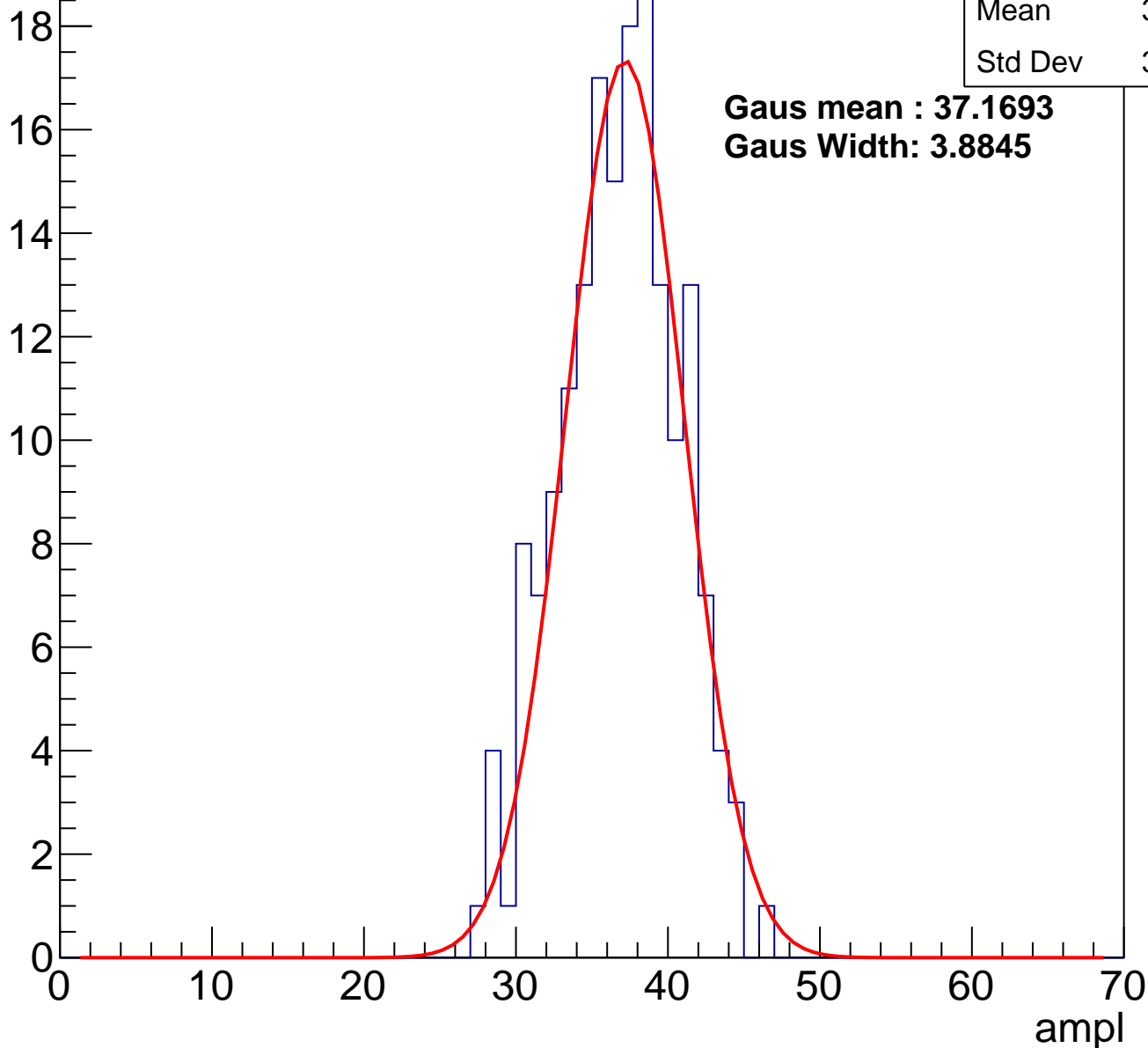
calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	36.35
Std Dev	3.847

Gaus mean : 37.1693

Gaus Width: 3.8845

Entry



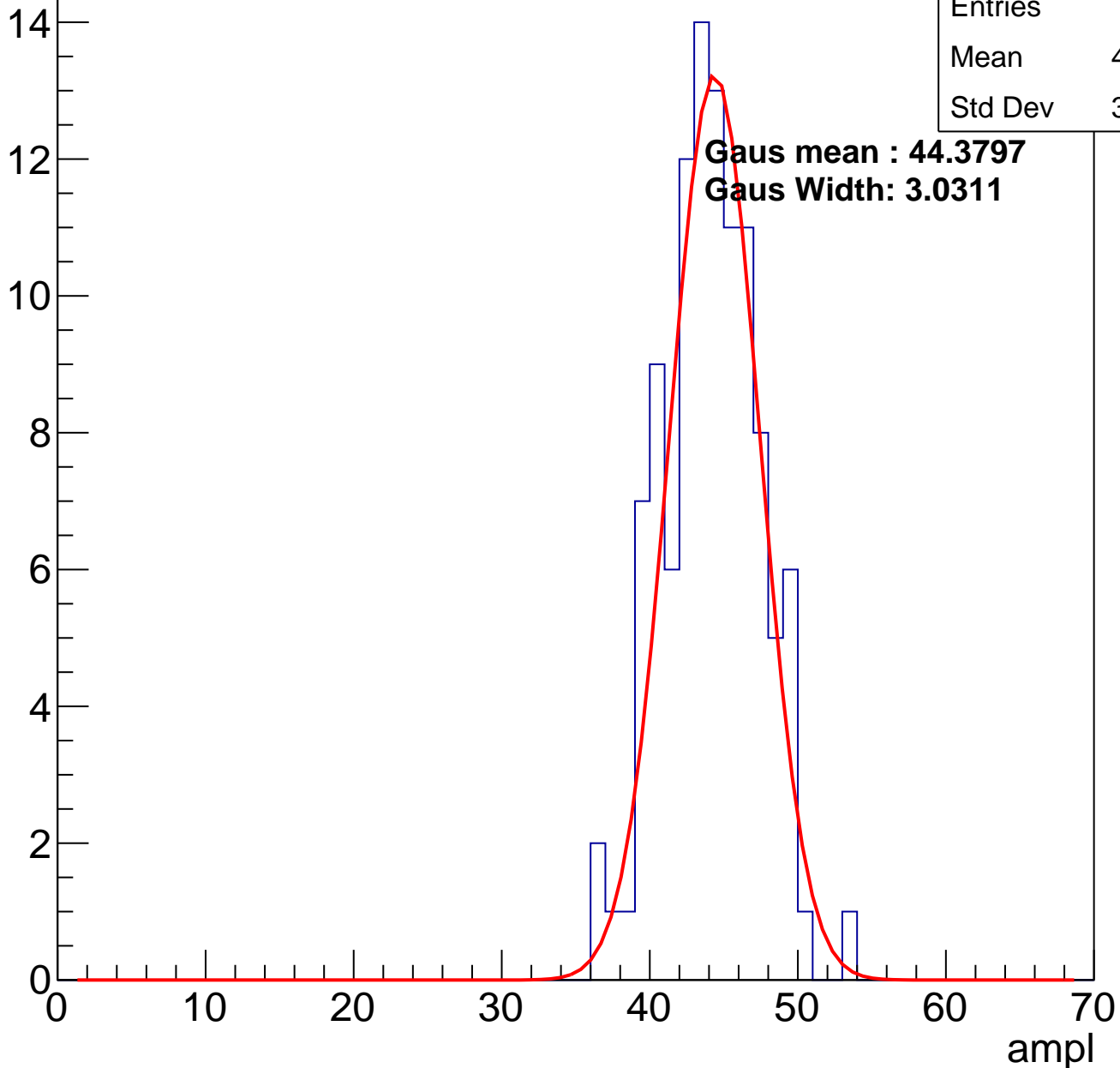
B0L101S, U21-ch30, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	43.69
Std Dev	3.208

Gaus mean : 44.3797
Gaus Width: 3.0311

Entry



B0L101S, U21-ch30, adc3

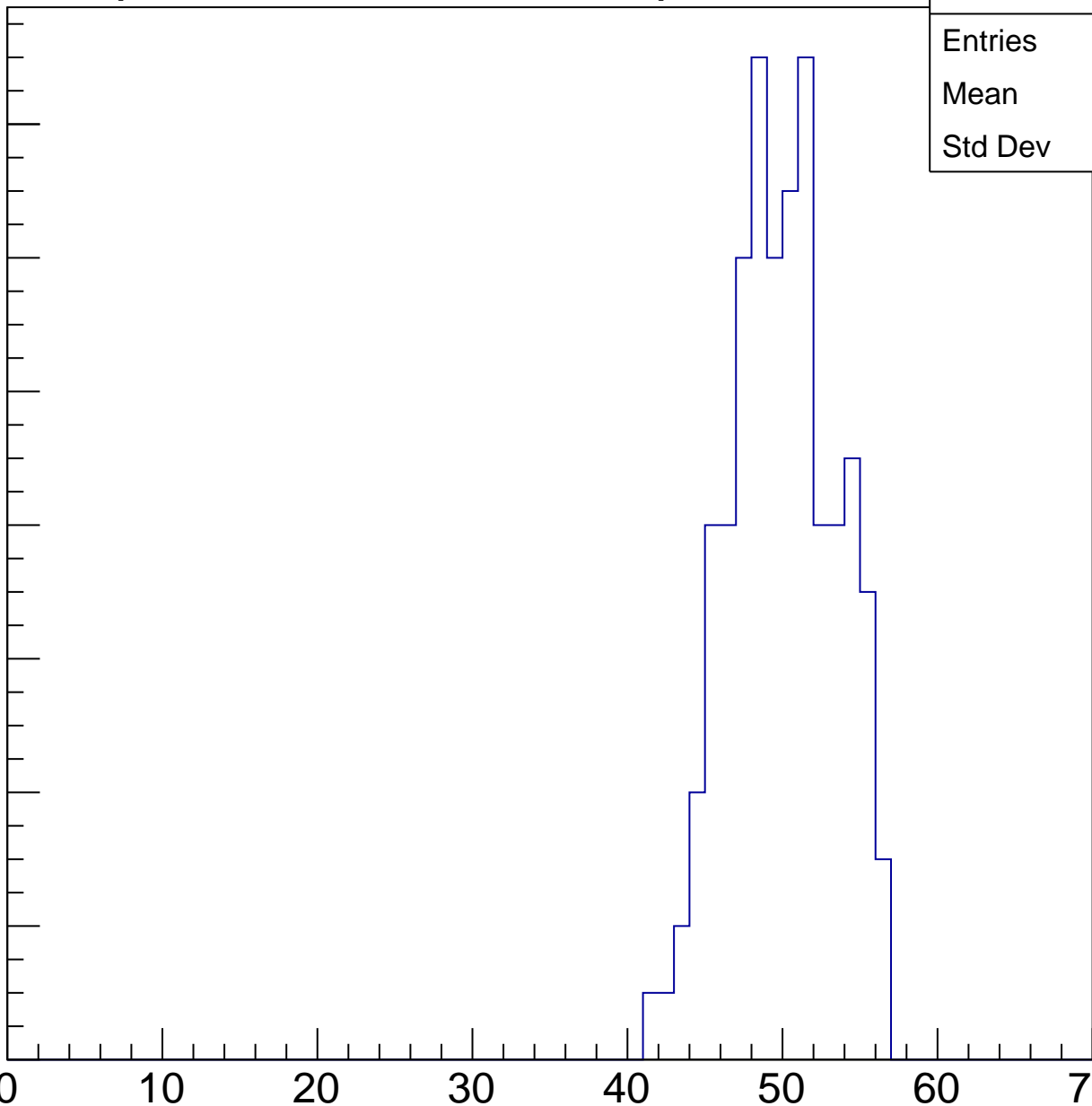
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	126
Mean	49.52
Std Dev	3.347

ampl



B0L101S, U21-ch30, adc4

calib_packv5_042523_0143.root, FC#1, port C1

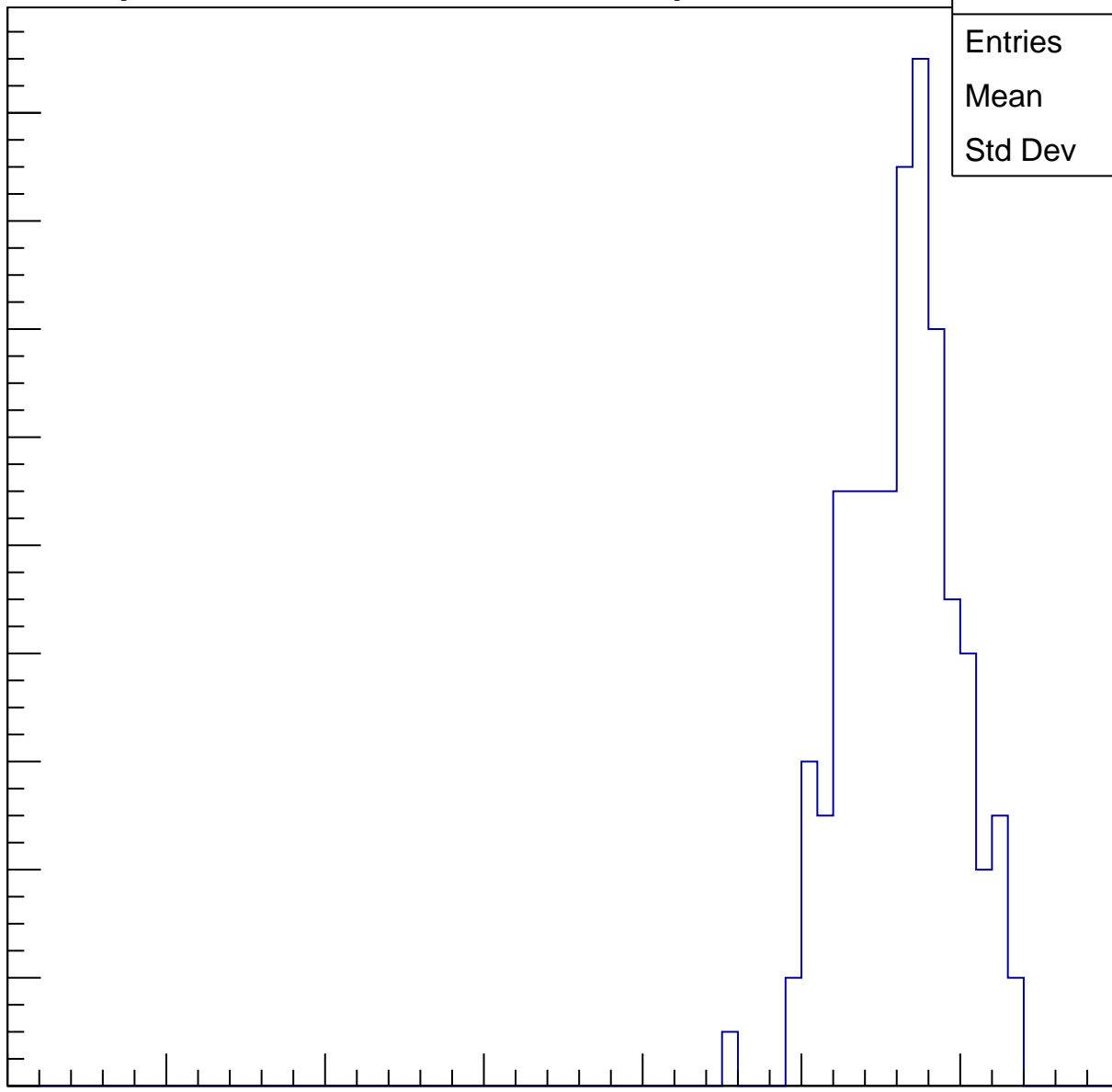
Entries	136
Mean	55.81
Std Dev	3.373

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch30, adc5

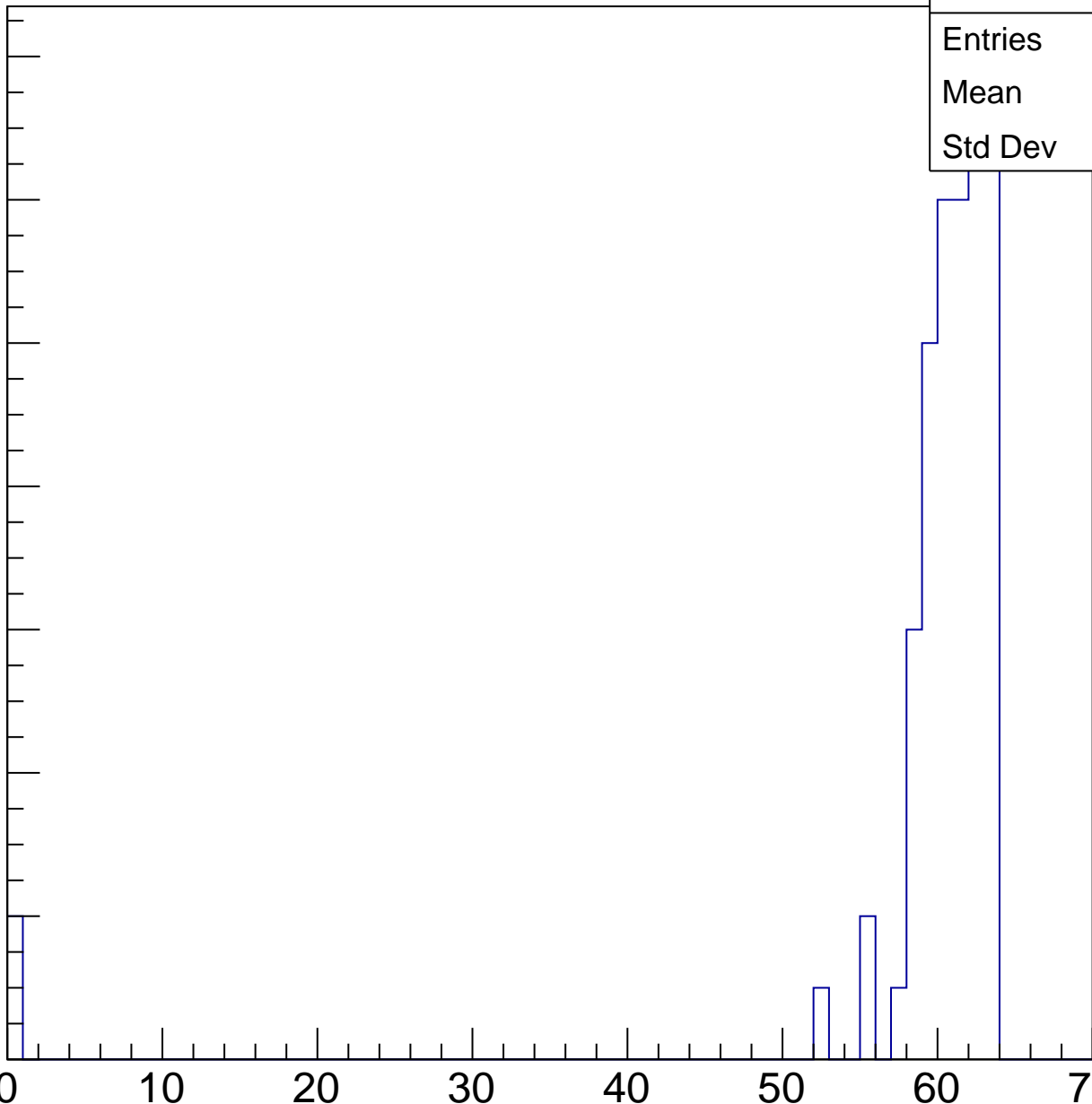
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	58.86
Std Dev	10.1

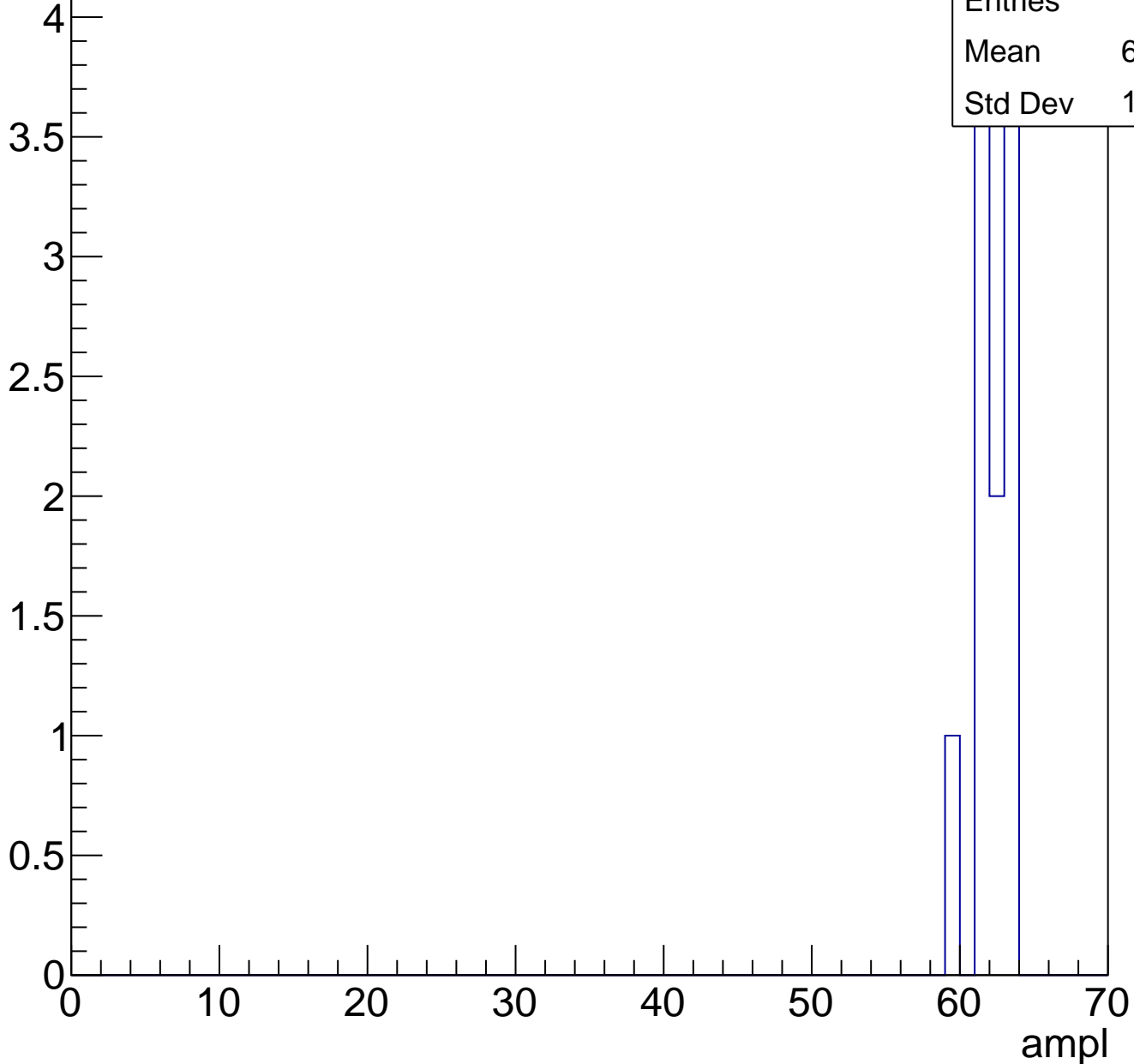
ampl



B0L101S, U21-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L101S, U21-ch31, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	30.56
Std Dev	2.878

Gaus mean : 31.0541

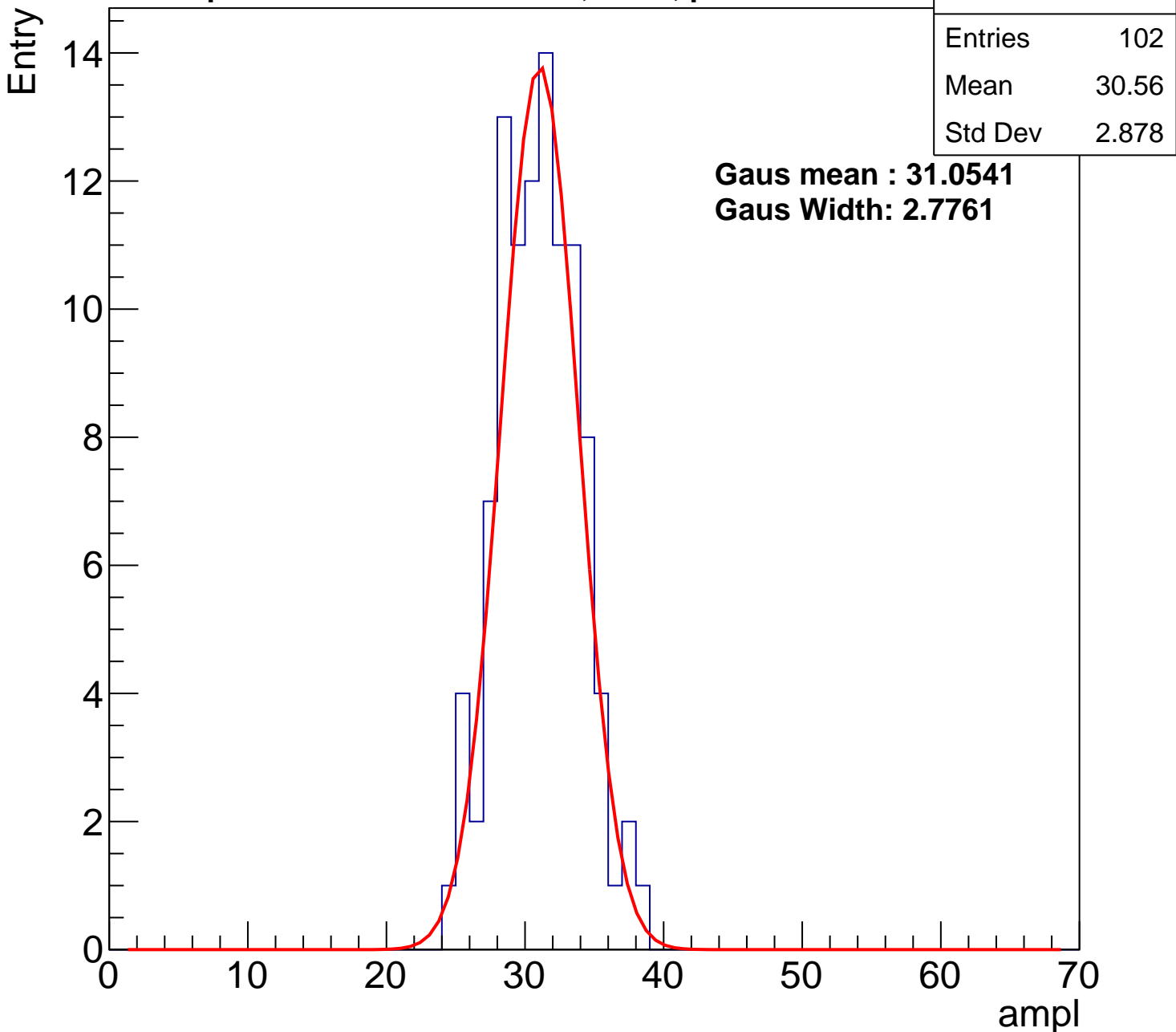
Gaus Width: 2.7761

Entry

14
12
10
8
6
4
2
0

ampl

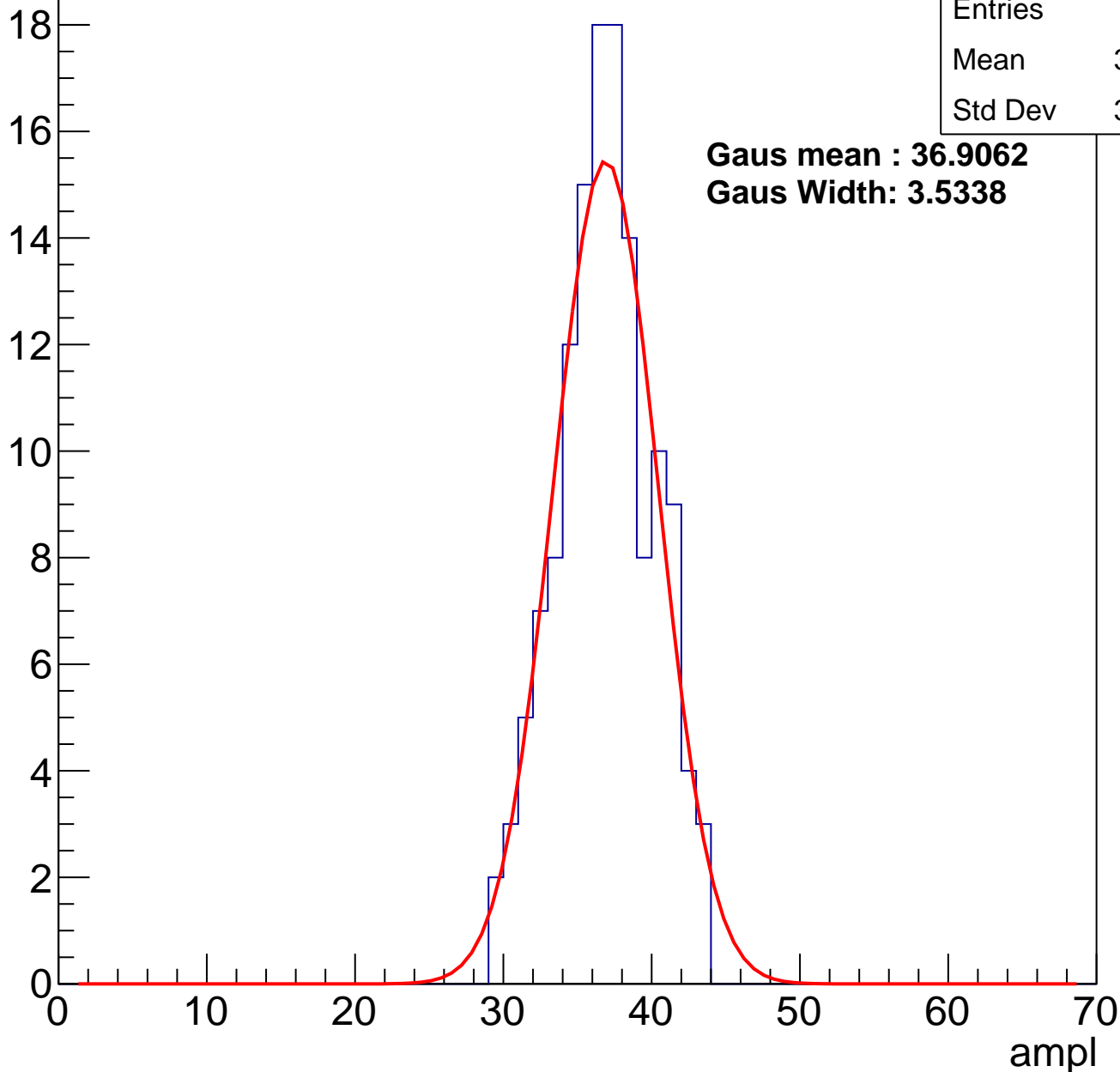
0 10 20 30 40 50 60 70



B0L101S, U21-ch31, adc1

calib_packv5_042523_0143.root, FC#1, port C1

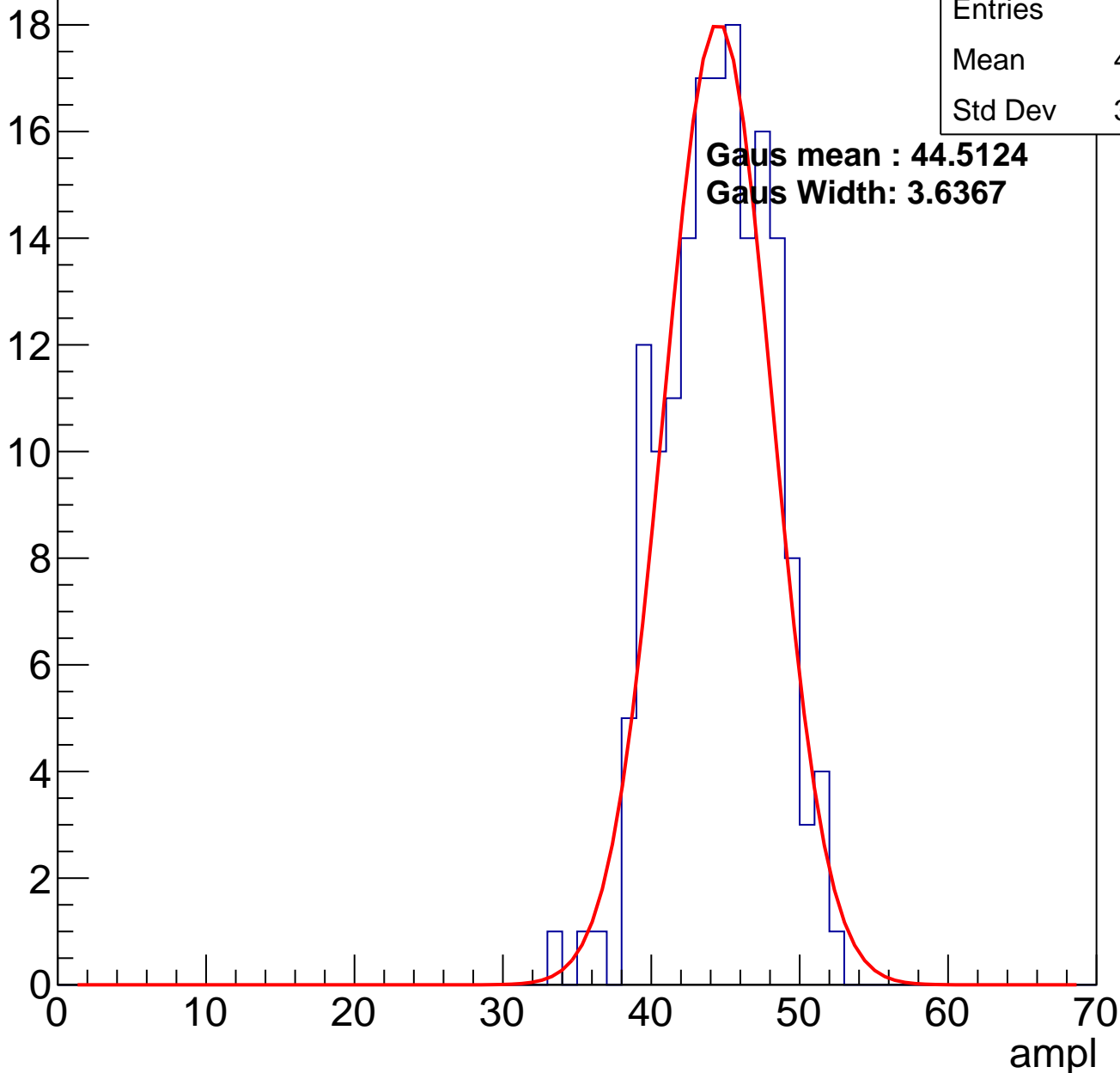
Entry



B0L101S, U21-ch31, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

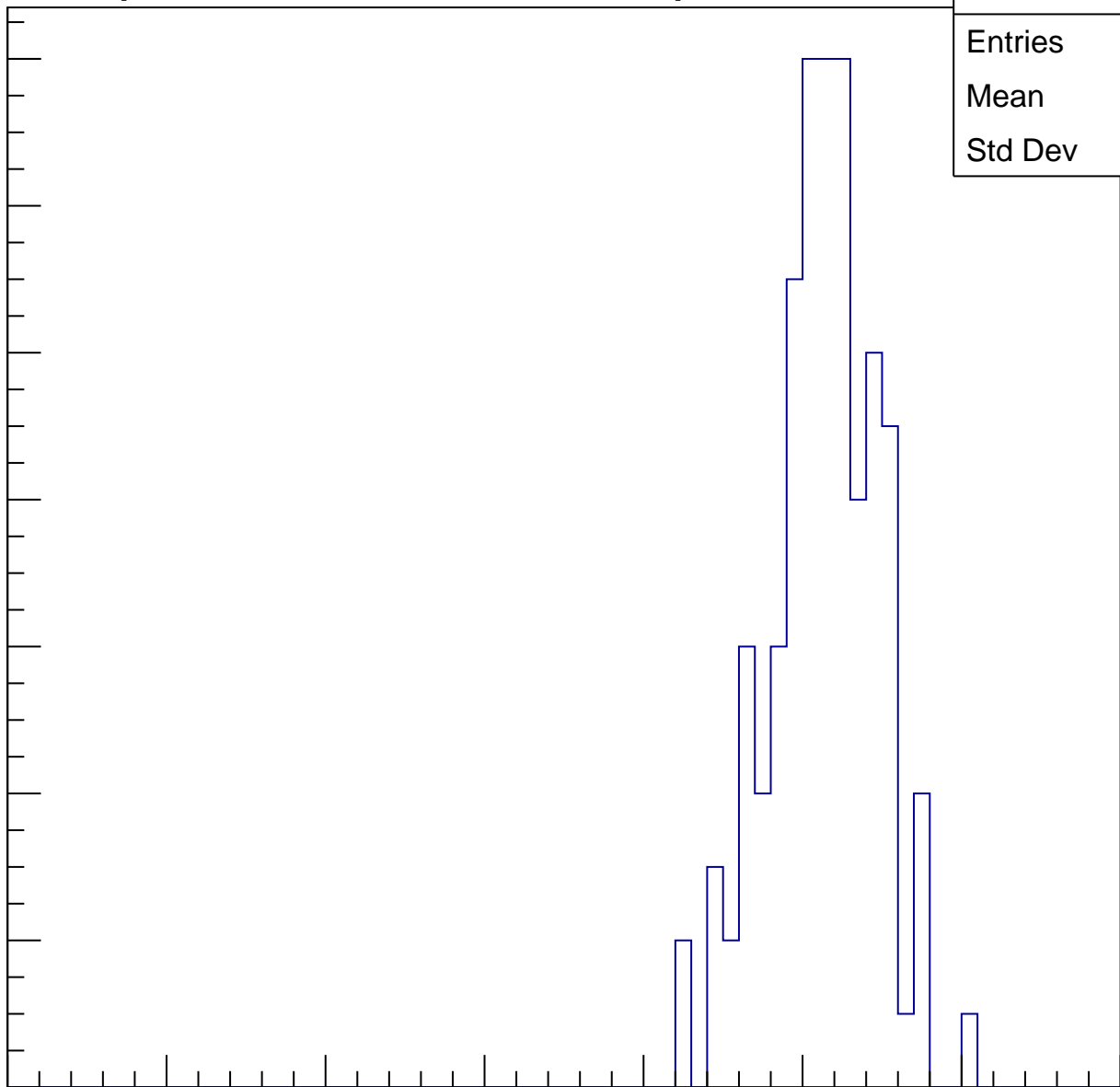
Entries	109
Mean	50.84
Std Dev	3.365

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

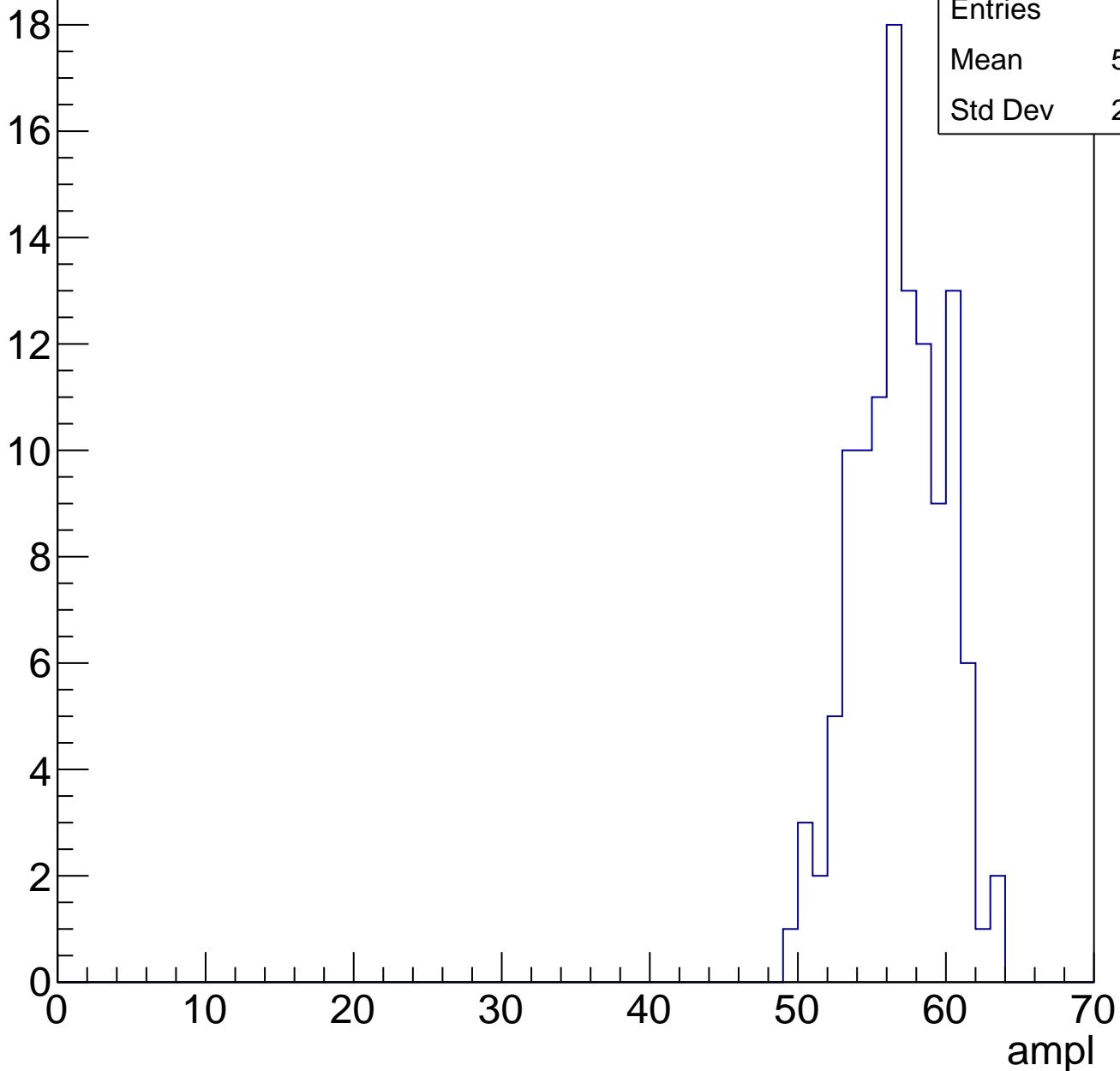
ampl



B0L101S, U21-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch31, adc5

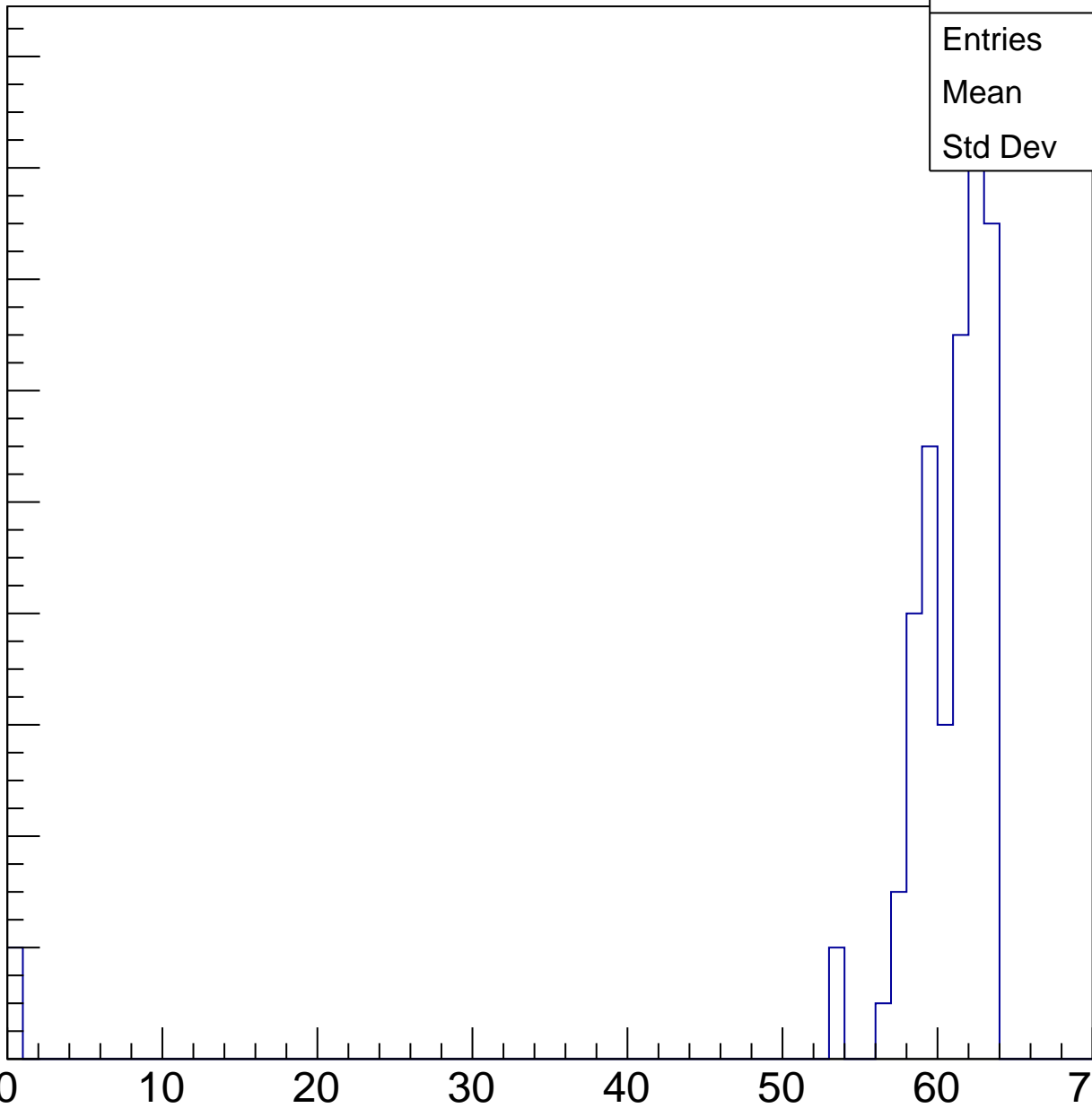
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	79
Mean	58.99
Std Dev	9.758

ampl



B0L101S, U21-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch32, adc0

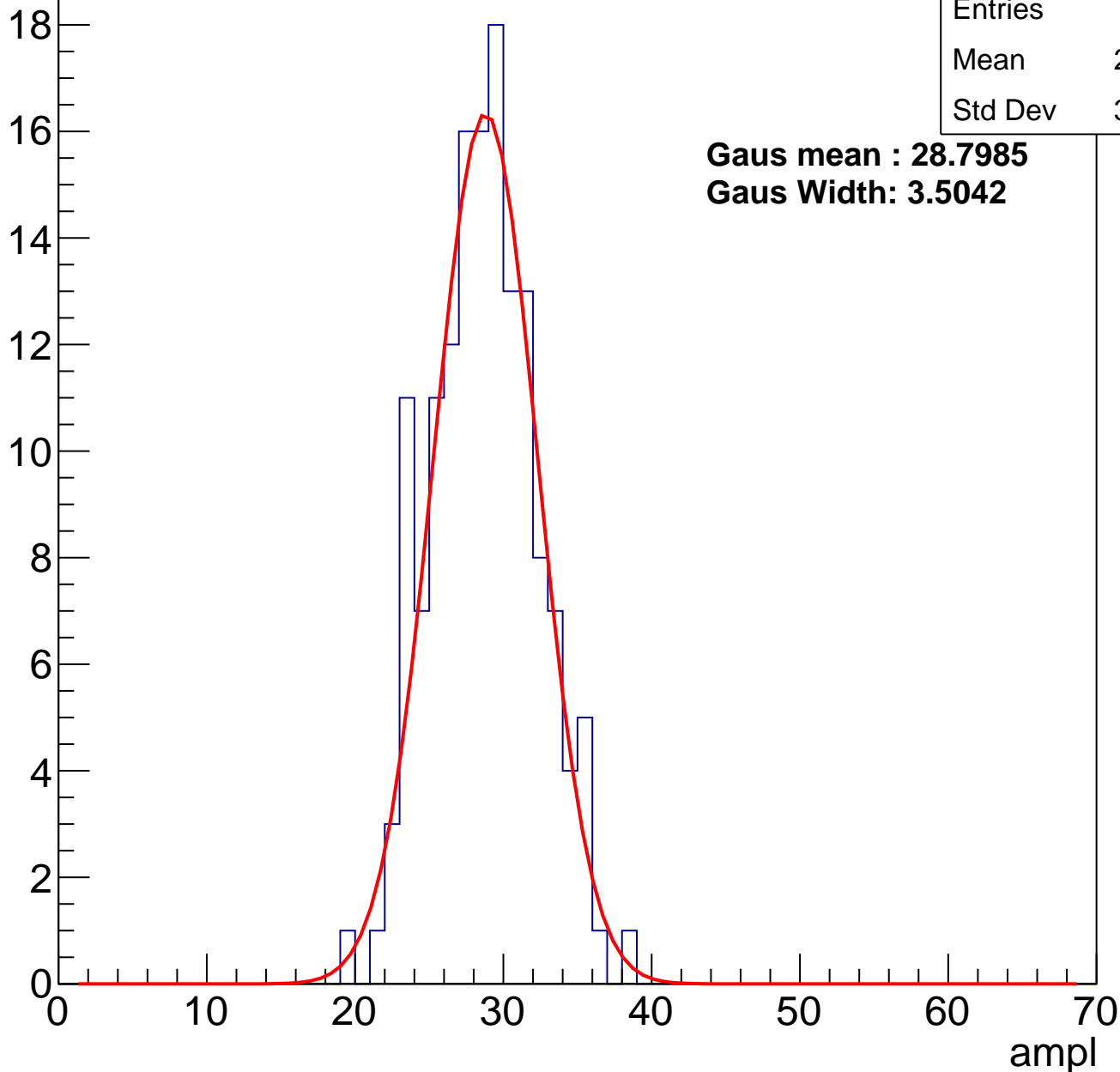
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	28.25
Std Dev	3.508

Gaus mean : 28.7985

Gaus Width: 3.5042

Entry



B0L101S, U21-ch32, adc1

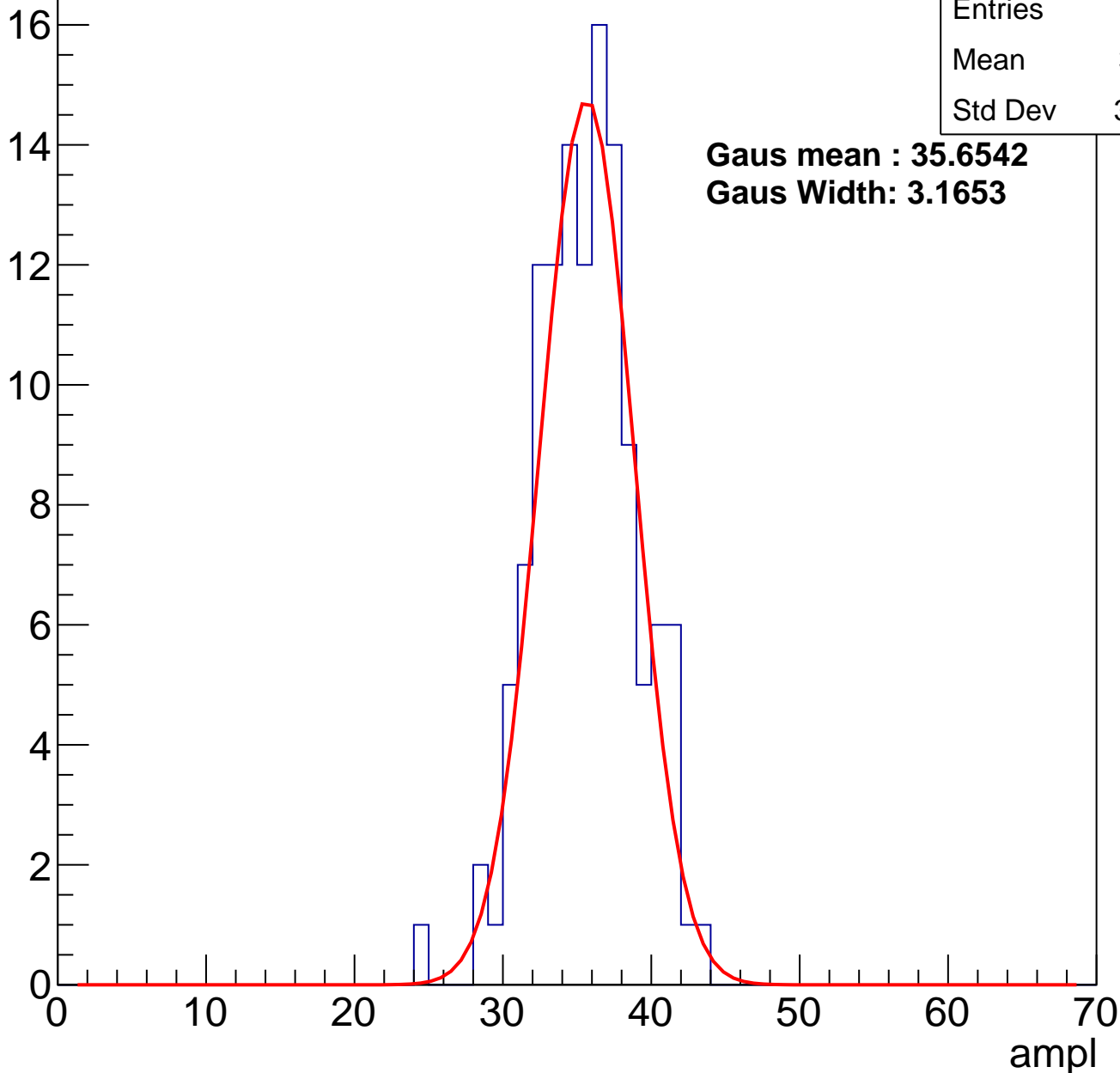
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	35.11
Std Dev	3.327

Gaus mean : 35.6542

Gaus Width: 3.1653

Entry



B0L101S, U21-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

157

Mean

42

Std Dev

3.457

Gaus mean : 42.4542

Gaus Width: 3.4562



B0L101S, U21-ch32, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

124

Mean

49.46

Std Dev

3.373

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch32, adc4

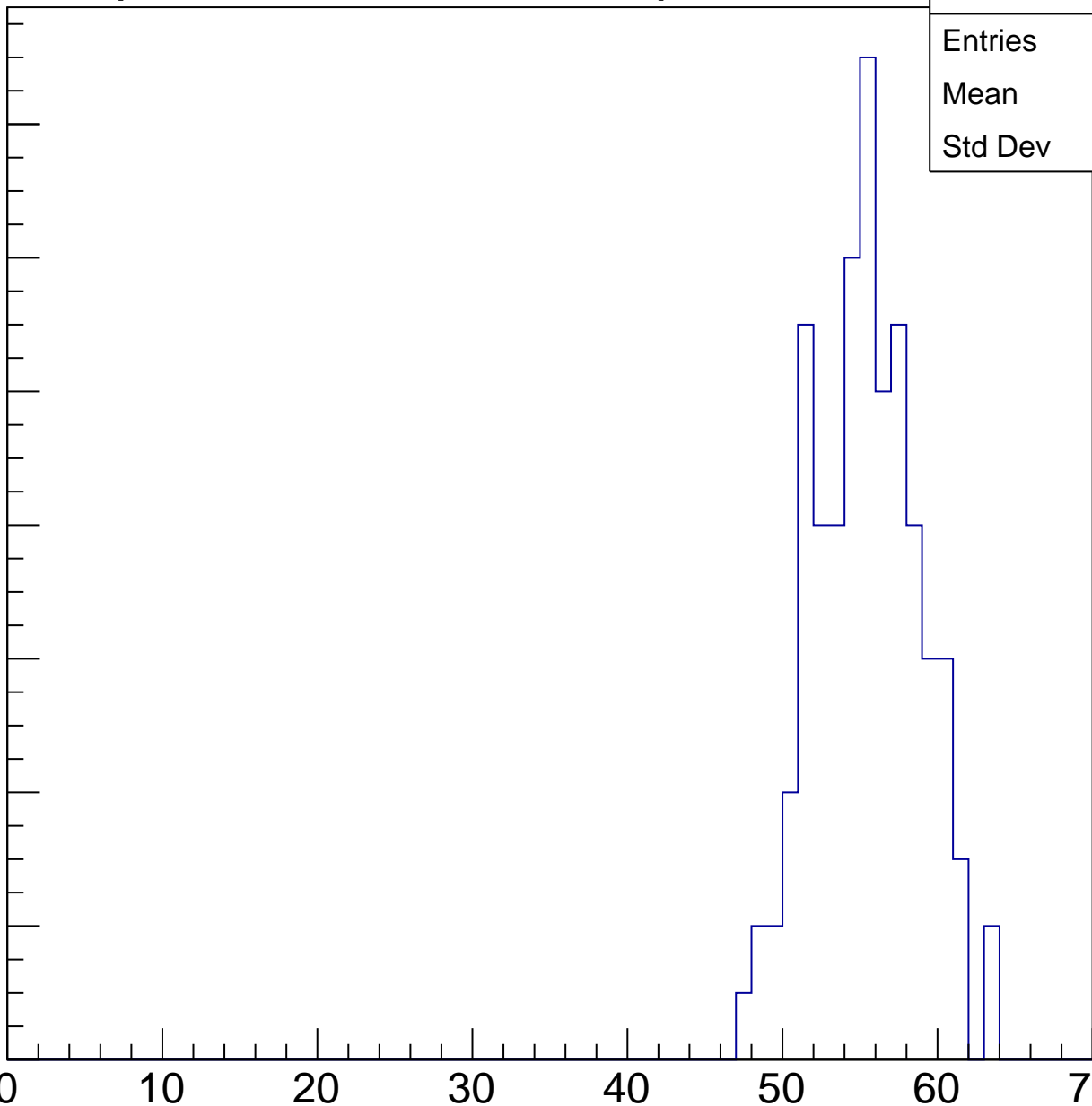
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	54.94
Std Dev	3.364

ampl



B0L101S, U21-ch32, adc5

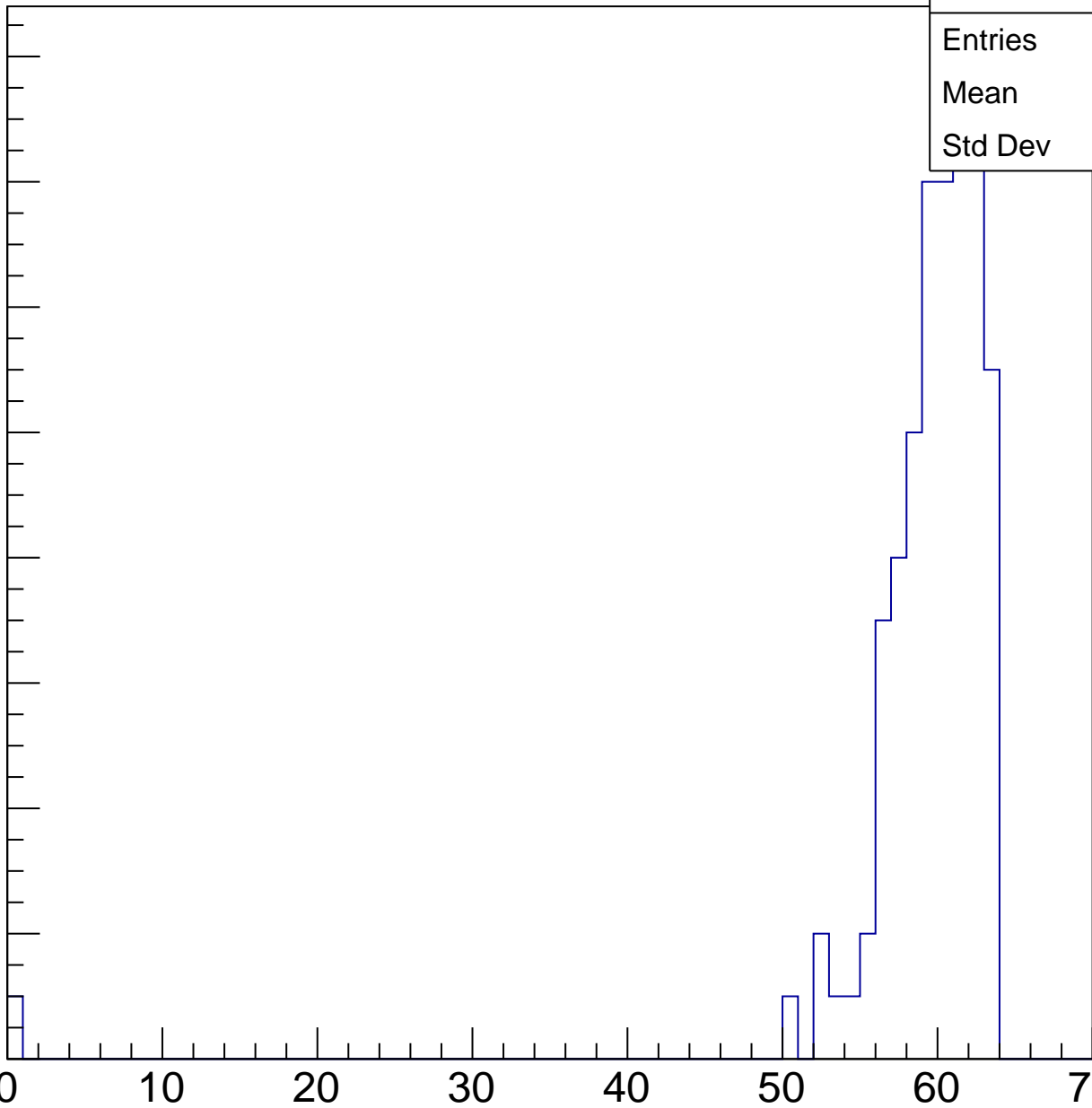
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.87
Std Dev	6.42

ampl

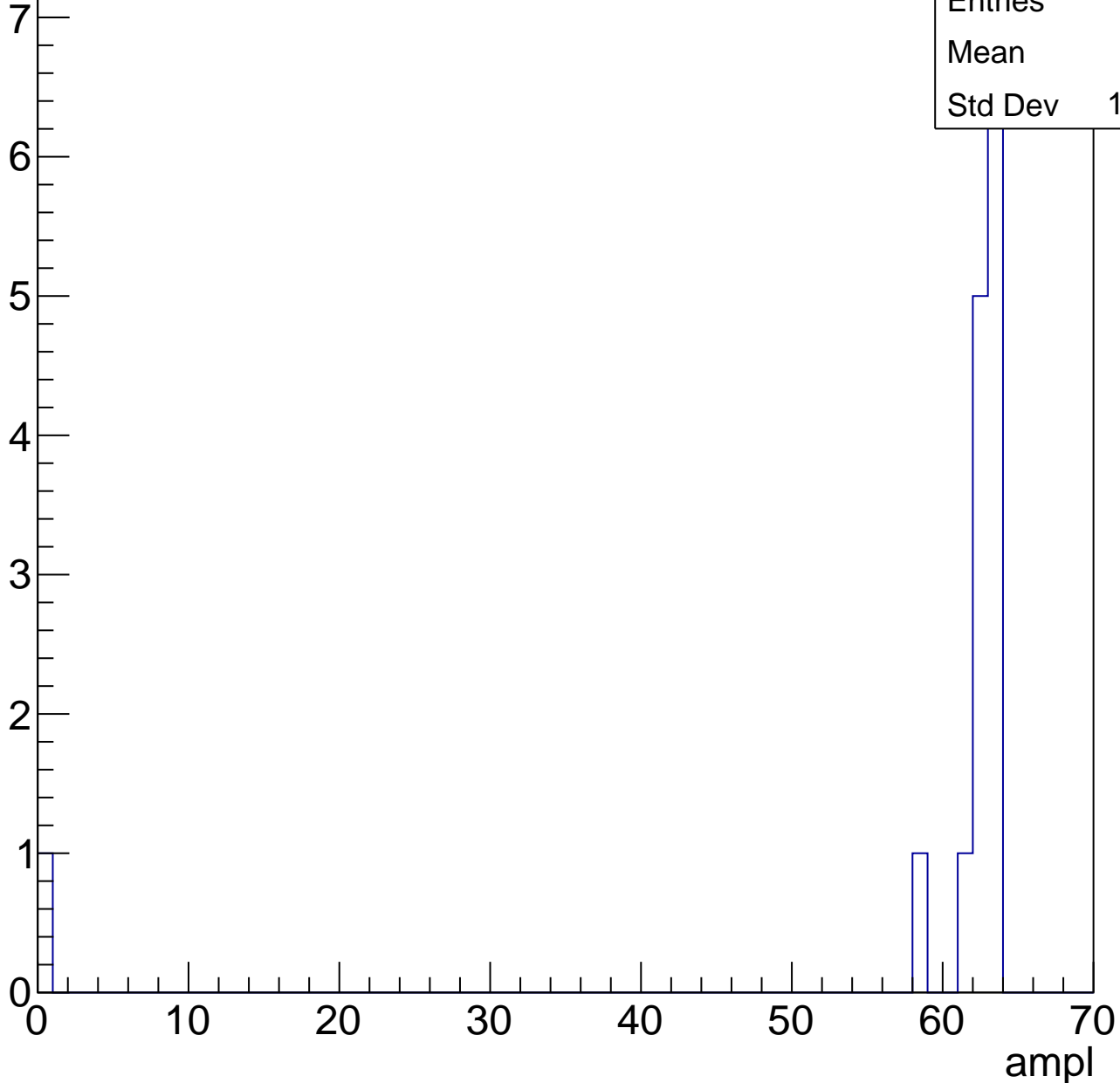


B0L101S, U21-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	58
Std Dev	15.55



B0L101S, U21-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch33, adc0

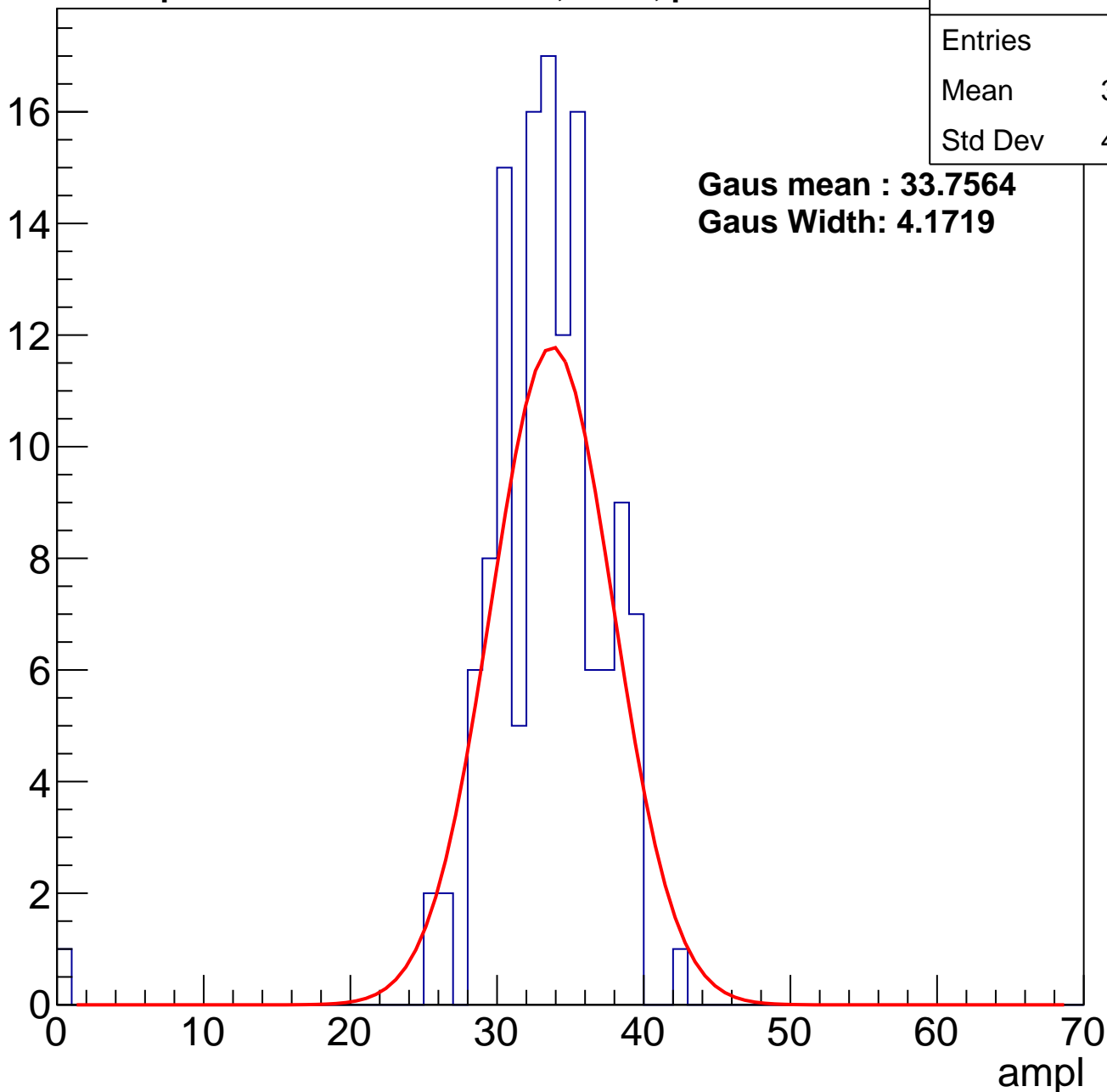
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	32.89
Std Dev	4.443

Gaus mean : 33.7564

Gaus Width: 4.1719

Entry



B0L101S, U21-ch33, adc1

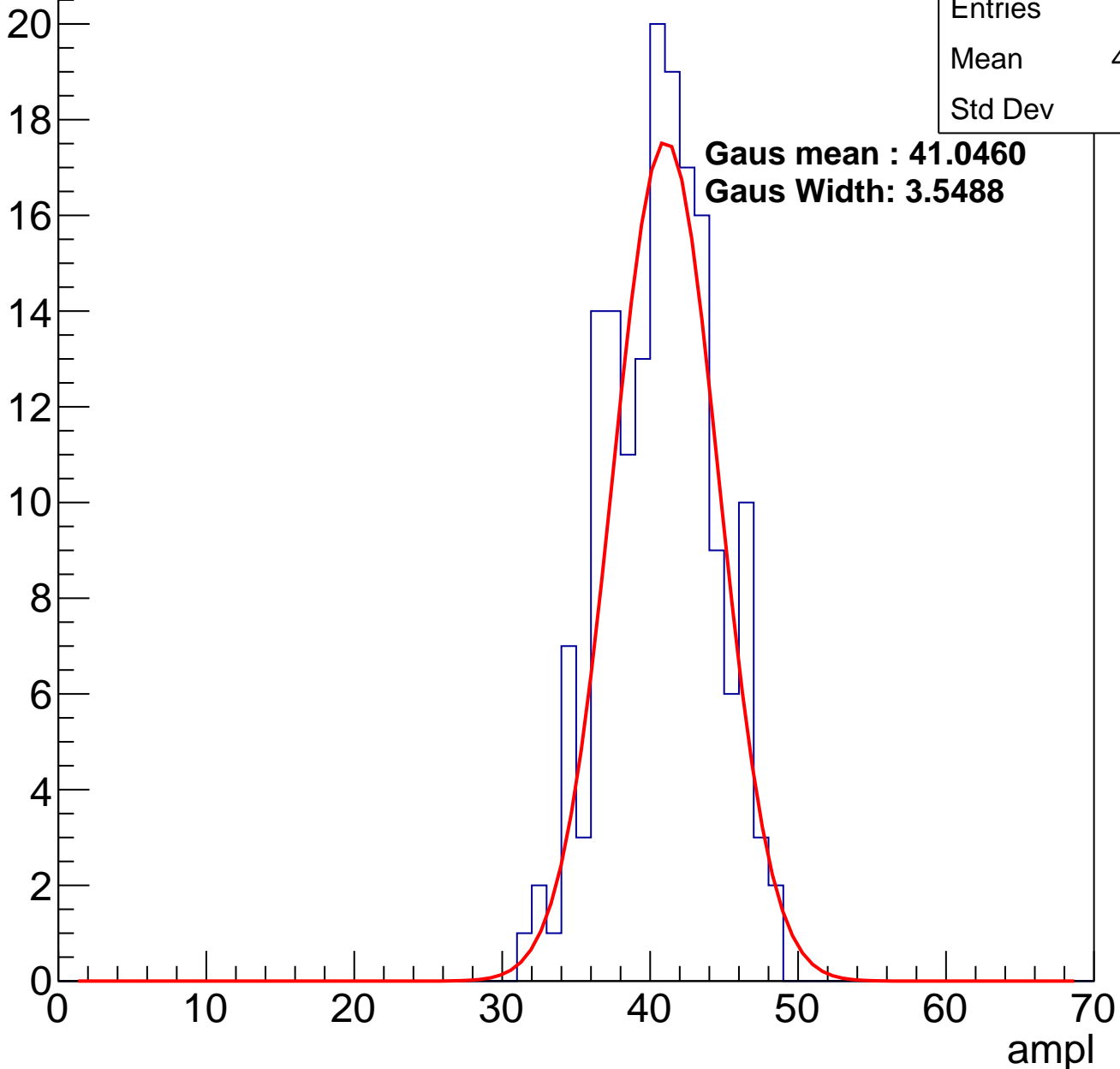
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	40.25
Std Dev	3.57

Gaus mean : 41.0460

Gaus Width: 3.5488

Entry



B0L101S, U21-ch33, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	48.42
Std Dev	3.181

Gaus mean : 48.8573

Gaus Width: 3.2351

Entry

16

14

12

10

8

6

4

2

0

0

10

20

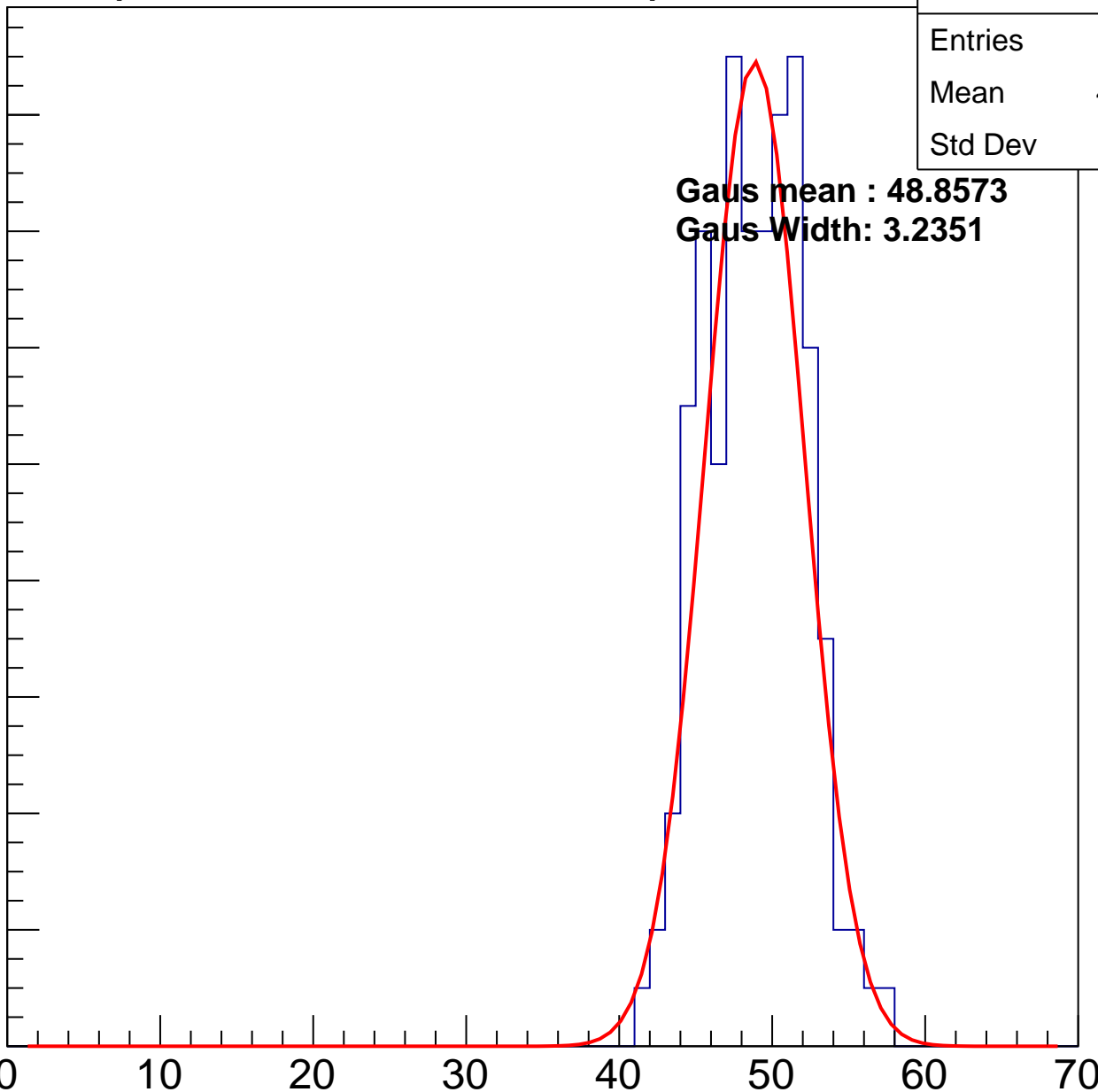
30

40

50

60

ampl

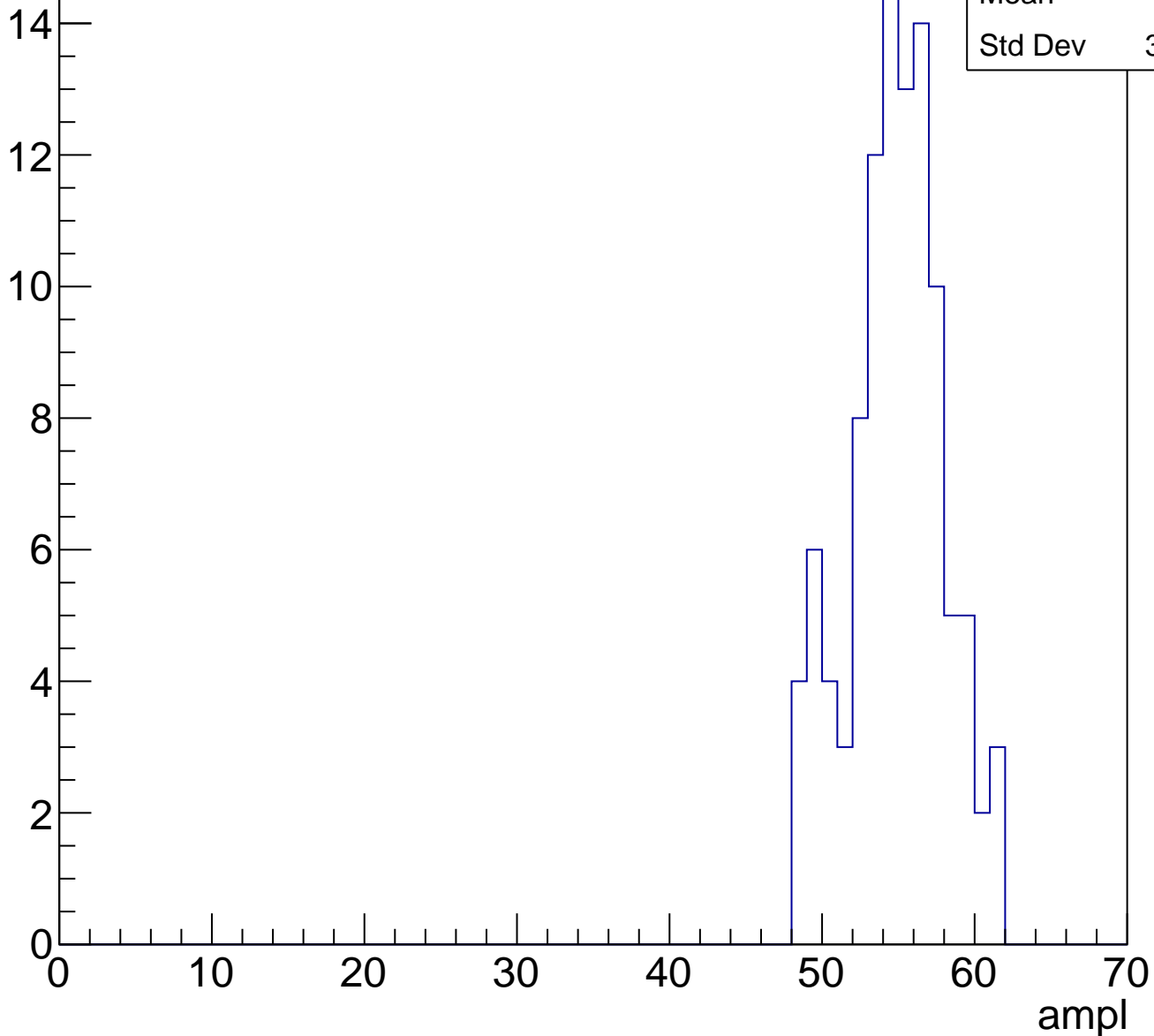


B0L101S, U21-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	54.4
Std Dev	3.093

Entry

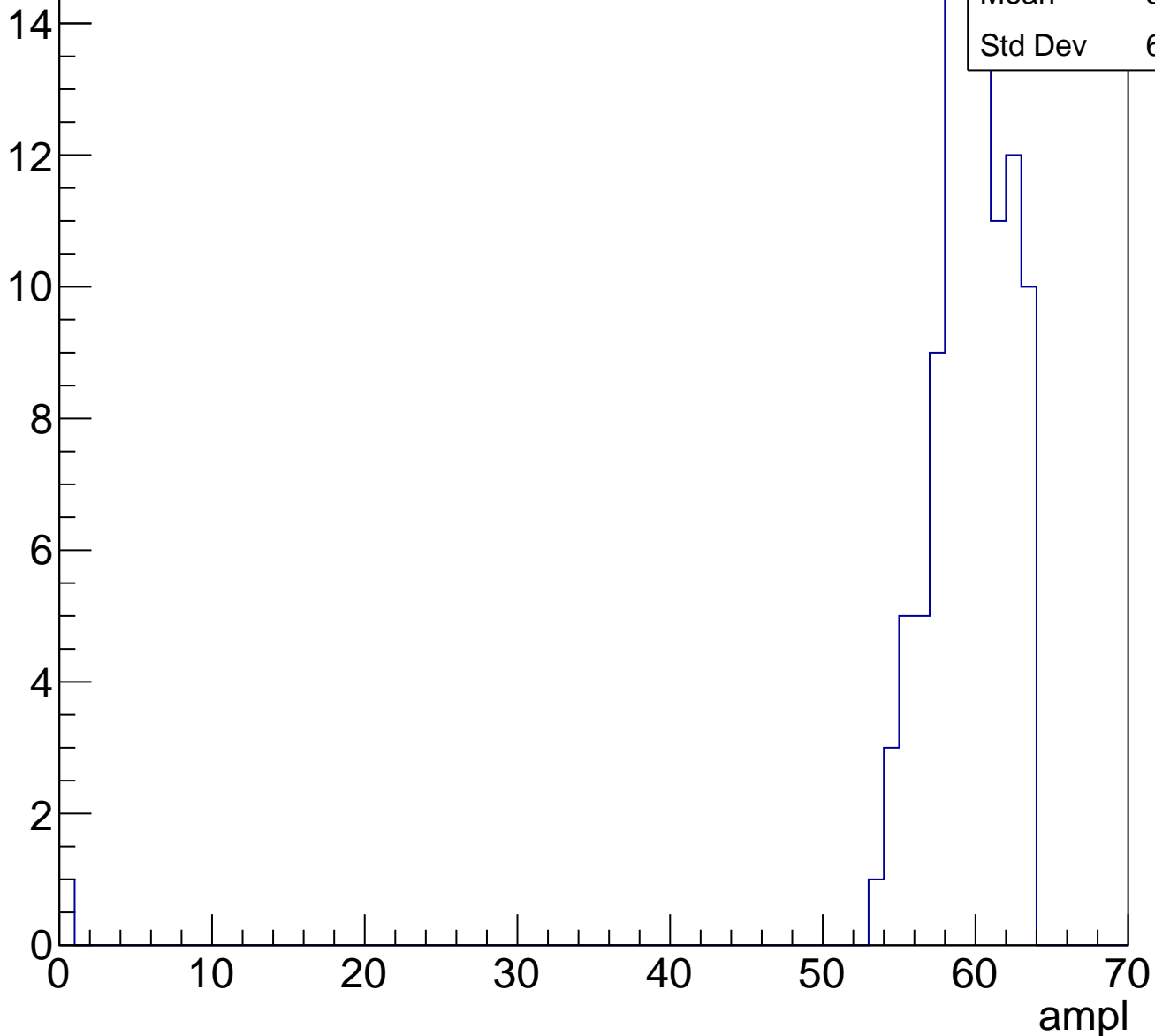


B0L101S, U21-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	58.66
Std Dev	6.327

Entry

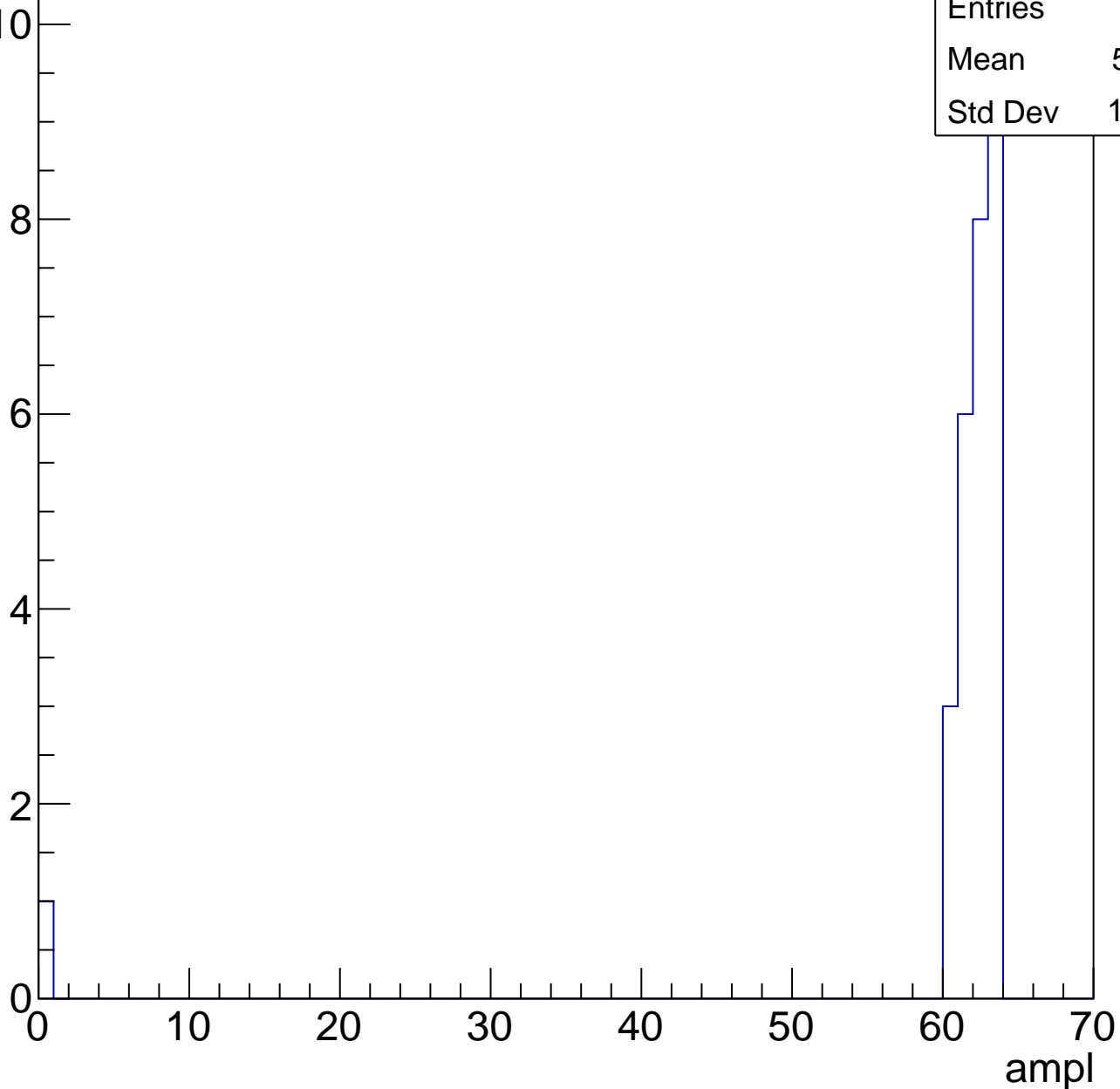


B0L101S, U21-ch33, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	28
Mean	59.71
Std Dev	11.54



B0L101S, U21-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

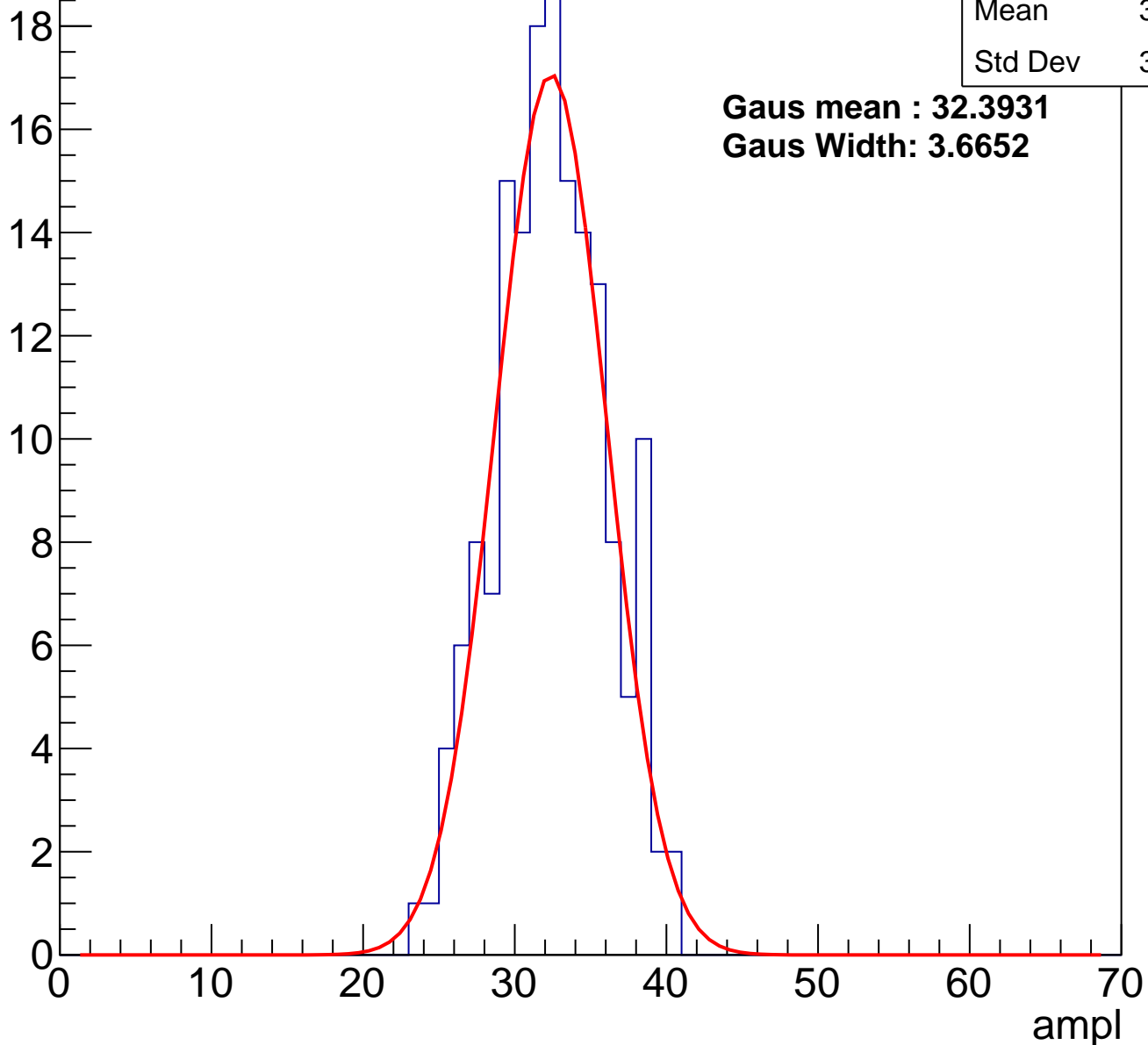


Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch34, adc0

calib_packv5_042523_0143.root, FC#1, port C1

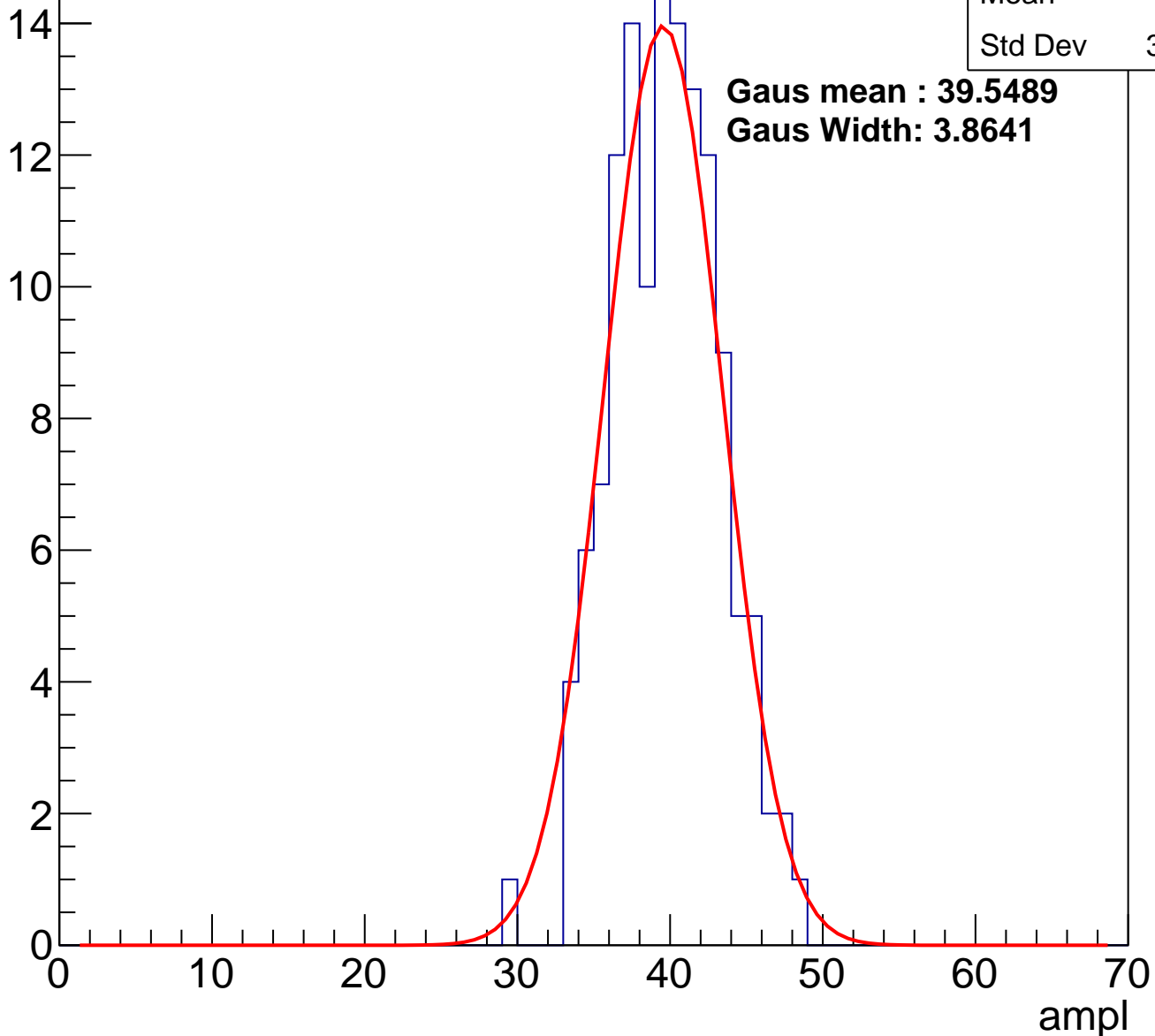
Entry



B0L101S, U21-ch34, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch34, adc2

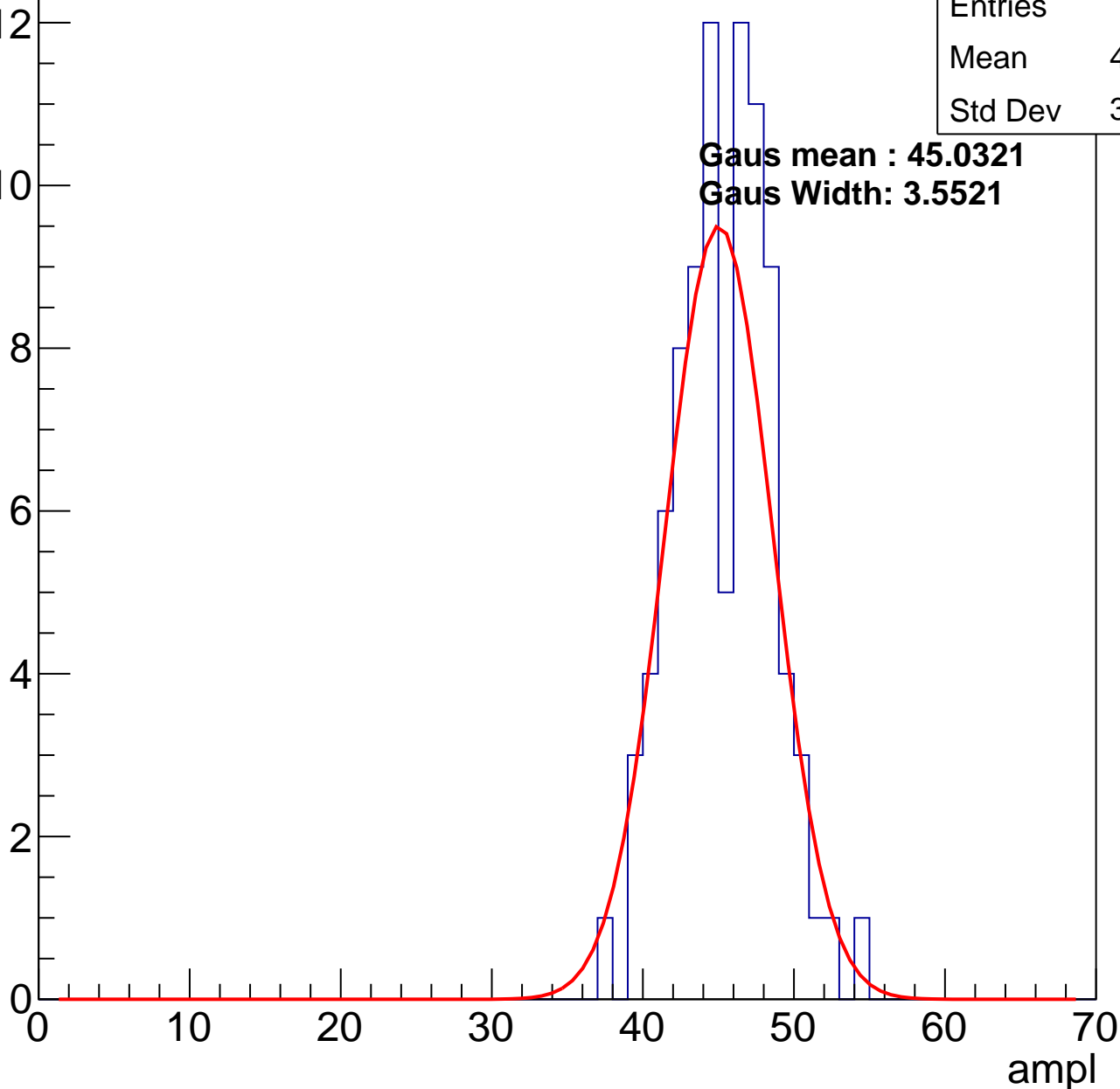
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	90
Mean	44.89
Std Dev	3.213

Gaus mean : 45.0321

Gaus Width: 3.5521

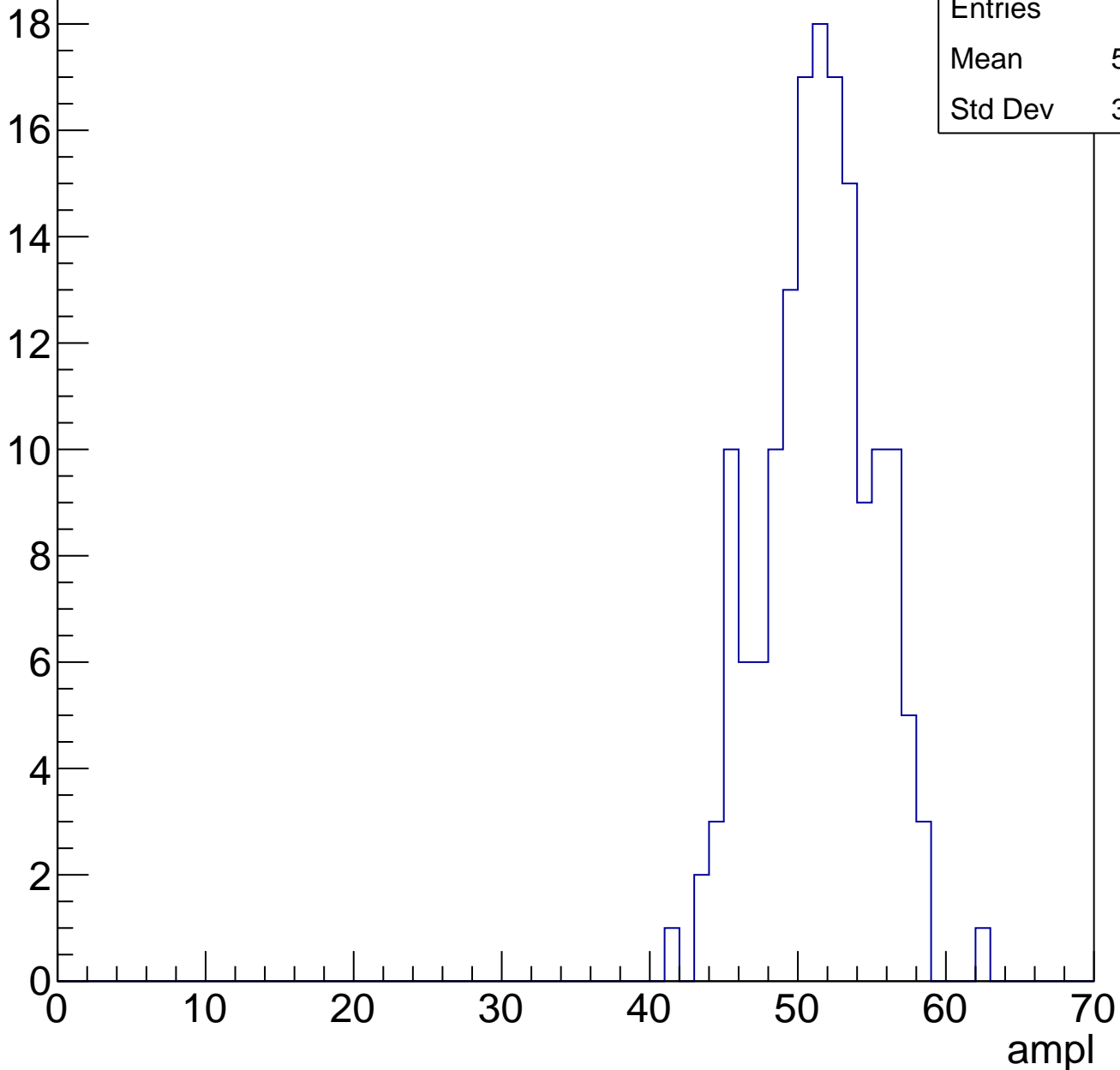


B0L101S, U21-ch34, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	50.95
Std Dev	3.717

Entry



B0L101S, U21-ch34, adc4

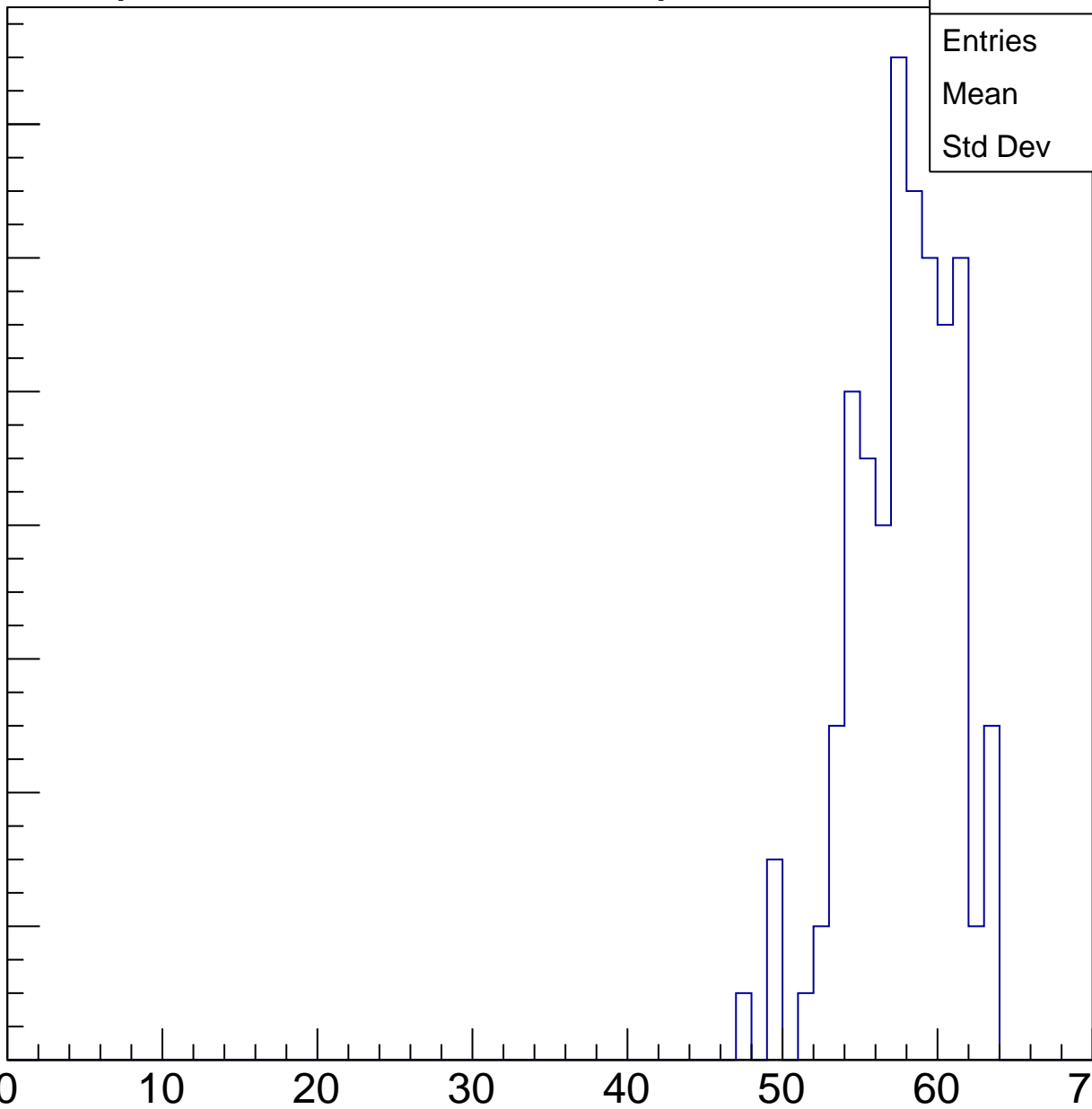
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	57.29
Std Dev	3.269

ampl



B0L101S, U21-ch34, adc5

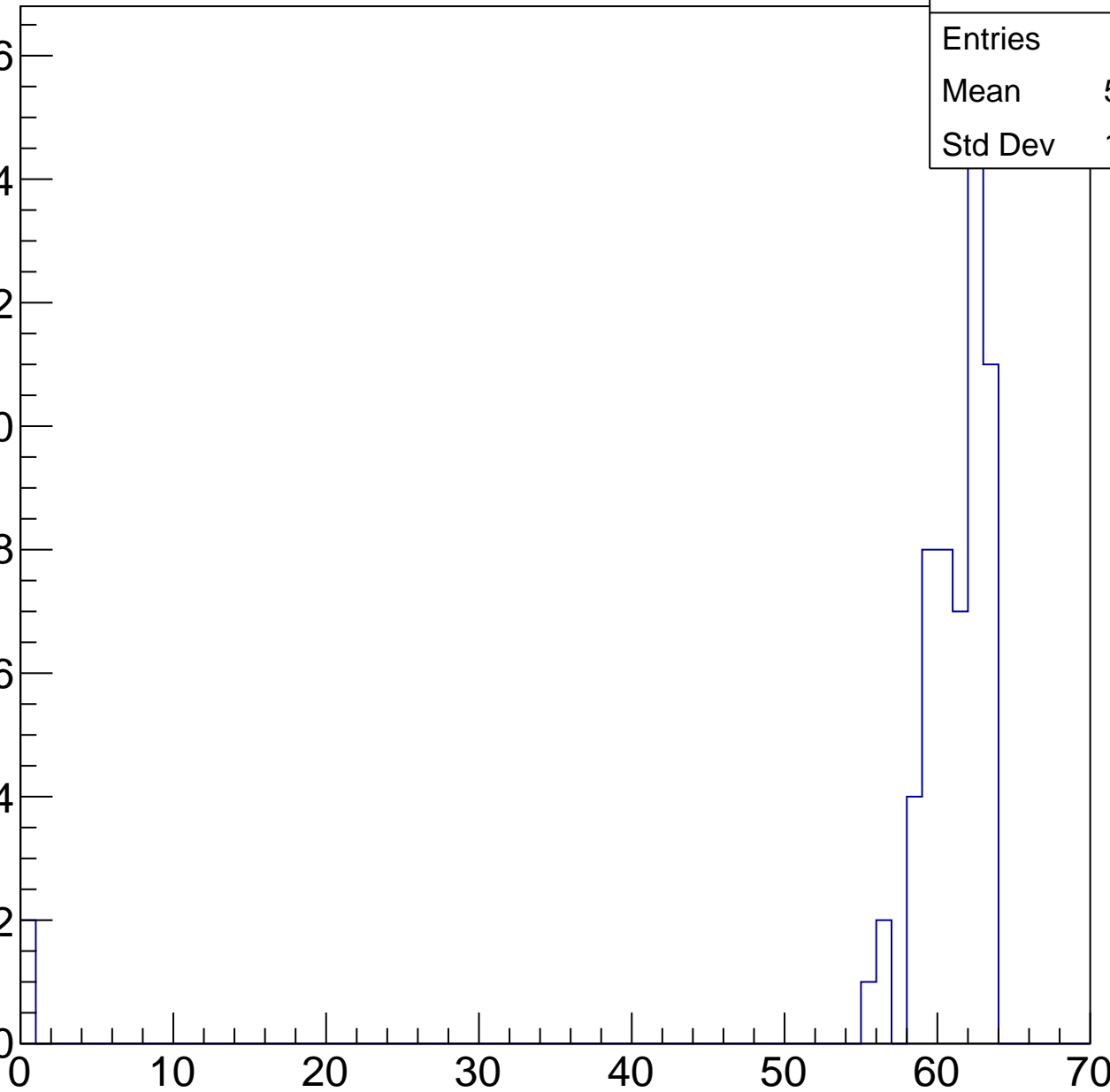
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	59
Mean	58.69
Std Dev	11.16

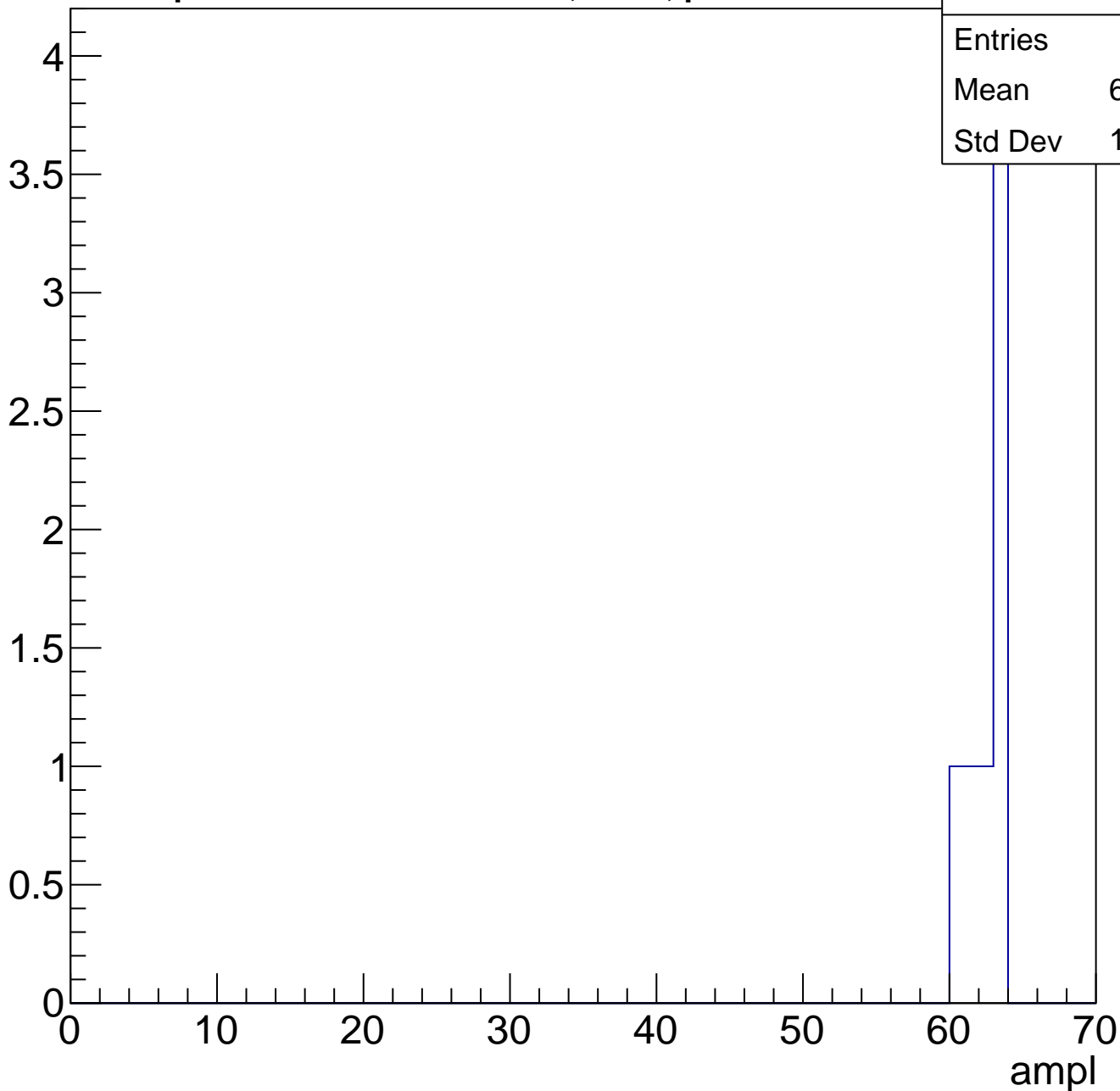
ampl



B0L101S, U21-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch35, adc0

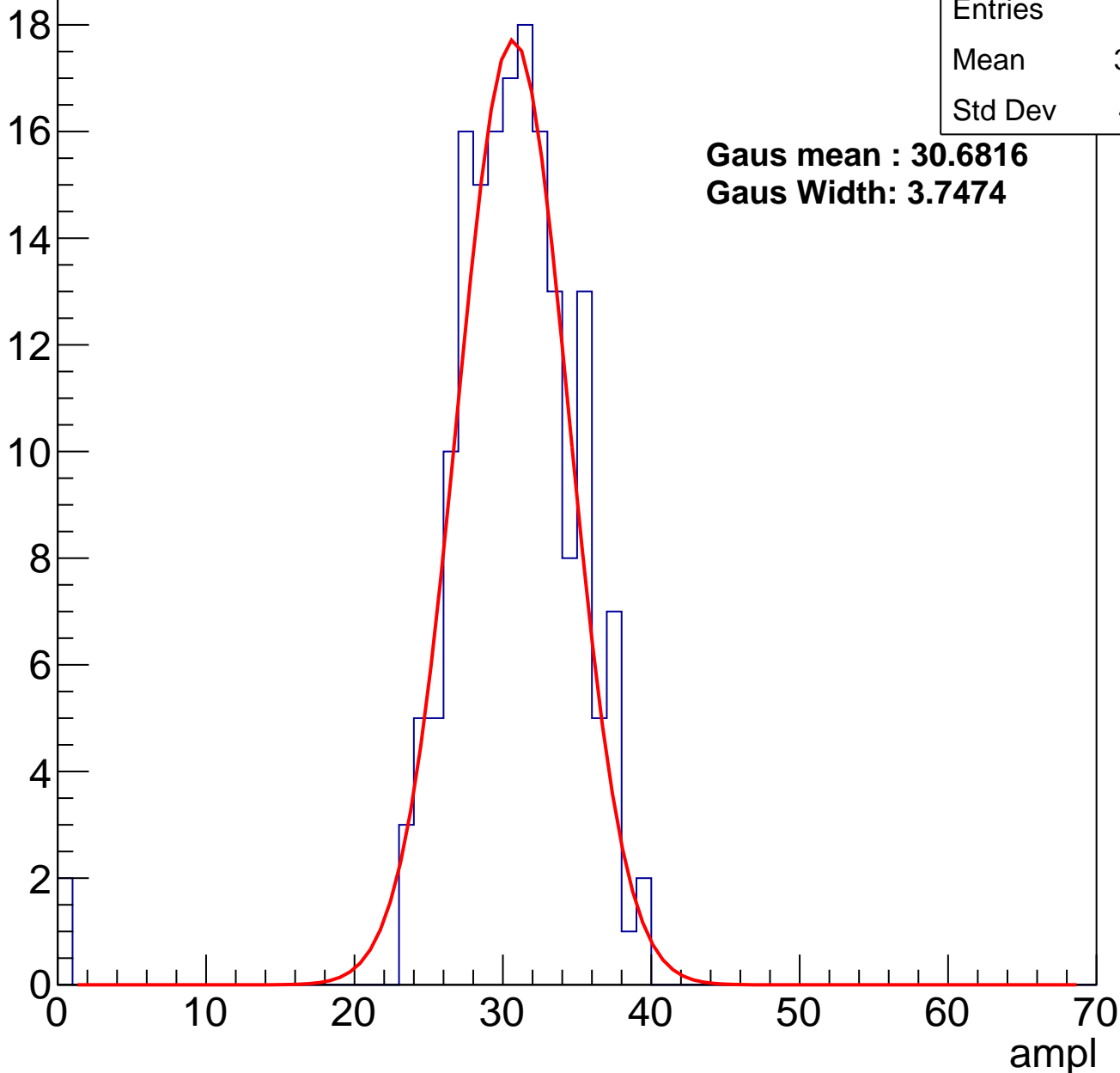
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	30.12
Std Dev	4.841

Gaus mean : 30.6816

Gaus Width: 3.7474

Entry



B0L101S, U21-ch35, adc1

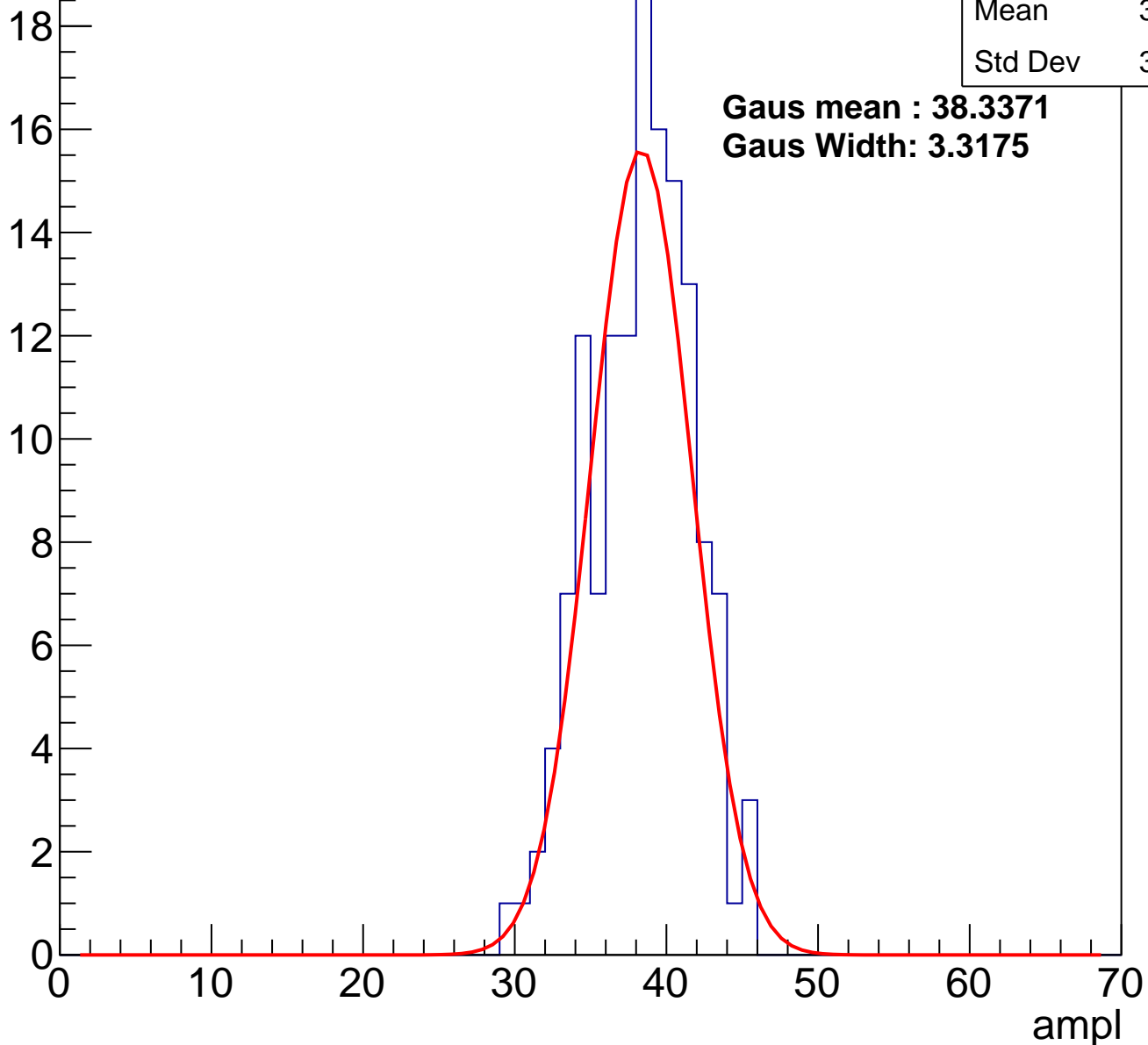
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	37.89
Std Dev	3.332

Gaus mean : 38.3371

Gaus Width: 3.3175

Entry



B0L101S, U21-ch35, adc2

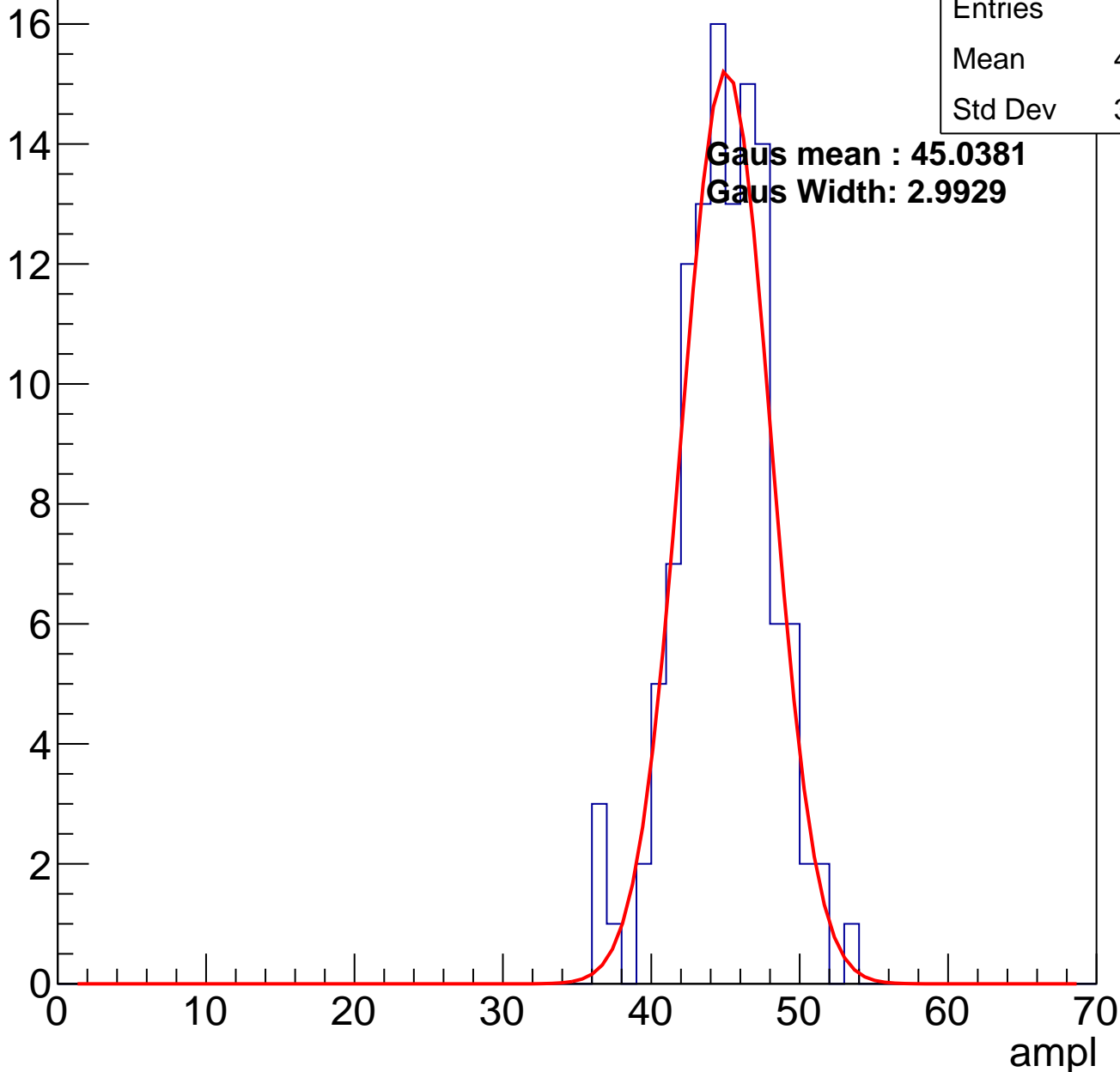
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	44.47
Std Dev	3.143

Gaus mean : 45.0381

Gaus Width: 2.9929

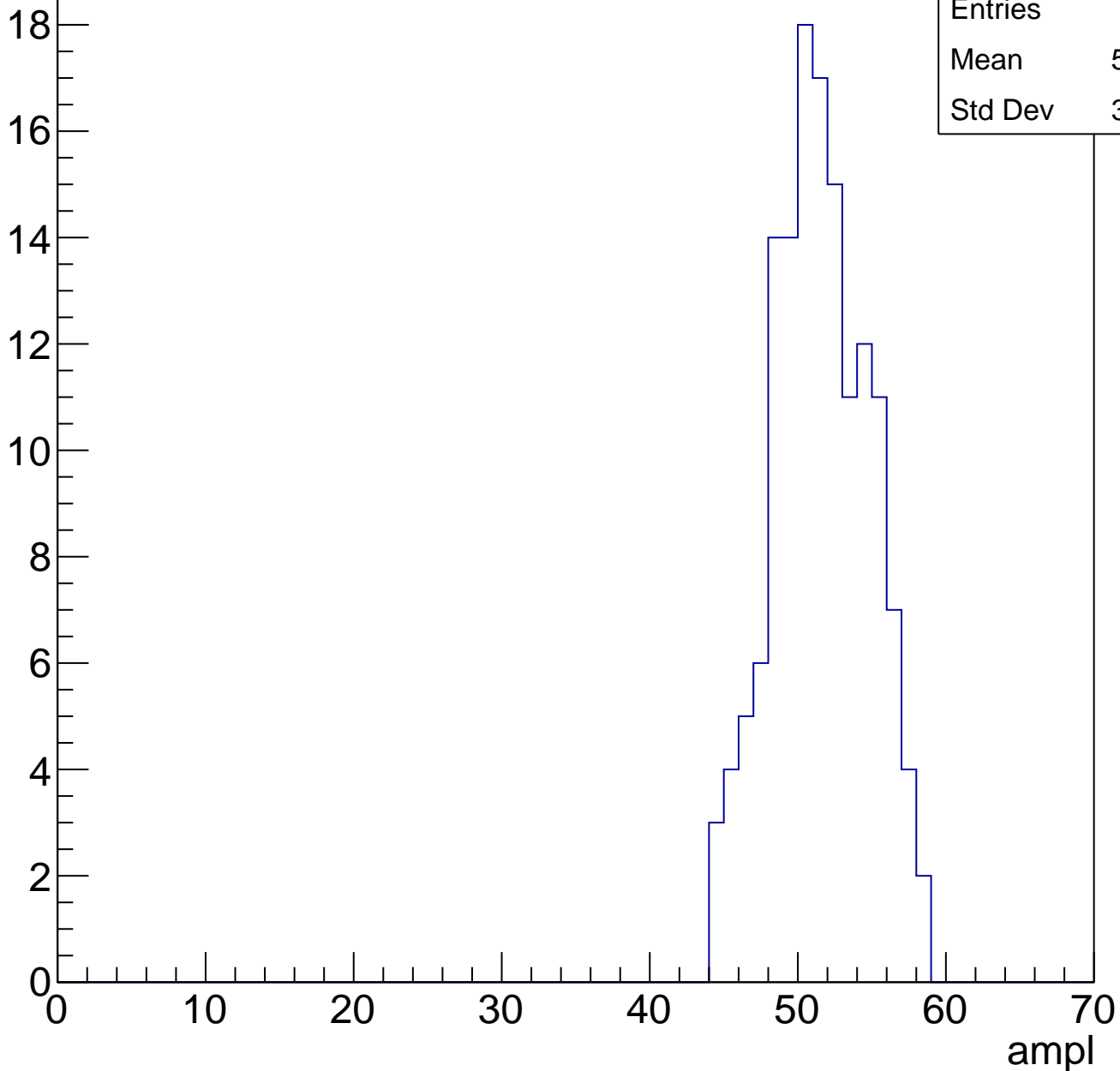
Entry



B0L101S, U21-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

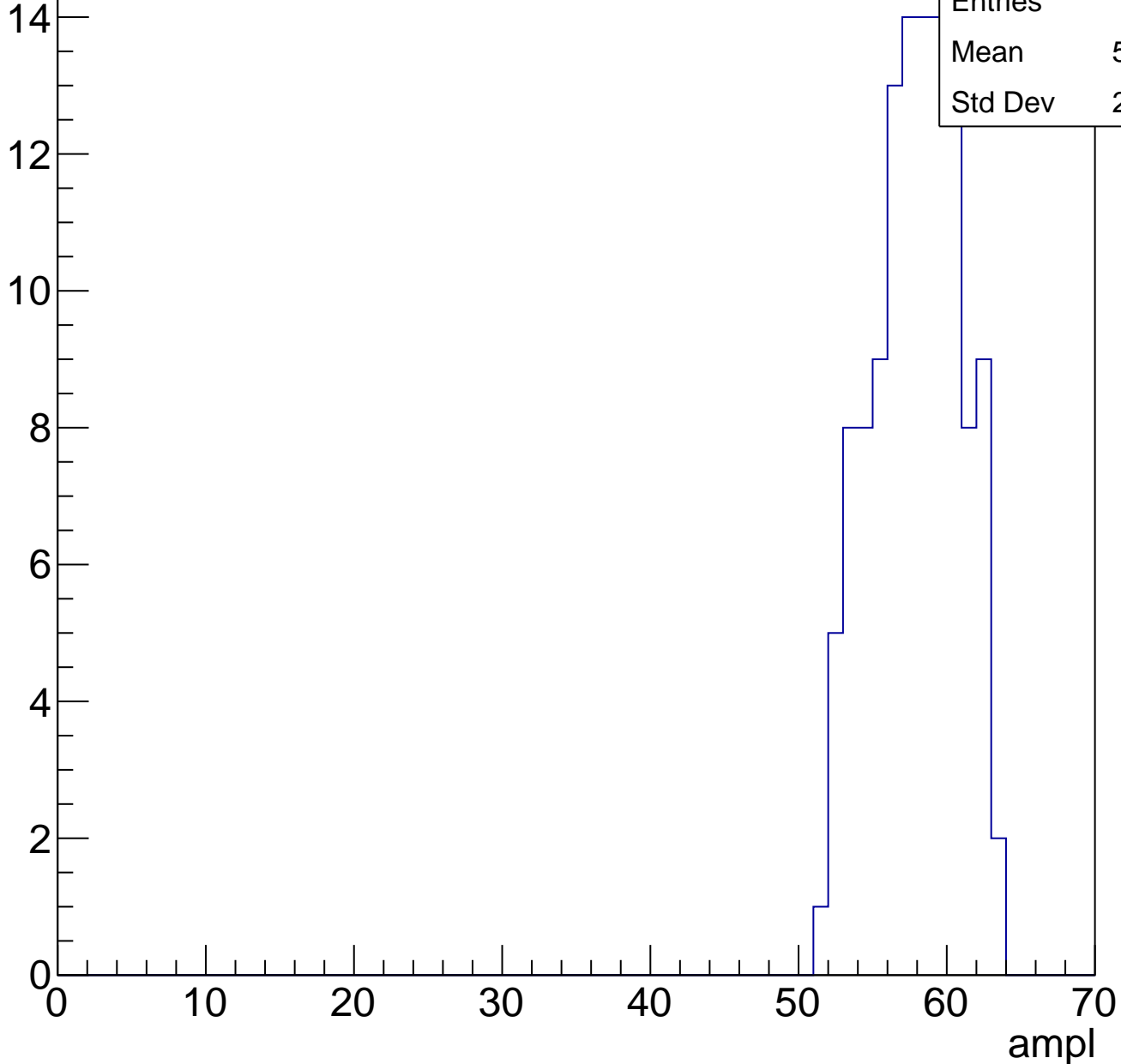
Entry



B0L101S, U21-ch35, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch35, adc5

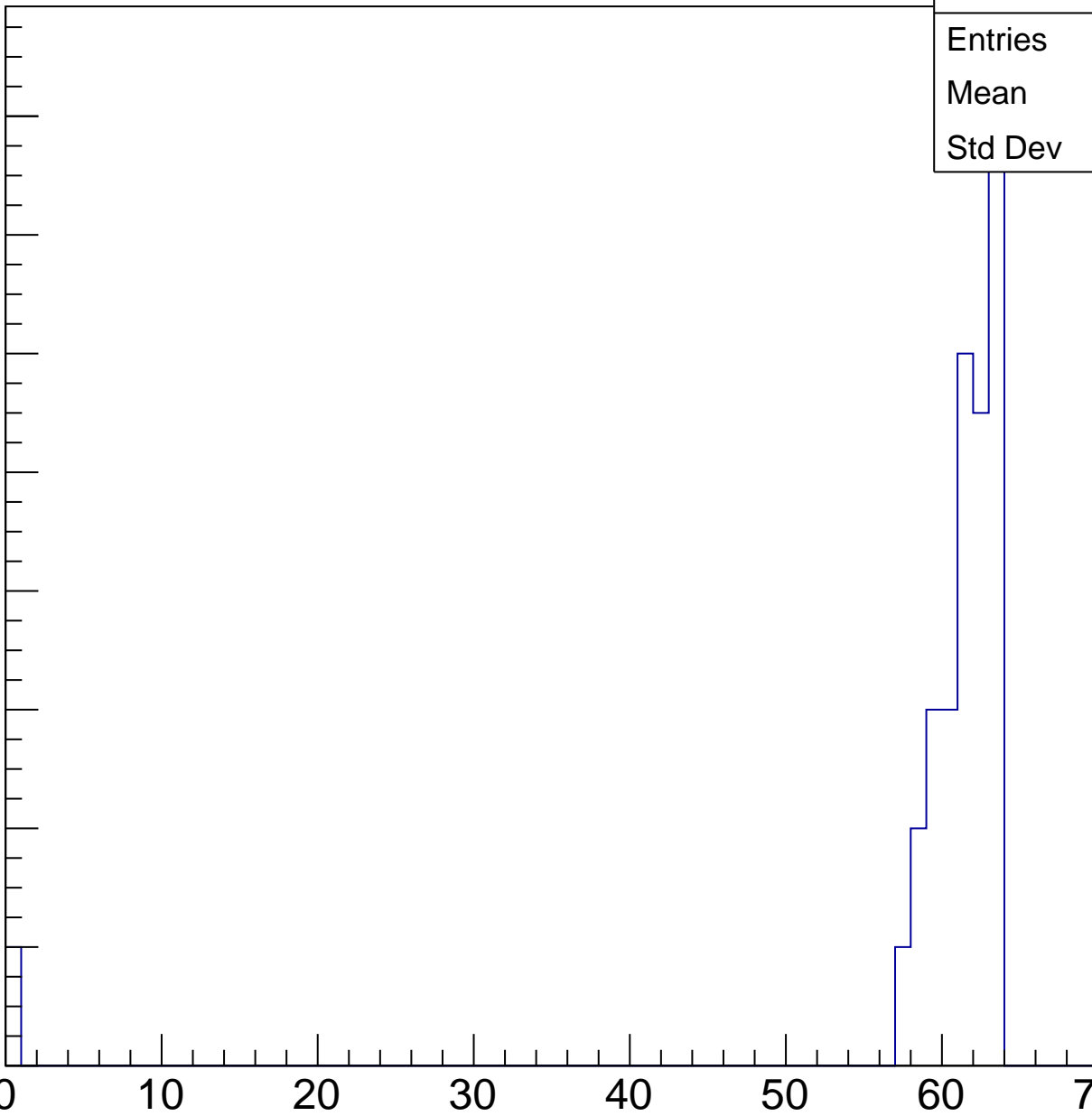
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	60
Mean	59.08
Std Dev	11.1

ampl



B0L101S, U21-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	22
Std Dev	0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch36, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	29.31
Std Dev	4.095

Gaus mean : 30.2537

Gaus Width: 3.6307

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

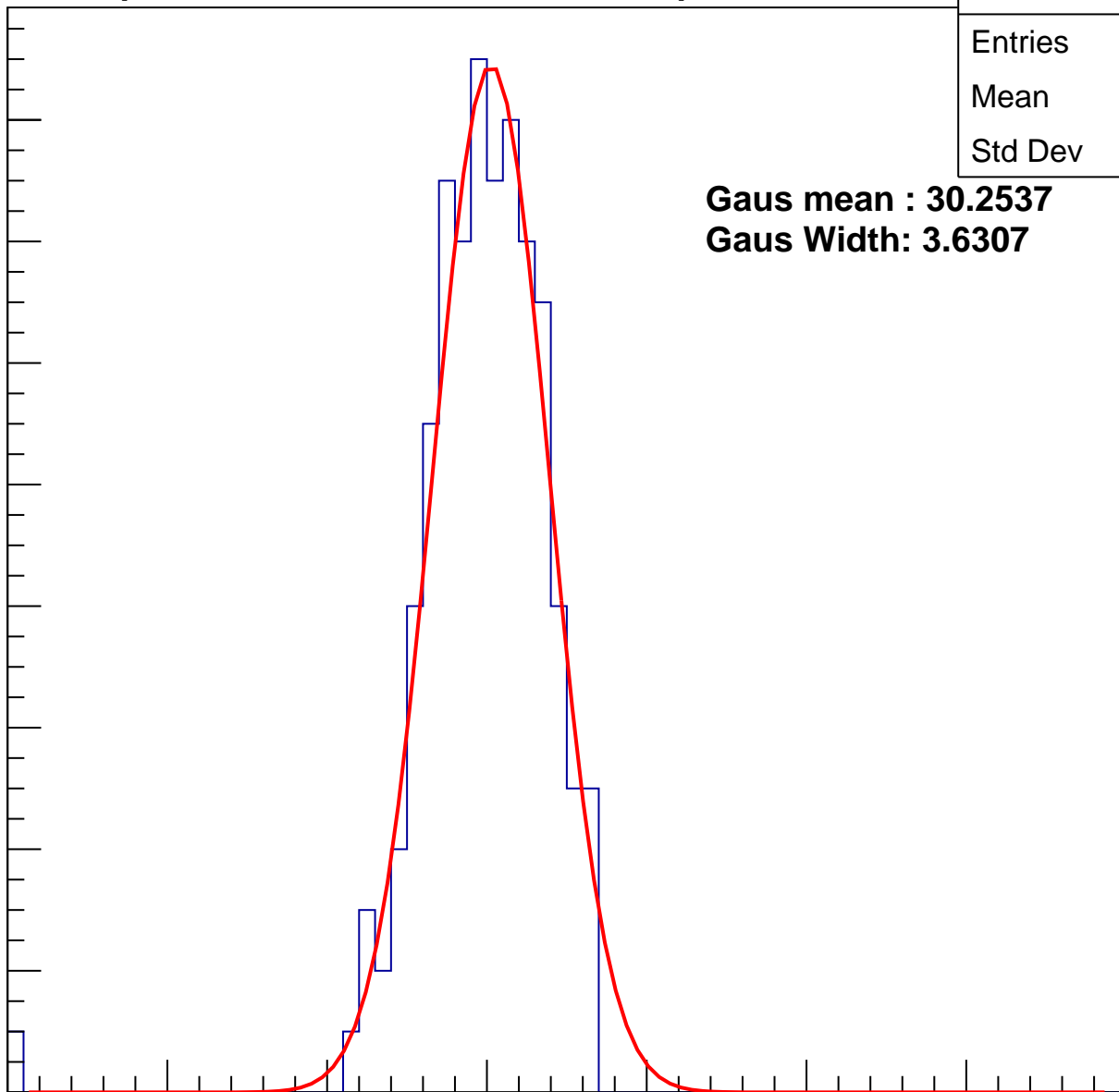
40

50

60

70

ampl



B0L101S, U21-ch36, adc1

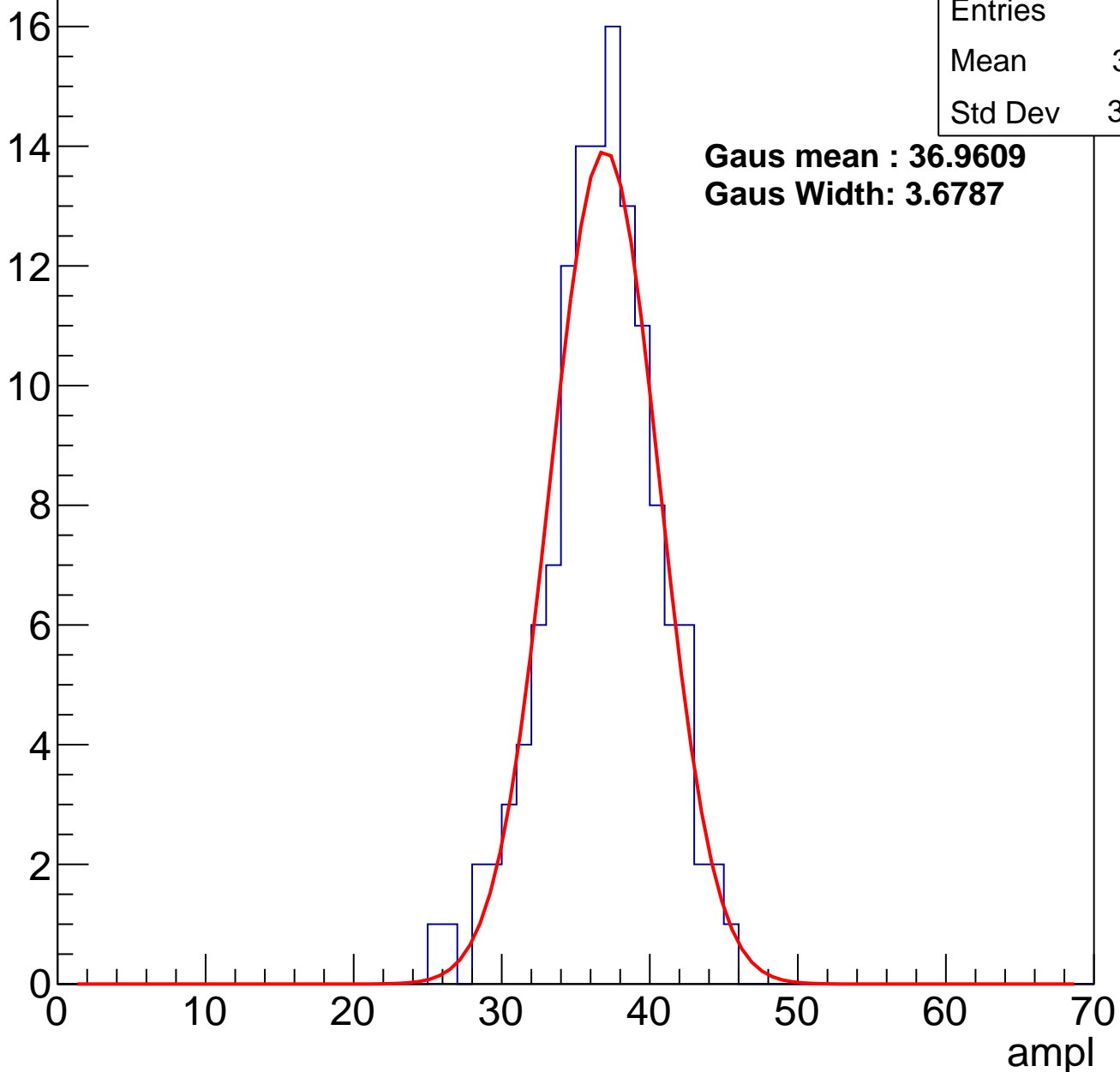
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.31
Std Dev	3.739

Gaus mean : 36.9609

Gaus Width: 3.6787

Entry



B0L101S, U21-ch36, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	43.77
Std Dev	3.777

Gaus mean : 43.9710

Gaus Width: 3.5448

Entry

16

14

12

10

8

6

4

2

0

0

10

20

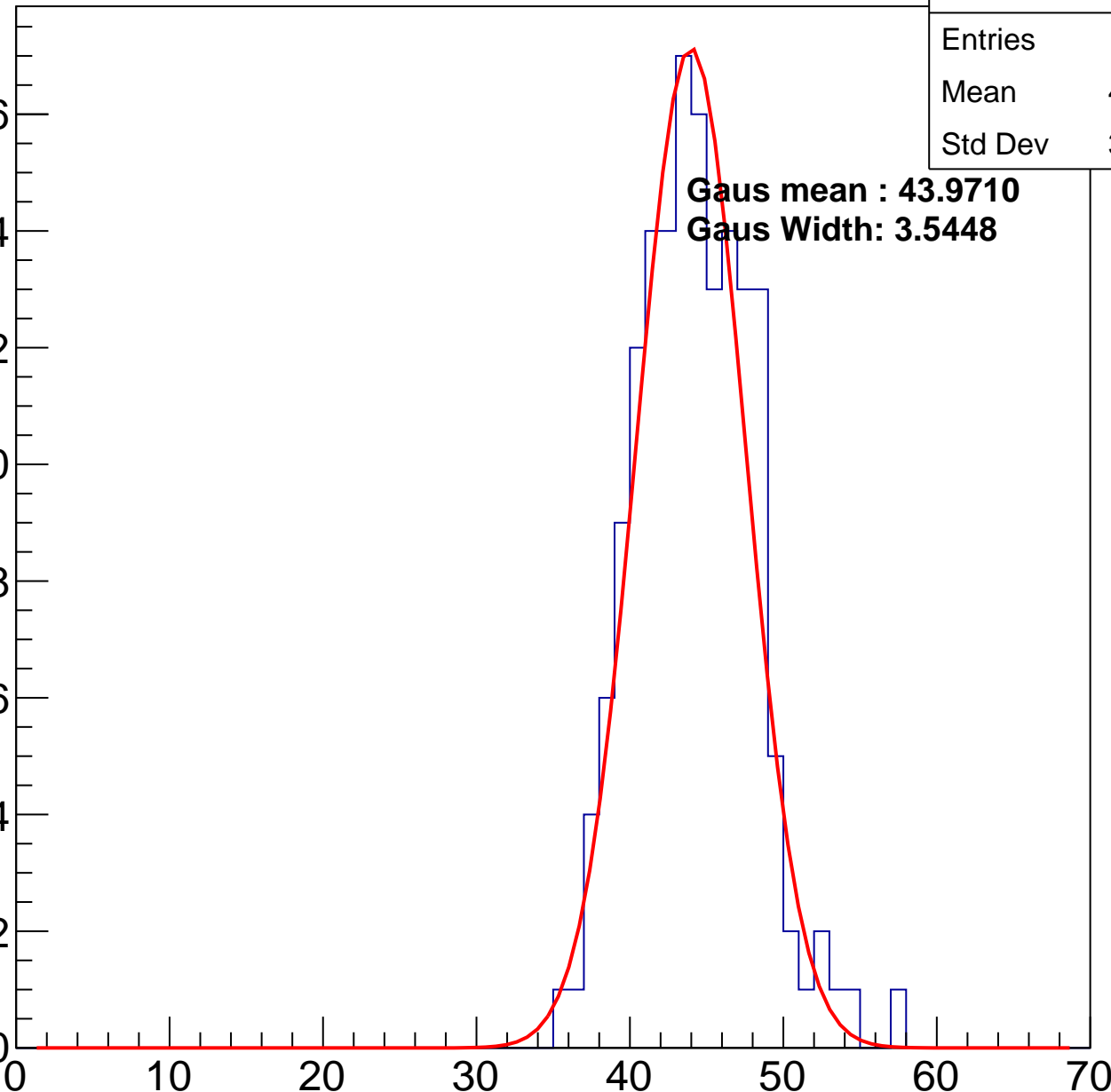
30

40

50

60

ampl

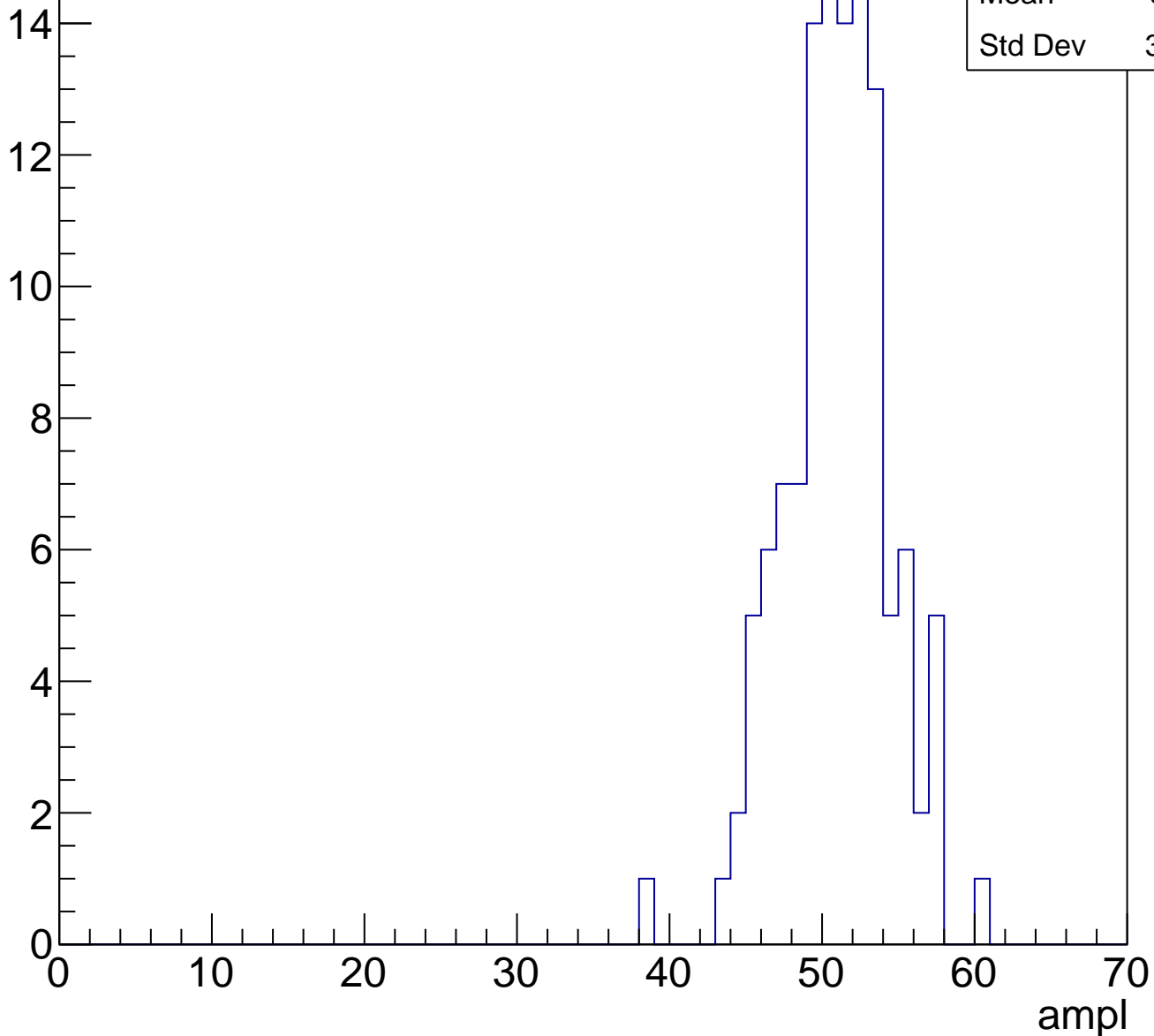


B0L101S, U21-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	50.51
Std Dev	3.427

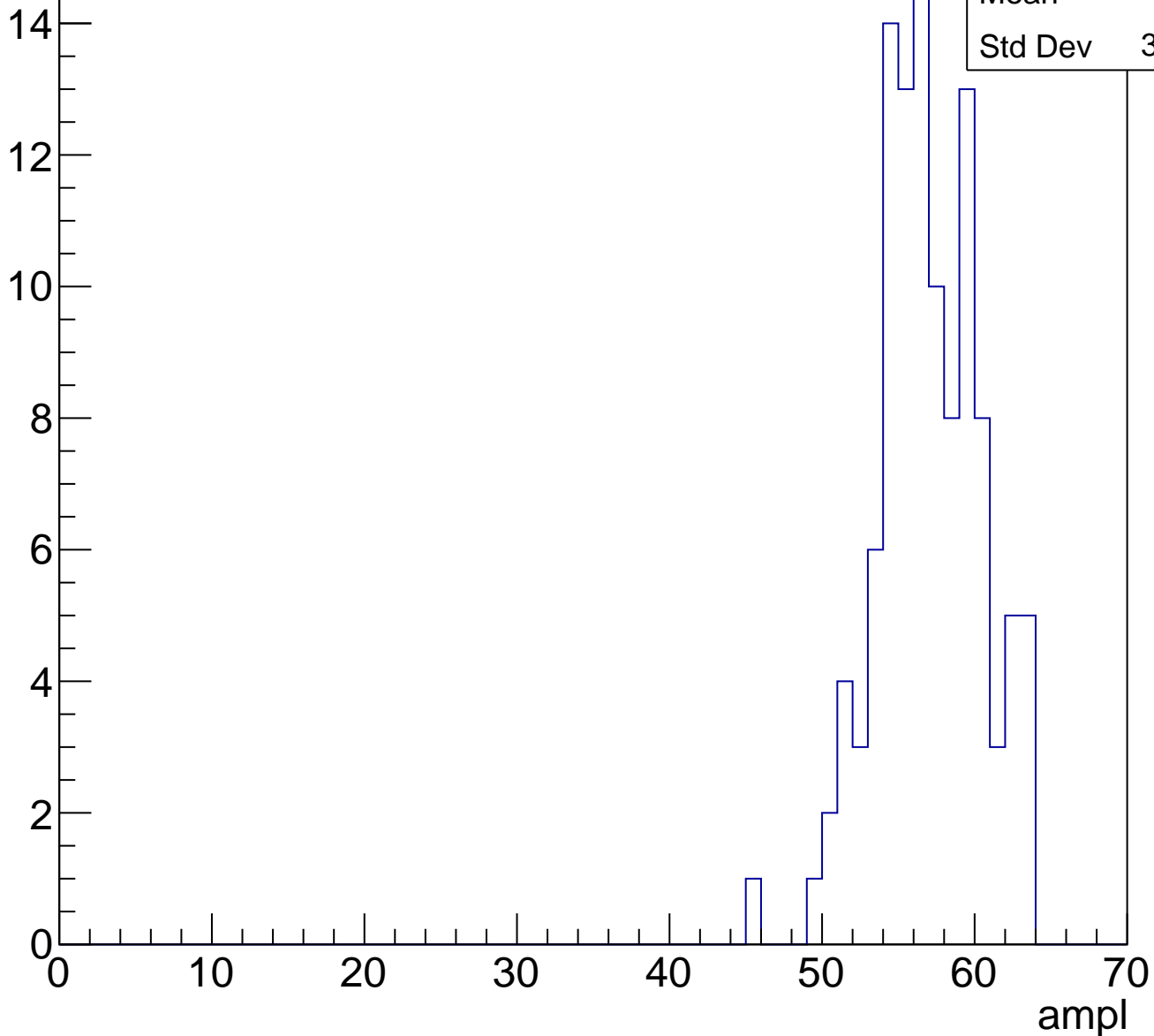
Entry



B0L101S, U21-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch36, adc5

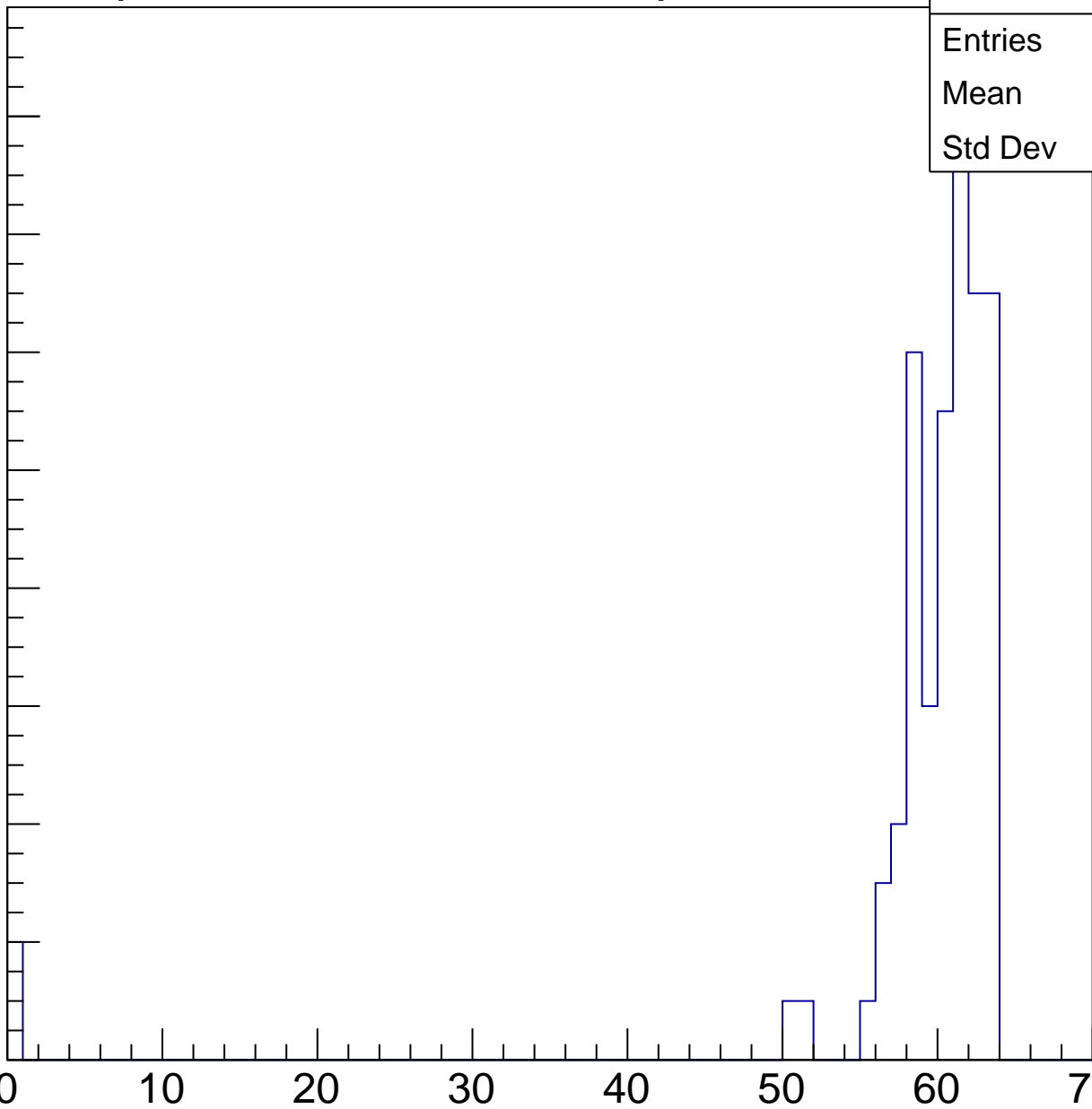
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.62
Std Dev	9.493

ampl



B0L101S, U21-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

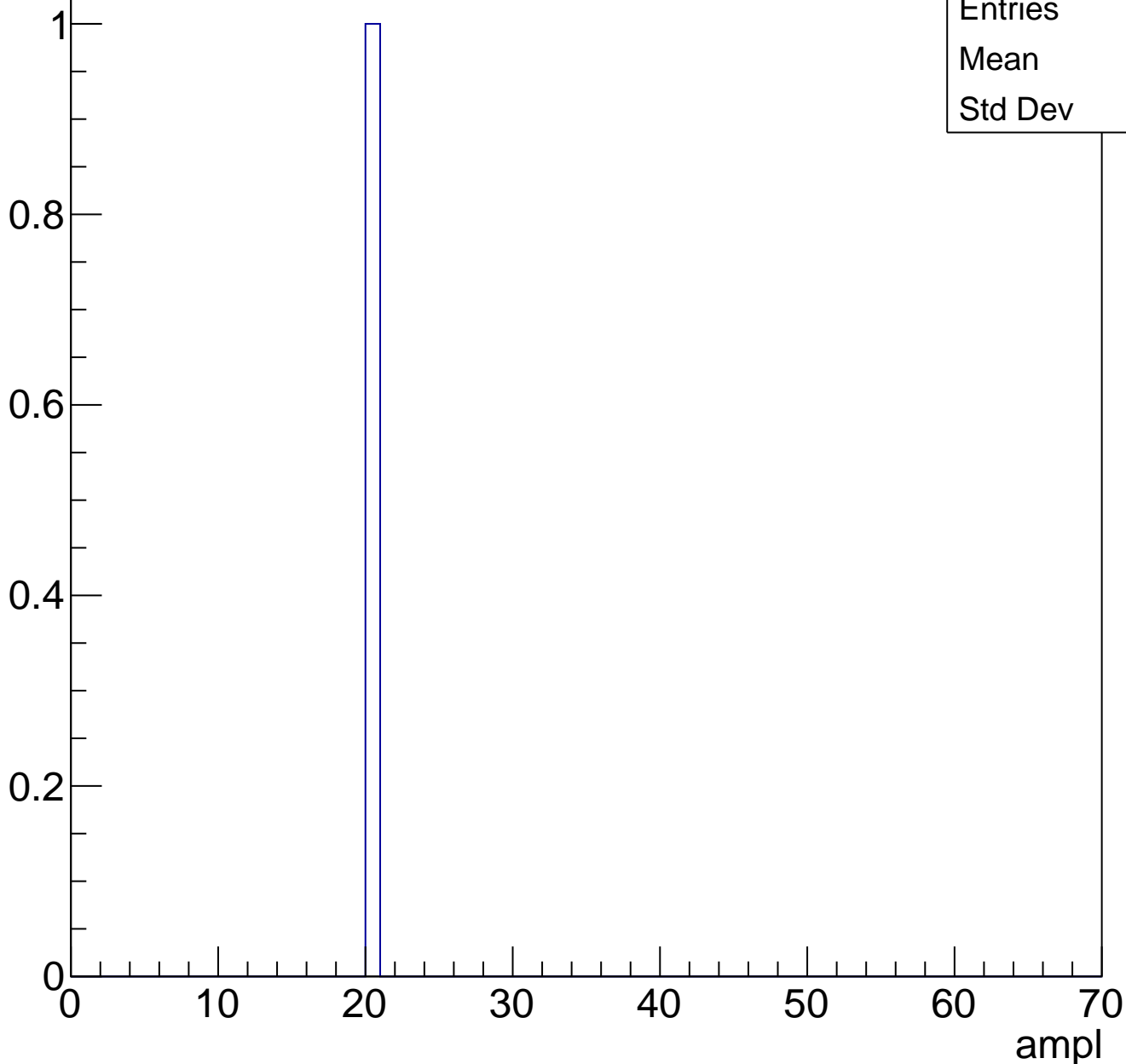
Entry



B0L101S, U21-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch37, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	30.45
Std Dev	5.382

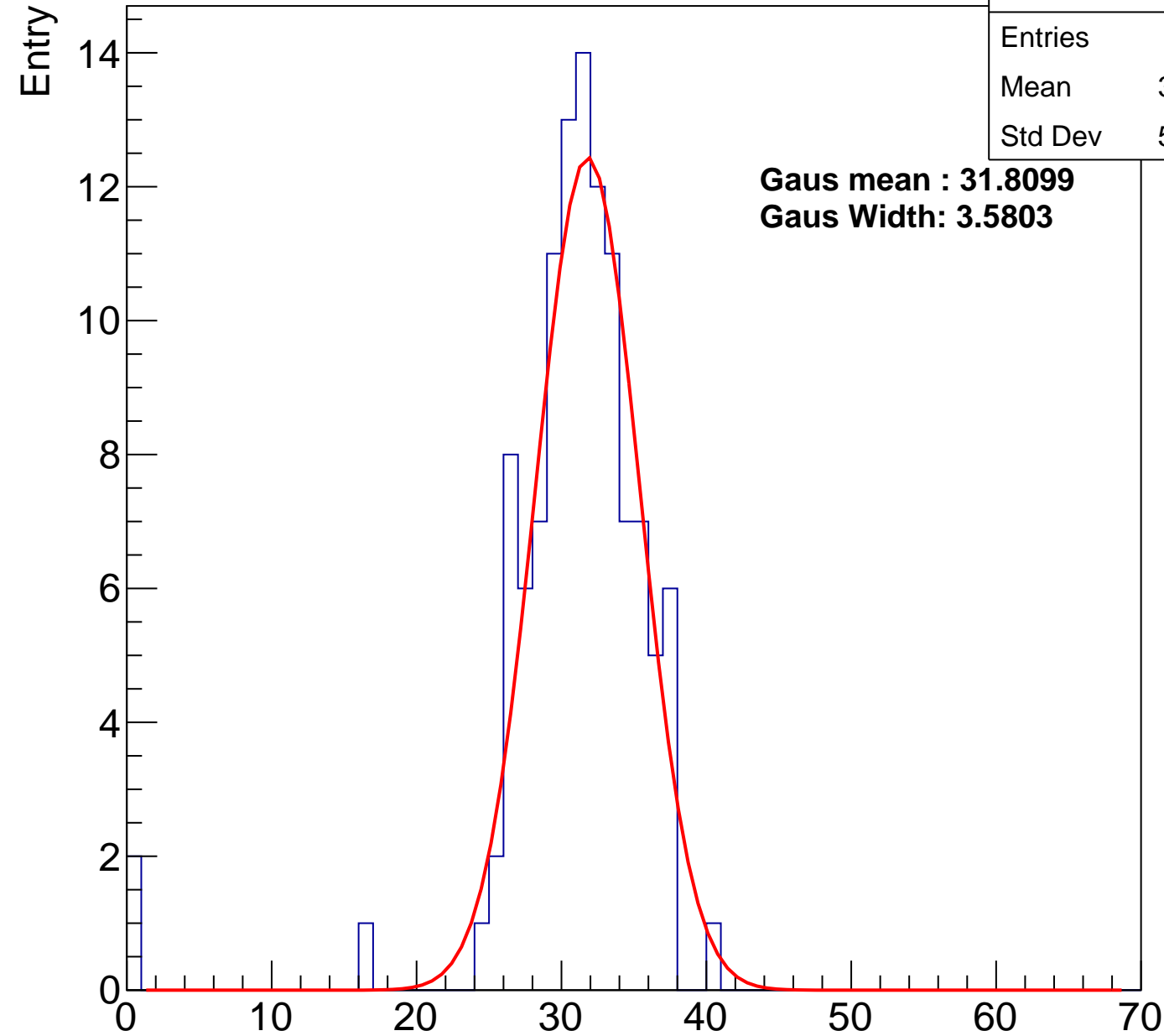
Gaus mean : 31.8099

Gaus Width: 3.5803

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch37, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	37.19
Std Dev	3.668

Gaus mean : 37.8490

Gaus Width: 3.8699

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

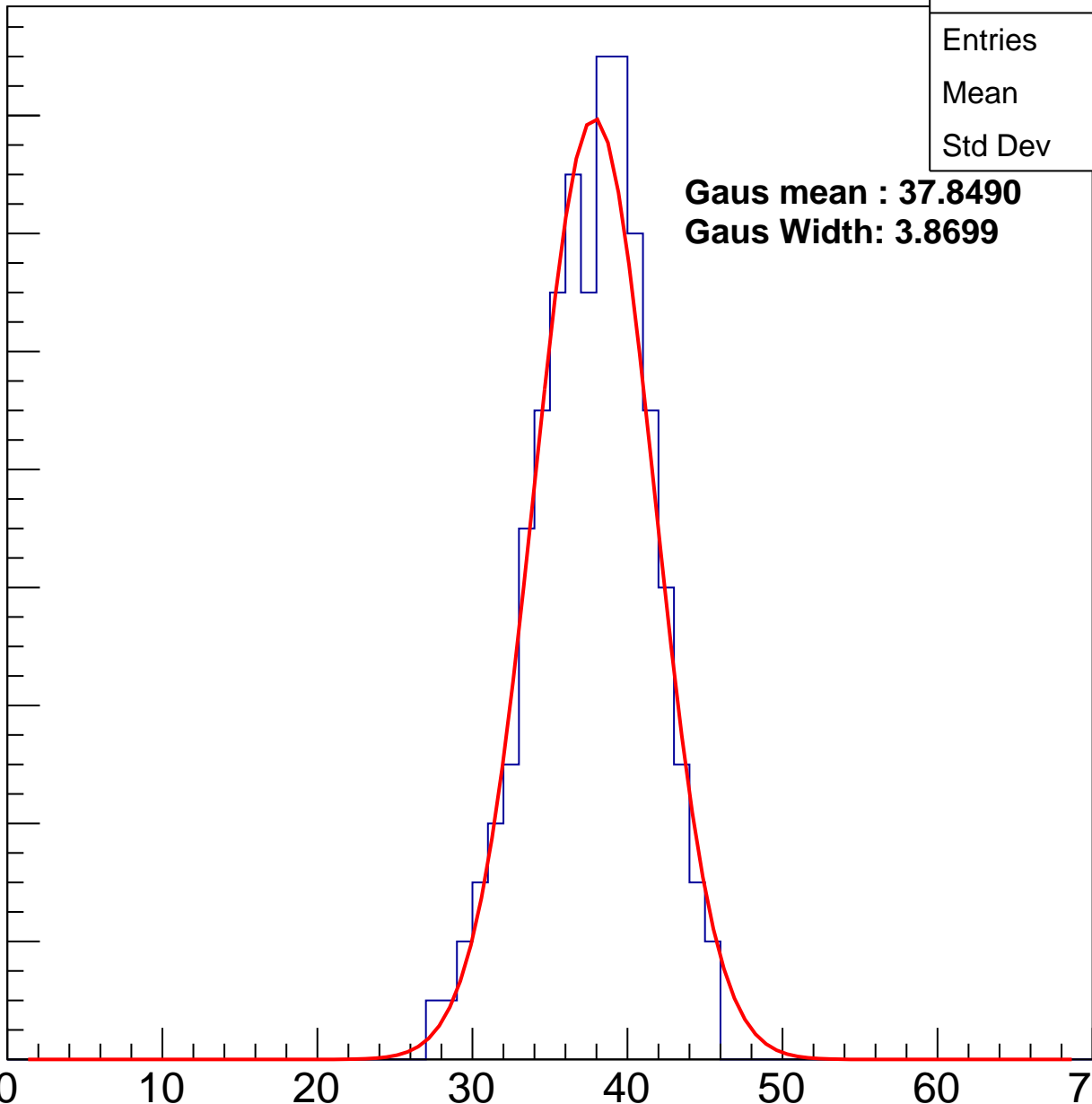
40

50

60

70

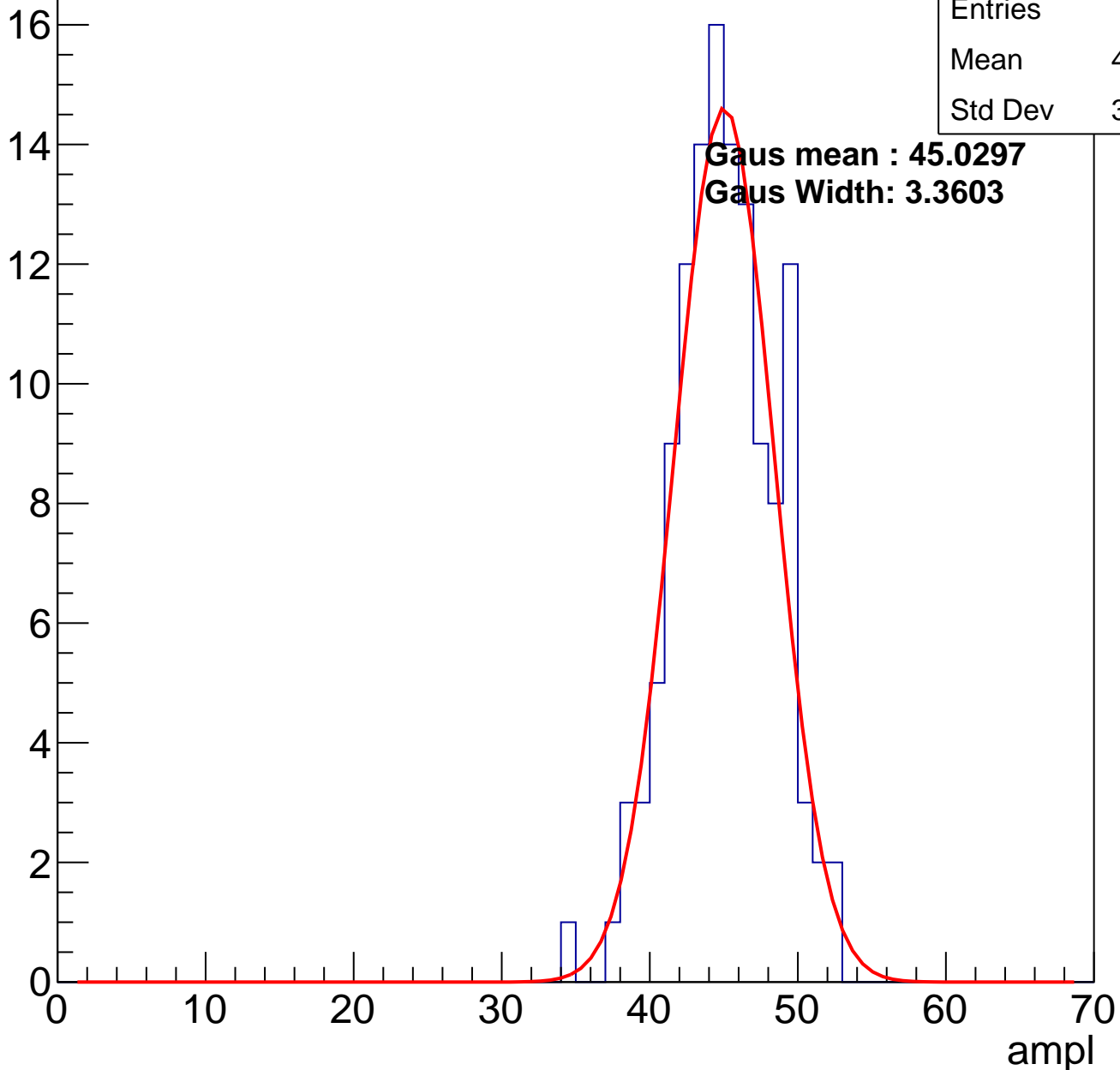
ampl



B0L101S, U21-ch37, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

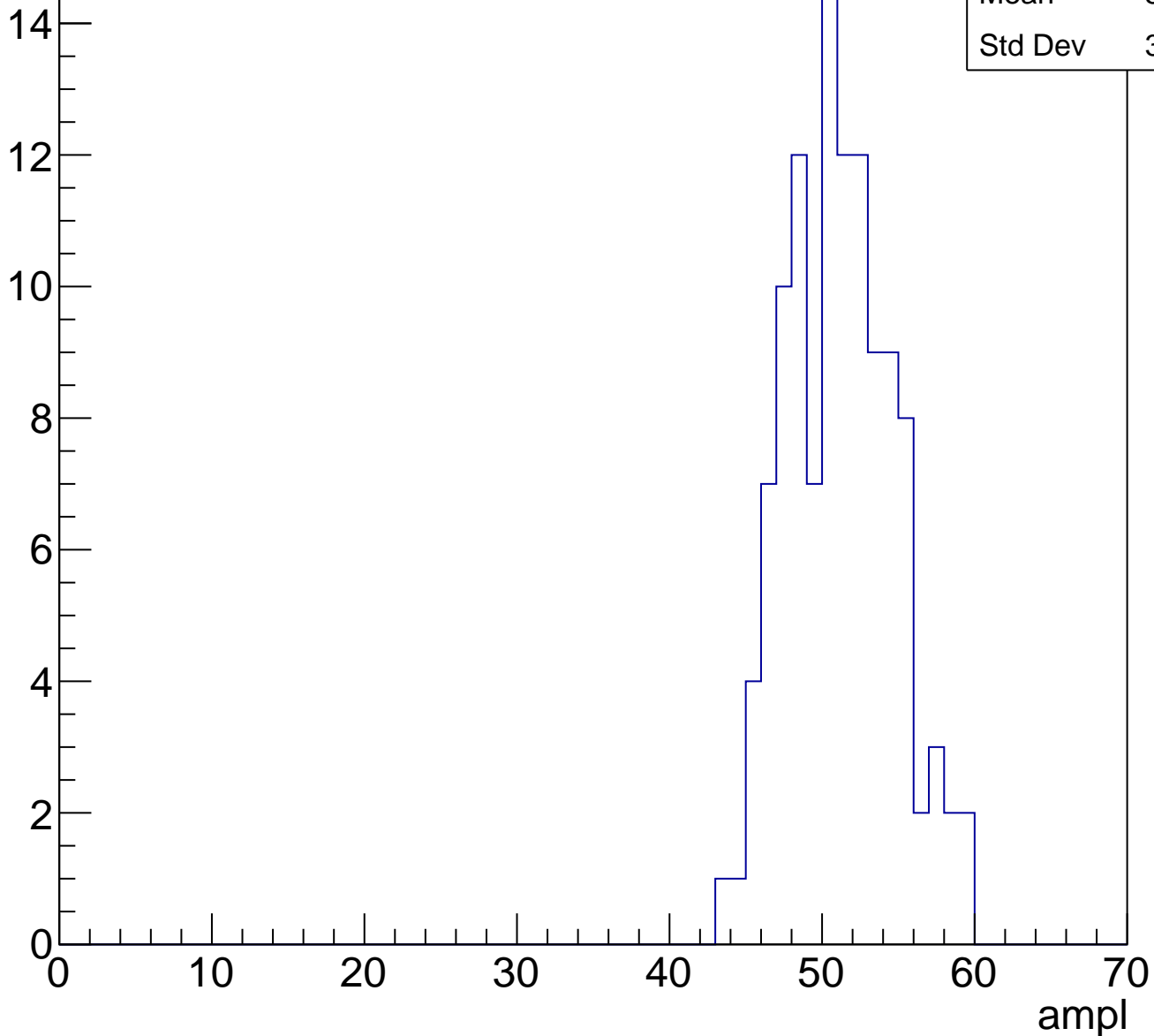


B0L101S, U21-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	50.72
Std Dev	3.443

Entry

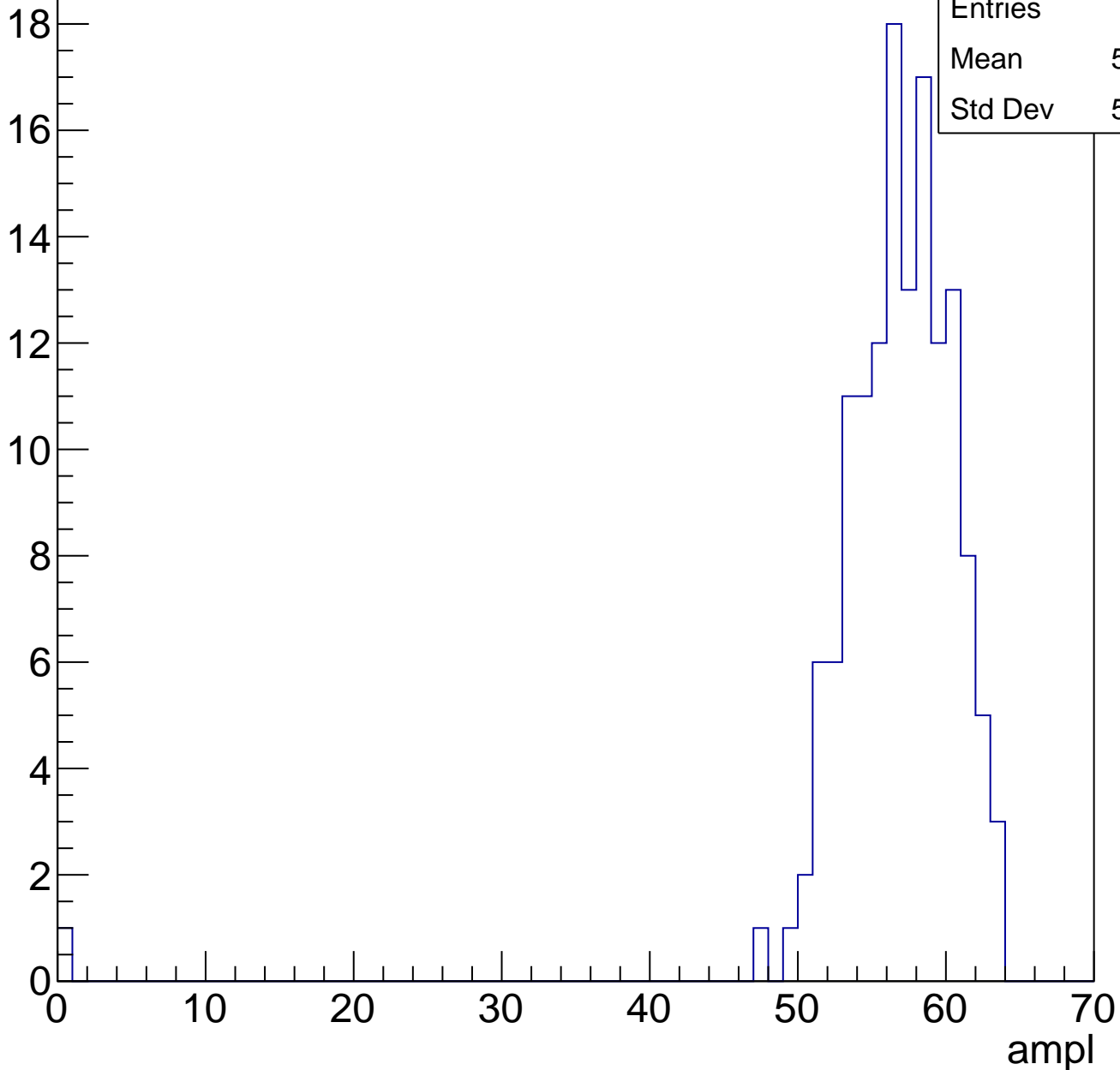


B0L101S, U21-ch37, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	56.15
Std Dev	5.769

Entry



B0L101S, U21-ch37, adc5

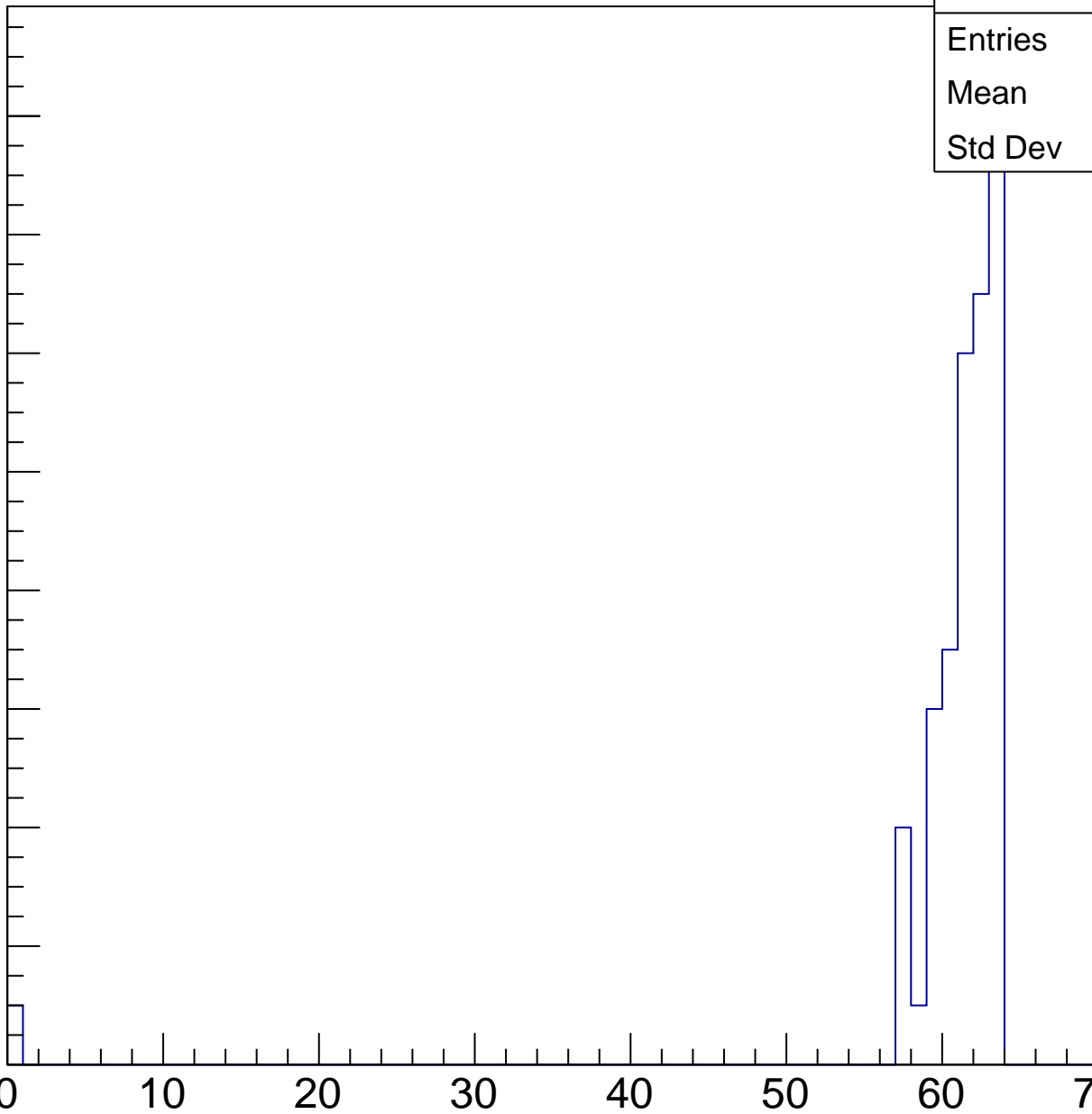
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	61
Mean	60.15
Std Dev	7.957

ampl



B0L101S, U21-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch38, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	30.41
Std Dev	3.497

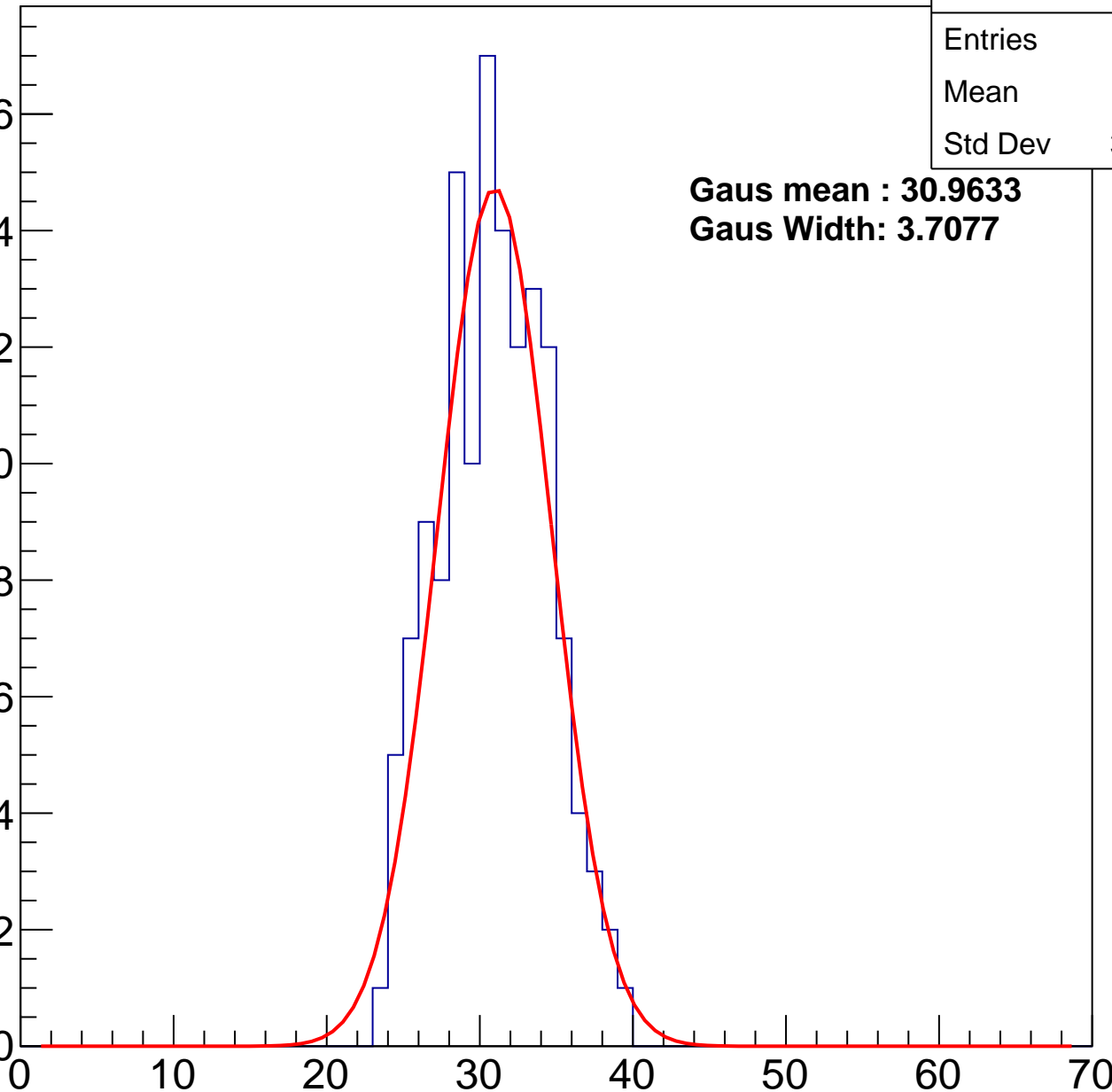
Gaus mean : 30.9633

Gaus Width: 3.7077

Entry

16
14
12
10
8
6
4
2
0

ampl



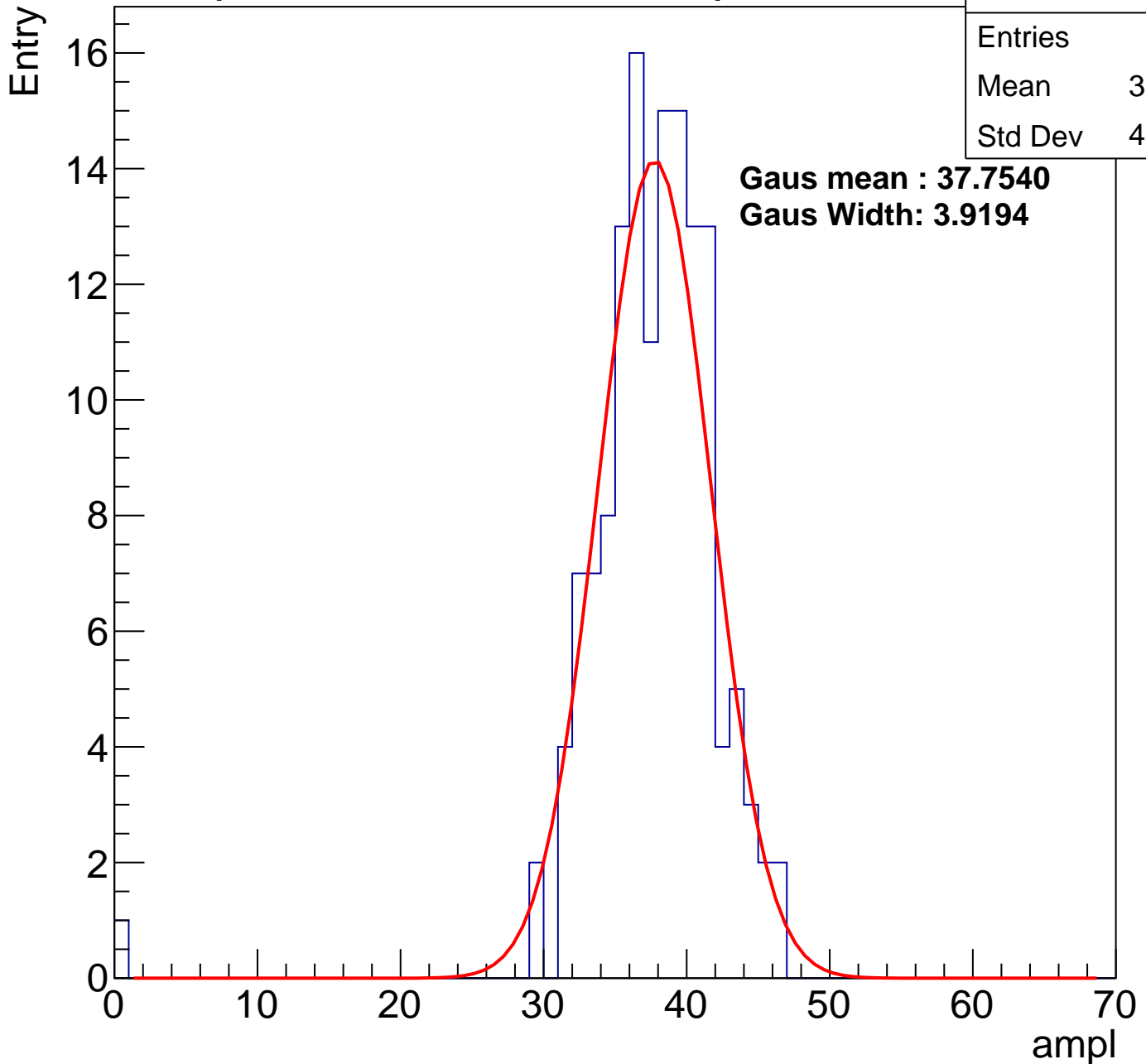
B0L101S, U21-ch38, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	37.25
Std Dev	4.746

Gaus mean : 37.7540

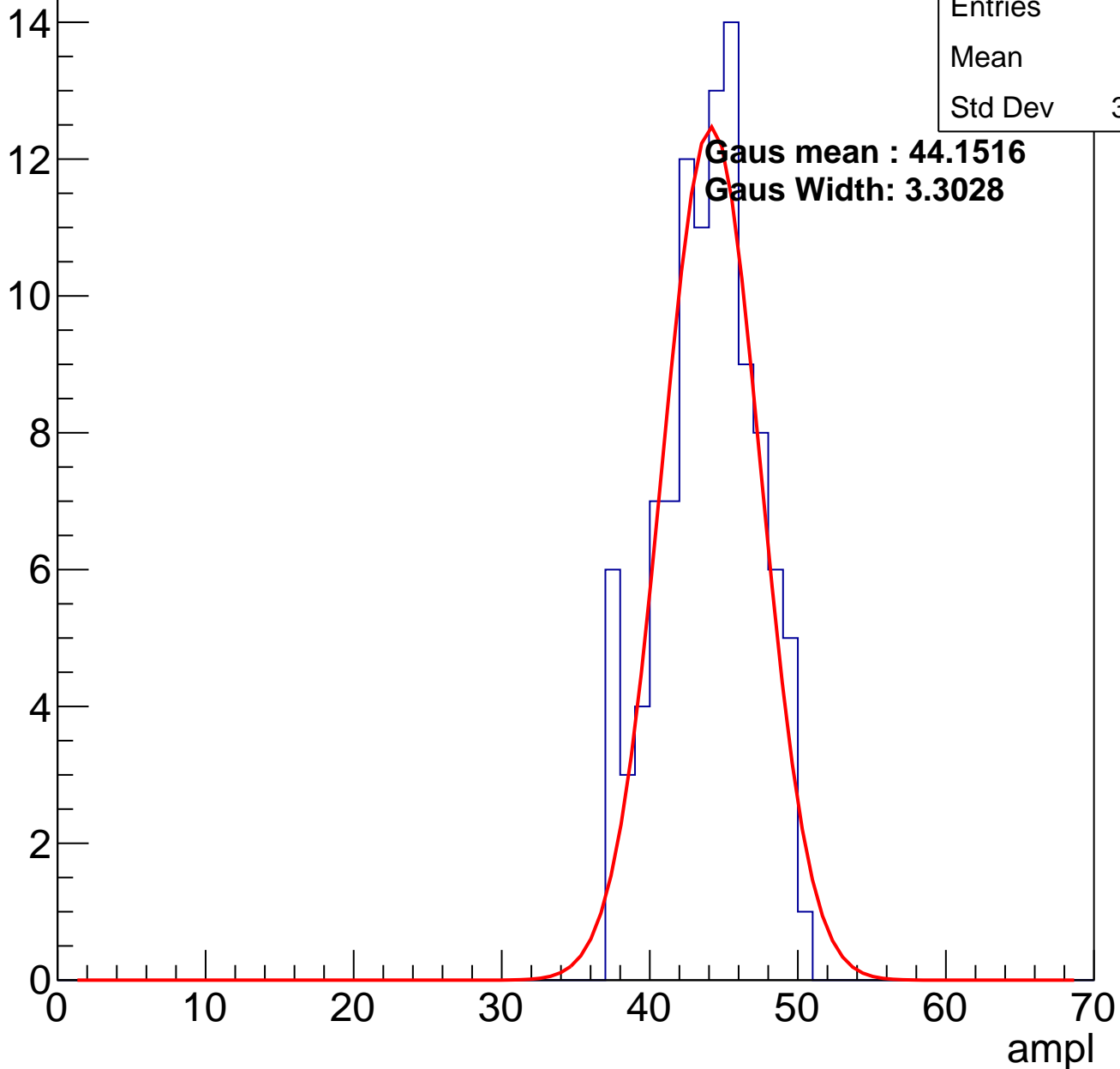
Gaus Width: 3.9194



B0L101S, U21-ch38, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

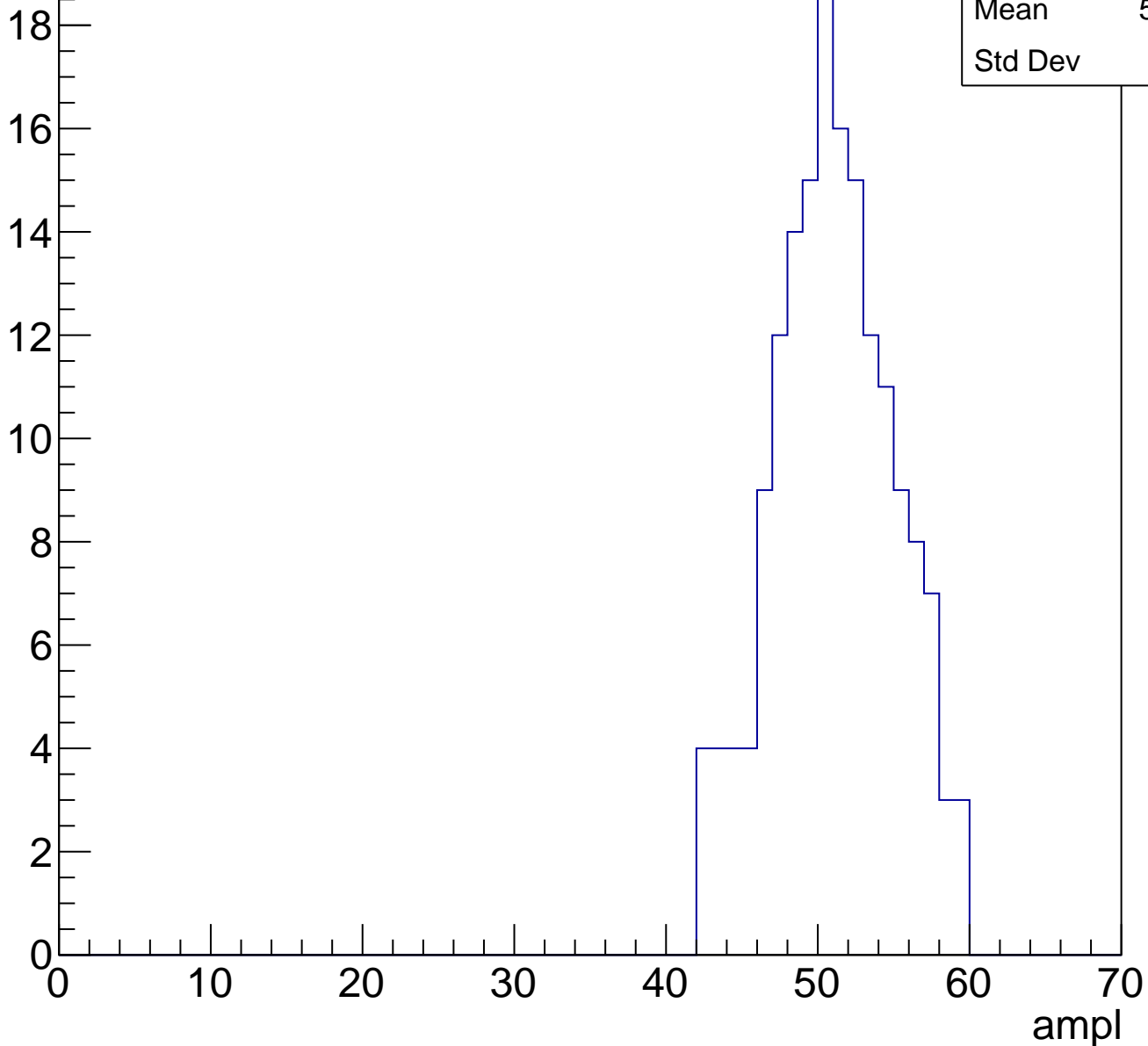


B0L101S, U21-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	50.59
Std Dev	3.93

Entry



B0L101S, U21-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

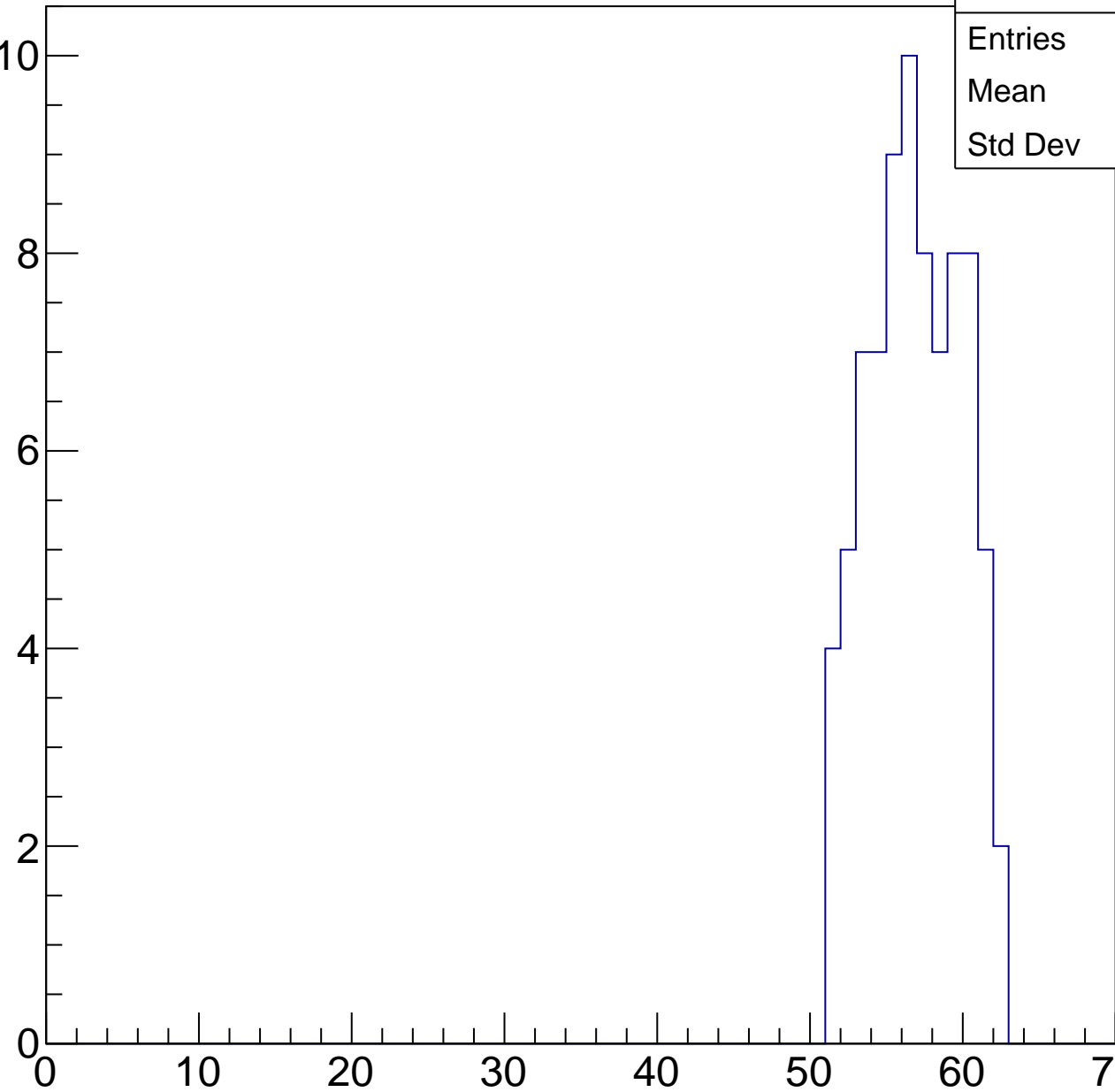
Entries	80
Mean	56.39
Std Dev	2.96

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

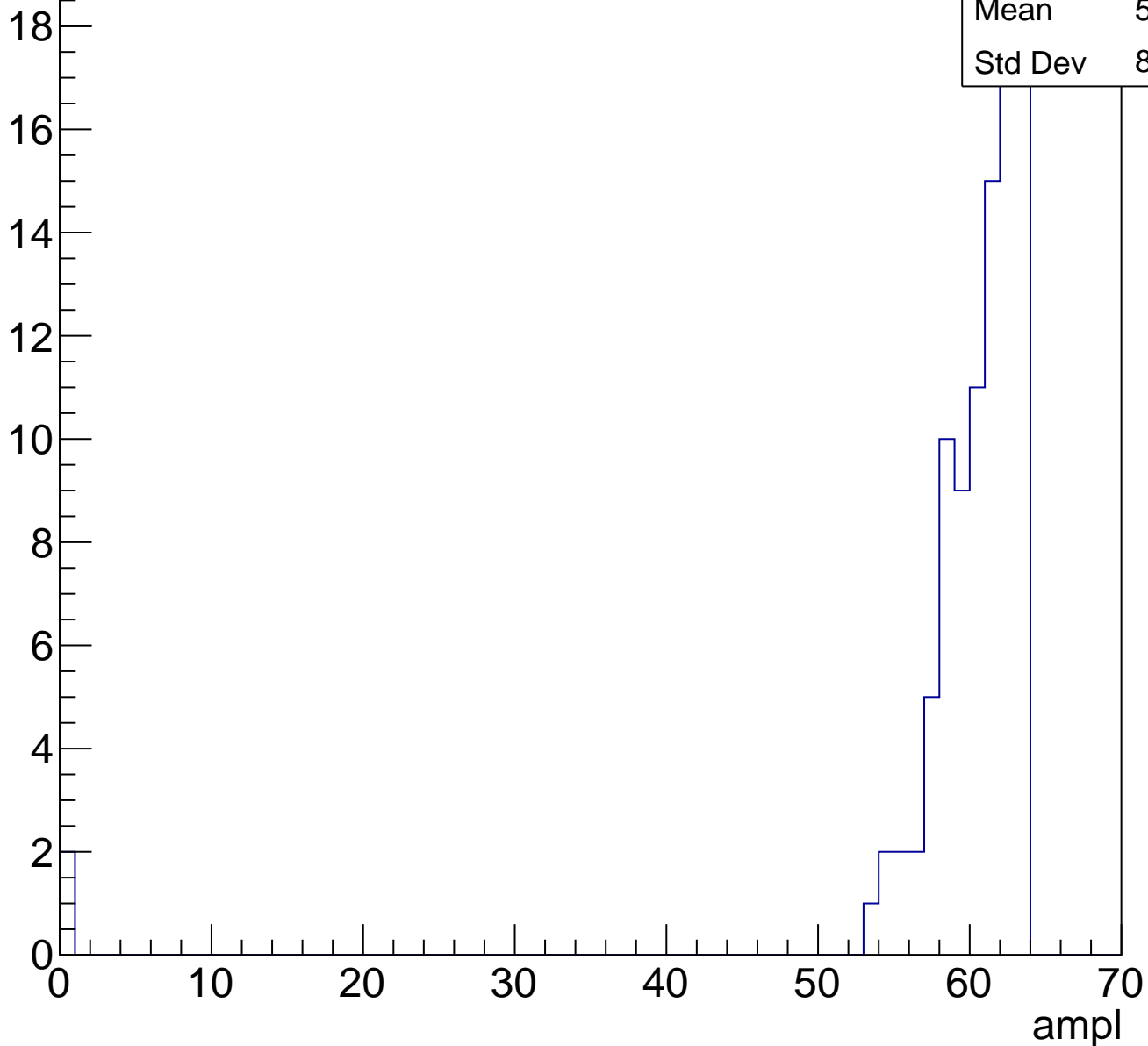
ampl



B0L101S, U21-ch38, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch39, adc0

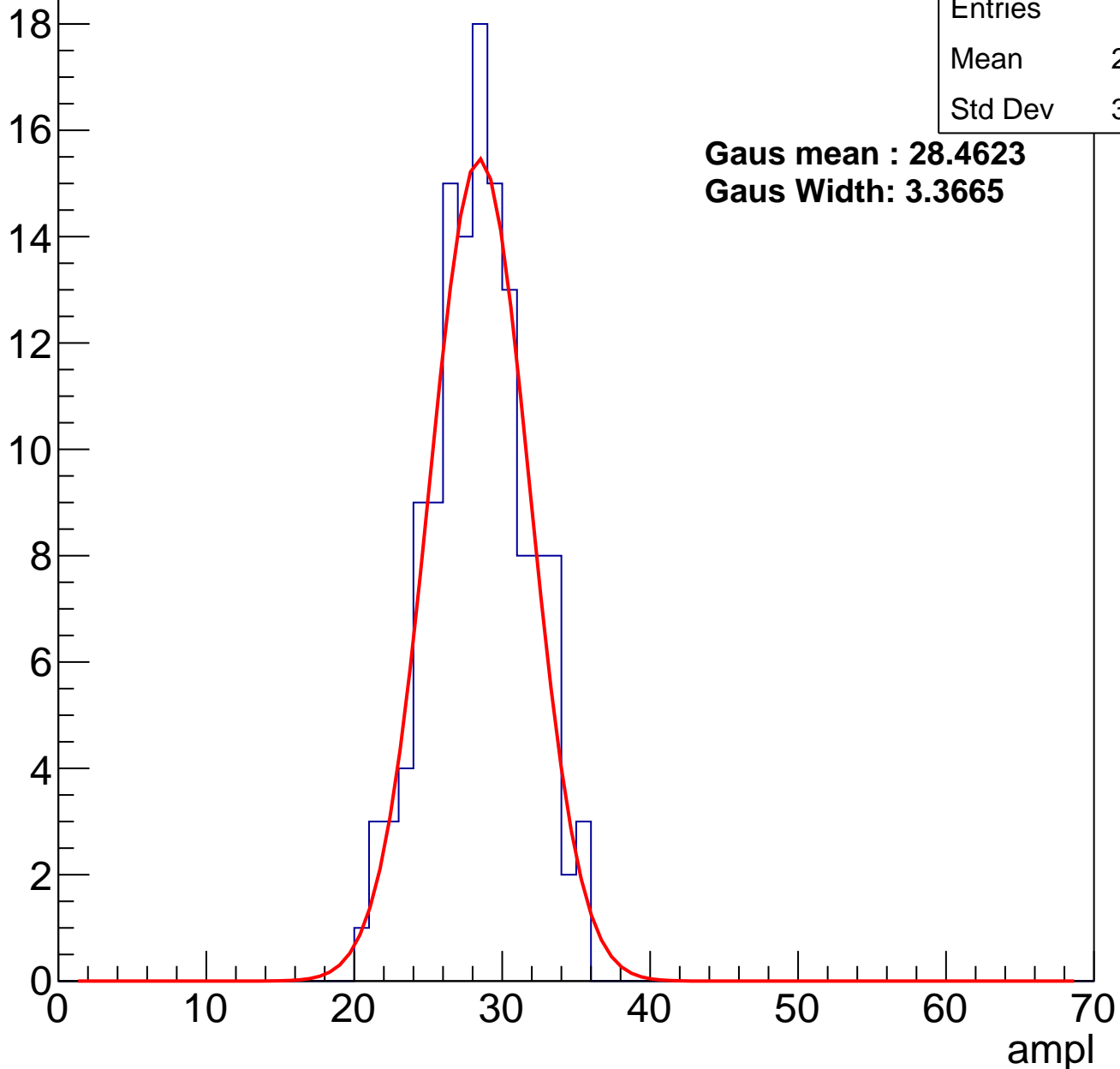
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	27.97
Std Dev	3.249

Gaus mean : 28.4623

Gaus Width: 3.3665

Entry



B0L101S, U21-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	34.59
Std Dev	3.491

Gaus mean : 35.2723

Gaus Width: 3.5627

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

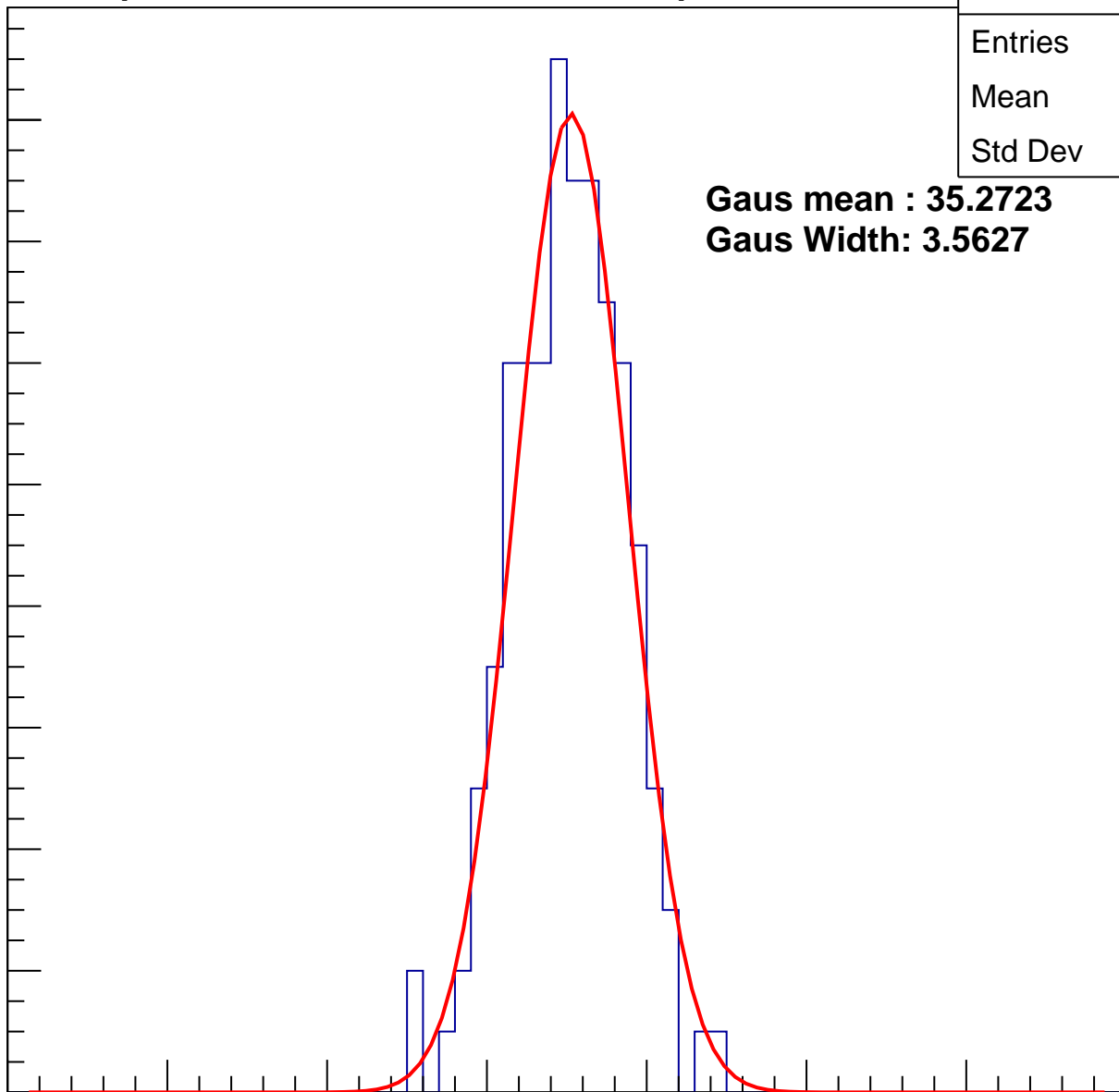
40

50

60

70

ampl



B0L101S, U21-ch39, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

146

Mean

42.11

Std Dev

3.448

Gaus mean : 42.4962

Gaus Width: 3.4874

0

2

4

6

8

10

12

14

16

Entry

calib_packv5_042523_0143.root, FC#1, port C1

Entries

146

Mean

42.11

Std Dev

3.448

Gaus mean : 42.4962

Gaus Width: 3.4874

B0L101S, U21-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

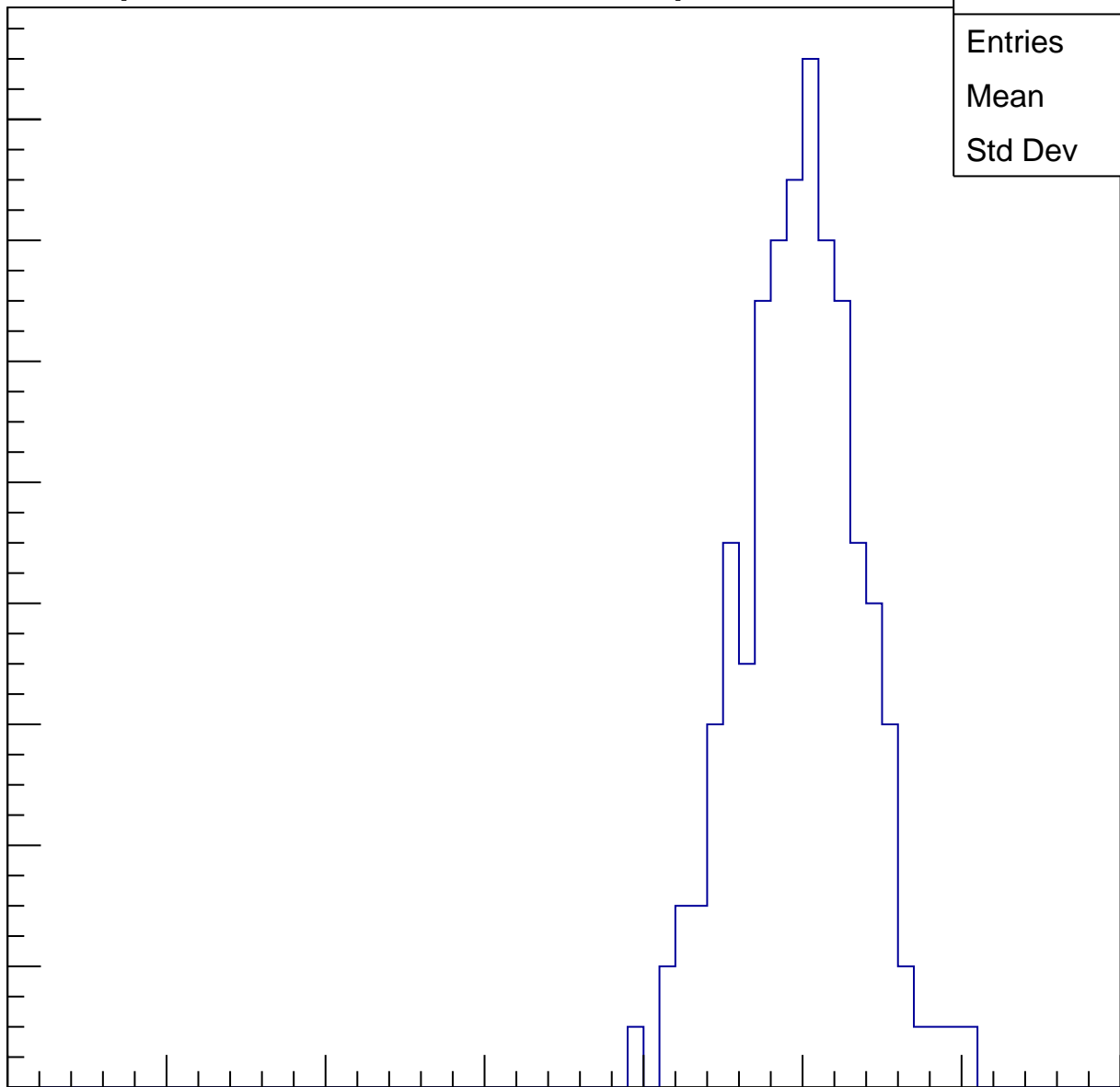
Entries	146
Mean	49.38
Std Dev	3.791

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

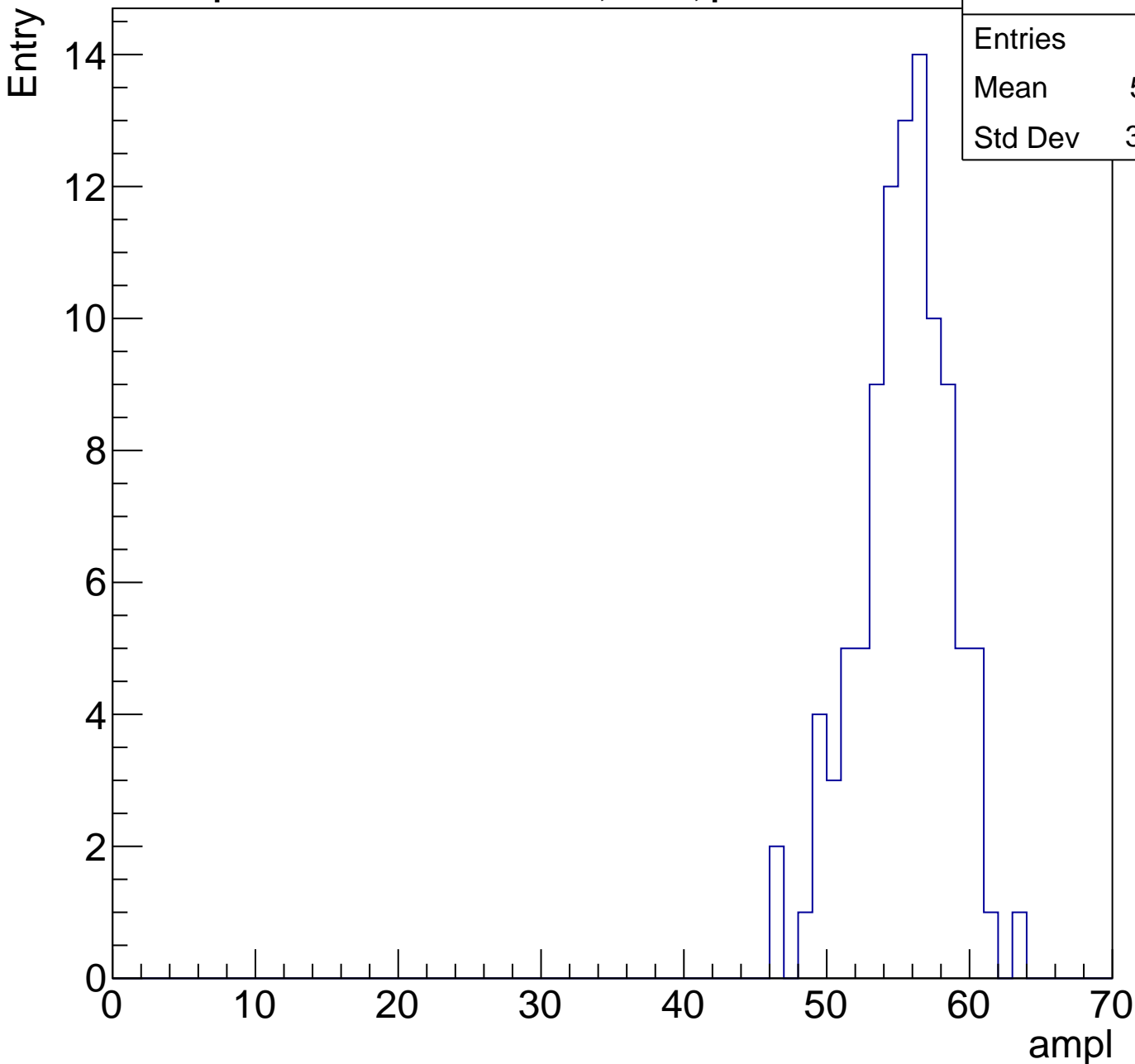
ampl



B0L101S, U21-ch39, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	54.91
Std Dev	3.245



B0L101S, U21-ch39, adc5

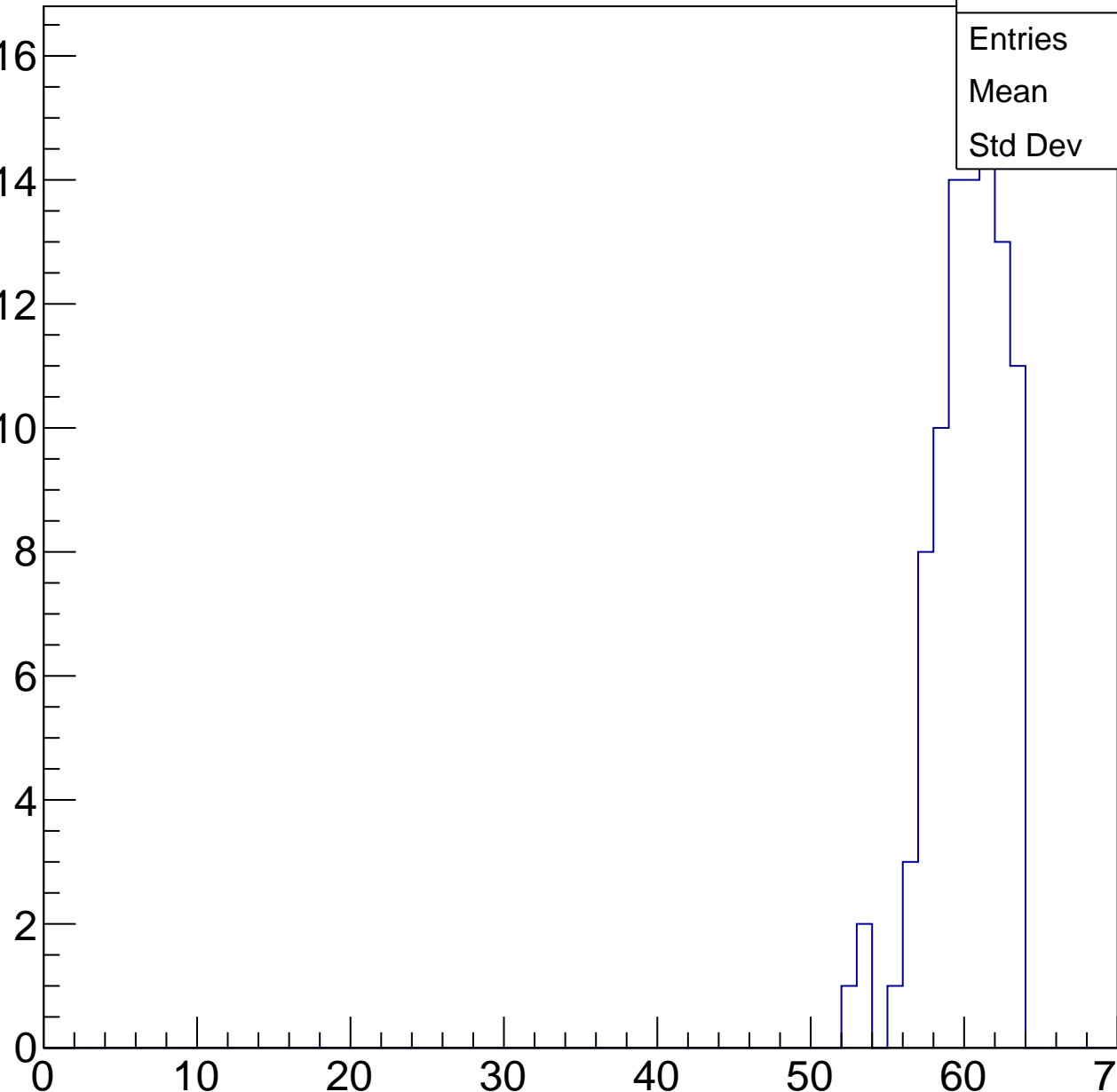
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.76
Std Dev	2.371

ampl

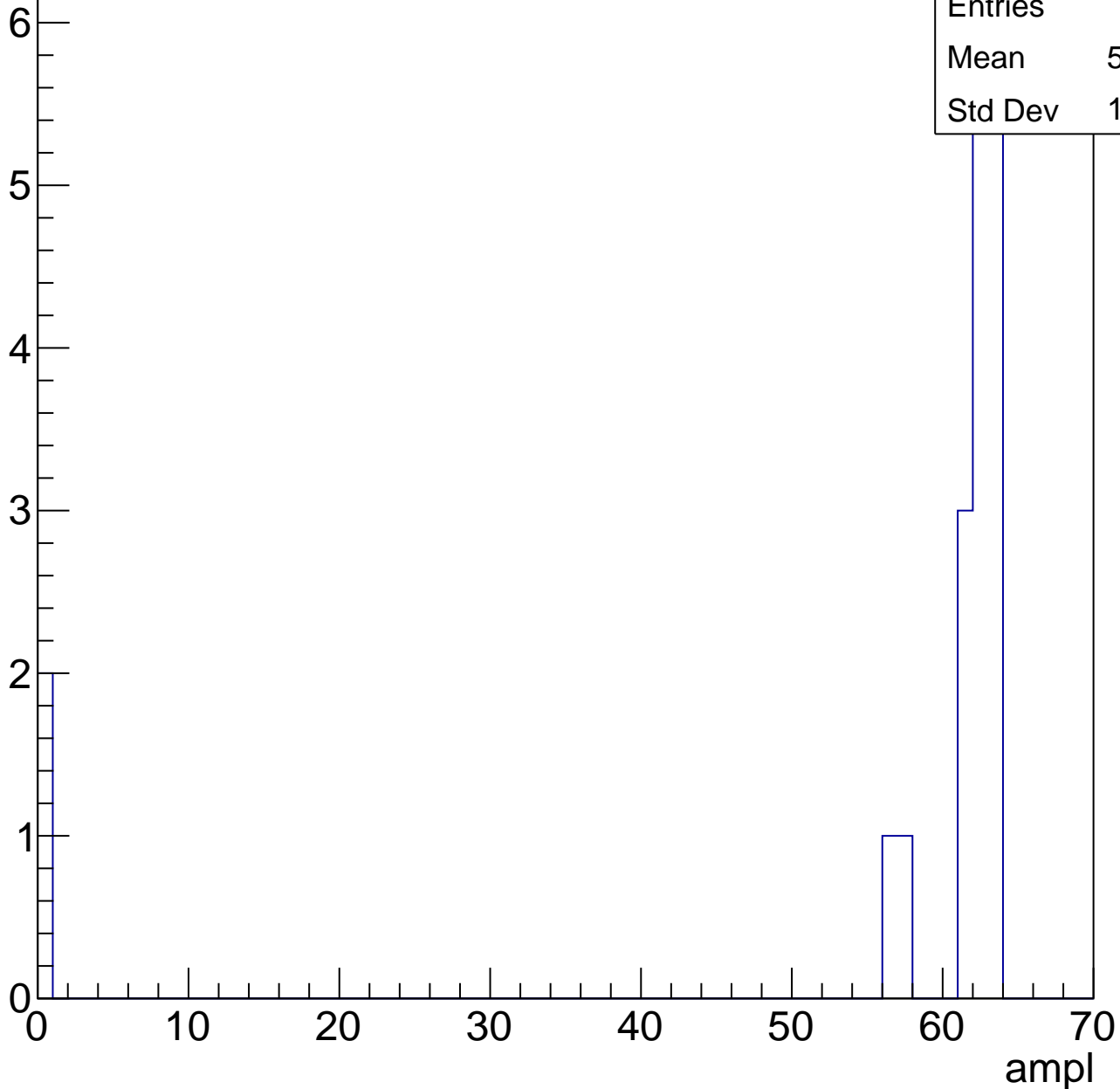


B0L101S, U21-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	55.05
Std Dev	18.97



B0L101S, U21-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch40, adc0

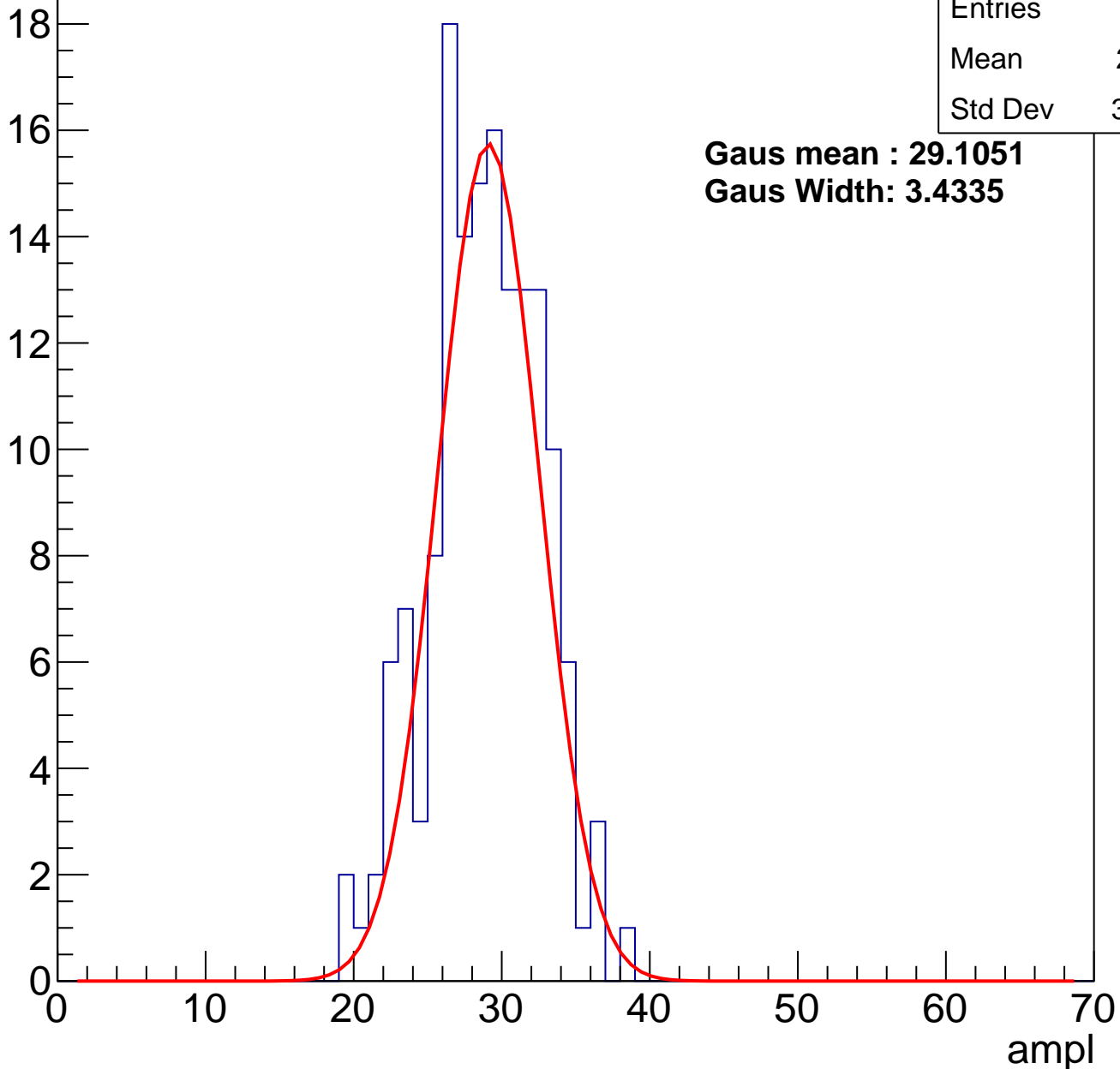
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.41
Std Dev	3.712

Gaus mean : 29.1051

Gaus Width: 3.4335

Entry



B0L101S, U21-ch40, adc1

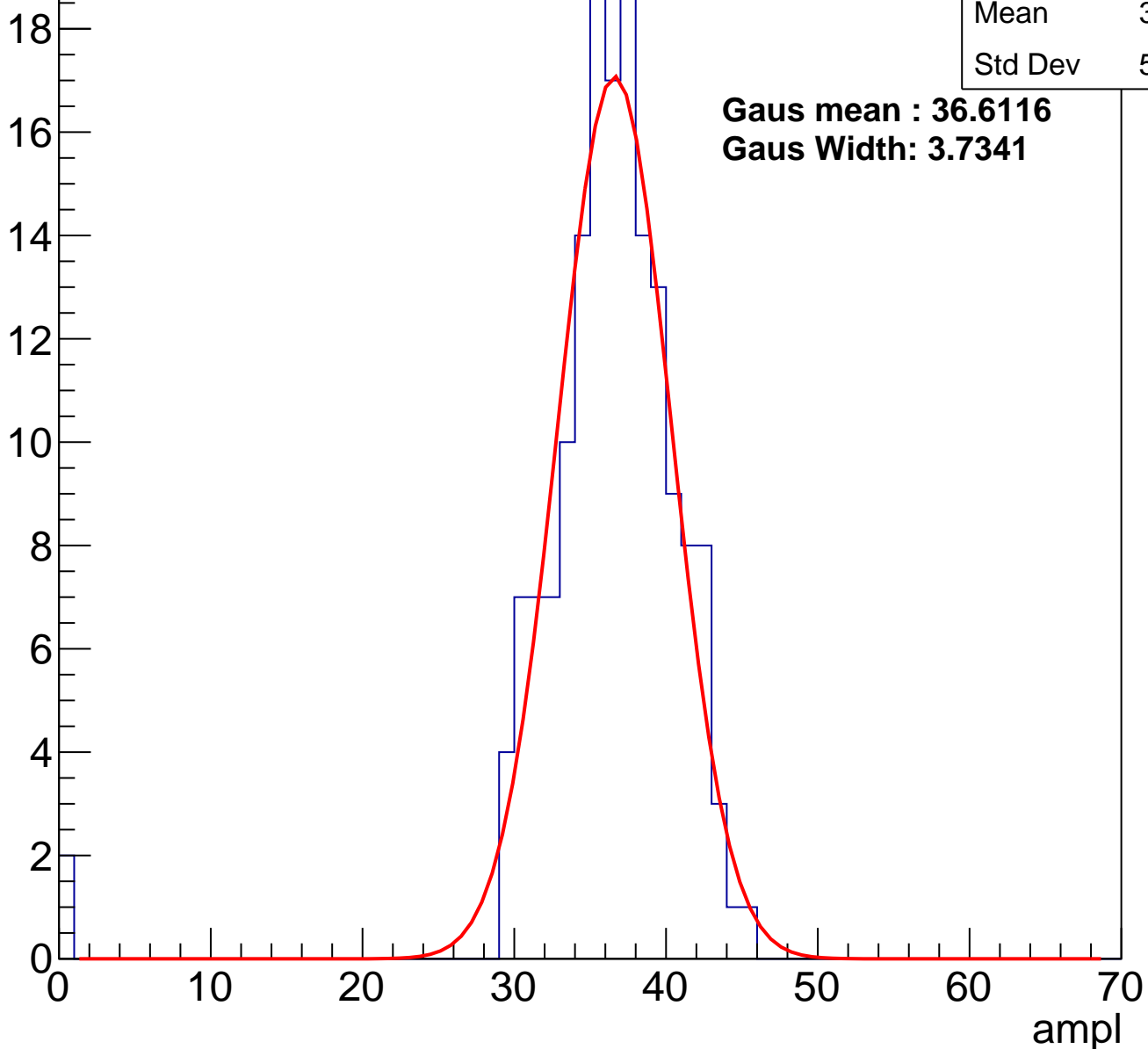
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	35.79
Std Dev	5.313

Gaus mean : 36.6116

Gaus Width: 3.7341

Entry



B0L101S, U21-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	43.4
Std Dev	3.171

Entry

Gaus mean : 43.2122

Gaus Width: 3.3709

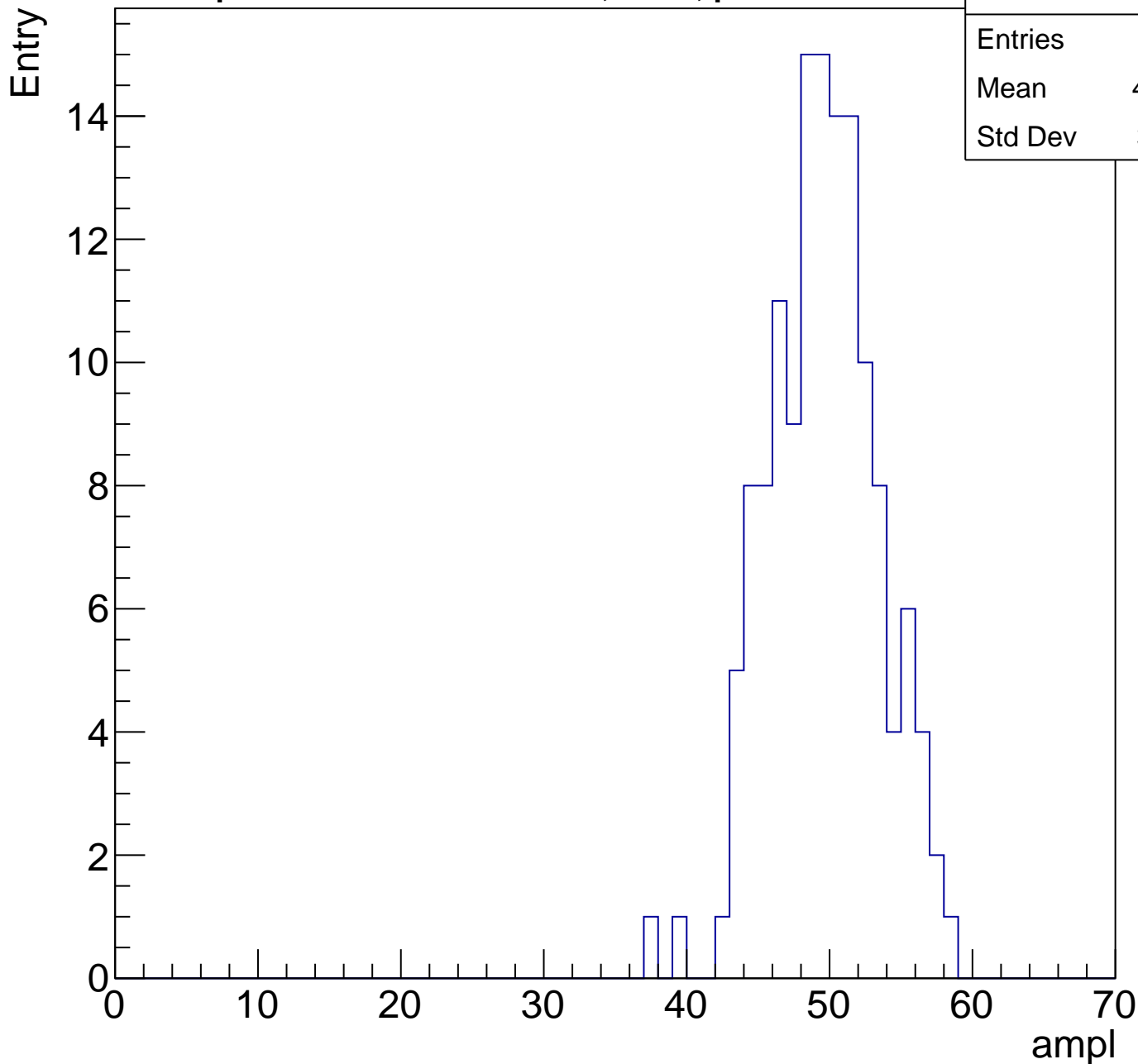
ampl

12
10
8
6
4
2
0

B0L101S, U21-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	49.12
Std Dev	3.781



B0L101S, U21-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

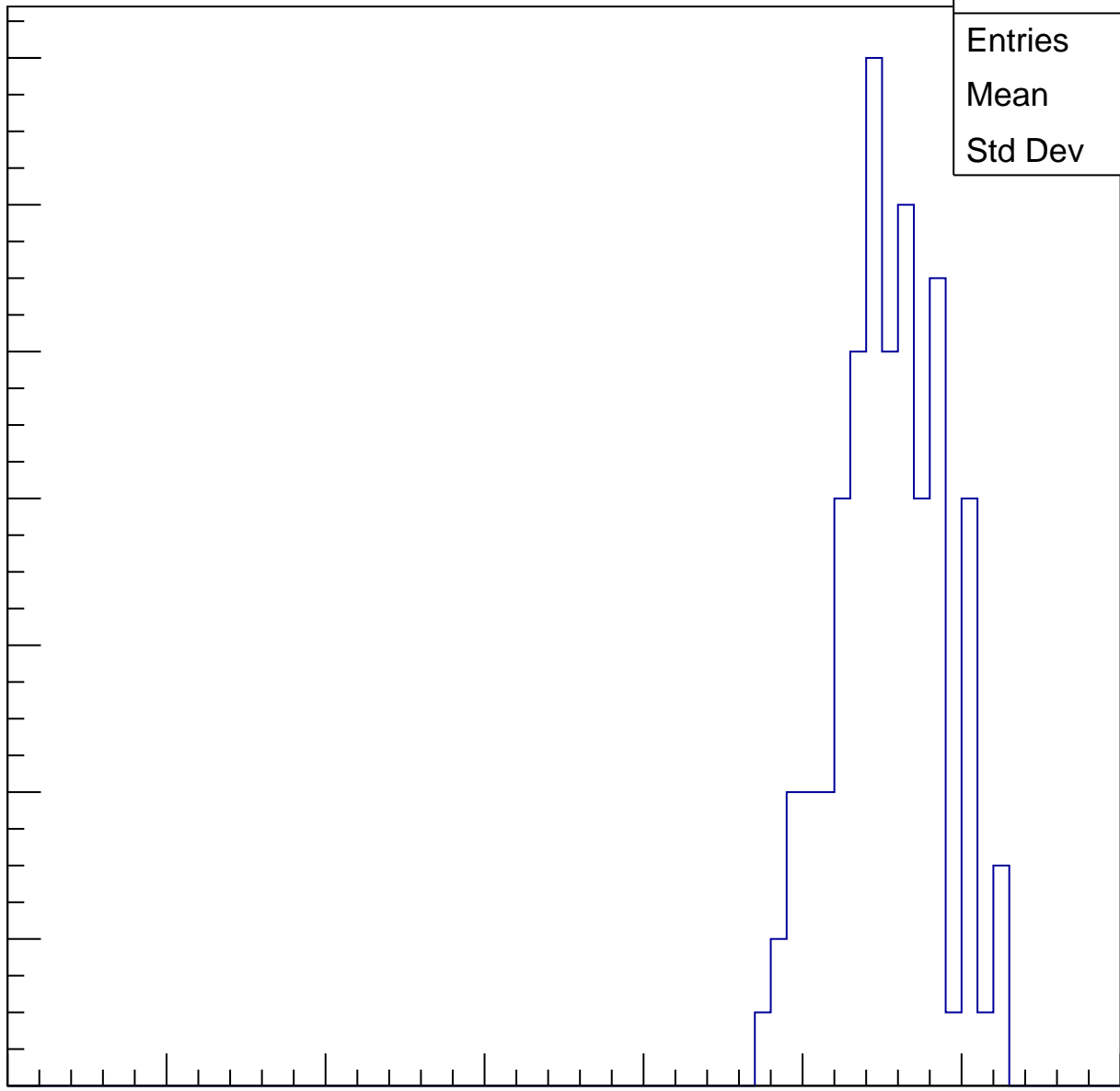
Entries	101
Mean	54.92
Std Dev	3.356

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch40, adc5

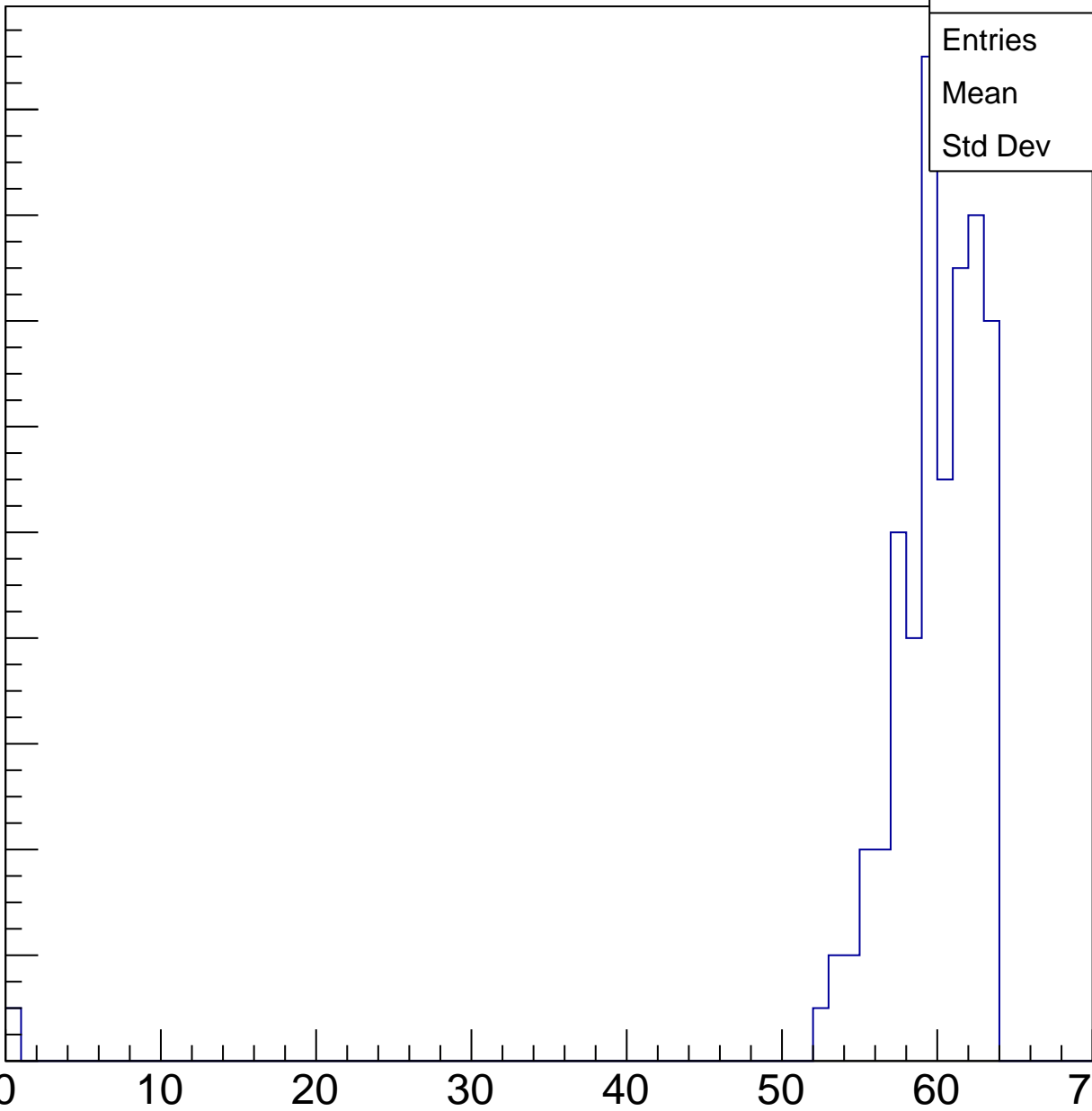
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	107
Mean	59.01
Std Dev	6.294

ampl

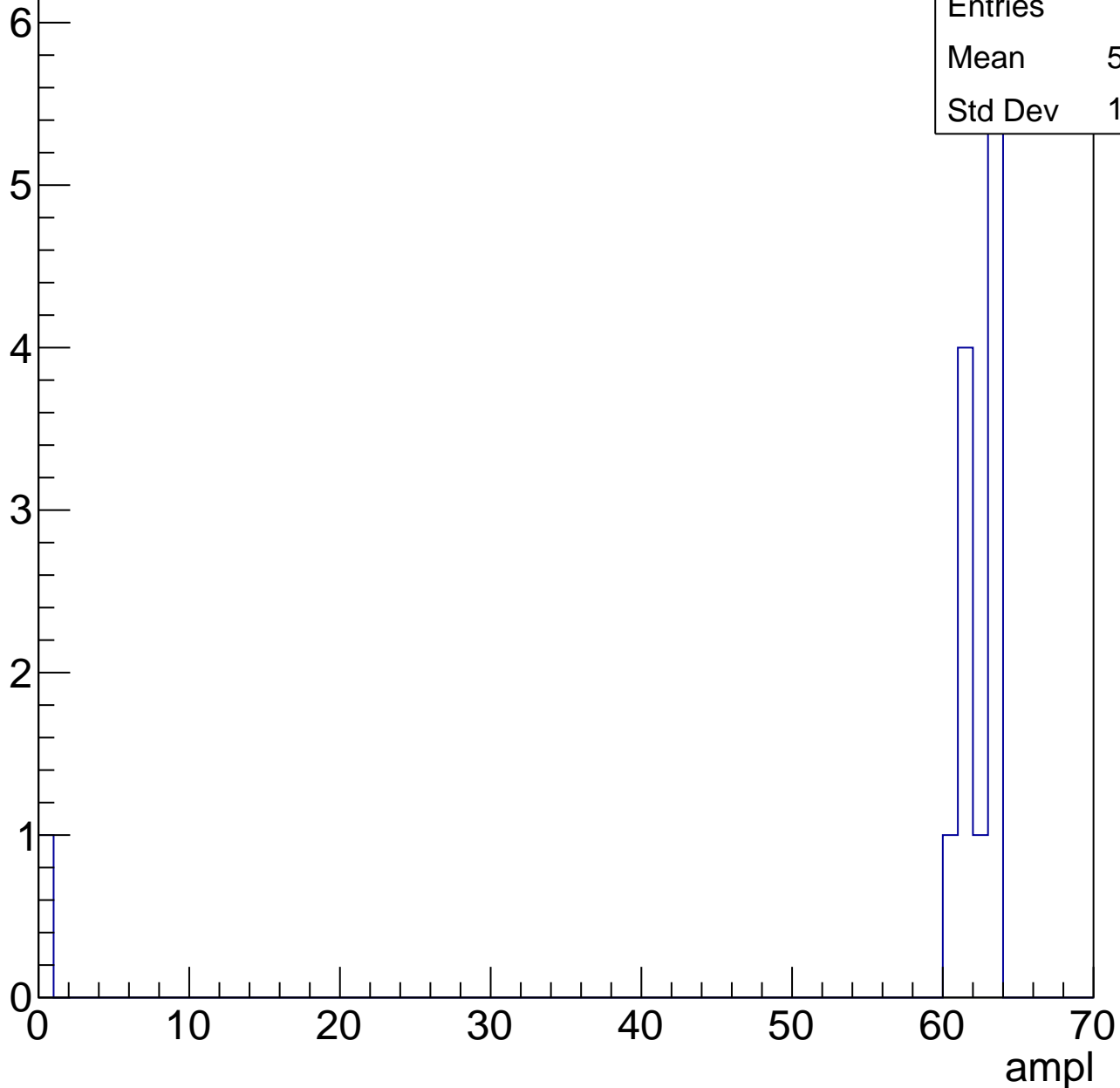


B0L101S, U21-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.23
Std Dev	16.55



B0L101S, U21-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



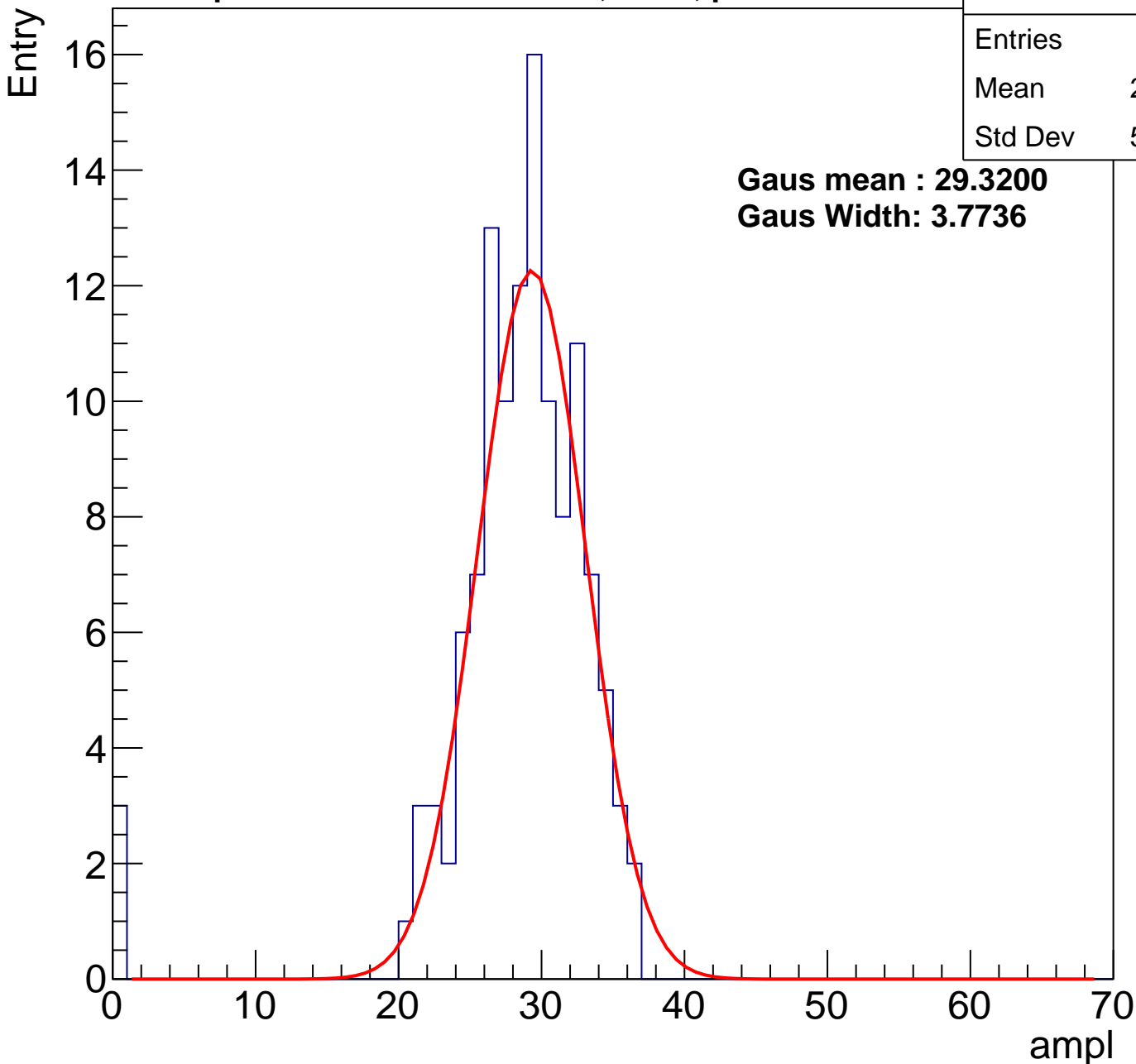
B0L101S, U21-ch41, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	27.87
Std Dev	5.635

Gaus mean : 29.3200

Gaus Width: 3.7736



B0L101S, U21-ch41, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	35.31
Std Dev	3.898

Gaus mean : 36.0331

Gaus Width: 3.8693

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

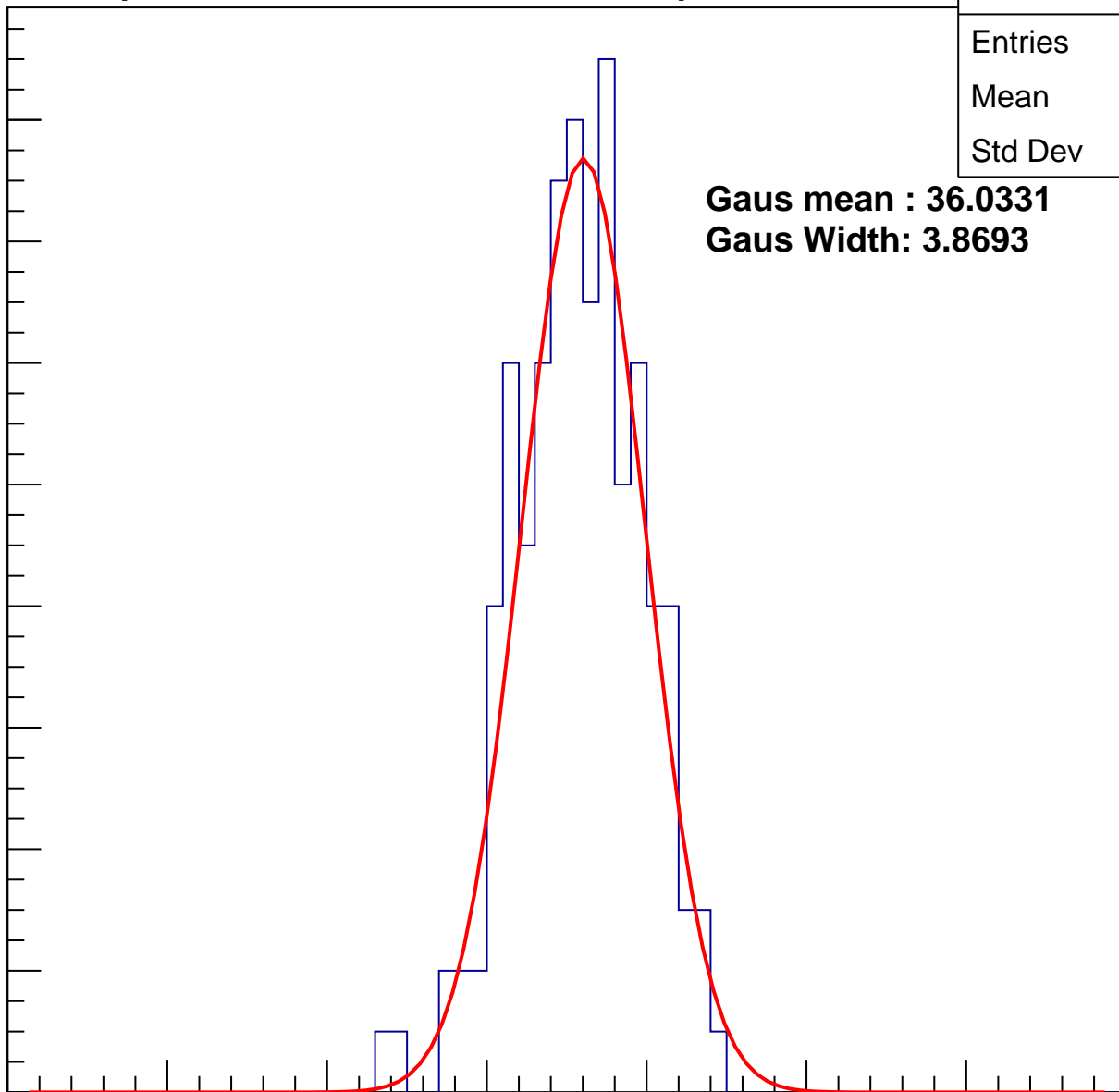
40

50

60

70

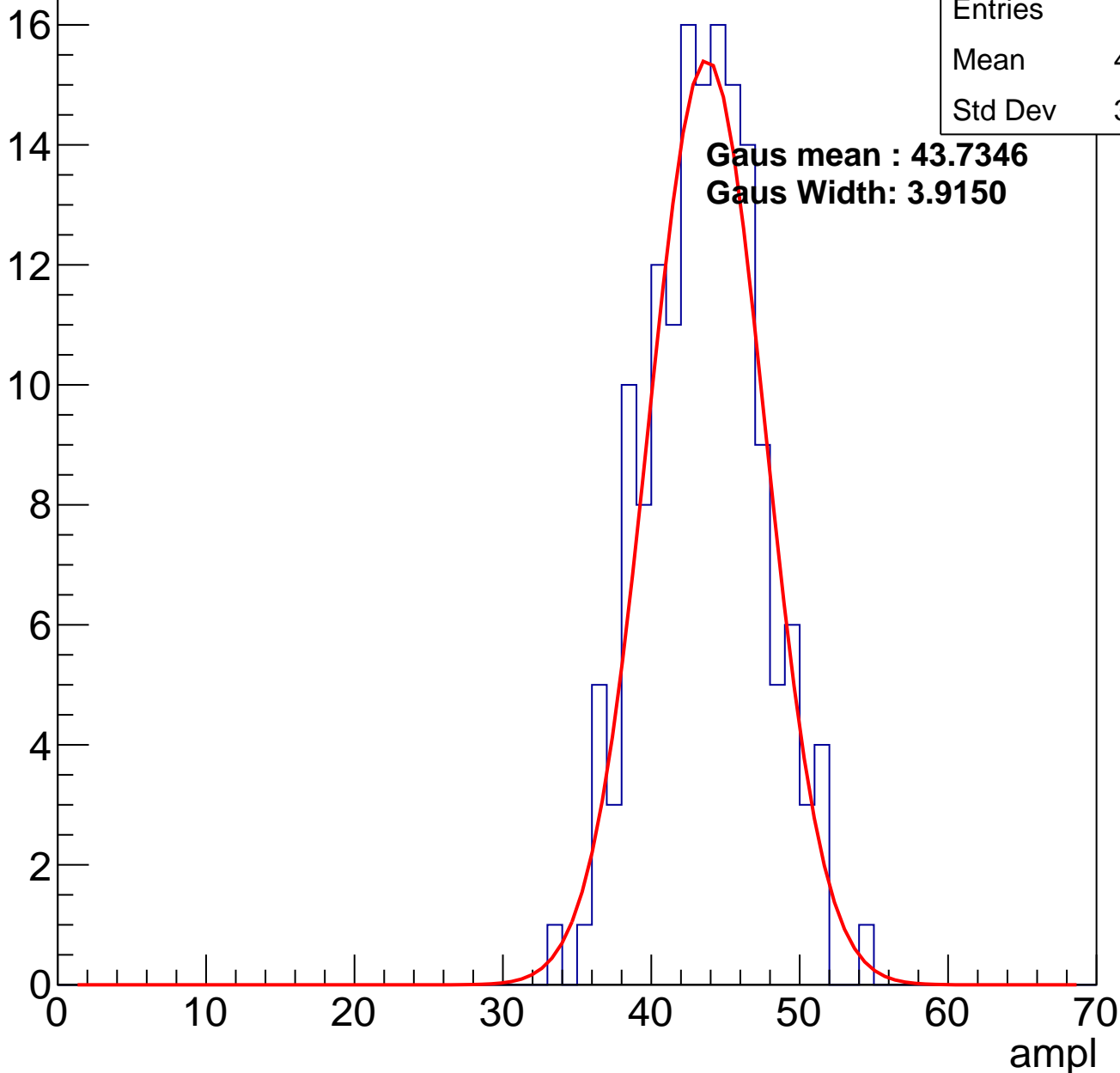
ampl



B0L101S, U21-ch41, adc2

calib_packv5_042523_0143.root, FC#1, port C1

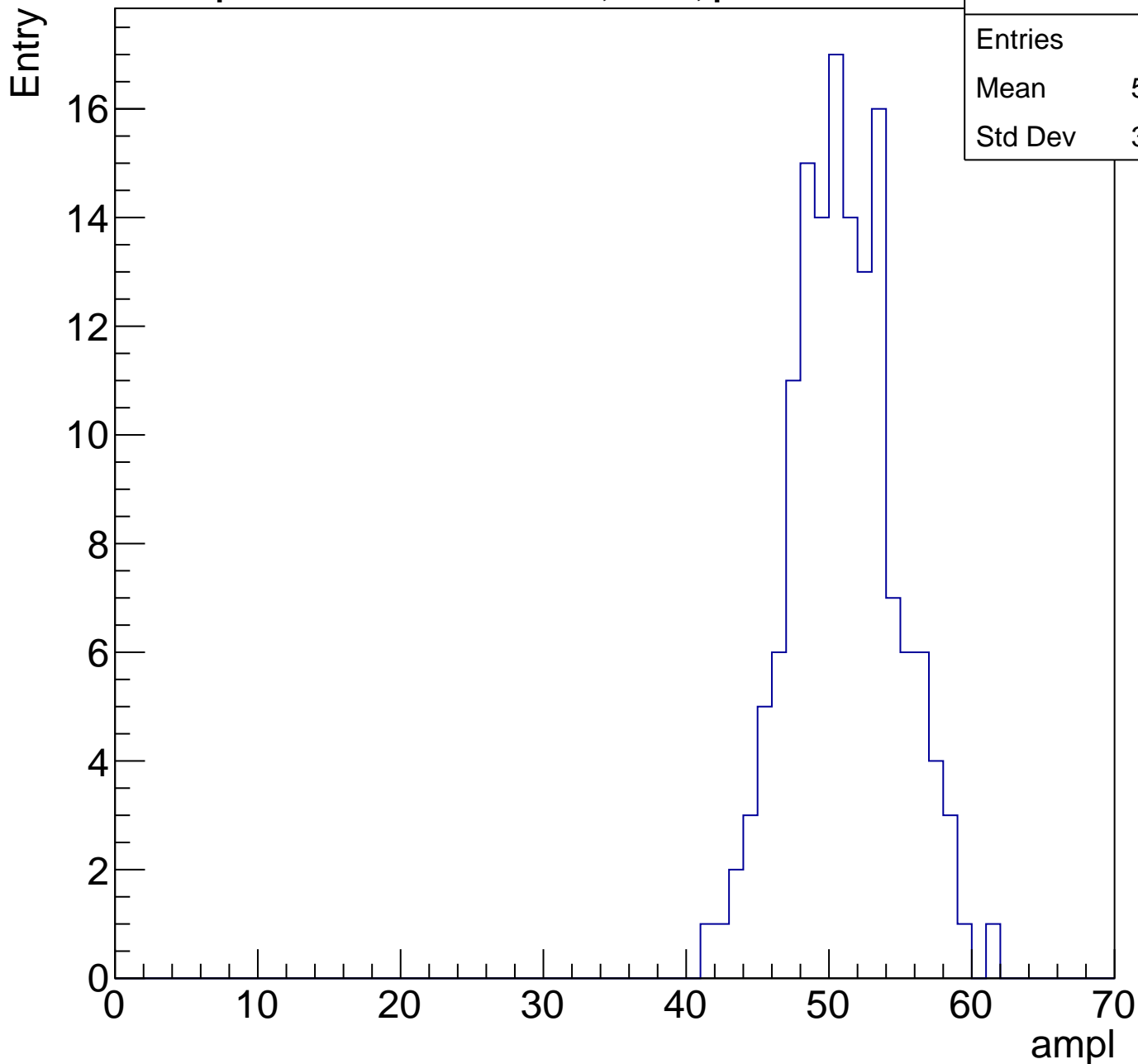
Entry



B0L101S, U21-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	50.54
Std Dev	3.683



B0L101S, U21-ch41, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	56.73
Std Dev	2.829

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch41, adc5

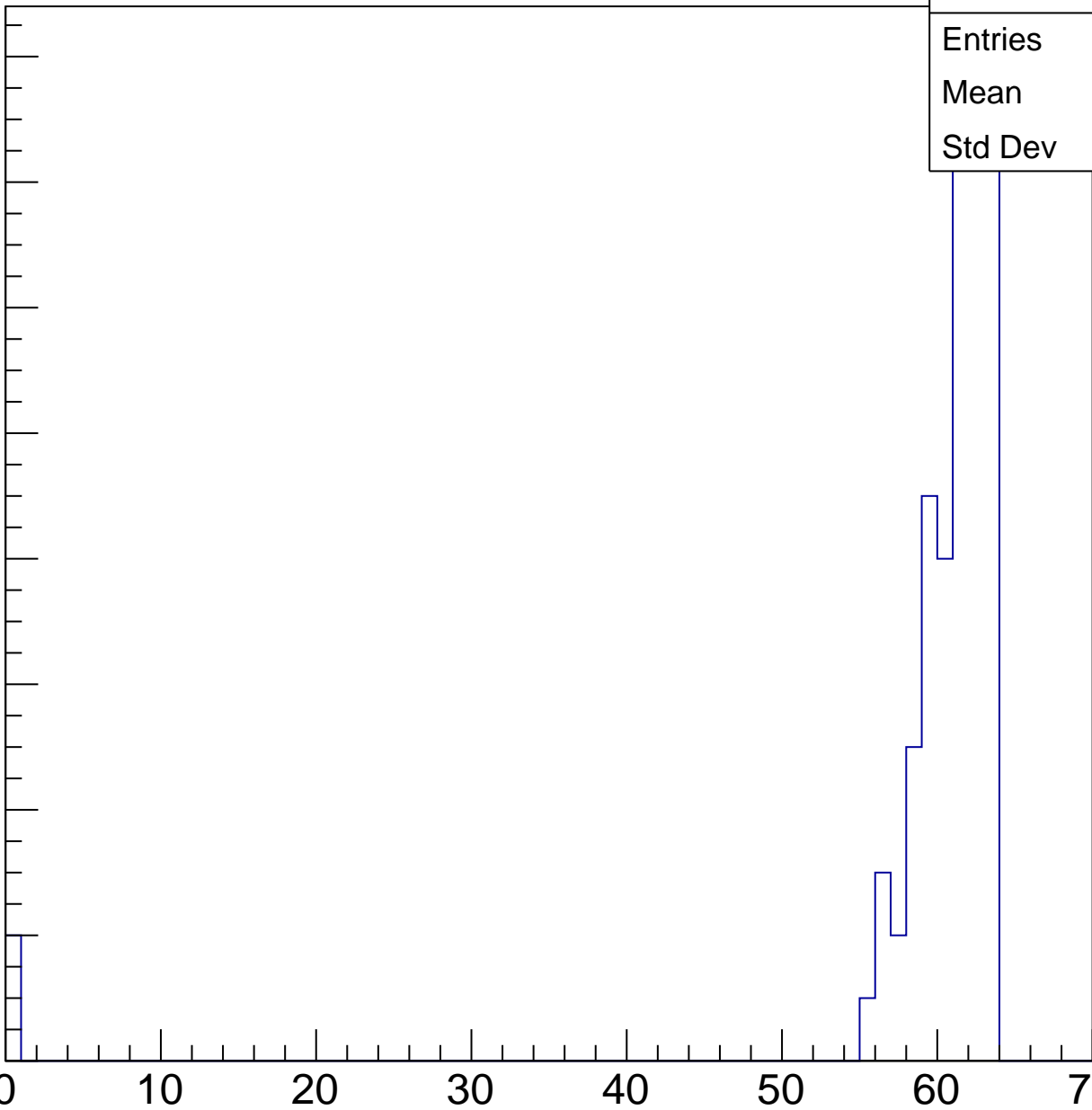
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.15
Std Dev	9.798

ampl



B0L101S, U21-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch42, adc0

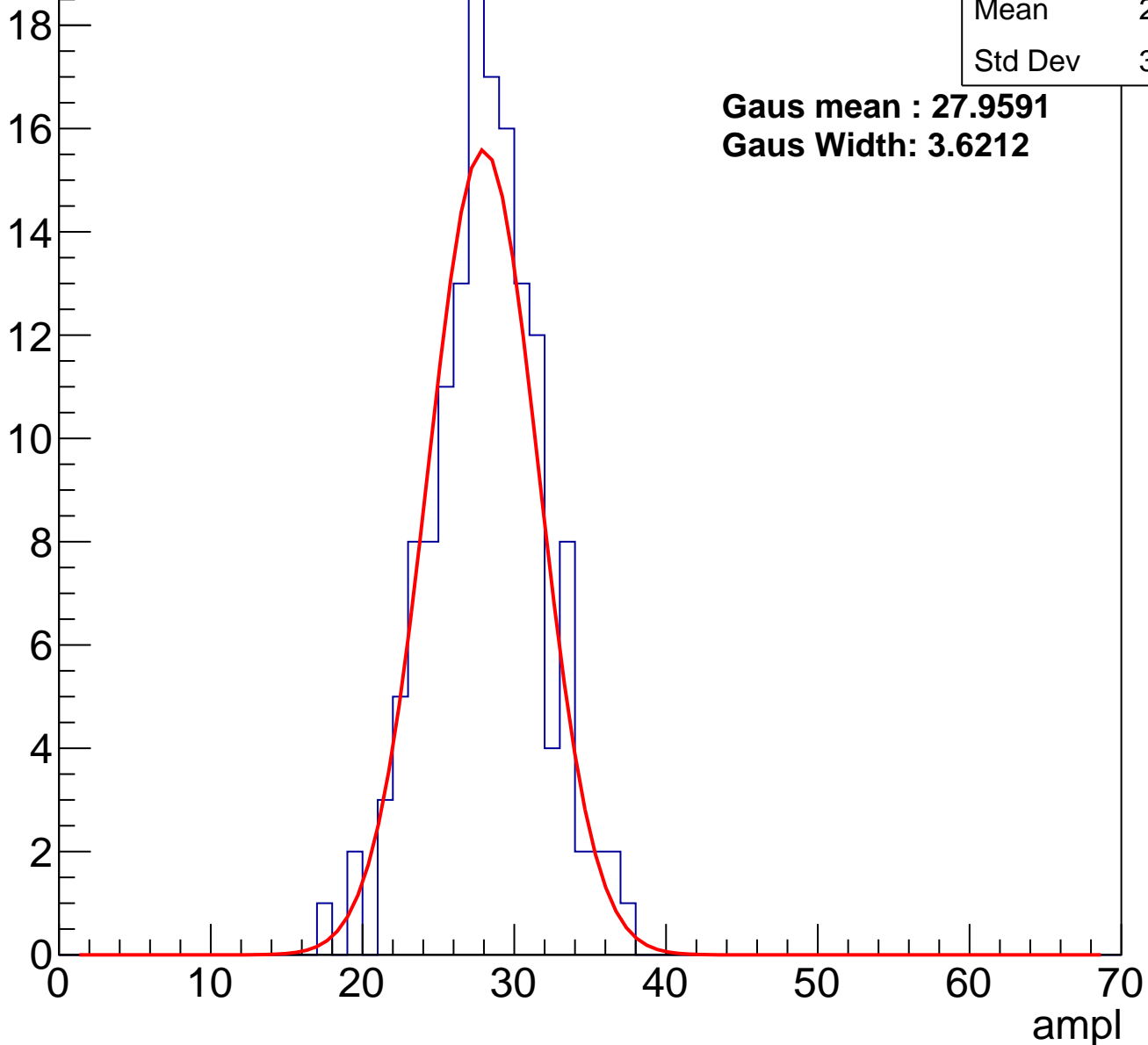
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	27.69
Std Dev	3.608

Gaus mean : 27.9591

Gaus Width: 3.6212

Entry



B0L101S, U21-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	34.56
Std Dev	4.554

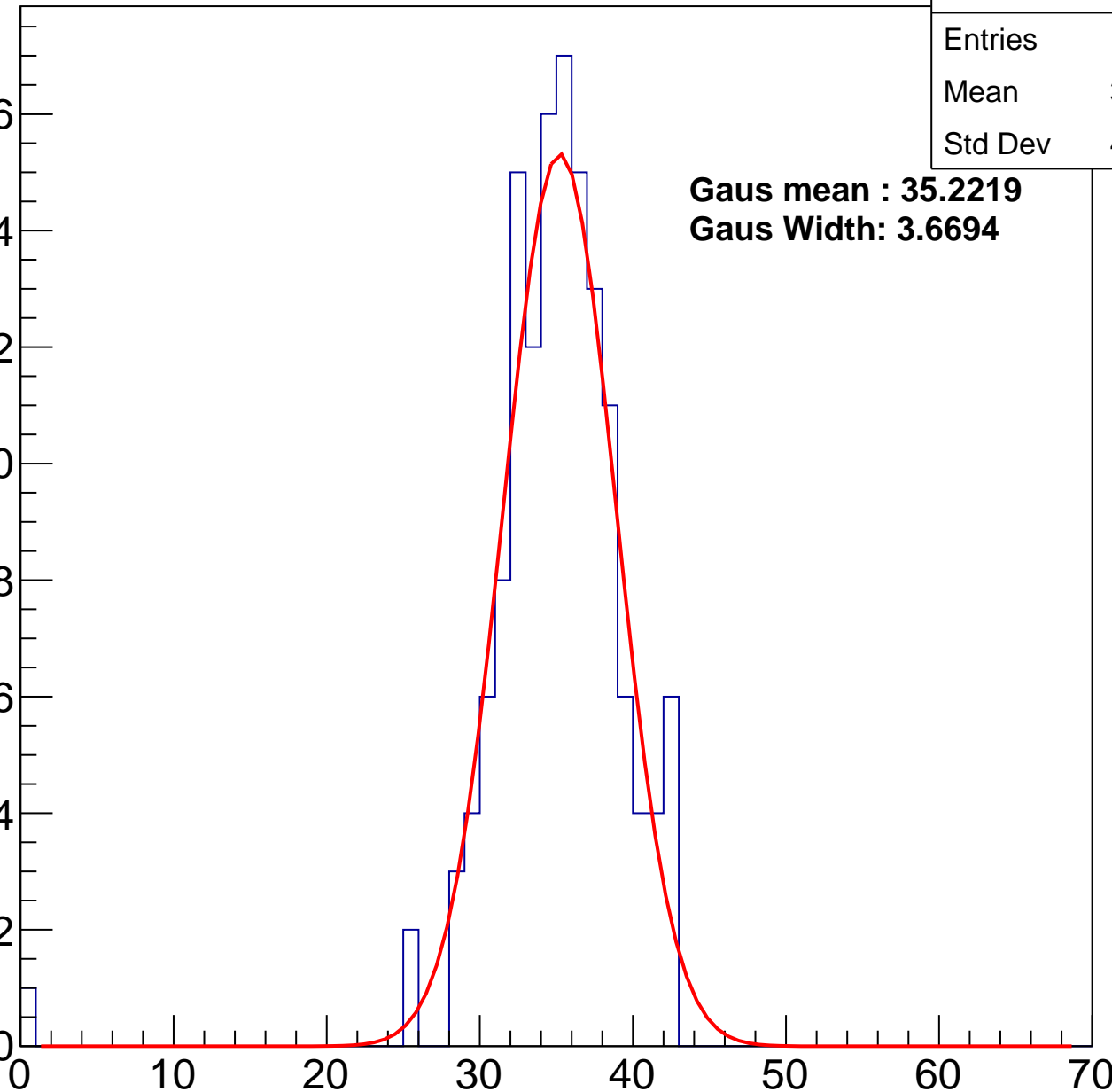
Gaus mean : 35.2219

Gaus Width: 3.6694

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch42, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

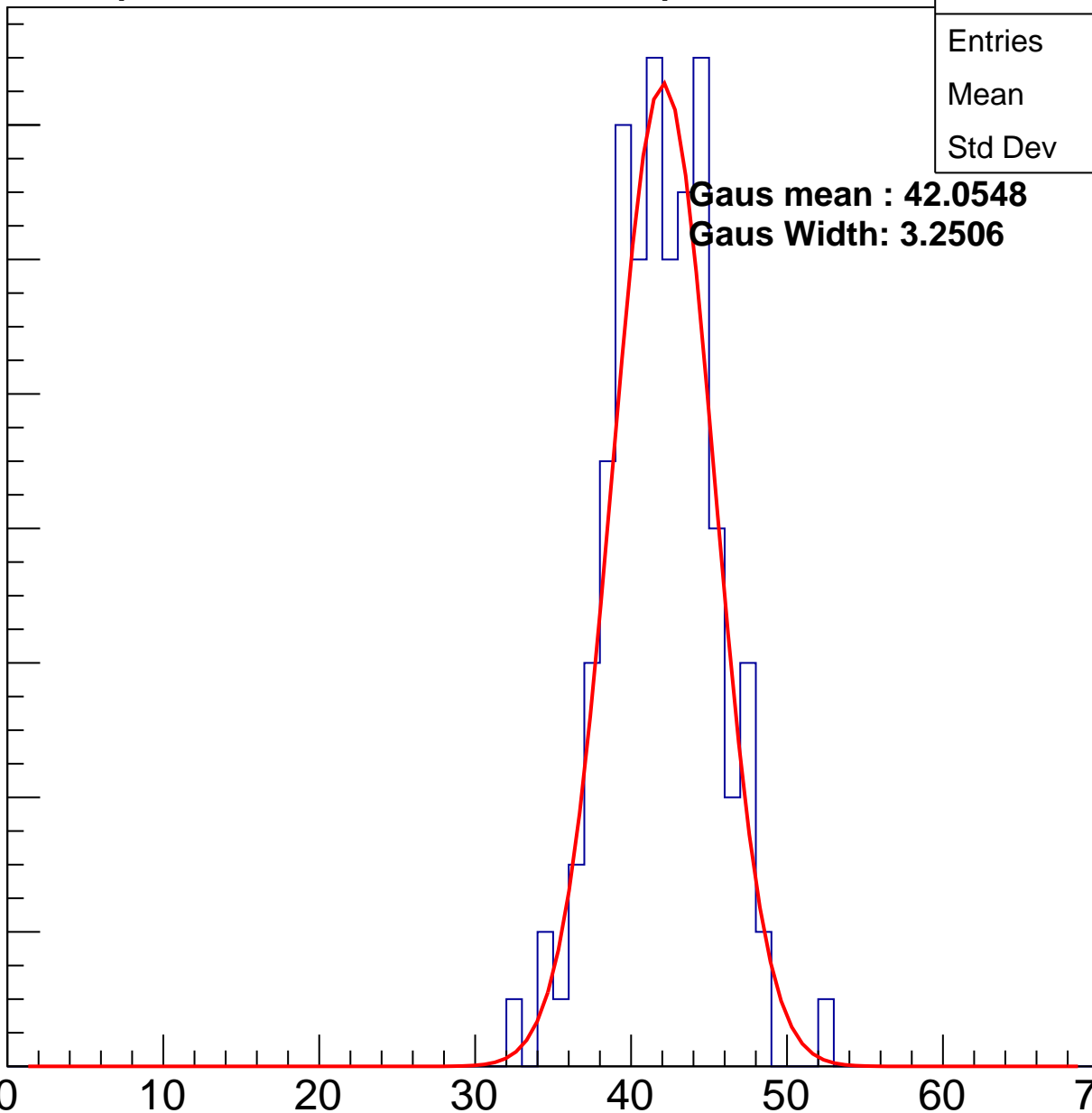
14
12
10
8
6
4
2
0

Entries	124
Mean	41.49
Std Dev	3.32

Gaus mean : 42.0548

Gaus Width: 3.2506

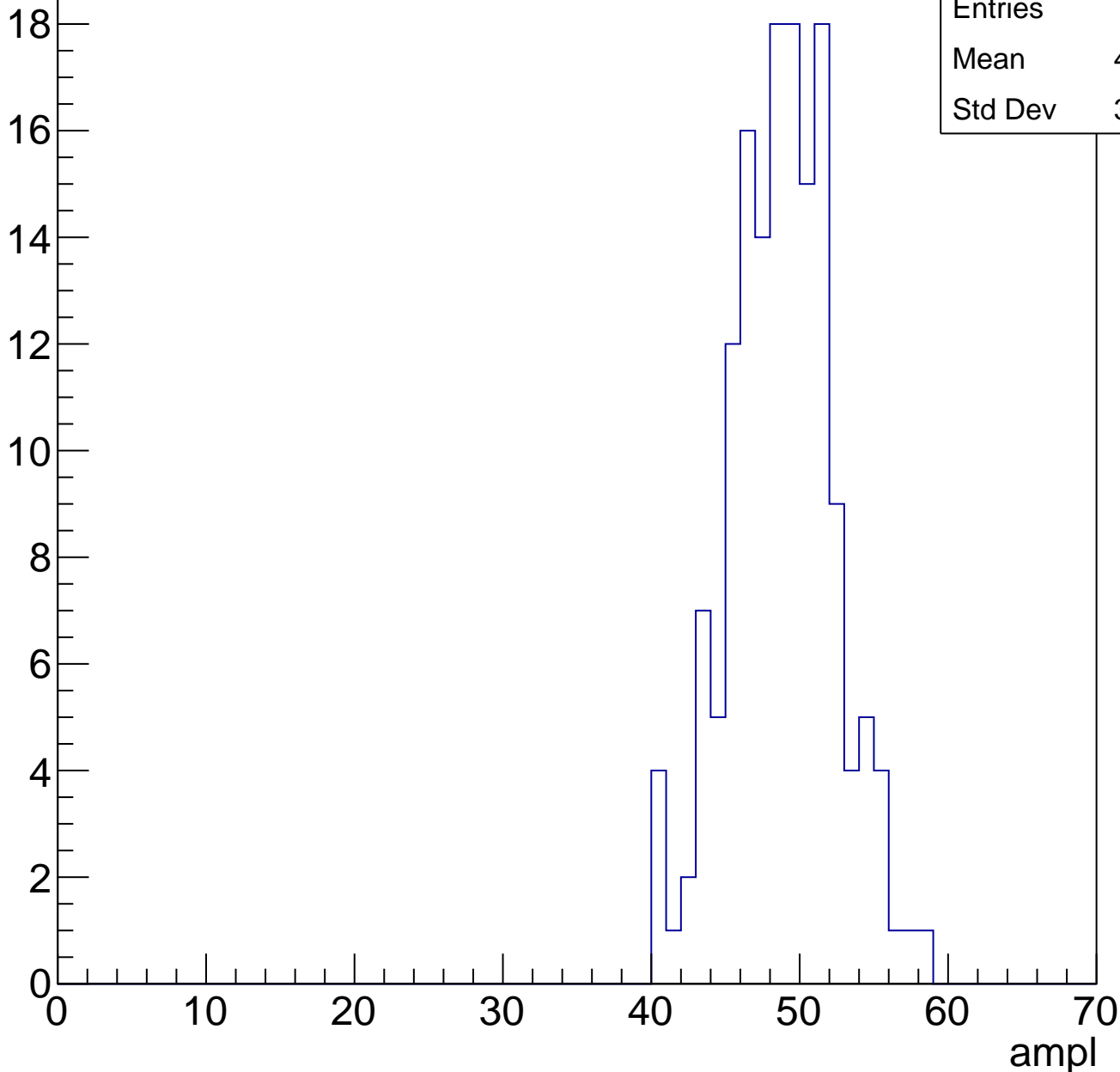
ampl



B0L101S, U21-ch42, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

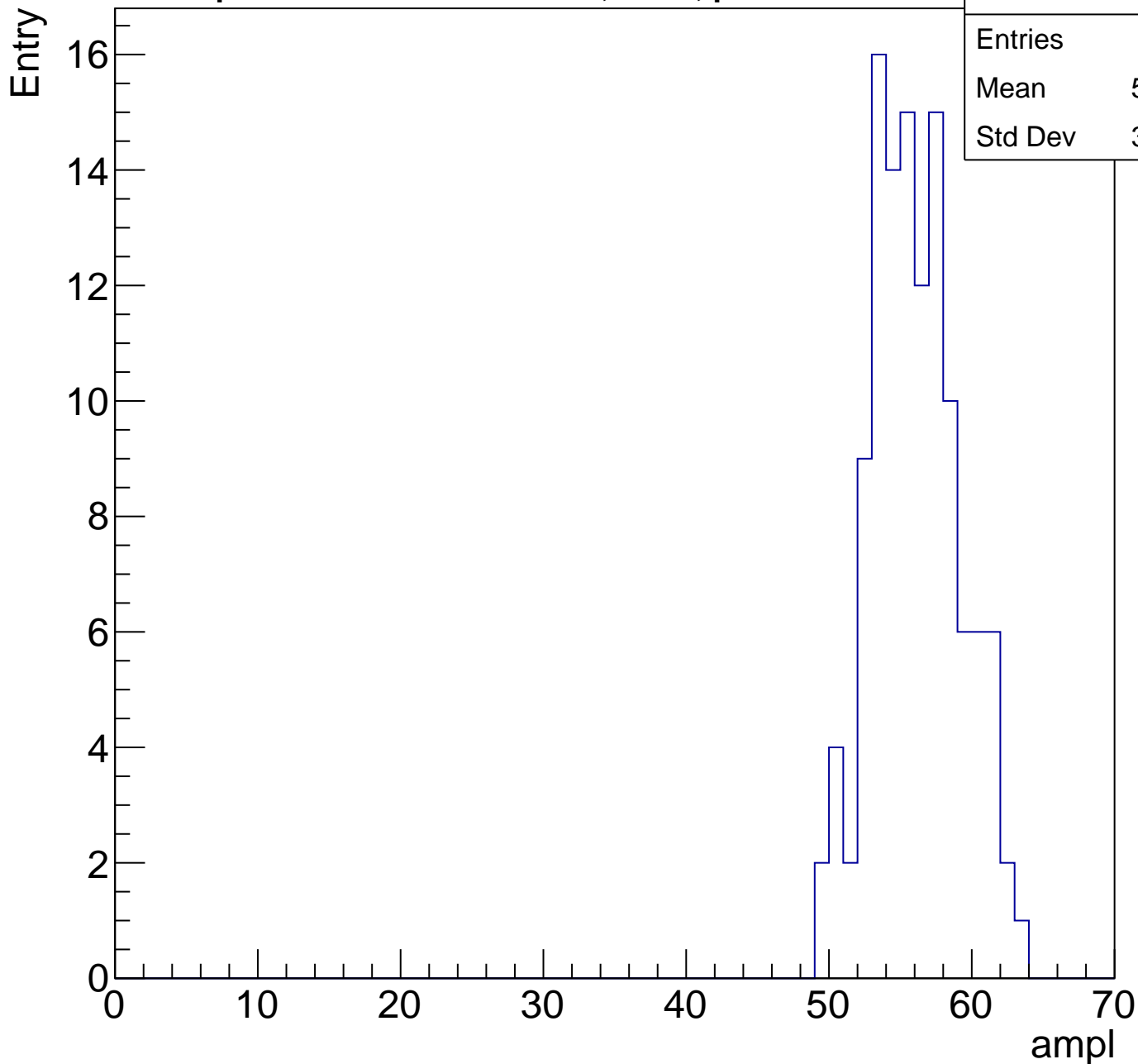


Entries	155
Mean	48.35
Std Dev	3.504

B0L101S, U21-ch42, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.59
Std Dev	3.037



B0L101S, U21-ch42, adc5

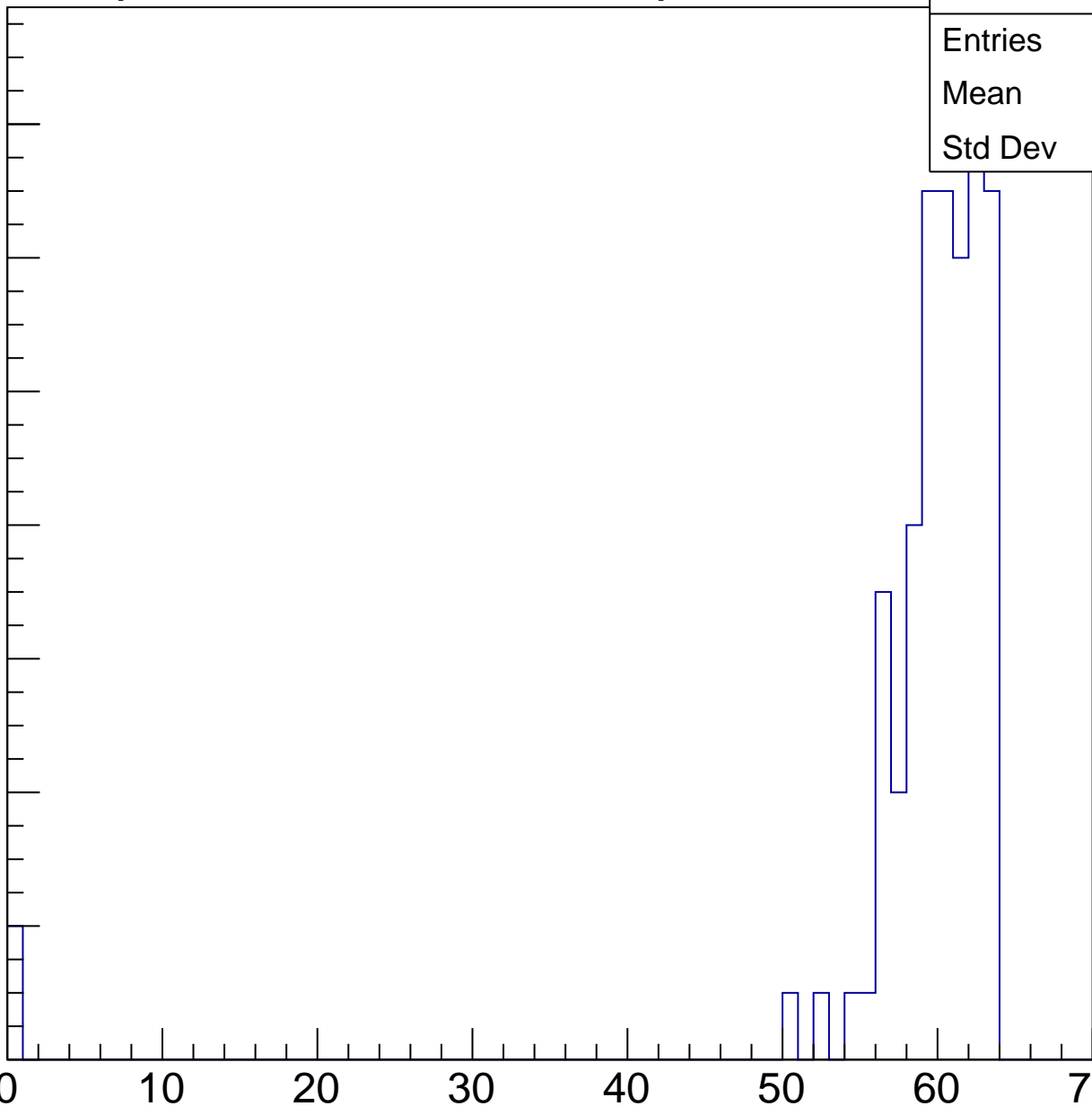
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.49
Std Dev	9.139

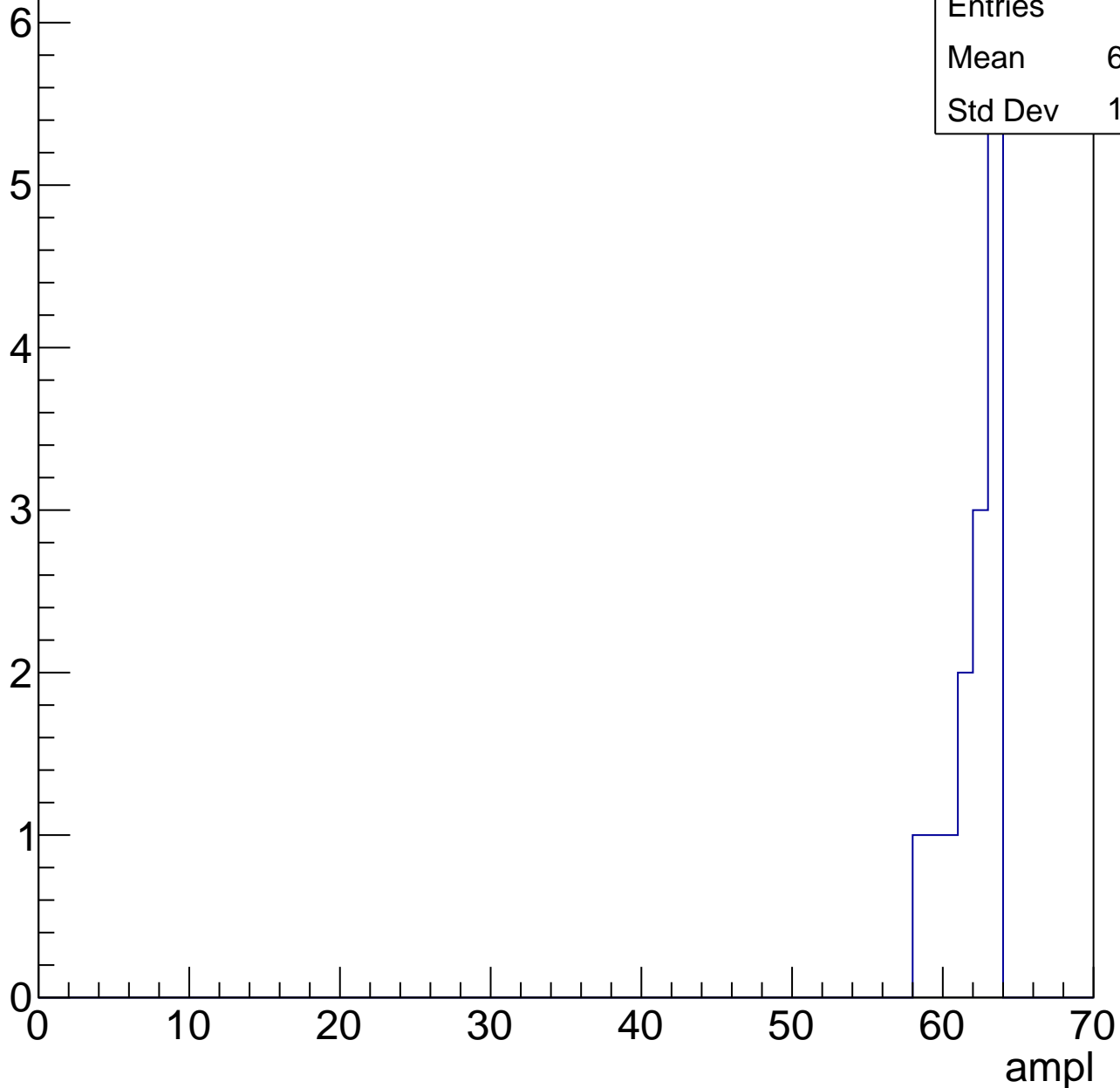
ampl



B0L101S, U21-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U21-ch43, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	31.02
Std Dev	3.24

Gaus mean : 31.3518

Gaus Width: 3.1781

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch43, adc1

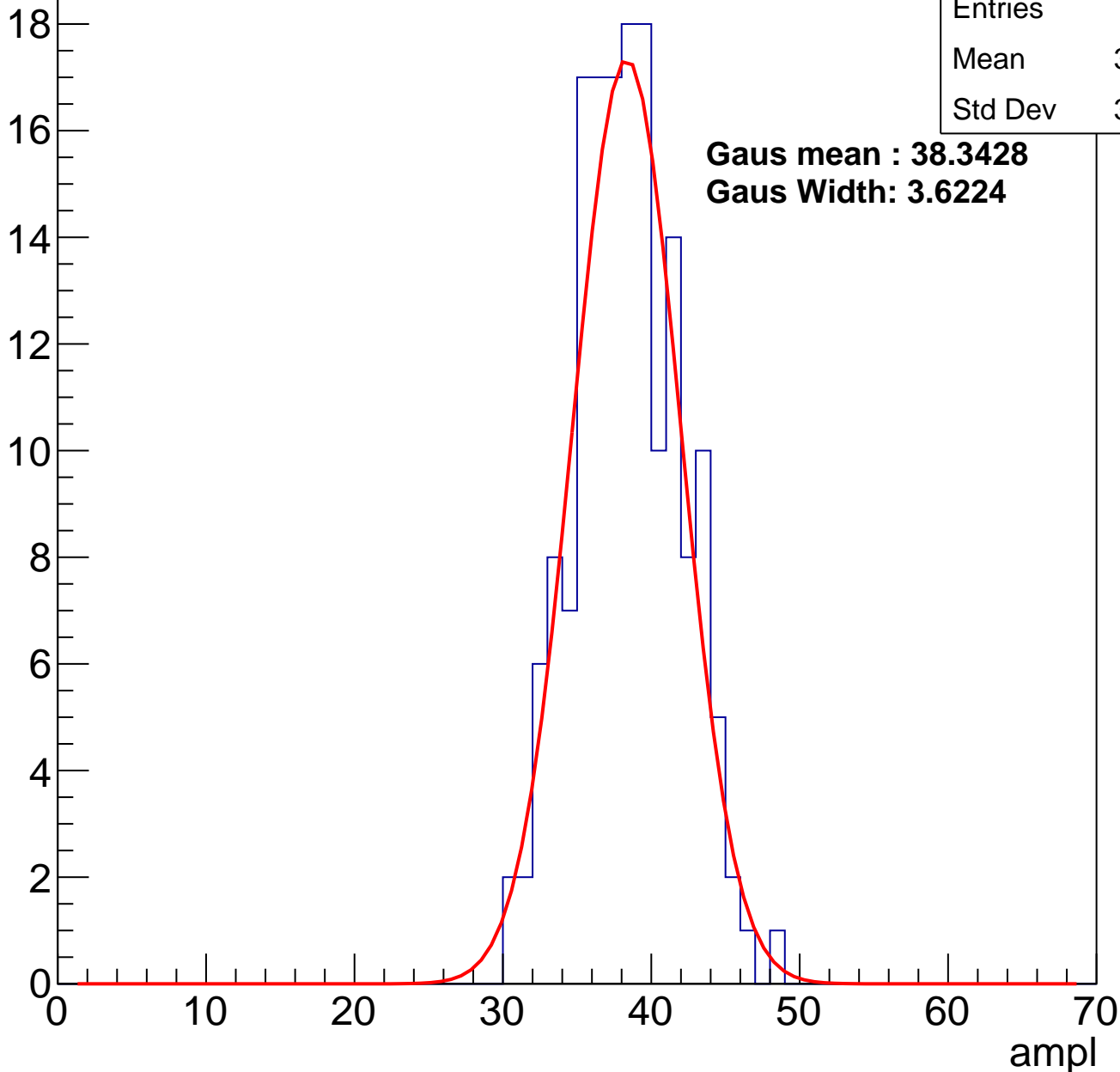
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	37.93
Std Dev	3.499

Gaus mean : 38.3428

Gaus Width: 3.6224

Entry



B0L101S, U21-ch43, adc2

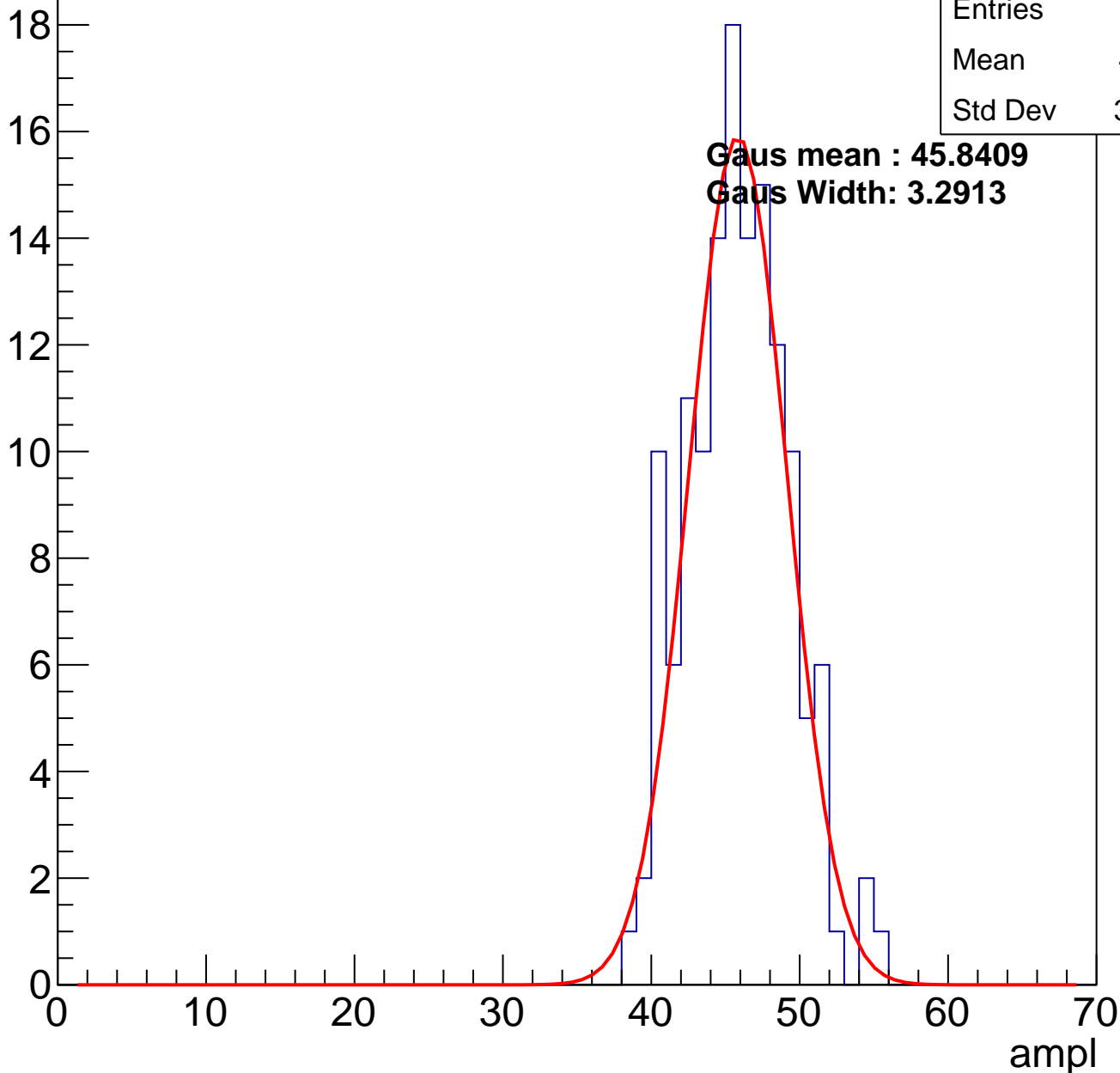
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	45.41
Std Dev	3.396

Gaus mean : 45.8409

Gaus Width: 3.2913

Entry

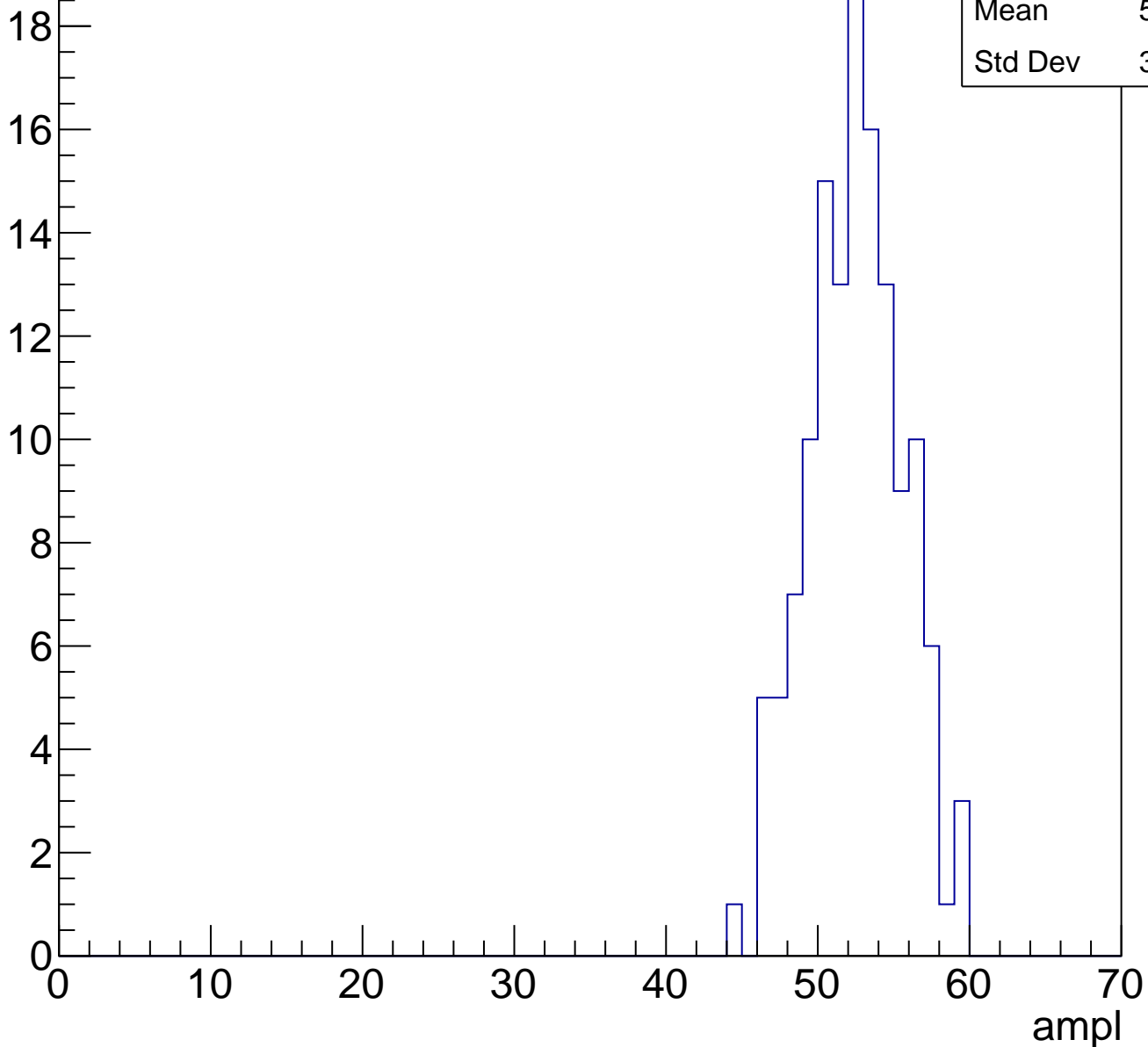


B0L101S, U21-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	52.02
Std Dev	3.109

Entry

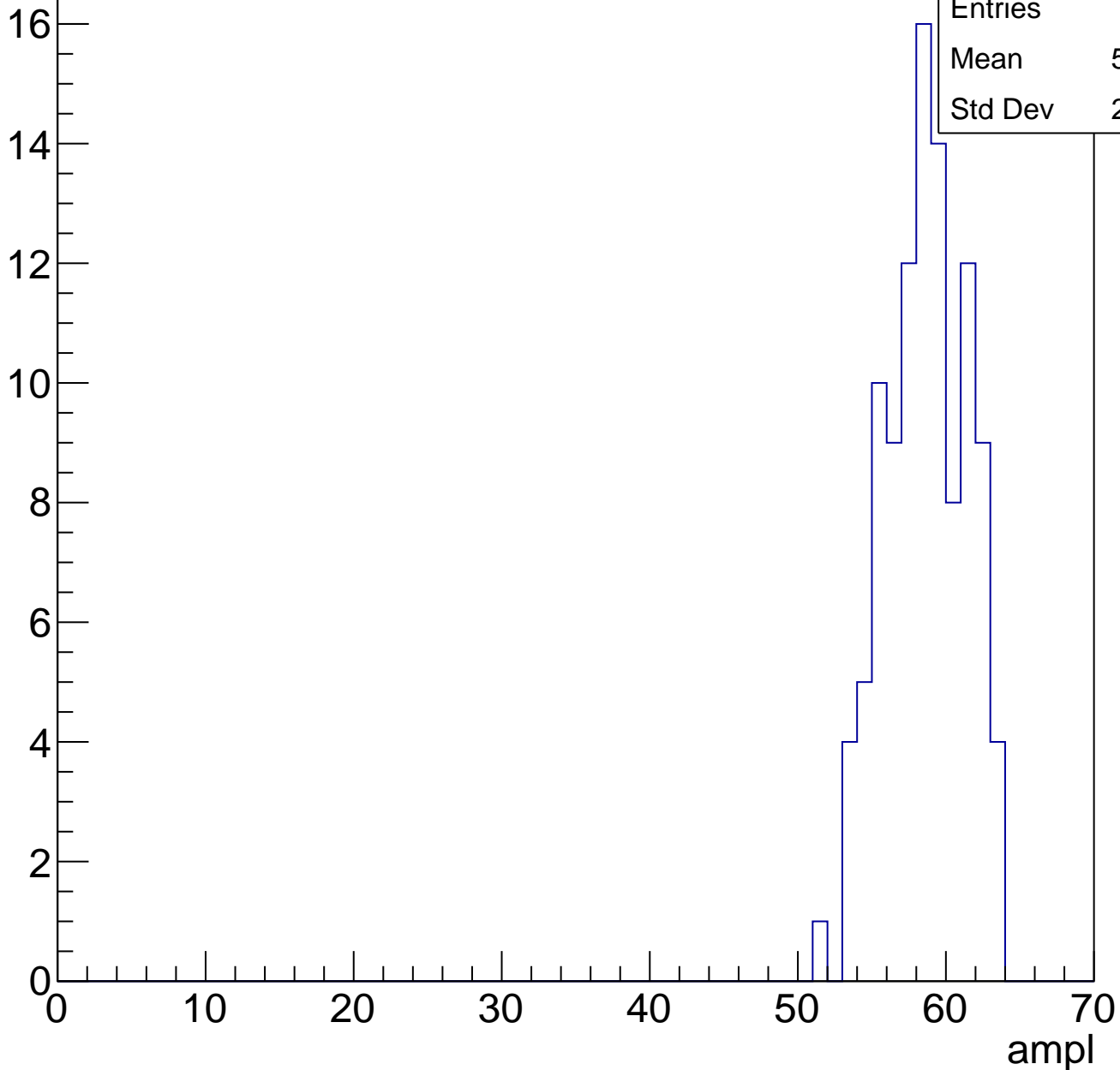


B0L101S, U21-ch43, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	58.14
Std Dev	2.708

Entry



B0L101S, U21-ch43, adc5

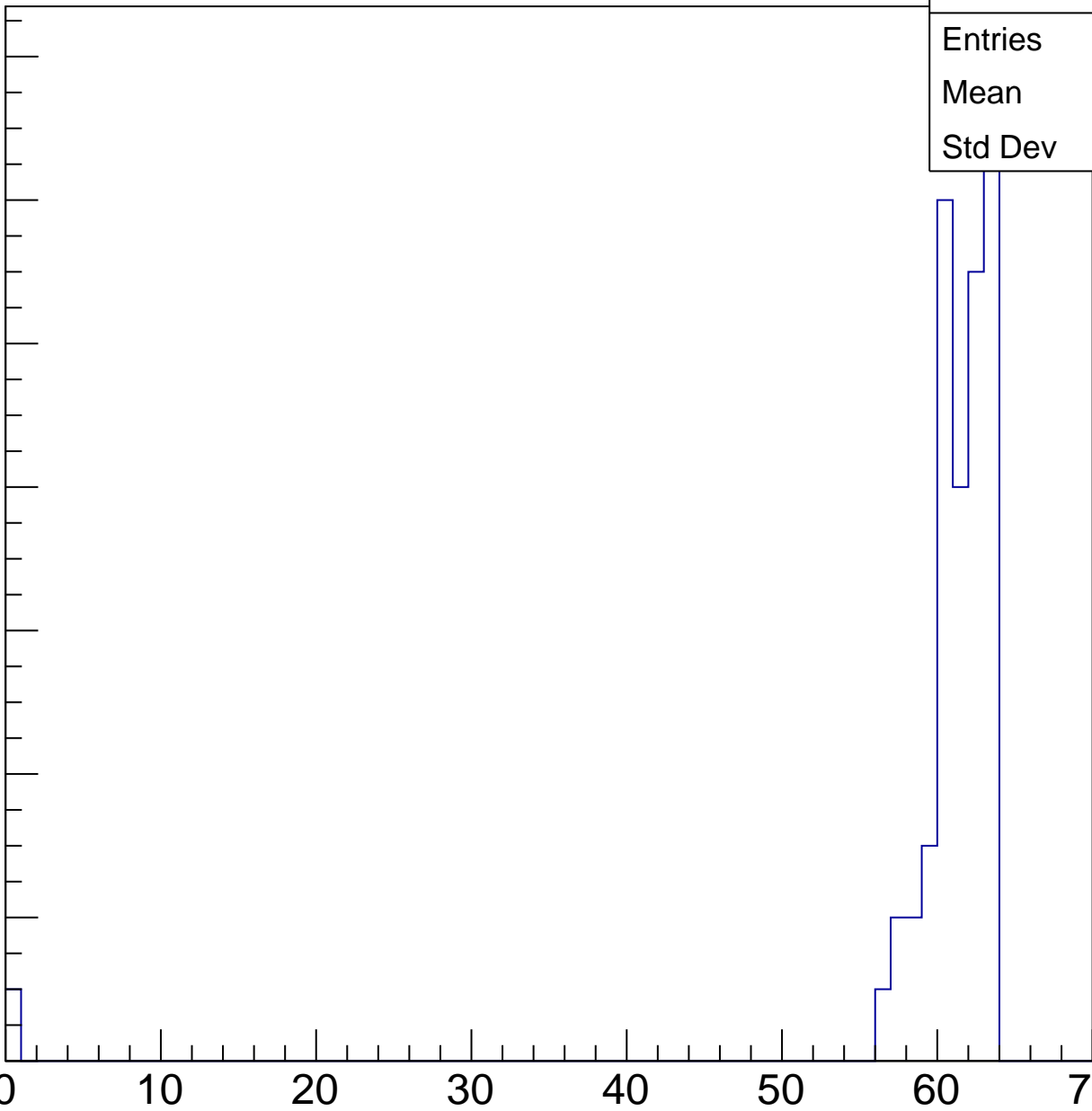
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	54
Mean	59.91
Std Dev	8.414

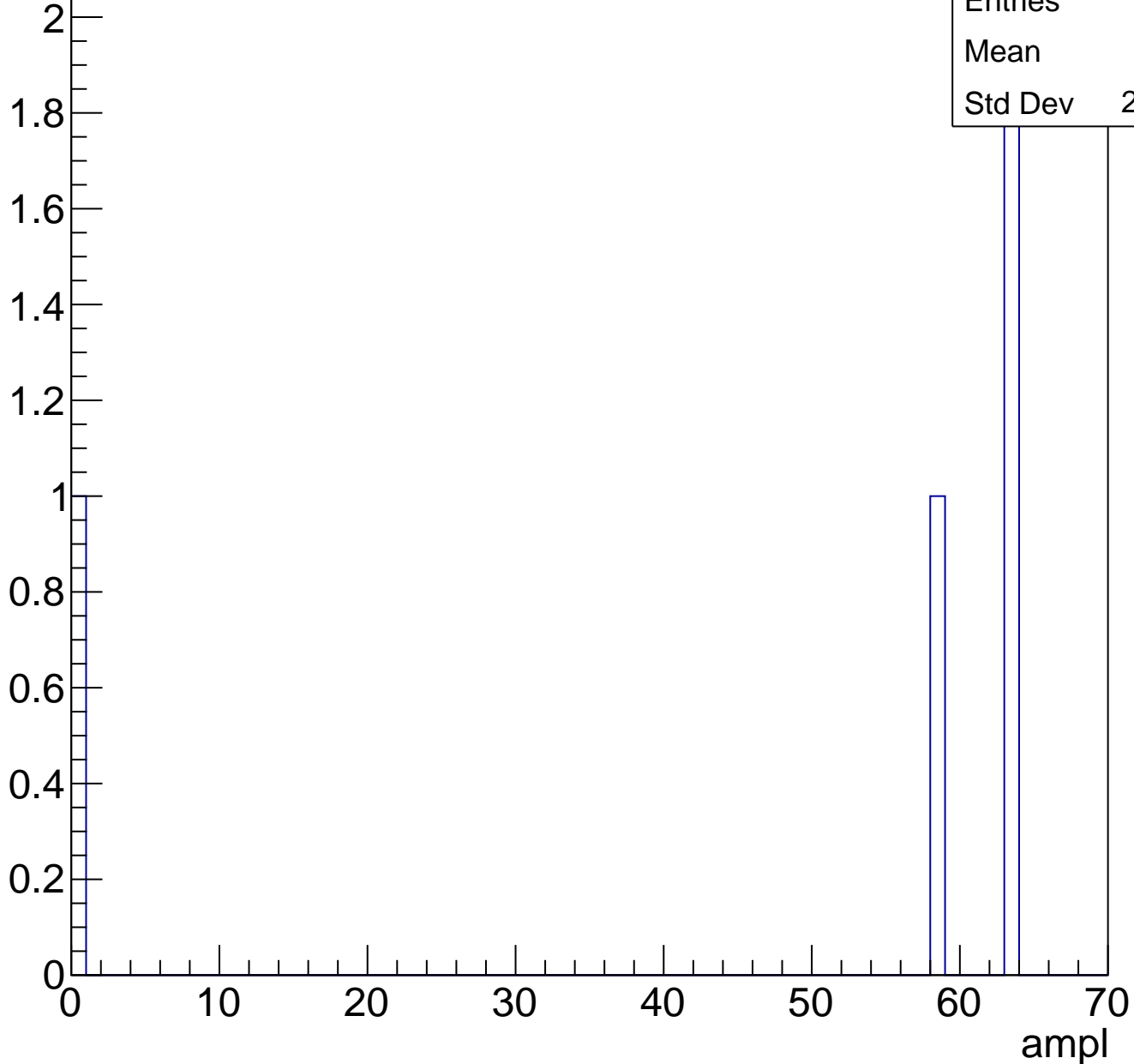
ampl



B0L101S, U21-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch44, adc0

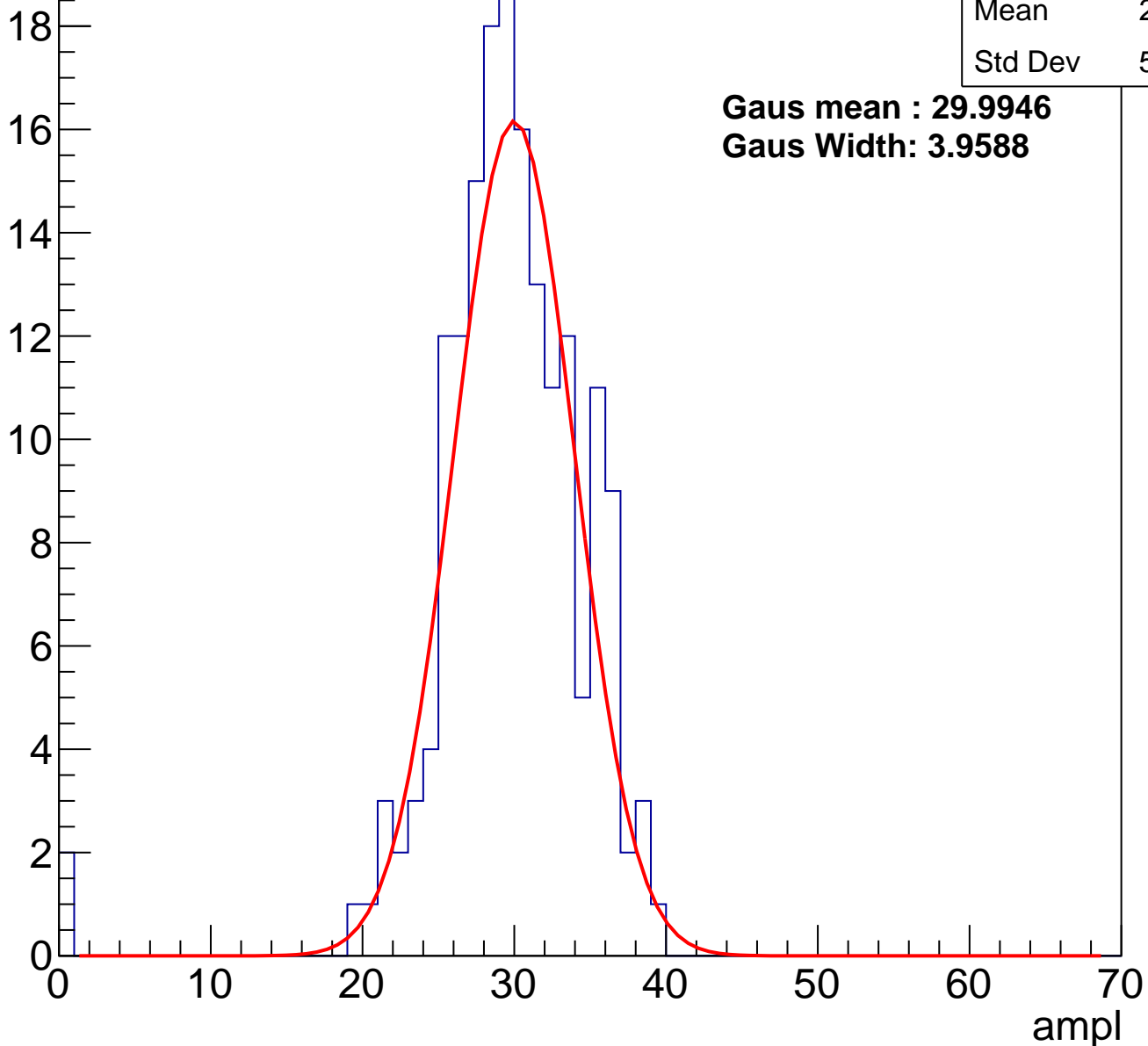
calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	29.26
Std Dev	5.082

Gaus mean : 29.9946

Gaus Width: 3.9588

Entry

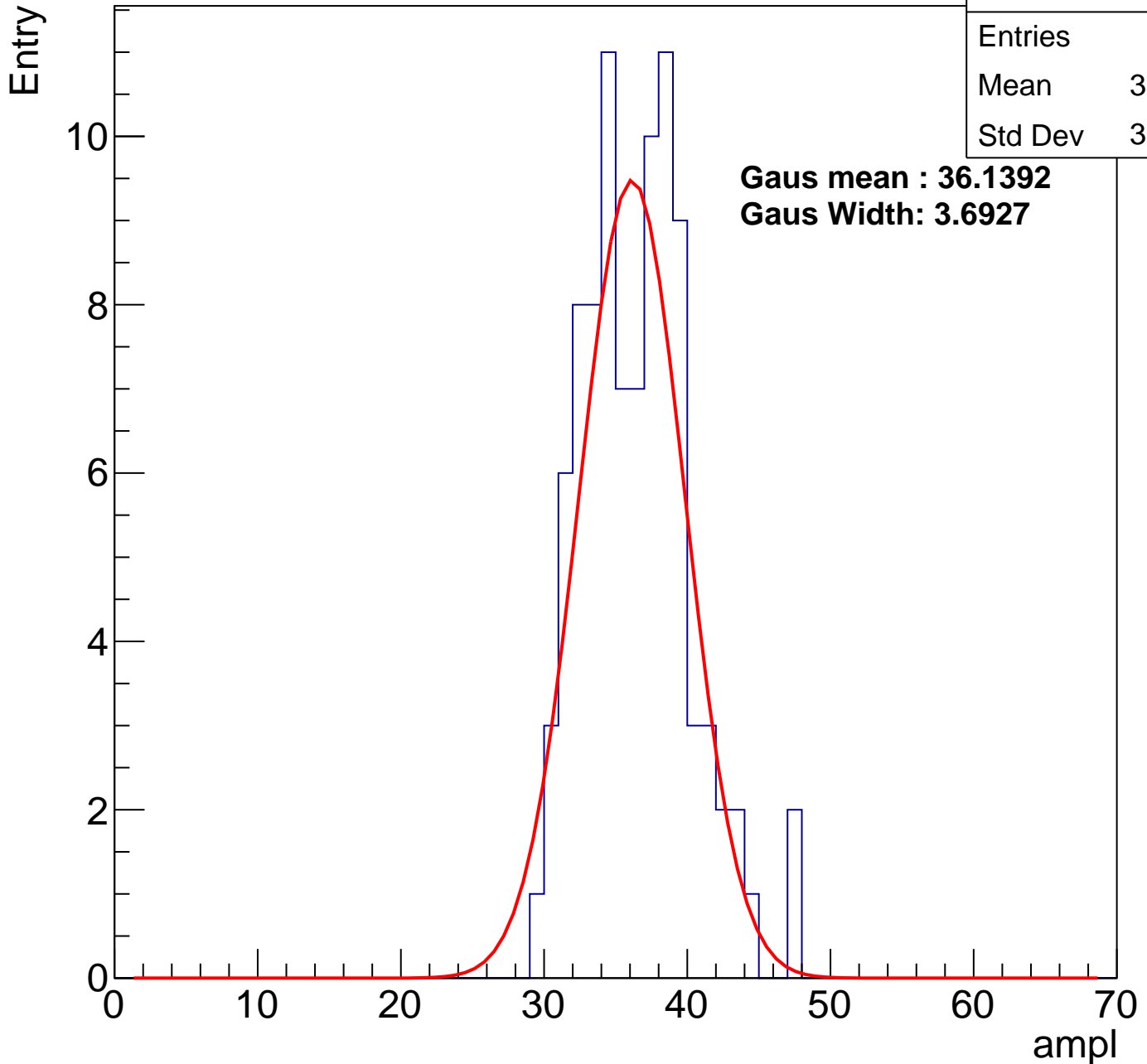


B0L101S, U21-ch44, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	36.02
Std Dev	3.707

Gaus mean : 36.1392
Gaus Width: 3.6927



B0L101S, U21-ch44, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

125

Mean

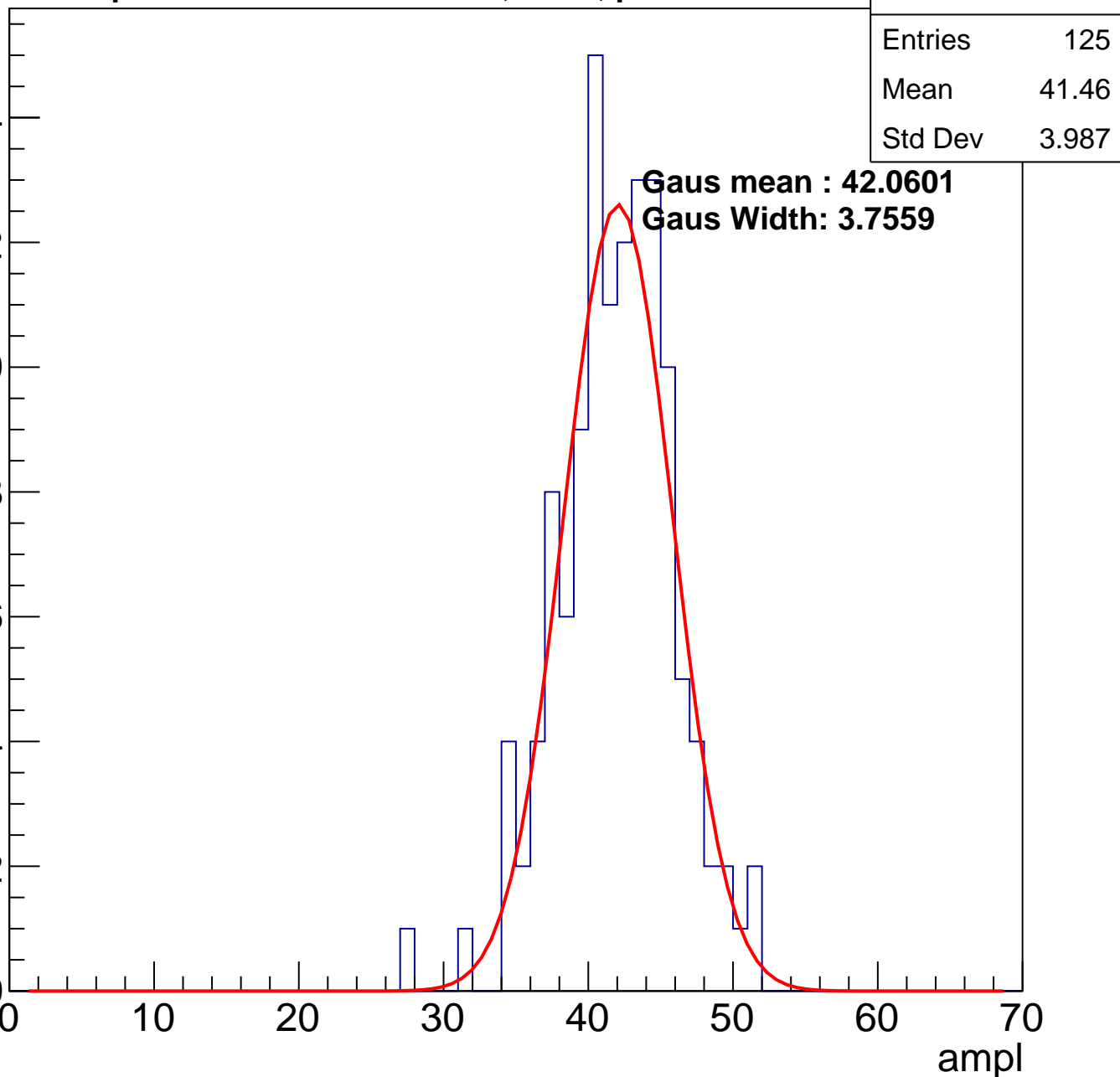
41.46

Std Dev

3.987

Gaus mean : 42.0601

Gaus Width: 3.7559

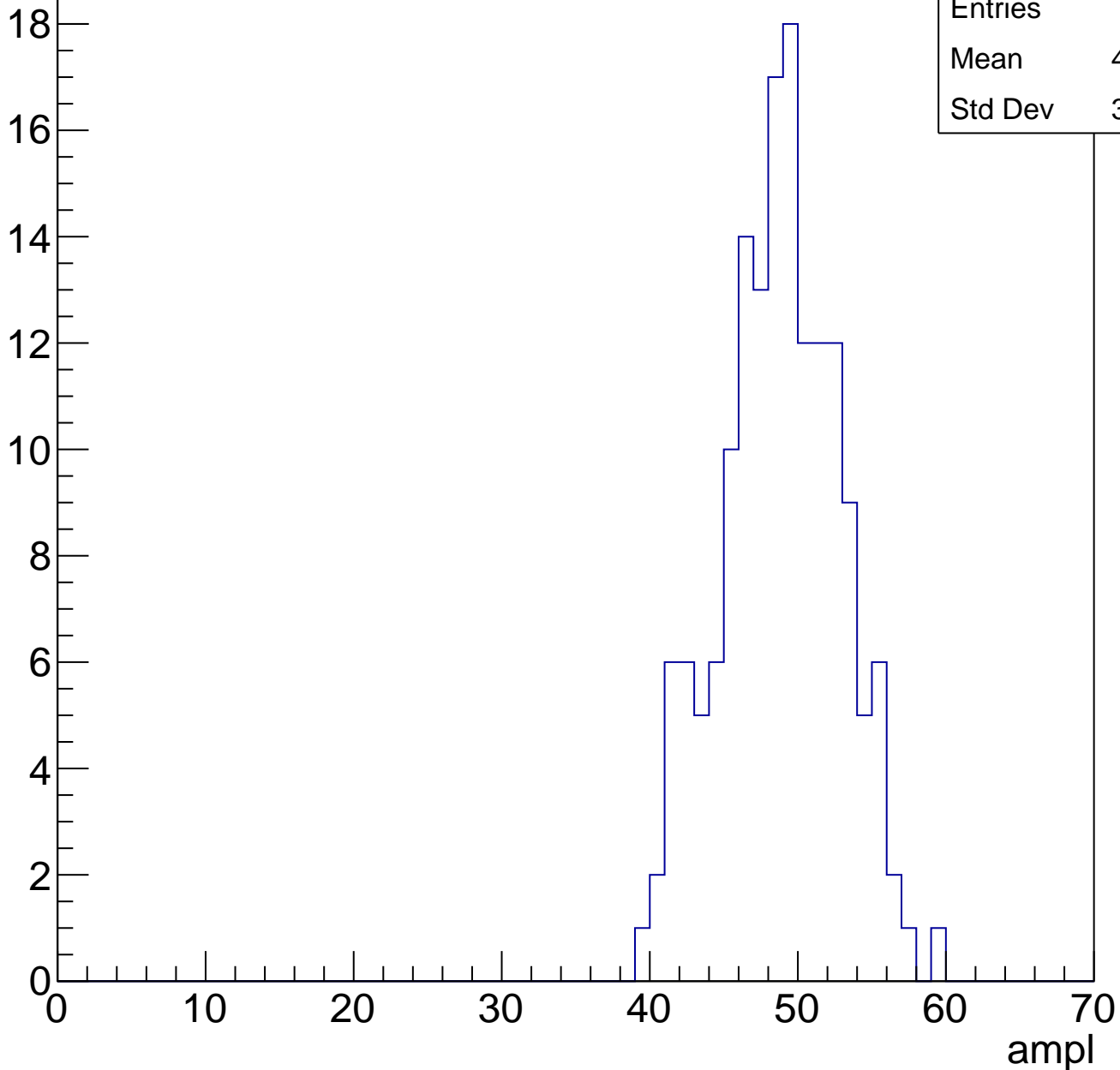


B0L101S, U21-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	48.35
Std Dev	3.957

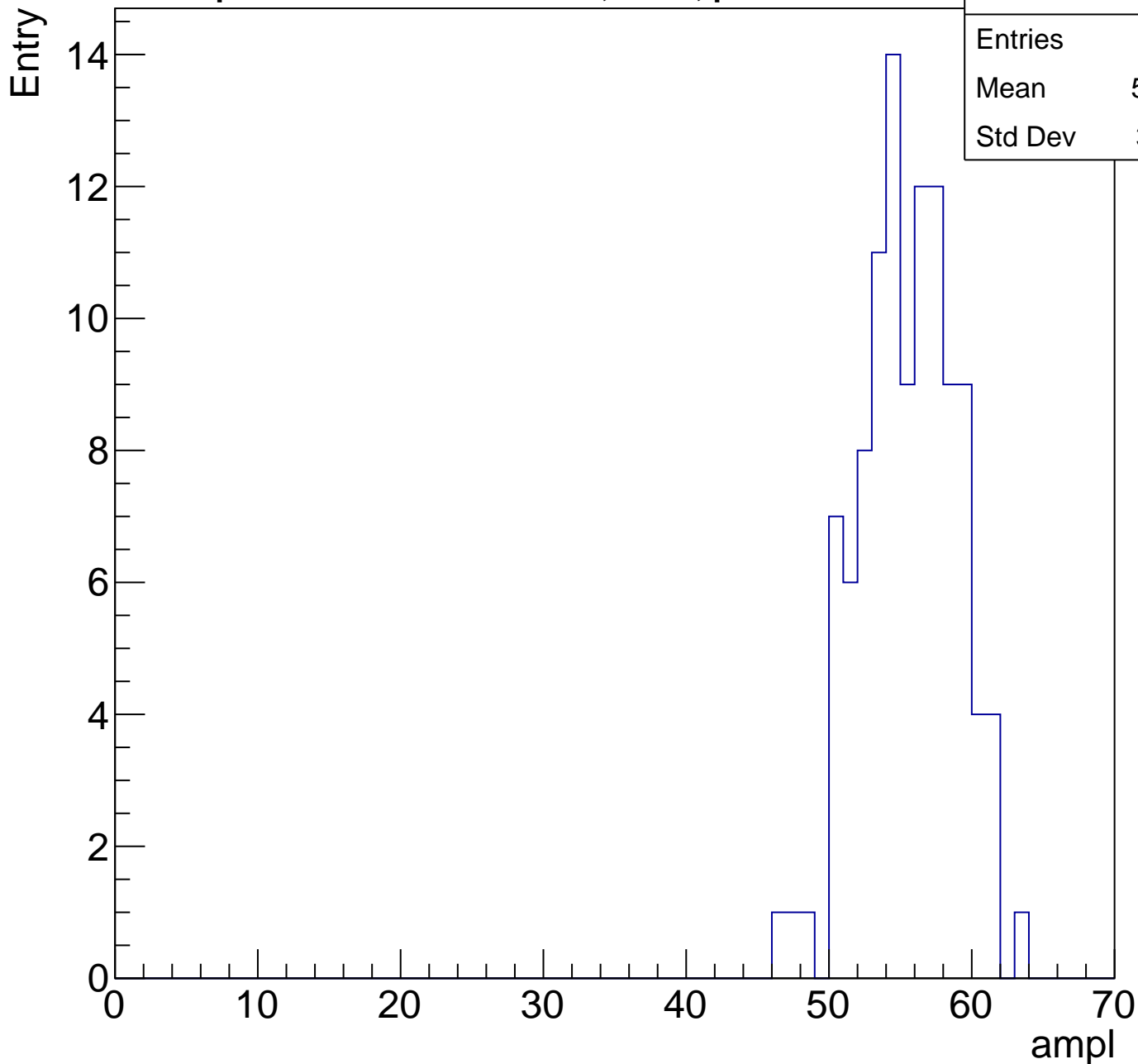
Entry



B0L101S, U21-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.07
Std Dev	3.301



B0L101S, U21-ch44, adc5

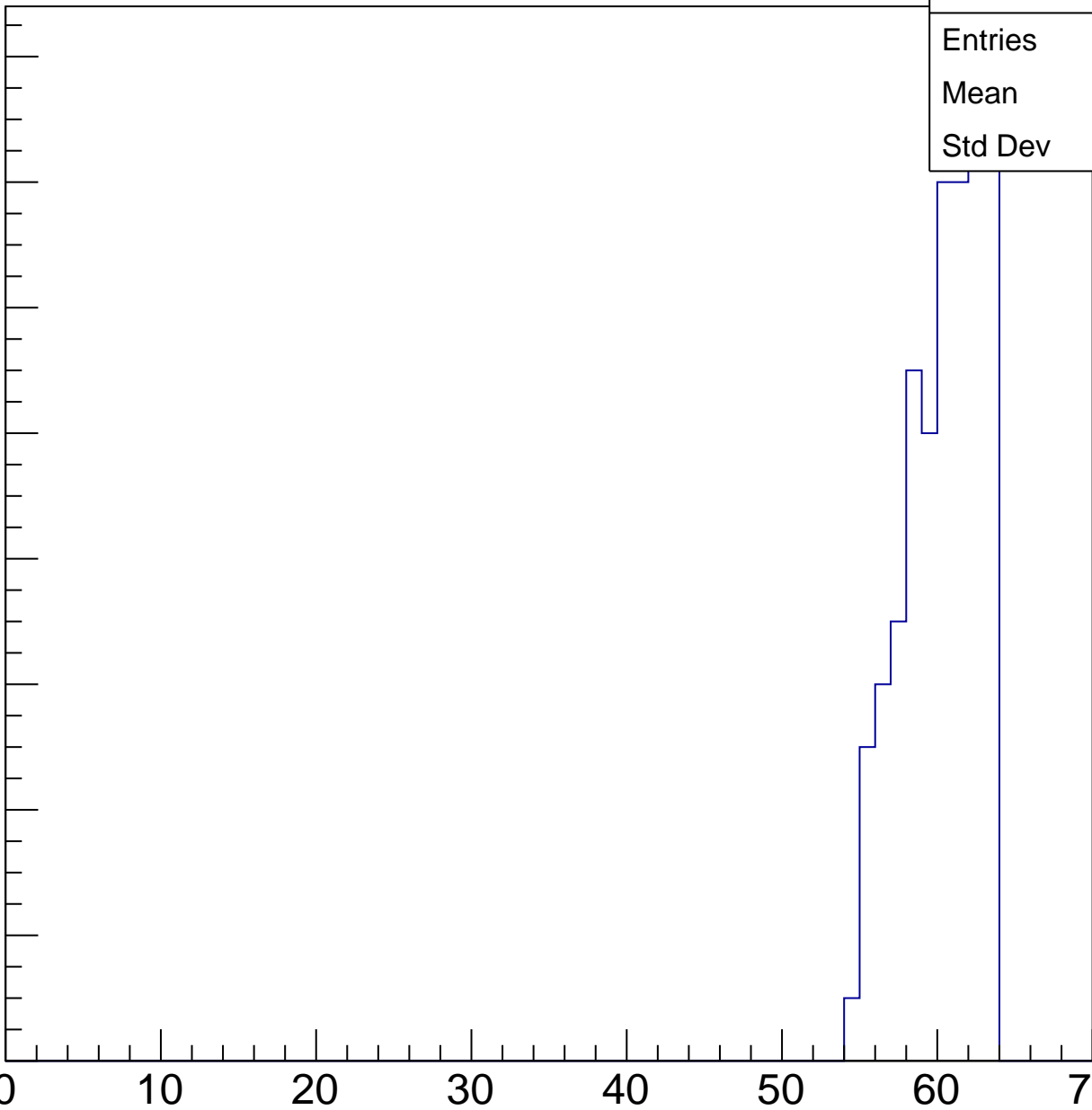
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.86
Std Dev	2.437

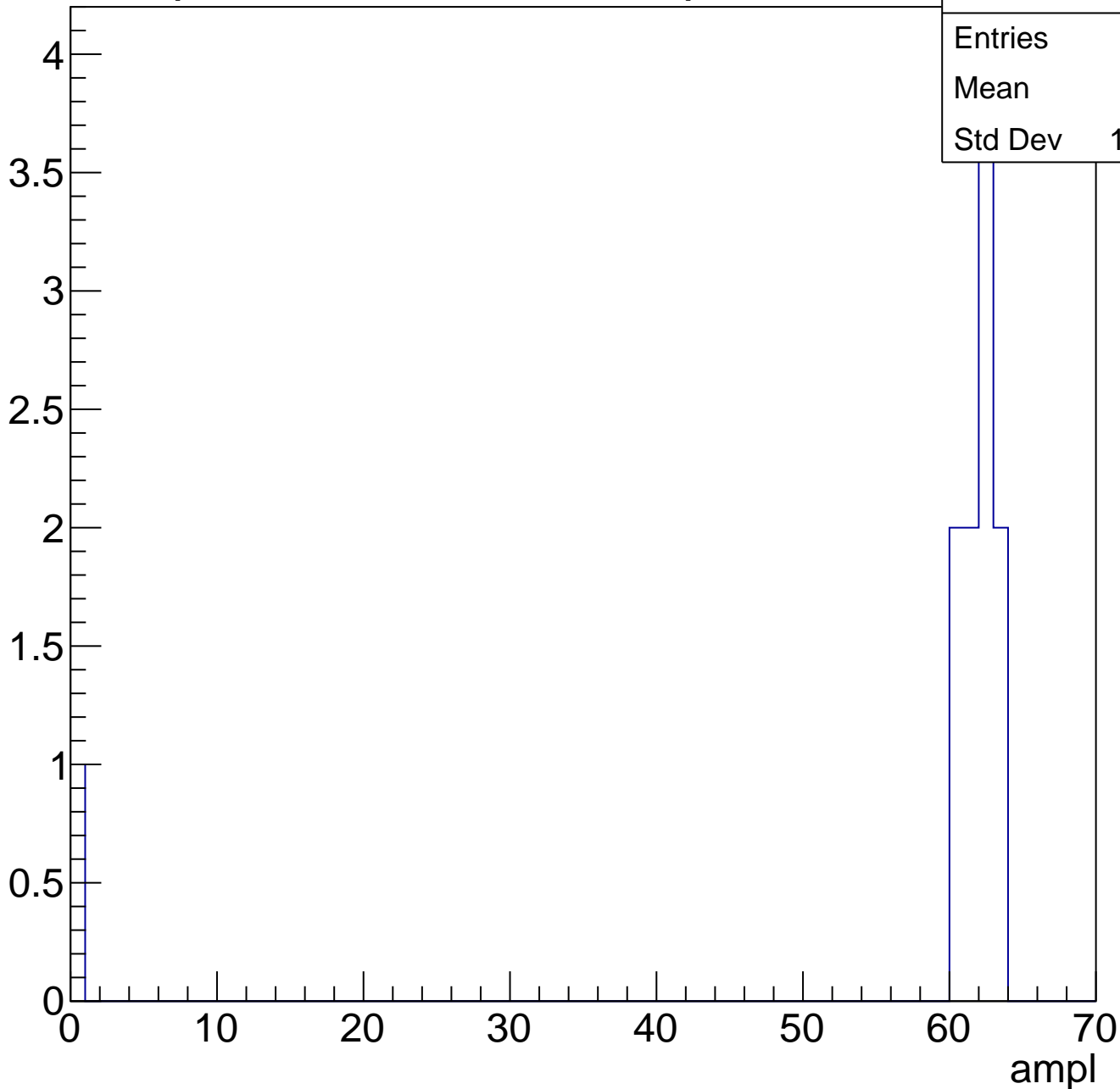
ampl



B0L101S, U21-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

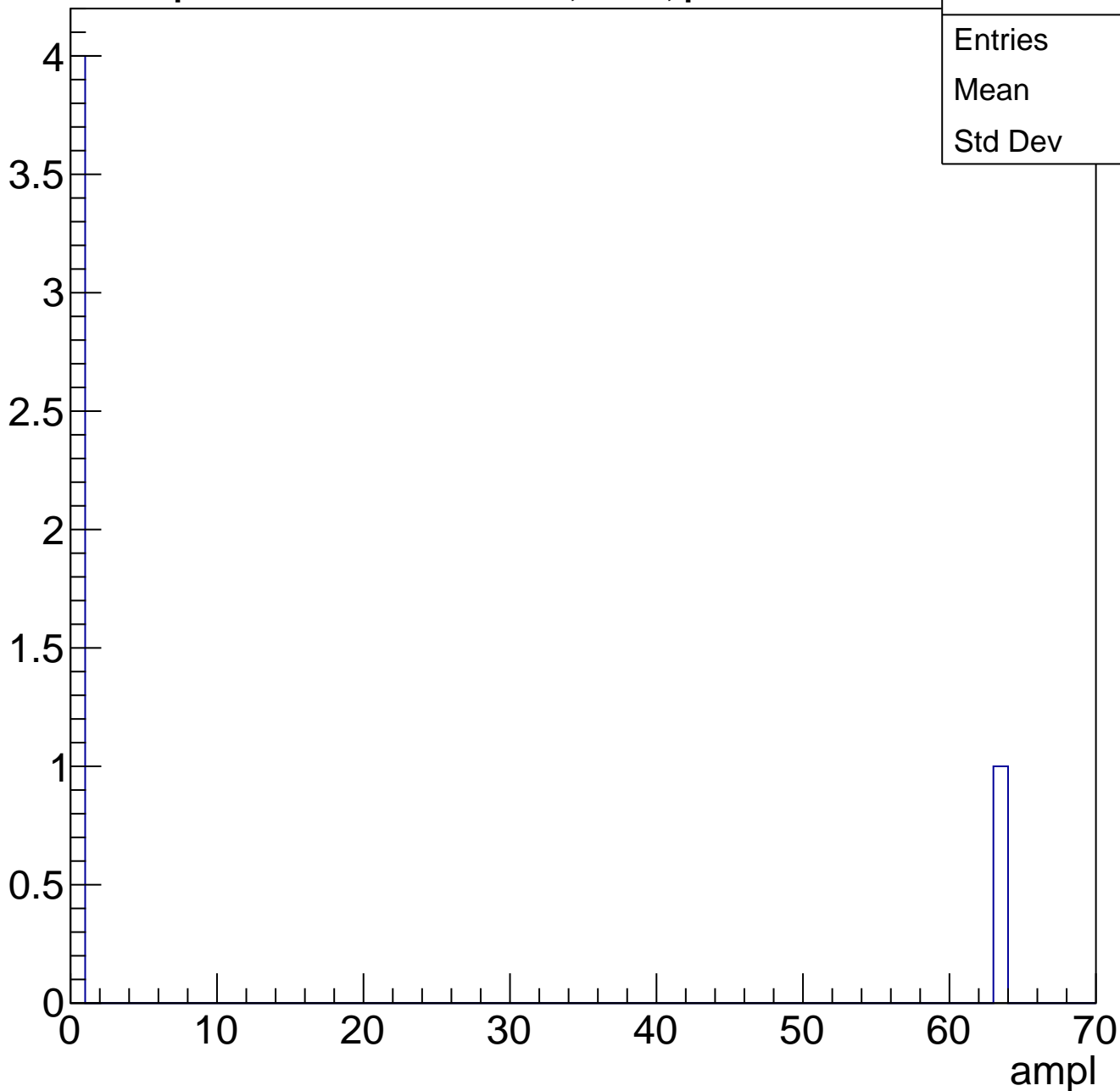
Entry



B0L101S, U21-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch45, adc0

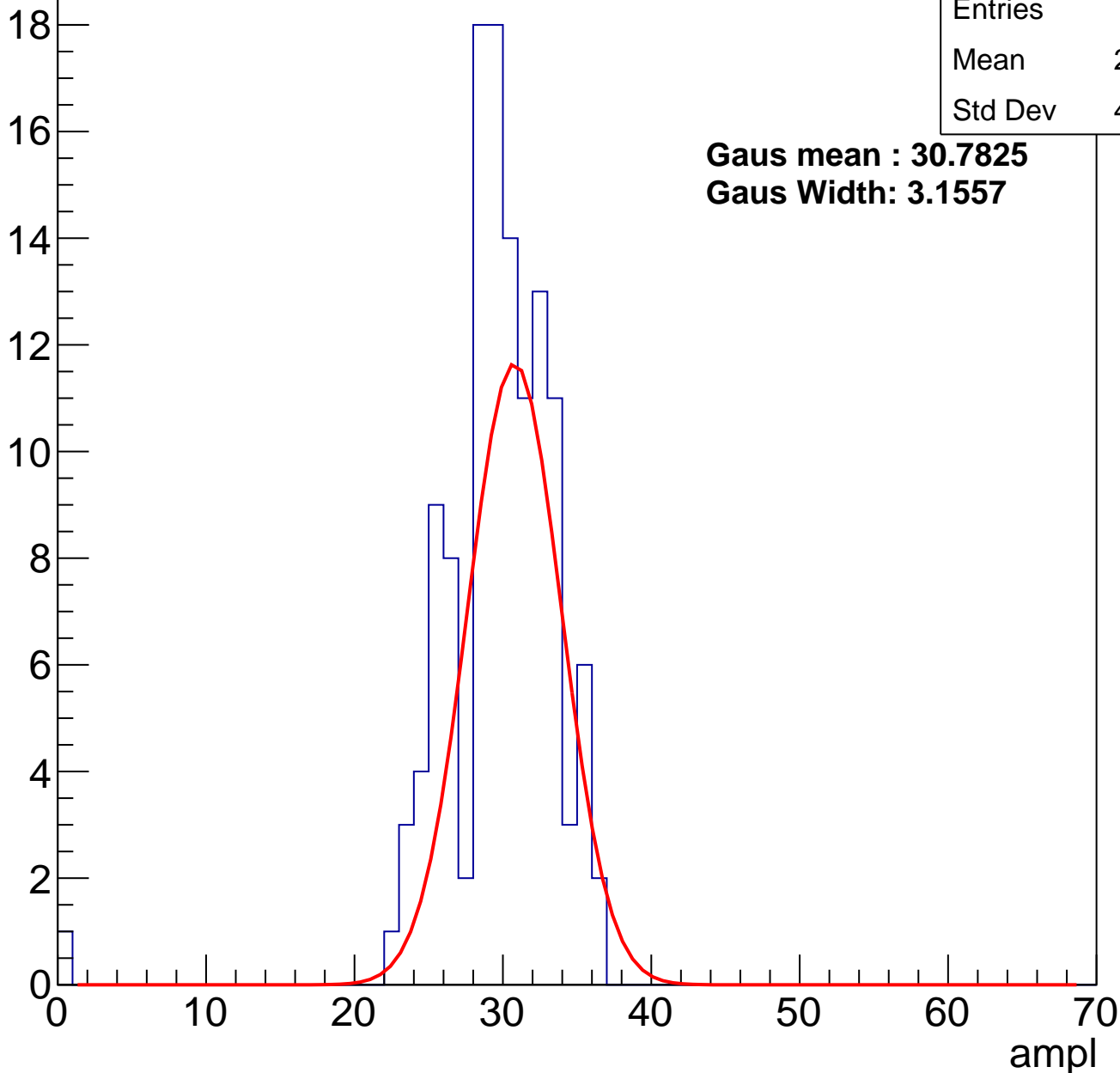
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	29.23
Std Dev	4.089

Gaus mean : 30.7825

Gaus Width: 3.1557

Entry



B0L101S, U21-ch45, adc1

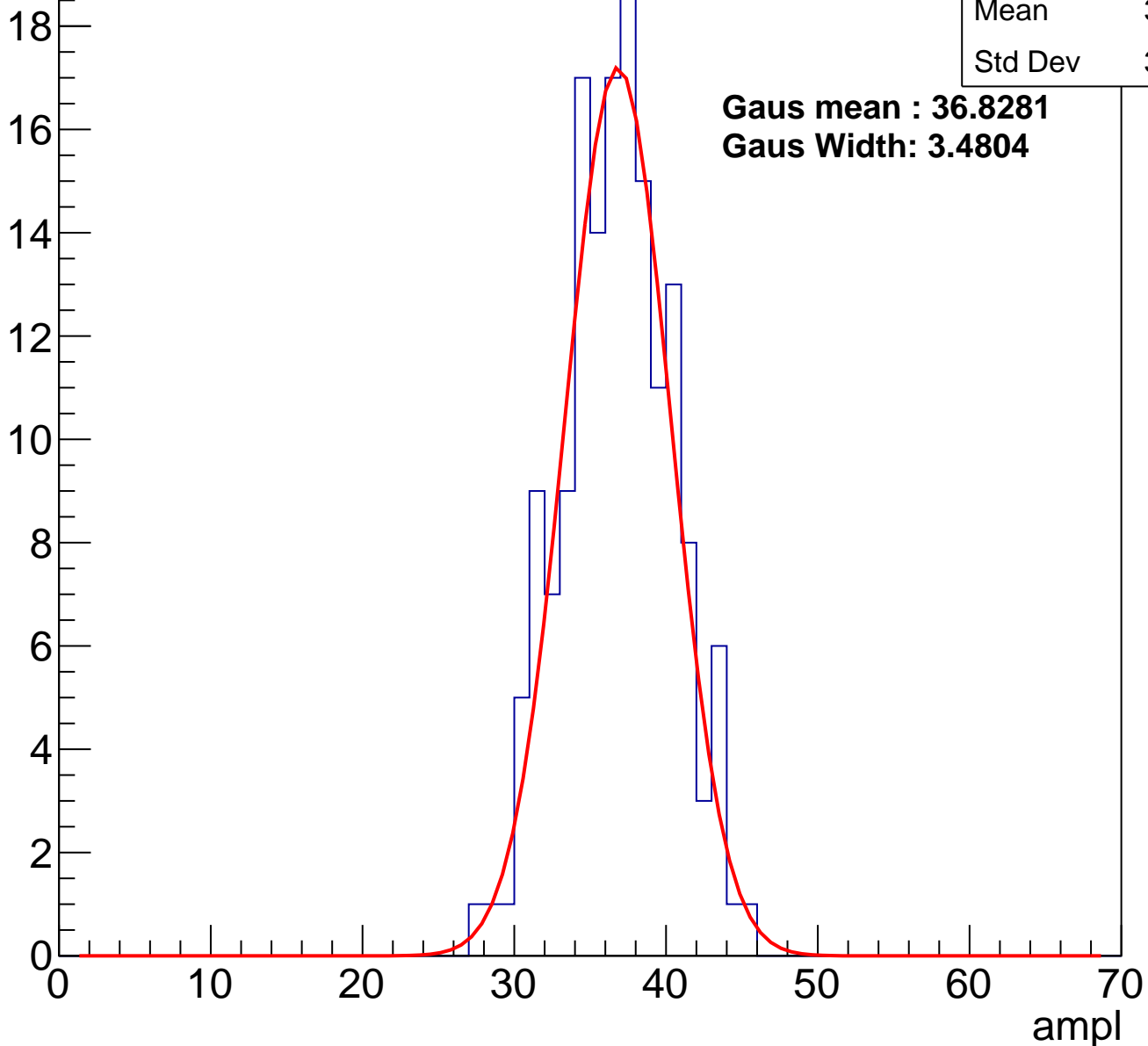
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	36.31
Std Dev	3.551

Gaus mean : 36.8281

Gaus Width: 3.4804

Entry



B0L101S, U21-ch45, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

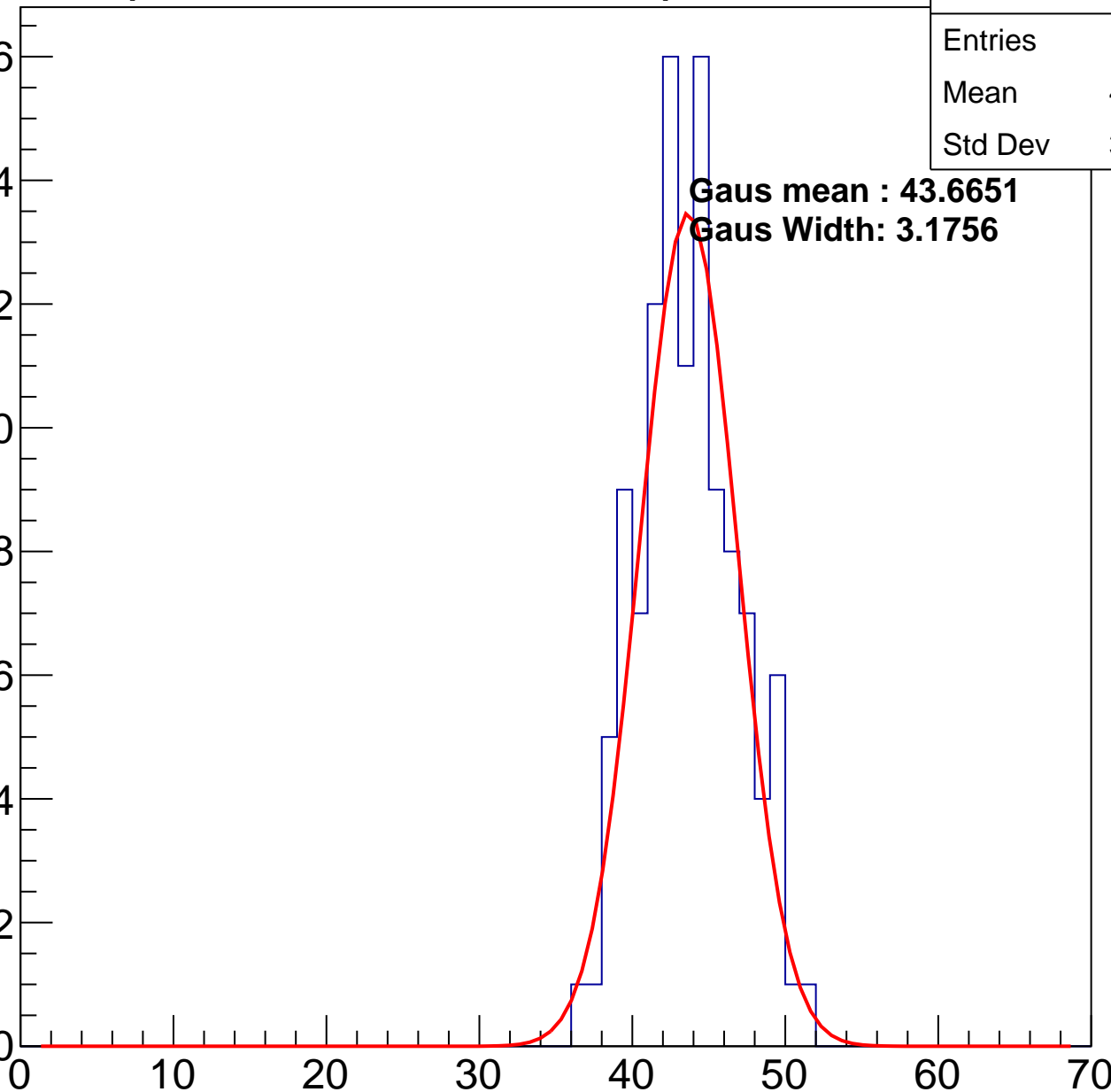
16
14
12
10
8
6
4
2
0

Entries	114
Mean	43.19
Std Dev	3.165

Gaus mean : 43.6651

Gaus Width: 3.1756

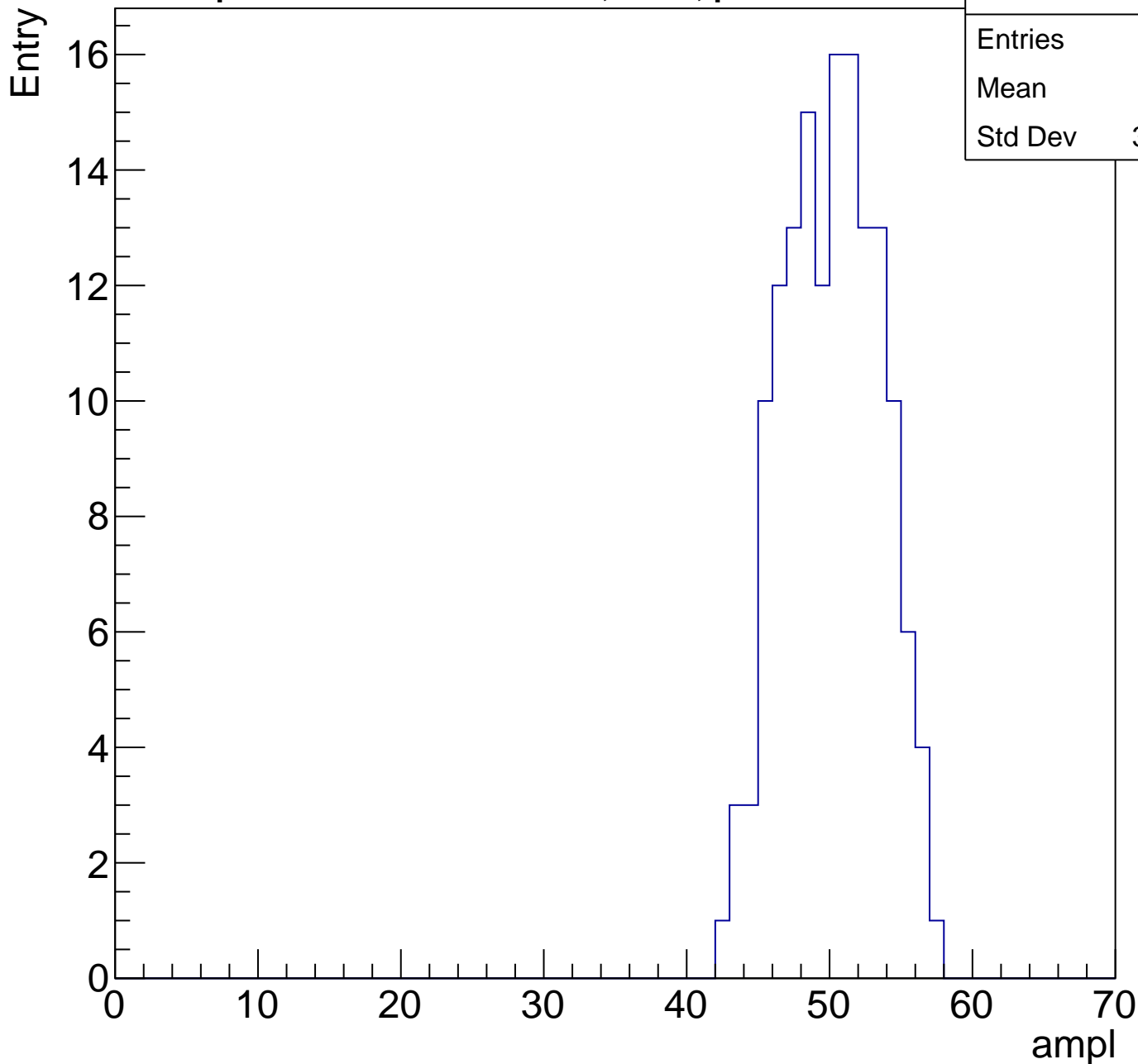
ampl



B0L101S, U21-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	49.7
Std Dev	3.313



B0L101S, U21-ch45, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	55.84
Std Dev	2.974

Entry

12

10

8

6

4

2

0

0

10

20

30

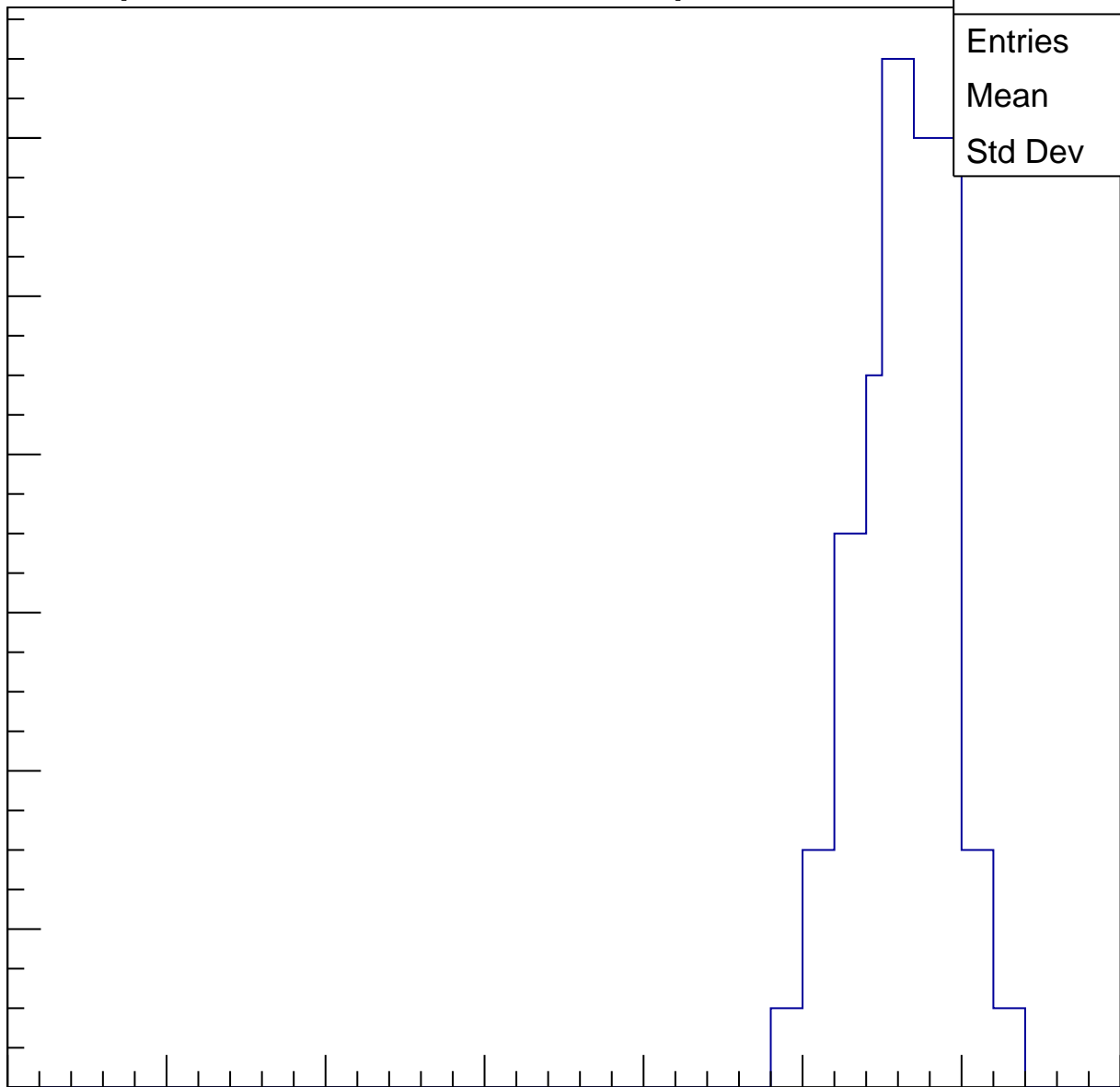
40

50

60

70

ampl



B0L101S, U21-ch45, adc5

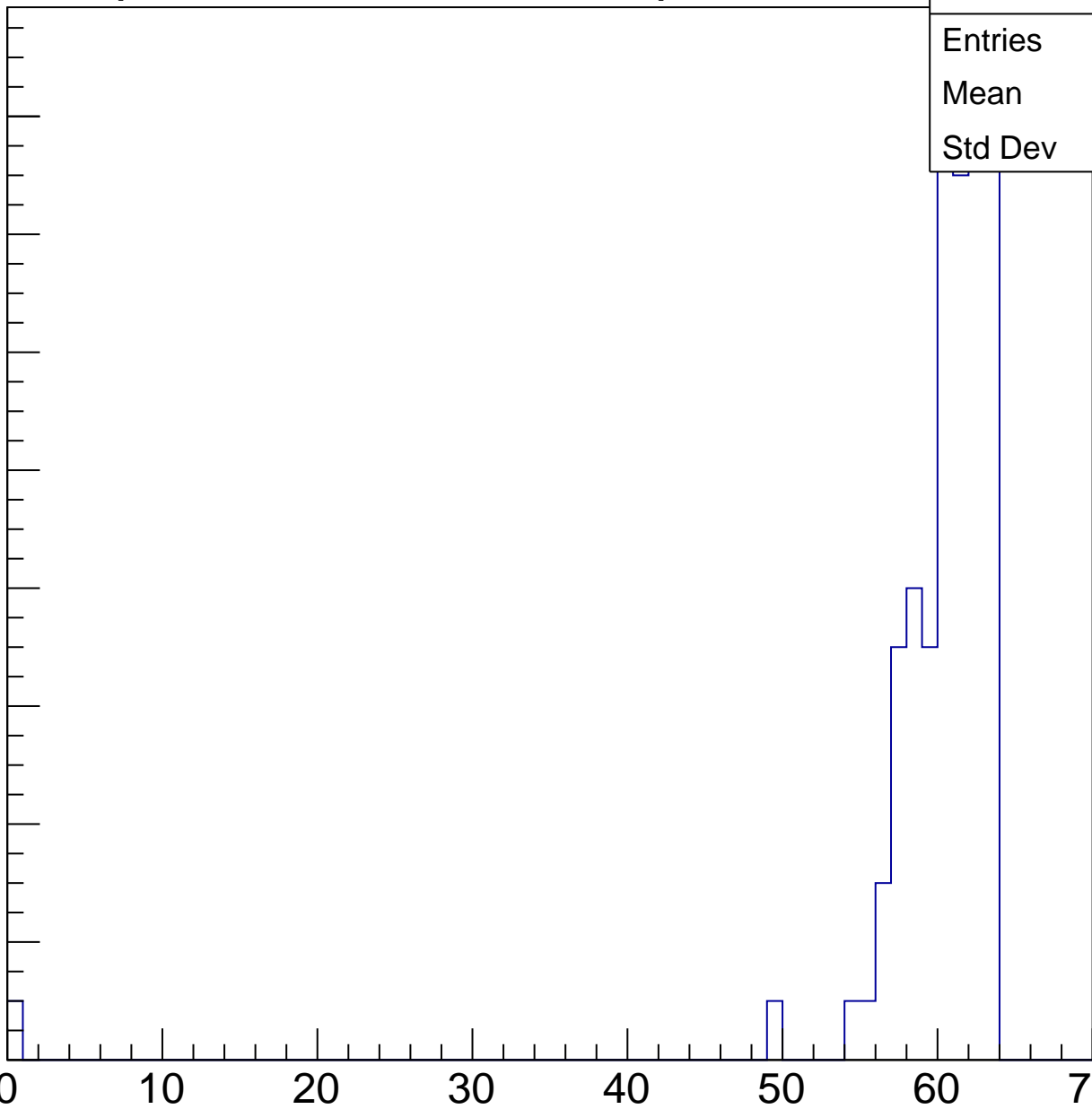
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.56
Std Dev	6.673

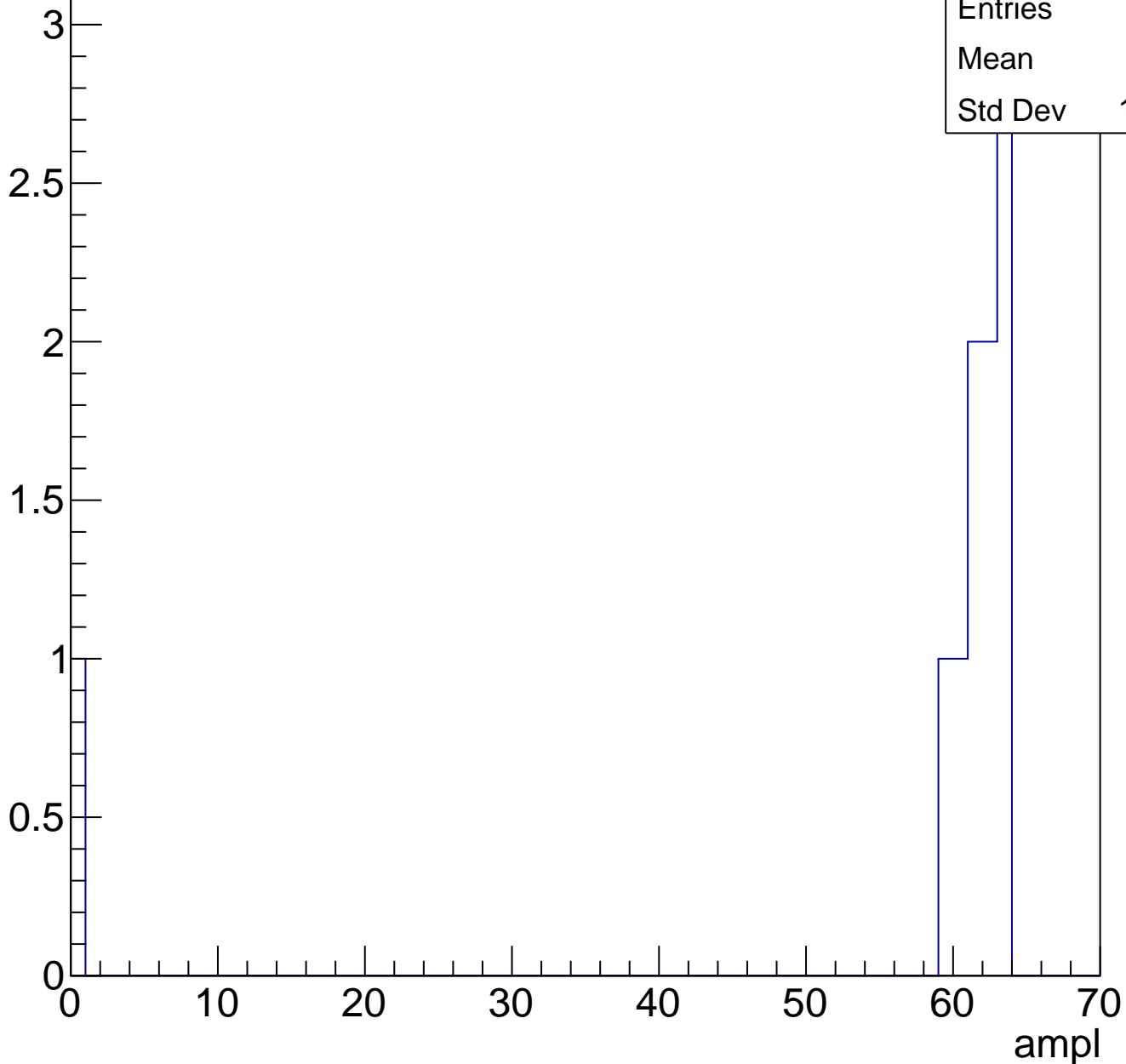
ampl



B0L101S, U21-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch46, adc0

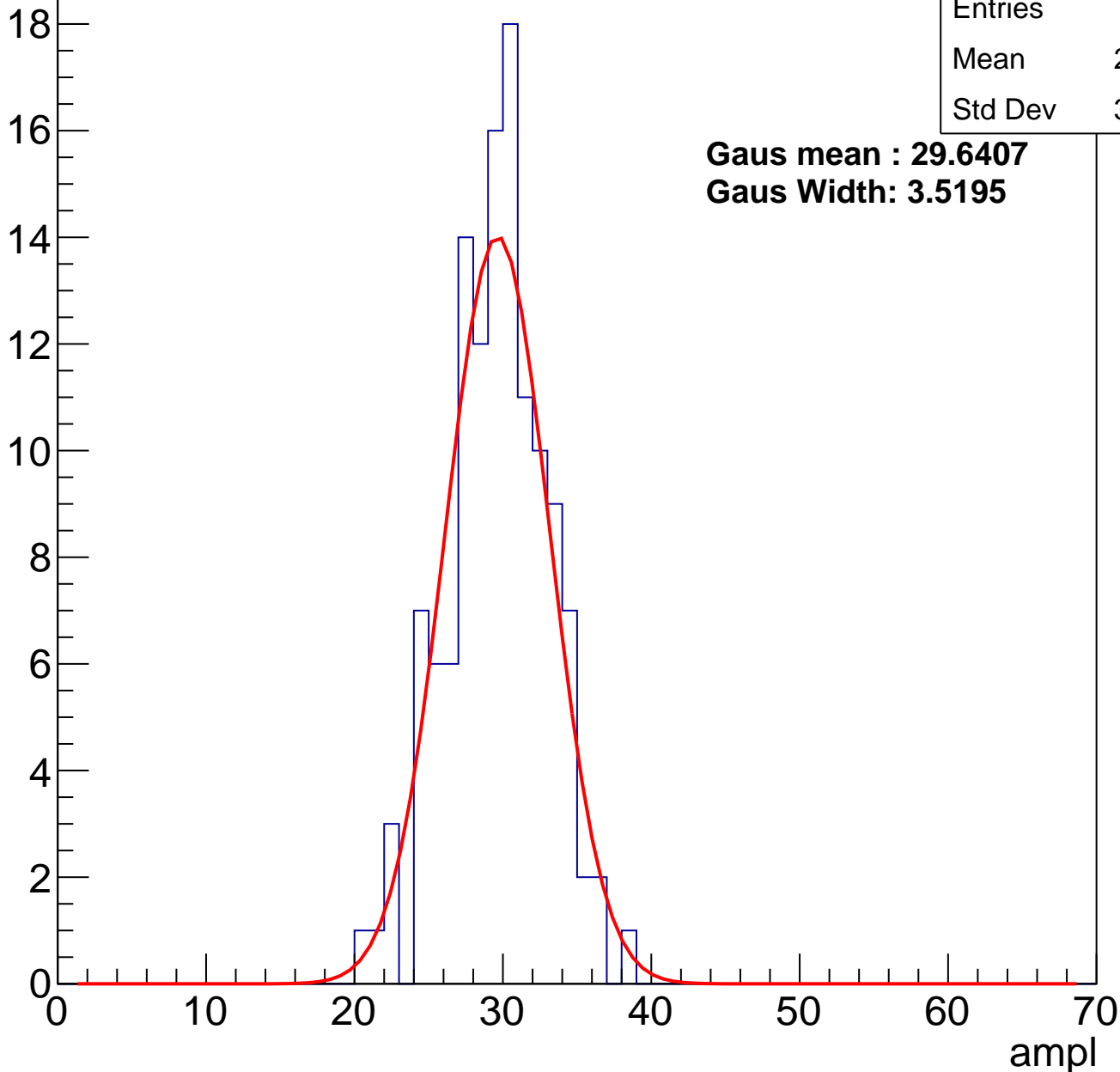
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.17
Std Dev	3.337

Gaus mean : 29.6407

Gaus Width: 3.5195

Entry



B0L101S, U21-ch46, adc1

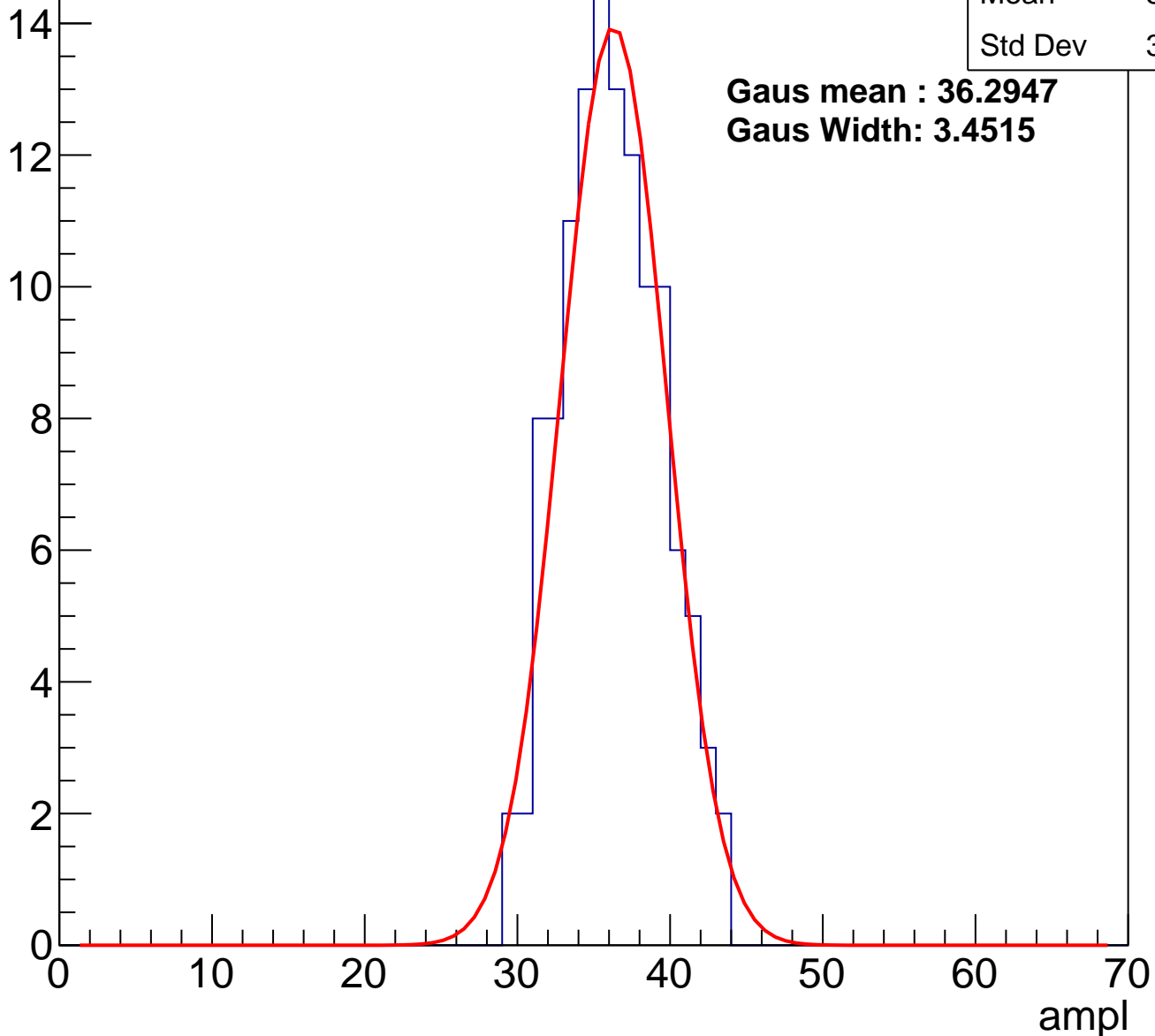
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	35.76
Std Dev	3.196

Gaus mean : 36.2947

Gaus Width: 3.4515

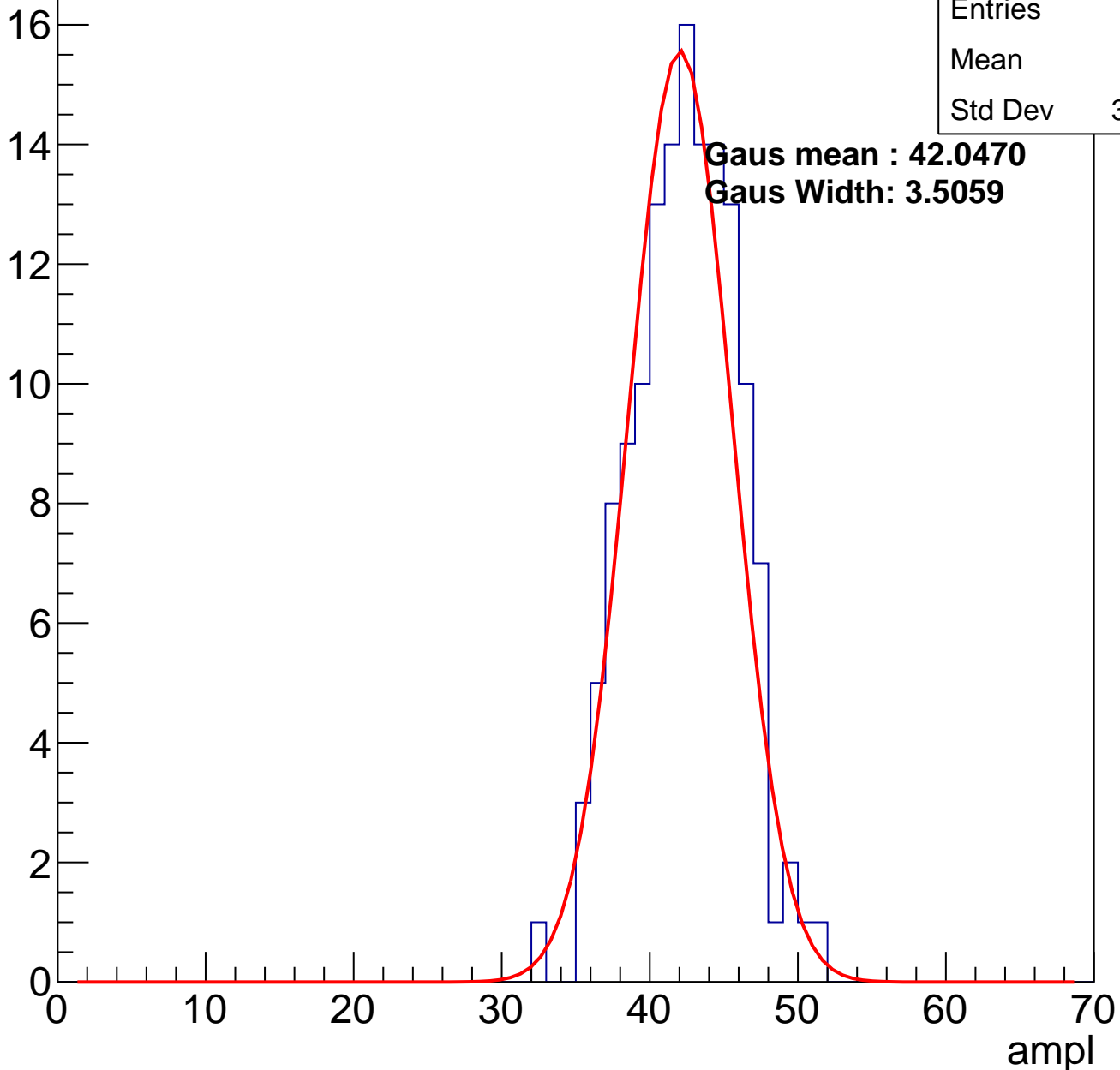
Entry



B0L101S, U21-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

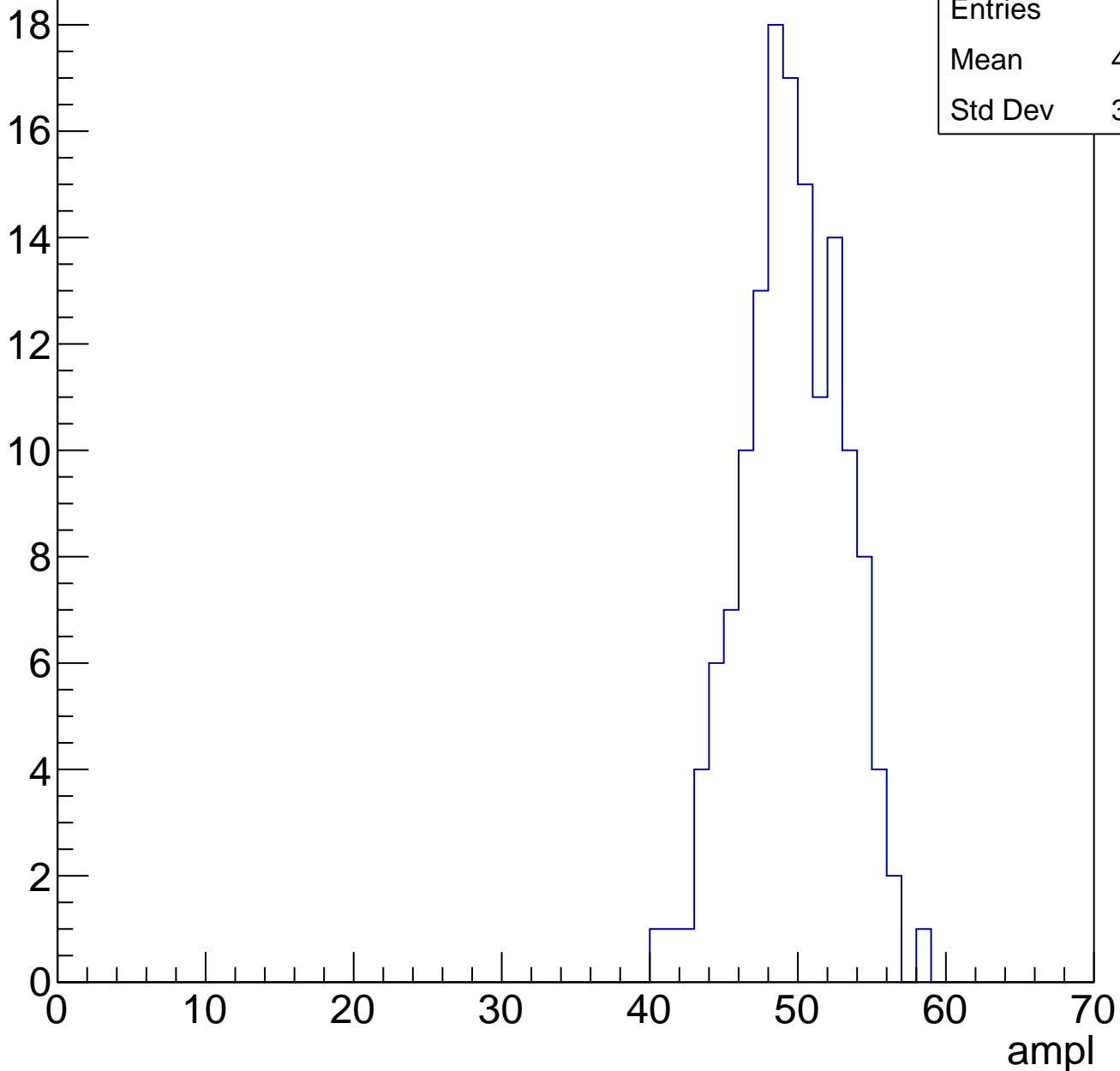
Entry



B0L101S, U21-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

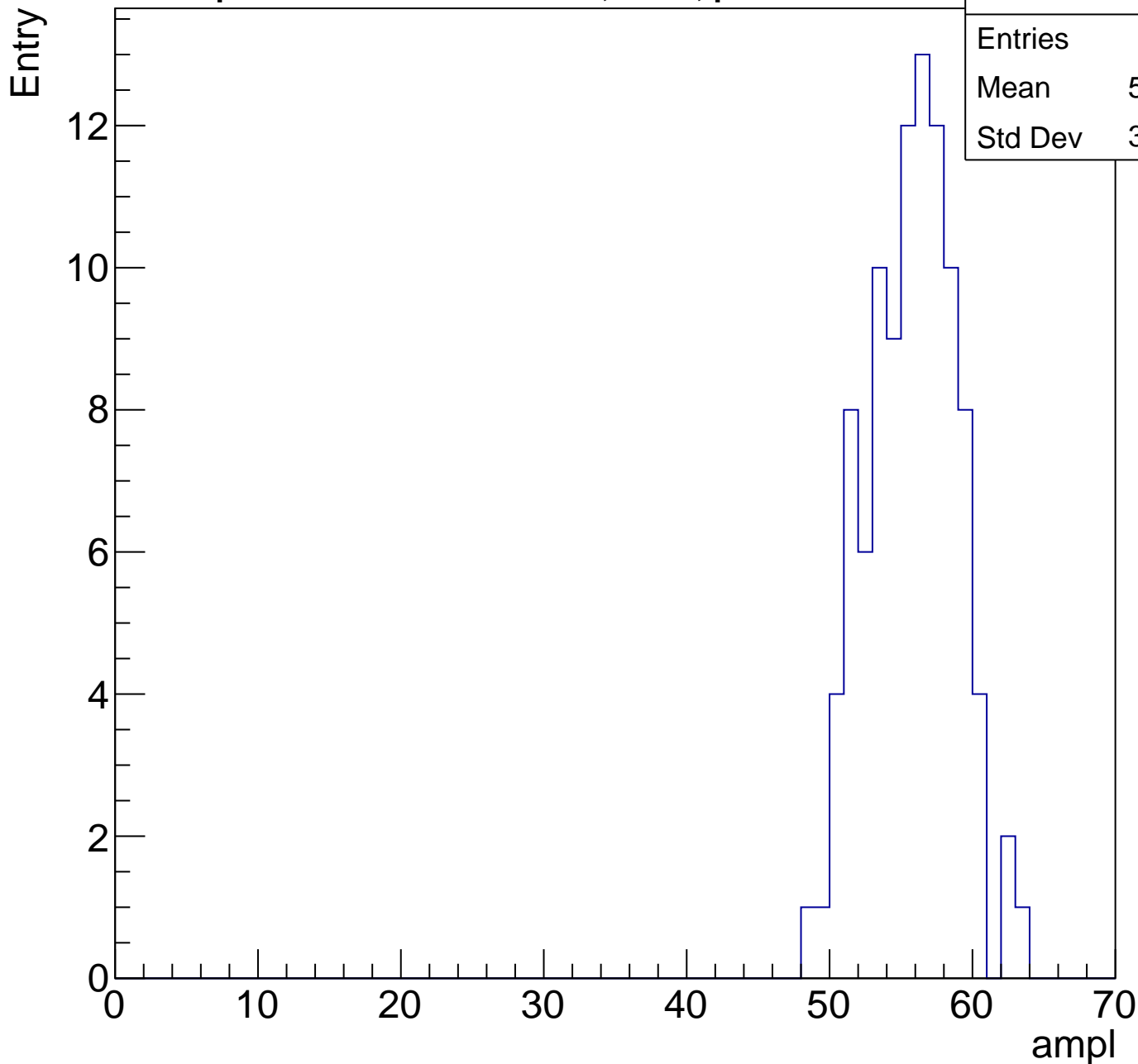
Entry



B0L101S, U21-ch46, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	55.29
Std Dev	3.065



B0L101S, U21-ch46, adc5

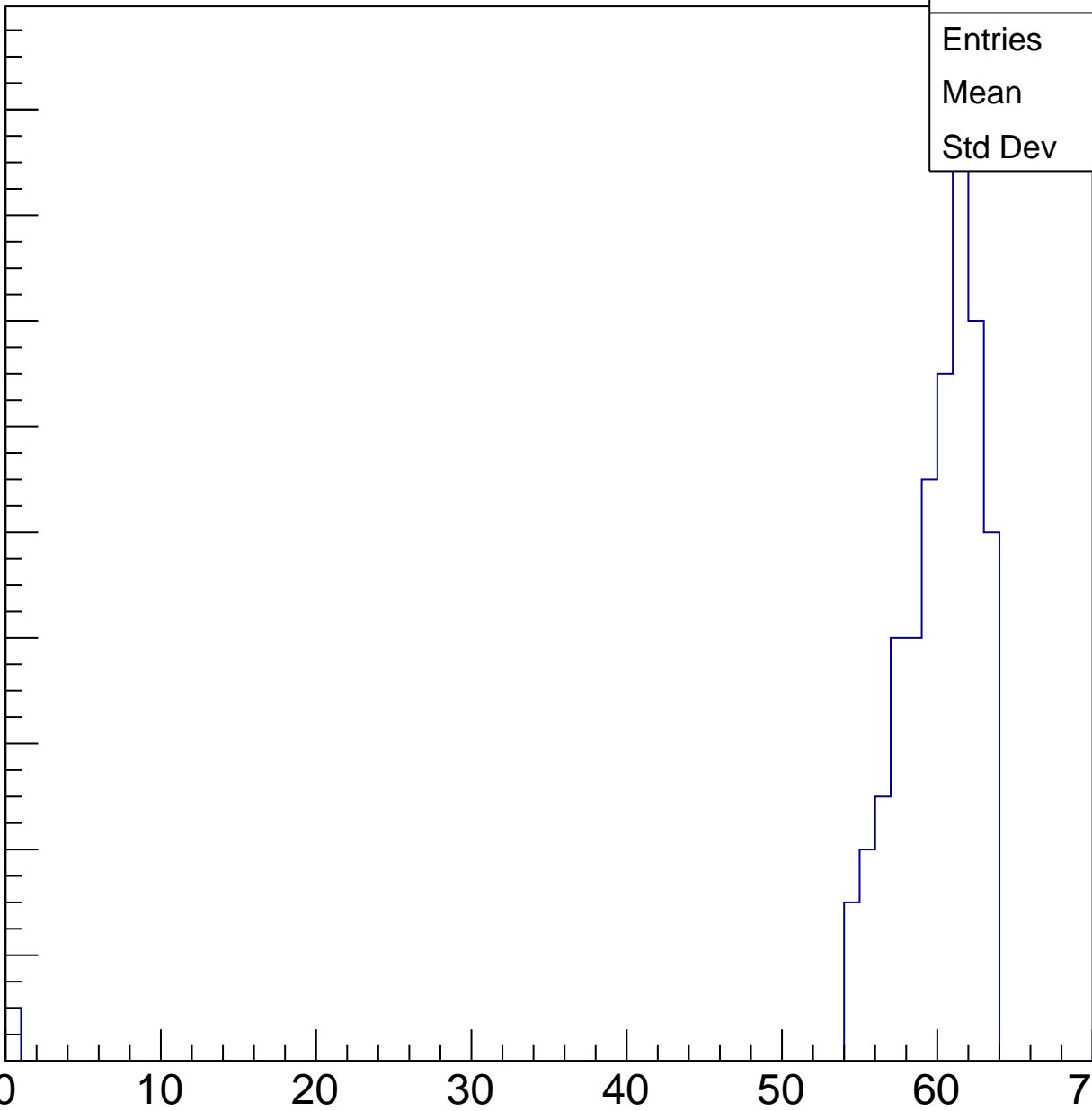
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.04
Std Dev	6.519

ampl

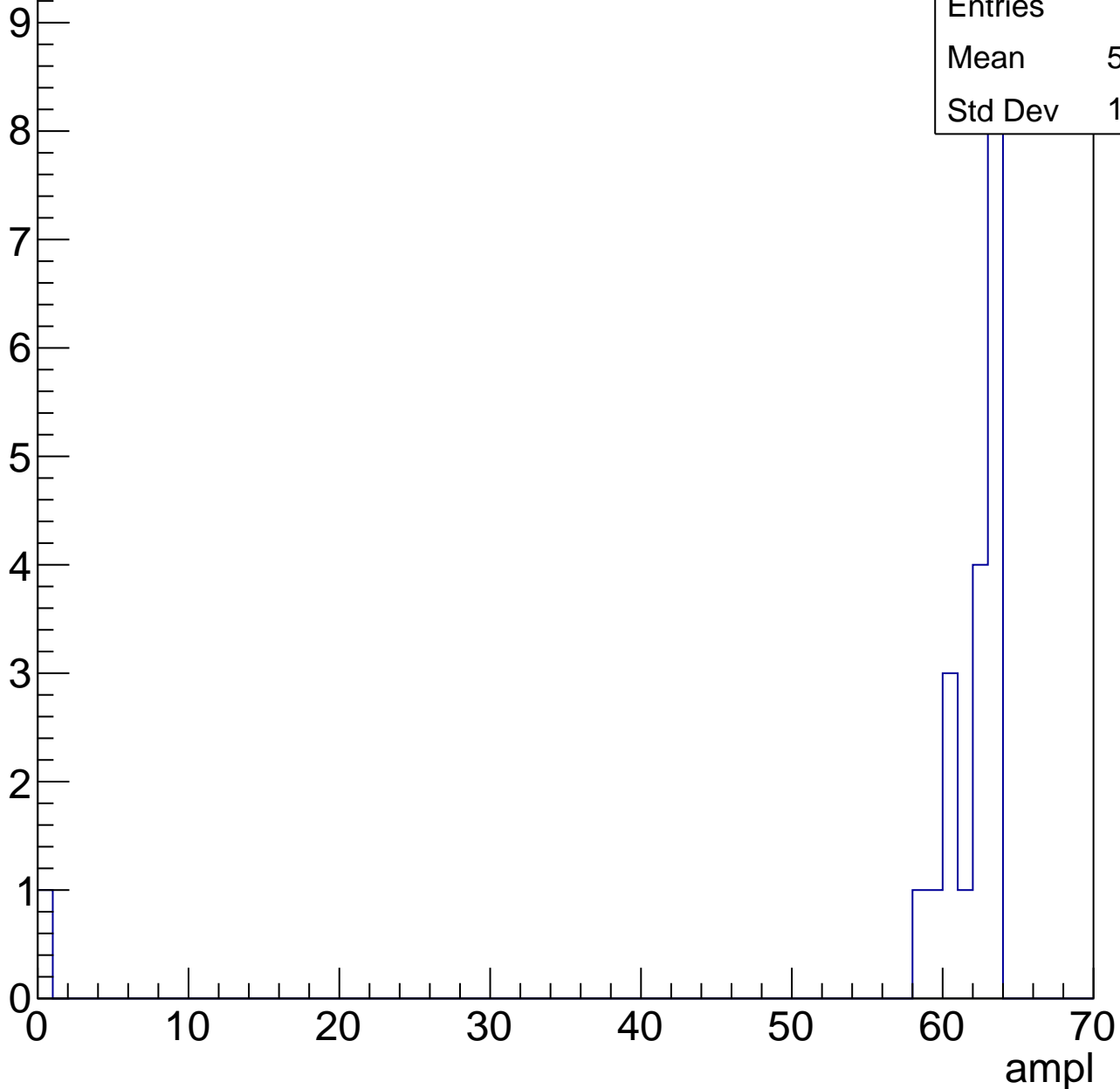


B0L101S, U21-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	58.65
Std Dev	13.54



B0L101S, U21-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch47, adc0

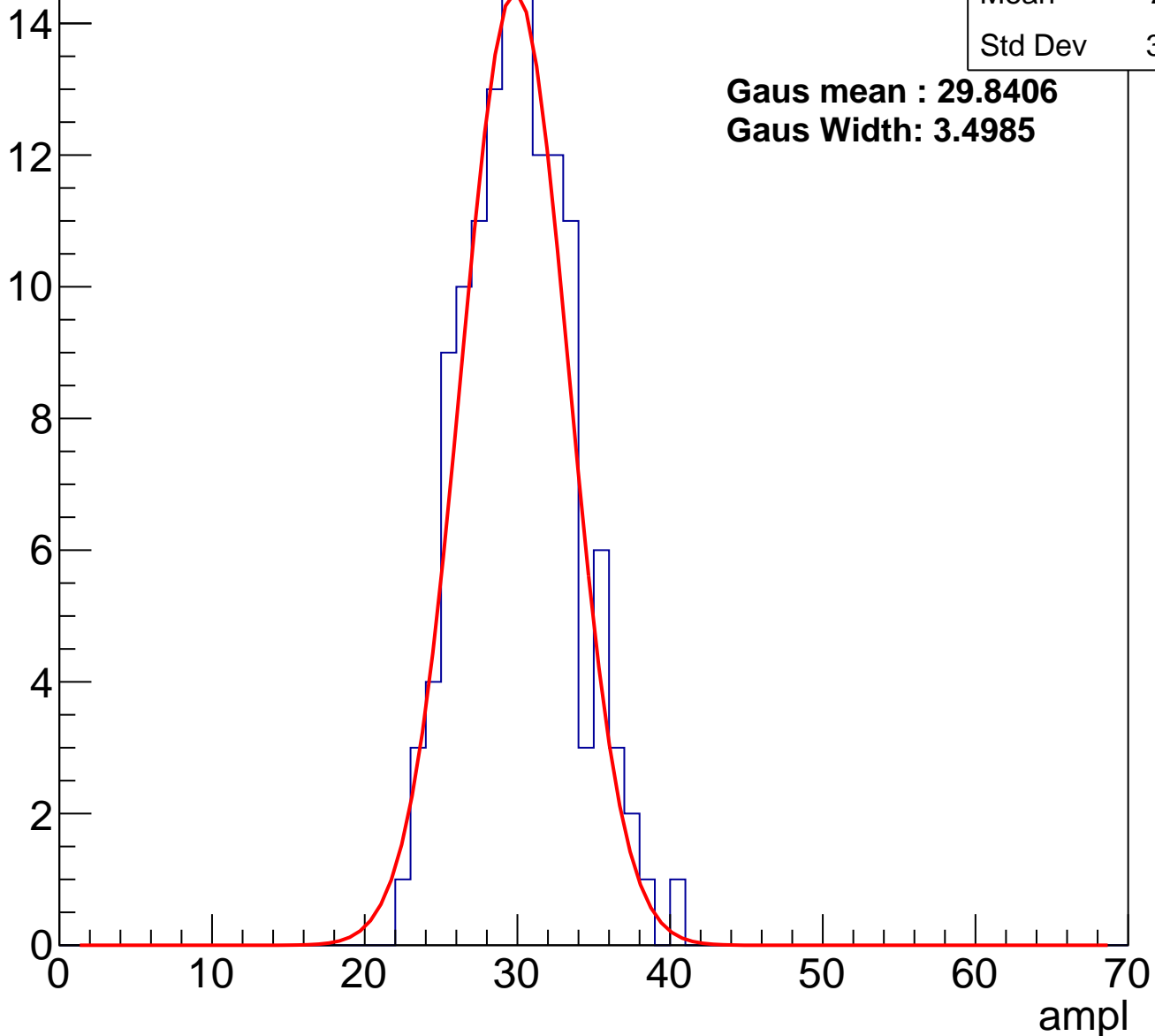
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	29.61
Std Dev	3.496

Gaus mean : 29.8406

Gaus Width: 3.4985

Entry



B0L101S, U21-ch47, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	36.41
Std Dev	3.391

Gaus mean : 37.0191

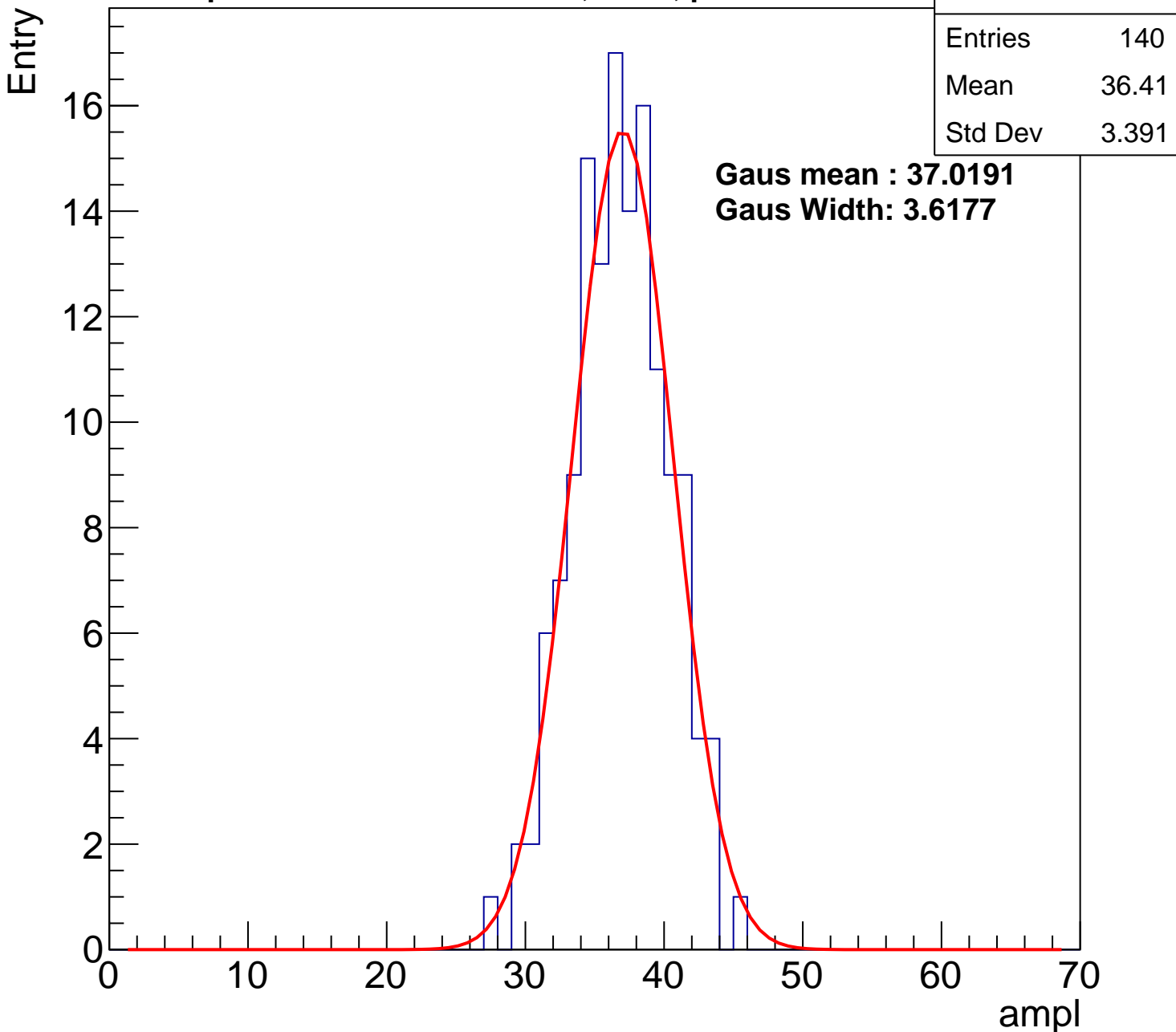
Gaus Width: 3.6177

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch47, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	43.28
Std Dev	3.605

Gaus mean : 43.5948

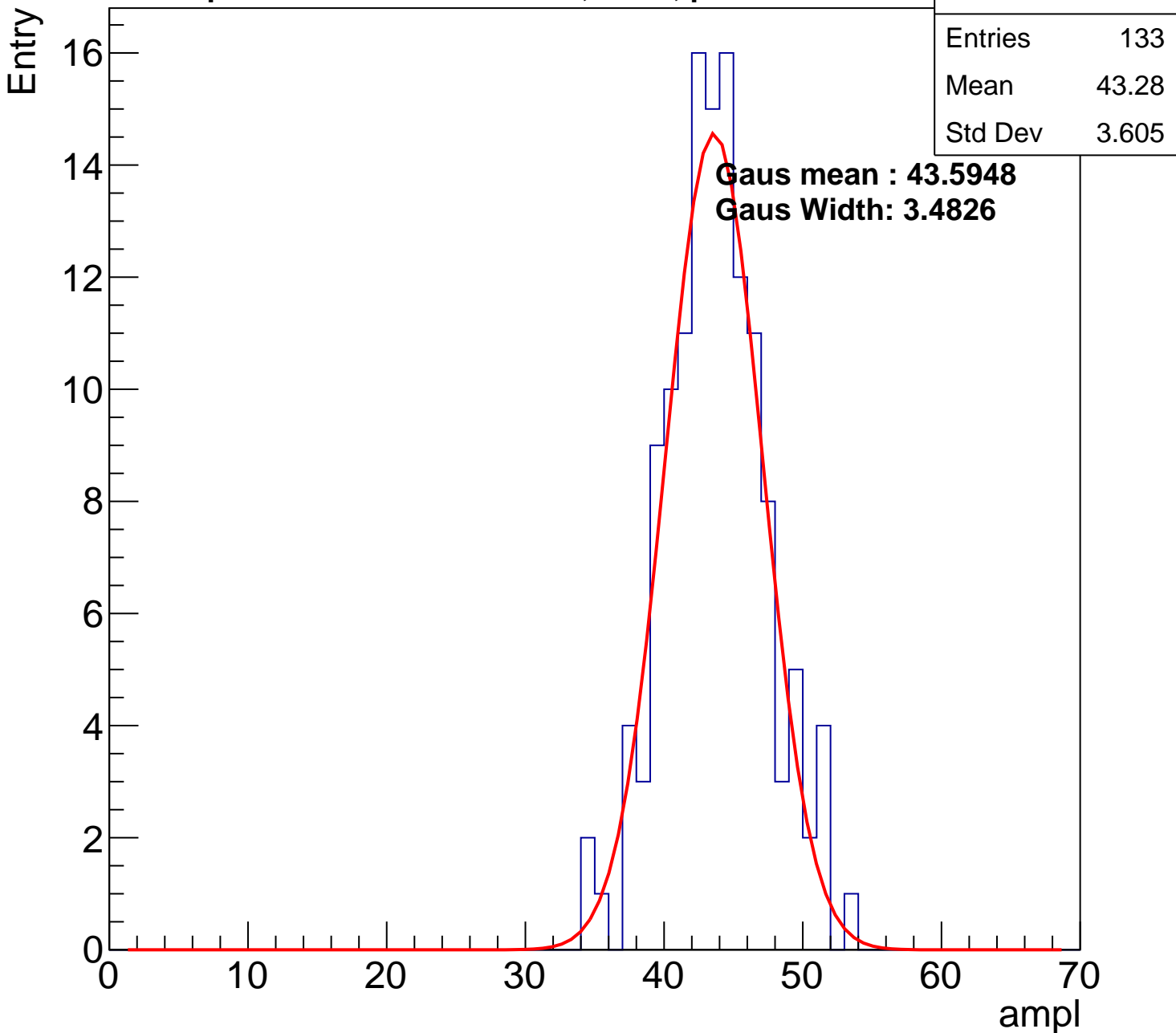
Gaus Width: 3.4826

Entry

16
14
12
10
8
6
4
2
0

ampl

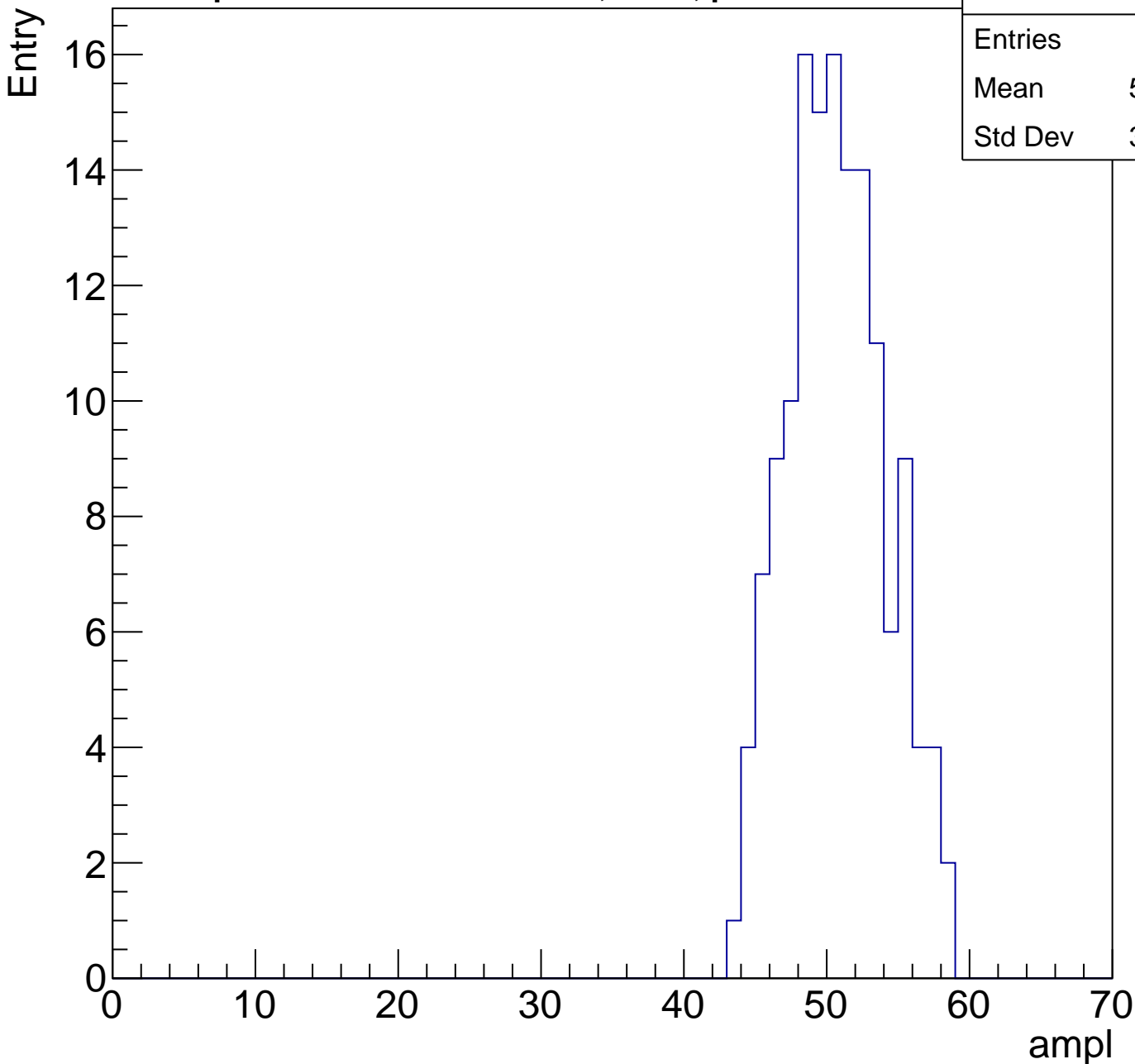
0 10 20 30 40 50 60 70



B0L101S, U21-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	50.23
Std Dev	3.389



B0L101S, U21-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

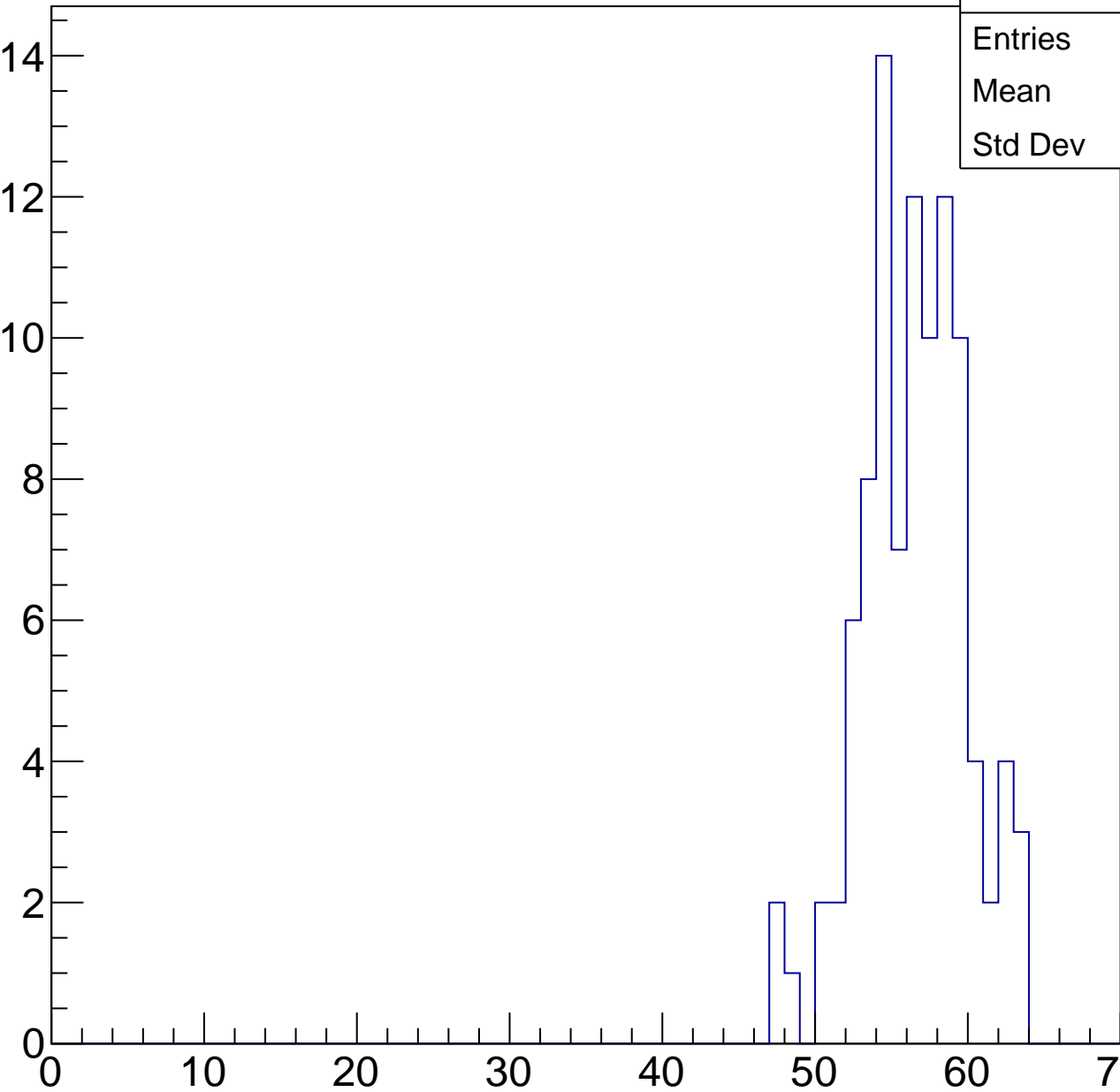
Entries	99
Mean	56.04
Std Dev	3.381

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch47, adc5

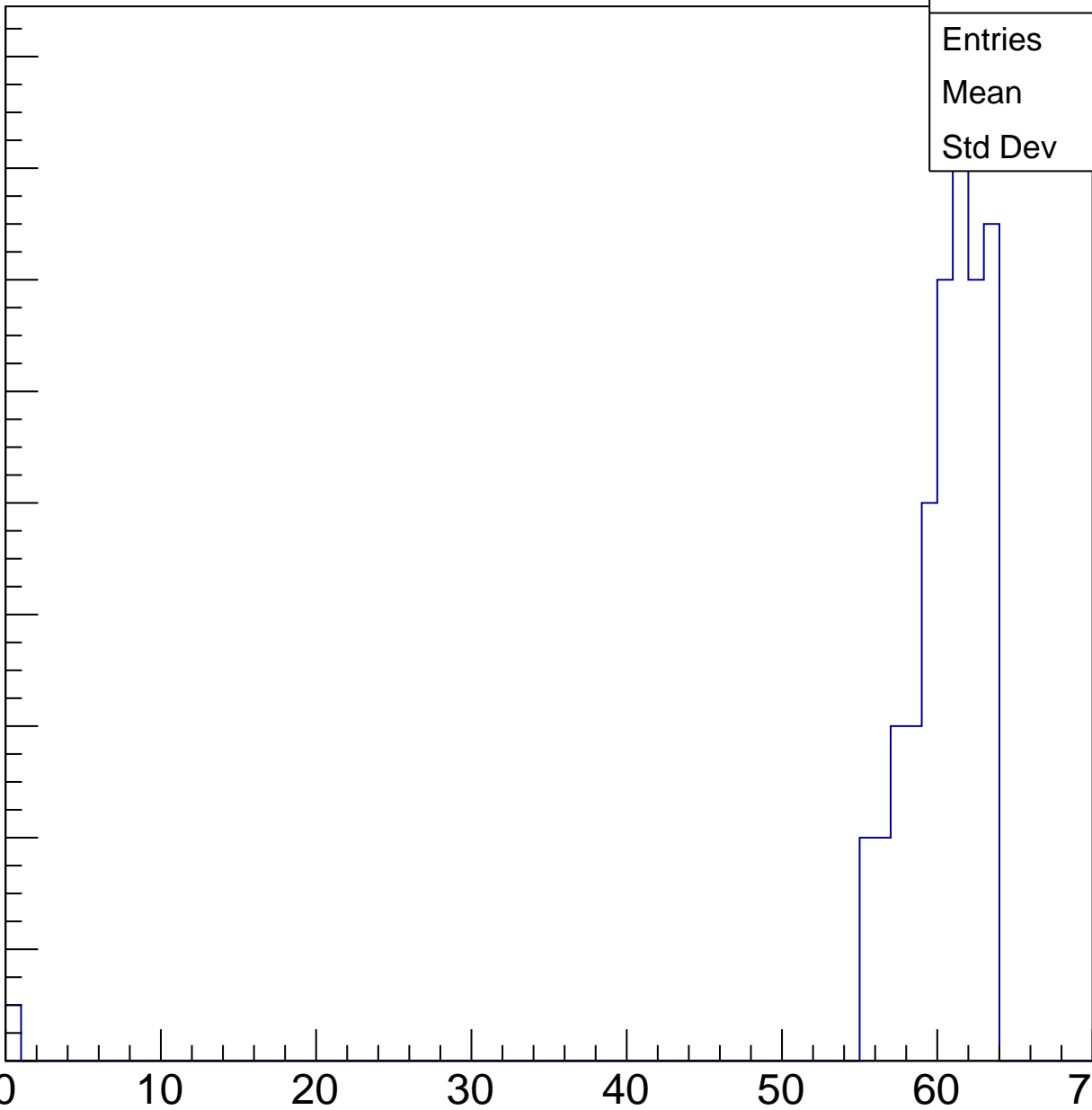
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.51
Std Dev	6.626

ampl



B0L101S, U21-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

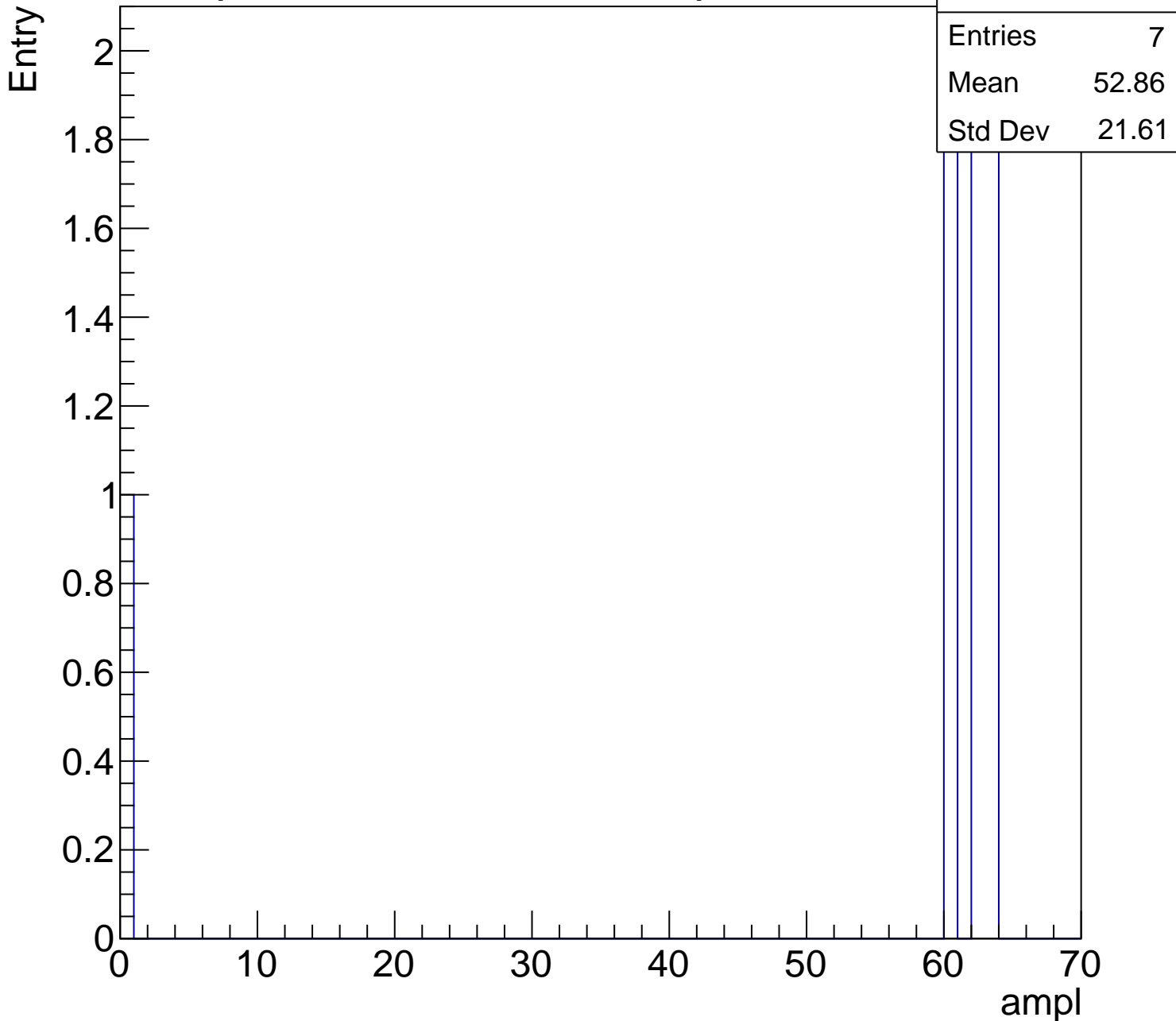
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.61

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch47, adc7

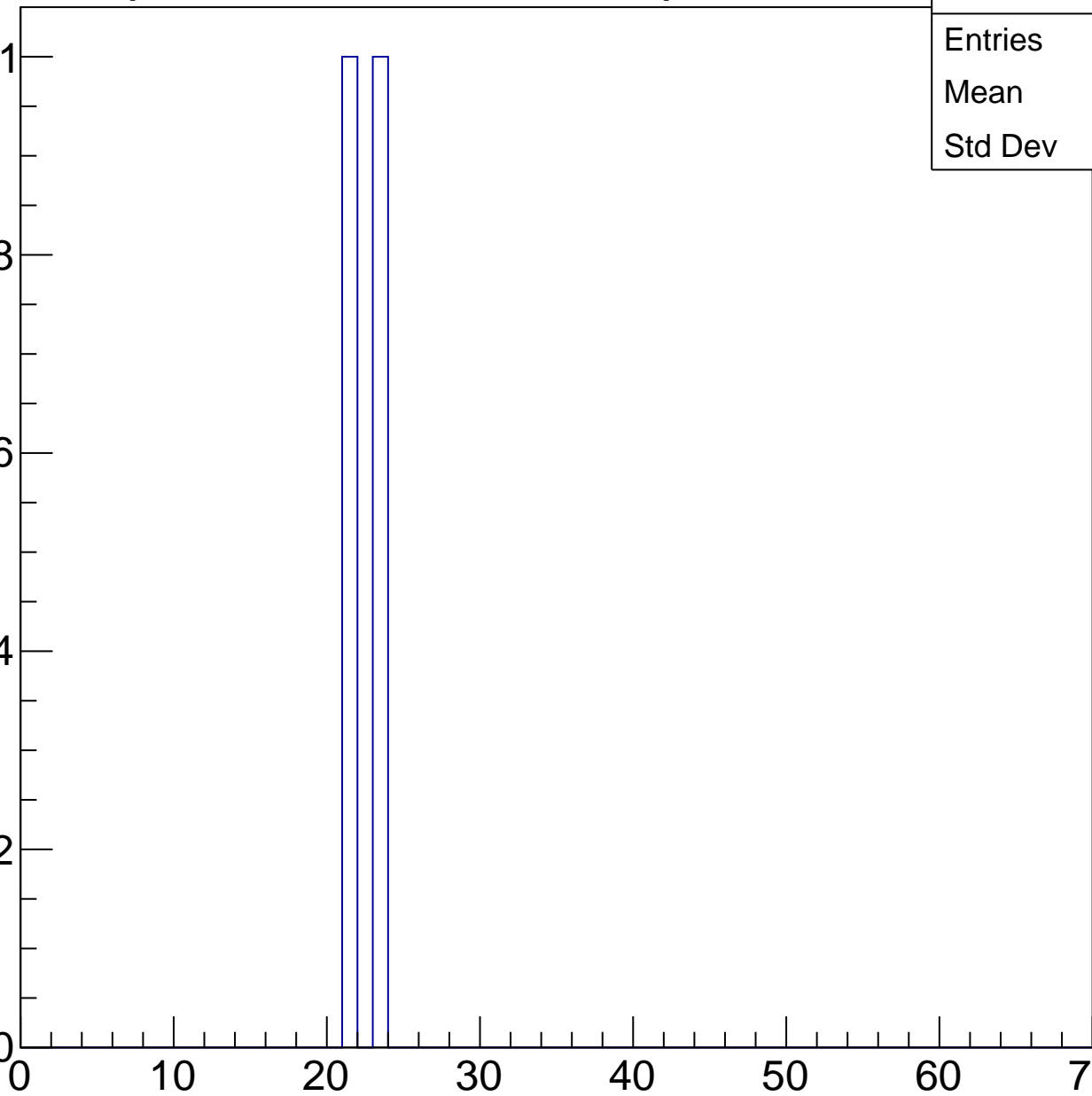
calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	22
Std Dev	1

ampl



B0L101S, U21-ch48, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	29.94
Std Dev	3.545

Gaus mean : 30.4727

Gaus Width: 3.5324

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch48, adc1

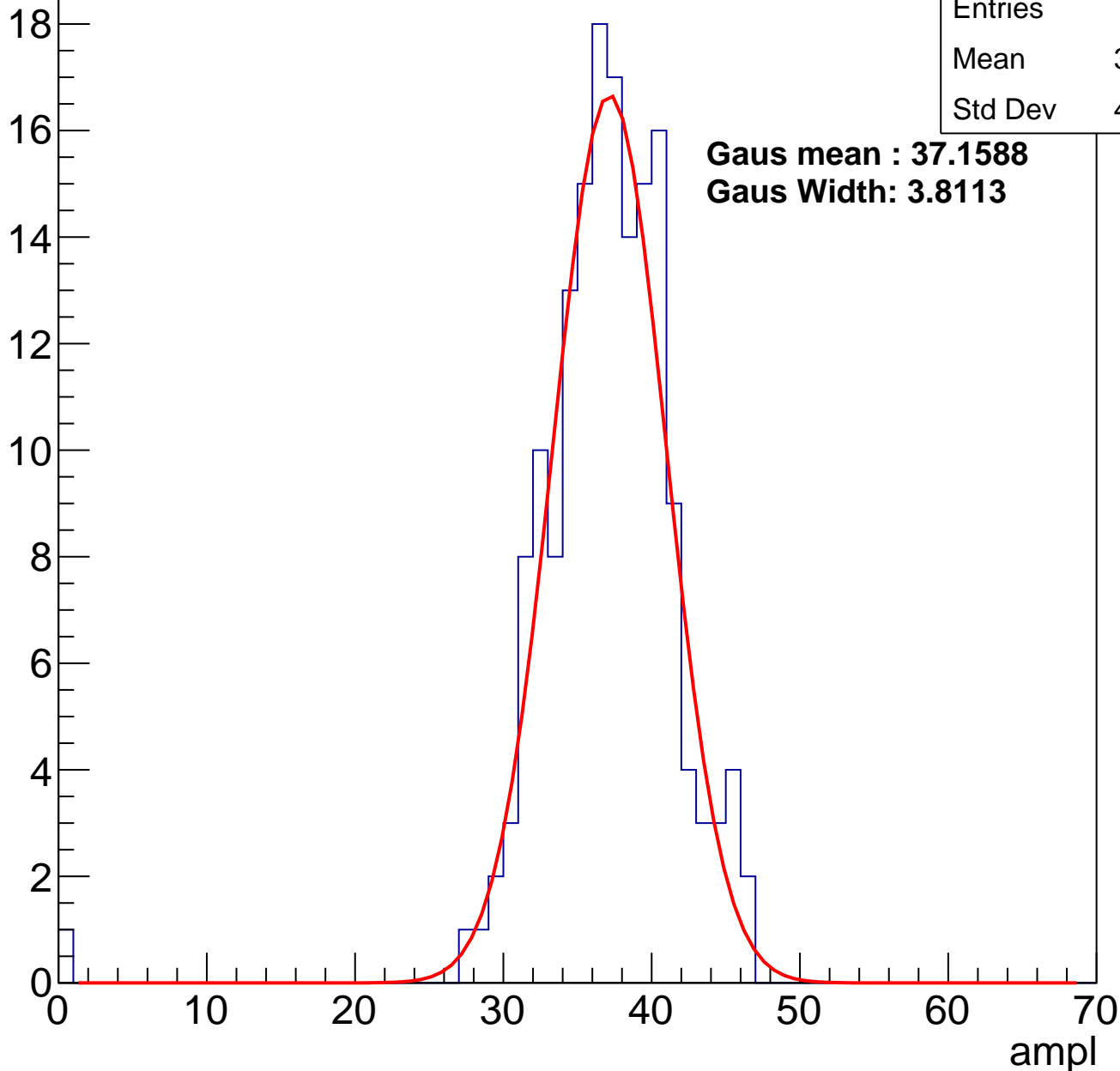
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	36.56
Std Dev	4.763

Gaus mean : 37.1588

Gaus Width: 3.8113

Entry



B0L101S, U21-ch48, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

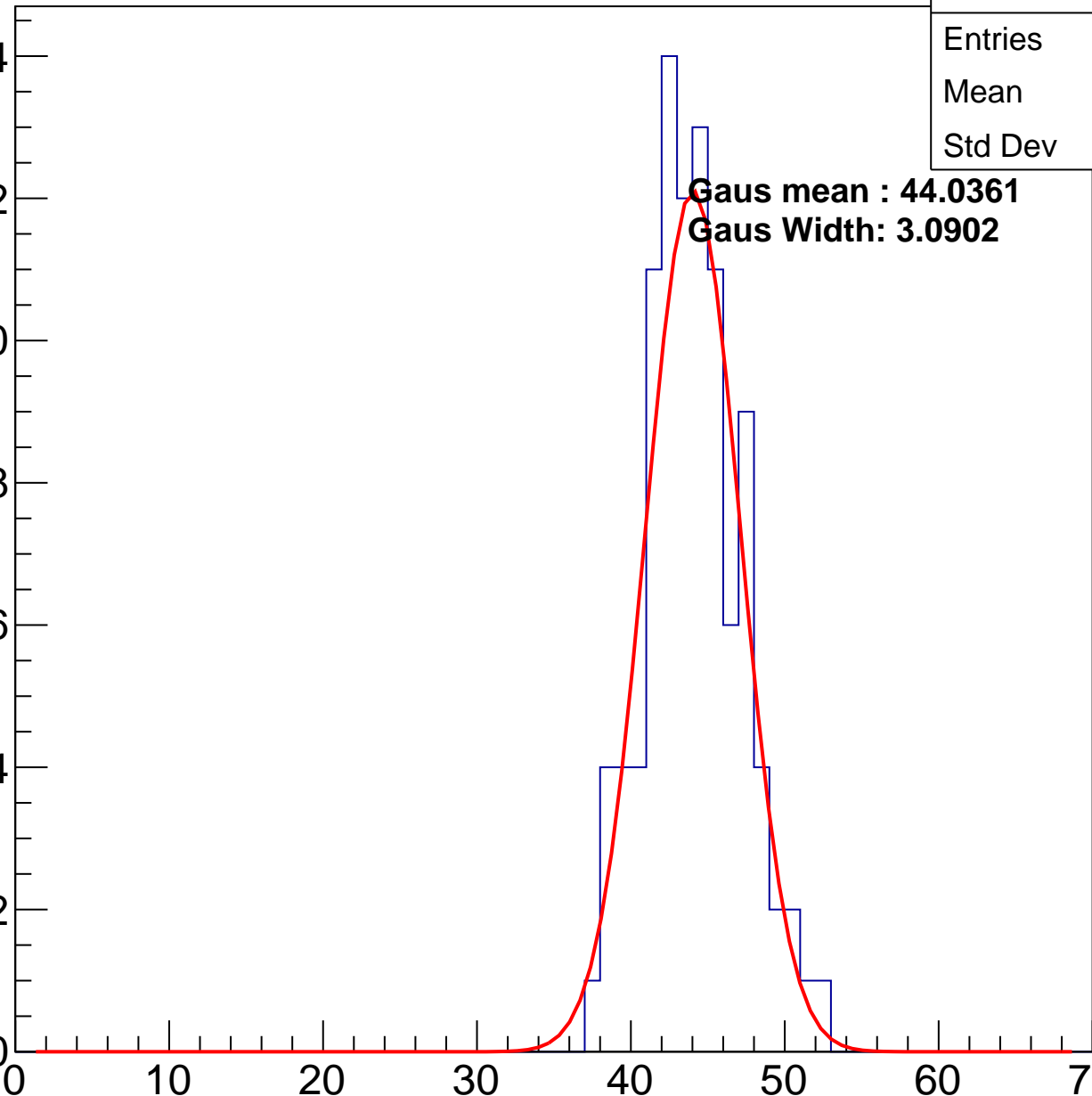
14
12
10
8
6
4
2
0

Entries	99
Mean	43.63
Std Dev	3.057

Gaus mean : 44.0361

Gaus Width: 3.0902

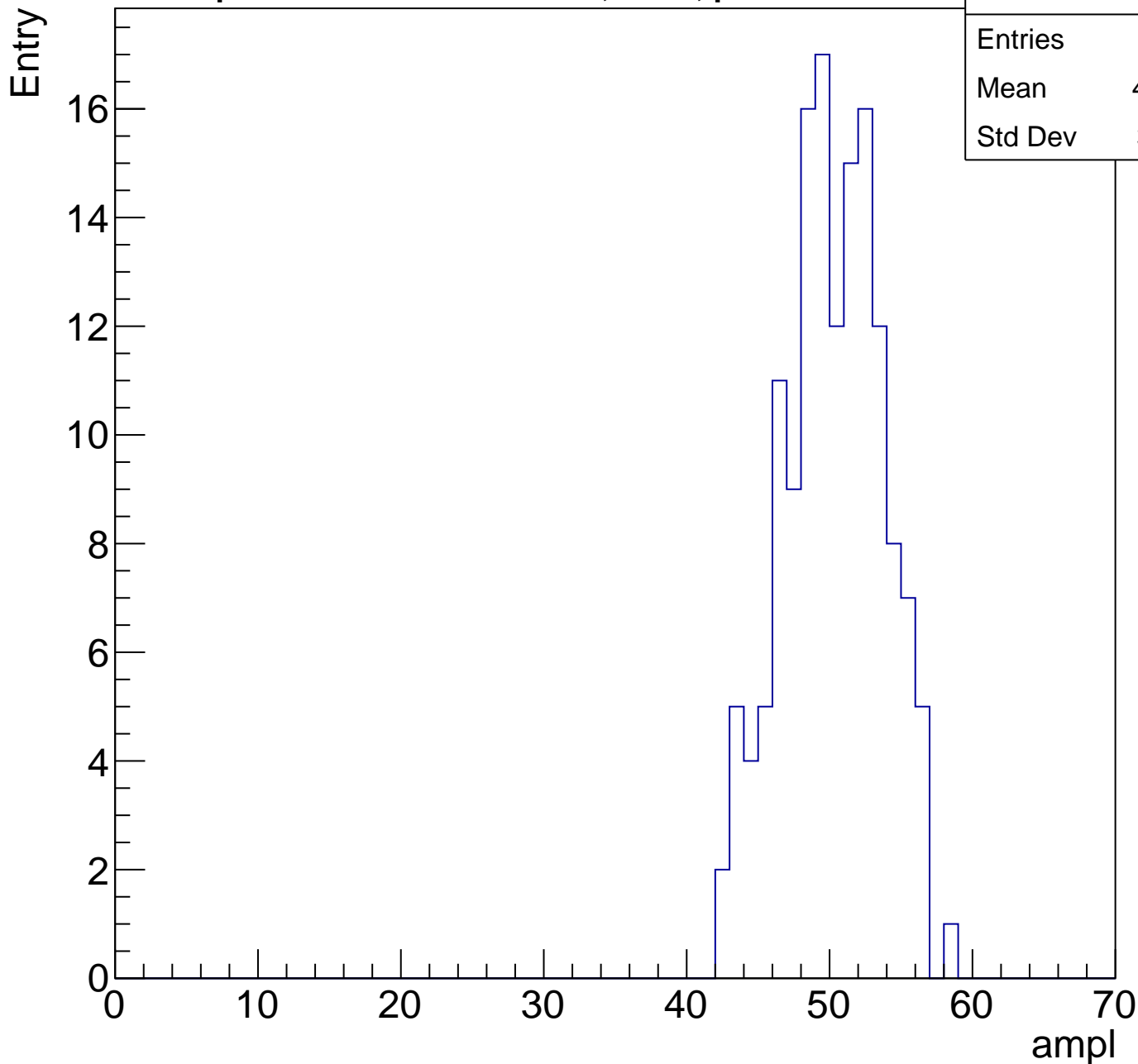
ampl



B0L101S, U21-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	49.78
Std Dev	3.431



B0L101S, U21-ch48, adc4

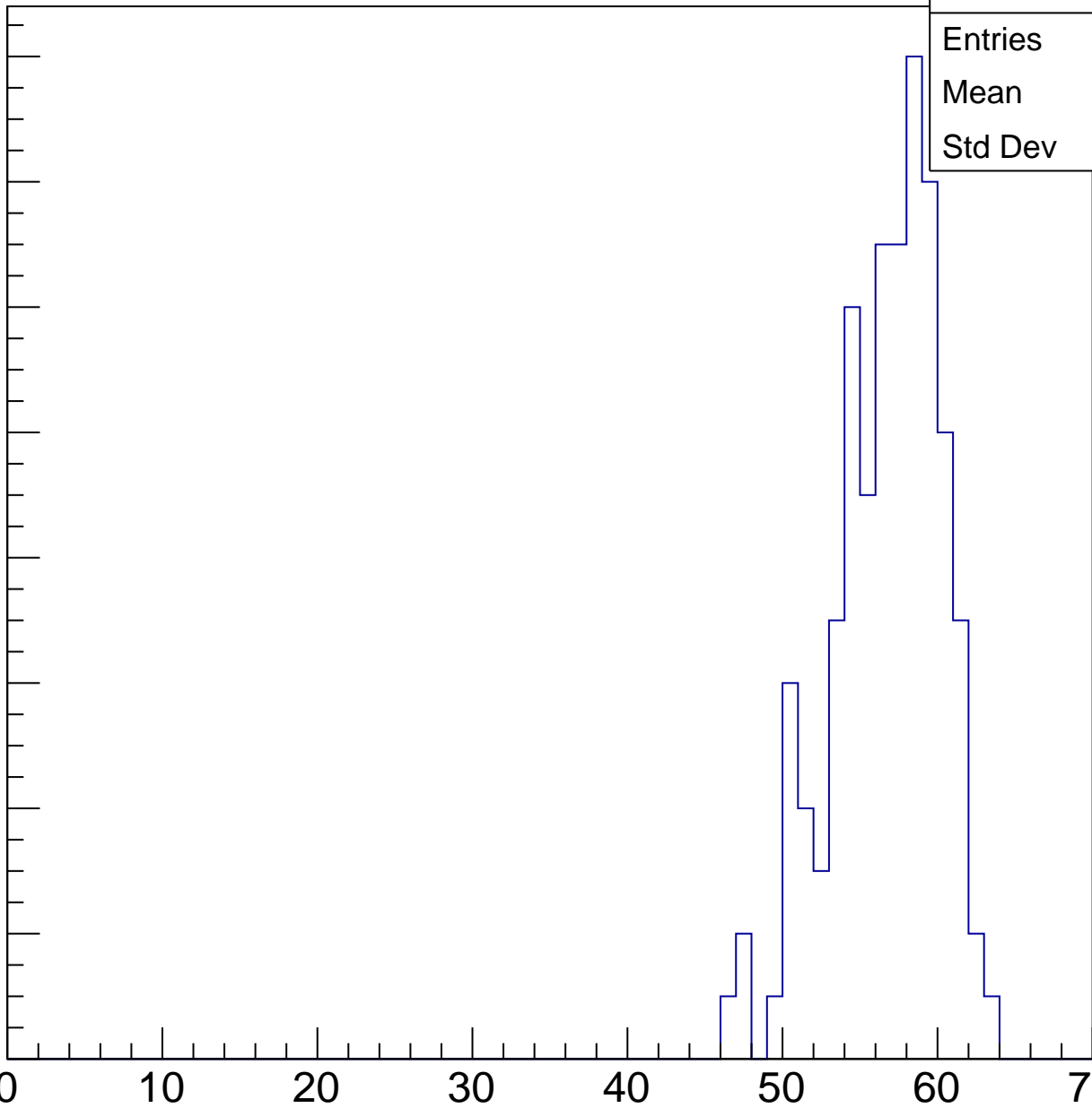
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	56.2
Std Dev	3.461

ampl



B0L101S, U21-ch48, adc5

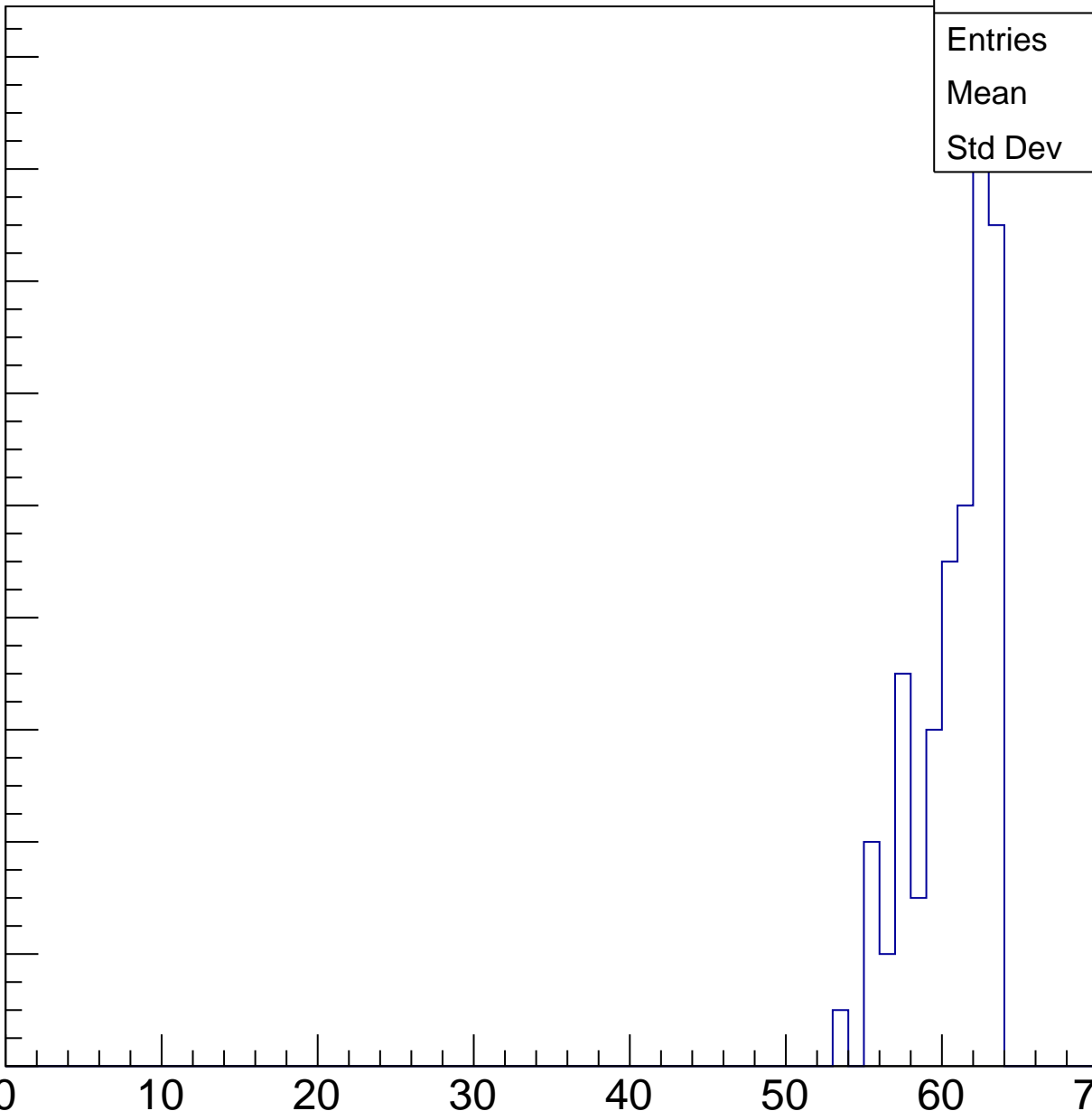
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	75
Mean	60.31
Std Dev	2.509

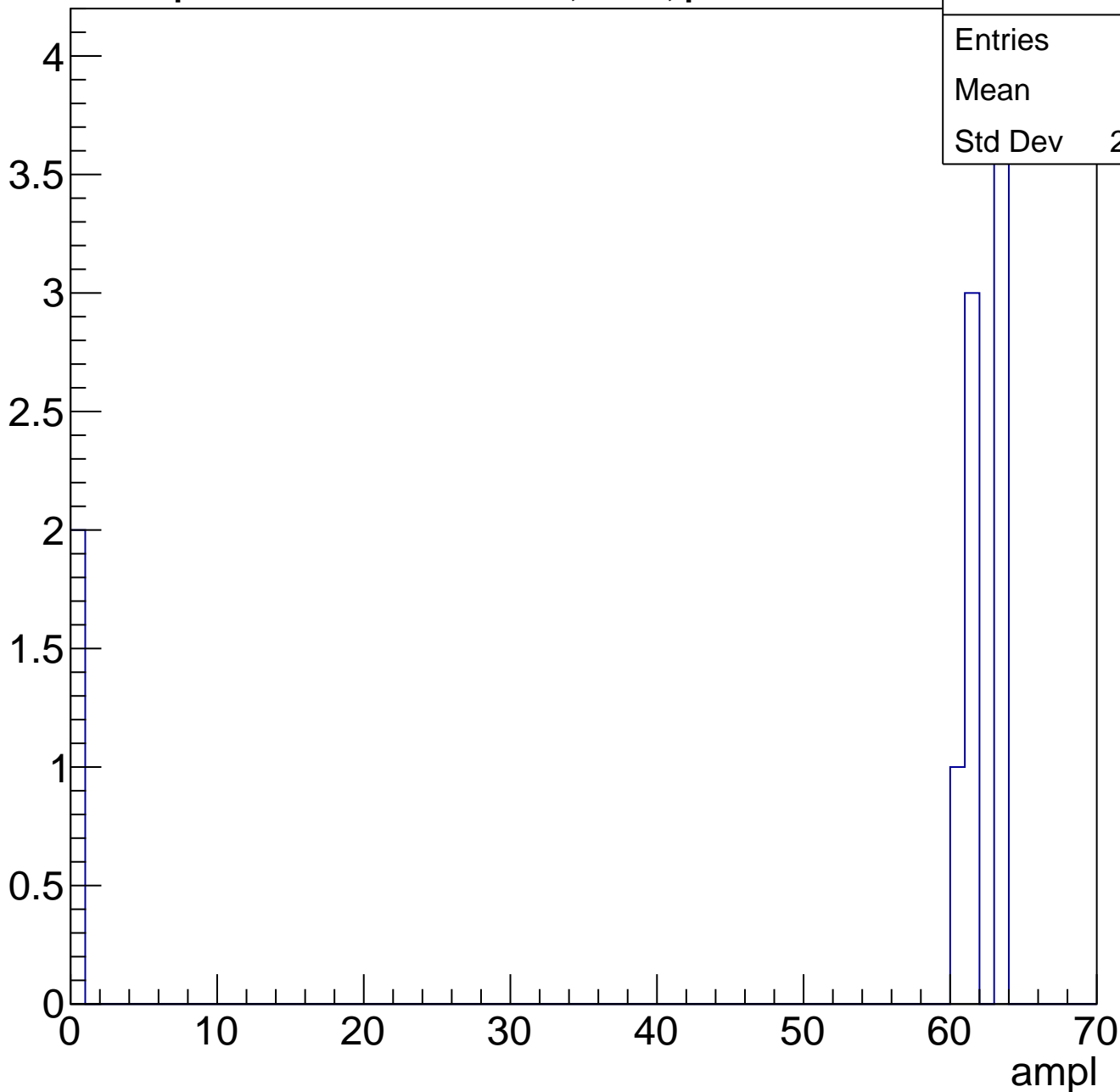
ampl



B0L101S, U21-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	10
Mean	49.5
Std Dev	24.77

B0L101S, U21-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch49, adc0

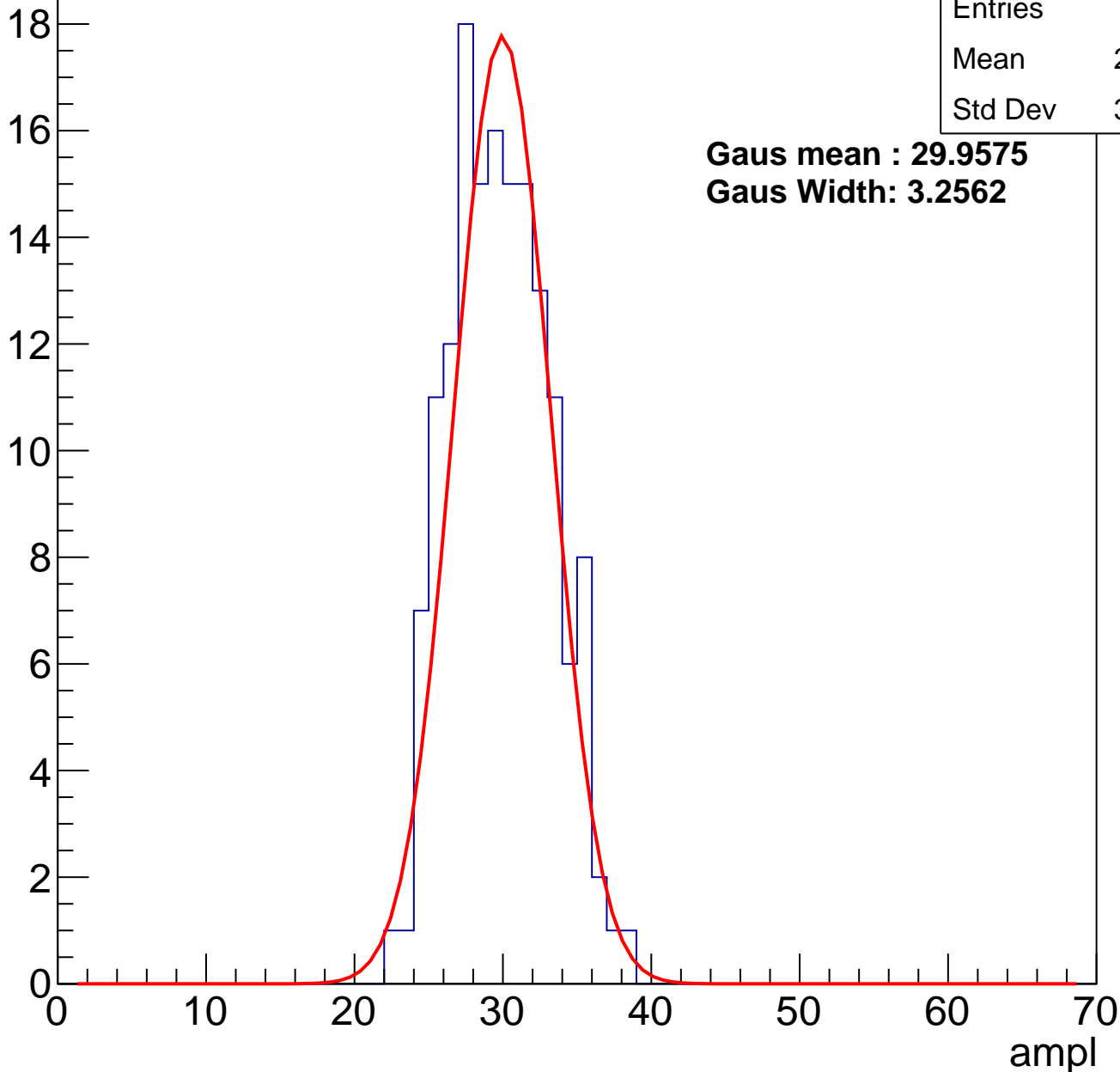
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	29.38
Std Dev	3.292

Gaus mean : 29.9575

Gaus Width: 3.2562

Entry



B0L101S, U21-ch49, adc1

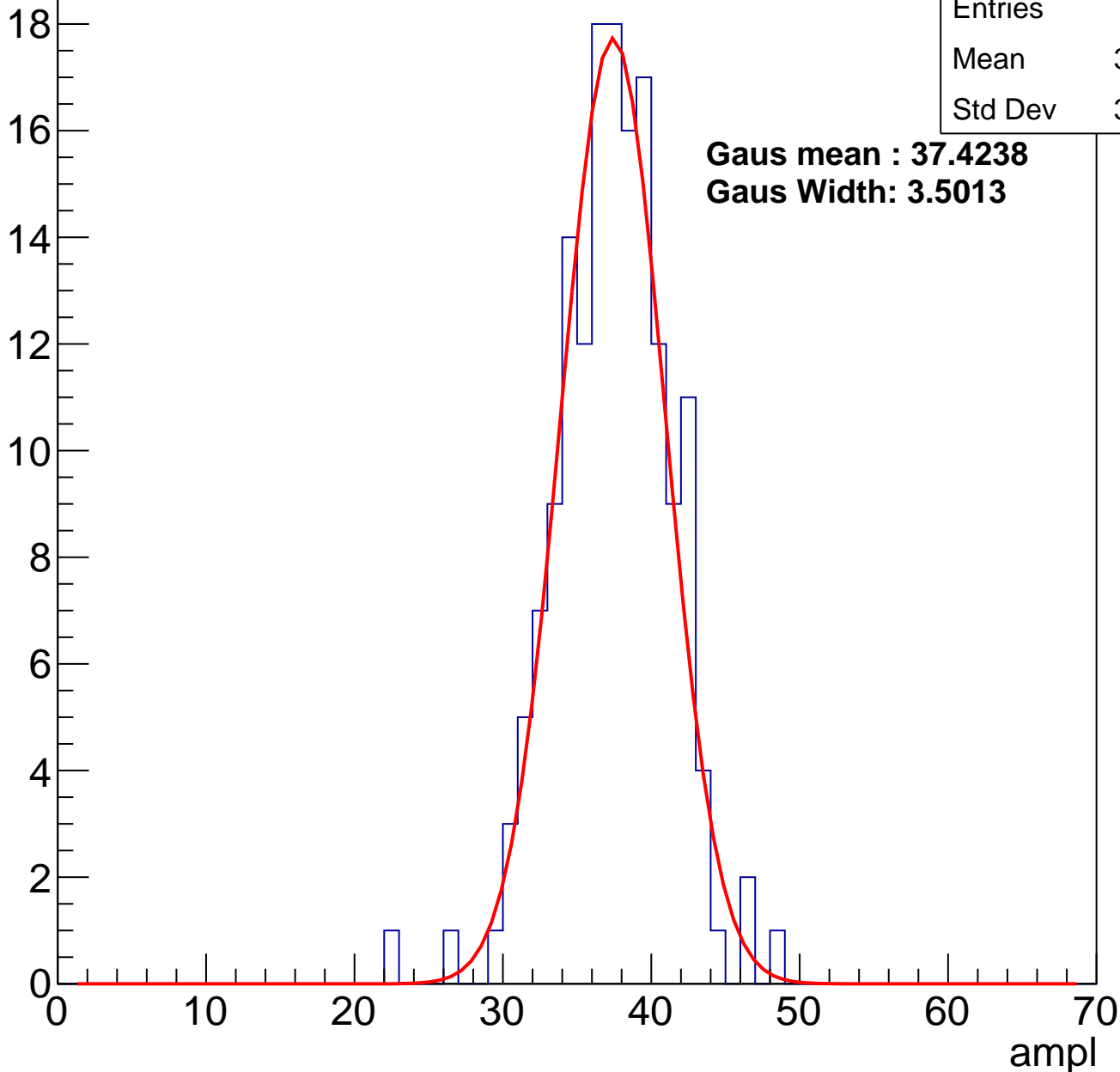
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	36.98
Std Dev	3.796

Gaus mean : 37.4238

Gaus Width: 3.5013

Entry



B0L101S, U21-ch49, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

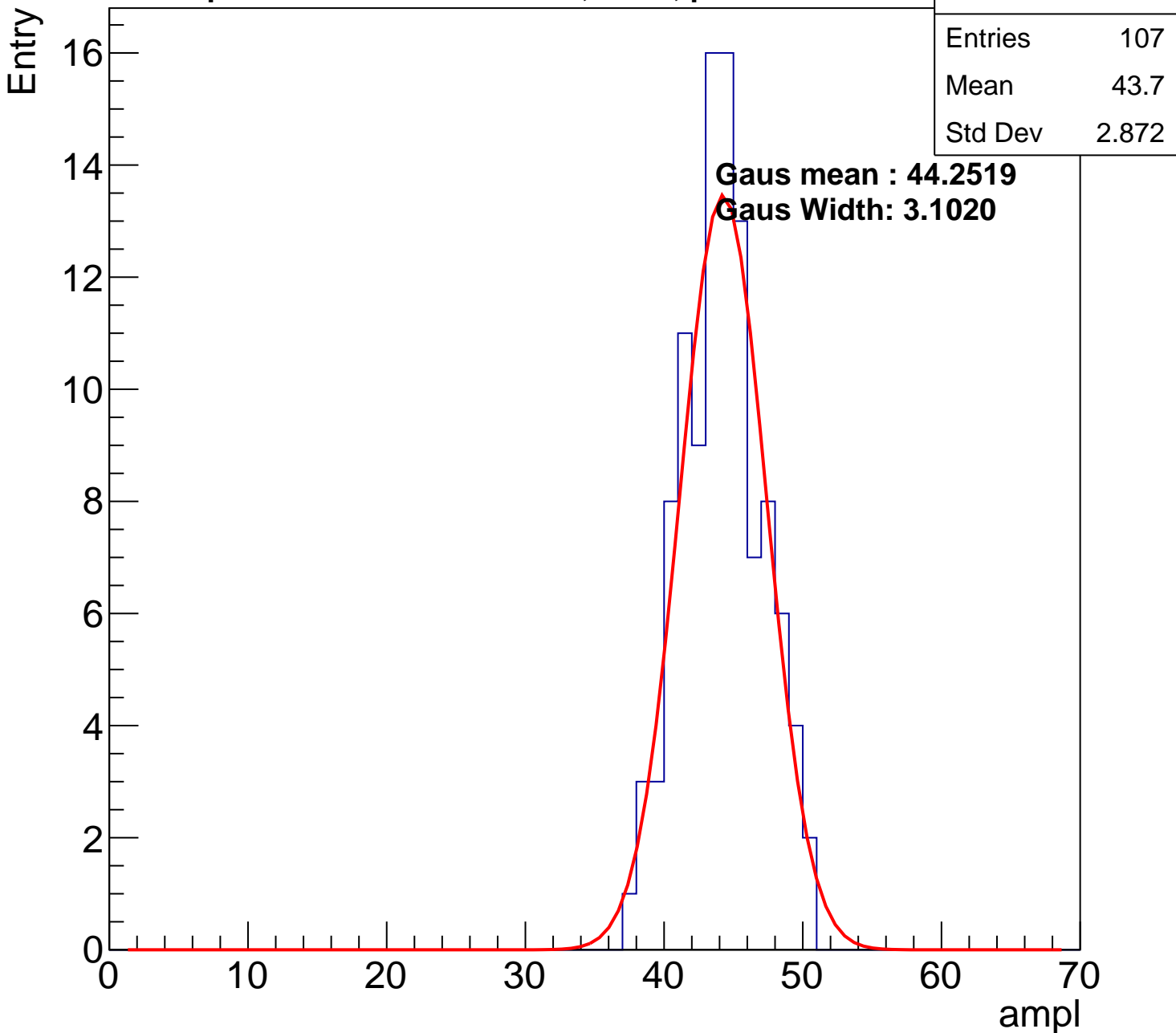
Entries	107
Mean	43.7
Std Dev	2.872

Gaus mean : 44.2519

Gaus Width: 3.1020

0 10 20 30 40 50 60 70

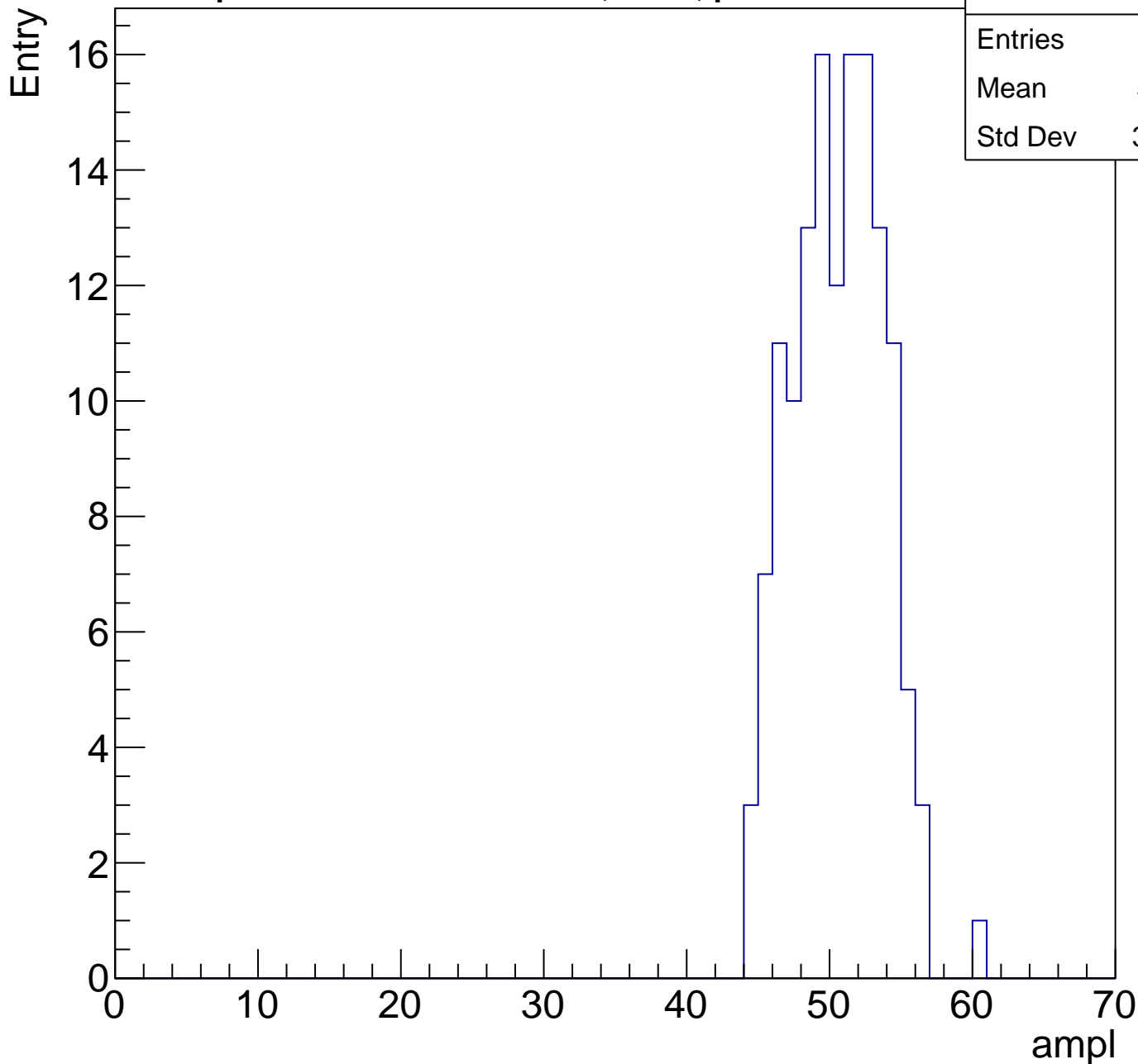
ampl



B0L101S, U21-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	50.11
Std Dev	3.106



B0L101S, U21-ch49, adc4

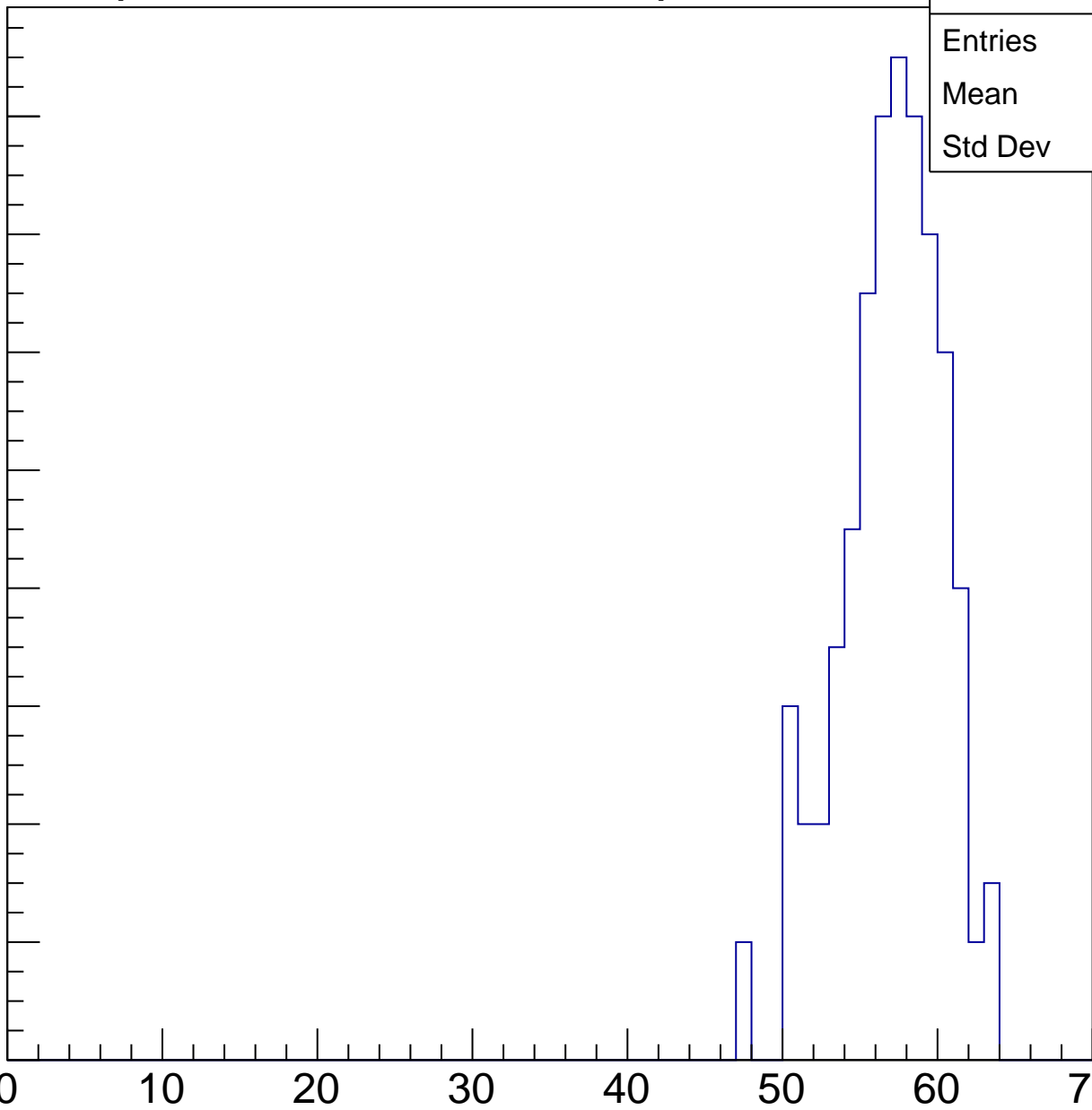
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	56.53
Std Dev	3.285

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch49, adc5

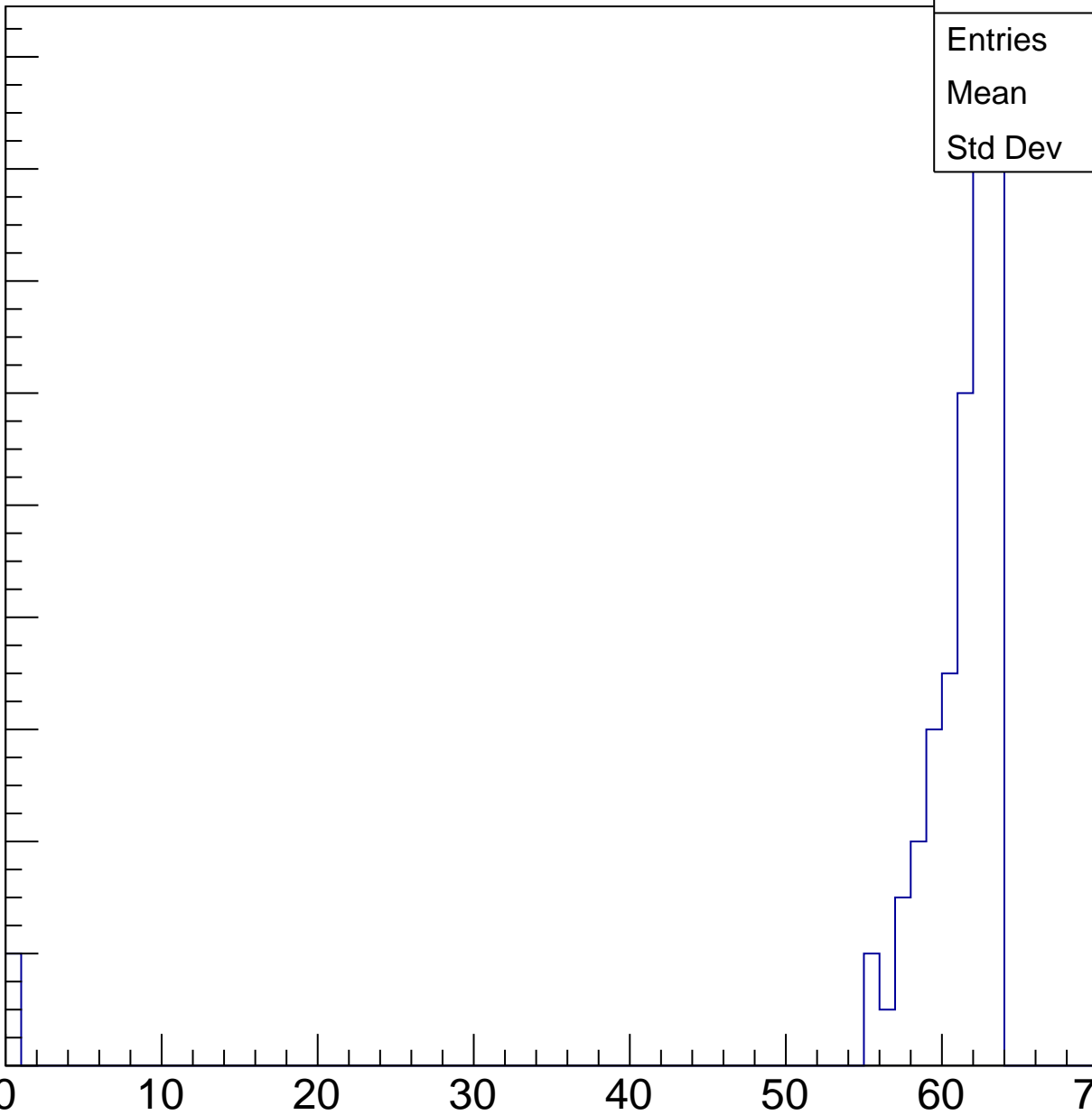
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	72
Mean	59.19
Std Dev	10.21

ampl



B0L101S, U21-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	21
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L101S, U21-ch50, adc0

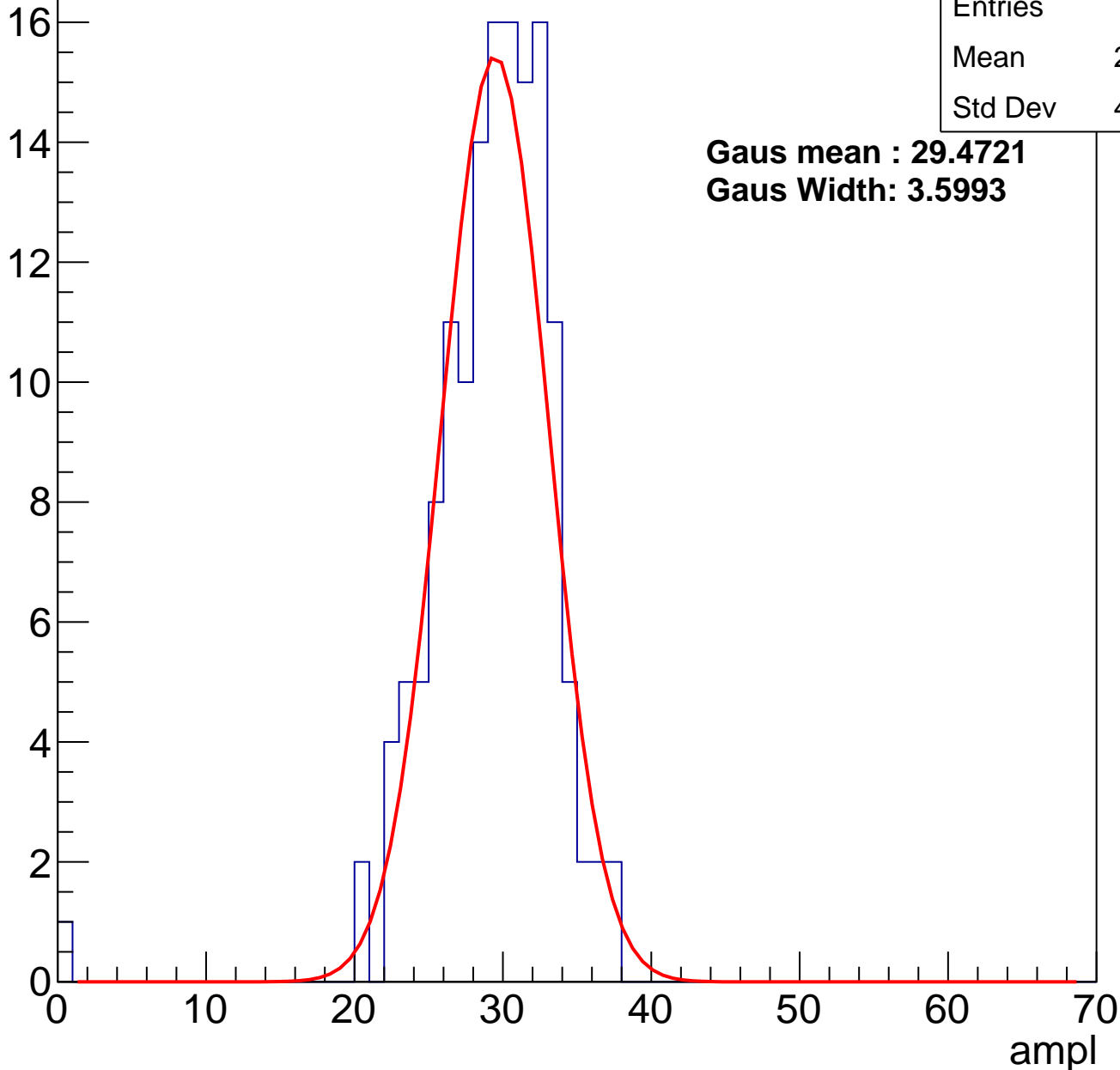
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.83
Std Dev	4.235

Gaus mean : 29.4721

Gaus Width: 3.5993

Entry



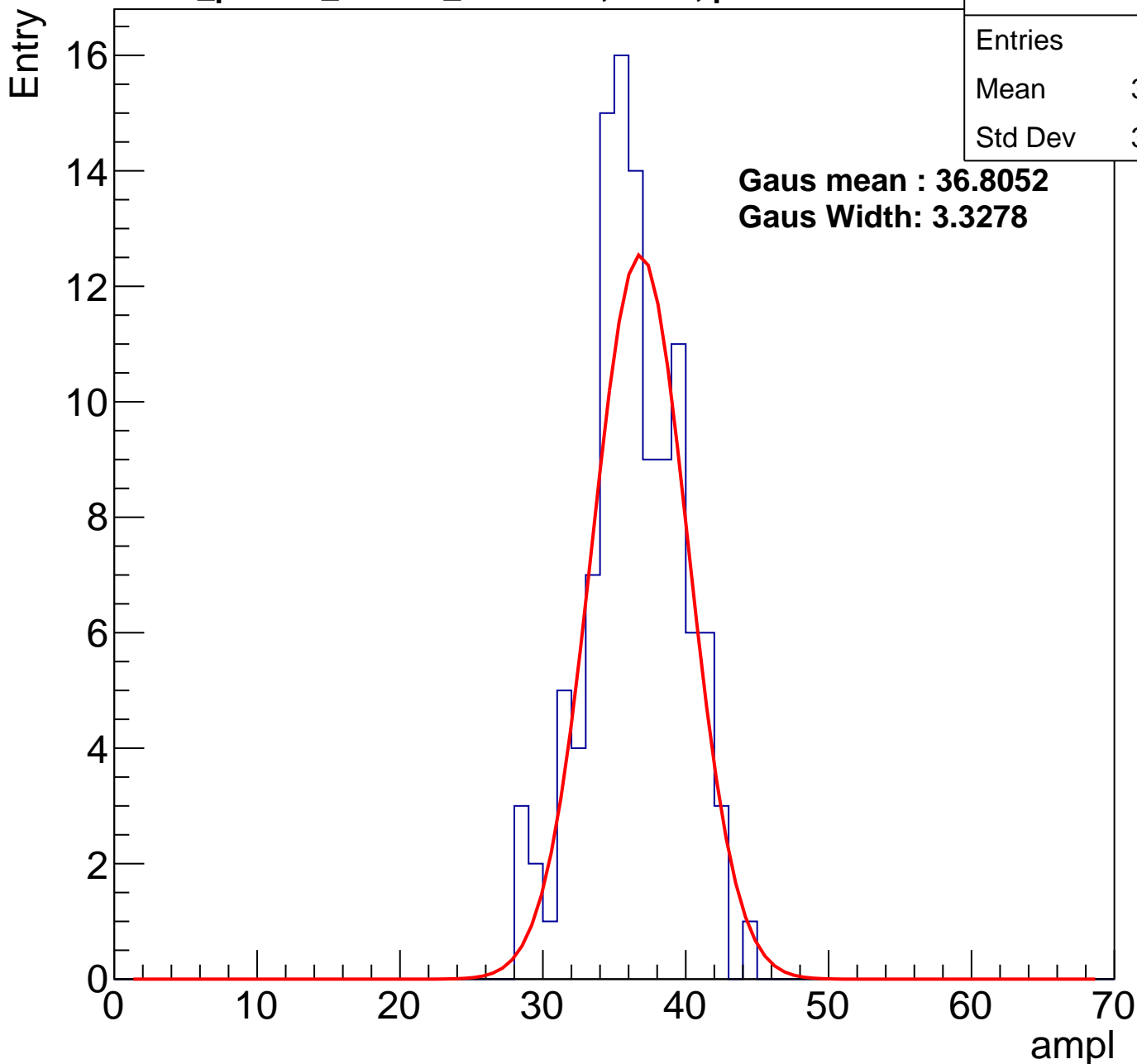
B0L101S, U21-ch50, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	35.89
Std Dev	3.293

Gaus mean : 36.8052

Gaus Width: 3.3278



B0L101S, U21-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	42.23
Std Dev	3.623

Gaus mean : 42.8060

Gaus Width: 3.9284

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

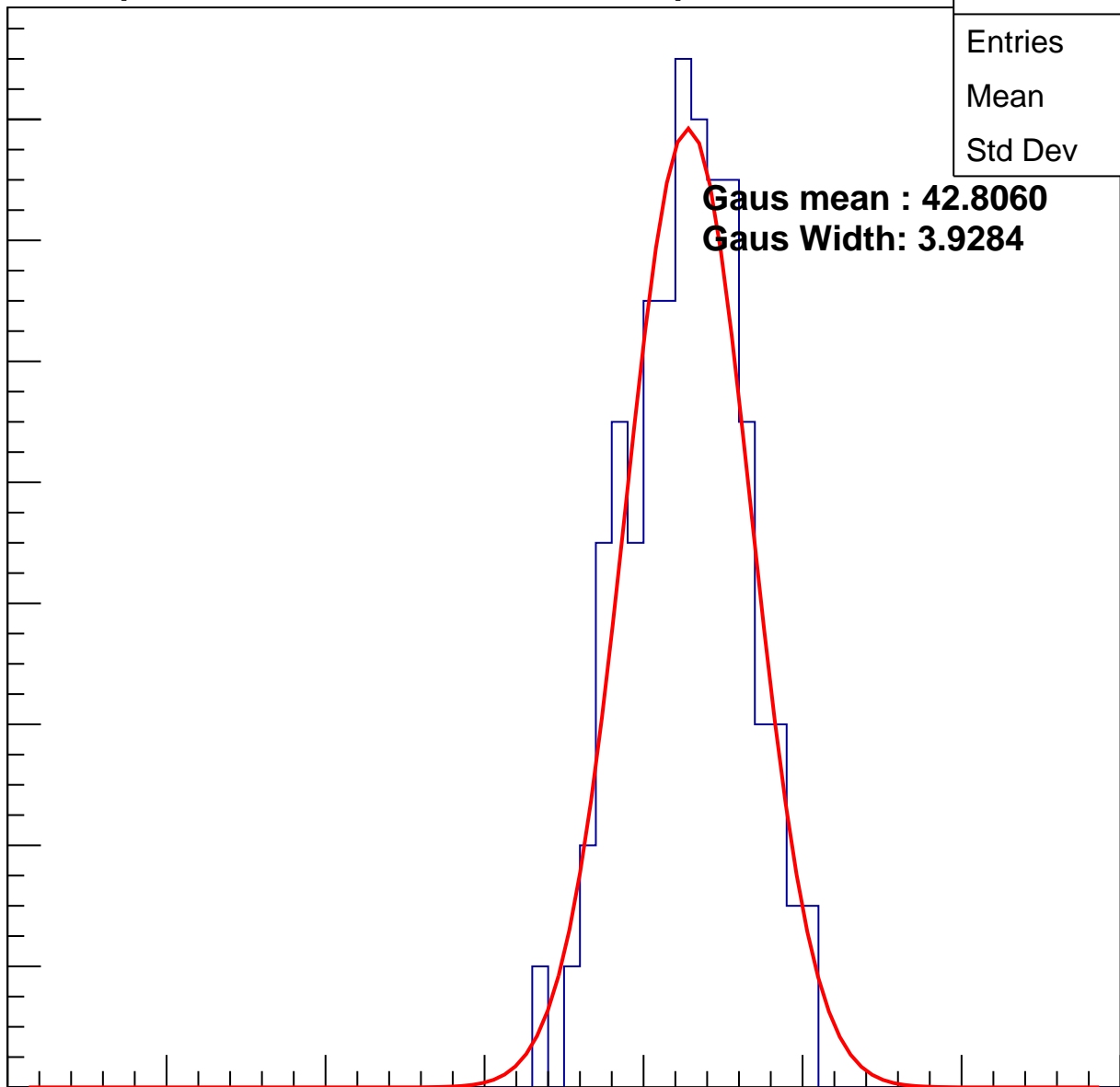
40

50

60

70

ampl

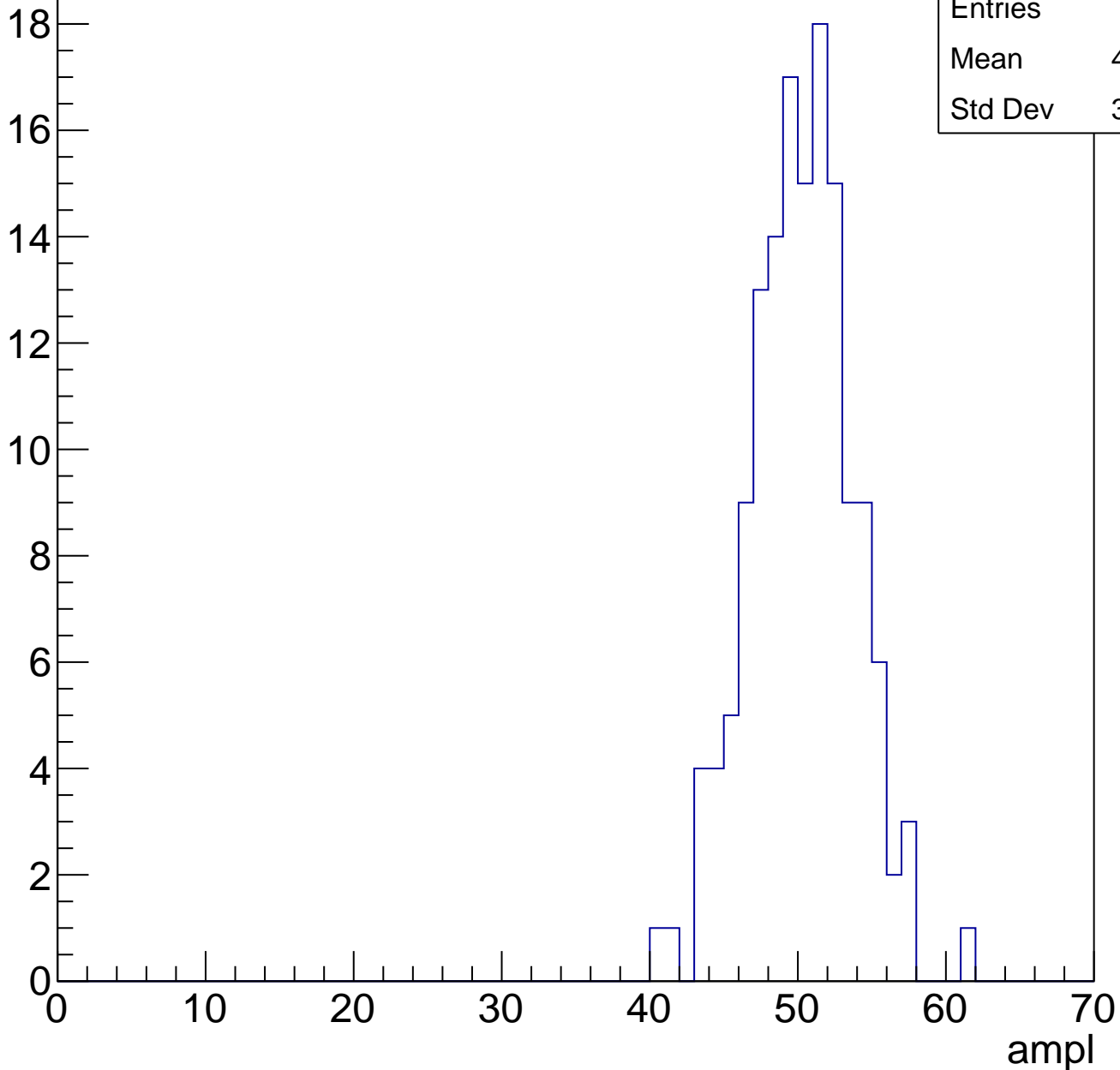


B0L101S, U21-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	49.79
Std Dev	3.478

Entry



B0L101S, U21-ch50, adc4

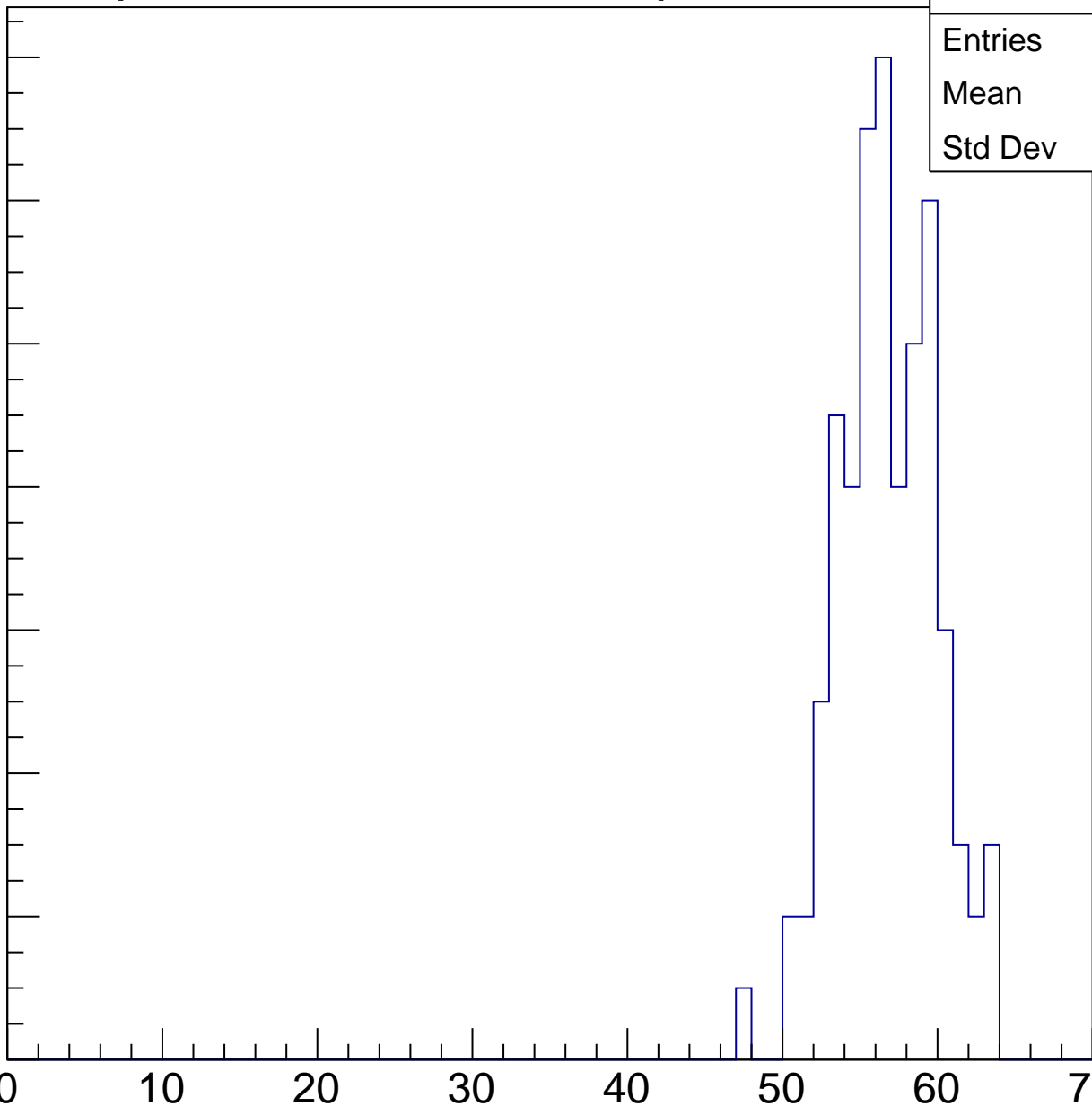
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	56.3
Std Dev	3.108

ampl



B0L101S, U21-ch50, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	83
Mean	58.08
Std Dev	9.481

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	21
Mean	61.62
Std Dev	1.463

ampl

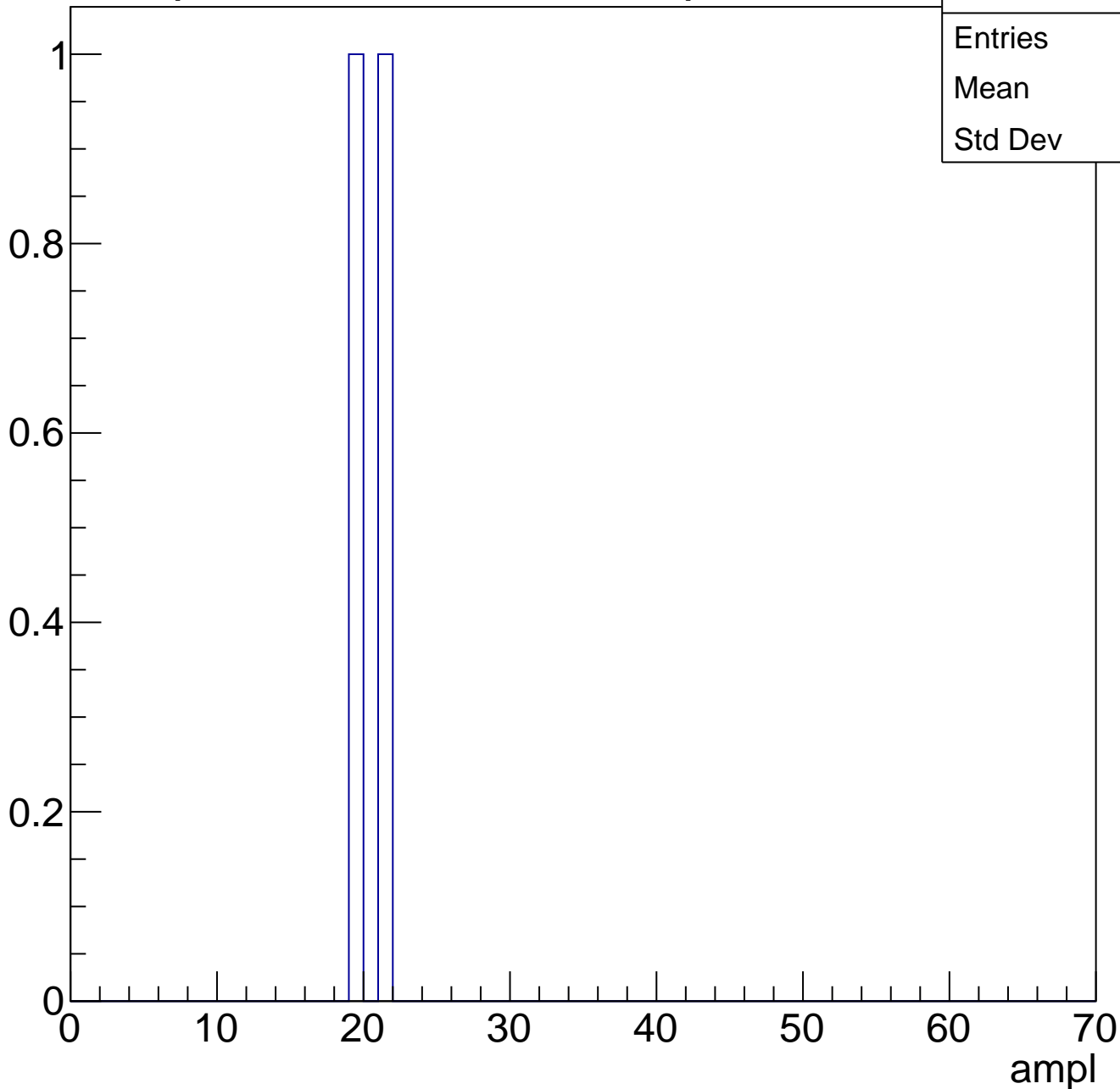
0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8

B0L101S, U21-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch51, adc0

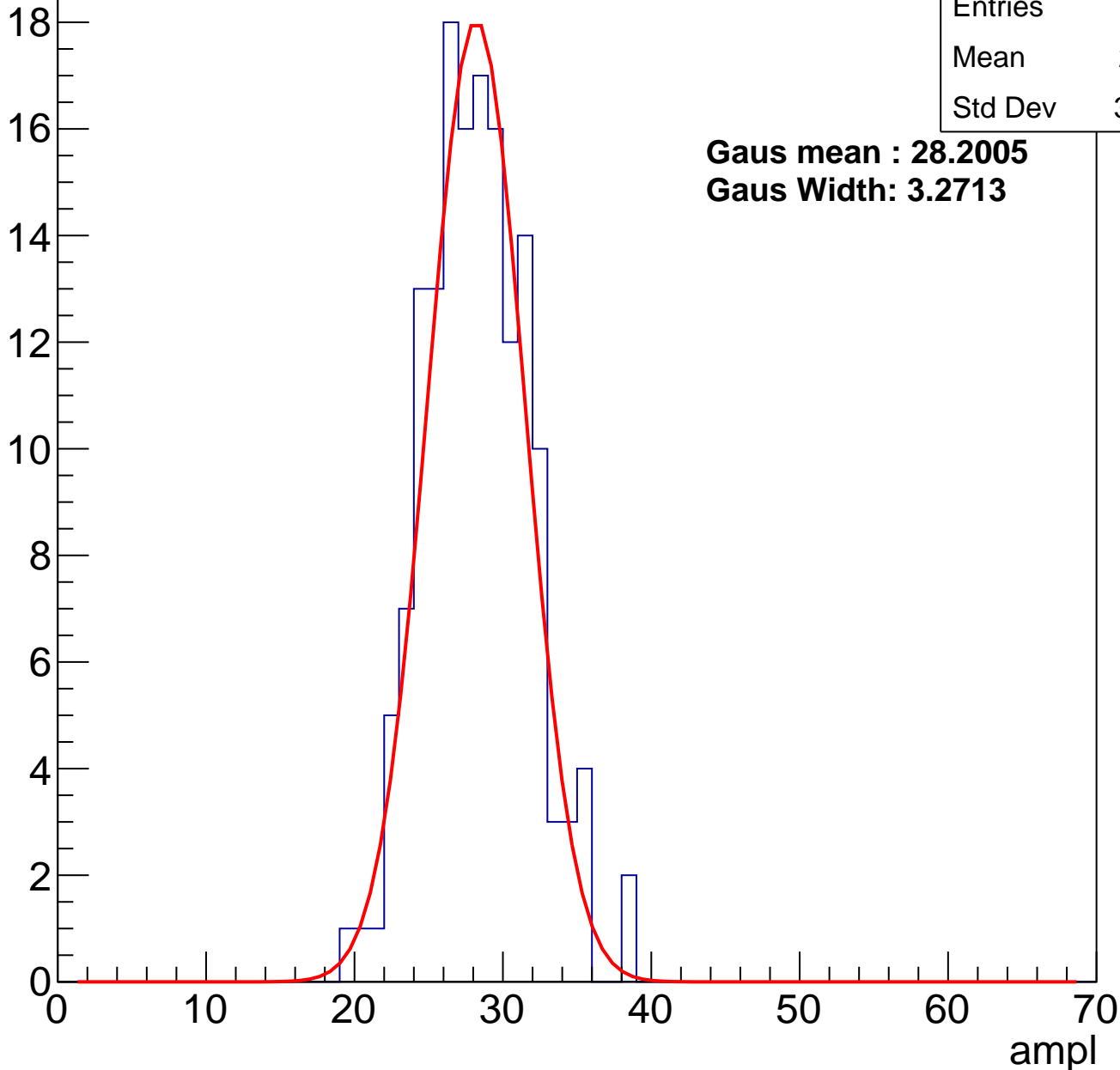
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	27.81
Std Dev	3.477

Gaus mean : 28.2005

Gaus Width: 3.2713

Entry



B0L101S, U21-ch51, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	35.36
Std Dev	3.55

Gaus mean : 35.8794

Gaus Width: 3.4490

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

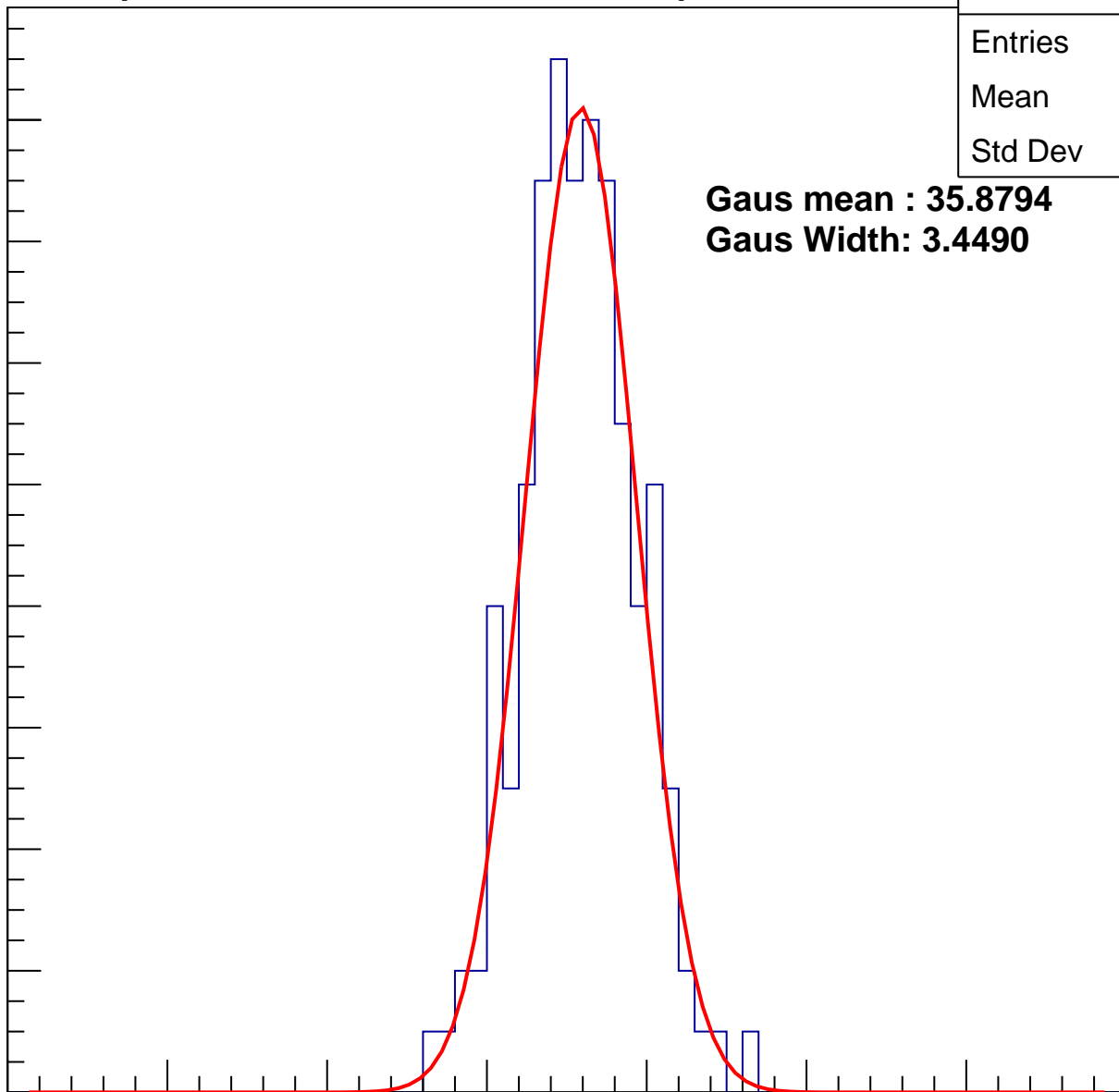
40

50

60

70

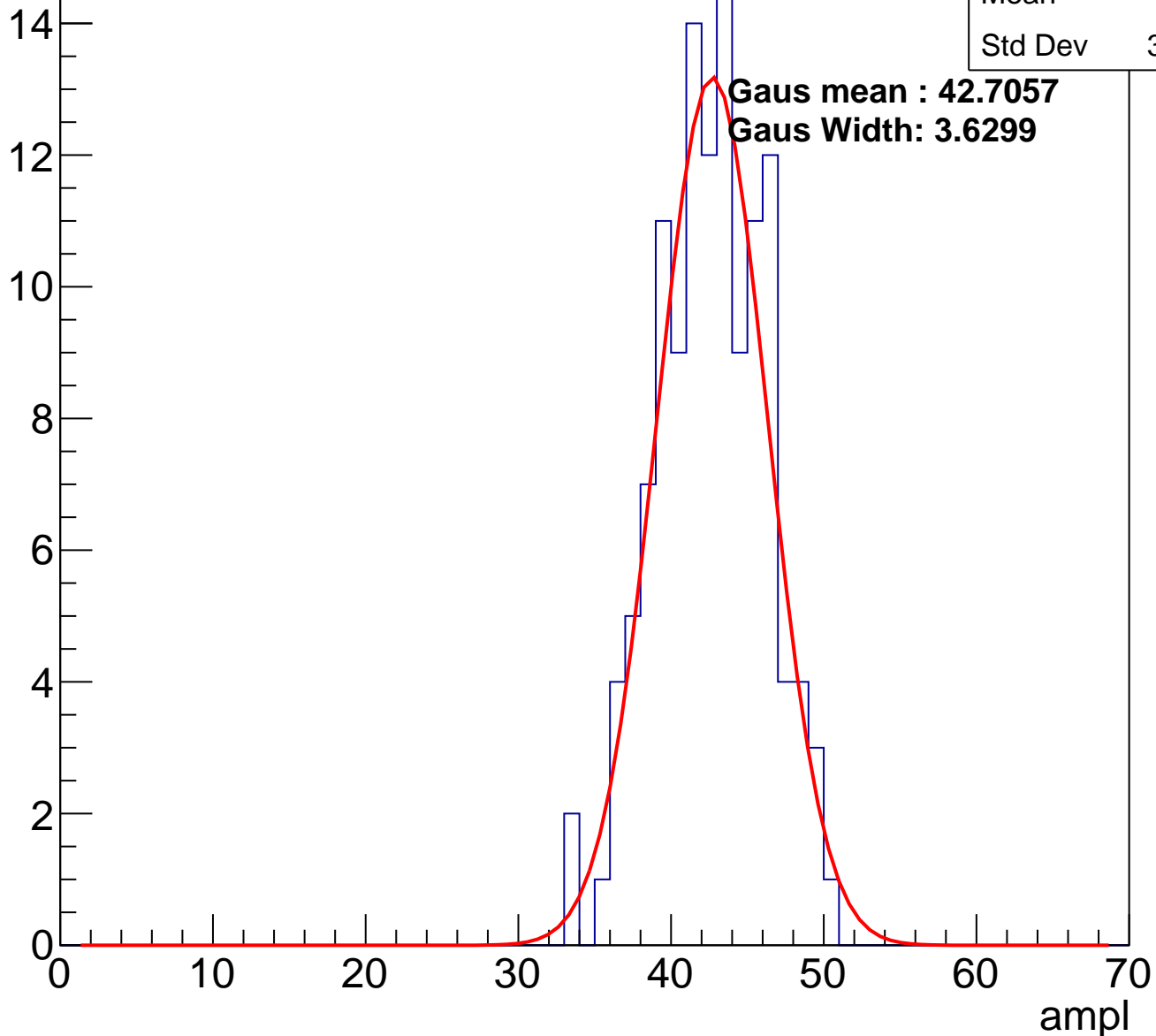
ampl



B0L101S, U21-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

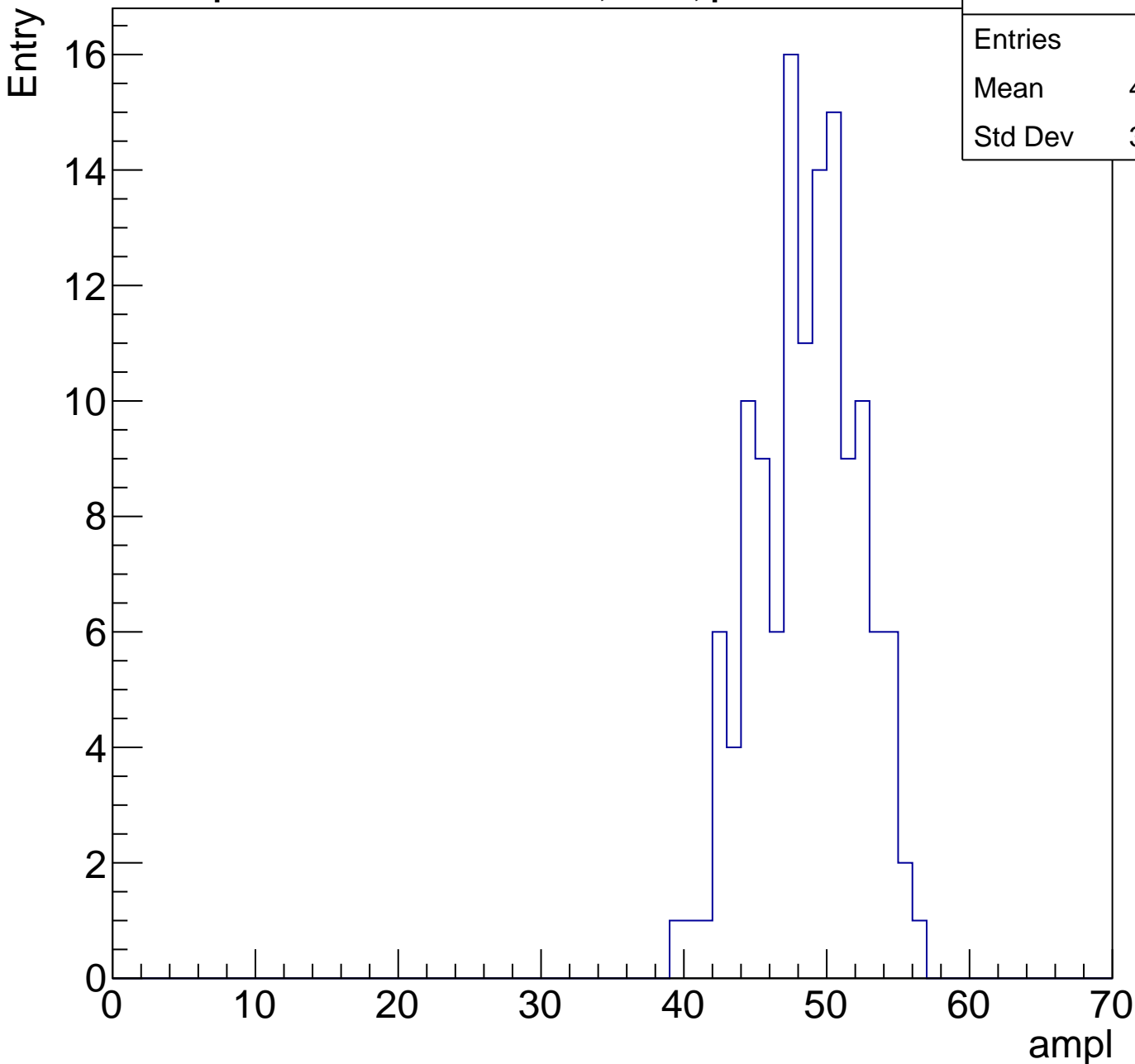
Entry



B0L101S, U21-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	48.19
Std Dev	3.568



B0L101S, U21-ch51, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	54.5
Std Dev	3.385

Entry

12

10

8

6

4

2

0

0

10

20

30

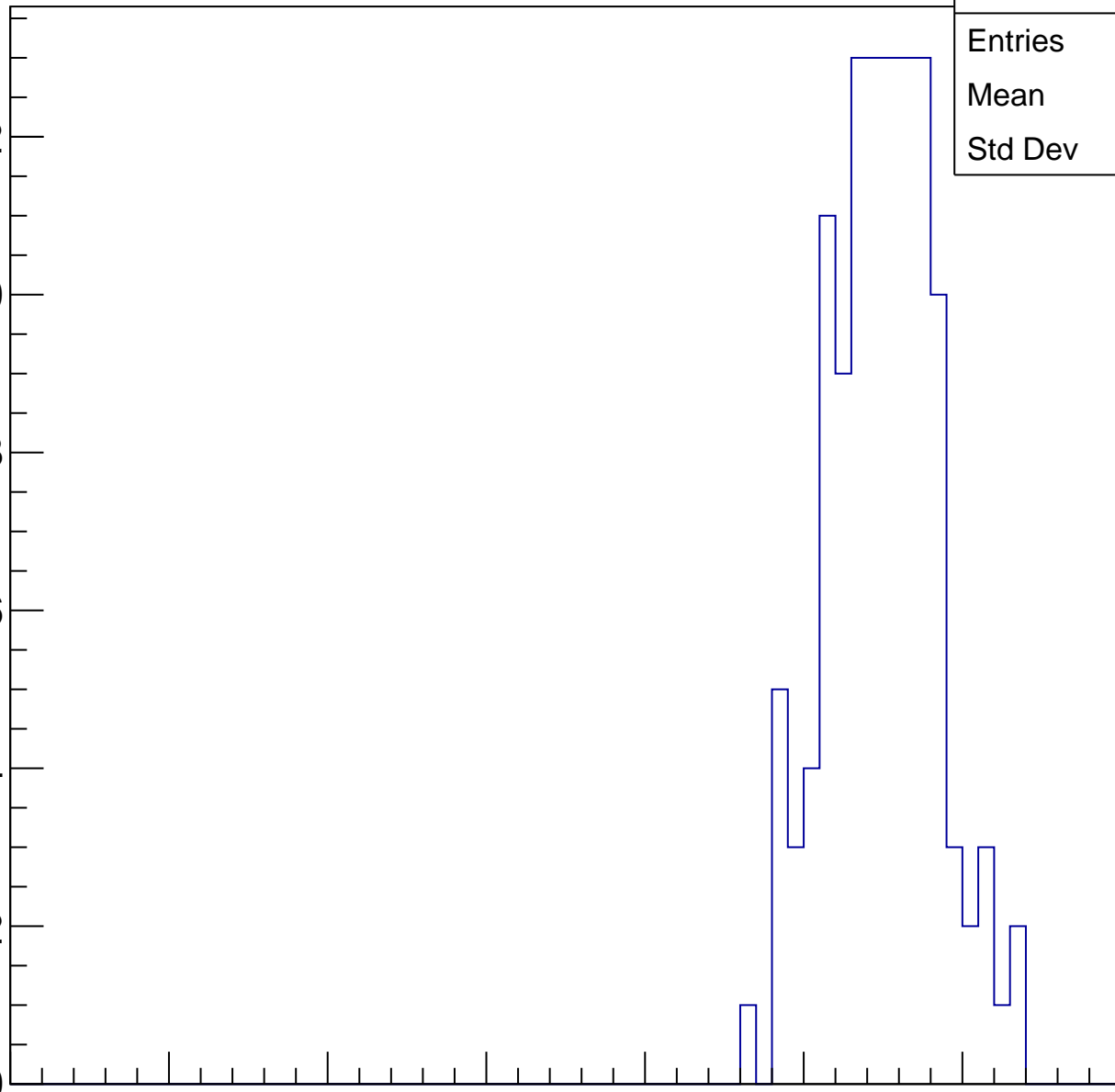
40

50

60

70

ampl



B0L101S, U21-ch51, adc5

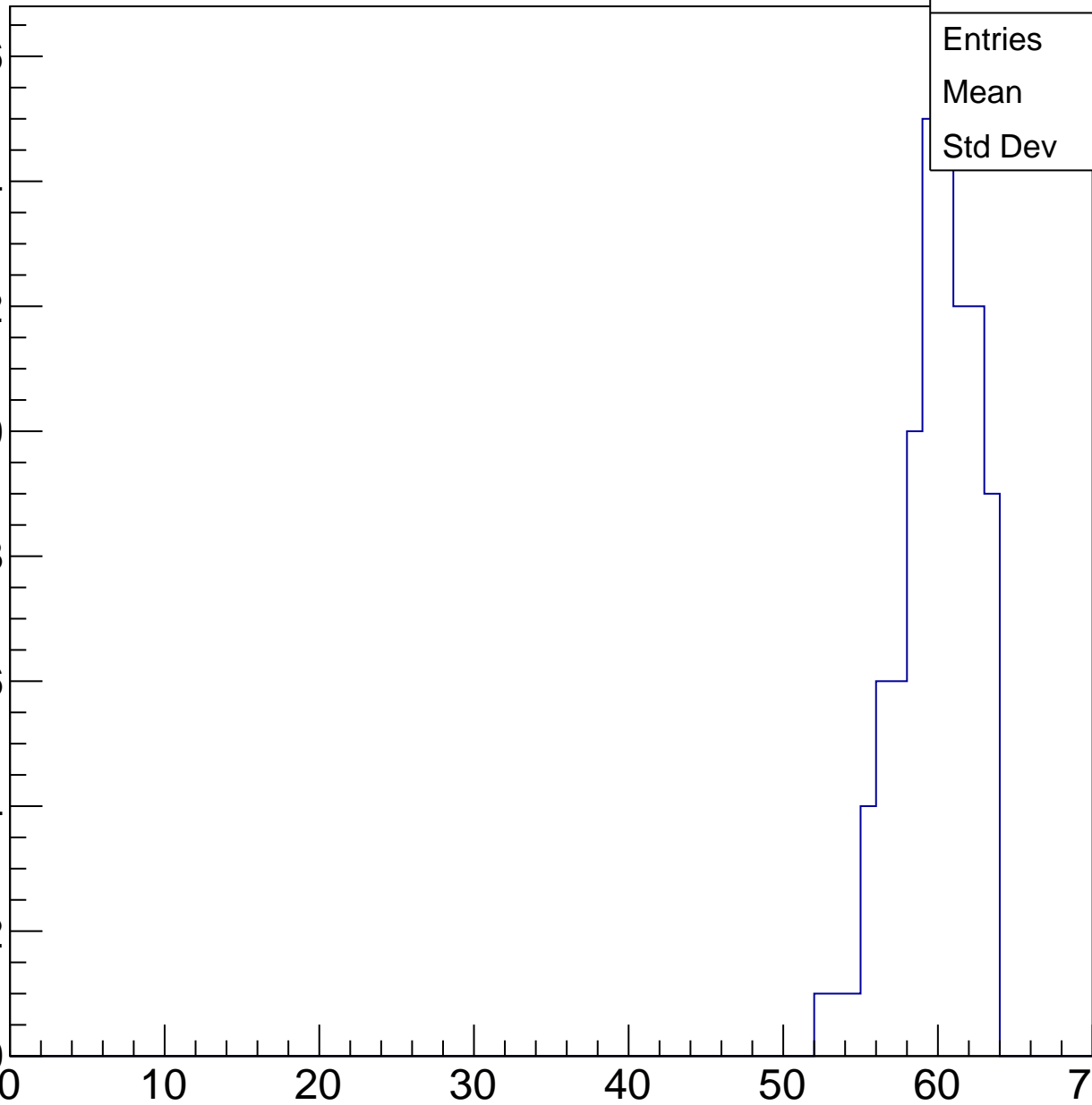
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.41
Std Dev	2.459

ampl



B0L101S, U21-ch51, adc6

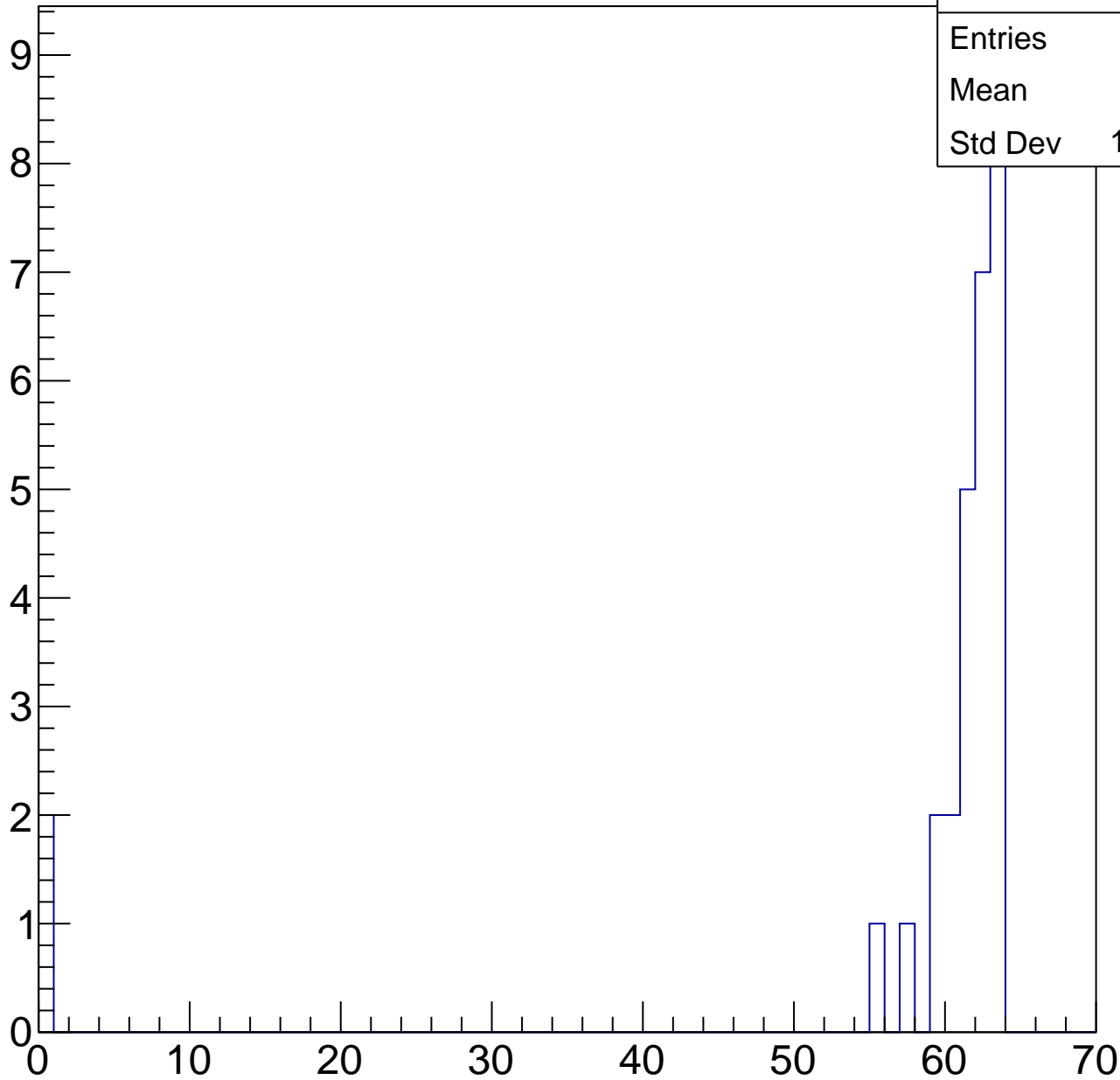
calib_packv5_042523_0143.root, FC#1, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	29
Mean	57.1
Std Dev	15.65

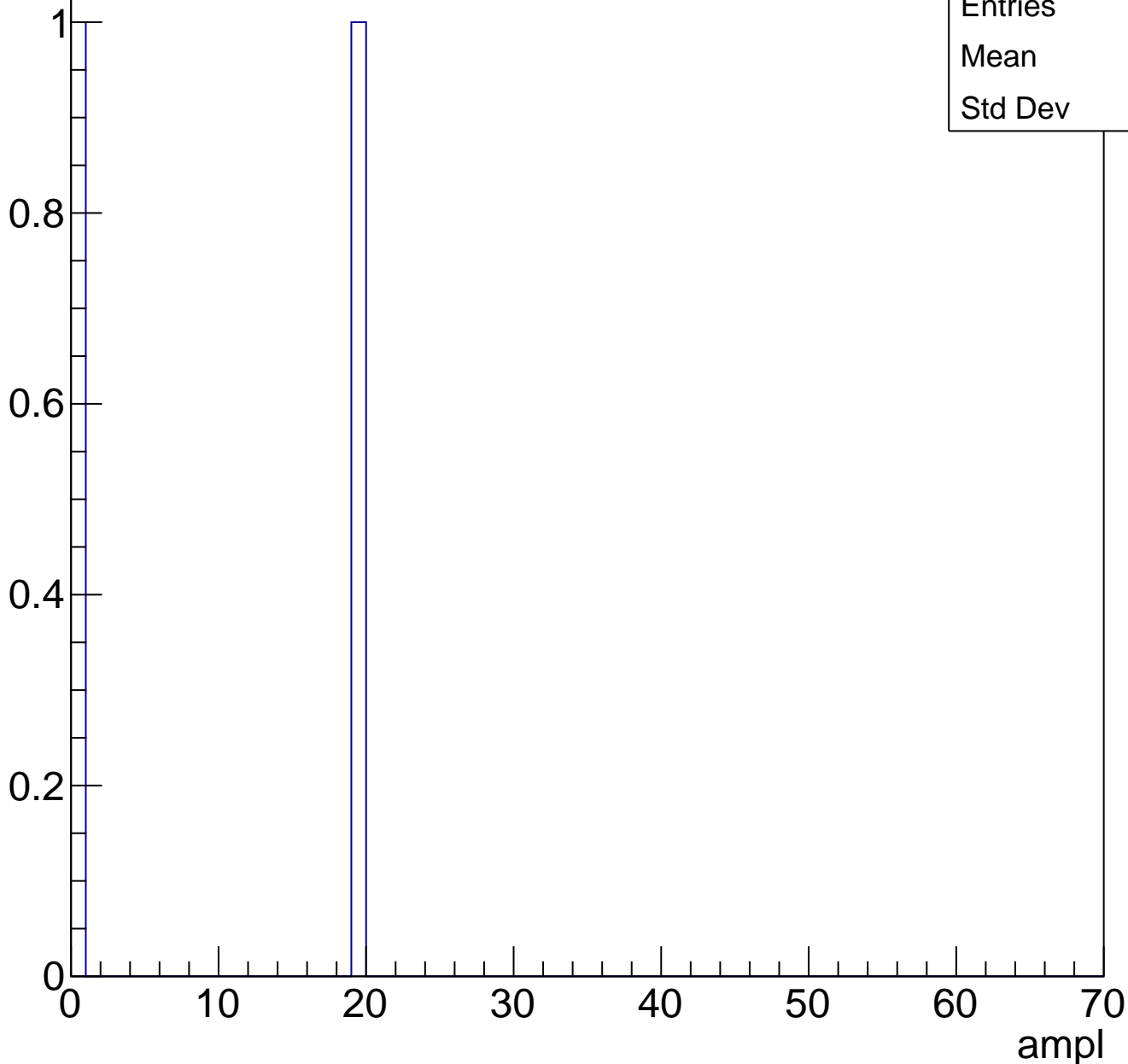
ampl



B0L101S, U21-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch52, adc0

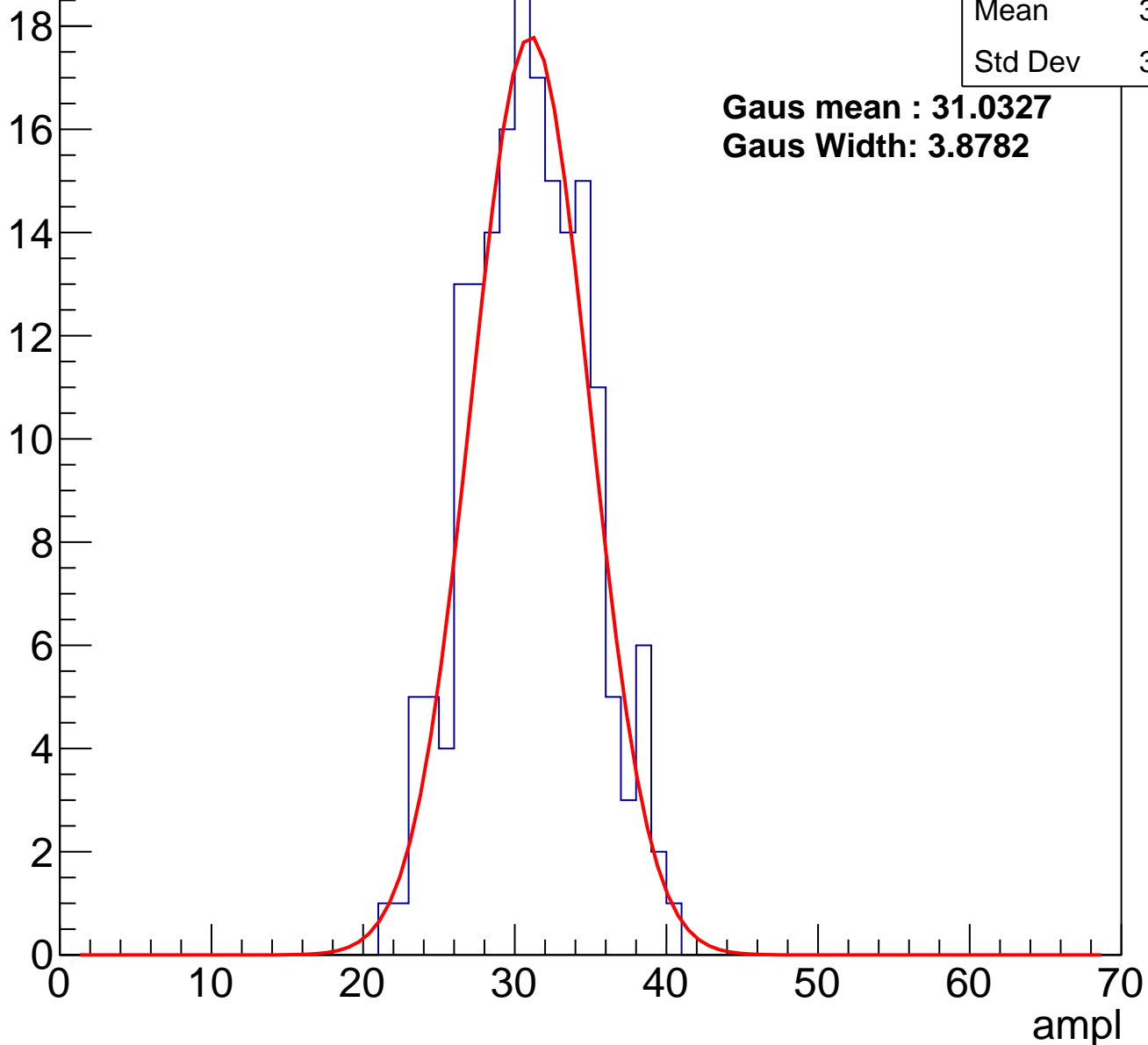
calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	30.52
Std Dev	3.879

Gaus mean : 31.0327

Gaus Width: 3.8782

Entry



B0L101S, U21-ch52, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	38.74
Std Dev	3.743

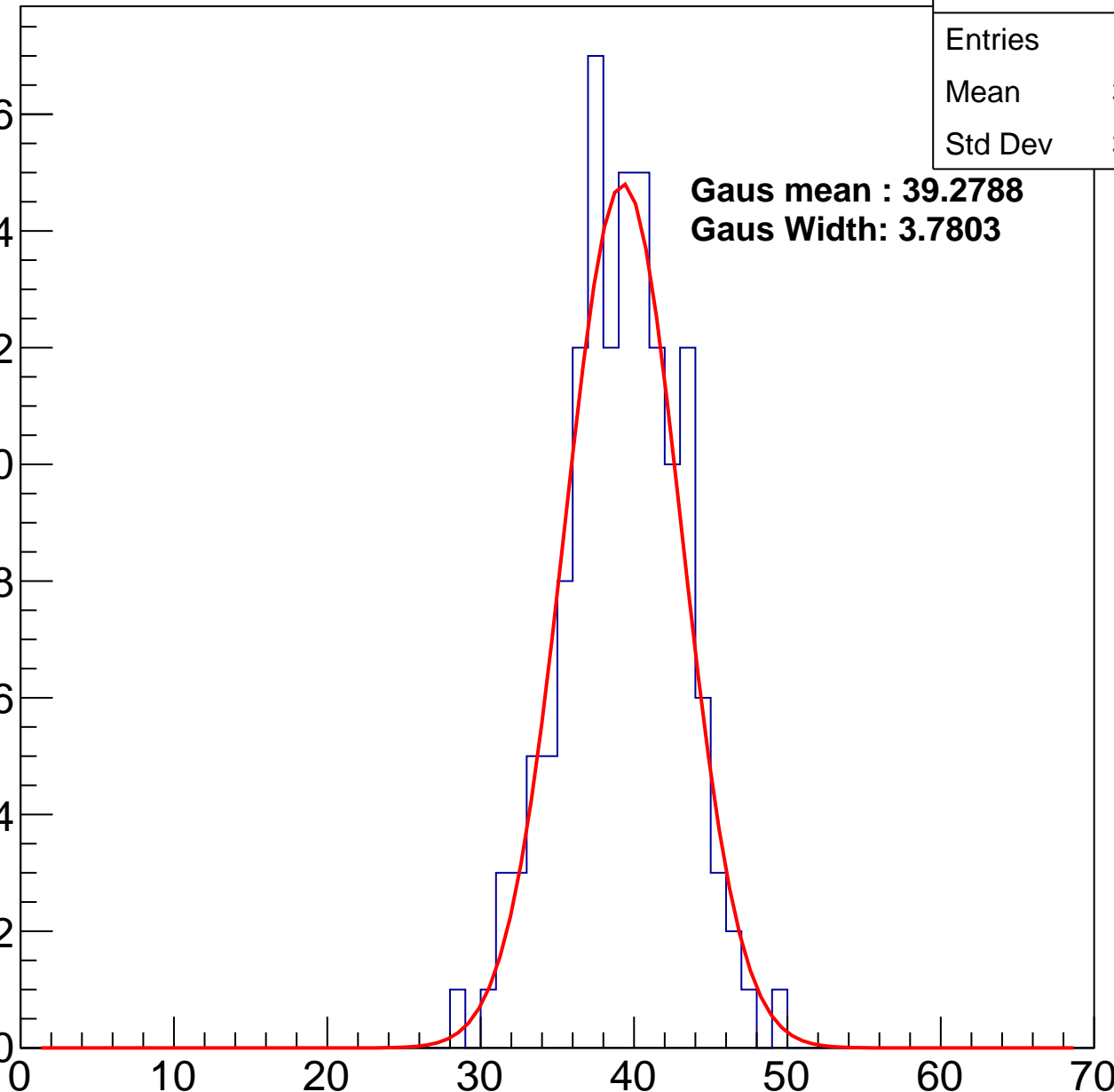
Gaus mean : 39.2788

Gaus Width: 3.7803

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch52, adc2

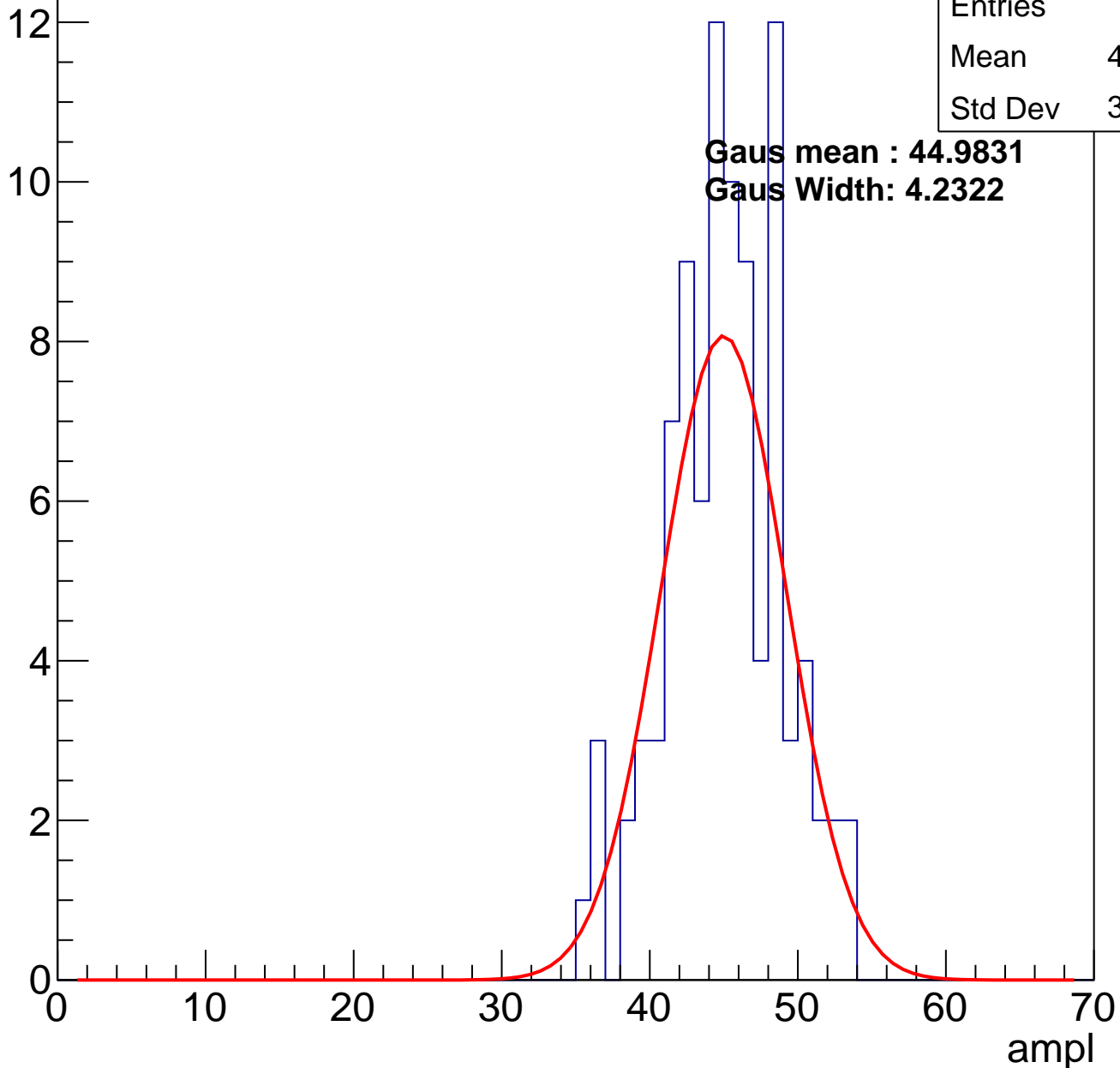
calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	44.62
Std Dev	3.884

Gaus mean : 44.9831

Gaus Width: 4.2322

Entry



B0L101S, U21-ch52, adc3

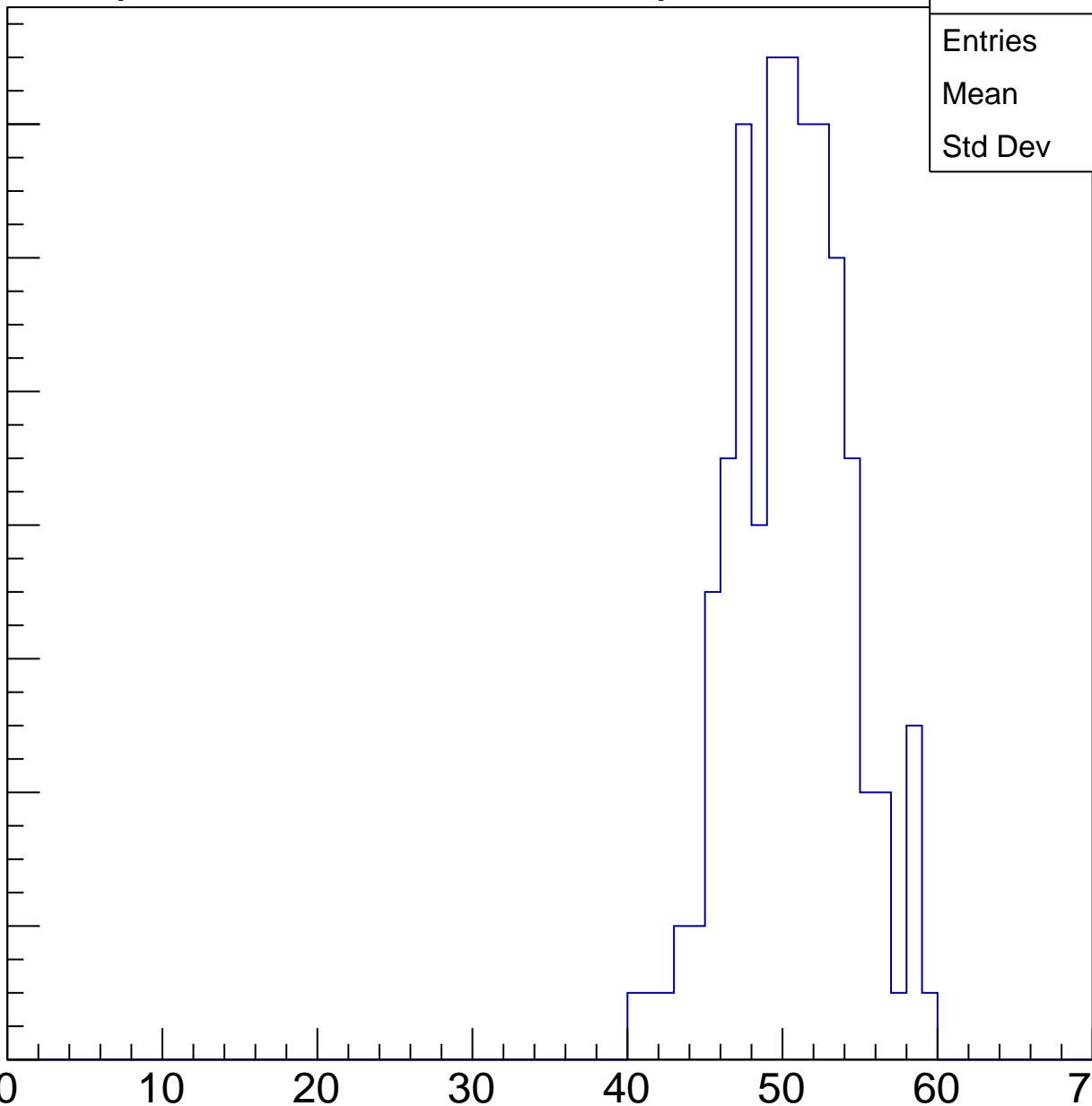
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	139
Mean	50.12
Std Dev	3.717

ampl



B0L101S, U21-ch52, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

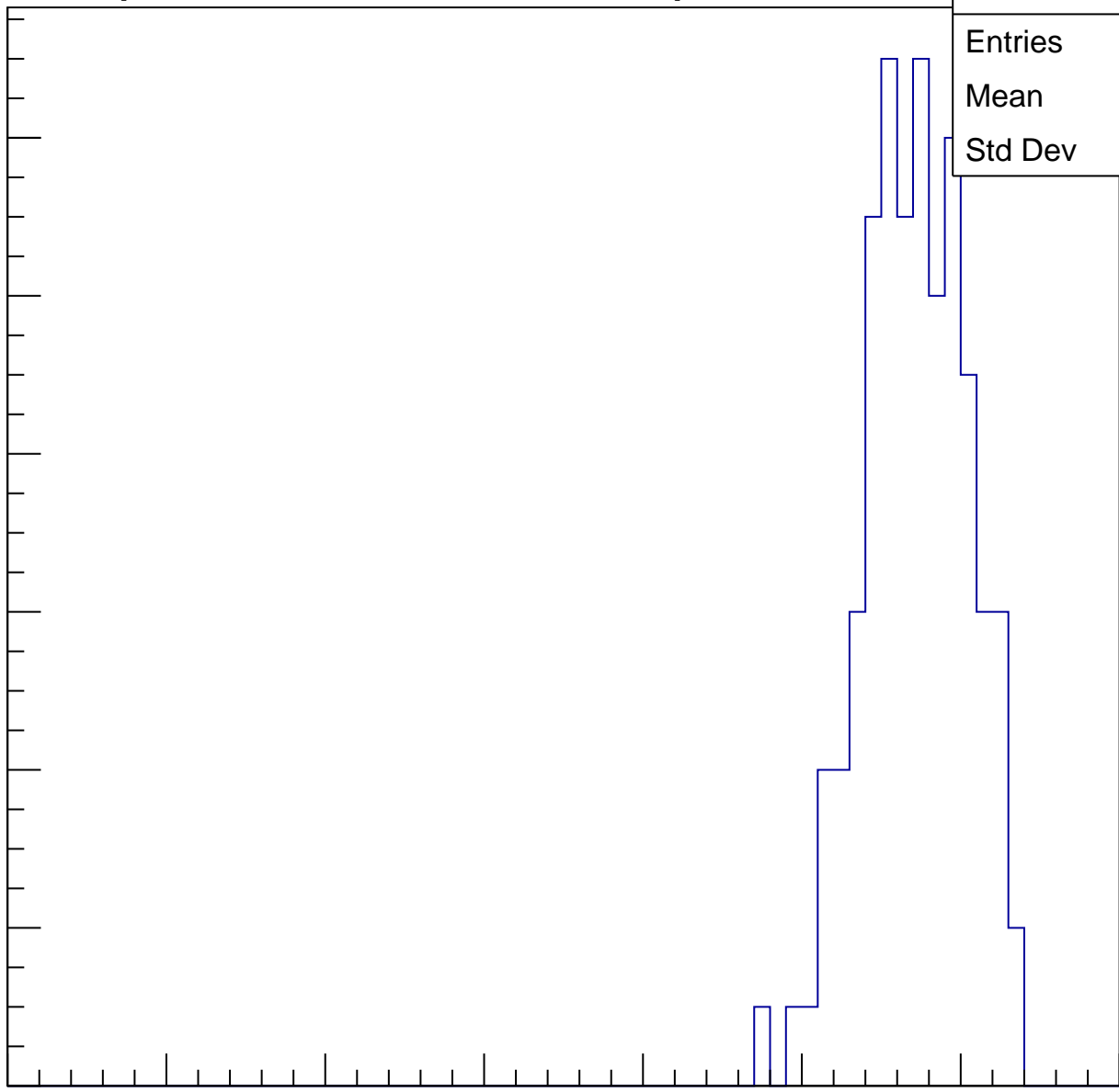
50

60

70

ampl

Entries	110
Mean	56.67
Std Dev	3.248



B0L101S, U21-ch52, adc5

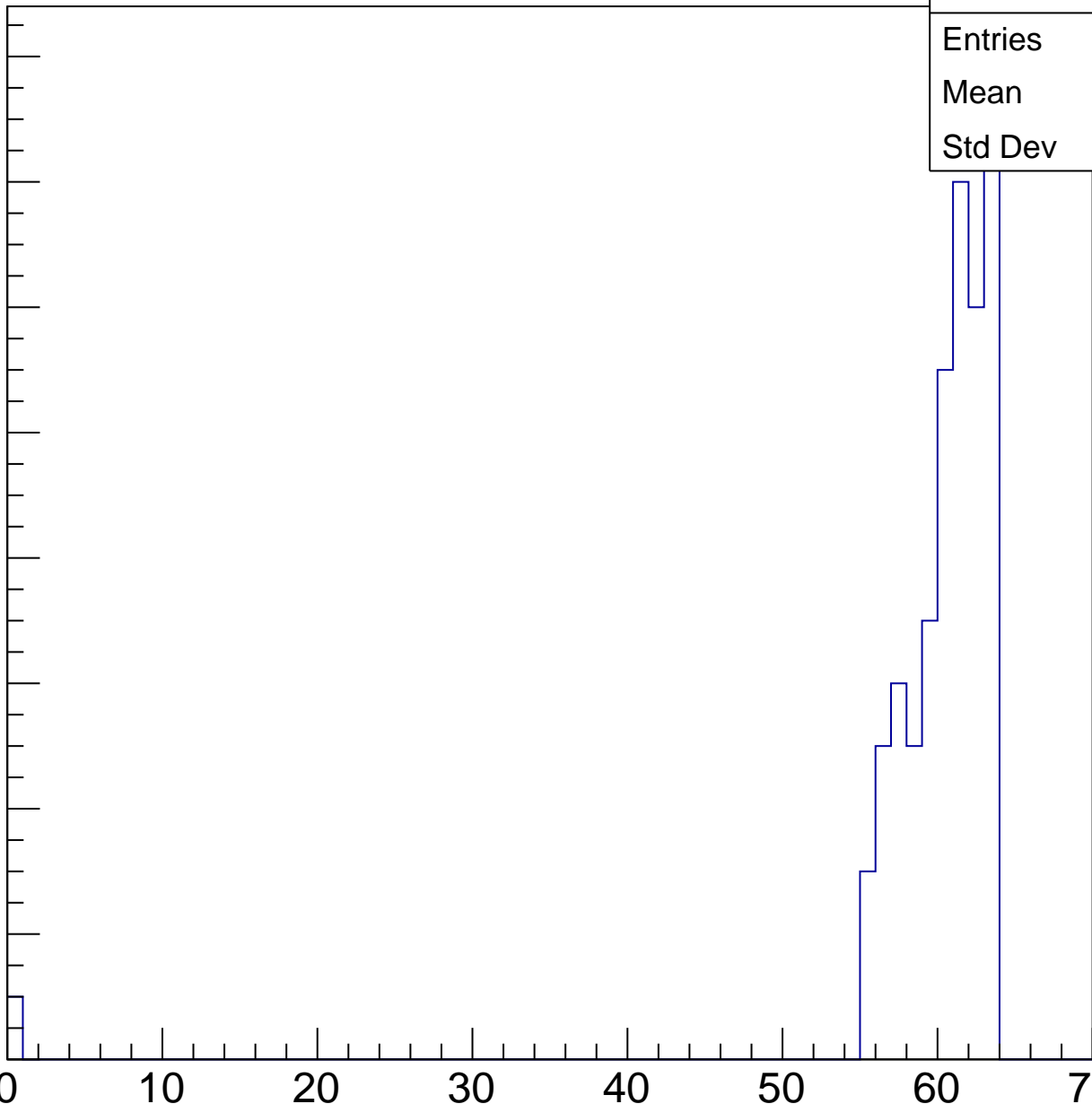
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.45
Std Dev	7.087

ampl



B0L101S, U21-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	50
Std Dev	25

ampl

B0L101S, U21-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

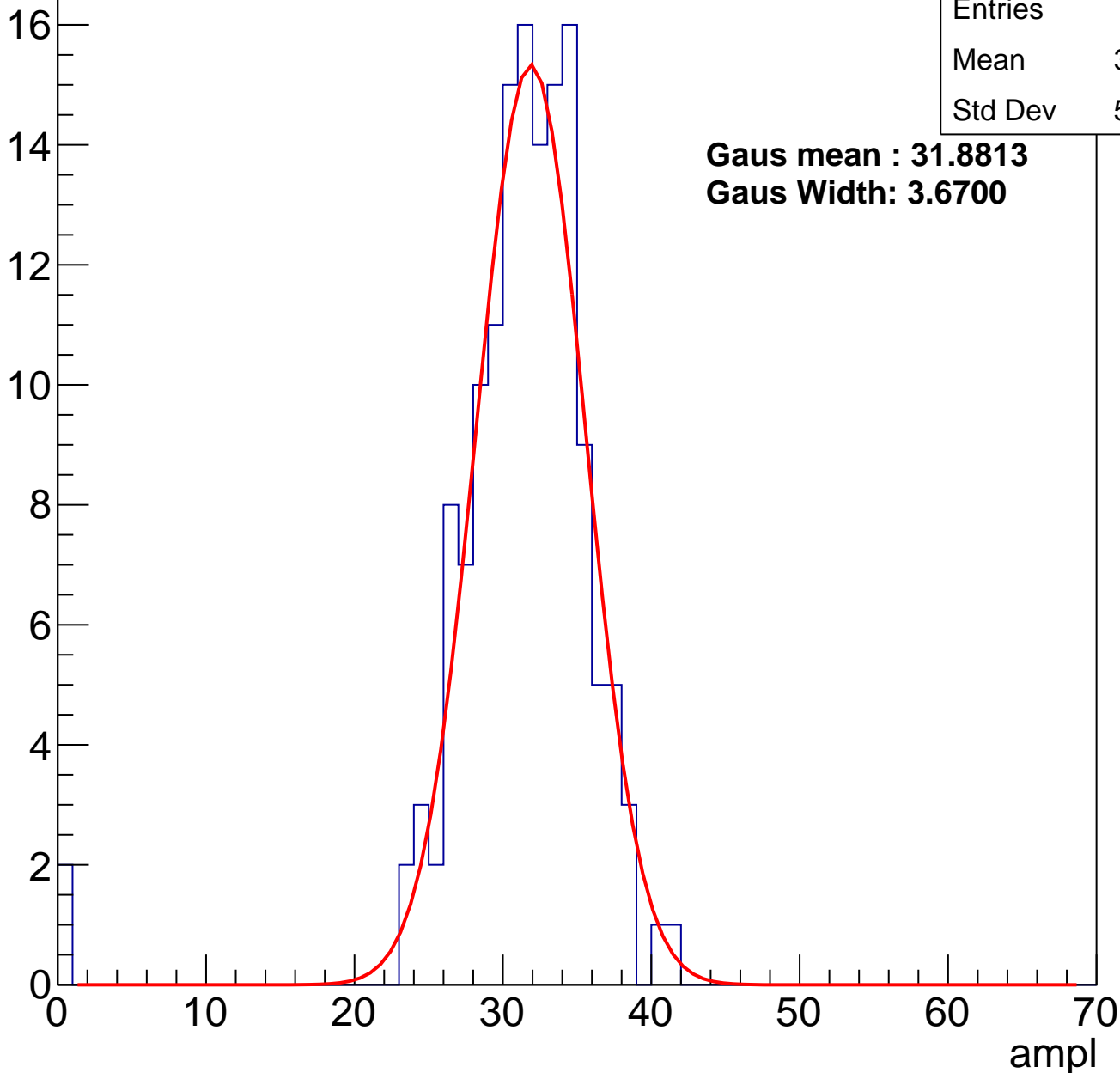


Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch53, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch53, adc1

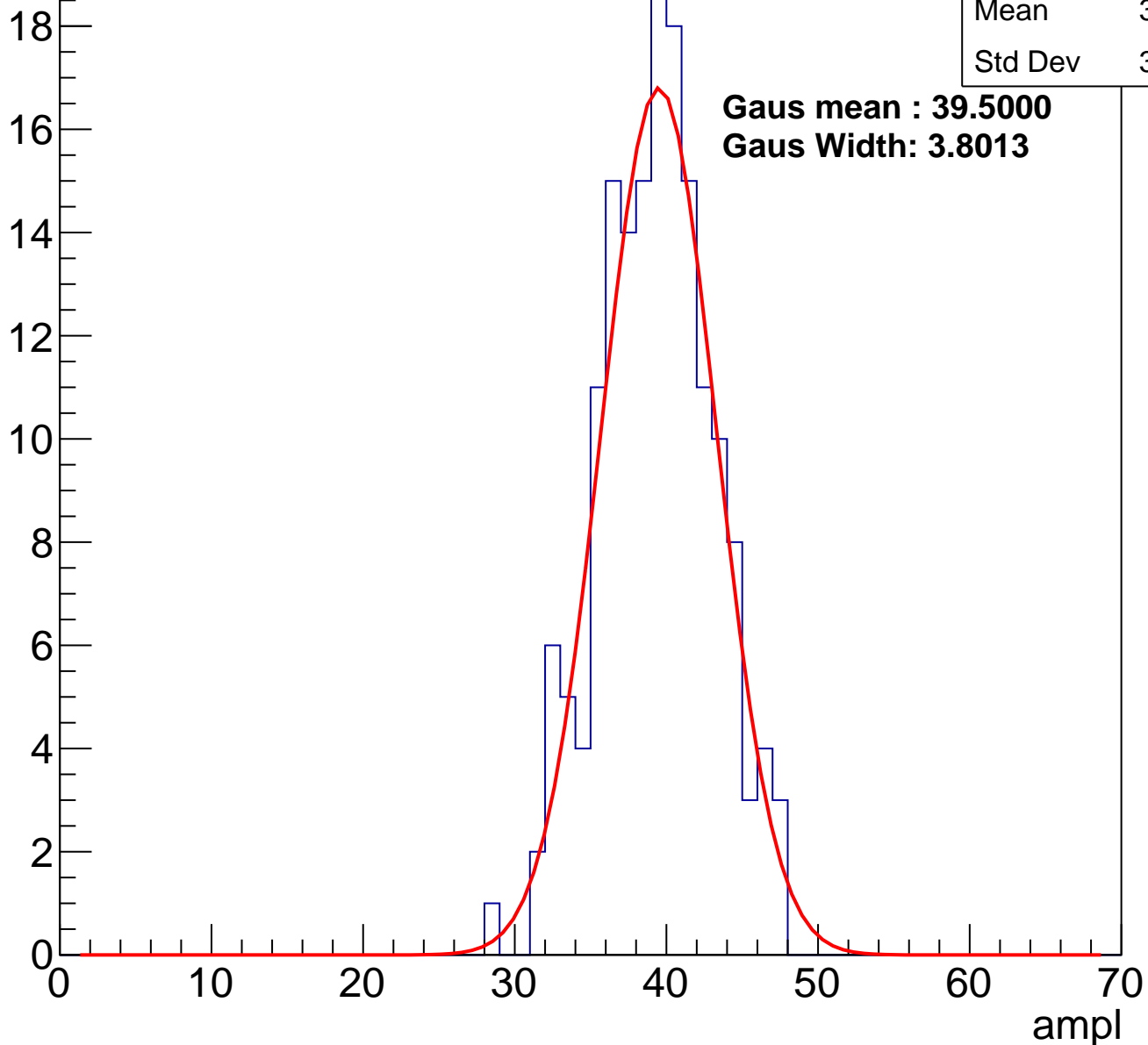
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	38.88
Std Dev	3.692

Gaus mean : 39.5000

Gaus Width: 3.8013

Entry



B0L101S, U21-ch53, adc2

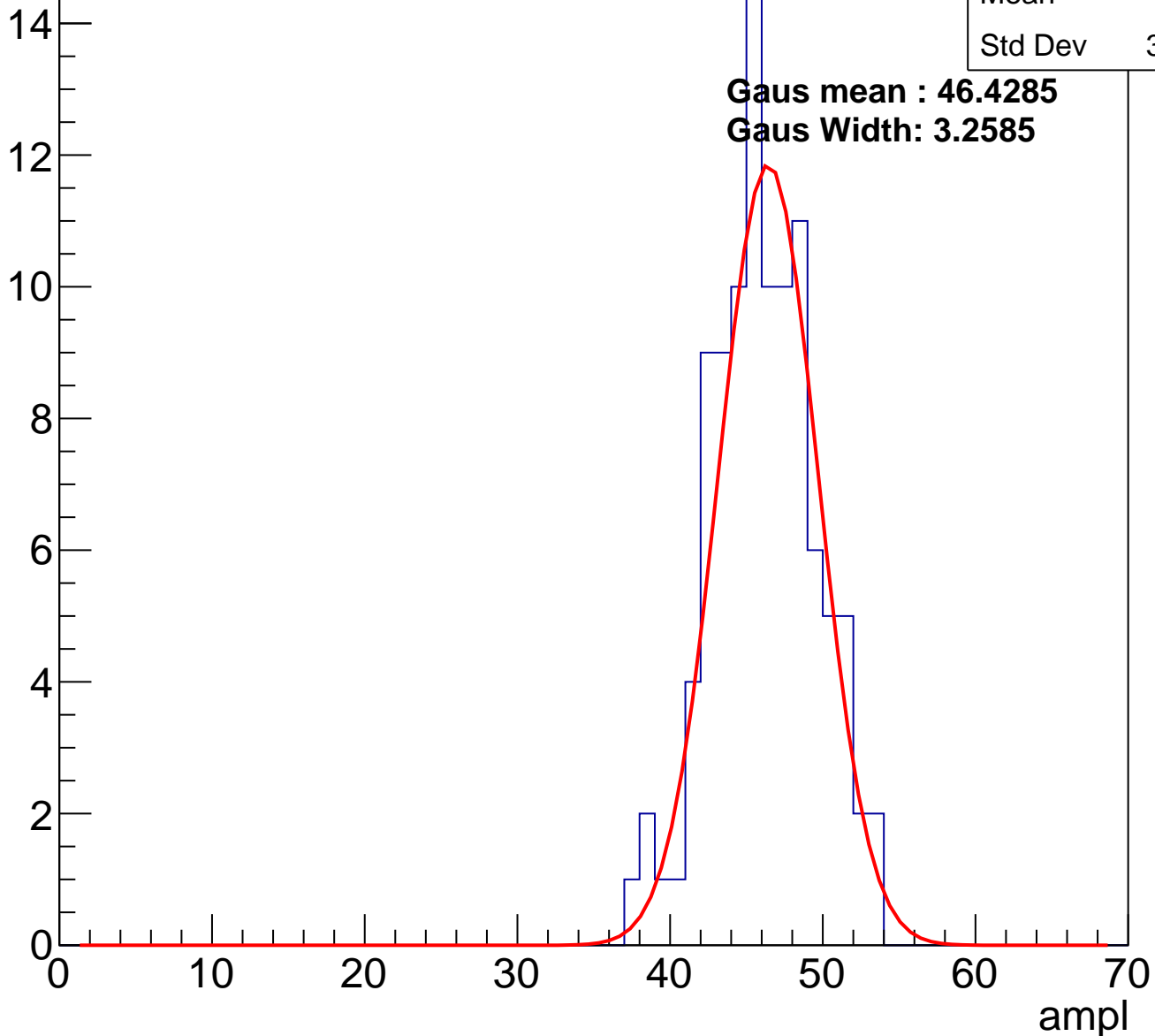
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	45.66
Std Dev	3.334

Gaus mean : 46.4285

Gaus Width: 3.2585

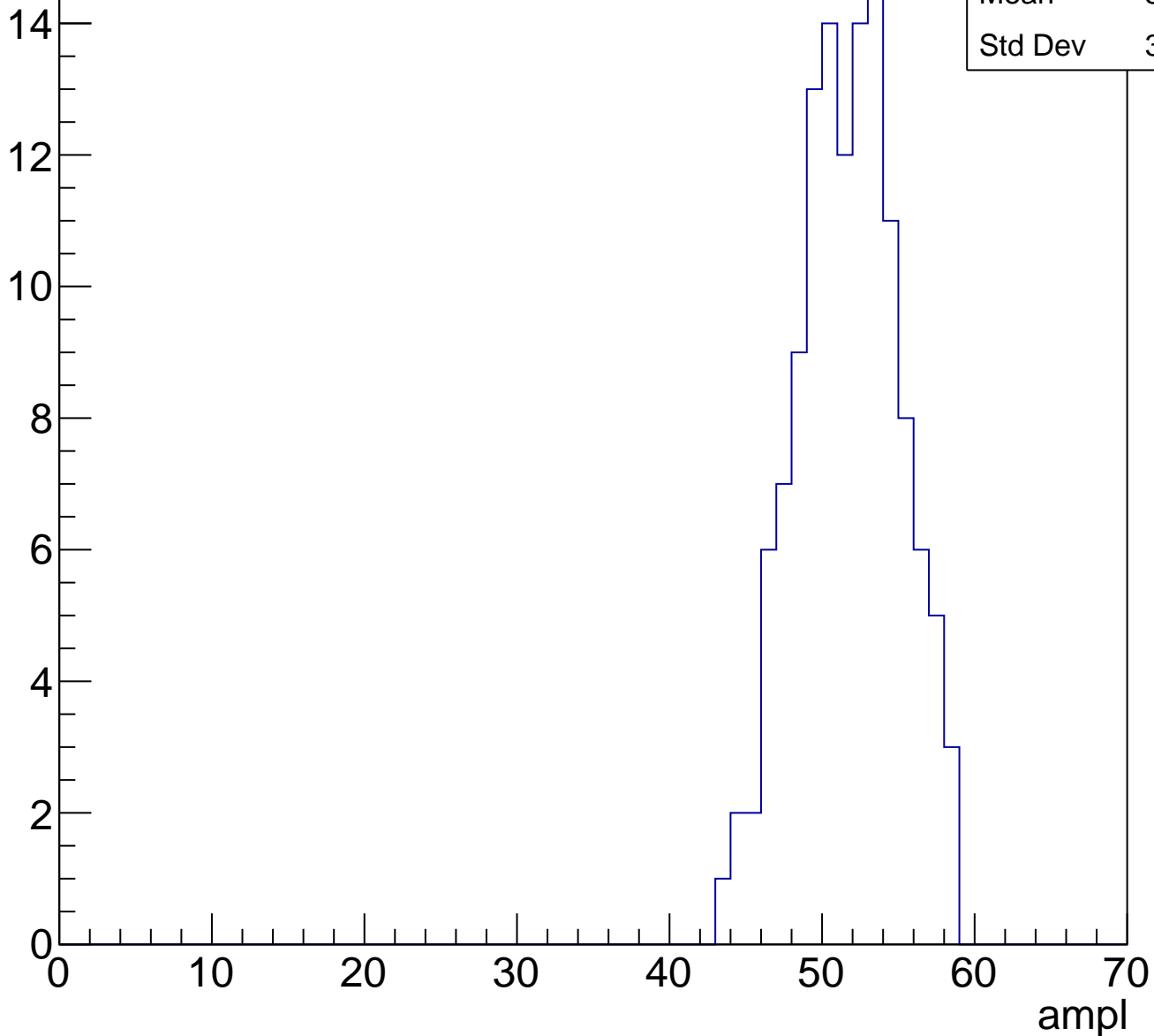
Entry



B0L101S, U21-ch53, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch53, adc4

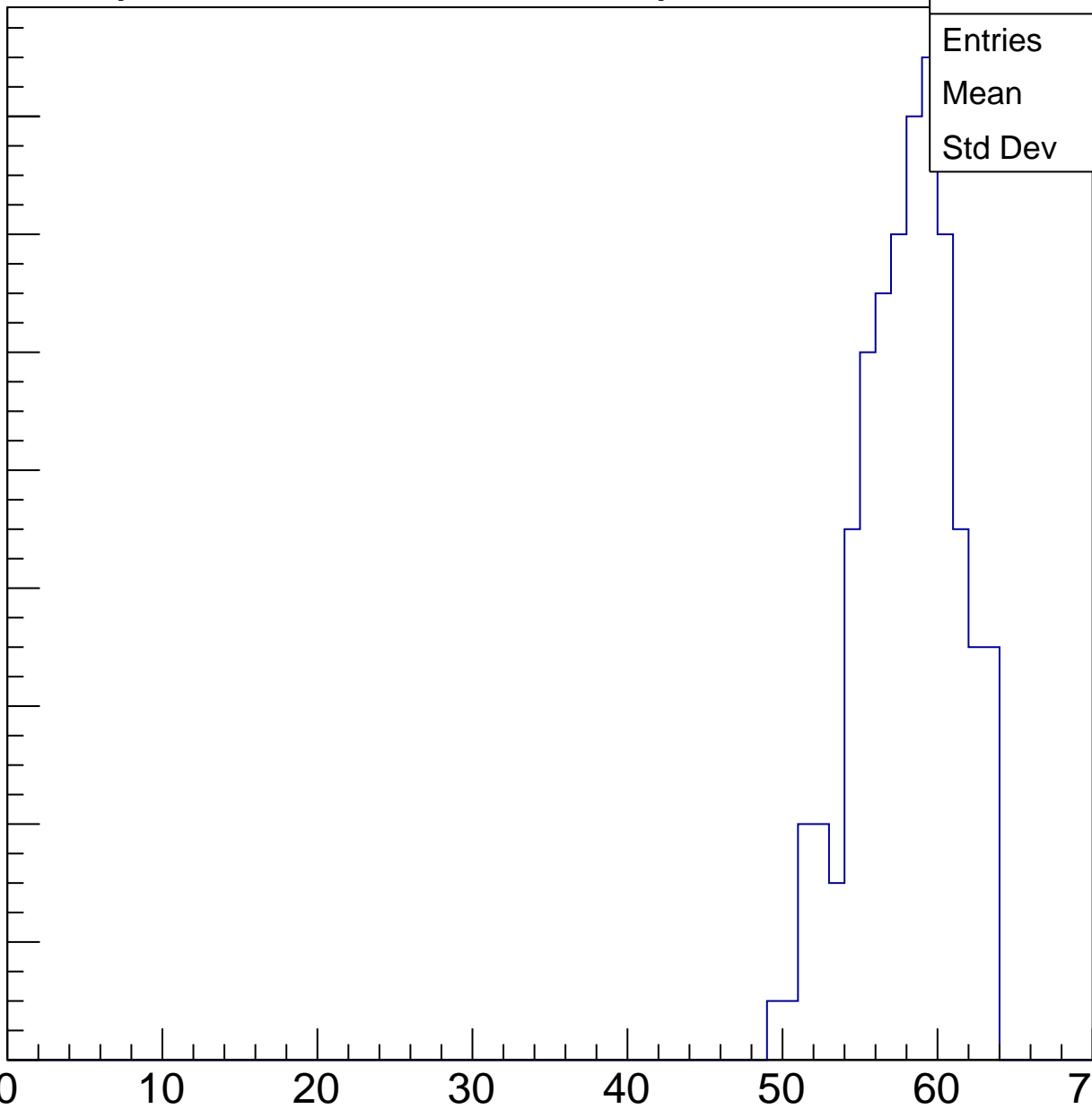
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	131
Mean	57.53
Std Dev	3.139

ampl



B0L101S, U21-ch53, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

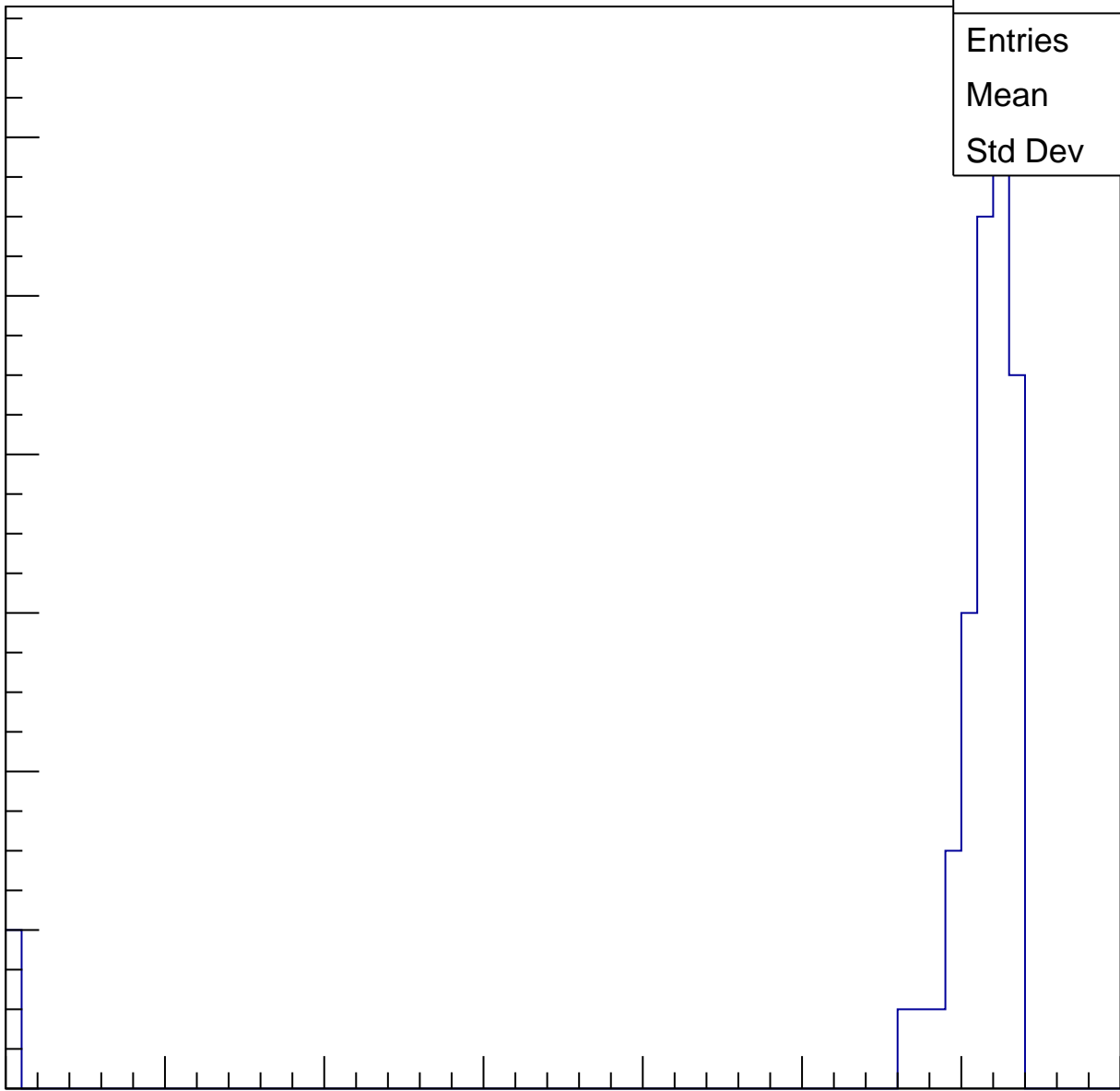
2

0

Entries	47
Mean	58.55
Std Dev	12.44

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch54, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	92
Mean	27.61
Std Dev	2.69

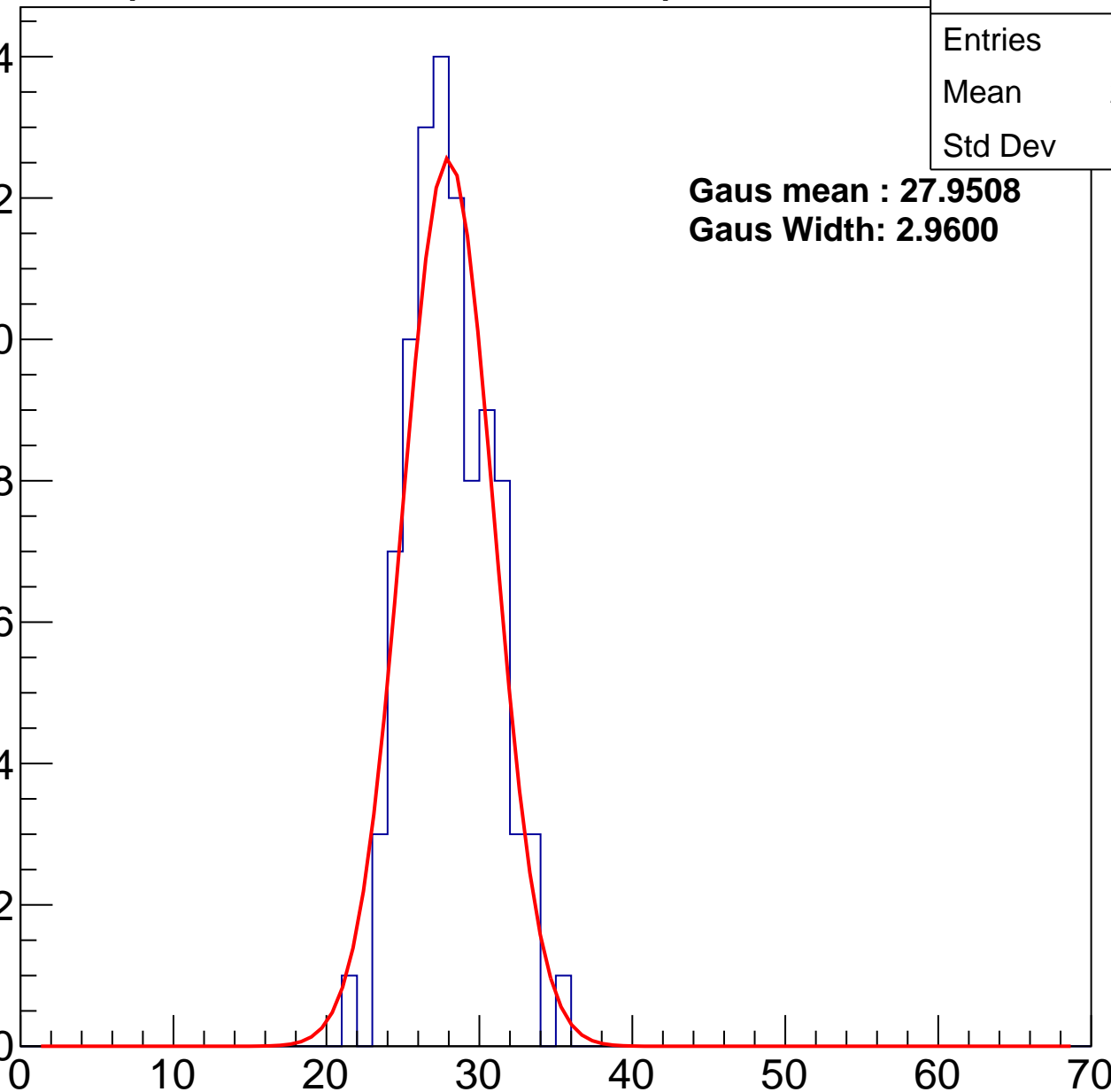
Gaus mean : 27.9508

Gaus Width: 2.9600

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch54, adc1

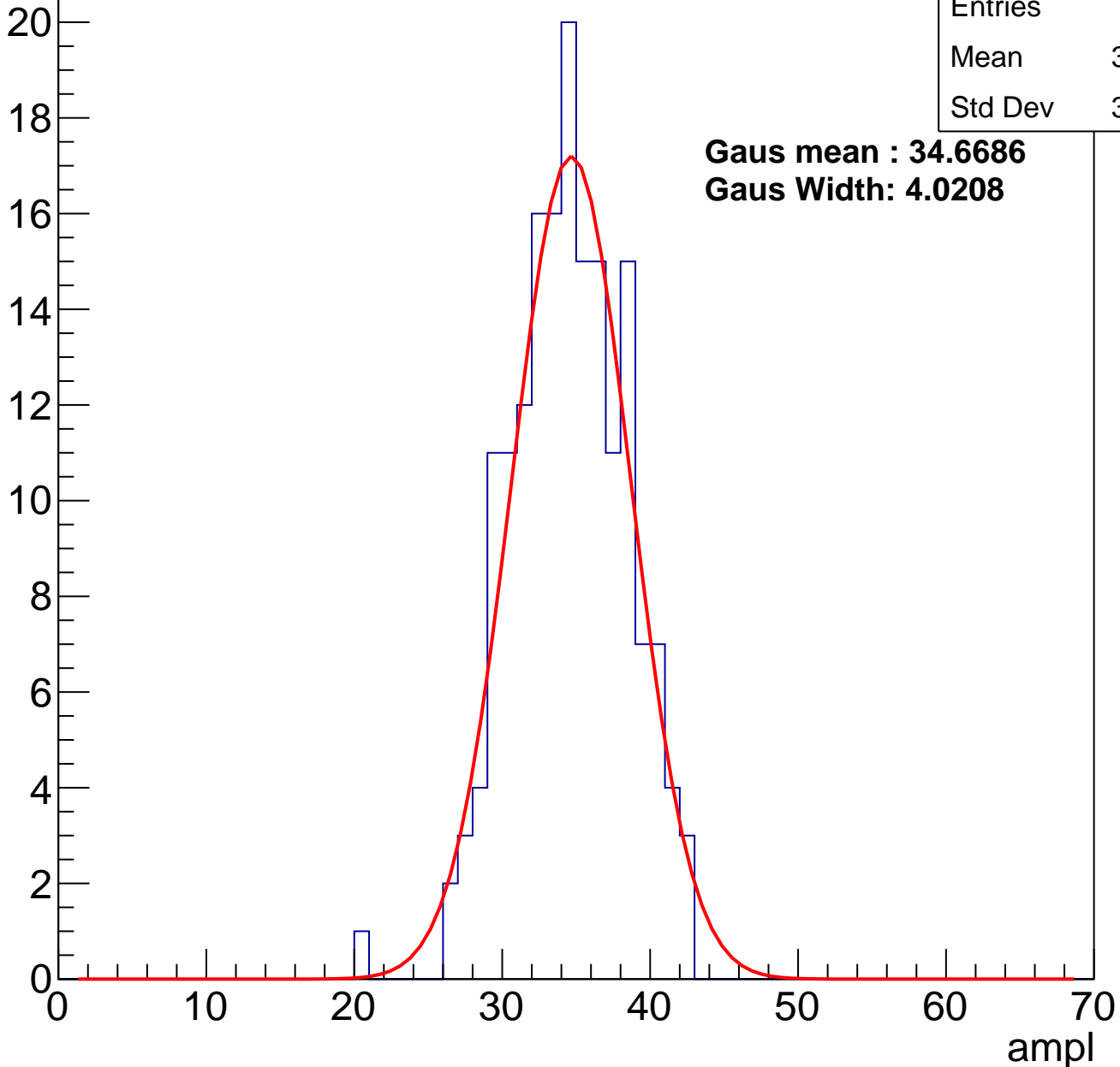
calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	34.05
Std Dev	3.788

Gaus mean : 34.6686

Gaus Width: 4.0208

Entry



B0L101S, U21-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	41.8
Std Dev	3.487

Gaus mean : 42.2645

Gaus Width: 3.5773

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

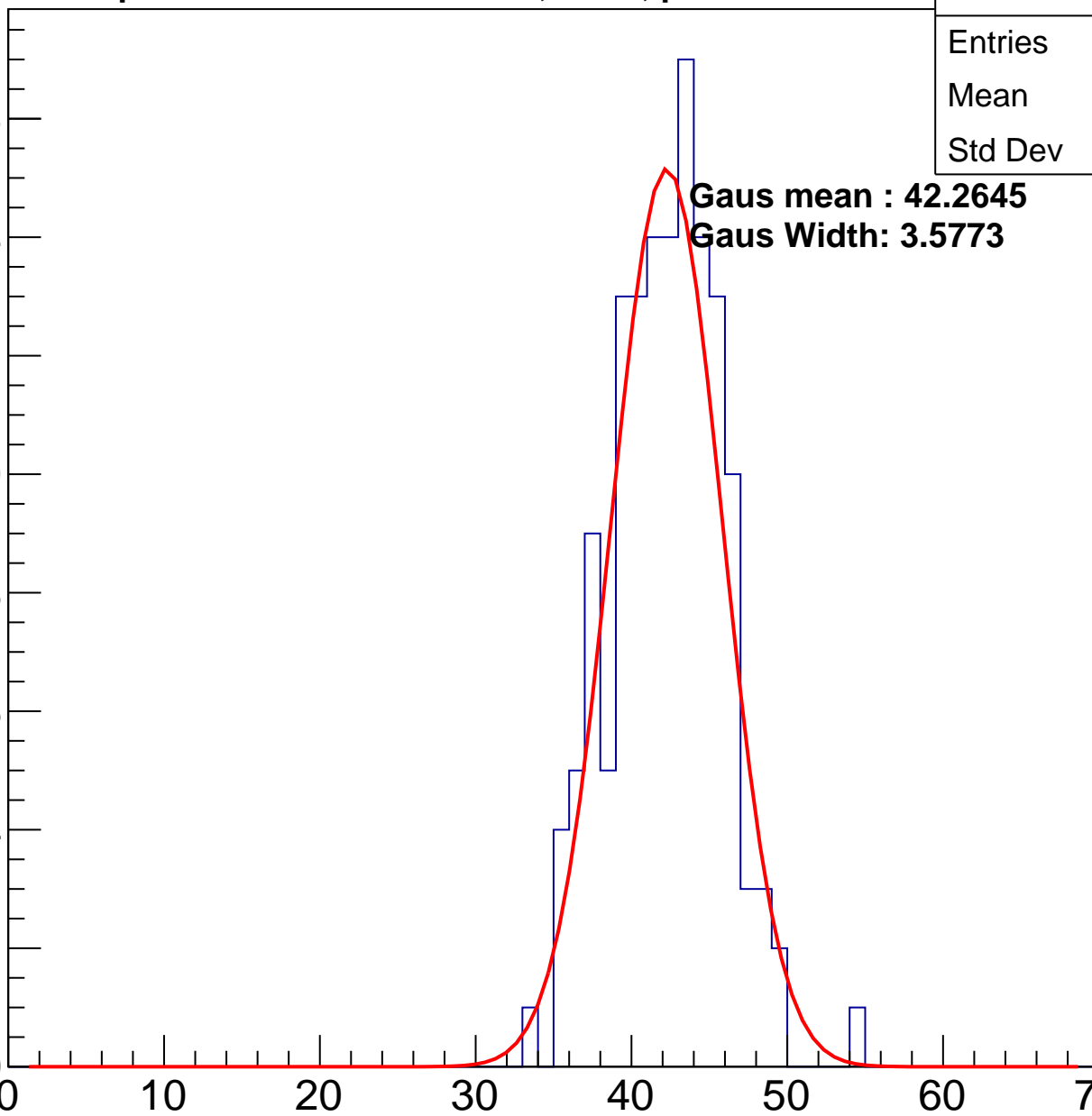
40

50

60

70

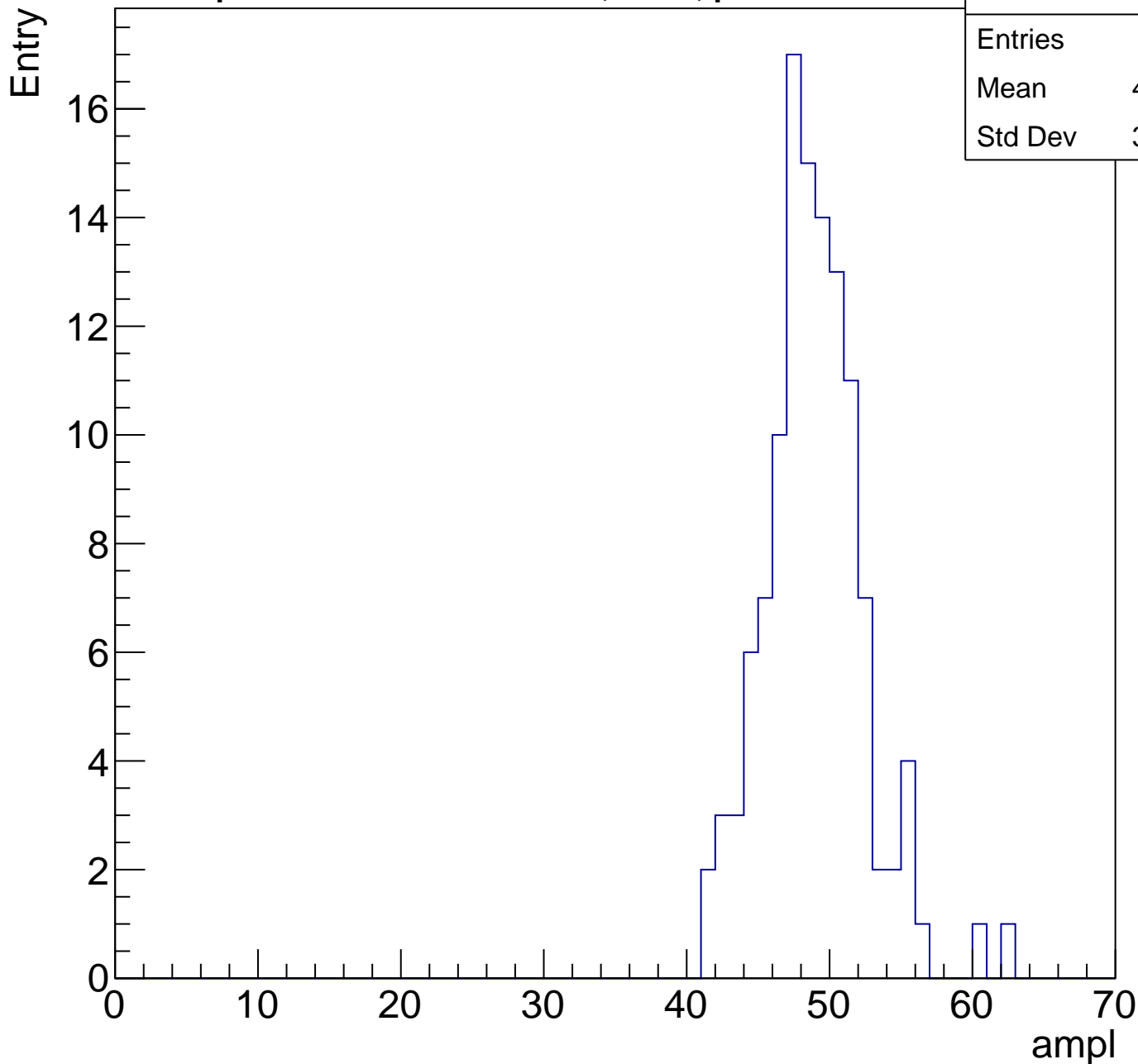
ampl



B0L101S, U21-ch54, adc3

calib_packv5_042523_0143.root, FC#1, port C1

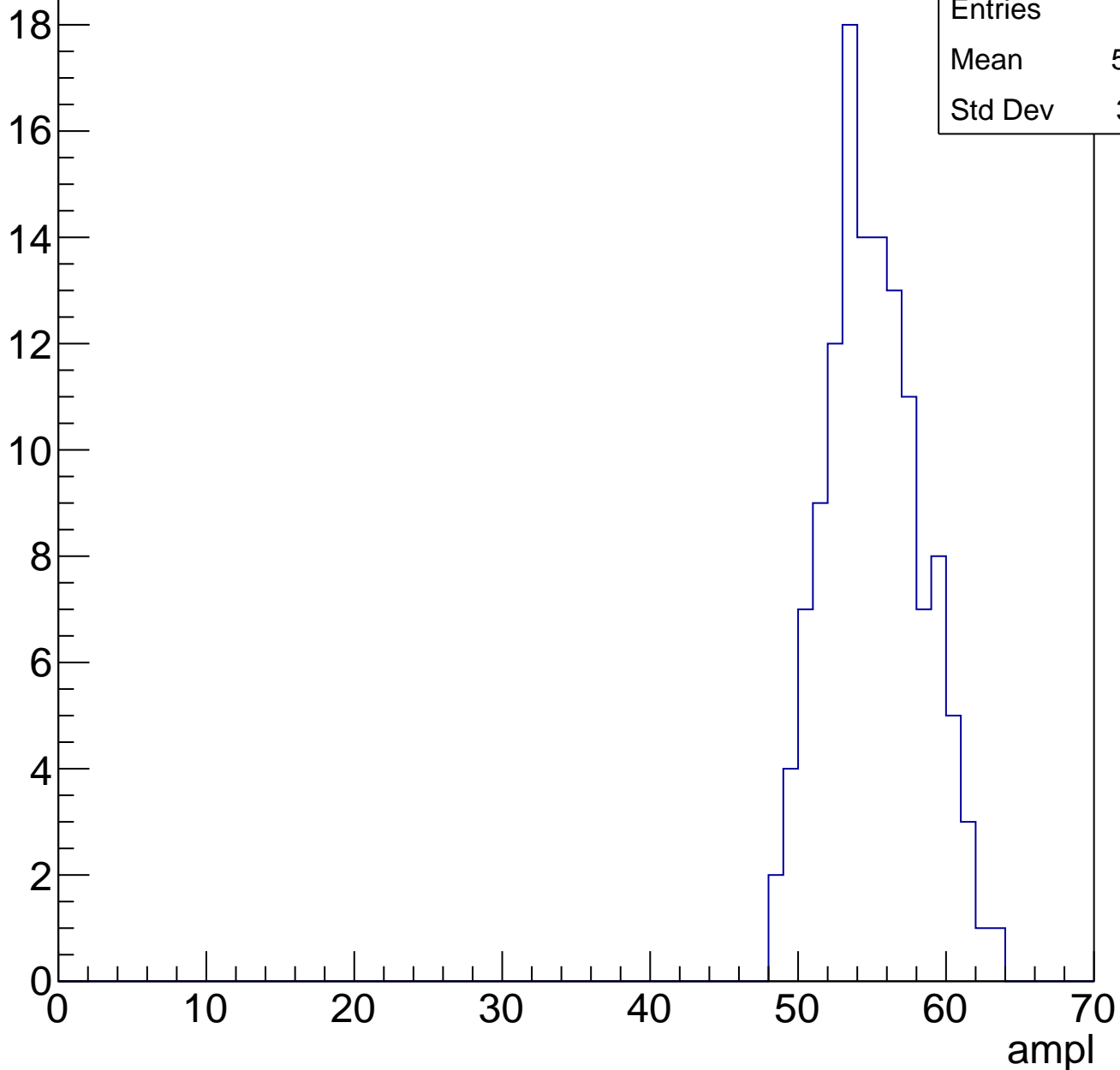
Entries	119
Mean	48.47
Std Dev	3.514



B0L101S, U21-ch54, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch54, adc5

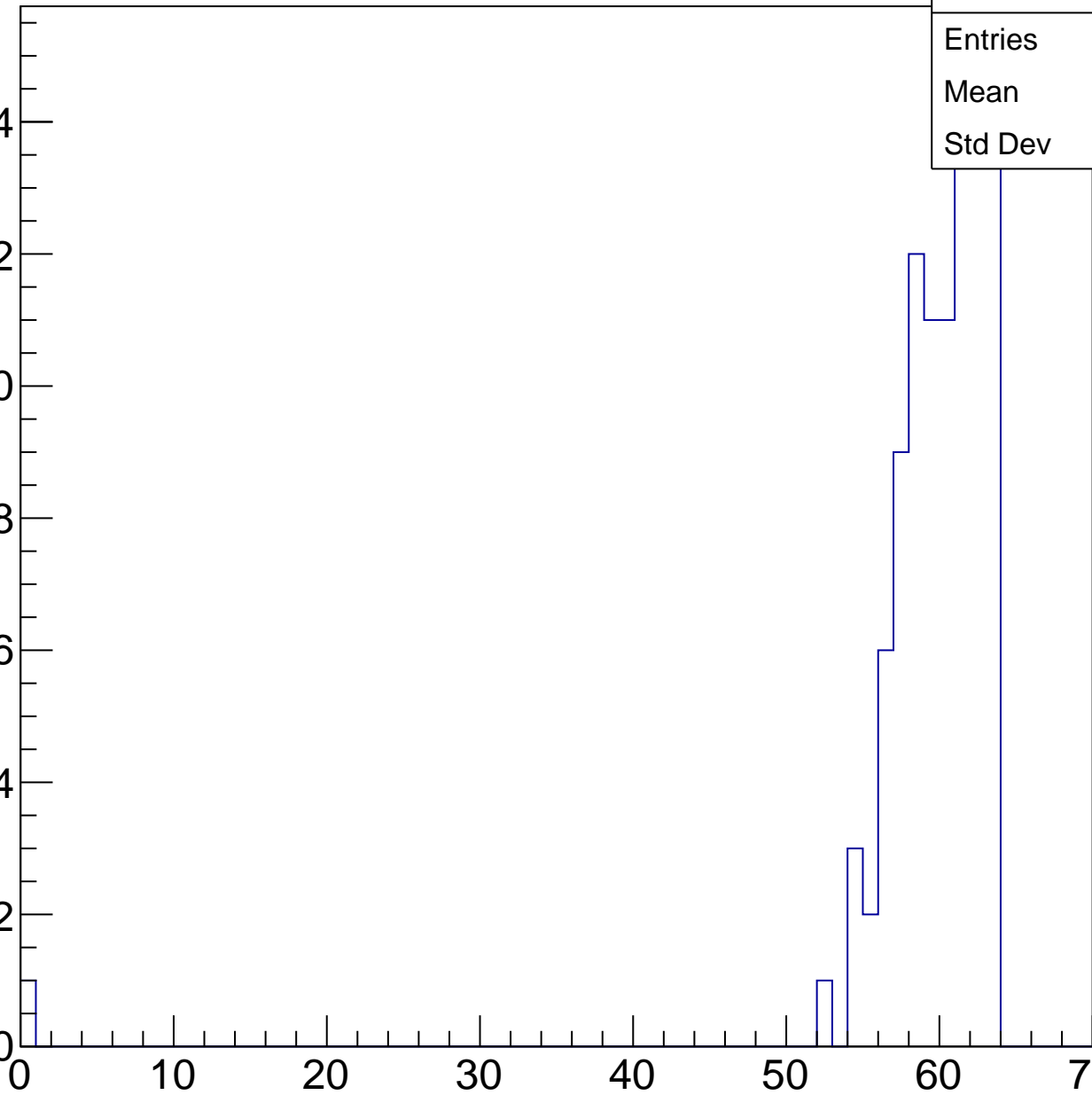
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	59.06
Std Dev	6.462

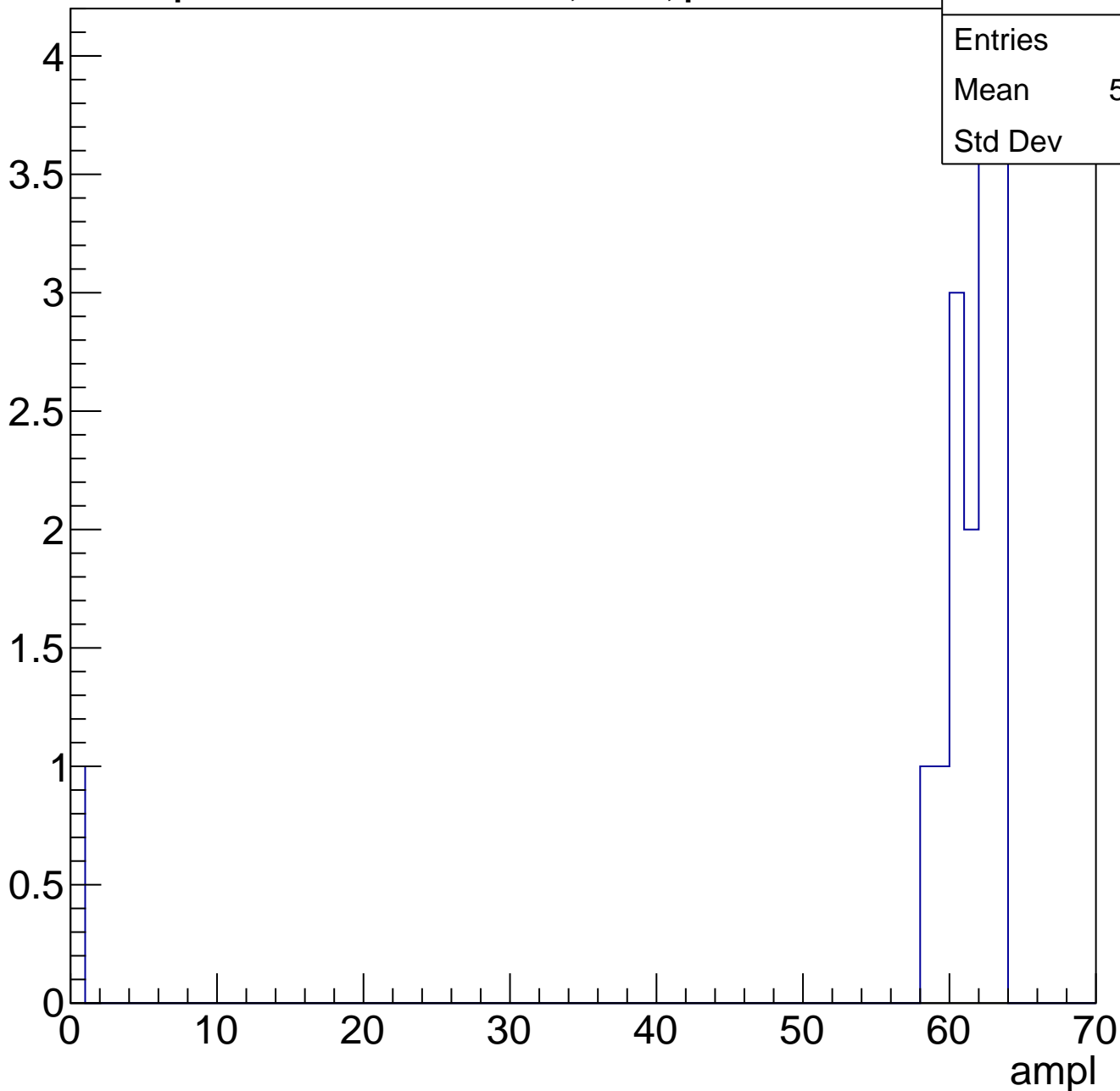
ampl



B0L101S, U21-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch55, adc0

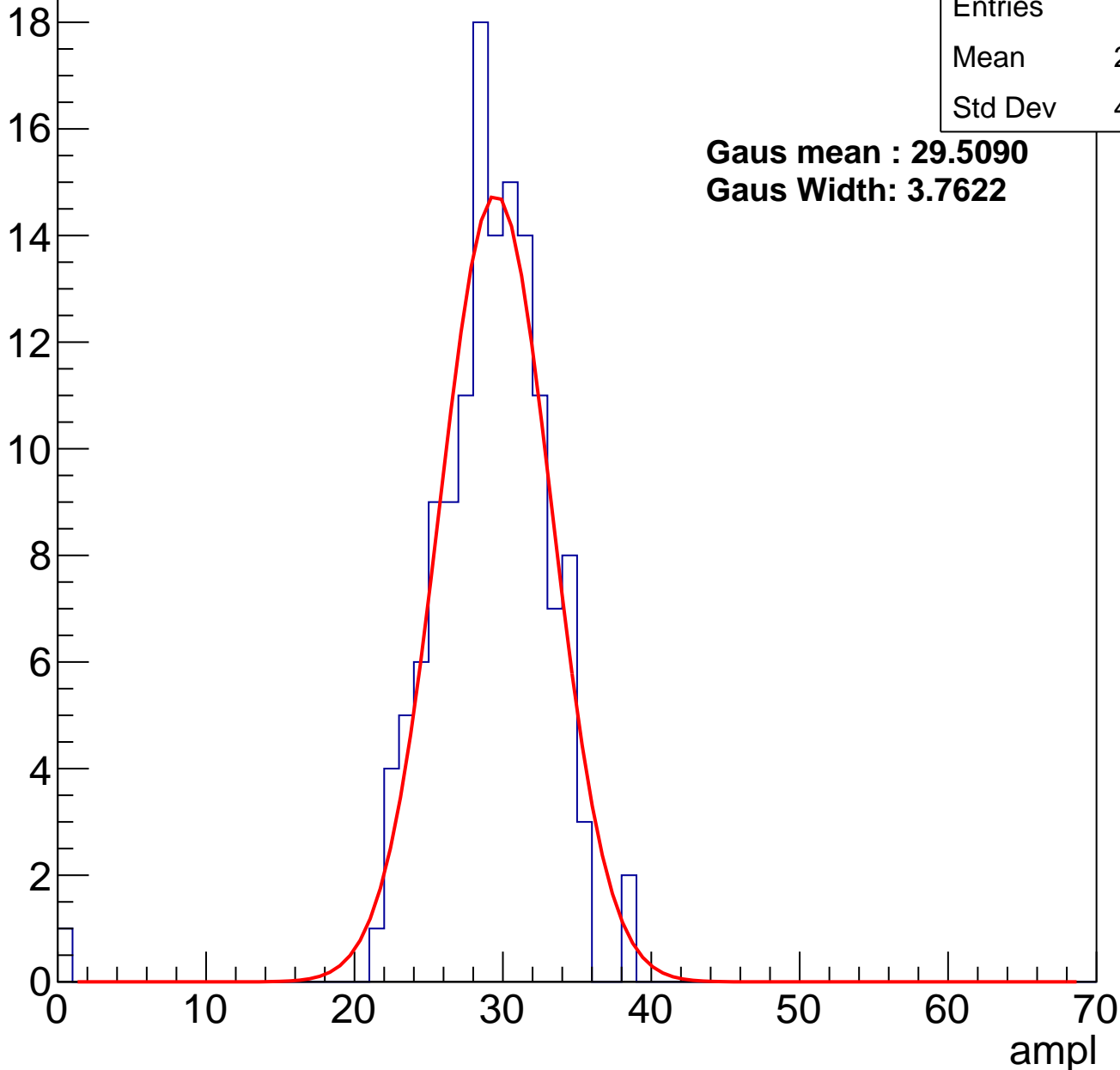
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.65
Std Dev	4.216

Gaus mean : 29.5090

Gaus Width: 3.7622

Entry



B0L101S, U21-ch55, adc1

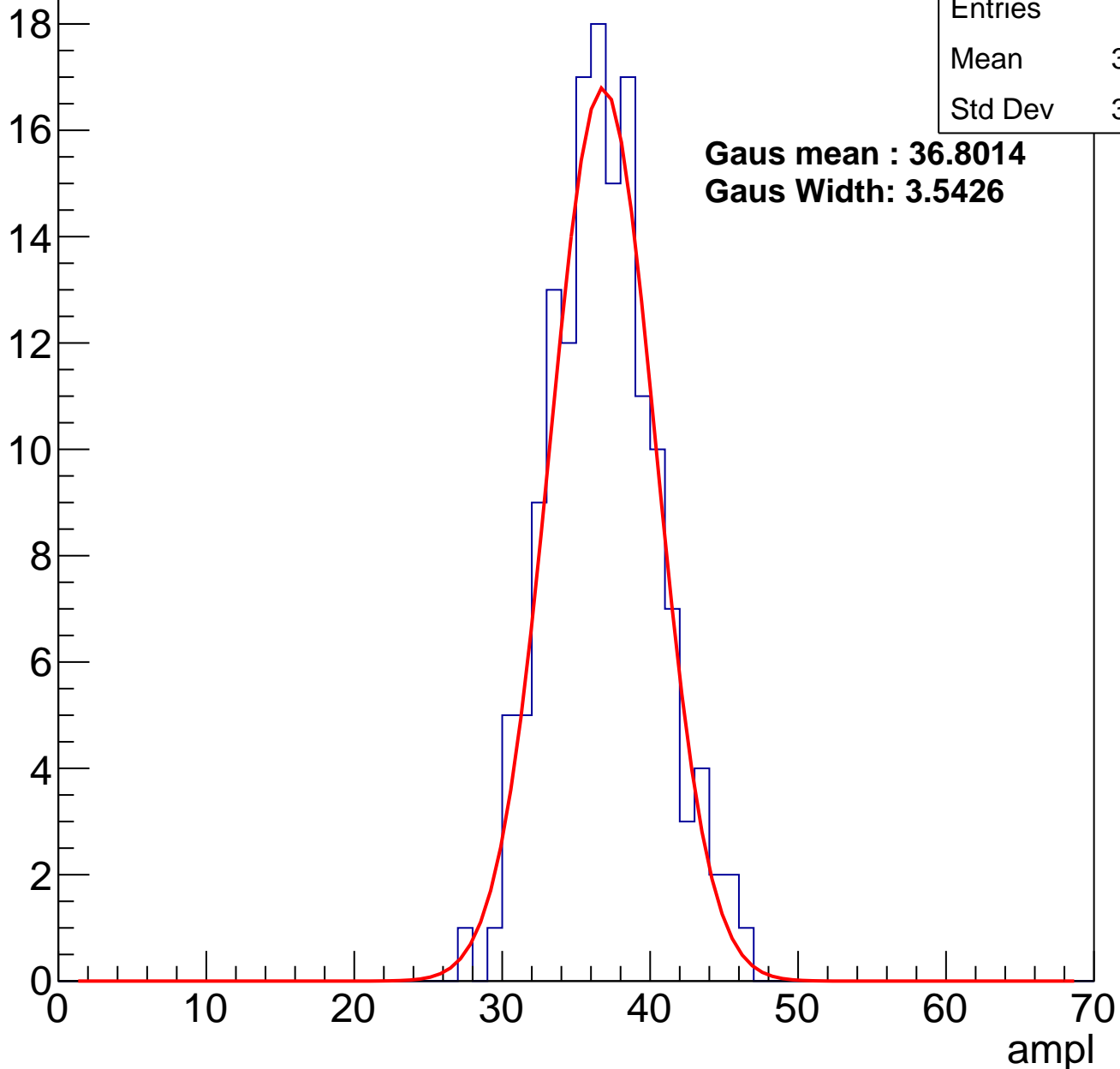
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	36.39
Std Dev	3.569

Gaus mean : 36.8014

Gaus Width: 3.5426

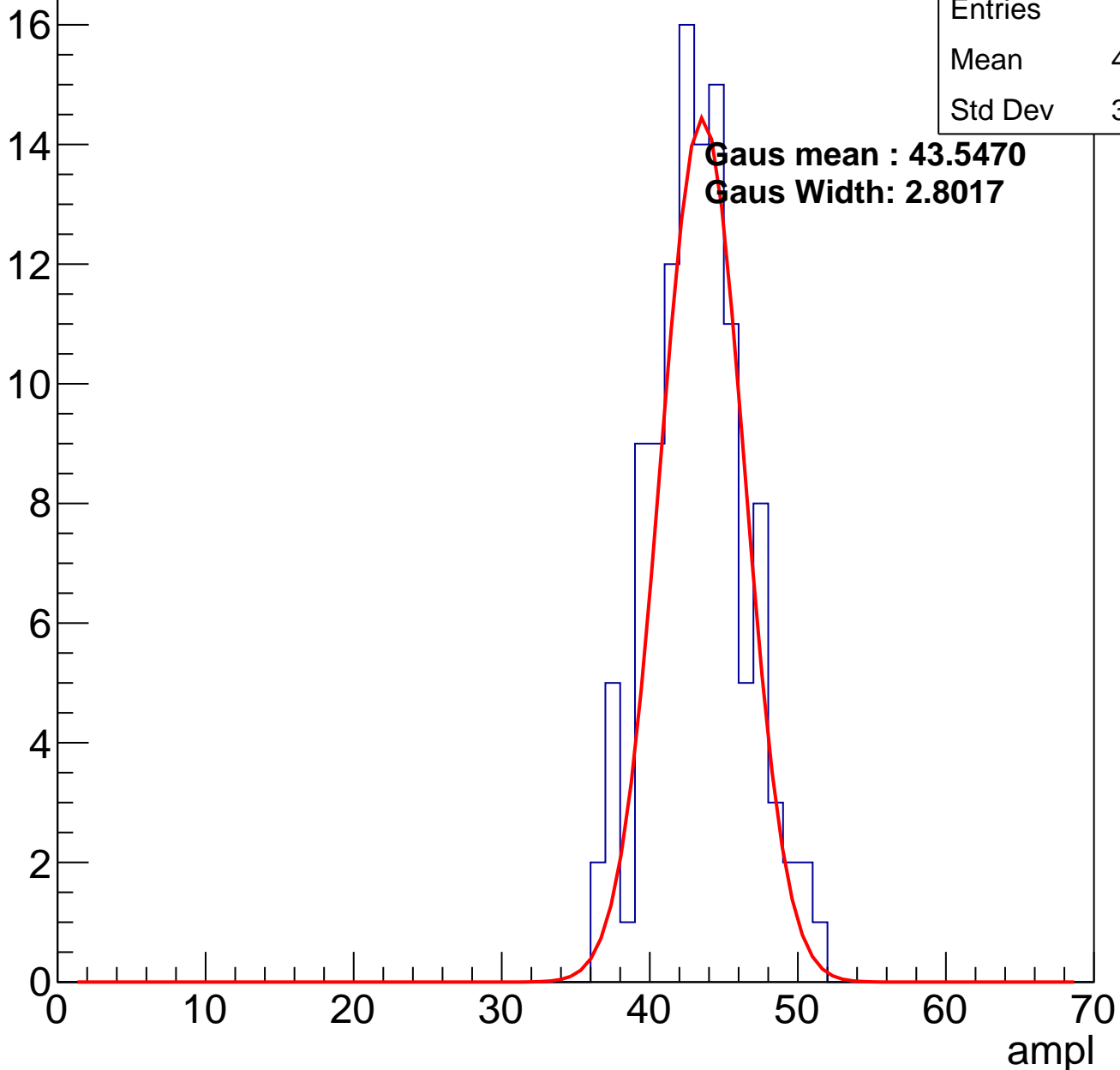
Entry



B0L101S, U21-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

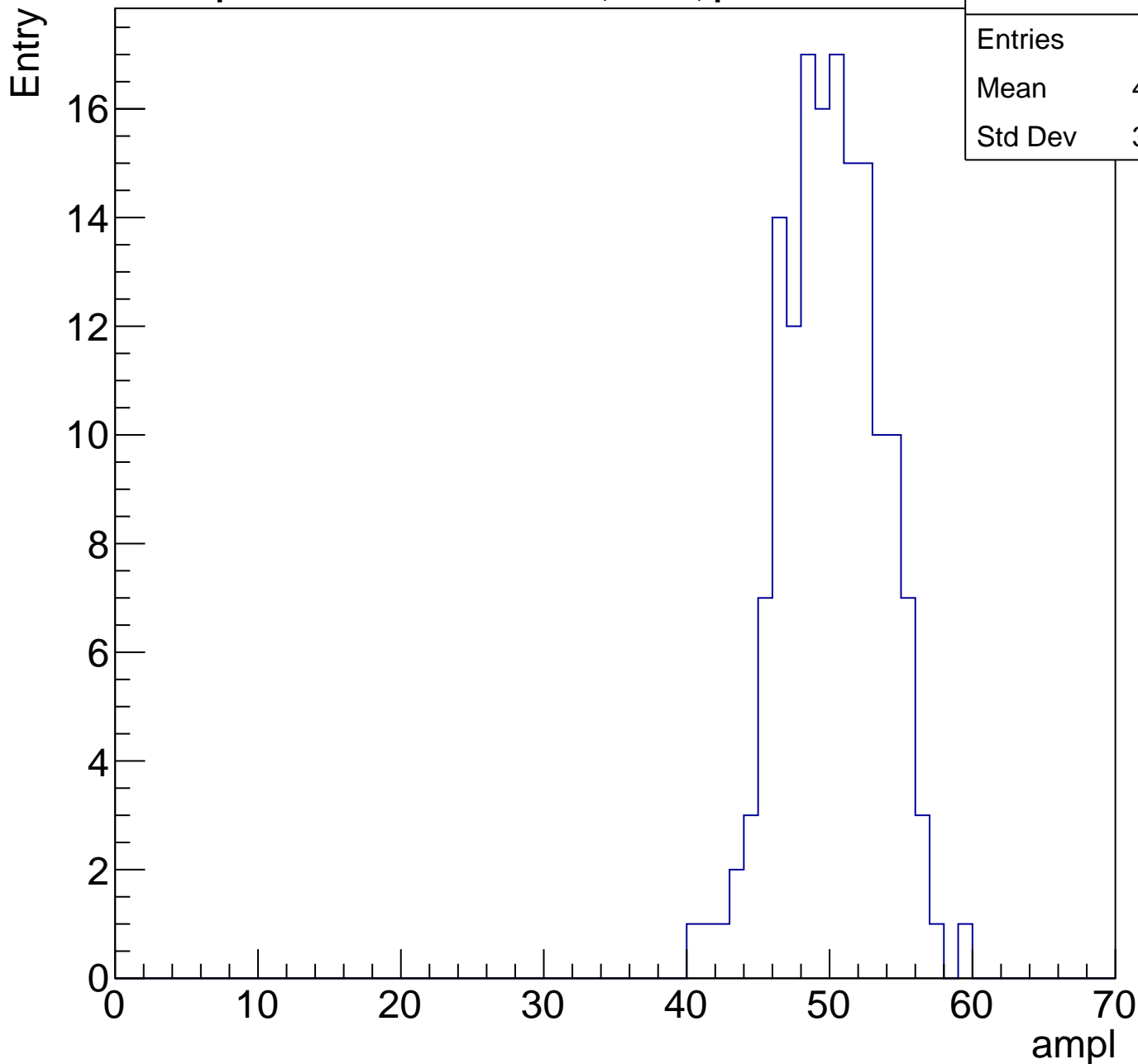
Entry



B0L101S, U21-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

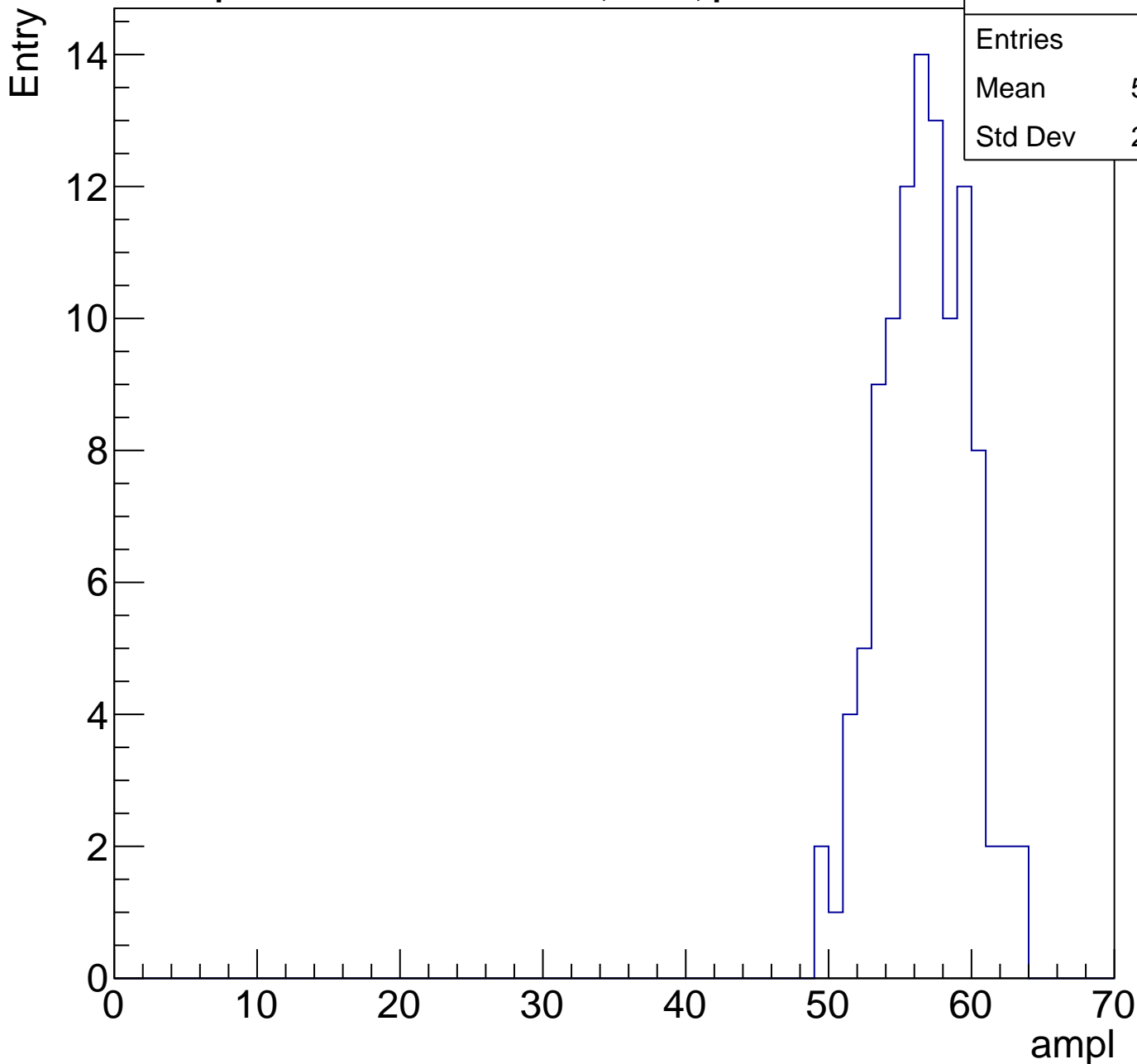
Entries	153
Mean	49.66
Std Dev	3.394



B0L101S, U21-ch55, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	56.17
Std Dev	2.998



B0L101S, U21-ch55, adc5

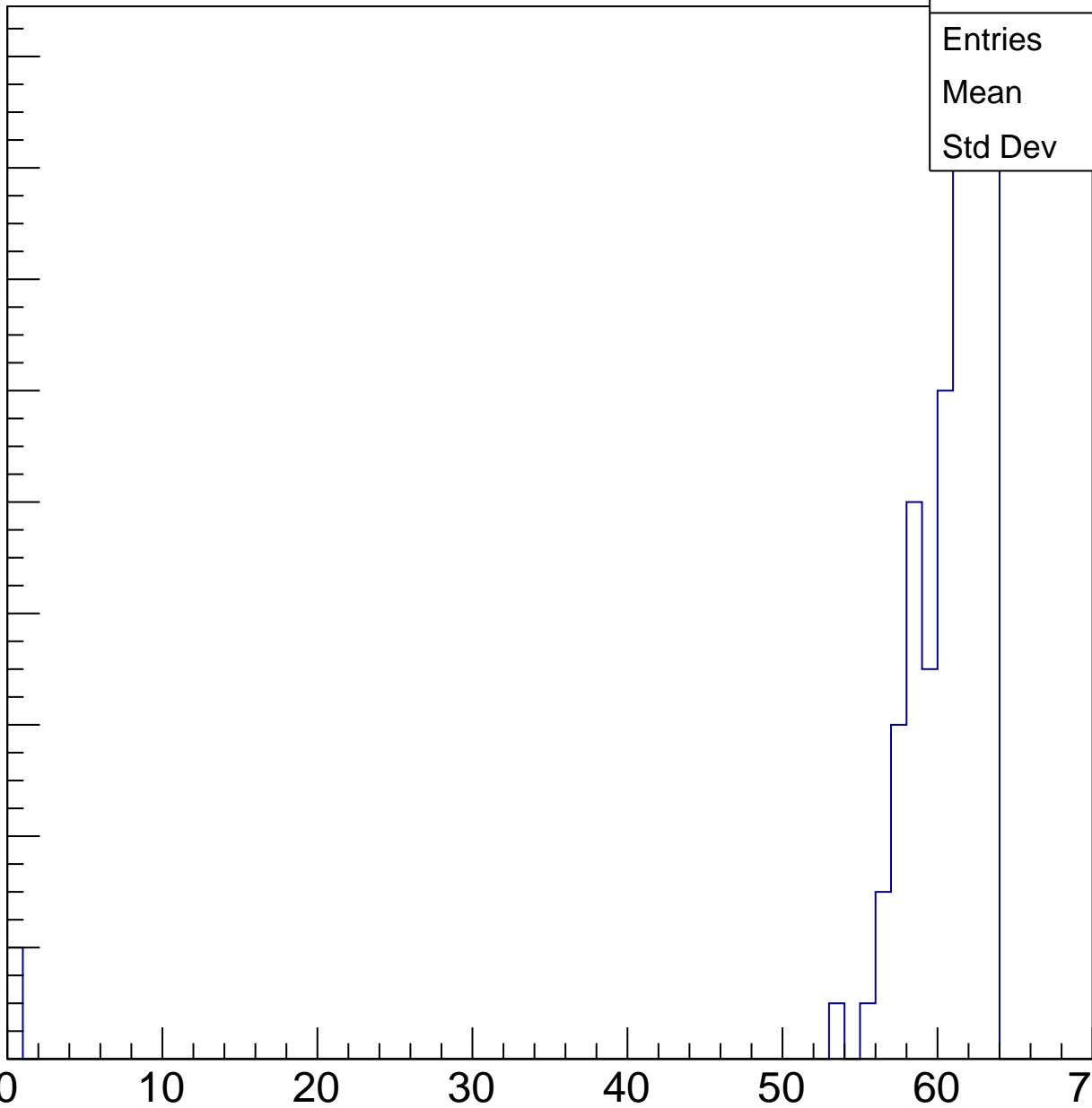
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	94
Mean	59.07
Std Dev	8.978

ampl



B0L101S, U21-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L101S, U21-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

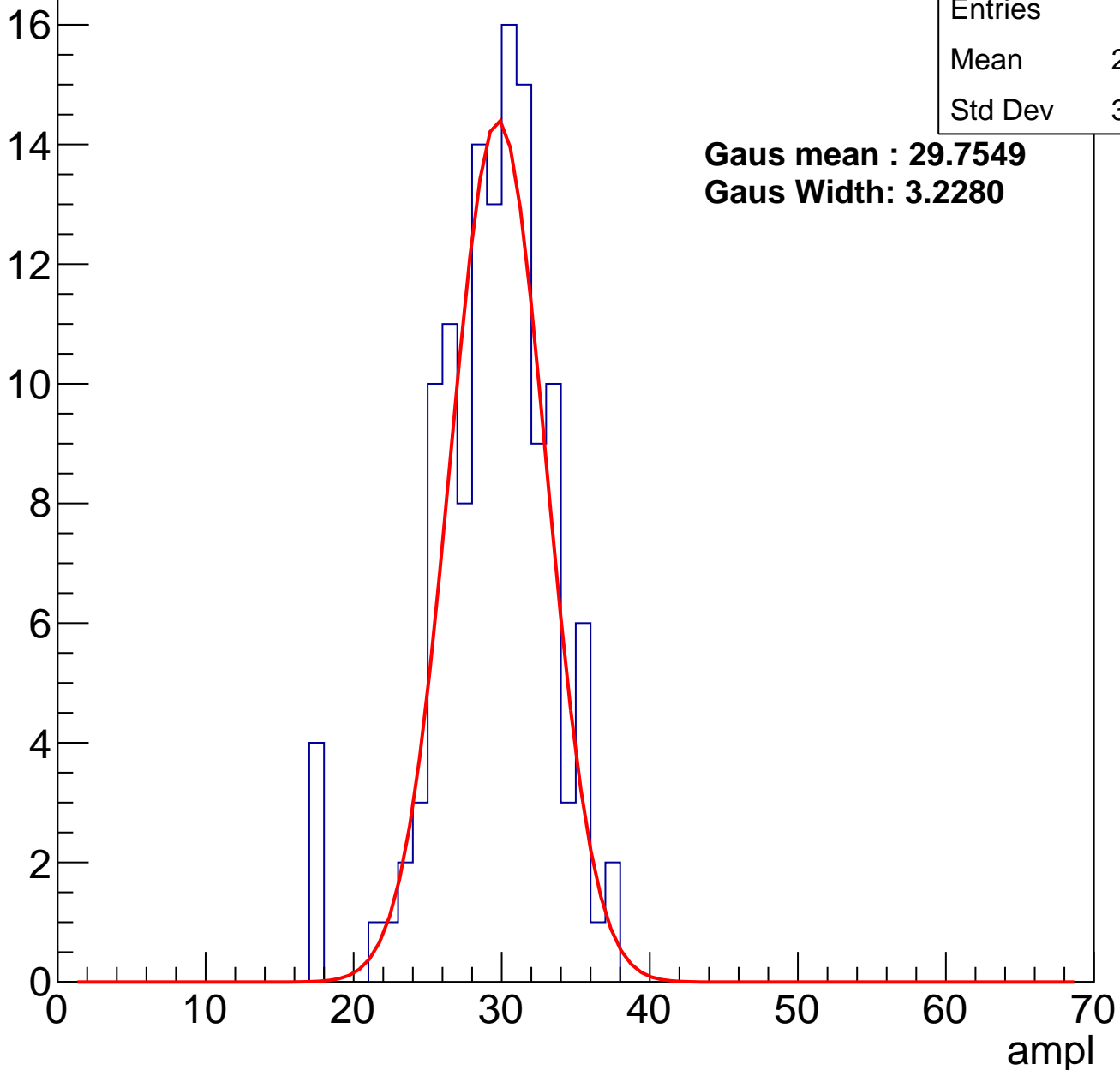


Entries	1
Mean	19
Std Dev	0

B0L101S, U21-ch56, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch56, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	35.62
Std Dev	4.29

Gaus mean : 36.6267

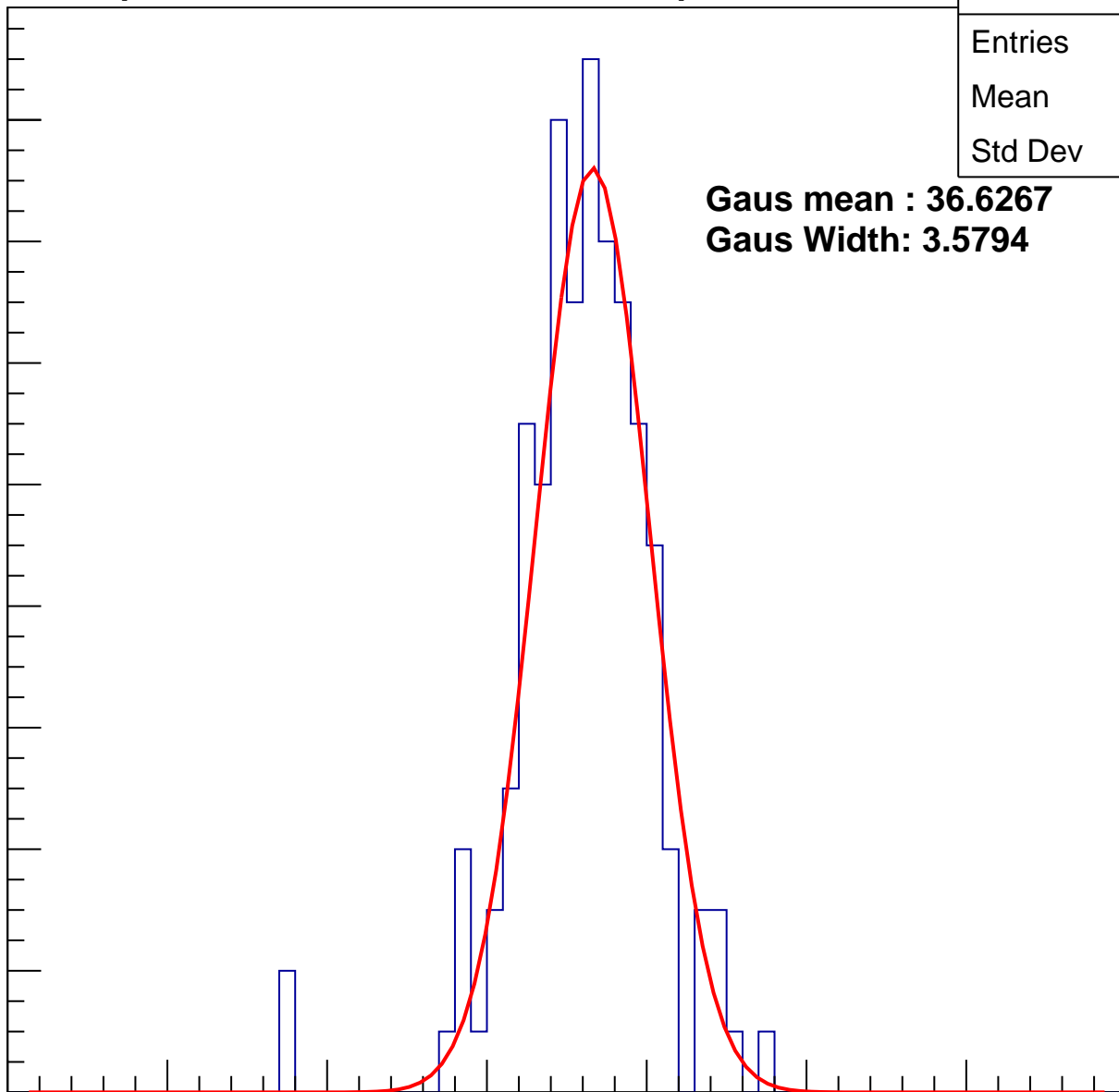
Gaus Width: 3.5794

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

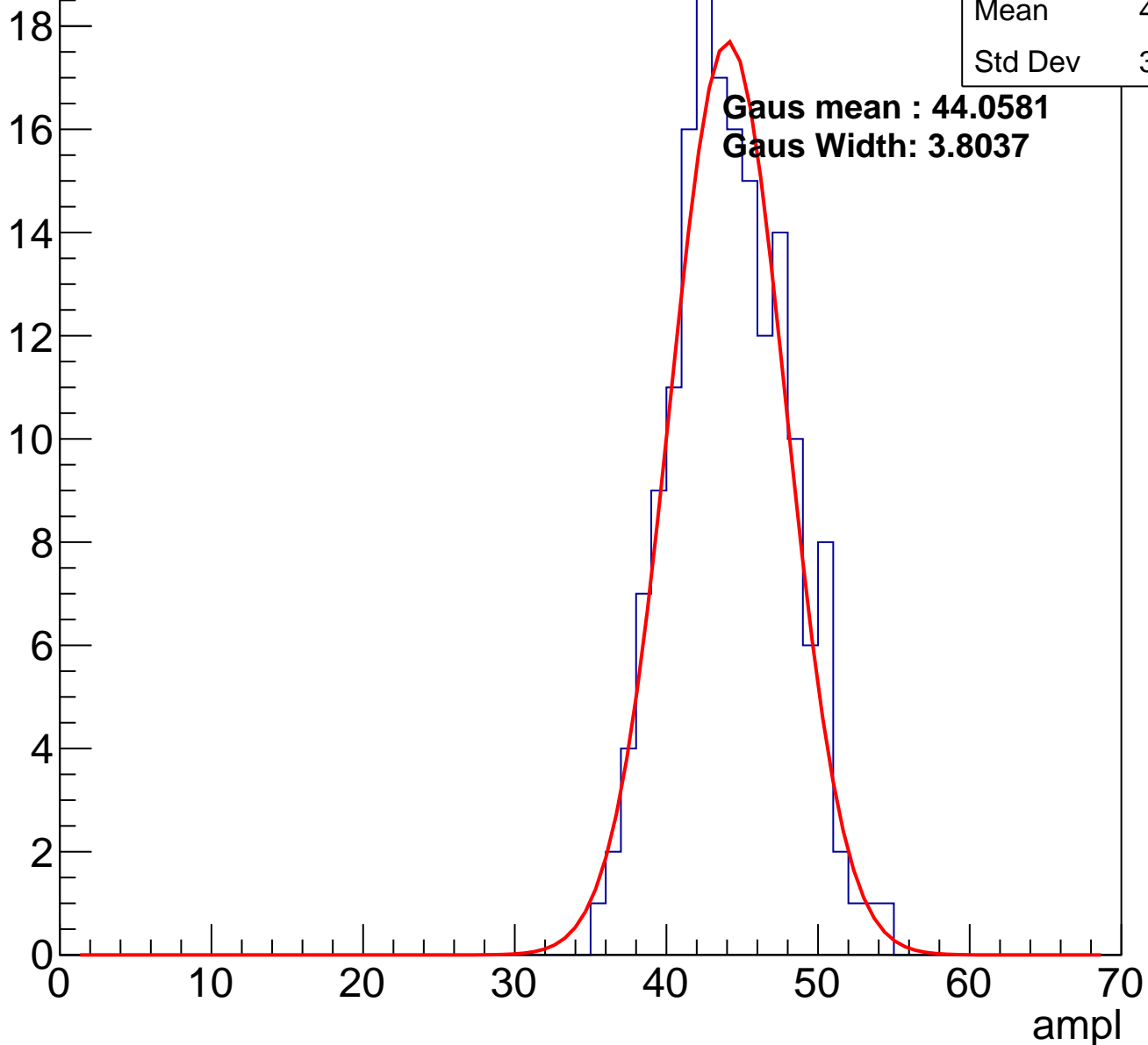
ampl



B0L101S, U21-ch56, adc2

calib_packv5_042523_0143.root, FC#1, port C1

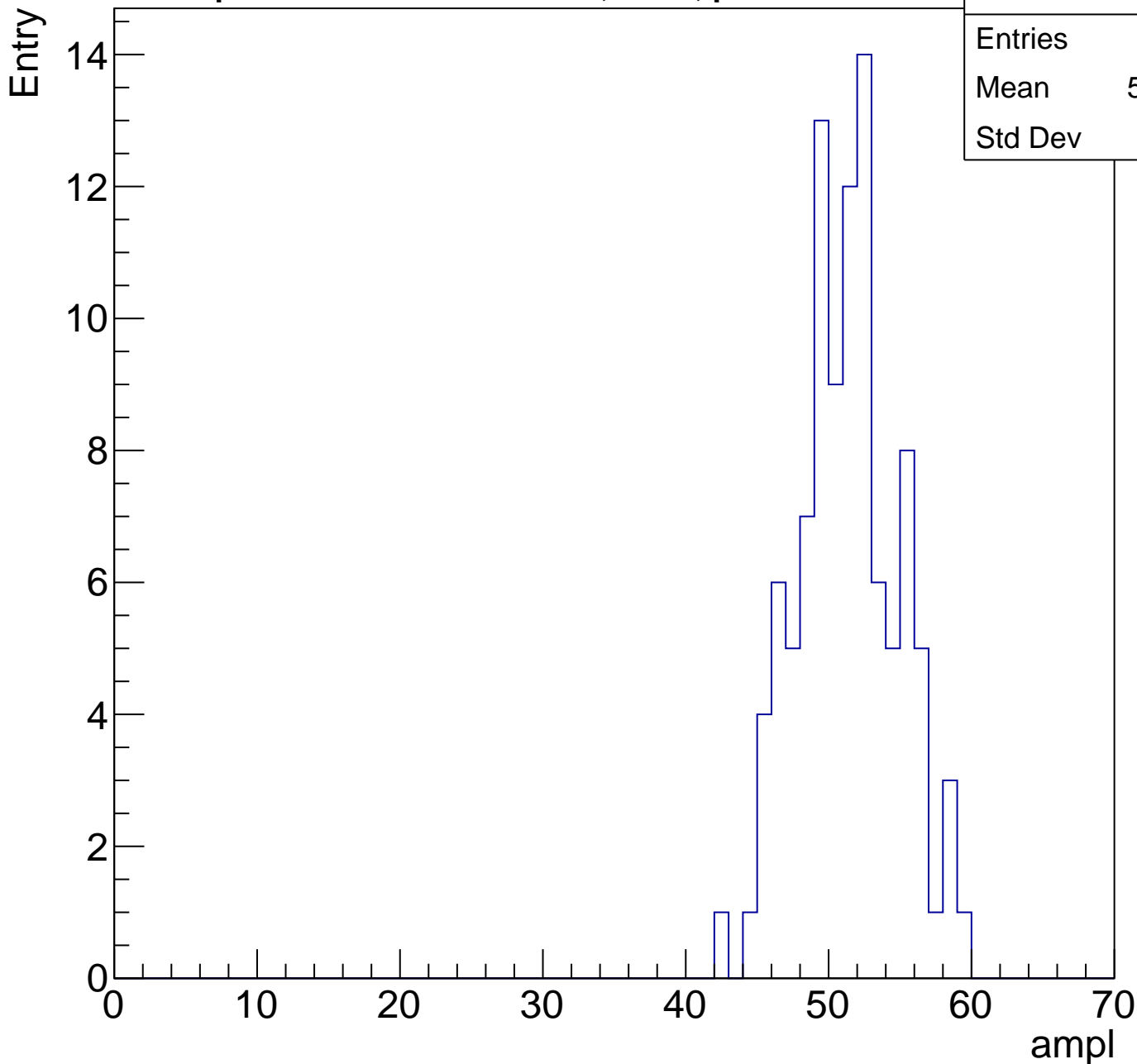
Entry



B0L101S, U21-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

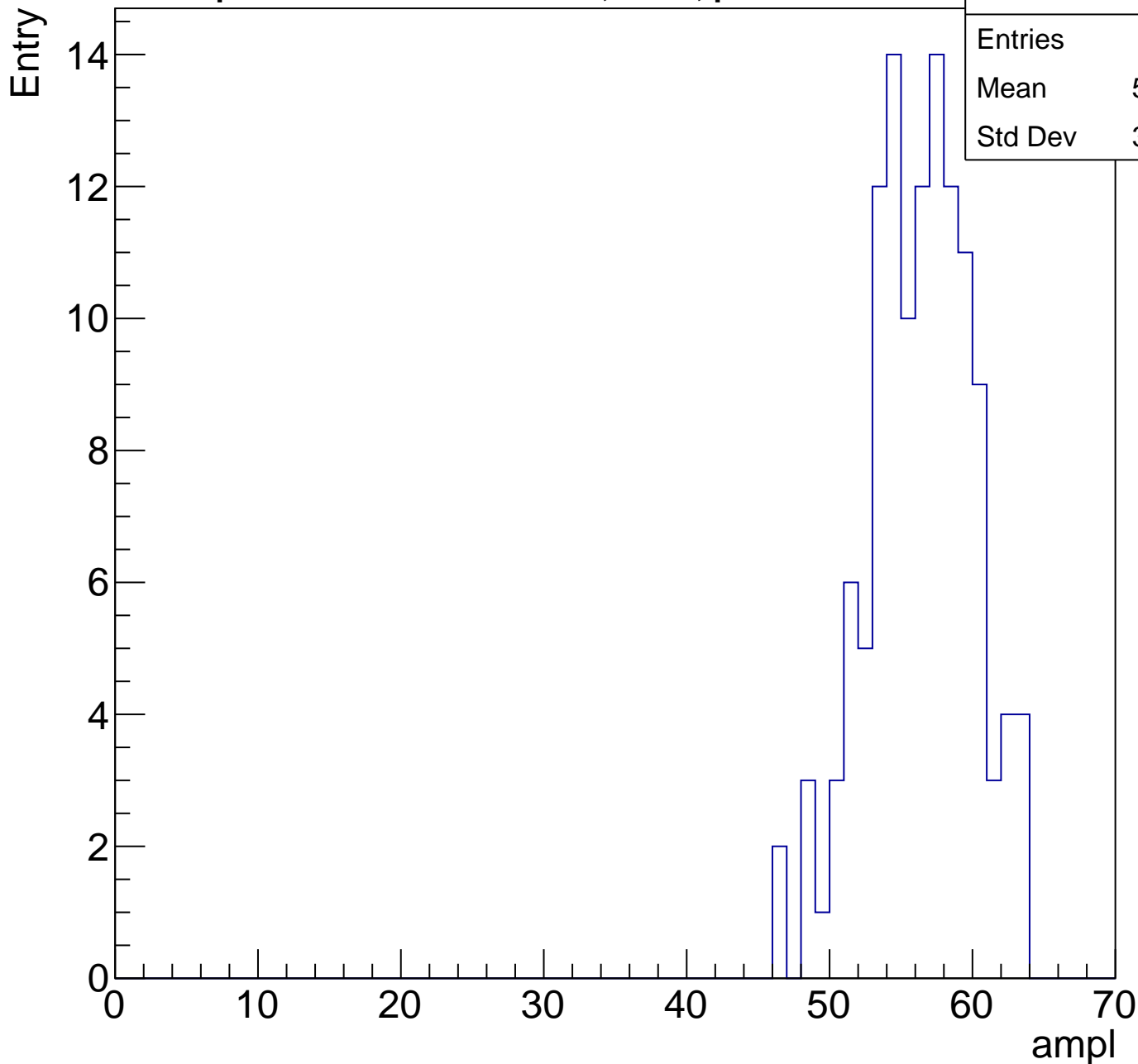
Entries	101
Mean	50.87
Std Dev	3.46



B0L101S, U21-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.85
Std Dev	3.655



B0L101S, U21-ch56, adc5

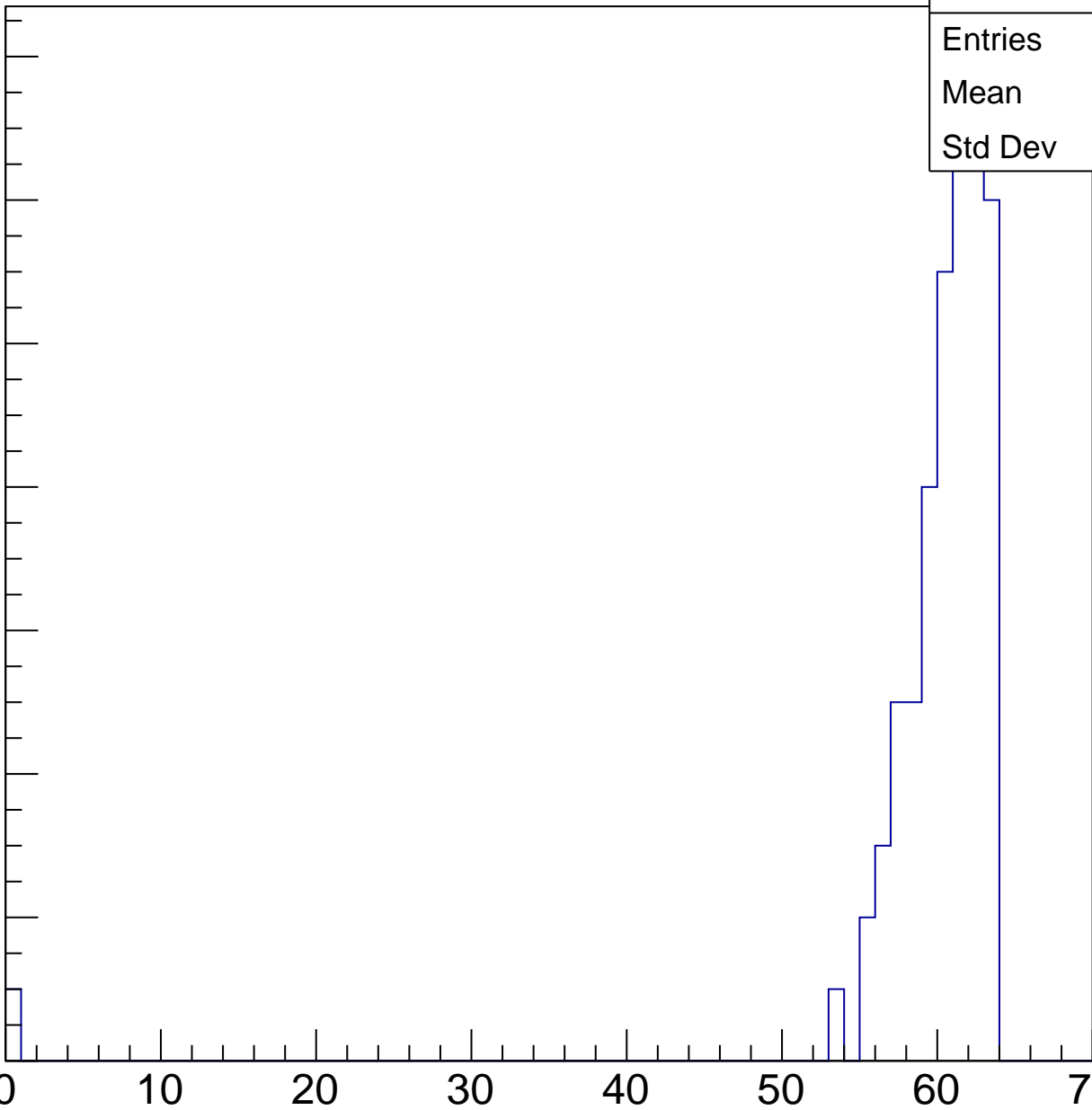
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	59.39
Std Dev	7.272

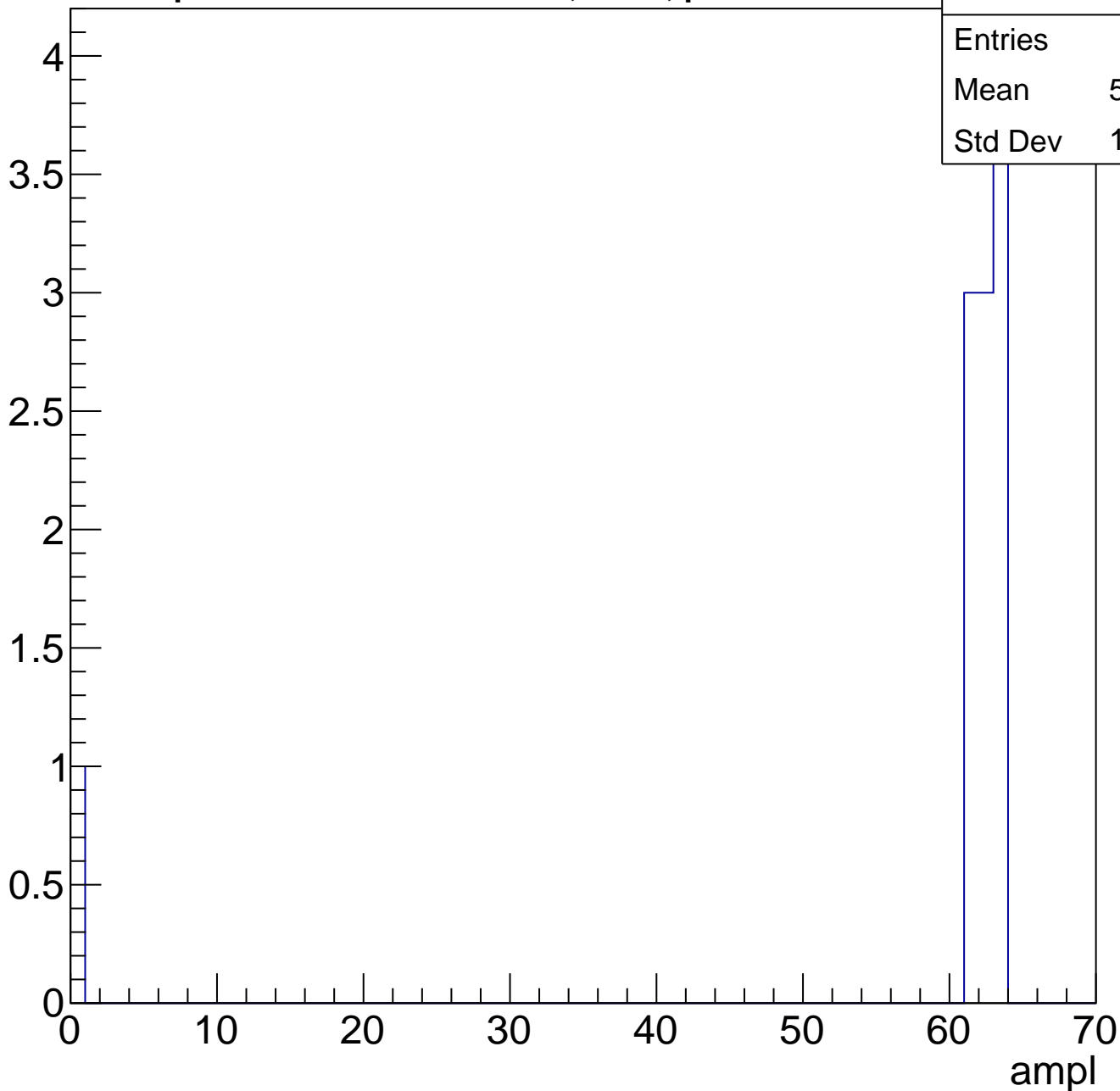
ampl



B0L101S, U21-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

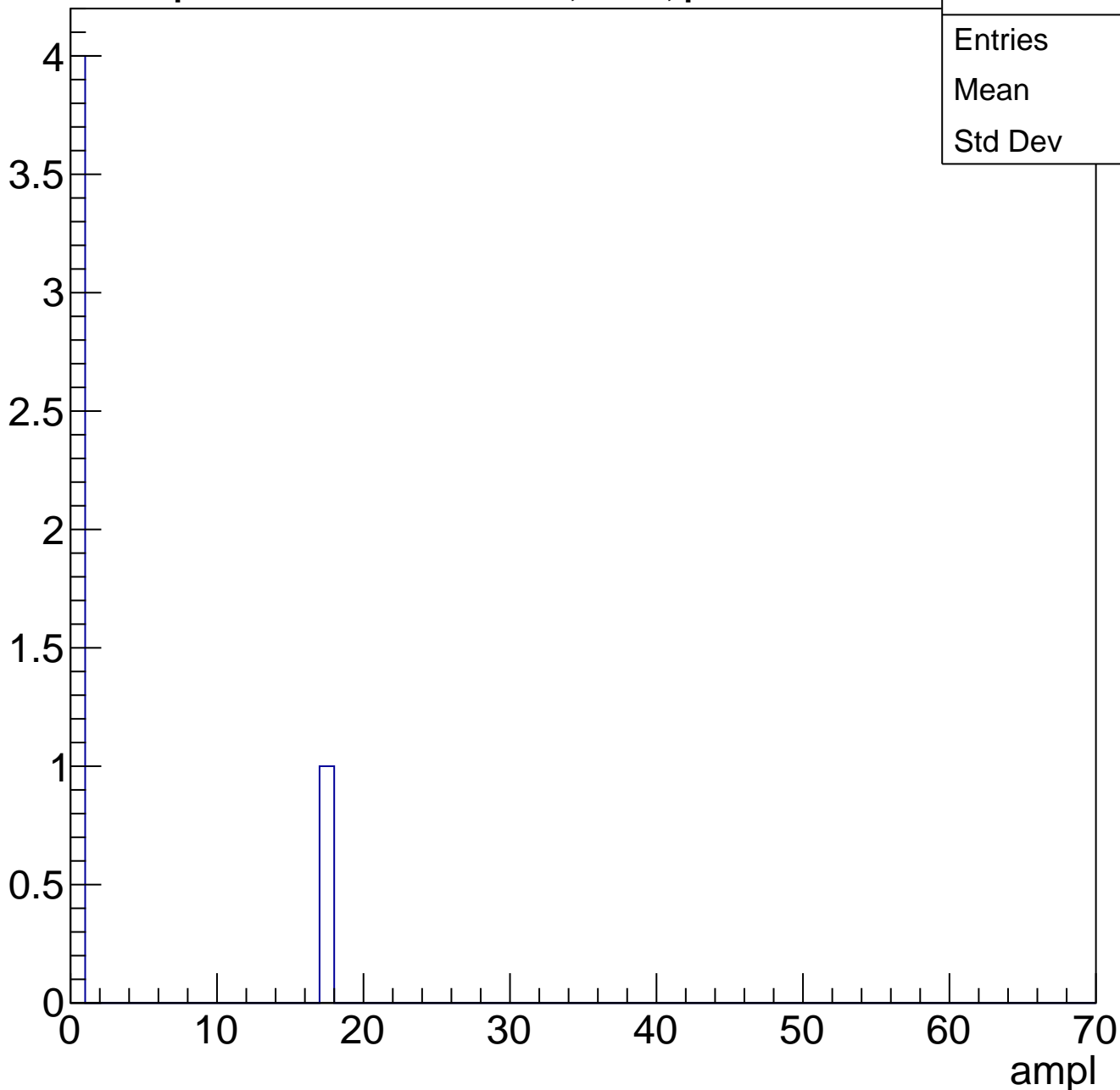
Entry



B0L101S, U21-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	3.4
Std Dev	6.8

B0L101S, U21-ch57, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.82
Std Dev	3.518

Gaus mean : 30.1960

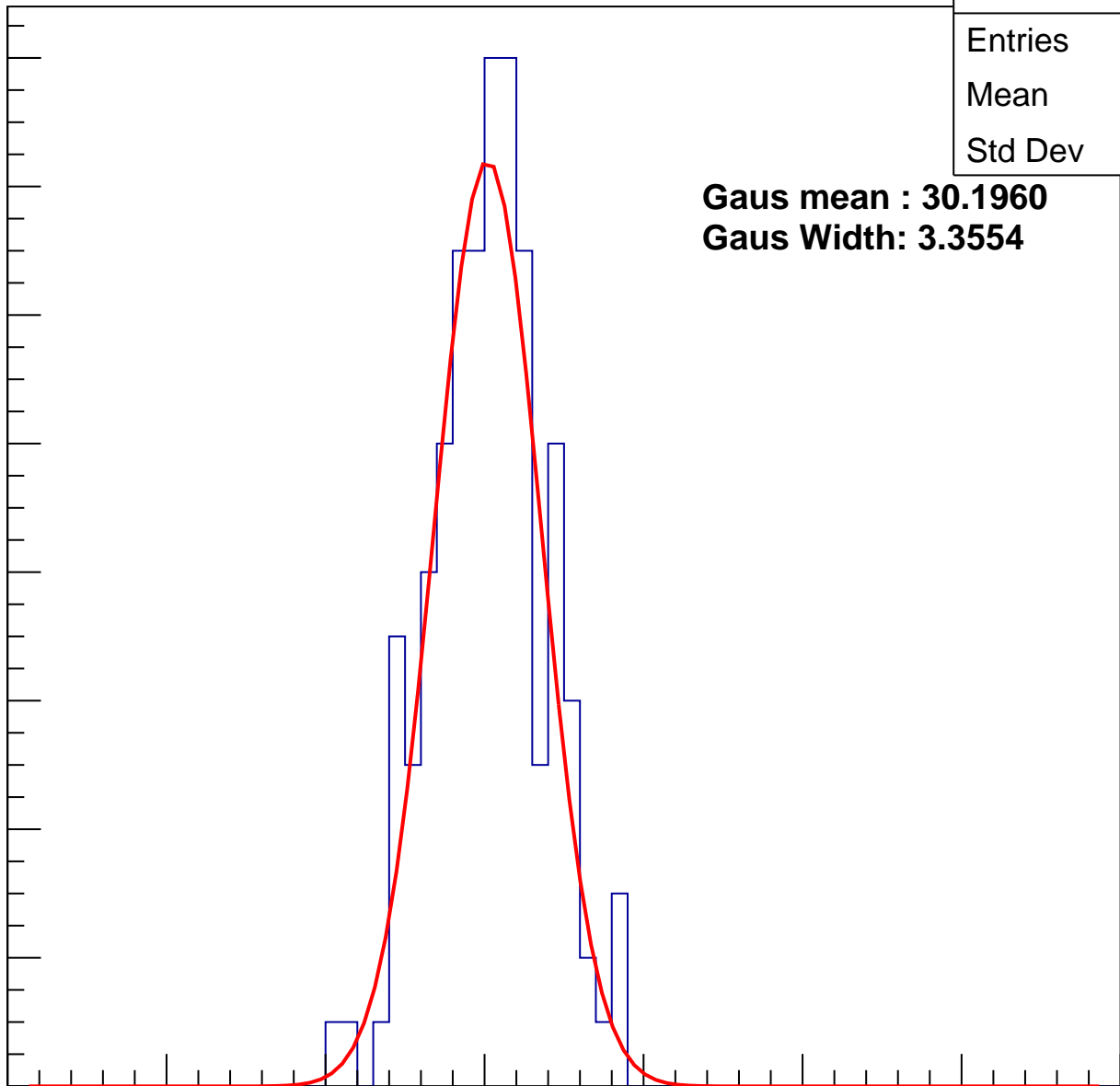
Gaus Width: 3.3554

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch57, adc1

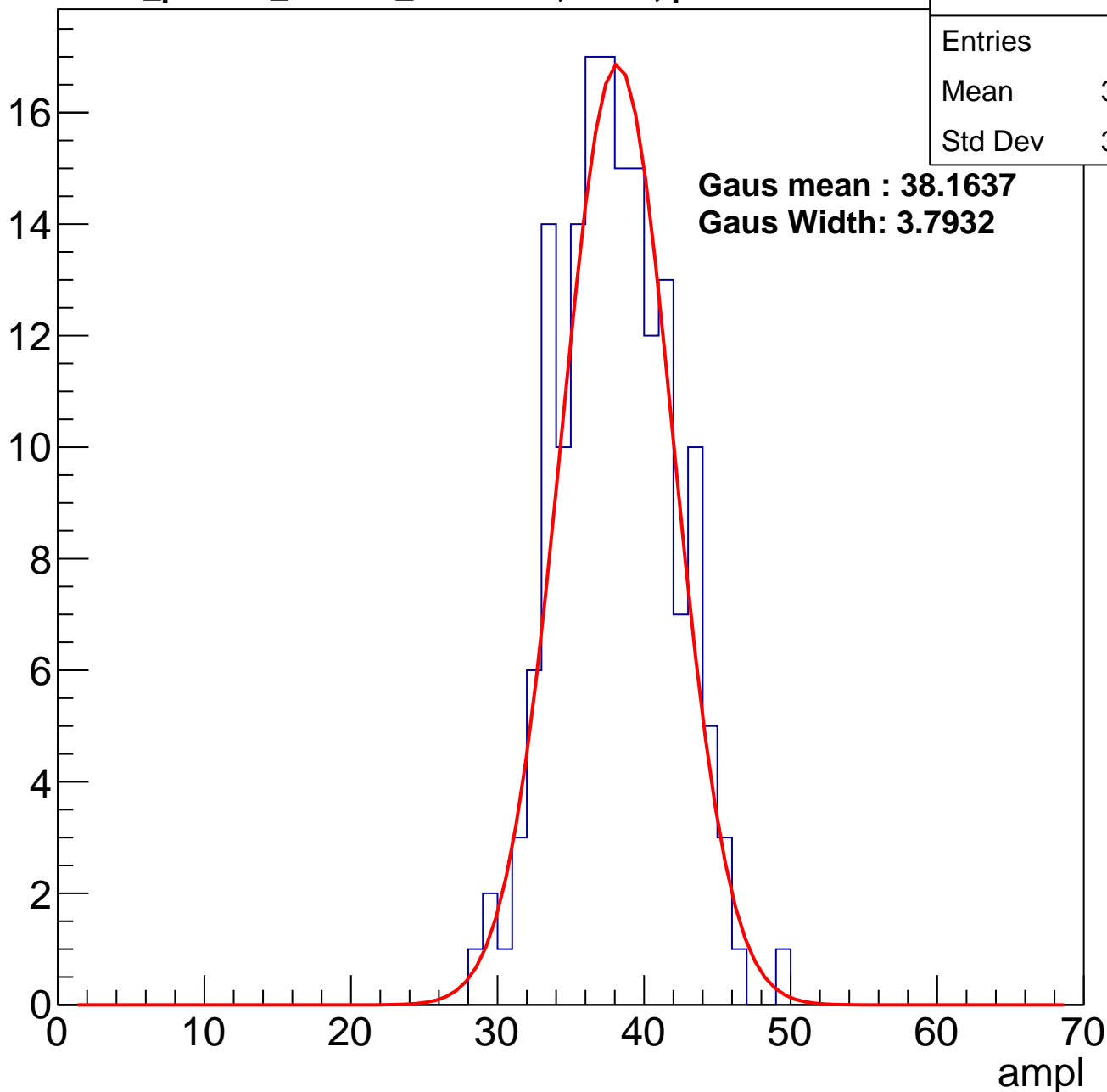
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	37.58
Std Dev	3.818

Gaus mean : 38.1637

Gaus Width: 3.7932

Entry



B0L101S, U21-ch57, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

120

Mean

44.4

Std Dev

3.68

Gaus mean : 45.1326

Gaus Width: 3.9381

0

10

20

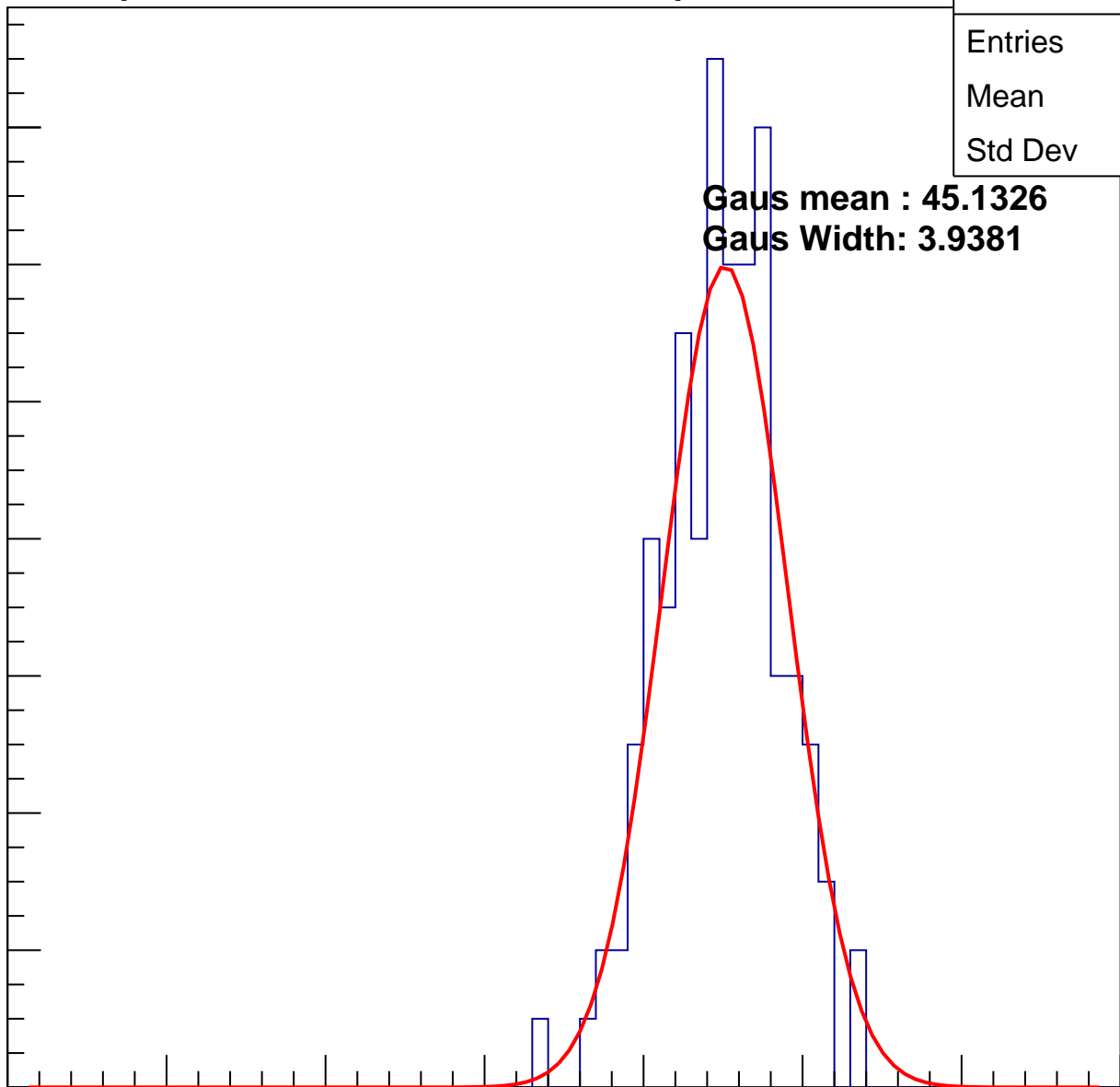
30

40

50

60

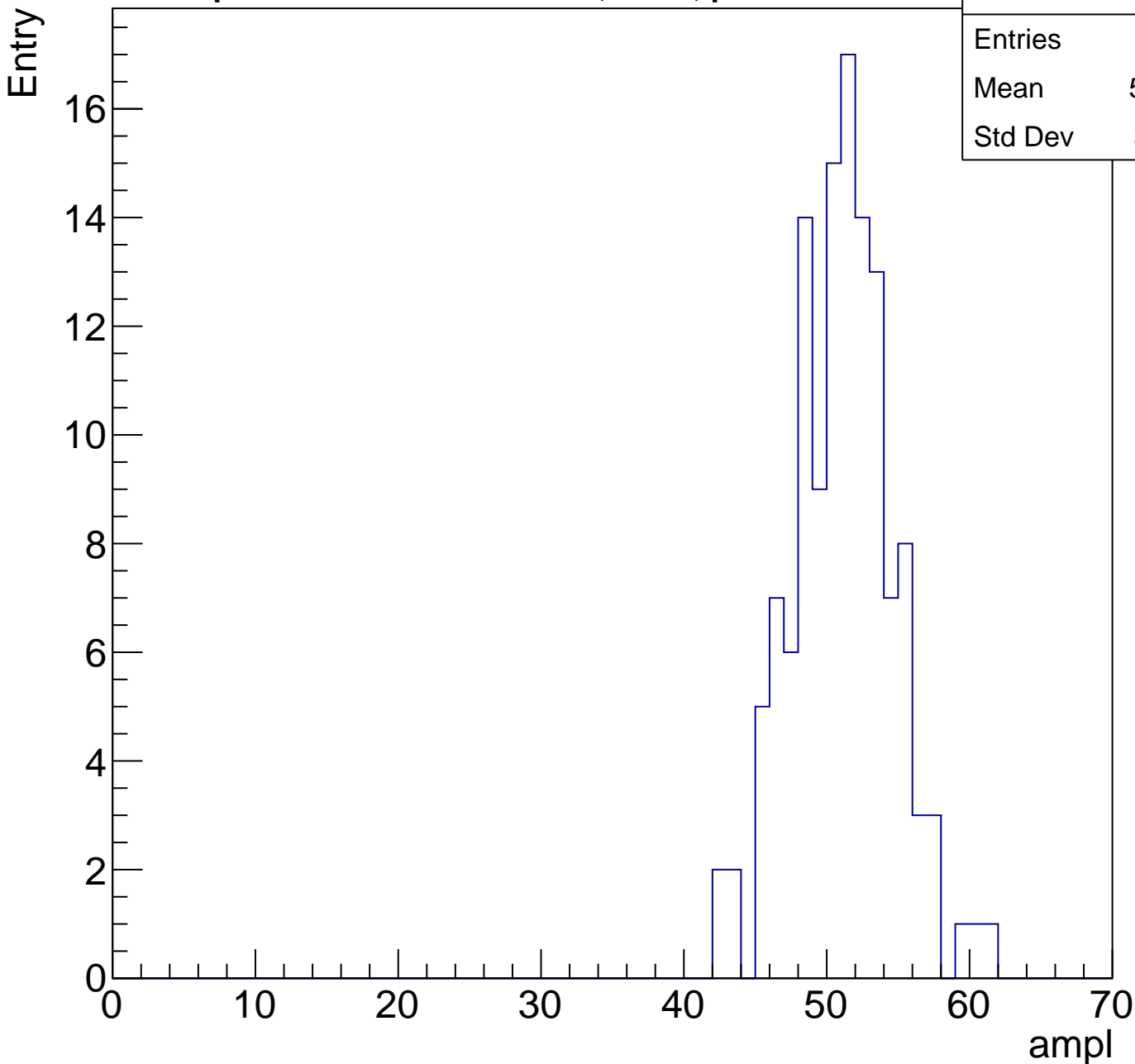
ampl



B0L101S, U21-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

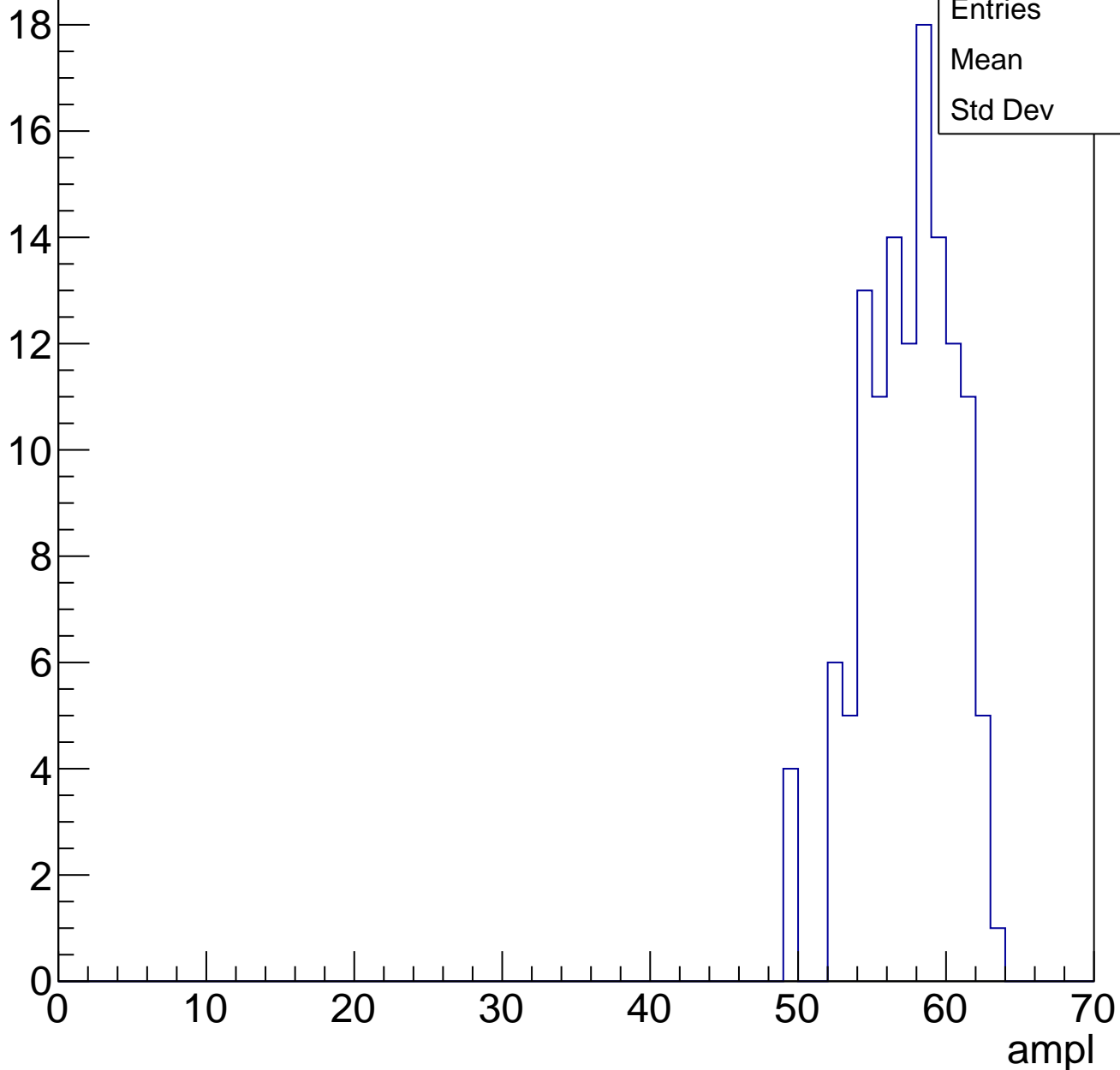
Entries	128
Mean	50.65
Std Dev	3.521



B0L101S, U21-ch57, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch57, adc5

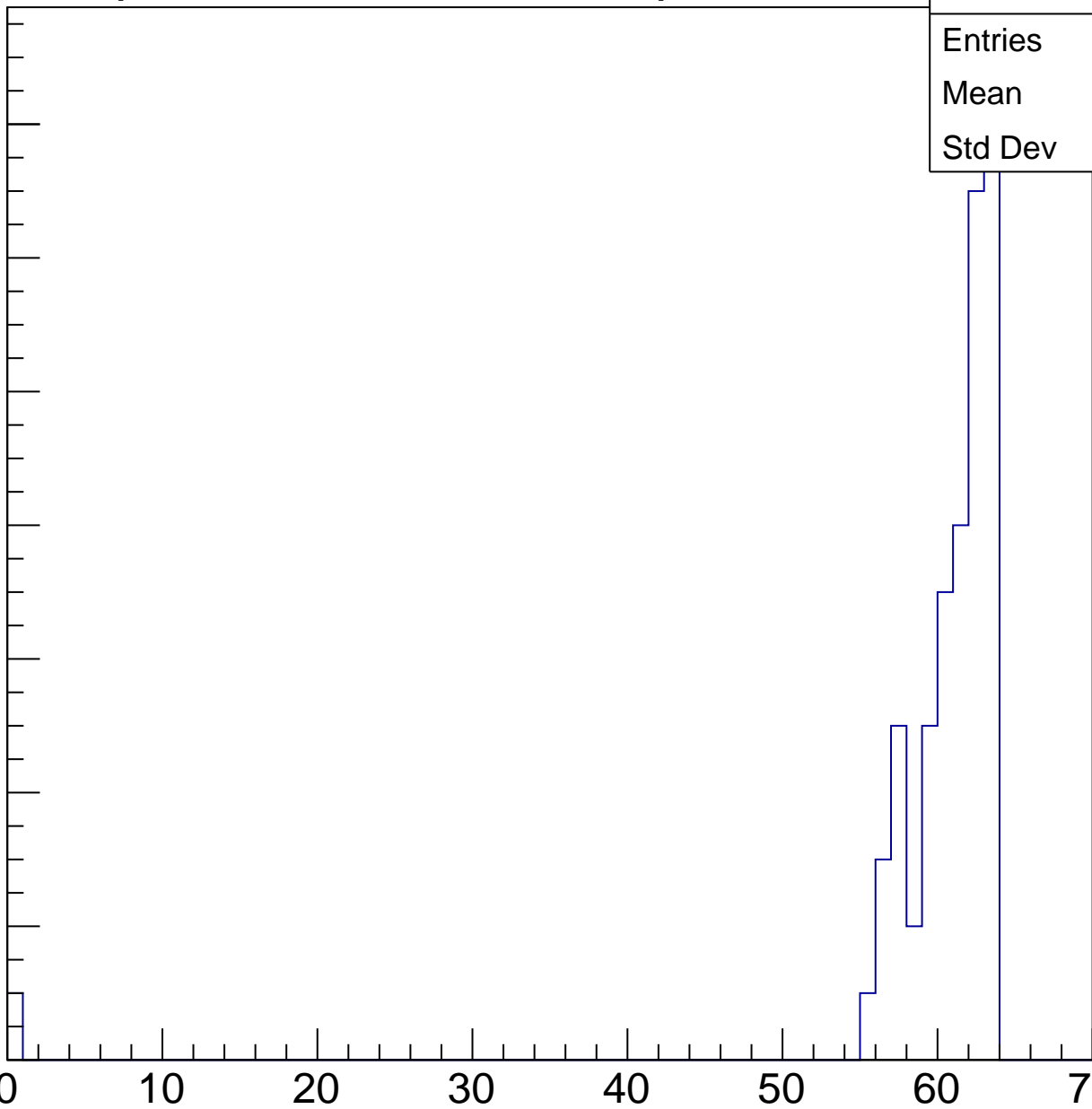
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	60
Mean	59.63
Std Dev	8.081

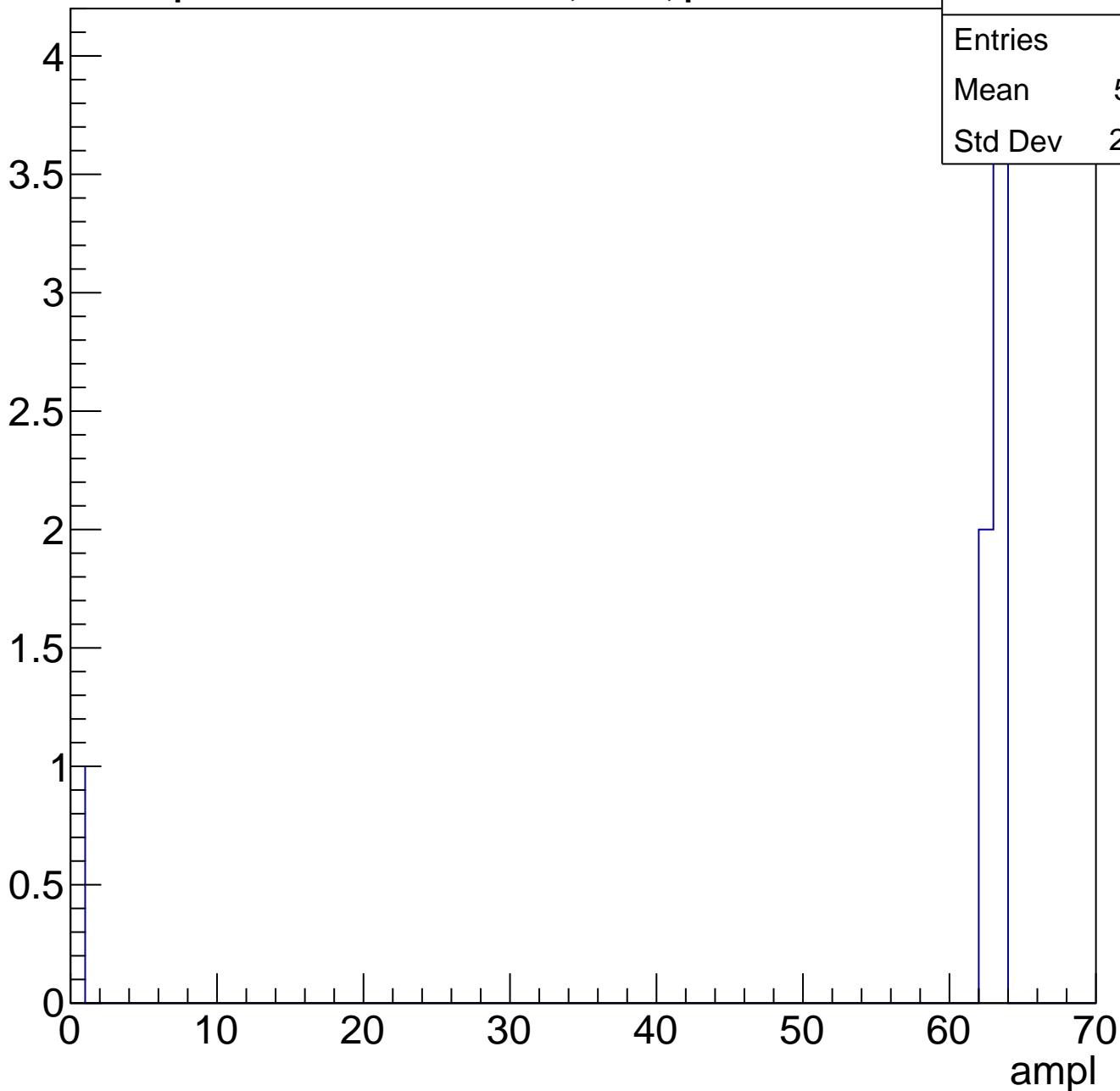
ampl



B0L101S, U21-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

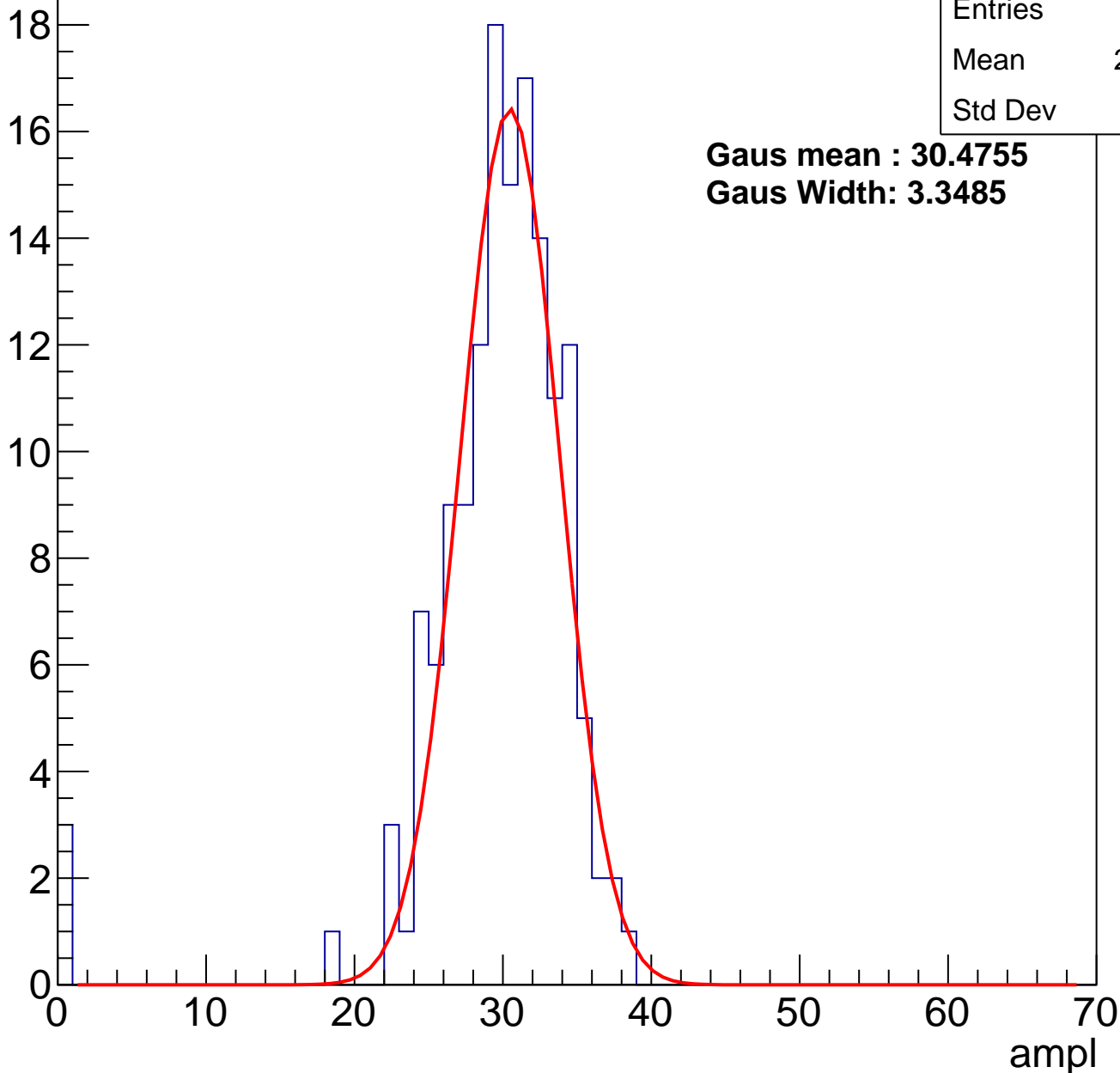


Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch58, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch58, adc1

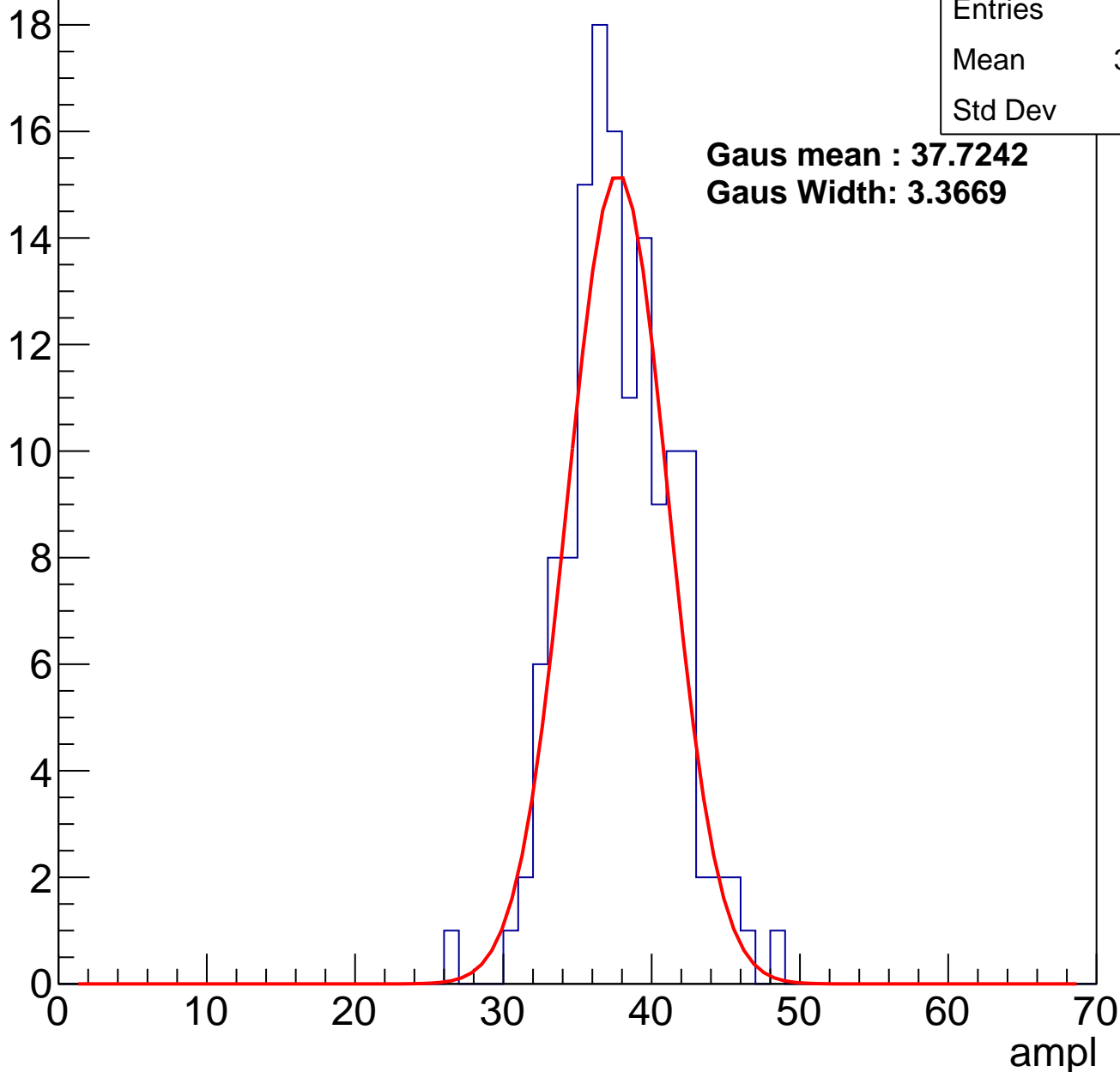
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	37.39
Std Dev	3.53

Gaus mean : 37.7242

Gaus Width: 3.3669

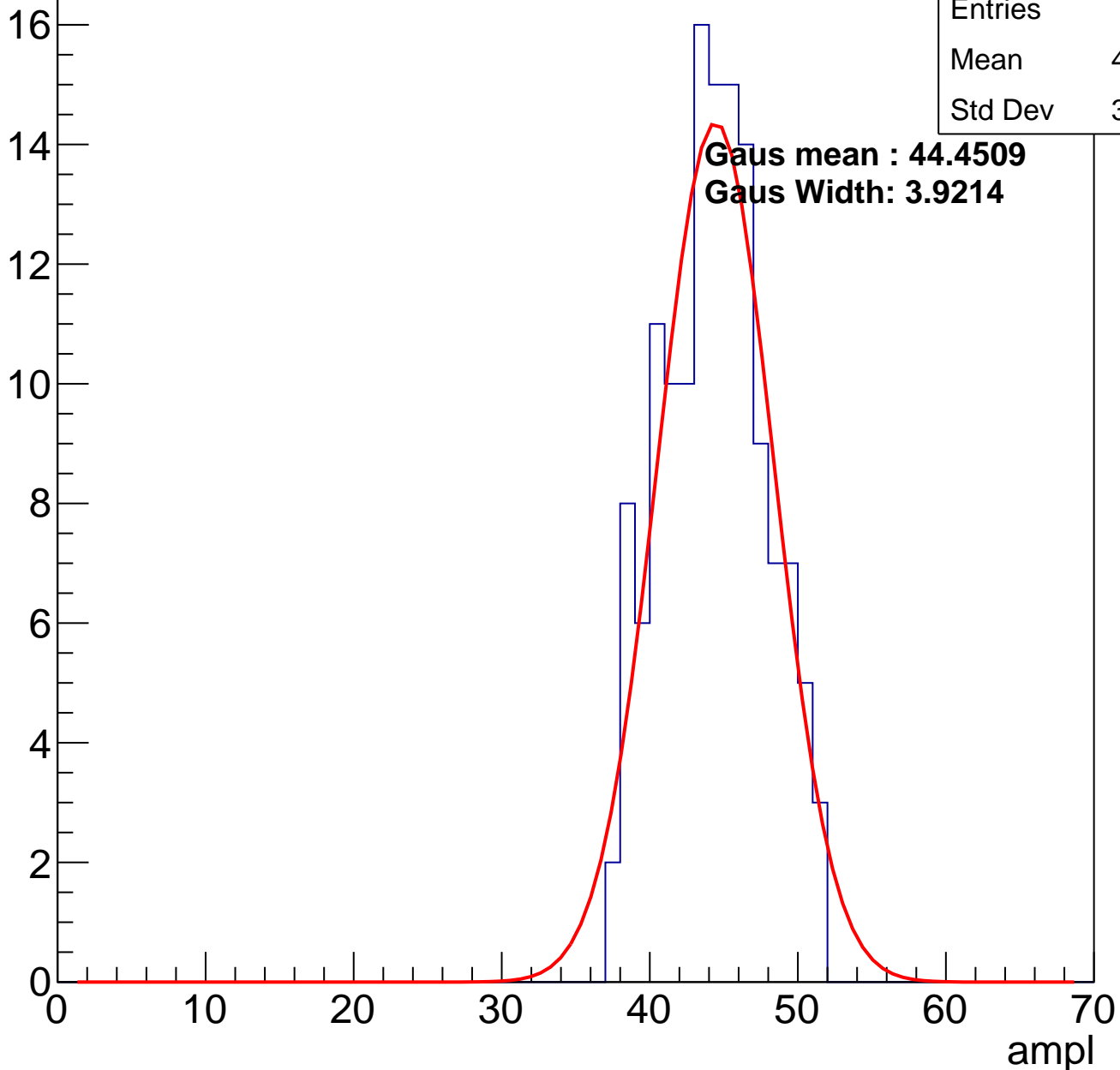
Entry



B0L101S, U21-ch58, adc2

calib_packv5_042523_0143.root, FC#1, port C1

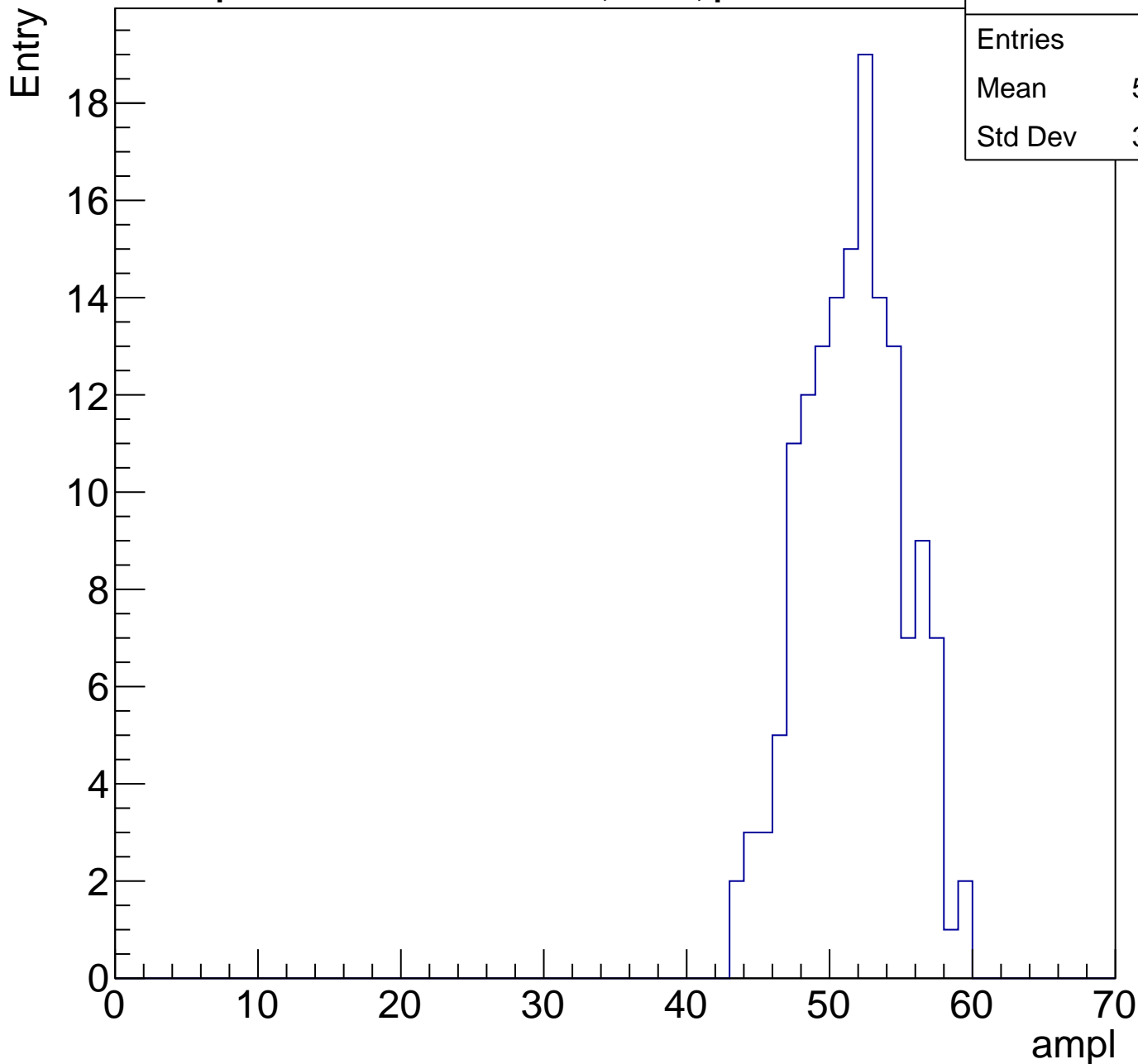
Entry



B0L101S, U21-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

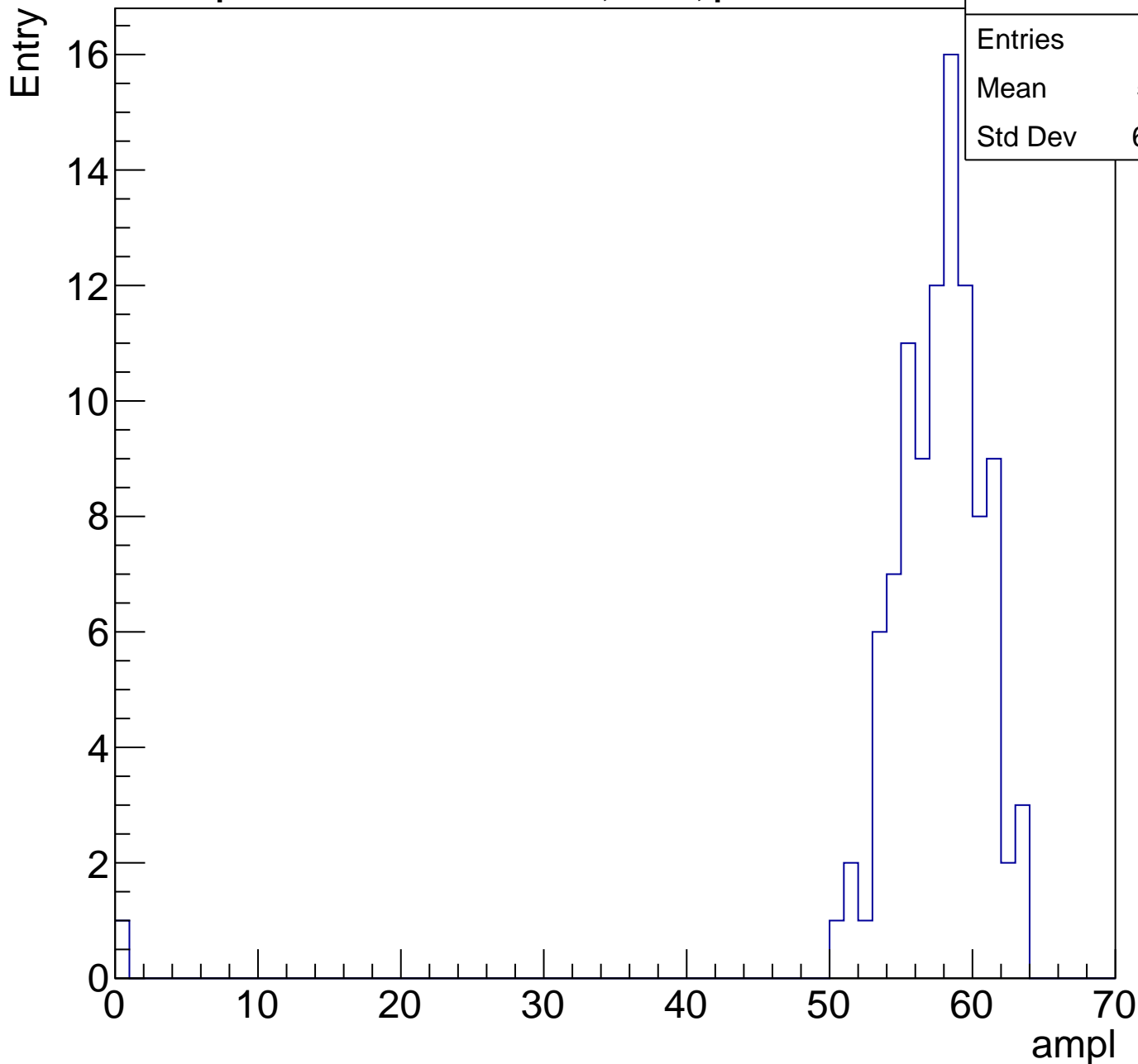
Entries	150
Mean	51.16
Std Dev	3.476



B0L101S, U21-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	56.71
Std Dev	6.353



B0L101S, U21-ch58, adc5

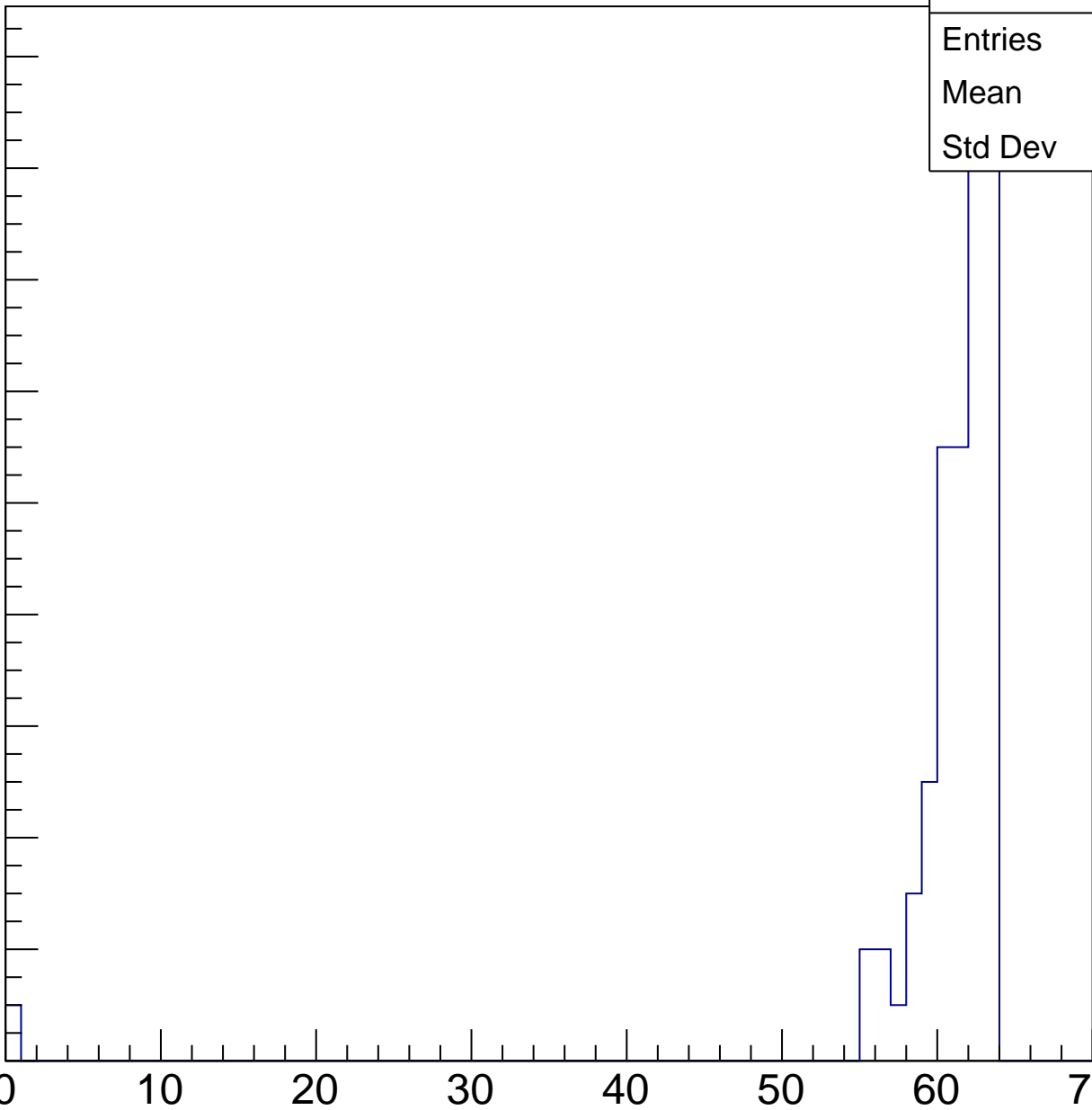
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	70
Mean	60.04
Std Dev	7.497

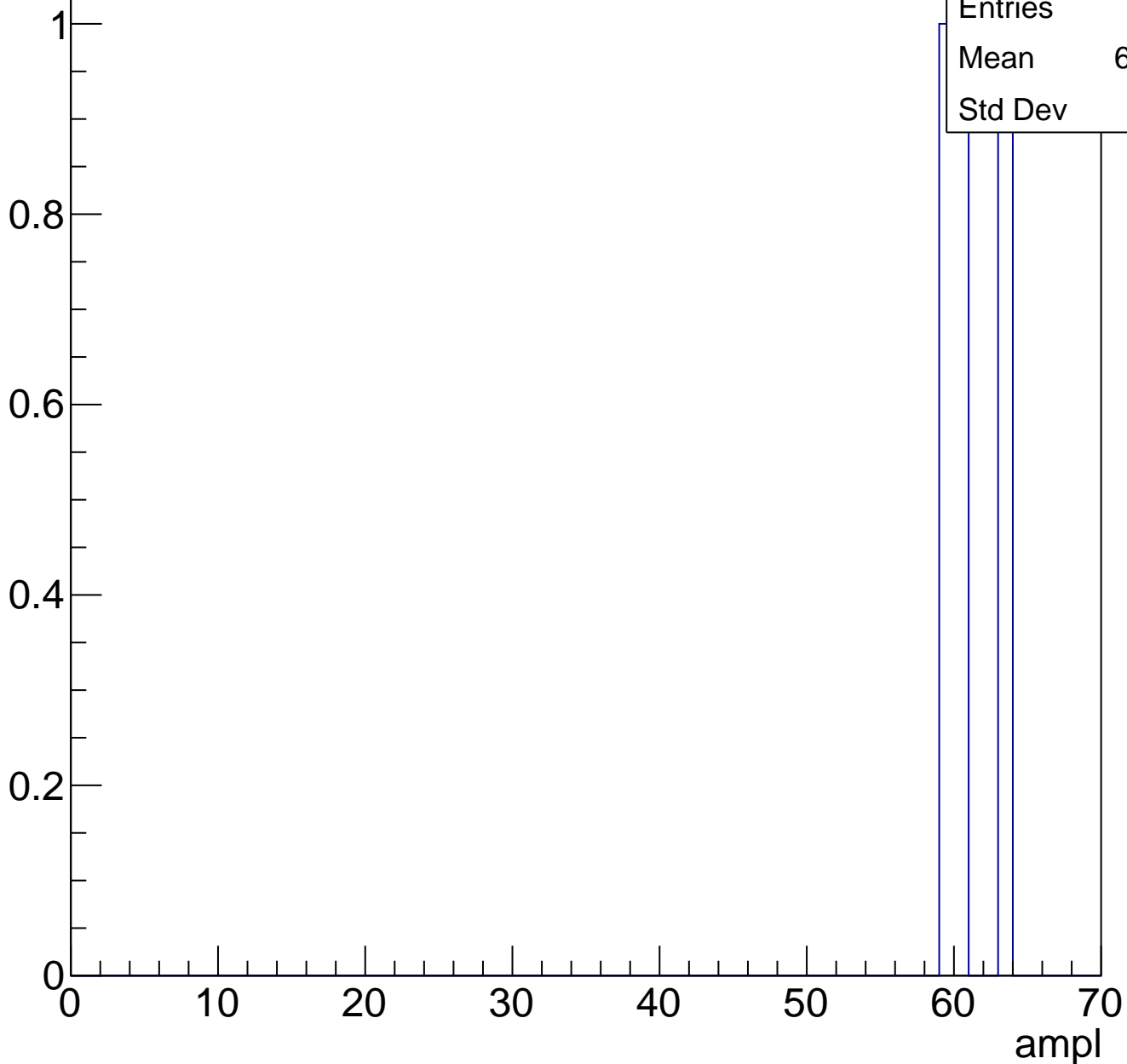
ampl



B0L101S, U21-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch59, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

28.65

Std Dev

3.616

Gaus mean : 28.7396

Gaus Width: 3.0488

0

10

20

30

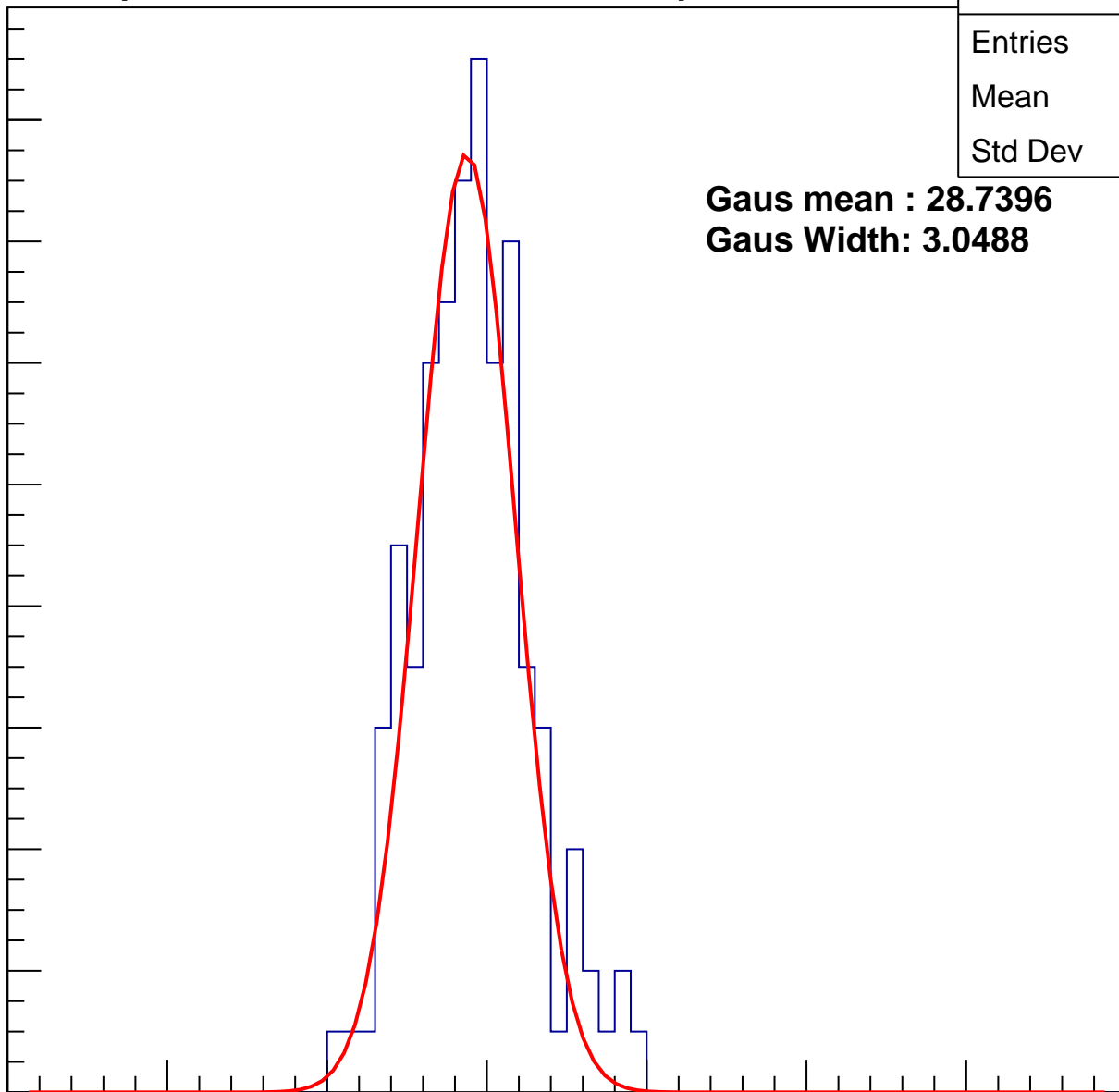
40

50

60

70

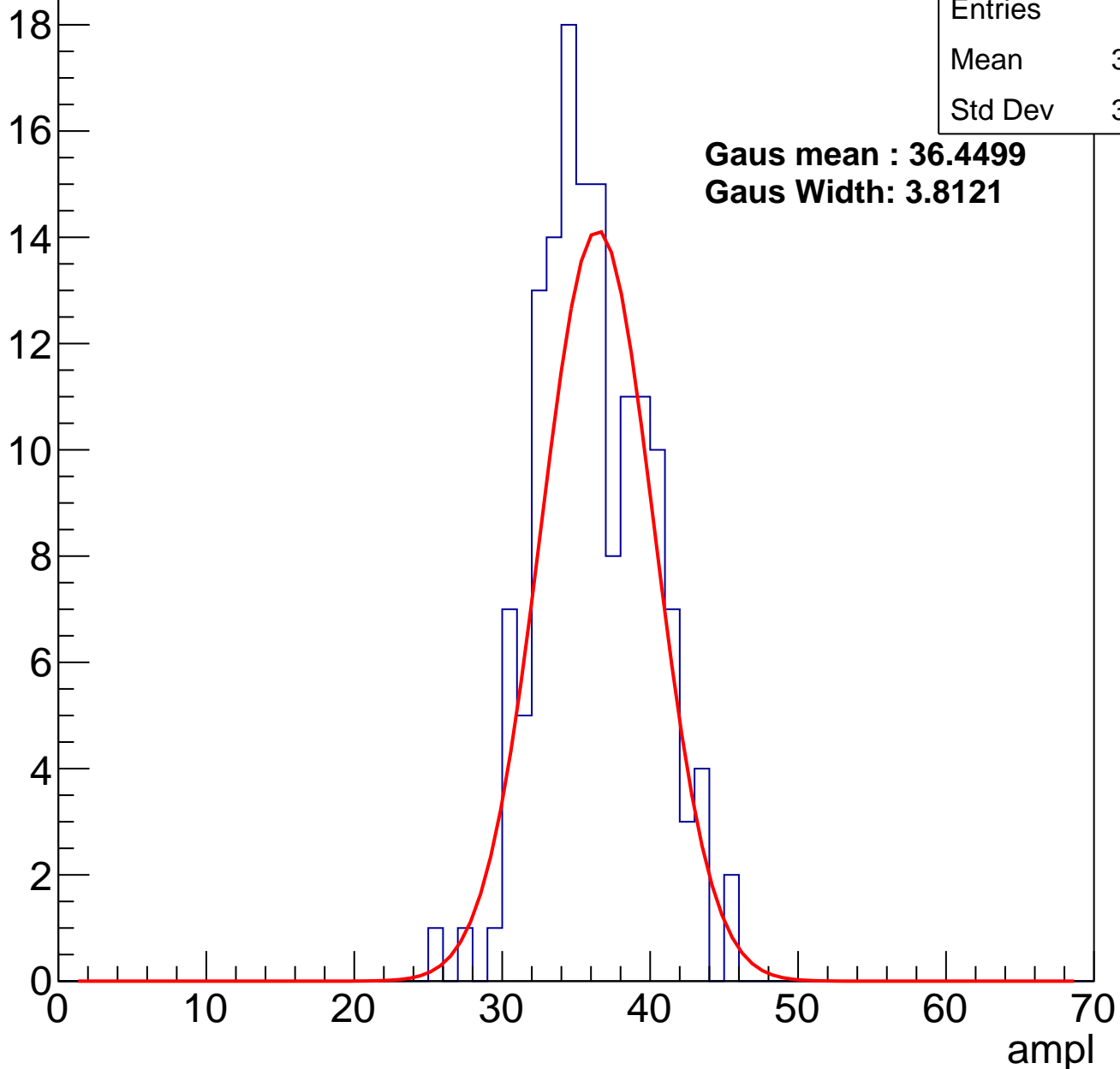
ampl



B0L101S, U21-ch59, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch59, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	42.59
Std Dev	3.645

Gaus mean : 43.3535

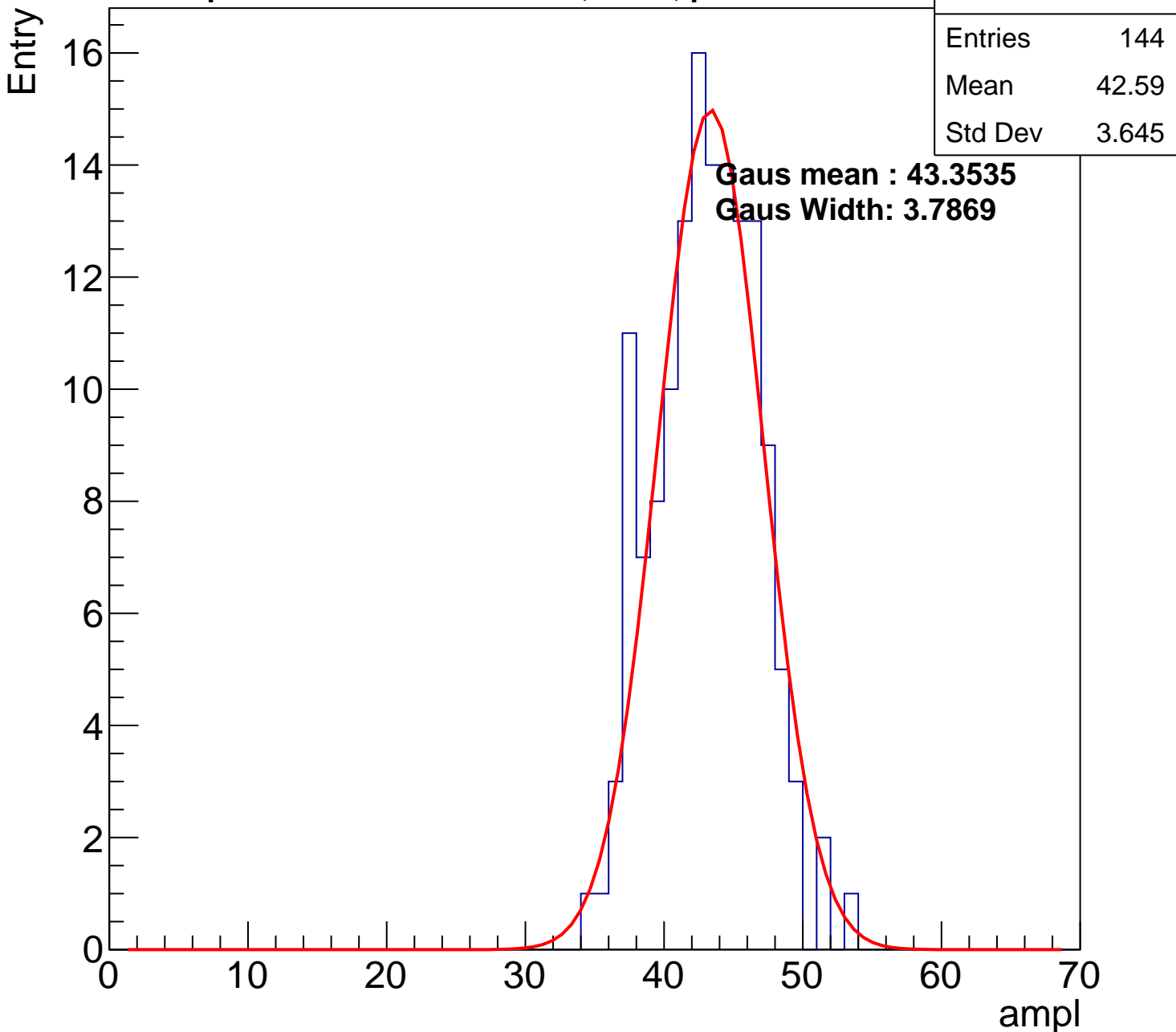
Gaus Width: 3.7869

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

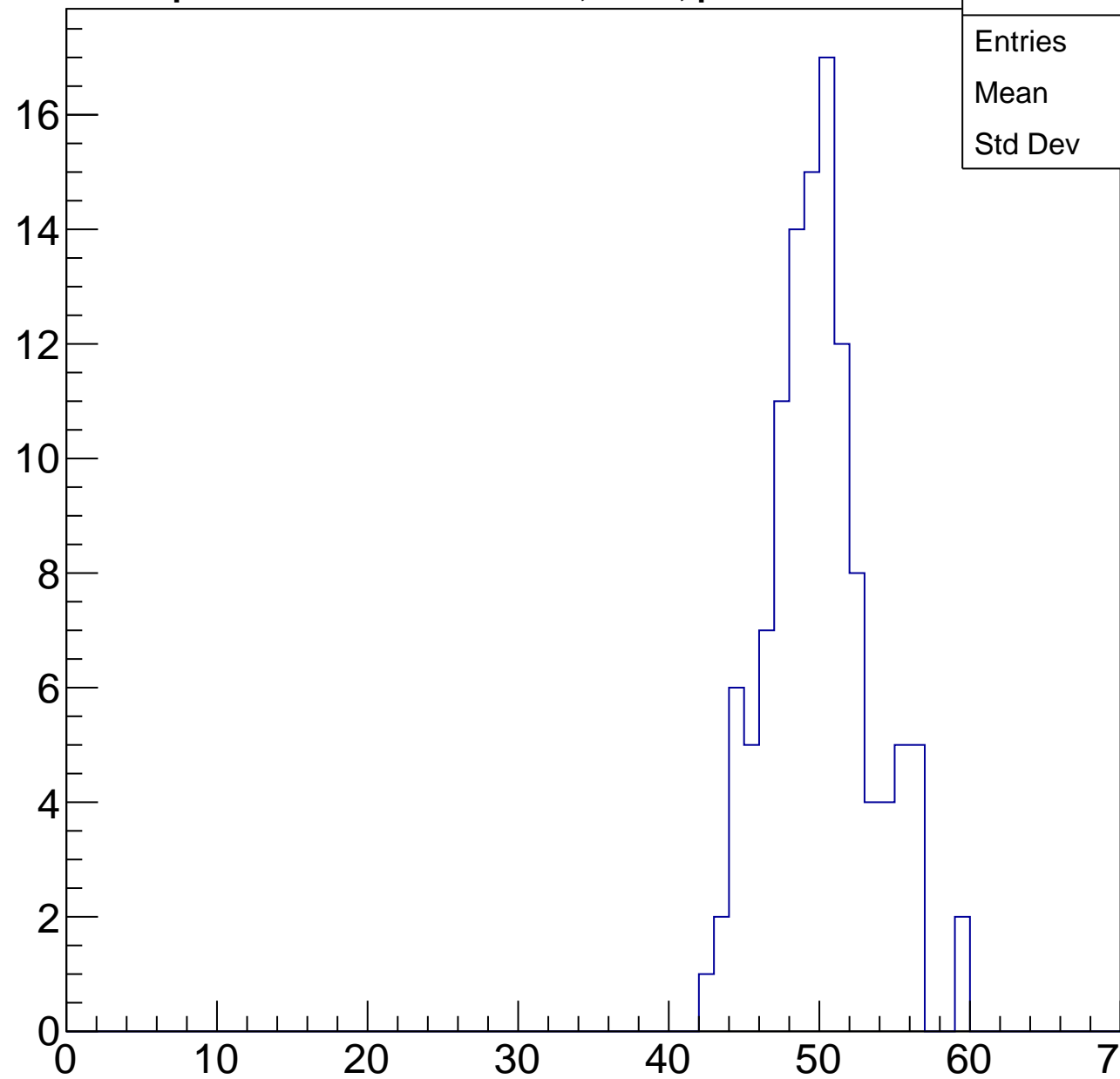
Entries	118
Mean	49.51
Std Dev	3.404

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch59, adc4

calib_packv5_042523_0143.root, FC#1, port C1

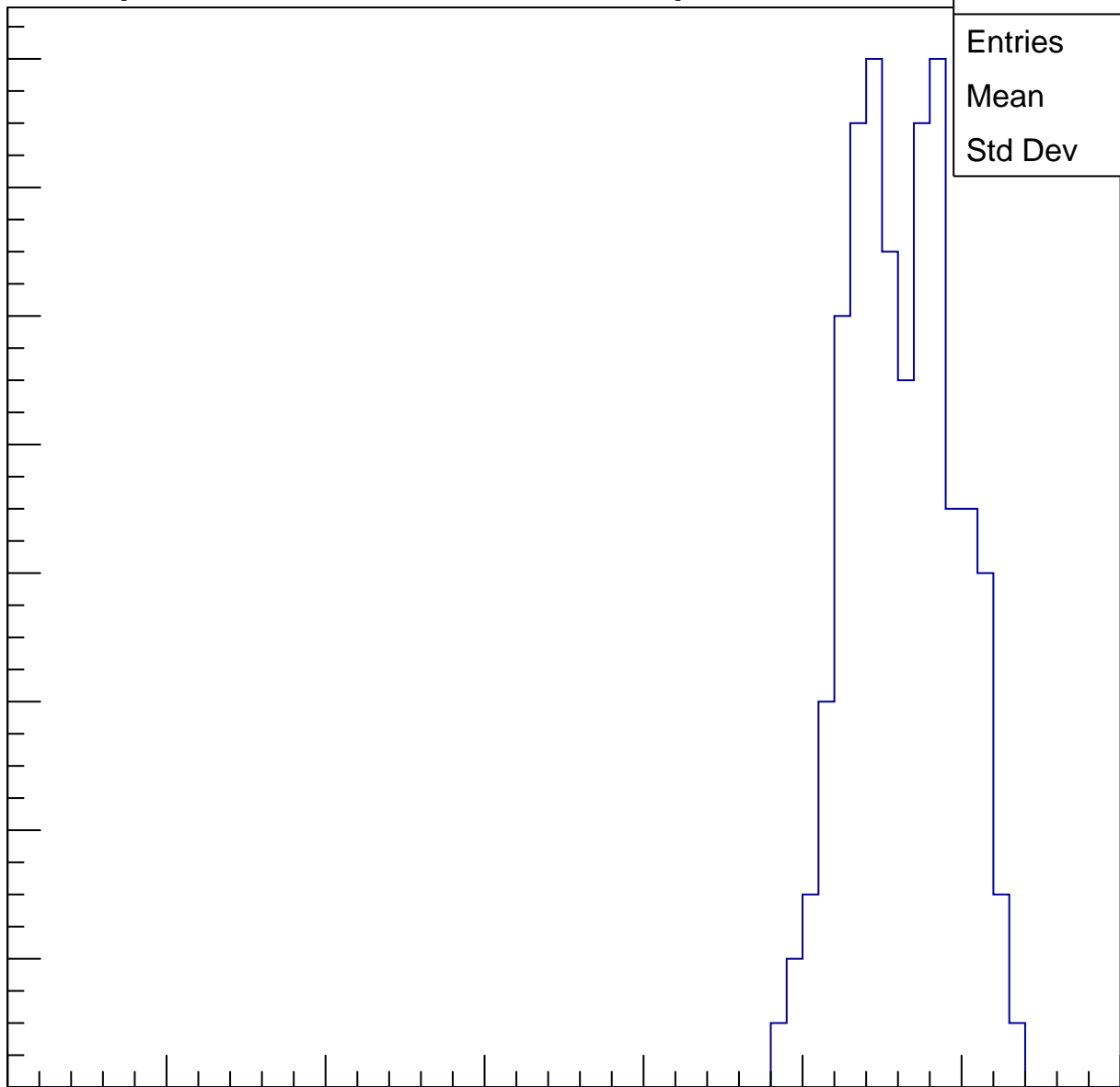
Entries	140
Mean	55.76
Std Dev	3.248

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

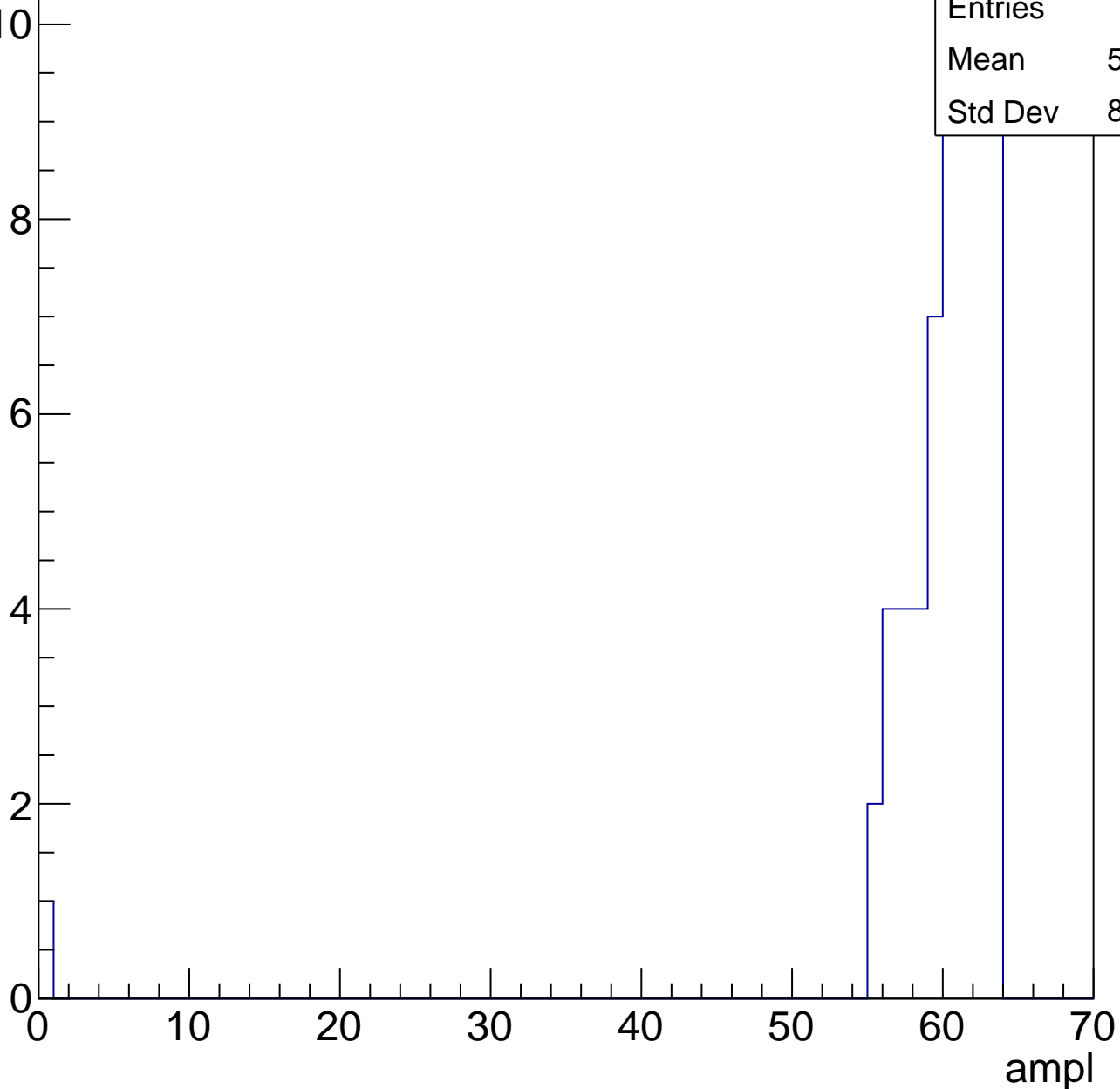


B0L101S, U21-ch59, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	60
Mean	59.07
Std Dev	8.012

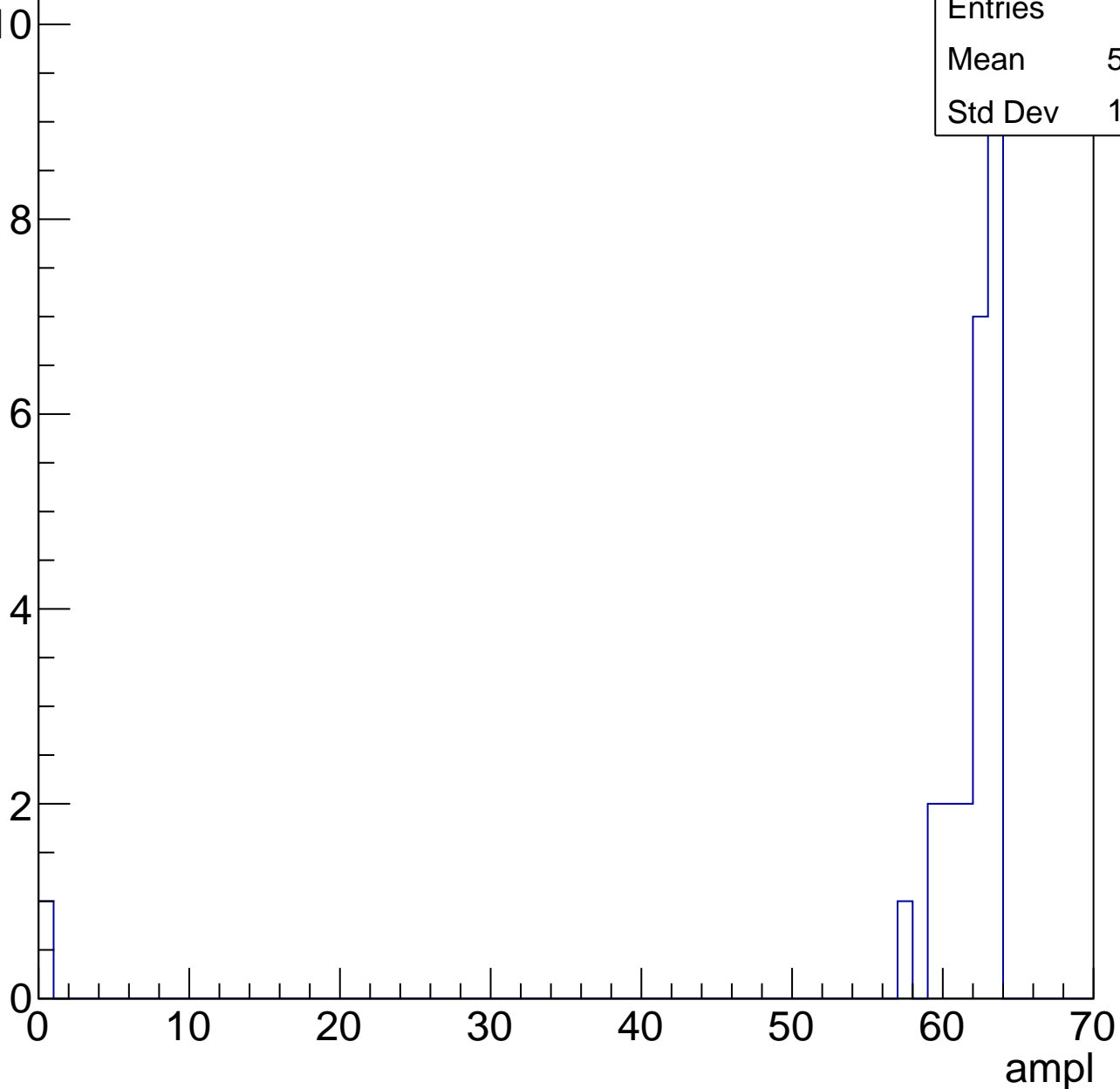


B0L101S, U21-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	25
Mean	59.24
Std Dev	12.19



B0L101S, U21-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch60, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

153

Mean

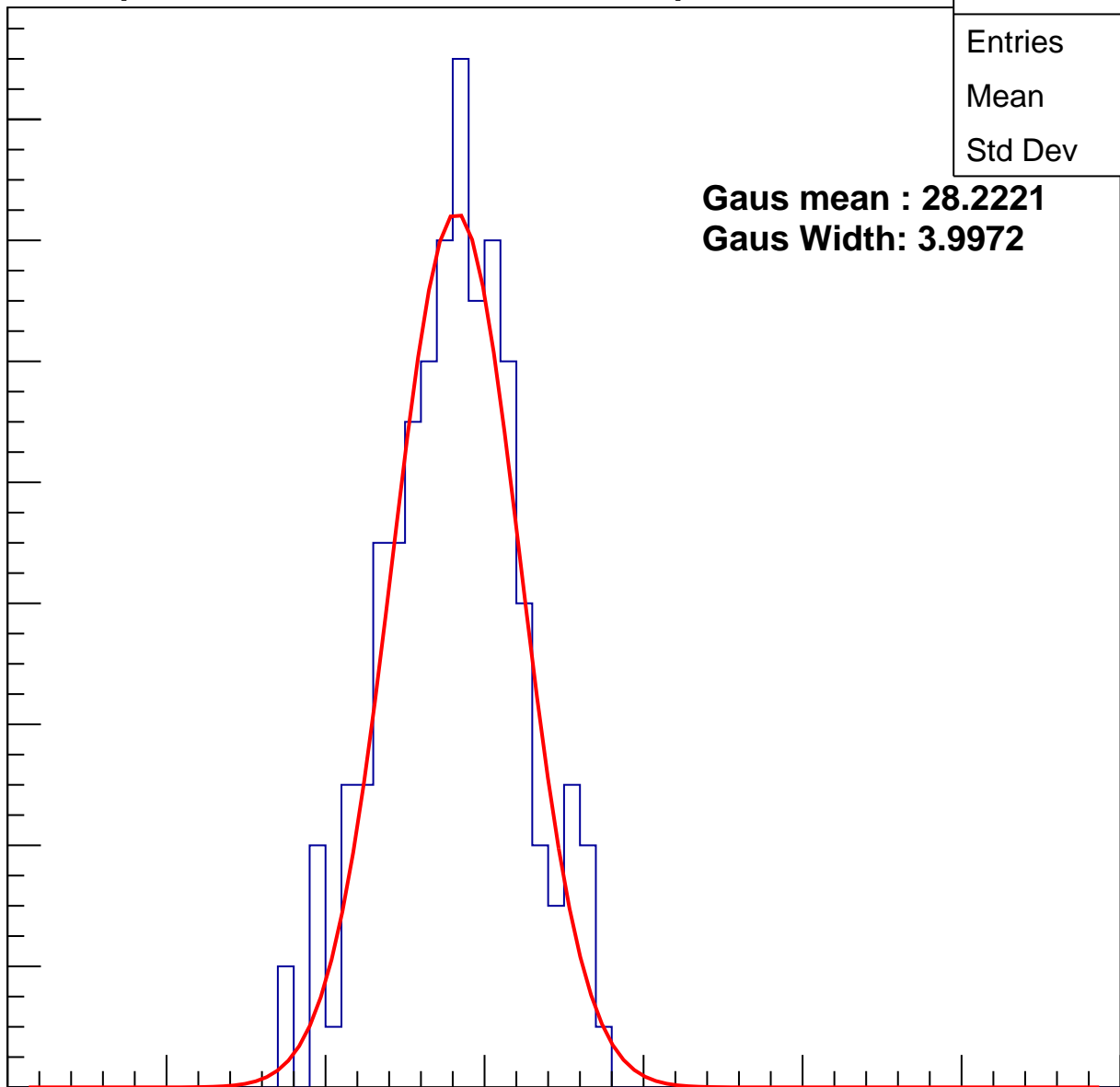
27.61

Std Dev

4.167

Gaus mean : 28.2221

Gaus Width: 3.9972



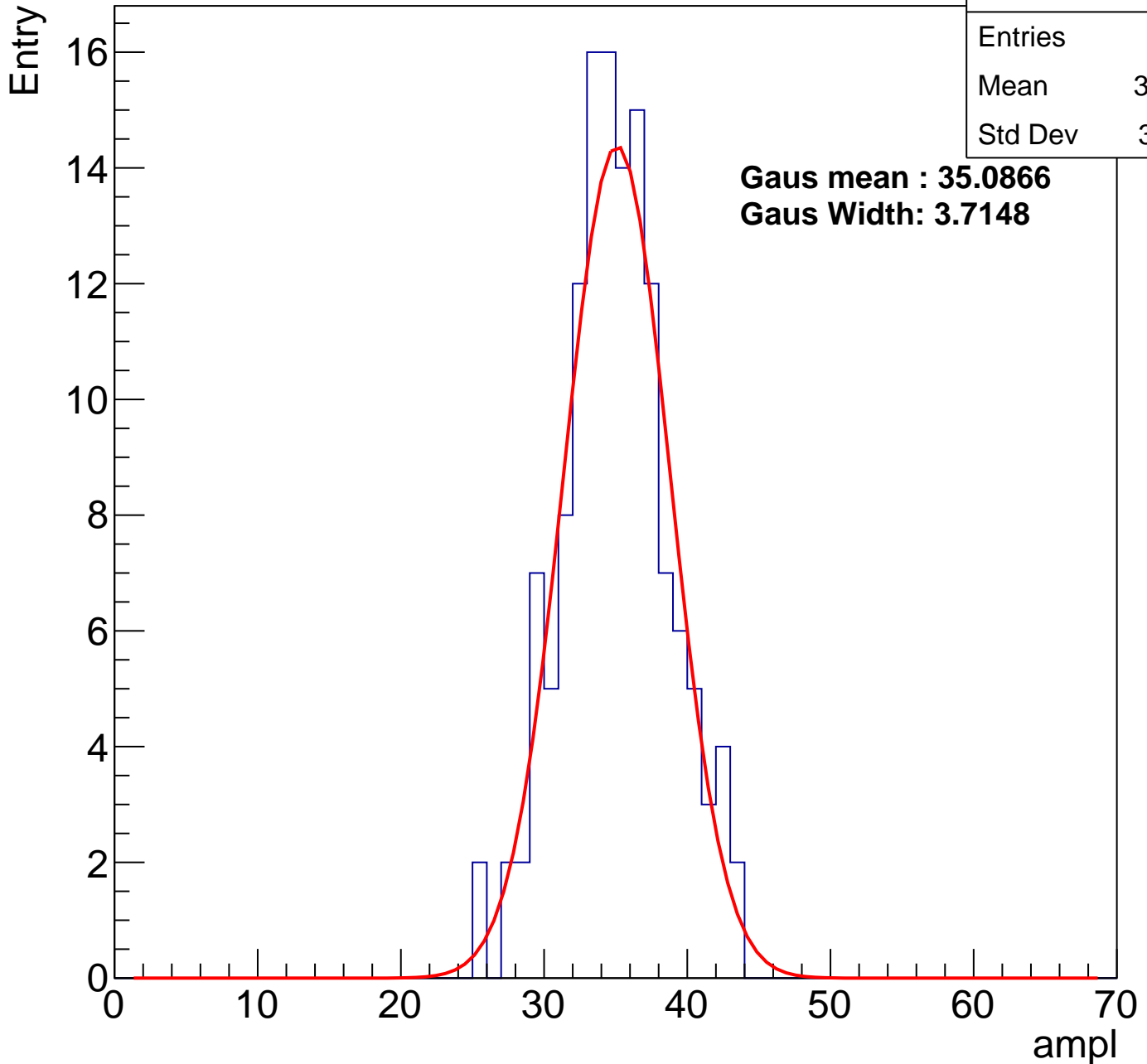
B0L101S, U21-ch60, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	34.55
Std Dev	3.681

Gaus mean : 35.0866

Gaus Width: 3.7148



B0L101S, U21-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	41.75
Std Dev	3.134

Gaus mean : 42.3085

Gaus Width: 2.9730

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

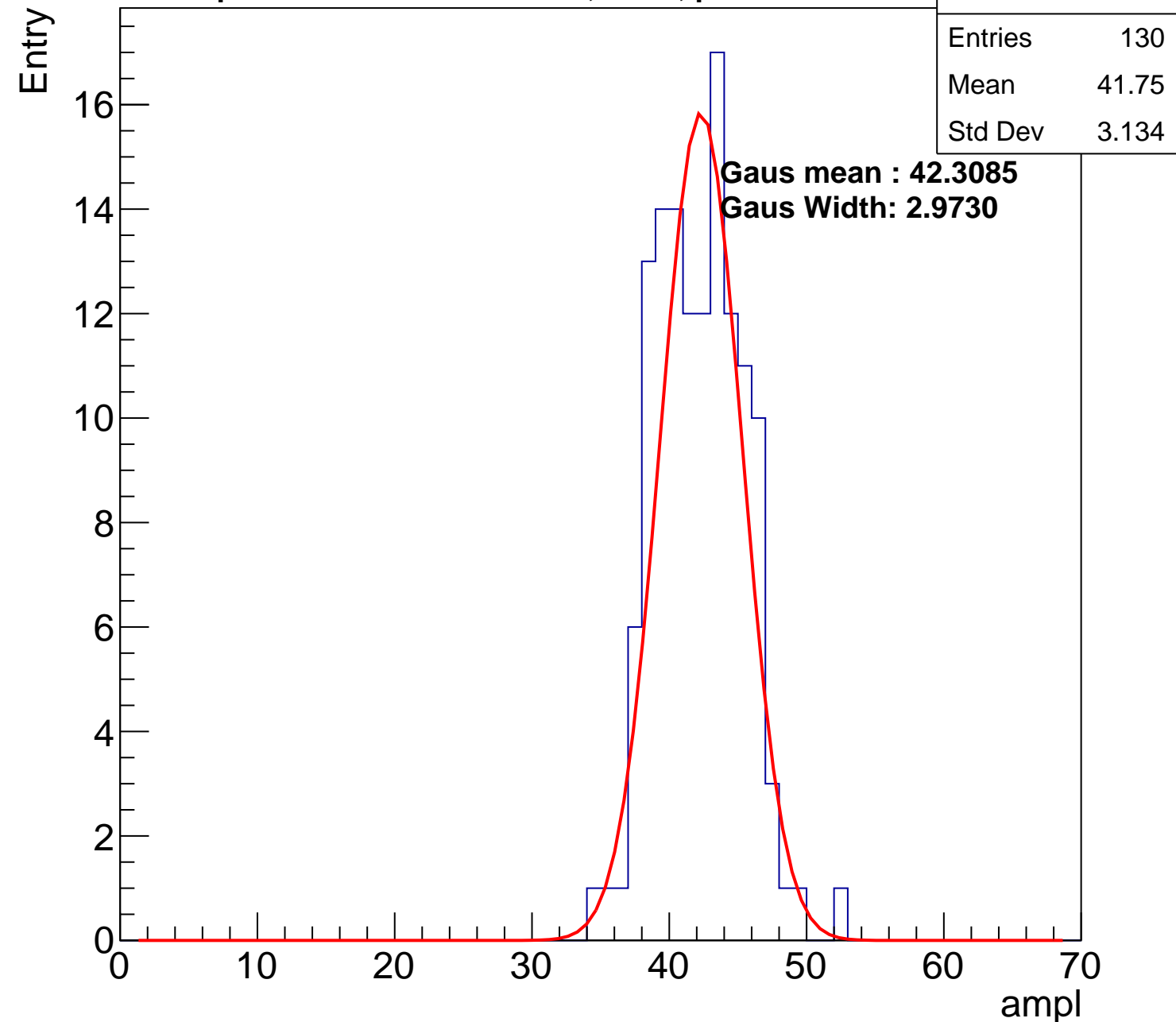
40

50

60

70

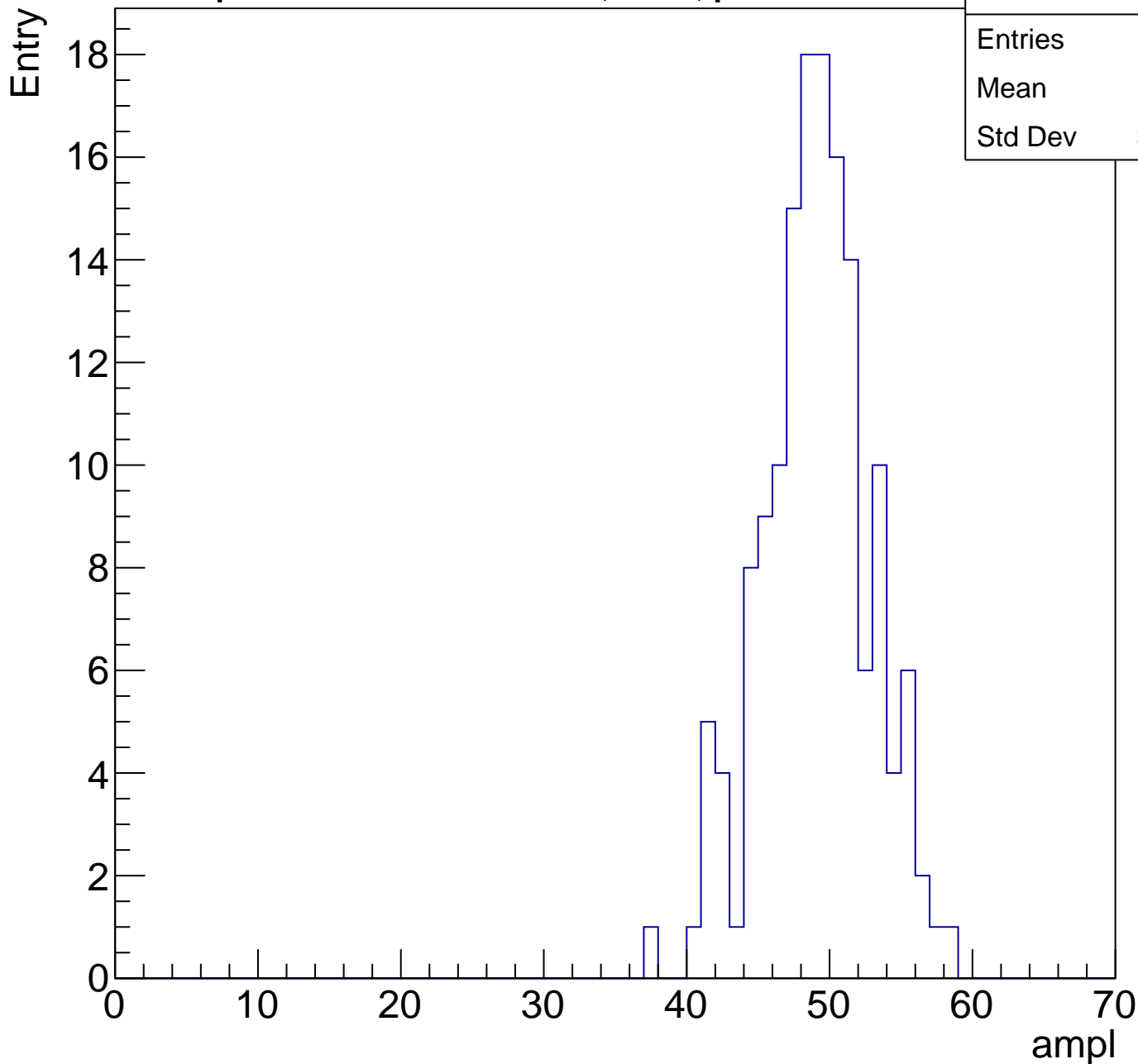
ampl



B0L101S, U21-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

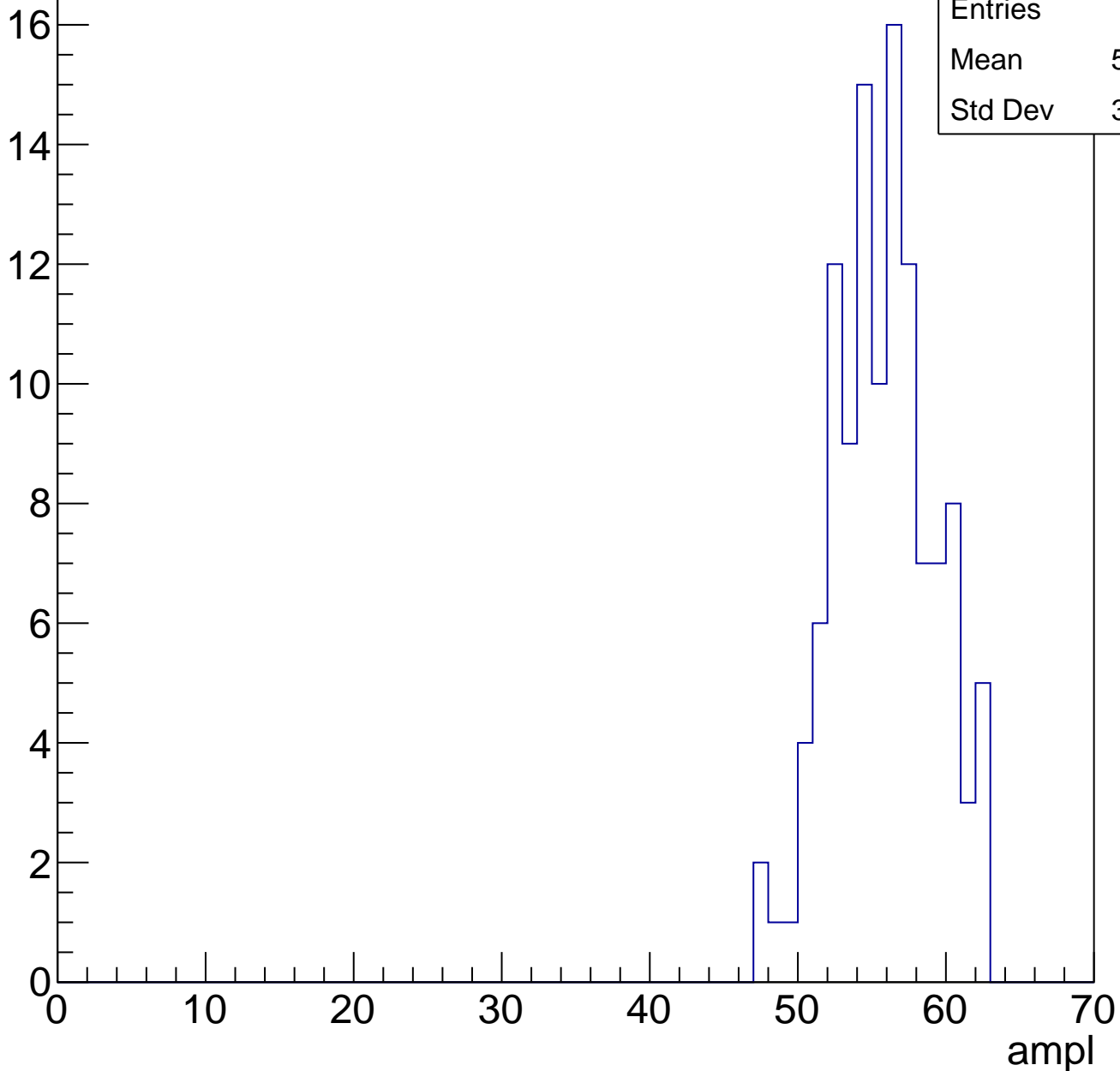
Entries	150
Mean	48.6
Std Dev	3.751



B0L101S, U21-ch60, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch60, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	87
Mean	59.59
Std Dev	2.535

0

2

4

6

8

10

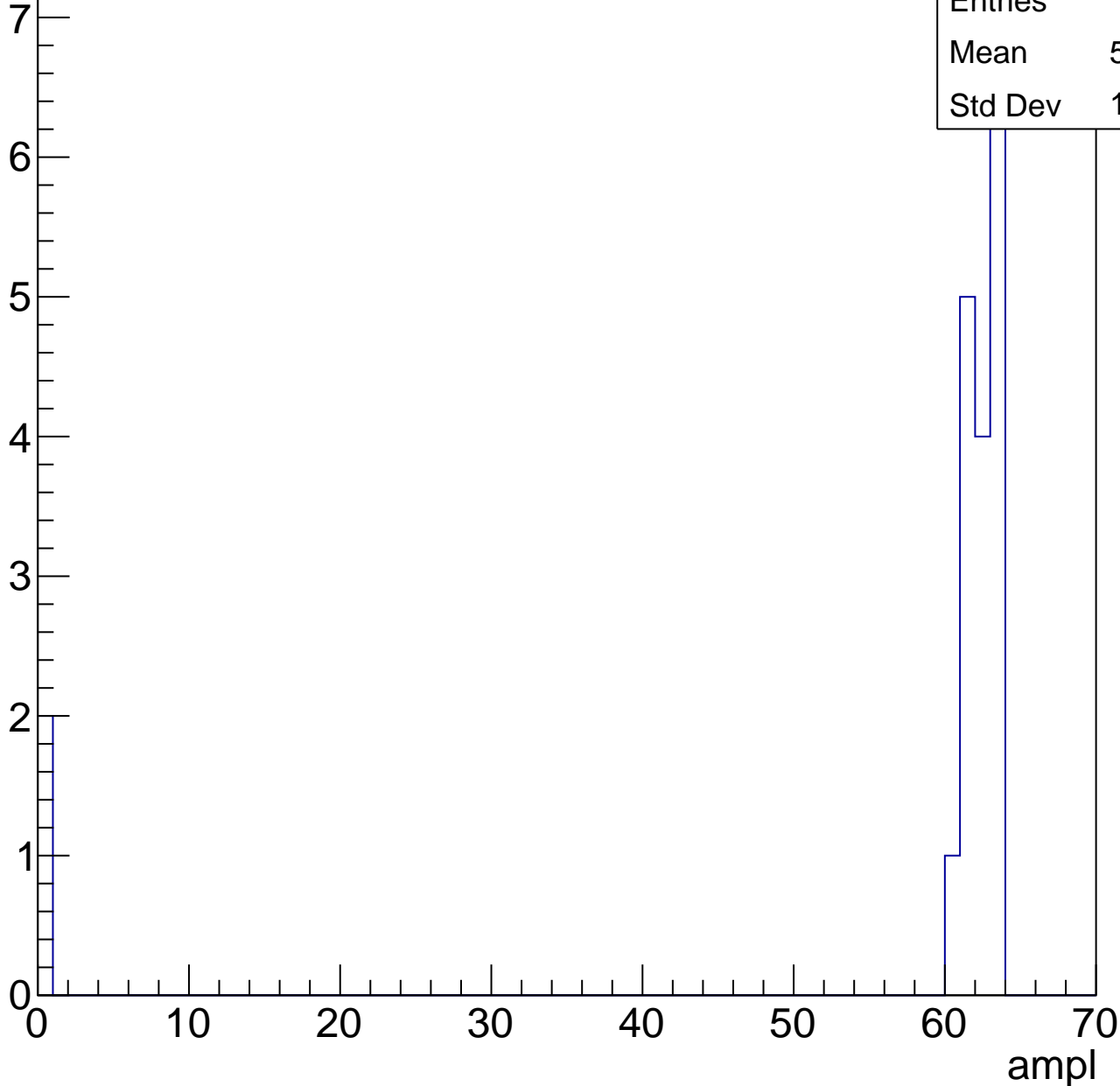
12

B0L101S, U21-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	55.47
Std Dev	19.05



B0L101S, U21-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

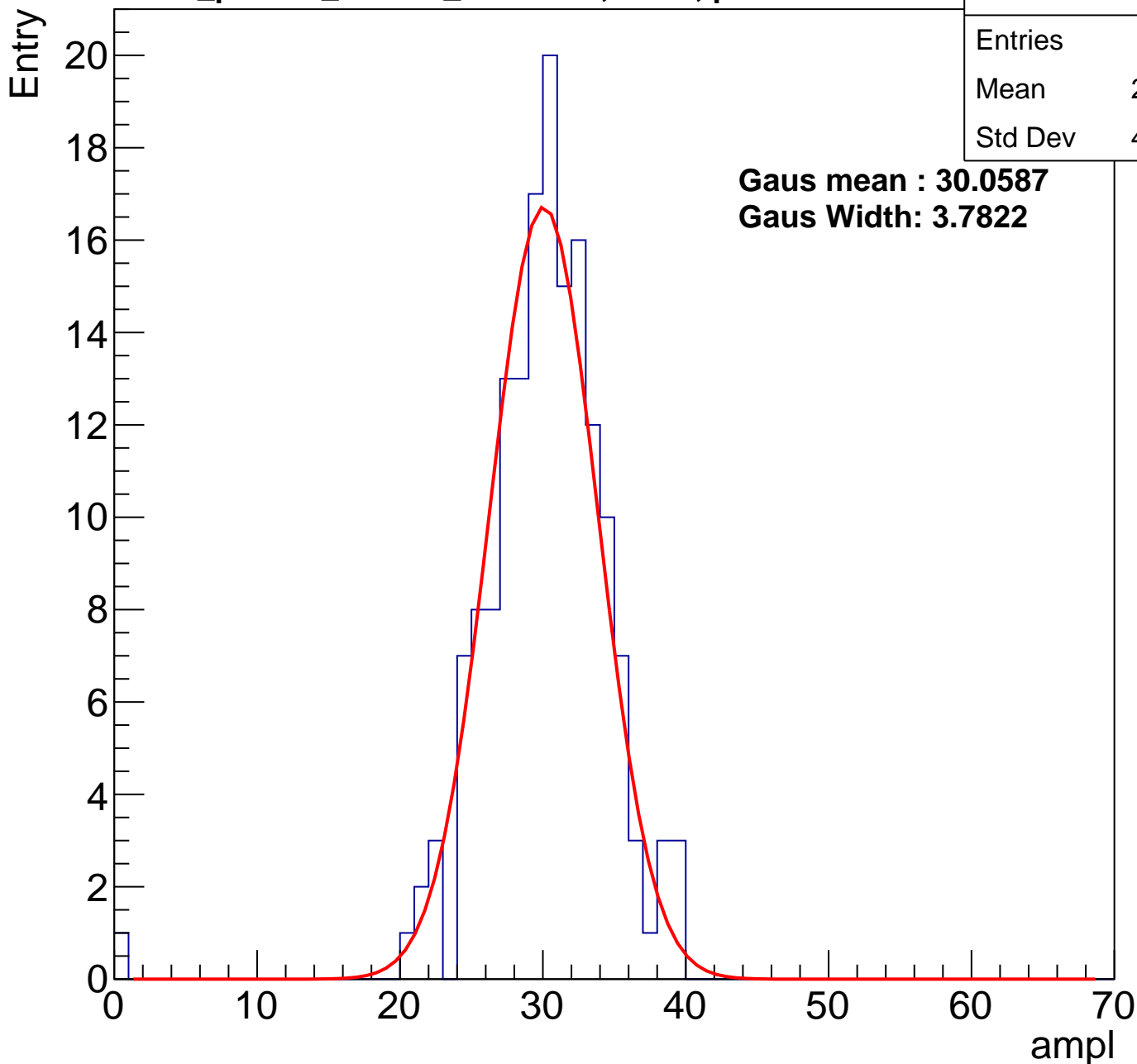
B0L101S, U21-ch61, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.73
Std Dev	4.446

Gaus mean : 30.0587

Gaus Width: 3.7822



B0L101S, U21-ch61, adc1

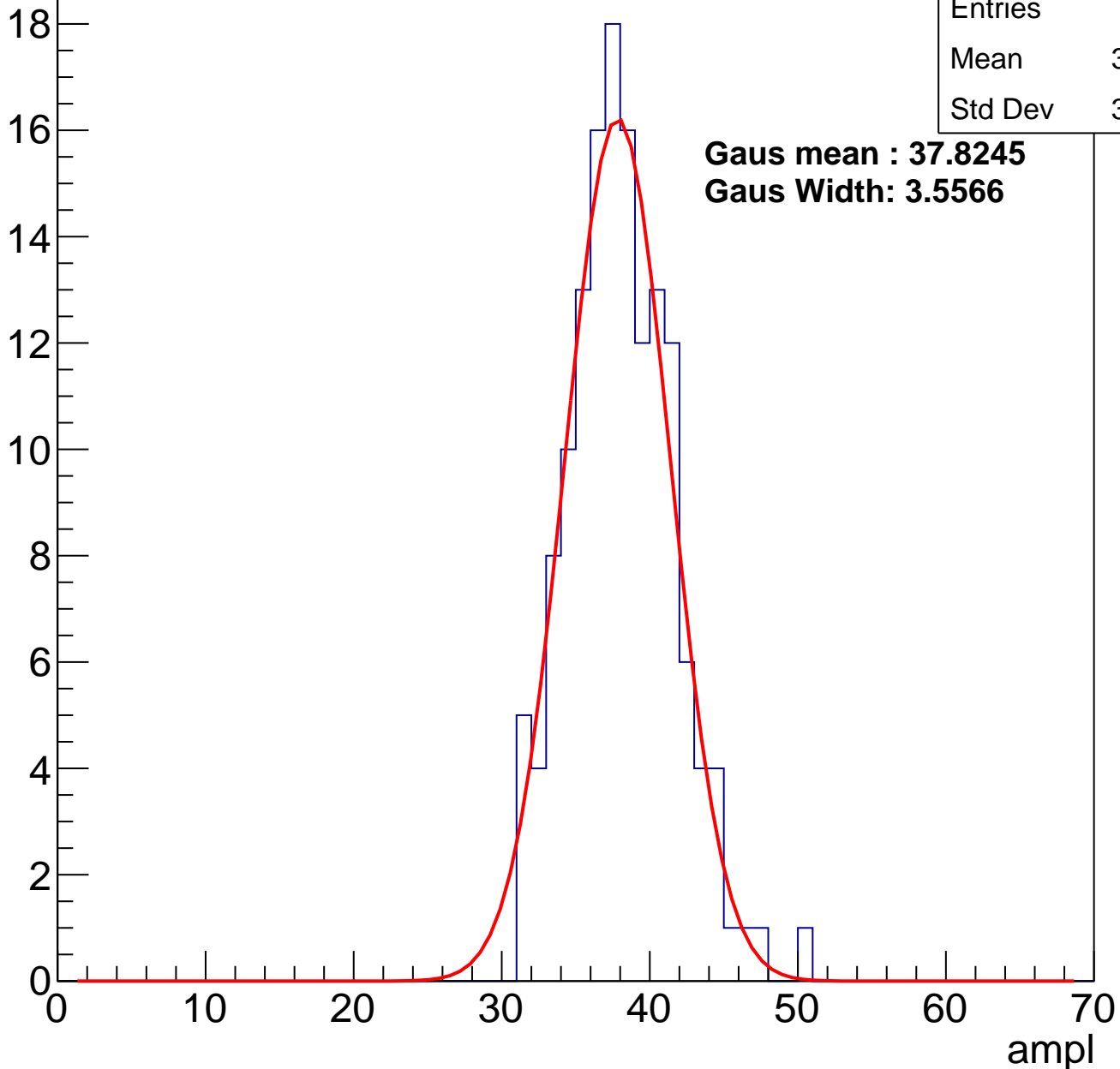
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	37.66
Std Dev	3.504

Gaus mean : 37.8245

Gaus Width: 3.5566

Entry



B0L101S, U21-ch61, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

146

Mean

45.03

Std Dev

3.594

Gaus mean : 45.3404

Gaus Width: 3.3088

0

10

20

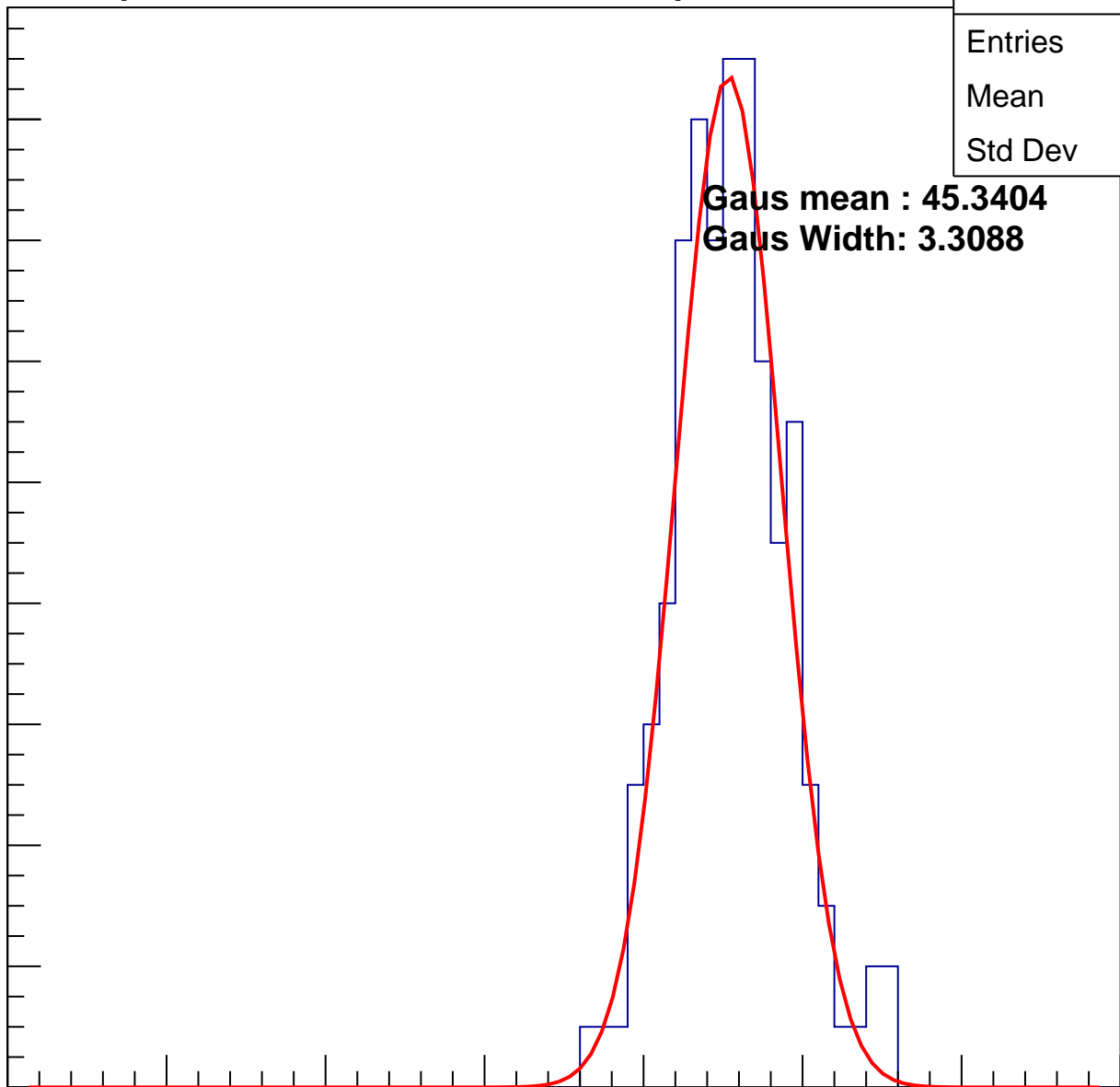
30

40

50

60

ampl



B0L101S, U21-ch61, adc3

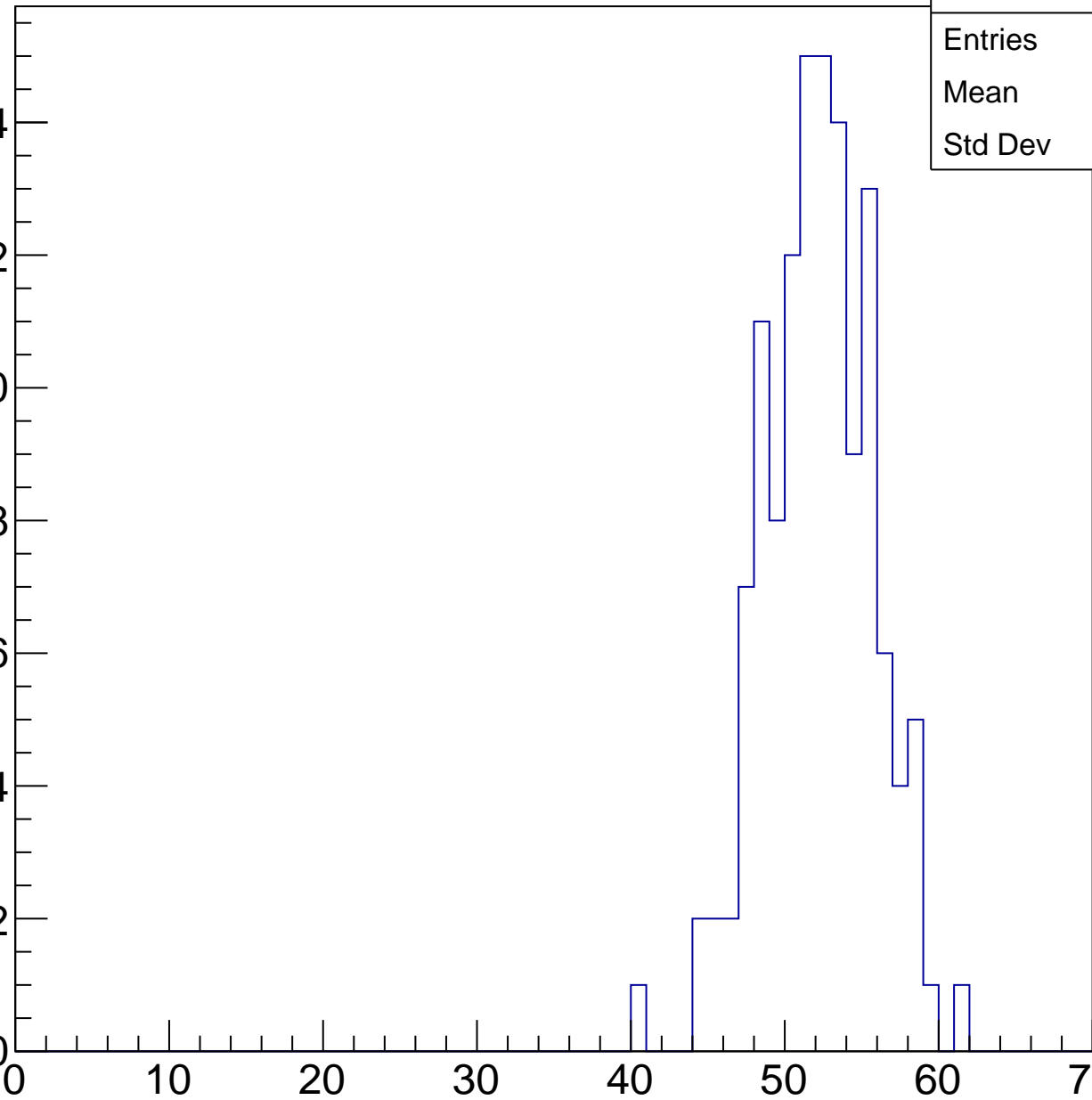
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	51.73
Std Dev	3.526

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch61, adc4

calib_packv5_042523_0143.root, FC#1, port C1

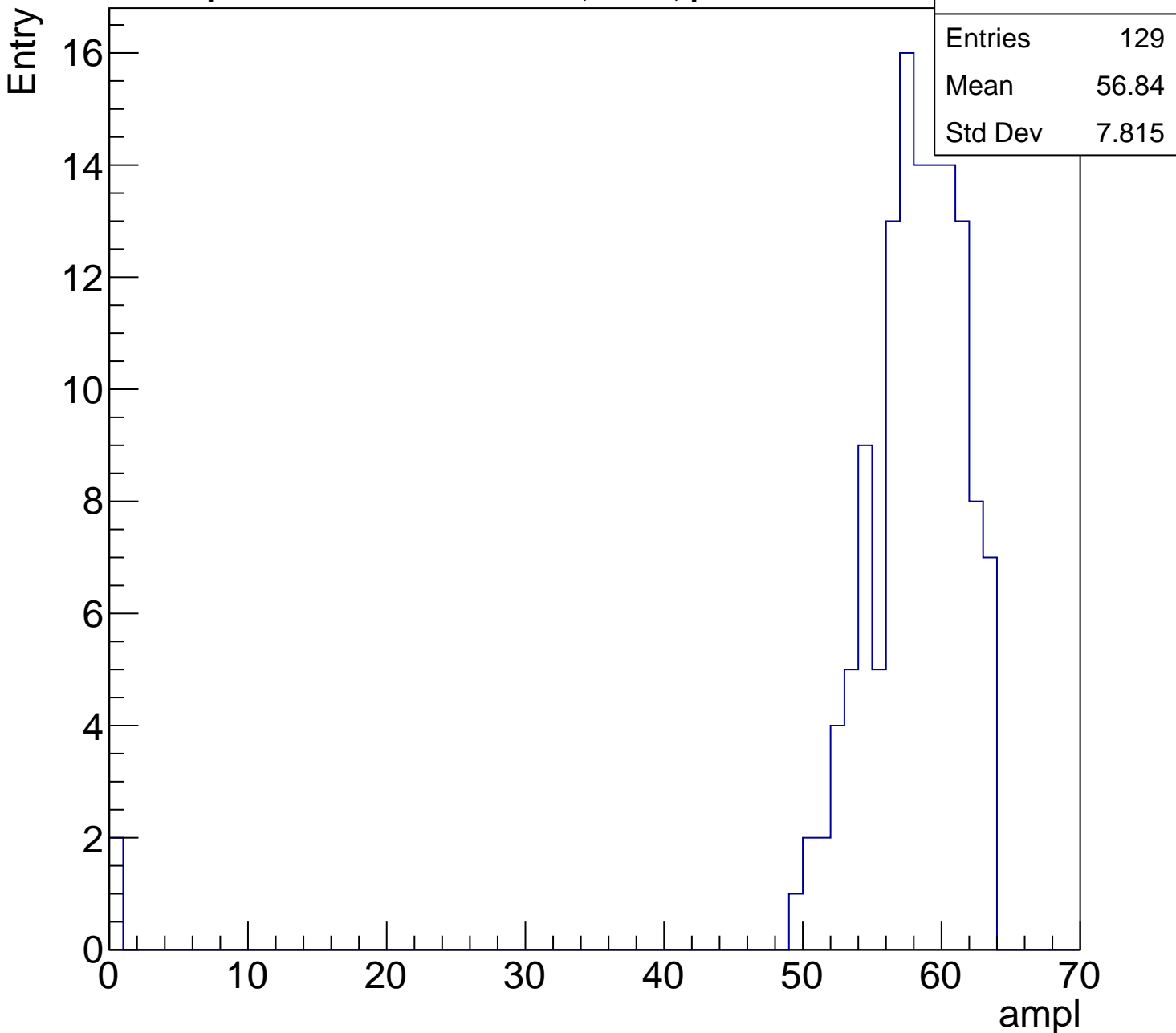
Entries	129
Mean	56.84
Std Dev	7.815

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch61, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

41

Mean

61.32

Std Dev

1.688

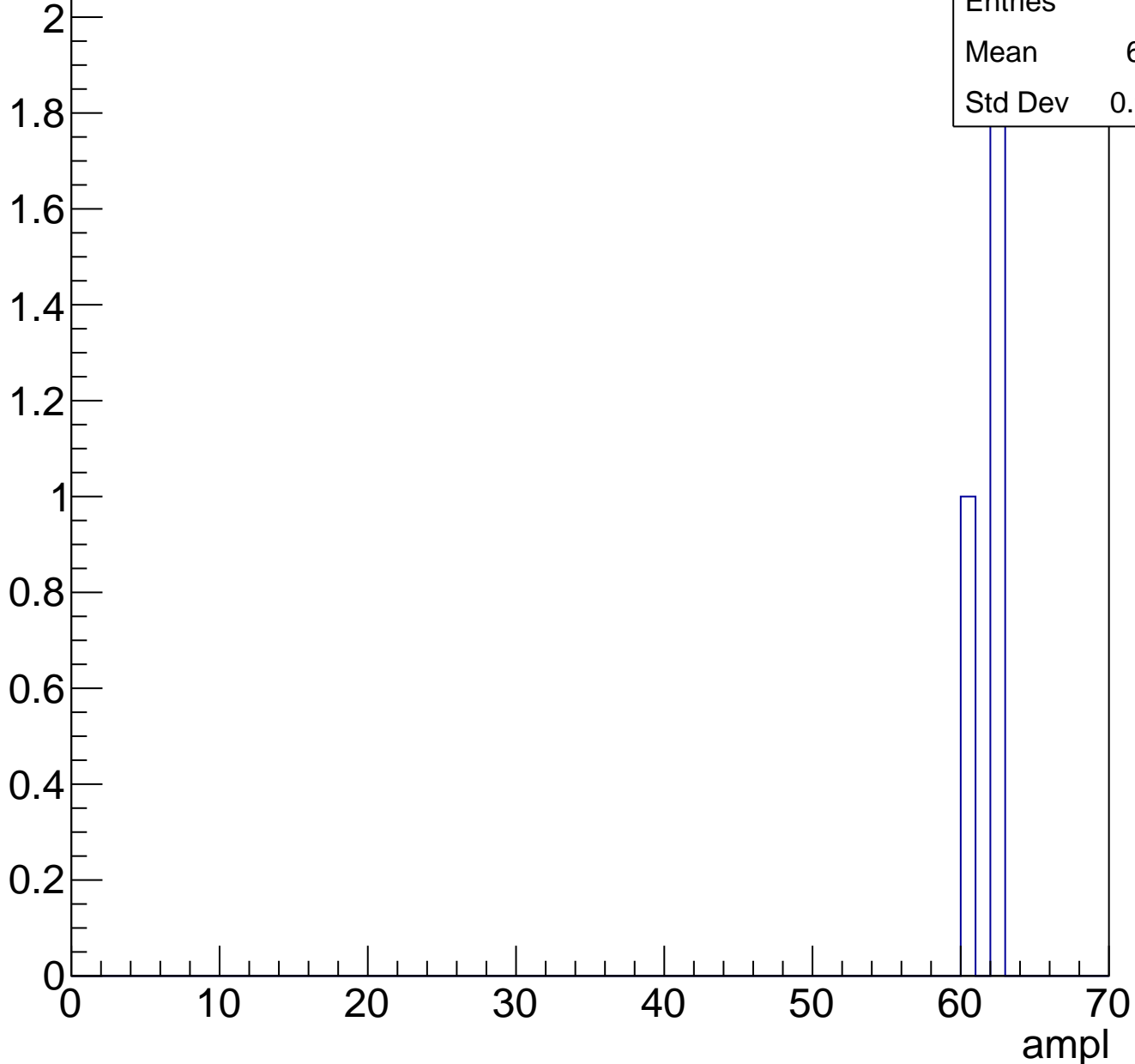
ampl

0 10 20 30 40 50 60 70

B0L101S, U21-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch62, adc0

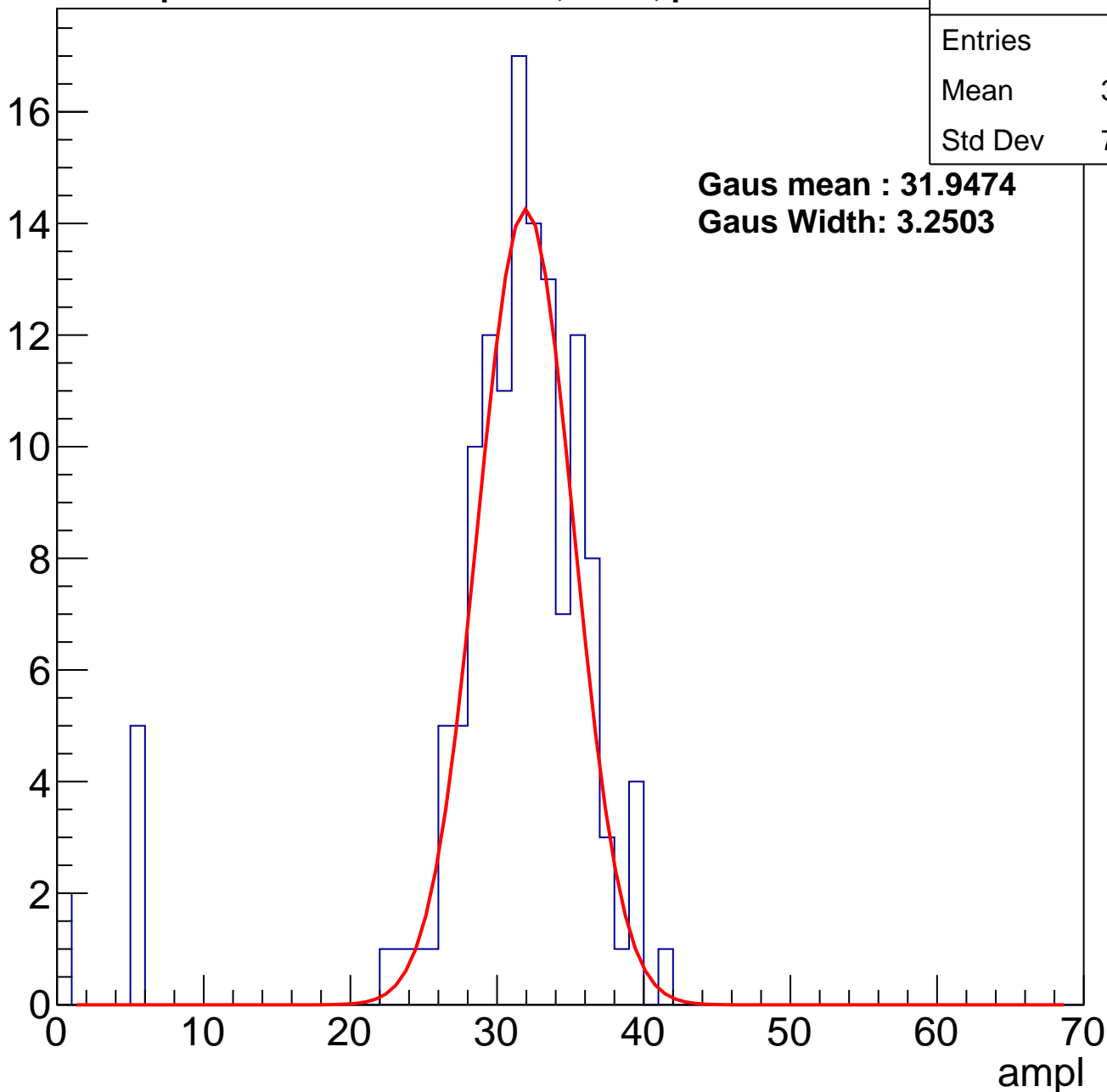
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	30.13
Std Dev	7.134

Gaus mean : 31.9474

Gaus Width: 3.2503

Entry



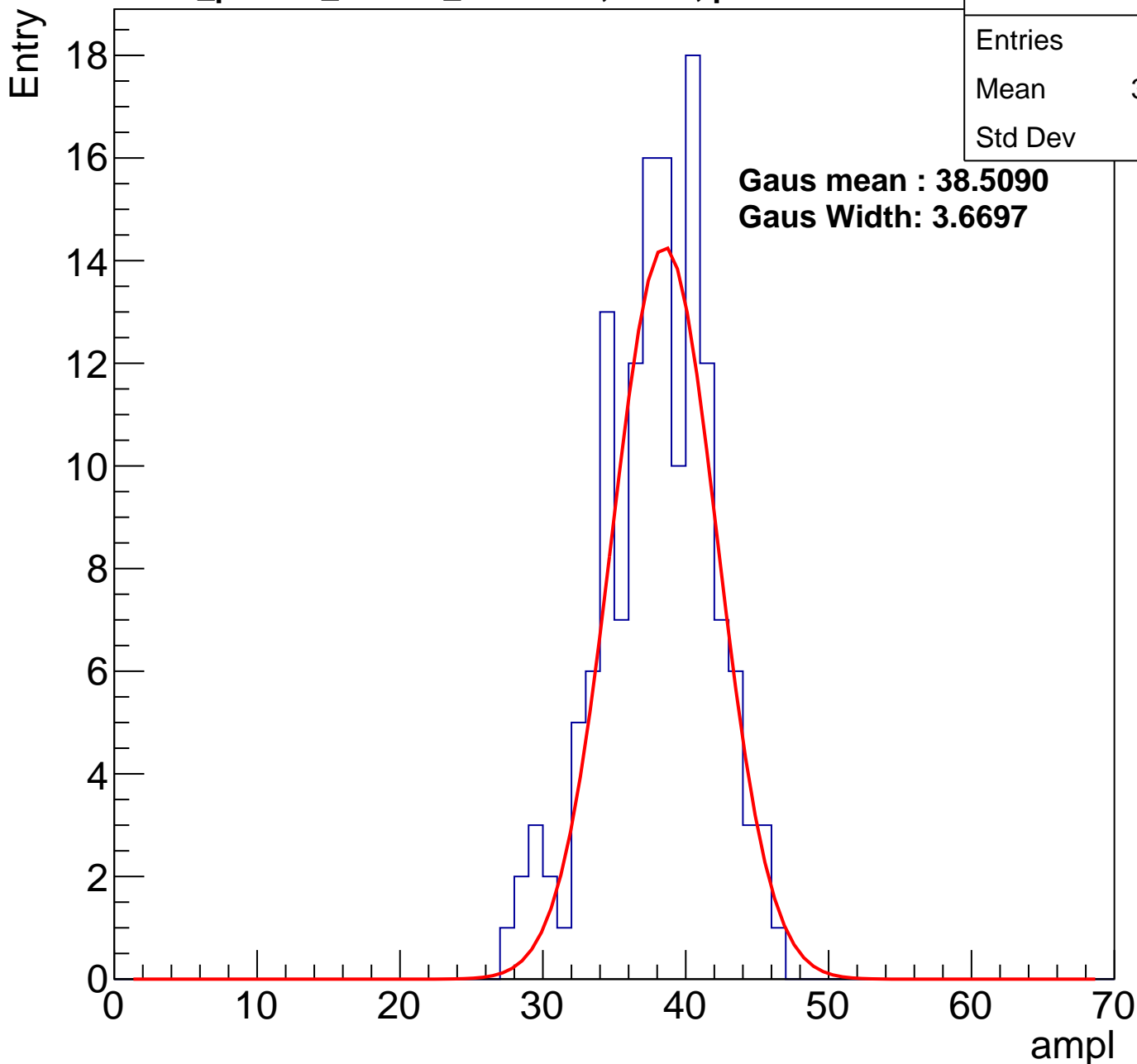
B0L101S, U21-ch62, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	37.53
Std Dev	3.86

Gaus mean : 38.5090

Gaus Width: 3.6697



B0L101S, U21-ch62, adc2

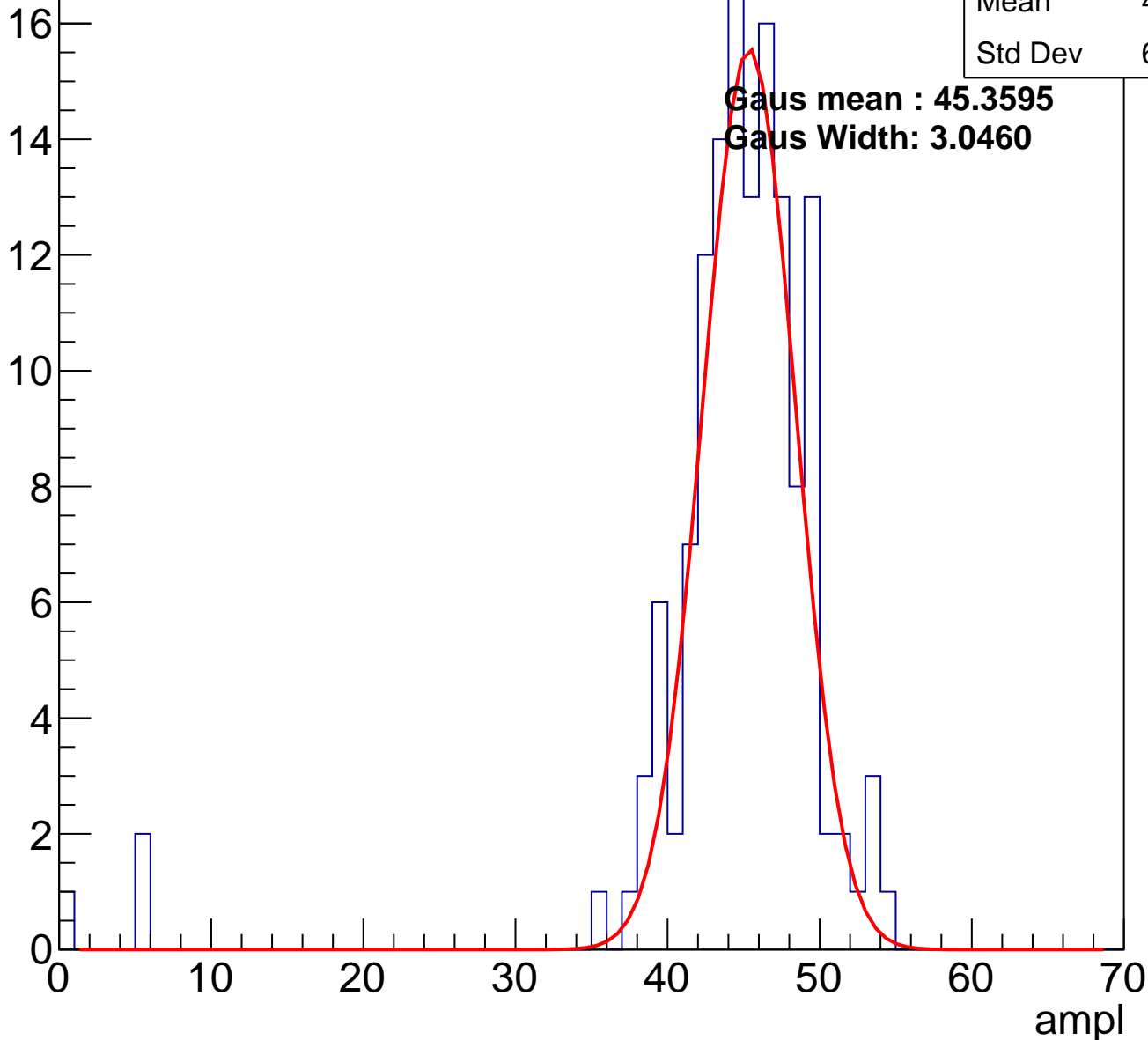
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	138
Mean	43.99
Std Dev	6.992

Gaus mean : 45.3595

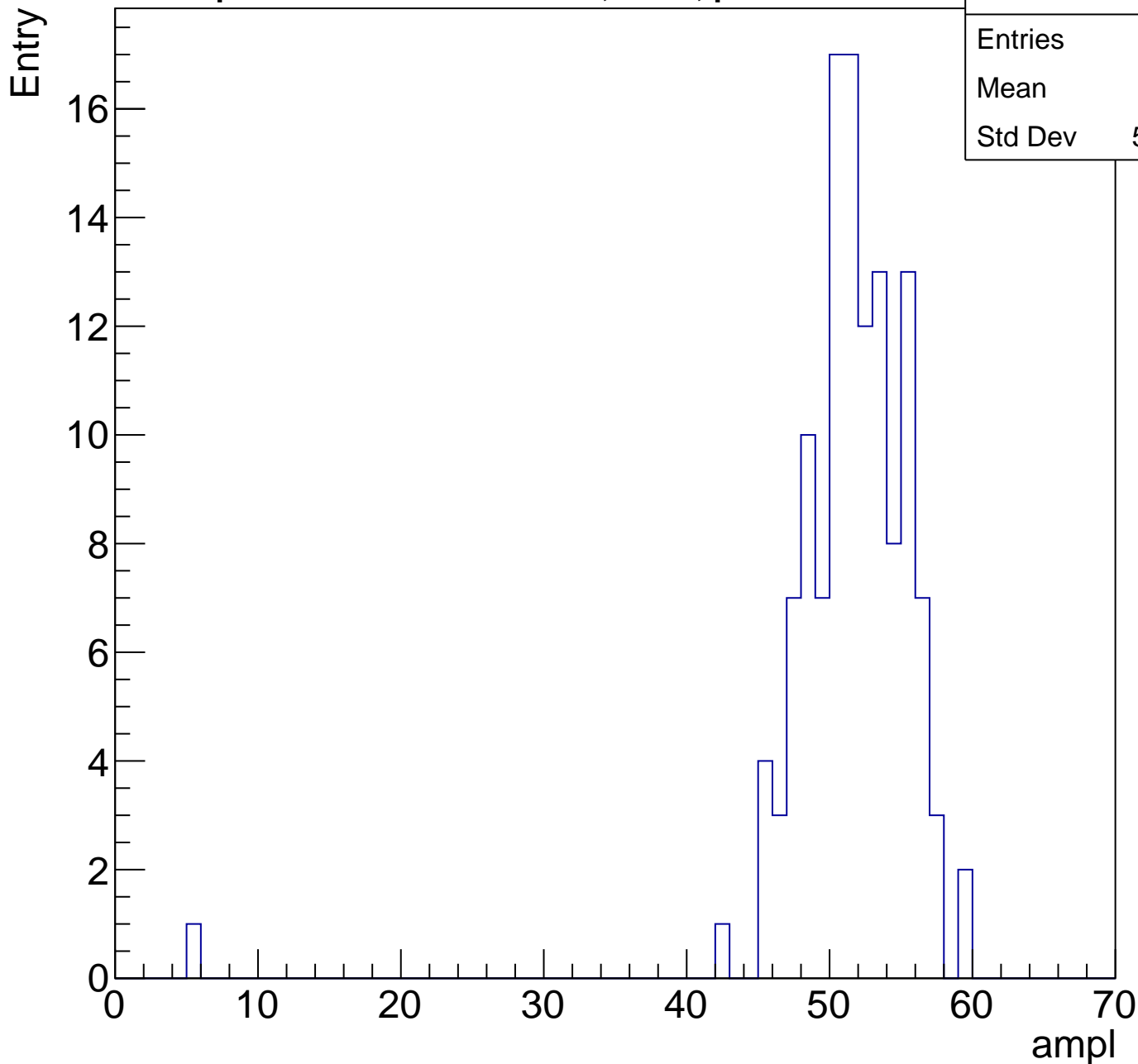
Gaus Width: 3.0460



B0L101S, U21-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	51
Std Dev	5.229



B0L101S, U21-ch62, adc4

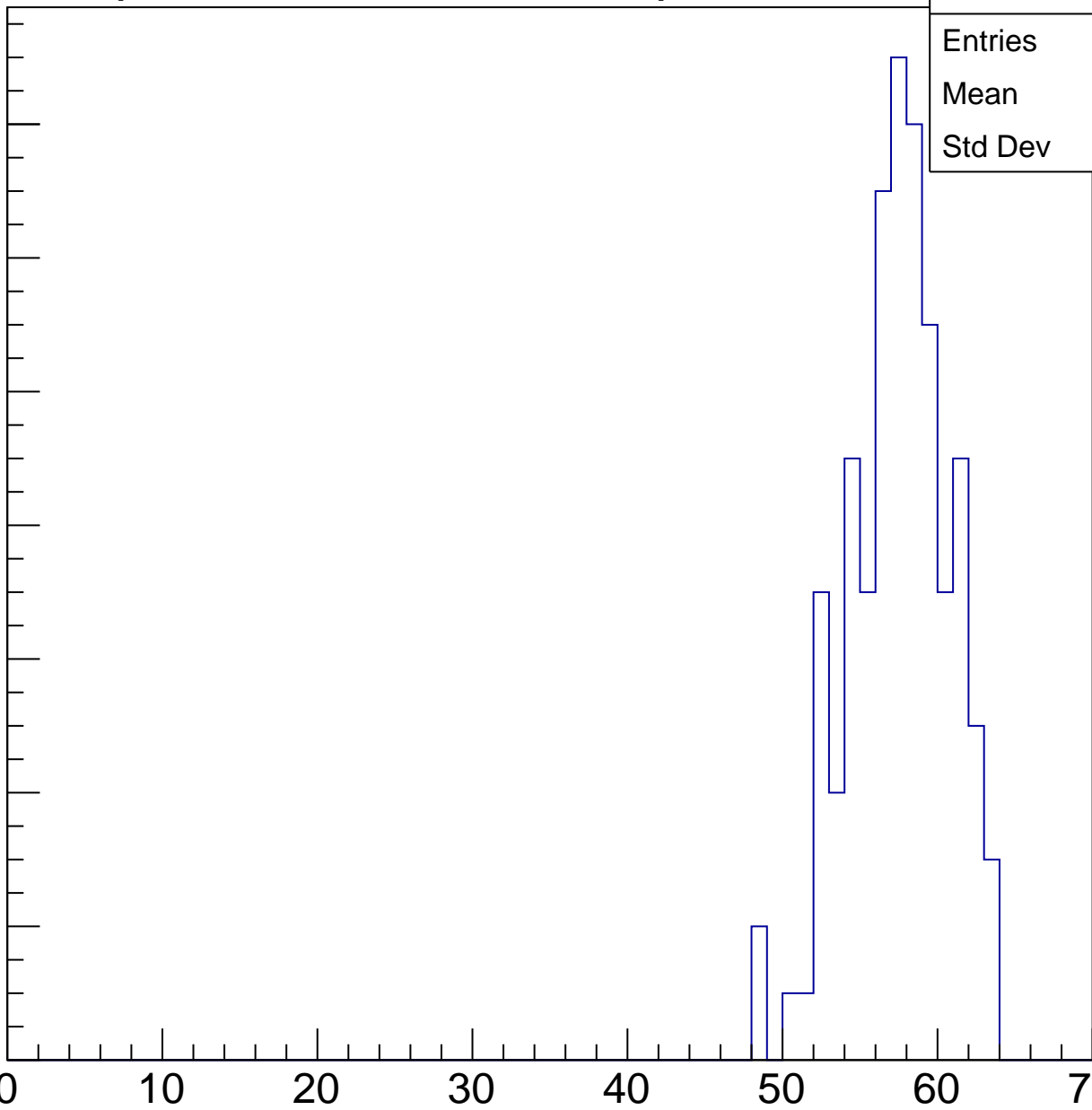
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	57
Std Dev	3.2

ampl



B0L101S, U21-ch62, adc5

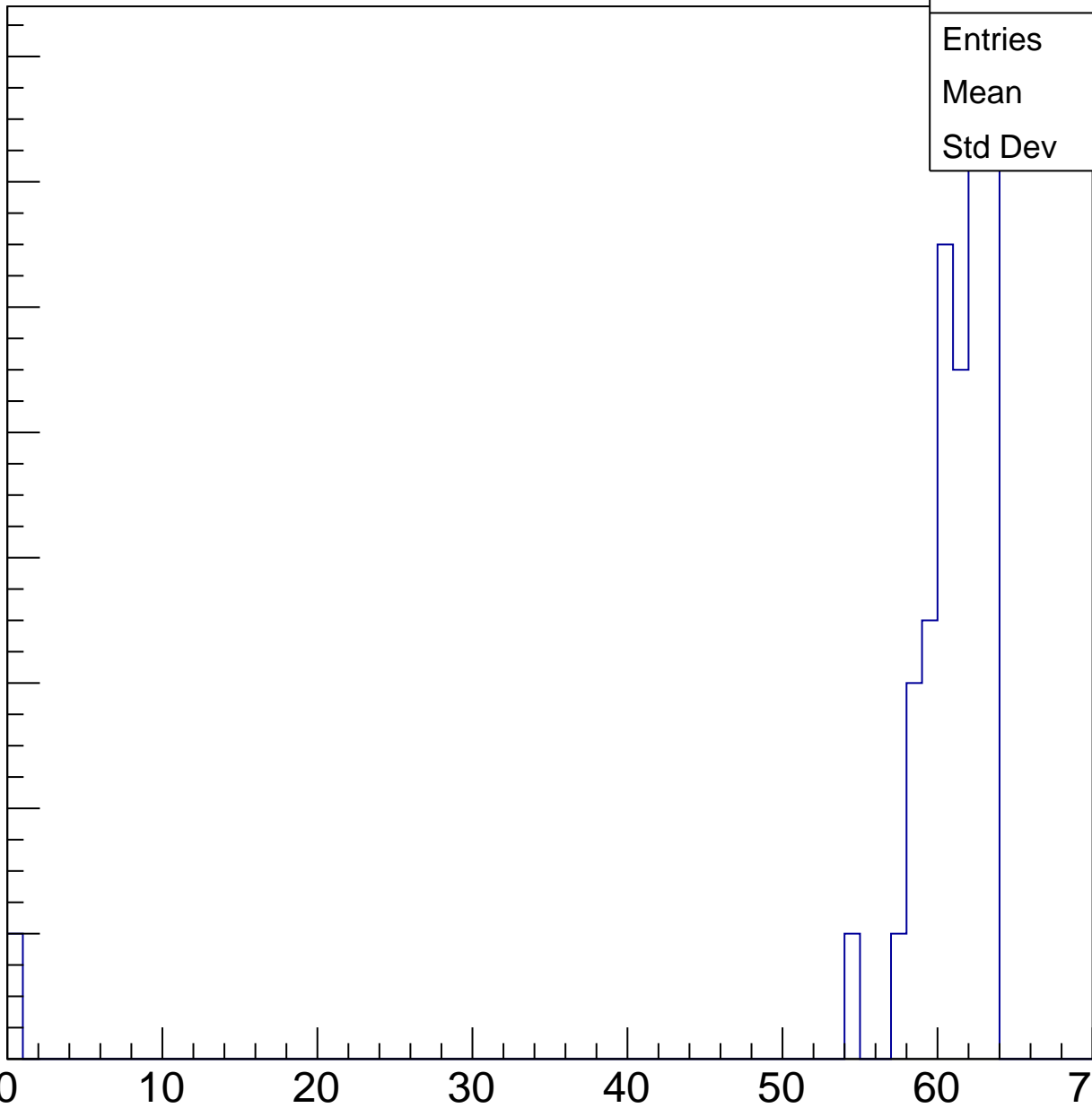
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.08
Std Dev	10.05

ampl



B0L101S, U21-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

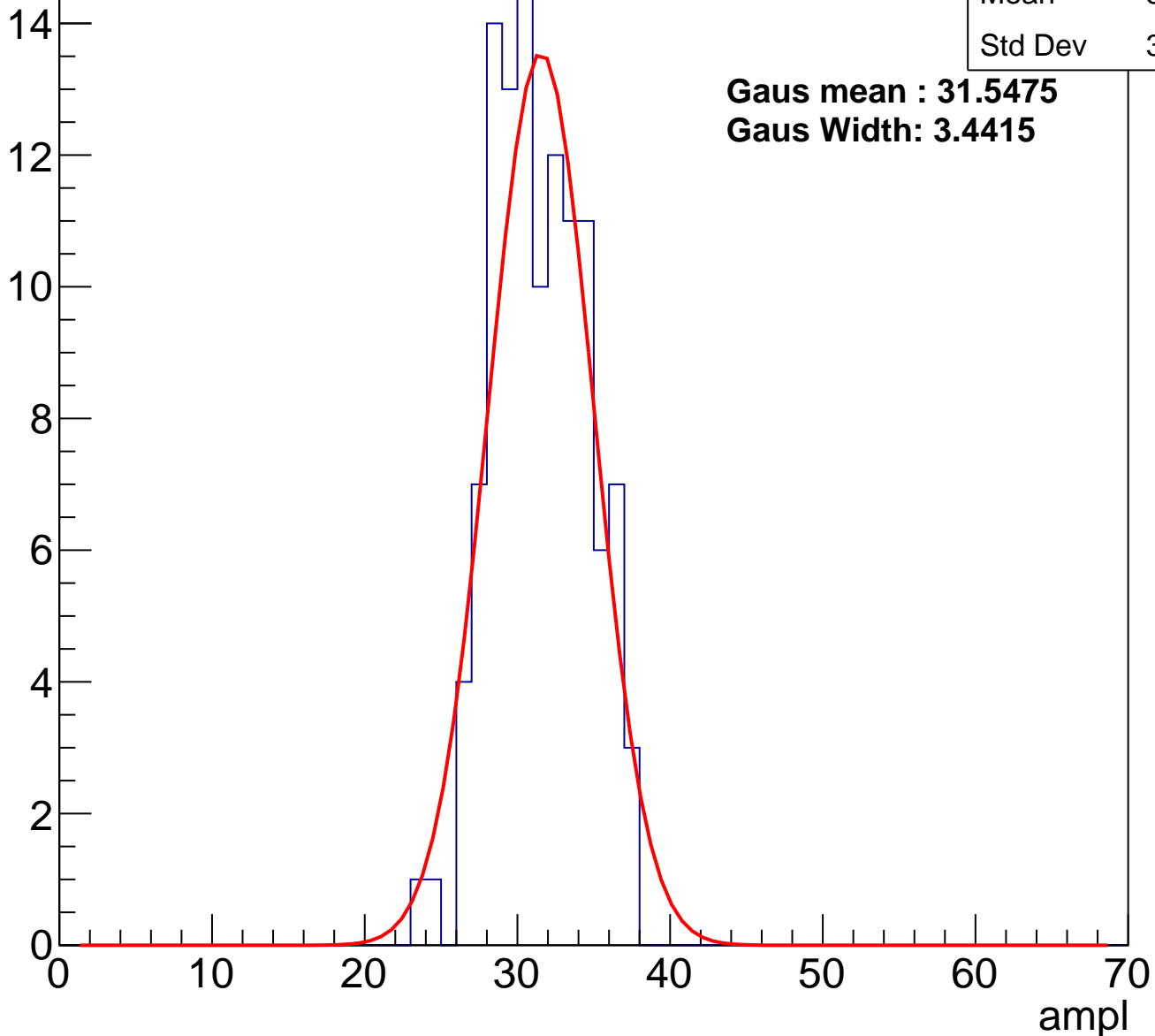
ampl



B0L101S, U21-ch63, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch63, adc1

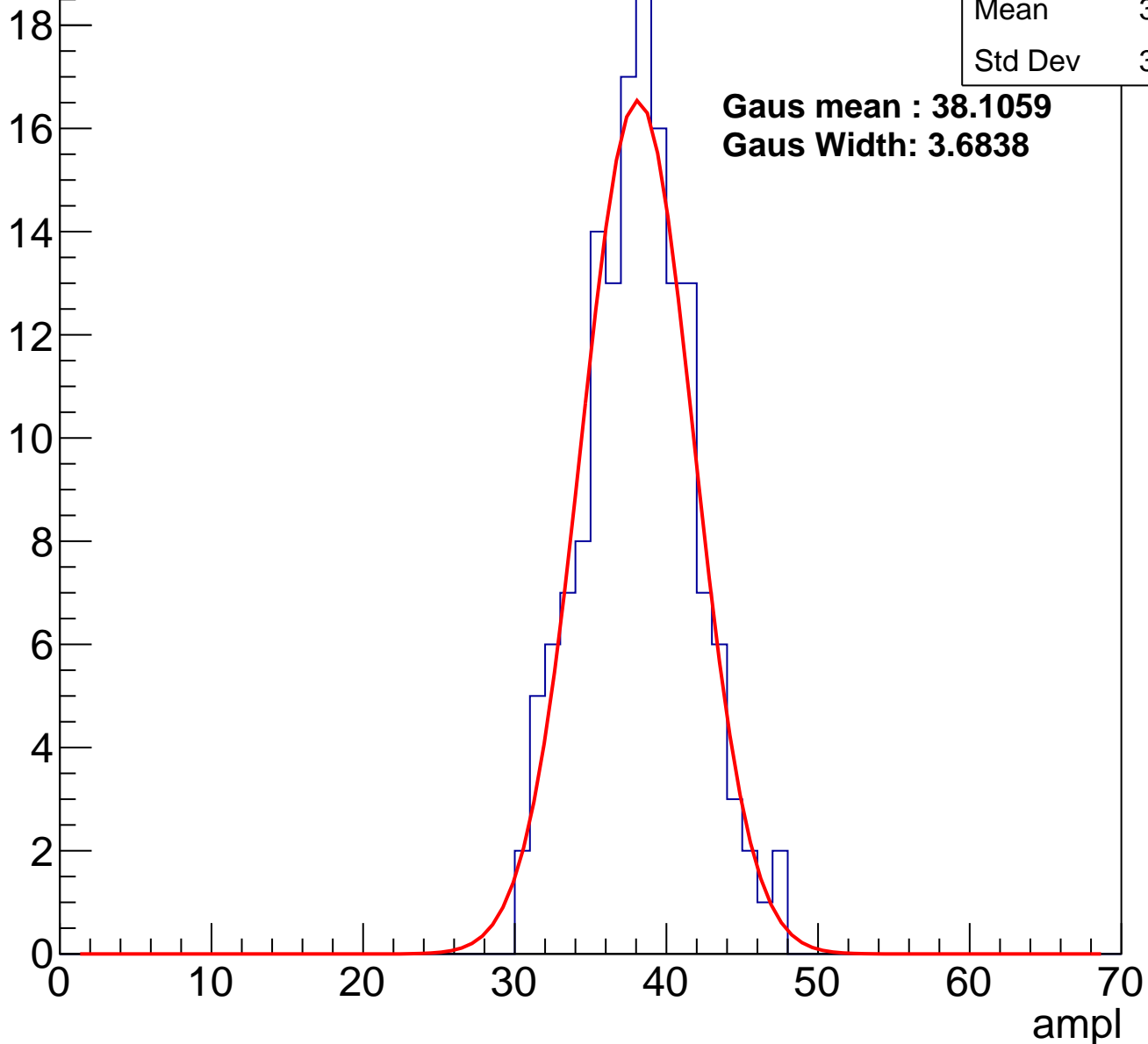
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	37.73
Std Dev	3.563

Gaus mean : 38.1059

Gaus Width: 3.6838

Entry



B0L101S, U21-ch63, adc2

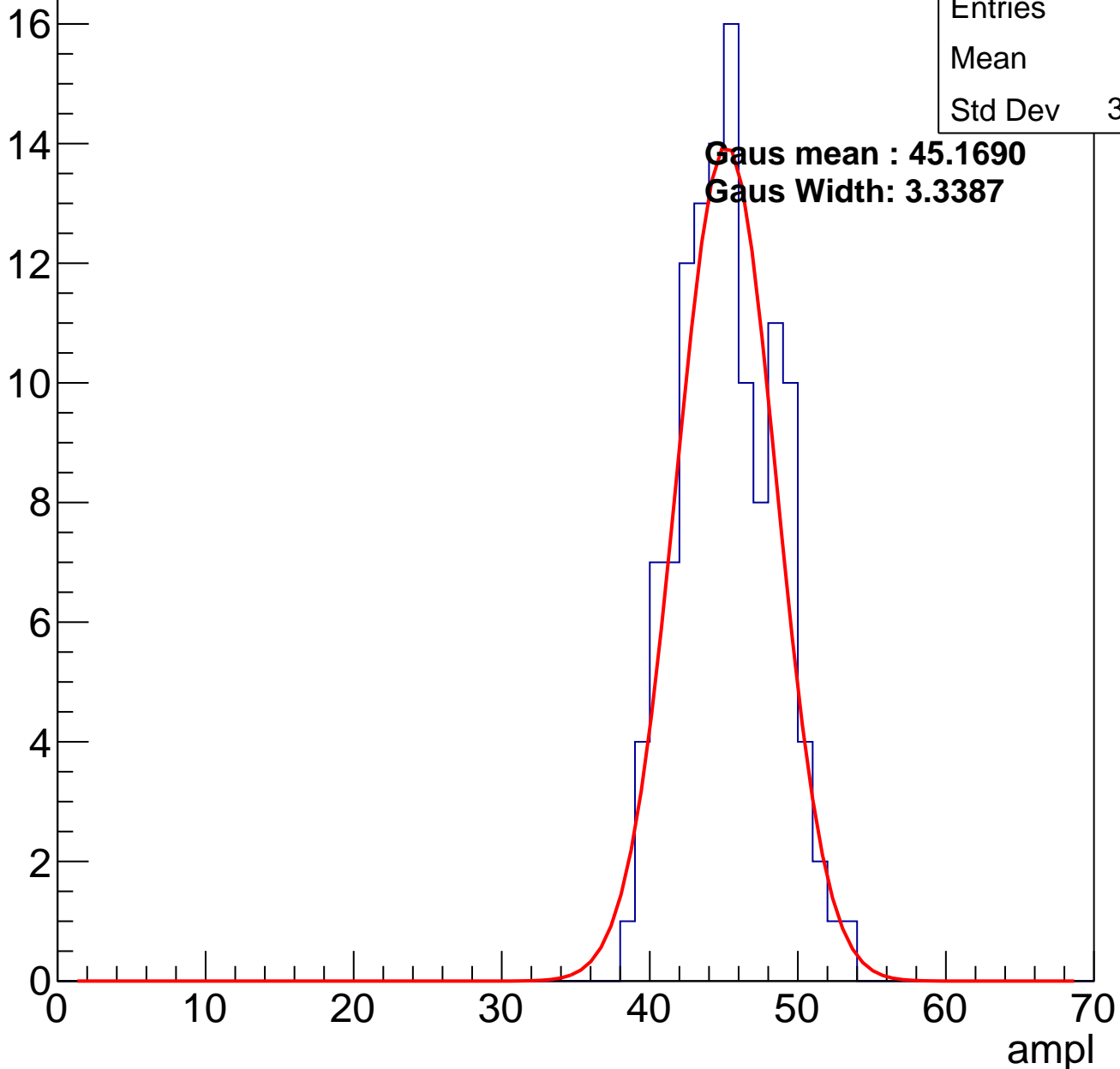
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	44.8
Std Dev	3.195

Gaus mean : 45.1690

Gaus Width: 3.3387

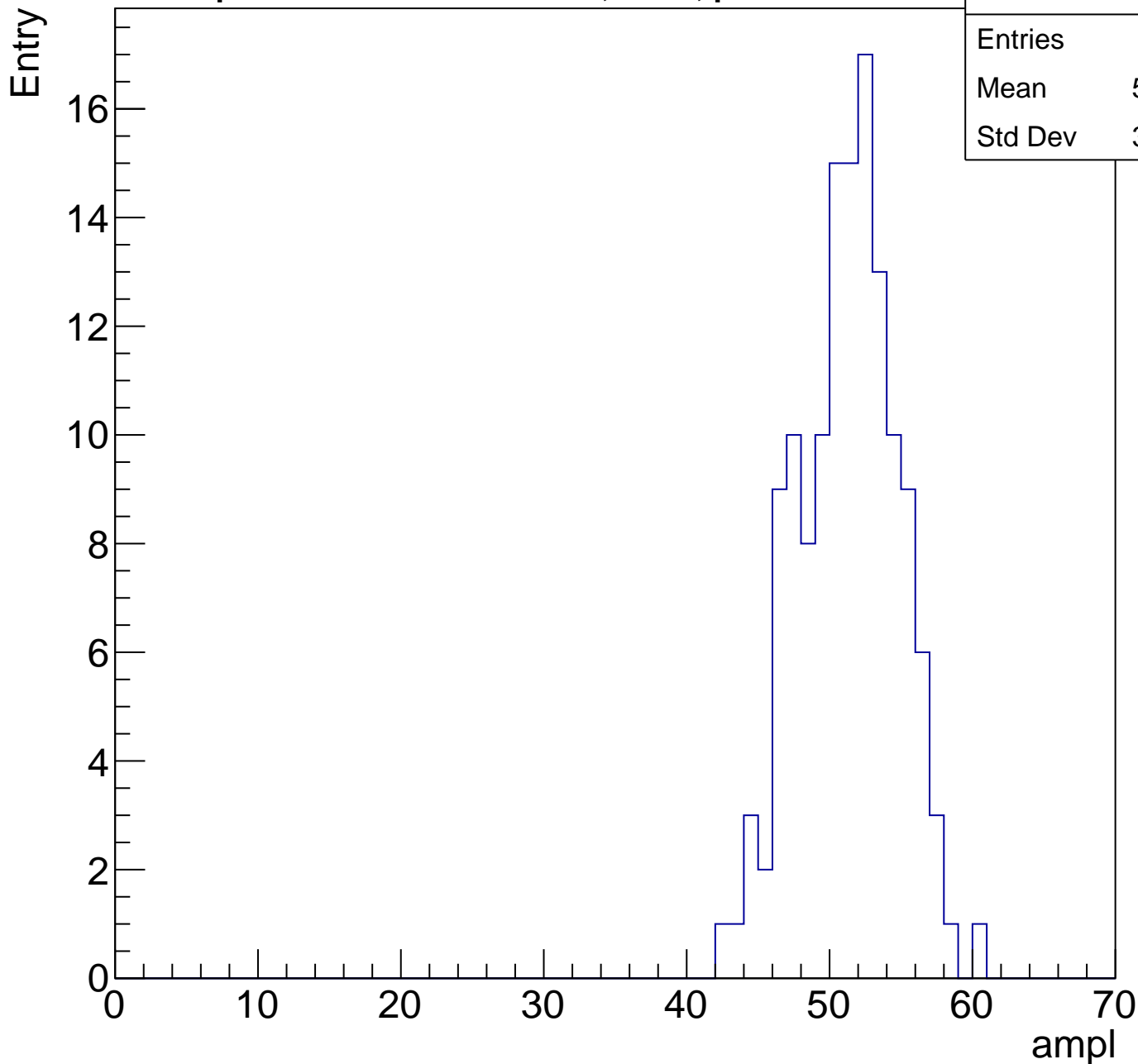
Entry



B0L101S, U21-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	50.84
Std Dev	3.416



B0L101S, U21-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

113

Mean

56.98

Std Dev

2.909

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch63, adc5

calib_packv5_042523_0143.root, FC#1, port C1

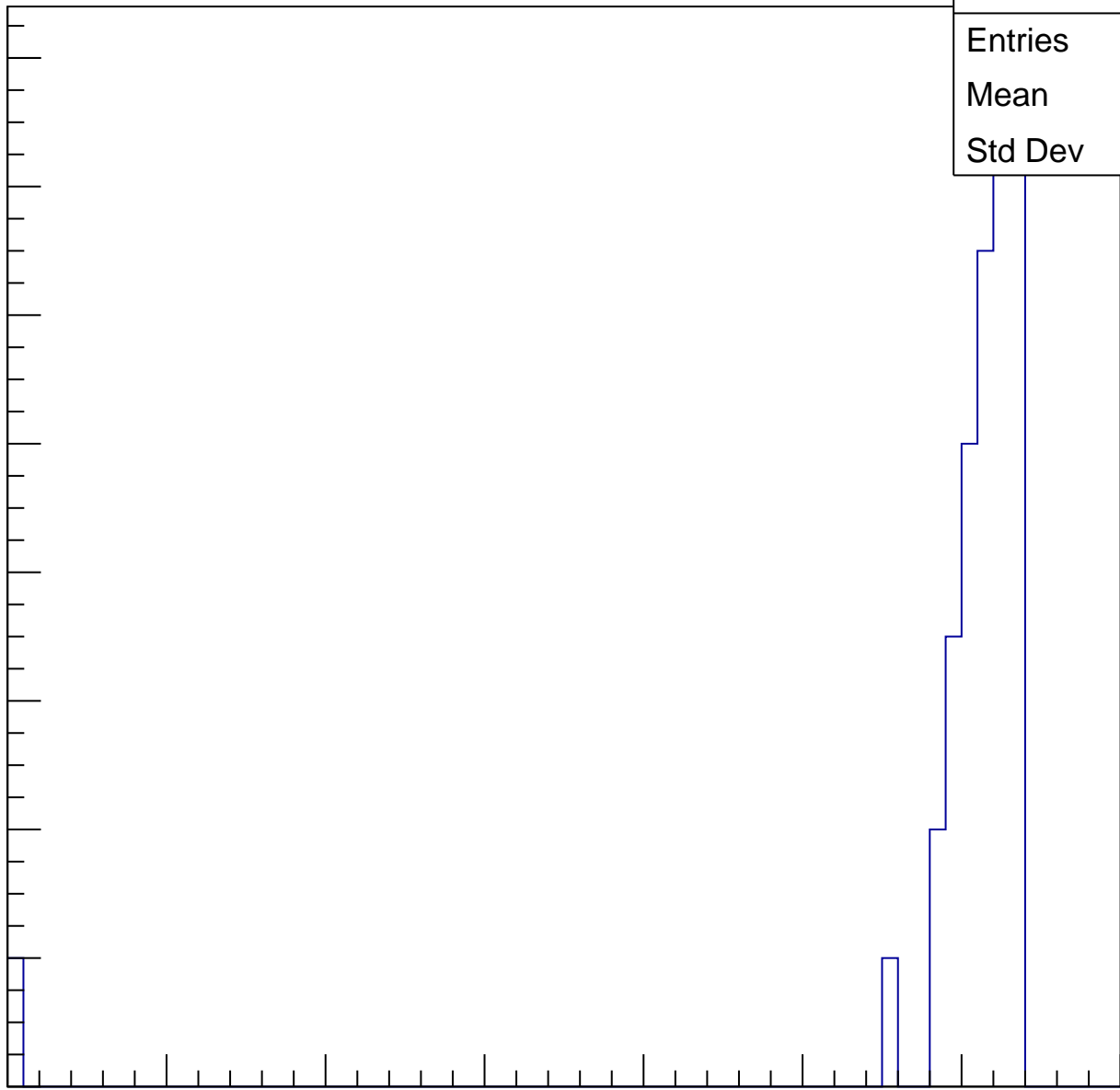
Entries	70
Mean	59.26
Std Dev	10.32

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L101S, U21-ch64, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	28.53
Std Dev	3.859

Gaus mean : 29.0277

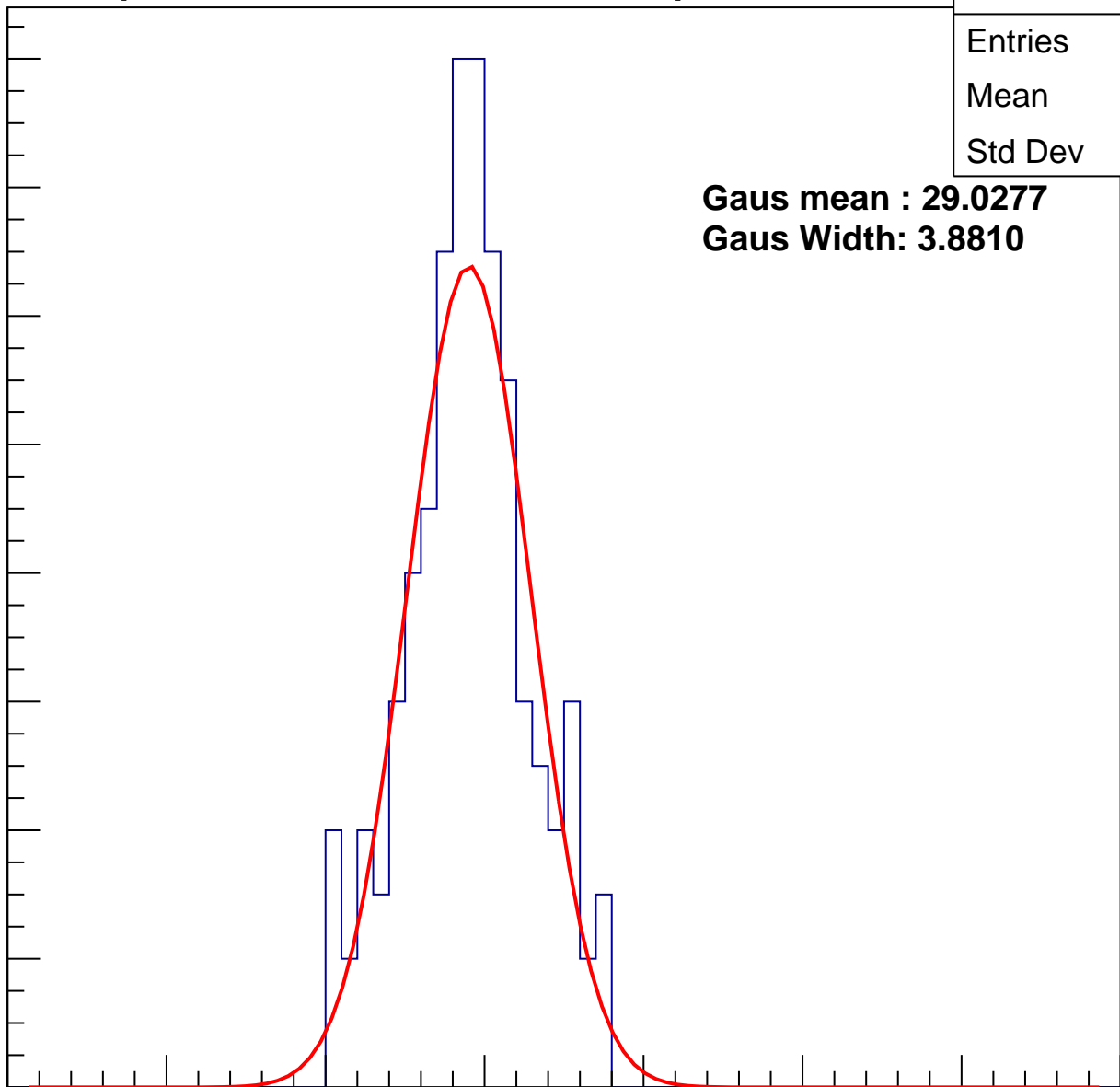
Gaus Width: 3.8810

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch64, adc1

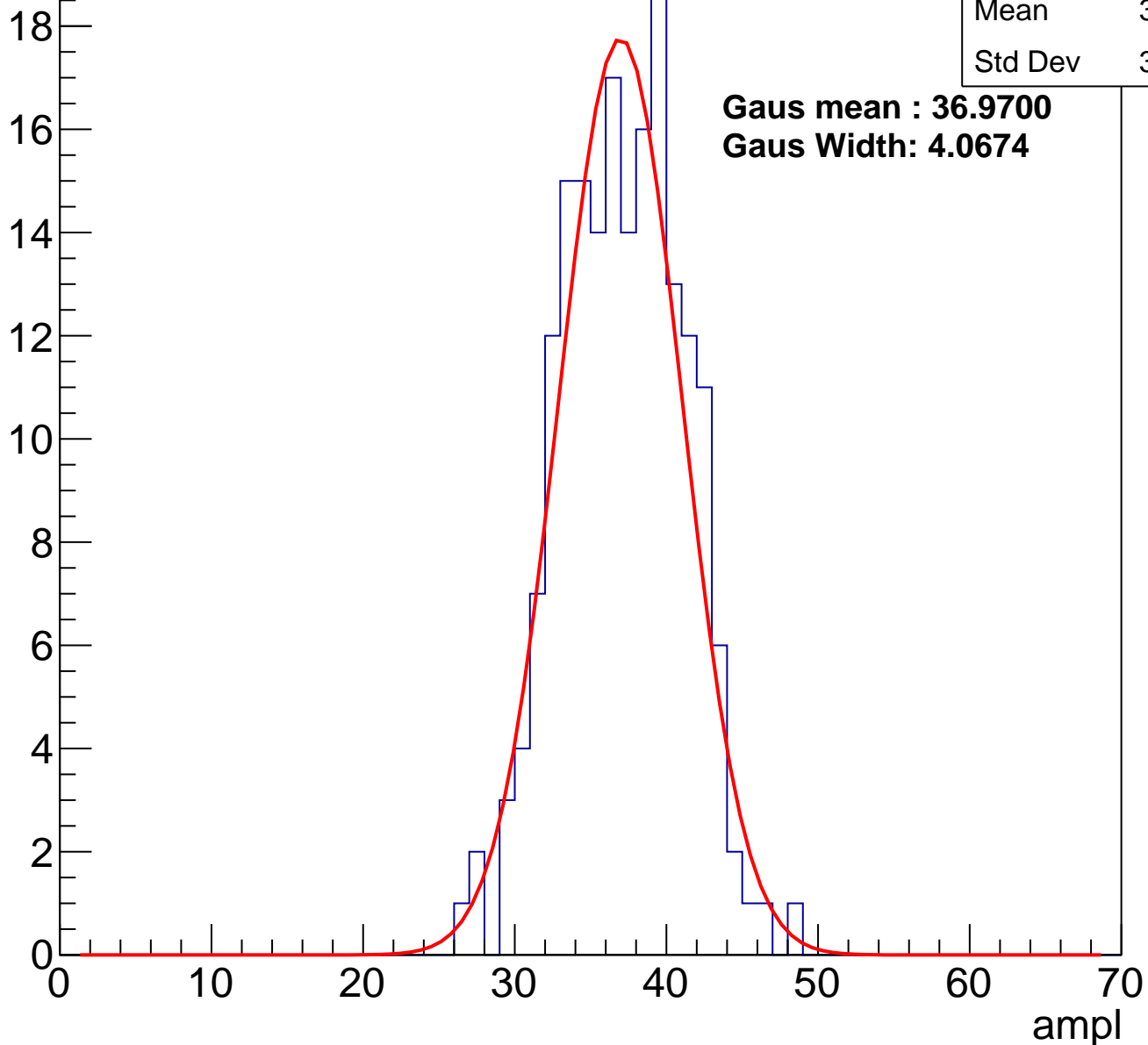
calib_packv5_042523_0143.root, FC#1, port C1

Entries	186
Mean	36.67
Std Dev	3.979

Gaus mean : 36.9700

Gaus Width: 4.0674

Entry



B0L101S, U21-ch64, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	43.93
Std Dev	3.509

Gaus mean : 44.9966

Gaus Width: 3.2509

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

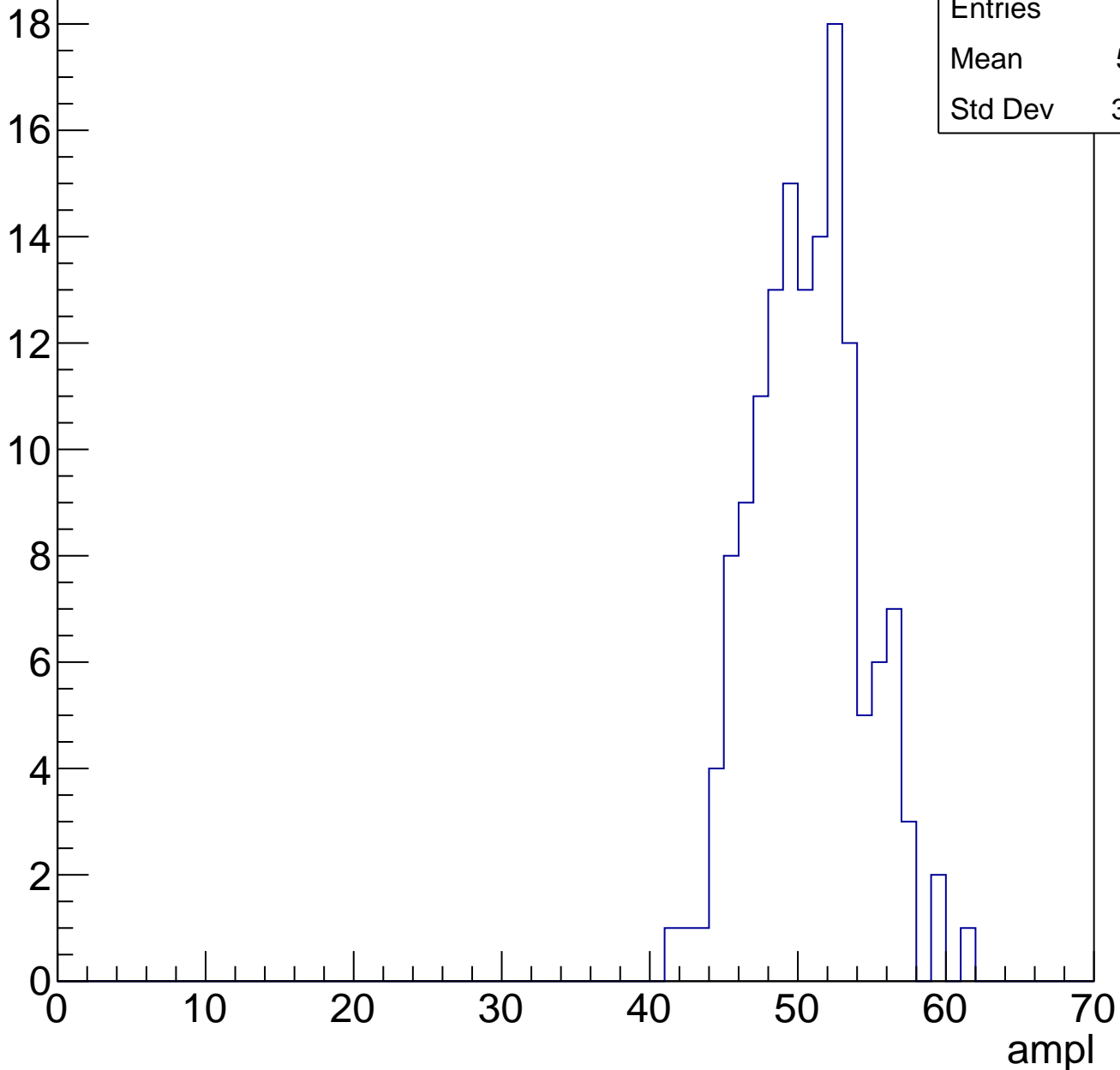
70

B0L101S, U21-ch64, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	50.21
Std Dev	3.678

Entry

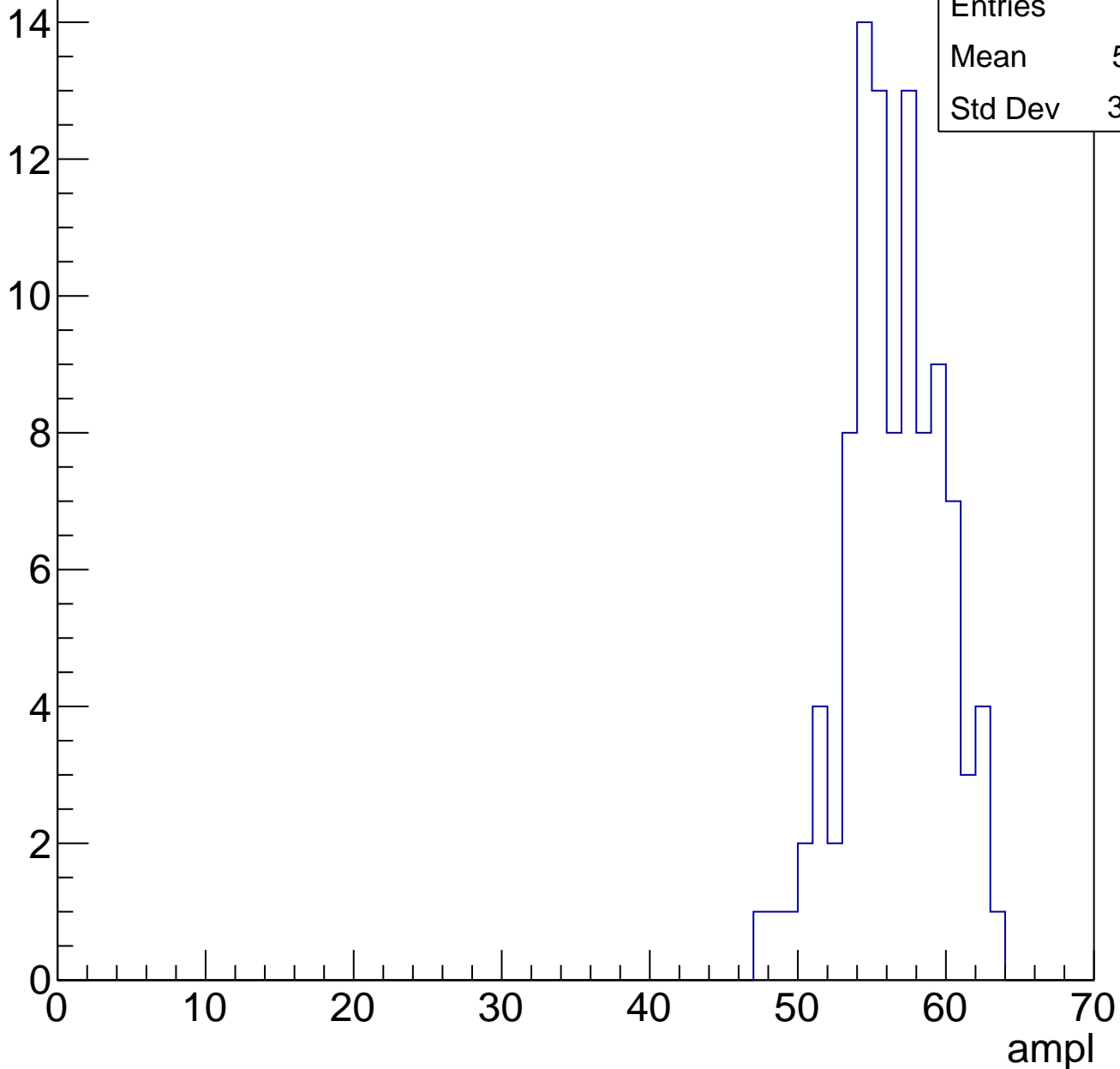


B0L101S, U21-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	56.01
Std Dev	3.249

Entry



B0L101S, U21-ch64, adc5

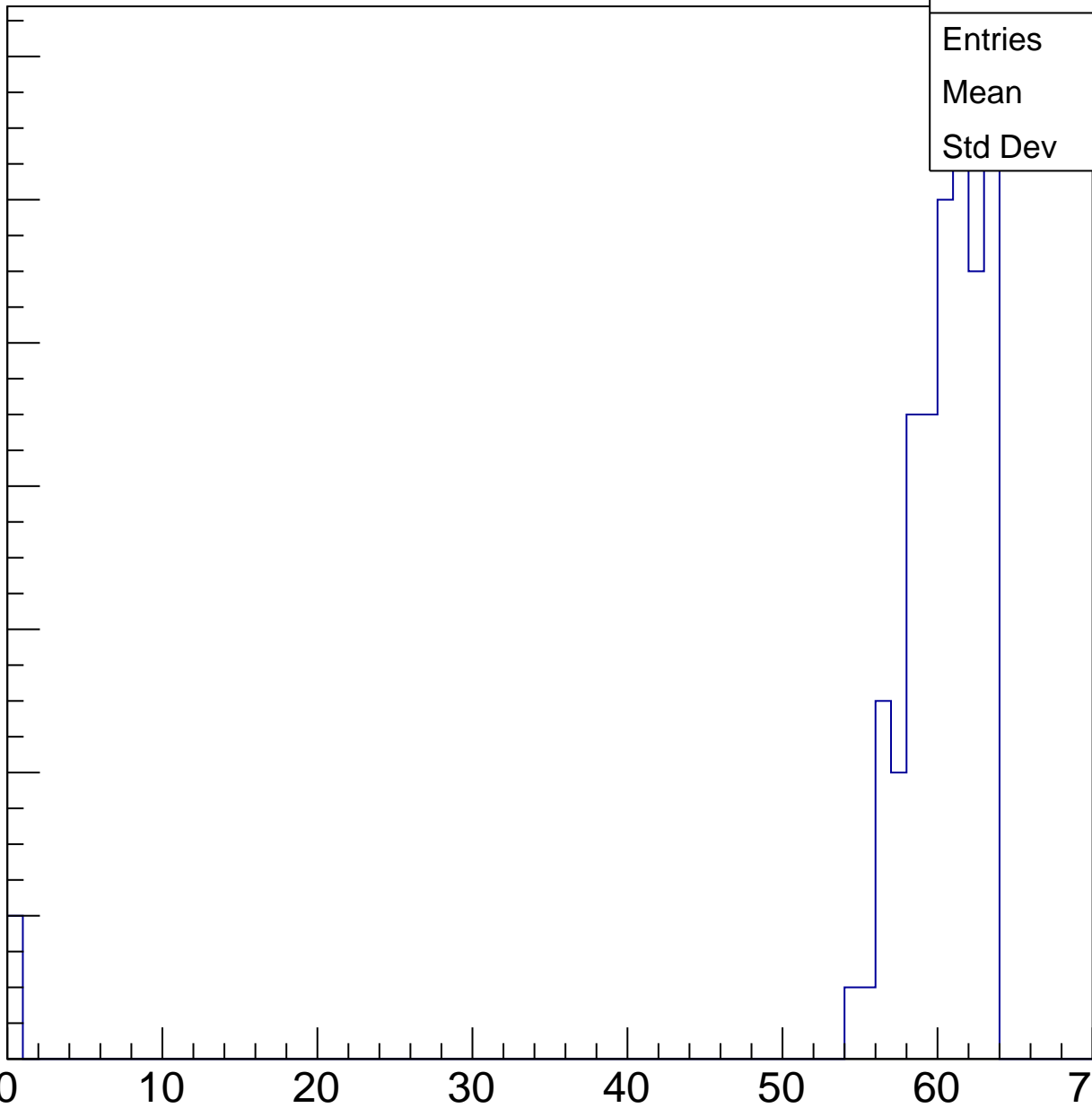
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	82
Mean	58.63
Std Dev	9.534

ampl

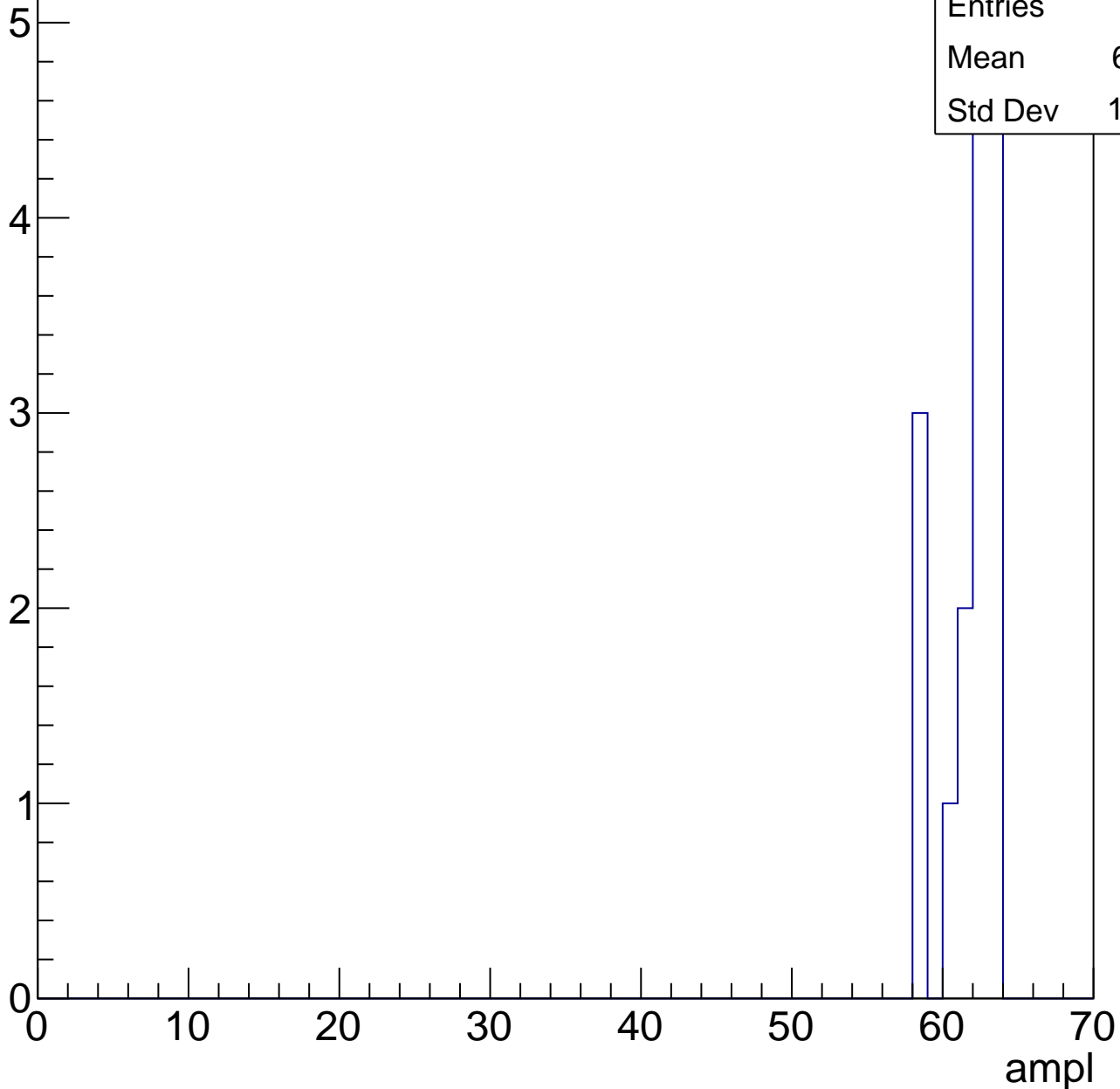


B0L101S, U21-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.31
Std Dev	1.793



B0L101S, U21-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch65, adc0

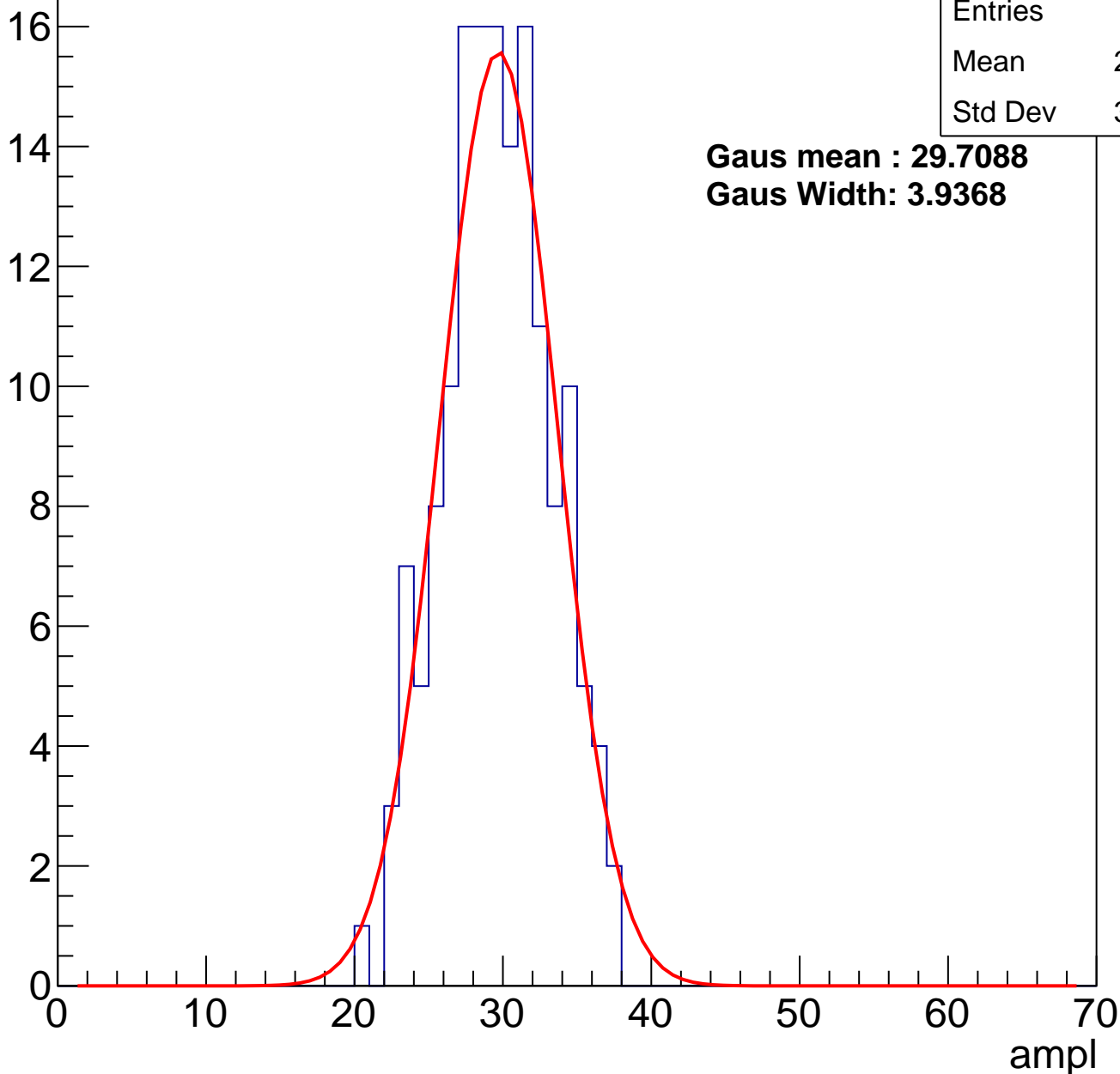
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	29.18
Std Dev	3.588

Gaus mean : 29.7088

Gaus Width: 3.9368

Entry



B0L101S, U21-ch65, adc1

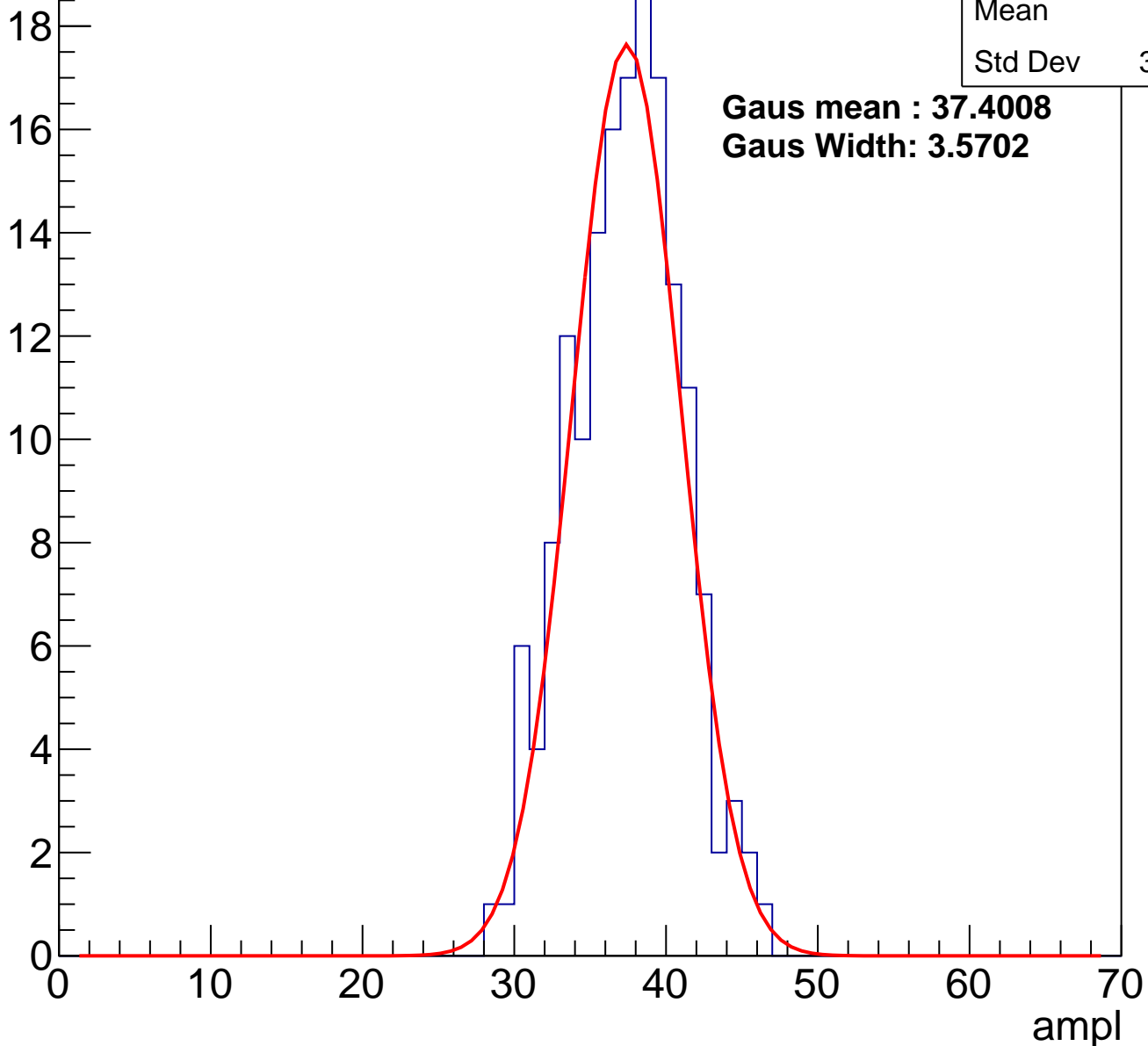
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	36.9
Std Dev	3.594

Gaus mean : 37.4008

Gaus Width: 3.5702

Entry



B0L101S, U21-ch65, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	44.32
Std Dev	3.307

Gaus mean : 44.9007

Gaus Width: 3.3453

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

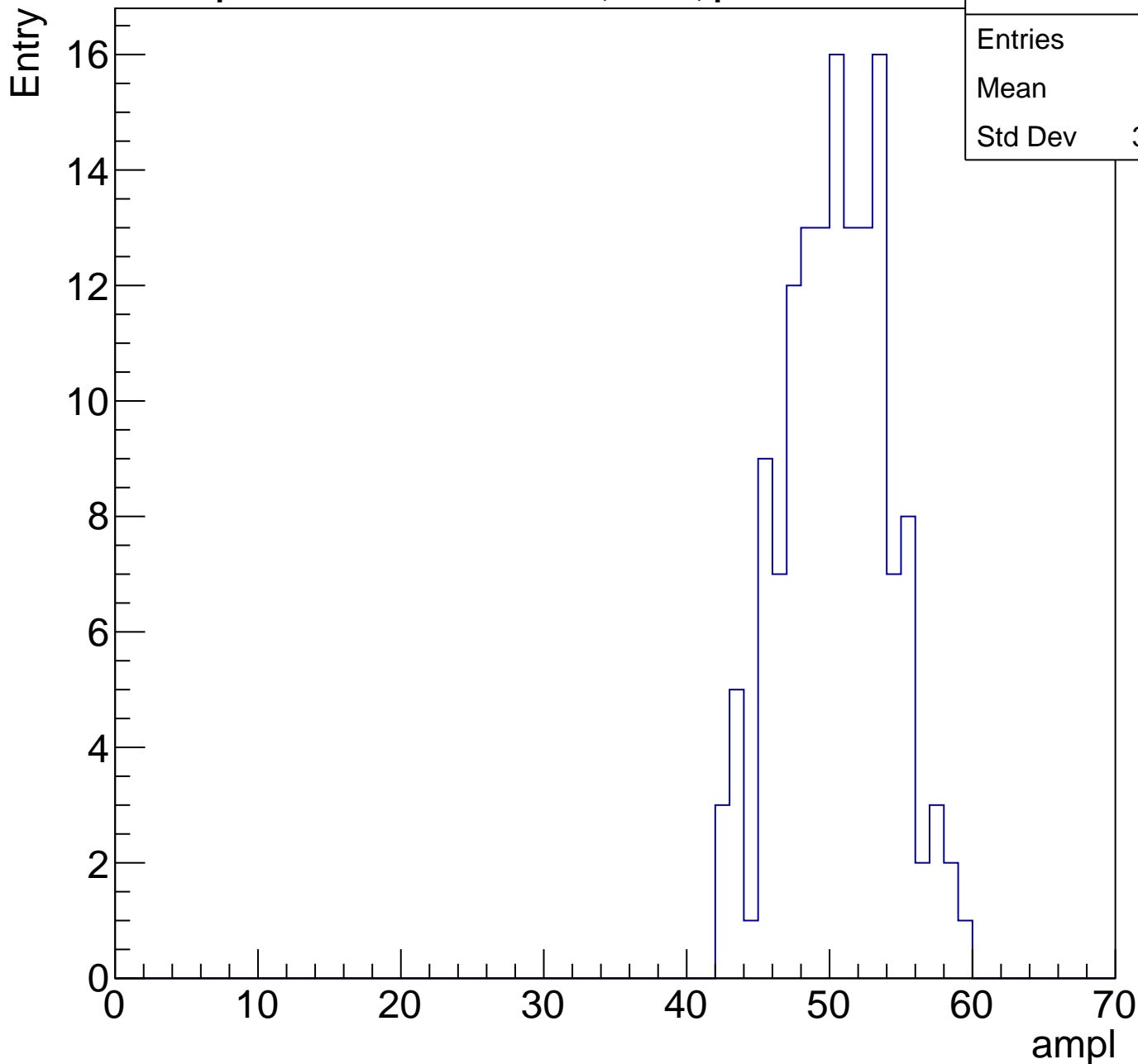
60

70

B0L101S, U21-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

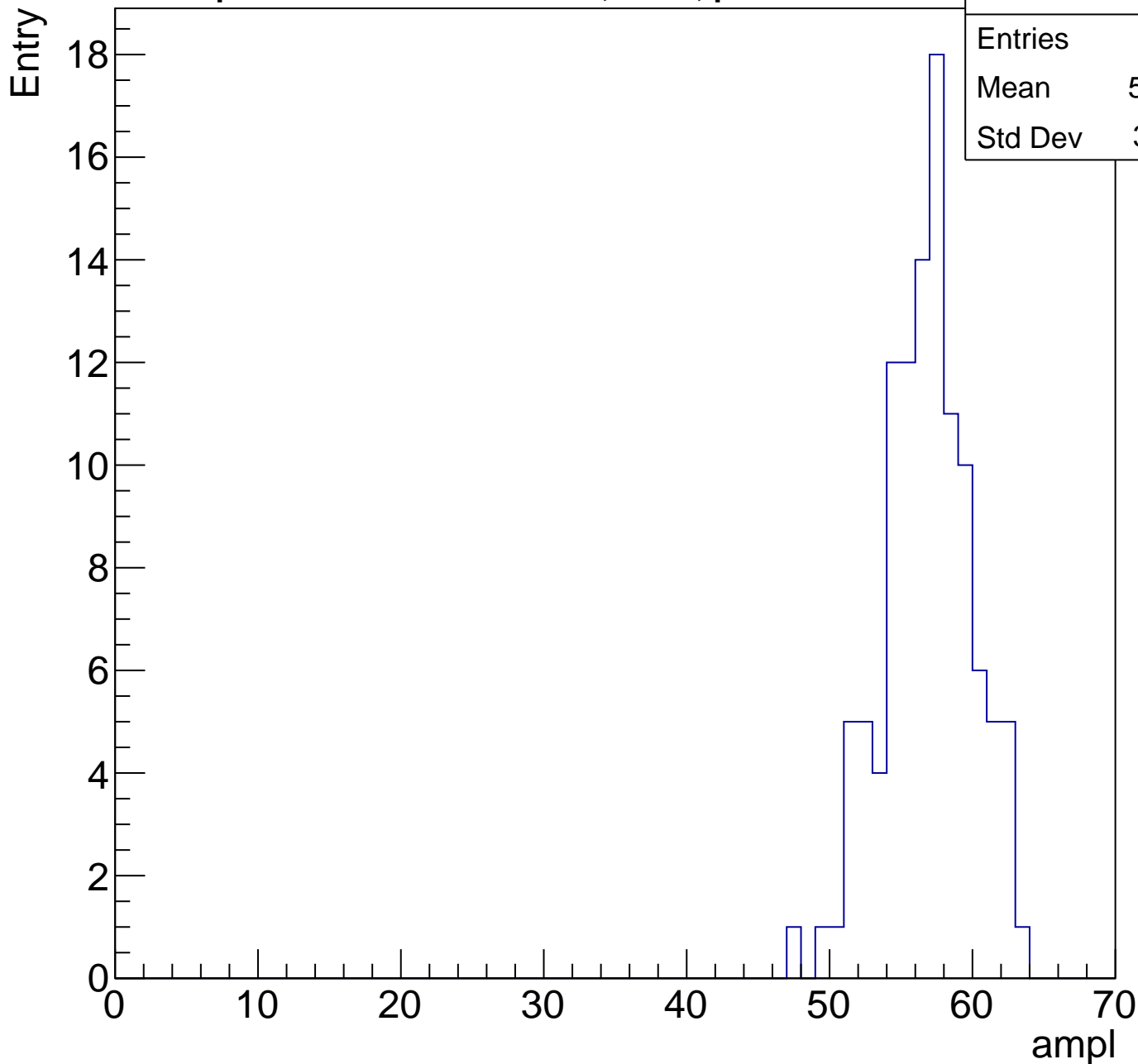
Entries	144
Mean	50
Std Dev	3.663



B0L101S, U21-ch65, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	56.37
Std Dev	3.081



B0L101S, U21-ch65, adc5

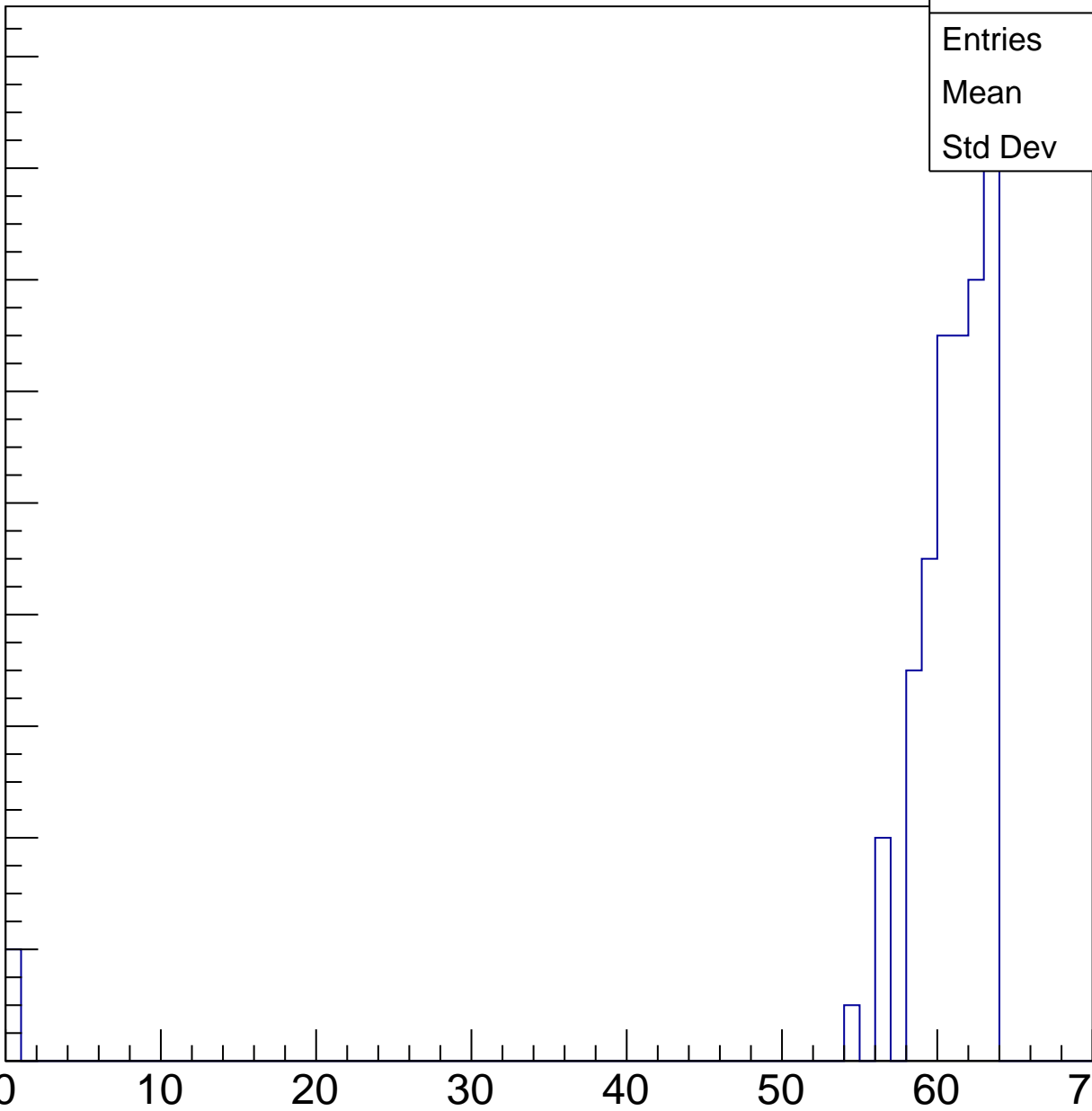
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	81
Mean	59.14
Std Dev	9.627

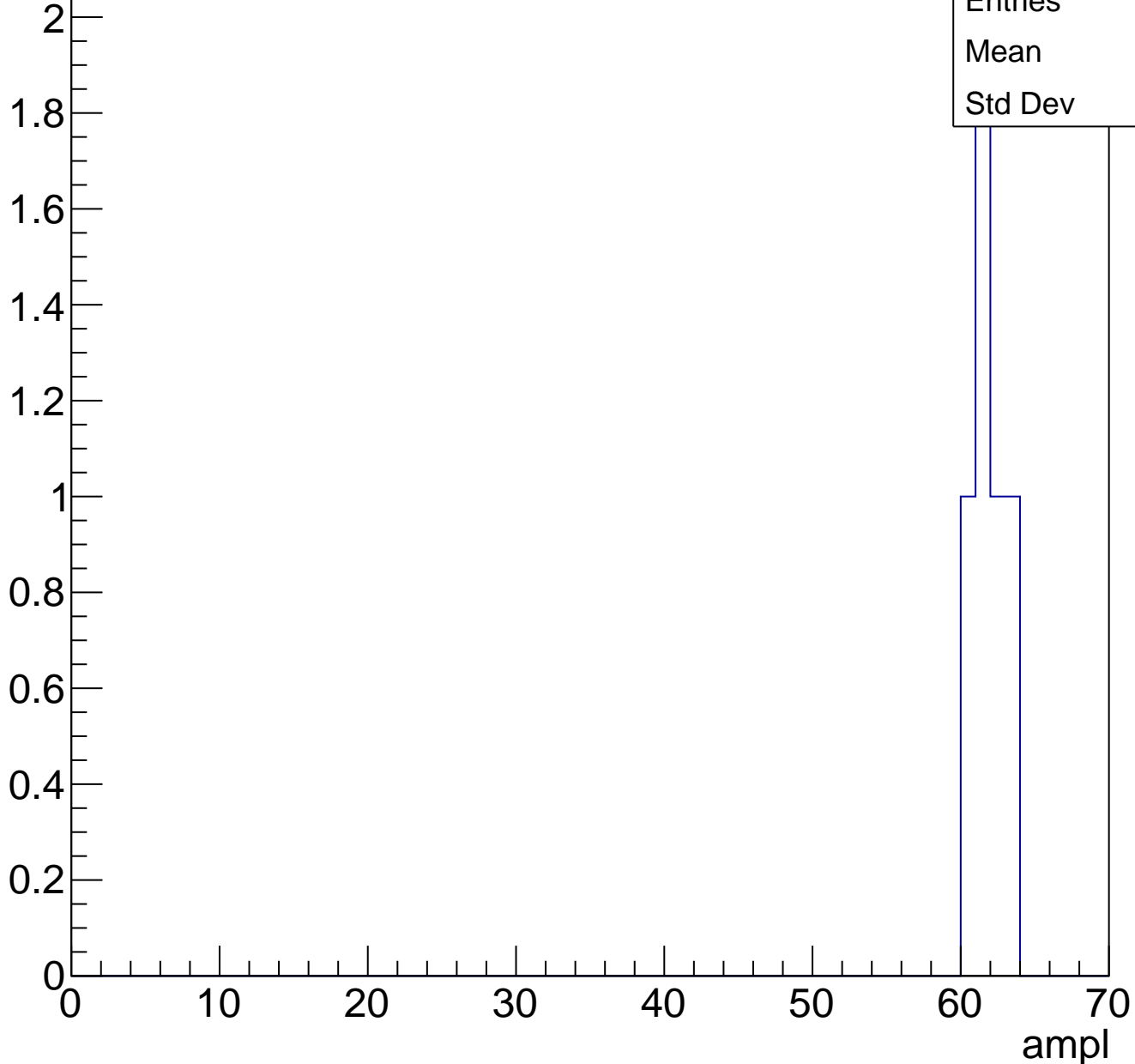
ampl



B0L101S, U21-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	61.4
Std Dev	1.02

B0L101S, U21-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch66, adc0

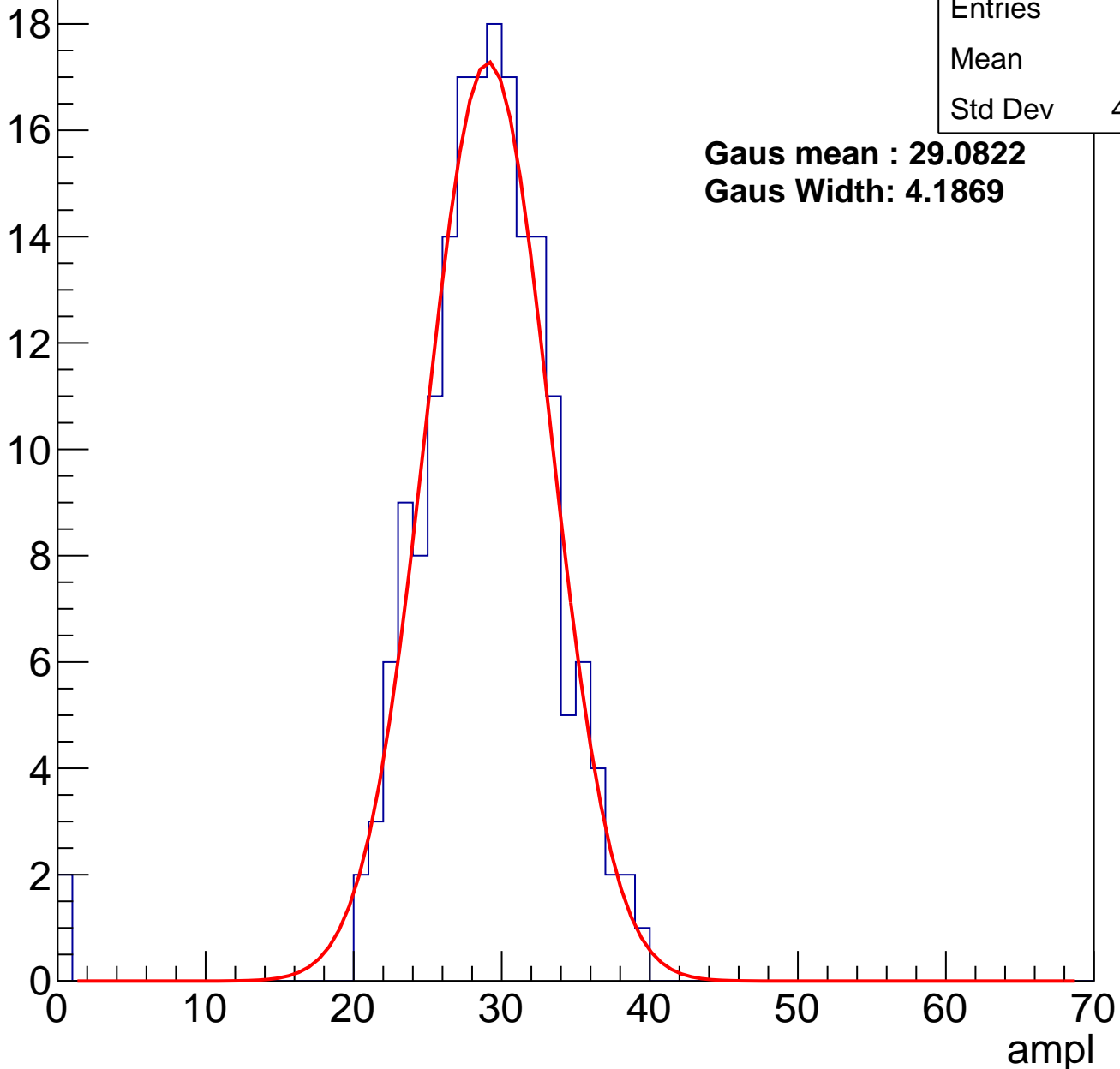
calib_packv5_042523_0143.root, FC#1, port C1

Entries	183
Mean	28.4
Std Dev	4.947

Gaus mean : 29.0822

Gaus Width: 4.1869

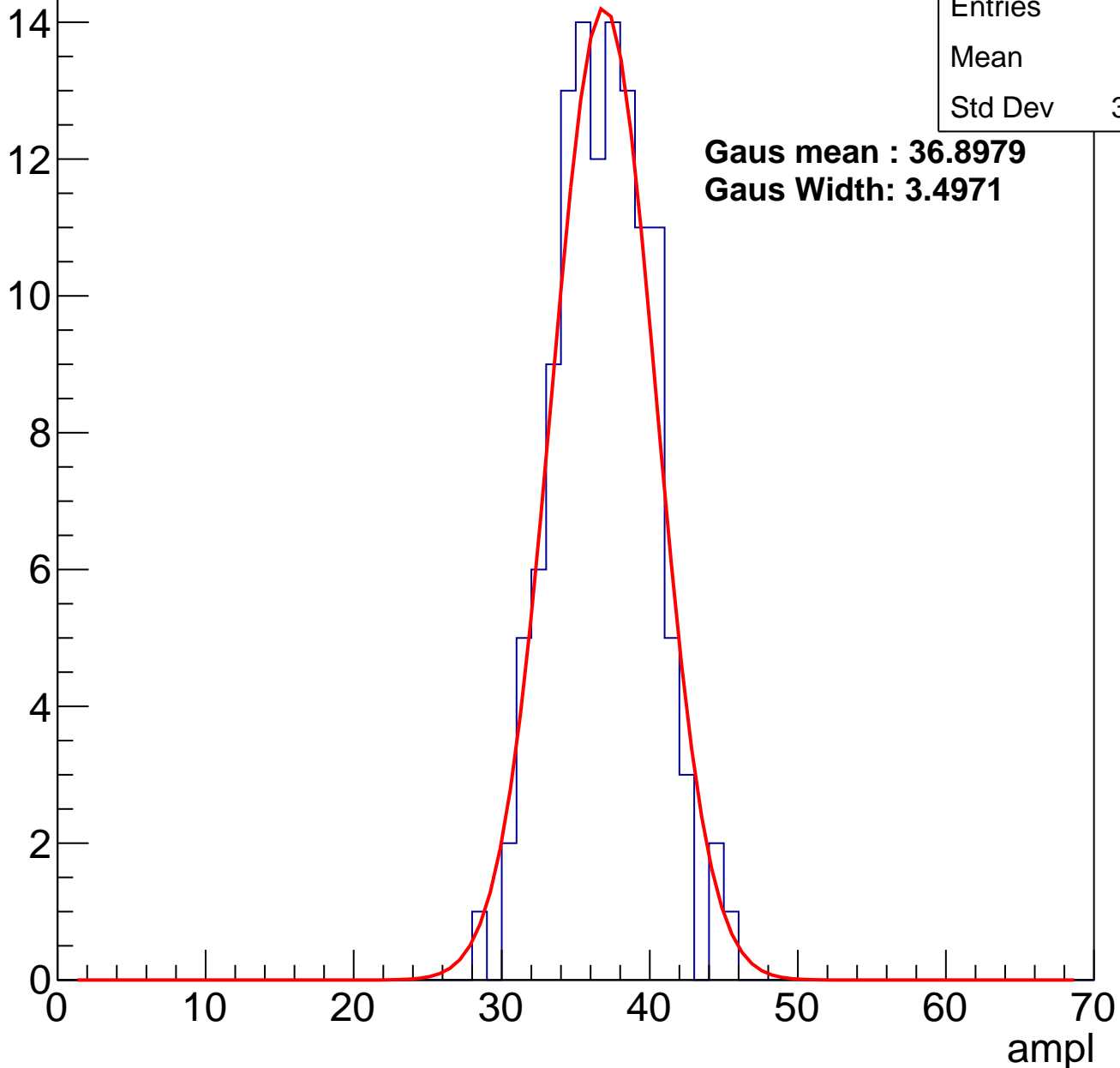
Entry



B0L101S, U21-ch66, adc1

calib_packv5_042523_0143.root, FC#1, port C1

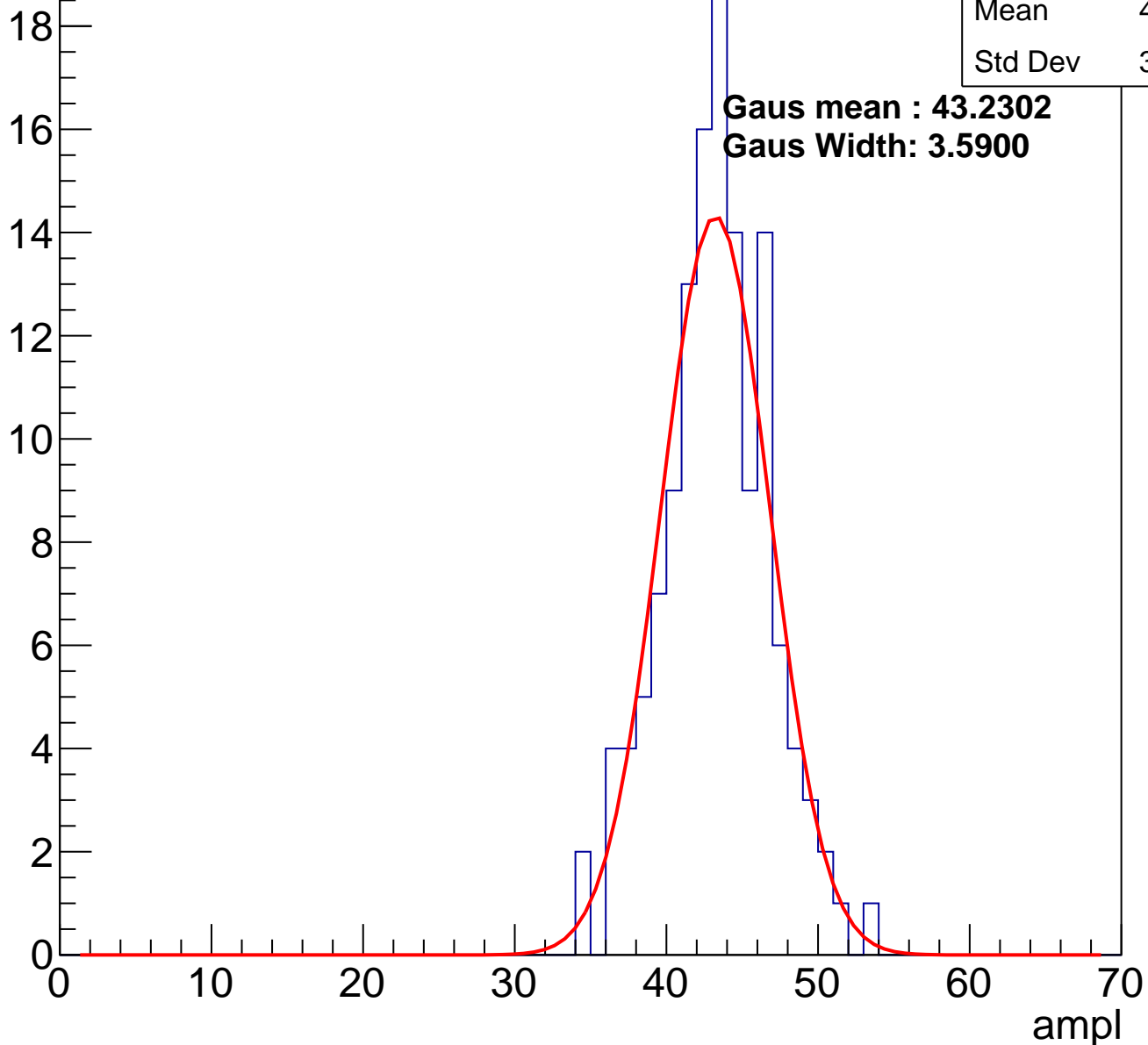
Entry



B0L101S, U21-ch66, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

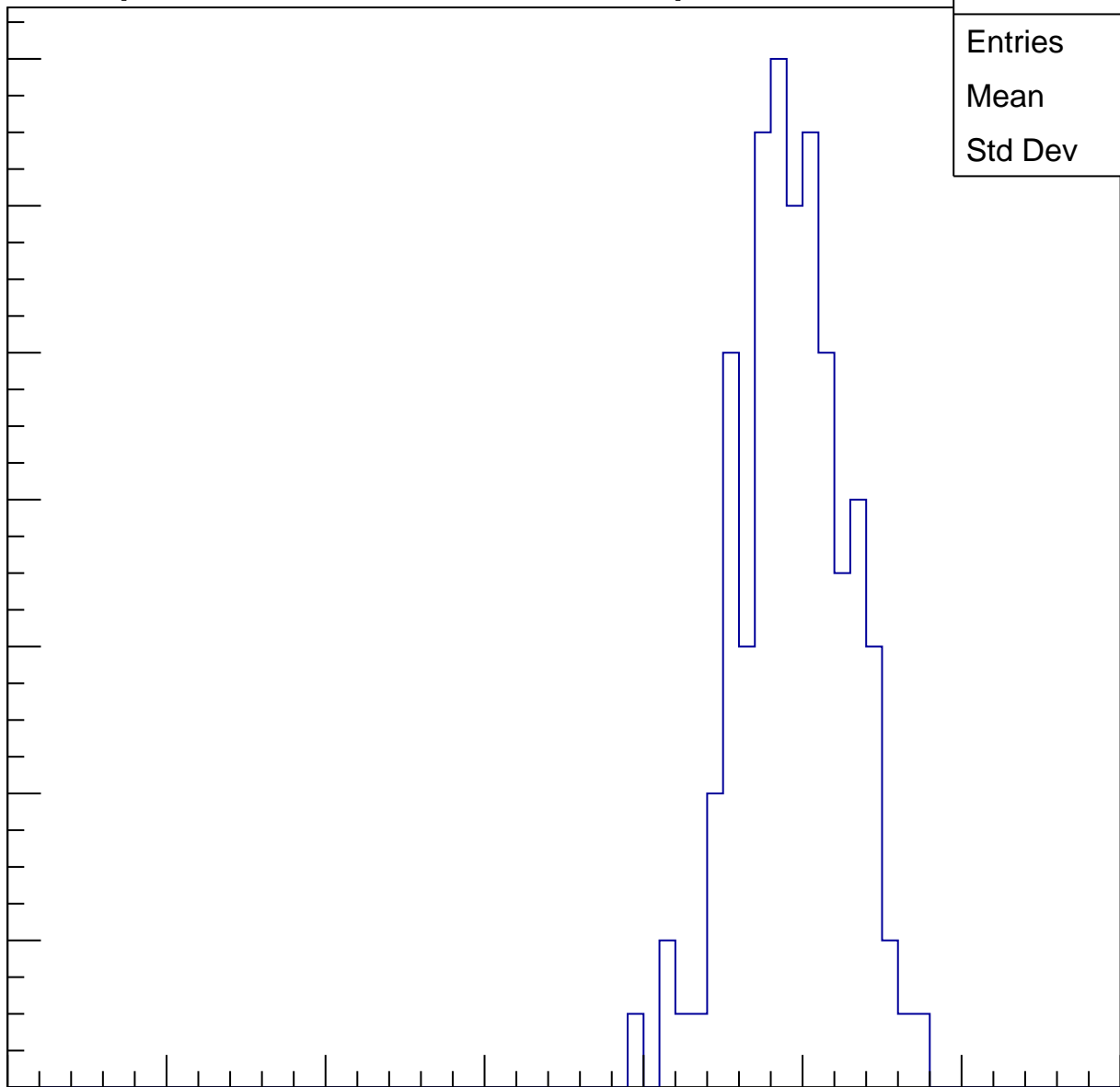
Entries	112
Mean	48.88
Std Dev	3.368

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

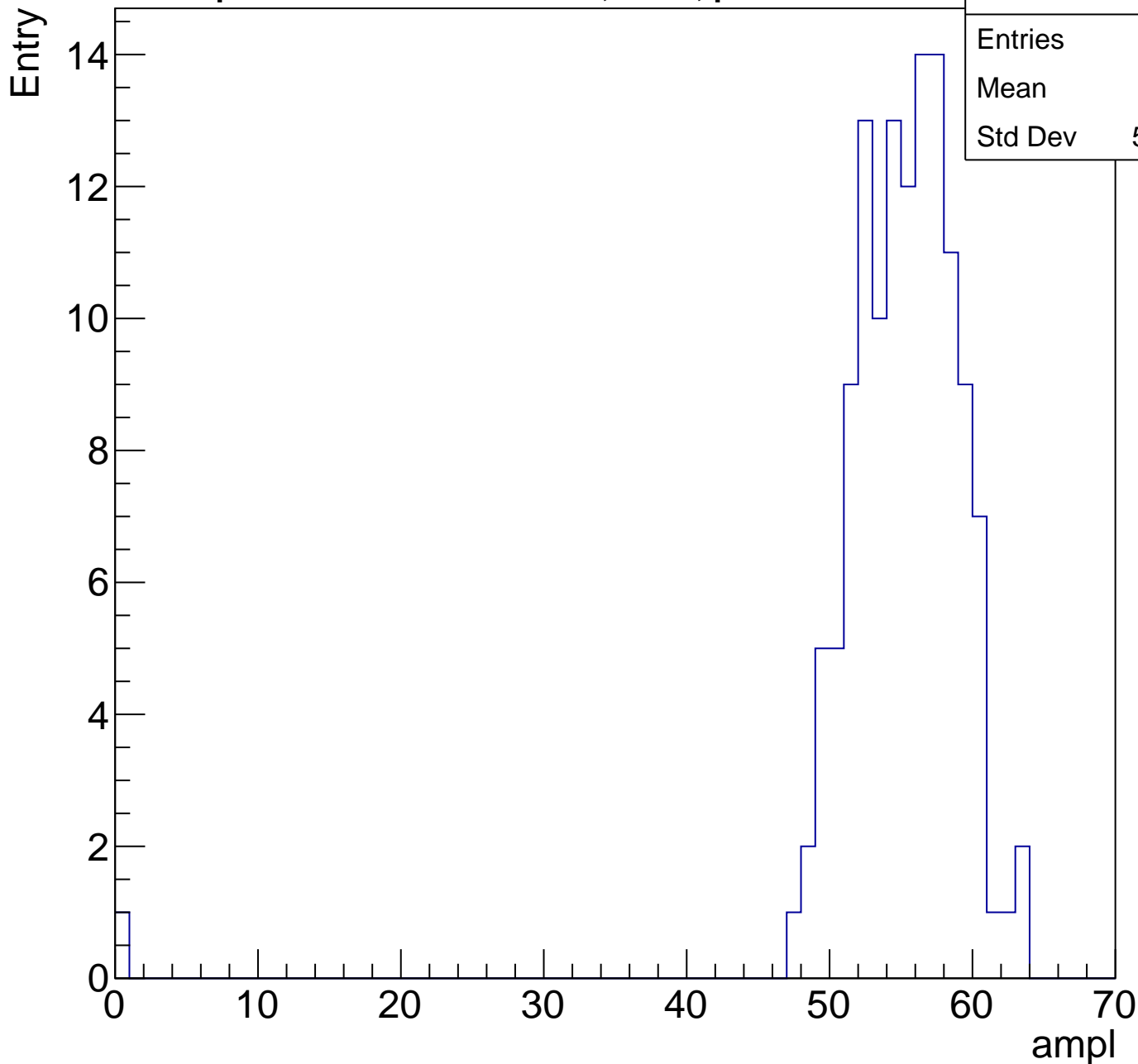
ampl



B0L101S, U21-ch66, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	54.5
Std Dev	5.867



B0L101S, U21-ch66, adc5

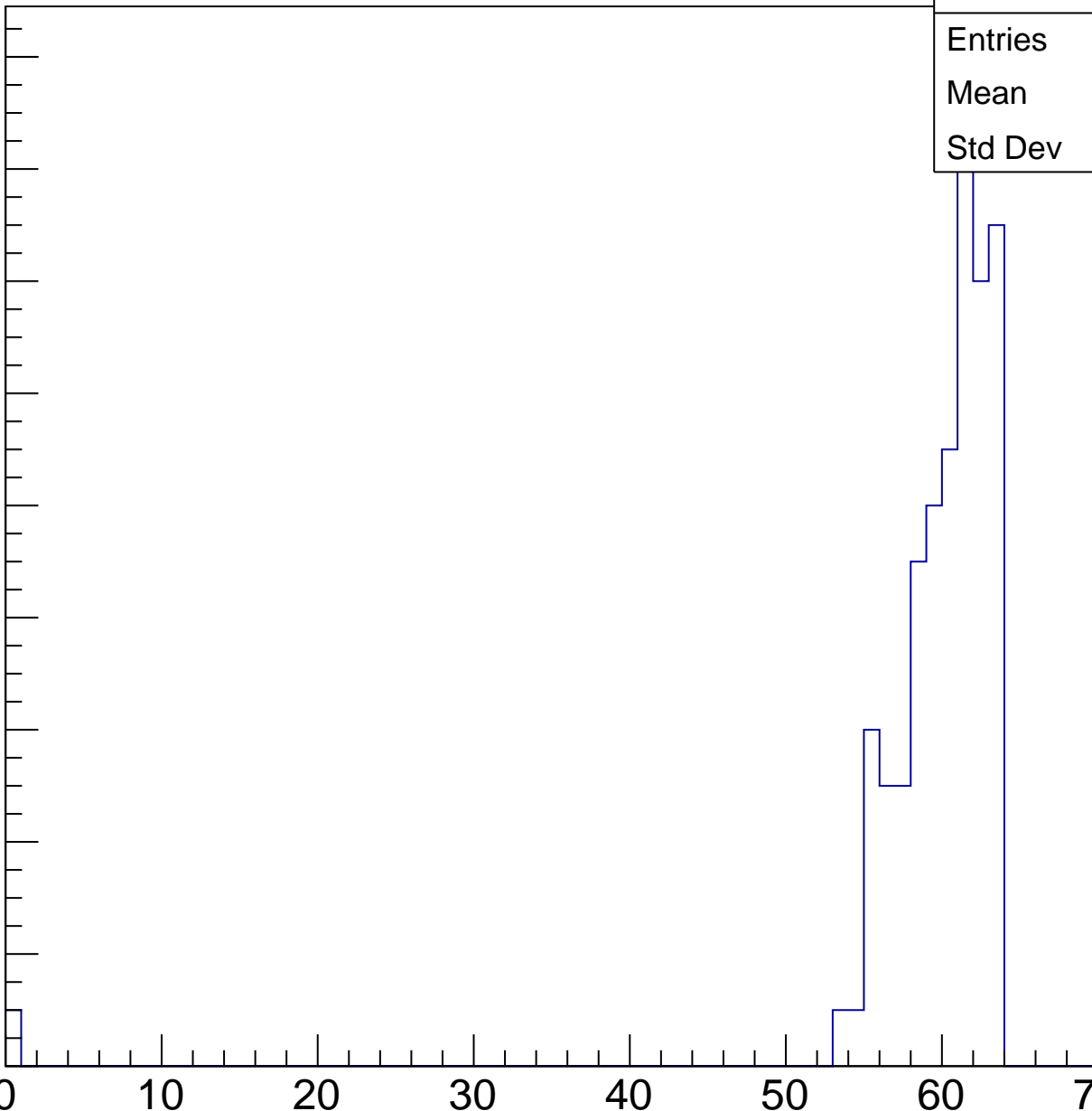
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.22
Std Dev	6.58

ampl

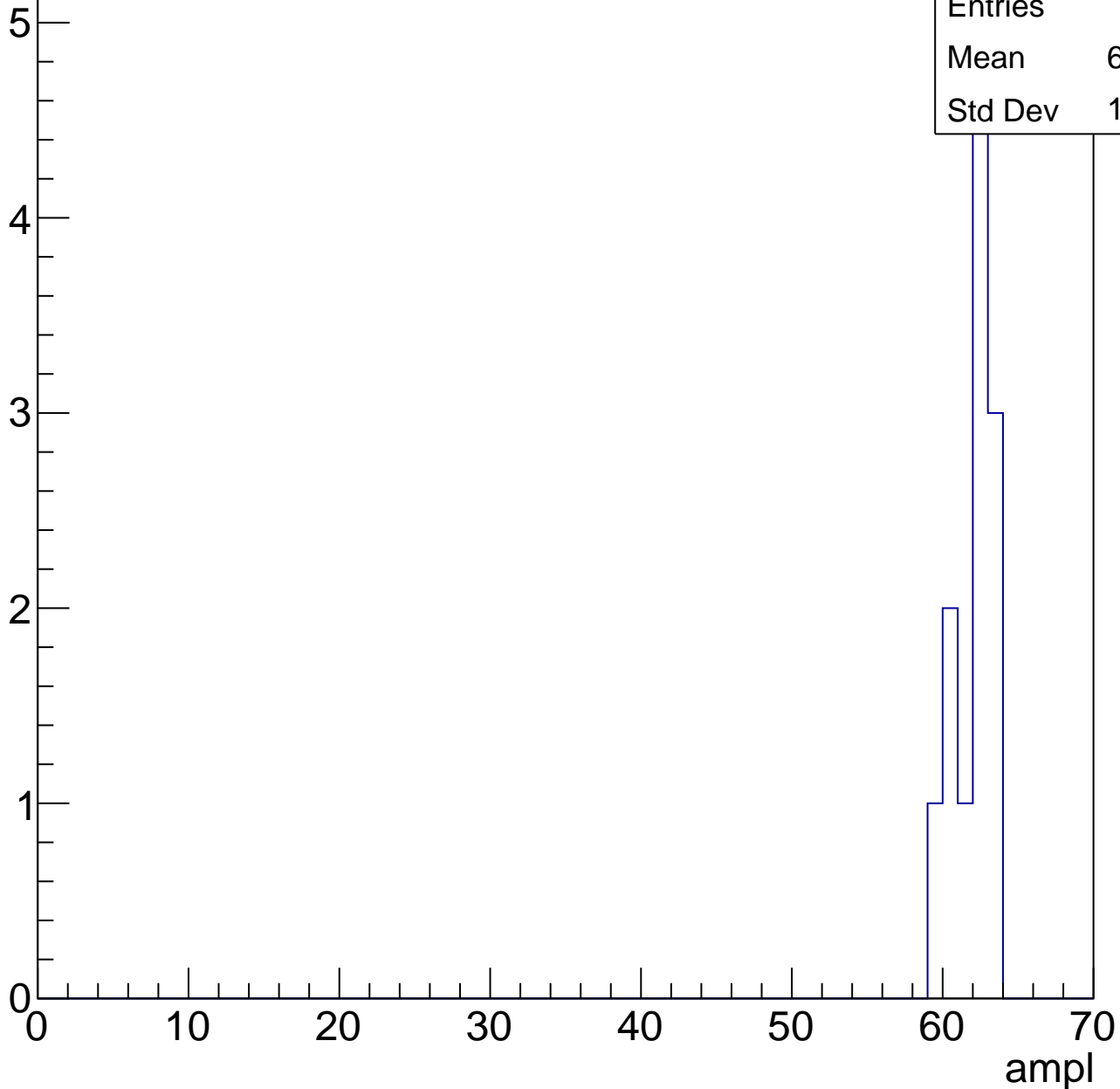


B0L101S, U21-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.58
Std Dev	1.256



B0L101S, U21-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch67, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	28.14
Std Dev	3.663

Gaus mean : 29.0018

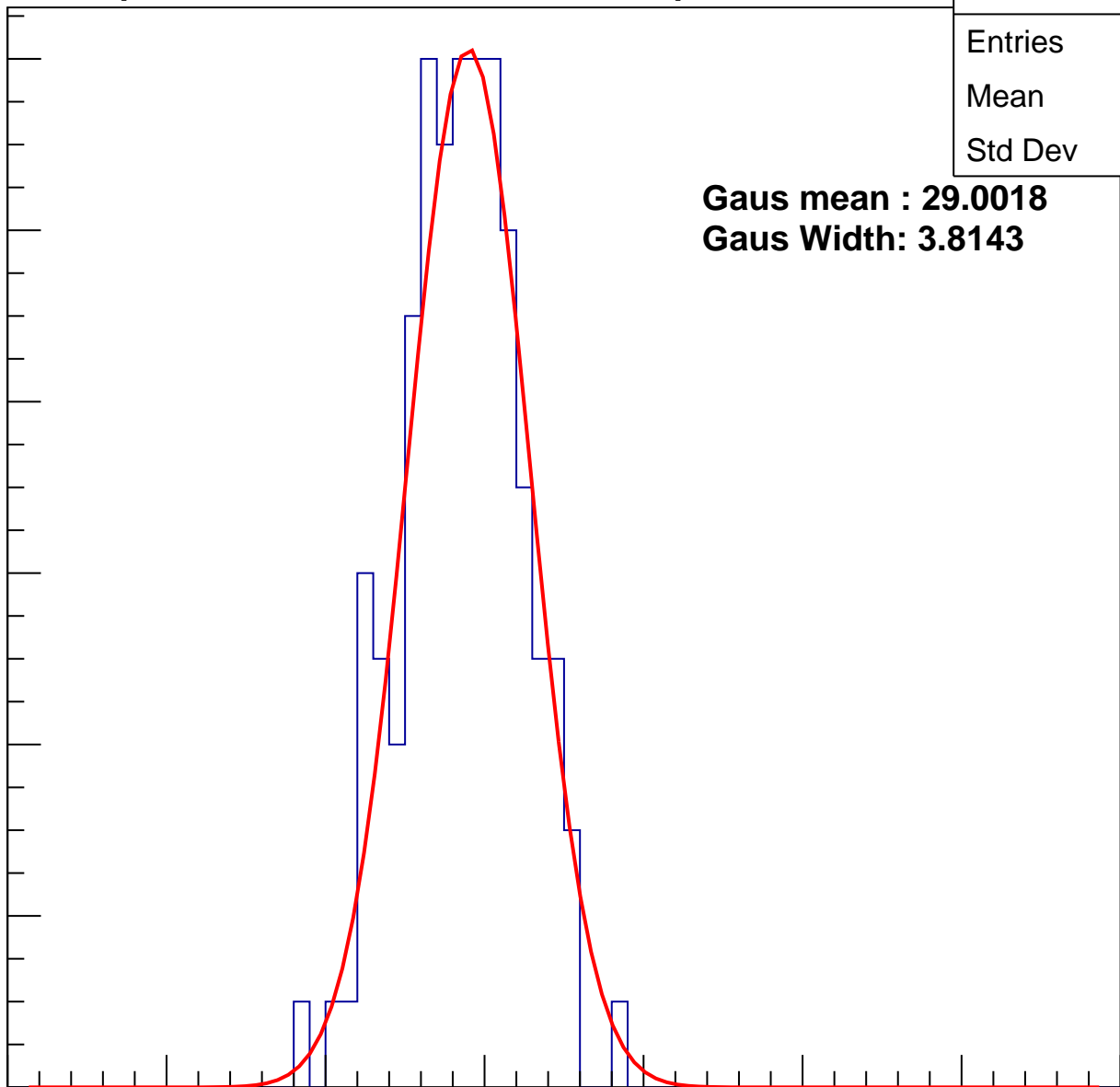
Gaus Width: 3.8143

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch67, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	35.03
Std Dev	3.696

Gaus mean : 35.5301

Gaus Width: 4.1327

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

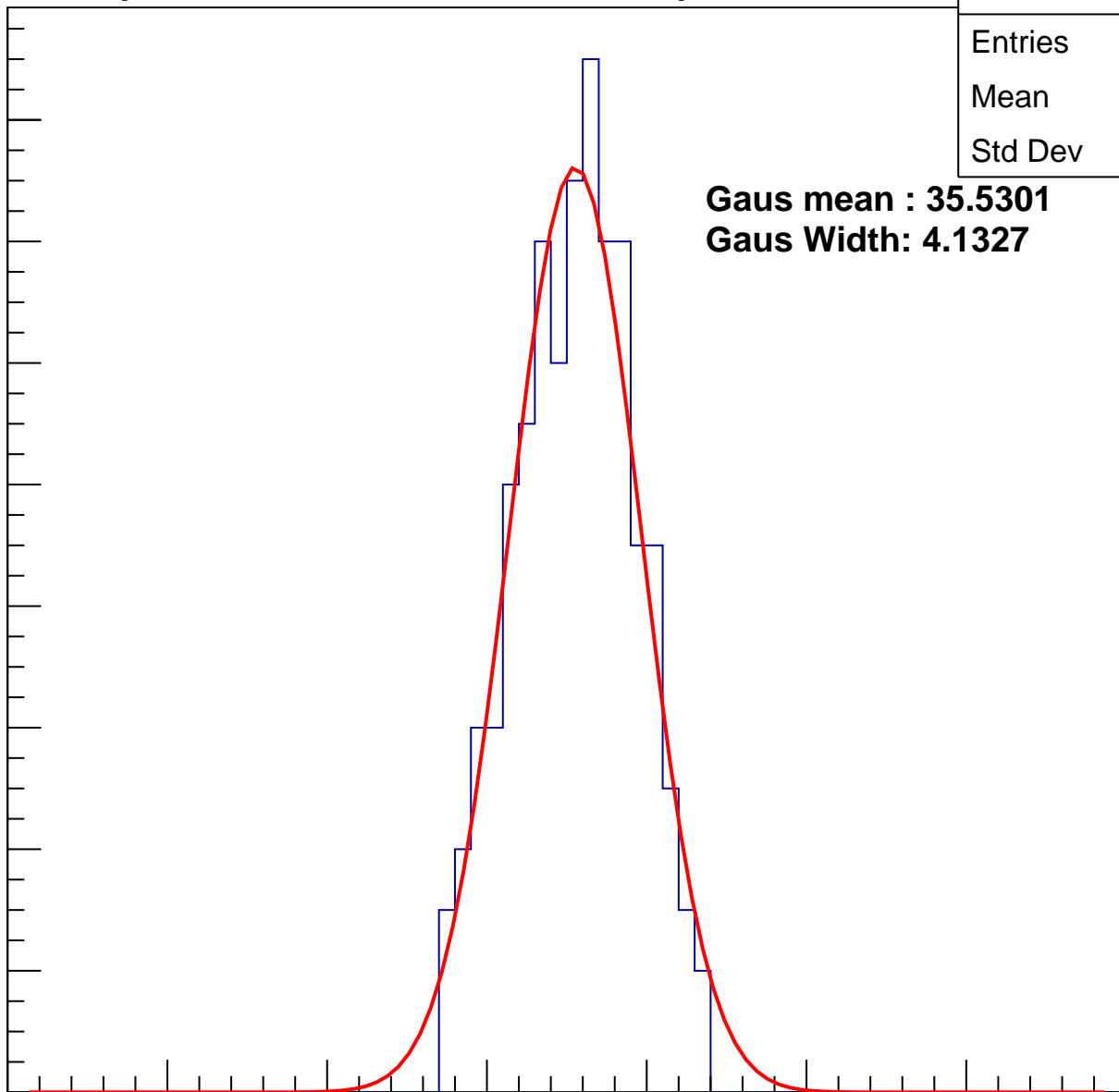
40

50

60

70

ampl



B0L101S, U21-ch67, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

152

Mean

42.49

Std Dev

3.749

Gaus mean : 43.2902

Gaus Width: 3.8985

0

2

4

6

8

10

12

14

16

Entry

calib_packv5_042523_0143.root, FC#1, port C1

Entries

152

Mean

42.49

Std Dev

3.749

Gaus mean : 43.2902

Gaus Width: 3.8985

B0L101S, U21-ch67, adc3

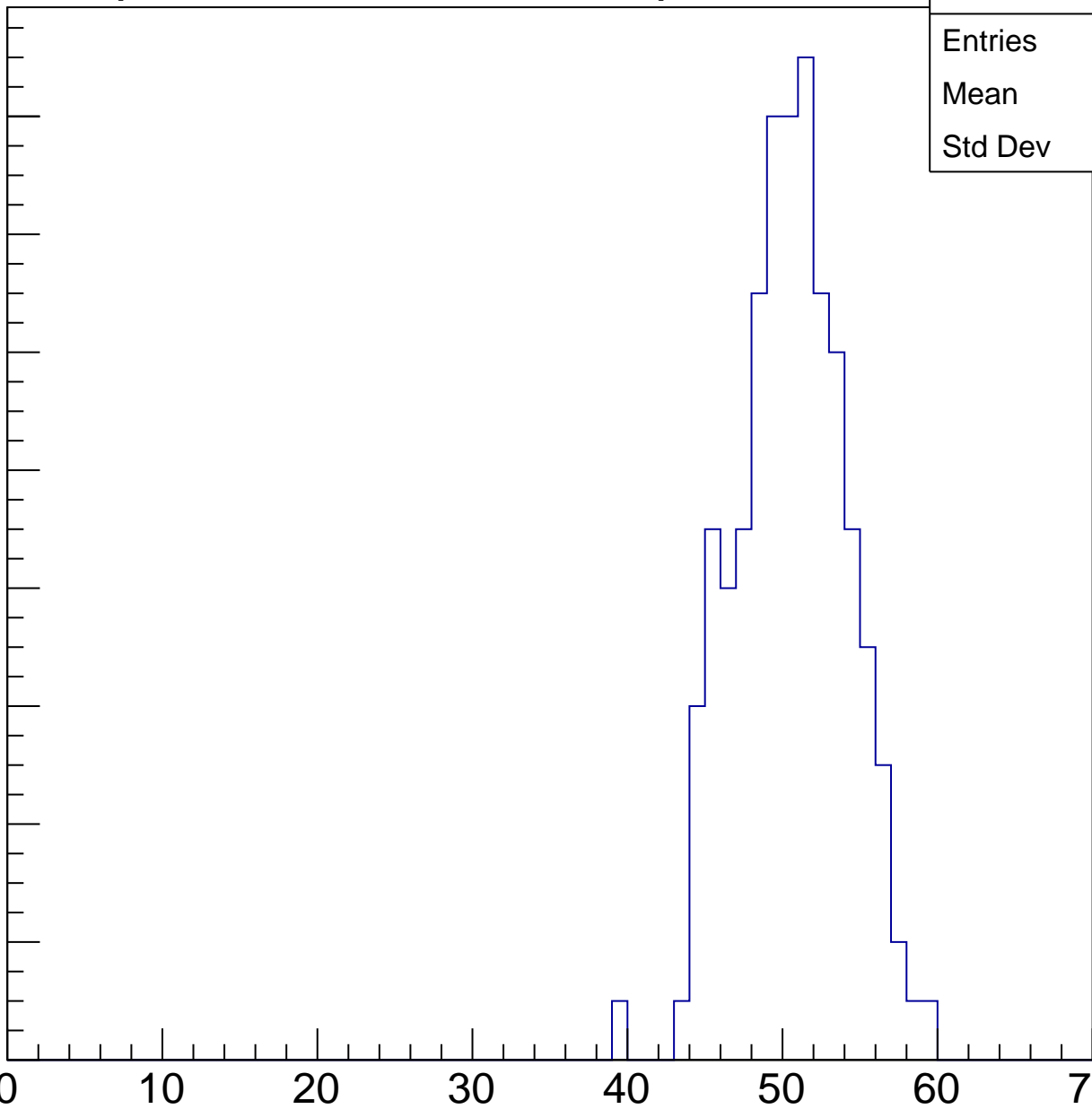
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	146
Mean	50.08
Std Dev	3.517

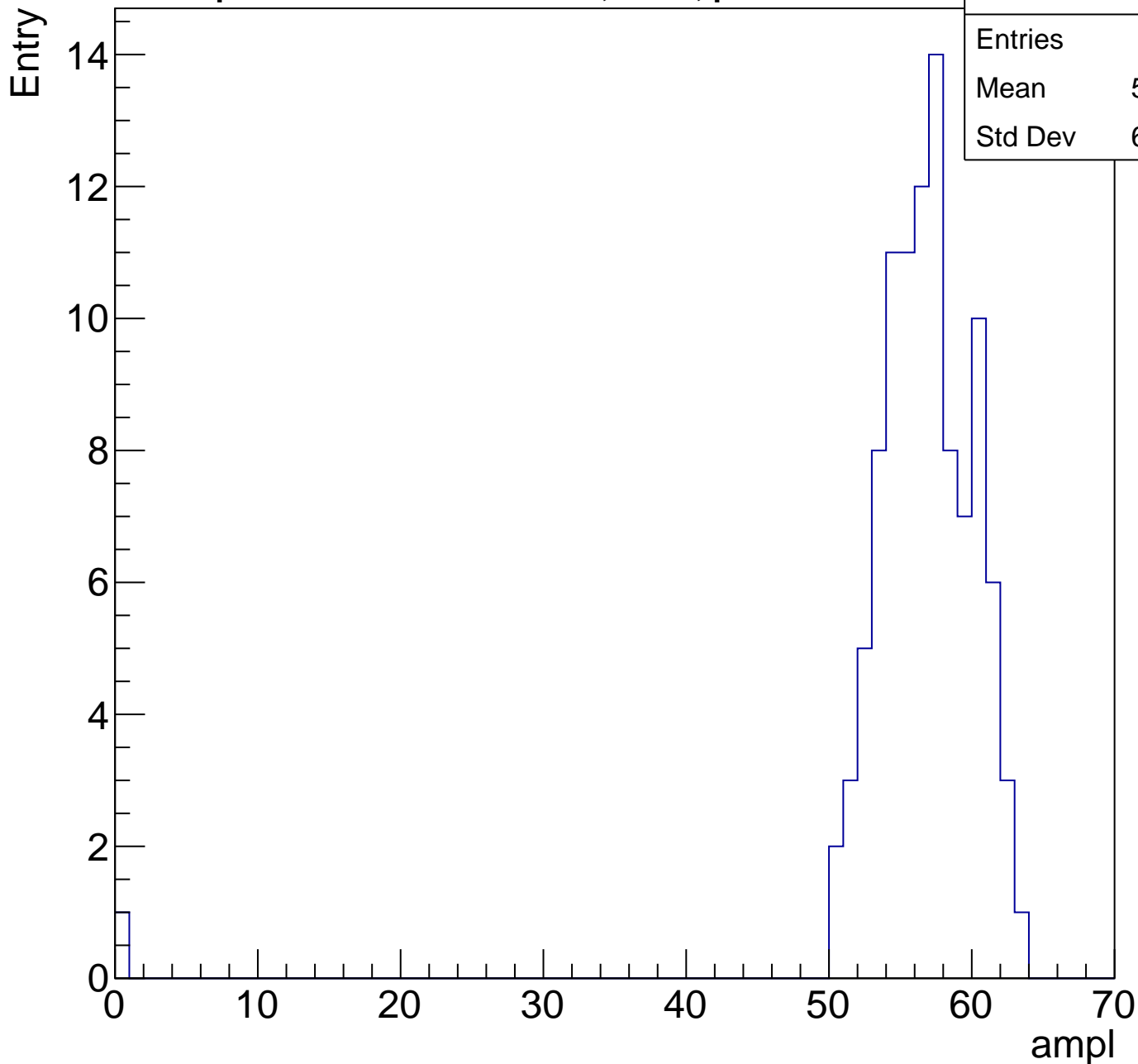
ampl



B0L101S, U21-ch67, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	55.86
Std Dev	6.312



B0L101S, U21-ch67, adc5

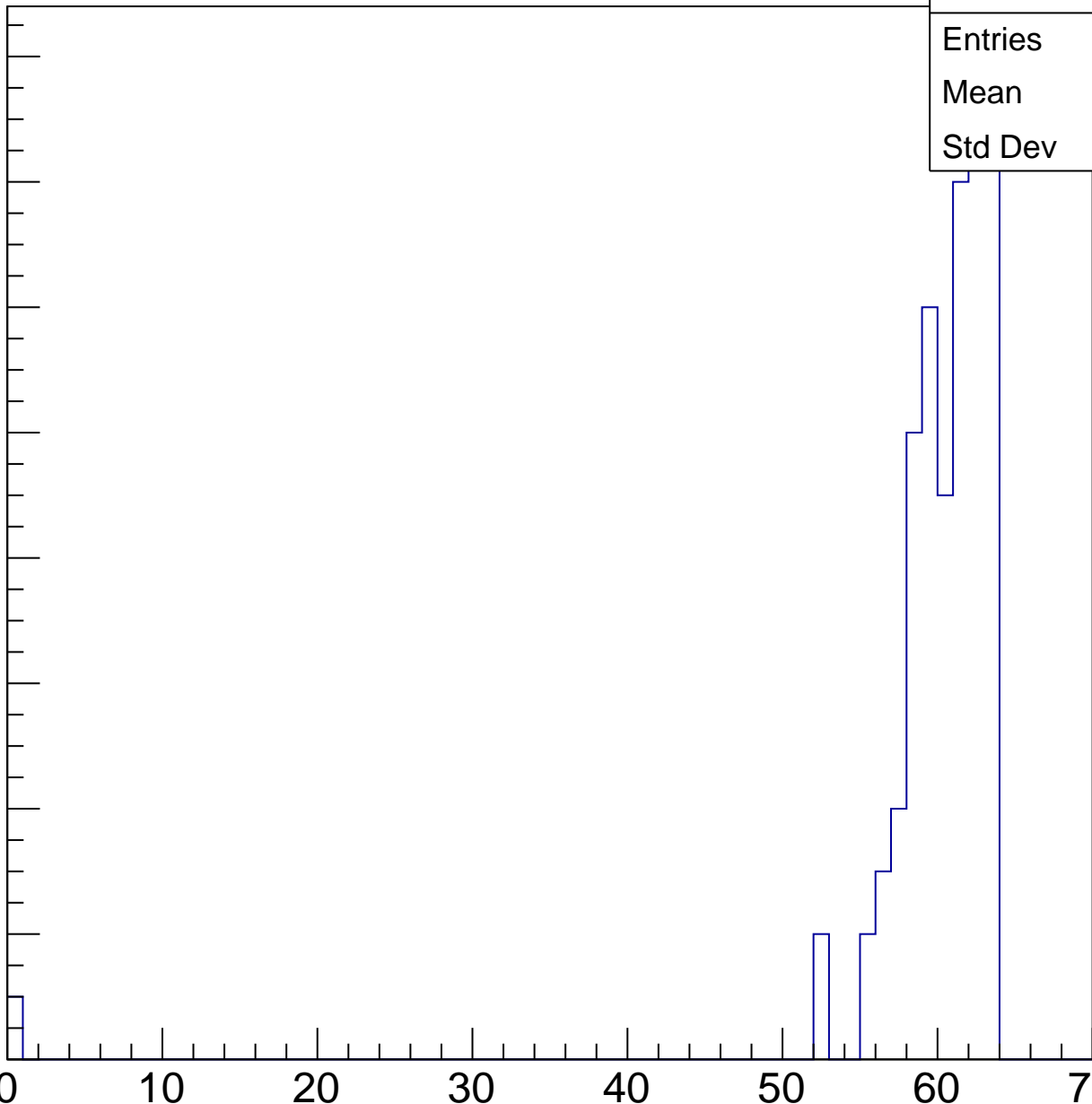
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.46
Std Dev	6.796

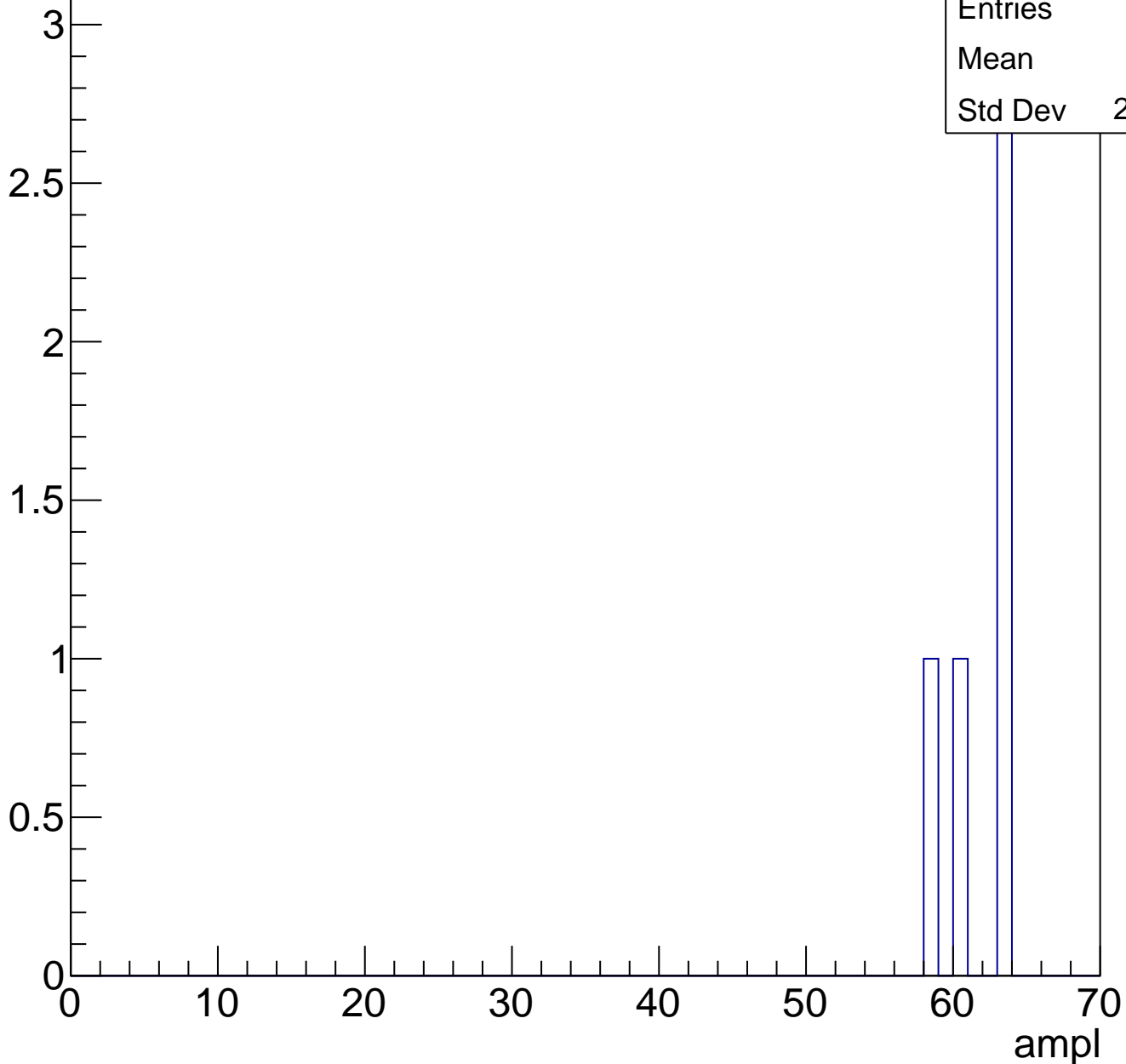
ampl



B0L101S, U21-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch68, adc0

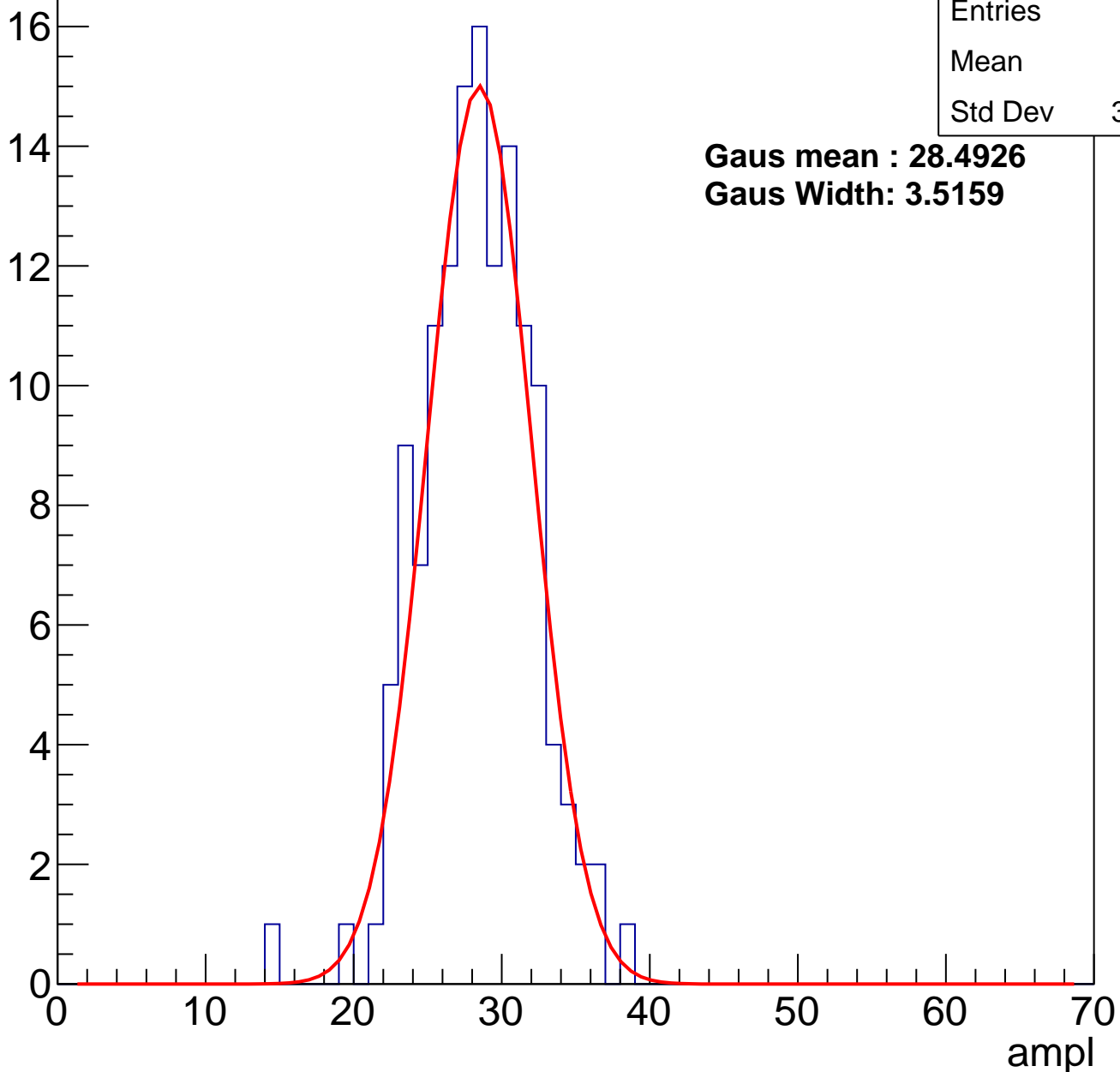
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	27.9
Std Dev	3.689

Gaus mean : 28.4926

Gaus Width: 3.5159

Entry



B0L101S, U21-ch68, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

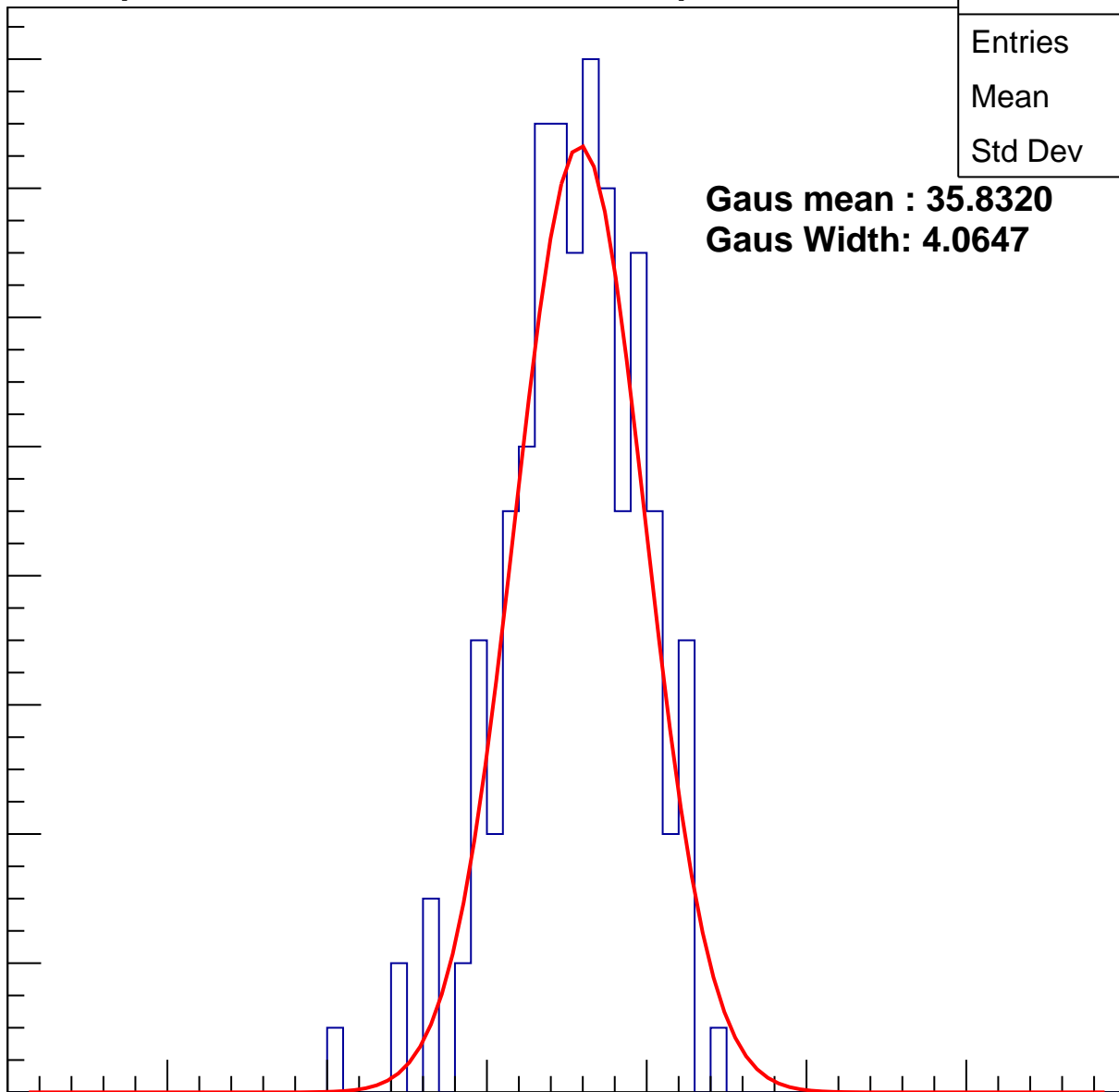
16
14
12
10
8
6
4
2
0

Entries	154
Mean	34.99
Std Dev	4.127

Gaus mean : 35.8320
Gaus Width: 4.0647

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch68, adc2

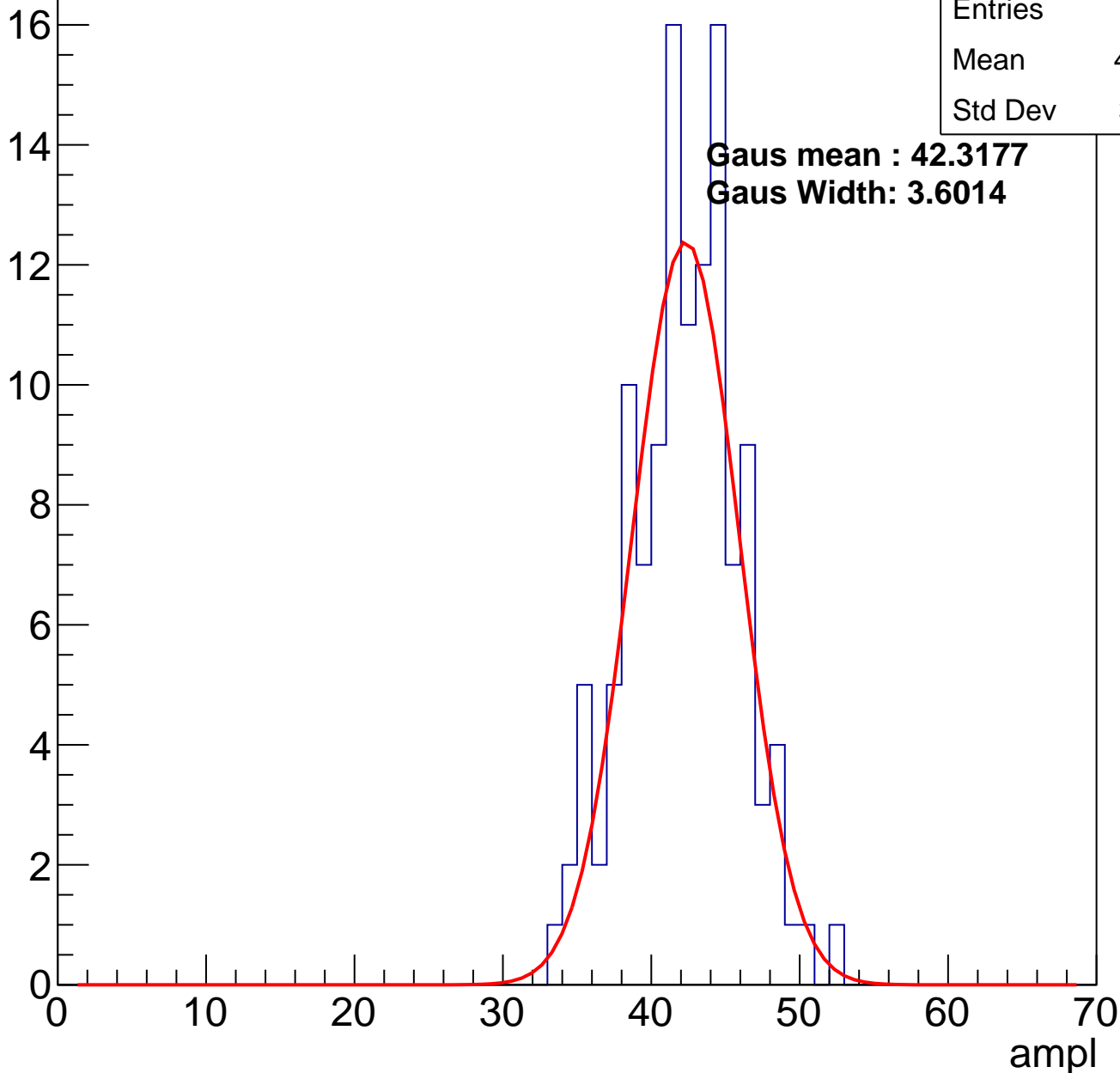
calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	41.78
Std Dev	3.661

Gaus mean : 42.3177

Gaus Width: 3.6014

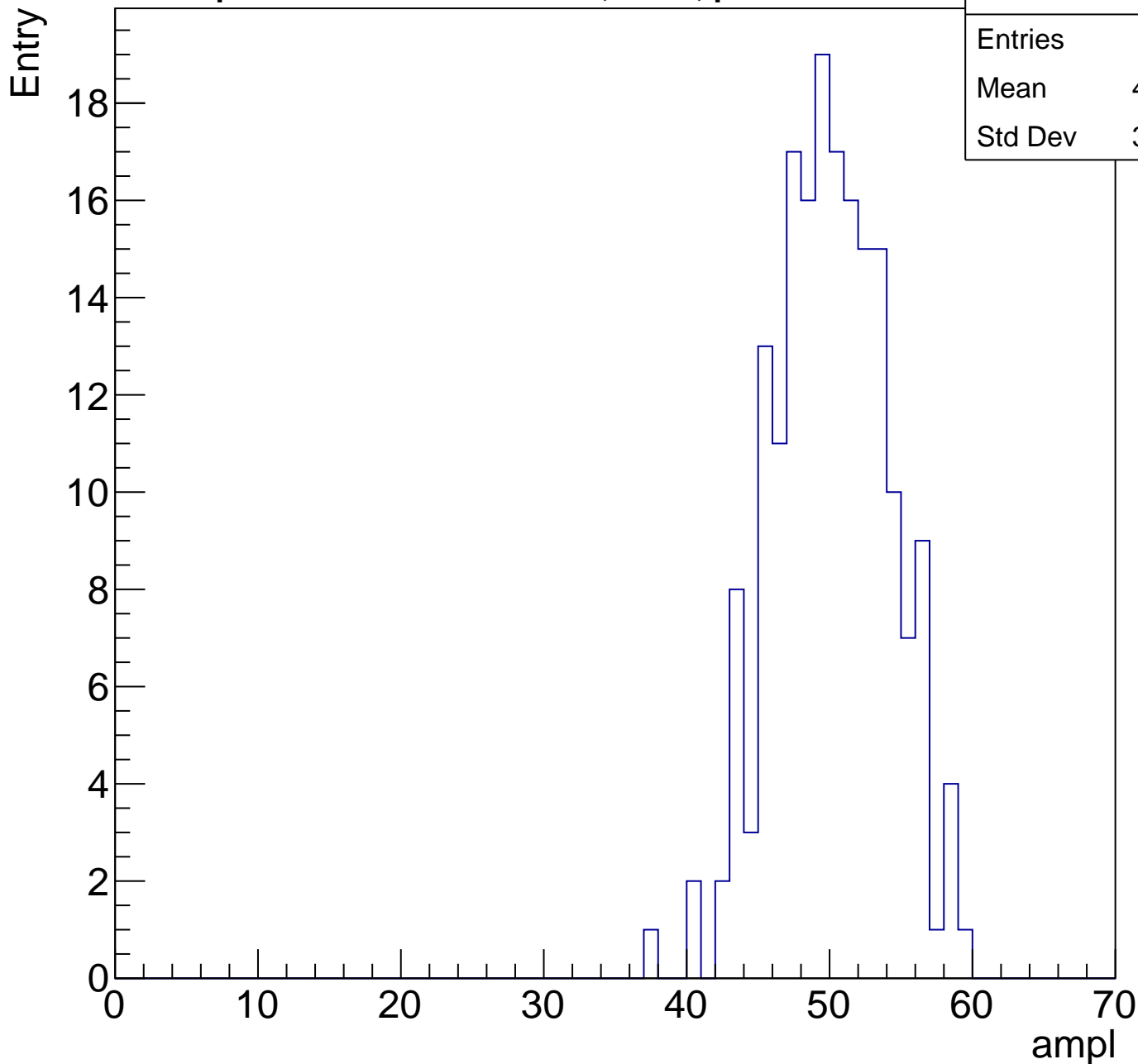
Entry



B0L101S, U21-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	49.65
Std Dev	3.983

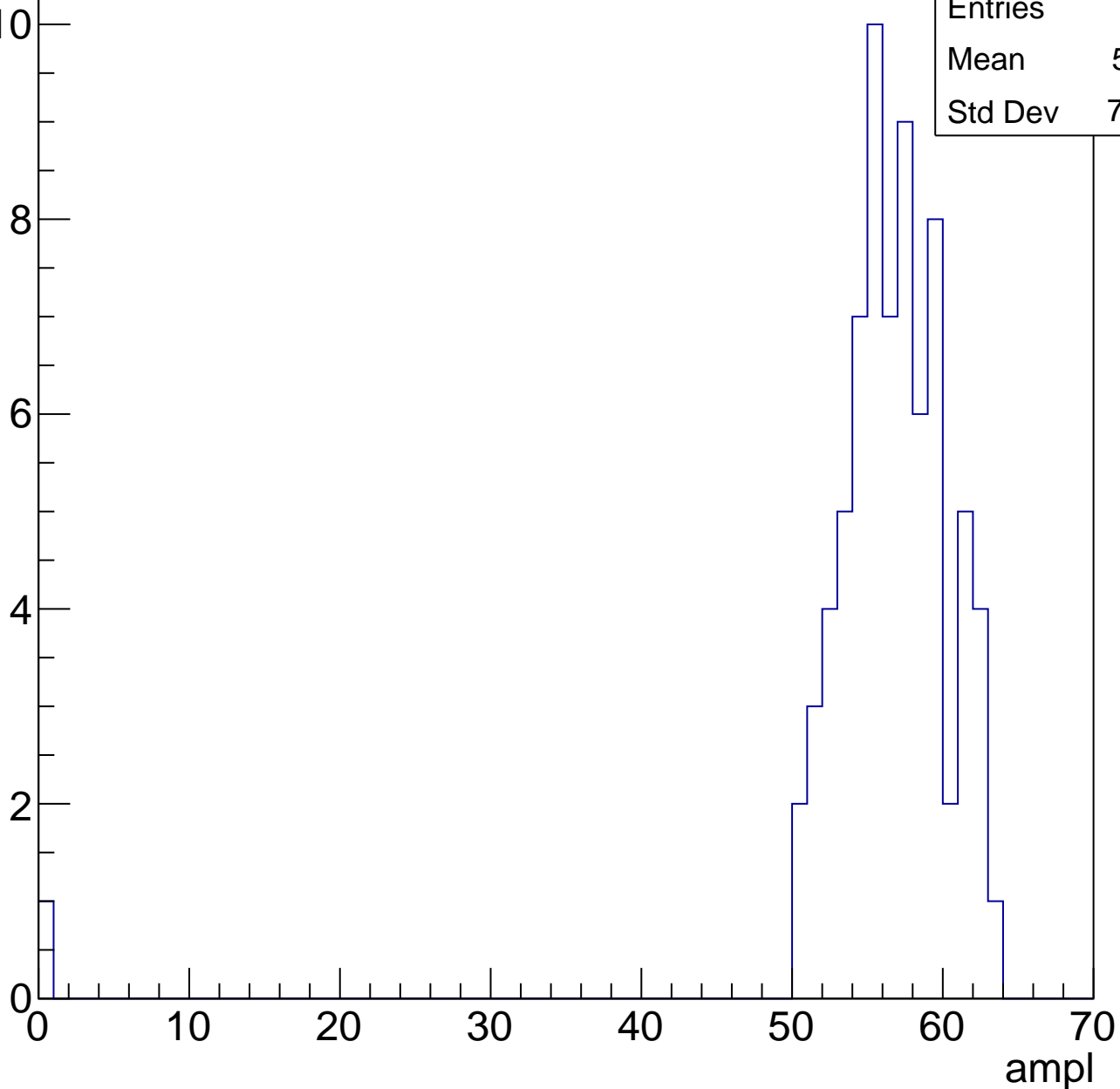


B0L101S, U21-ch68, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	74
Mean	55.61
Std Dev	7.235



B0L101S, U21-ch68, adc5

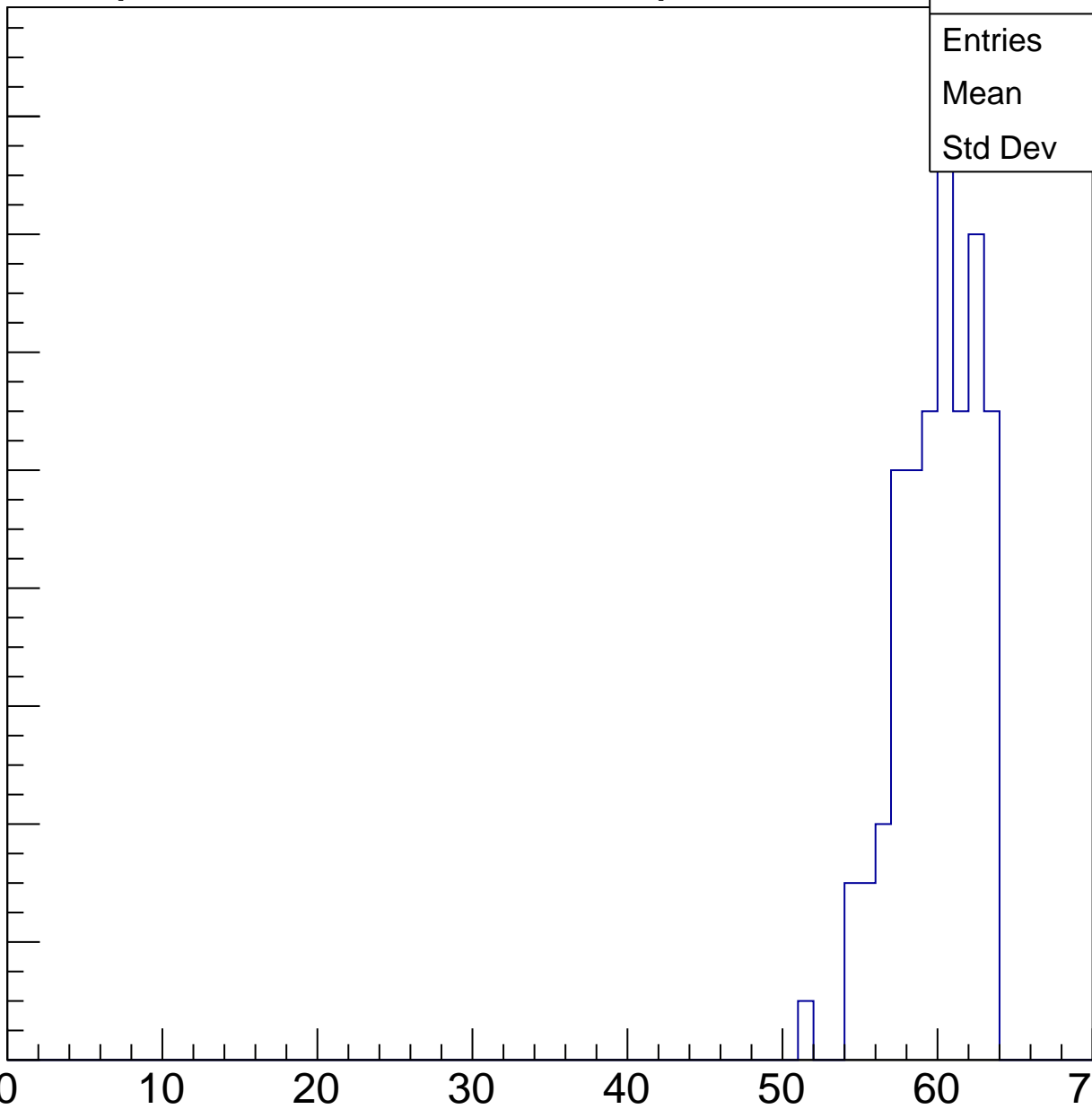
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.51
Std Dev	2.538

ampl

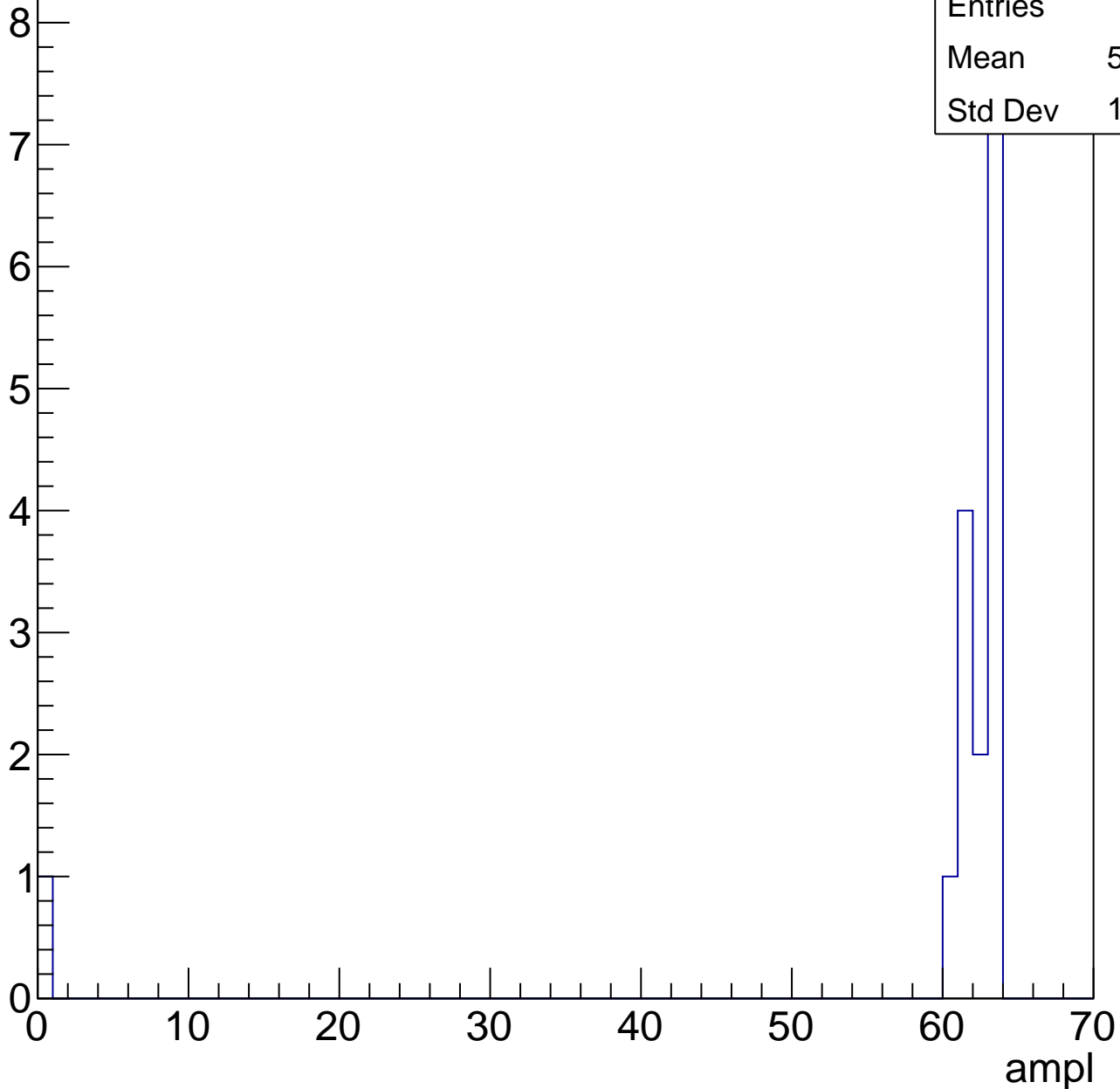


B0L101S, U21-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	58.25
Std Dev	15.07



B0L101S, U21-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch69, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	29.46
Std Dev	3.455

Gaus mean : 30.0299

Gaus Width: 3.5550

Entry

12

10

8

6

4

2

0

0

10

20

30

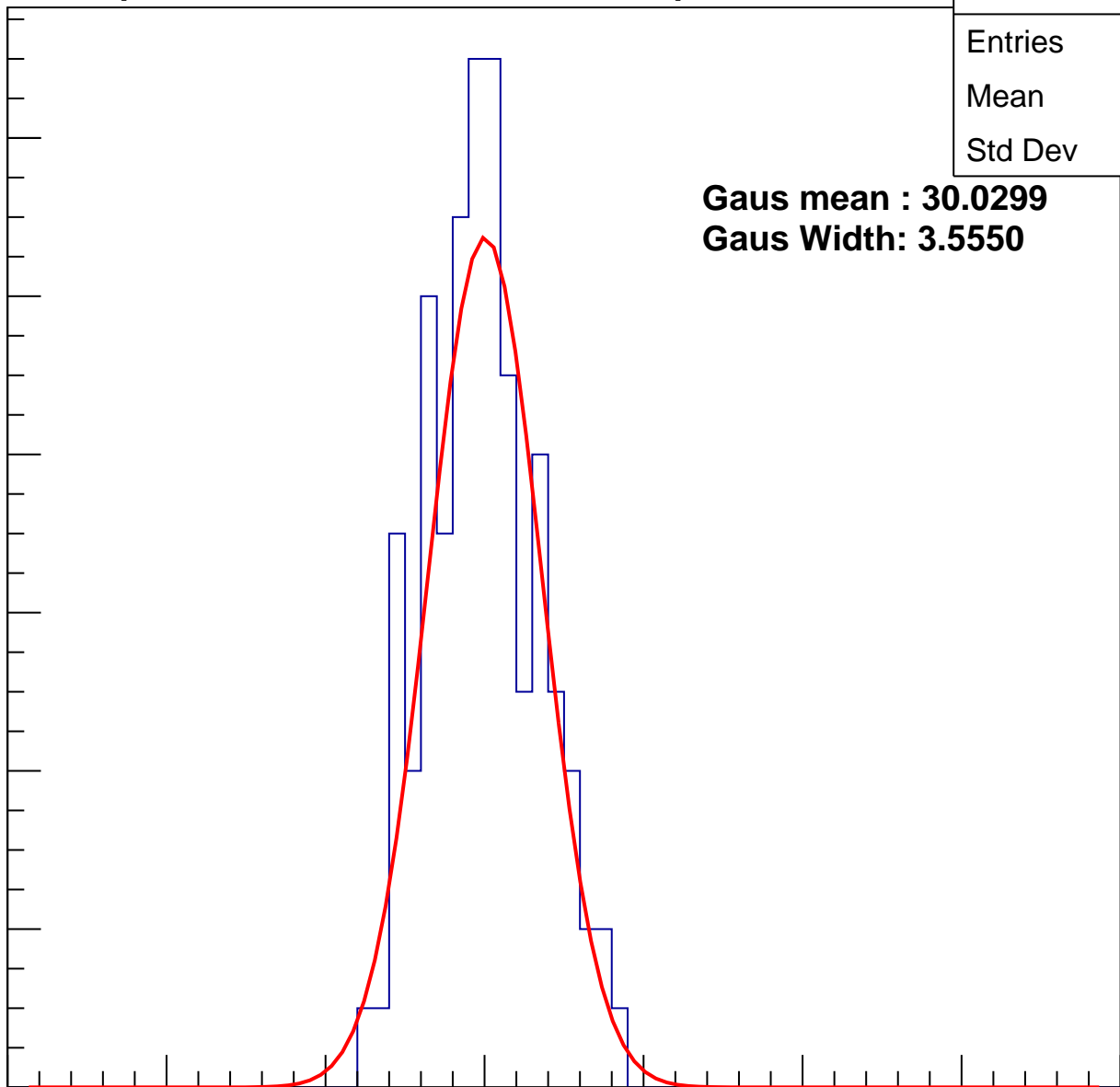
40

50

60

70

ampl



B0L101S, U21-ch69, adc1

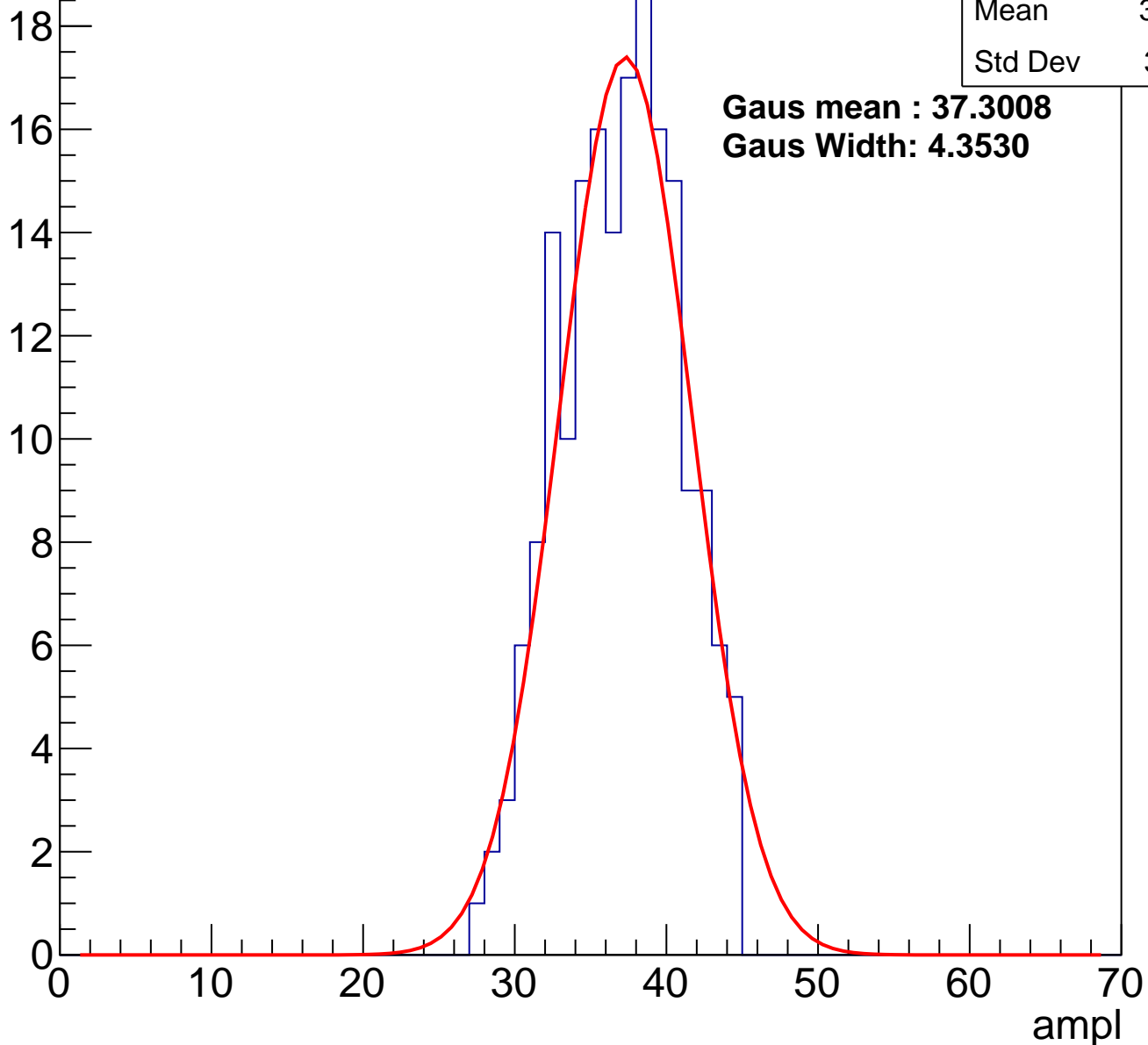
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	36.49
Std Dev	3.861

Gaus mean : 37.3008

Gaus Width: 4.3530

Entry



B0L101S, U21-ch69, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

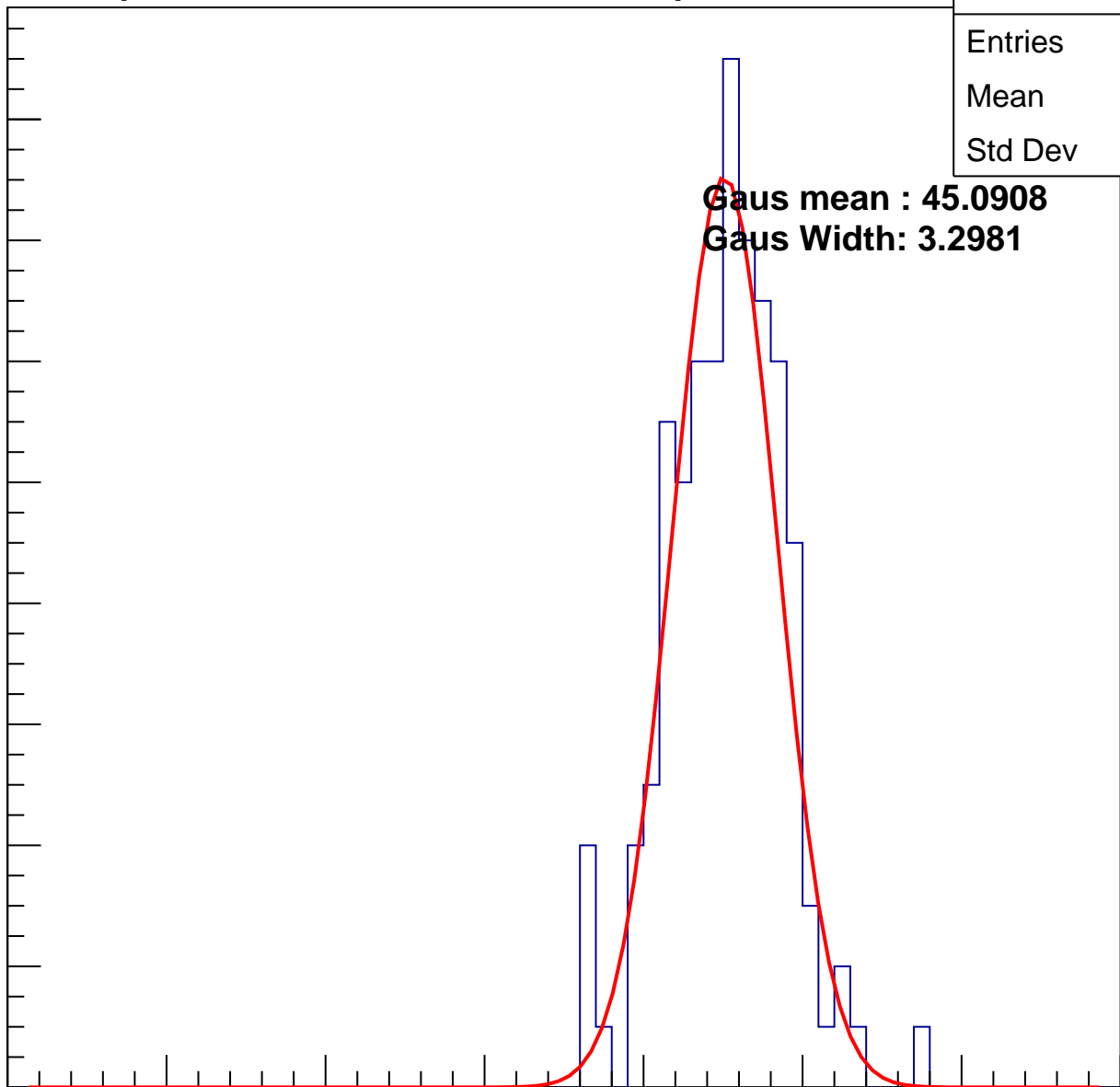
Entries	132
Mean	44.73
Std Dev	3.597

Gaus mean : 45.0908

Gaus Width: 3.2981

0 10 20 30 40 50 60 70

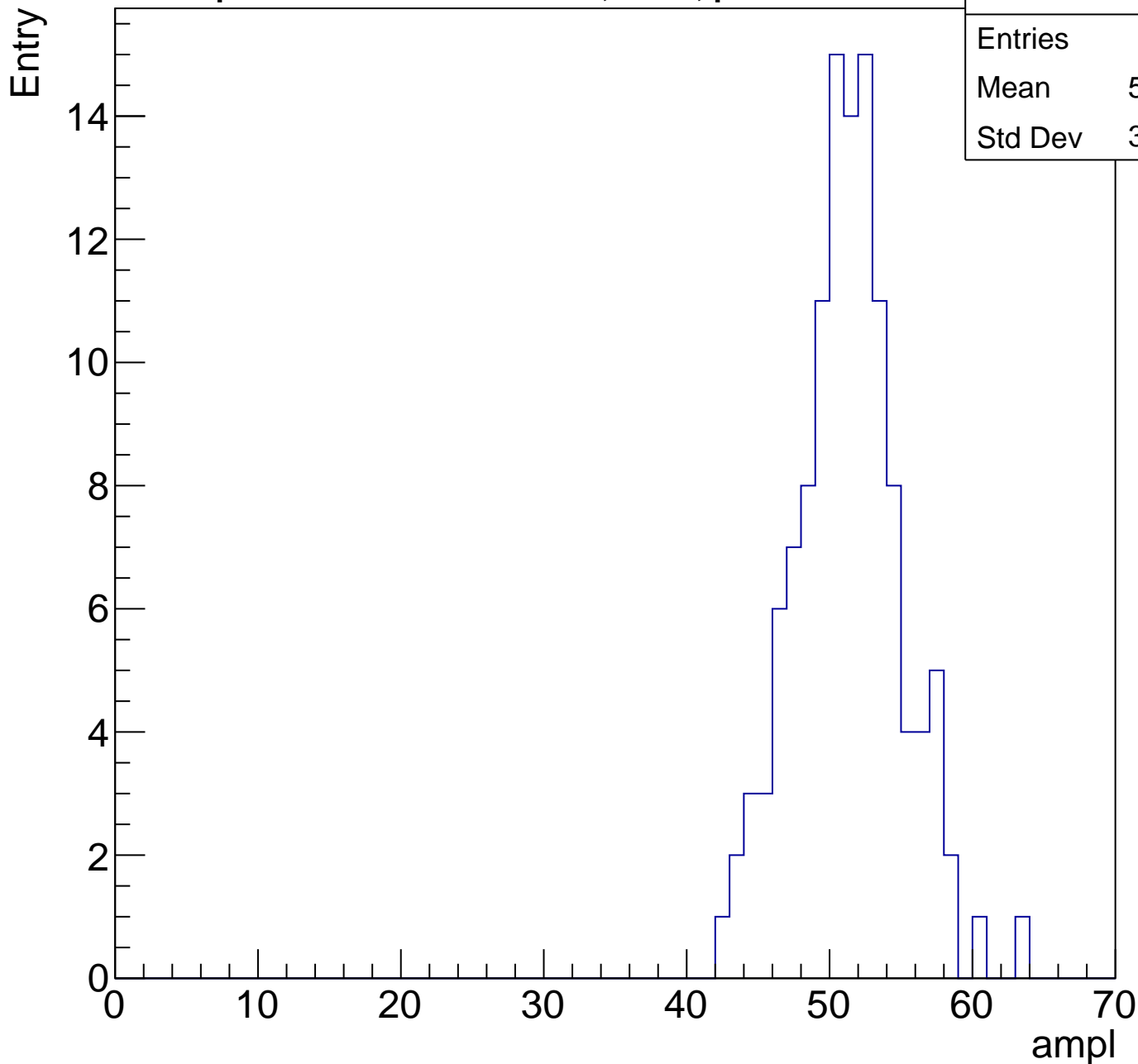
ampl



B0L101S, U21-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	50.83
Std Dev	3.694



B0L101S, U21-ch69, adc4

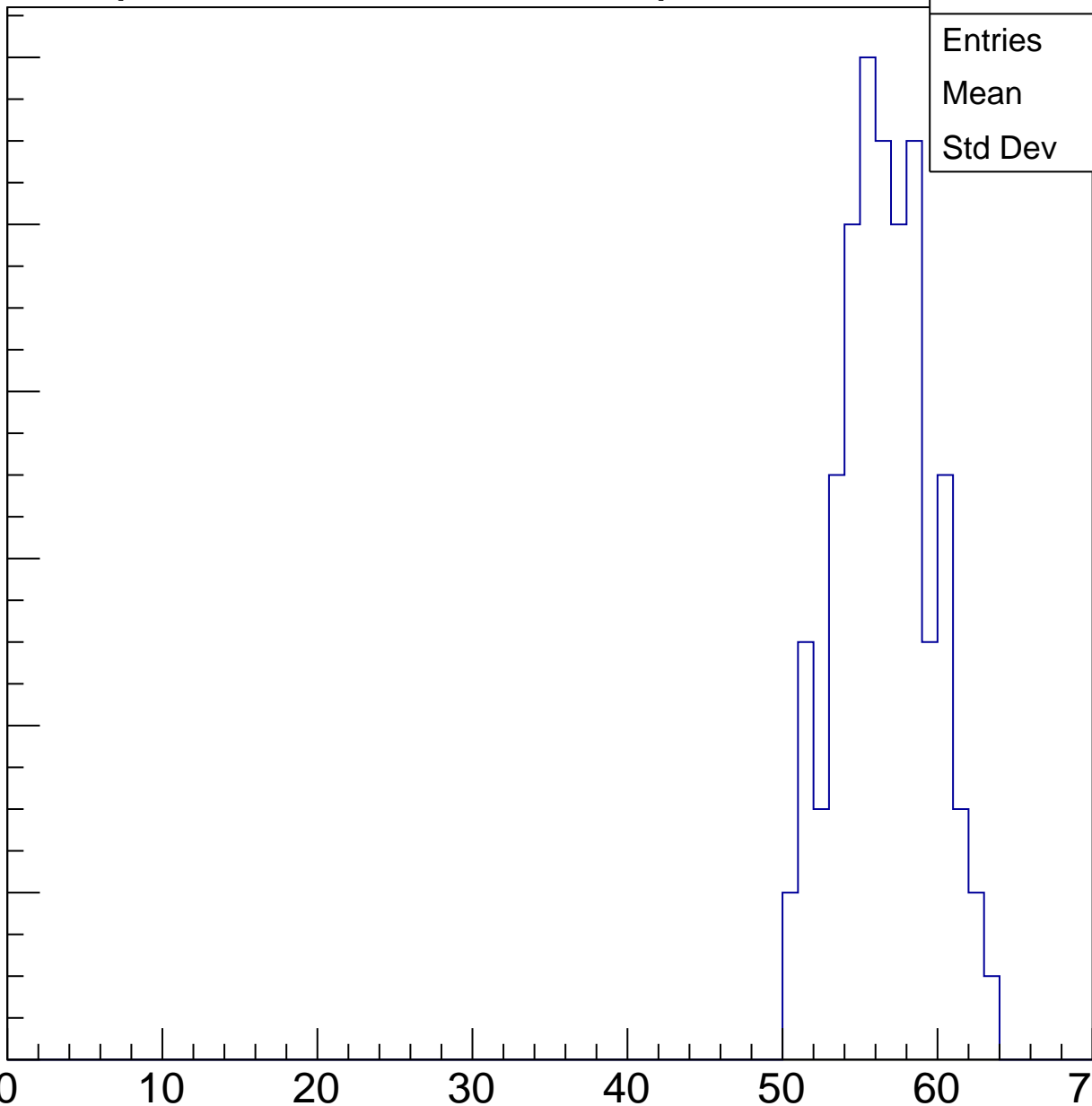
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	89
Mean	56.08
Std Dev	2.935

ampl



B0L101S, U21-ch69, adc5

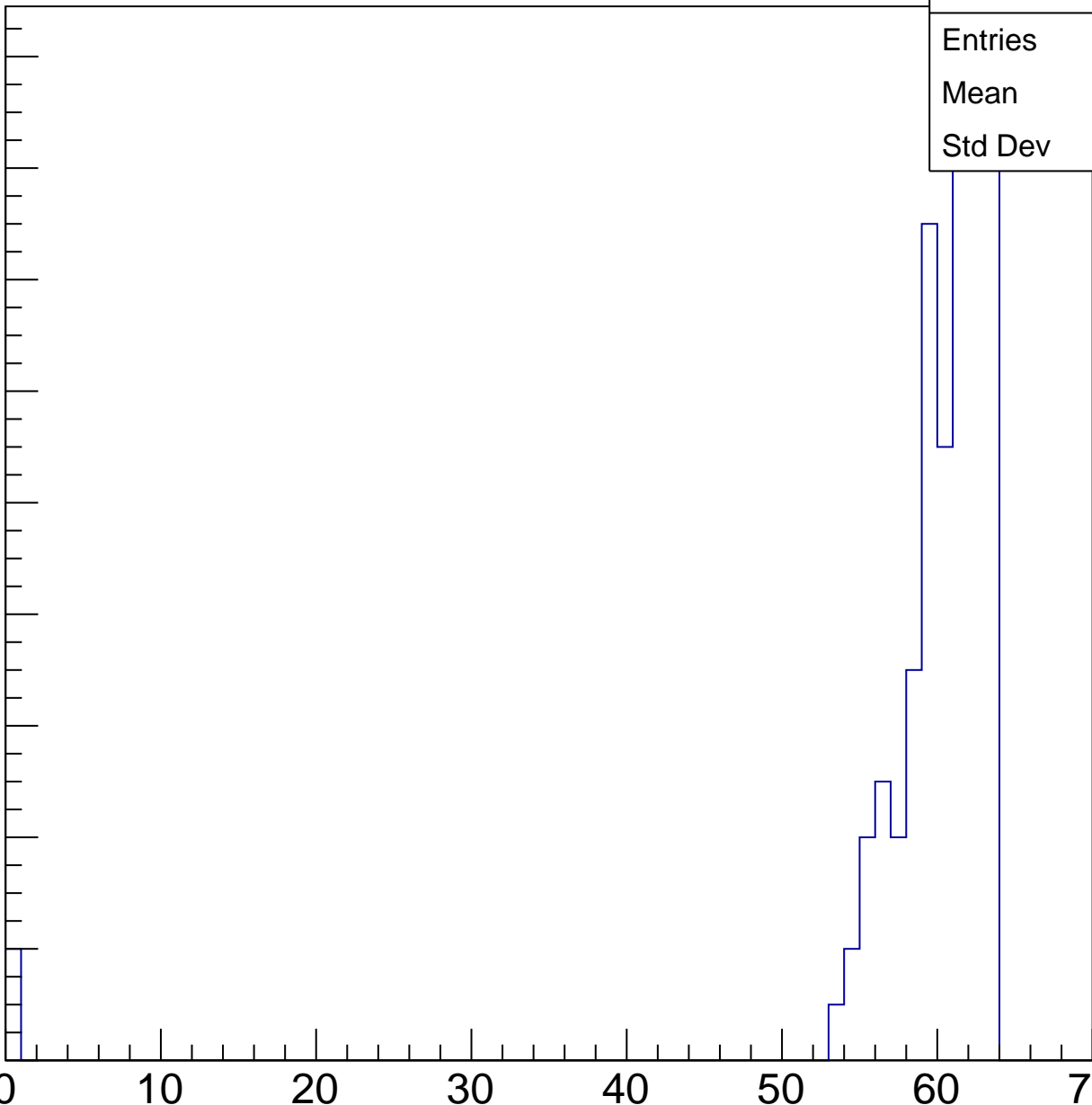
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.83
Std Dev	8.674

ampl



B0L101S, U21-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



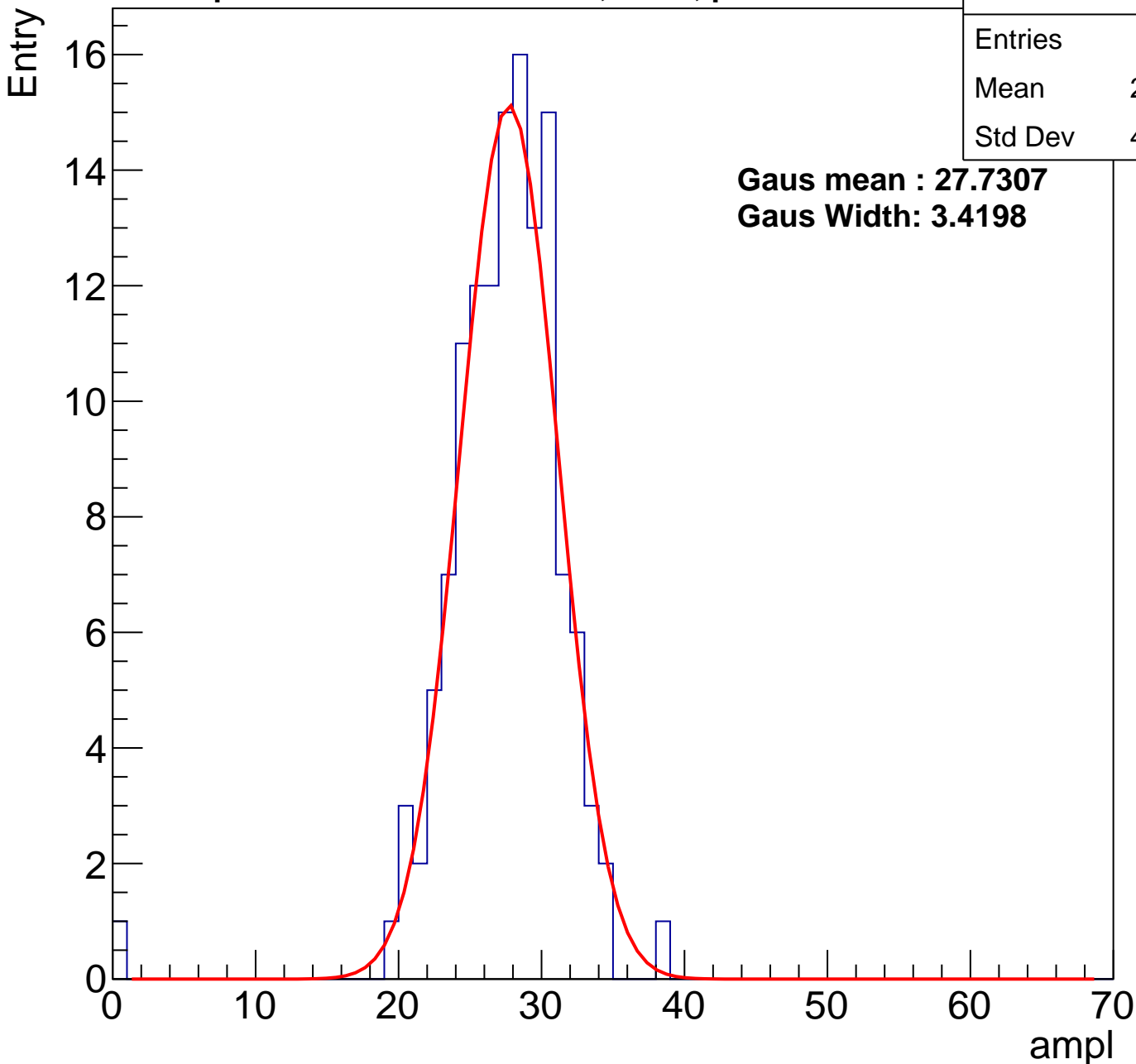
B0L101S, U21-ch70, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	26.98
Std Dev	4.082

Gaus mean : 27.7307

Gaus Width: 3.4198



B0L101S, U21-ch70, adc1

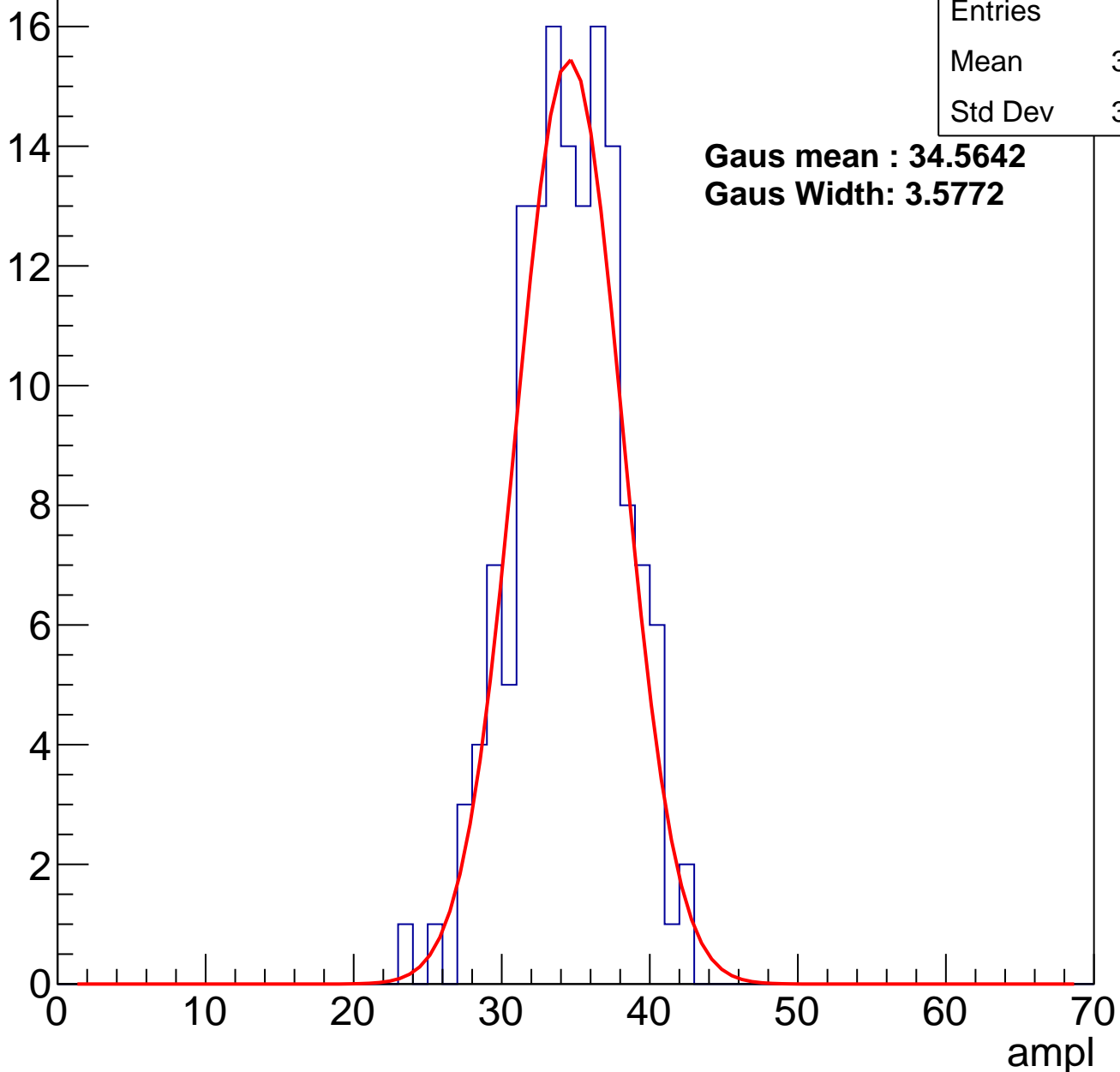
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	34.08
Std Dev	3.566

Gaus mean : 34.5642

Gaus Width: 3.5772

Entry



B0L101S, U21-ch70, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	41.1
Std Dev	3.943

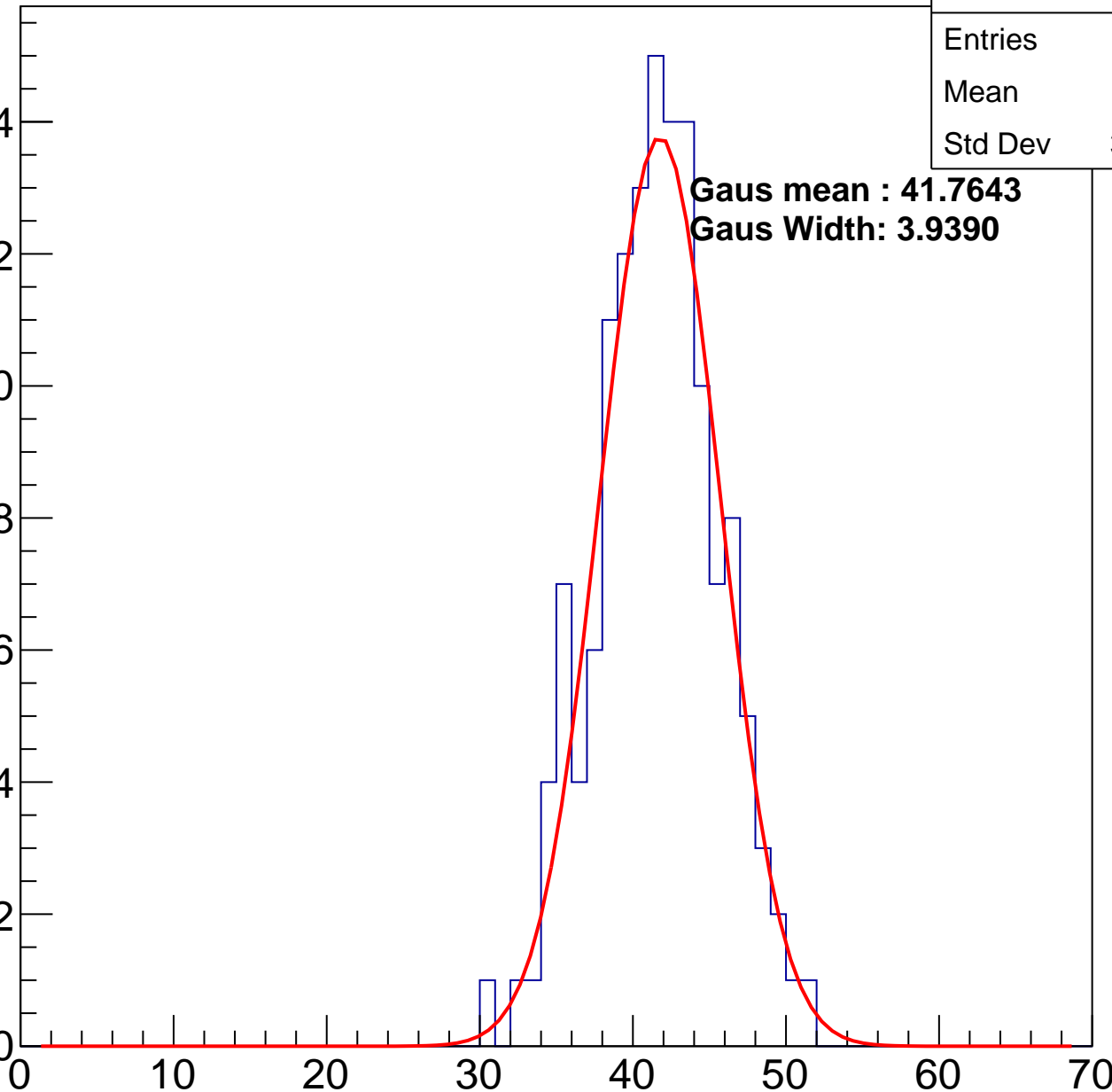
Gaus mean : 41.7643

Gaus Width: 3.9390

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

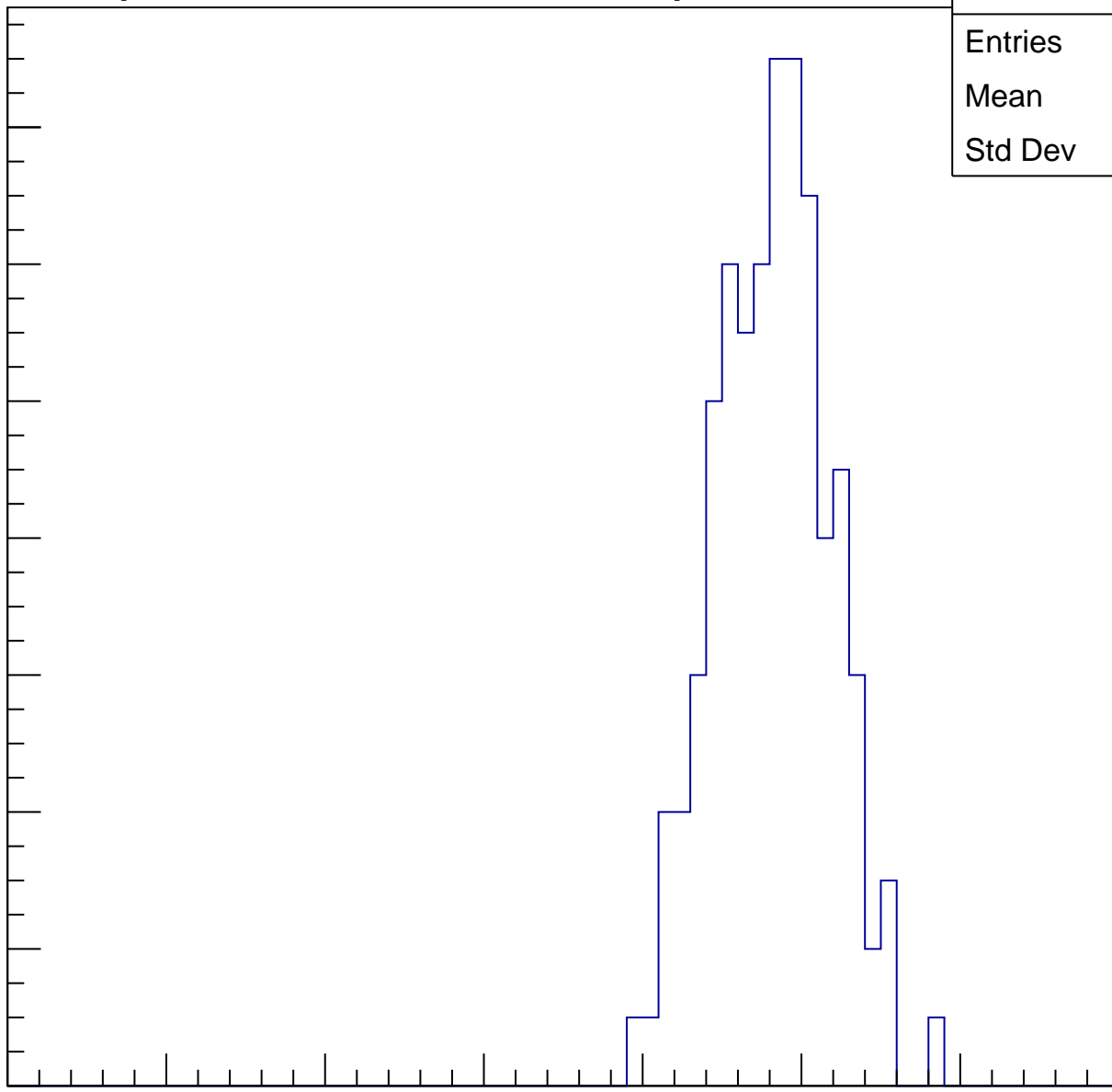
Entries	133
Mean	47.74
Std Dev	3.577

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch70, adc4

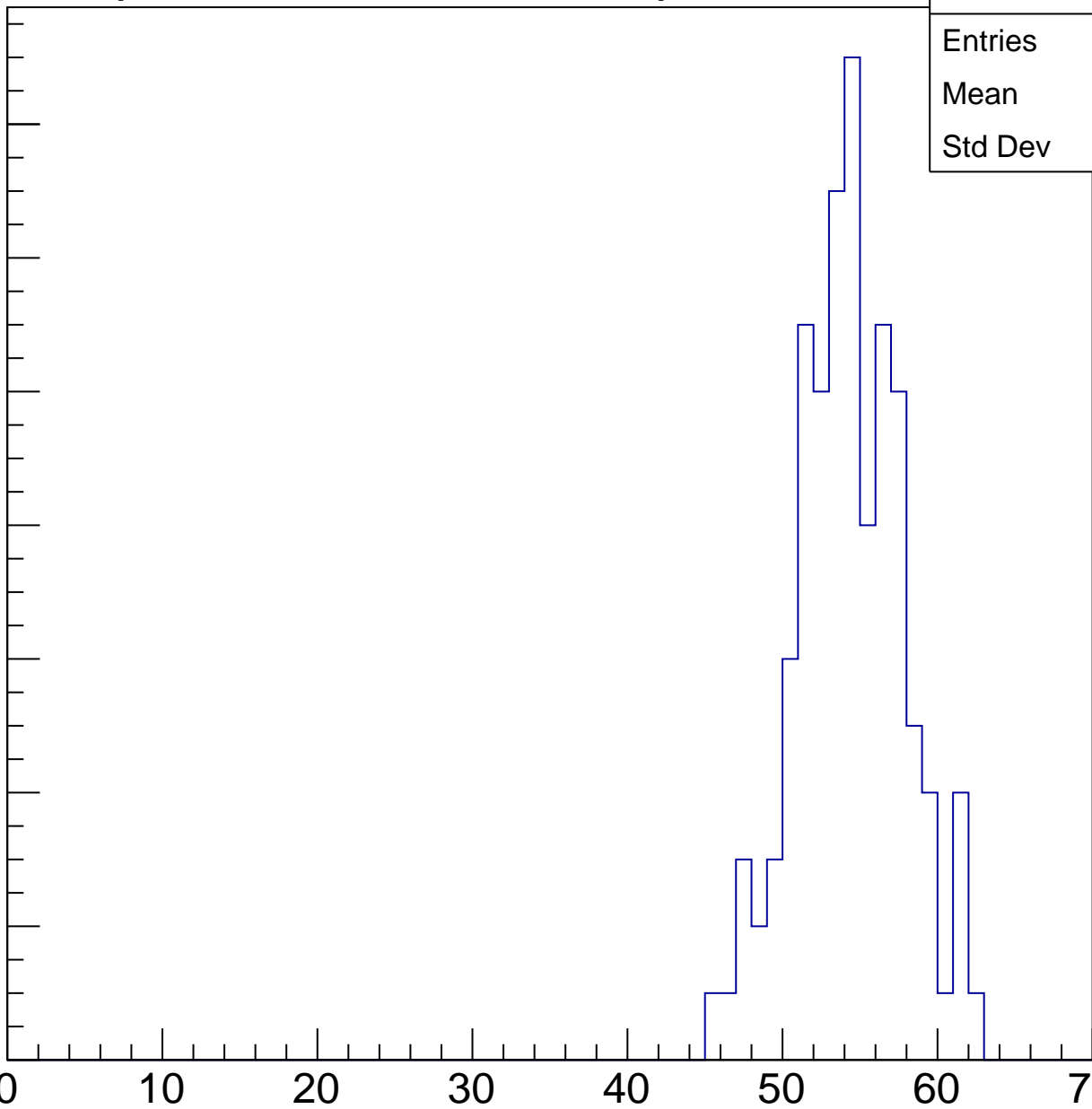
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	53.88
Std Dev	3.458

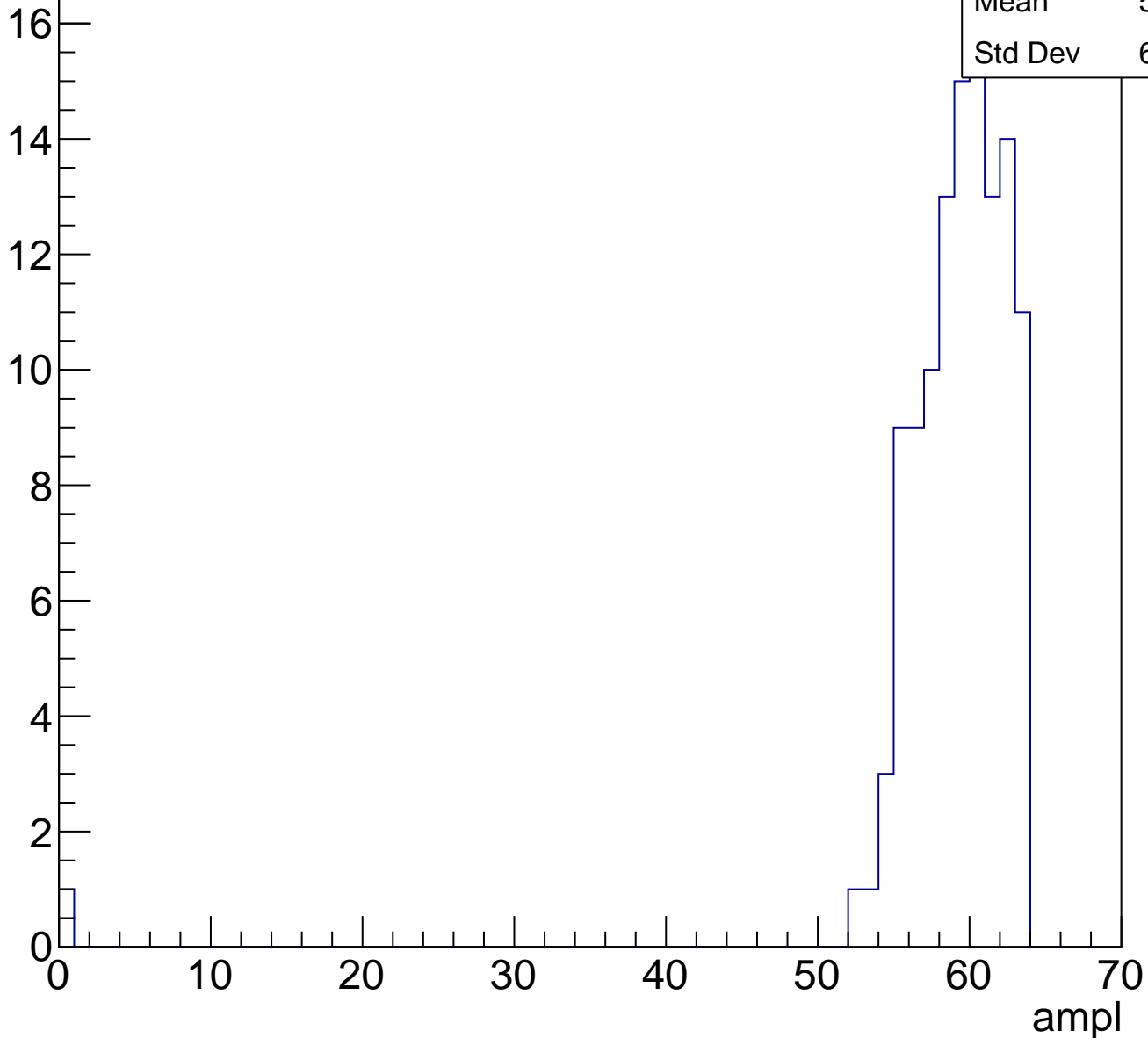
ampl



B0L101S, U21-ch70, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

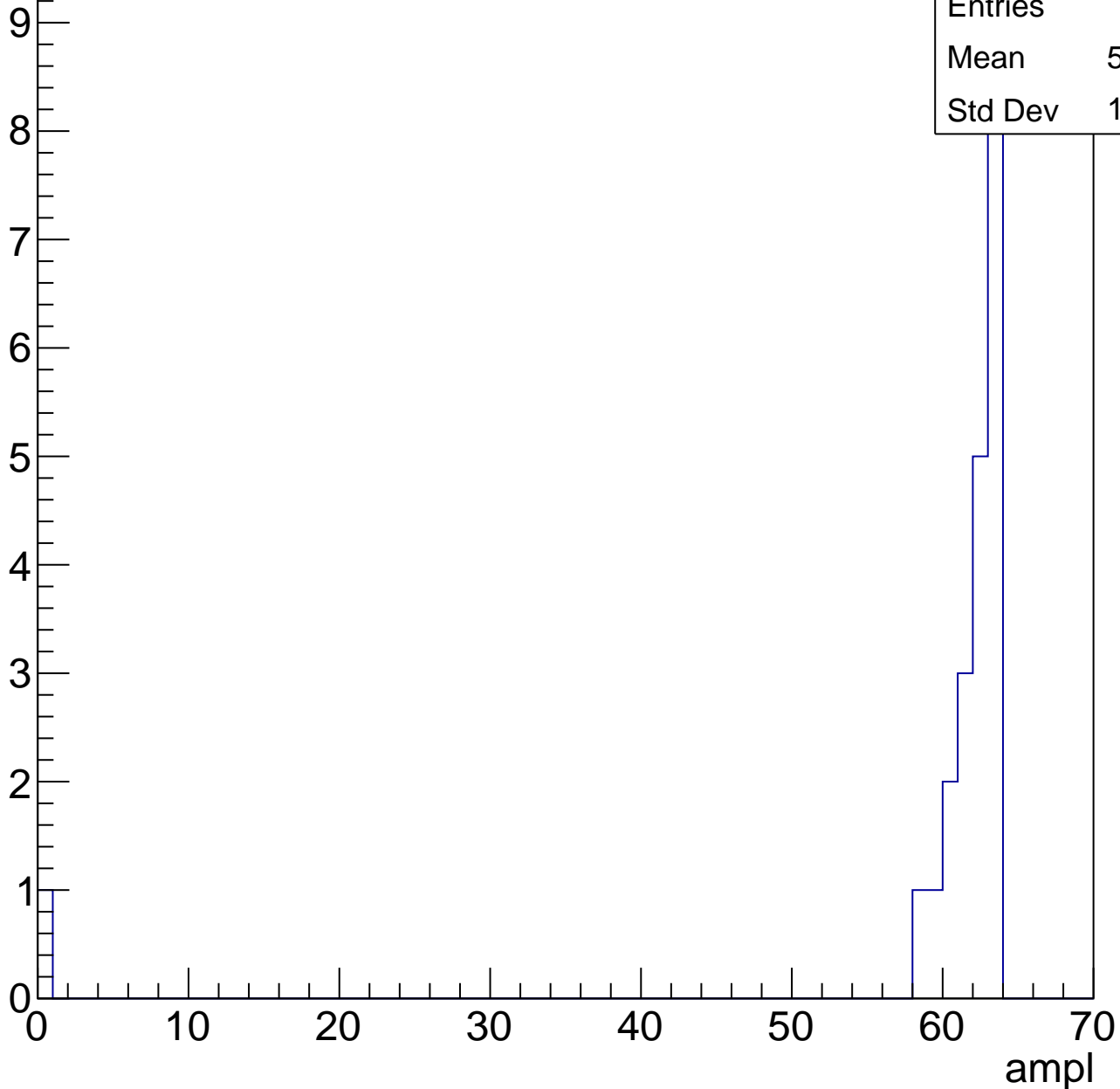


B0L101S, U21-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	22
Mean	58.95
Std Dev	12.94



B0L101S, U21-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch71, adc0

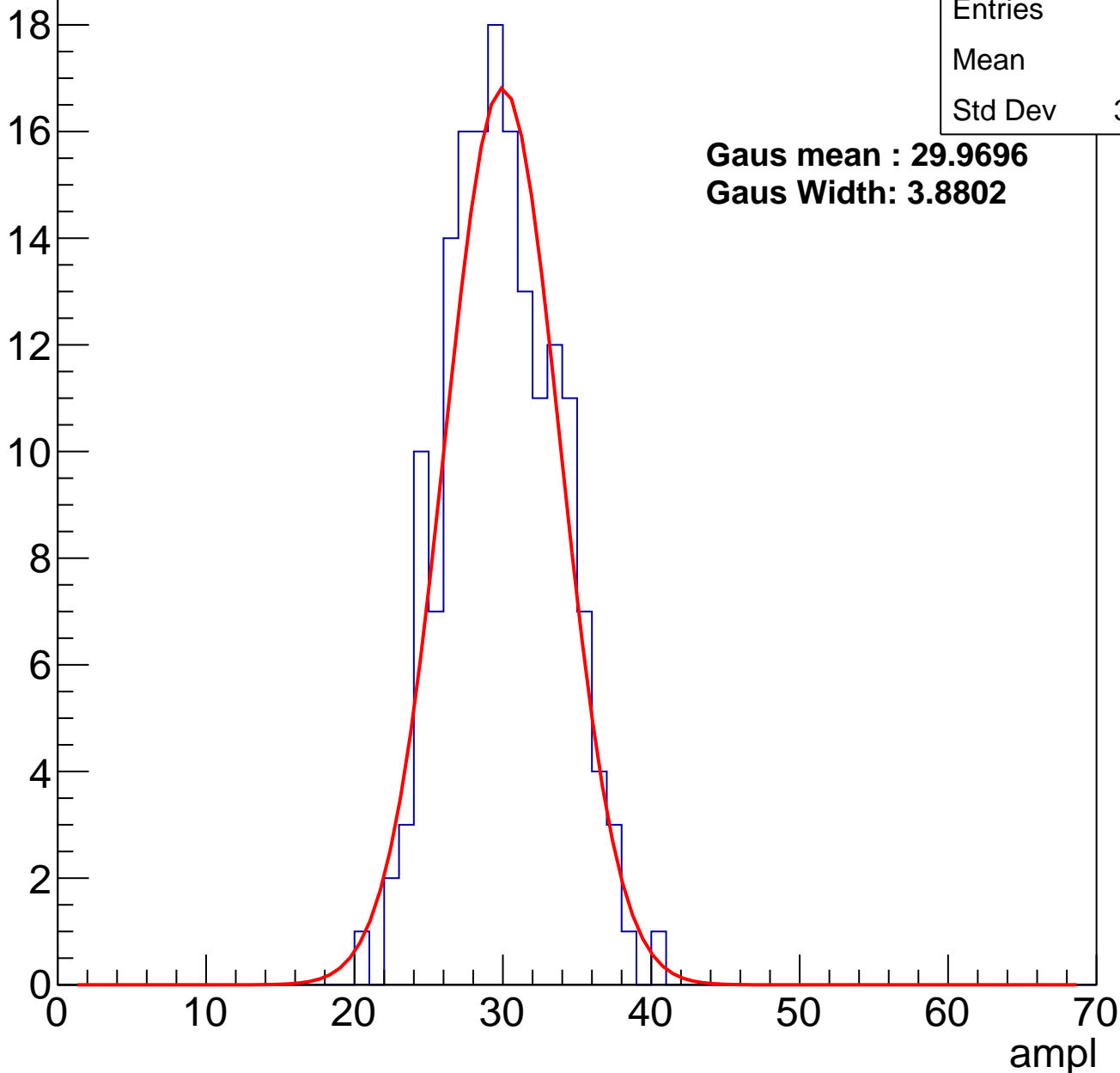
calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	29.5
Std Dev	3.714

Gaus mean : 29.9696

Gaus Width: 3.8802

Entry



B0L101S, U21-ch71, adc1

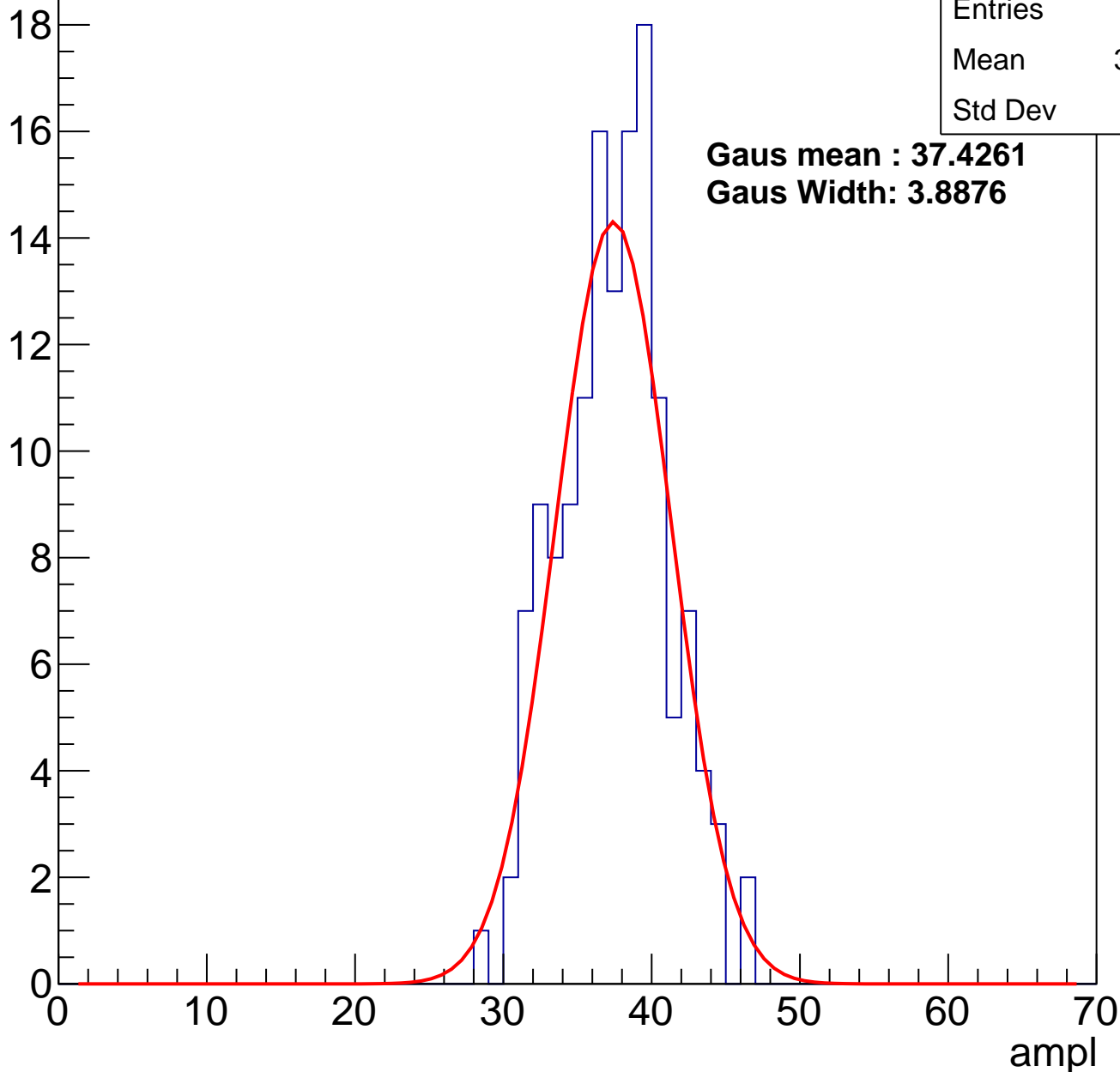
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	36.97
Std Dev	3.58

Gaus mean : 37.4261

Gaus Width: 3.8876

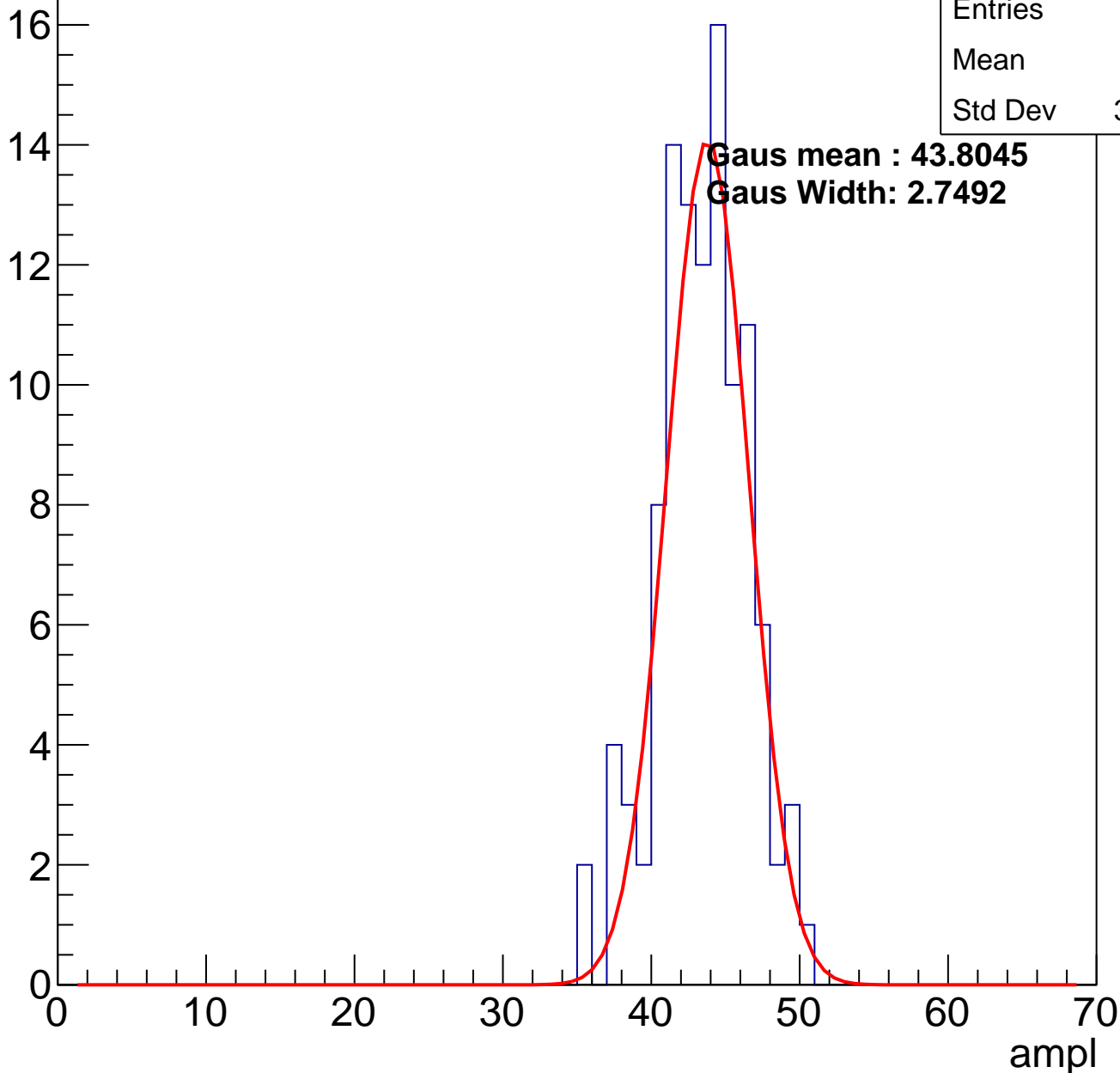
Entry



B0L101S, U21-ch71, adc2

calib_packv5_042523_0143.root, FC#1, port C1

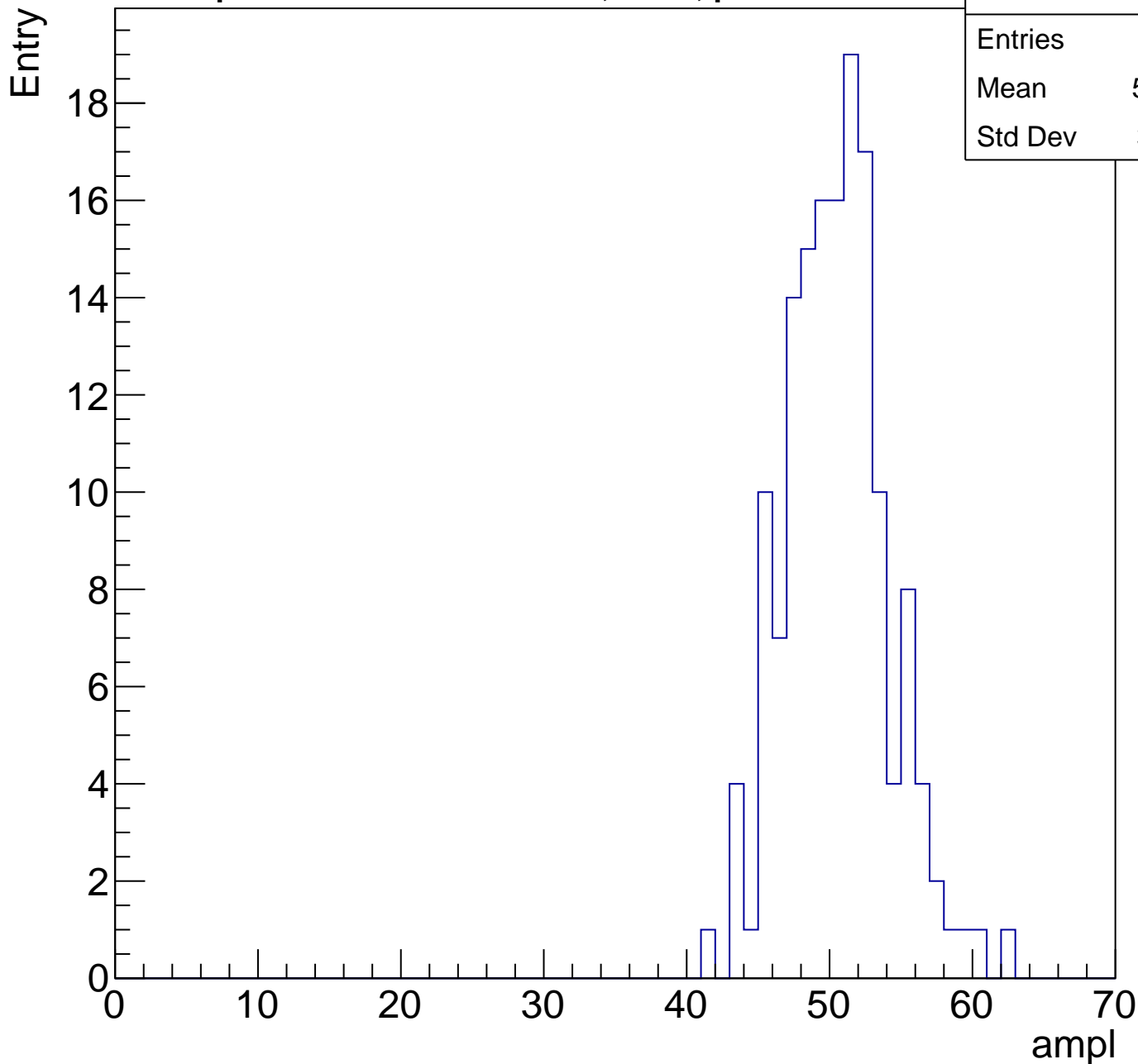
Entry



B0L101S, U21-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

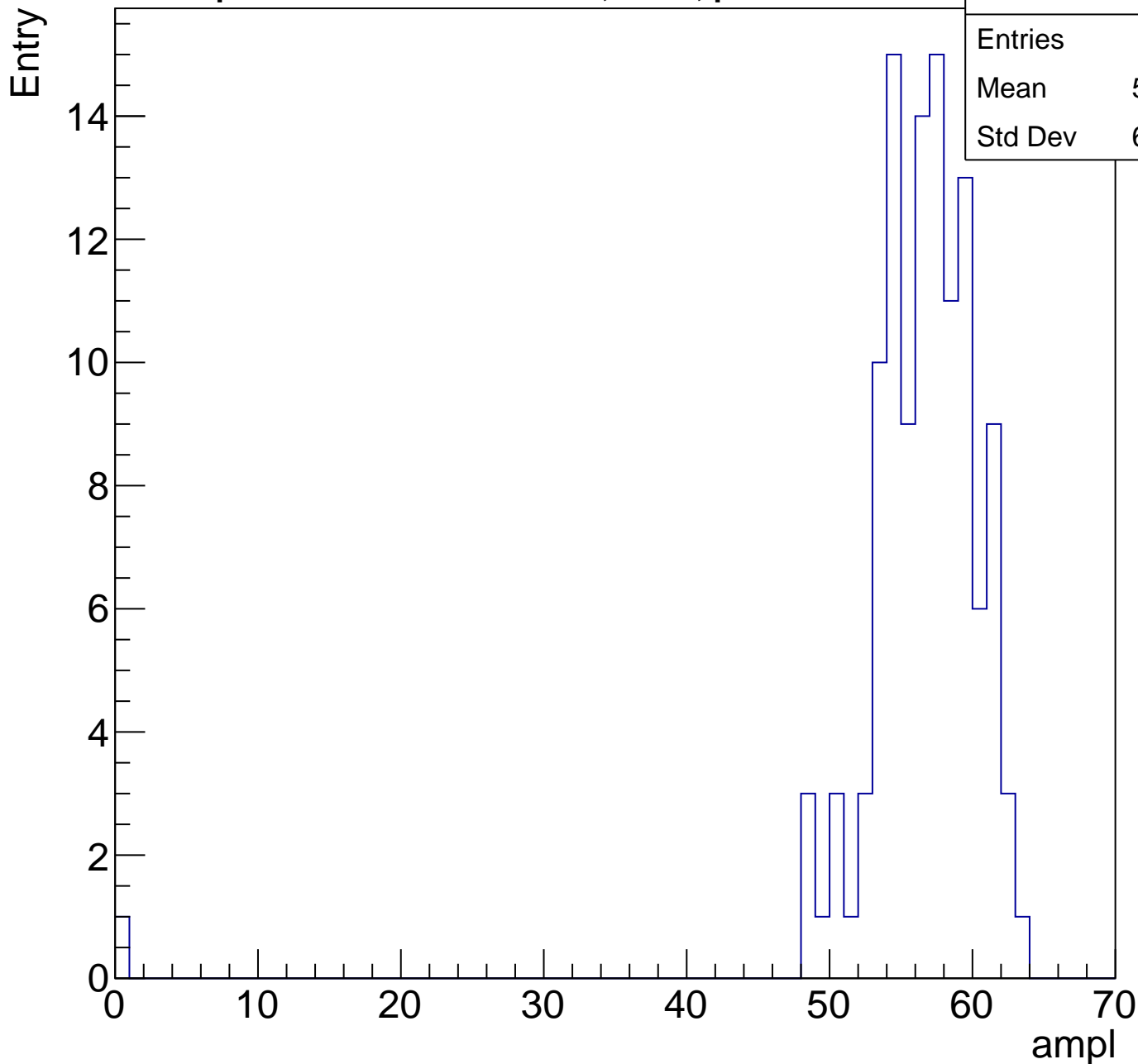
Entries	152
Mean	50.05
Std Dev	3.581



B0L101S, U21-ch71, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	55.82
Std Dev	6.084



B0L101S, U21-ch71, adc5

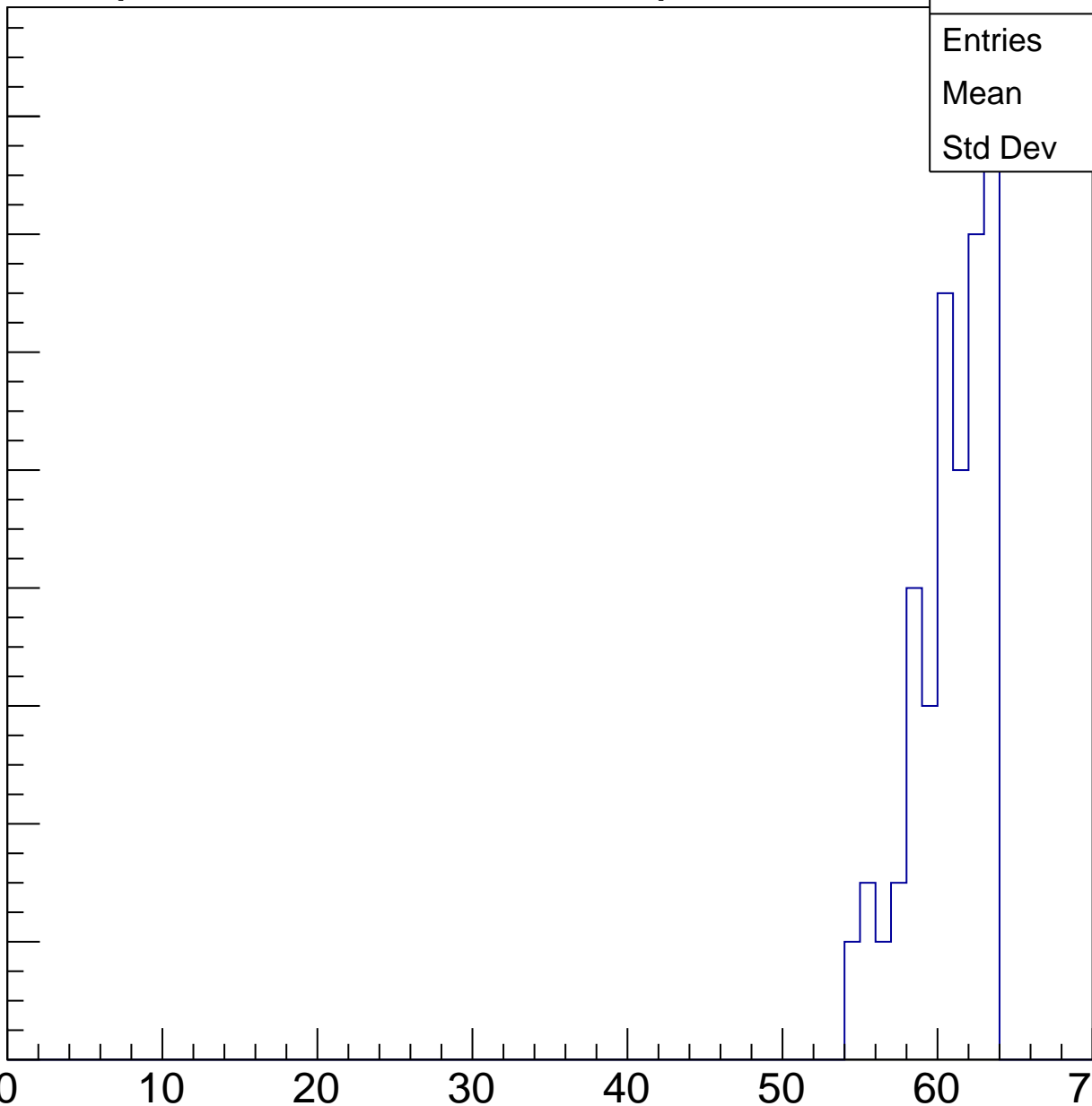
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	60.29
Std Dev	2.418

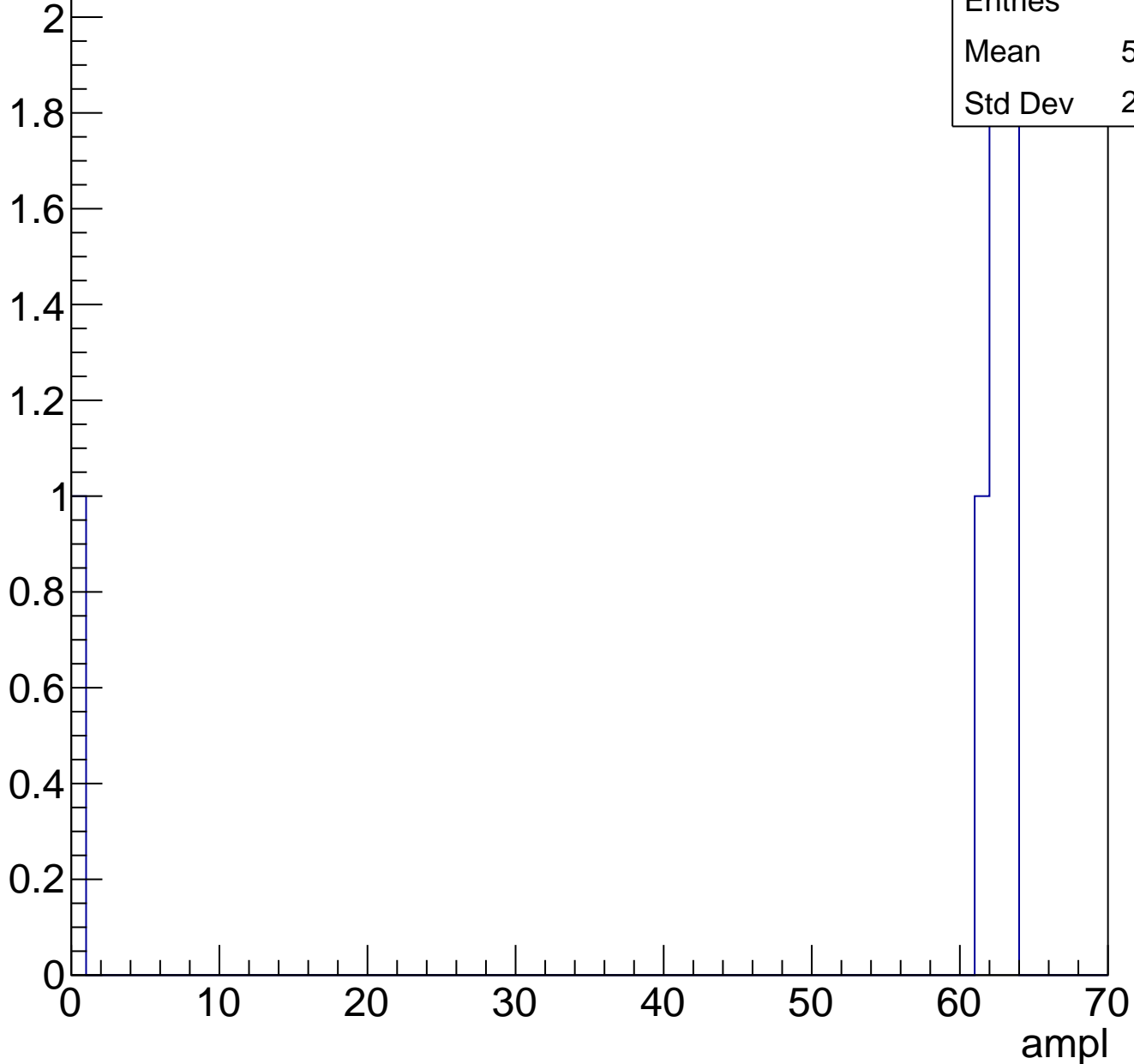
ampl



B0L101S, U21-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch72, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	30.08
Std Dev	4.004

Gaus mean : 30.2939

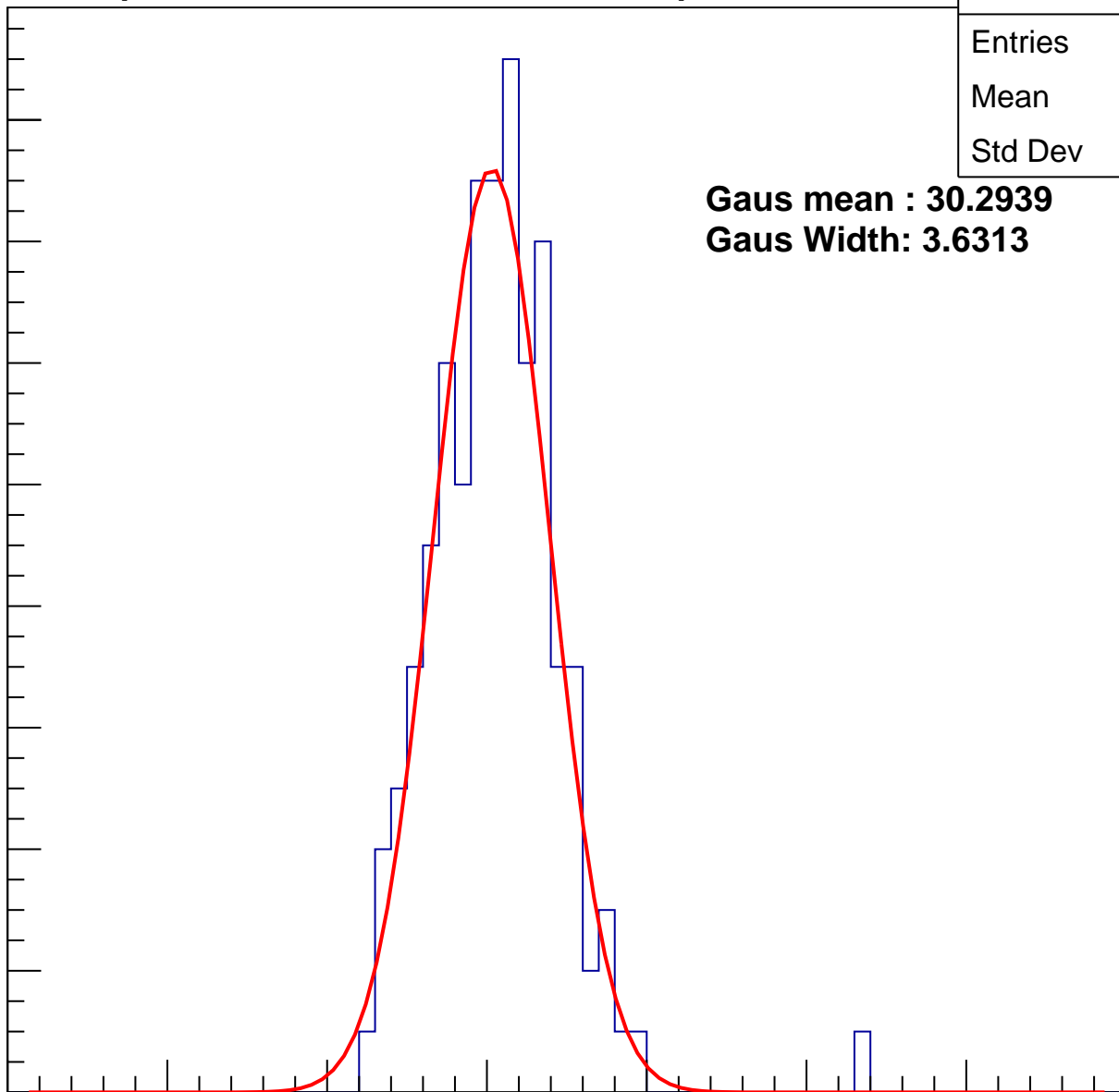
Gaus Width: 3.6313

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch72, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.64
Std Dev	3.449

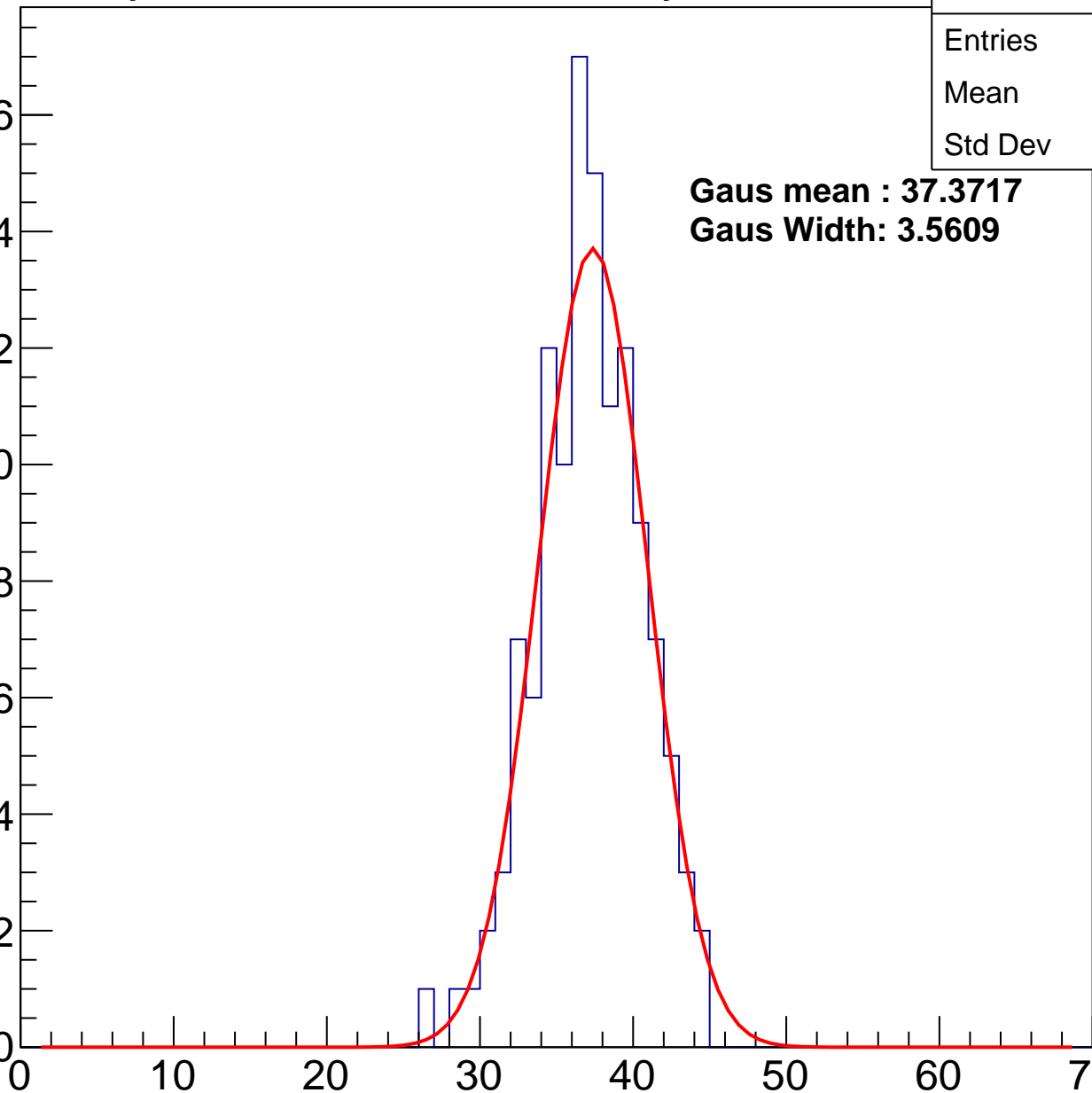
Gaus mean : 37.3717

Gaus Width: 3.5609

Entry

16
14
12
10
8
6
4
2
0

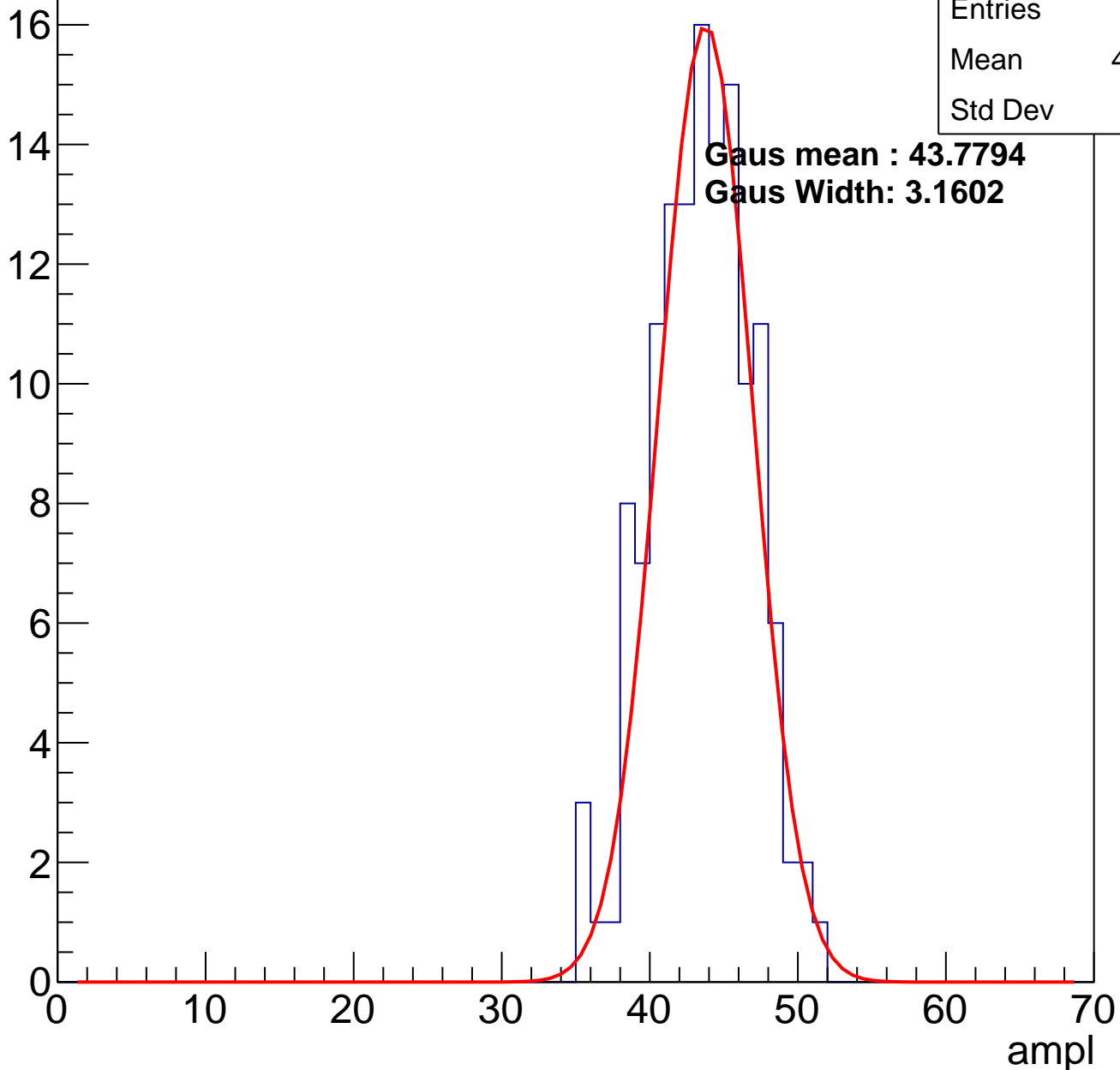
ampl



B0L101S, U21-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



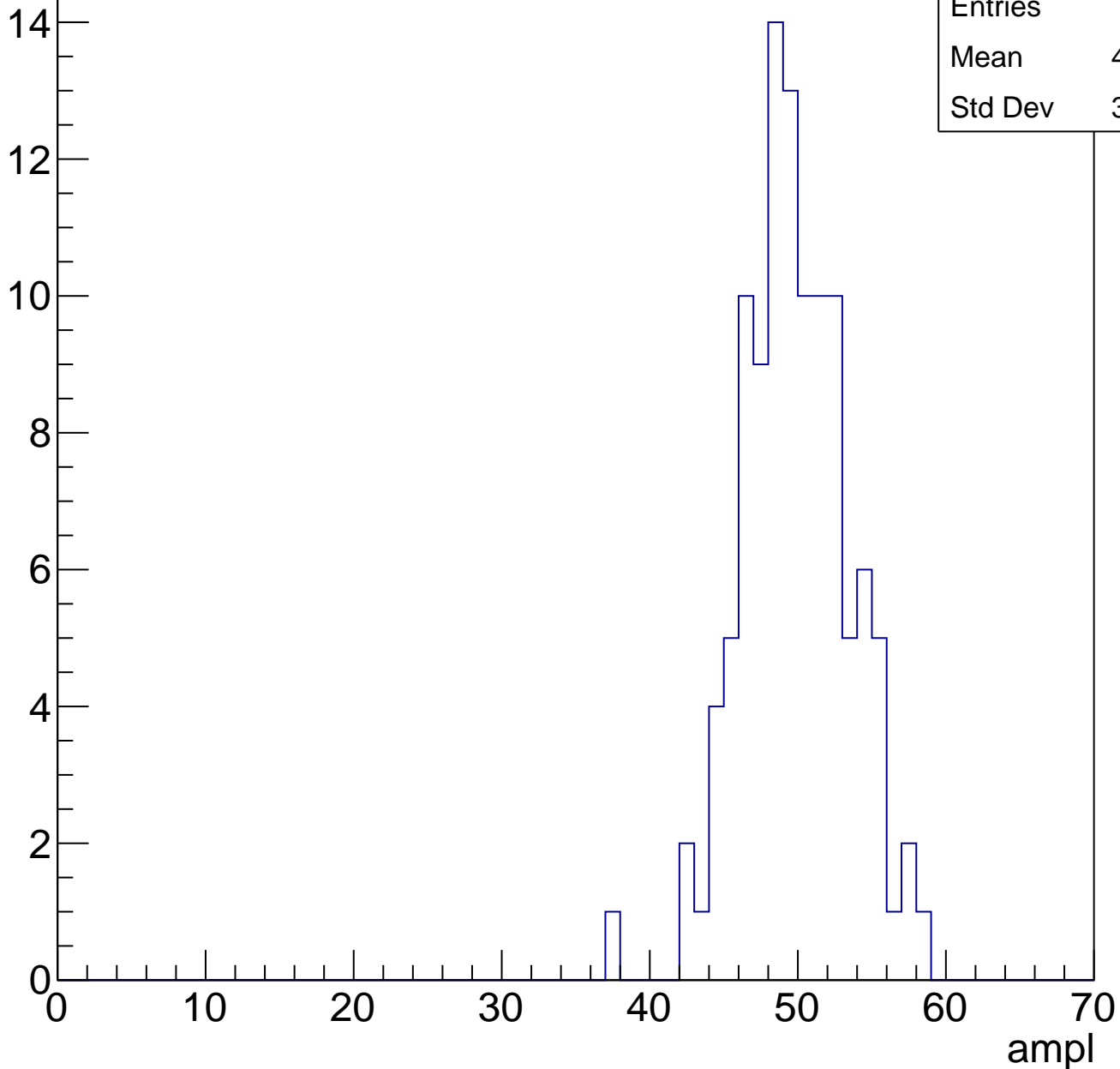
Entries	134
Mean	43.04
Std Dev	3.32

B0L101S, U21-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	49.35
Std Dev	3.575

Entry



B0L101S, U21-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

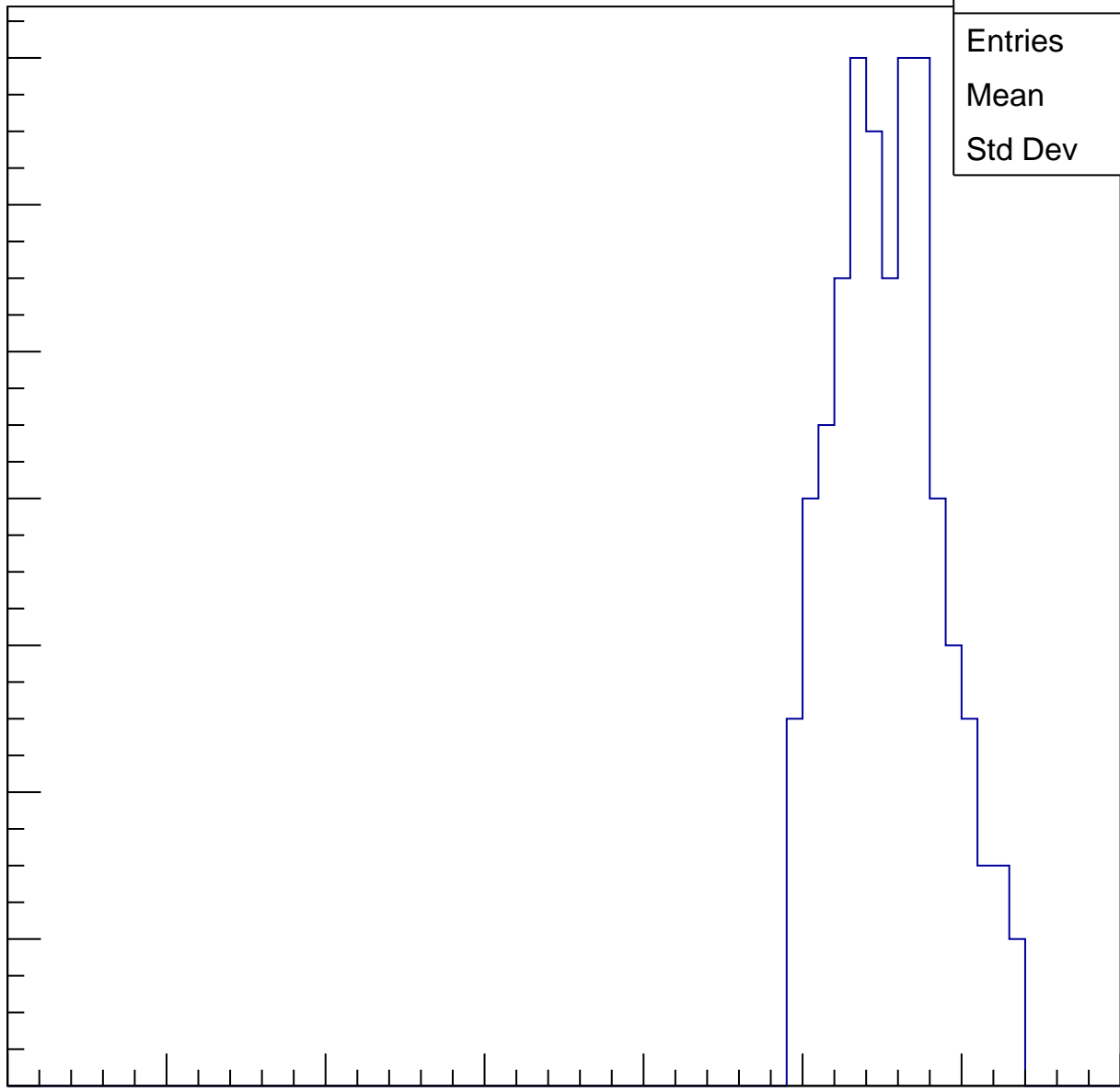
Entries	126
Mean	54.92
Std Dev	3.377

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch72, adc5

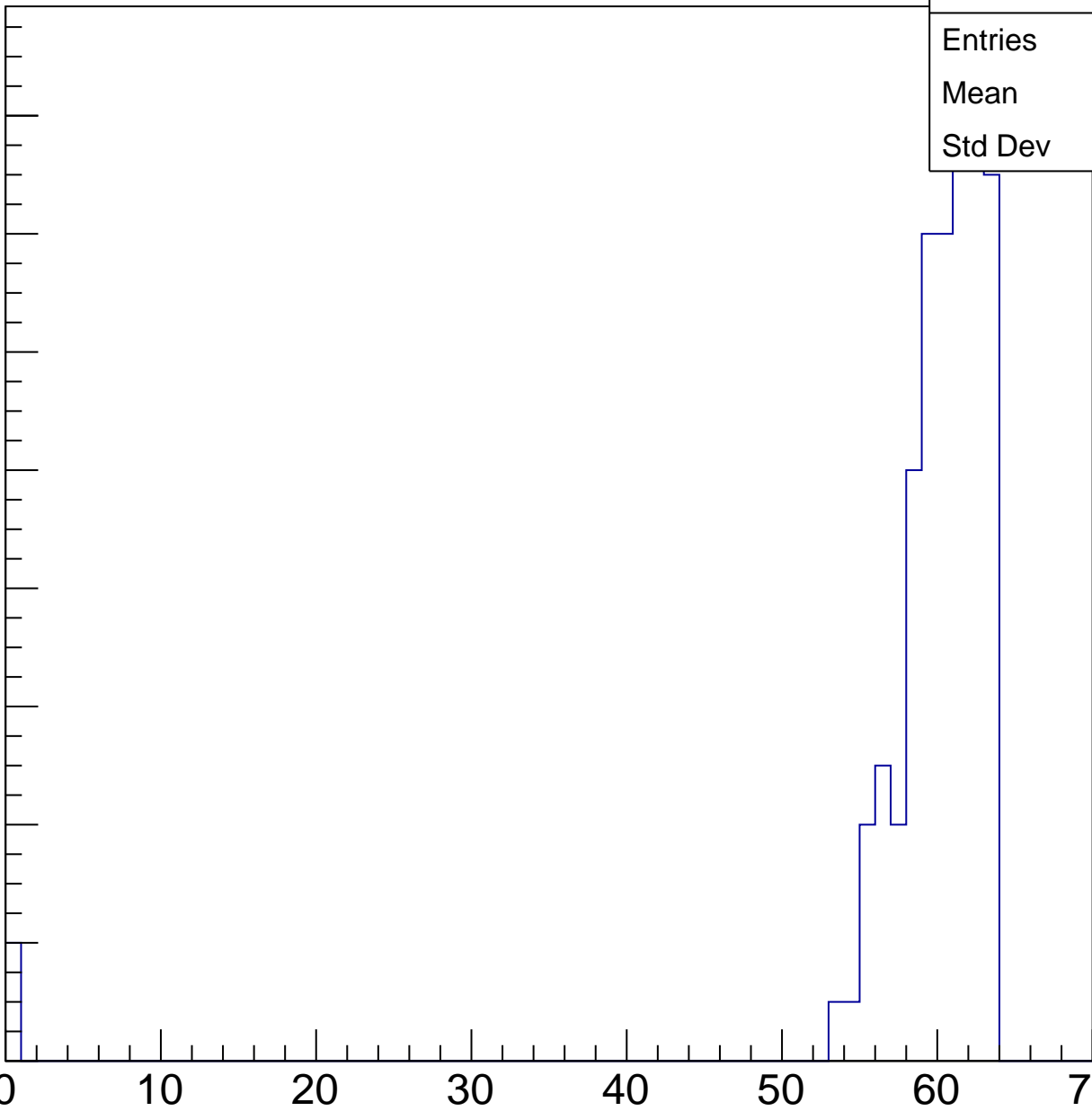
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.79
Std Dev	8.601

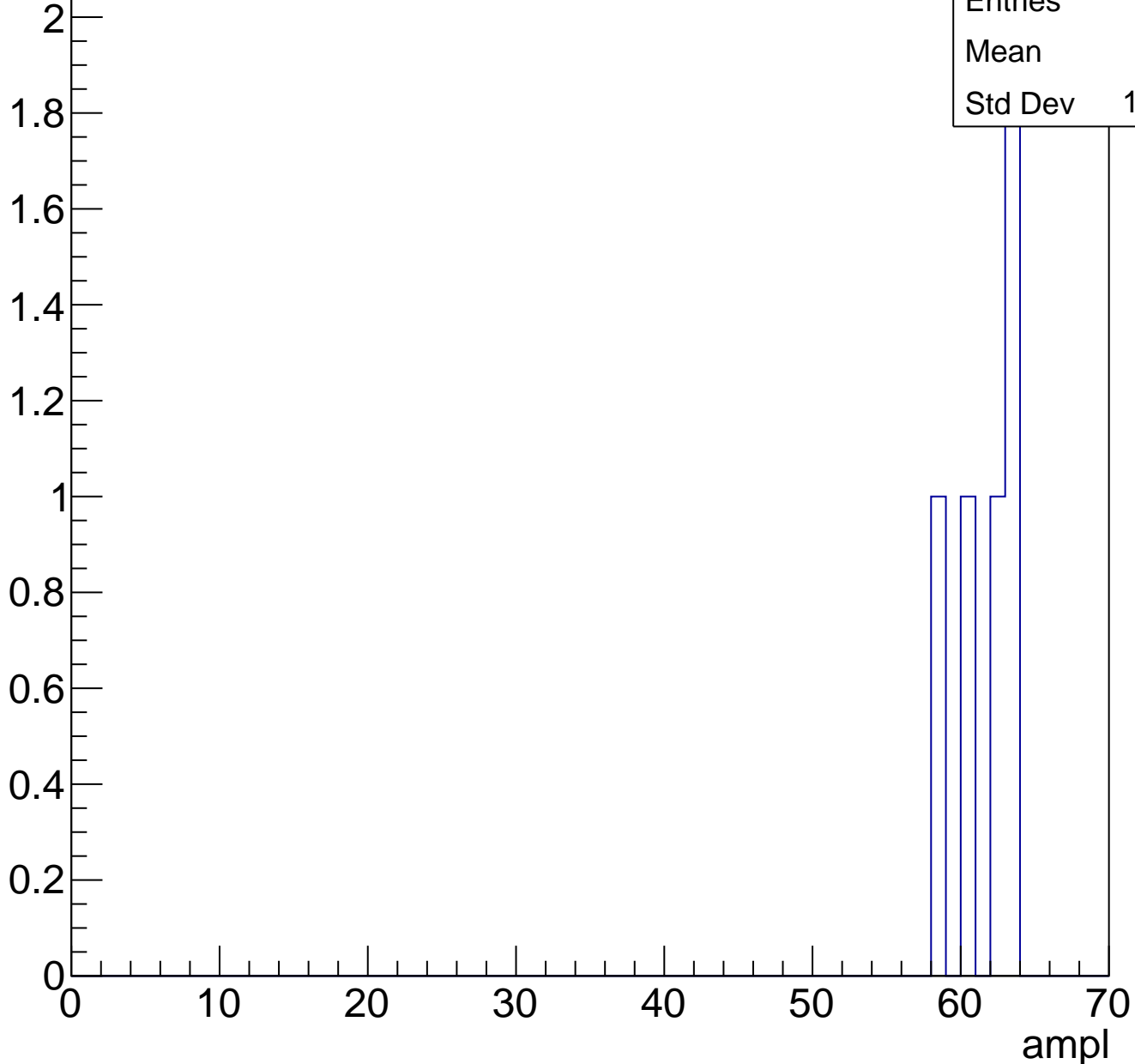
ampl



B0L101S, U21-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch73, adc0

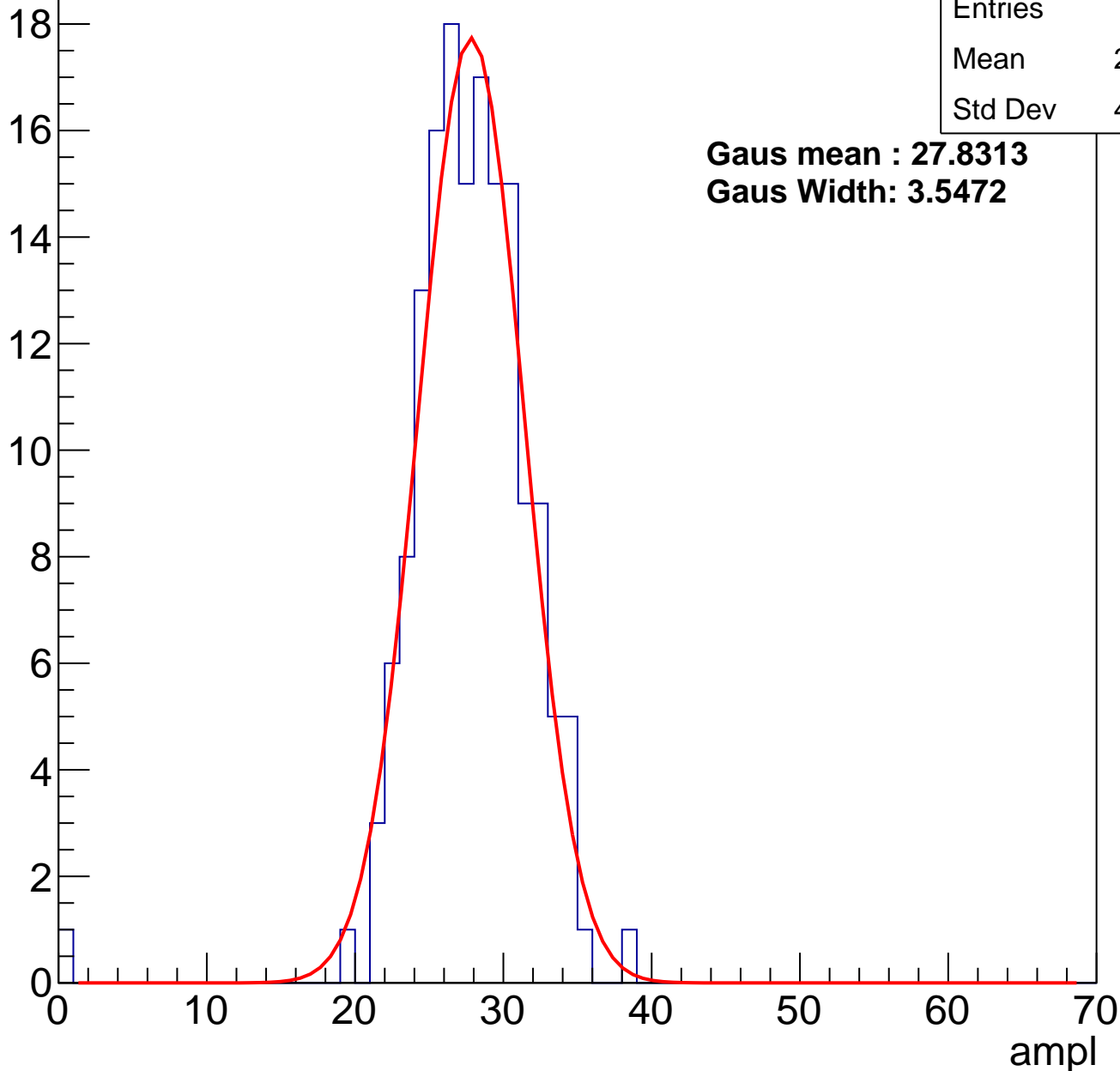
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	27.34
Std Dev	4.018

Gaus mean : 27.8313

Gaus Width: 3.5472

Entry

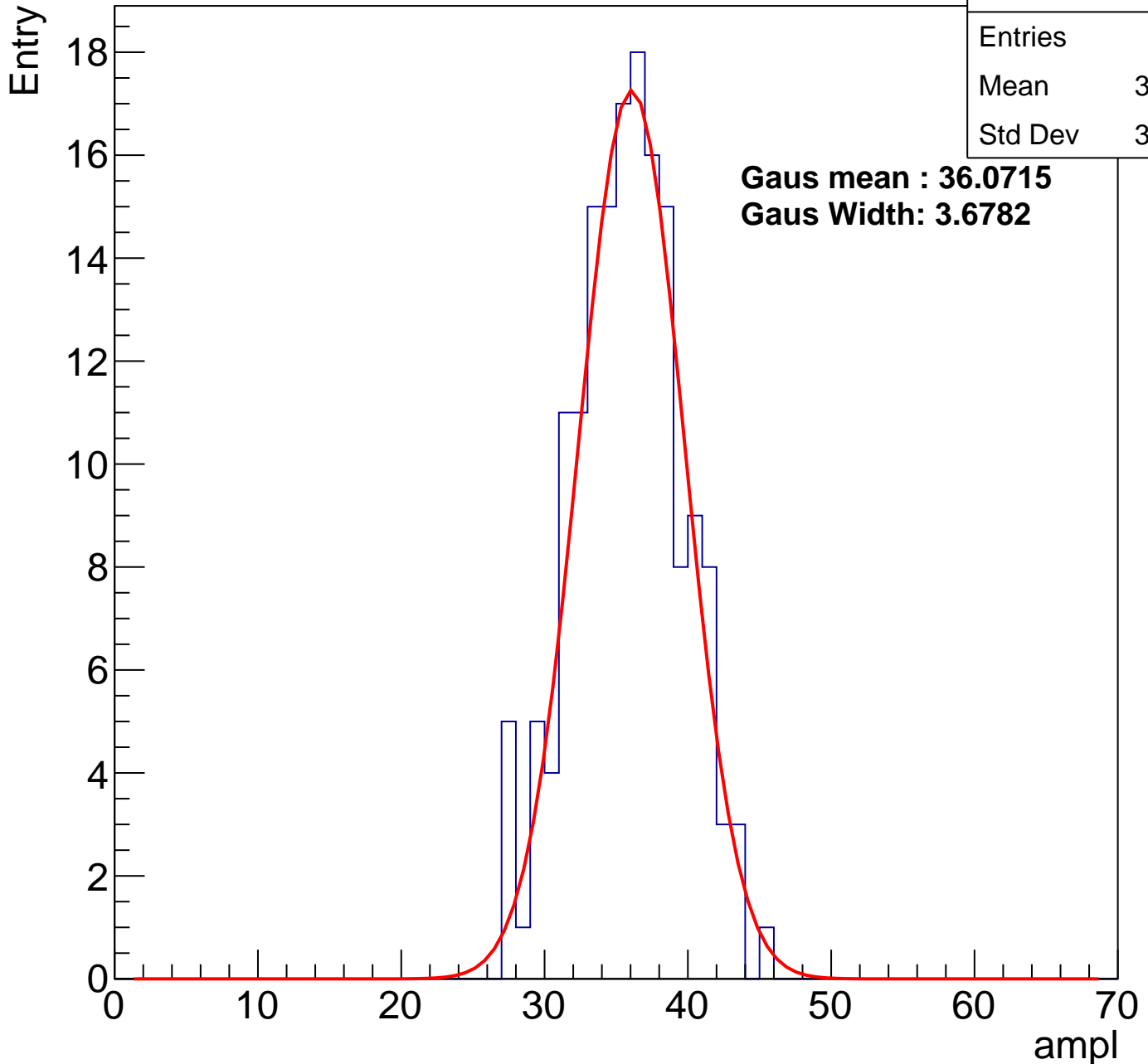


B0L101S, U21-ch73, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	35.34
Std Dev	3.715

Gaus mean : 36.0715
Gaus Width: 3.6782



B0L101S, U21-ch73, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

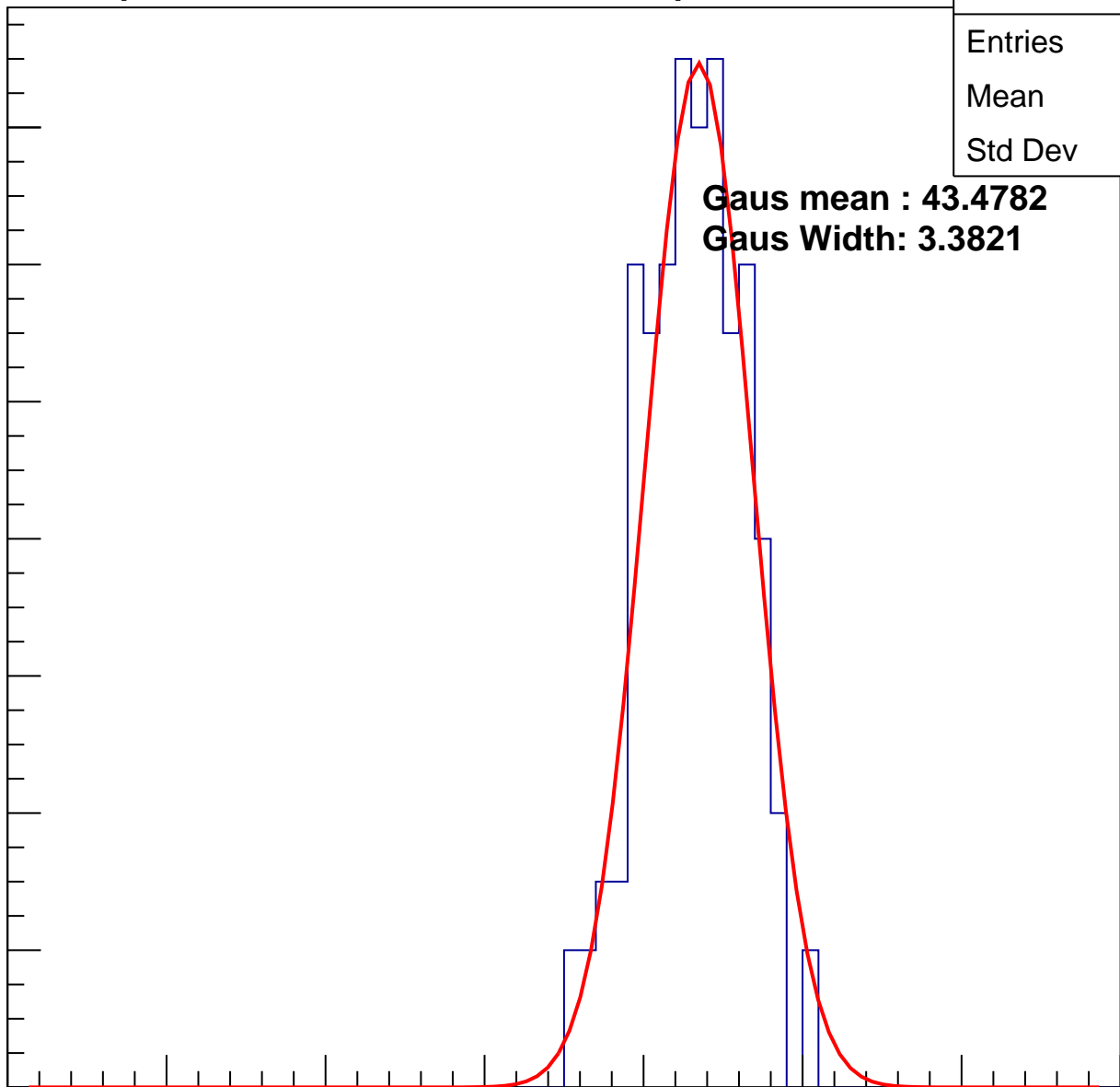
70

ampl

Entries	126
Mean	42.65
Std Dev	3.138

Gaus mean : 43.4782

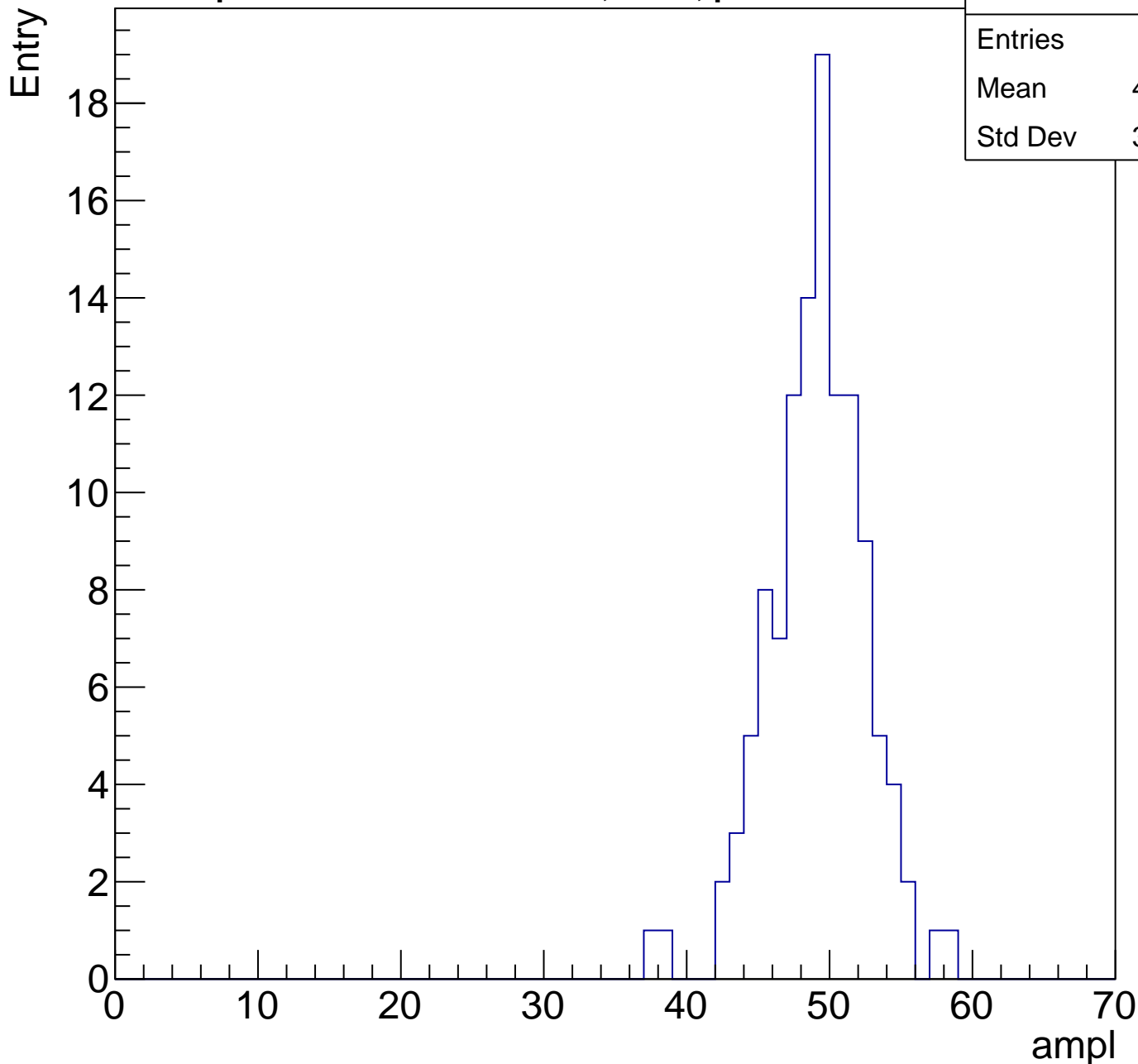
Gaus Width: 3.3821



B0L101S, U21-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

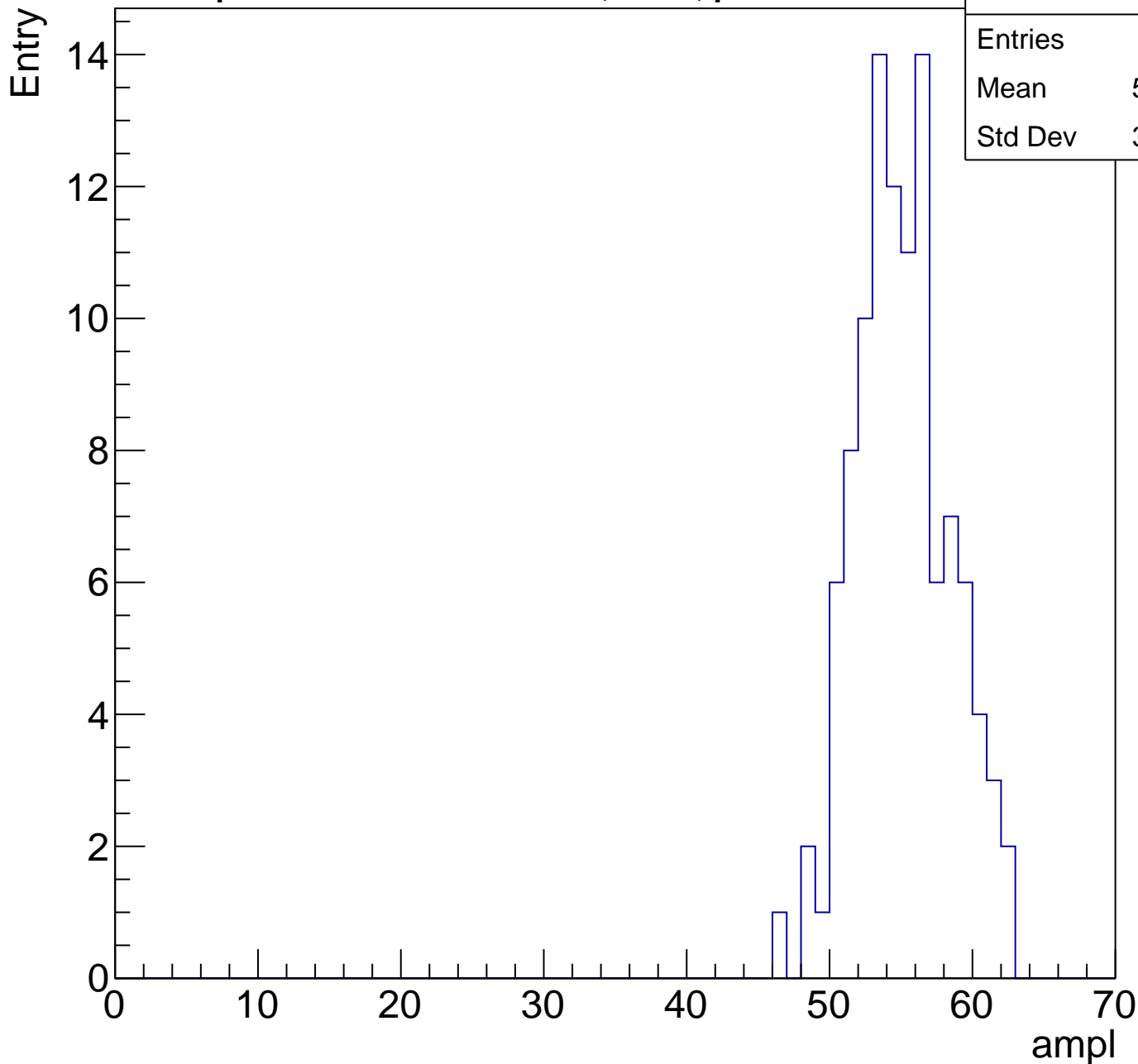
Entries	118
Mean	48.67
Std Dev	3.412



B0L101S, U21-ch73, adc4

calib_packv5_042523_0143.root, FC#1, port C1

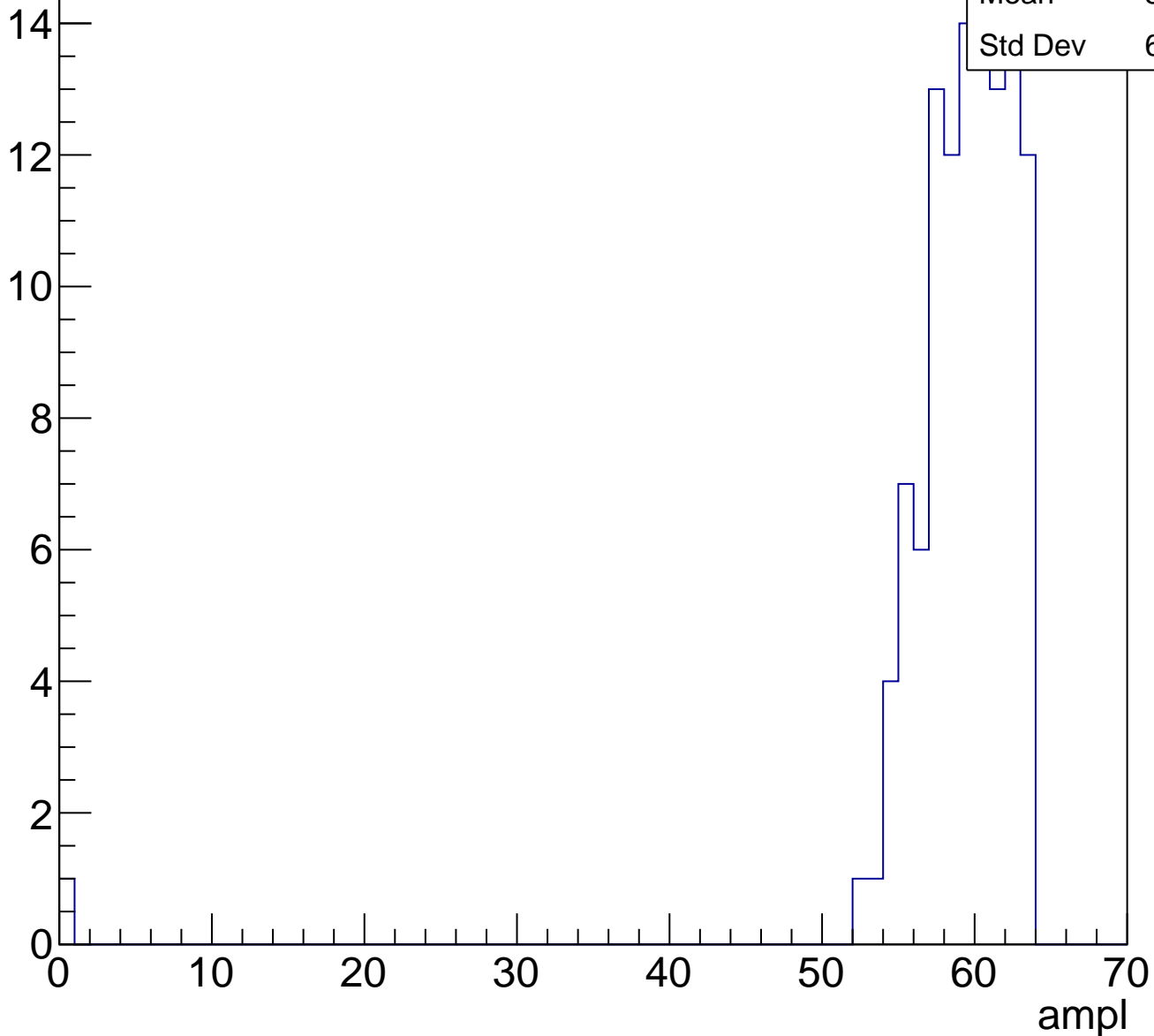
Entries	107
Mean	54.64
Std Dev	3.265



B0L101S, U21-ch73, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

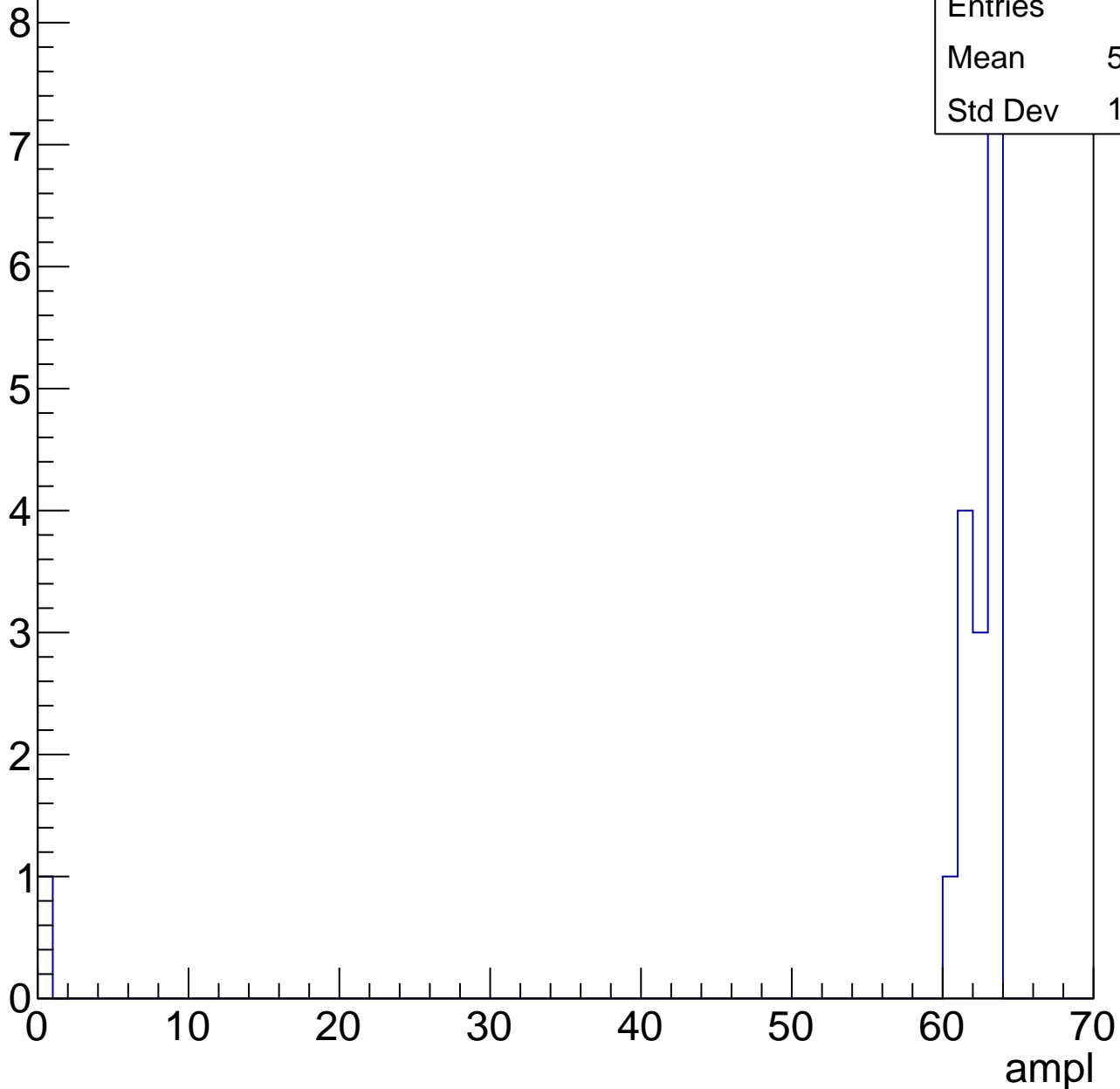


B0L101S, U21-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	58.47
Std Dev	14.65



B0L101S, U21-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch74, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	29.13
Std Dev	4.271

Gaus mean : 29.8803

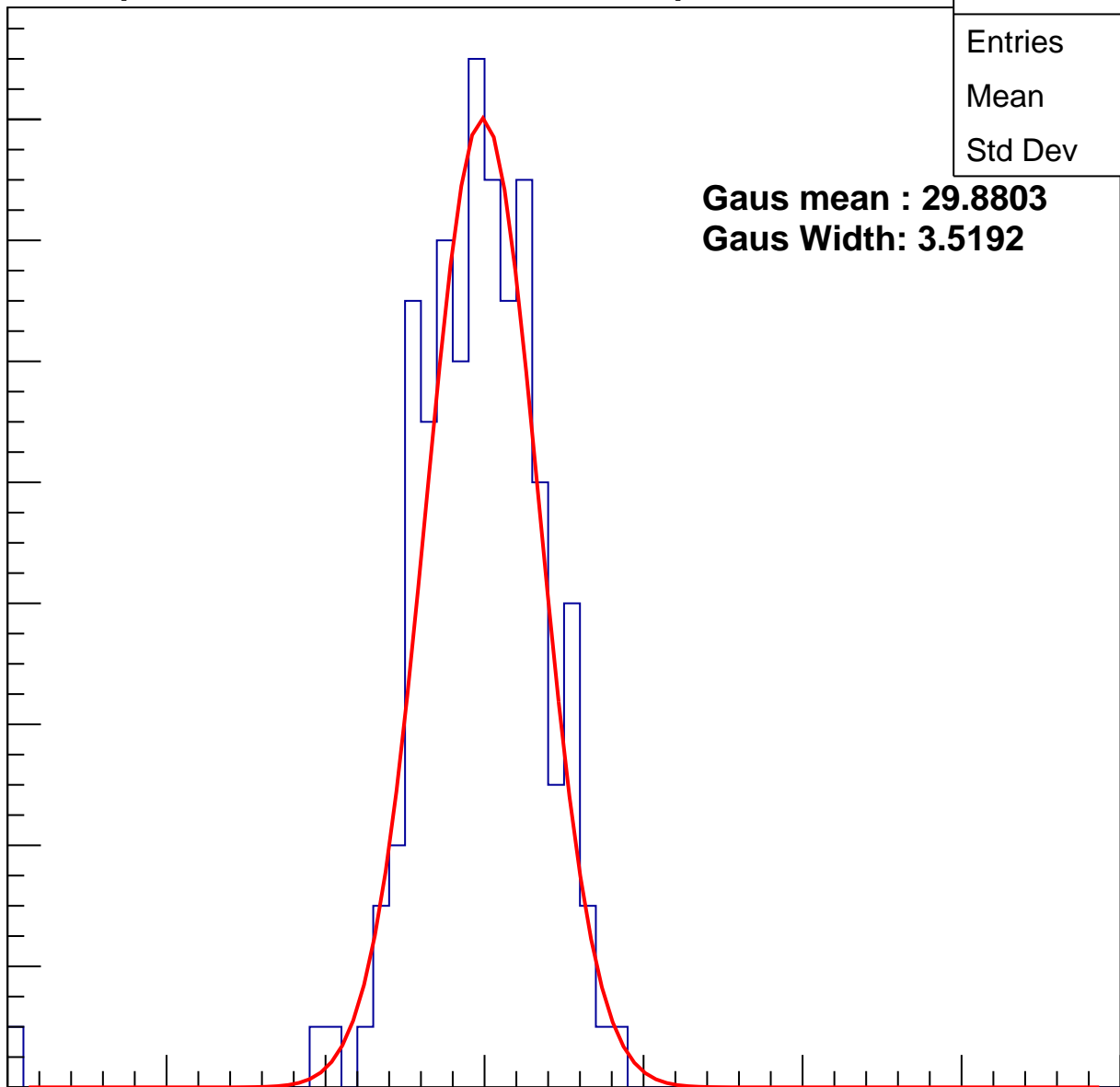
Gaus Width: 3.5192

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	36.48
Std Dev	3.749

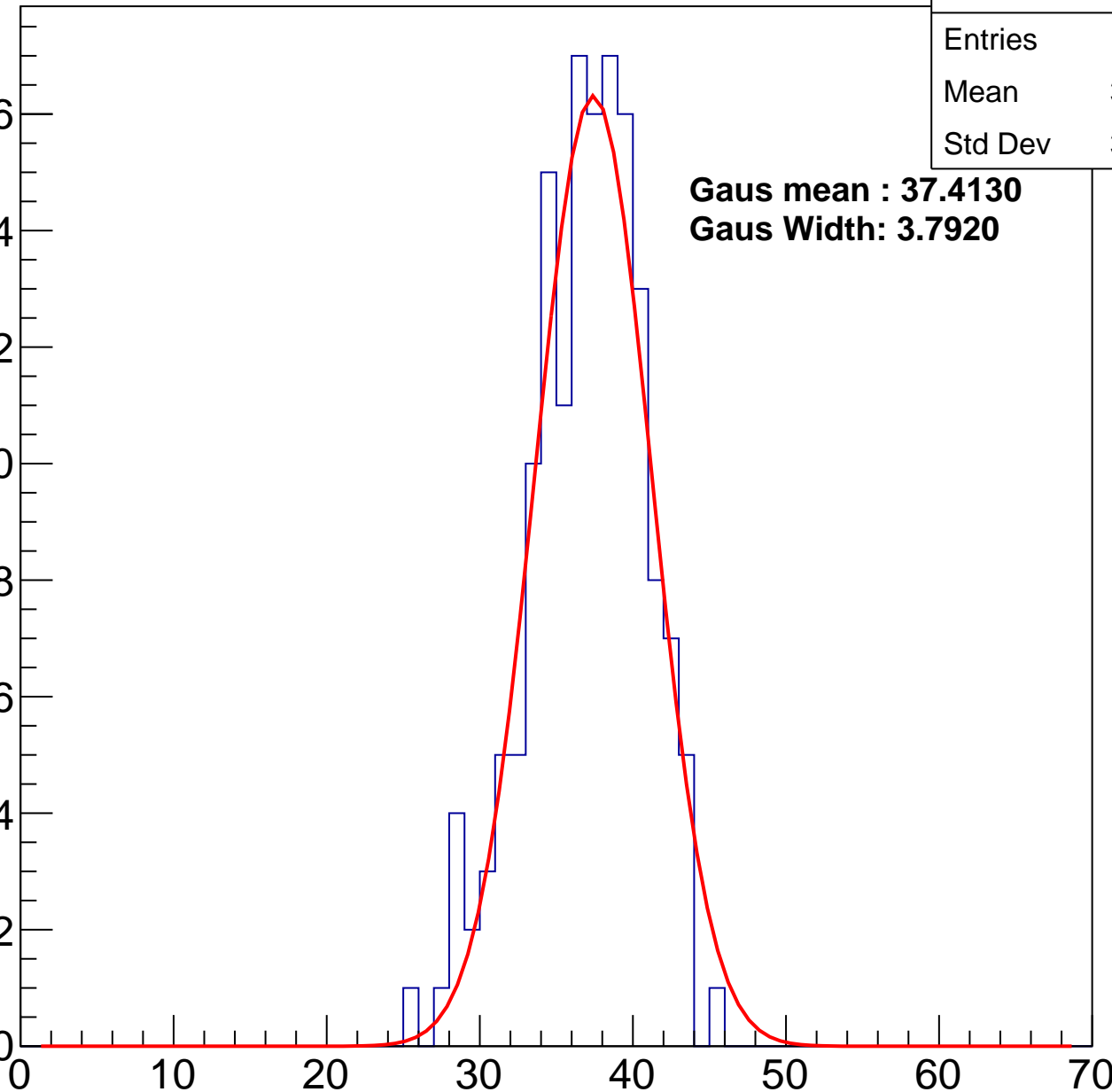
Gaus mean : 37.4130

Gaus Width: 3.7920

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch74, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

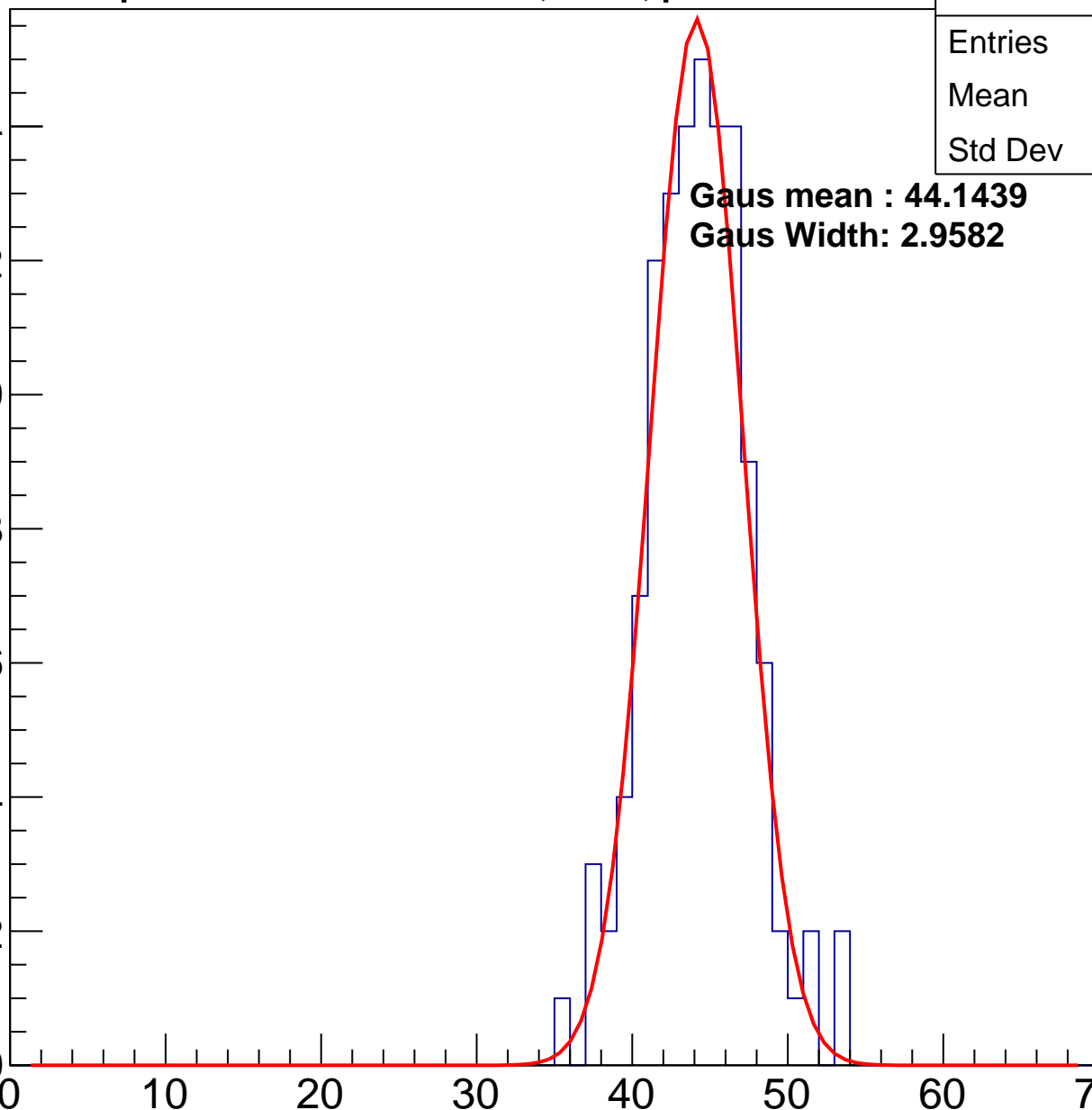
70

ampl

Entries	121
Mean	43.79
Std Dev	3.245

Gaus mean : 44.1439

Gaus Width: 2.9582



B0L101S, U21-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

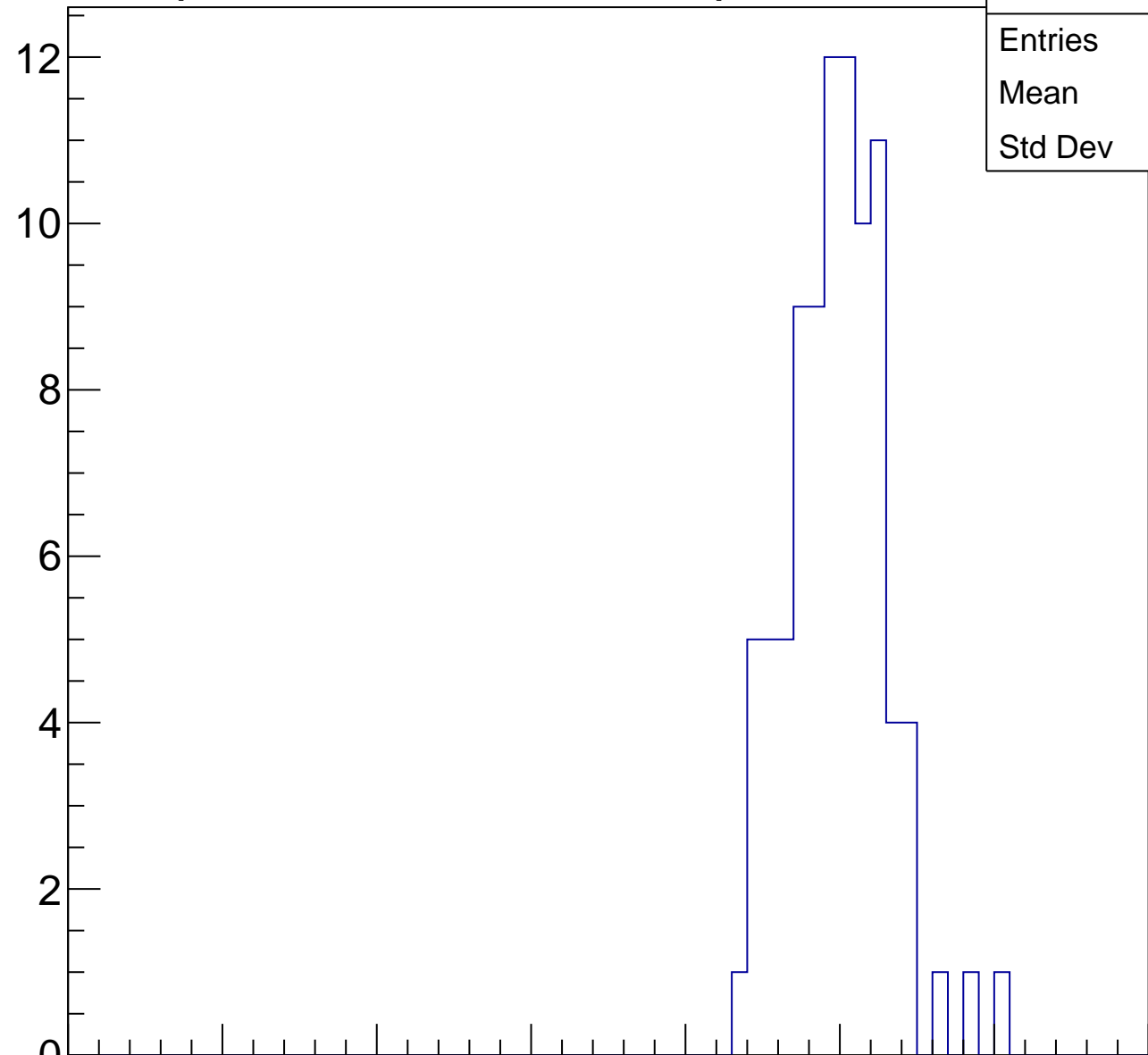
Entries	90
Mean	49.39
Std Dev	3.133

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

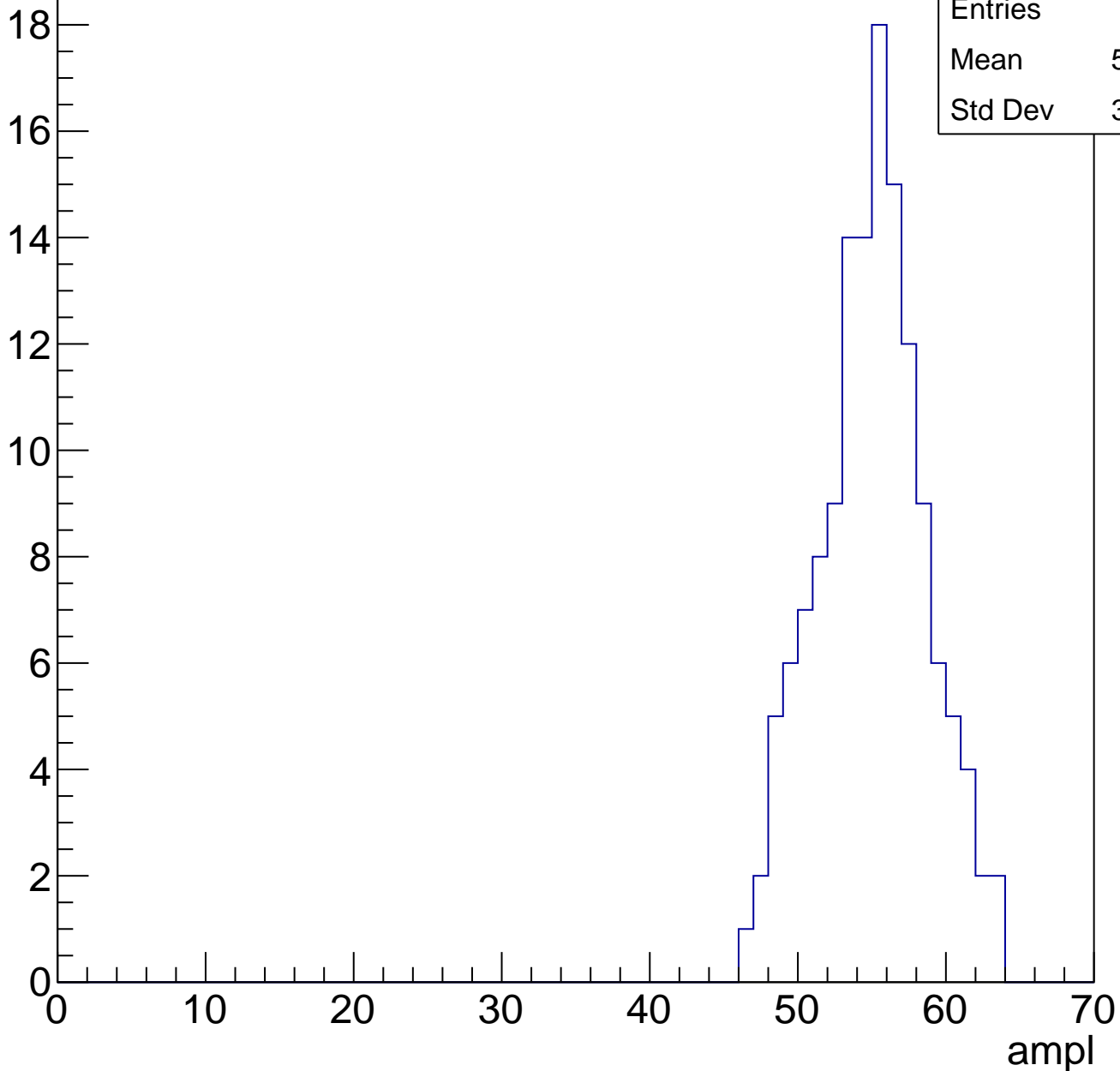
ampl



B0L101S, U21-ch74, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch74, adc5

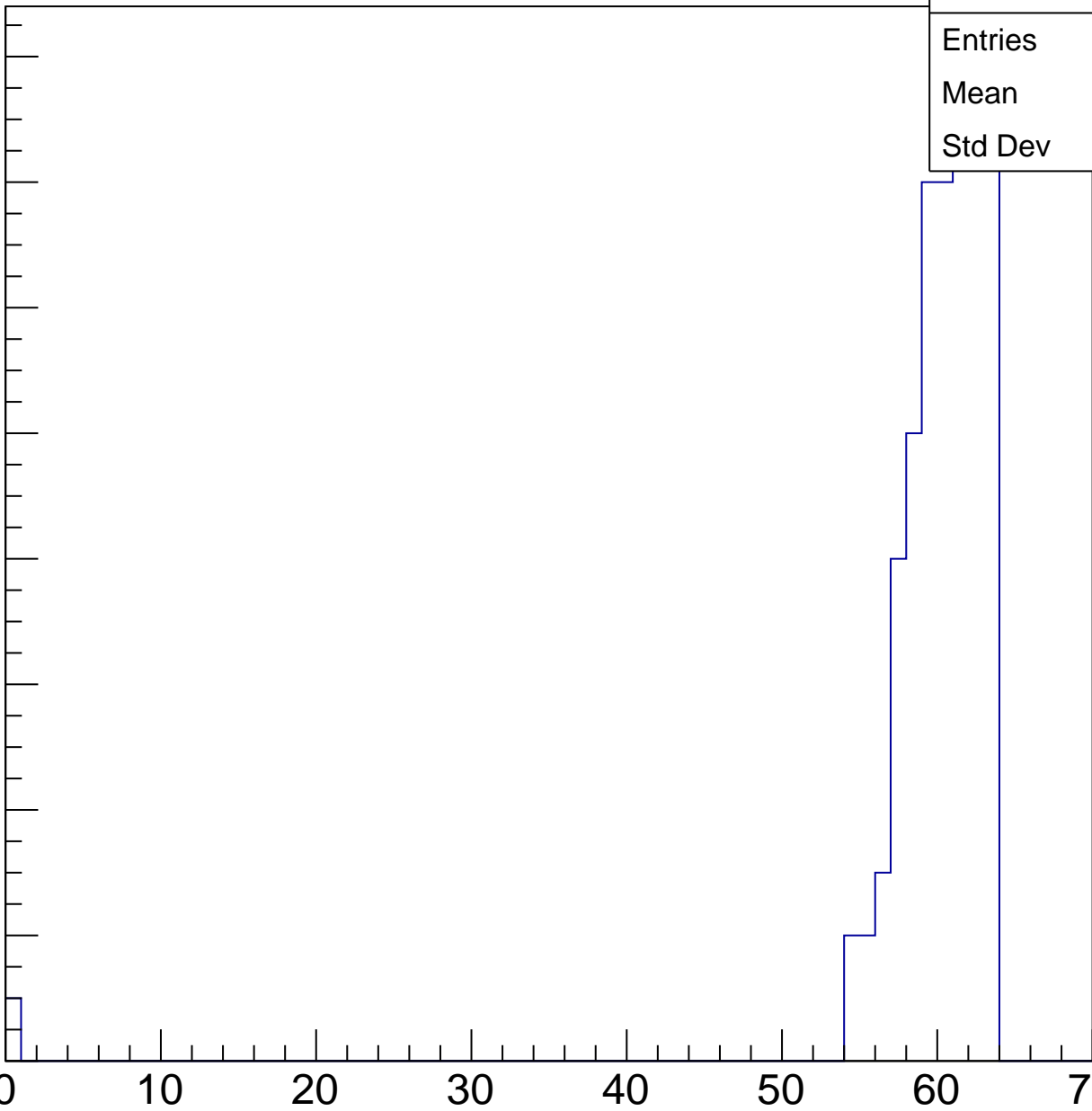
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.4
Std Dev	6.383

ampl



B0L101S, U21-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

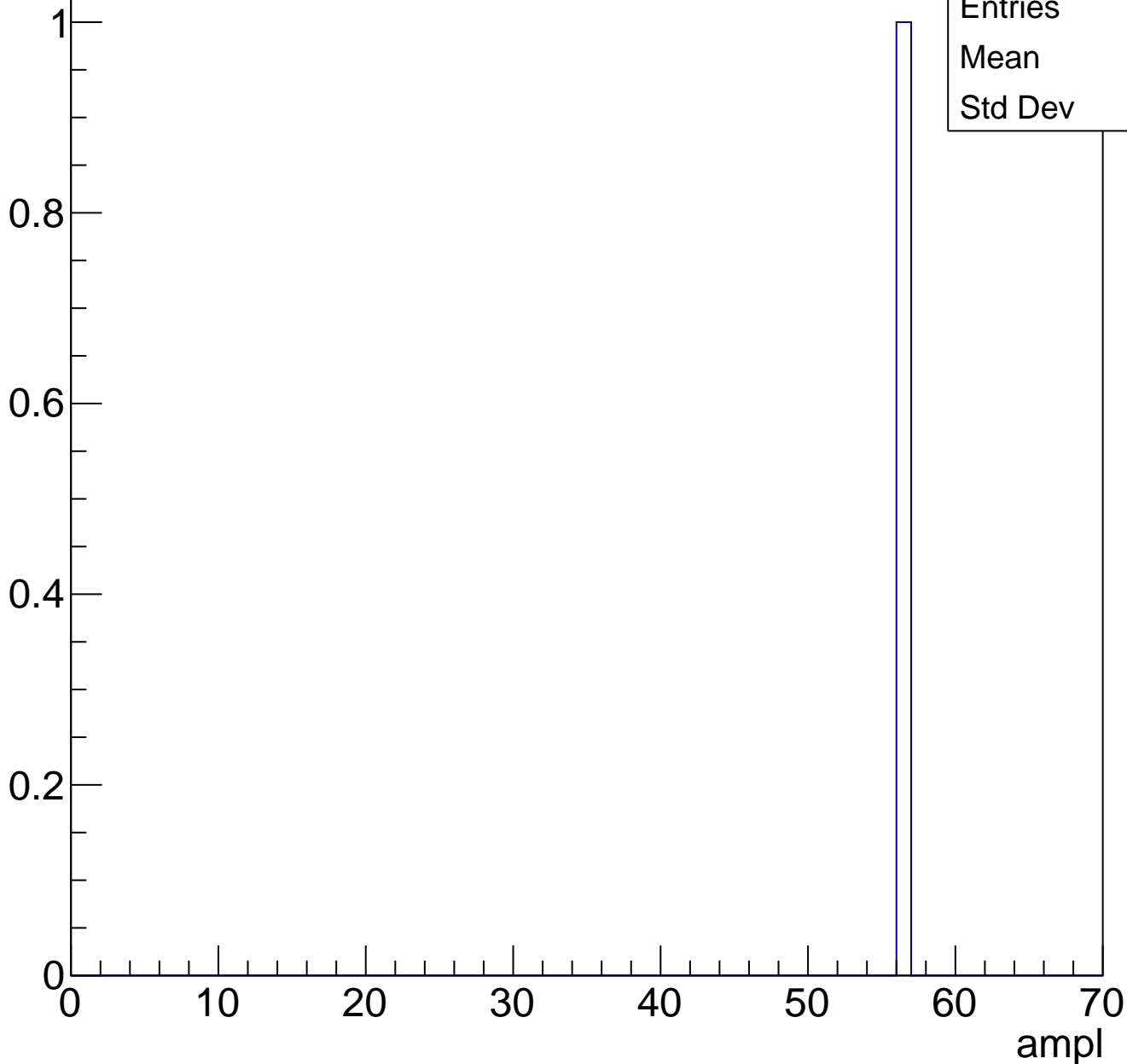
Entry



B0L101S, U21-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch75, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	30.17
Std Dev	2.836

Gaus mean : 30.6242

Gaus Width: 3.1094

Entry

14

12

10

8

6

4

2

0

0

10

20

30

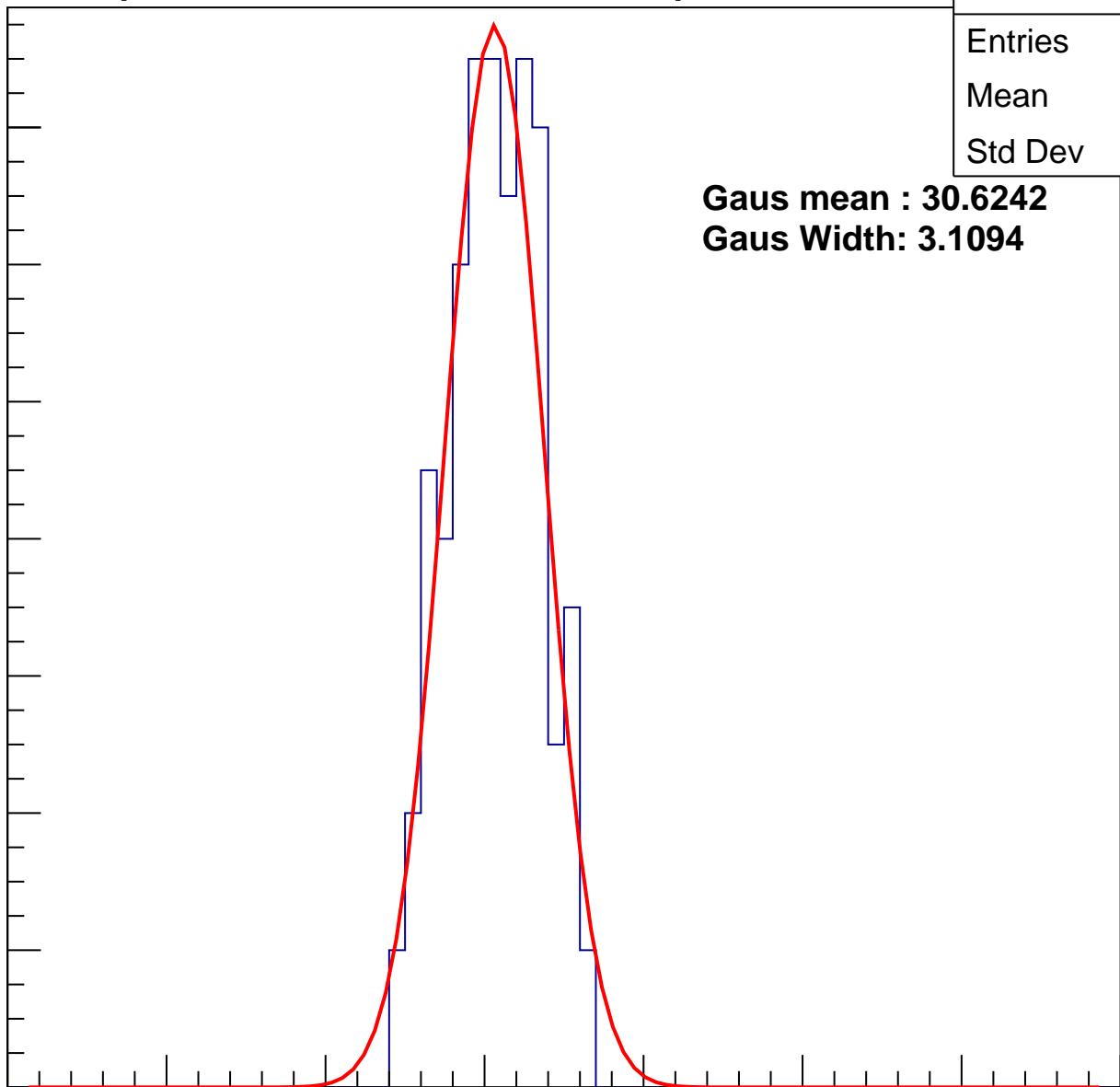
40

50

60

70

ampl



B0L101S, U21-ch75, adc1

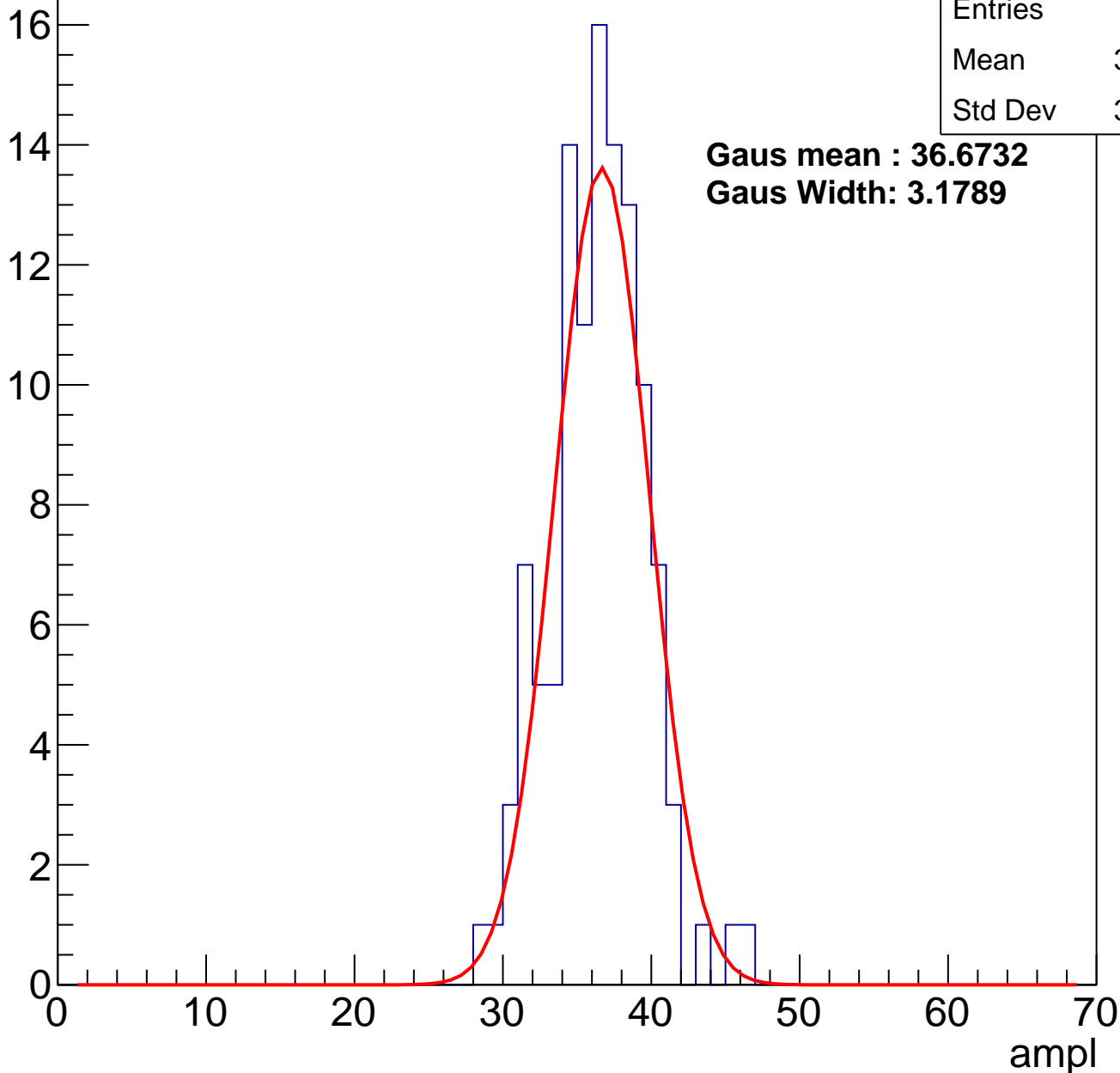
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	35.97
Std Dev	3.205

Gaus mean : 36.6732

Gaus Width: 3.1789

Entry



B0L101S, U21-ch75, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	42.14
Std Dev	3.302

Gaus mean : 42.5014

Gaus Width: 3.3792

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

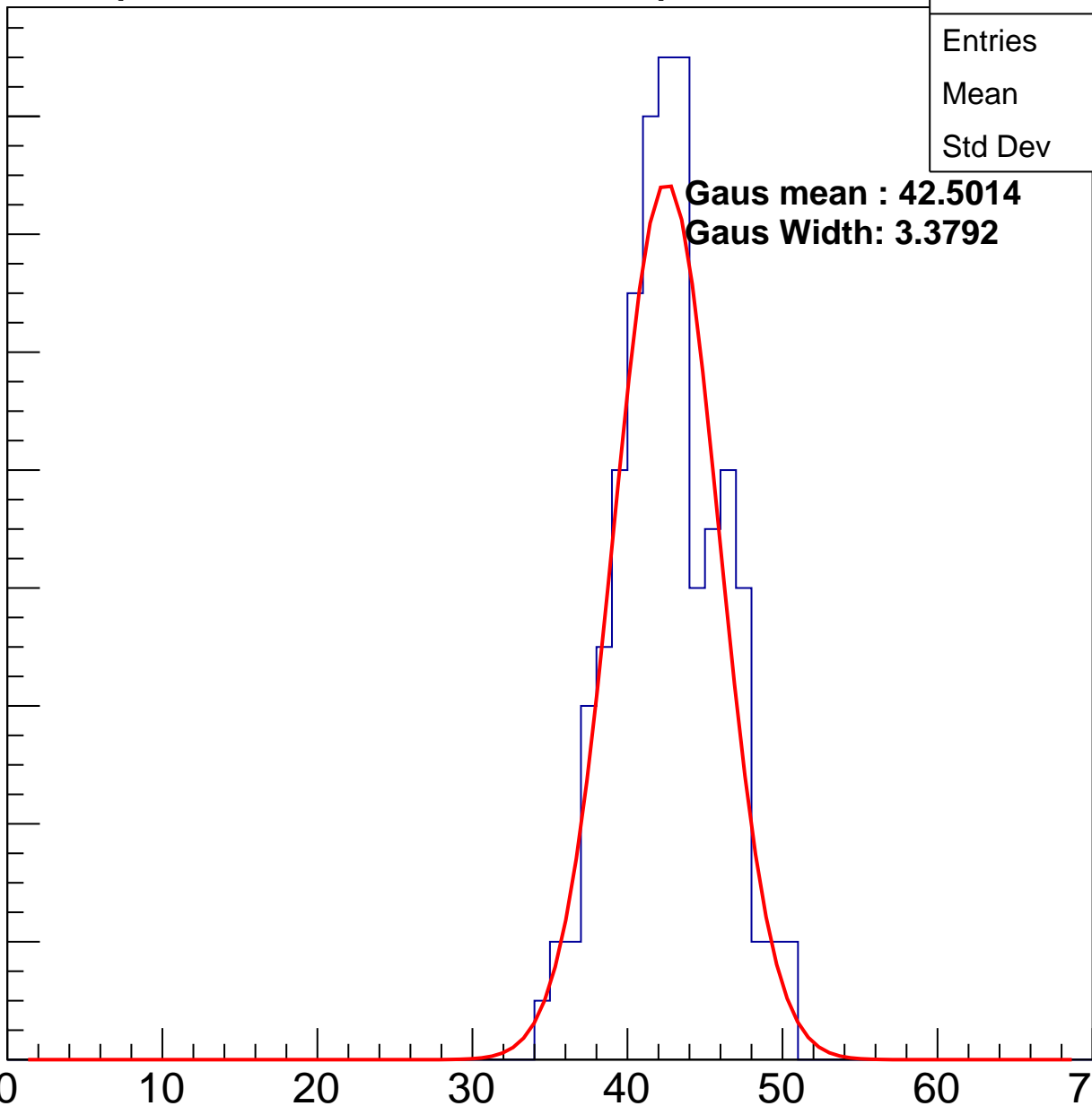
40

50

60

70

ampl

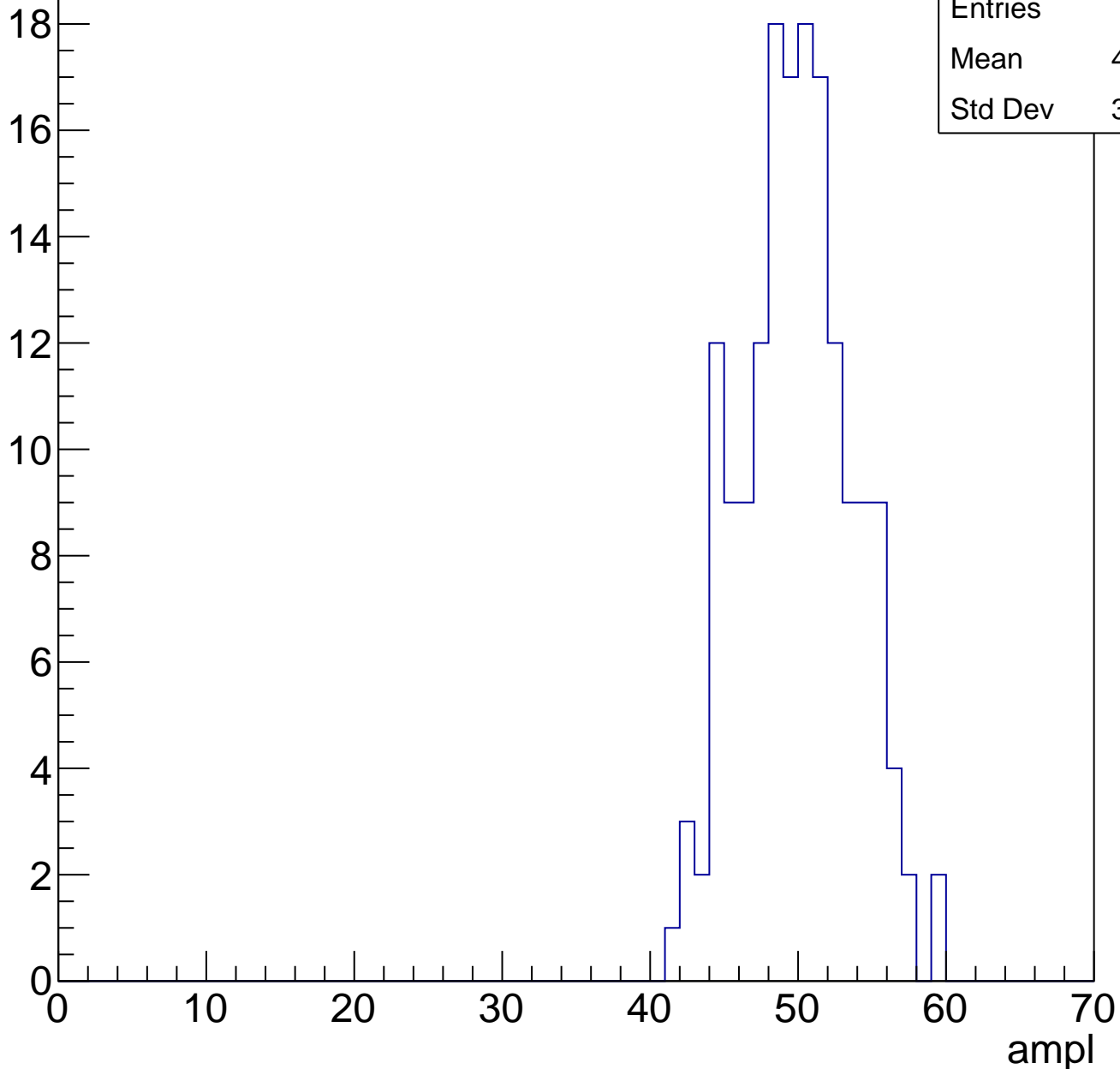


B0L101S, U21-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	49.49
Std Dev	3.689

Entry

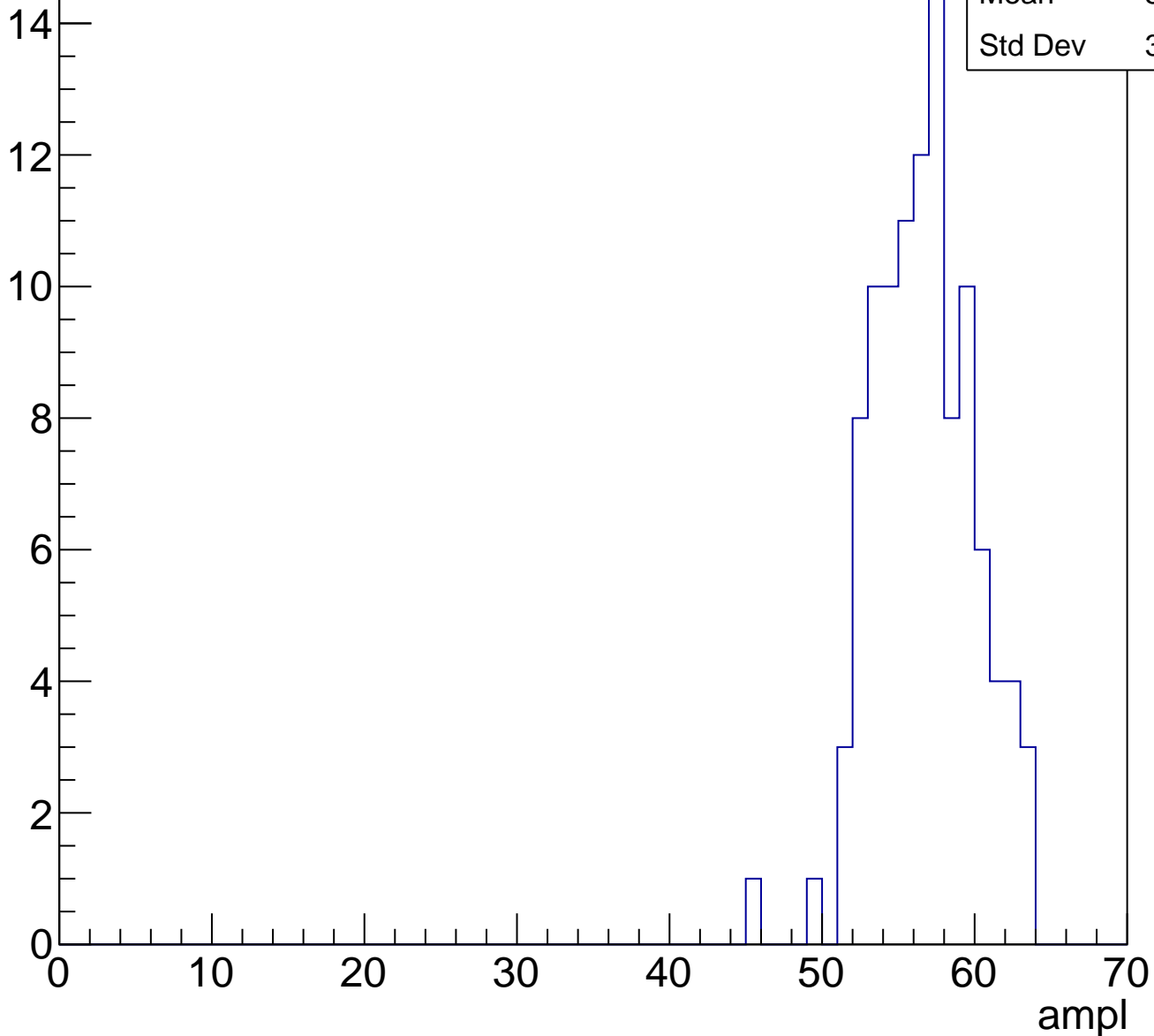


B0L101S, U21-ch75, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	56.23
Std Dev	3.269

Entry



B0L101S, U21-ch75, adc5

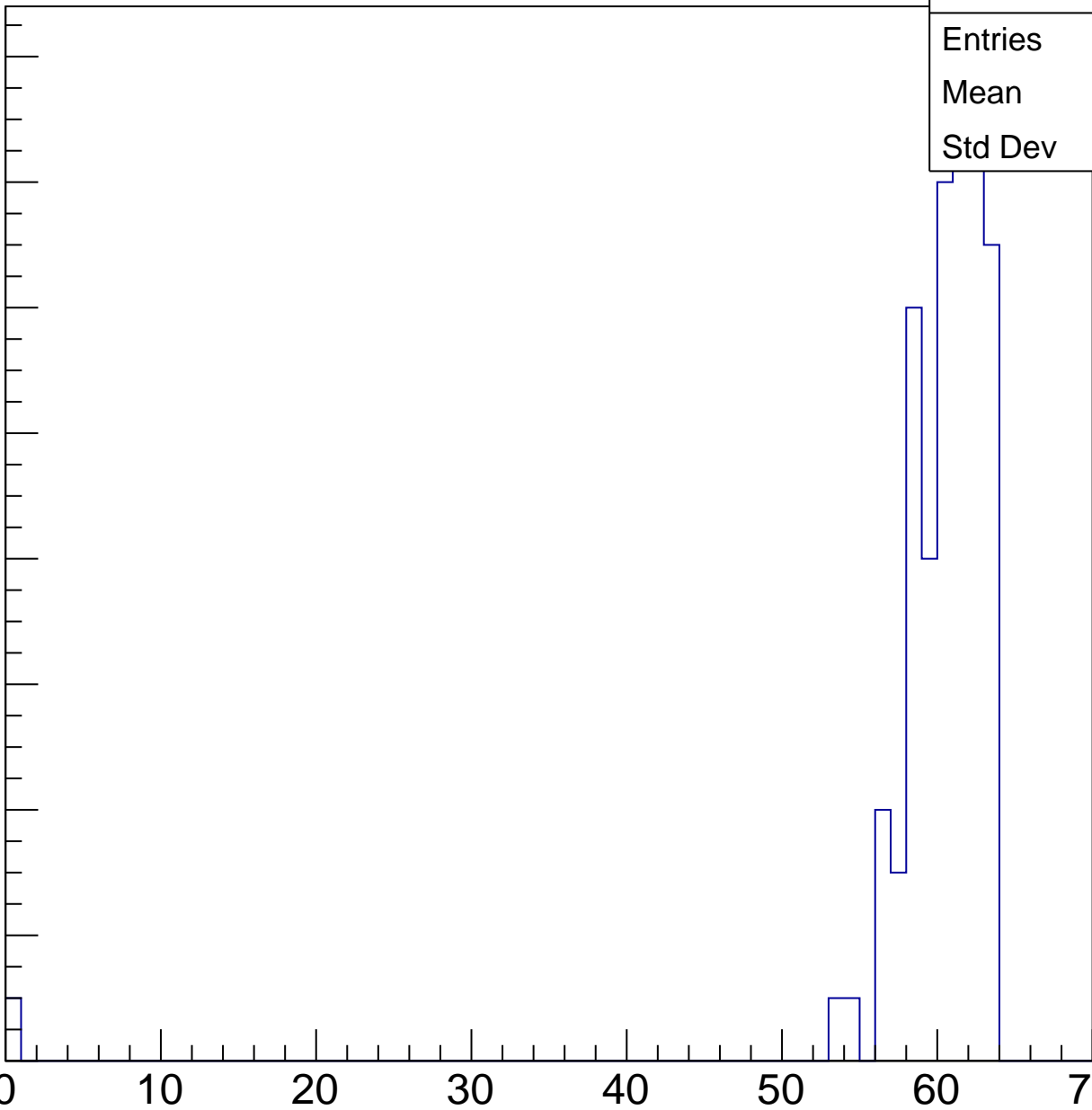
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.49
Std Dev	6.744

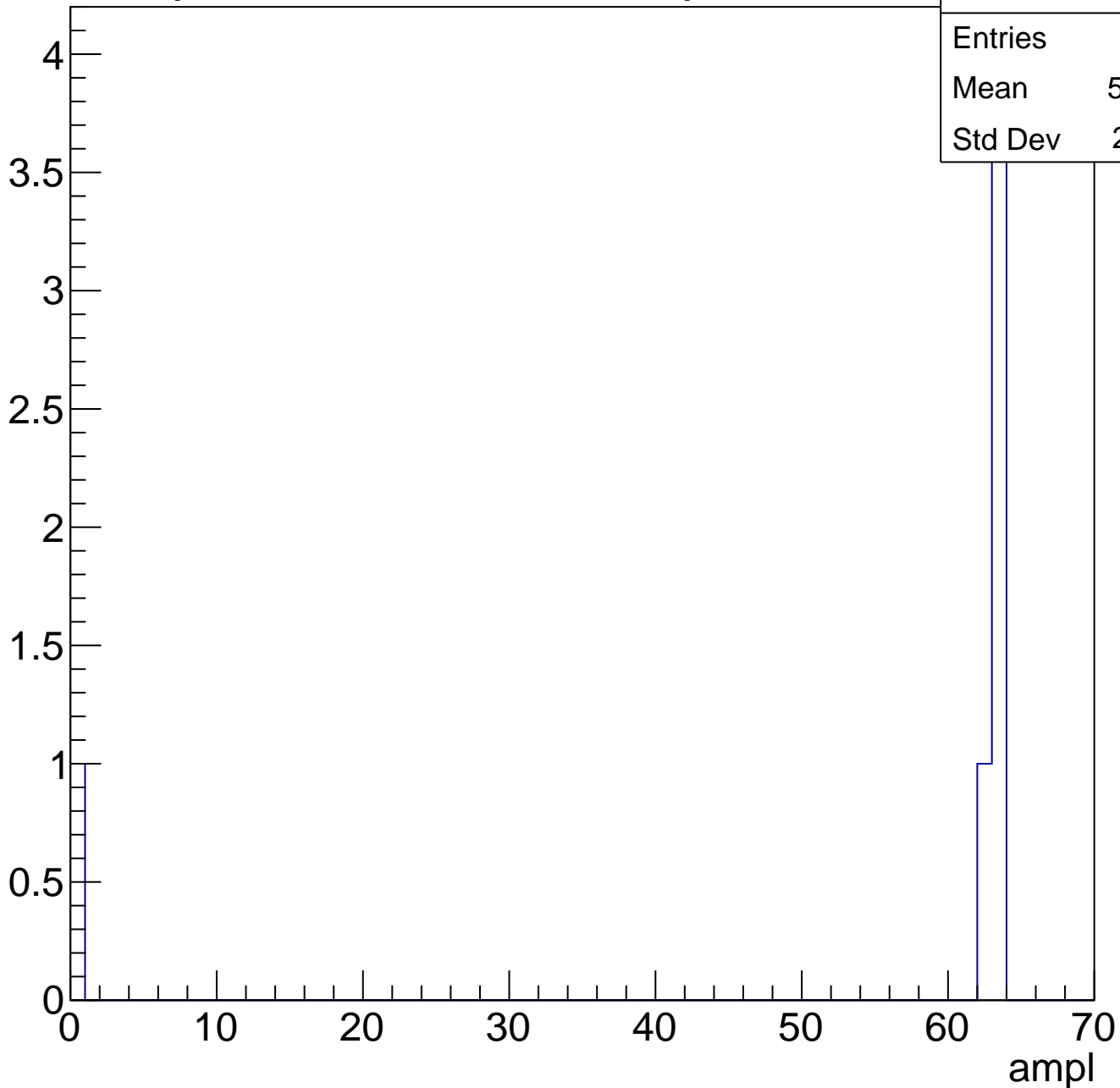
ampl



B0L101S, U21-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	6
Mean	52.33
Std Dev	23.41

B0L101S, U21-ch75, adc7

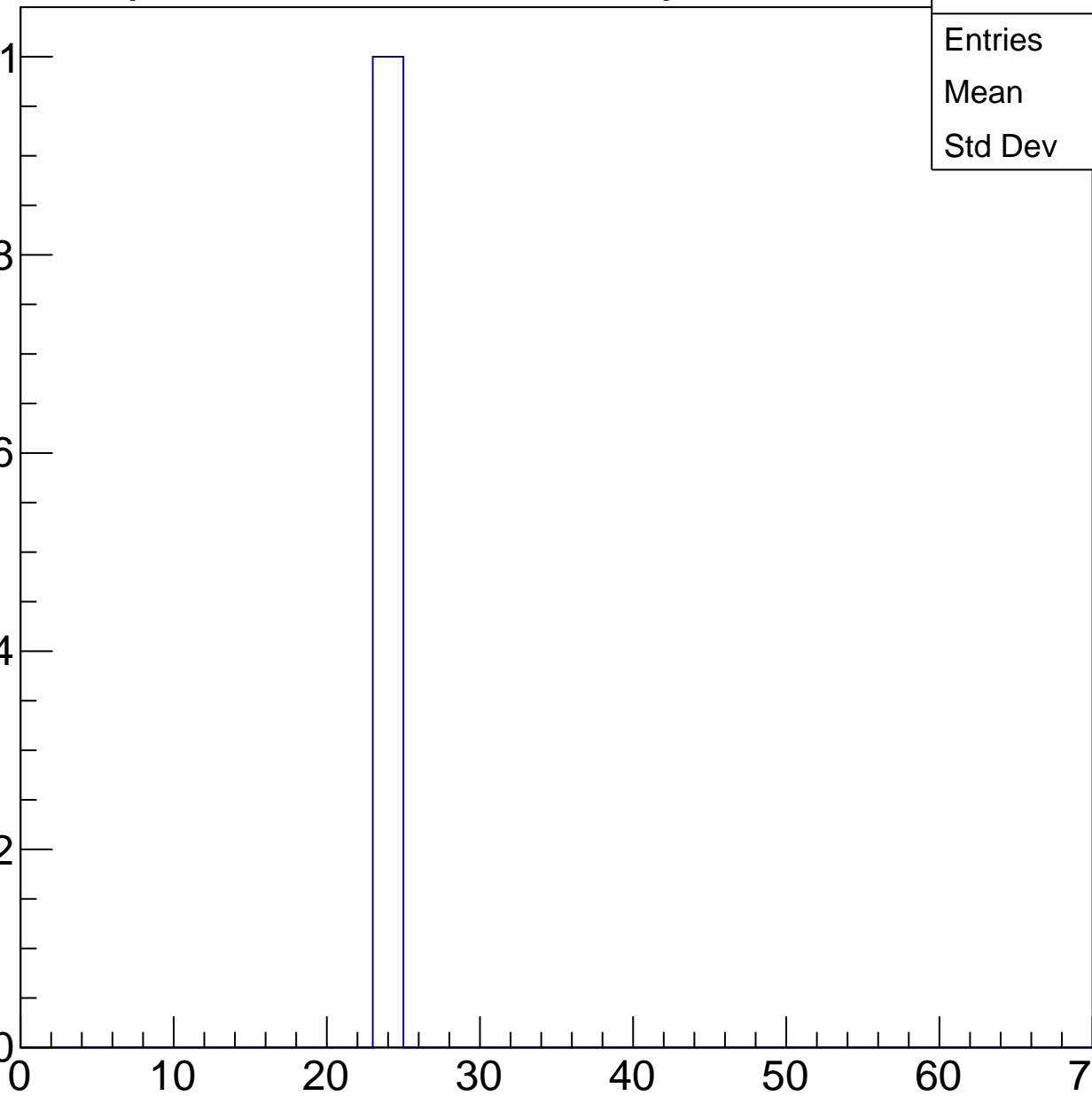
calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23.5
Std Dev	0.5

ampl



B0L101S, U21-ch76, adc0

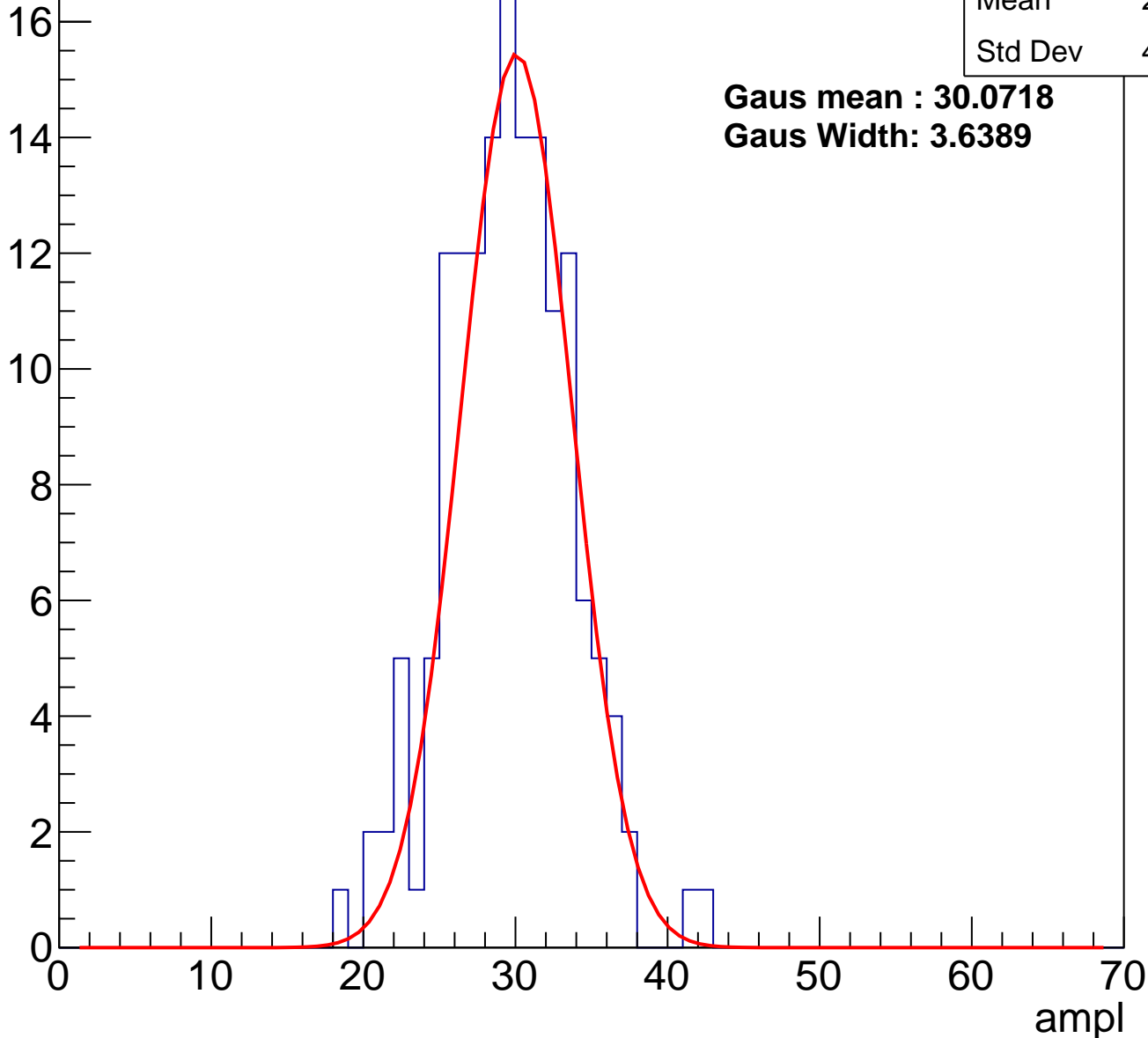
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	29.12
Std Dev	4.046

Gaus mean : 30.0718

Gaus Width: 3.6389

Entry

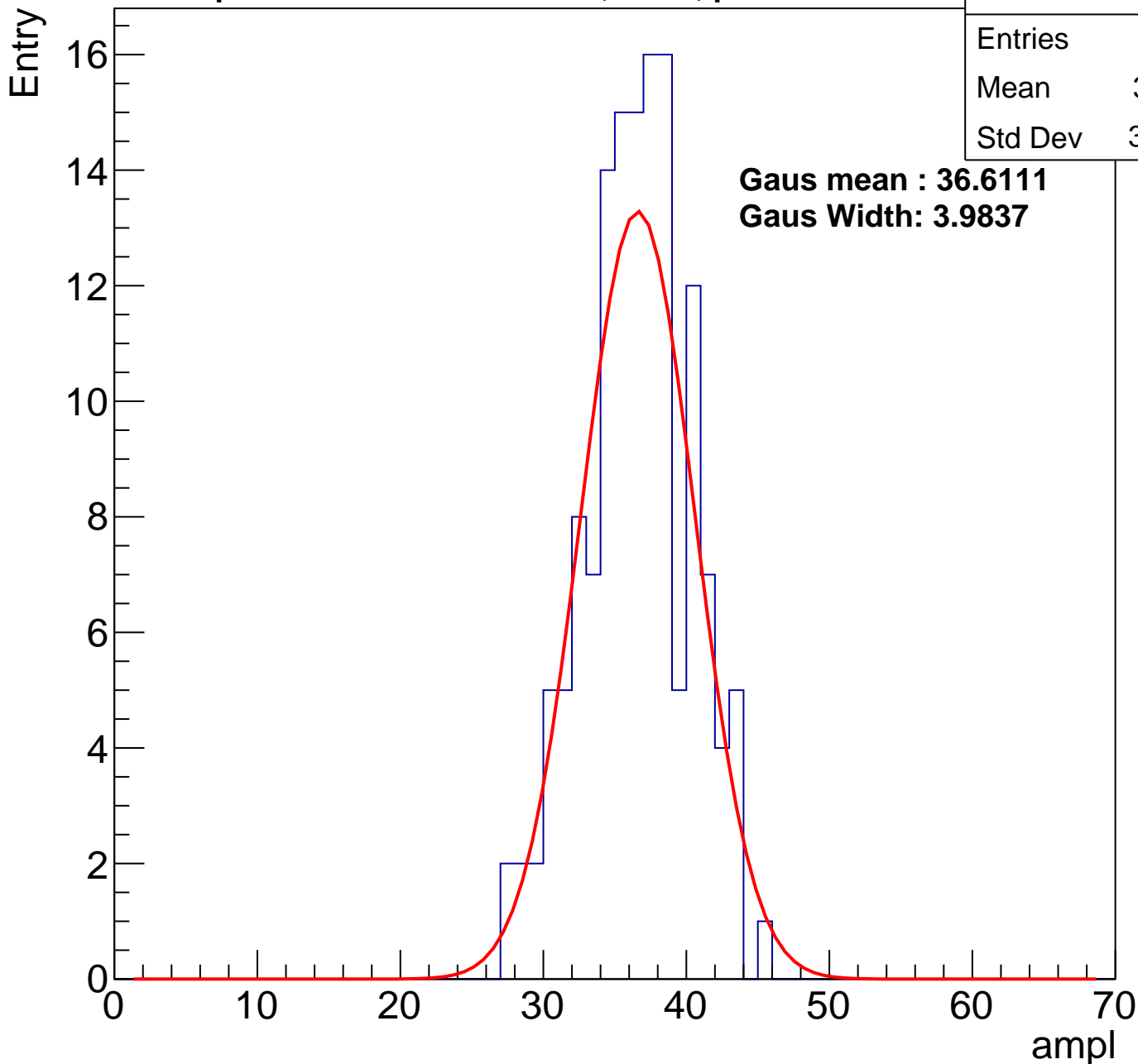


B0L101S, U21-ch76, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.11
Std Dev	3.686

Gaus mean : 36.6111
Gaus Width: 3.9837



B0L101S, U21-ch76, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	43.41
Std Dev	3.444

Gaus mean : 43.8442

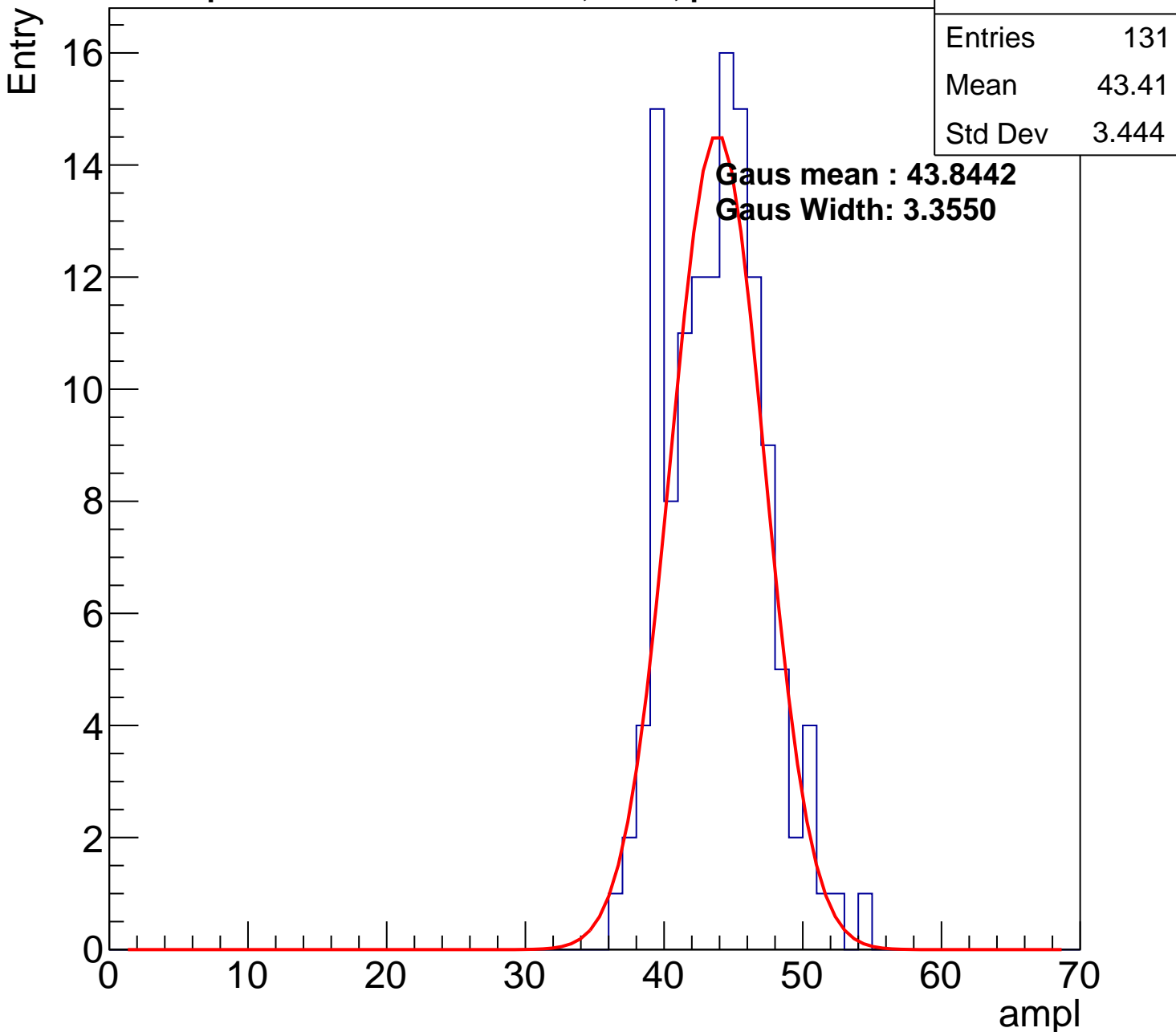
Gaus Width: 3.3550

Entry

16
14
12
10
8
6
4
2
0

ampl

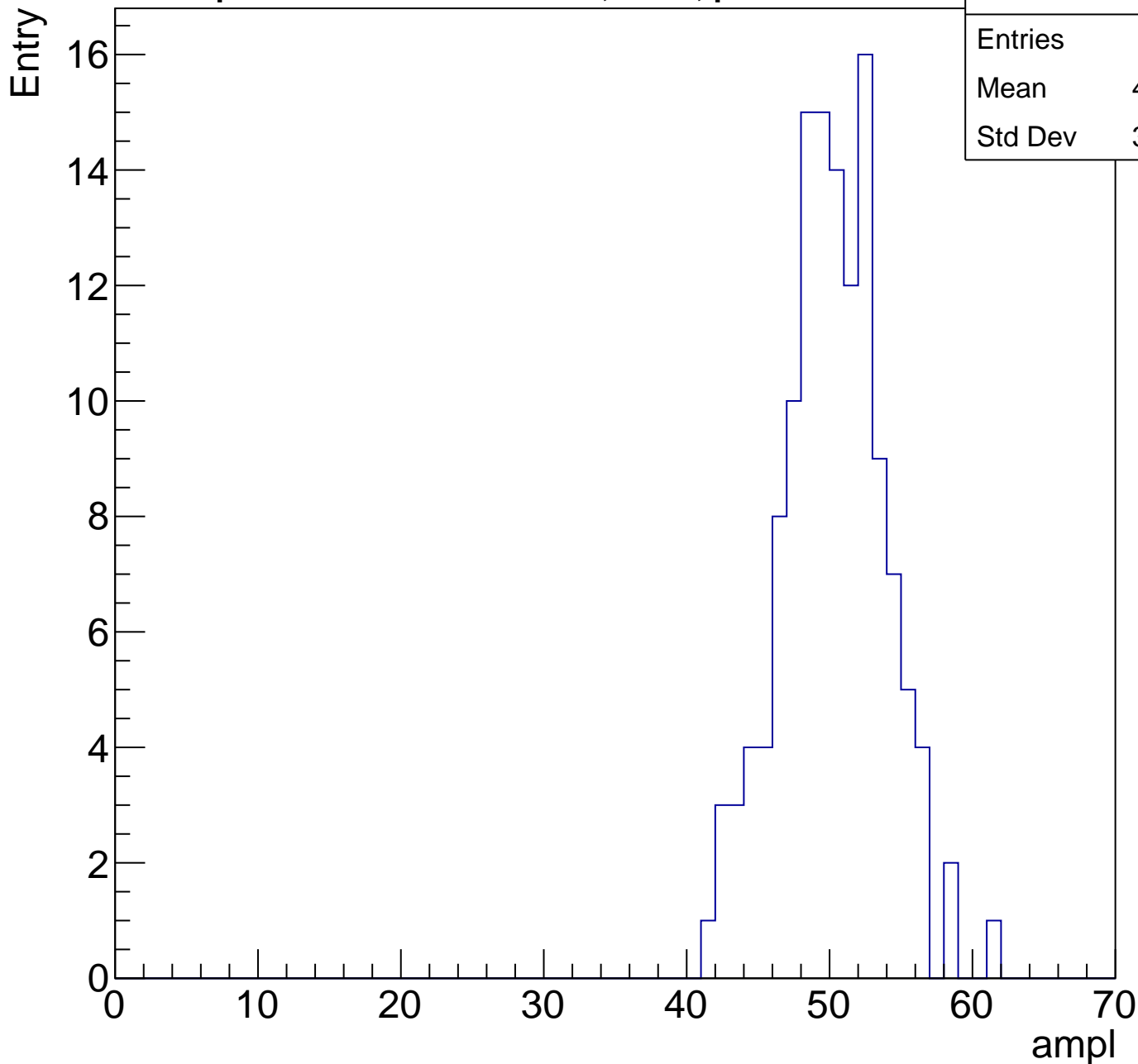
0 10 20 30 40 50 60 70



B0L101S, U21-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	49.77
Std Dev	3.614



B0L101S, U21-ch76, adc4

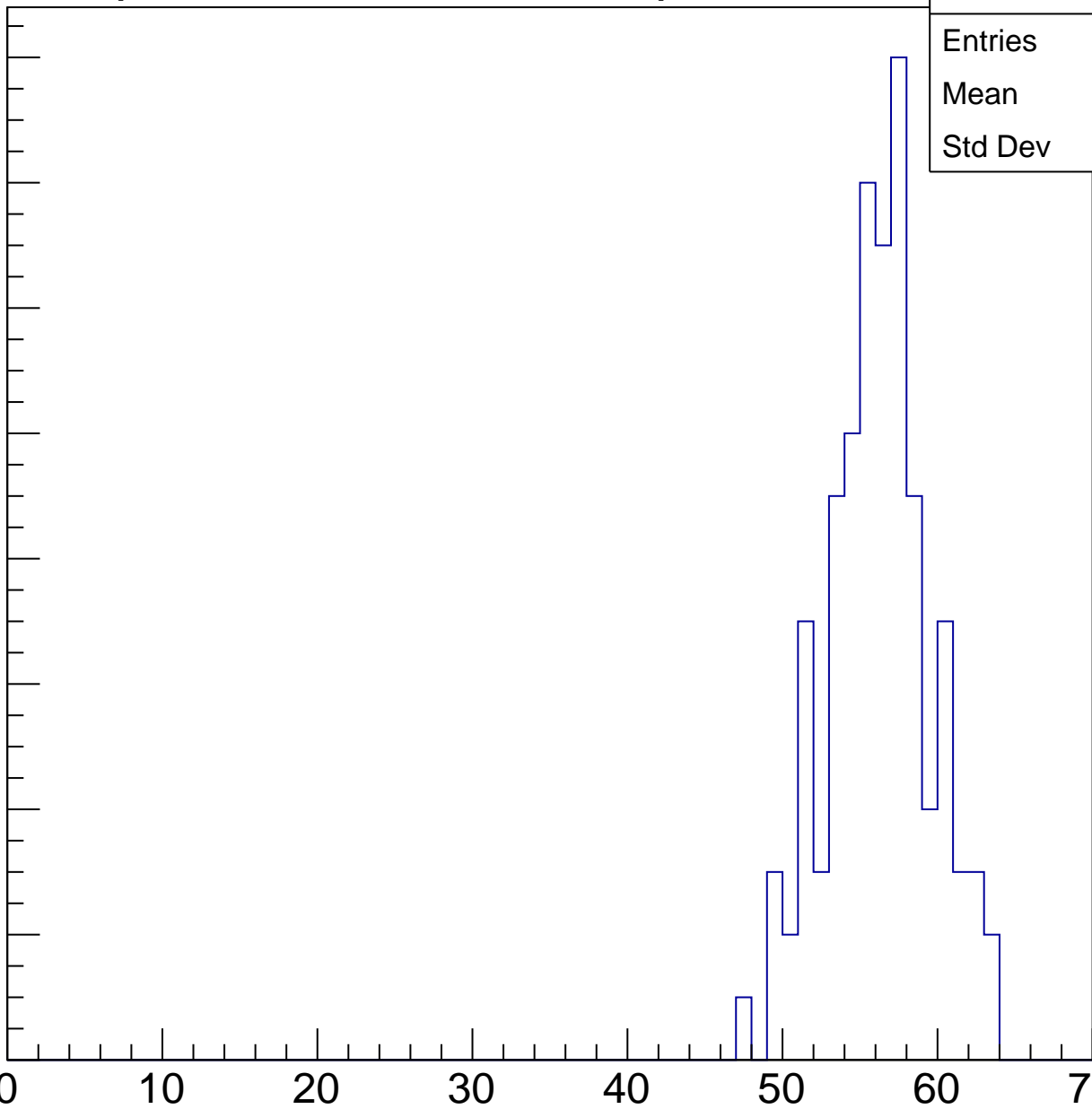
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	55.73
Std Dev	3.267

ampl



B0L101S, U21-ch76, adc5

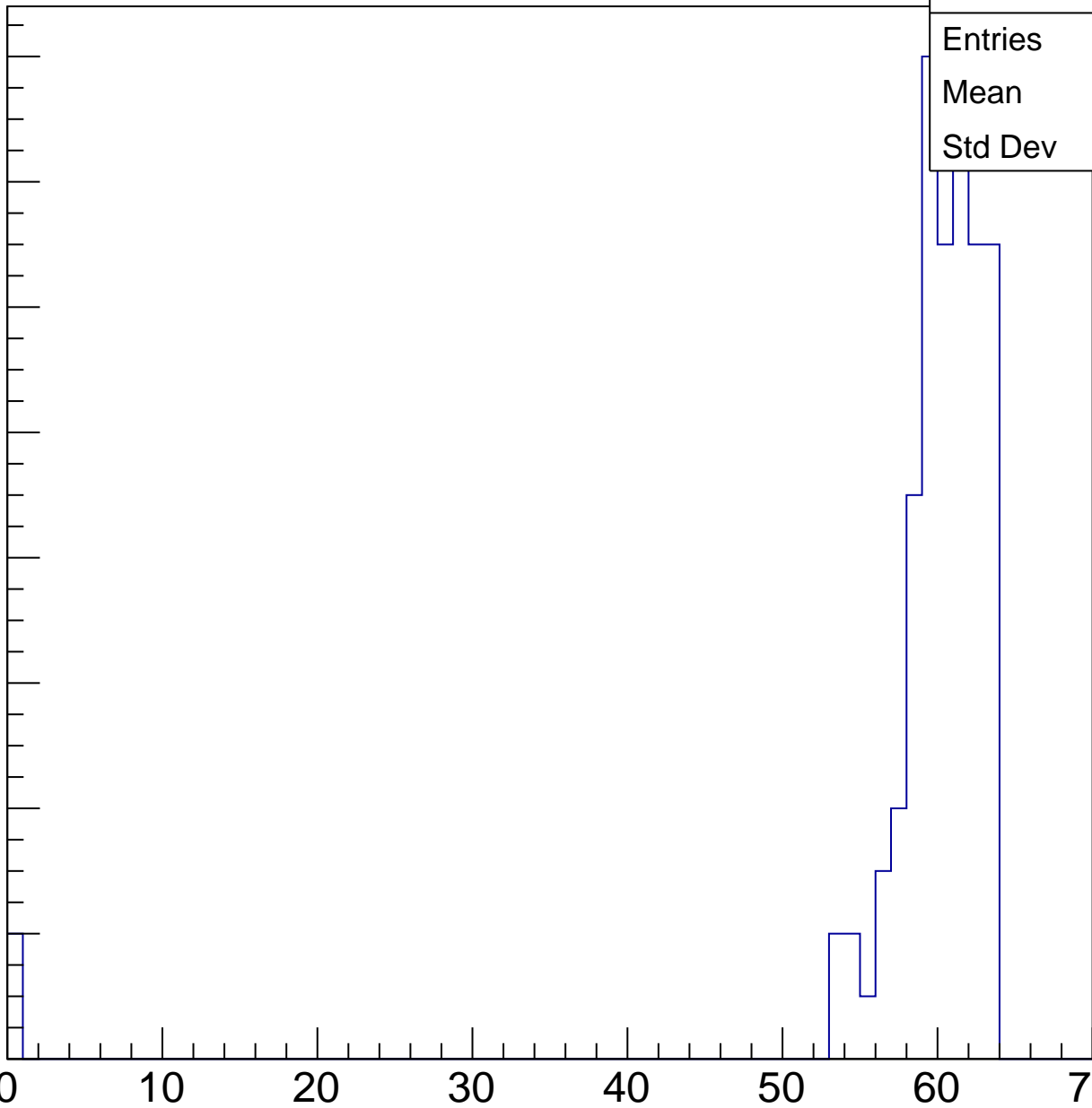
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.61
Std Dev	9.002

ampl

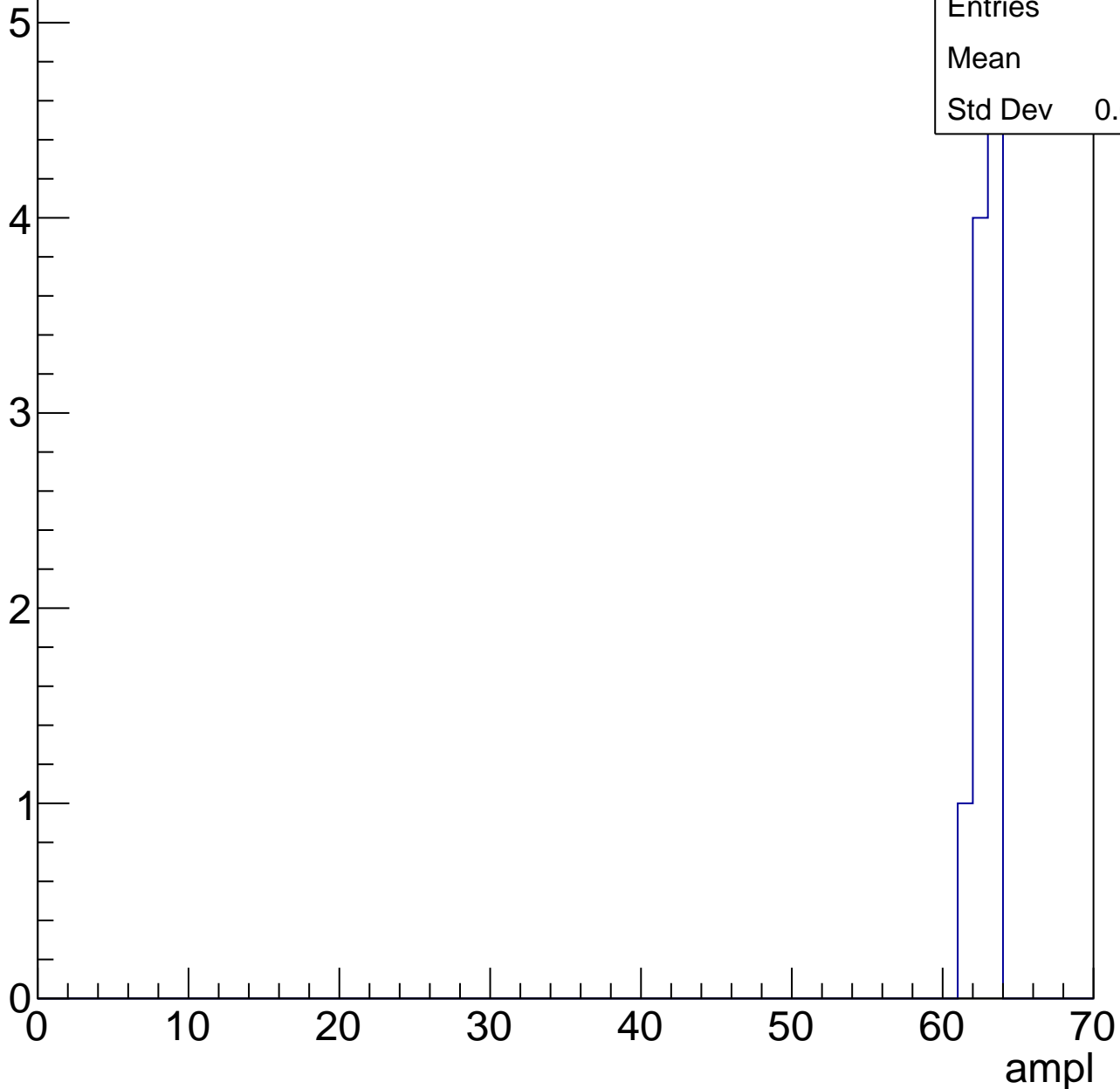


B0L101S, U21-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.4
Std Dev	0.6633



B0L101S, U21-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

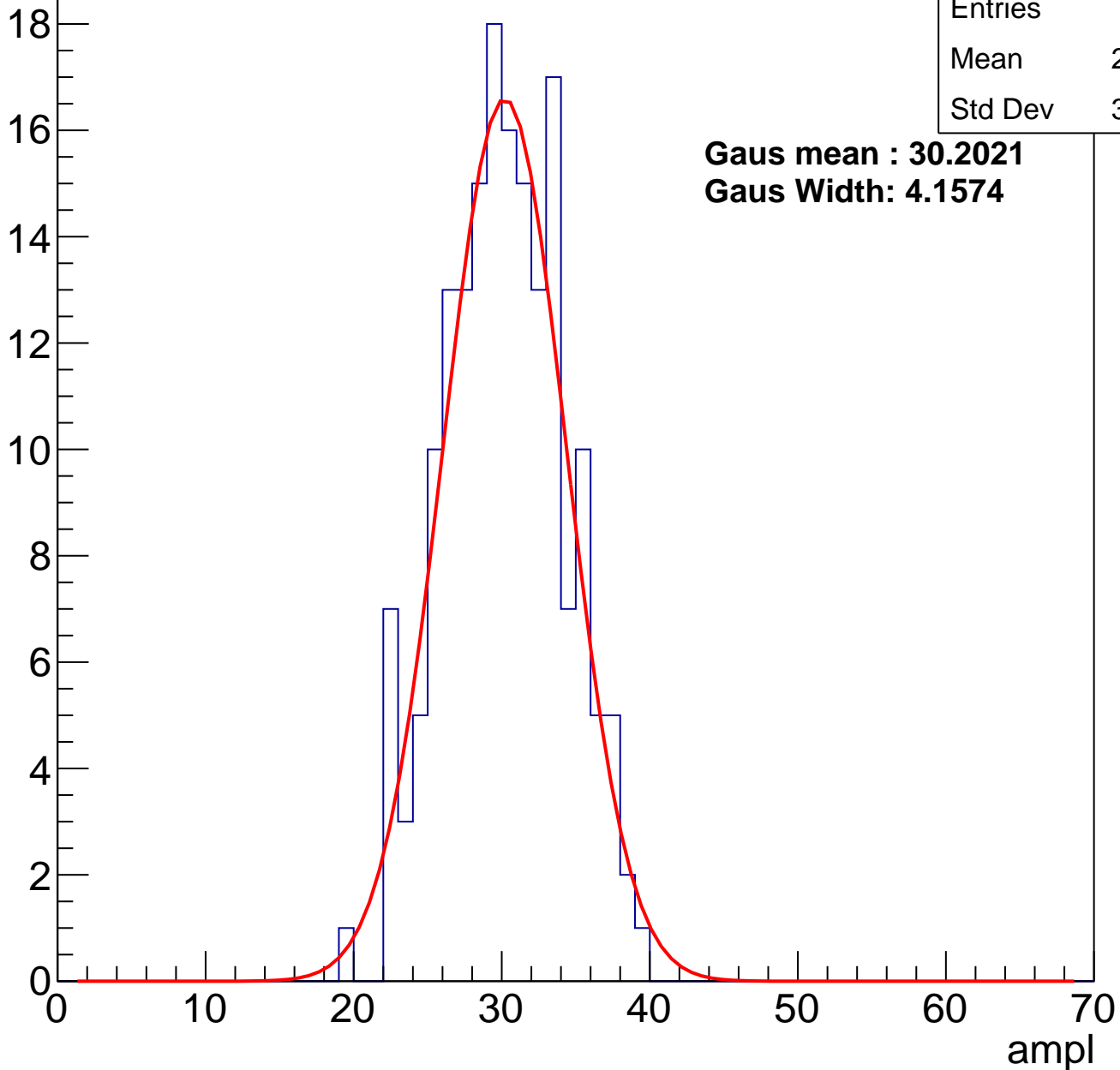
Entry



B0L101S, U21-ch77, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch77, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	36.69
Std Dev	3.723

Gaus mean : 37.2653

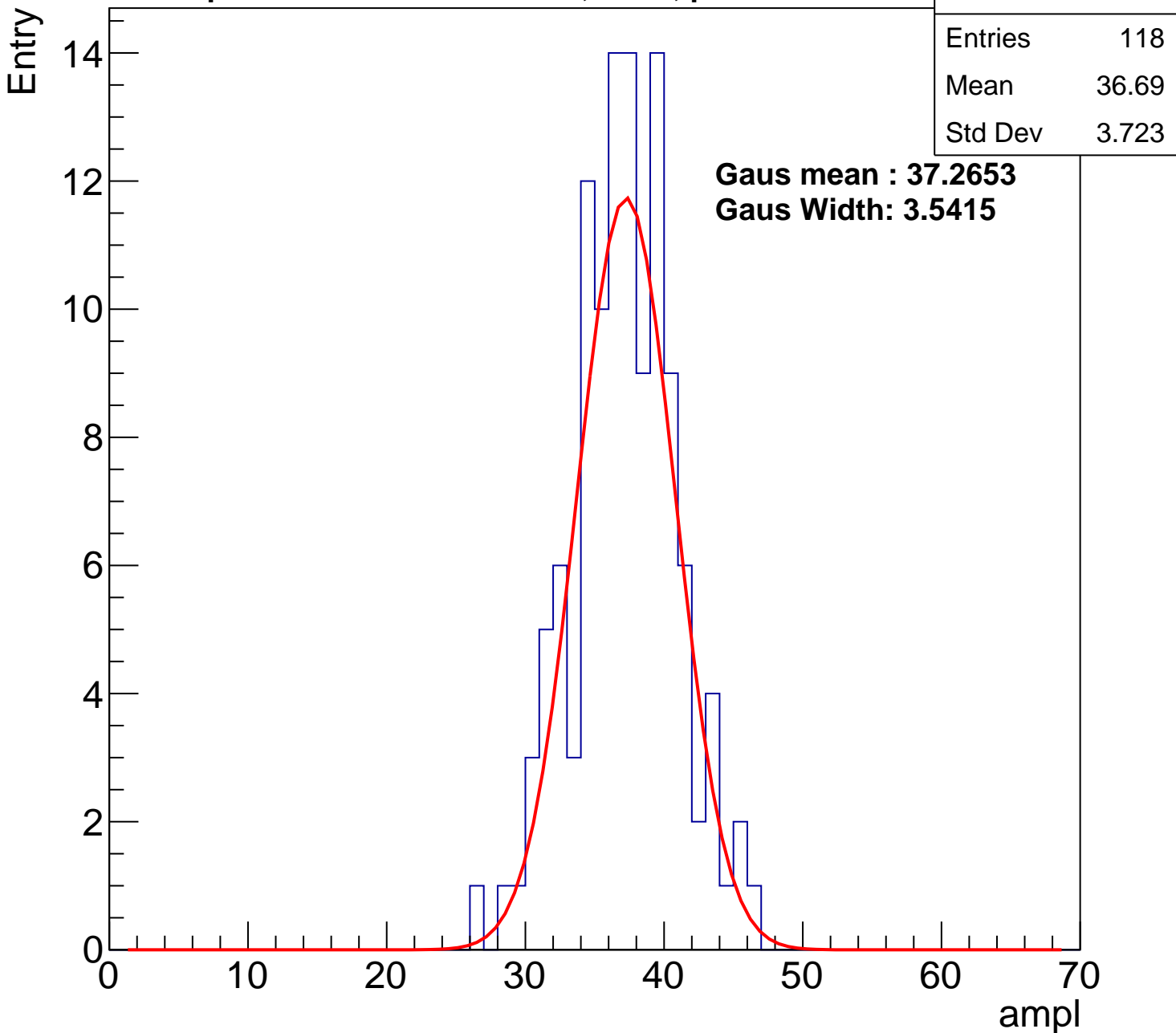
Gaus Width: 3.5415

Entry

14
12
10
8
6
4
2
0

ampl

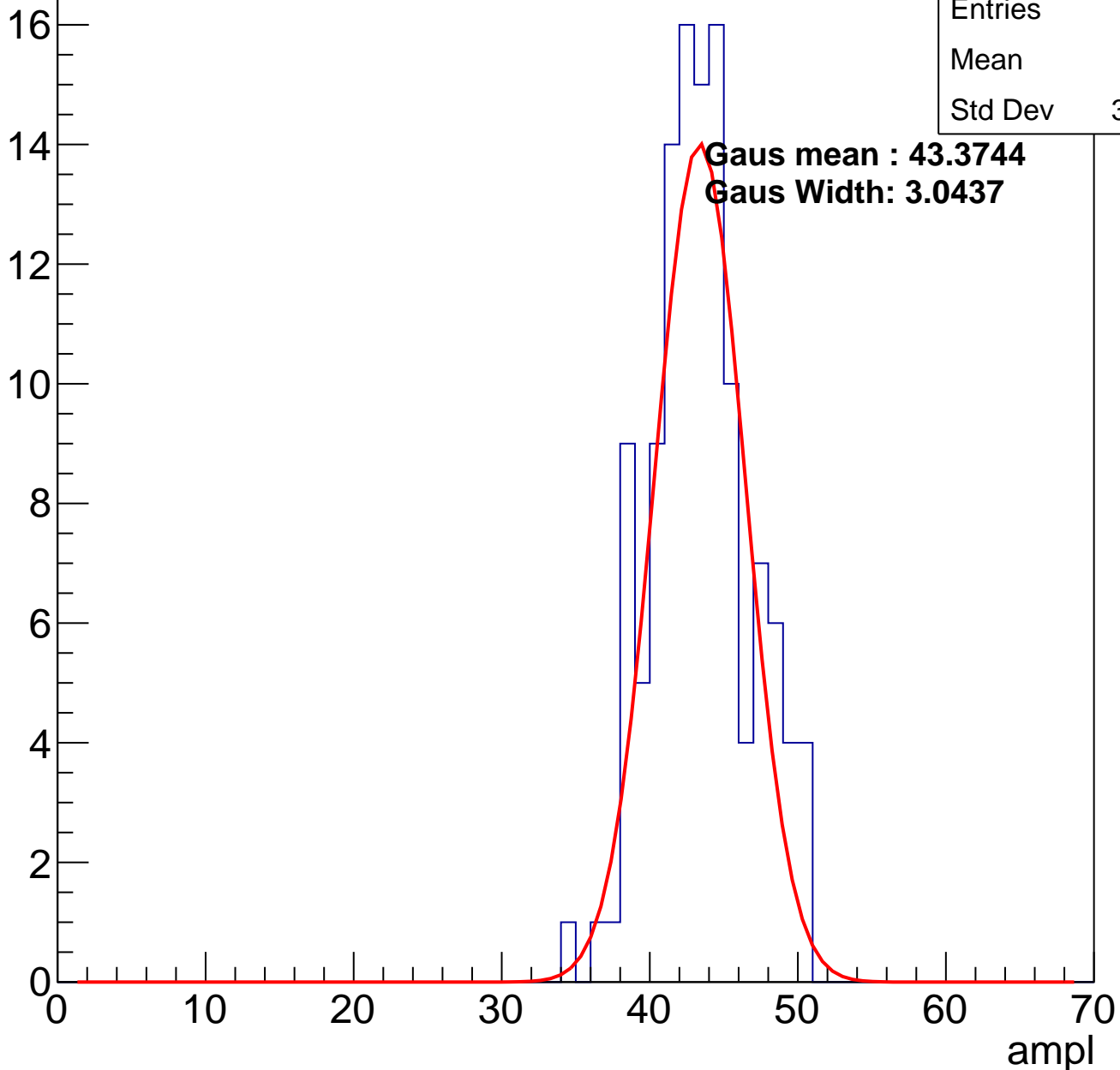
0 10 20 30 40 50 60 70



B0L101S, U21-ch77, adc2

calib_packv5_042523_0143.root, FC#1, port C1

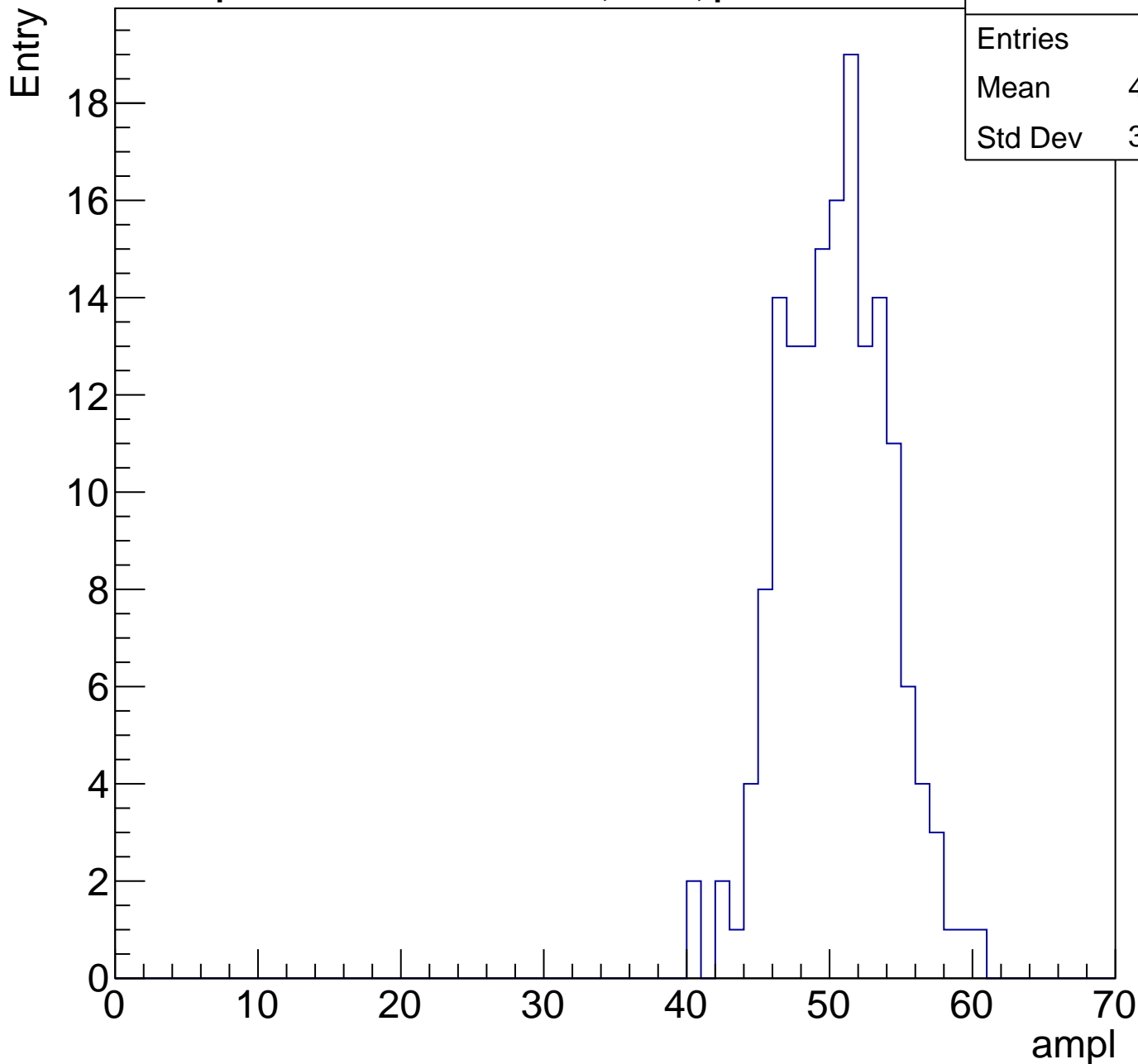
Entry



B0L101S, U21-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	49.94
Std Dev	3.686



B0L101S, U21-ch77, adc4

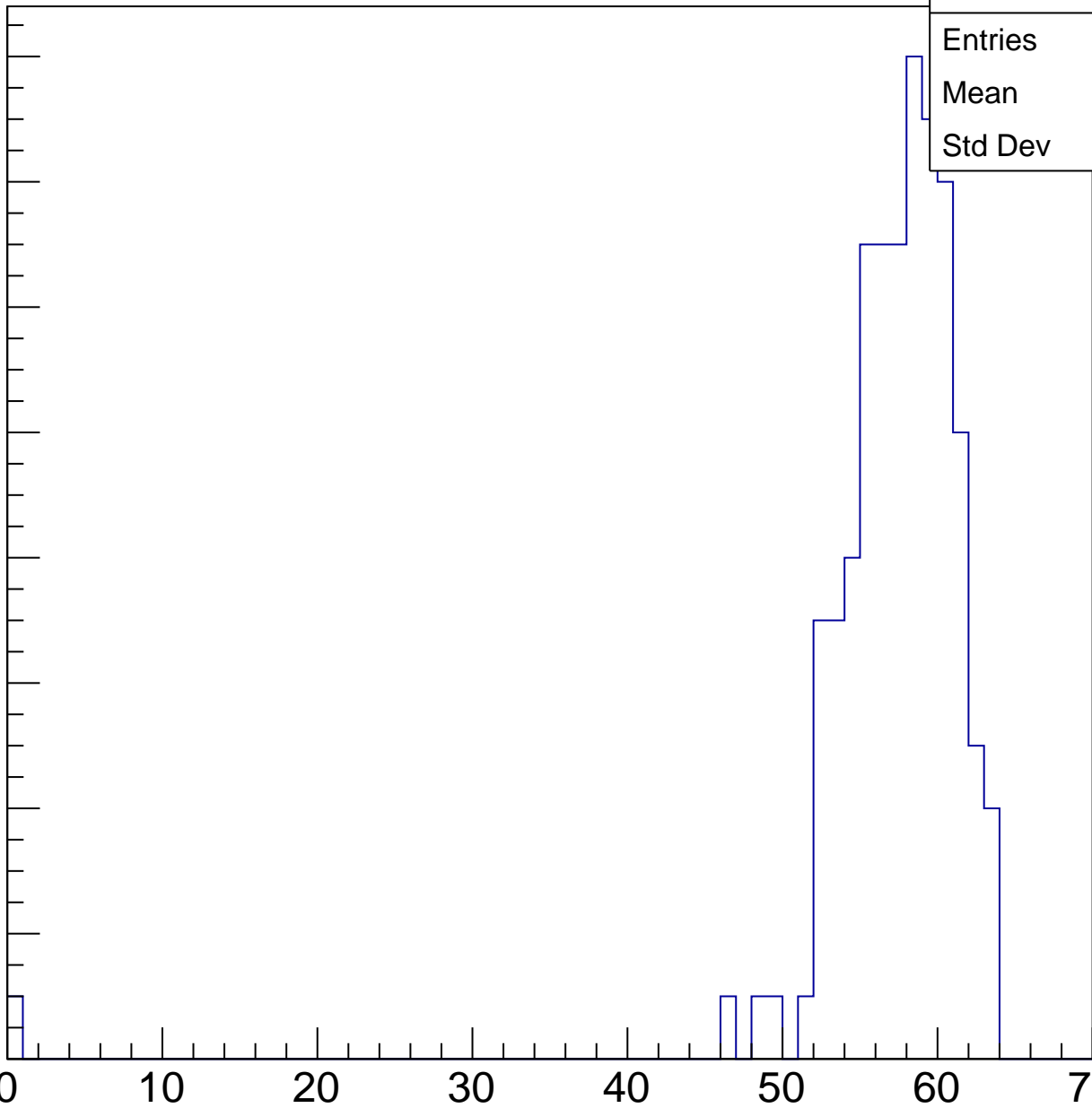
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	130
Mean	56.69
Std Dev	5.944

ampl



B0L101S, U21-ch77, adc5

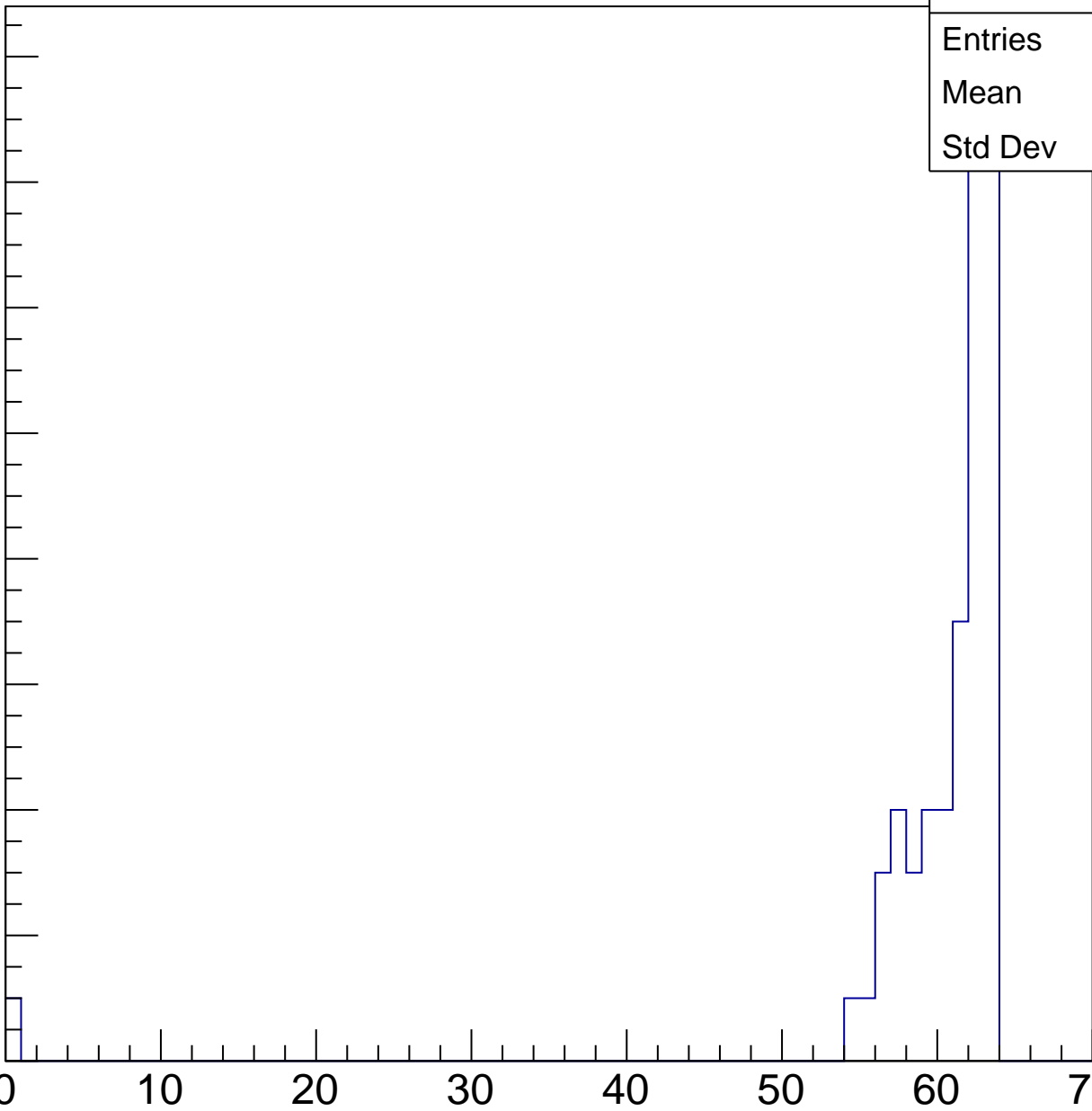
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	59
Mean	59.66
Std Dev	8.198

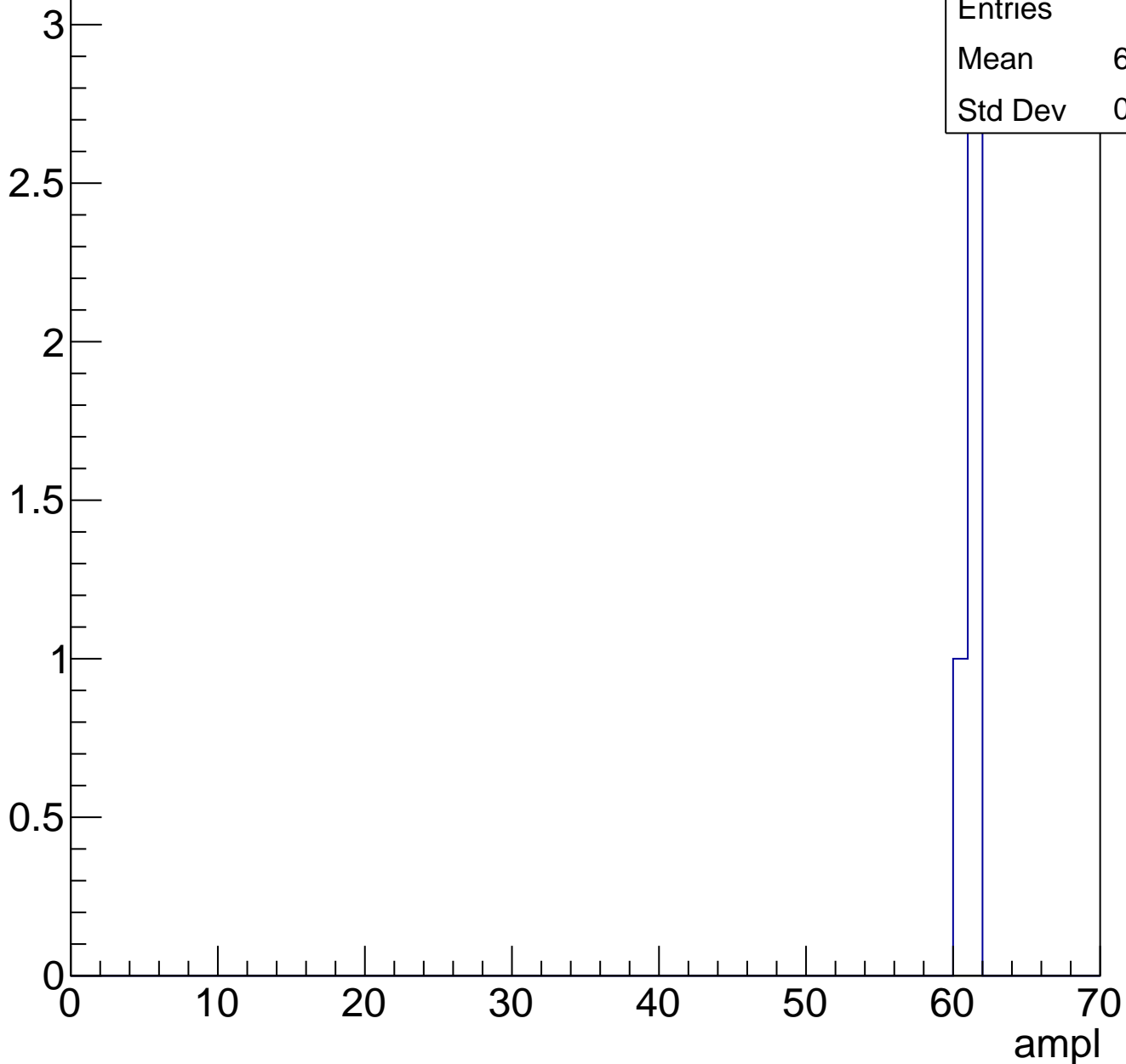
ampl



B0L101S, U21-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch78, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	27.34
Std Dev	4.183

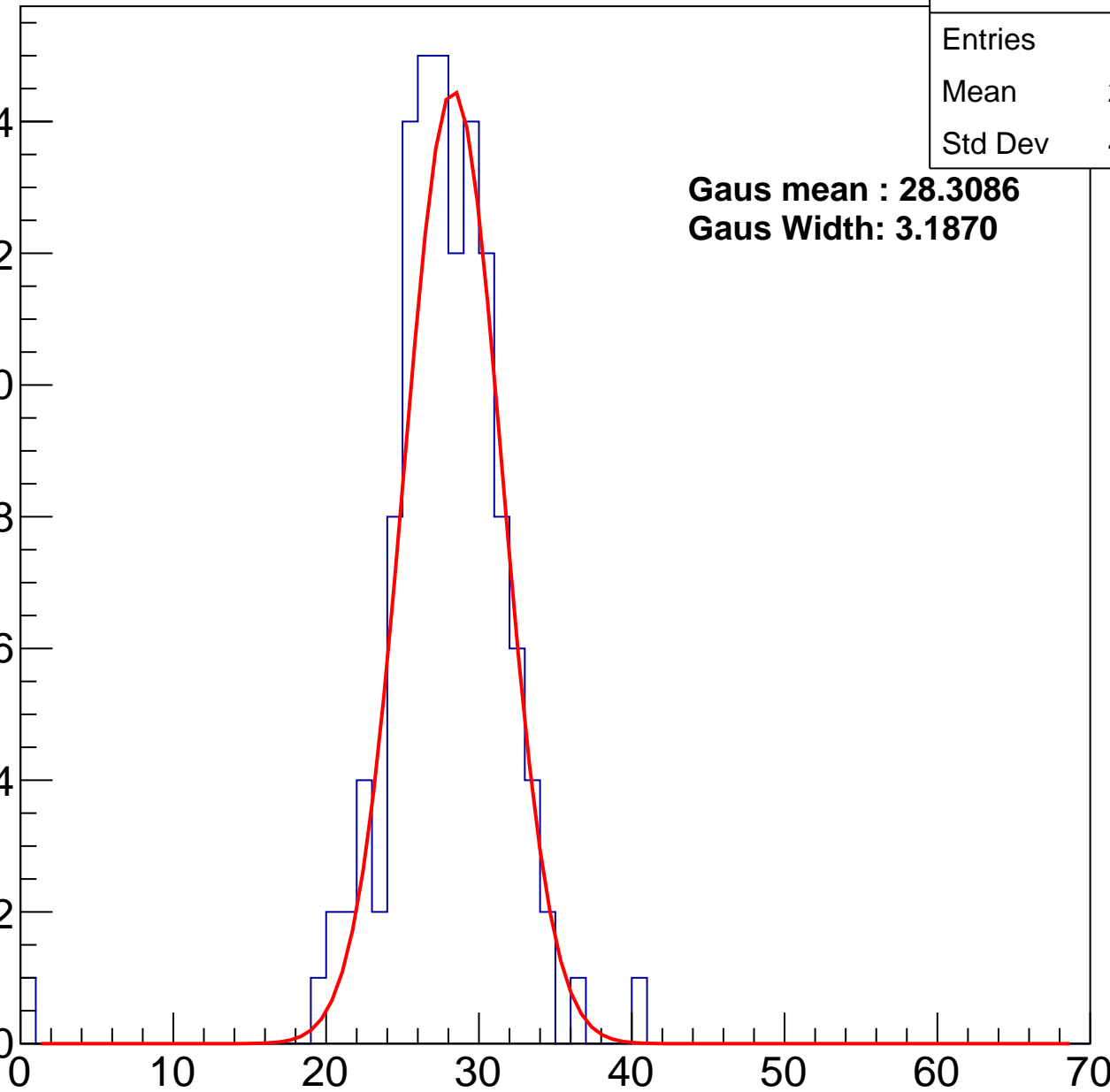
Gaus mean : 28.3086

Gaus Width: 3.1870

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch78, adc1

calib_packv5_042523_0143.root, FC#1, port C1

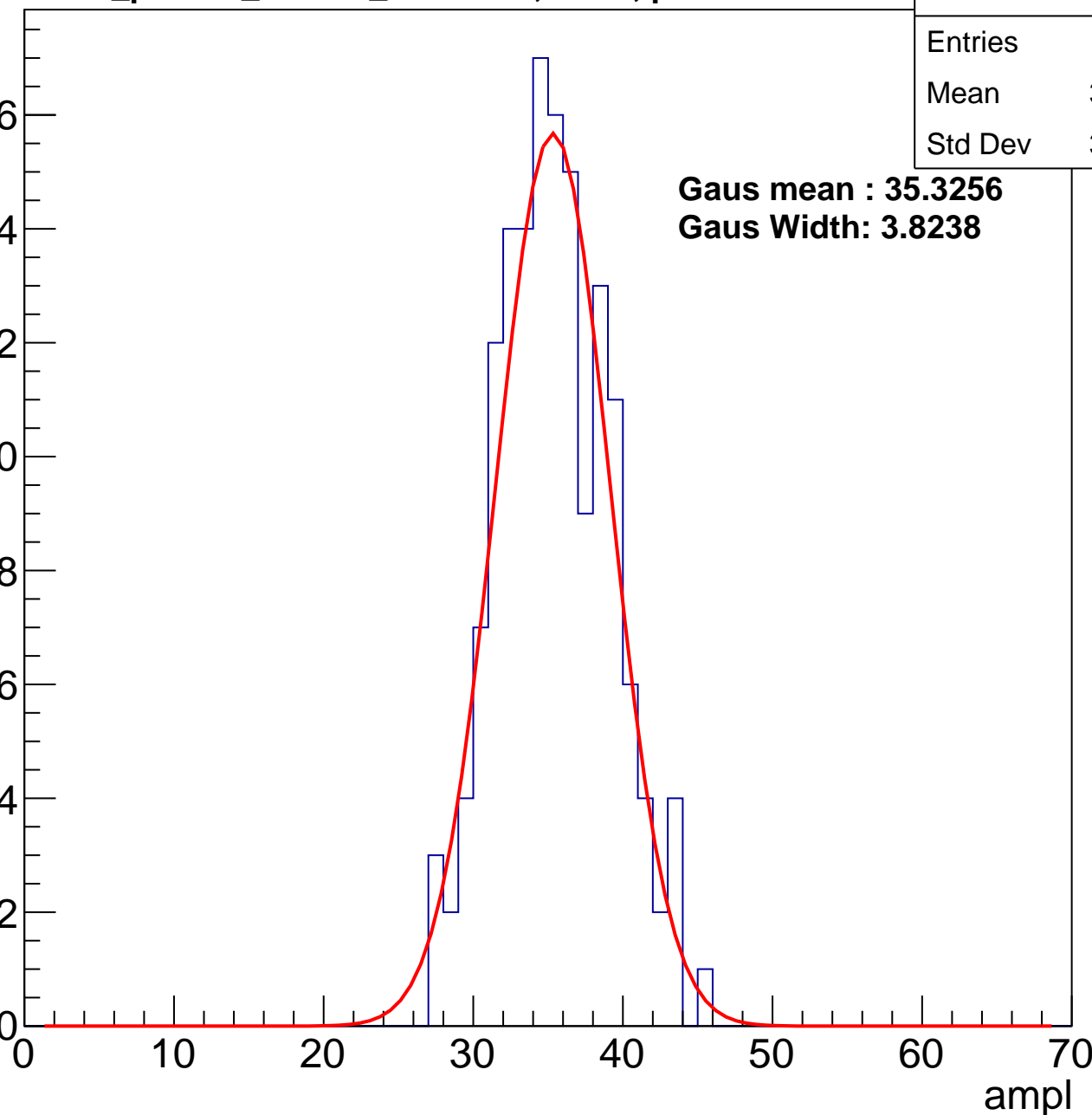
Entry

16
14
12
10
8
6
4
2
0

Entries	154
Mean	34.96
Std Dev	3.685

Gaus mean : 35.3256

Gaus Width: 3.8238



ampl

B0L101S, U21-ch78, adc2

calib_packv5_042523_0143.root, FC#1, port C1

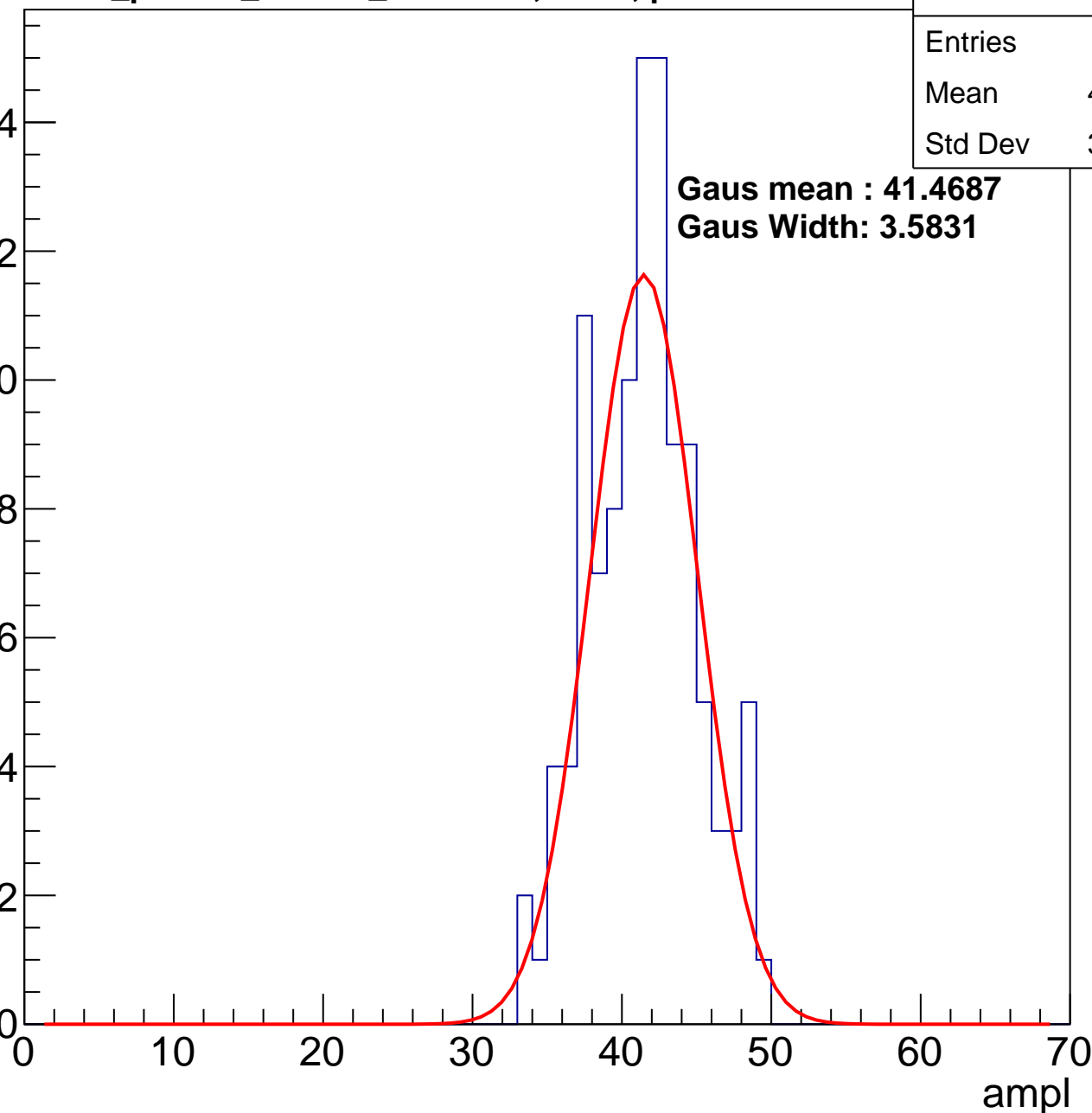
Entry

14
12
10
8
6
4
2
0

Entries	112
Mean	40.98
Std Dev	3.543

Gaus mean : 41.4687

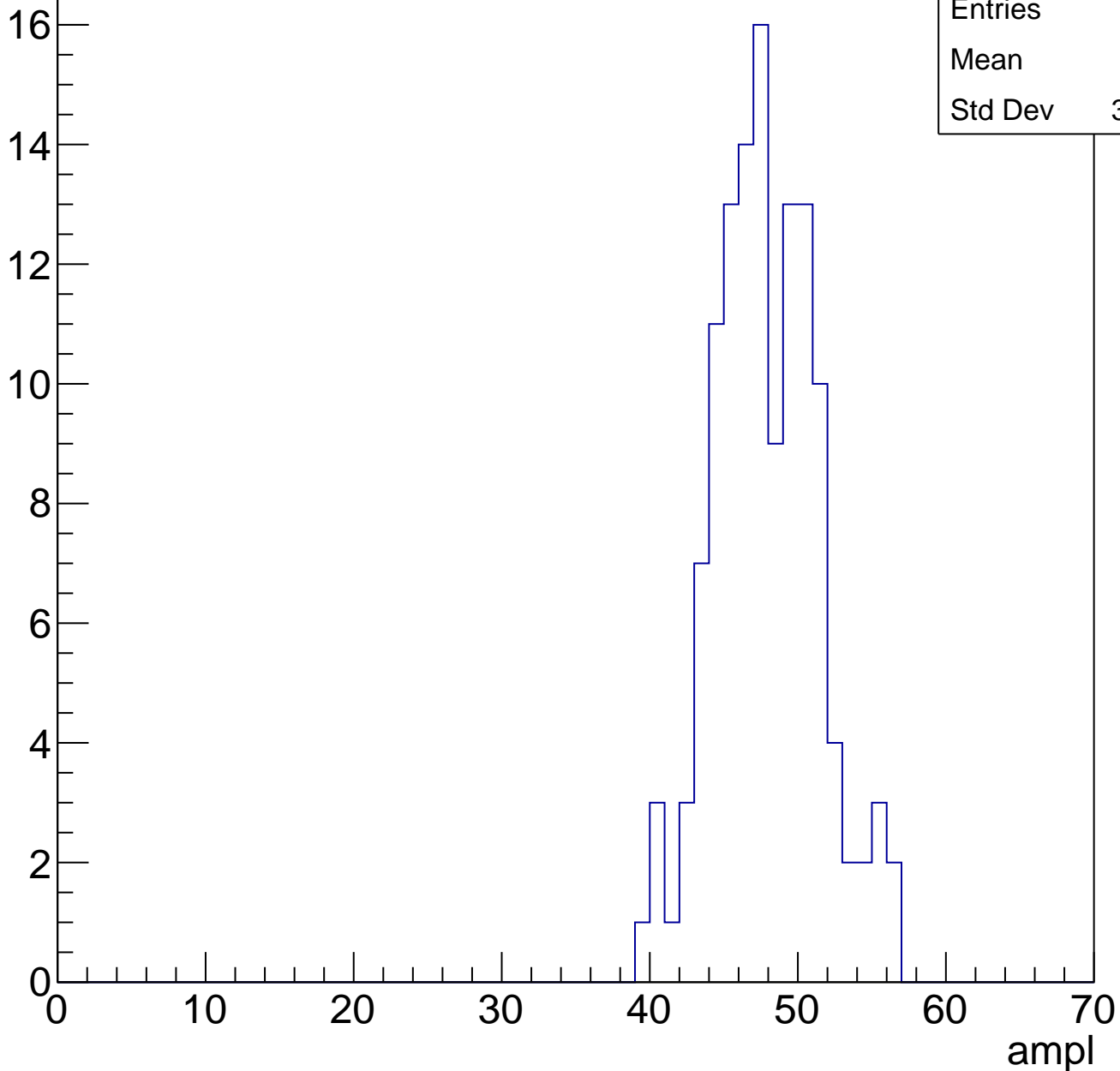
Gaus Width: 3.5831



B0L101S, U21-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

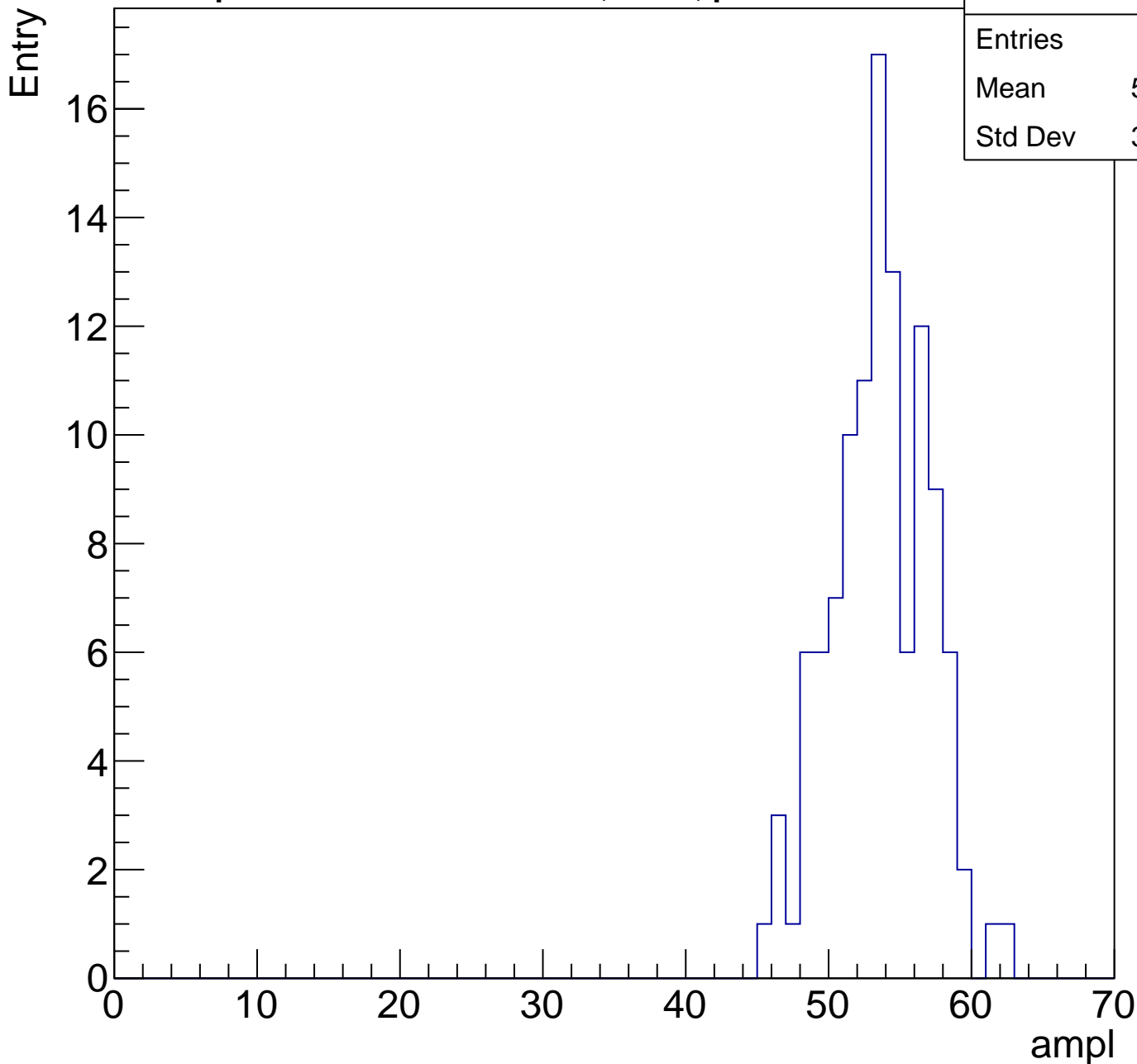
Entry



B0L101S, U21-ch78, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	53.13
Std Dev	3.355



B0L101S, U21-ch78, adc5

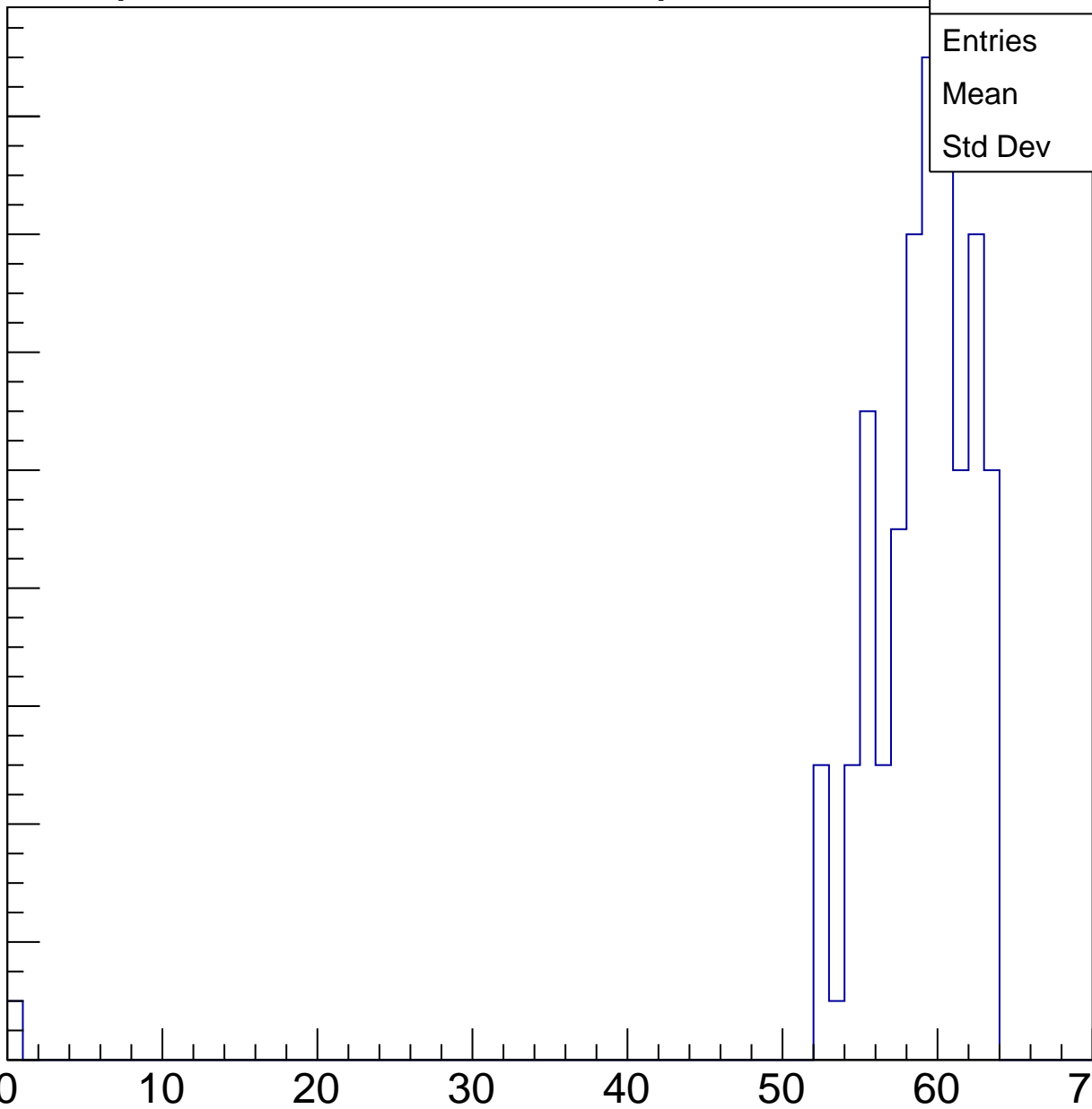
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	58.18
Std Dev	6.096

ampl

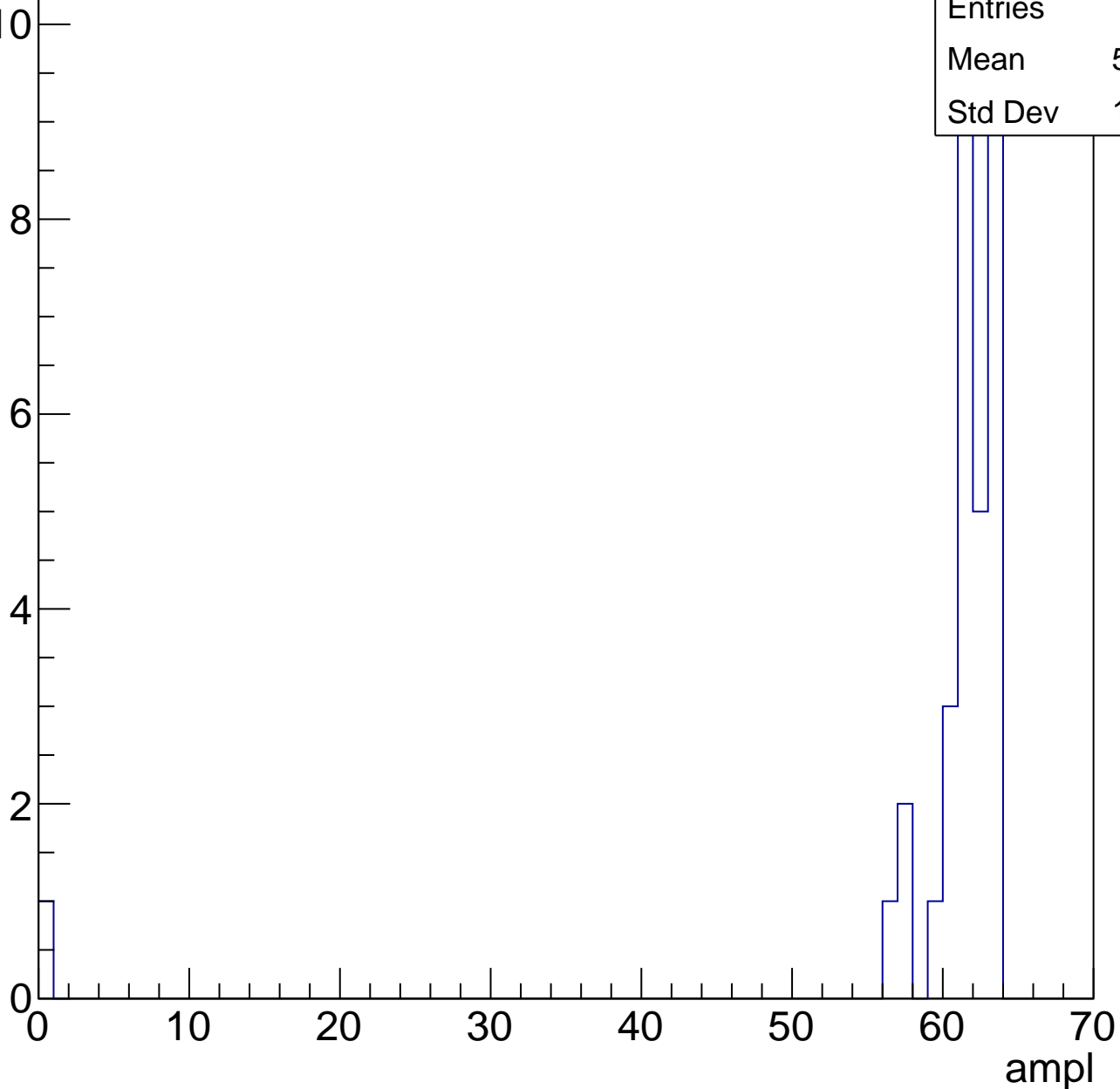


B0L101S, U21-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	32
Mean	59.31
Std Dev	10.81



B0L101S, U21-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

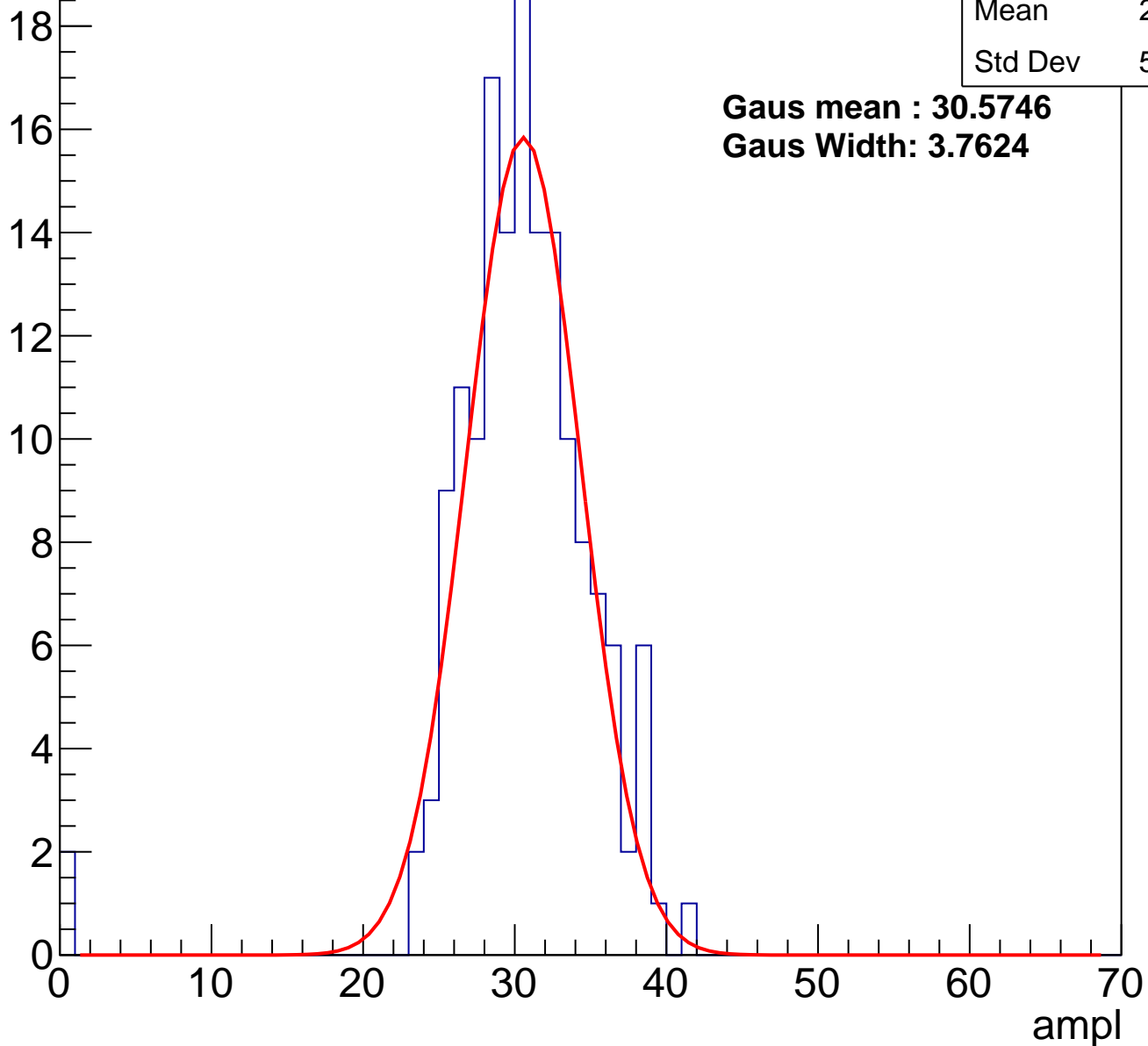


Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch79, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch79, adc1

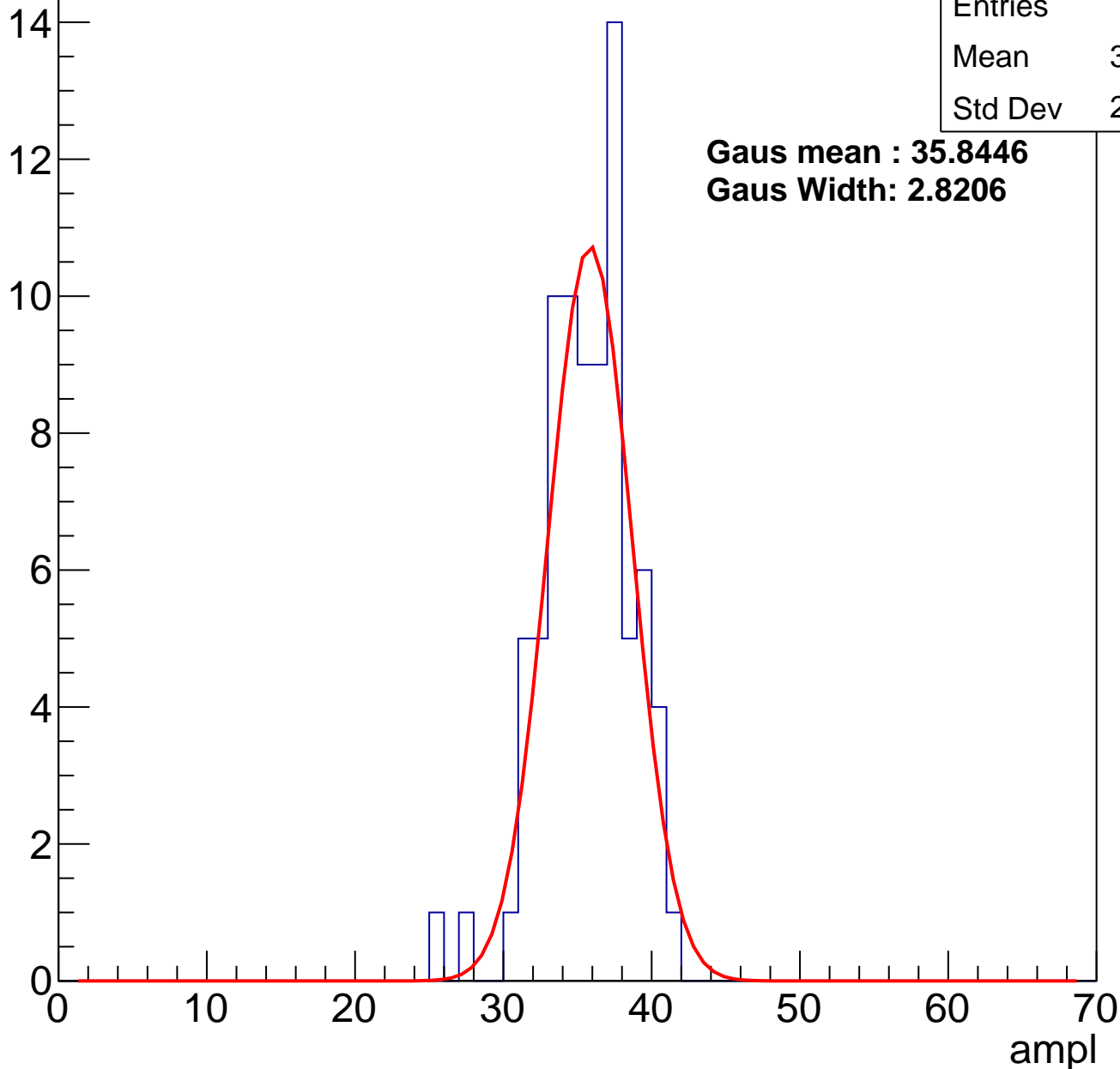
calib_packv5_042523_0143.root, FC#1, port C1

Entries	81
Mean	35.17
Std Dev	2.947

Gaus mean : 35.8446

Gaus Width: 2.8206

Entry



B0L101S, U21-ch79, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

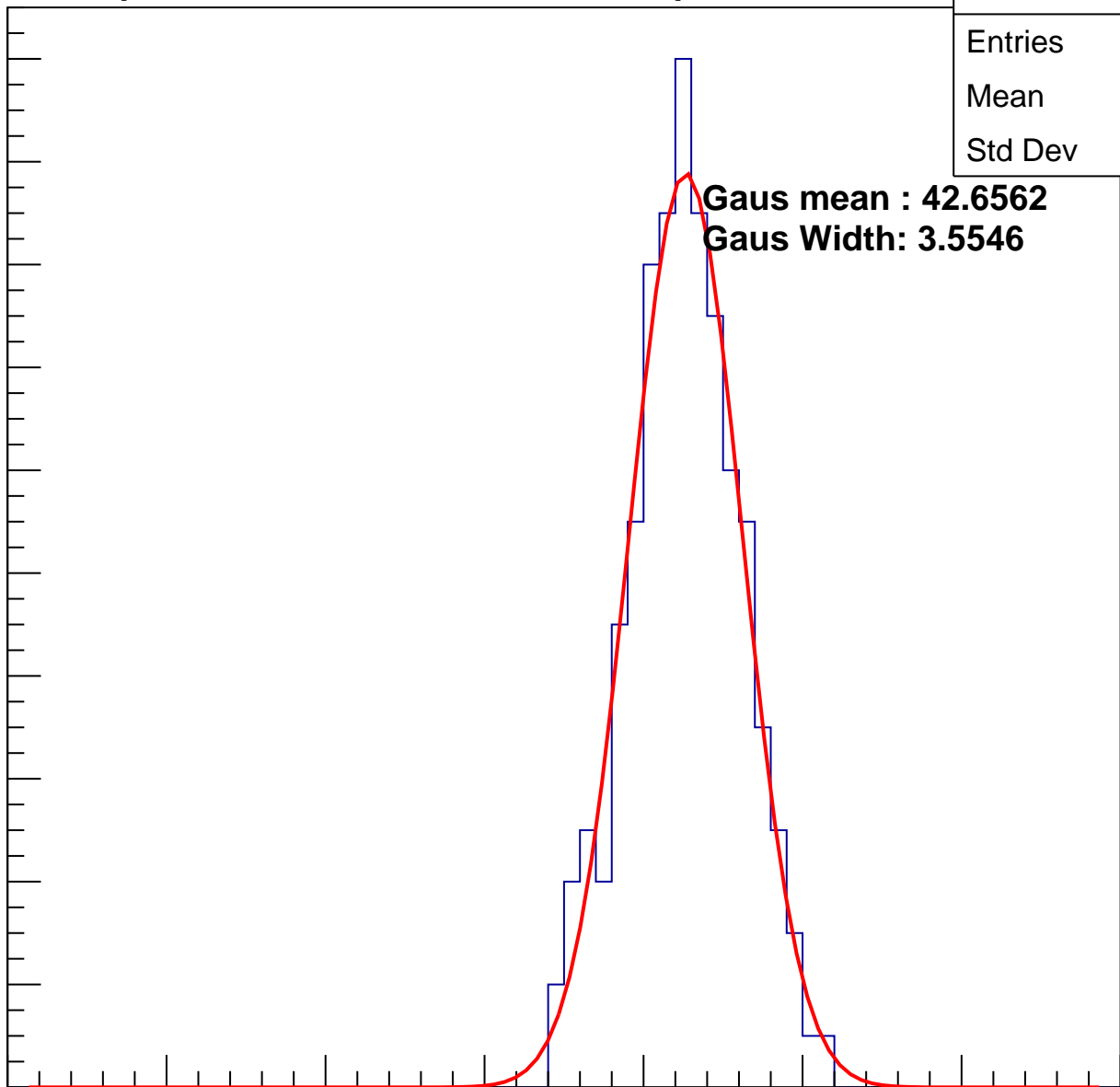
Entries	160
Mean	42.11
Std Dev	3.477

Gaus mean : 42.6562

Gaus Width: 3.5546

0 10 20 30 40 50 60 70

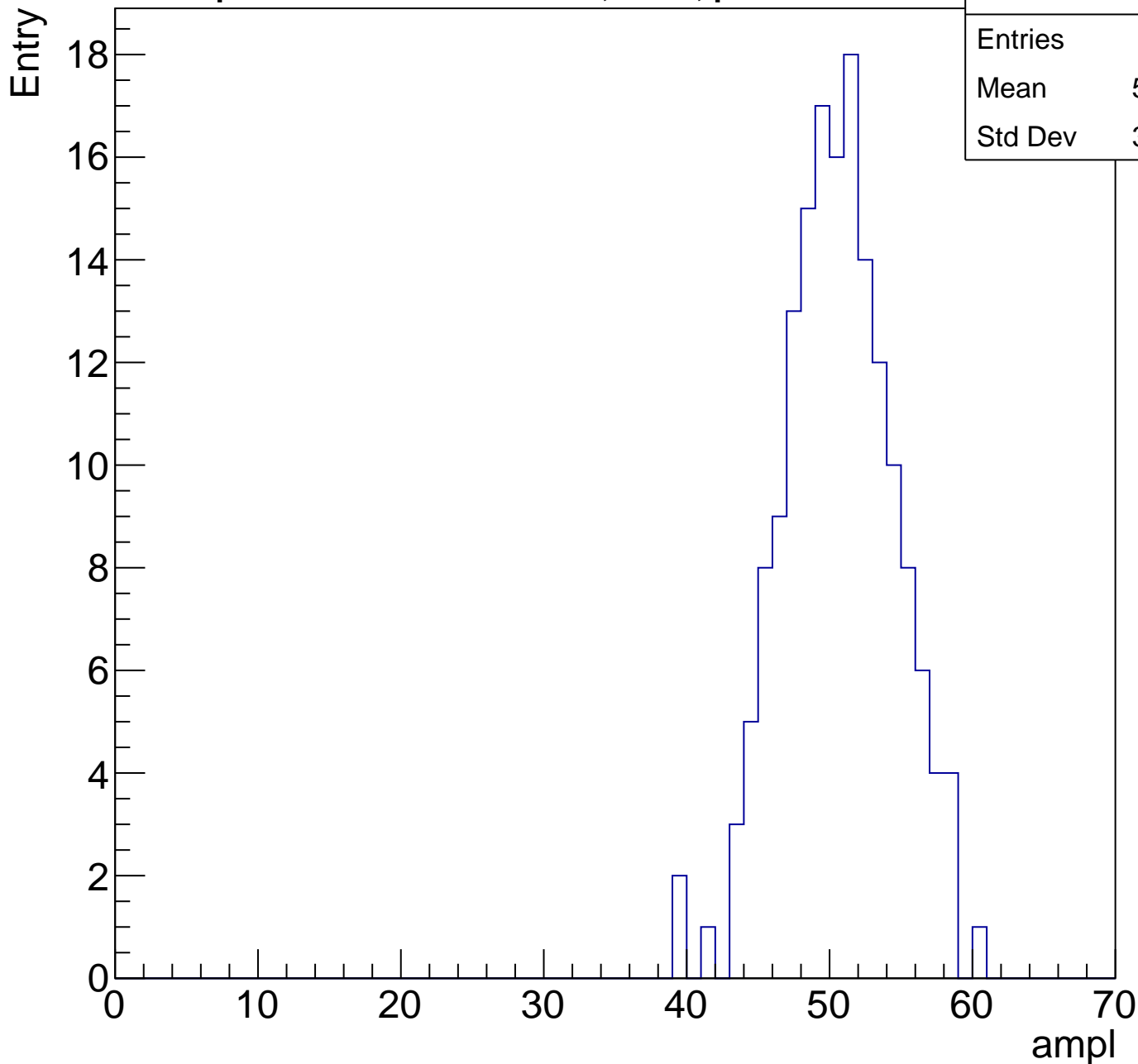
ampl



B0L101S, U21-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	50.14
Std Dev	3.877

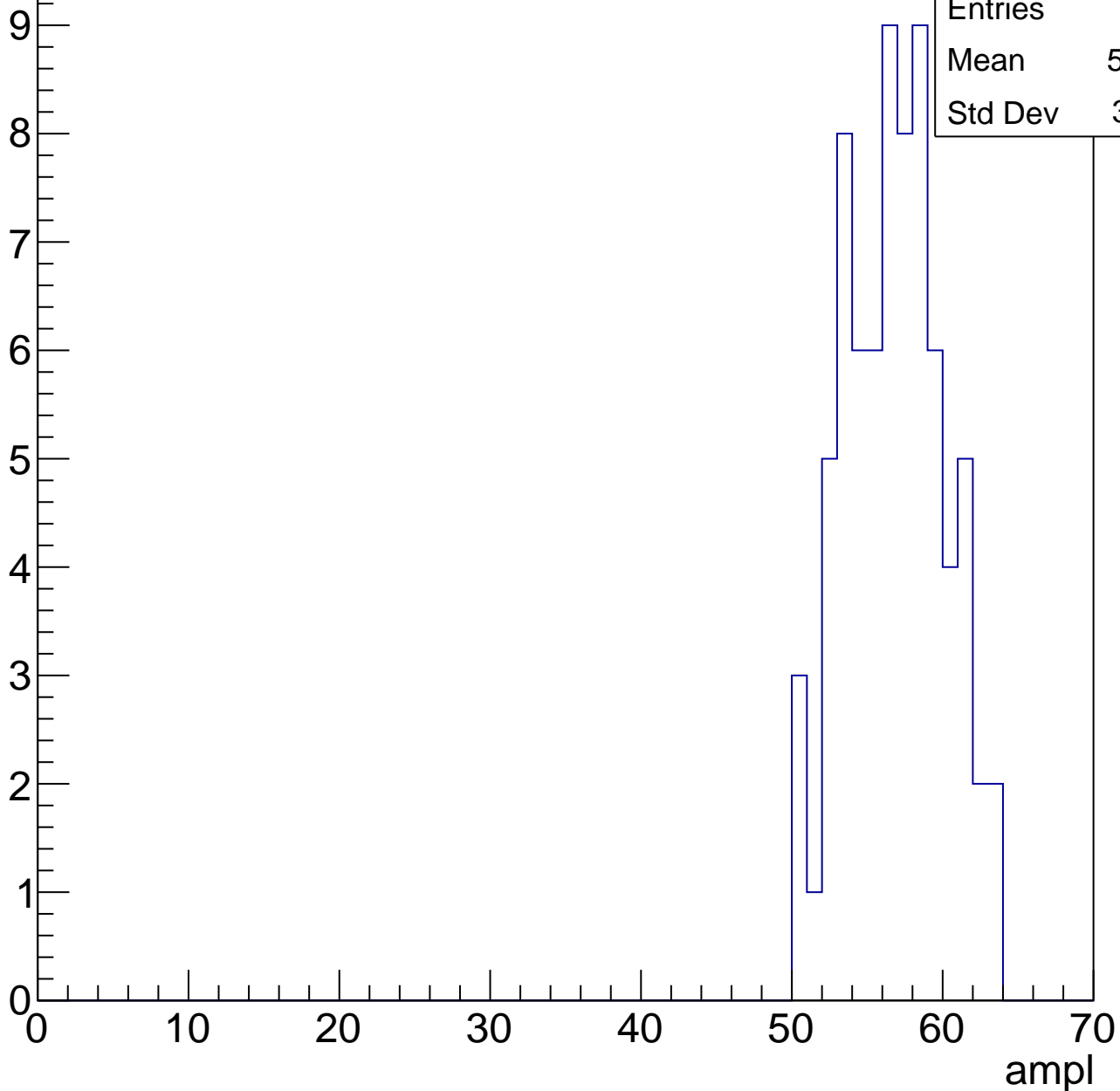


B0L101S, U21-ch79, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	74
Mean	56.35
Std Dev	3.211



B0L101S, U21-ch79, adc5

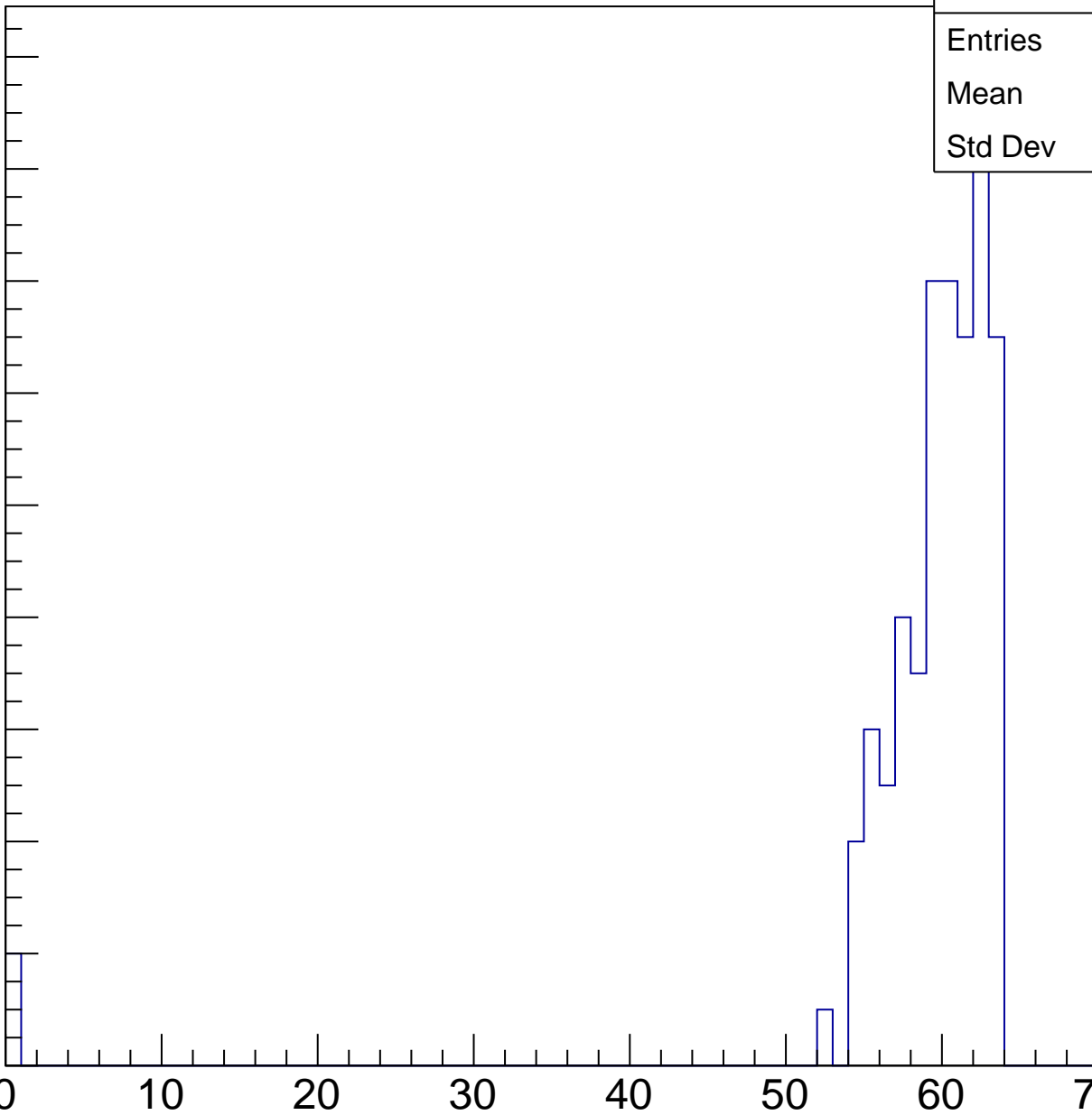
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.42
Std Dev	8.559

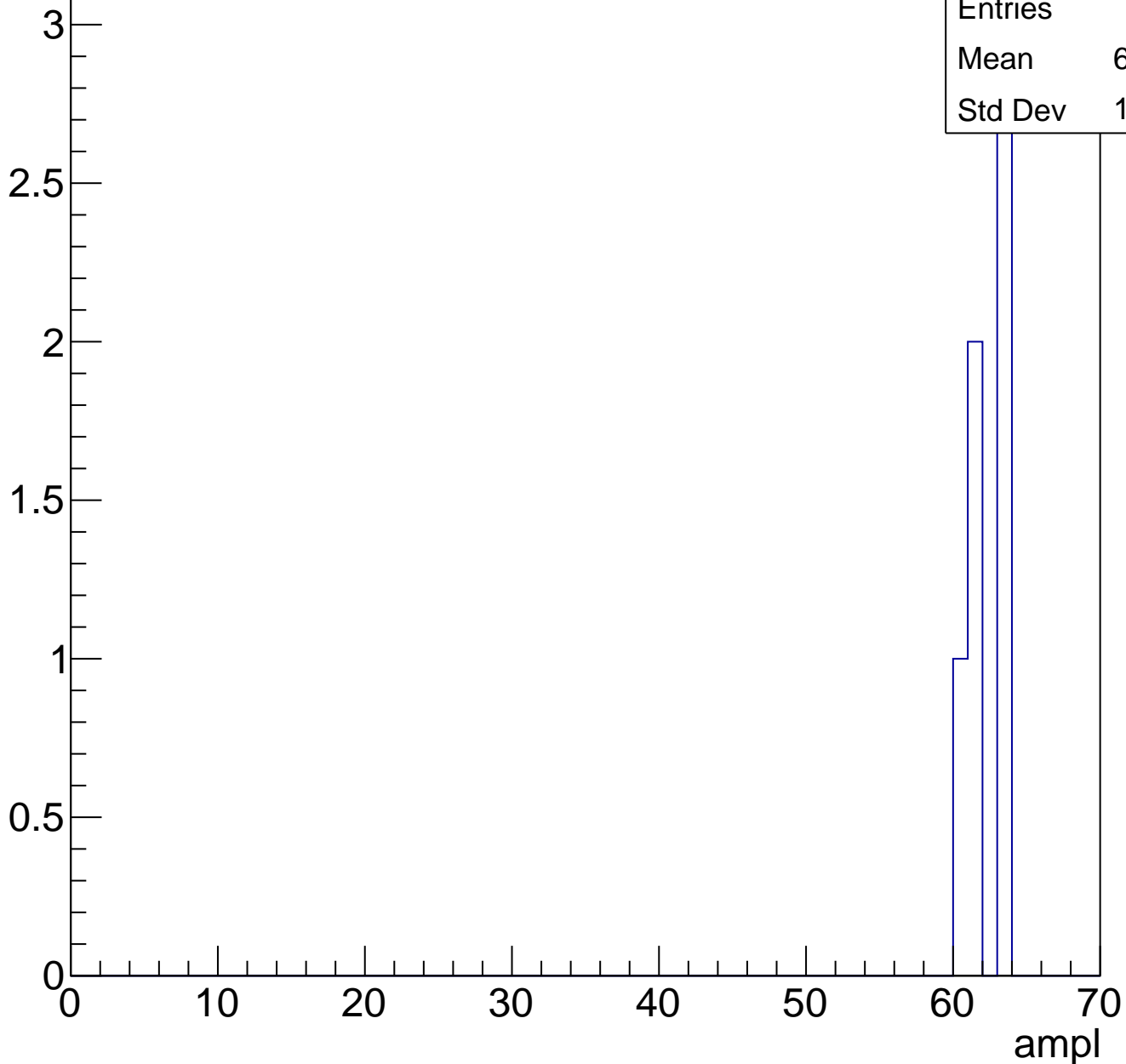
ampl



B0L101S, U21-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch80, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	27.44
Std Dev	3.906

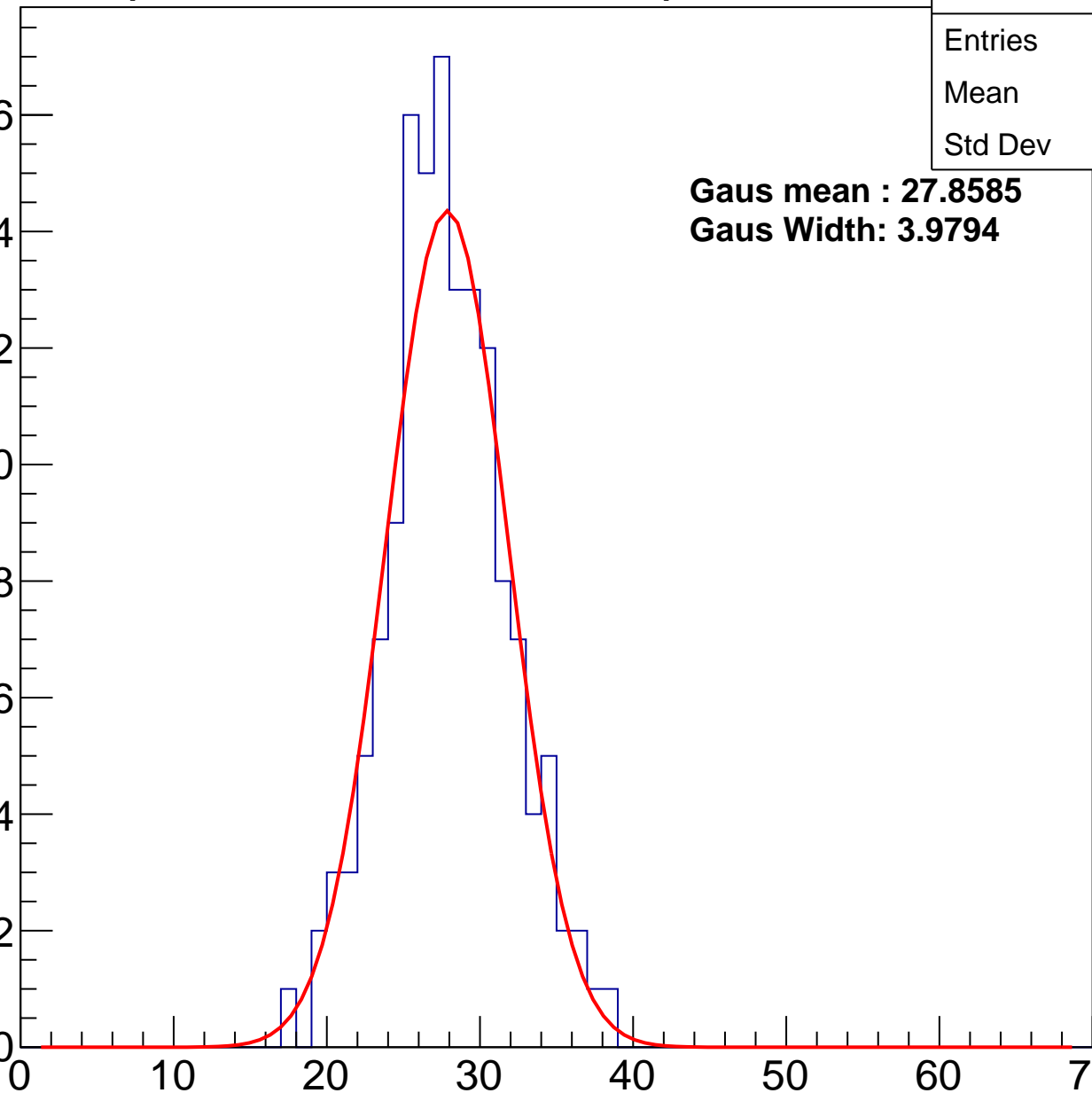
Gaus mean : 27.8585

Gaus Width: 3.9794

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch80, adc1

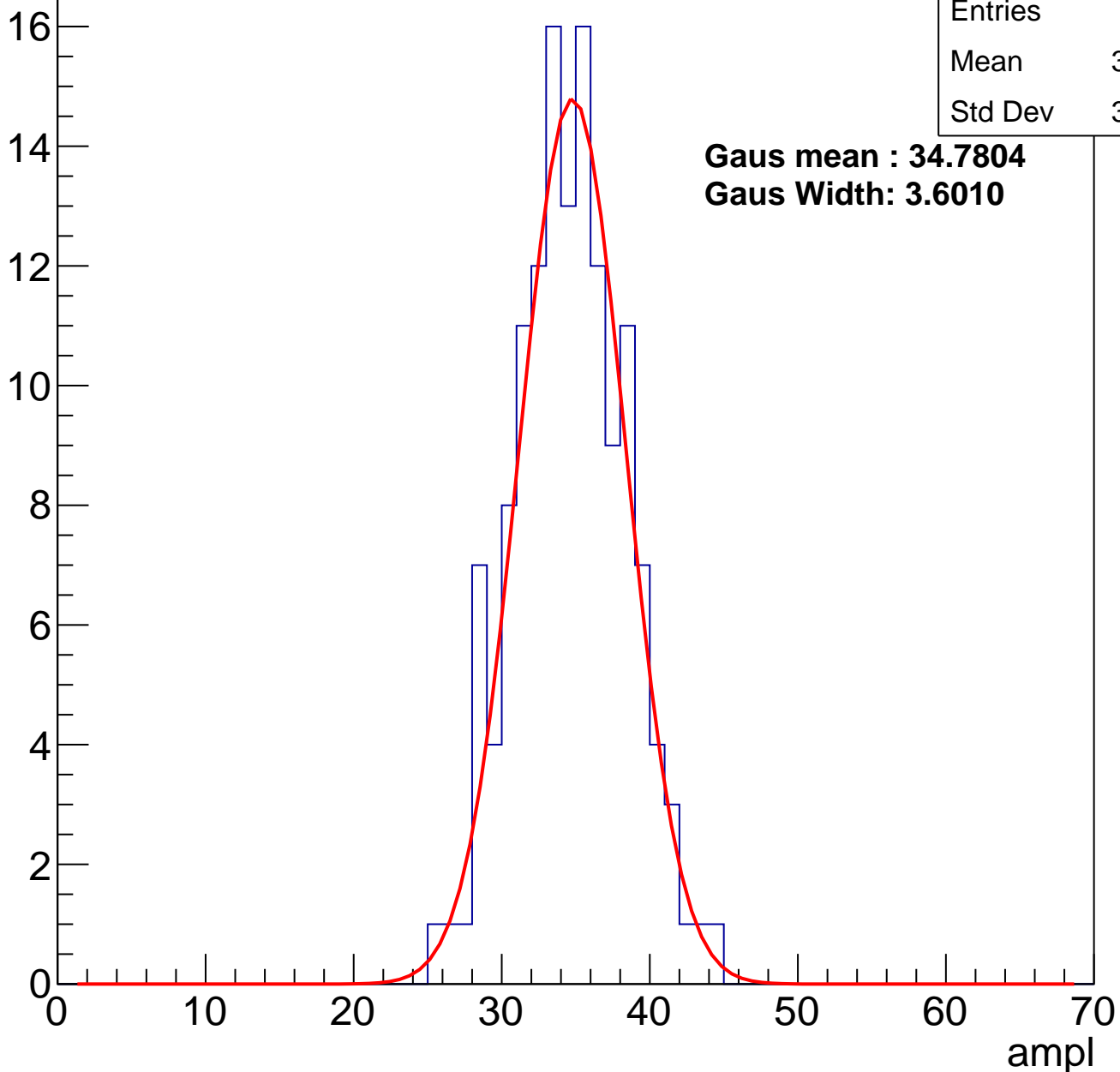
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	34.19
Std Dev	3.662

Gaus mean : 34.7804

Gaus Width: 3.6010

Entry



B0L101S, U21-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	40.91
Std Dev	3.434

Gaus mean : 41.1194

Gaus Width: 3.2182

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U21-ch80, adc3

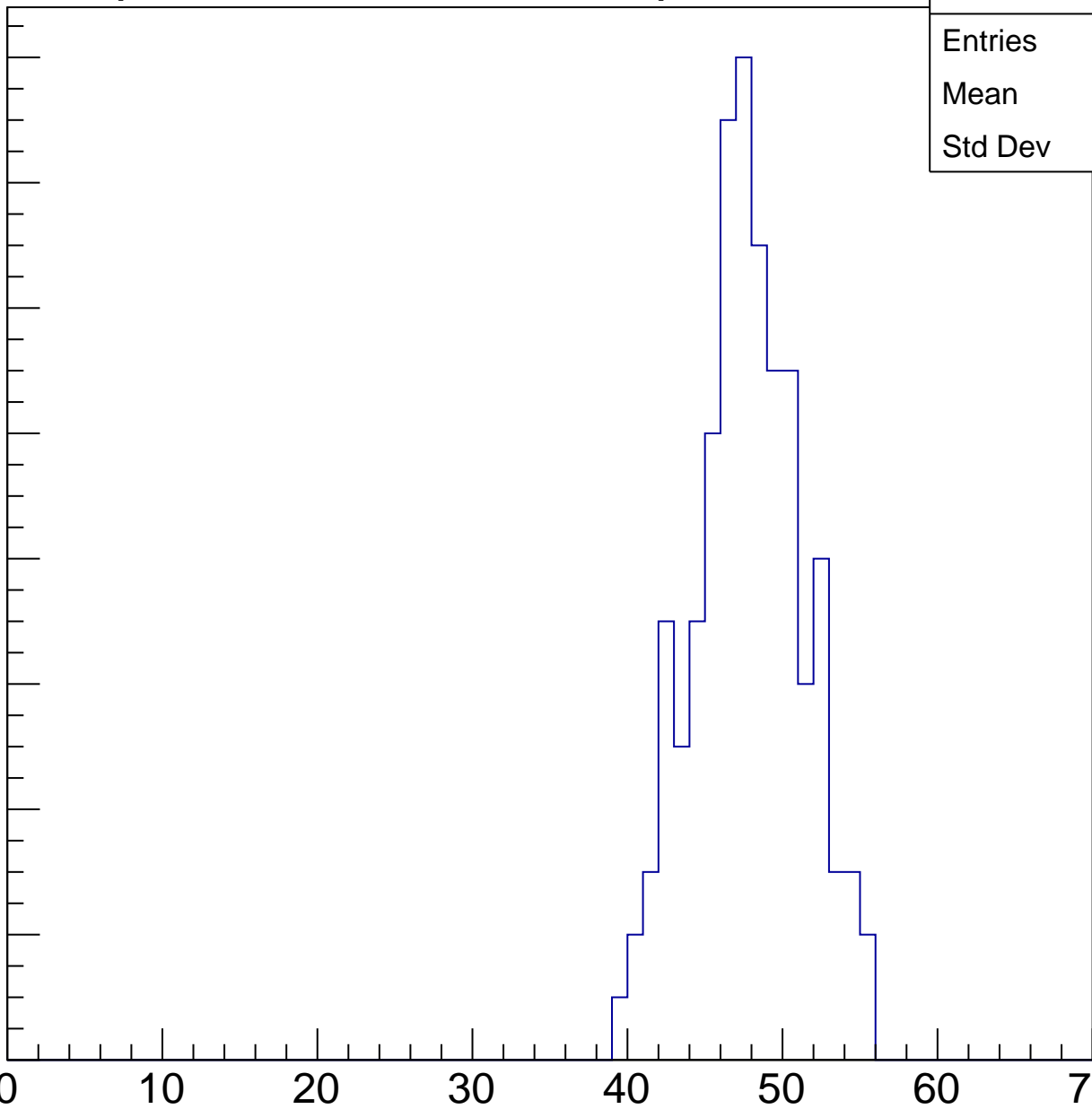
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	123
Mean	47.29
Std Dev	3.454

ampl



B0L101S, U21-ch80, adc4

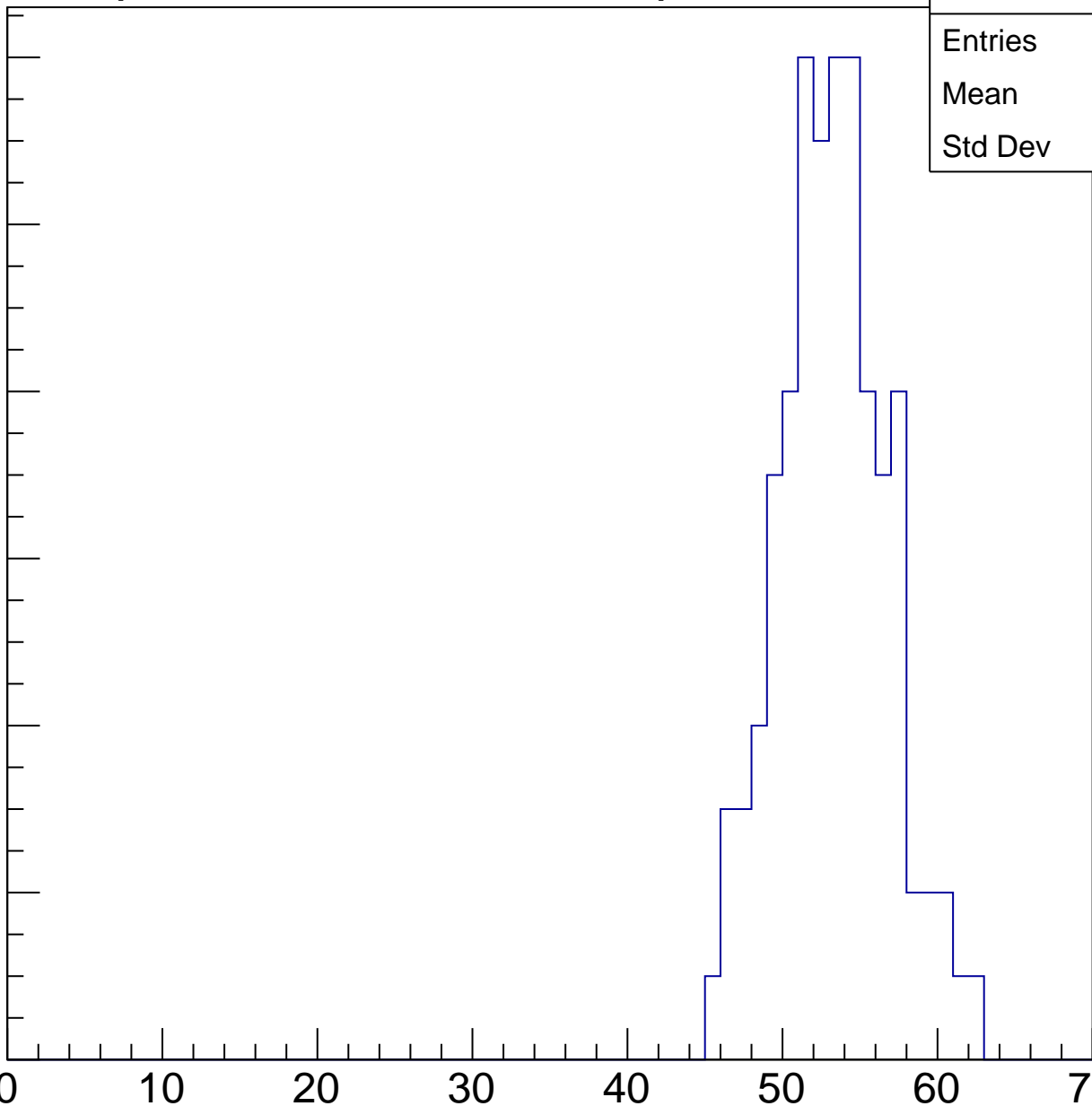
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	104
Mean	52.81
Std Dev	3.478

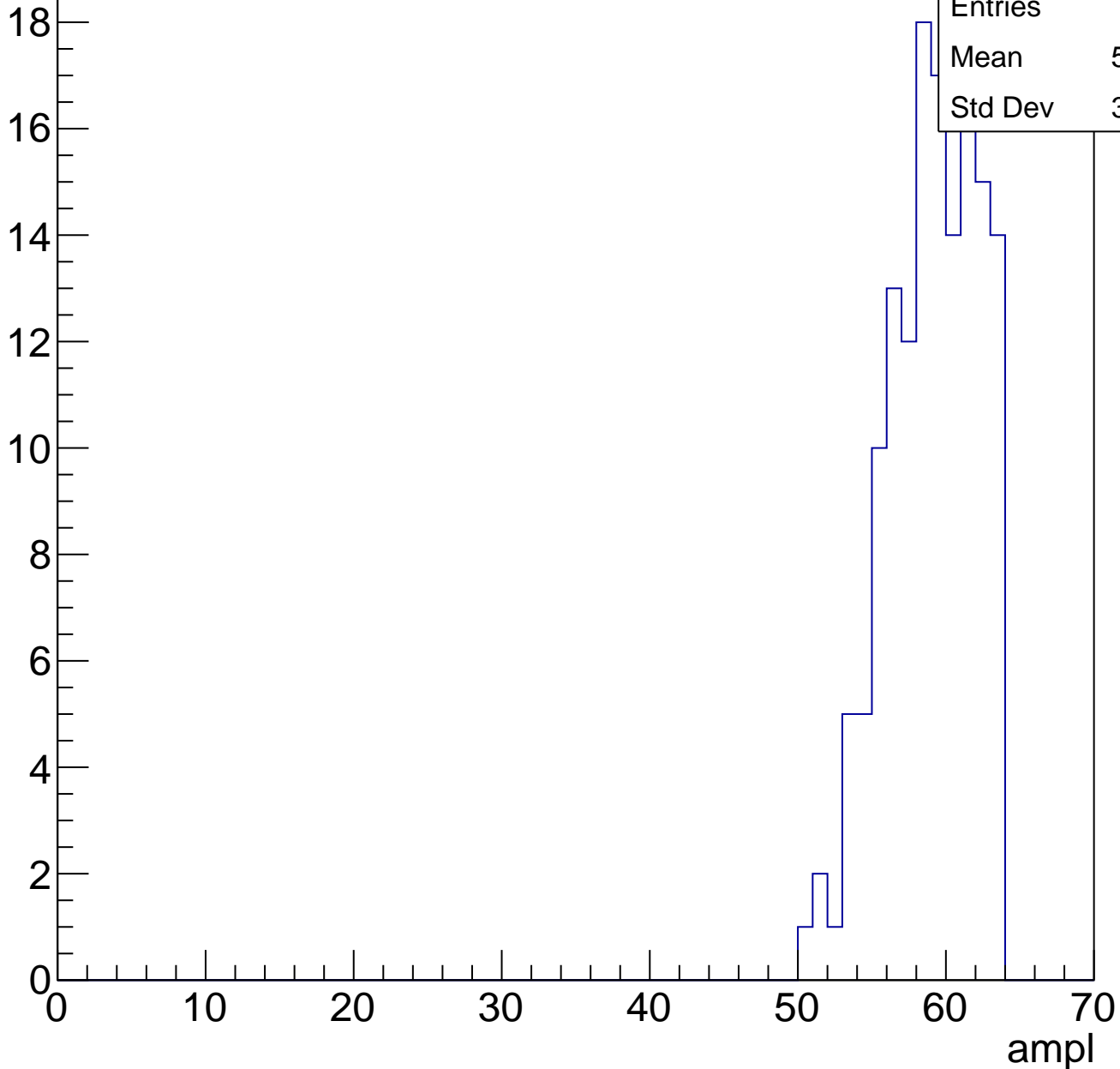
ampl



B0L101S, U21-ch80, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

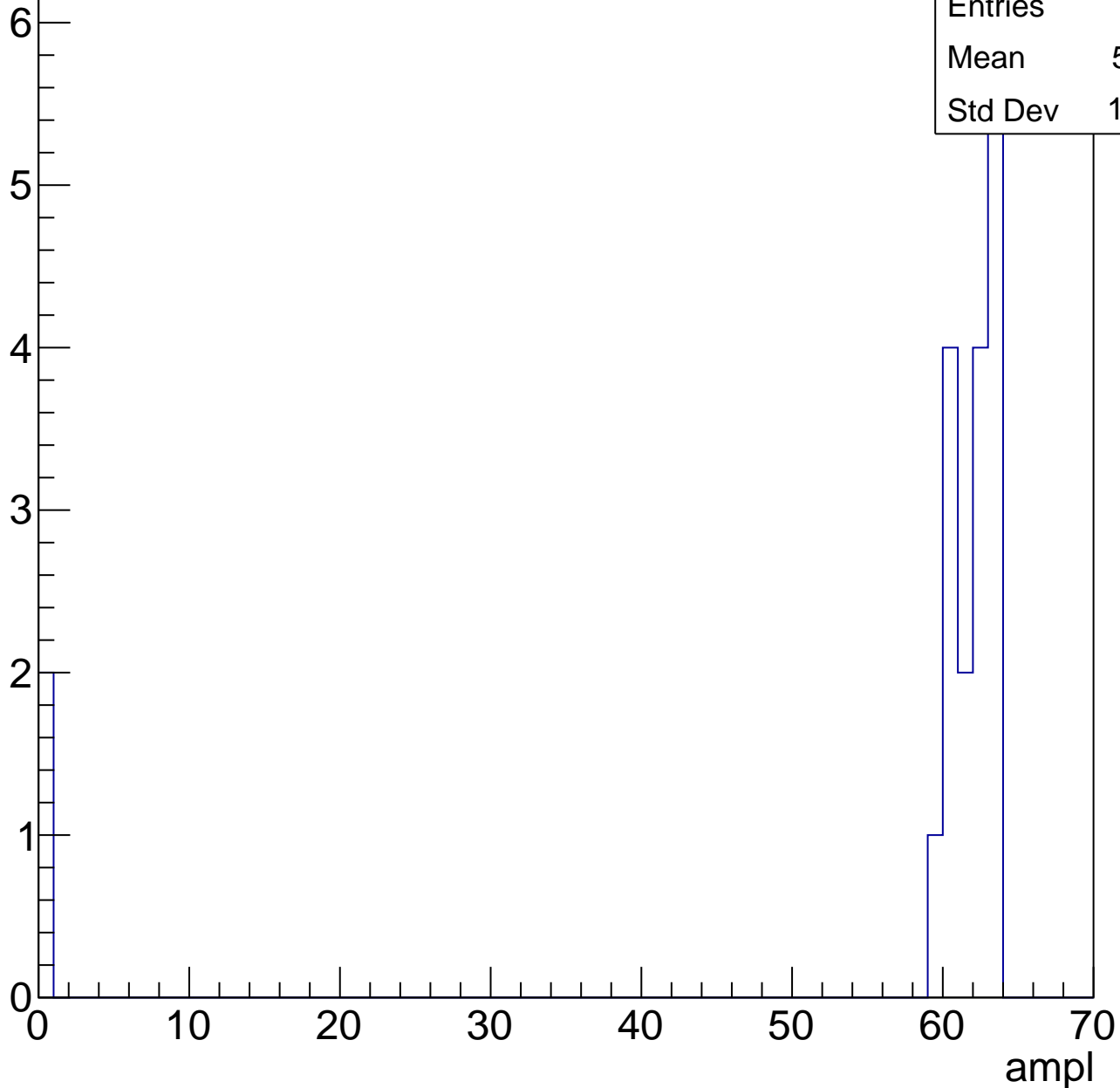


B0L101S, U21-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

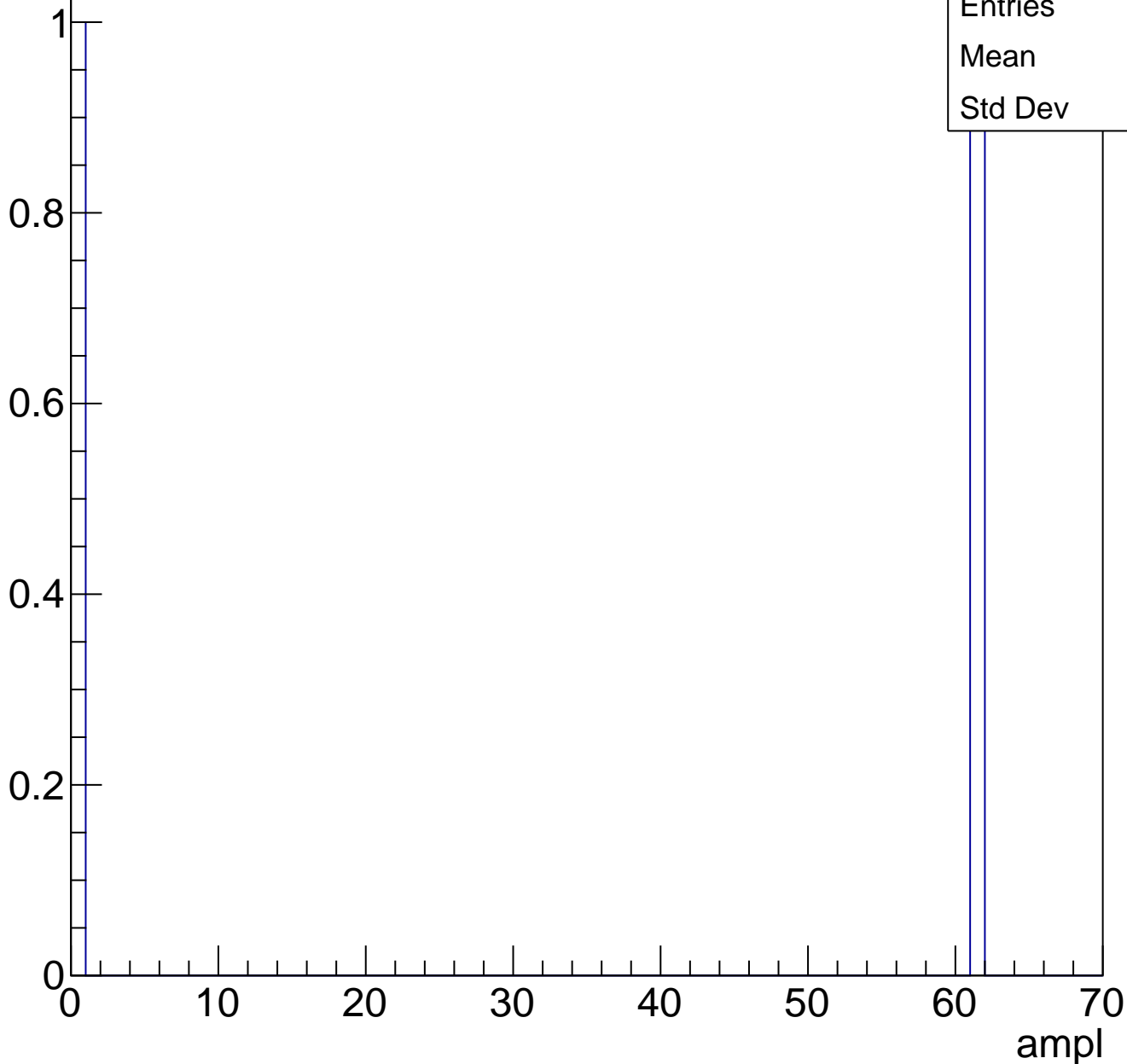
Entries	19
Mean	55.11
Std Dev	18.94



B0L101S, U21-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch81, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.18
Std Dev	3.462

Gaus mean : 30.0387

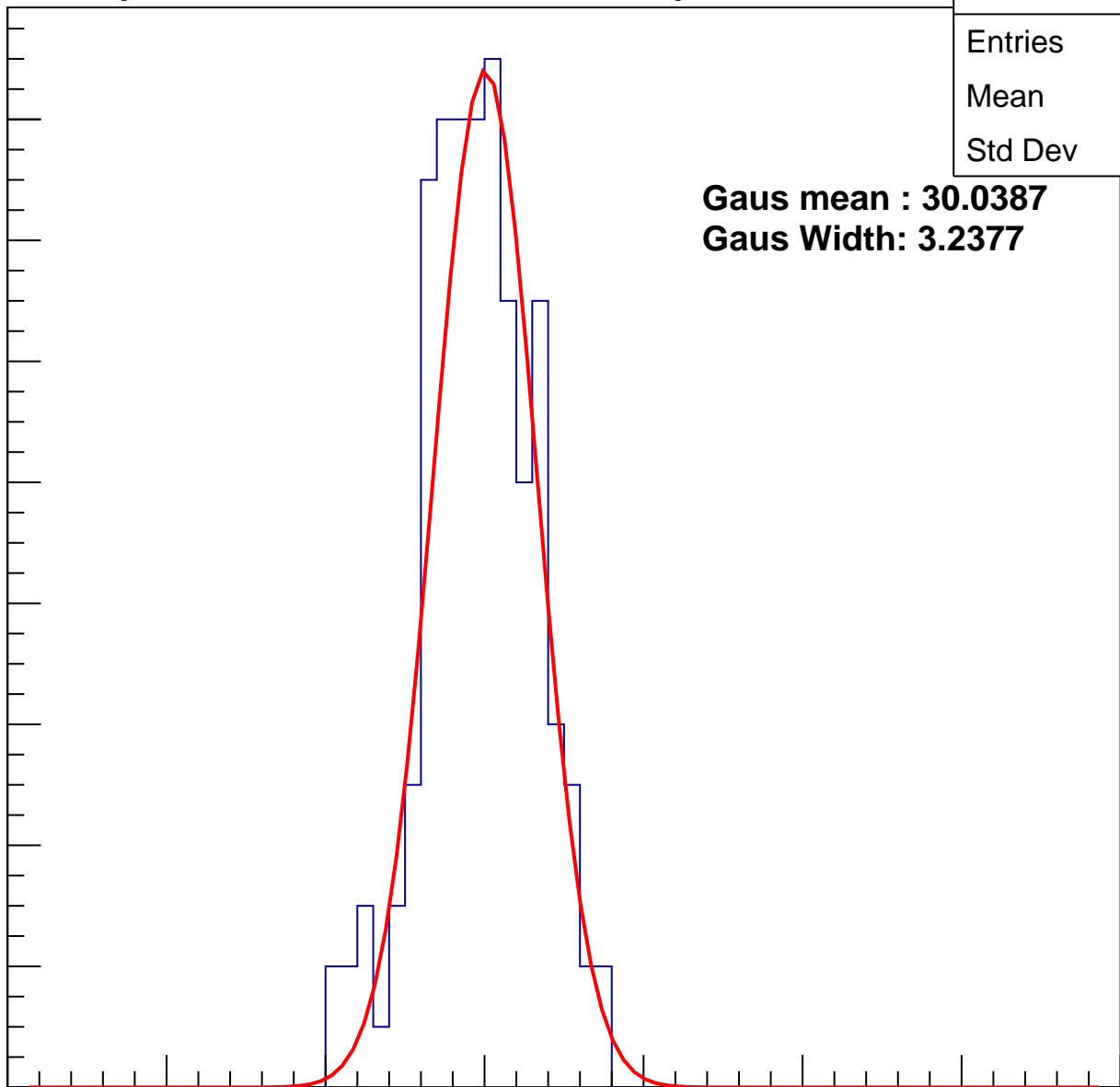
Gaus Width: 3.2377

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch81, adc1

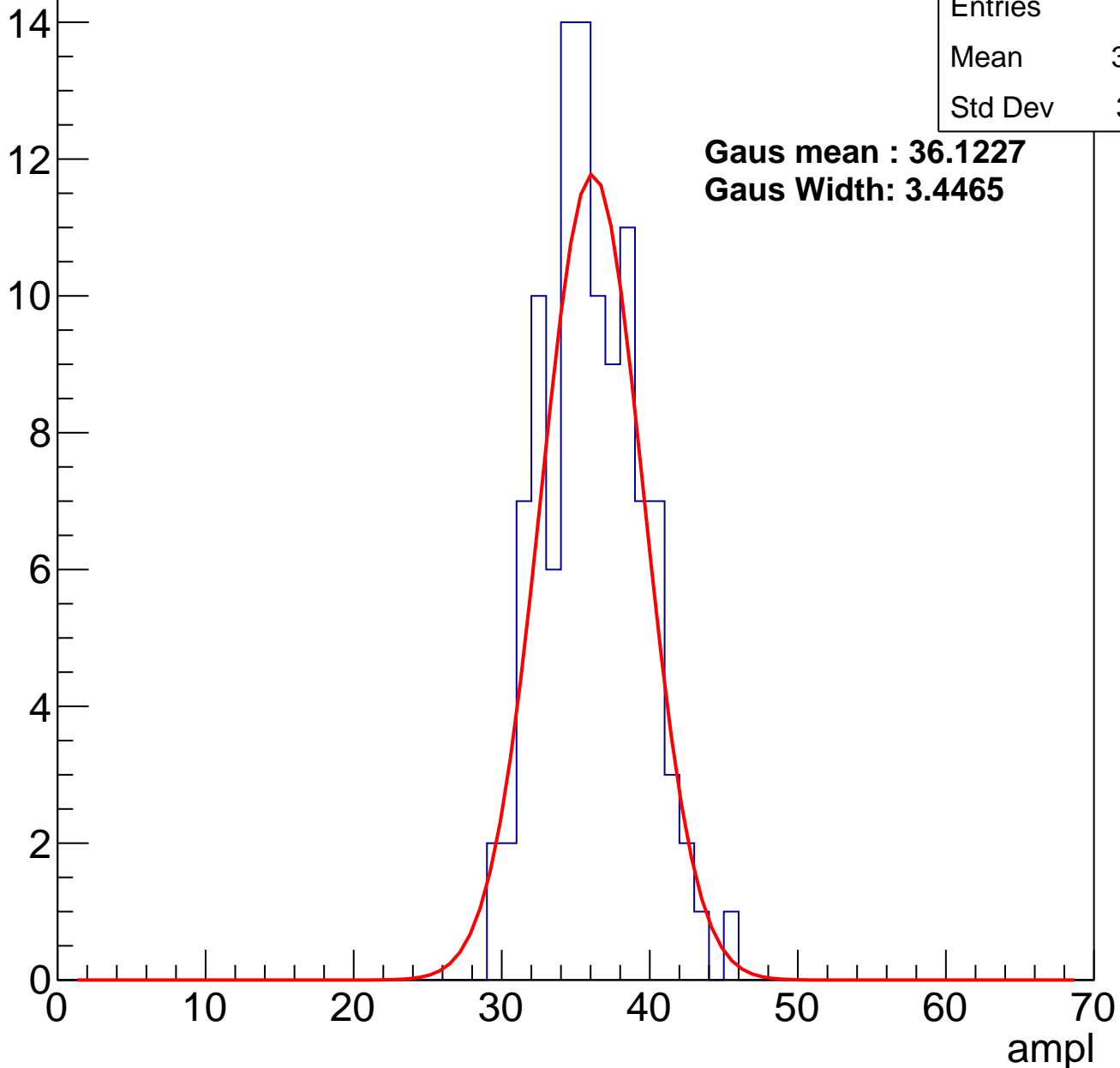
calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	35.64
Std Dev	3.251

Gaus mean : 36.1227

Gaus Width: 3.4465

Entry



B0L101S, U21-ch81, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	42.22
Std Dev	3.639

Gaus mean : 42.7917

Gaus Width: 3.6722

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

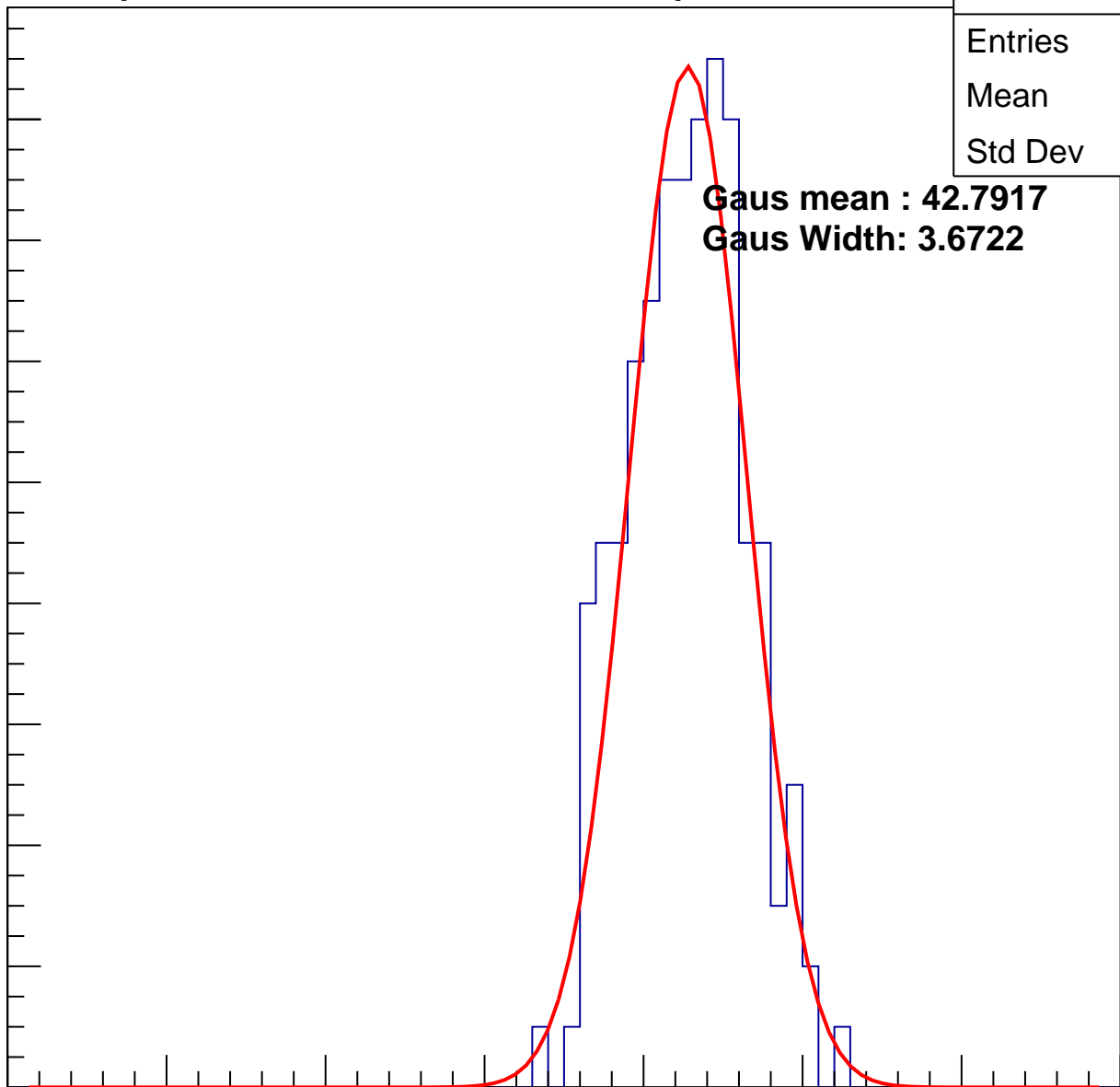
40

50

60

70

ampl



B0L101S, U21-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

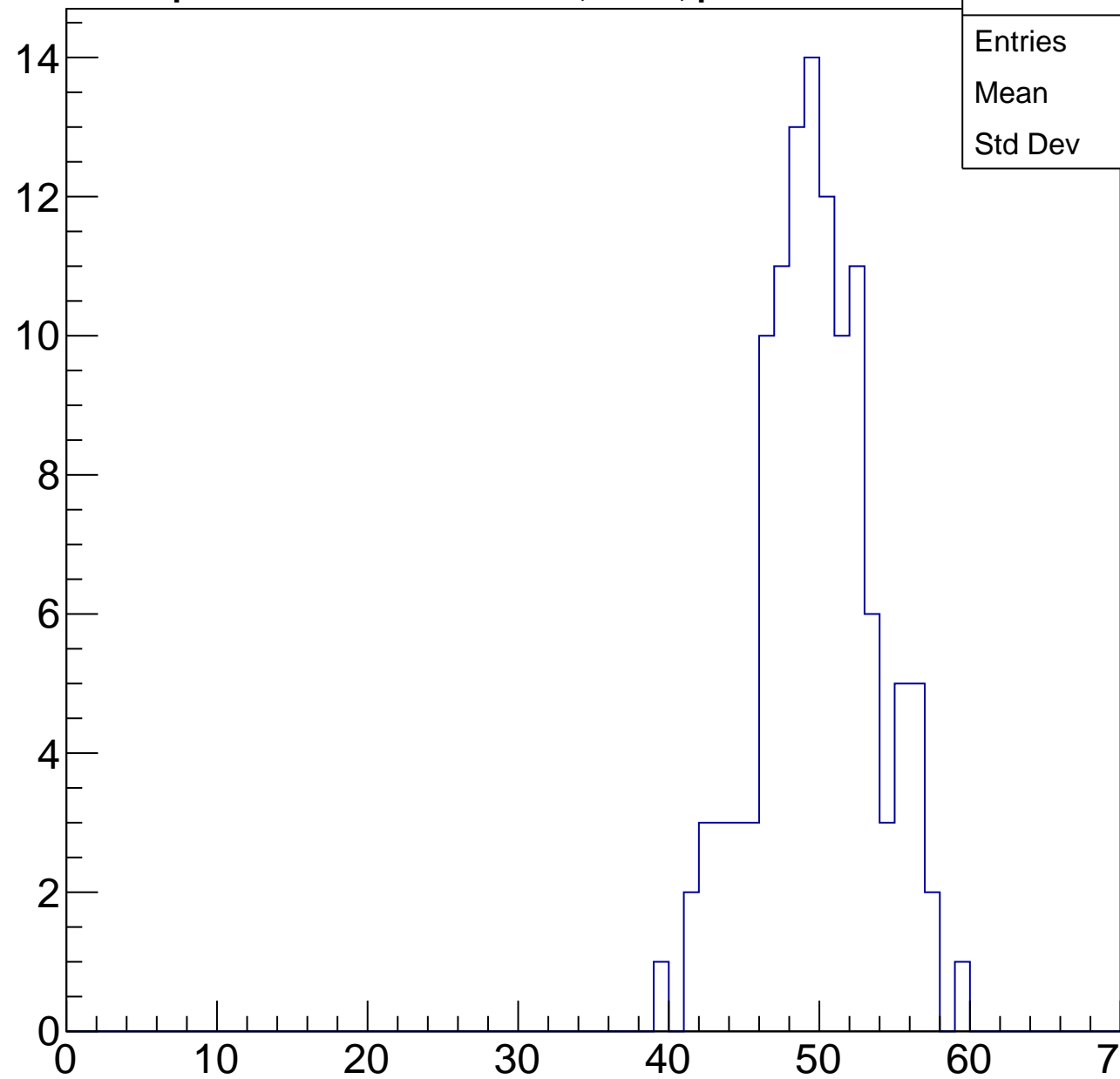
Entries	118
Mean	49.32
Std Dev	3.813

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch81, adc4

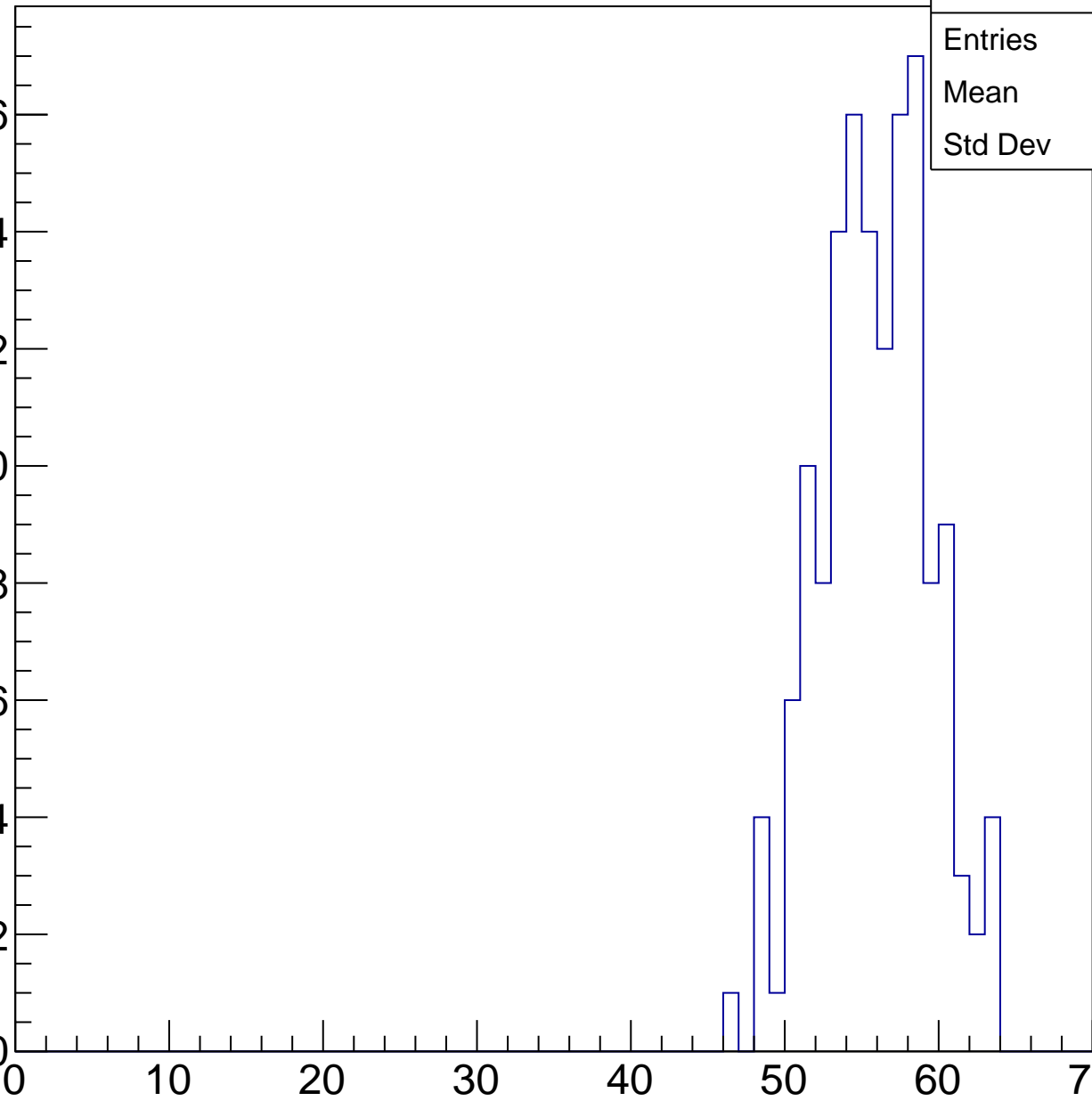
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	55.38
Std Dev	3.514

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch81, adc5

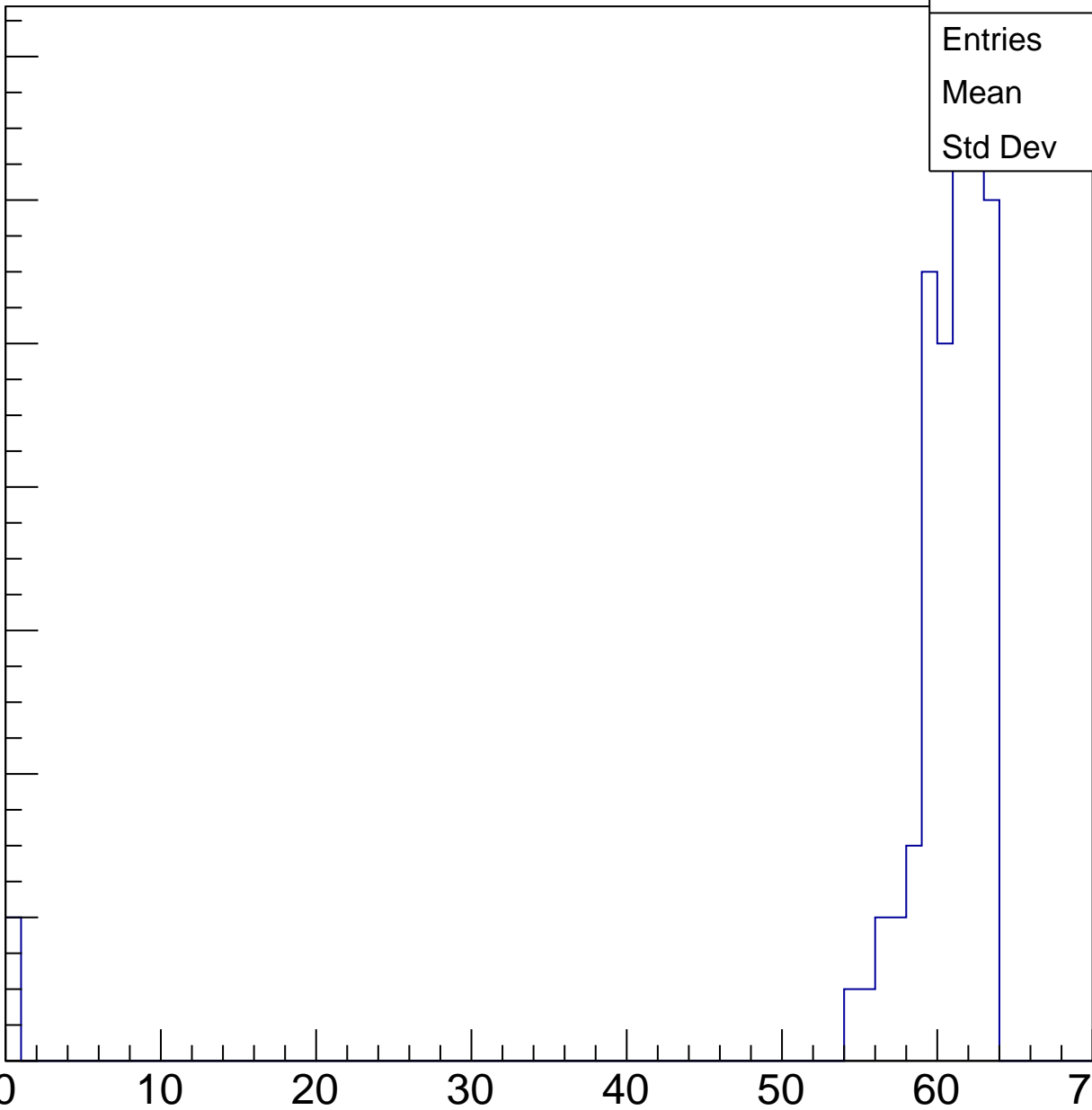
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.8
Std Dev	10.22

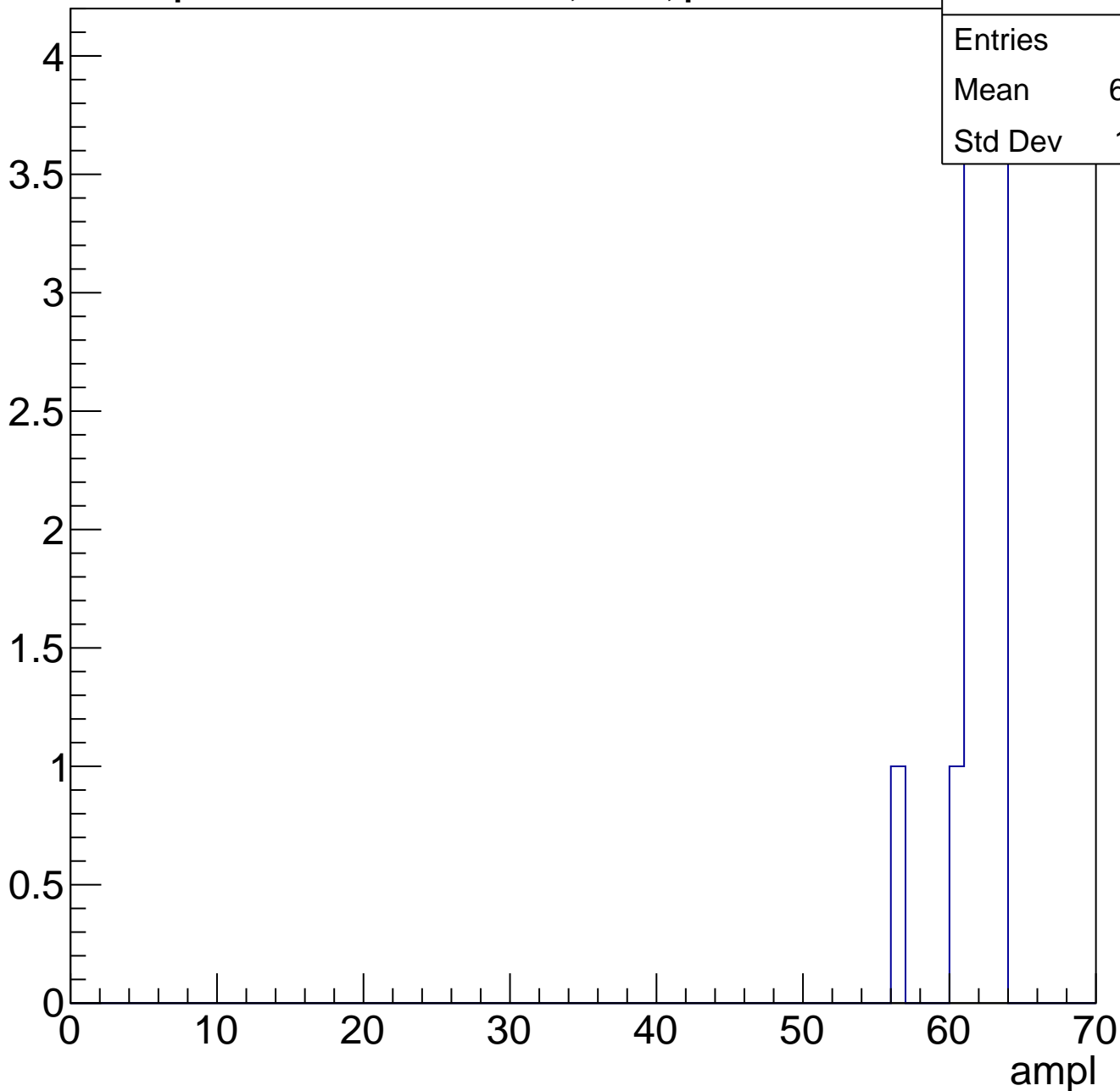
ampl



B0L101S, U21-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch82, adc0

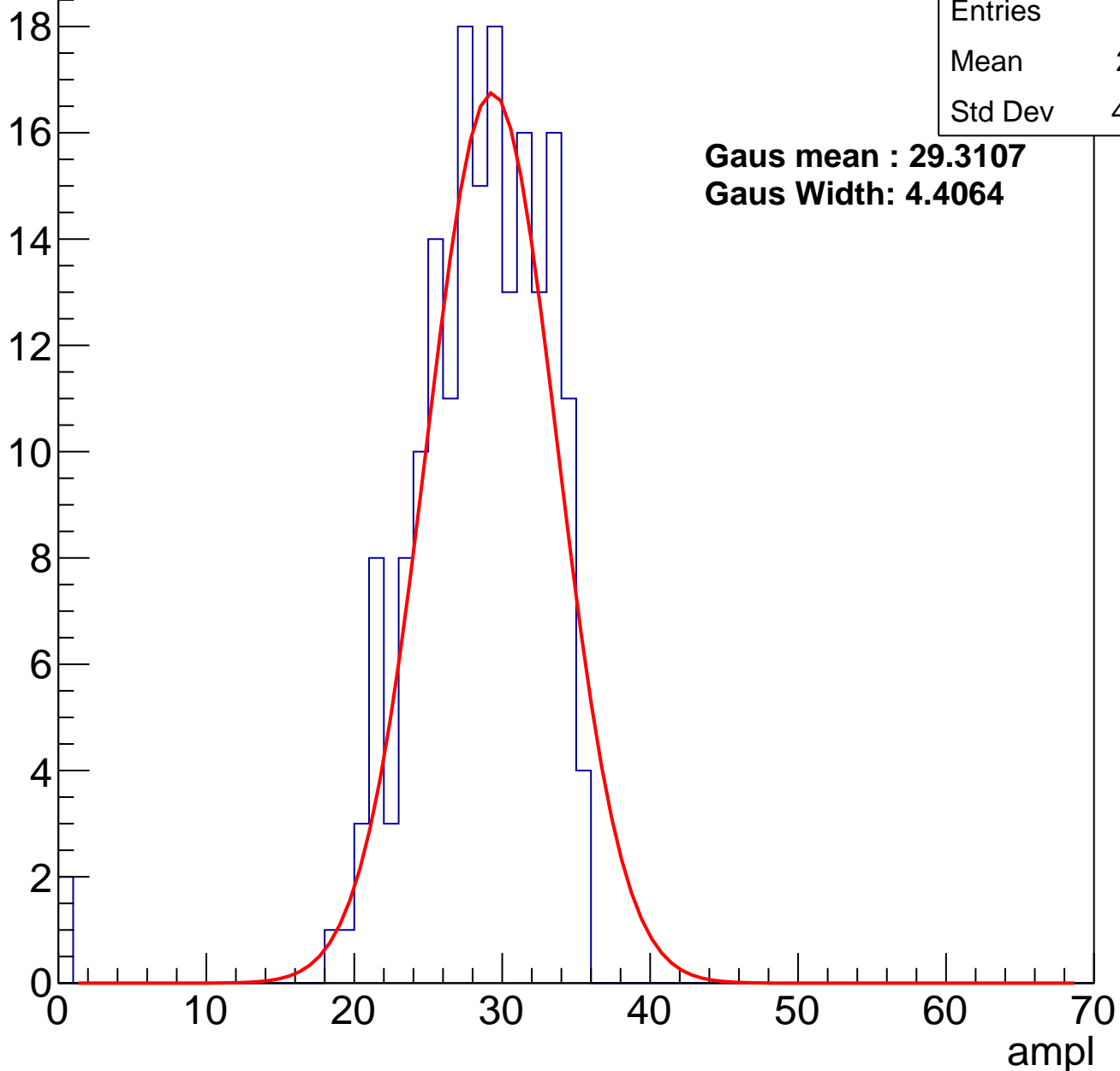
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	27.91
Std Dev	4.878

Gaus mean : 29.3107

Gaus Width: 4.4064

Entry



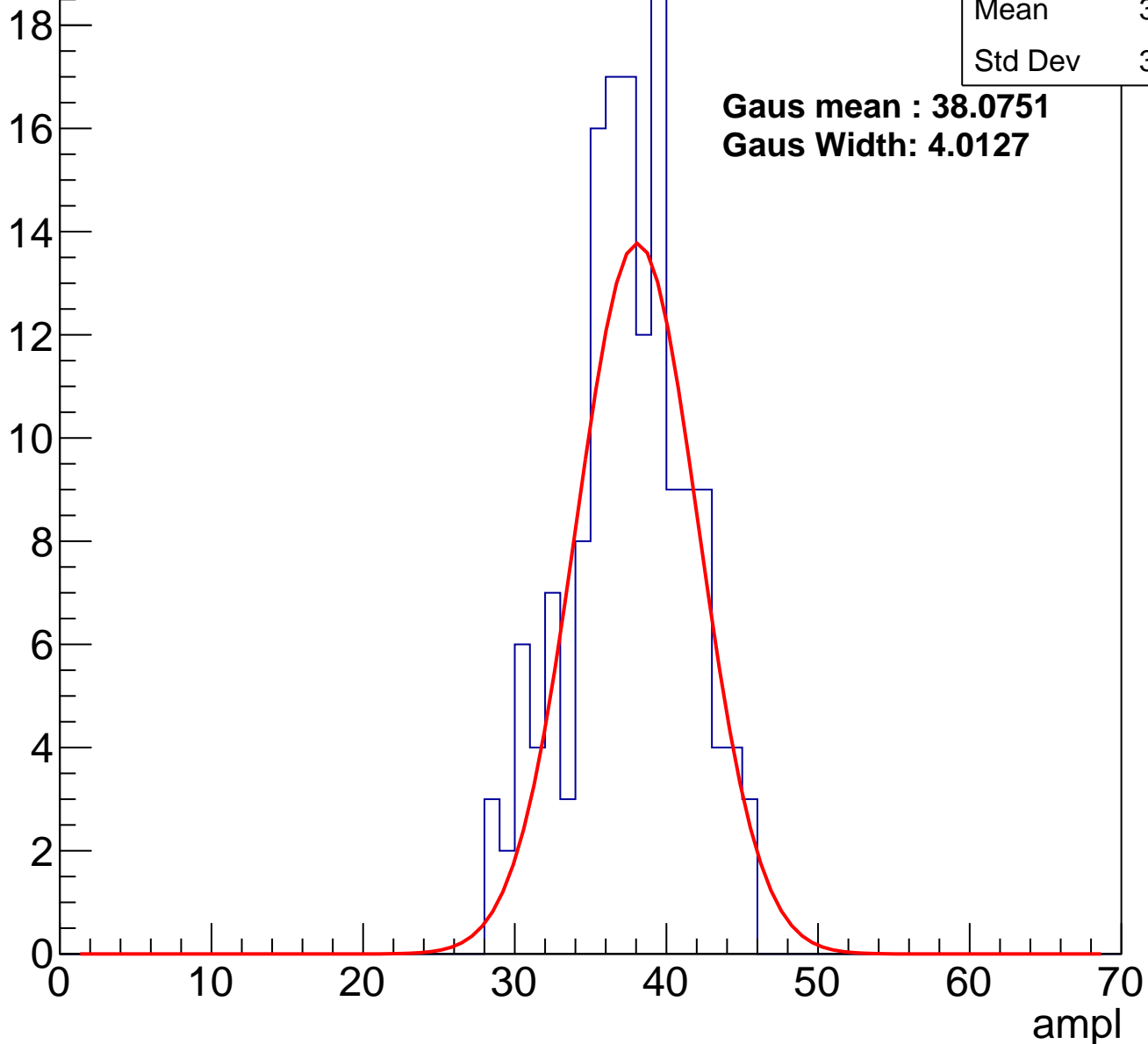
B0L101S, U21-ch82, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	37.03
Std Dev	3.852

Gaus mean : 38.0751
Gaus Width: 4.0127

Entry



B0L101S, U21-ch82, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

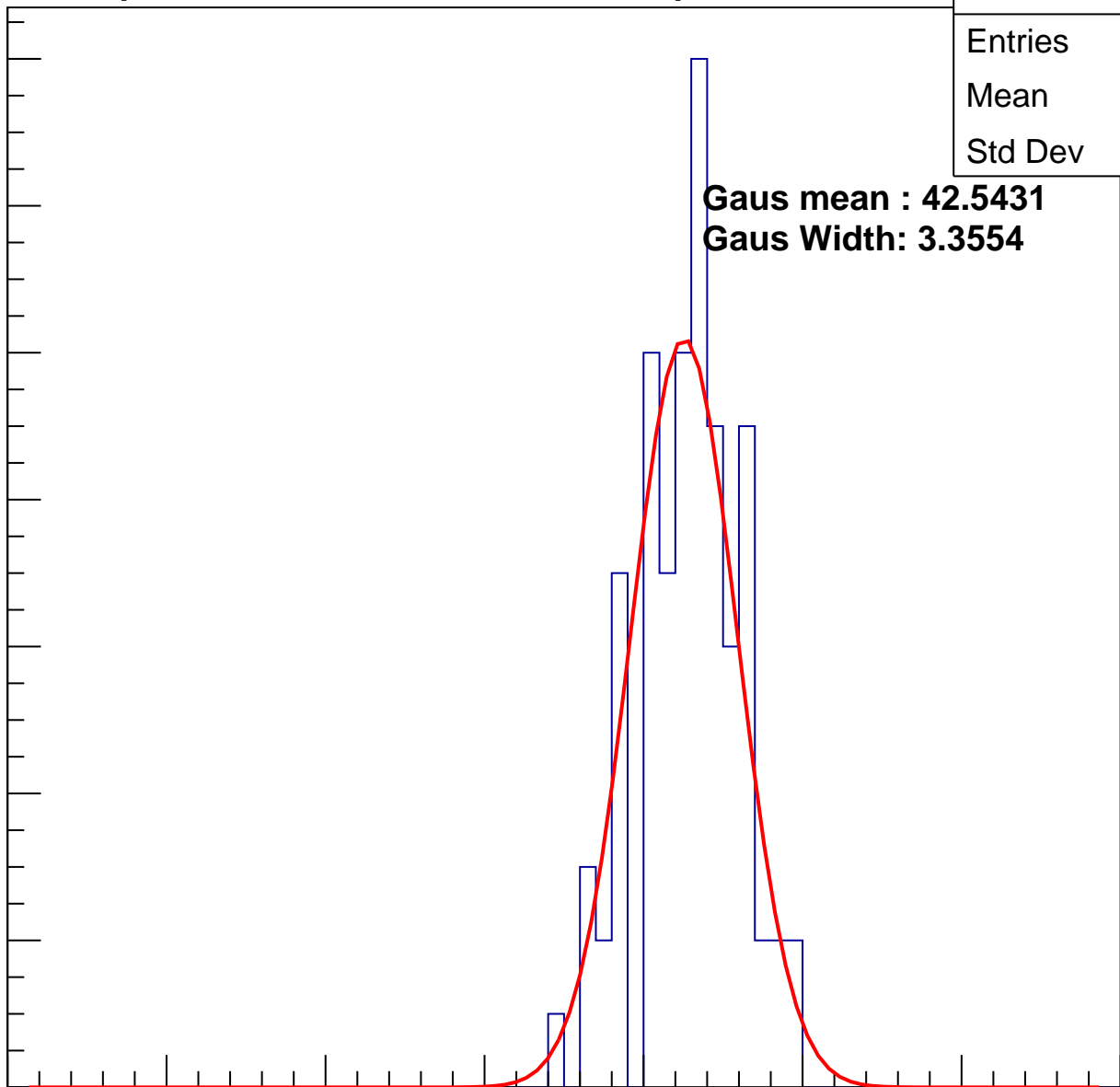
14
12
10
8
6
4
2
0

Entries	84
Mean	42.37
Std Dev	3.158

Gaus mean : 42.5431
Gaus Width: 3.3554

0 10 20 30 40 50 60 70

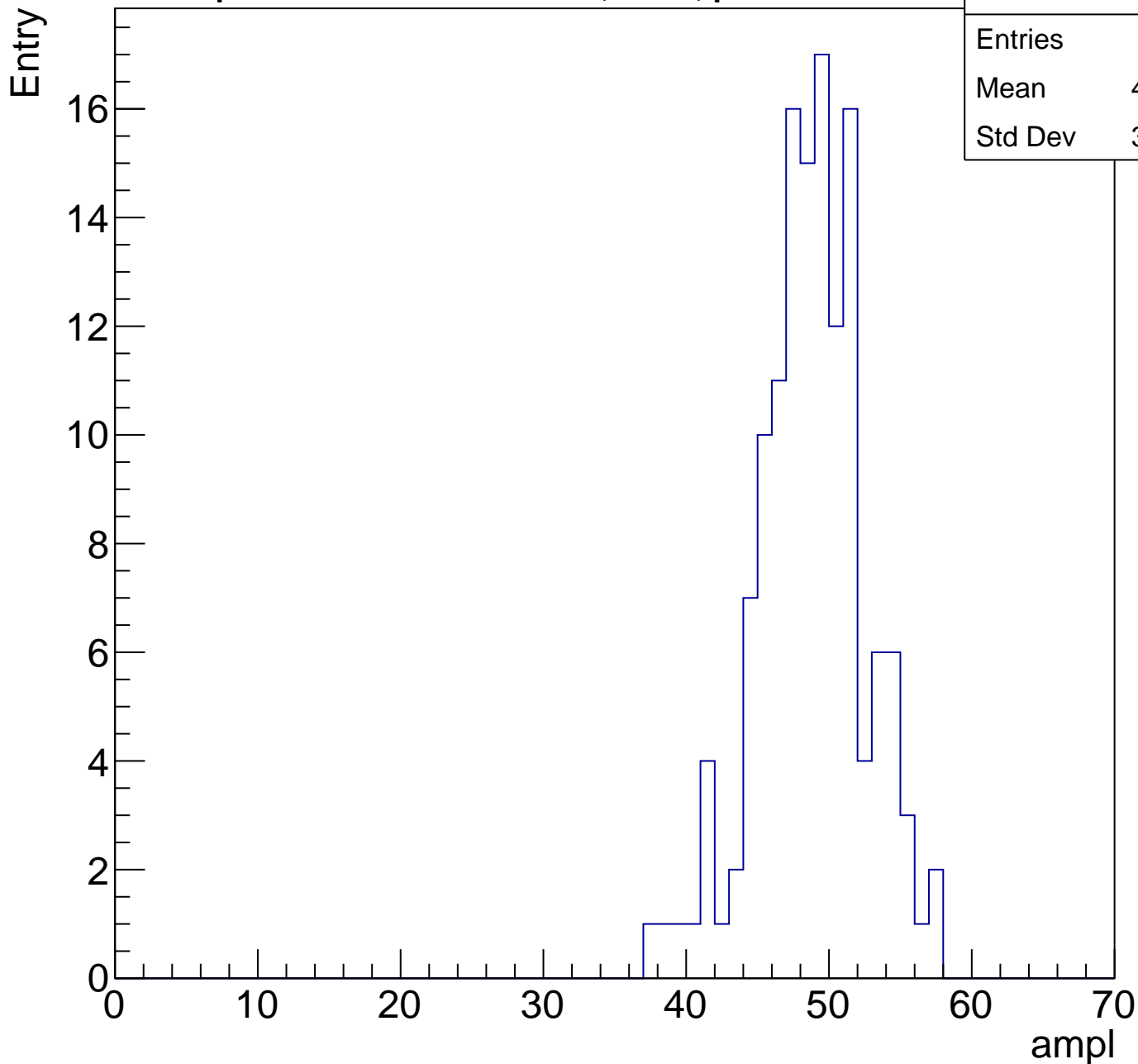
ampl



B0L101S, U21-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	48.29
Std Dev	3.734

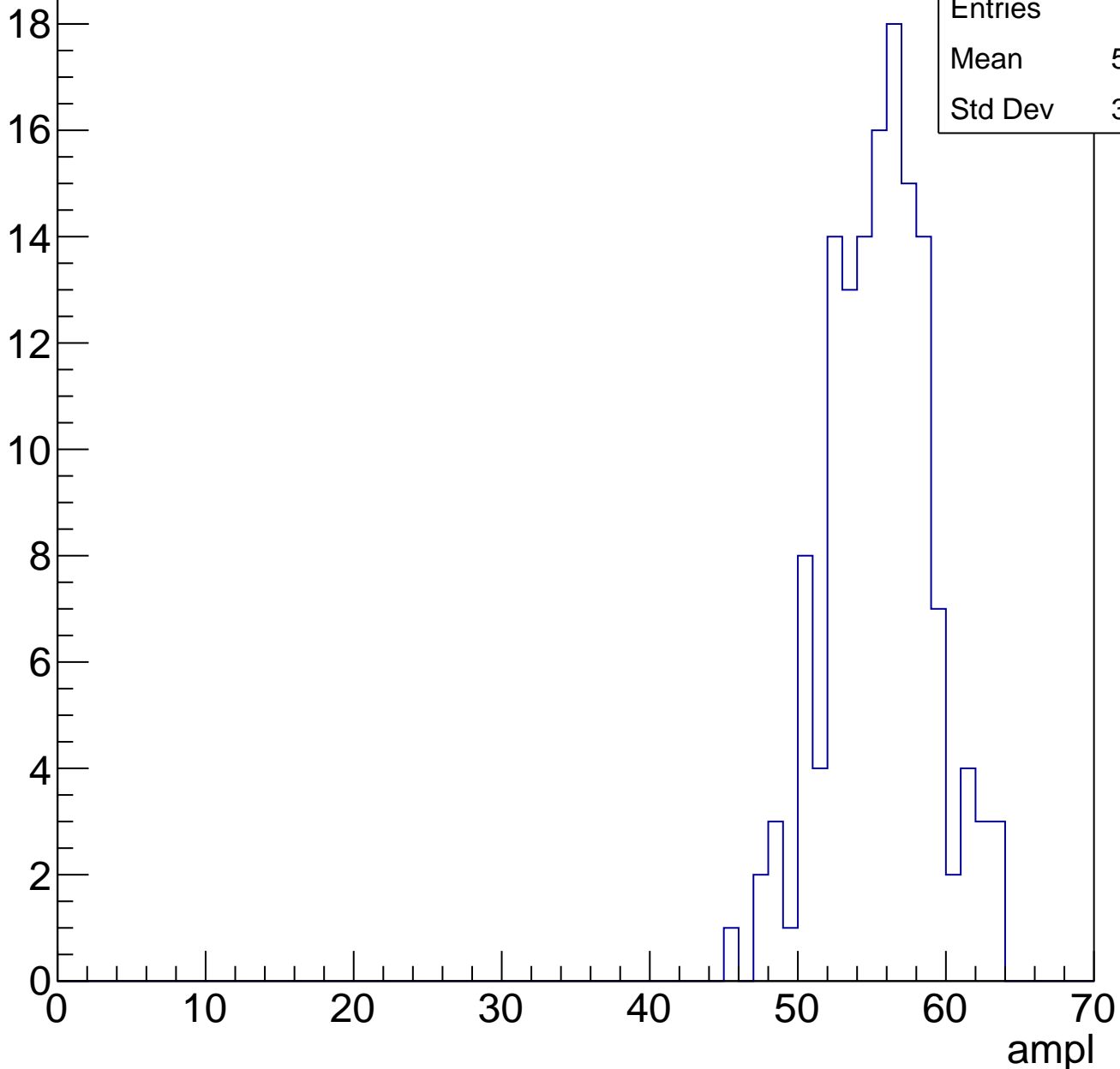


B0L101S, U21-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	55.04
Std Dev	3.476

Entry



B0L101S, U21-ch82, adc5

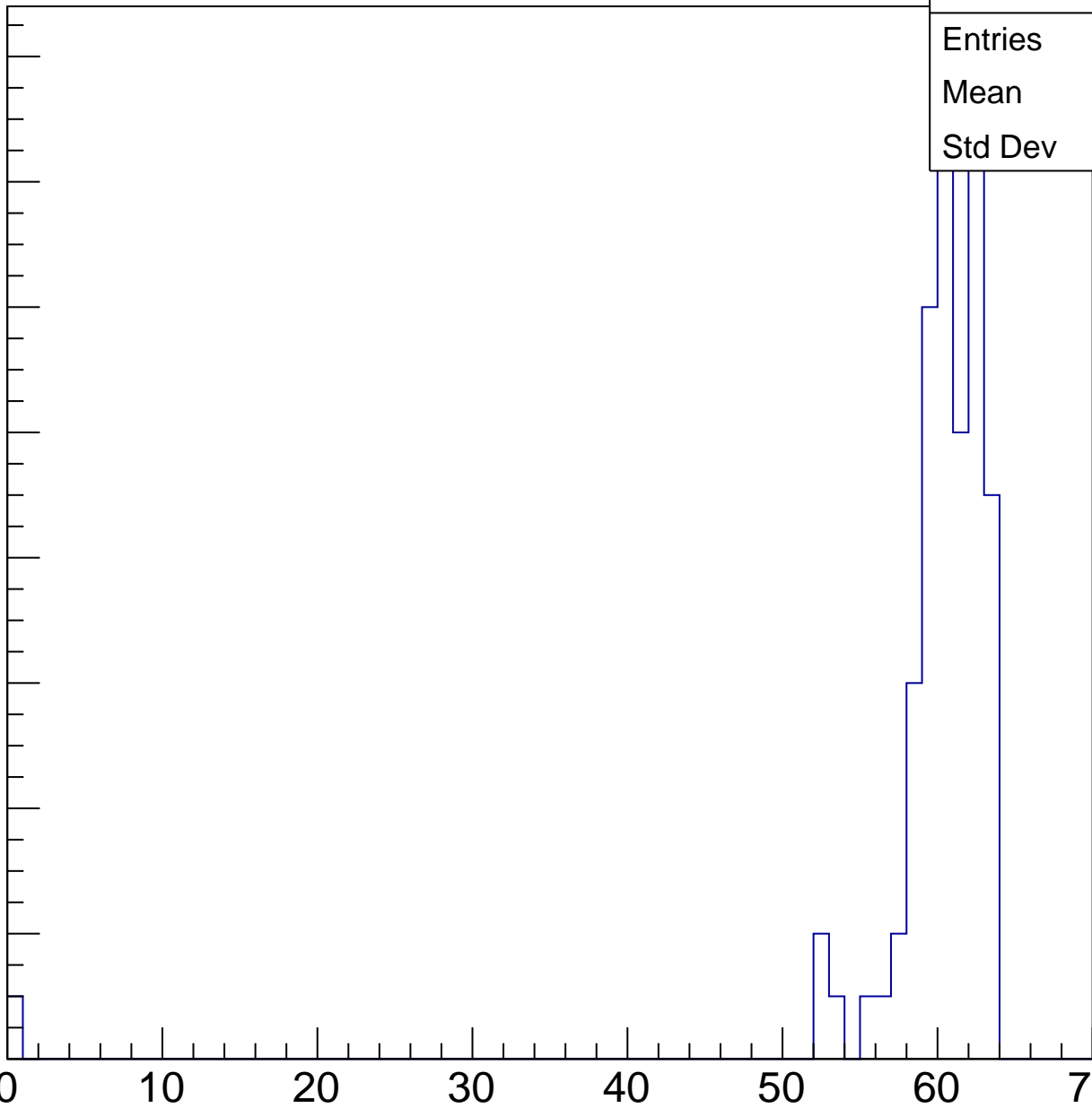
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.28
Std Dev	7.24

ampl

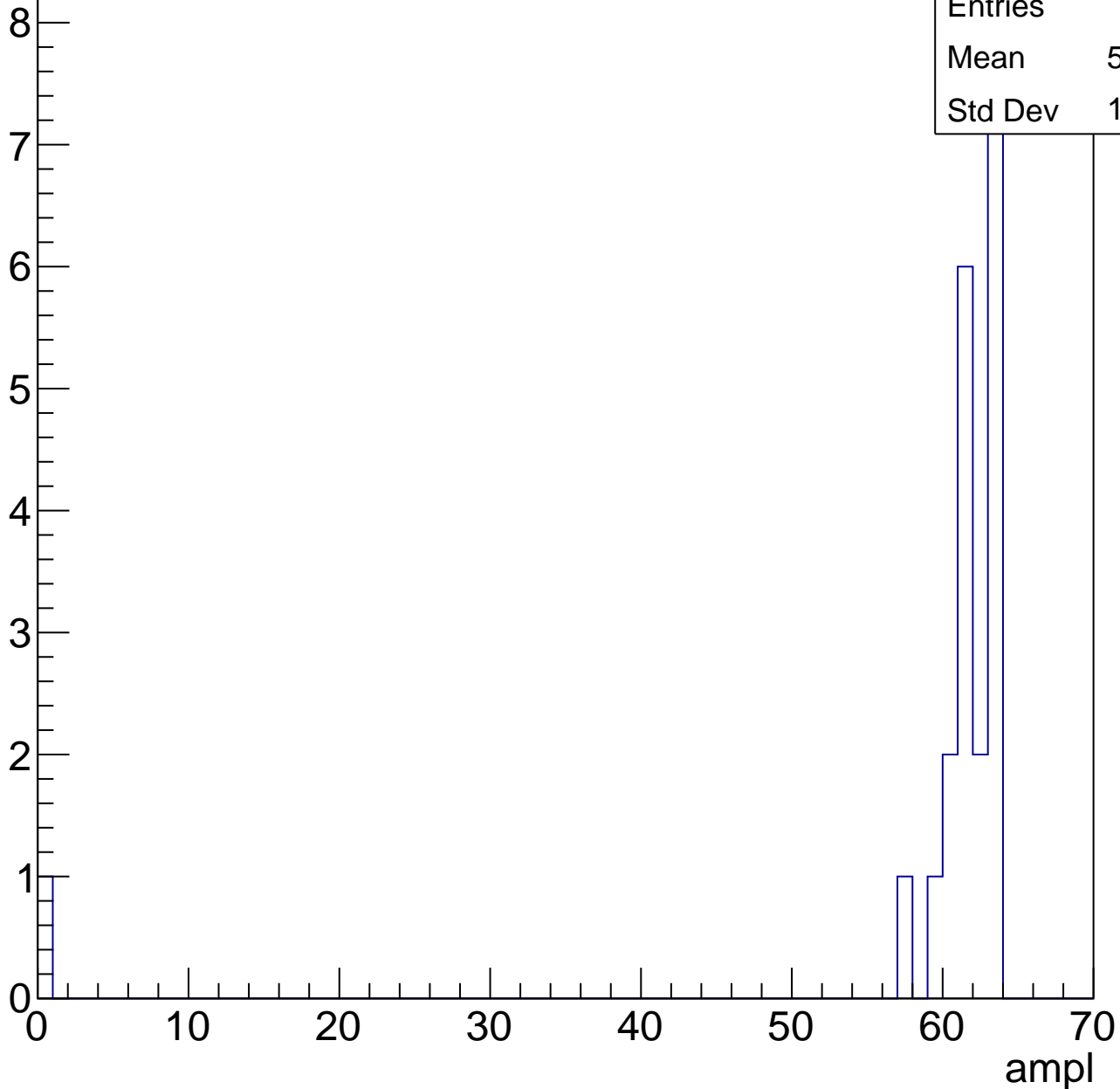


B0L101S, U21-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	58.57
Std Dev	13.19



B0L101S, U21-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch83, adc0

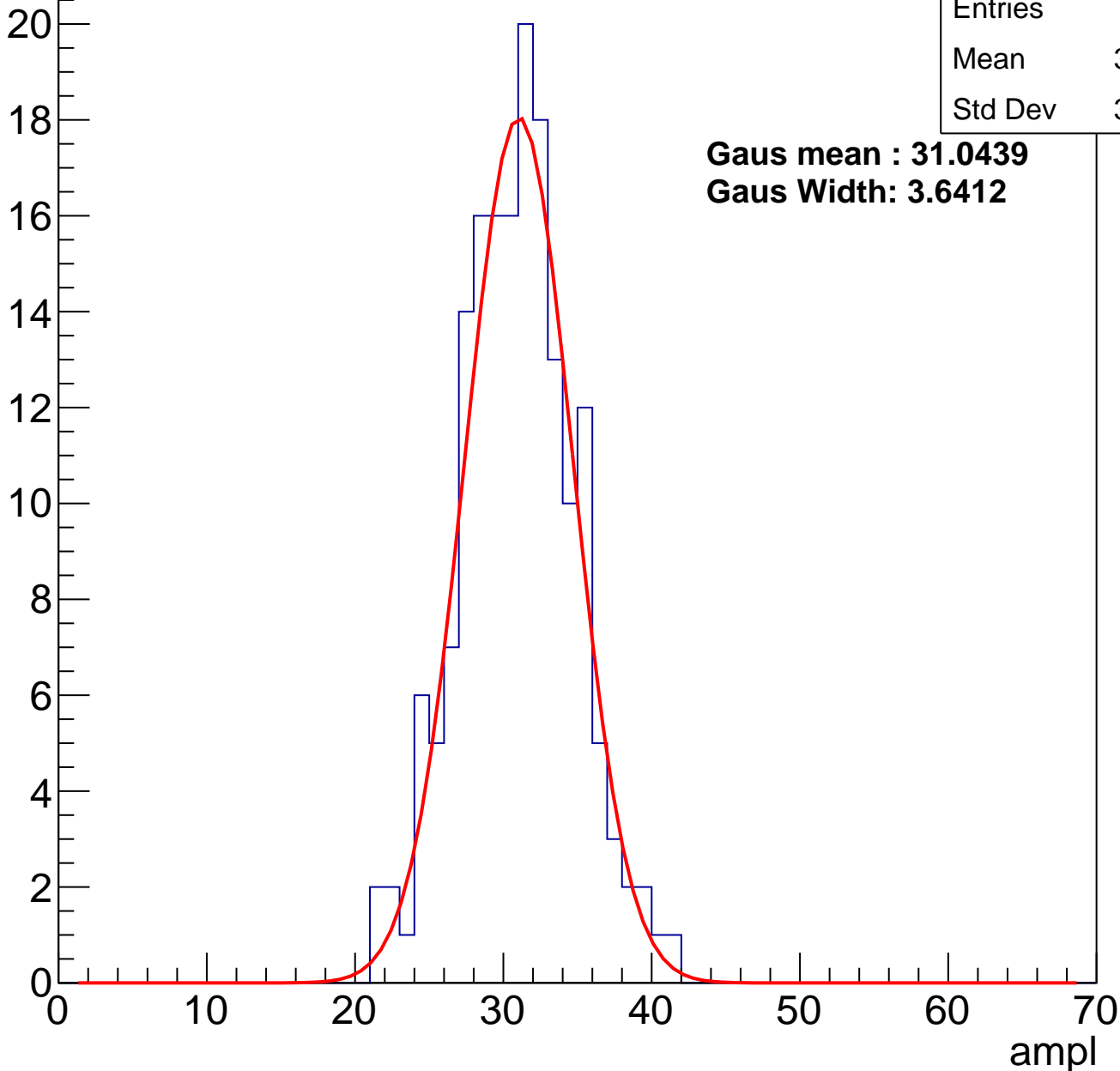
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	30.47
Std Dev	3.787

Gaus mean : 31.0439

Gaus Width: 3.6412

Entry

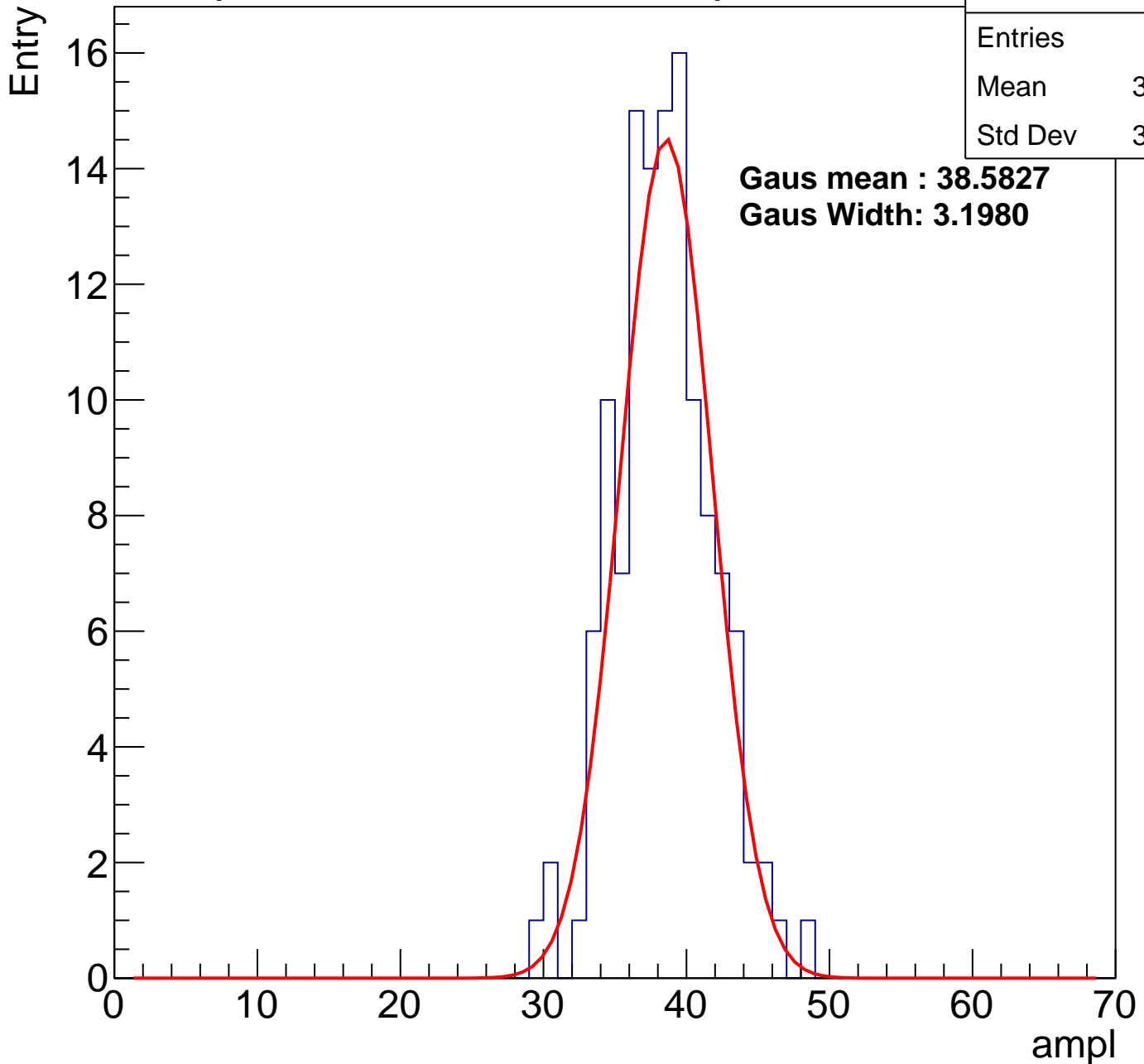


B0L101S, U21-ch83, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	37.97
Std Dev	3.379

Gaus mean : 38.5827
Gaus Width: 3.1980



B0L101S, U21-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

143

Mean

44.43

Std Dev

3.631

Gaus mean : 45.0078

Gaus Width: 3.3208

0

10

20

30

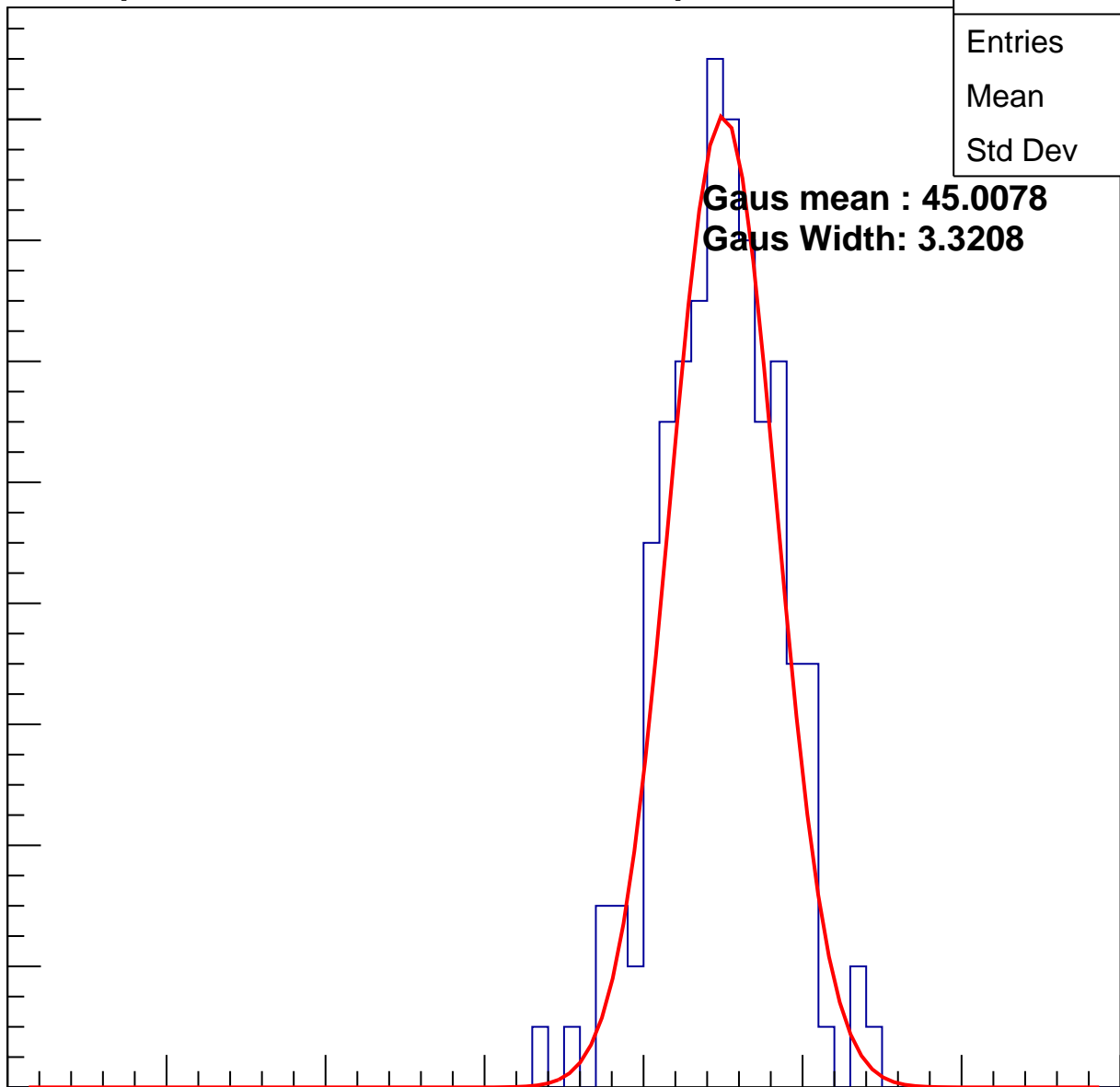
40

50

60

70

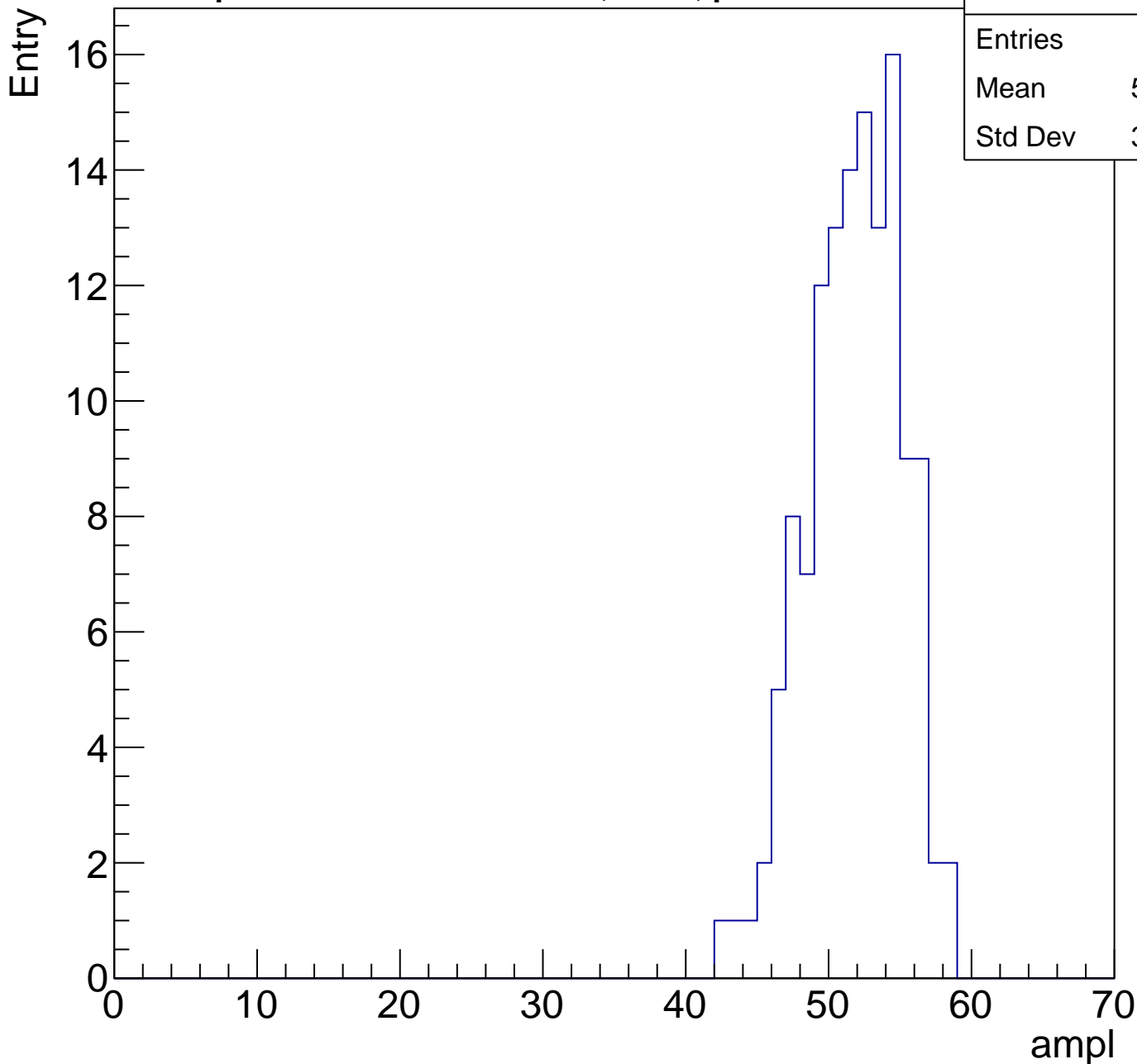
ampl



B0L101S, U21-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	51.35
Std Dev	3.255



B0L101S, U21-ch83, adc4

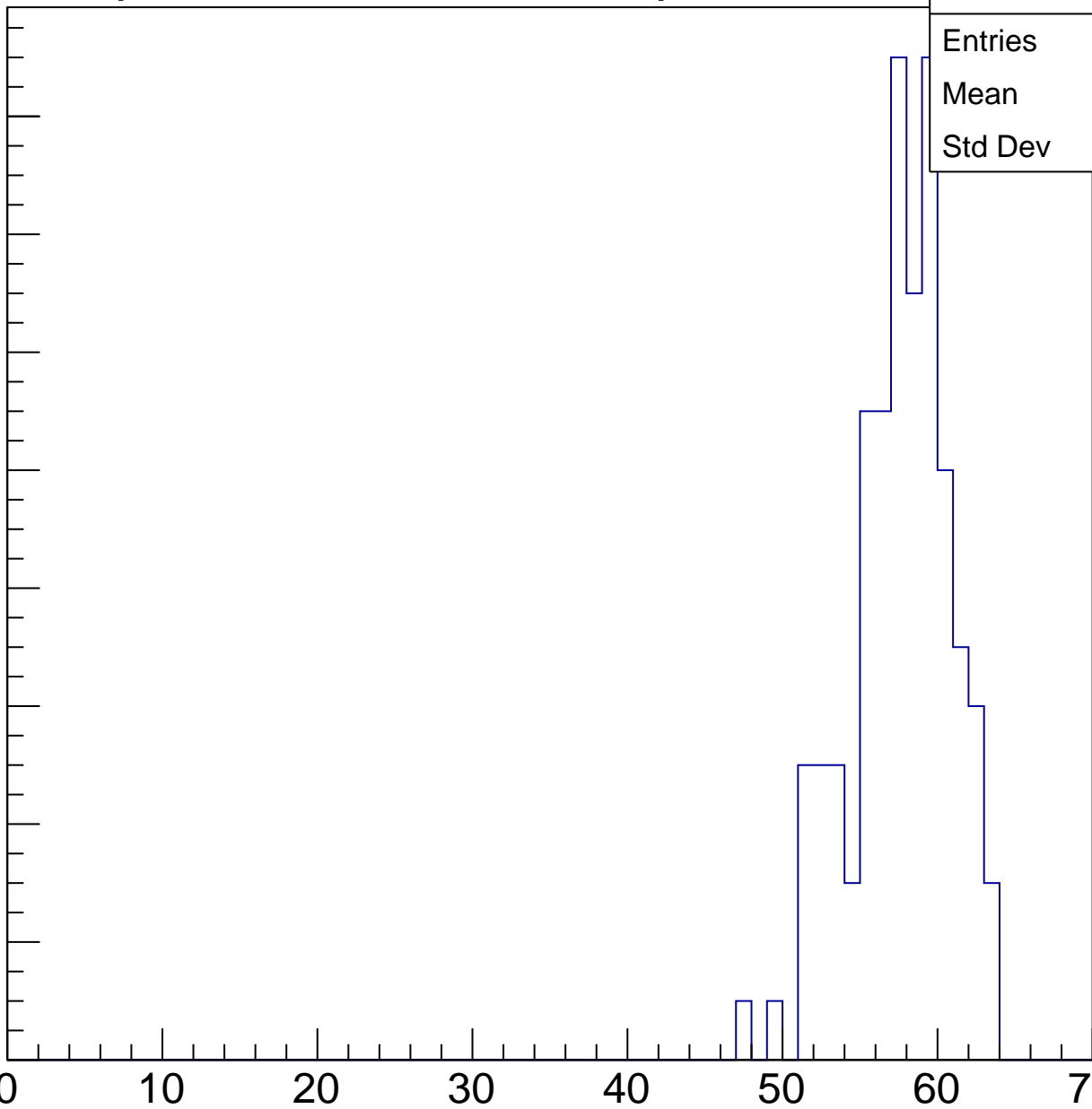
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	57.16
Std Dev	3.189

ampl



B0L101S, U21-ch83, adc5

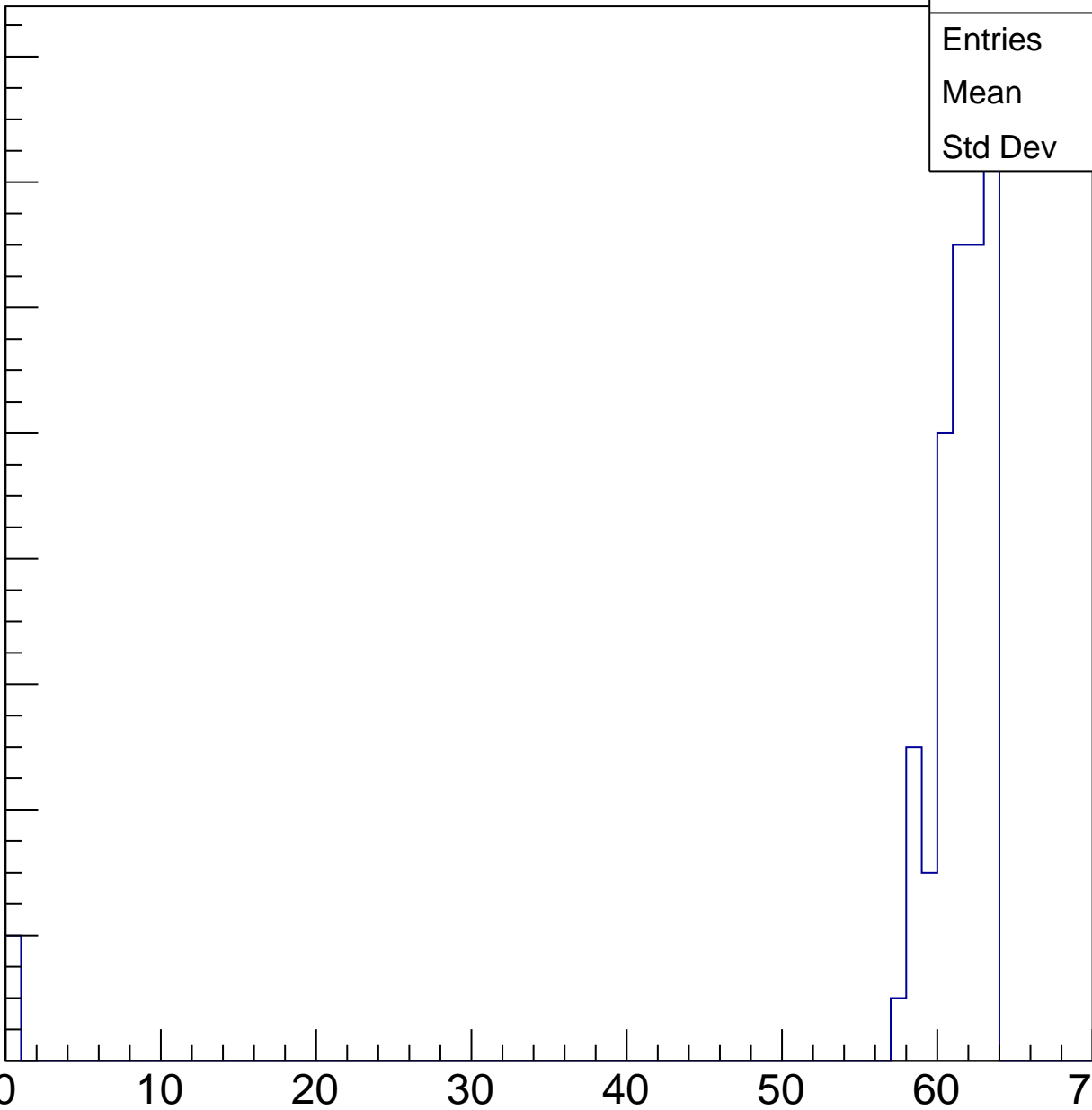
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	63
Mean	59.22
Std Dev	10.84

ampl



B0L101S, U21-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch84, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.4
Std Dev	4.408

Gaus mean : 29.0486

Gaus Width: 3.3371

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

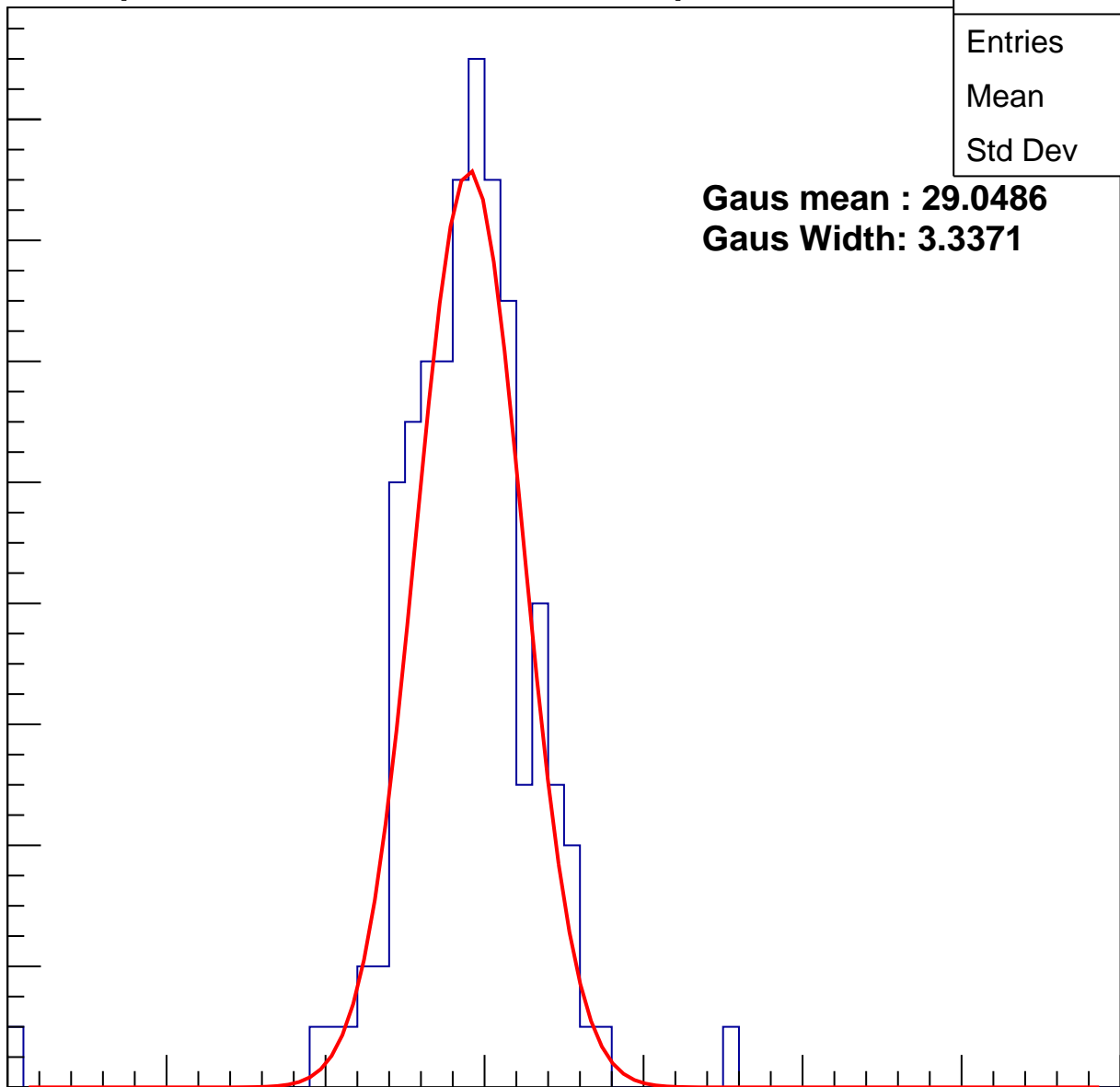
40

50

60

70

ampl



B0L101S, U21-ch84, adc1

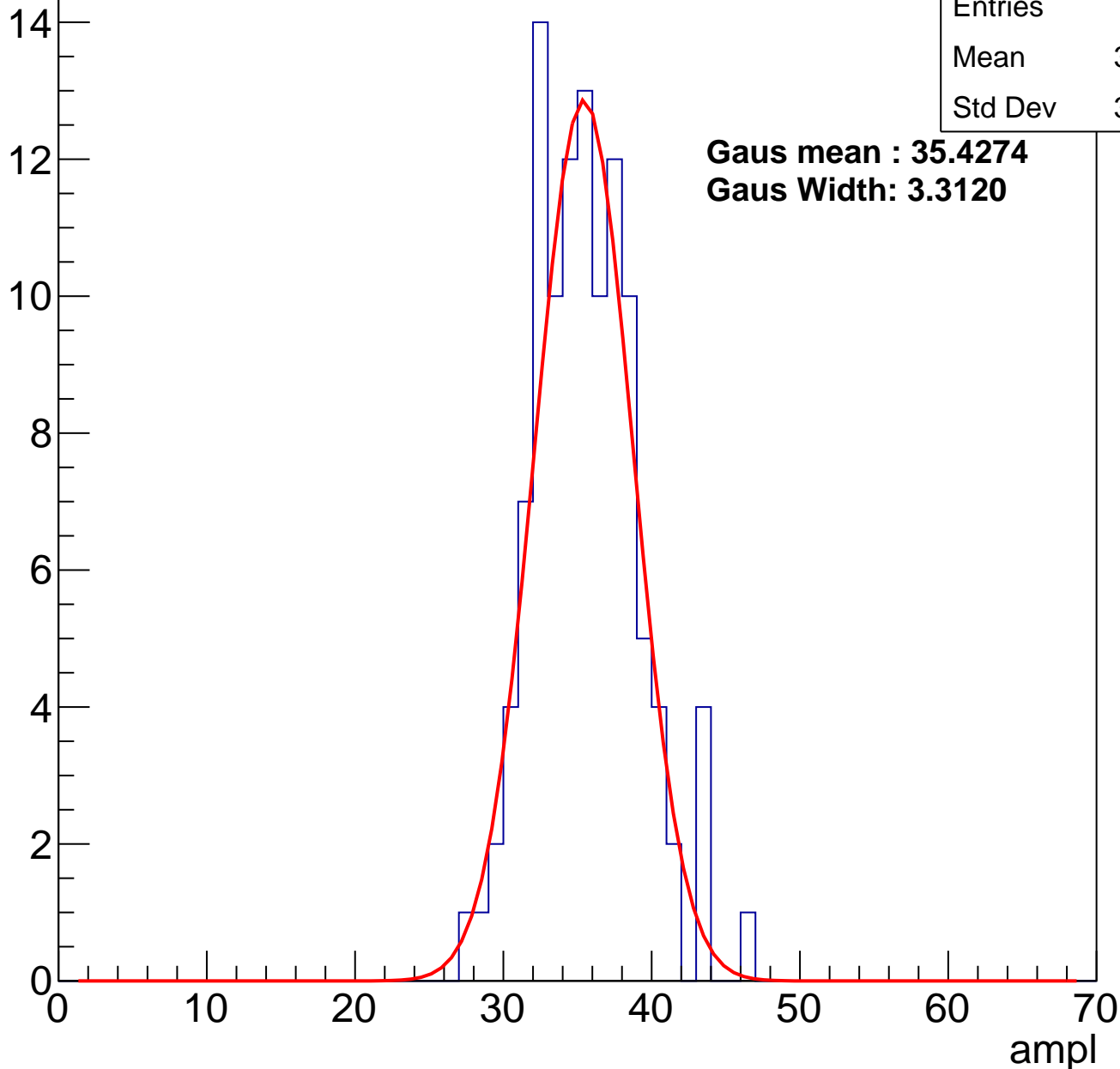
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	35.09
Std Dev	3.473

Gaus mean : 35.4274

Gaus Width: 3.3120

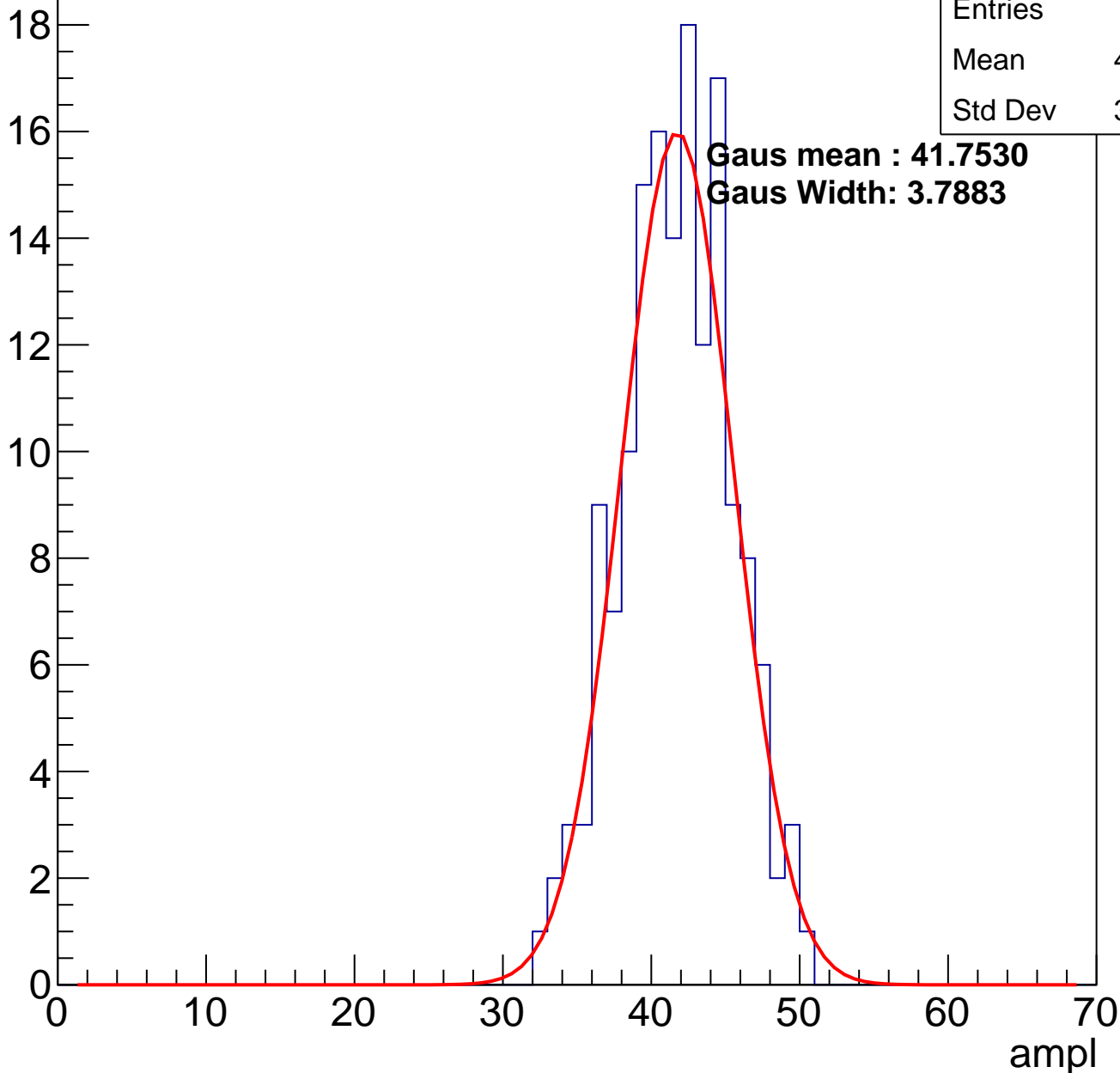
Entry



B0L101S, U21-ch84, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch84, adc3

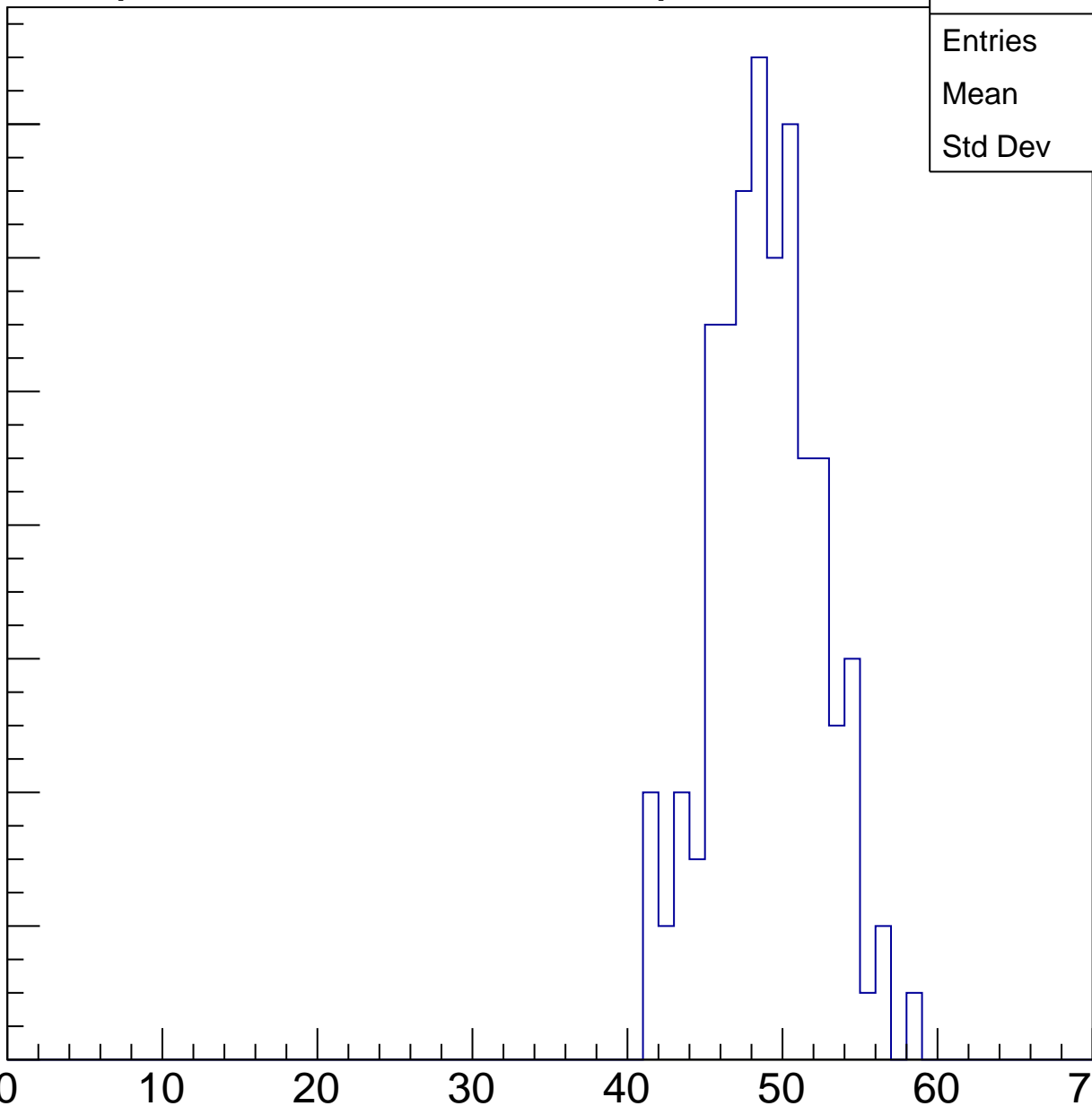
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	48.47
Std Dev	3.455

ampl



B0L101S, U21-ch84, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	54.62
Std Dev	3.311

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

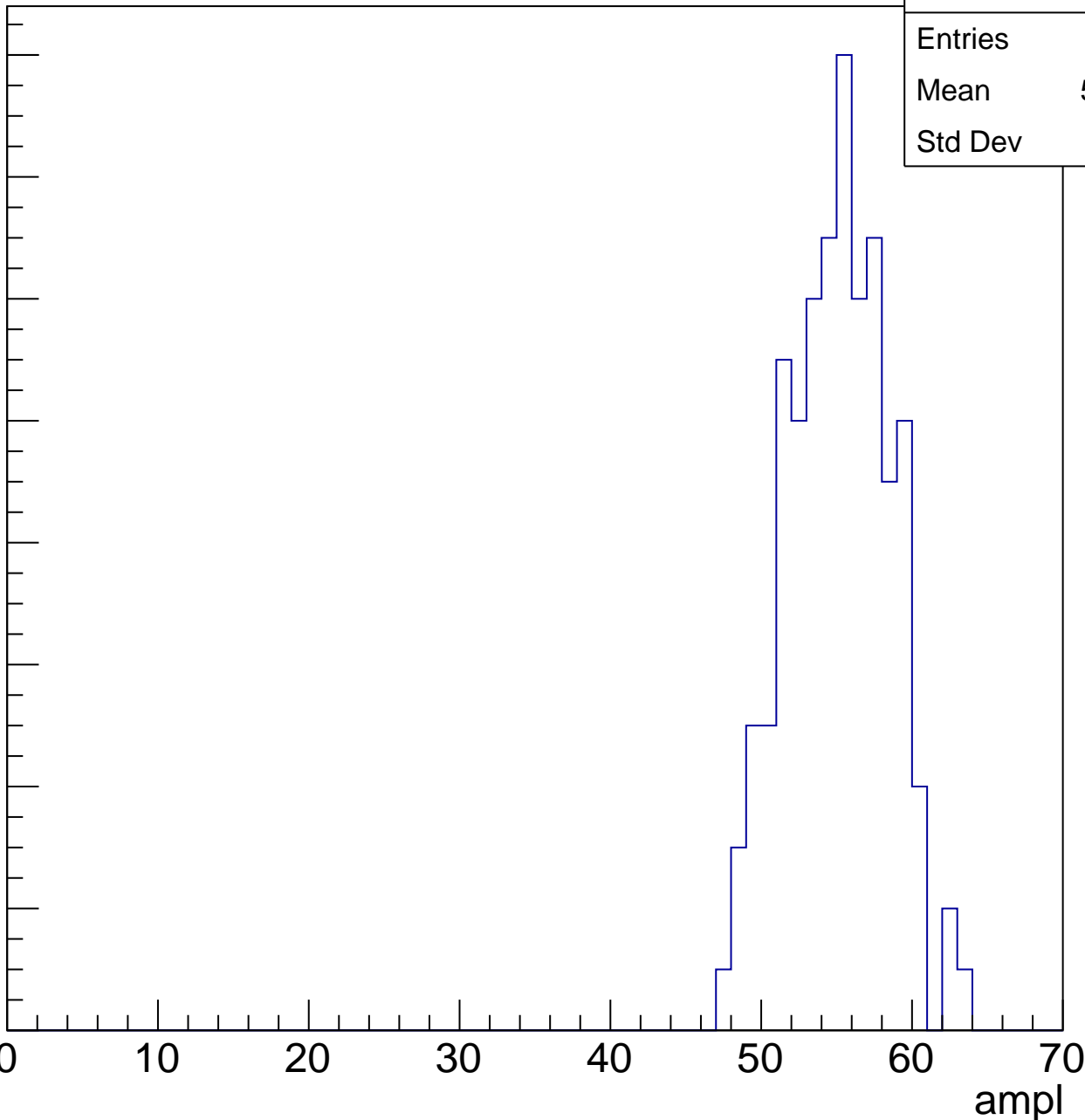
40

50

60

70

ampl



B0L101S, U21-ch84, adc5

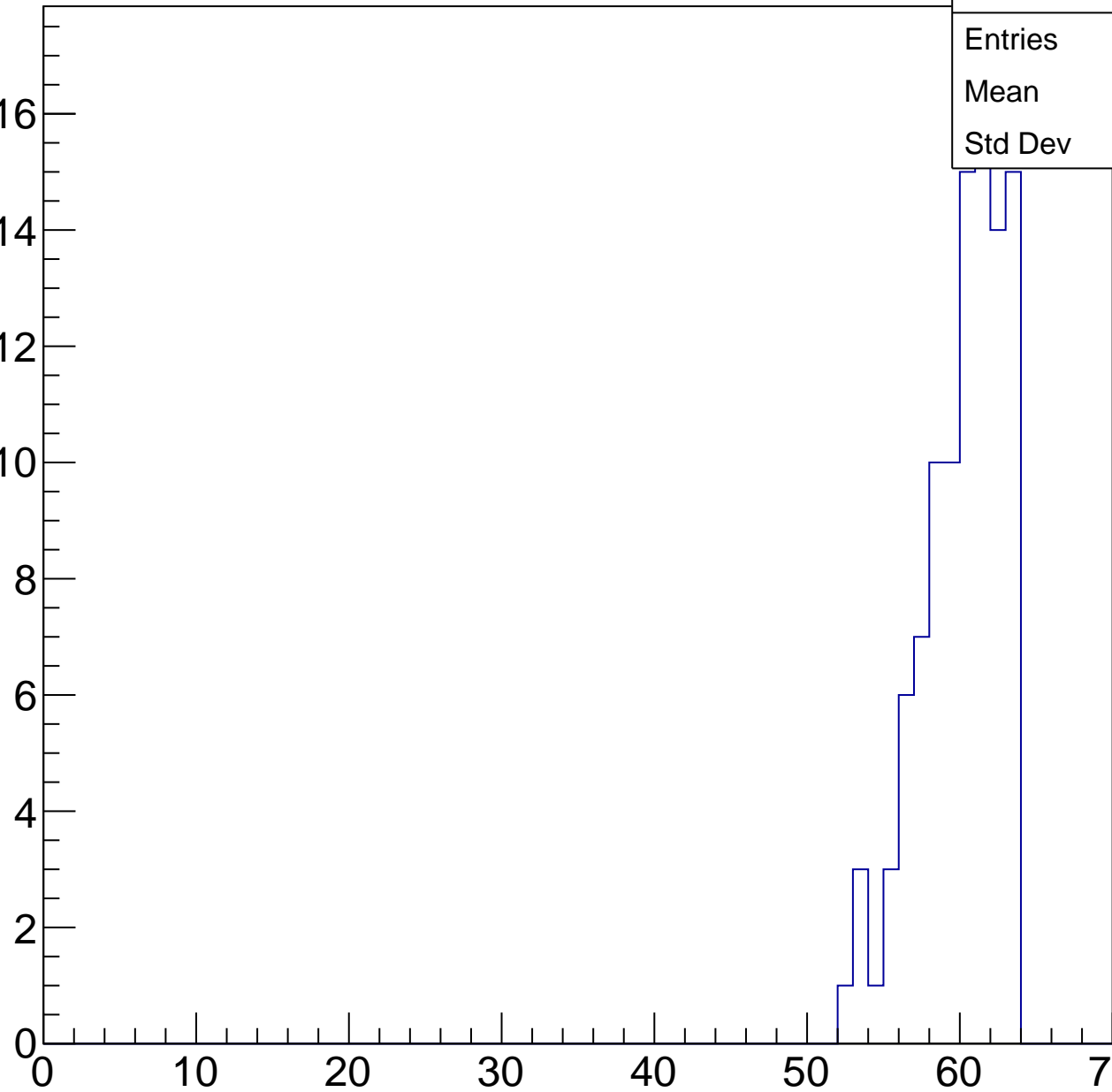
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	59.66
Std Dev	2.67

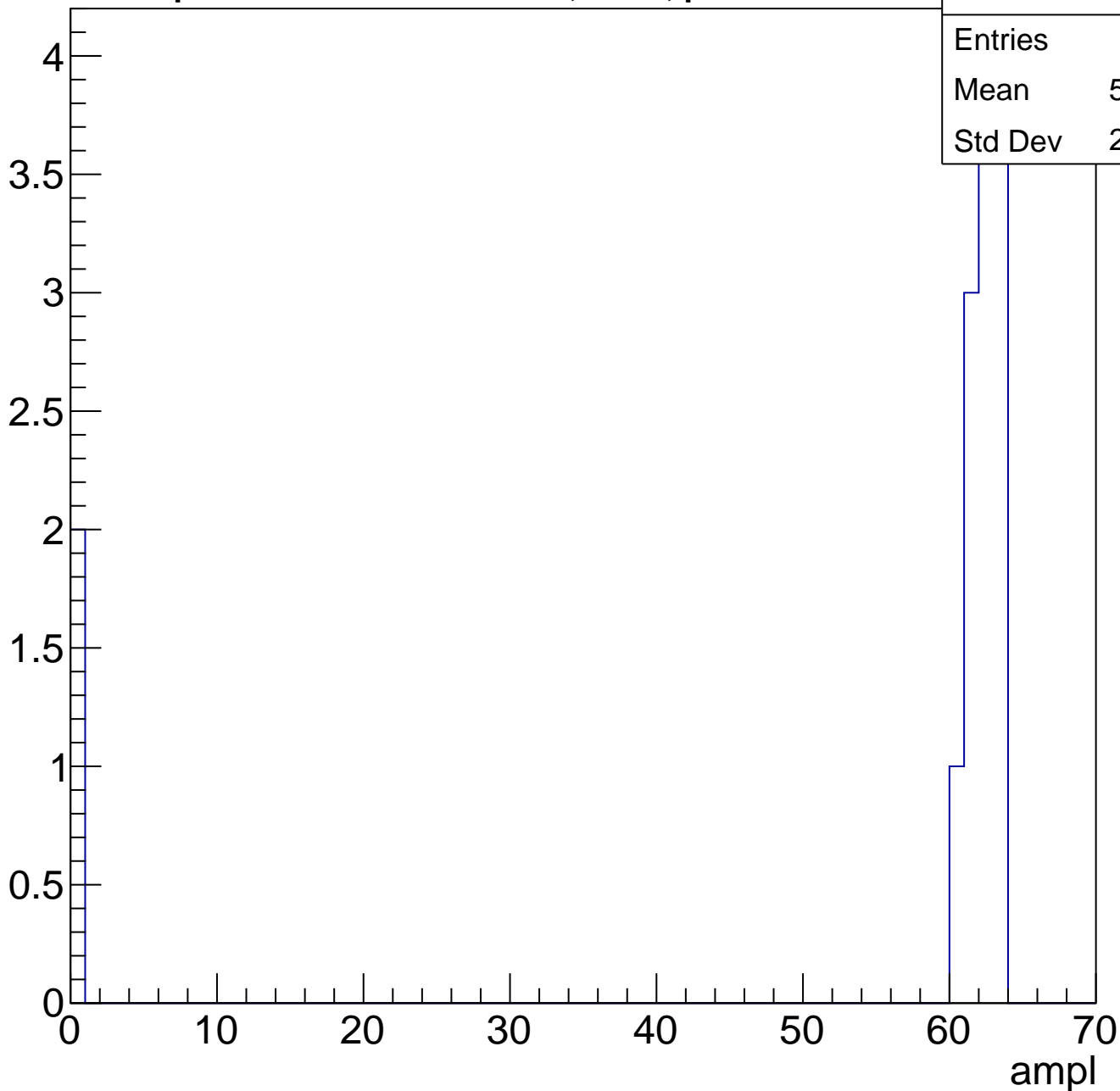
ampl



B0L101S, U21-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch85, adc0

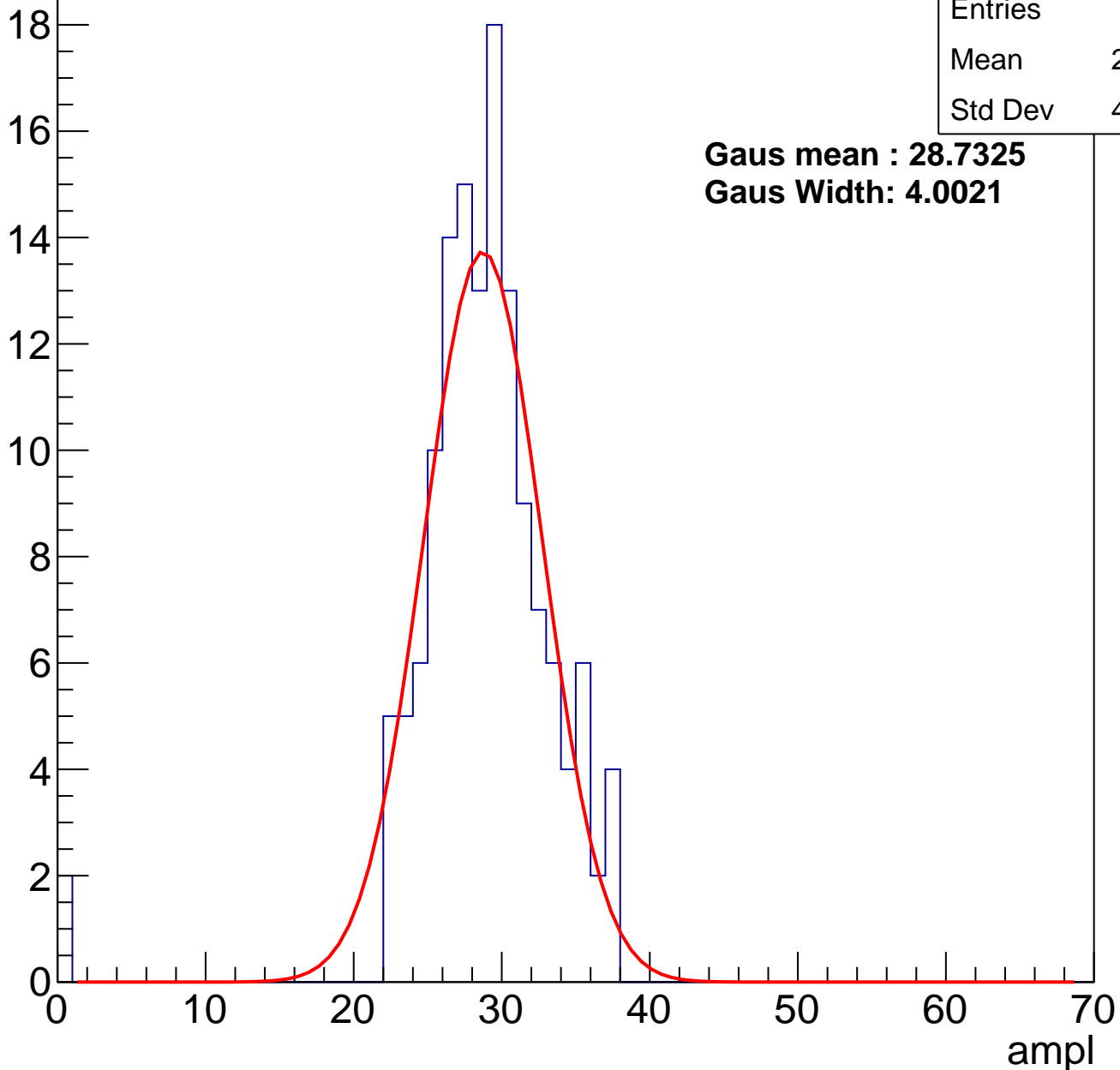
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	28.28
Std Dev	4.962

Gaus mean : 28.7325

Gaus Width: 4.0021

Entry



B0L101S, U21-ch85, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	35.22
Std Dev	3.857

Gaus mean : 35.4935

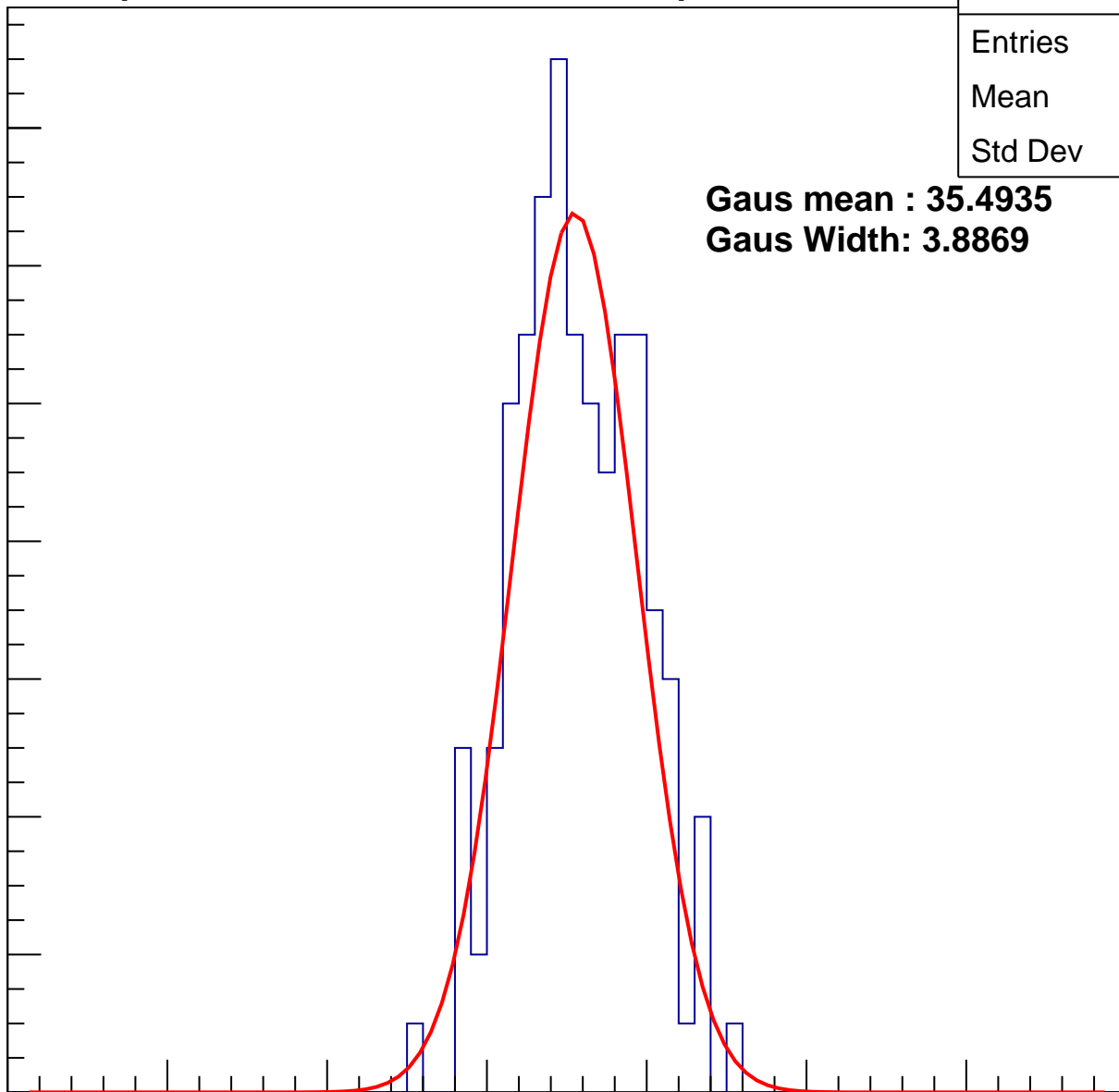
Gaus Width: 3.8869

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

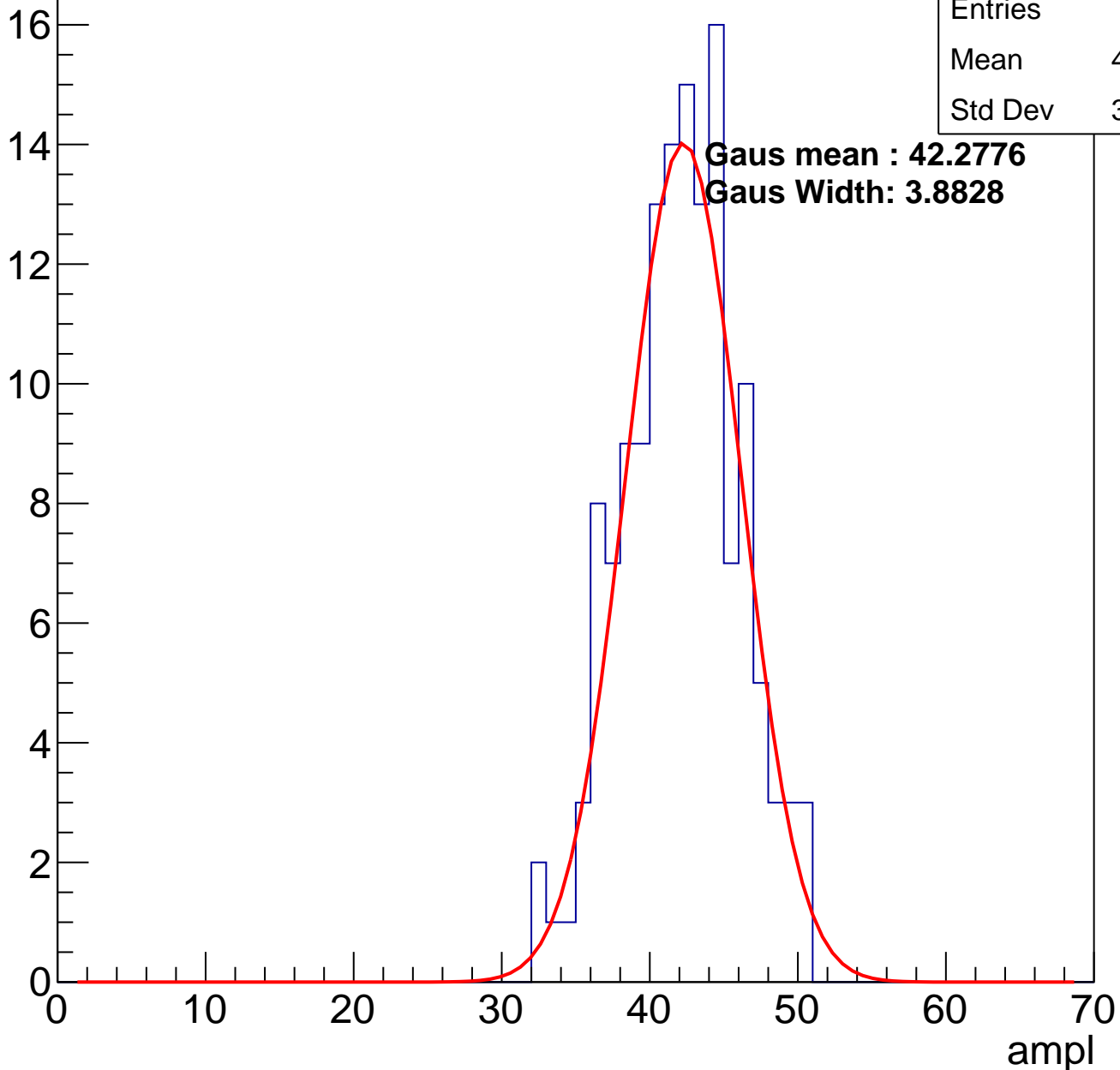
ampl



B0L101S, U21-ch85, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

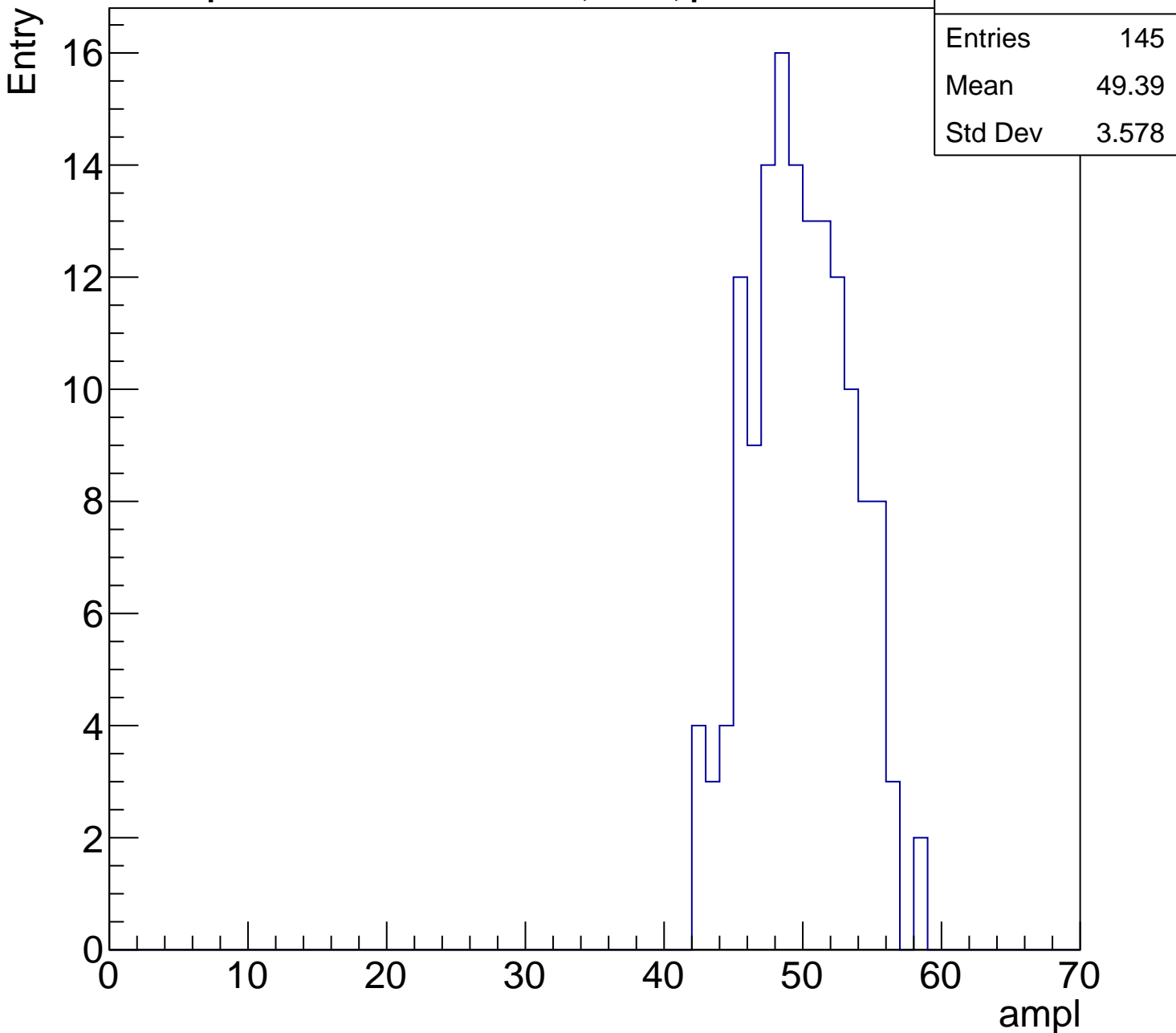
Entries	145
Mean	49.39
Std Dev	3.578

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch85, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

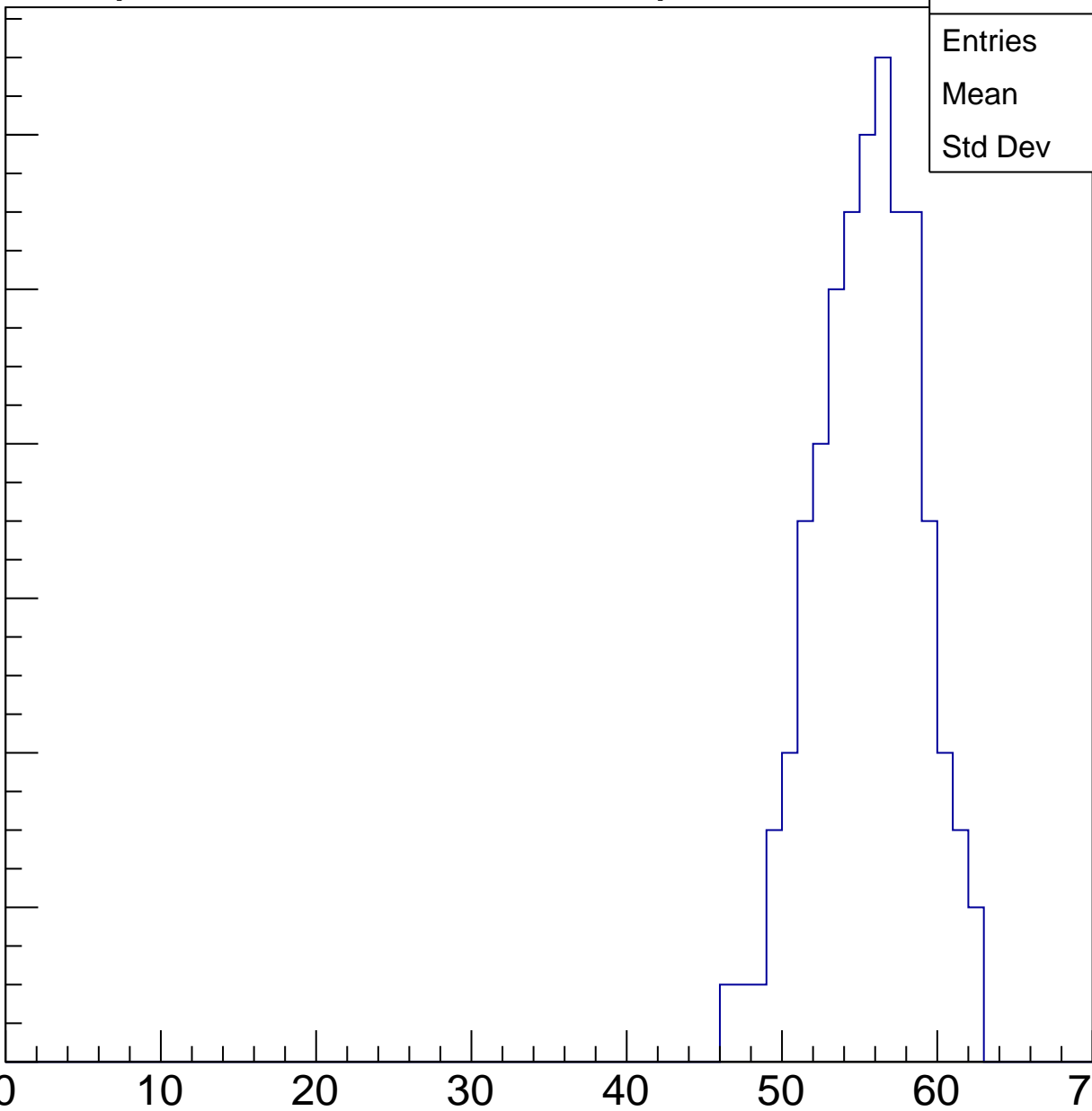
50

60

70

ampl

Entries	109
Mean	55.03
Std Dev	3.333



B0L101S, U21-ch85, adc5

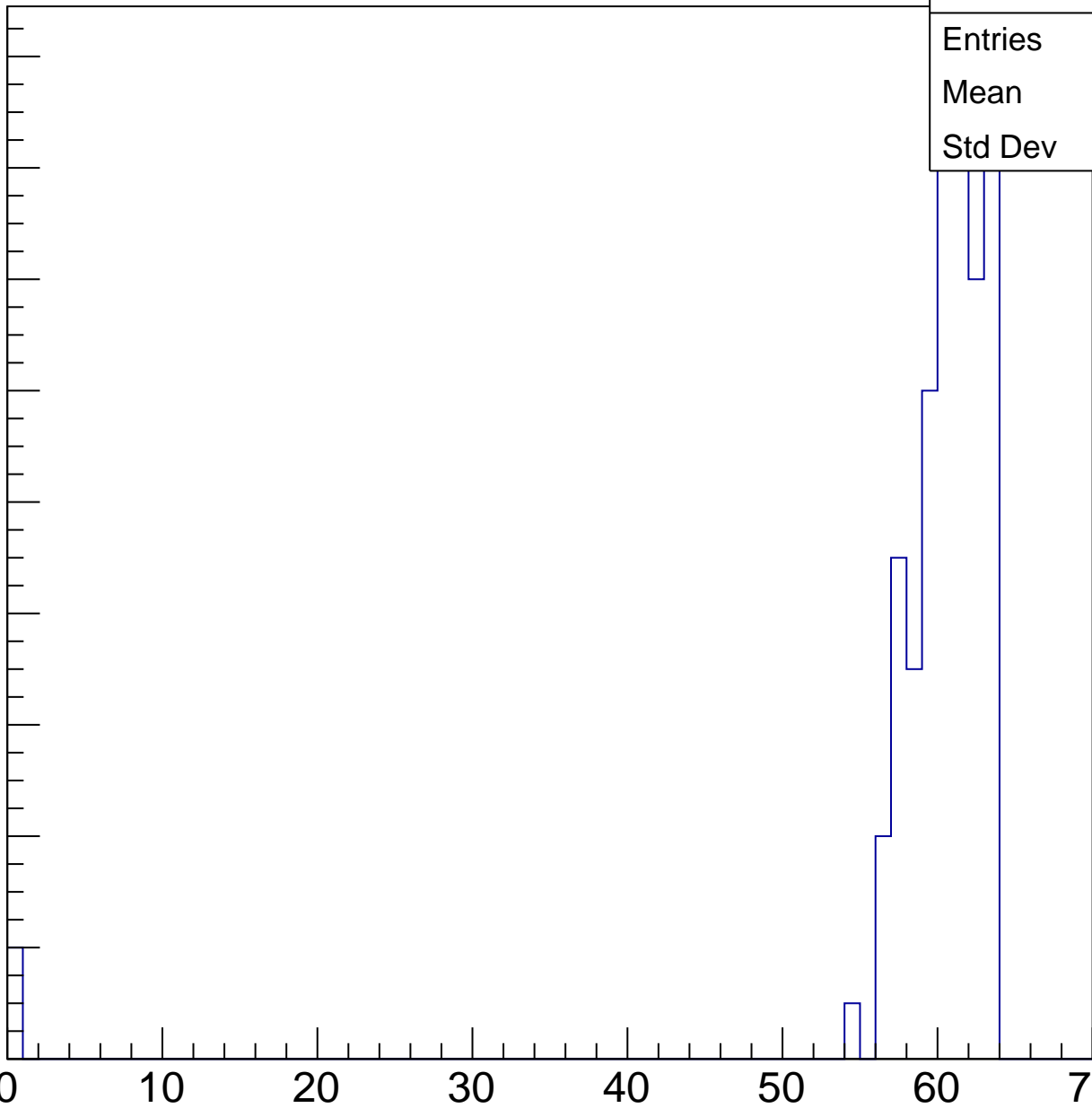
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.02
Std Dev	8.739

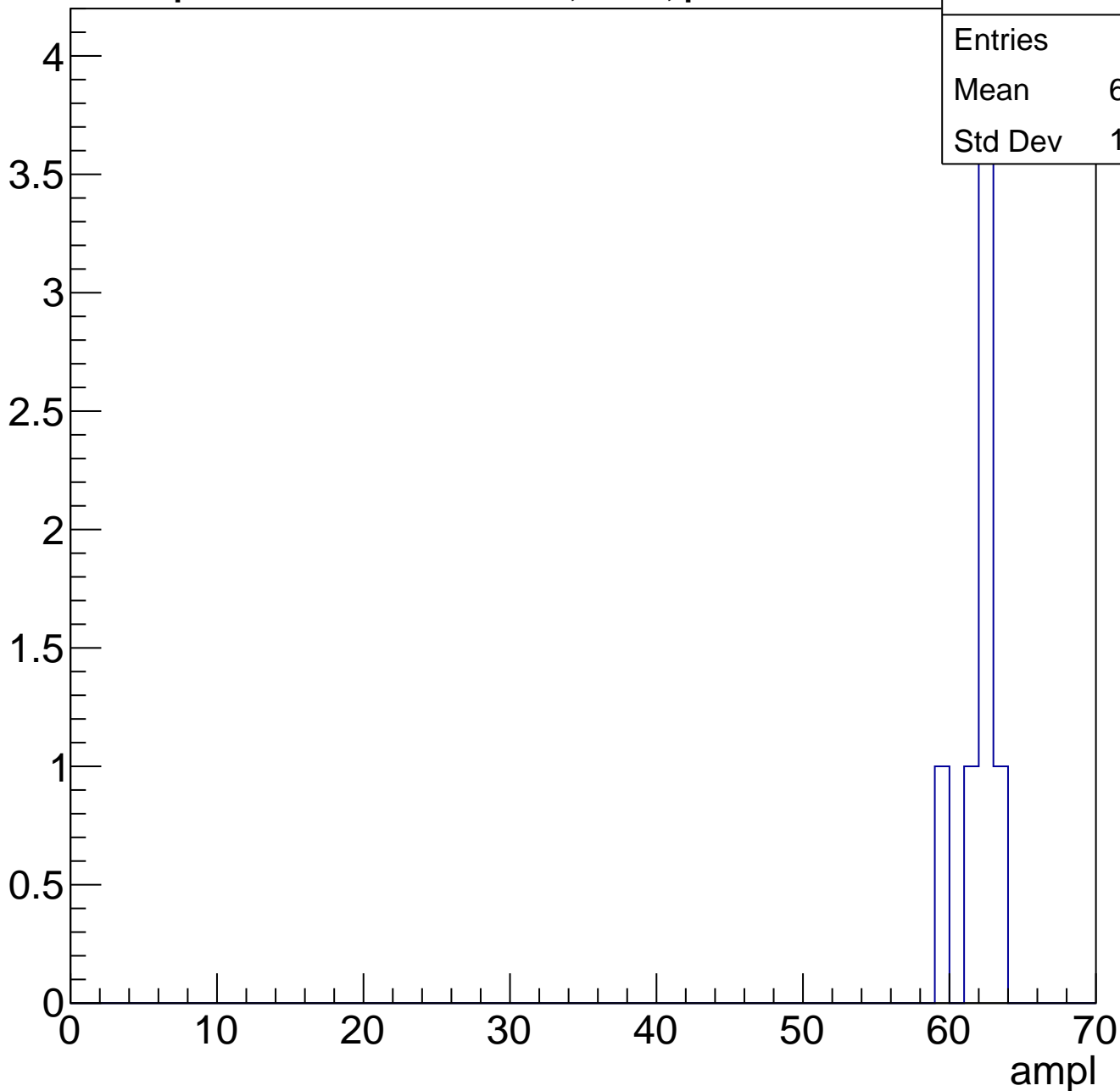
ampl



B0L101S, U21-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch86, adc0

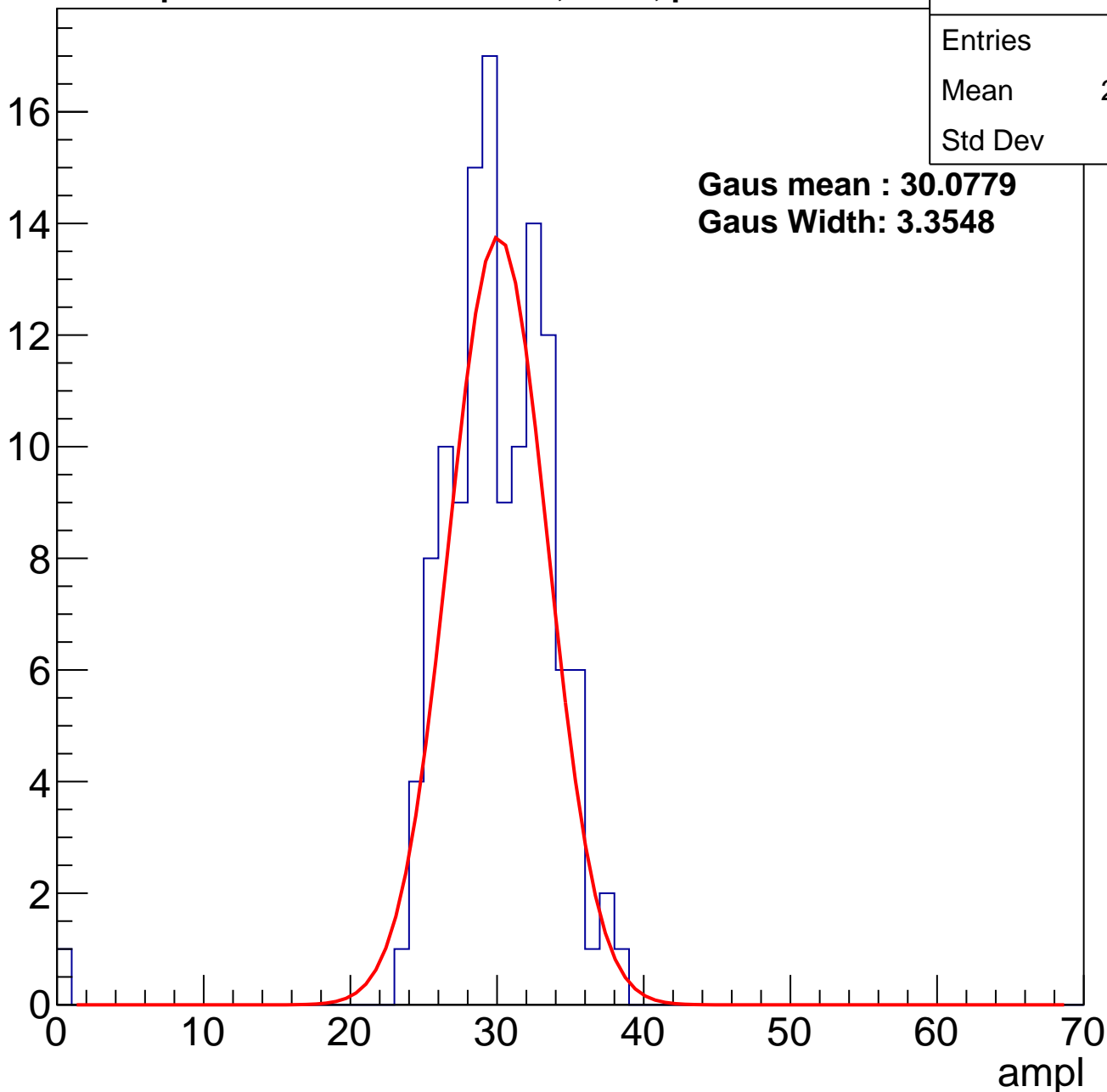
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.53
Std Dev	4.18

Gaus mean : 30.0779

Gaus Width: 3.3548

Entry

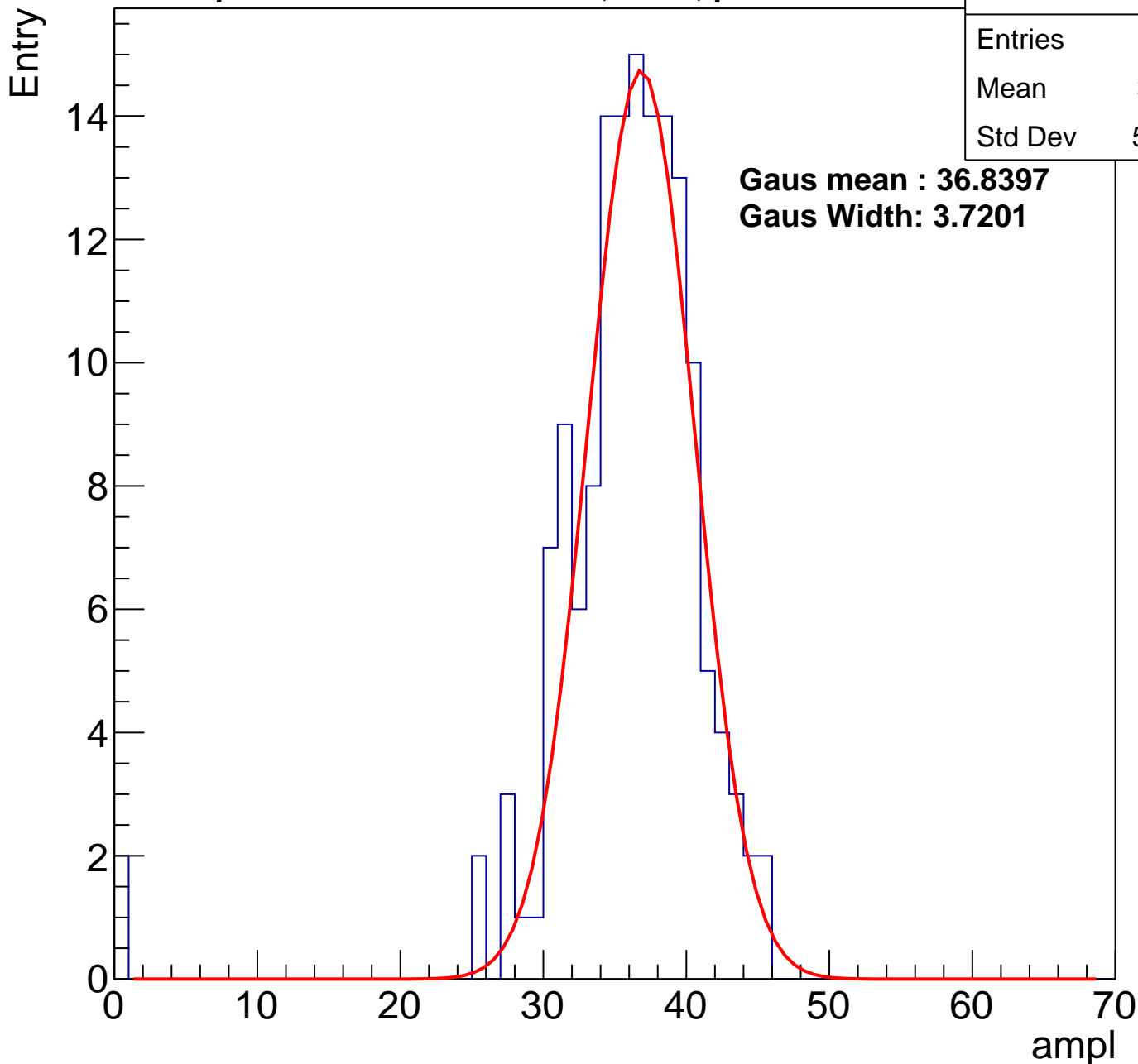


B0L101S, U21-ch86, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	35.41
Std Dev	5.738

Gaus mean : 36.8397
Gaus Width: 3.7201



B0L101S, U21-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

144

Mean

43.76

Std Dev

3.682

Gaus mean : 44.1697

Gaus Width: 4.0473

0

10

20

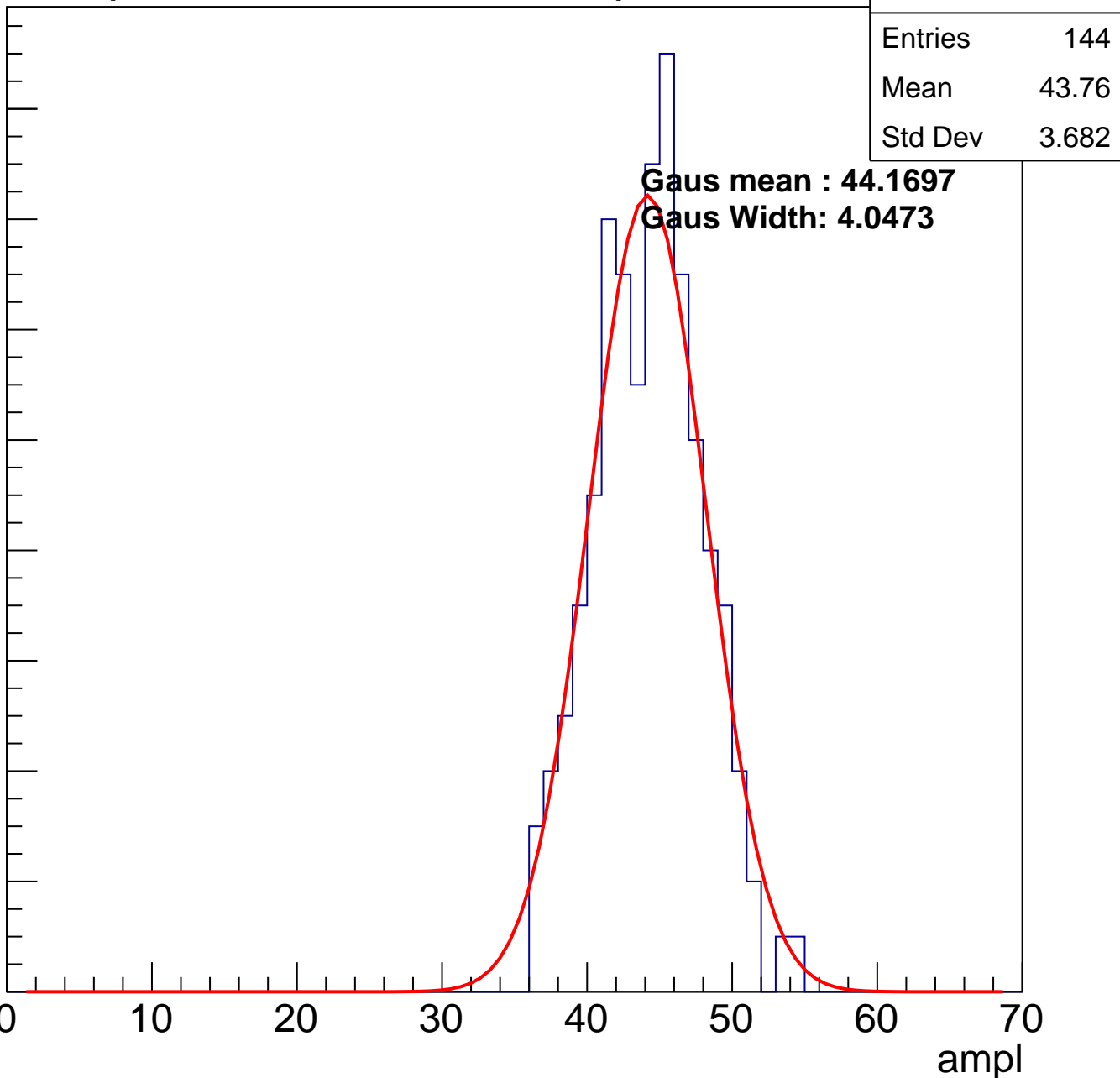
30

40

50

60

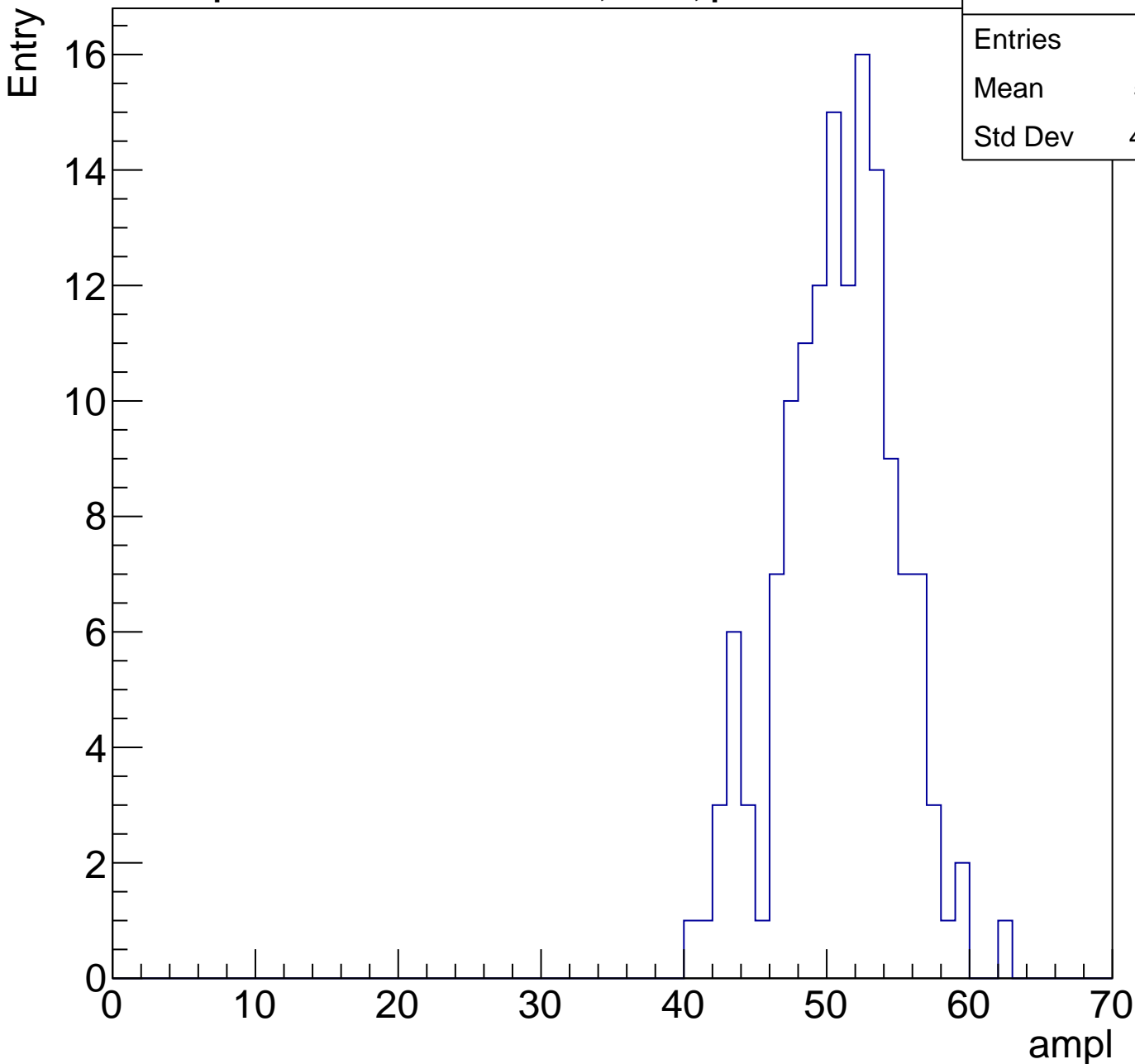
ampl



B0L101S, U21-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

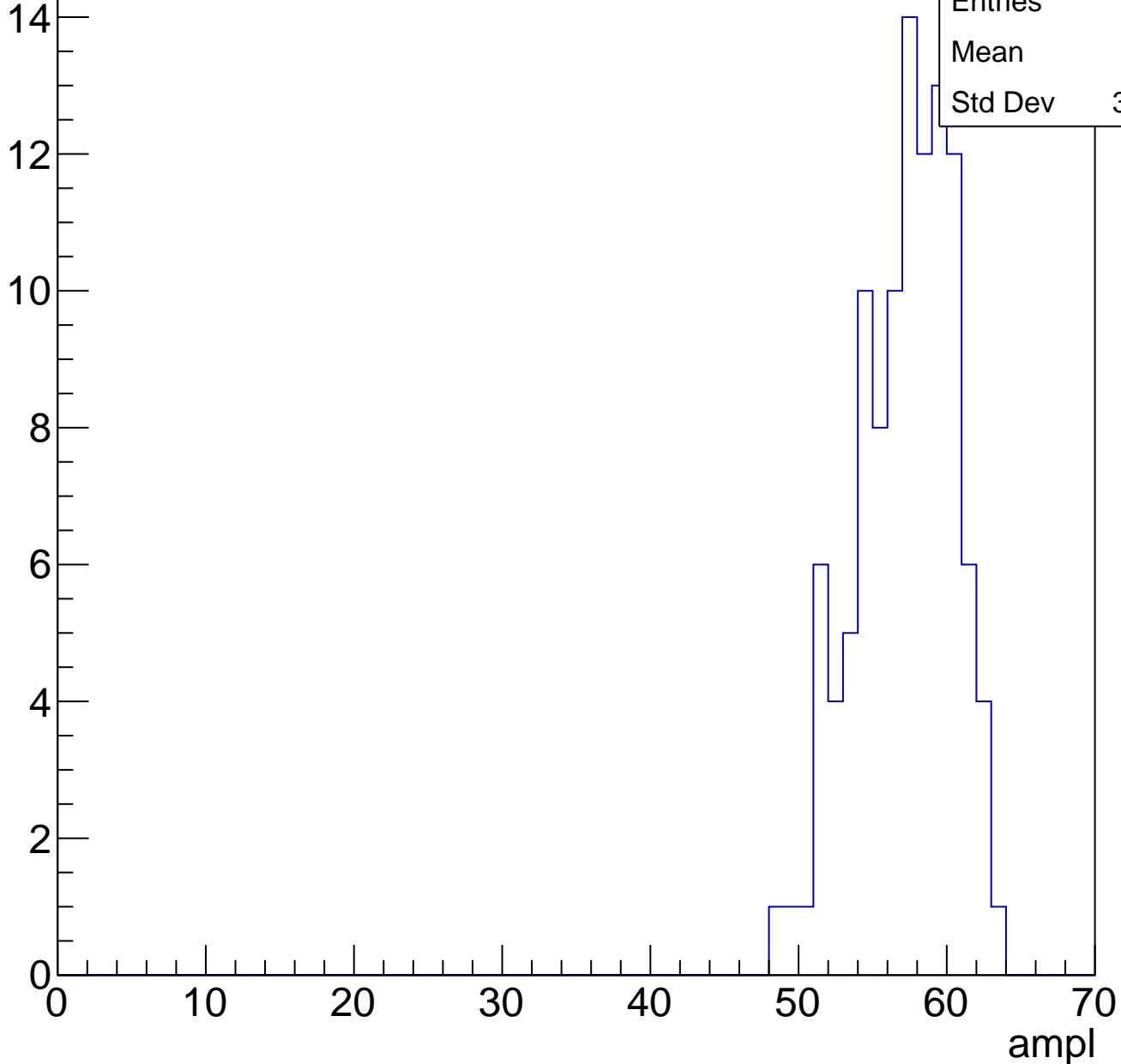
Entries	142
Mean	50.41
Std Dev	4.046



B0L101S, U21-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch86, adc5

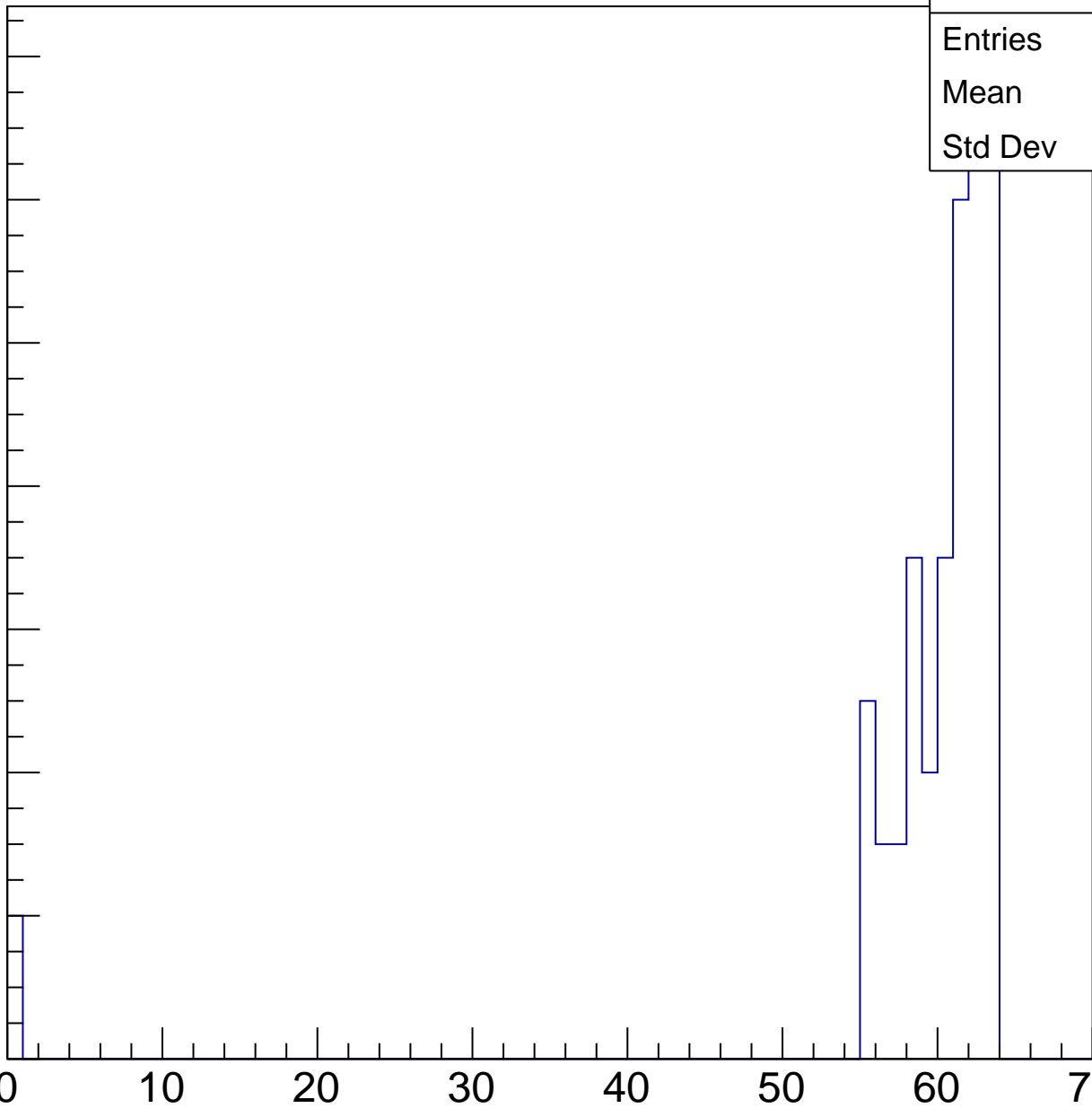
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	58.5
Std Dev	10.32

ampl

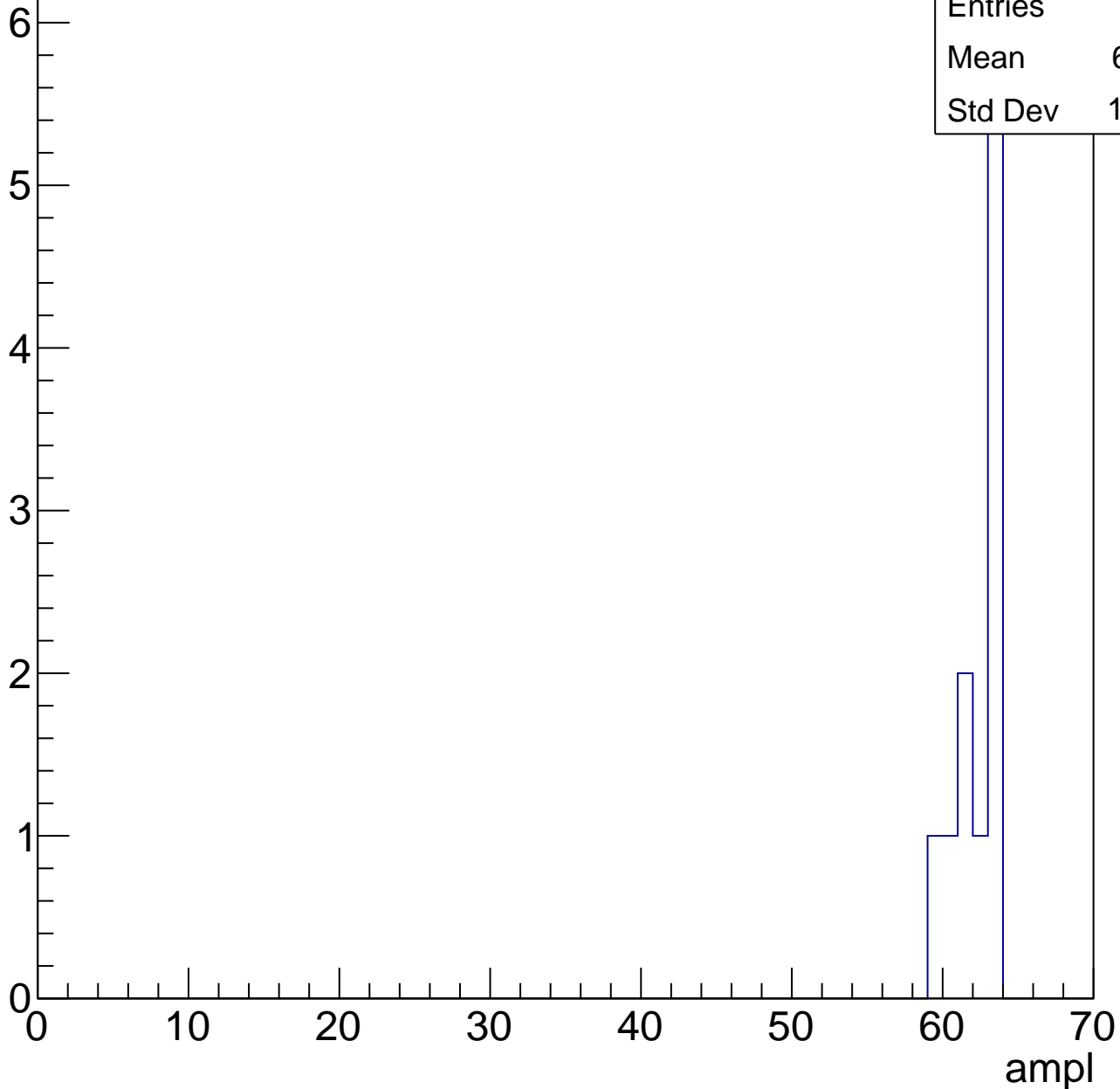


B0L101S, U21-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

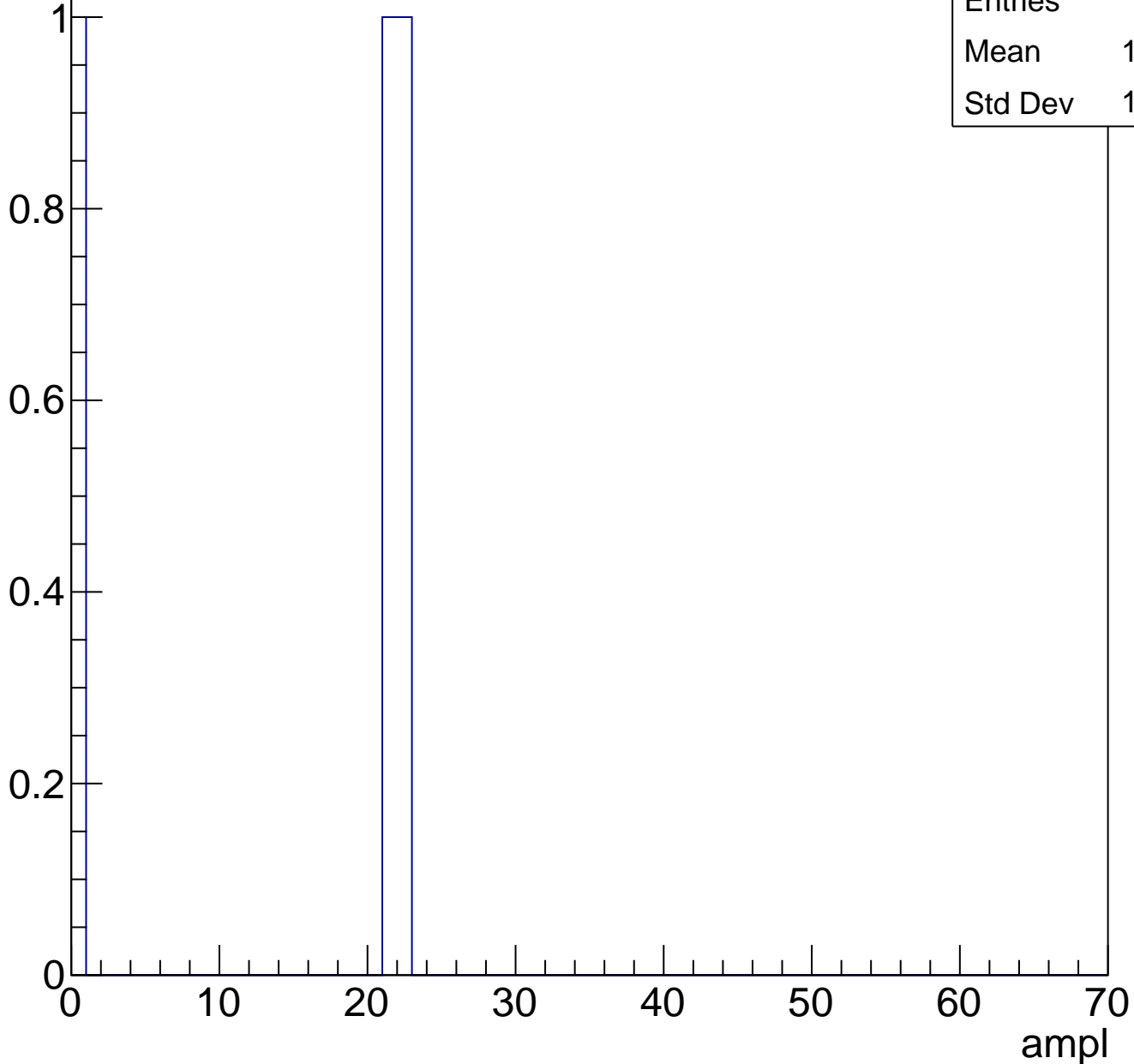
Entries	11
Mean	61.91
Std Dev	1.379



B0L101S, U21-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	14.33
Std Dev	10.14

B0L101S, U21-ch87, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	27.95
Std Dev	3.982

Gaus mean : 28.5654

Gaus Width: 3.1558

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

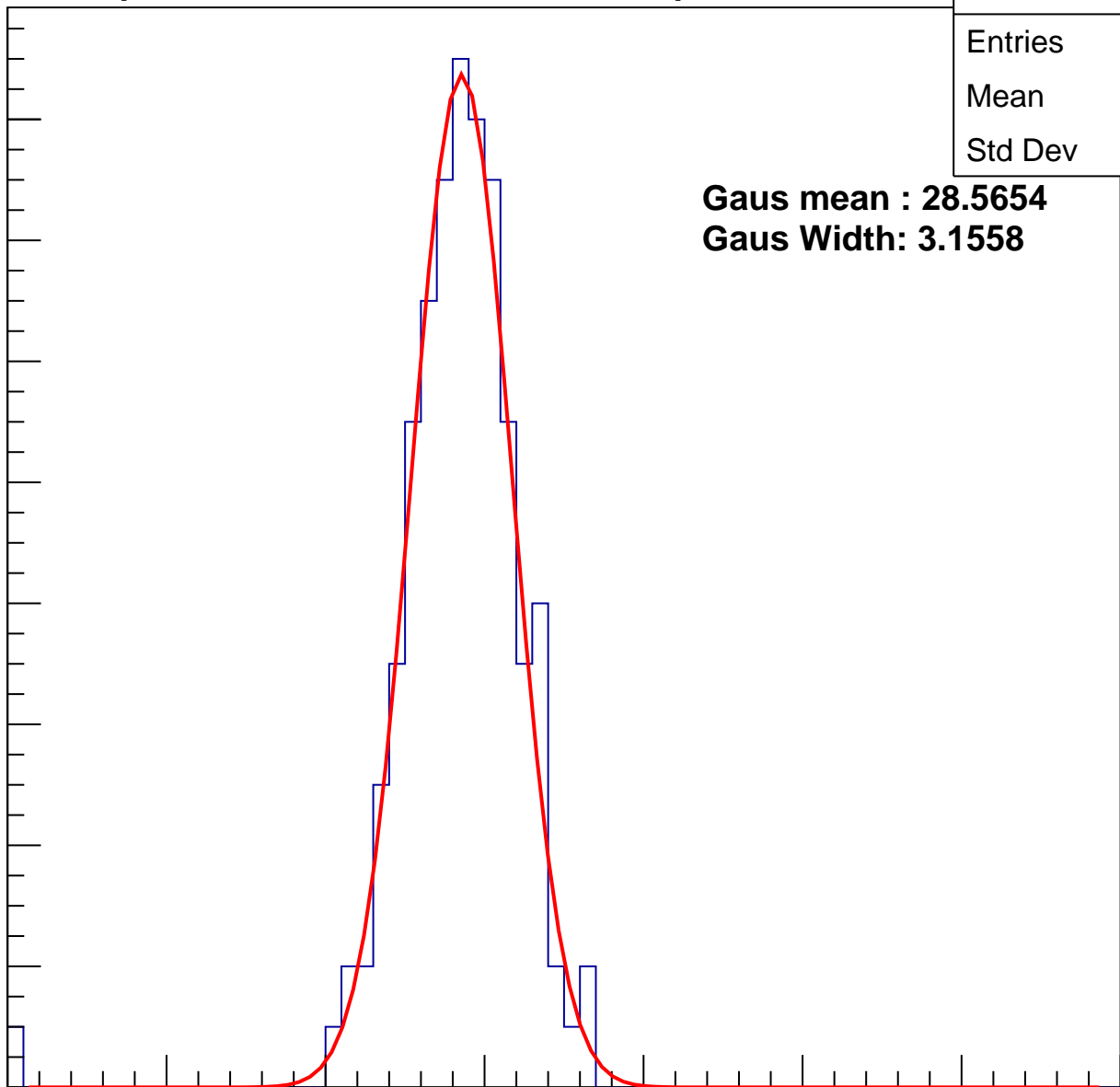
40

50

60

70

ampl



B0L101S, U21-ch87, adc1

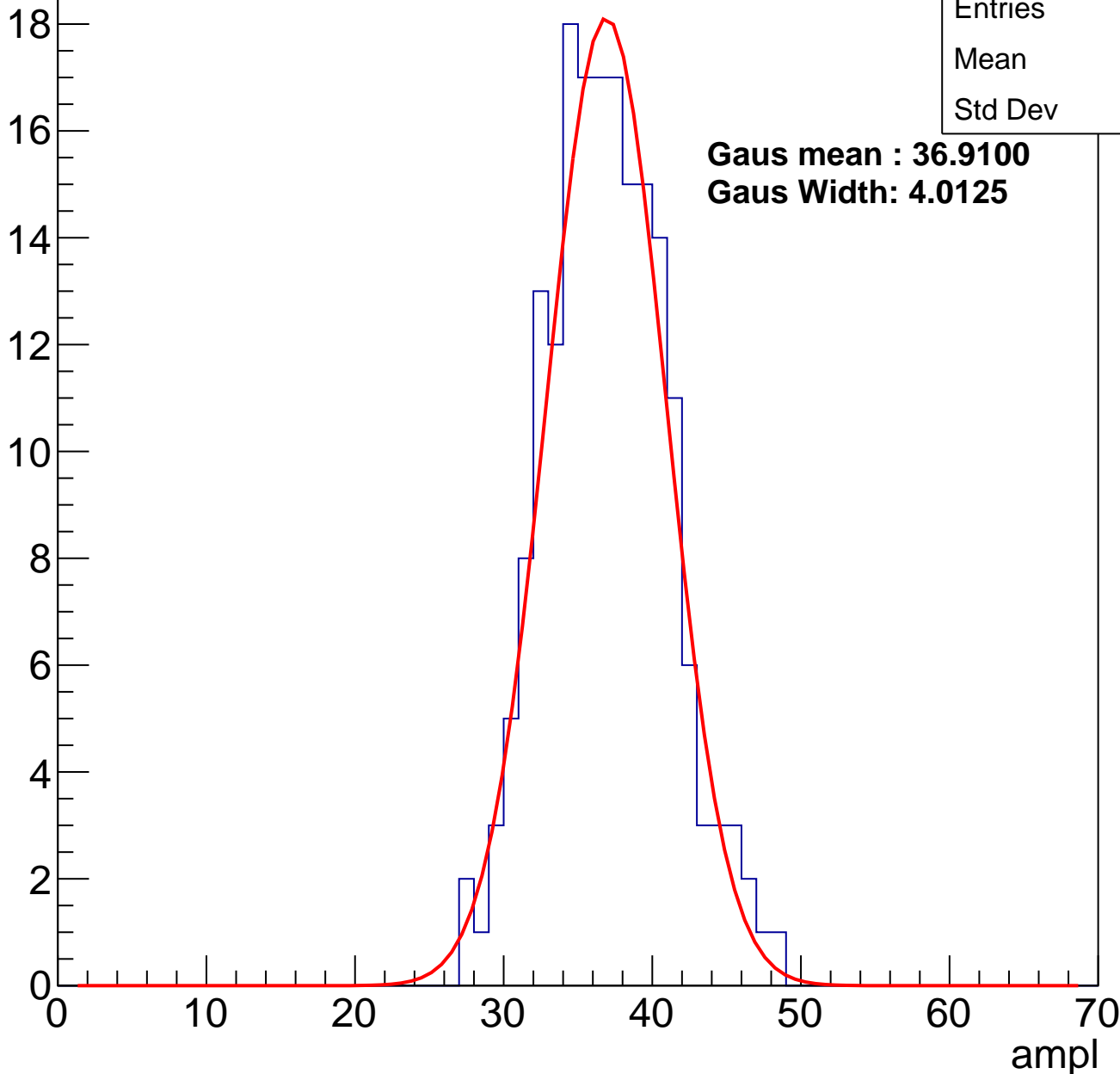
calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	36.51
Std Dev	4.06

Gaus mean : 36.9100

Gaus Width: 4.0125

Entry



B0L101S, U21-ch87, adc2

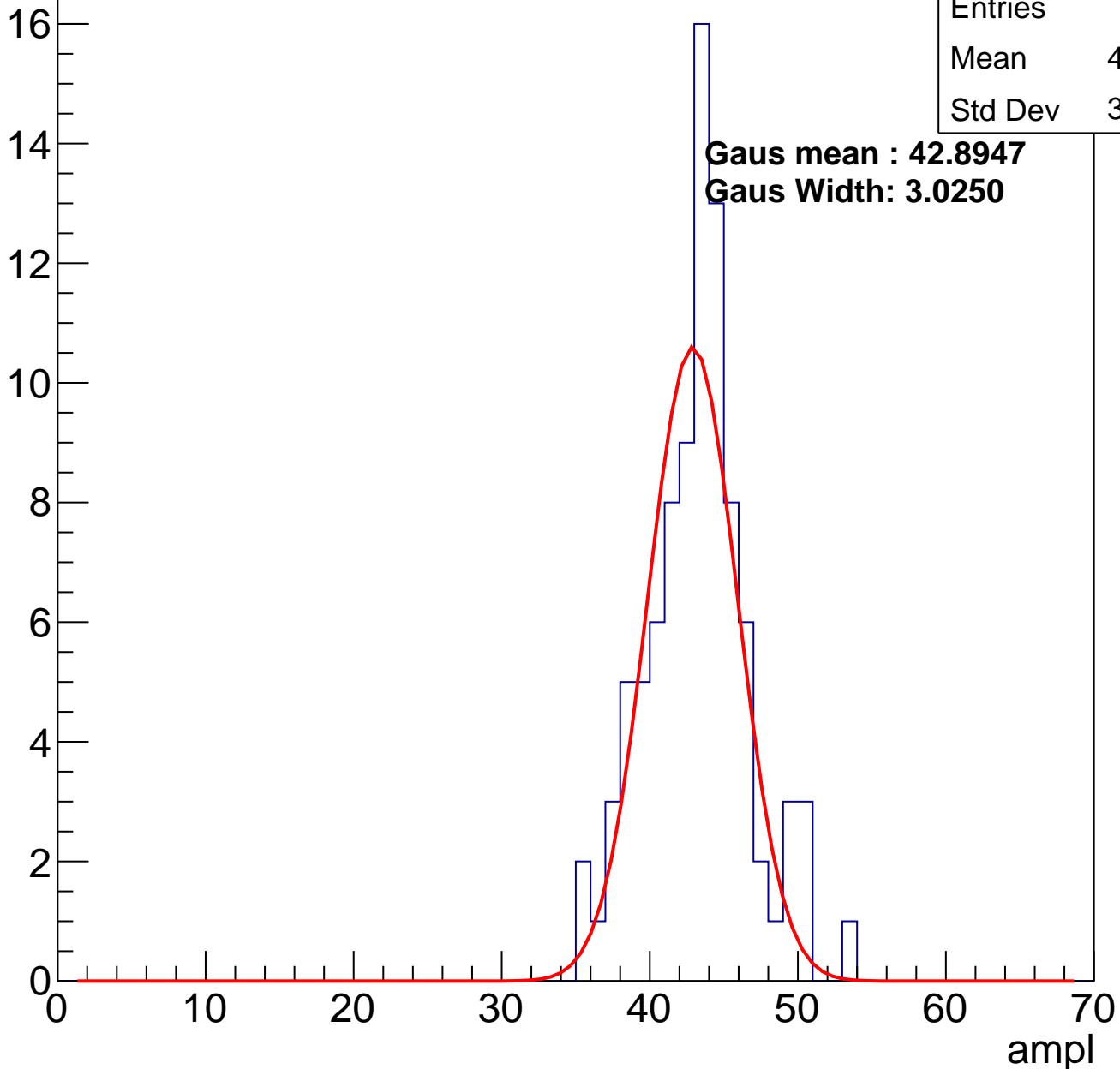
calib_packv5_042523_0143.root, FC#1, port C1

Entries	92
Mean	42.78
Std Dev	3.445

Gaus mean : 42.8947

Gaus Width: 3.0250

Entry

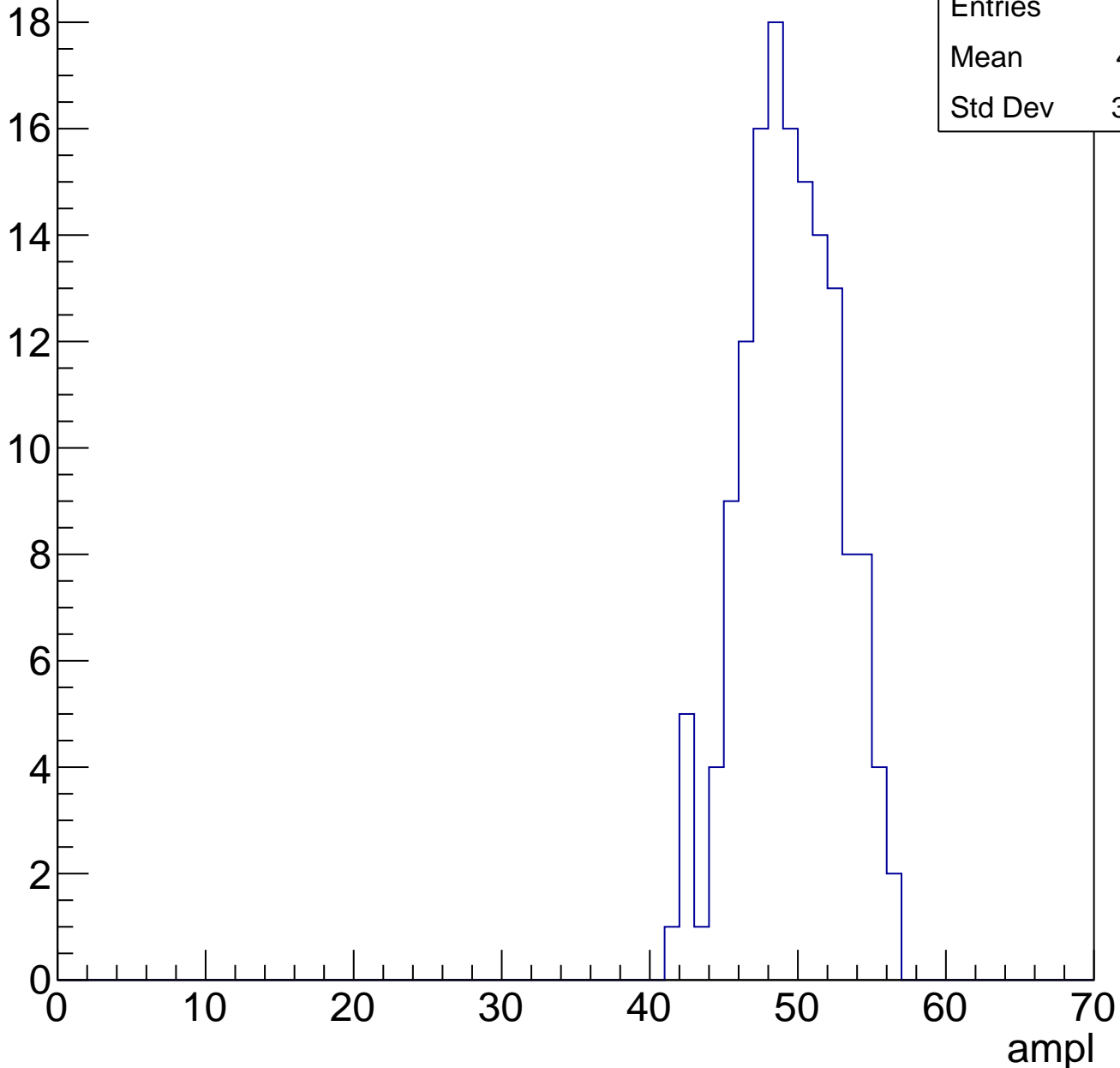


B0L101S, U21-ch87, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	49.01
Std Dev	3.245

Entry



B0L101S, U21-ch87, adc4

calib_packv5_042523_0143.root, FC#1, port C1

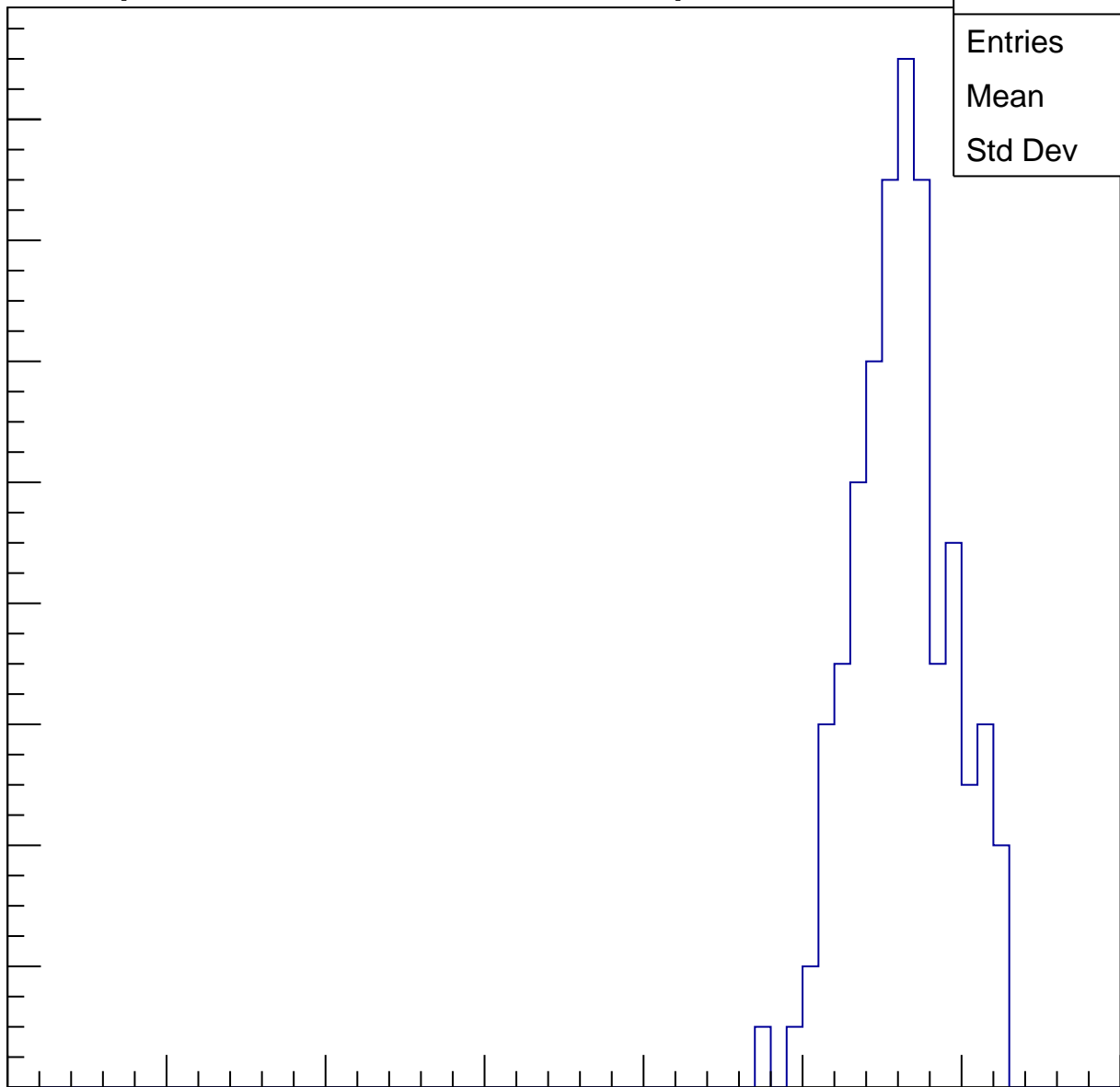
Entries	117
Mean	55.79
Std Dev	3.08

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch87, adc5

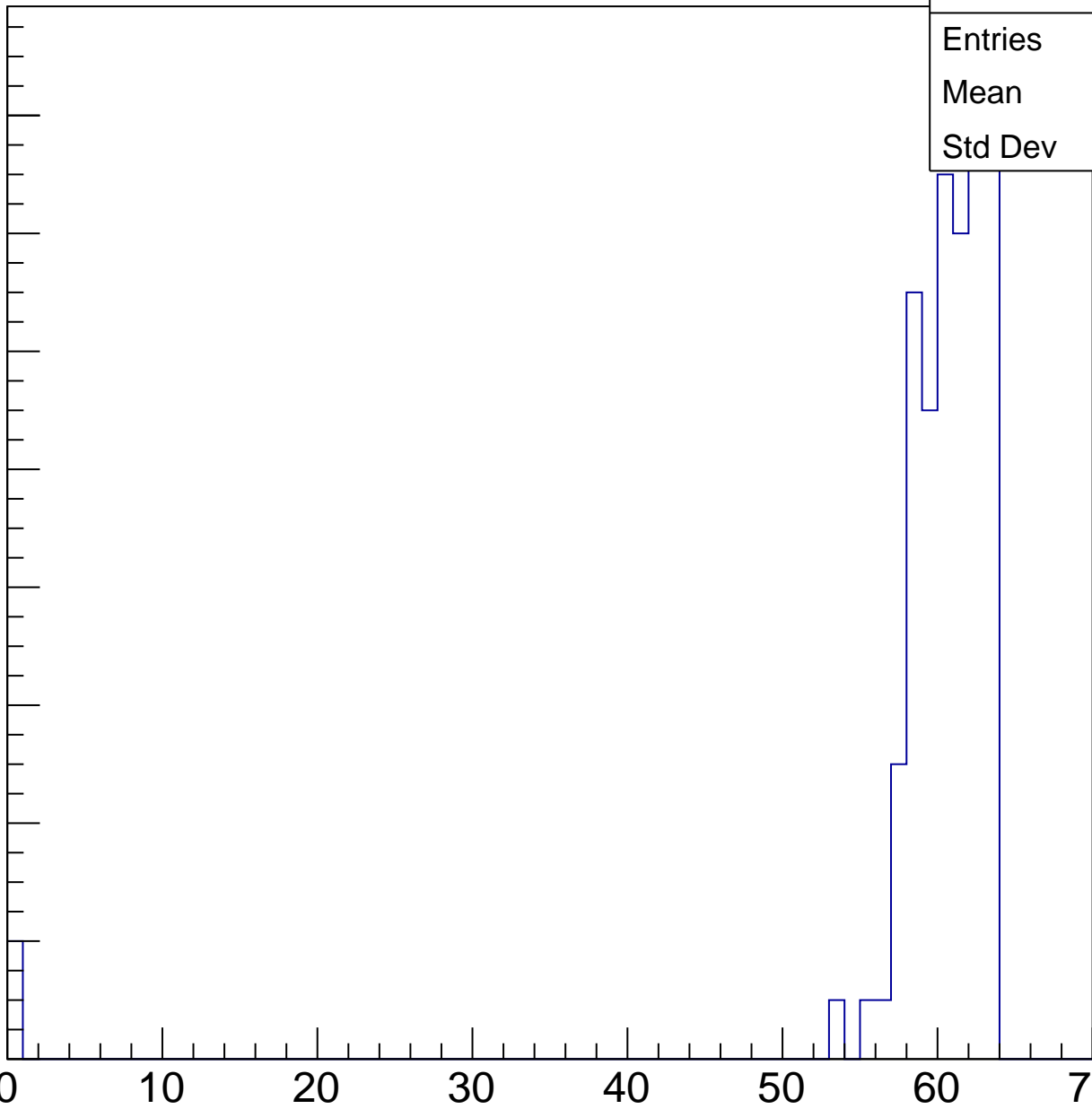
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.05
Std Dev	8.864

ampl



B0L101S, U21-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch88, adc0

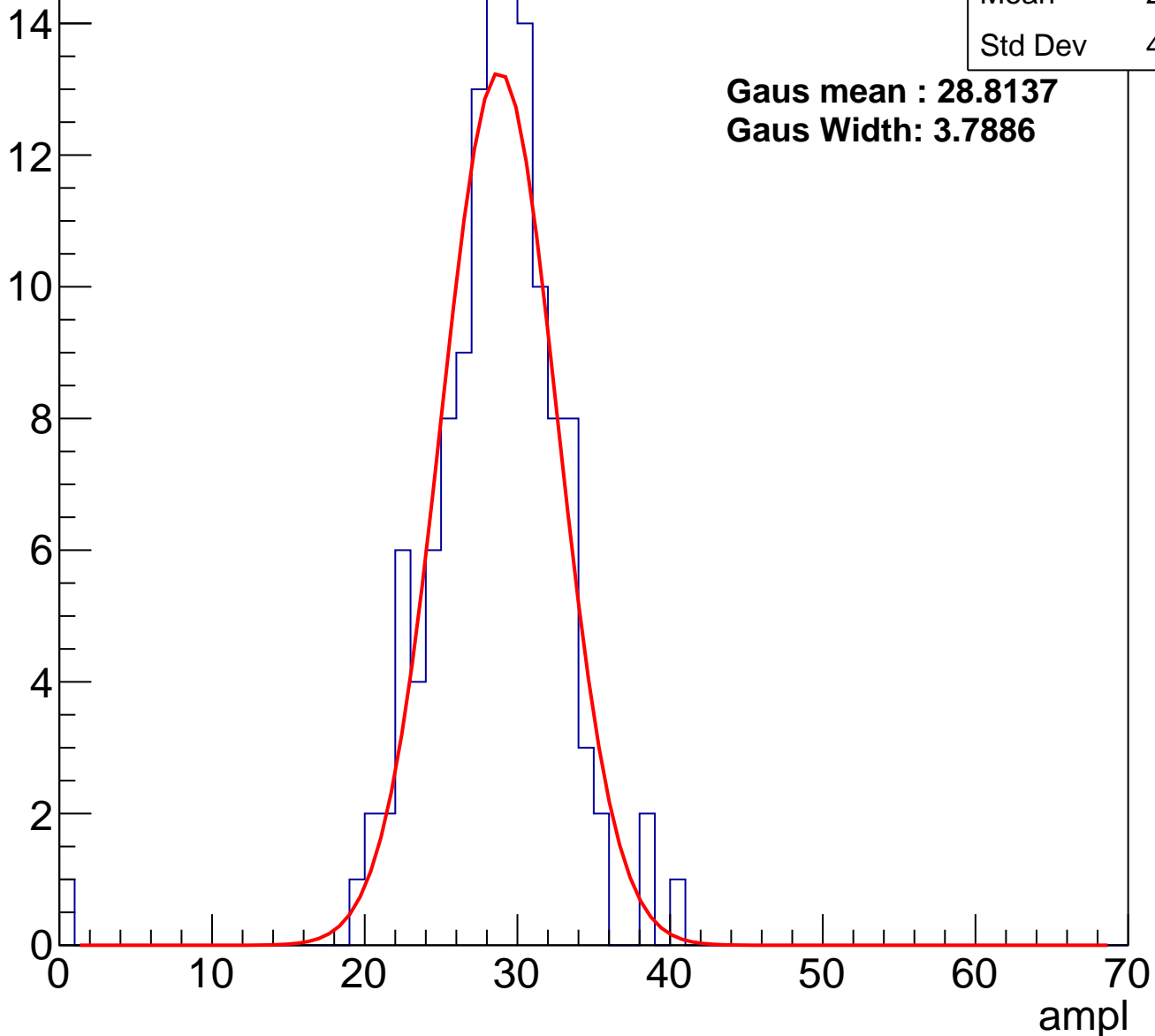
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	28.05
Std Dev	4.526

Gaus mean : 28.8137

Gaus Width: 3.7886

Entry



B0L101S, U21-ch88, adc1

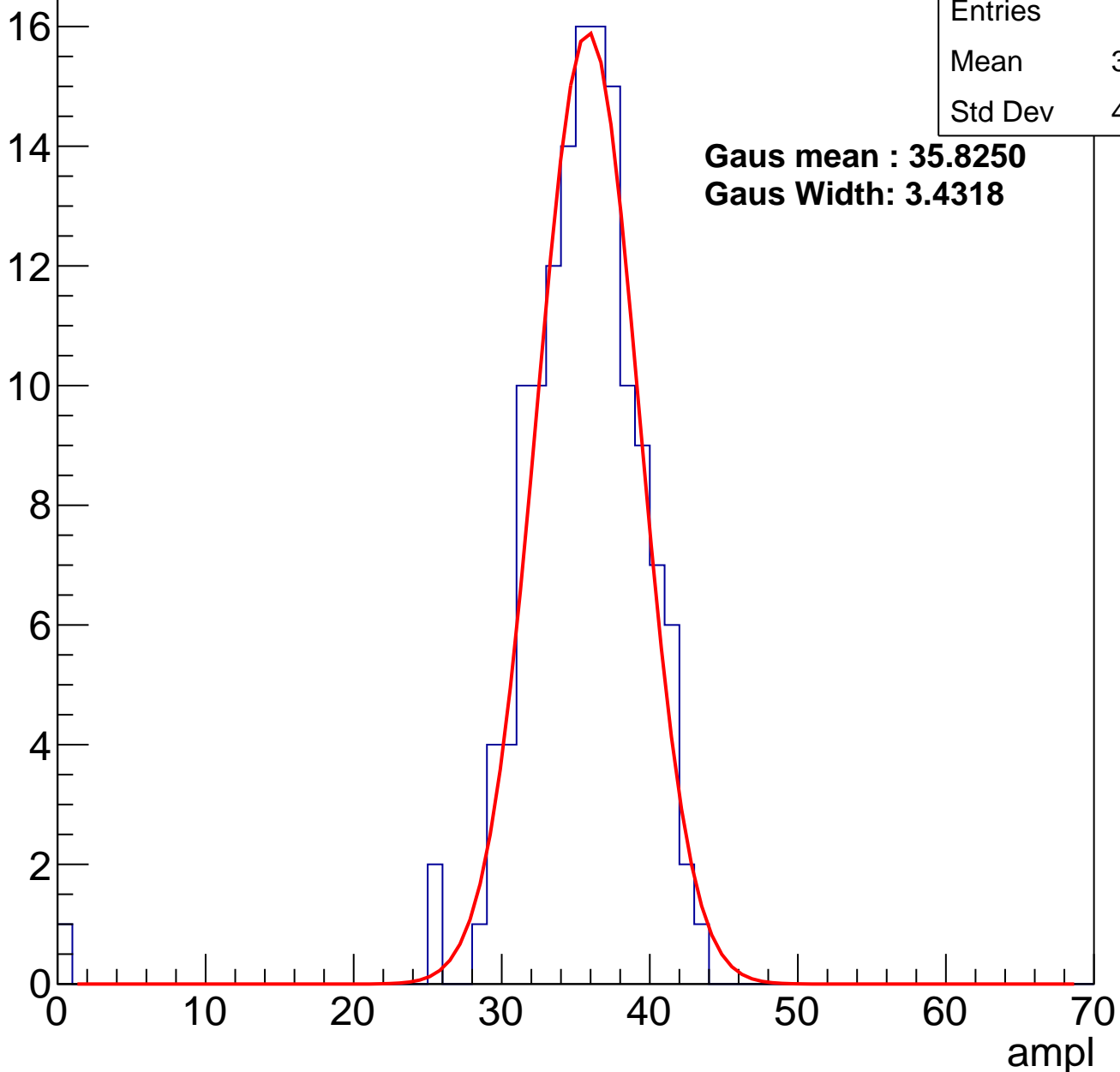
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	34.94
Std Dev	4.545

Gaus mean : 35.8250

Gaus Width: 3.4318

Entry



B0L101S, U21-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

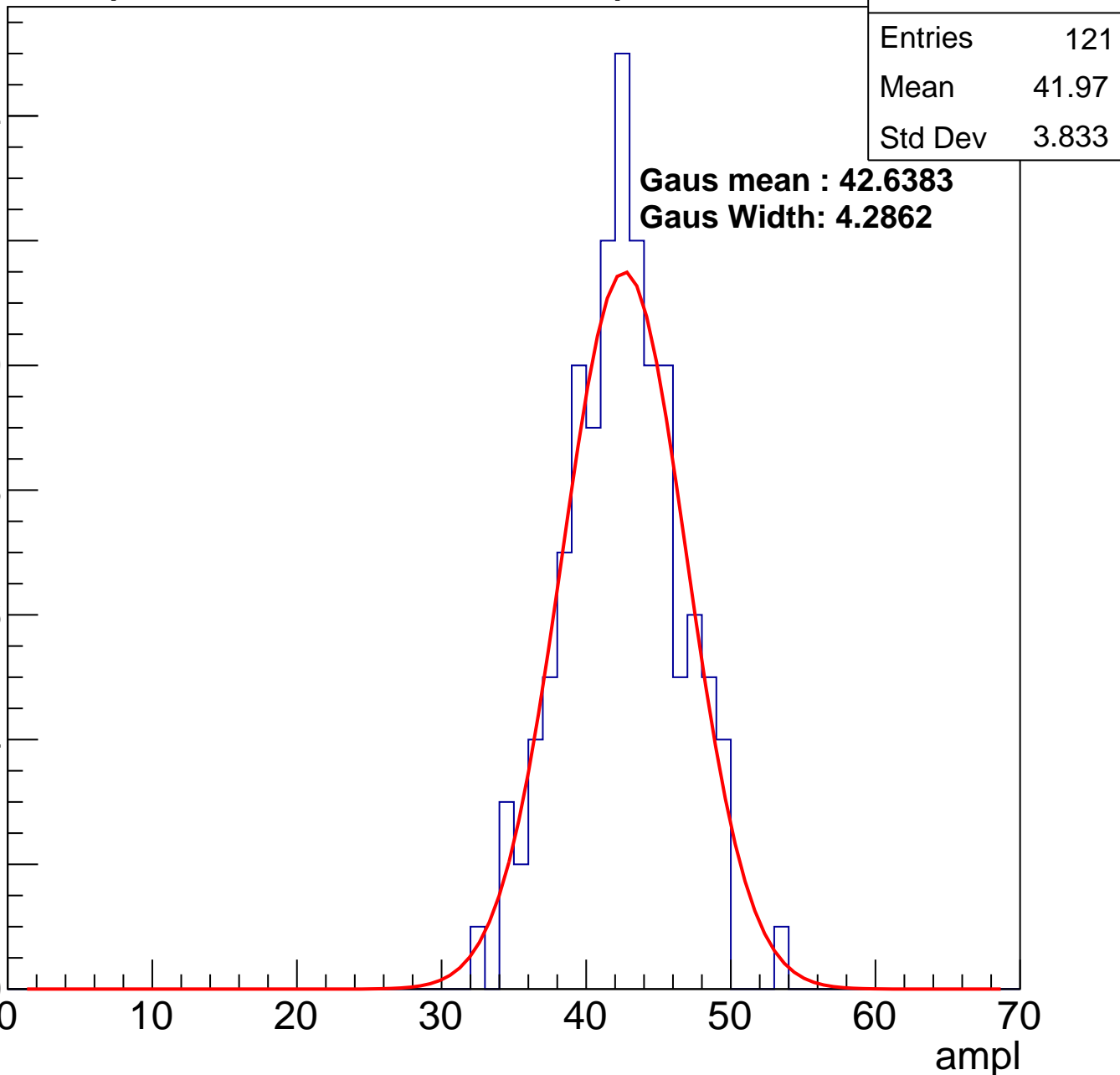
Entries	121
Mean	41.97
Std Dev	3.833

Gaus mean : 42.6383

Gaus Width: 4.2862

0 10 20 30 40 50 60 70

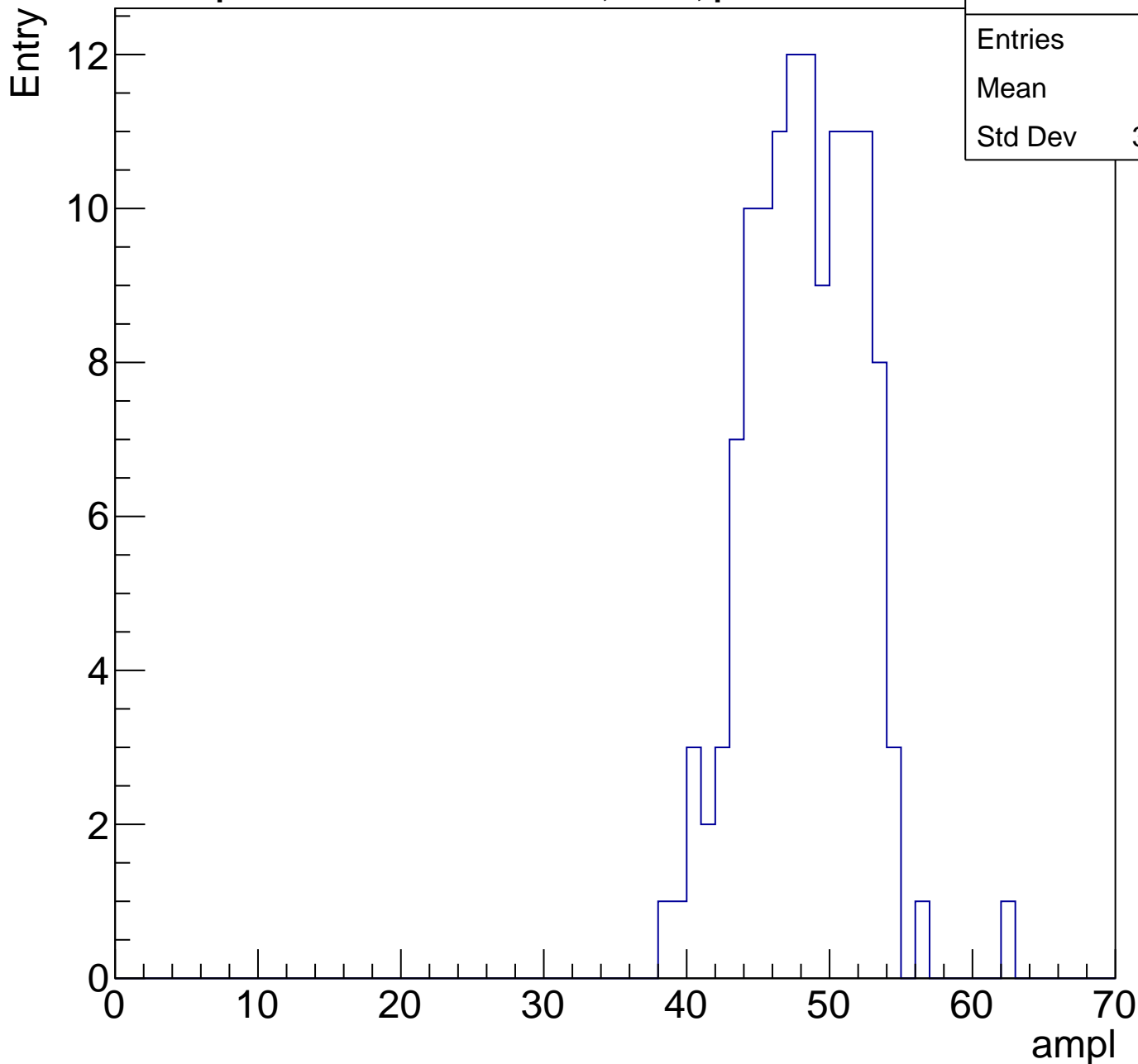
ampl



B0L101S, U21-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

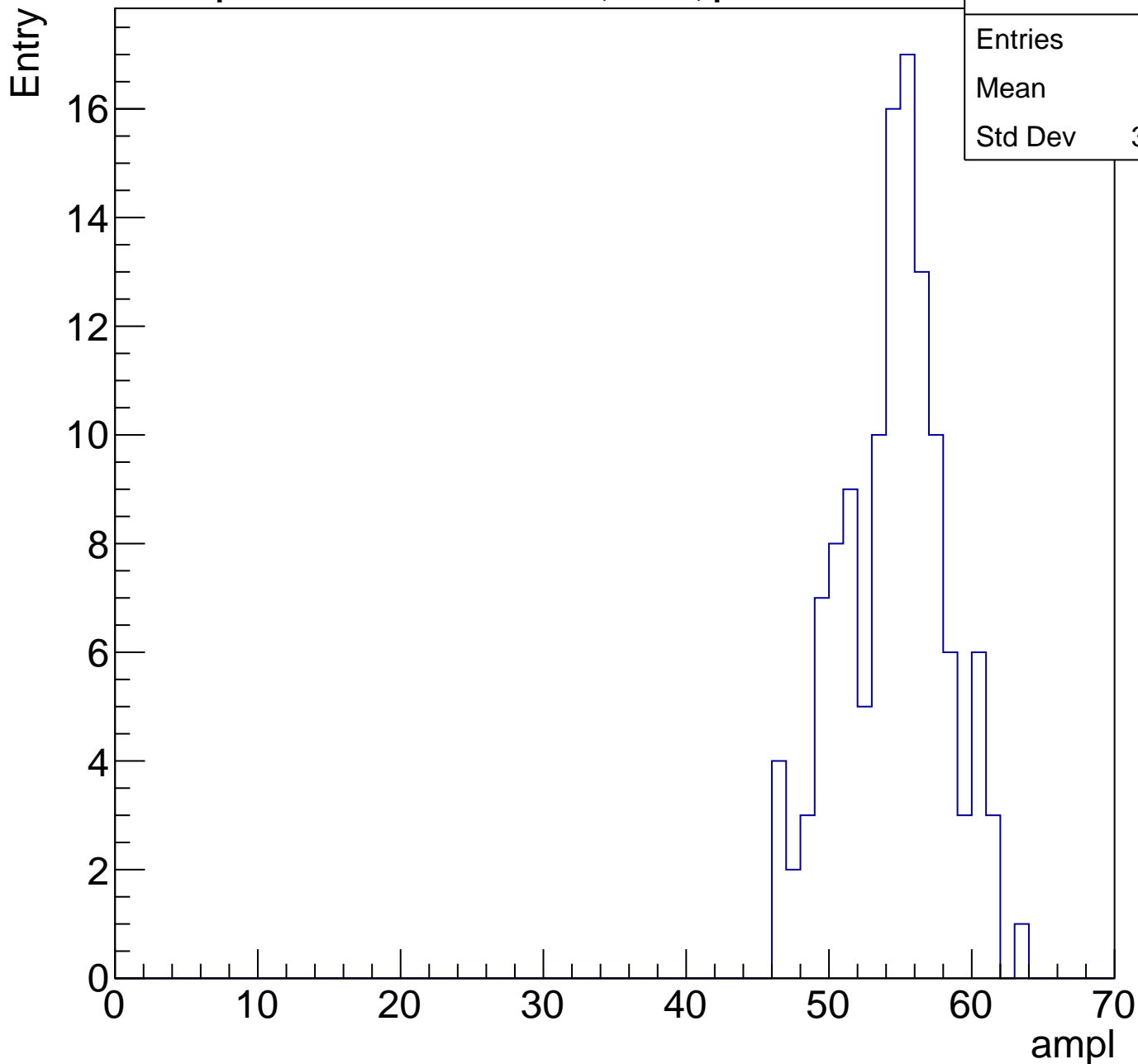
Entries	127
Mean	47.8
Std Dev	3.919



B0L101S, U21-ch88, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	54
Std Dev	3.663



B0L101S, U21-ch88, adc5

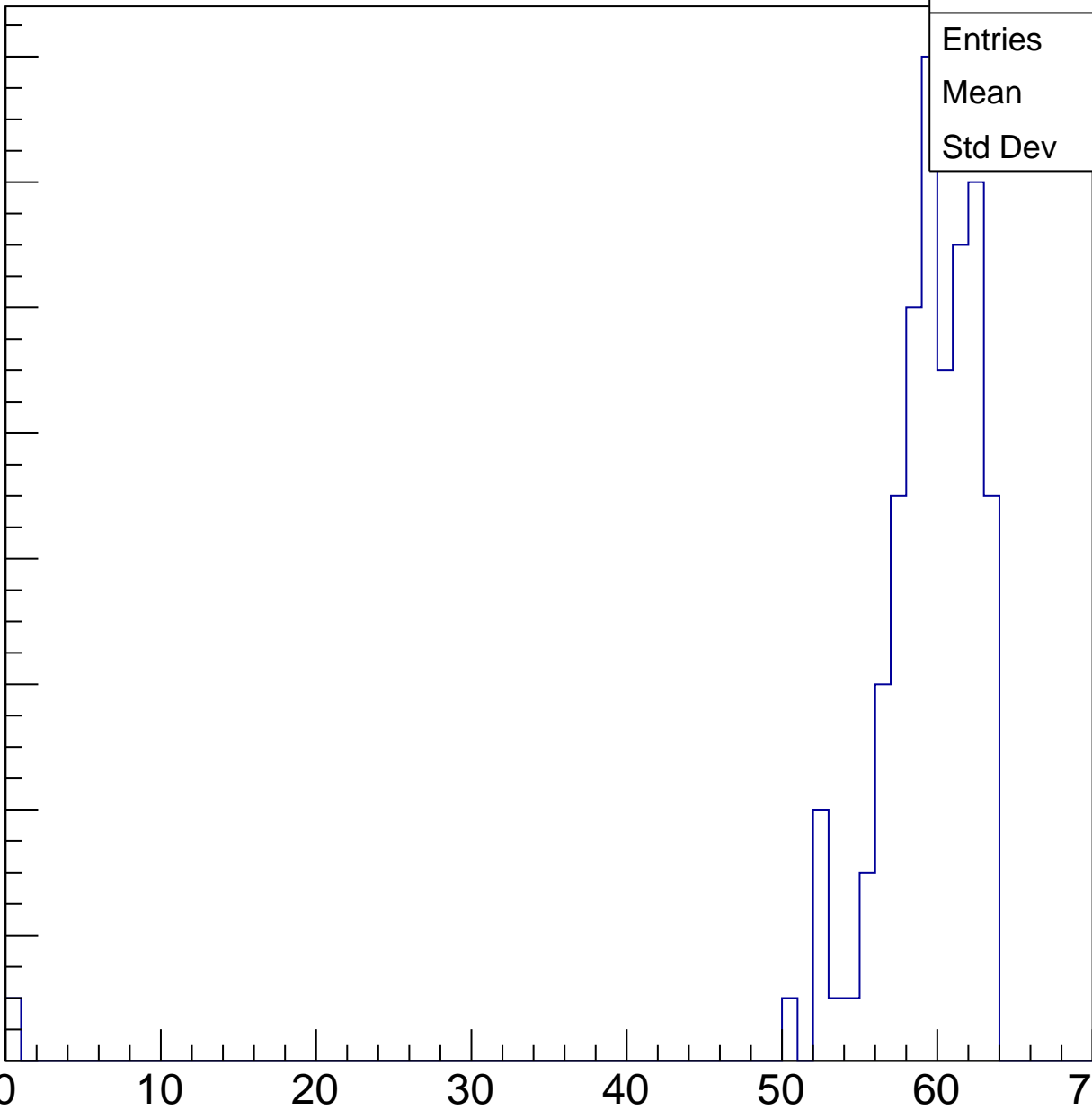
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.49
Std Dev	6.506

ampl

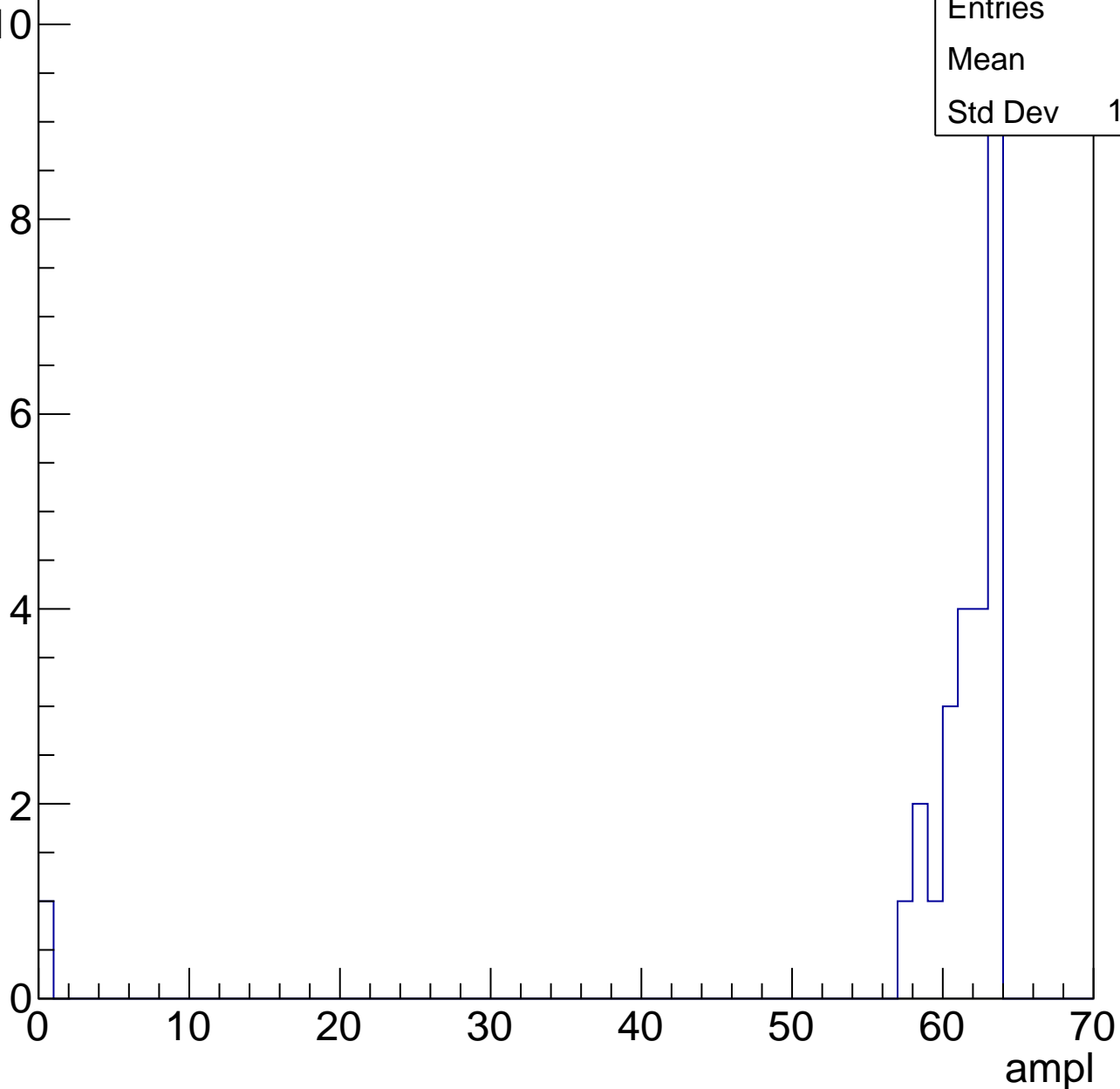


B0L101S, U21-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	26
Mean	59
Std Dev	11.93



B0L101S, U21-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch89, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	29.62
Std Dev	3.581

Gaus mean : 30.0882

Gaus Width: 3.8822

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

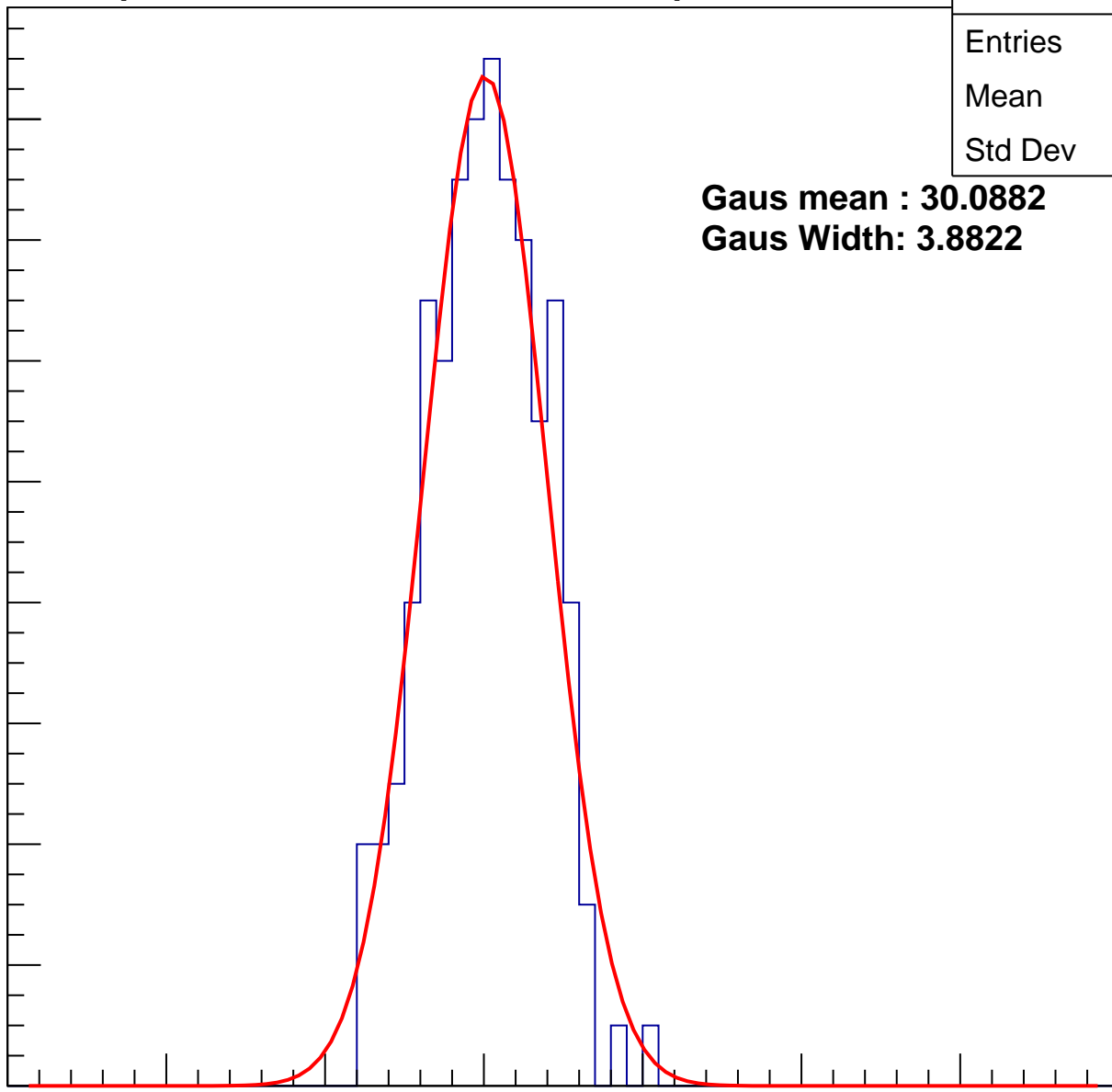
40

50

60

70

ampl



B0L101S, U21-ch89, adc1

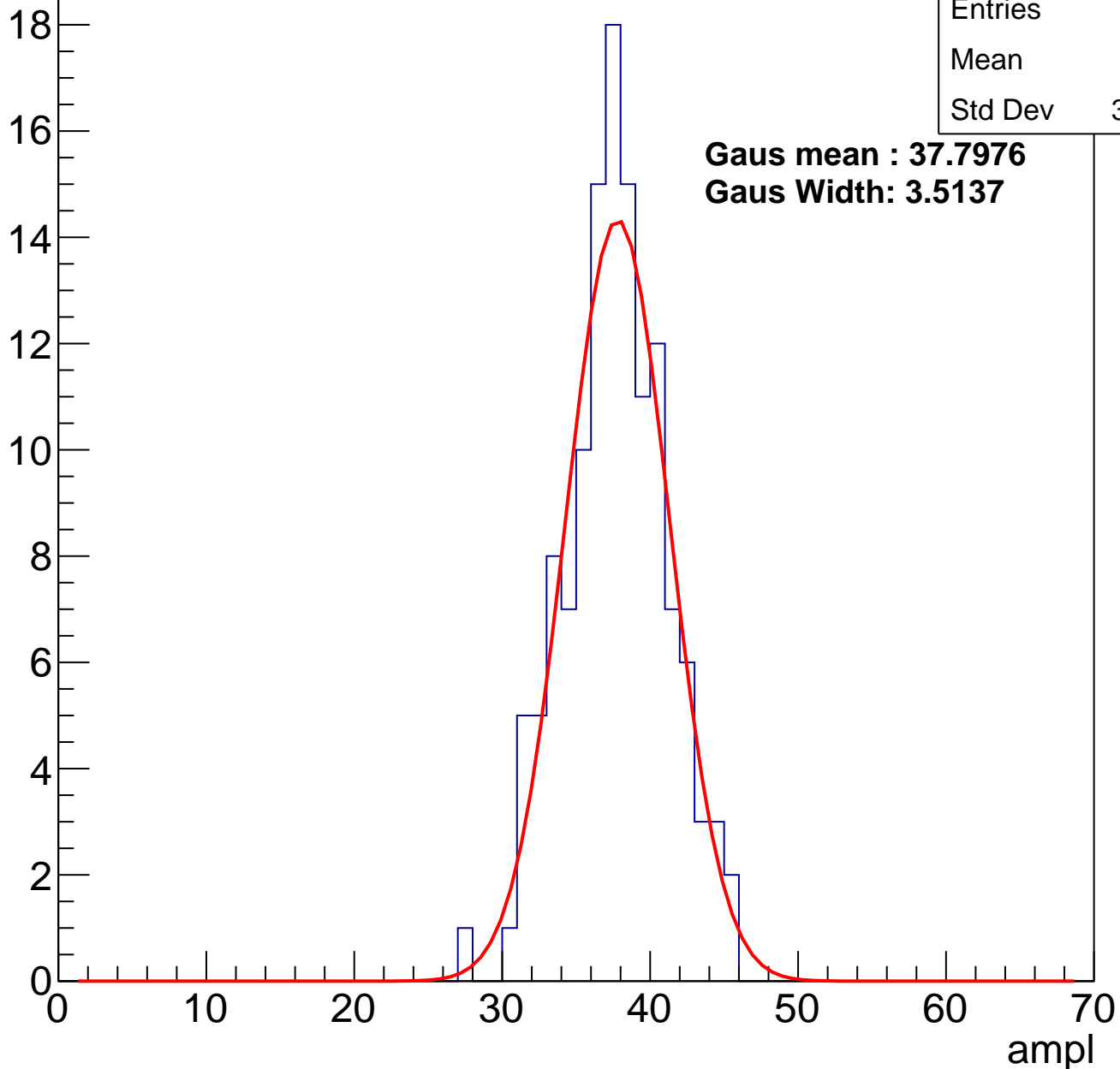
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	37.2
Std Dev	3.418

Gaus mean : 37.7976

Gaus Width: 3.5137

Entry



B0L101S, U21-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	43.48
Std Dev	3.389

Gaus mean : 43.9544

Gaus Width: 3.3224

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

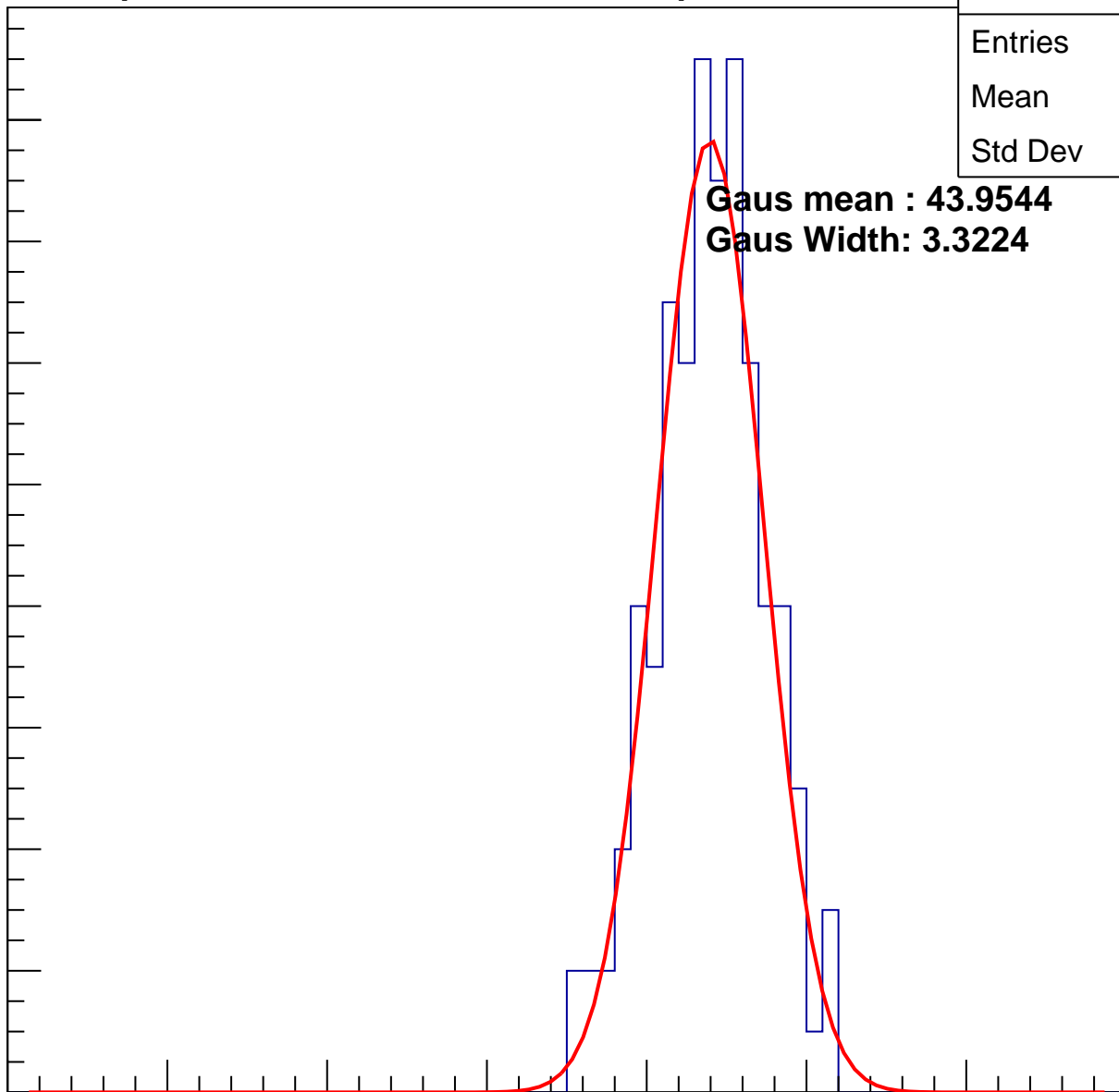
40

50

60

70

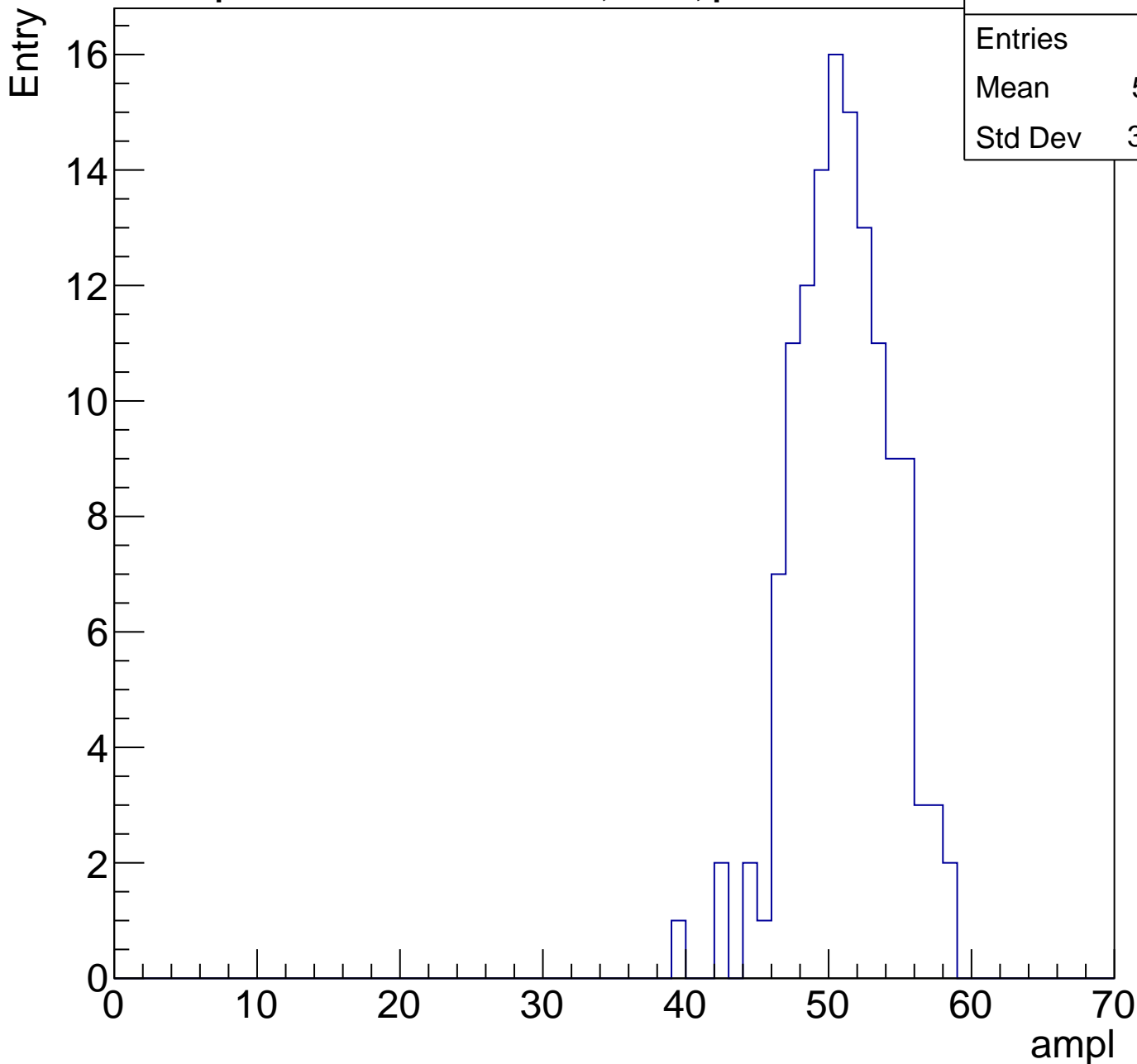
ampl



B0L101S, U21-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	50.51
Std Dev	3.387



B0L101S, U21-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

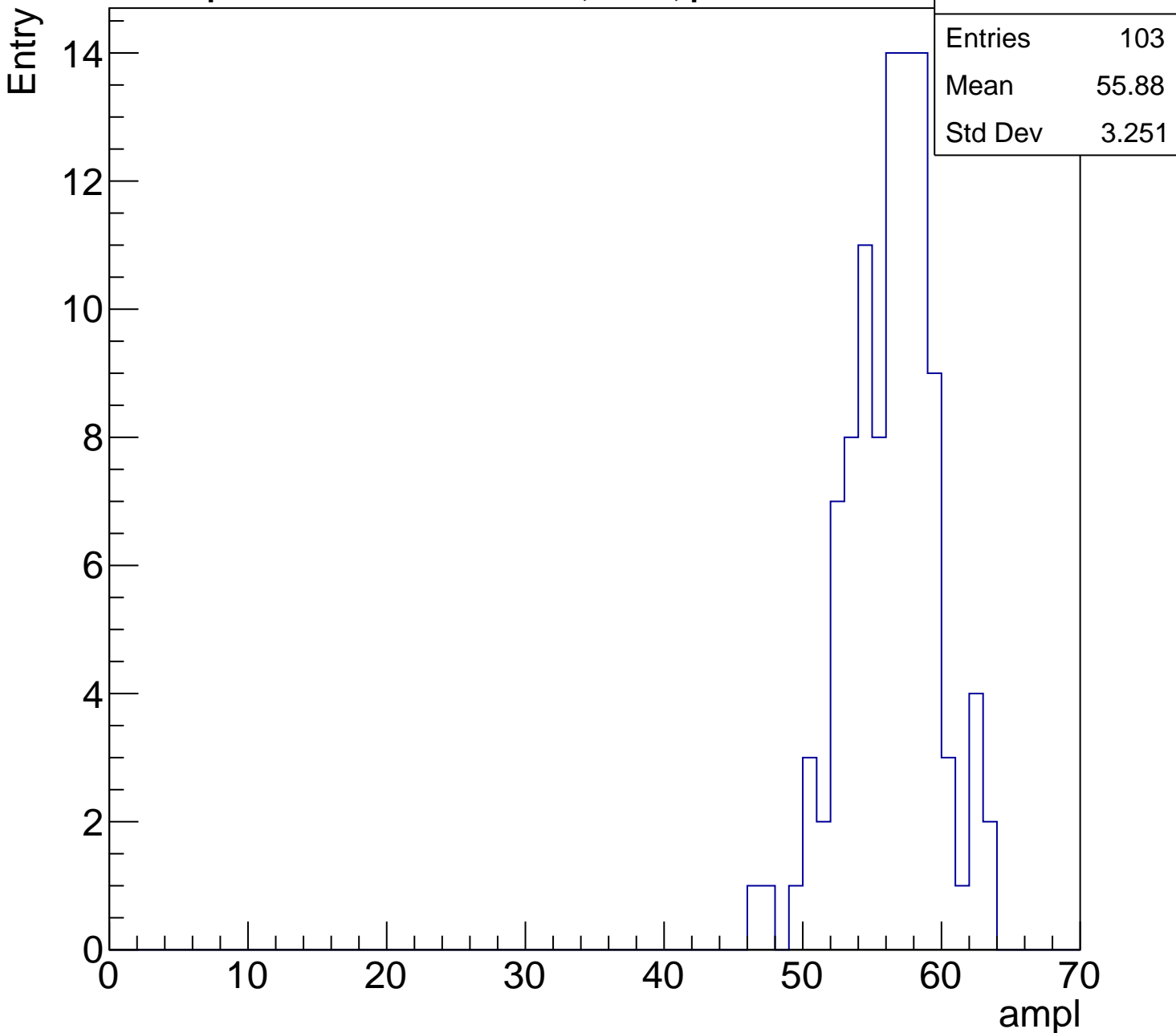
Entries	103
Mean	55.88
Std Dev	3.251

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch89, adc5

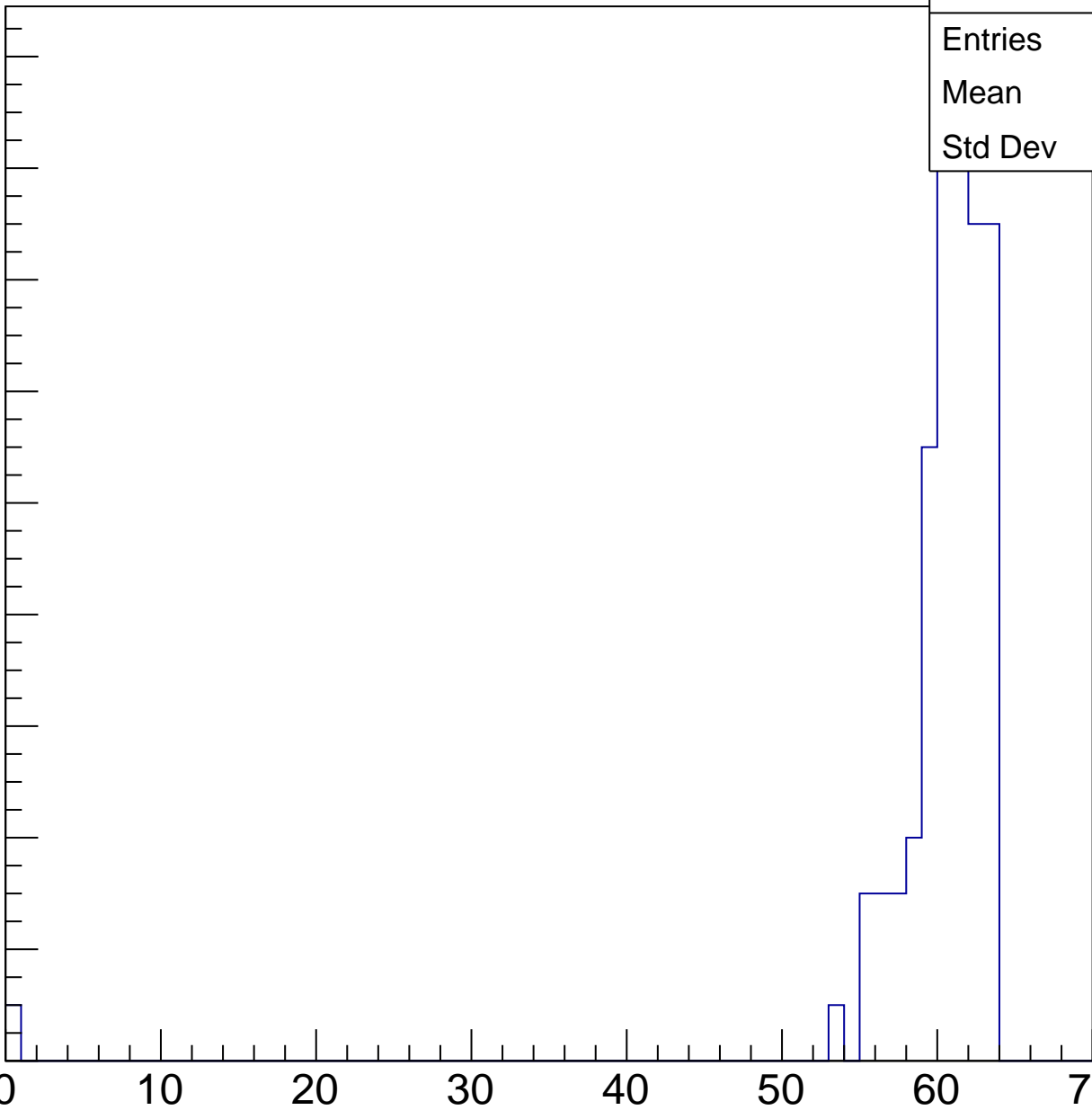
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.68
Std Dev	6.655

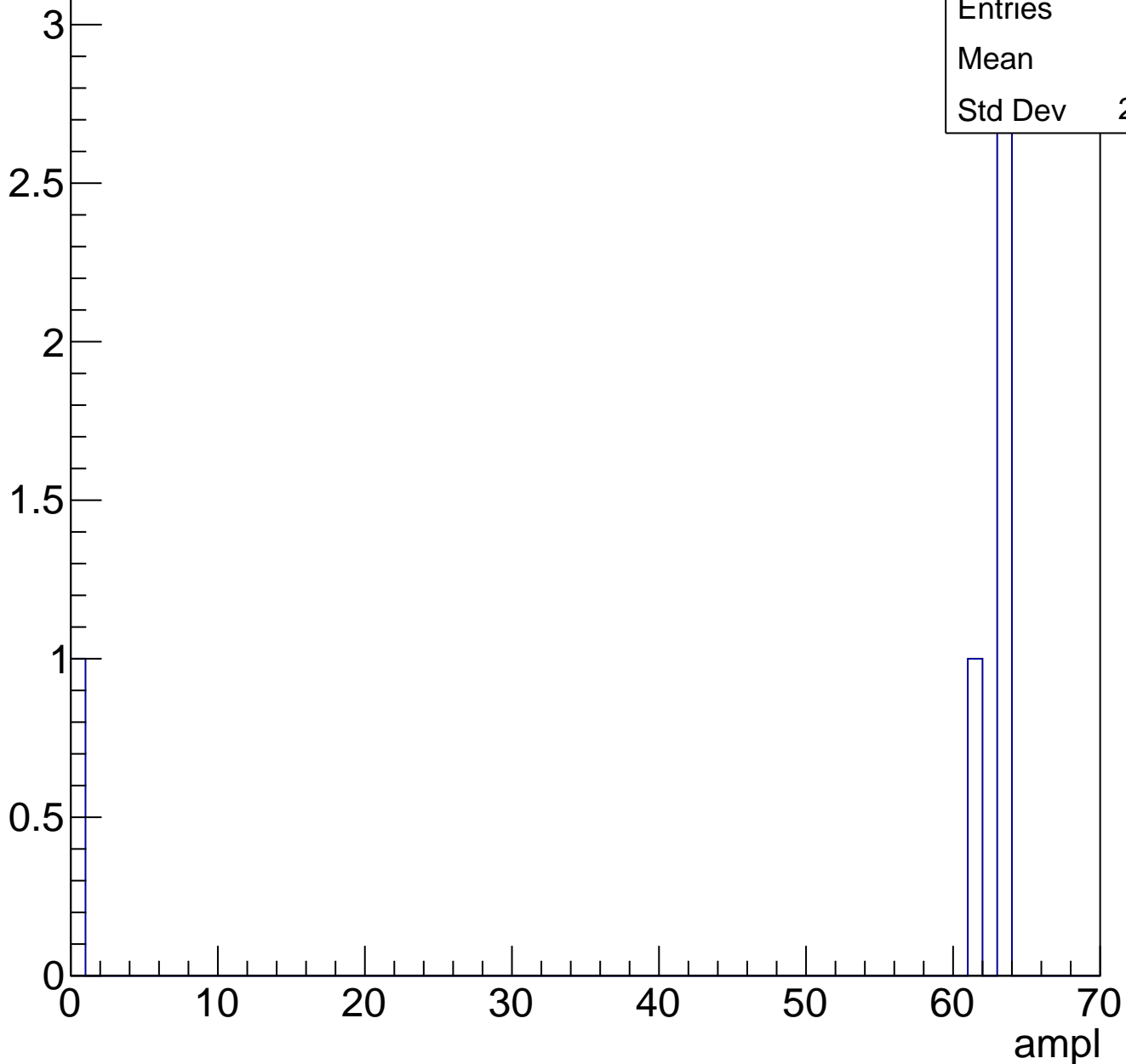
ampl



B0L101S, U21-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

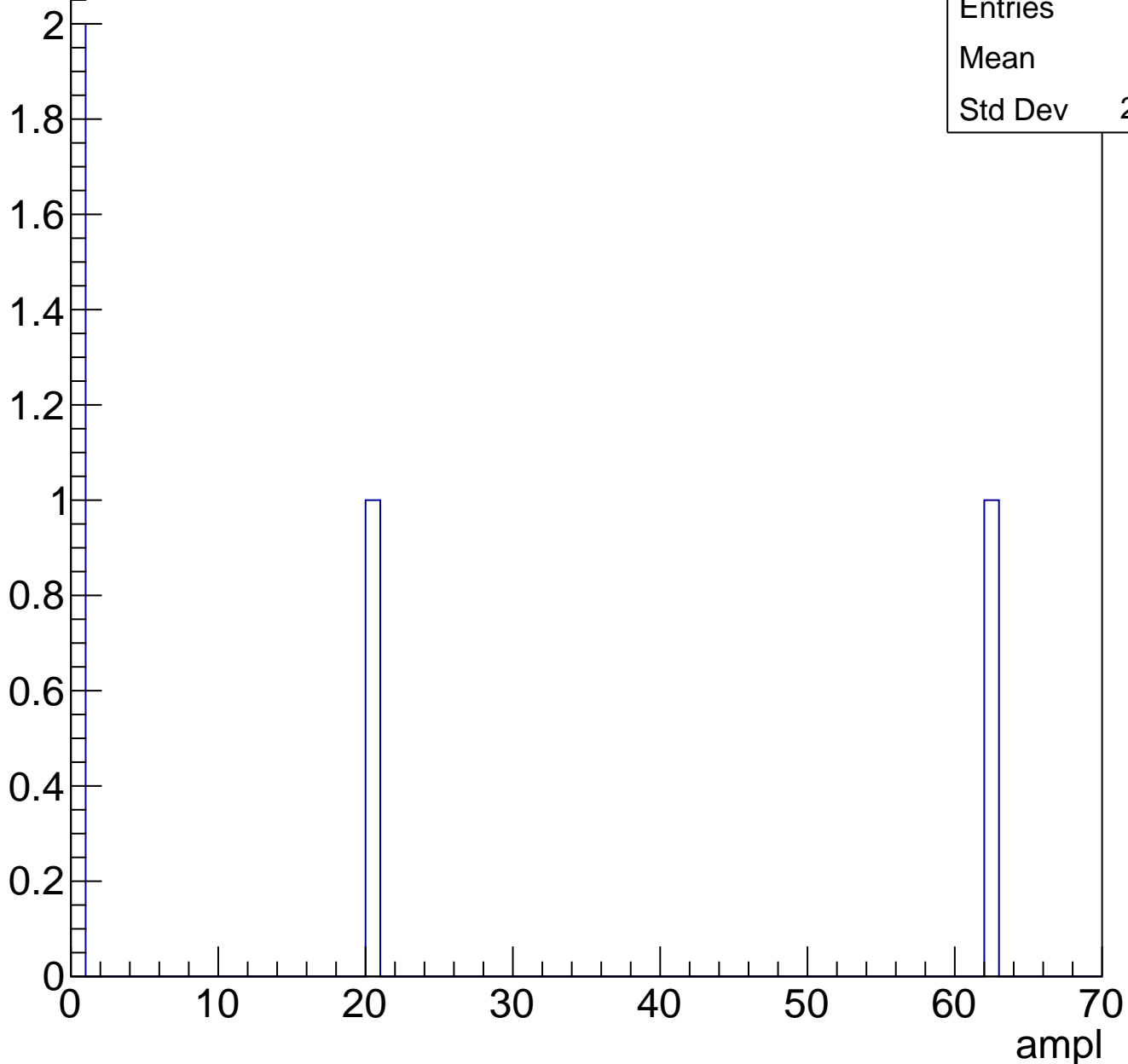
Entry



B0L101S, U21-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	20.5
Std Dev	25.31

B0L101S, U21-ch90, adc0

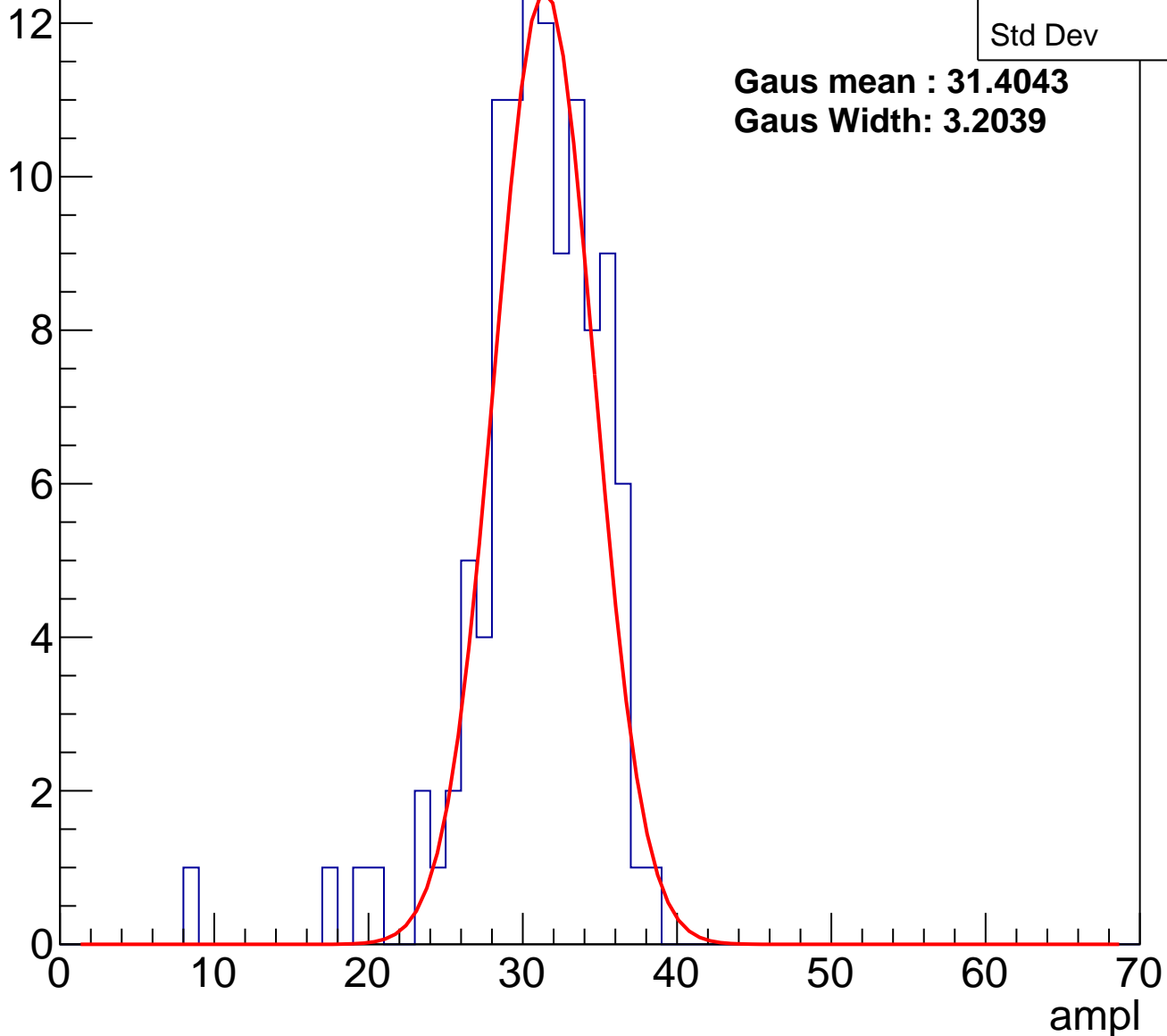
calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	30.36
Std Dev	4.31

Gaus mean : 31.4043

Gaus Width: 3.2039

Entry



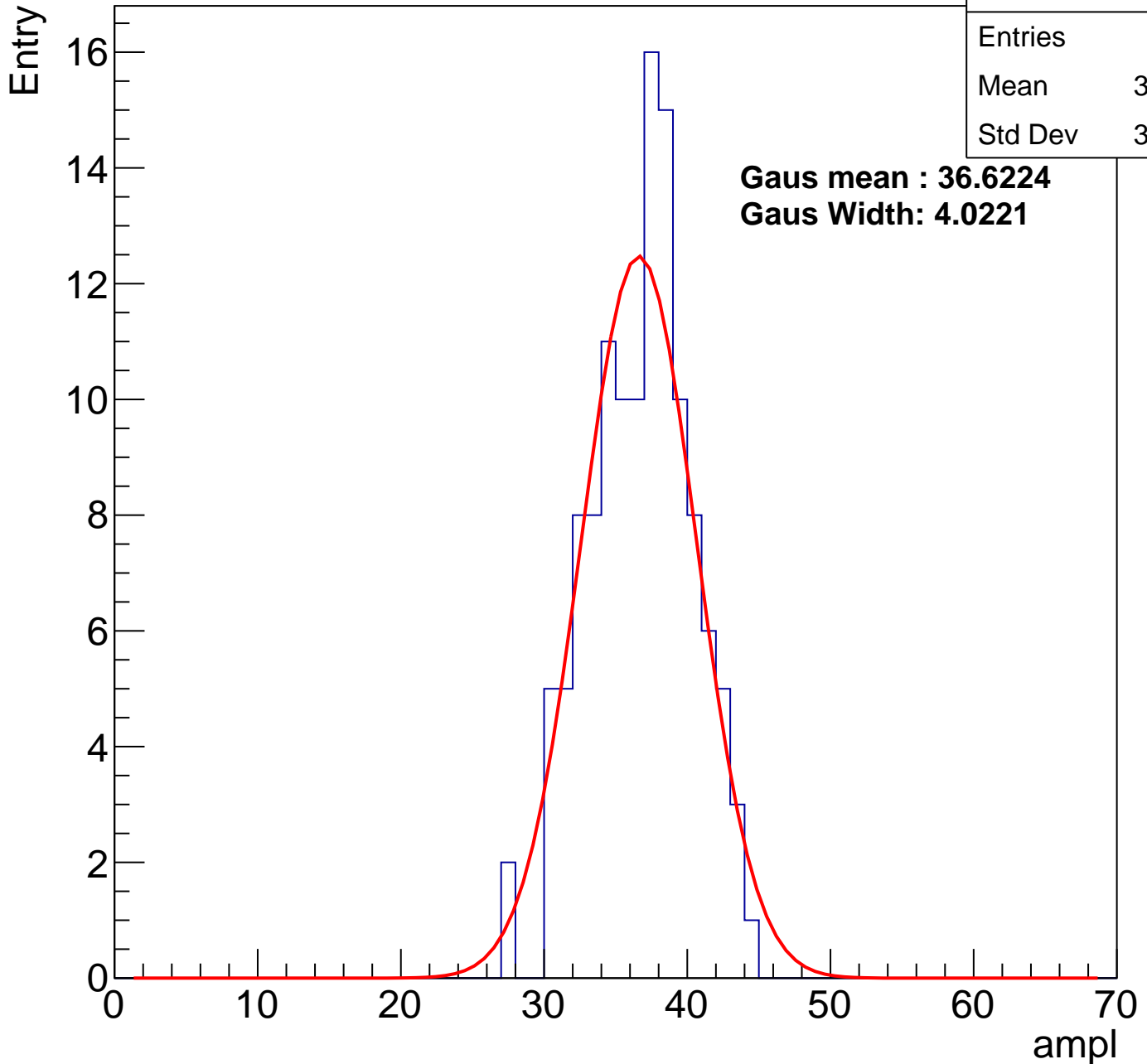
B0L101S, U21-ch90, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.29
Std Dev	3.545

Gaus mean : 36.6224

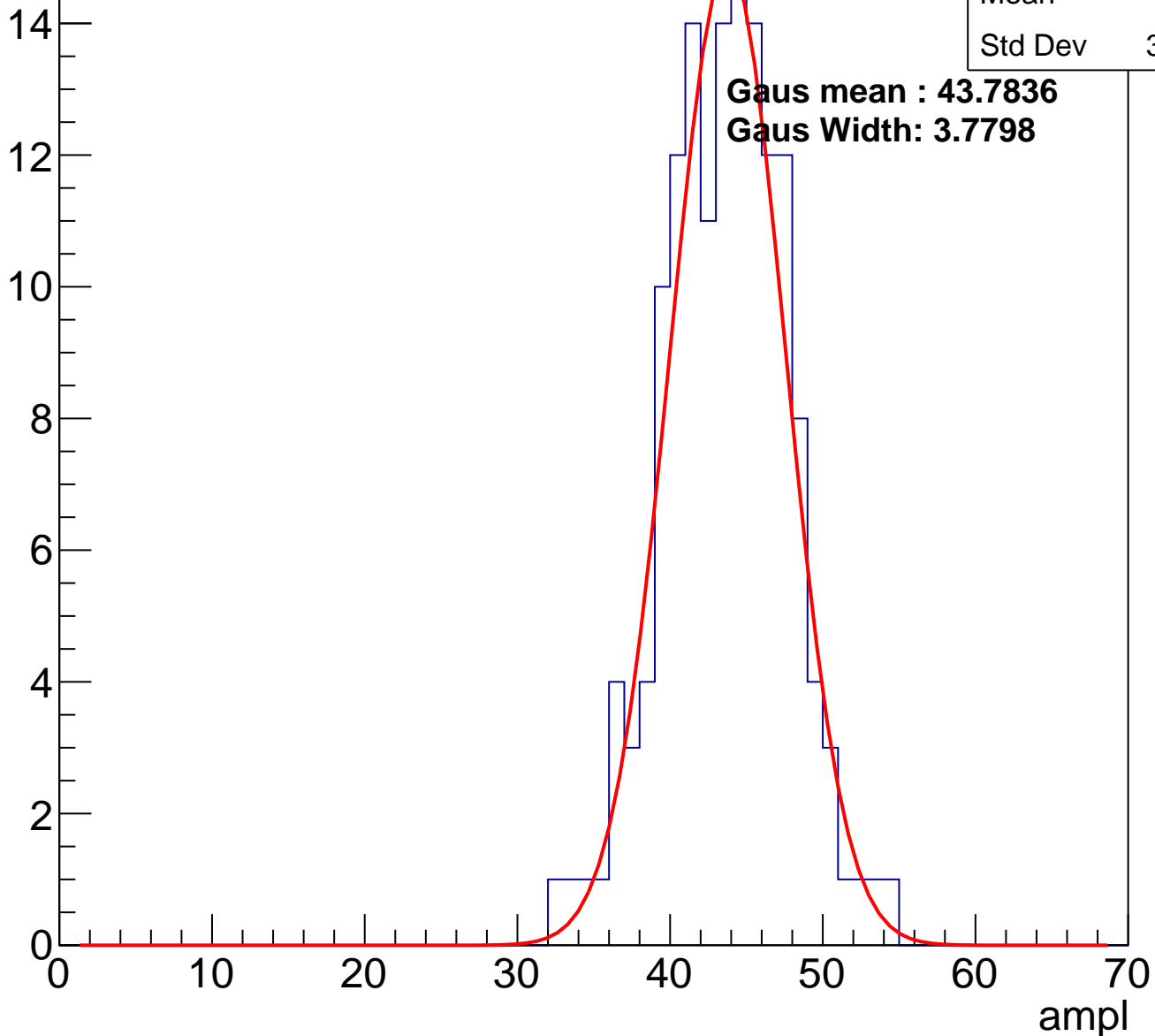
Gaus Width: 4.0221



B0L101S, U21-ch90, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	148
Mean	43.21
Std Dev	3.965

B0L101S, U21-ch90, adc3

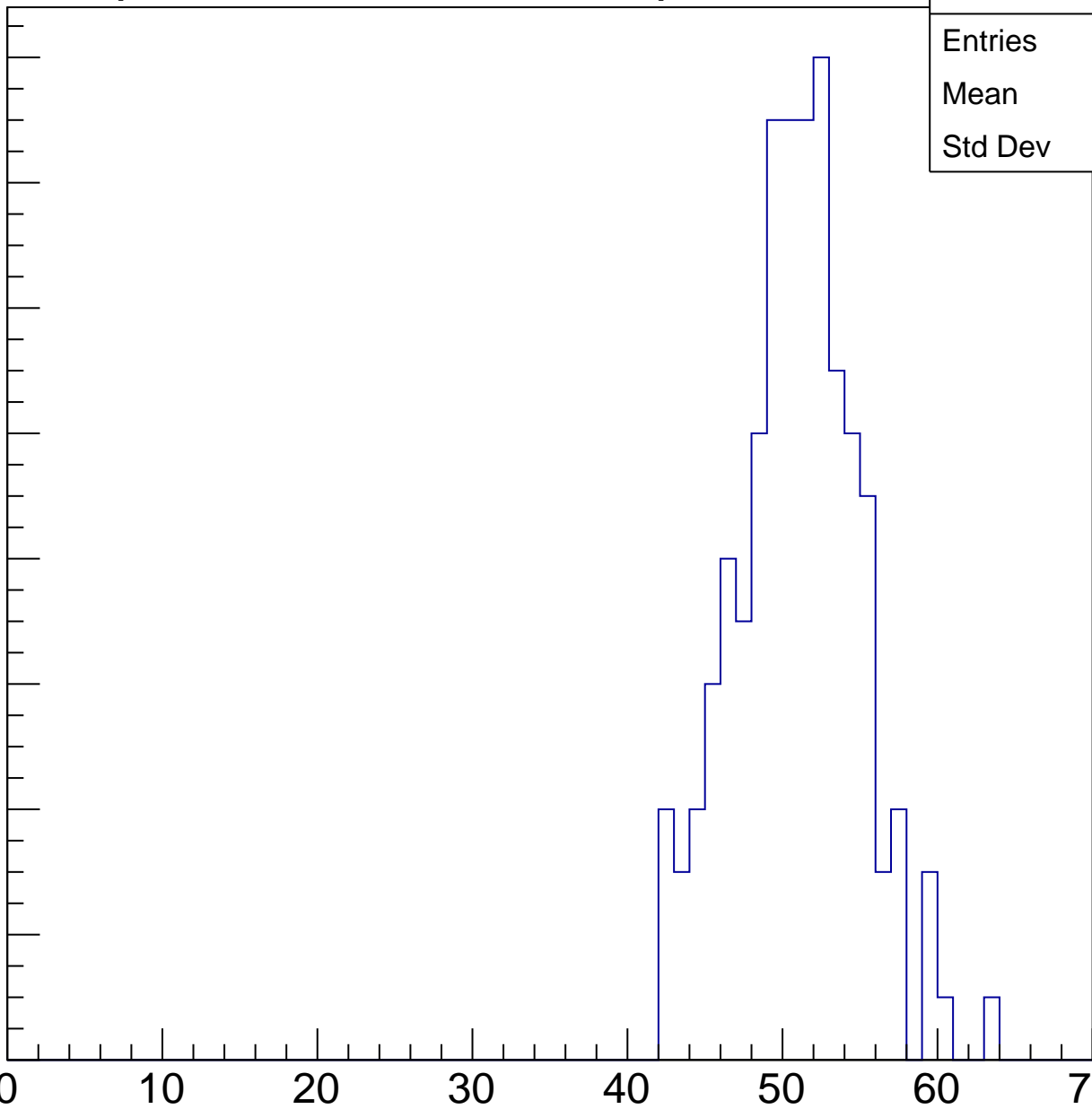
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	145
Mean	50.46
Std Dev	3.976

ampl



B0L101S, U21-ch90, adc4

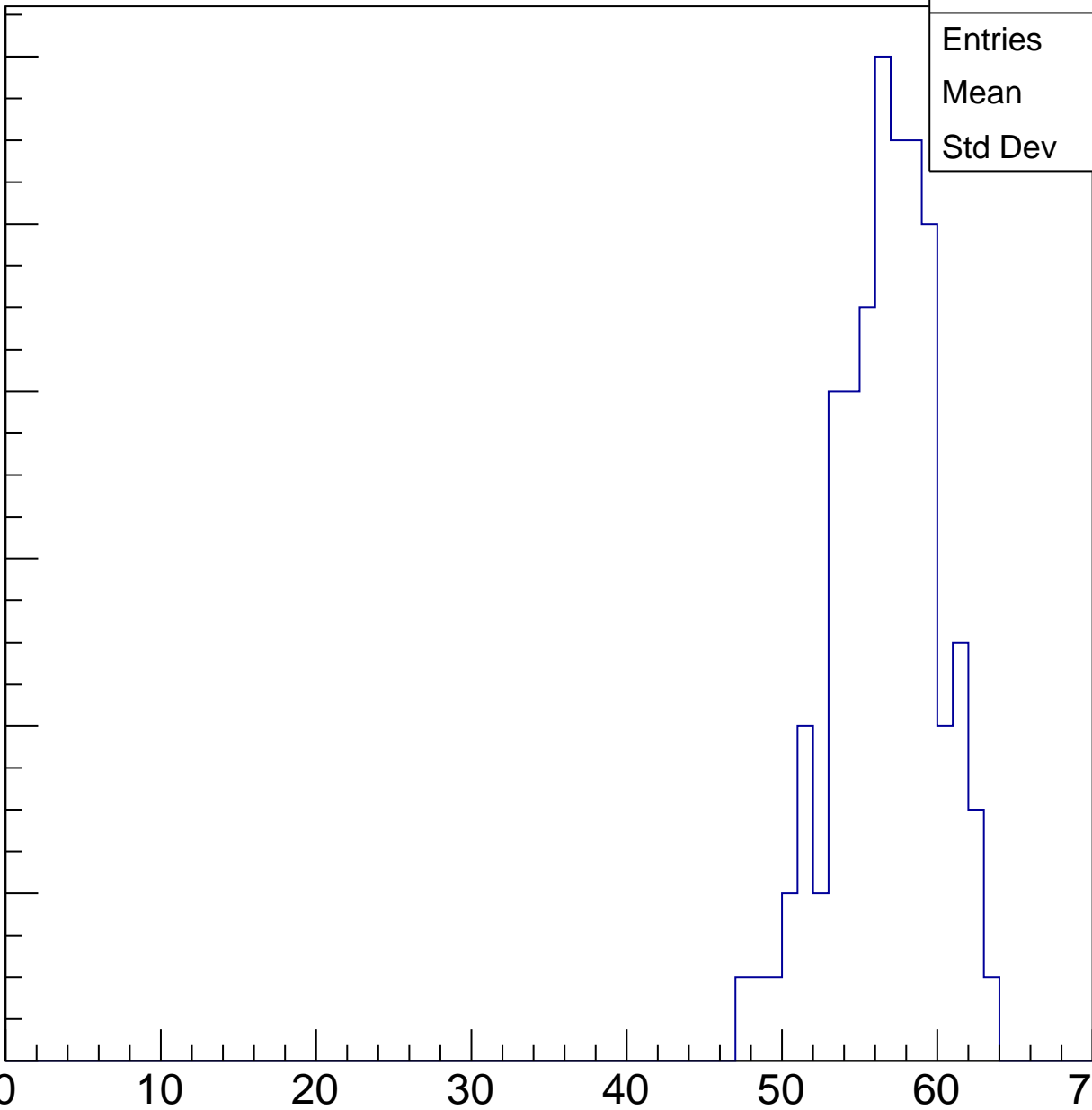
calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	56.17
Std Dev	3.278

Entry

12
10
8
6
4
2
0

ampl



B0L101S, U21-ch90, adc5

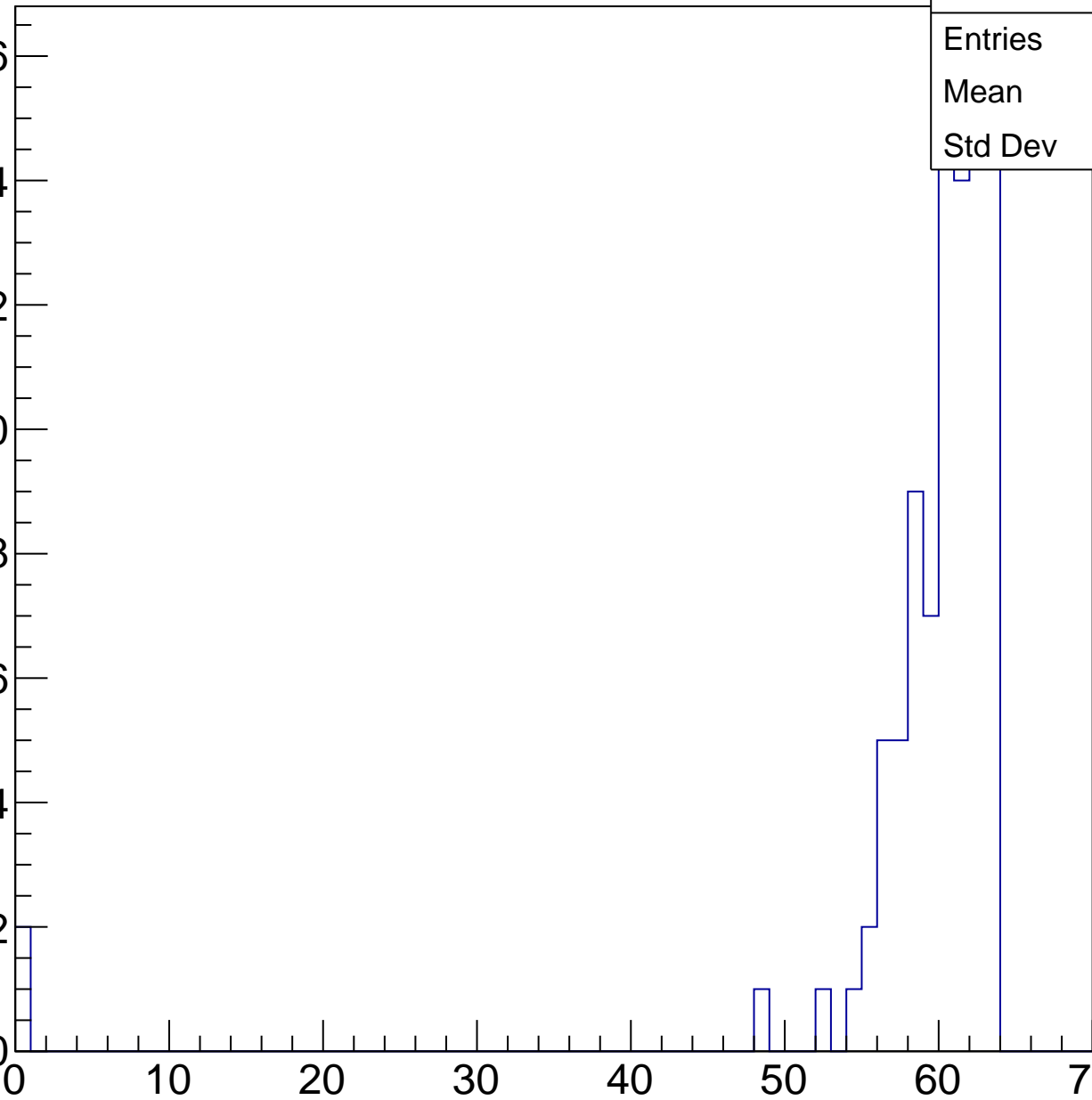
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	58.7
Std Dev	9.066

ampl



B0L101S, U21-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

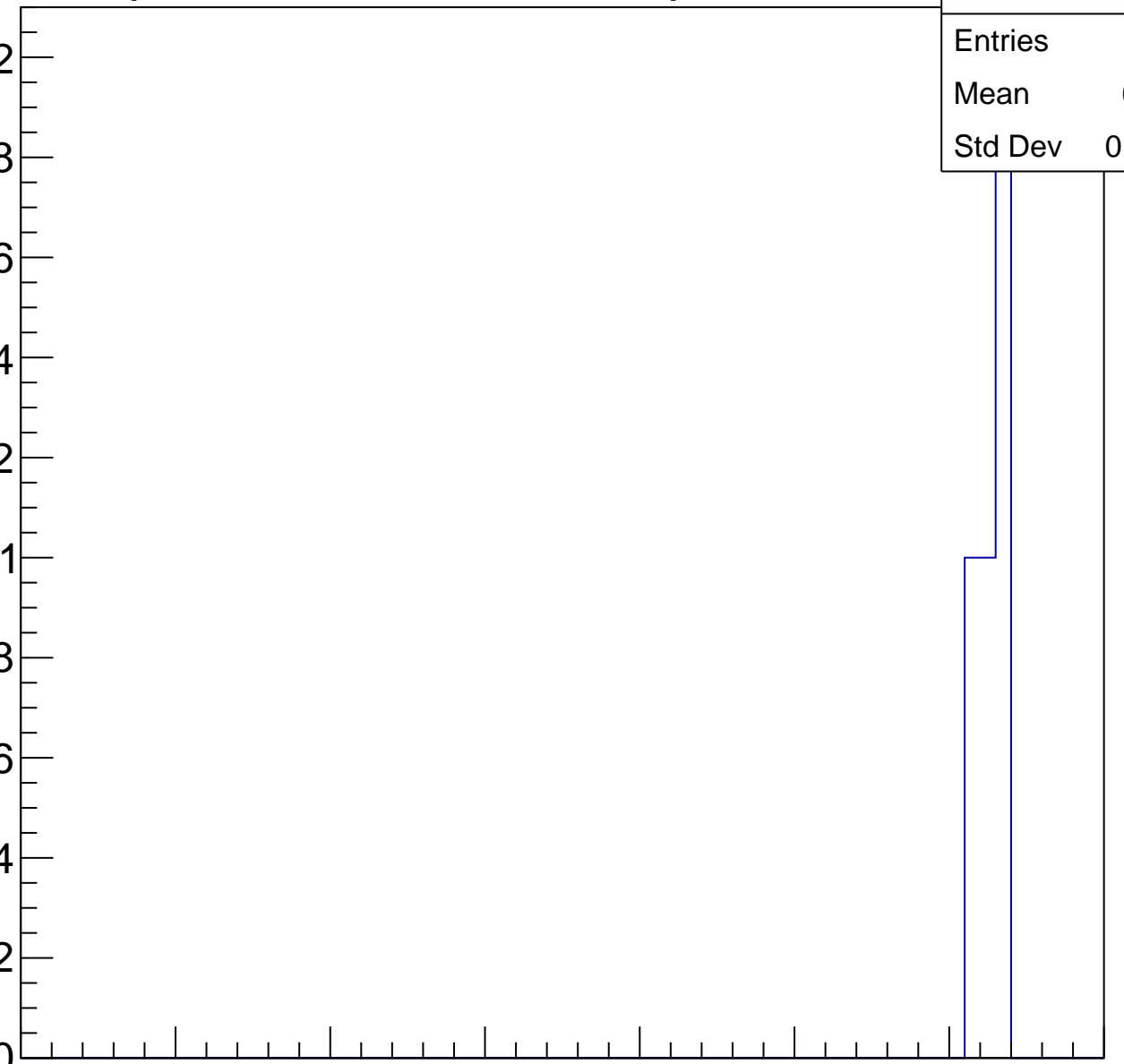
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch91, adc0

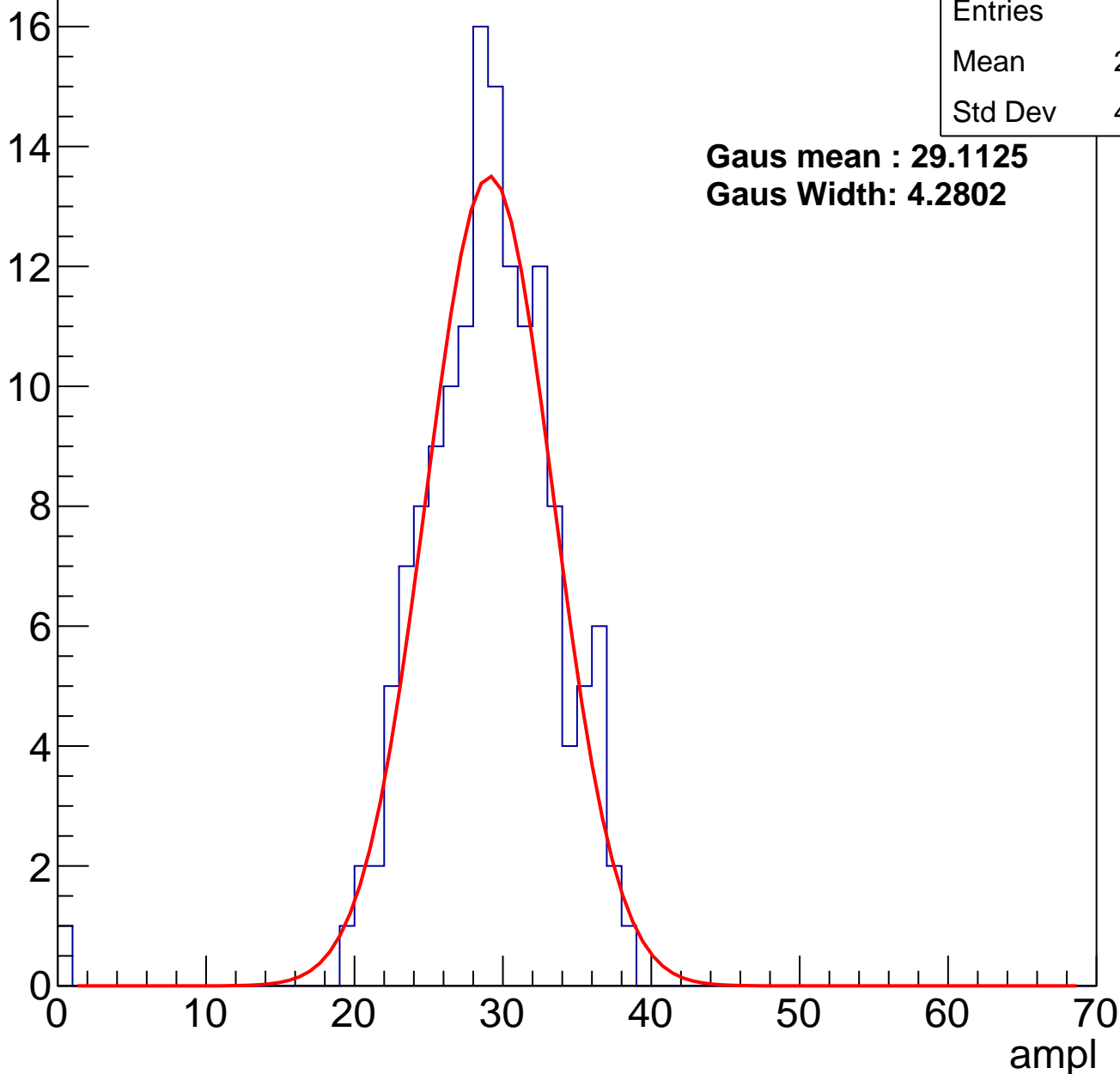
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	28.49
Std Dev	4.693

Gaus mean : 29.1125

Gaus Width: 4.2802

Entry



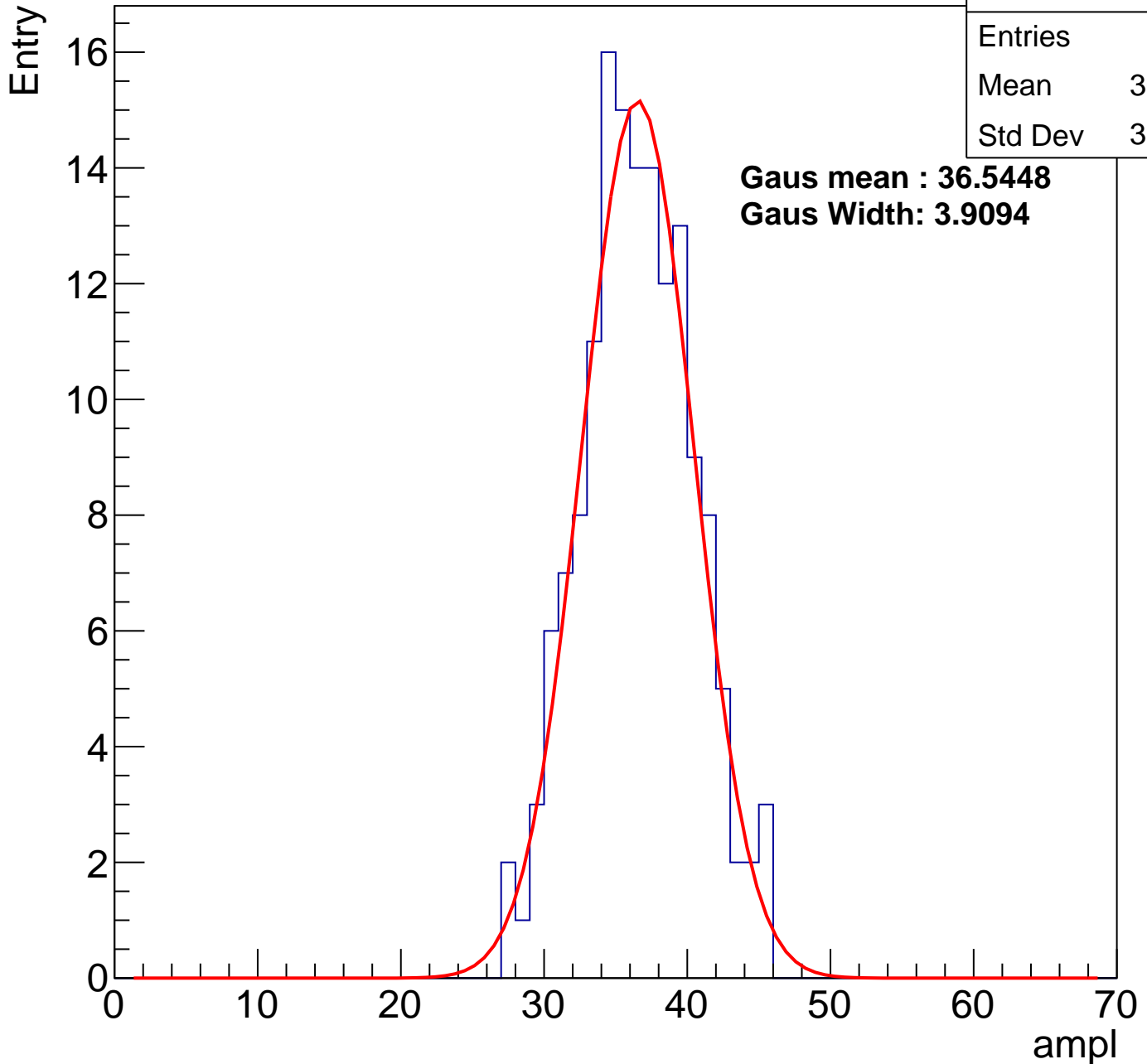
B0L101S, U21-ch91, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	36.07
Std Dev	3.853

Gaus mean : 36.5448

Gaus Width: 3.9094



B0L101S, U21-ch91, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

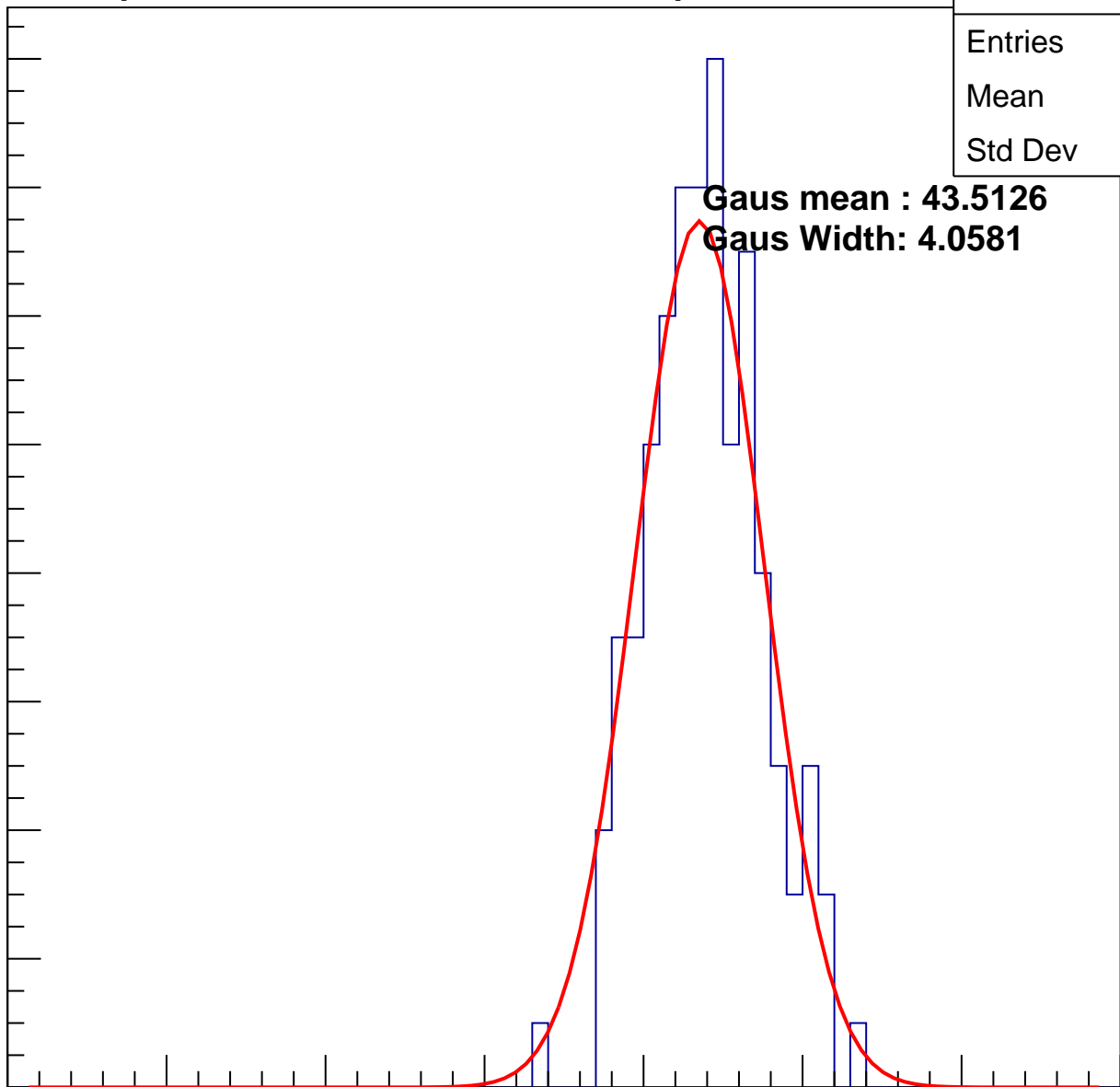
Entries	133
Mean	43.41
Std Dev	3.609

Gaus mean : 43.5126

Gaus Width: 4.0581

ampl

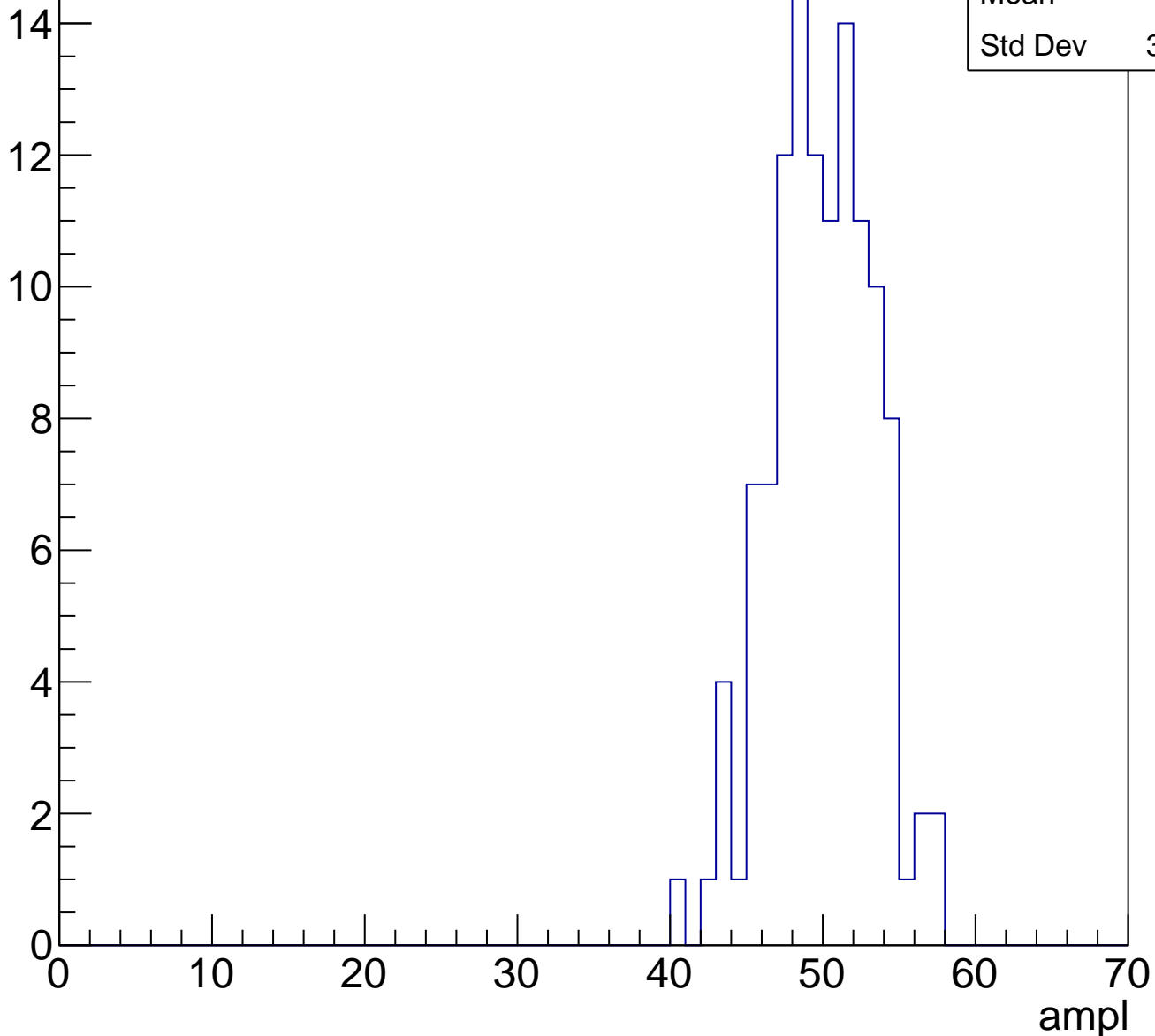
0 10 20 30 40 50 60 70



B0L101S, U21-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

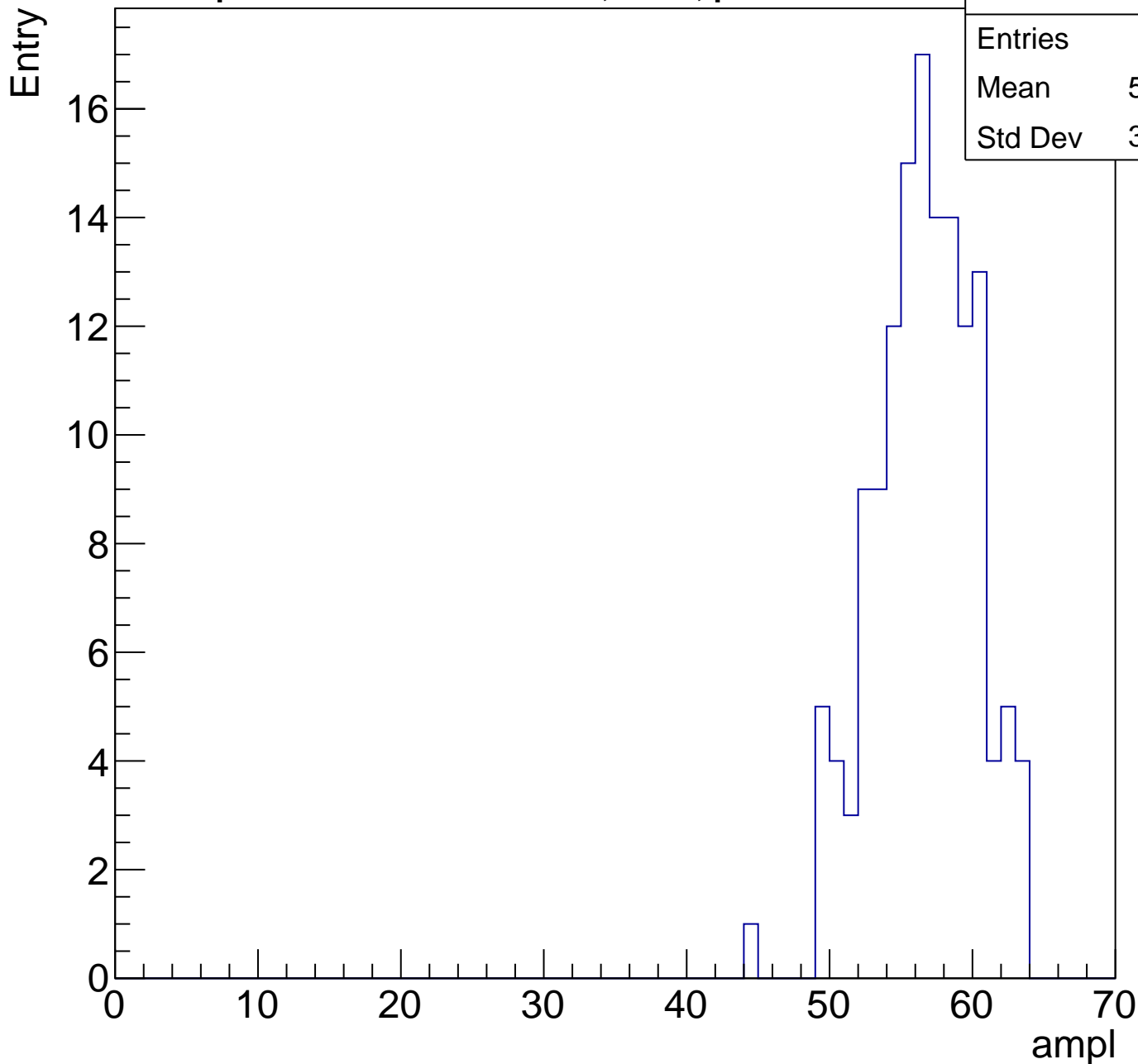
Entry



B0L101S, U21-ch91, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	56.14
Std Dev	3.528



B0L101S, U21-ch91, adc5

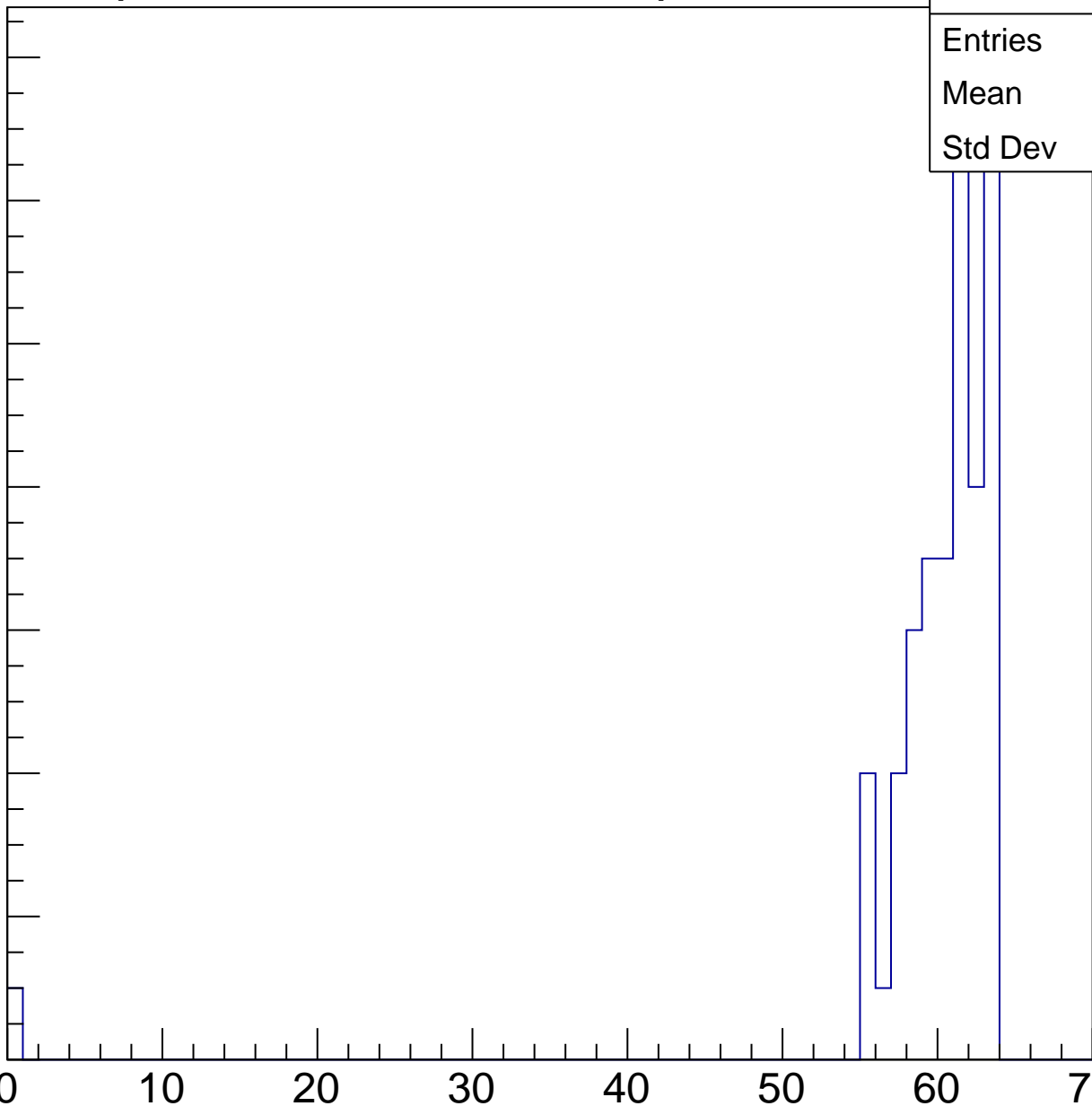
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	59.29
Std Dev	7.759

ampl

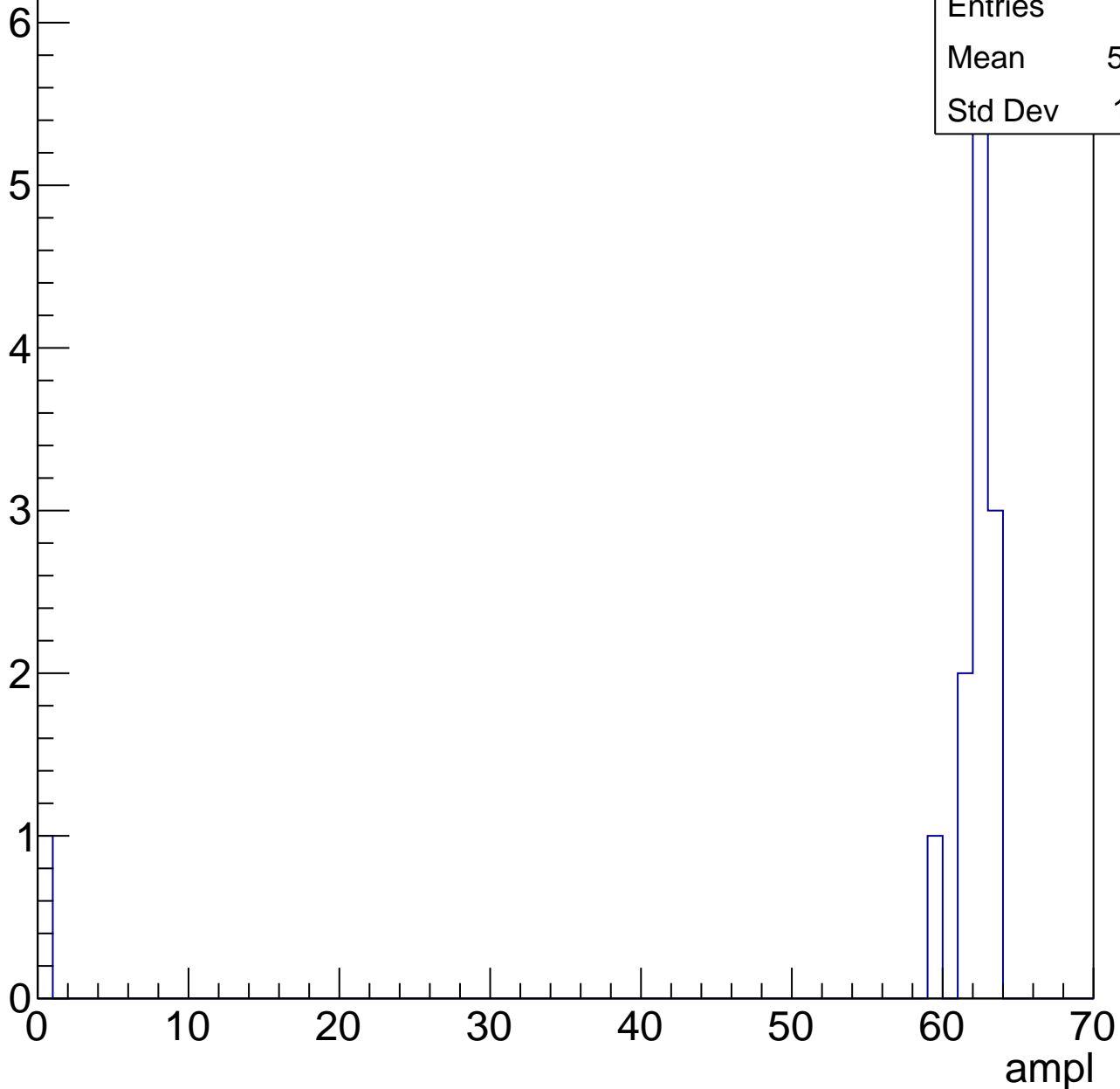


B0L101S, U21-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.08
Std Dev	16.51



B0L101S, U21-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch92, adc0

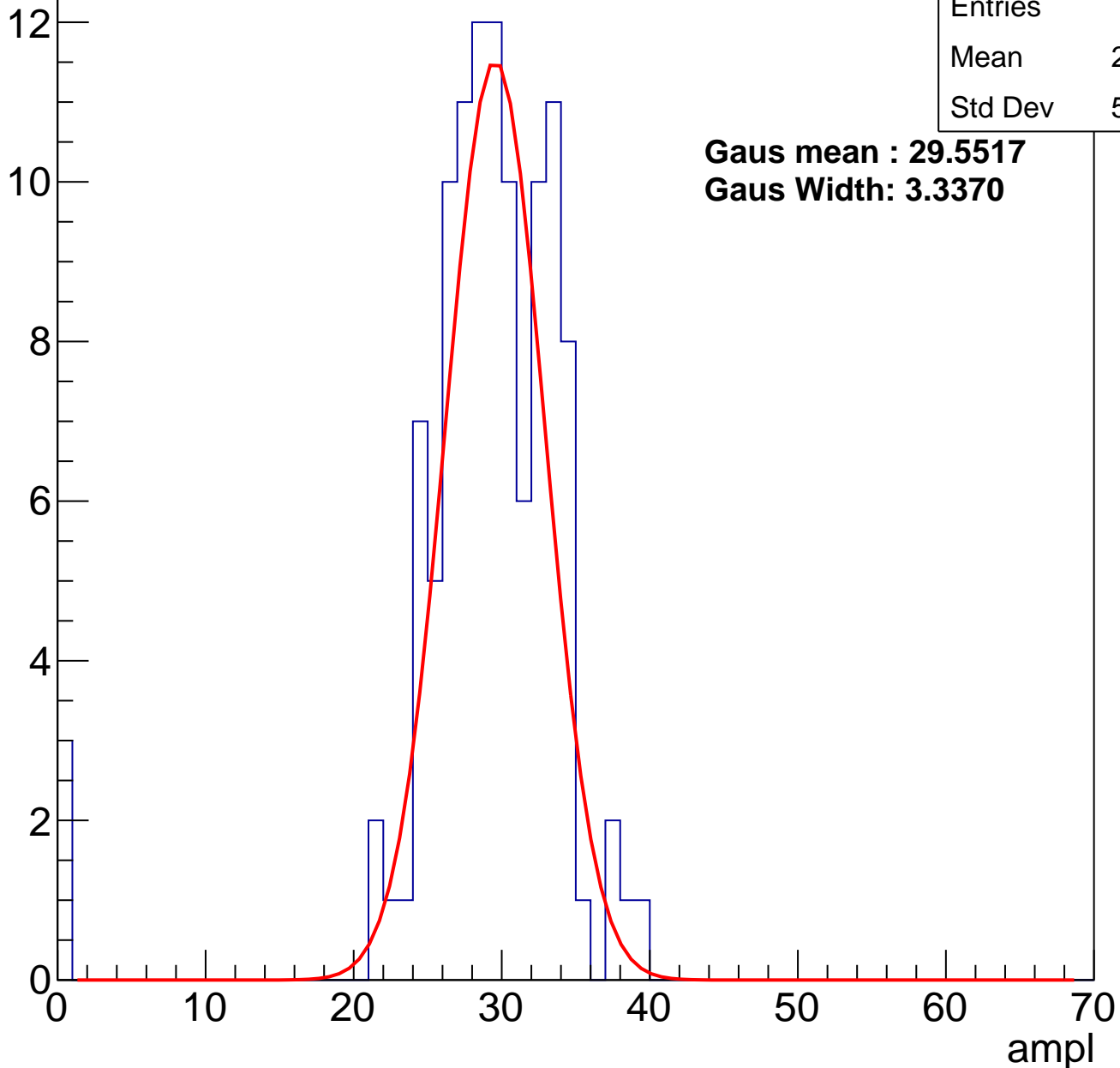
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	28.49
Std Dev	5.888

Gaus mean : 29.5517

Gaus Width: 3.3370

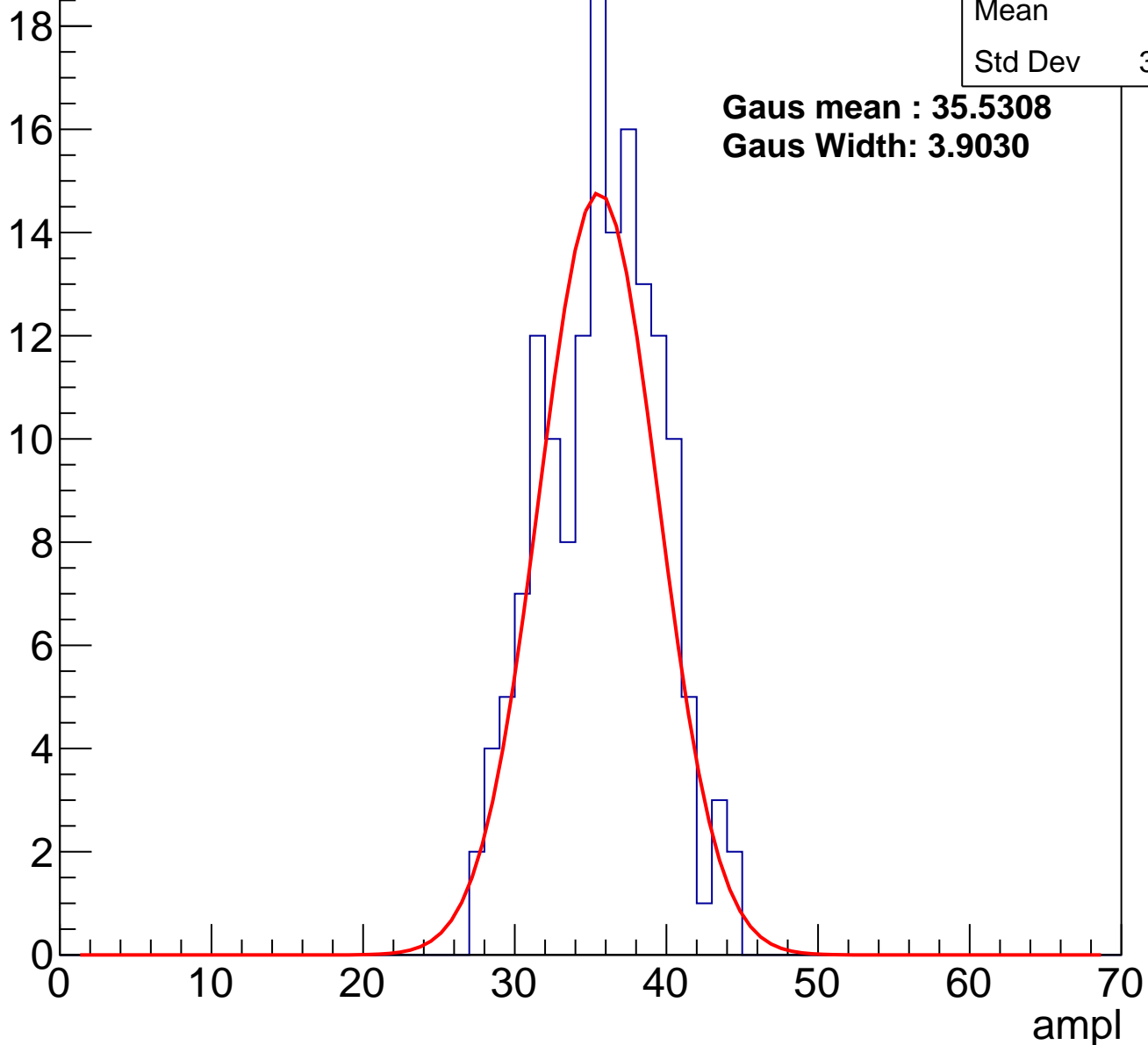
Entry



B0L101S, U21-ch92, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch92, adc2

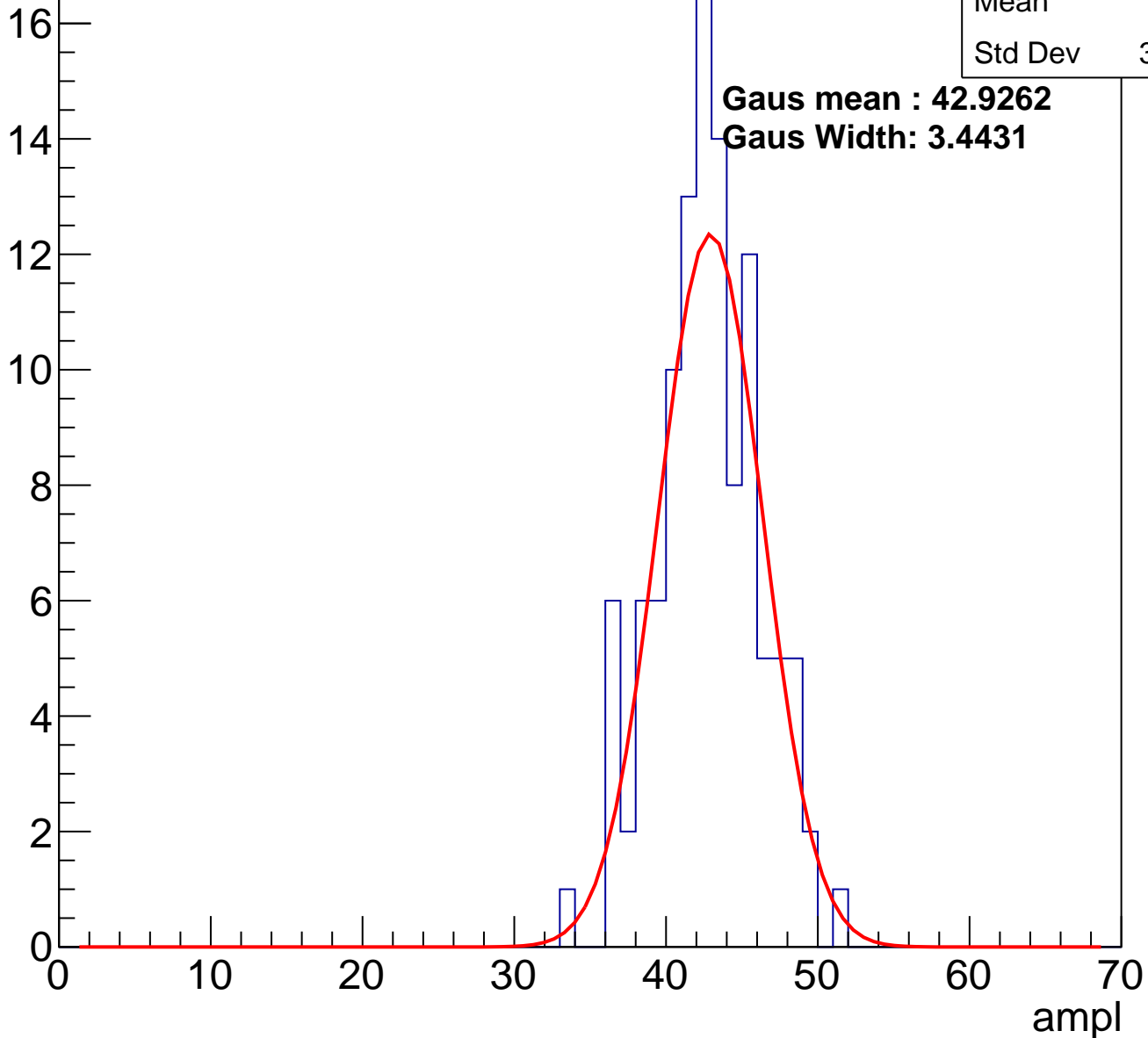
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	42.3
Std Dev	3.336

Gaus mean : 42.9262

Gaus Width: 3.4431

Entry



B0L101S, U21-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

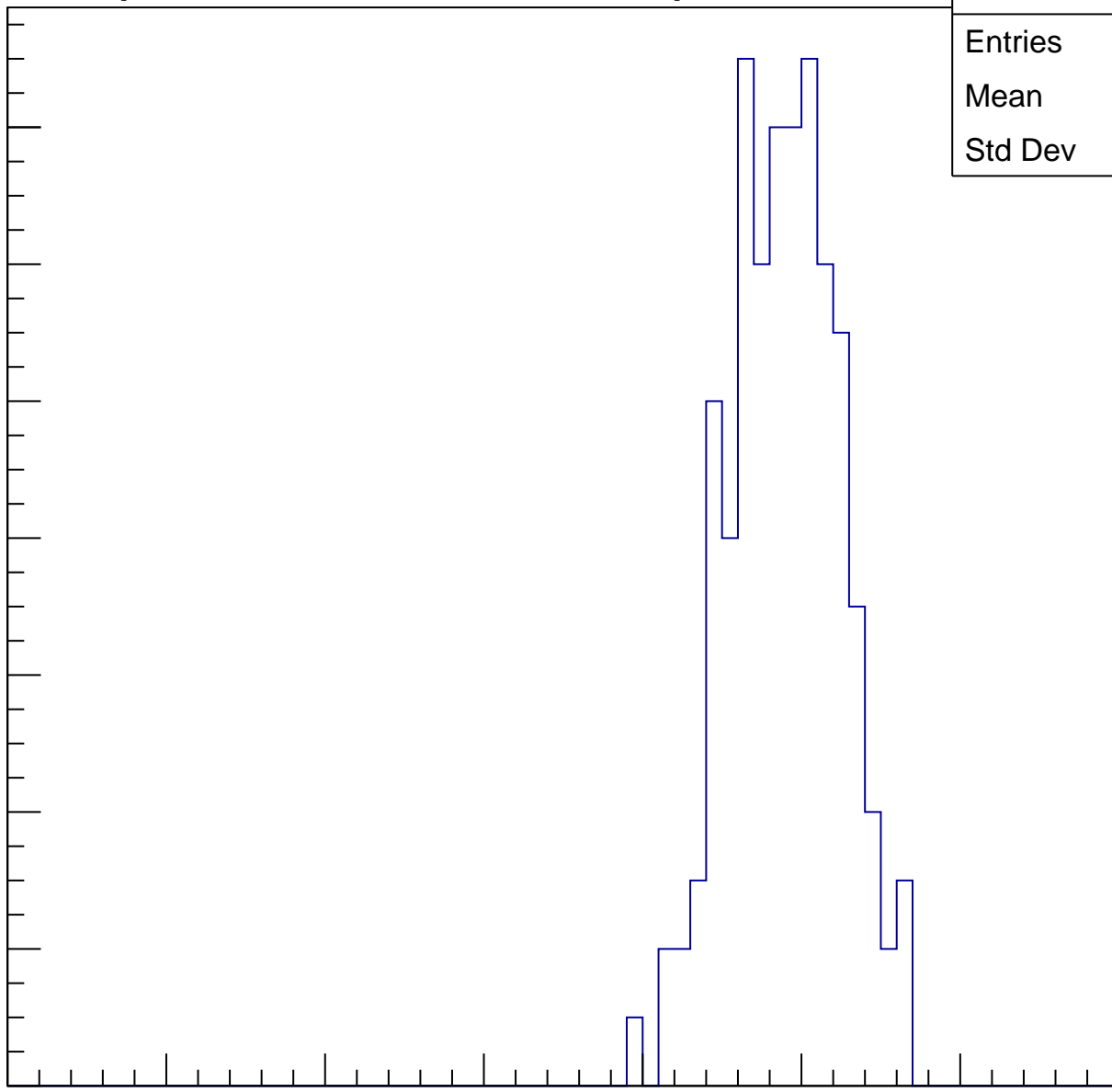
Entries	135
Mean	48.48
Std Dev	3.406

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

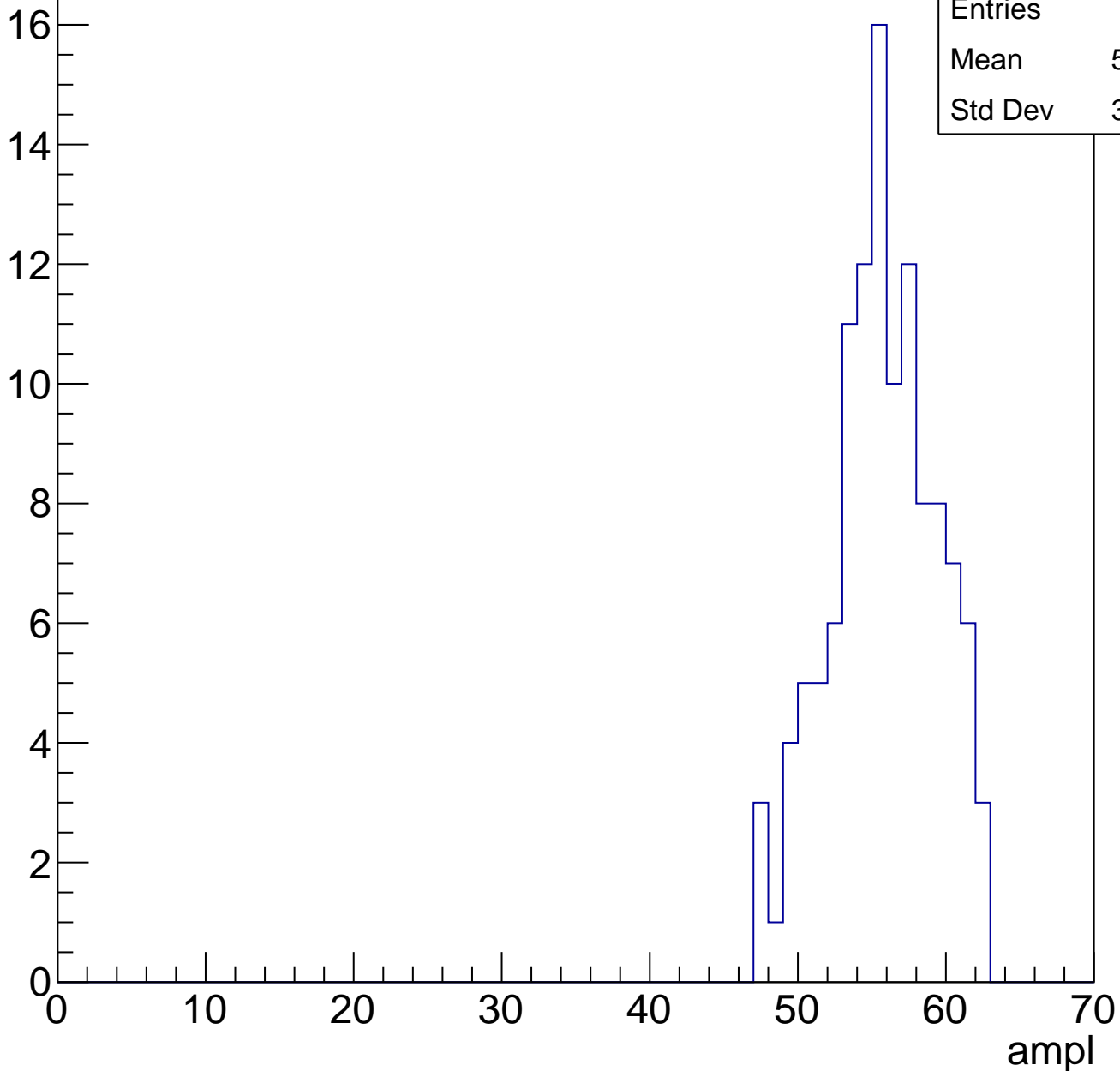
ampl



B0L101S, U21-ch92, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	117
Mean	55.26
Std Dev	3.567

B0L101S, U21-ch92, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

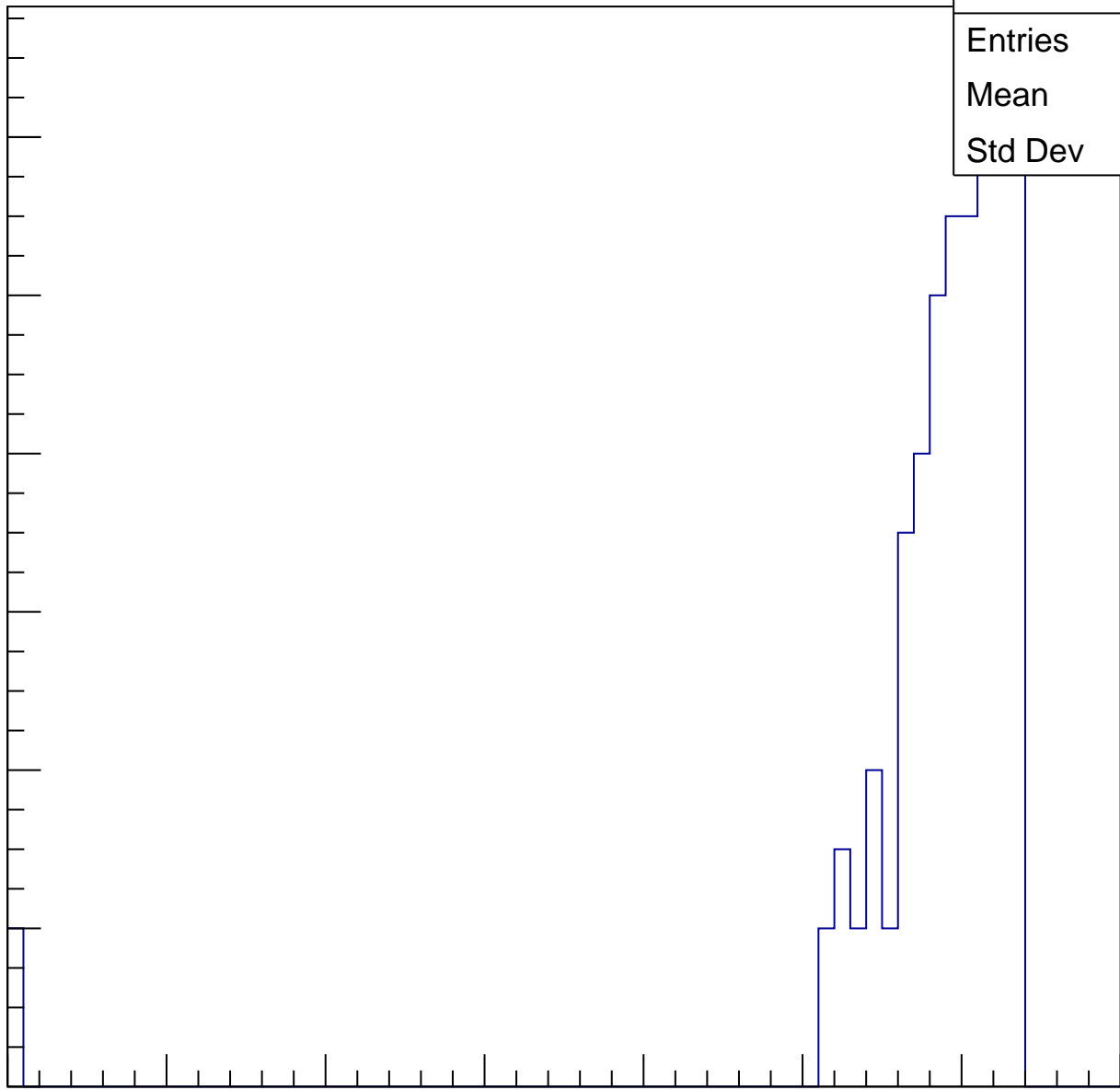
2

0

Entries	99
Mean	57.78
Std Dev	8.854

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

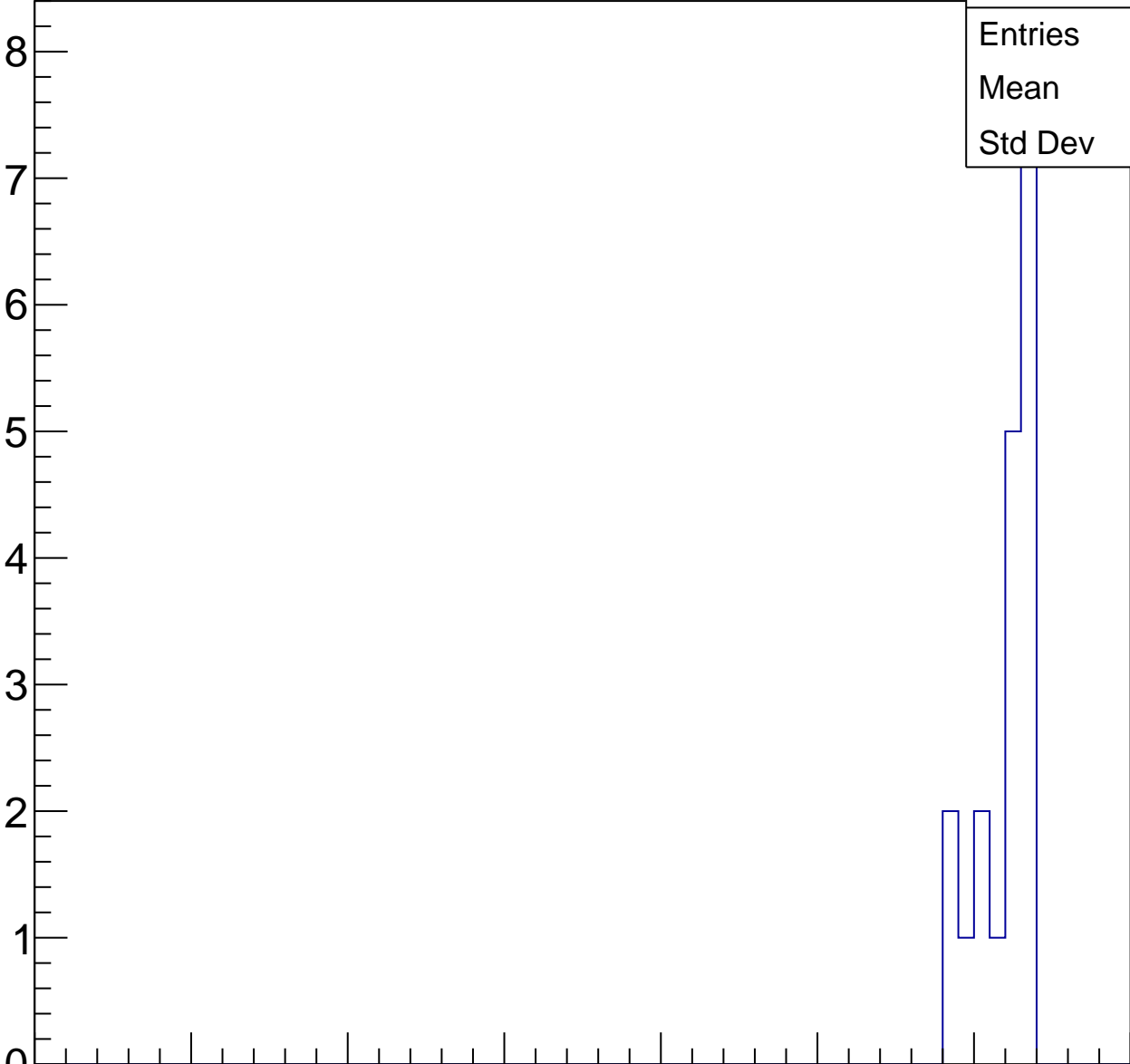
Entry

8
7
6
5
4
3
2
1
0

Entries	19
Mean	61.58
Std Dev	1.696

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch93, adc0

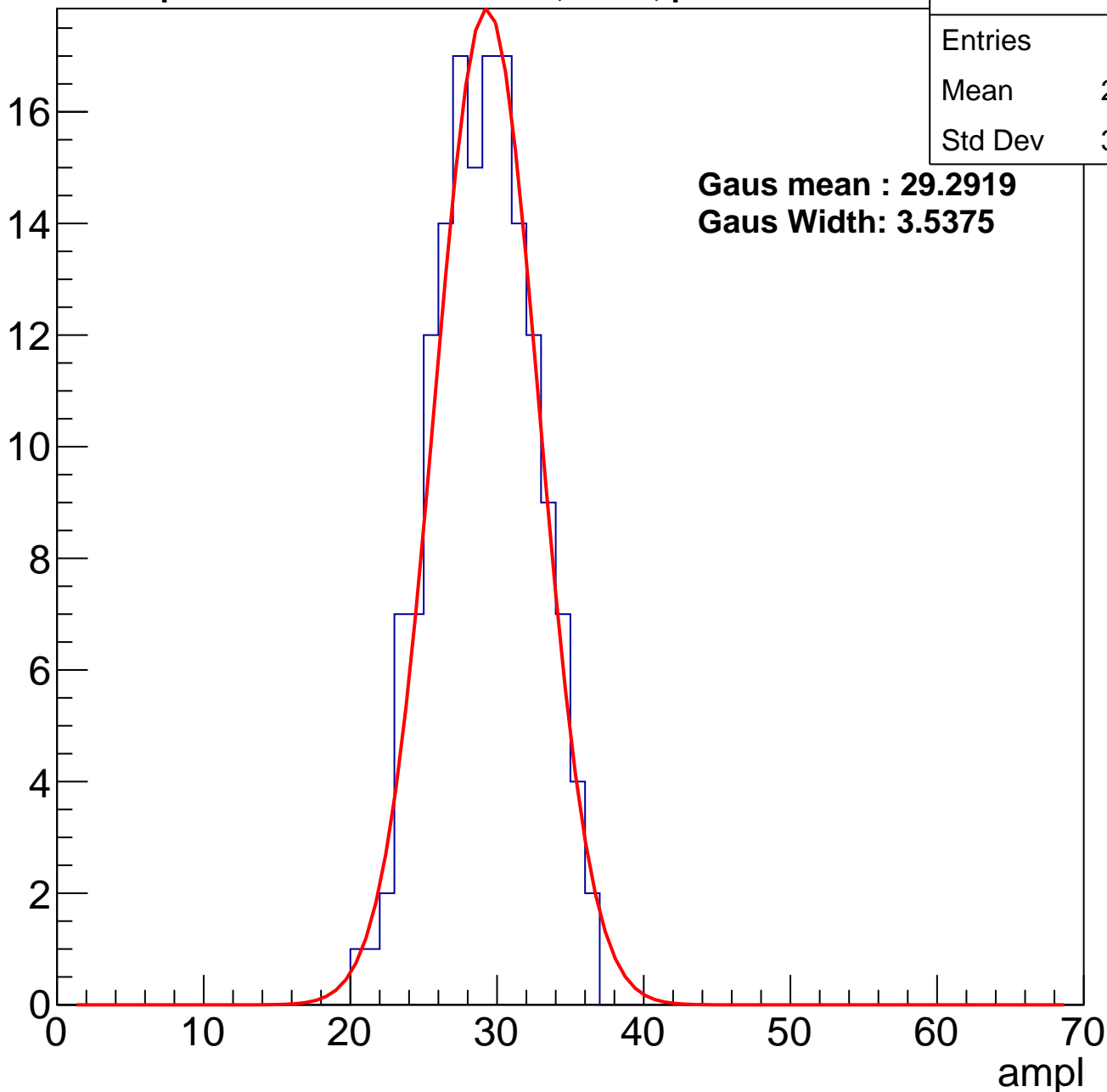
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	28.64
Std Dev	3.368

Gaus mean : 29.2919

Gaus Width: 3.5375

Entry



B0L101S, U21-ch93, adc1

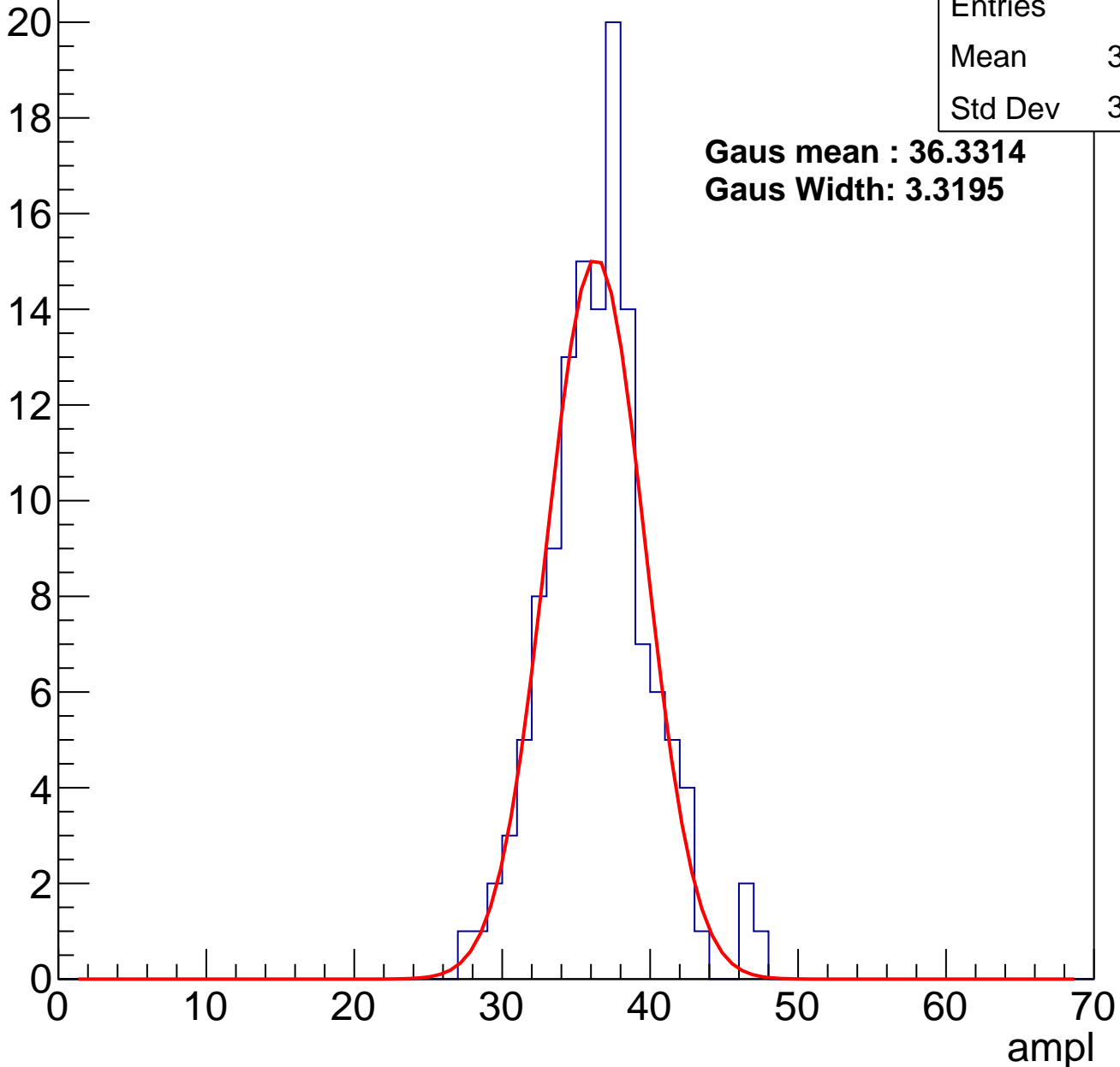
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.05
Std Dev	3.523

Gaus mean : 36.3314

Gaus Width: 3.3195

Entry



B0L101S, U21-ch93, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

42.48

Std Dev

3.405

Gaus mean : 42.8789

Gaus Width: 3.5091

0

10

20

30

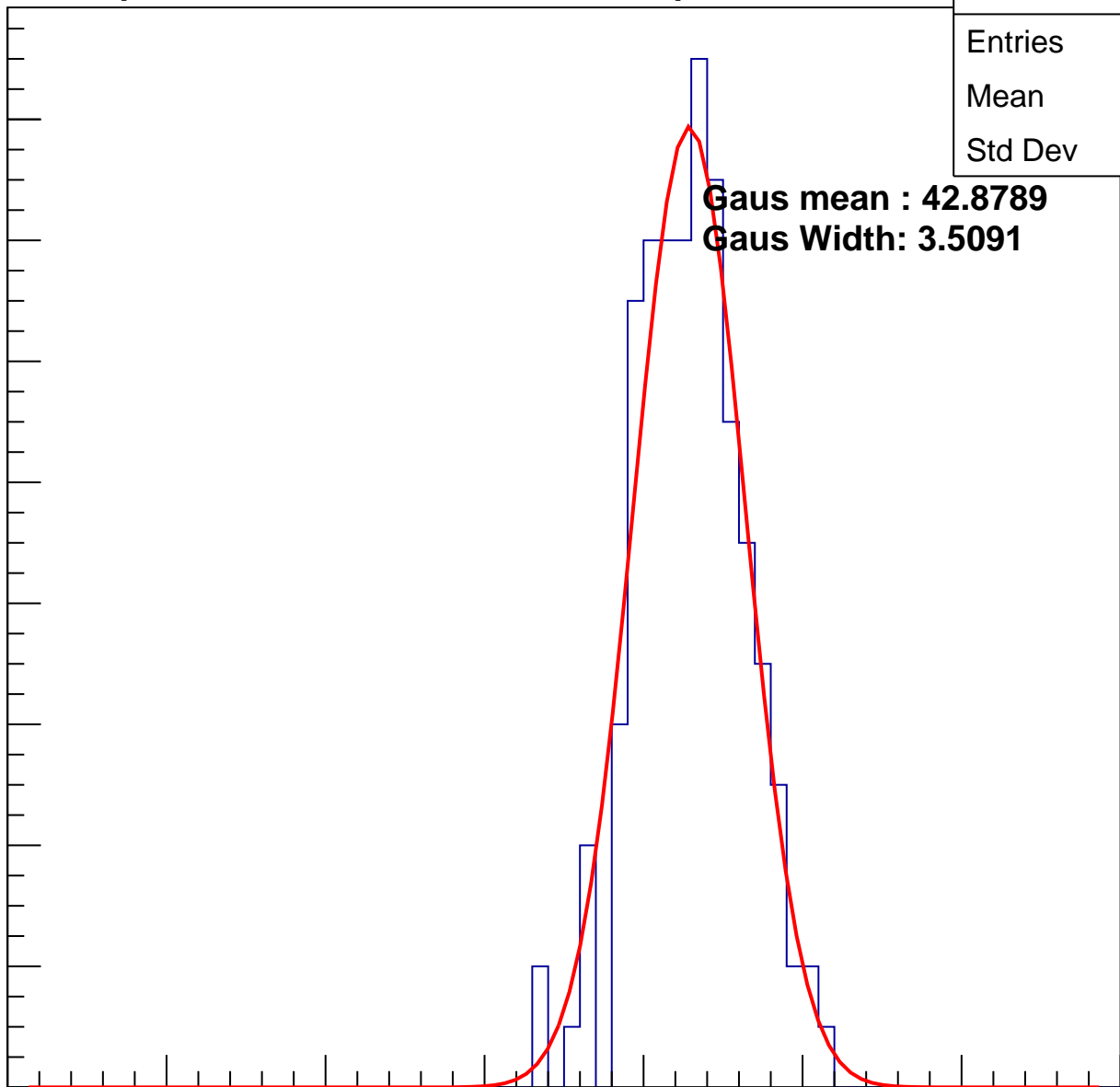
40

50

60

70

ampl



B0L101S, U21-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	49.72
Std Dev	3.495

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

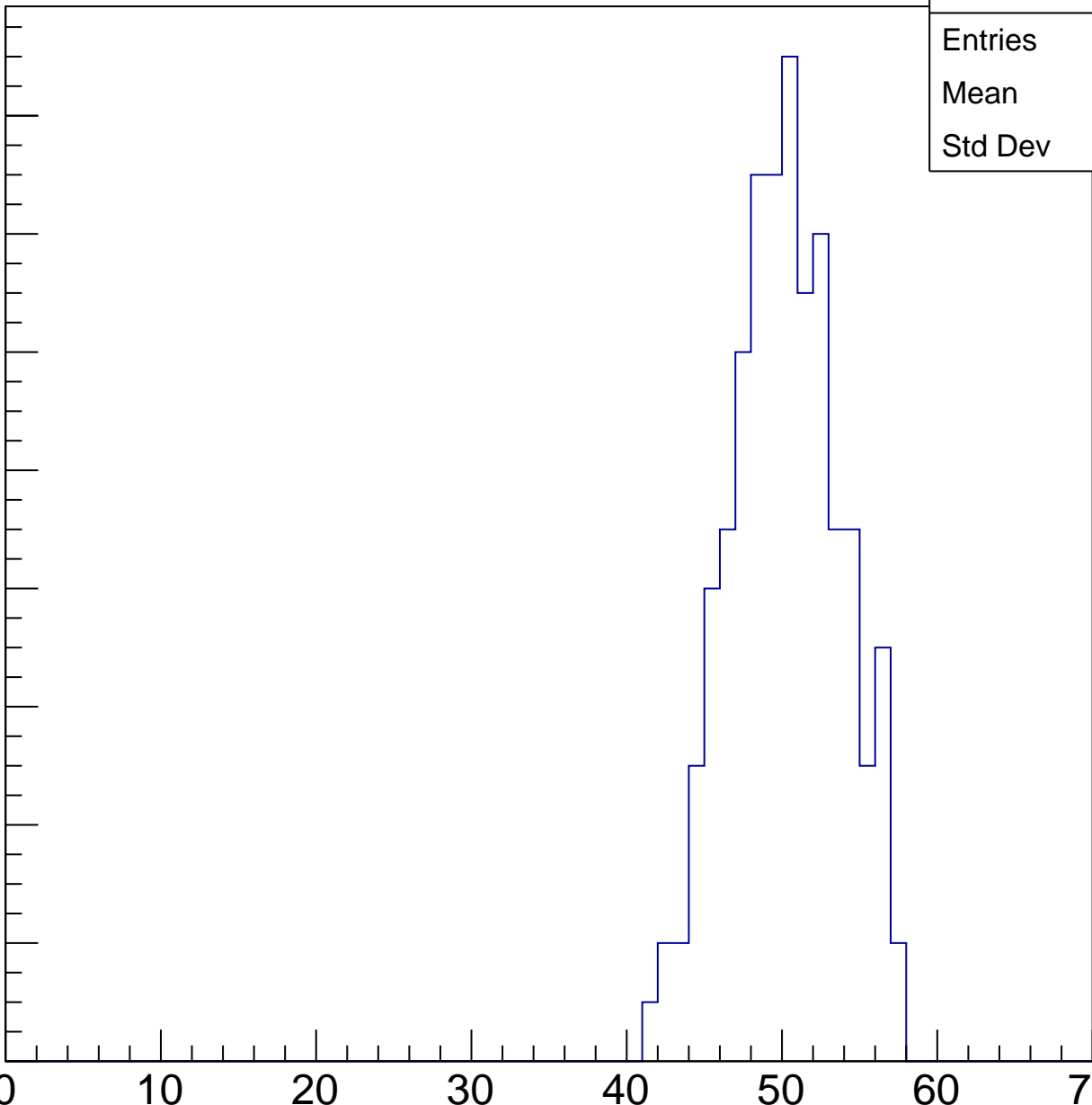
40

50

60

70

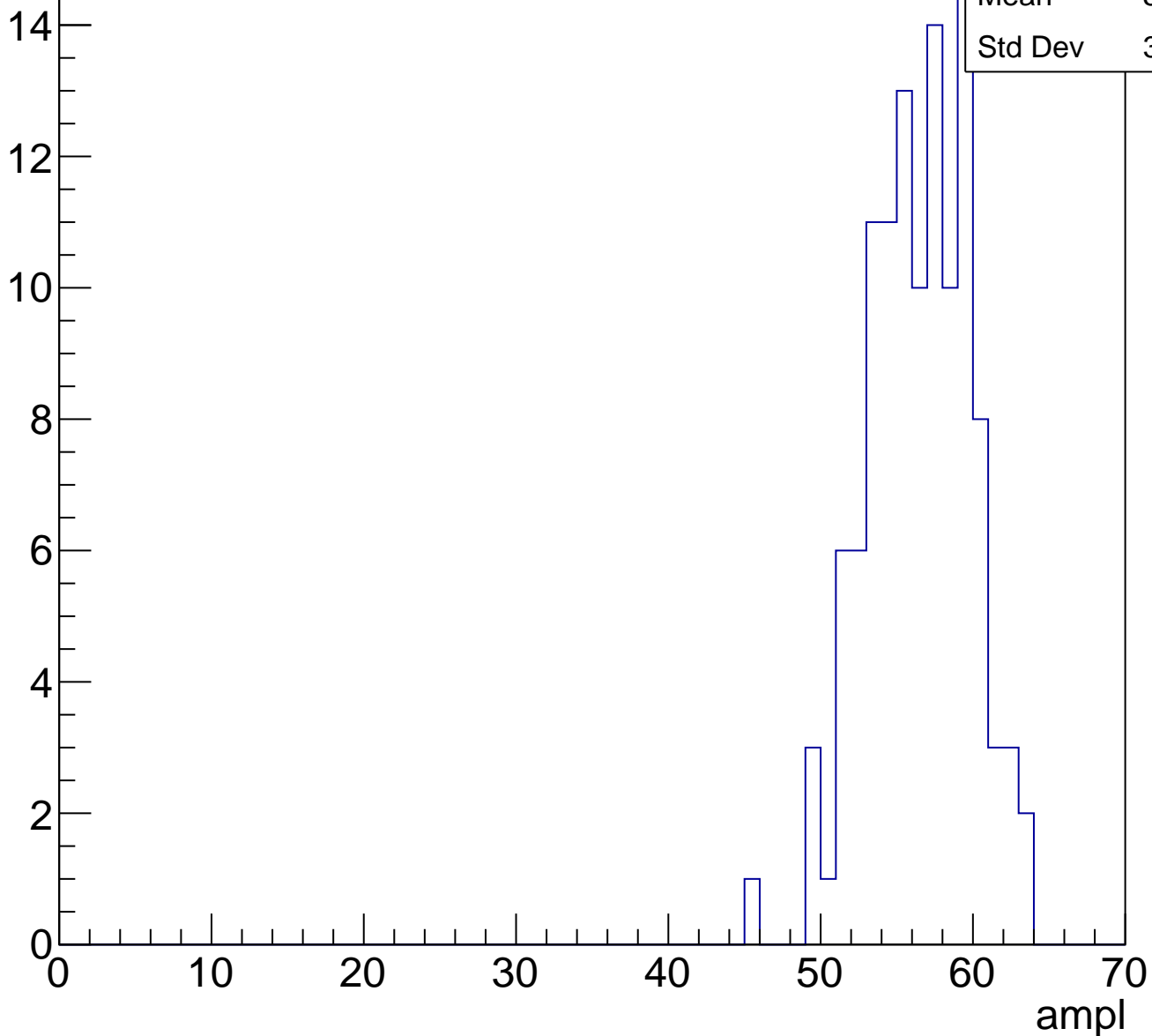
ampl



B0L101S, U21-ch93, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch93, adc5

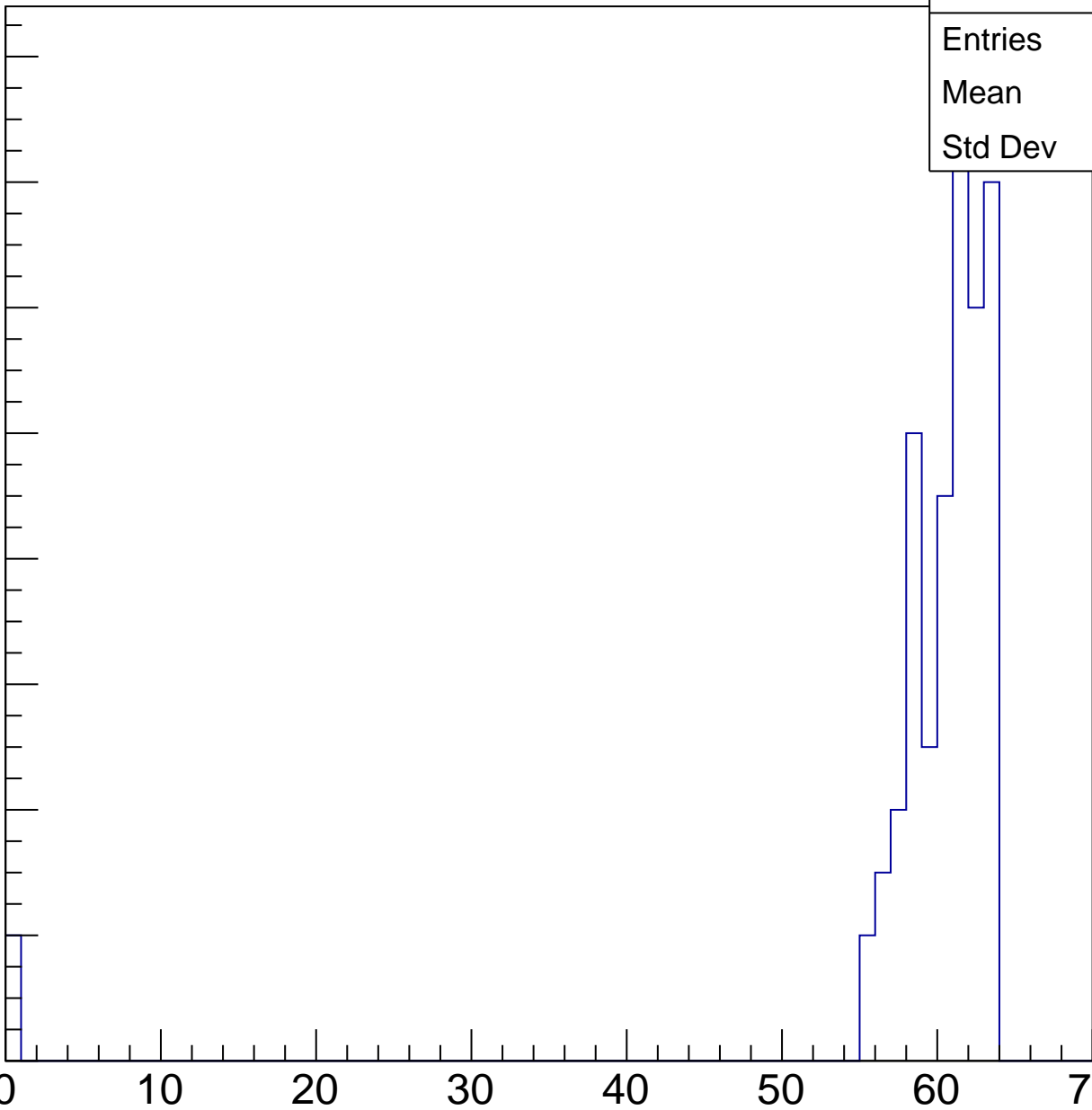
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	58.74
Std Dev	9.834

ampl

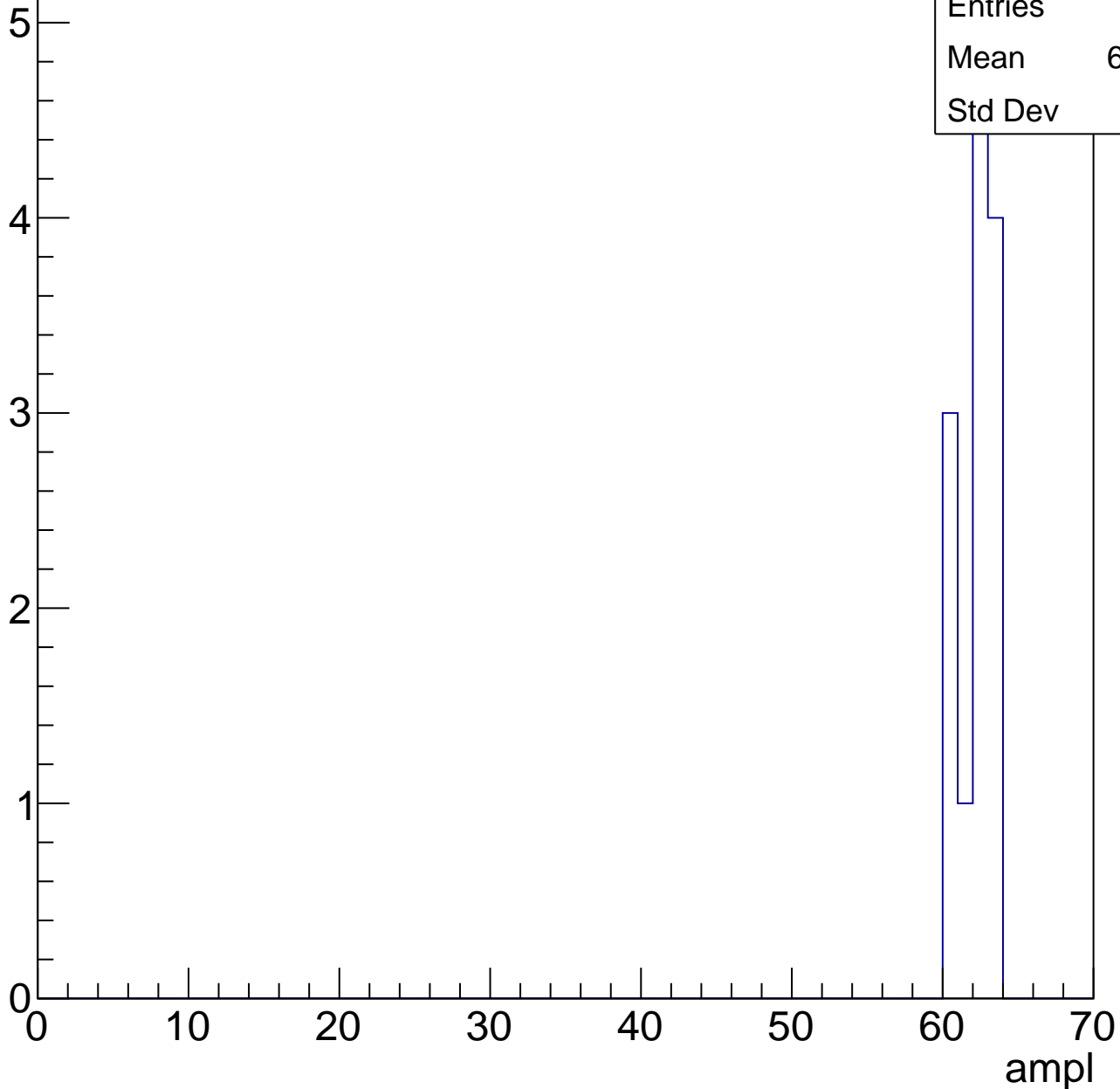


B0L101S, U21-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

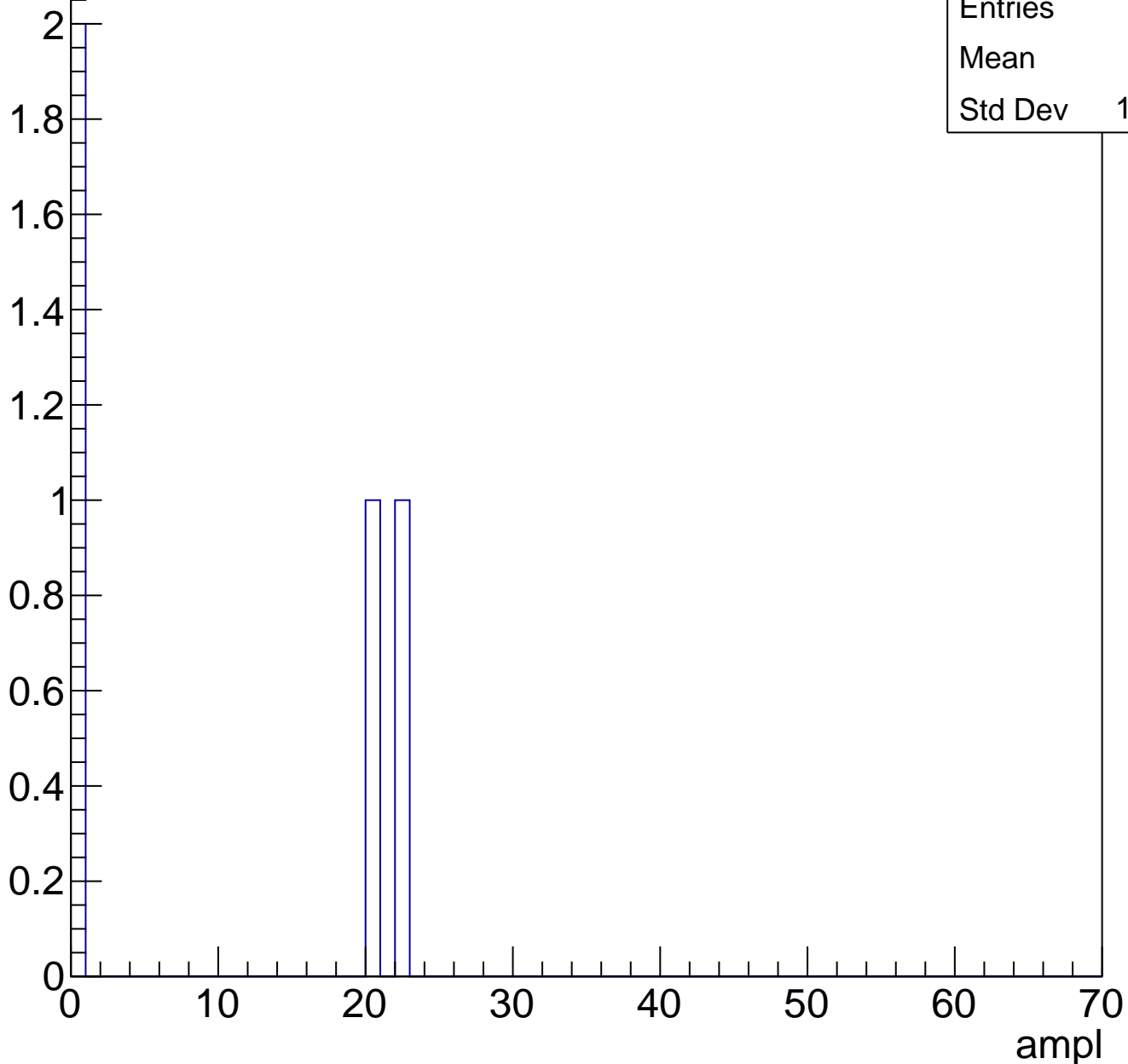
Entries	13
Mean	61.77
Std Dev	1.12



B0L101S, U21-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	10.5
Std Dev	10.52

B0L101S, U21-ch94, adc0

calib_packv5_042523_0143.root, FC#1, port C1

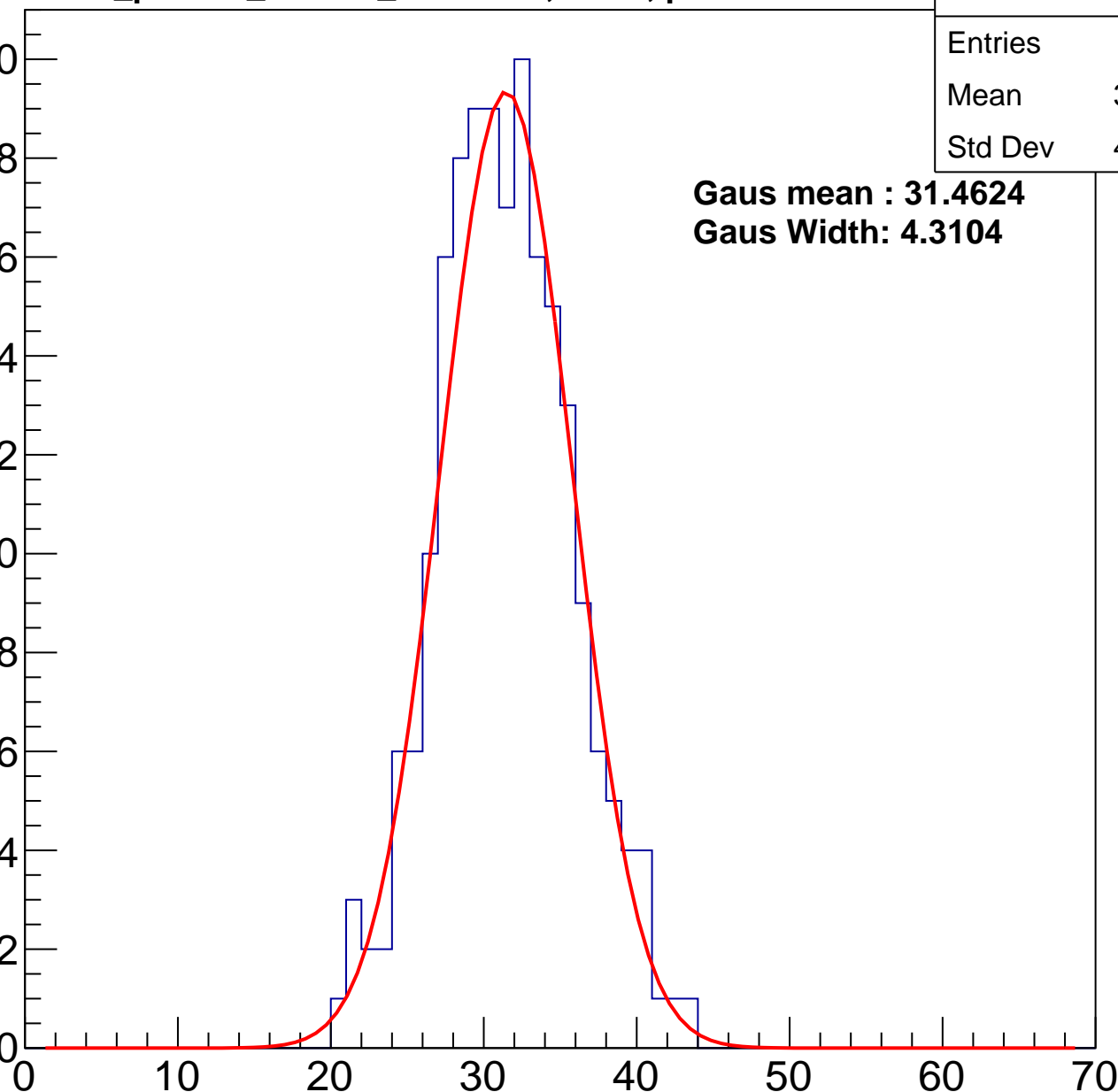
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	214
Mean	30.94
Std Dev	4.346

Gaus mean : 31.4624

Gaus Width: 4.3104



ampl

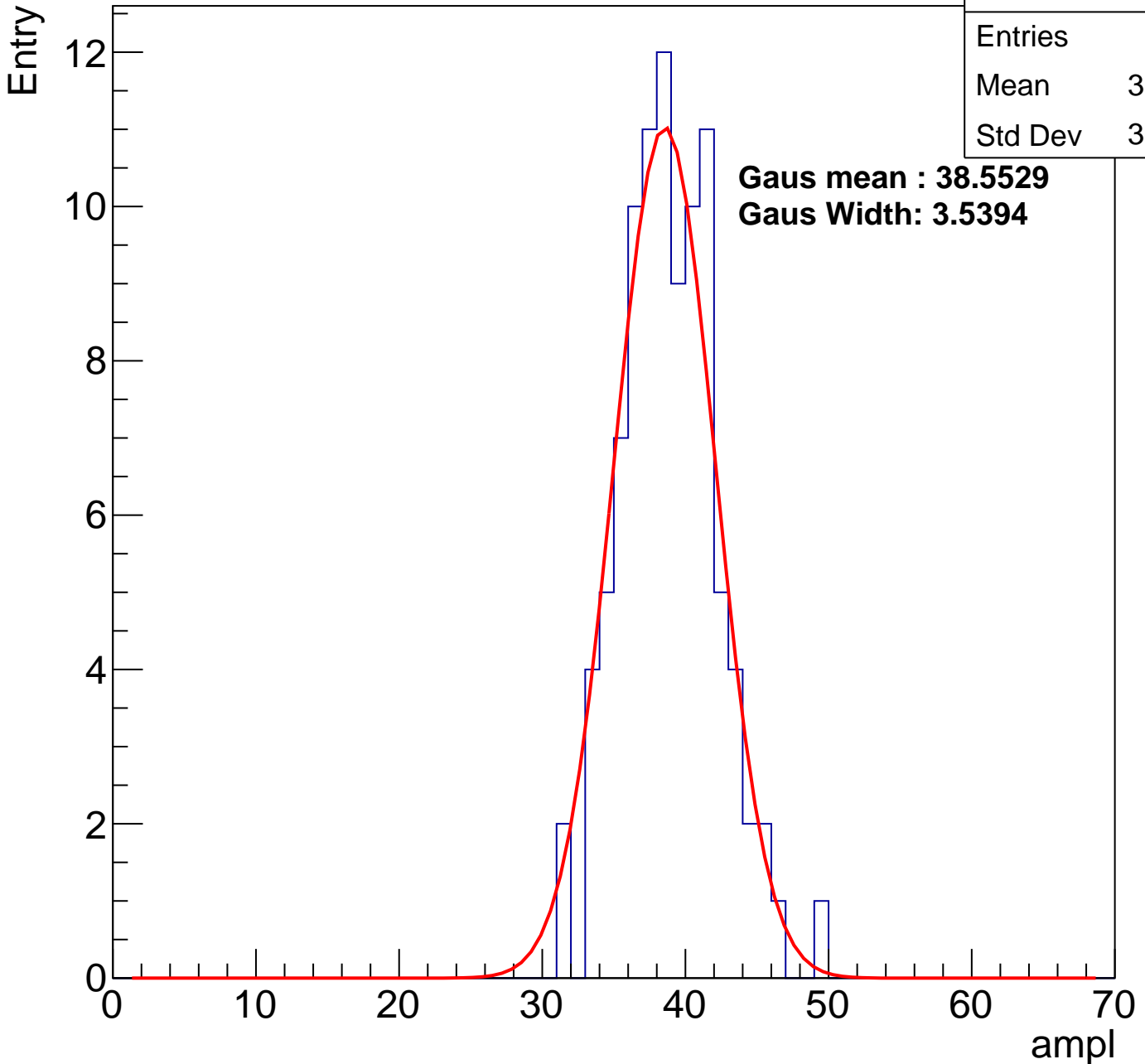
B0L101S, U21-ch94, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	38.43
Std Dev	3.319

Gaus mean : 38.5529

Gaus Width: 3.5394



B0L101S, U21-ch94, adc2

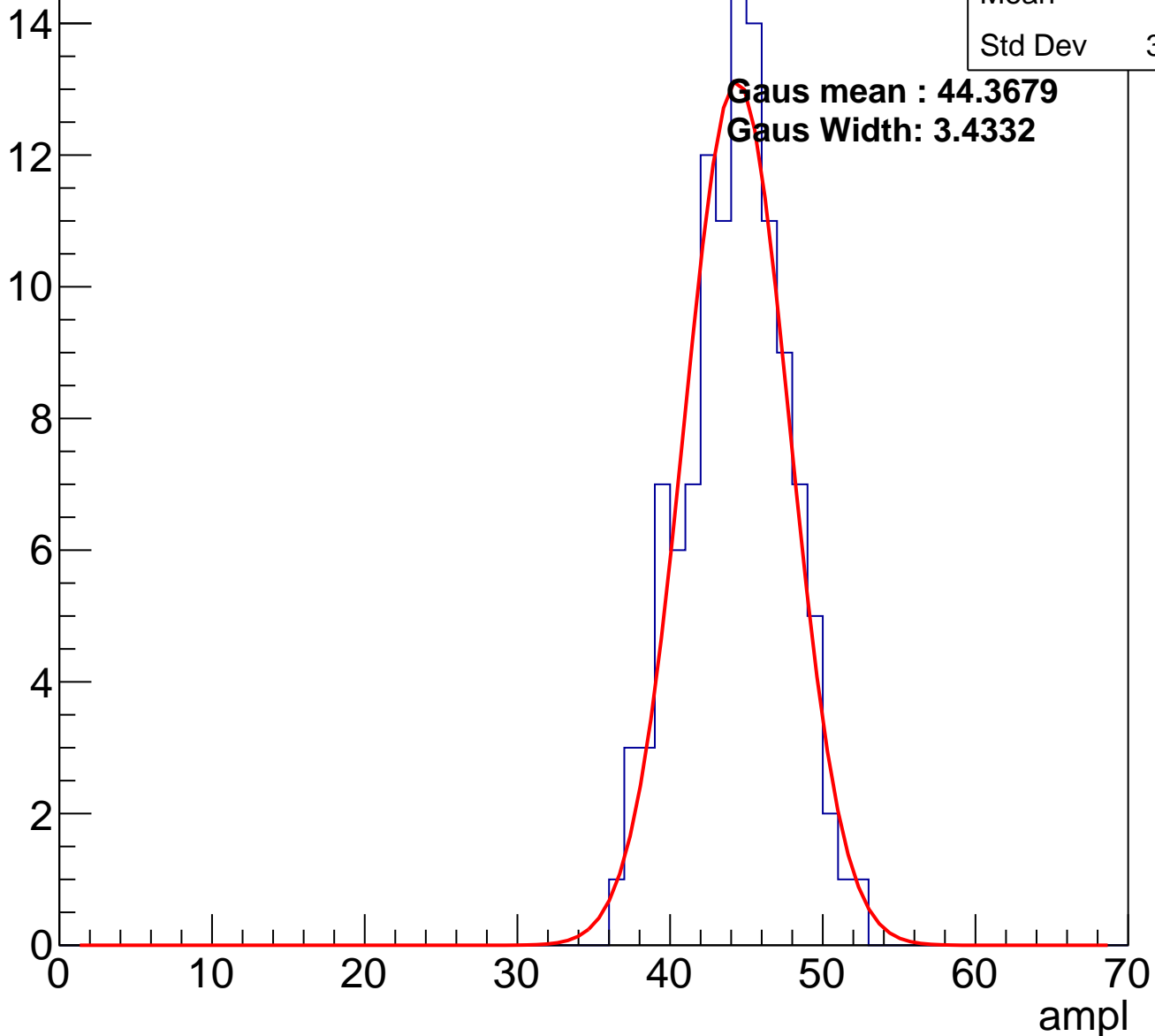
calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	43.83
Std Dev	3.323

Gaus mean : 44.3679

Gaus Width: 3.4332

Entry

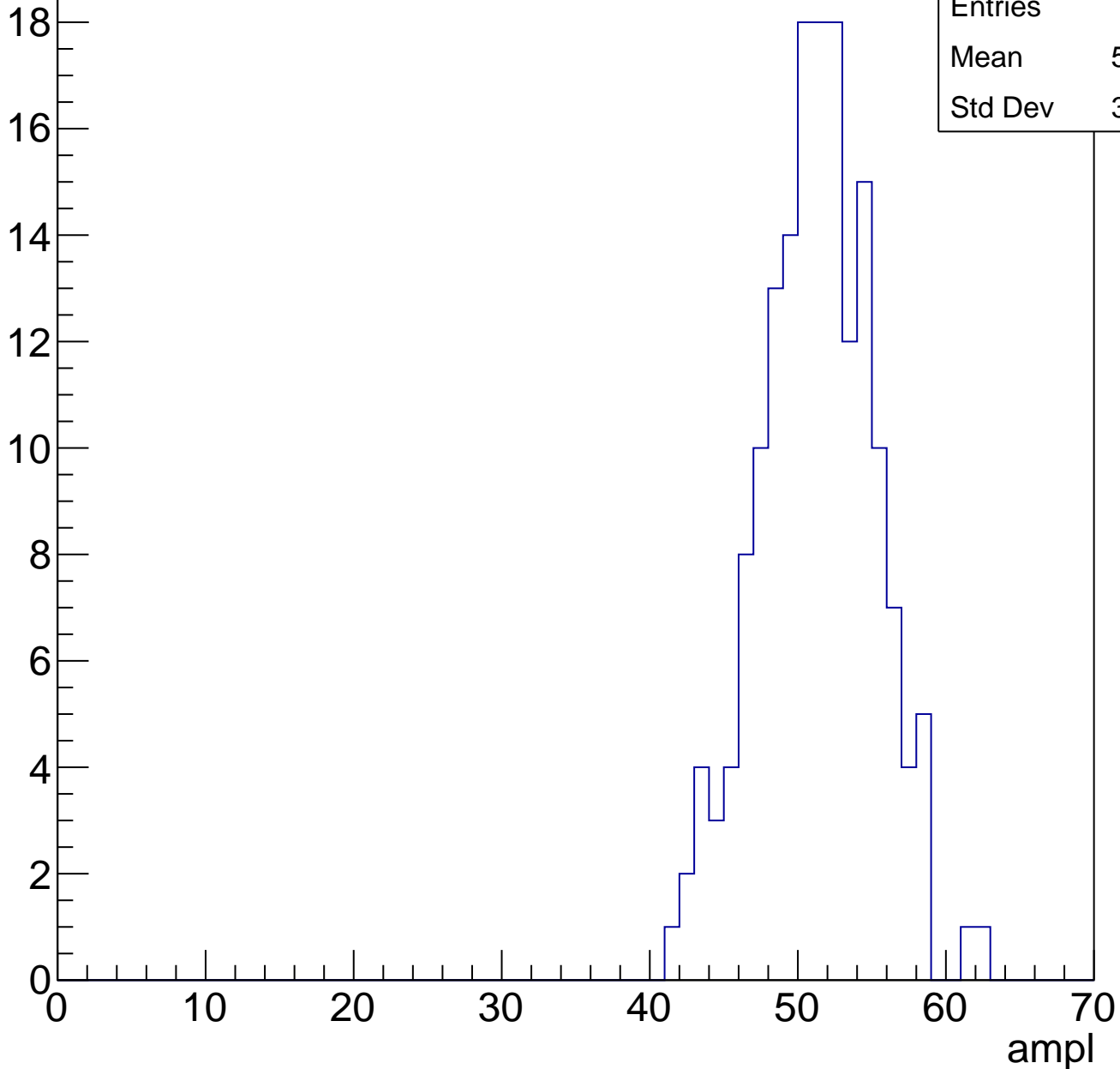


B0L101S, U21-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	50.83
Std Dev	3.859

Entry



B0L101S, U21-ch94, adc4

calib_packv5_042523_0143.root, FC#1, port C1

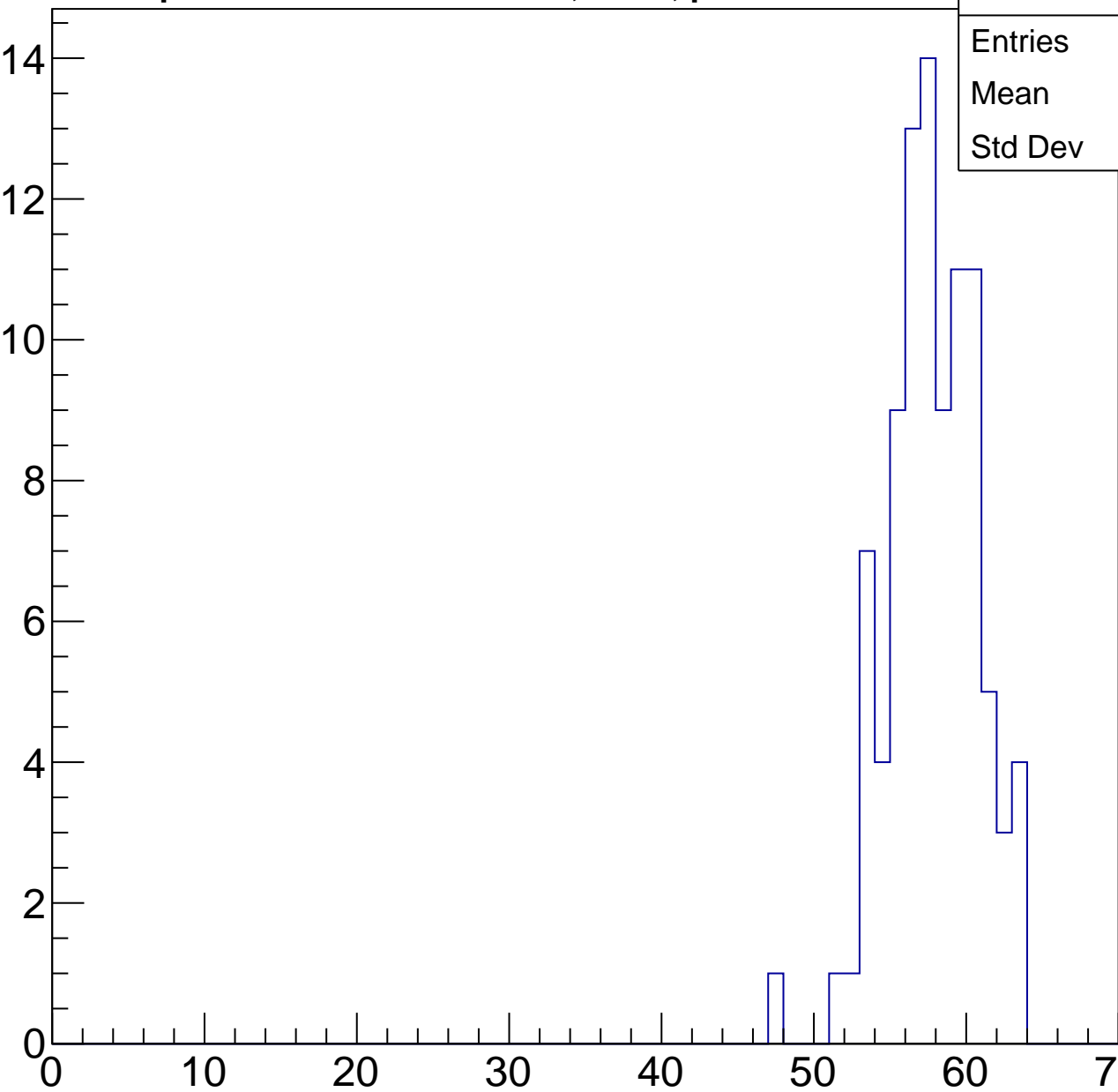
Entries	93
Mean	57.33
Std Dev	2.93

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch94, adc5

calib_packv5_042523_0143.root, FC#1, port C1

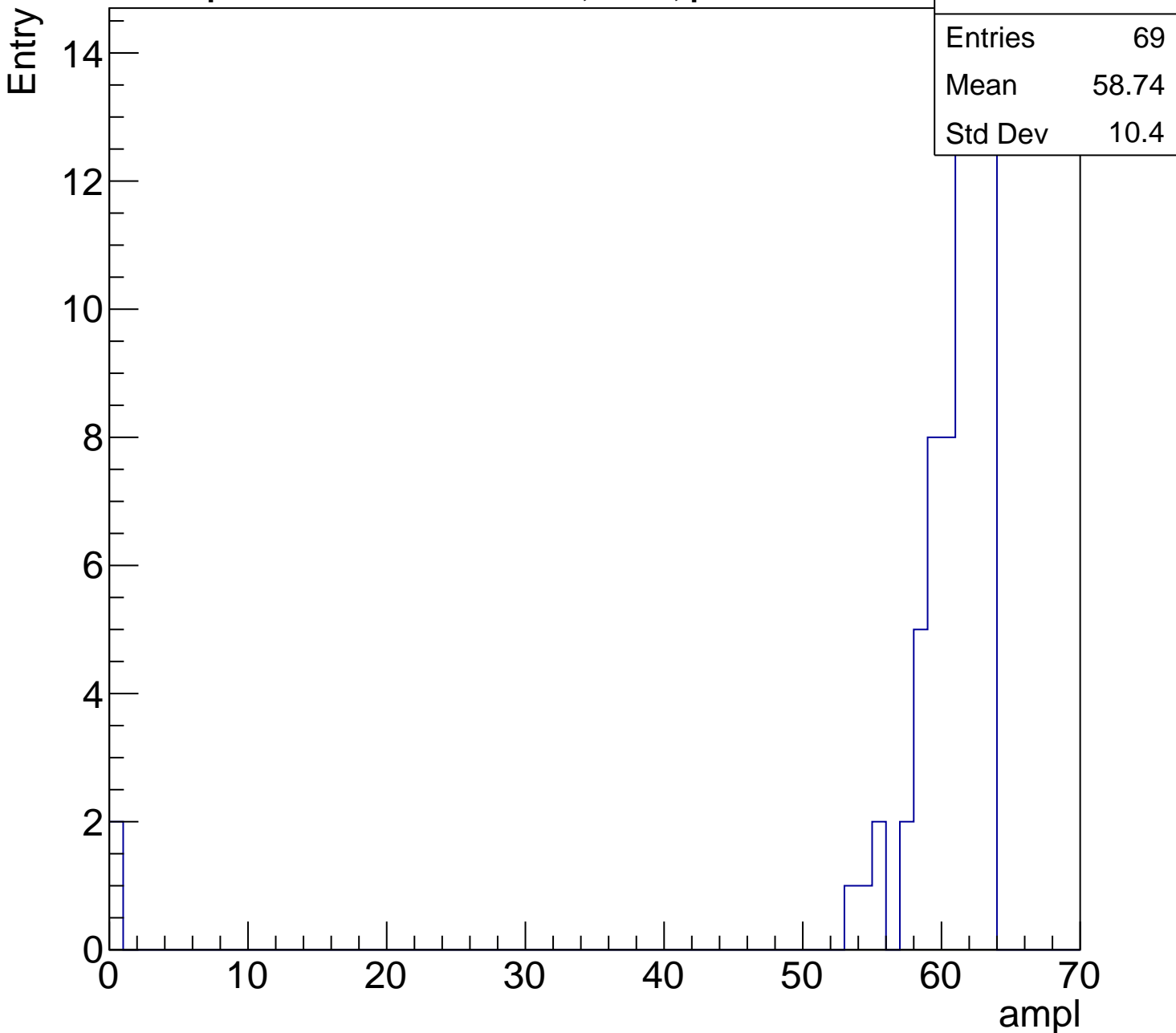
Entries	69
Mean	58.74
Std Dev	10.4

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

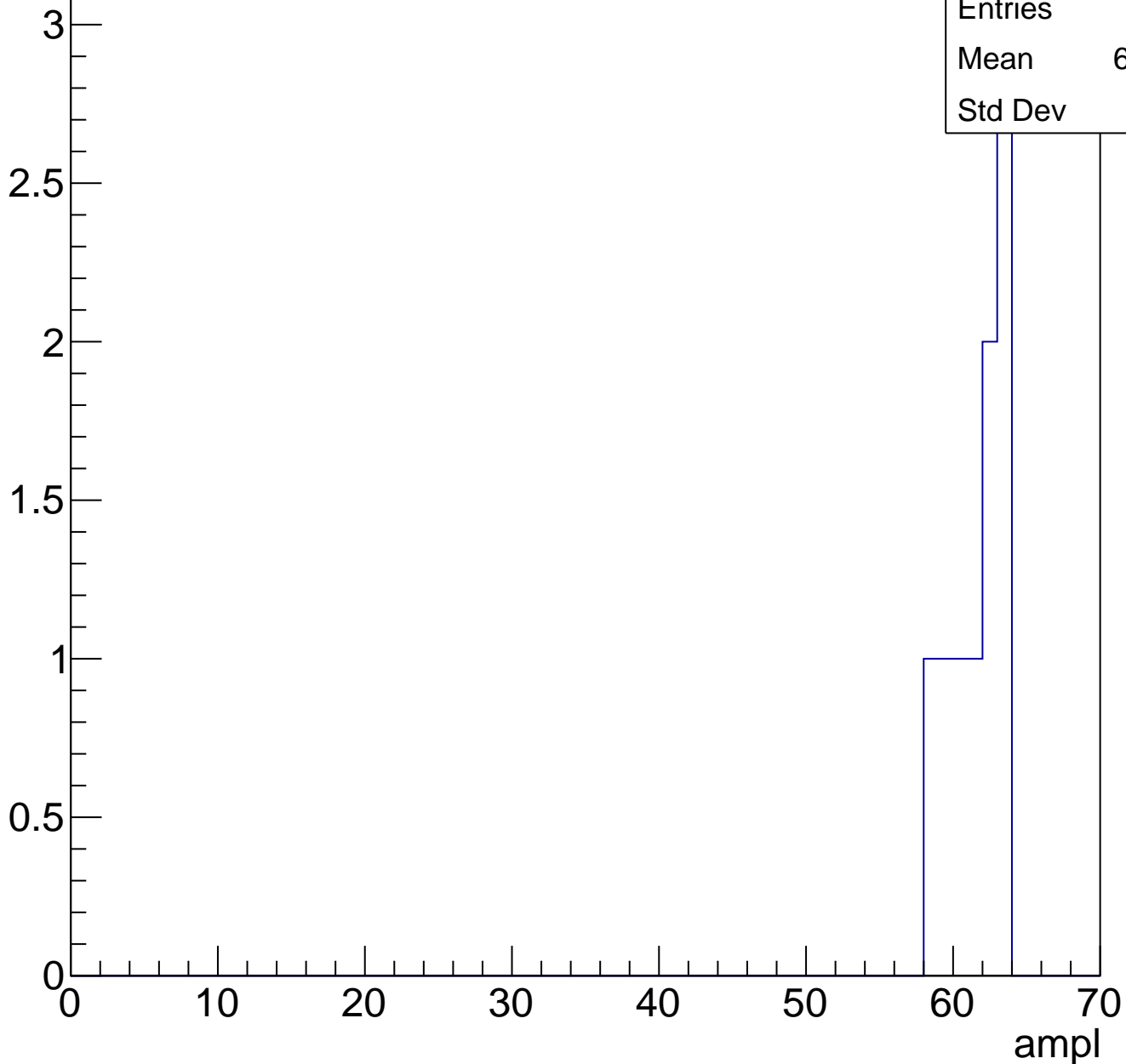
ampl



B0L101S, U21-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

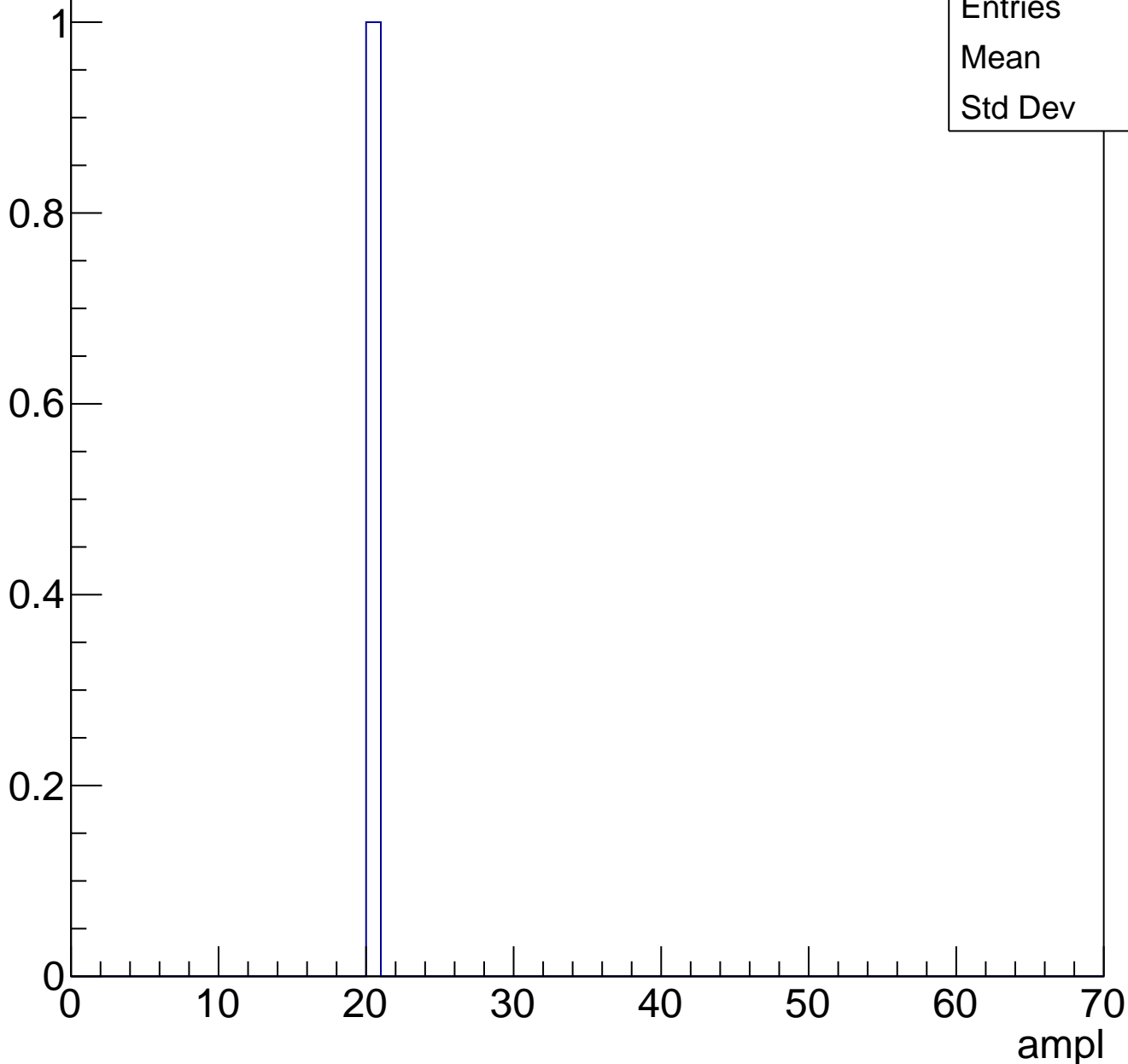
Entry



B0L101S, U21-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch95, adc0

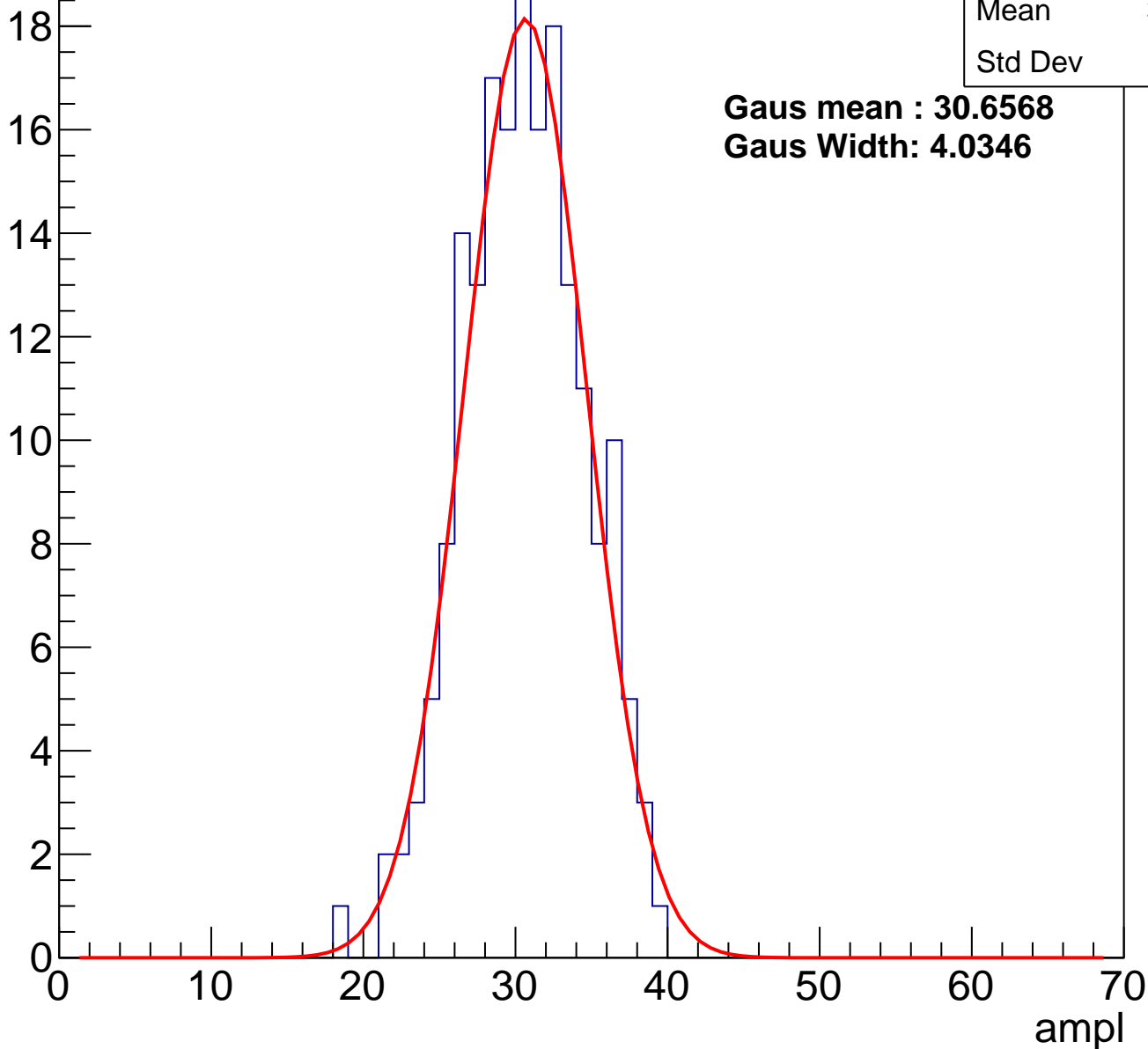
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	30.11
Std Dev	3.92

Gaus mean : 30.6568

Gaus Width: 4.0346

Entry



B0L101S, U21-ch95, adc1

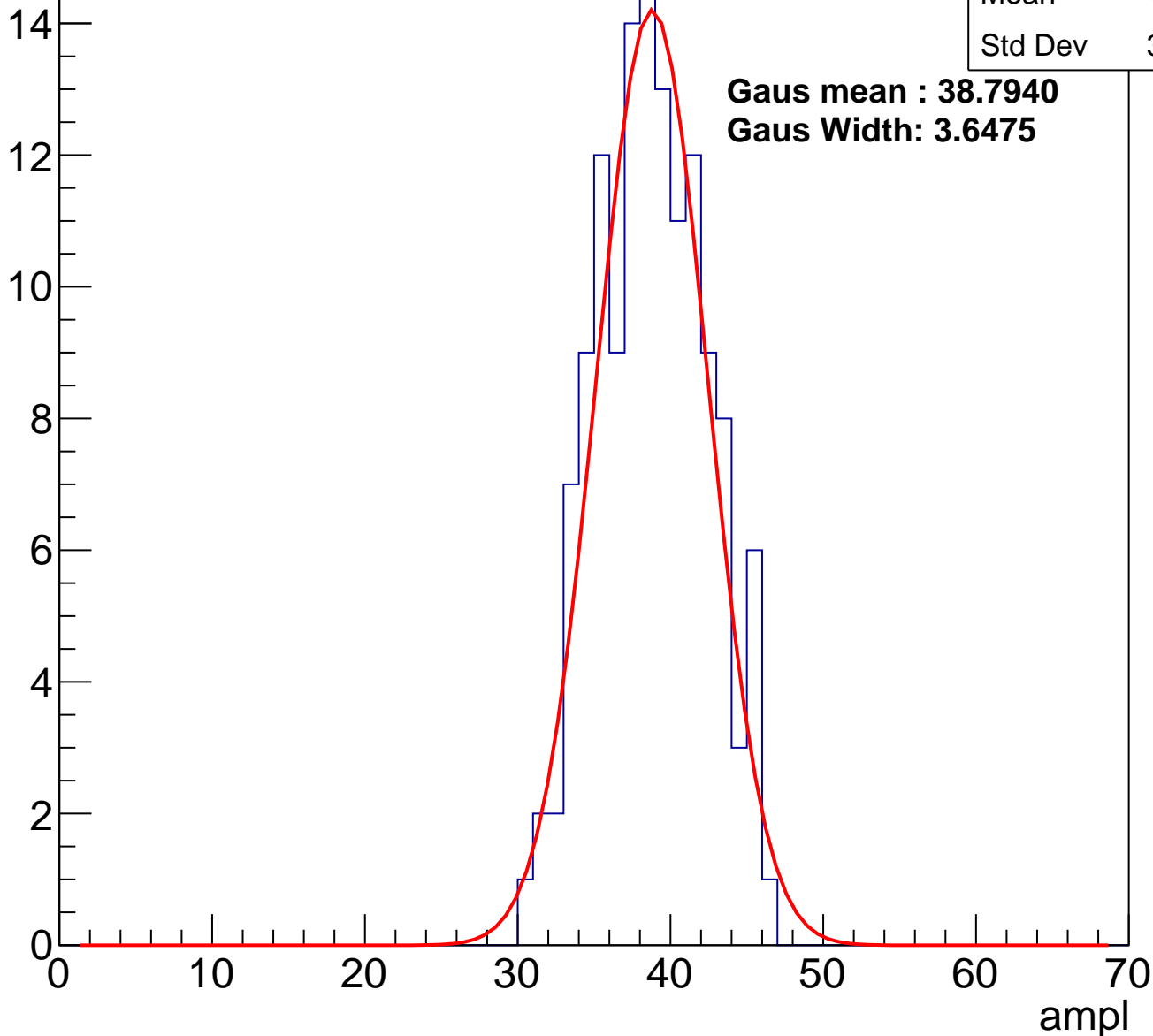
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	38.31
Std Dev	3.533

Gaus mean : 38.7940

Gaus Width: 3.6475

Entry



B0L101S, U21-ch95, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

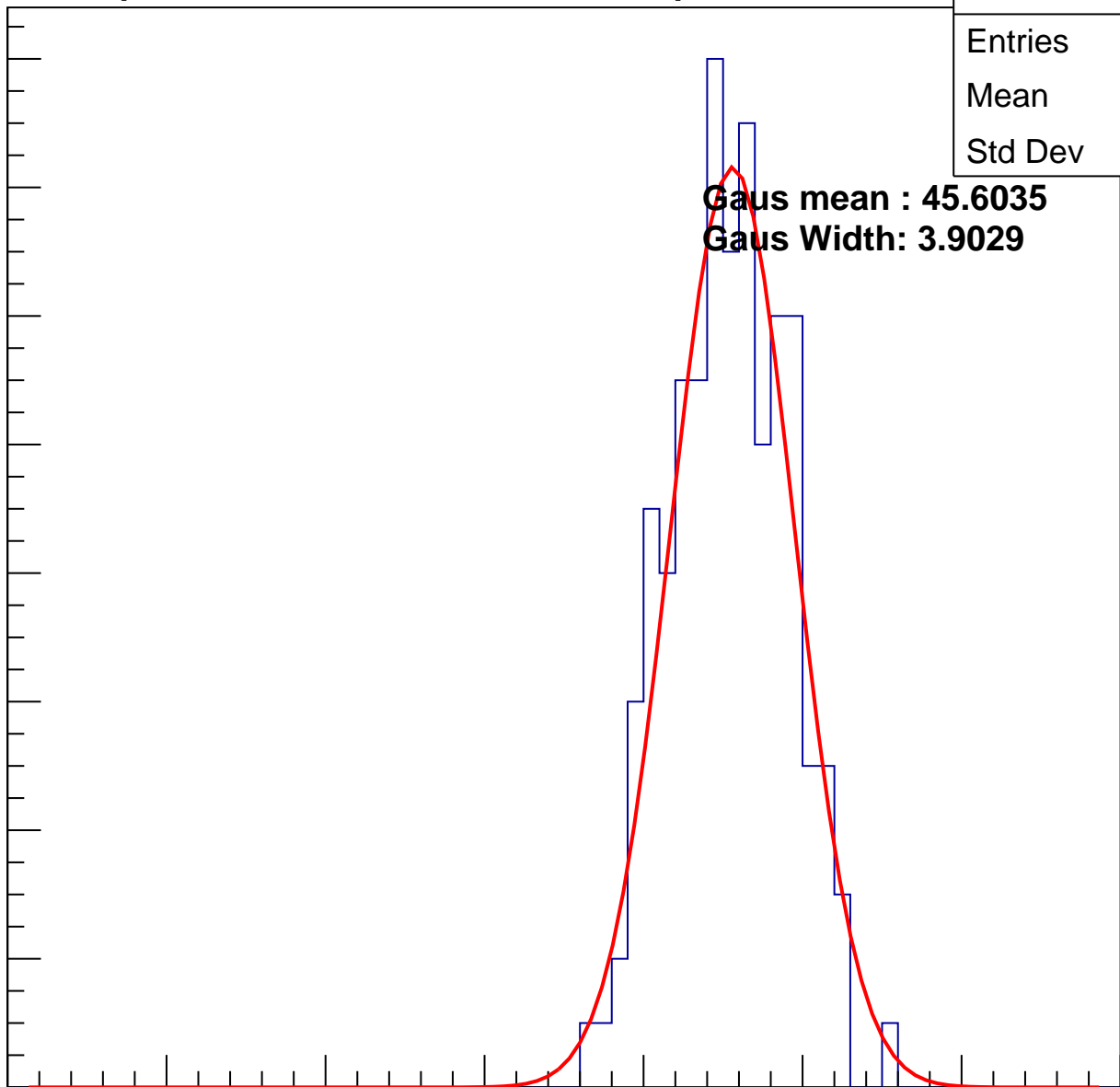
Entries	141
Mean	44.93
Std Dev	3.649

Gaus mean : 45.6035

Gaus Width: 3.9029

0 10 20 30 40 50 60 70

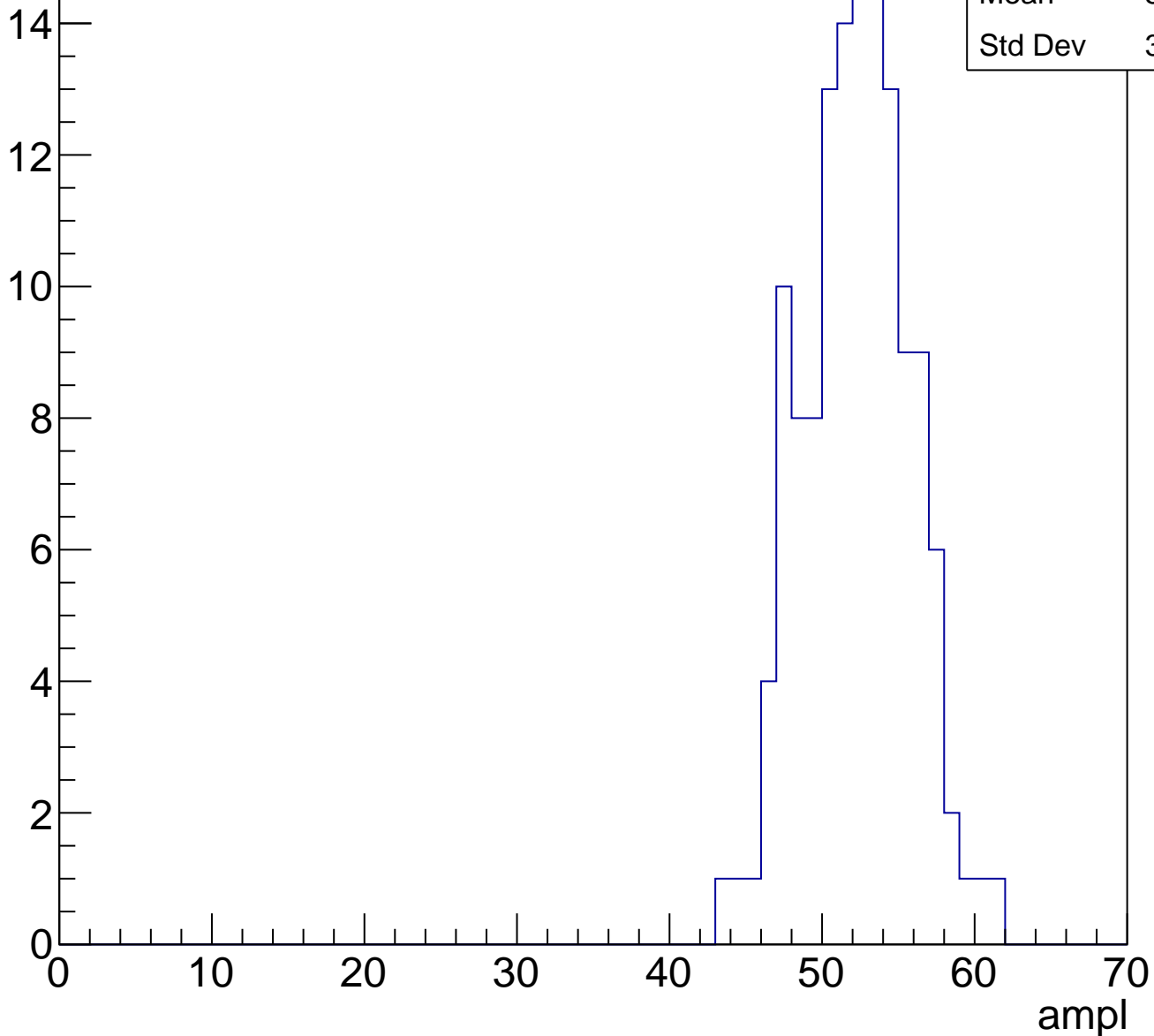
ampl



B0L101S, U21-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

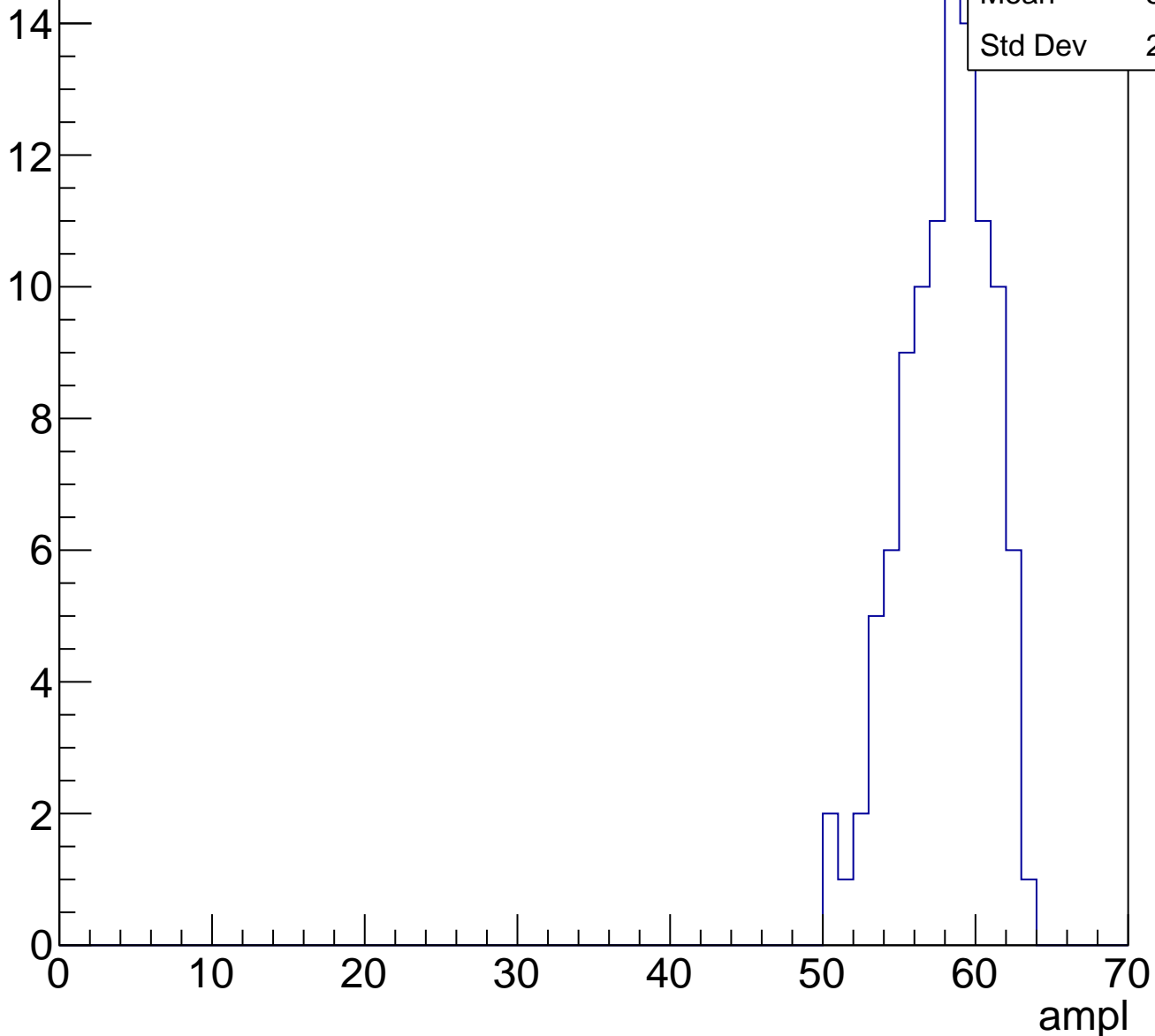


B0L101S, U21-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	57.54
Std Dev	2.872

Entry



B0L101S, U21-ch95, adc5

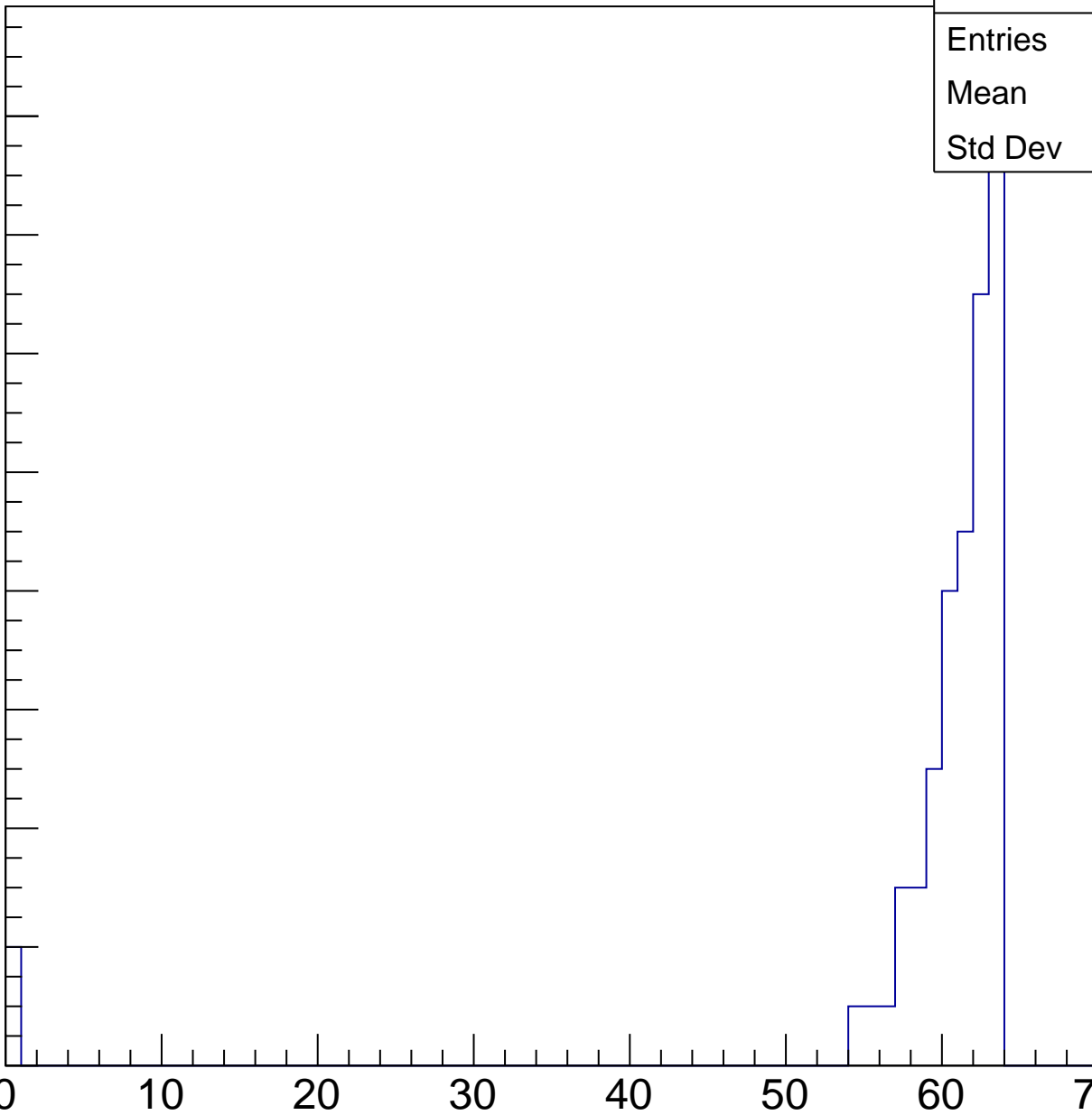
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	63
Mean	58.9
Std Dev	10.88

ampl



B0L101S, U21-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch96, adc0

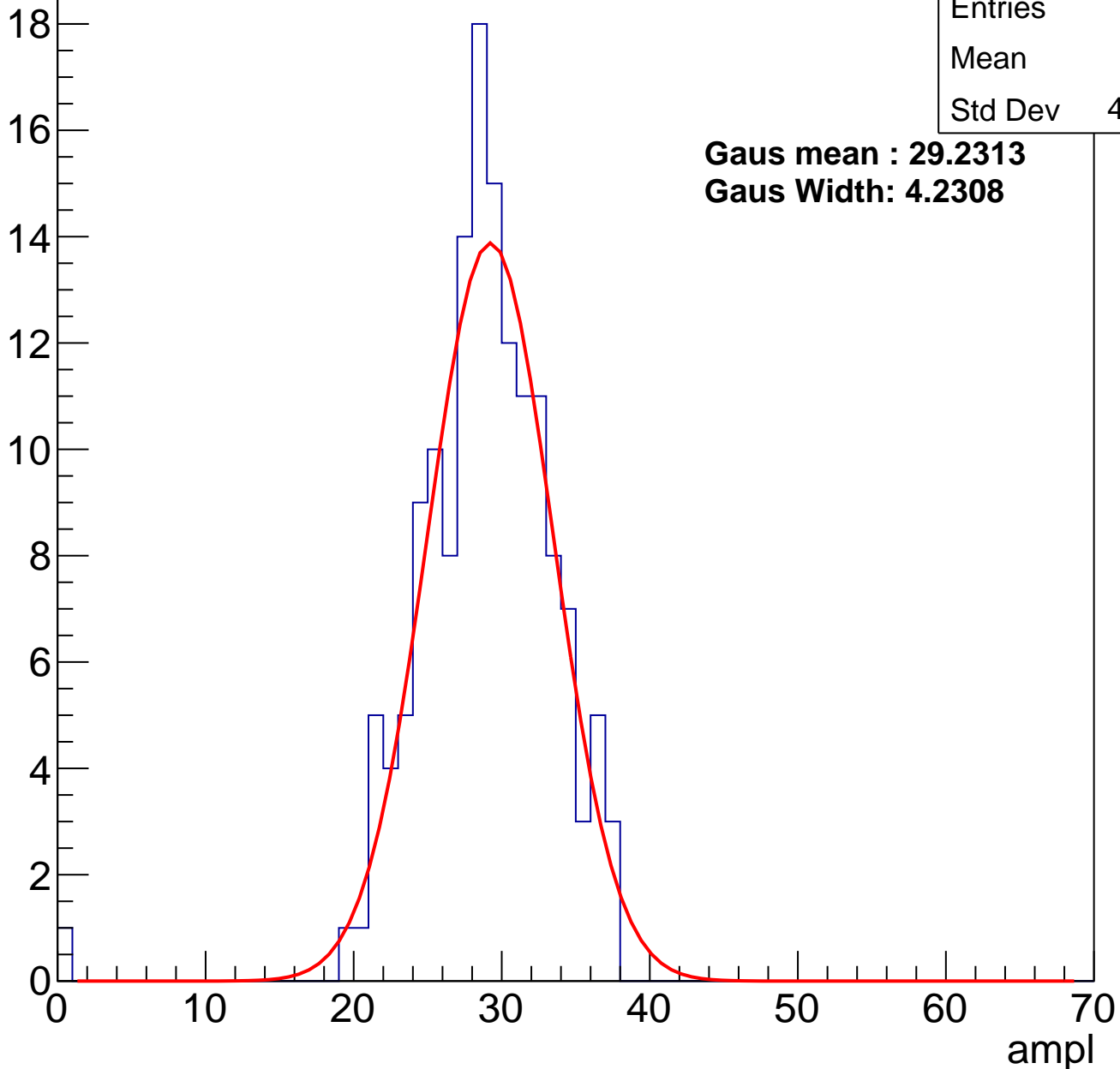
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	28.4
Std Dev	4.602

Gaus mean : 29.2313

Gaus Width: 4.2308

Entry



B0L101S, U21-ch96, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	36.39
Std Dev	3.91

Gaus mean : 36.8606

Gaus Width: 3.9592

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

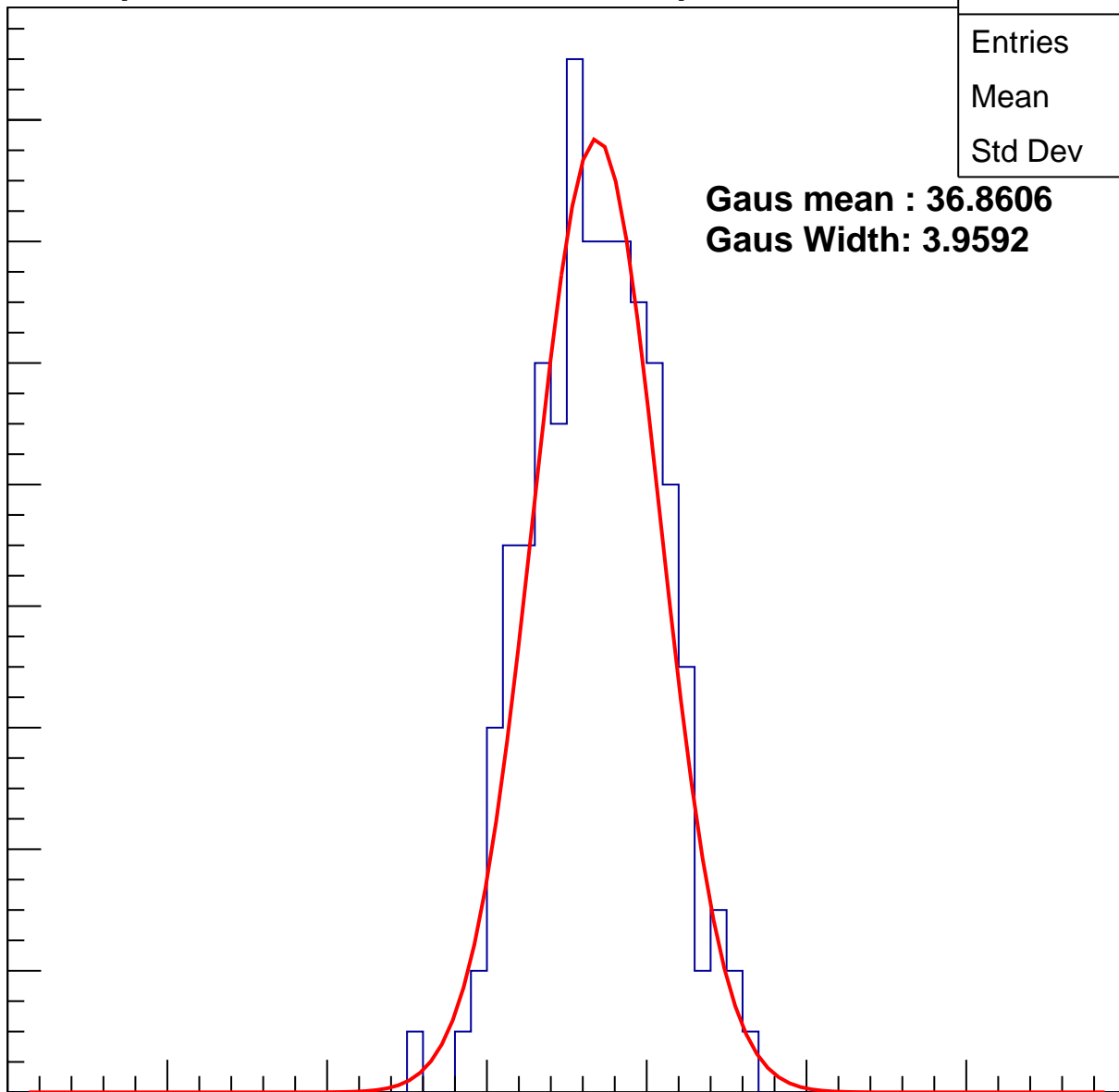
40

50

60

70

ampl



B0L101S, U21-ch96, adc2

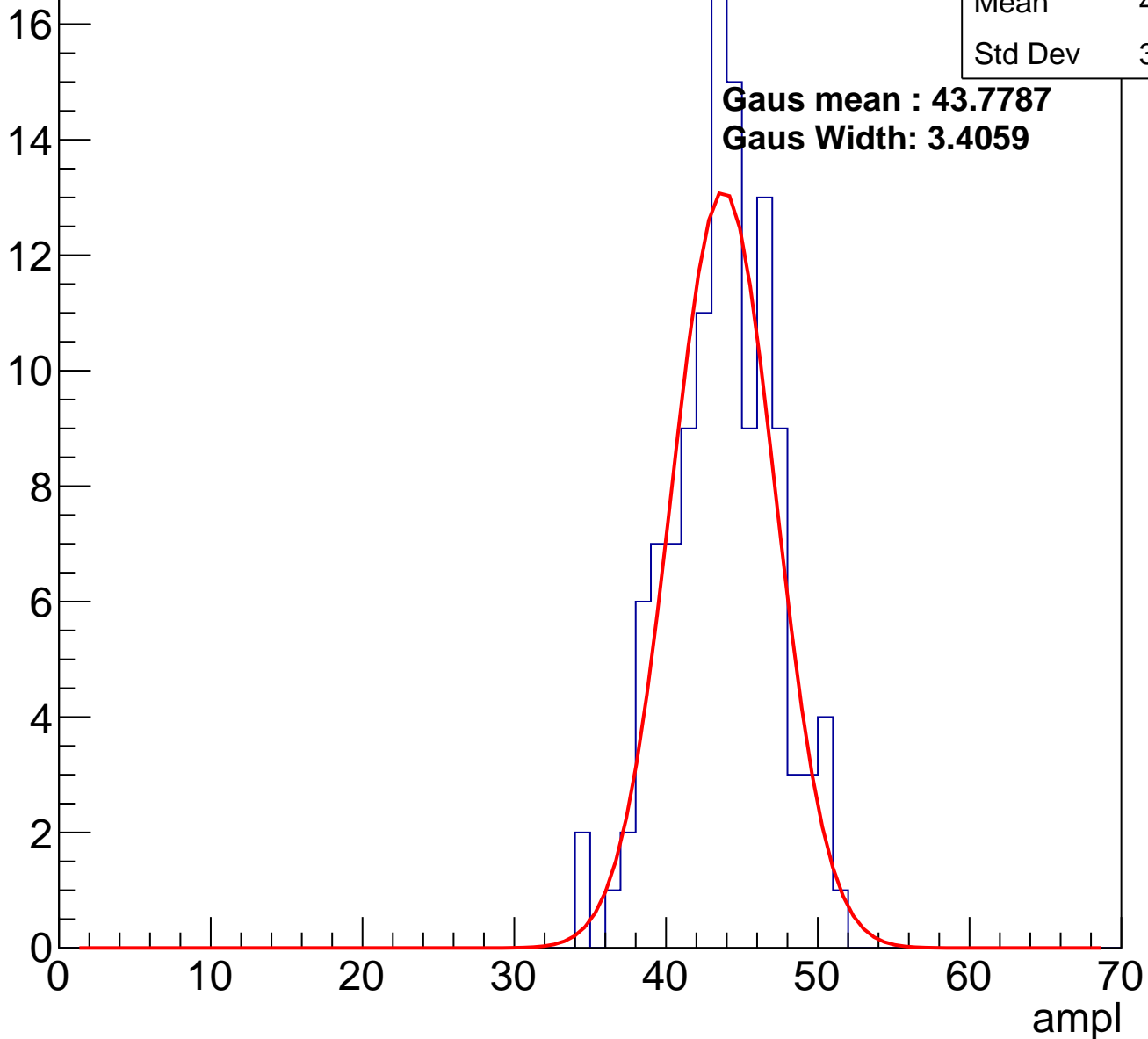
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	119
Mean	43.27
Std Dev	3.432

Gaus mean : 43.7787

Gaus Width: 3.4059



B0L101S, U21-ch96, adc3

calib_packv5_042523_0143.root, FC#1, port C1

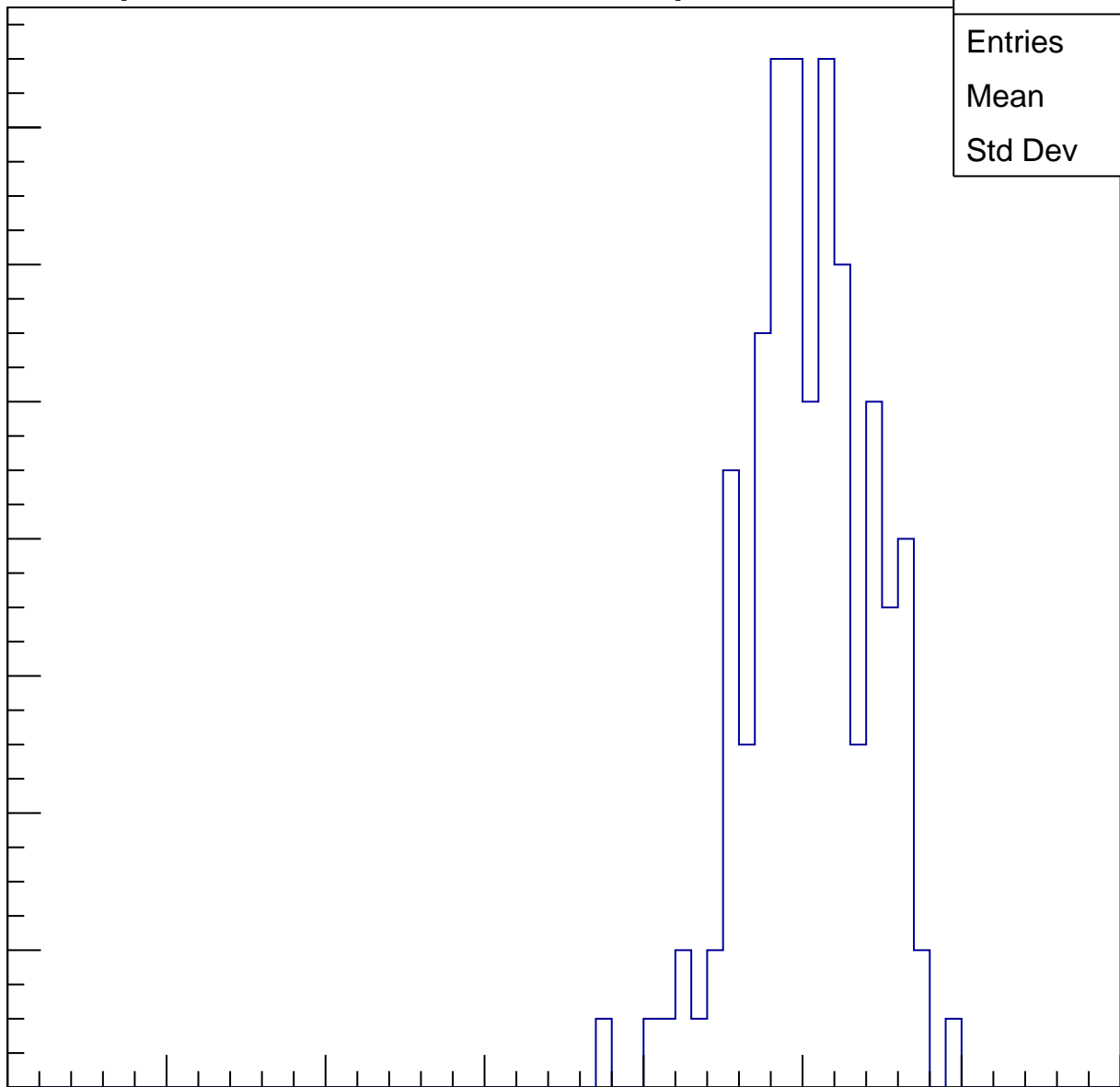
Entries	133
Mean	49.92
Std Dev	3.865

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch96, adc4

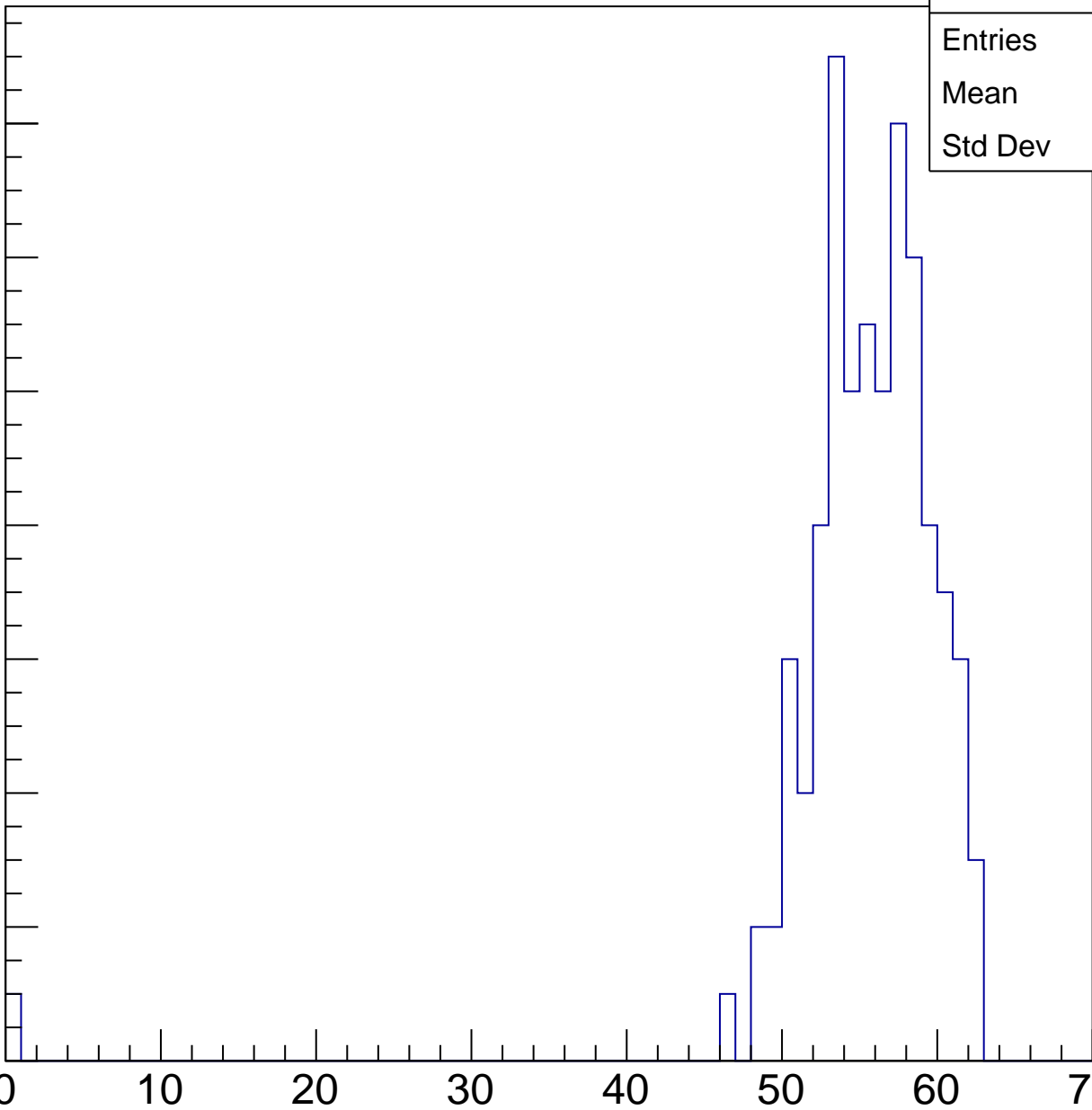
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	54.98
Std Dev	6.105

ampl



B0L101S, U21-ch96, adc5

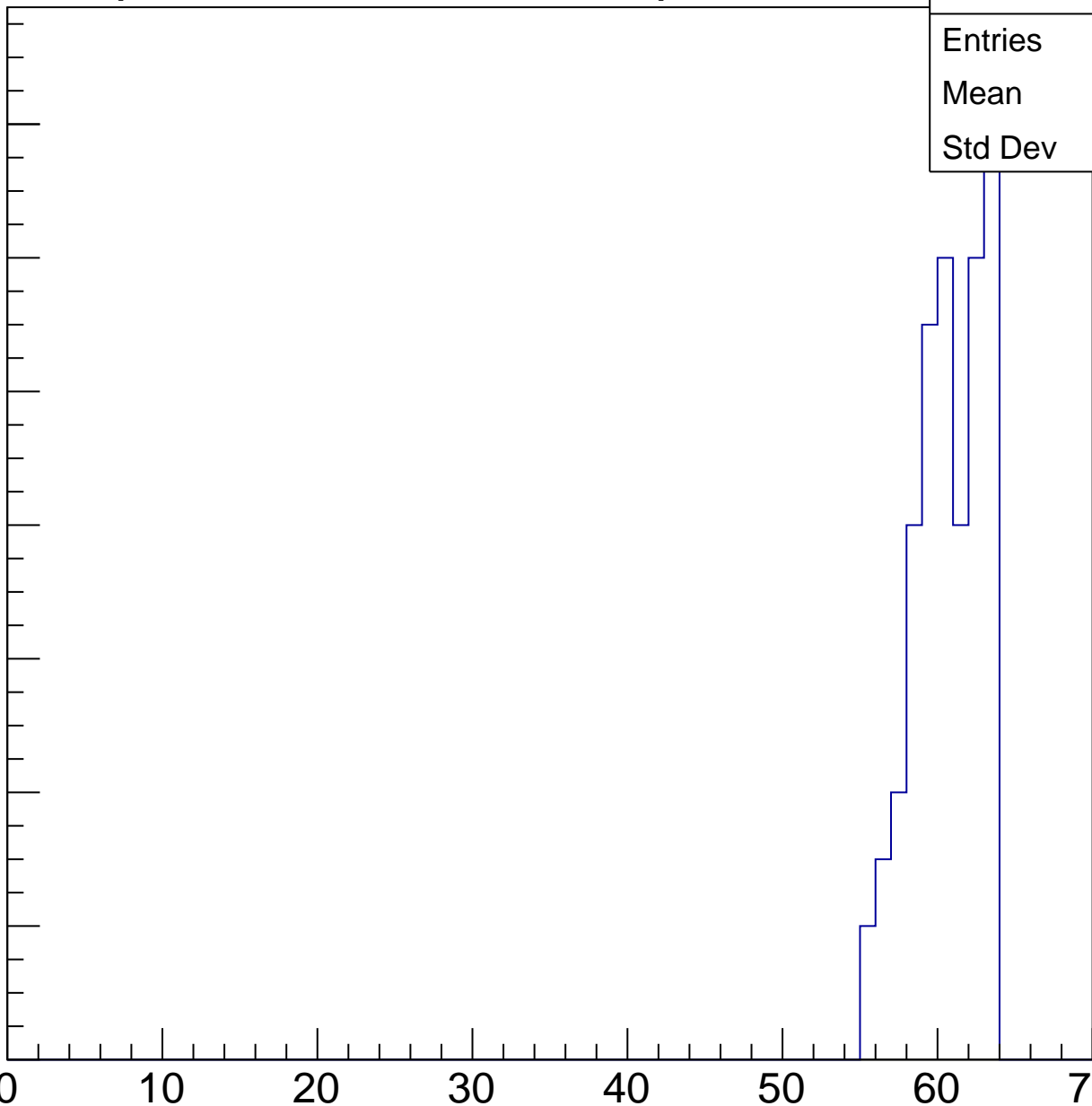
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	60.21
Std Dev	2.205

ampl

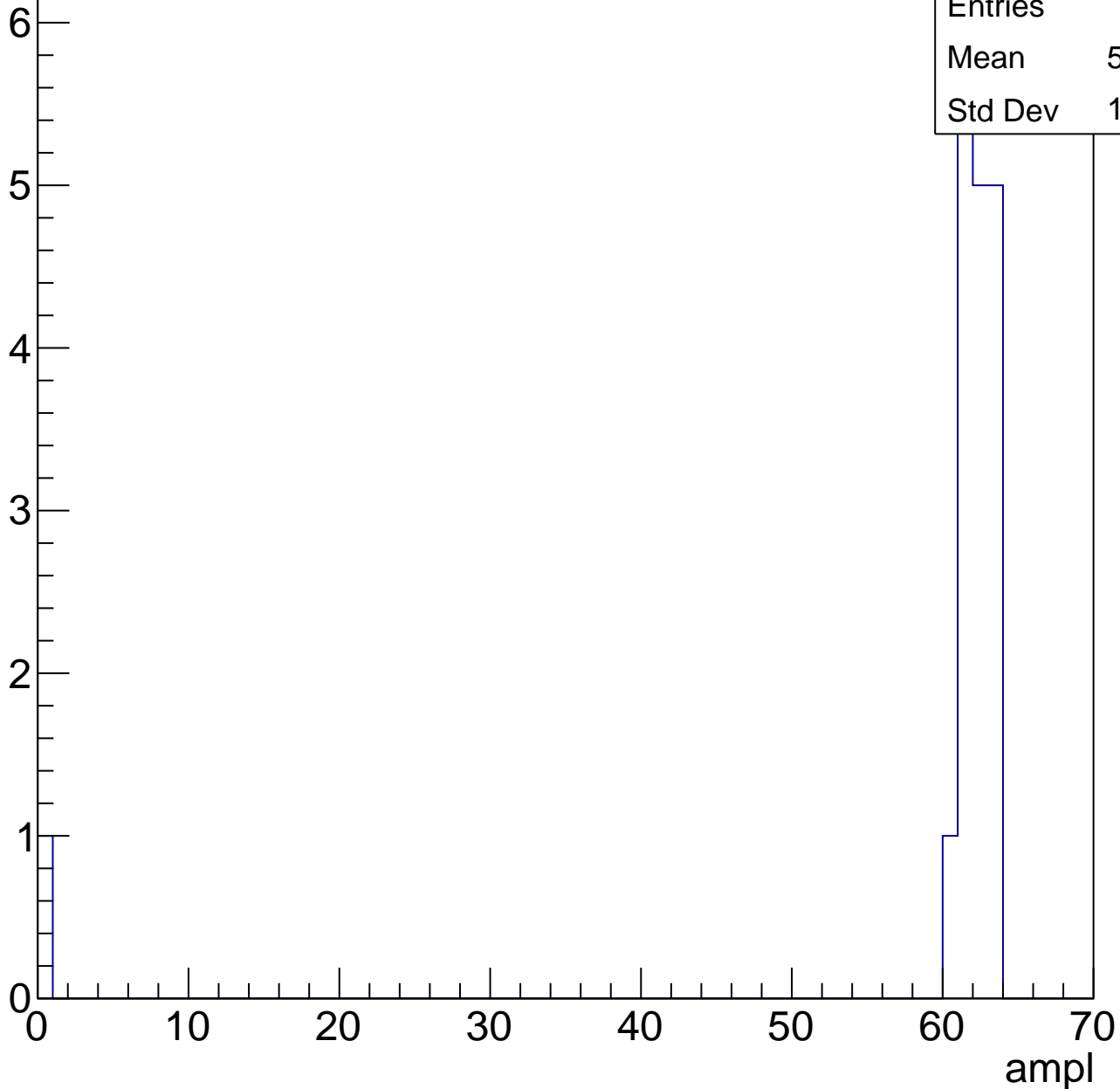


B0L101S, U21-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	58.39
Std Dev	14.19



B0L101S, U21-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch97, adc0

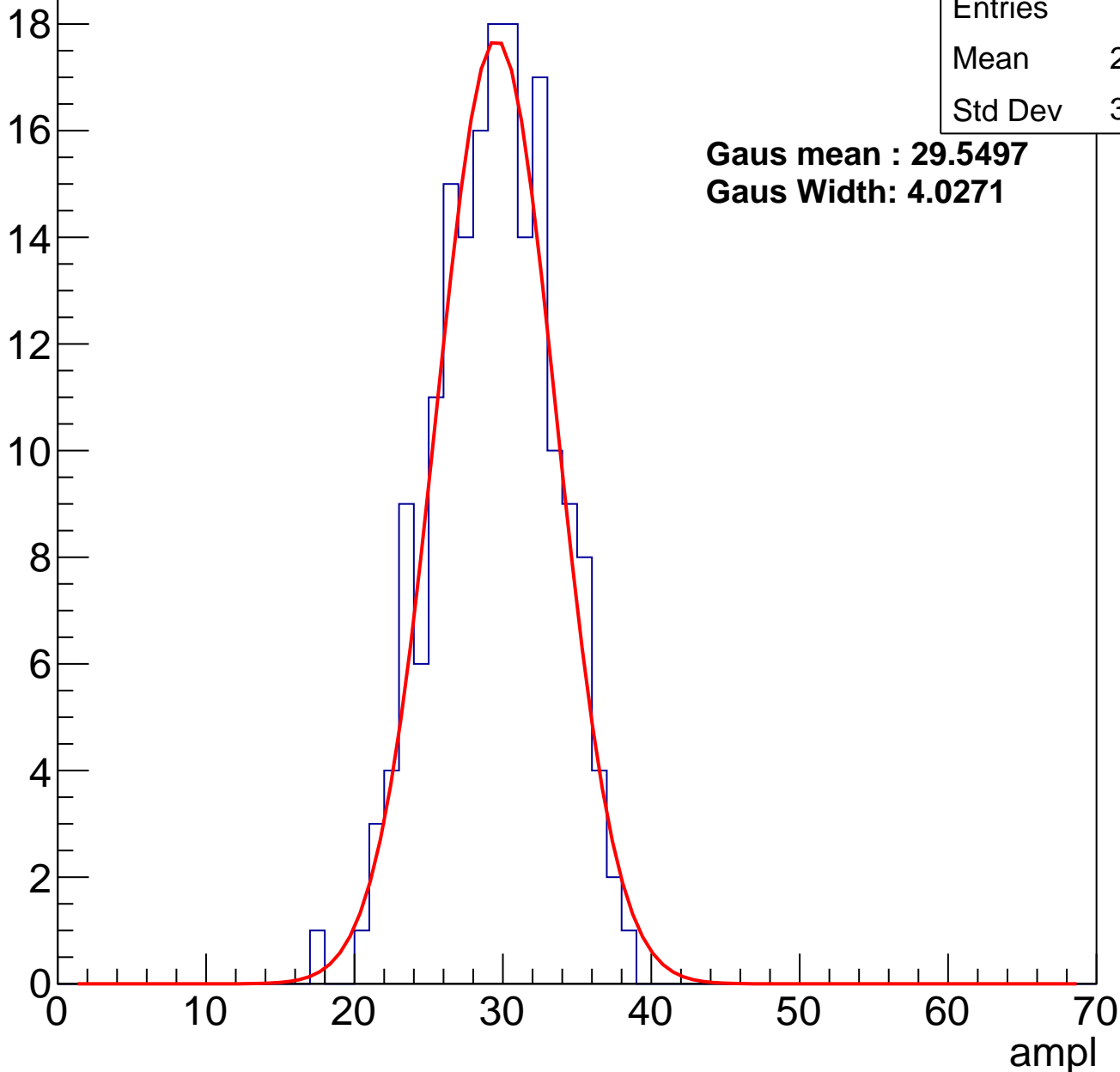
calib_packv5_042523_0143.root, FC#1, port C1

Entries	181
Mean	28.96
Std Dev	3.915

Gaus mean : 29.5497

Gaus Width: 4.0271

Entry



B0L101S, U21-ch97, adc1

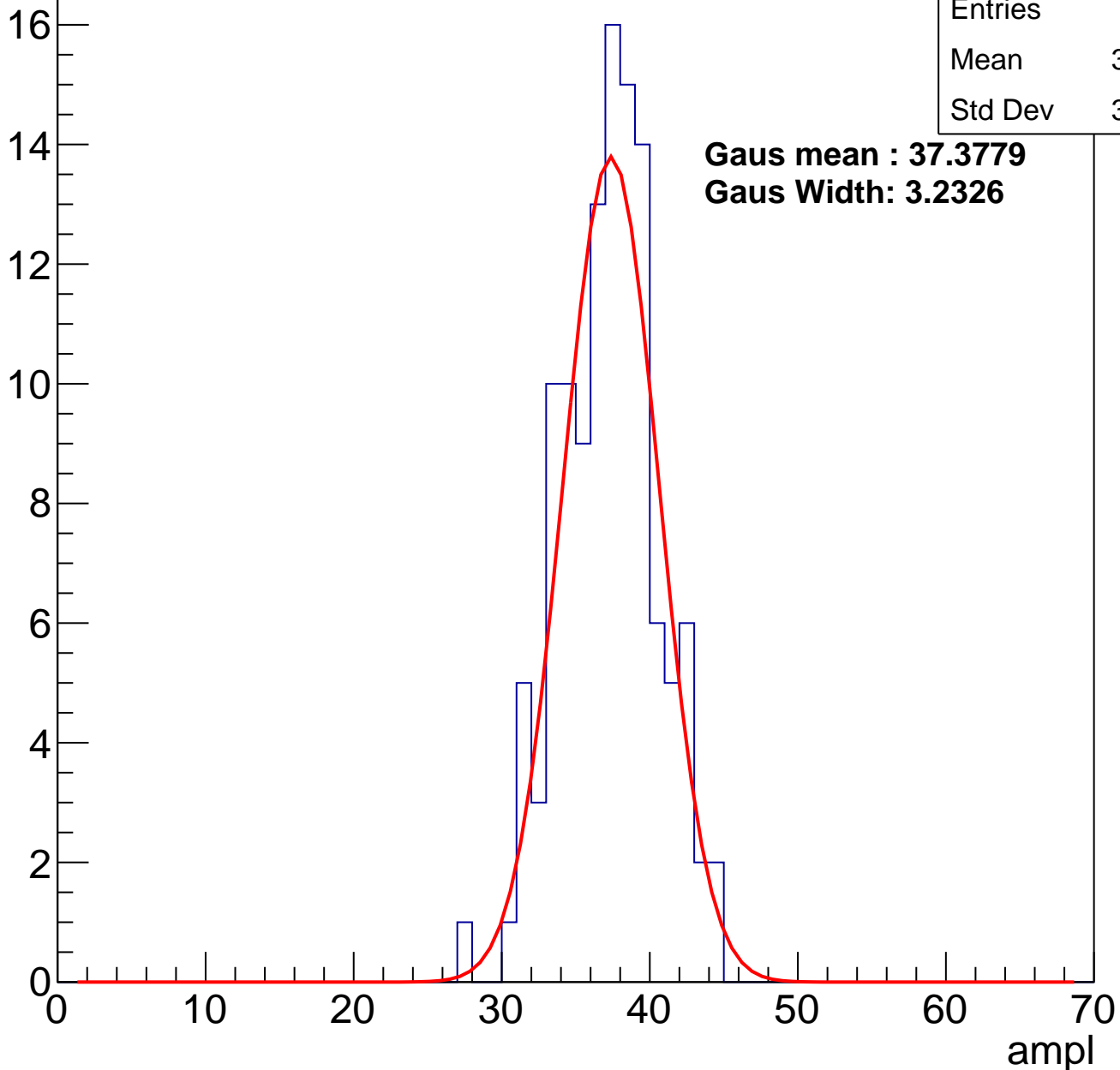
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	36.78
Std Dev	3.218

Gaus mean : 37.3779

Gaus Width: 3.2326

Entry



B0L101S, U21-ch97, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	42.96
Std Dev	3.845

Gaus mean : 43.9914

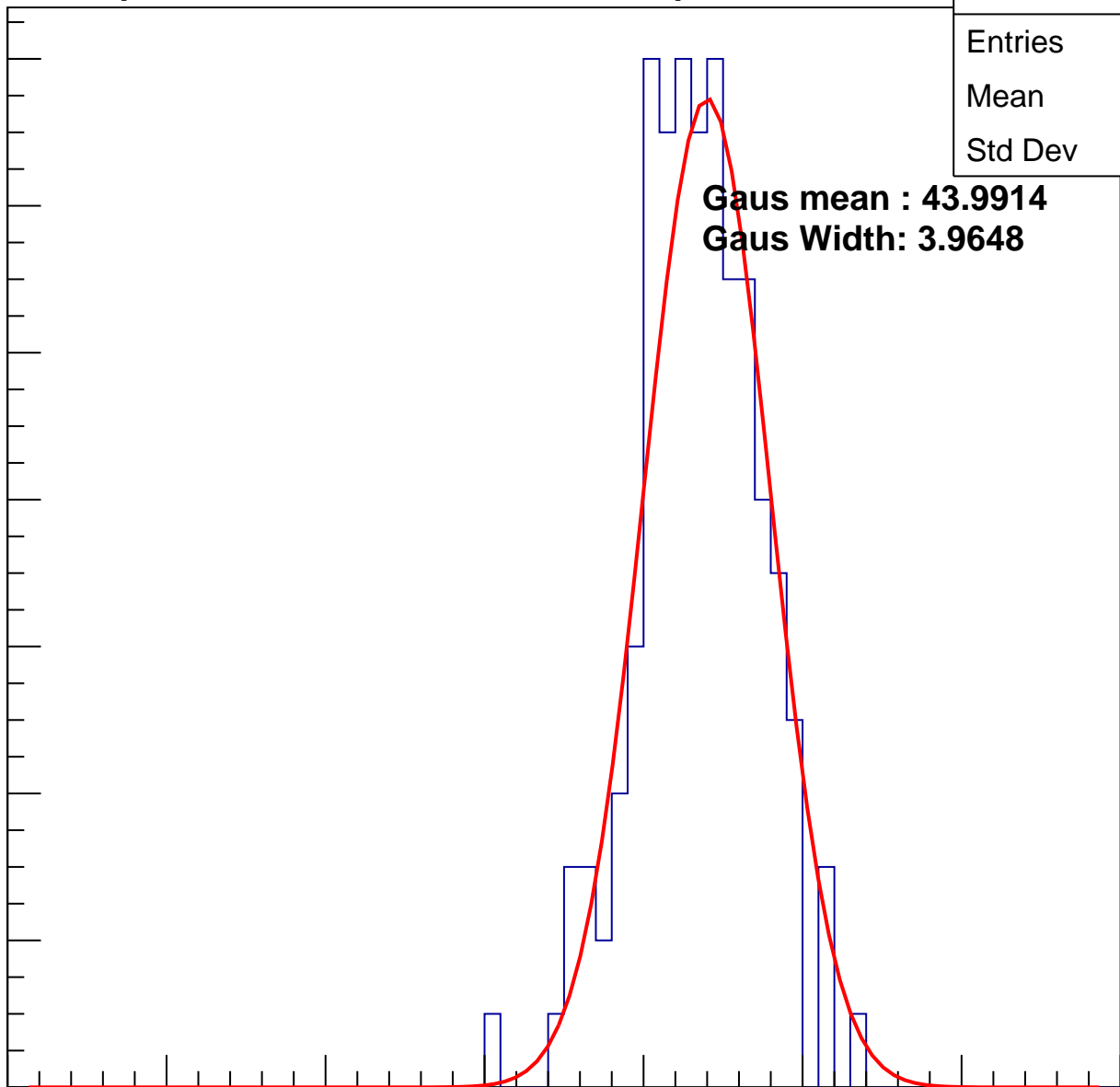
Gaus Width: 3.9648

Entry

14
12
10
8
6
4
2
0

ampl

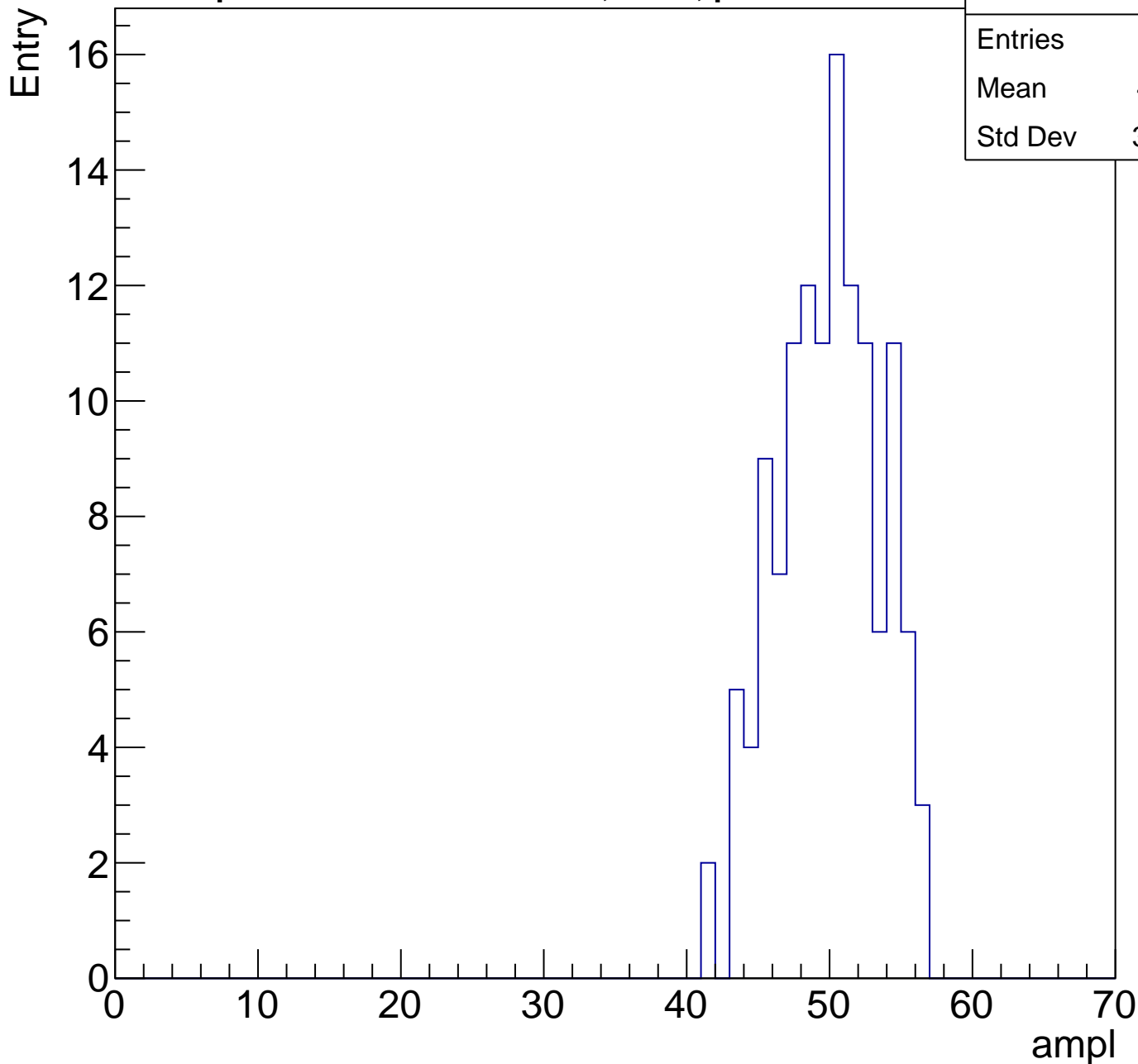
0 10 20 30 40 50 60 70



B0L101S, U21-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	49.41
Std Dev	3.503



B0L101S, U21-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

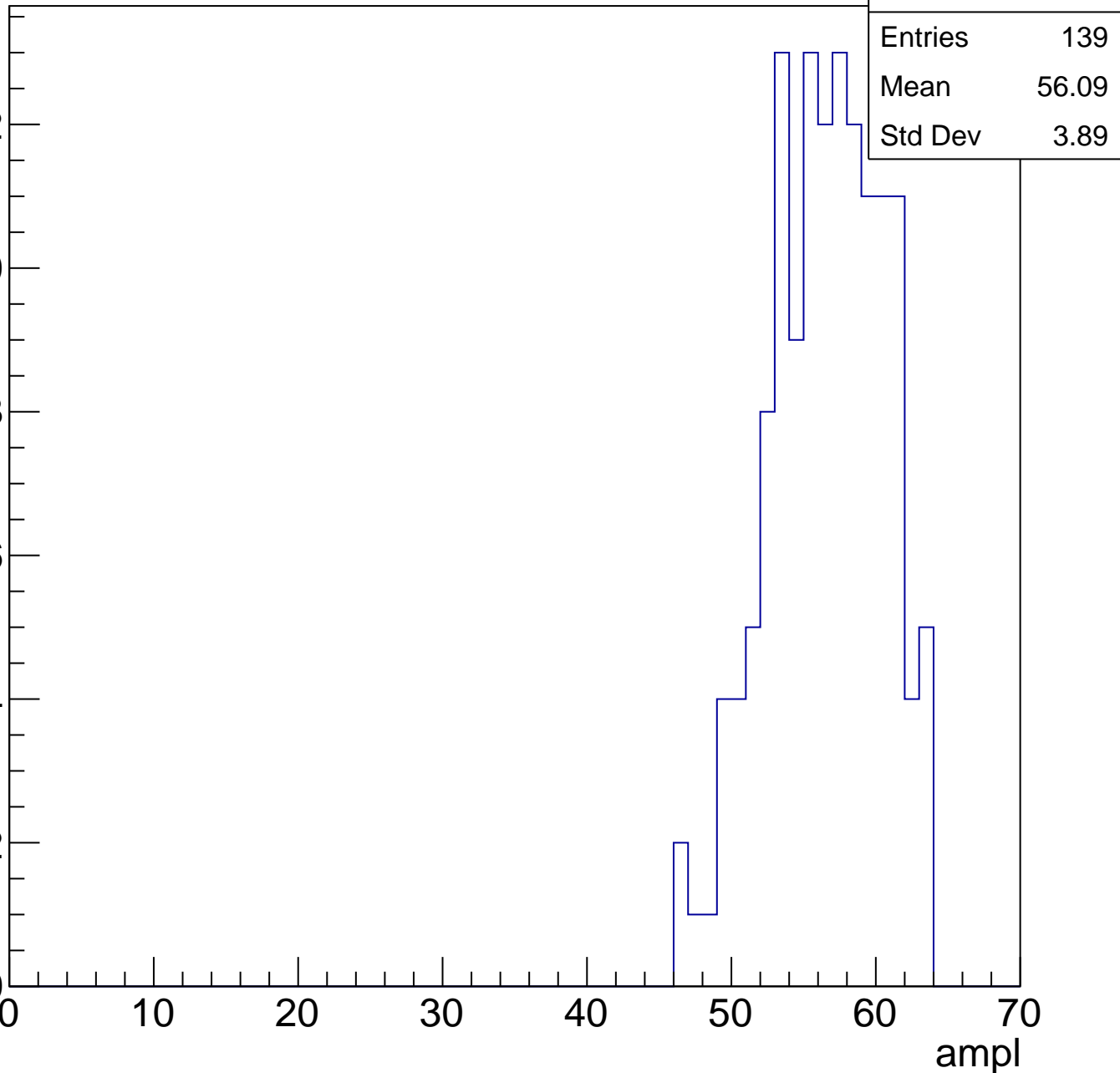
40

50

60

ampl

Entries	139
Mean	56.09
Std Dev	3.89



B0L101S, U21-ch97, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	71
Mean	59.01
Std Dev	7.469

ampl

0

10

20

30

40

50

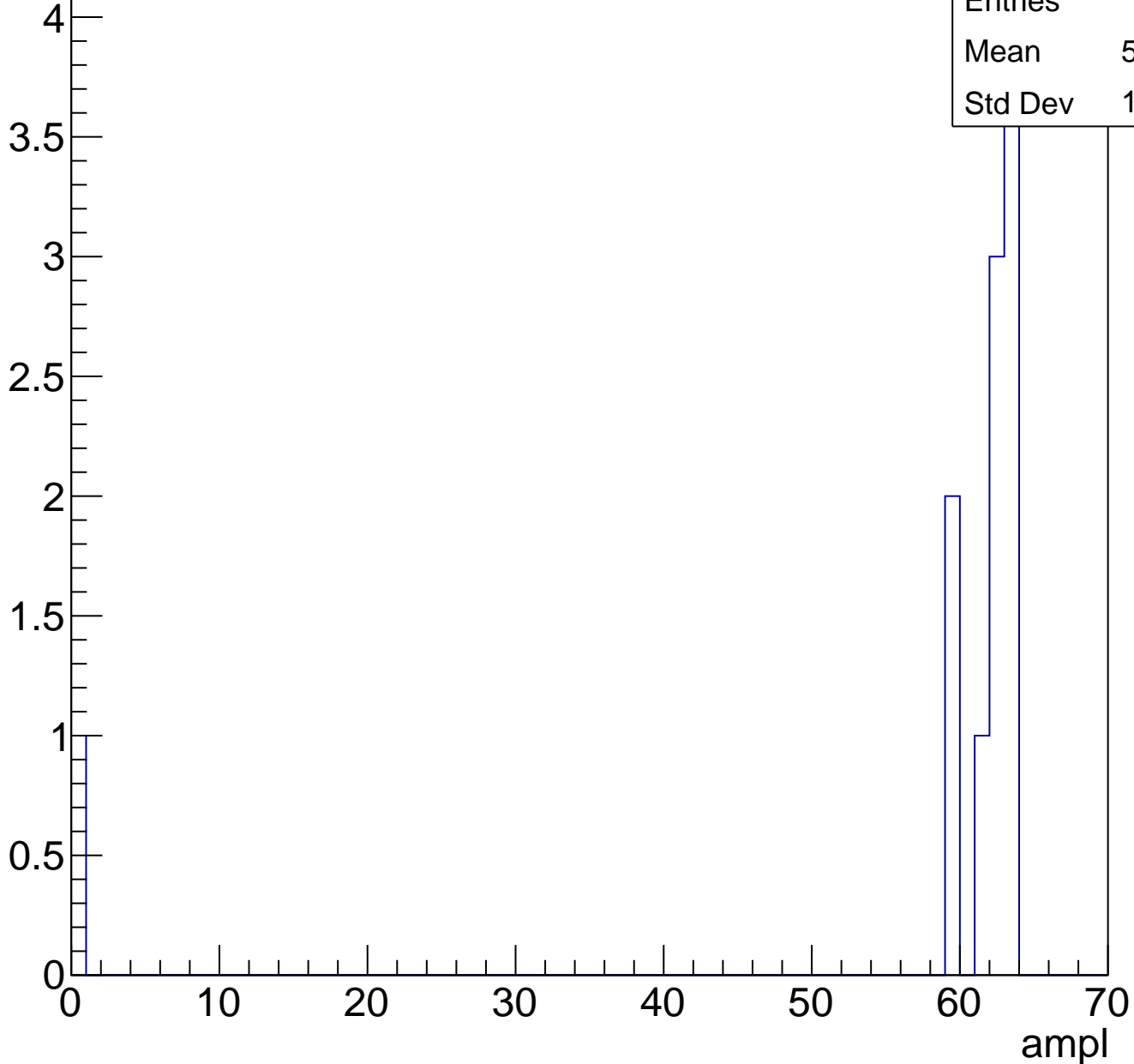
60

70

B0L101S, U21-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

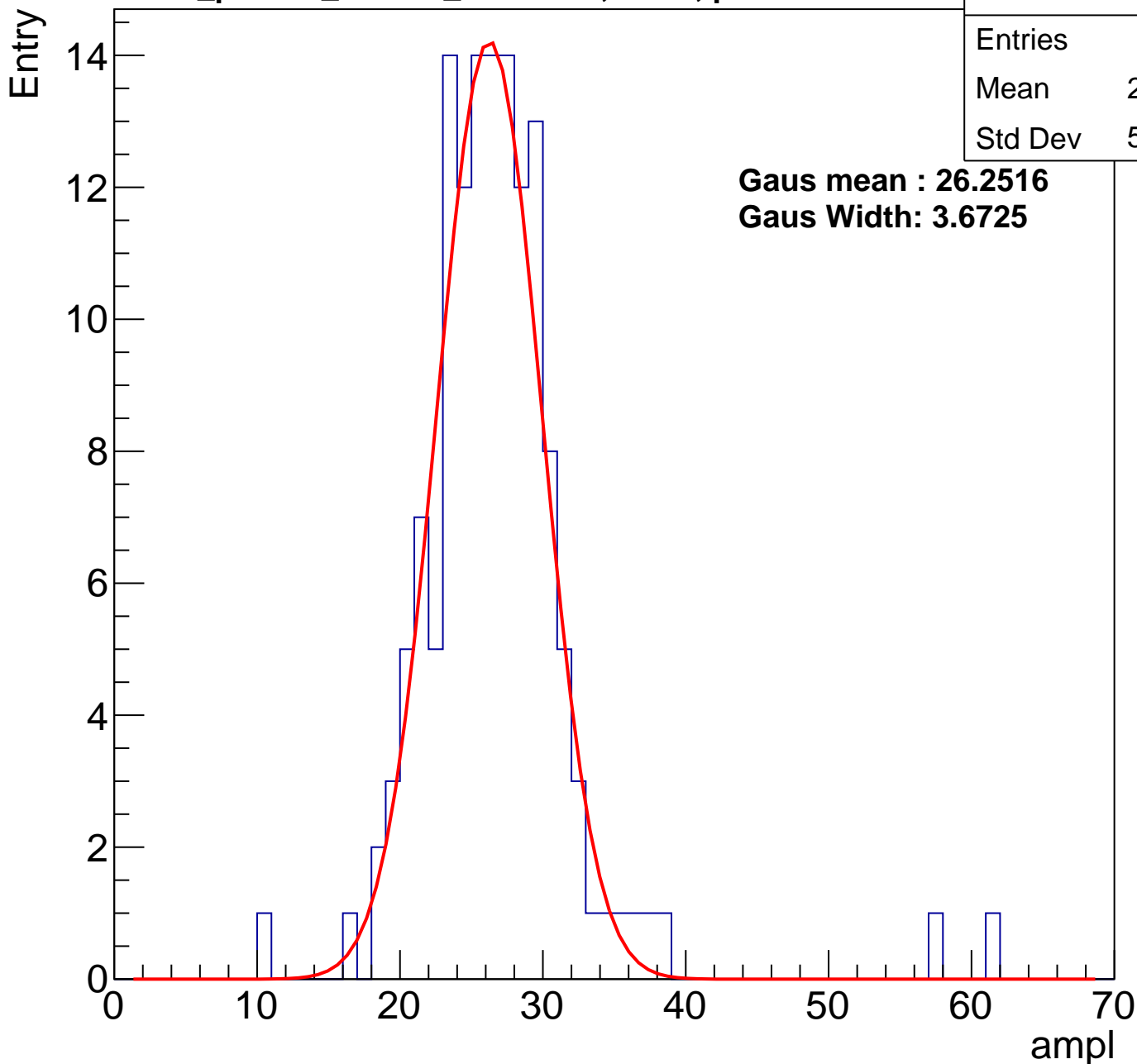
B0L101S, U21-ch98, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	26.33
Std Dev	5.664

Gaus mean : 26.2516

Gaus Width: 3.6725

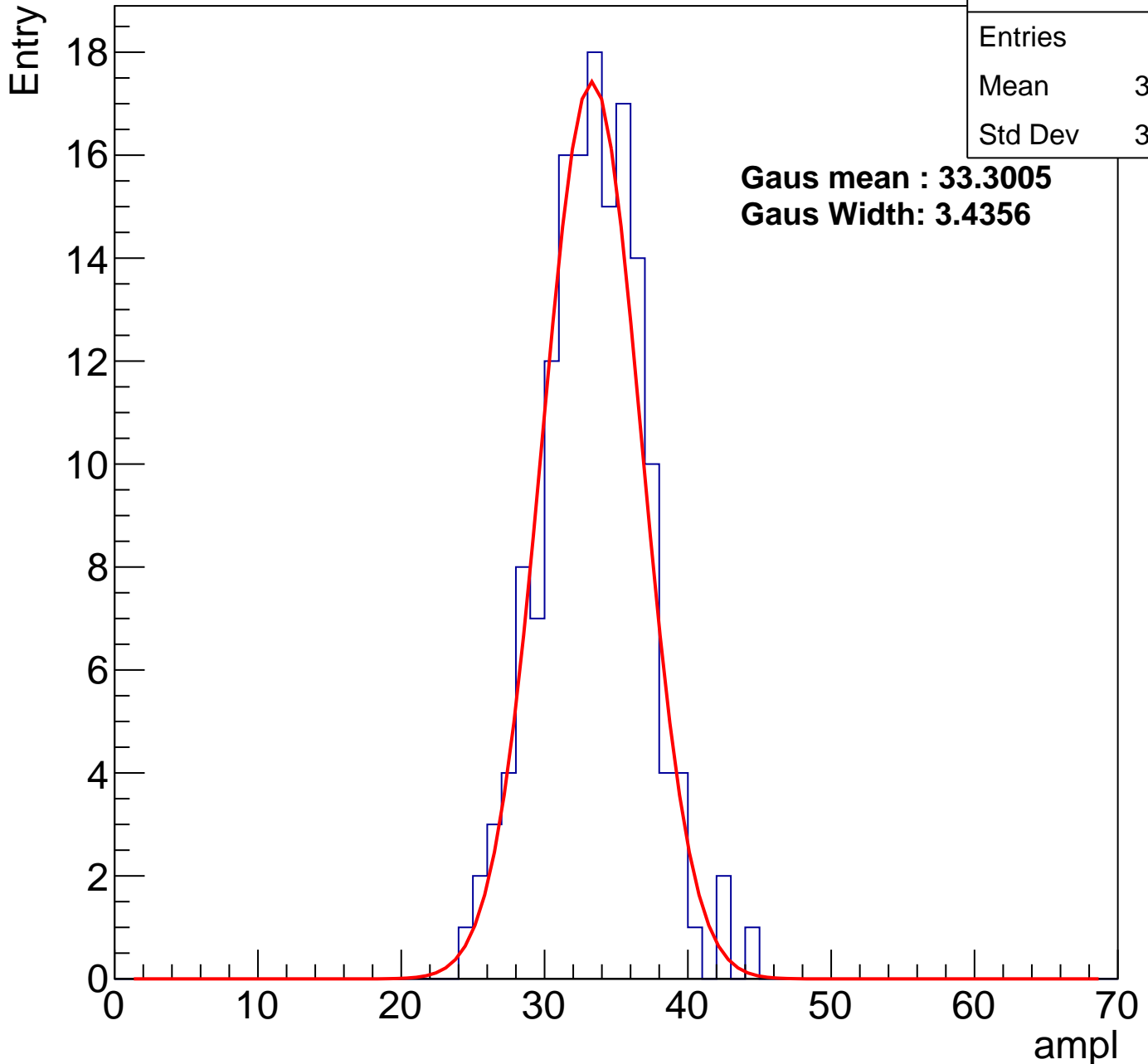


B0L101S, U21-ch98, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	32.93
Std Dev	3.542

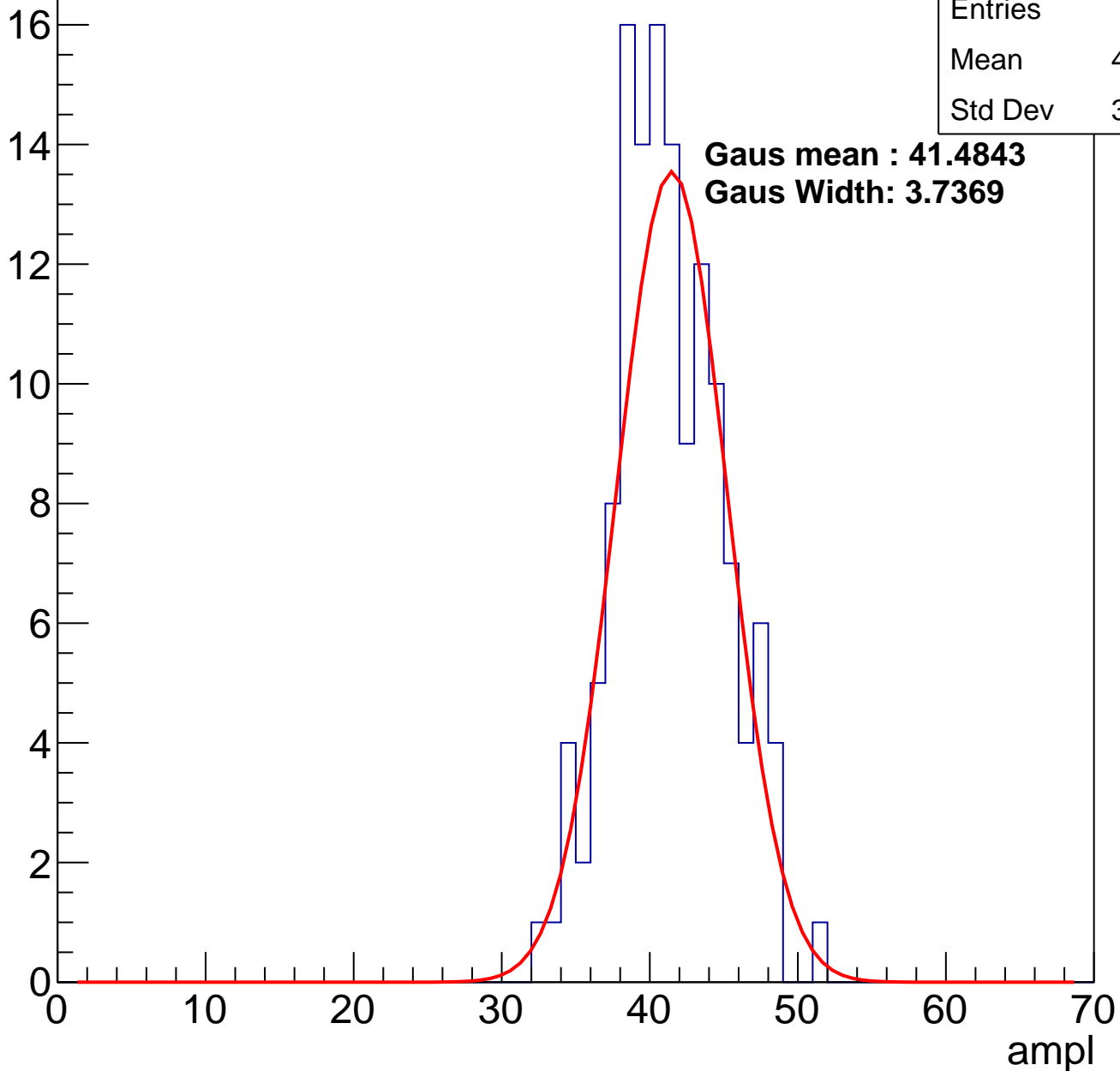
Gaus mean : 33.3005
Gaus Width: 3.4356



B0L101S, U21-ch98, adc2

calib_packv5_042523_0143.root, FC#1, port C1

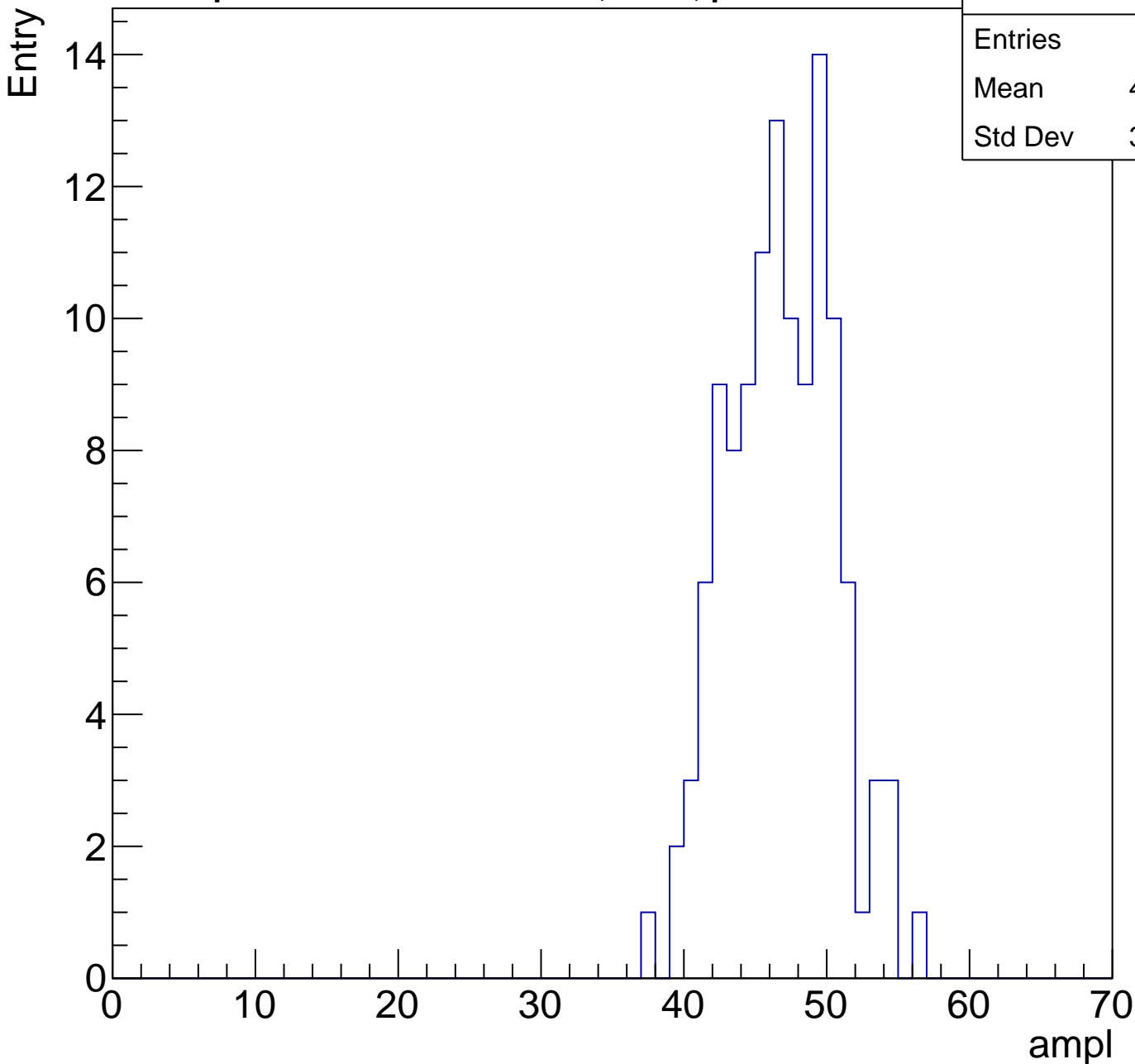
Entry



B0L101S, U21-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

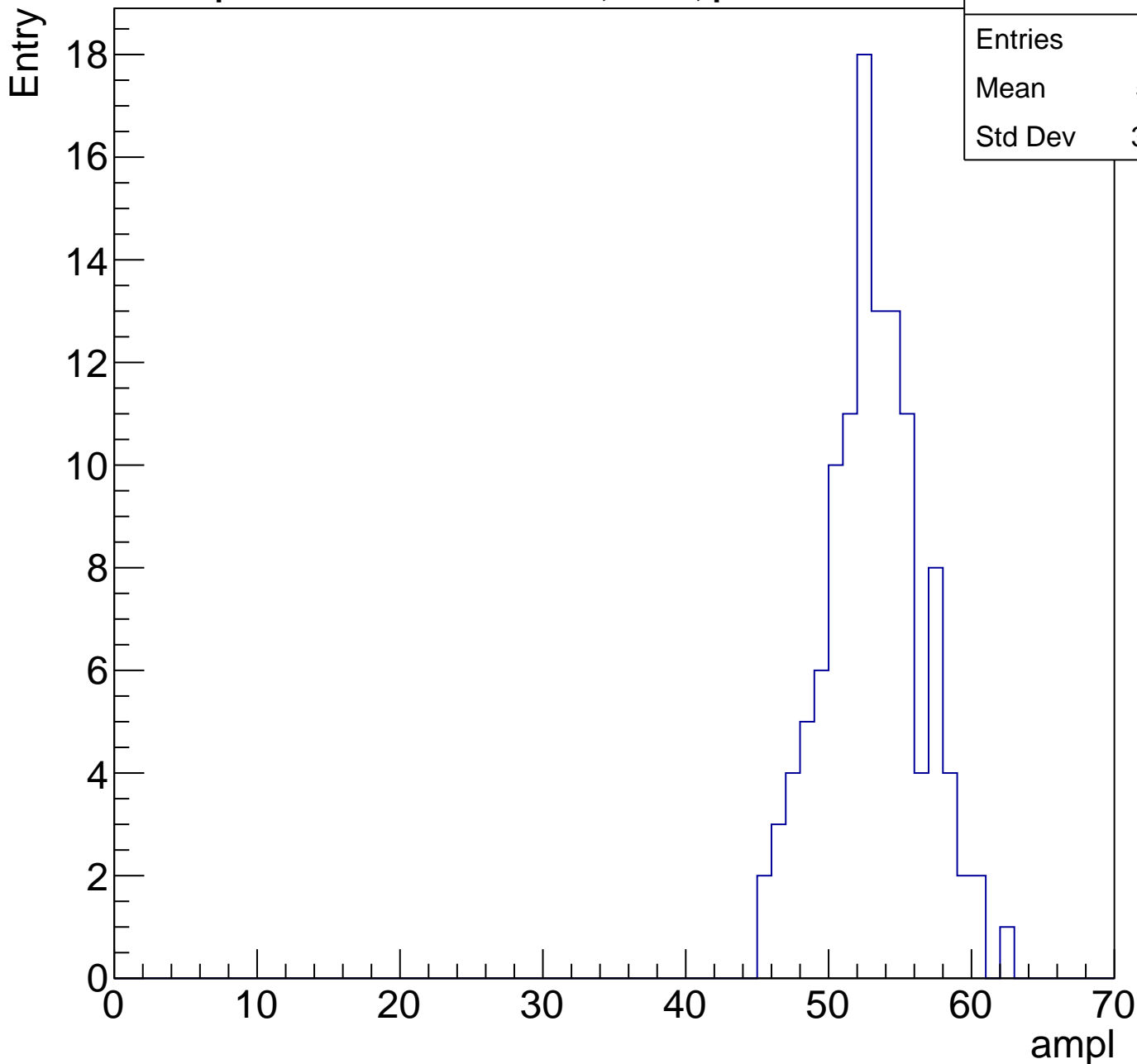
Entries	119
Mean	46.34
Std Dev	3.716



B0L101S, U21-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	52.61
Std Dev	3.392



B0L101S, U21-ch98, adc5

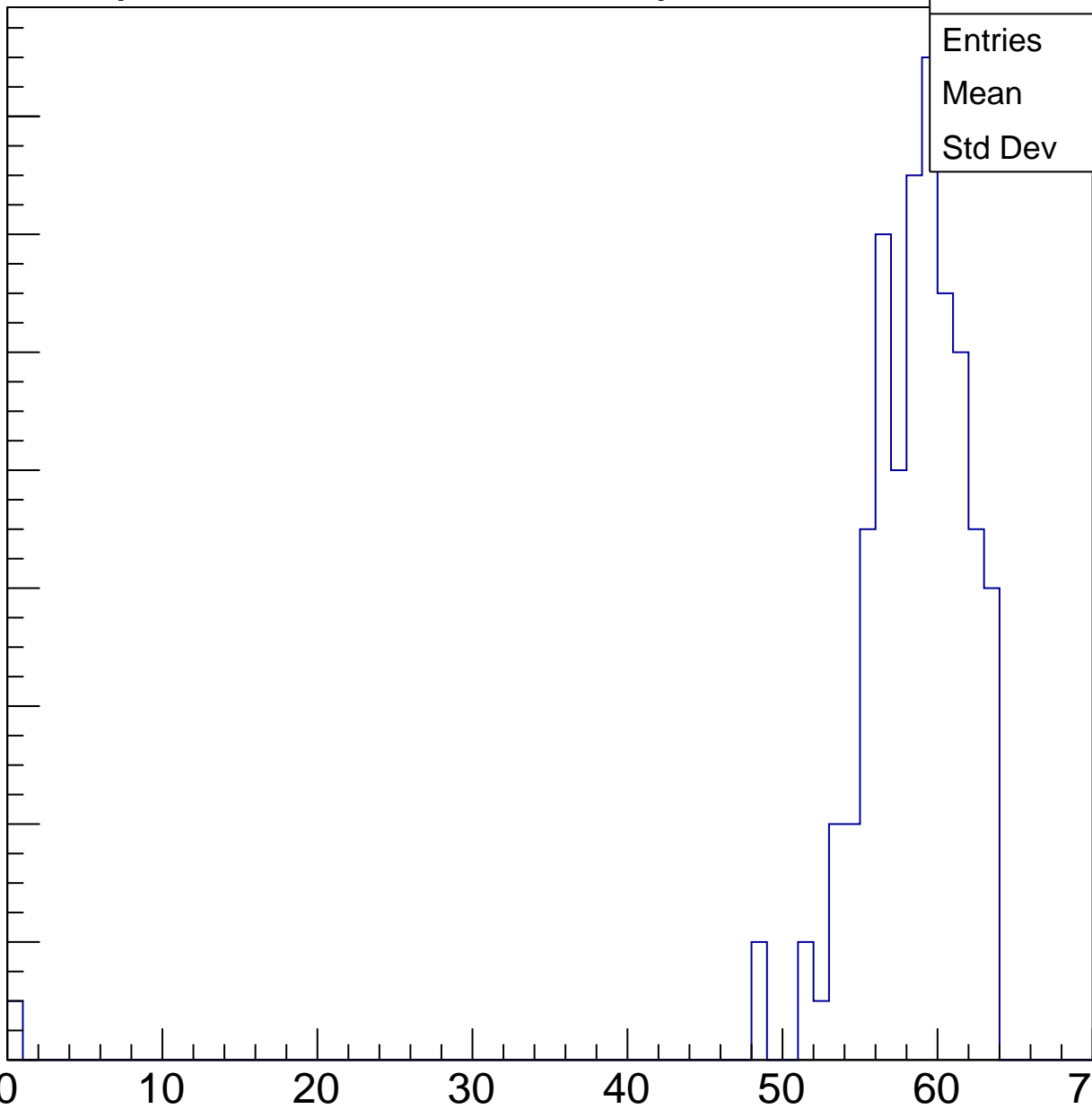
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	57.64
Std Dev	6.108

ampl



B0L101S, U21-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

41

Mean

59.78

Std Dev

9.618

ampl

0

10

20

30

40

50

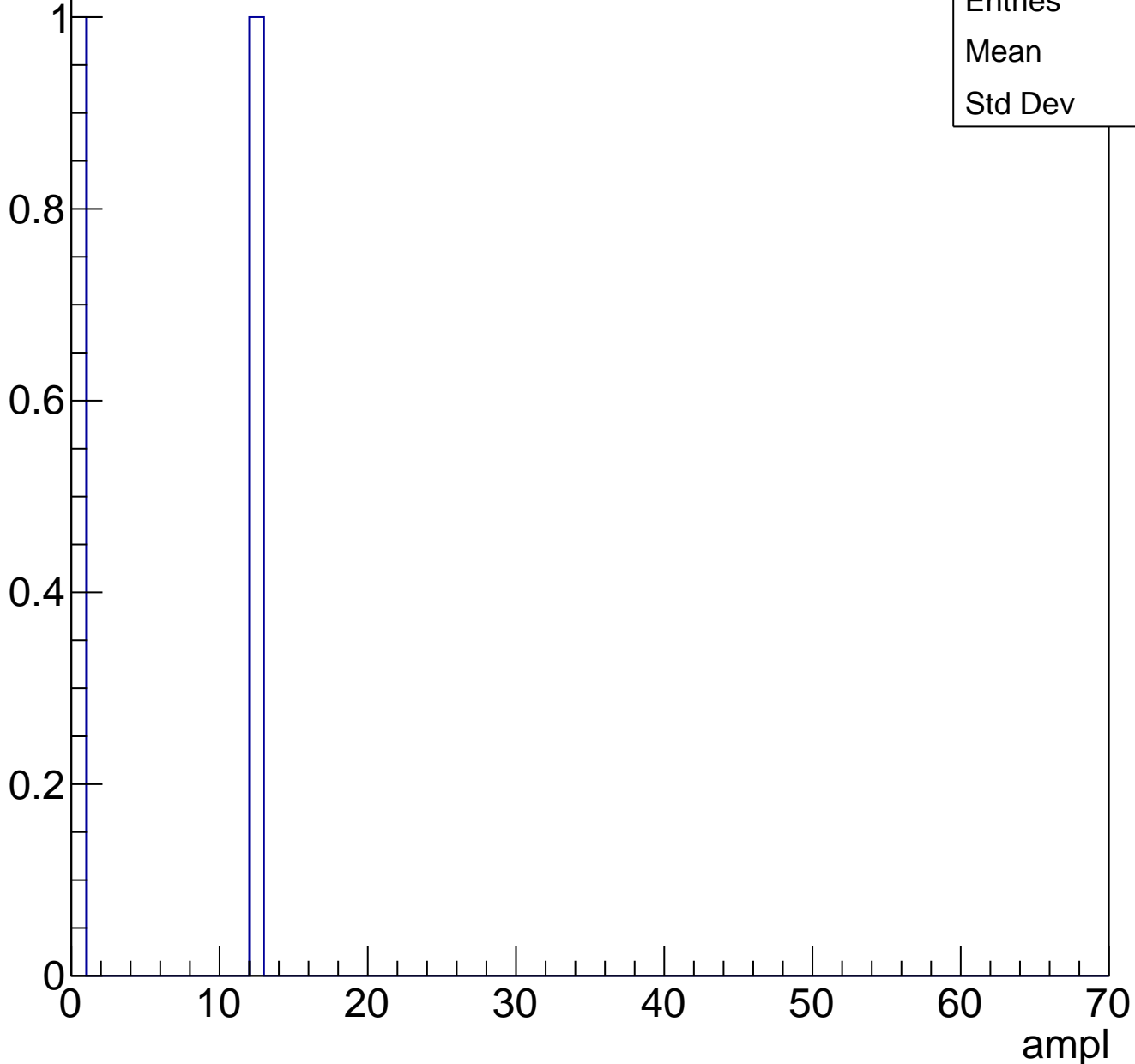
60

70

B0L101S, U21-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	6
Std Dev	6

B0L101S, U21-ch99, adc0

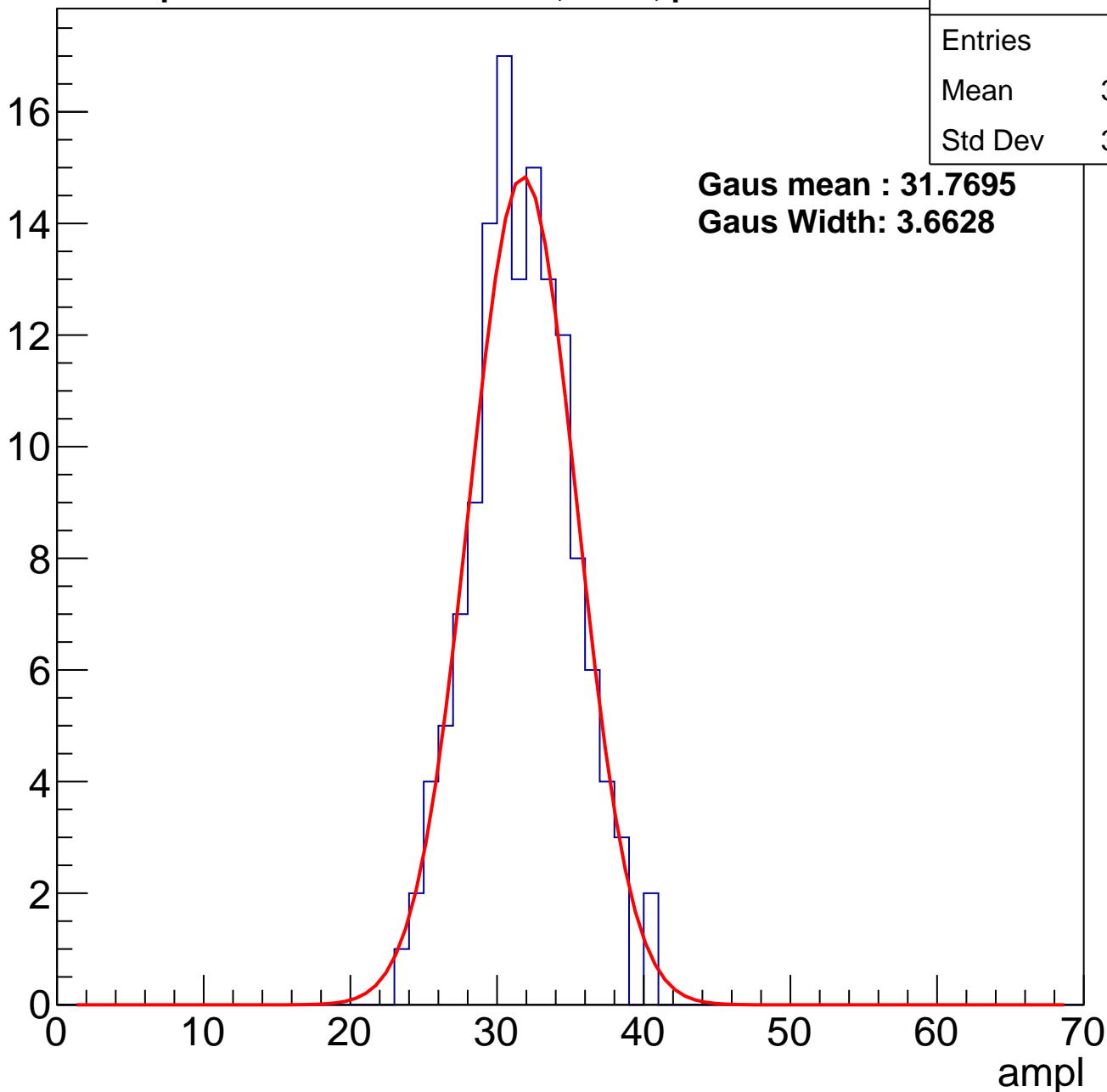
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	31.23
Std Dev	3.438

Gaus mean : 31.7695

Gaus Width: 3.6628

Entry



B0L101S, U21-ch99, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	38.14
Std Dev	3.542

Gaus mean : 38.7944

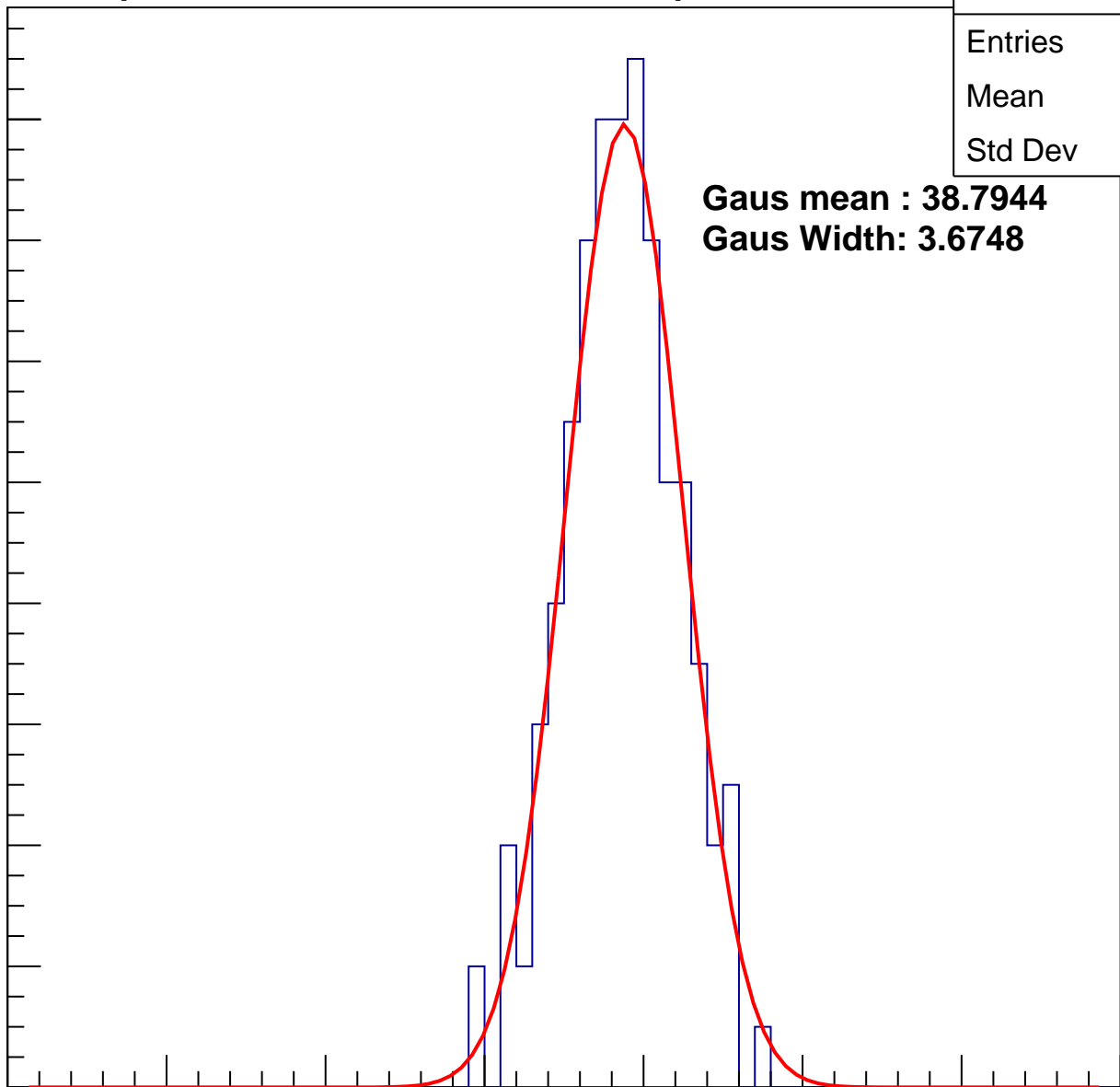
Gaus Width: 3.6748

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch99, adc2

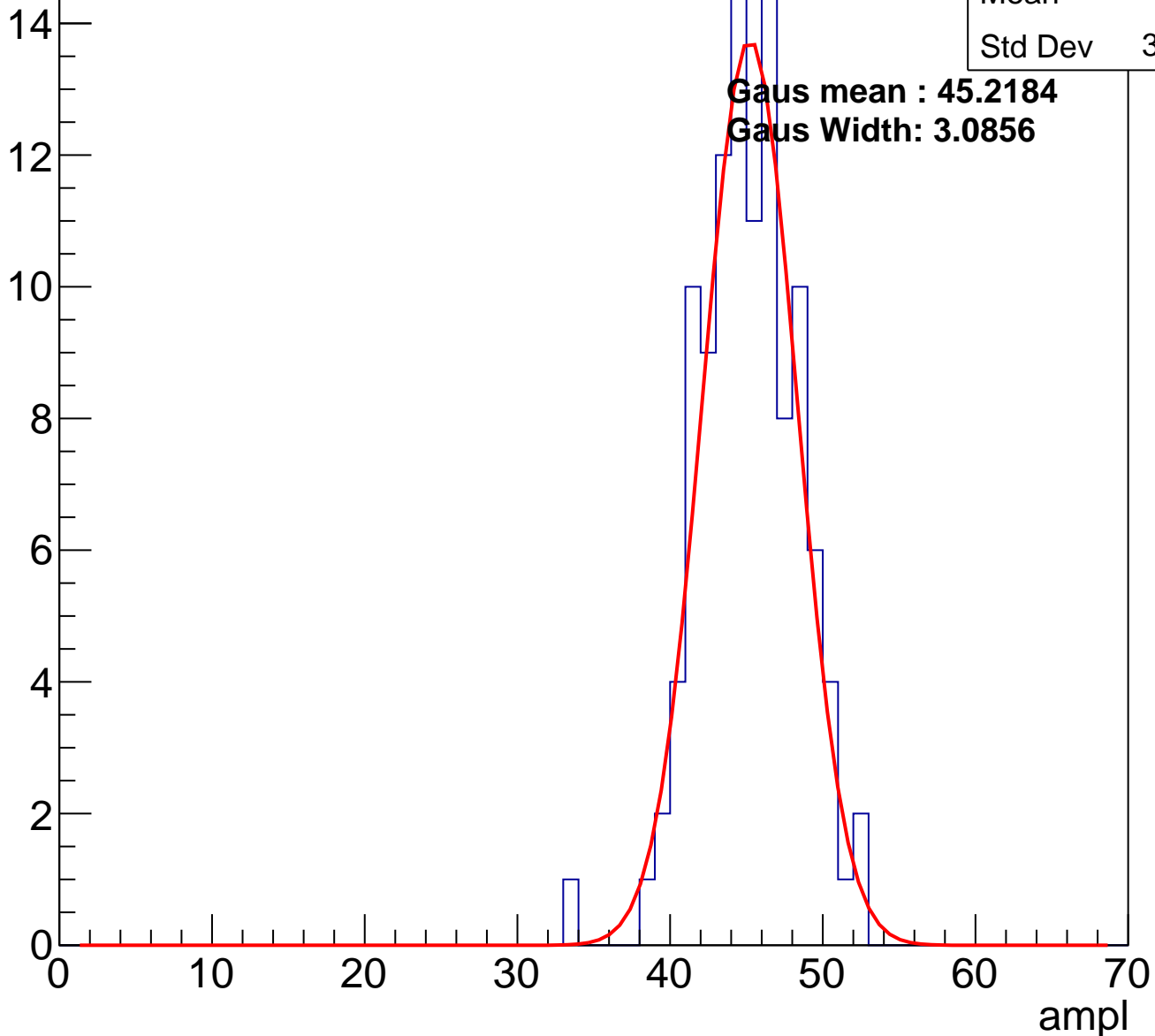
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	44.71
Std Dev	3.186

Gaus mean : 45.2184

Gaus Width: 3.0856

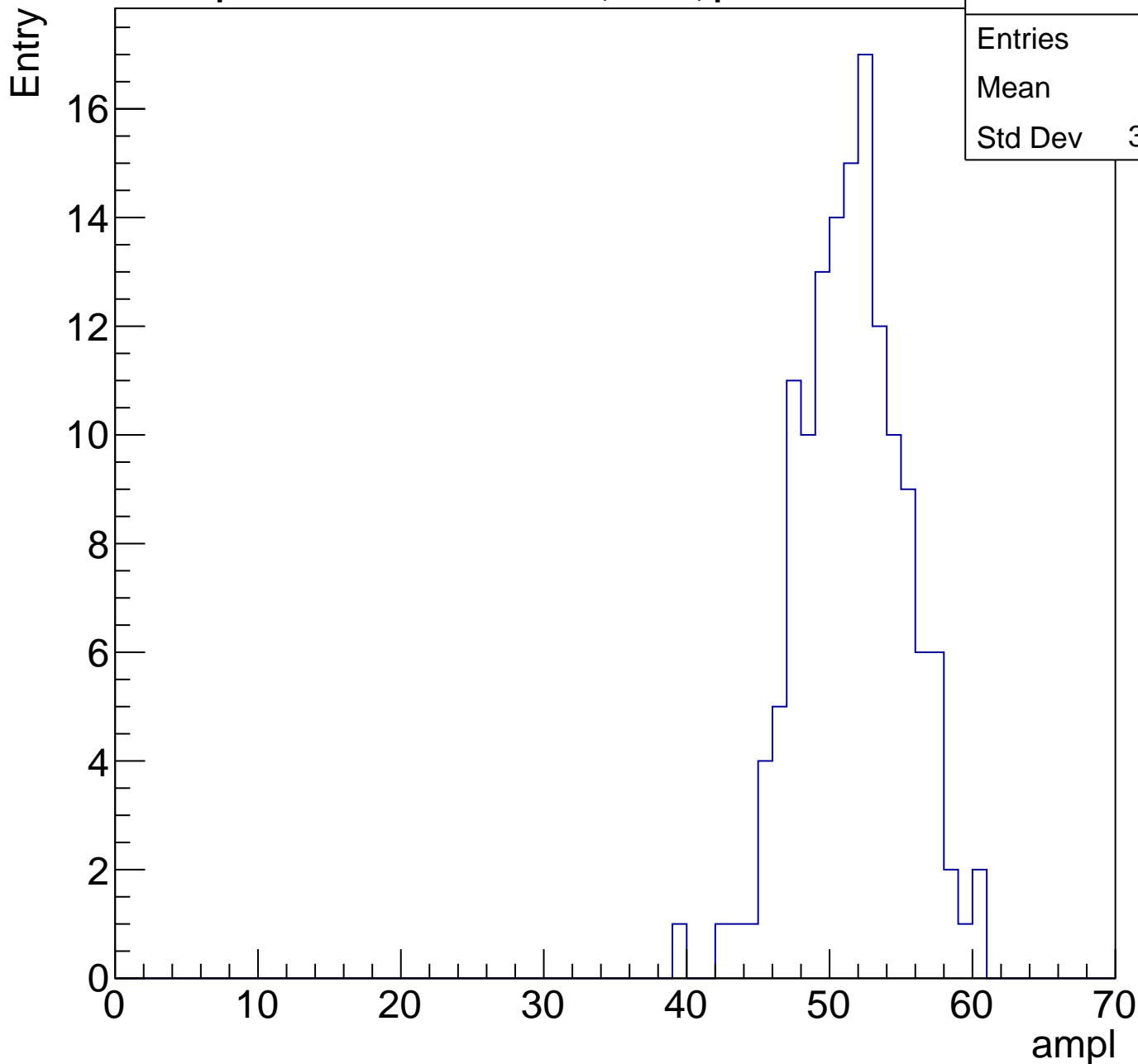
Entry



B0L101S, U21-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	51.1
Std Dev	3.696

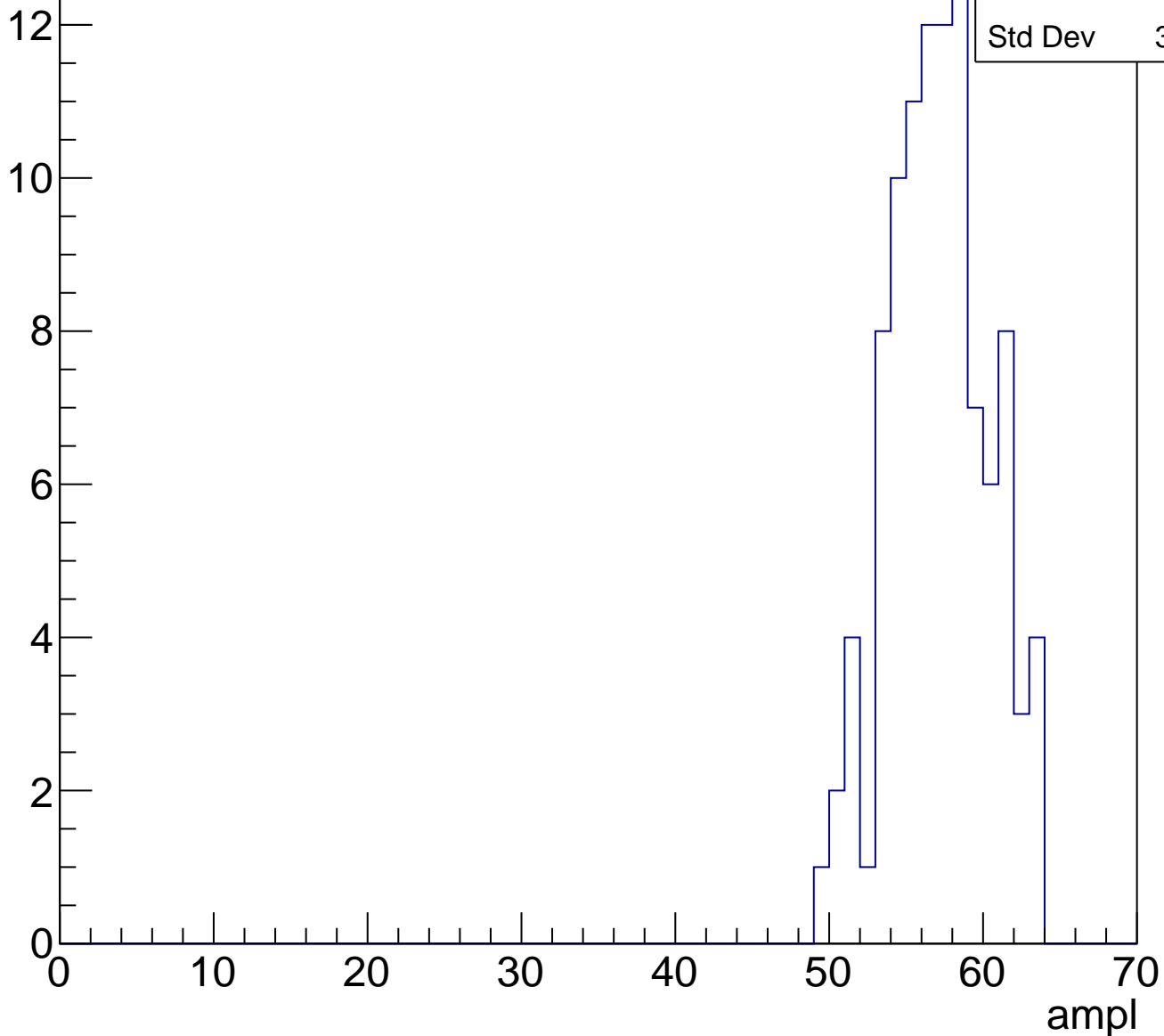


B0L101S, U21-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	56.7
Std Dev	3.189

Entry



B0L101S, U21-ch99, adc5

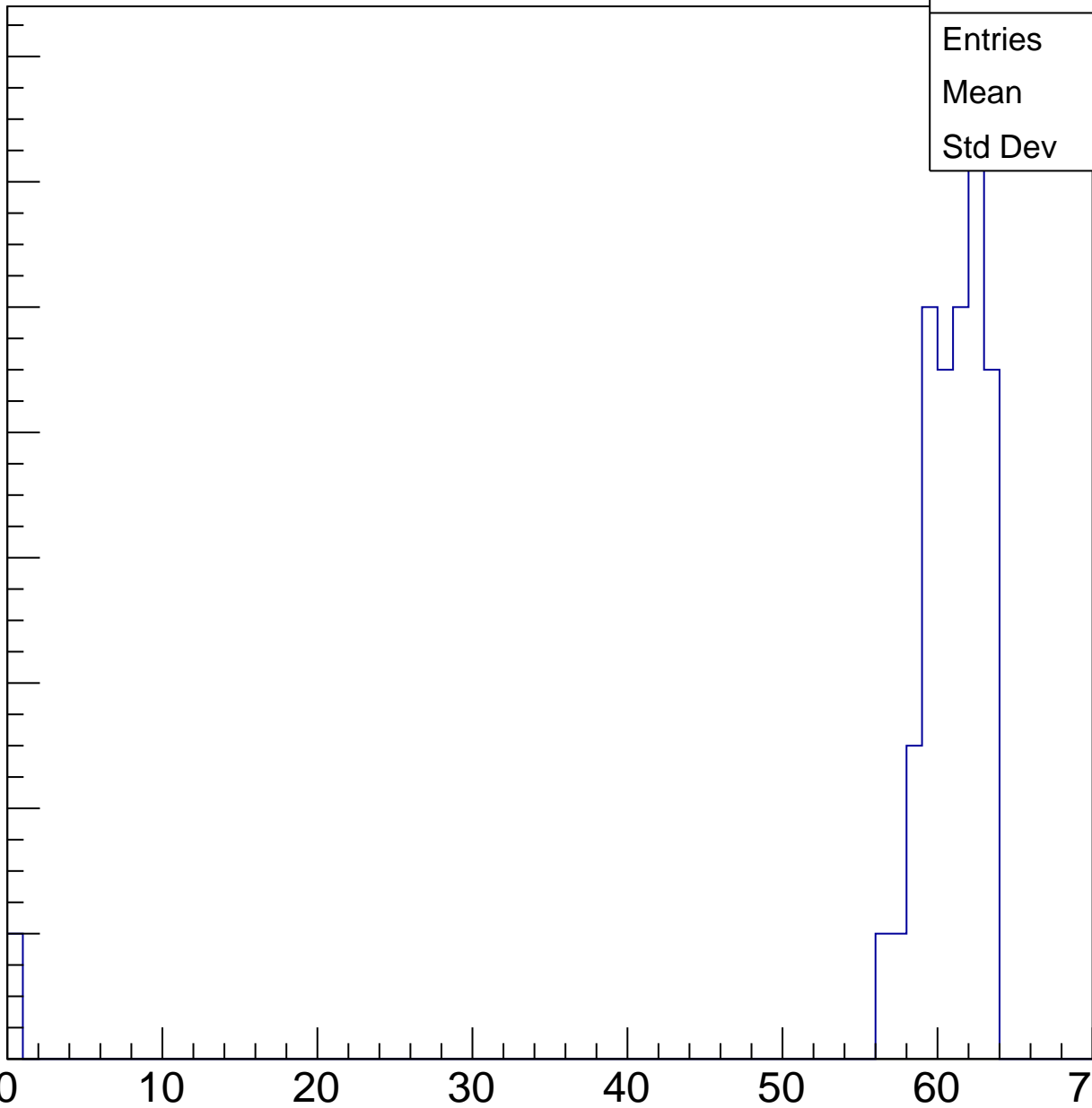
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	58.92
Std Dev	10.05

ampl

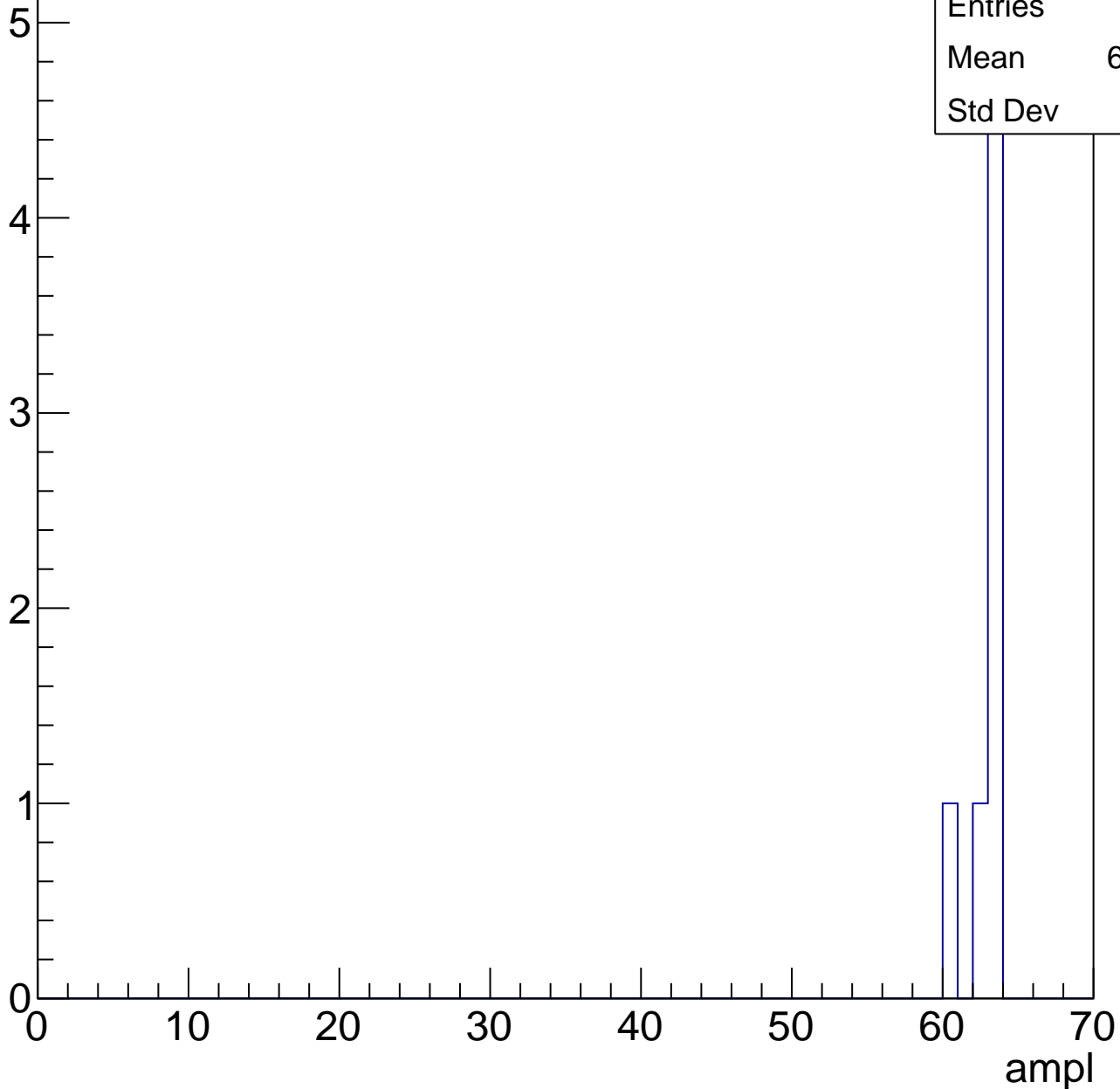


B0L101S, U21-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	7
Mean	62.43
Std Dev	1.05



B0L101S, U21-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch100, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	27.06
Std Dev	5.258

Gaus mean : 27.9956

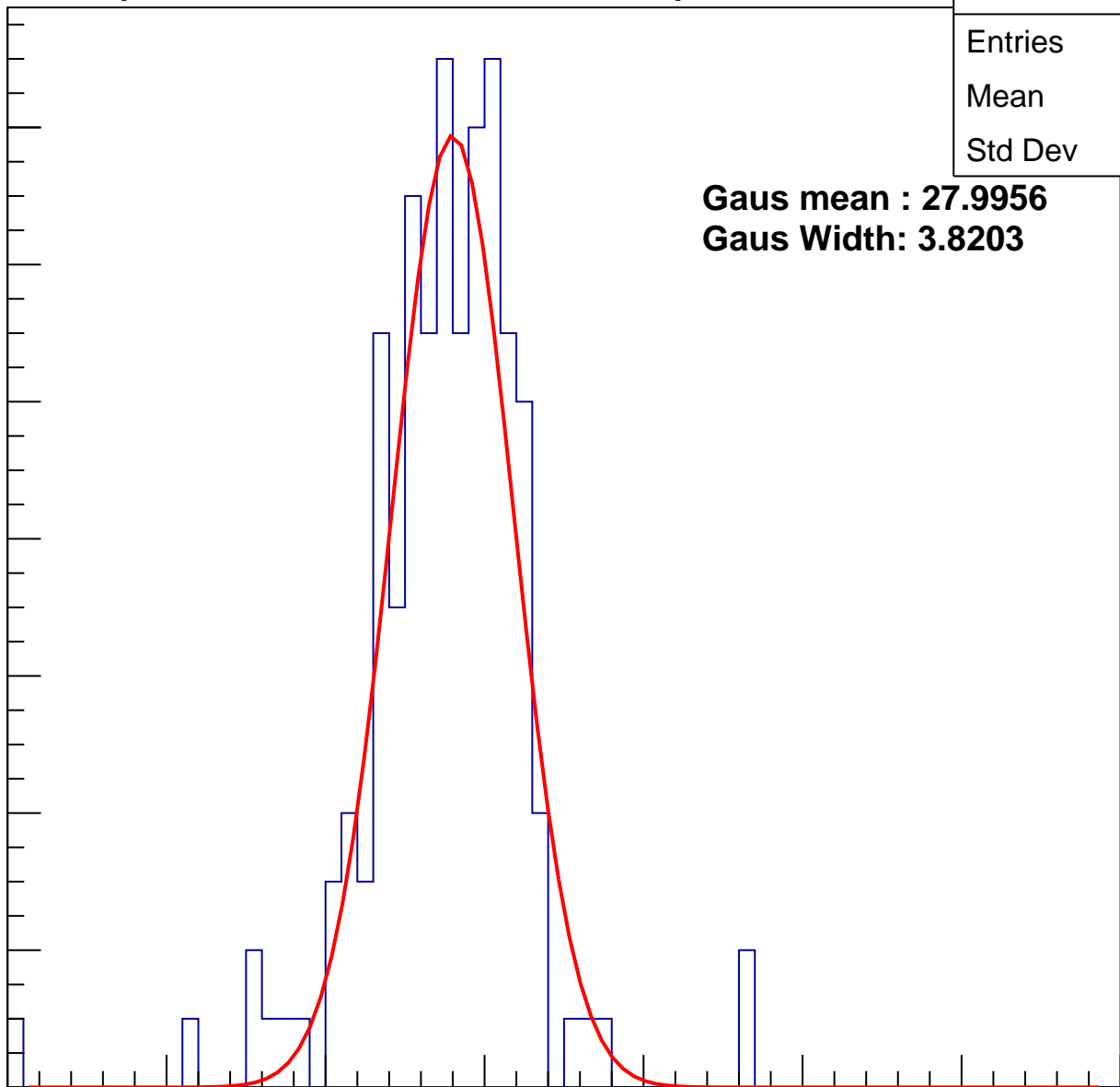
Gaus Width: 3.8203

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch100, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

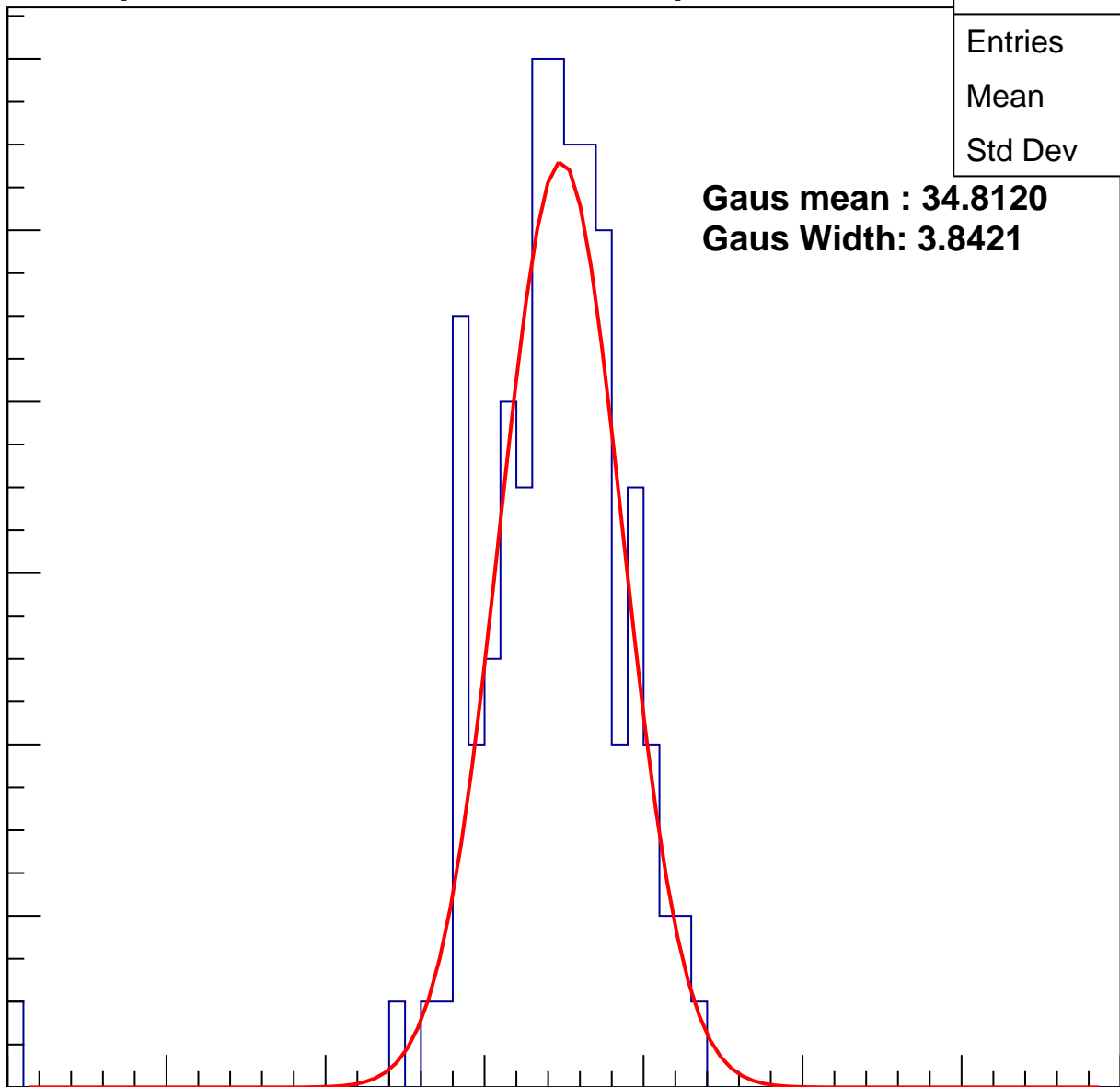
12
10
8
6
4
2
0

Entries	113
Mean	33.77
Std Dev	4.99

Gaus mean : 34.8120
Gaus Width: 3.8421

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch100, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

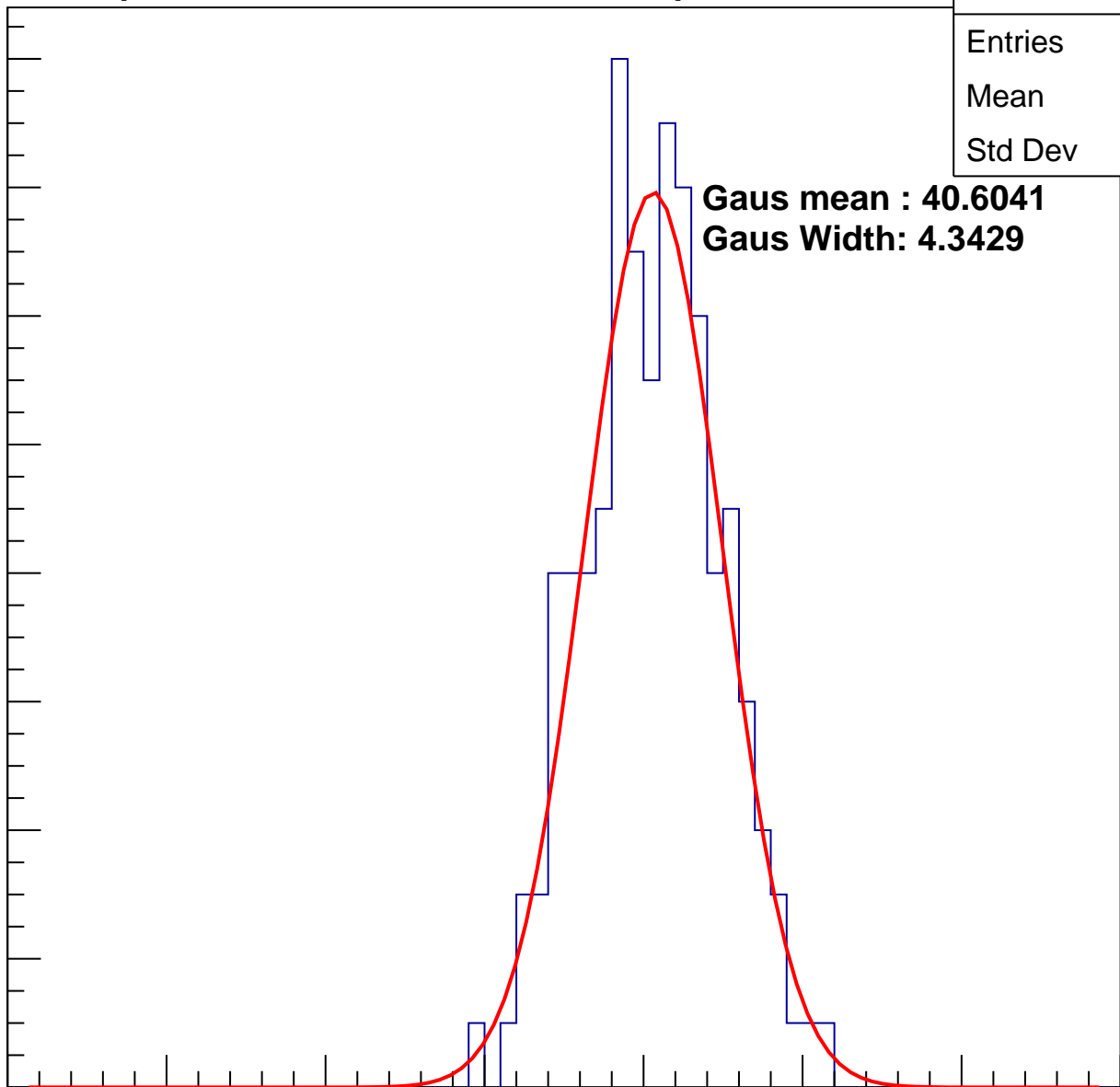
Entries	155
Mean	40.11
Std Dev	4.213

Gaus mean : 40.6041

Gaus Width: 4.3429

0 10 20 30 40 50 60 70

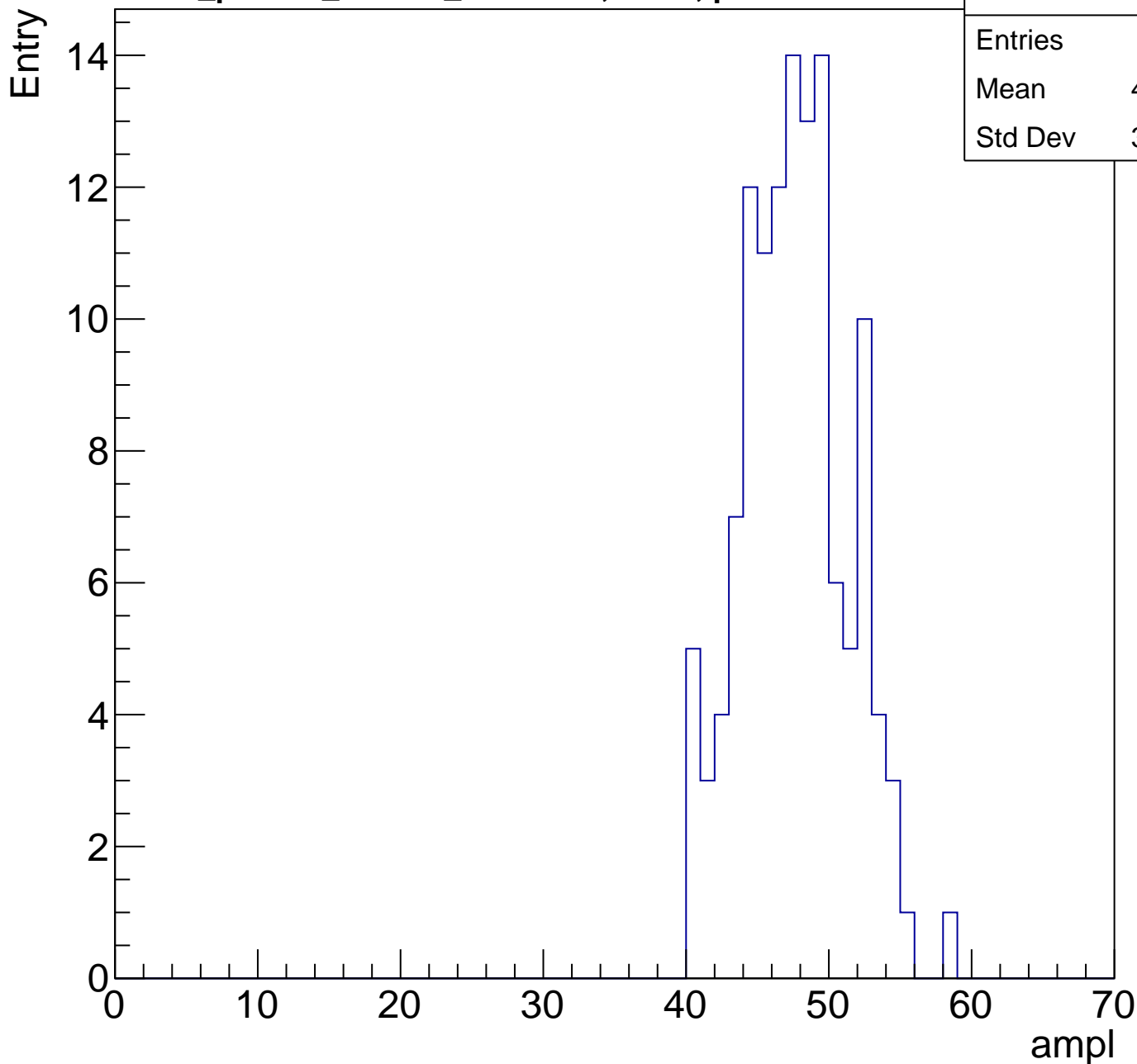
ampl



B0L101S, U21-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

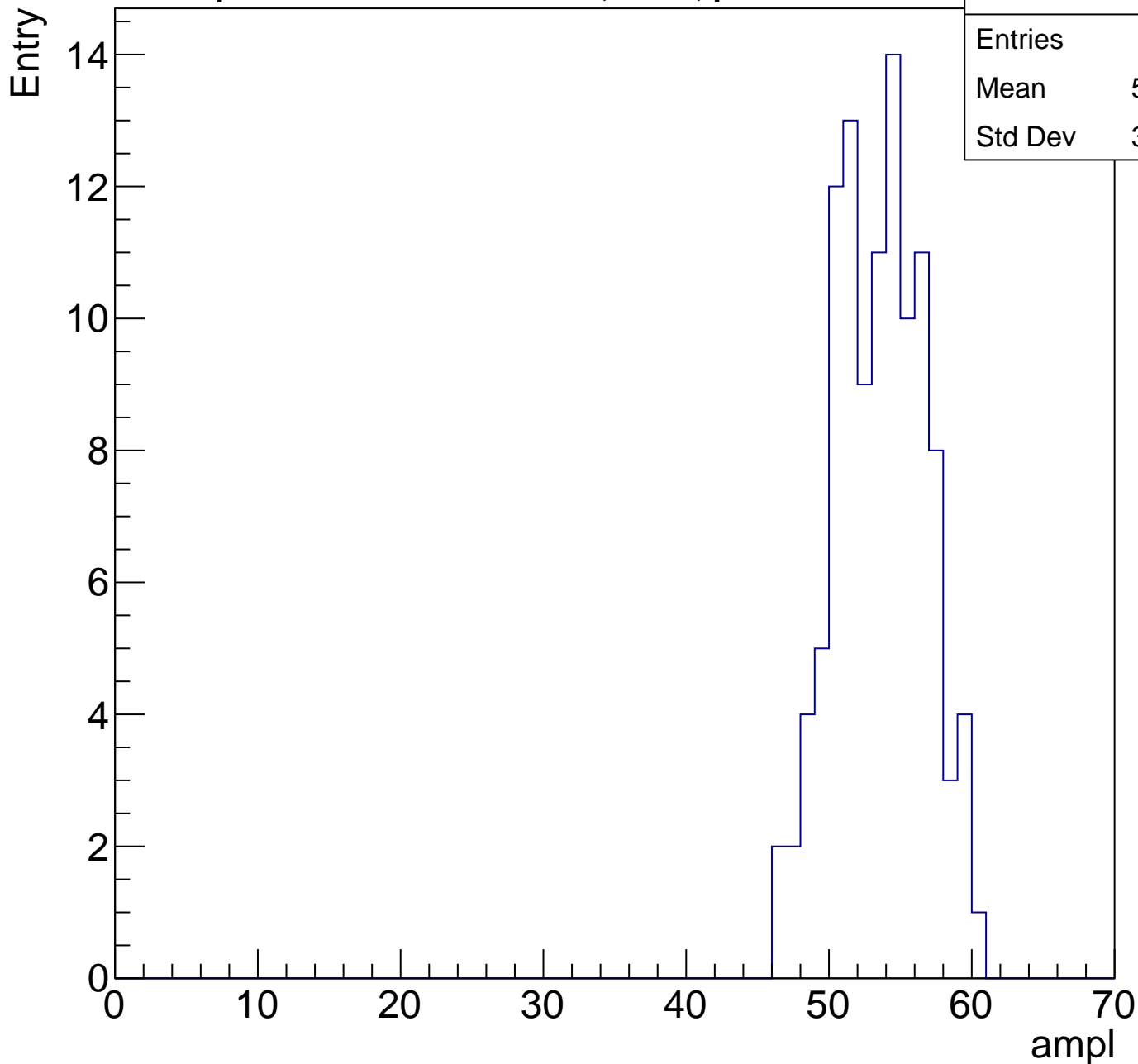
Entries	125
Mean	47.18
Std Dev	3.638



B0L101S, U21-ch100, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	53.07
Std Dev	3.129



B0L101S, U21-ch100, adc5

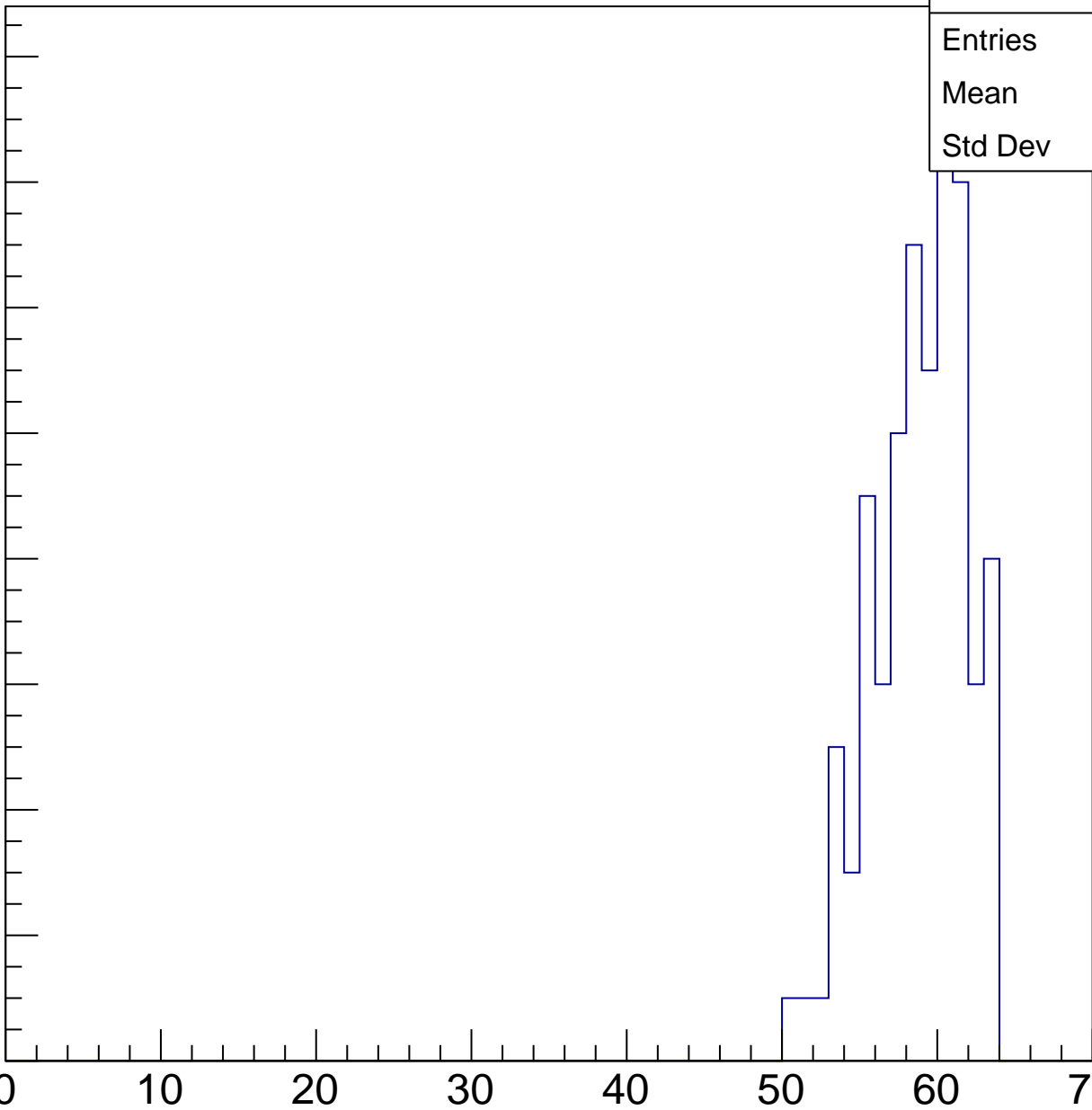
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.4
Std Dev	2.969

ampl



B0L101S, U21-ch100, adc6

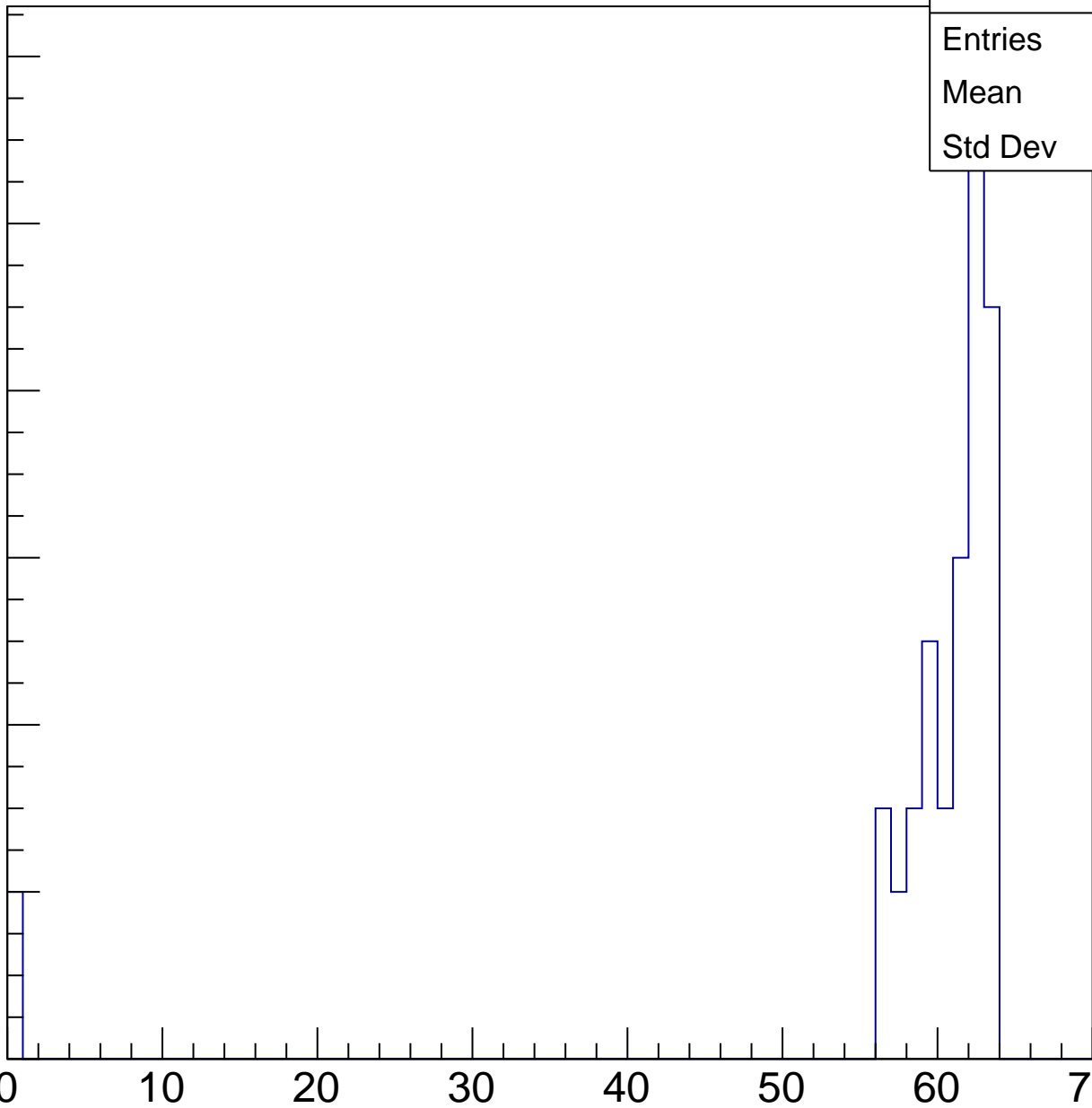
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	45
Mean	57.96
Std Dev	12.68

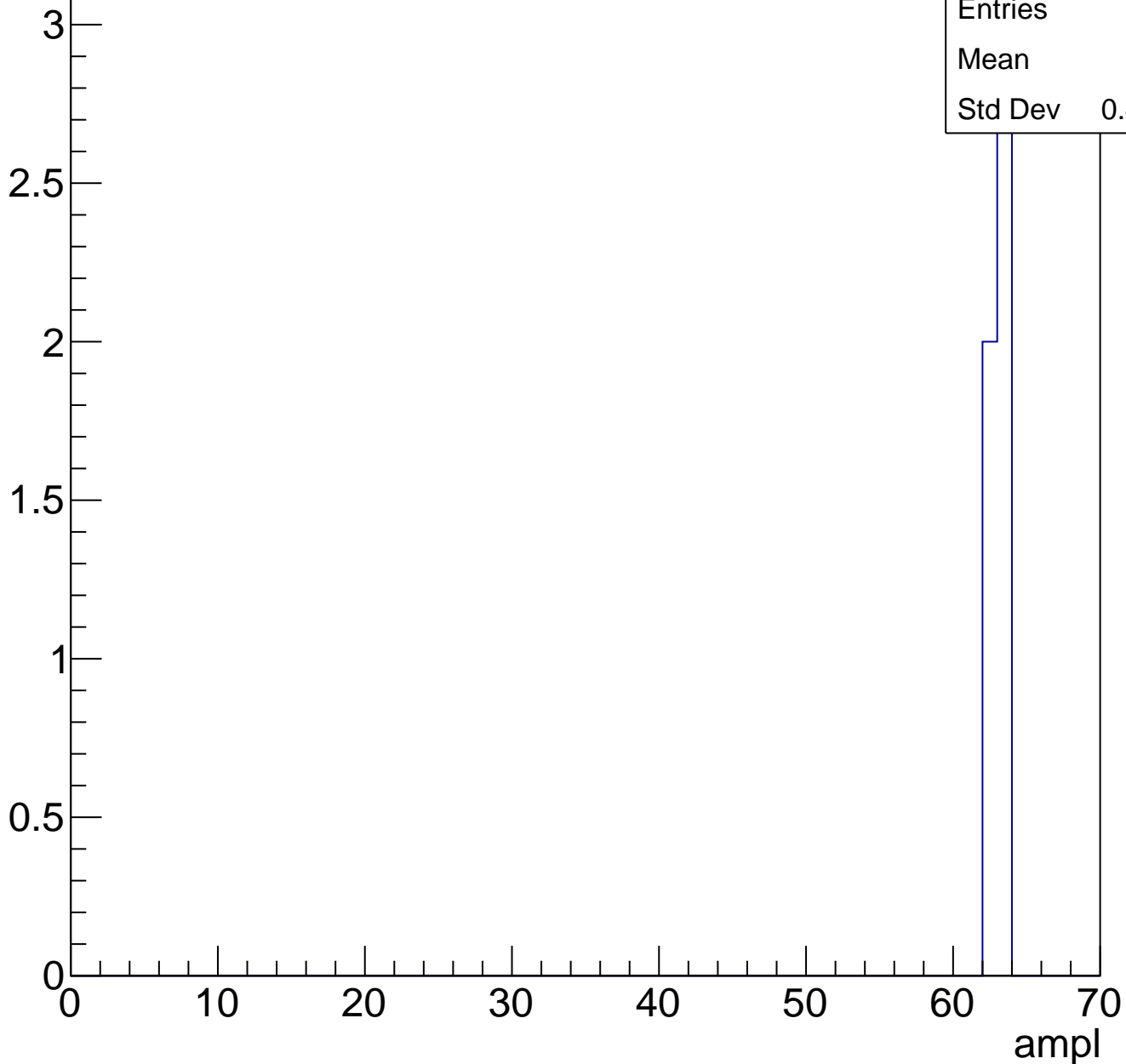
ampl



B0L101S, U21-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch101, adc0

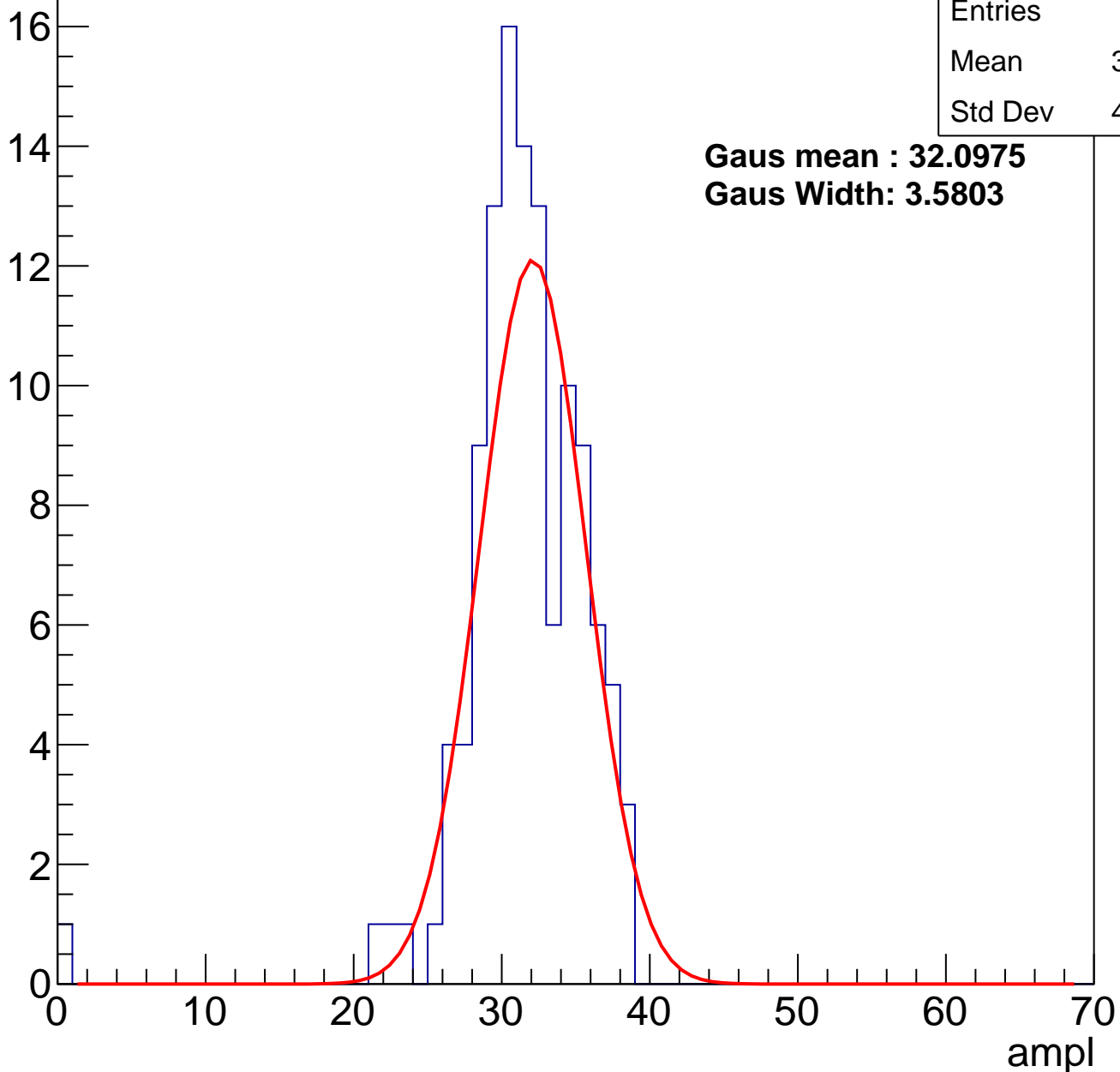
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	31.03
Std Dev	4.437

Gaus mean : 32.0975

Gaus Width: 3.5803

Entry



B0L101S, U21-ch101, adc1

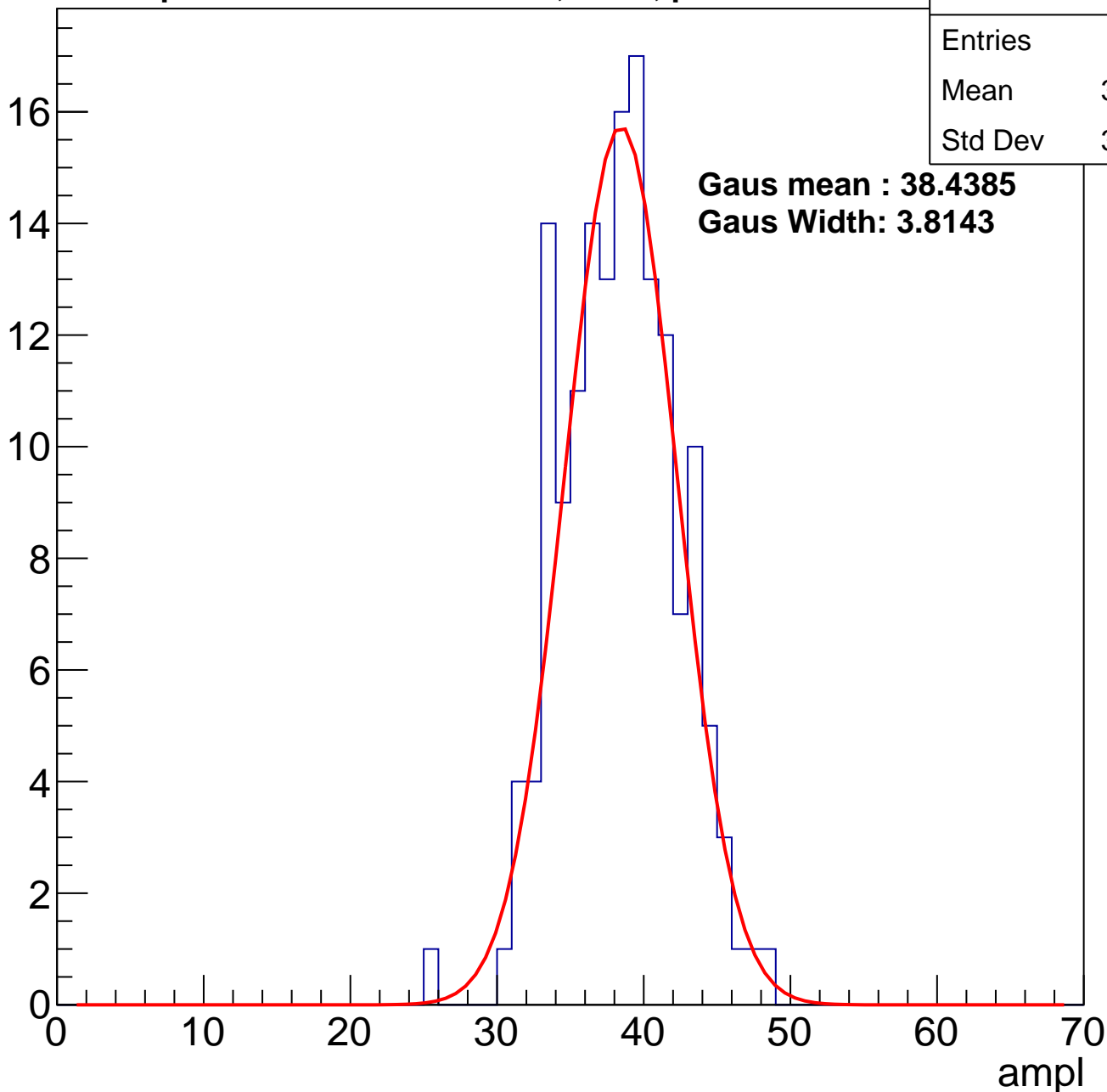
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	37.89
Std Dev	3.864

Gaus mean : 38.4385

Gaus Width: 3.8143

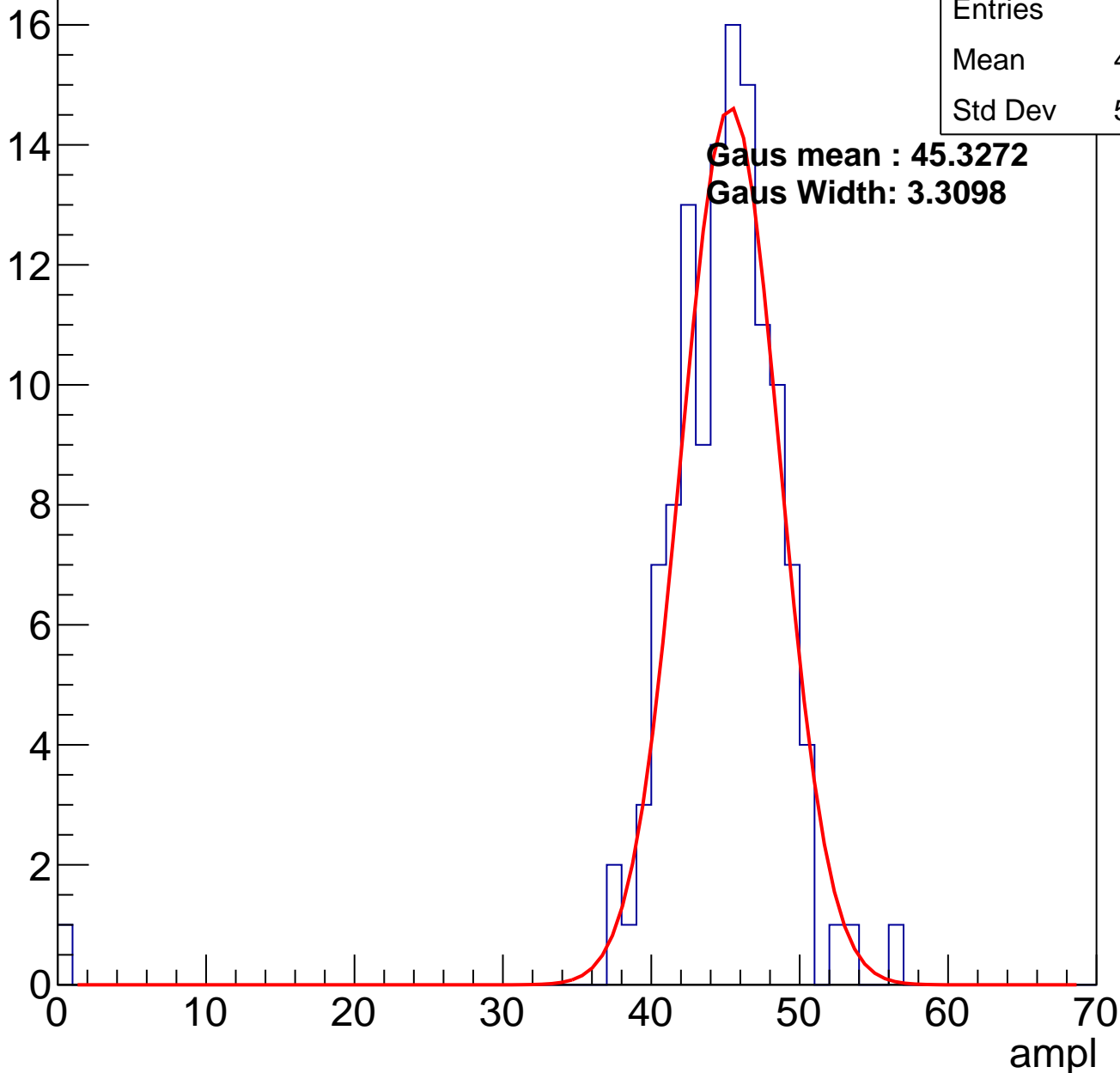
Entry



B0L101S, U21-ch101, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

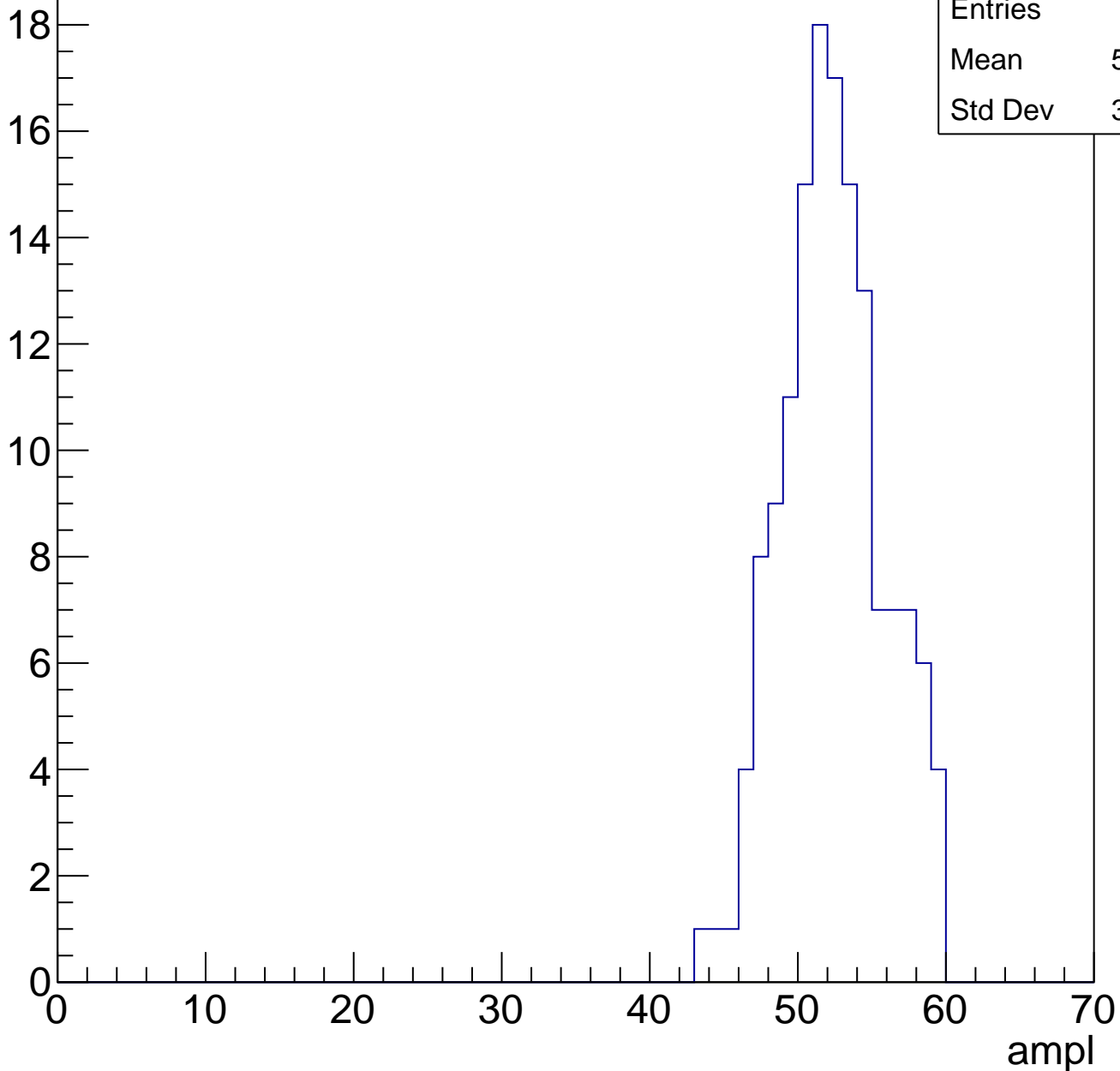


B0L101S, U21-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

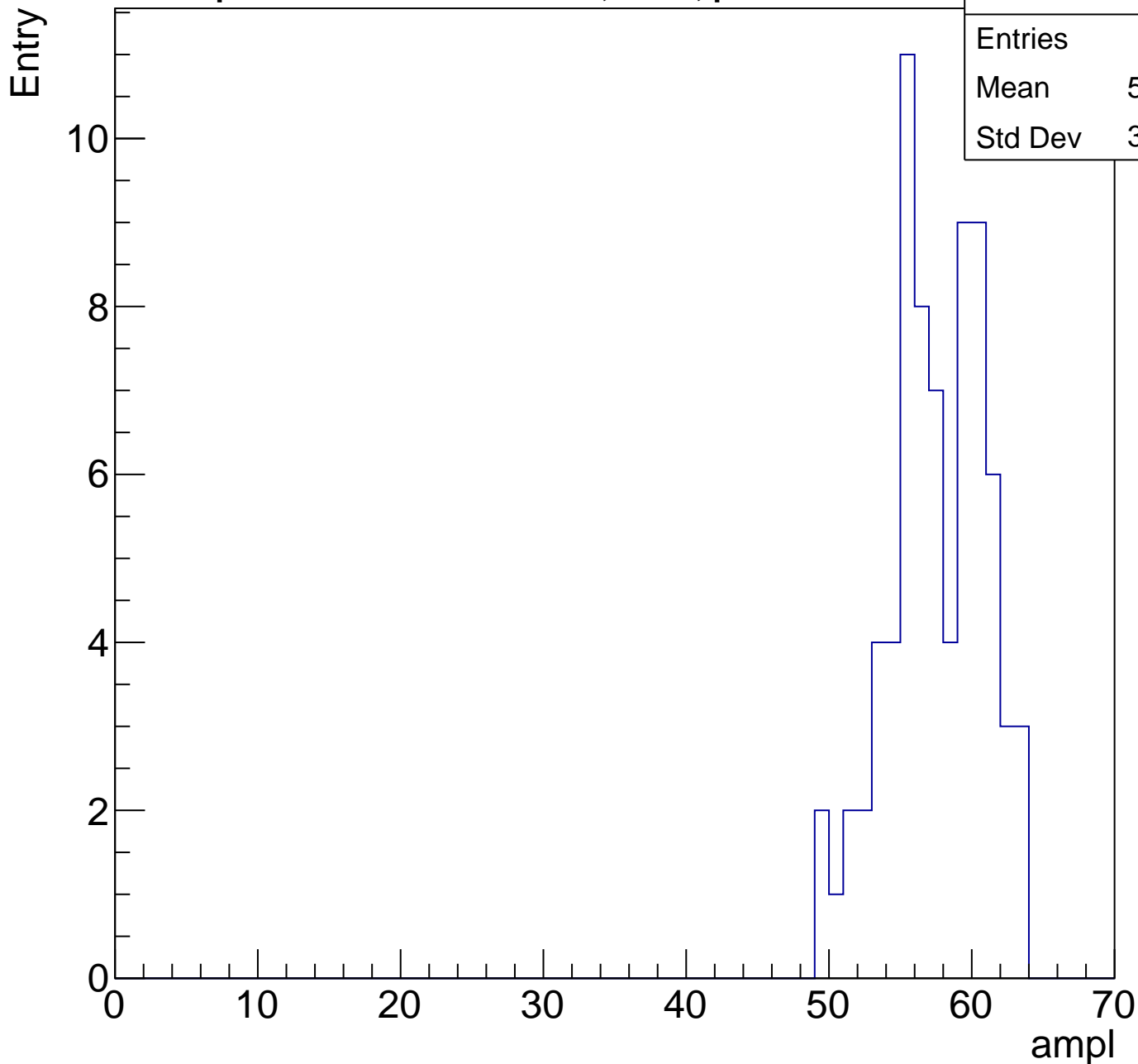
Entries	144
Mean	51.89
Std Dev	3.422



B0L101S, U21-ch101, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	75
Mean	57.04
Std Dev	3.368



B0L101S, U21-ch101, adc5

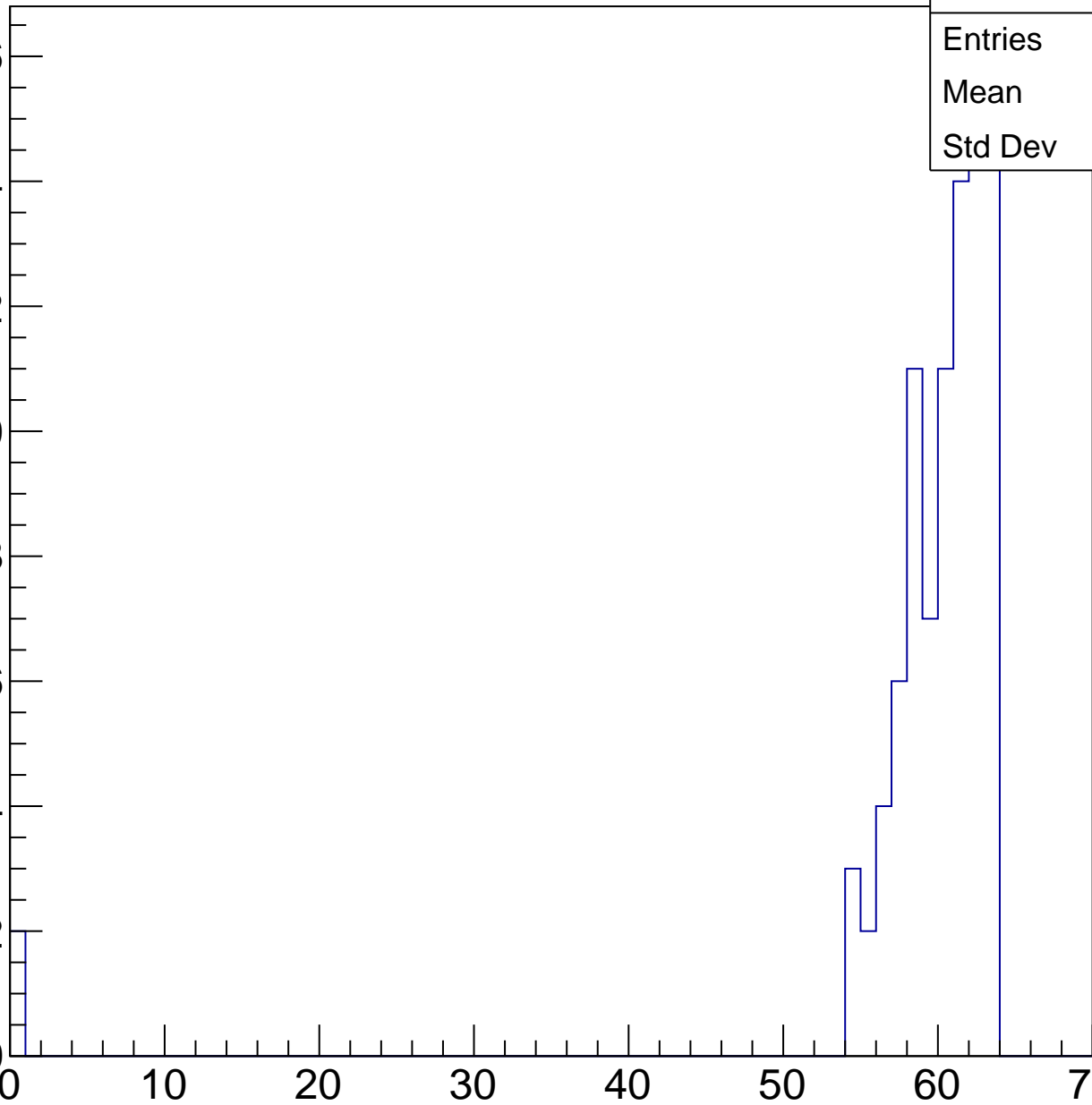
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.68
Std Dev	9.128

ampl



B0L101S, U21-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U21-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch102, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	27.78
Std Dev	6.46

Gaus mean : 27.9973

Gaus Width: 3.2631

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

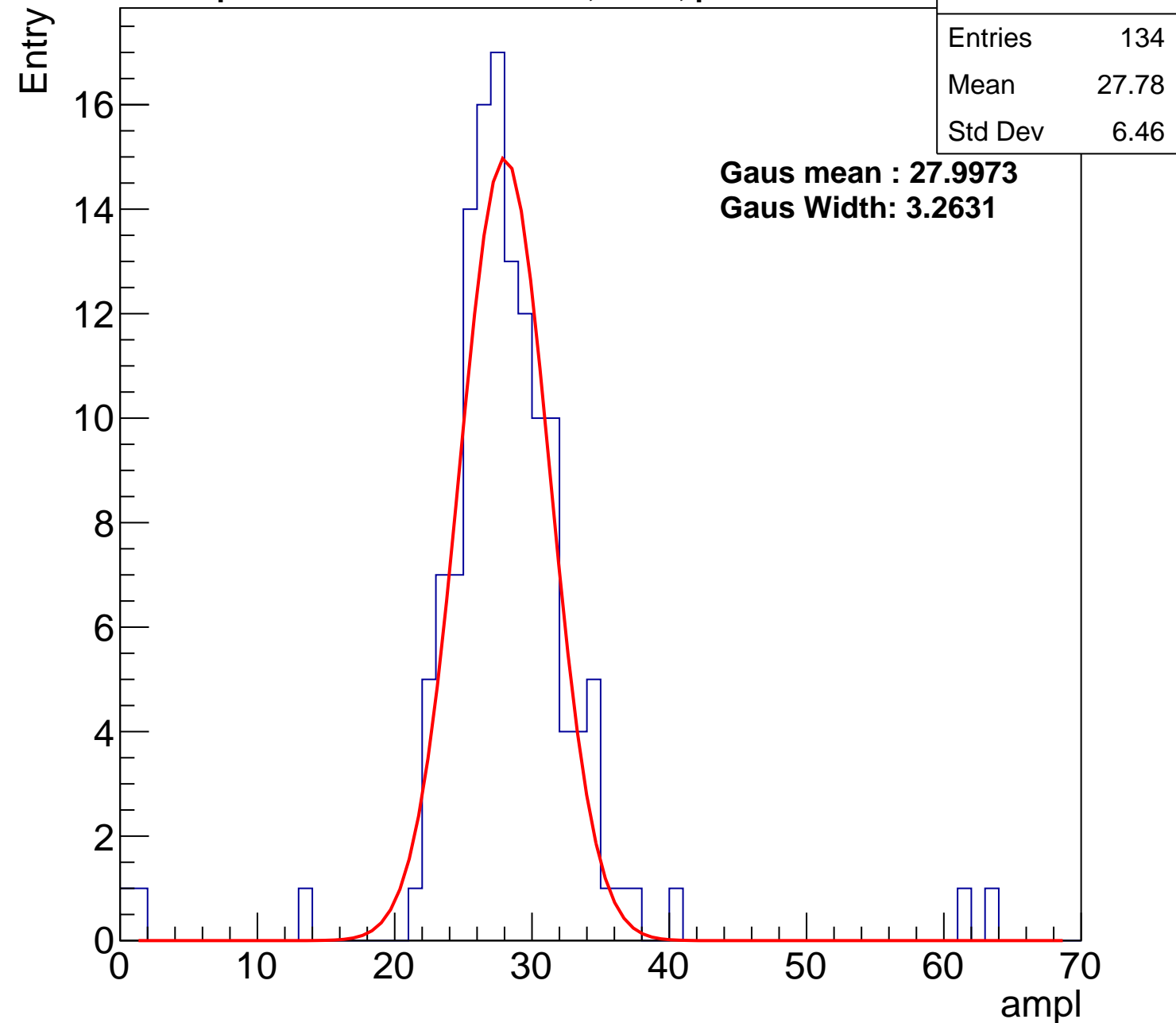
40

50

60

70

ampl



B0L101S, U21-ch102, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

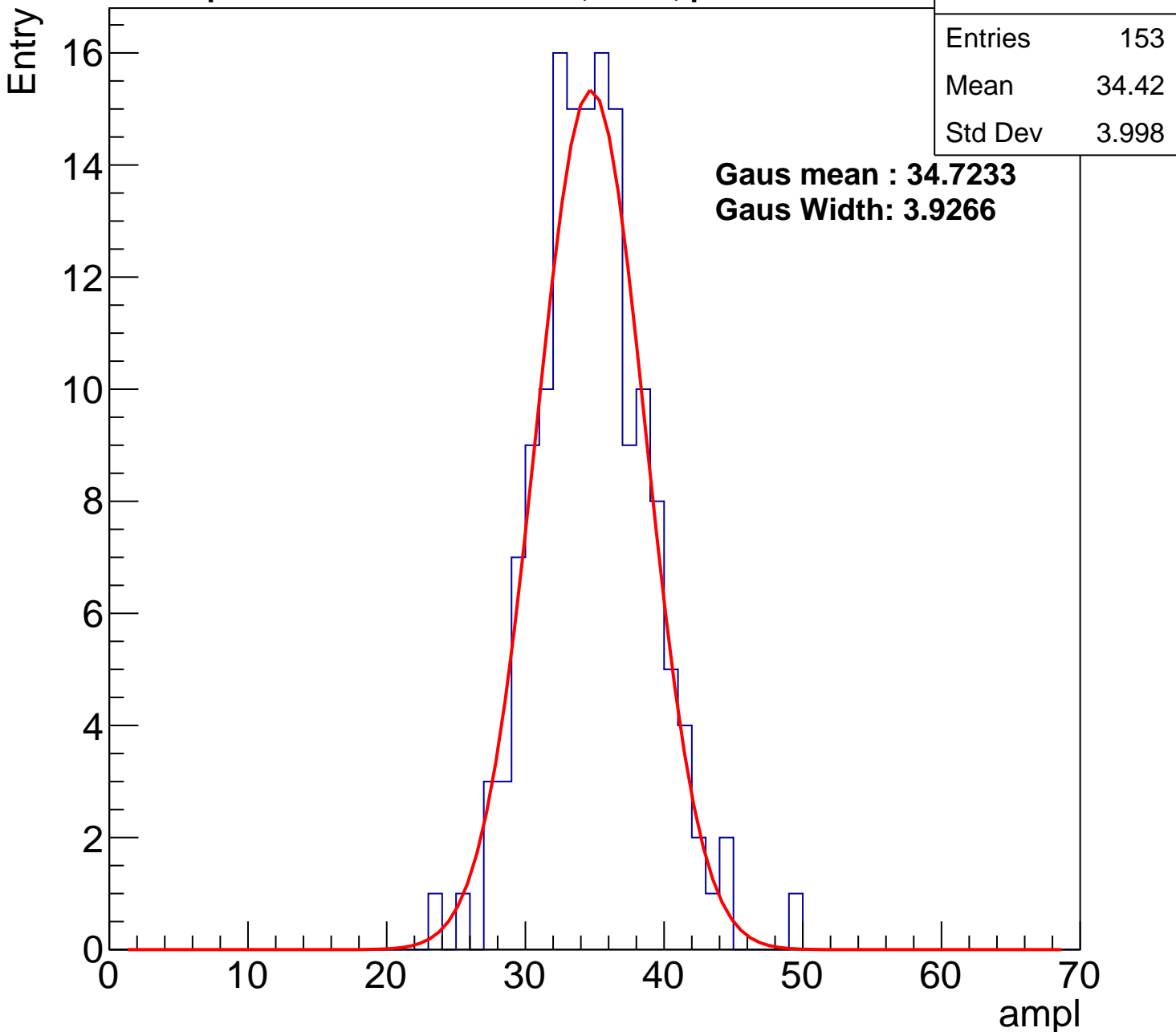
16
14
12
10
8
6
4
2
0

Entries	153
Mean	34.42
Std Dev	3.998

Gaus mean : 34.7233
Gaus Width: 3.9266

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

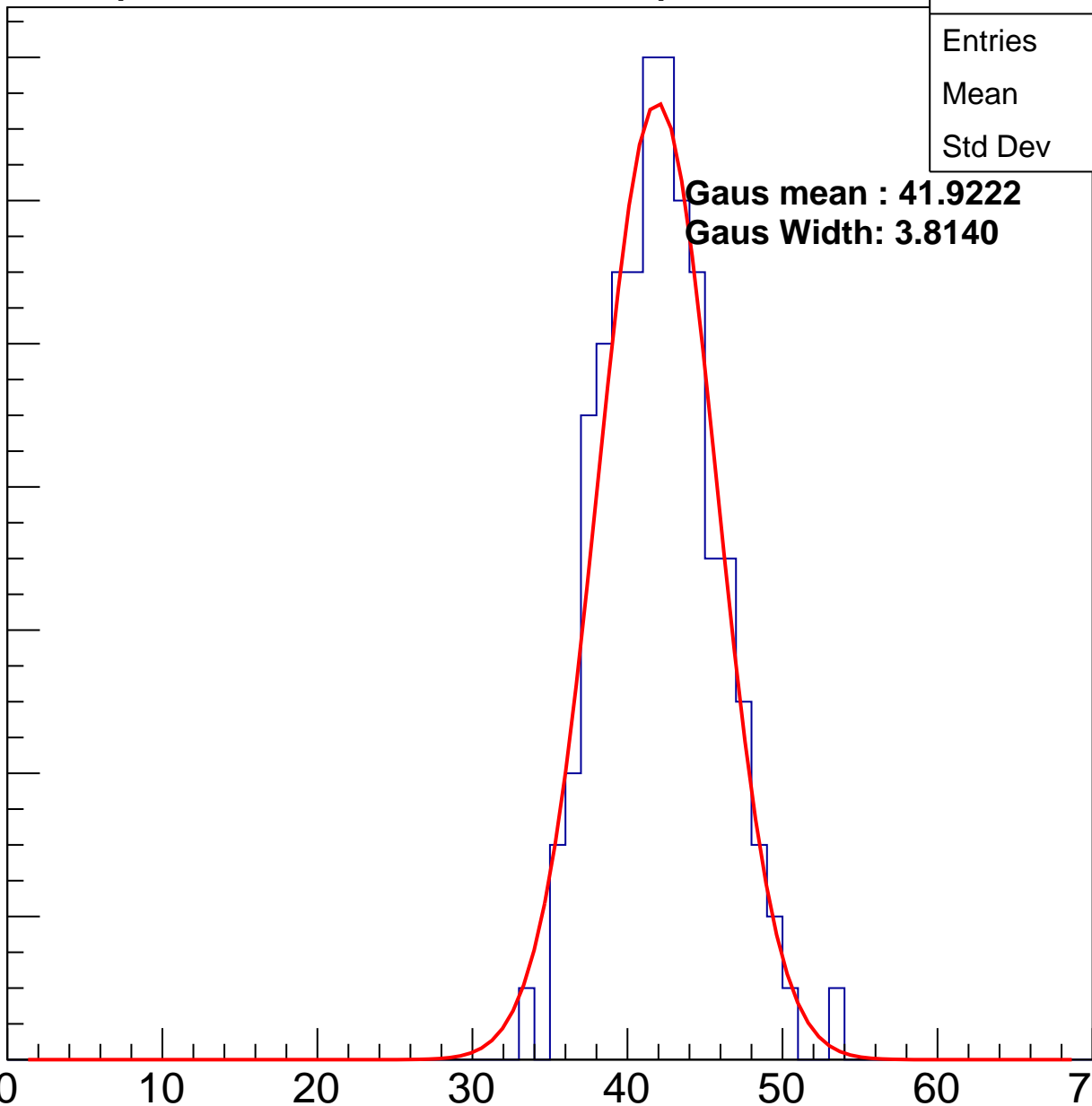
14
12
10
8
6
4
2
0

Entries	126
Mean	41.61
Std Dev	3.61

Gaus mean : 41.9222

Gaus Width: 3.8140

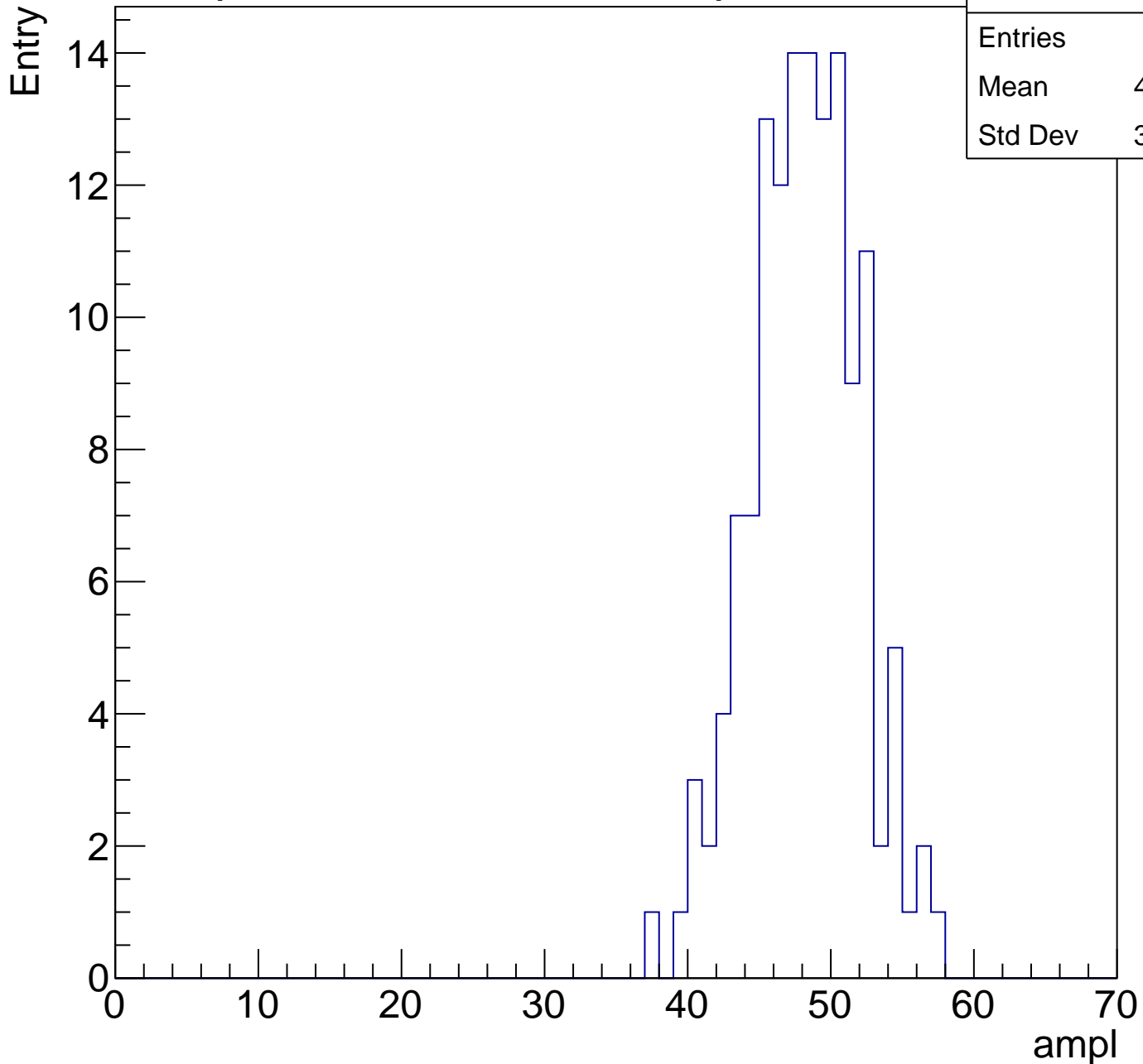
ampl



B0L101S, U21-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	47.72
Std Dev	3.743



B0L101S, U21-ch102, adc4

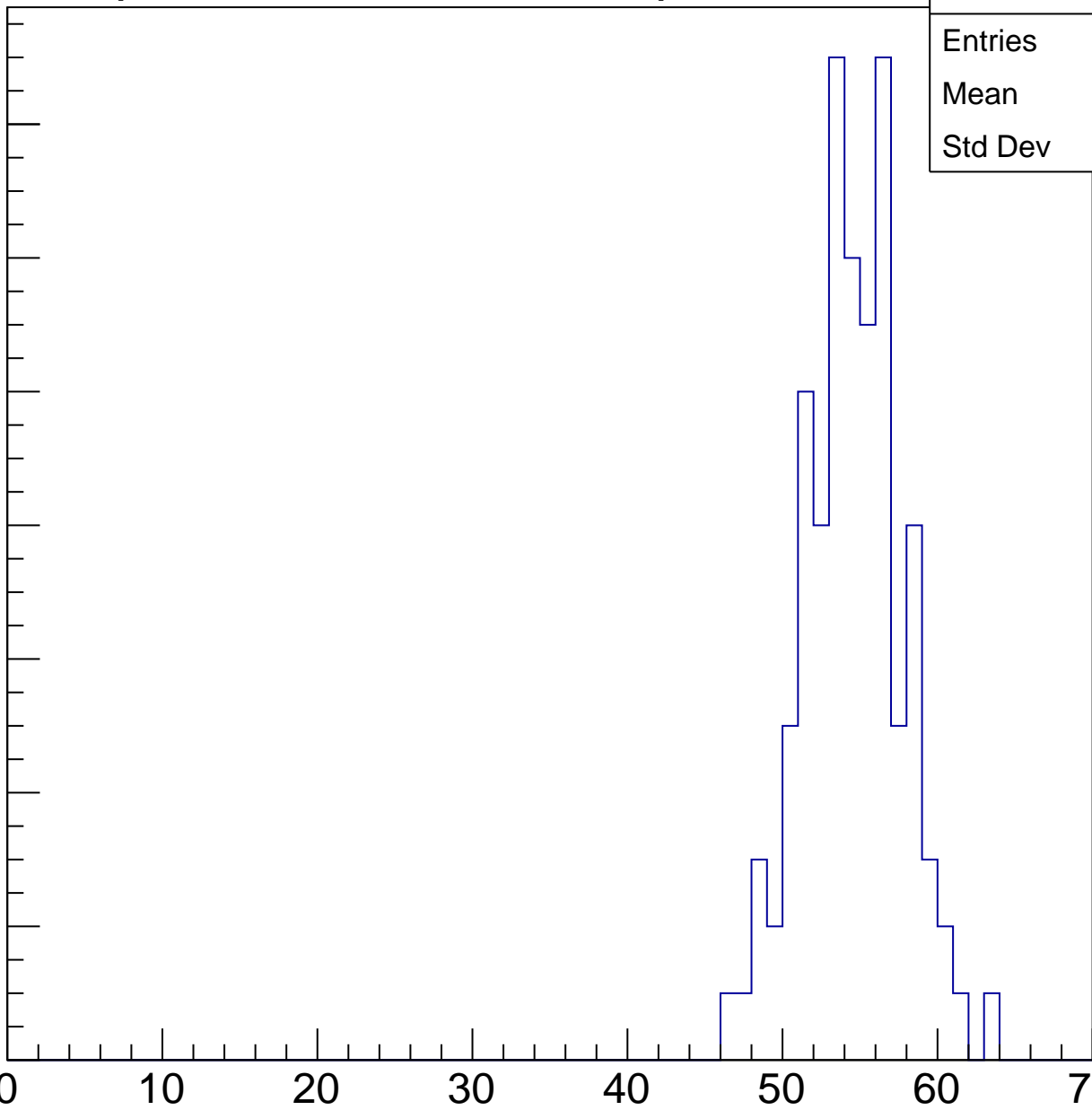
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	103
Mean	54.07
Std Dev	3.129

ampl



B0L101S, U21-ch102, adc5

calib_packv5_042523_0143.root, FC#1, port C1

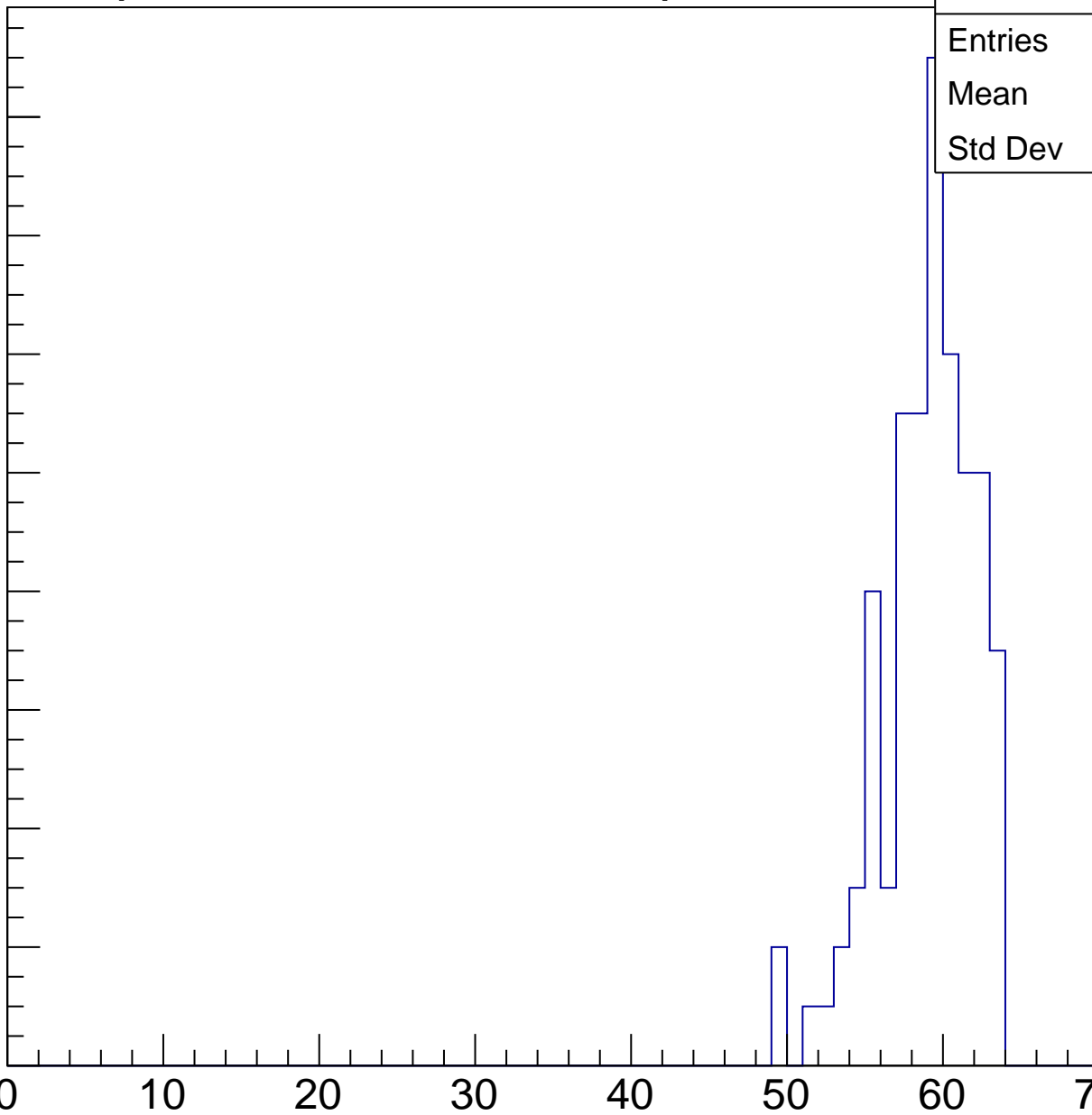
Entries	98
Mean	58.53
Std Dev	3.035

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

Entries	40
Mean	58.2
Std Dev	13.46

ampl

0

10

20

30

40

50

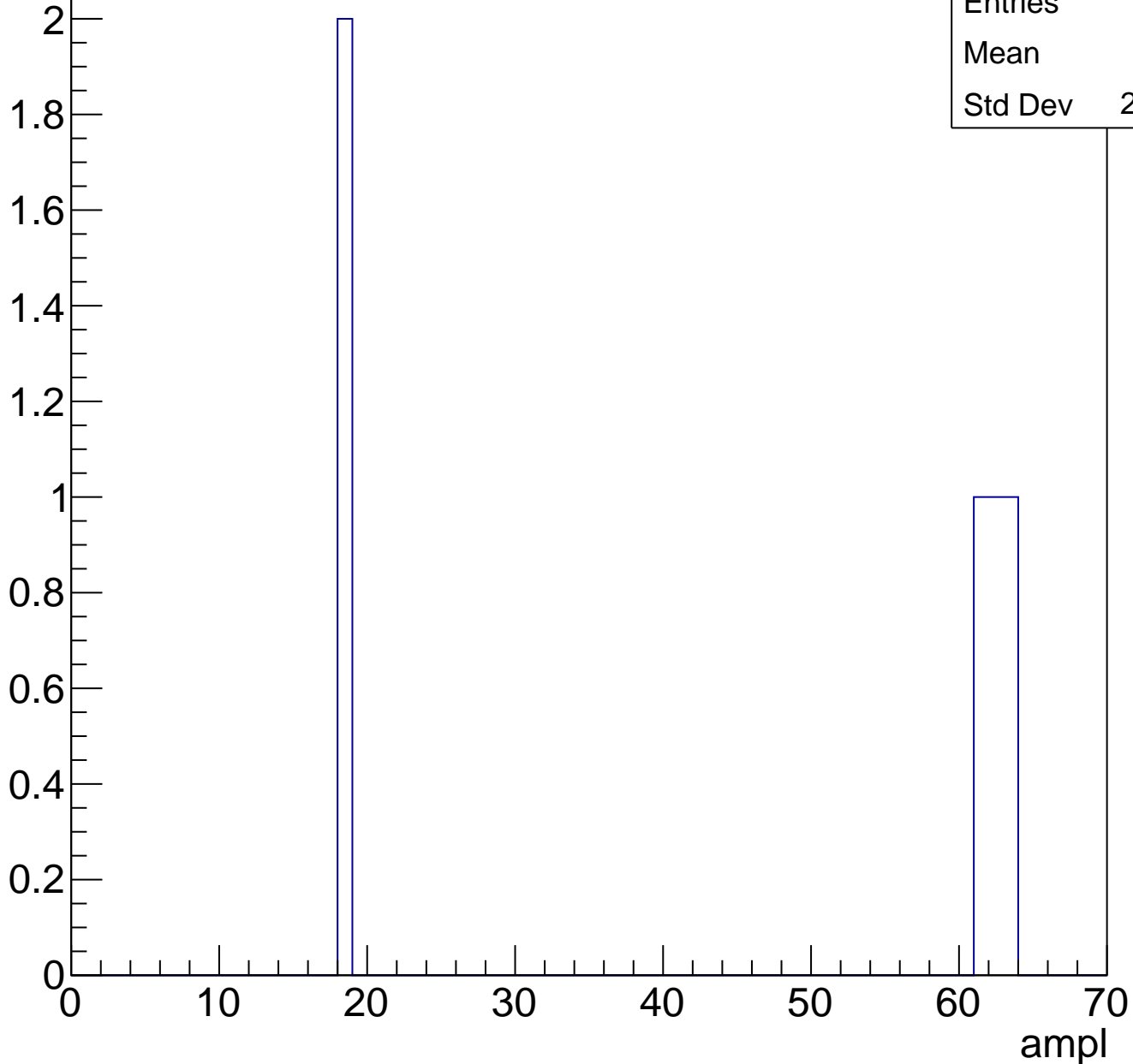
60

70

B0L101S, U21-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch103, adc0

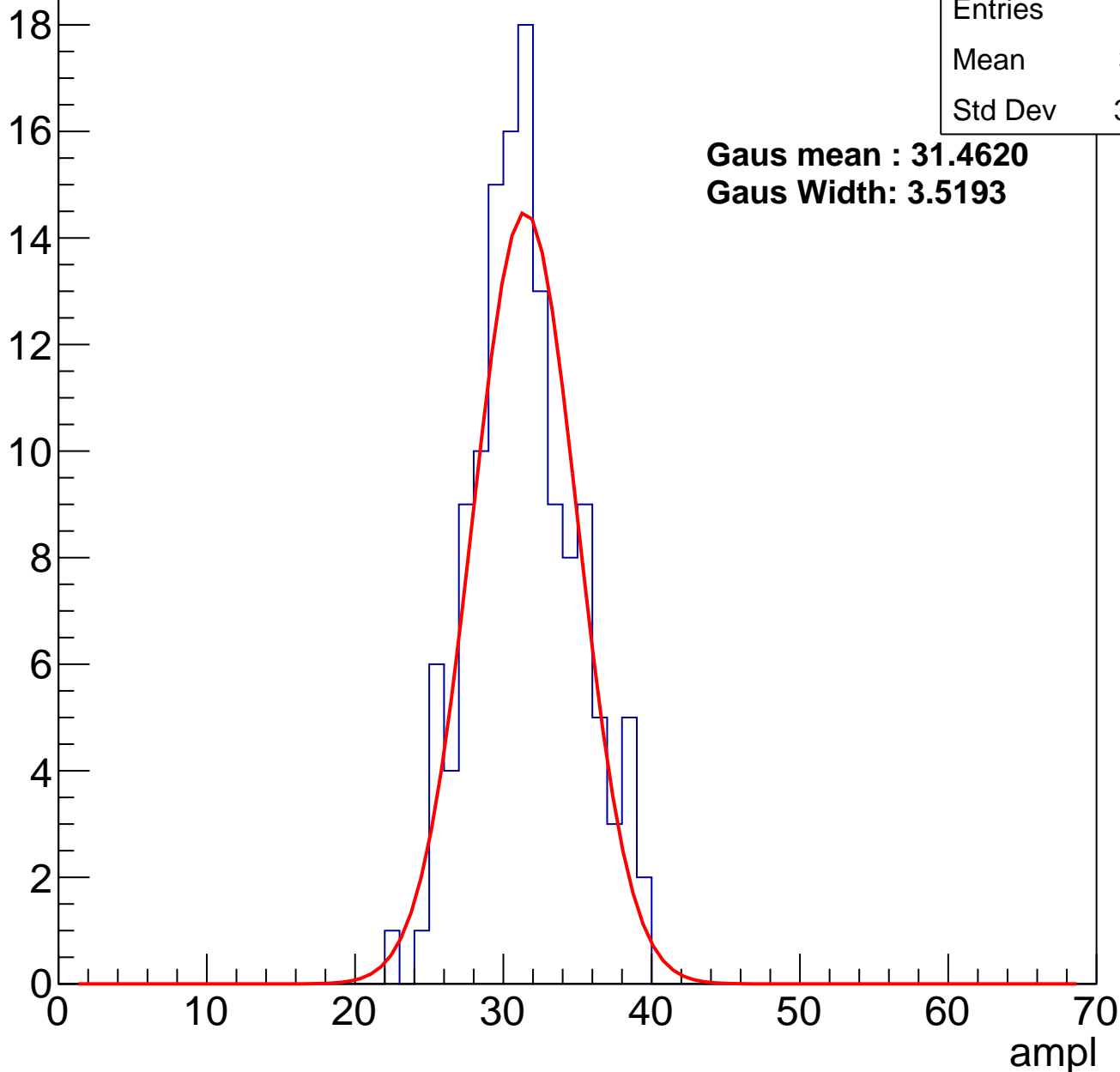
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	31.01
Std Dev	3.484

Gaus mean : 31.4620

Gaus Width: 3.5193

Entry



B0L101S, U21-ch103, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	37.94
Std Dev	4.821

Gaus mean : 38.6193

Gaus Width: 4.0096

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

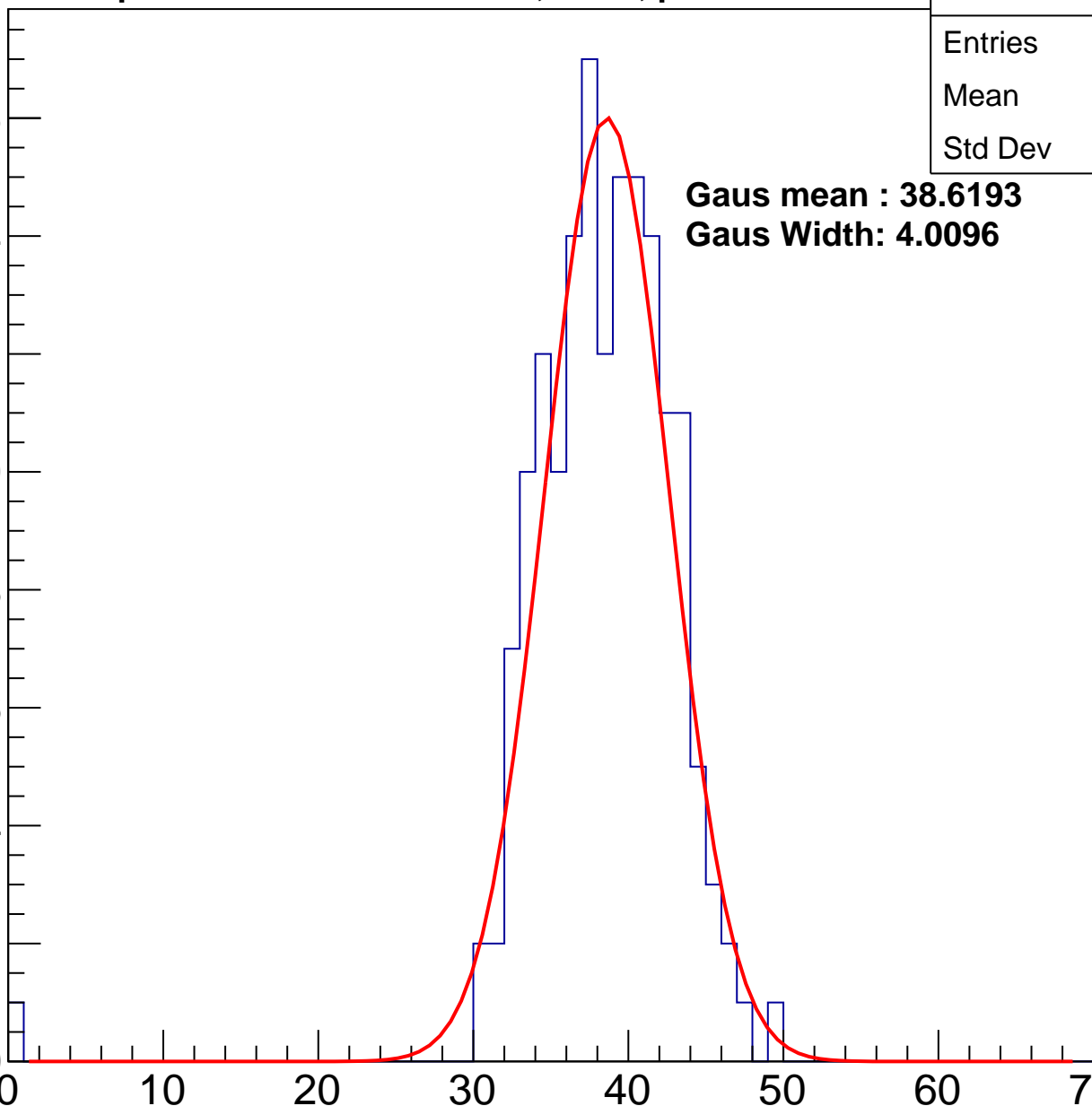
40

50

60

70

ampl



B0L101S, U21-ch103, adc2

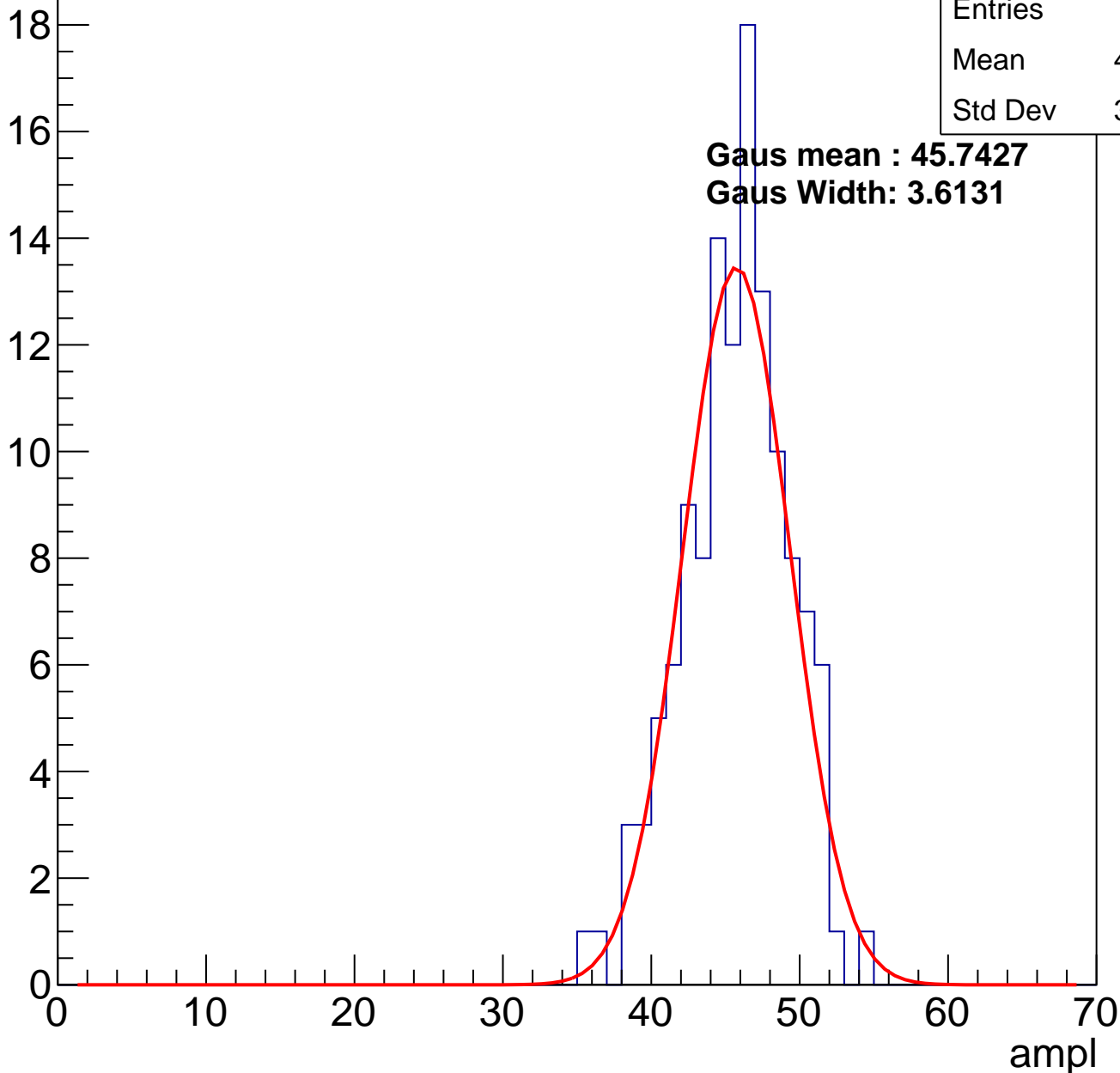
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	126
Mean	45.23
Std Dev	3.548

Gaus mean : 45.7427

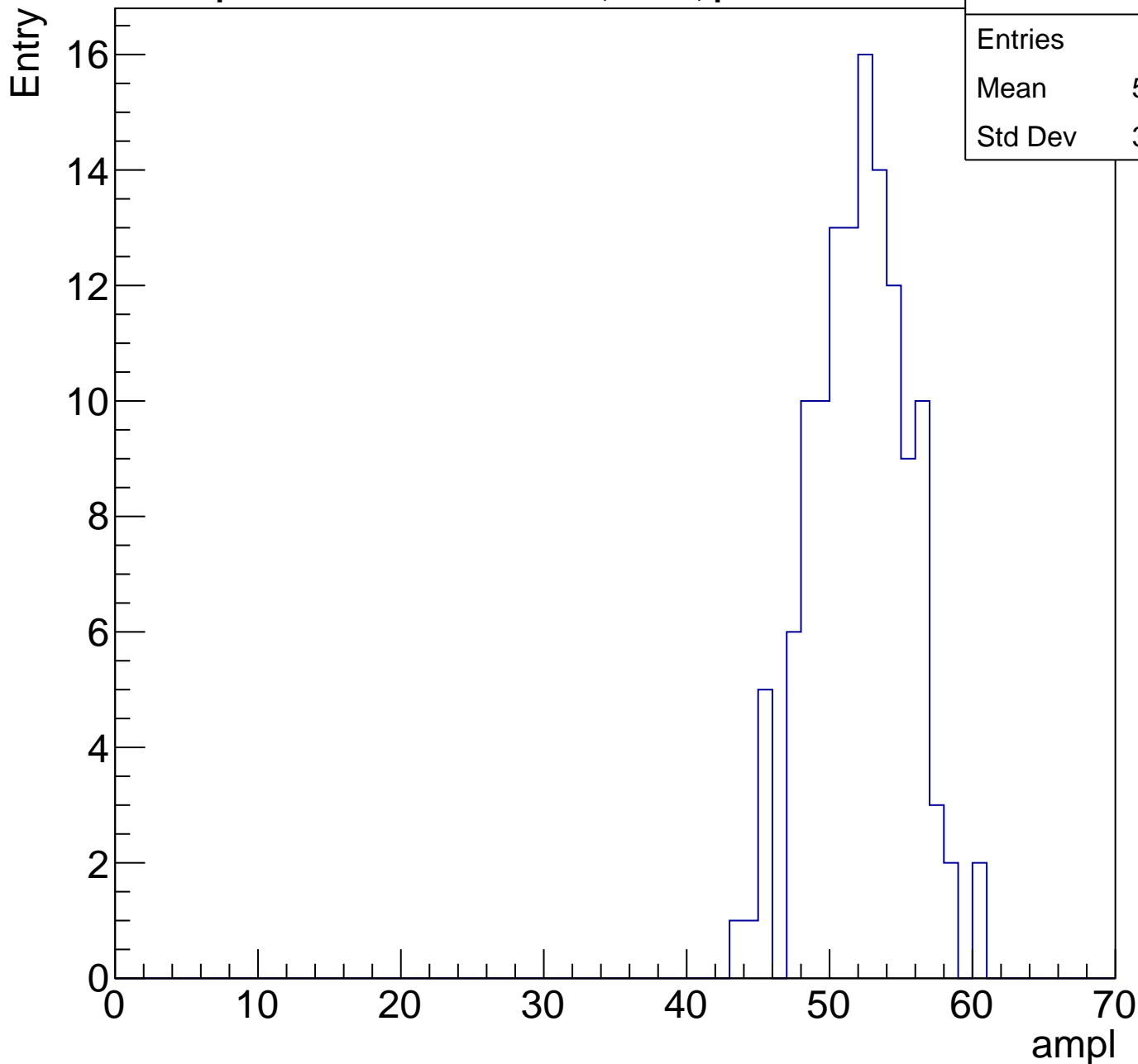
Gaus Width: 3.6131



B0L101S, U21-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	51.66
Std Dev	3.342



B0L101S, U21-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	57.57
Std Dev	5.959

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

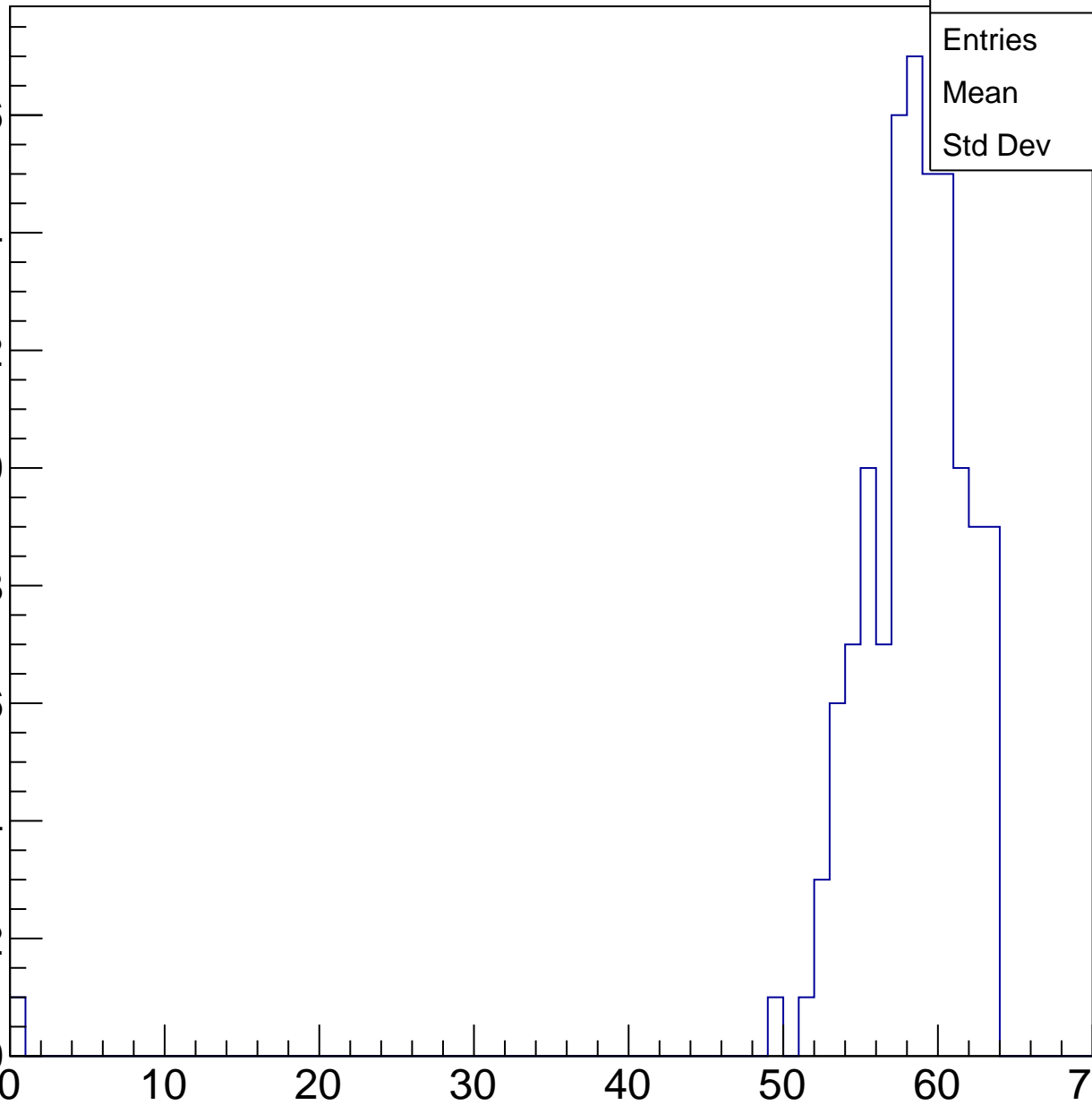
40

50

60

70

ampl

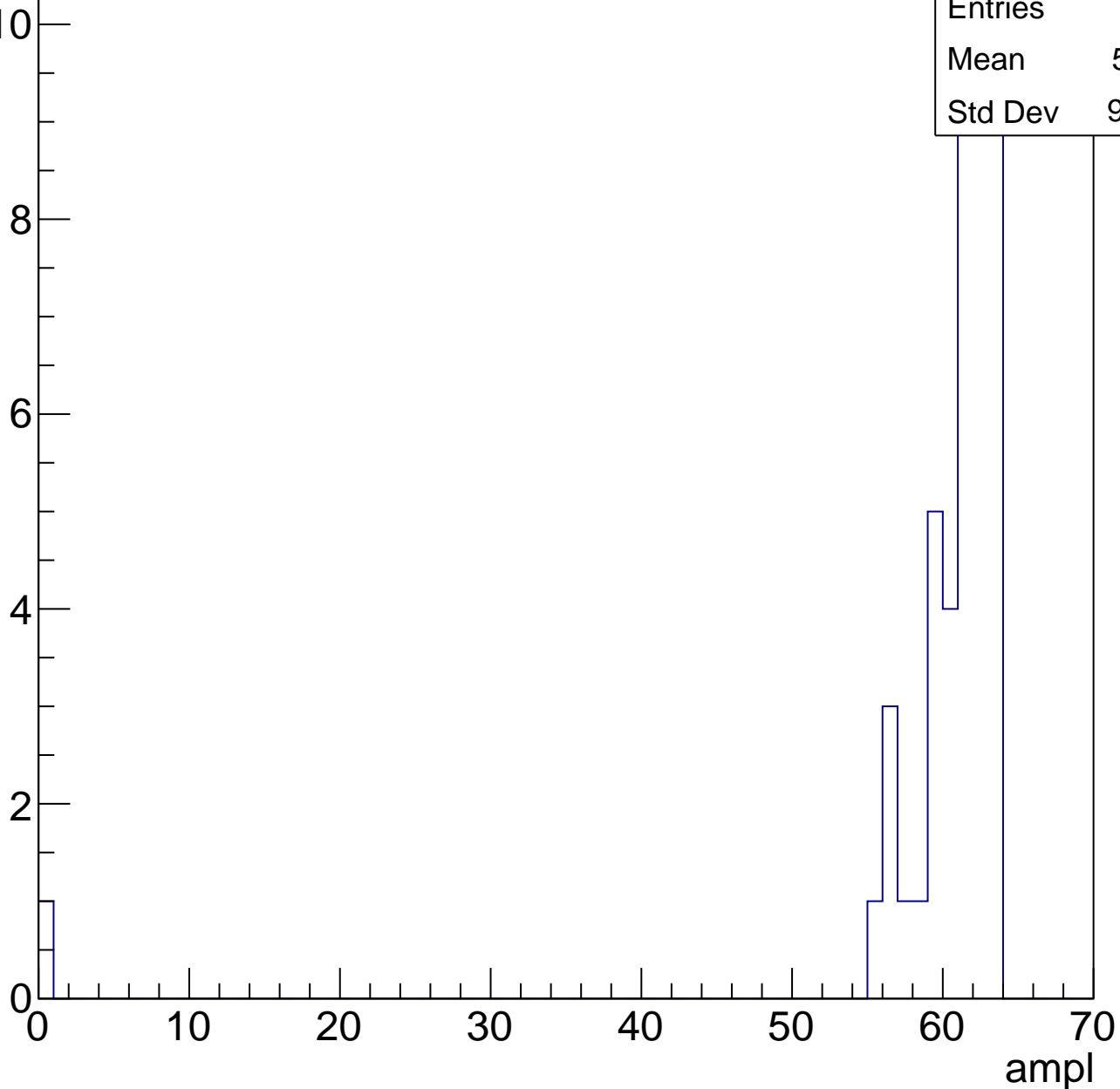


B0L101S, U21-ch103, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	46
Mean	59.41
Std Dev	9.107



B0L101S, U21-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch104, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	27.94
Std Dev	5.258

Gaus mean : 28.8905

Gaus Width: 4.1260

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

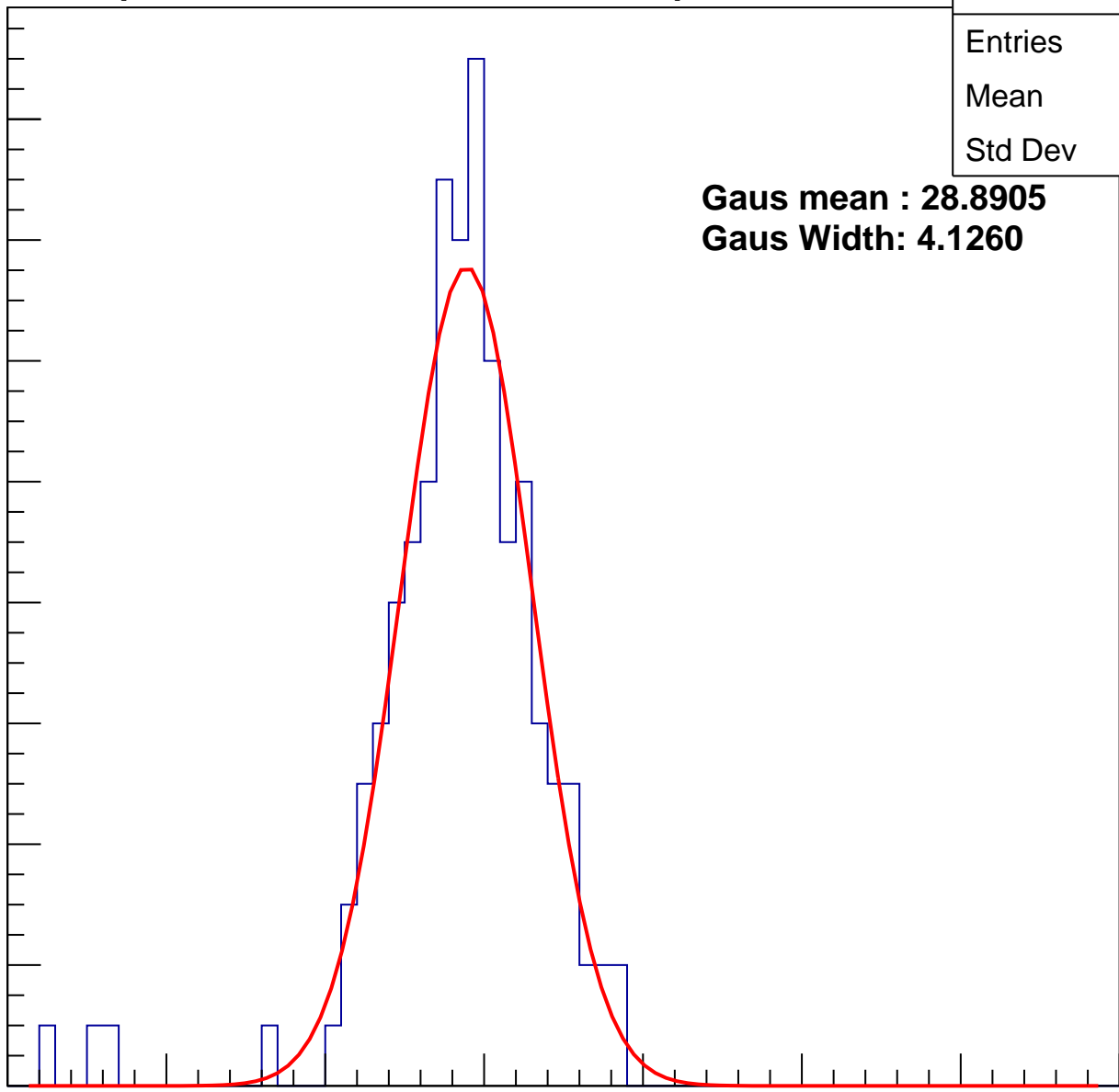
40

50

60

70

ampl



B0L101S, U21-ch104, adc1

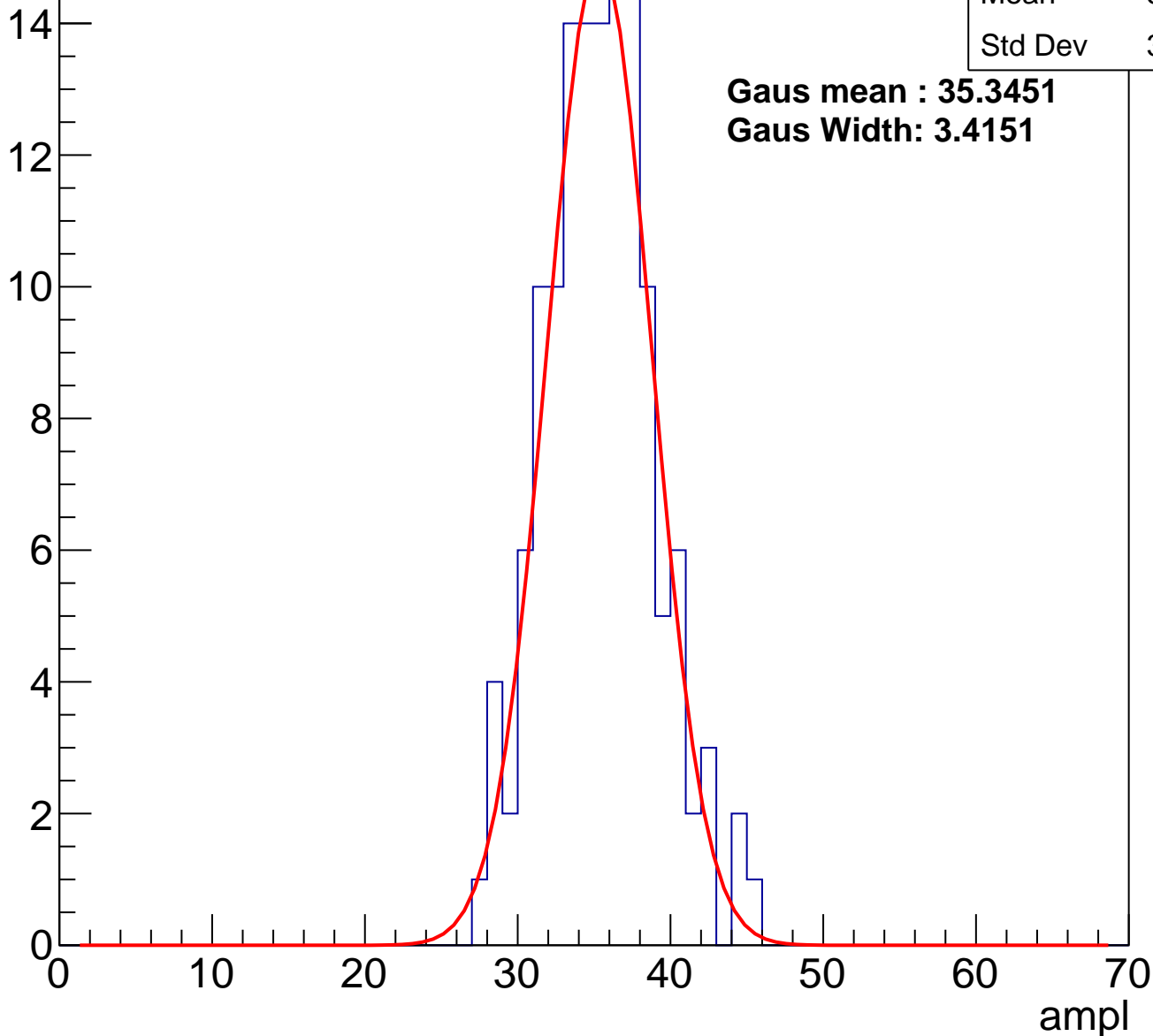
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	34.97
Std Dev	3.539

Gaus mean : 35.3451

Gaus Width: 3.4151

Entry



B0L101S, U21-ch104, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

123

Mean

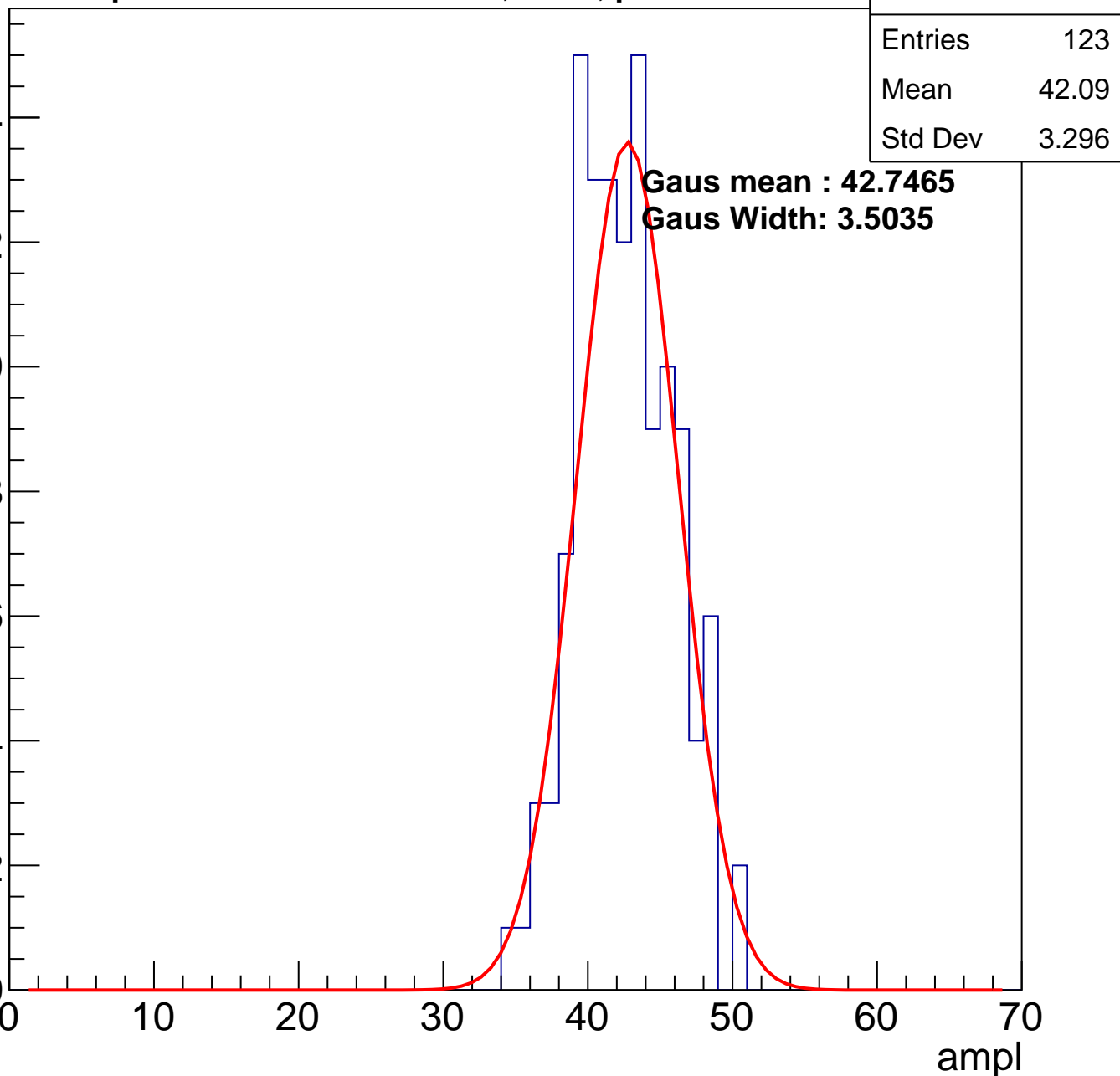
42.09

Std Dev

3.296

Gaus mean : 42.7465

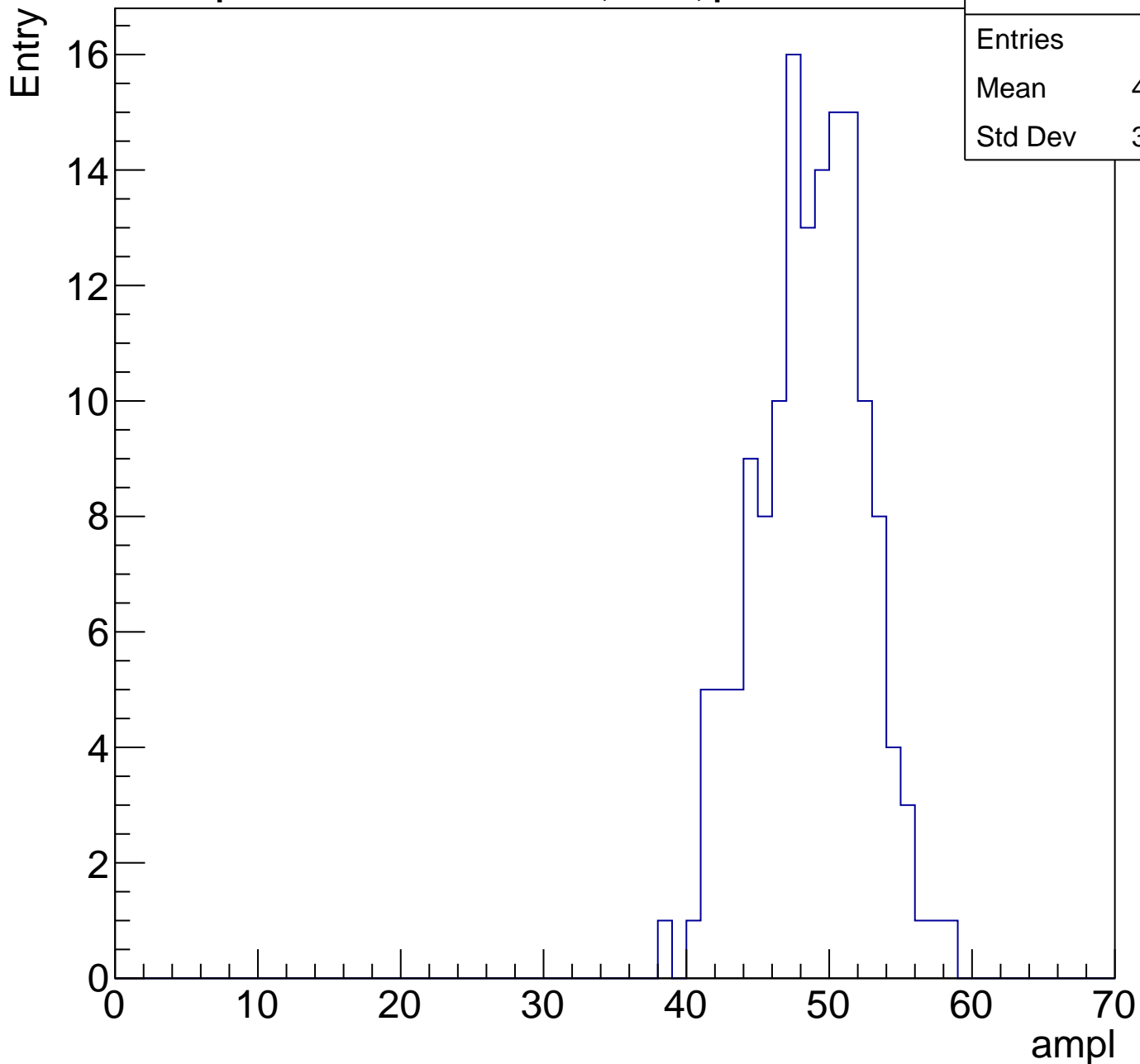
Gaus Width: 3.5035



B0L101S, U21-ch104, adc3

calib_packv5_042523_0143.root, FC#1, port C1

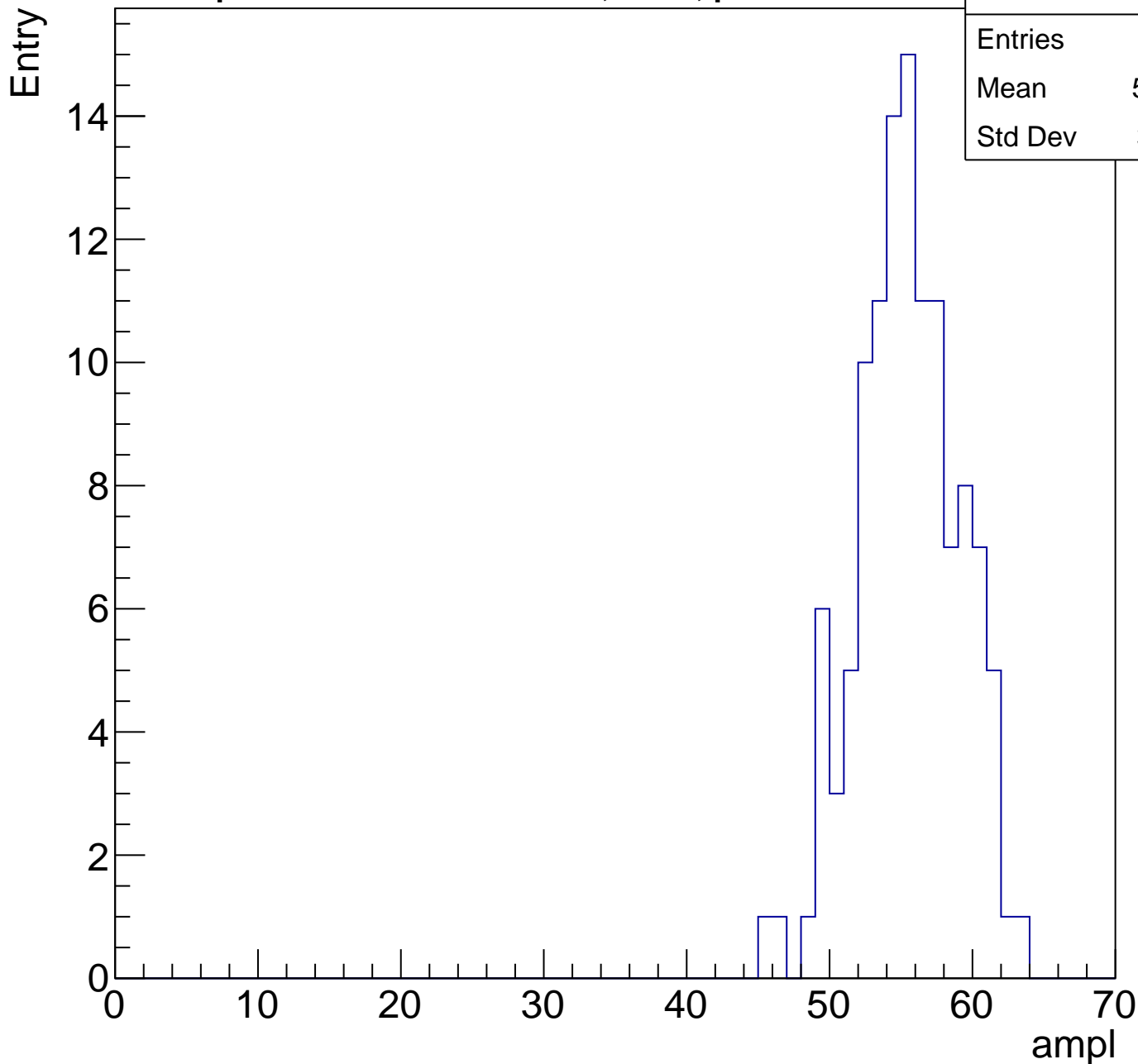
Entries	145
Mean	48.26
Std Dev	3.789



B0L101S, U21-ch104, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	55.03
Std Dev	3.521



B0L101S, U21-ch104, adc5

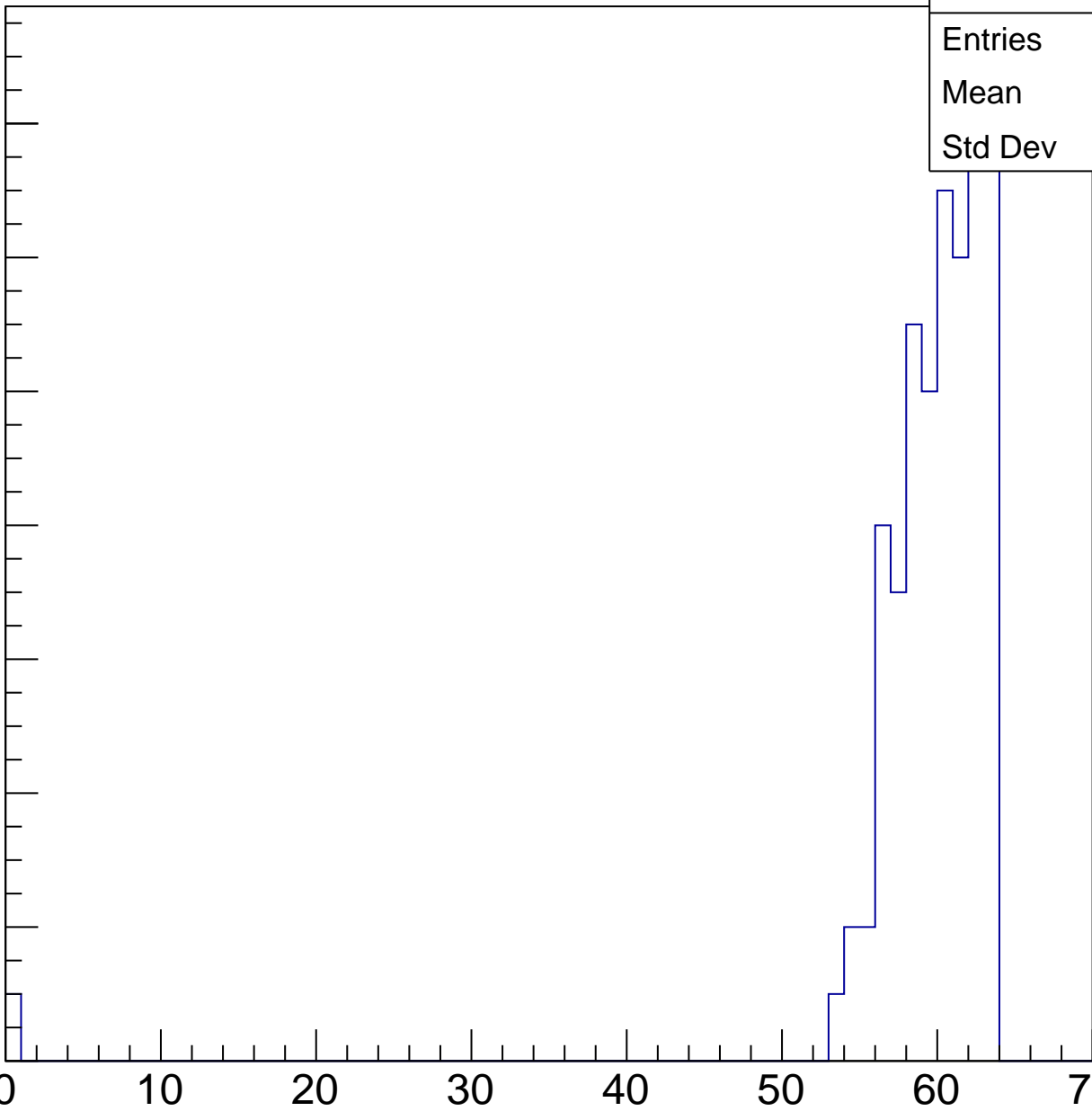
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	59.06
Std Dev	6.559

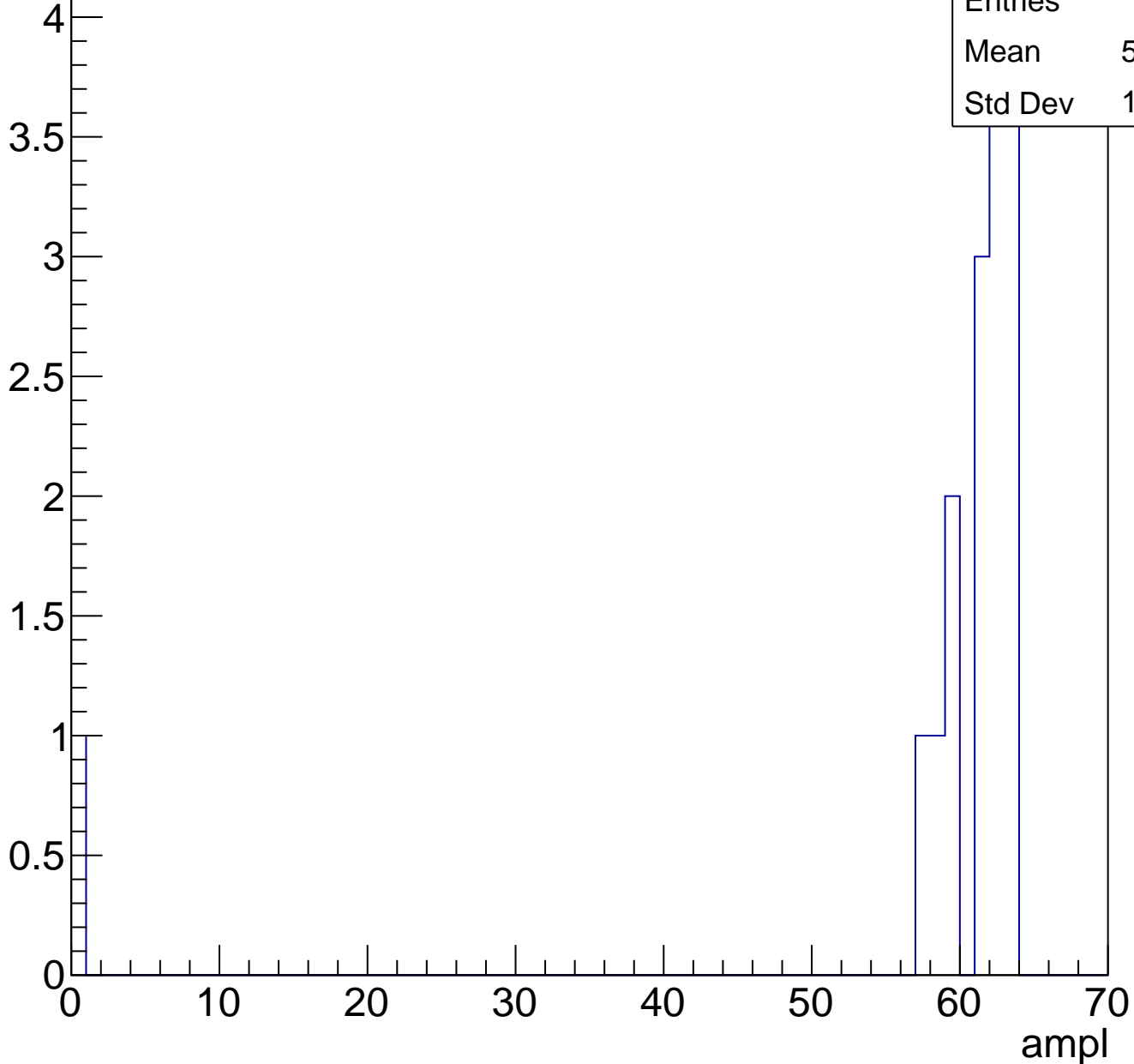
ampl



B0L101S, U21-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch105, adc0

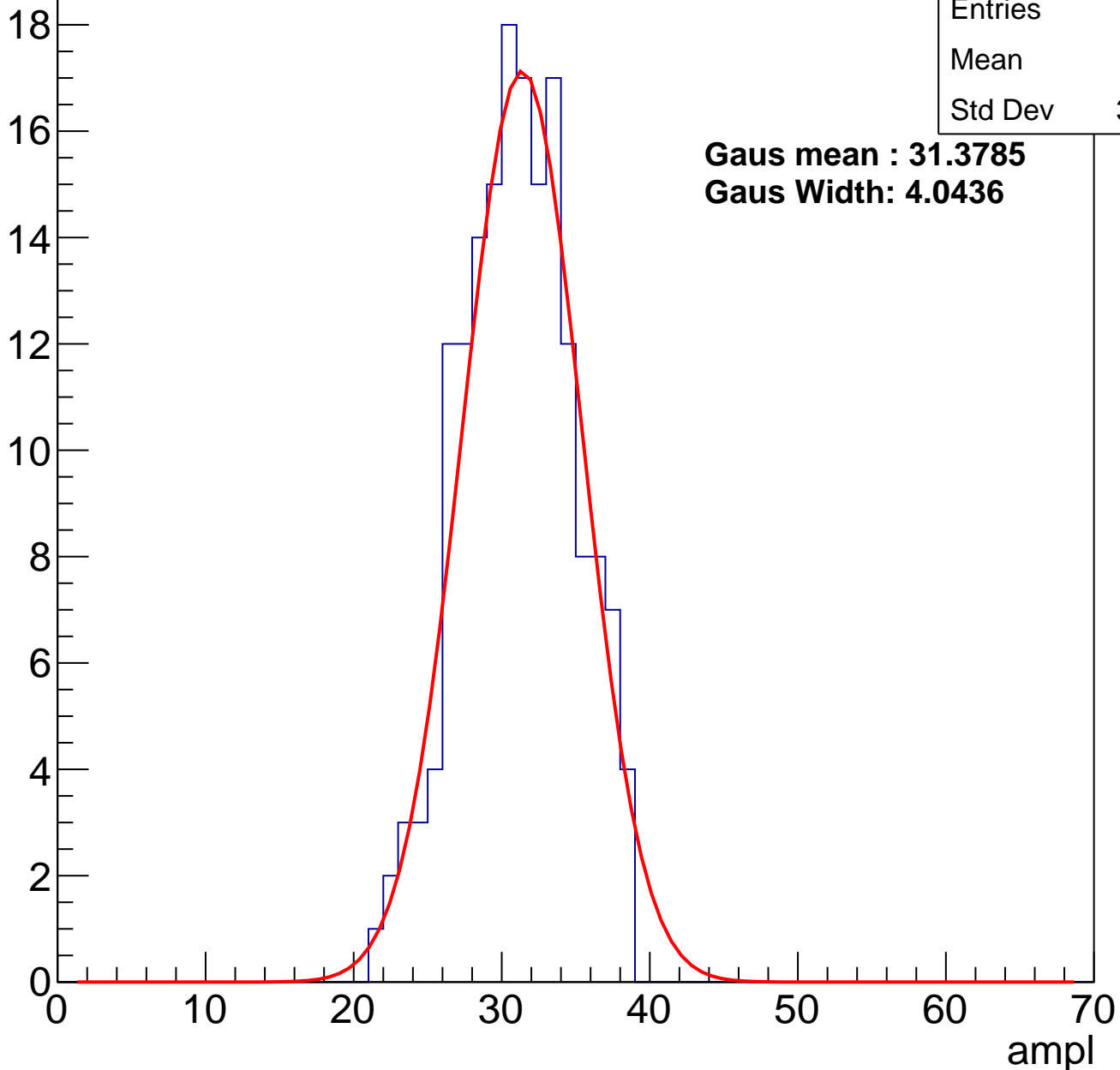
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	30.6
Std Dev	3.721

Gaus mean : 31.3785

Gaus Width: 4.0436

Entry



B0L101S, U21-ch105, adc1

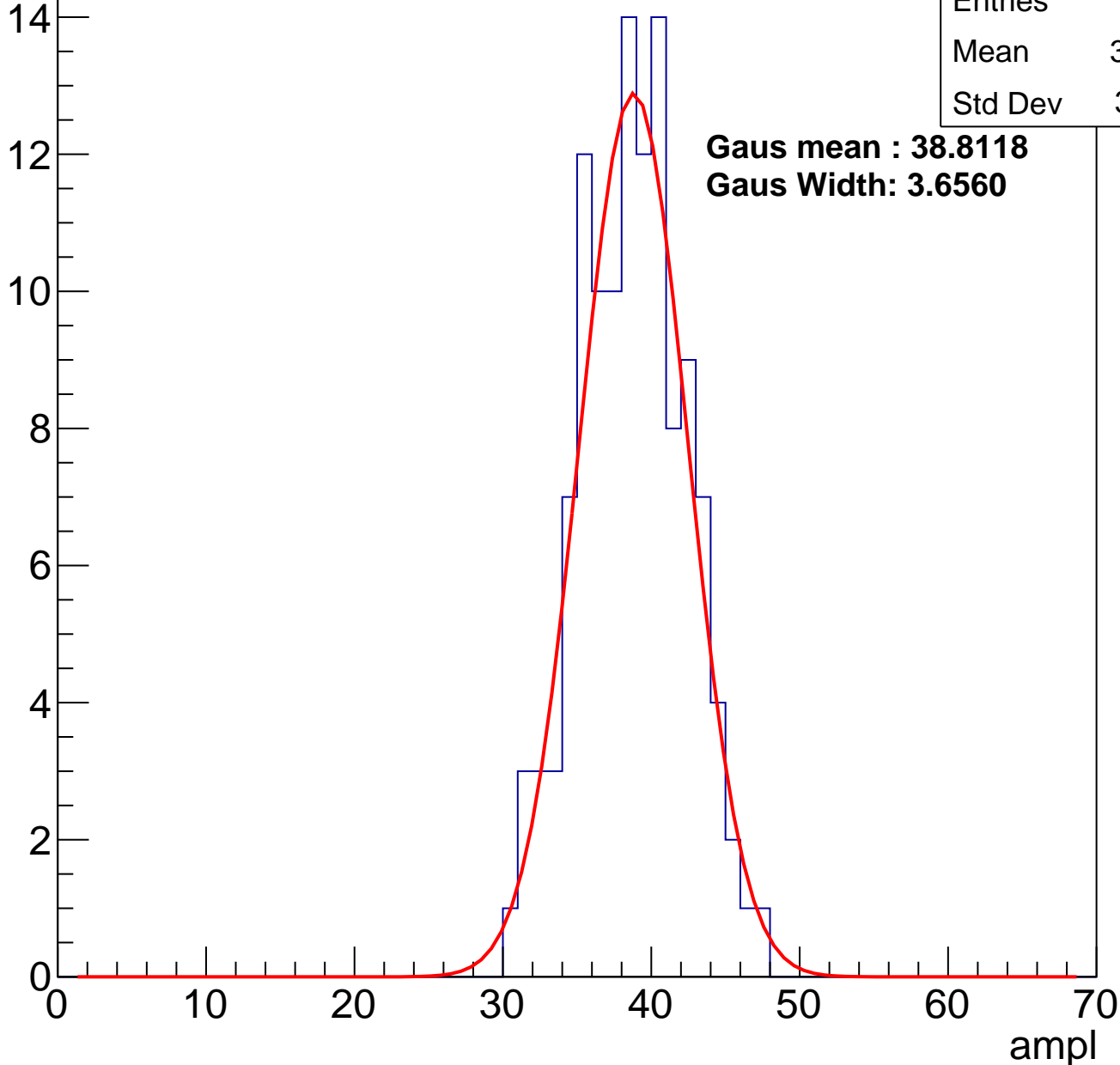
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	38.28
Std Dev	3.521

Gaus mean : 38.8118

Gaus Width: 3.6560

Entry



B0L101S, U21-ch105, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	43.95
Std Dev	3.544

Gaus mean : 44.7623

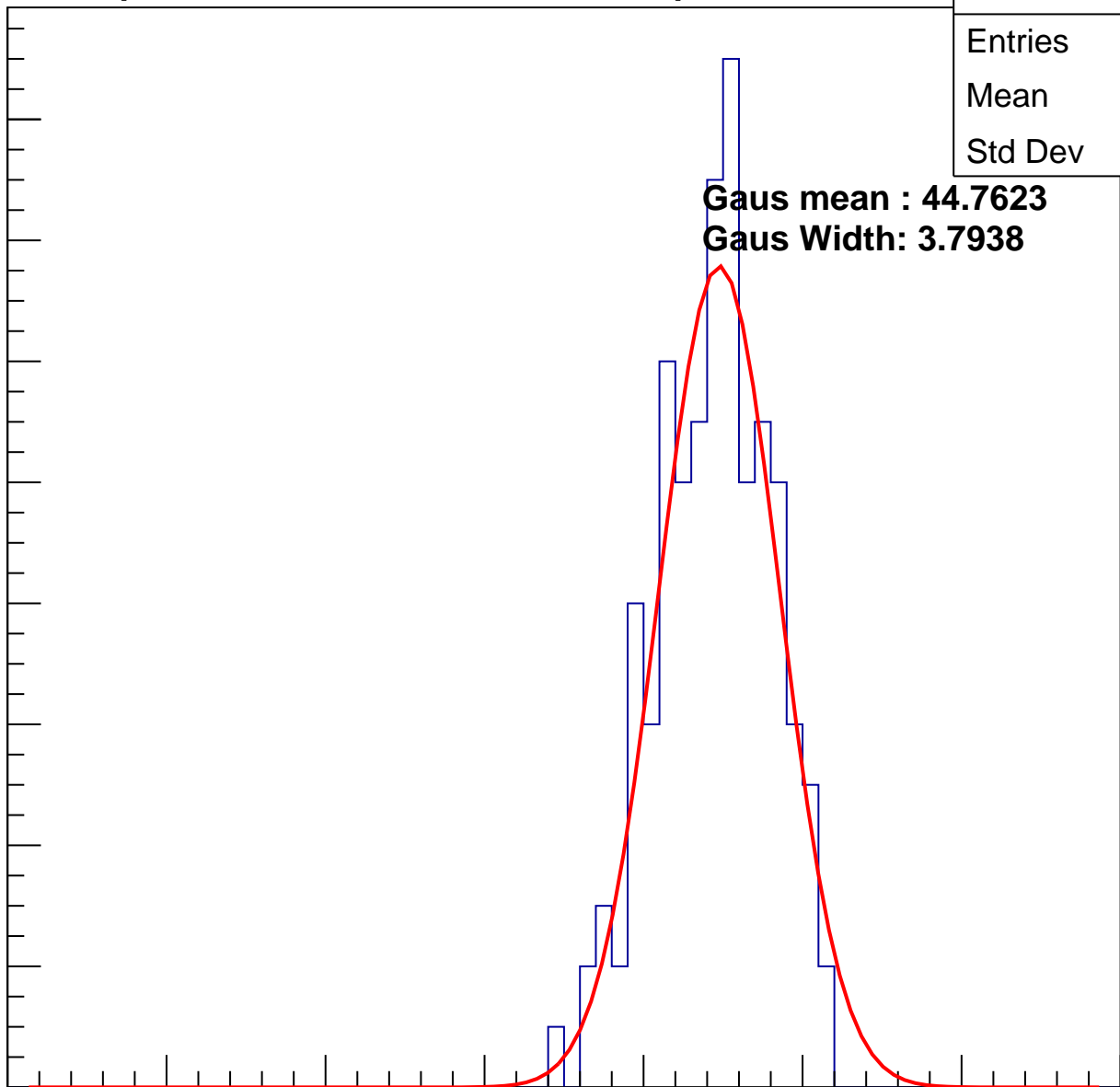
Gaus Width: 3.7938

Entry

16
14
12
10
8
6
4
2
0

ampl

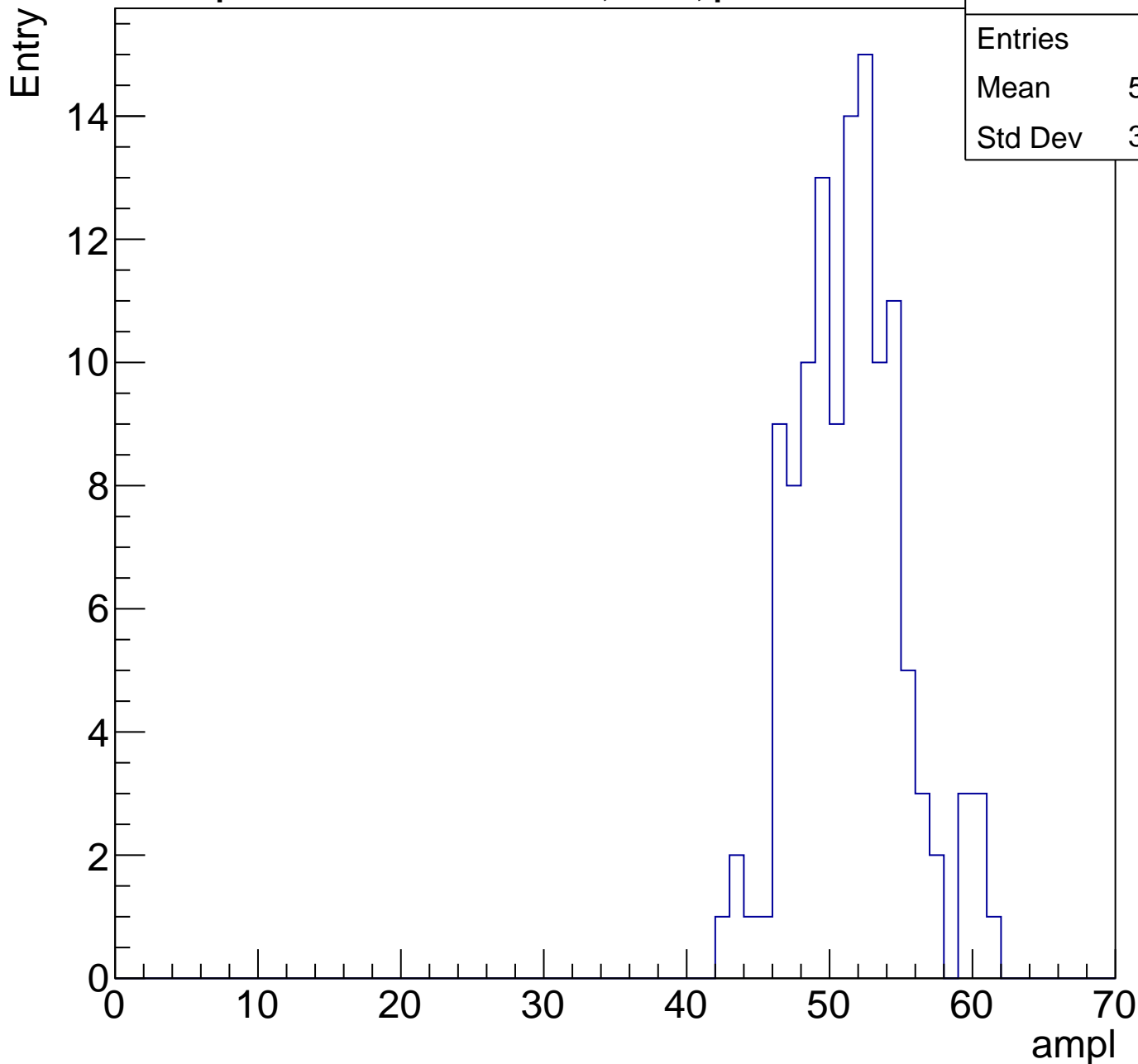
0 10 20 30 40 50 60 70



B0L101S, U21-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

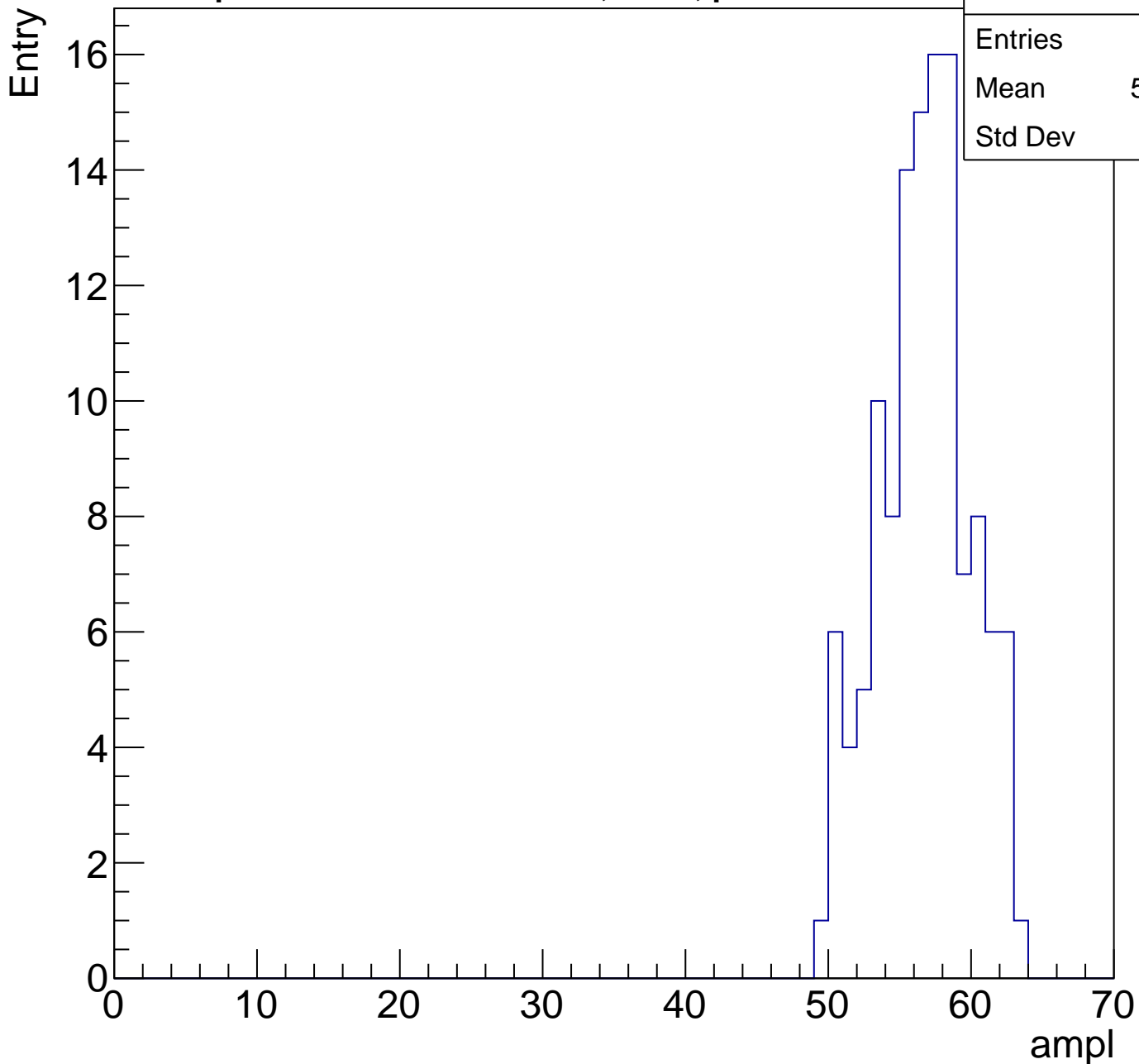
Entries	121
Mean	50.97
Std Dev	3.764



B0L101S, U21-ch105, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	56.25
Std Dev	3.2



B0L101S, U21-ch105, adc5

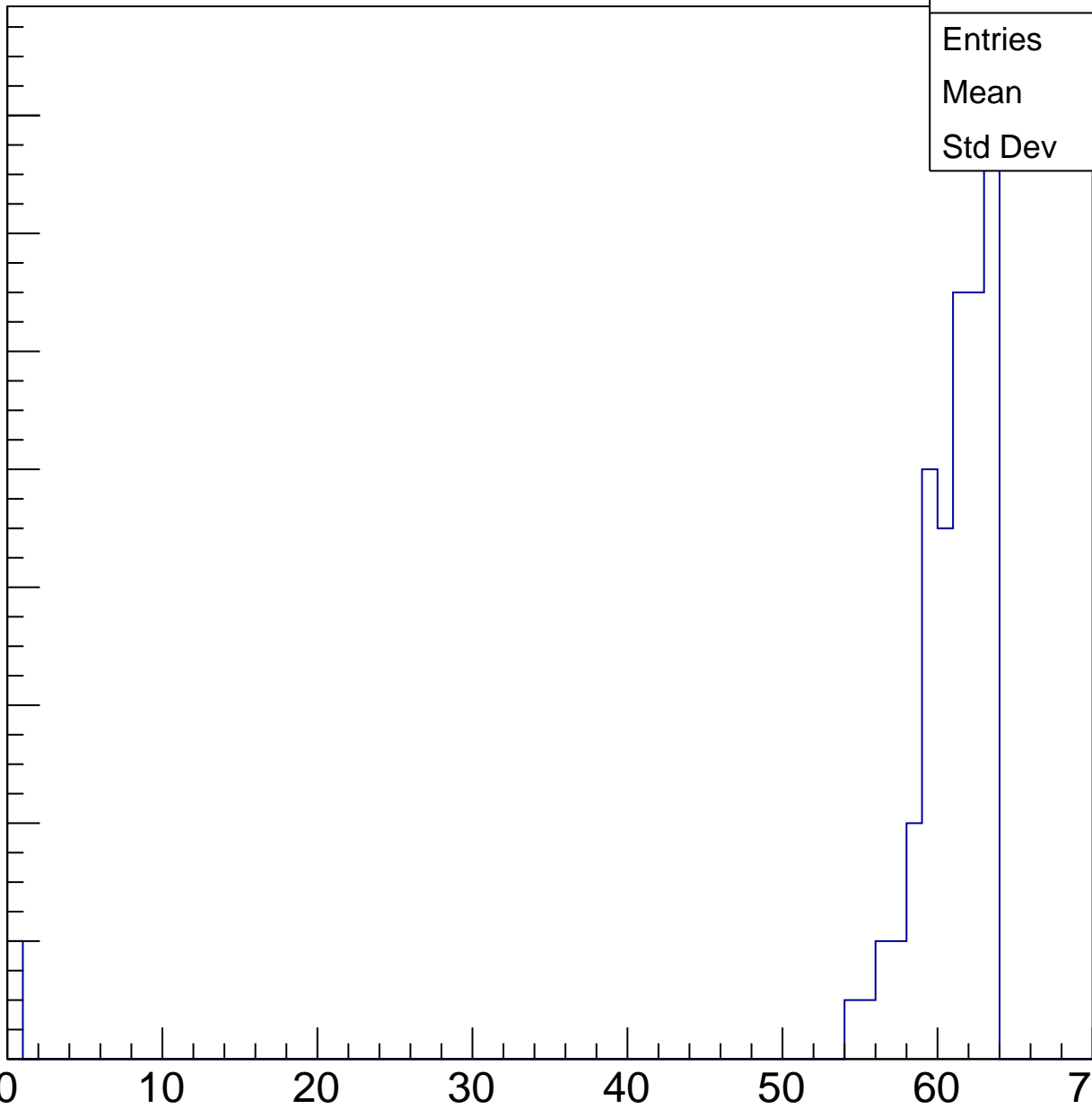
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.01
Std Dev	10.06

ampl



B0L101S, U21-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

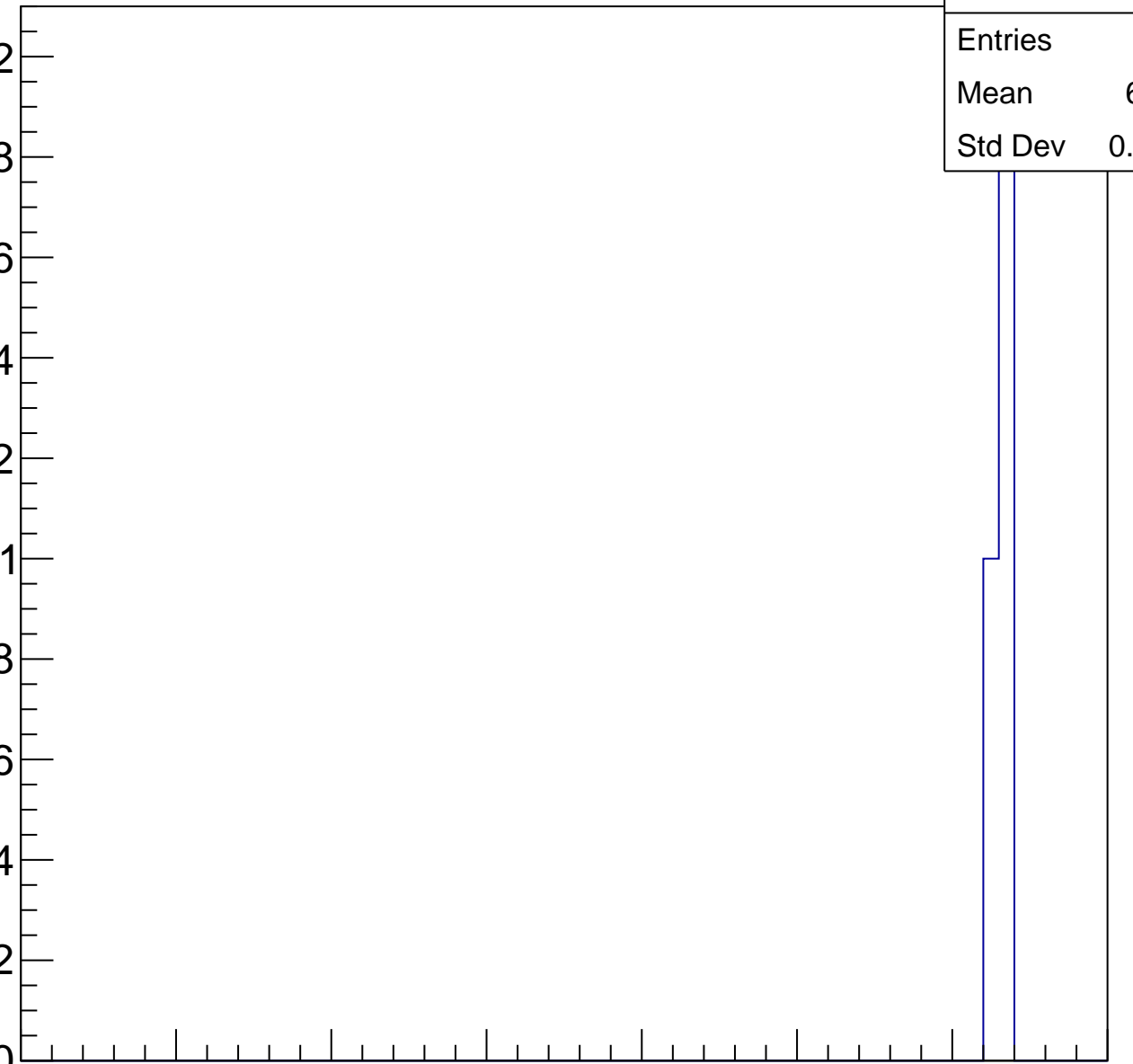
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch106, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	27.37
Std Dev	4.832

Gaus mean : 28.3656

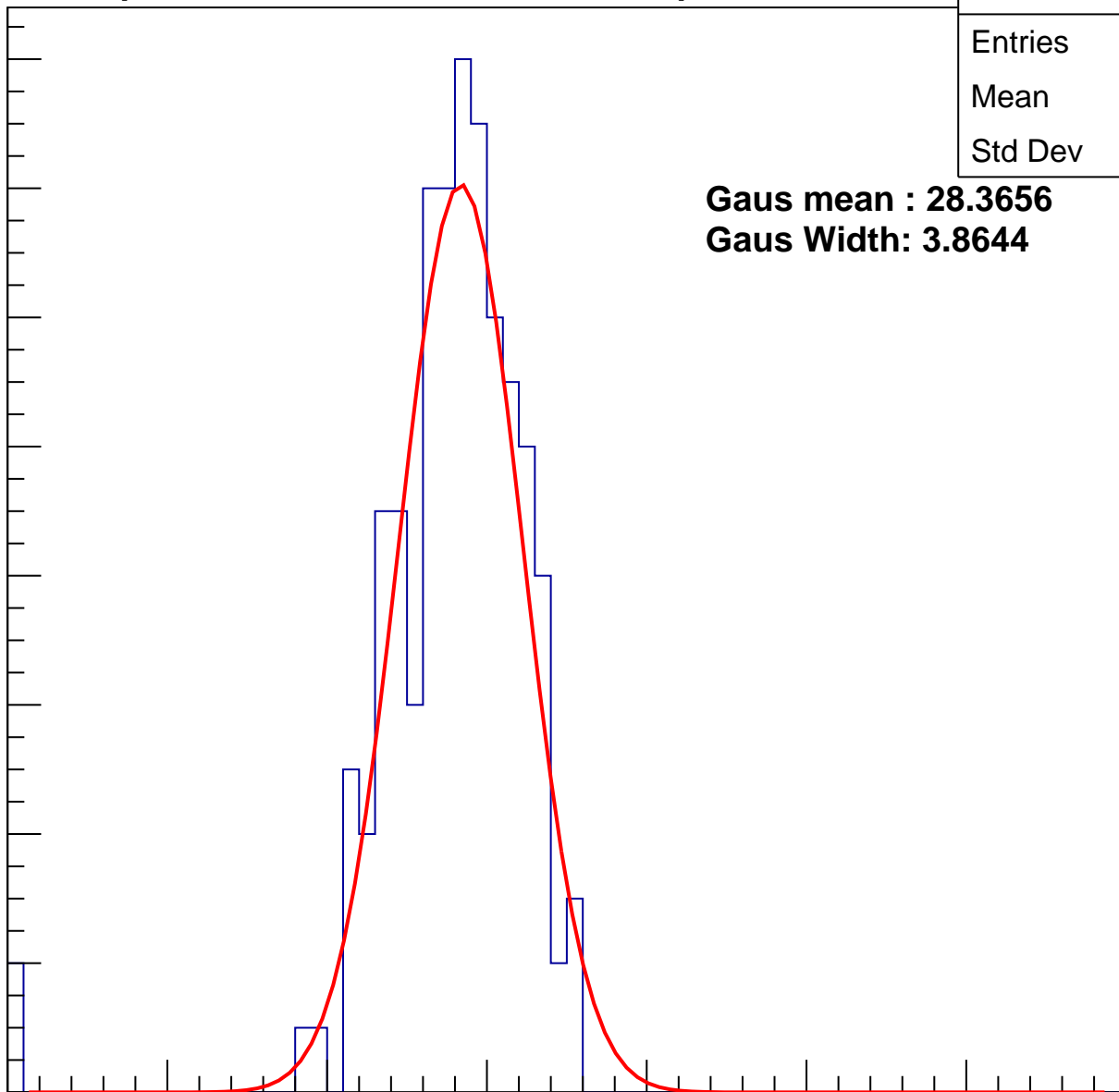
Gaus Width: 3.8644

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U21-ch106, adc1

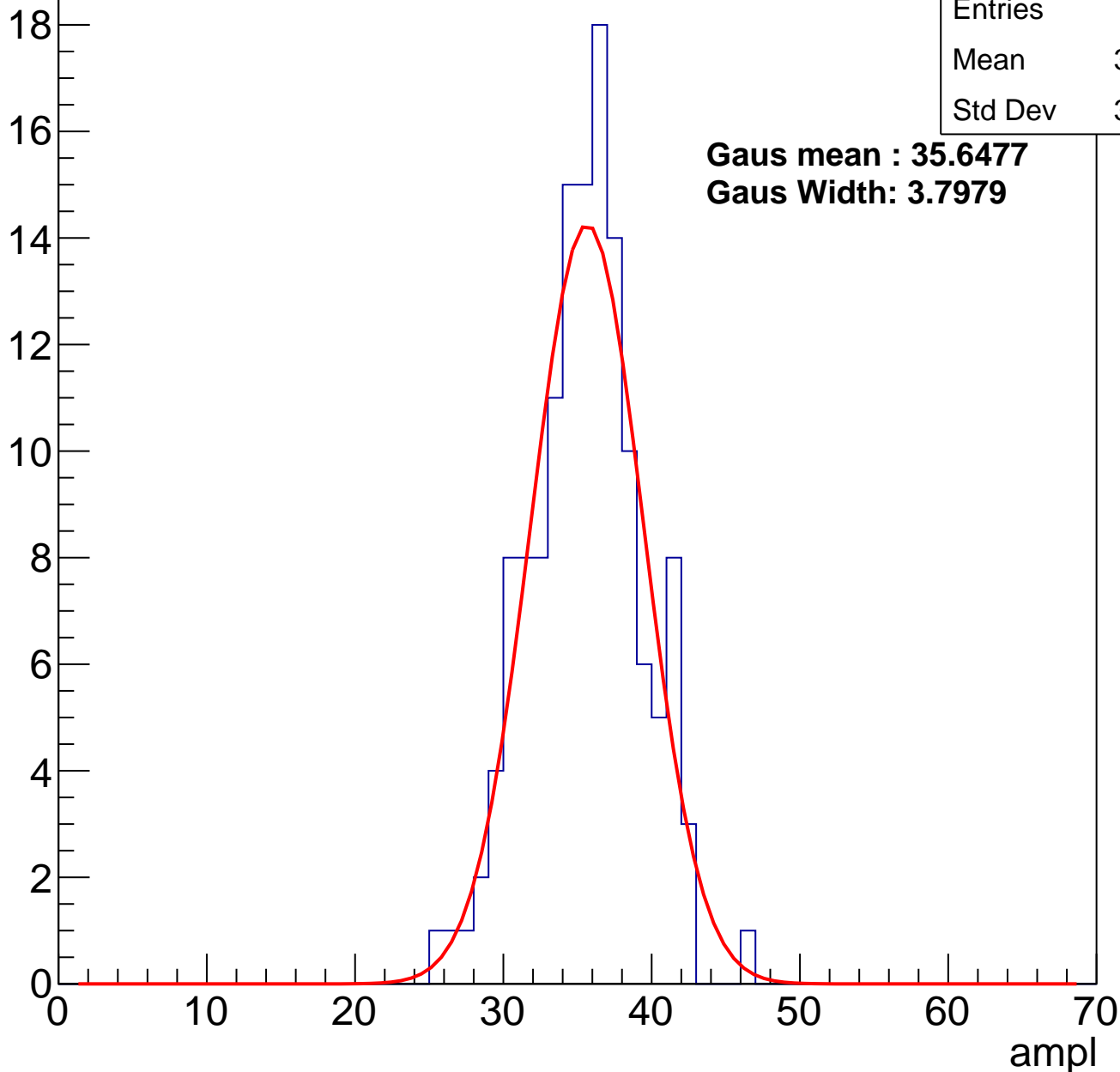
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	35.05
Std Dev	3.684

Gaus mean : 35.6477

Gaus Width: 3.7979

Entry



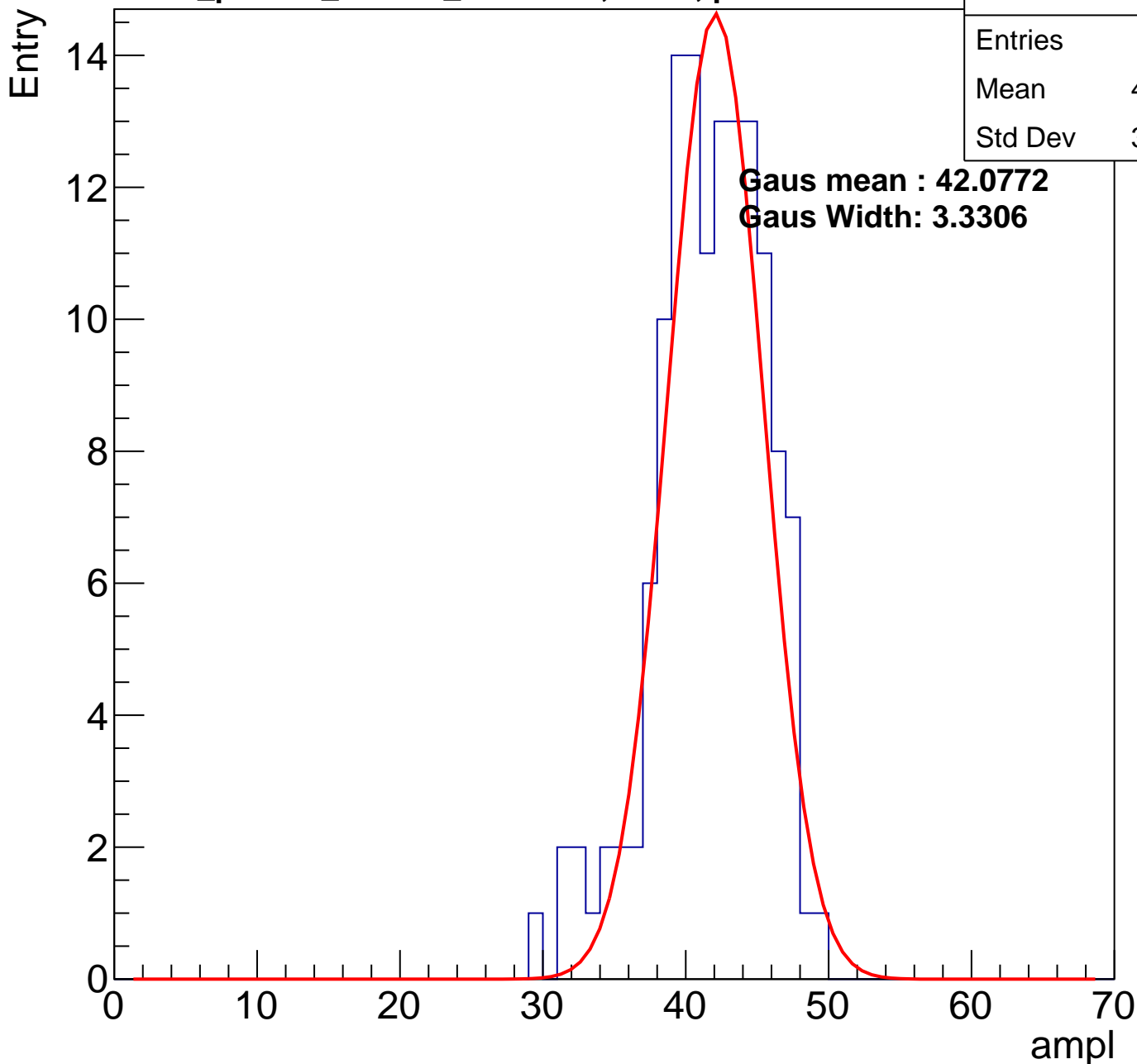
B0L101S, U21-ch106, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	41.22
Std Dev	3.827

Gaus mean : 42.0772

Gaus Width: 3.3306



B0L101S, U21-ch106, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	48.5
Std Dev	3.733

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

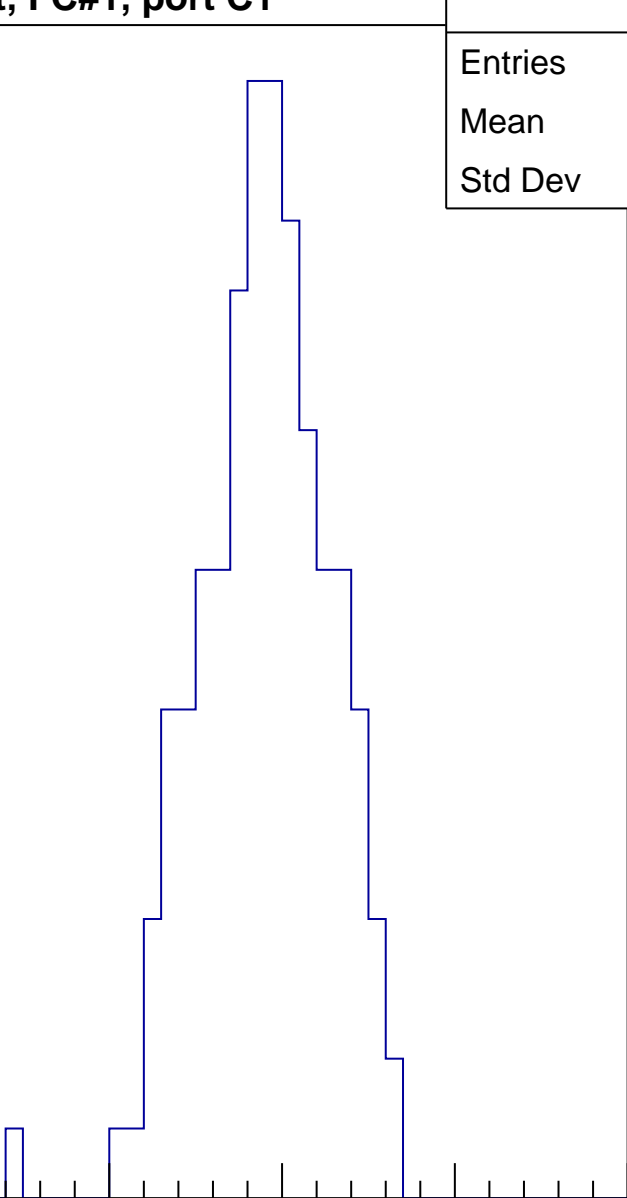
40

50

60

70

ampl



B0L101S, U21-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

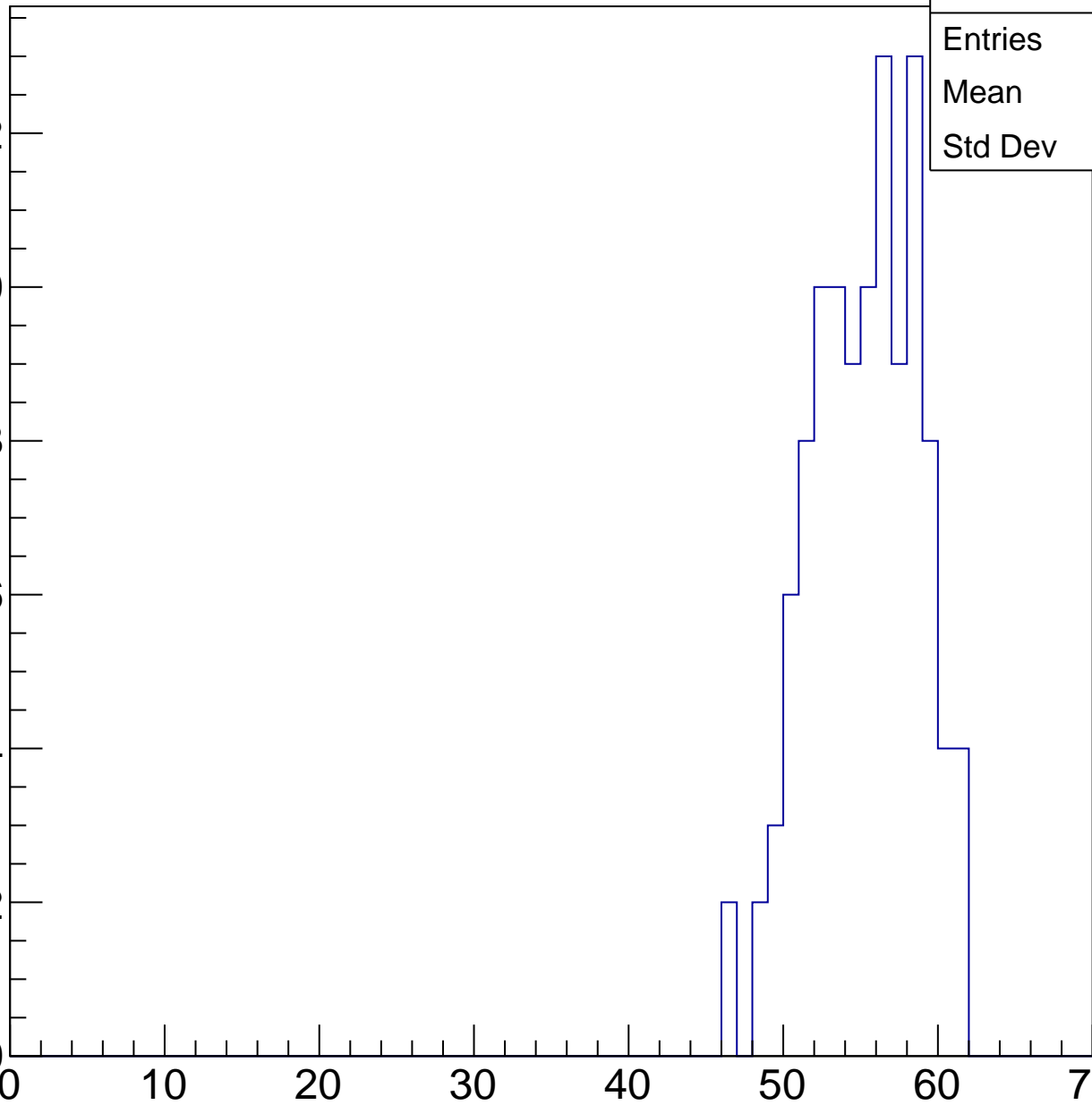
40

50

60

ampl

Entries	111
Mean	54.77
Std Dev	3.445



B0L101S, U21-ch106, adc5

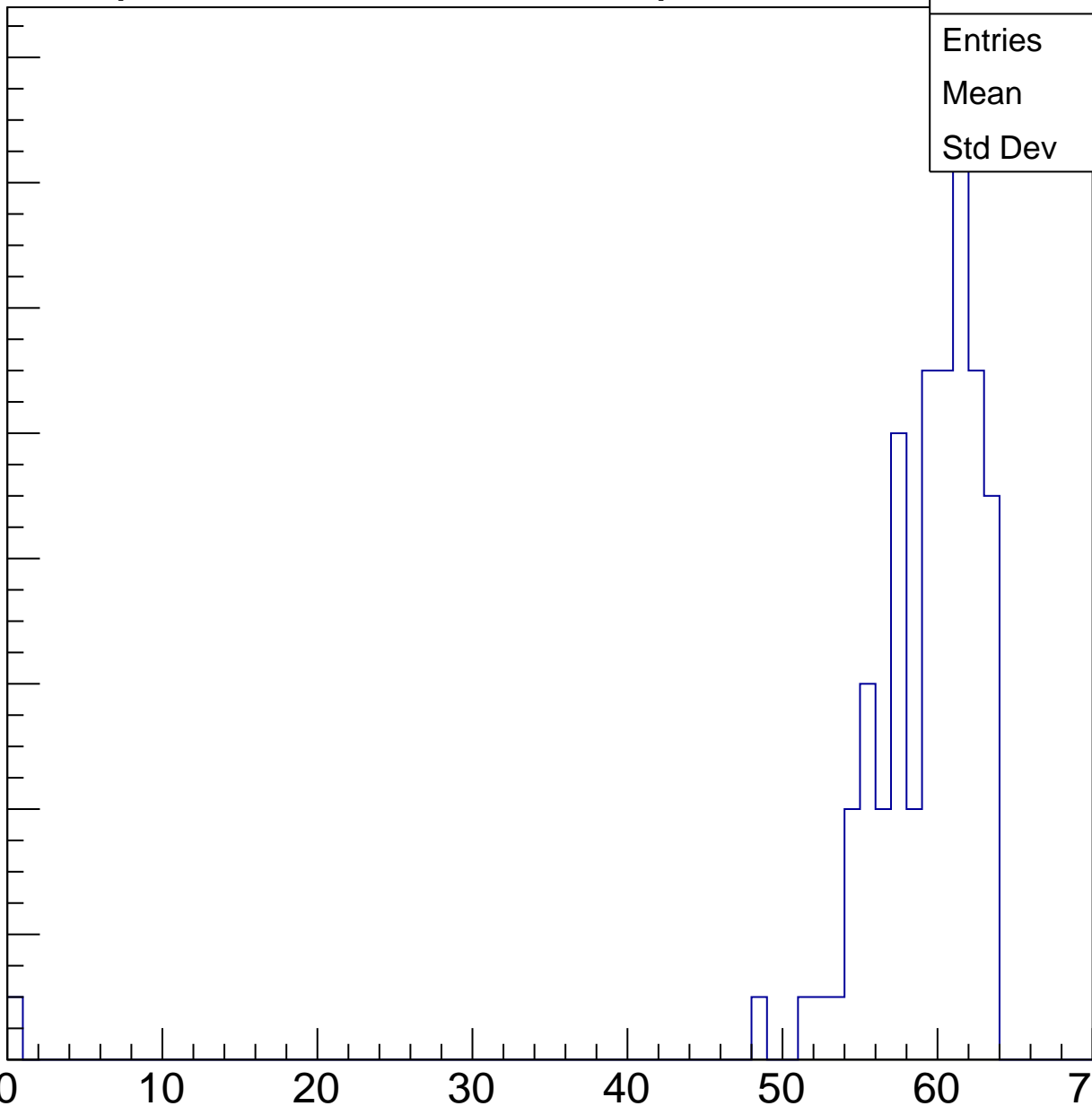
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.35
Std Dev	6.879

ampl

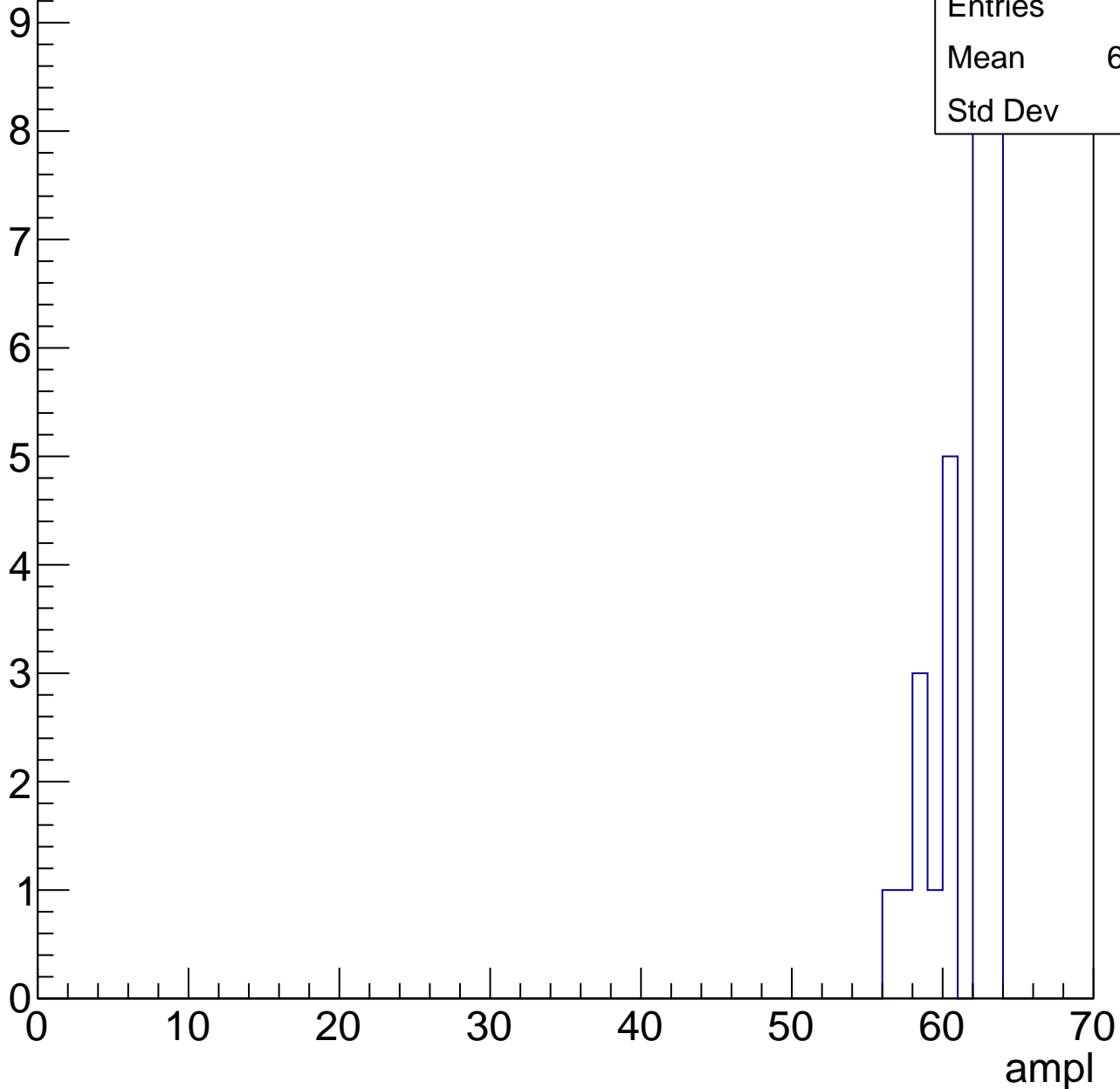


B0L101S, U21-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

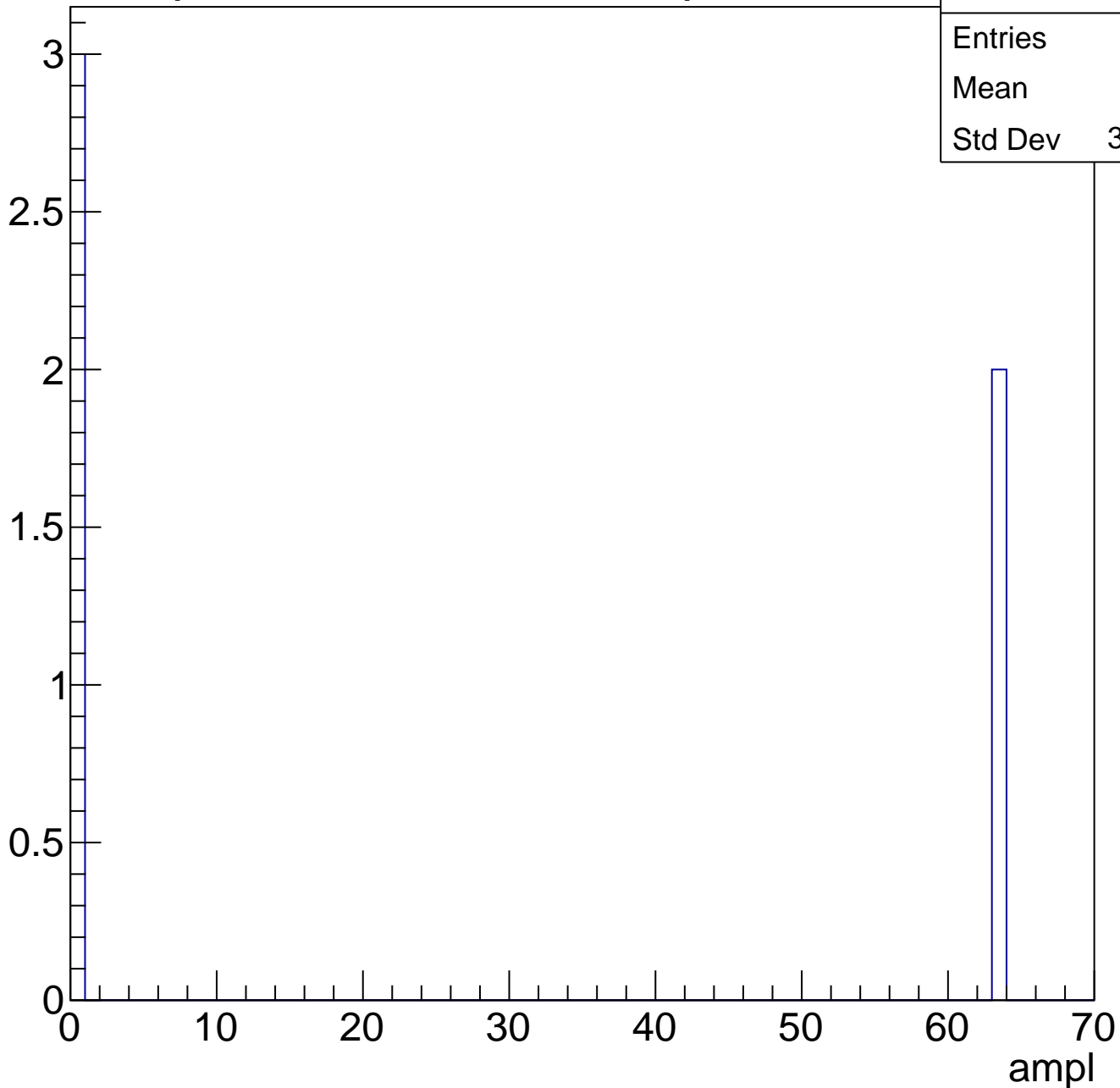
Entries	29
Mean	61.07
Std Dev	2.05



B0L101S, U21-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	25.2
Std Dev	30.86

B0L101S, U21-ch107, adc0

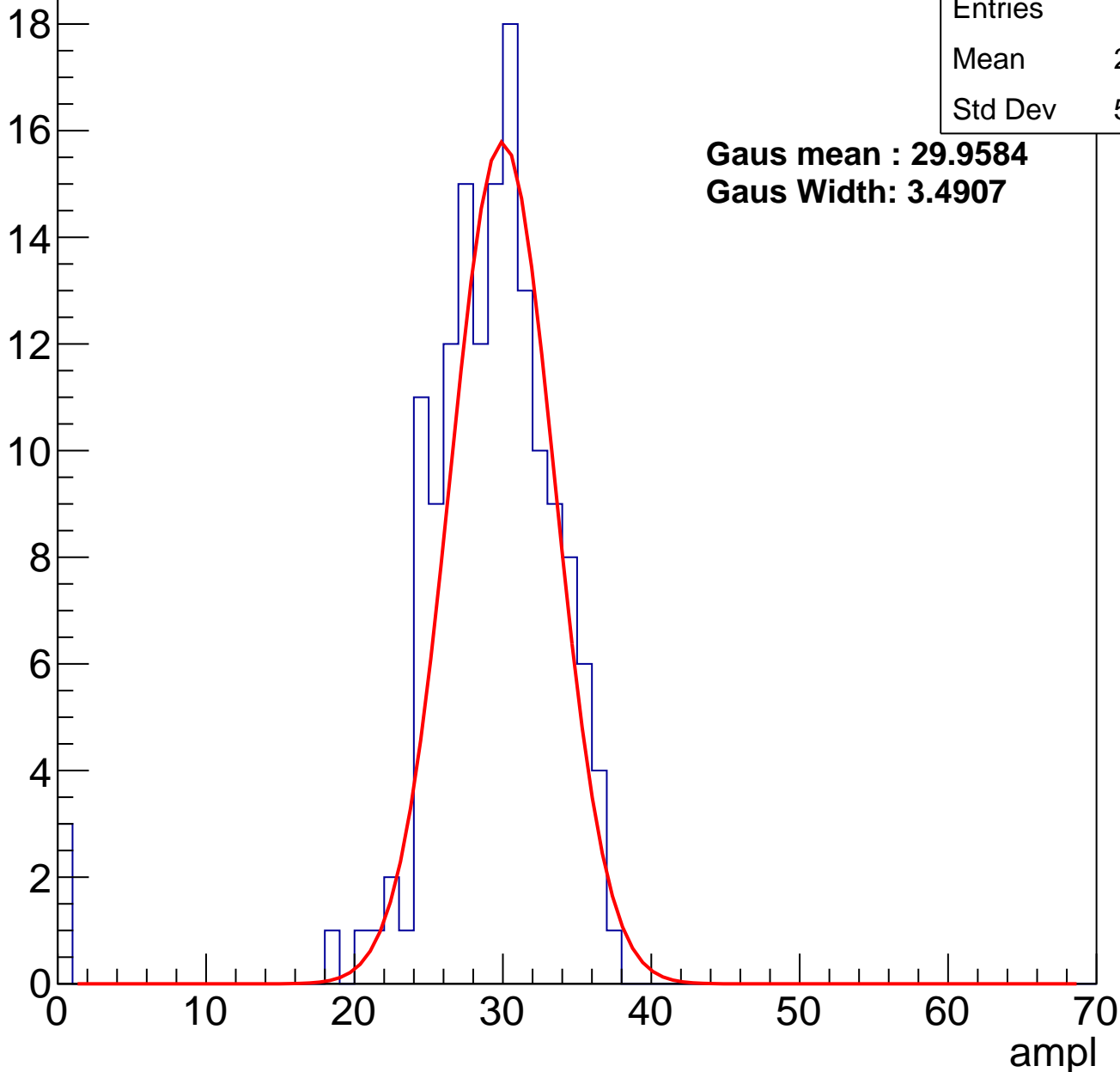
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.46
Std Dev	5.415

Gaus mean : 29.9584

Gaus Width: 3.4907

Entry



B0L101S, U21-ch107, adc1

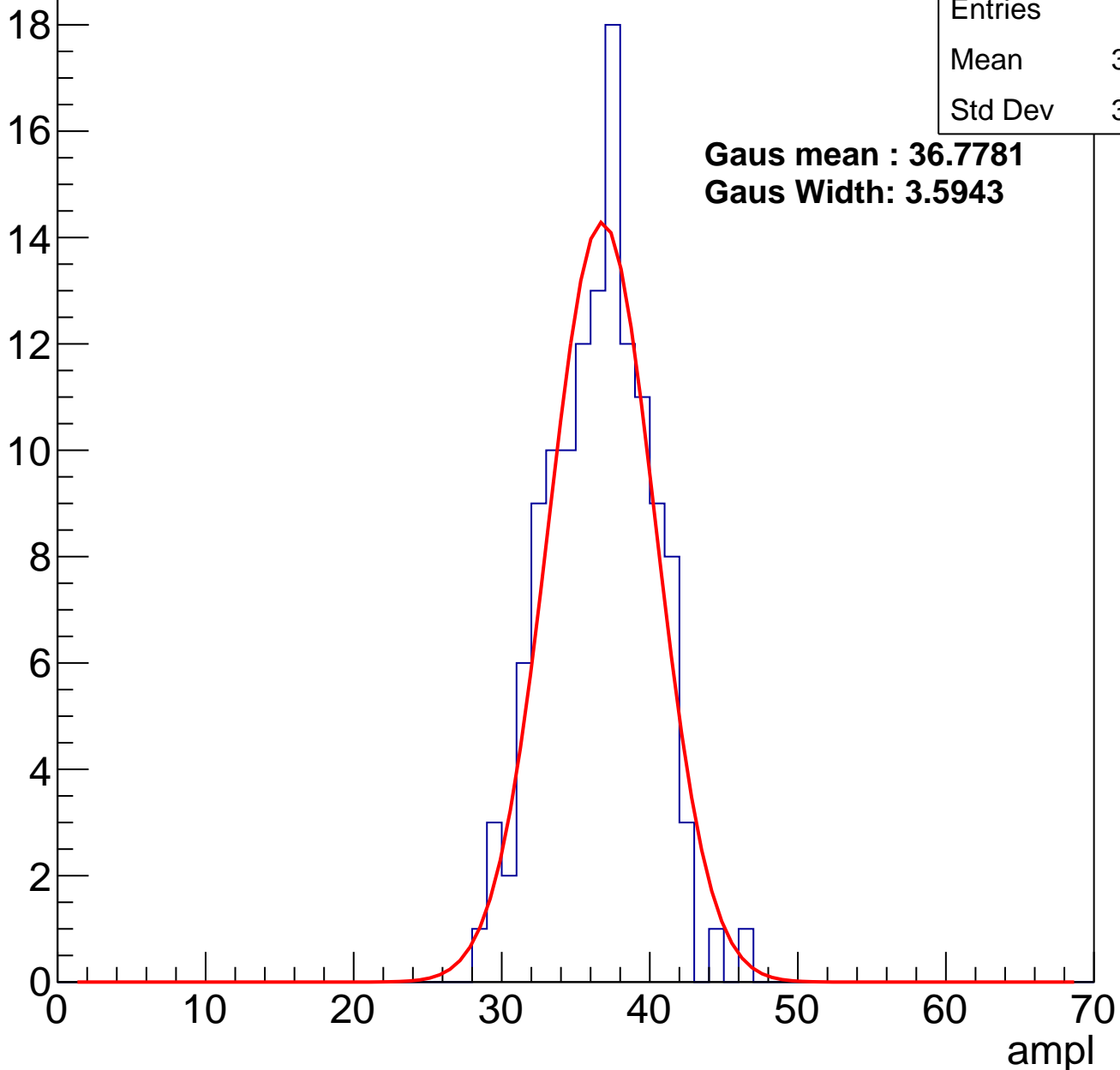
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	36.14
Std Dev	3.407

Gaus mean : 36.7781

Gaus Width: 3.5943

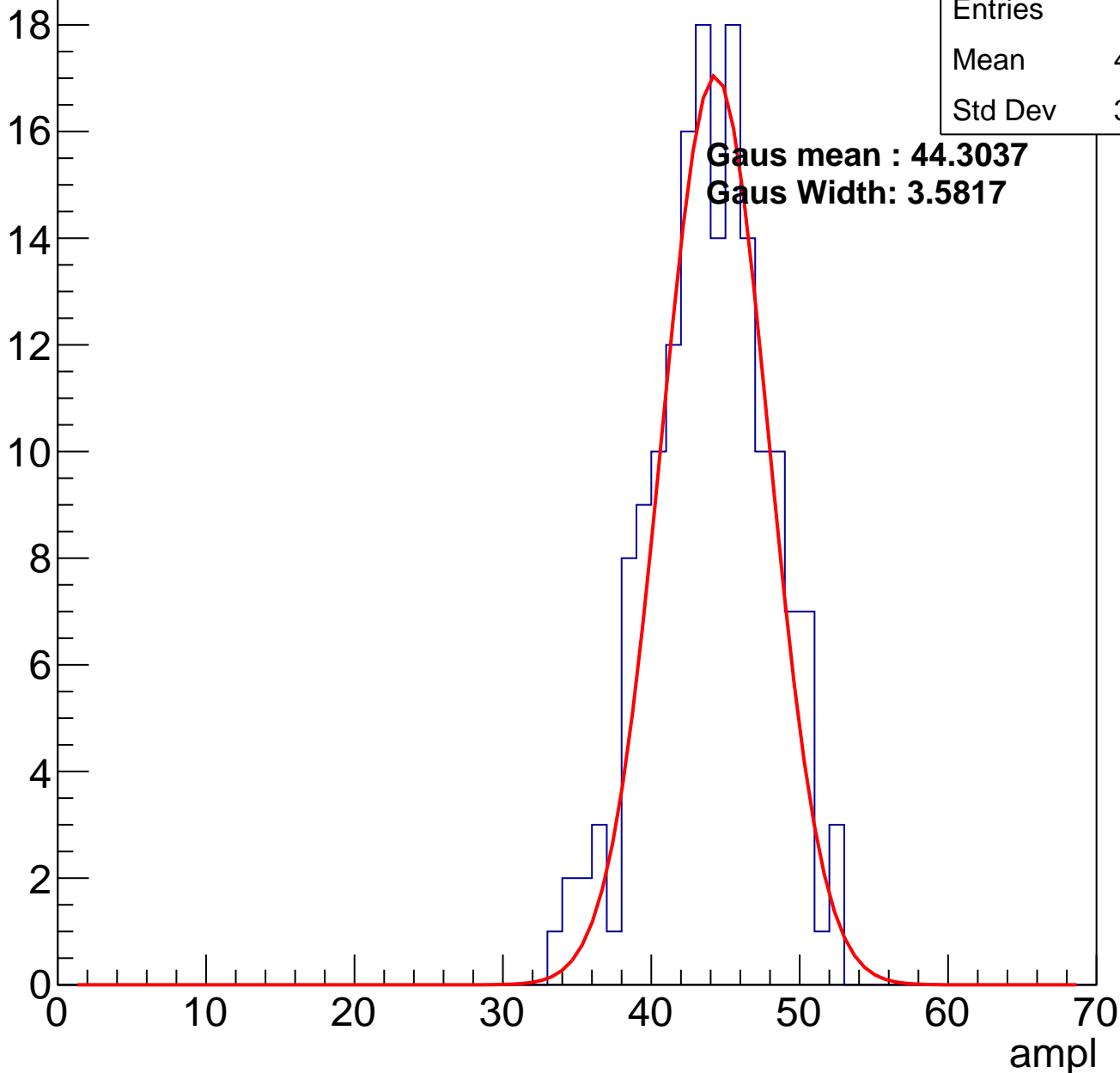
Entry



B0L101S, U21-ch107, adc2

calib_packv5_042523_0143.root, FC#1, port C1

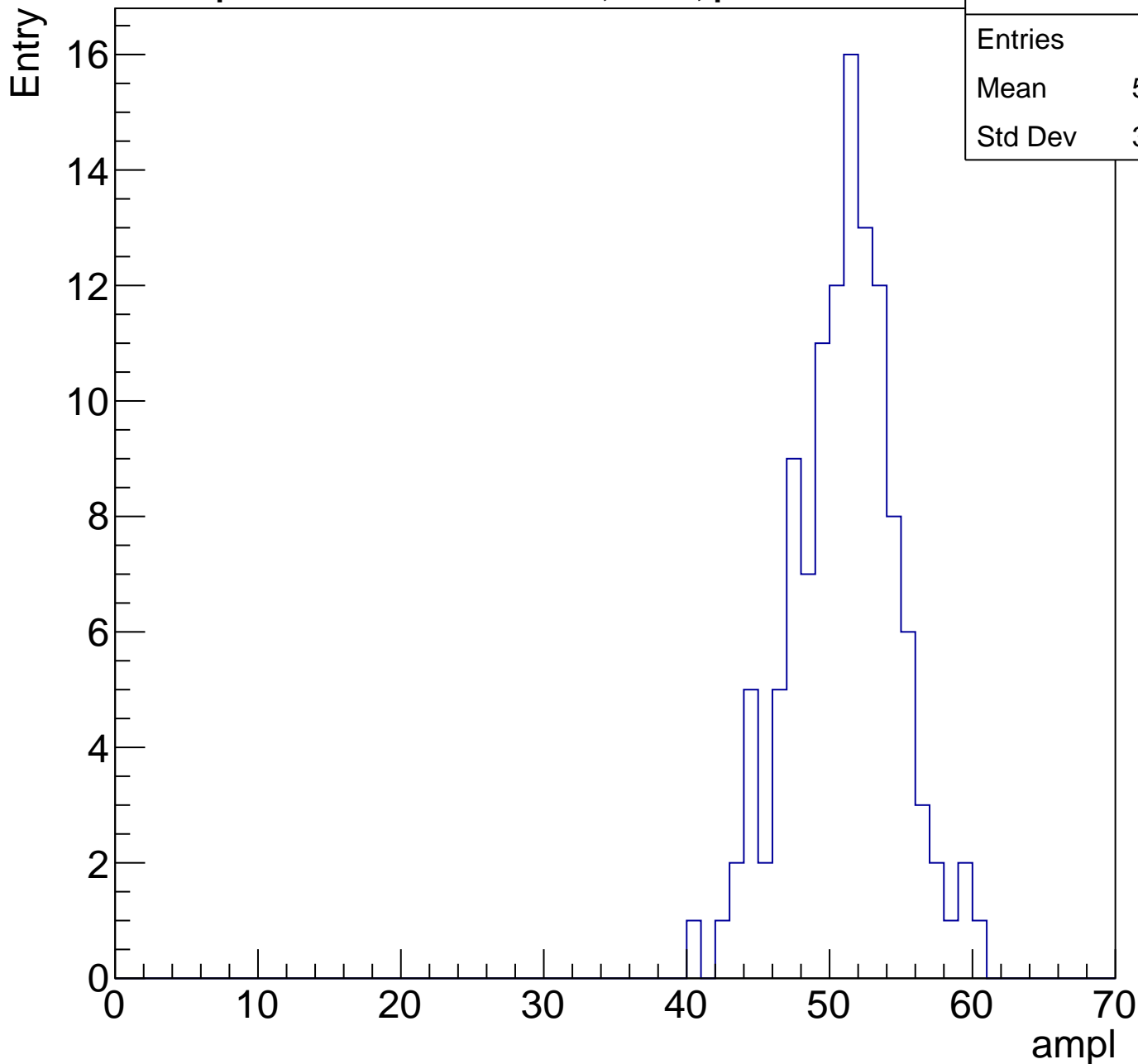
Entry



B0L101S, U21-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

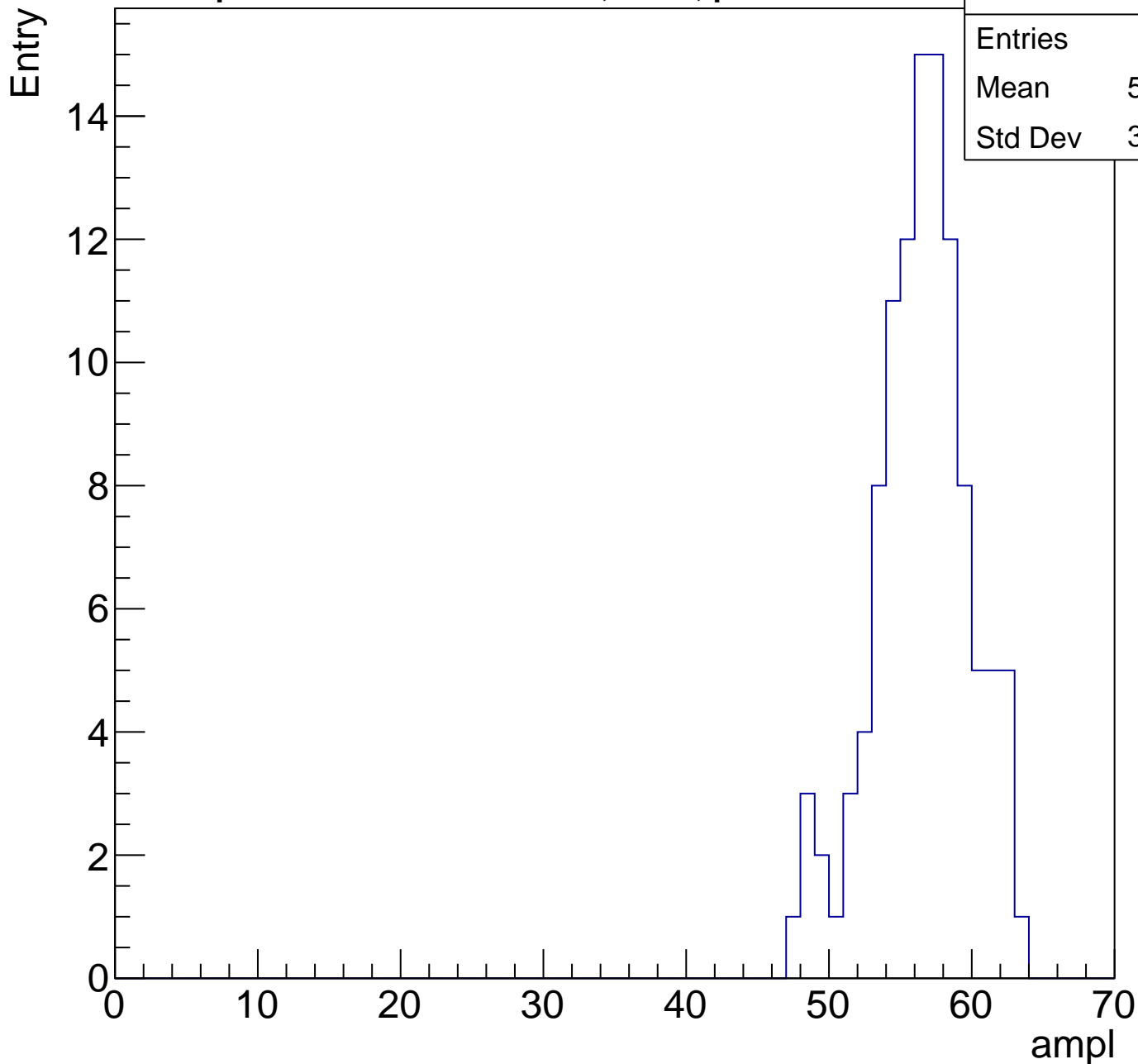
Entries	119
Mean	50.54
Std Dev	3.716



B0L101S, U21-ch107, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	56.03
Std Dev	3.346



B0L101S, U21-ch107, adc5

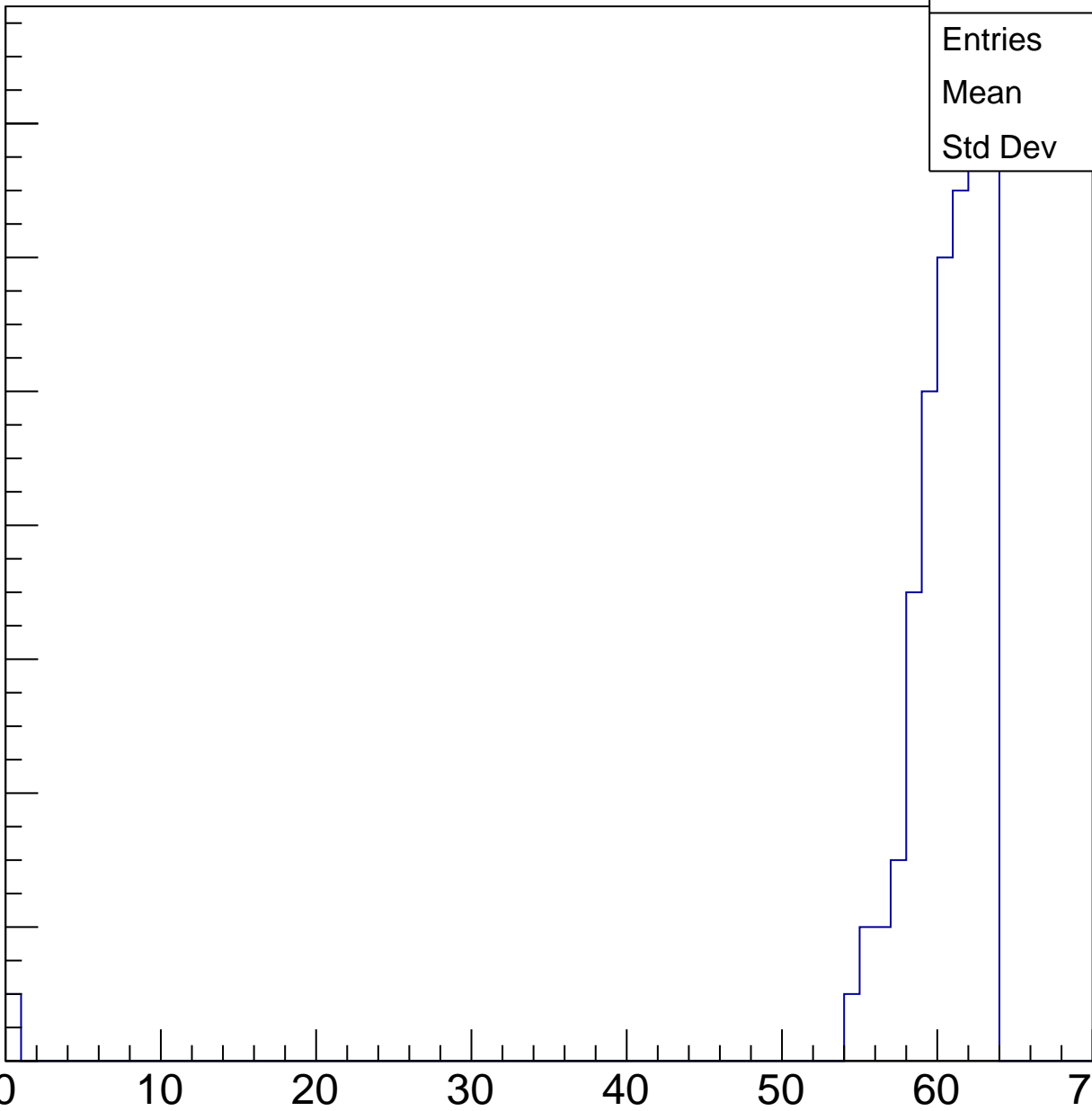
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	59.61
Std Dev	7.049

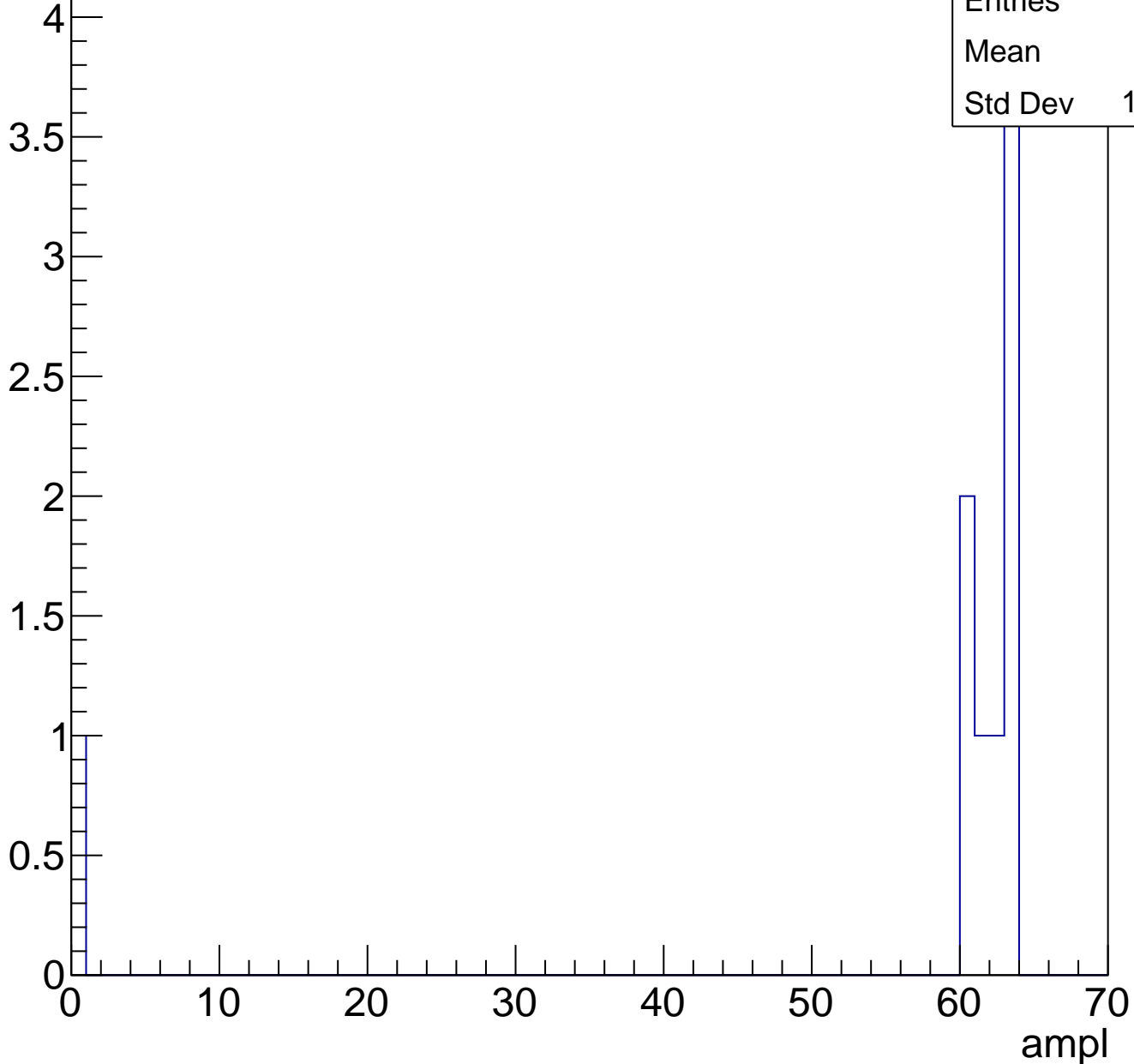
ampl



B0L101S, U21-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

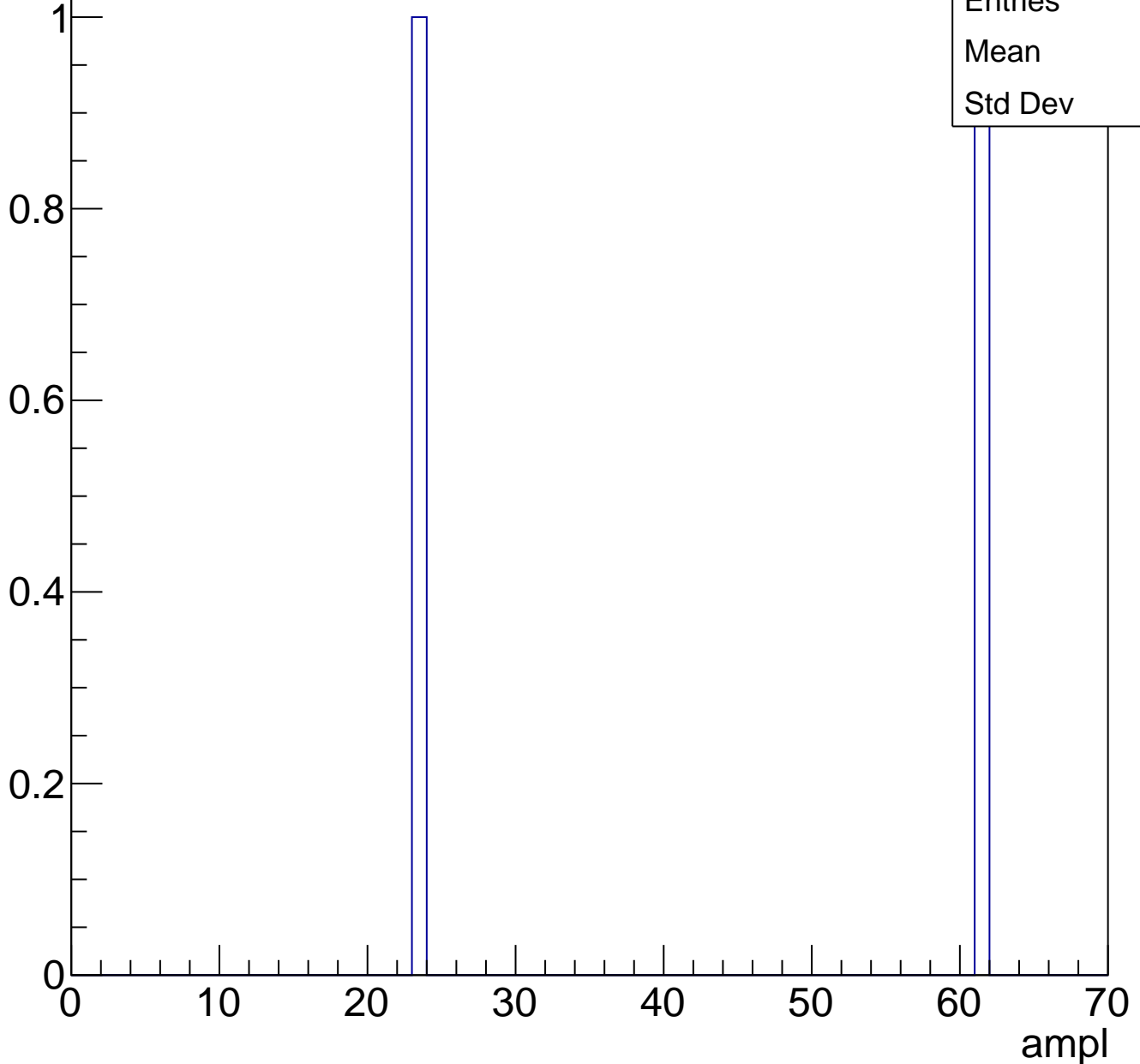


Entries	9
Mean	55
Std Dev	19.48

B0L101S, U21-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch108, adc0

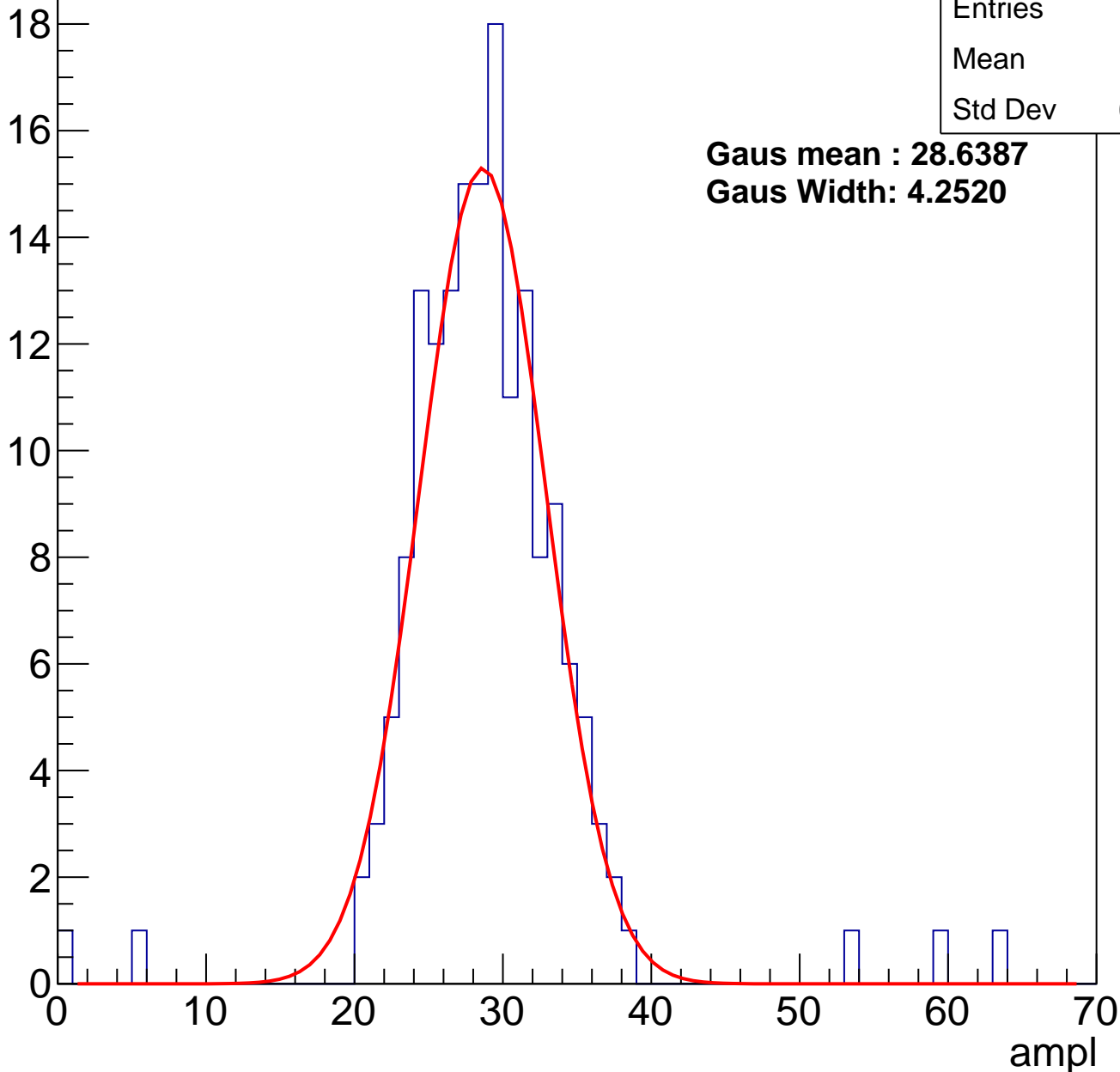
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	28.5
Std Dev	6.271

Gaus mean : 28.6387

Gaus Width: 4.2520

Entry



B0L101S, U21-ch108, adc1

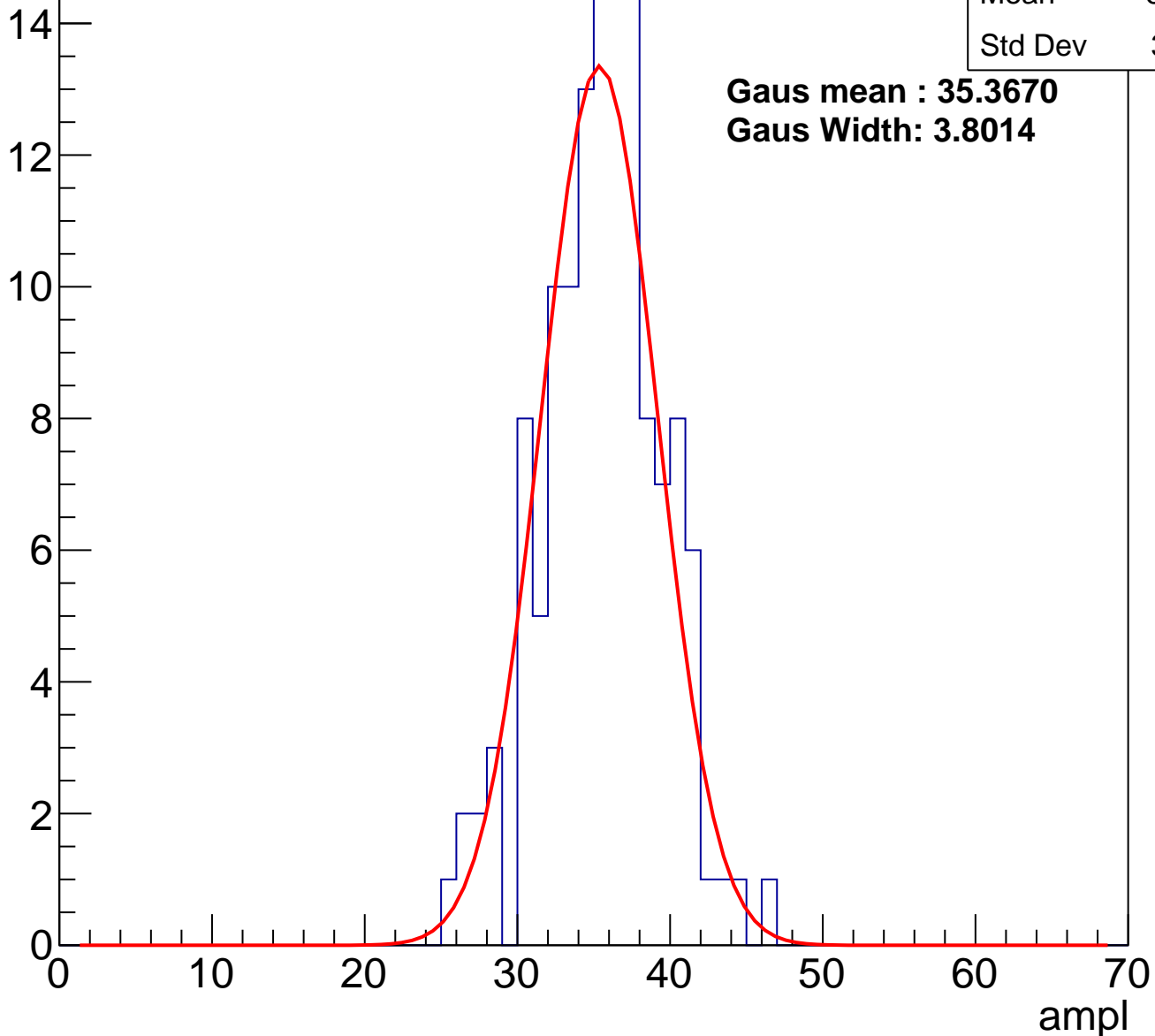
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	35.15
Std Dev	3.841

Gaus mean : 35.3670

Gaus Width: 3.8014

Entry



B0L101S, U21-ch108, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

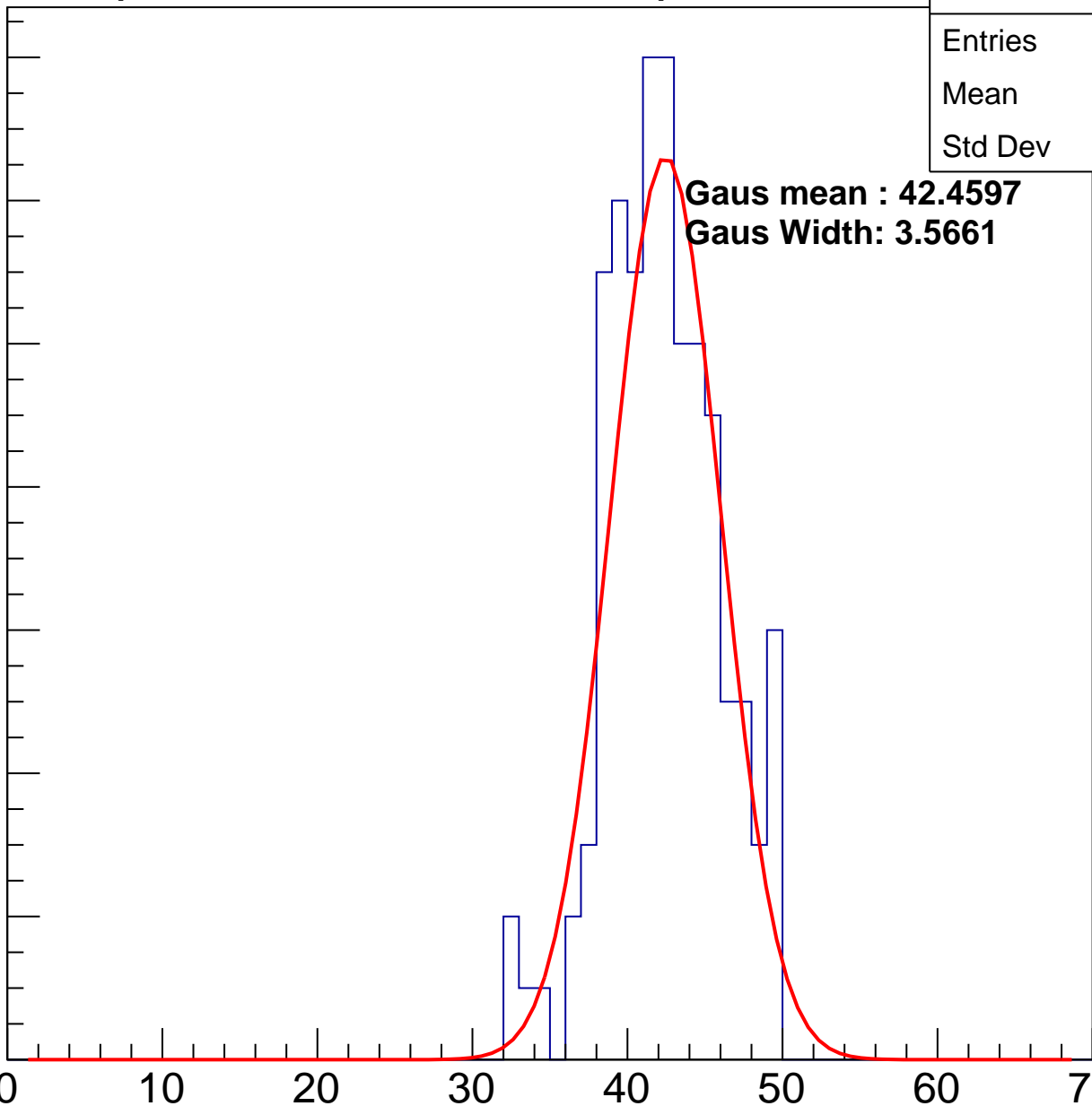
14
12
10
8
6
4
2
0

Entries	119
Mean	41.85
Std Dev	3.631

Gaus mean : 42.4597

Gaus Width: 3.5661

ampl



B0L101S, U21-ch108, adc3

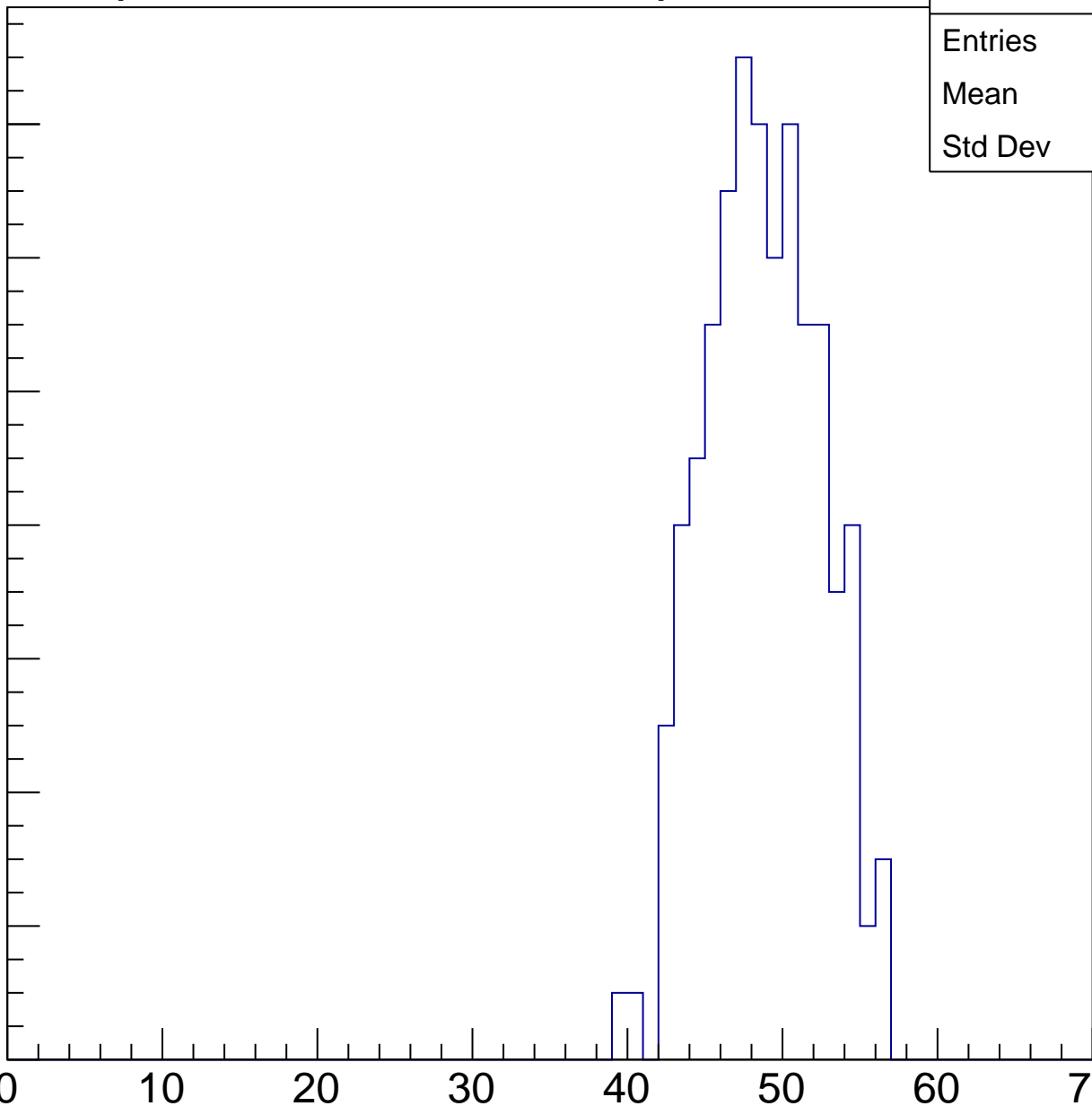
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	145
Mean	48.28
Std Dev	3.638

ampl



B0L101S, U21-ch108, adc4

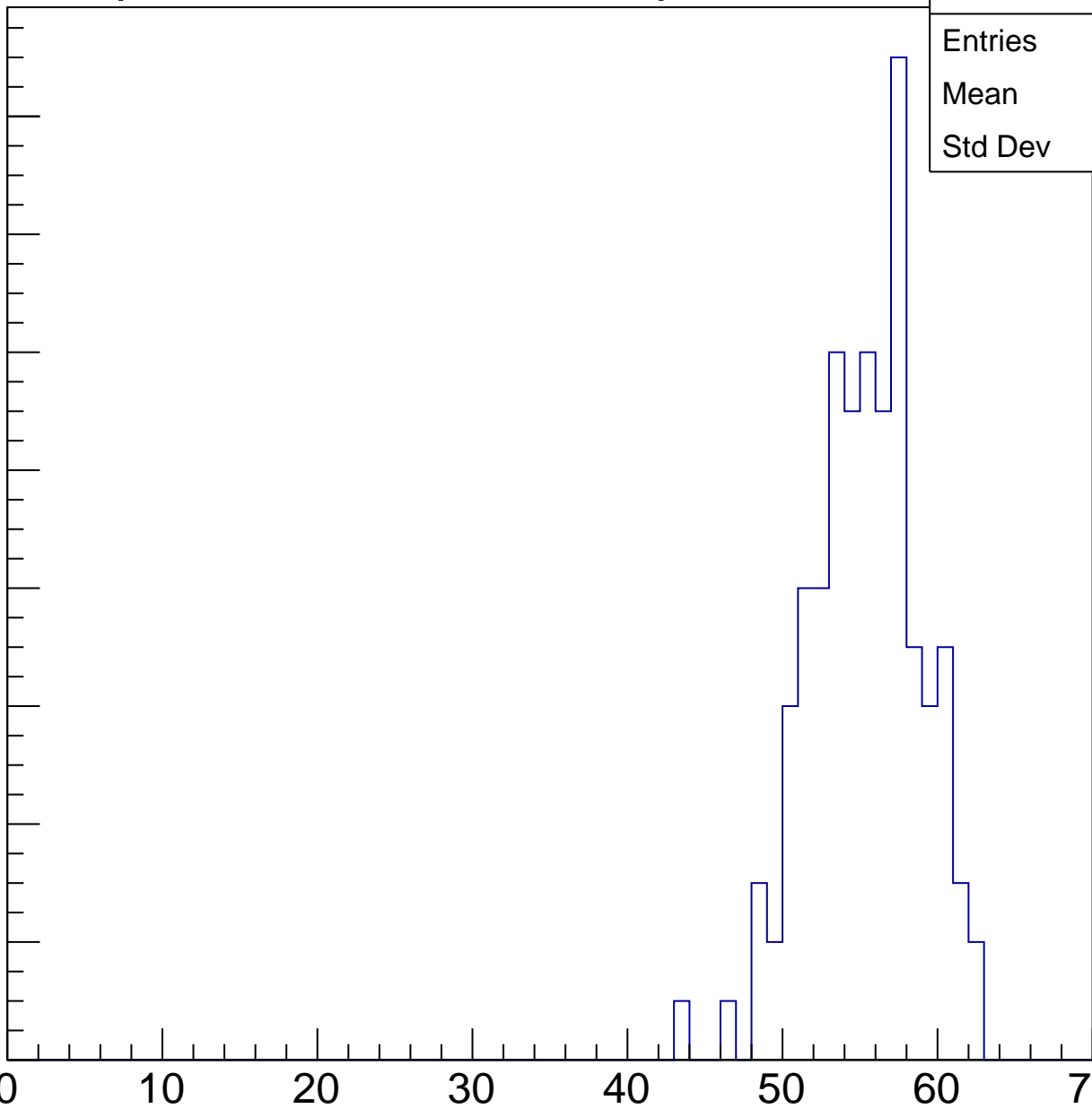
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	117
Mean	54.85
Std Dev	3.544

ampl



B0L101S, U21-ch108, adc5

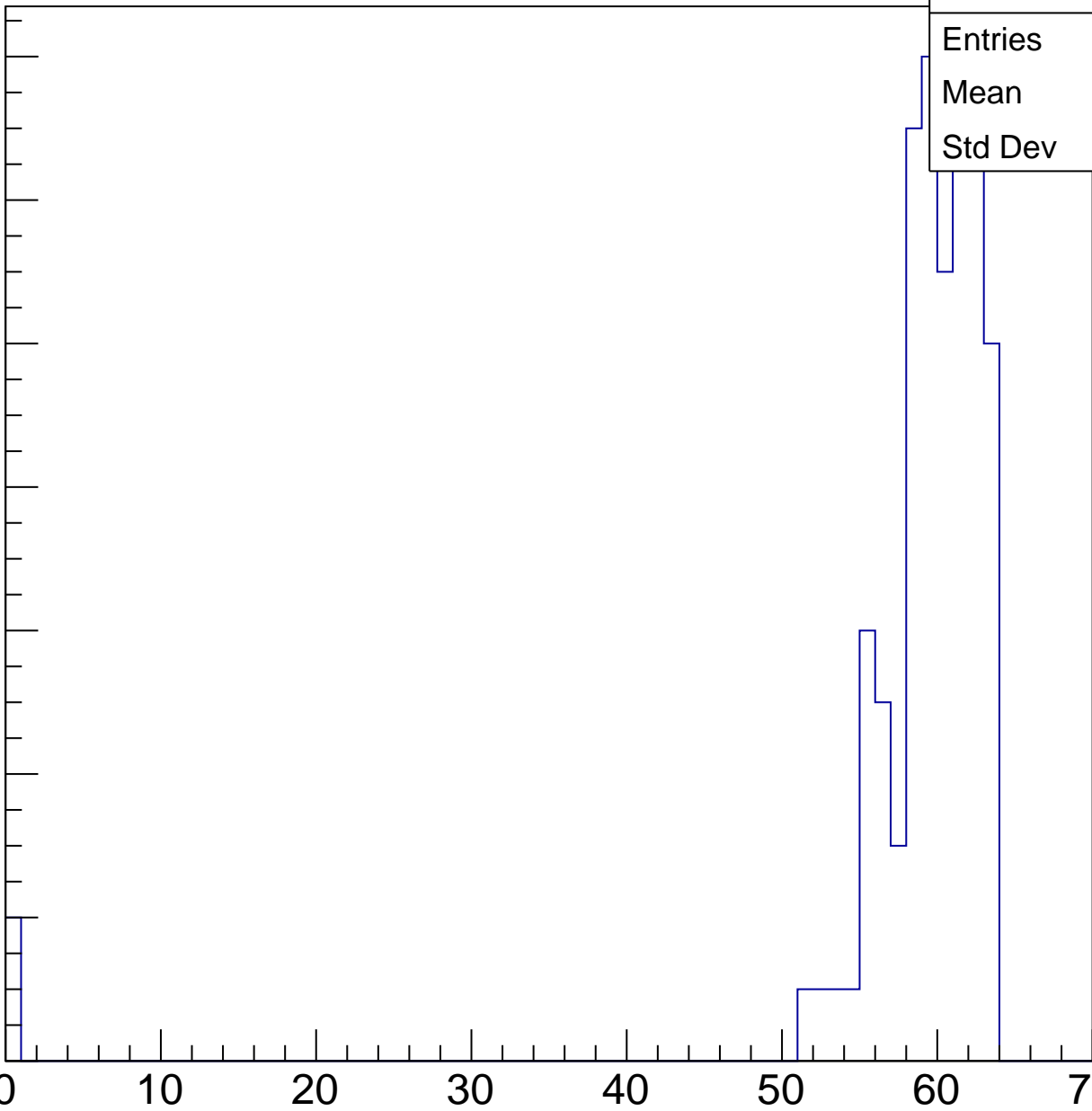
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	58.16
Std Dev	8.89

ampl

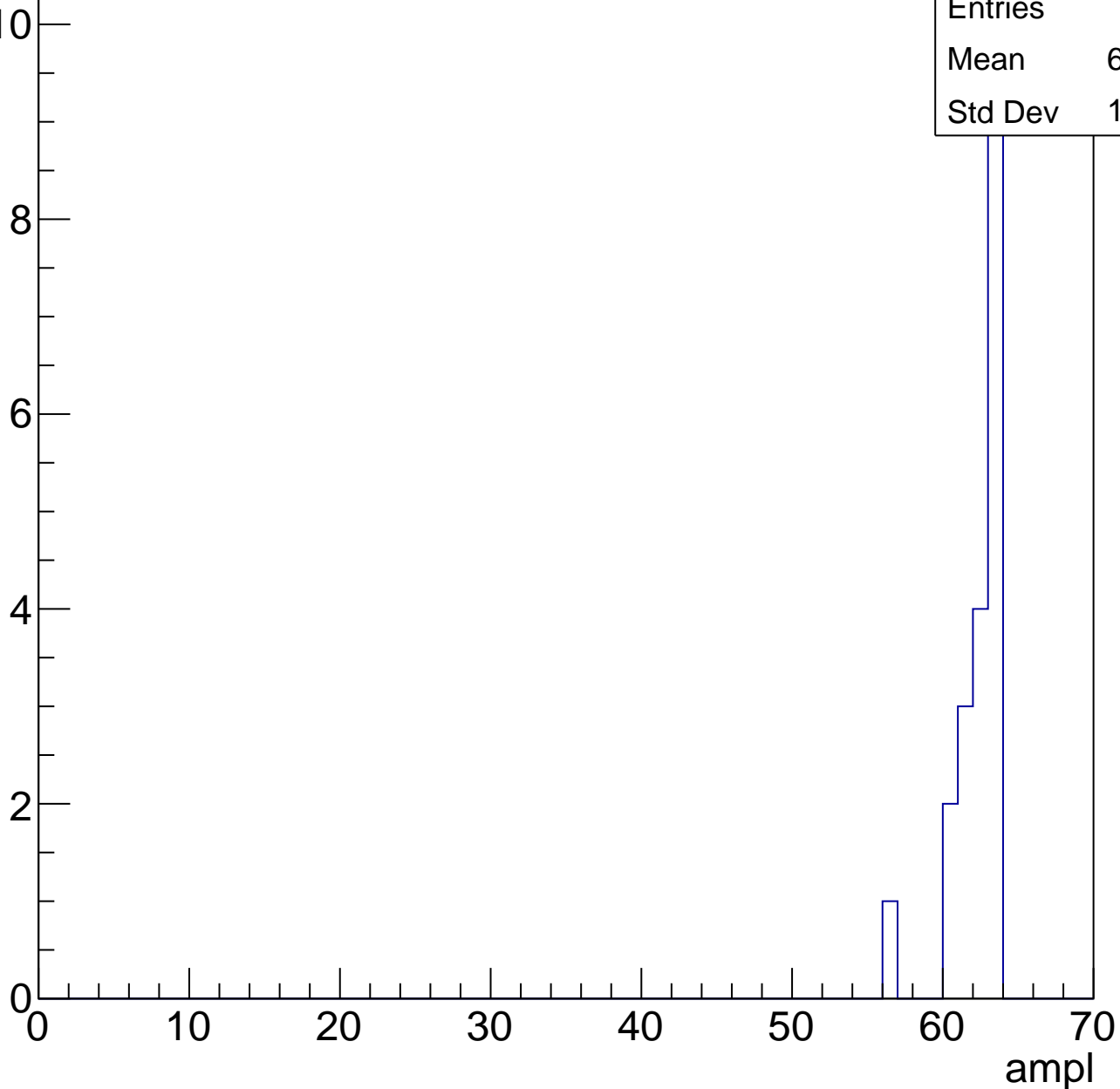


B0L101S, U21-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

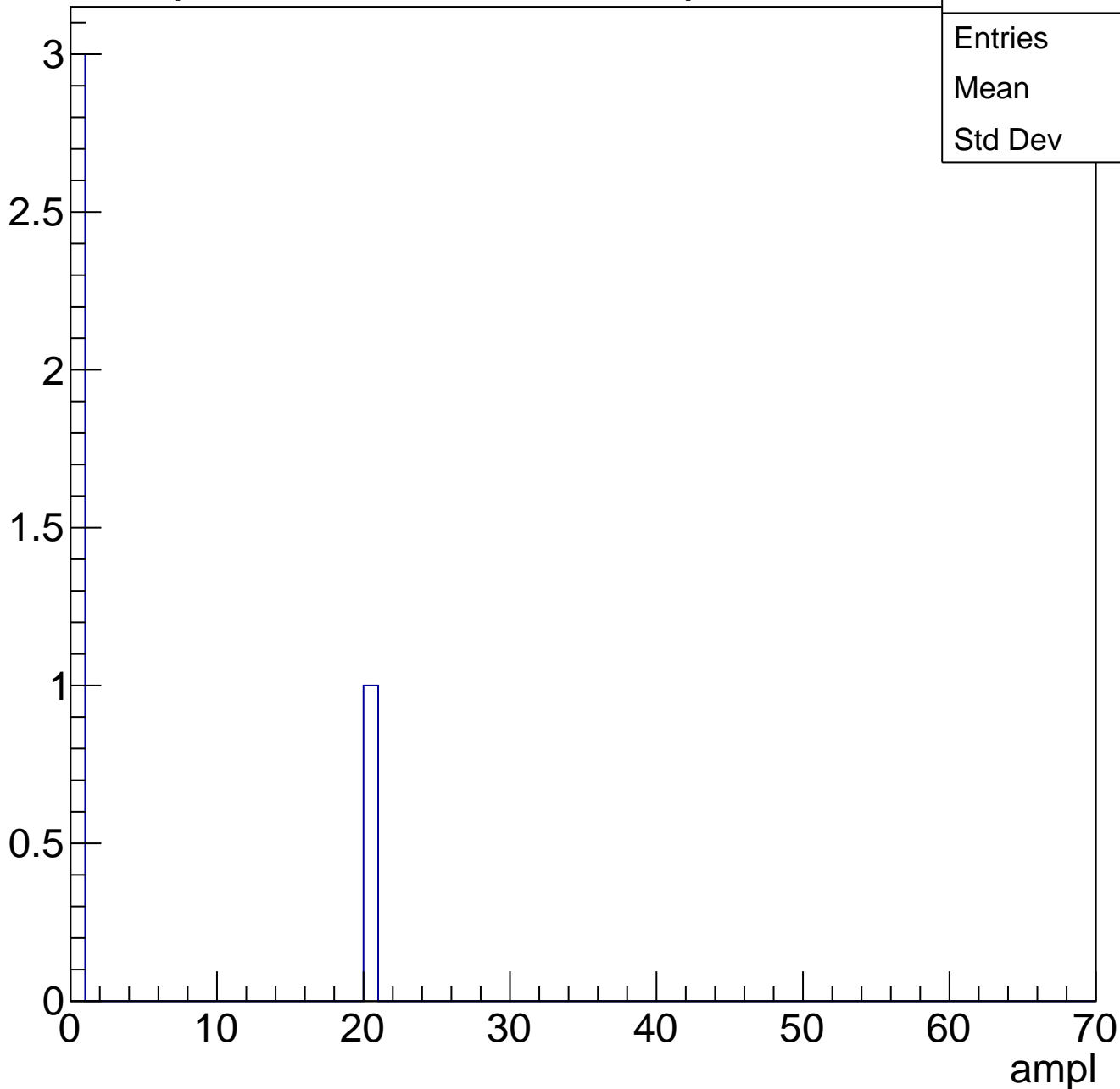
Entries	20
Mean	61.85
Std Dev	1.682



B0L101S, U21-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	5
Std Dev	8.66

B0L101S, U21-ch109, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	29.14
Std Dev	4.931

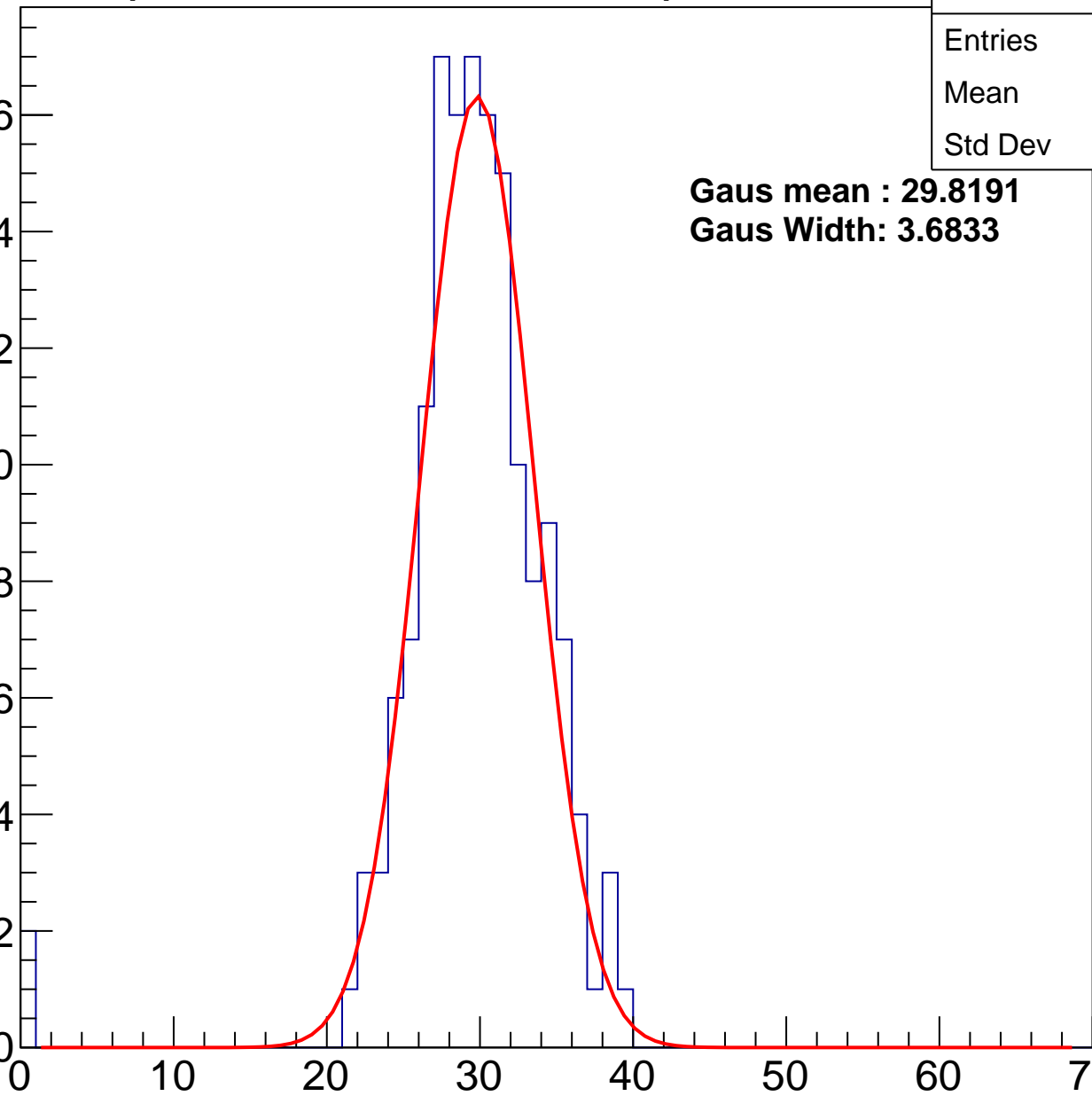
Gaus mean : 29.8191

Gaus Width: 3.6833

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch109, adc1

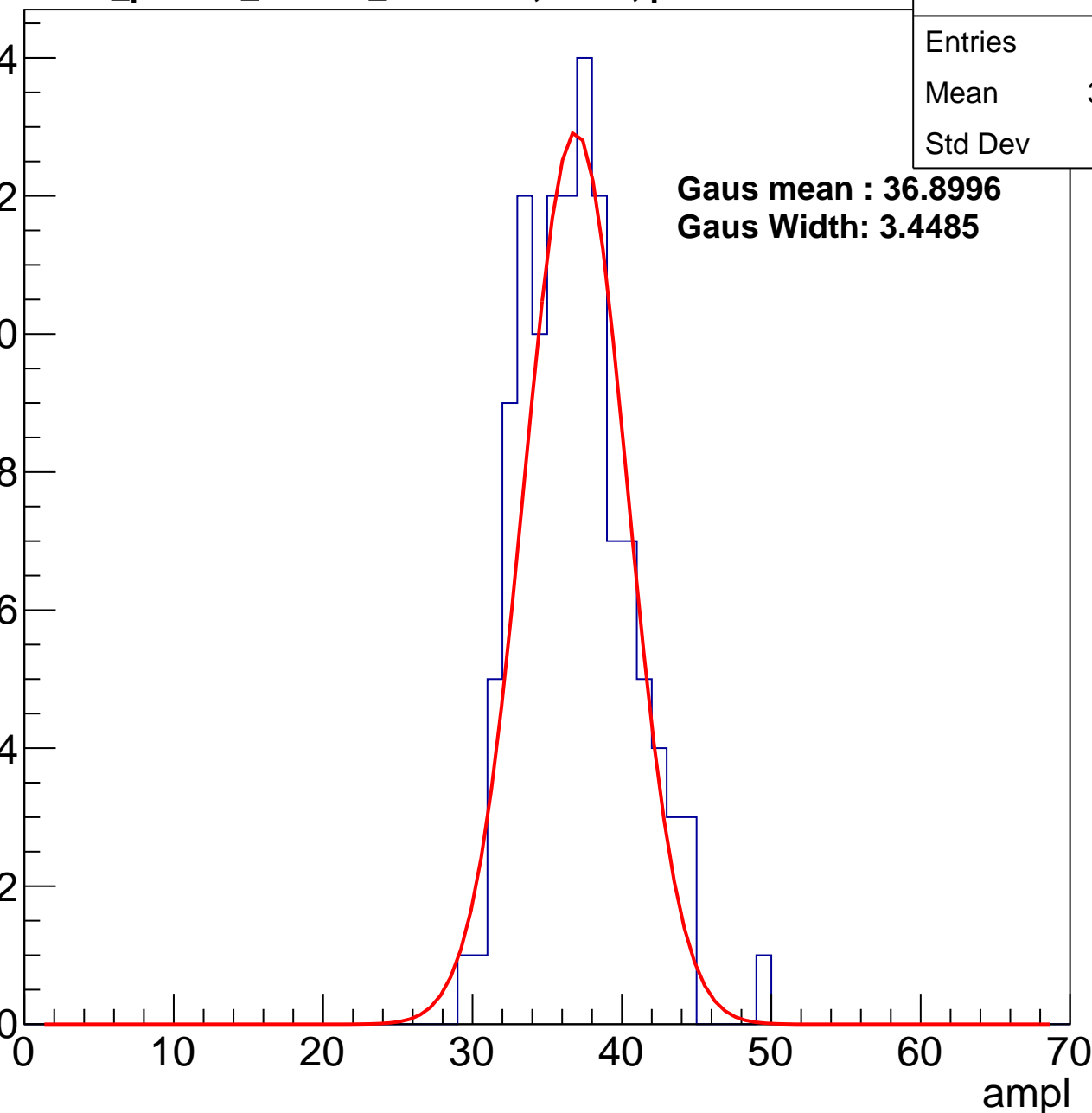
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	118
Mean	36.44
Std Dev	3.571

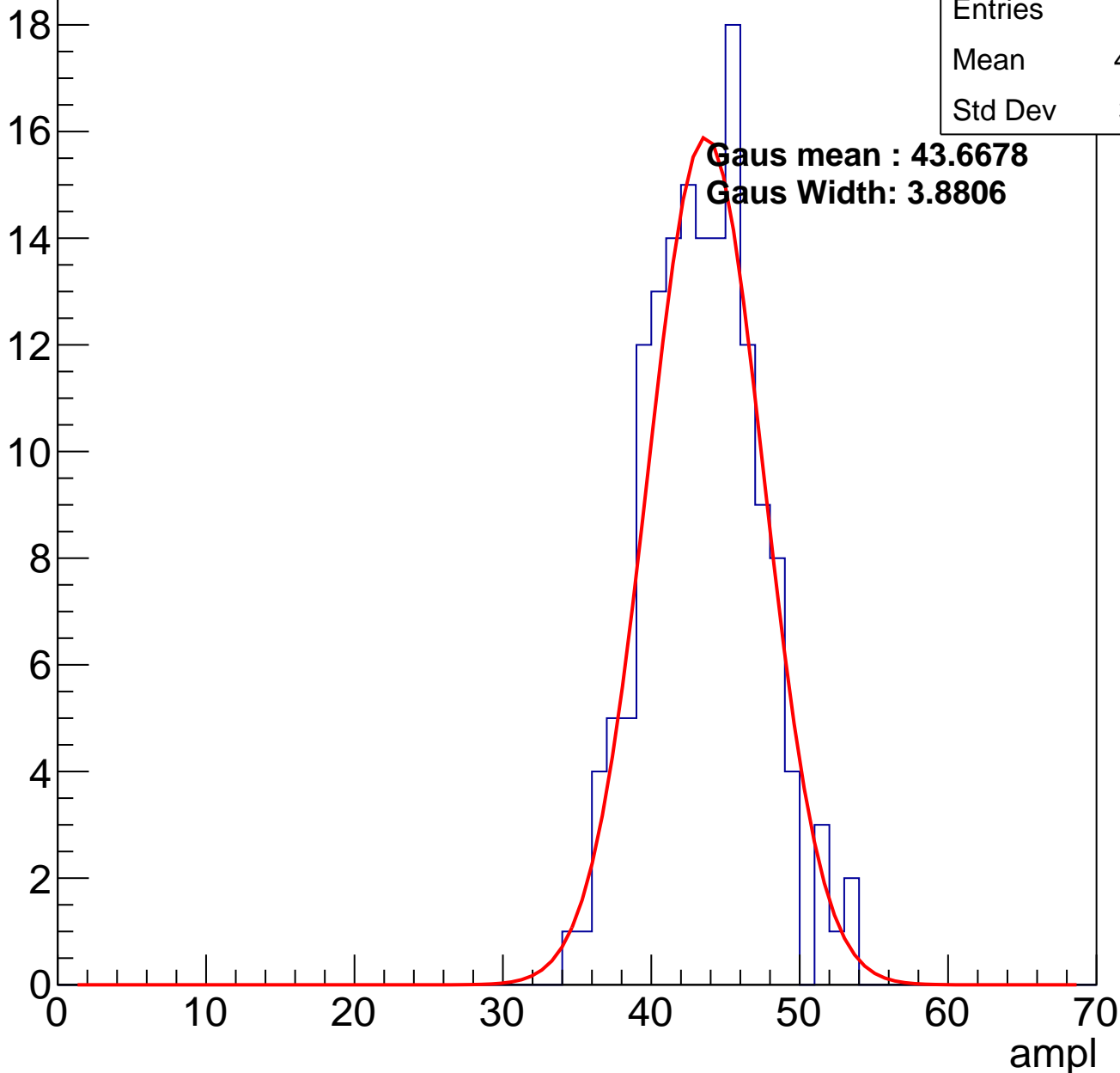
Gaus mean : 36.8996
Gaus Width: 3.4485



B0L101S, U21-ch109, adc2

calib_packv5_042523_0143.root, FC#1, port C1

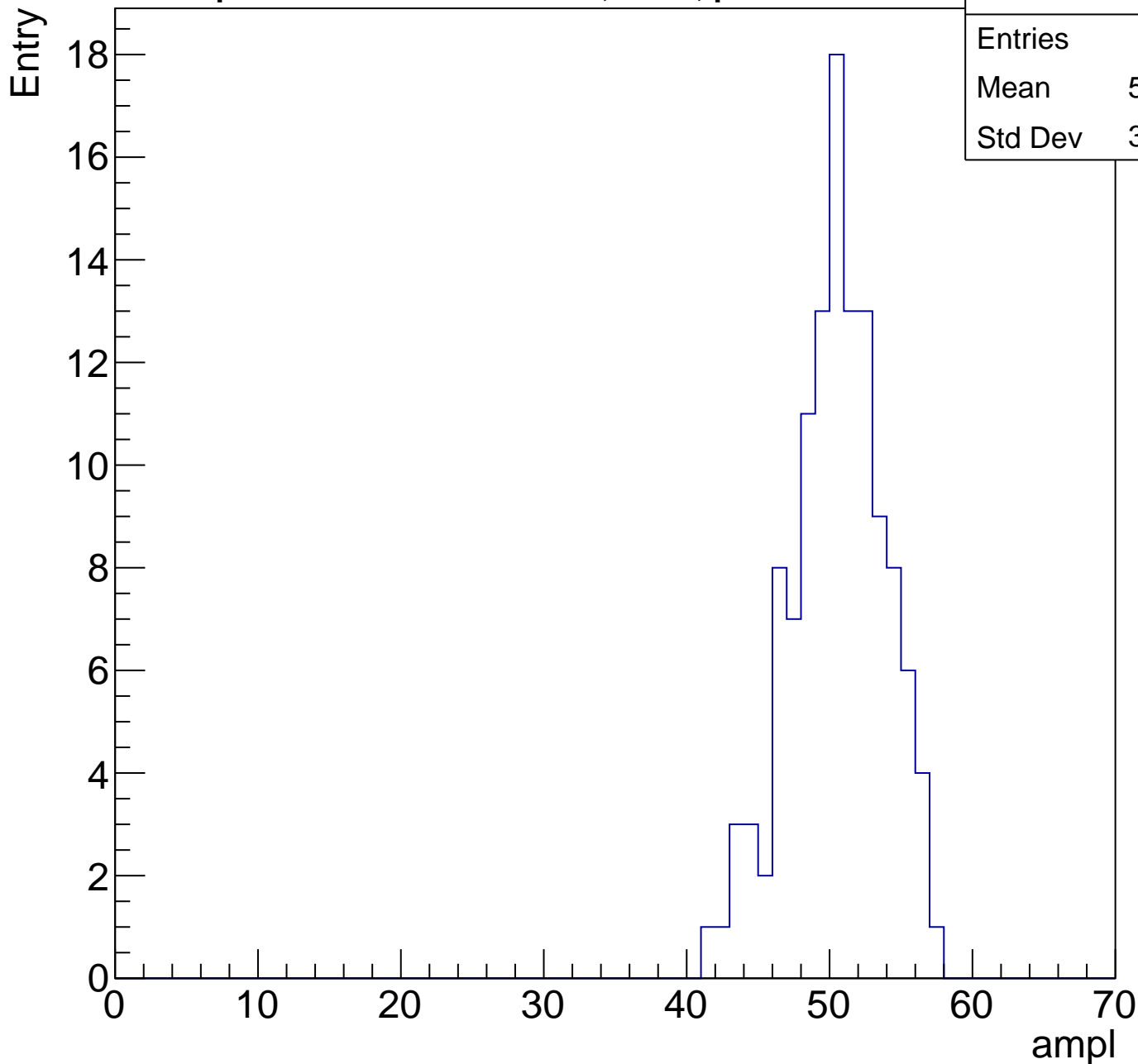
Entry



B0L101S, U21-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	50.04
Std Dev	3.296

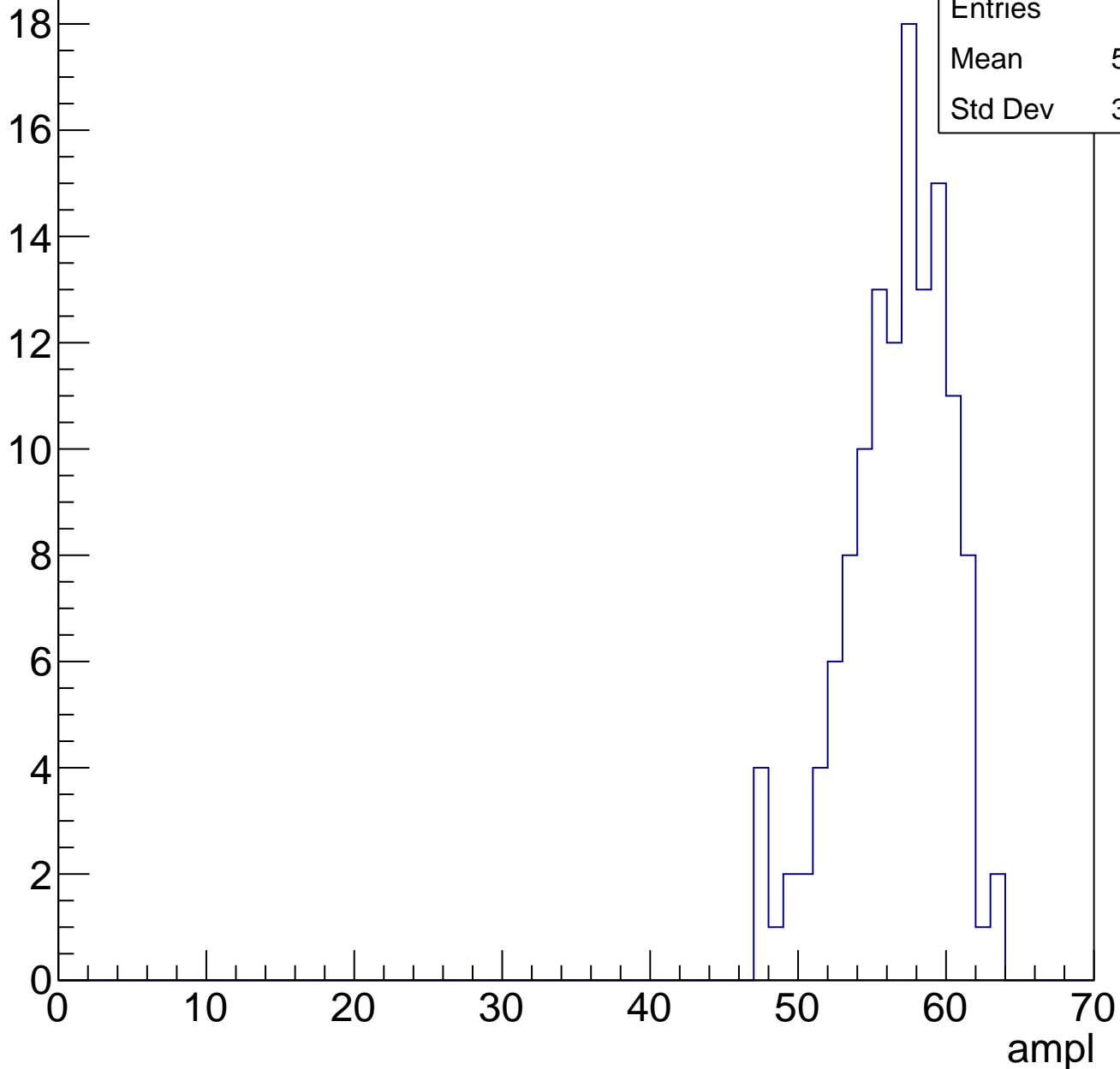


B0L101S, U21-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	56.17
Std Dev	3.498

Entry



B0L101S, U21-ch109, adc5

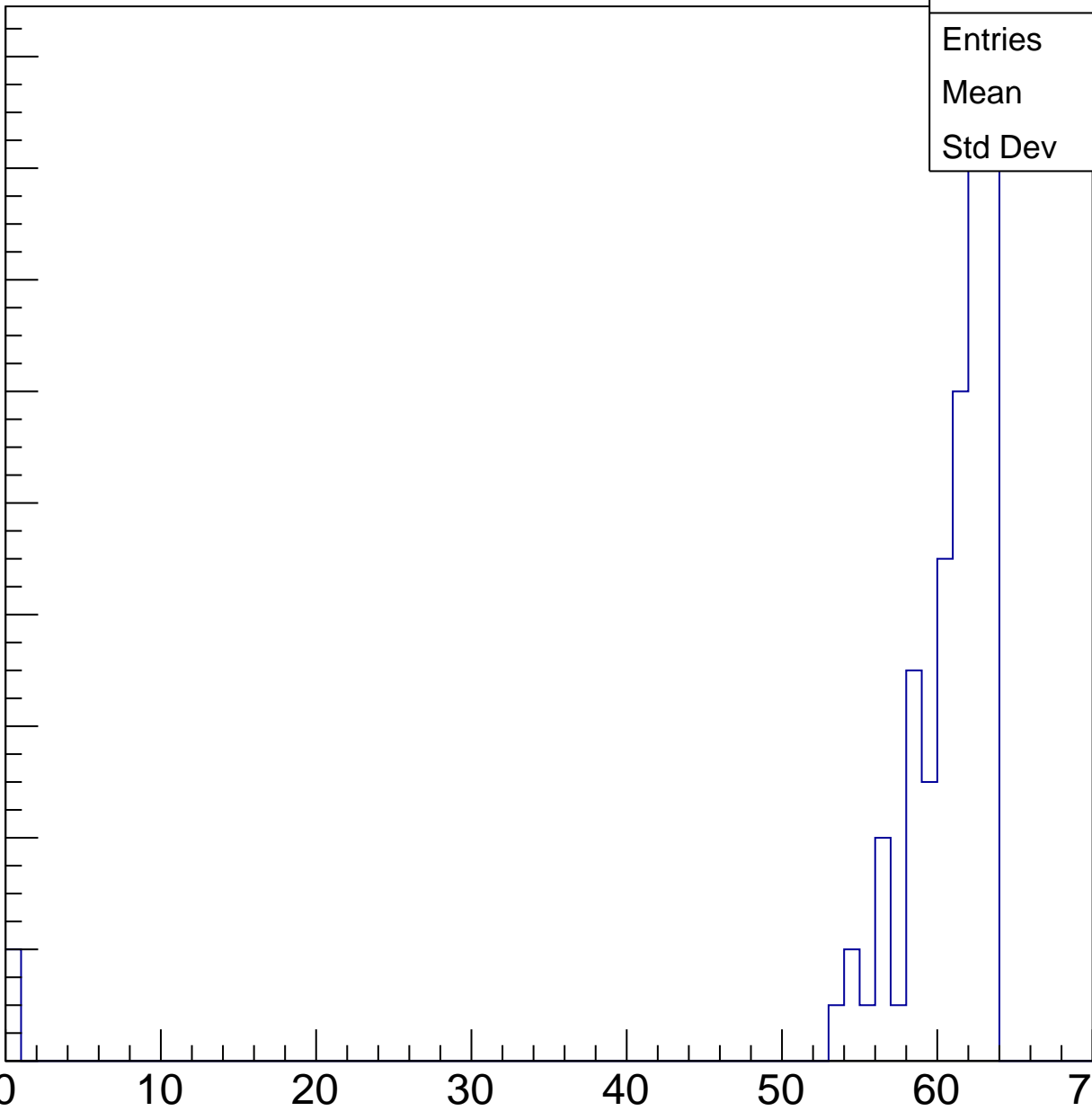
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	79
Mean	58.95
Std Dev	9.809

ampl



B0L101S, U21-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch110, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	27.47
Std Dev	4.992

Gaus mean : 28.1957

Gaus Width: 3.6676

Entry

16

14

12

10

8

6

4

2

0

0

10

20

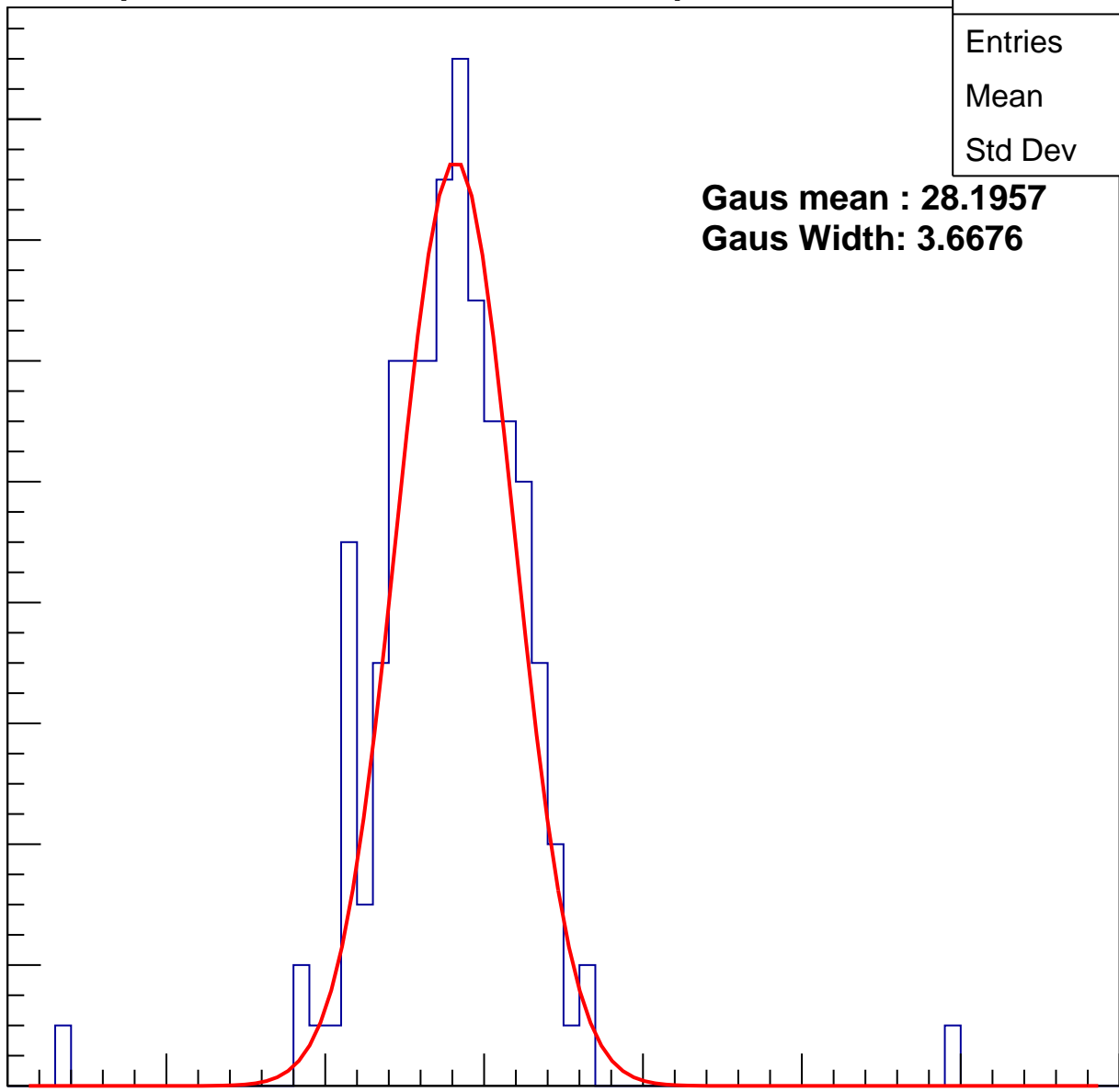
30

40

50

60

ampl



B0L101S, U21-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

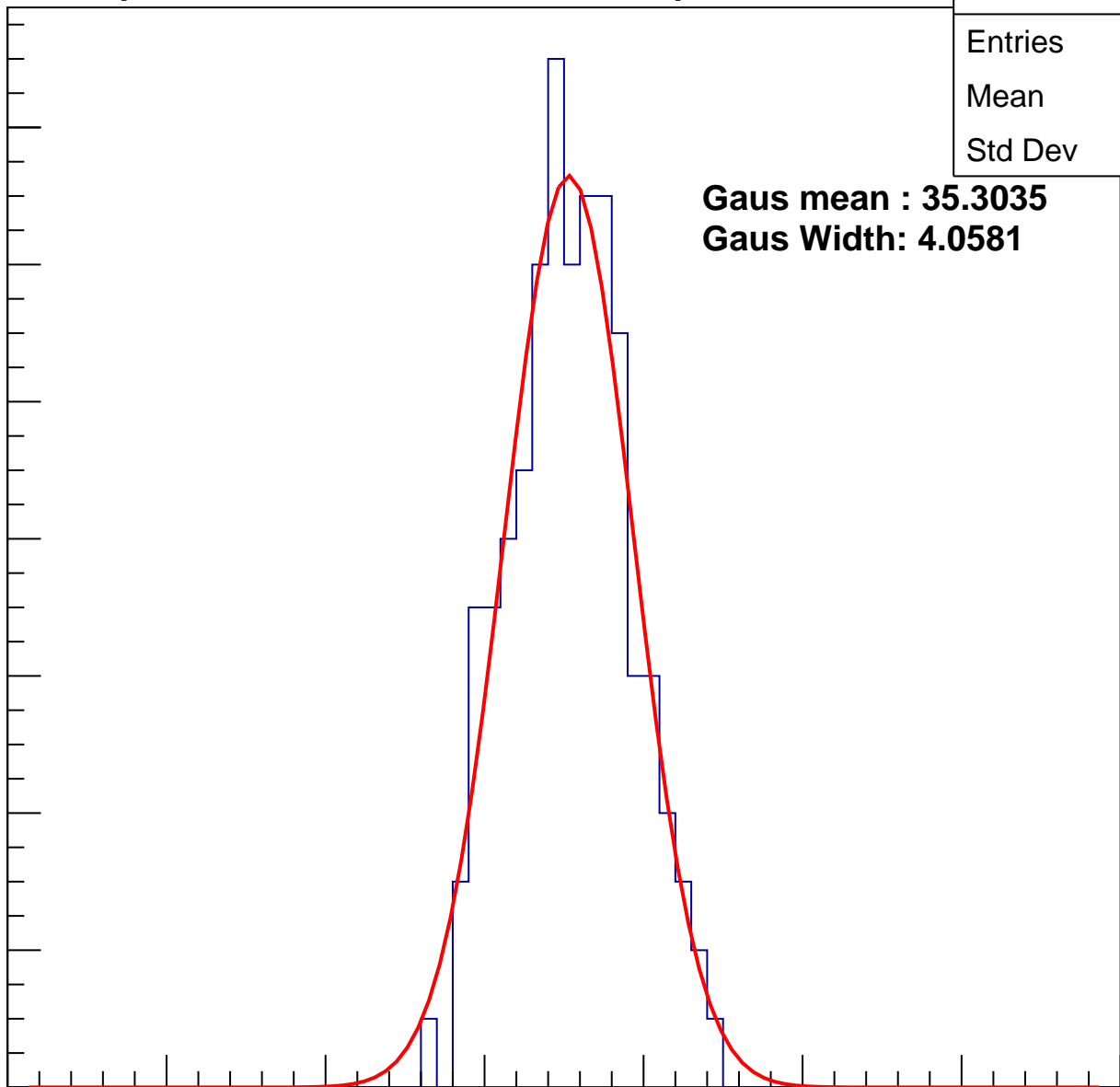
70

ampl

Entries	133
Mean	34.93
Std Dev	3.716

Gaus mean : 35.3035

Gaus Width: 4.0581



B0L101S, U21-ch110, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	40.84
Std Dev	5.284

Gaus mean : 41.8409

Gaus Width: 3.7813

Entry

12

10

8

6

4

2

0

0

10

20

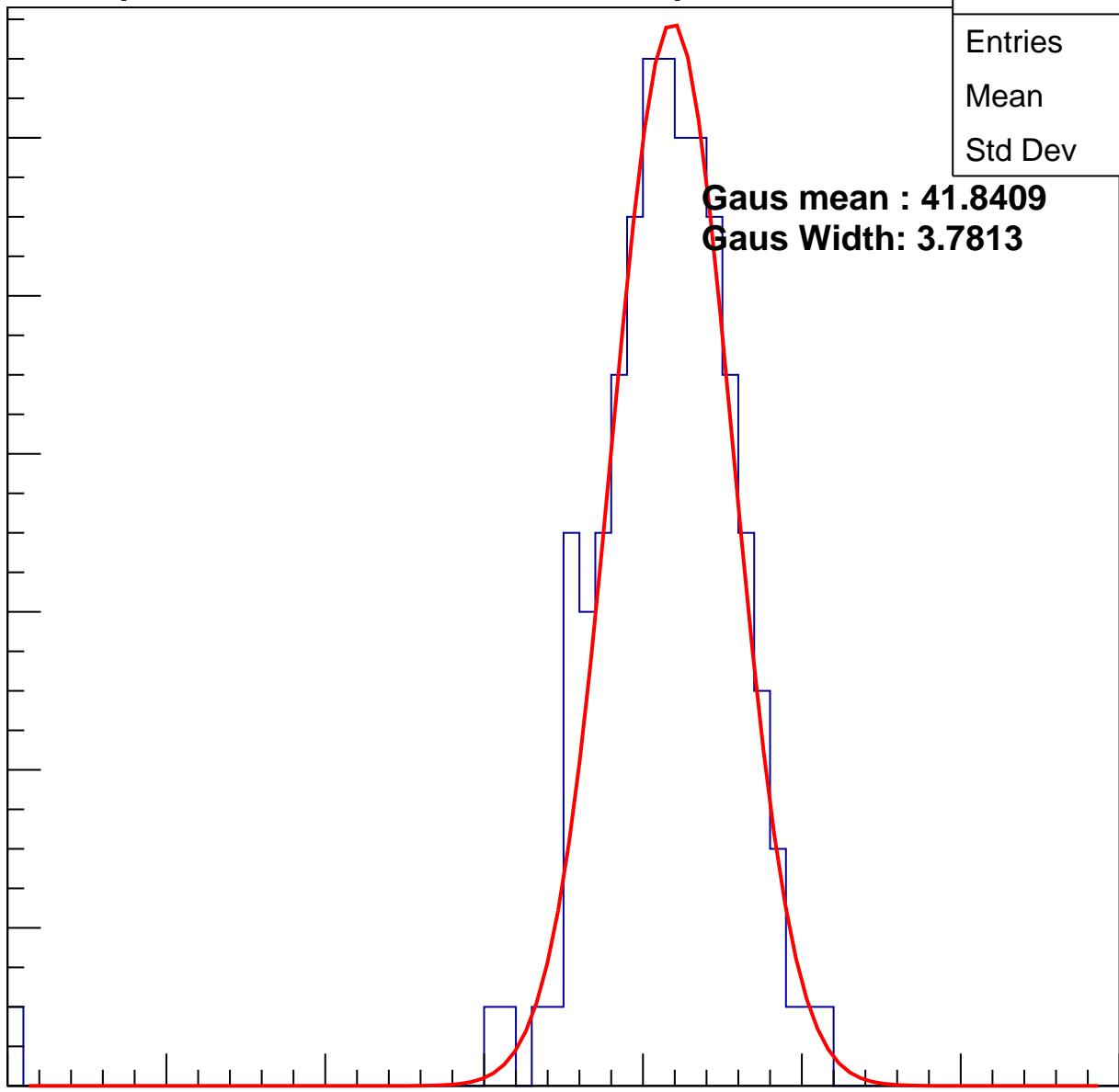
30

40

50

60

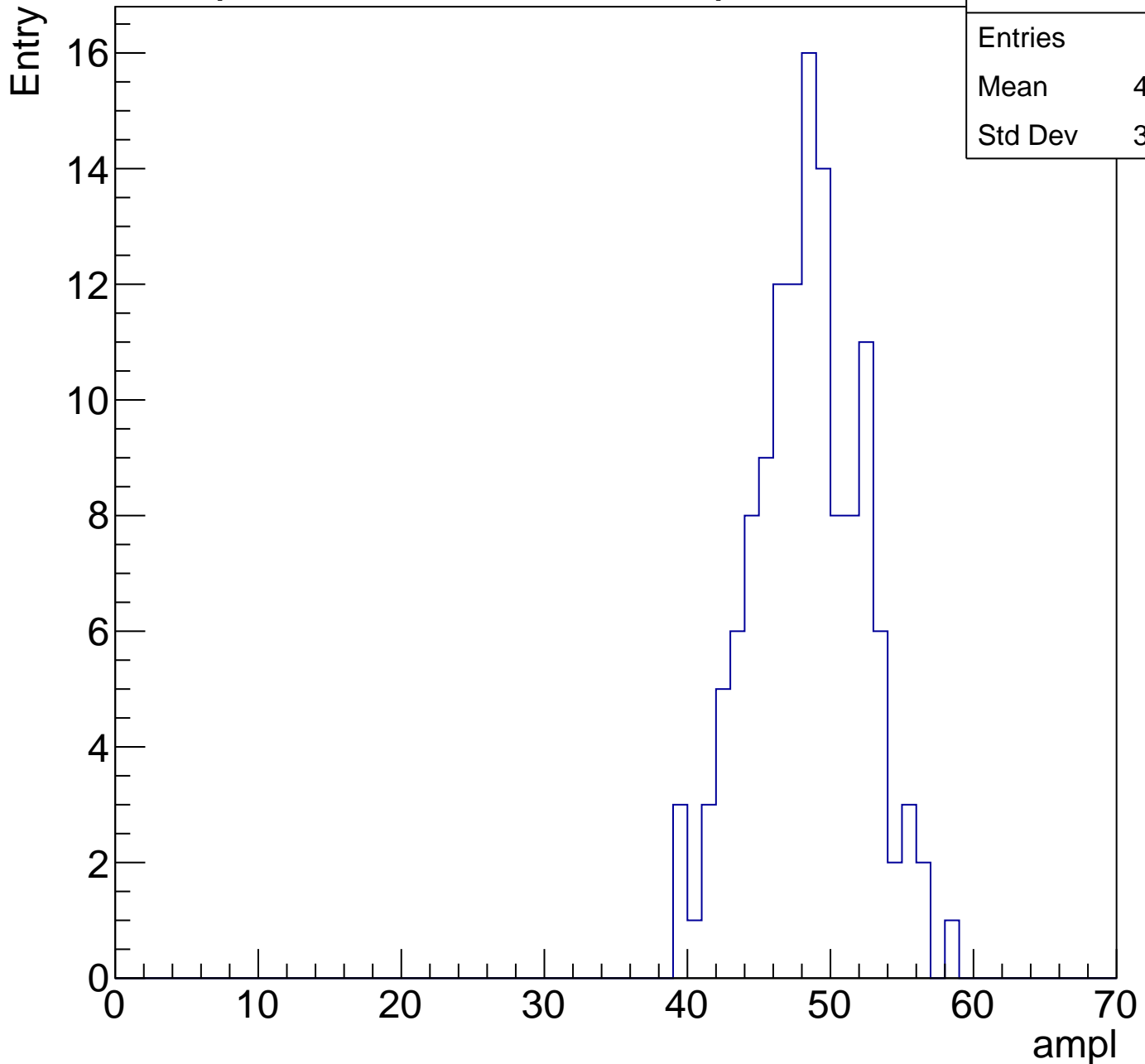
ampl



B0L101S, U21-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	47.82
Std Dev	3.863



B0L101S, U21-ch110, adc4

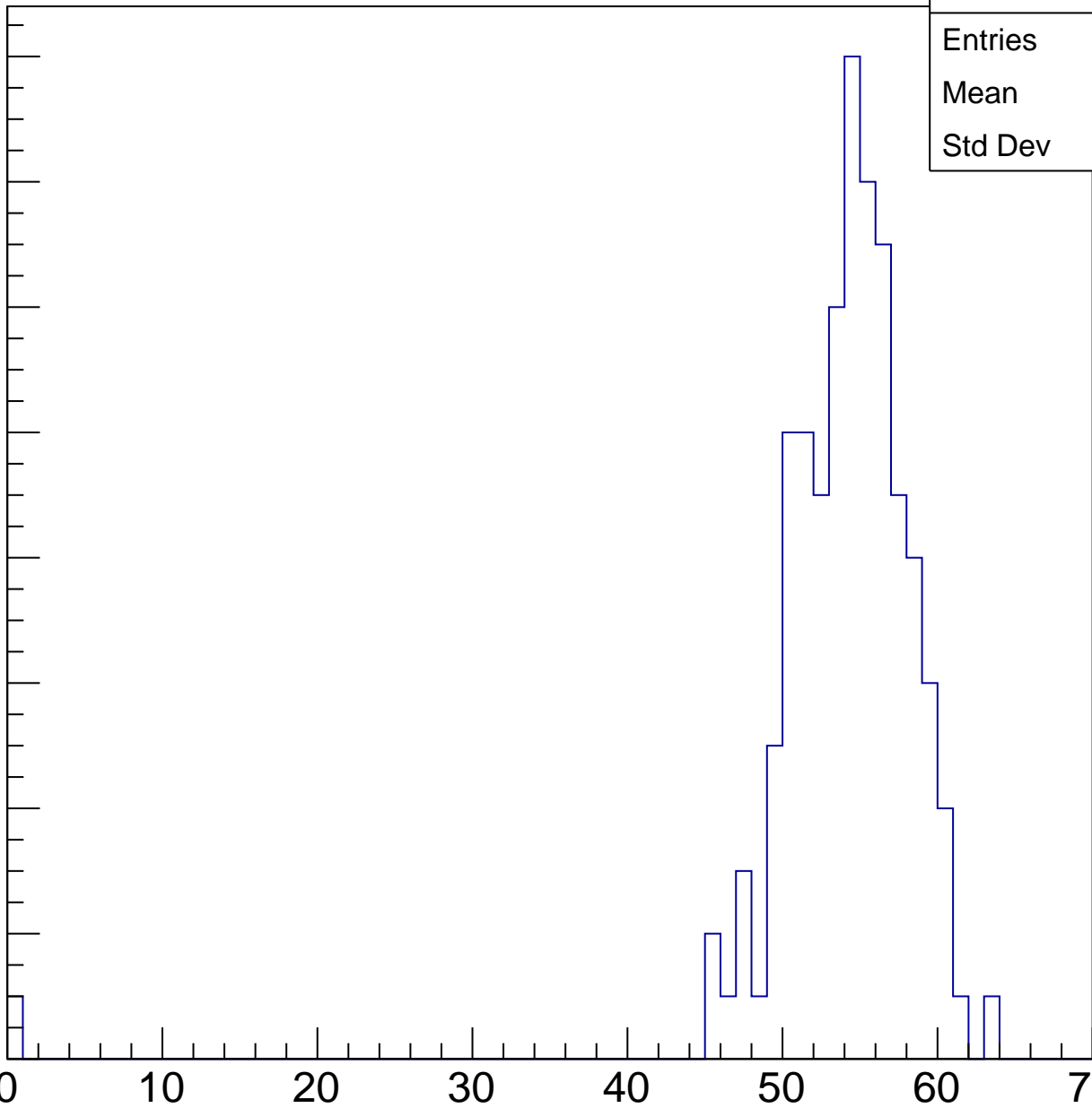
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	53.5
Std Dev	5.92

ampl



B0L101S, U21-ch110, adc5

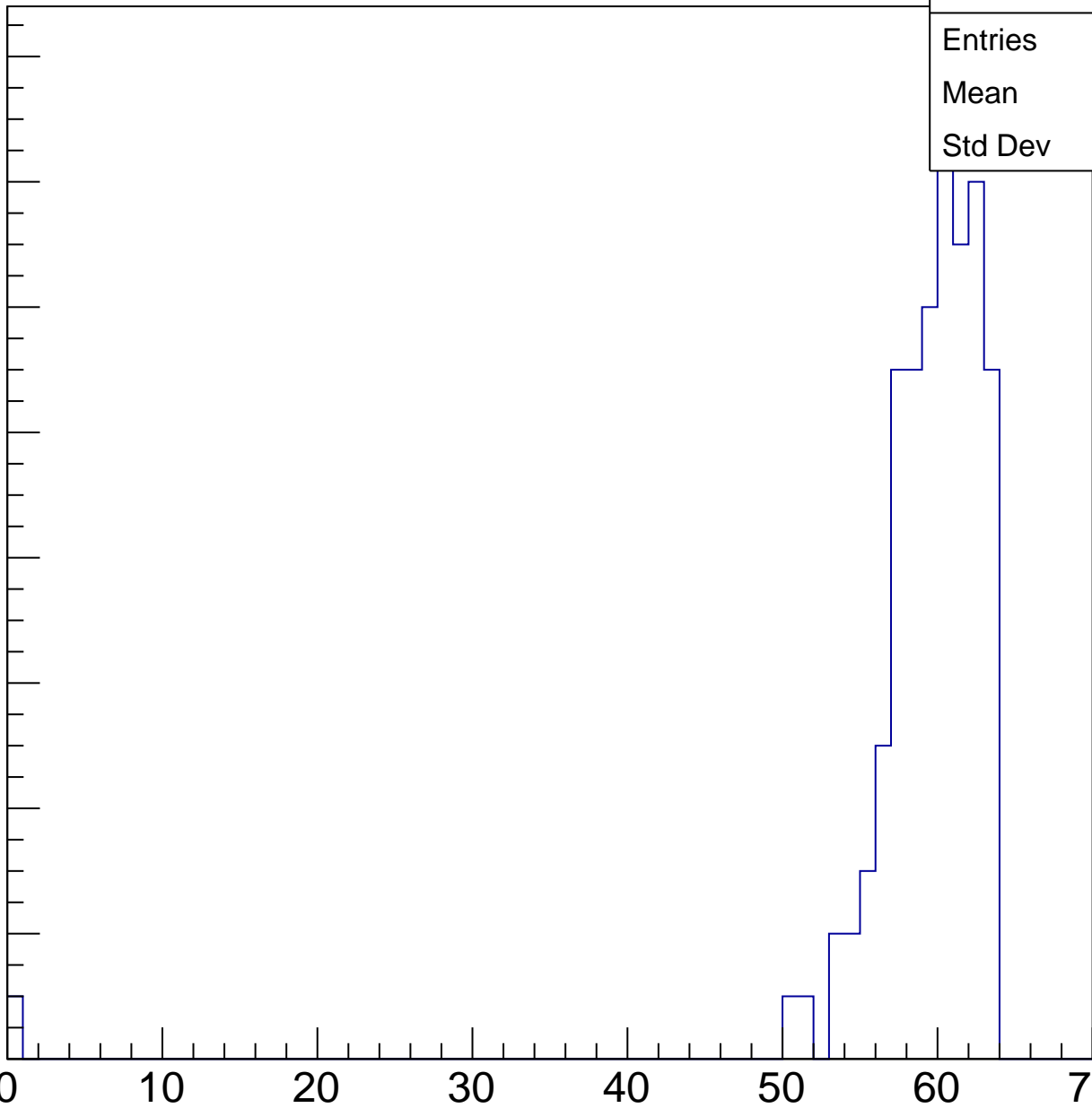
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.71
Std Dev	6.429

ampl

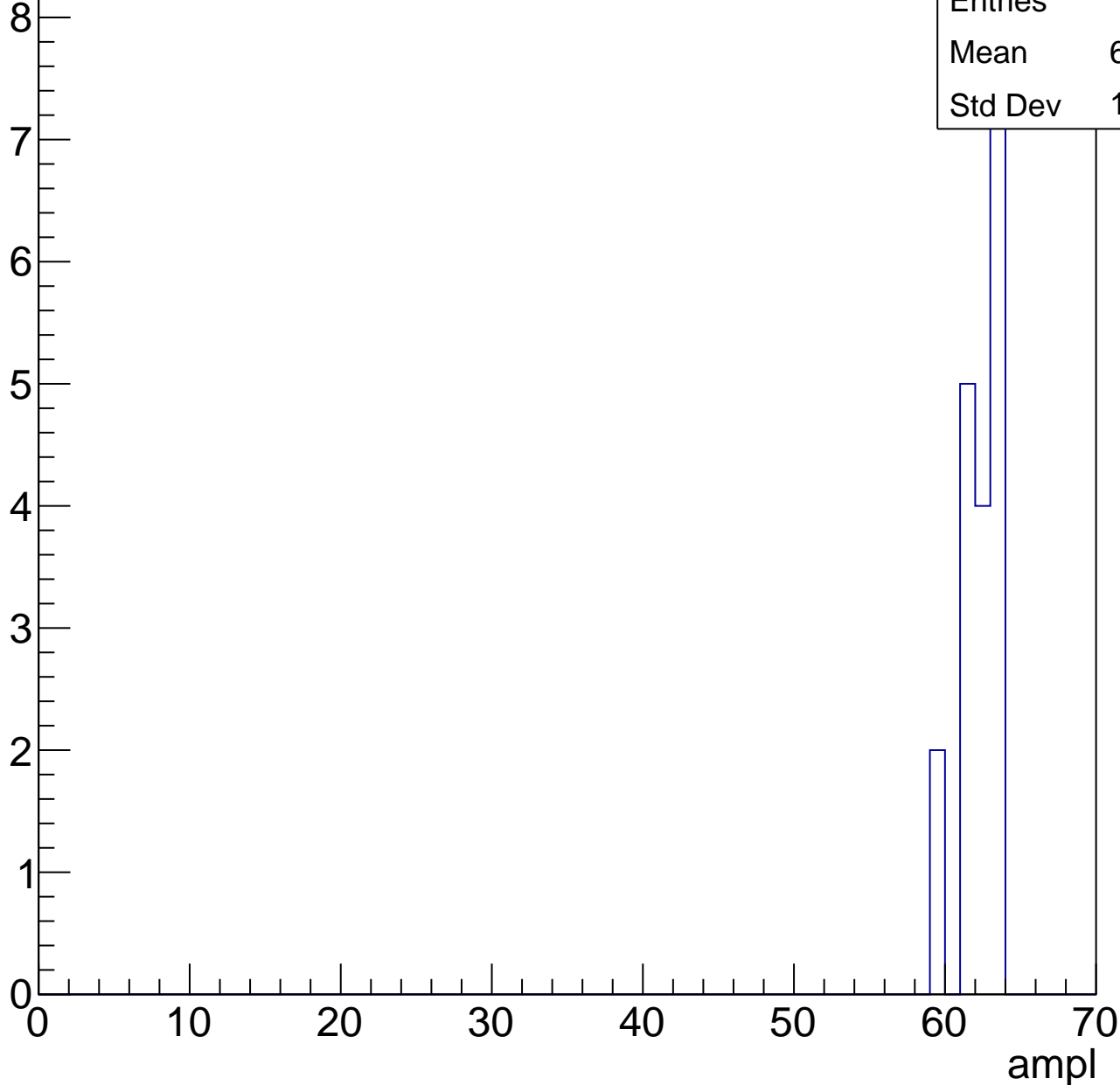


B0L101S, U21-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

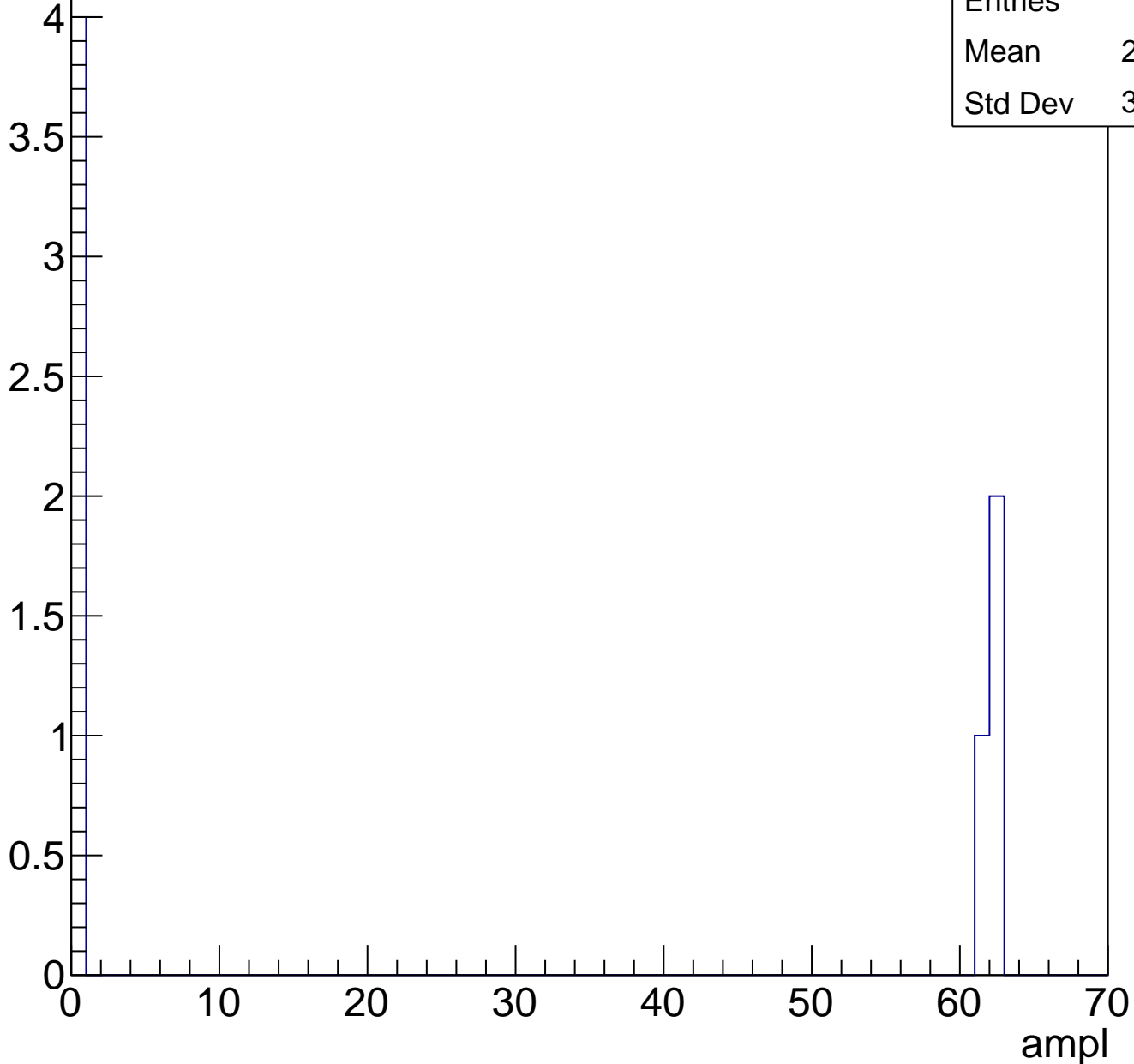
Entries	19
Mean	61.84
Std Dev	1.268



B0L101S, U21-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	7
Mean	26.43
Std Dev	30.52

B0L101S, U21-ch111, adc0

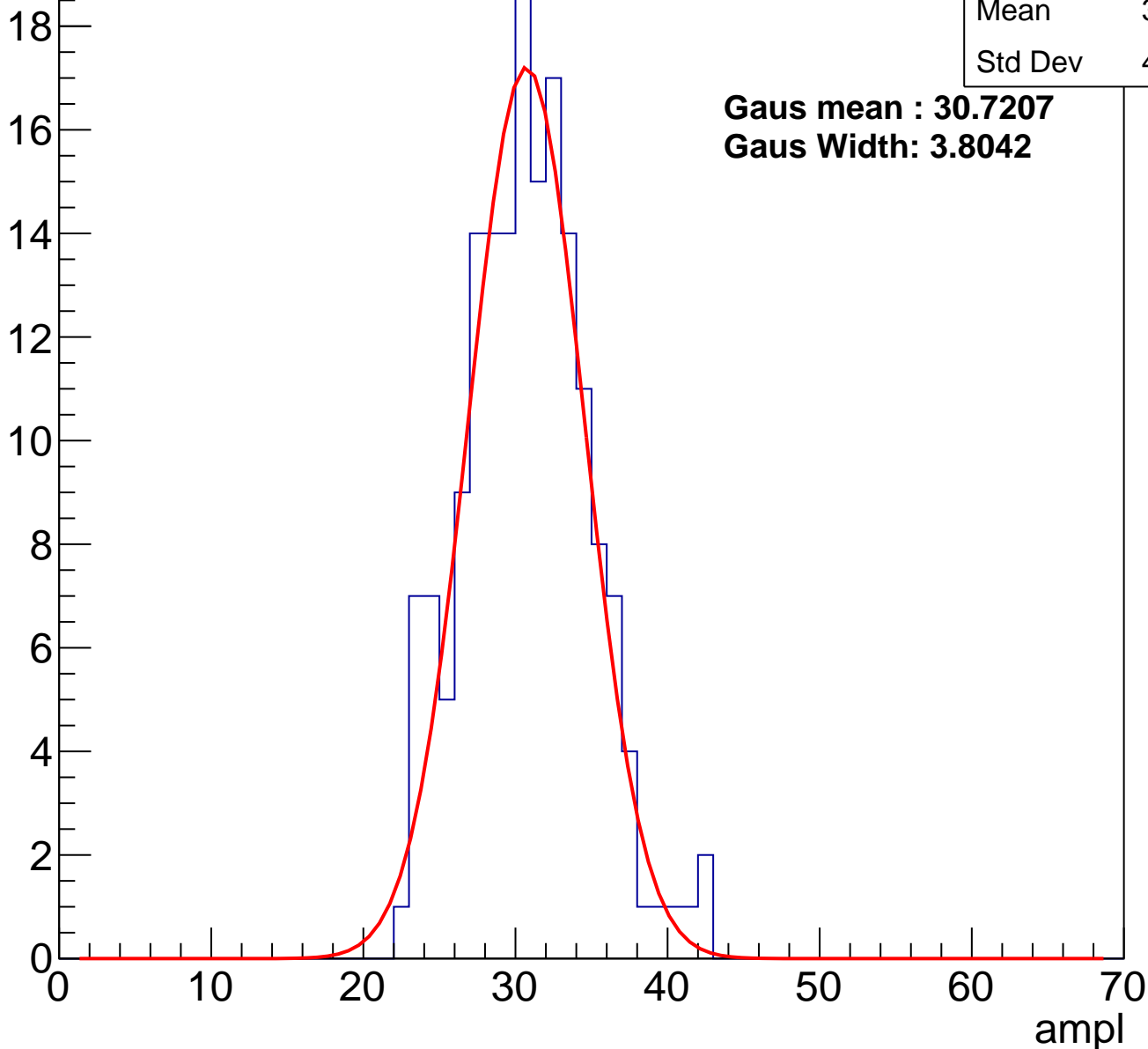
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	30.37
Std Dev	4.026

Gaus mean : 30.7207

Gaus Width: 3.8042

Entry



B0L101S, U21-ch111, adc1

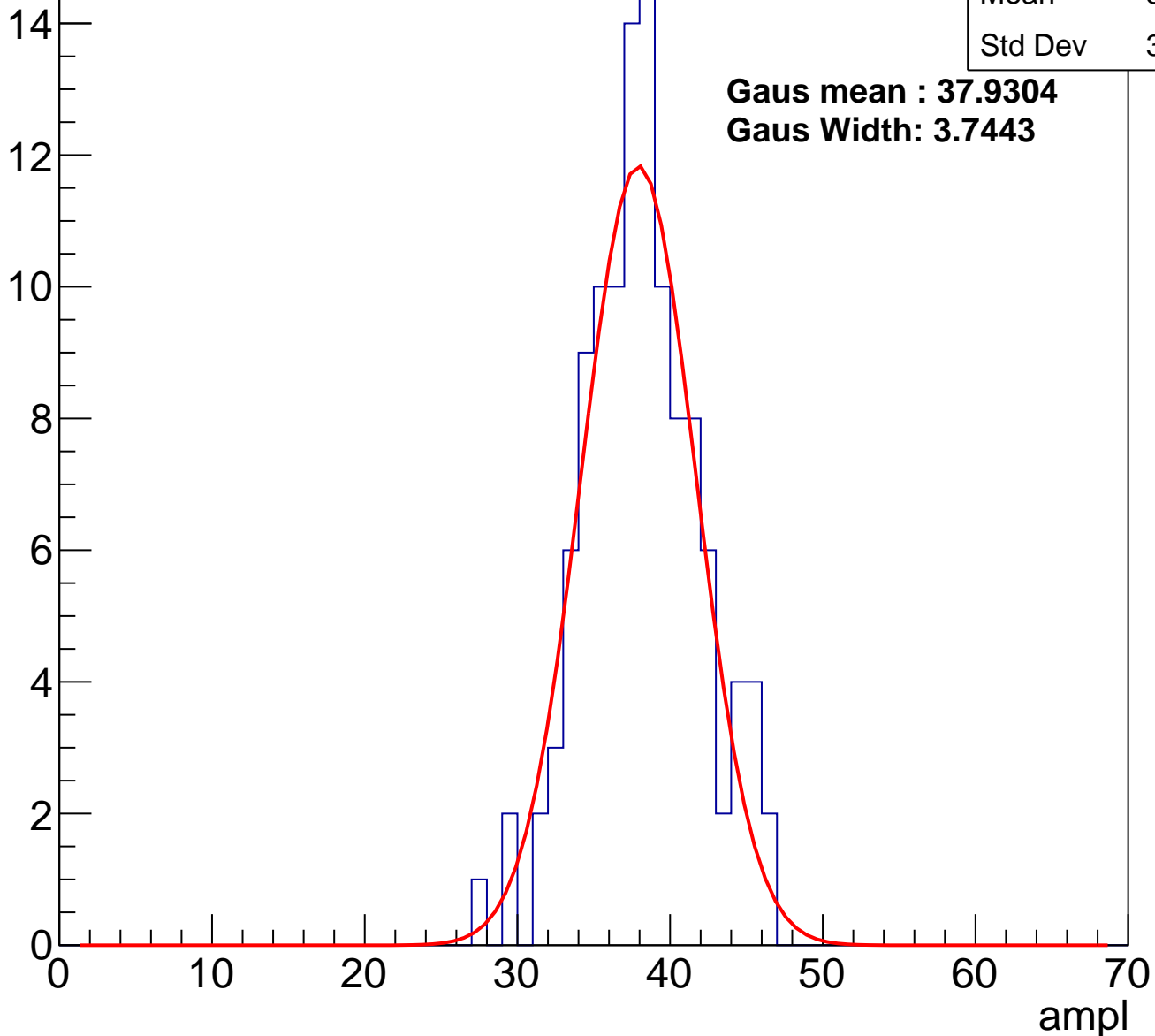
calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	37.66
Std Dev	3.762

Gaus mean : 37.9304

Gaus Width: 3.7443

Entry



B0L101S, U21-ch111, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	43.27
Std Dev	3.621

Gaus mean : 43.6789

Gaus Width: 3.5889

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

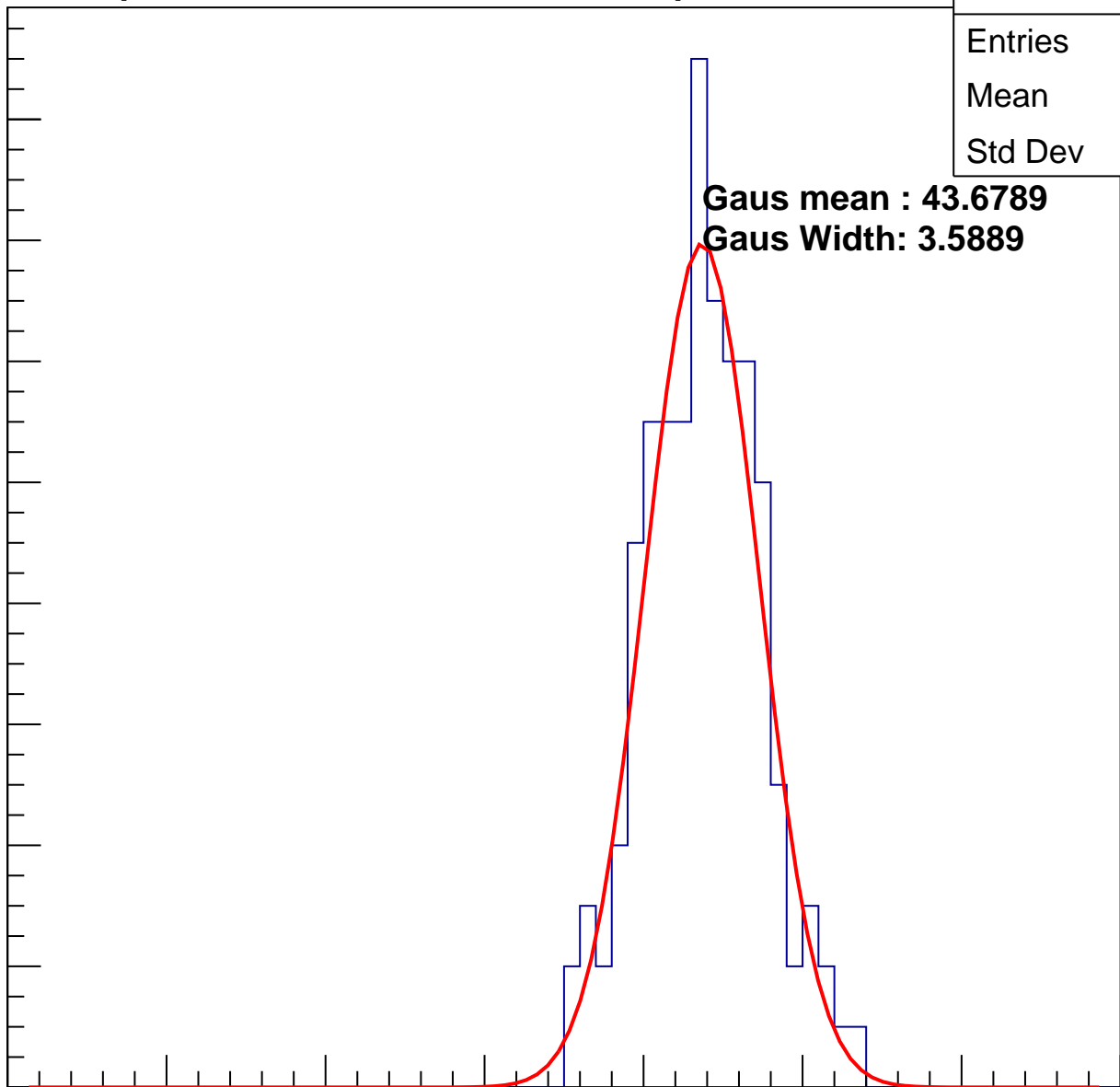
40

50

60

70

ampl



B0L101S, U21-ch111, adc3

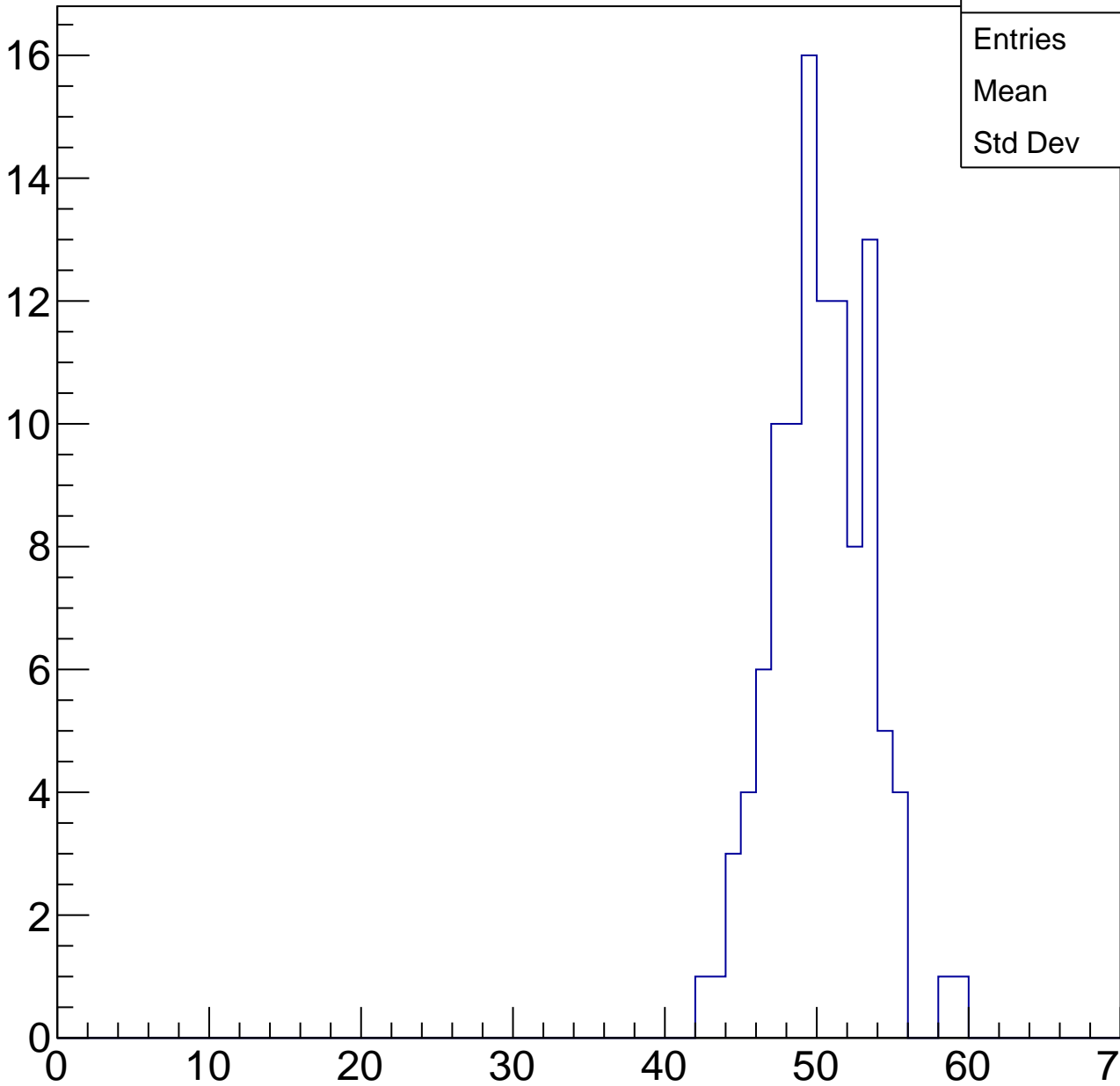
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	49.82
Std Dev	3.126

ampl



B0L101S, U21-ch111, adc4

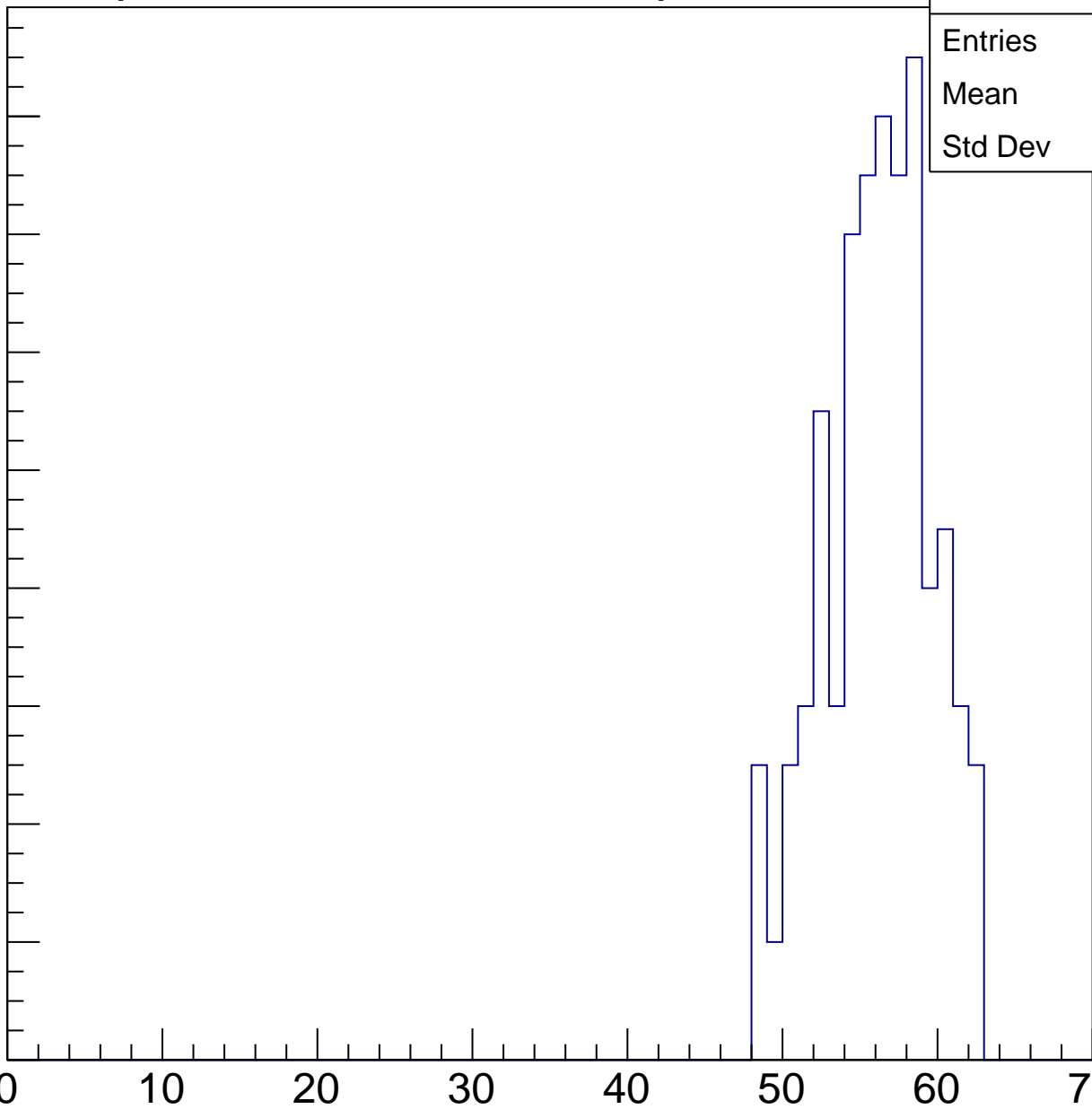
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	140
Mean	55.64
Std Dev	3.444

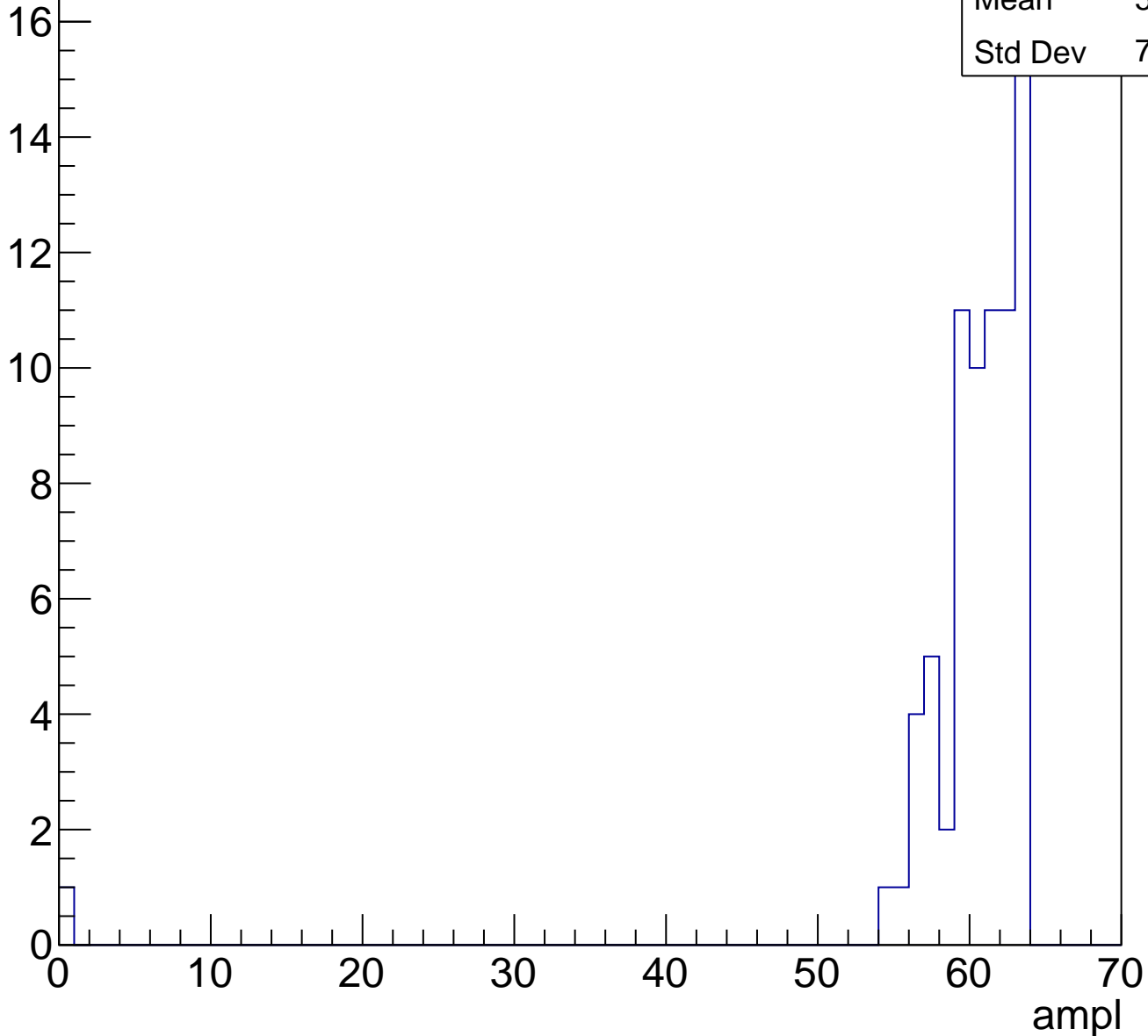
ampl



B0L101S, U21-ch111, adc5

calib_packv5_042523_0143.root, FC#1, port C1

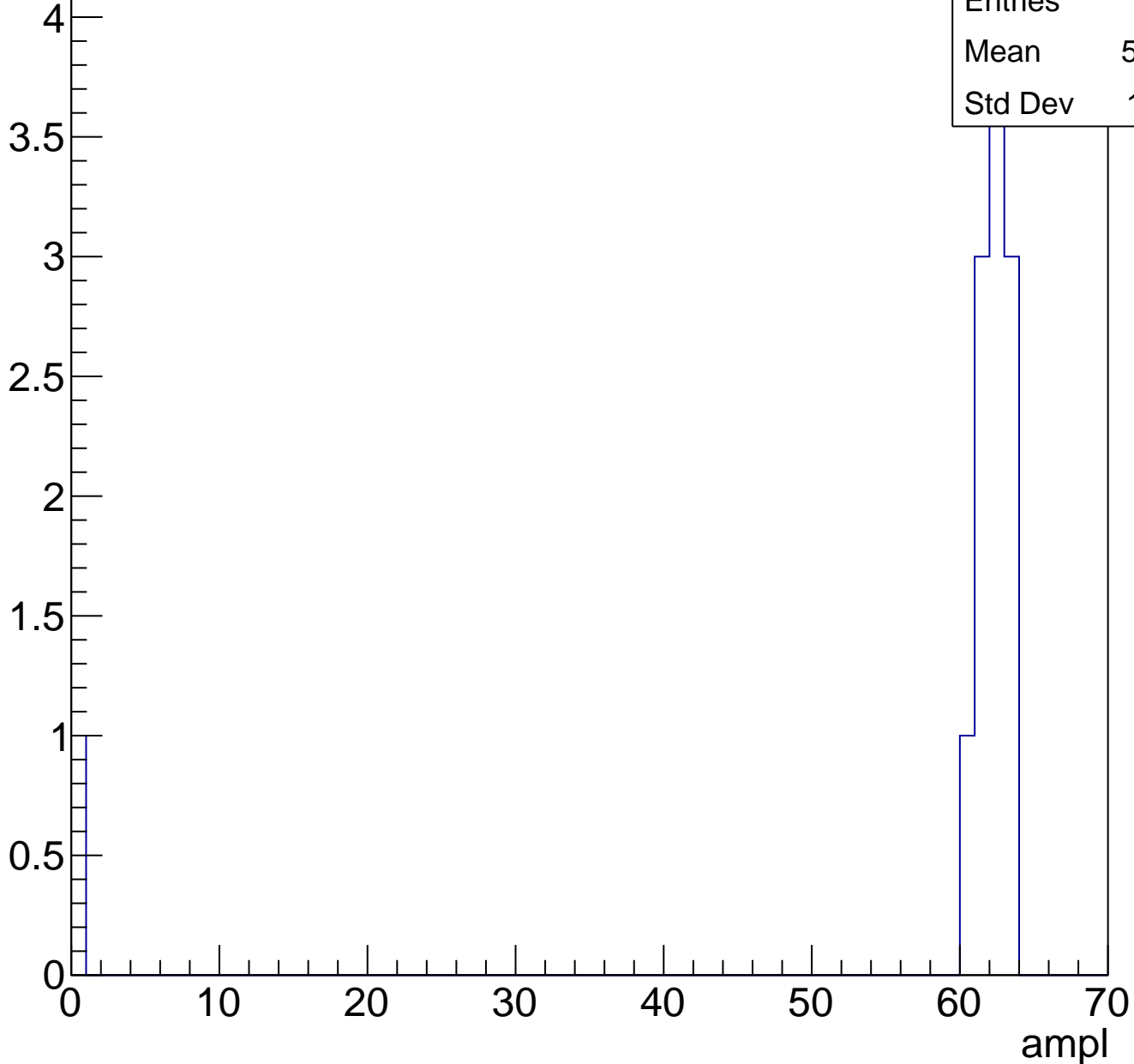
Entry



B0L101S, U21-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch112, adc0

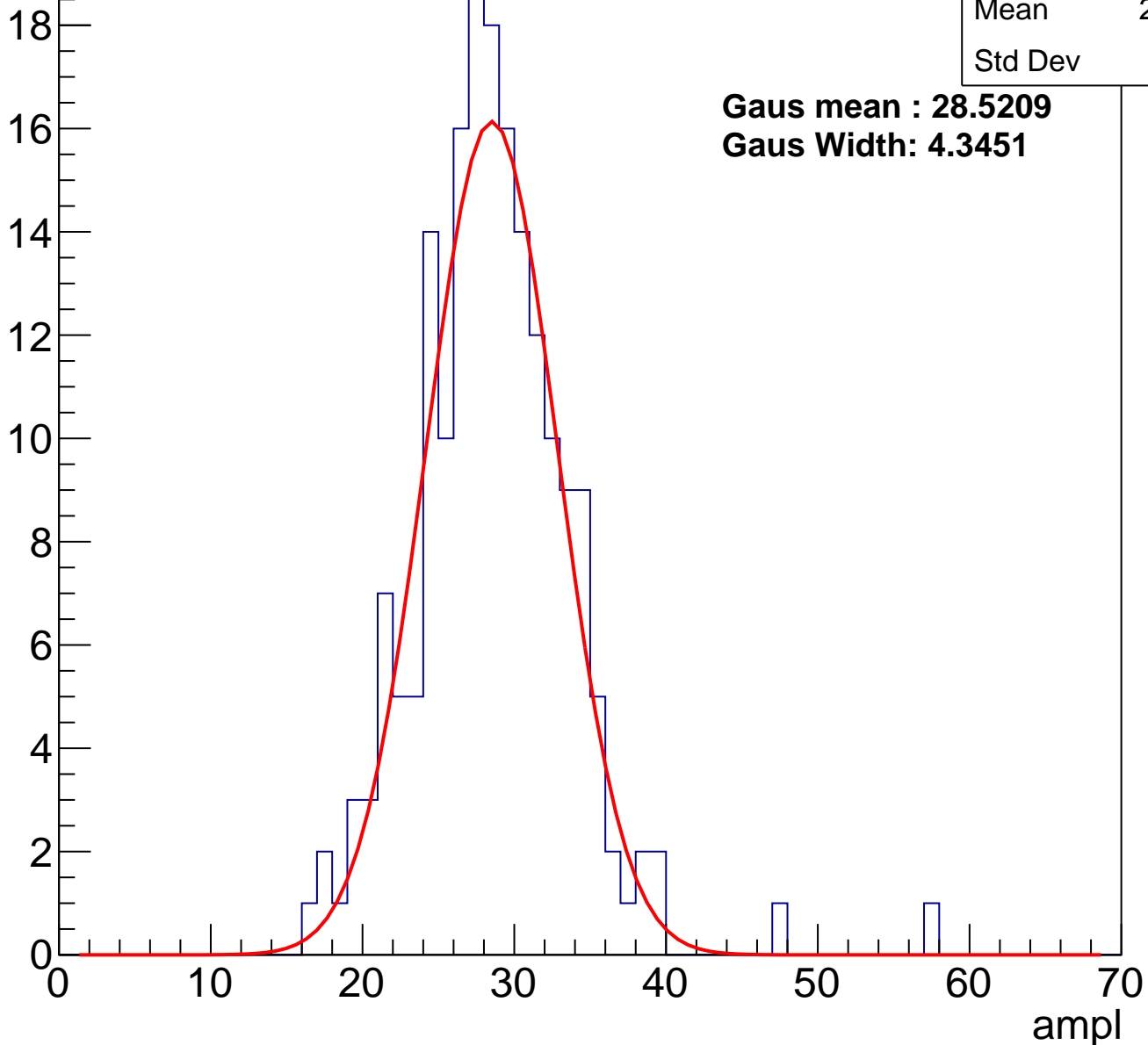
calib_packv5_042523_0143.root, FC#1, port C1

Entries	188
Mean	28.18
Std Dev	5.13

Gaus mean : 28.5209

Gaus Width: 4.3451

Entry



B0L101S, U21-ch112, adc1

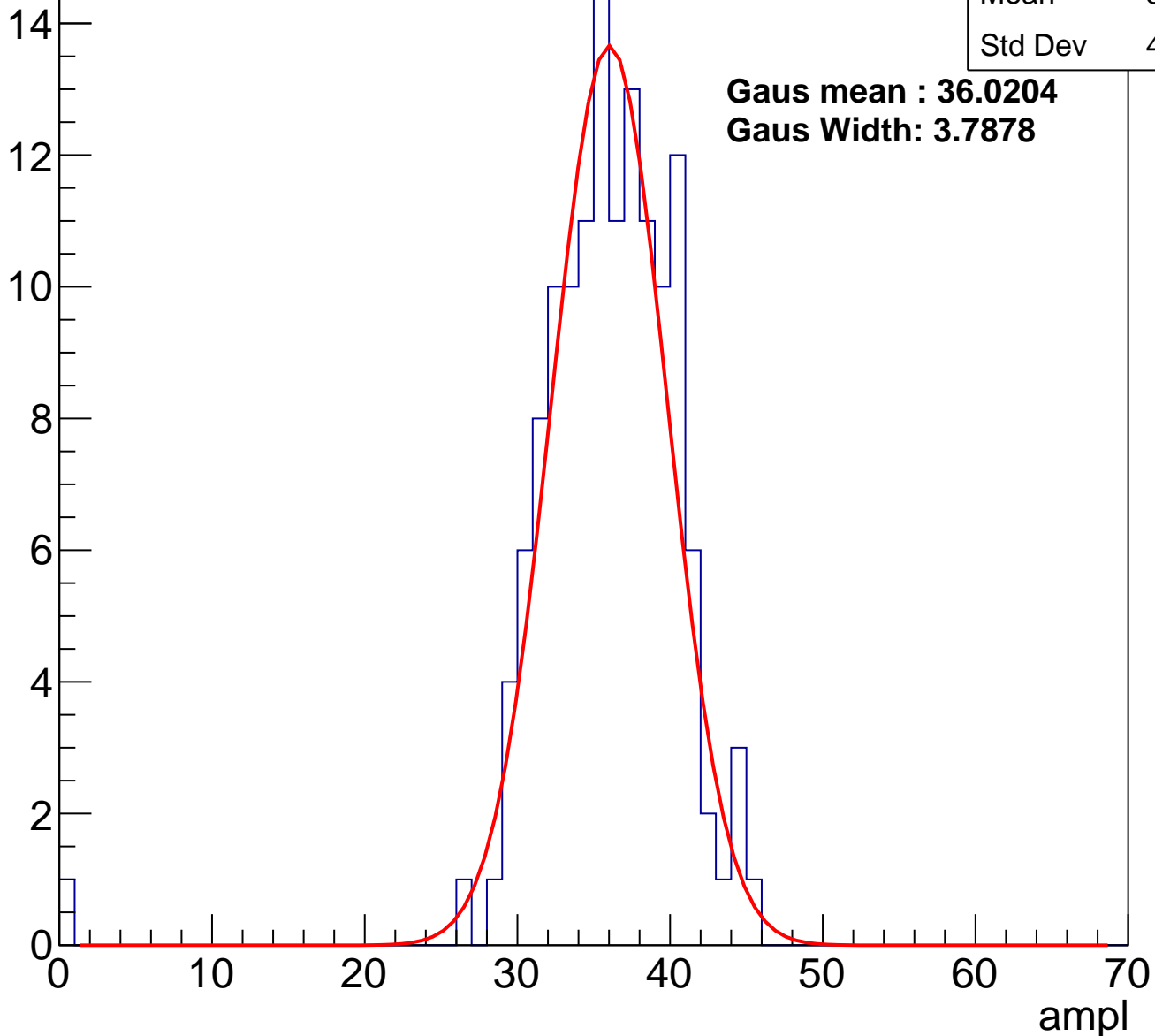
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	35.49
Std Dev	4.854

Gaus mean : 36.0204

Gaus Width: 3.7878

Entry



B0L101S, U21-ch112, adc2

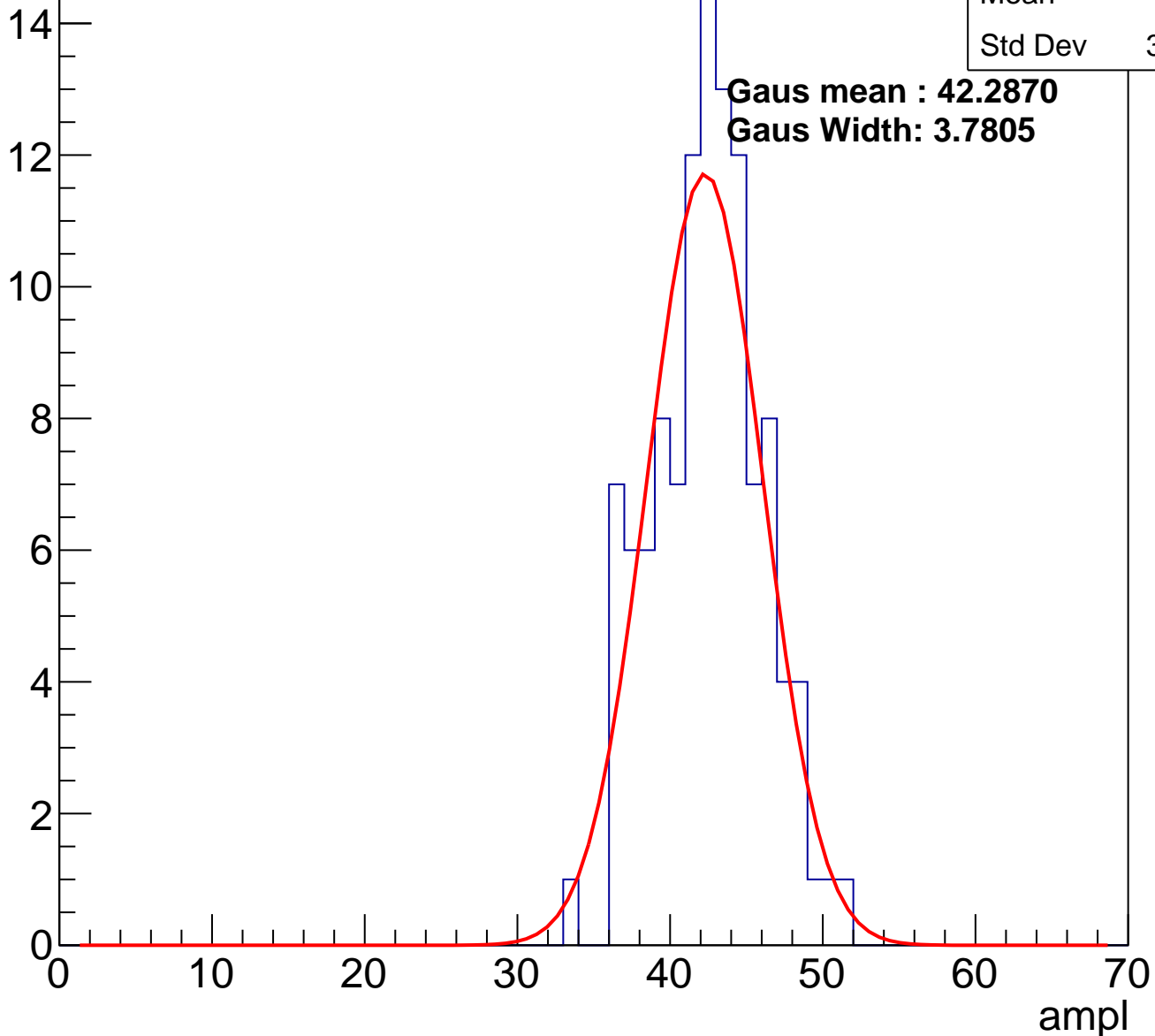
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	113
Mean	42.03
Std Dev	3.496

Gaus mean : 42.2870

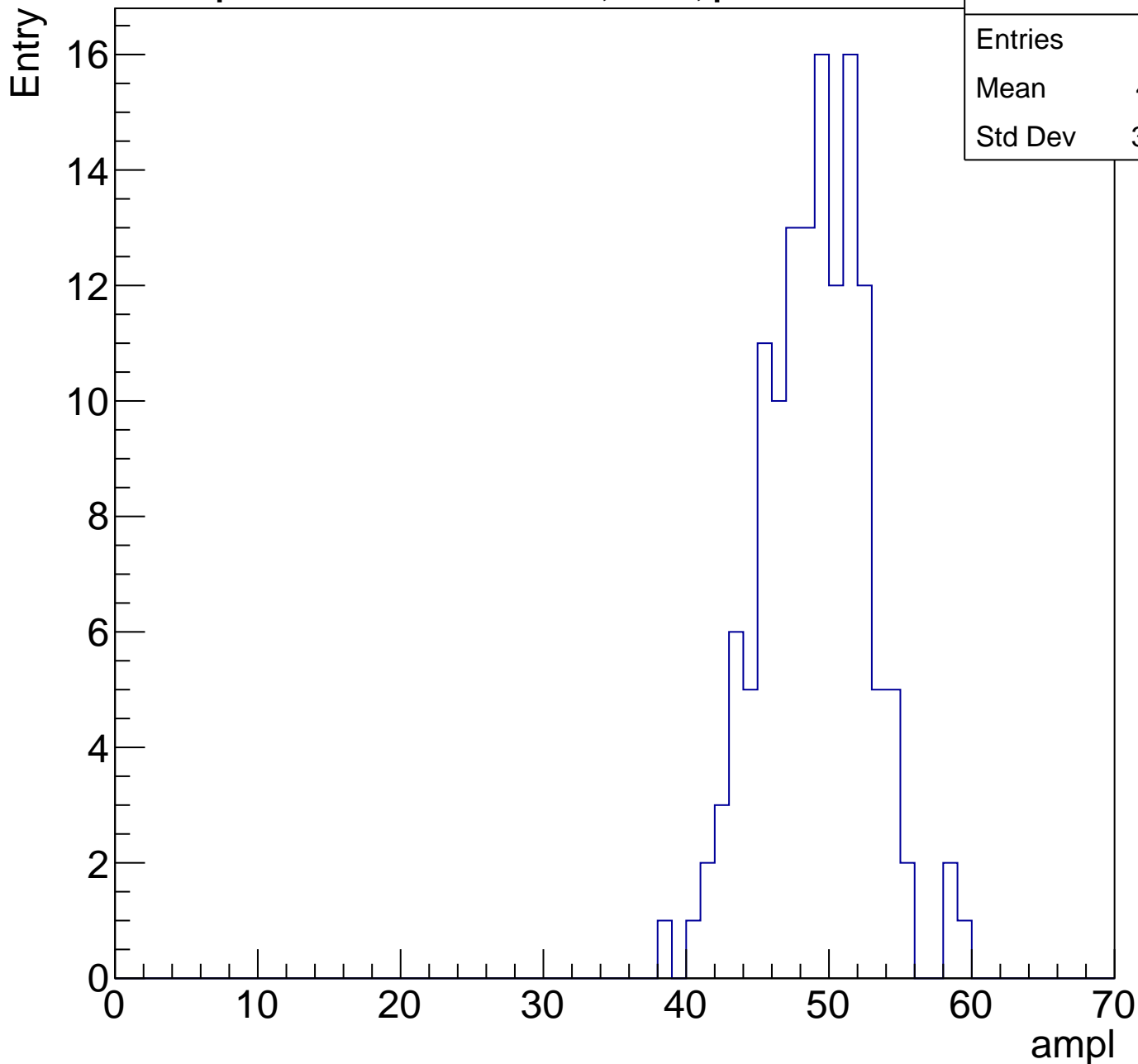
Gaus Width: 3.7805



B0L101S, U21-ch112, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	48.51
Std Dev	3.678



B0L101S, U21-ch112, adc4

calib_packv5_042523_0143.root, FC#1, port C1

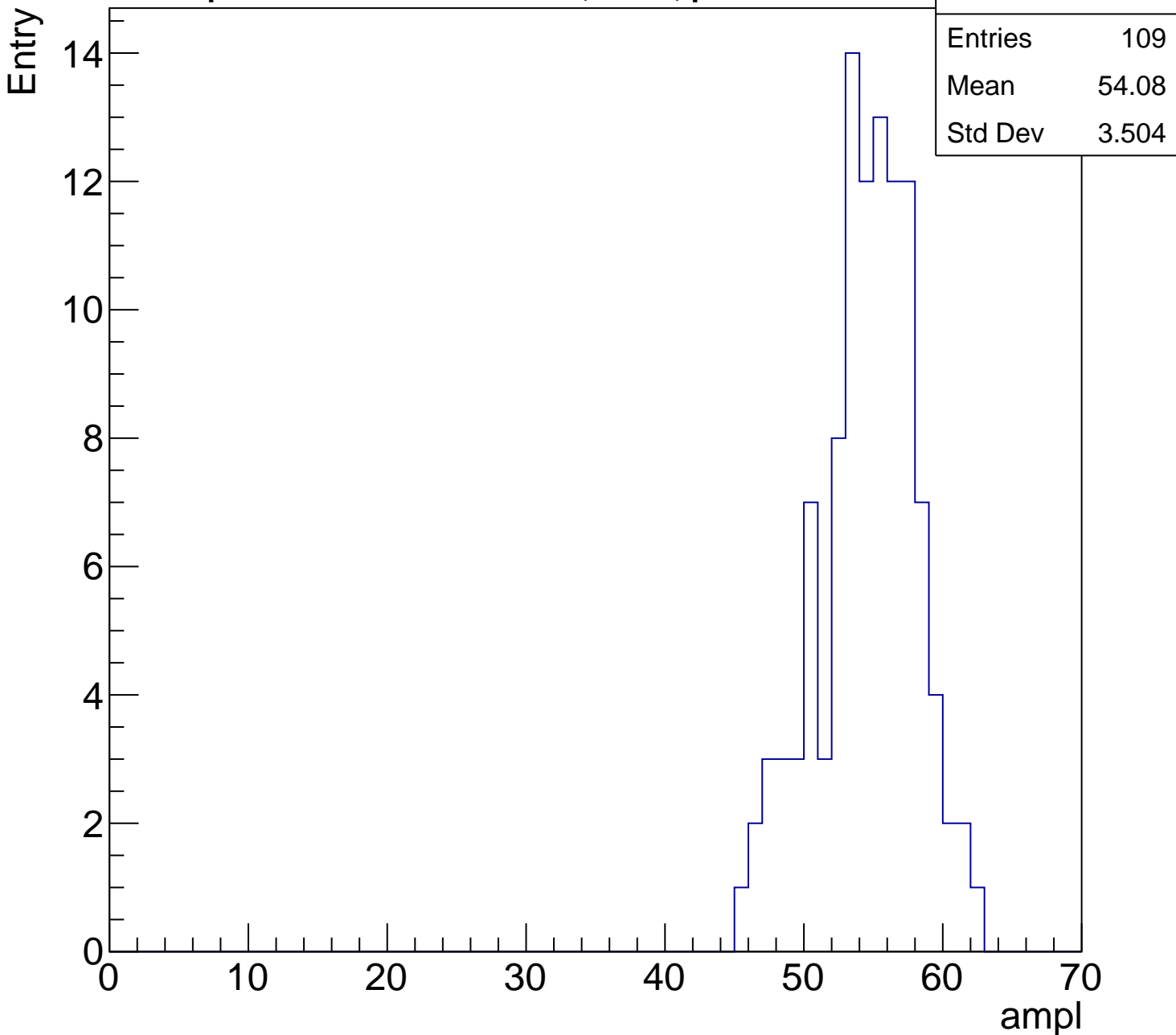
Entries	109
Mean	54.08
Std Dev	3.504

Entry

14
12
10
8
6
4
2
0

ampl

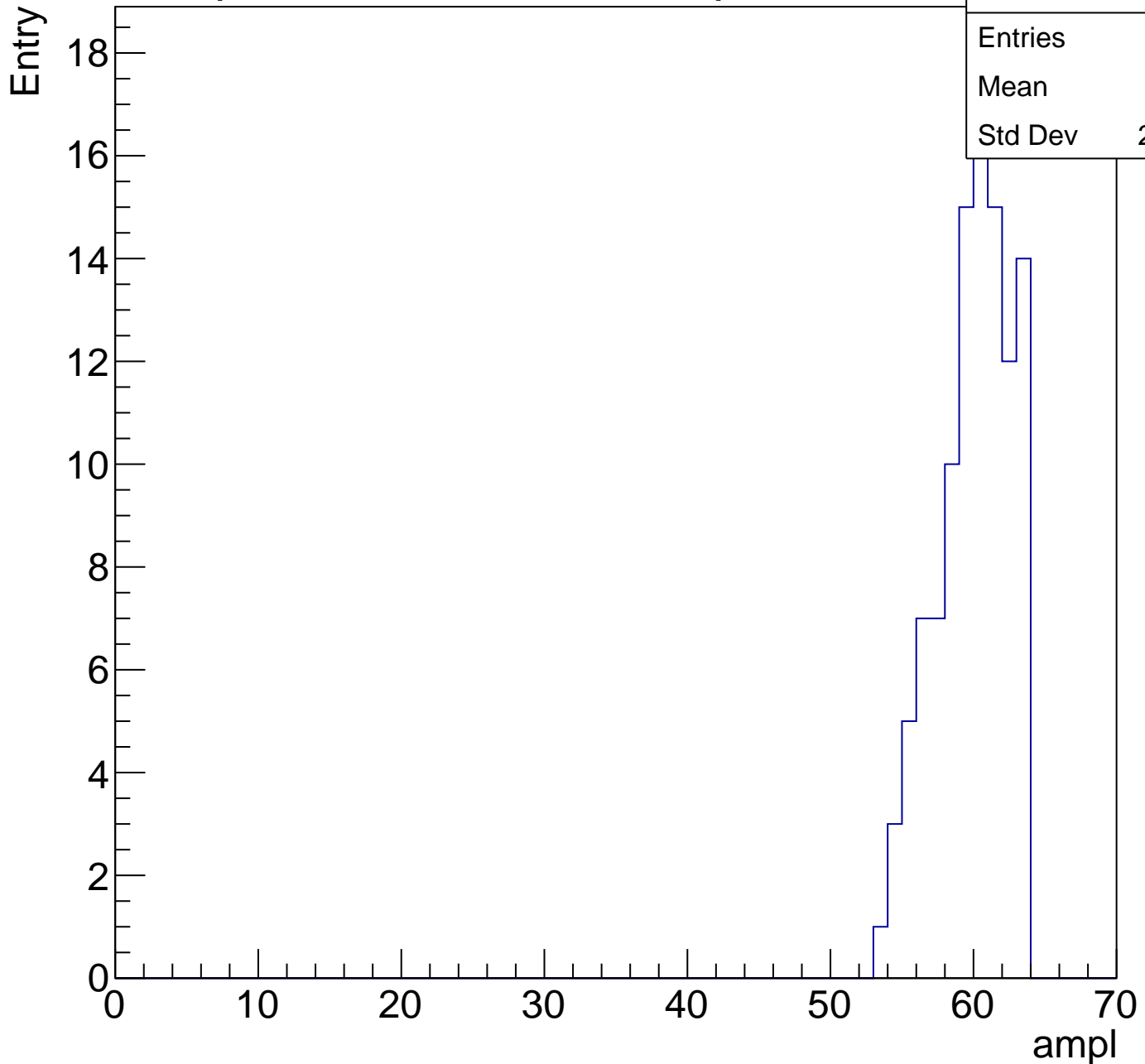
0 10 20 30 40 50 60 70



B0L101S, U21-ch112, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	59.5
Std Dev	2.511

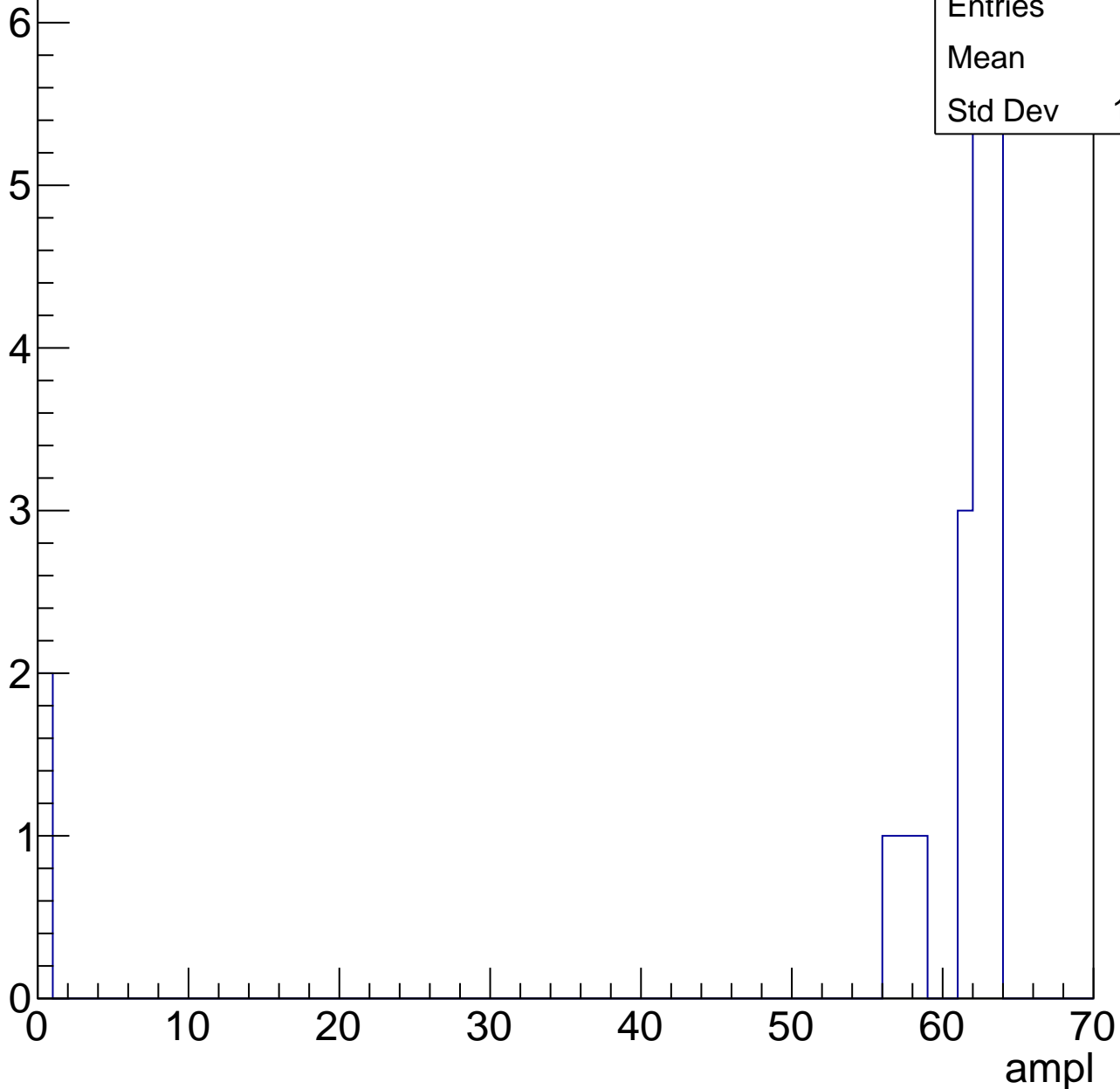


B0L101S, U21-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	55.2
Std Dev	18.51



B0L101S, U21-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch113, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	30.35
Std Dev	3.525

Gaus mean : 30.7047

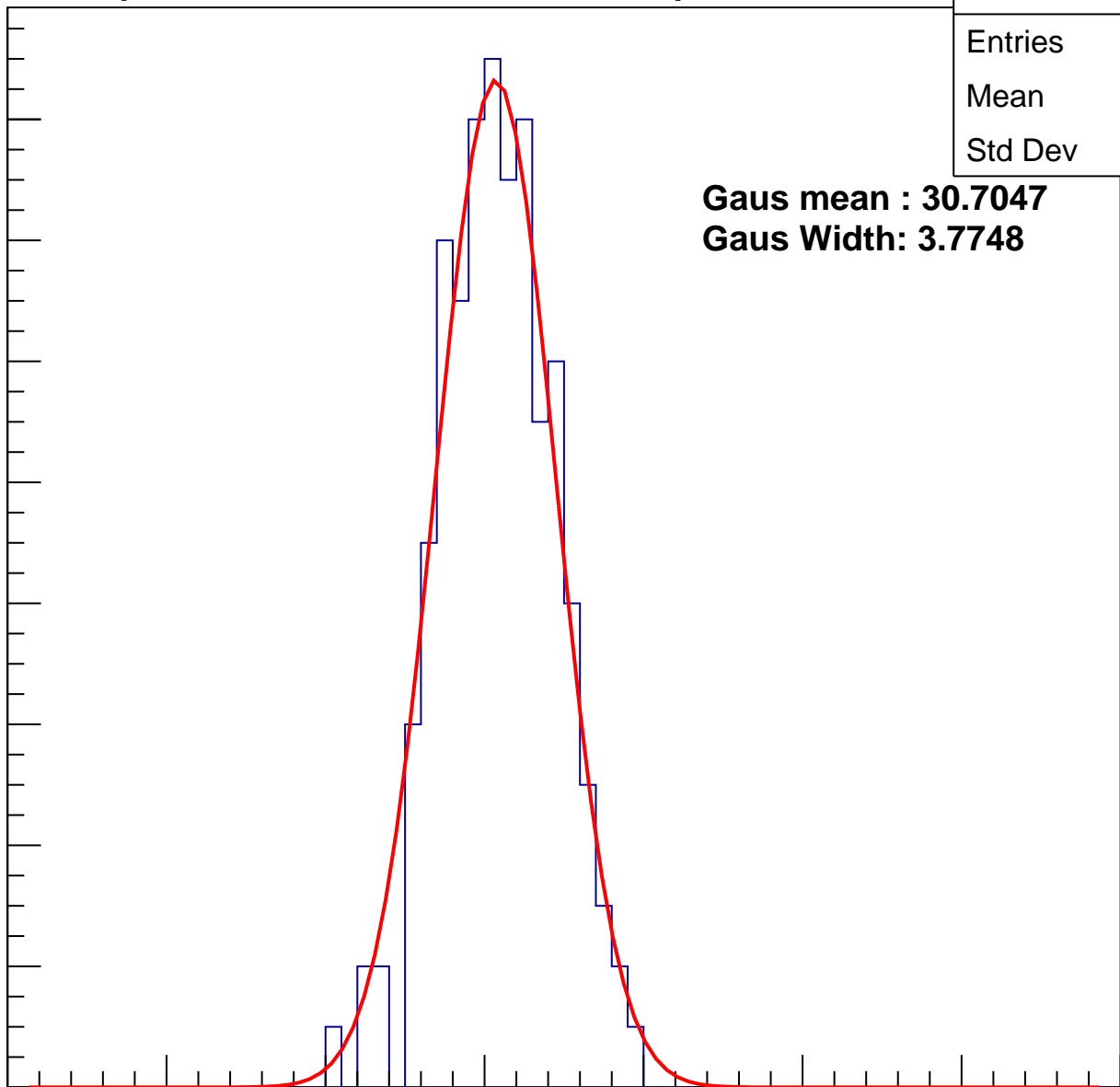
Gaus Width: 3.7748

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U21-ch113, adc1

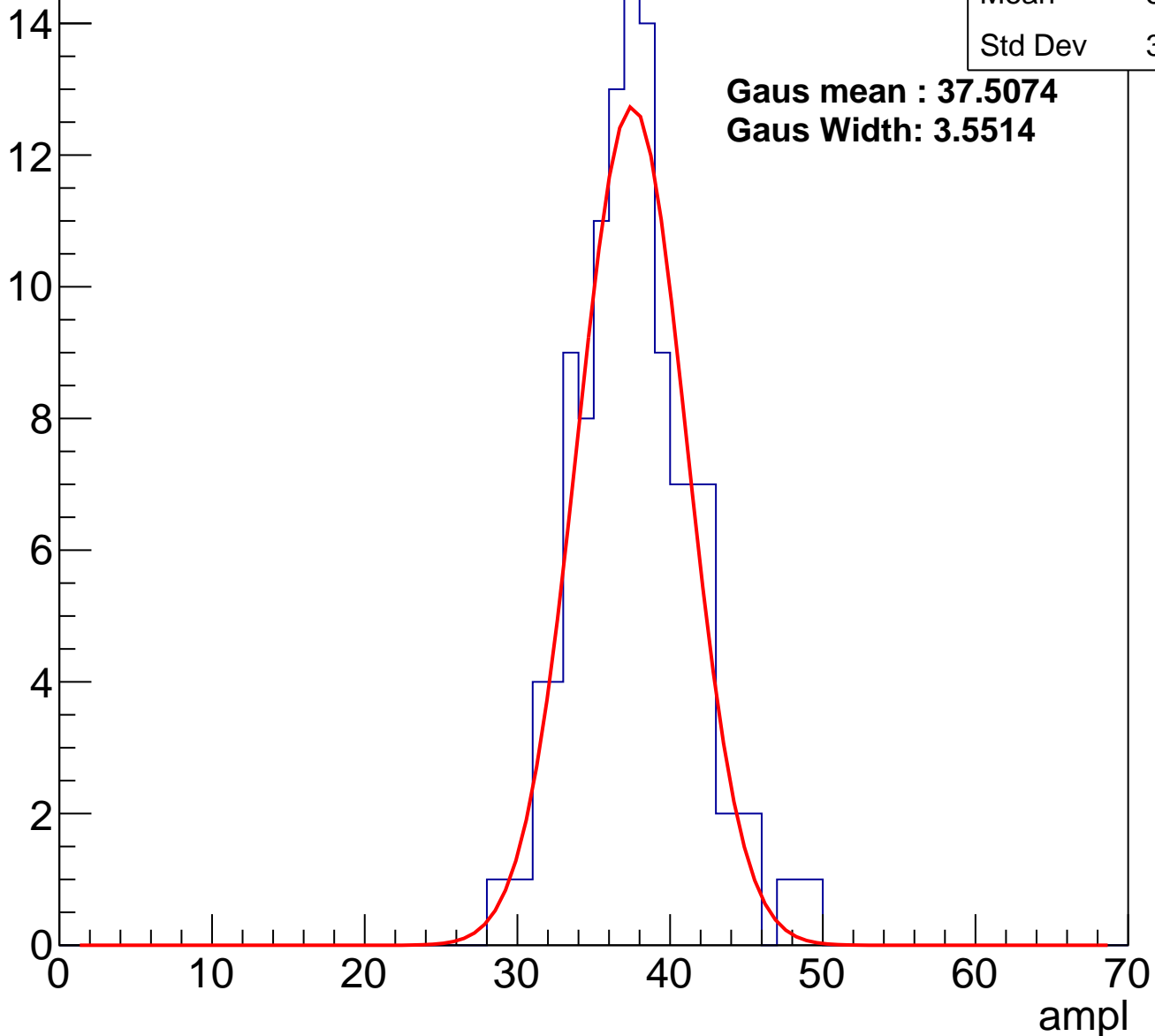
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	37.23
Std Dev	3.838

Gaus mean : 37.5074

Gaus Width: 3.5514

Entry



B0L101S, U21-ch113, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	43.98
Std Dev	3.699

Gaus mean : 44.4518

Gaus Width: 3.7693

Entry

16

14

12

10

8

6

4

2

0

0

10

20

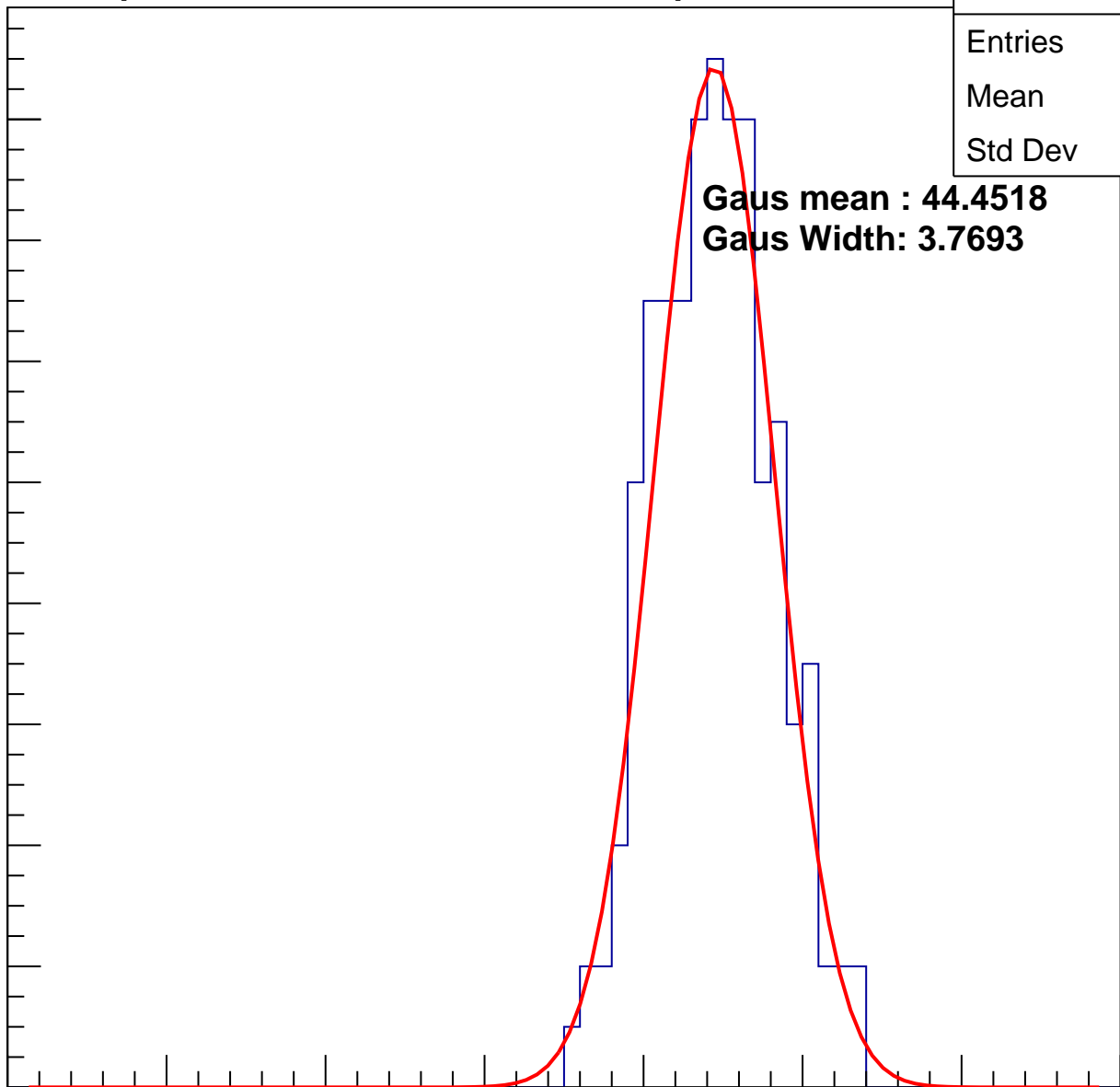
30

40

50

60

ampl



B0L101S, U21-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	51.25
Std Dev	3.655

Entry

16
14
12
10
8
6
4
2
0

0

10

20

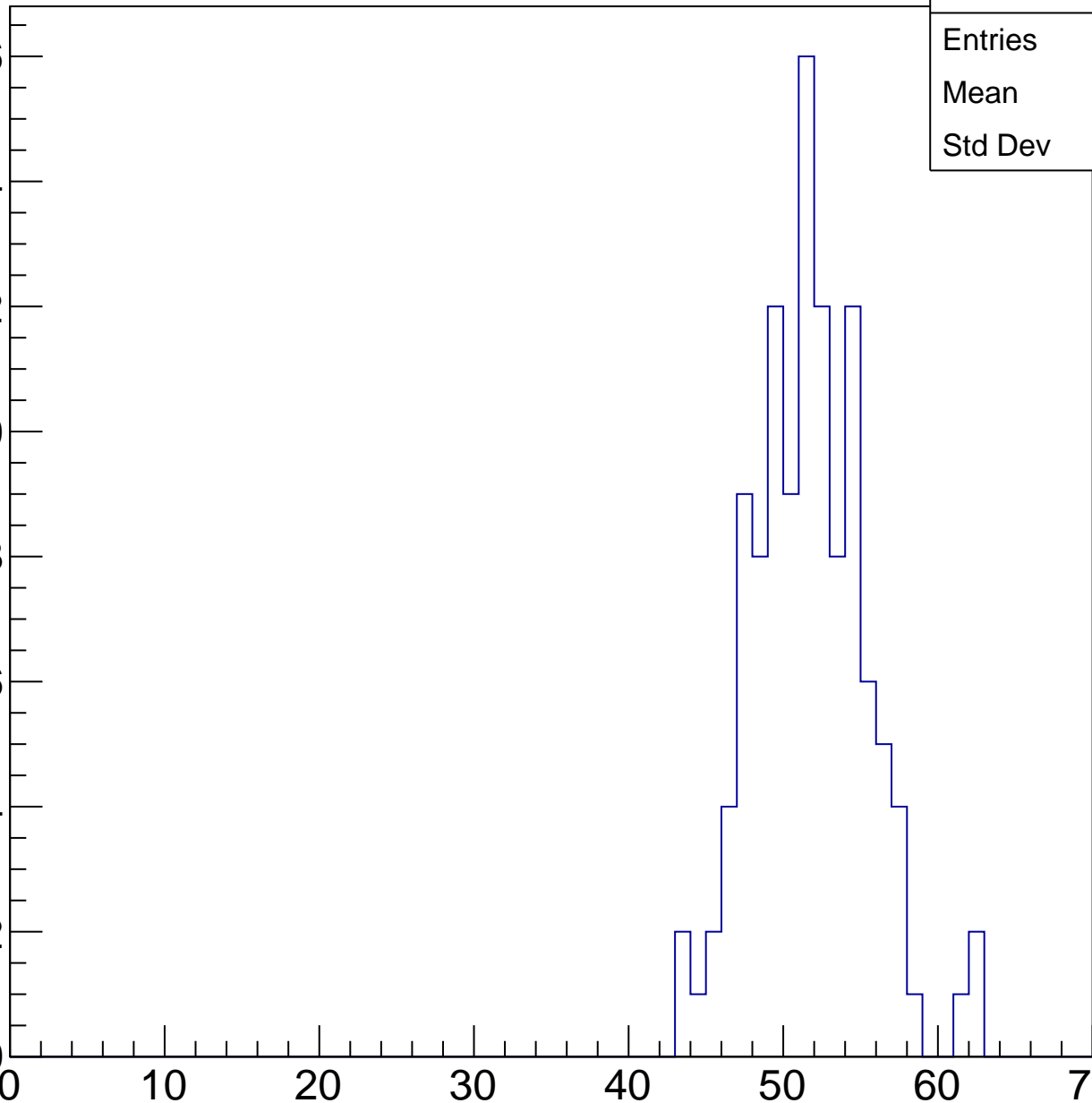
30

40

50

60

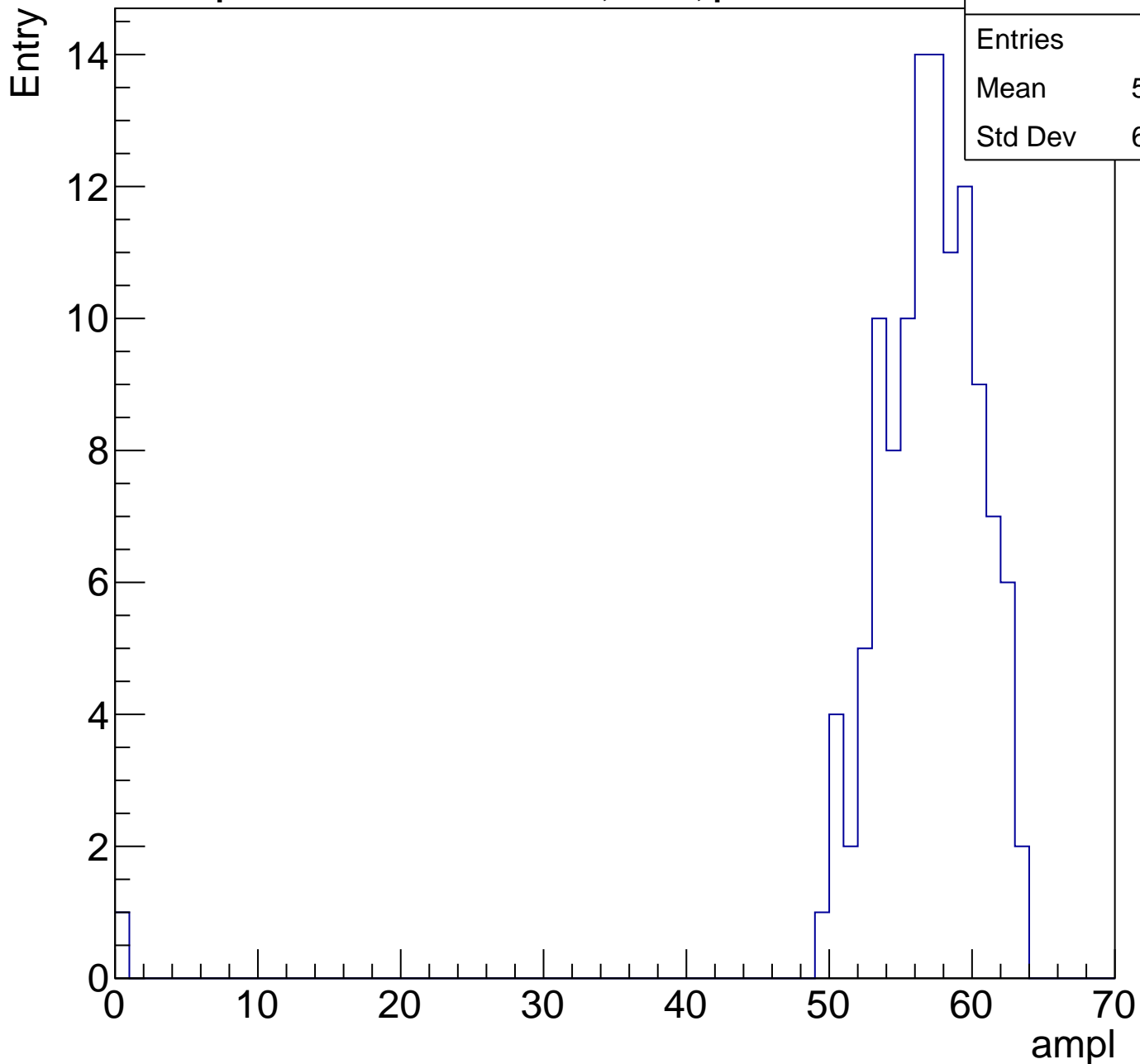
ampl



B0L101S, U21-ch113, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	56.17
Std Dev	6.149



B0L101S, U21-ch113, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	71
Mean	59.37
Std Dev	7.499

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

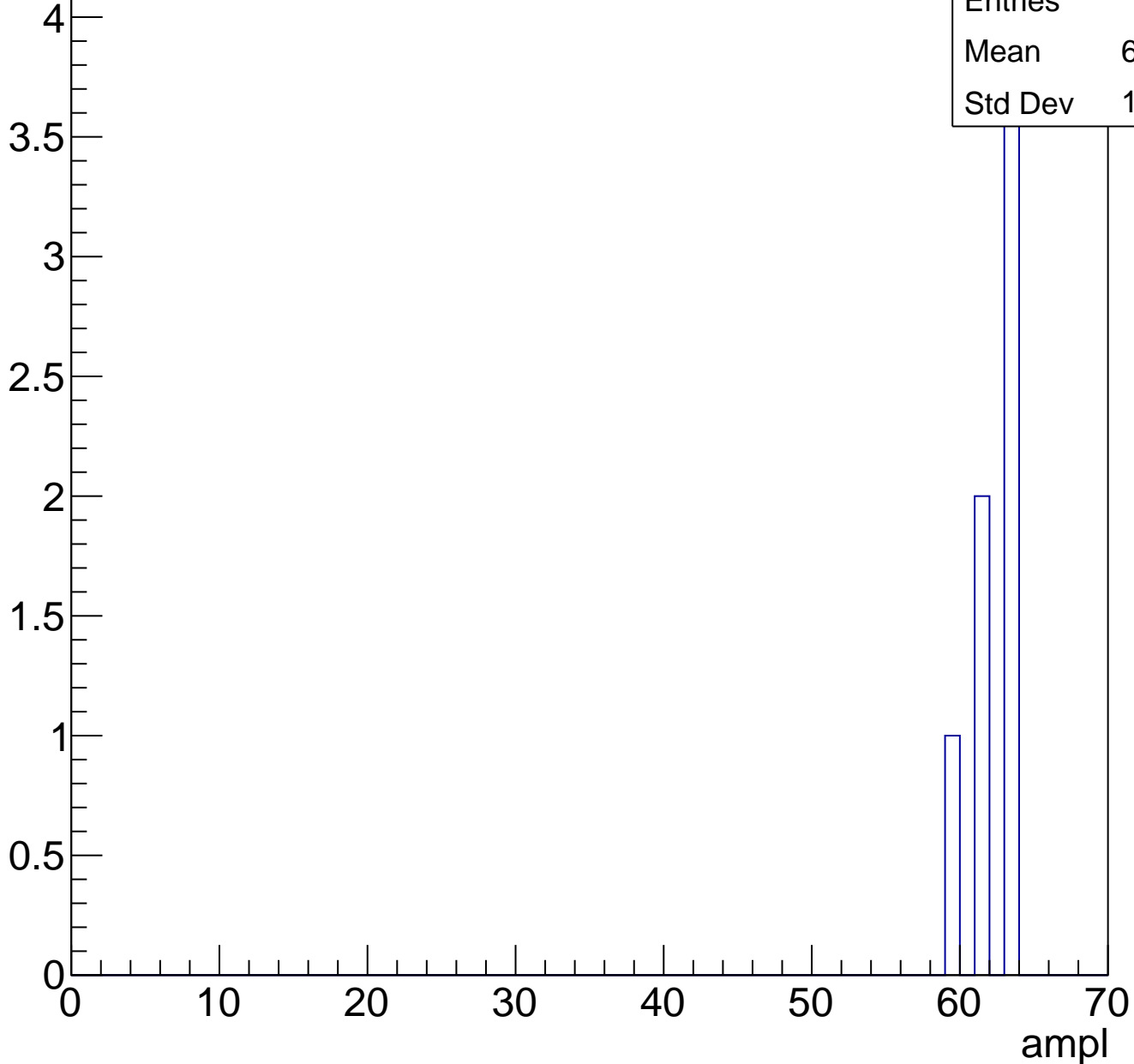
60

70

B0L101S, U21-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch114, adc0

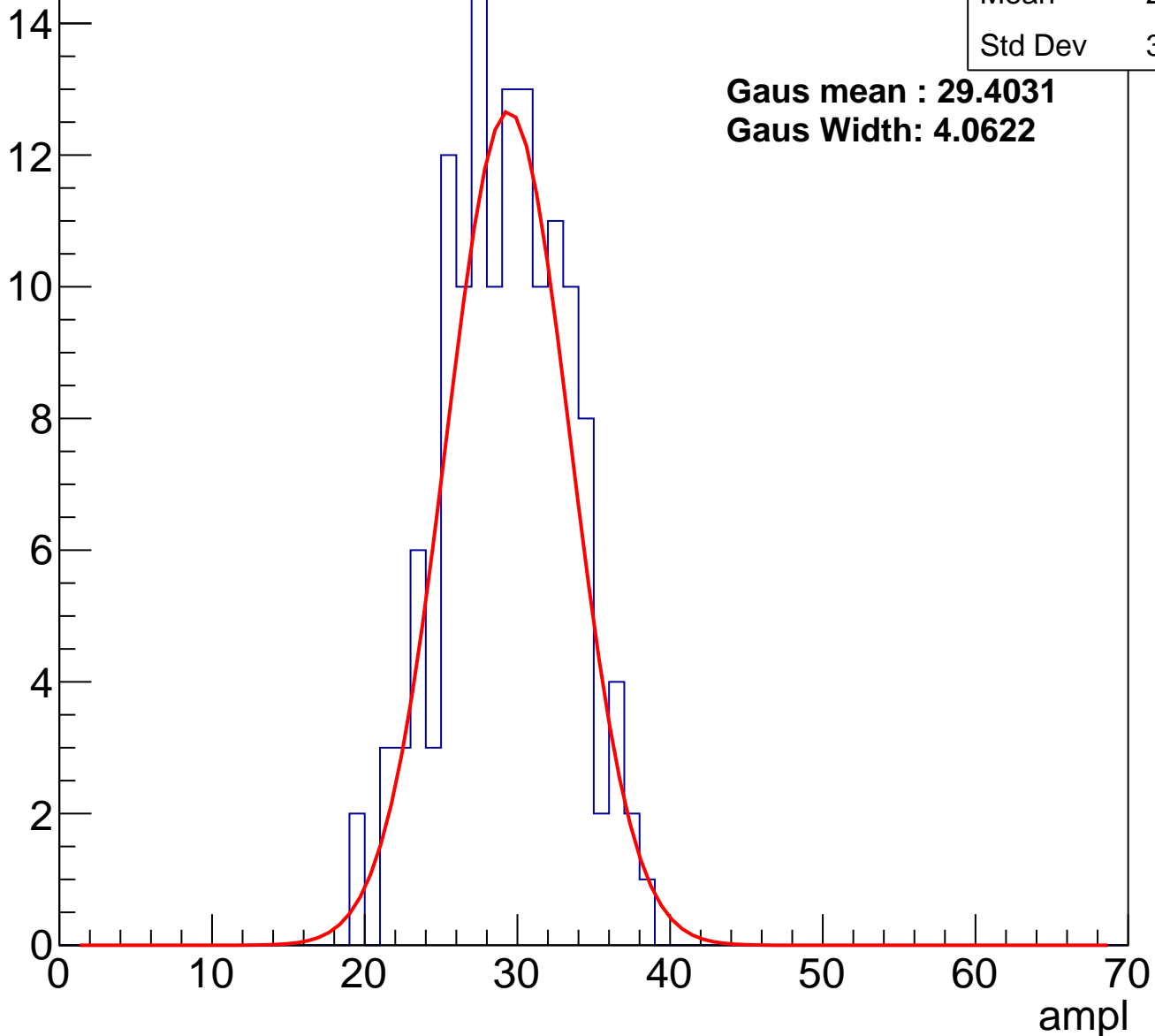
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.83
Std Dev	3.963

Gaus mean : 29.4031

Gaus Width: 4.0622

Entry



B0L101S, U21-ch114, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	35.6
Std Dev	4.12

Gaus mean : 35.9513

Gaus Width: 4.2790

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

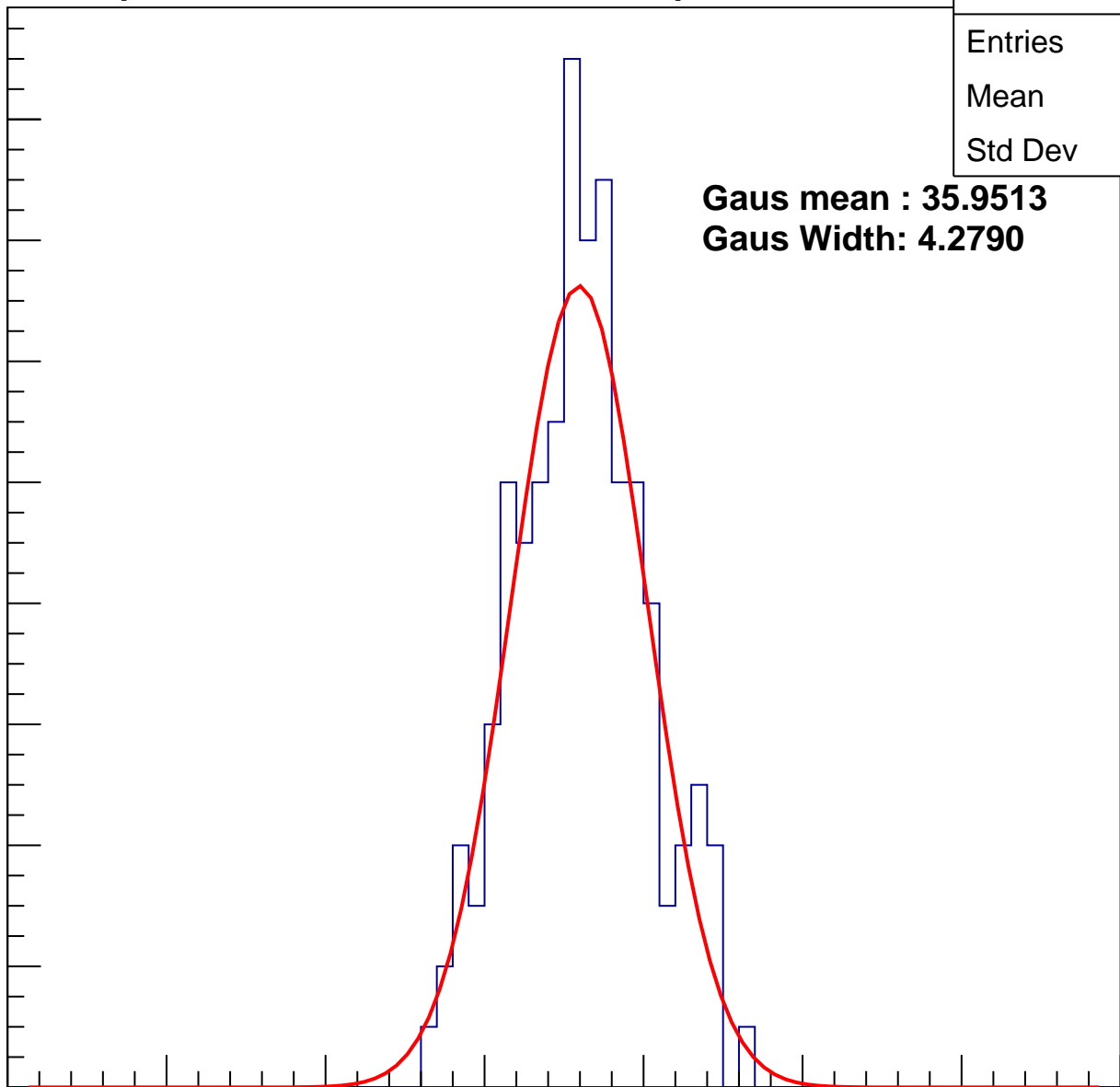
40

50

60

70

ampl



B0L101S, U21-ch114, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

134

Mean

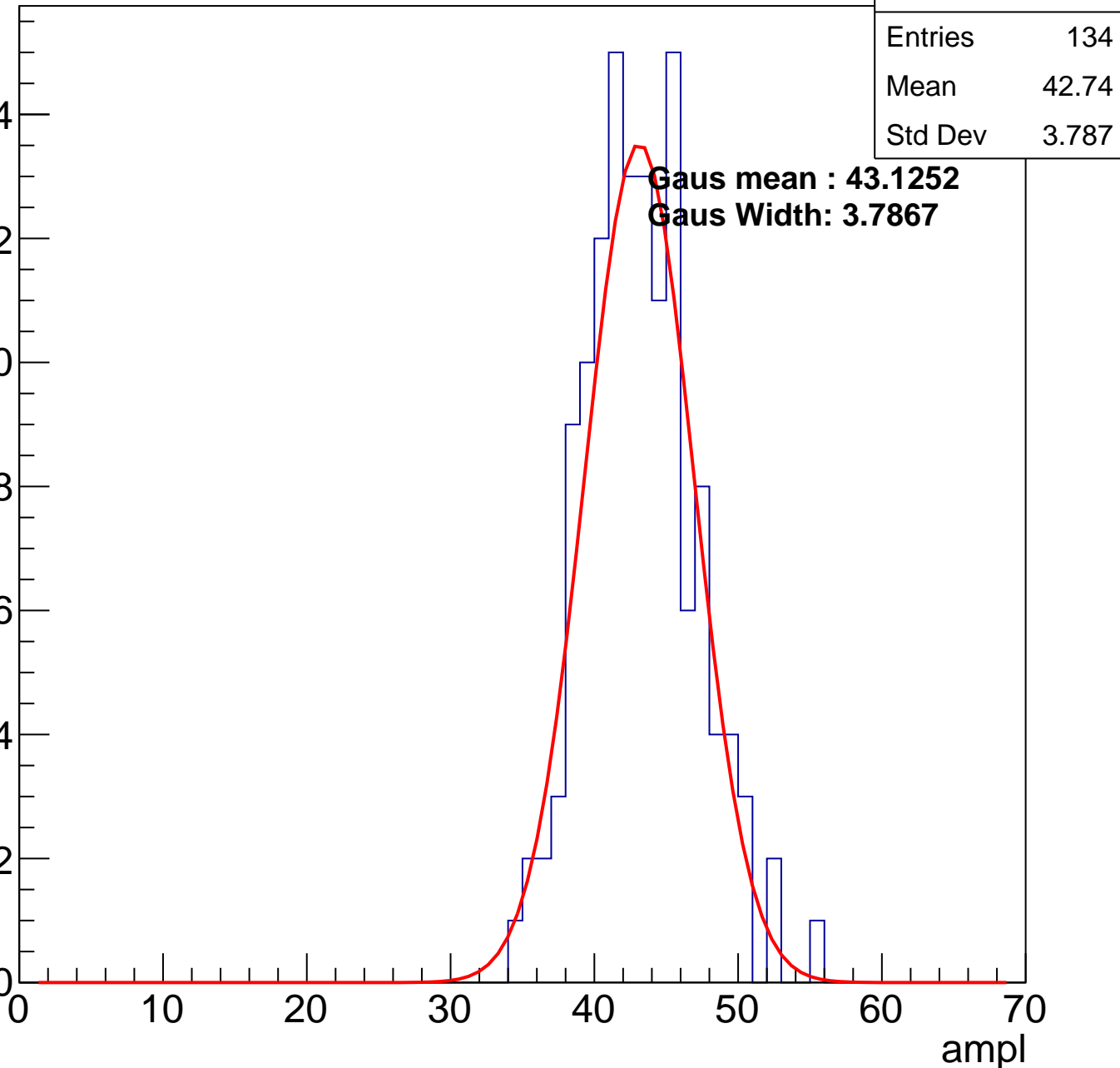
42.74

Std Dev

3.787

Gaus mean : 43.1252

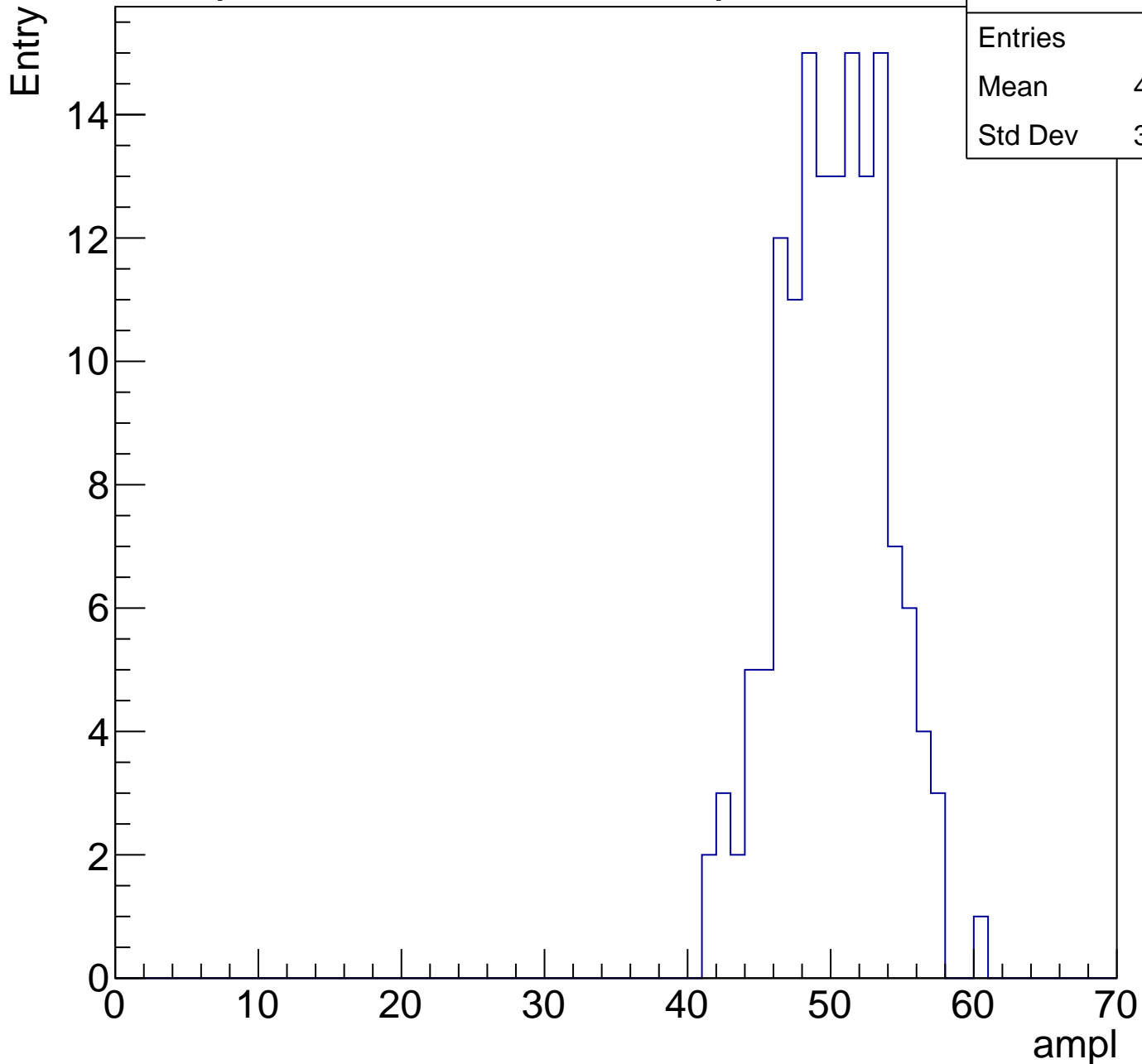
Gaus Width: 3.7867



B0L101S, U21-ch114, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	49.75
Std Dev	3.683



B0L101S, U21-ch114, adc4

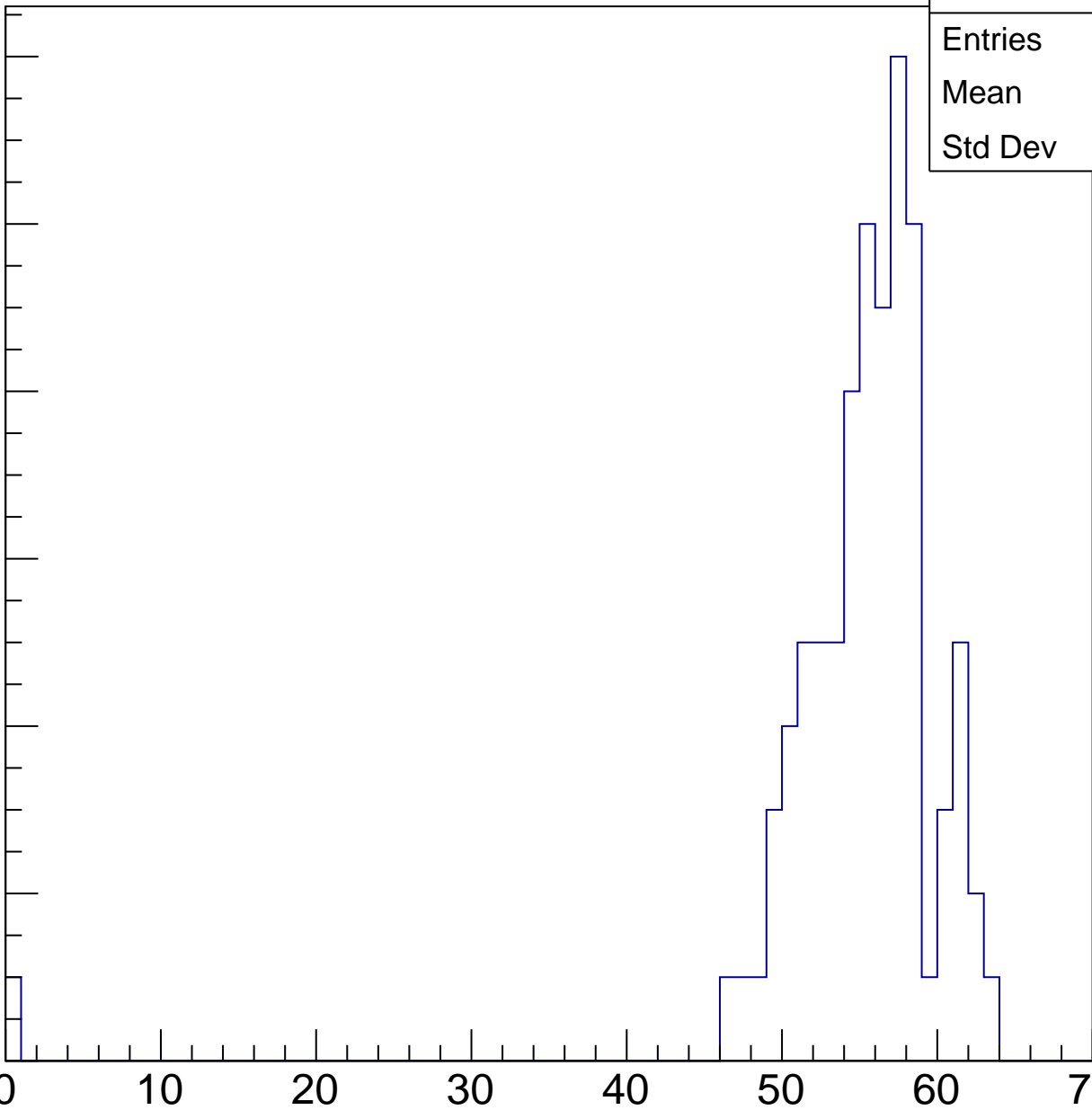
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	87
Mean	54.59
Std Dev	6.903

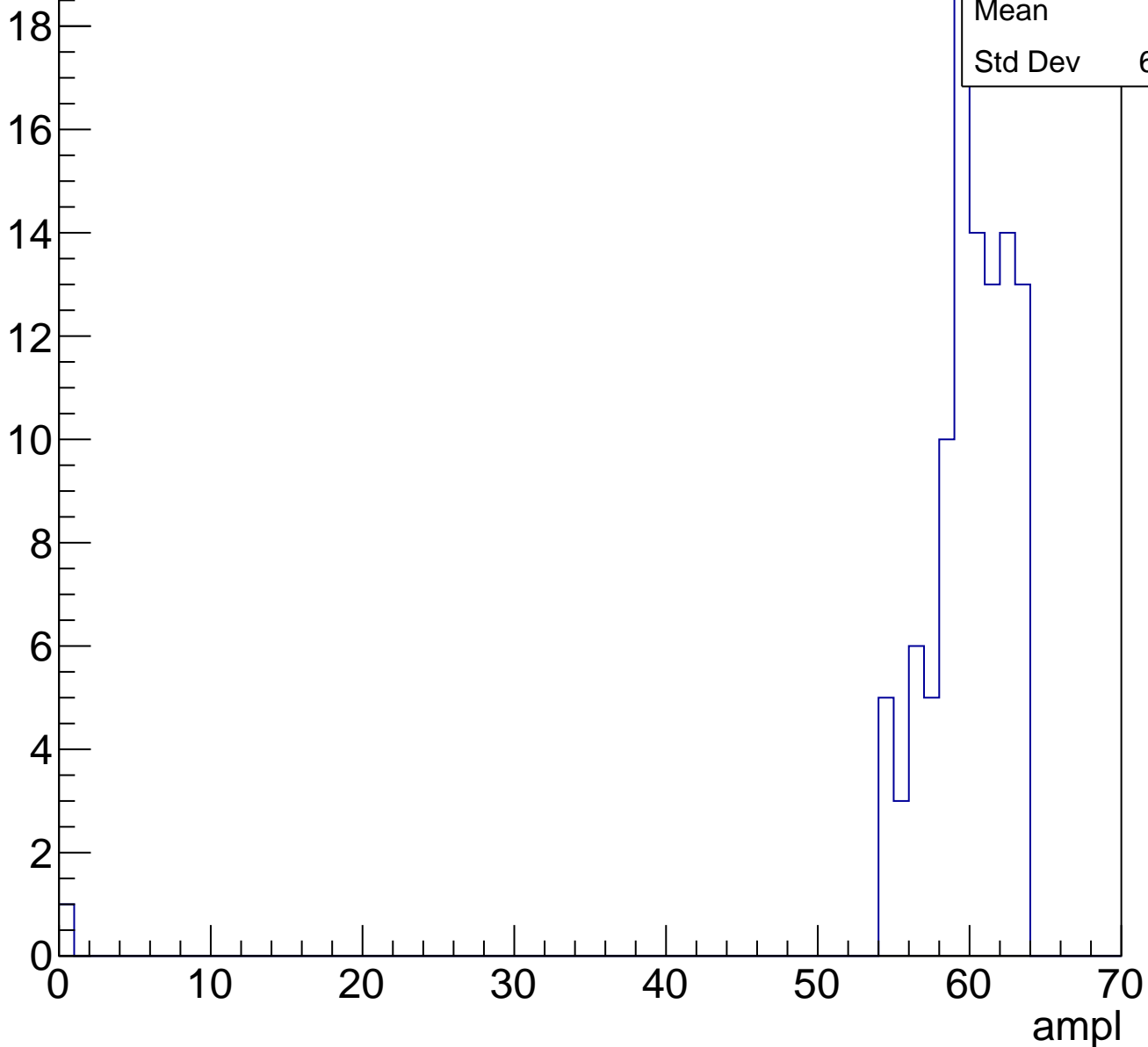
ampl



B0L101S, U21-ch114, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

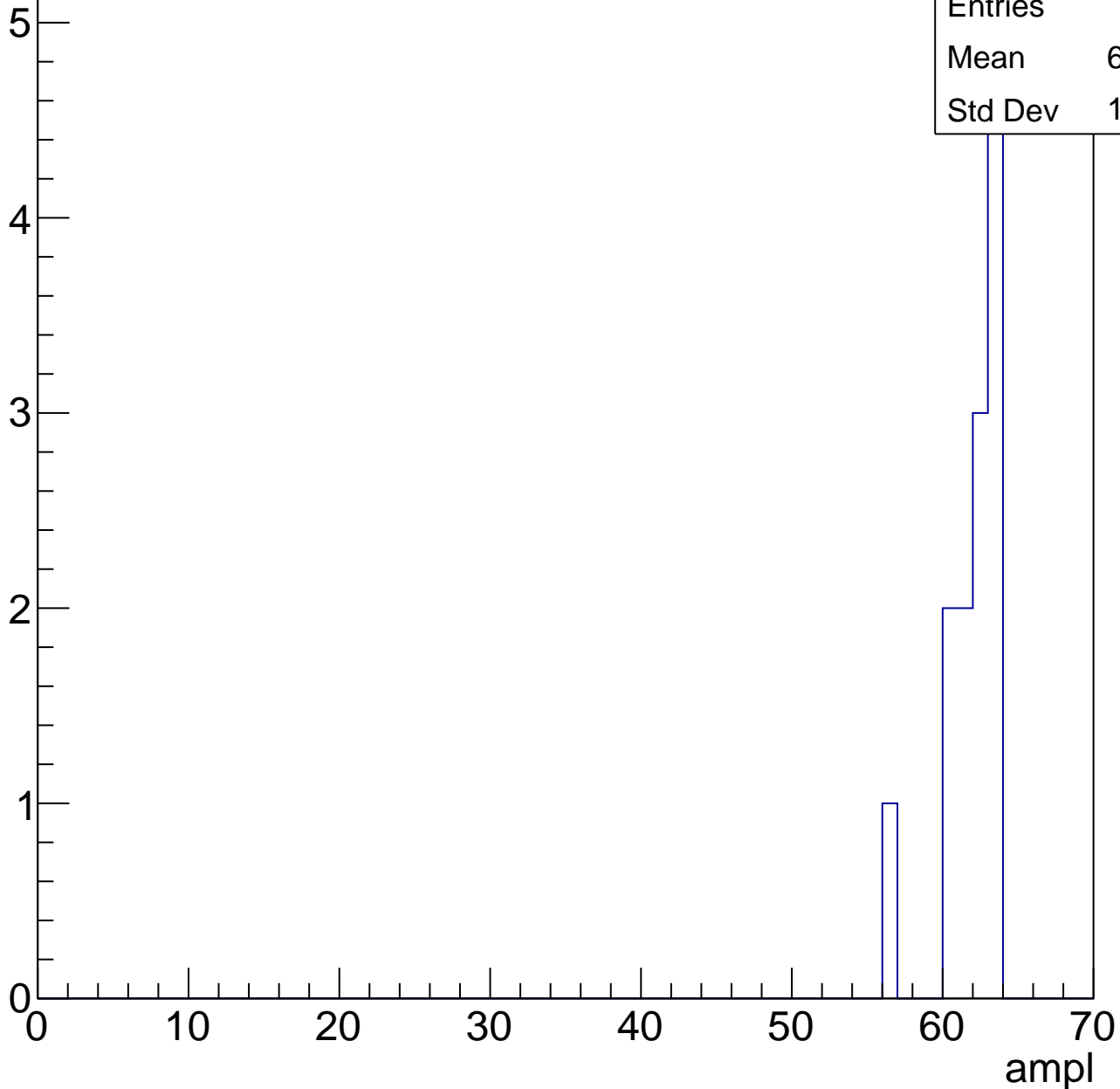


B0L101S, U21-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

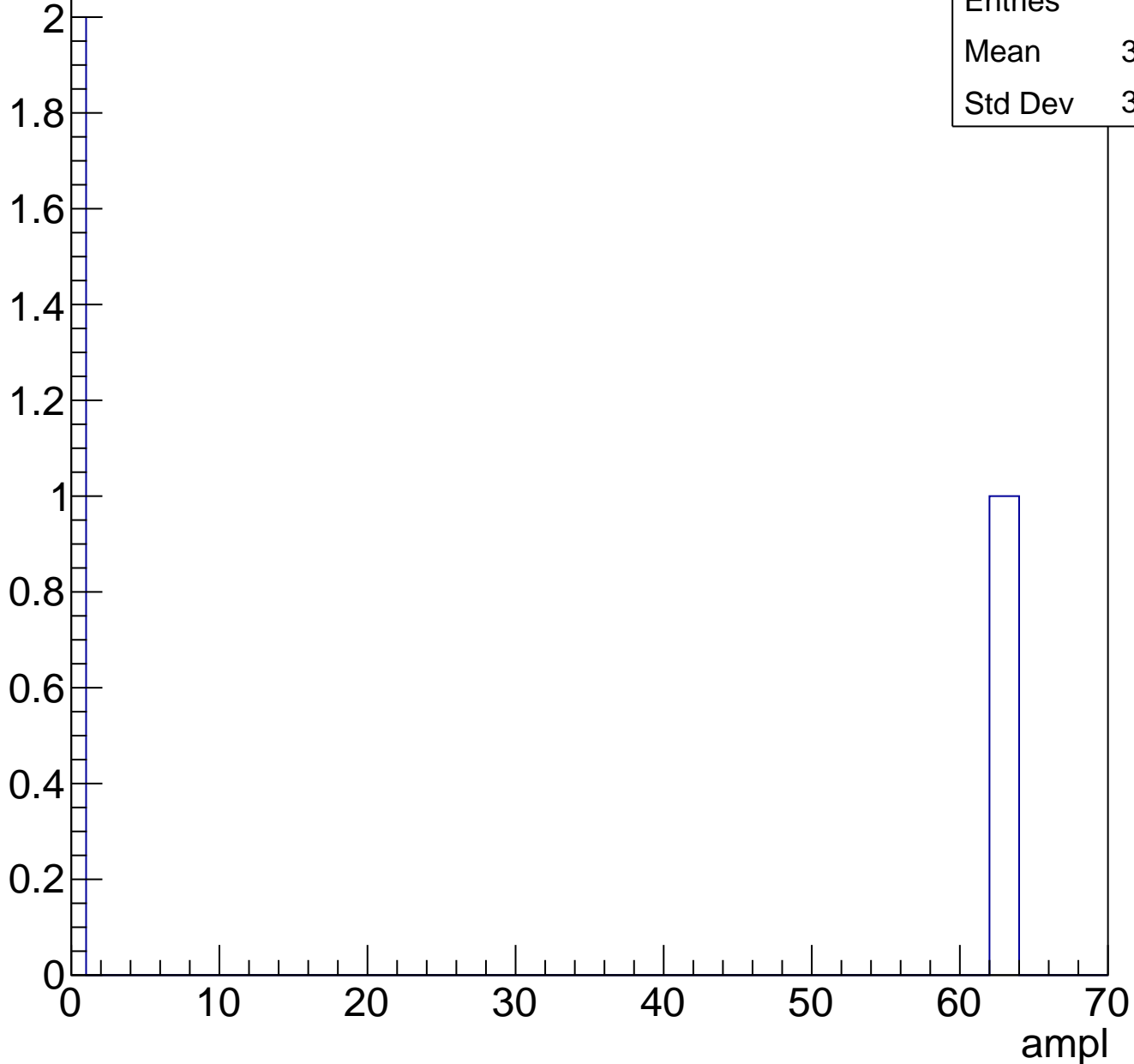
Entries	13
Mean	61.46
Std Dev	1.906



B0L101S, U21-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch115, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	29.97
Std Dev	4.21

Gaus mean : 30.8739

Gaus Width: 3.2347

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

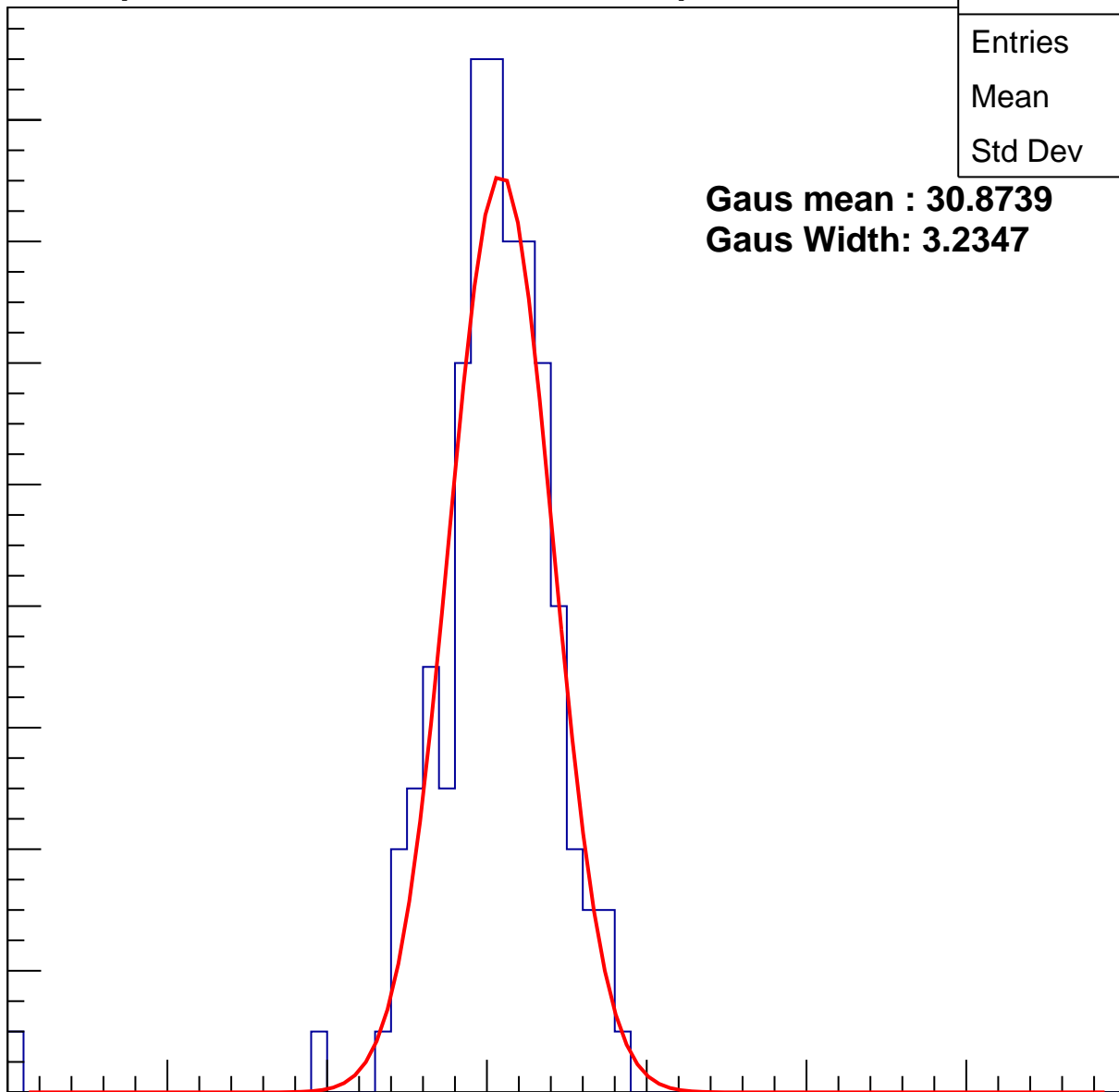
40

50

60

70

ampl



B0L101S, U21-ch115, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	37.12
Std Dev	3.422

Gaus mean : 37.7663

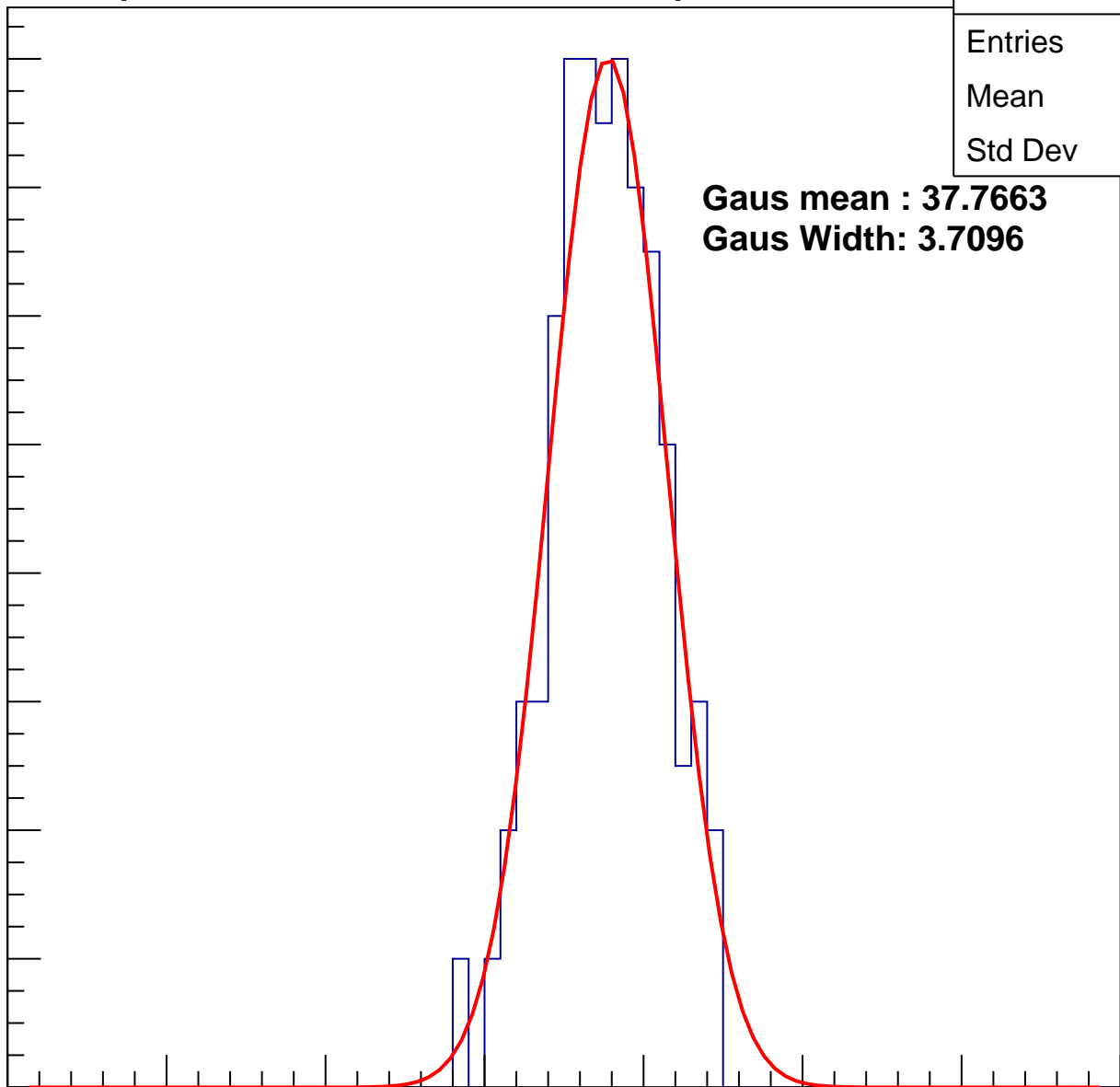
Gaus Width: 3.7096

Entry

16
14
12
10
8
6
4
2
0

ampl

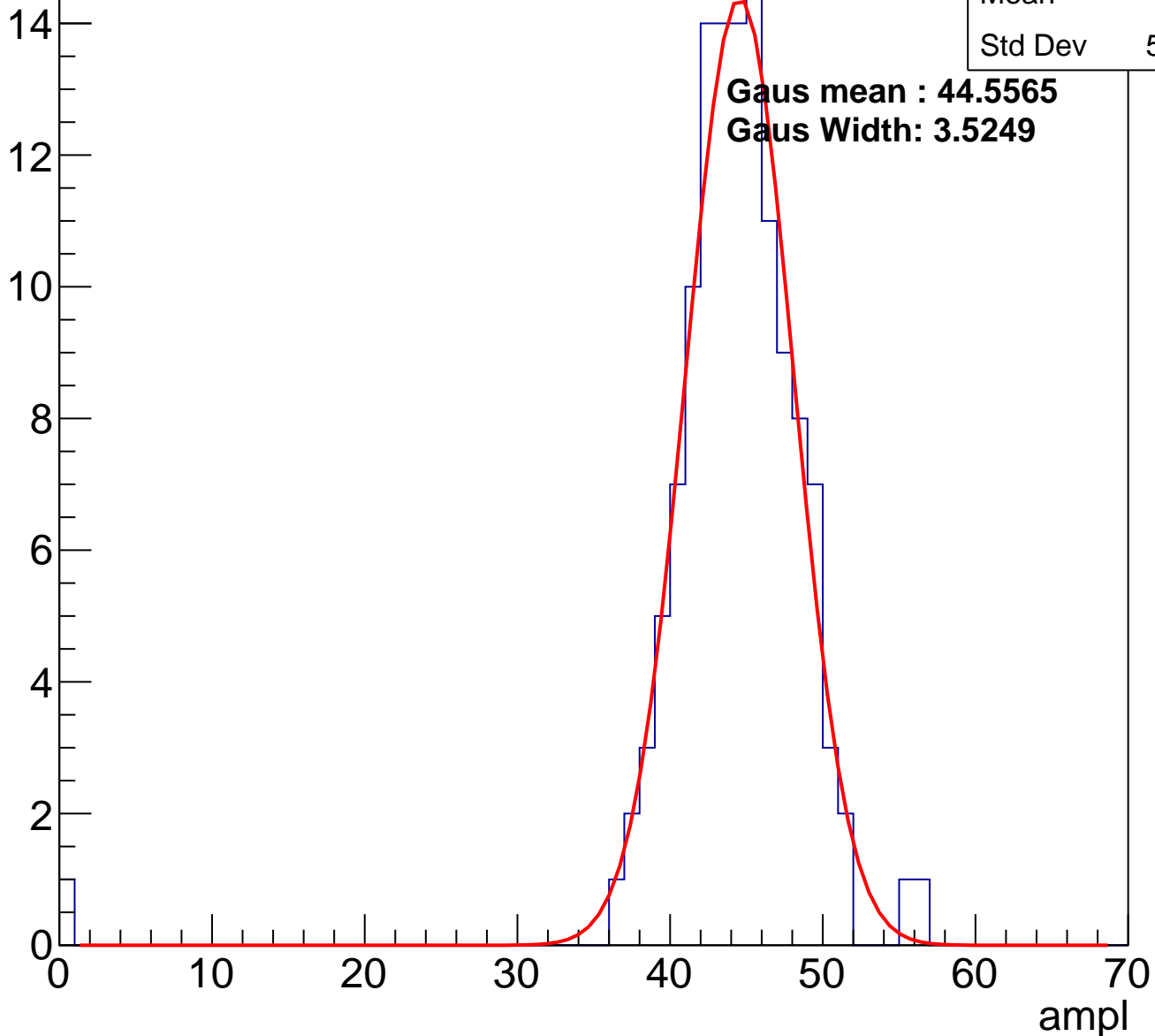
0 10 20 30 40 50 60 70



B0L101S, U21-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



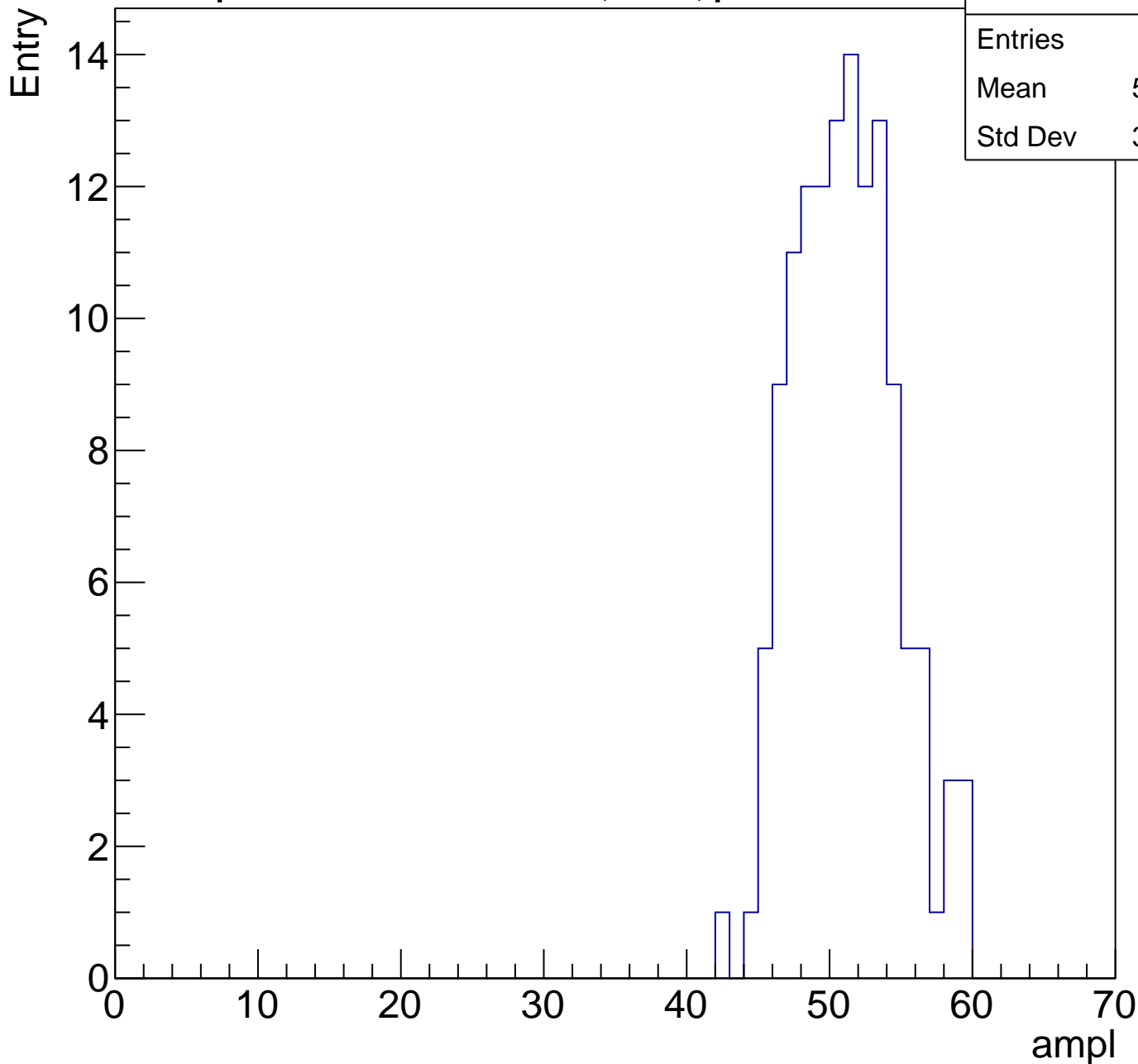
Entries	128
Mean	43.82
Std Dev	5.239

Gaus mean : 44.5565
Gaus Width: 3.5249

B0L101S, U21-ch115, adc3

calib_packv5_042523_0143.root, FC#1, port C1

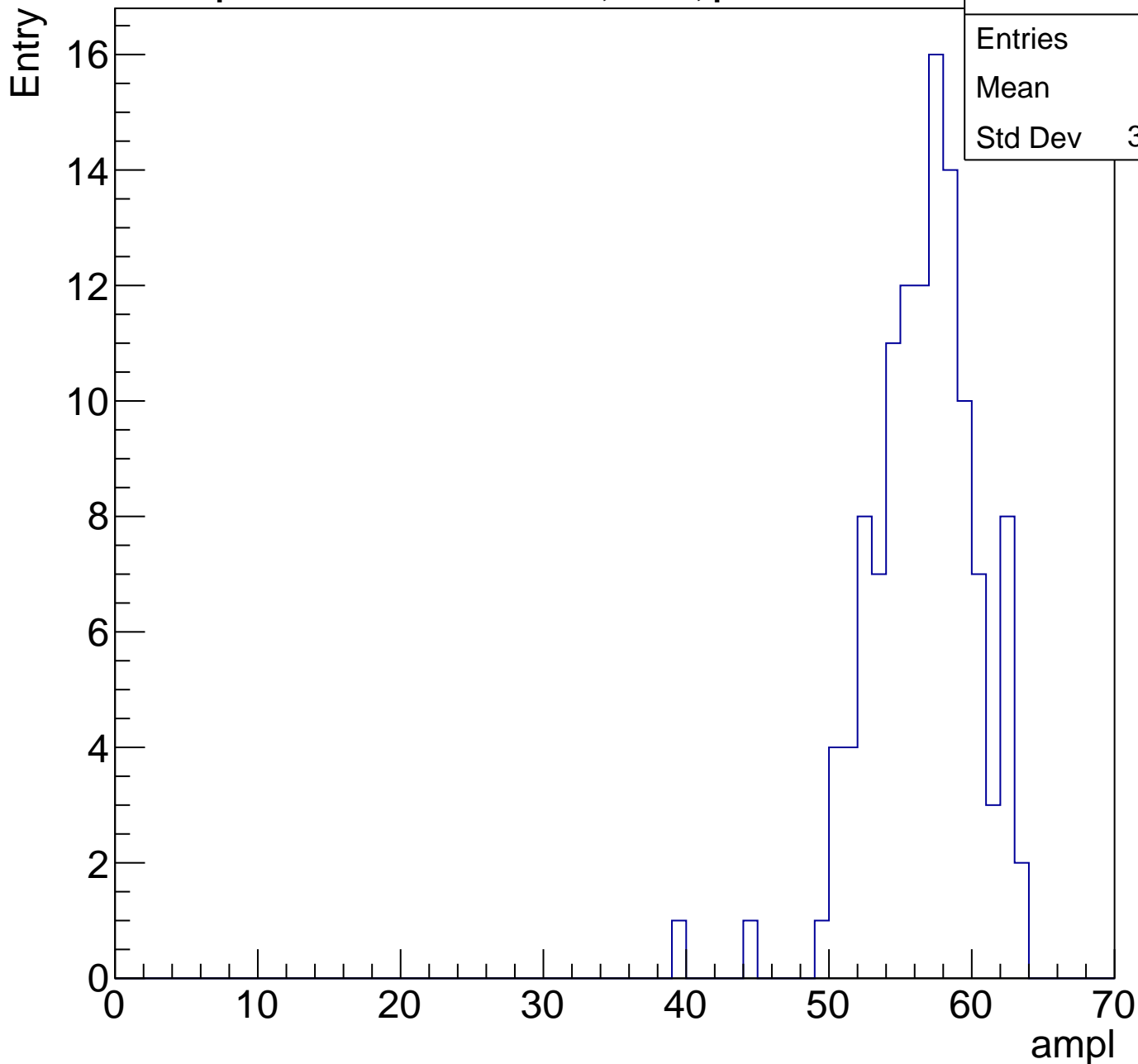
Entries	129
Mean	50.64
Std Dev	3.508



B0L101S, U21-ch115, adc4

calib_packv5_042523_0143.root, FC#1, port C1

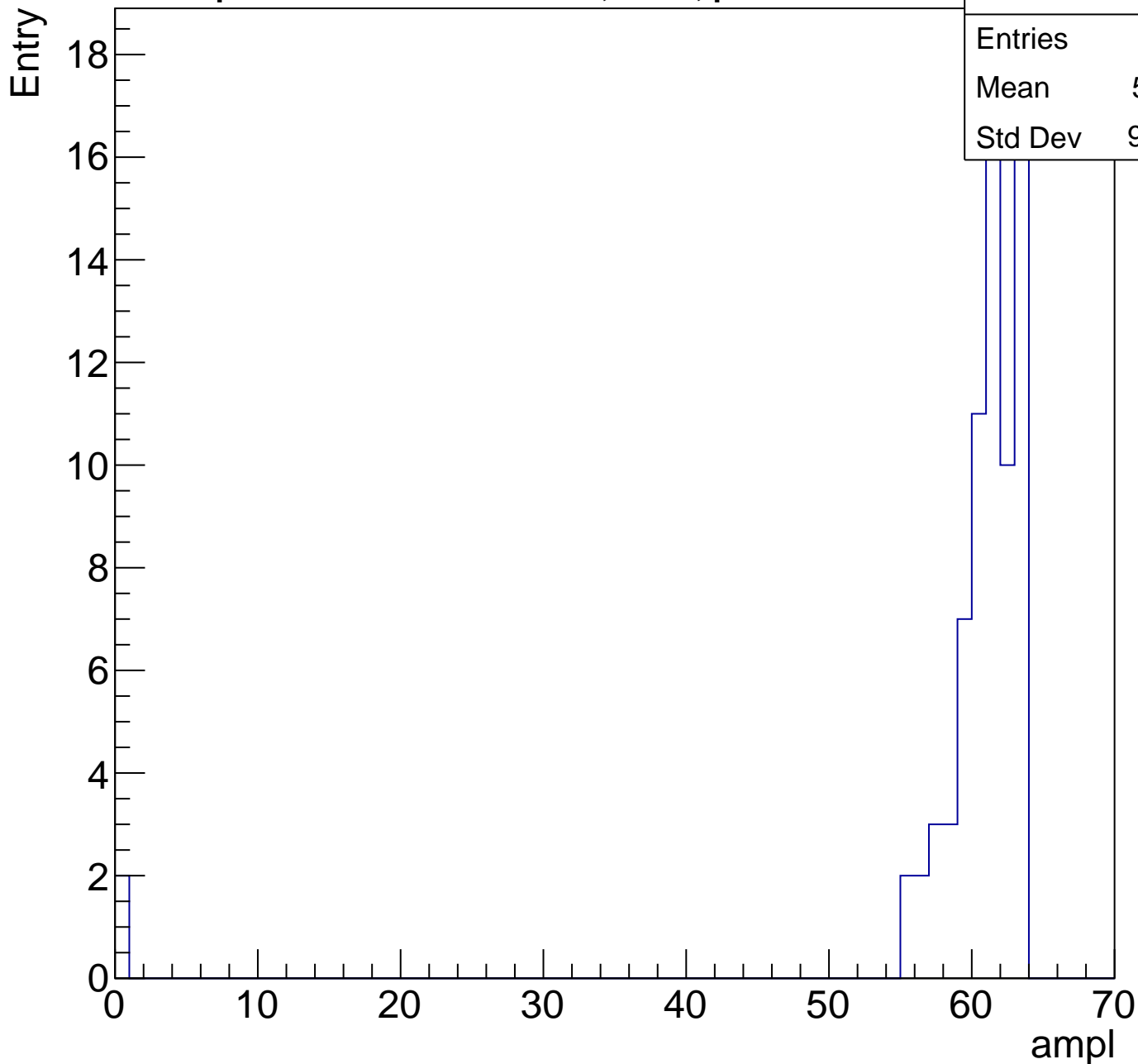
Entries	121
Mean	56.1
Std Dev	3.756



B0L101S, U21-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	76
Mean	59.11
Std Dev	9.925



B0L101S, U21-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	4
Mean	61
Std Dev	1

B0L101S, U21-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch116, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	29.2
Std Dev	6.636

Gaus mean : 29.8756

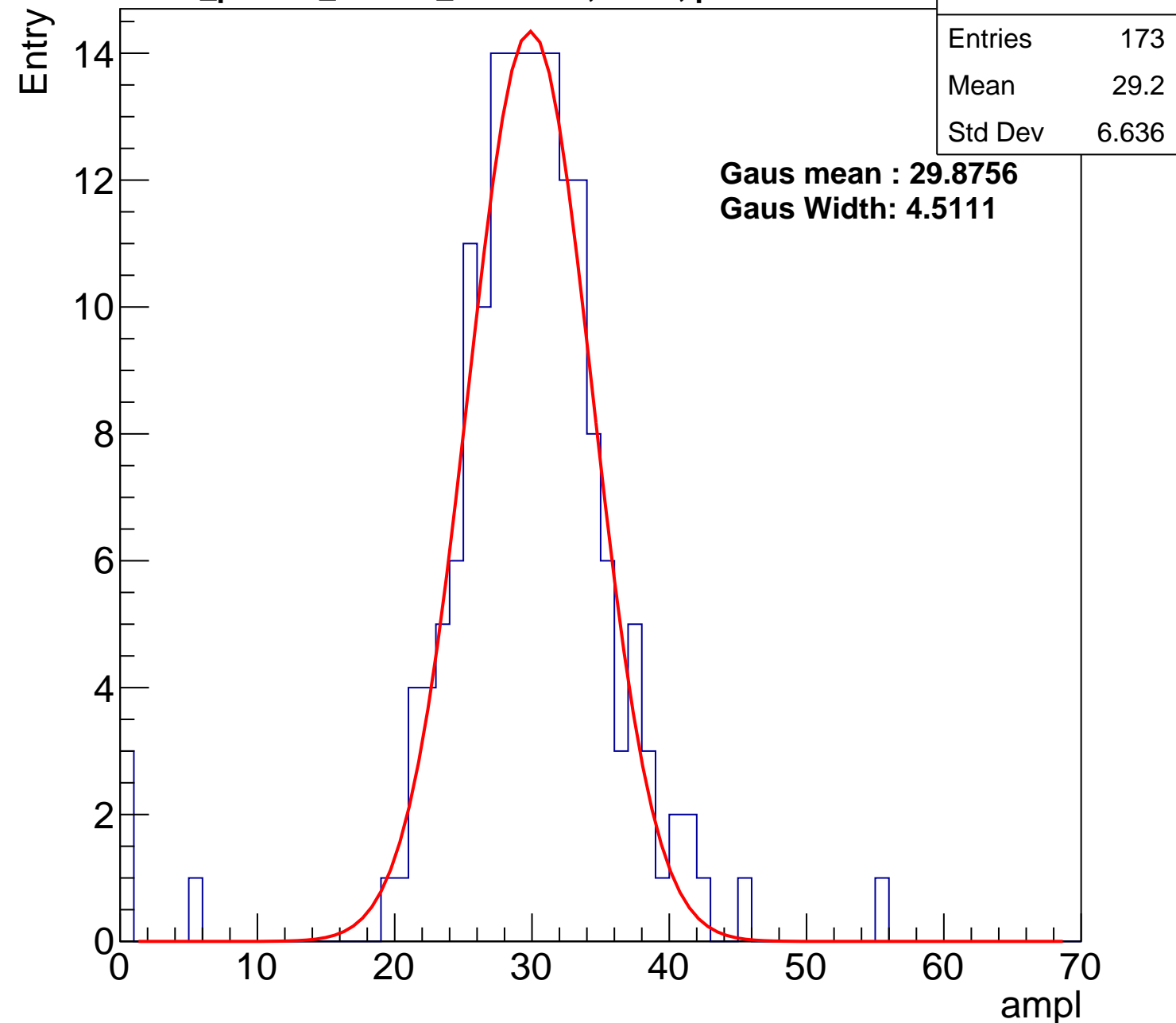
Gaus Width: 4.5111

Entry

14
12
10
8
6
4
2
0

ampl

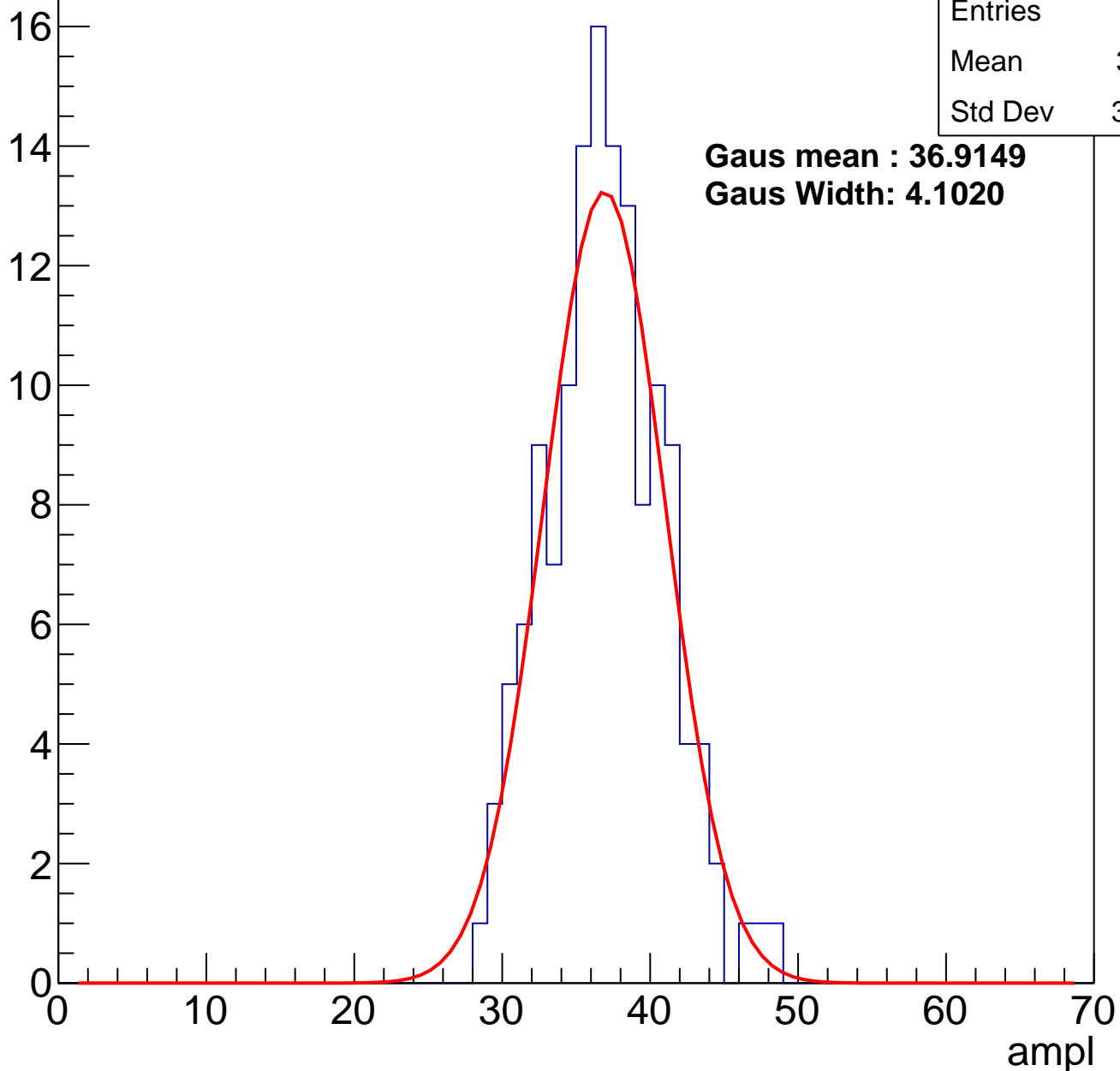
0 10 20 30 40 50 60 70



B0L101S, U21-ch116, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

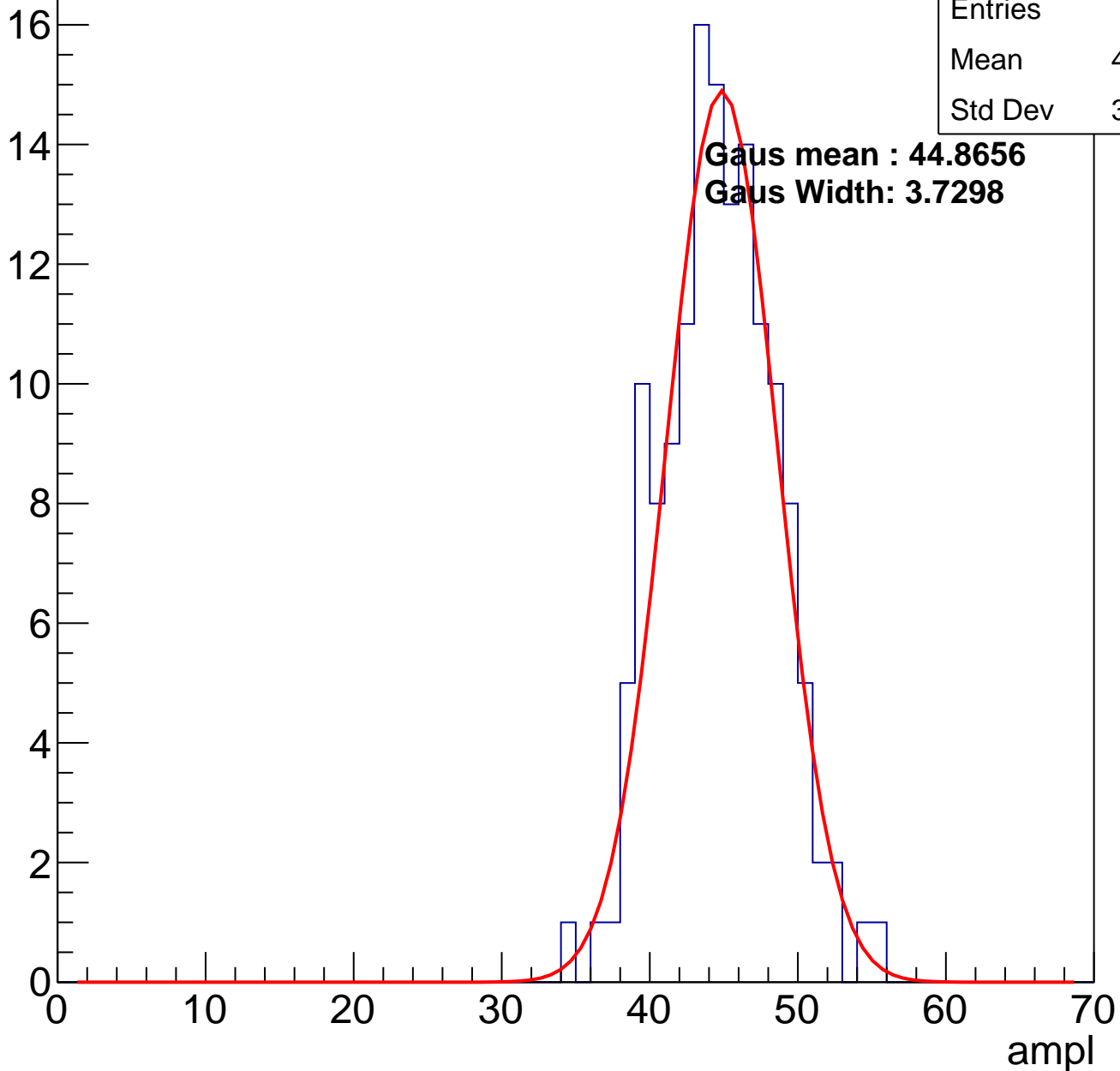


Entries	138
Mean	36.51
Std Dev	3.922

B0L101S, U21-ch116, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

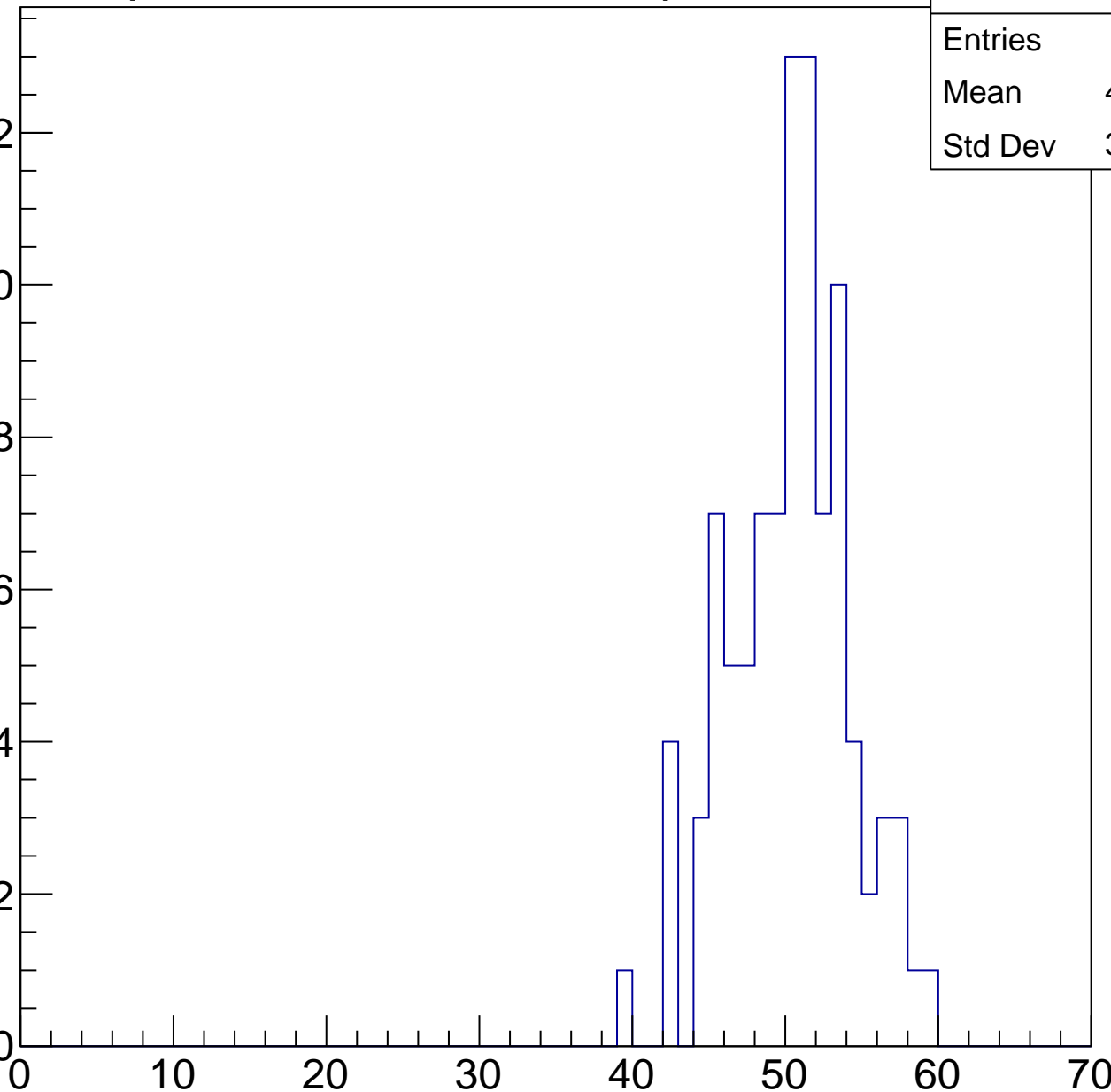
0

Entries 96

Mean 49.86

Std Dev 3.904

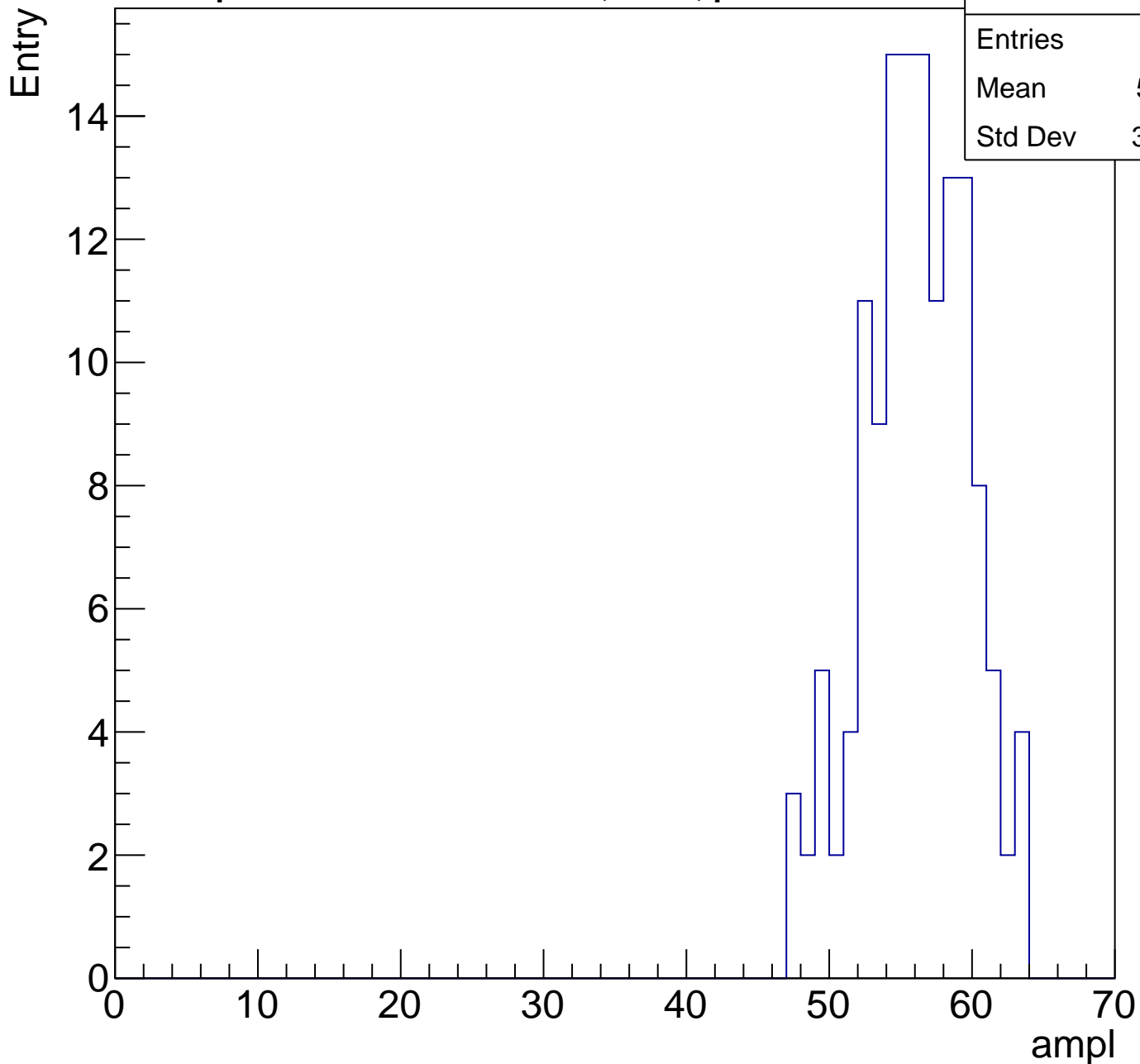
ampl



B0L101S, U21-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	55.61
Std Dev	3.625



B0L101S, U21-ch116, adc5

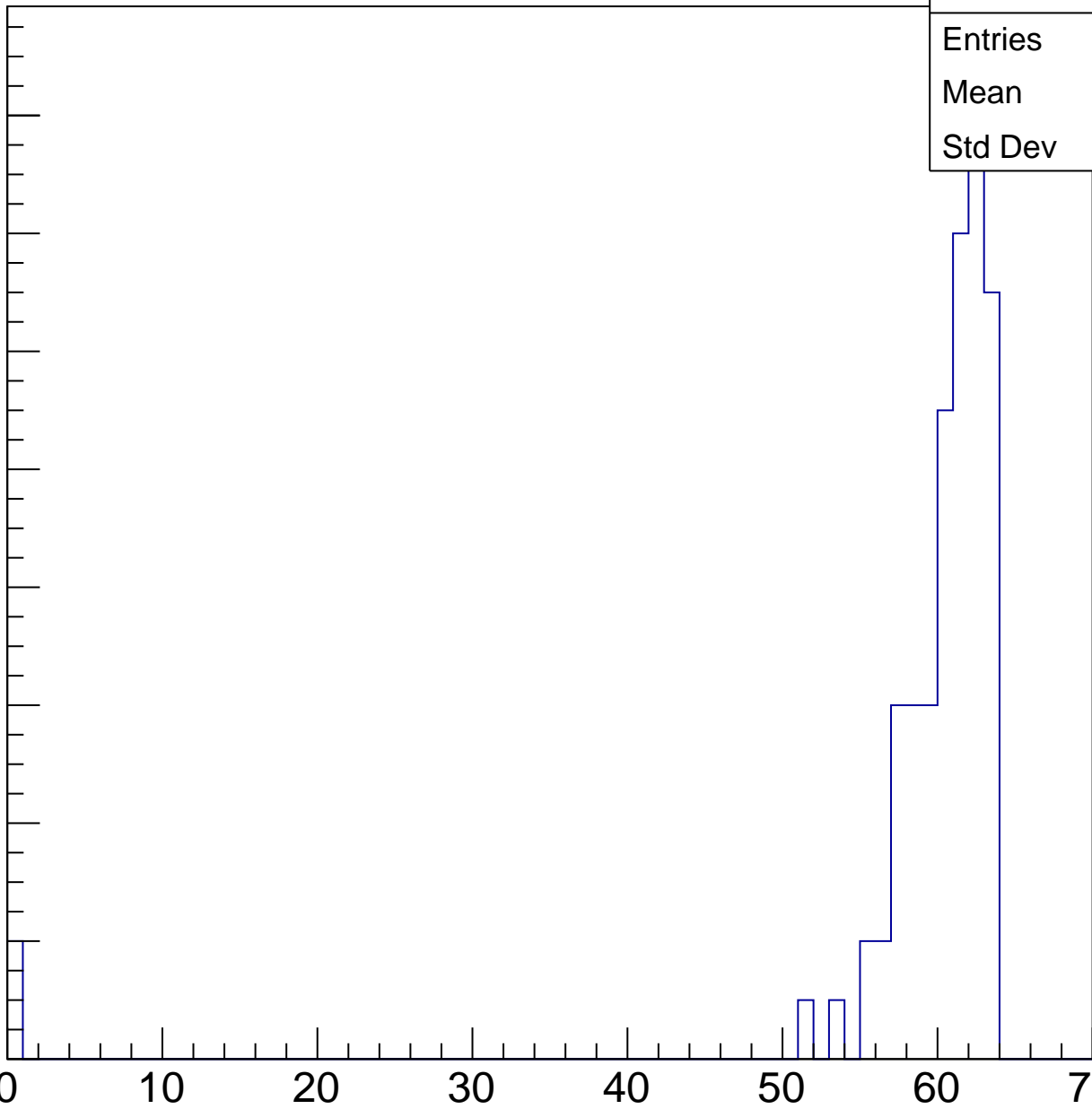
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.73
Std Dev	9.663

ampl



B0L101S, U21-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch117, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	27.47
Std Dev	4.935

Gaus mean : 28.1852

Gaus Width: 4.0474

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

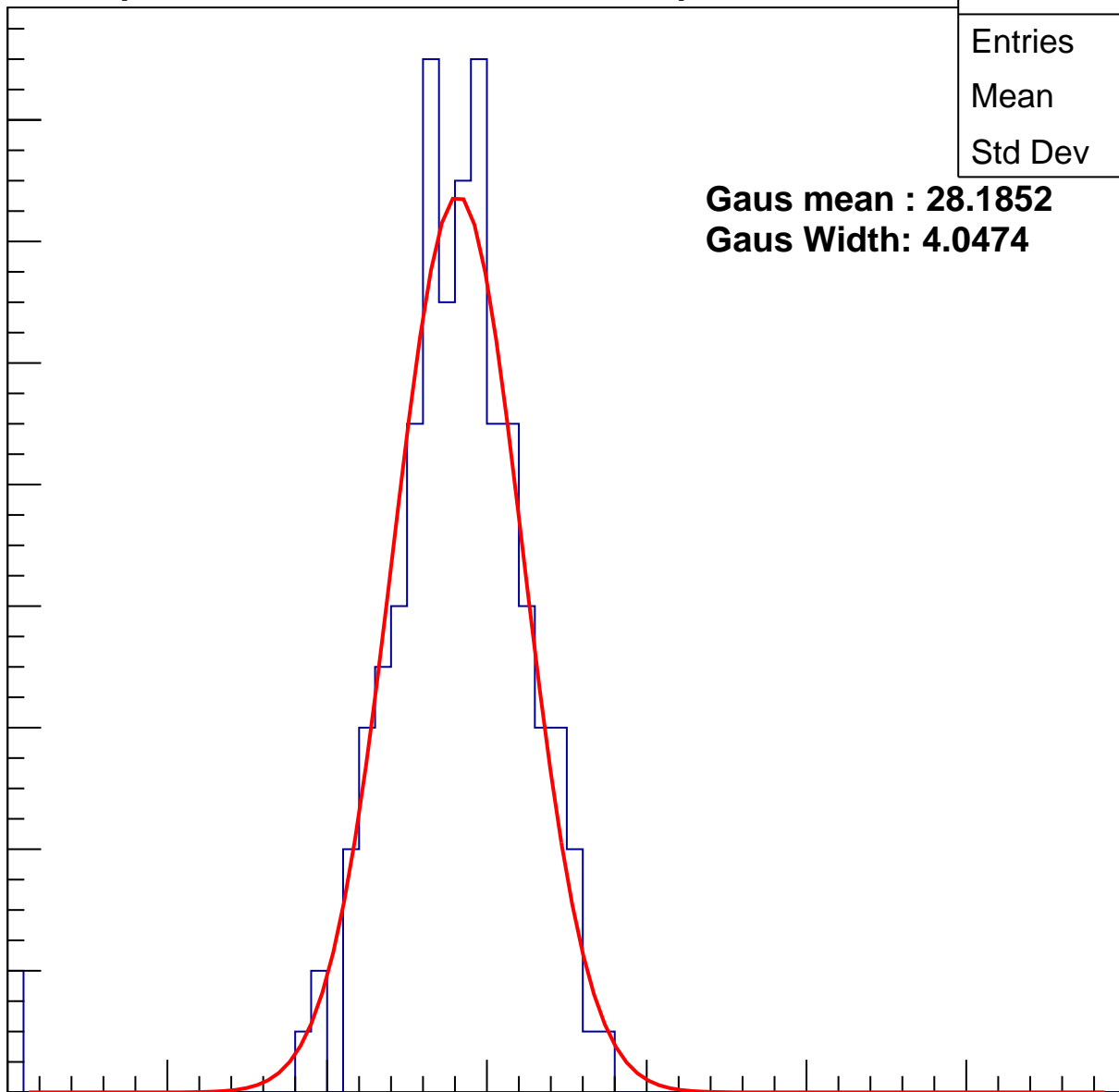
40

50

60

70

ampl



B0L101S, U21-ch117, adc1

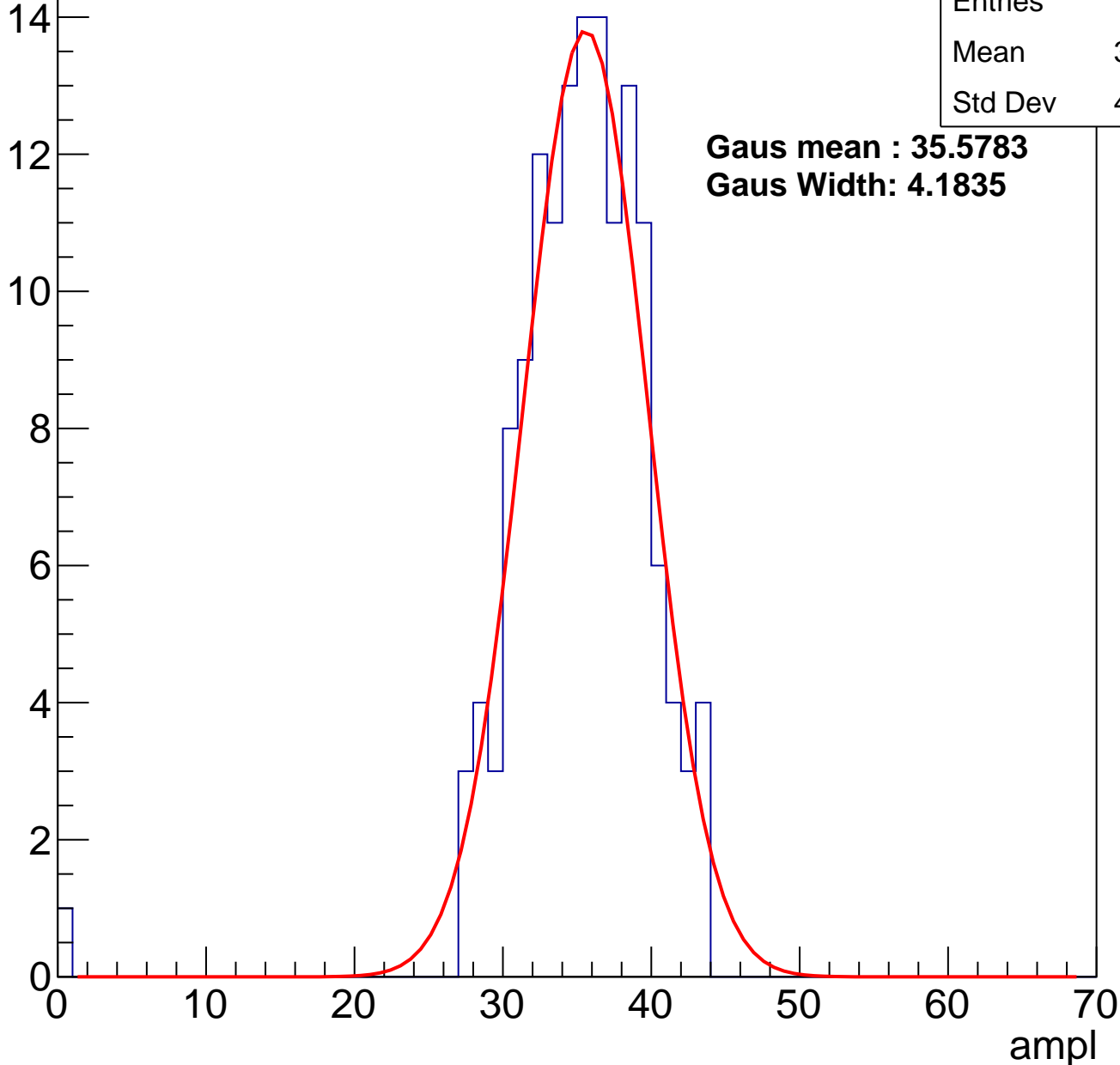
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	34.82
Std Dev	4.768

Gaus mean : 35.5783

Gaus Width: 4.1835

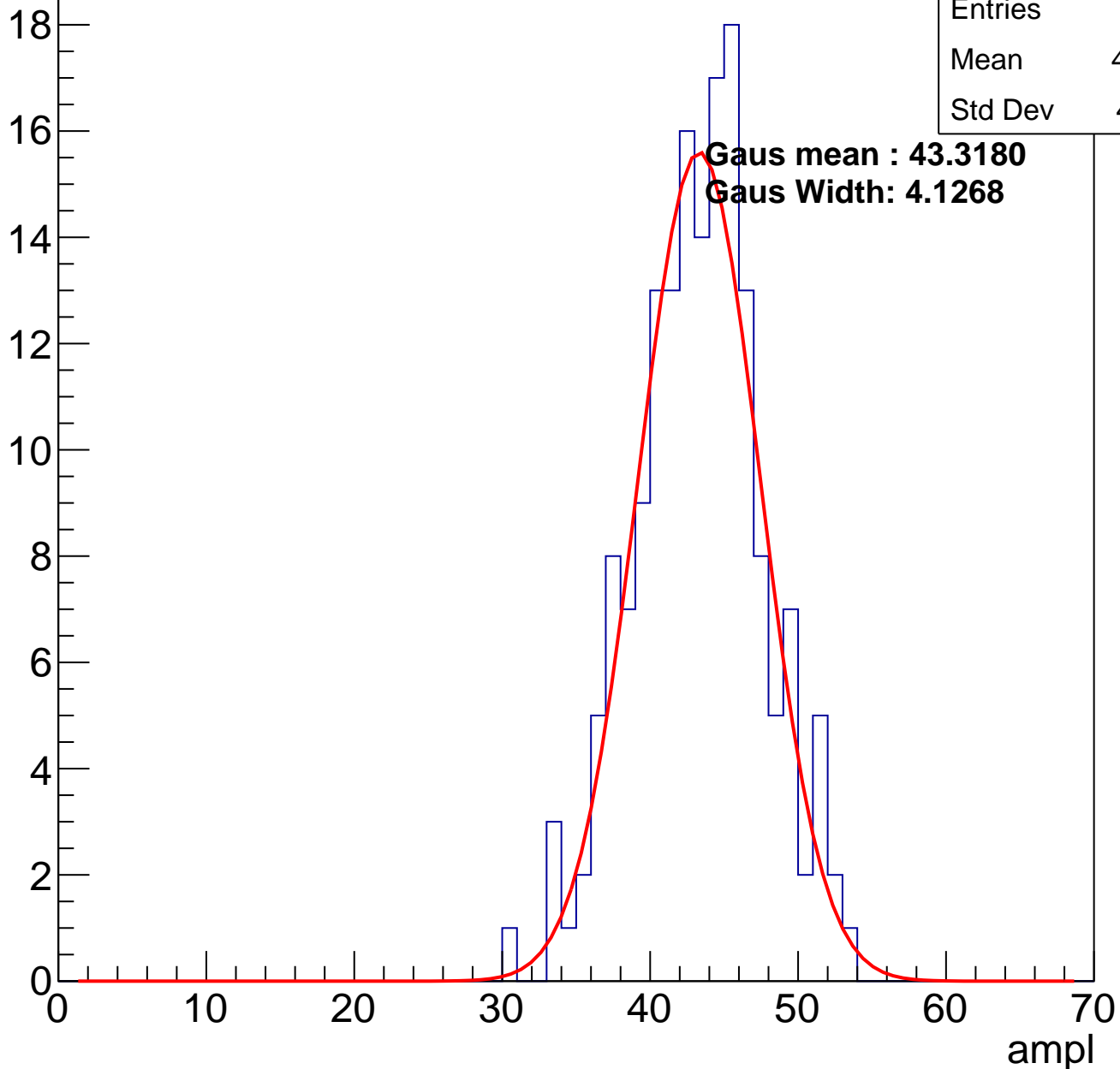
Entry



B0L101S, U21-ch117, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

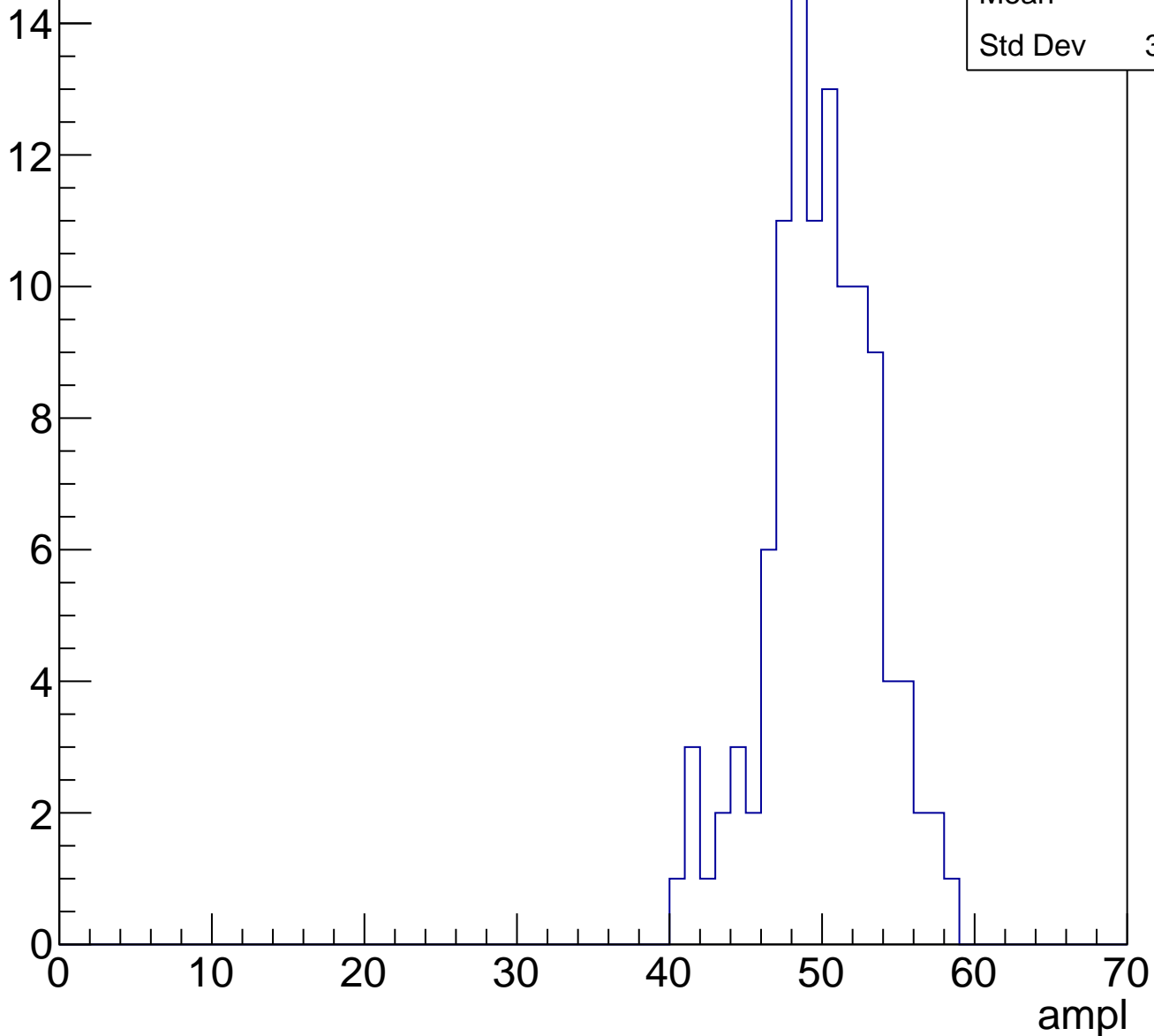


B0L101S, U21-ch117, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	49.47
Std Dev	3.602

Entry



B0L101S, U21-ch117, adc4

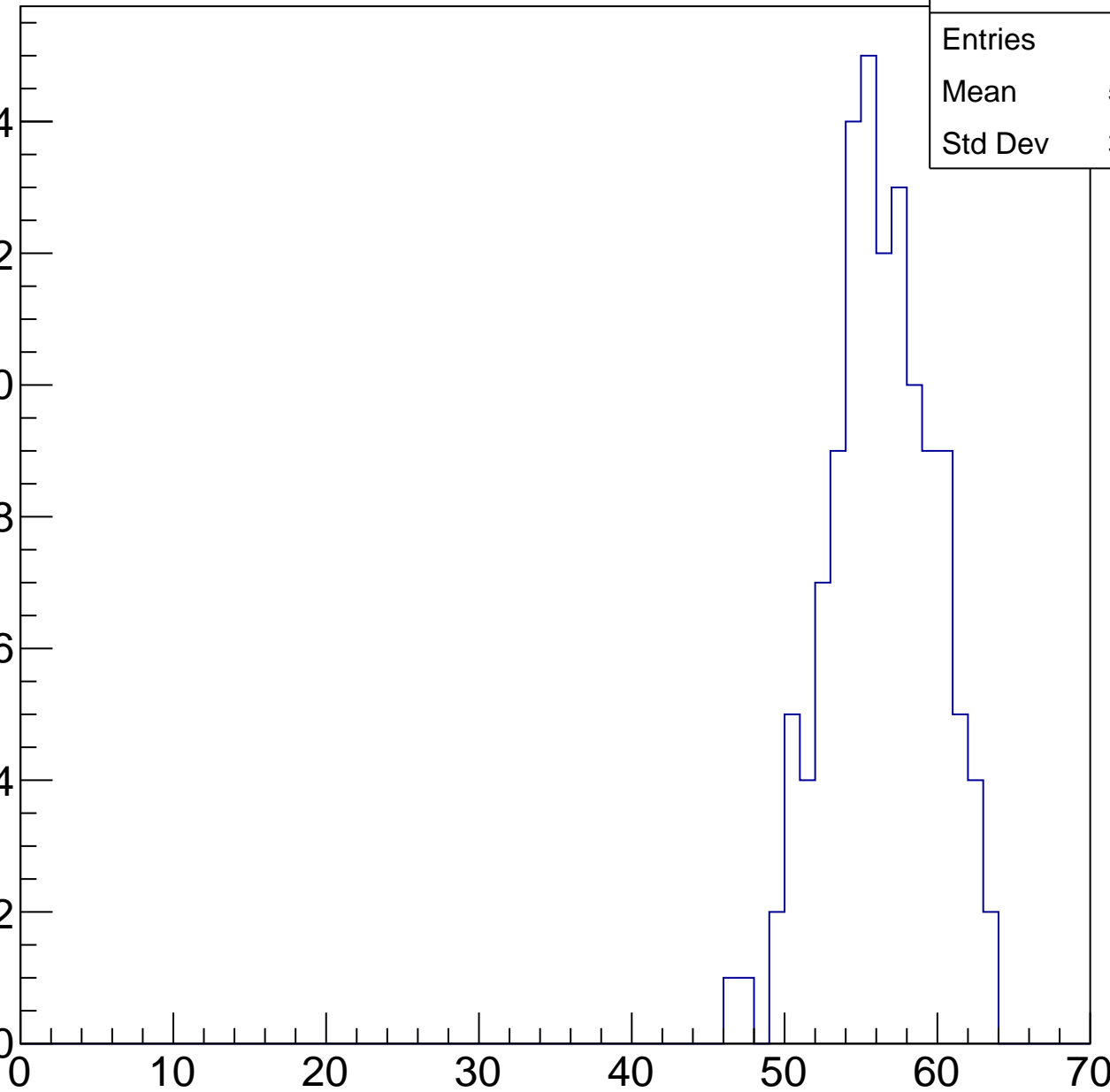
calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	55.82
Std Dev	3.464

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch117, adc5

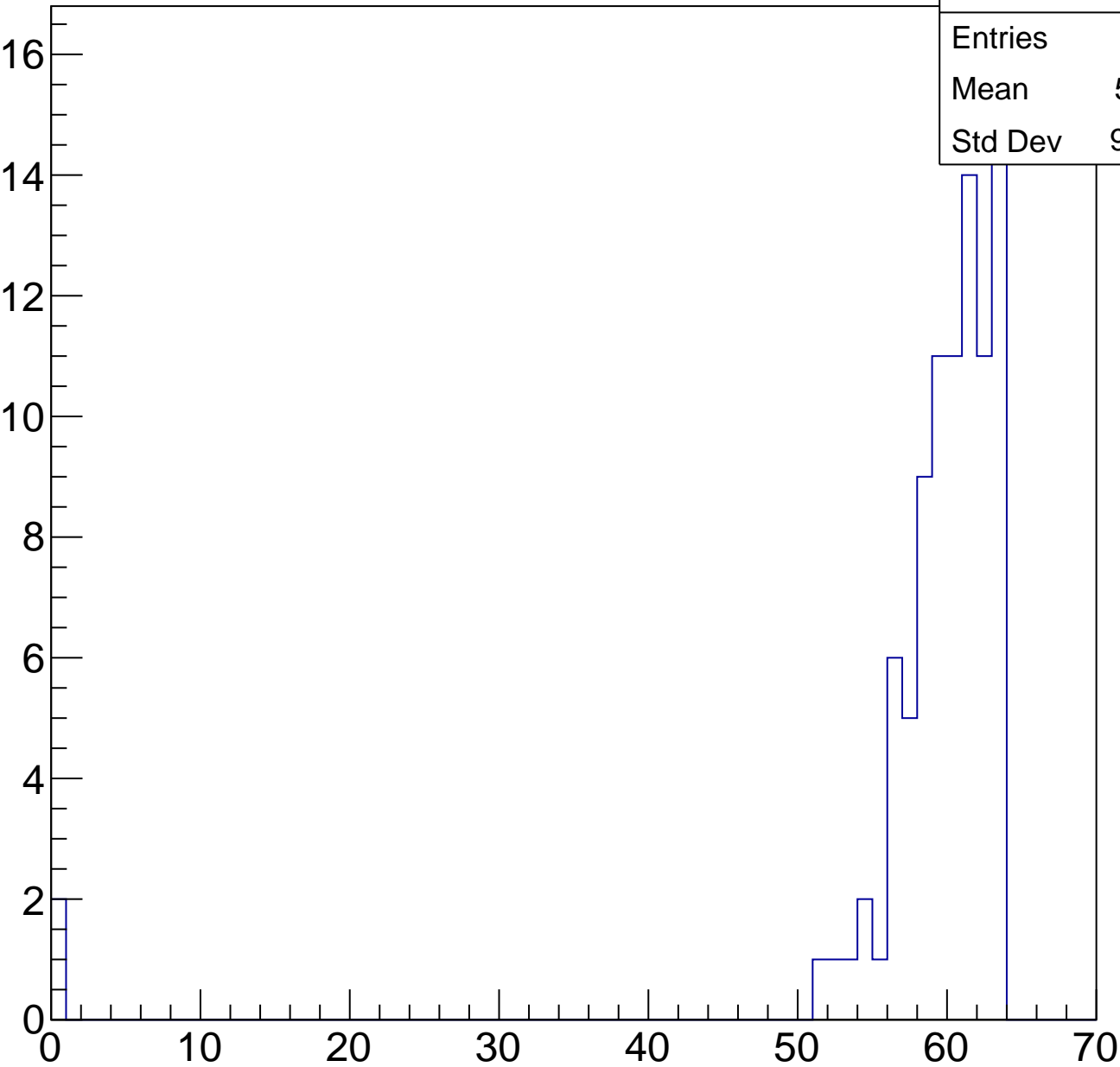
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.41
Std Dev	9.169

ampl

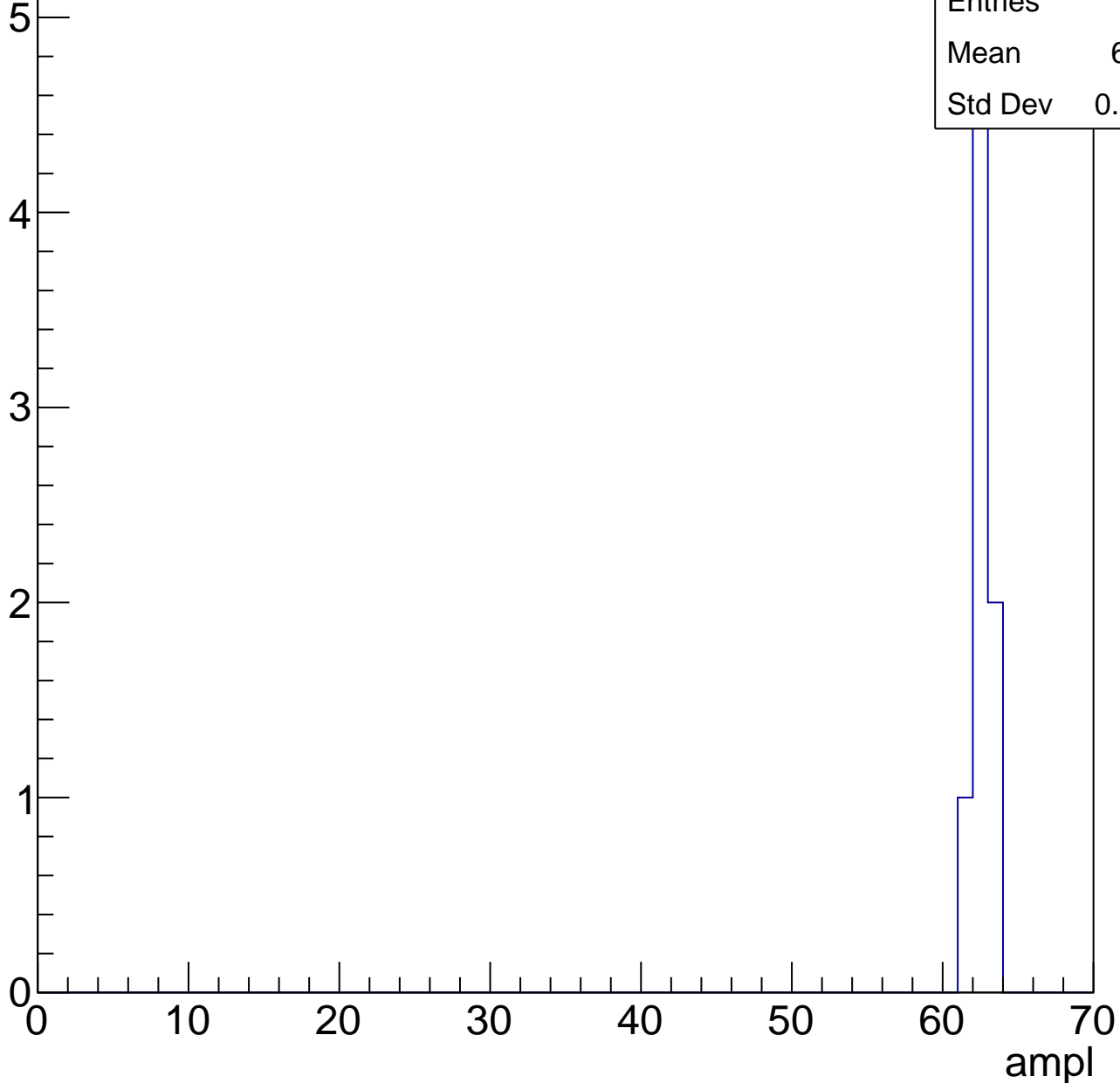


B0L101S, U21-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	62.12
Std Dev	0.5995



B0L101S, U21-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch118, adc0

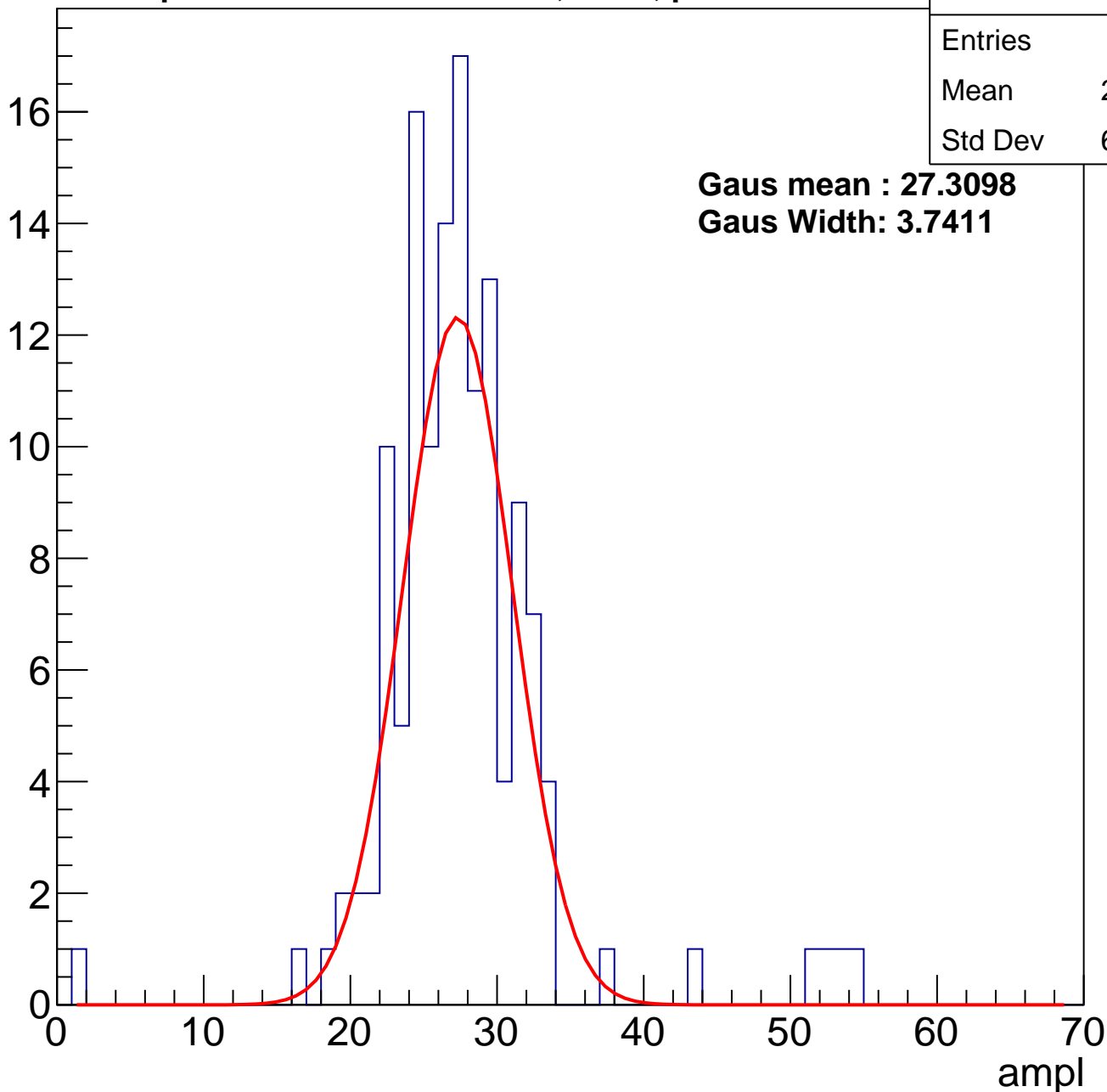
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	27.24
Std Dev	6.218

Gaus mean : 27.3098

Gaus Width: 3.7411

Entry



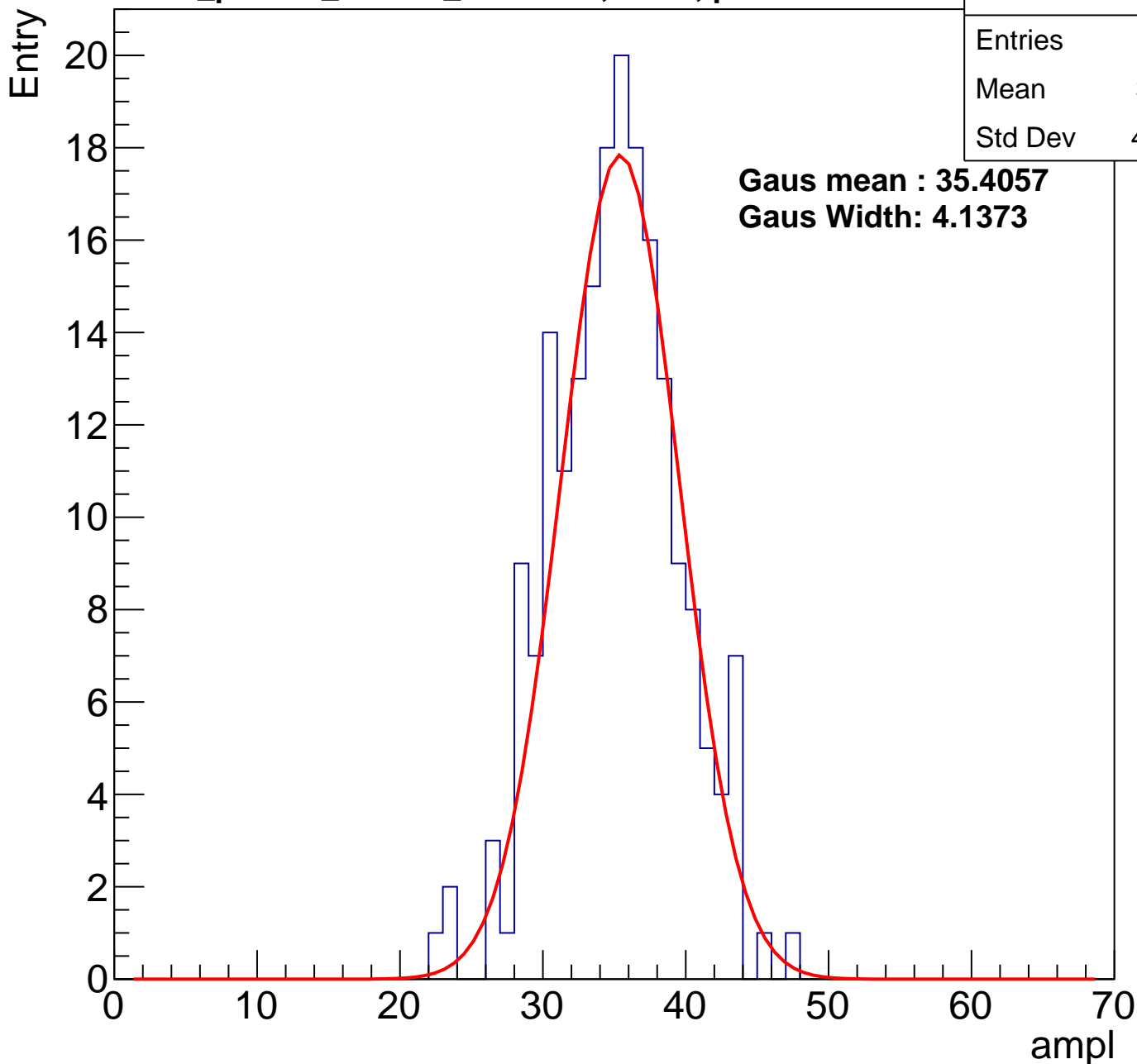
B0L101S, U21-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	196
Mean	34.61
Std Dev	4.379

Gaus mean : 35.4057

Gaus Width: 4.1373



B0L101S, U21-ch118, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

122

Mean

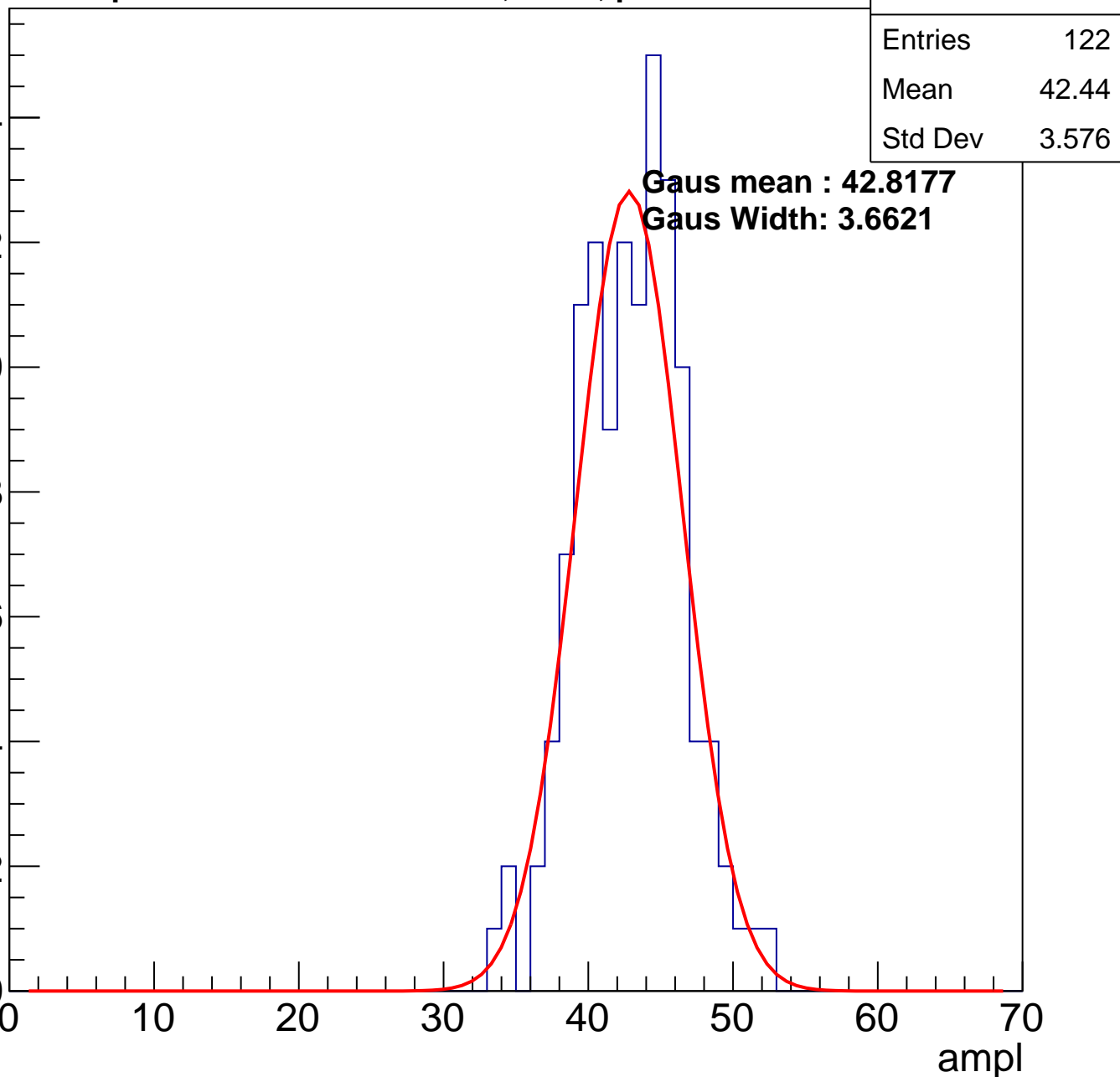
42.44

Std Dev

3.576

Gaus mean : 42.8177

Gaus Width: 3.6621

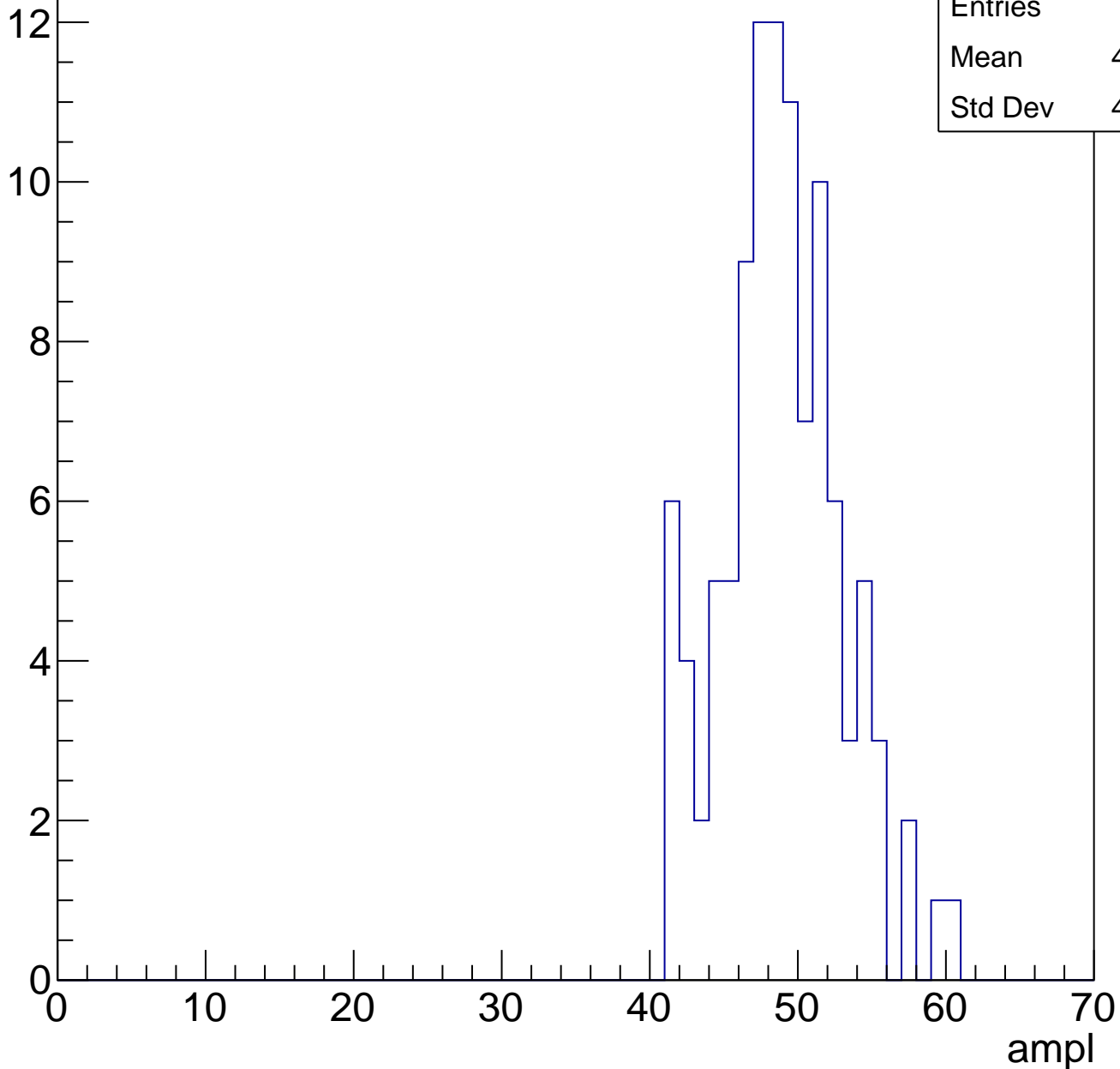


B0L101S, U21-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	48.43
Std Dev	4.038

Entry



B0L101S, U21-ch118, adc4

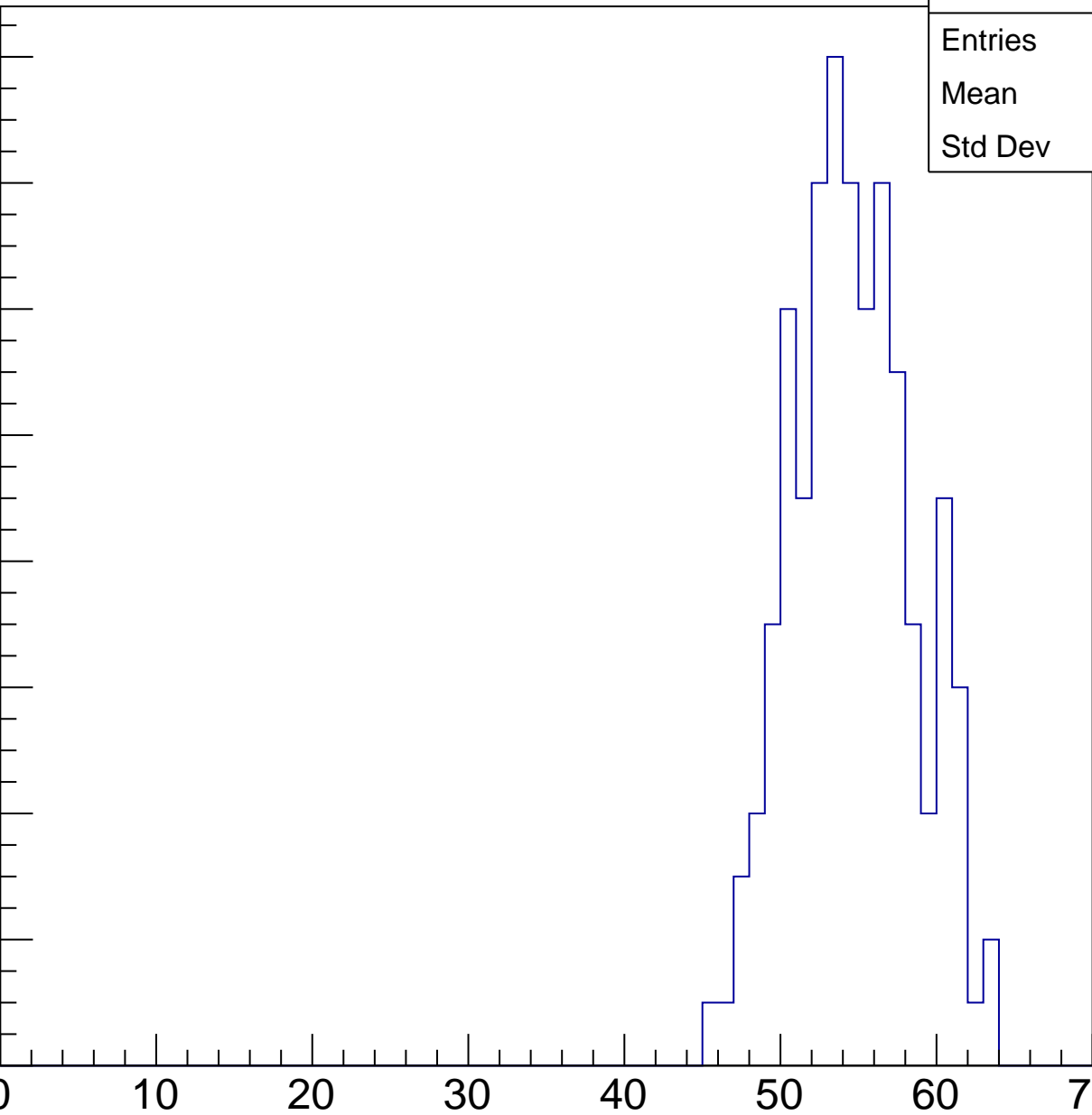
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	147
Mean	54.18
Std Dev	3.846

ampl



B0L101S, U21-ch118, adc5

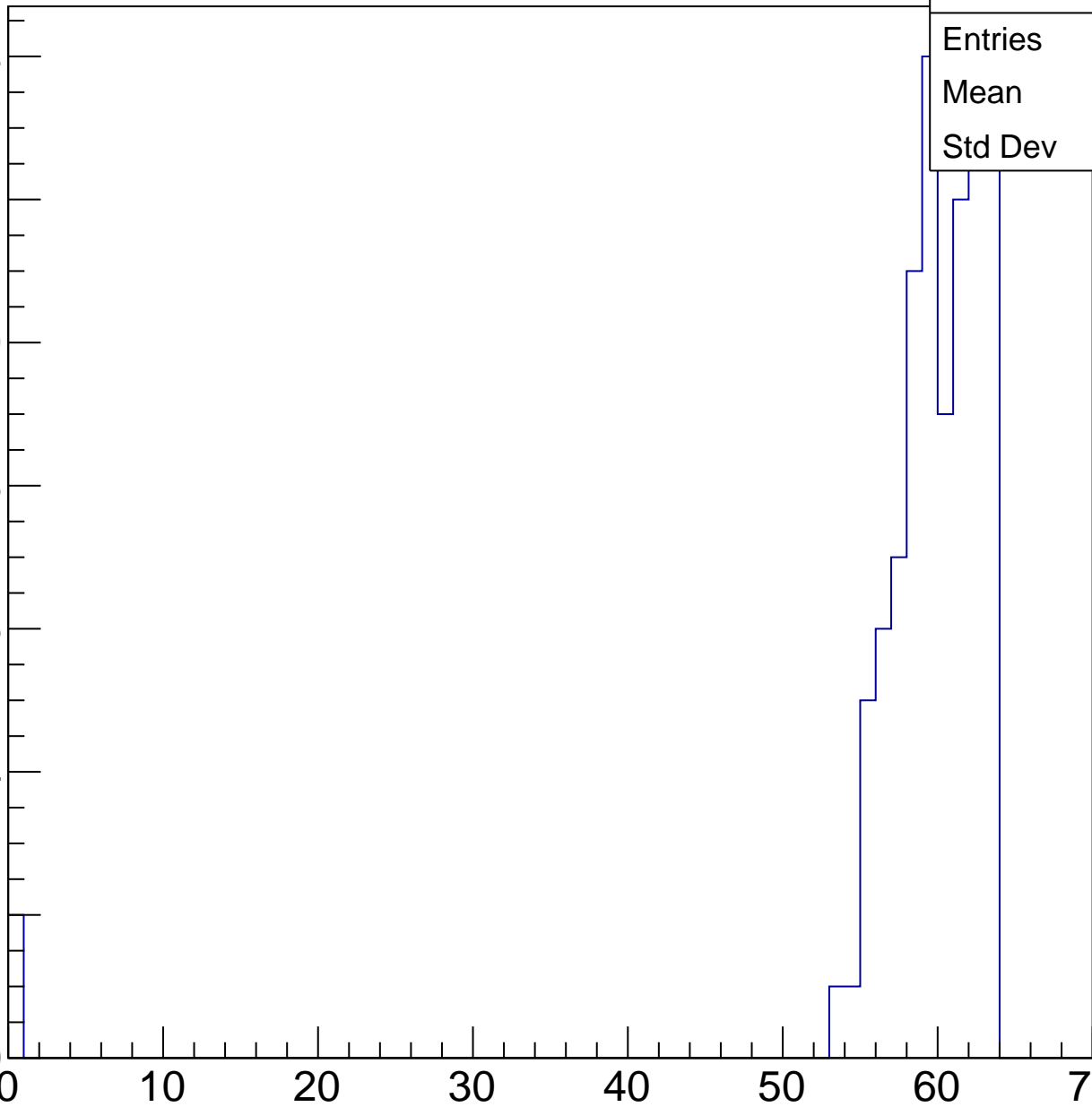
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	58.28
Std Dev	8.95

ampl

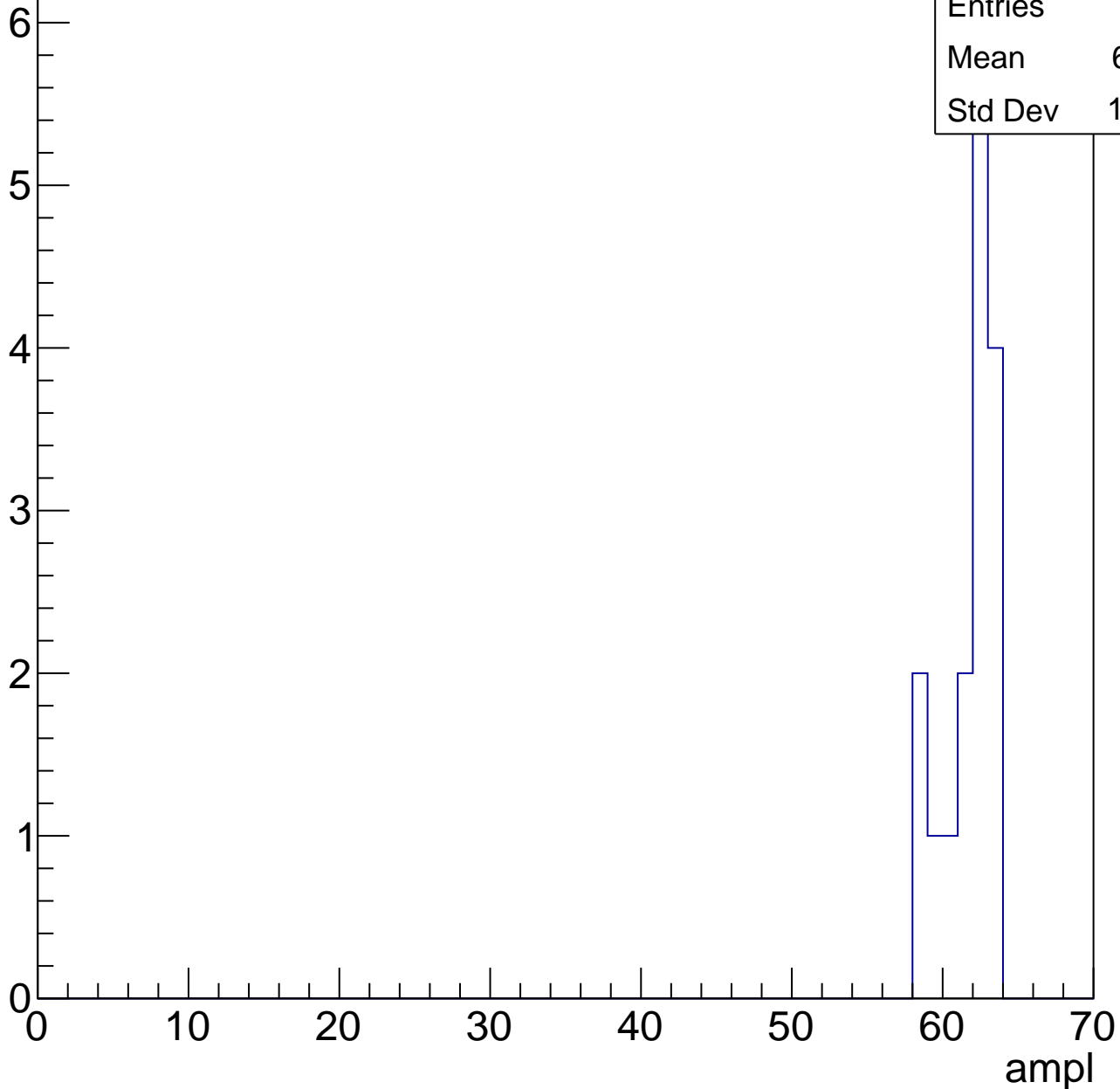


B0L101S, U21-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

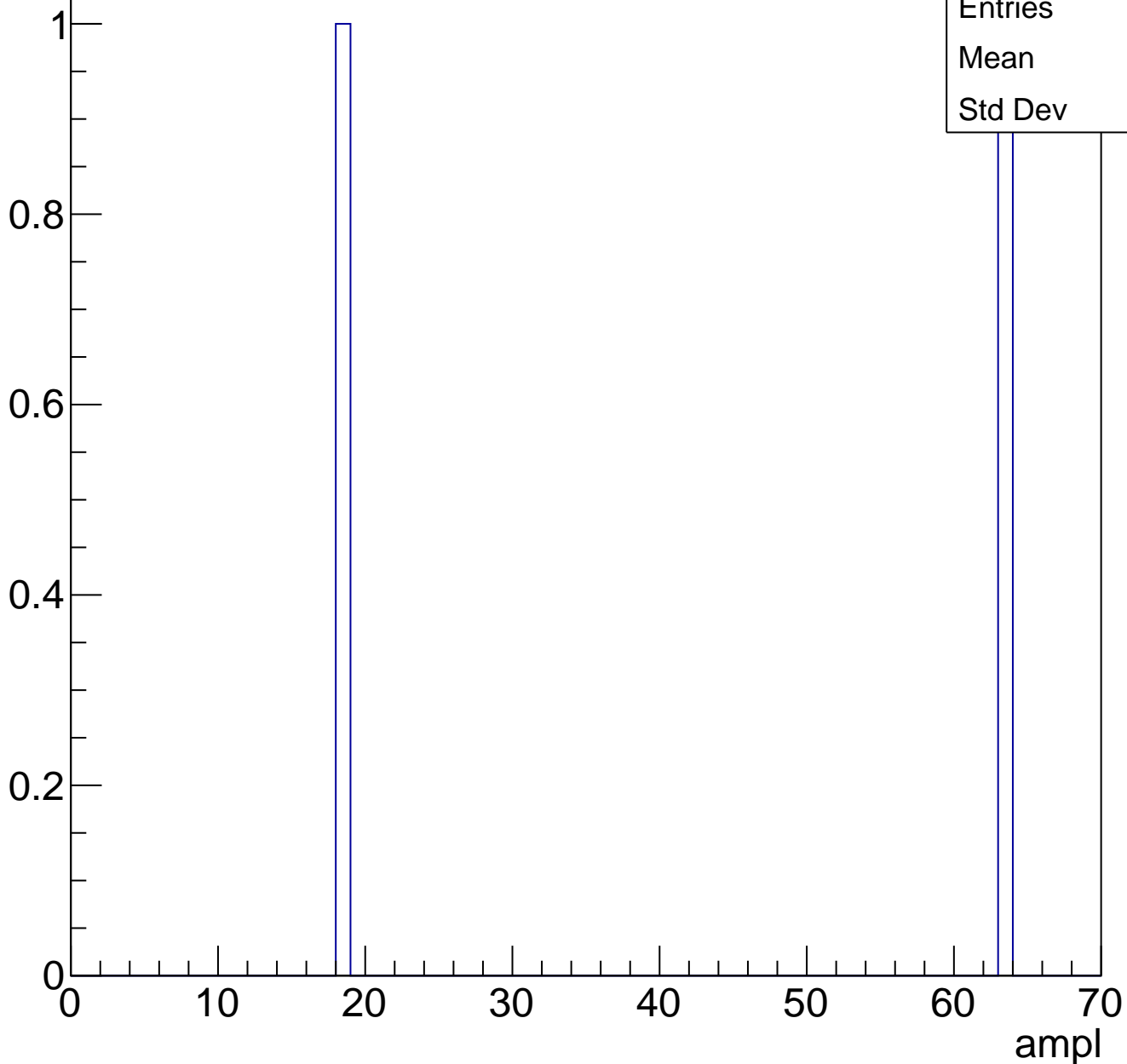
Entries	16
Mean	61.31
Std Dev	1.648



B0L101S, U21-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



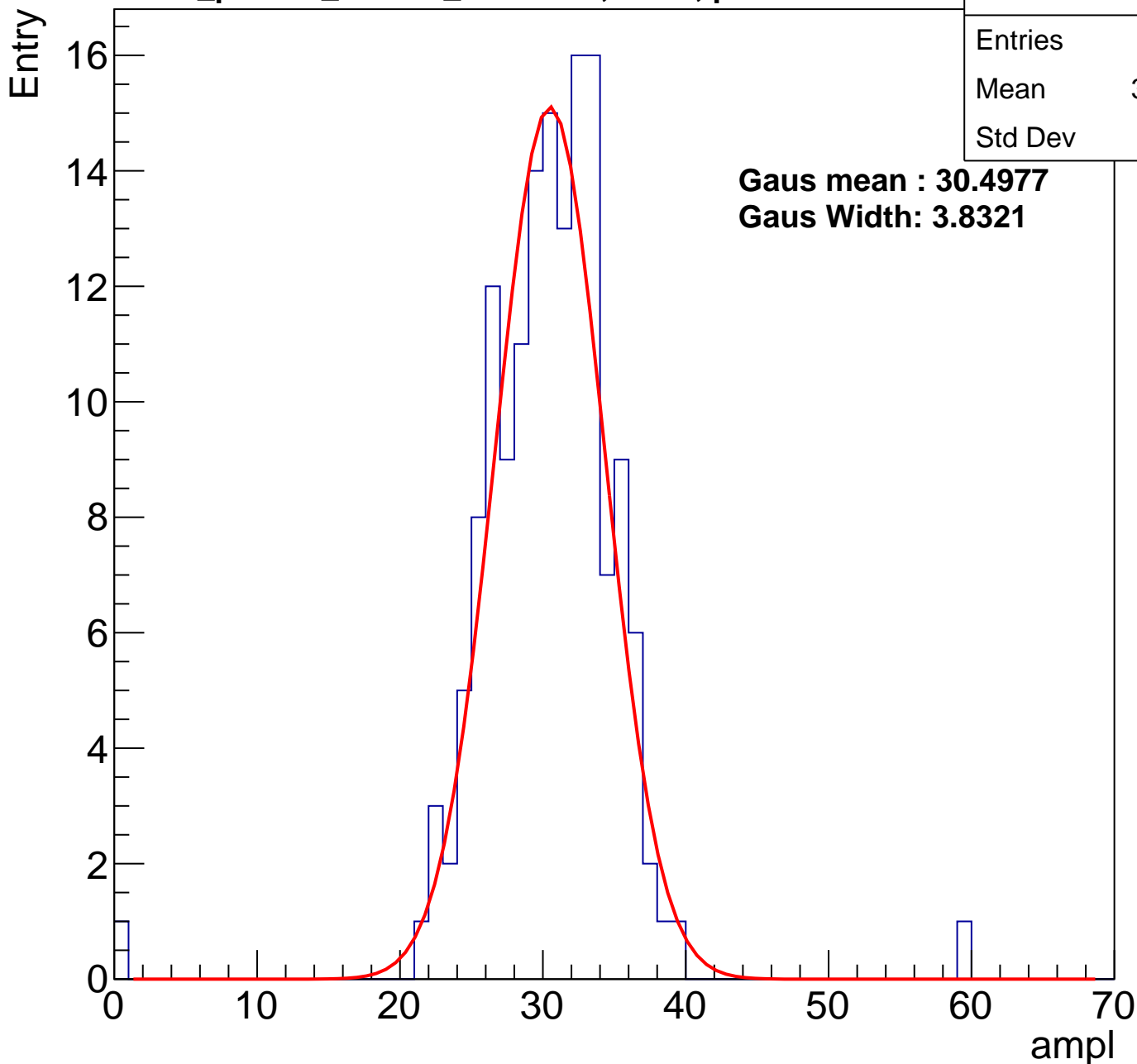
B0L101S, U21-ch119, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	30.03
Std Dev	5.01

Gaus mean : 30.4977

Gaus Width: 3.8321



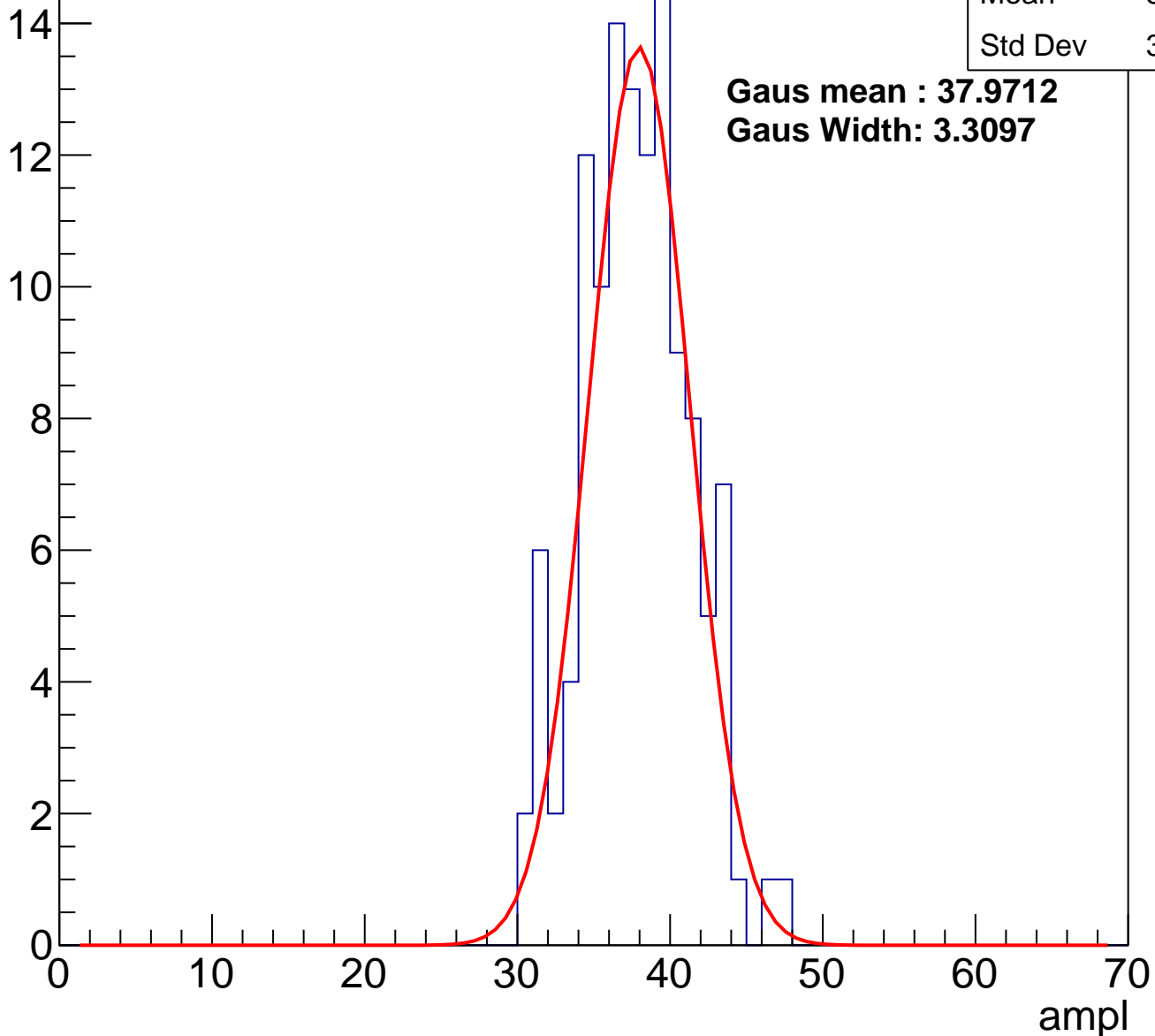
B0L101S, U21-ch119, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	37.39
Std Dev	3.458

Gaus mean : 37.9712
Gaus Width: 3.3097

Entry



B0L101S, U21-ch119, adc2

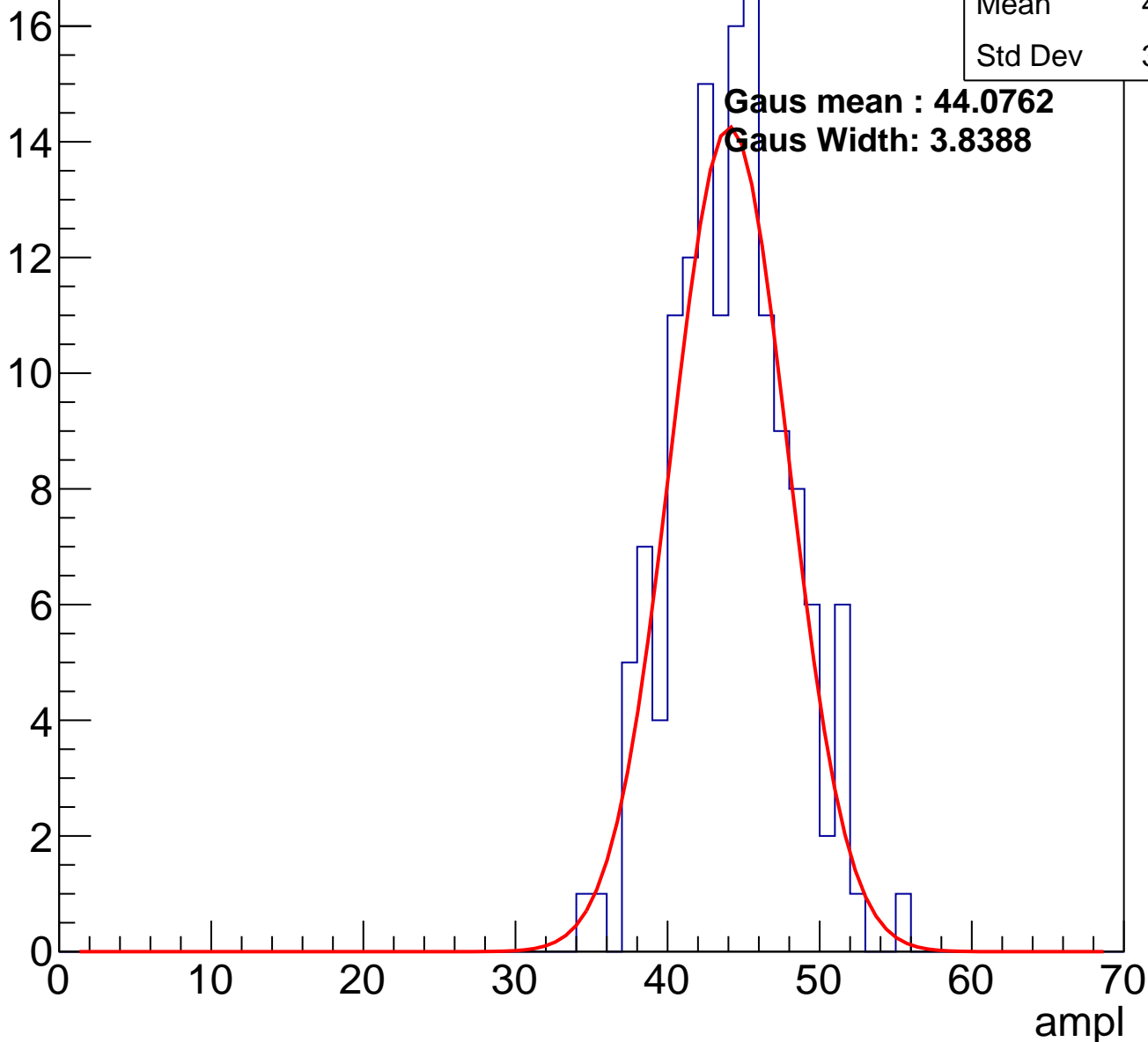
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	144
Mean	43.75
Std Dev	3.814

Gaus mean : 44.0762

Gaus Width: 3.8388



B0L101S, U21-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	50.66
Std Dev	3.335

Entry

16

14

12

10

8

6

4

2

0

0

10

20

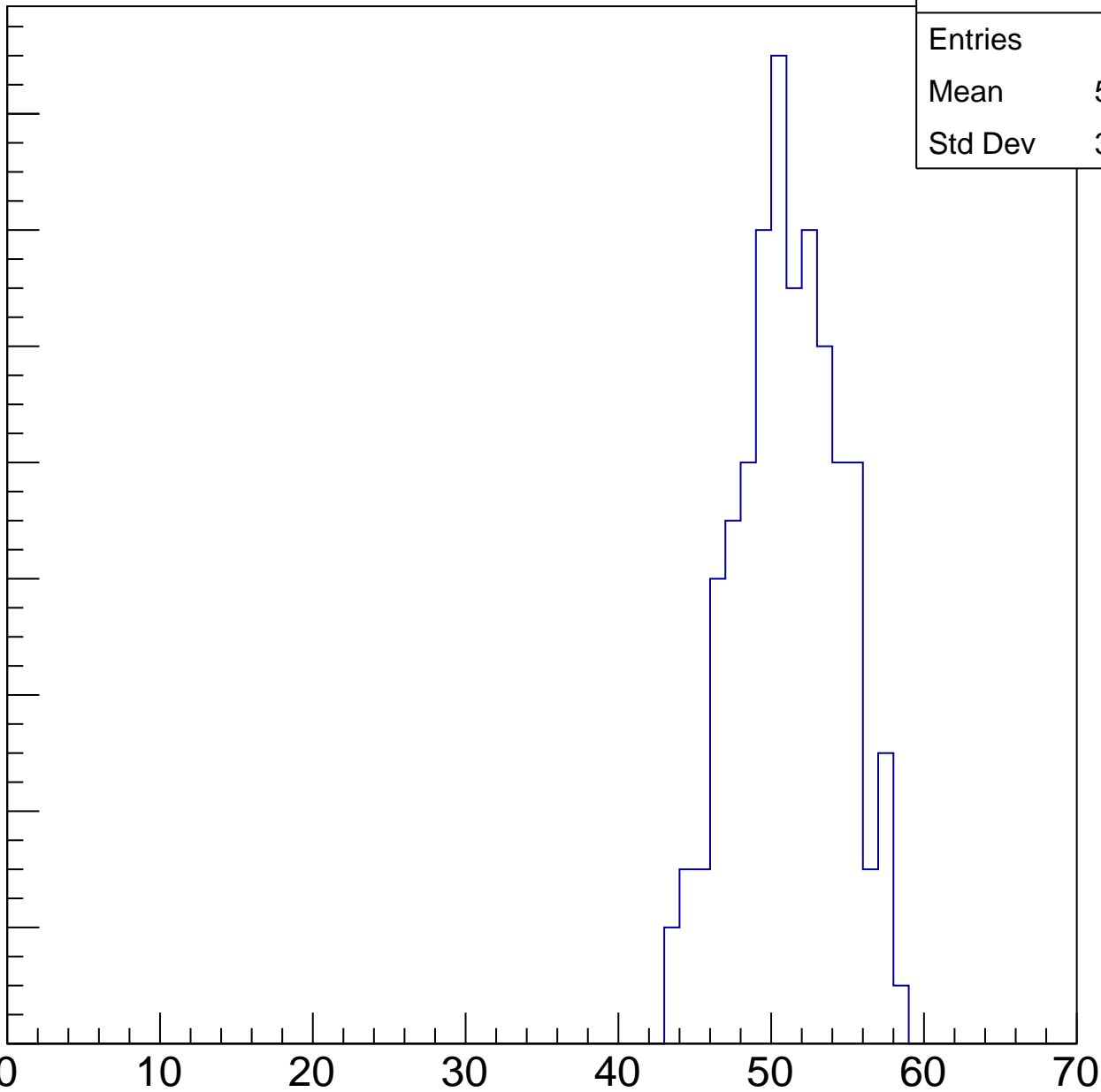
30

40

50

60

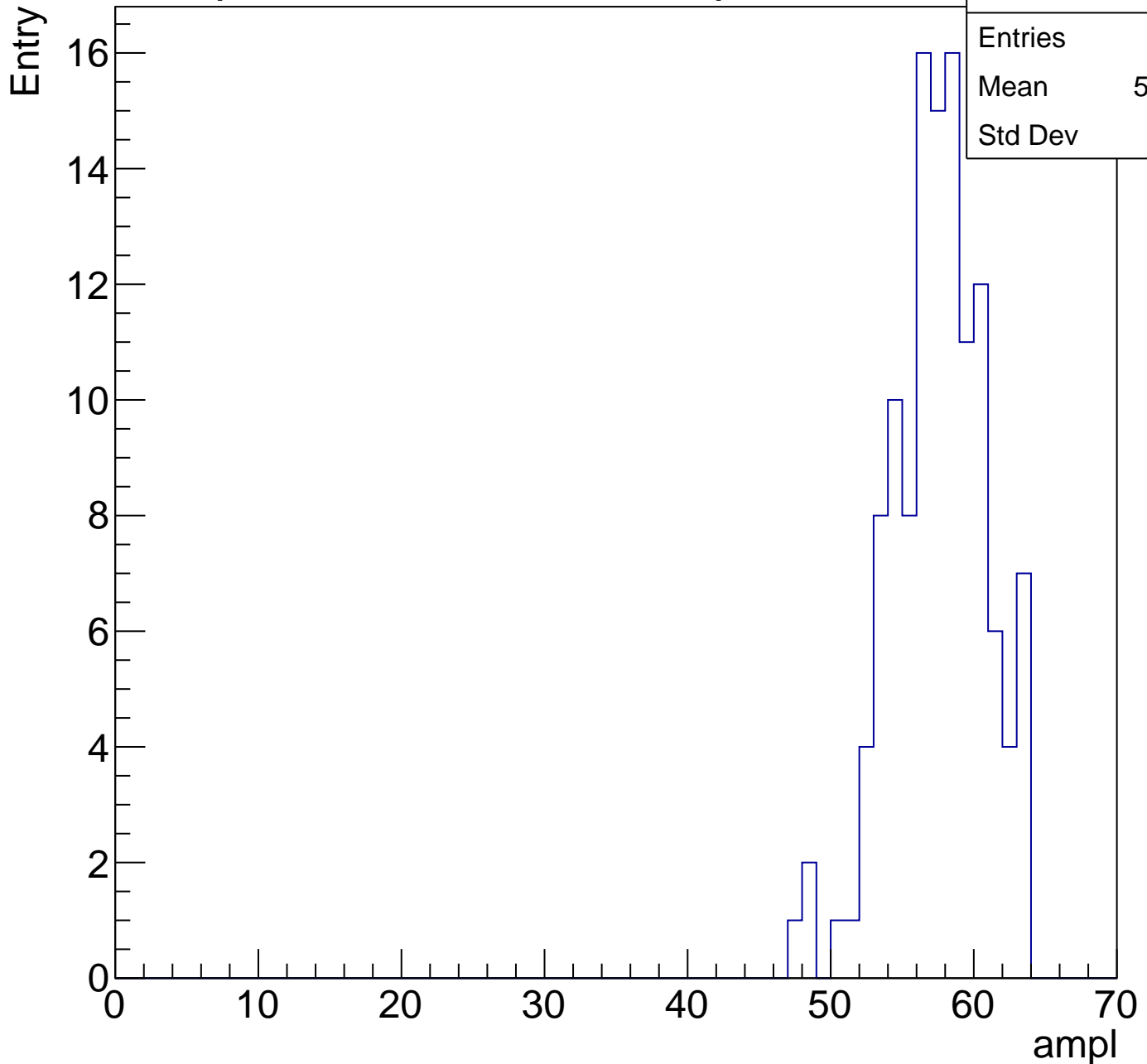
ampl



B0L101S, U21-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	57.04
Std Dev	3.3



B0L101S, U21-ch119, adc5

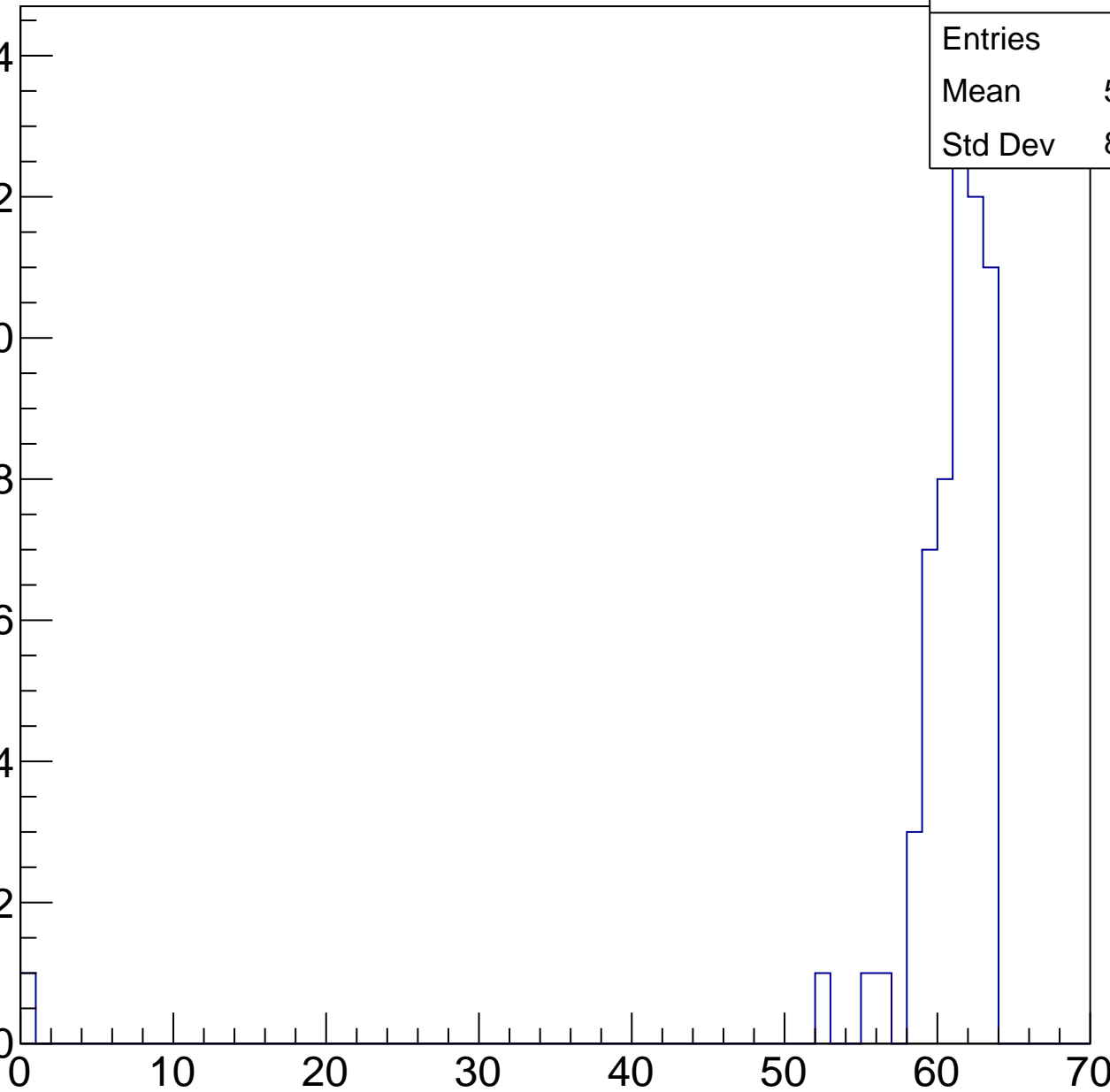
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.68
Std Dev	8.108

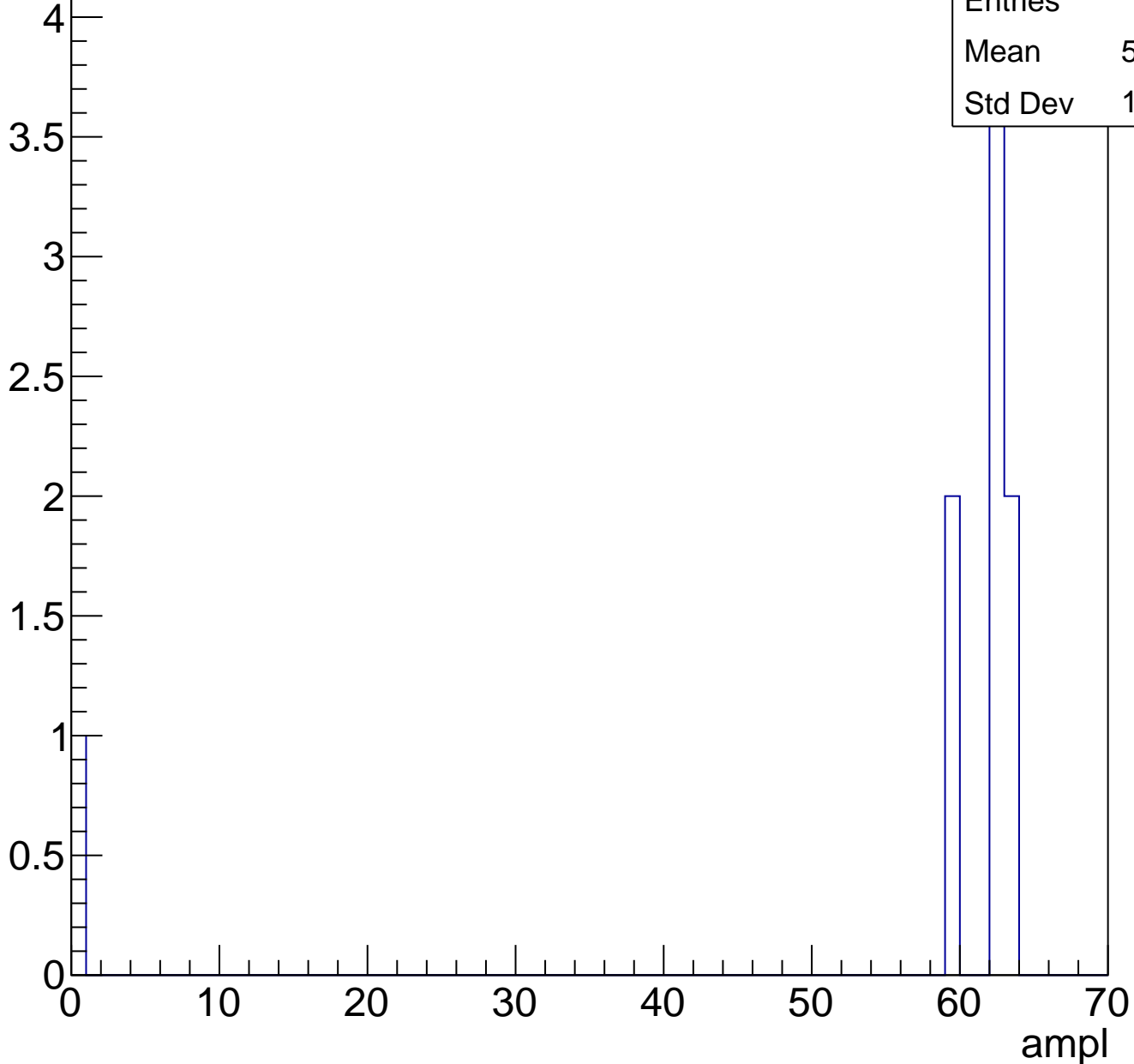
ampl



B0L101S, U21-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch120, adc0

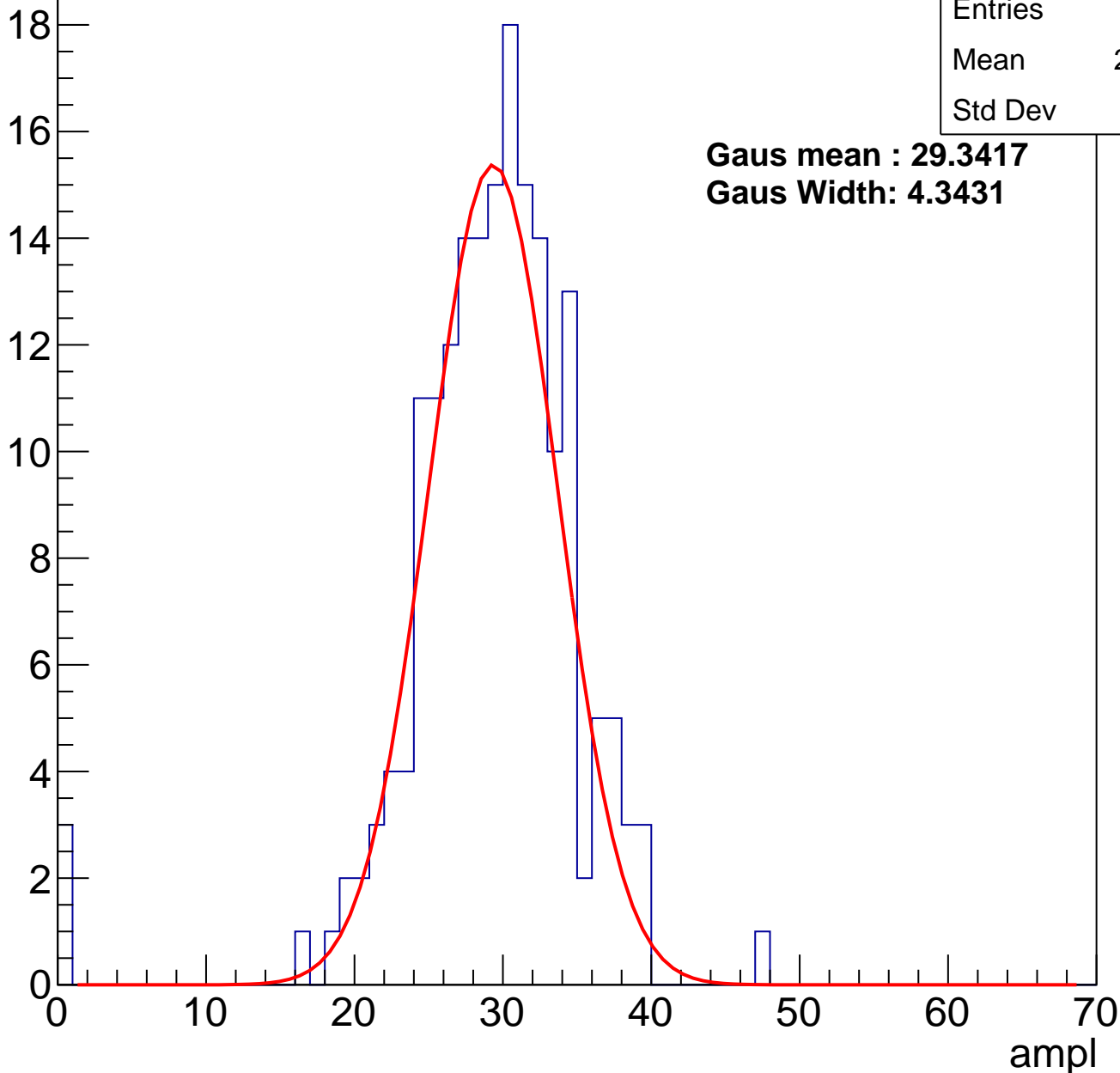
calib_packv5_042523_0143.root, FC#1, port C1

Entries	186
Mean	28.76
Std Dev	5.92

Gaus mean : 29.3417

Gaus Width: 4.3431

Entry



B0L101S, U21-ch120, adc1

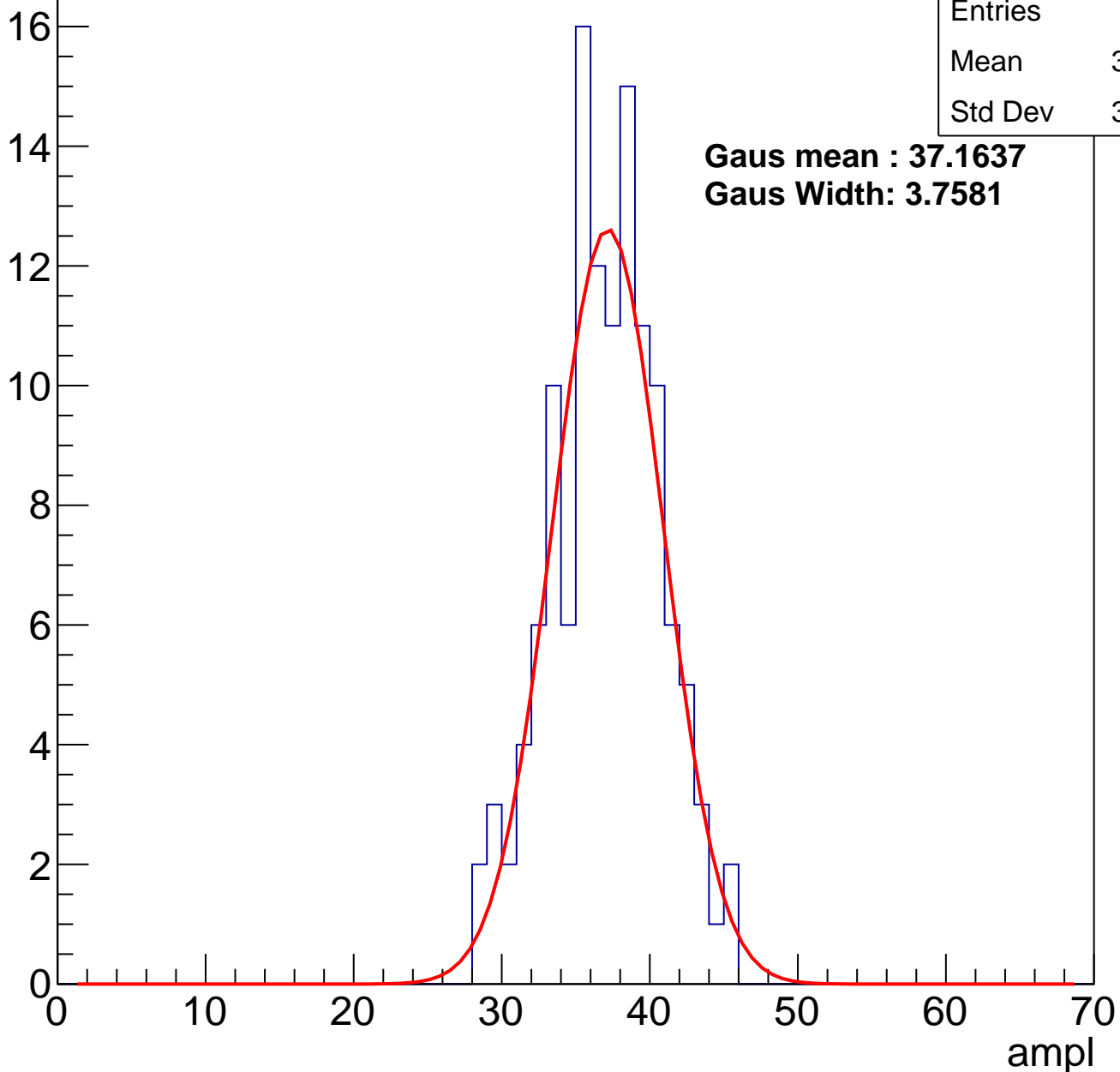
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	36.56
Std Dev	3.652

Gaus mean : 37.1637

Gaus Width: 3.7581

Entry



B0L101S, U21-ch120, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

109

Mean

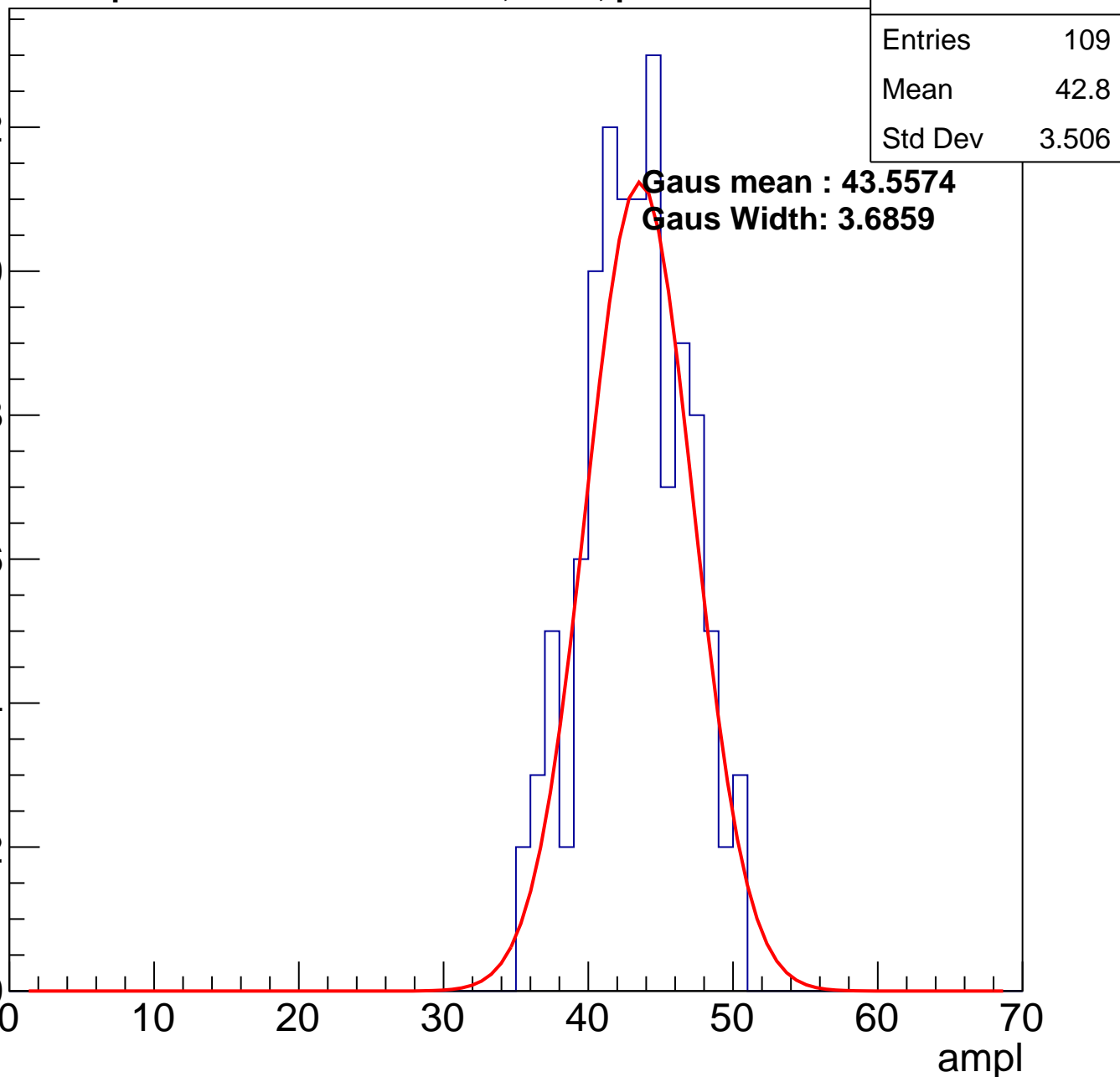
42.8

Std Dev

3.506

Gaus mean : 43.5574

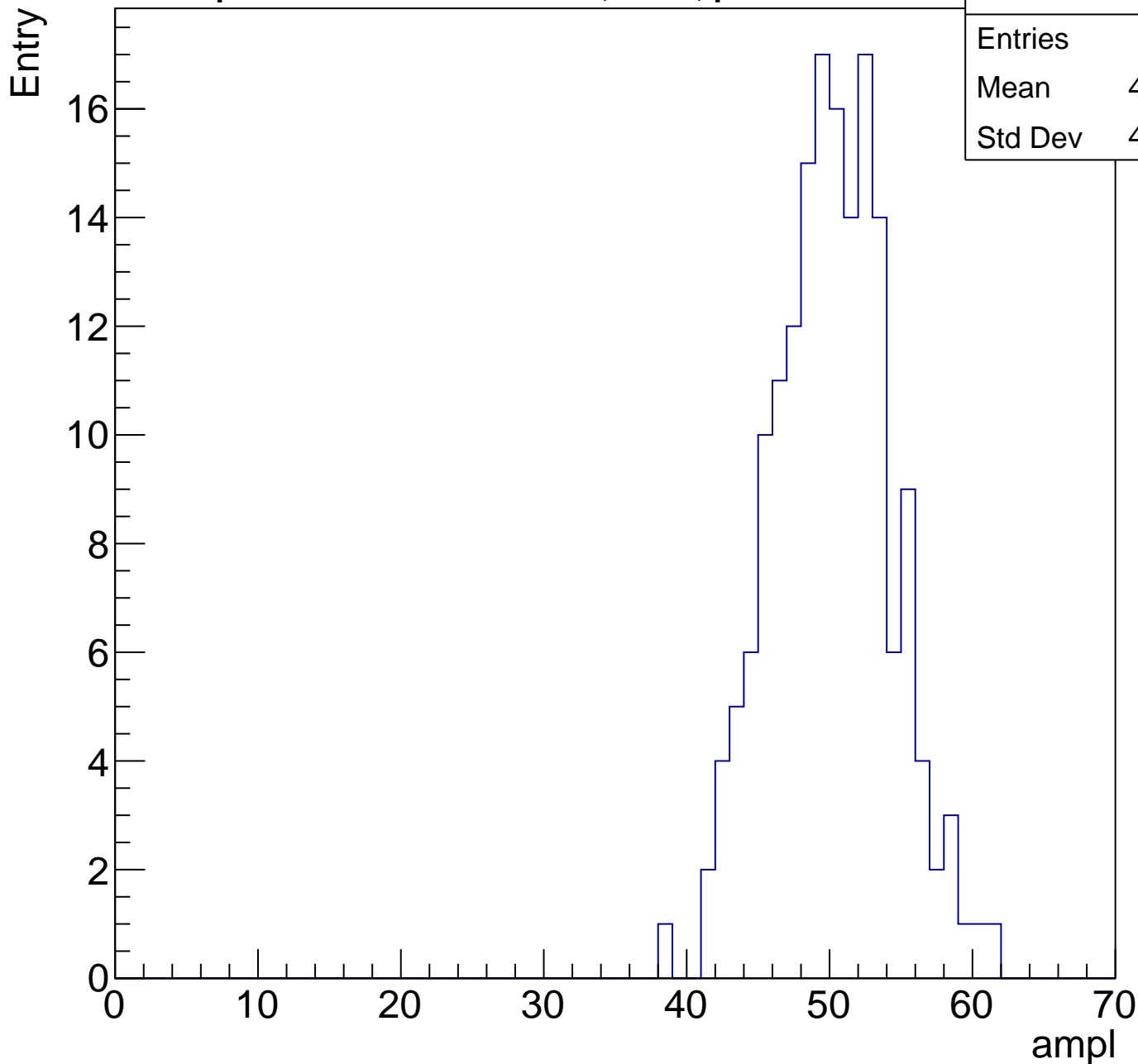
Gaus Width: 3.6859



B0L101S, U21-ch120, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	49.65
Std Dev	4.103



B0L101S, U21-ch120, adc4

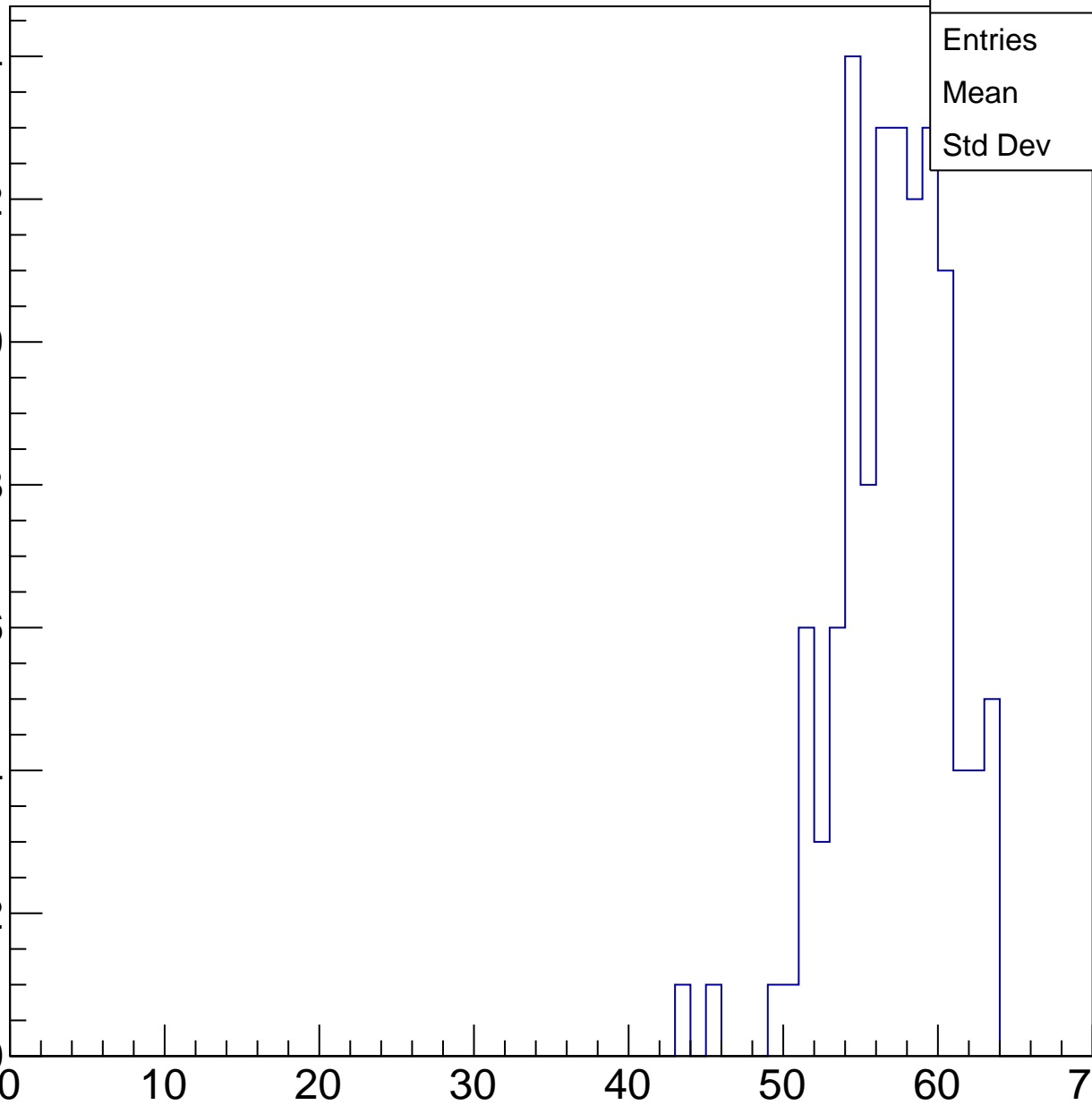
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	116
Mean	56.57
Std Dev	3.592

ampl



B0L101S, U21-ch120, adc5

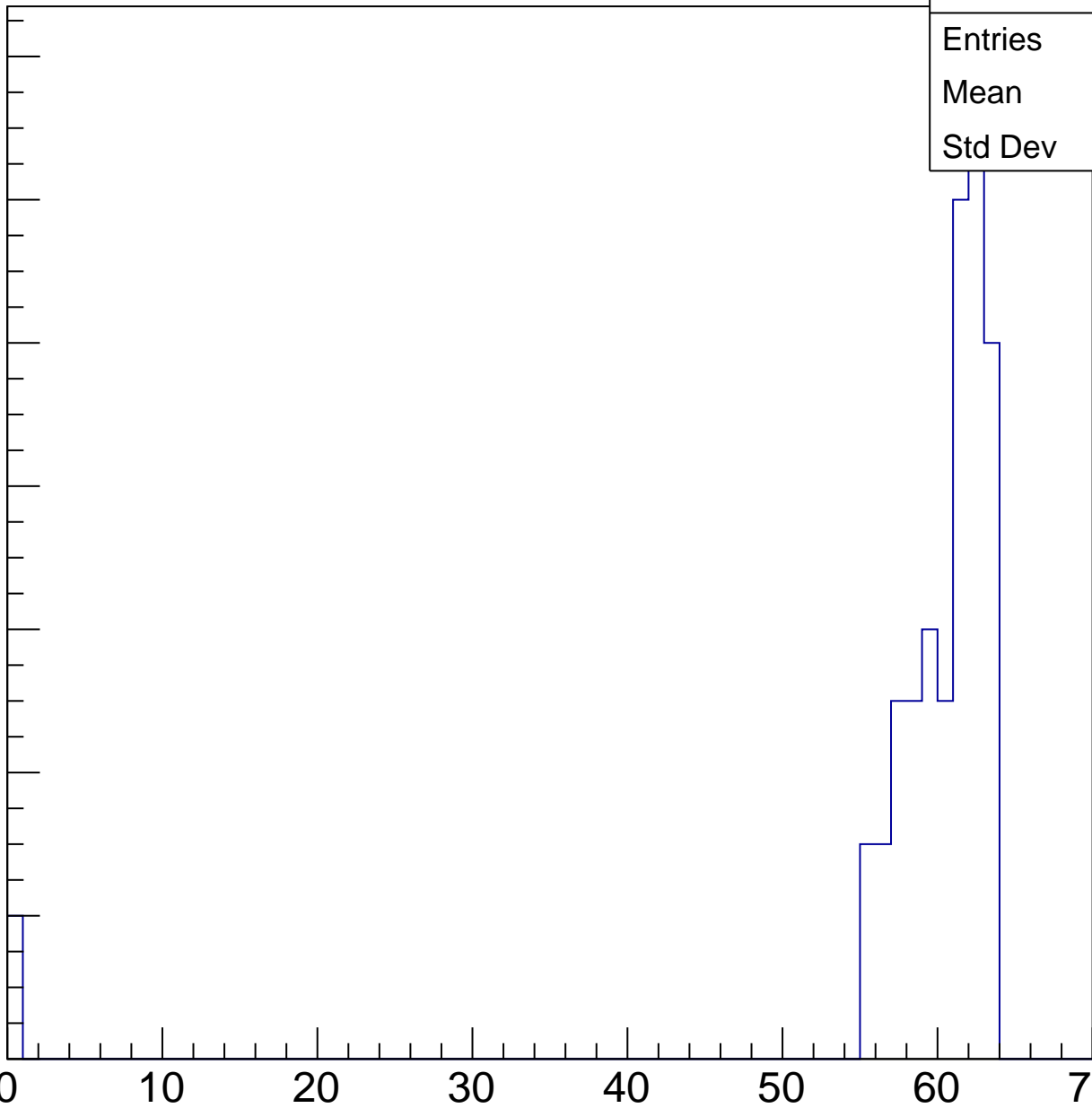
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.34
Std Dev	10.65

ampl

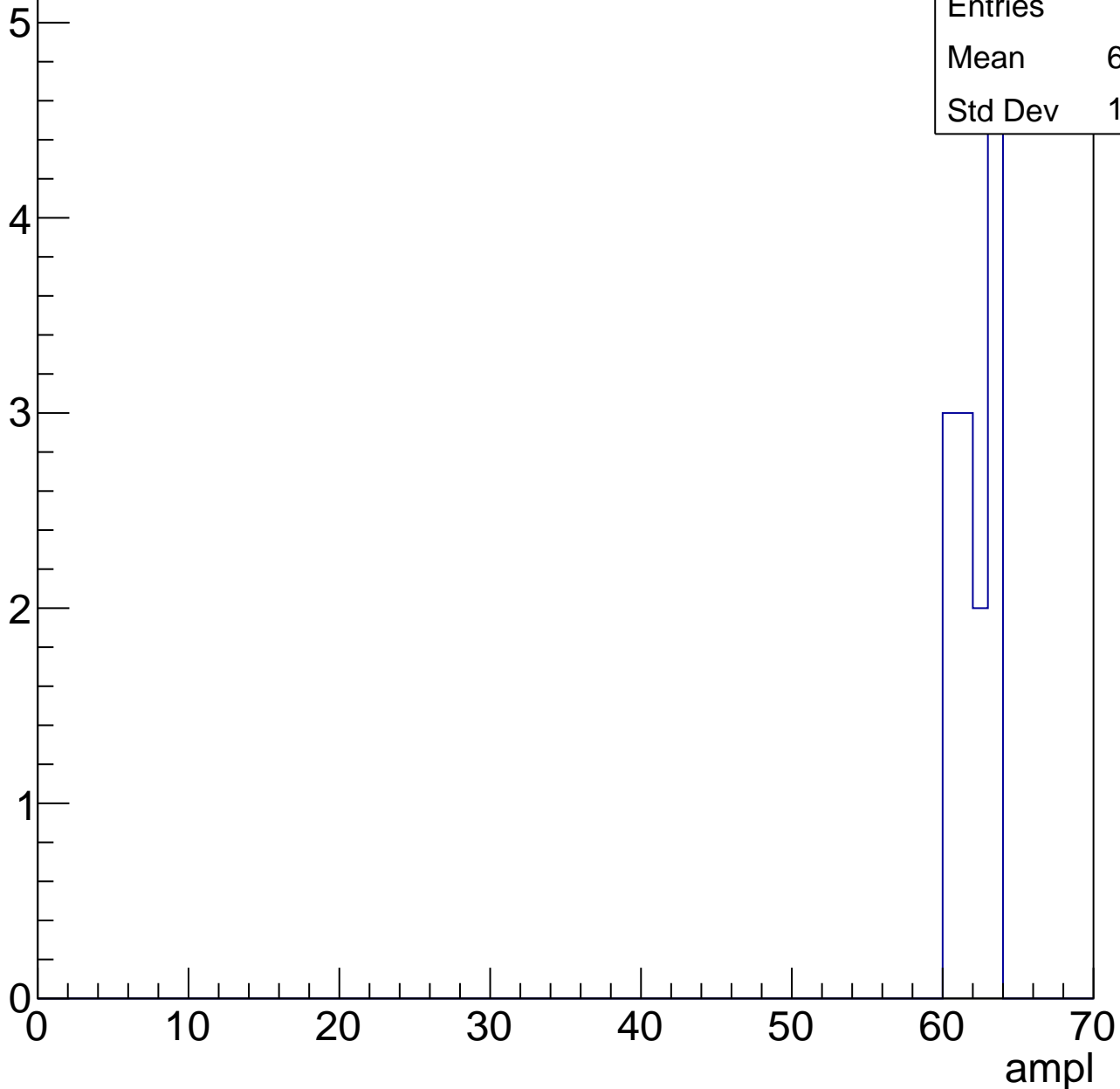


B0L101S, U21-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	61.69
Std Dev	1.202



B0L101S, U21-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch121, adc0

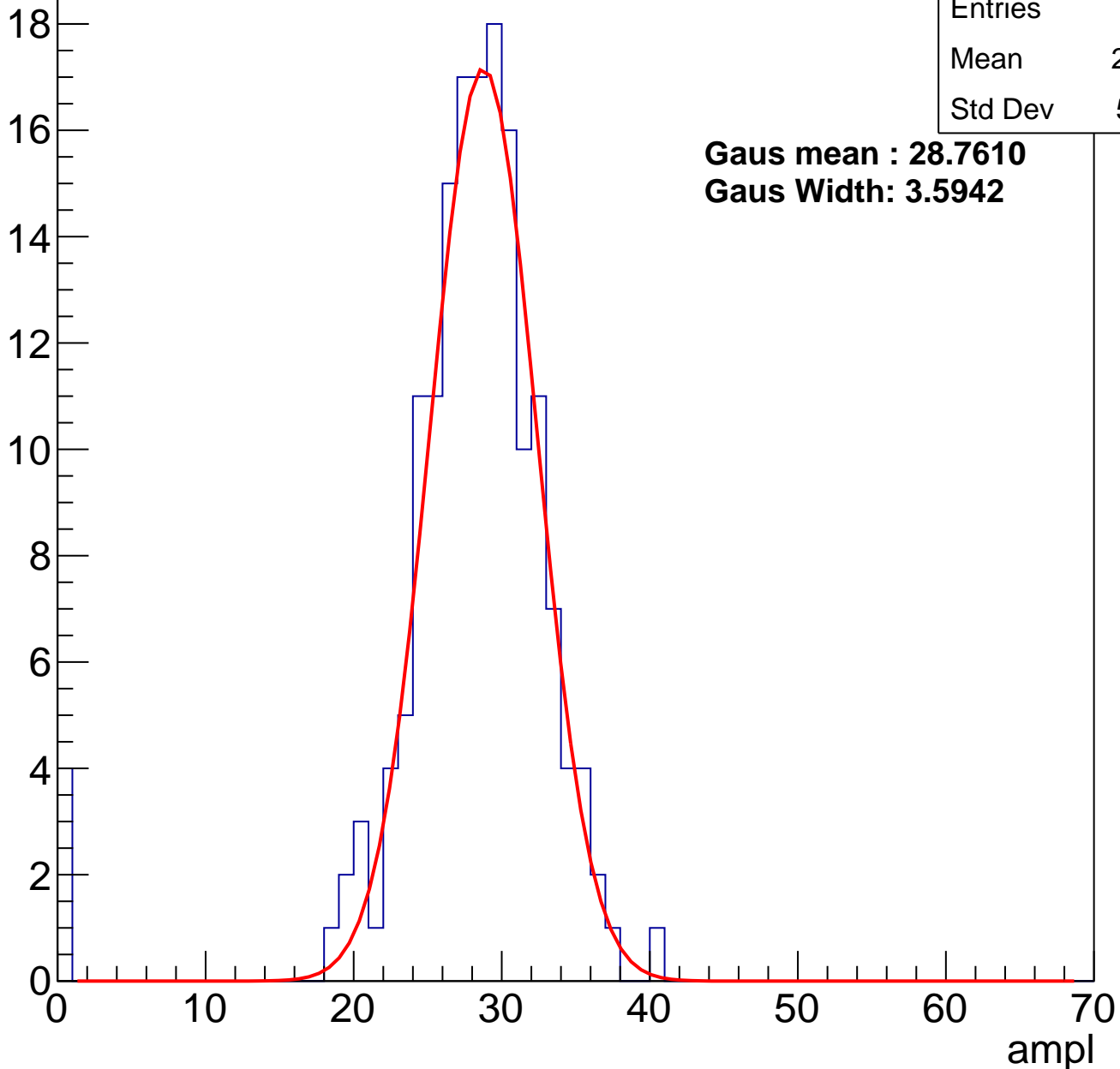
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	27.42
Std Dev	5.731

Gaus mean : 28.7610

Gaus Width: 3.5942

Entry



B0L101S, U21-ch121, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

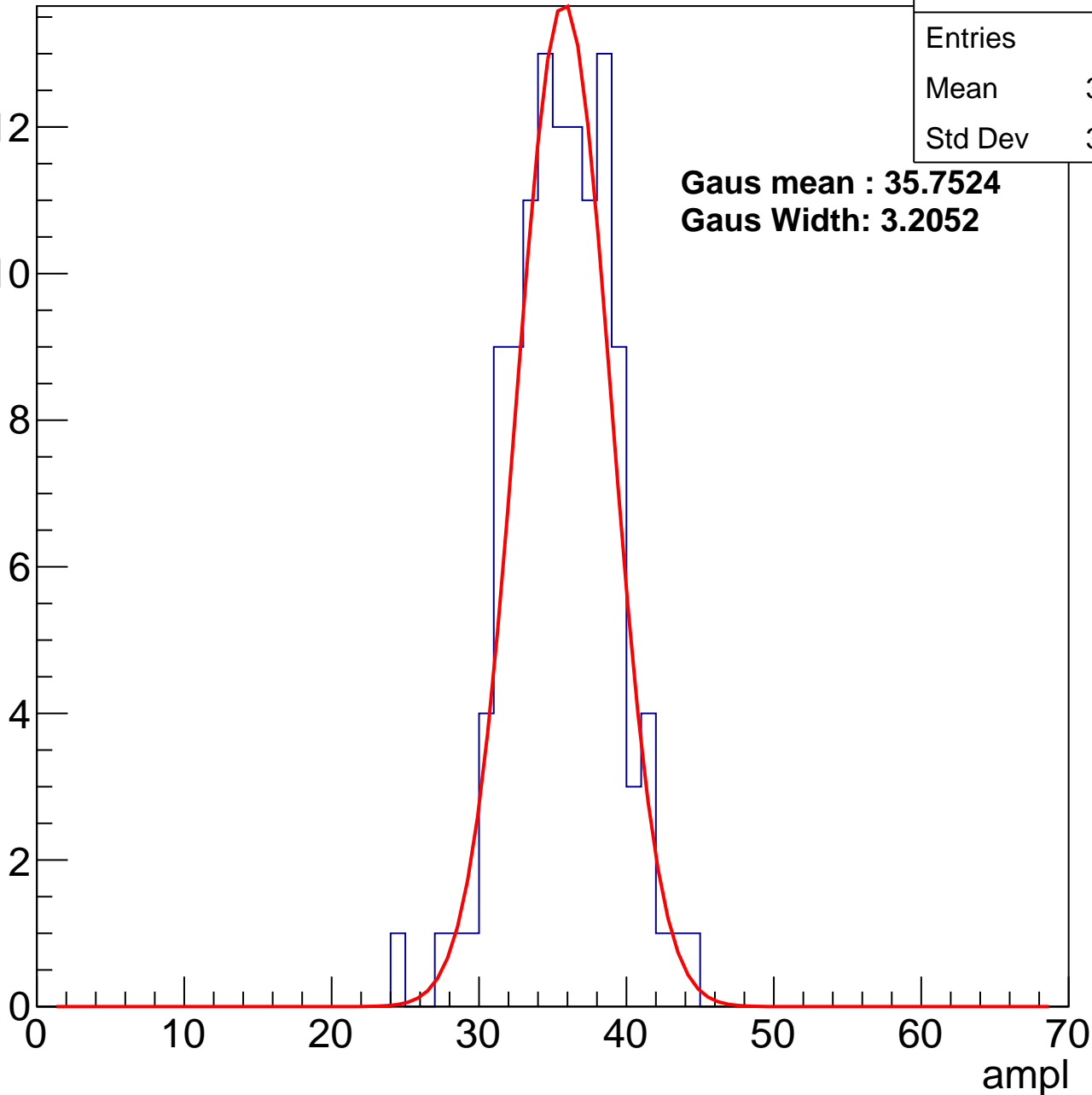
70

ampl

Entries	117
Mean	35.19
Std Dev	3.432

Gaus mean : 35.7524

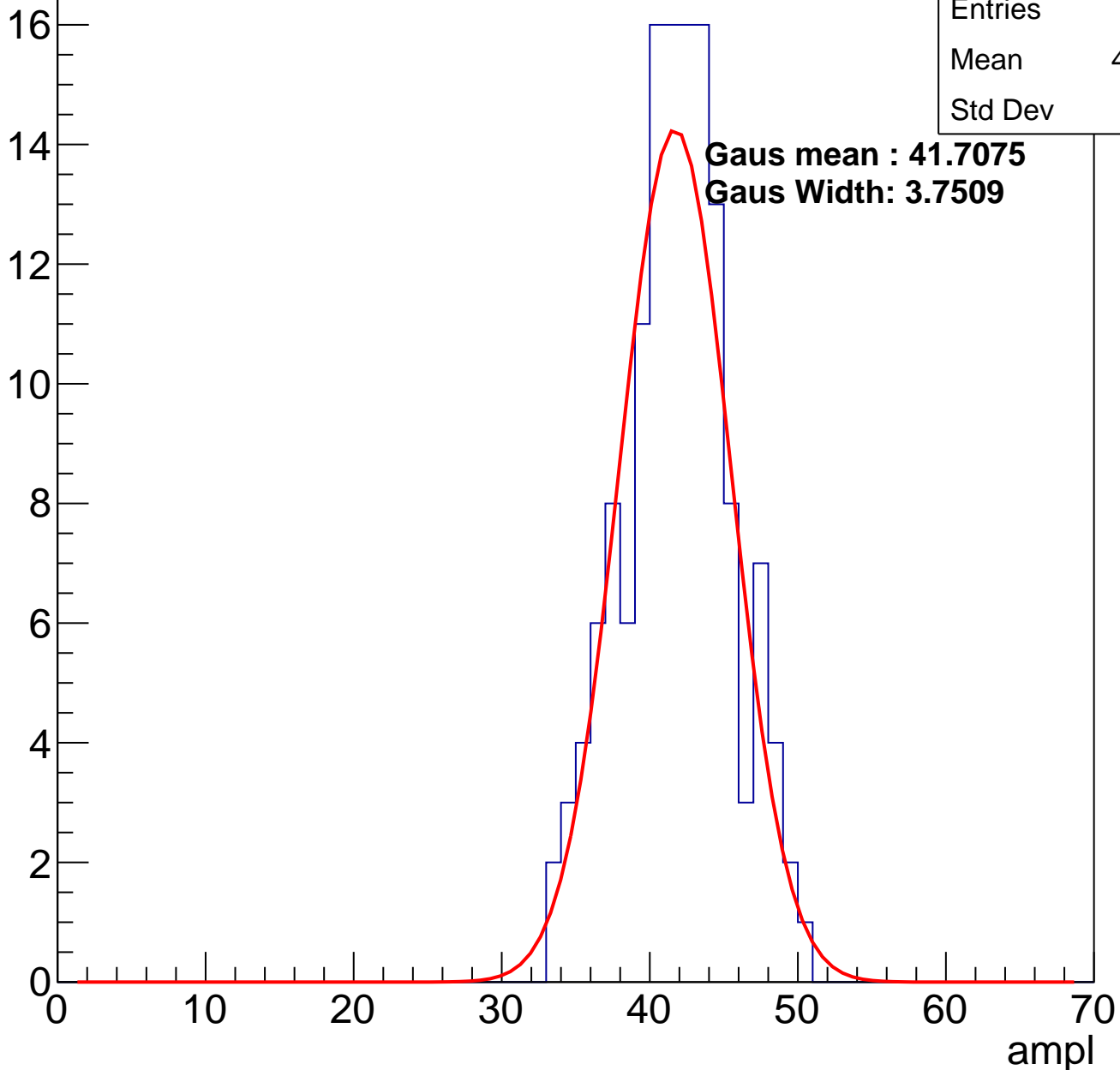
Gaus Width: 3.2052



B0L101S, U21-ch121, adc2

calib_packv5_042523_0143.root, FC#1, port C1

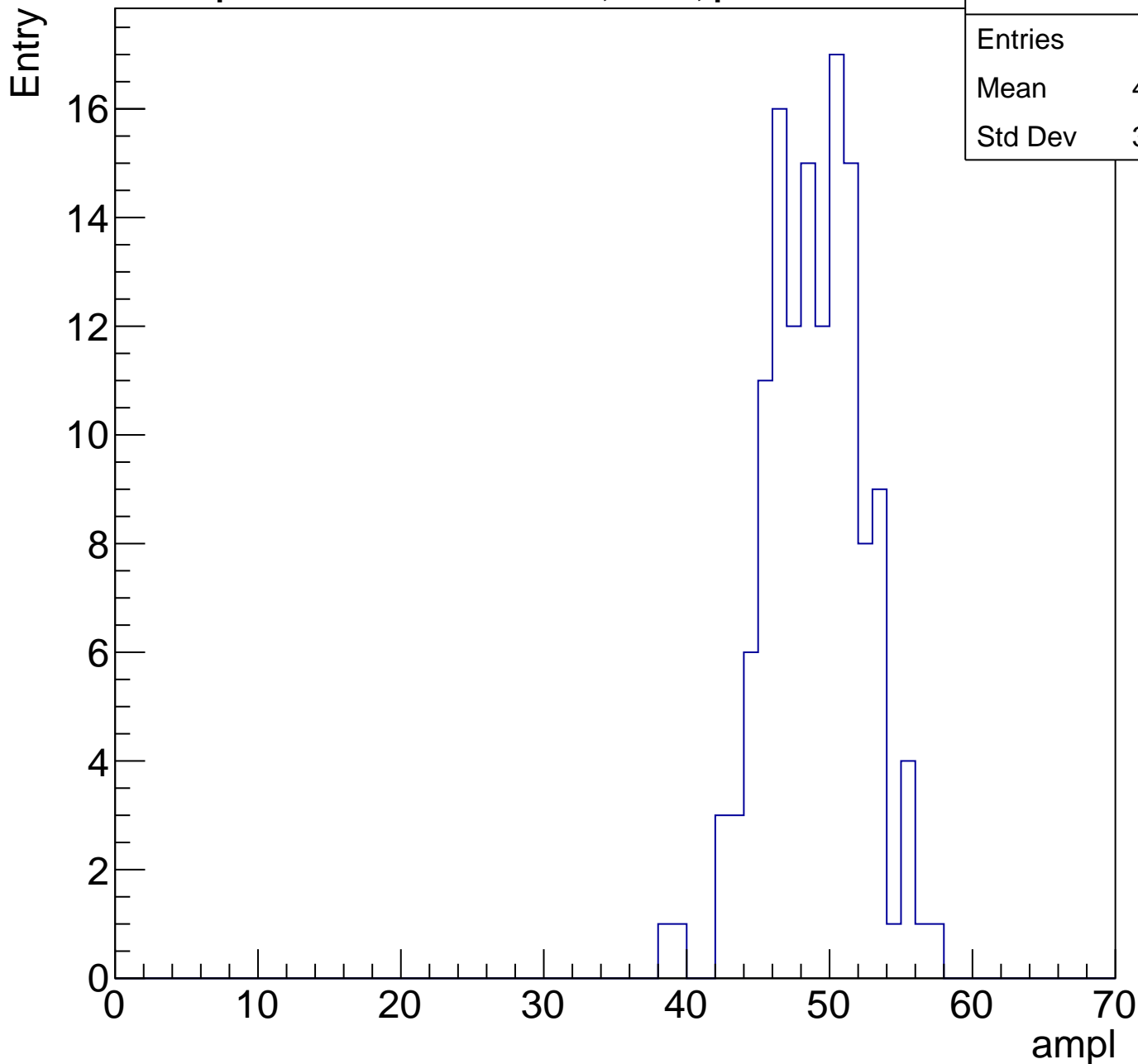
Entry



B0L101S, U21-ch121, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	48.49
Std Dev	3.374



B0L101S, U21-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	54.83
Std Dev	3.367

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

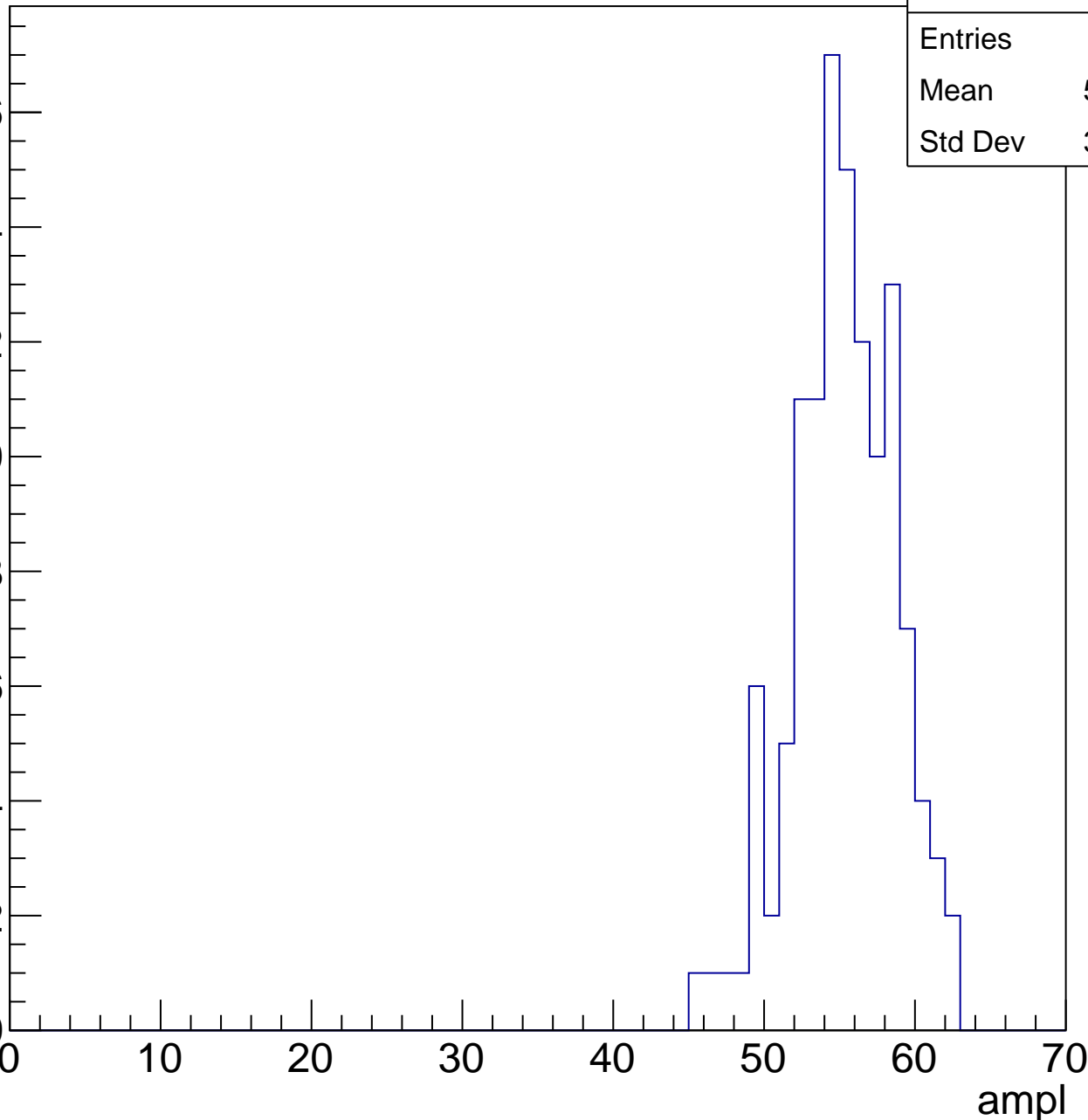
40

50

60

70

ampl



B0L101S, U21-ch121, adc5

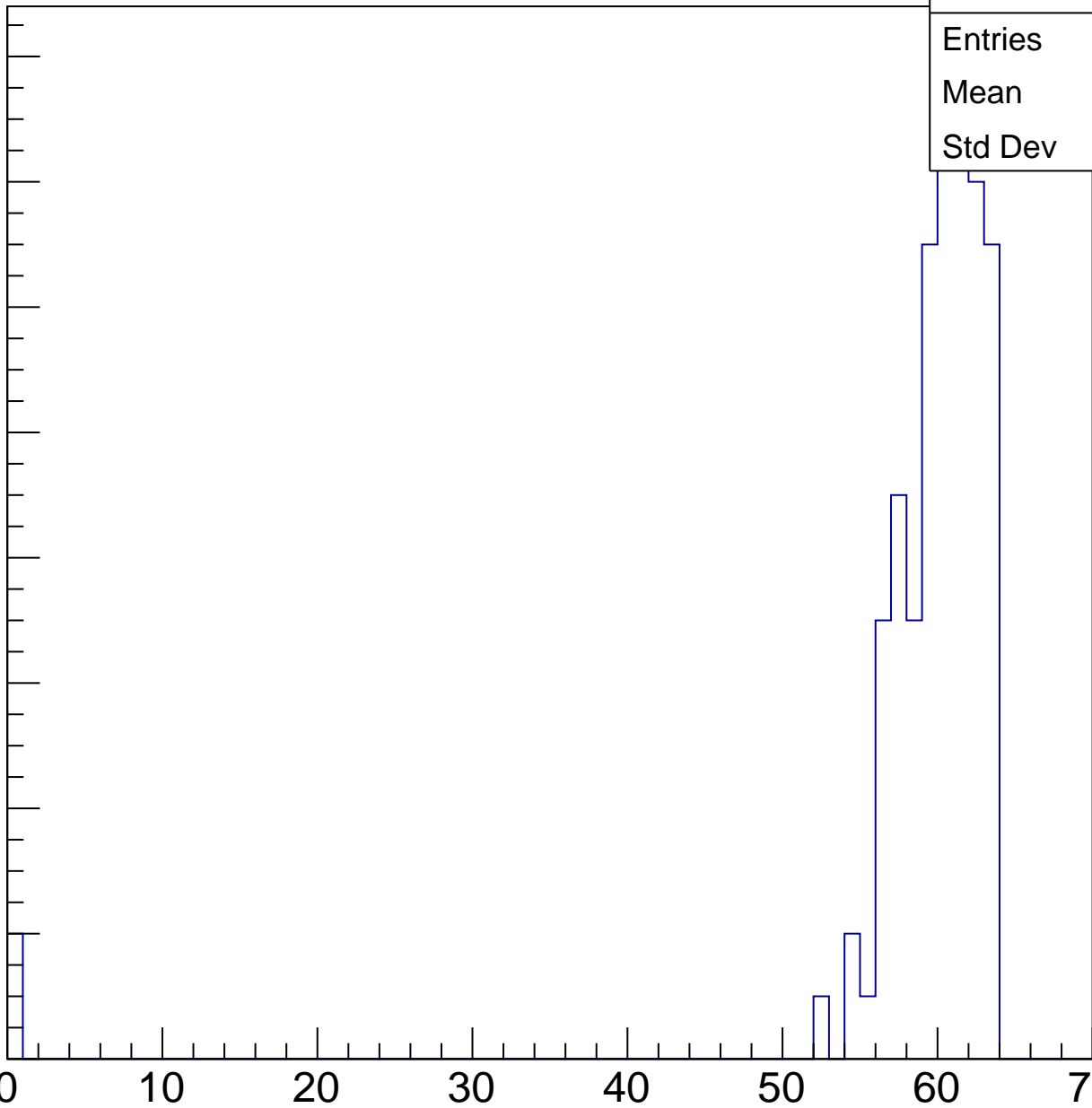
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.57
Std Dev	8.662

ampl

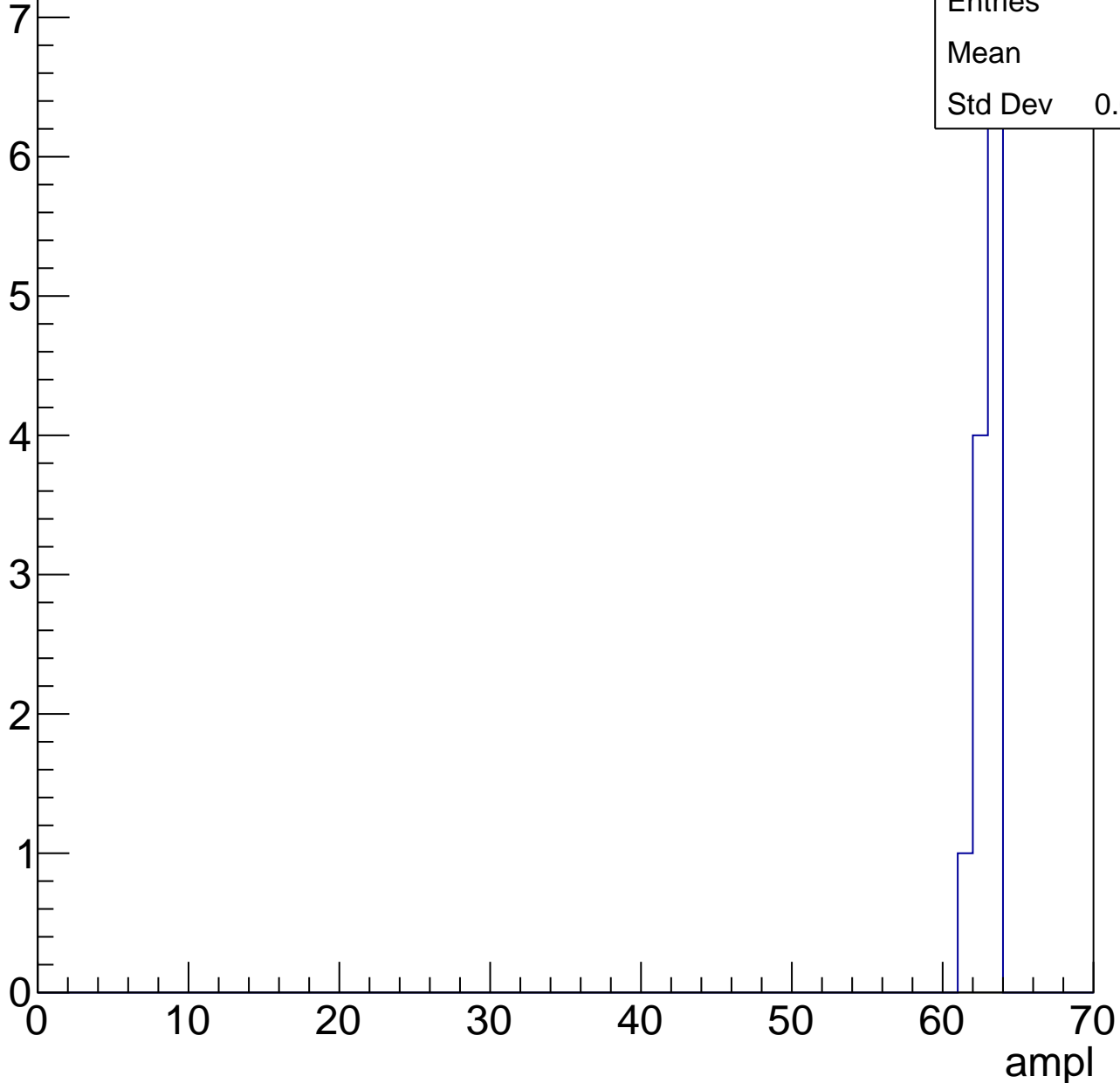


B0L101S, U21-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	62.5
Std Dev	0.6455



B0L101S, U21-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch122, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	28.58
Std Dev	4.854

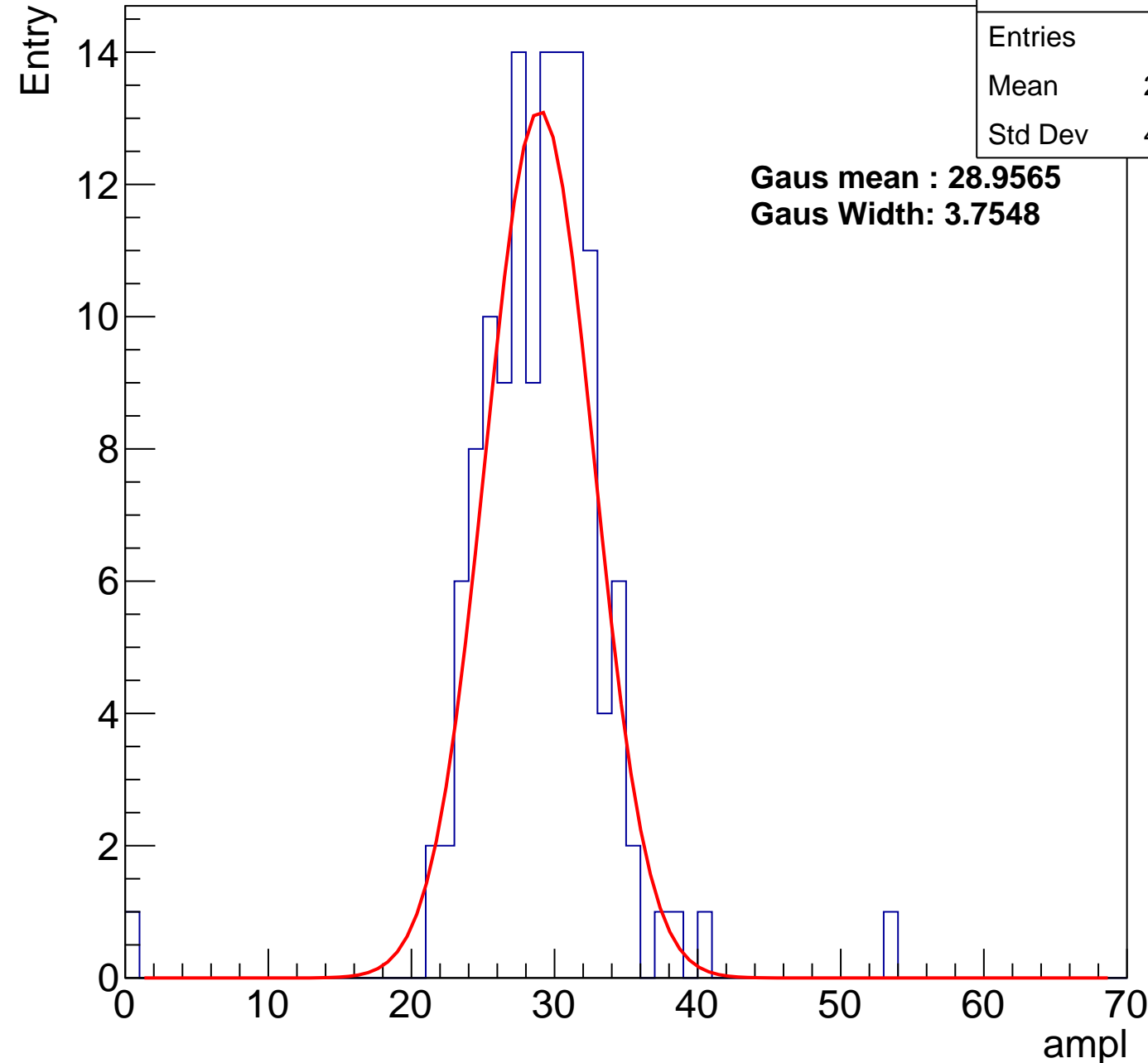
Gaus mean : 28.9565

Gaus Width: 3.7548

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch122, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	35.6
Std Dev	3.842

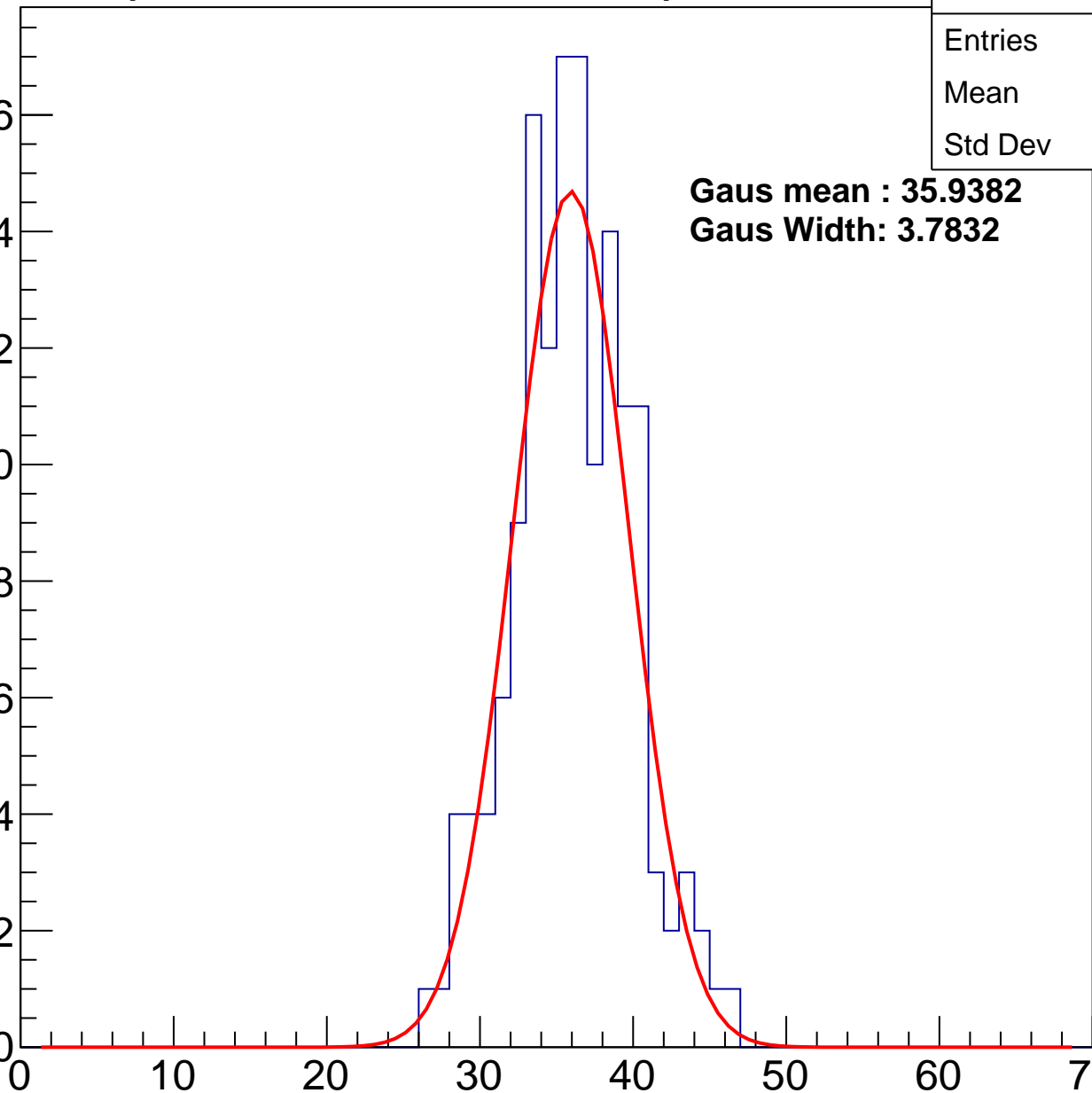
Gaus mean : 35.9382

Gaus Width: 3.7832

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U21-ch122, adc2

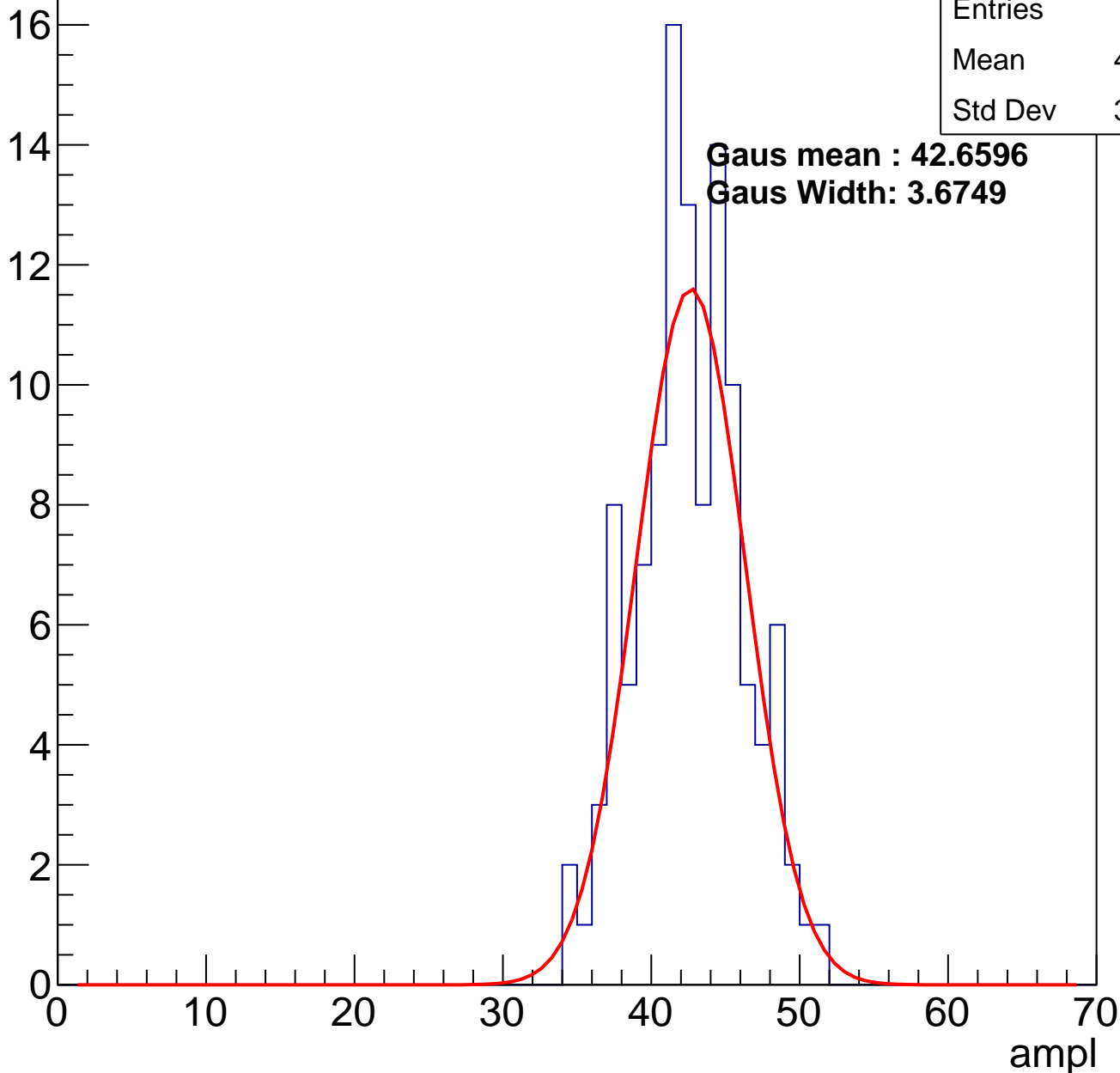
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	115
Mean	42.15
Std Dev	3.576

Gaus mean : 42.6596

Gaus Width: 3.6749



B0L101S, U21-ch122, adc3

calib_packv5_042523_0143.root, FC#1, port C1

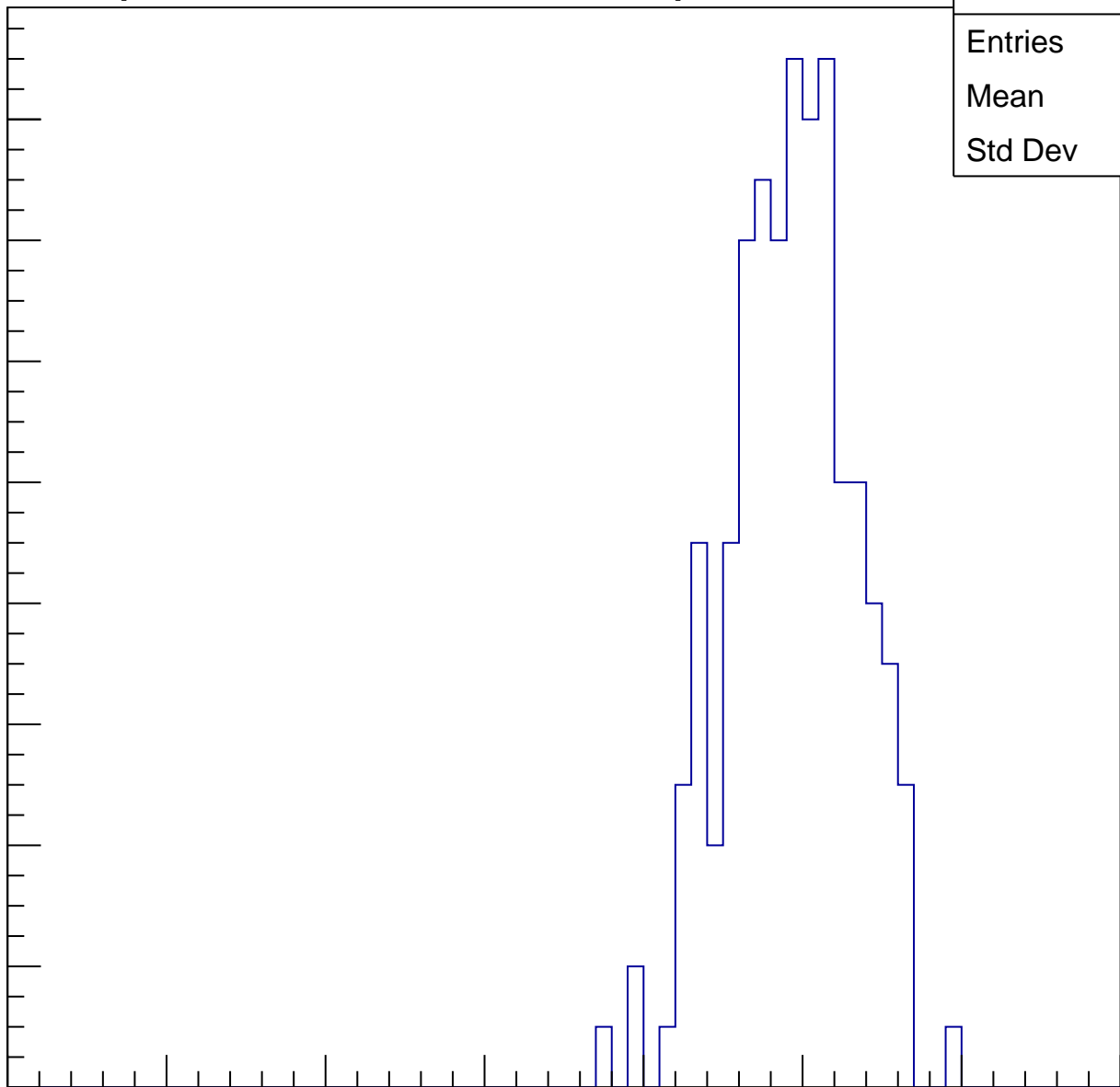
Entries	165
Mean	48.85
Std Dev	3.93

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

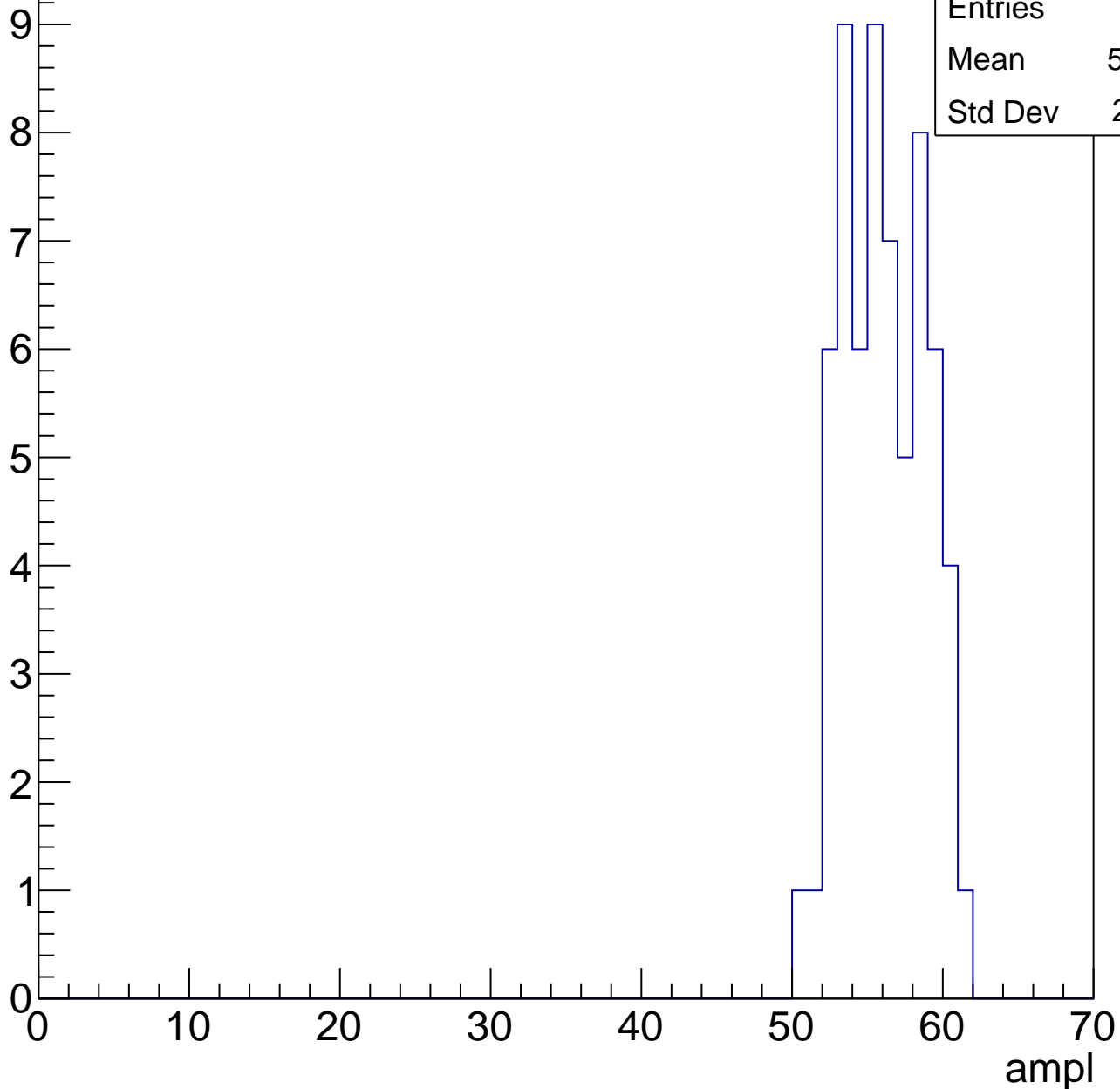
ampl



B0L101S, U21-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	63
Mean	55.63
Std Dev	2.651

B0L101S, U21-ch122, adc5

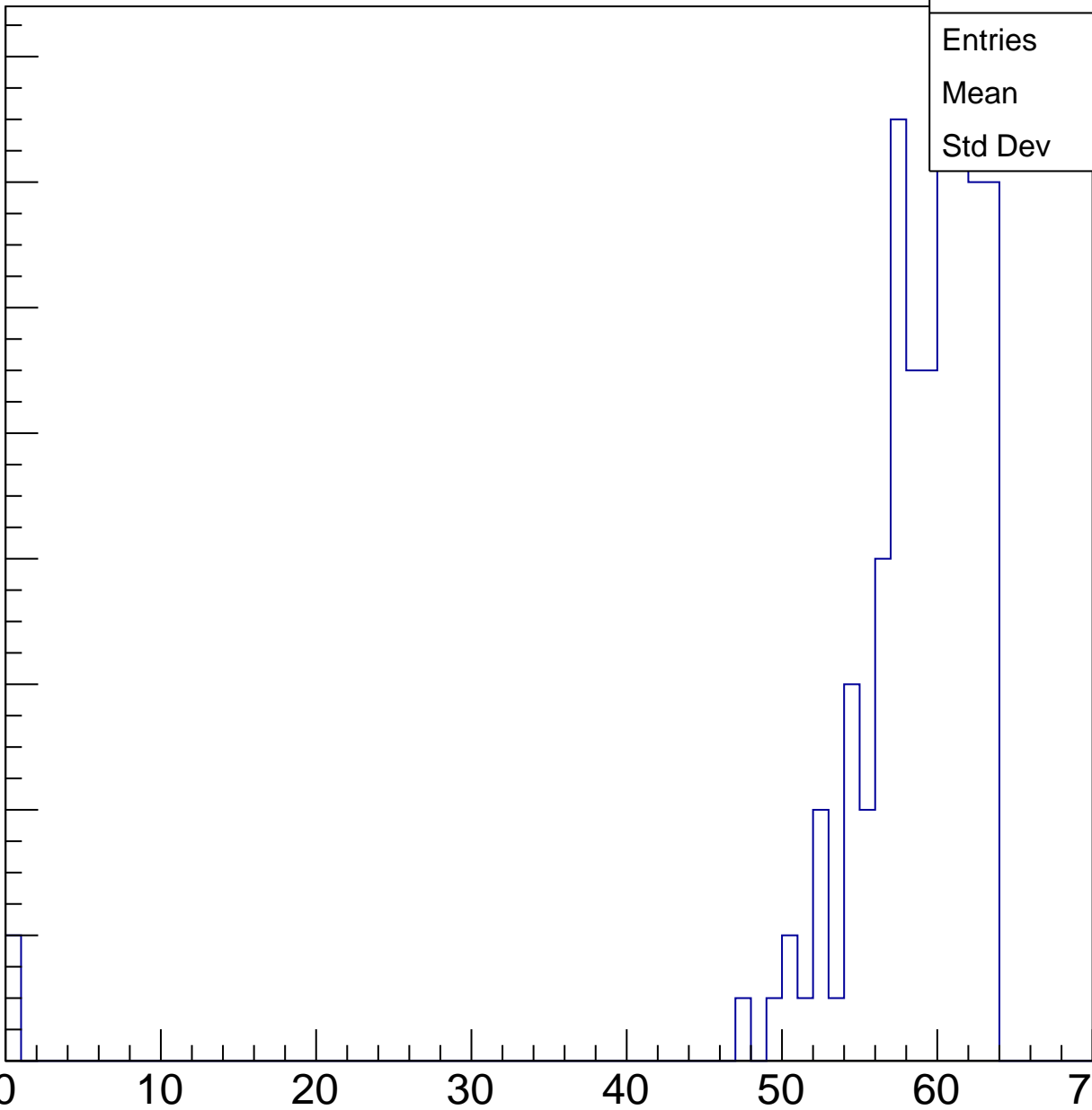
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	57.67
Std Dev	8.08

ampl

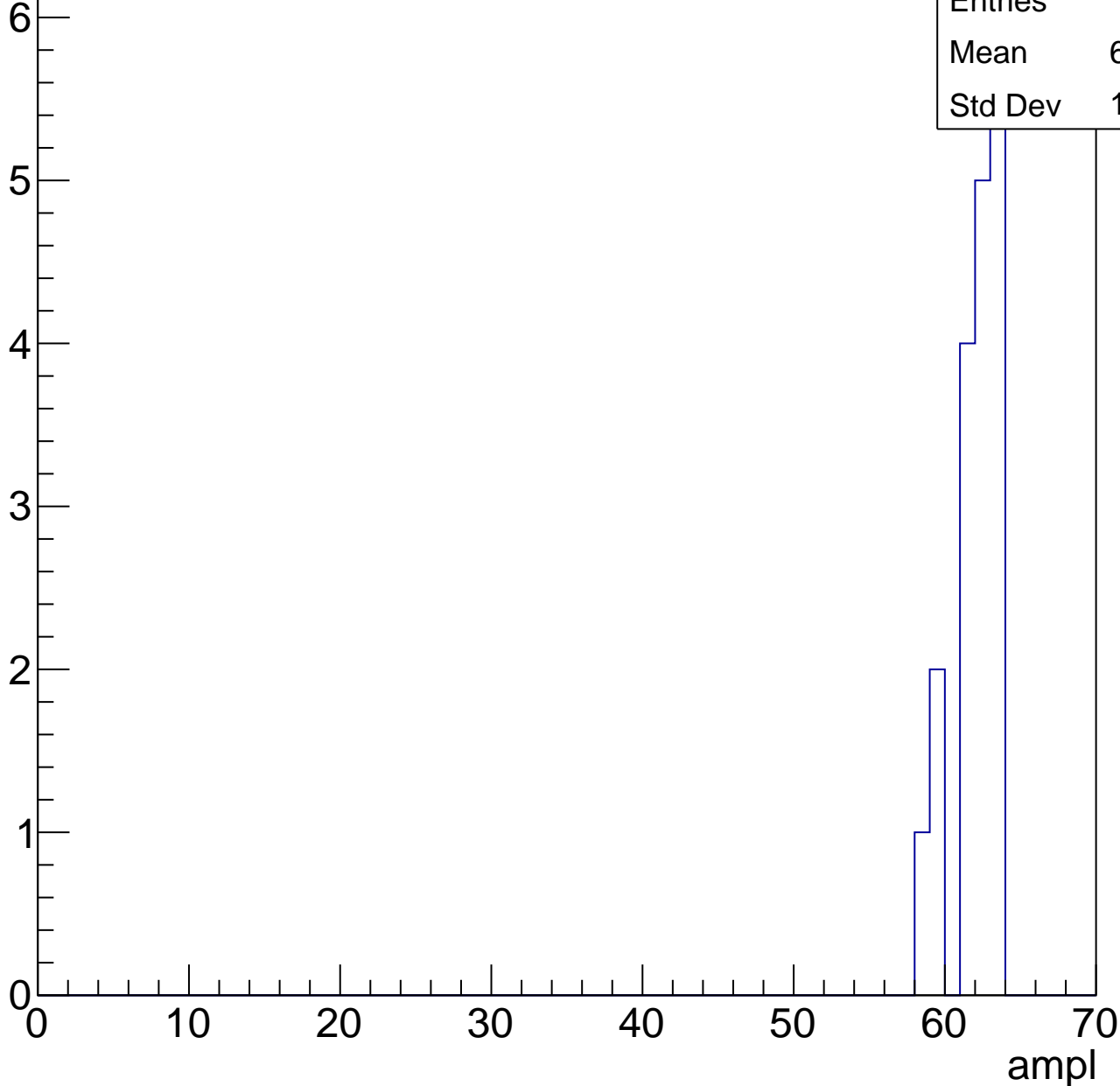


B0L101S, U21-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	61.56
Std Dev	1.499



B0L101S, U21-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch123, adc0

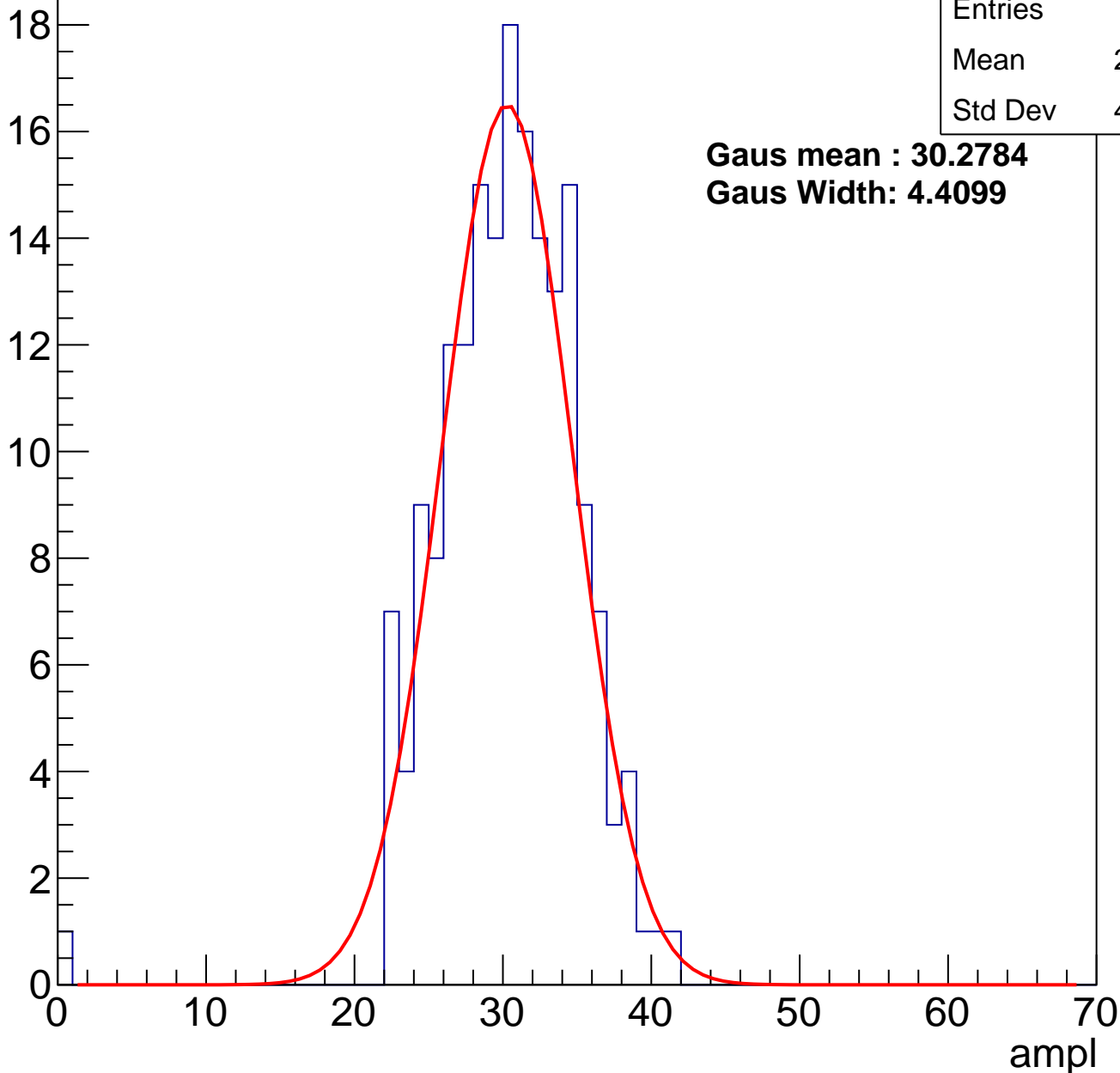
calib_packv5_042523_0143.root, FC#1, port C1

Entries	184
Mean	29.88
Std Dev	4.692

Gaus mean : 30.2784

Gaus Width: 4.4099

Entry



B0L101S, U21-ch123, adc1

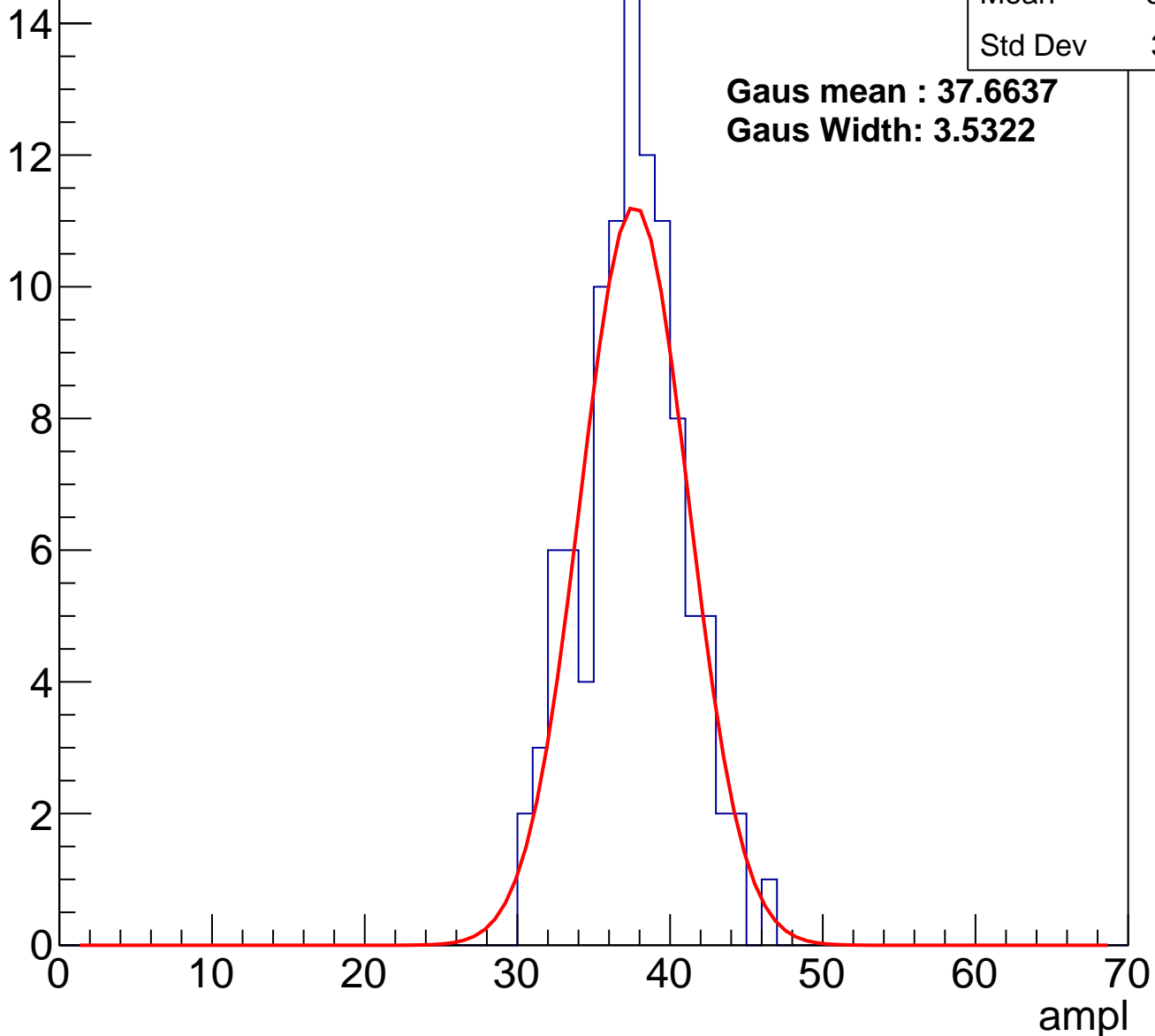
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	37.09
Std Dev	3.301

Gaus mean : 37.6637

Gaus Width: 3.5322

Entry



B0L101S, U21-ch123, adc2

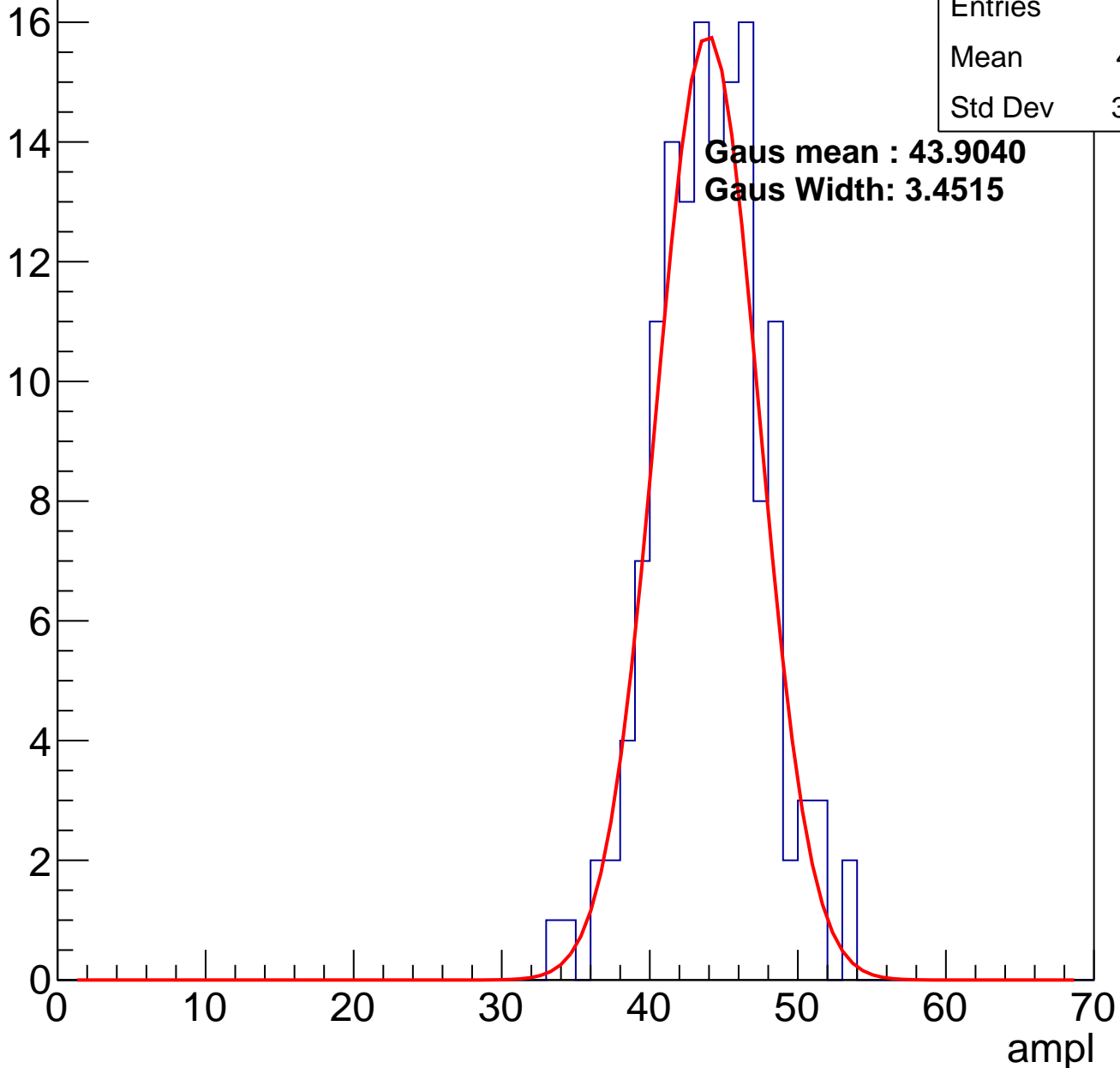
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	43.61
Std Dev	3.628

Gaus mean : 43.9040

Gaus Width: 3.4515

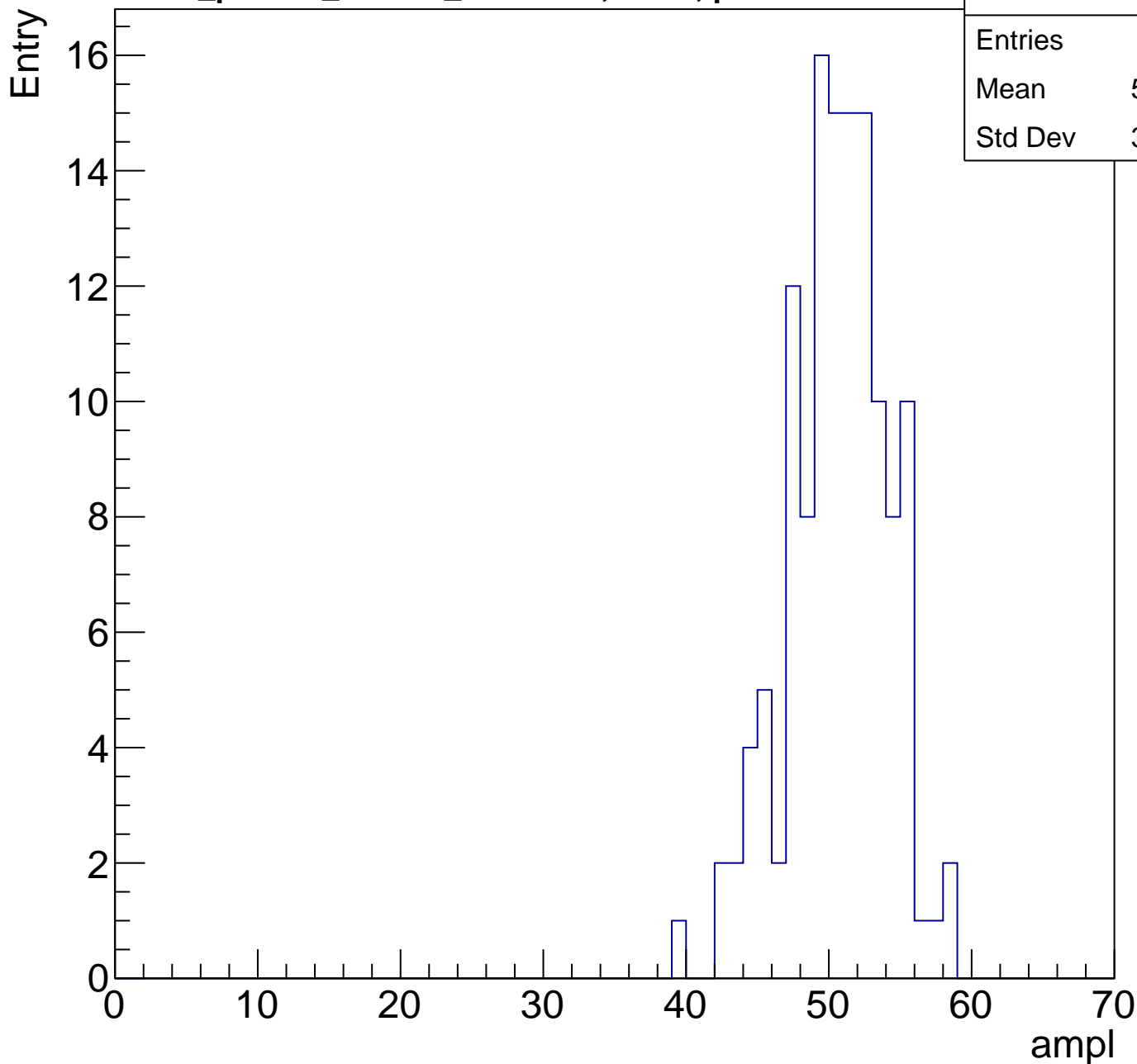
Entry



B0L101S, U21-ch123, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	50.16
Std Dev	3.474



B0L101S, U21-ch123, adc4

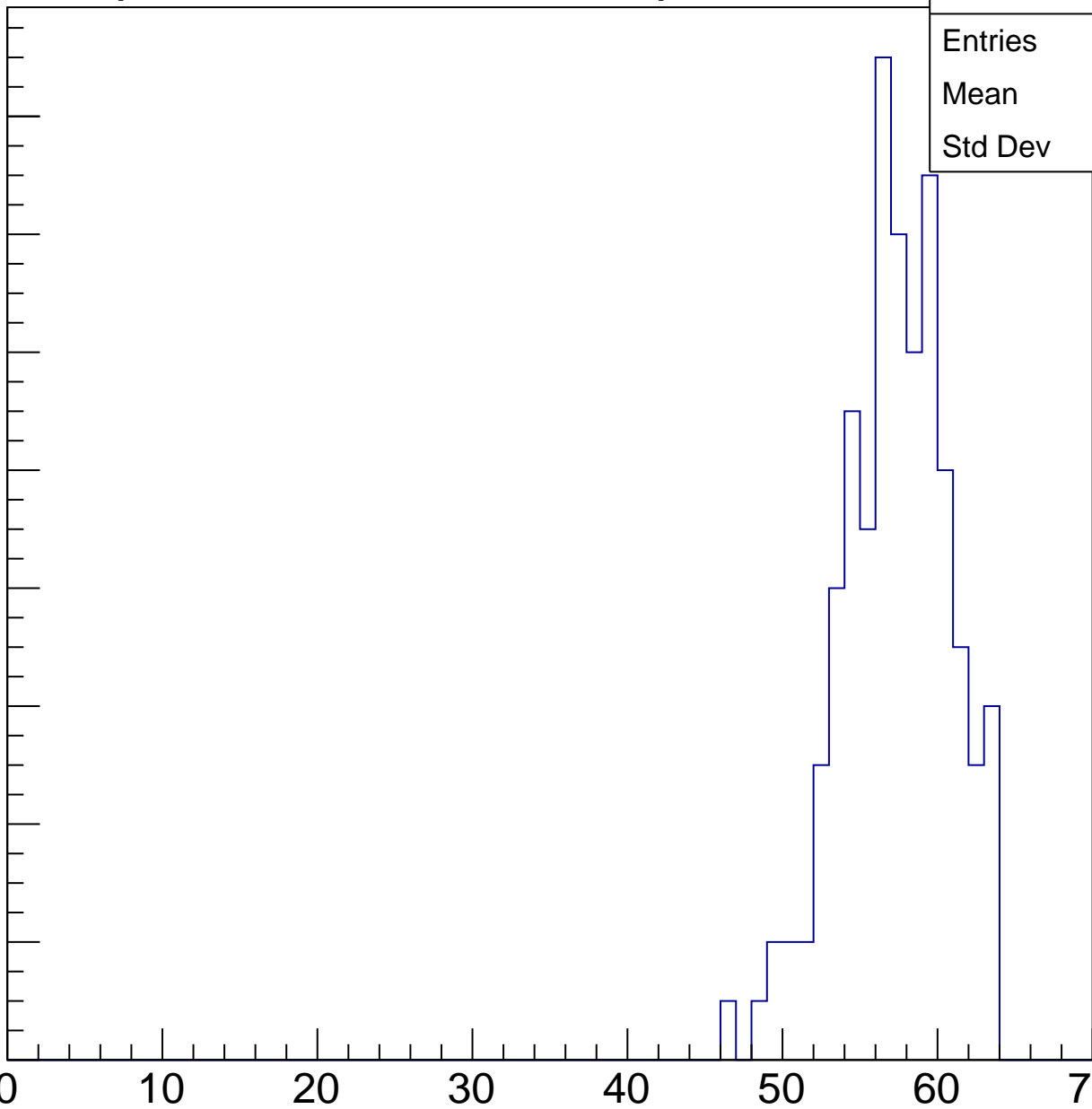
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	56.8
Std Dev	3.453

ampl



B0L101S, U21-ch123, adc5

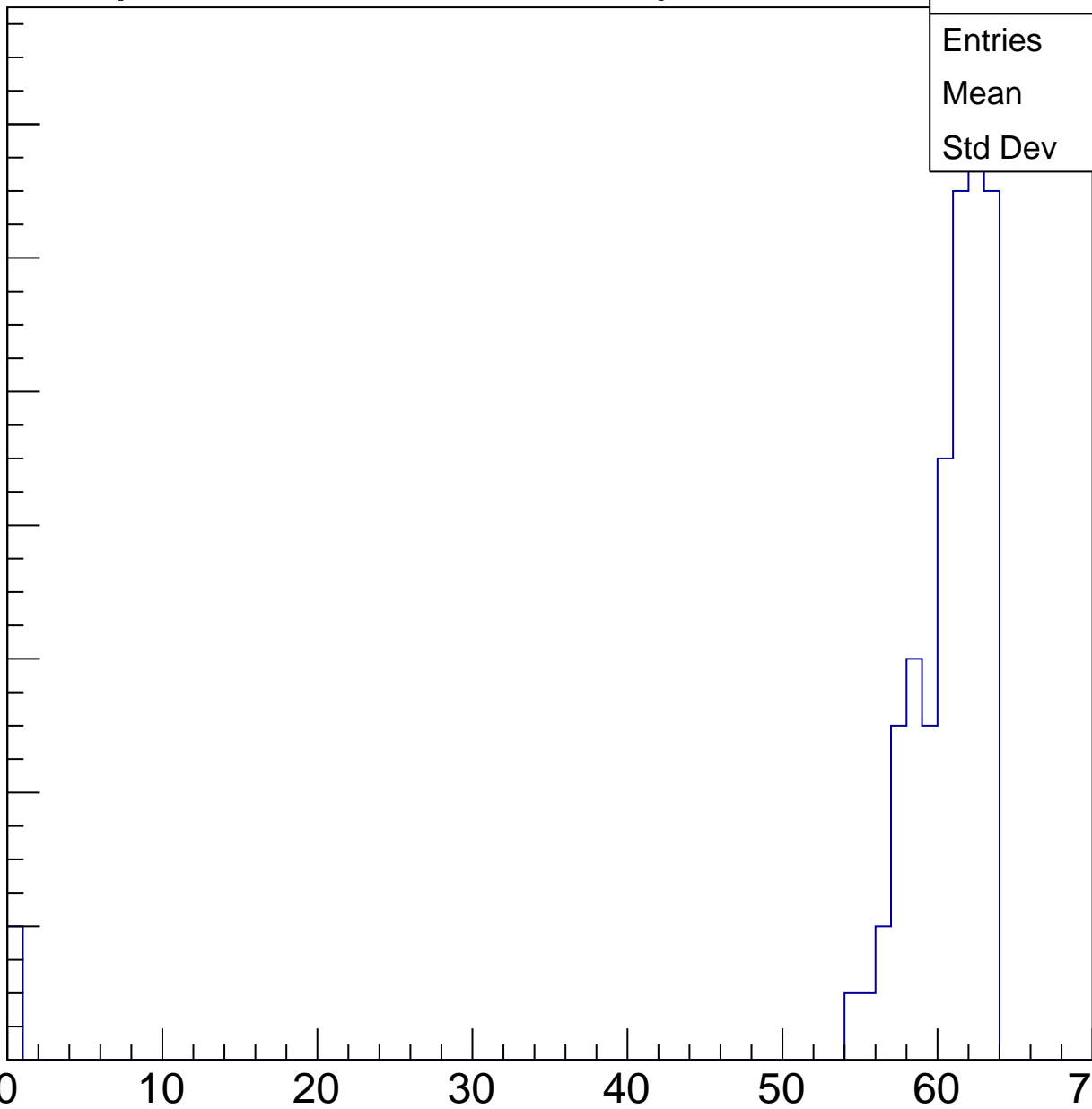
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.76
Std Dev	10.17

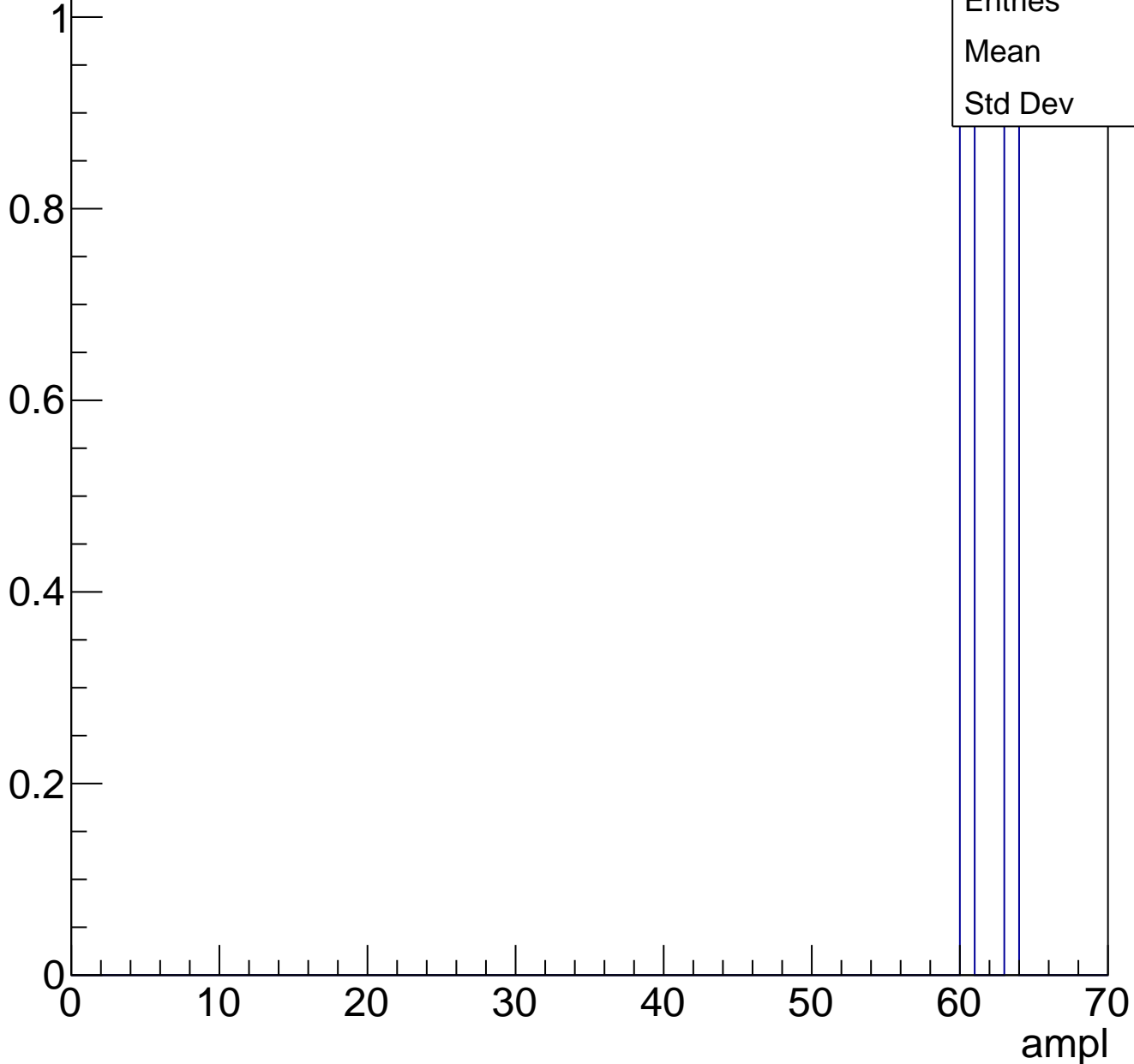
ampl



B0L101S, U21-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U21-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch124, adc0

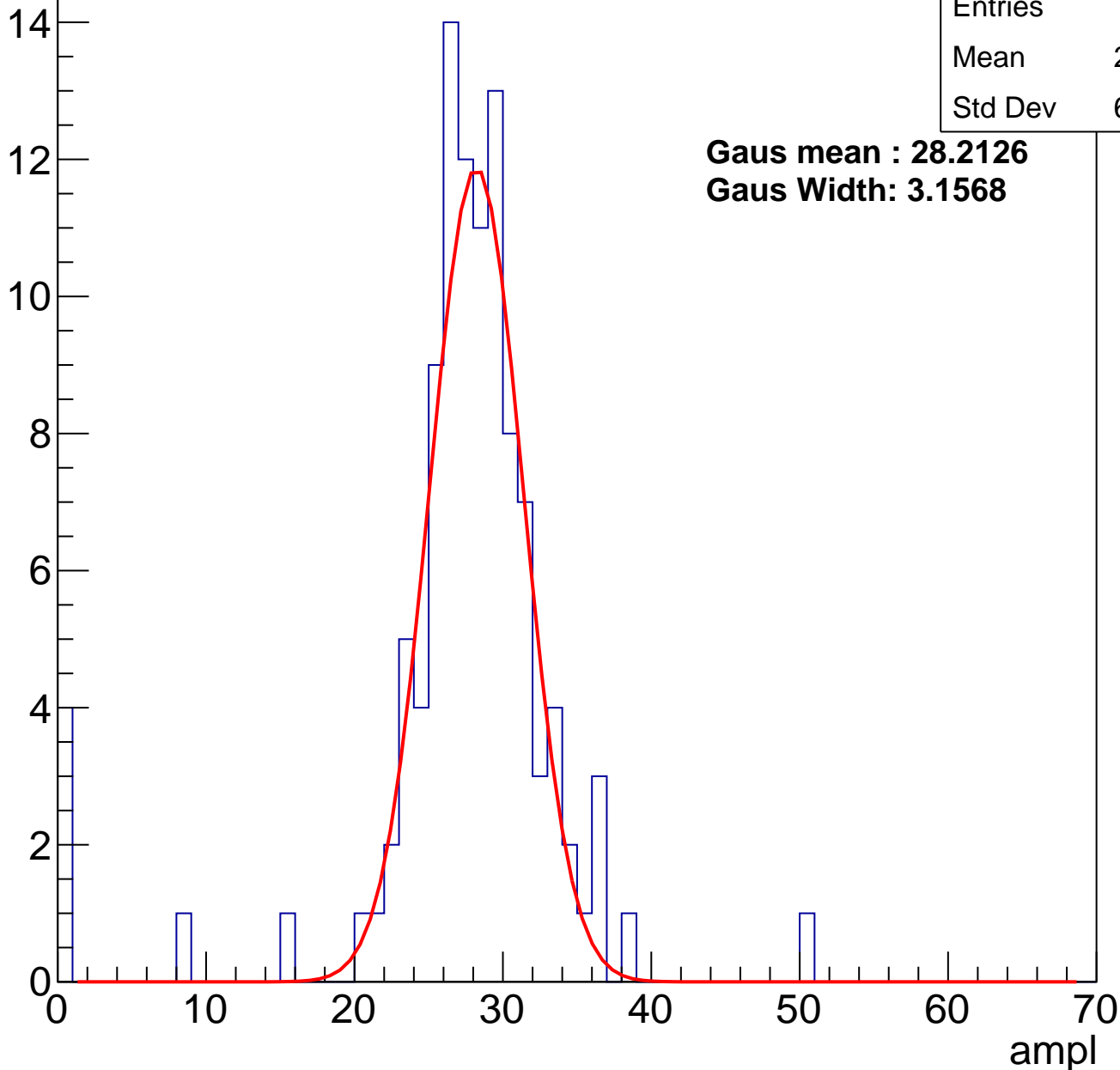
calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	26.86
Std Dev	6.966

Gaus mean : 28.2126

Gaus Width: 3.1568

Entry



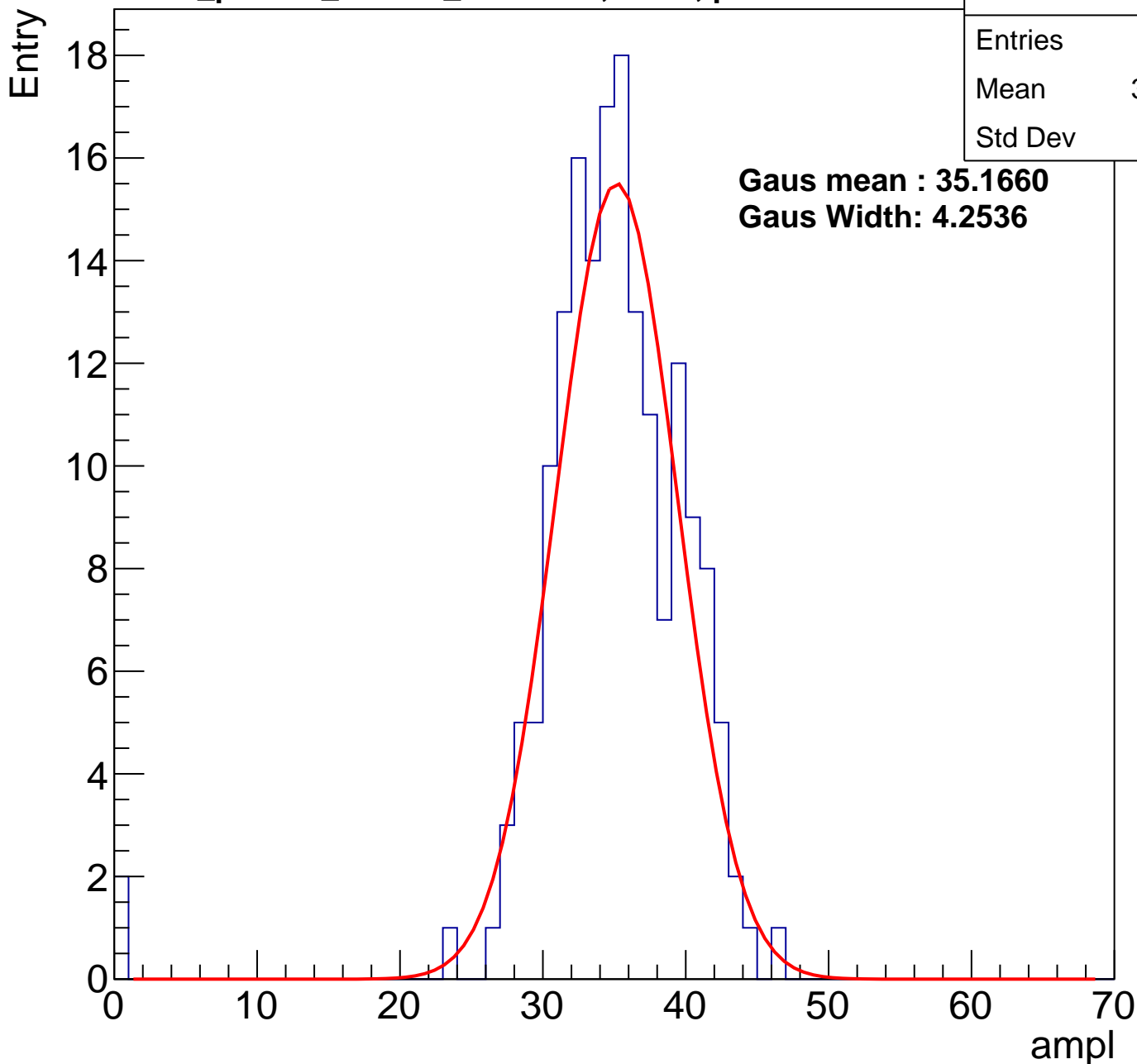
B0L101S, U21-ch124, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	34.39
Std Dev	5.52

Gaus mean : 35.1660

Gaus Width: 4.2536



B0L101S, U21-ch124, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

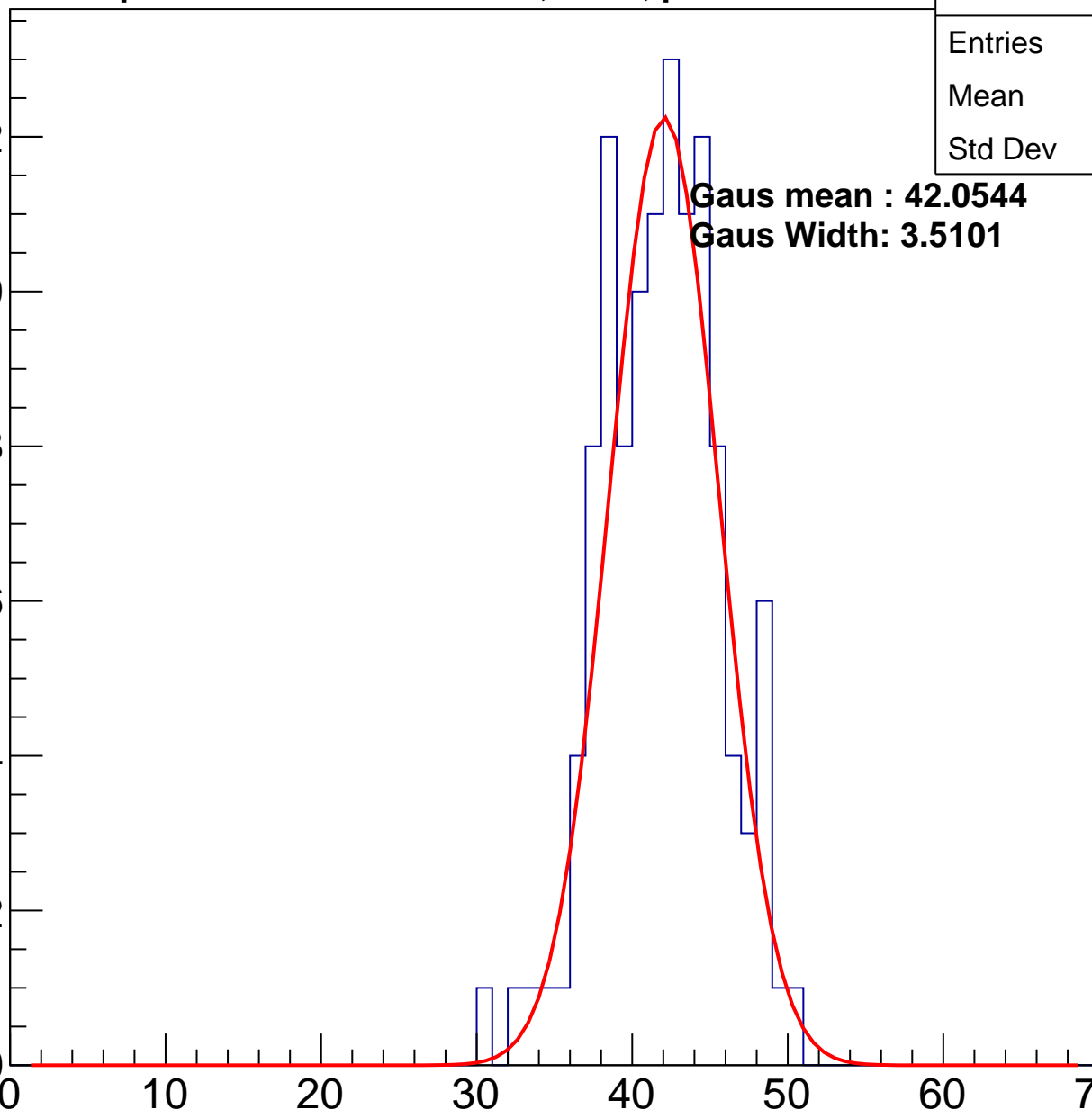
70

ampl

Entries	117
Mean	41.38
Std Dev	3.757

Gaus mean : 42.0544

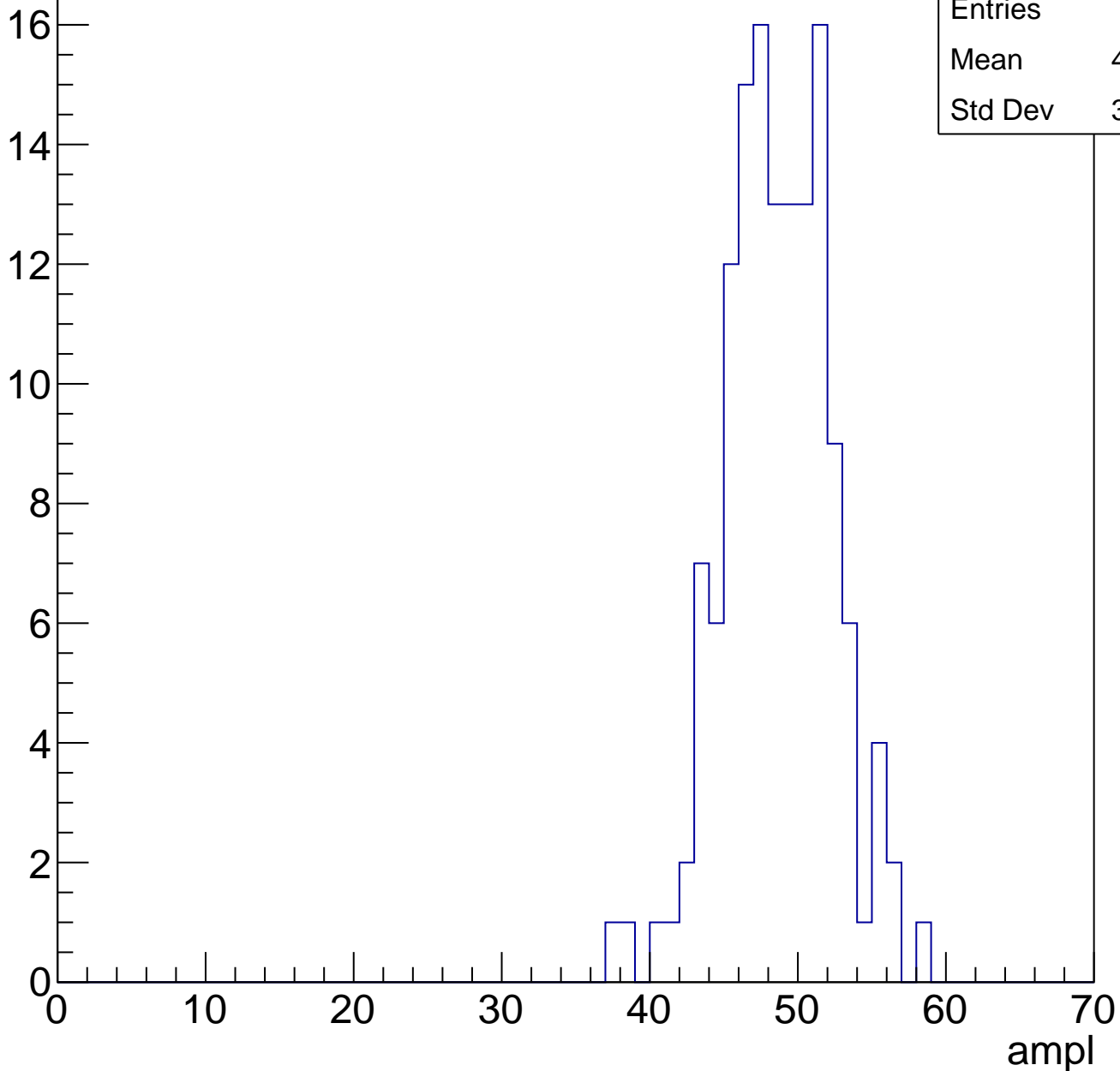
Gaus Width: 3.5101



B0L101S, U21-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

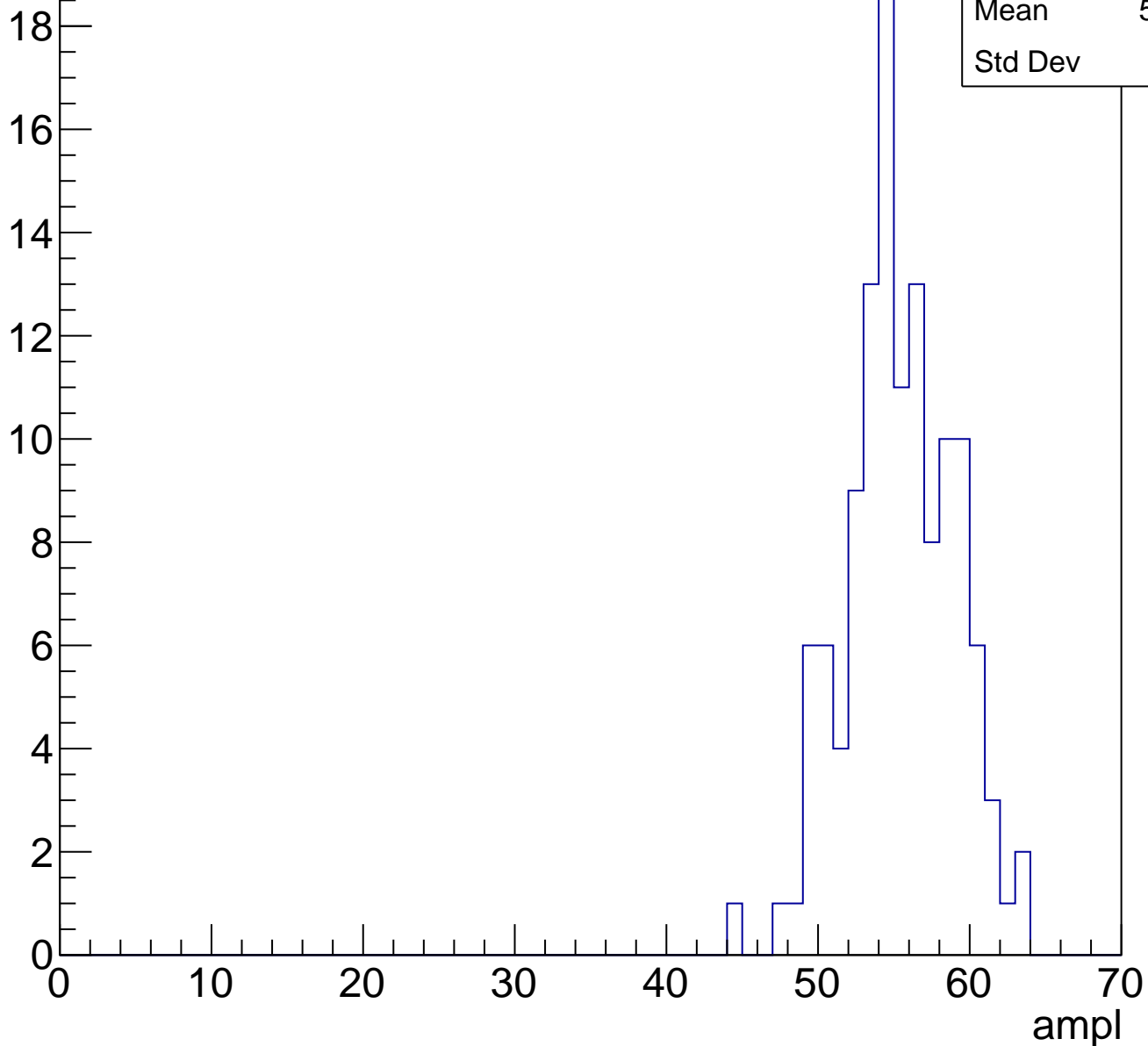


B0L101S, U21-ch124, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	54.92
Std Dev	3.53

Entry



B0L101S, U21-ch124, adc5

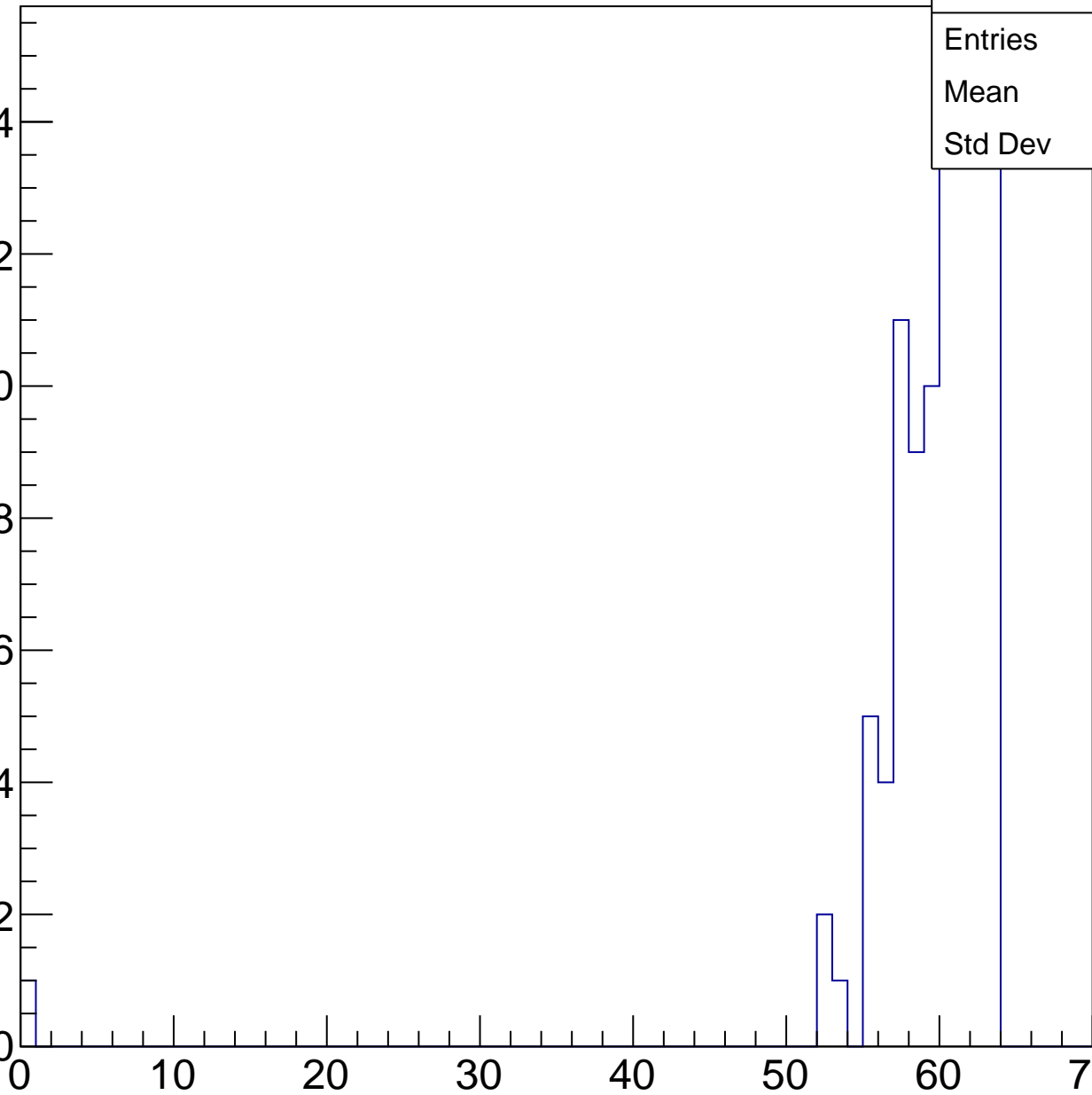
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	59.02
Std Dev	6.496

ampl

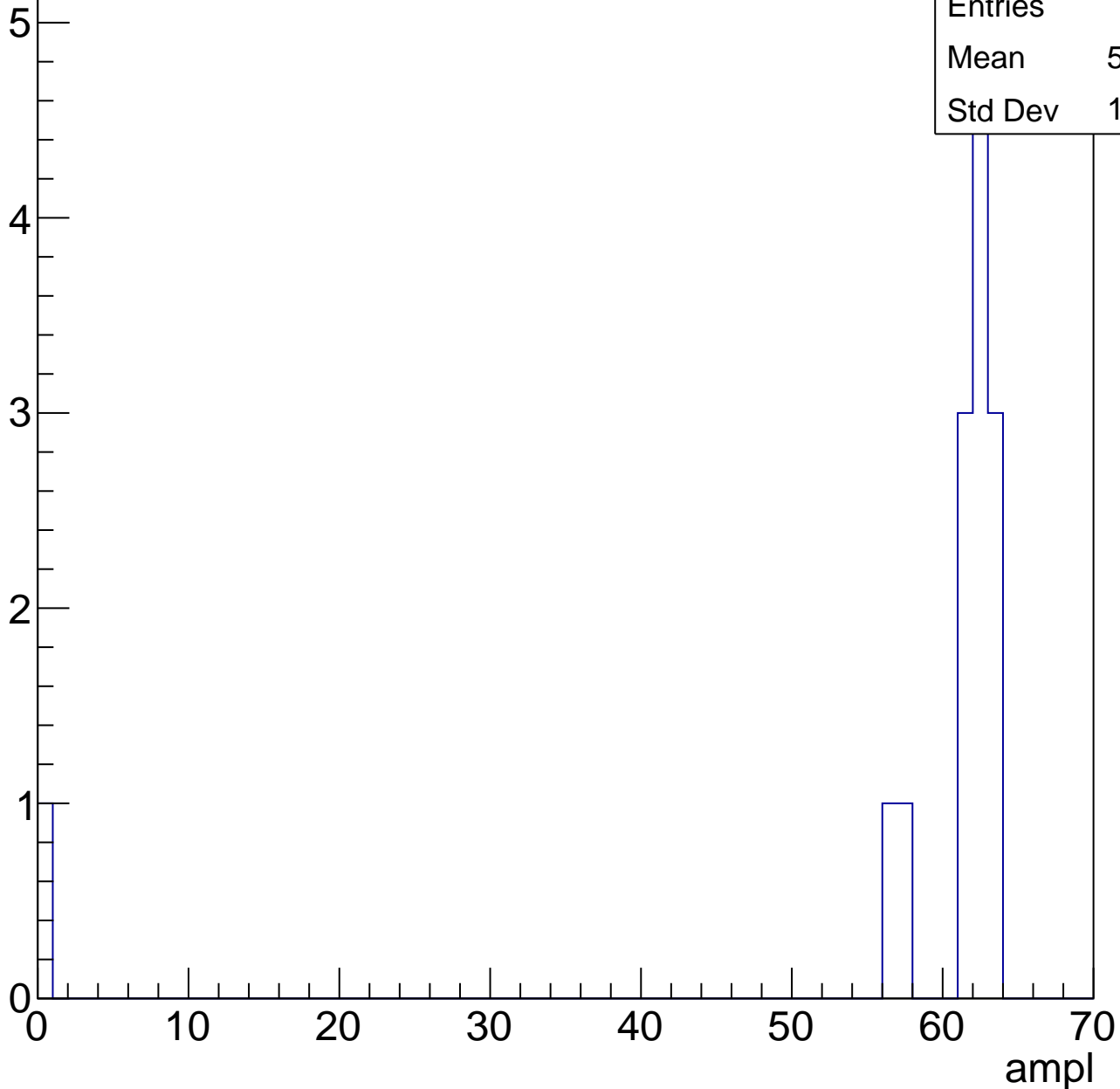


B0L101S, U21-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	56.79
Std Dev	15.88



B0L101S, U21-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch125, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	29.73
Std Dev	3.283

Gaus mean : 30.3238

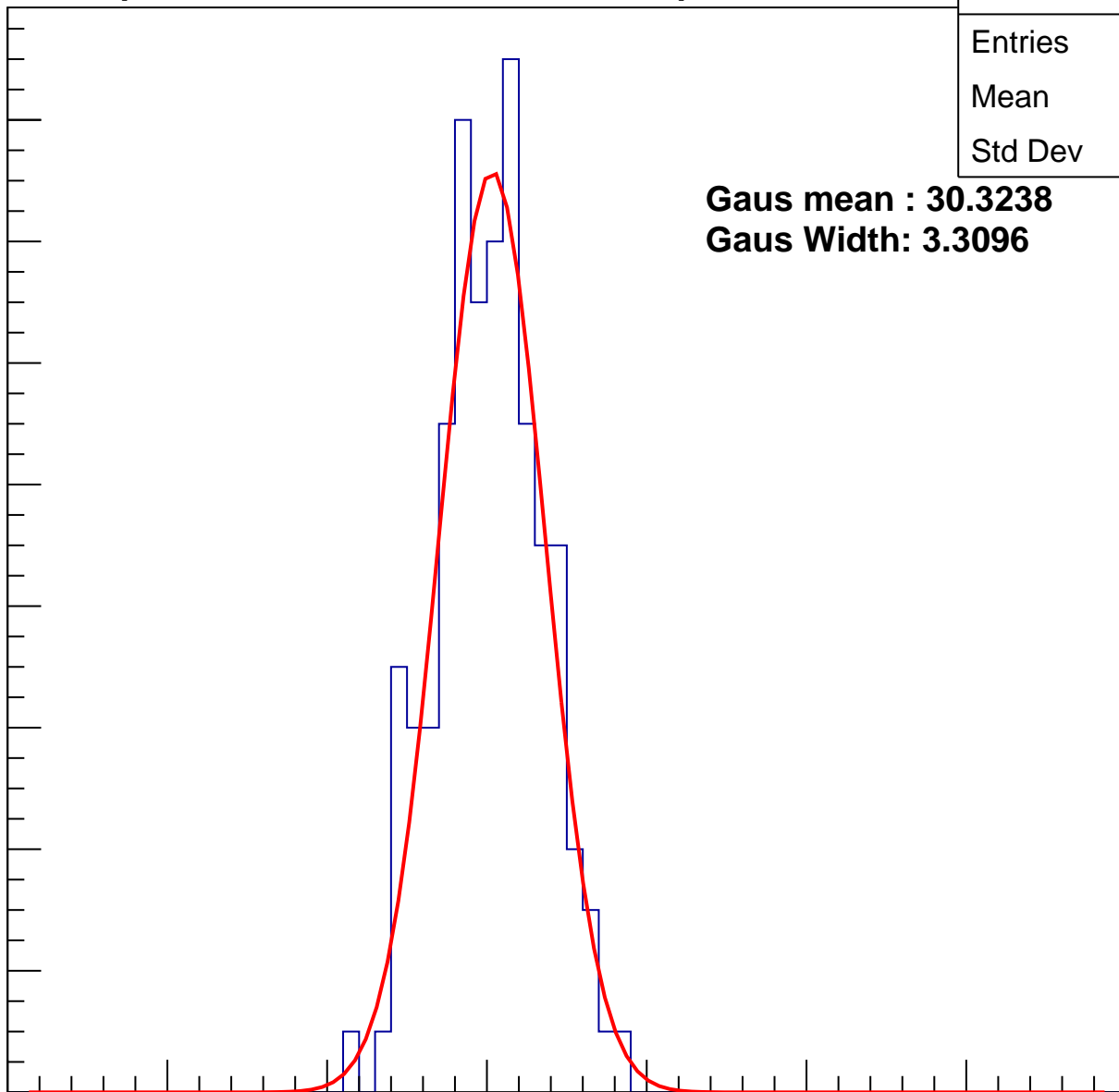
Gaus Width: 3.3096

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

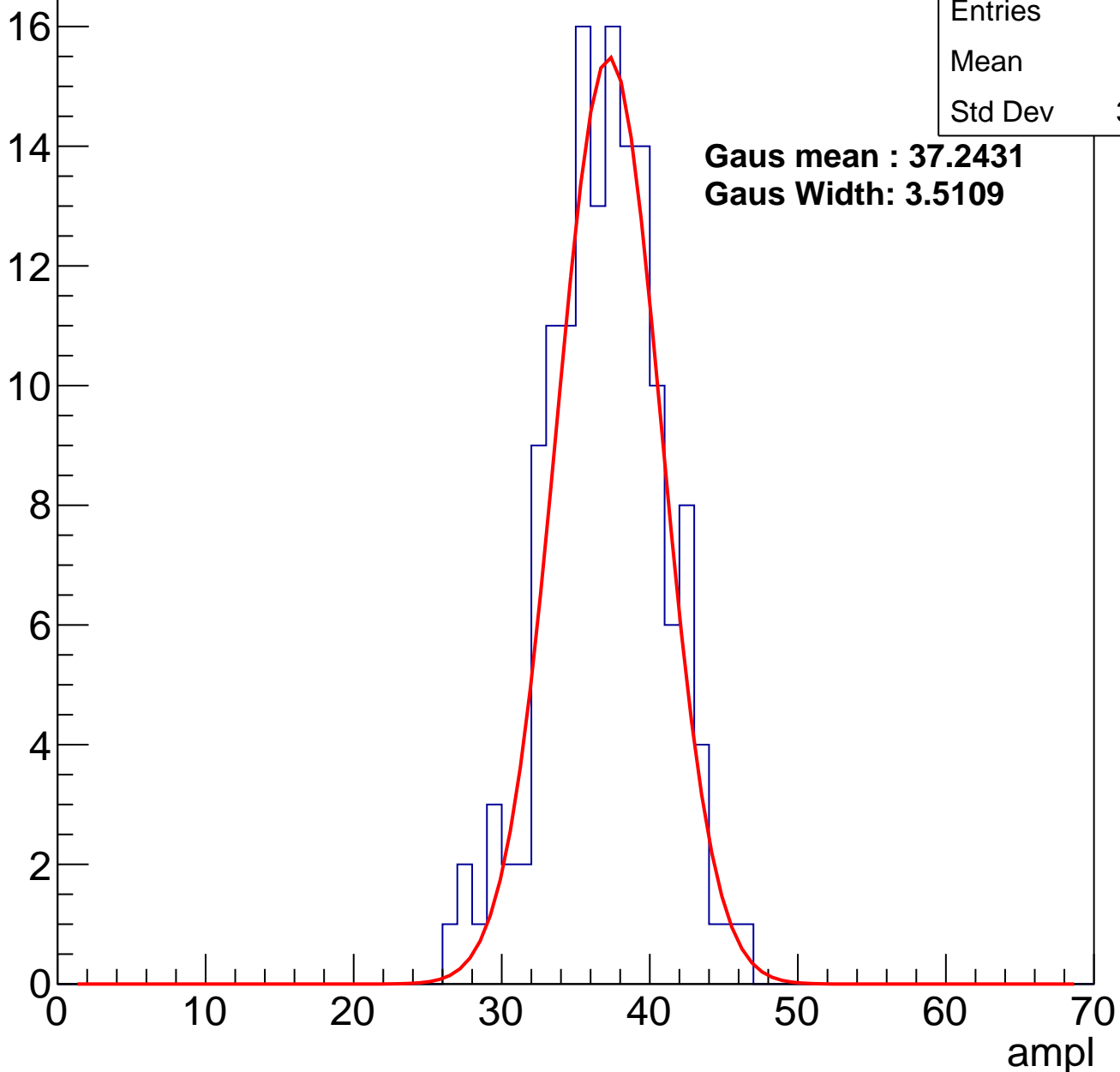
ampl



B0L101S, U21-ch125, adc1

calib_packv5_042523_0143.root, FC#1, port C1

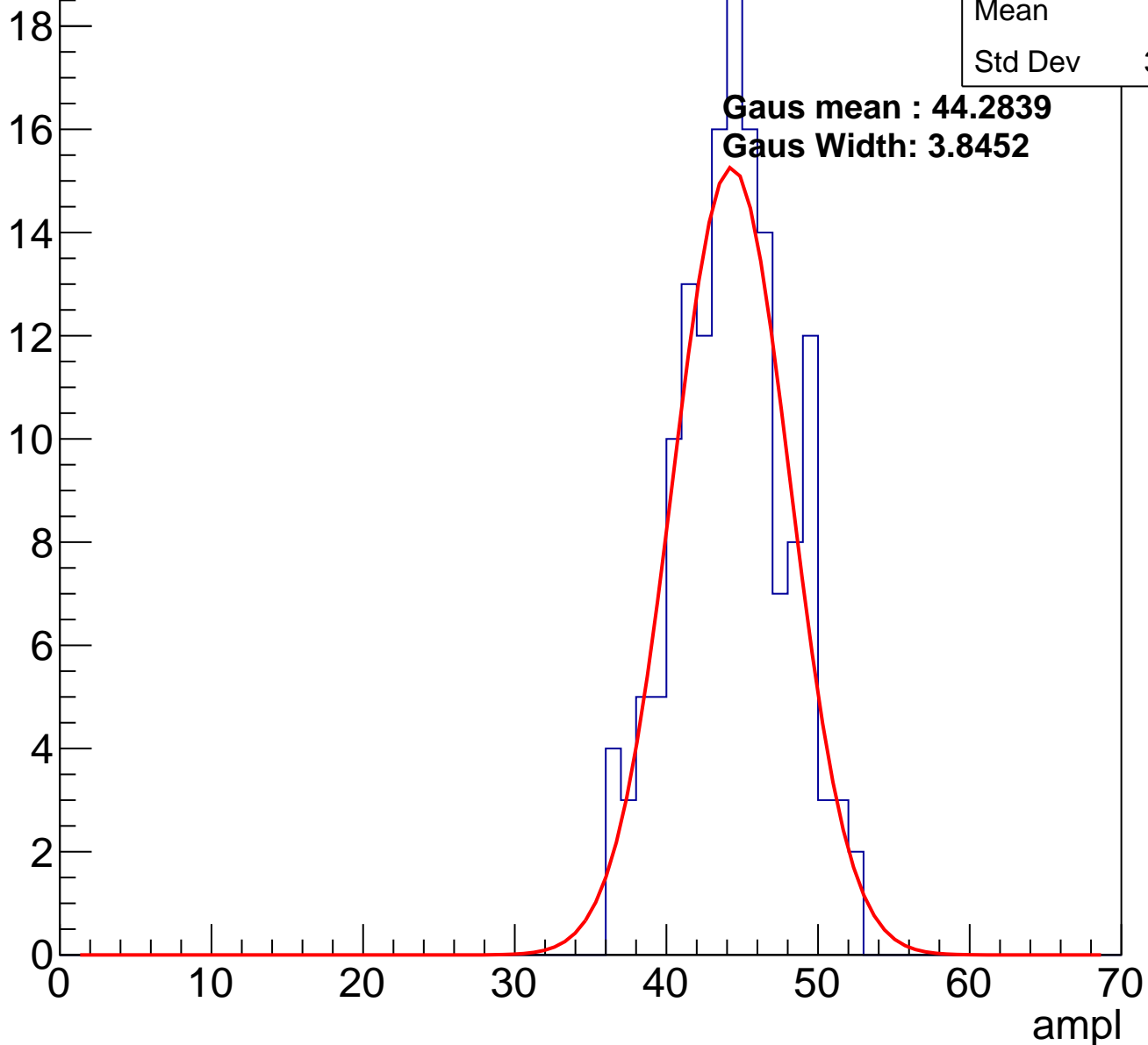
Entry



B0L101S, U21-ch125, adc2

calib_packv5_042523_0143.root, FC#1, port C1

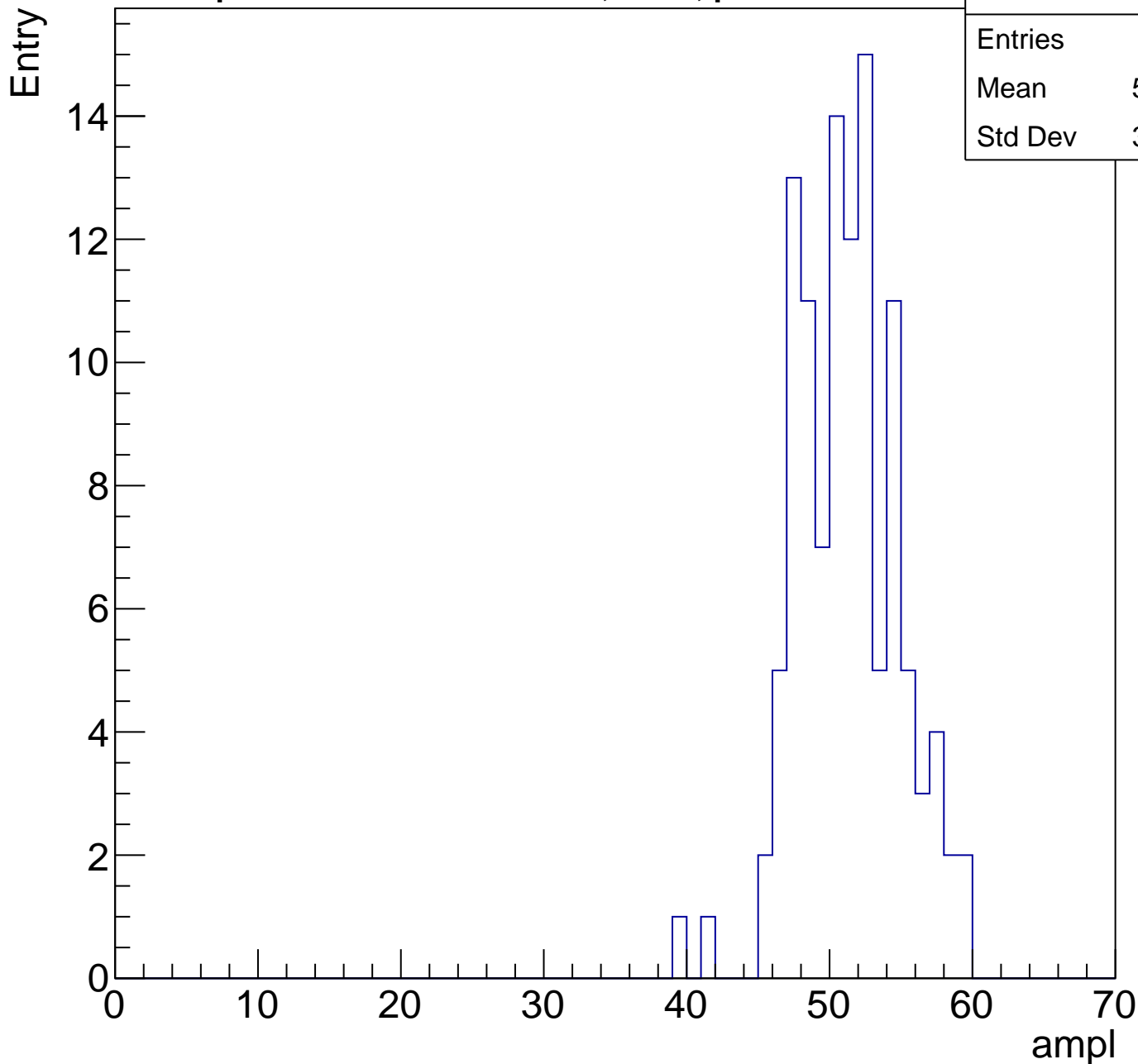
Entry



B0L101S, U21-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

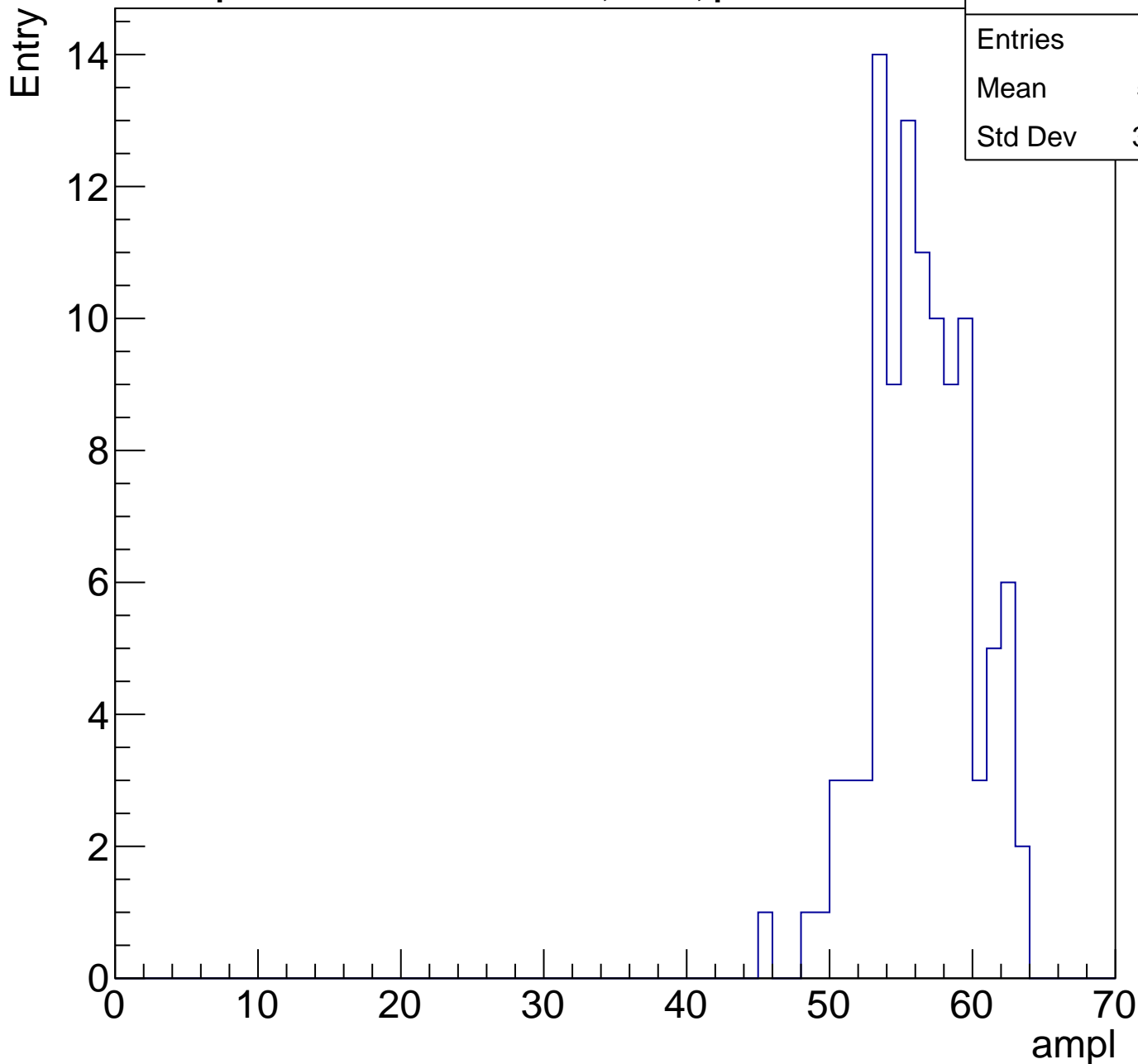
Entries	113
Mean	50.78
Std Dev	3.574



B0L101S, U21-ch125, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	56.01
Std Dev	3.485



B0L101S, U21-ch125, adc5

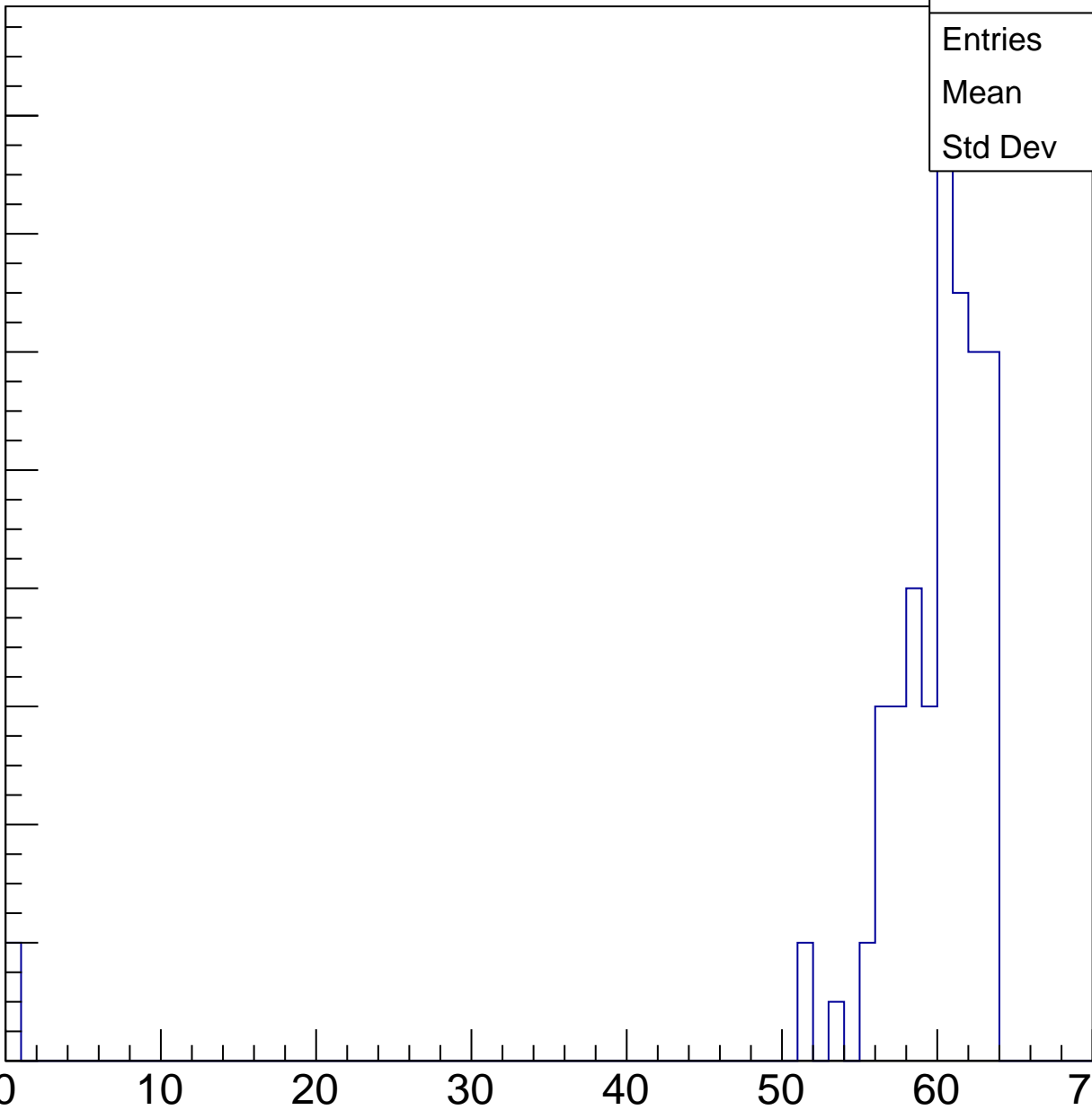
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.32
Std Dev	9.33

ampl

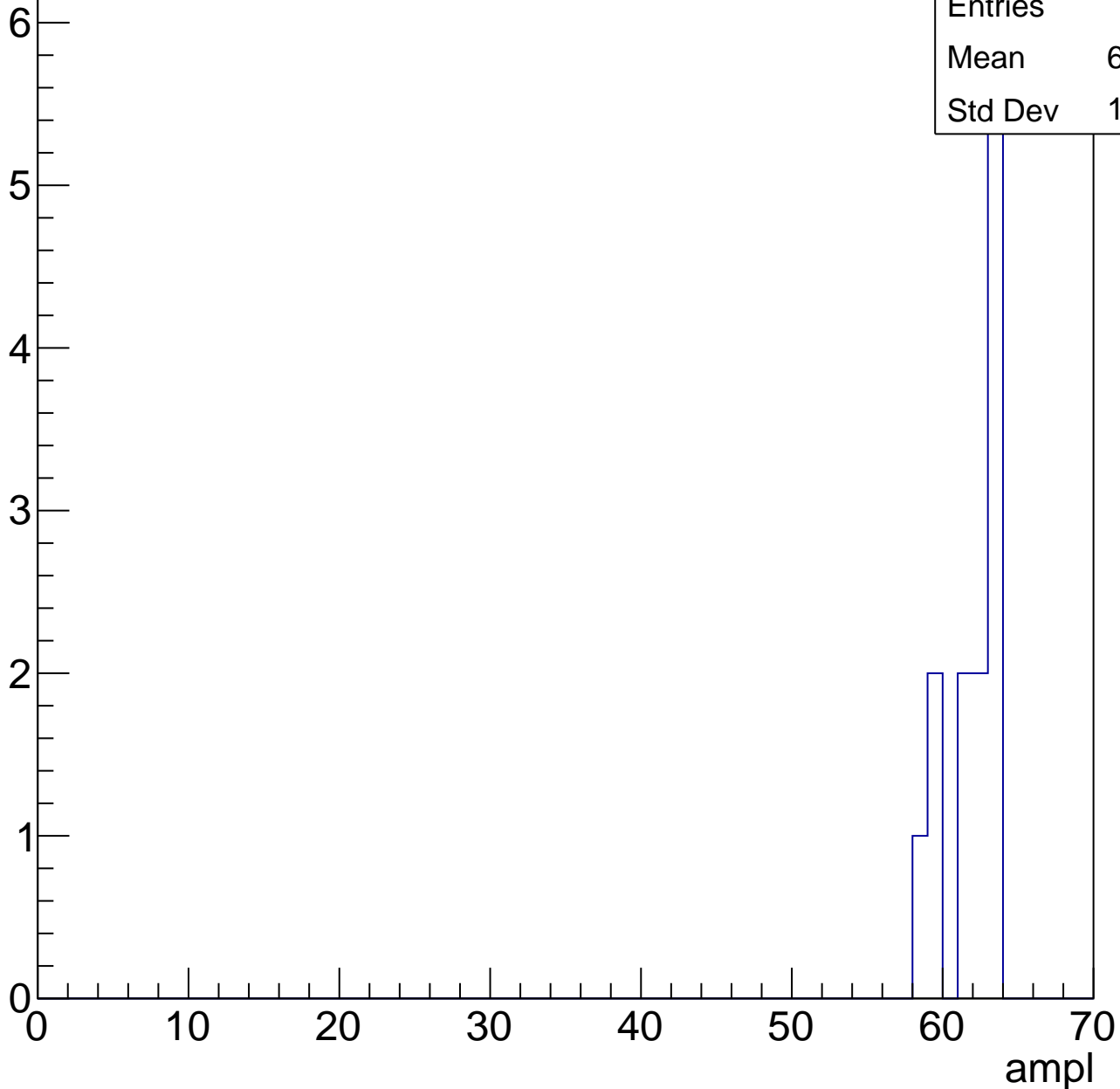


B0L101S, U21-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

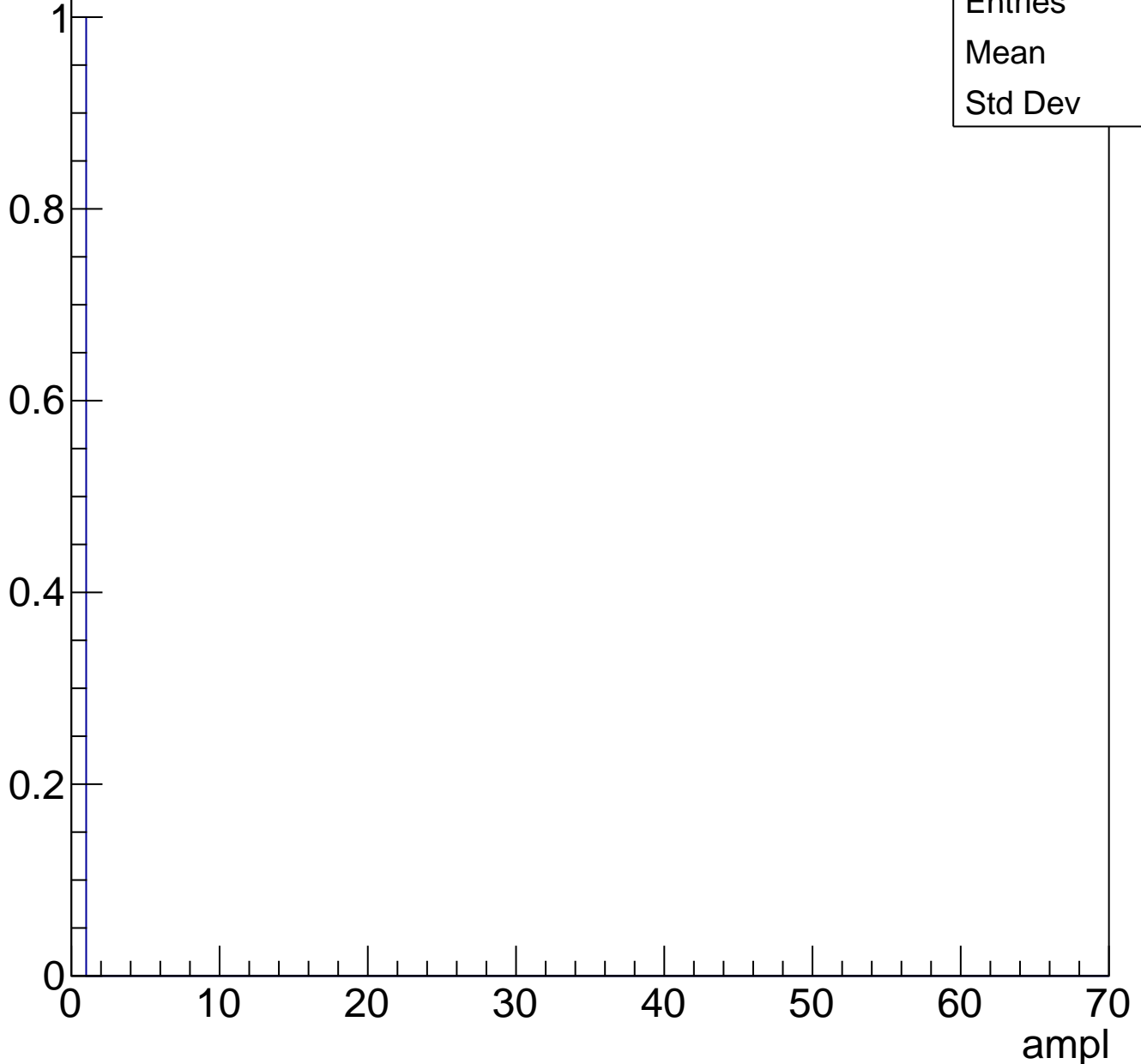
Entries	13
Mean	61.54
Std Dev	1.737



B0L101S, U21-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U21-ch126, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	189
Mean	27.34
Std Dev	9.649

Gaus mean : 27.0674

Gaus Width: 4.7173

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

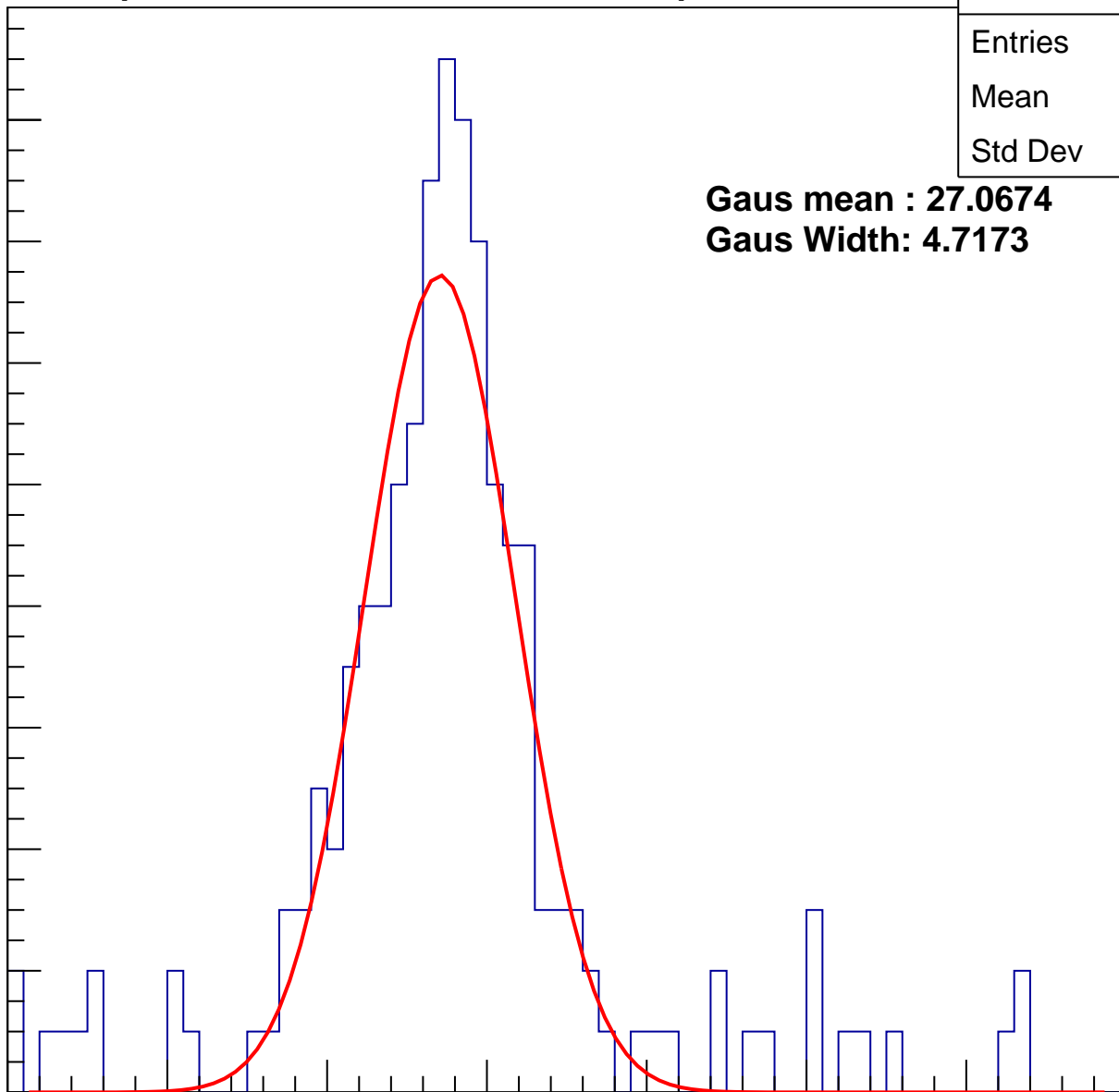
40

50

60

70

ampl



B0L101S, U21-ch126, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	34.88
Std Dev	4.098

Gaus mean : 35.1360

Gaus Width: 3.8708

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

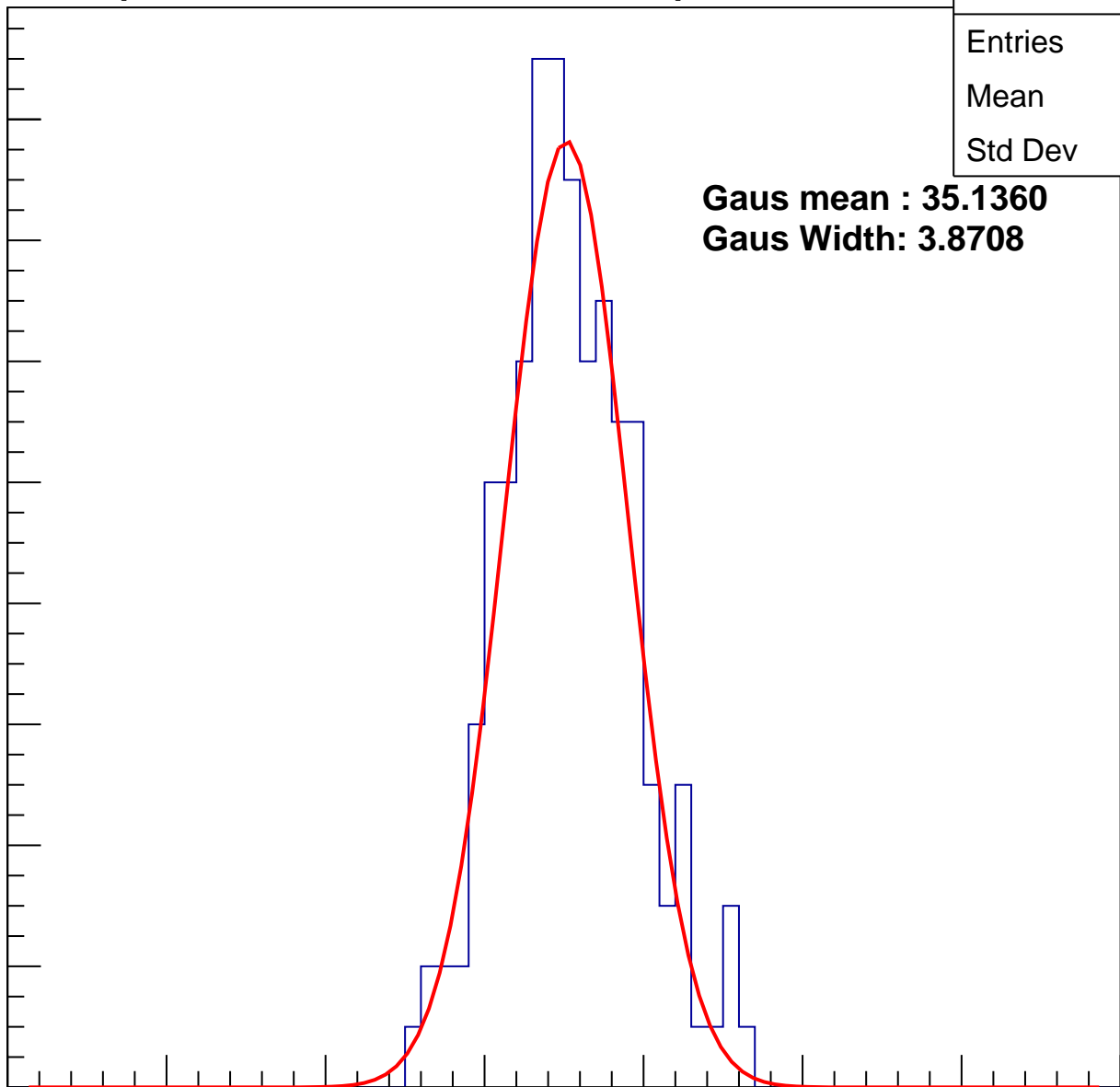
40

50

60

70

ampl



B0L101S, U21-ch126, adc2

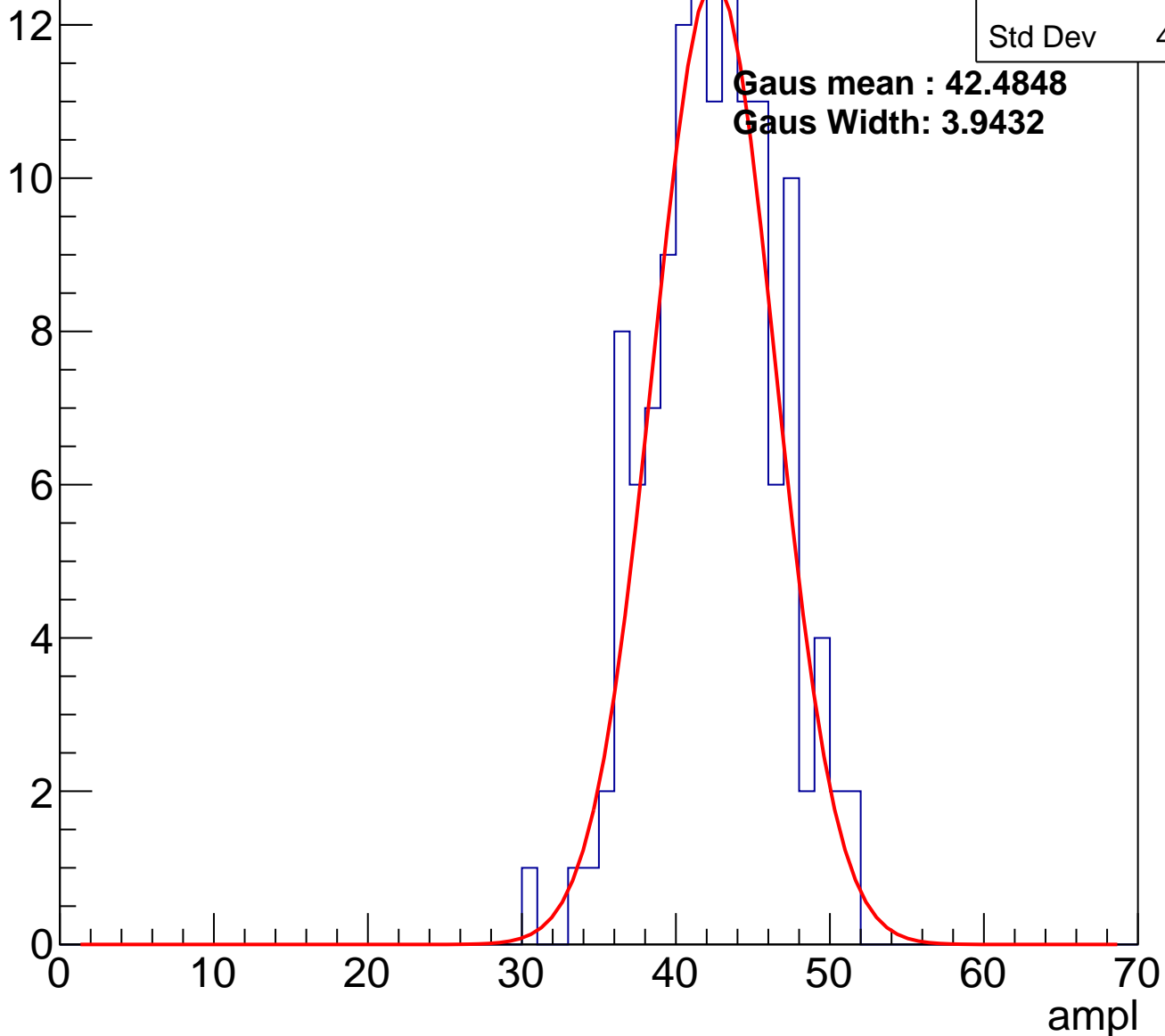
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	42.02
Std Dev	4.025

Gaus mean : 42.4848

Gaus Width: 3.9432

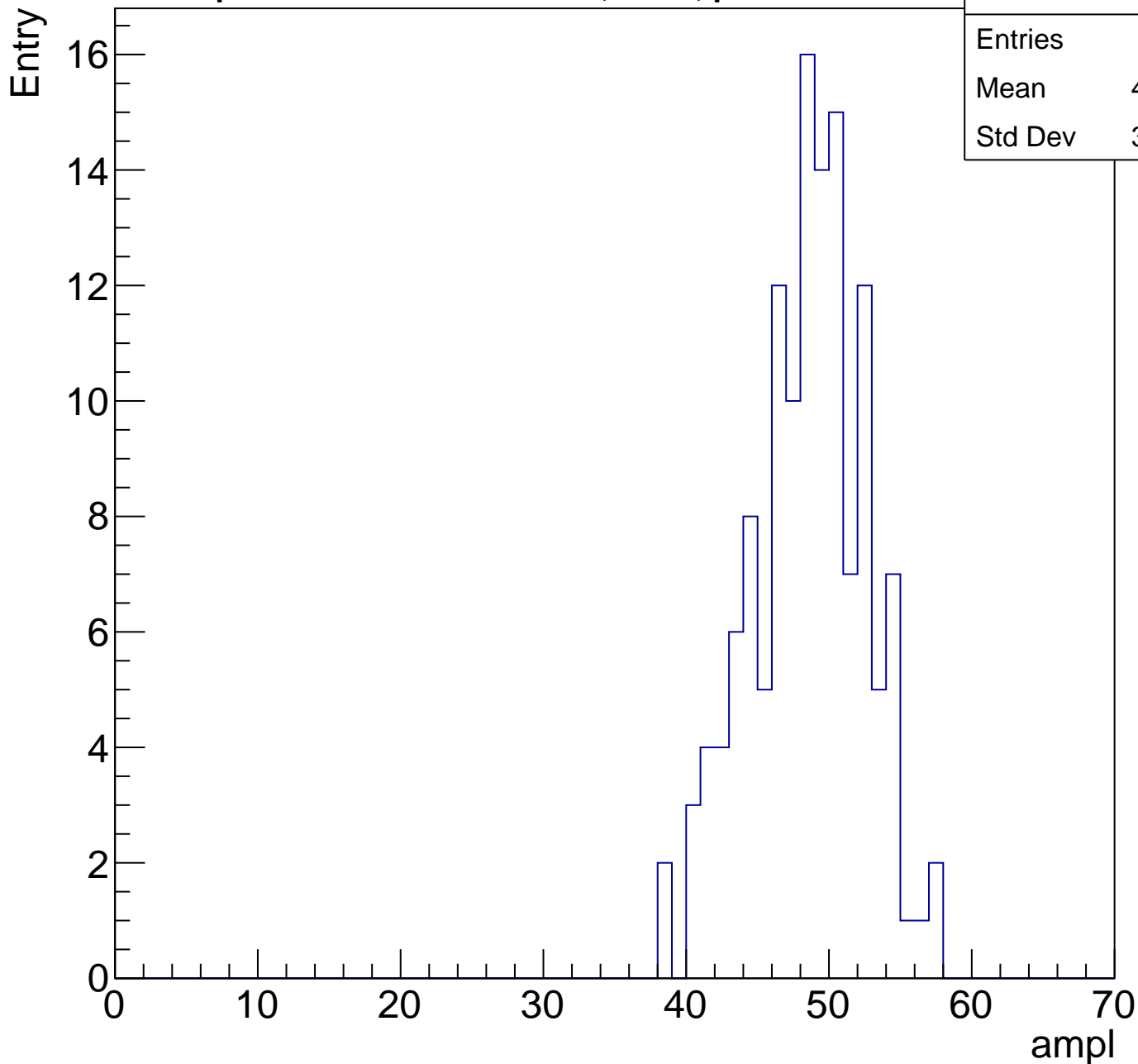
Entry



B0L101S, U21-ch126, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	48.04
Std Dev	3.953

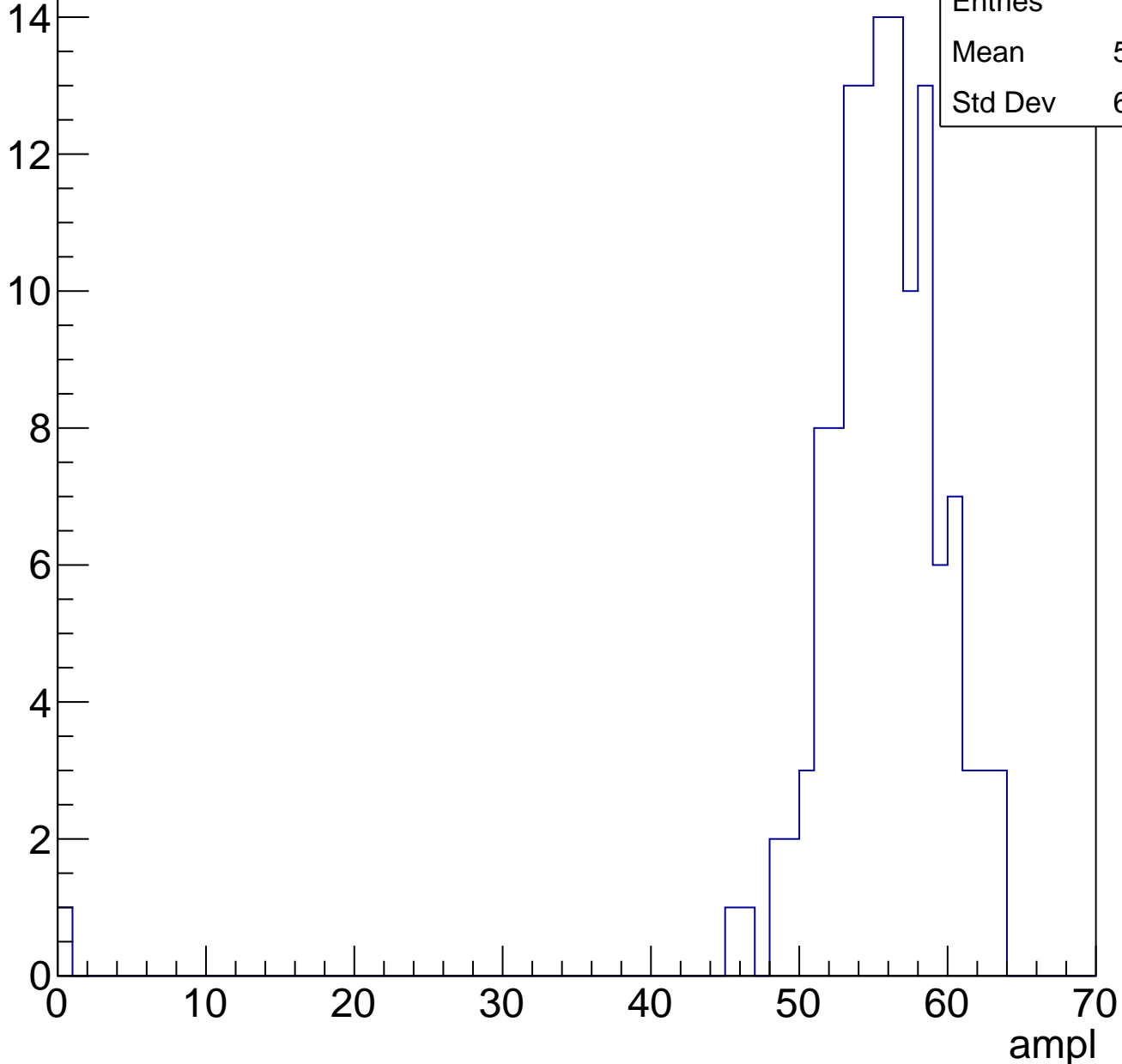


B0L101S, U21-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	54.88
Std Dev	6.068

Entry



B0L101S, U21-ch126, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

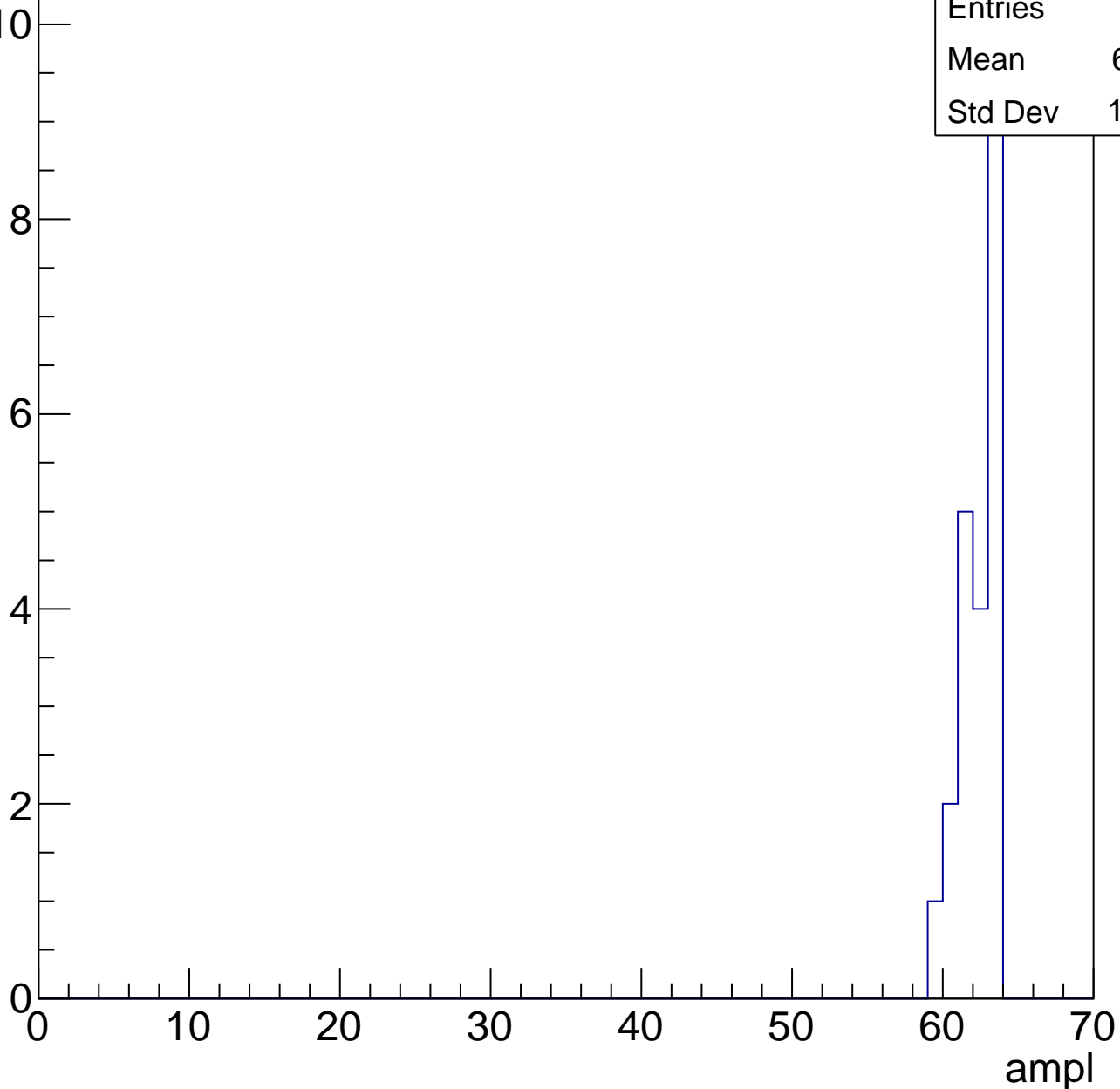
Entries	87
Mean	58.41
Std Dev	6.921

B0L101S, U21-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	22
Mean	61.91
Std Dev	1.203



B0L101S, U21-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U21-ch127, adc0

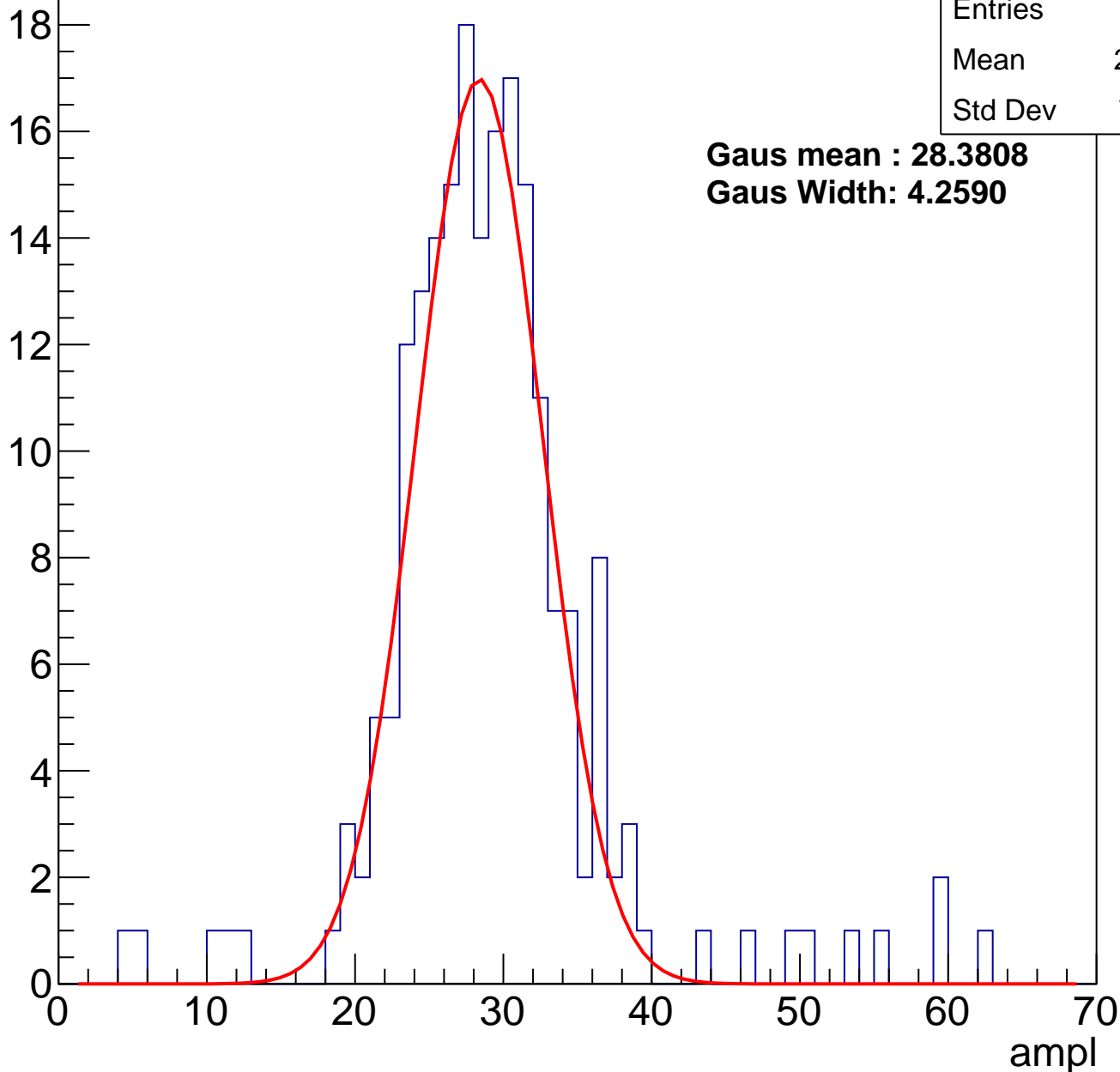
calib_packv5_042523_0143.root, FC#1, port C1

Entries	205
Mean	28.78
Std Dev	7.451

Gaus mean : 28.3808

Gaus Width: 4.2590

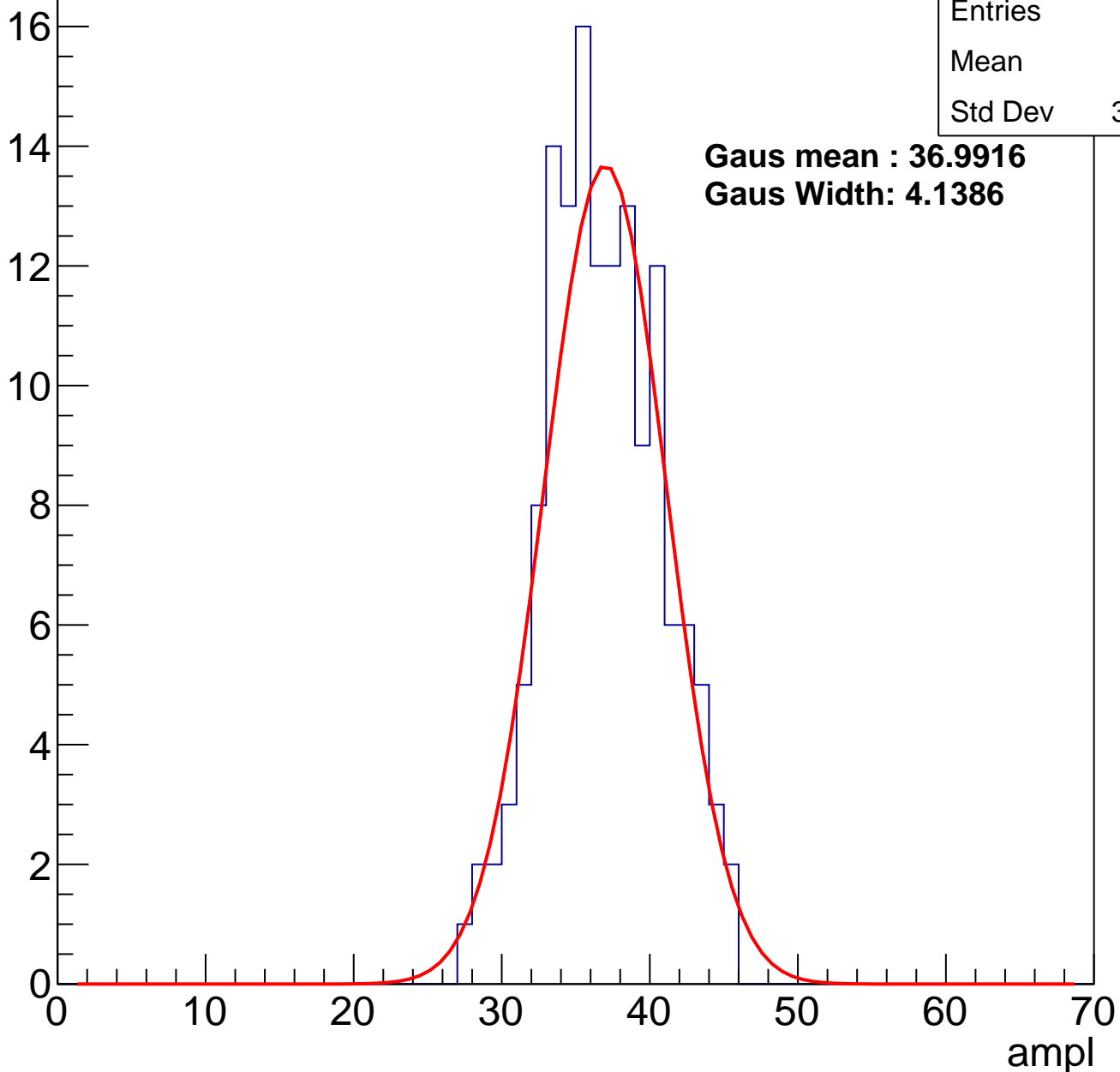
Entry



B0L101S, U21-ch127, adc1

calib_packv5_042523_0143.root, FC#1, port C1

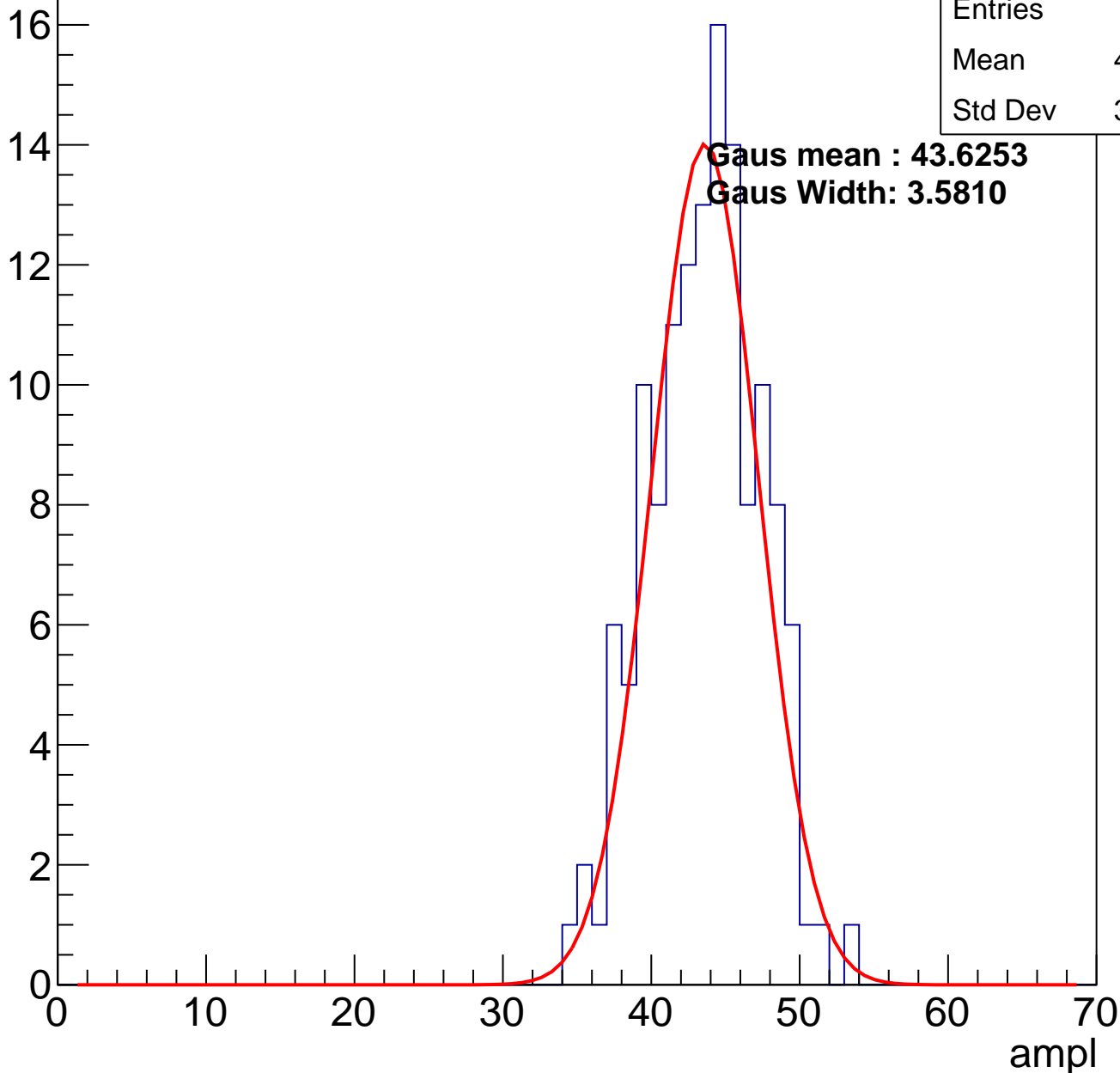
Entry



B0L101S, U21-ch127, adc2

calib_packv5_042523_0143.root, FC#1, port C1

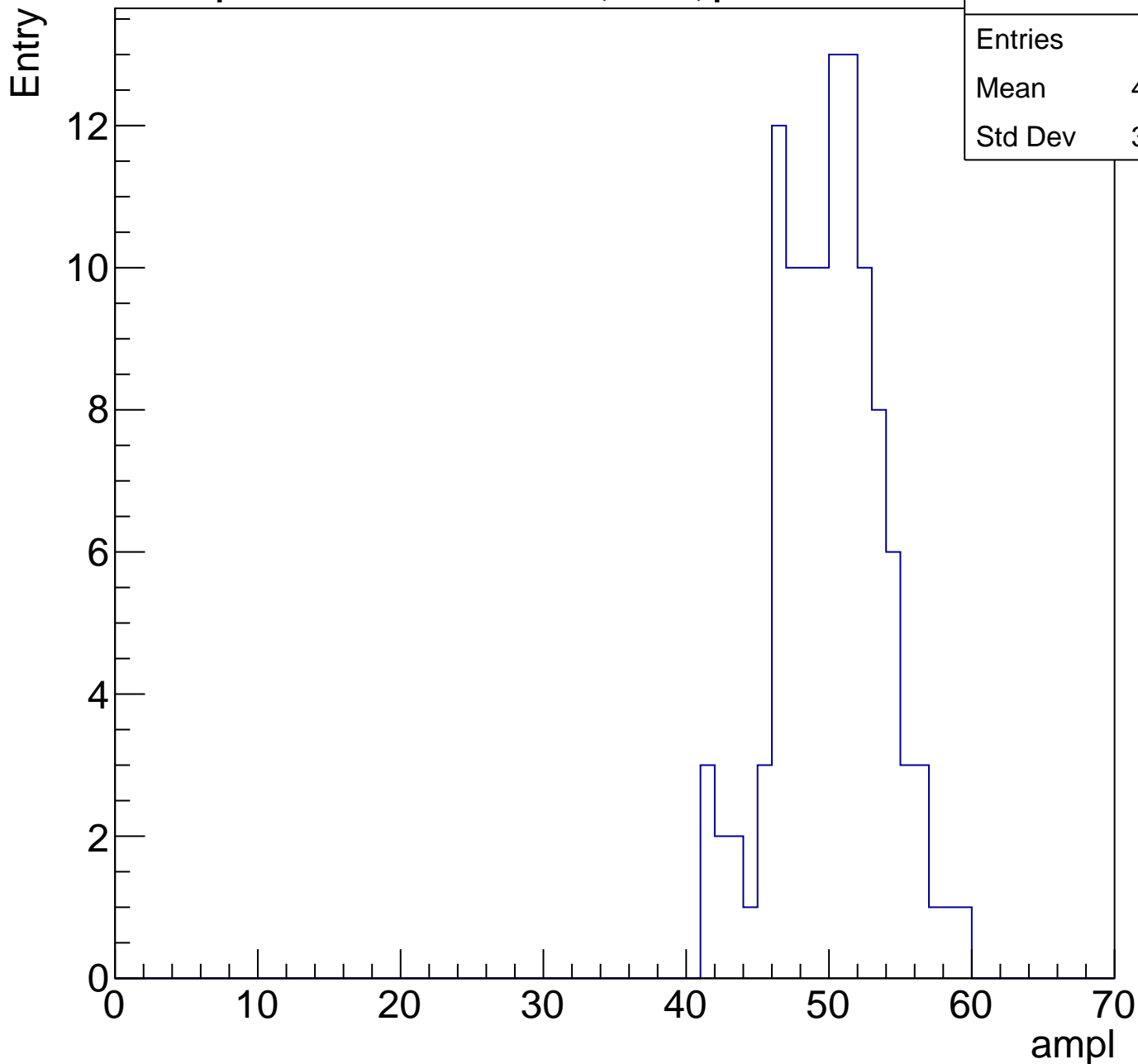
Entry



B0L101S, U21-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	49.57
Std Dev	3.647



B0L101S, U21-ch127, adc4

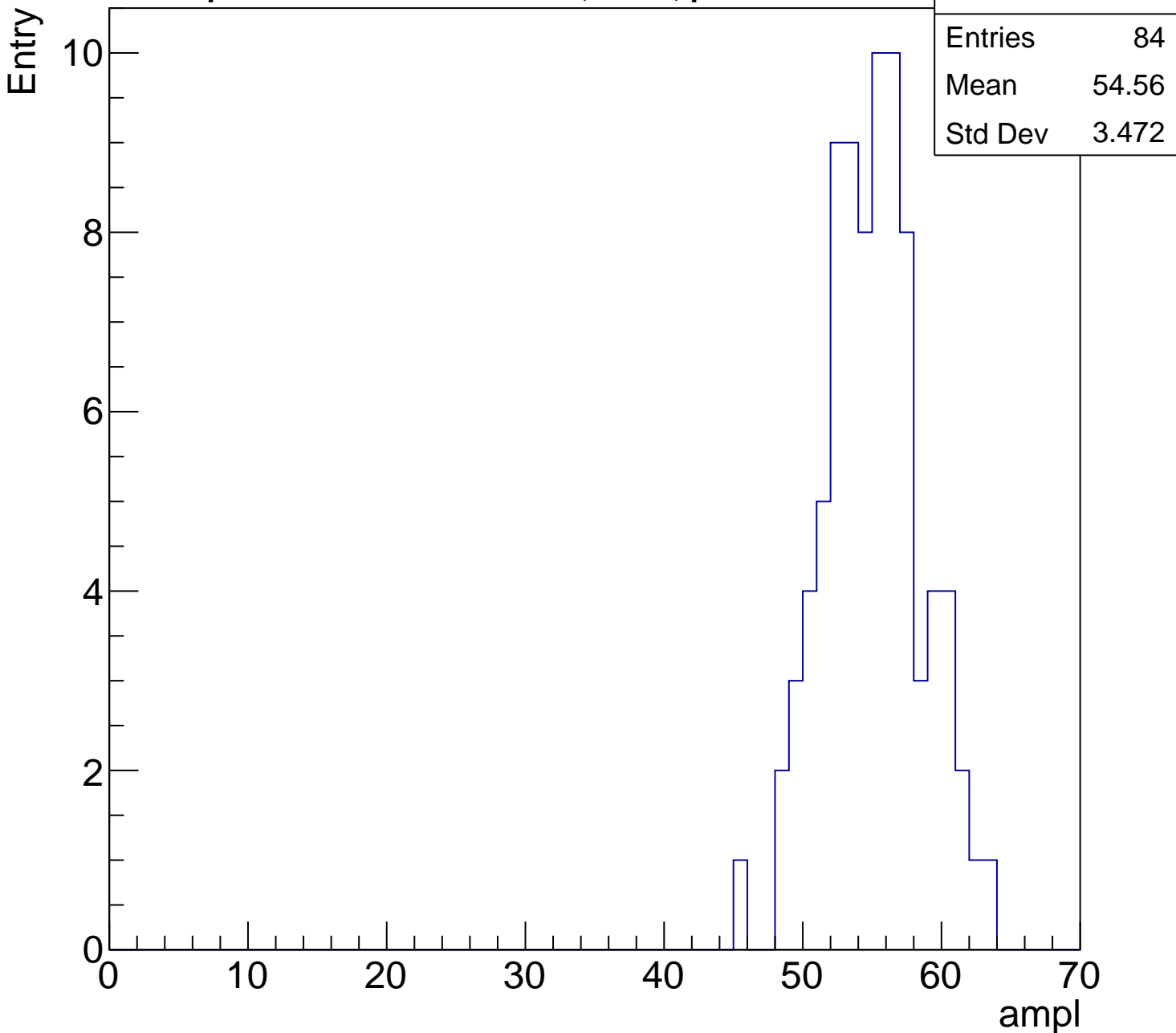
calib_packv5_042523_0143.root, FC#1, port C1

Entries	84
Mean	54.56
Std Dev	3.472

Entry

10
8
6
4
2
0

ampl



B0L101S, U21-ch127, adc5

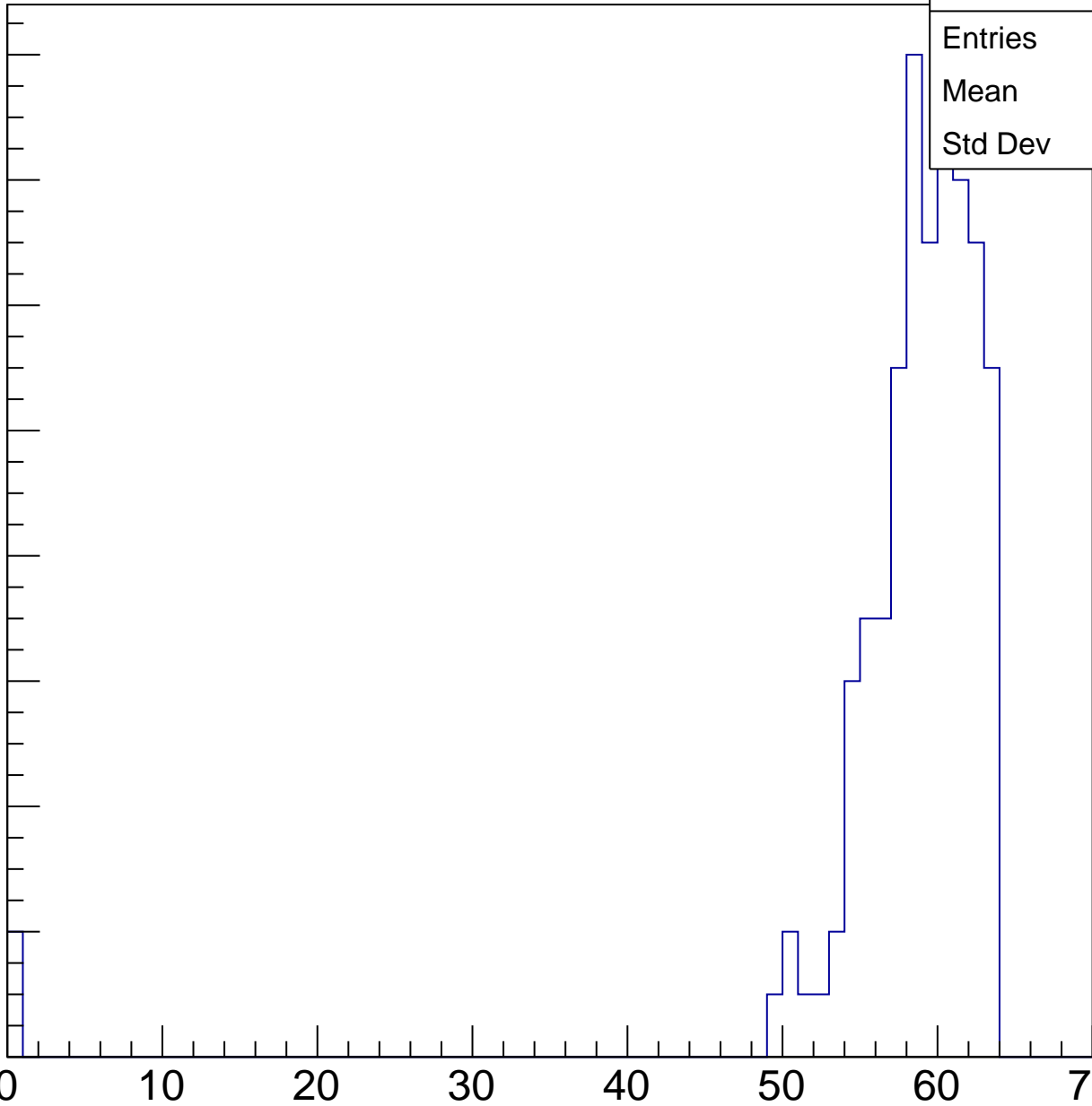
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	57.66
Std Dev	8.066

ampl

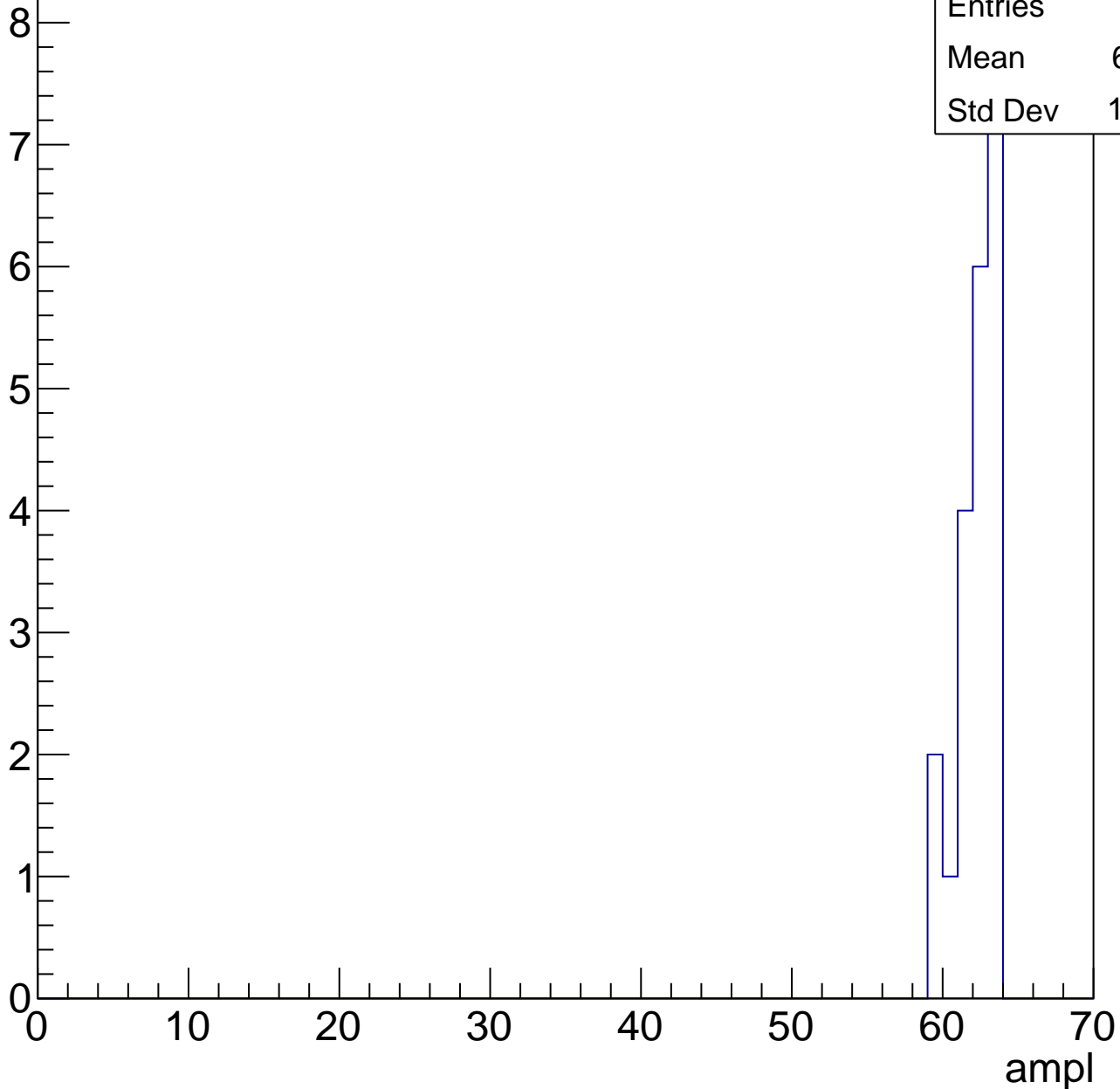


B0L101S, U21-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	61.81
Std Dev	1.258



B0L101S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

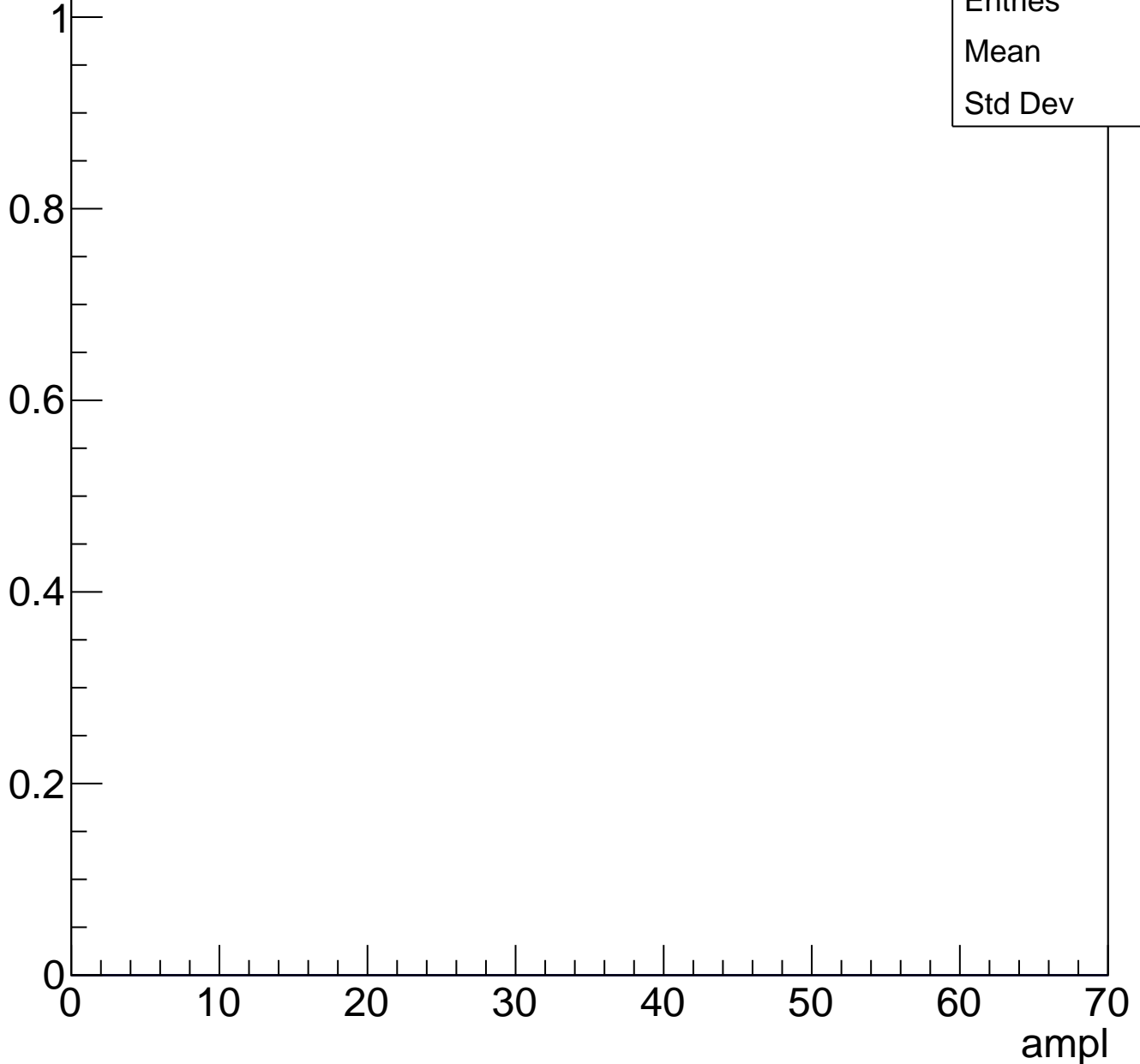


Entries	0
Mean	0
Std Dev	0

B0L101S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0