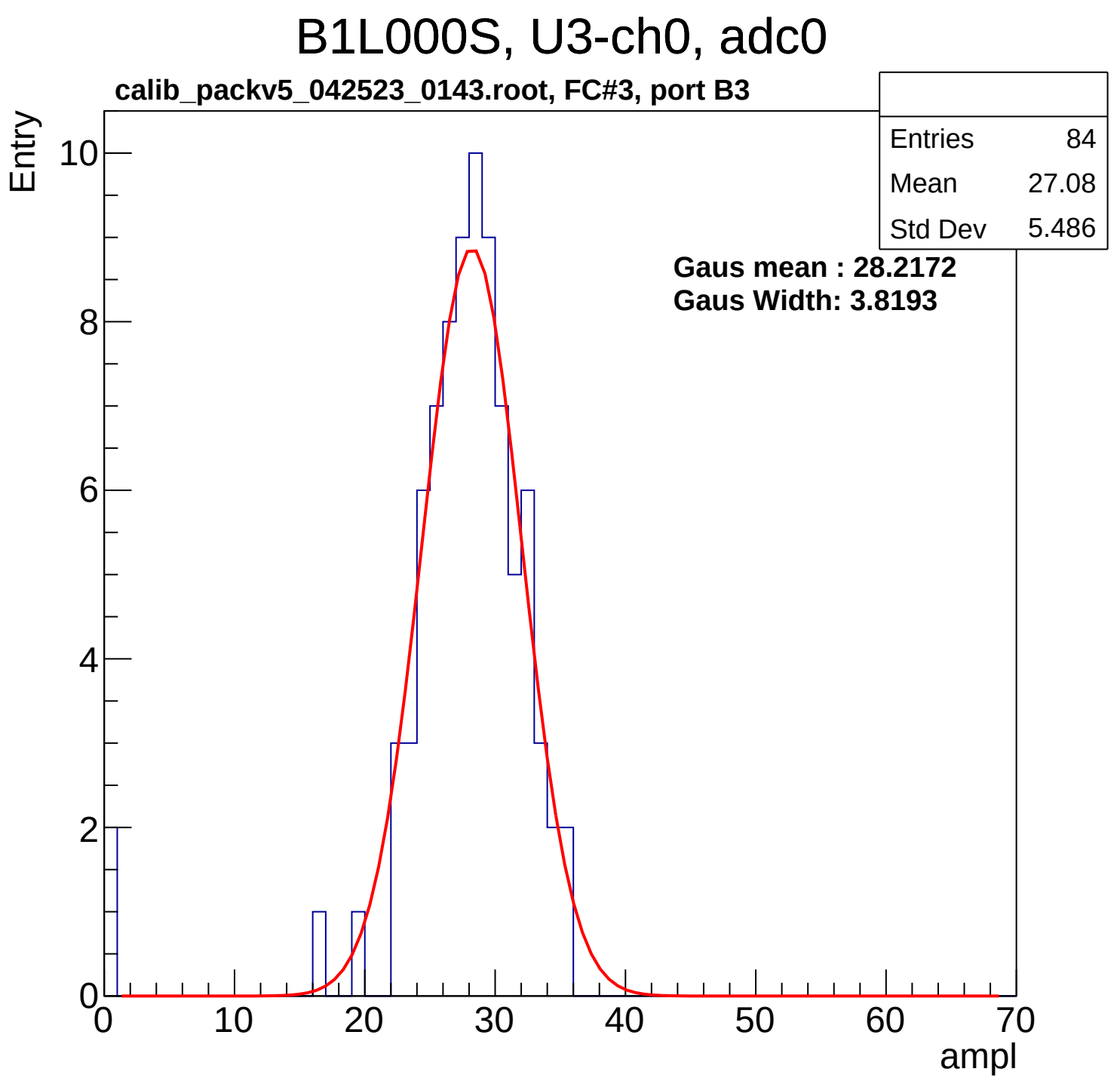
B1L000S, U3-ch0, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 27.08
Std Dev 5.486
Gaus mean : 28.2172
Gaus Width: 3.8193
Entry
ampl
0
2
4
6
8
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0
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20
30
40
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60
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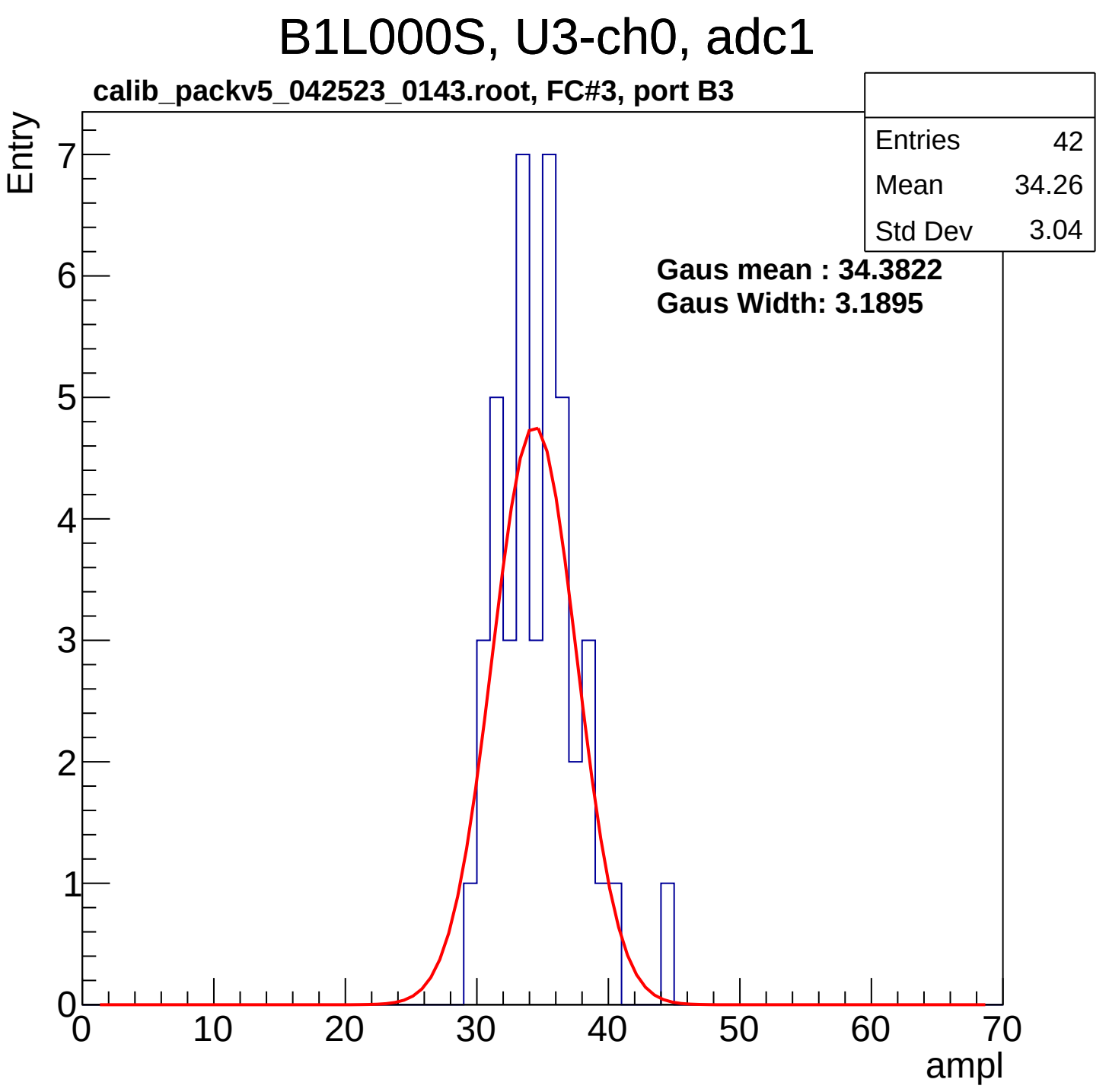
B1L000S, U3-ch0, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 34.26
Std Dev 3.04
Gaus mean : 34.3822
Gaus Width: 3.1895
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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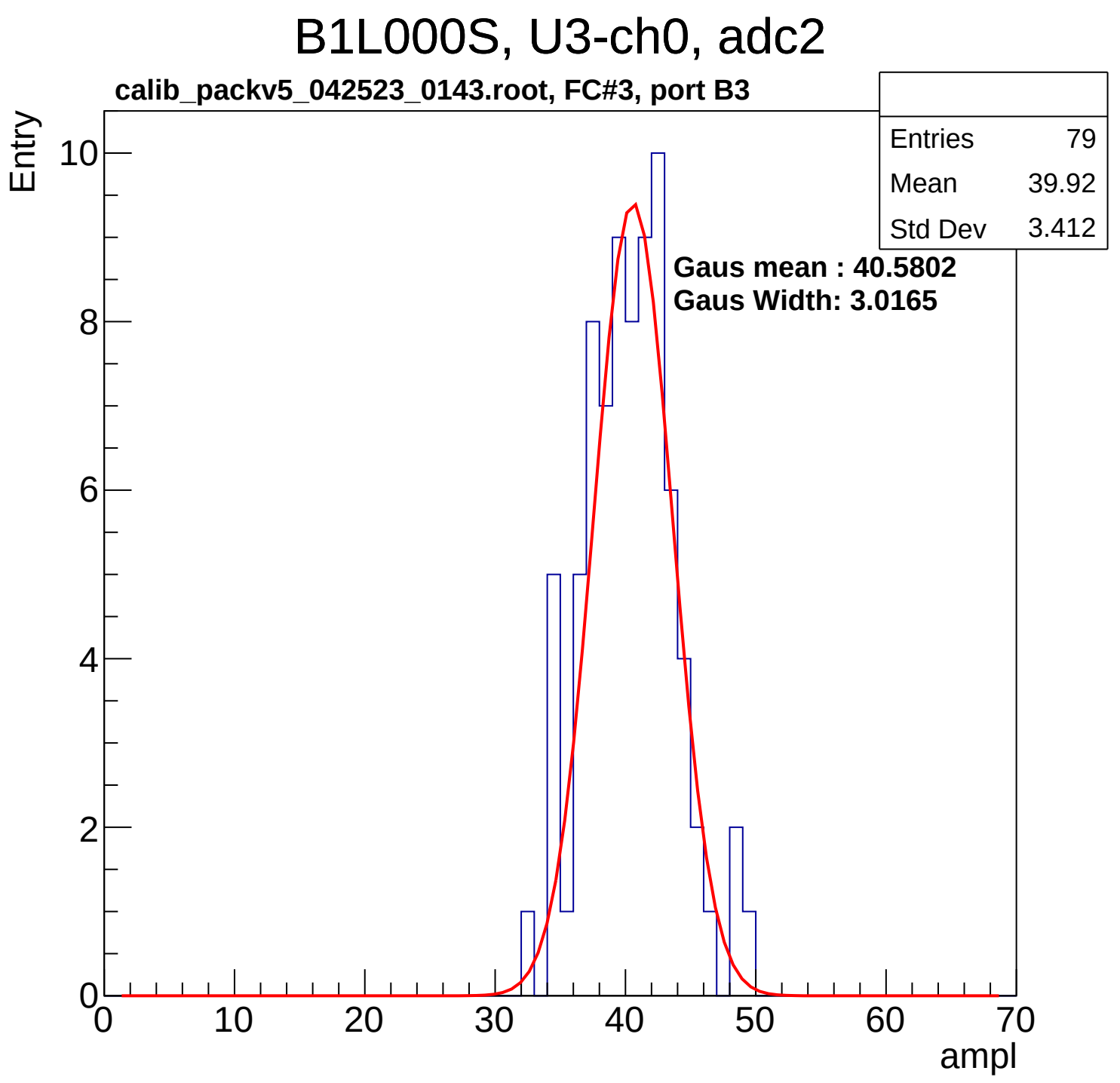
B1L000S, U3-ch0, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 39.92
Std Dev 3.412
Gaus mean : 40.5802
Gaus Width: 3.0165
Entry
ampl
0
2
4
6
8
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0
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60
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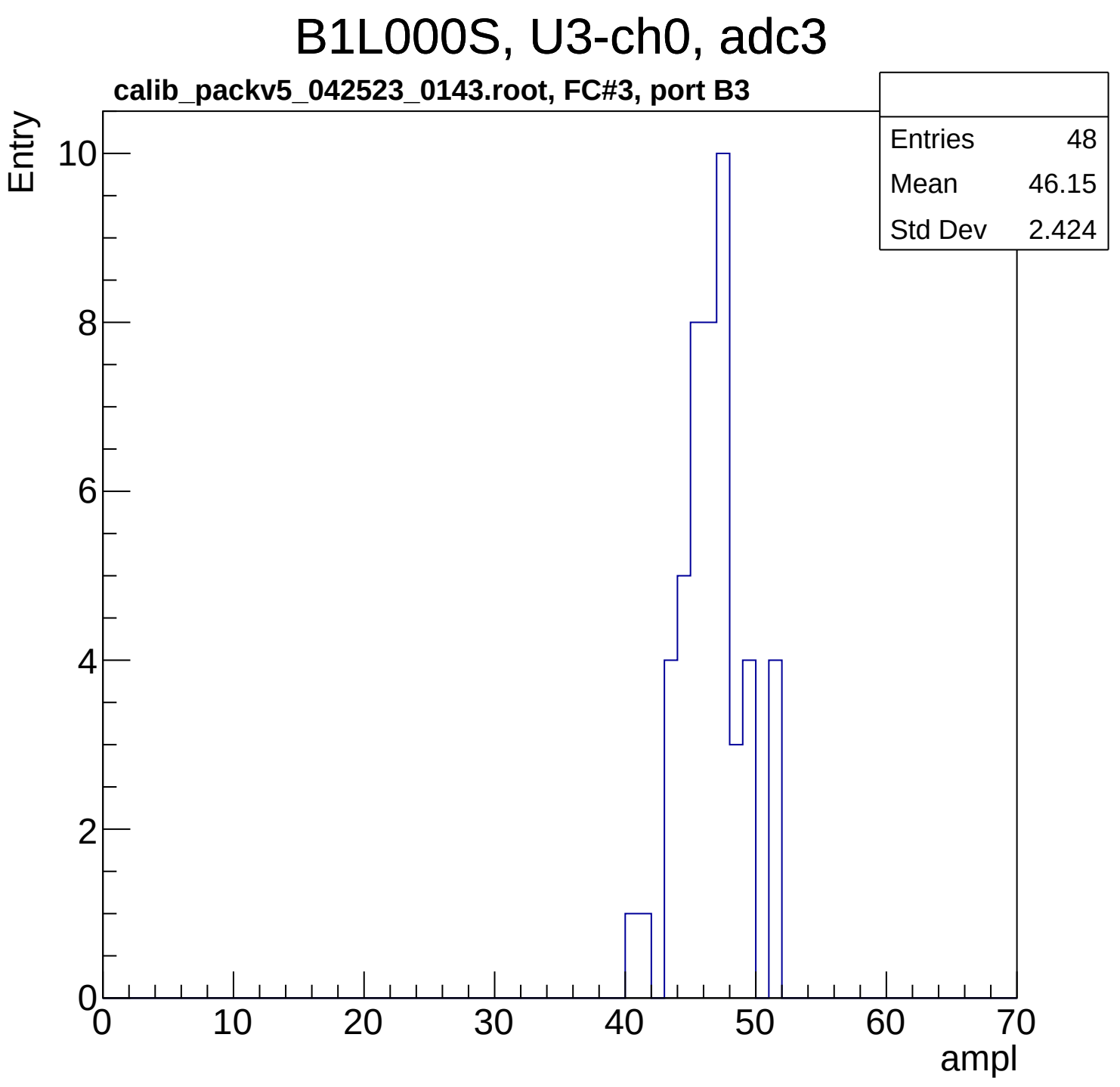
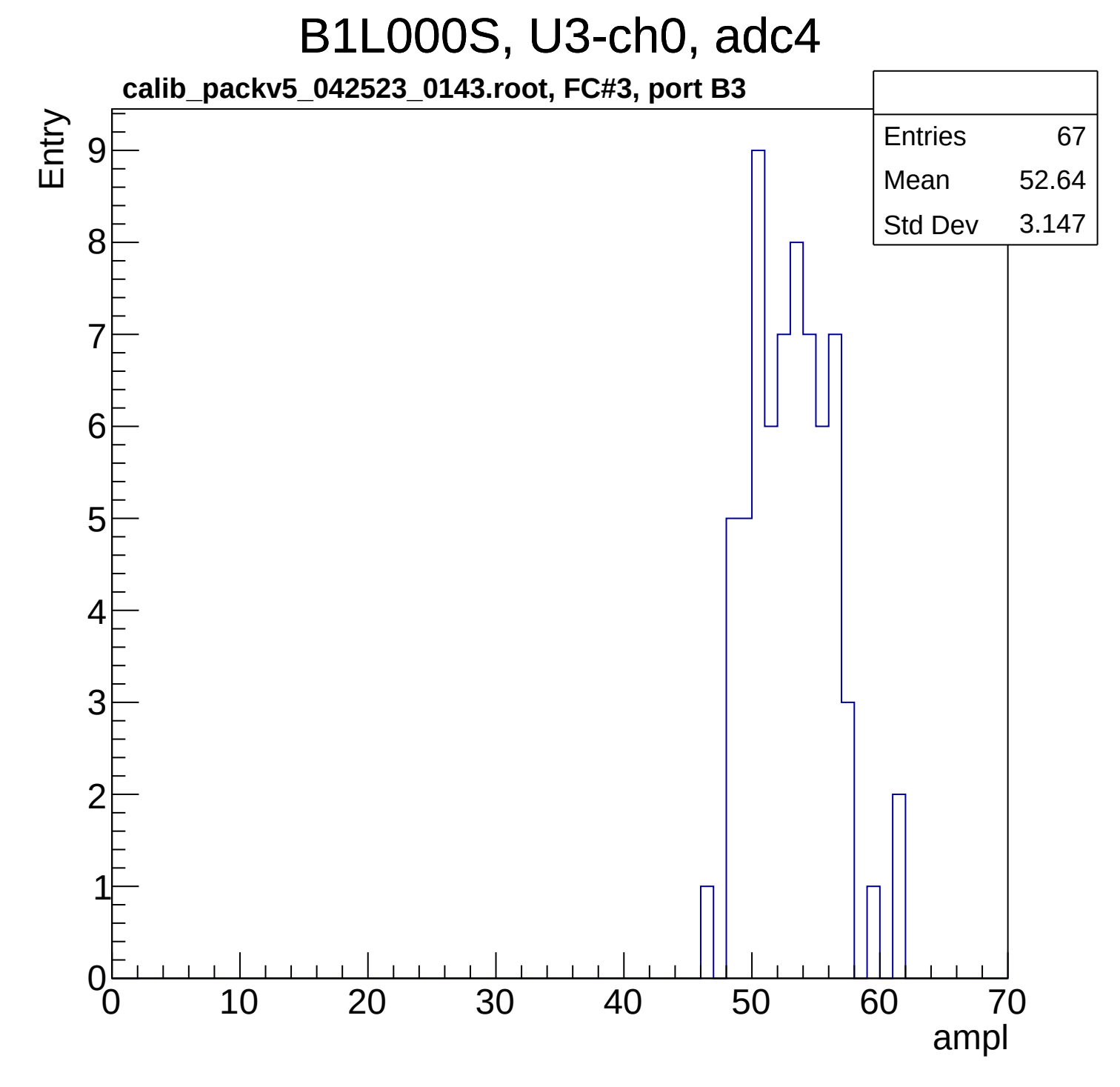

B1L000S, U3-ch0, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 46.15
Std Dev 2.424
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch0, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 52.64
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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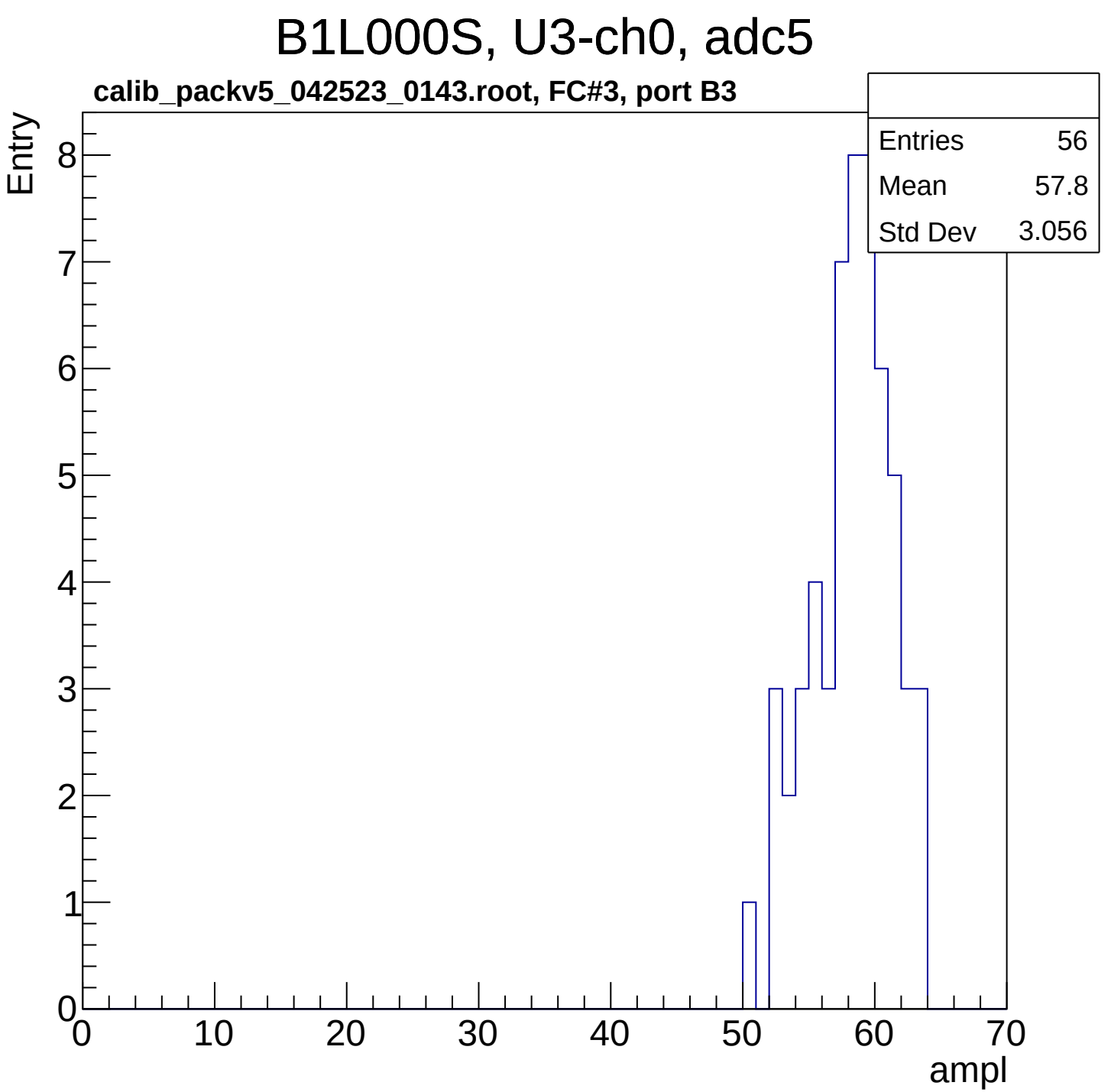
B1L000S, U3-ch0, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 57.8
Std Dev 3.056
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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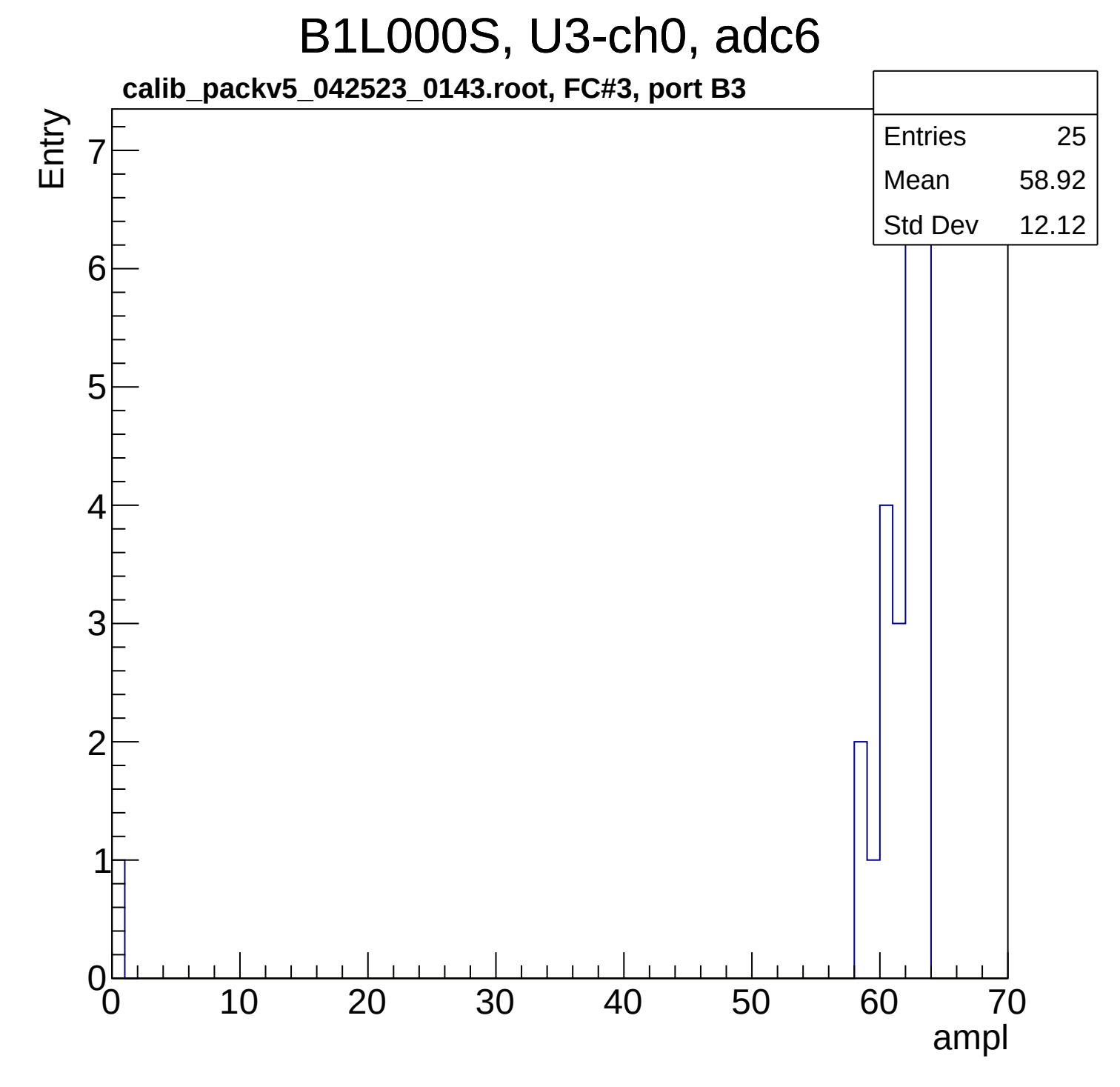
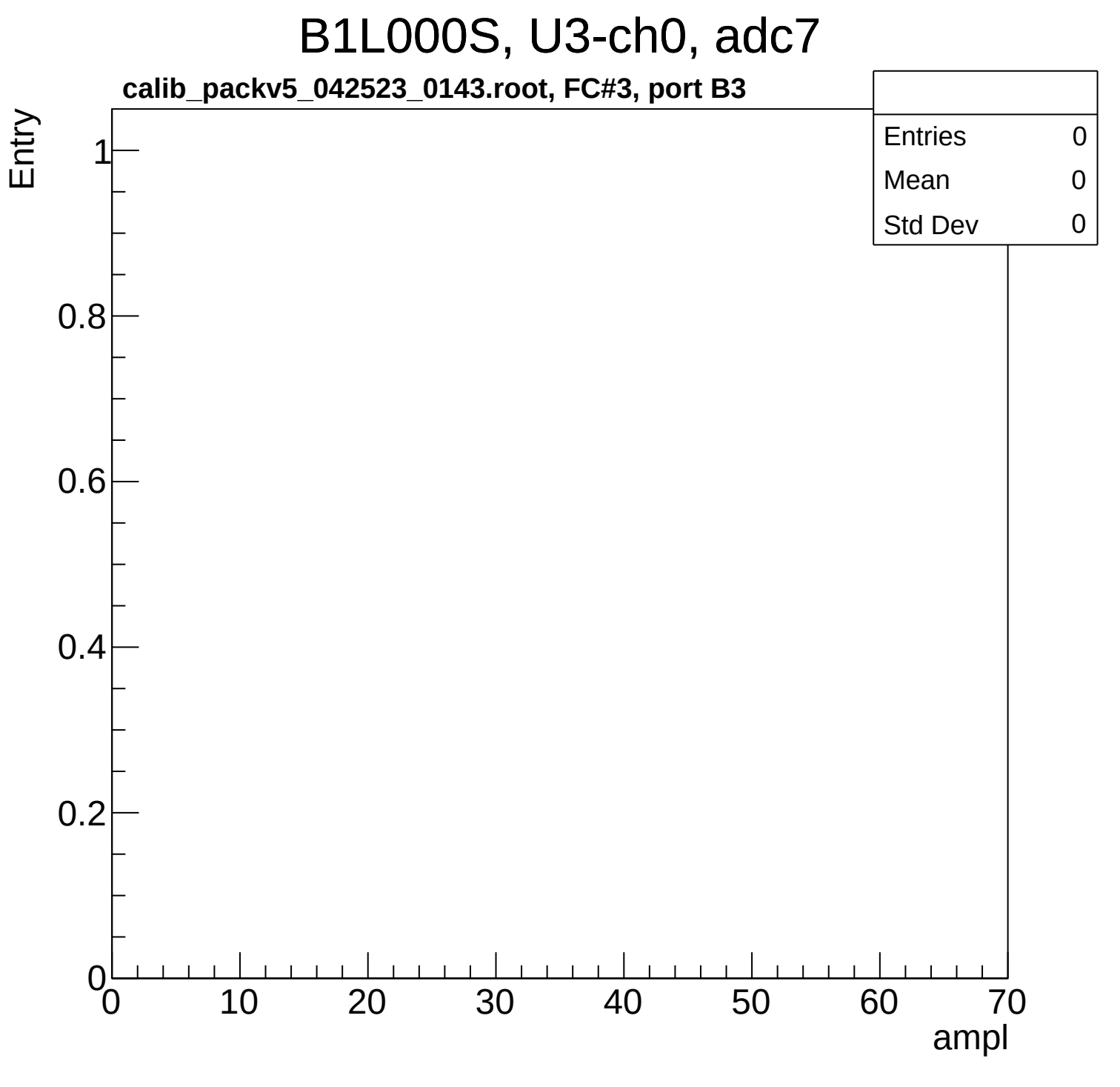

B1L000S, U3-ch0, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 58.92
Std Dev 12.12
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch0, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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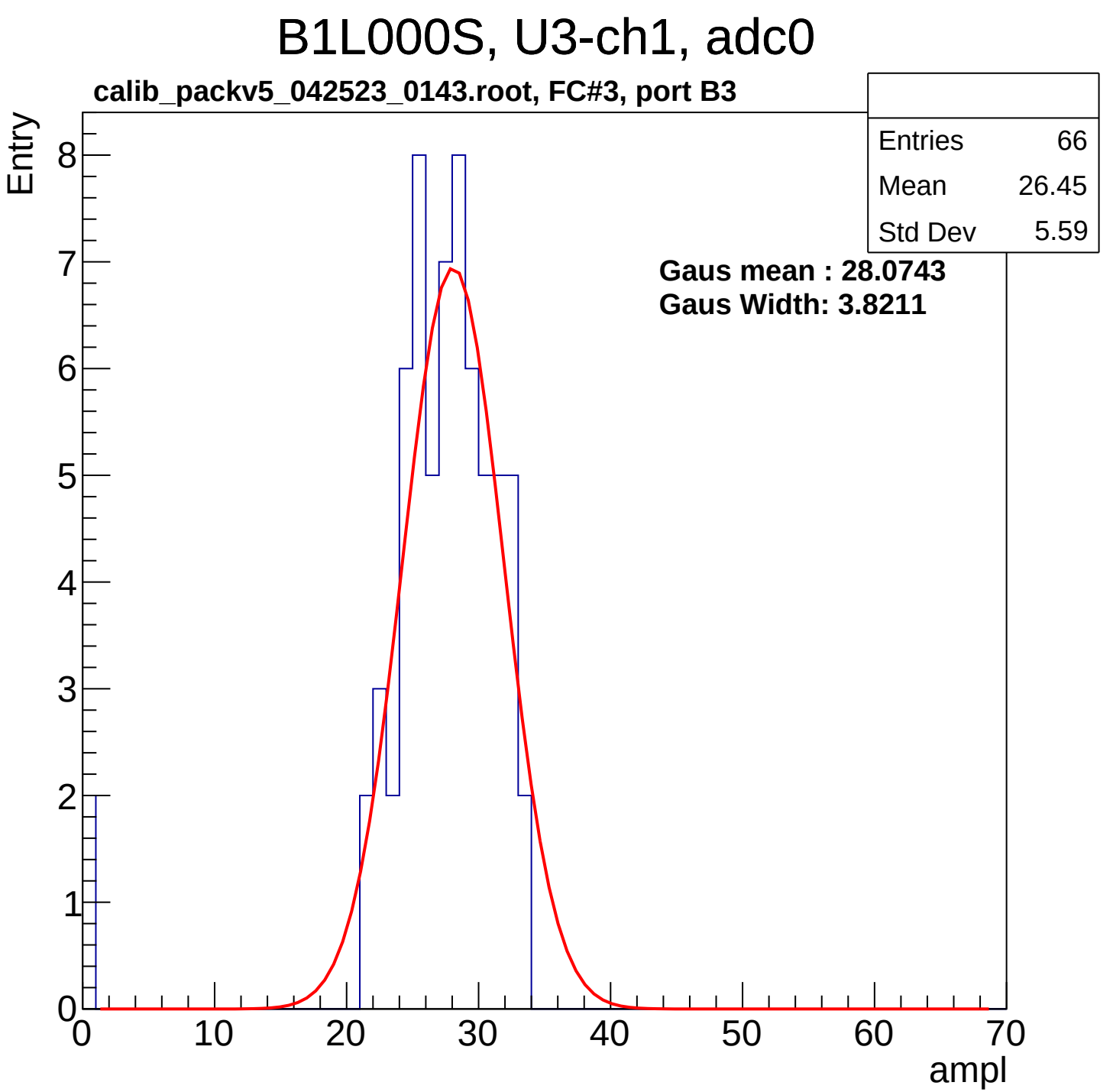
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch1, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 26.45
Std Dev 5.59
Gaus mean : 28.0743
Gaus Width: 3.8211
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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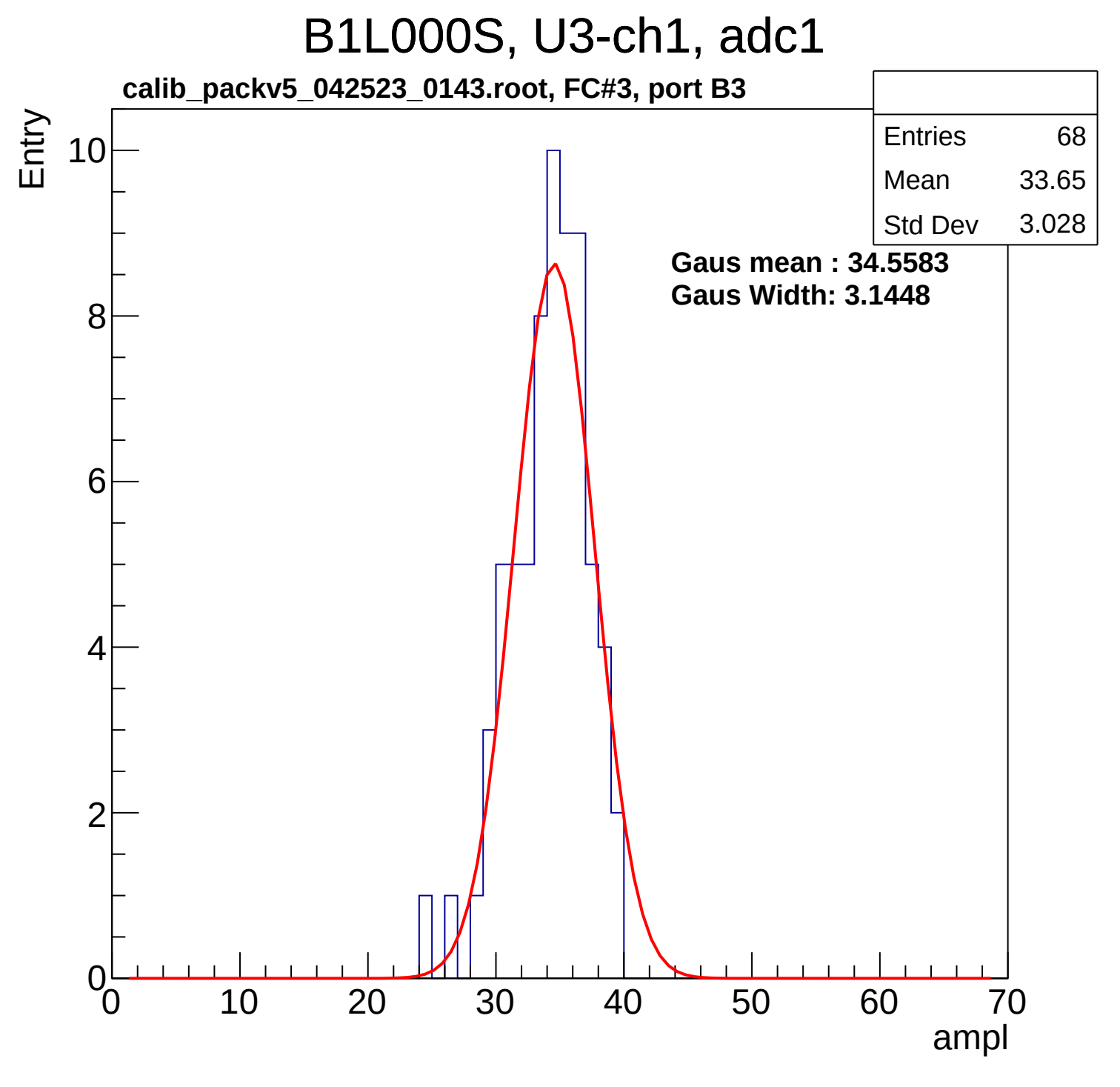
B1L000S, U3-ch1, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 33.65
Std Dev 3.028
Gaus mean : 34.5583
Gaus Width: 3.1448
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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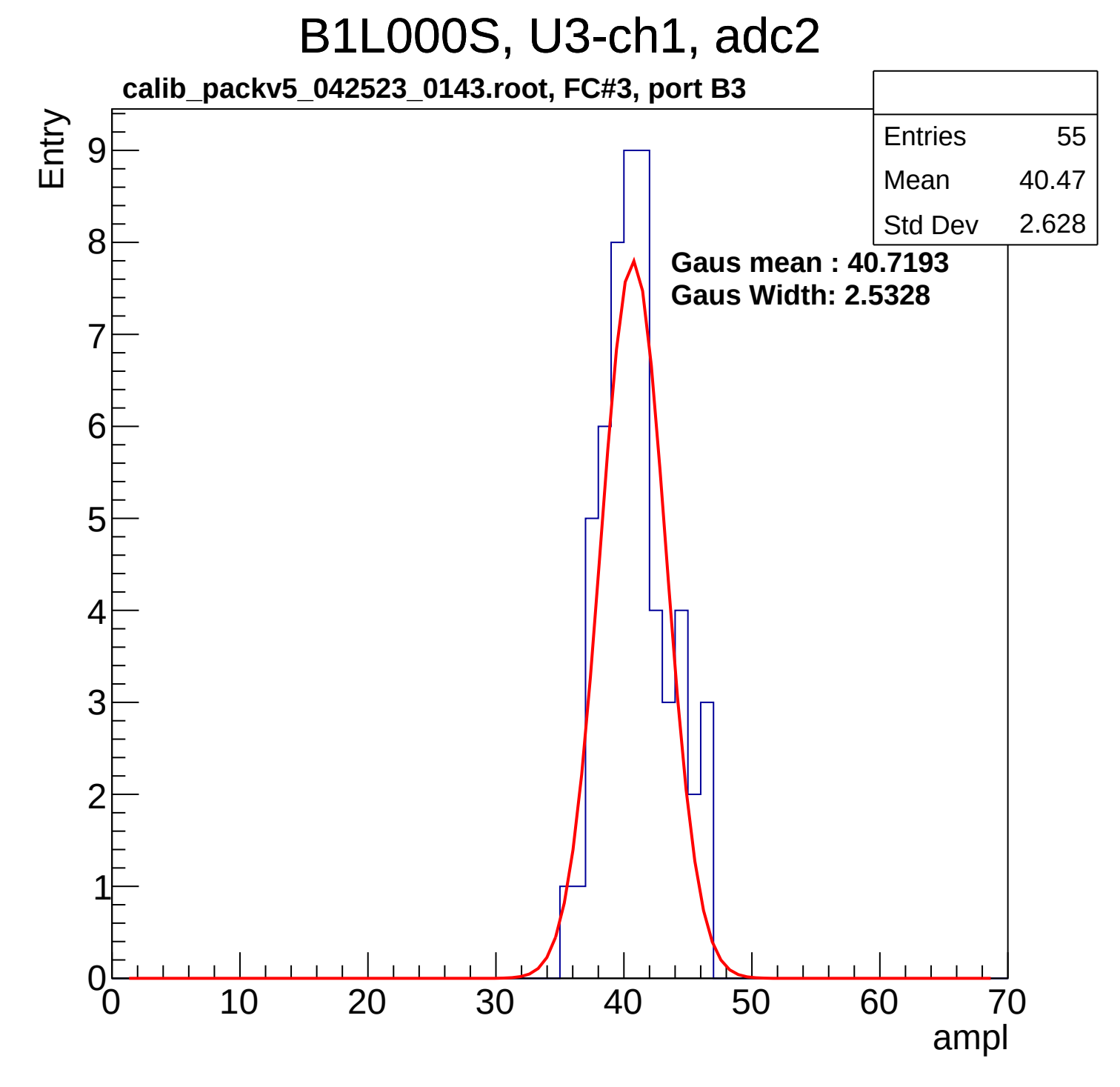
B1L000S, U3-ch1, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 40.47
Std Dev 2.628
Gaus mean : 40.7193
Gaus Width: 2.5328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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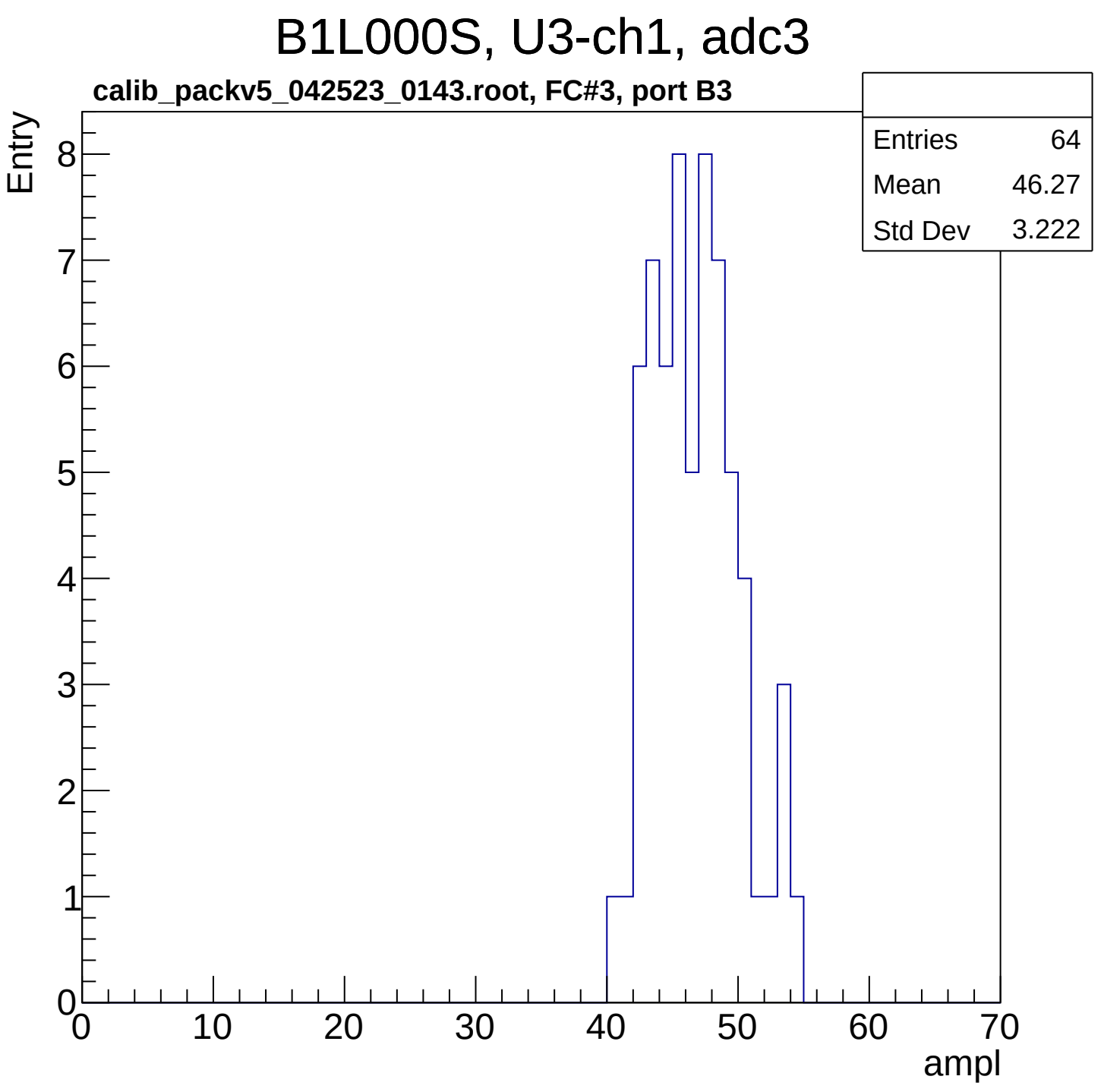
B1L000S, U3-ch1, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 46.27
Std Dev 3.222
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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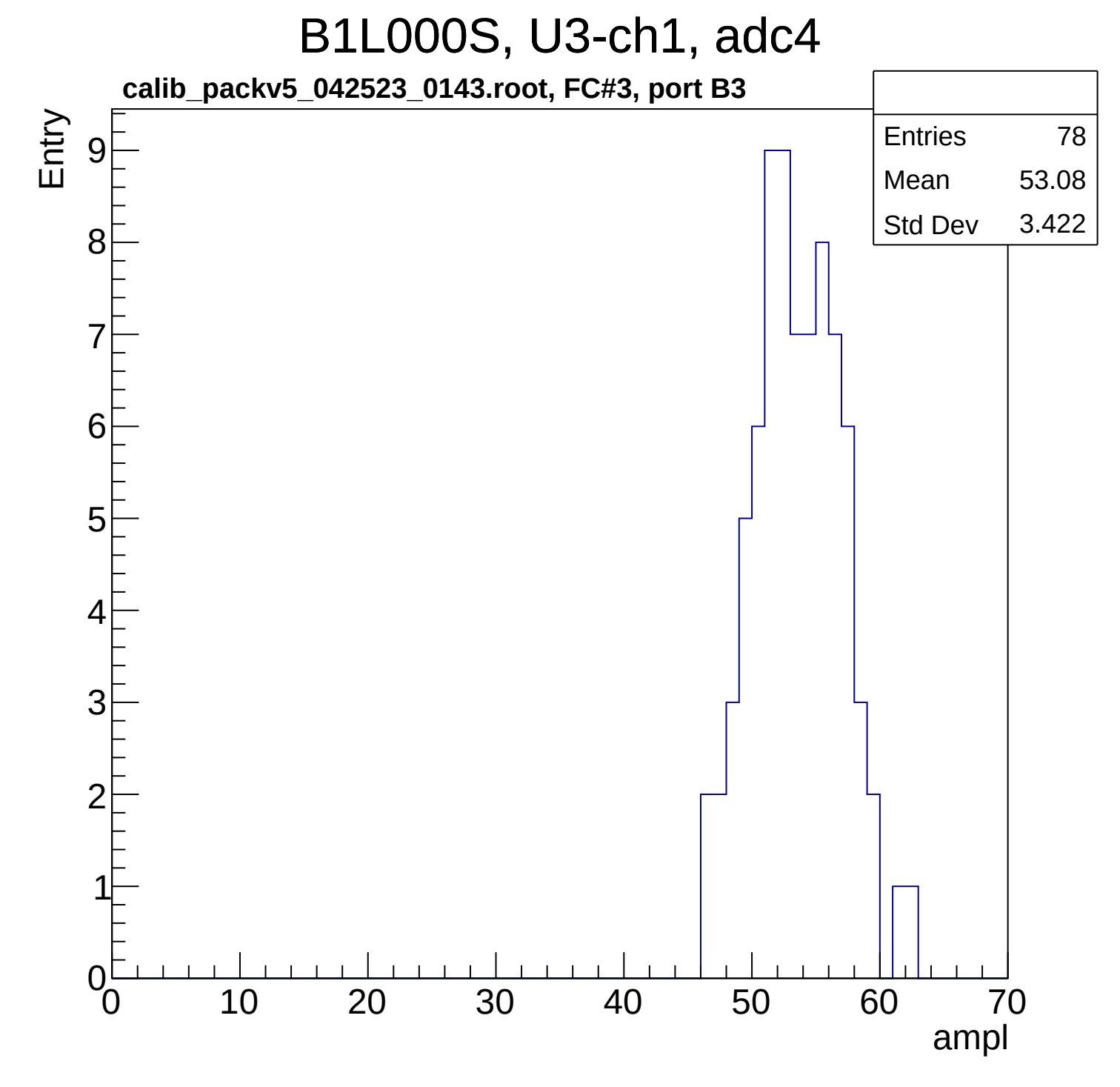
B1L000S, U3-ch1, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 53.08
Std Dev 3.422
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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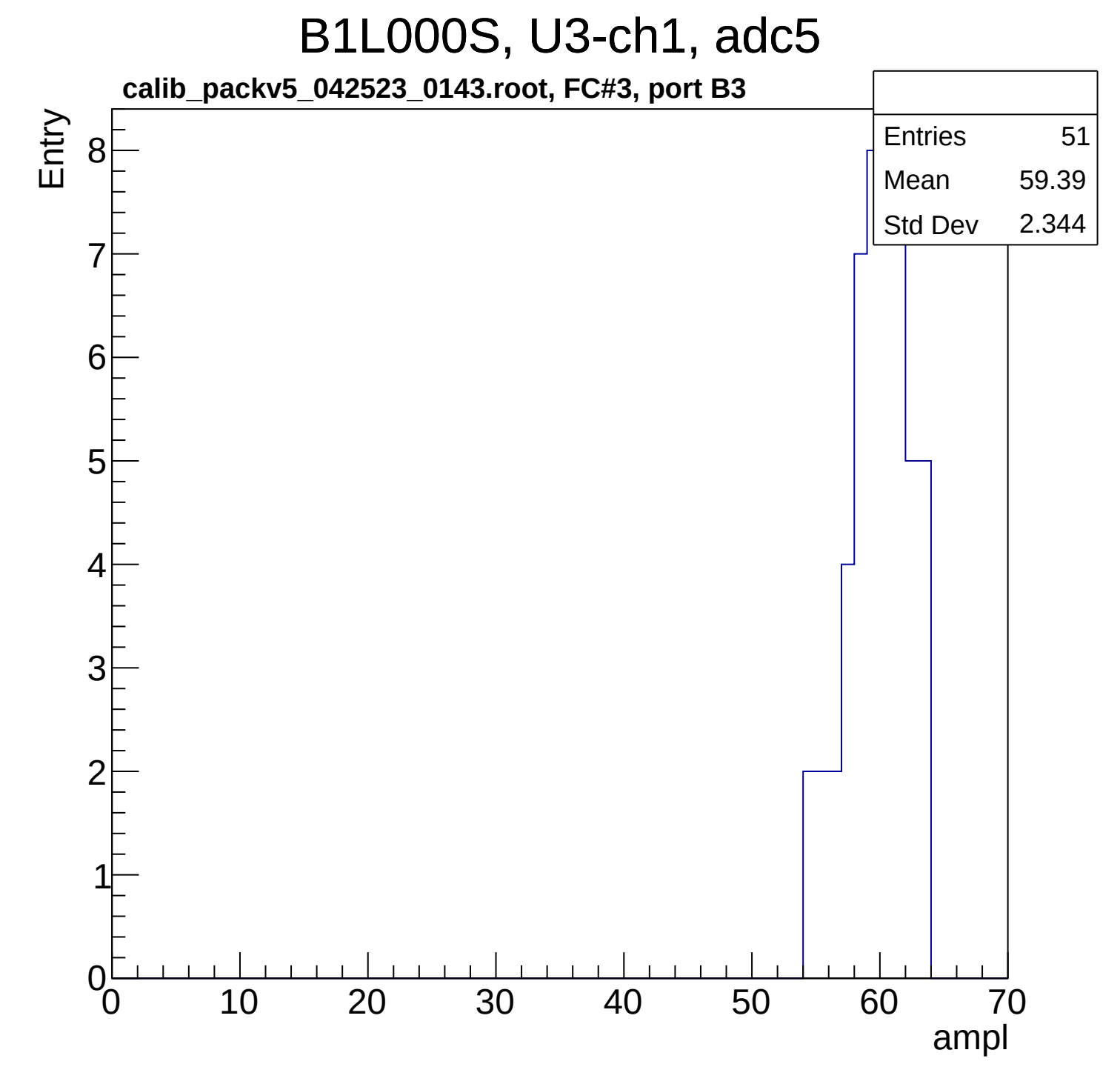
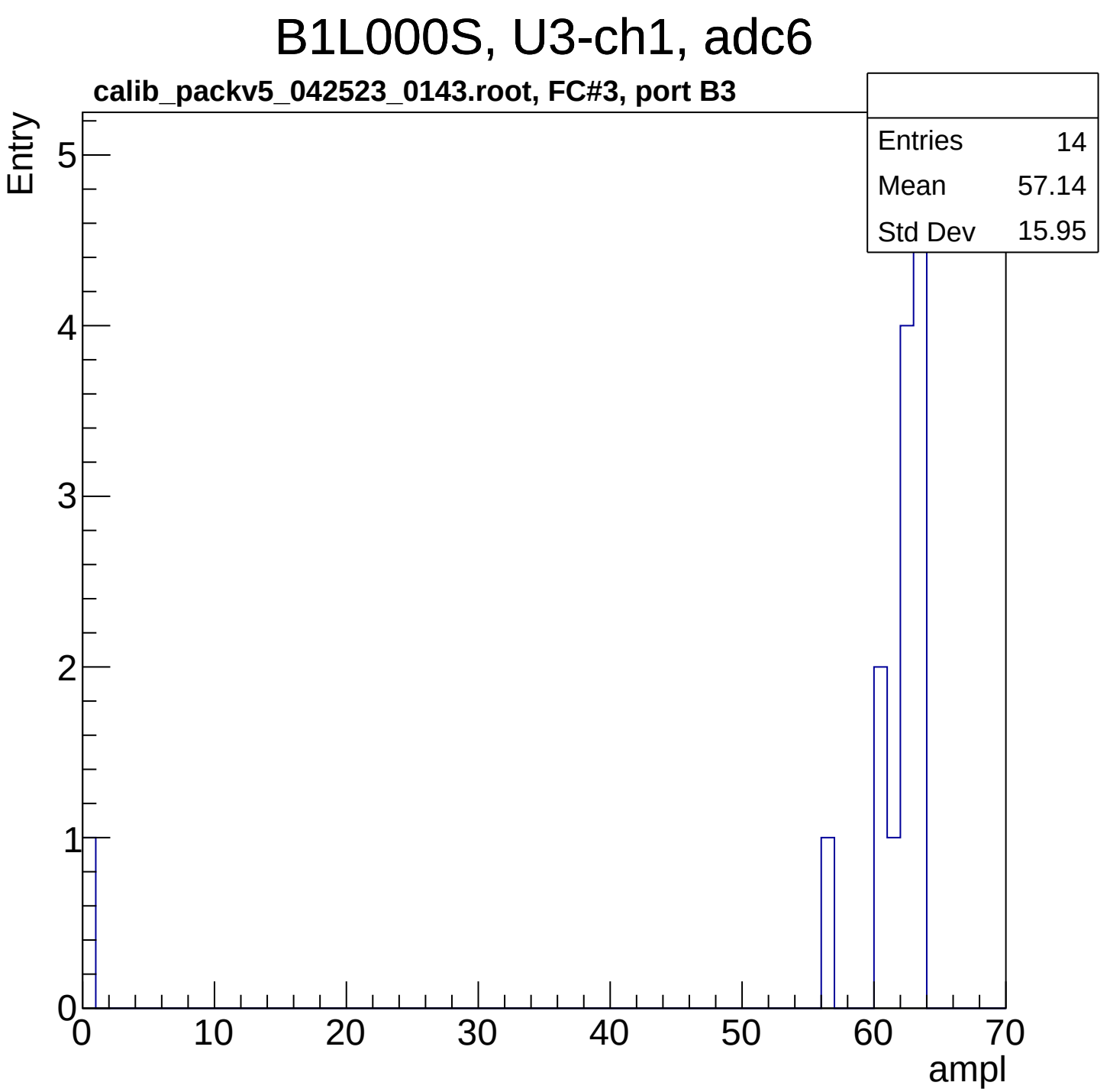
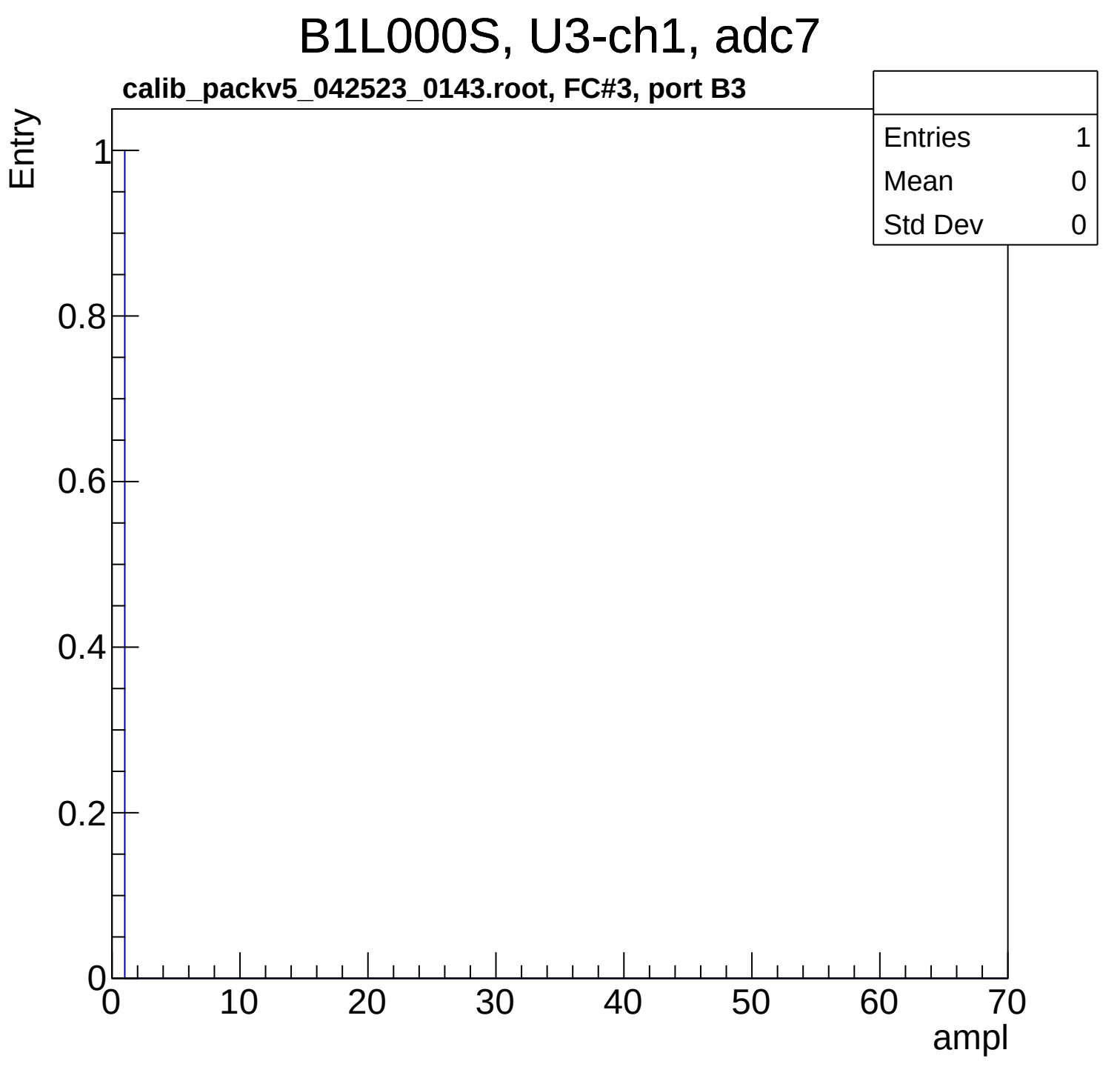
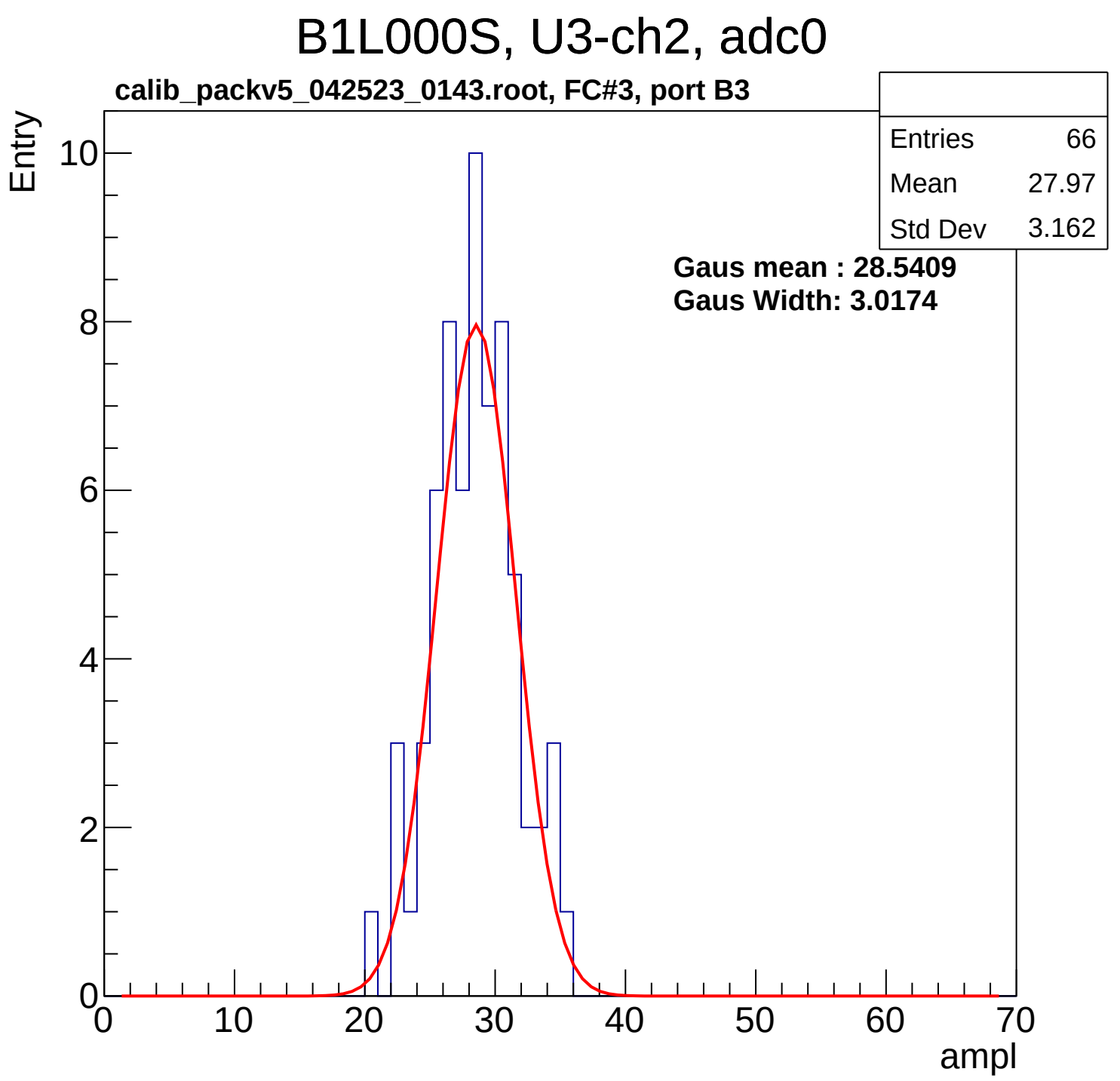

B1L000S, U3-ch1, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 59.39
Std Dev 2.344
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch1, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.14
Std Dev 15.95
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch1, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch2, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 27.97
Std Dev 3.162
Gaus mean : 28.5409
Gaus Width: 3.0174
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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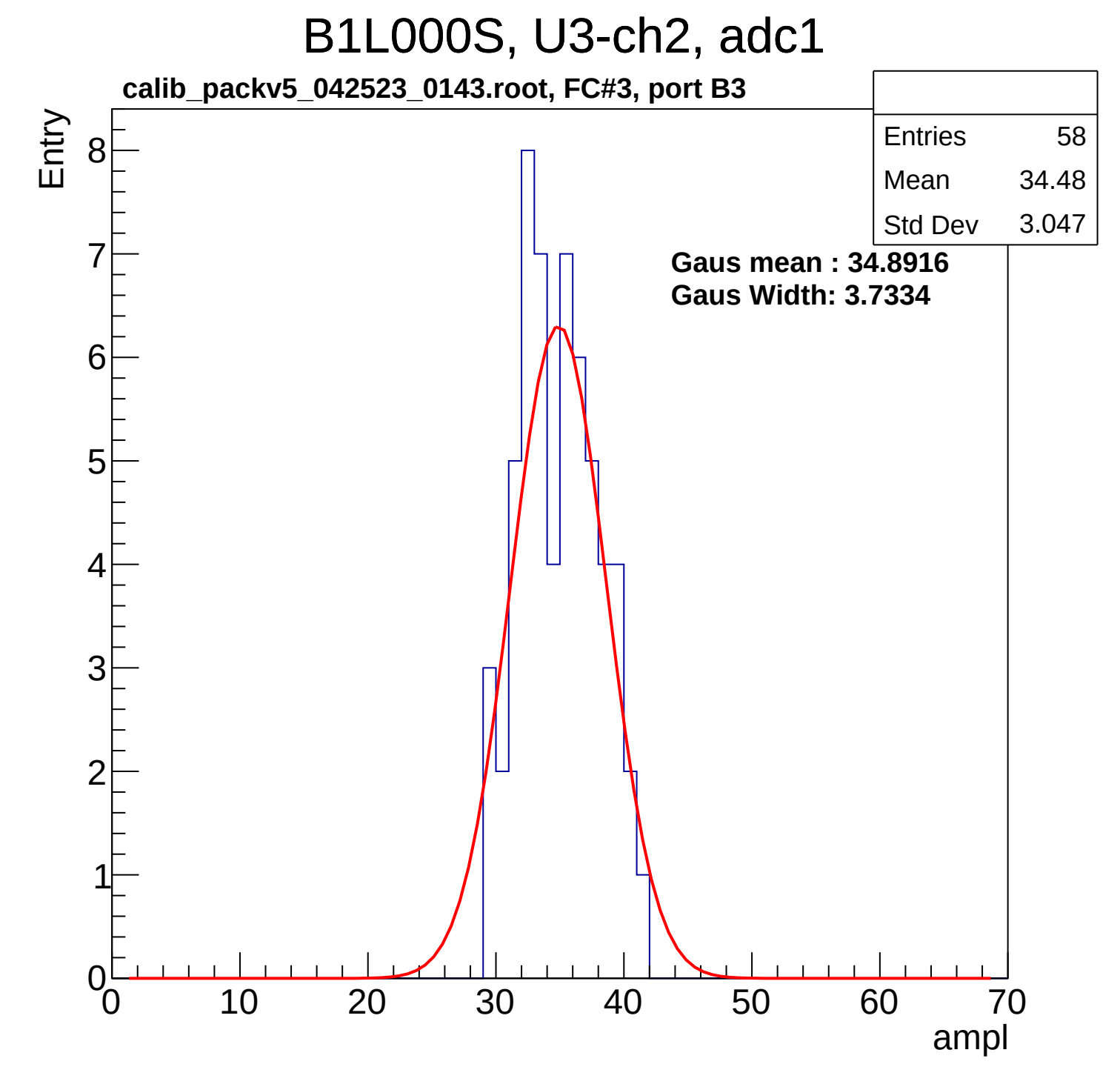
B1L000S, U3-ch2, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 34.48
Std Dev 3.047
Gaus mean : 34.8916
Gaus Width: 3.7334
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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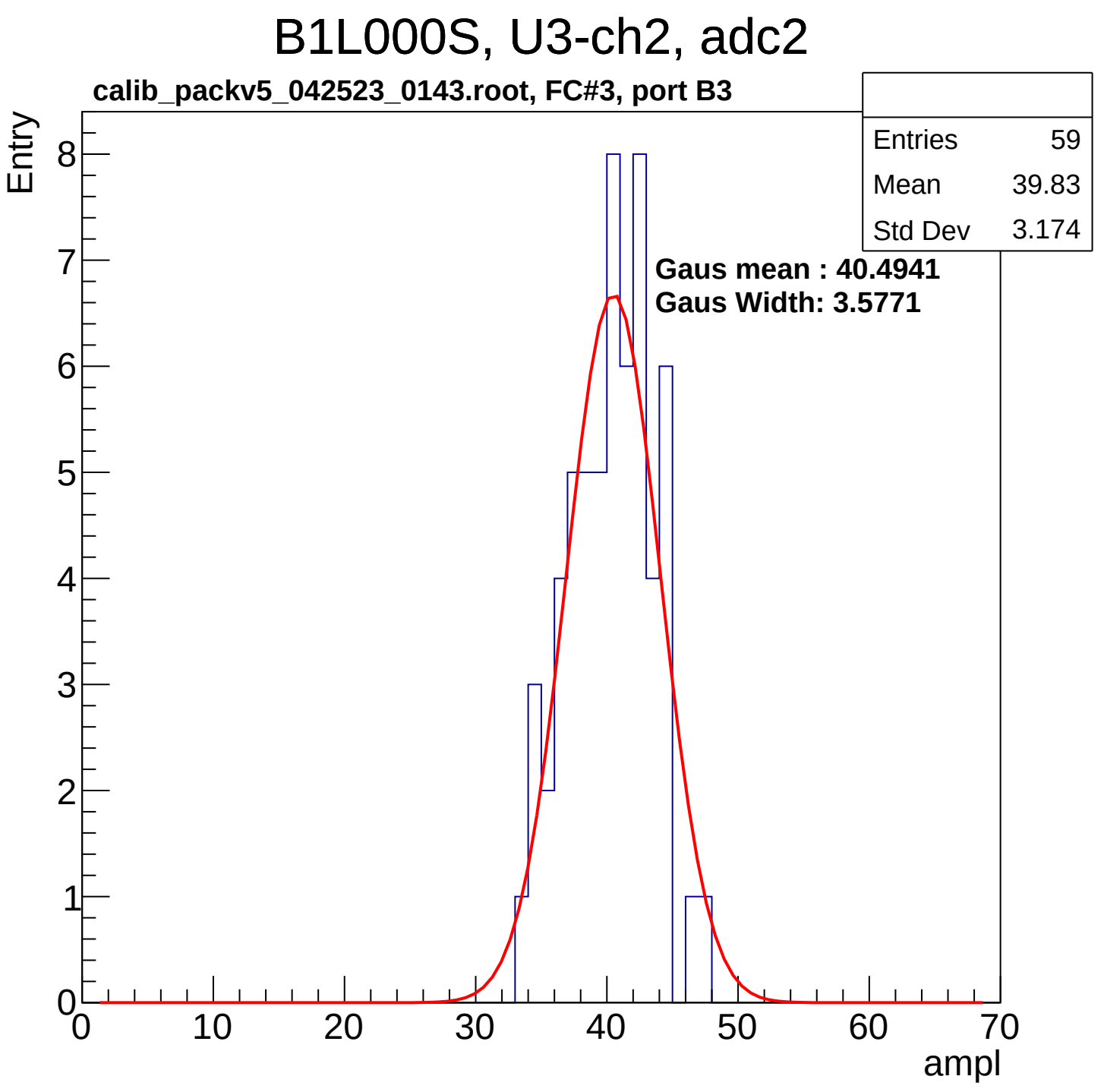
B1L000S, U3-ch2, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 39.83
Std Dev 3.174
Gaus mean : 40.4941
Gaus Width: 3.5771
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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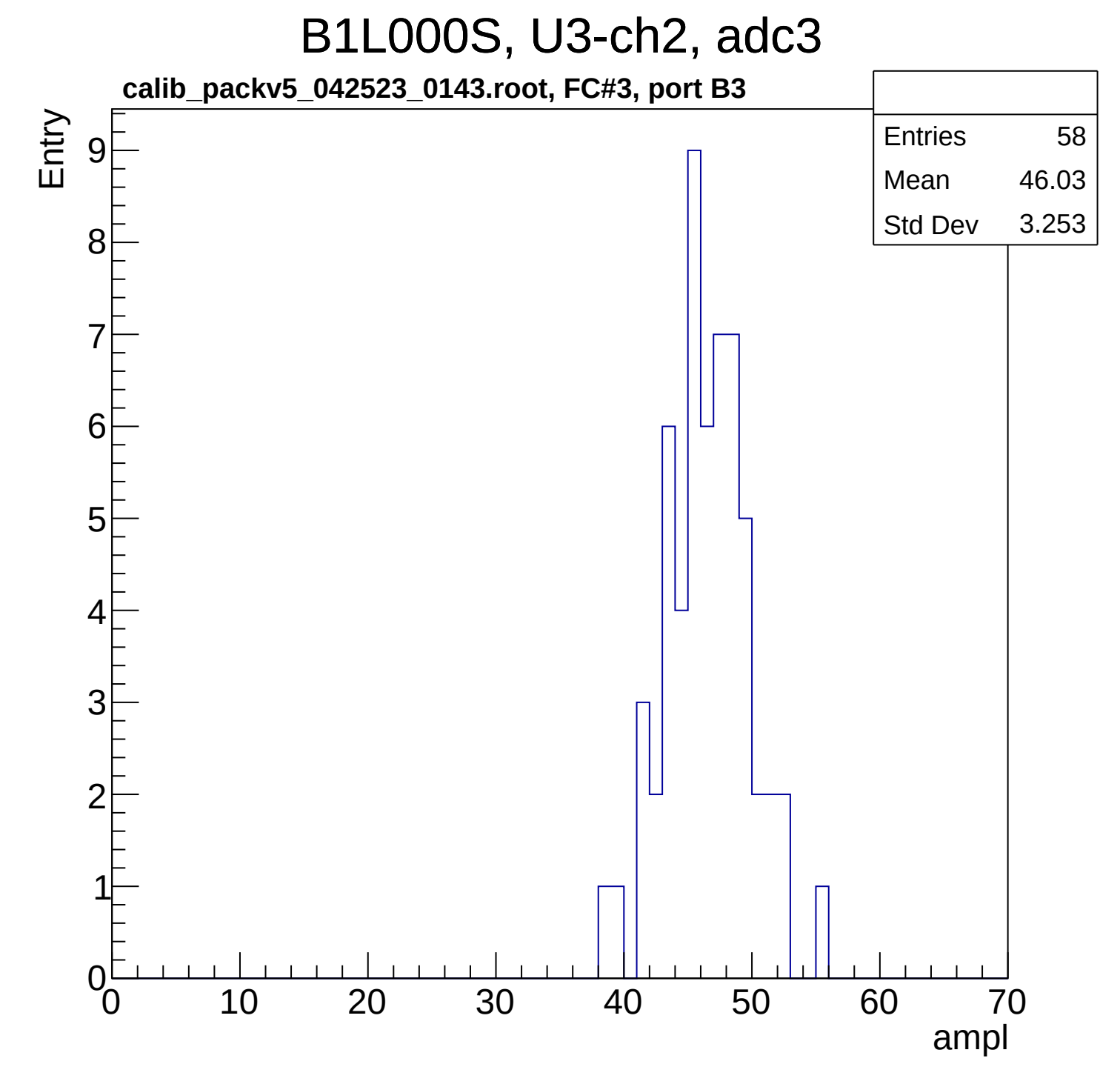
B1L000S, U3-ch2, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 46.03
Std Dev 3.253
Entry
ampl
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3
4
5
6
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9
0
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60
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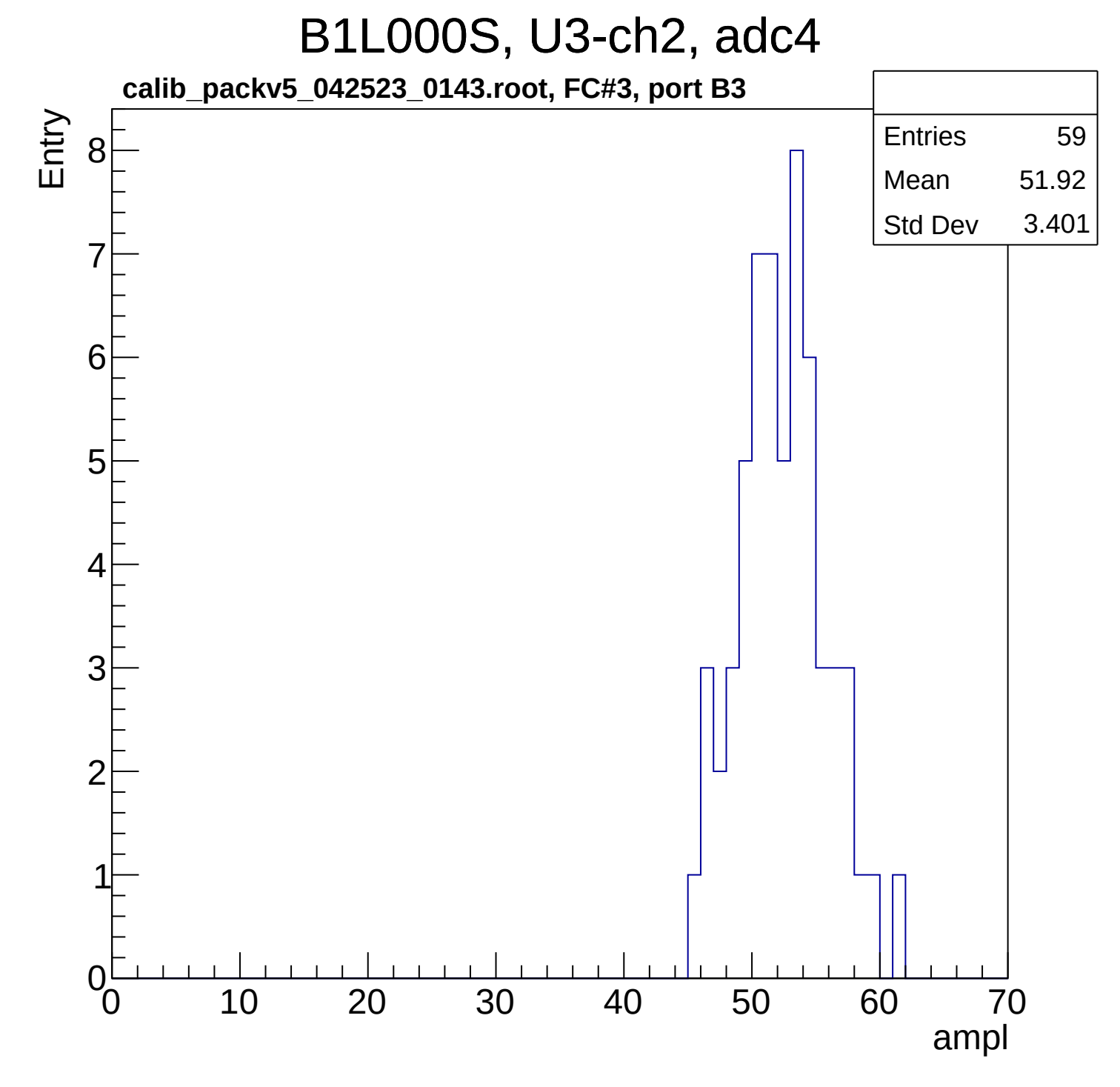
B1L000S, U3-ch2, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 51.92
Std Dev 3.401
Entry
ampl
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2
3
4
5
6
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0
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60
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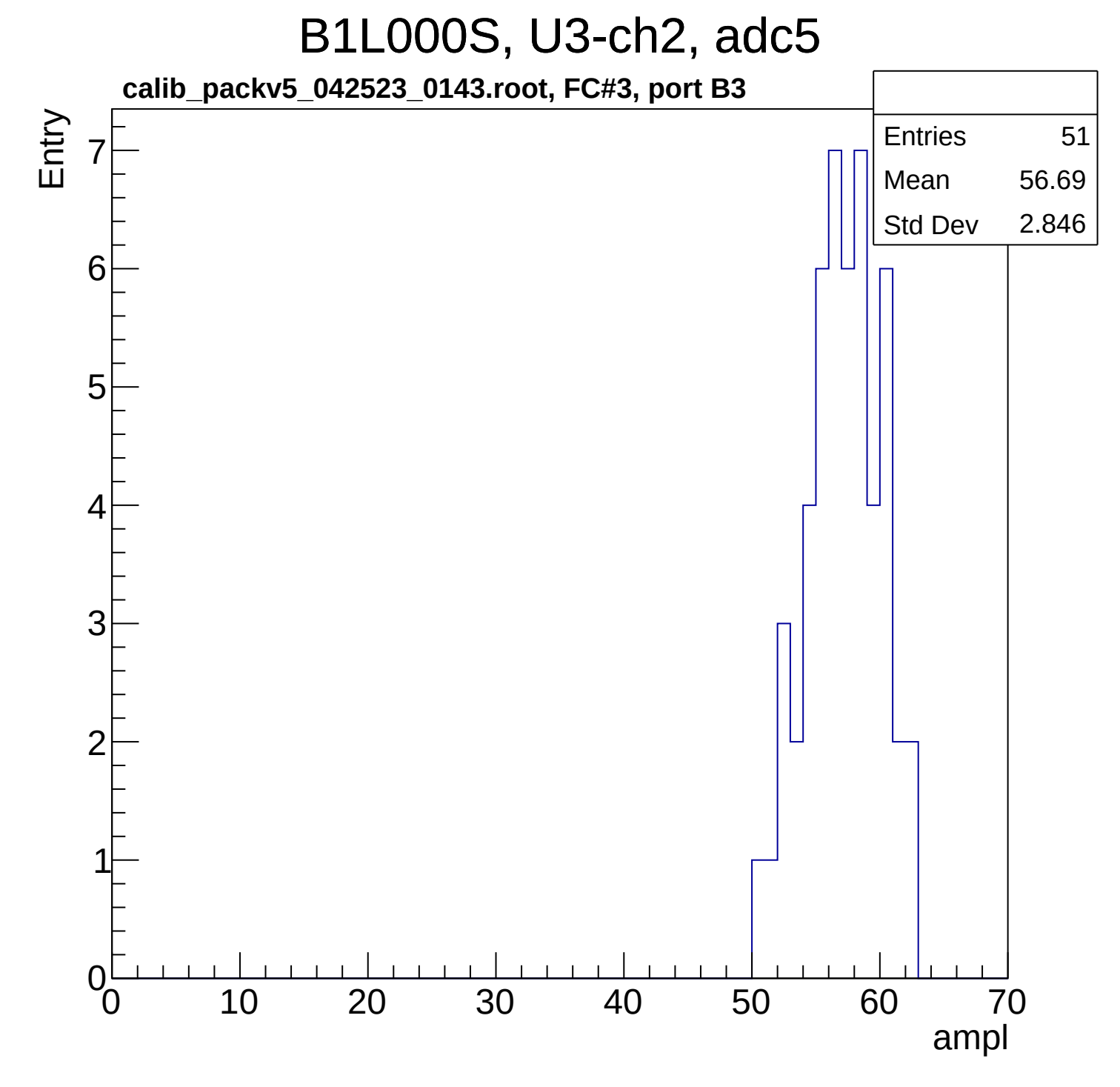
B1L000S, U3-ch2, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 56.69
Std Dev 2.846
Entry
ampl
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6
7
0
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20
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60
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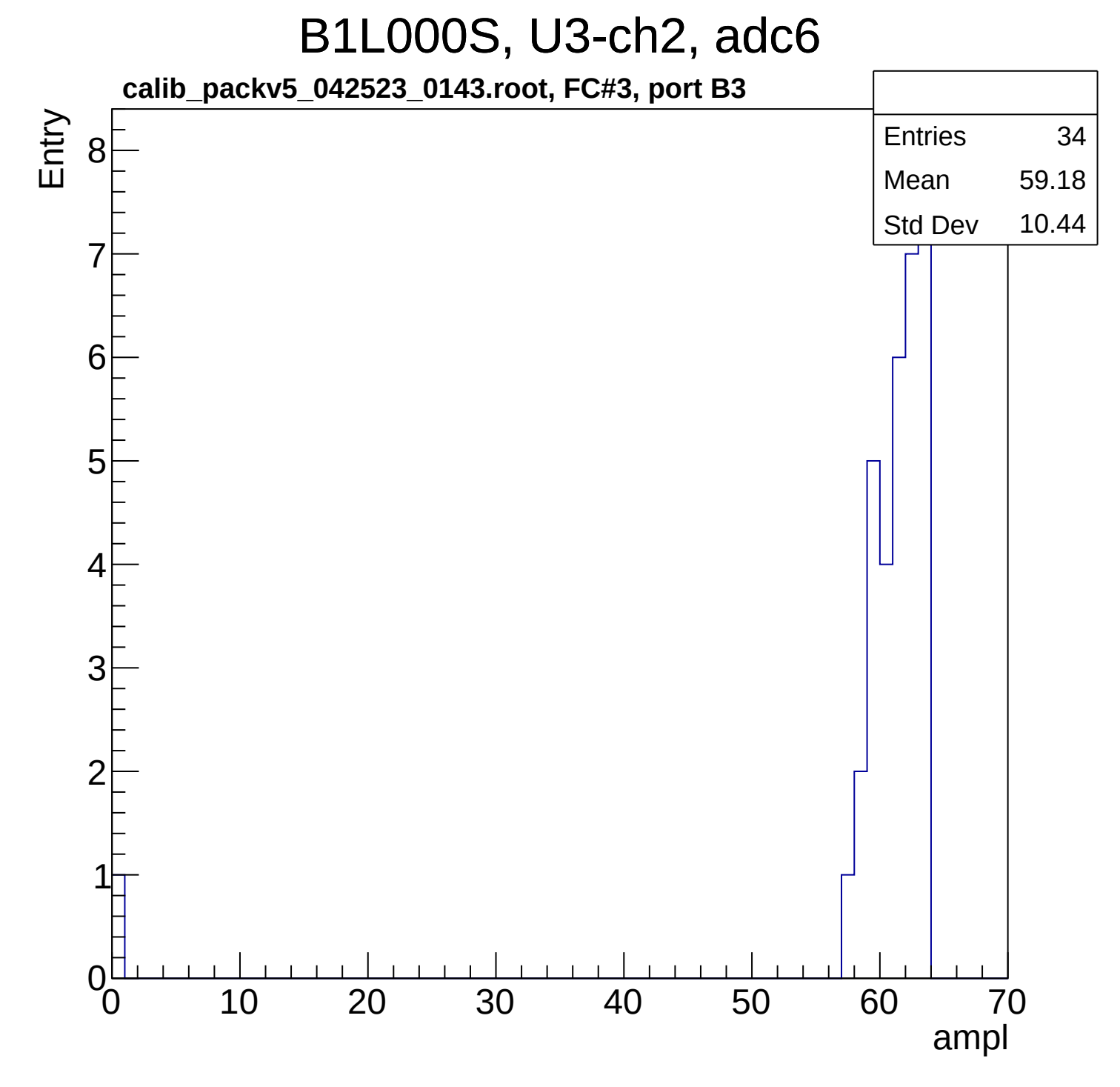
B1L000S, U3-ch2, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 34
Mean 59.18
Std Dev 10.44
Entry
ampl
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3
4
5
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0
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60
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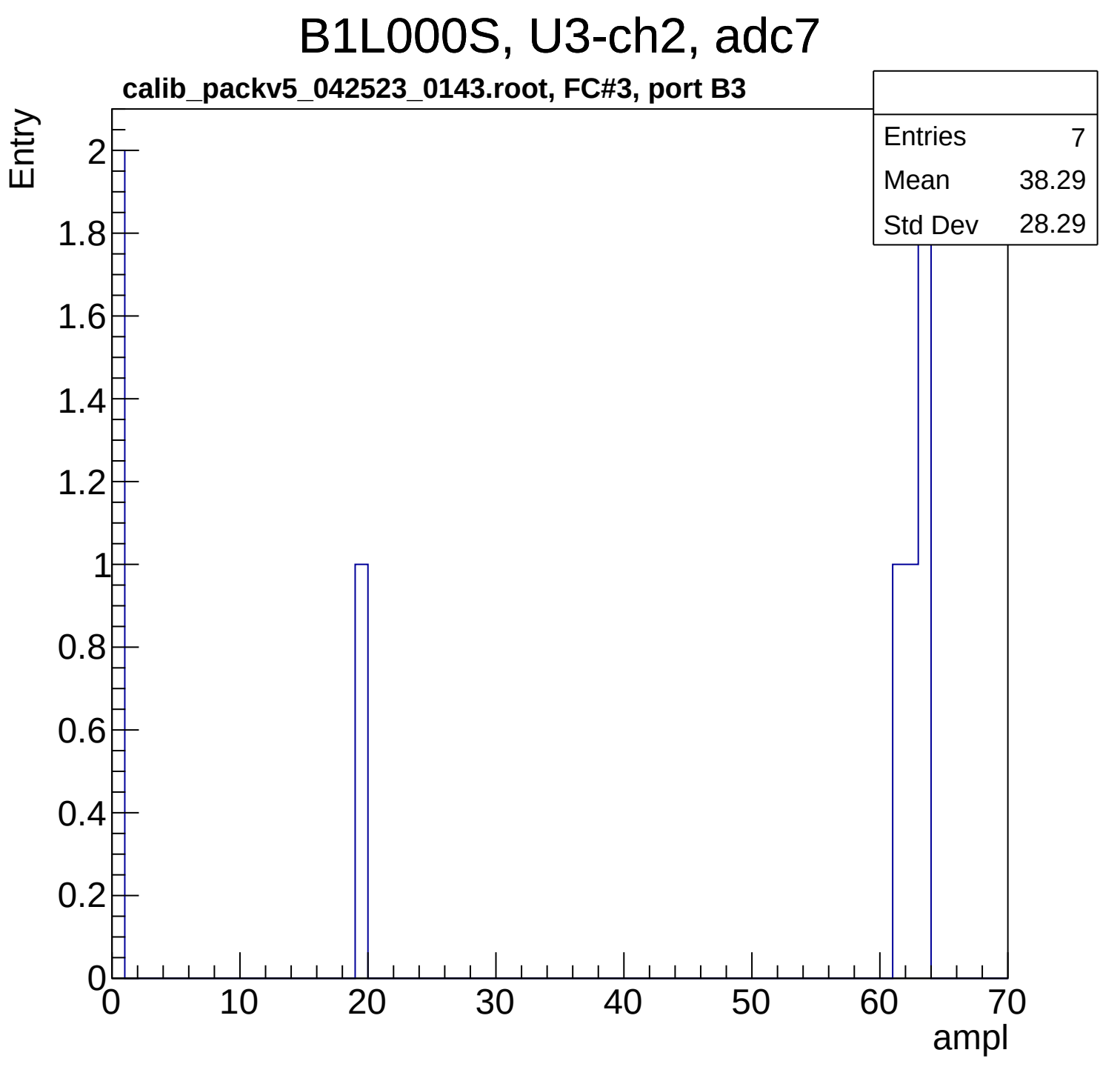
B1L000S, U3-ch2, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 38.29
Std Dev 28.29
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
30
40
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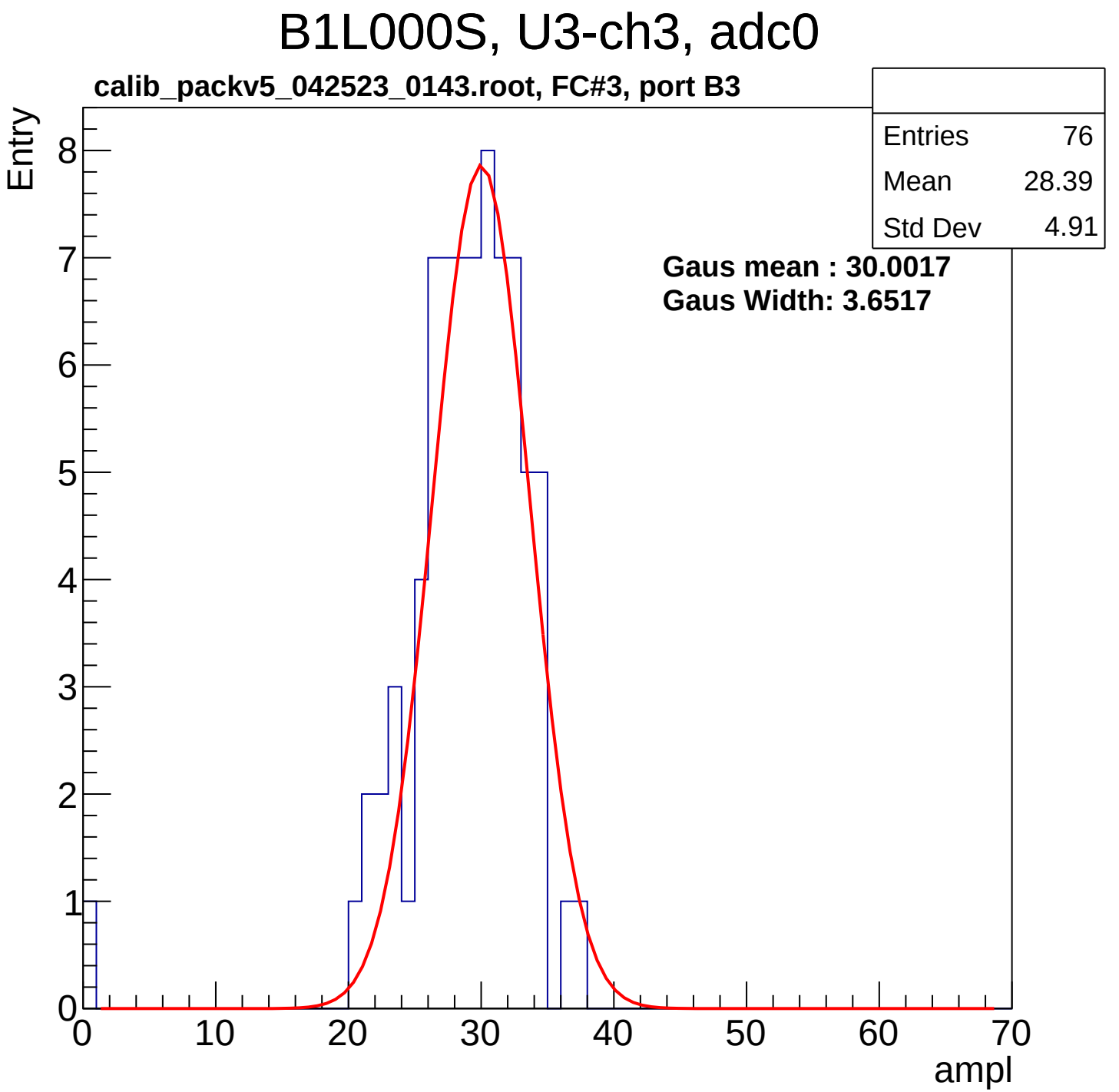
60
70


B1L000S, U3-ch3, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 28.39
Std Dev 4.91
Gaus mean : 30.0017
Gaus Width: 3.6517
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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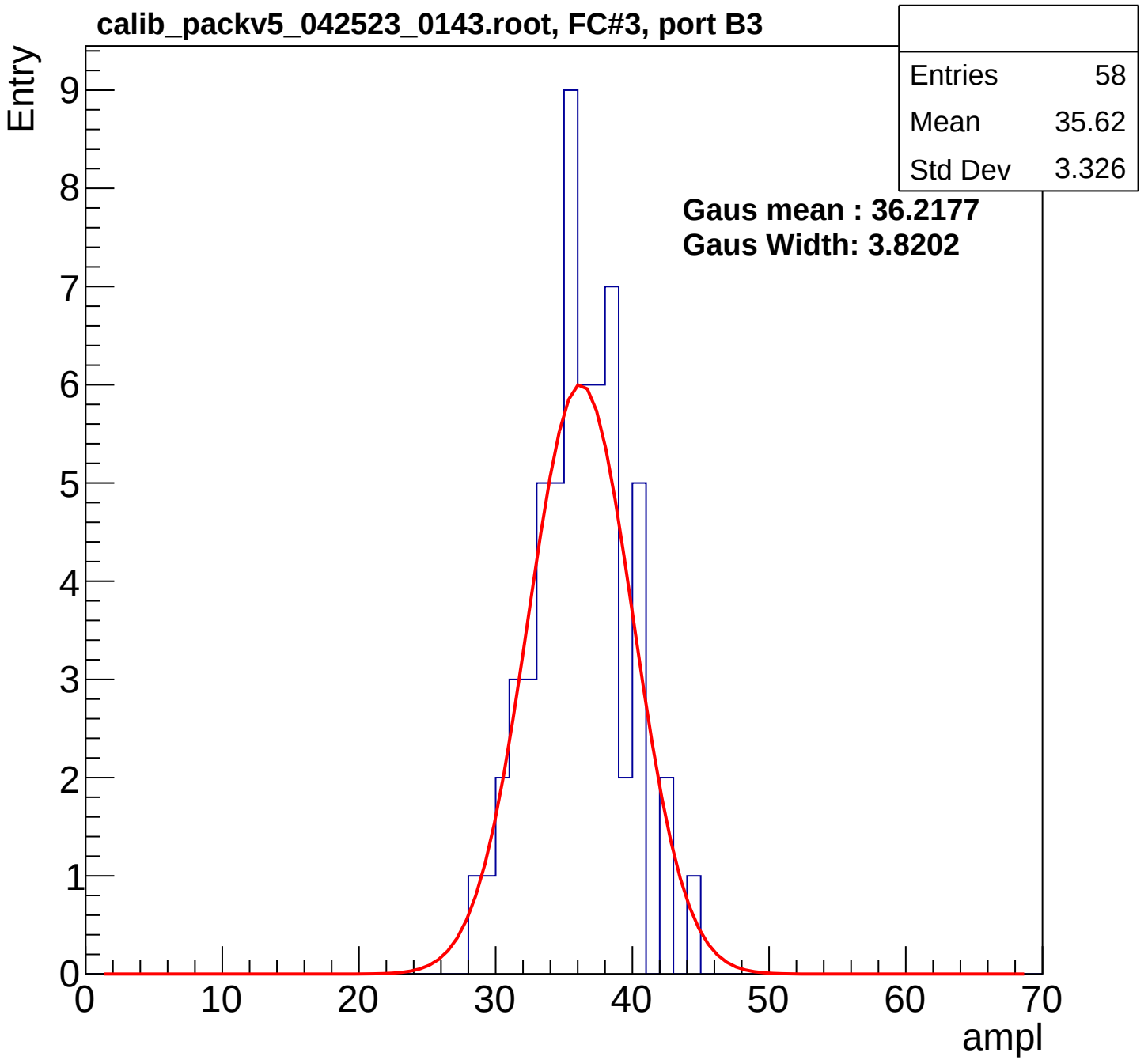
B1L000S, U3-ch3, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 35.62
Std Dev 3.326
Gaus mean : 36.2177
Gaus Width: 3.8202
Entry
ampl
0
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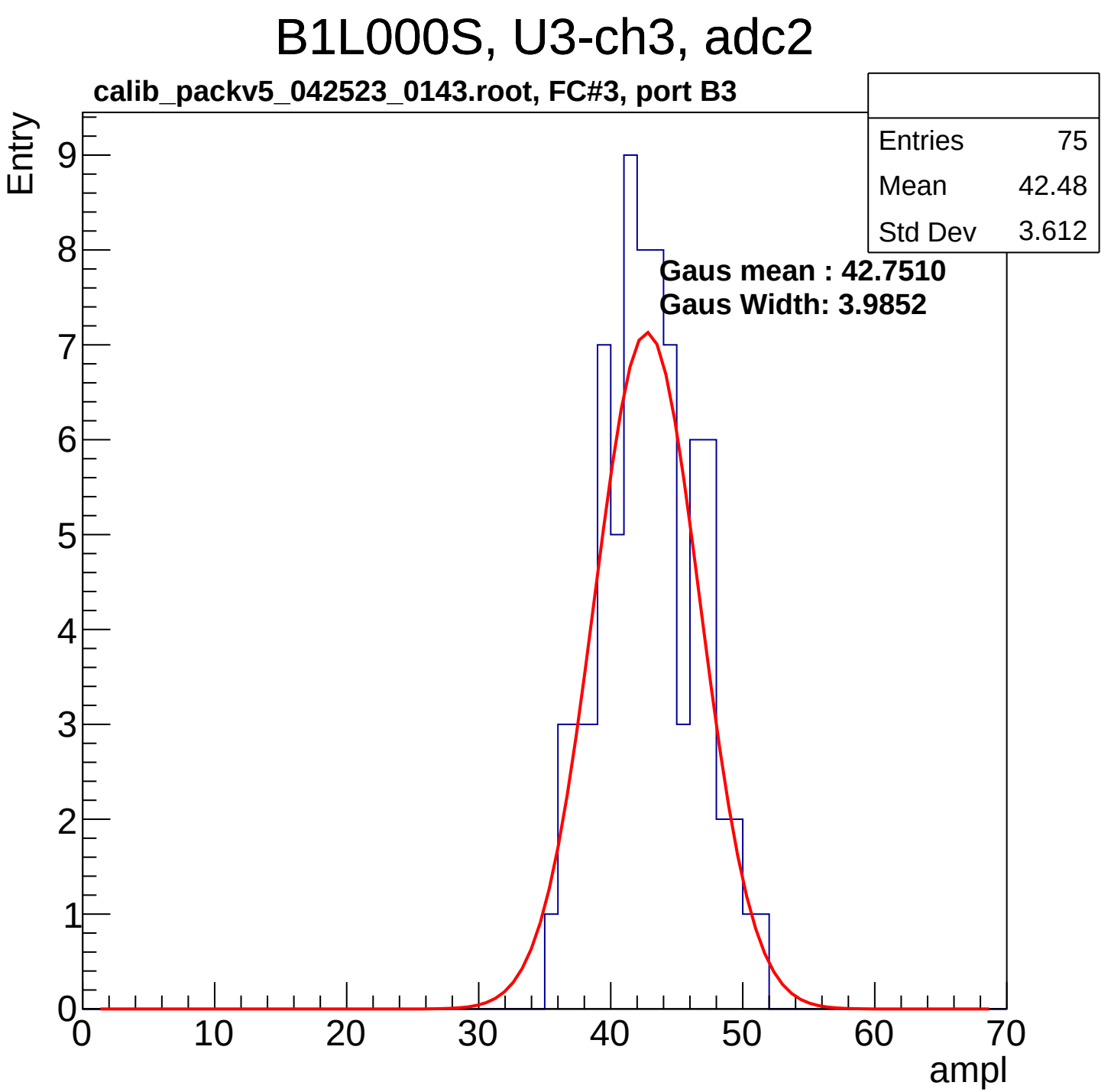
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch3, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 42.48
Std Dev 3.612
Gaus mean : 42.7510
Gaus Width: 3.9852
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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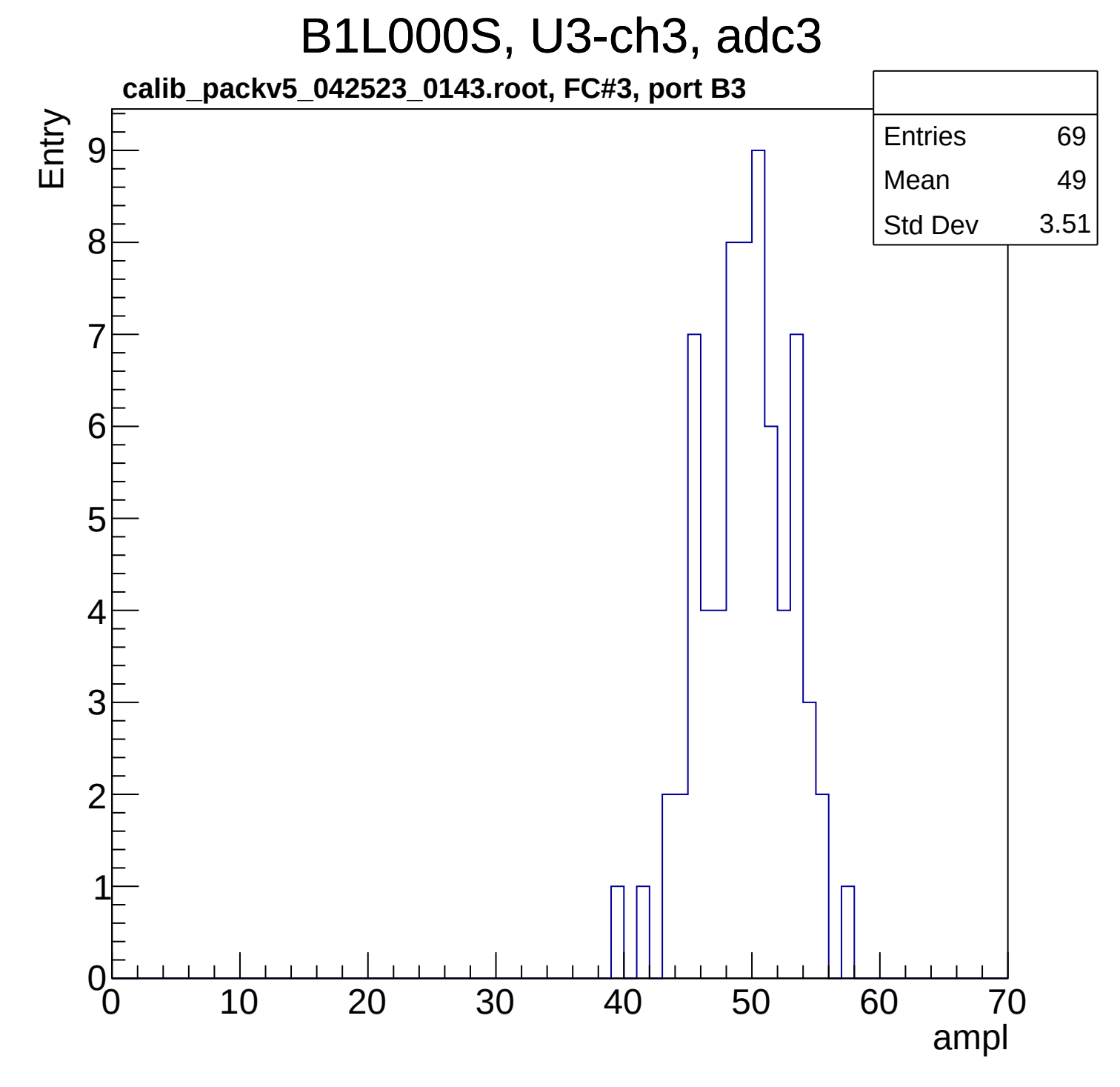
B1L000S, U3-ch3, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 49
Std Dev 3.51
Entry
ampl
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2
3
4
5
6
7
8
9
0
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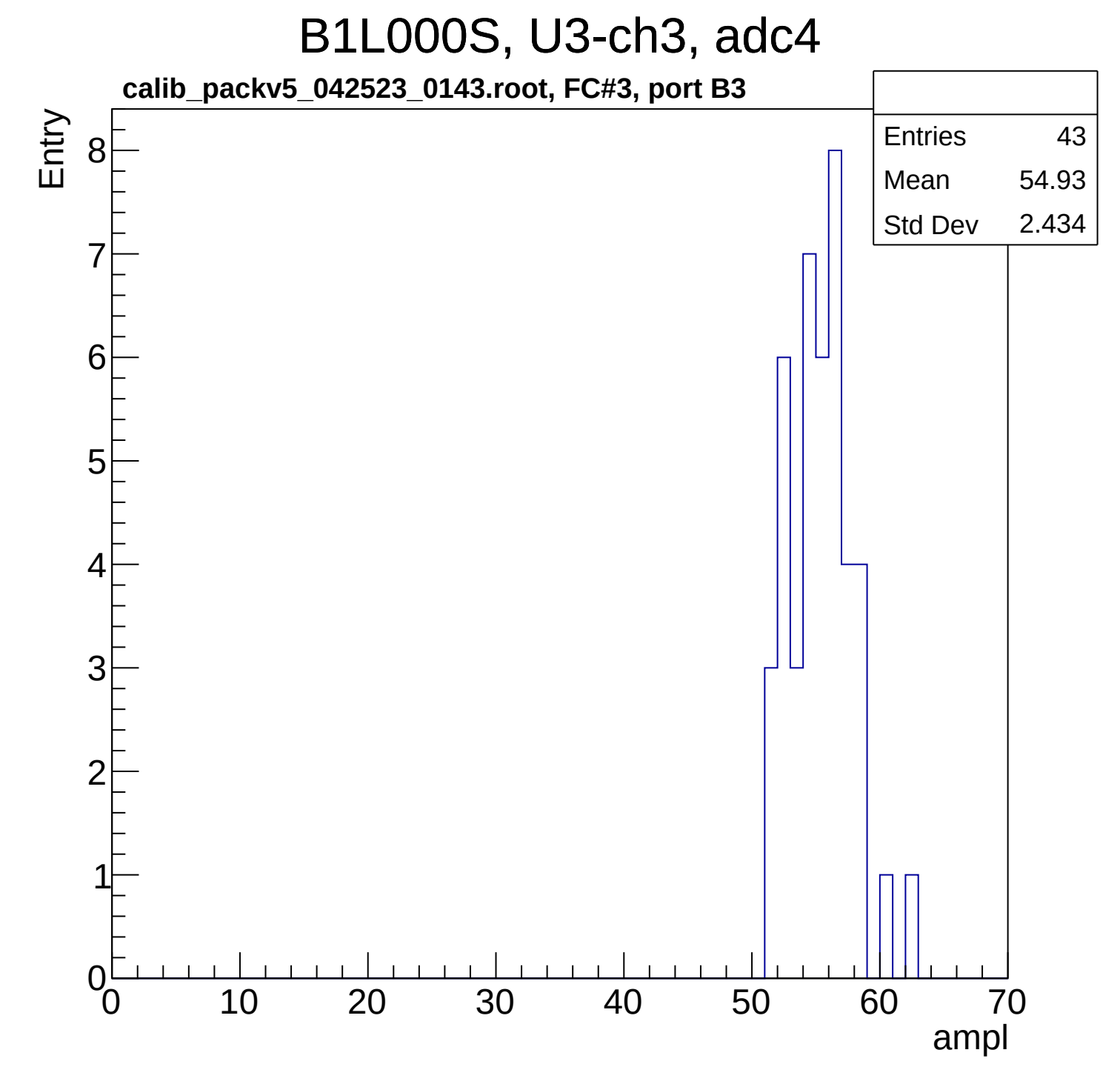
40
50
60
70


B1L000S, U3-ch3, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 54.93
Std Dev 2.434
Entry
ampl
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2
3
4
5
6
7
8
0
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30
40
50
60
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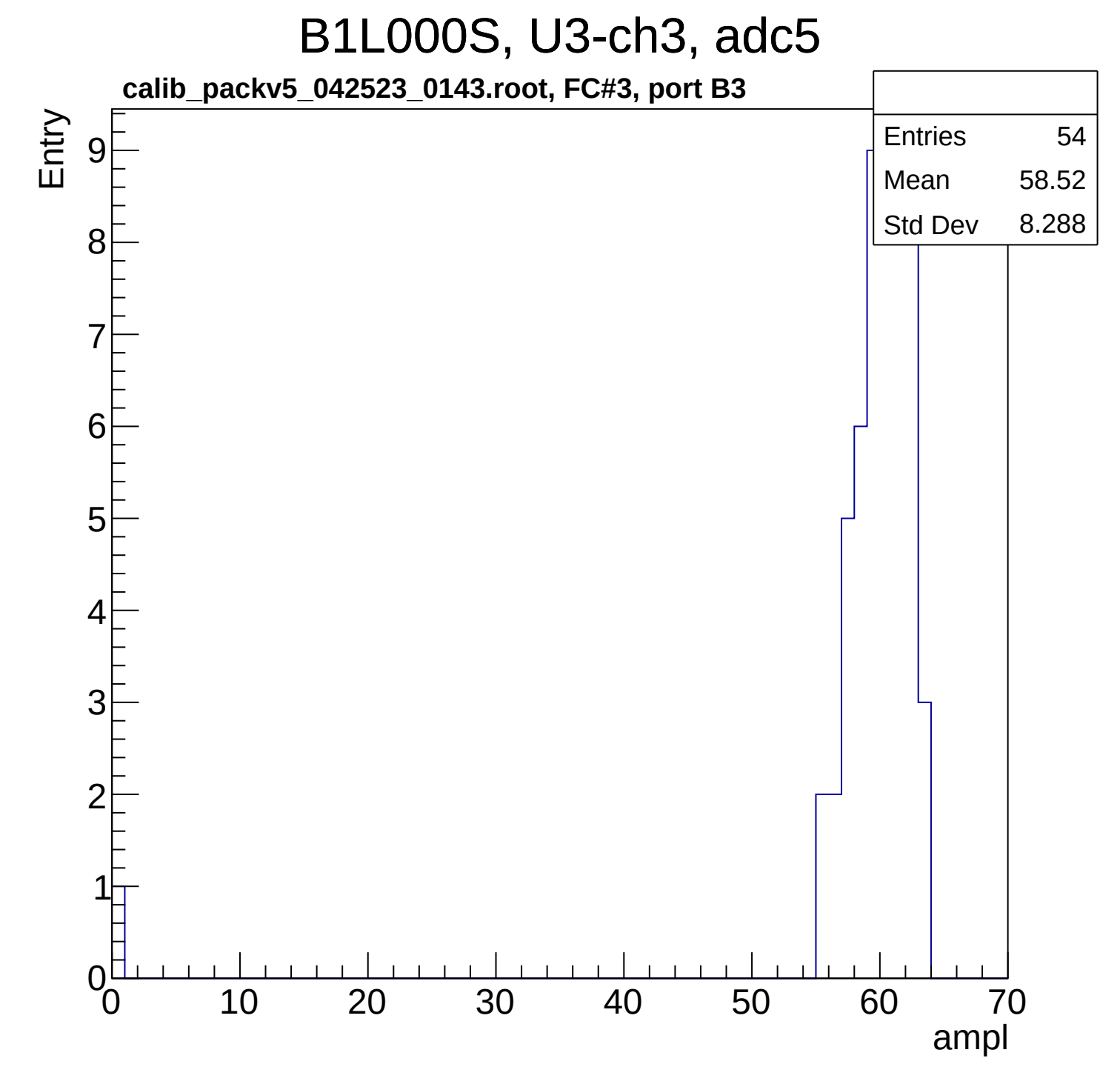
B1L000S, U3-ch3, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 58.52
Std Dev 8.288
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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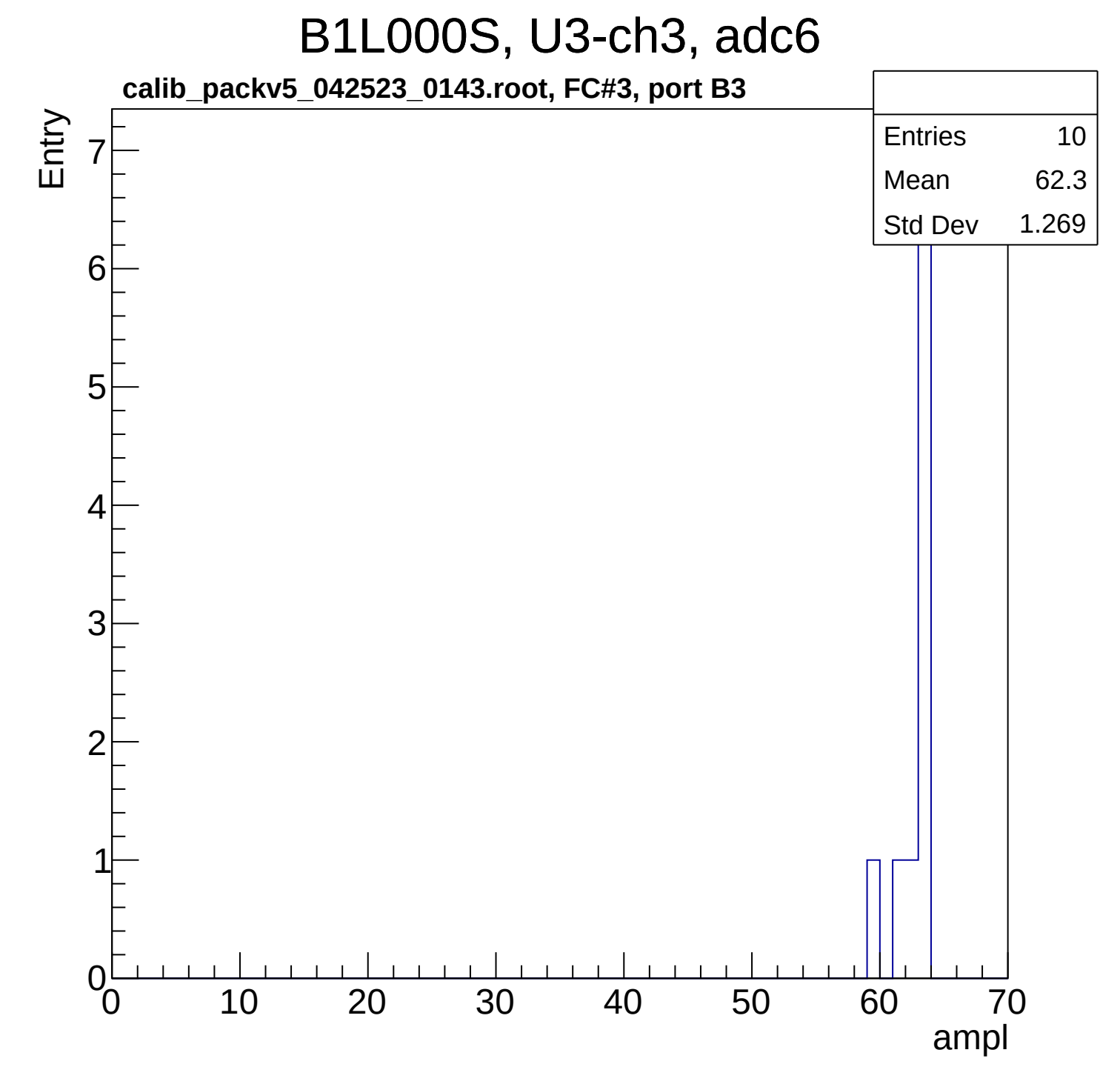
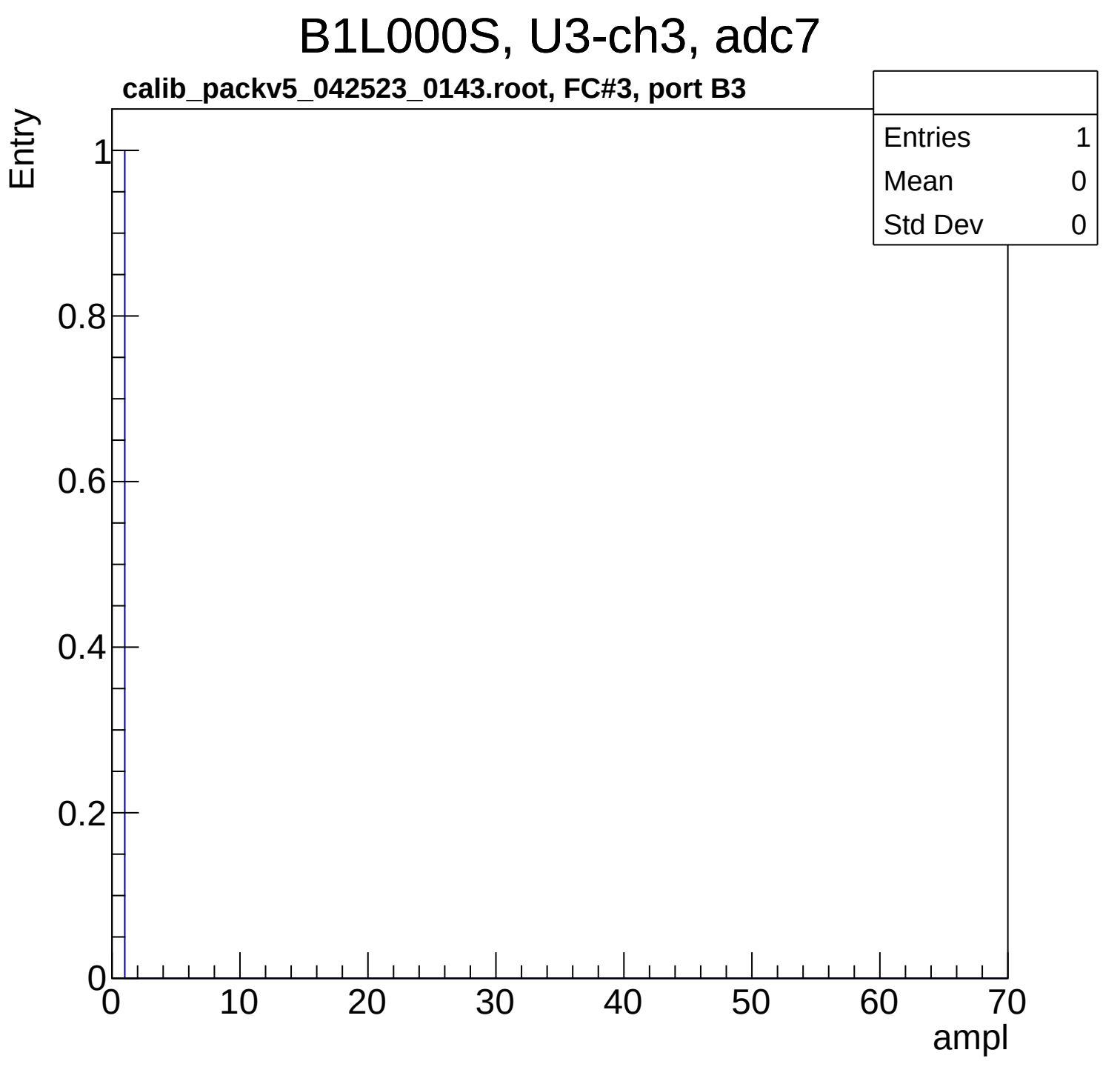

B1L000S, U3-ch3, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62.3
Std Dev 1.269
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch3, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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50
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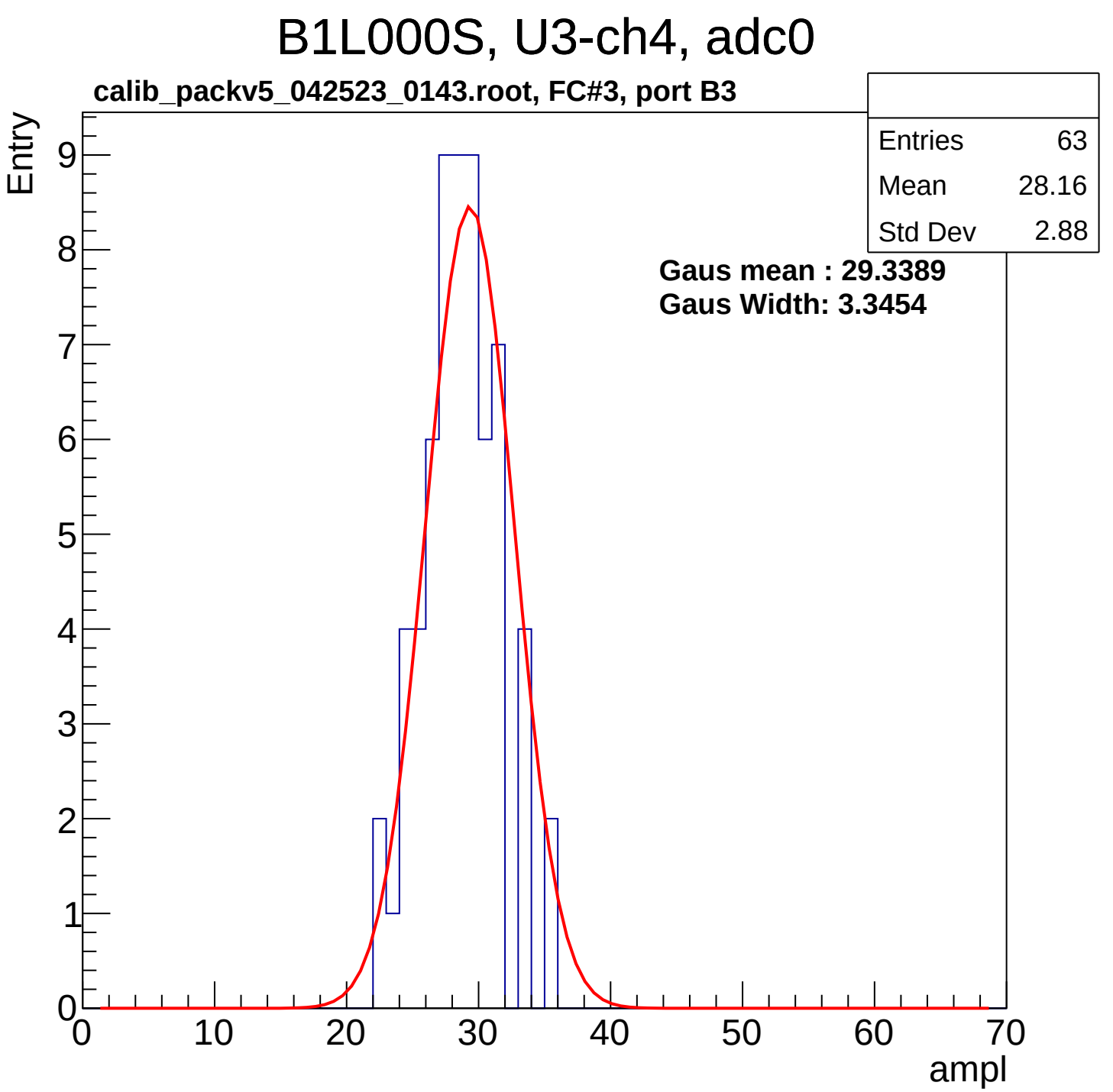
70
ampl


B1L000S, U3-ch4, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 28.16
Std Dev 2.88
Gaus mean : 29.3389
Gaus Width: 3.3454
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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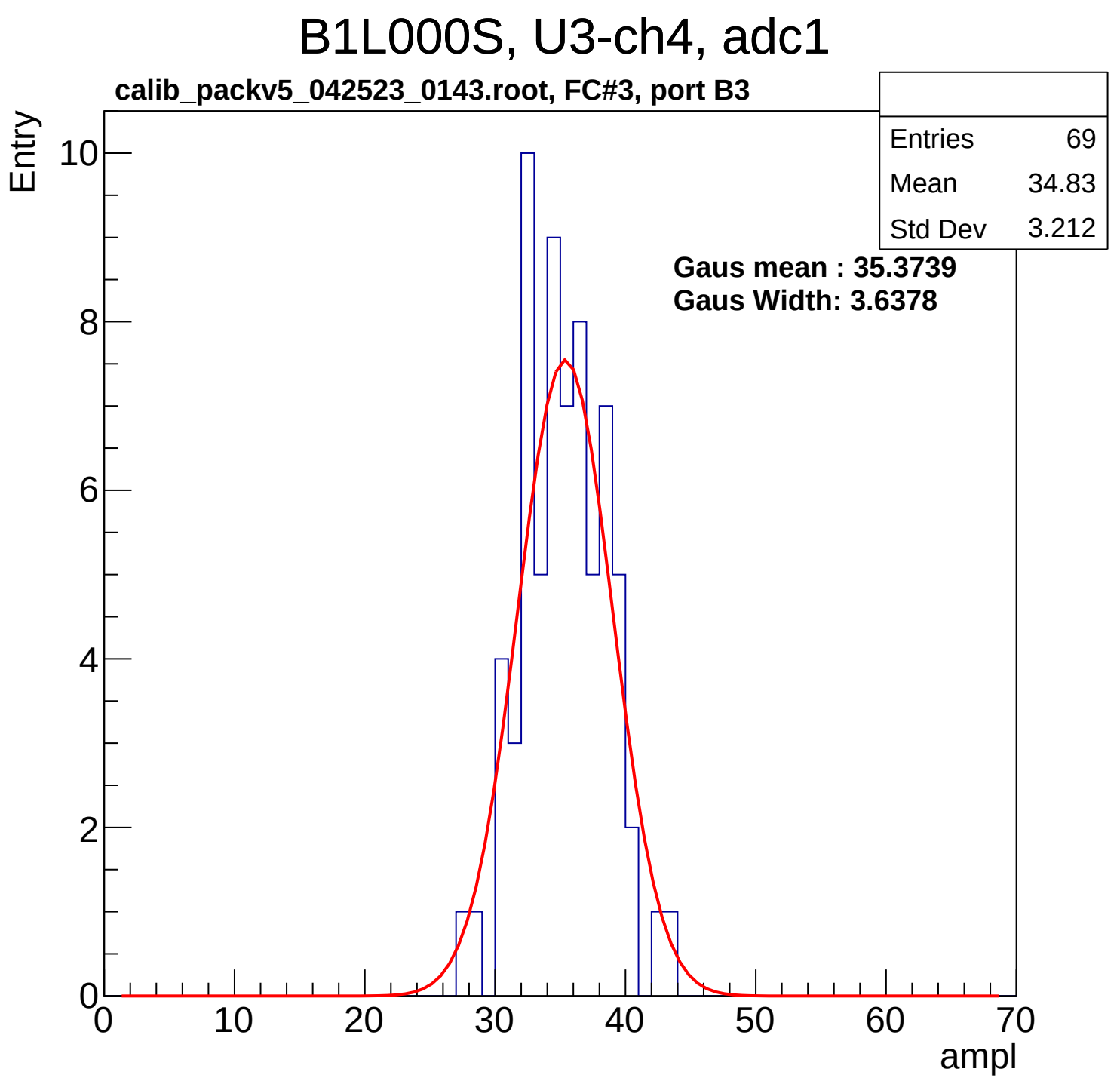
B1L000S, U3-ch4, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 34.83
Std Dev 3.212
Gaus mean : 35.3739
Gaus Width: 3.6378
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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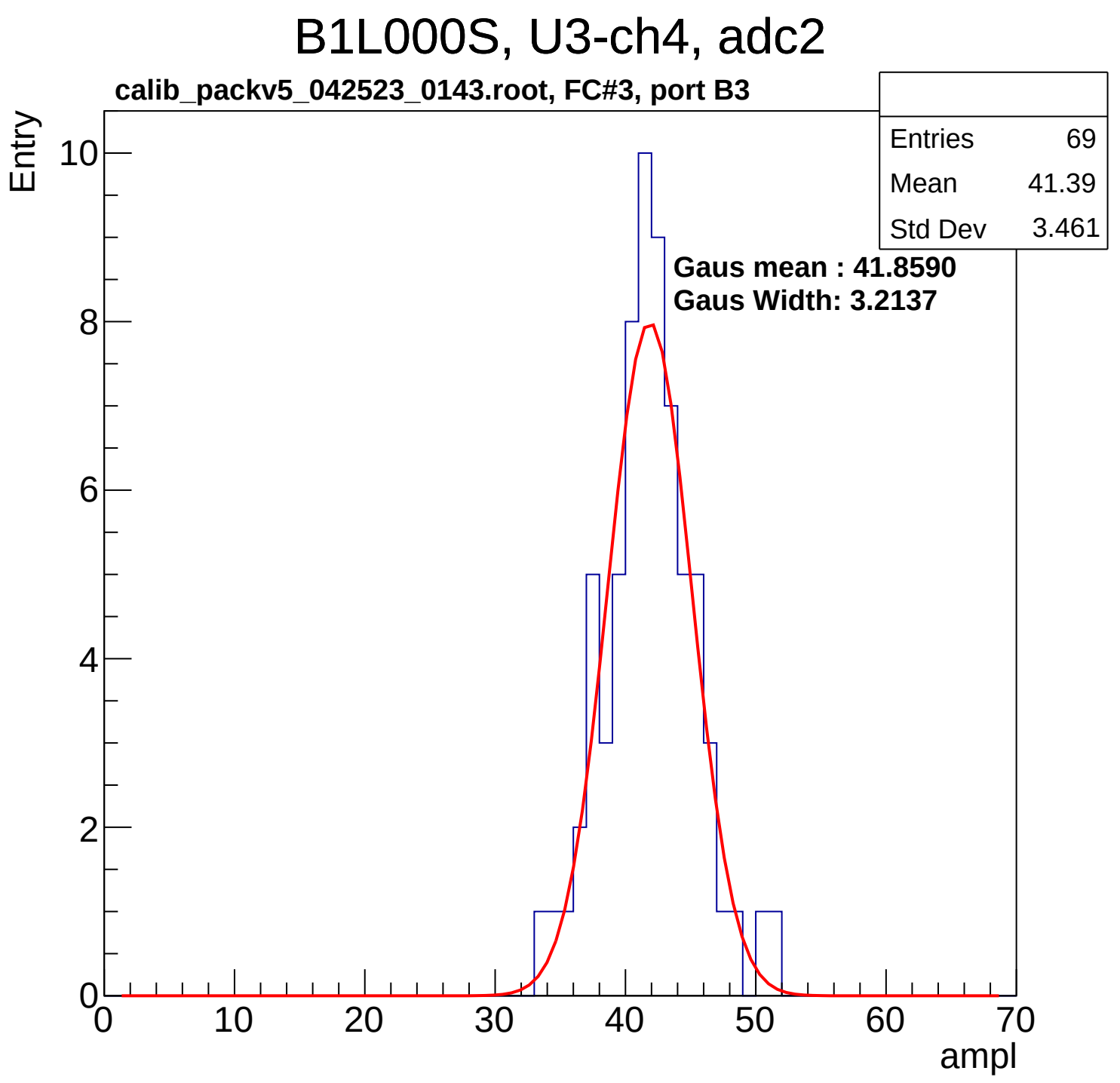
B1L000S, U3-ch4, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 41.39
Std Dev 3.461
Gaus mean : 41.8590
Gaus Width: 3.2137
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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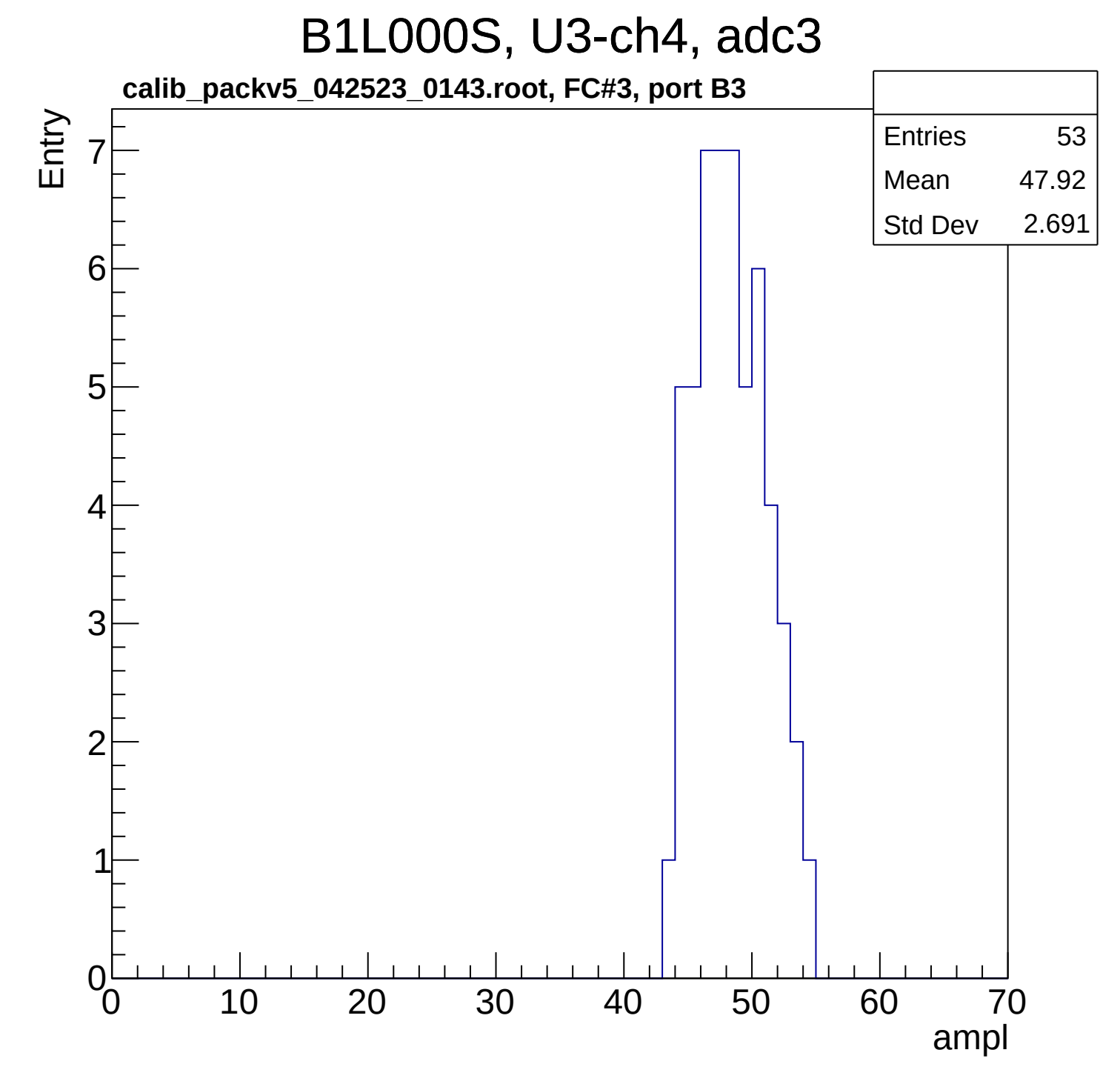
B1L000S, U3-ch4, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 47.92
Std Dev 2.691
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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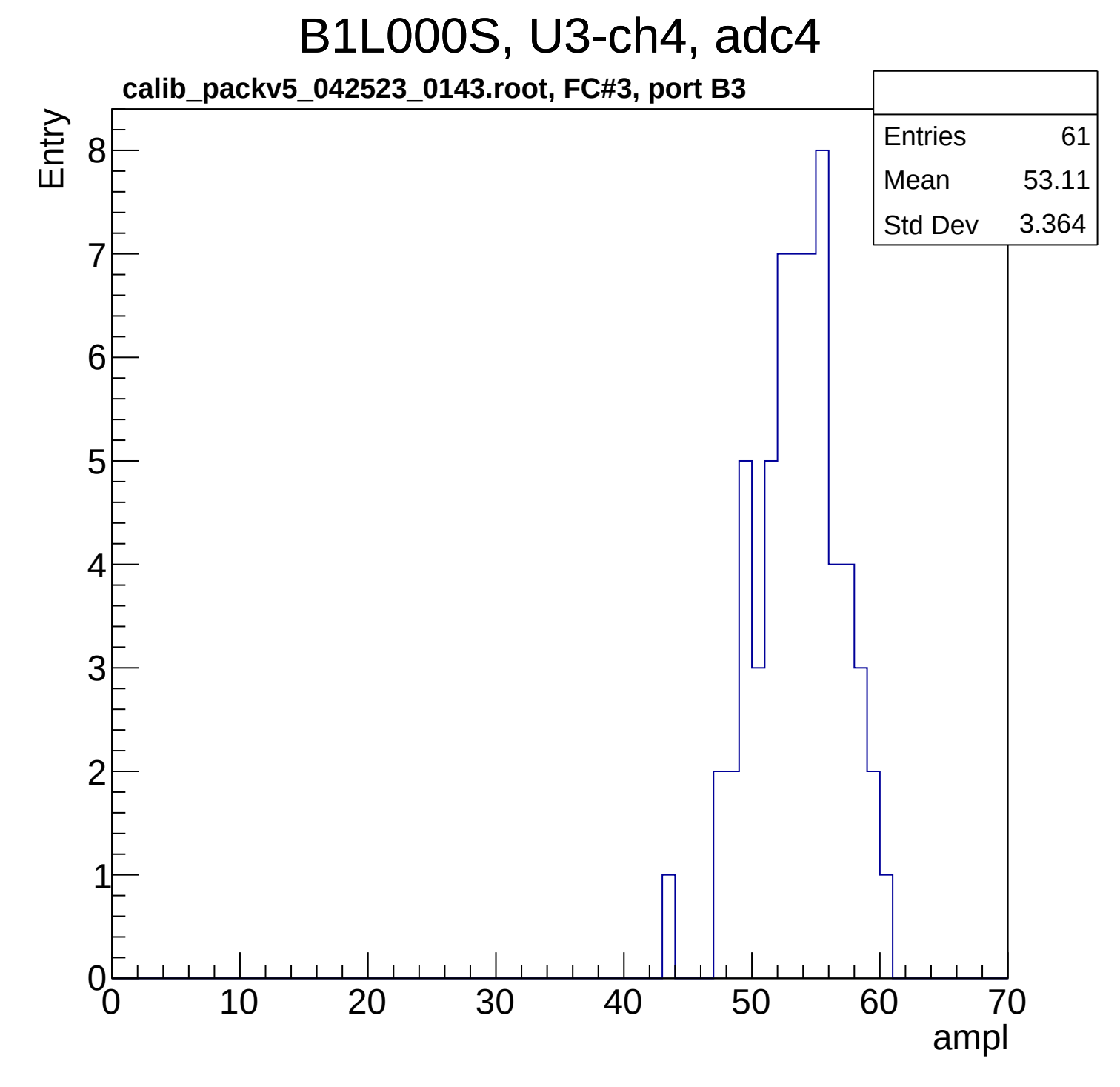
B1L000S, U3-ch4, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 53.11
Std Dev 3.364
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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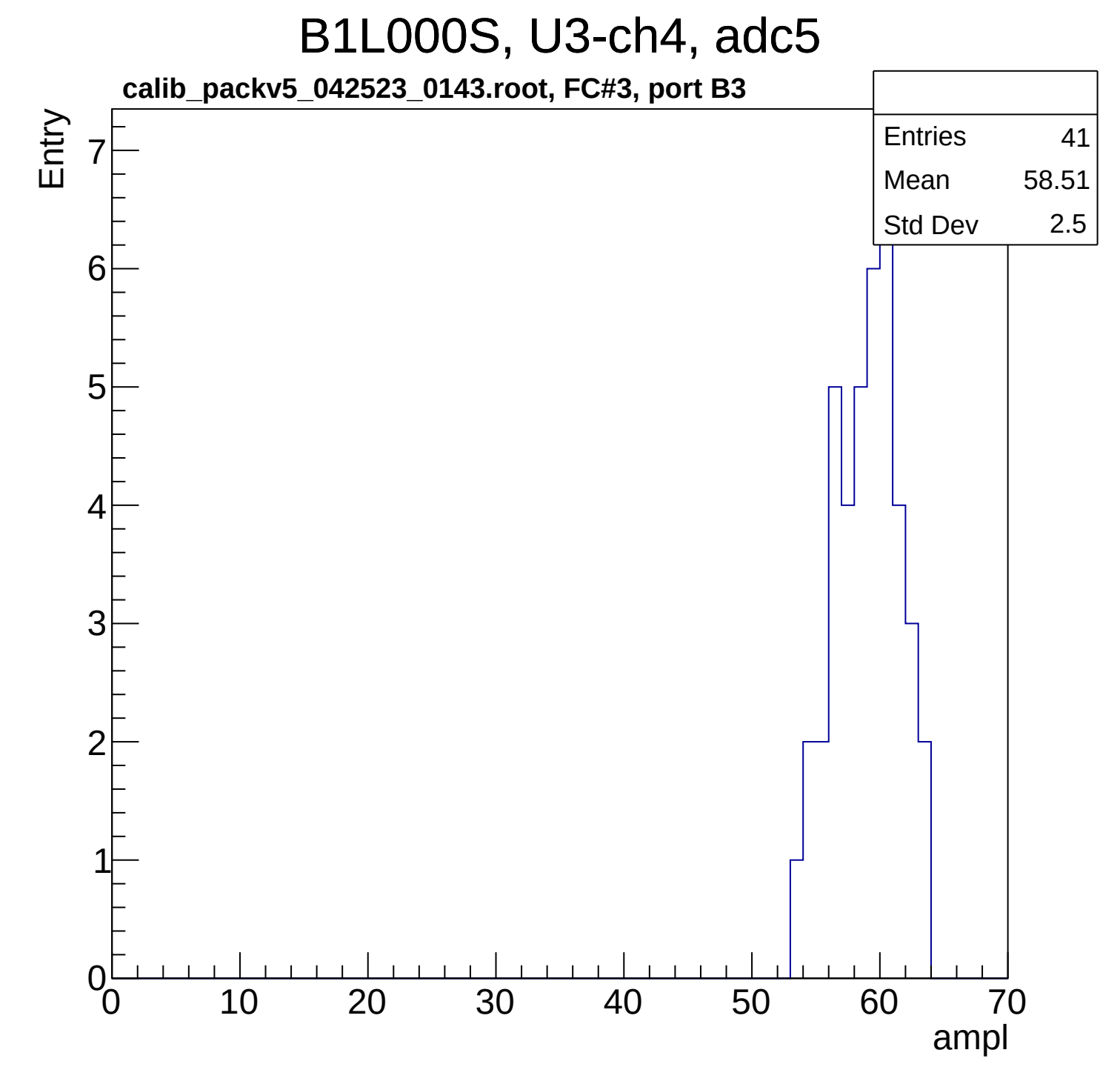
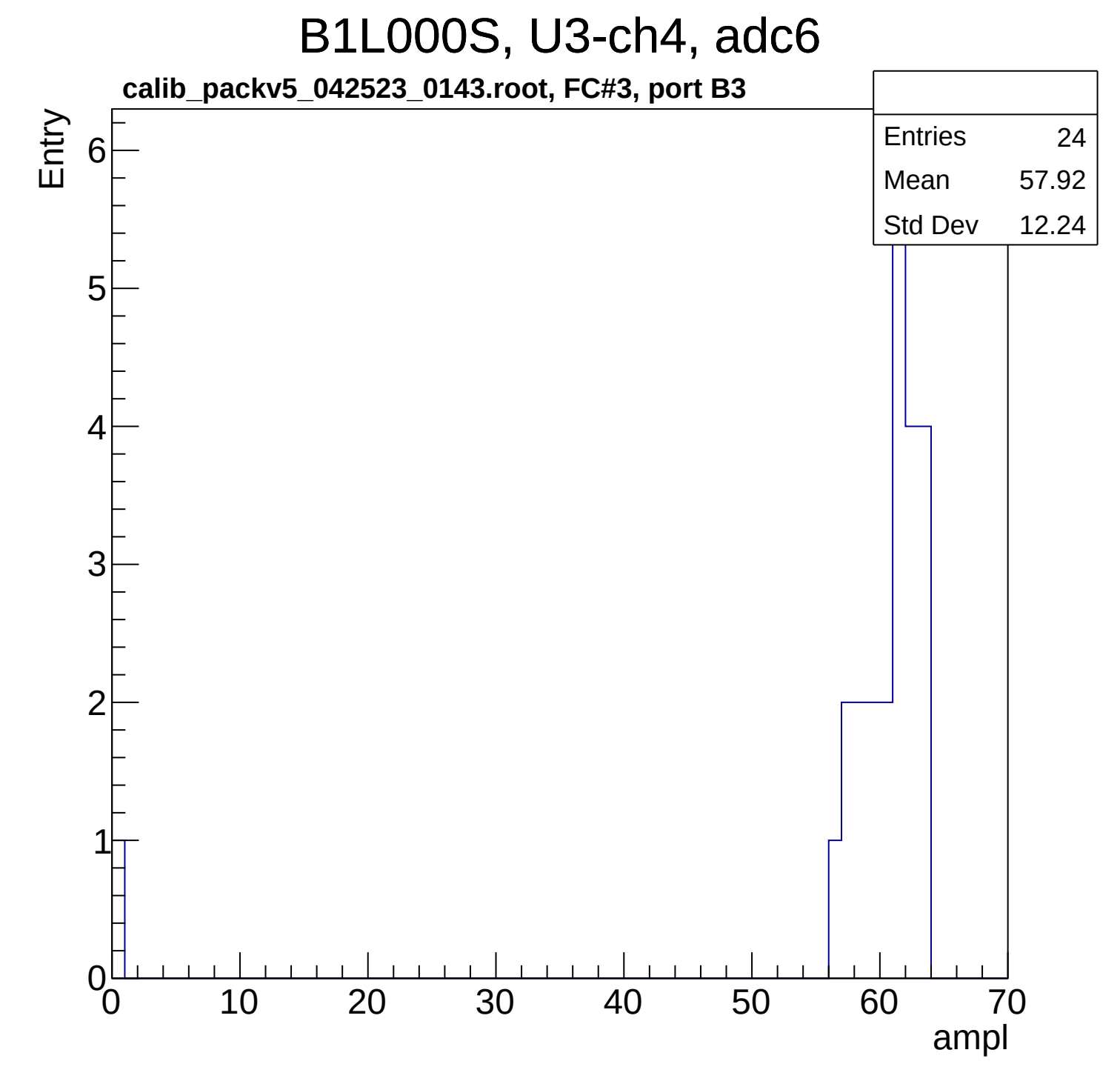
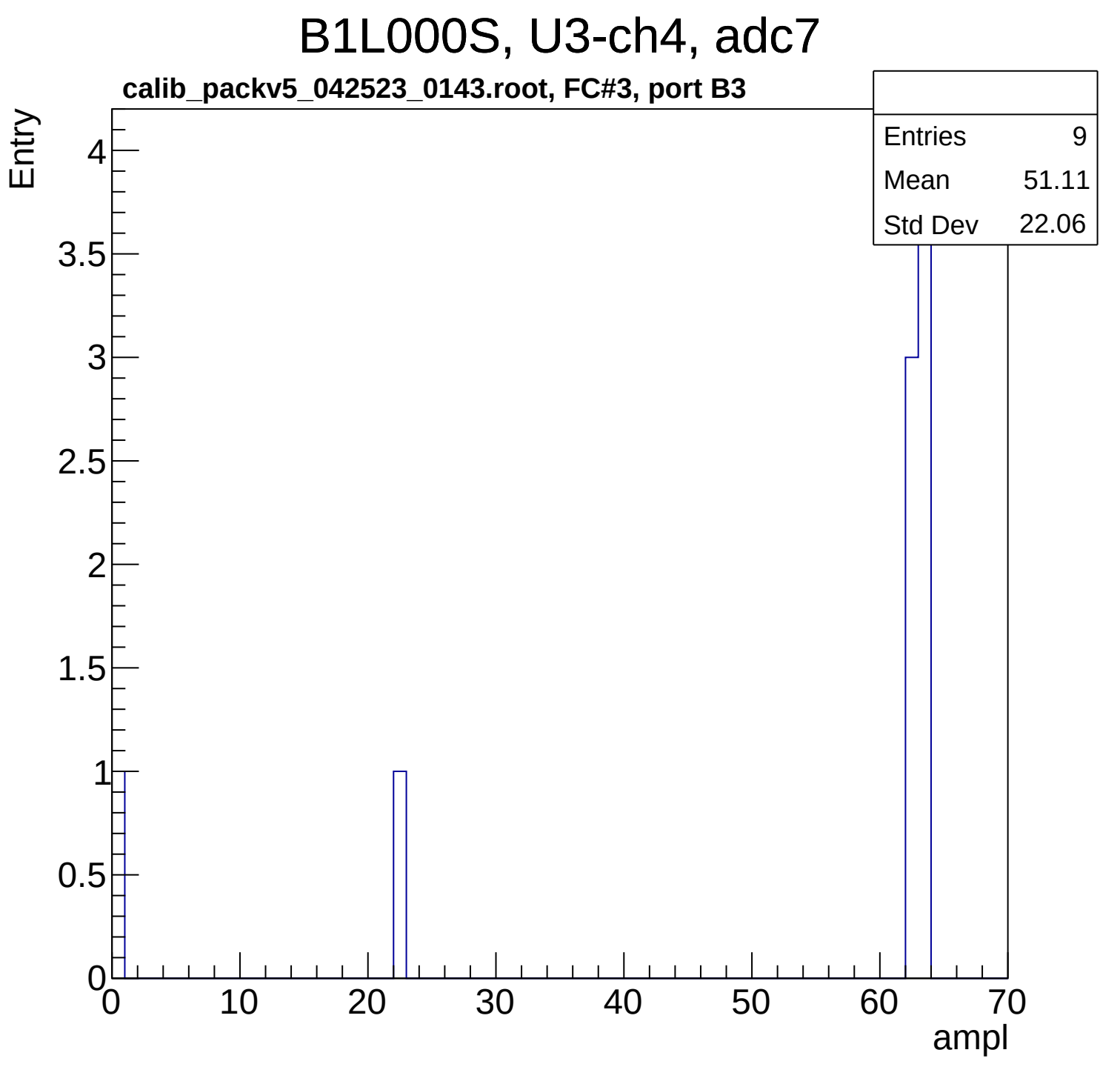
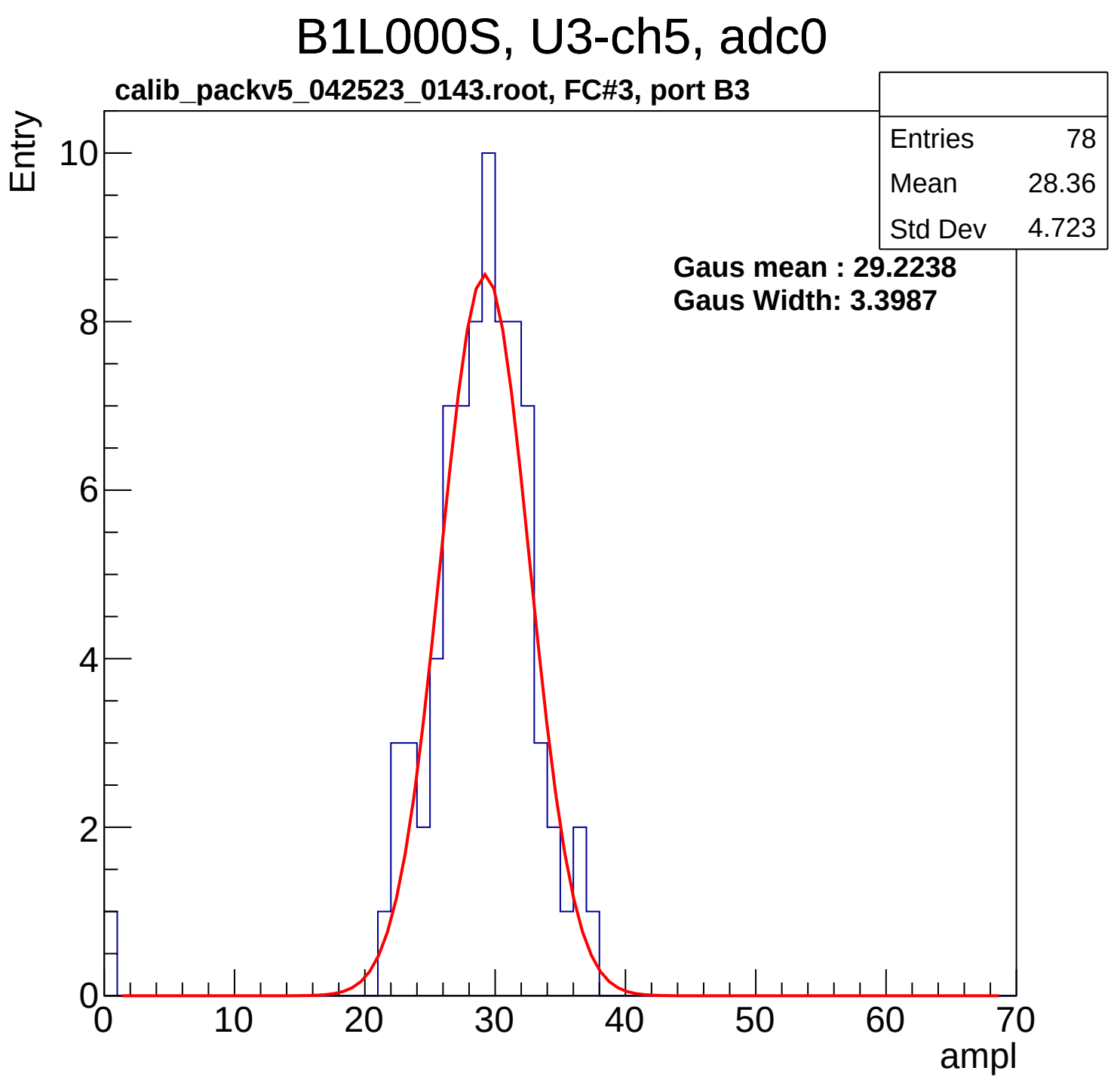
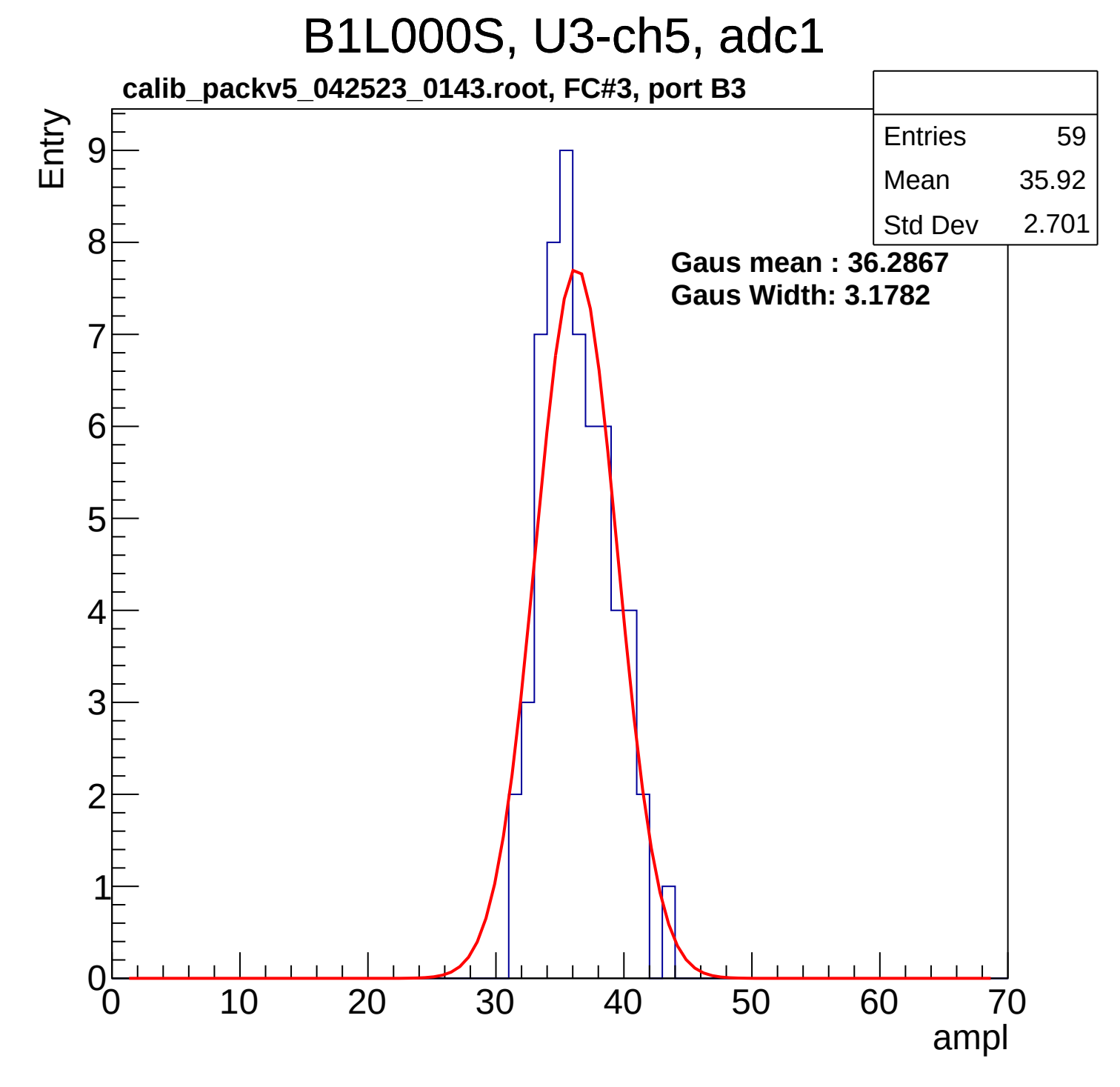
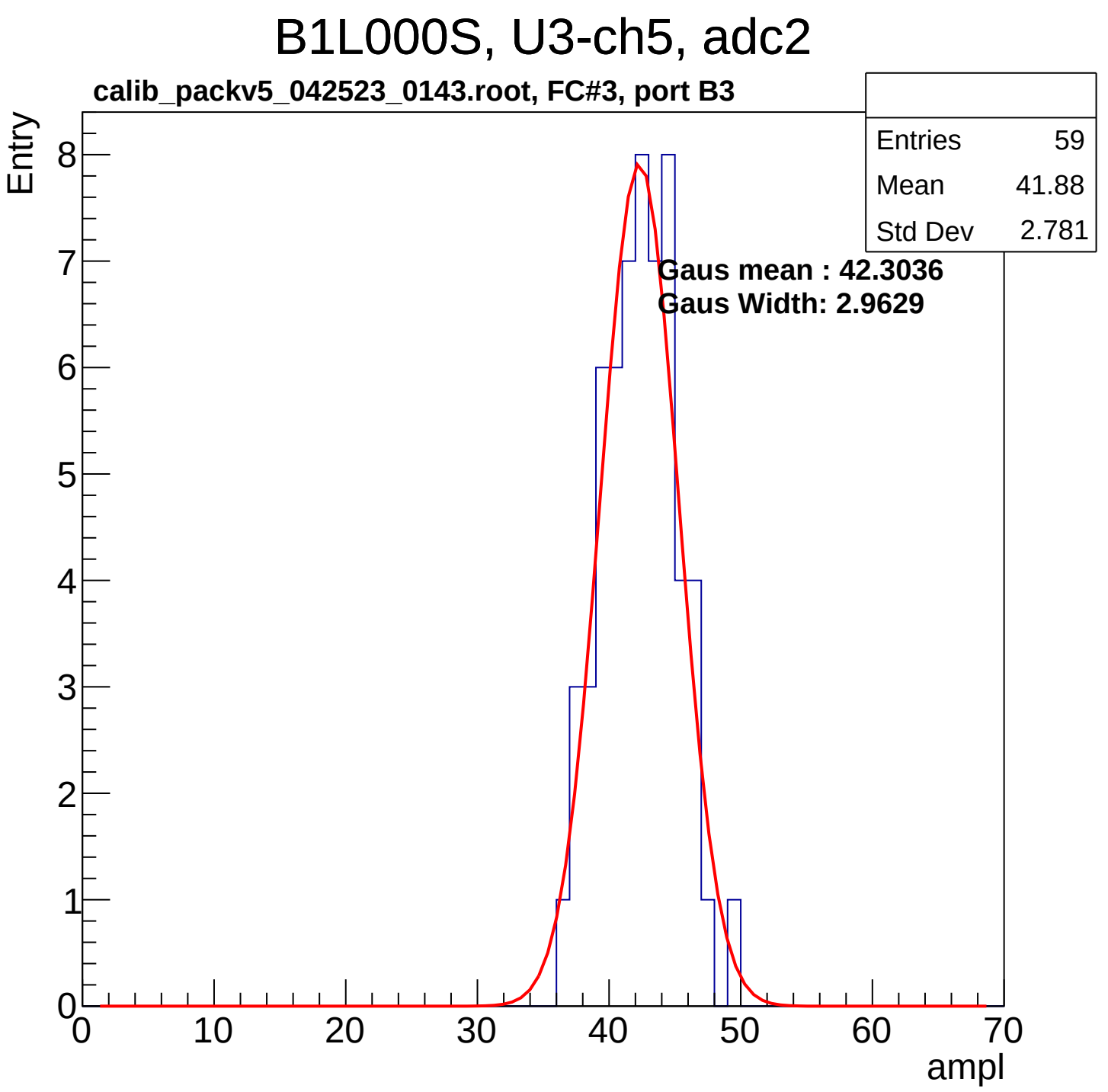

B1L000S, U3-ch4, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 58.51
Std Dev 2.5
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch4, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 24
Mean 57.92
Std Dev 12.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch4, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 51.11
Std Dev 22.06
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch5, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 28.36
Std Dev 4.723
Gaus mean : 29.2238
Gaus Width: 3.3987
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch5, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 35.92
Std Dev 2.701
Gaus mean : 36.2867
Gaus Width: 3.1782
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch5, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 41.88
Std Dev 2.781
Gaus mean : 42.3036
Gaus Width: 2.9629
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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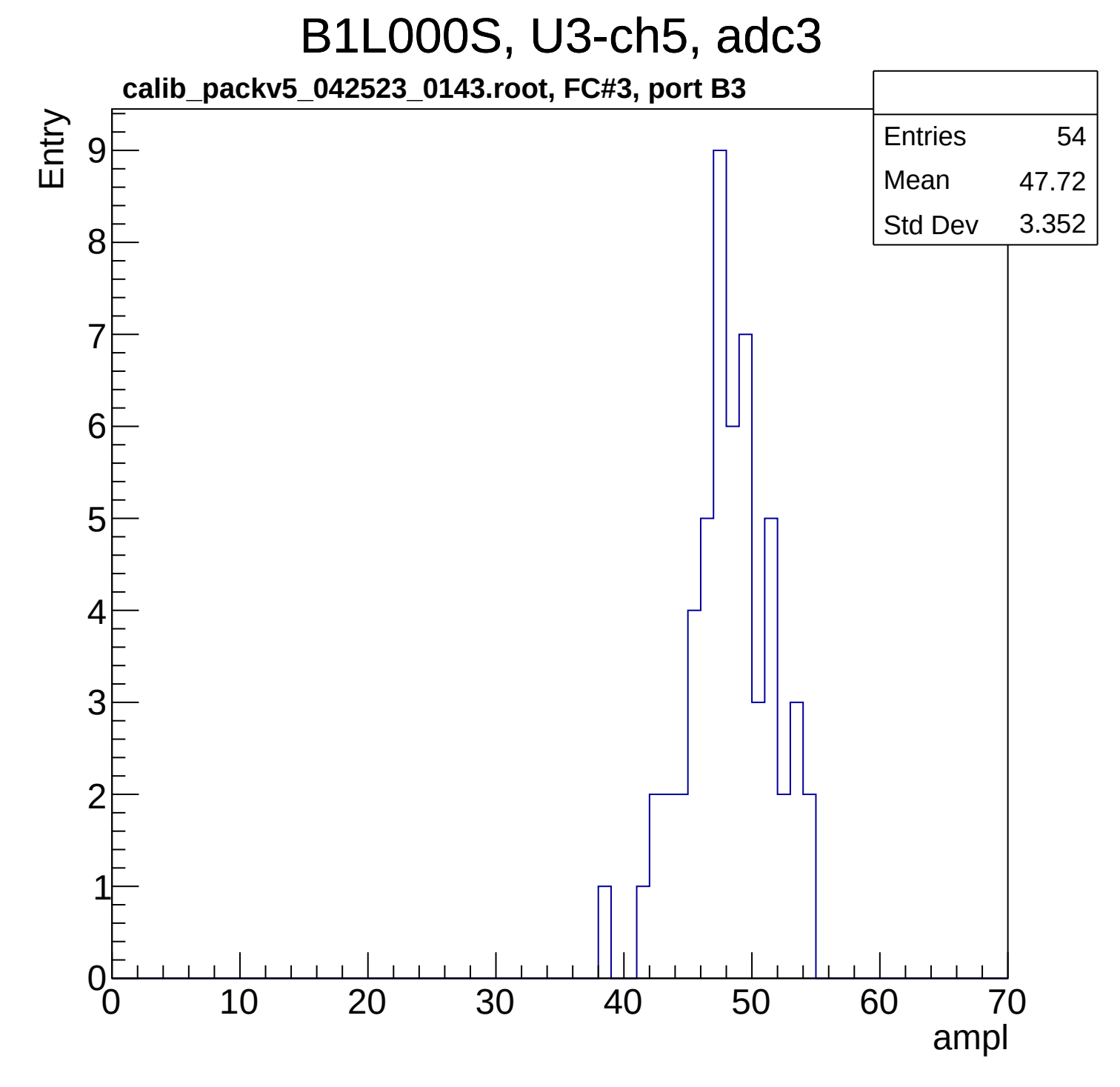
B1L000S, U3-ch5, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 47.72
Std Dev 3.352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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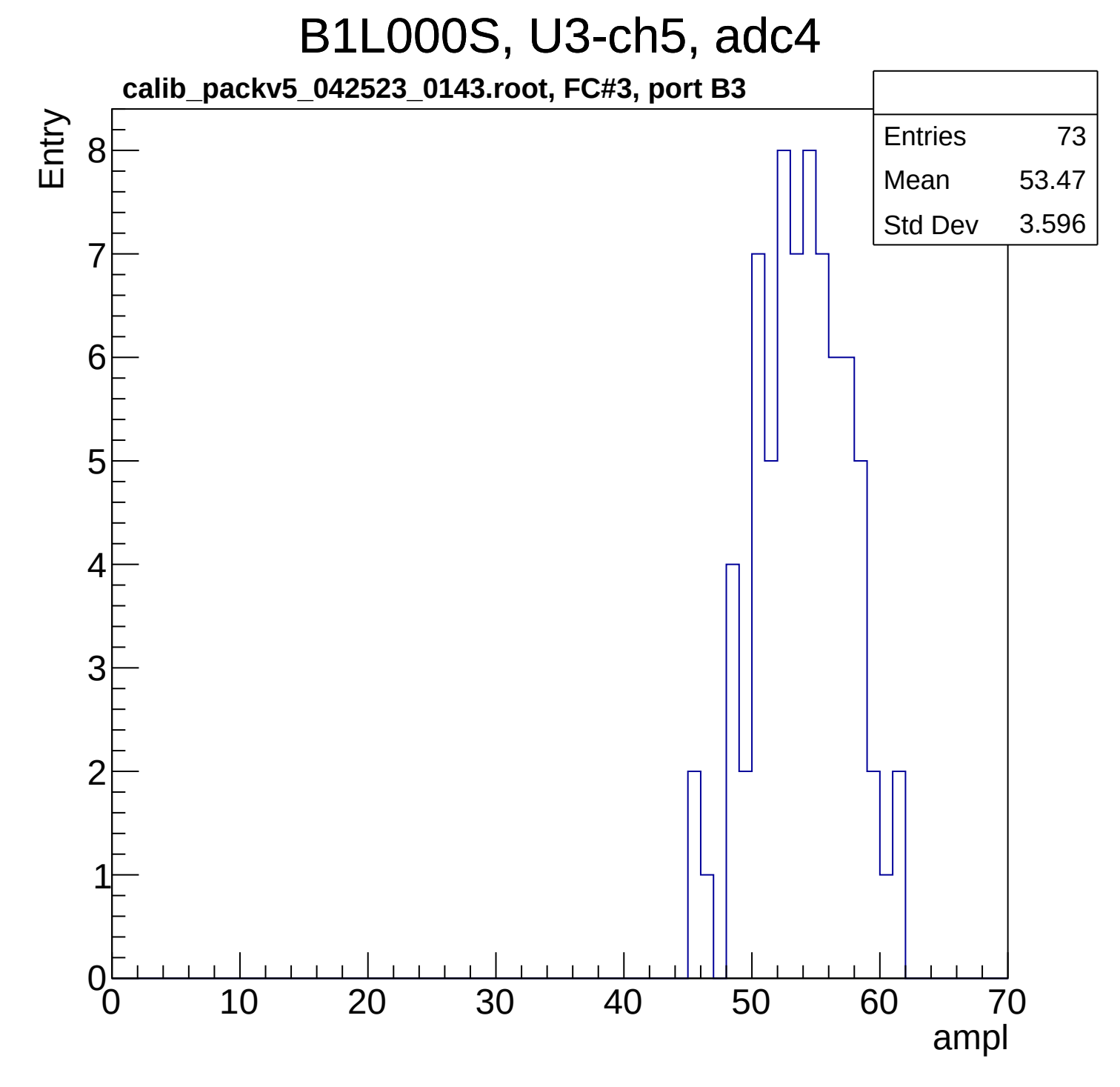
B1L000S, U3-ch5, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 53.47
Std Dev 3.596
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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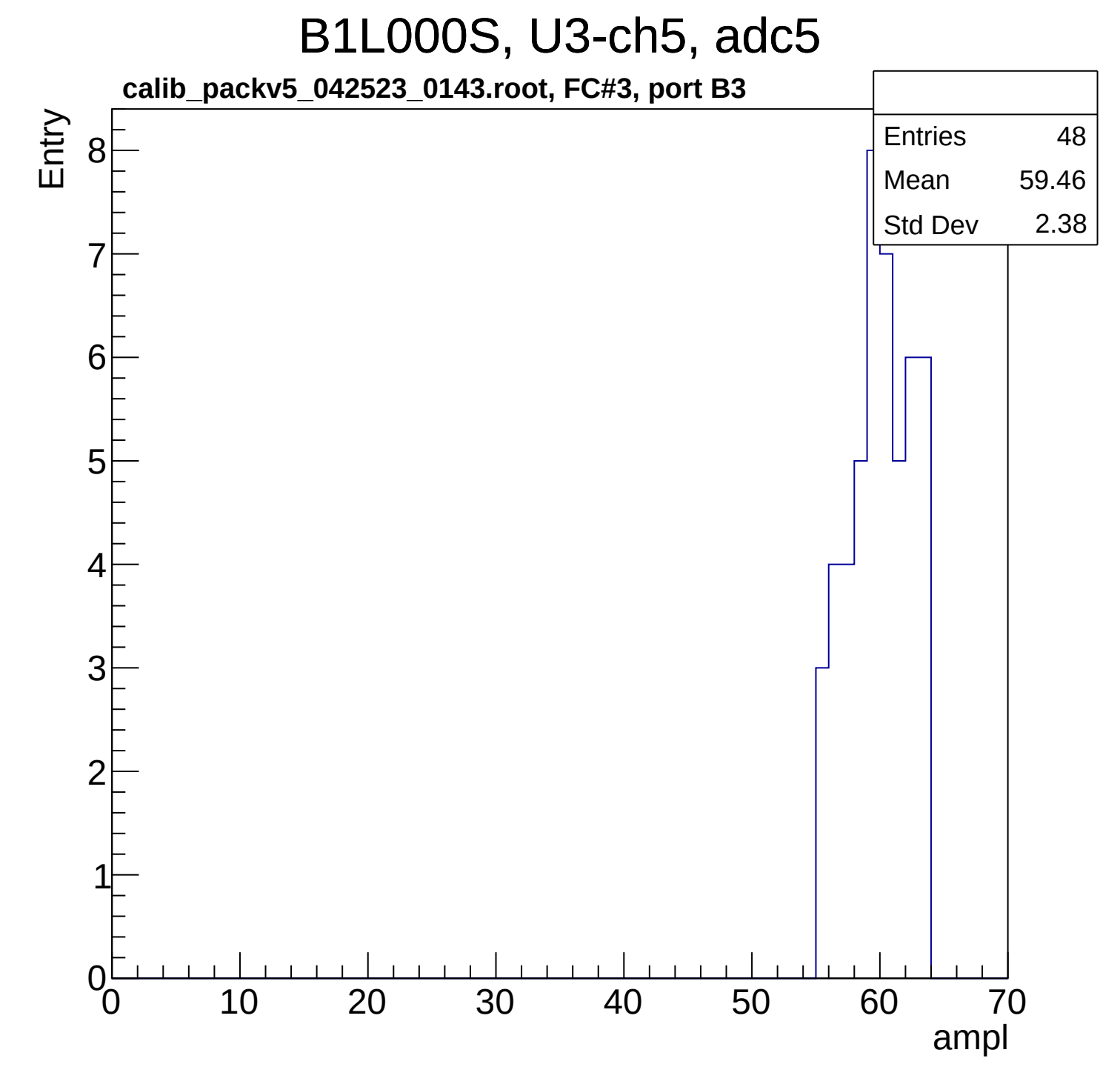
B1L000S, U3-ch5, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 59.46
Std Dev 2.38
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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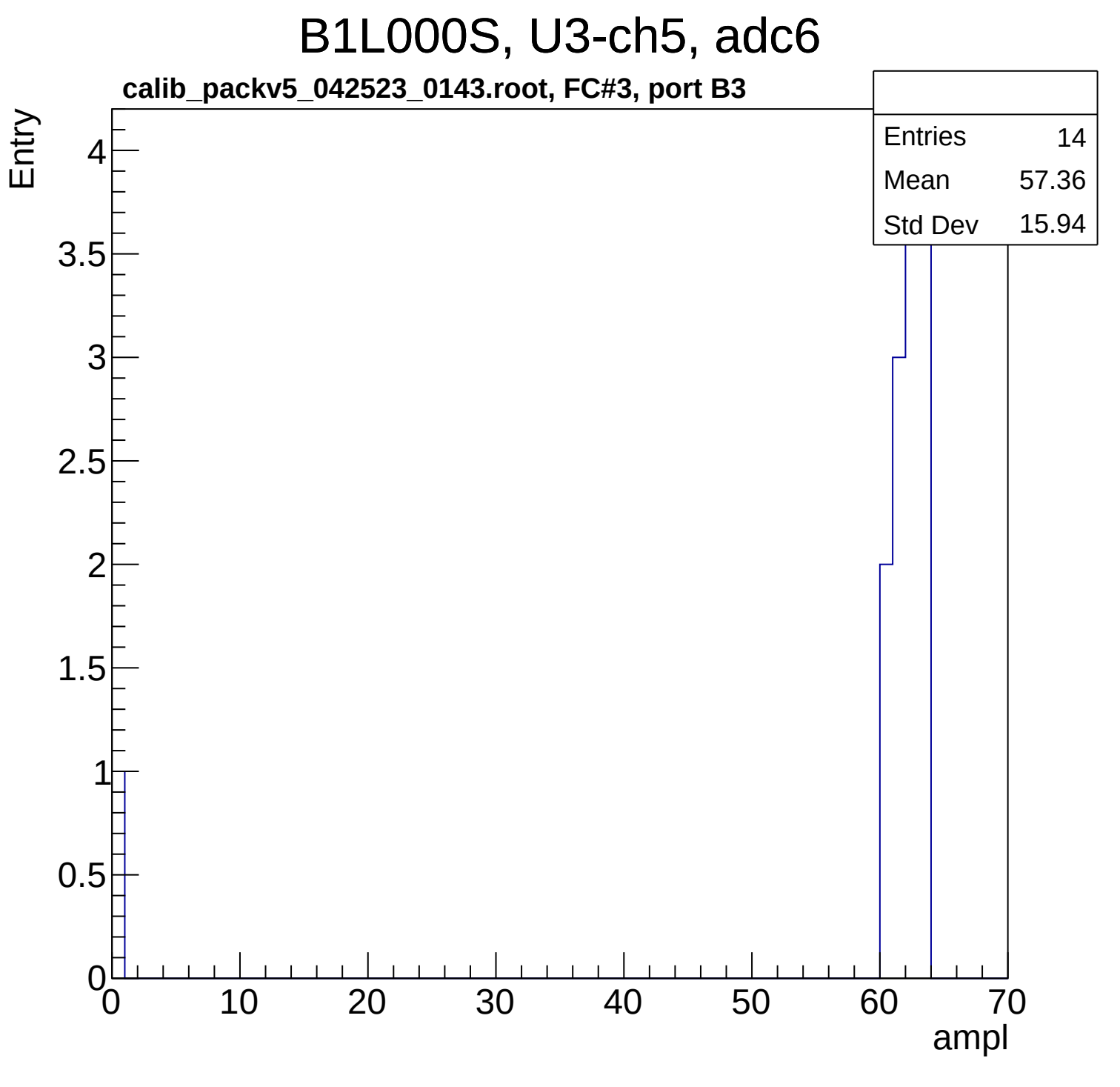
B1L000S, U3-ch5, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.36
Std Dev 15.94
Entry
ampl
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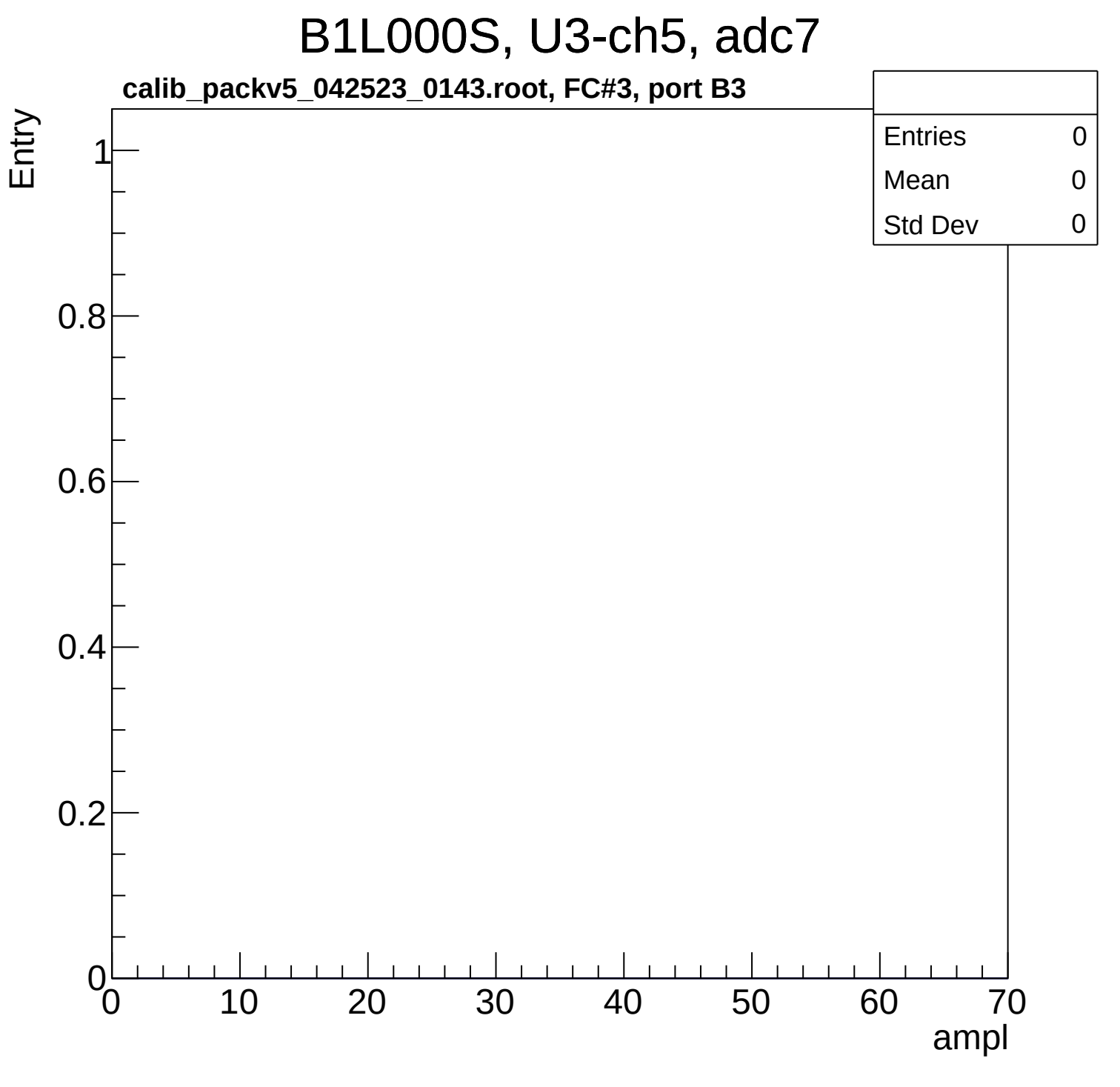
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch5, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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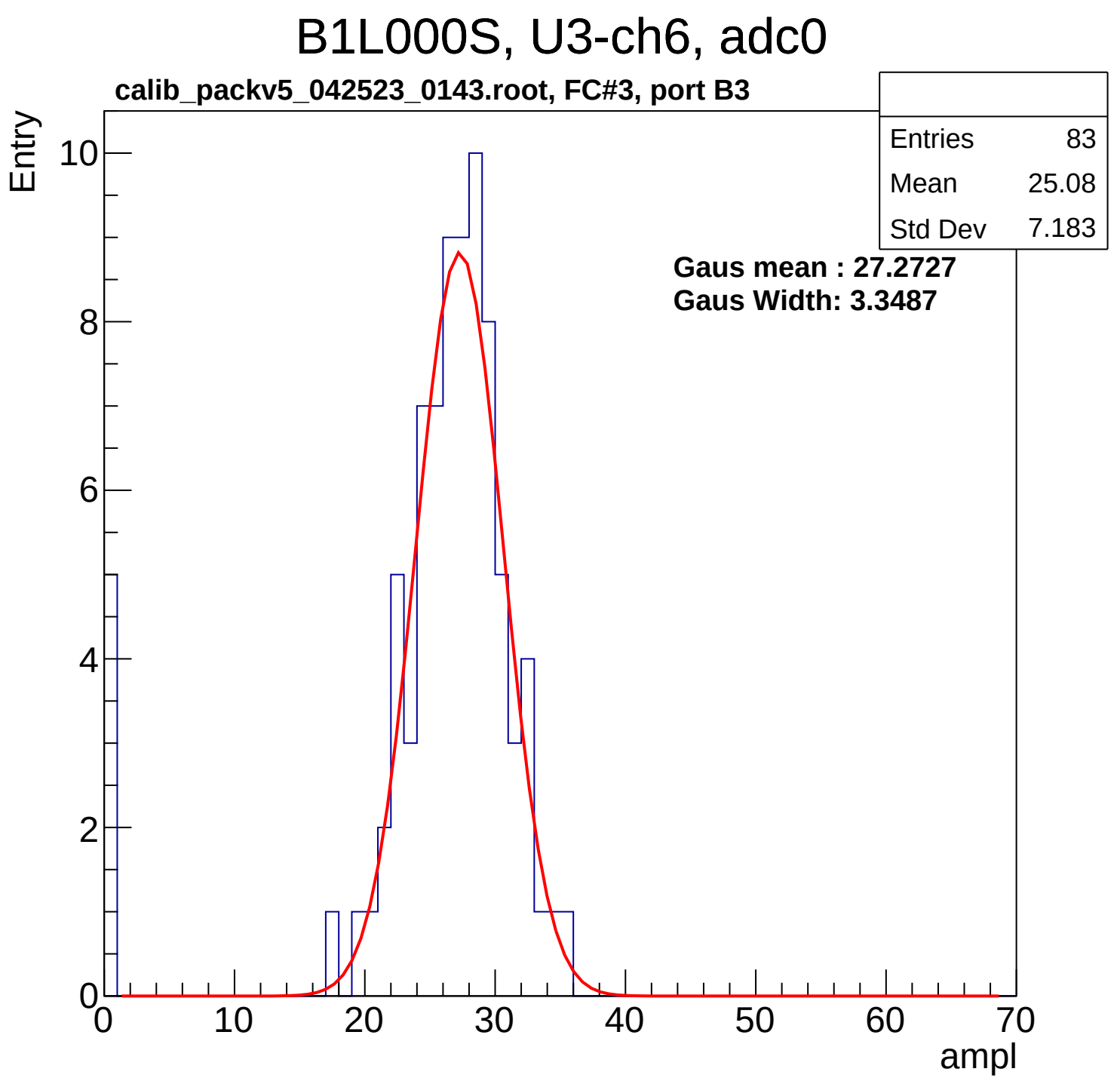
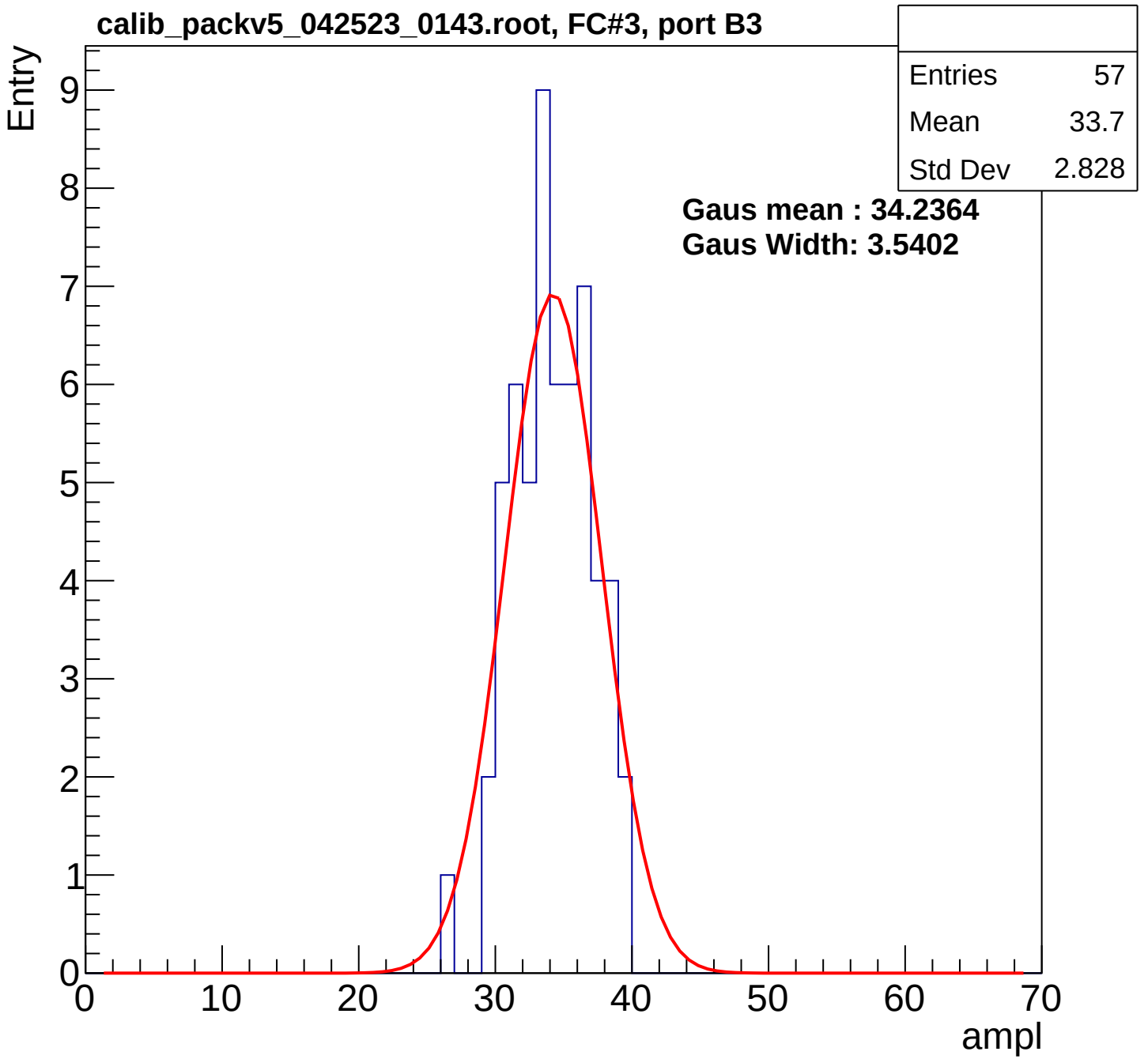
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch6, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 25.08
Std Dev 7.183
Gaus mean : 27.2727
Gaus Width: 3.3487
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch6, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 33.7
Std Dev 2.828
Gaus mean : 34.2364
Gaus Width: 3.5402
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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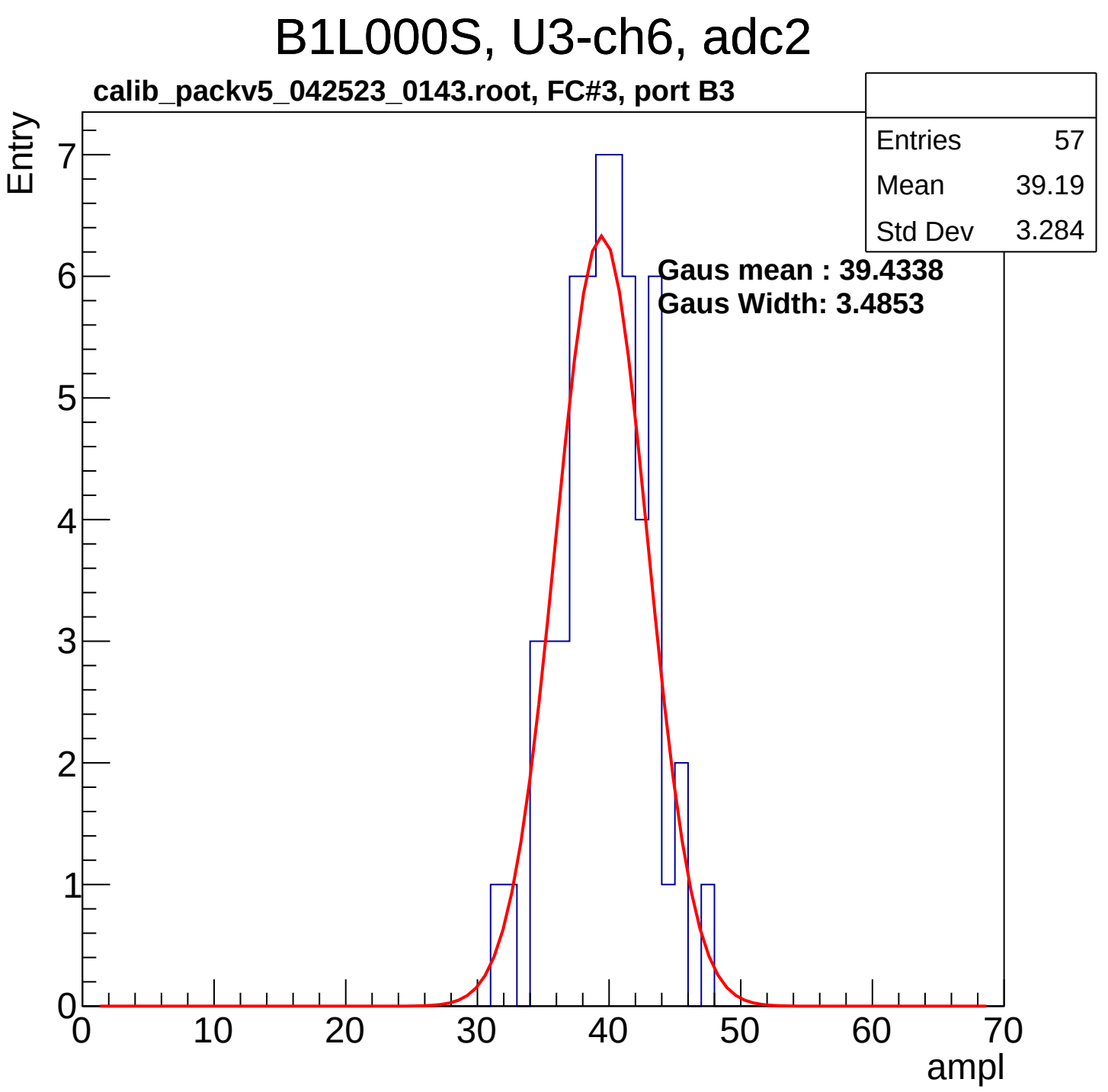
B1L000S, U3-ch6, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 39.19
Std Dev 3.284
Gaus mean : 39.4338
Gaus Width: 3.4853
Entry
ampl
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1
2
3
4
5
6
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0
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60
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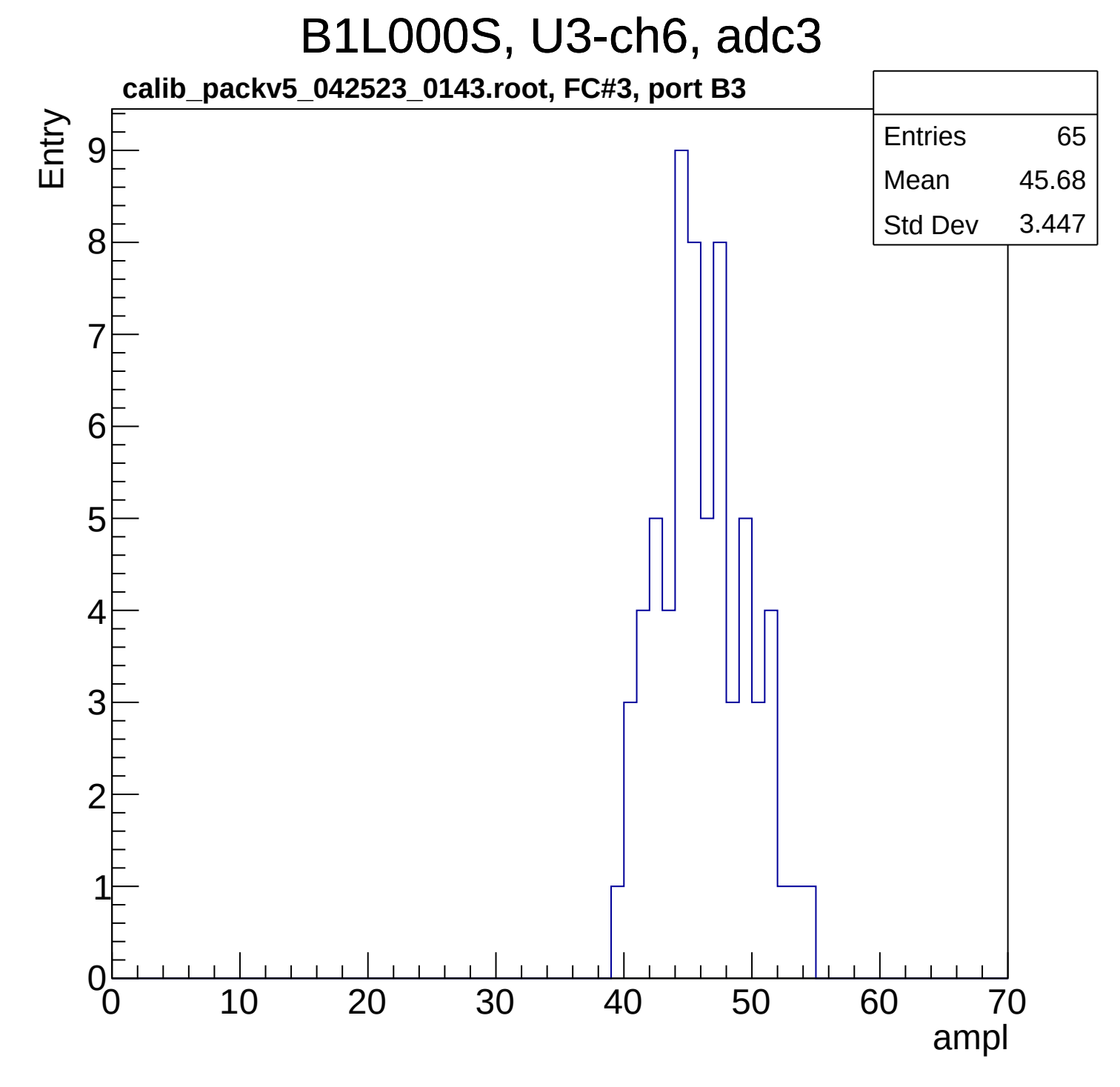
B1L000S, U3-ch6, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 45.68
Std Dev 3.447
Entry
ampl
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3
4
5
6
7
8
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0
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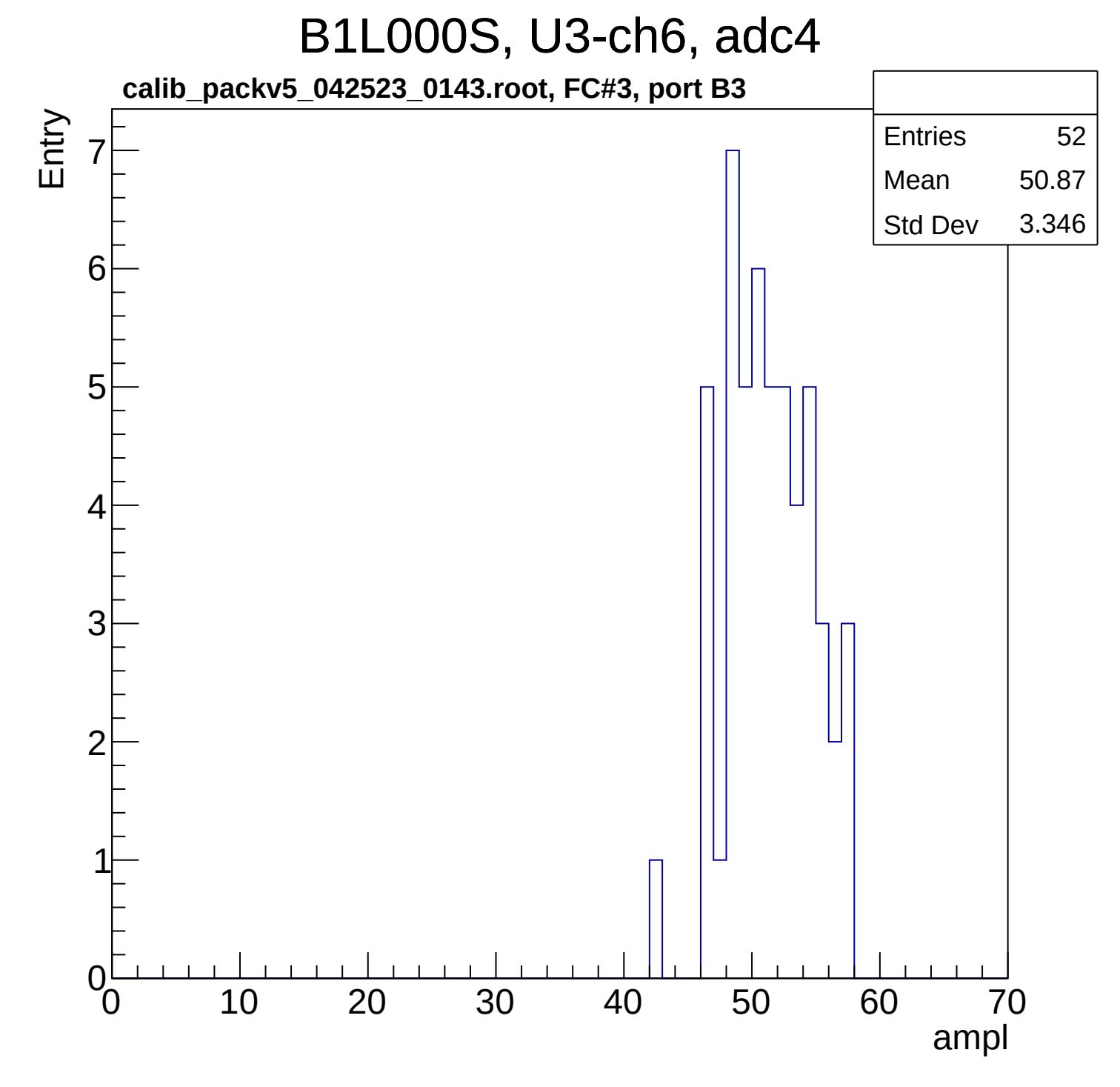
B1L000S, U3-ch6, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 50.87
Std Dev 3.346
Entry
ampl
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60
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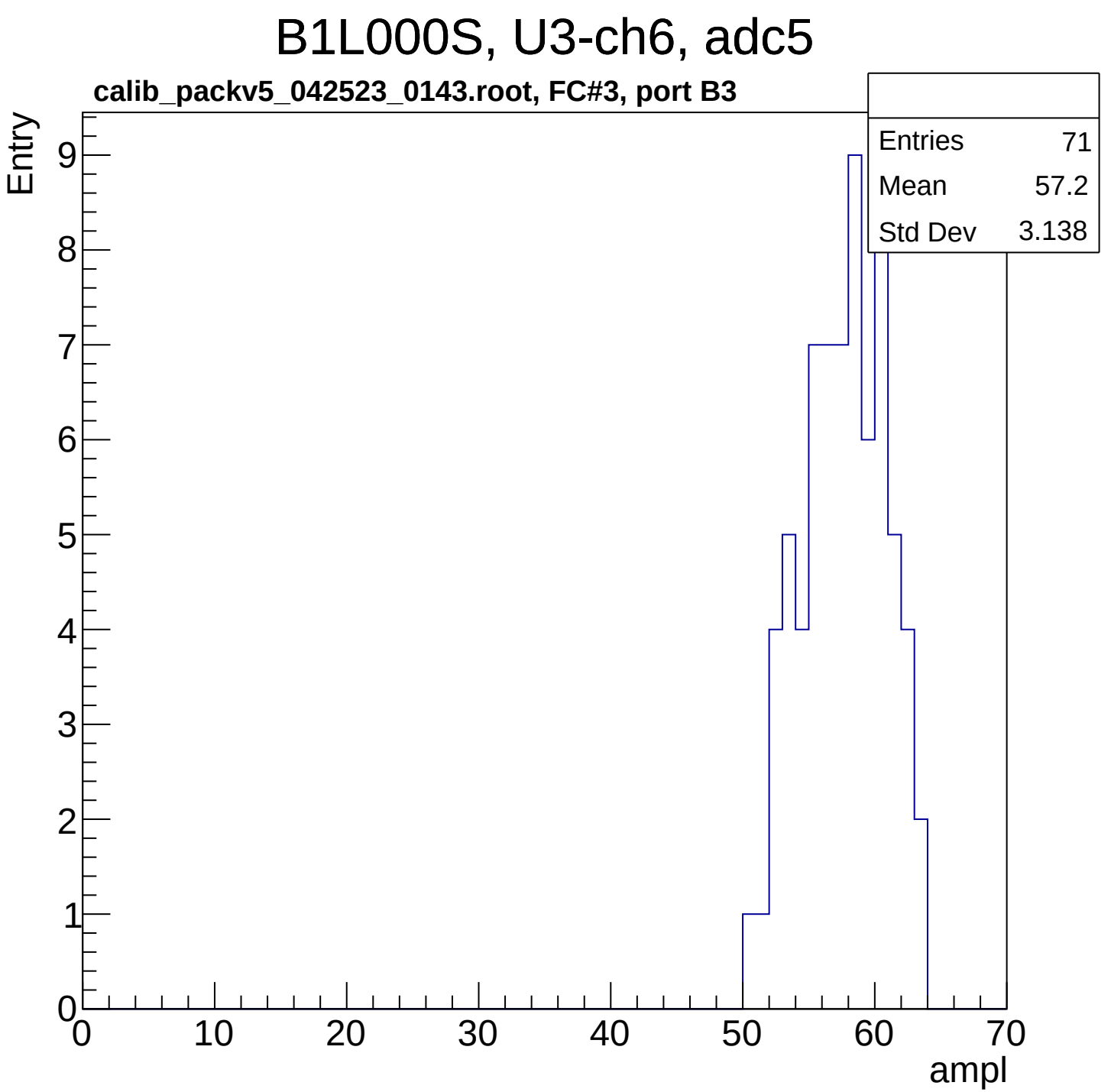
B1L000S, U3-ch6, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 57.2
Std Dev 3.138
Entry
ampl
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3
4
5
6
7
8
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0
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60
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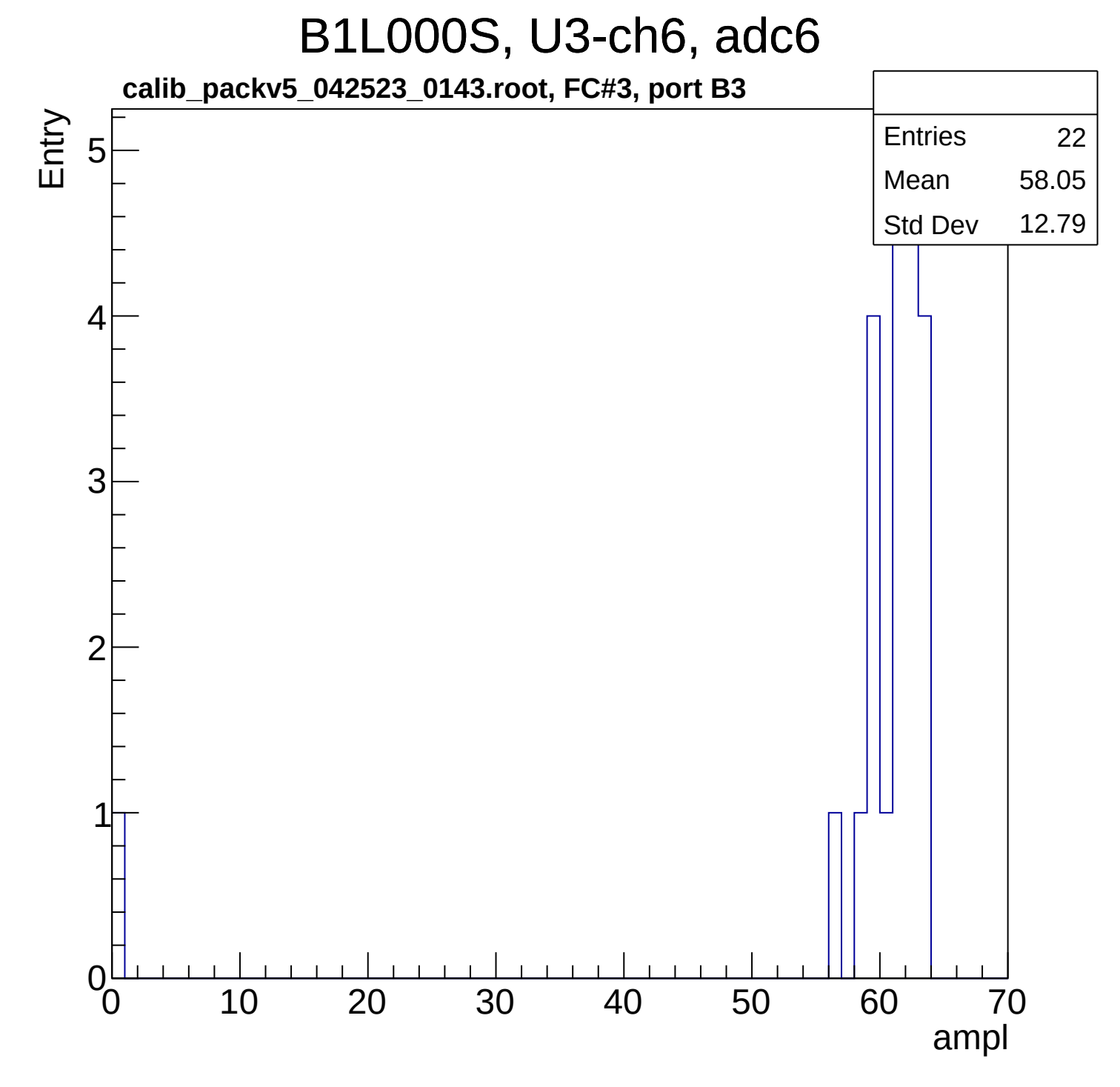
B1L000S, U3-ch6, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 58.05
Std Dev 12.79
Entry
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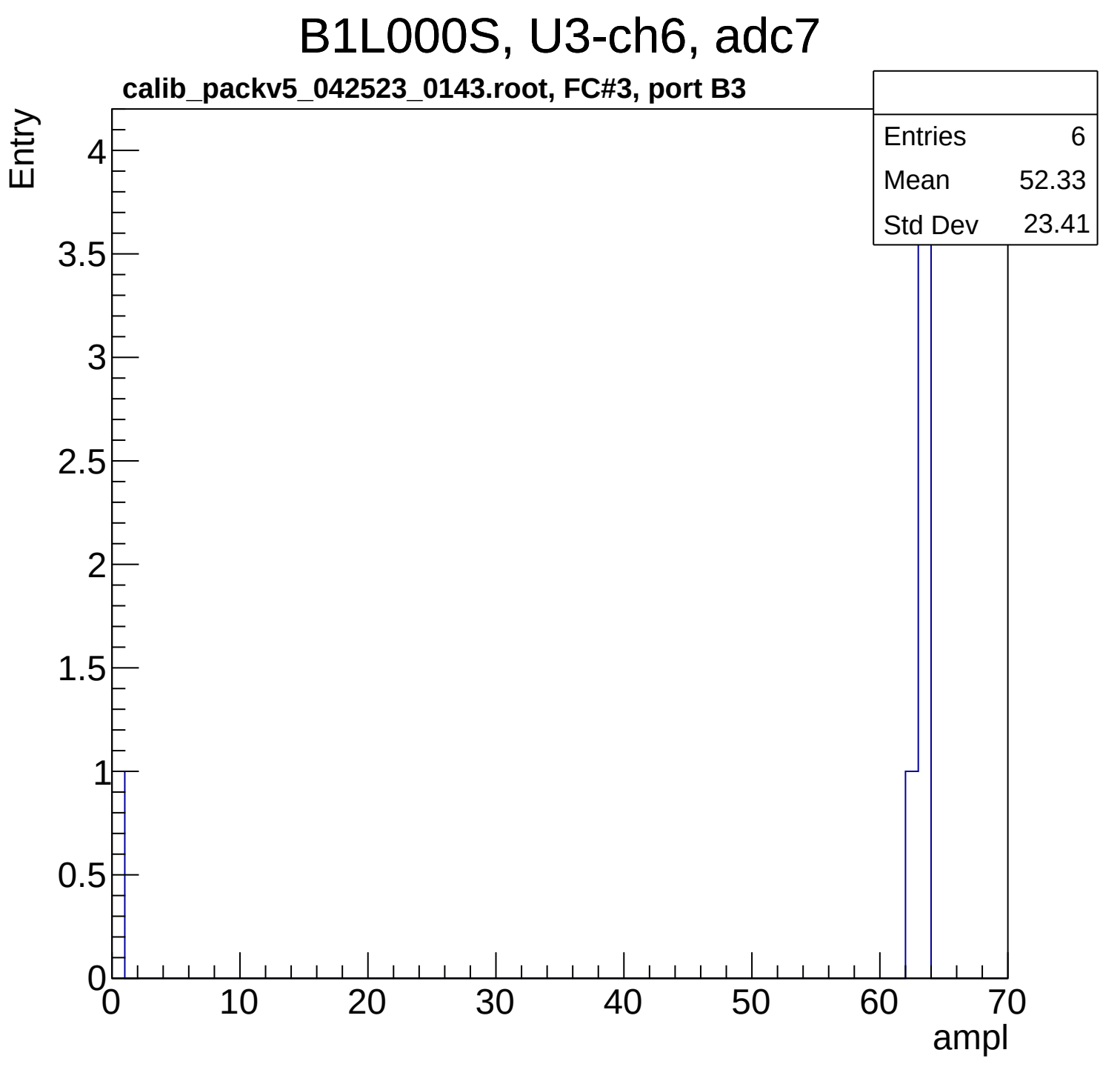
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch6, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 52.33
Std Dev 23.41
Entry
ampl
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1.5
2
2.5
3
3.5
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0
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60
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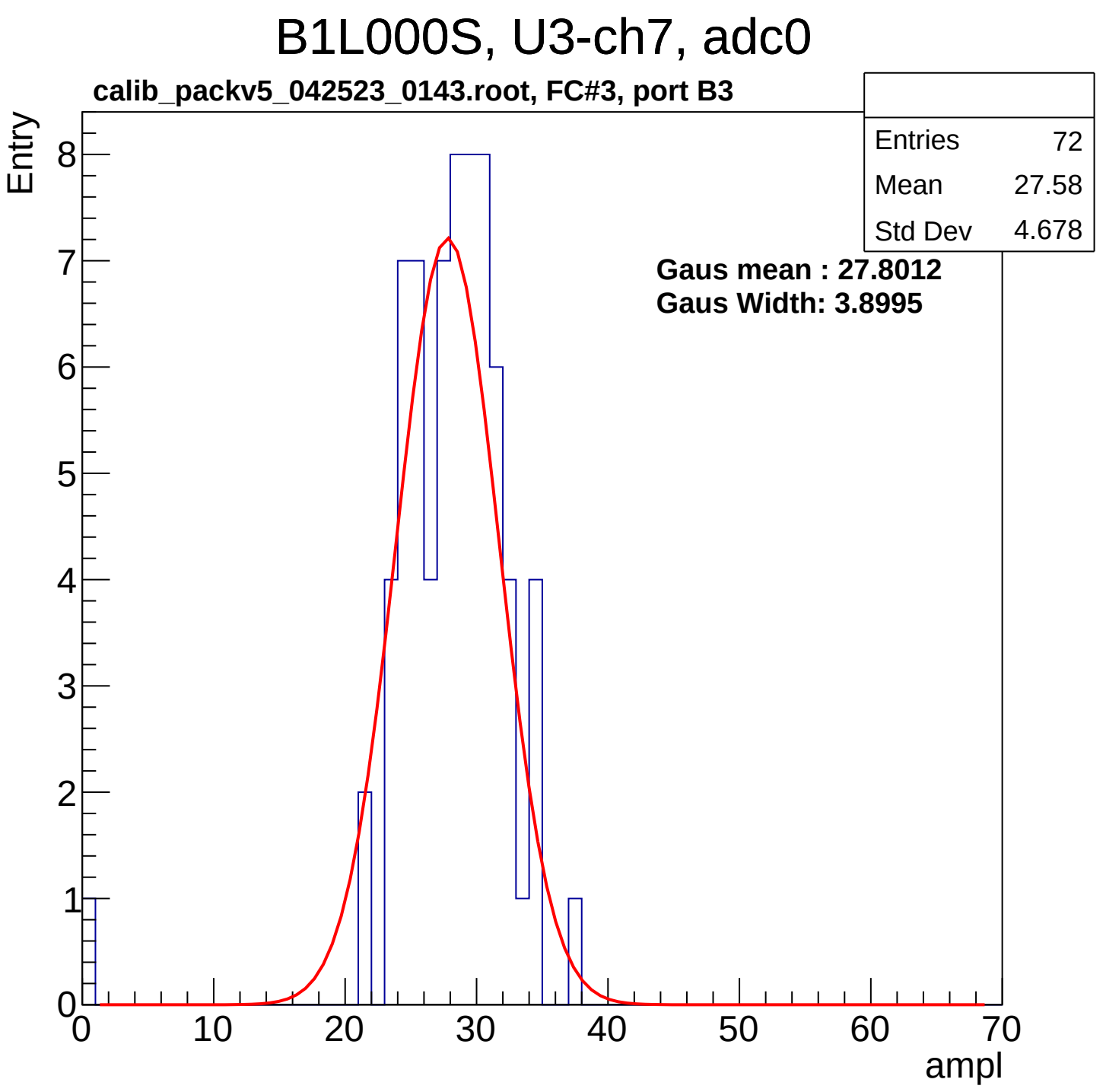
B1L000S, U3-ch7, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 27.58
Std Dev 4.678
Gaus mean : 27.8012
Gaus Width: 3.8995
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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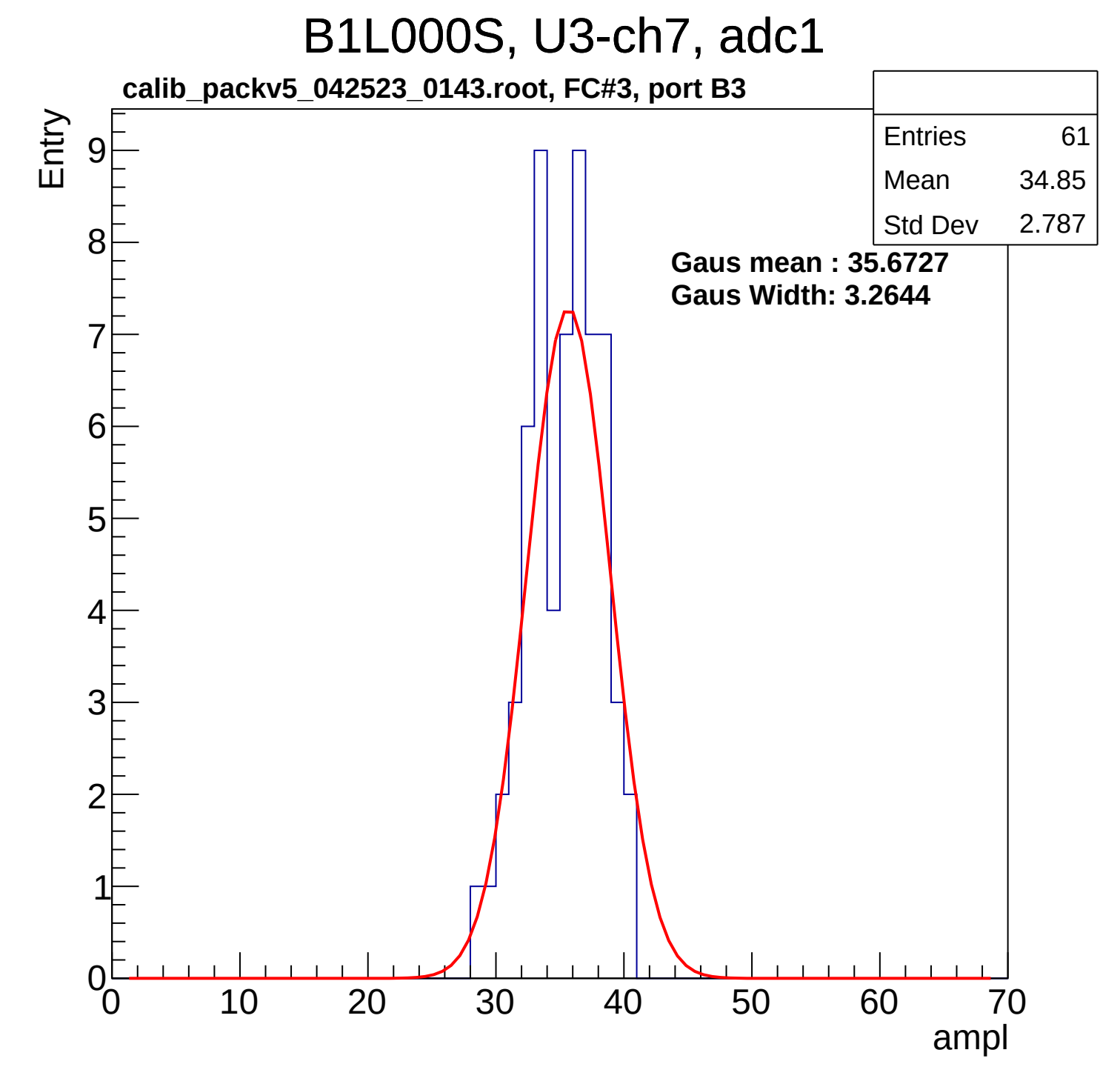
B1L000S, U3-ch7, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 34.85
Std Dev 2.787
Gaus mean : 35.6727
Gaus Width: 3.2644
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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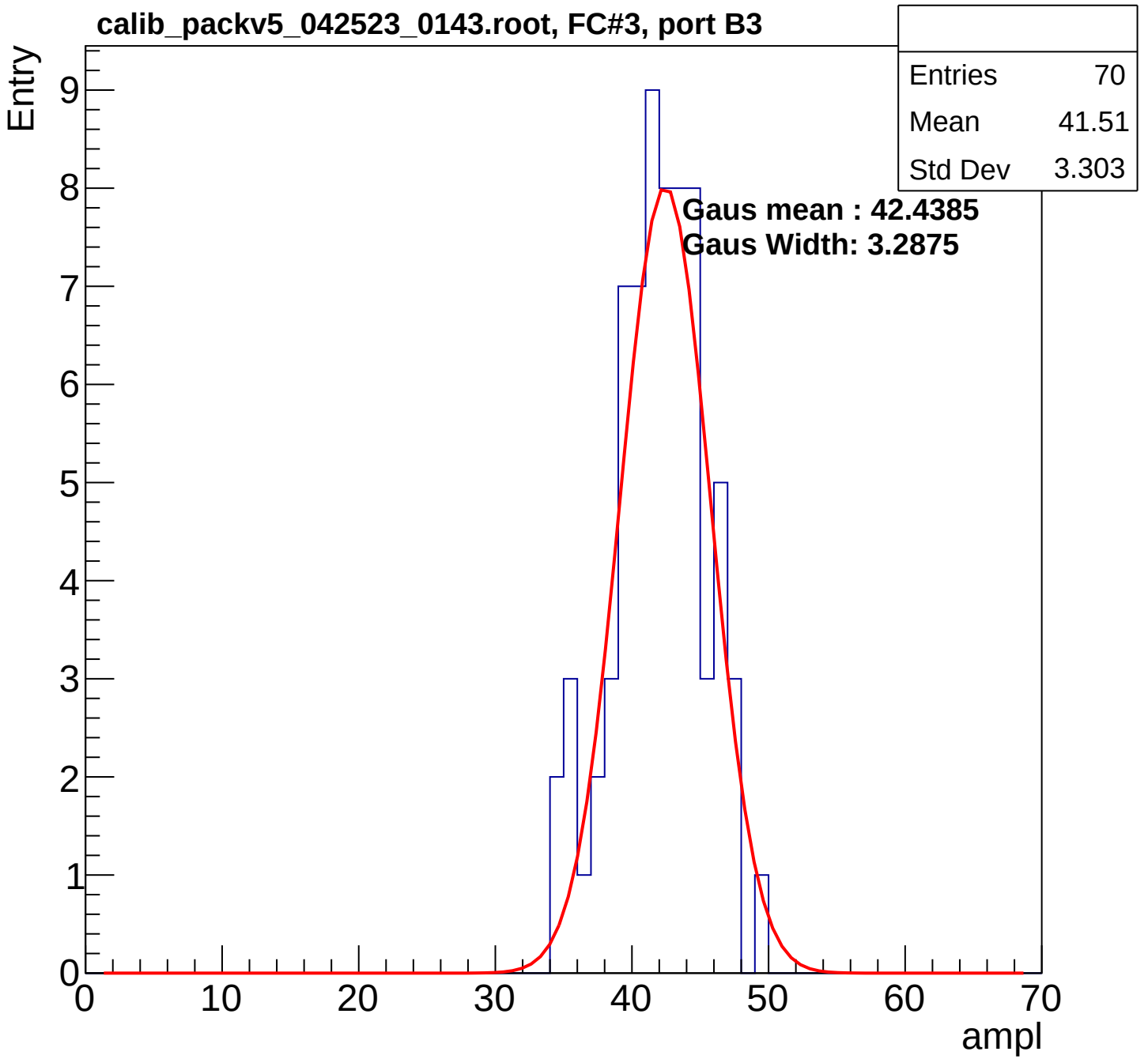
B1L000S, U3-ch7, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 41.51
Std Dev 3.303
Gaus mean : 42.4385
Gaus Width: 3.2875
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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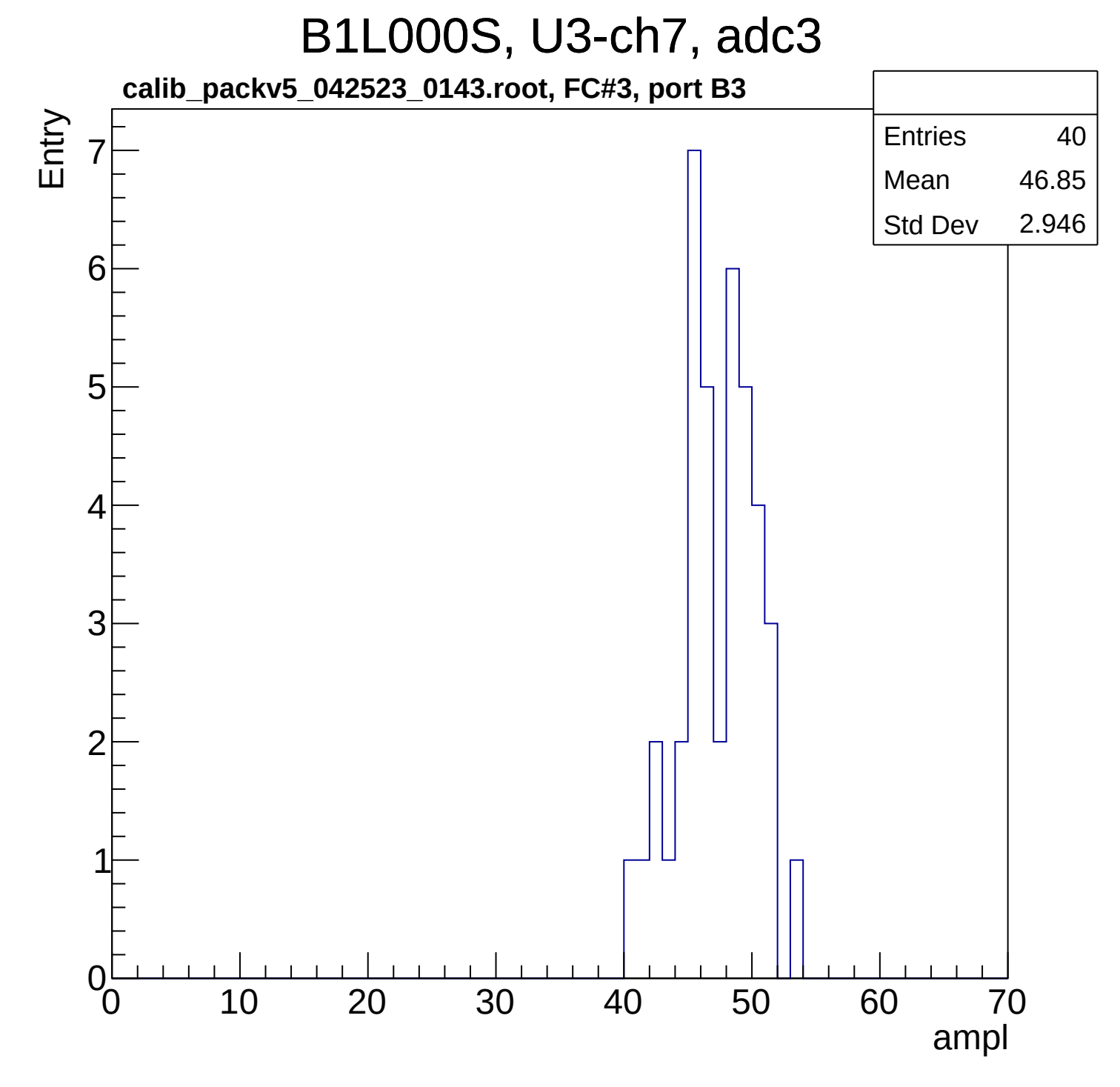
B1L000S, U3-ch7, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 46.85
Std Dev 2.946
Entry
ampl
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3
4
5
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7
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40
50
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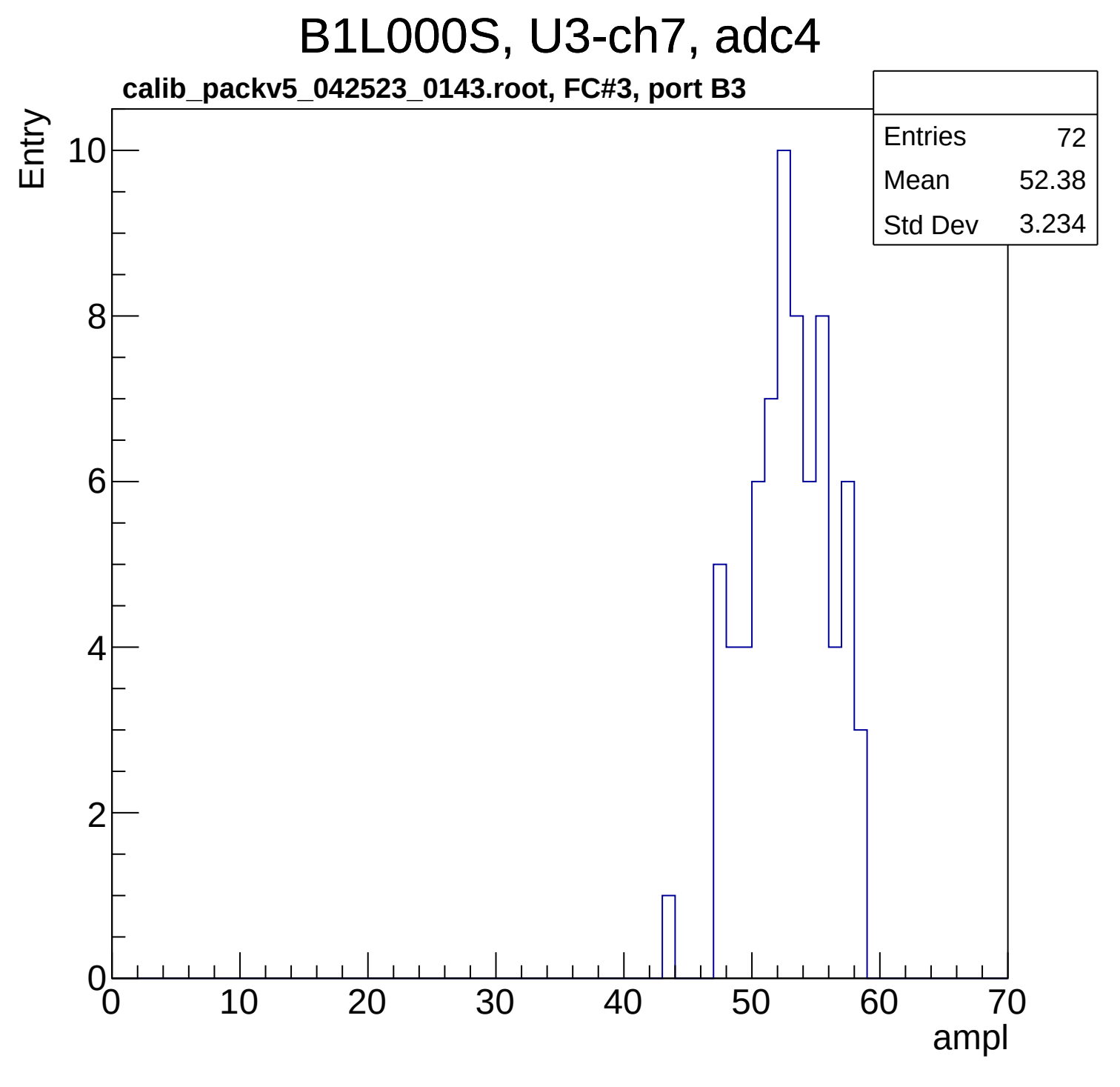
B1L000S, U3-ch7, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 52.38
Std Dev 3.234
Entry
ampl
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60
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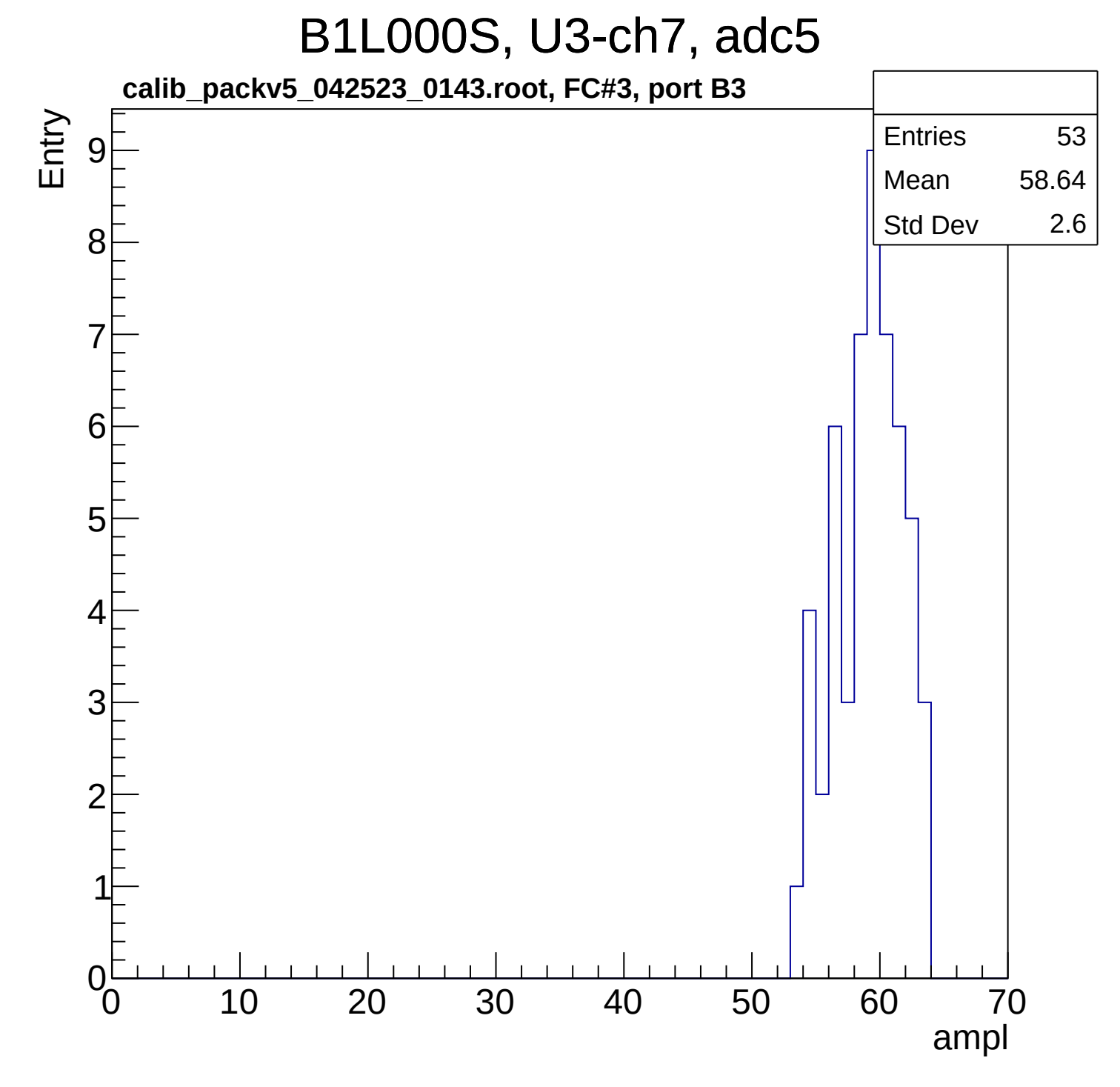
B1L000S, U3-ch7, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.64
Std Dev 2.6
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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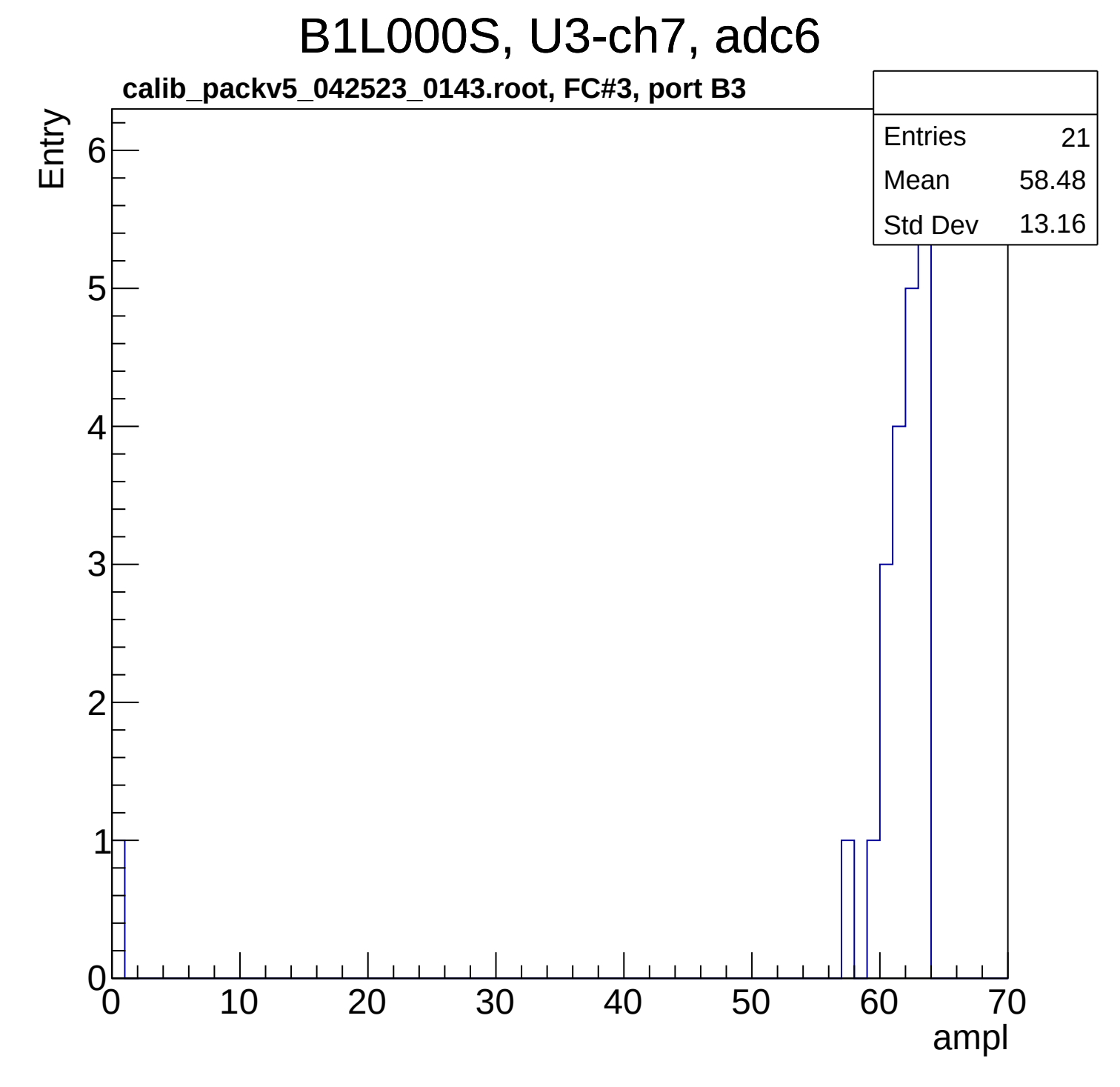
B1L000S, U3-ch7, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 58.48
Std Dev 13.16
Entry
0
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2
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4
5
6
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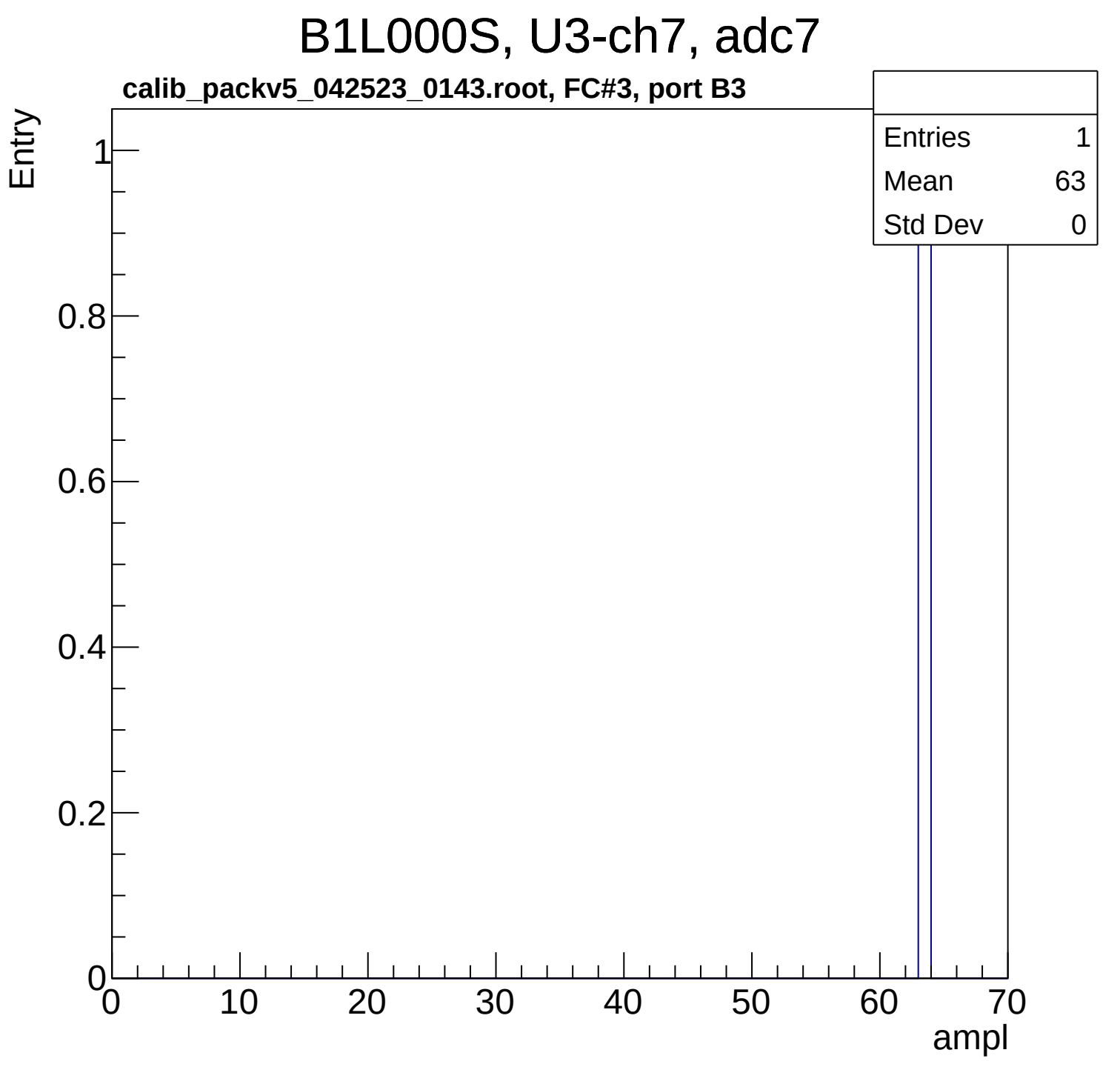
ampl


B1L000S, U3-ch7, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
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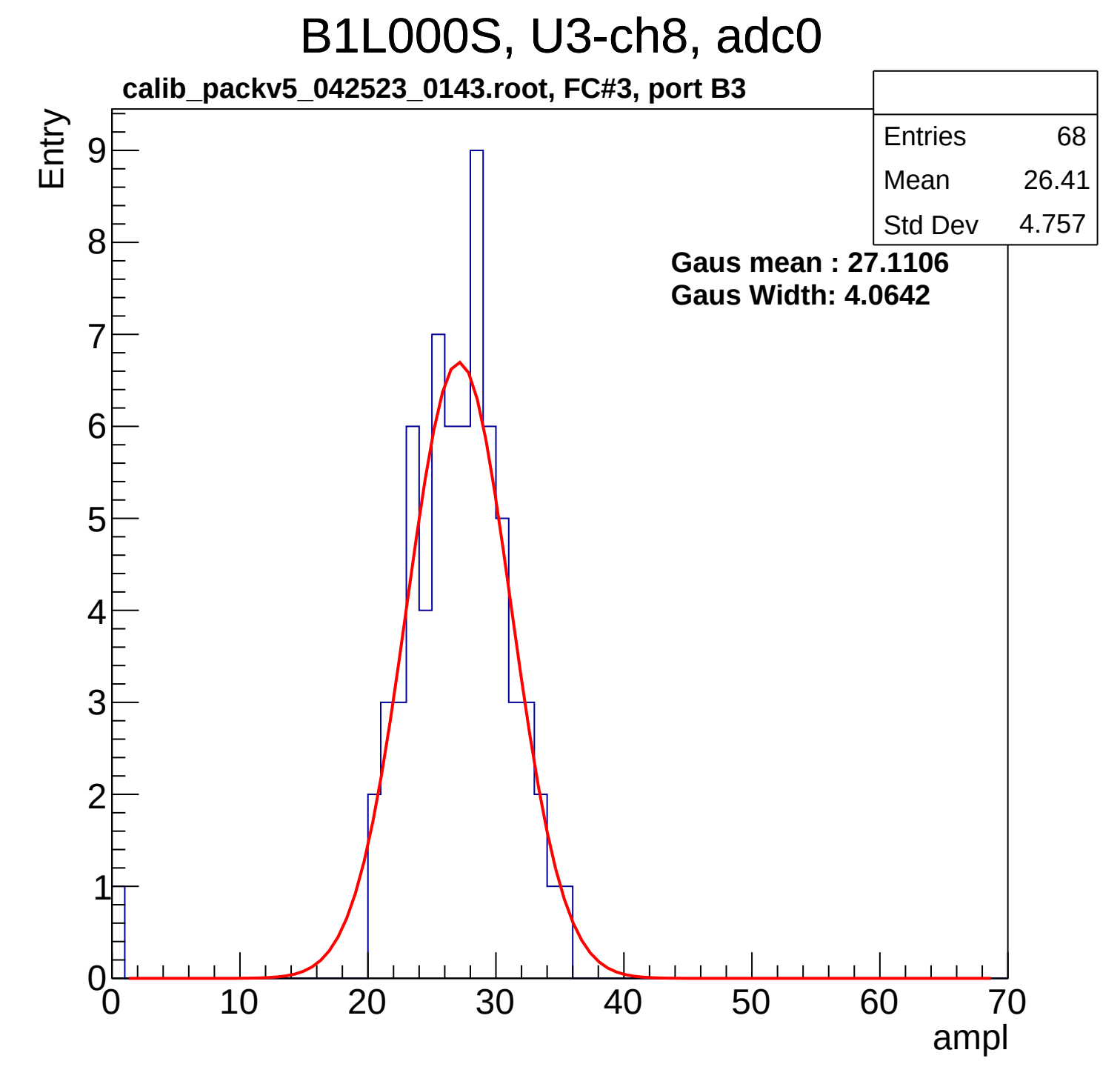
50
60
70
ampl


B1L000S, U3-ch8, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 26.41
Std Dev 4.757
Gaus mean : 27.1106
Gaus Width: 4.0642
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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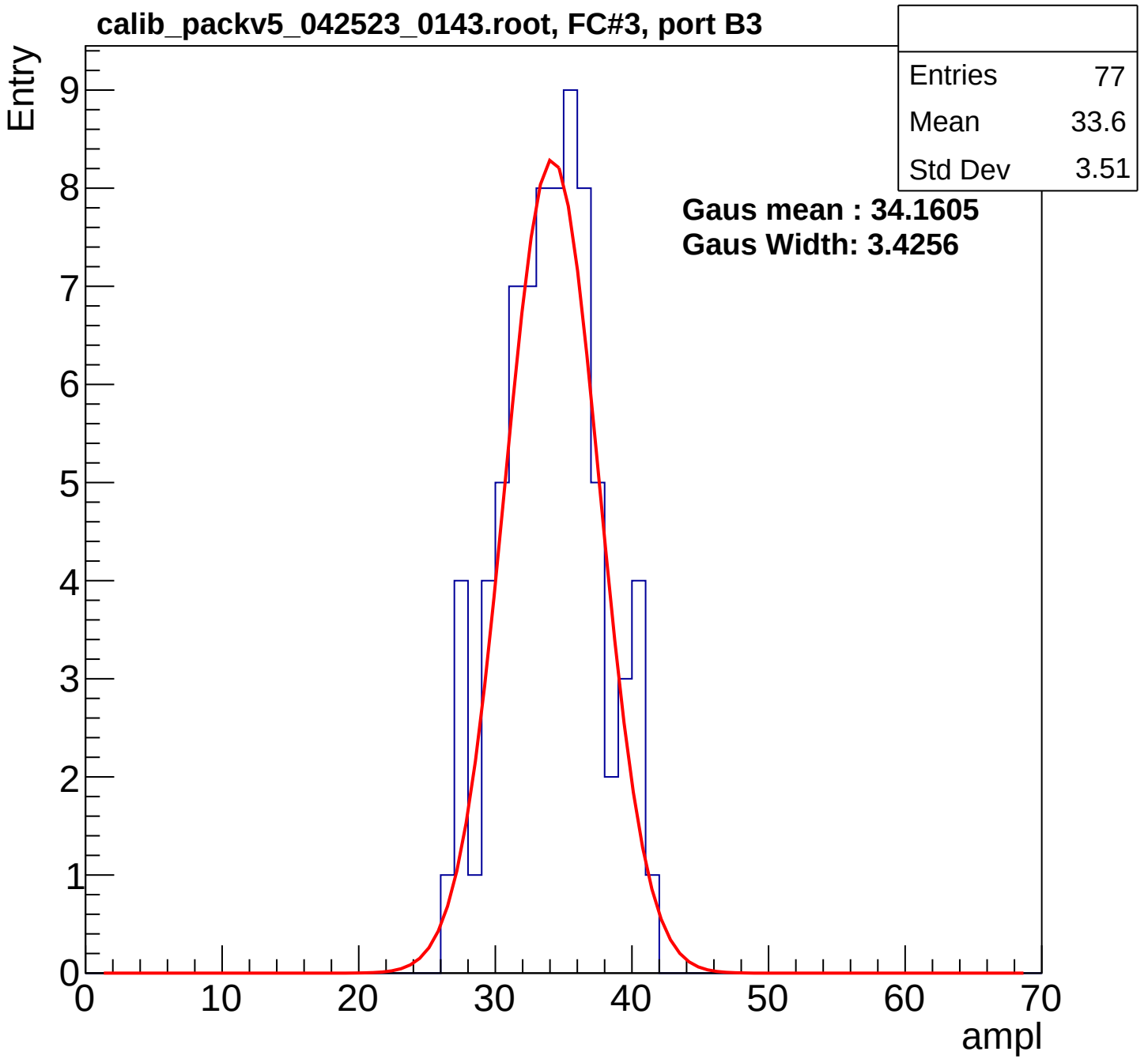
B1L000S, U3-ch8, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 33.6
Std Dev 3.51
Gaus mean : 34.1605
Gaus Width: 3.4256
Entry
ampl
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2
3
4
5
6
7
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0
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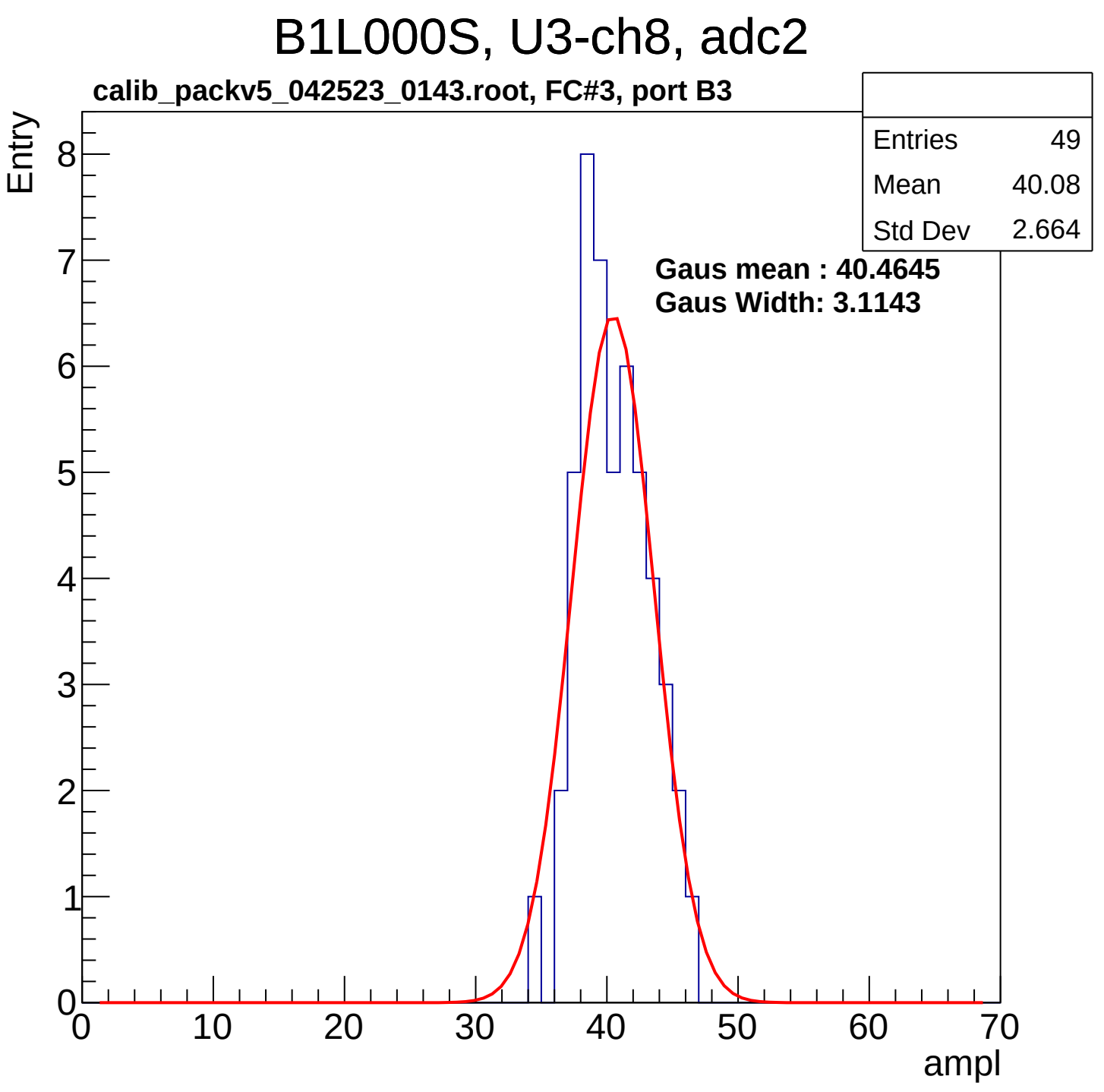
B1L000S, U3-ch8, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 40.08
Std Dev 2.664
Gaus mean : 40.4645
Gaus Width: 3.1143
Entry
ampl
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1
2
3
4
5
6
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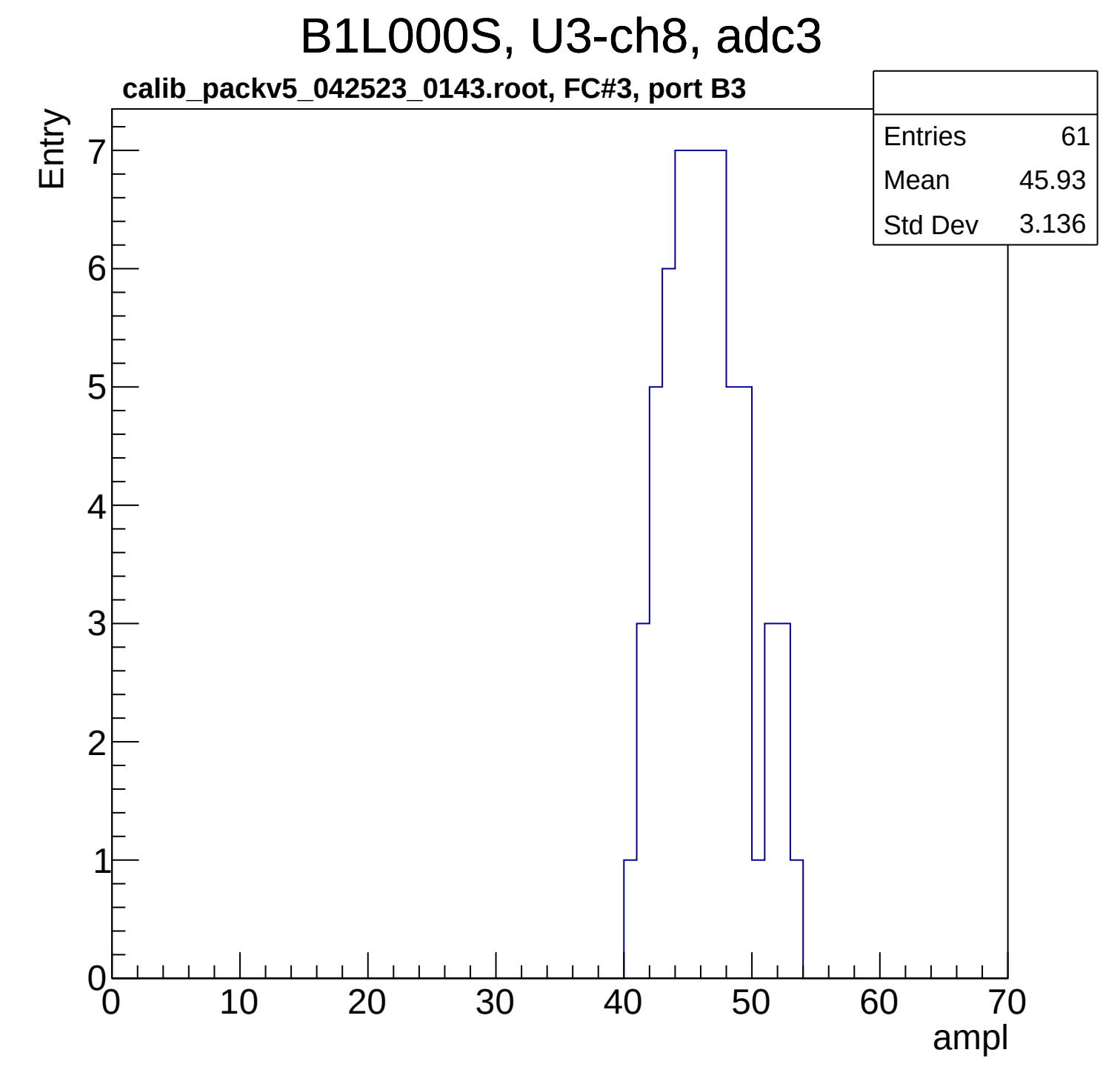
B1L000S, U3-ch8, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 45.93
Std Dev 3.136
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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60
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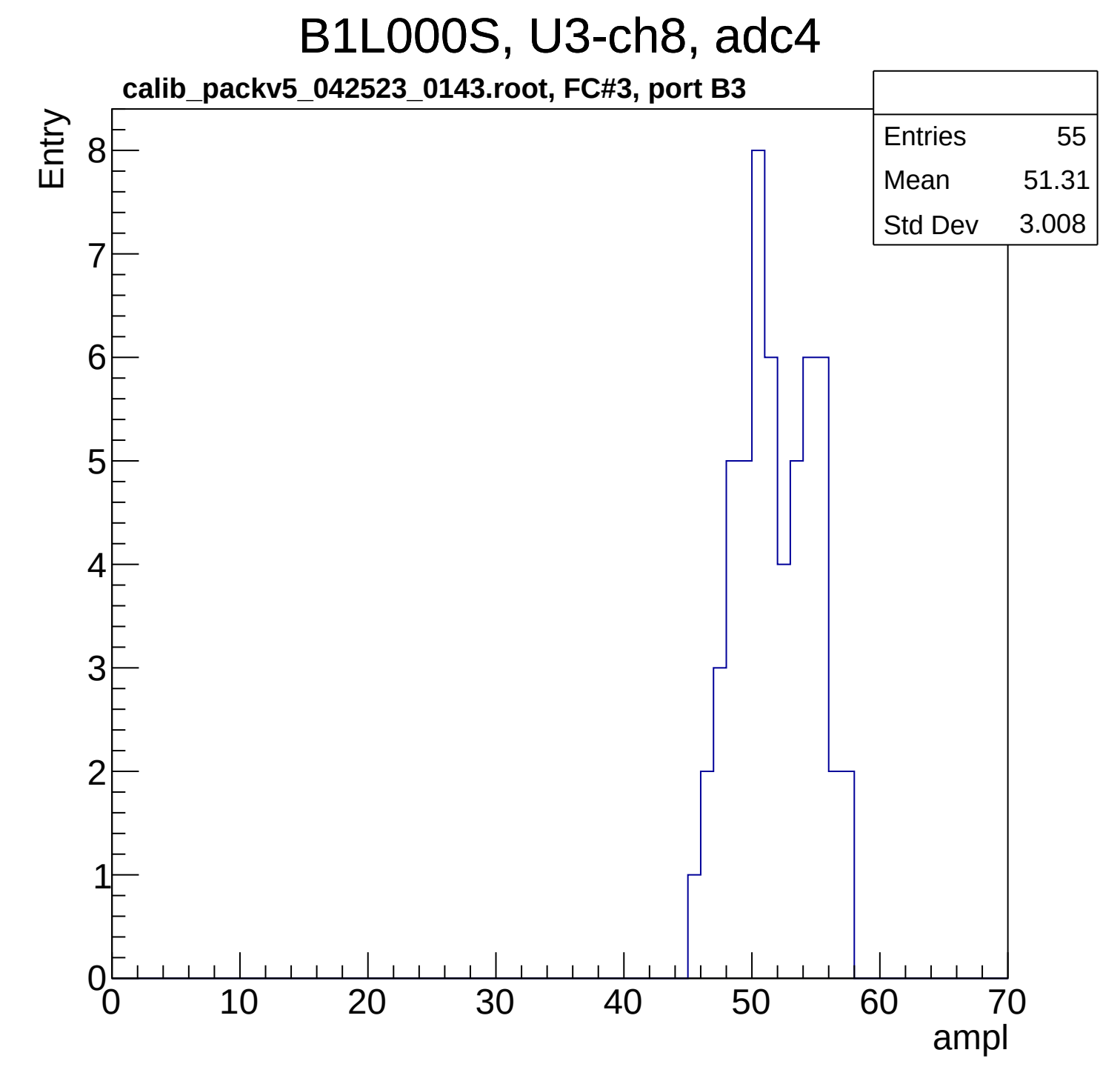
B1L000S, U3-ch8, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 51.31
Std Dev 3.008
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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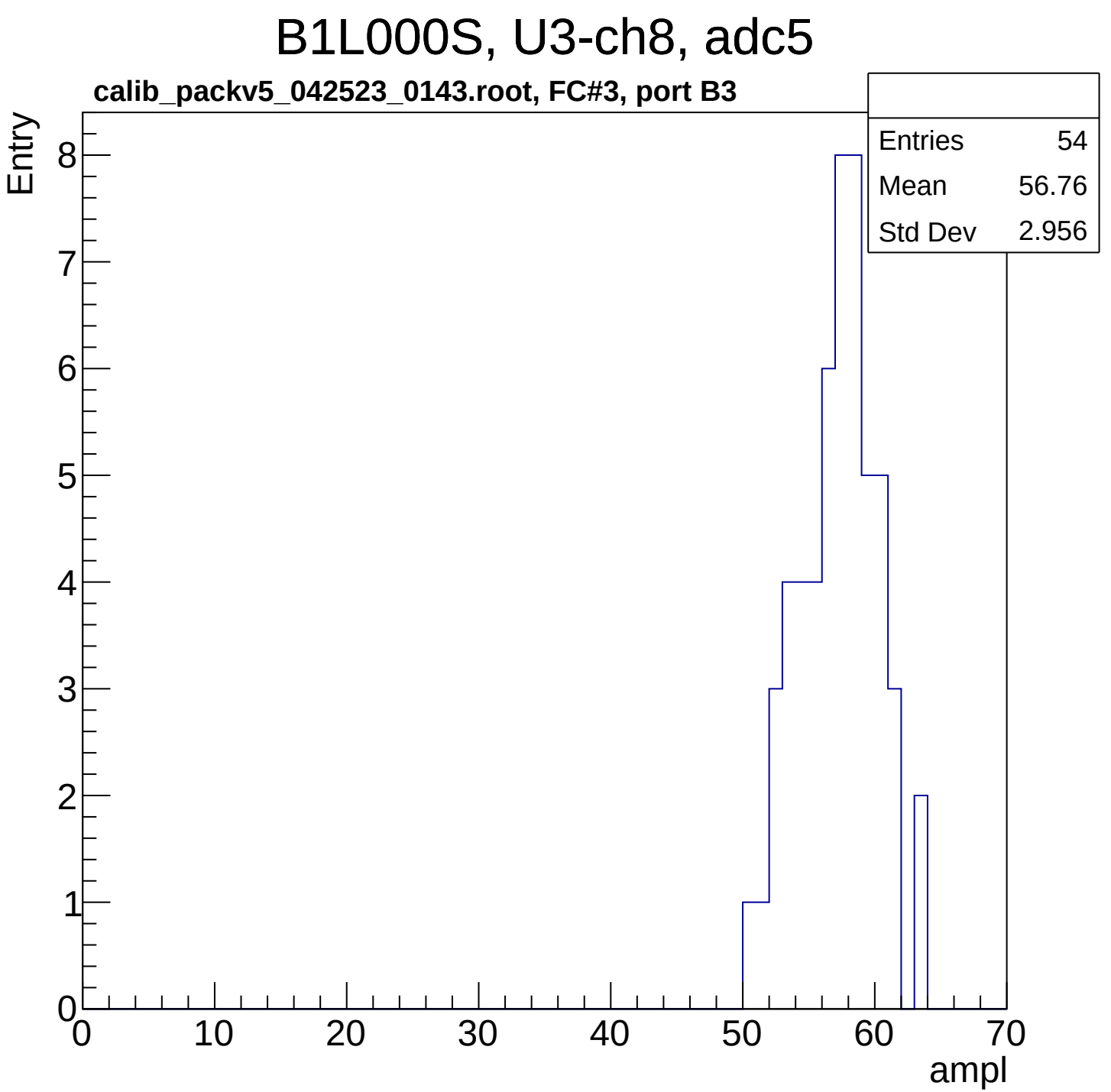
B1L000S, U3-ch8, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 56.76
Std Dev 2.956
Entry
ampl
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2
3
4
5
6
7
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0
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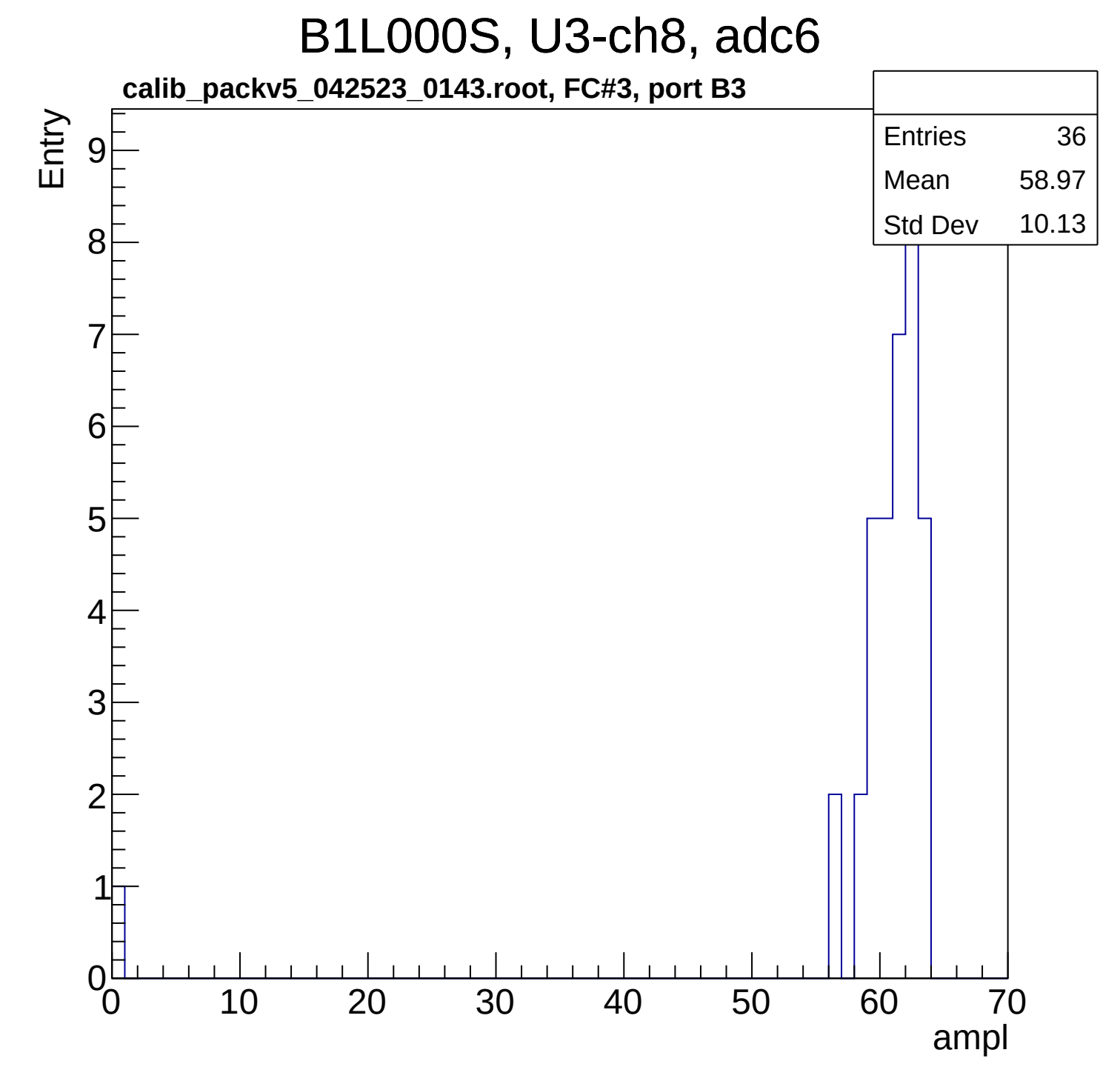
B1L000S, U3-ch8, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 36
Mean 58.97
Std Dev 10.13
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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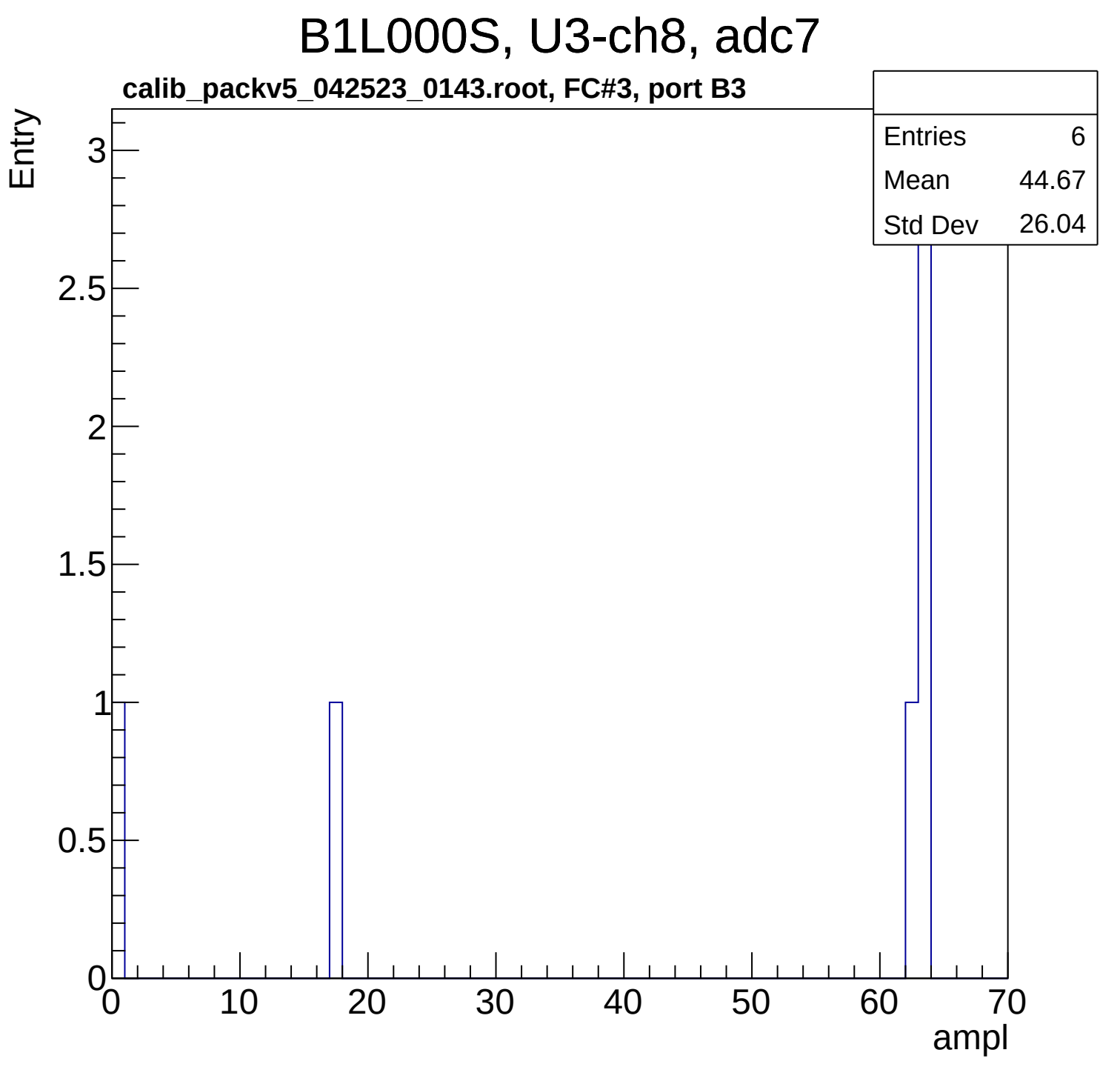
B1L000S, U3-ch8, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 44.67
Std Dev 26.04
Entry
ampl
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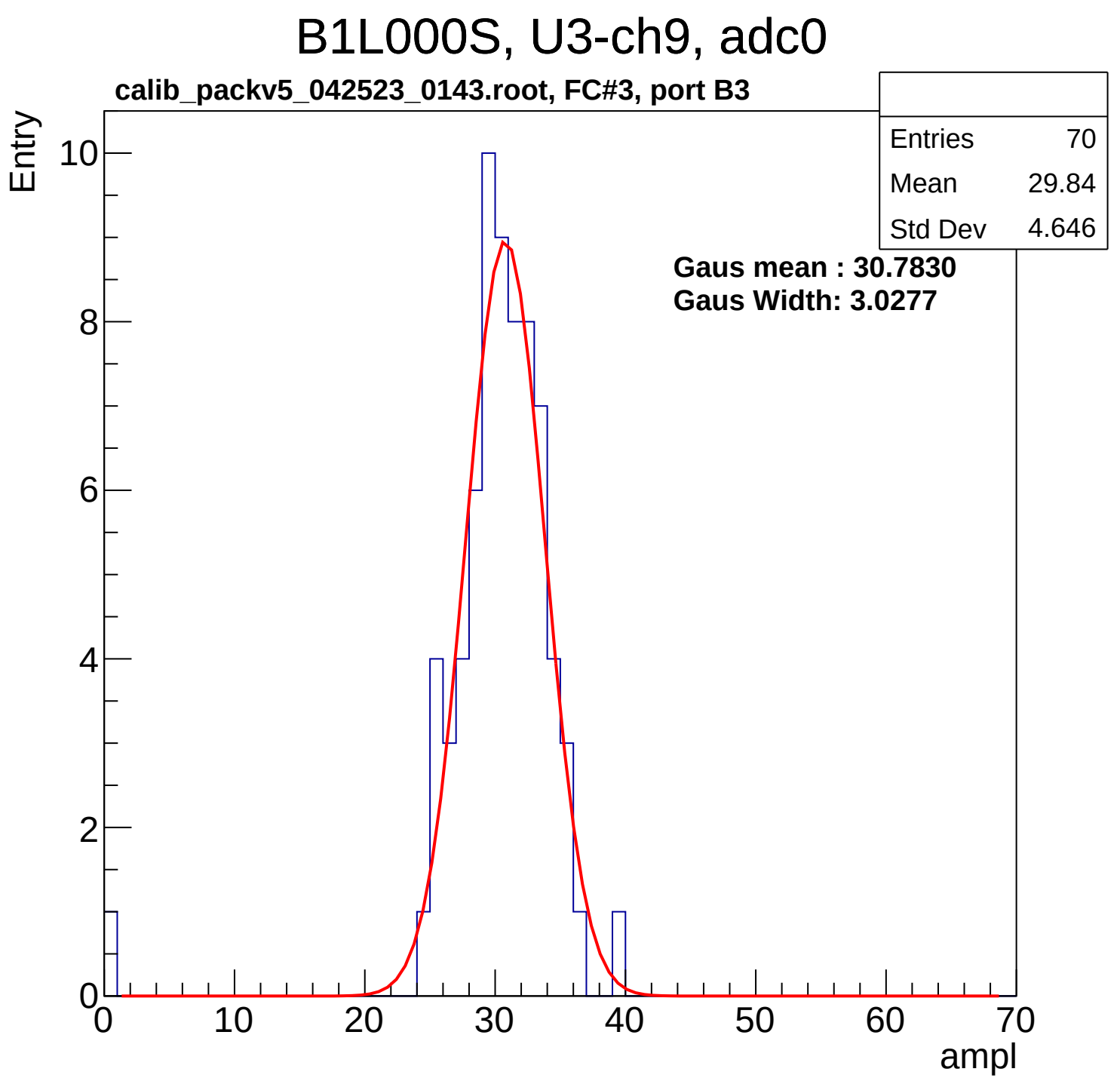
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch9, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 29.84
Std Dev 4.646
Gaus mean : 30.7830
Gaus Width: 3.0277
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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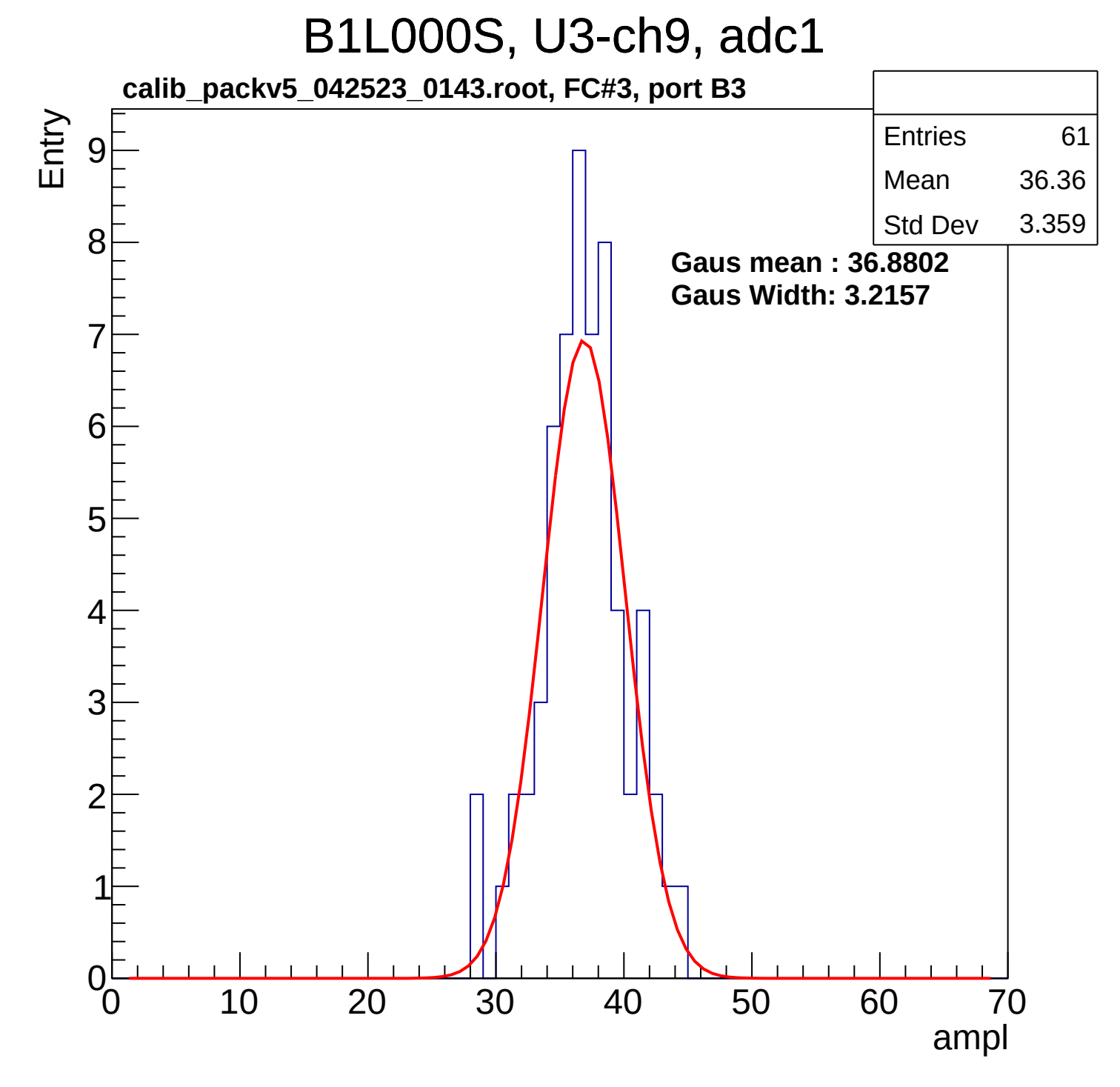
B1L000S, U3-ch9, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 36.36
Std Dev 3.359
Gaus mean : 36.8802
Gaus Width: 3.2157
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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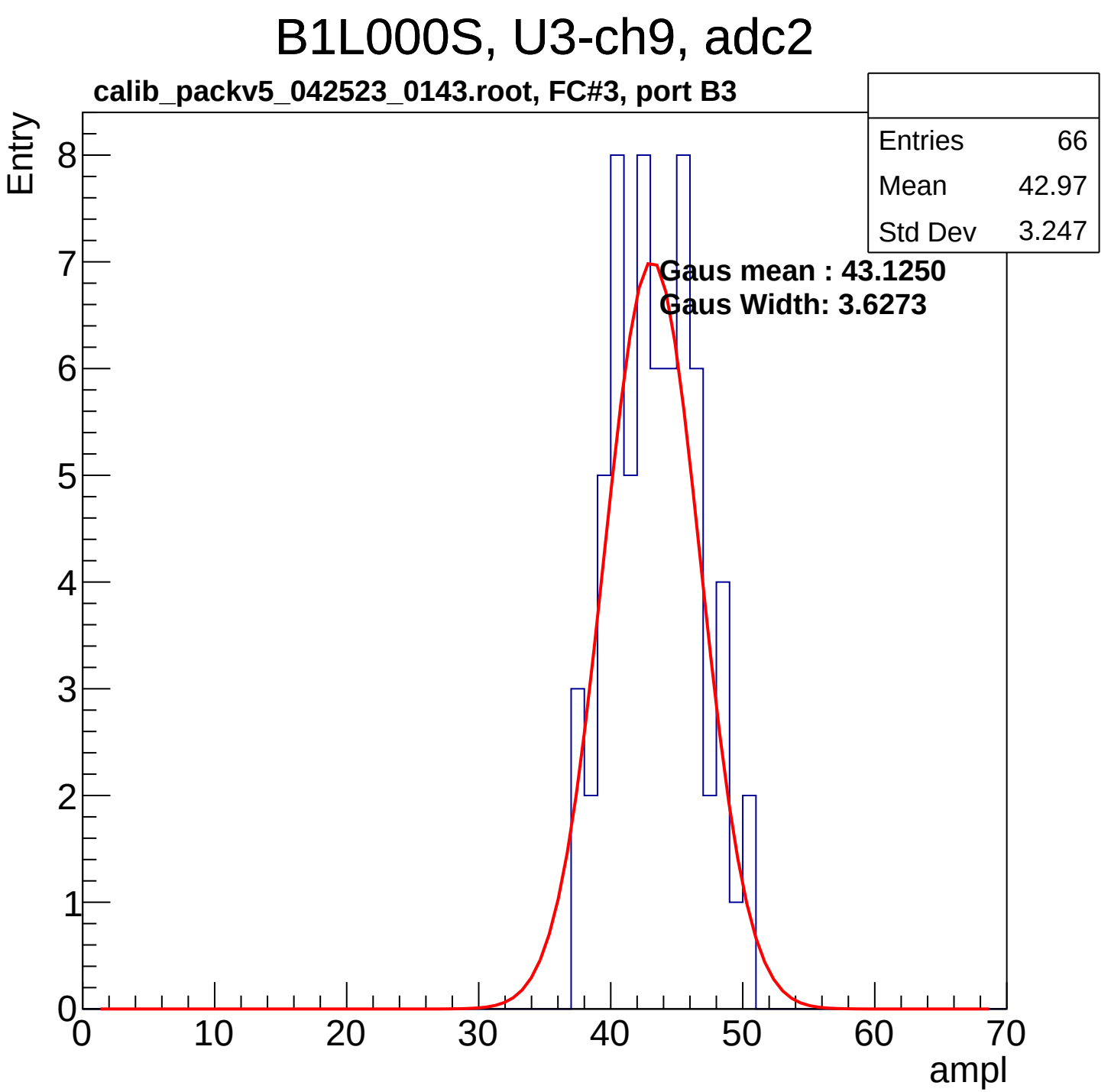
B1L000S, U3-ch9, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 42.97
Std Dev 3.247
Gaus mean : 43.1250
Gaus Width: 3.6273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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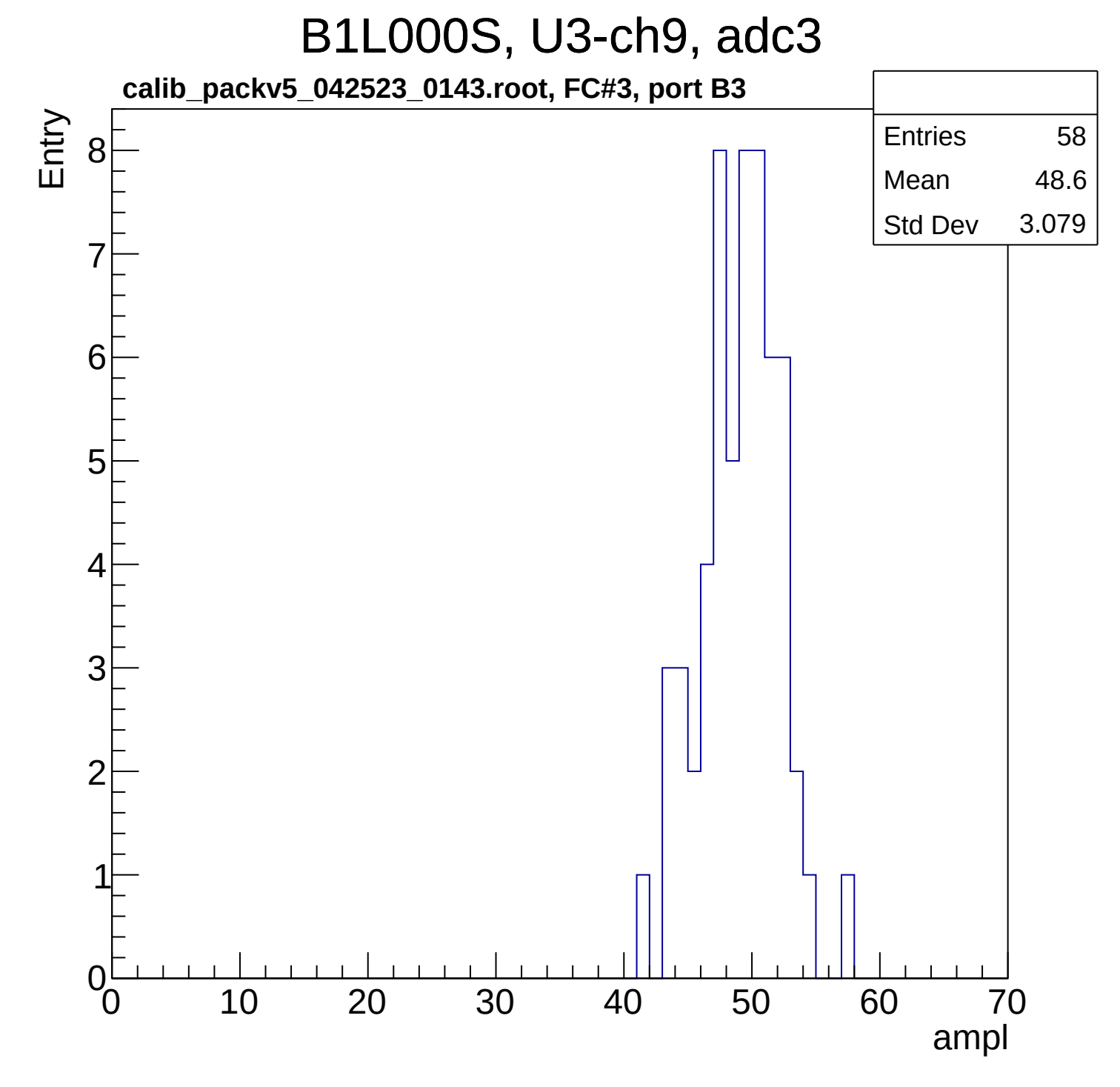
B1L000S, U3-ch9, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 48.6
Std Dev 3.079
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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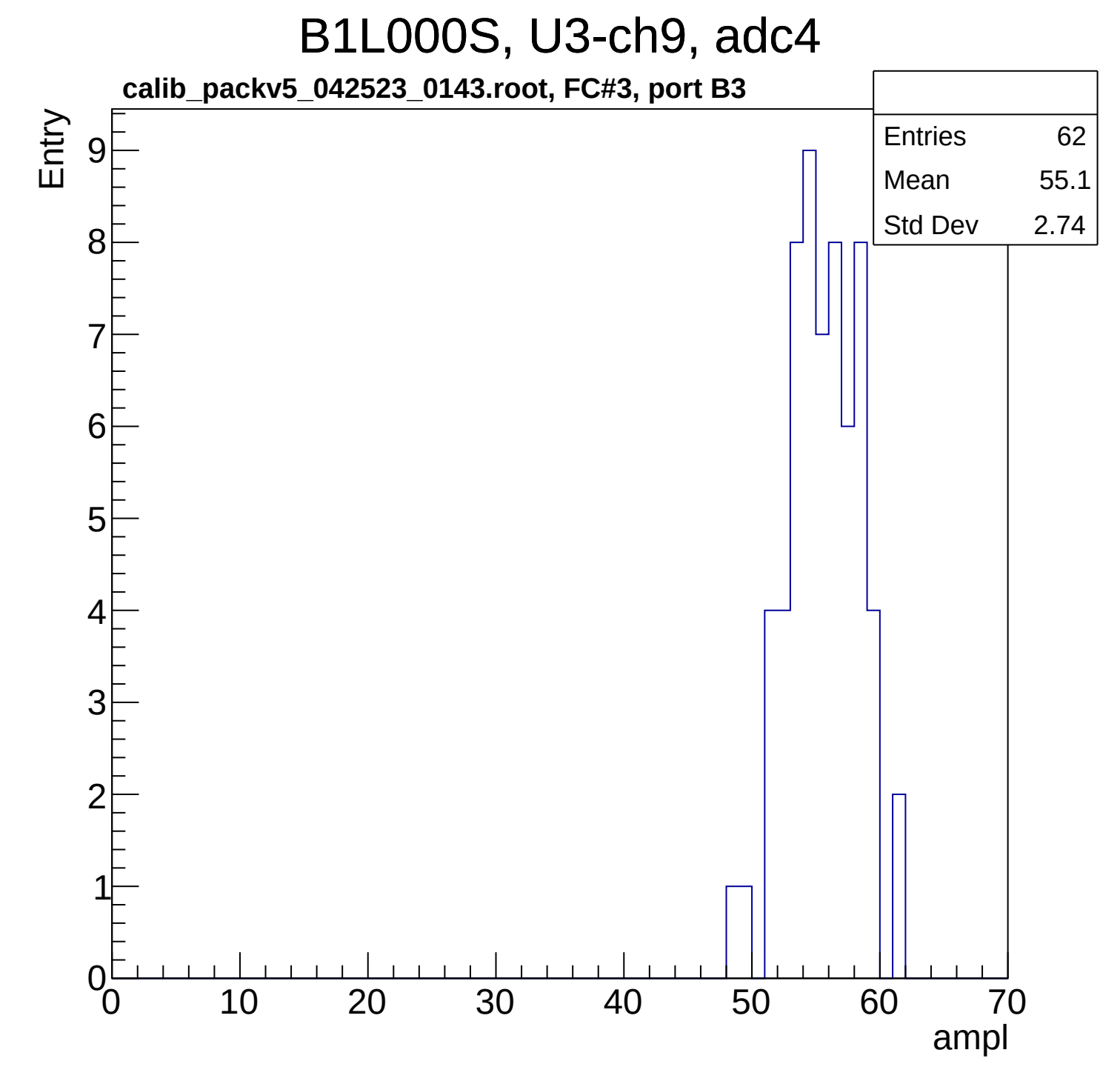
B1L000S, U3-ch9, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 55.1
Std Dev 2.74
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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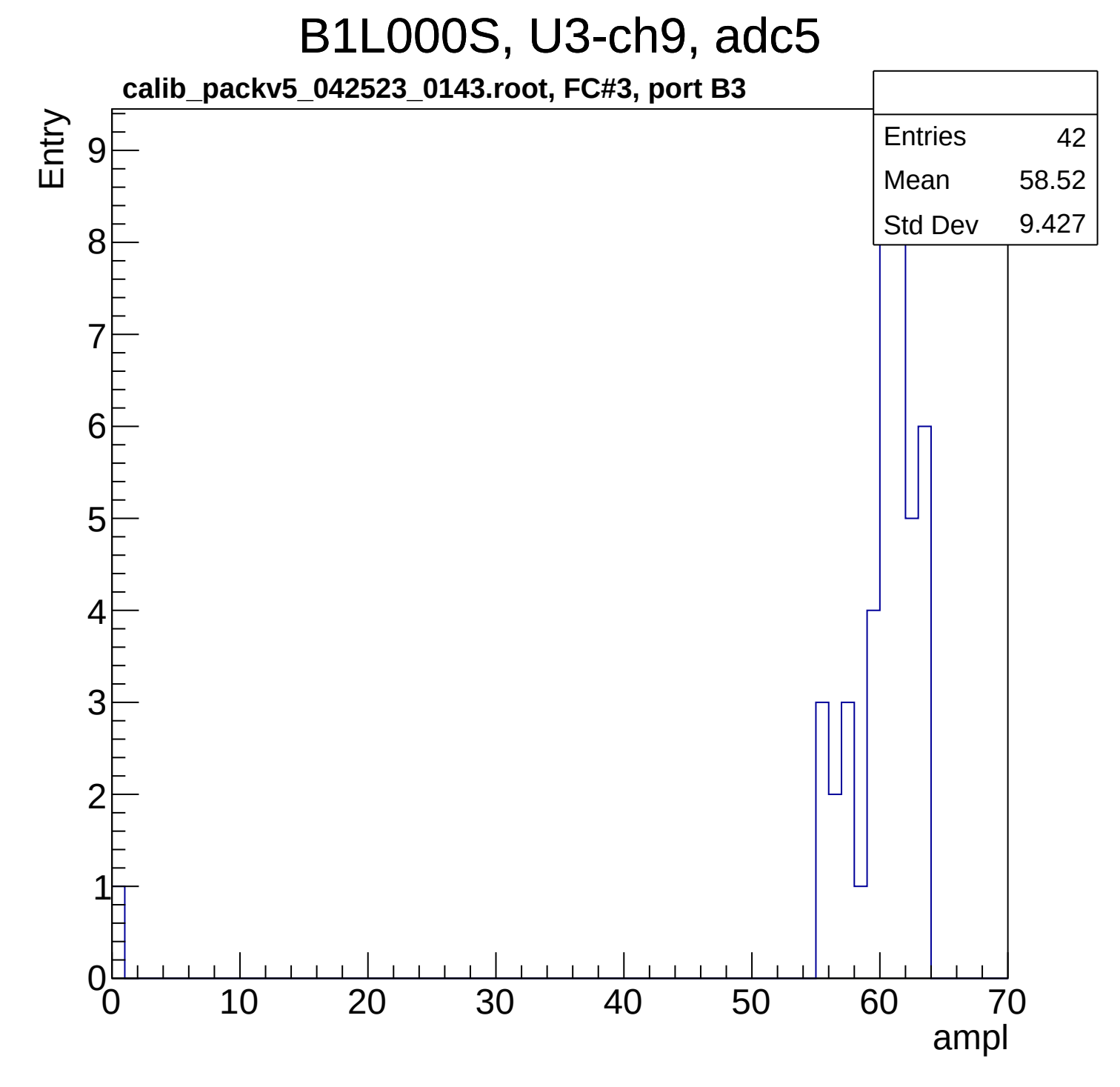
B1L000S, U3-ch9, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 58.52
Std Dev 9.427
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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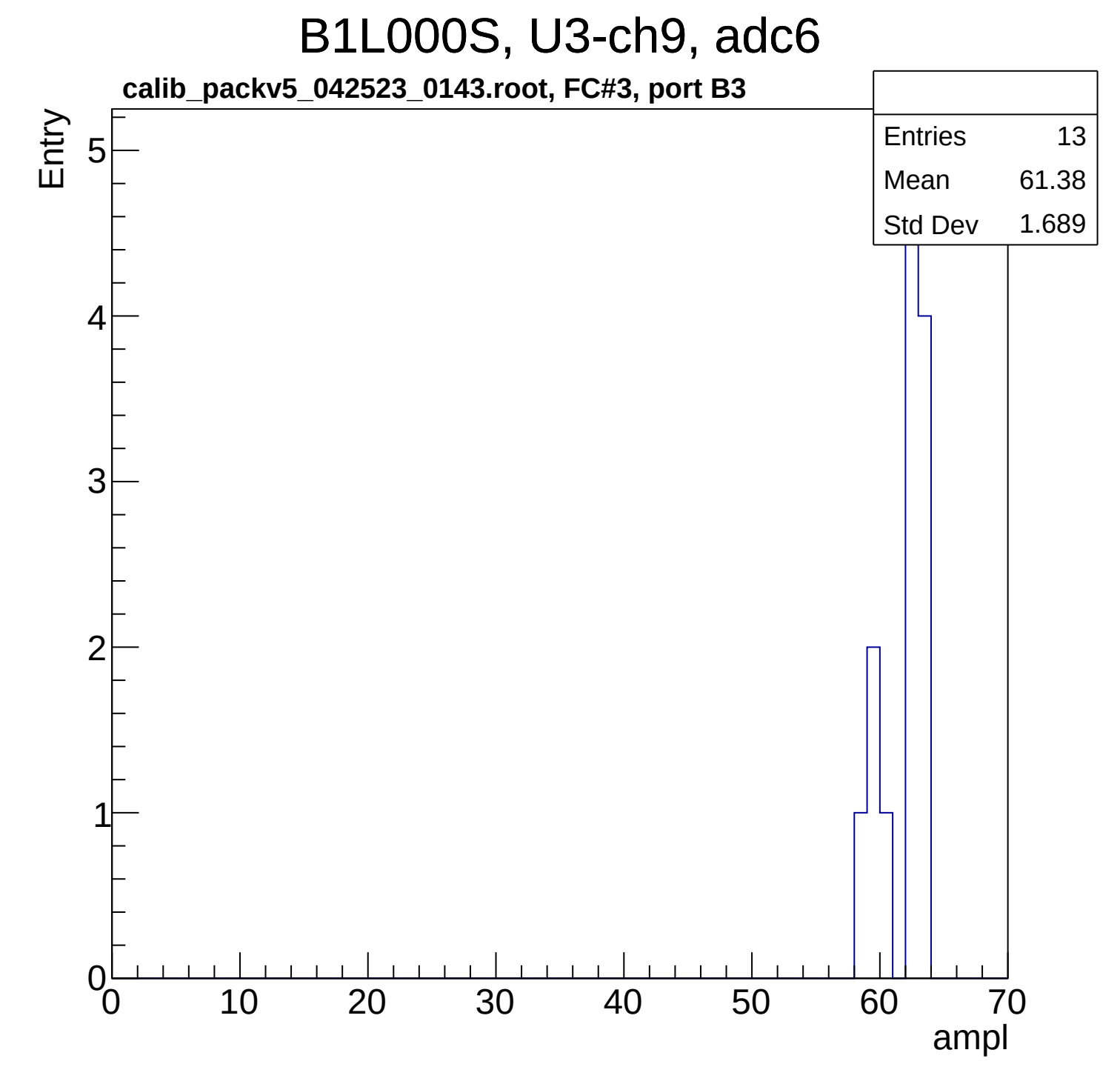
B1L000S, U3-ch9, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.38
Std Dev 1.689
Entry
ampl
0
1
2
3
4
5
0
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20
30
40
50
60
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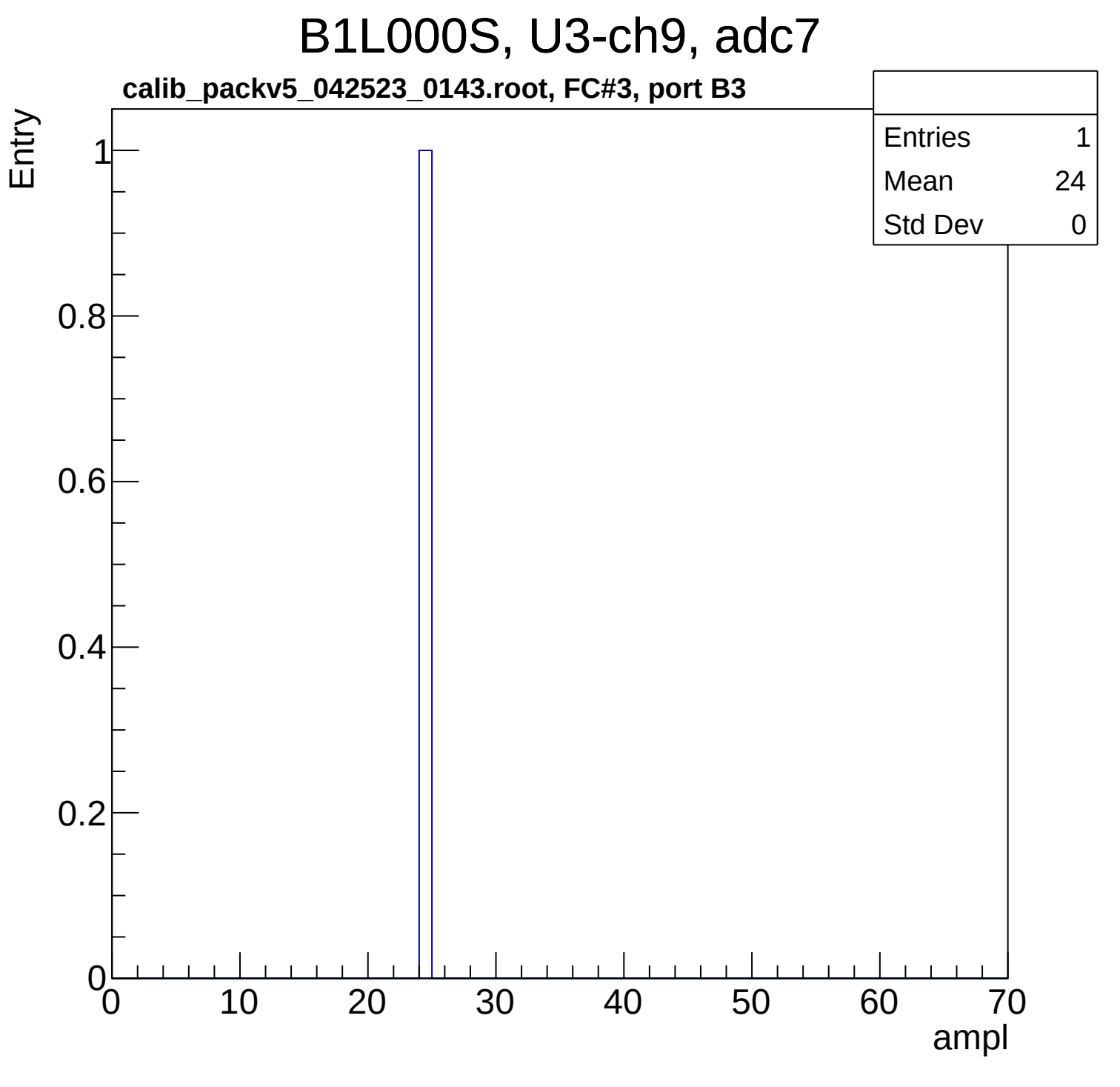
B1L000S, U3-ch9, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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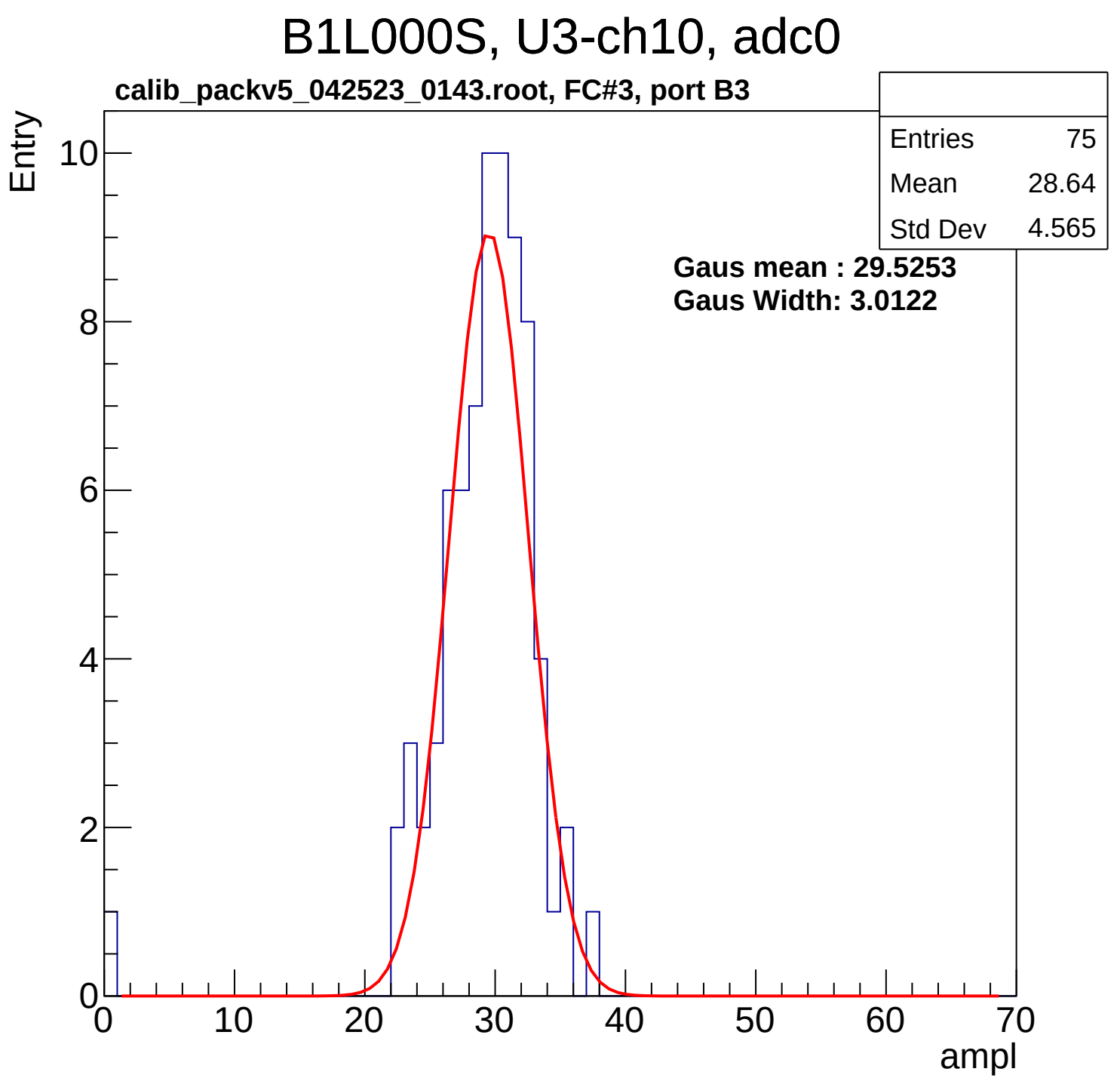
50
60
70
ampl


B1L000S, U3-ch10, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 28.64
Std Dev 4.565
Gaus mean : 29.5253
Gaus Width: 3.0122
Entry
ampl
0
2
4
6
8
10
0
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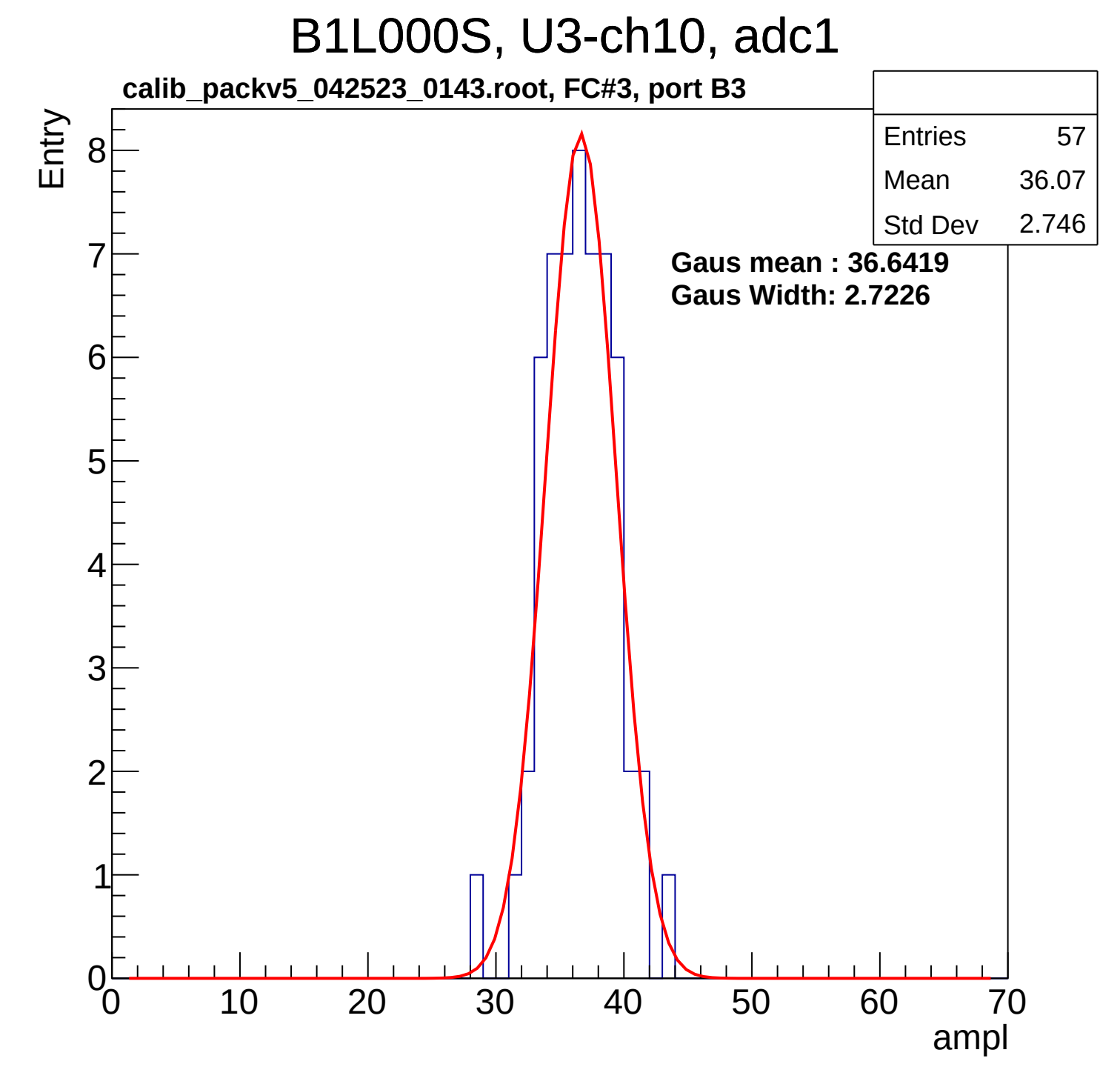
20
30
40
50
60
70


B1L000S, U3-ch10, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 36.07
Std Dev 2.746
Gaus mean : 36.6419
Gaus Width: 2.7226
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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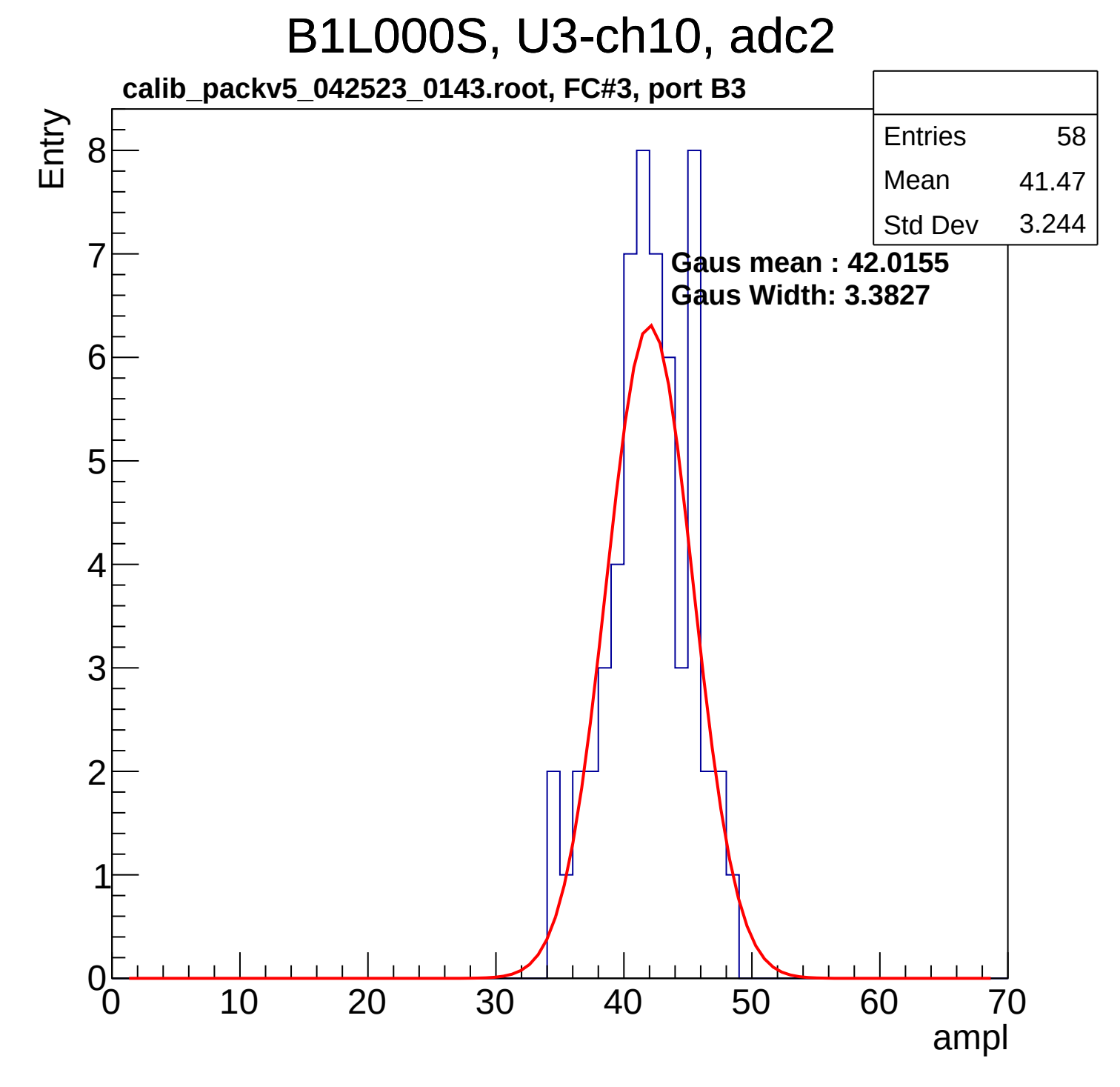
B1L000S, U3-ch10, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 41.47
Std Dev 3.244
Gaus mean : 42.0155
Gaus Width: 3.3827
Entry
ampl
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1
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3
4
5
6
7
8
0
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60
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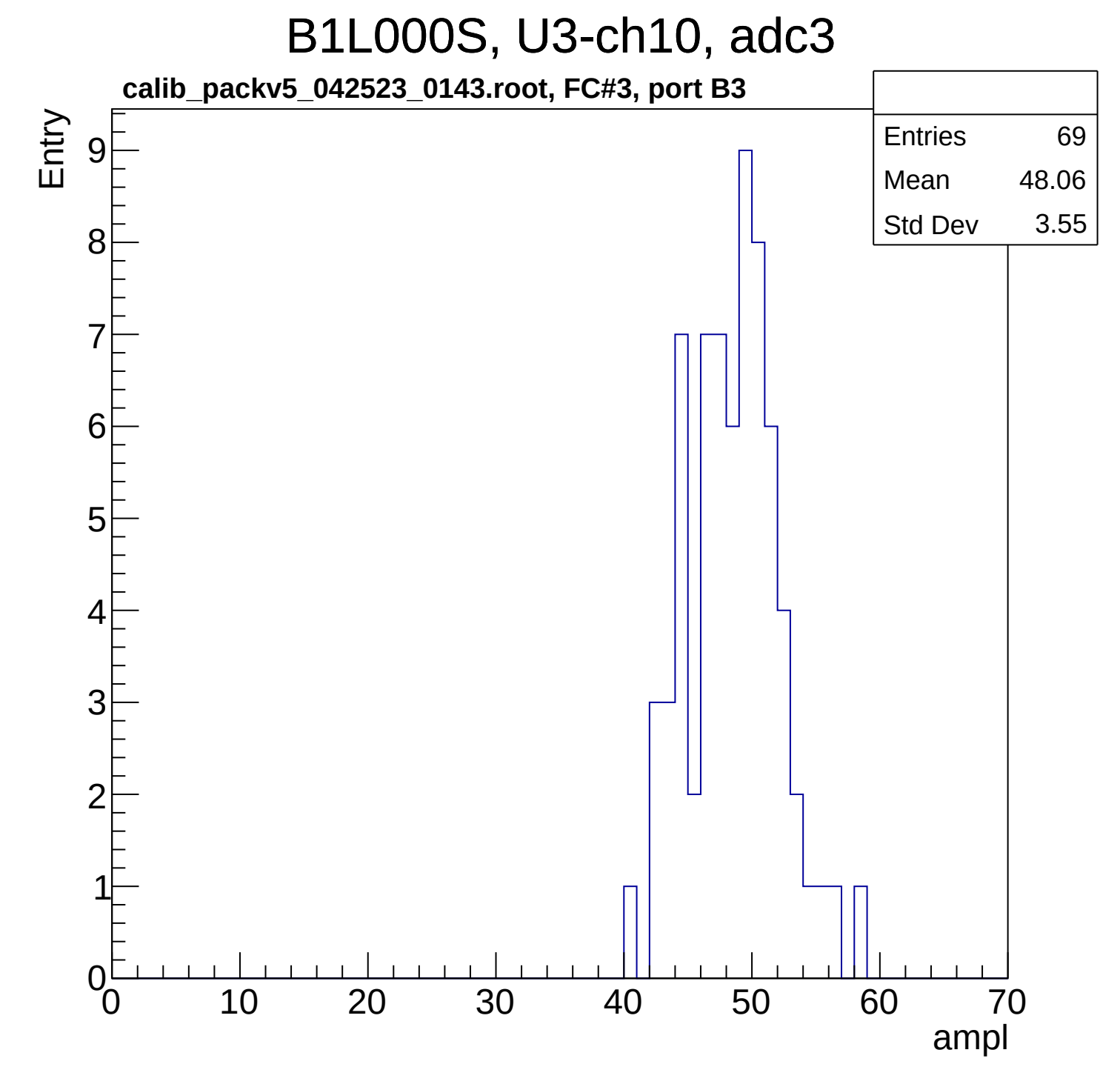
B1L000S, U3-ch10, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 48.06
Std Dev 3.55
Entry
ampl
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4
5
6
7
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0
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60
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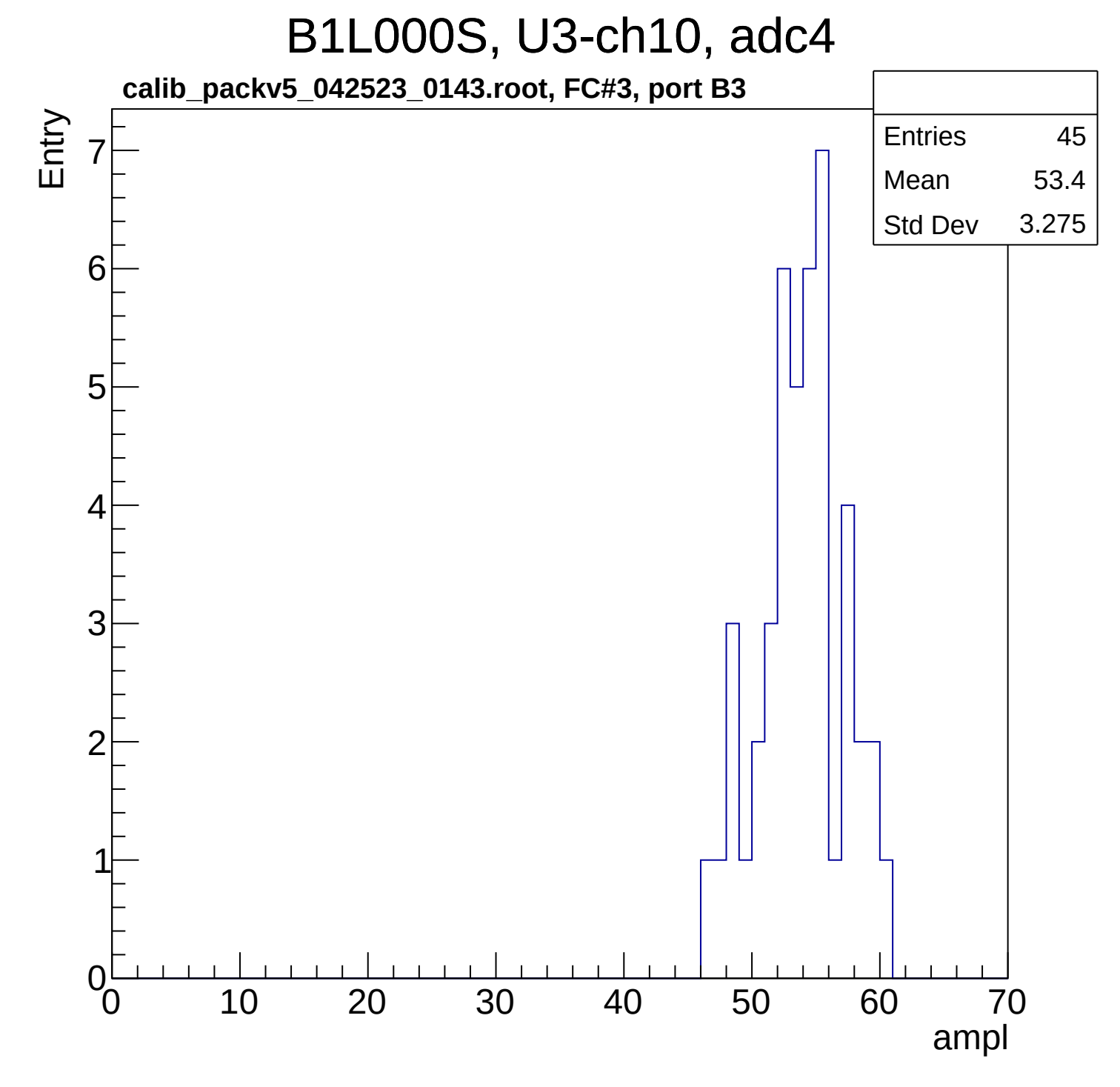
B1L000S, U3-ch10, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 53.4
Std Dev 3.275
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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50
60
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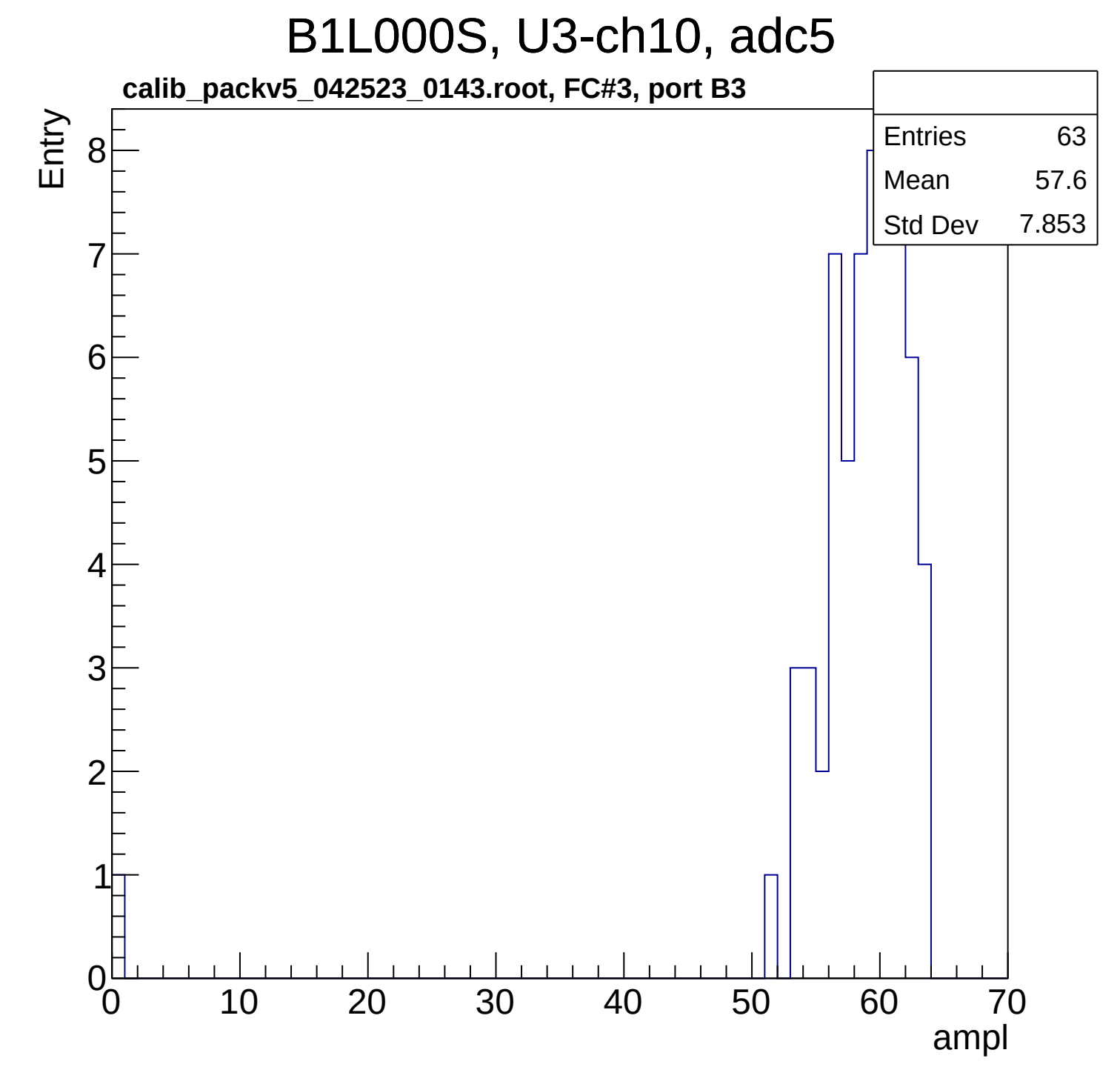
B1L000S, U3-ch10, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 57.6
Std Dev 7.853
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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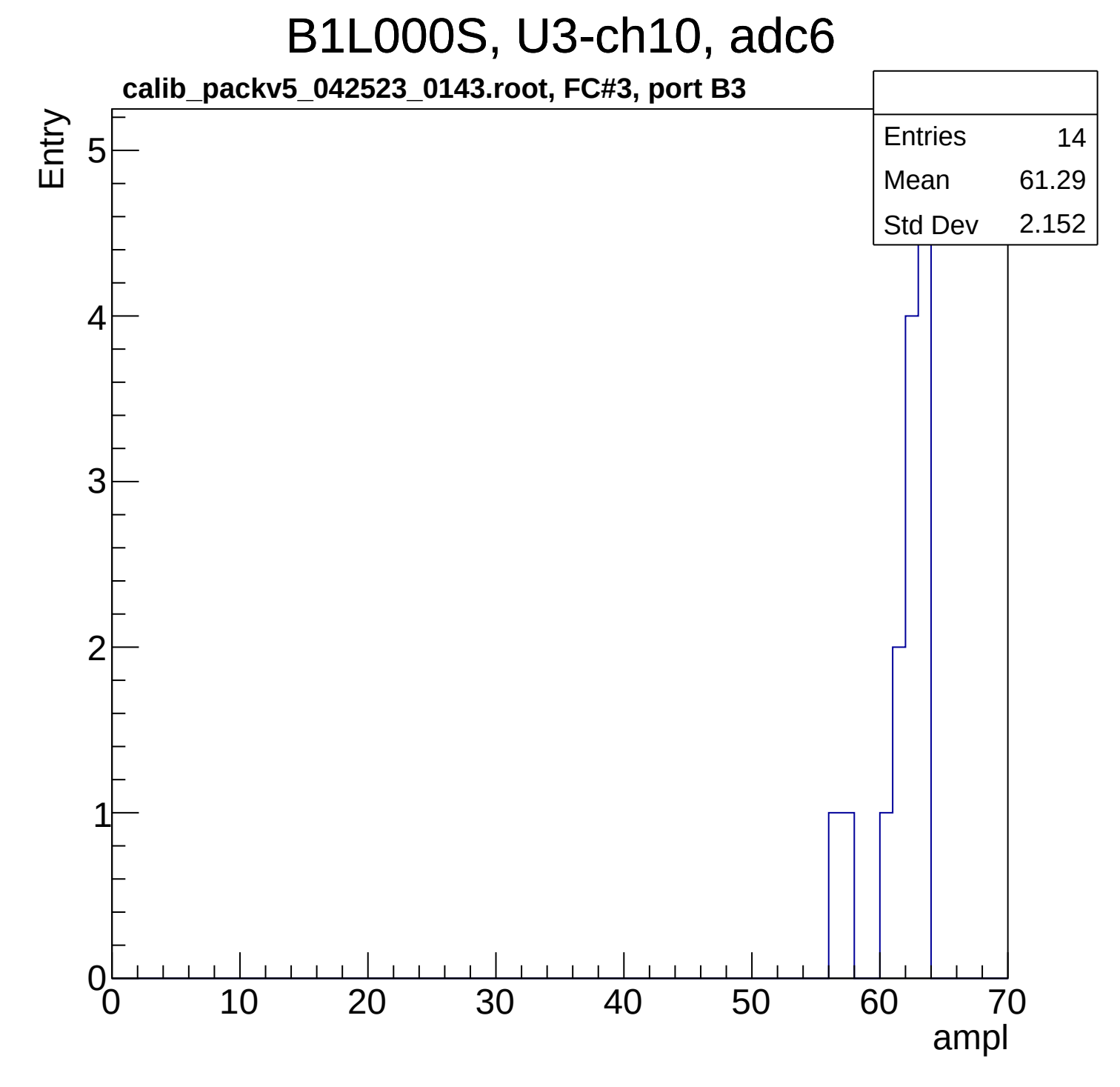
B1L000S, U3-ch10, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 61.29
Std Dev 2.152
Entry
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1
2
3
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5
0
10
20
30
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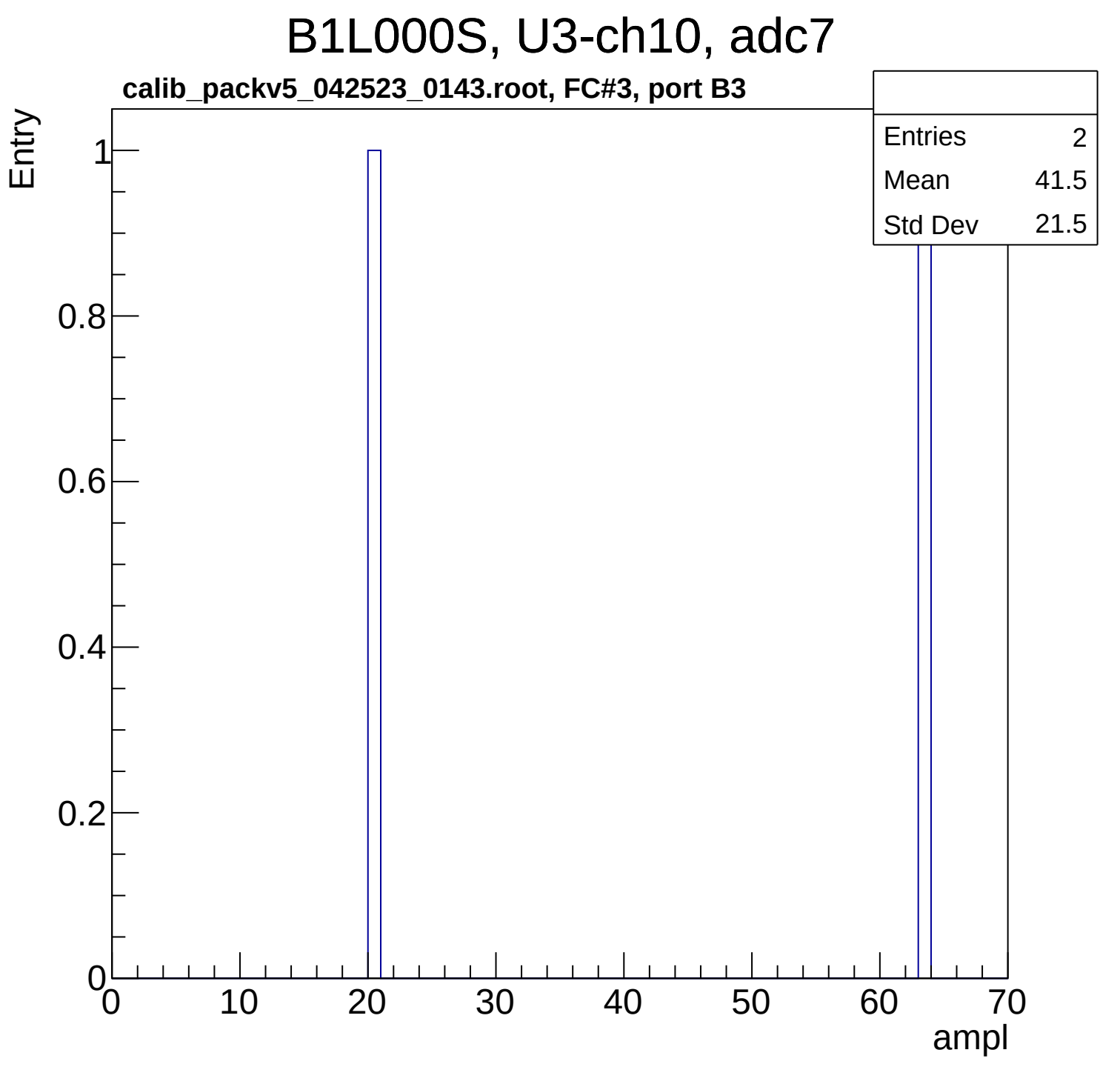
50
60
70
ampl


B1L000S, U3-ch10, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 41.5
Std Dev 21.5
Entry
ampl
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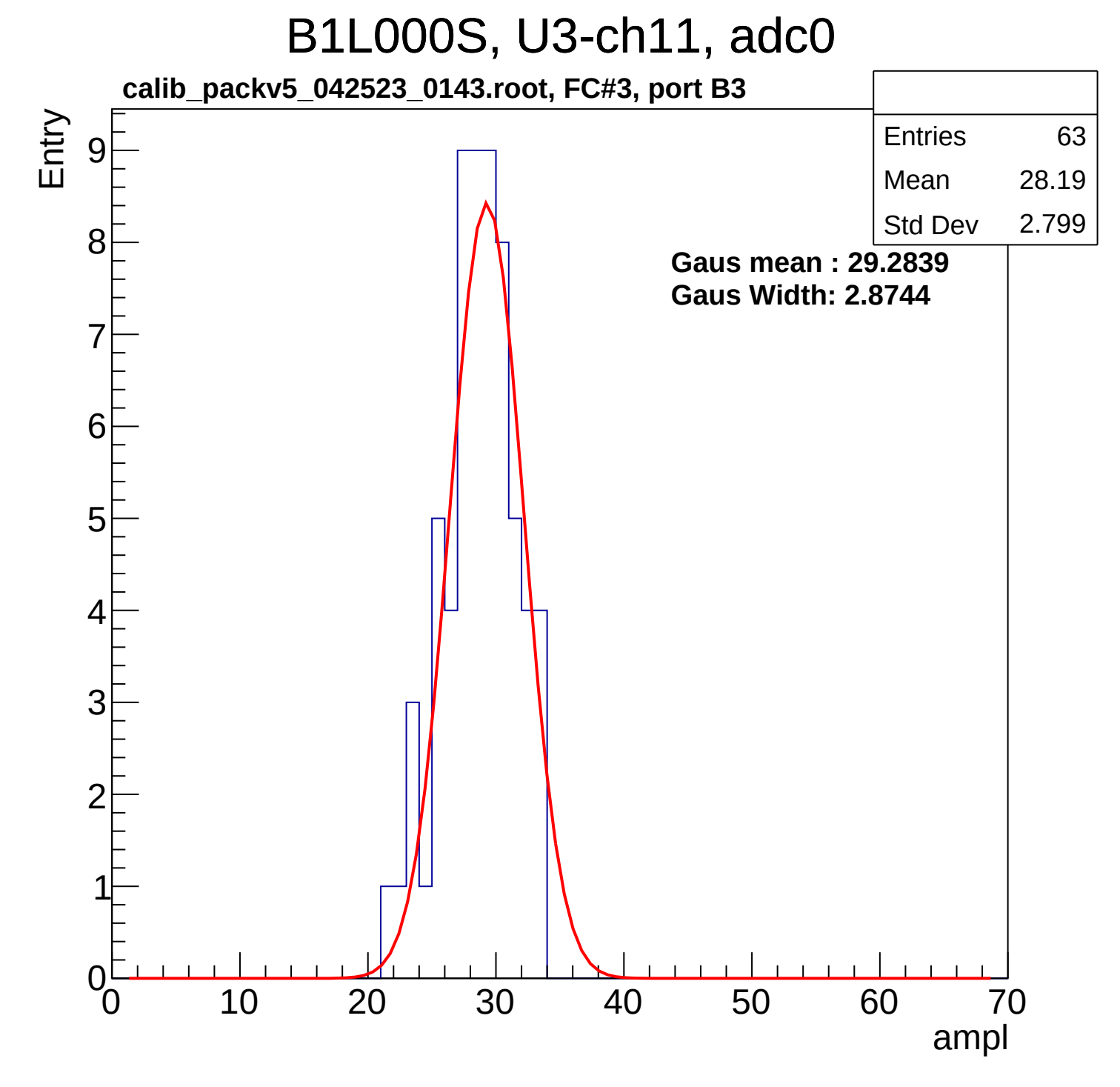
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U3-ch11, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 28.19
Std Dev 2.799
Gaus mean : 29.2839
Gaus Width: 2.8744
Entry
ampl
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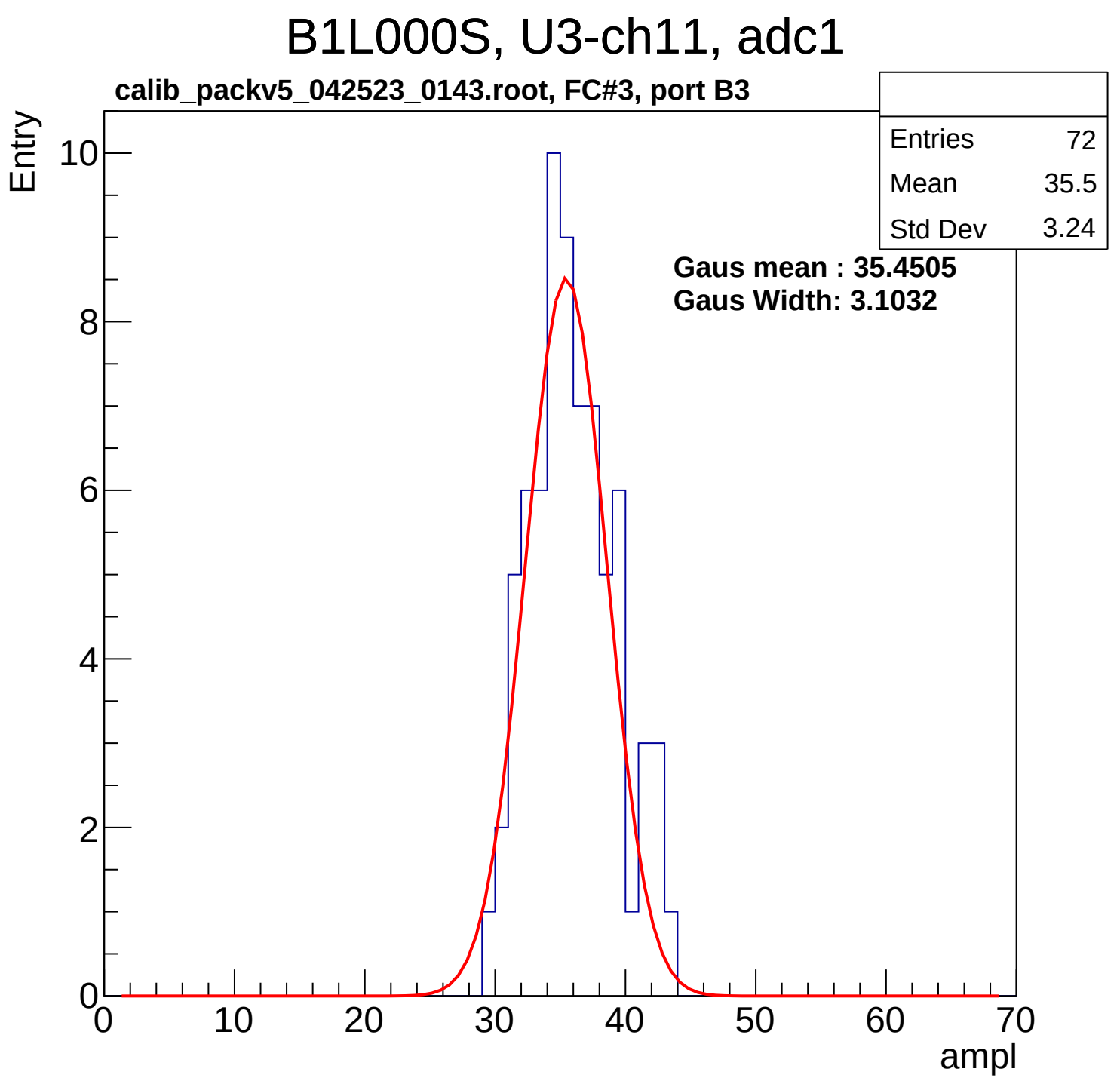
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch11, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 35.5
Std Dev 3.24
Gaus mean : 35.4505
Gaus Width: 3.1032
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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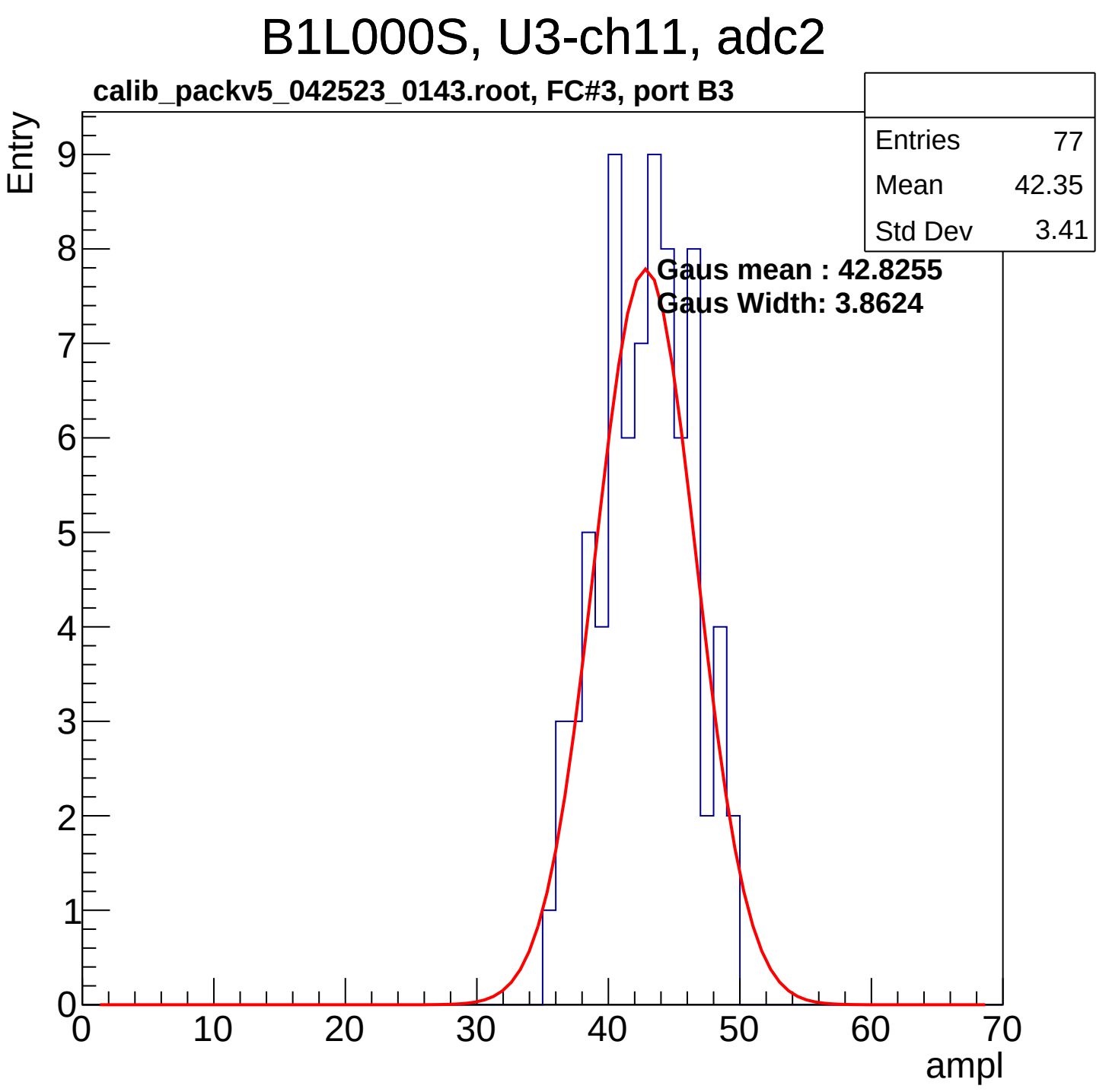
B1L000S, U3-ch11, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 42.35
Std Dev 3.41
Gaus mean : 42.8255
Gaus Width: 3.8624
Entry
ampl
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3
4
5
6
7
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0
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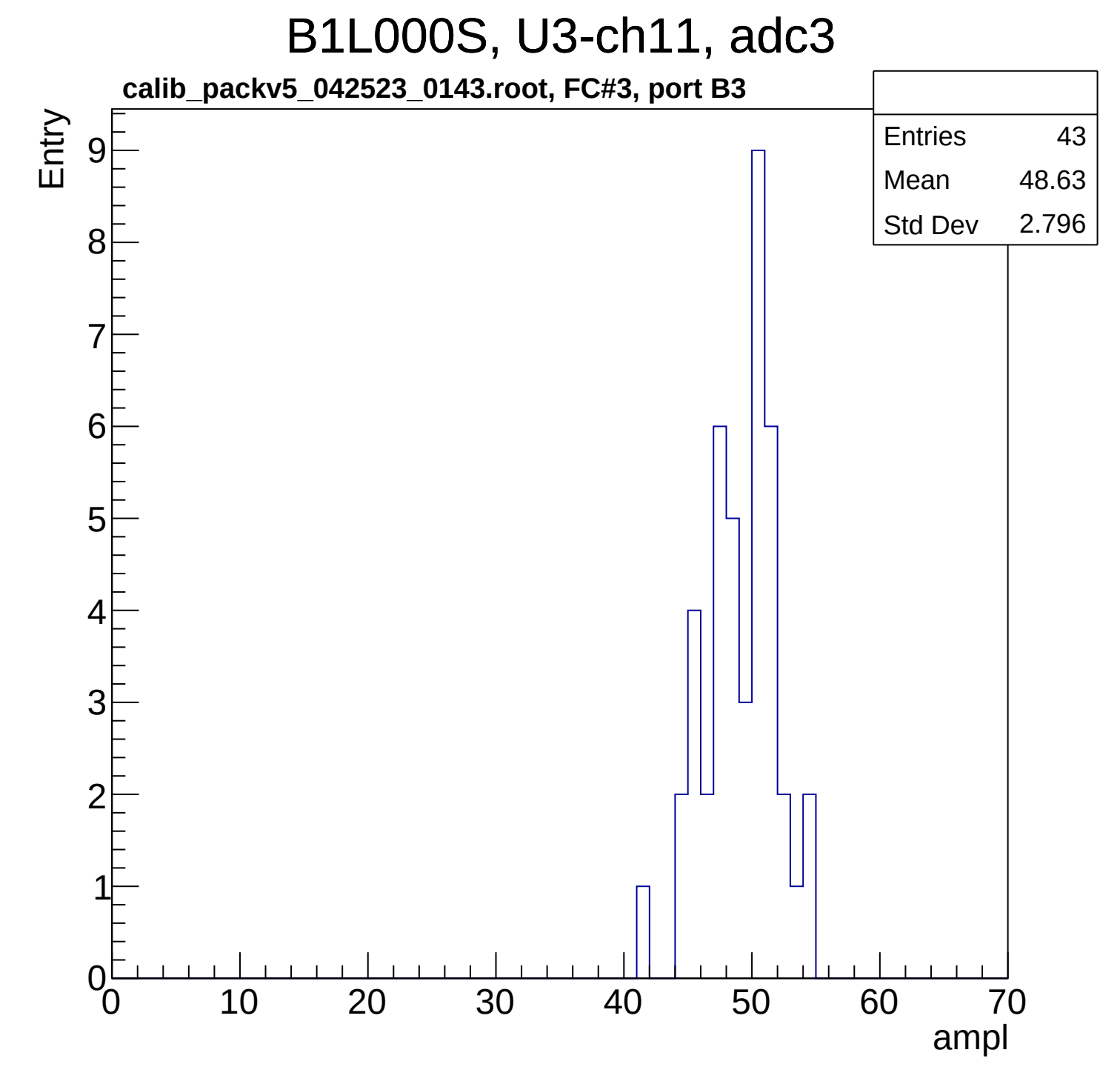
B1L000S, U3-ch11, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 48.63
Std Dev 2.796
Entry
ampl
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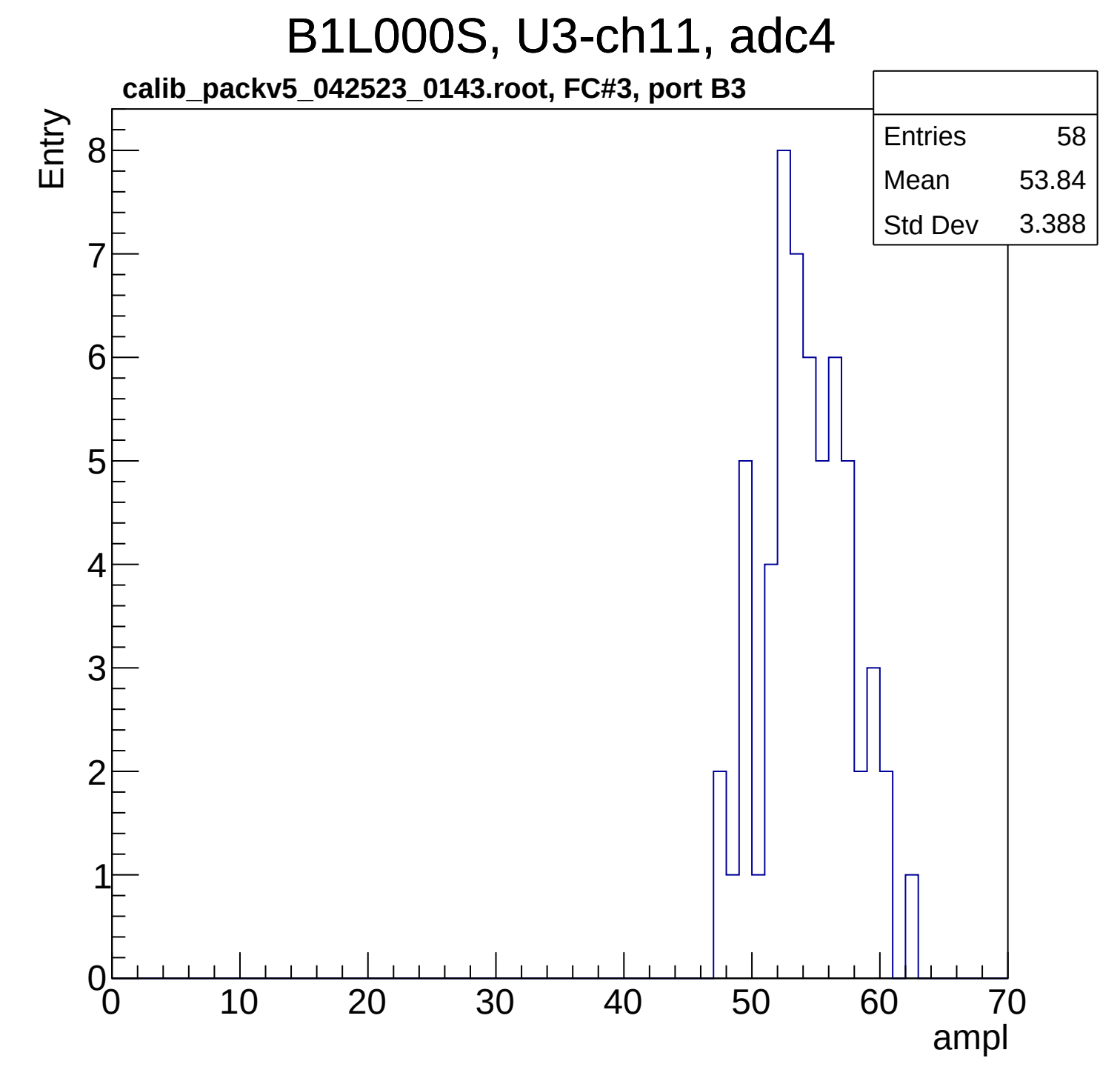
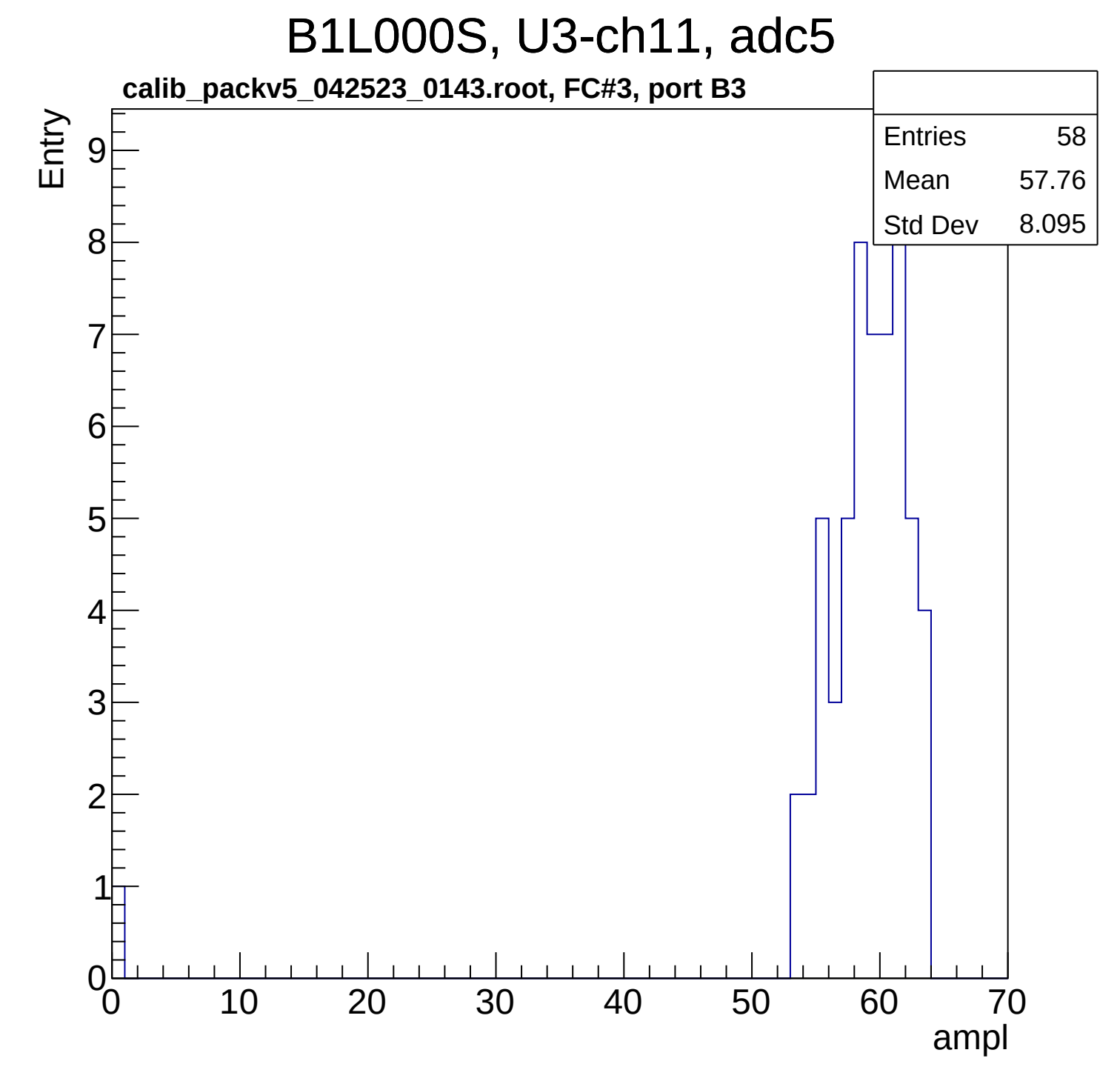

B1L000S, U3-ch11, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 53.84
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch11, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 57.76
Std Dev 8.095
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
10
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60
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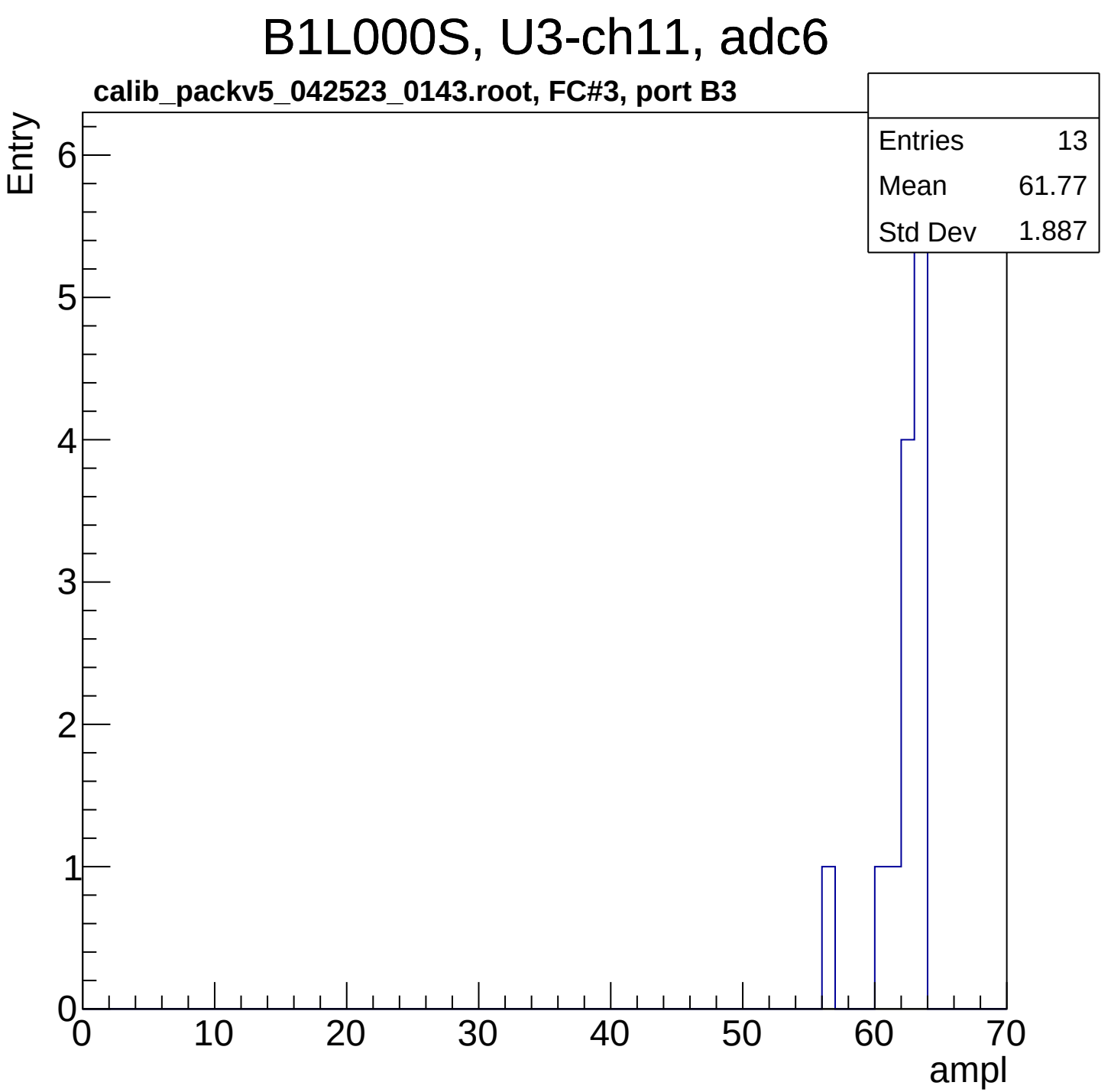
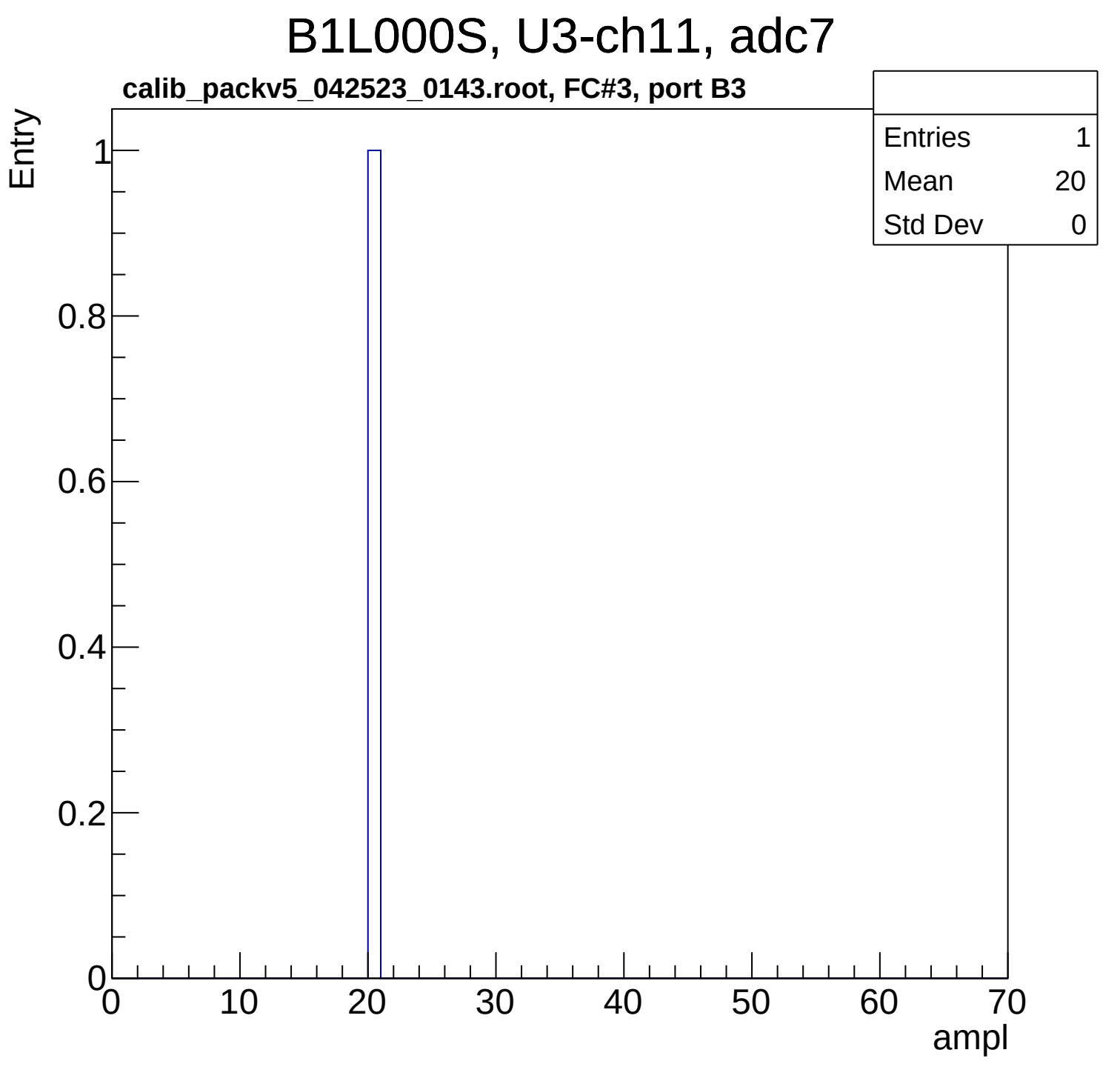
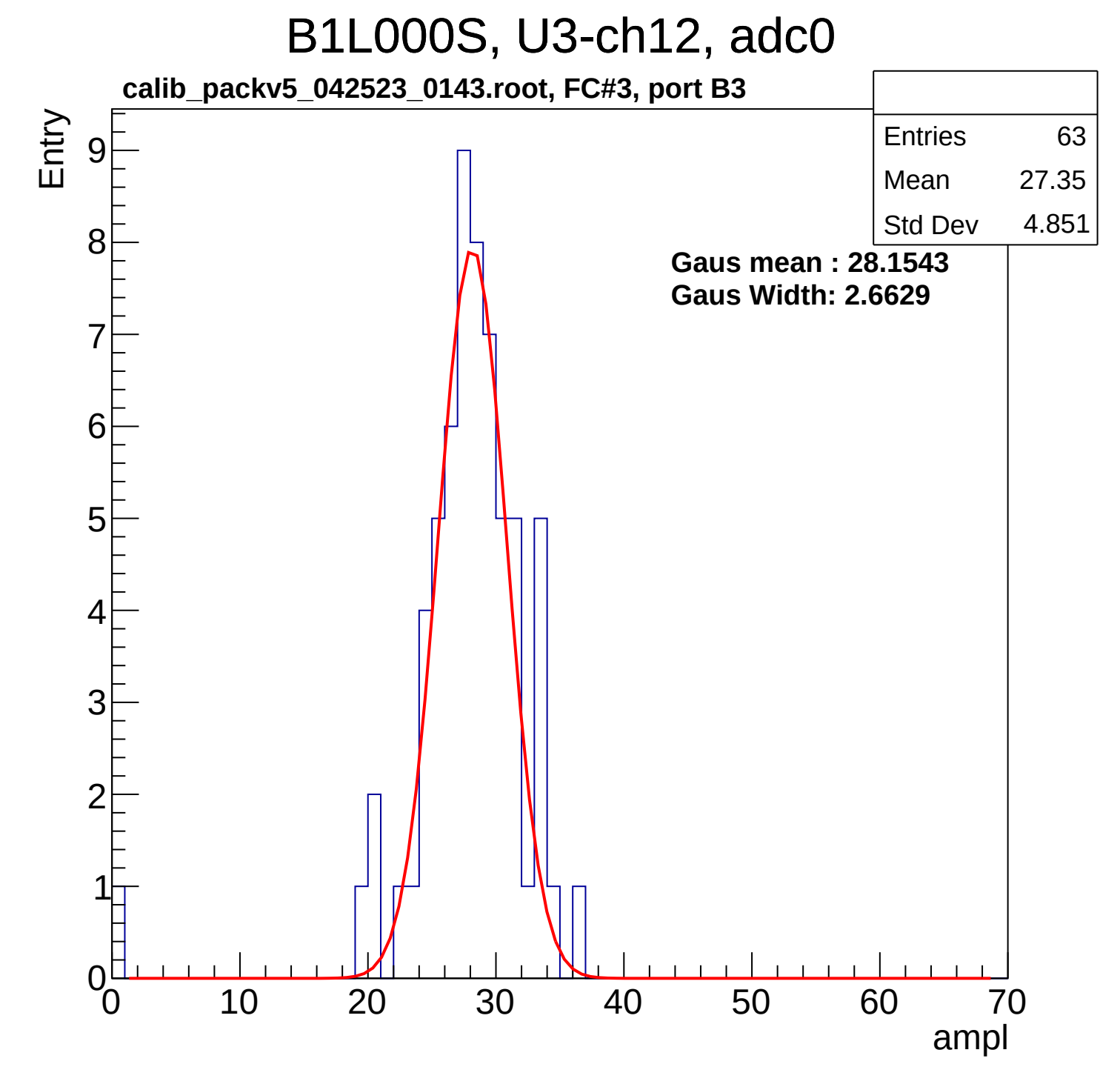
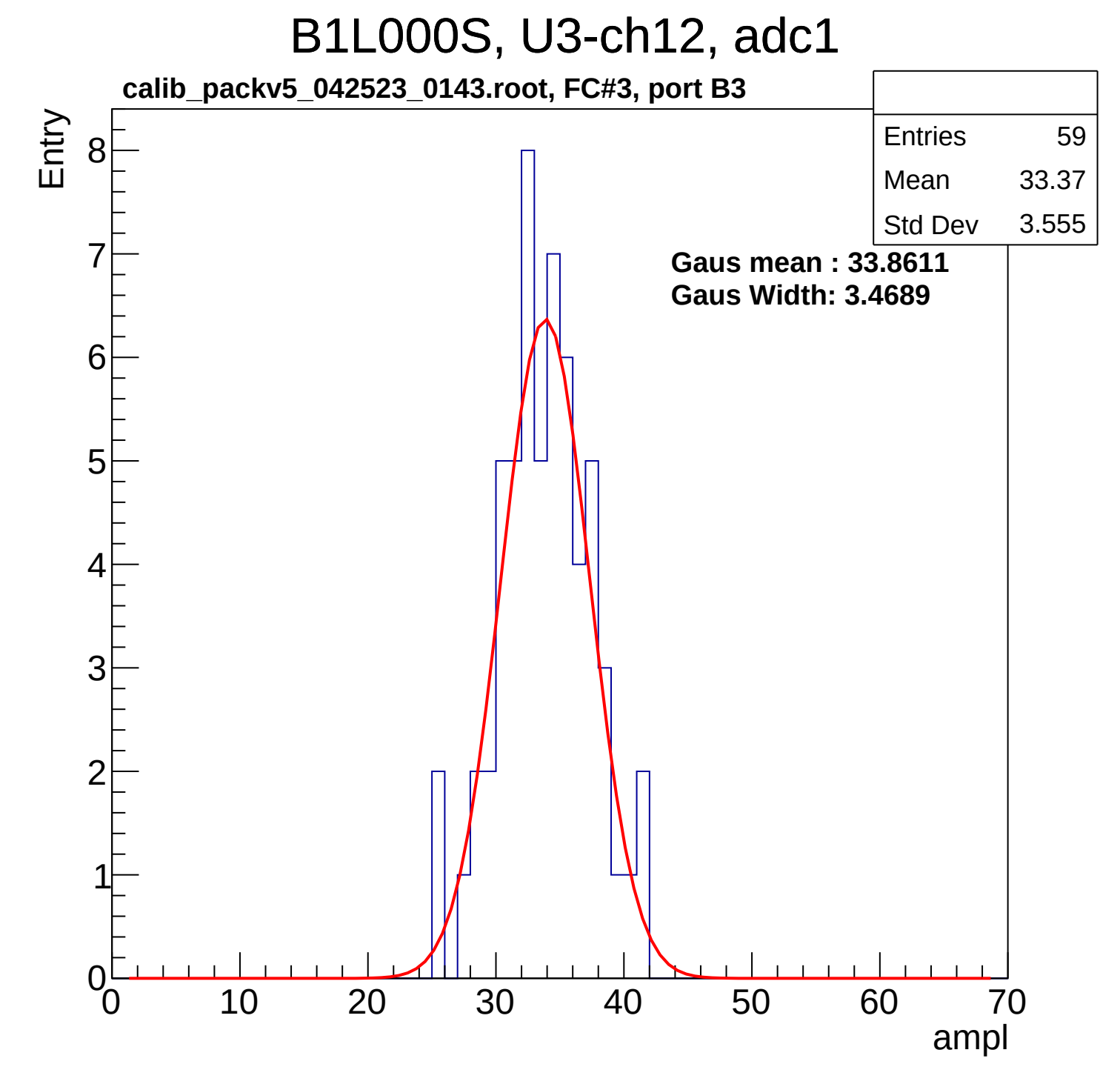
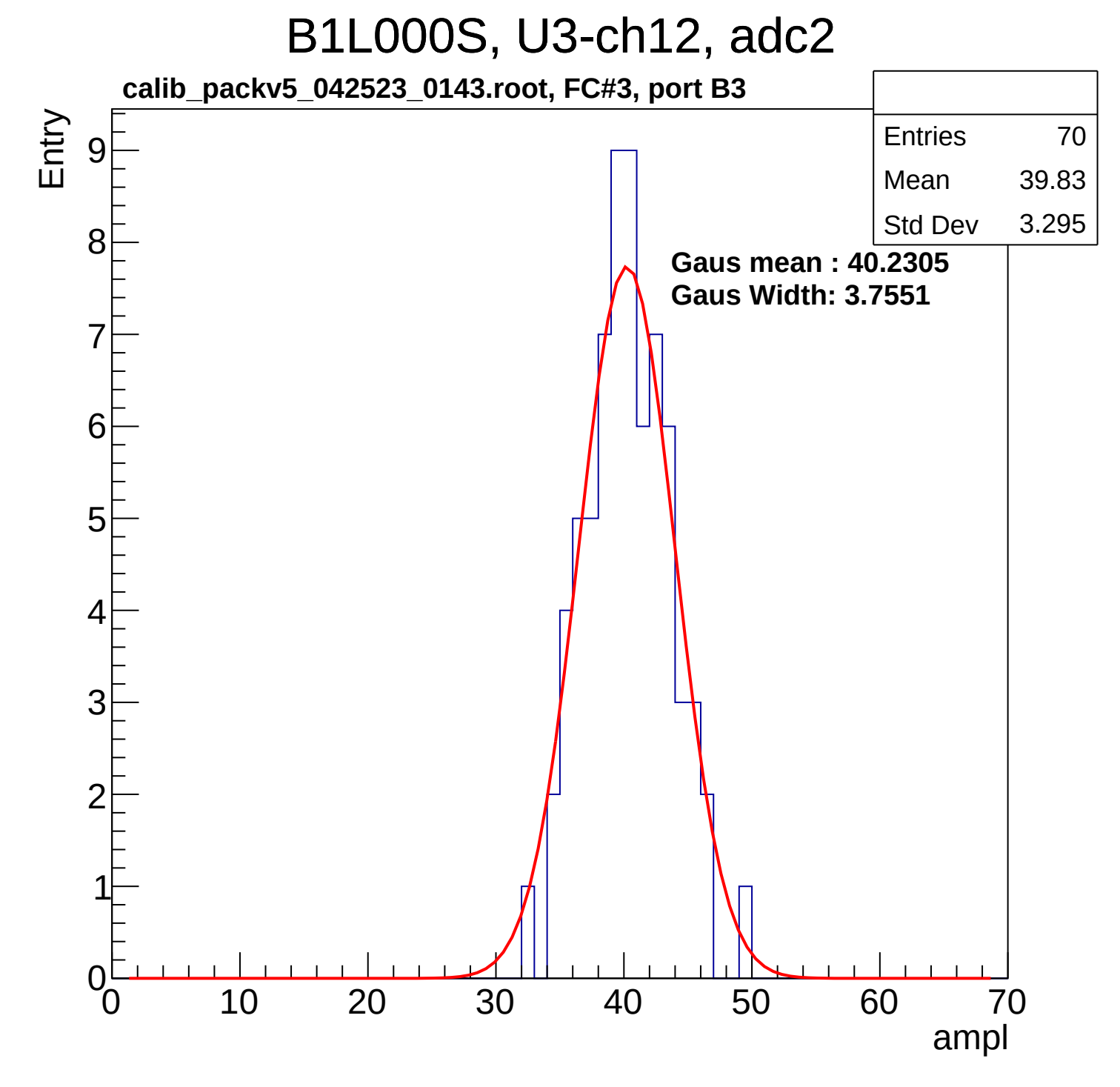

B1L000S, U3-ch11, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.77
Std Dev 1.887
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch11, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch12, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 27.35
Std Dev 4.851
Gaus mean : 28.1543
Gaus Width: 2.6629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch12, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 33.37
Std Dev 3.555
Gaus mean : 33.8611
Gaus Width: 3.4689
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch12, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 39.83
Std Dev 3.295
Gaus mean : 40.2305
Gaus Width: 3.7551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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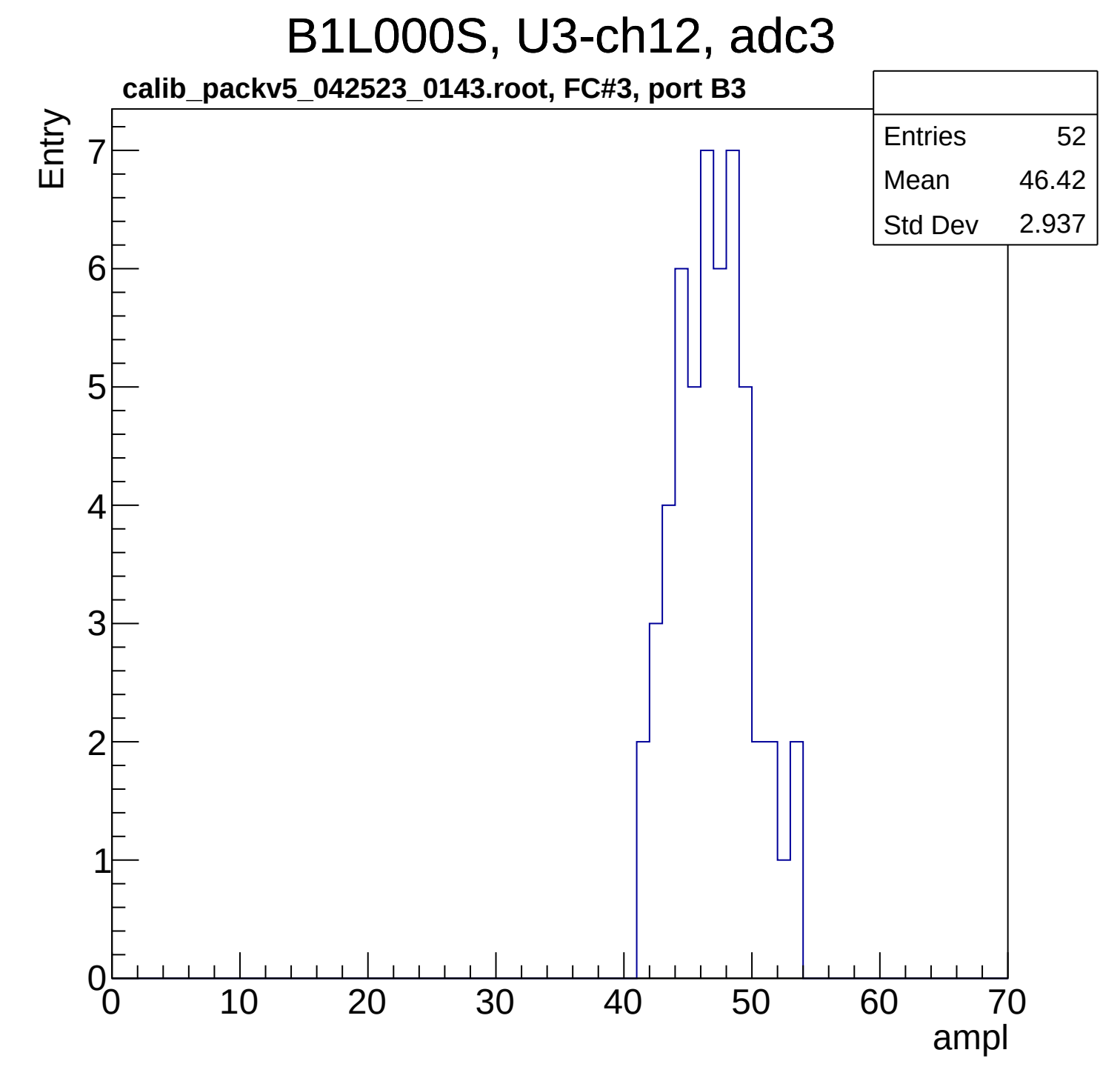
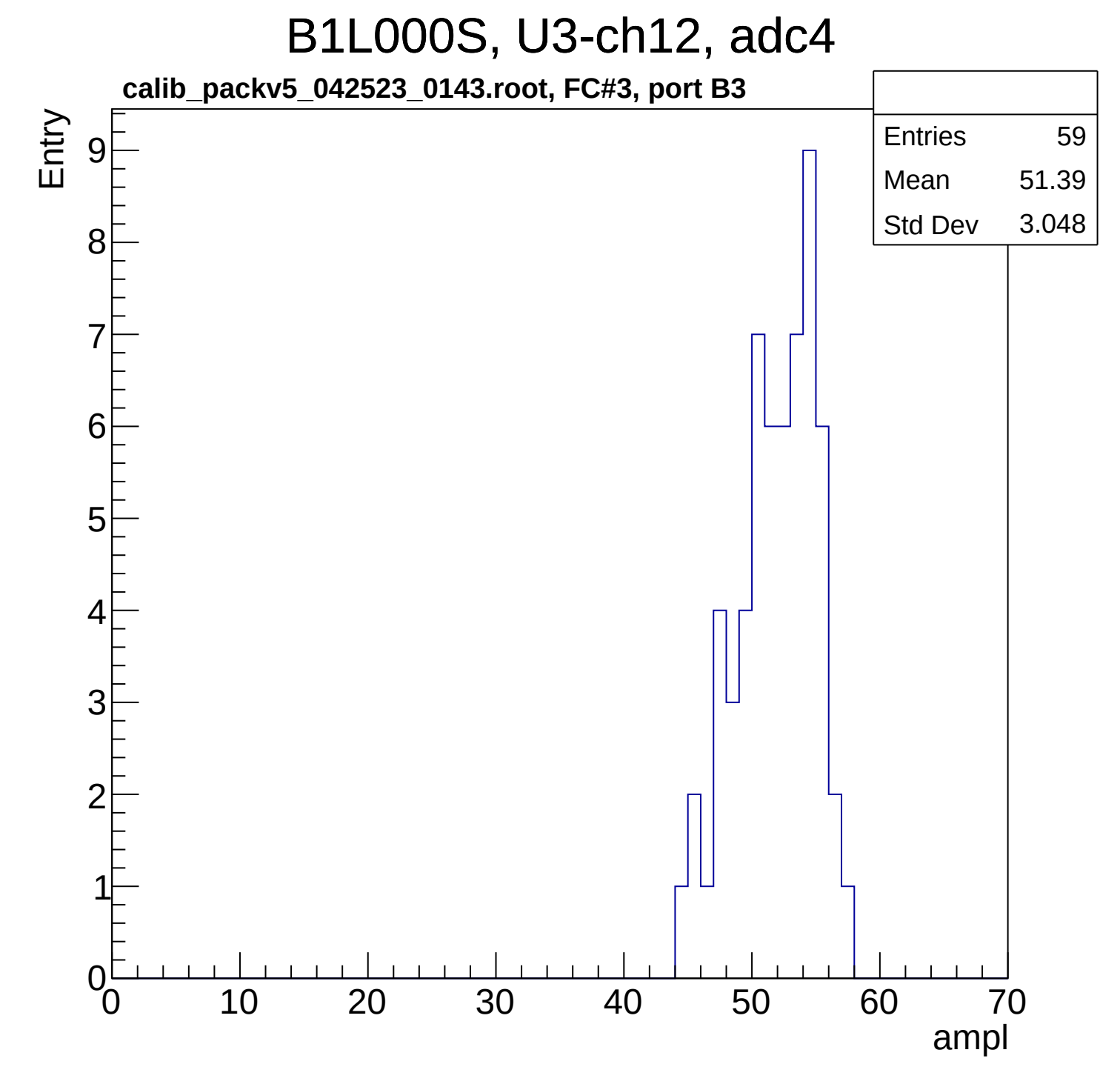
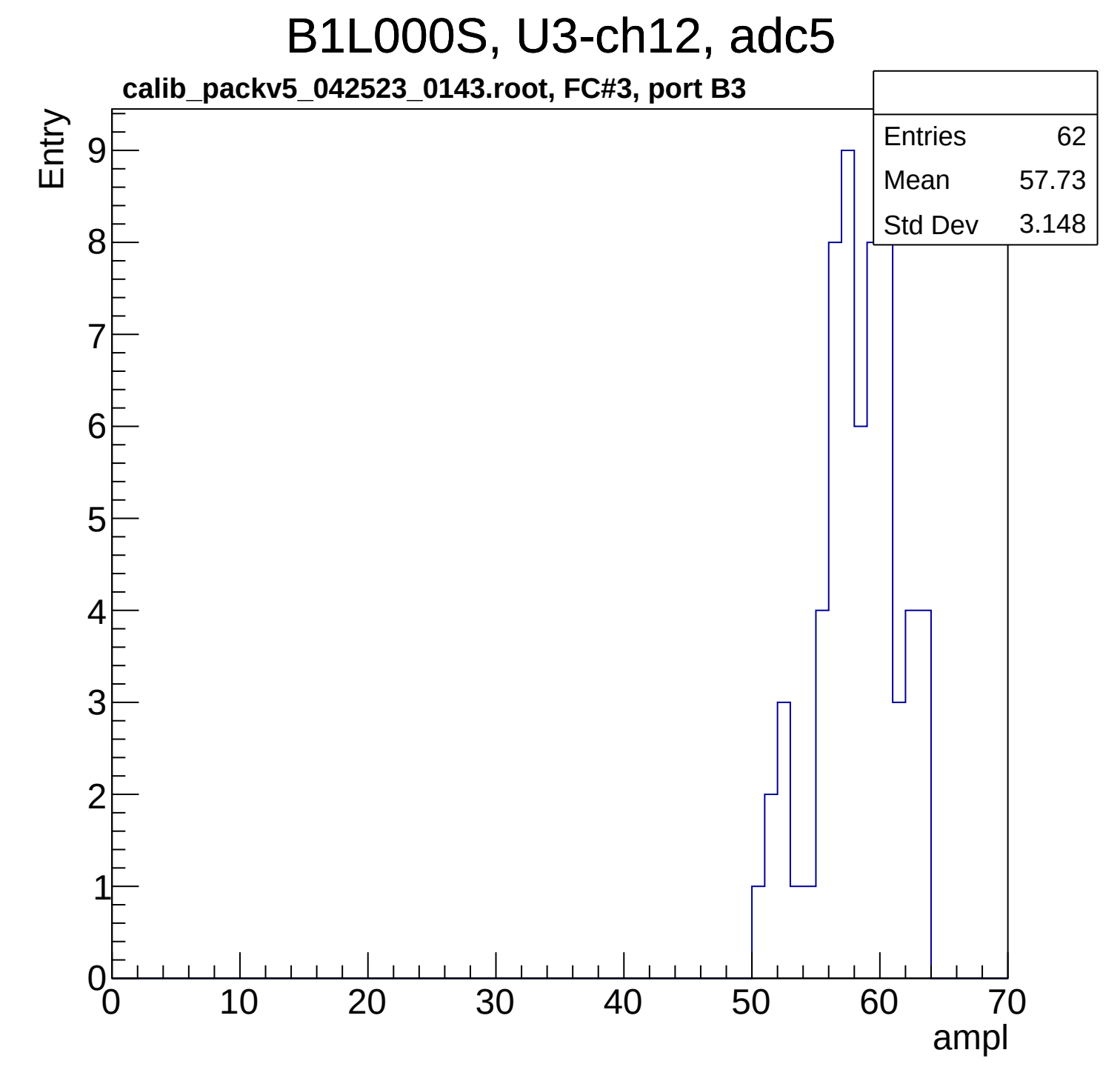
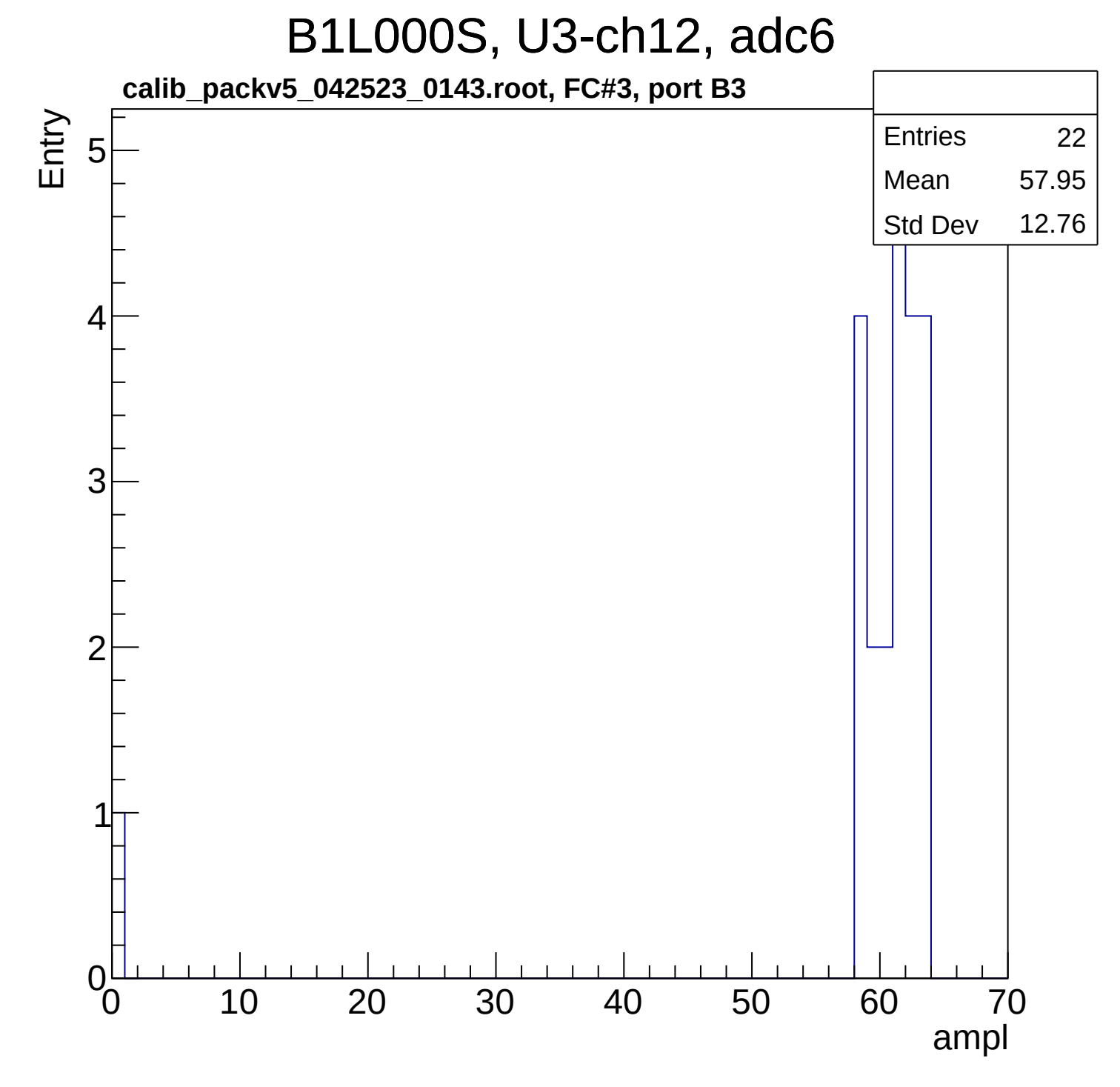
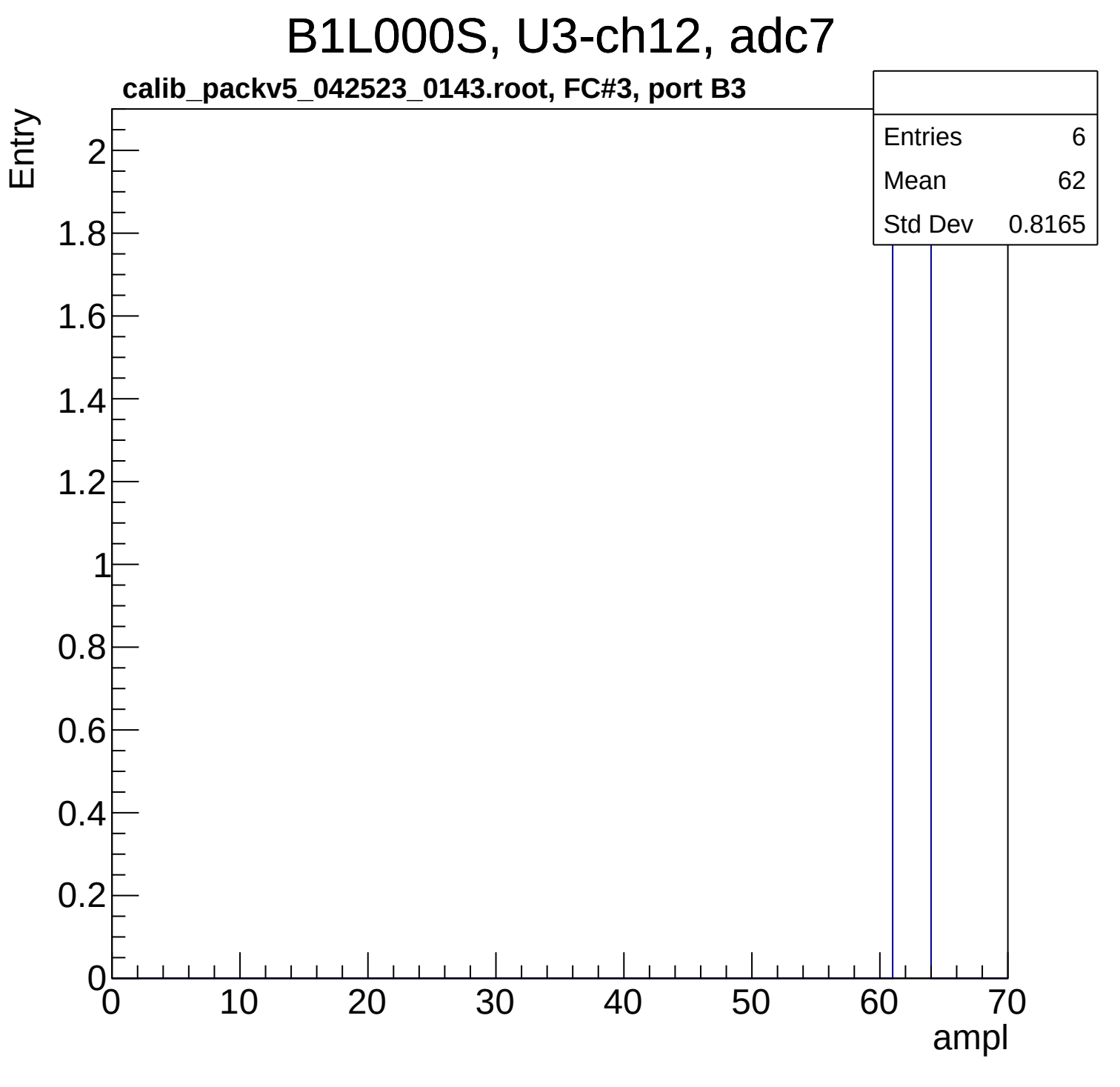

B1L000S, U3-ch12, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 46.42
Std Dev 2.937
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch12, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 51.39
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch12, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 57.73
Std Dev 3.148
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch12, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 57.95
Std Dev 12.76
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch12, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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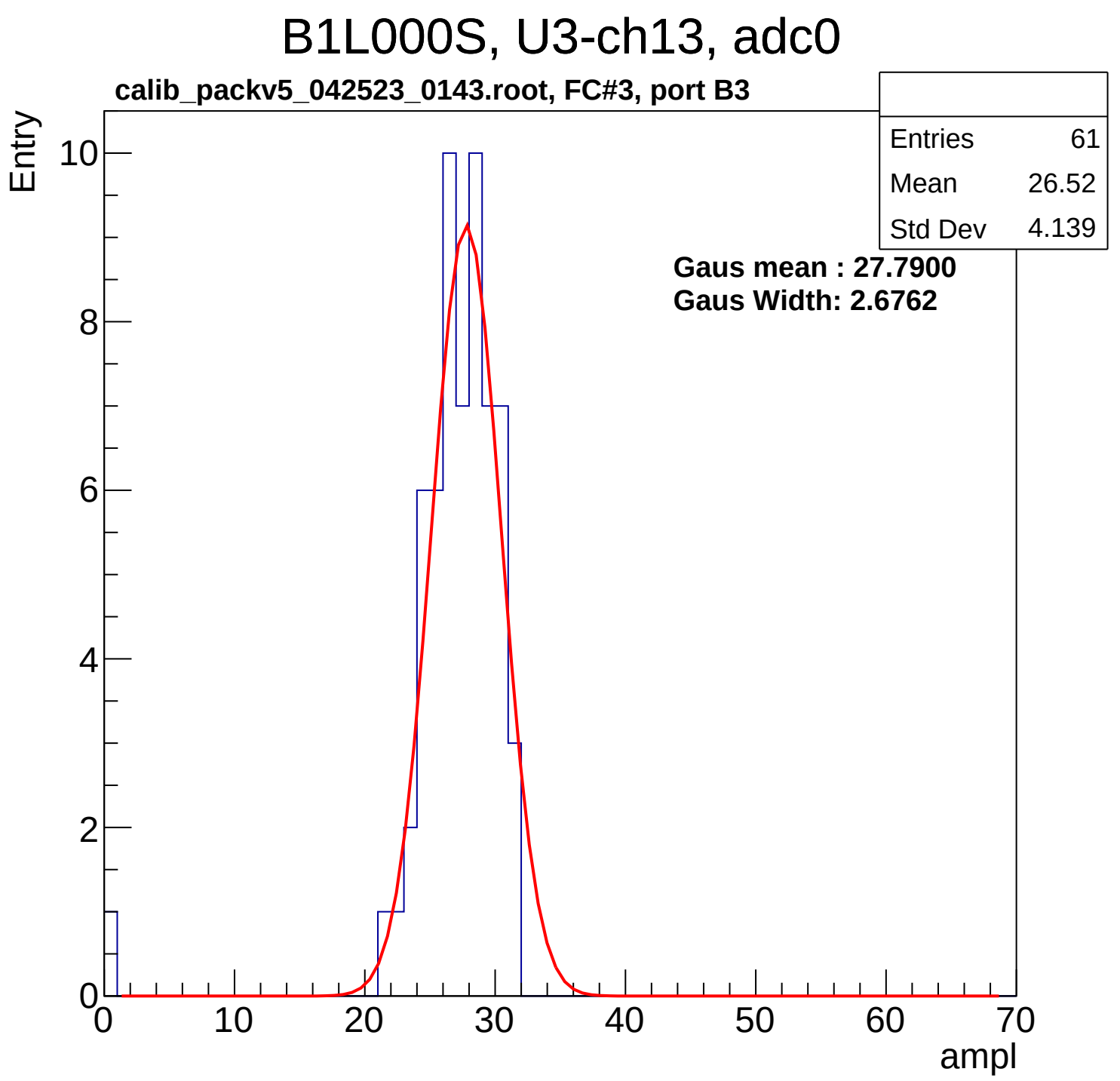
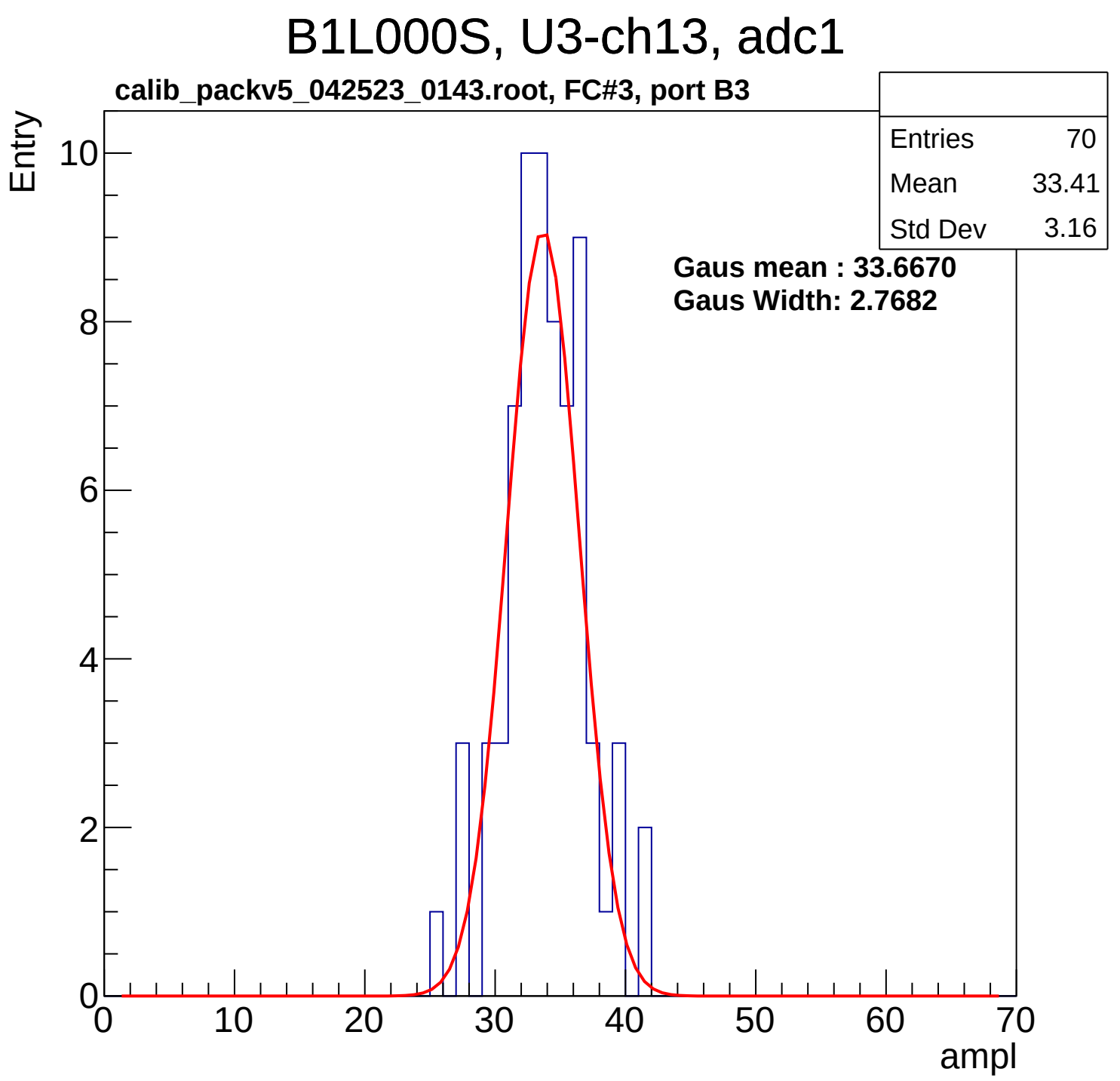
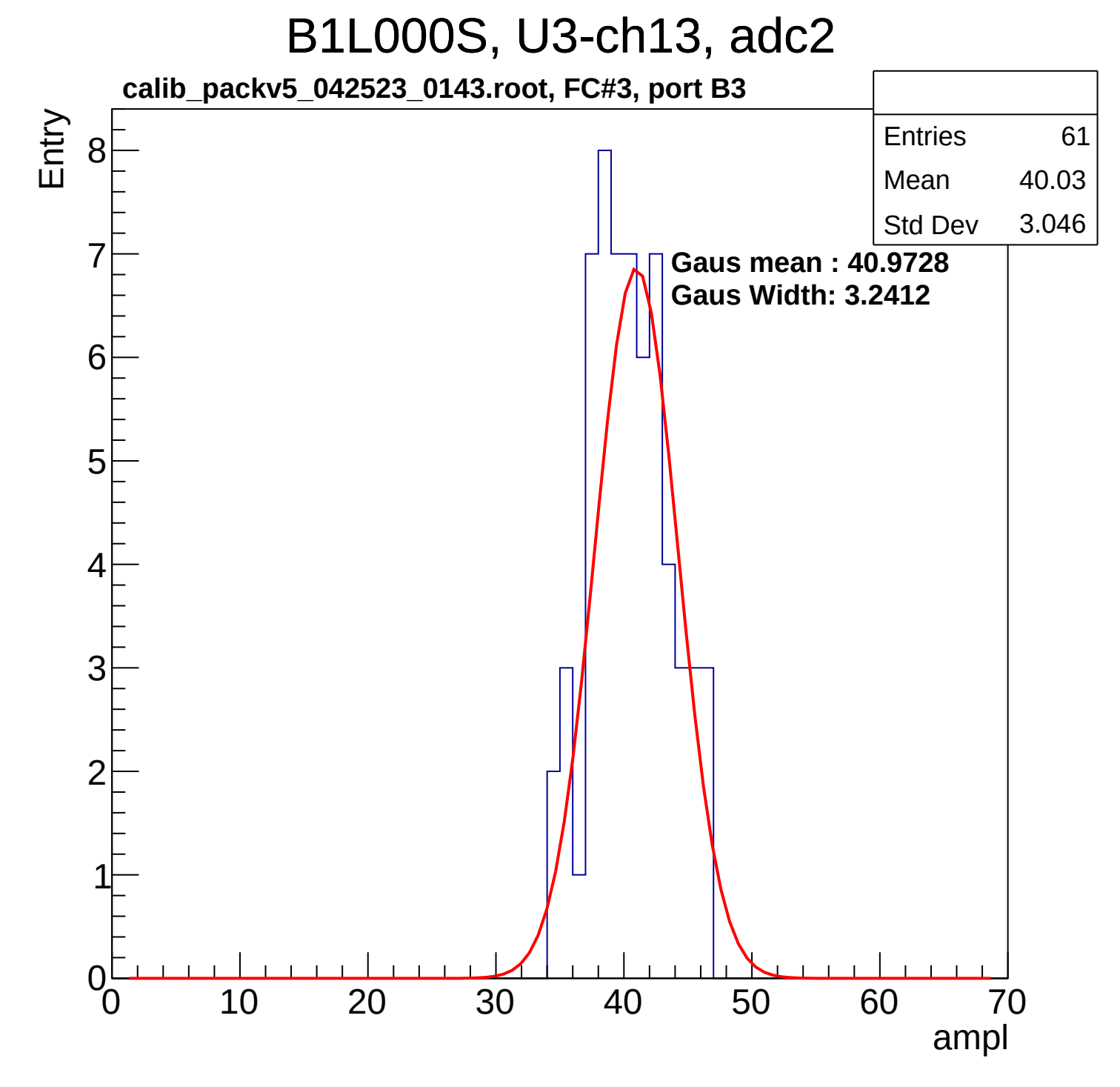
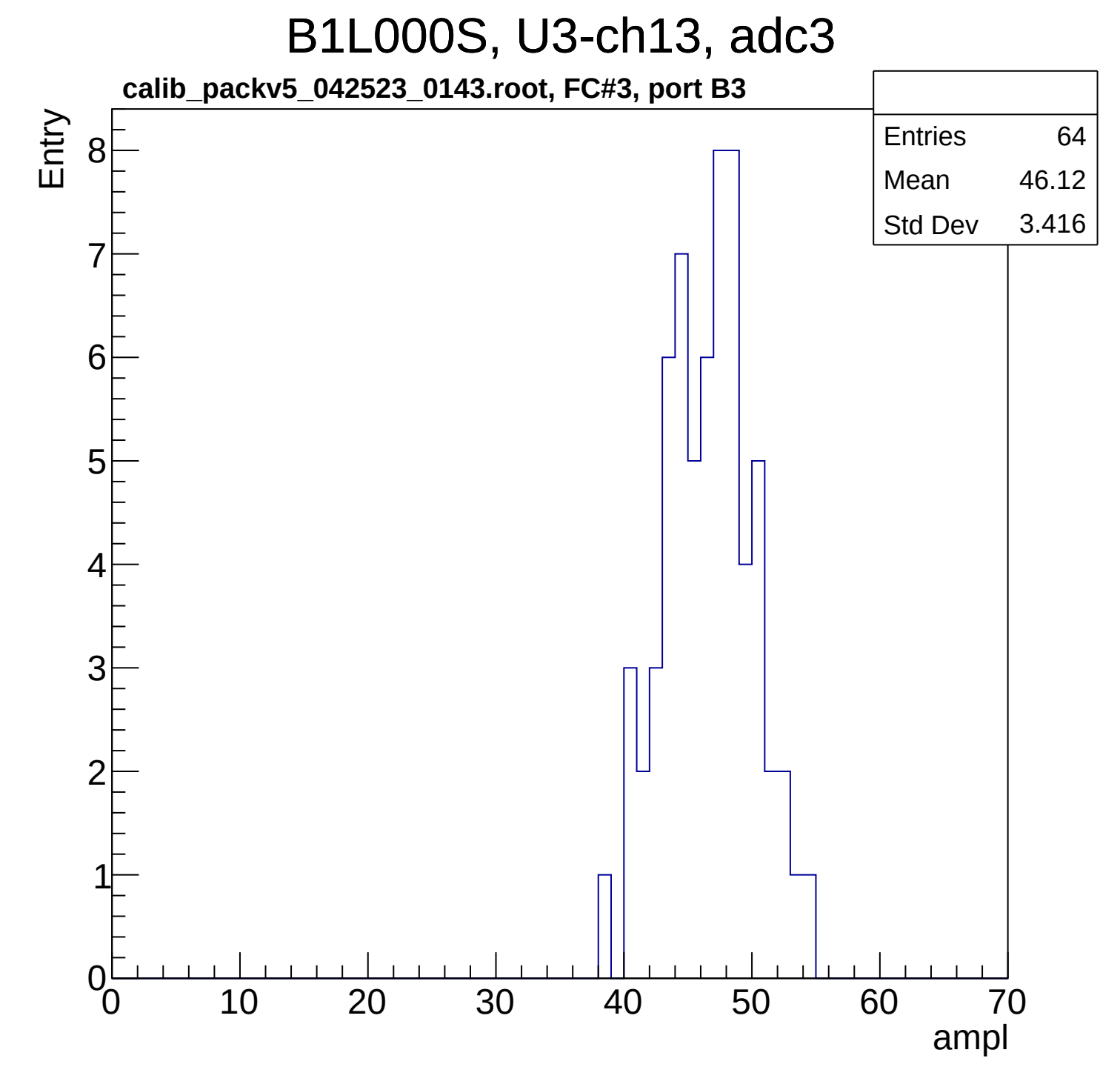

B1L000S, U3-ch13, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 26.52
Std Dev 4.139
Gaus mean : 27.7900
Gaus Width: 2.6762
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch13, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 33.41
Std Dev 3.16
Gaus mean : 33.6670
Gaus Width: 2.7682
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch13, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 40.03
Std Dev 3.046
Gaus mean : 40.9728
Gaus Width: 3.2412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch13, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 46.12
Std Dev 3.416
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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60
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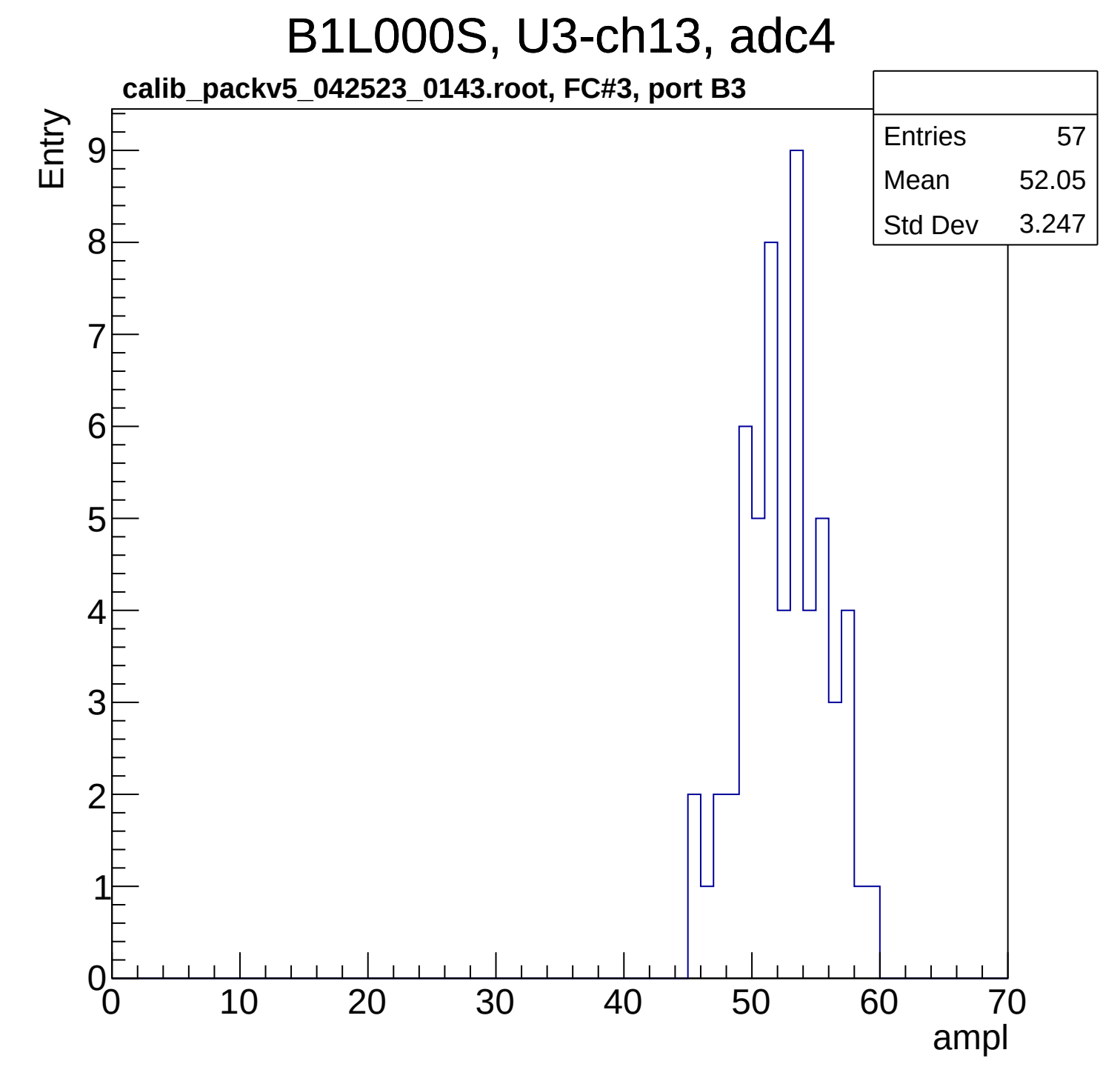
B1L000S, U3-ch13, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 52.05
Std Dev 3.247
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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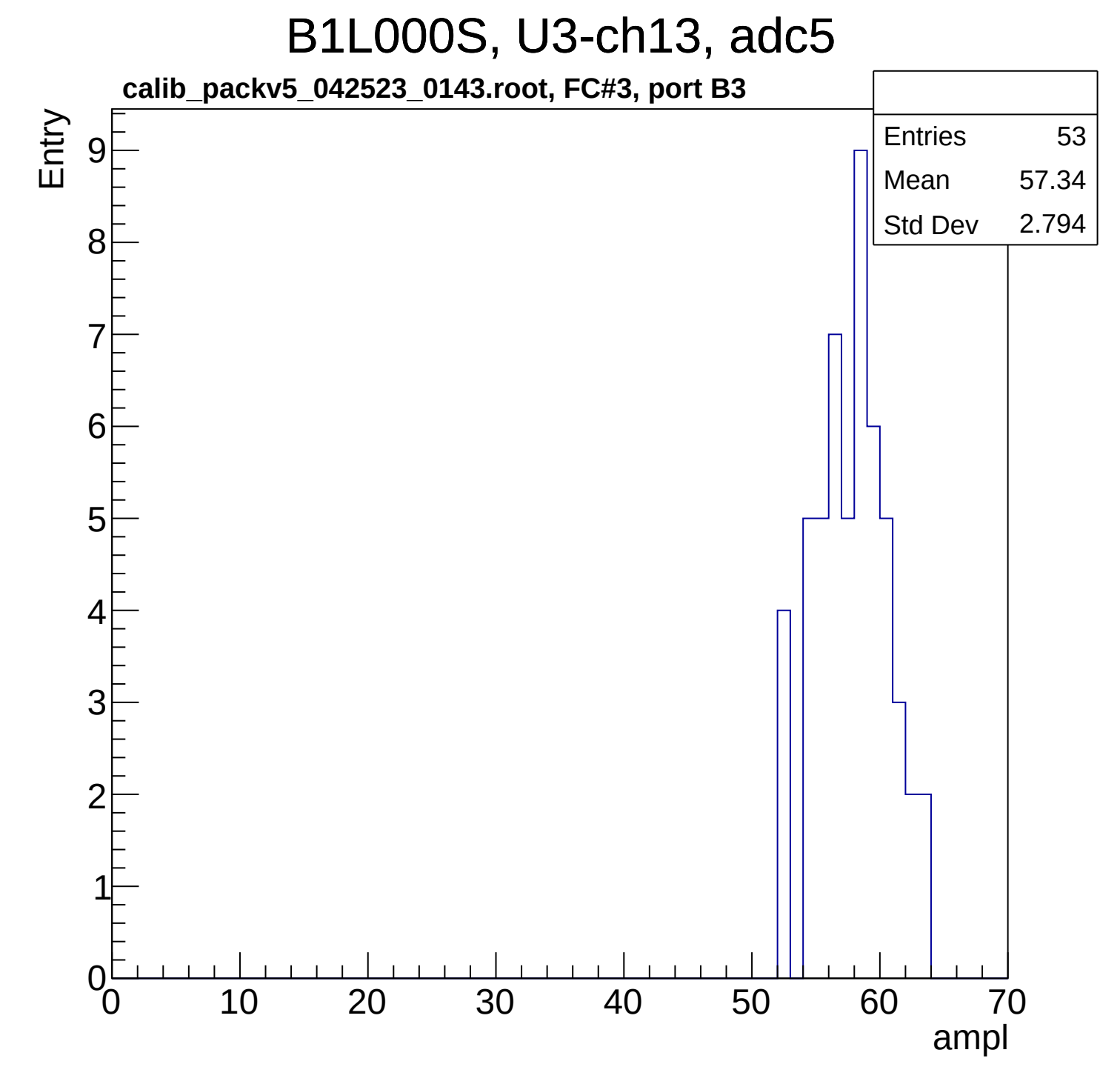
B1L000S, U3-ch13, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 57.34
Std Dev 2.794
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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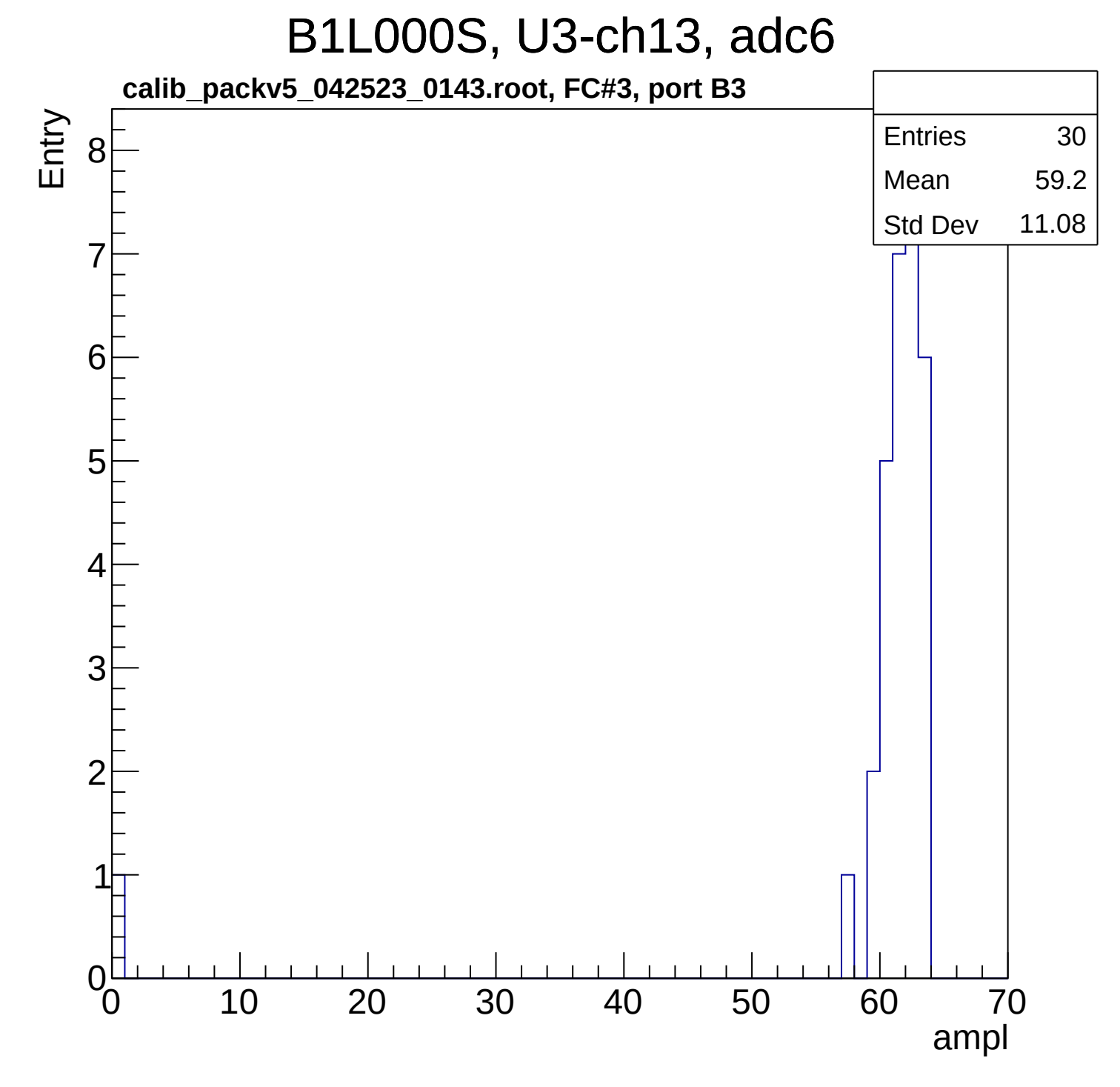
B1L000S, U3-ch13, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 30
Mean 59.2
Std Dev 11.08
Entry
ampl
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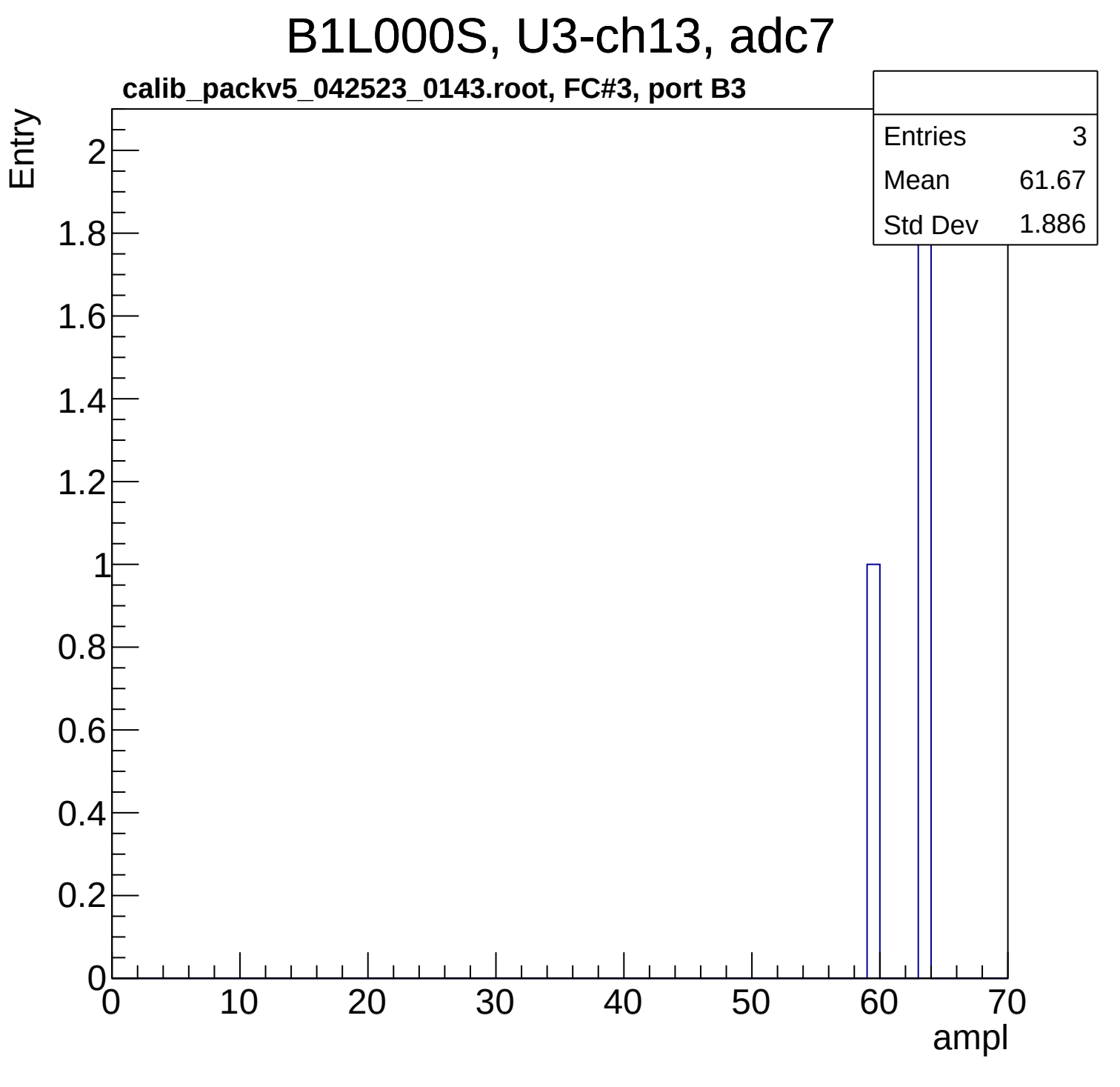
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch13, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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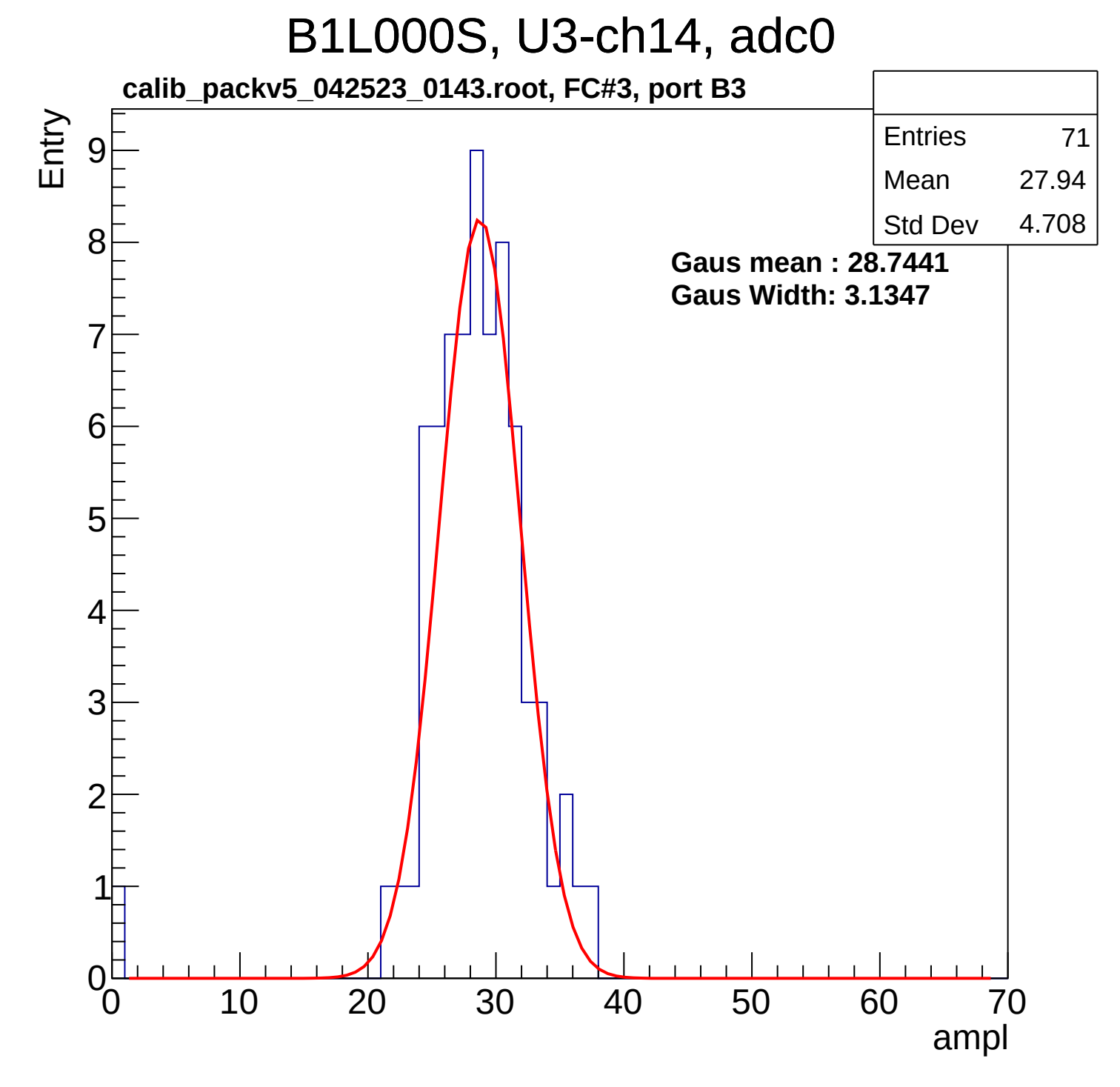
B1L000S, U3-ch14, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 27.94
Std Dev 4.708
Gaus mean : 28.7441
Gaus Width: 3.1347
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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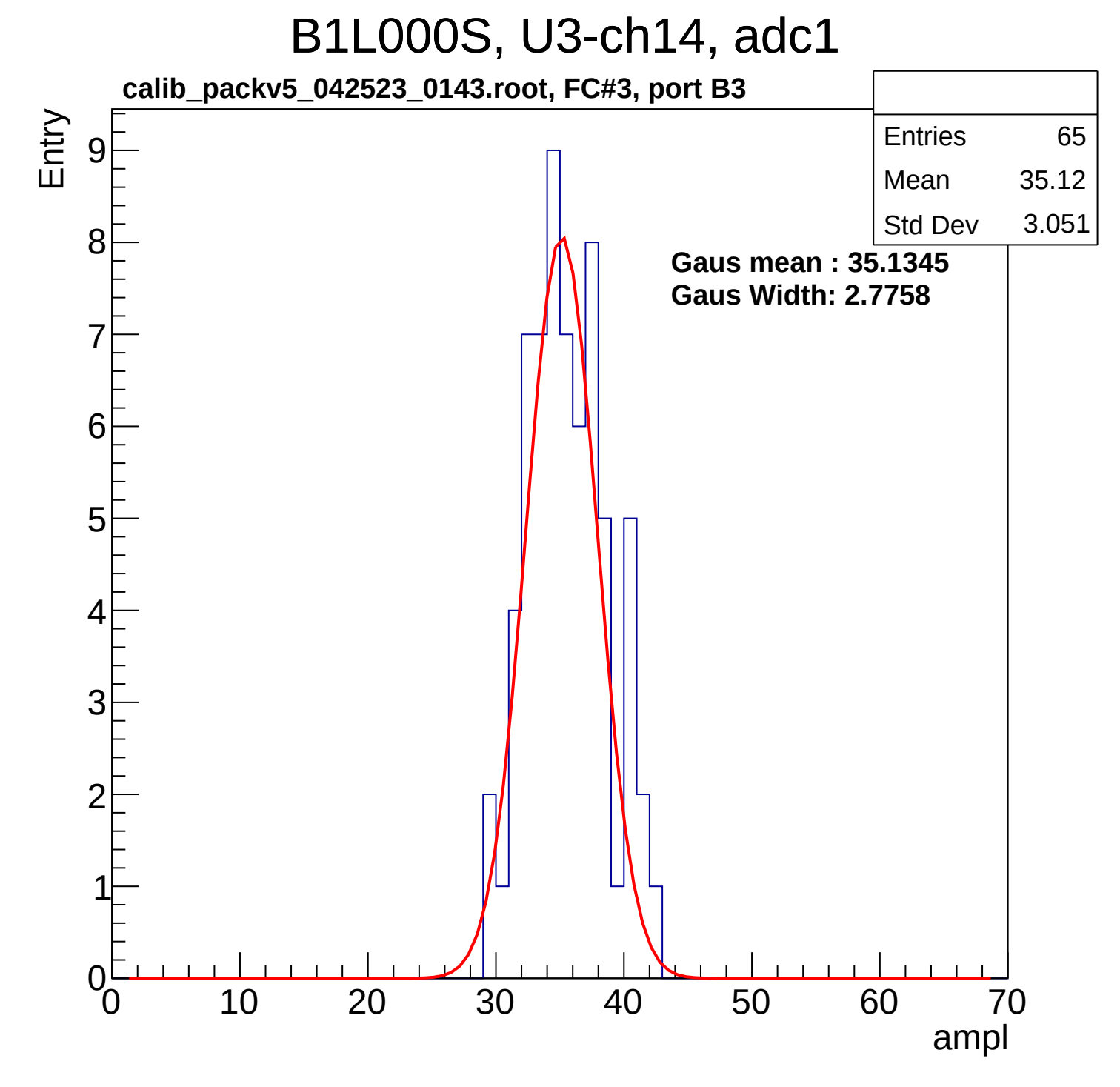
B1L000S, U3-ch14, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.12
Std Dev 3.051
Gaus mean : 35.1345
Gaus Width: 2.7758
Entry
ampl
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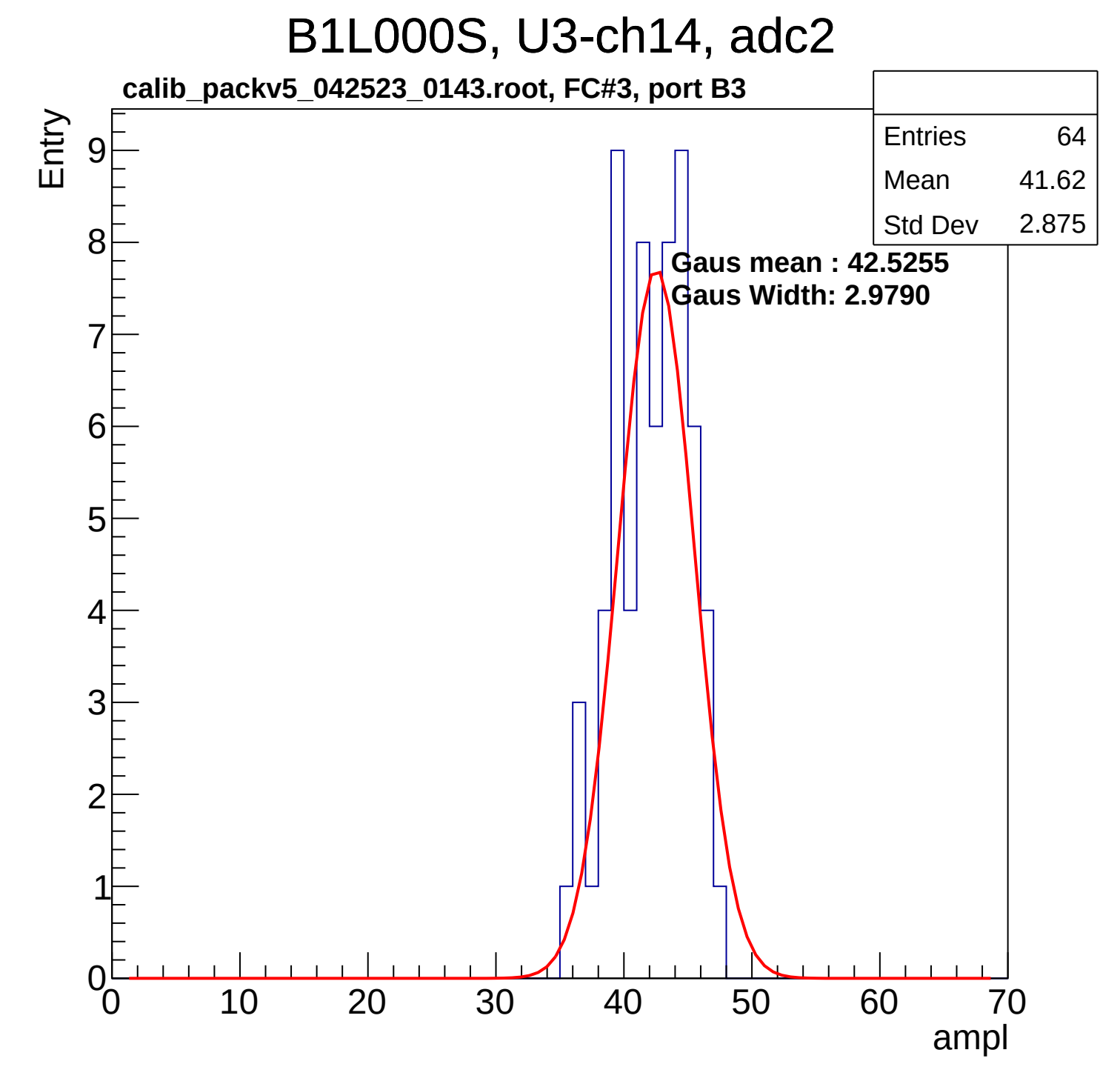
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch14, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 41.62
Std Dev 2.875
Gaus mean : 42.5255
Gaus Width: 2.9790
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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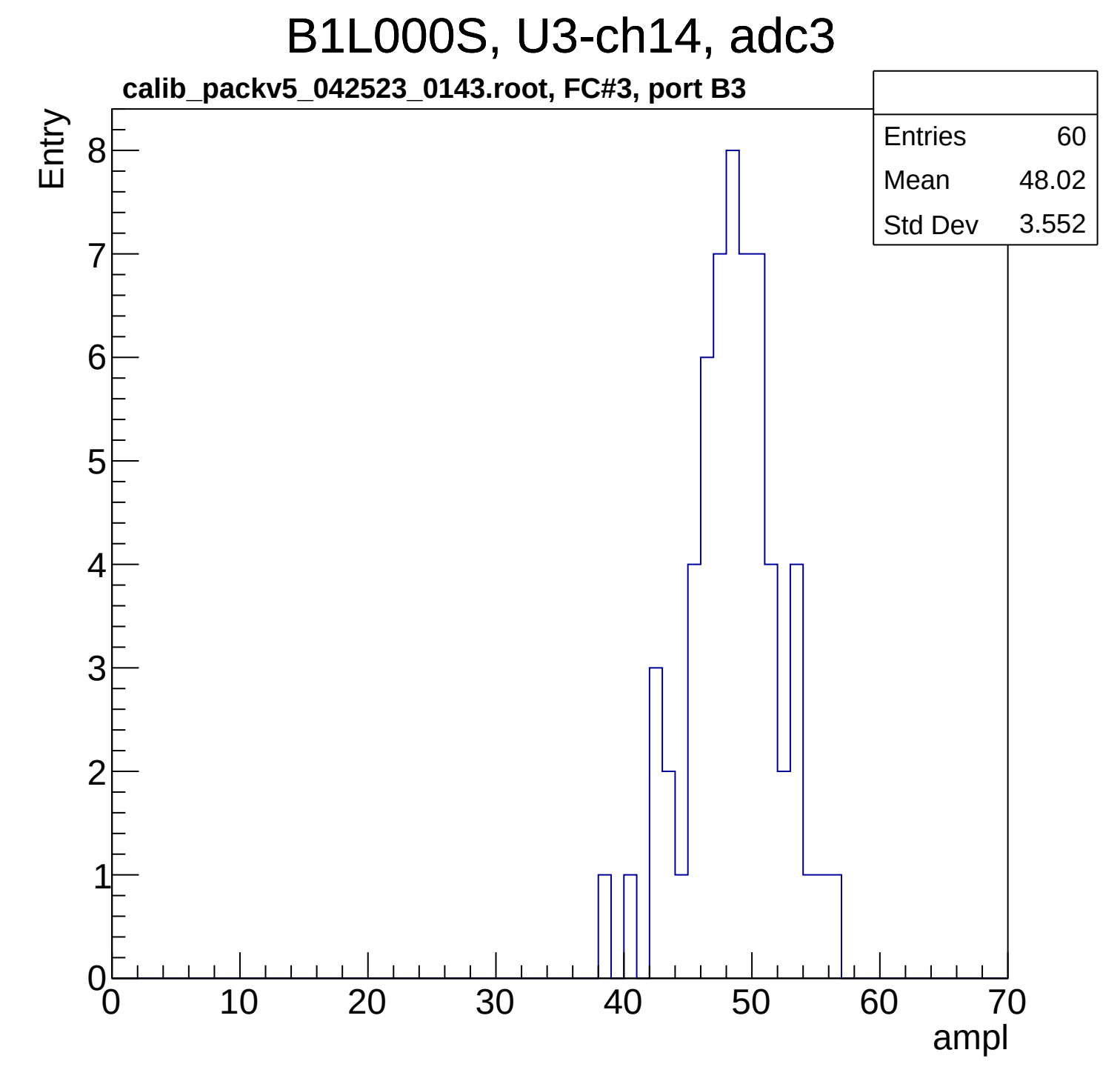
B1L000S, U3-ch14, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 48.02
Std Dev 3.552
Entry
ampl
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3
4
5
6
7
8
0
10
20
30
40
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60
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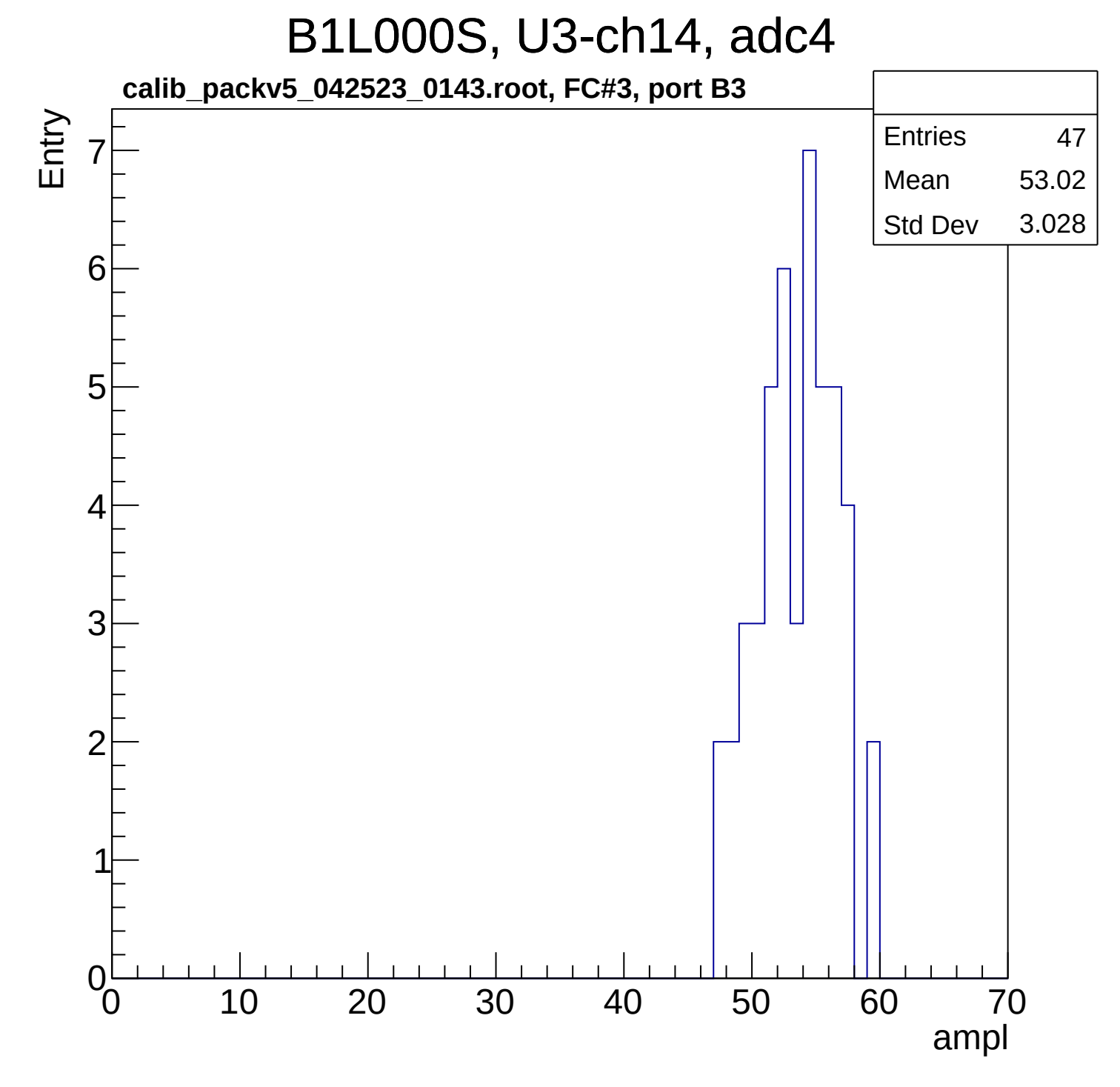
B1L000S, U3-ch14, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 53.02
Std Dev 3.028
Entry
ampl
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4
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0
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60
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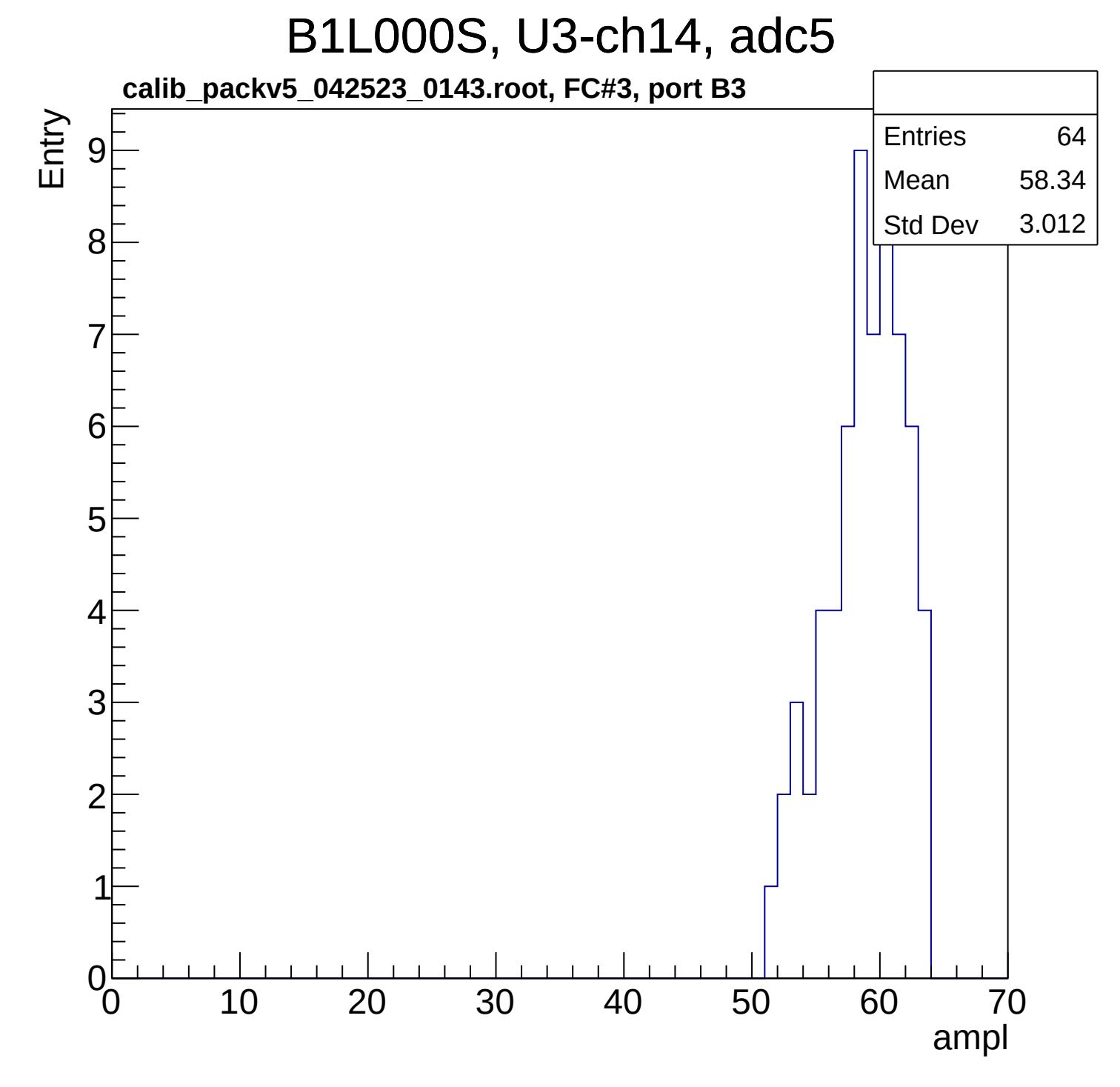
B1L000S, U3-ch14, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 58.34
Std Dev 3.012
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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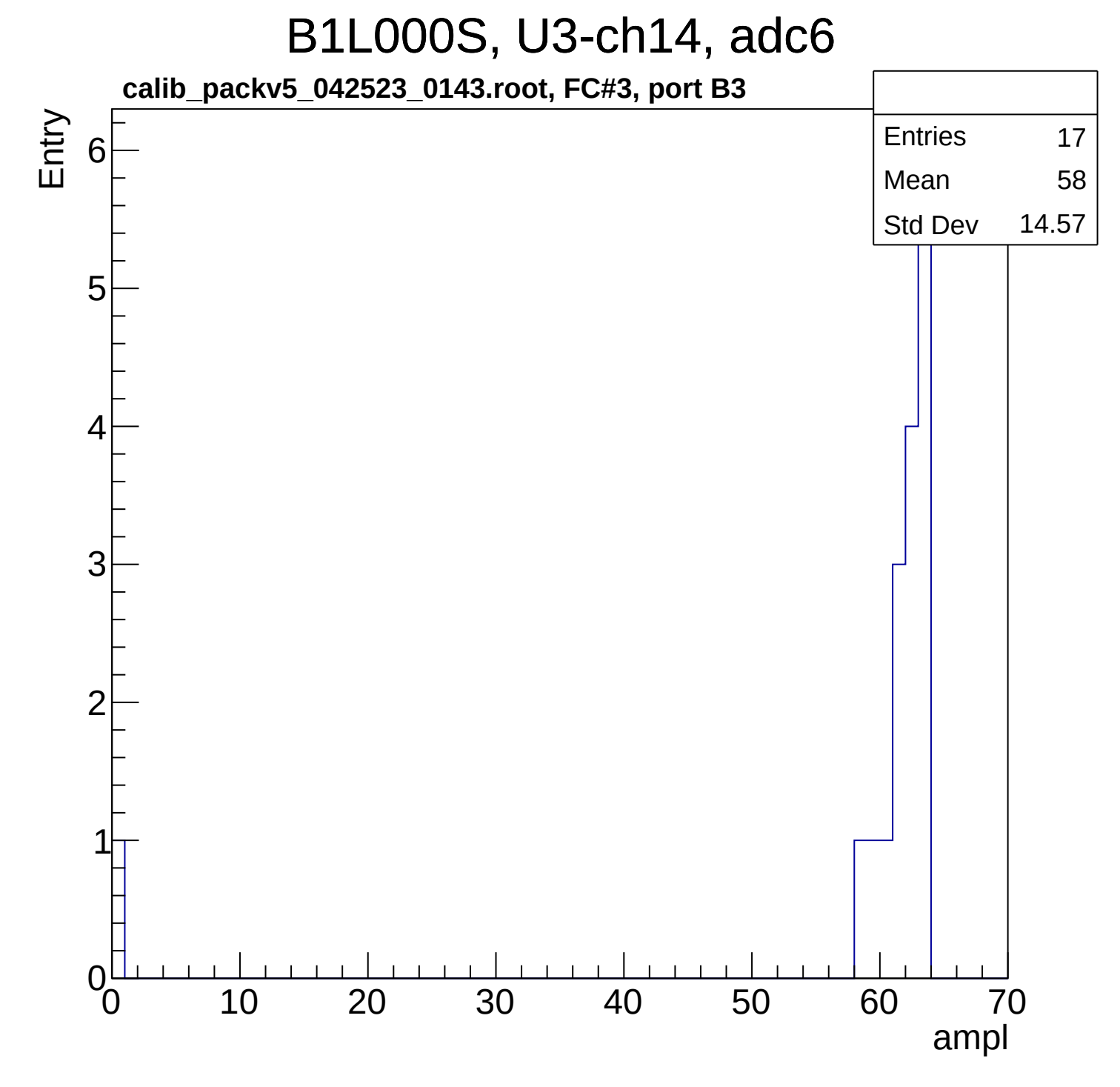
B1L000S, U3-ch14, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 58
Std Dev 14.57
Entry
0
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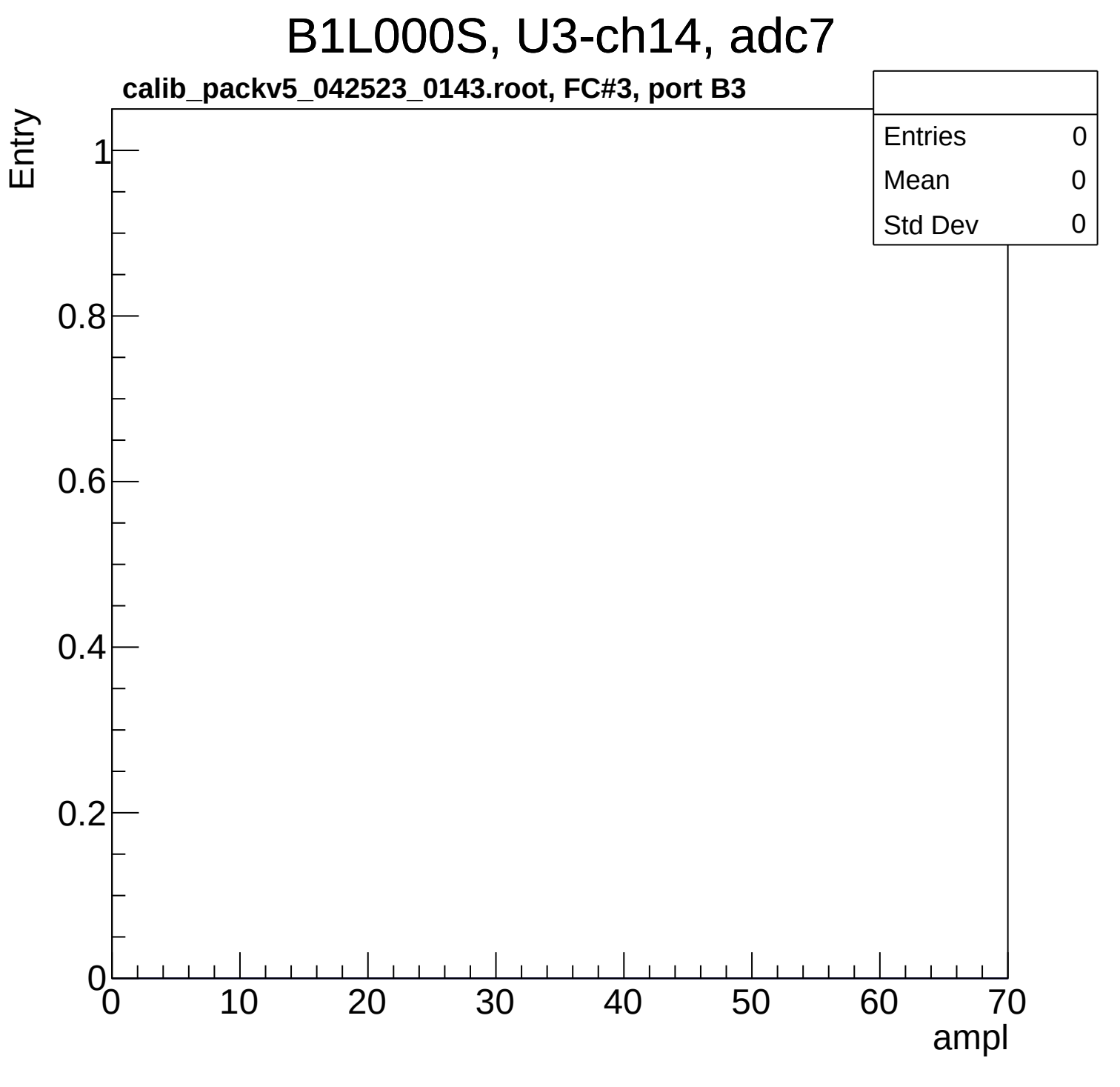
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch14, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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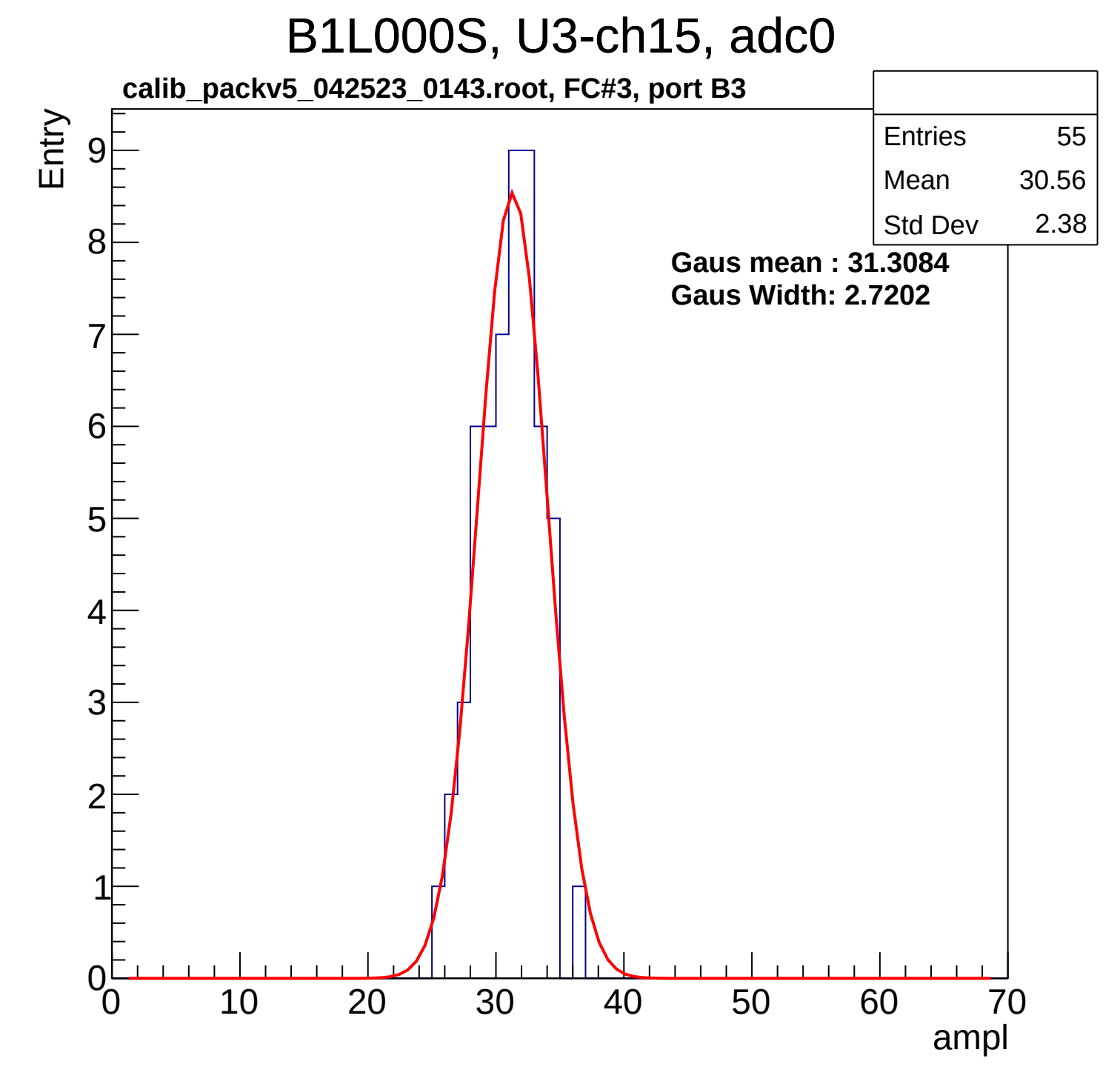
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch15, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 30.56
Std Dev 2.38
Gaus mean : 31.3084
Gaus Width: 2.7202
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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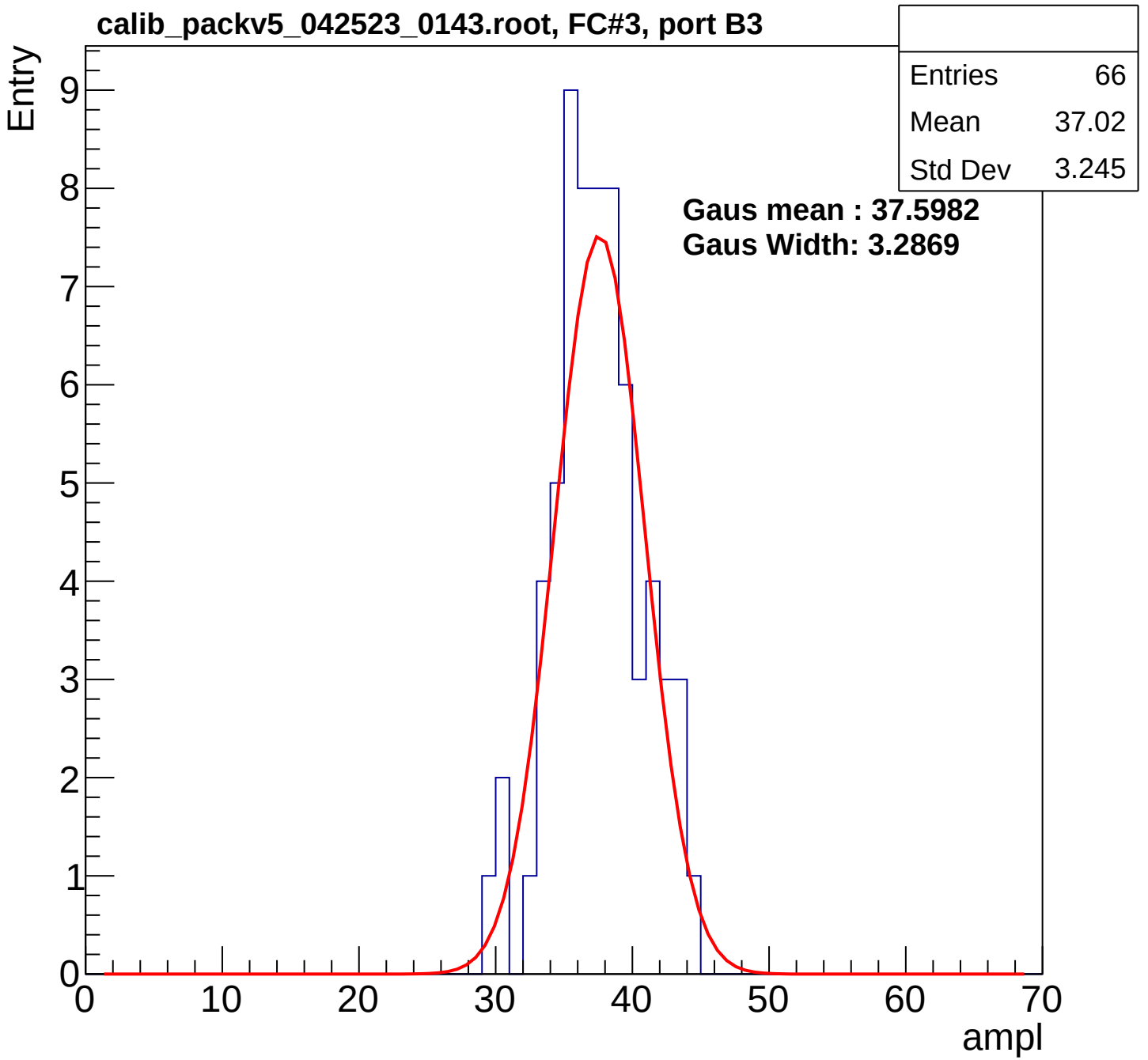
B1L000S, U3-ch15, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 37.02
Std Dev 3.245
Gaus mean : 37.5982
Gaus Width: 3.2869
Entry
ampl
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2
3
4
5
6
7
8
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0
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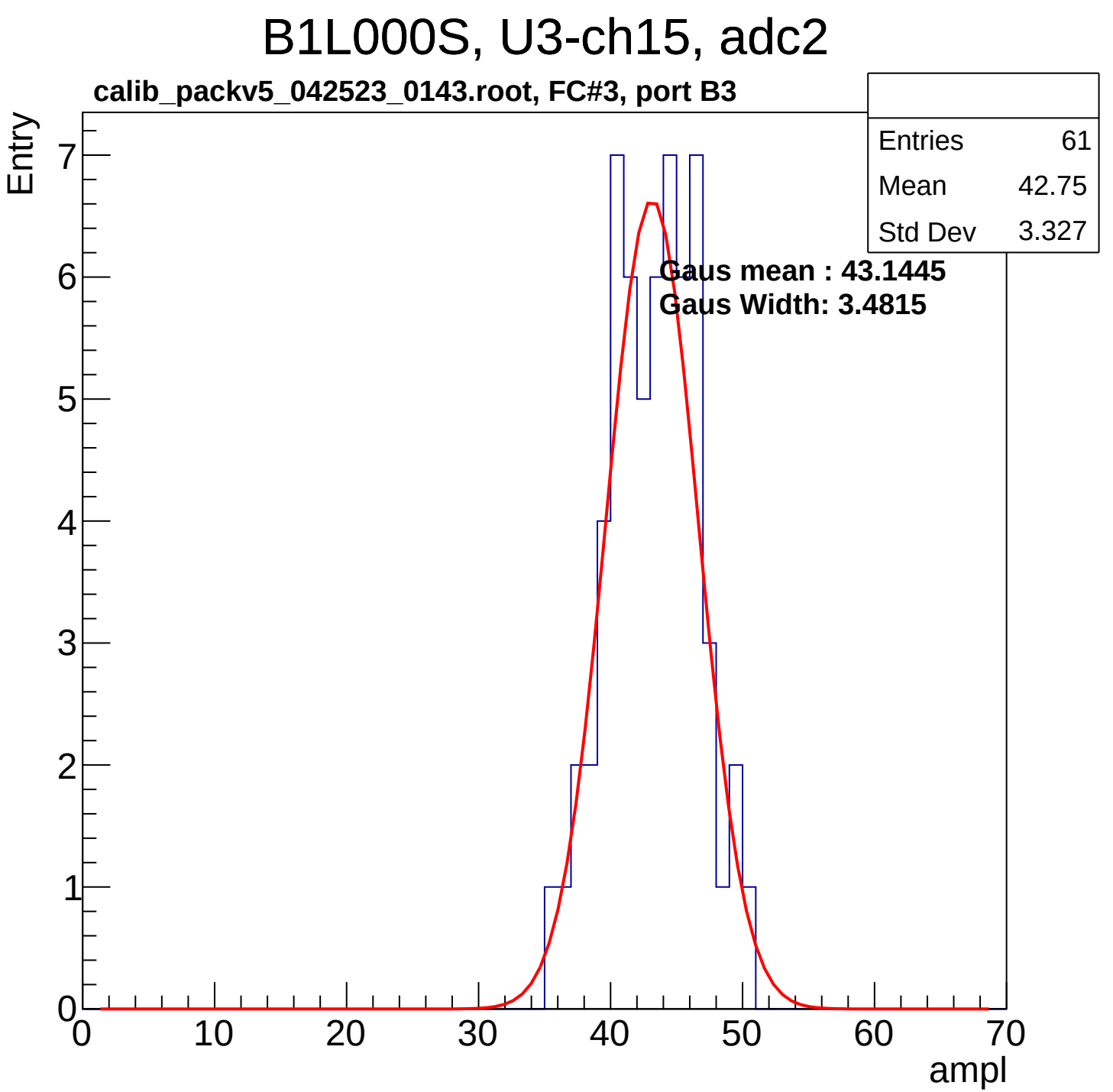
B1L000S, U3-ch15, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 42.75
Std Dev 3.327
Gaus mean : 43.1445
Gaus Width: 3.4815
Entry
ampl
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3
4
5
6
7
0
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50
60
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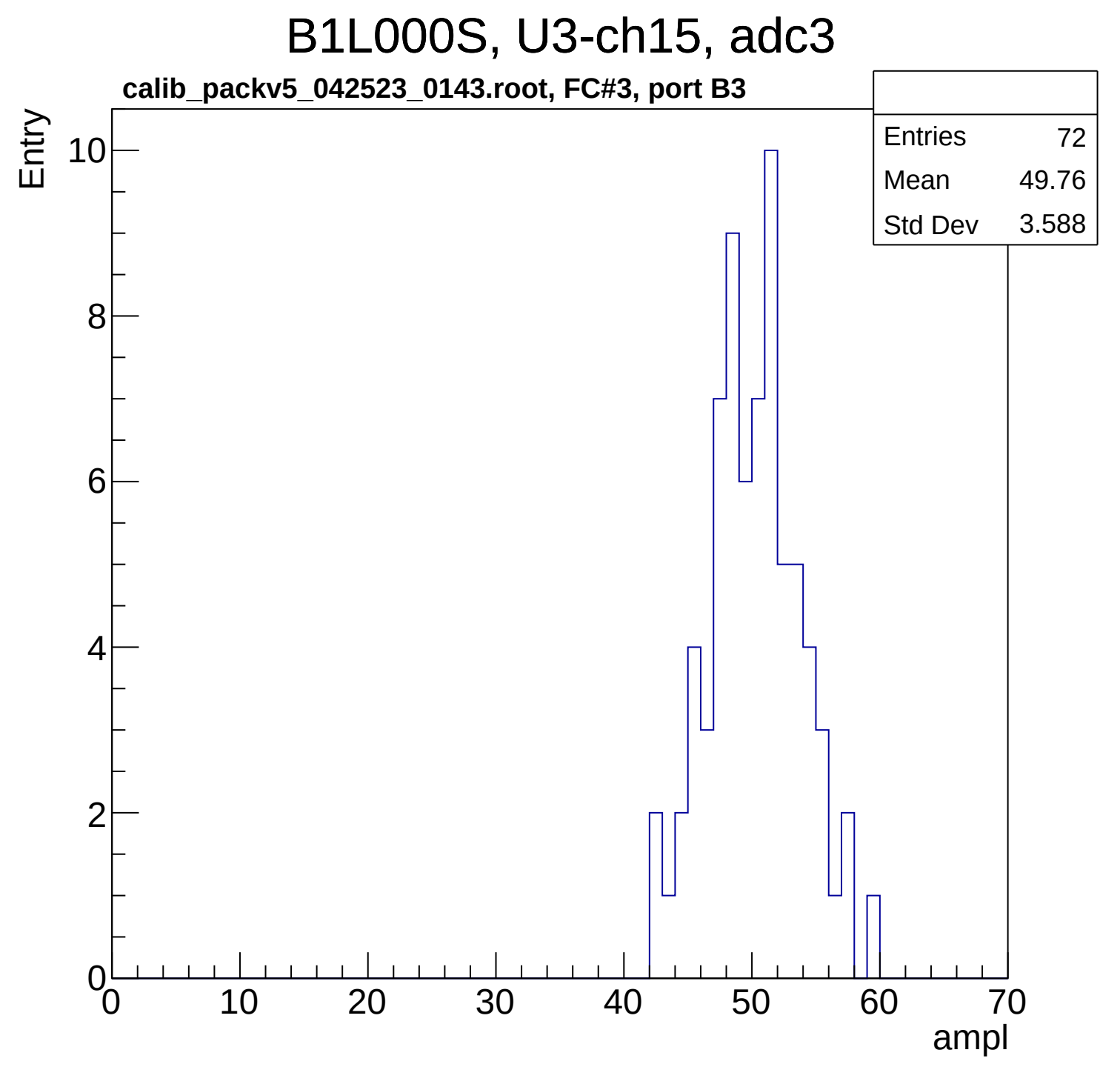
B1L000S, U3-ch15, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 49.76
Std Dev 3.588
Entry
ampl
0
2
4
6
8
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0
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60
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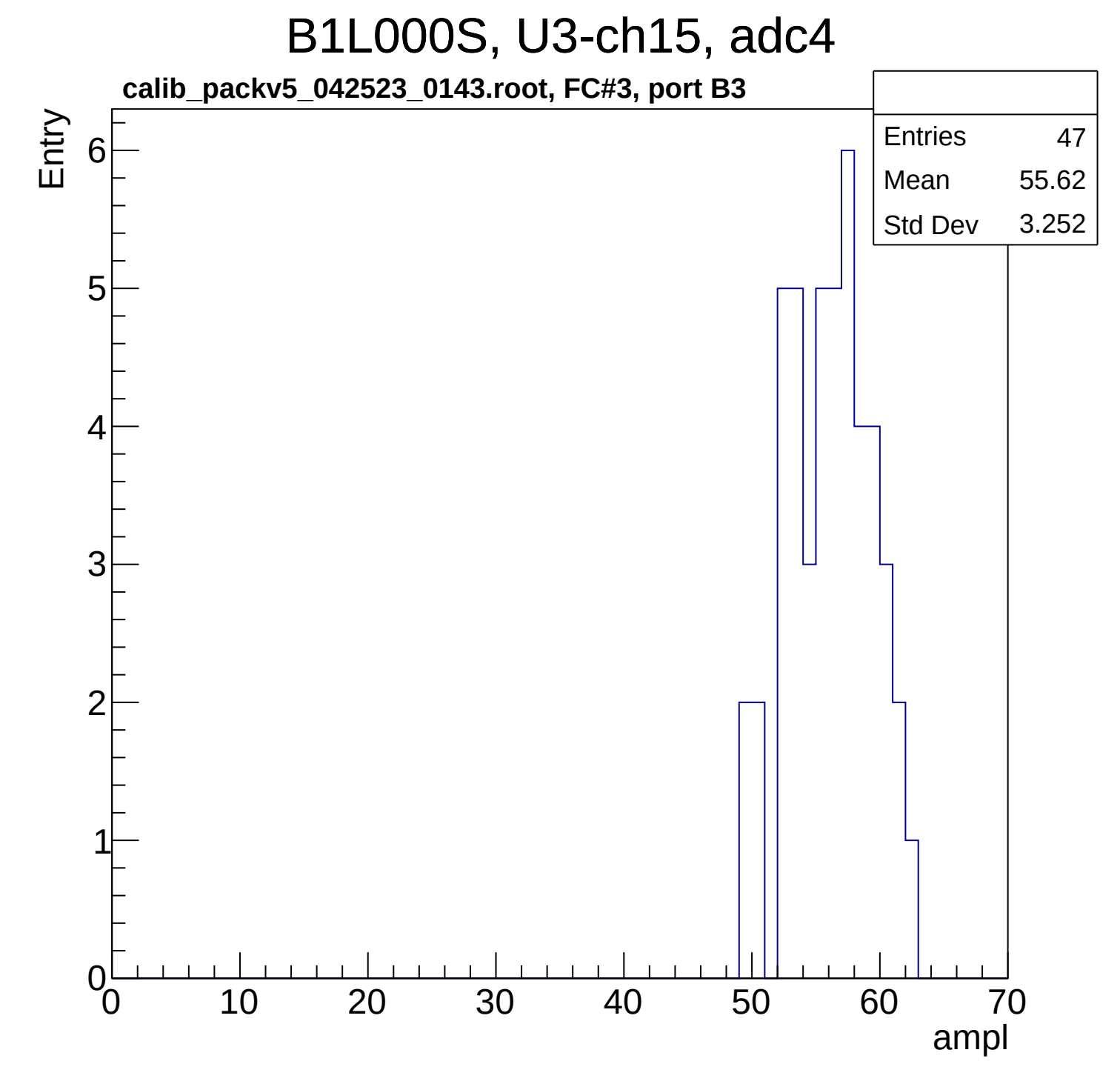
B1L000S, U3-ch15, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 55.62
Std Dev 3.252
Entry
0
1
2
3
4
5
6
0
10
20
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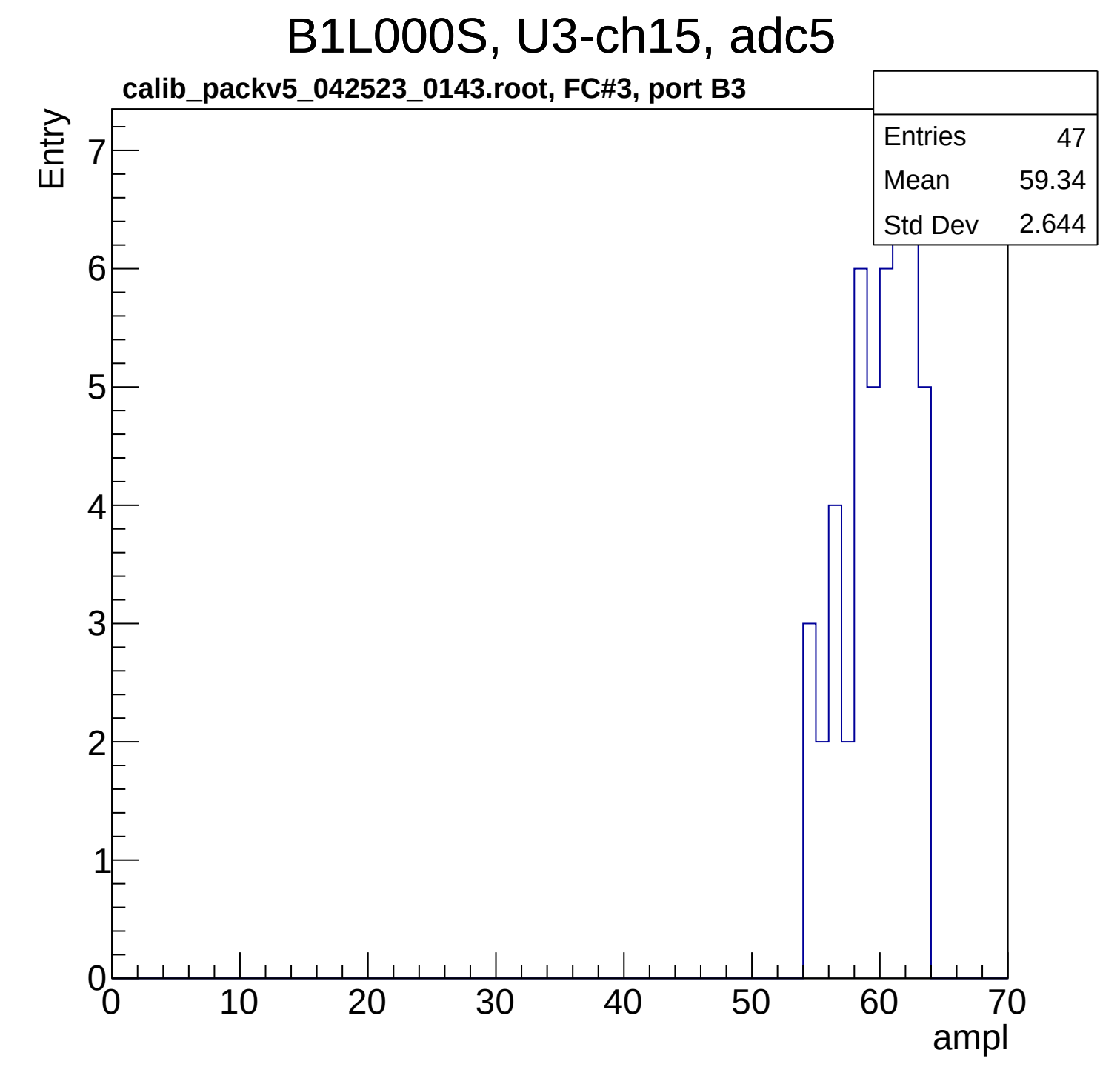
70
ampl


B1L000S, U3-ch15, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 59.34
Std Dev 2.644
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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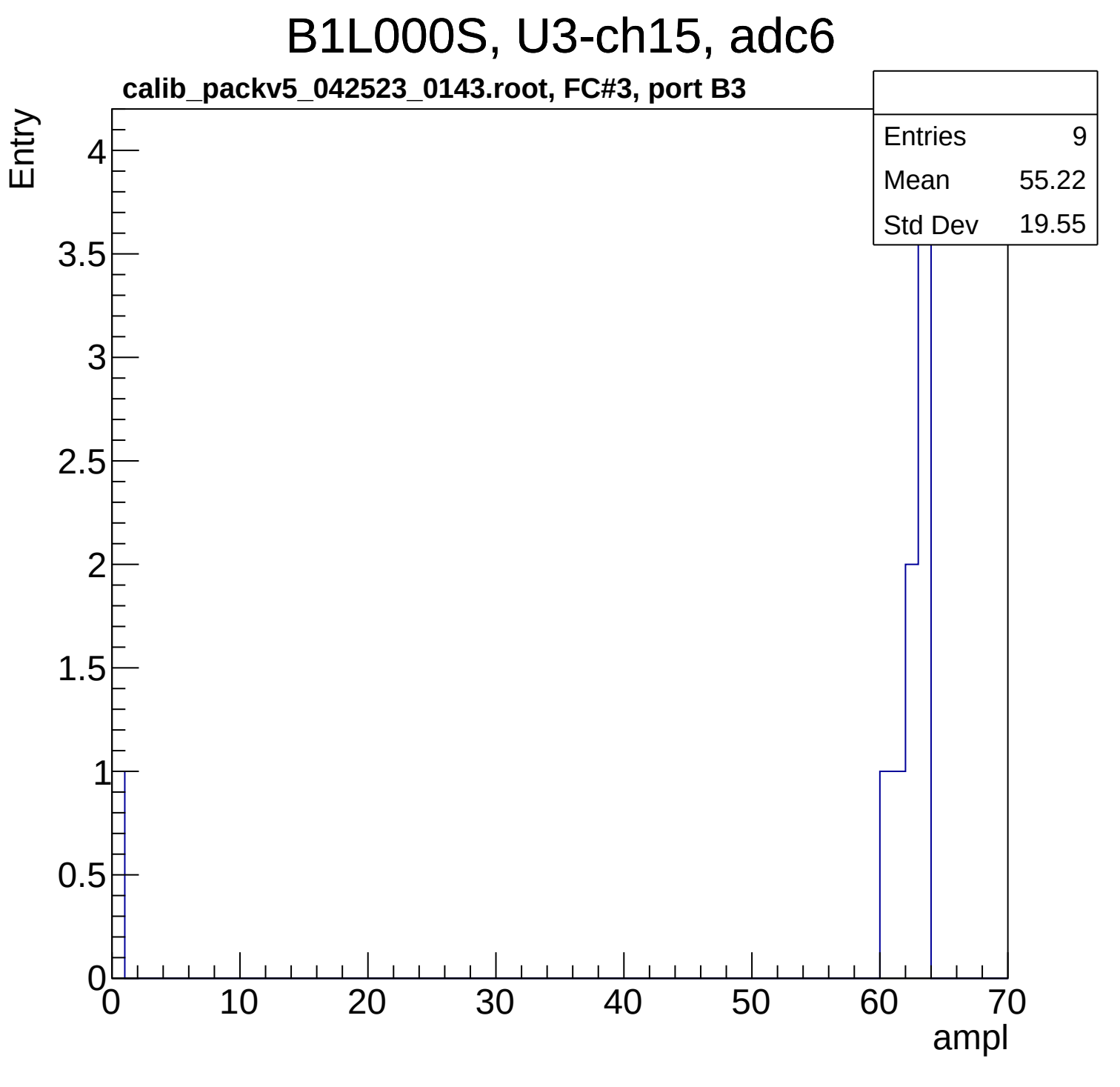
40
50
60
70


B1L000S, U3-ch15, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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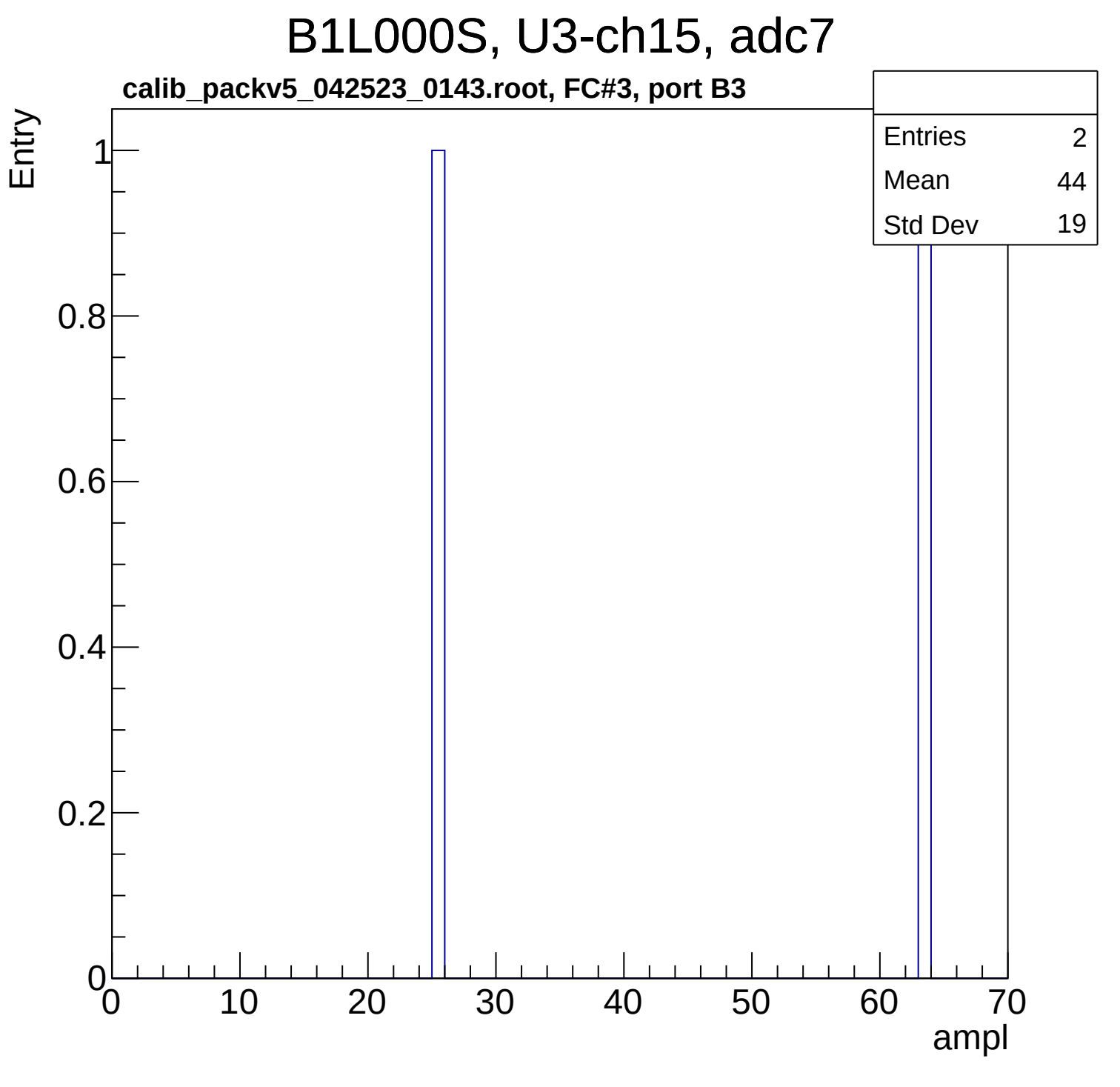
B1L000S, U3-ch15, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 44
Std Dev 19
Entry
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0.8
0.6
0.4
0.2
0
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10
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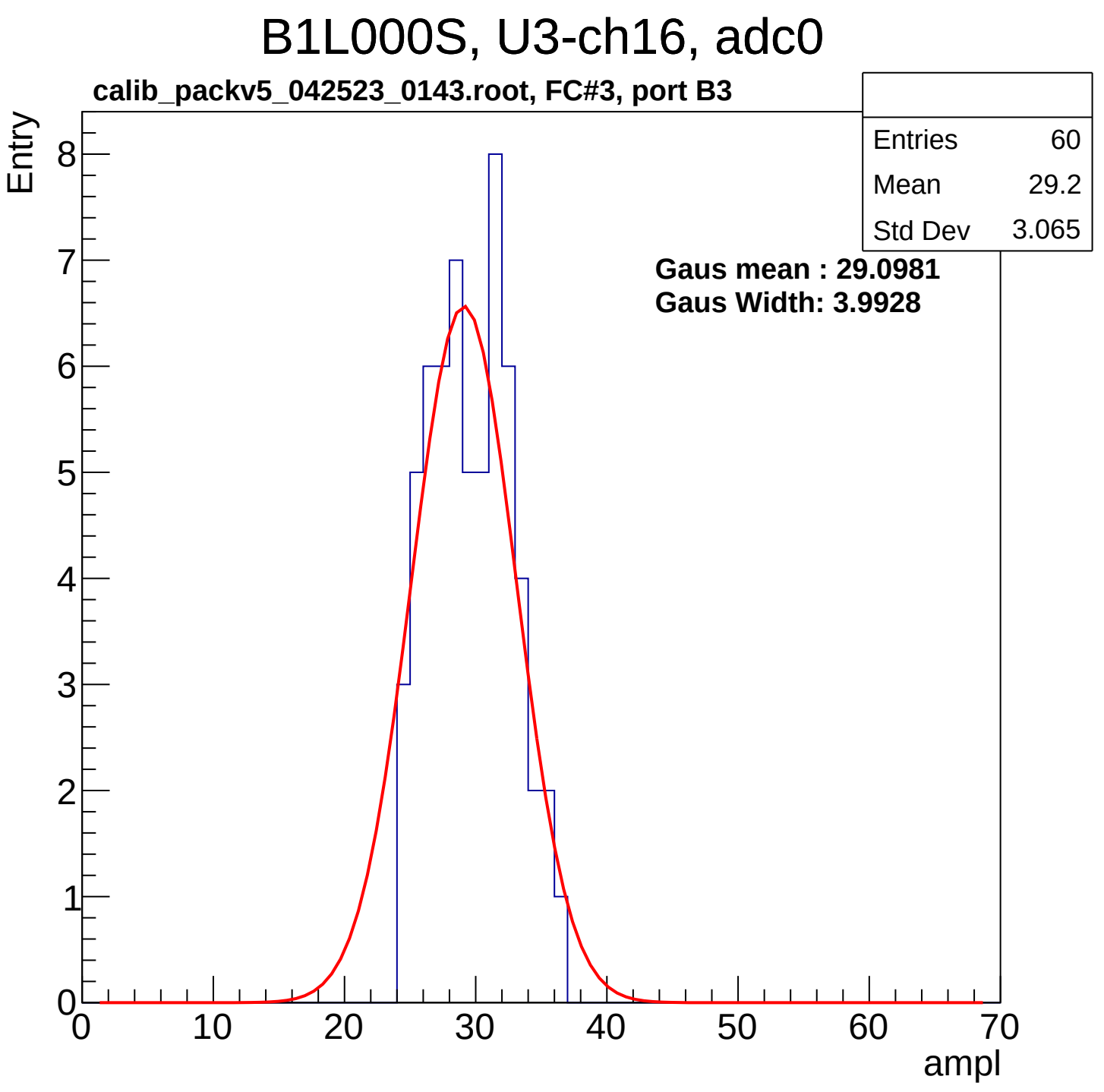
40
50
60
70
ampl


B1L000S, U3-ch16, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 29.2
Std Dev 3.065
Gaus mean : 29.0981
Gaus Width: 3.9928
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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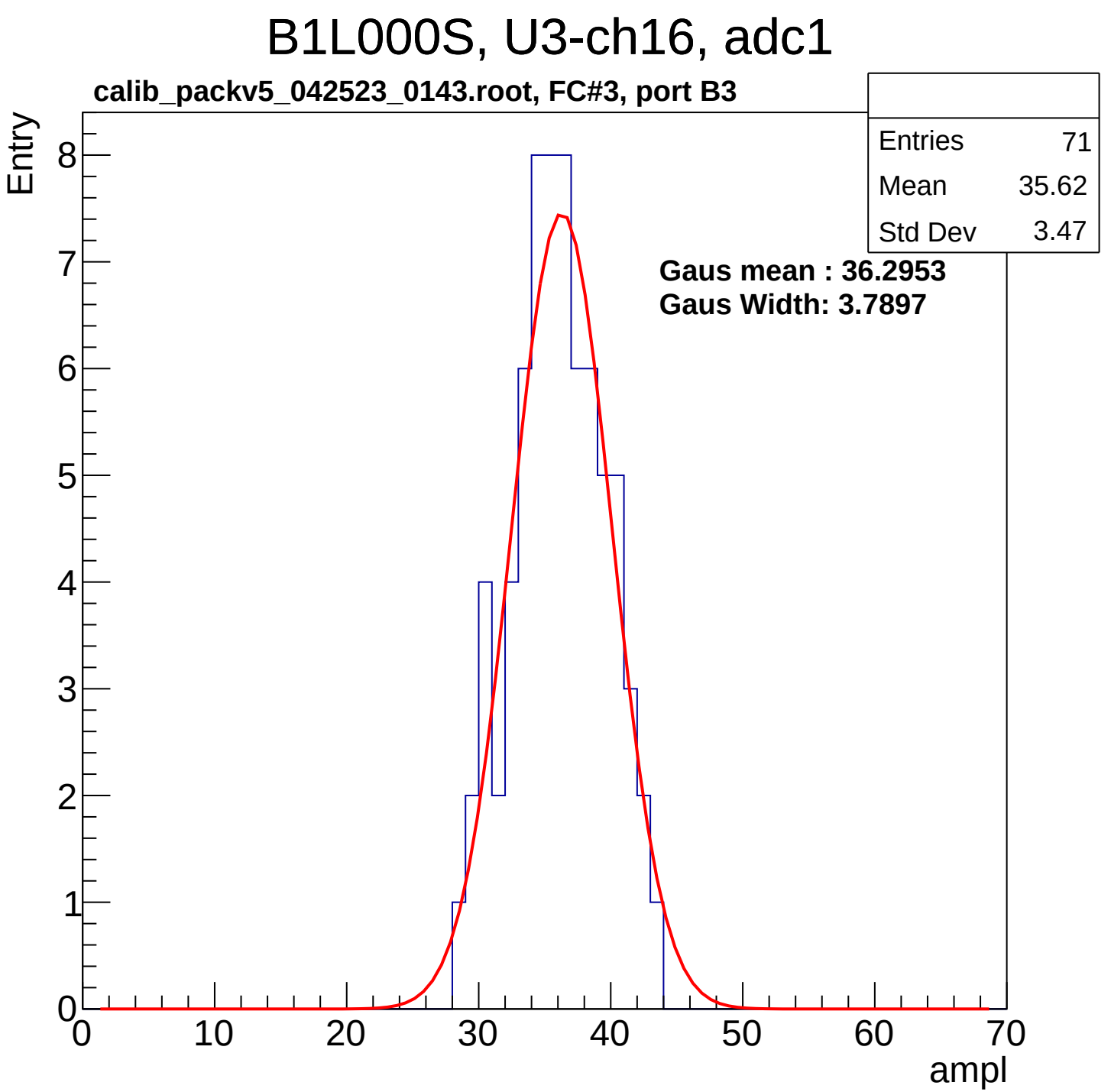
B1L000S, U3-ch16, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.62
Std Dev 3.47
Gaus mean : 36.2953
Gaus Width: 3.7897
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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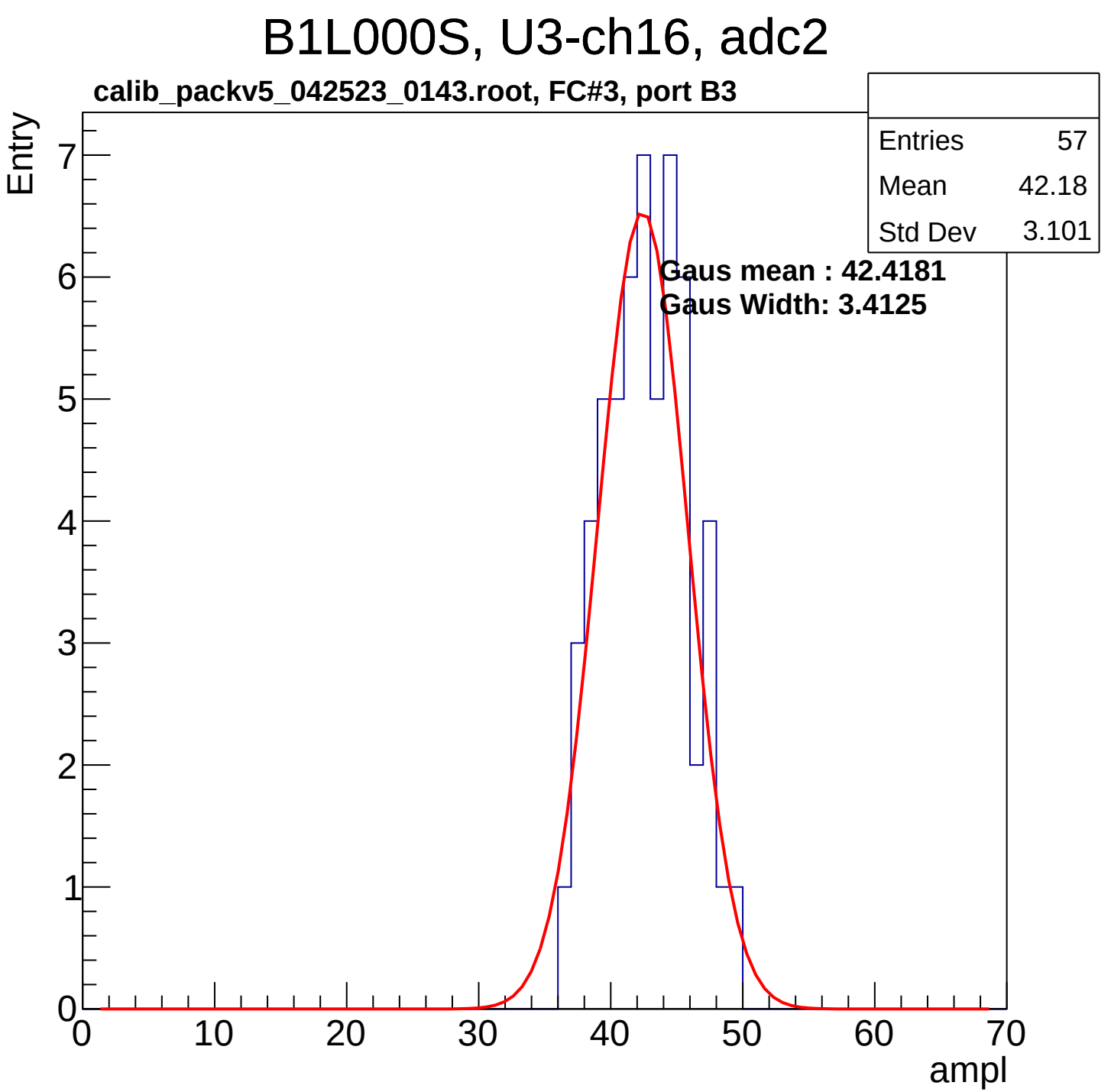
B1L000S, U3-ch16, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 42.18
Std Dev 3.101
Gaus mean : 42.4181
Gaus Width: 3.4125
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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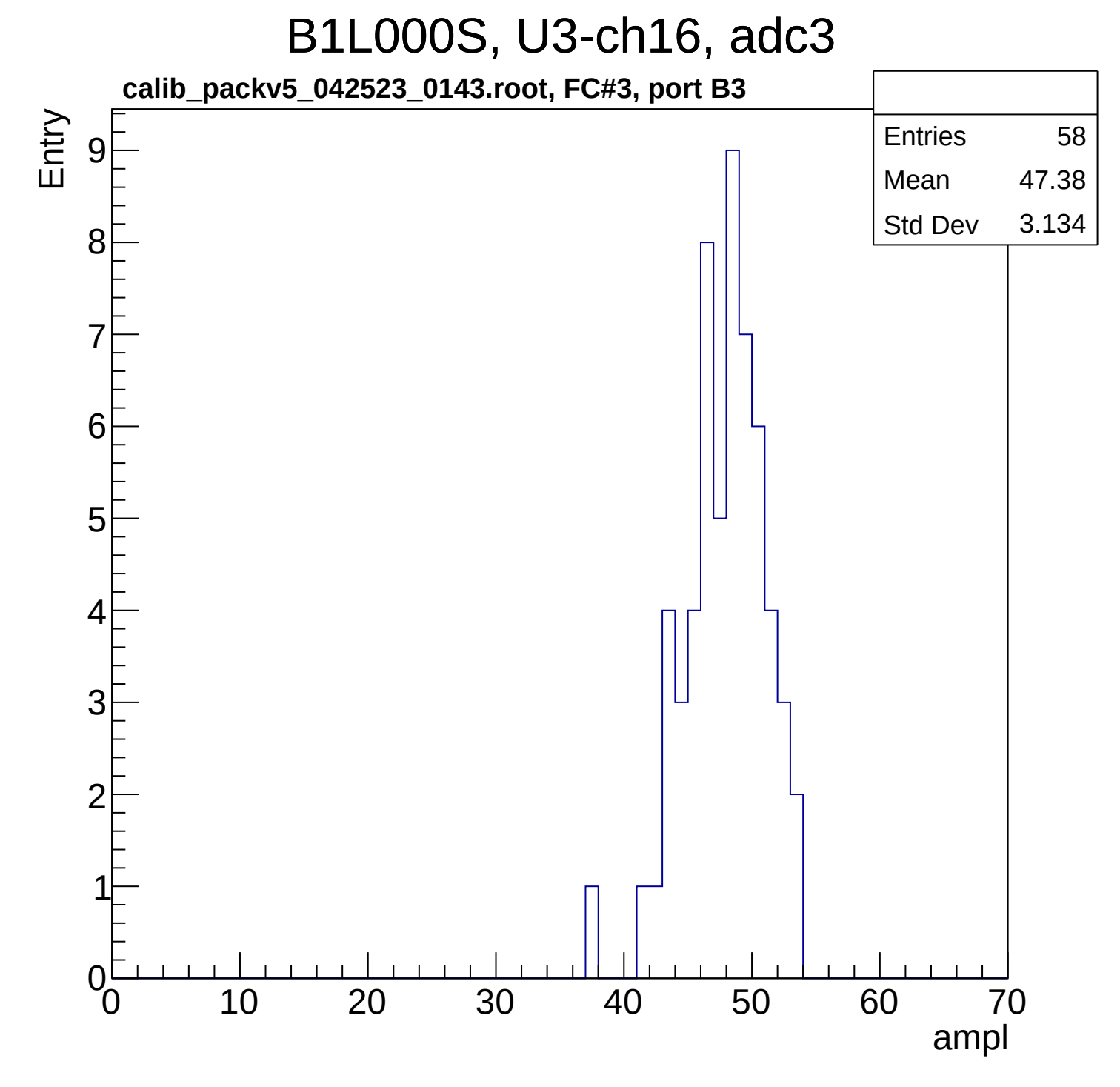
B1L000S, U3-ch16, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 47.38
Std Dev 3.134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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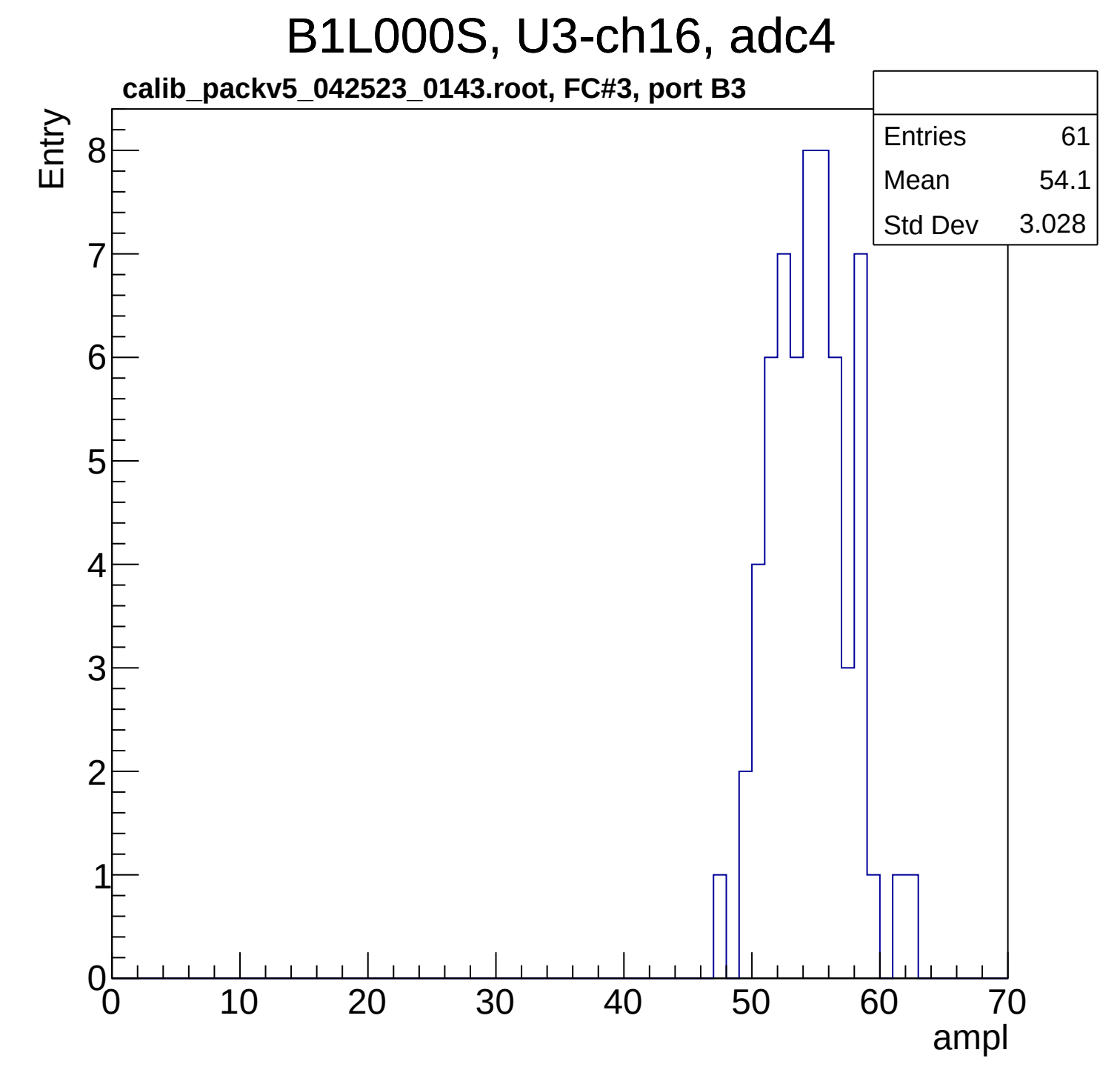
B1L000S, U3-ch16, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 54.1
Std Dev 3.028
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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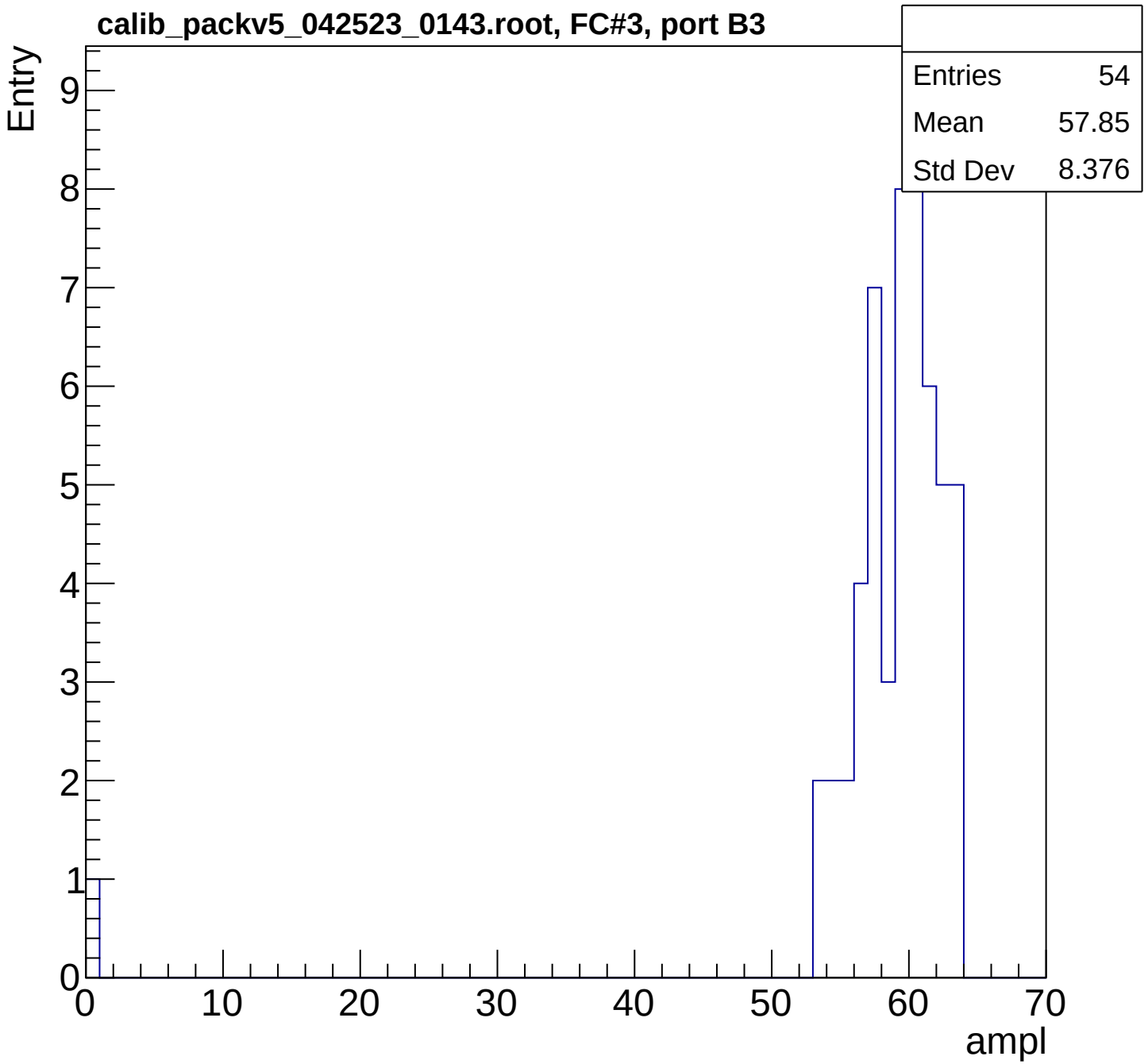
B1L000S, U3-ch16, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 57.85
Std Dev 8.376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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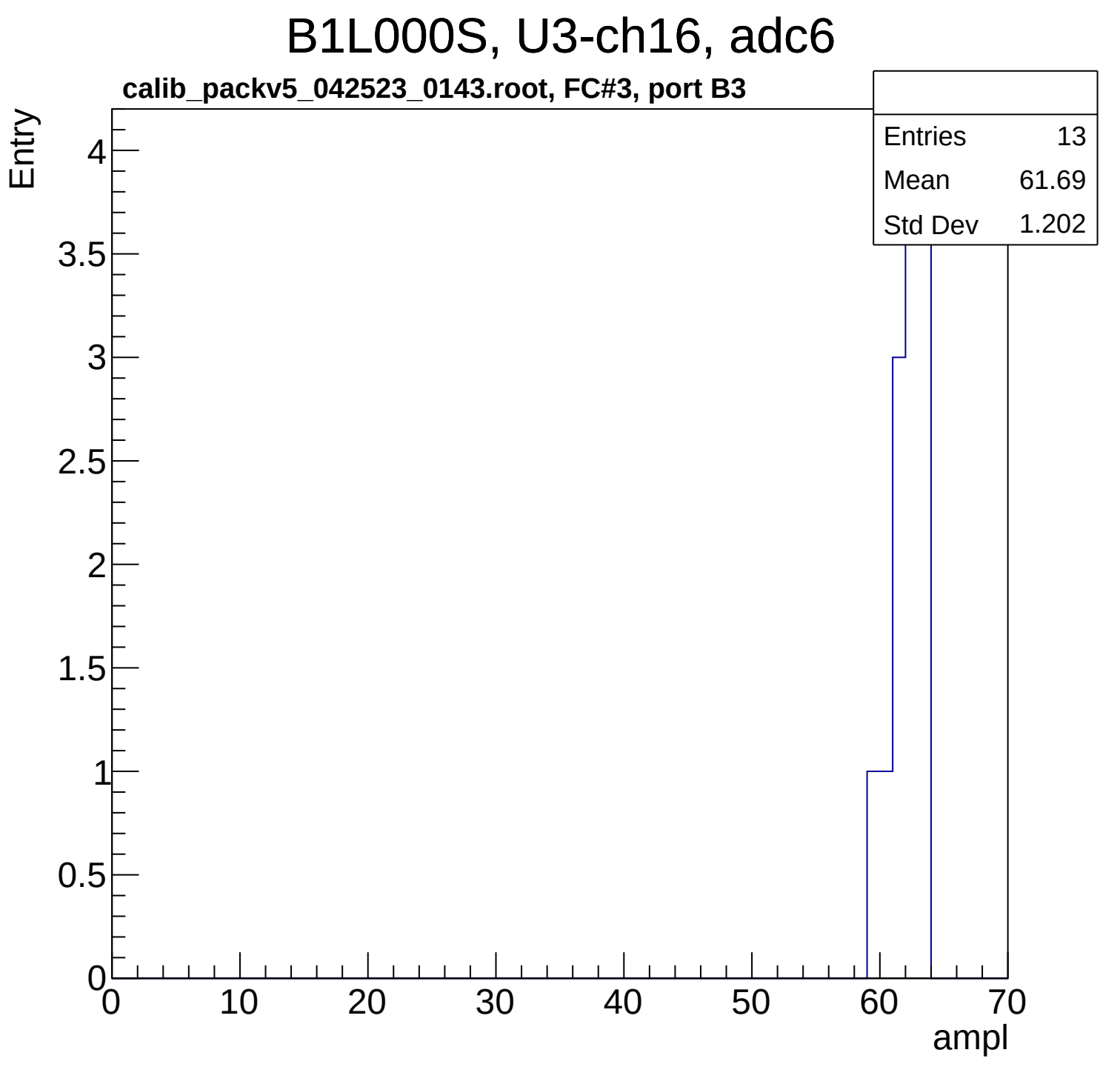
B1L000S, U3-ch16, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.69
Std Dev 1.202
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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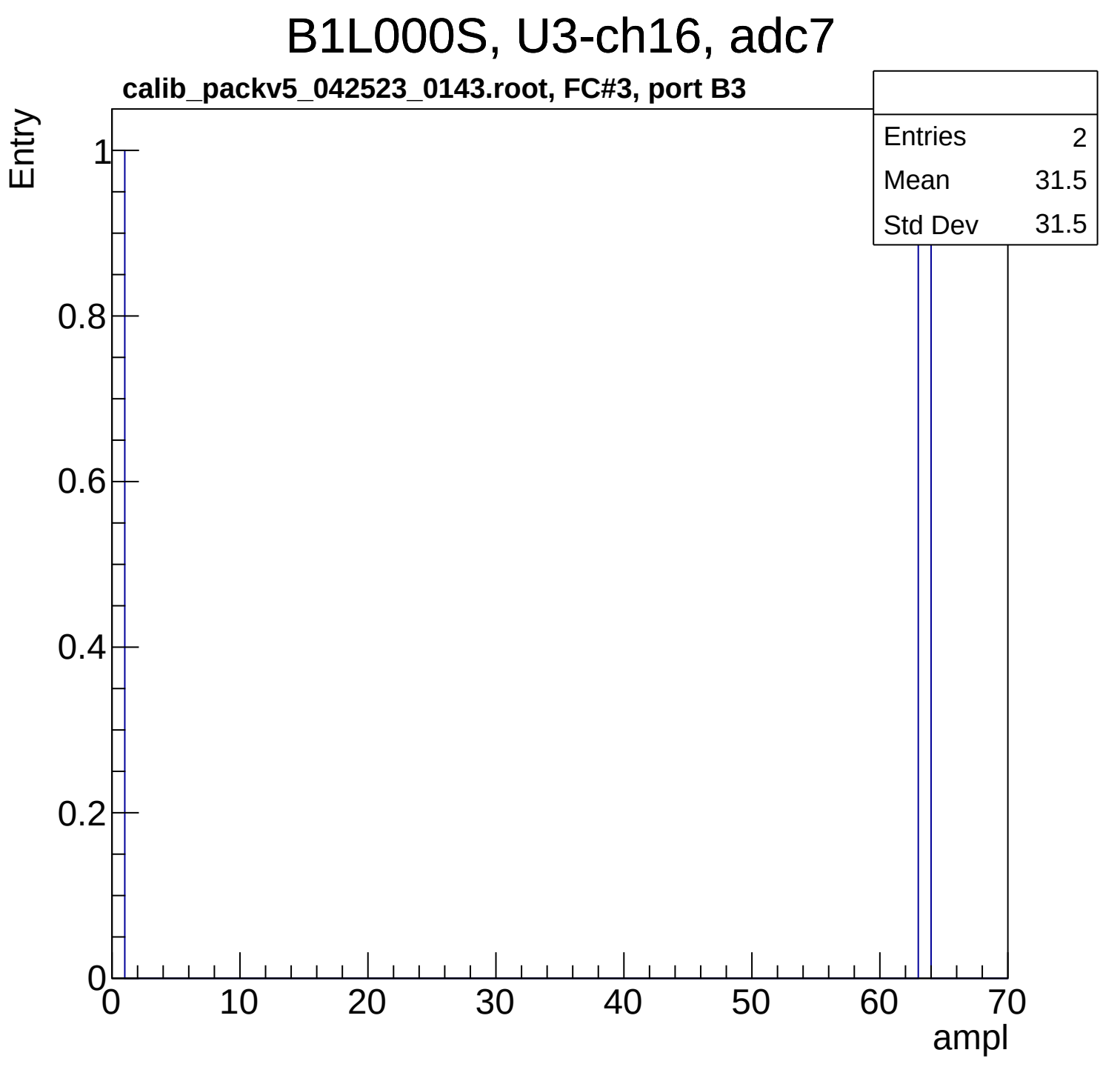
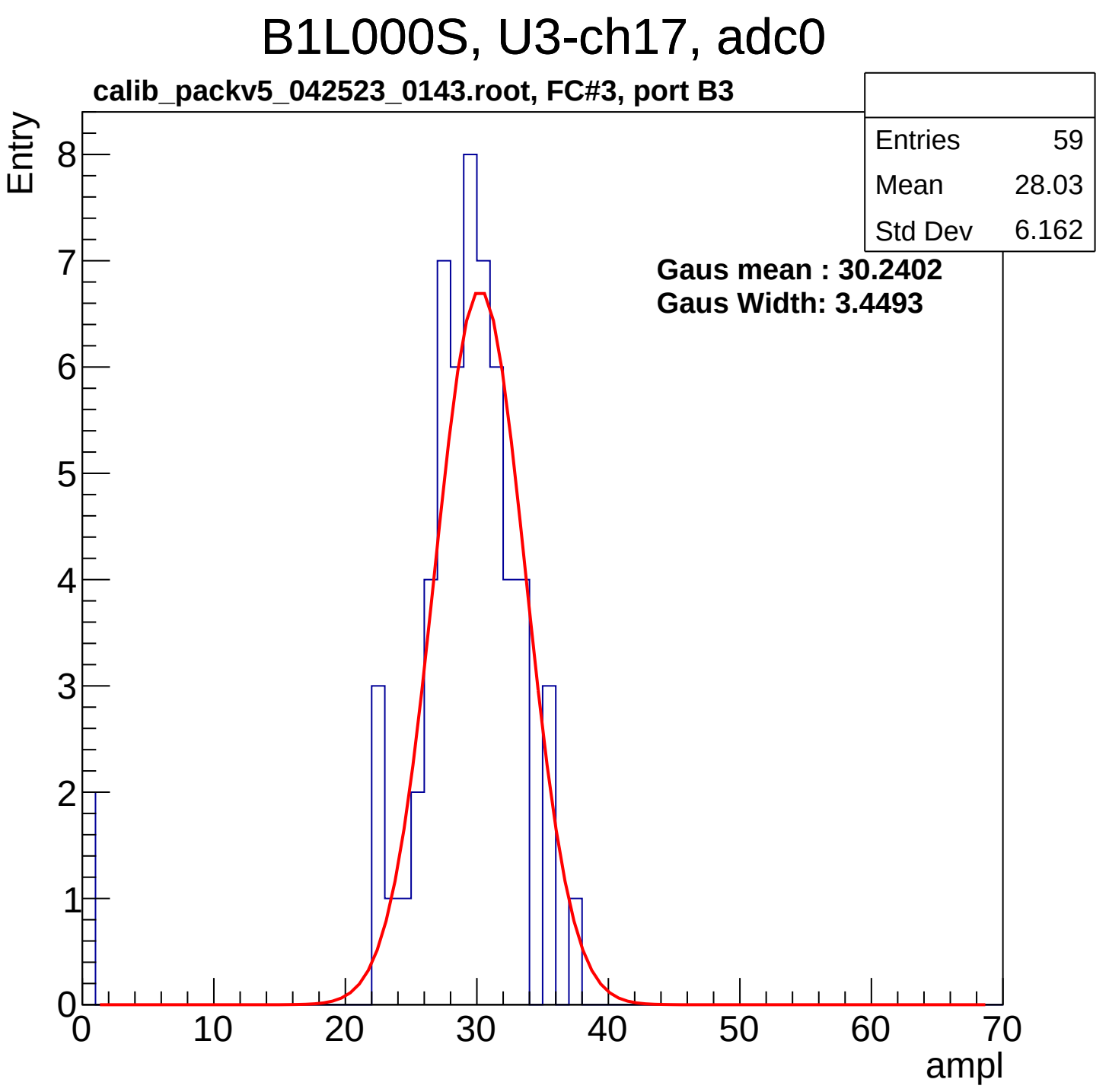
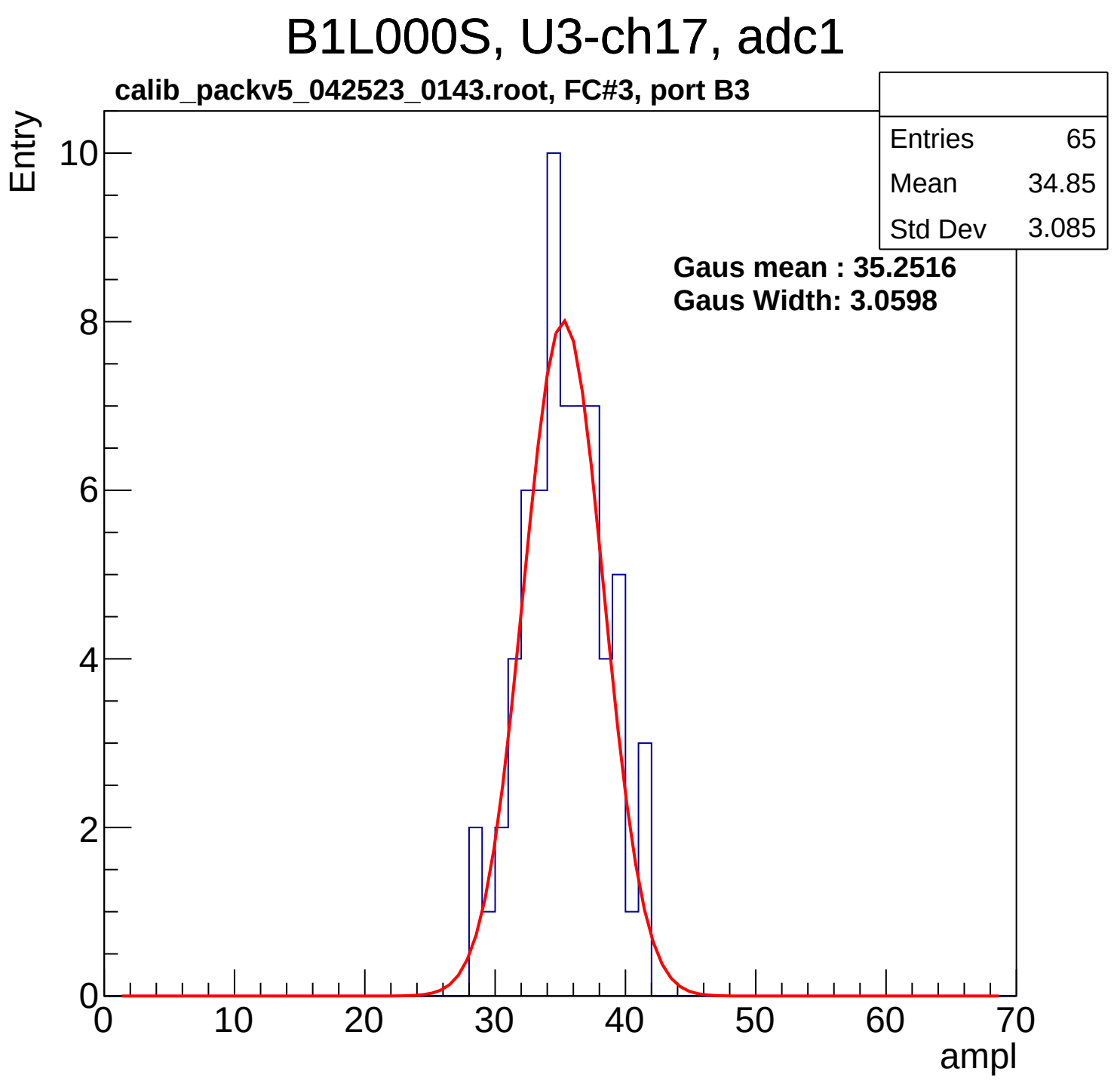
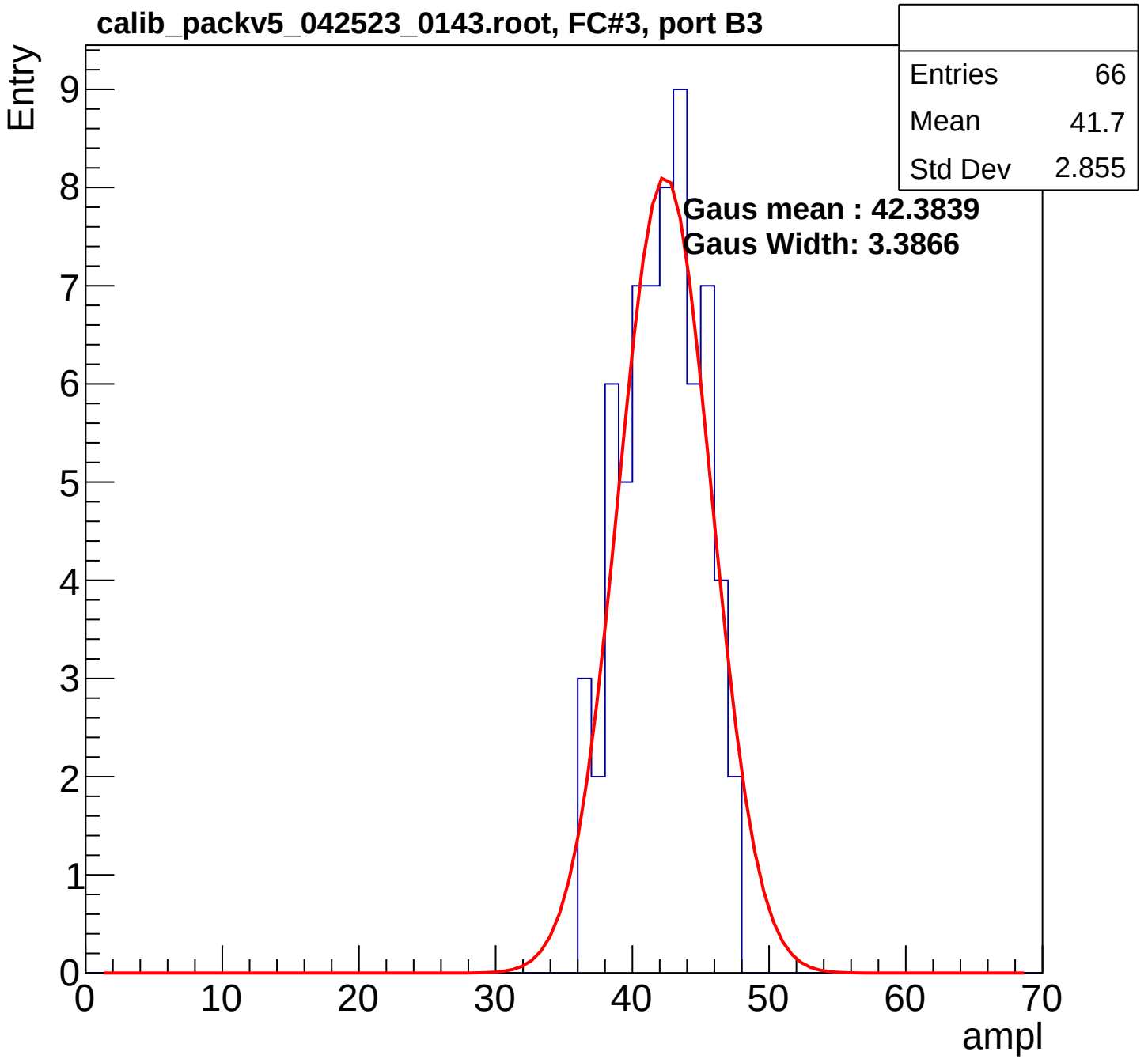

B1L000S, U3-ch16, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch17, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 28.03
Std Dev 6.162
Gaus mean : 30.2402
Gaus Width: 3.4493
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch17, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 34.85
Std Dev 3.085
Gaus mean : 35.2516
Gaus Width: 3.0598
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch17, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 41.7
Std Dev 2.855
Gaus mean : 42.3839
Gaus Width: 3.3866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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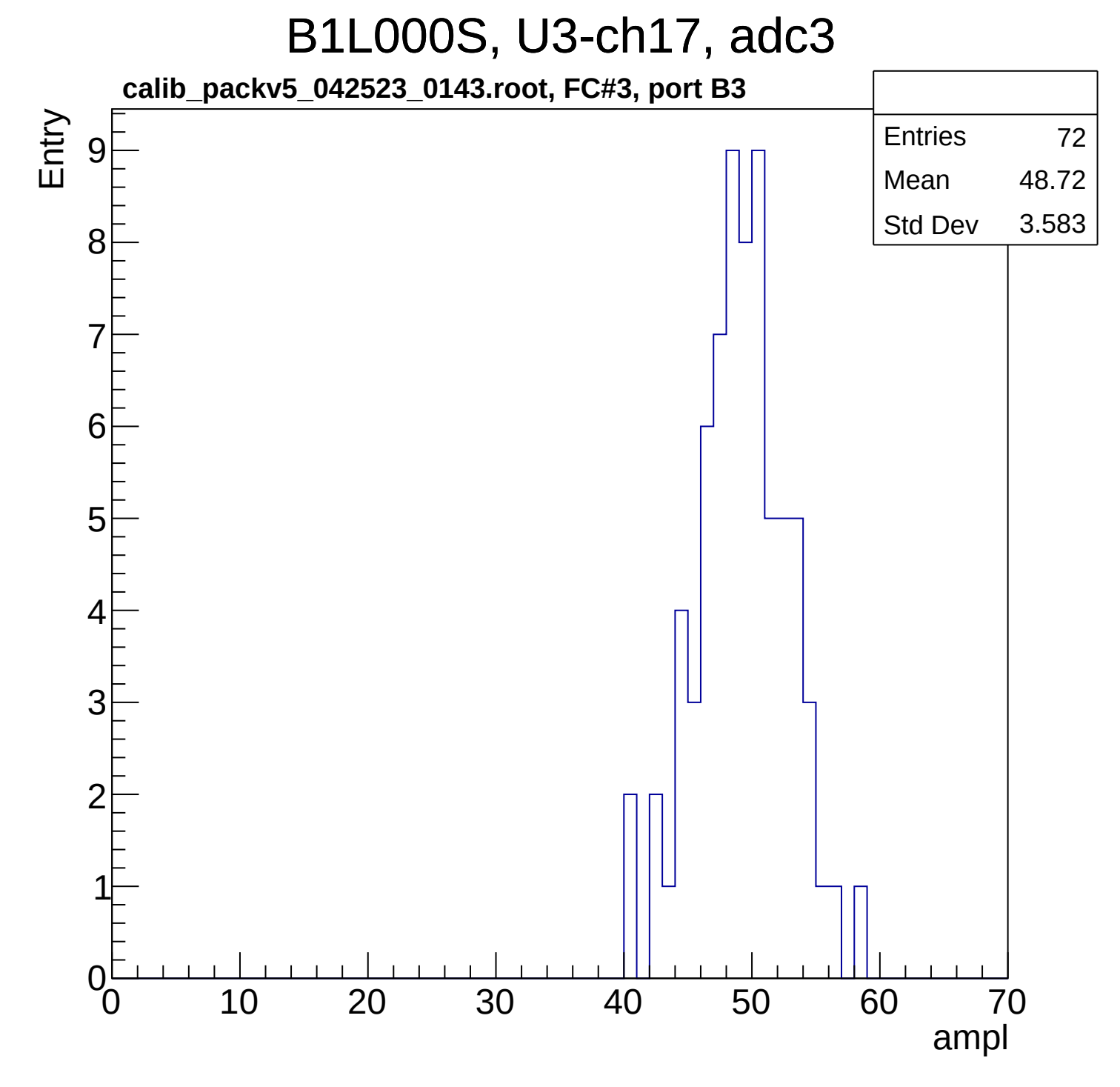
B1L000S, U3-ch17, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 48.72
Std Dev 3.583
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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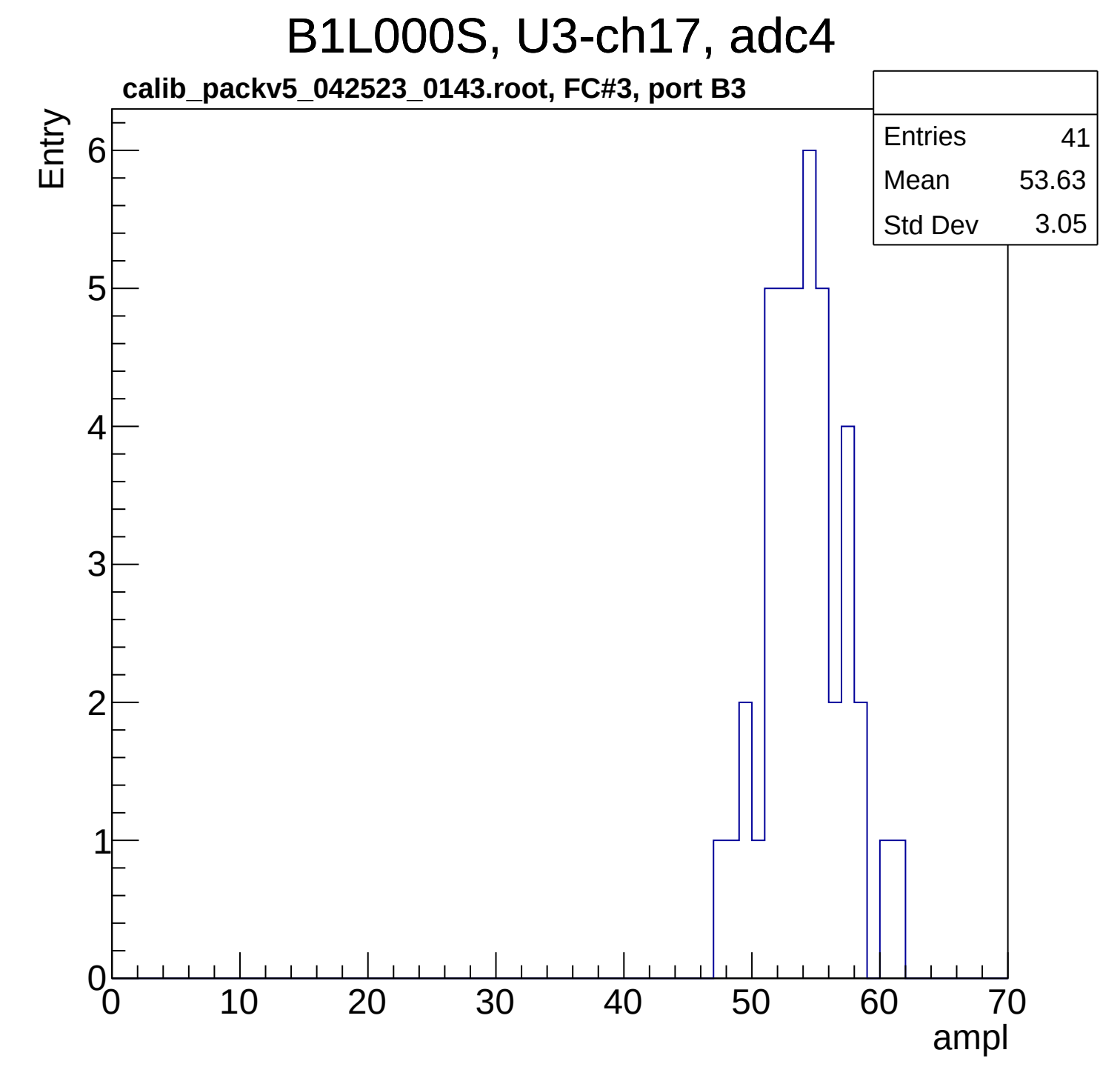
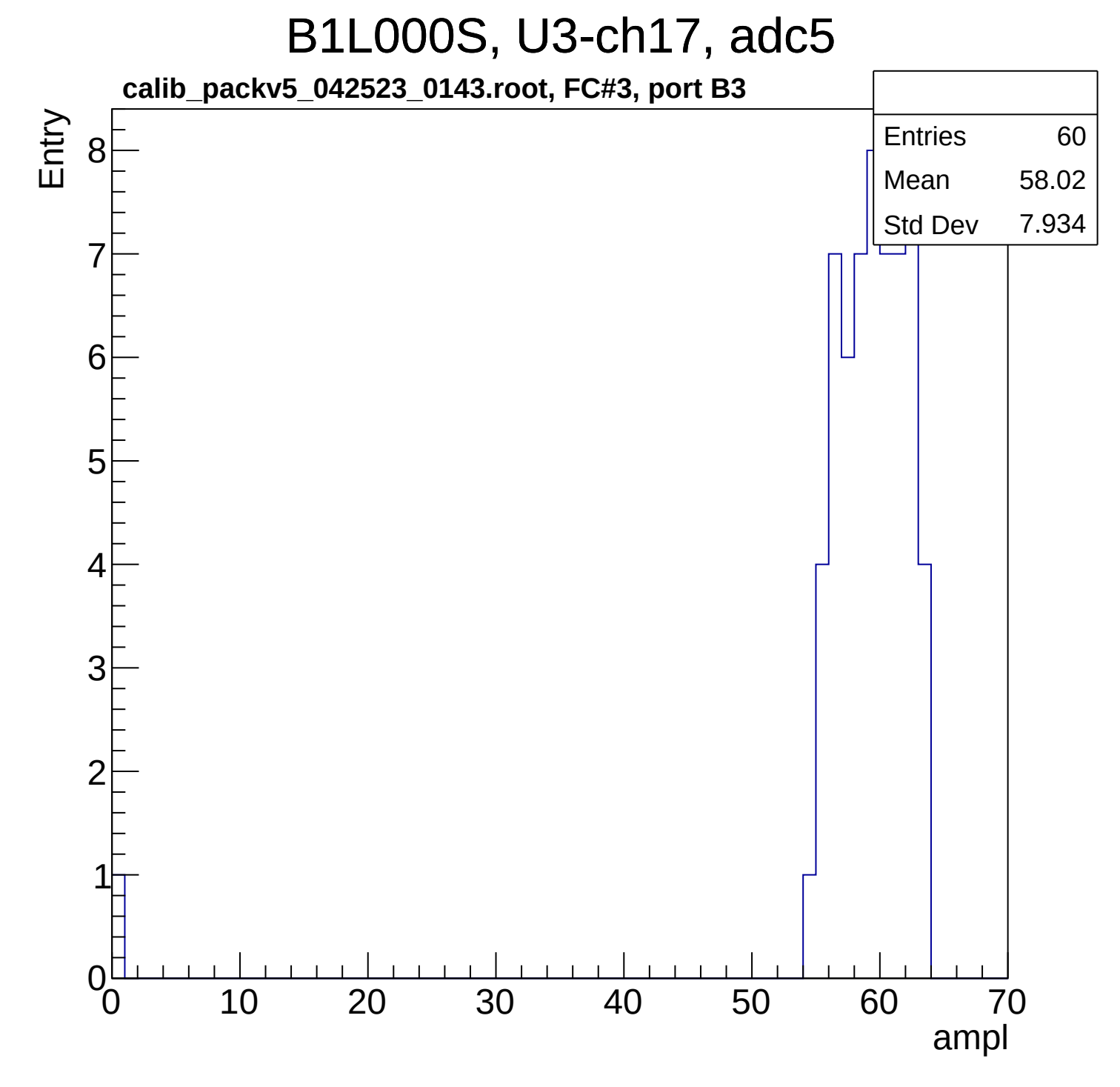

B1L000S, U3-ch17, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 53.63
Std Dev 3.05
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch17, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.02
Std Dev 7.934
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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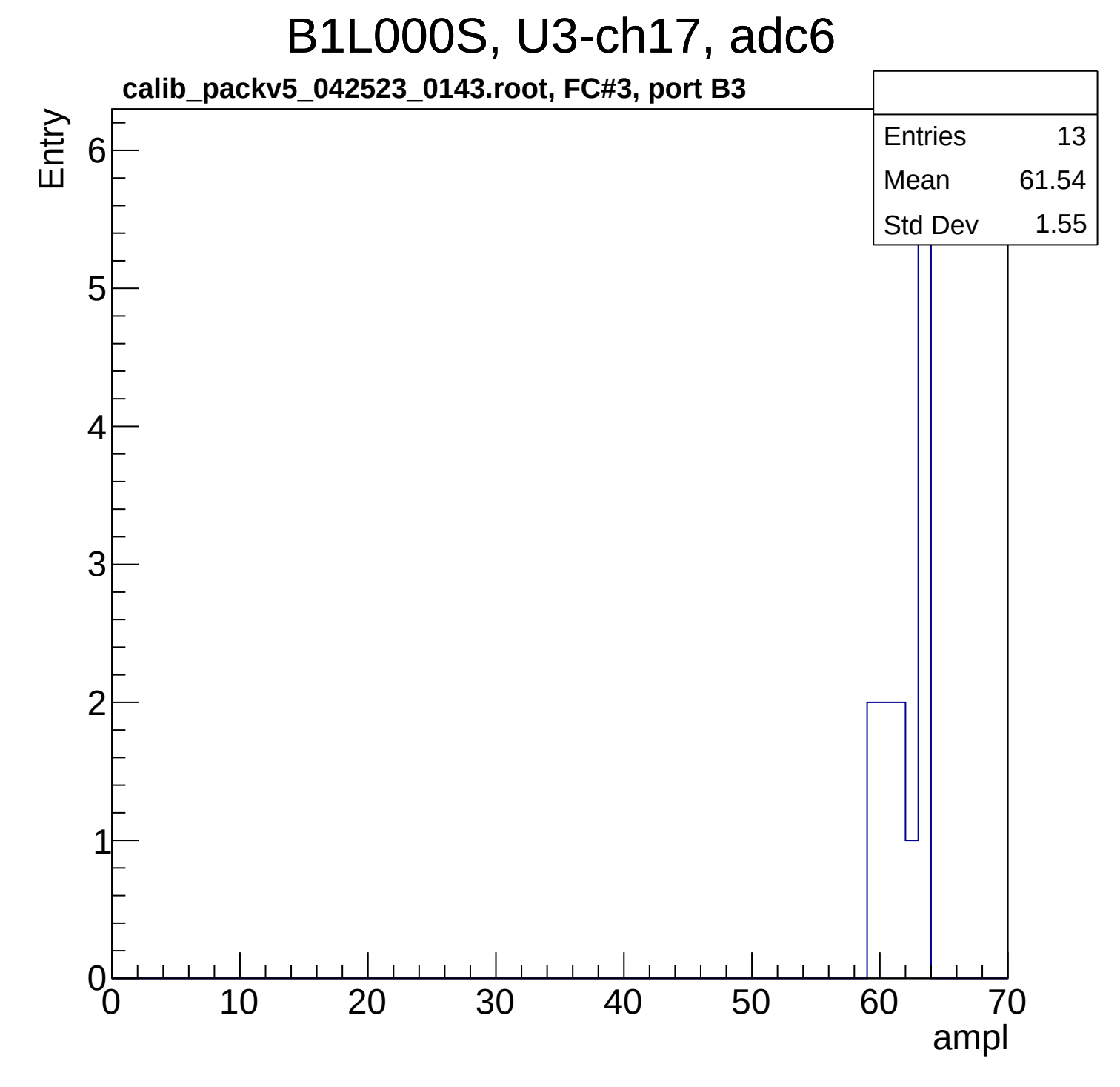
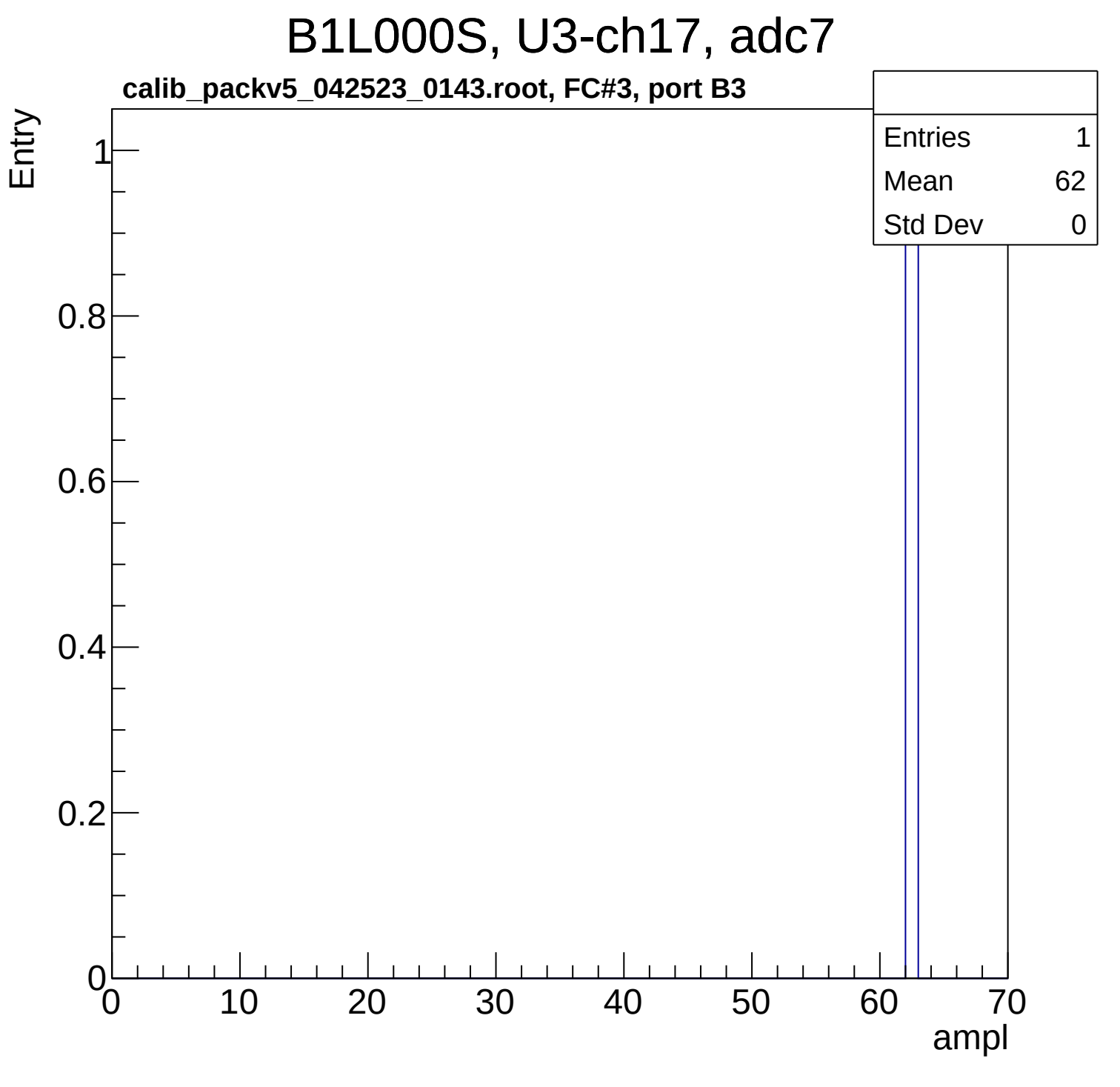

B1L000S, U3-ch17, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.54
Std Dev 1.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch17, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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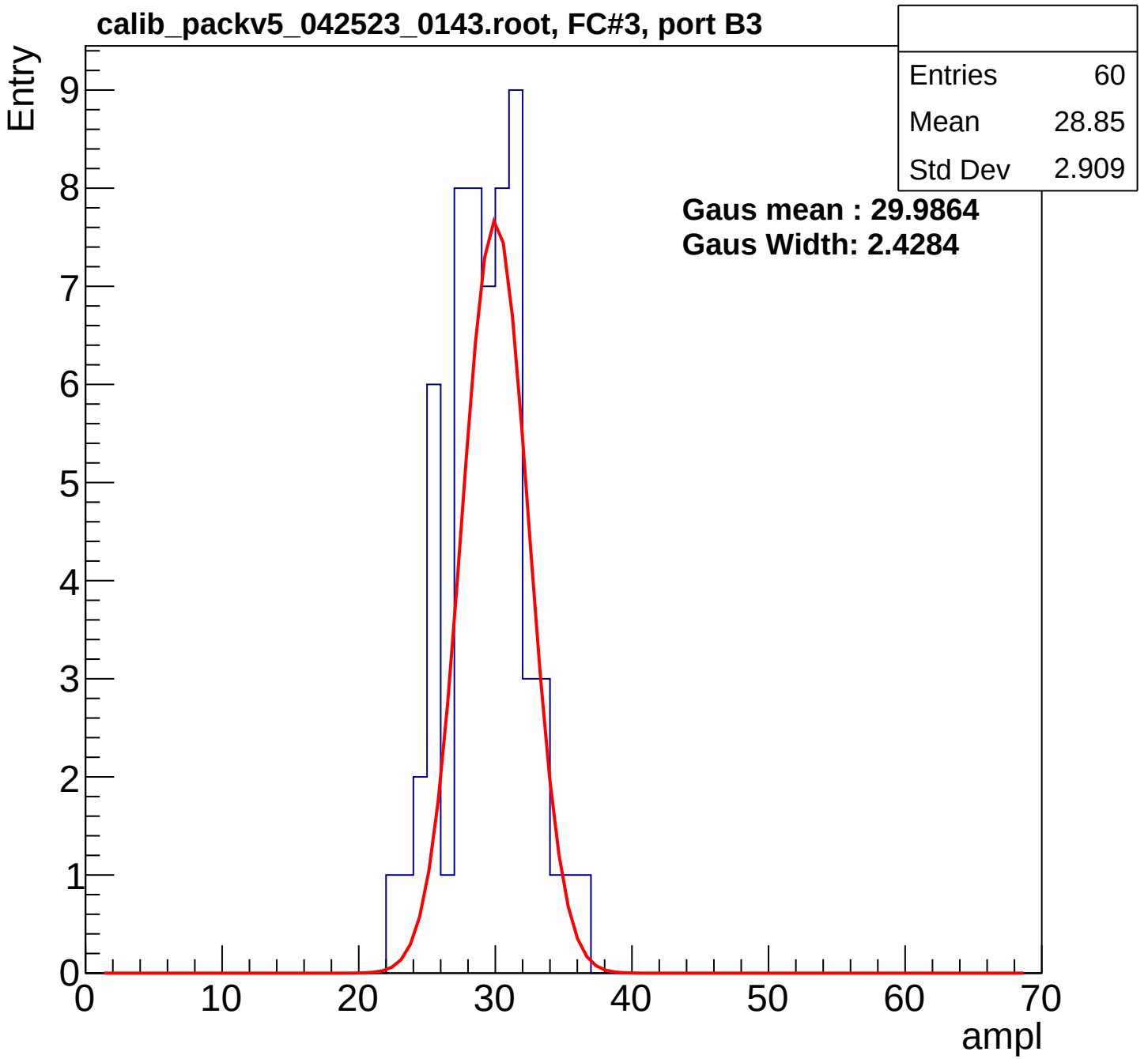
50
60
70
ampl


B1L000S, U3-ch18, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 28.85
Std Dev 2.909
Gaus mean : 29.9864
Gaus Width: 2.4284
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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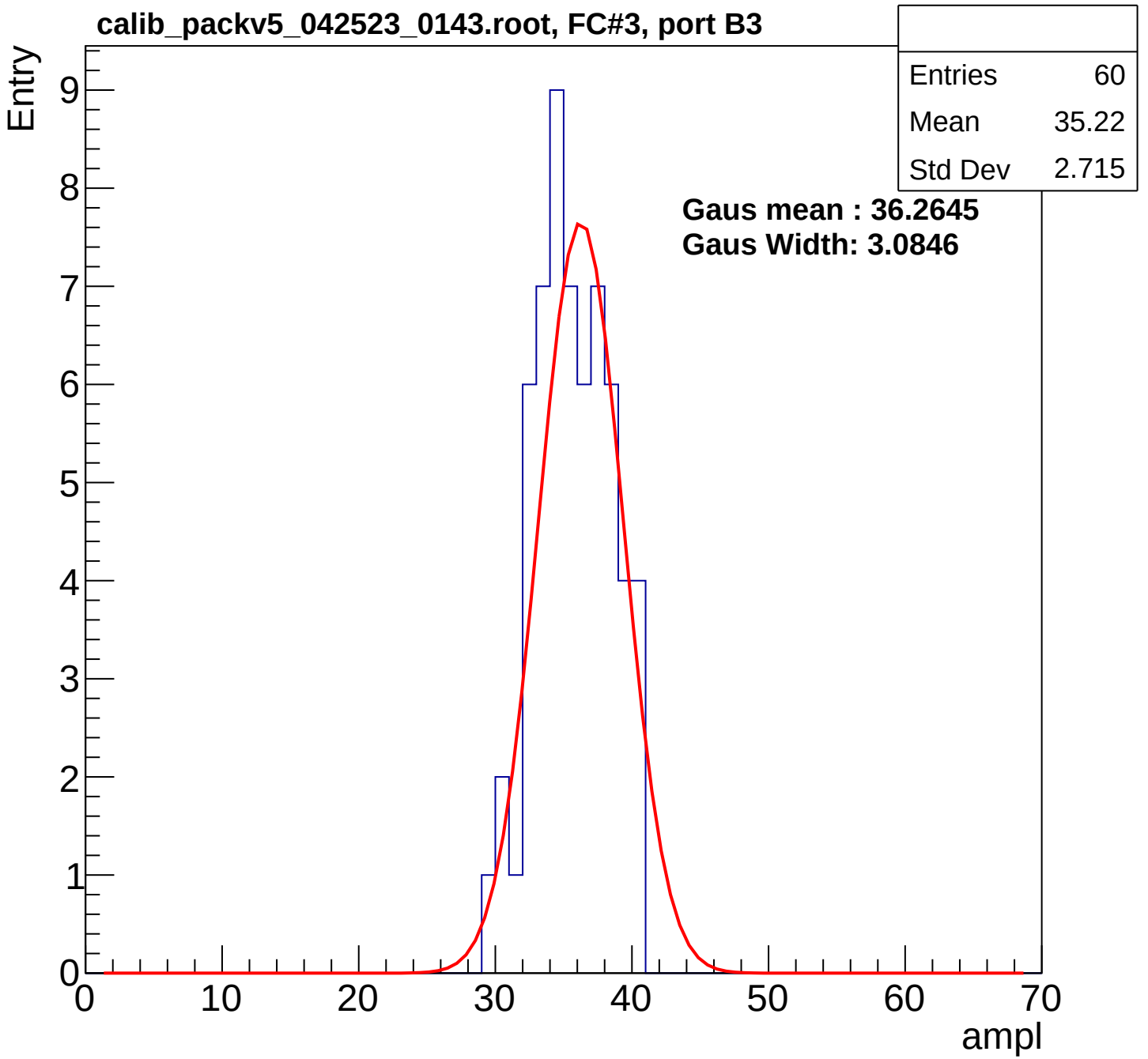
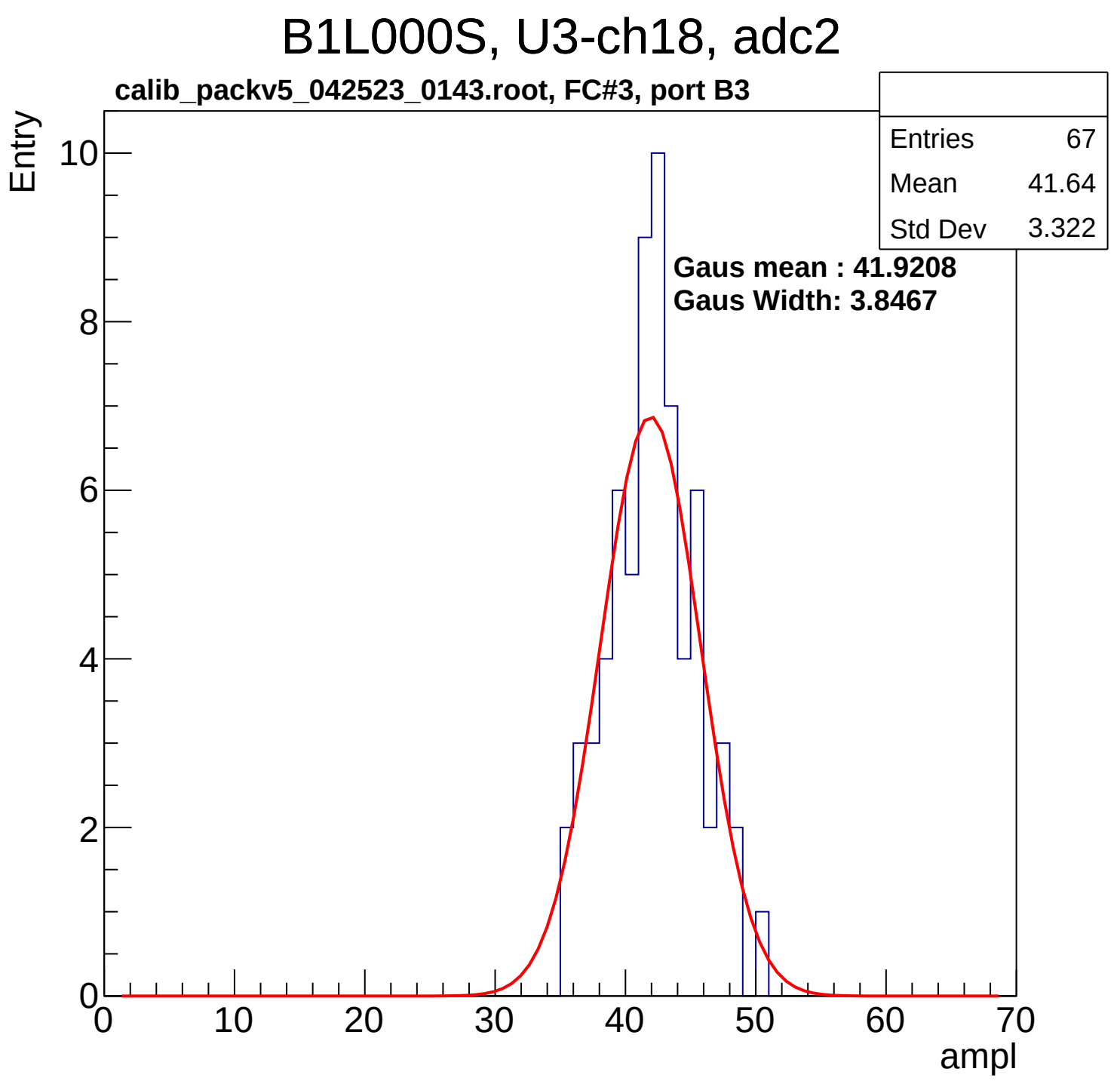
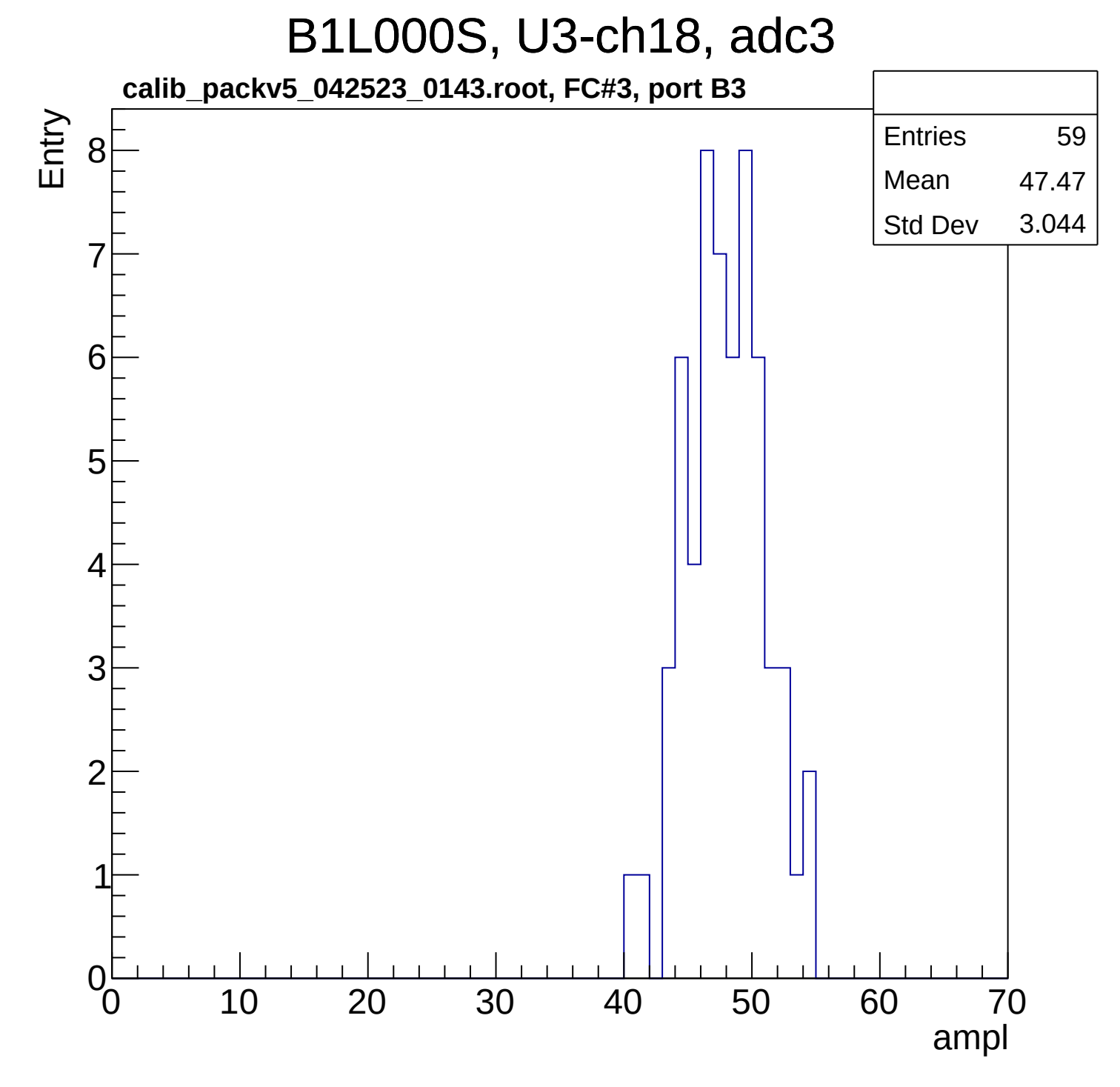

B1L000S, U3-ch18, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 35.22
Std Dev 2.715
Gaus mean : 36.2645
Gaus Width: 3.0846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch18, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.64
Std Dev 3.322
Gaus mean : 41.9208
Gaus Width: 3.8467
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch18, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 47.47
Std Dev 3.044
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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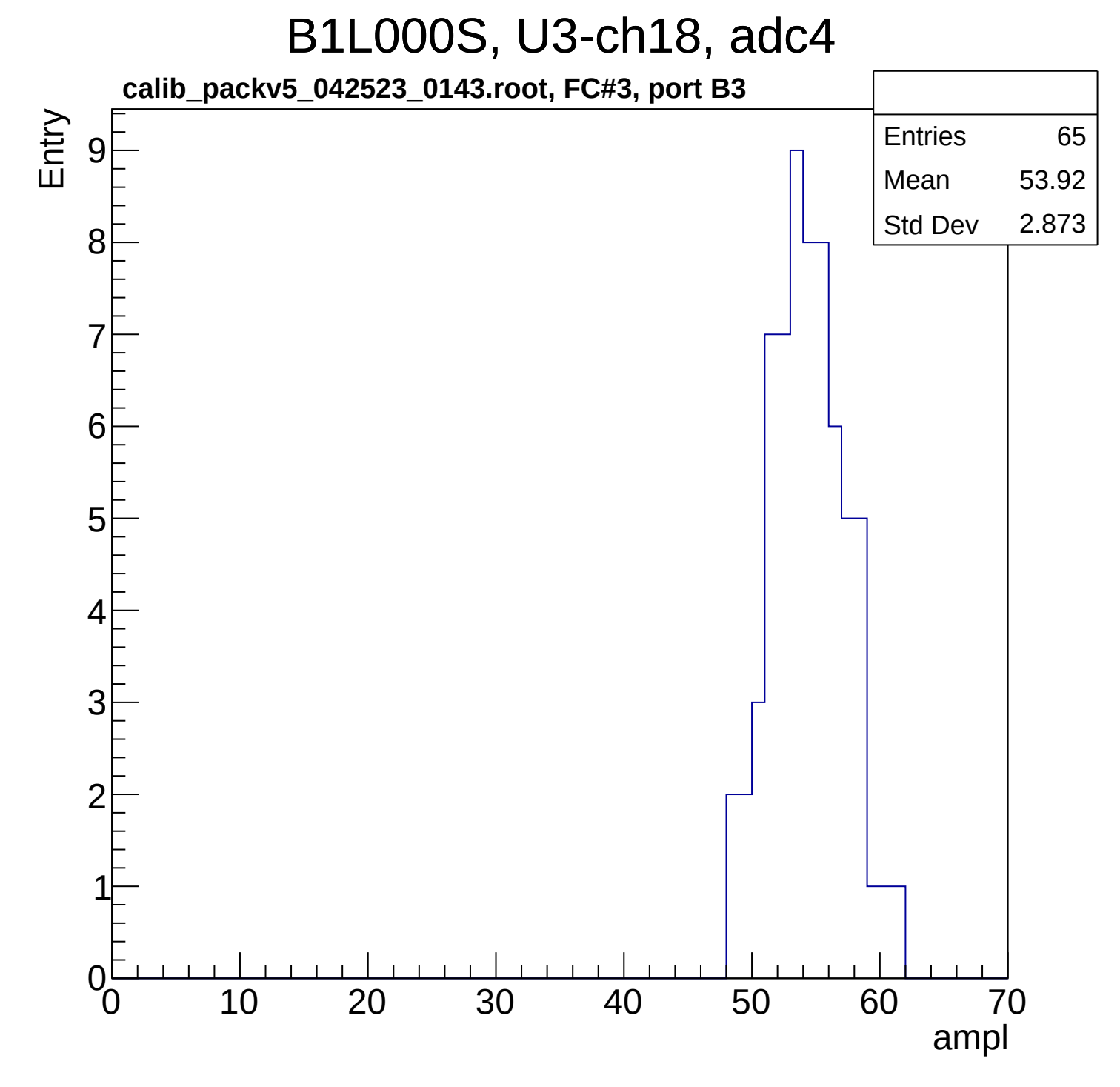
B1L000S, U3-ch18, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 53.92
Std Dev 2.873
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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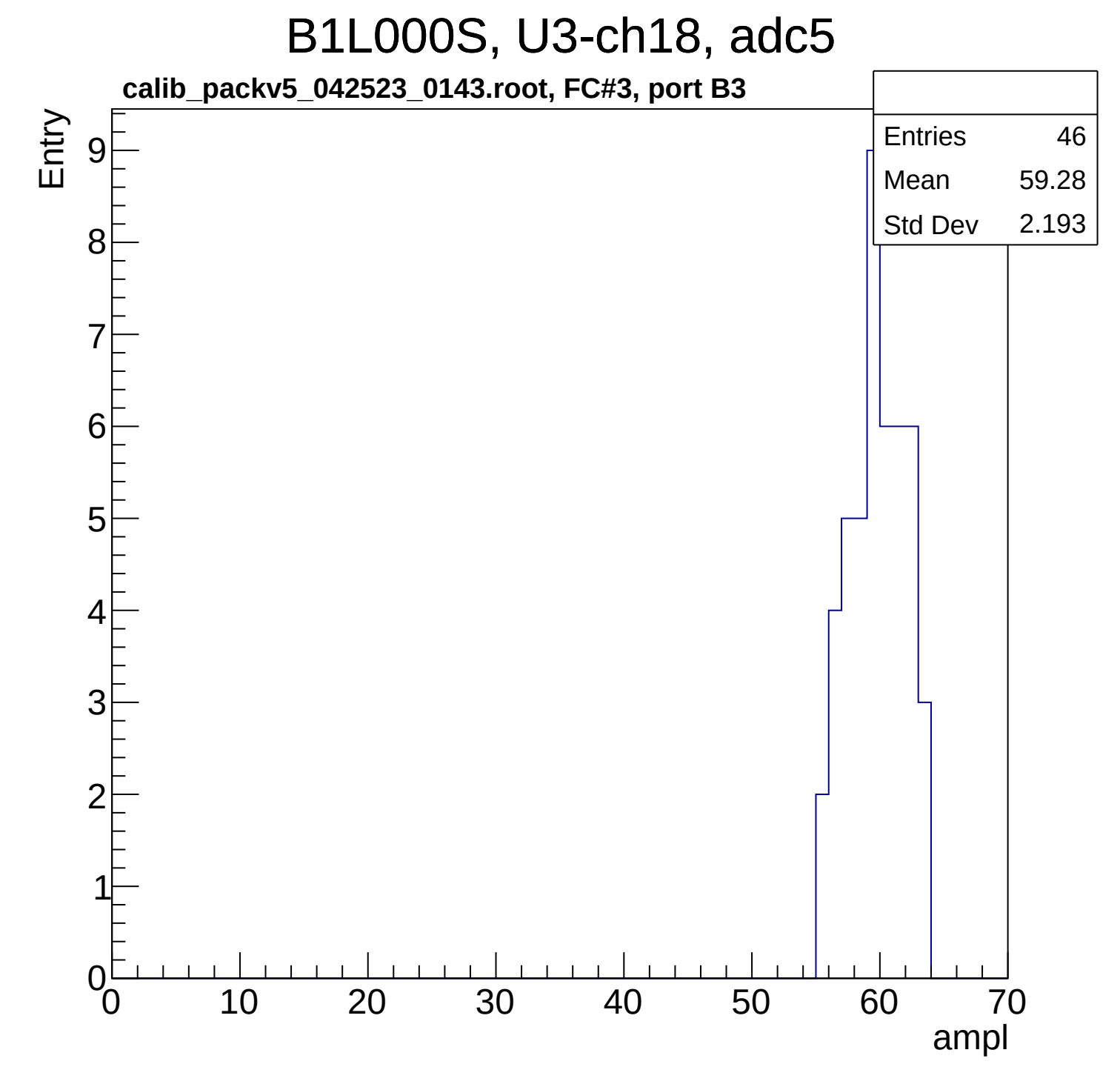
B1L000S, U3-ch18, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 59.28
Std Dev 2.193
Entry
ampl
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2
3
4
5
6
7
8
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0
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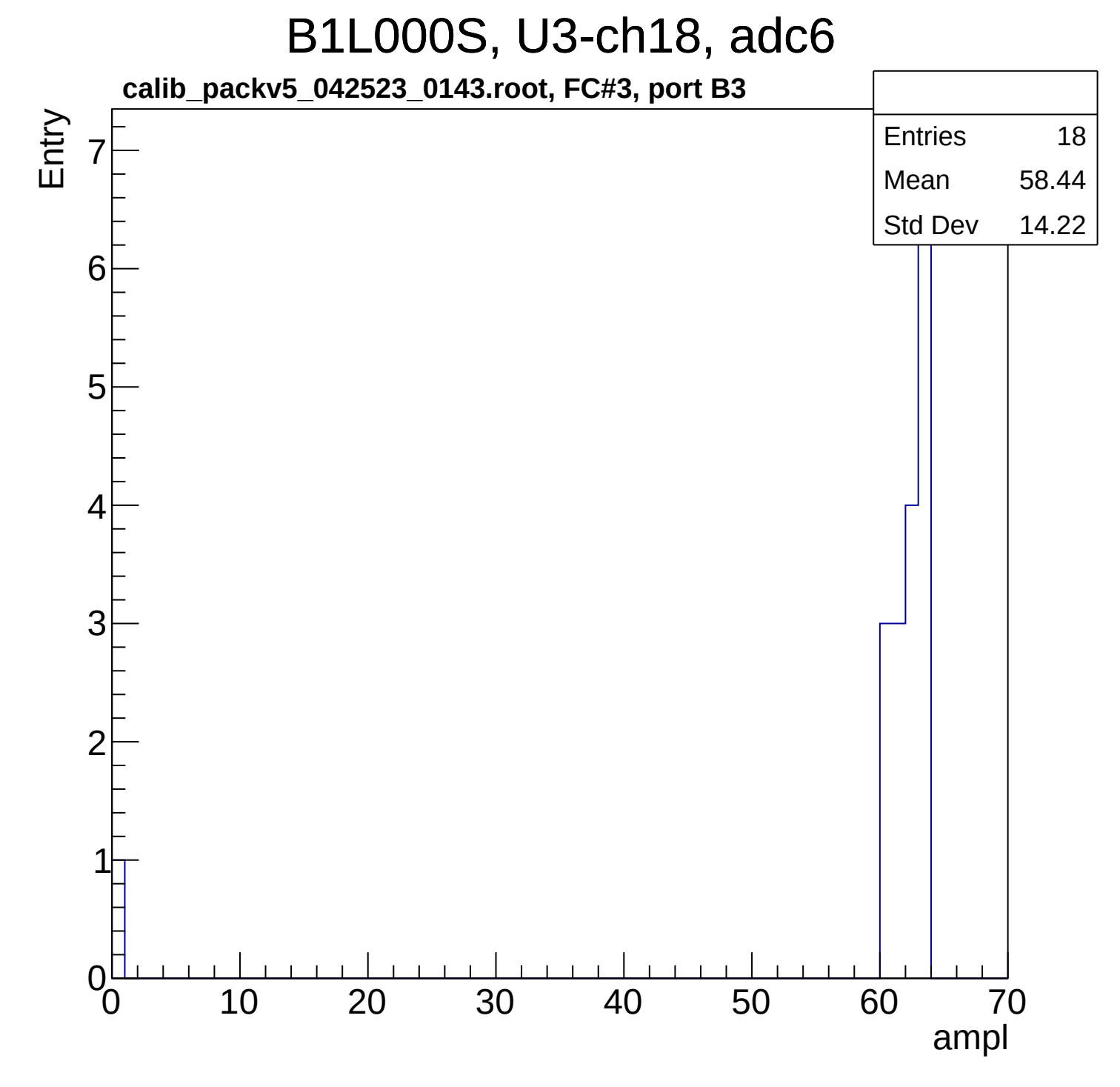
B1L000S, U3-ch18, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 58.44
Std Dev 14.22
Entry
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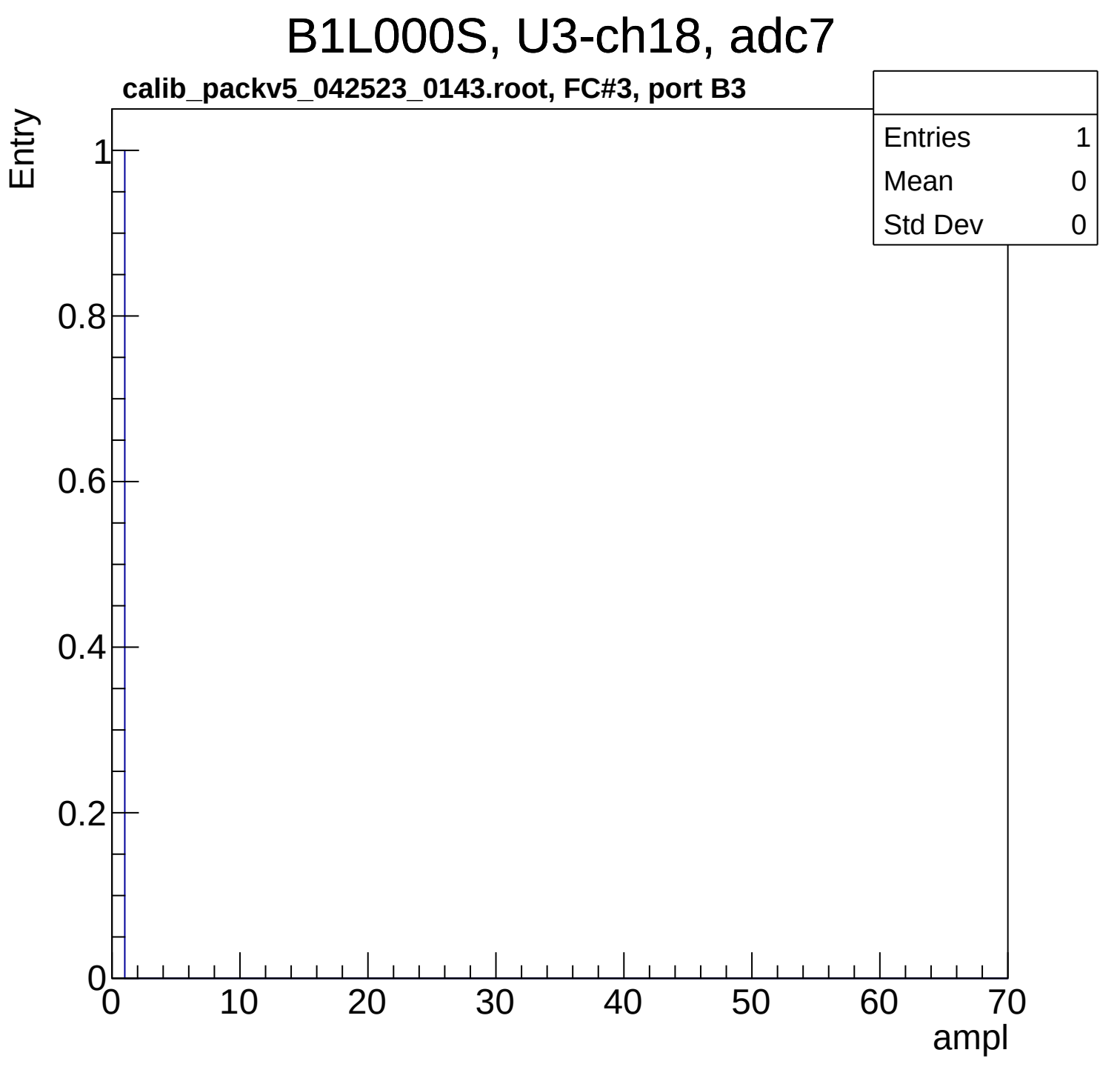
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch18, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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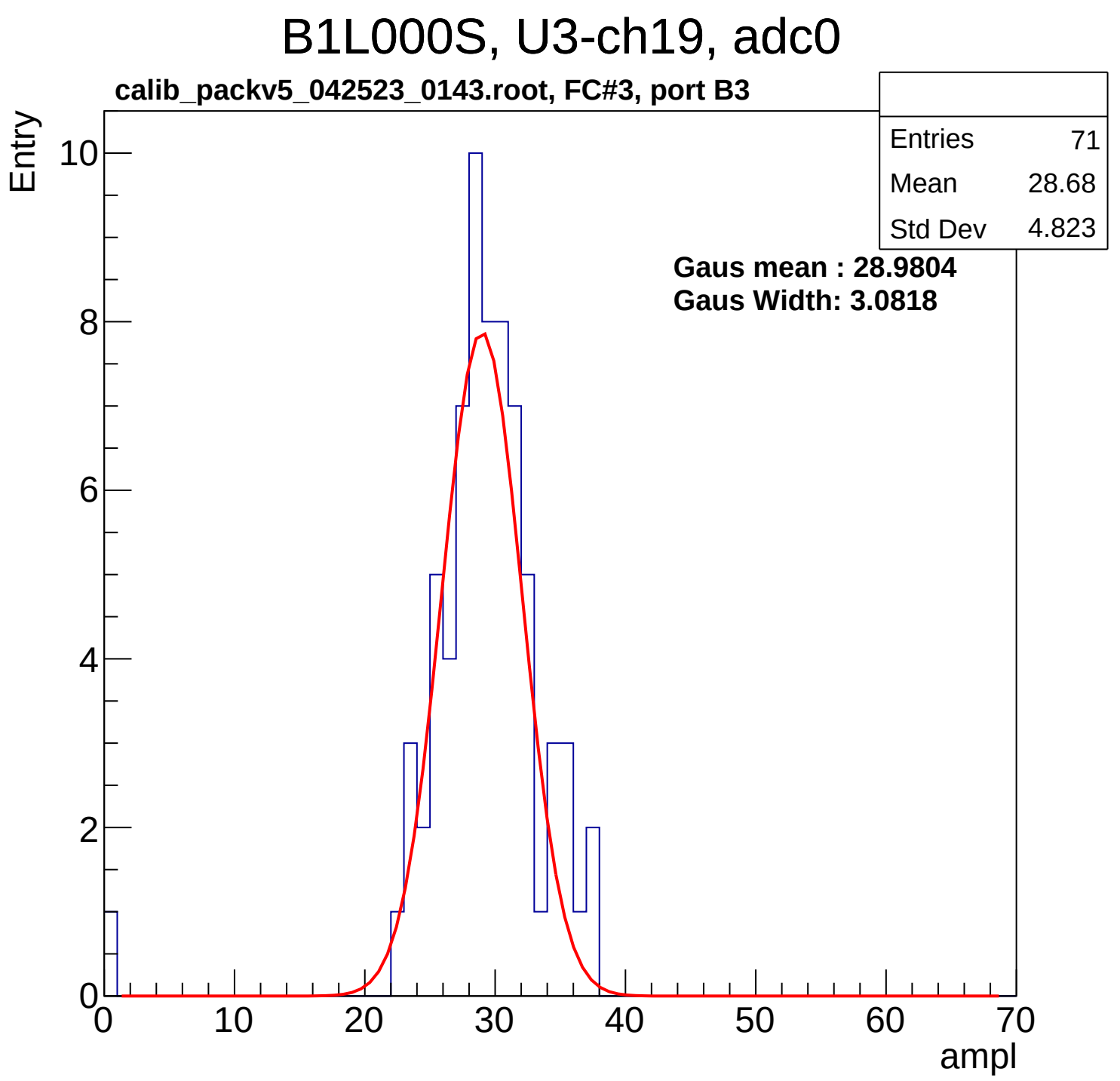
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch19, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 28.68
Std Dev 4.823
Gaus mean : 28.9804
Gaus Width: 3.0818
Entry
ampl
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6
8
10
0
10
20
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40
50
60
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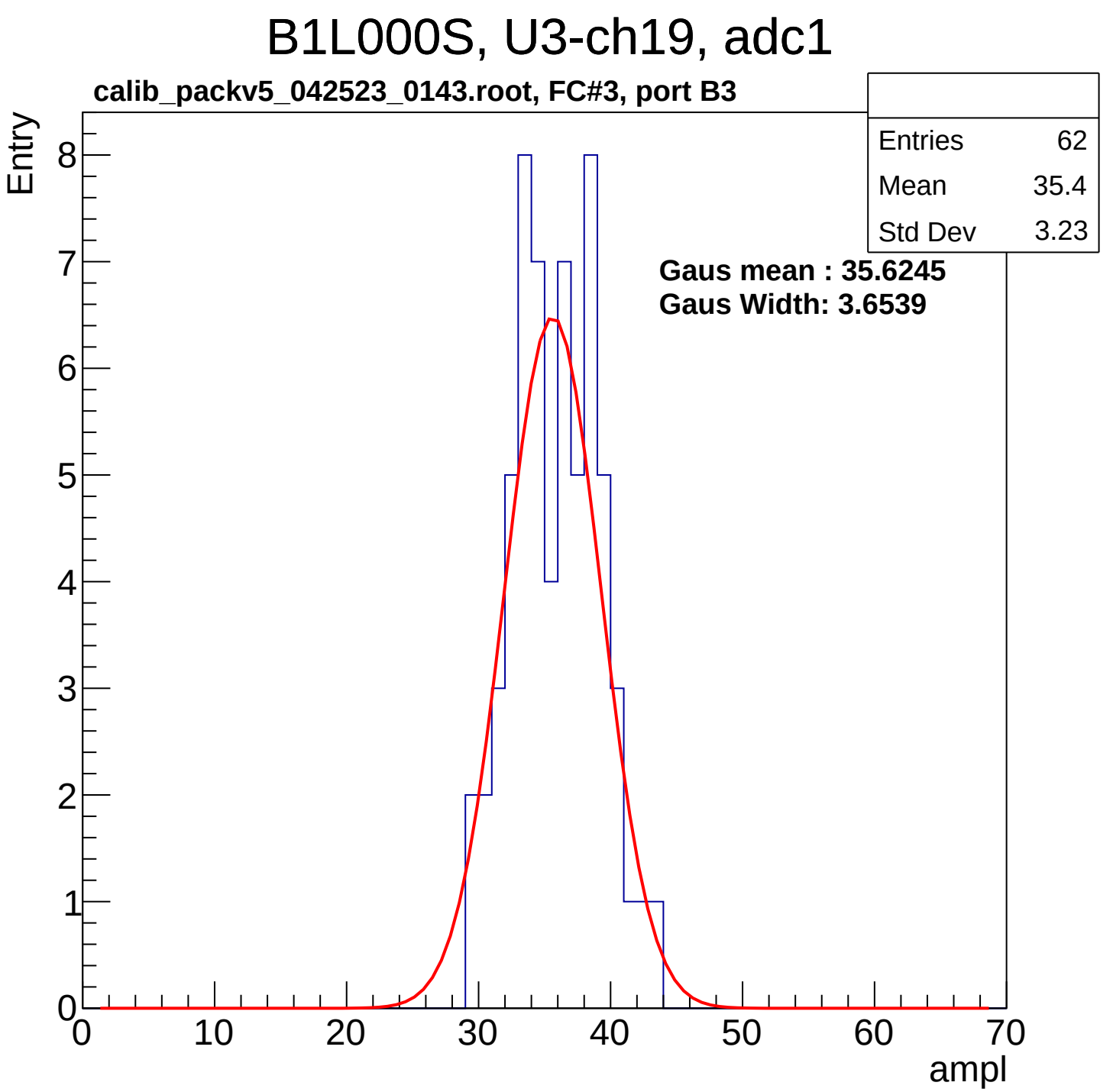
B1L000S, U3-ch19, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 35.4
Std Dev 3.23
Gaus mean : 35.6245
Gaus Width: 3.6539
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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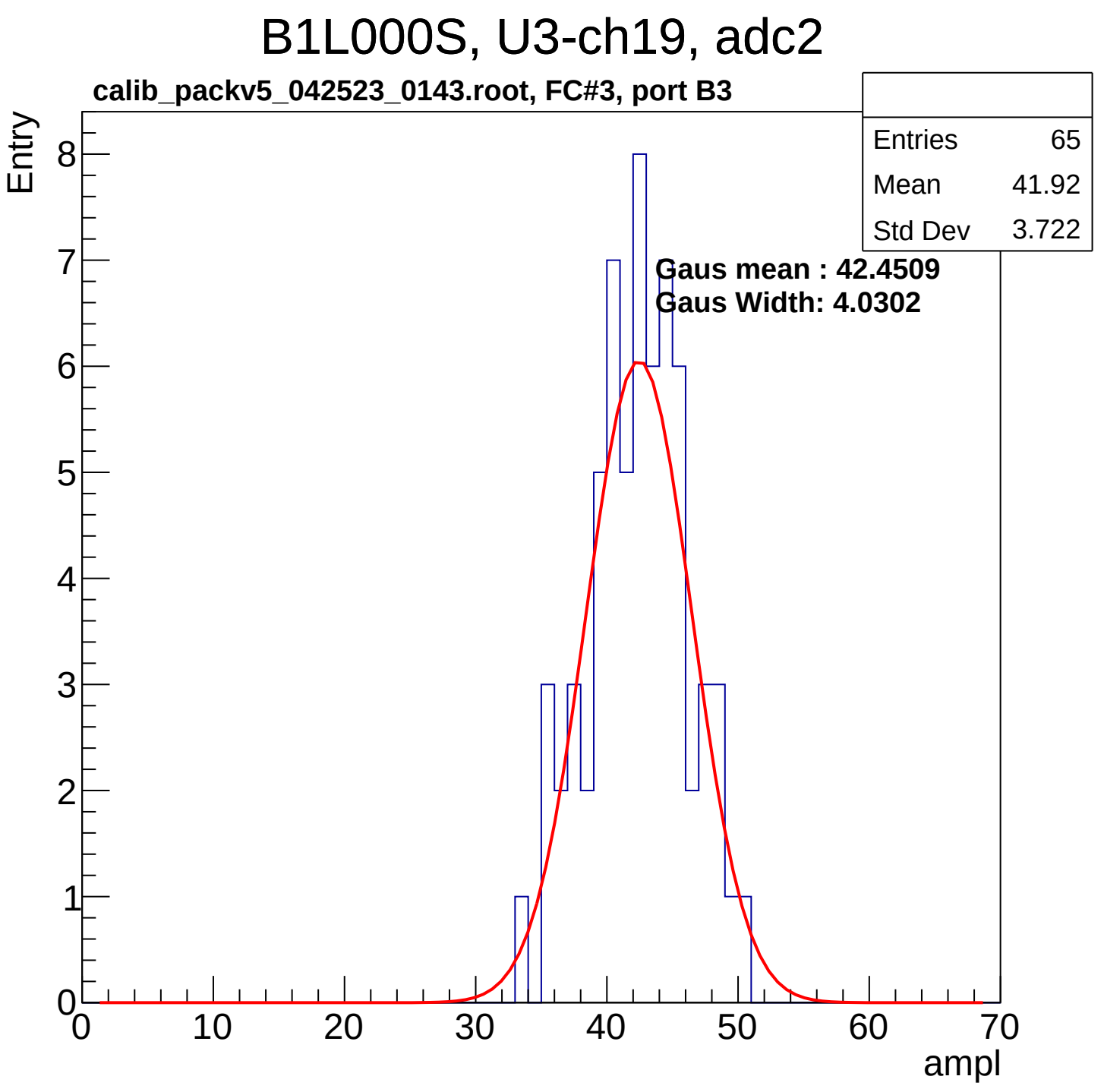
B1L000S, U3-ch19, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 41.92
Std Dev 3.722
Gaus mean : 42.4509
Gaus Width: 4.0302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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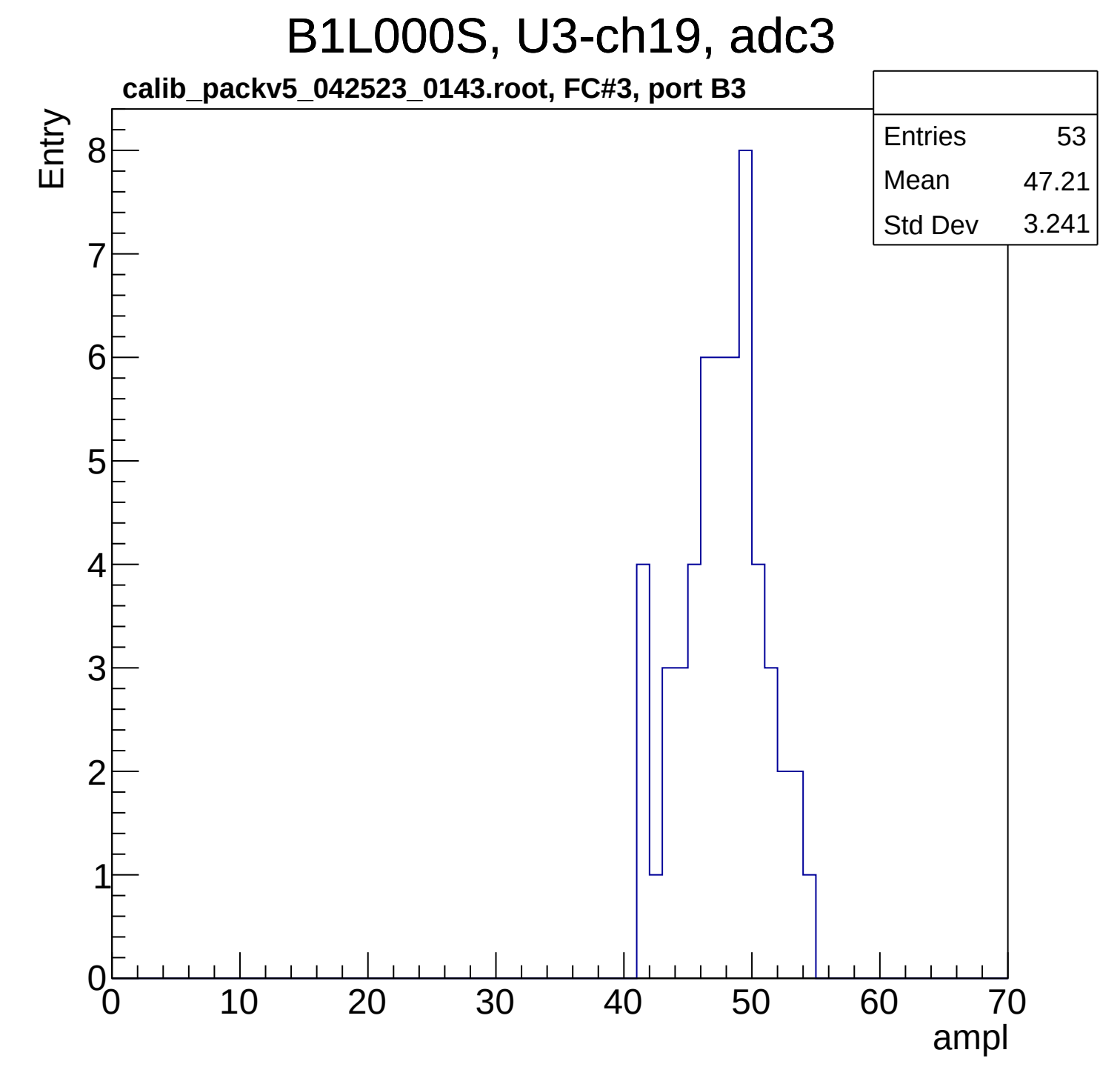
B1L000S, U3-ch19, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 47.21
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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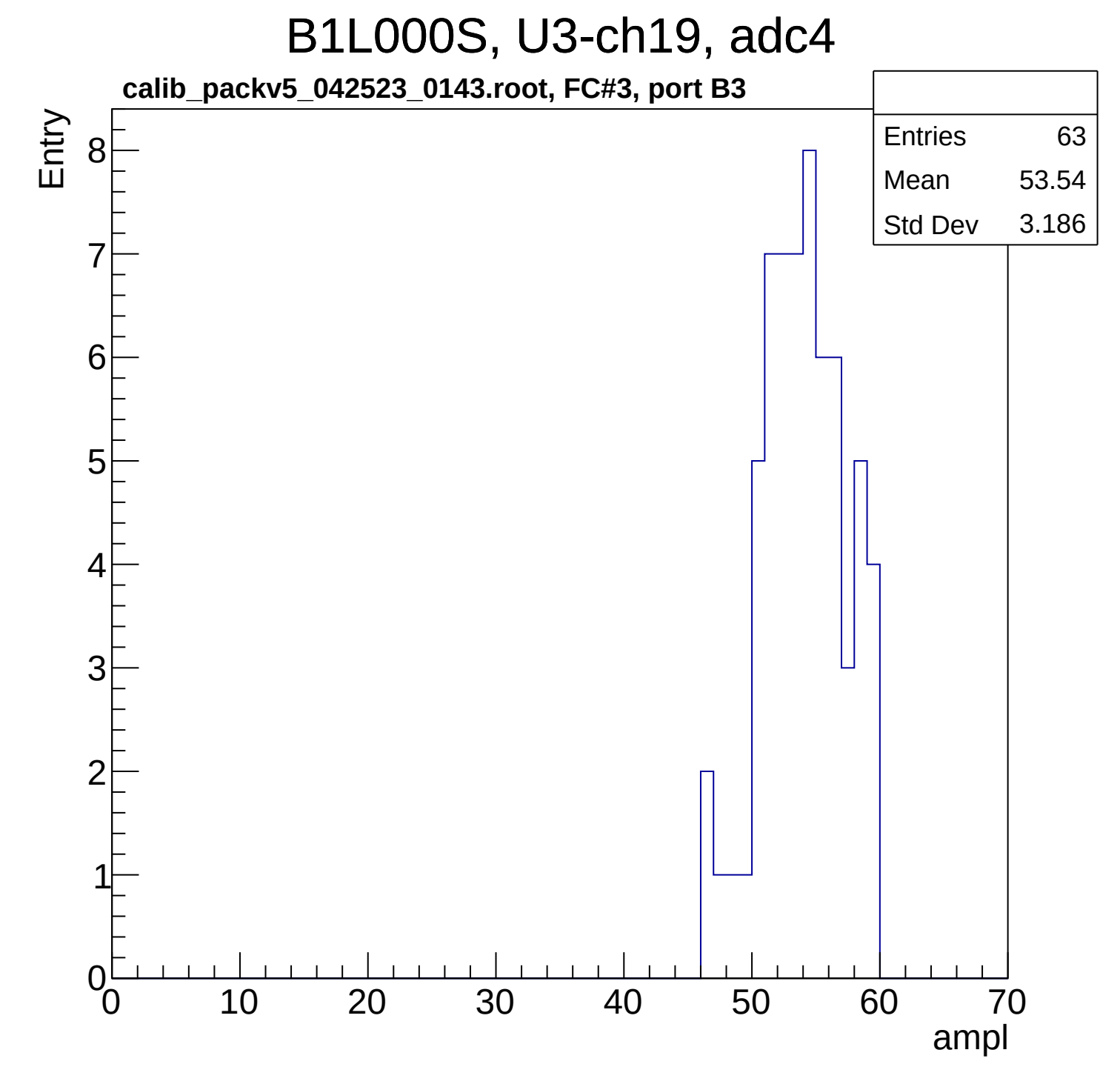
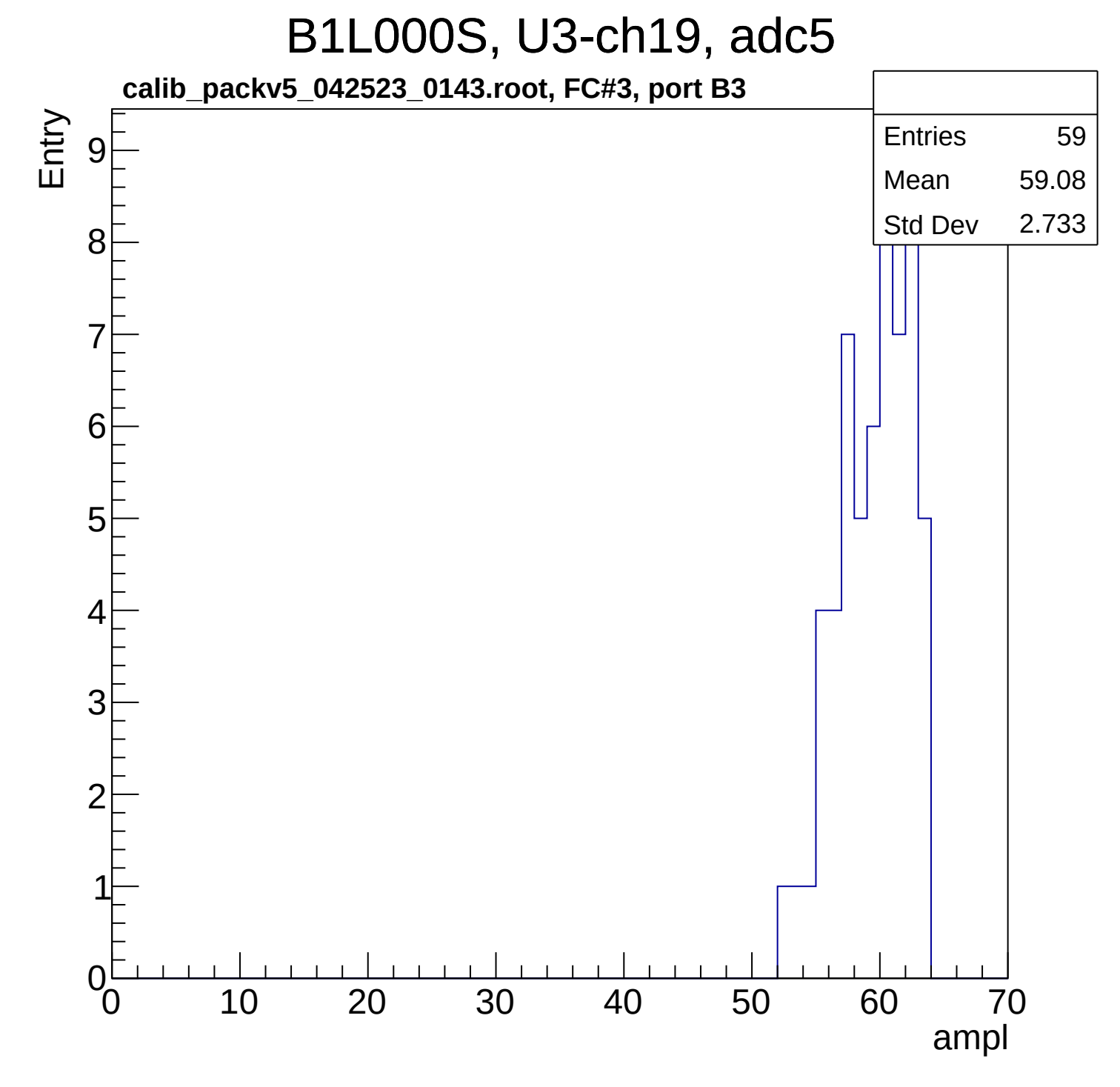

B1L000S, U3-ch19, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 53.54
Std Dev 3.186
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch19, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 59.08
Std Dev 2.733
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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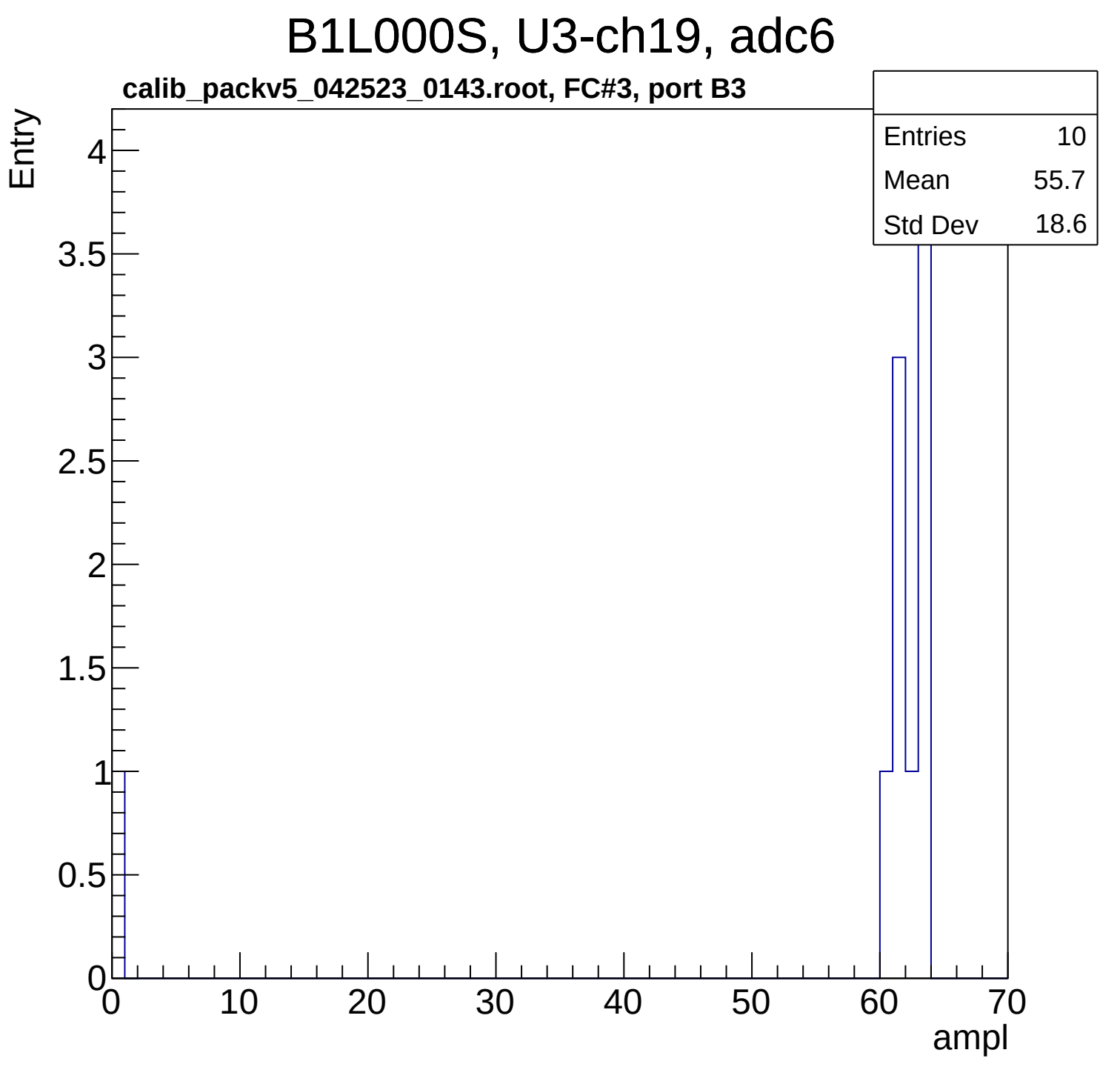
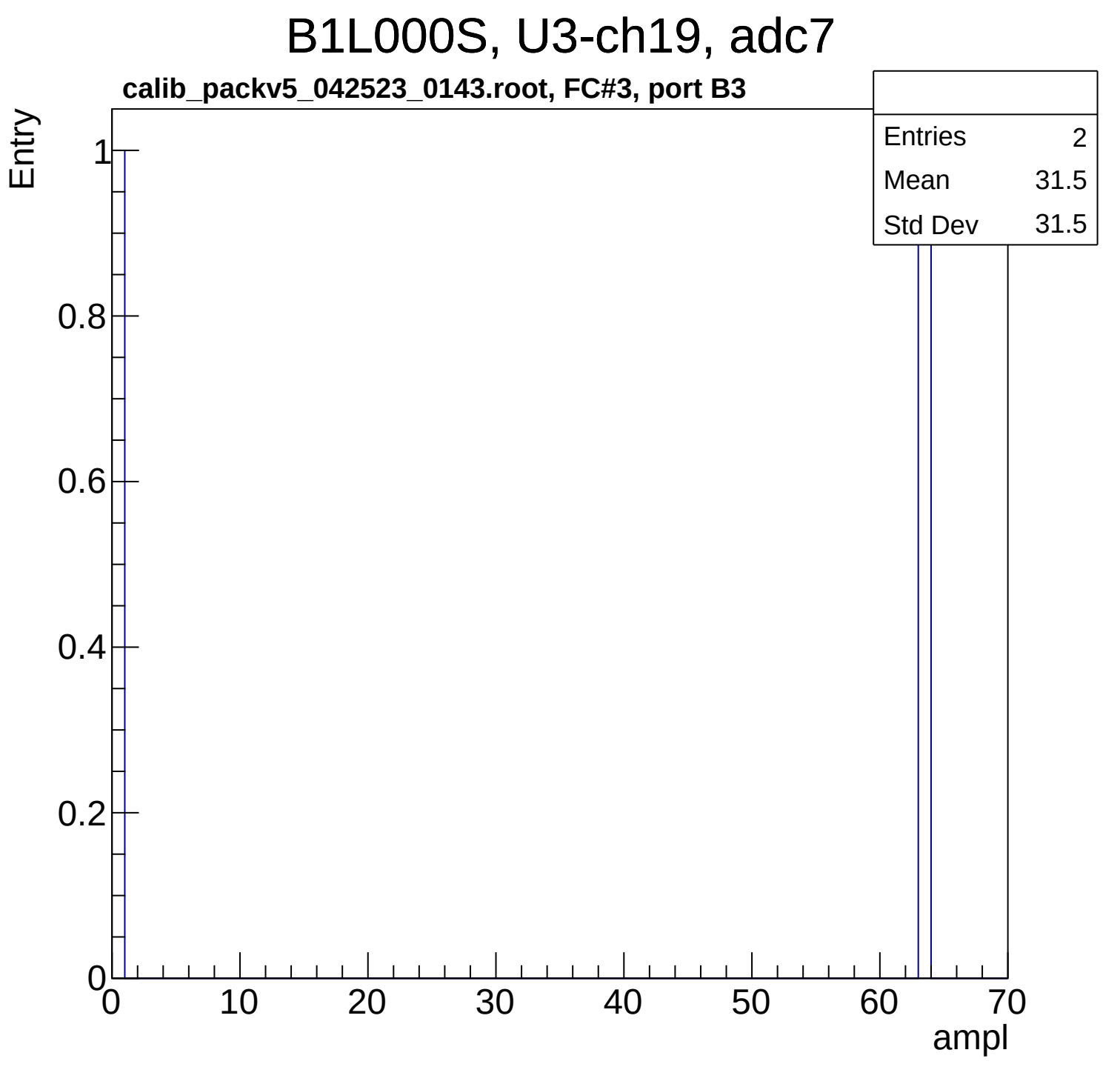
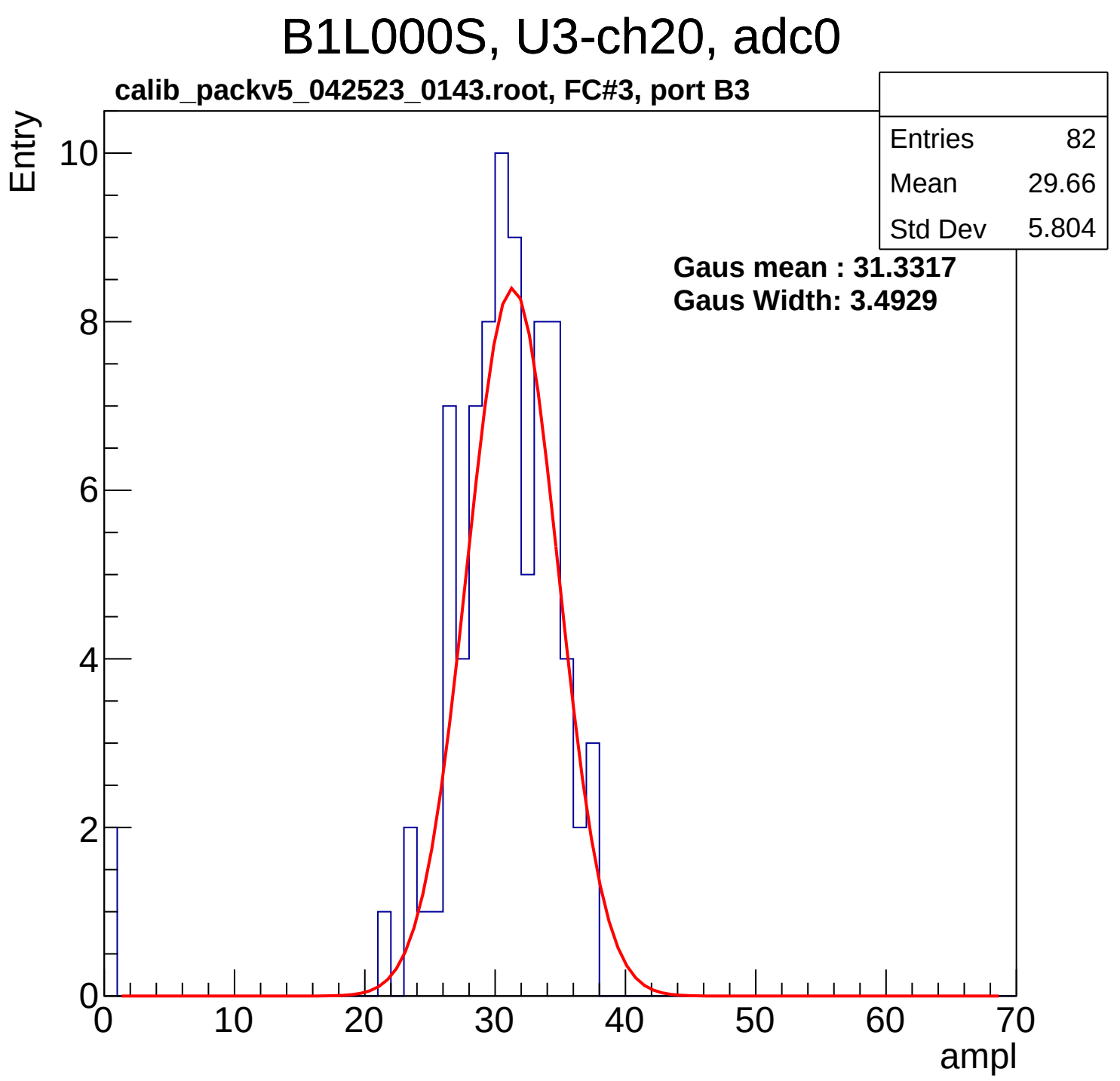
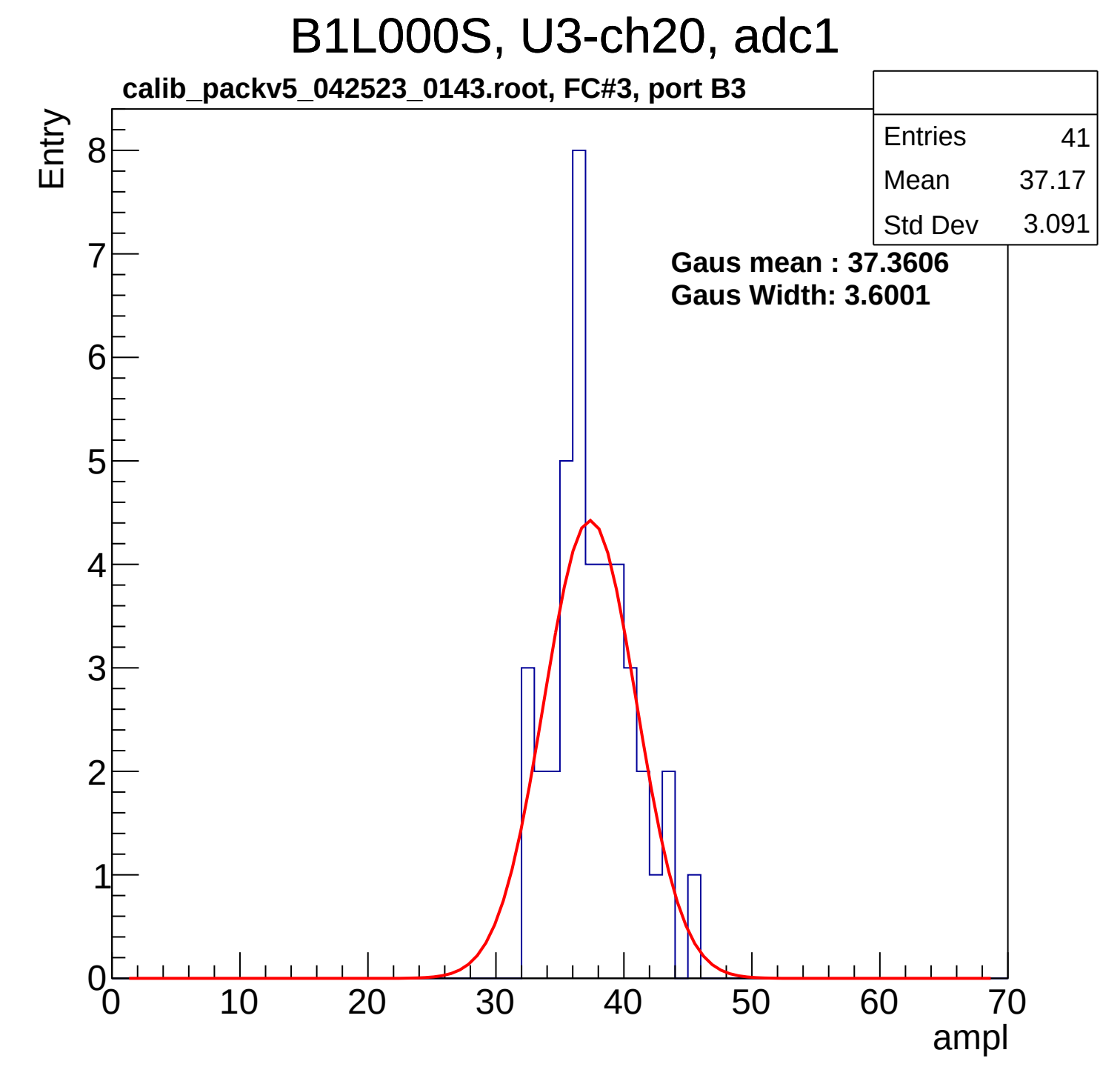
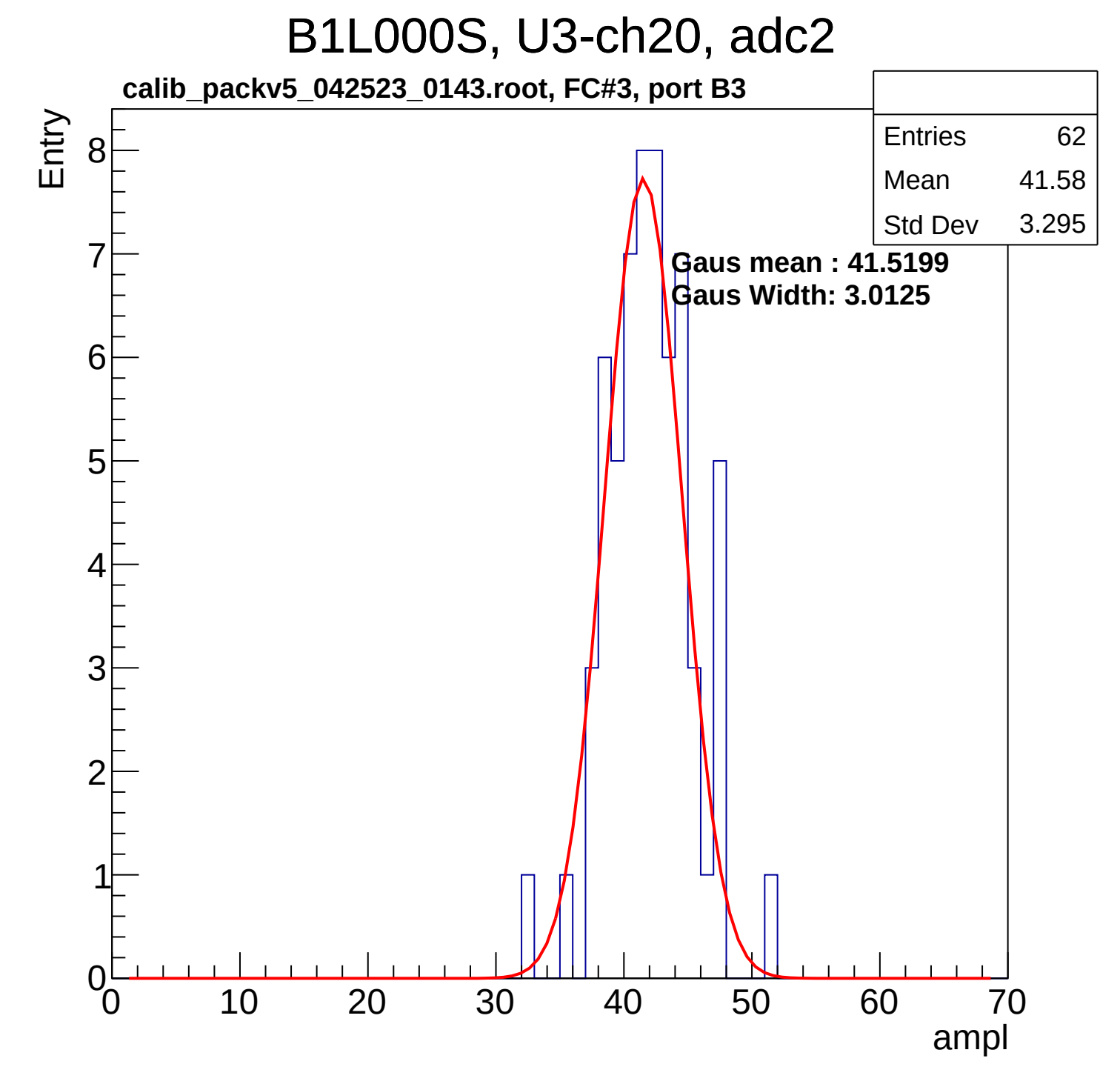
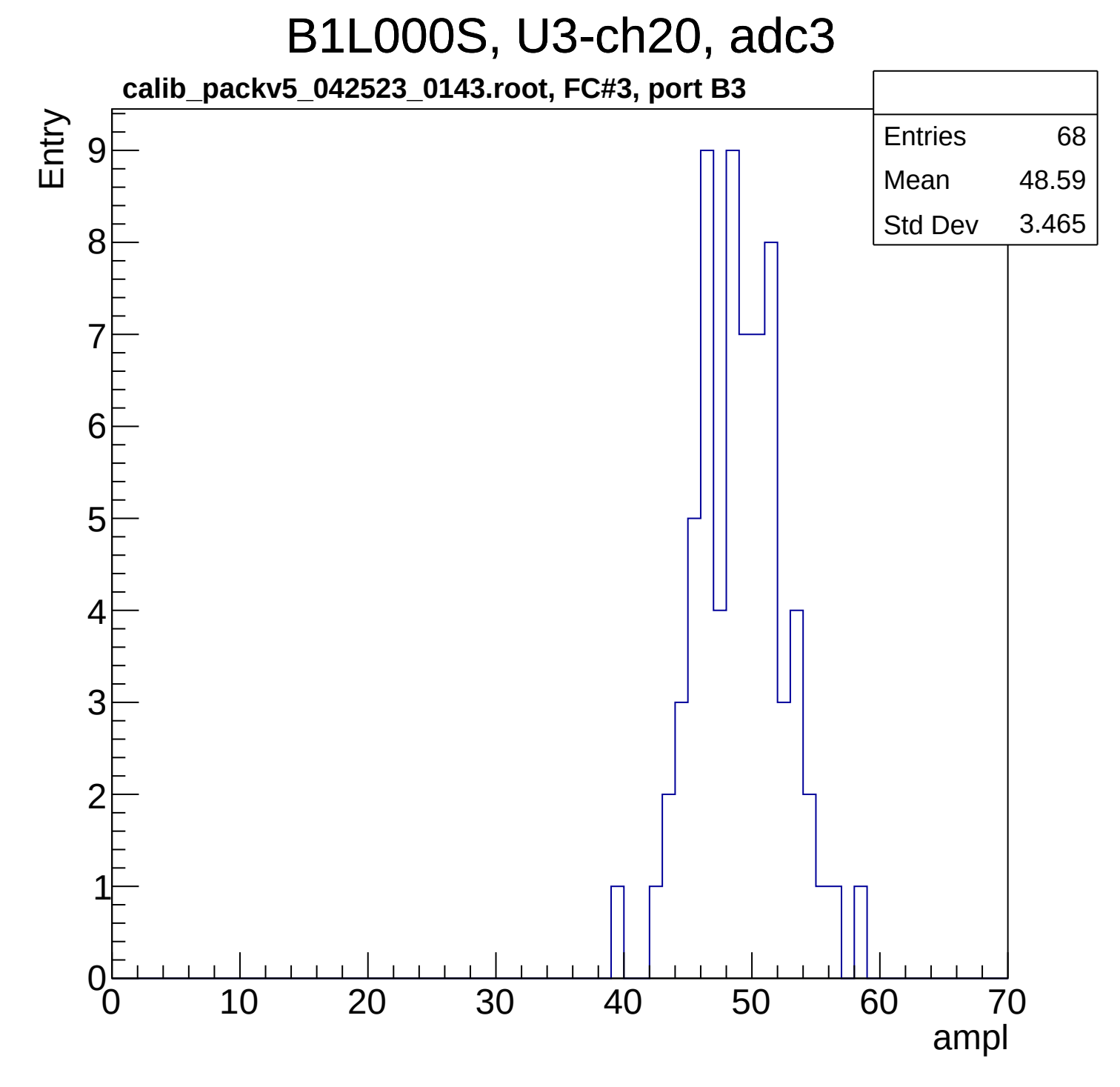

B1L000S, U3-ch19, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 55.7
Std Dev 18.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

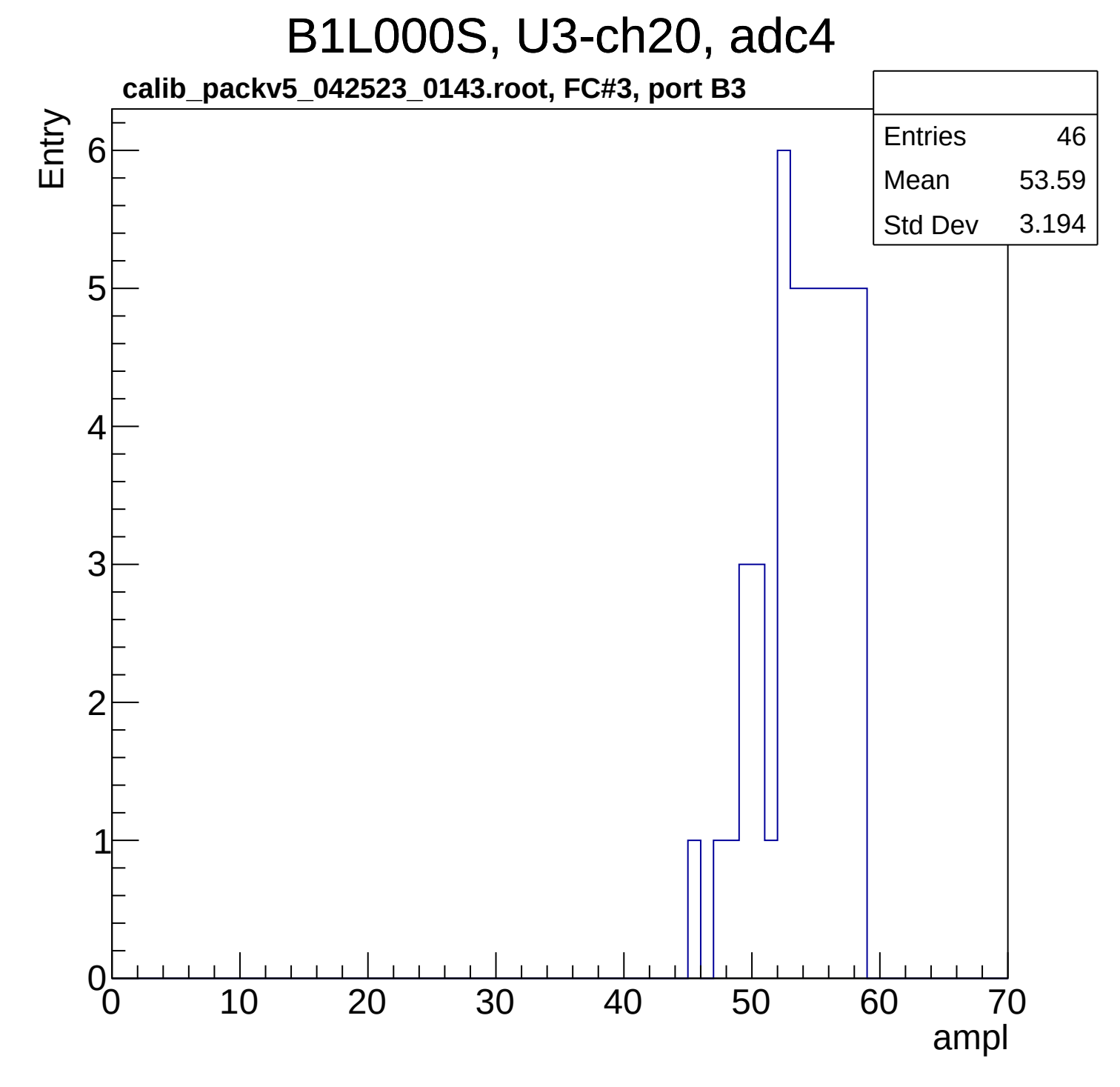

B1L000S, U3-ch19, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

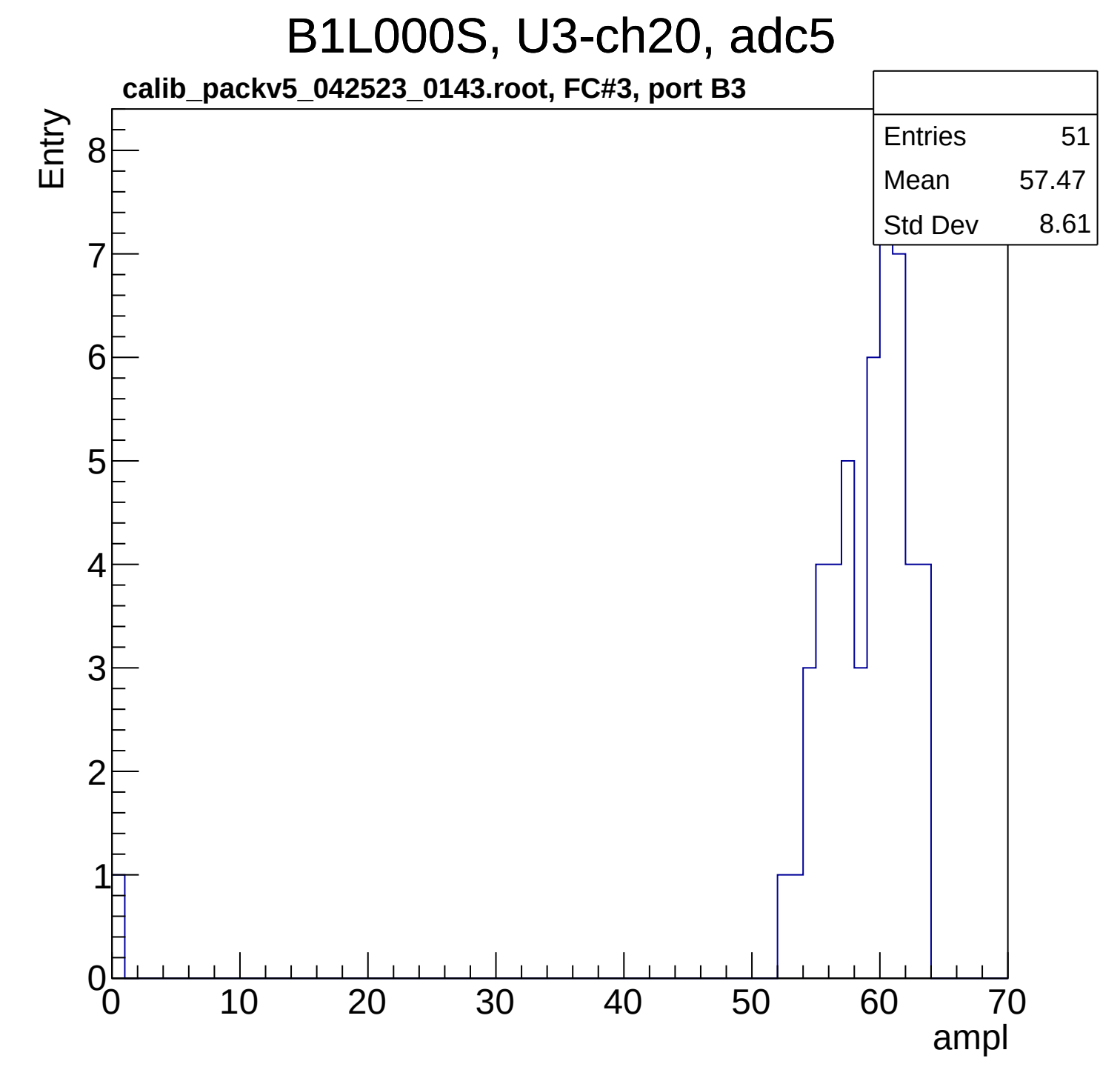

B1L000S, U3-ch20, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 29.66
Std Dev 5.804
Gaus mean : 31.3317
Gaus Width: 3.4929
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

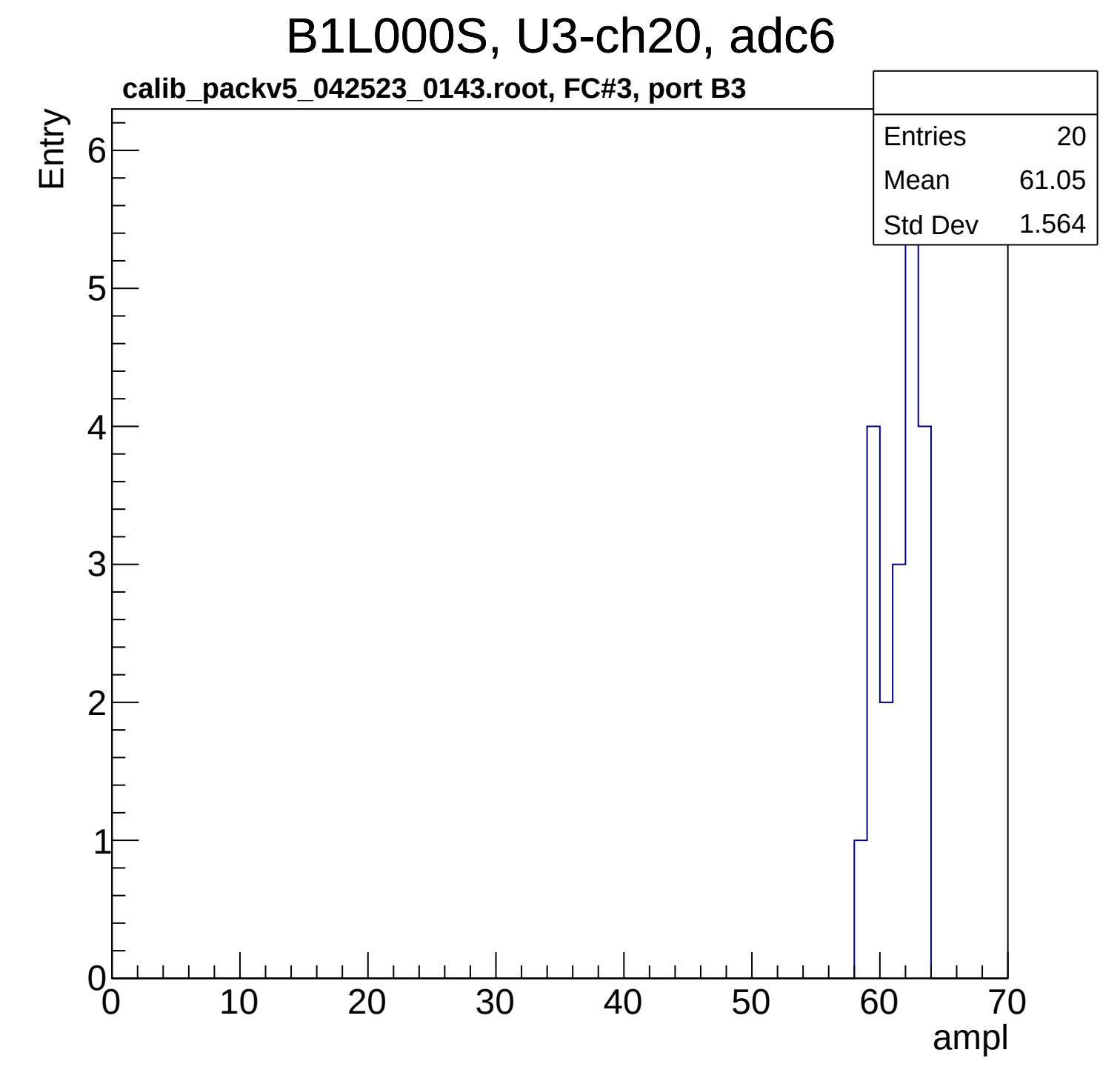

B1L000S, U3-ch20, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 37.17
Std Dev 3.091
Gaus mean : 37.3606
Gaus Width: 3.6001
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

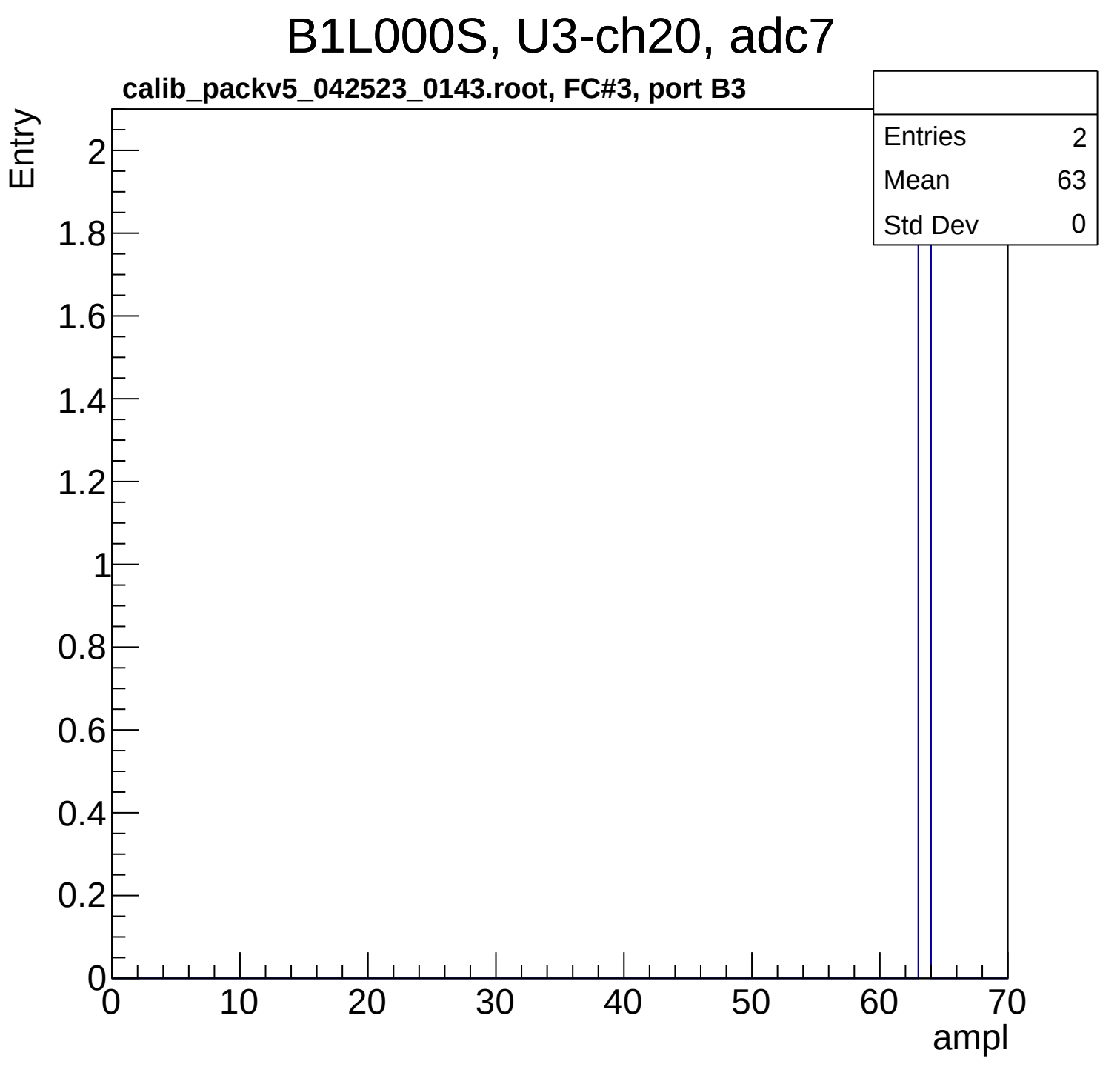

B1L000S, U3-ch20, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 41.58
Std Dev 3.295
Gaus mean : 41.5199
Gaus Width: 3.0125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

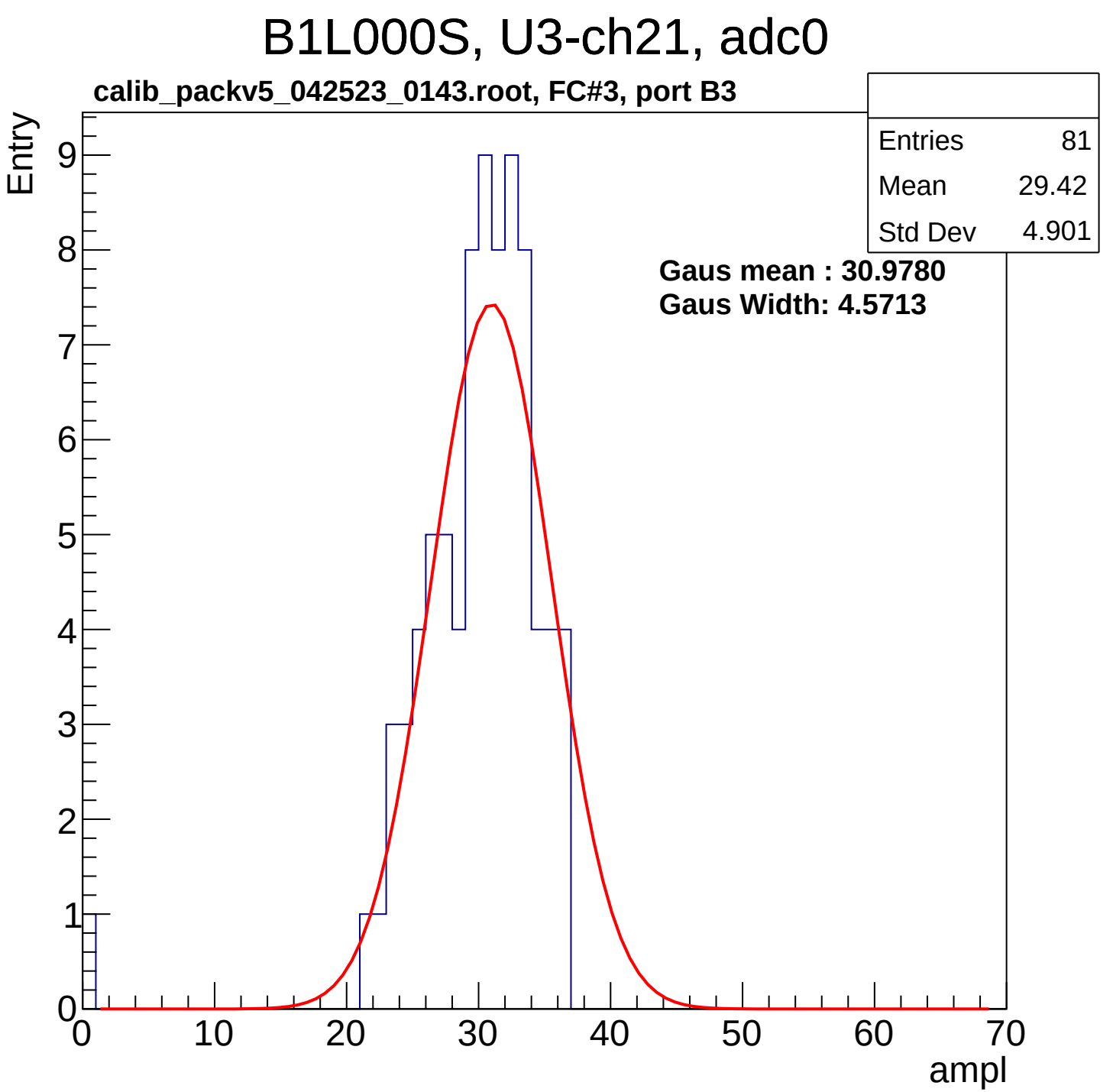

B1L000S, U3-ch20, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 48.59
Std Dev 3.465
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

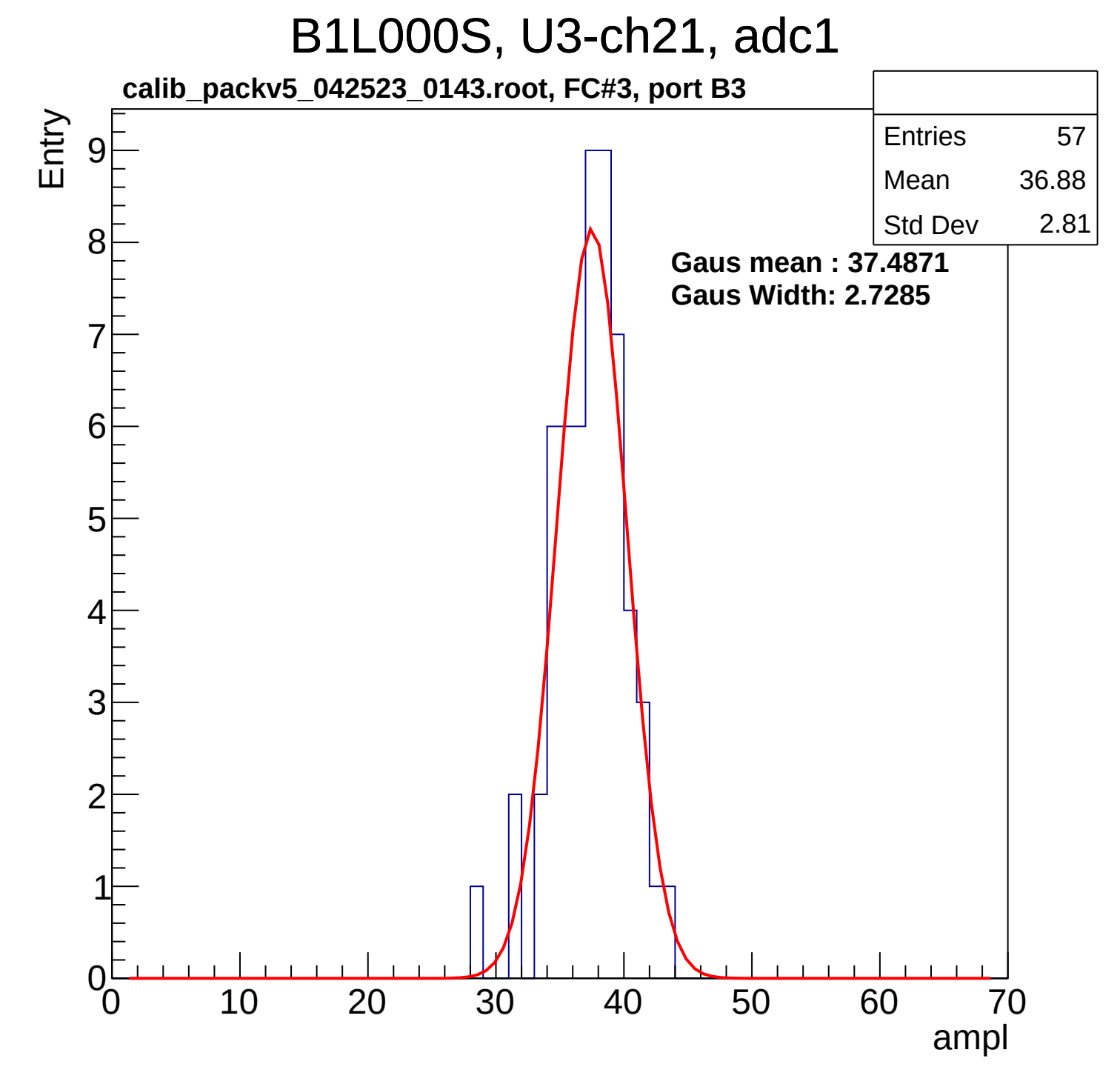

B1L000S, U3-ch20, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 53.59
Std Dev 3.194
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

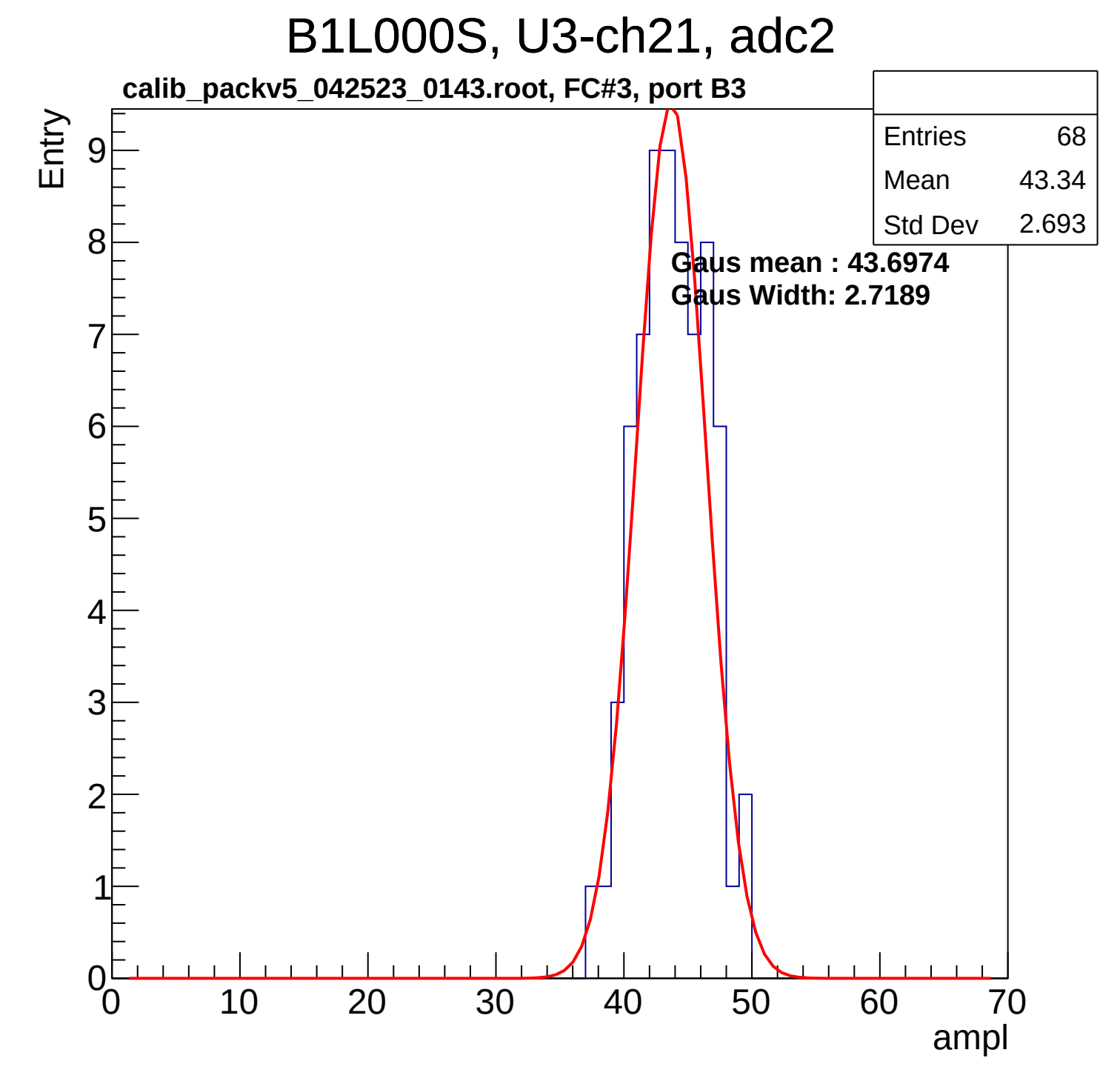

B1L000S, U3-ch20, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 57.47
Std Dev 8.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

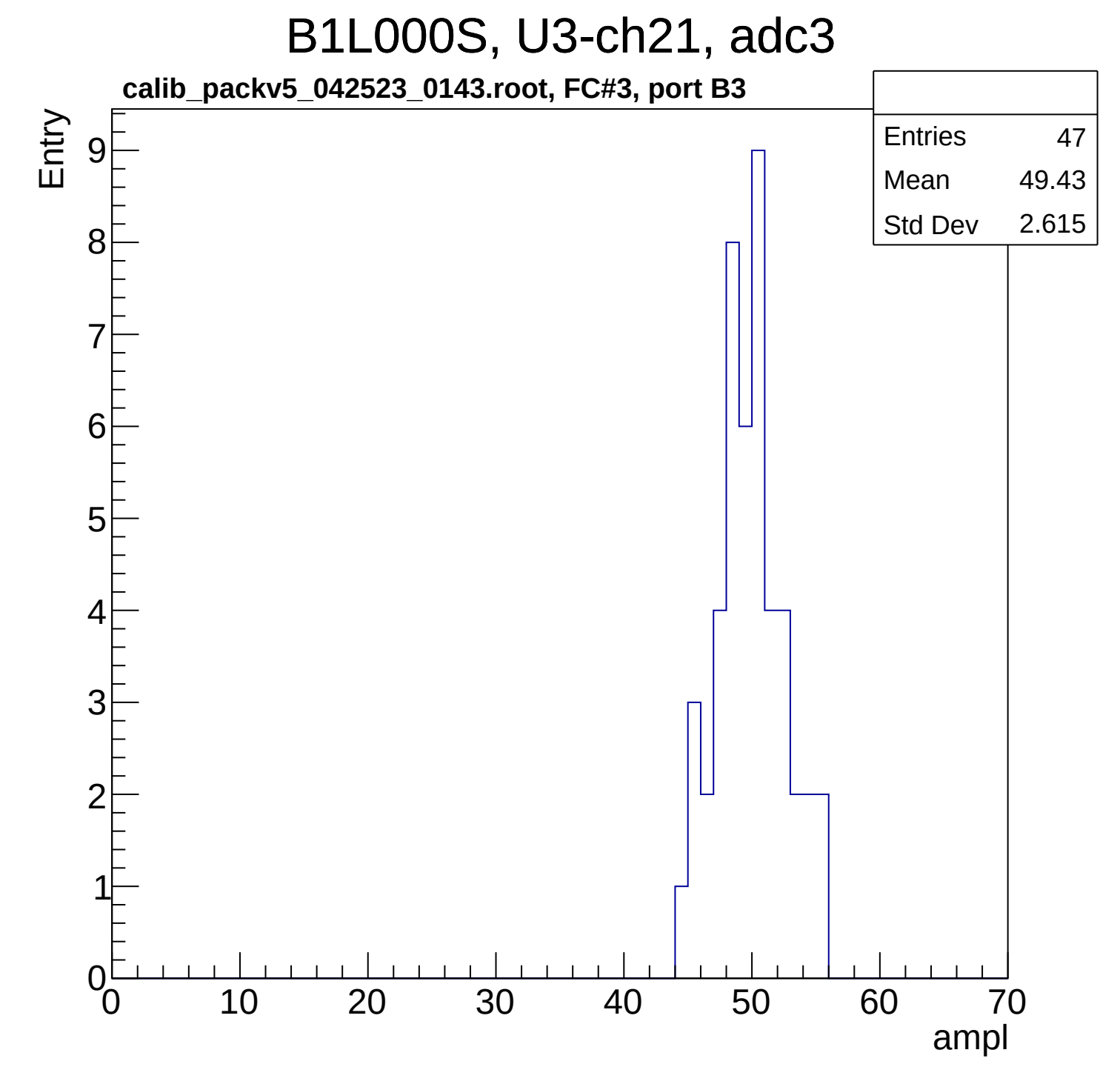

B1L000S, U3-ch20, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 61.05
Std Dev 1.564
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

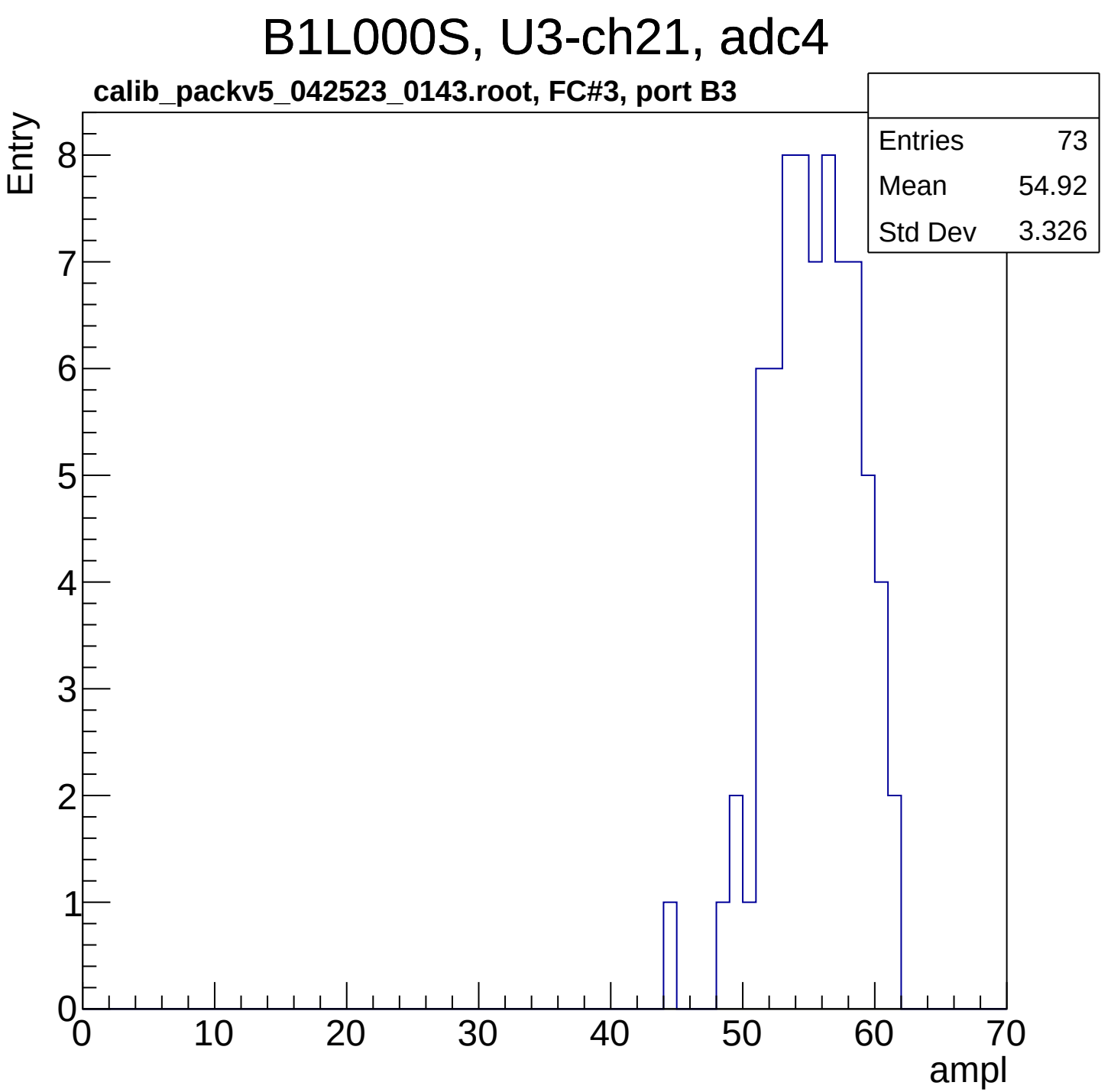

B1L000S, U3-ch20, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch21, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 29.42
Std Dev 4.901
Gaus mean : 30.9780
Gaus Width: 4.5713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch21, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 36.88
Std Dev 2.81
Gaus mean : 37.4871
Gaus Width: 2.7285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch21, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 43.34
Std Dev 2.693
Gaus mean : 43.6974
Gaus Width: 2.7189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch21, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 49.43
Std Dev 2.615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch21, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 54.92
Std Dev 3.326
Entry
ampl
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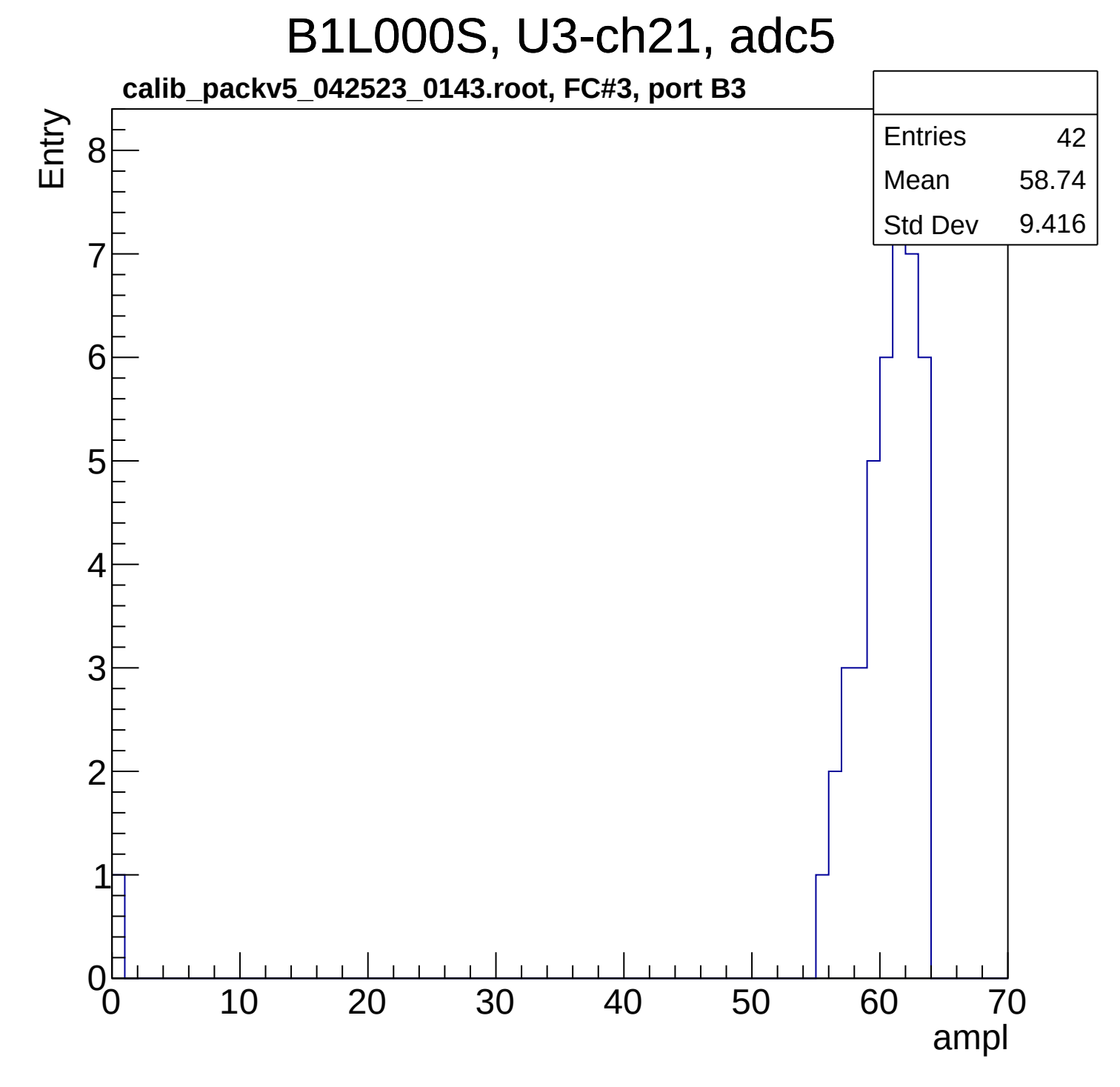
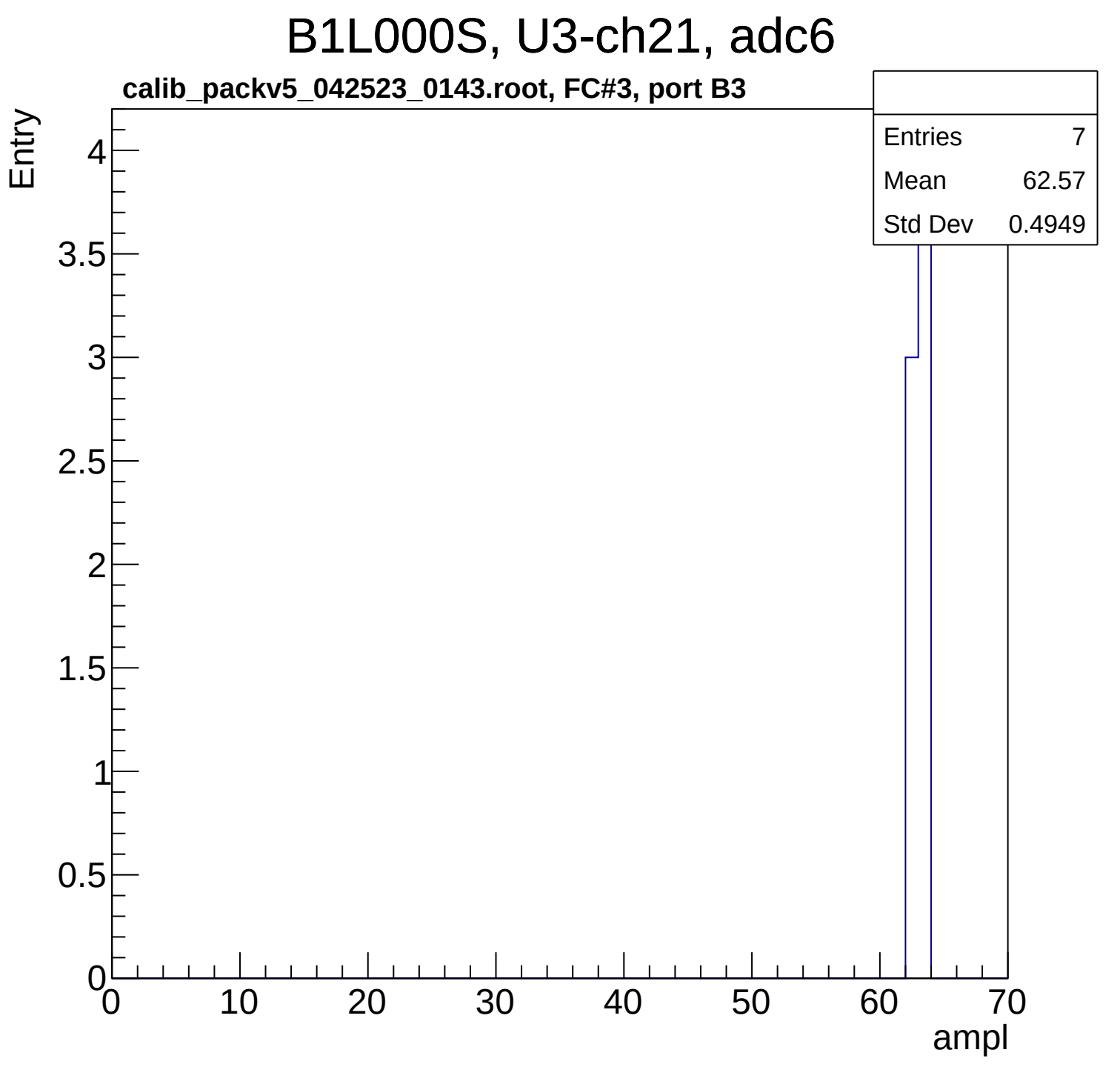
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch21, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 58.74
Std Dev 9.416
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch21, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
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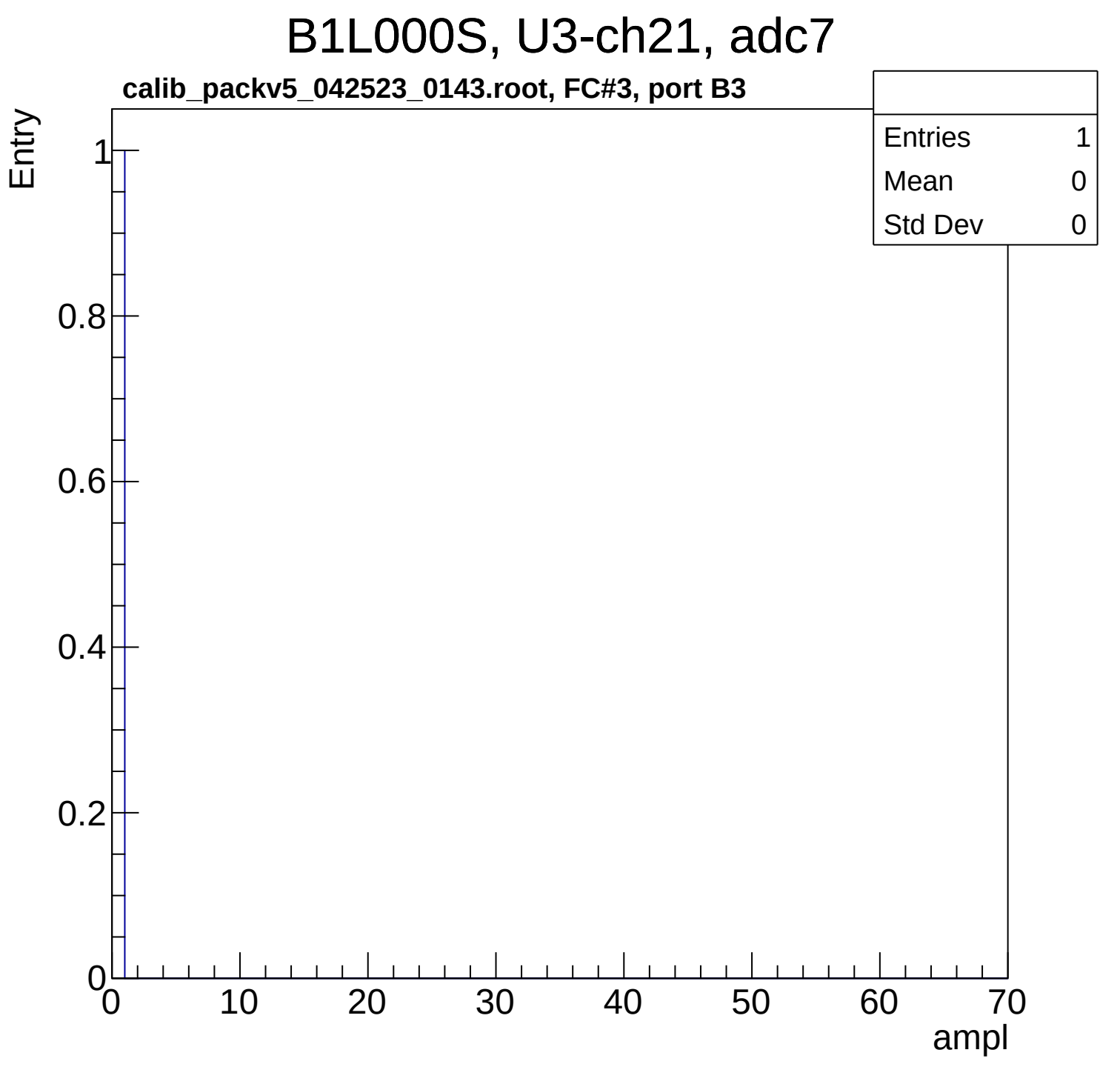
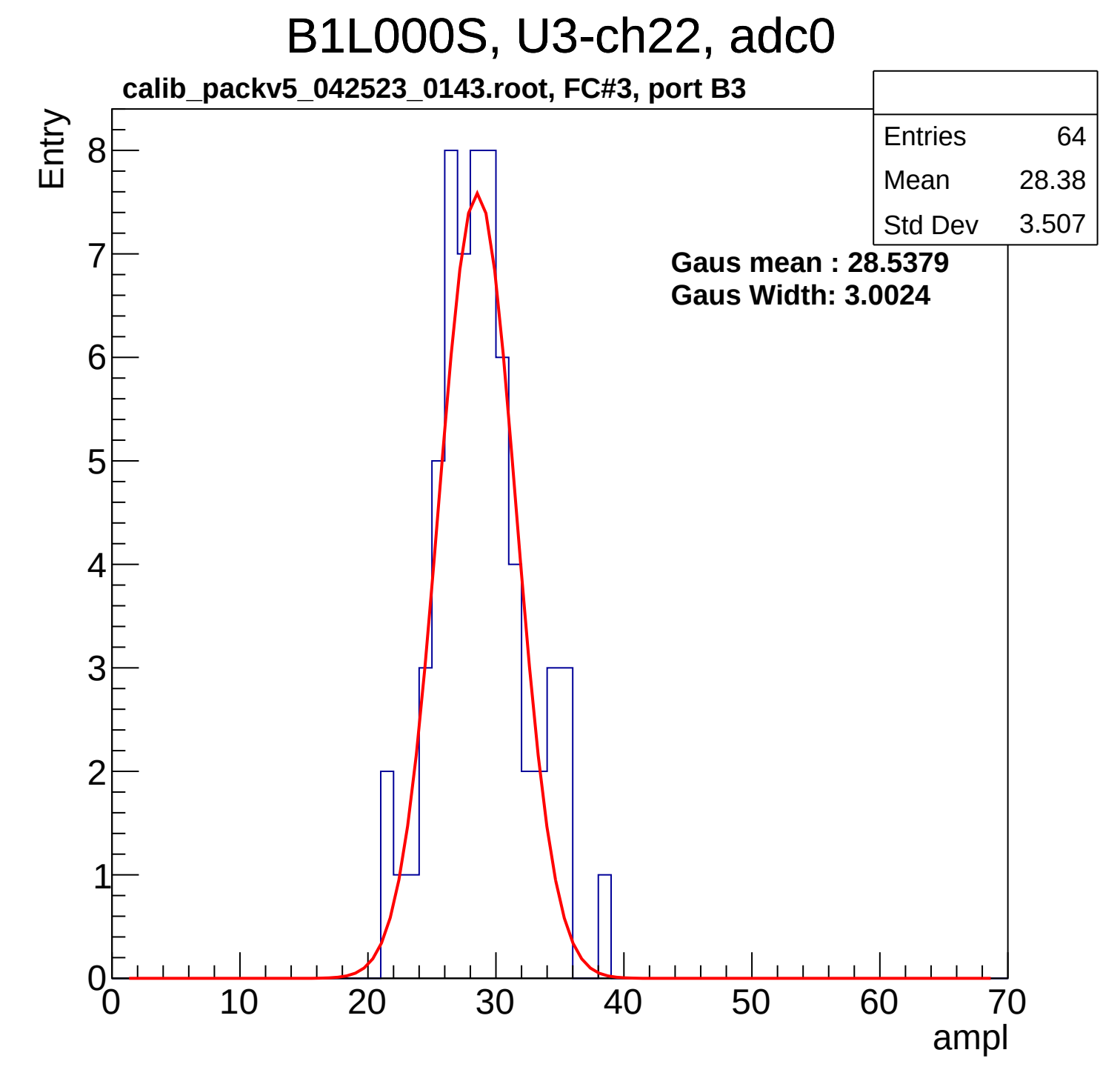
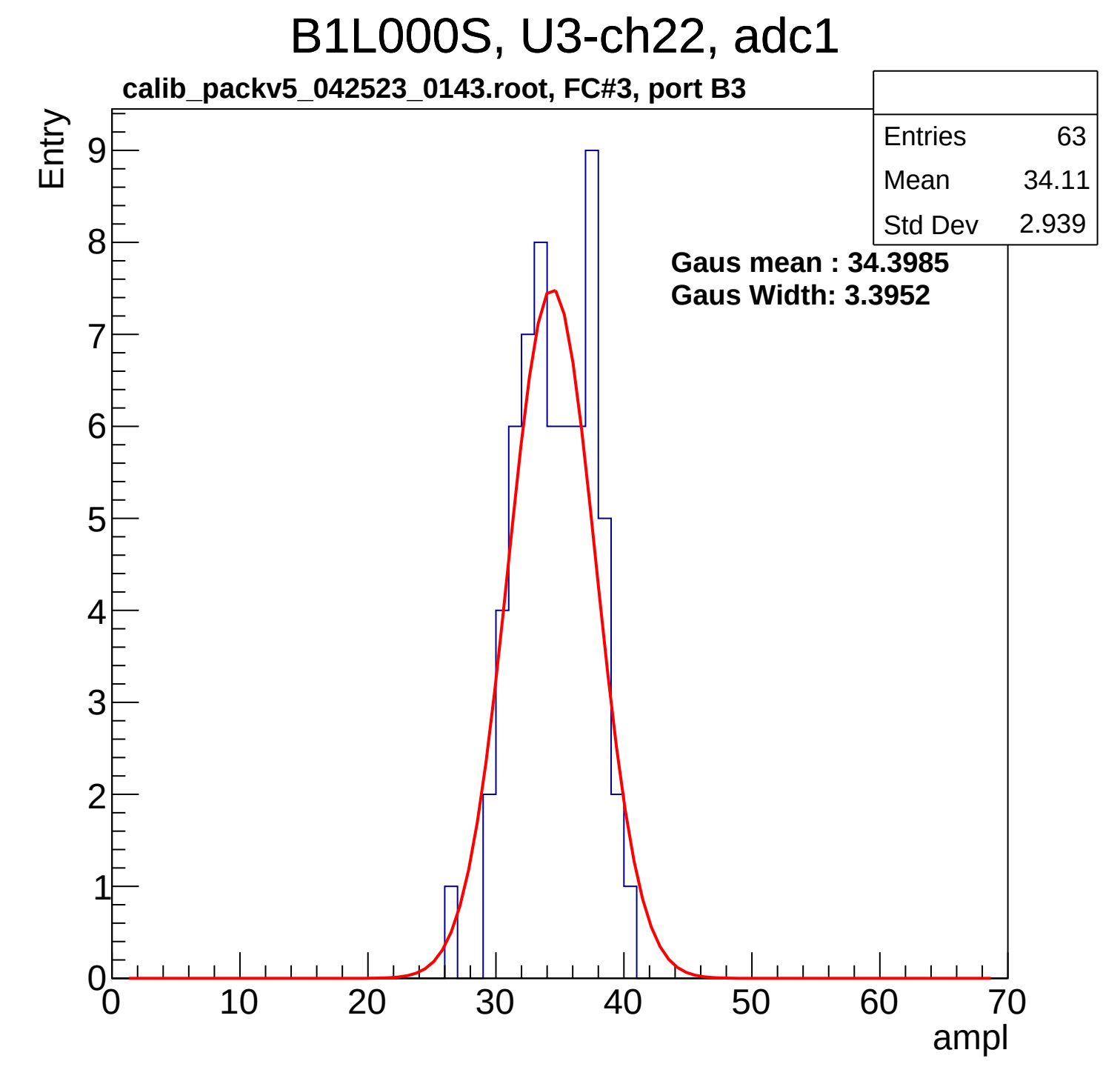
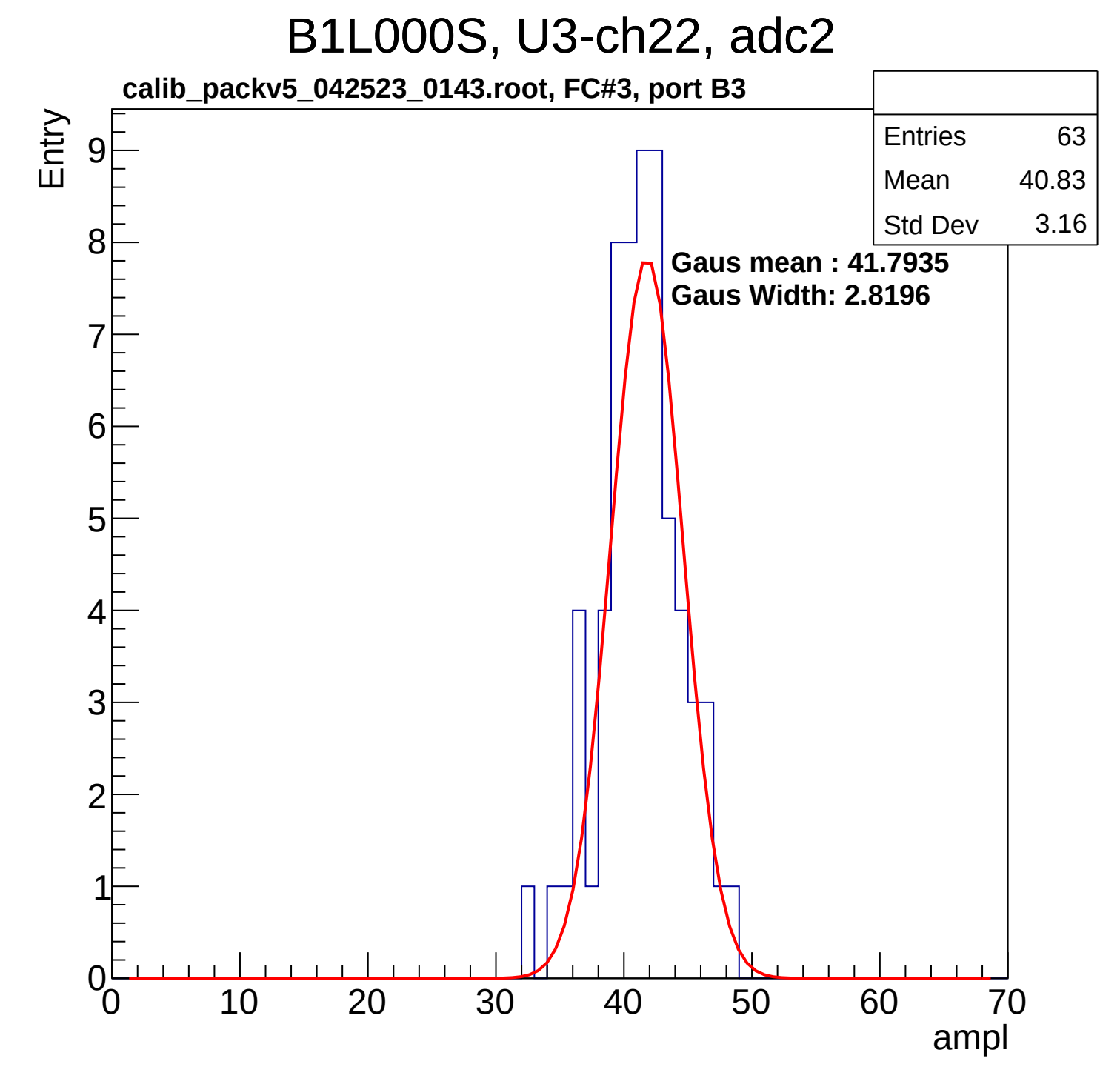
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch21, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch22, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.38
Std Dev 3.507
Gaus mean : 28.5379
Gaus Width: 3.0024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch22, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.11
Std Dev 2.939
Gaus mean : 34.3985
Gaus Width: 3.3952
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch22, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 40.83
Std Dev 3.16
Gaus mean : 41.7935
Gaus Width: 2.8196
Entry
ampl
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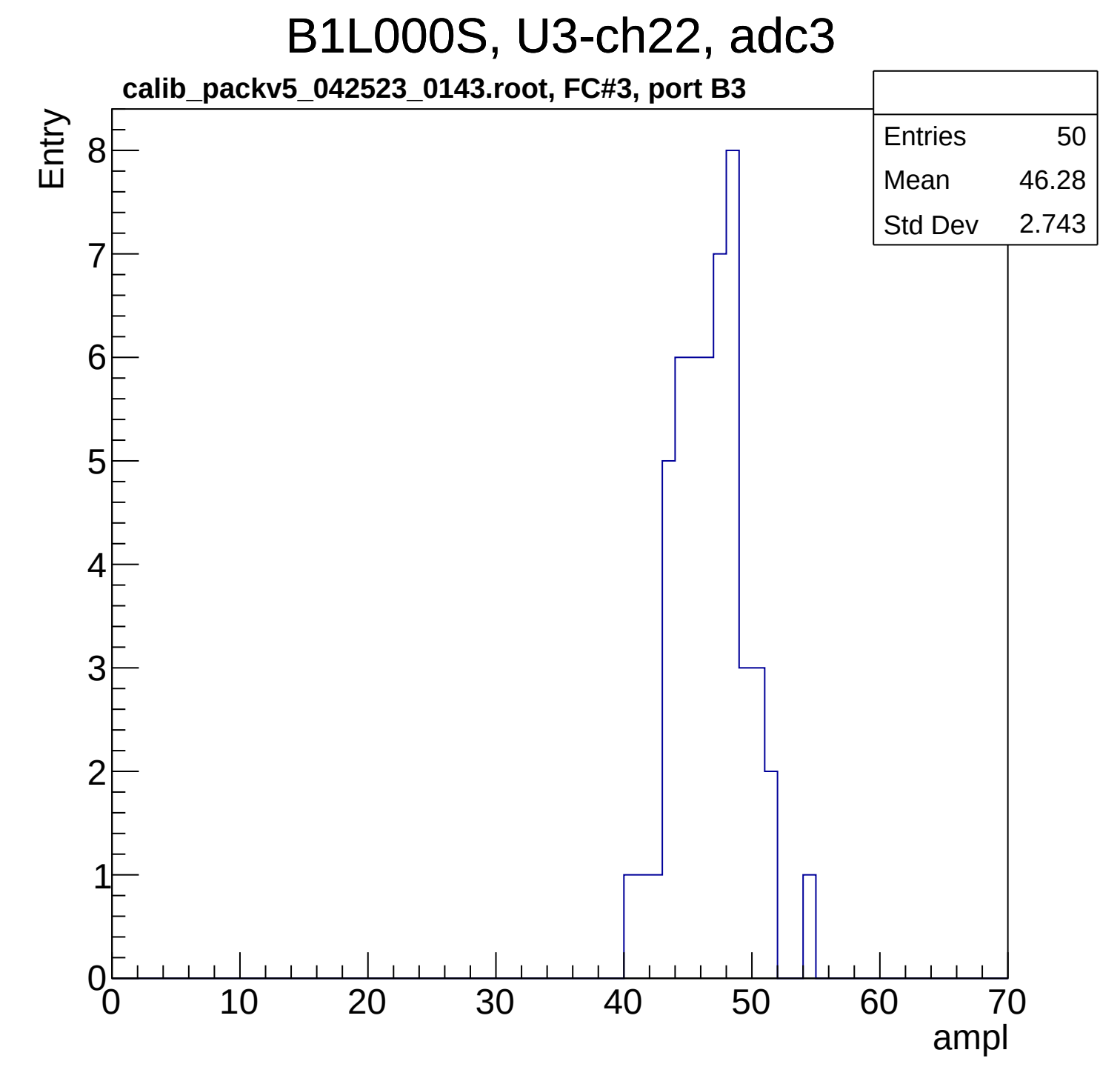
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch22, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 46.28
Std Dev 2.743
Entry
ampl
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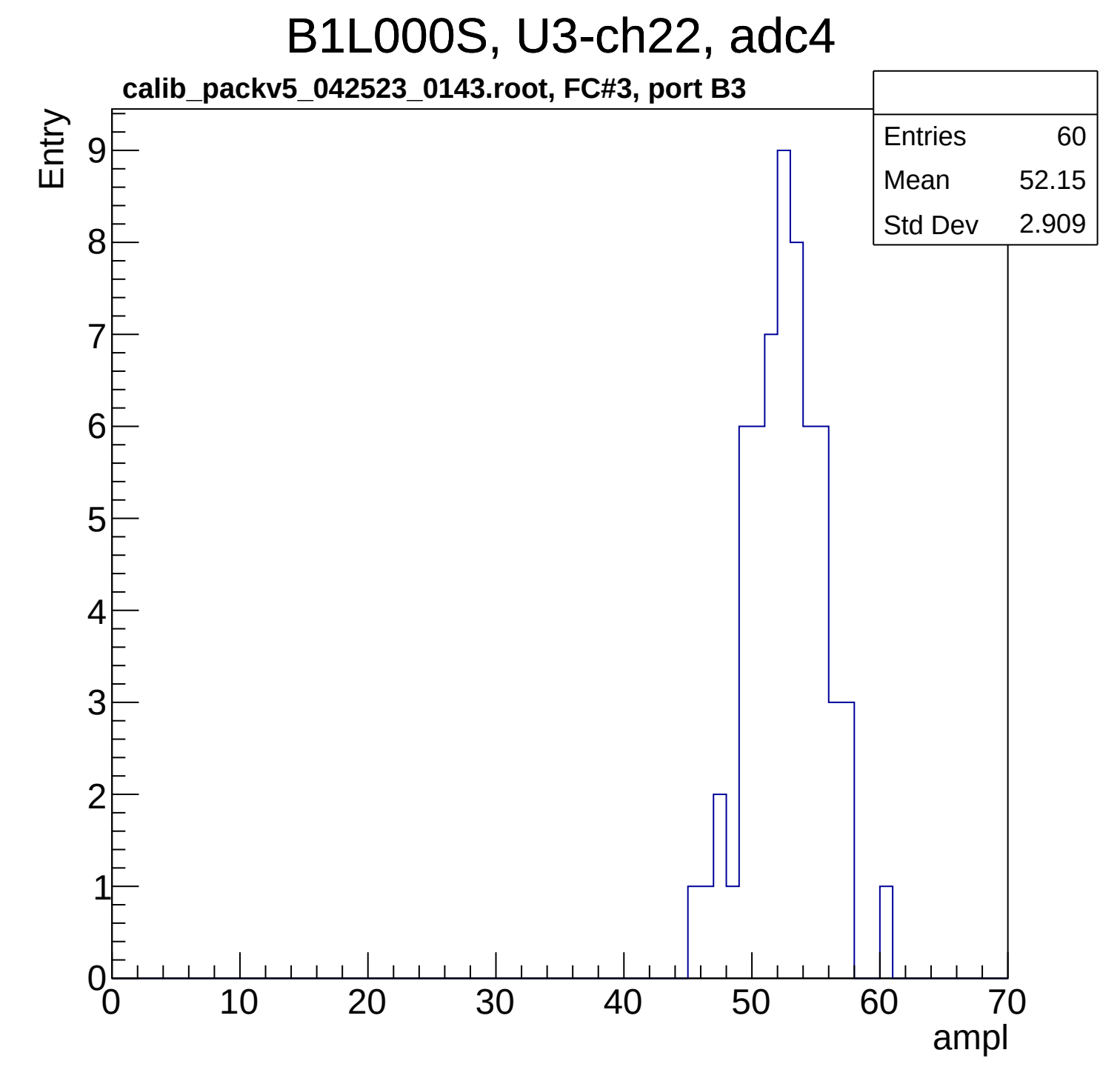
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch22, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 52.15
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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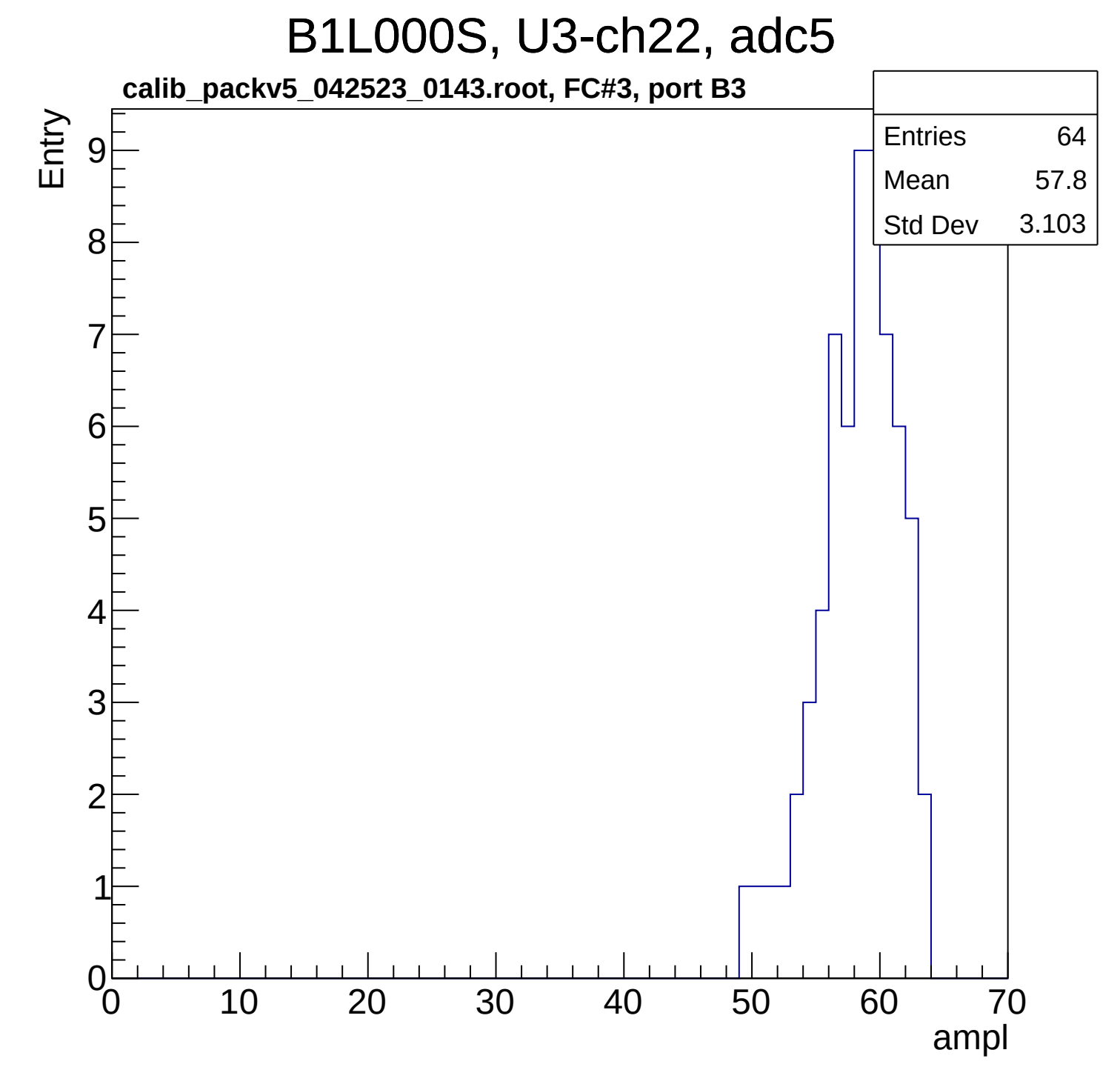
60
70


B1L000S, U3-ch22, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 57.8
Std Dev 3.103
Entry
ampl
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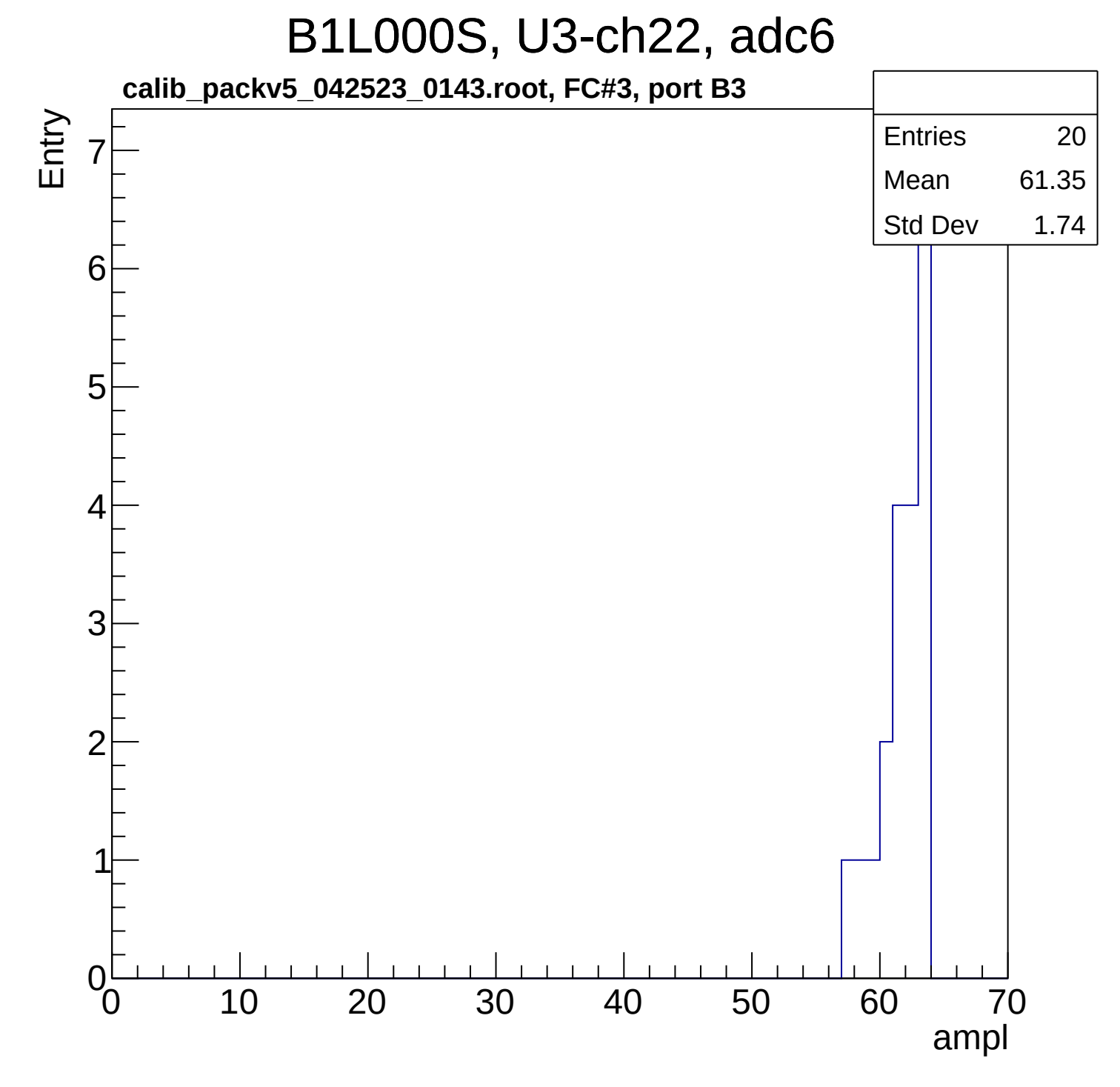
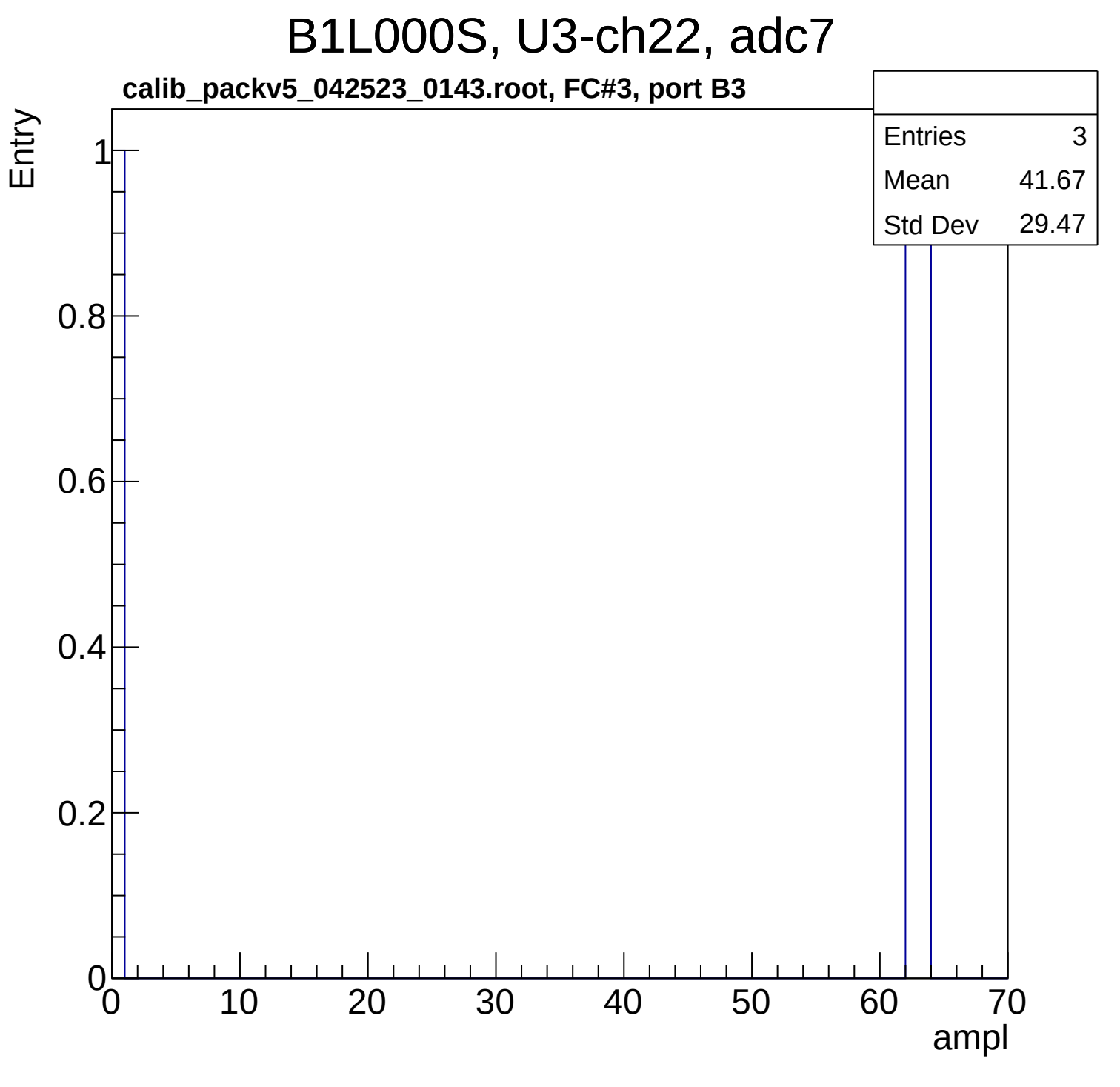
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch22, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 61.35
Std Dev 1.74
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch22, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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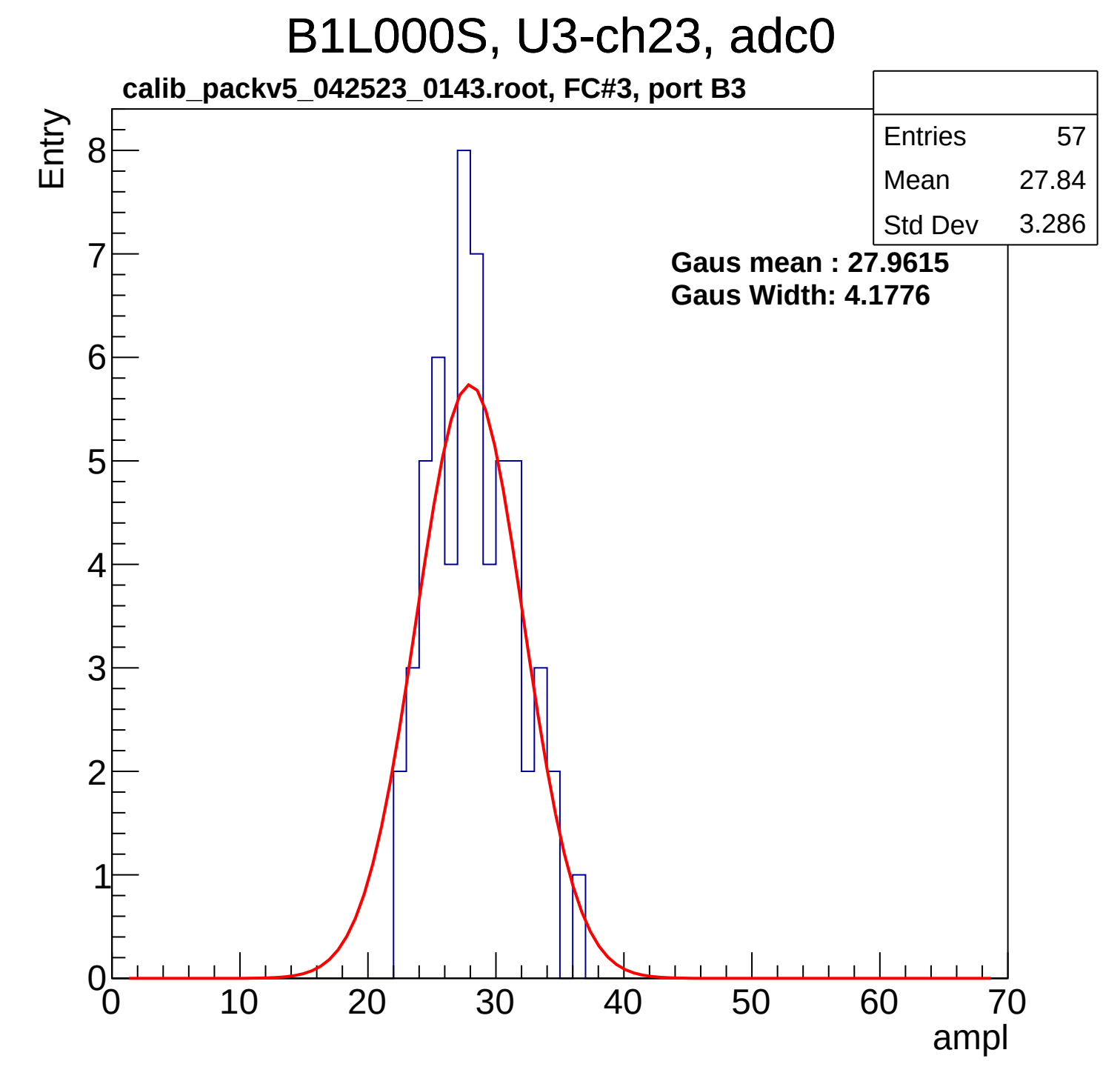
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch23, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 27.84
Std Dev 3.286
Gaus mean : 27.9615
Gaus Width: 4.1776
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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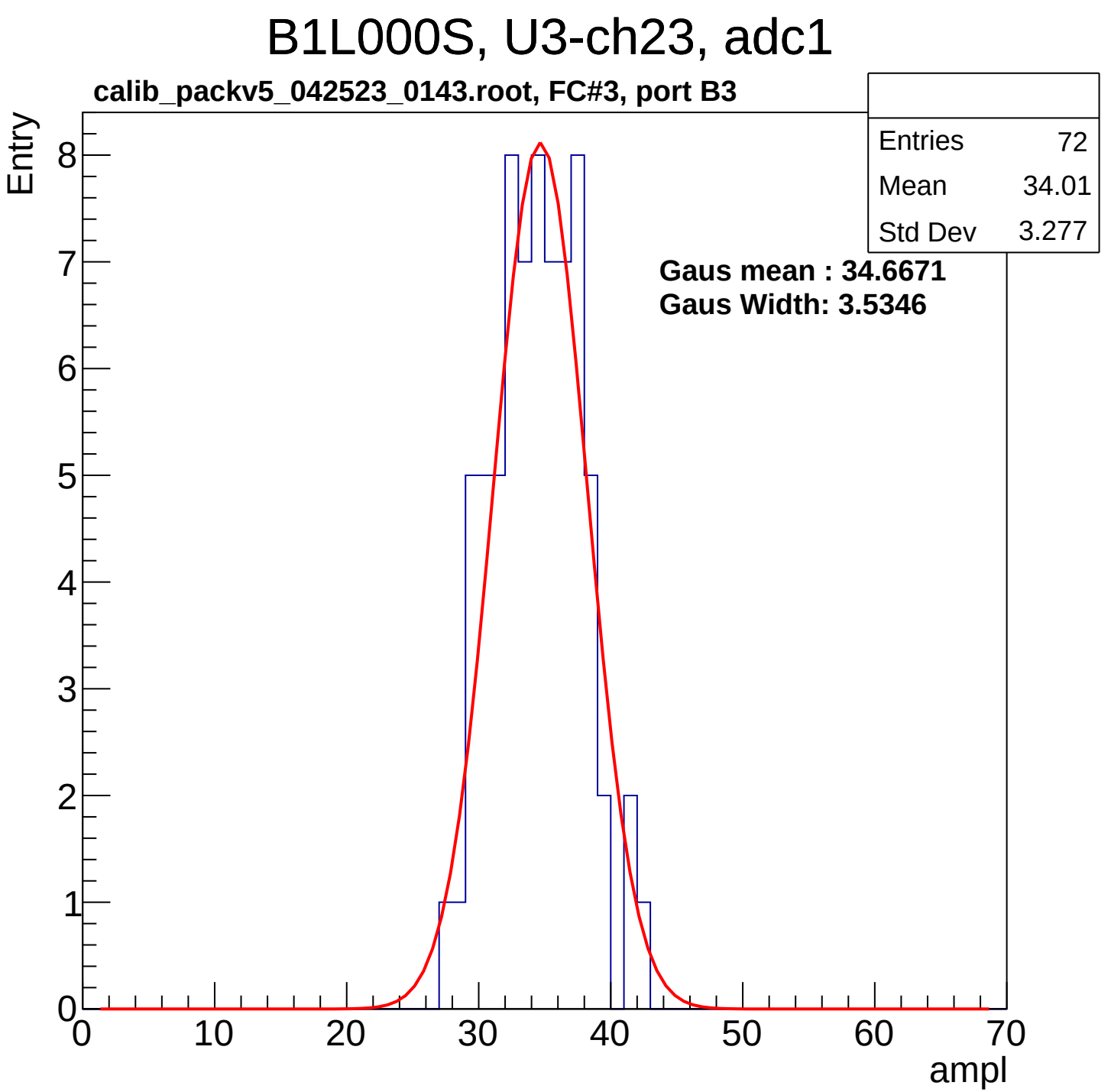
40
50
60
70


B1L000S, U3-ch23, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 34.01
Std Dev 3.277
Gaus mean : 34.6671
Gaus Width: 3.5346
Entry
ampl
0
1
2
3
4
5
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7
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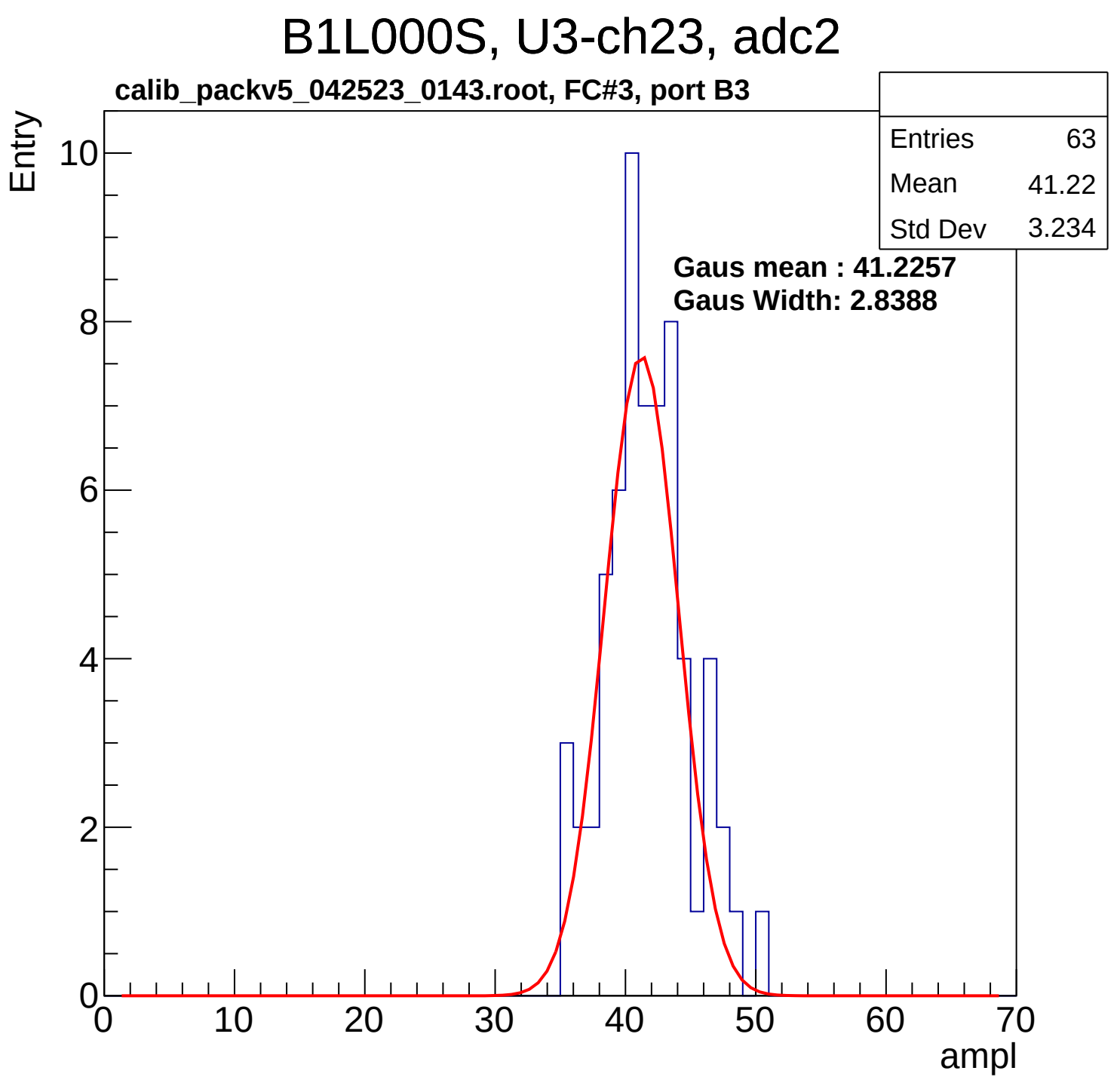
B1L000S, U3-ch23, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 41.22
Std Dev 3.234
Gaus mean : 41.2257
Gaus Width: 2.8388
Entry
ampl
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2
4
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0
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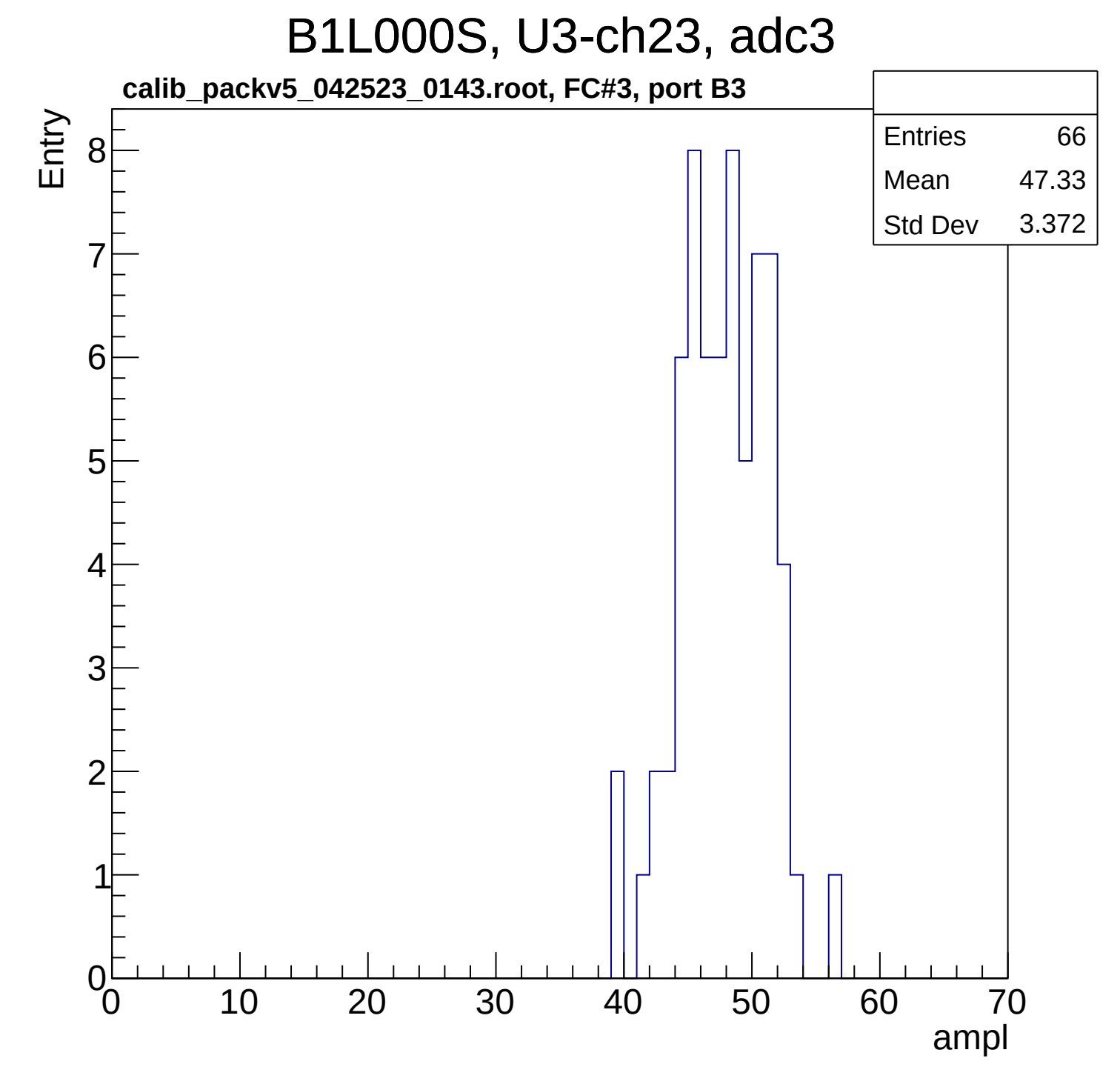
B1L000S, U3-ch23, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.33
Std Dev 3.372
Entry
ampl
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1
2
3
4
5
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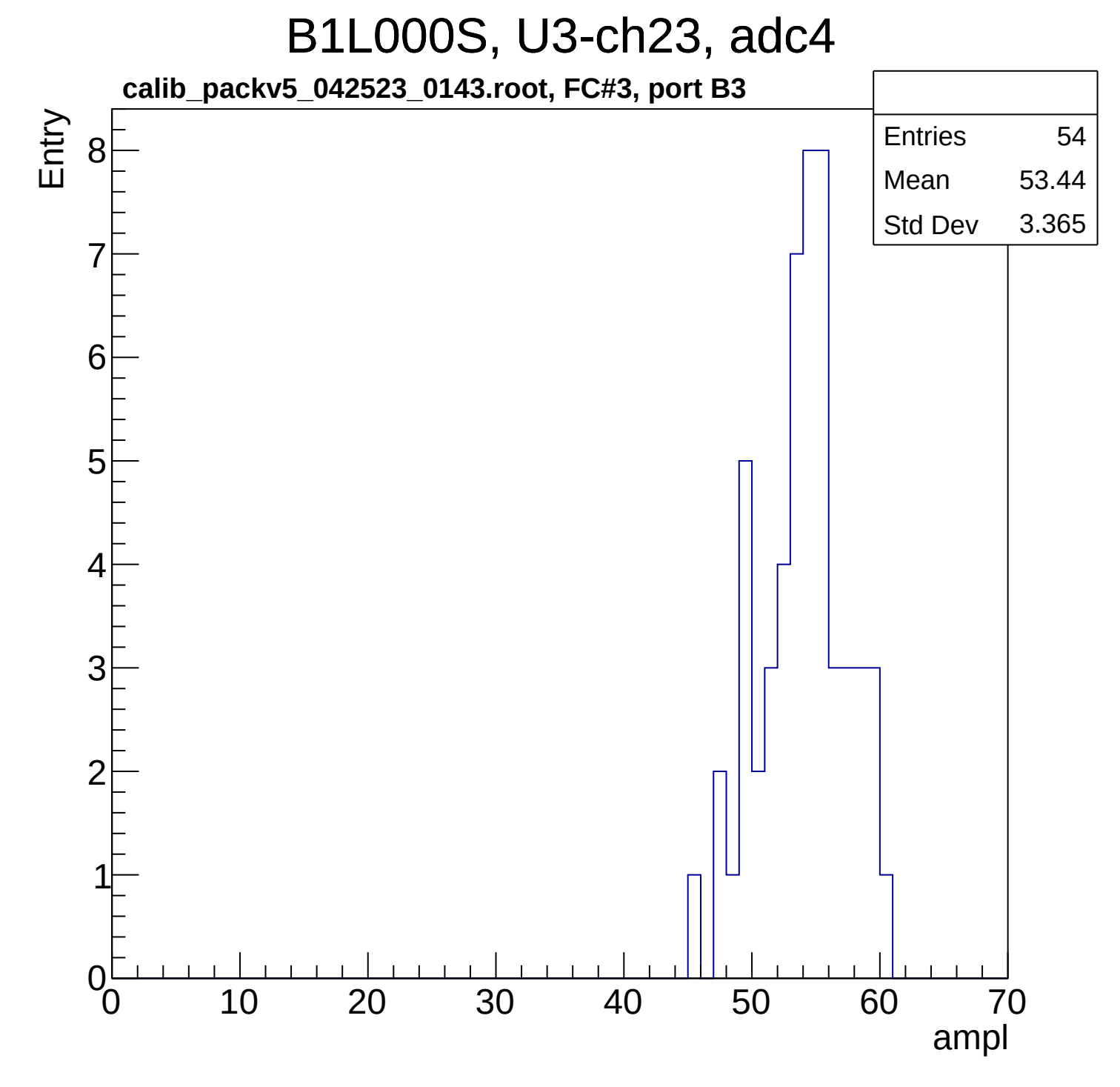
B1L000S, U3-ch23, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 53.44
Std Dev 3.365
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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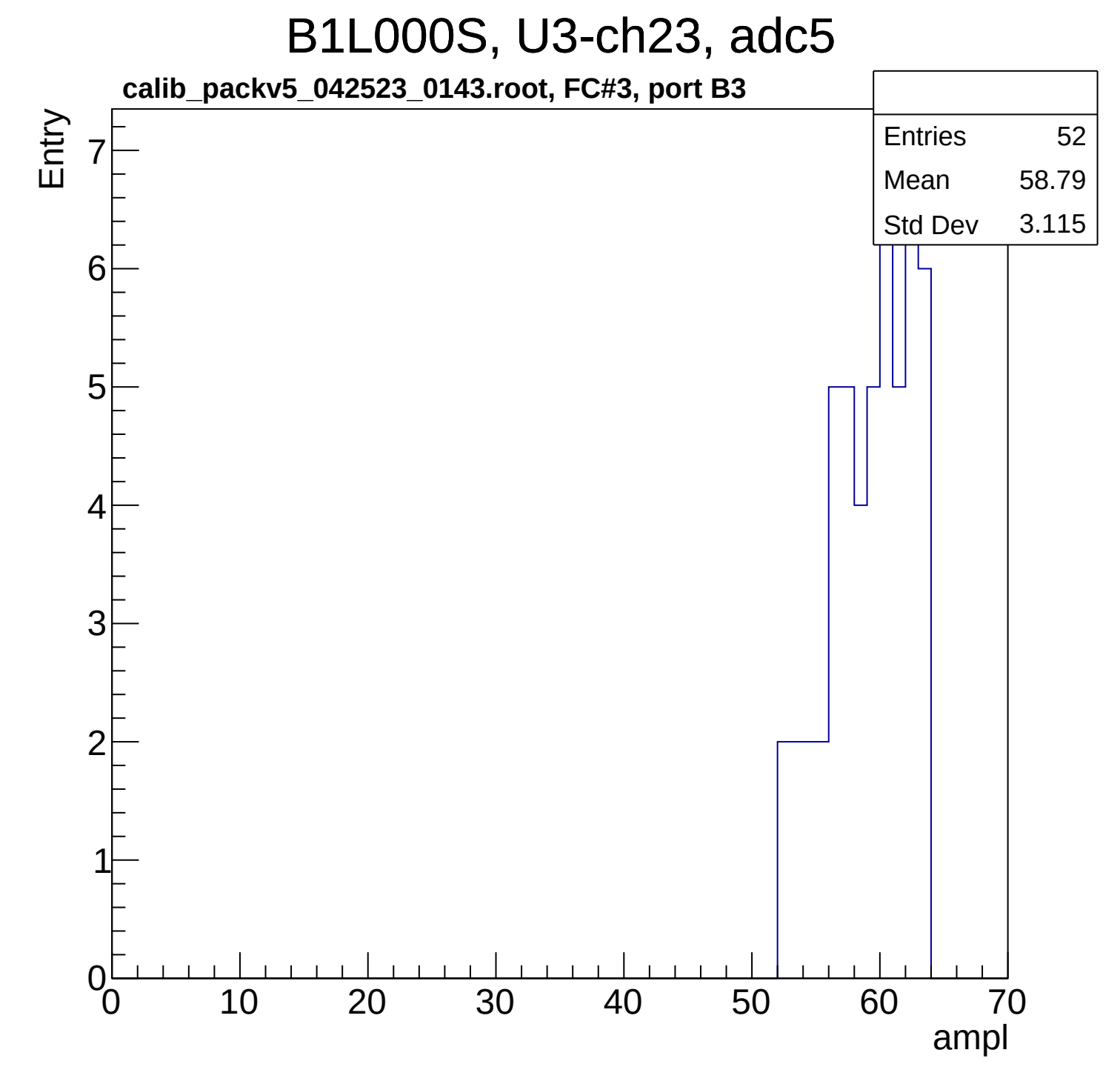
B1L000S, U3-ch23, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.79
Std Dev 3.115
Entry
ampl
0
1
2
3
4
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0
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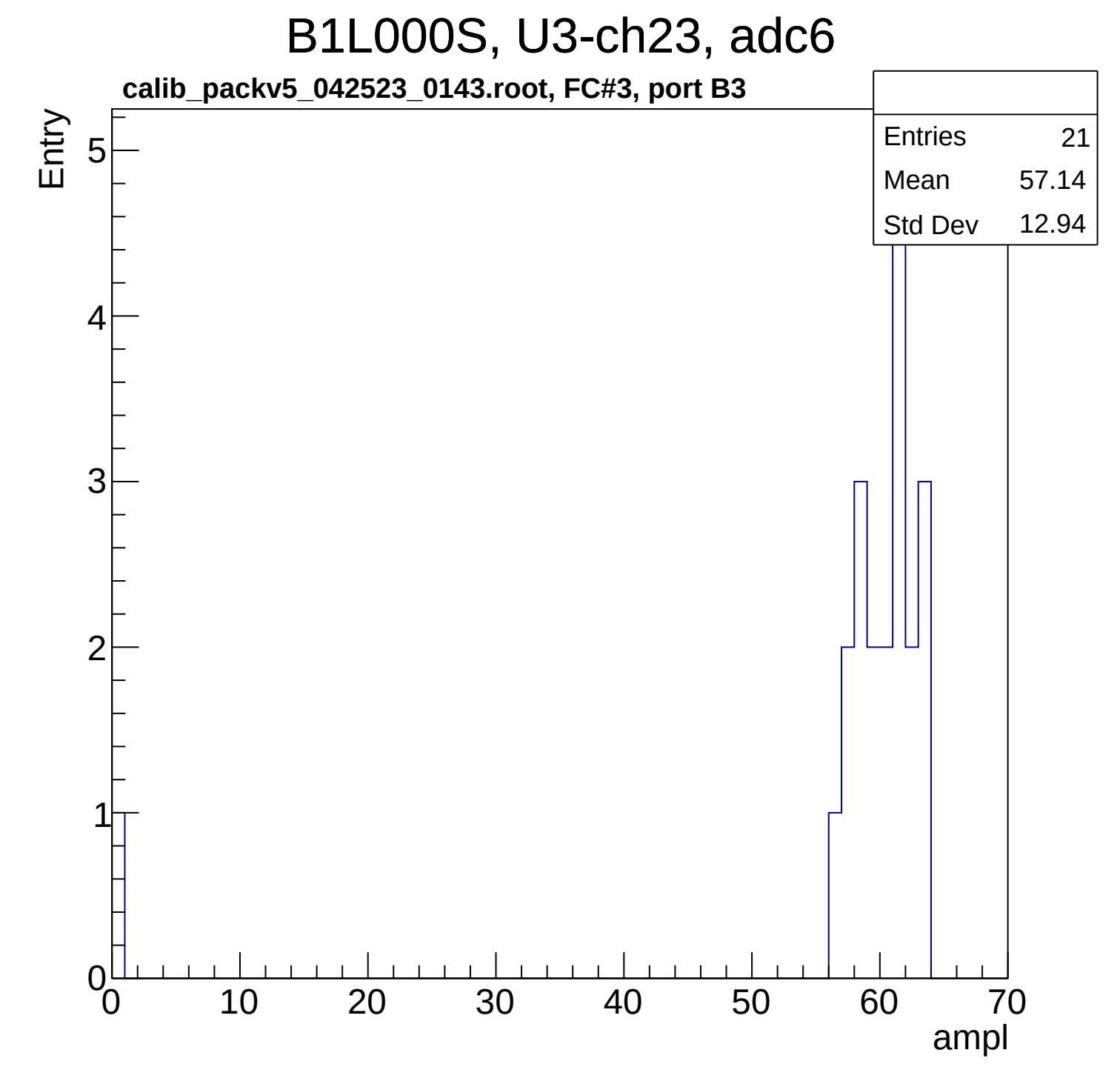
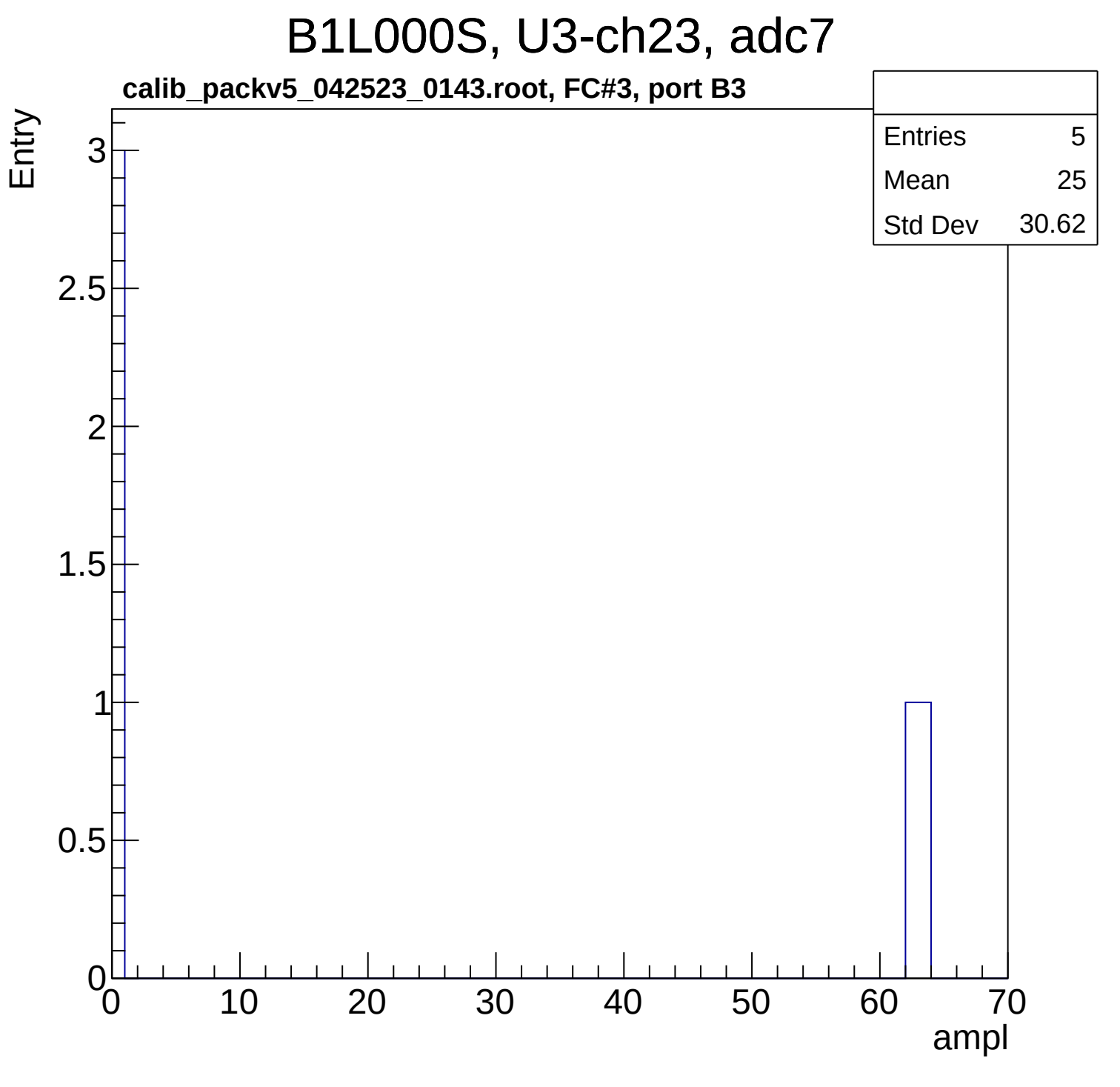
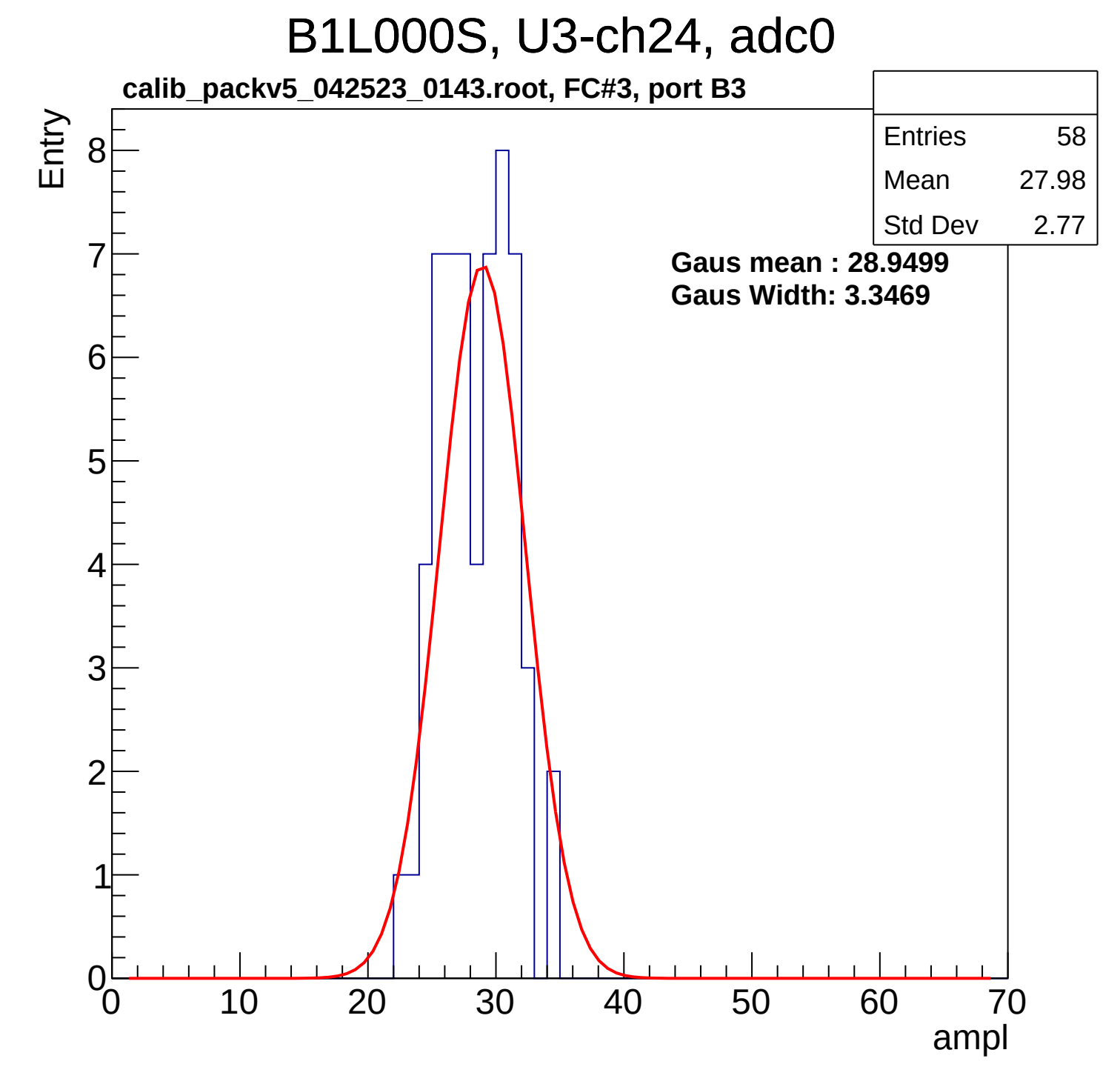

B1L000S, U3-ch23, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 57.14
Std Dev 12.94
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch23, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 25
Std Dev 30.62
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch24, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 27.98
Std Dev 2.77
Gaus mean : 28.9499
Gaus Width: 3.3469
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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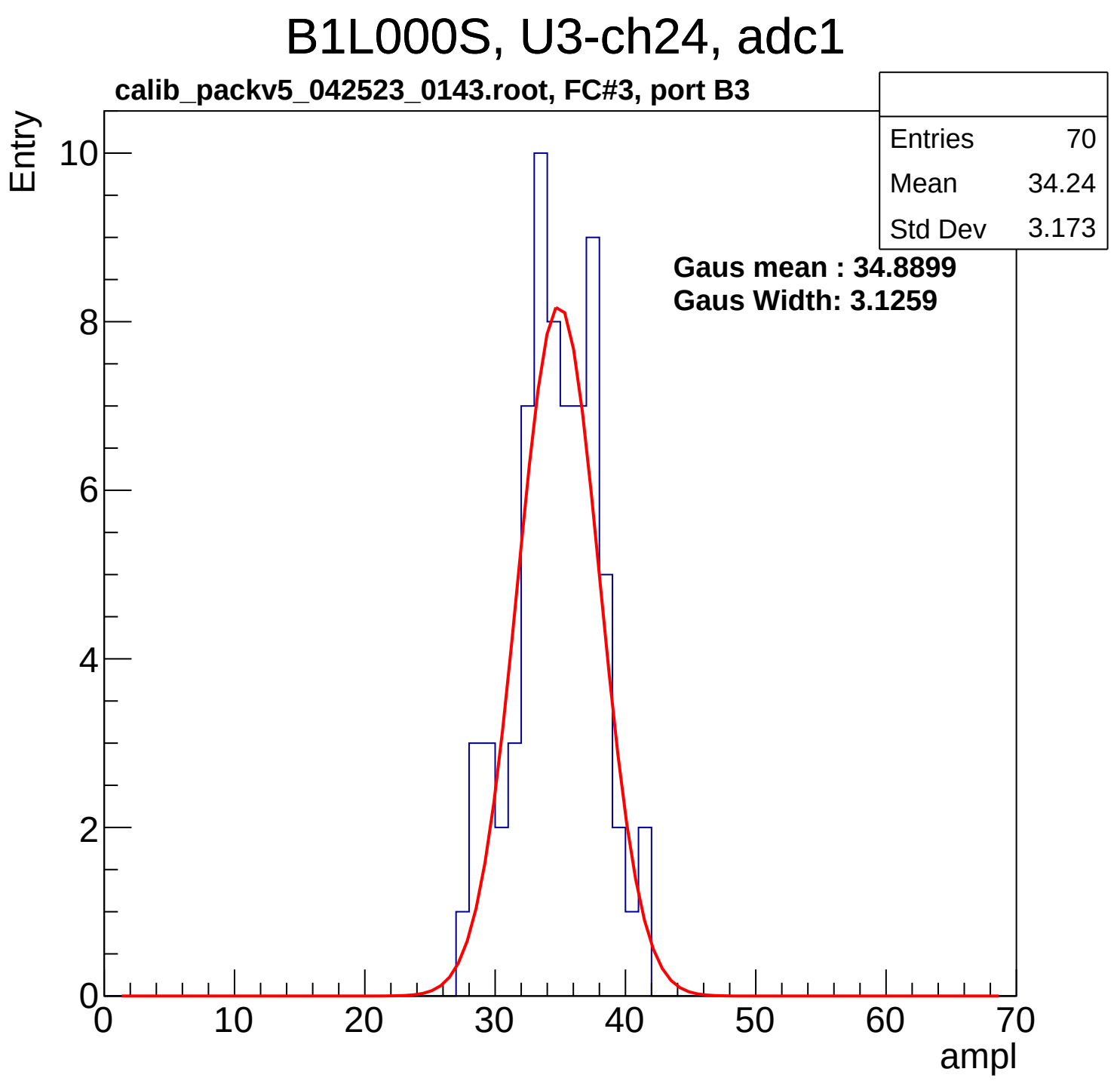
B1L000S, U3-ch24, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 34.24
Std Dev 3.173
Gaus mean : 34.8899
Gaus Width: 3.1259
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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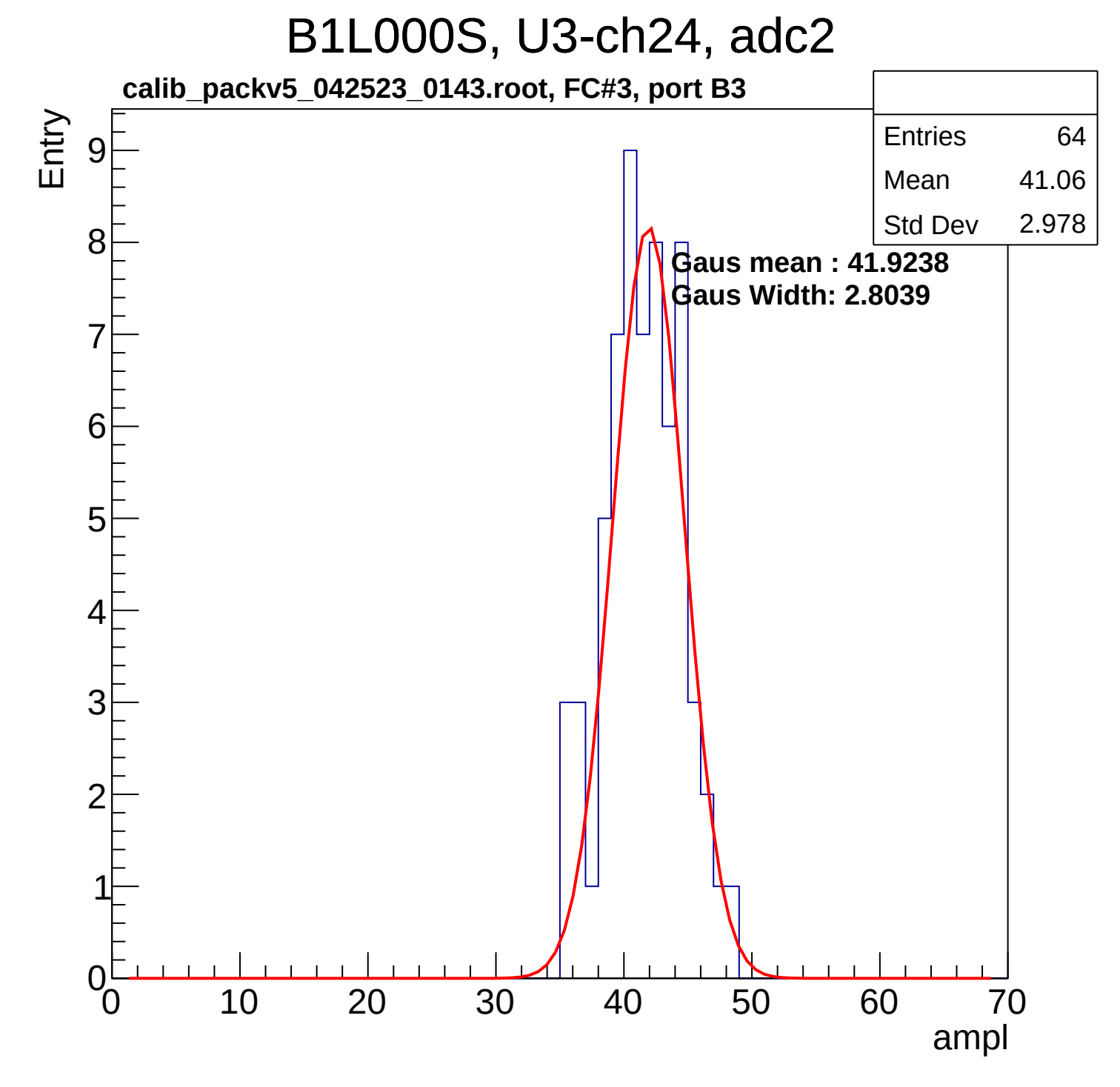
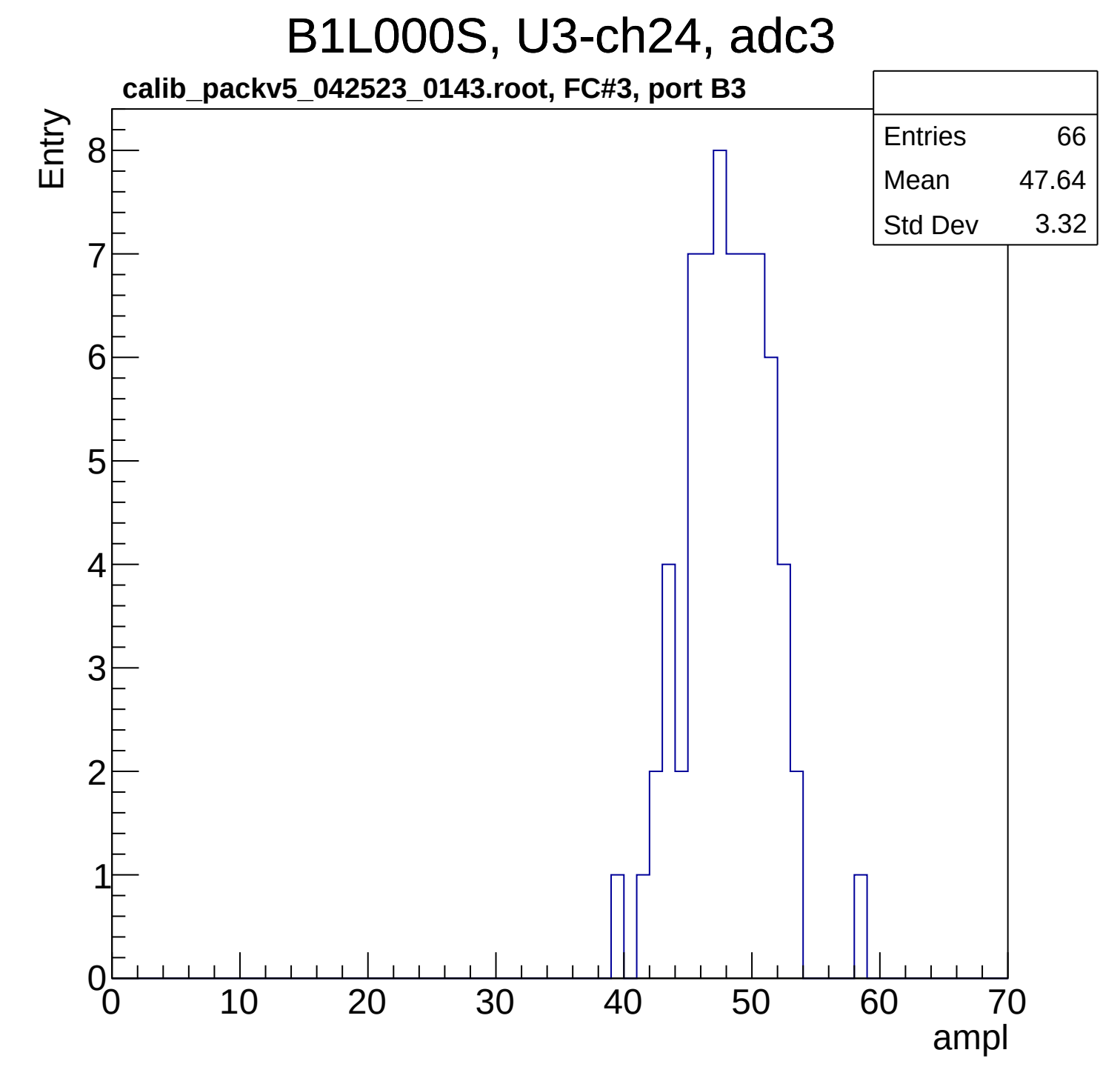
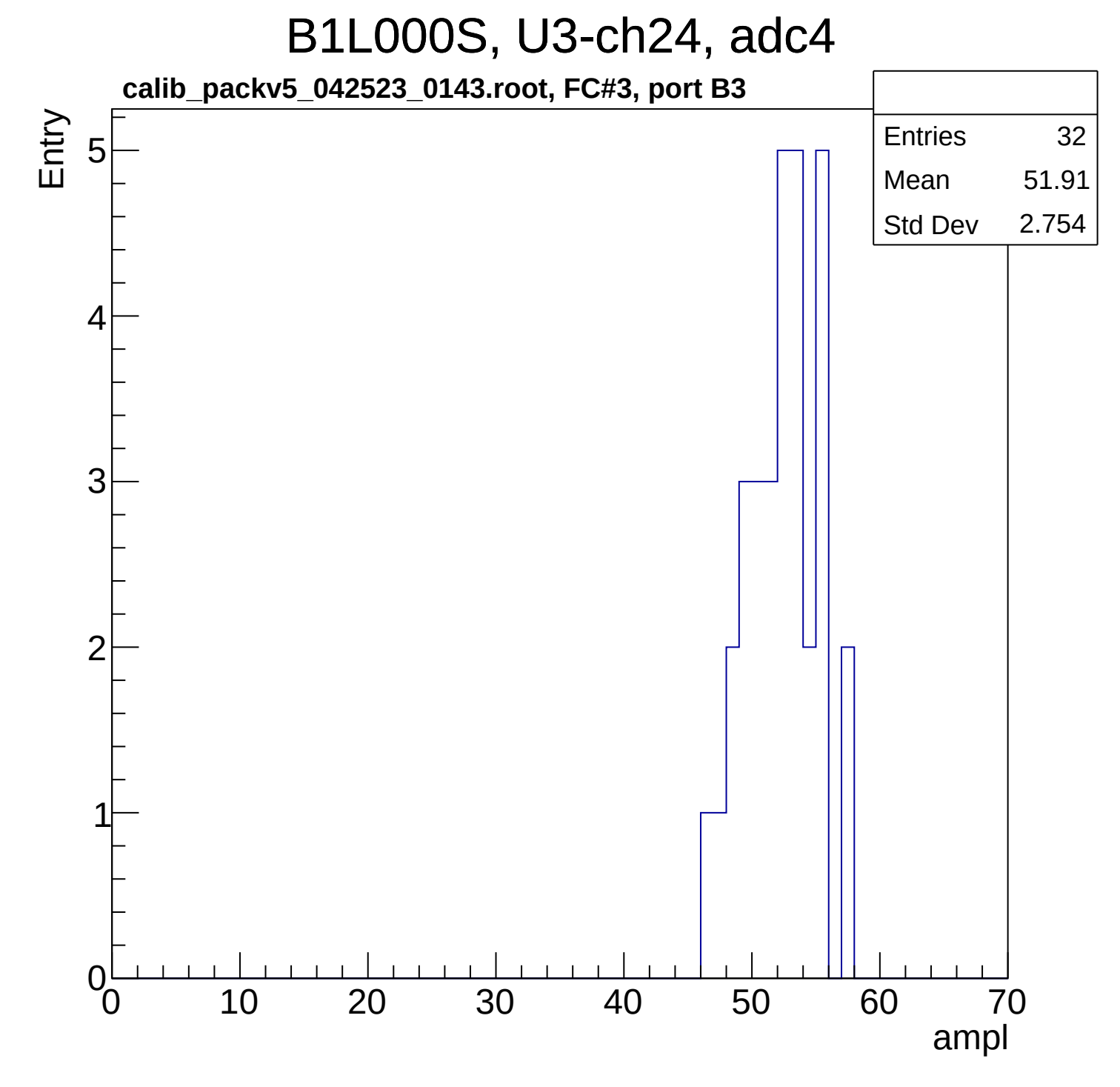
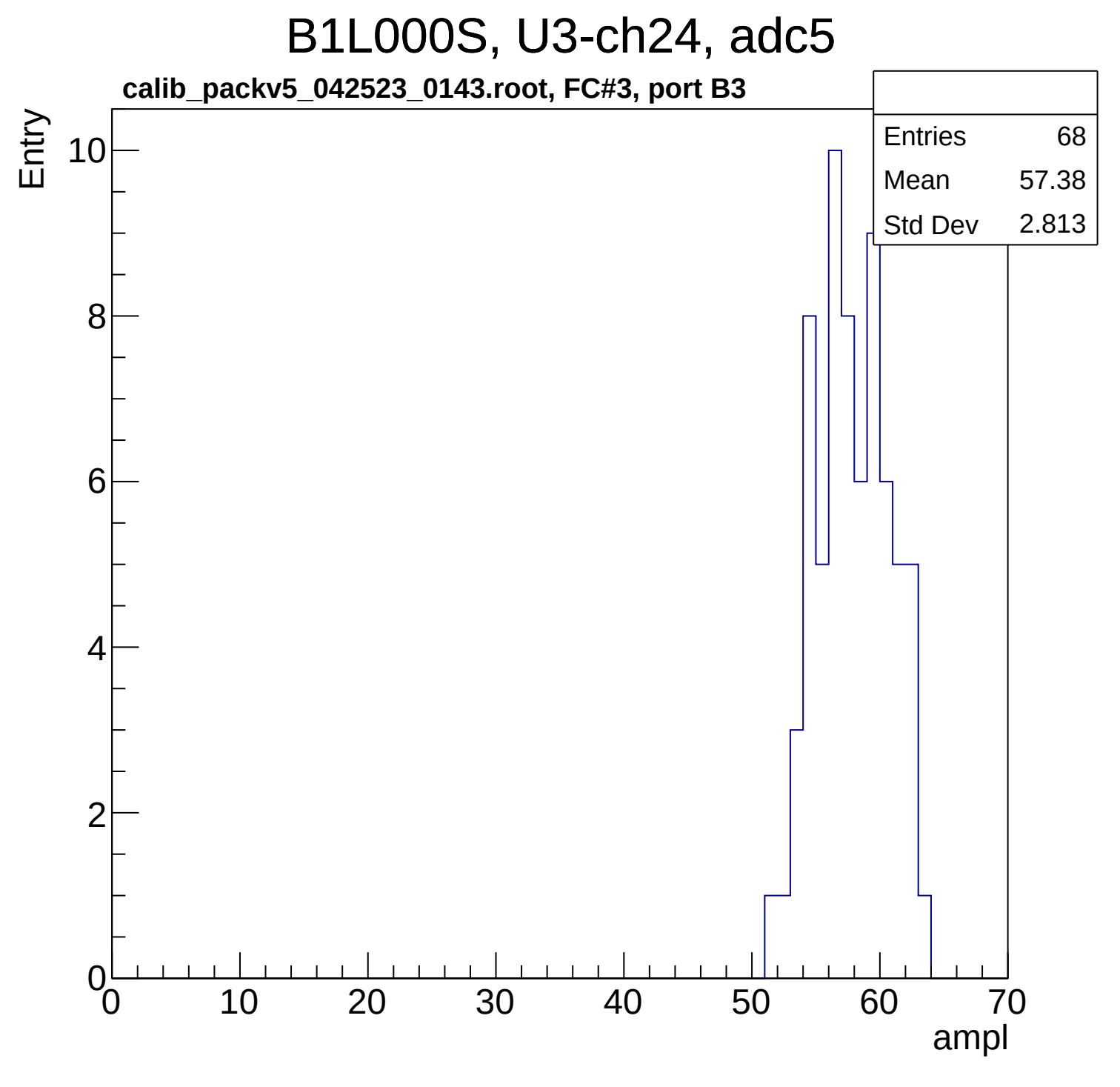

B1L000S, U3-ch24, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 41.06
Std Dev 2.978
Gaus mean : 41.9238
Gaus Width: 2.8039
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch24, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.64
Std Dev 3.32
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch24, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 51.91
Std Dev 2.754
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch24, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 57.38
Std Dev 2.813
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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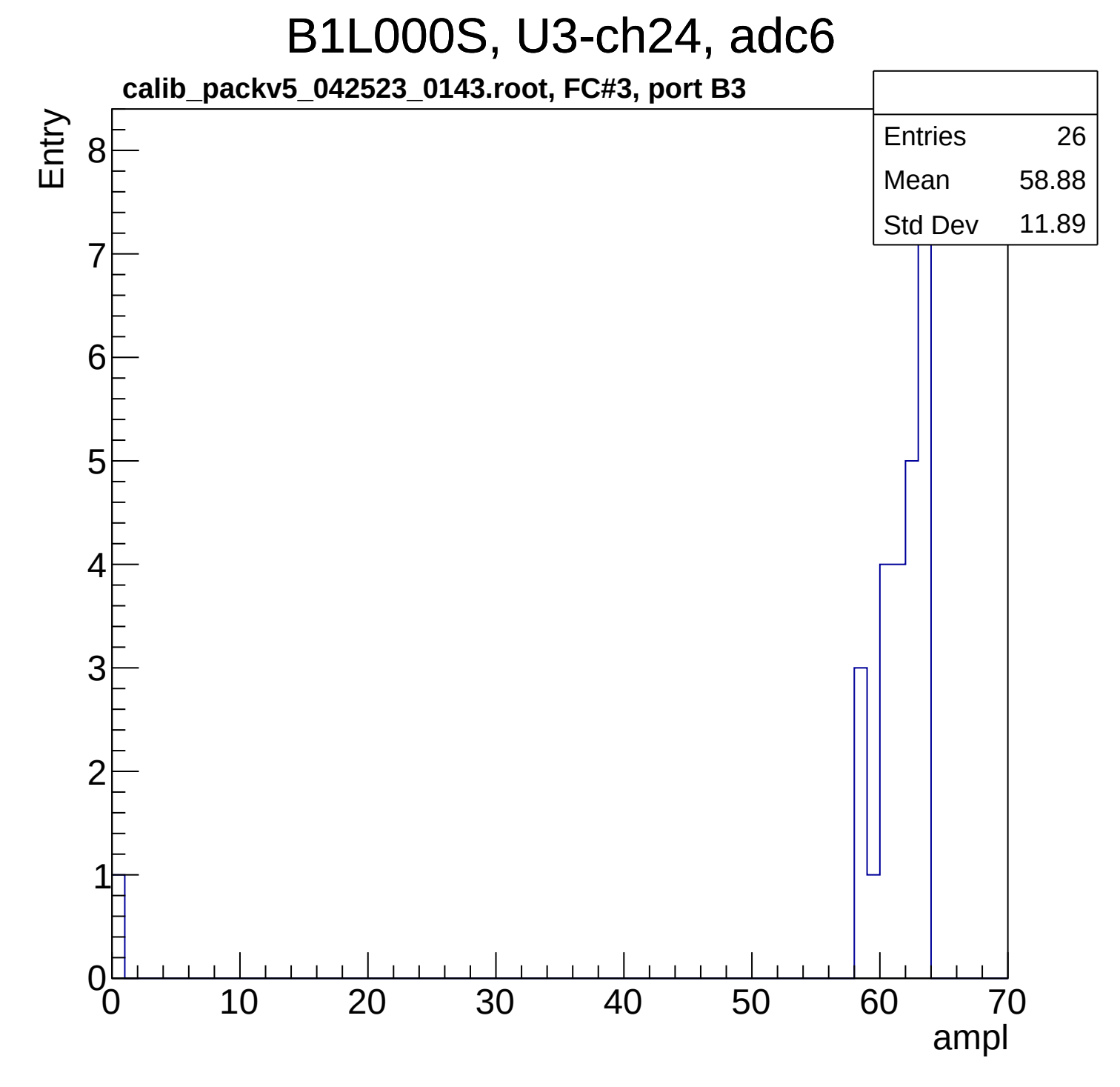
B1L000S, U3-ch24, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 58.88
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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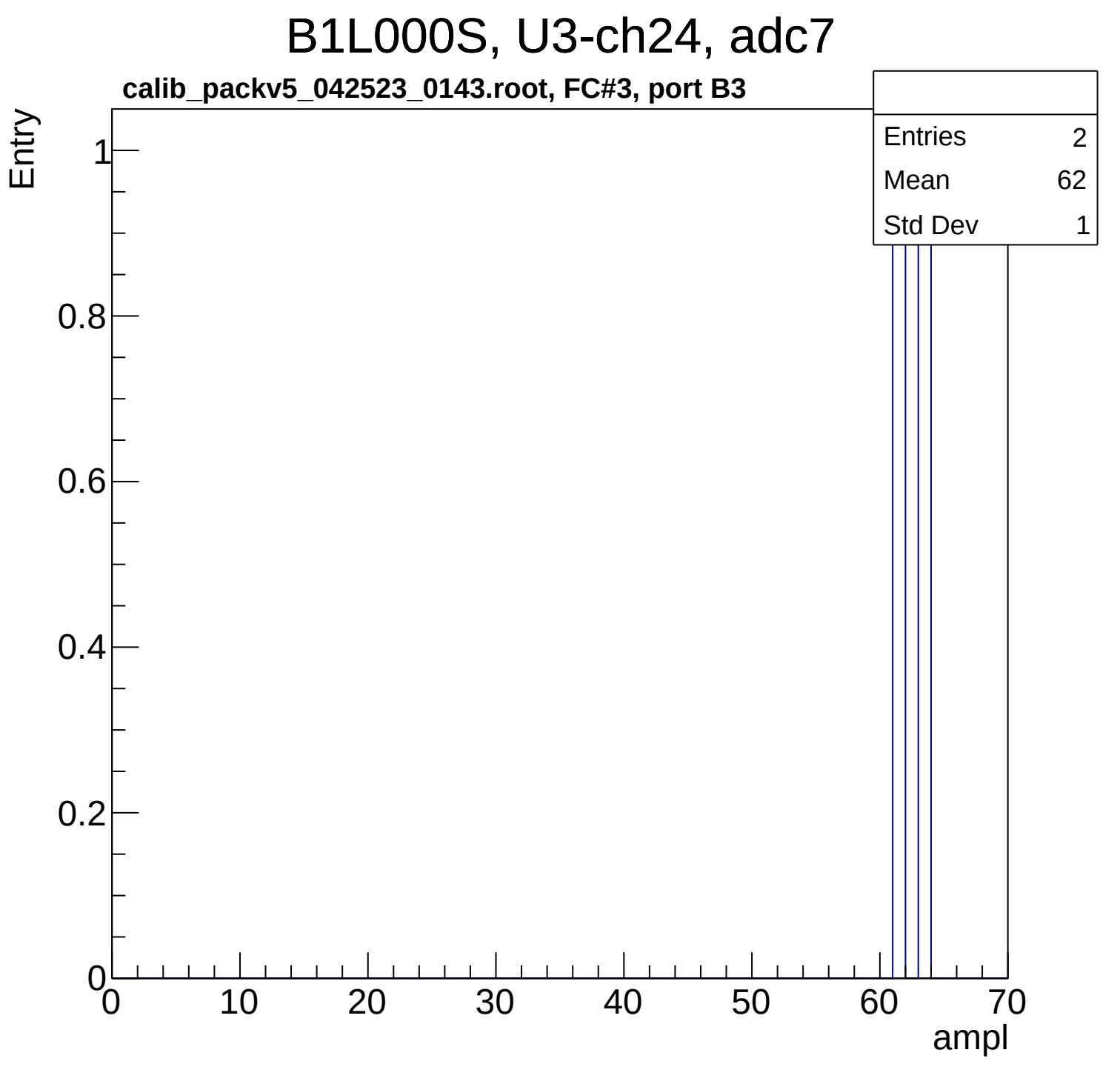
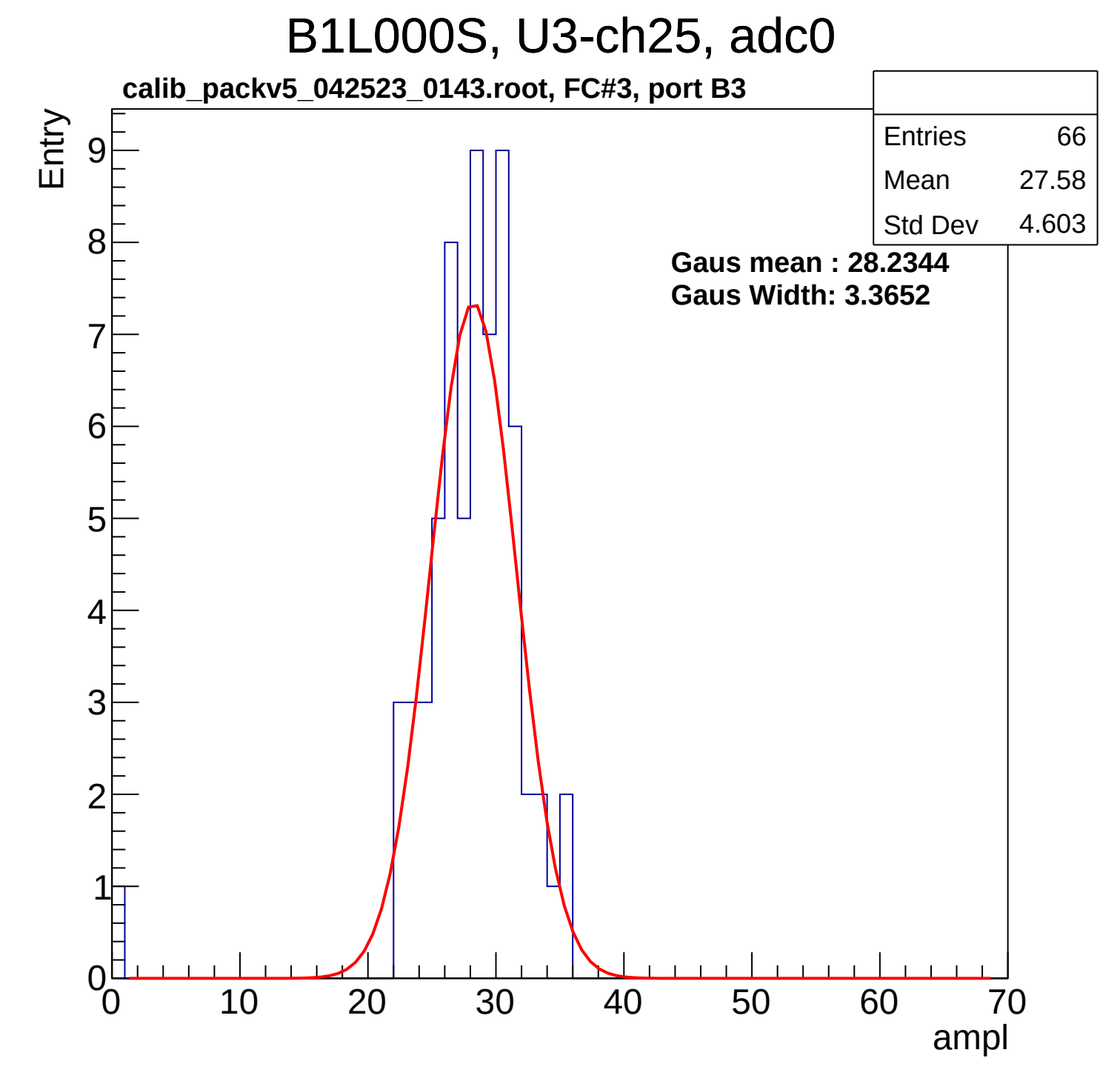
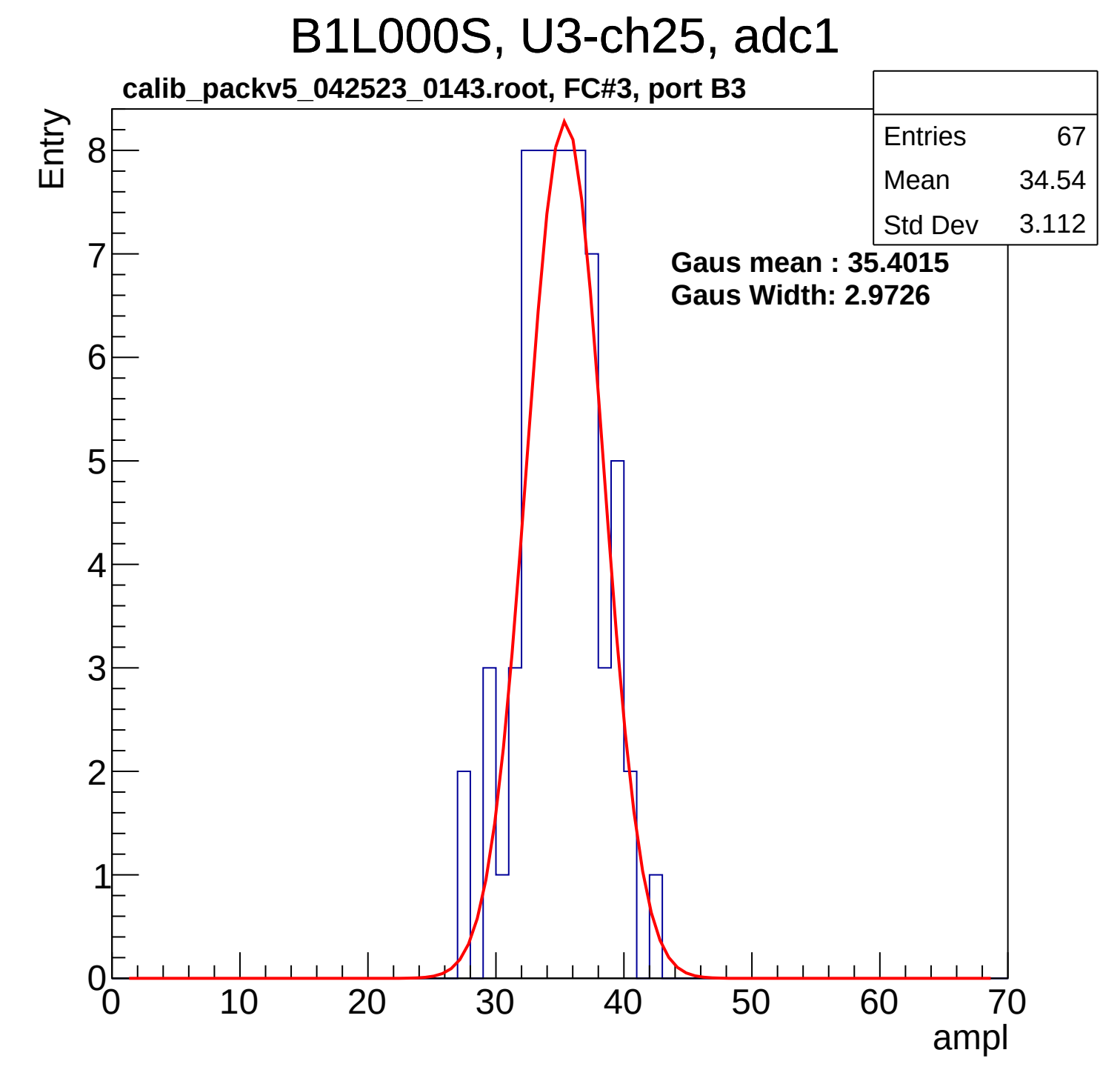
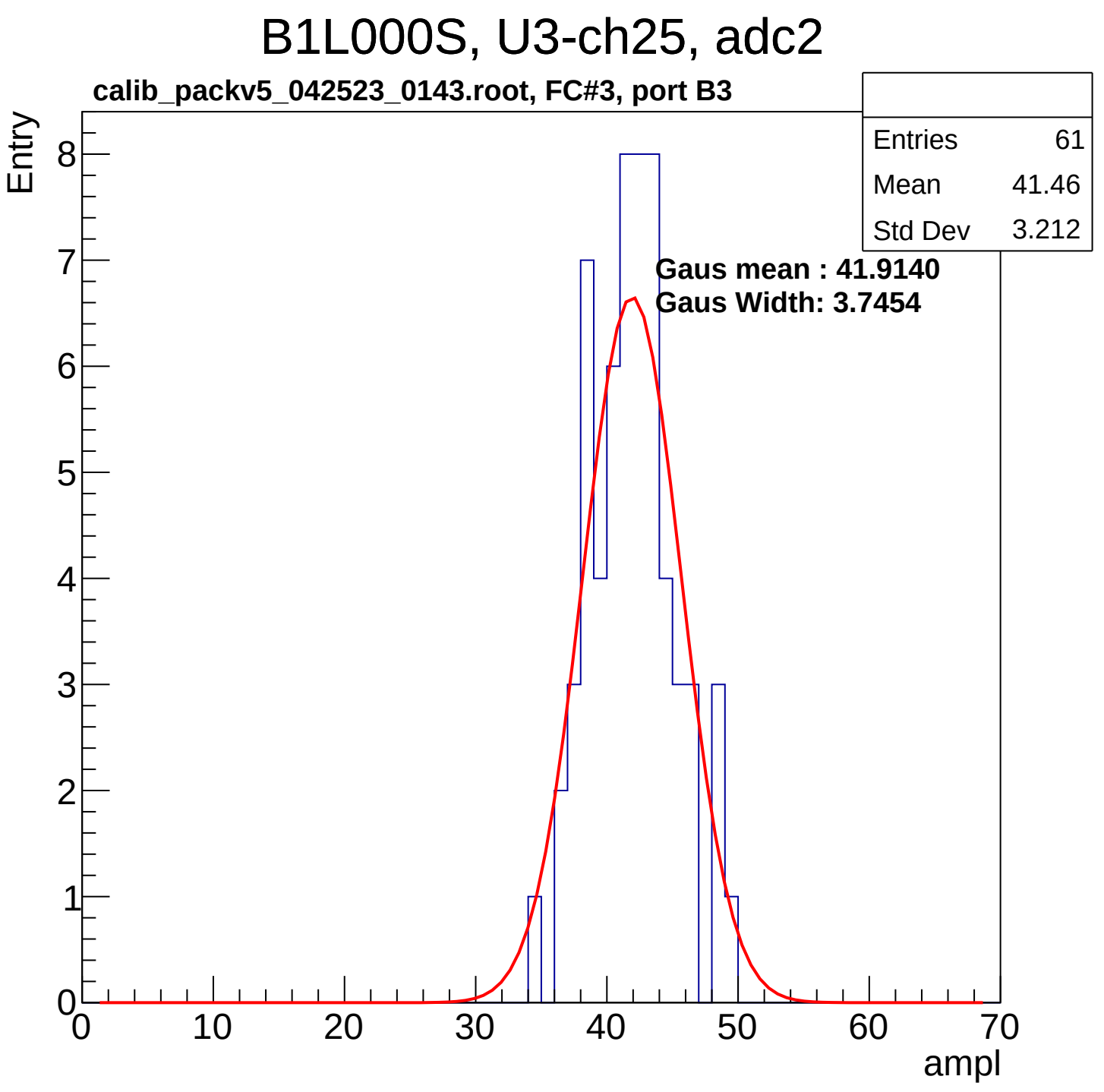
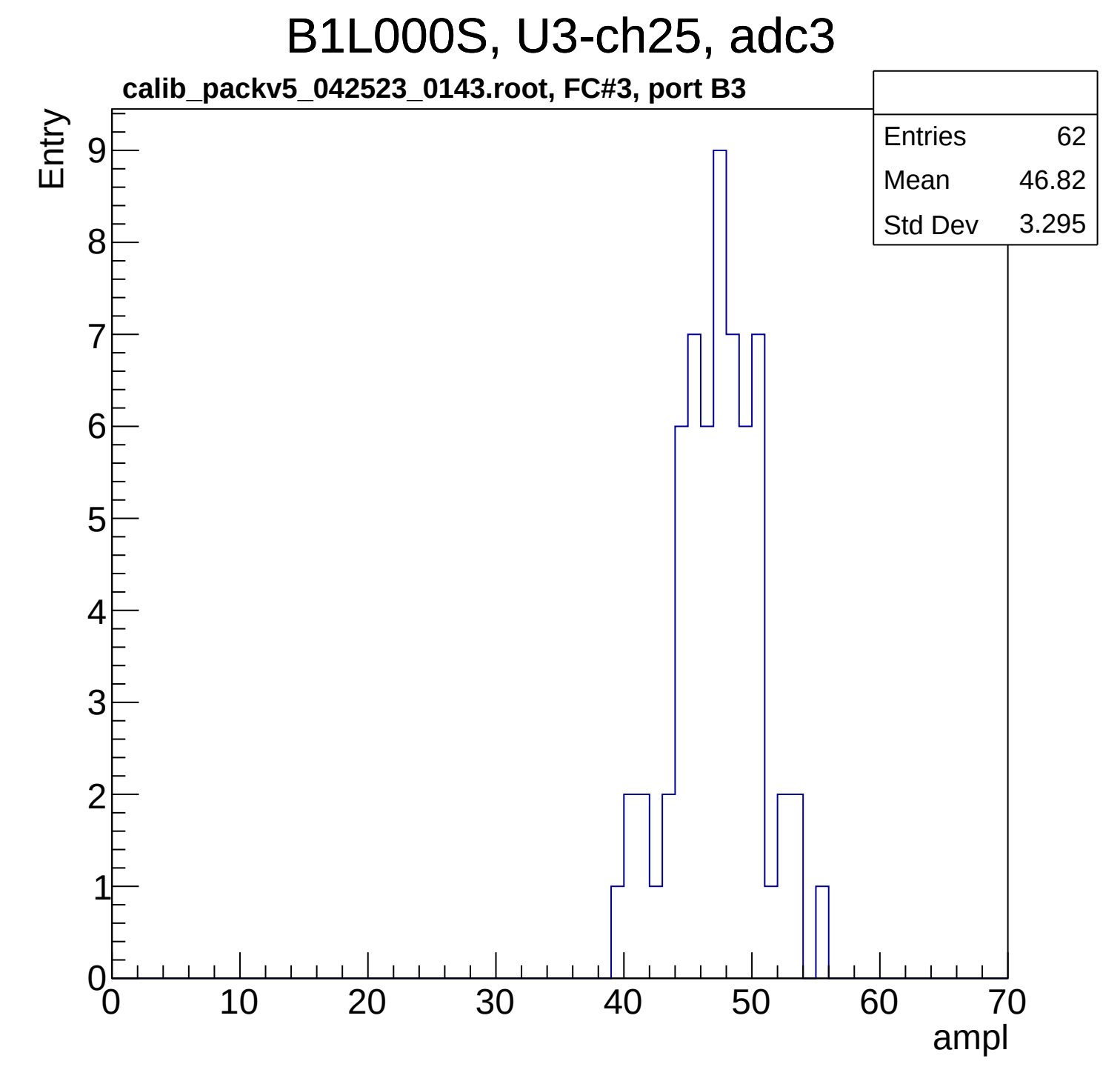
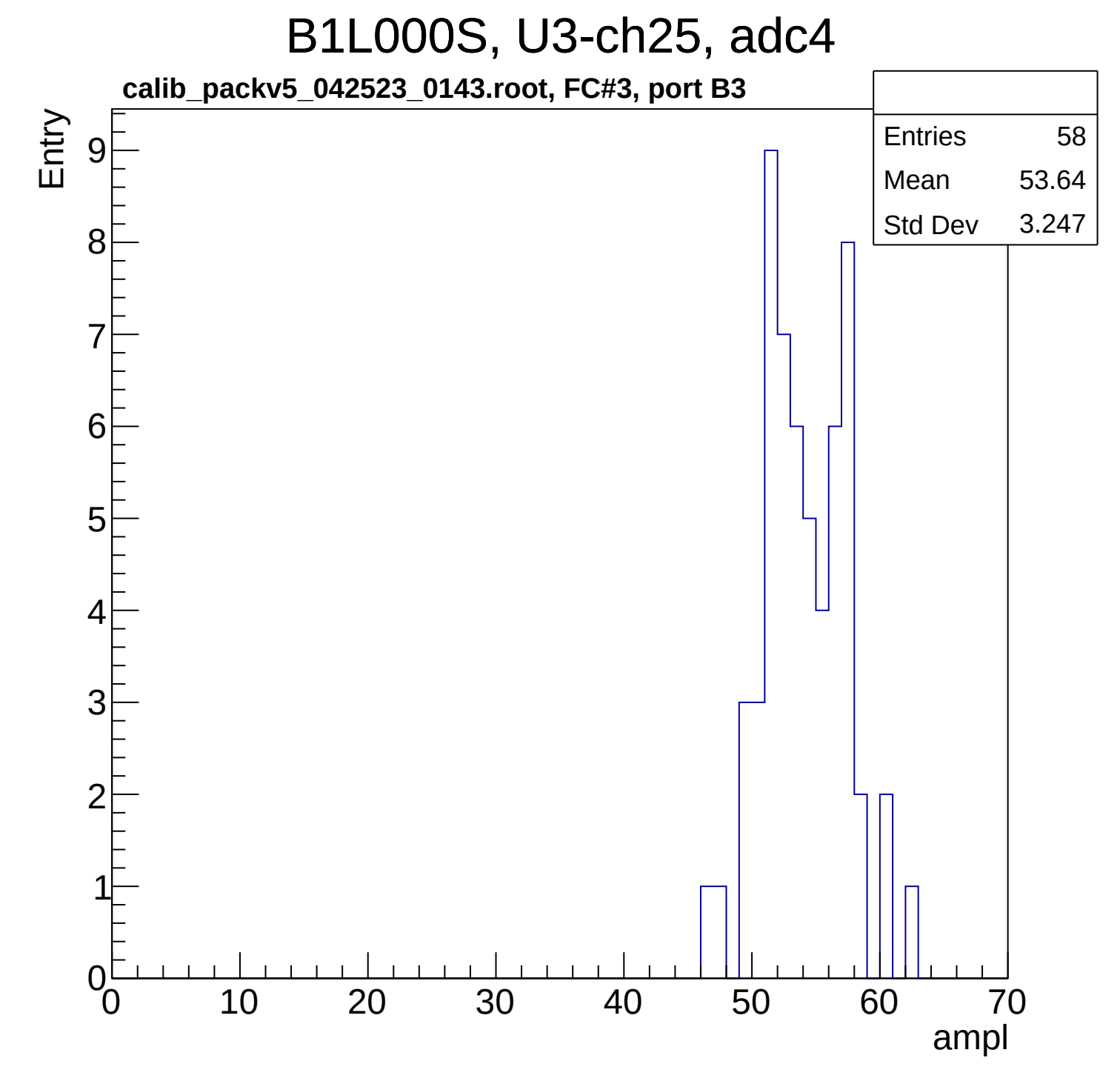

B1L000S, U3-ch24, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch25, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 27.58
Std Dev 4.603
Gaus mean : 28.2344
Gaus Width: 3.3652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch25, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 34.54
Std Dev 3.112
Gaus mean : 35.4015
Gaus Width: 2.9726
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch25, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 41.46
Std Dev 3.212
Gaus mean : 41.9140
Gaus Width: 3.7454
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch25, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 46.82
Std Dev 3.295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch25, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 53.64
Std Dev 3.247
Entry
ampl
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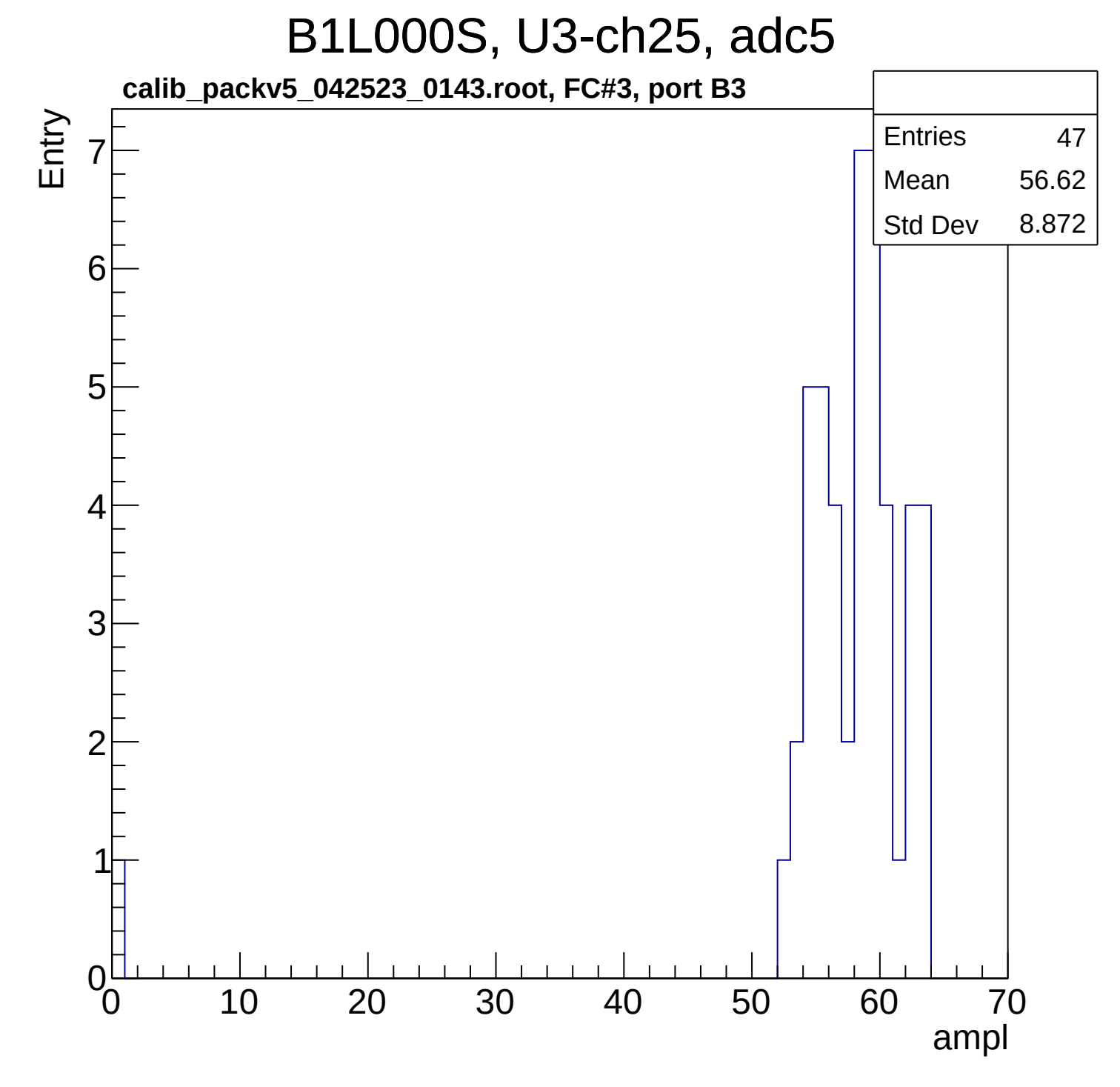
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch25, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 56.62
Std Dev 8.872
Entry
ampl
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1
2
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4
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6
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0
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20
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40
50
60
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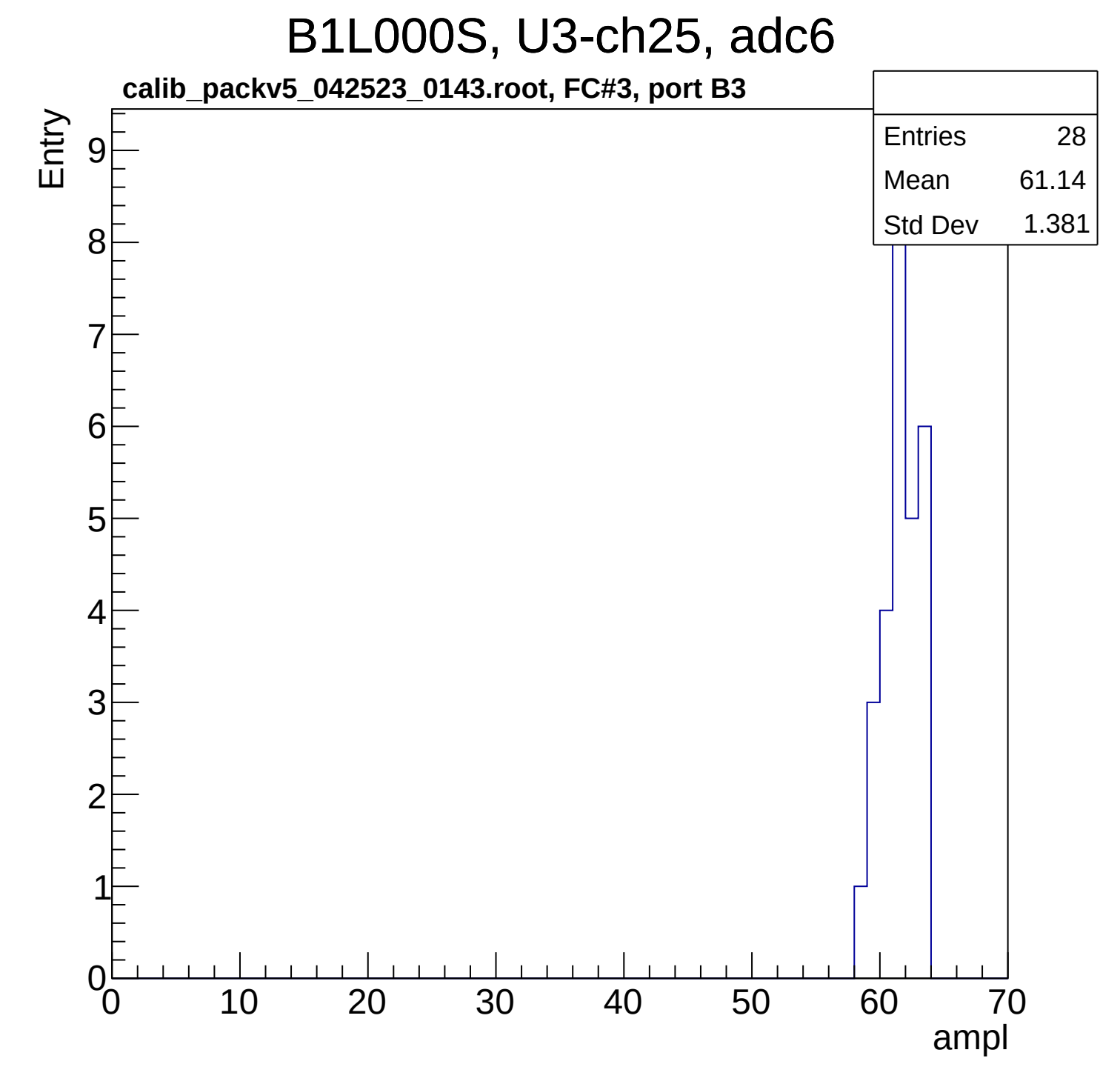
B1L000S, U3-ch25, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 28
Mean 61.14
Std Dev 1.381
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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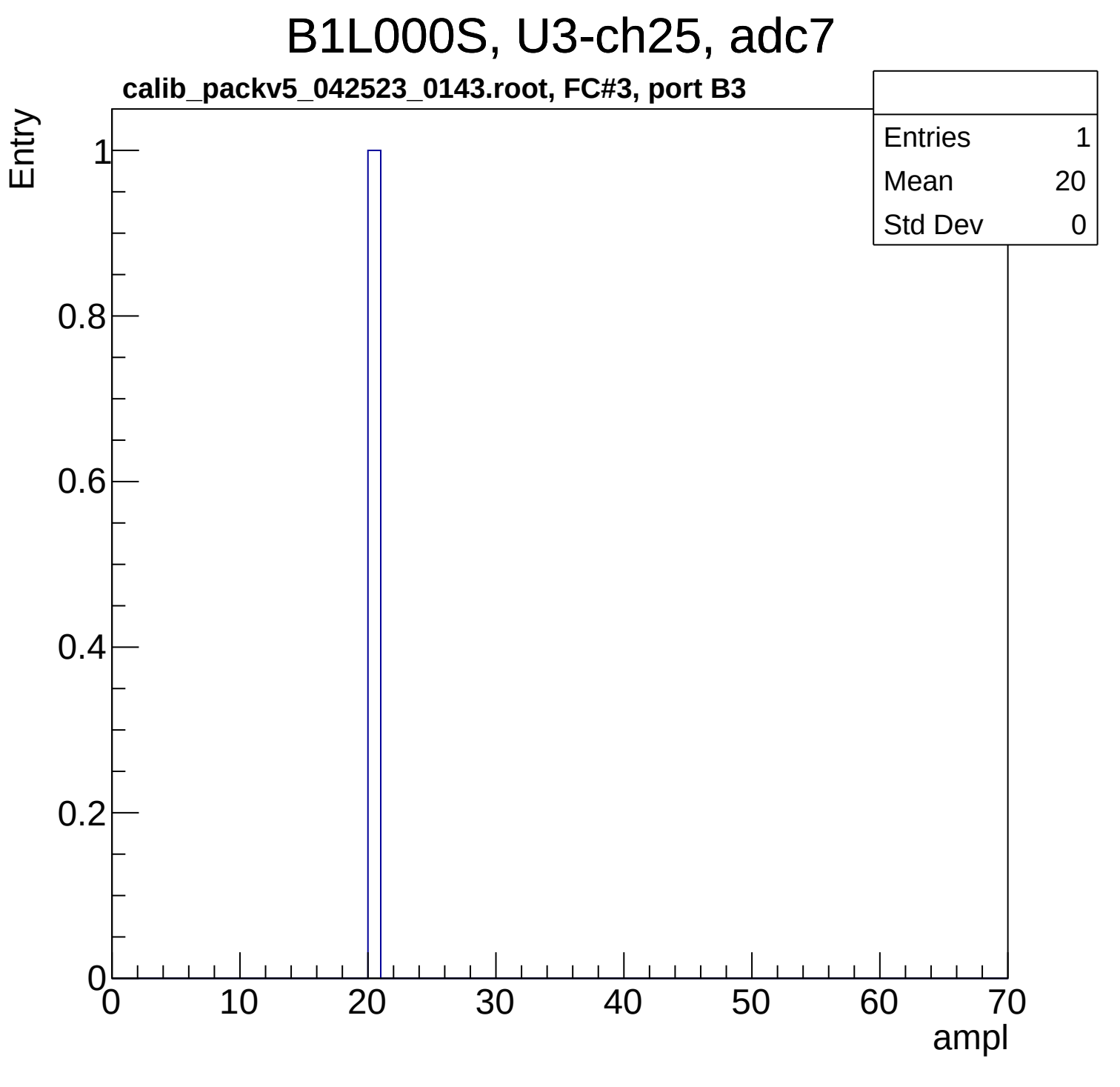
60
70


B1L000S, U3-ch25, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 20
Std Dev 0
Entry
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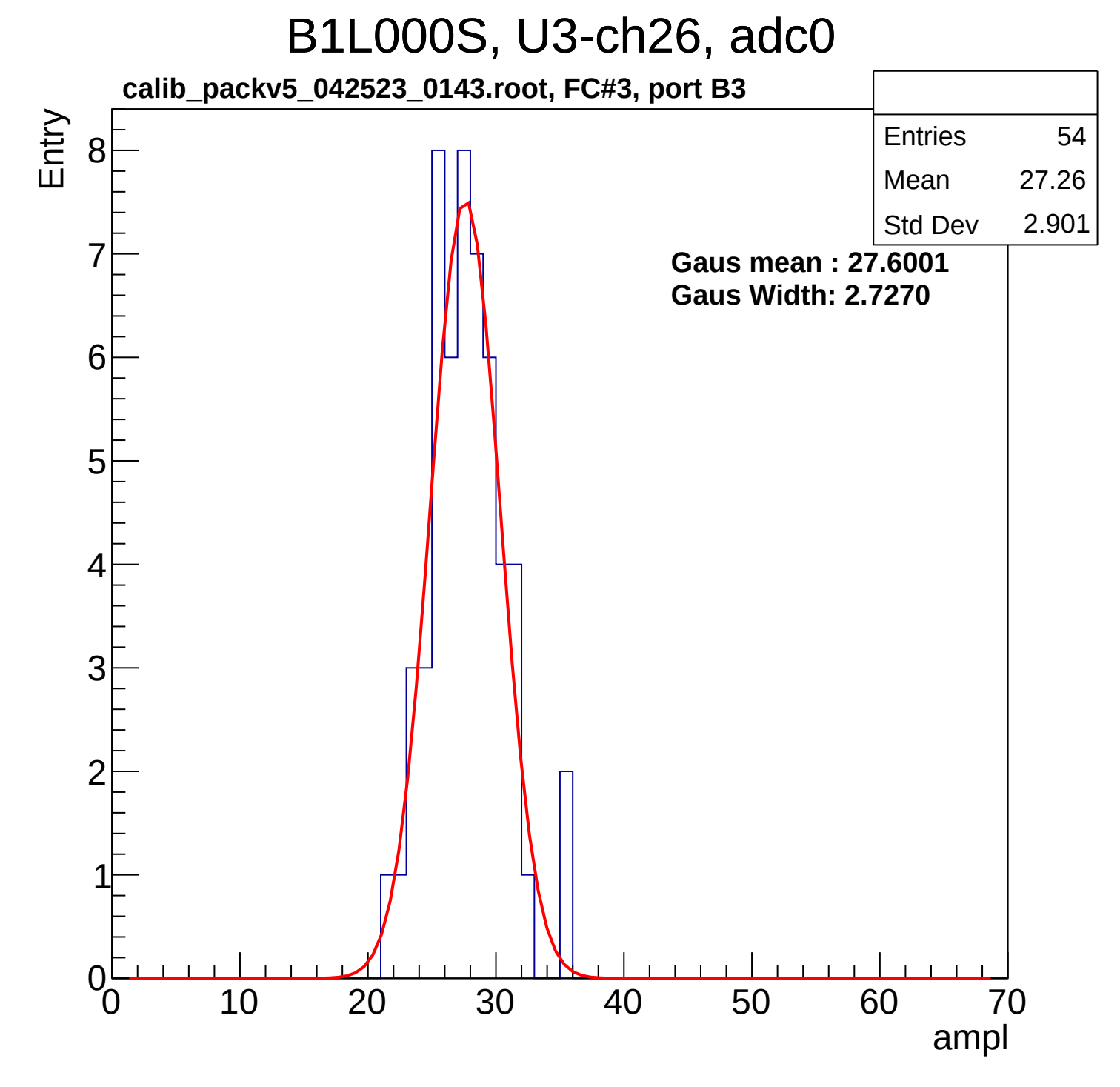
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch26, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 27.26
Std Dev 2.901
Gaus mean : 27.6001
Gaus Width: 2.7270
Entry
ampl
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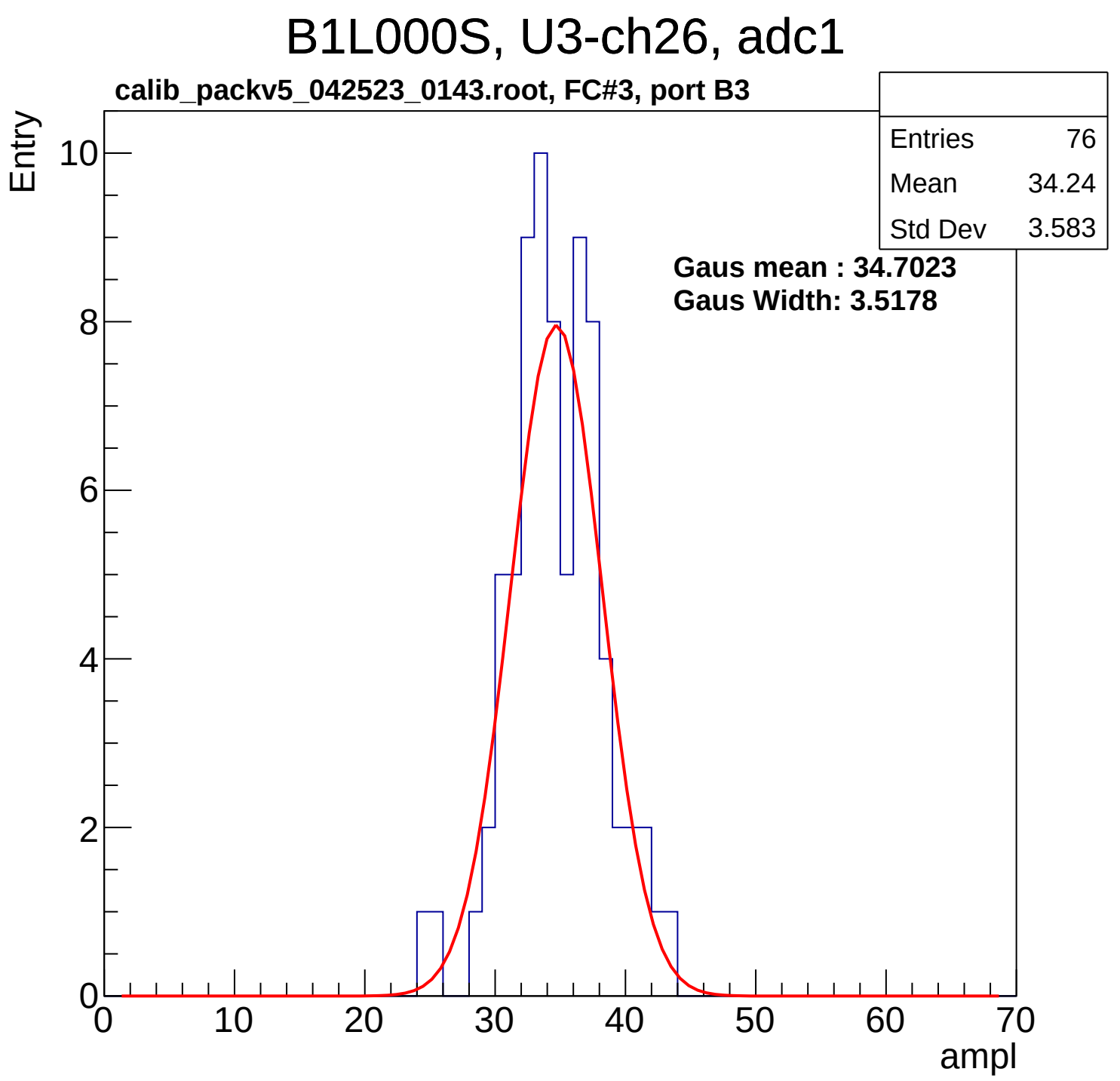
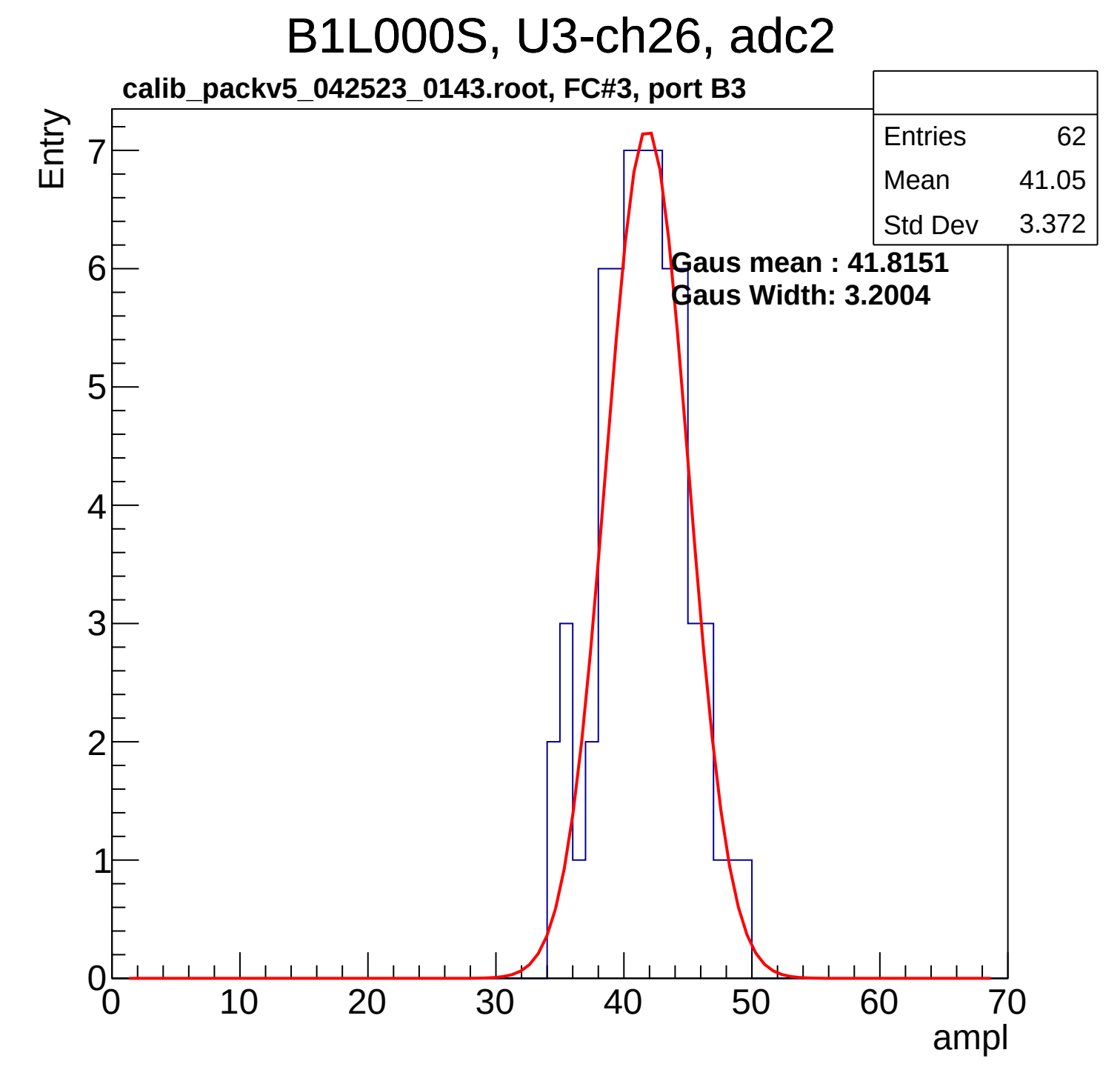
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch26, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 34.24
Std Dev 3.583
Gaus mean : 34.7023
Gaus Width: 3.5178
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch26, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 41.05
Std Dev 3.372
Gaus mean : 41.8151
Gaus Width: 3.2004
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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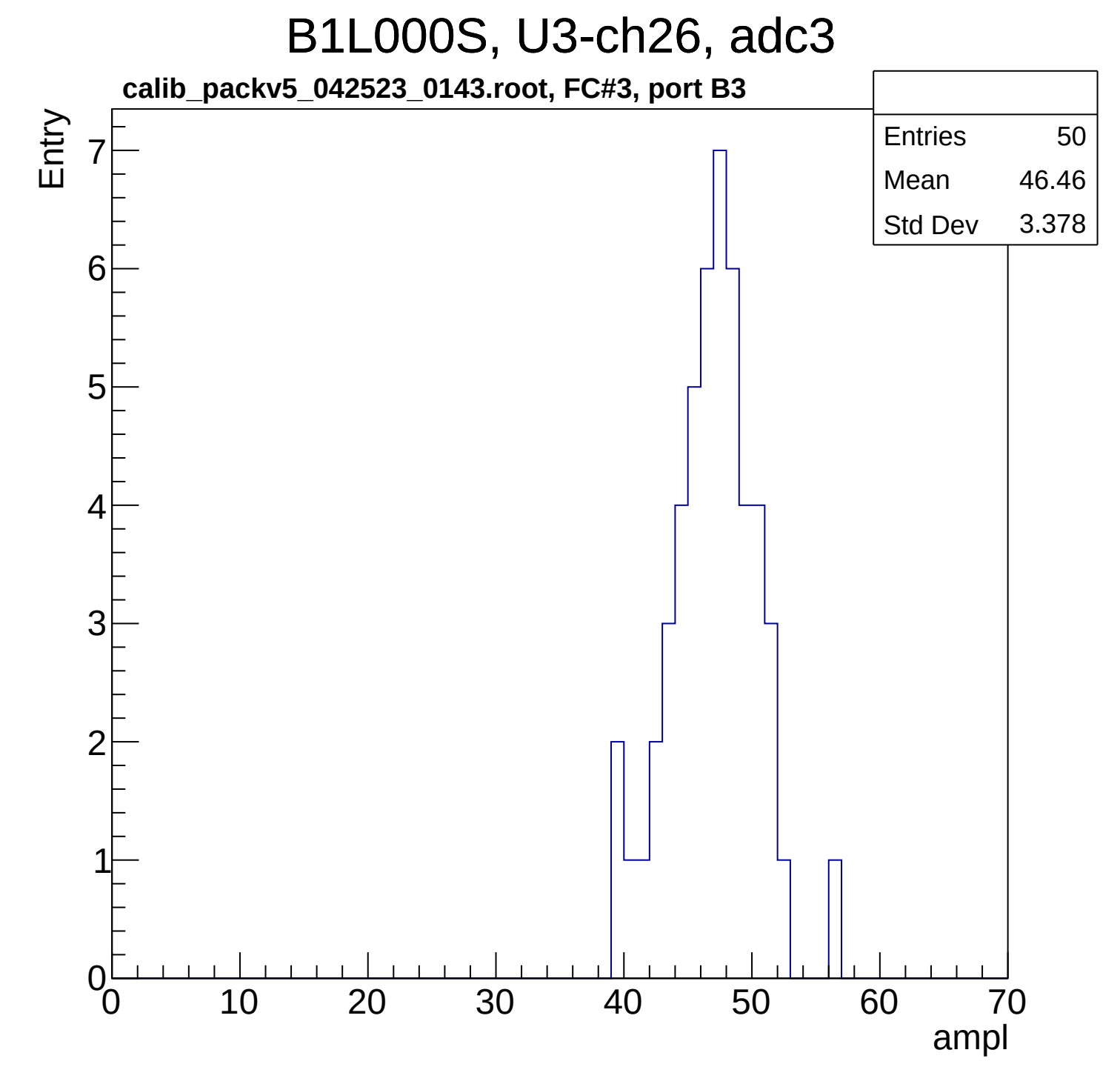
B1L000S, U3-ch26, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 46.46
Std Dev 3.378
Entry
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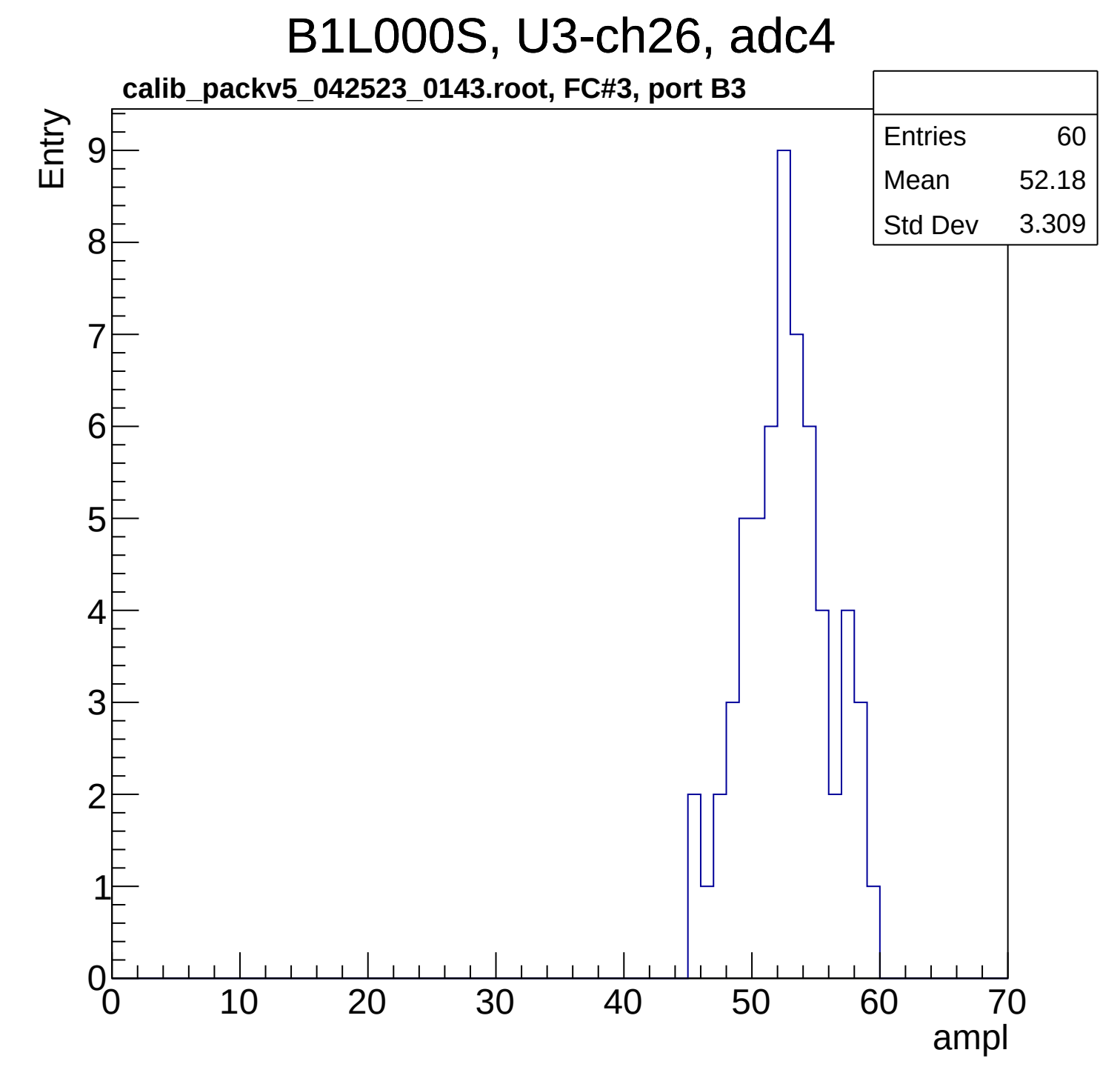
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch26, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 52.18
Std Dev 3.309
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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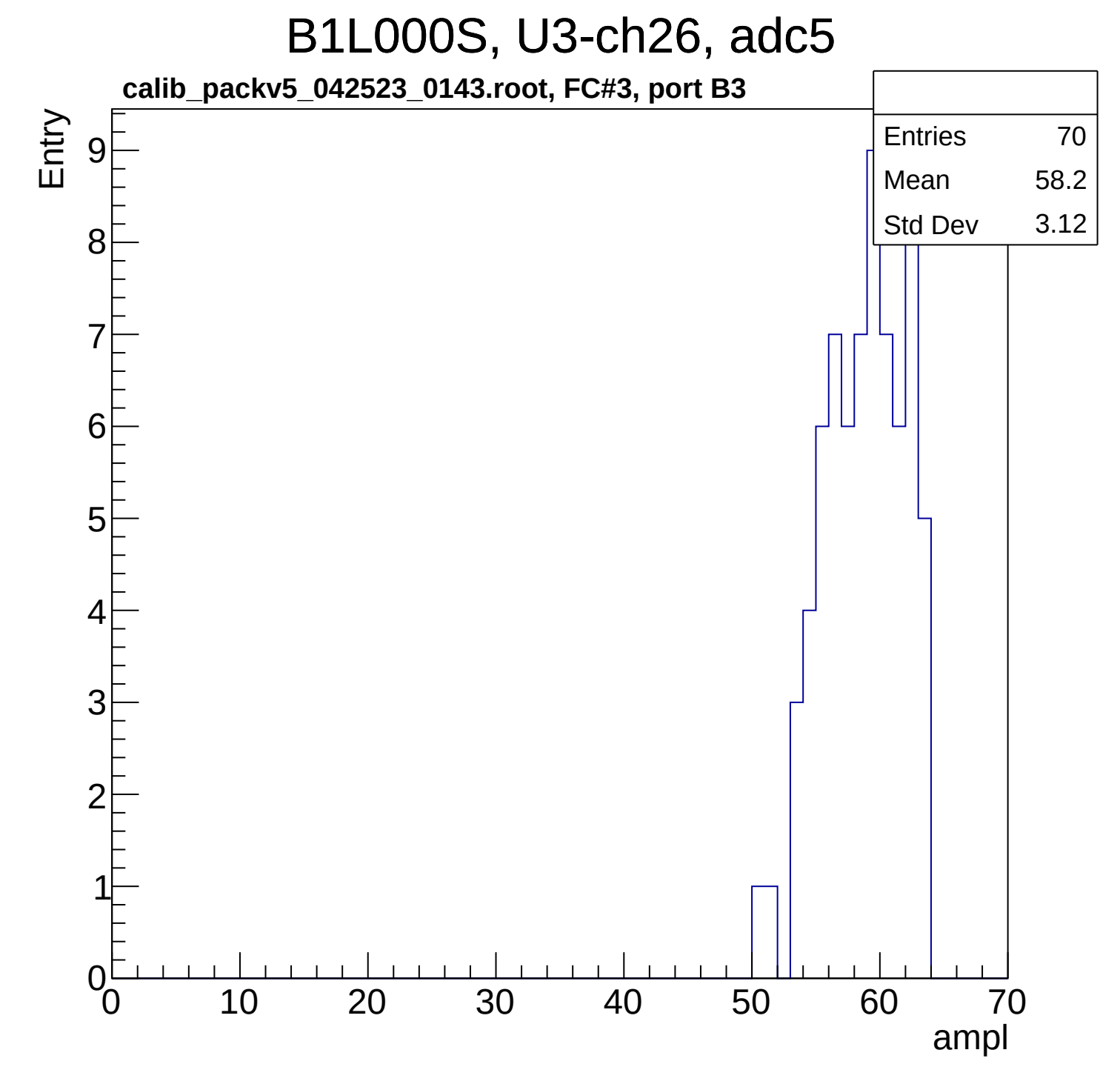
60
70


B1L000S, U3-ch26, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 58.2
Std Dev 3.12
Entry
ampl
0
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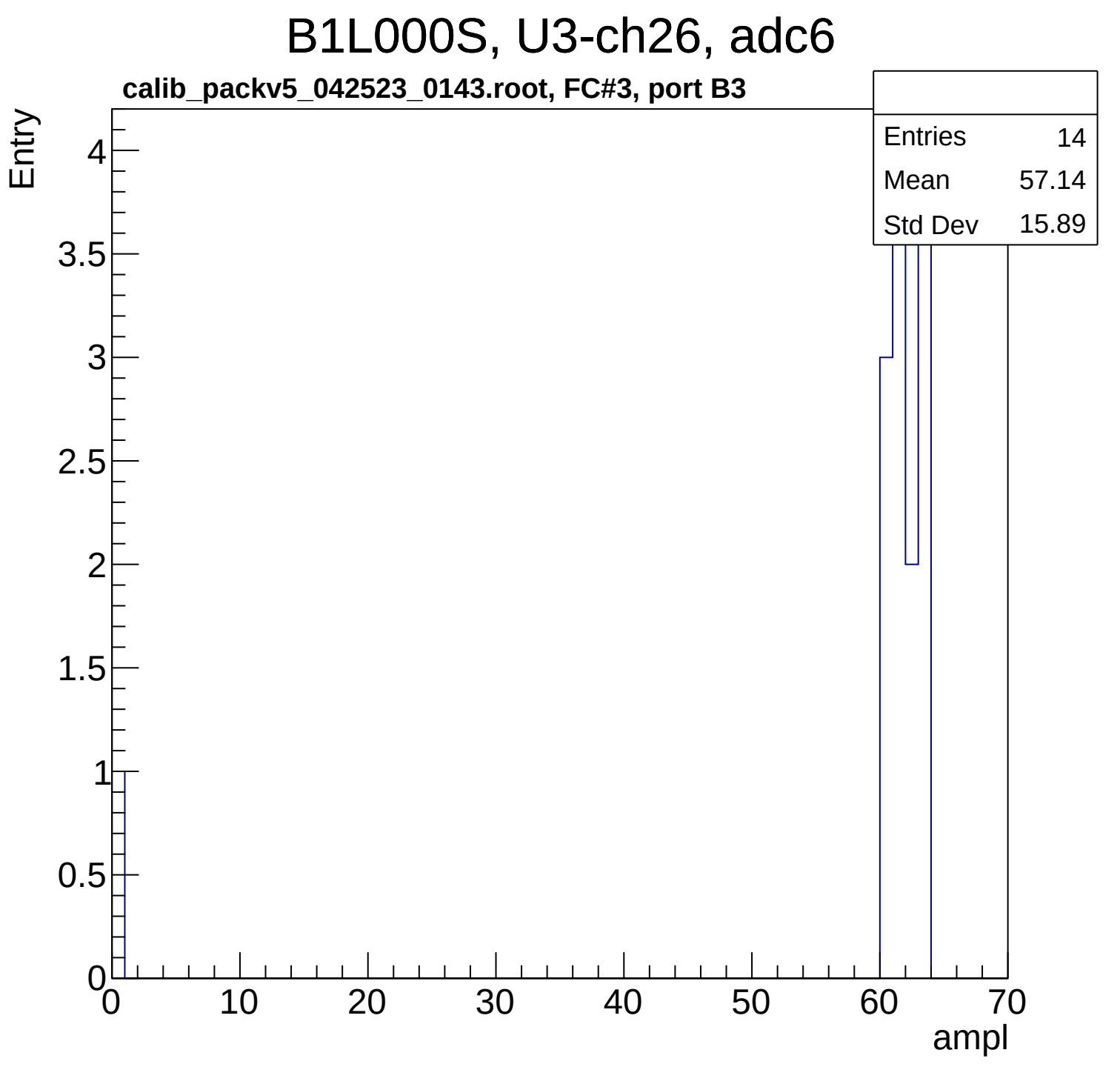
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch26, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.14
Std Dev 15.89
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
30
40
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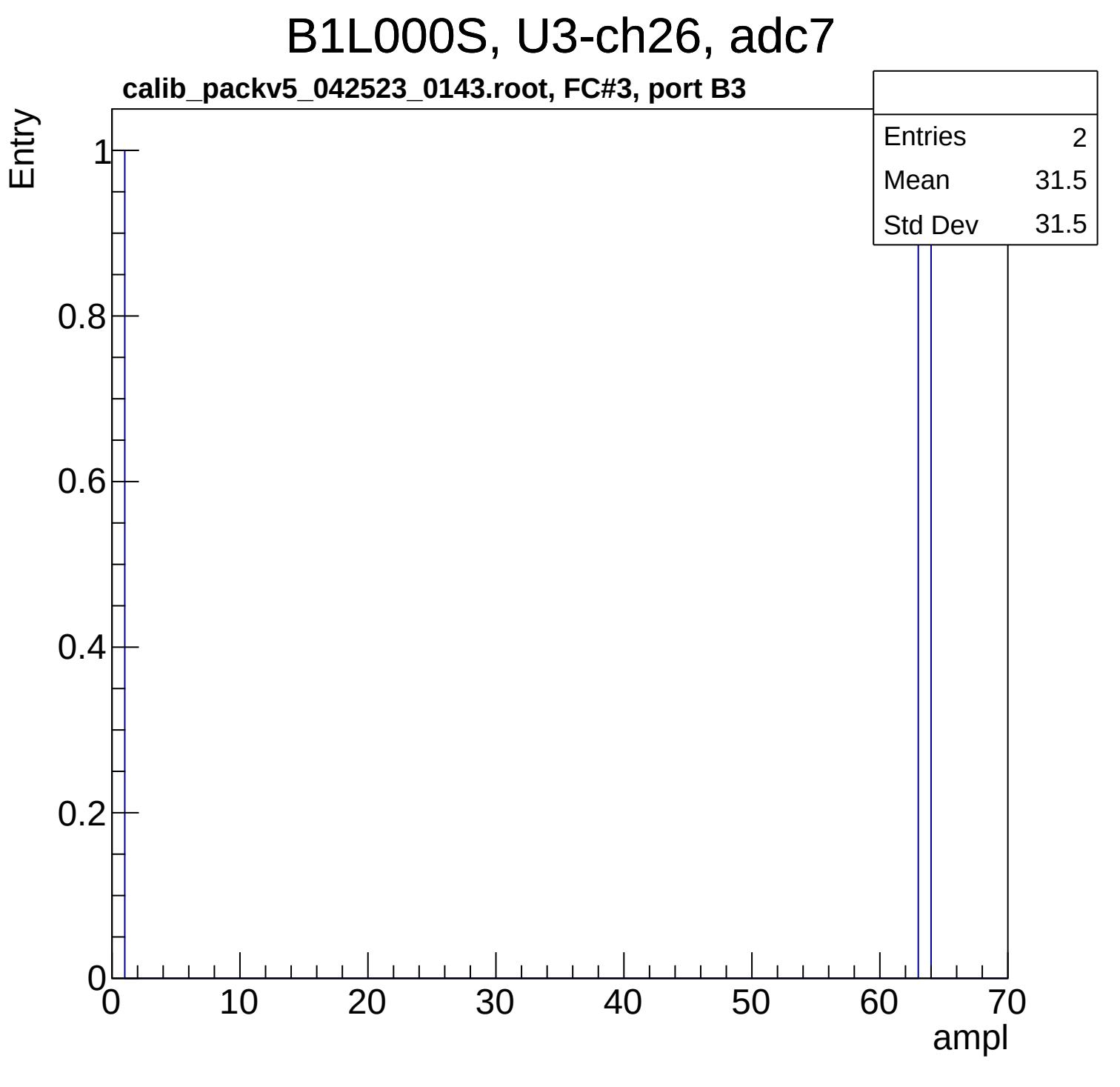
60
70


B1L000S, U3-ch26, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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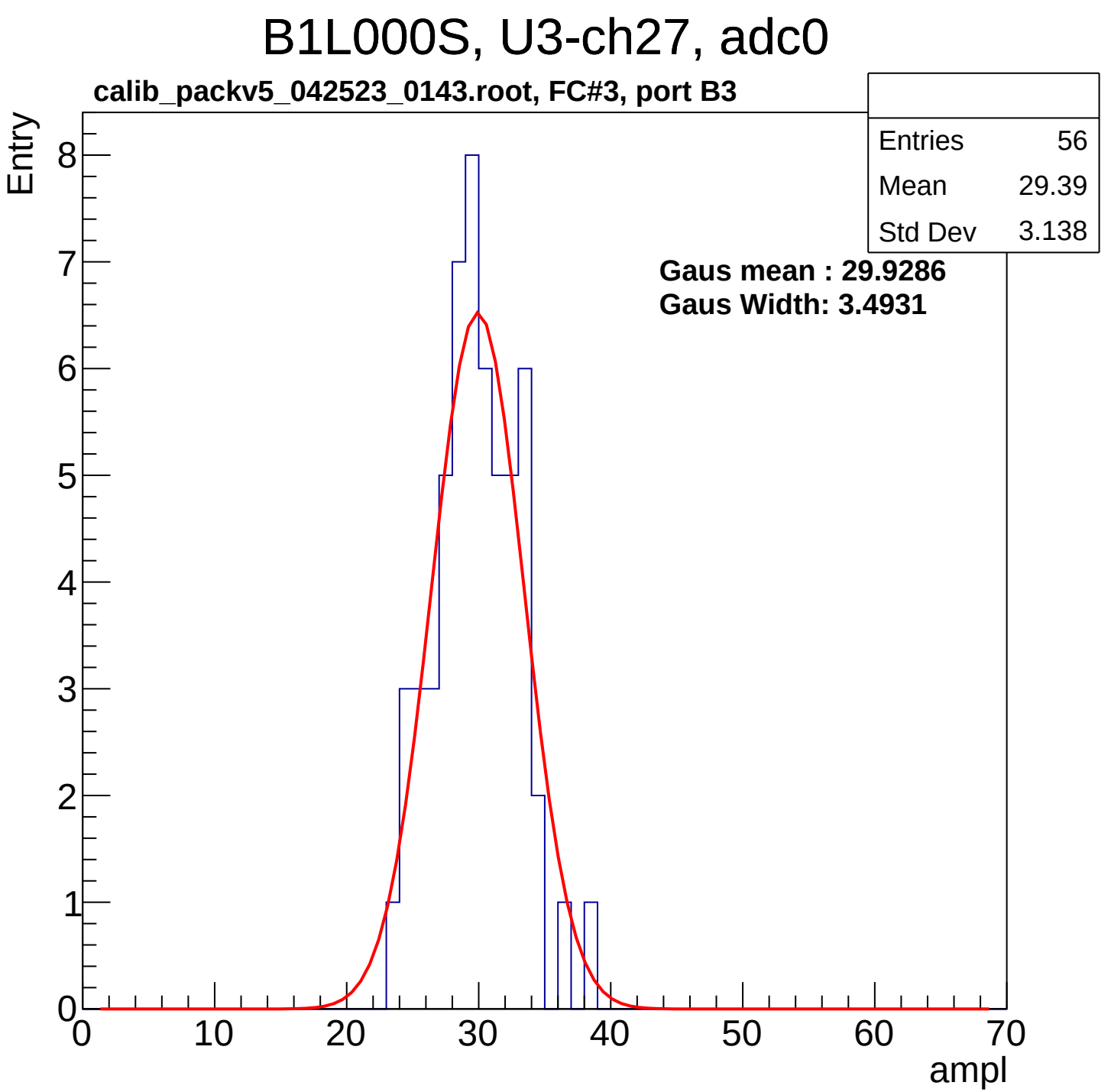
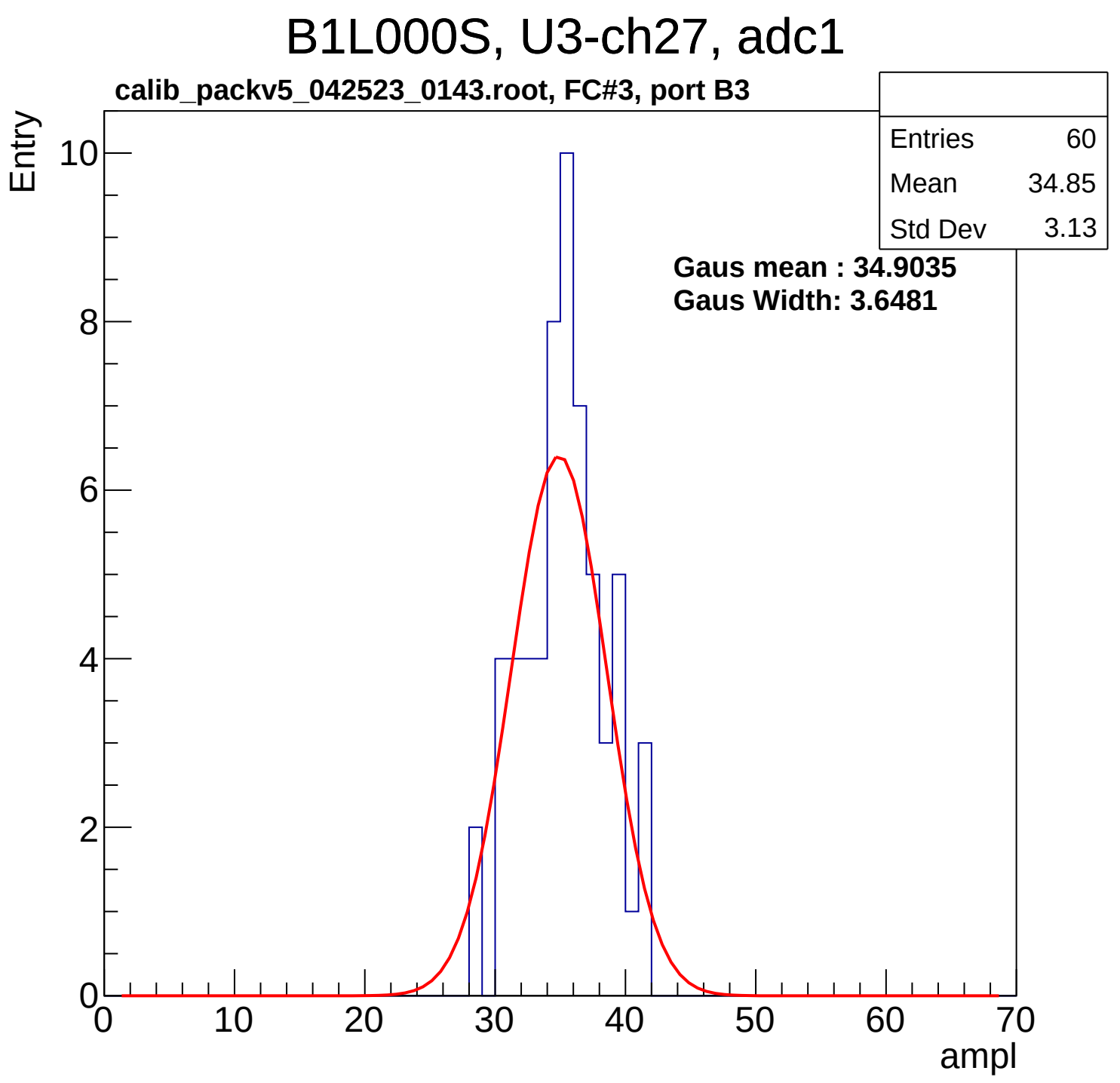
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch27, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 29.39
Std Dev 3.138
Gaus mean : 29.9286
Gaus Width: 3.4931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch27, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 34.85
Std Dev 3.13
Gaus mean : 34.9035
Gaus Width: 3.6481
Entry
ampl
0
2
4
6
8
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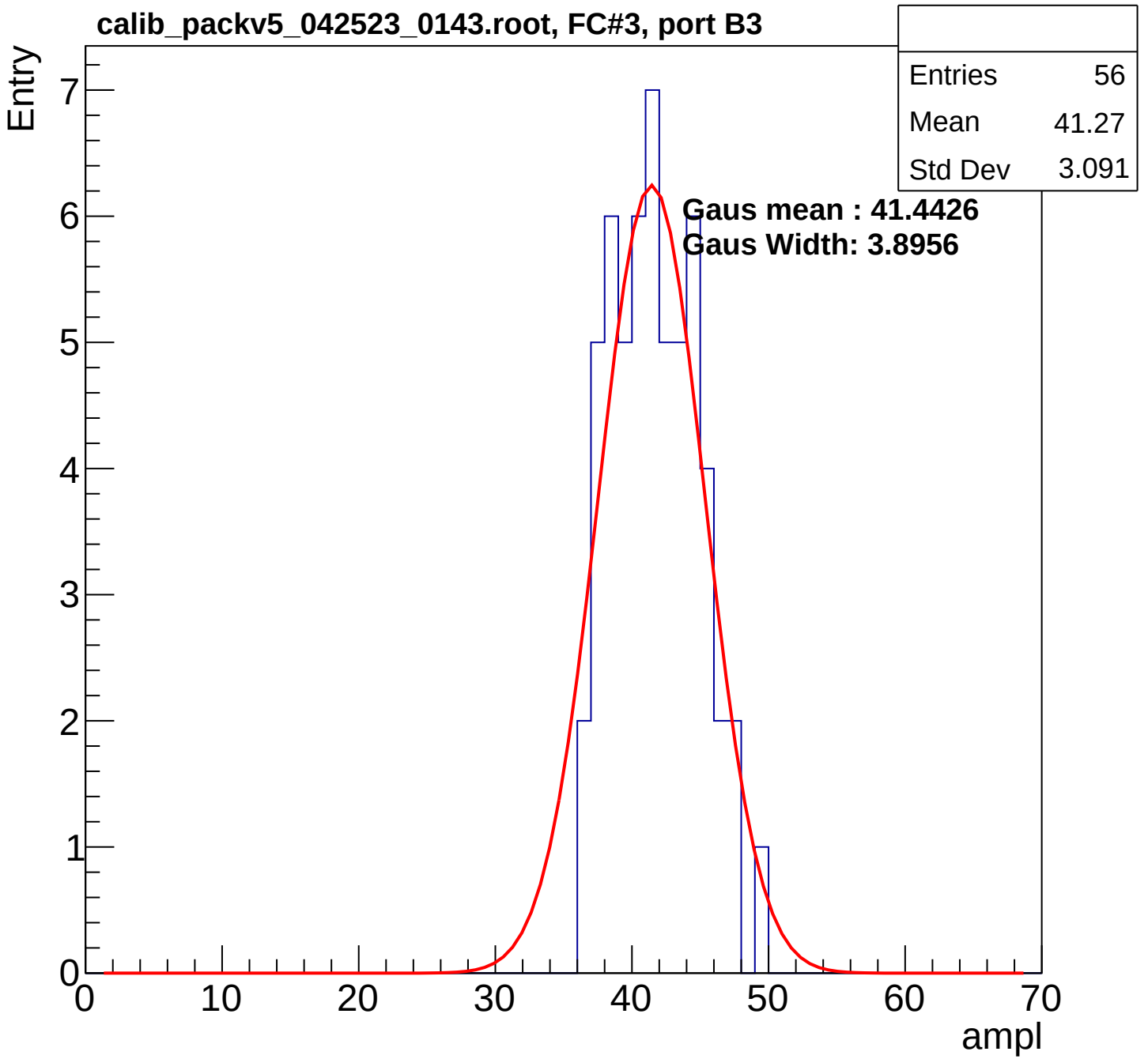
B1L000S, U3-ch27, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 41.27
Std Dev 3.091
Gaus mean : 41.4426
Gaus Width: 3.8956
Entry
ampl
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1
2
3
4
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6
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0
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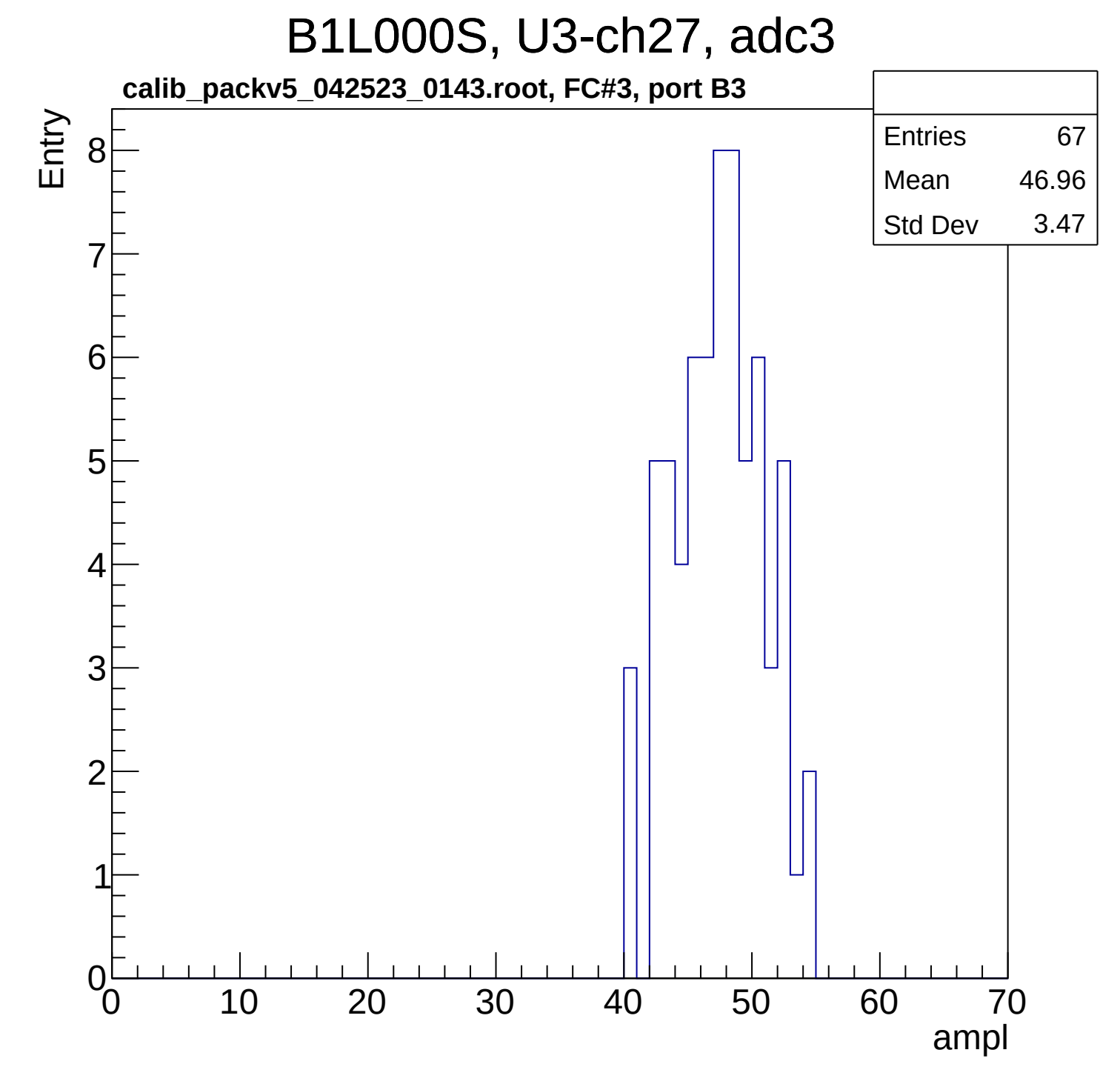
B1L000S, U3-ch27, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 46.96
Std Dev 3.47
Entry
ampl
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2
3
4
5
6
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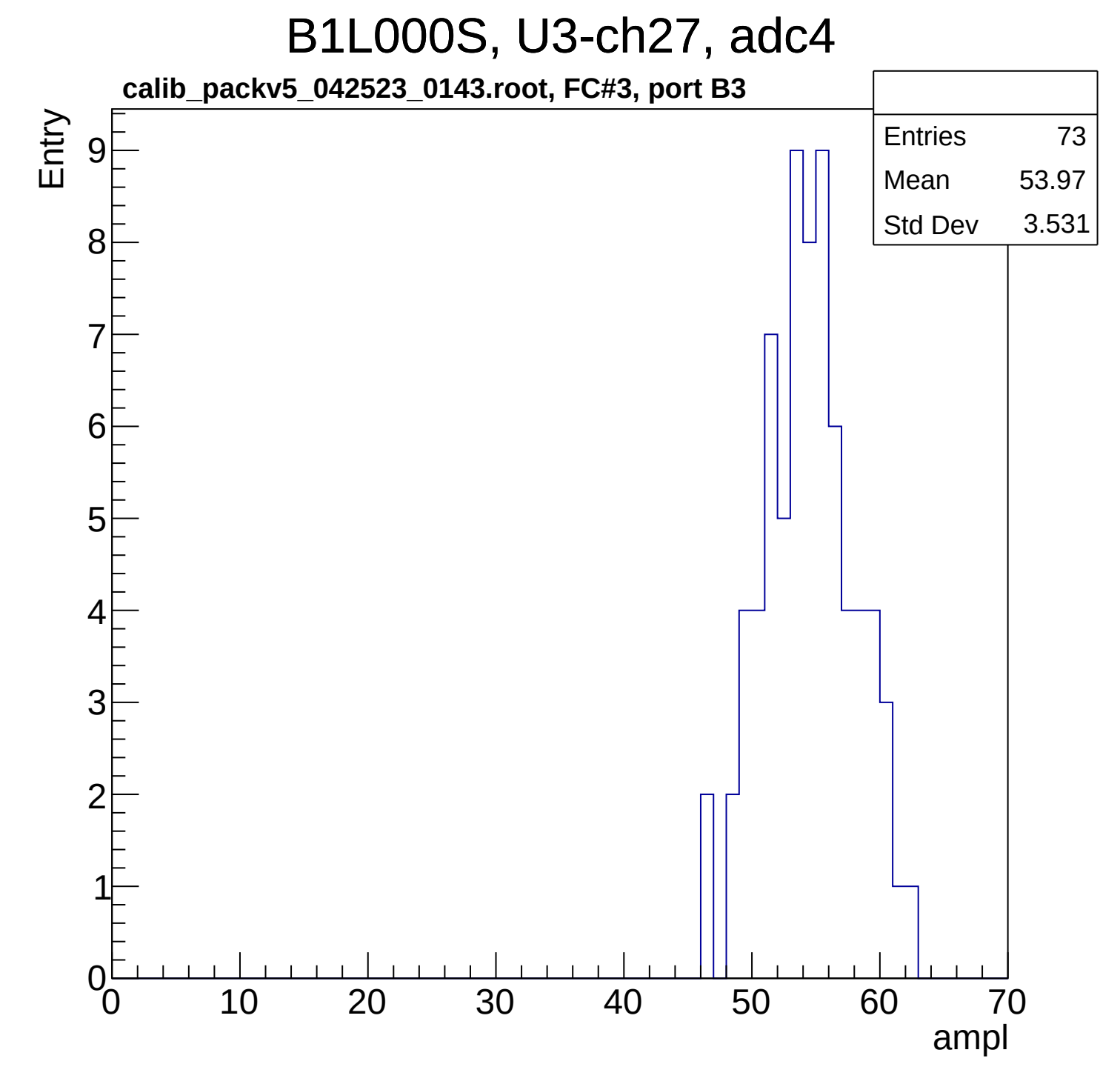
B1L000S, U3-ch27, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 53.97
Std Dev 3.531
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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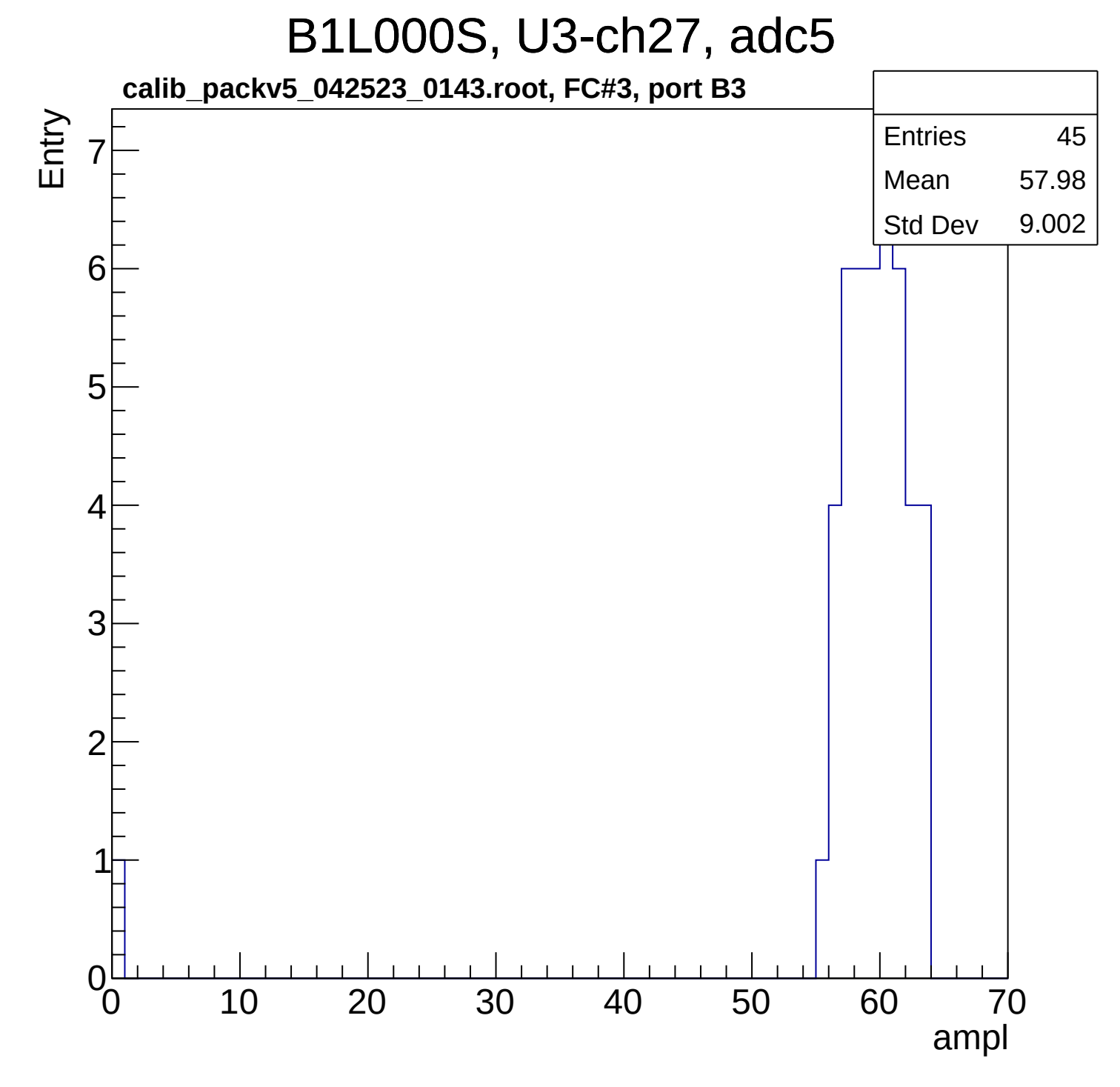
B1L000S, U3-ch27, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 57.98
Std Dev 9.002
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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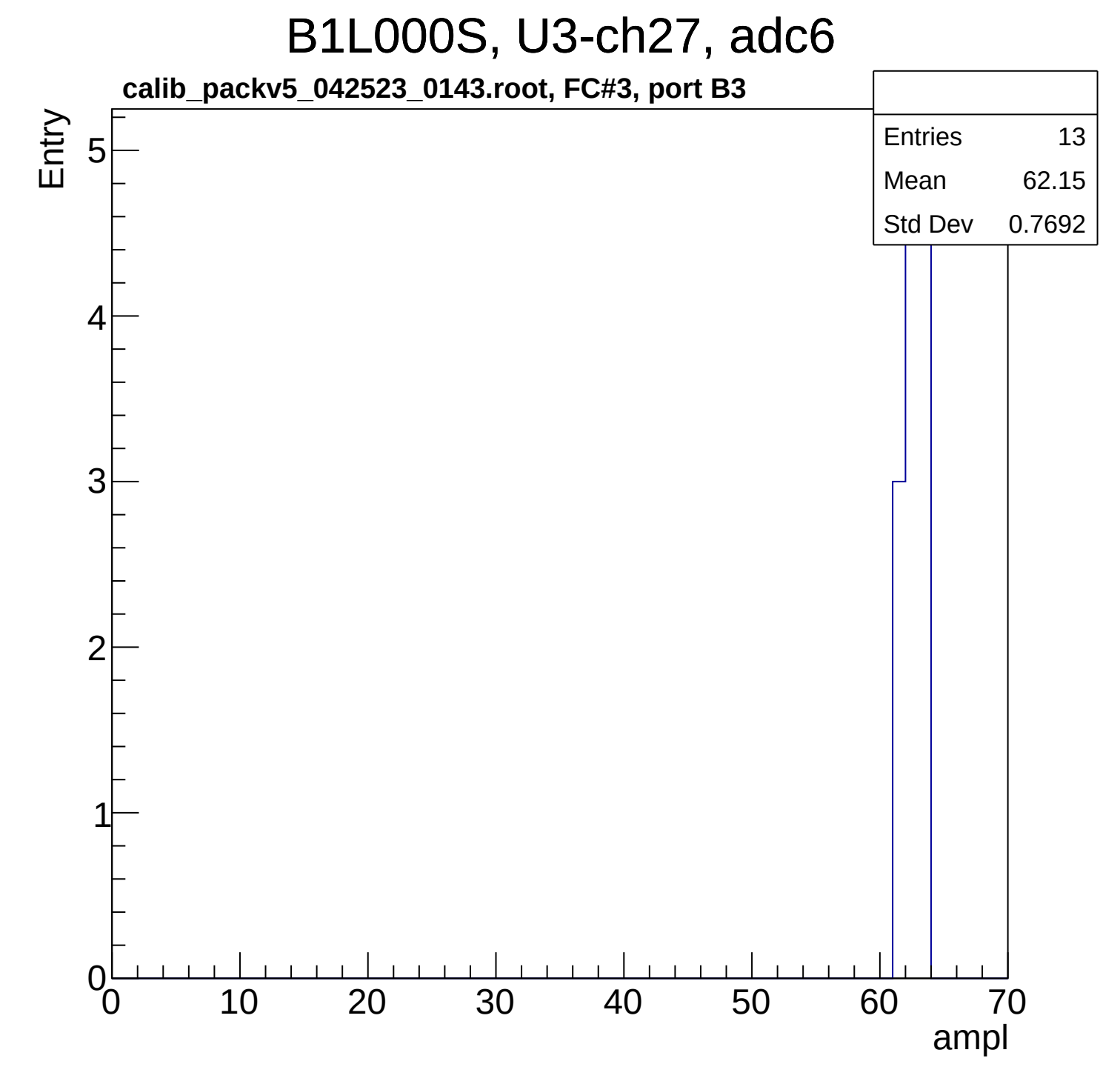
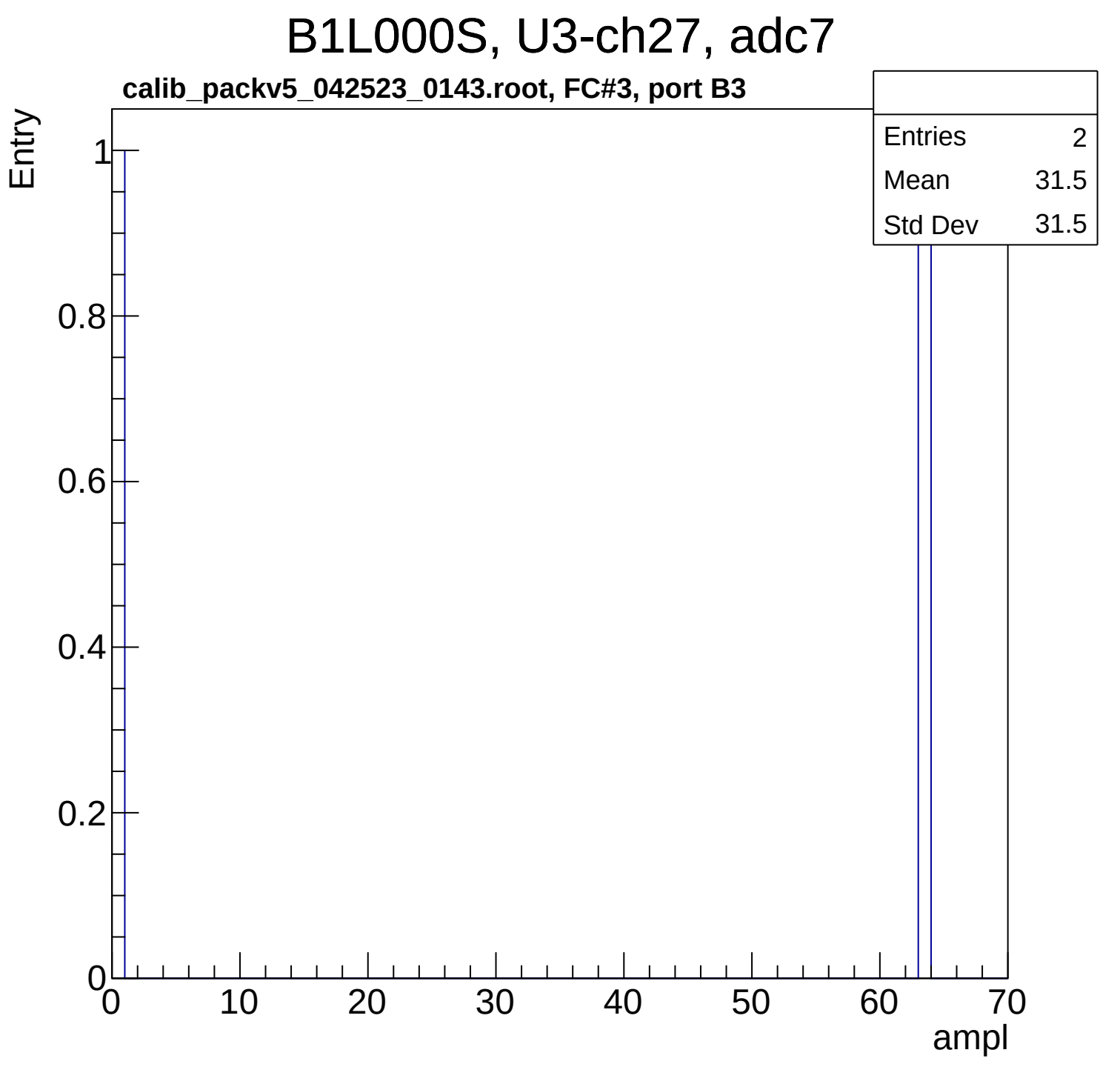
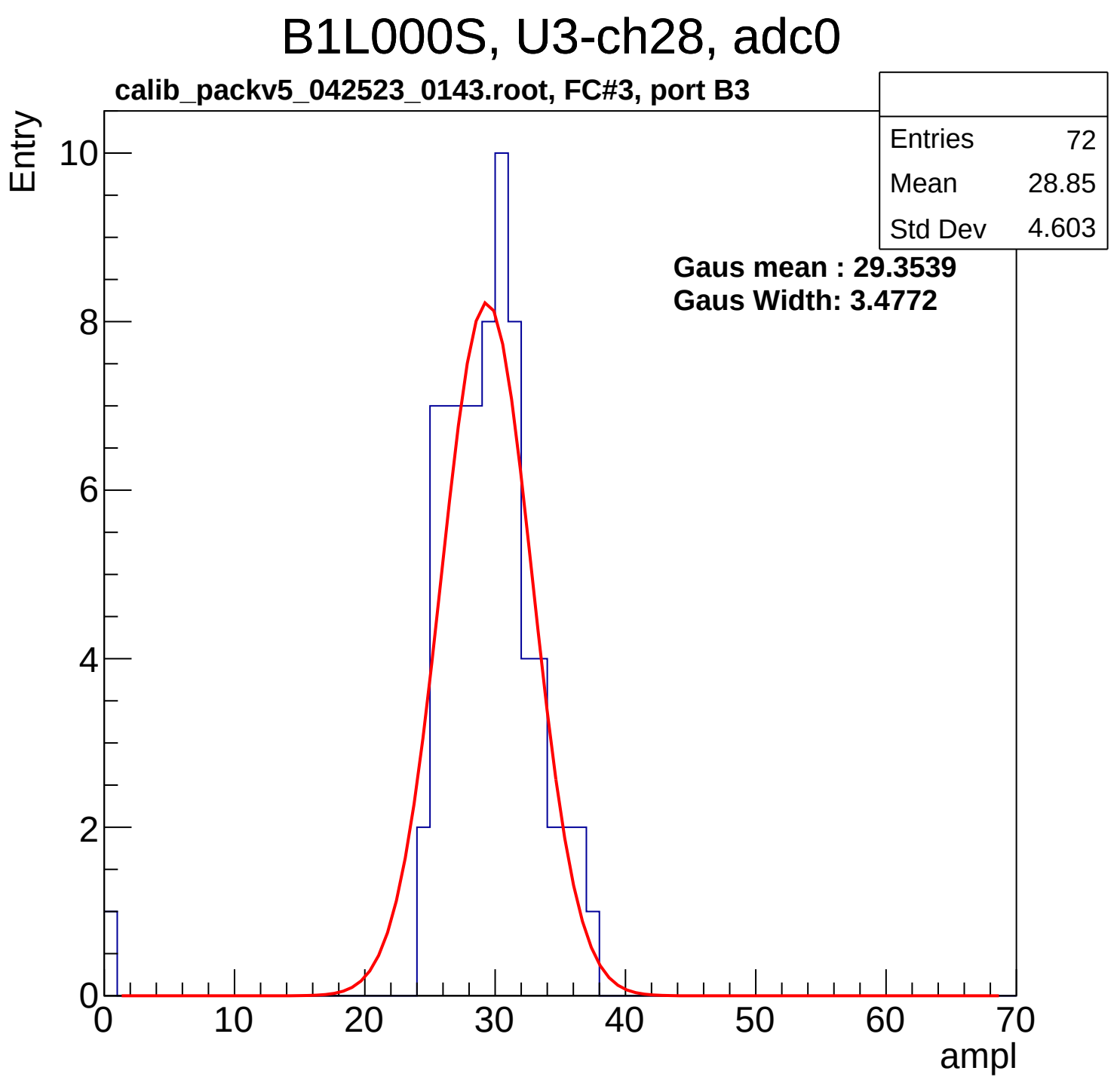

B1L000S, U3-ch27, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 62.15
Std Dev 0.7692
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch27, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch28, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 28.85
Std Dev 4.603
Gaus mean : 29.3539
Gaus Width: 3.4772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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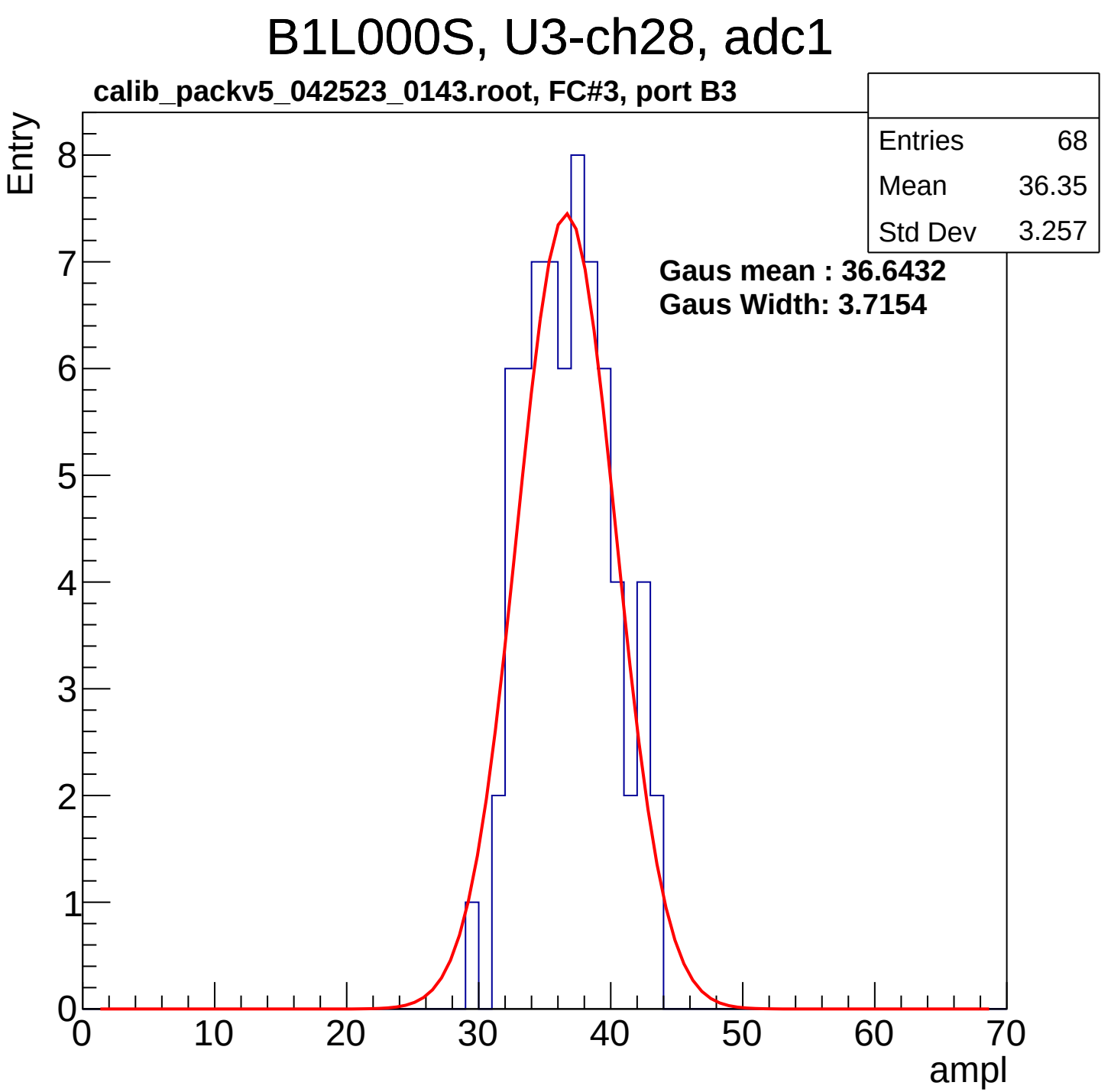
B1L000S, U3-ch28, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 36.35
Std Dev 3.257
Gaus mean : 36.6432
Gaus Width: 3.7154
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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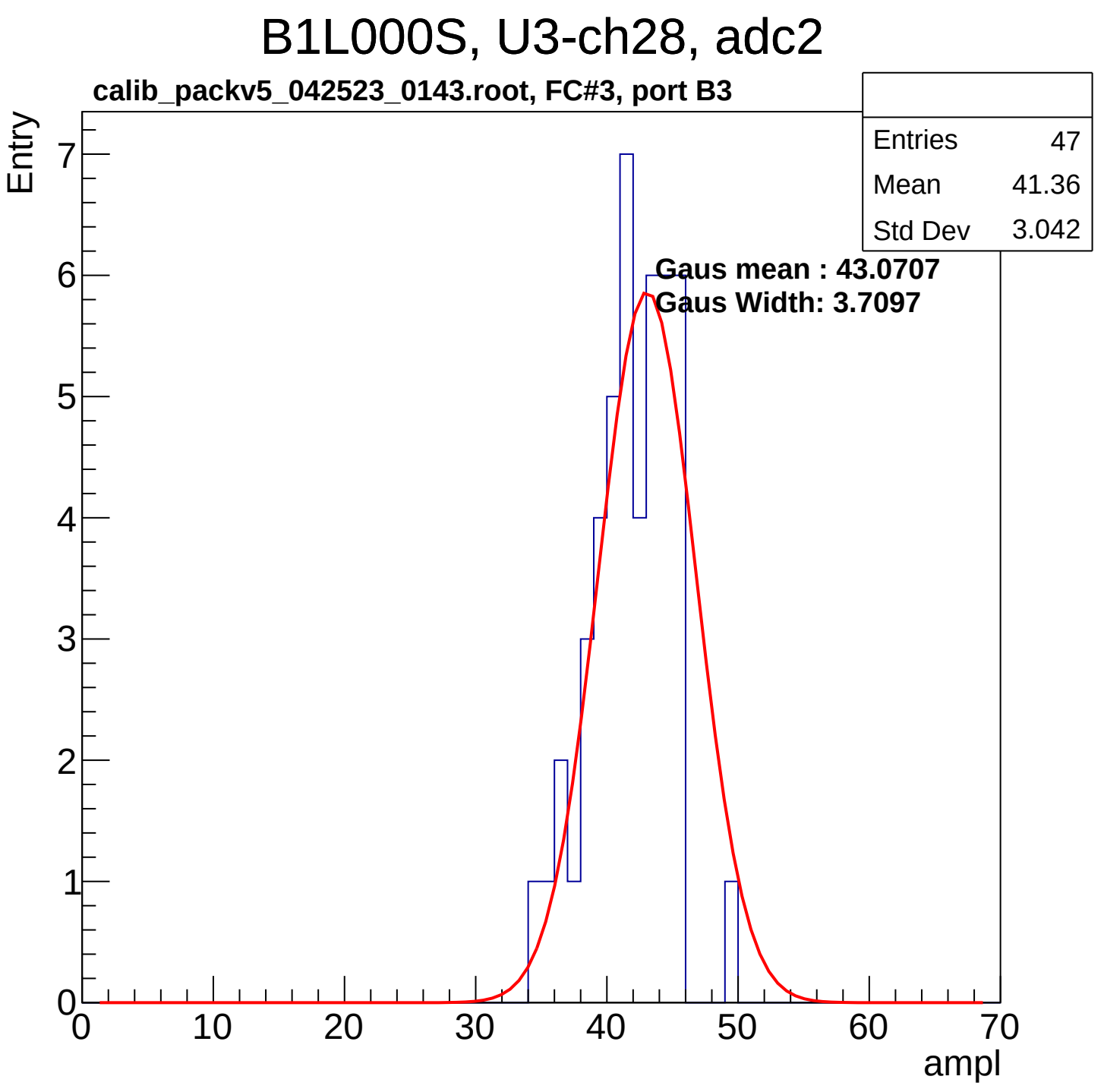
B1L000S, U3-ch28, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 41.36
Std Dev 3.042
Gaus mean : 43.0707
Gaus Width: 3.7097
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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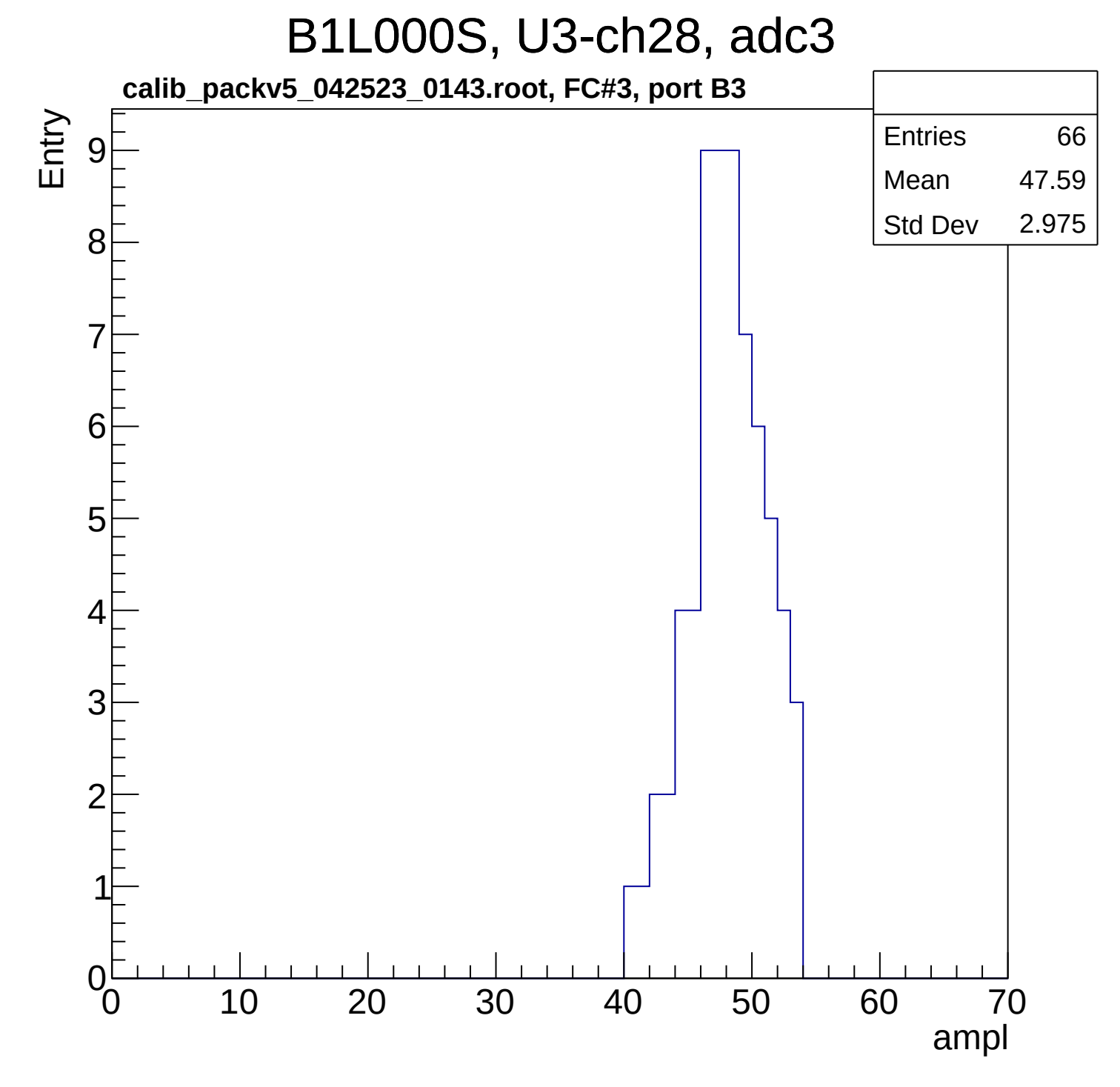
B1L000S, U3-ch28, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.59
Std Dev 2.975
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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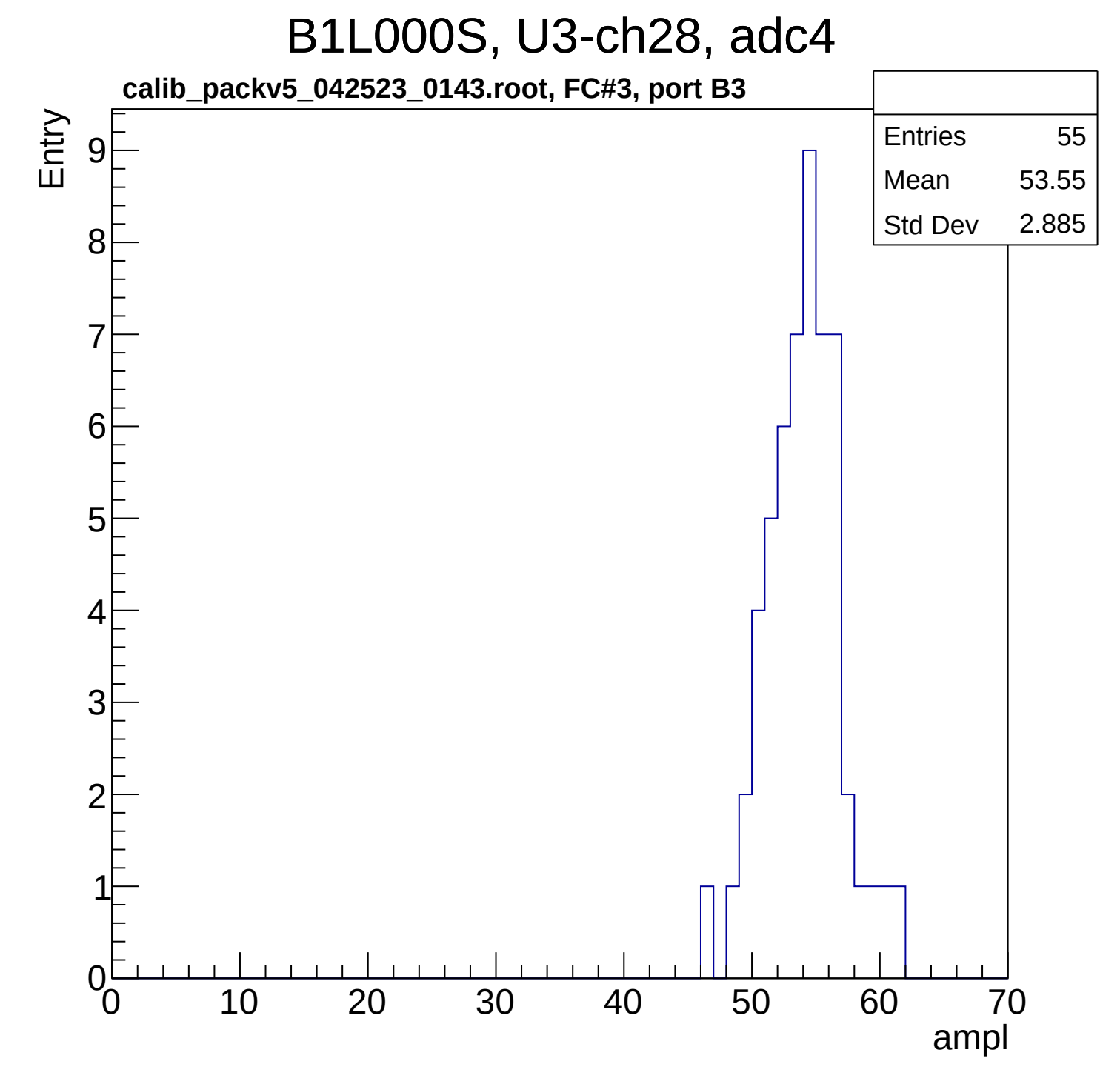
B1L000S, U3-ch28, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.55
Std Dev 2.885
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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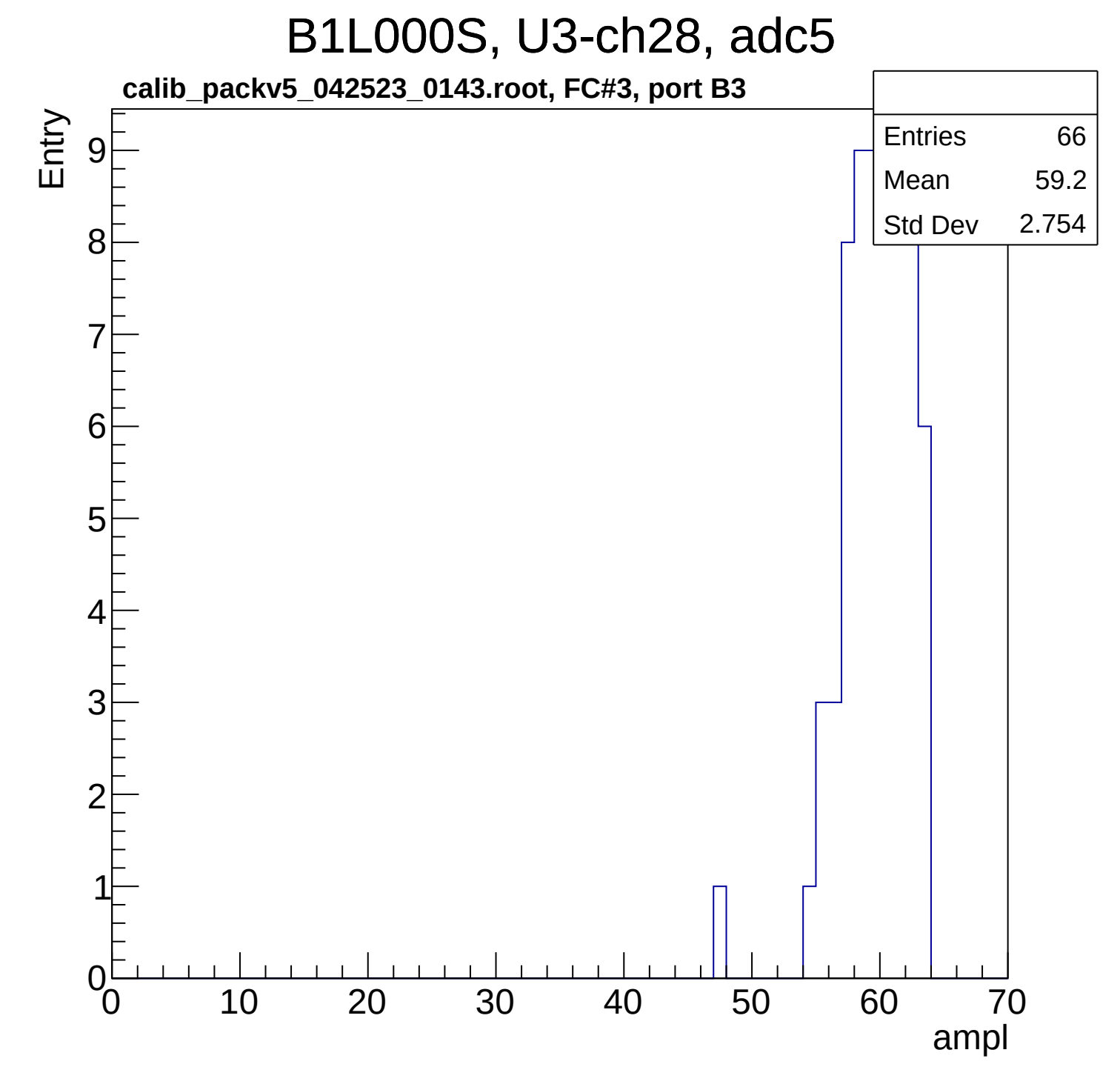
B1L000S, U3-ch28, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 59.2
Std Dev 2.754
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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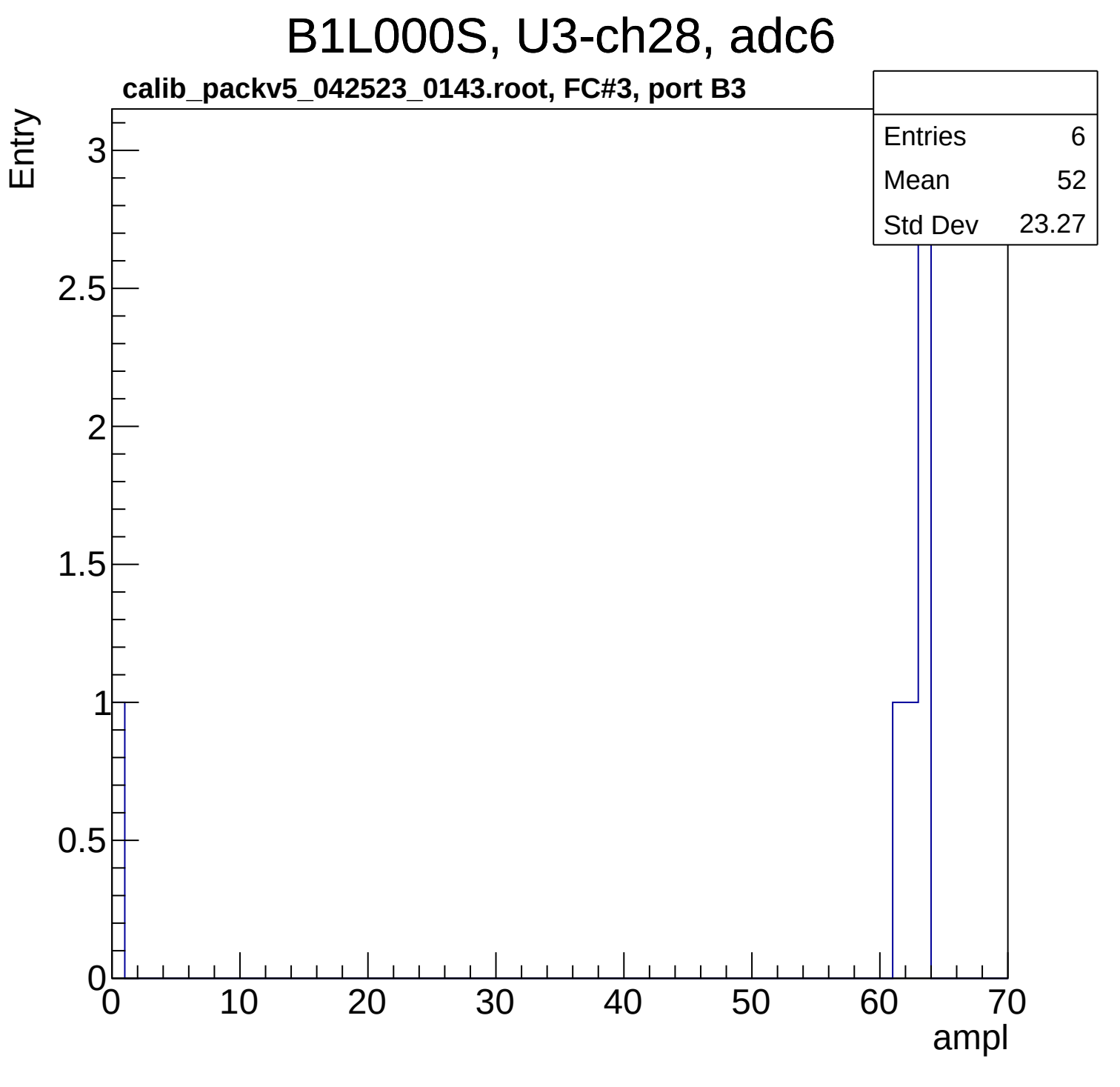
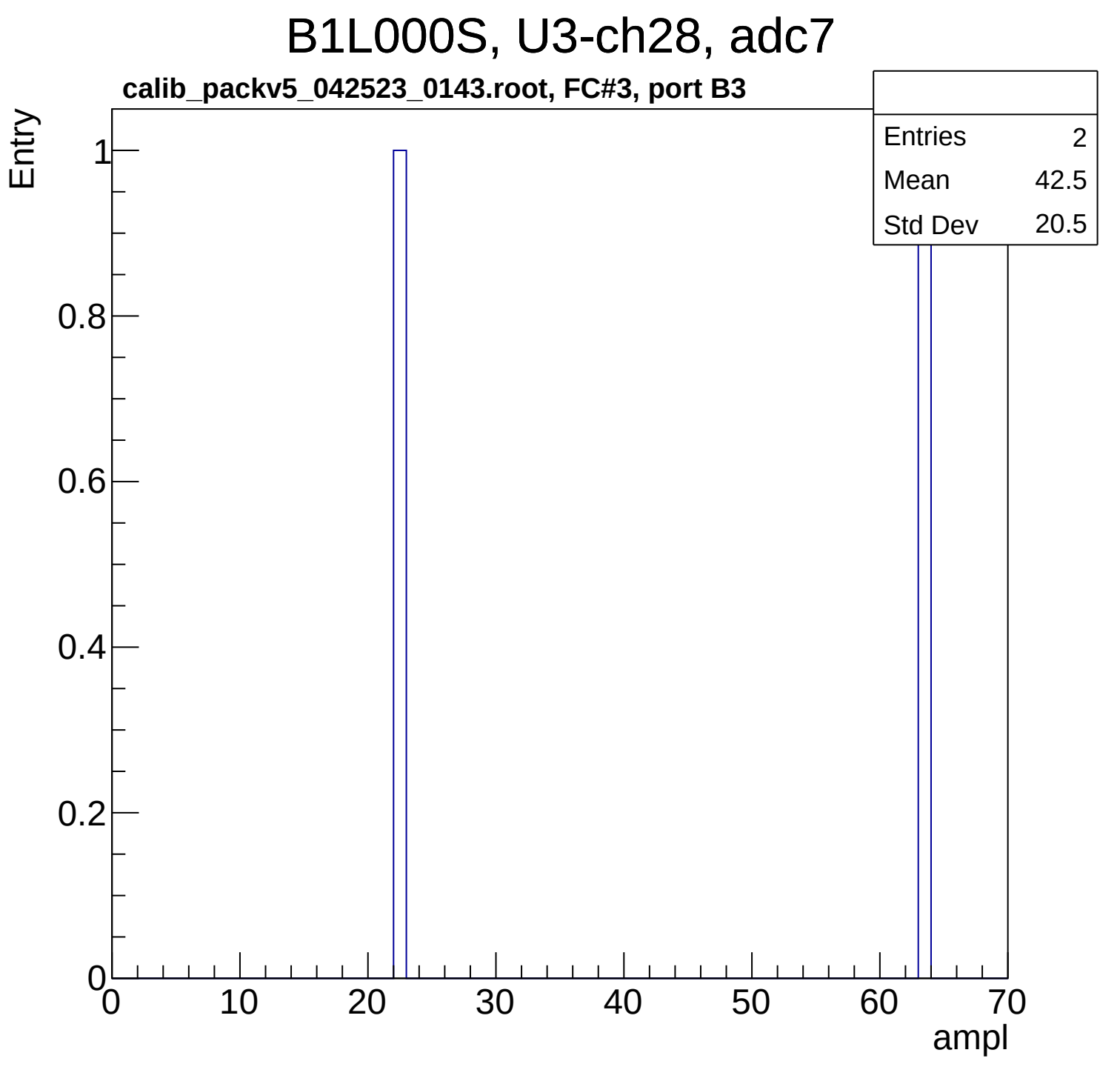

B1L000S, U3-ch28, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch28, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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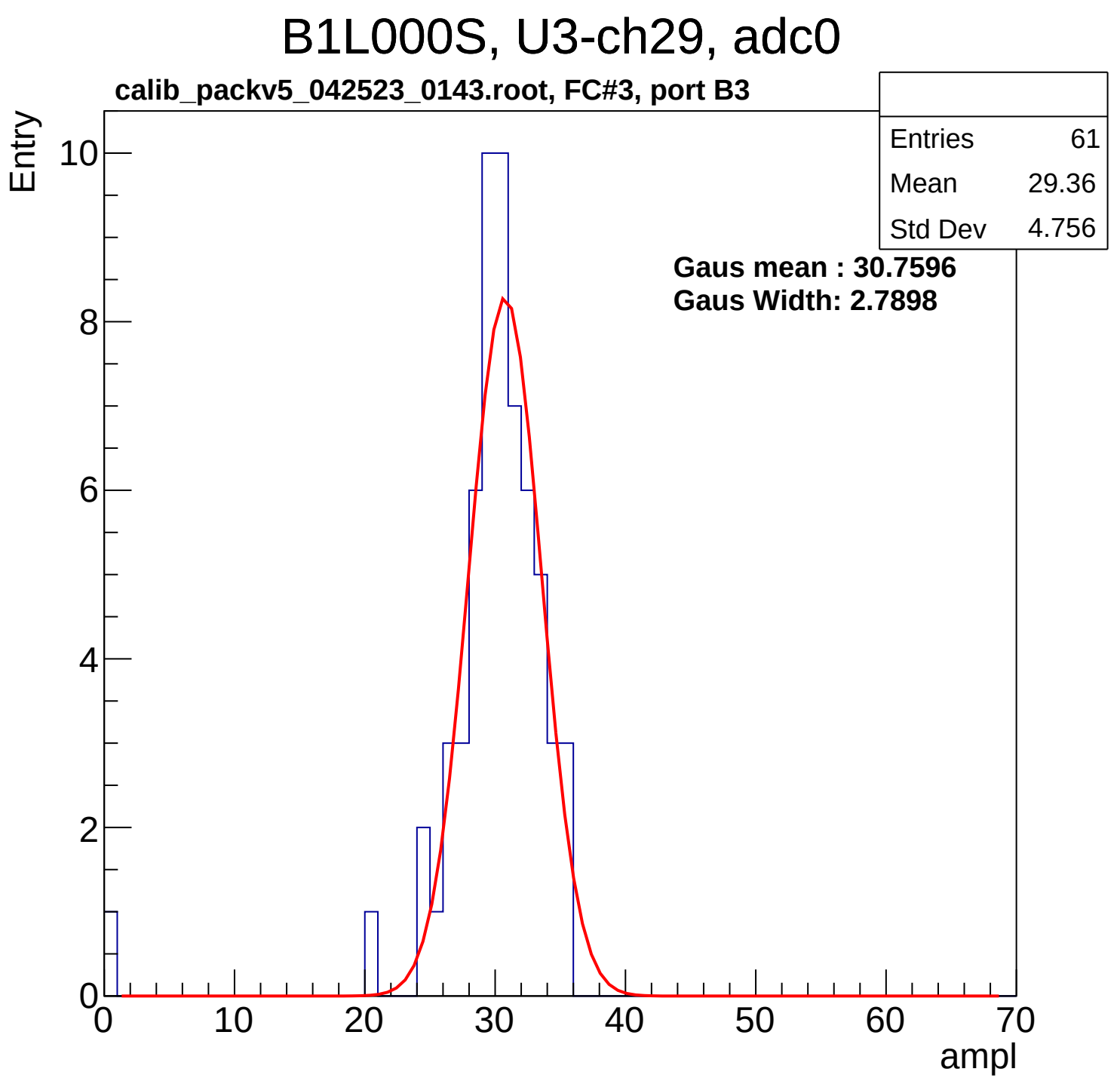
B1L000S, U3-ch29, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 29.36
Std Dev 4.756
Gaus mean : 30.7596
Gaus Width: 2.7898
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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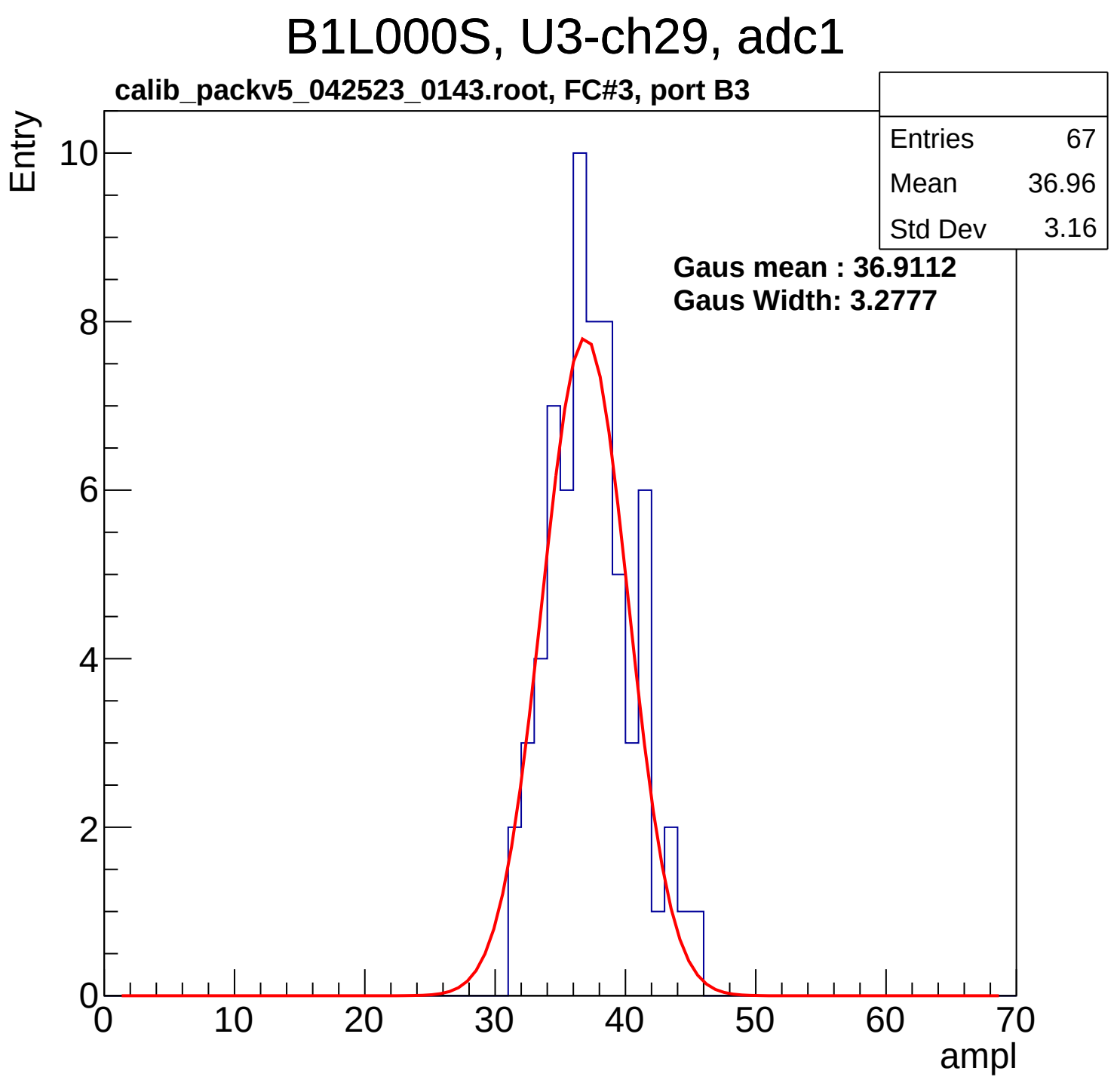
B1L000S, U3-ch29, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 36.96
Std Dev 3.16
Gaus mean : 36.9112
Gaus Width: 3.2777
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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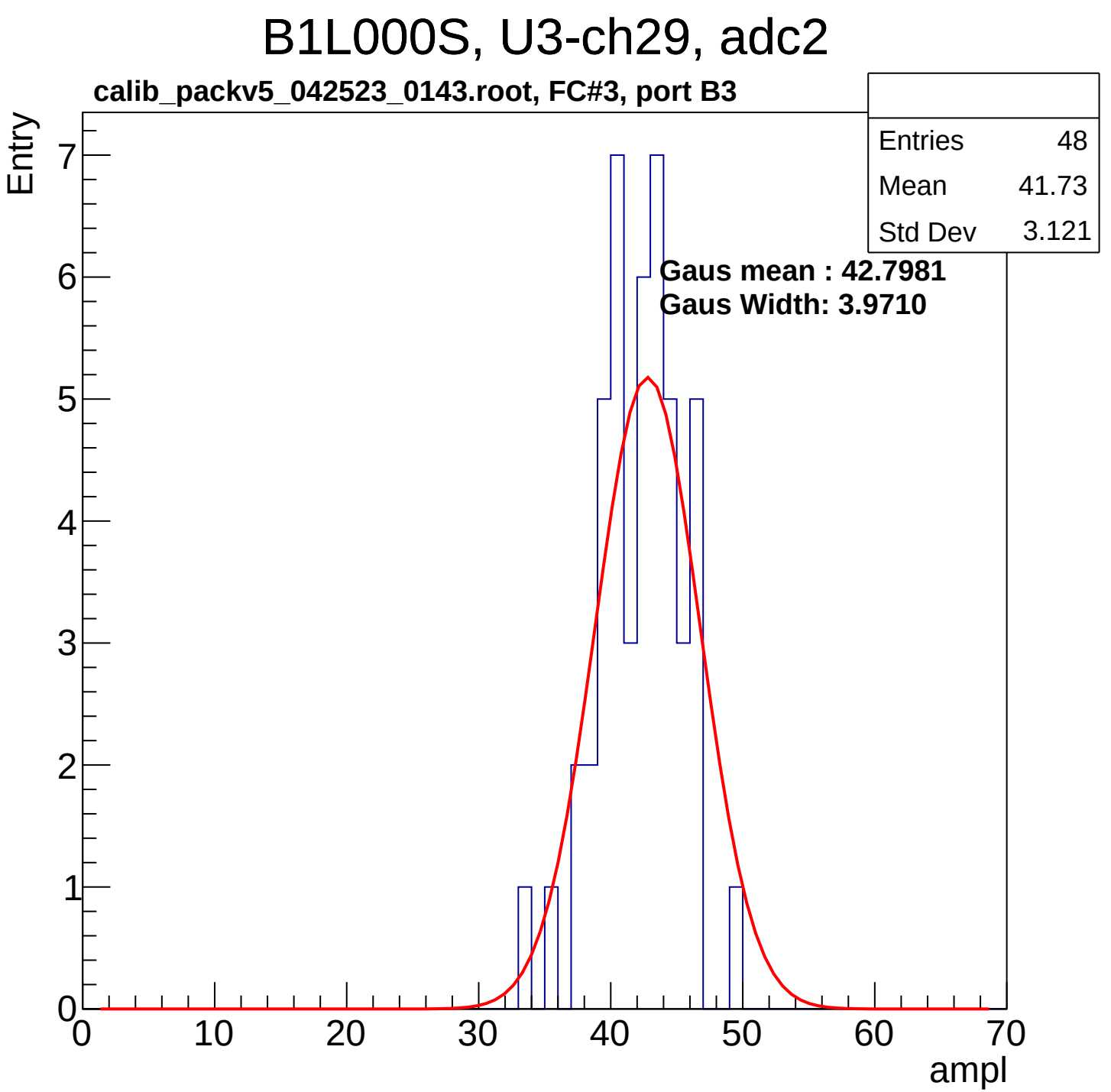
B1L000S, U3-ch29, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 41.73
Std Dev 3.121
Gaus mean : 42.7981
Gaus Width: 3.9710
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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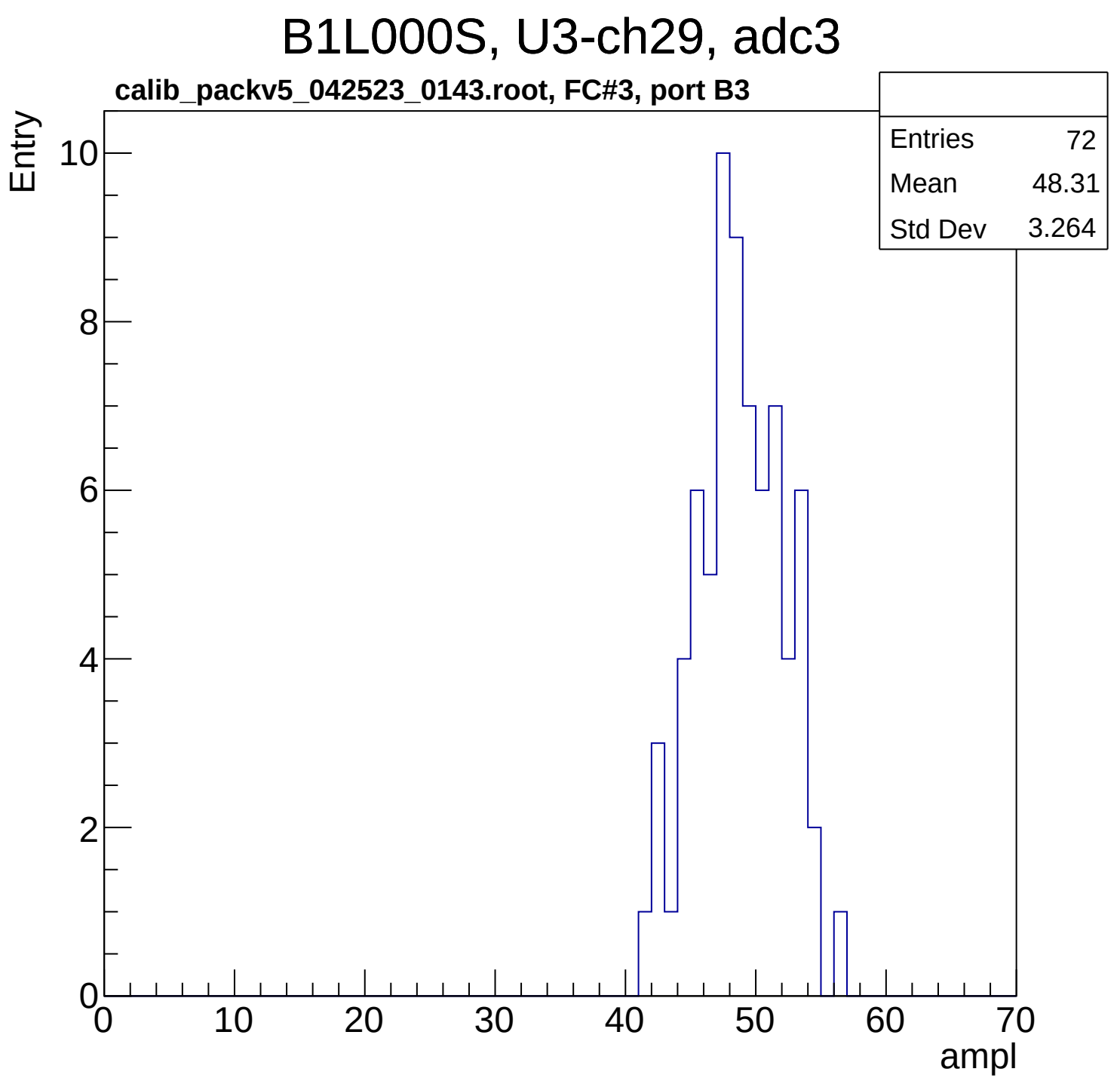
B1L000S, U3-ch29, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 48.31
Std Dev 3.264
Entry
ampl
0
2
4
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10
0
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20
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40
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60
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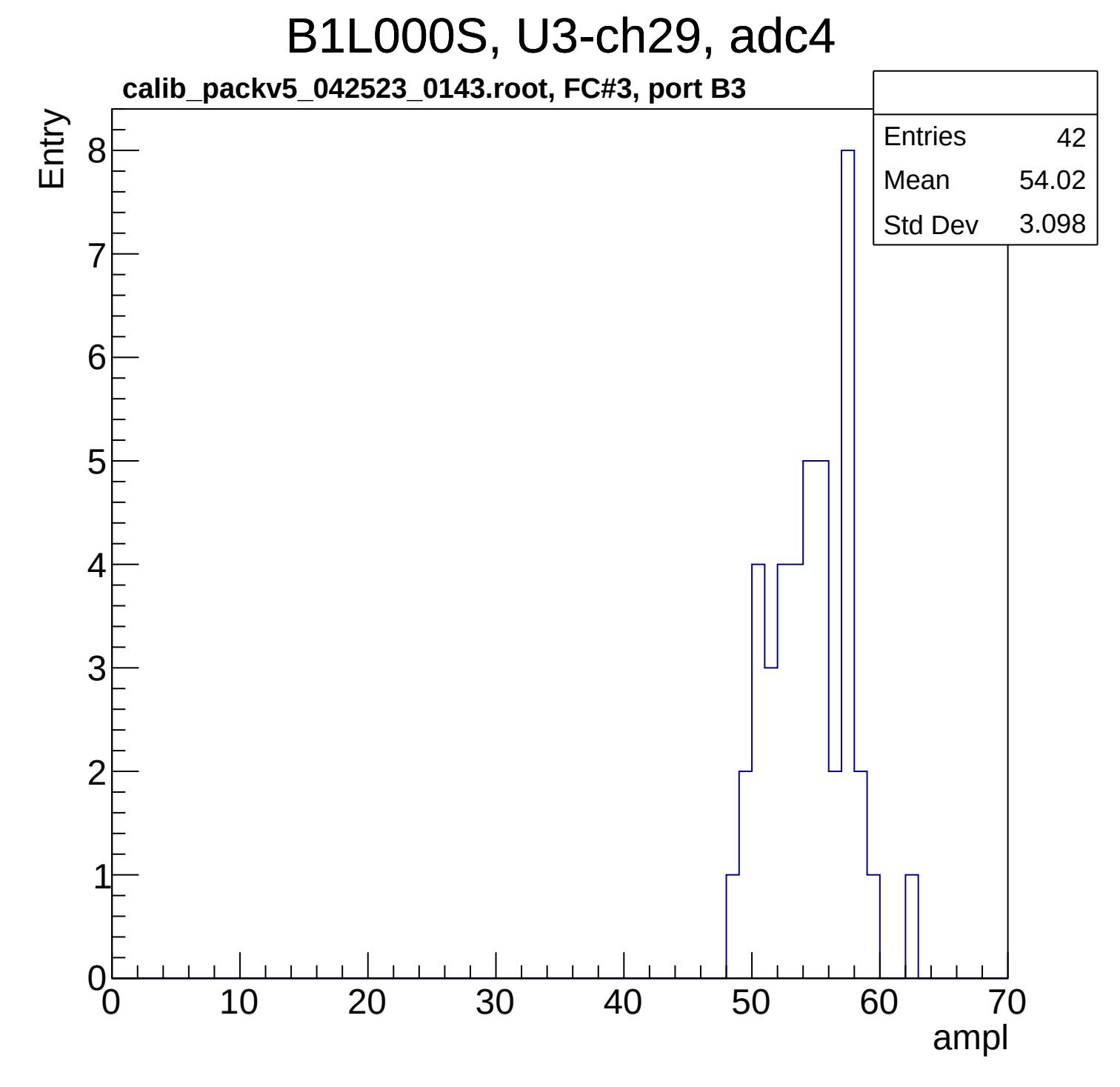
B1L000S, U3-ch29, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 54.02
Std Dev 3.098
Entry
ampl
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2
3
4
5
6
7
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0
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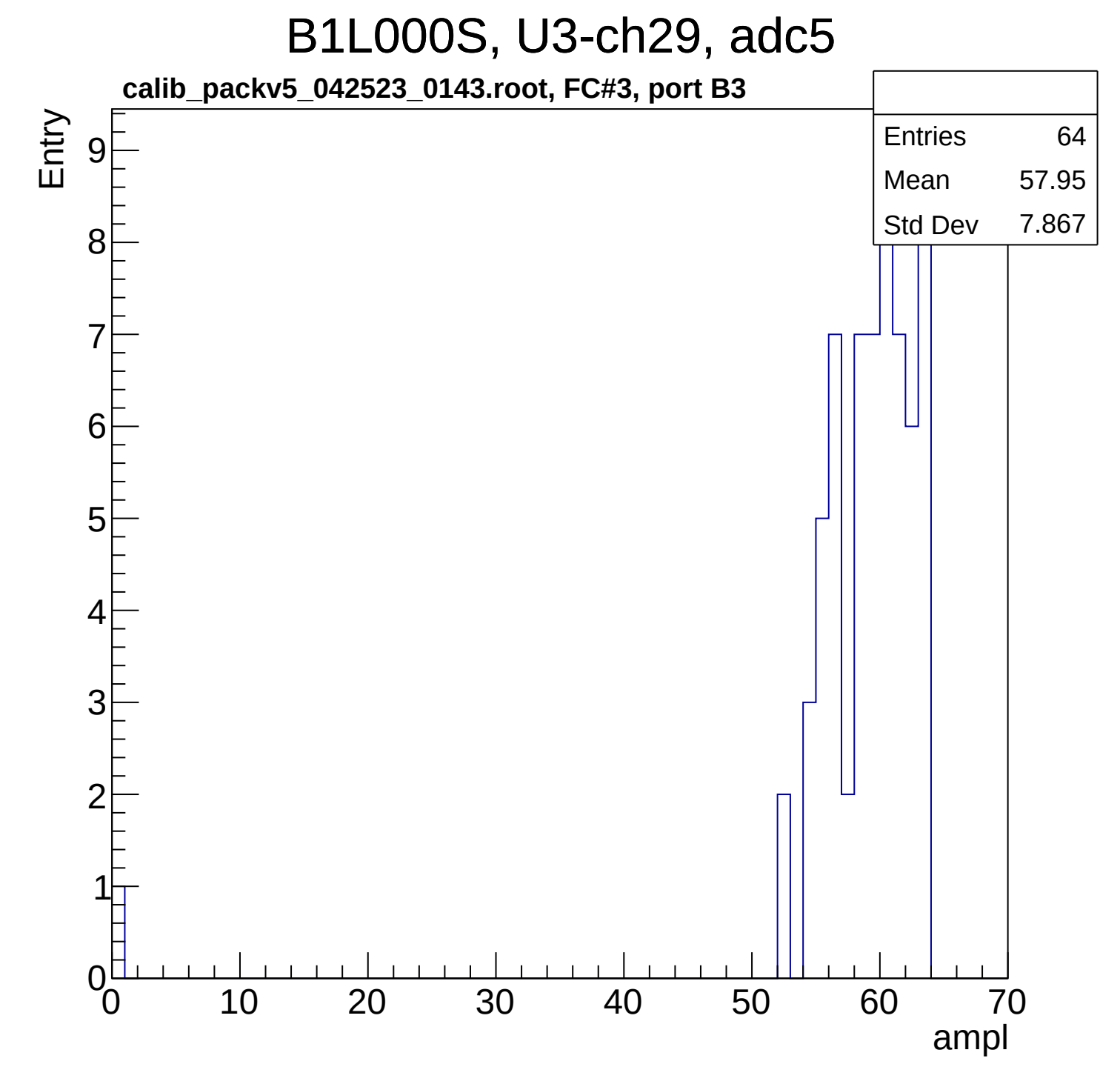
B1L000S, U3-ch29, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 57.95
Std Dev 7.867
Entry
ampl
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0
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60
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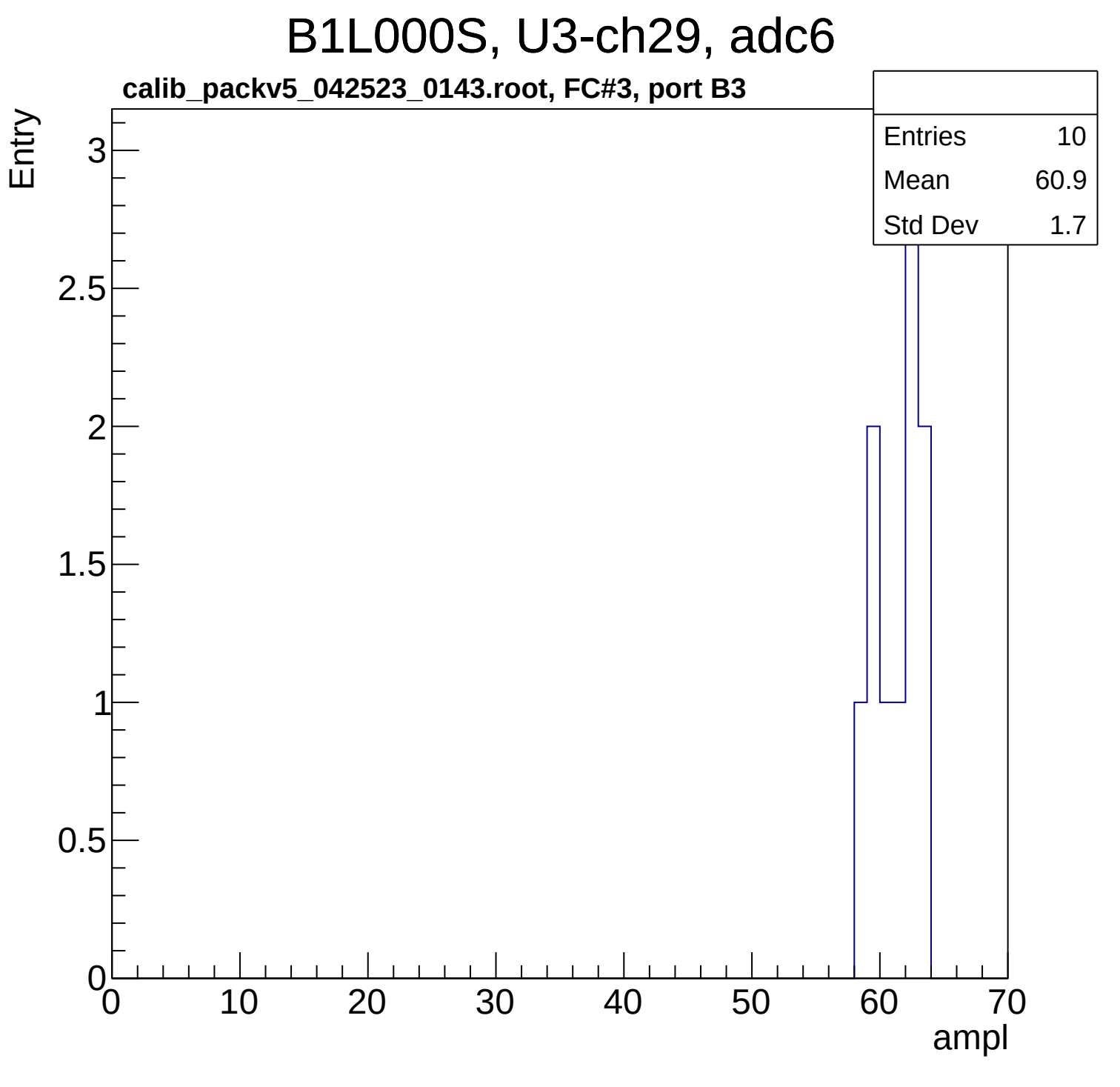
B1L000S, U3-ch29, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 60.9
Std Dev 1.7
Entry
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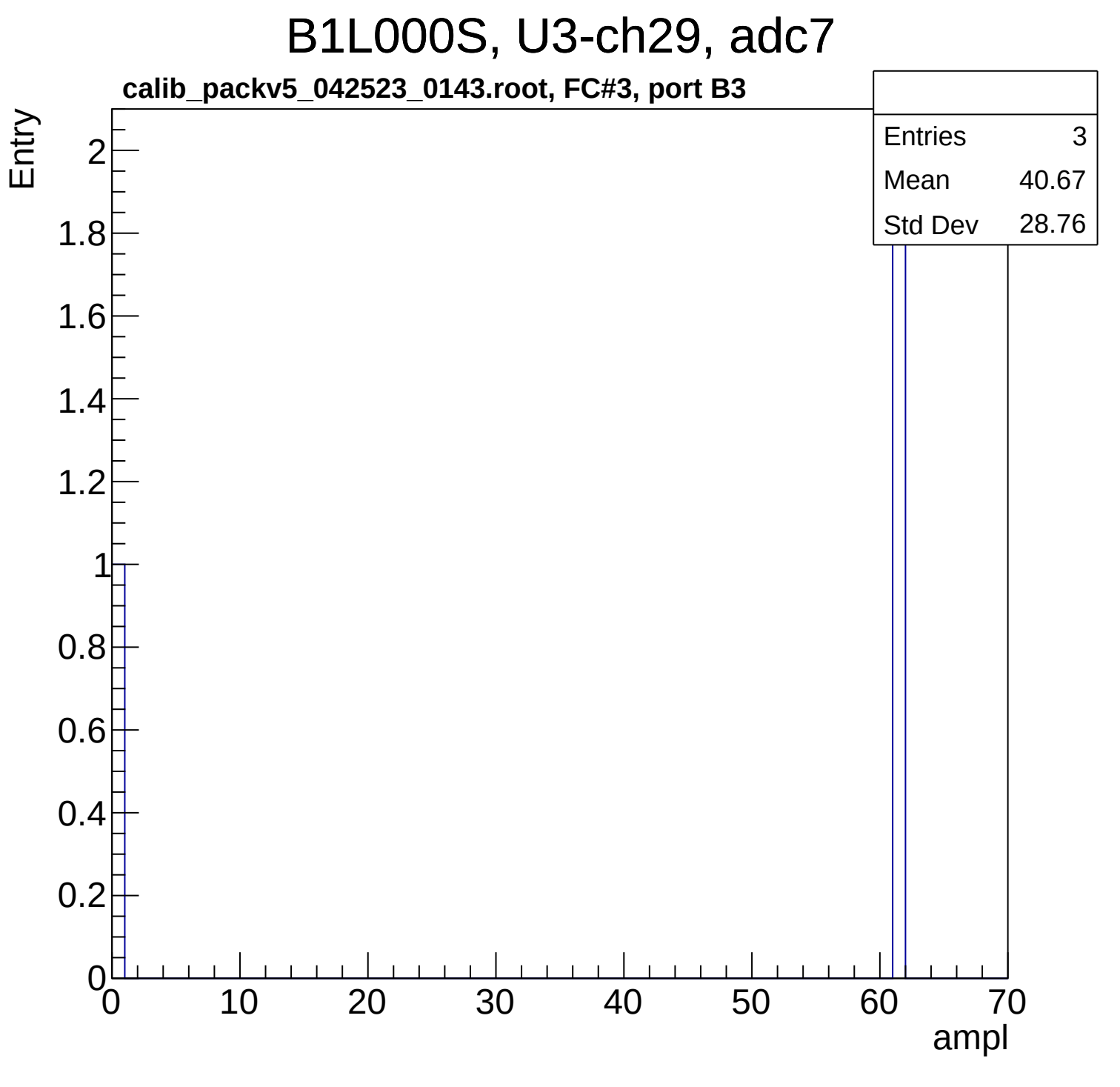
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch29, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 40.67
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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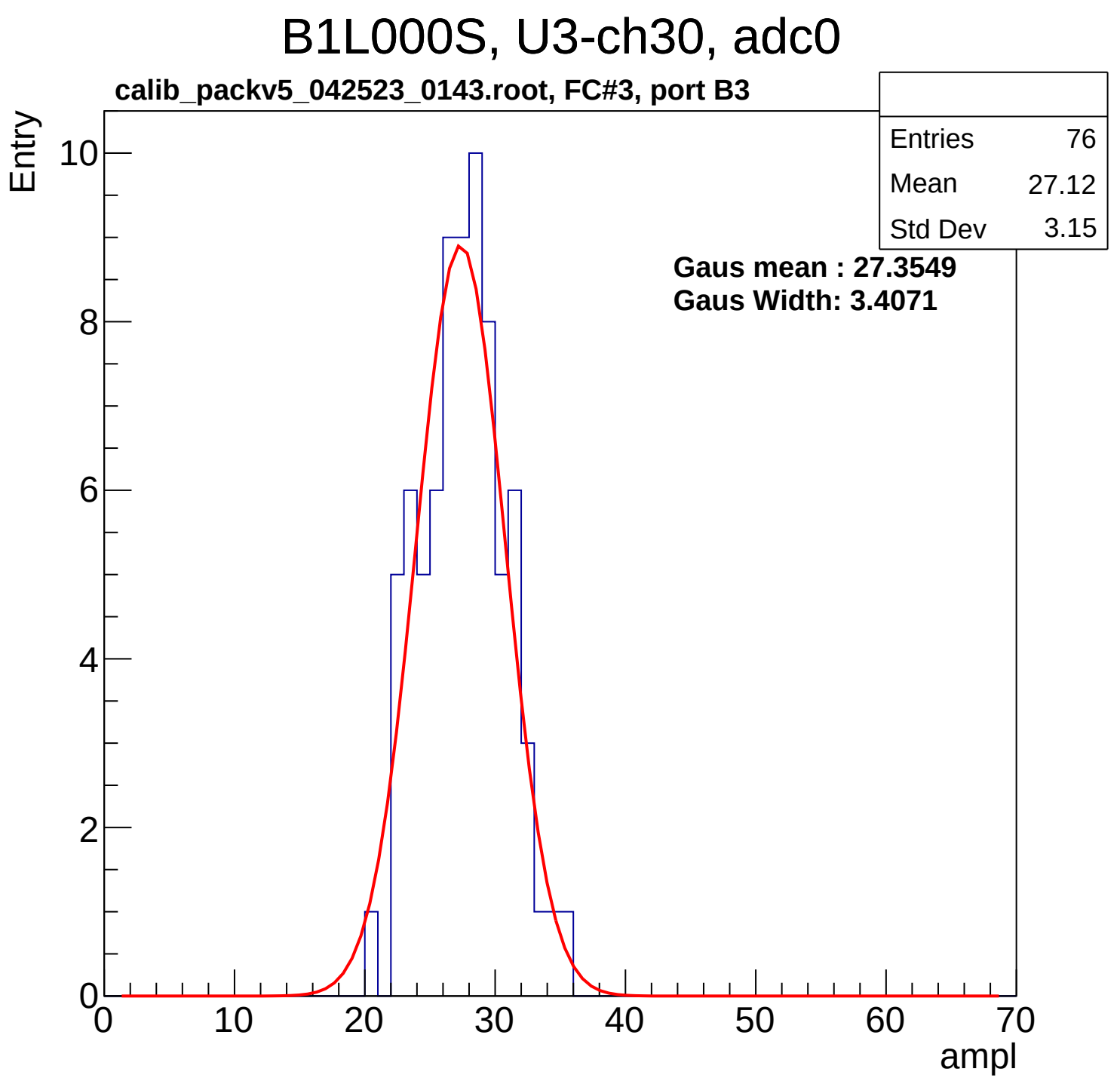
B1L000S, U3-ch30, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 27.12
Std Dev 3.15
Gaus mean : 27.3549
Gaus Width: 3.4071
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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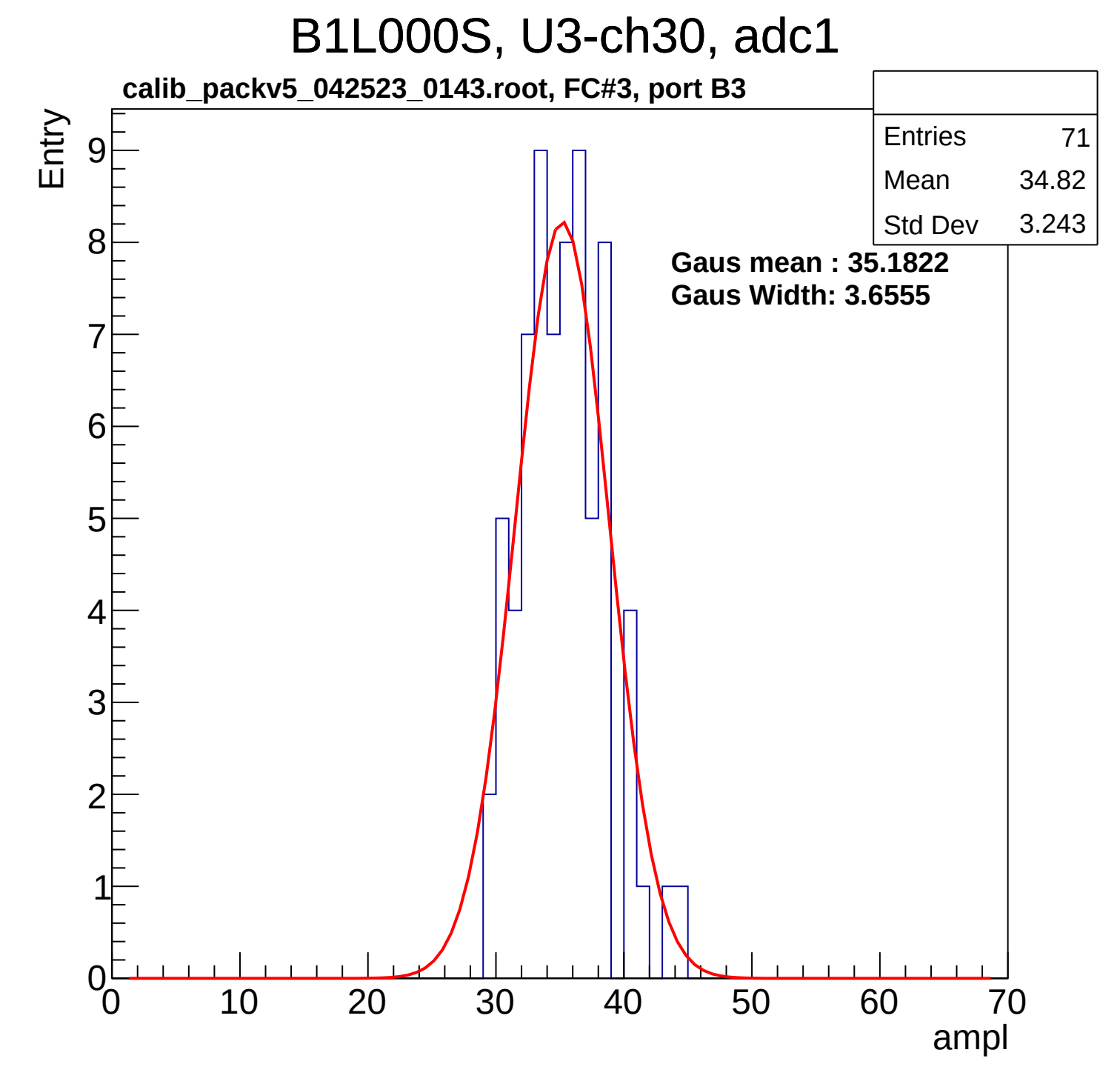
B1L000S, U3-ch30, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 34.82
Std Dev 3.243
Gaus mean : 35.1822
Gaus Width: 3.6555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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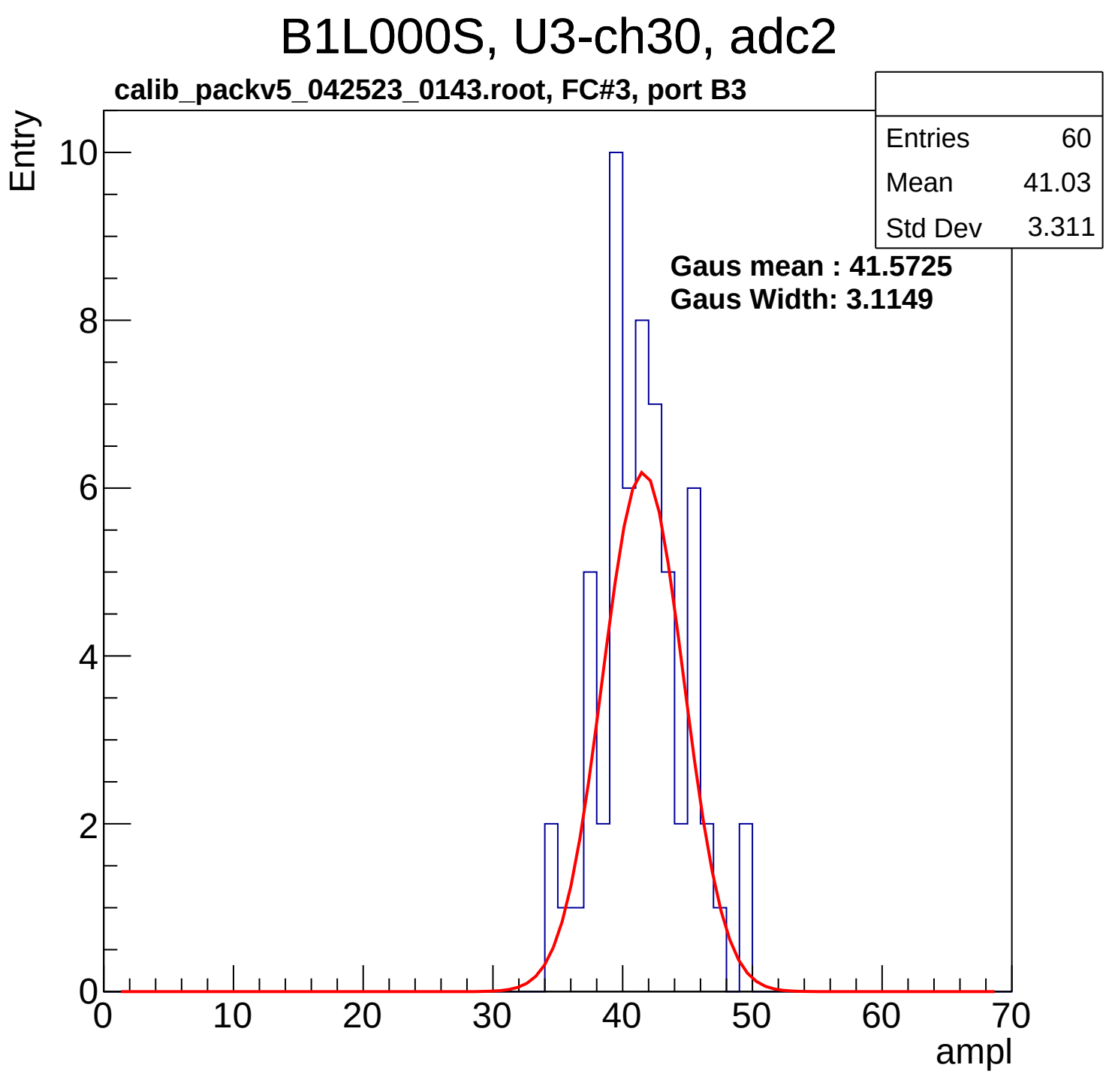
B1L000S, U3-ch30, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 41.03
Std Dev 3.311
Gaus mean : 41.5725
Gaus Width: 3.1149
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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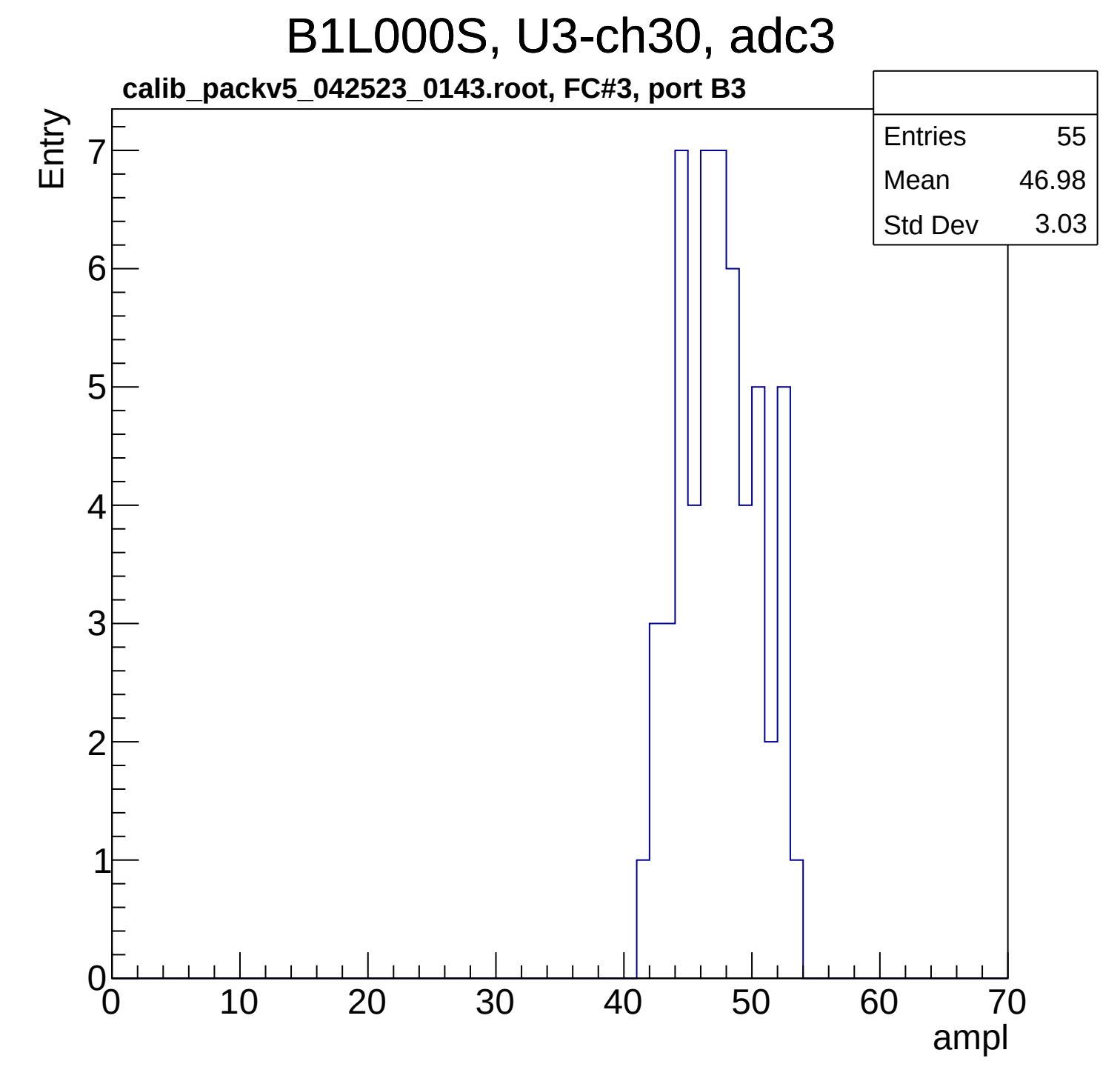
B1L000S, U3-ch30, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 46.98
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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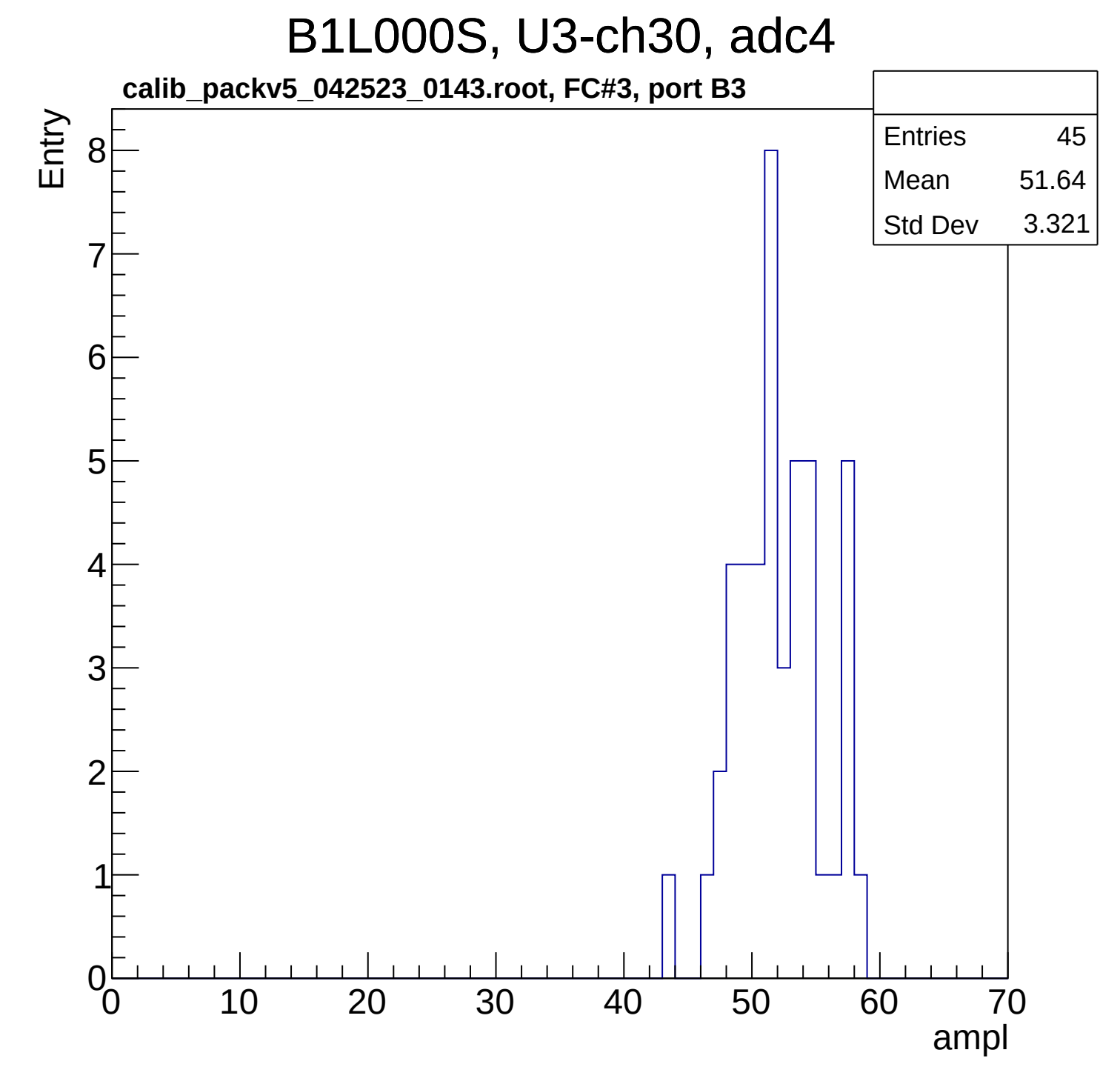
B1L000S, U3-ch30, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 51.64
Std Dev 3.321
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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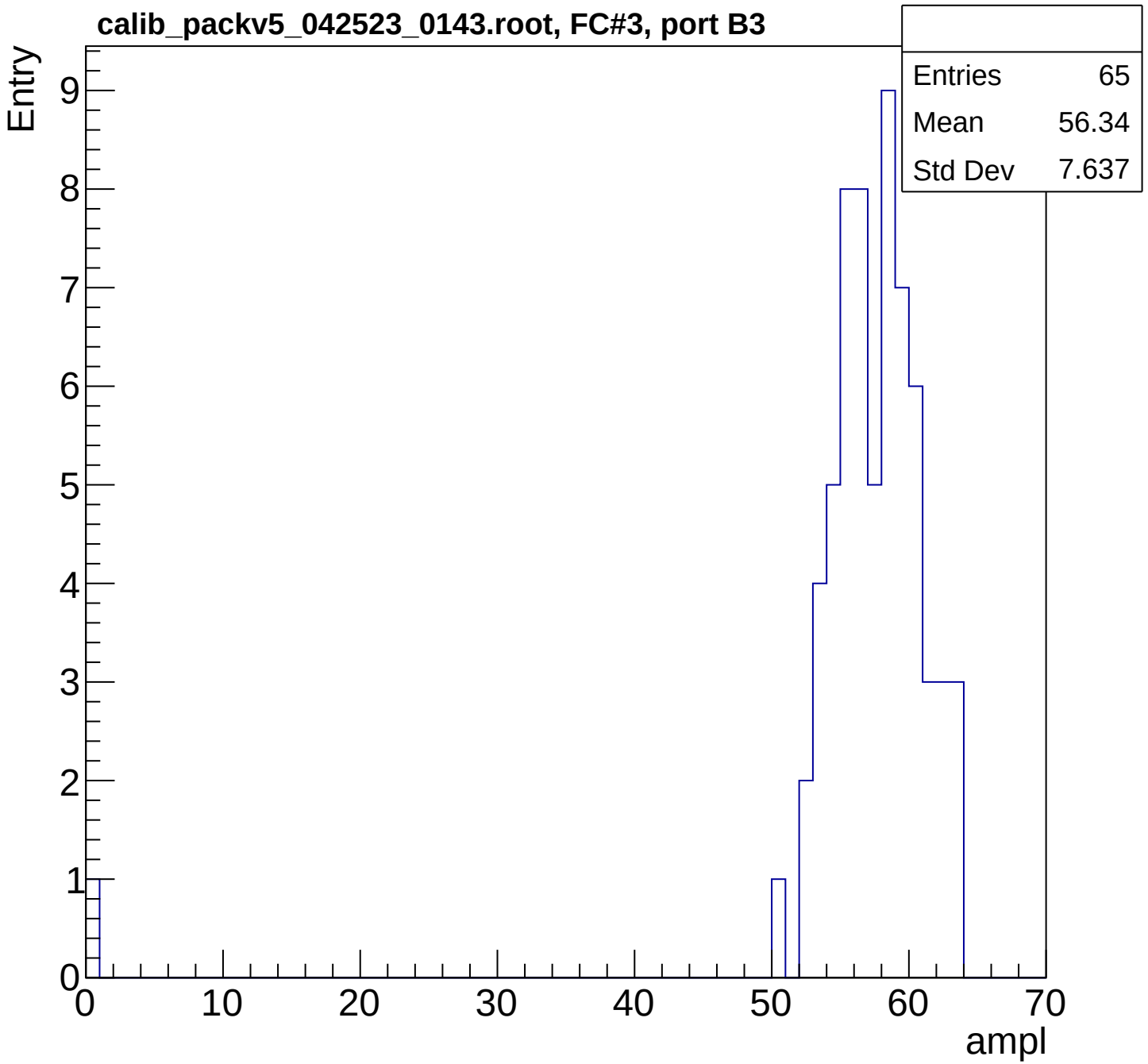
B1L000S, U3-ch30, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 56.34
Std Dev 7.637
Entry
ampl
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3
4
5
6
7
8
9
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60
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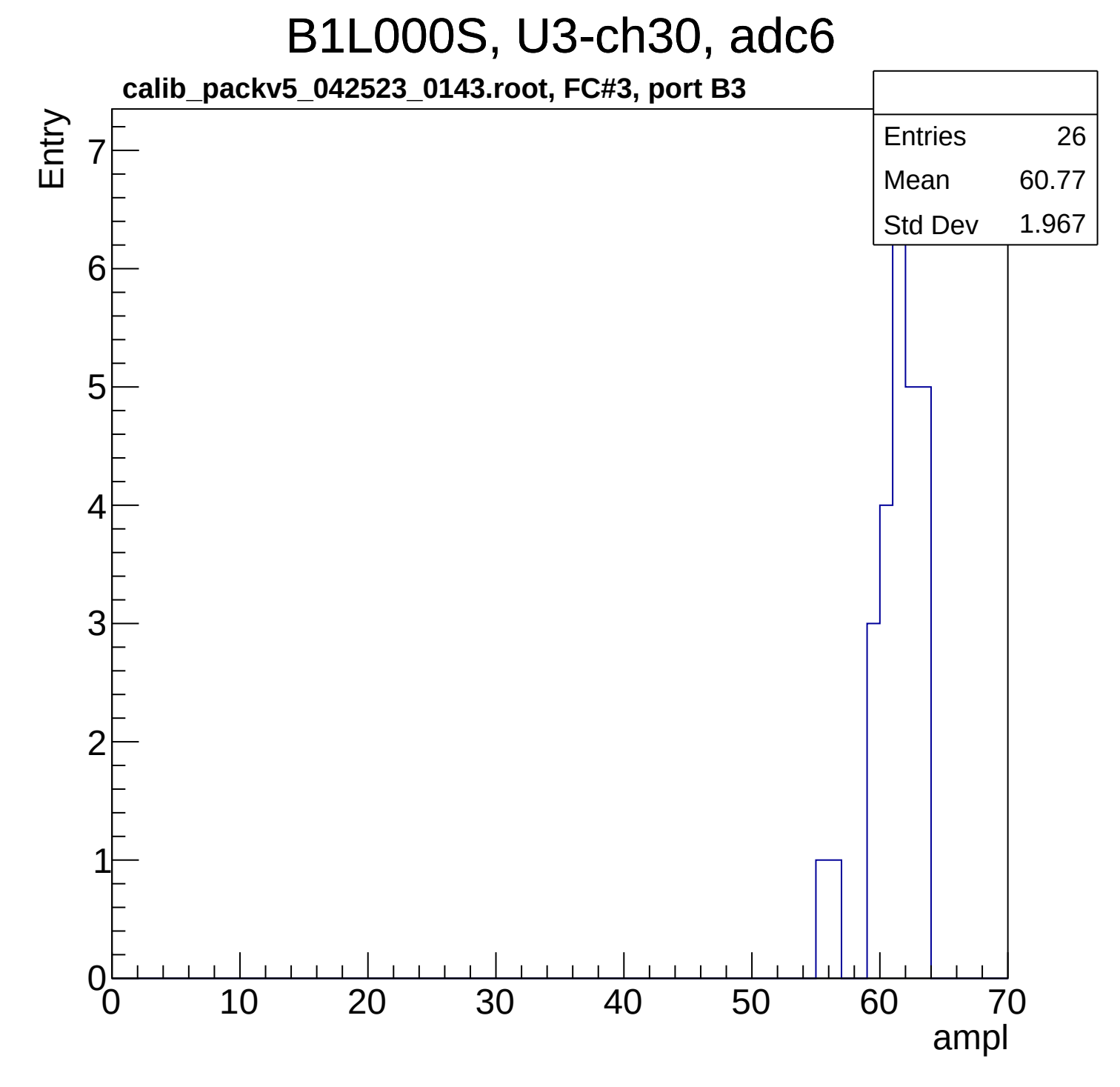
B1L000S, U3-ch30, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 60.77
Std Dev 1.967
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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60
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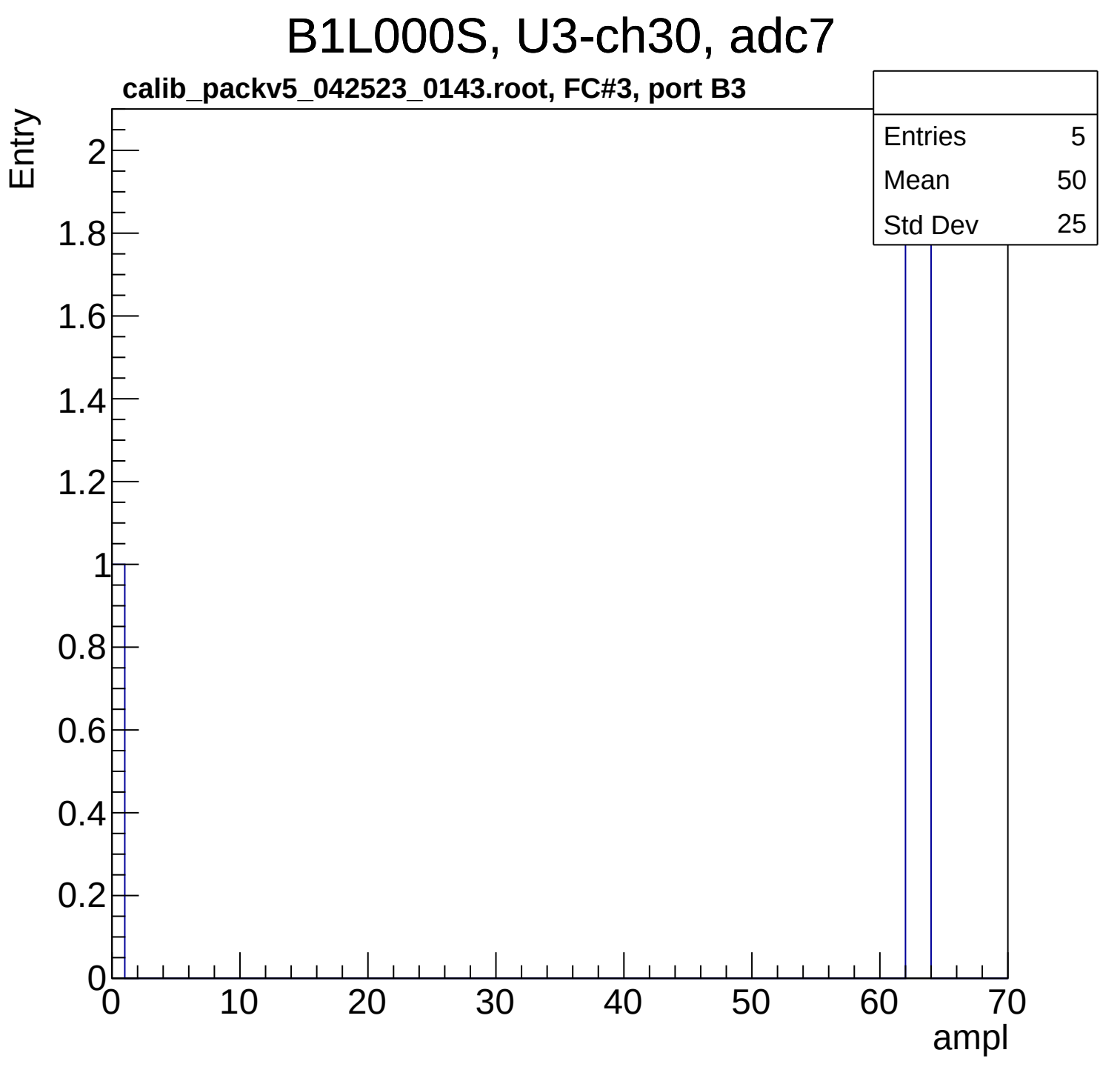
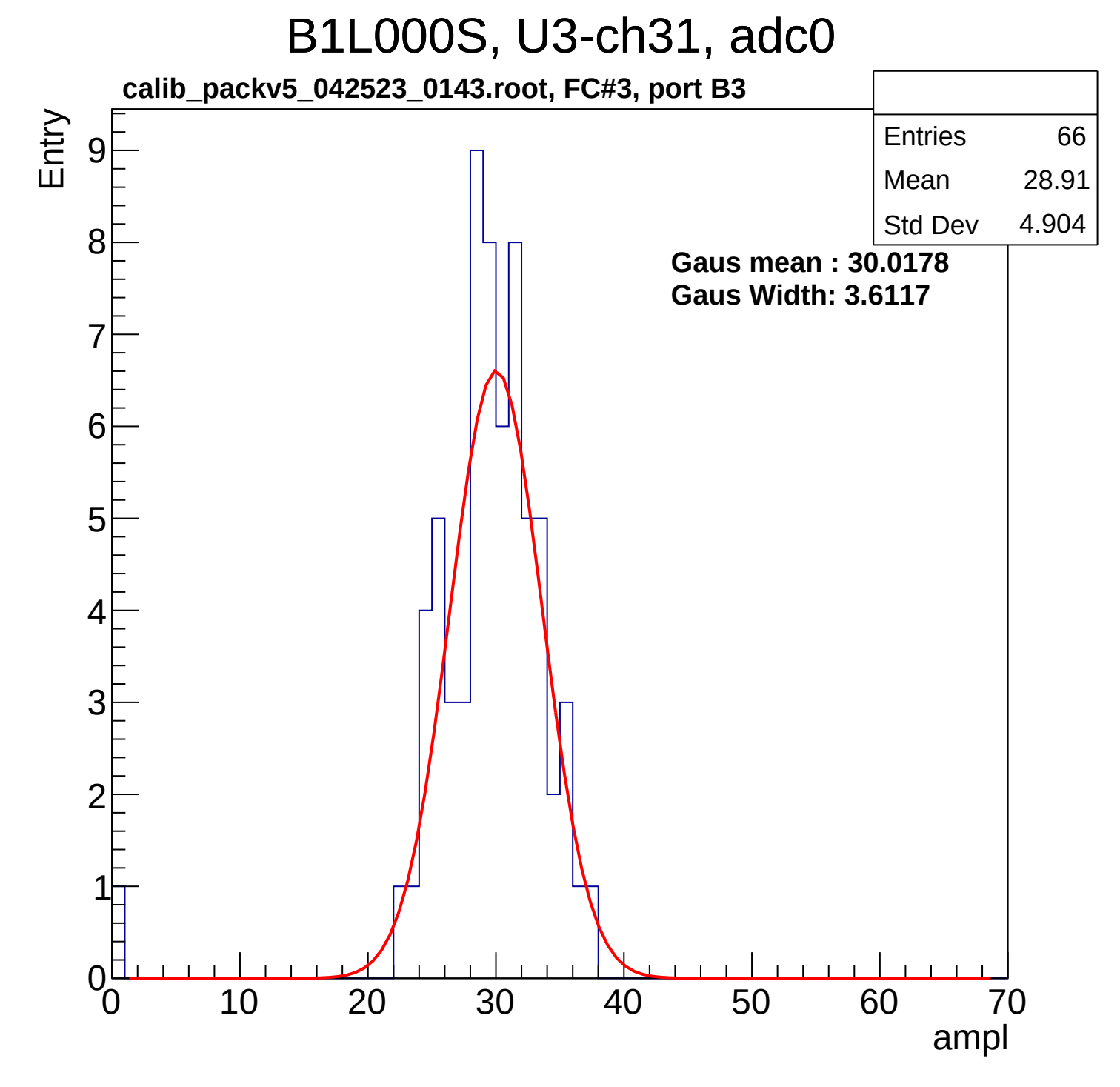

B1L000S, U3-ch30, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch31, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.91
Std Dev 4.904
Gaus mean : 30.0178
Gaus Width: 3.6117
Entry
ampl
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2
3
4
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6
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8
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0
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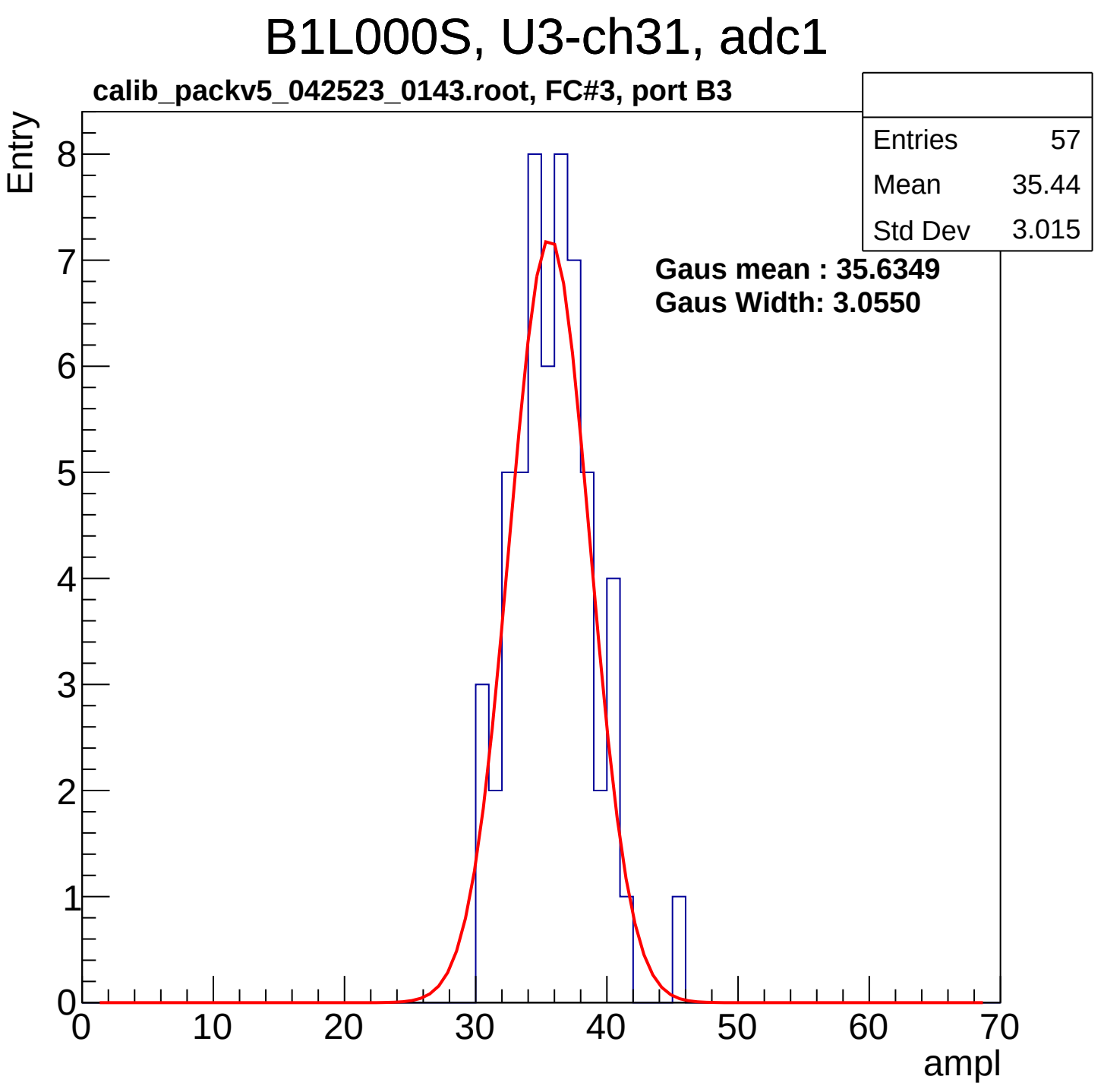
B1L000S, U3-ch31, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 35.44
Std Dev 3.015
Gaus mean : 35.6349
Gaus Width: 3.0550
Entry
ampl
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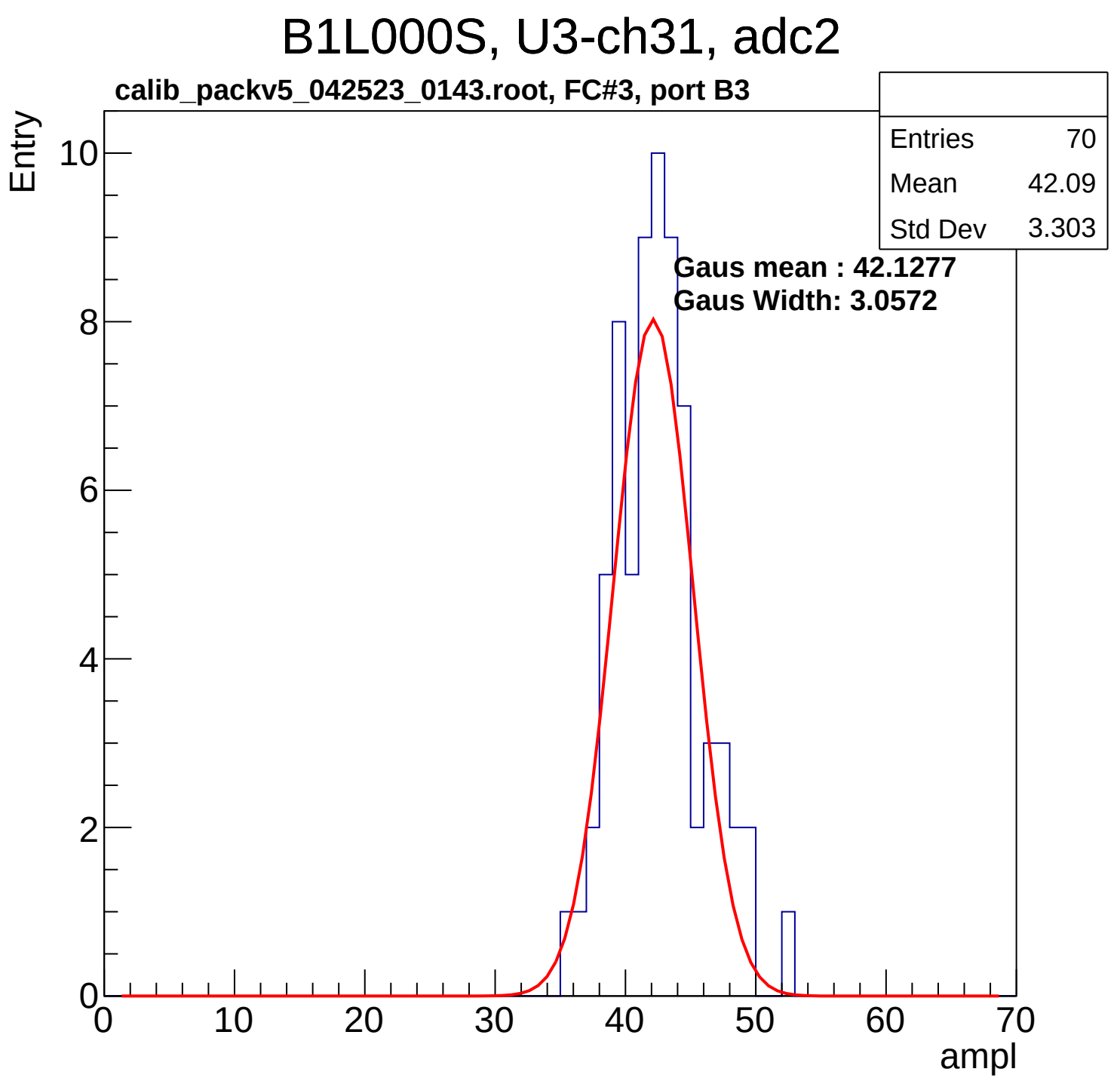
B1L000S, U3-ch31, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 42.09
Std Dev 3.303
Gaus mean : 42.1277
Gaus Width: 3.0572
Entry
ampl
0
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4
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0
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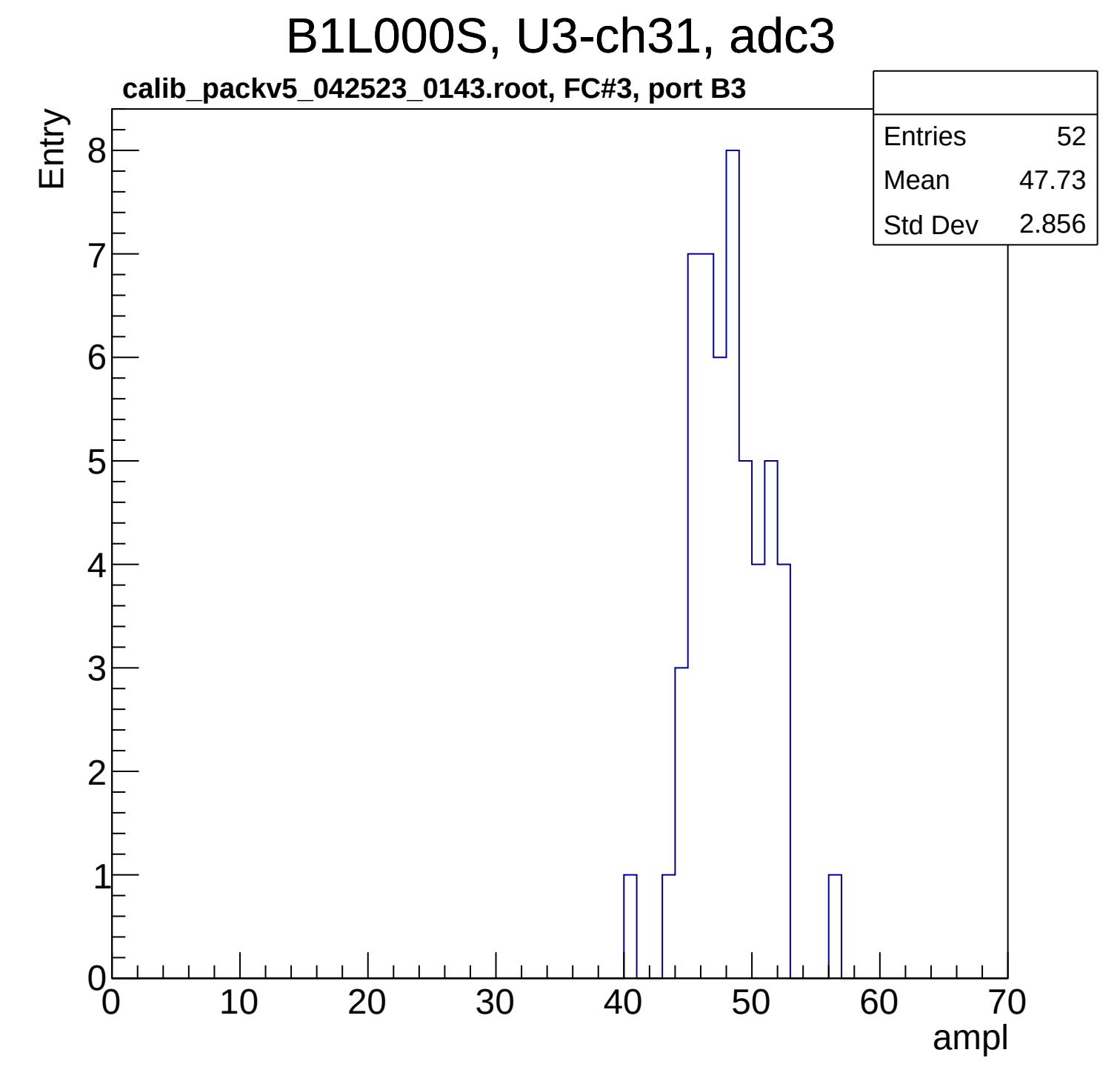
B1L000S, U3-ch31, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 47.73
Std Dev 2.856
Entry
ampl
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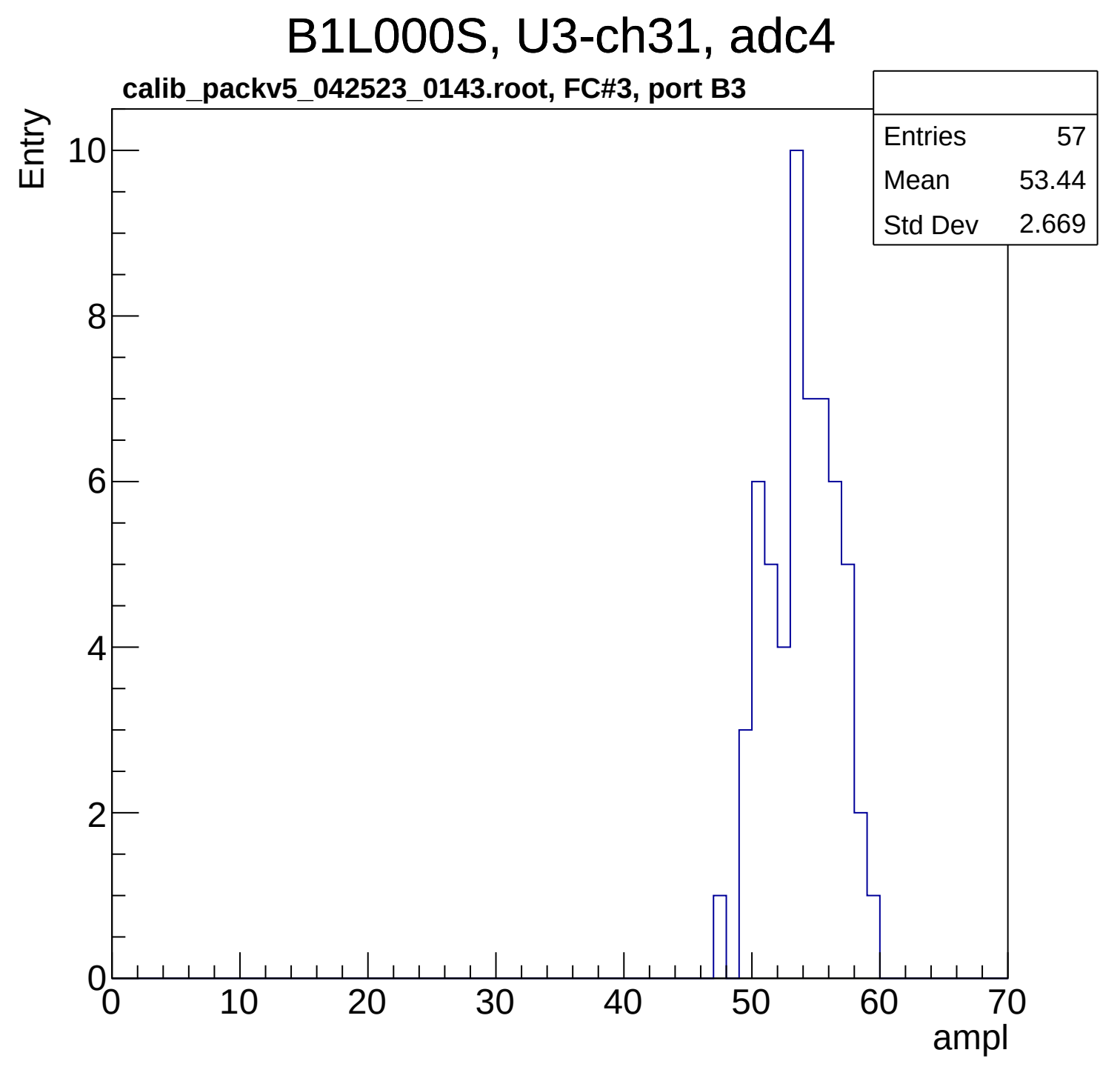
B1L000S, U3-ch31, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 53.44
Std Dev 2.669
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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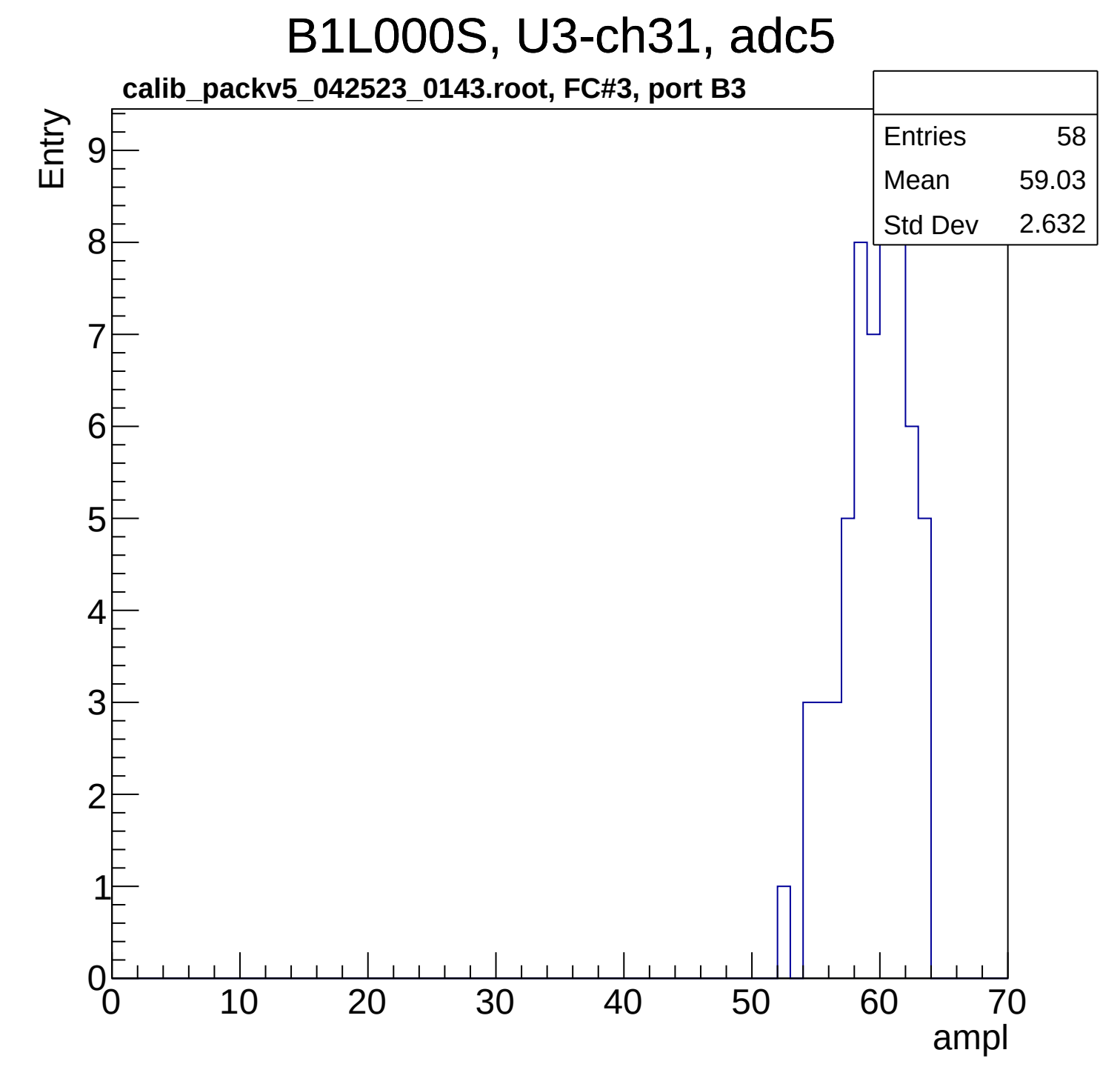
B1L000S, U3-ch31, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 59.03
Std Dev 2.632
Entry
ampl
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2
3
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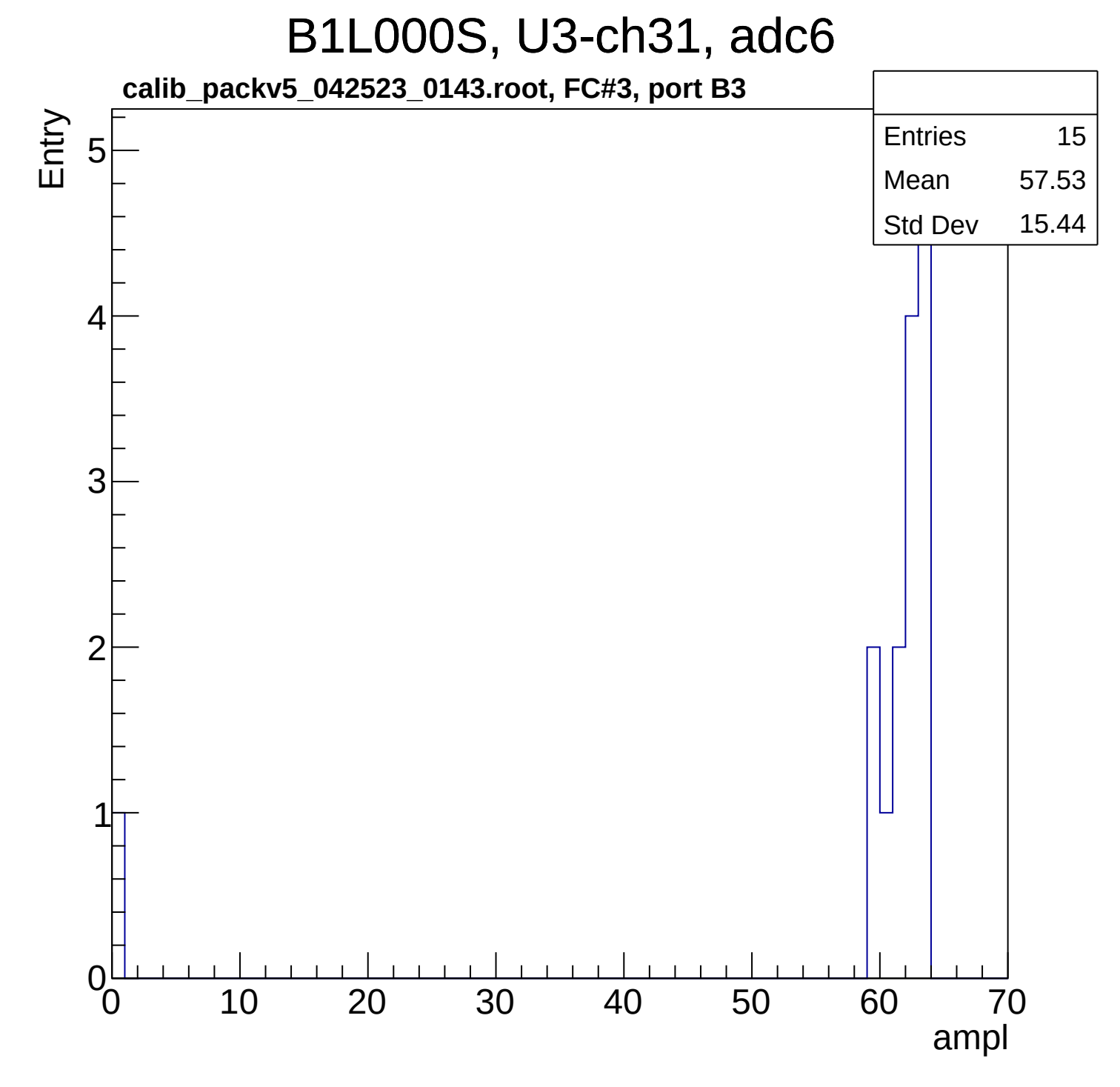
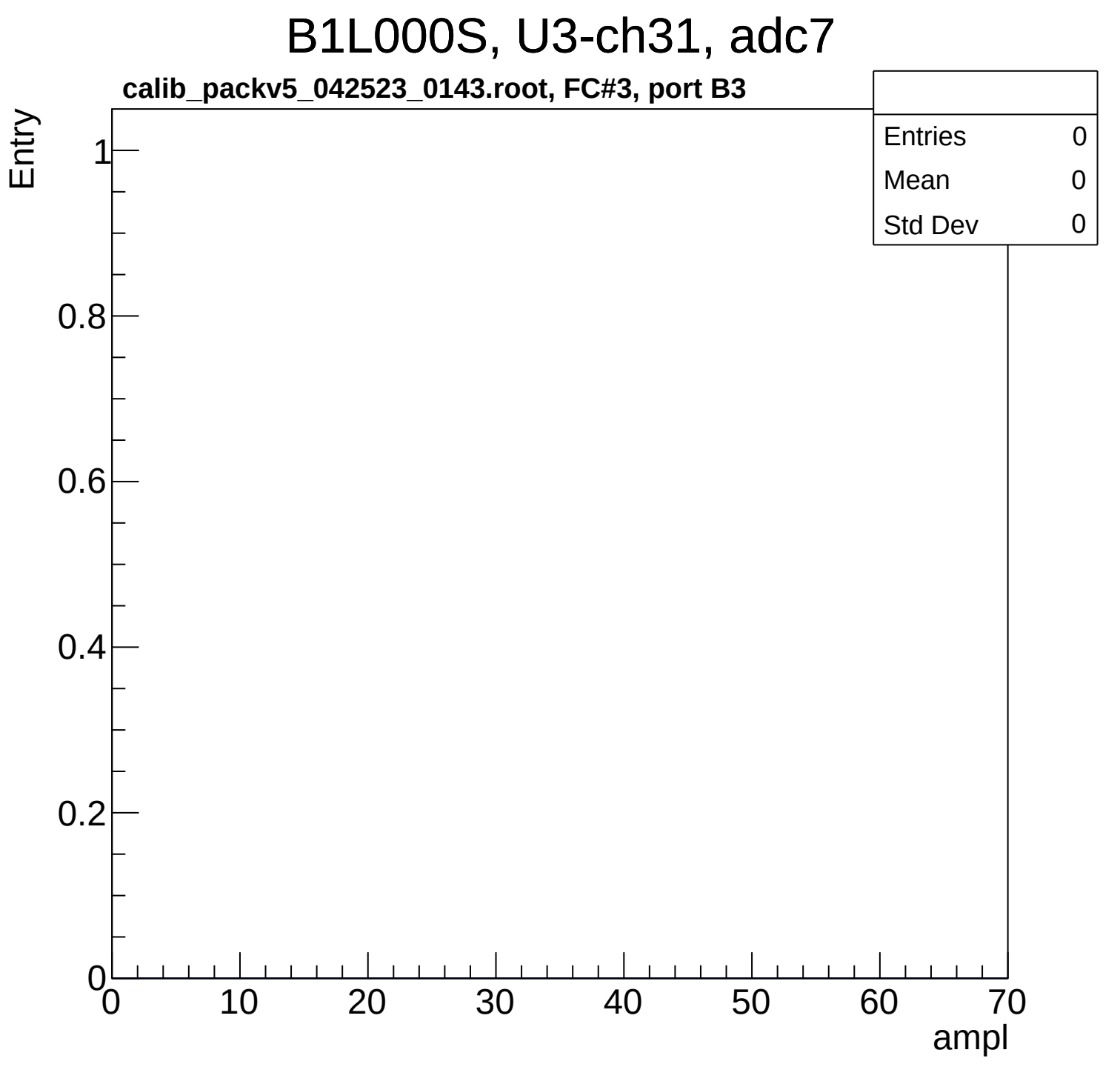

B1L000S, U3-ch31, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57.53
Std Dev 15.44
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch31, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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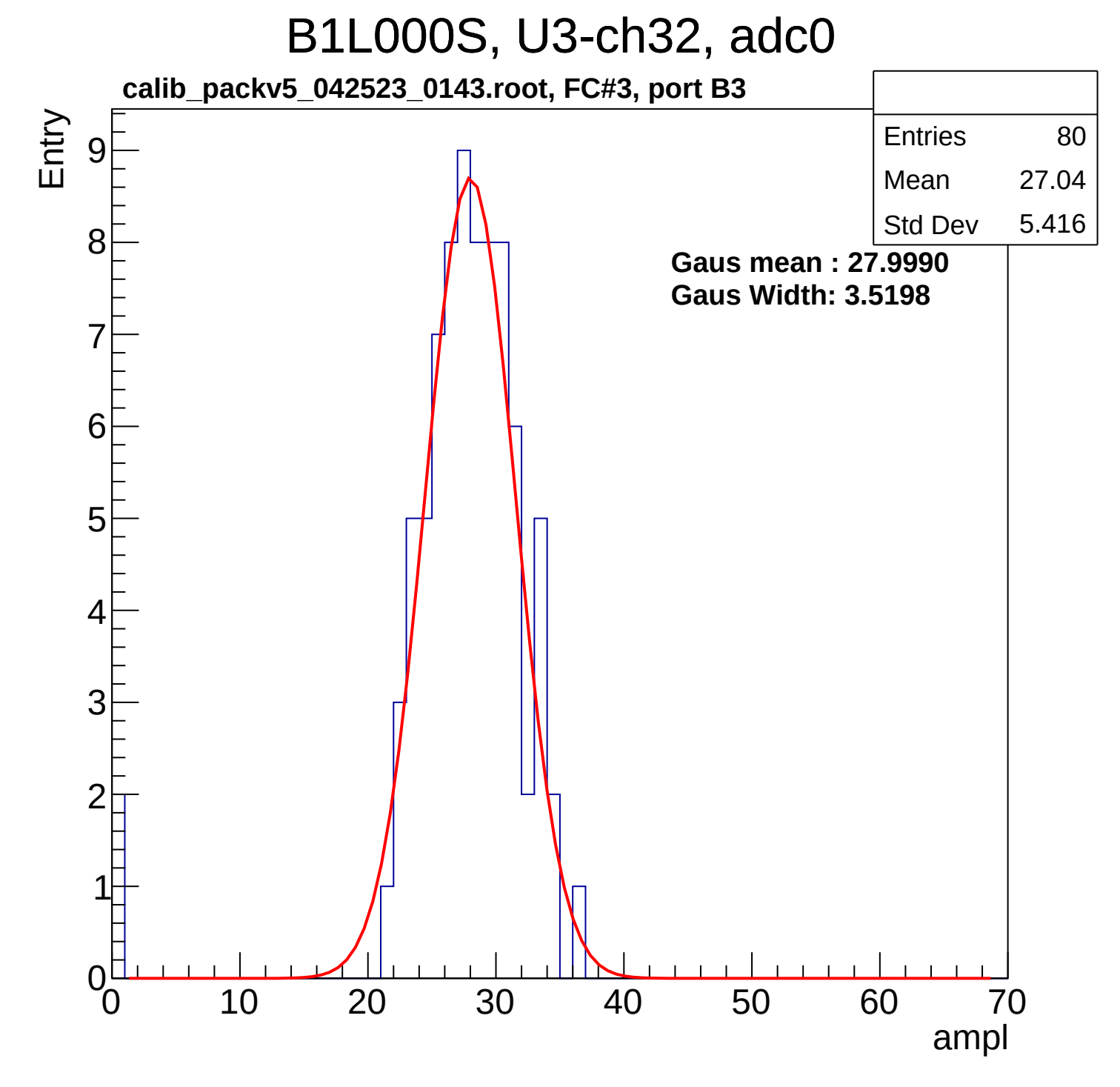
40
50
60
70
ampl


B1L000S, U3-ch32, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 27.04
Std Dev 5.416
Gaus mean : 27.9990
Gaus Width: 3.5198
Entry
ampl
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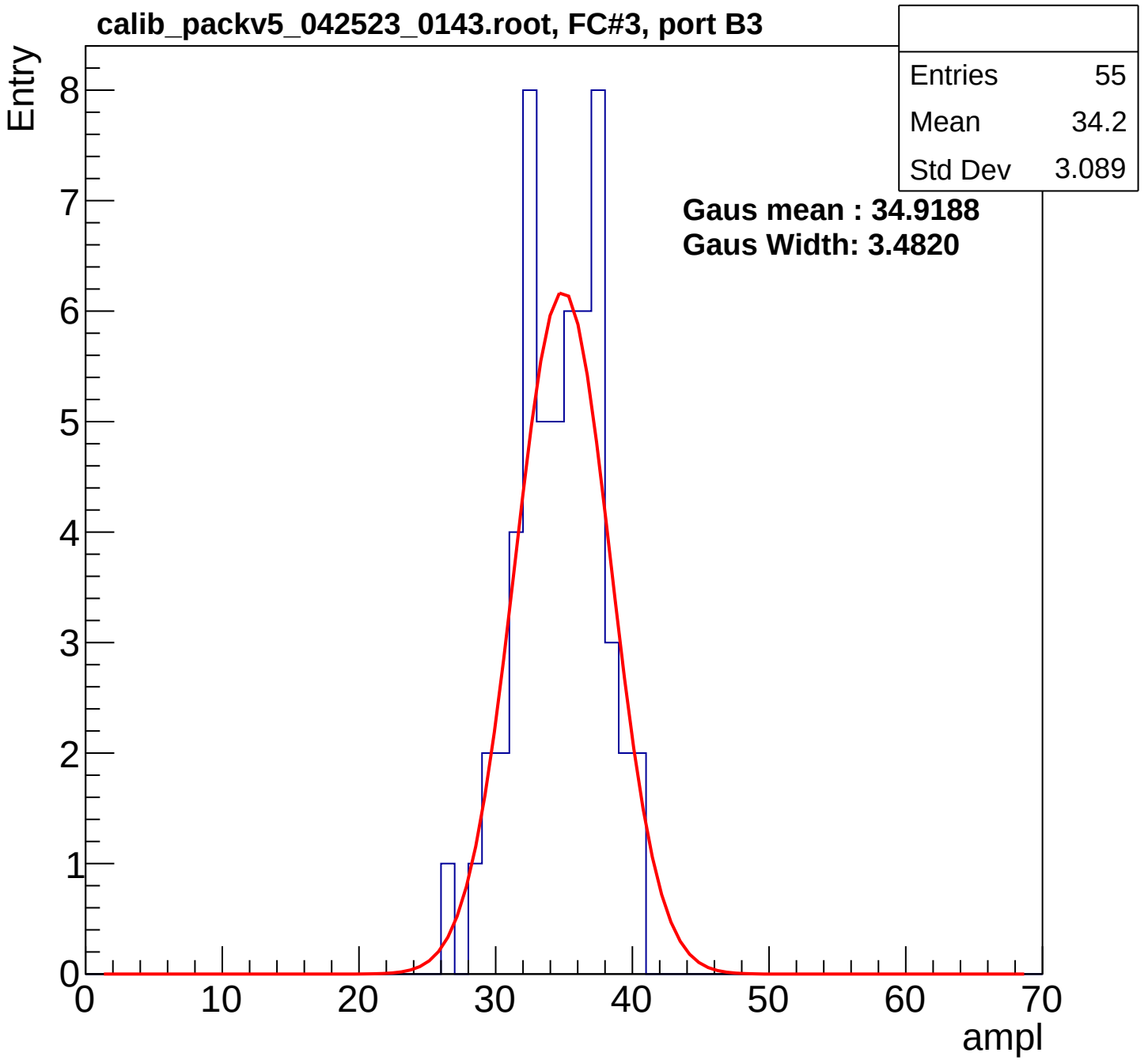
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch32, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 34.2
Std Dev 3.089
Gaus mean : 34.9188
Gaus Width: 3.4820
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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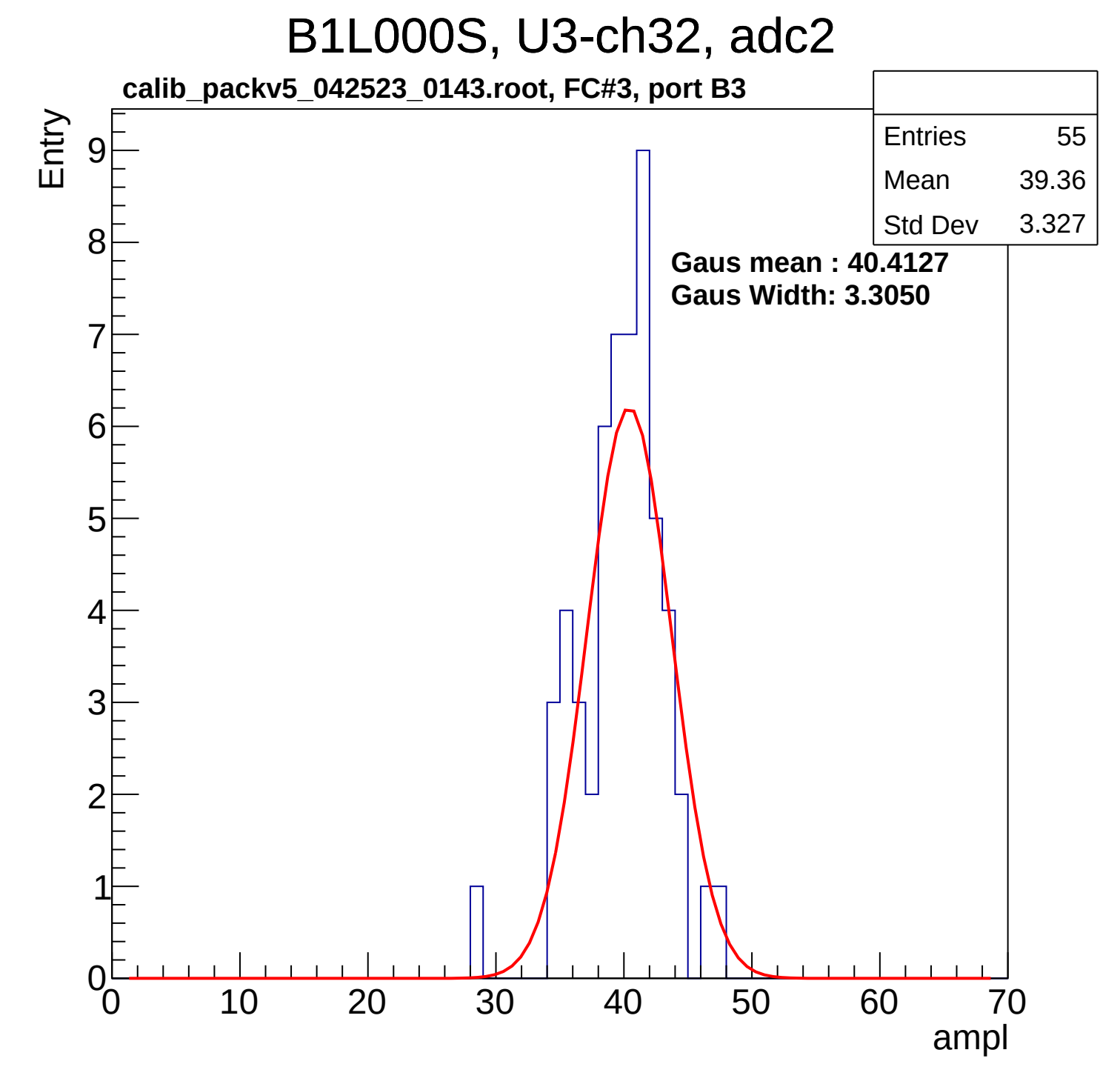
B1L000S, U3-ch32, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 39.36
Std Dev 3.327
Gaus mean : 40.4127
Gaus Width: 3.3050
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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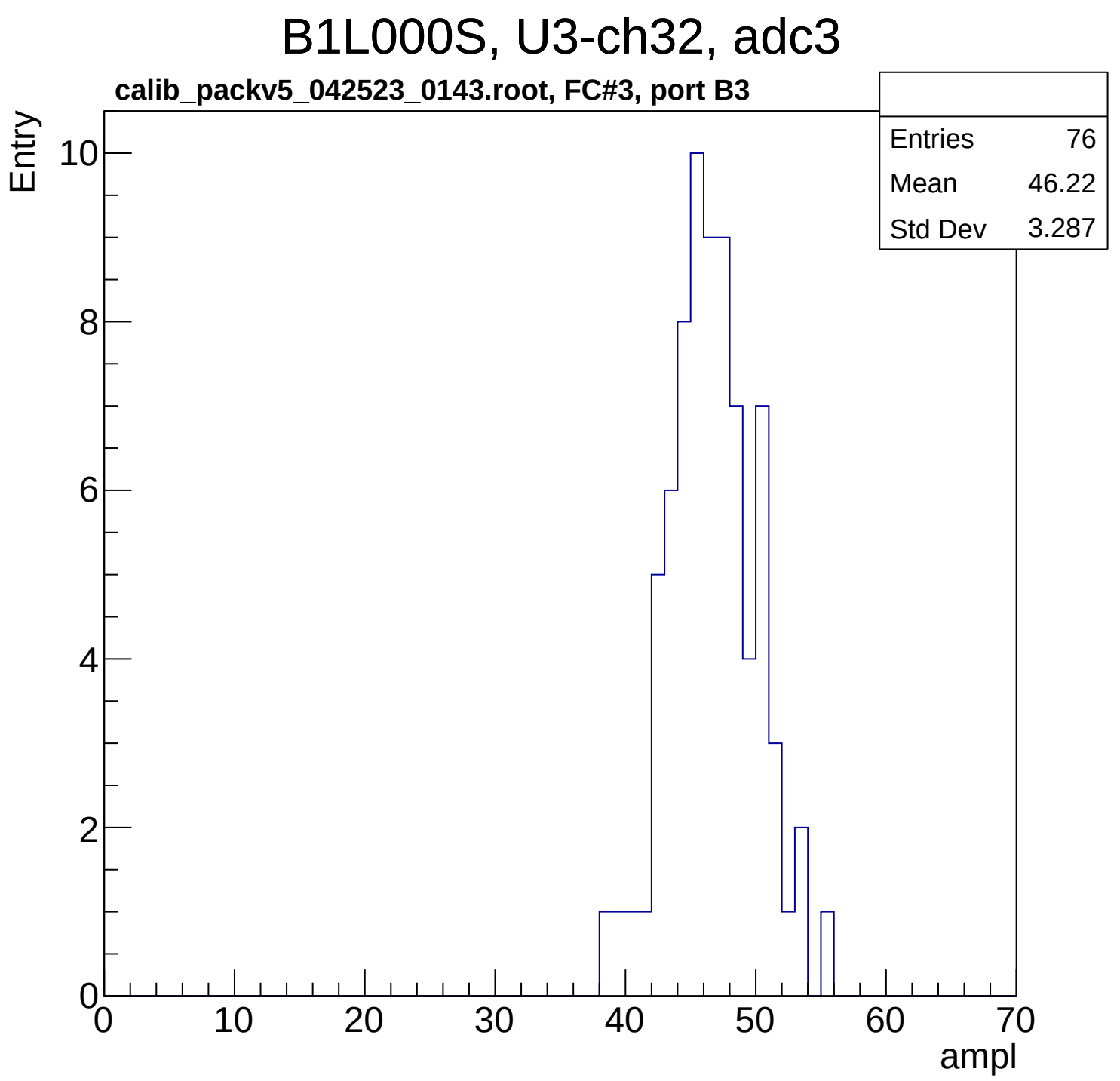
B1L000S, U3-ch32, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 46.22
Std Dev 3.287
Entry
ampl
0
2
4
6
8
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0
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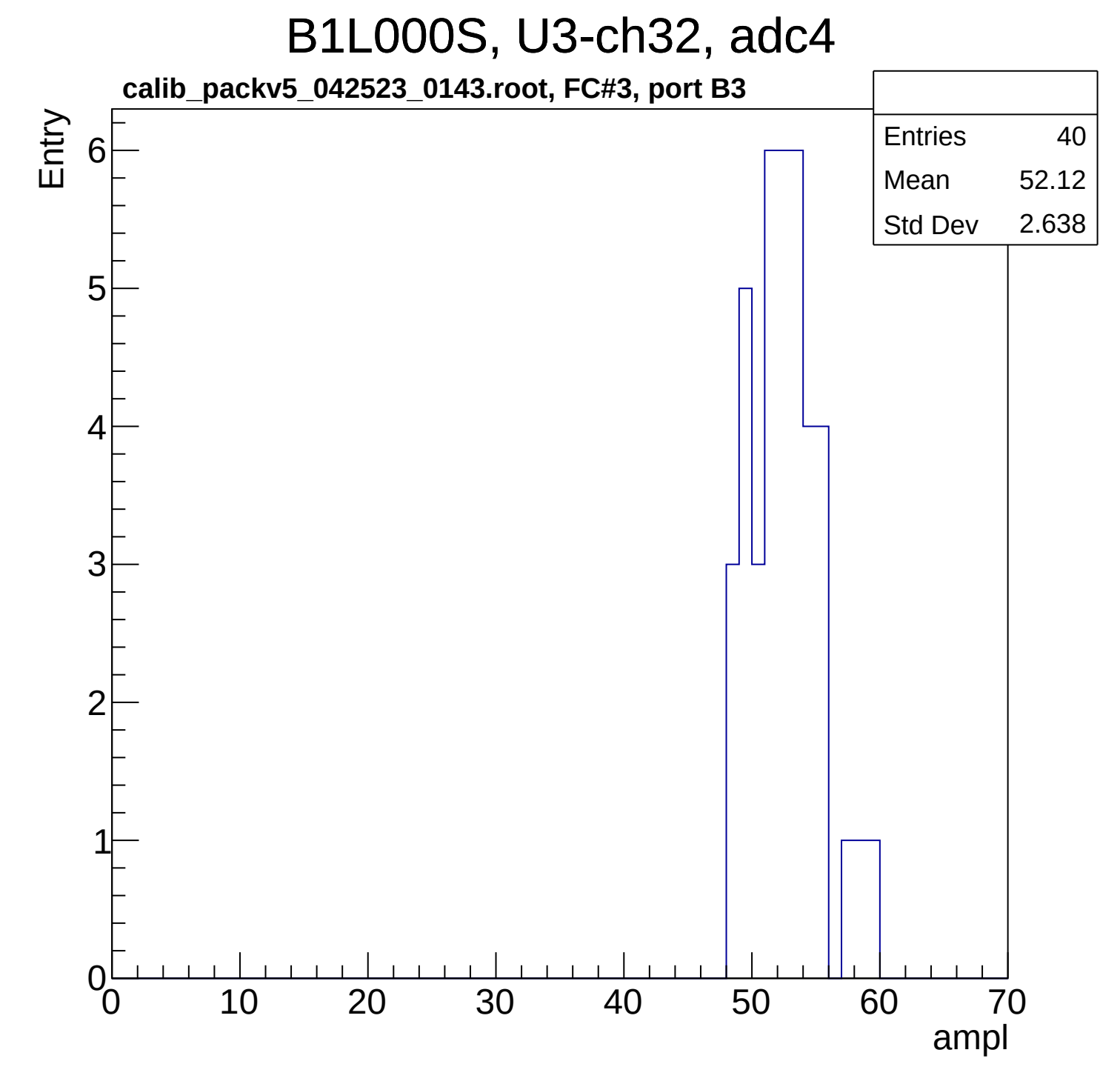
40
50
60
70


B1L000S, U3-ch32, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 52.12
Std Dev 2.638
Entry
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1
2
3
4
5
6
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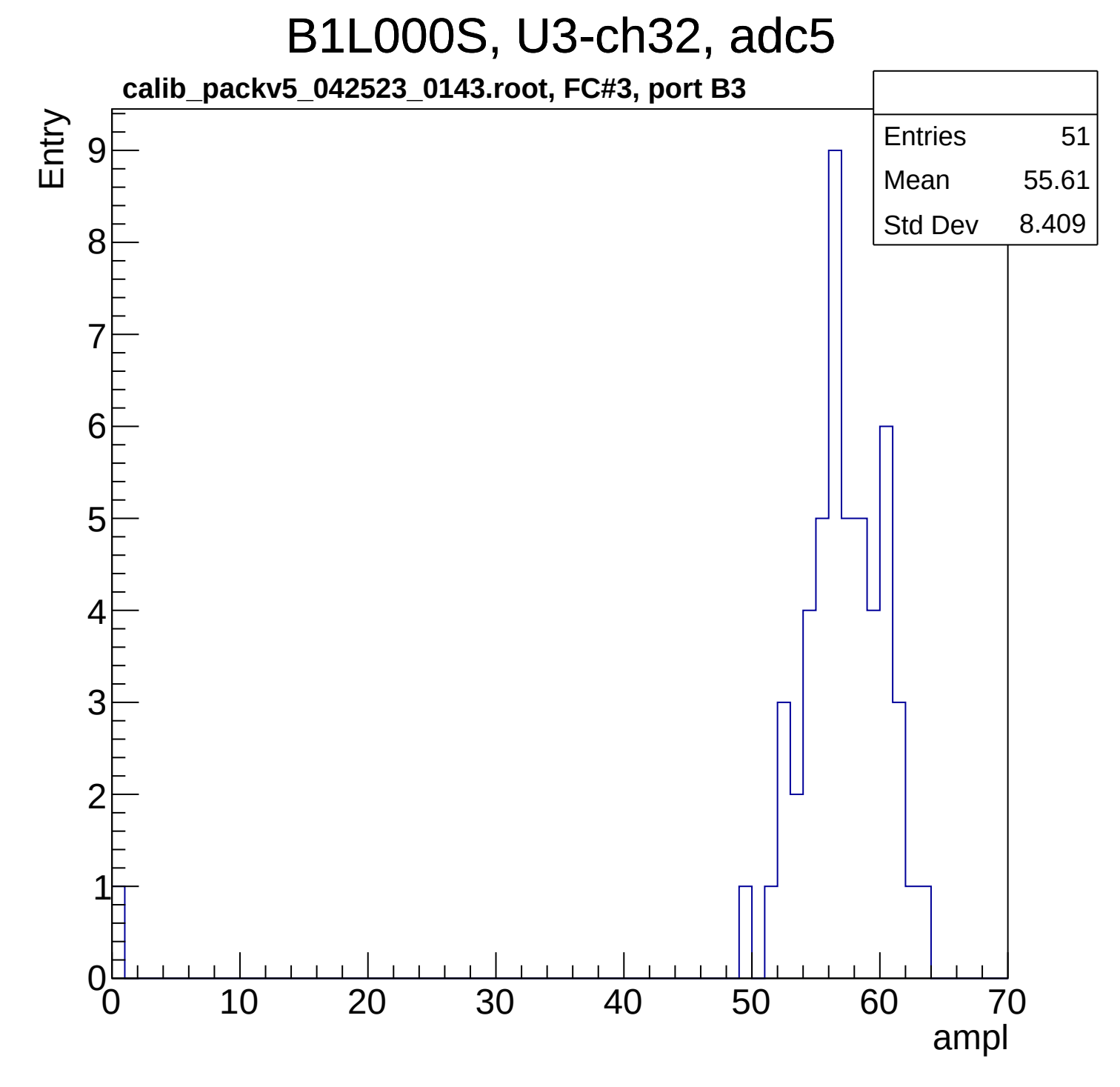
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch32, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 55.61
Std Dev 8.409
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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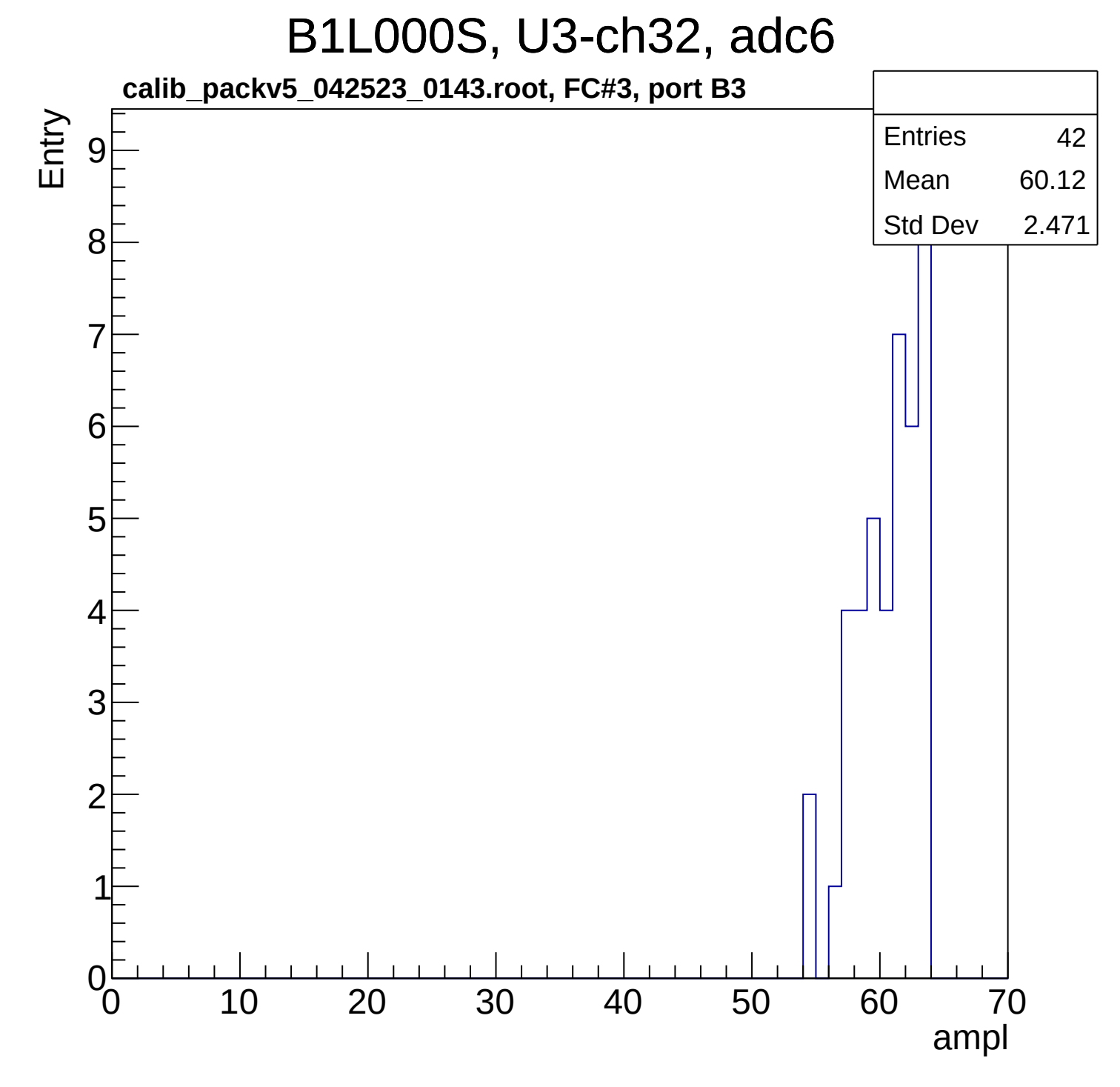
B1L000S, U3-ch32, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 60.12
Std Dev 2.471
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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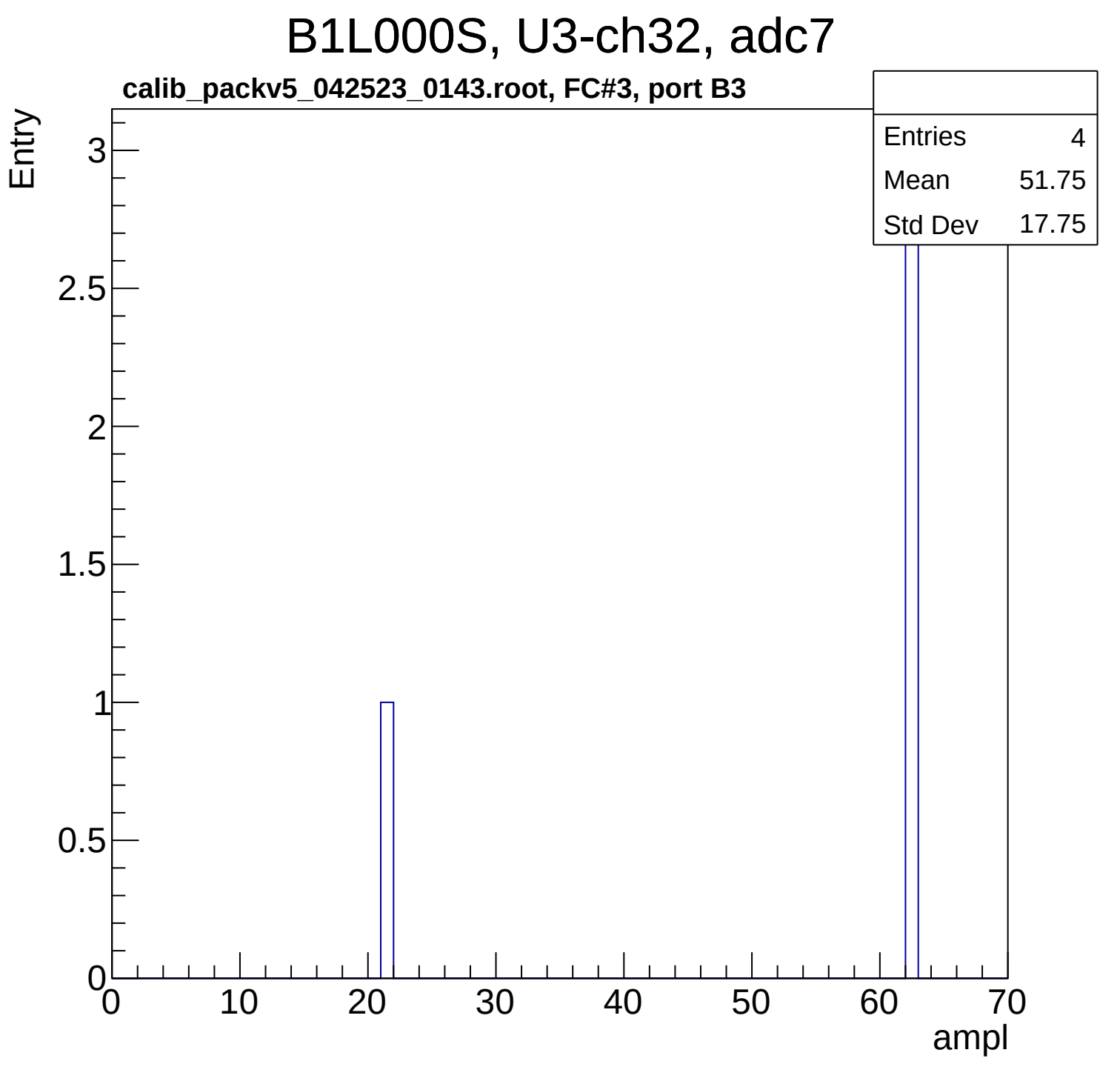
B1L000S, U3-ch32, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 51.75
Std Dev 17.75
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
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60
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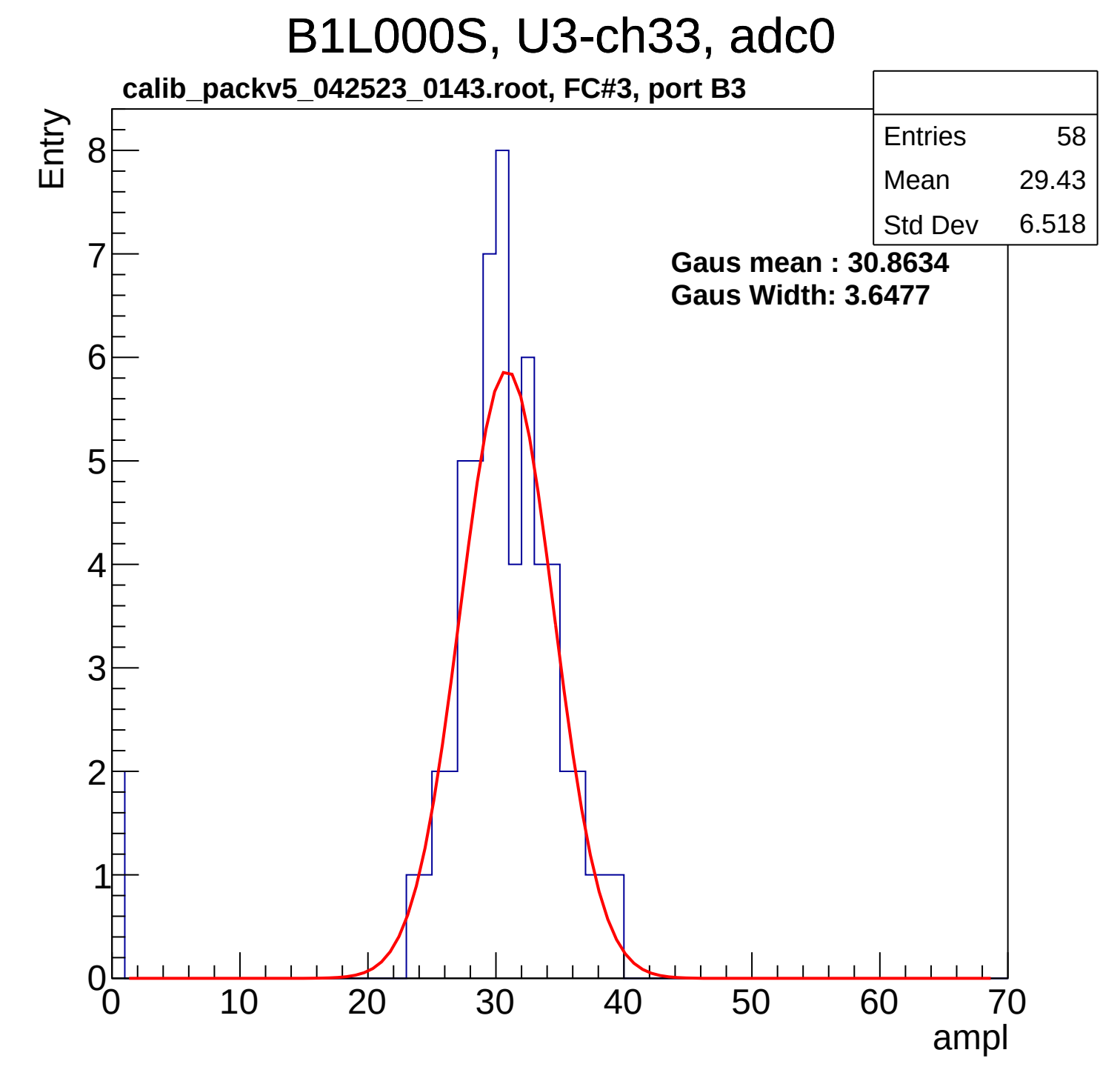
B1L000S, U3-ch33, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 29.43
Std Dev 6.518
Gaus mean : 30.8634
Gaus Width: 3.6477
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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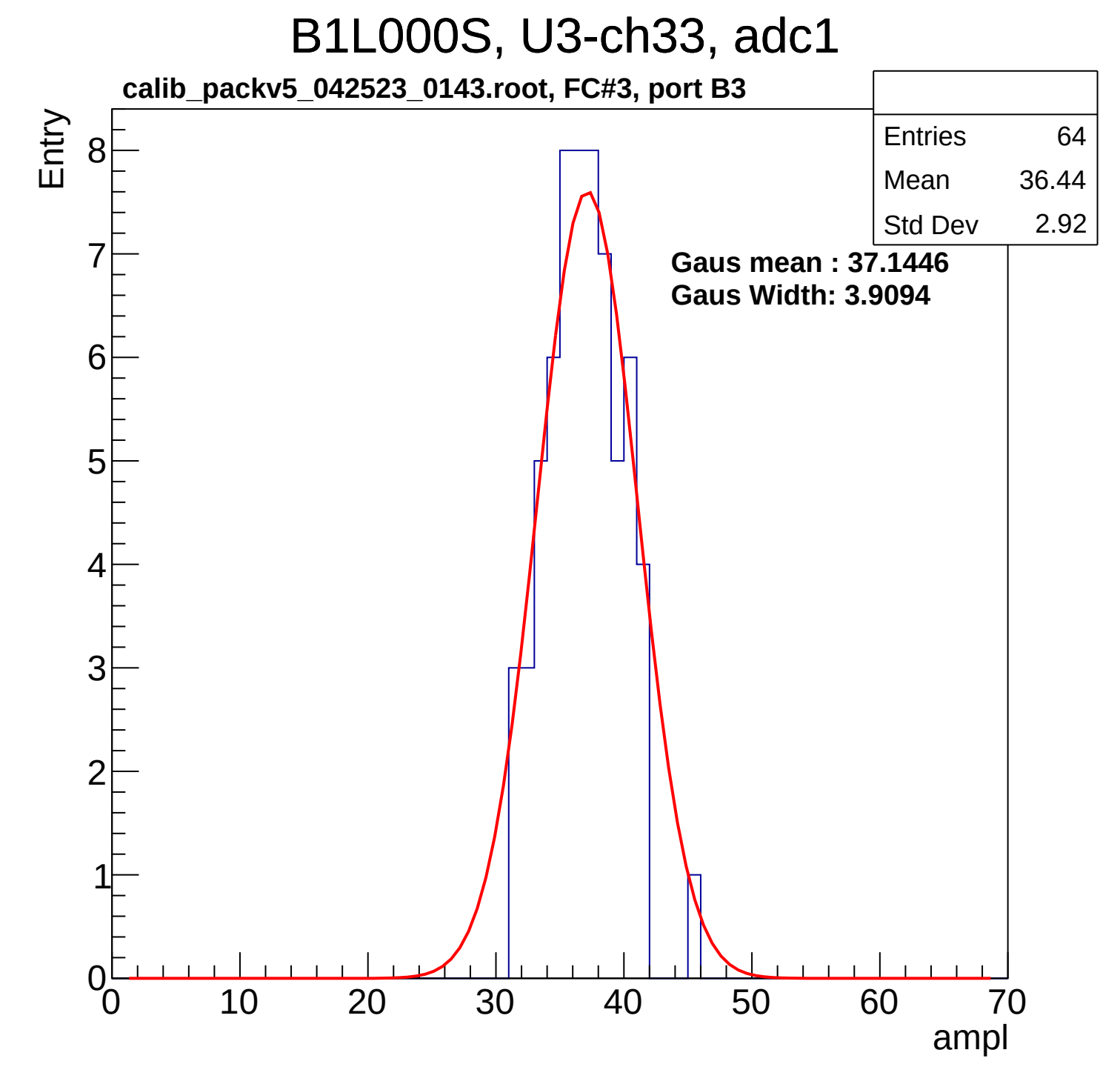
B1L000S, U3-ch33, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 36.44
Std Dev 2.92
Gaus mean : 37.1446
Gaus Width: 3.9094
Entry
ampl
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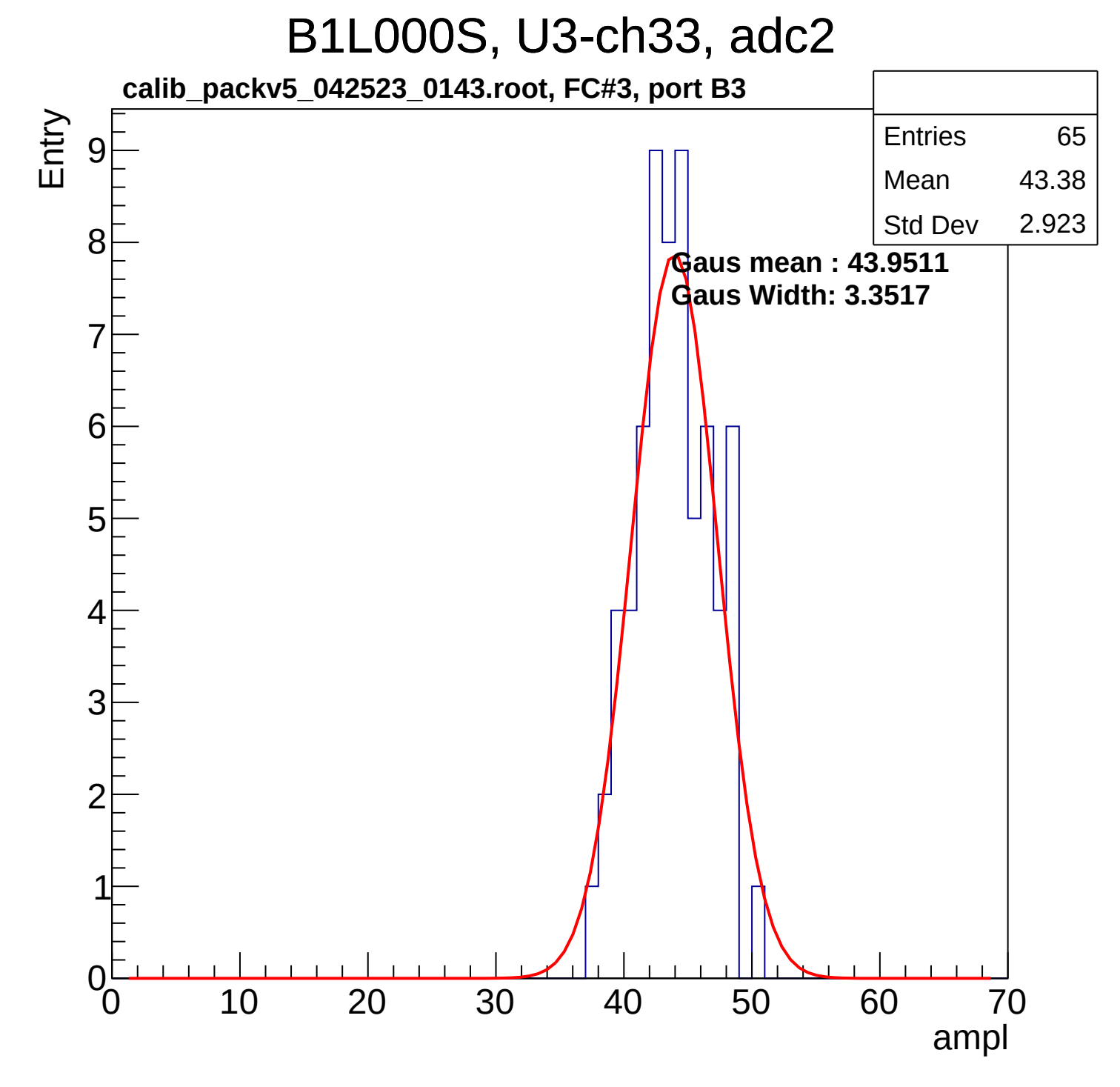
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch33, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 43.38
Std Dev 2.923
Gaus mean : 43.9511
Gaus Width: 3.3517
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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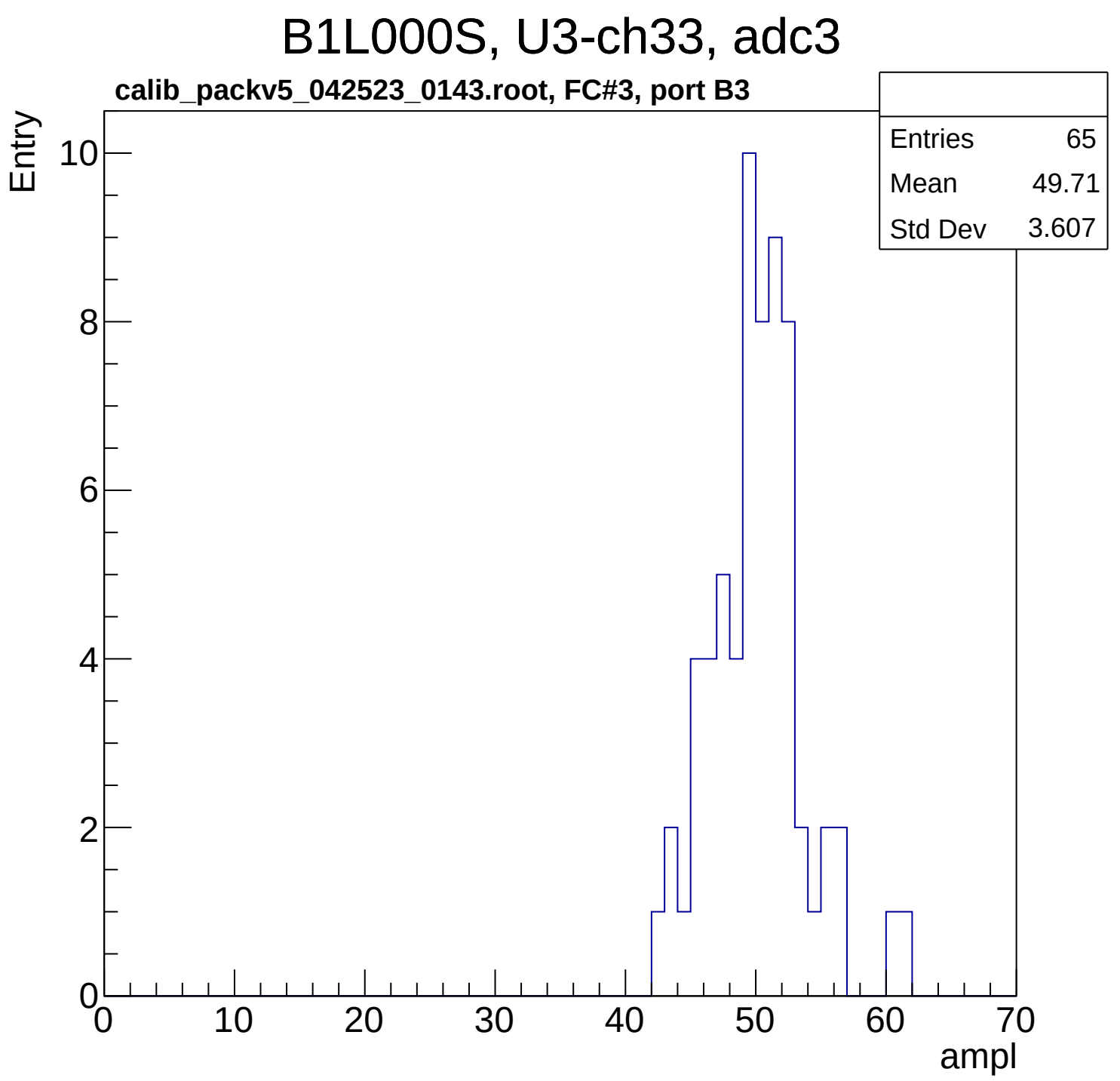
B1L000S, U3-ch33, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 49.71
Std Dev 3.607
Entry
ampl
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2
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0
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60
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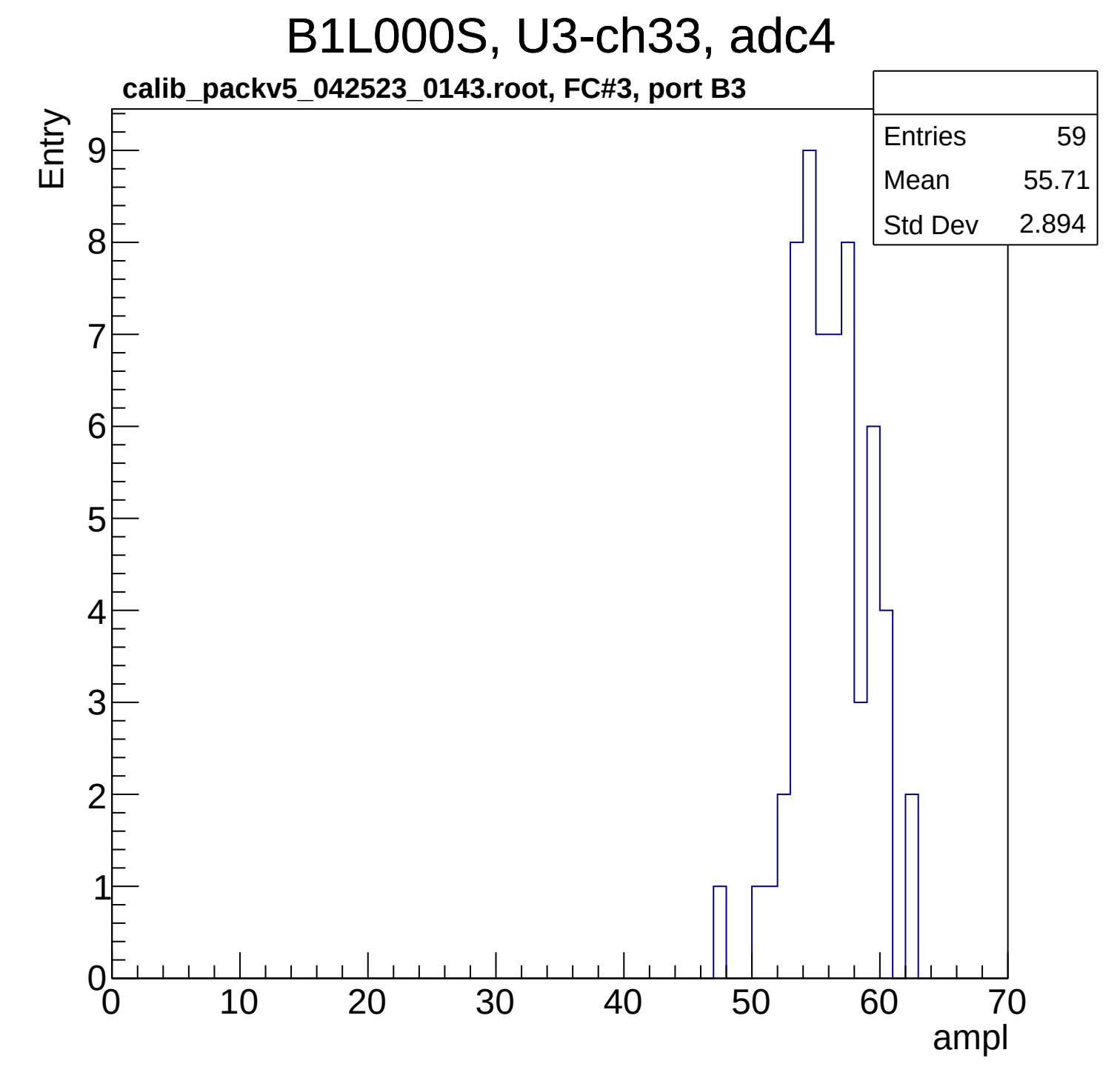
B1L000S, U3-ch33, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 55.71
Std Dev 2.894
Entry
ampl
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6
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0
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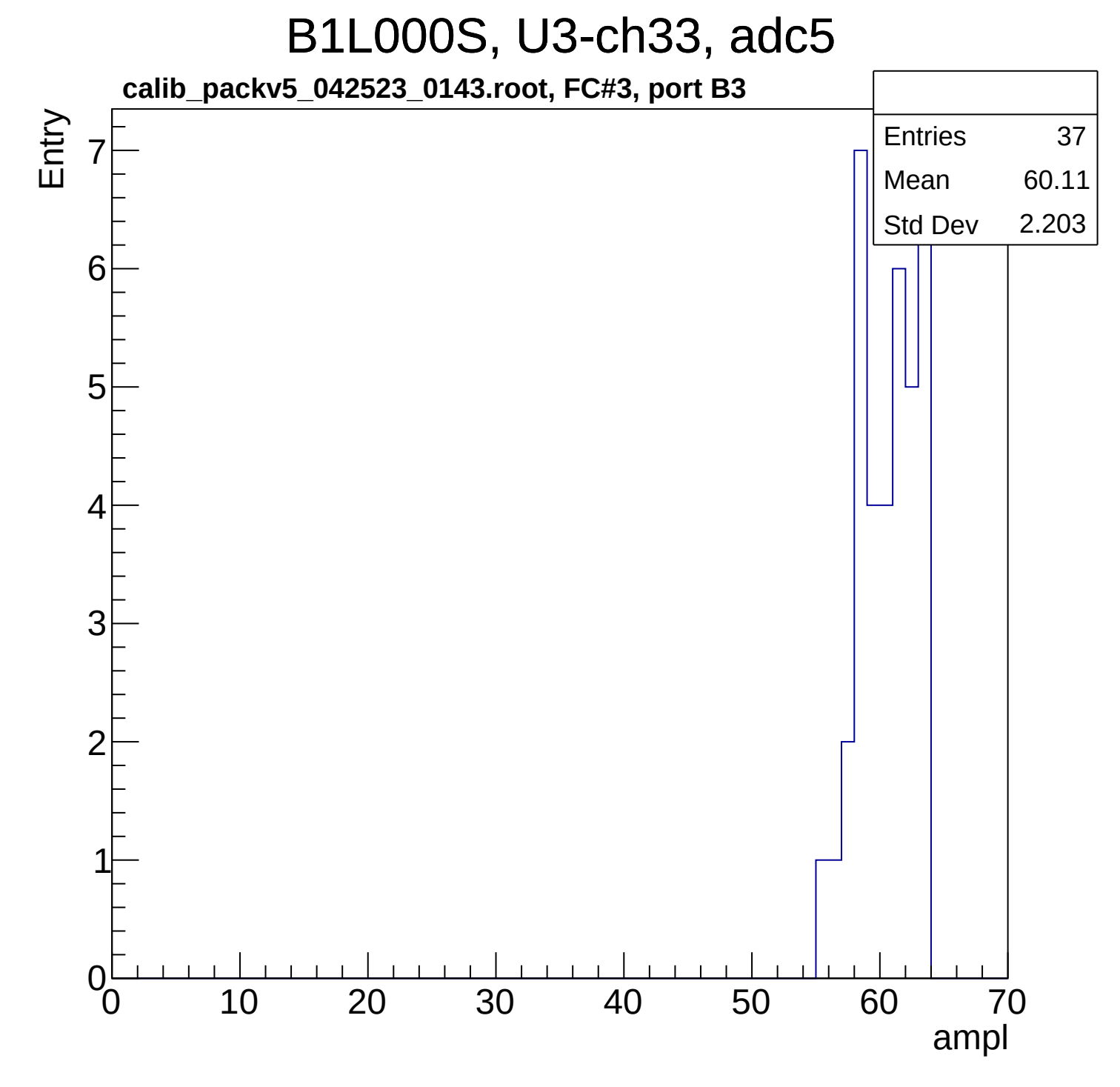
B1L000S, U3-ch33, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 60.11
Std Dev 2.203
Entry
ampl
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3
4
5
6
7
0
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60
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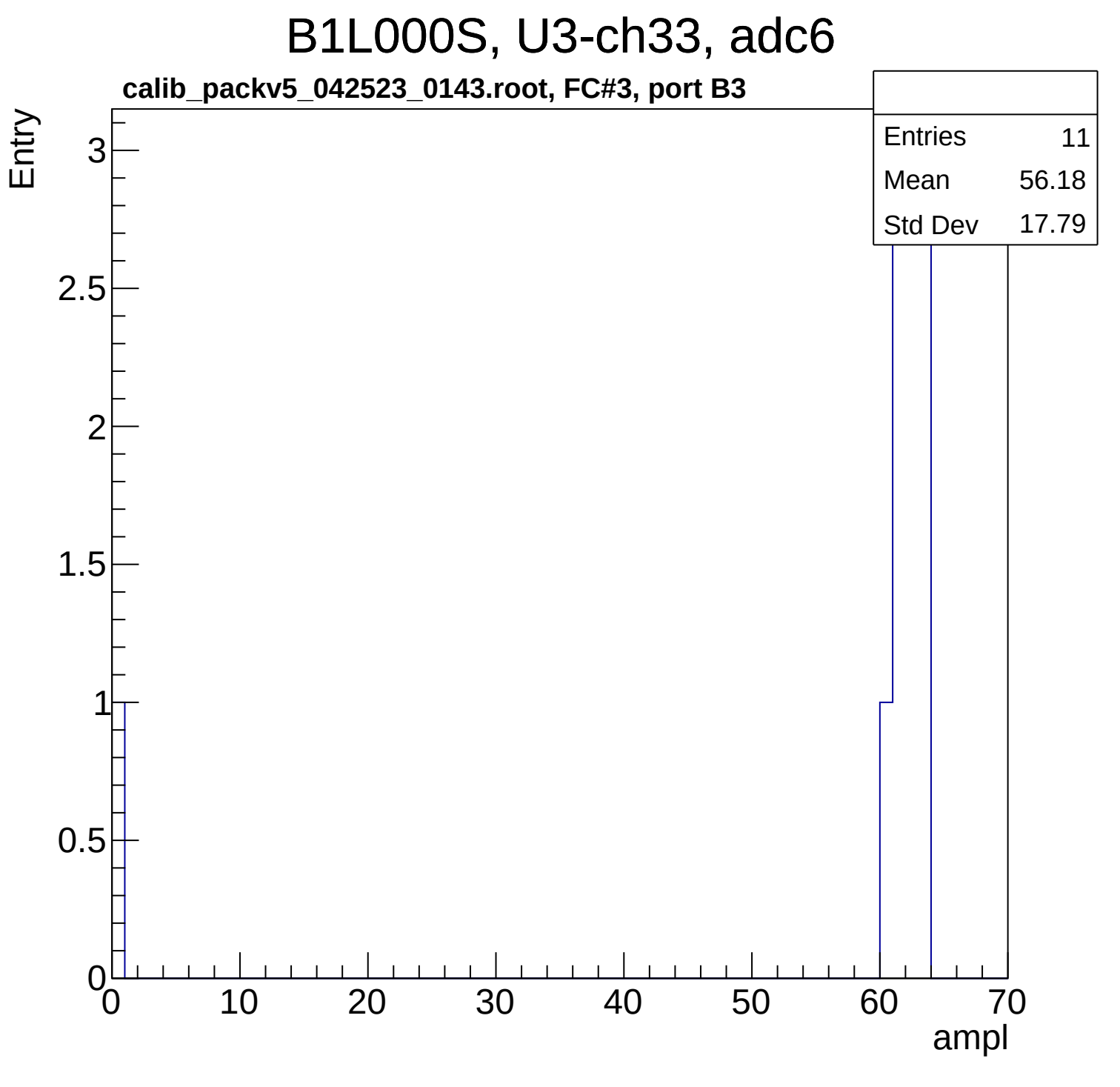
B1L000S, U3-ch33, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 56.18
Std Dev 17.79
Entry
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1
1.5
2
2.5
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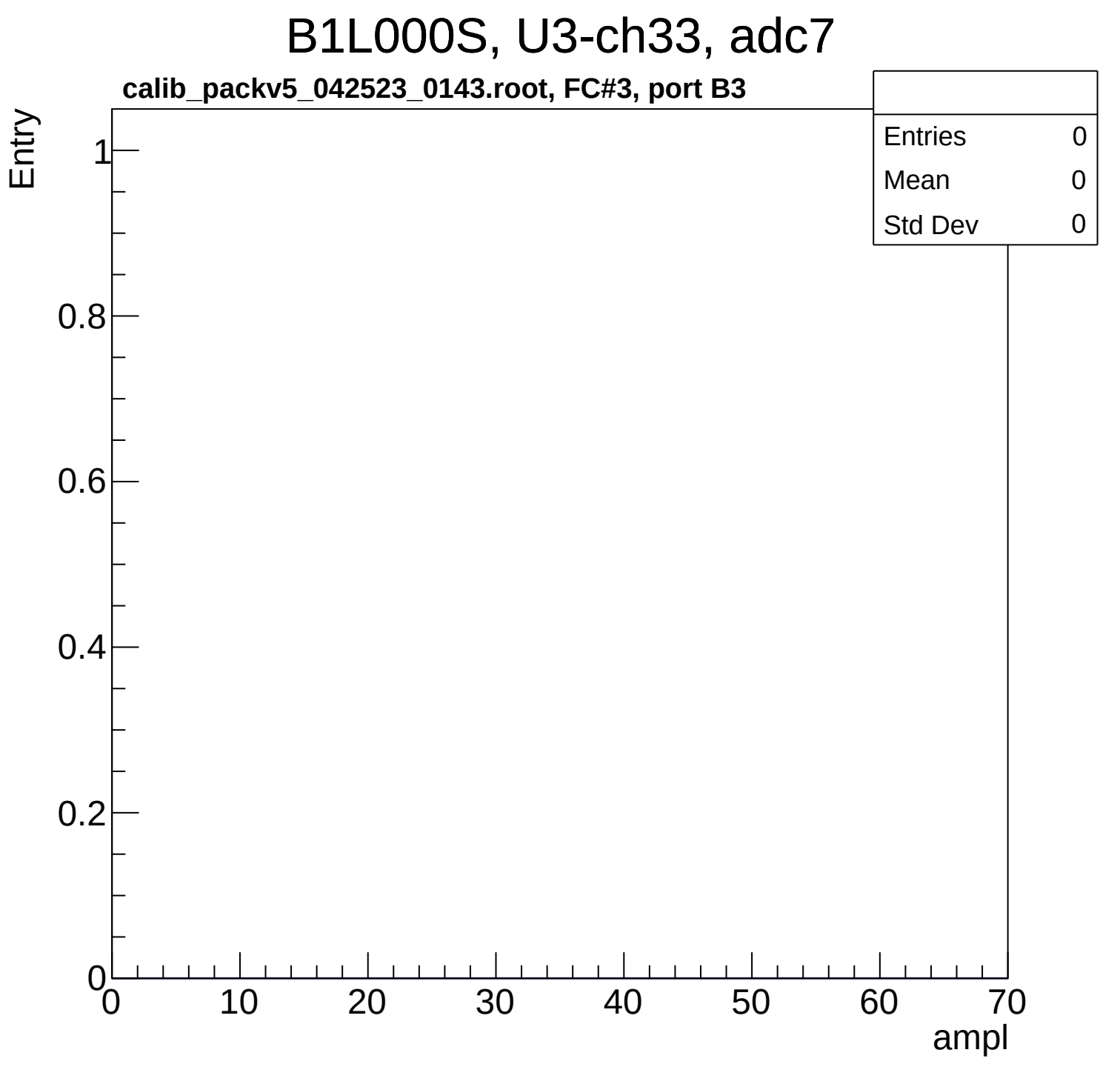
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch33, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
10
20
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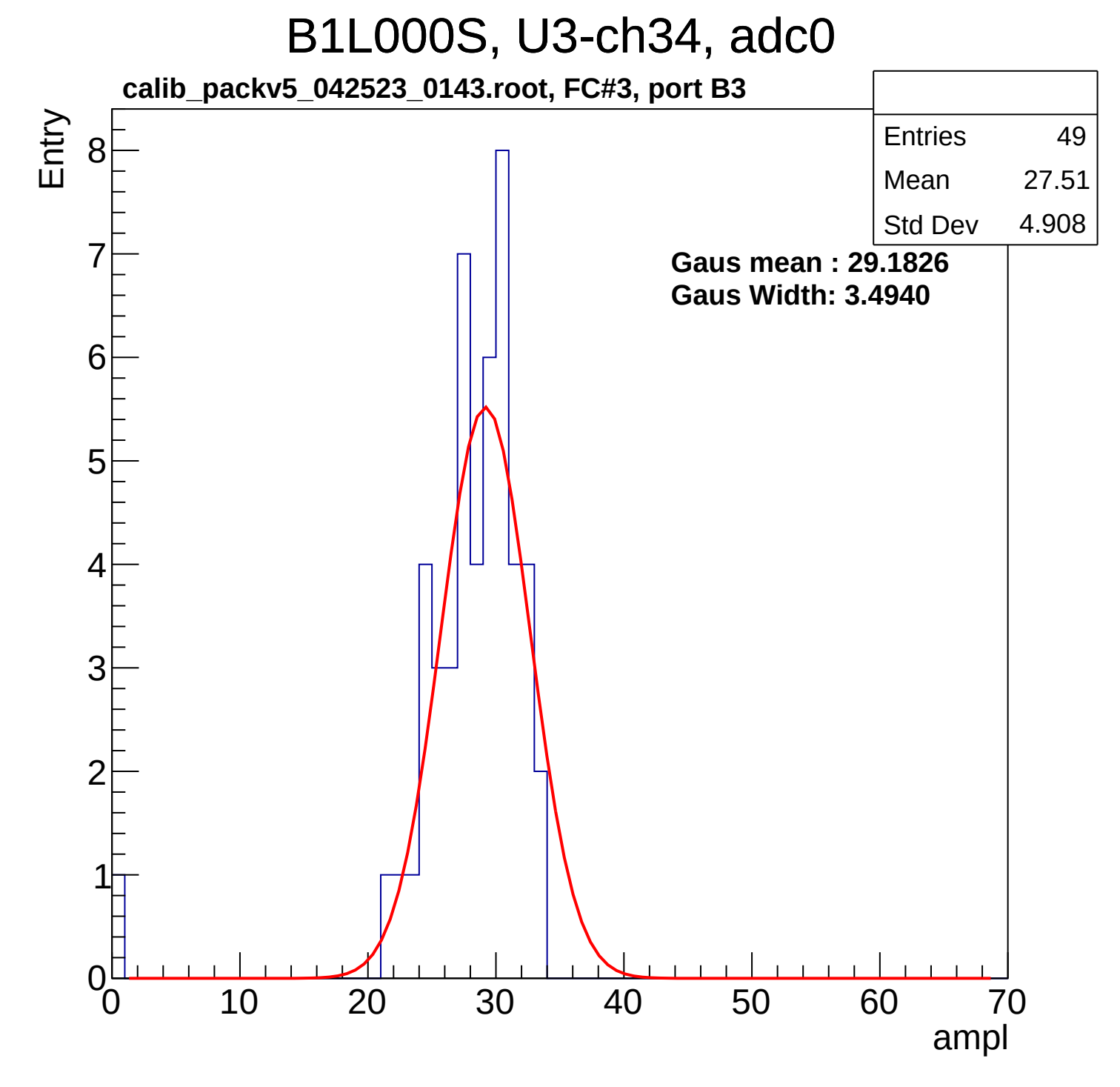
40
50
60
70
ampl


B1L000S, U3-ch34, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 27.51
Std Dev 4.908
Gaus mean : 29.1826
Gaus Width: 3.4940
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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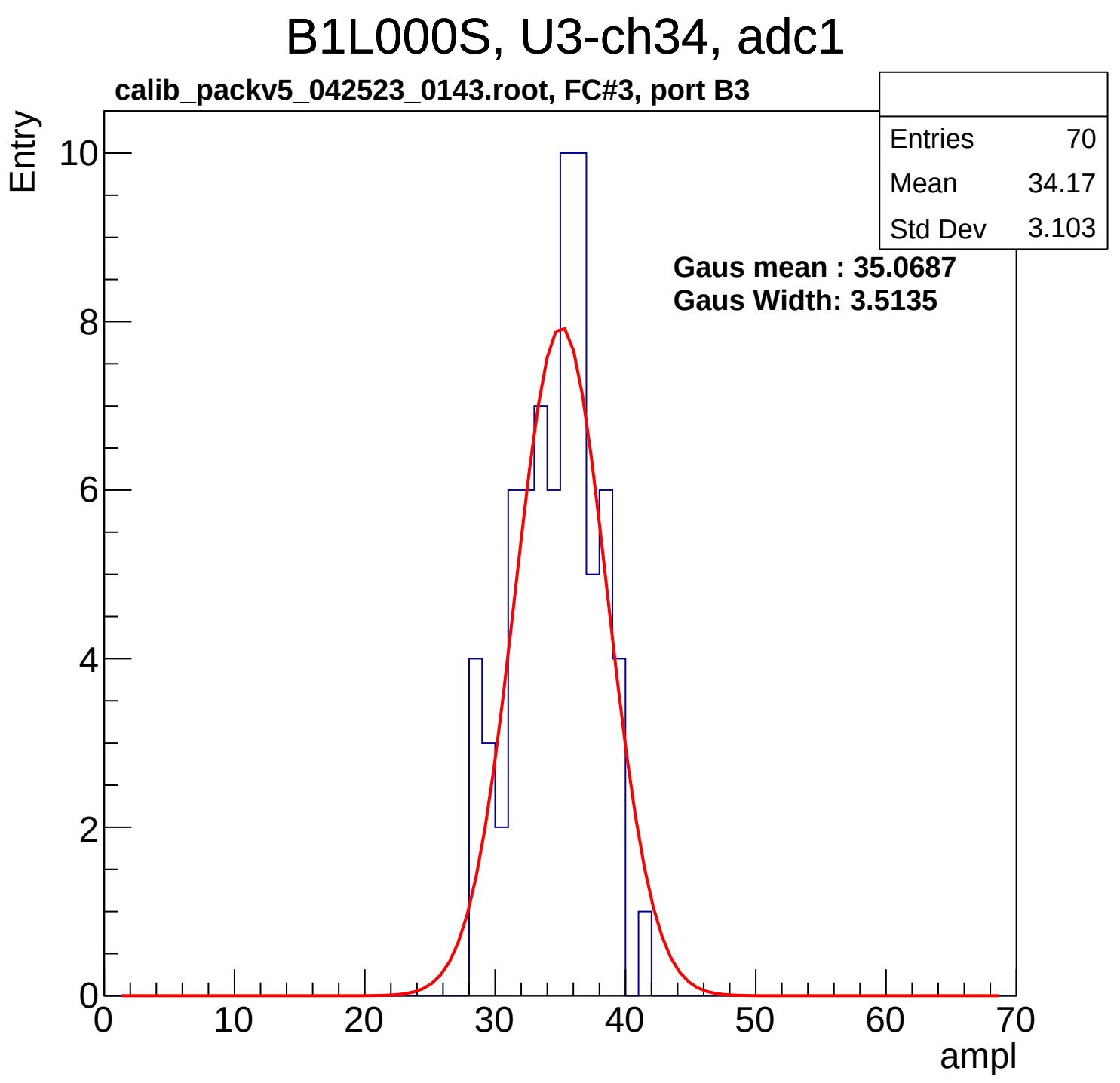
B1L000S, U3-ch34, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 34.17
Std Dev 3.103
Gaus mean : 35.0687
Gaus Width: 3.5135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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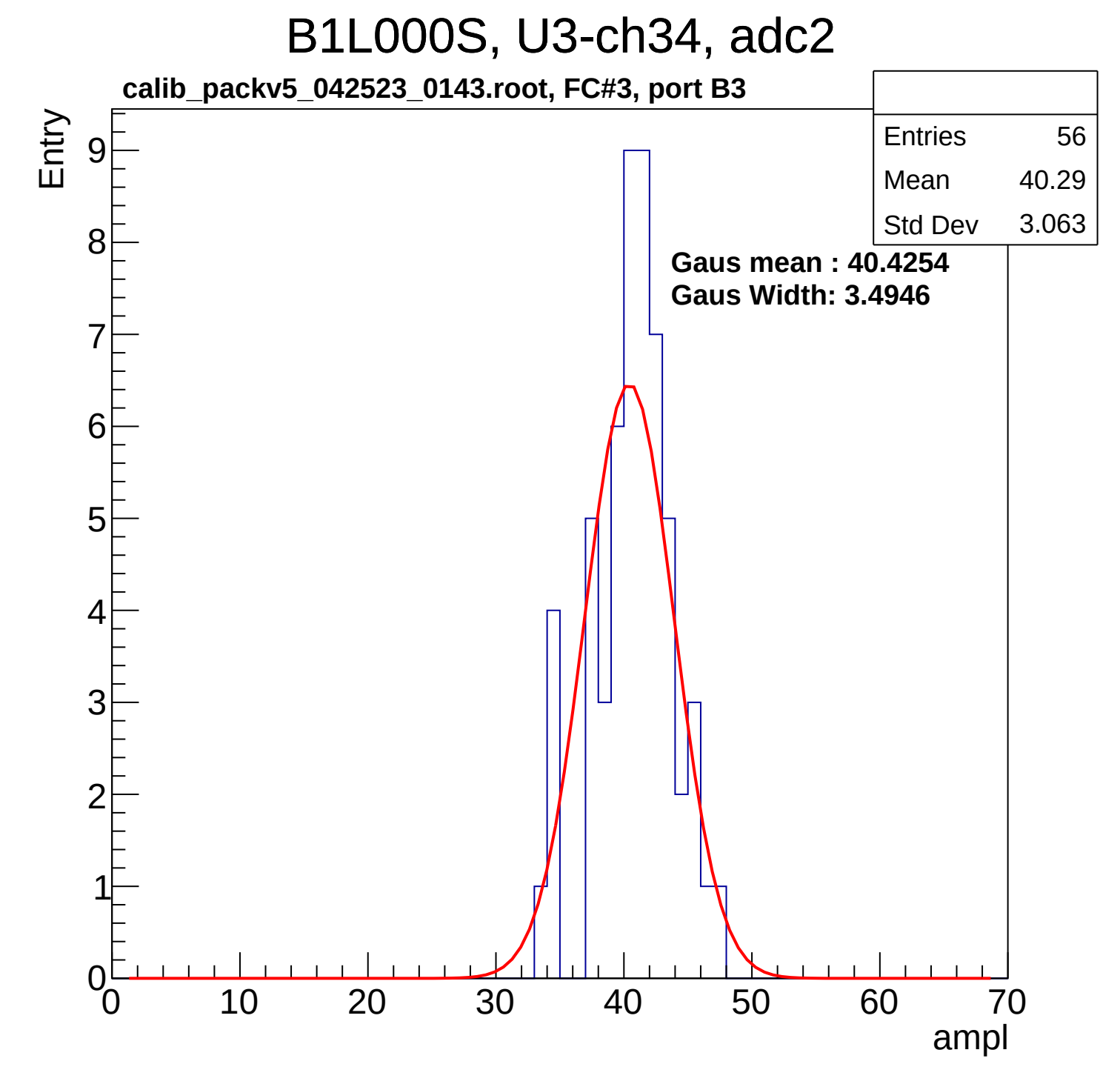
B1L000S, U3-ch34, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 40.29
Std Dev 3.063
Gaus mean : 40.4254
Gaus Width: 3.4946
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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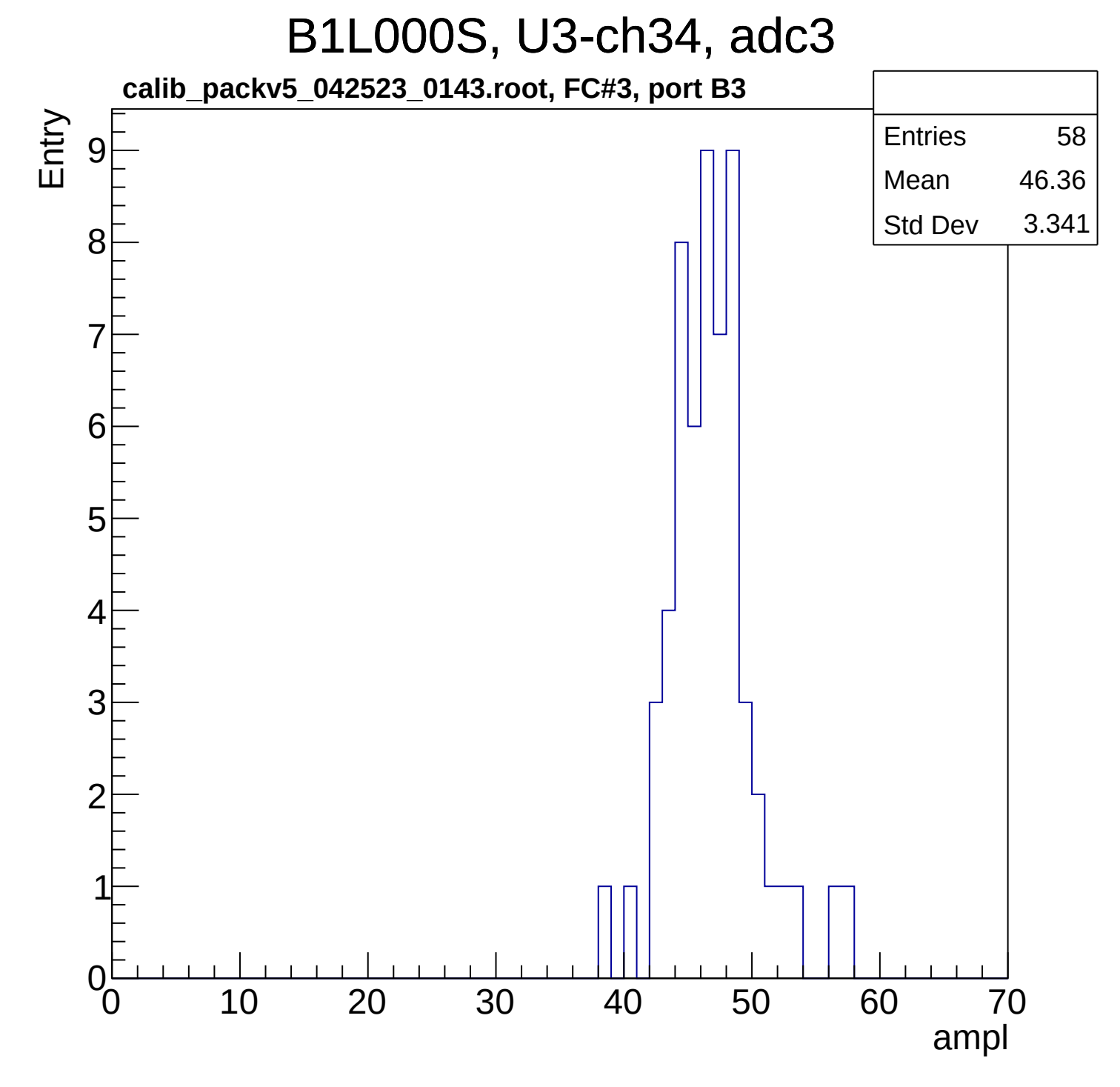
B1L000S, U3-ch34, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 46.36
Std Dev 3.341
Entry
ampl
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2
3
4
5
6
7
8
9
0
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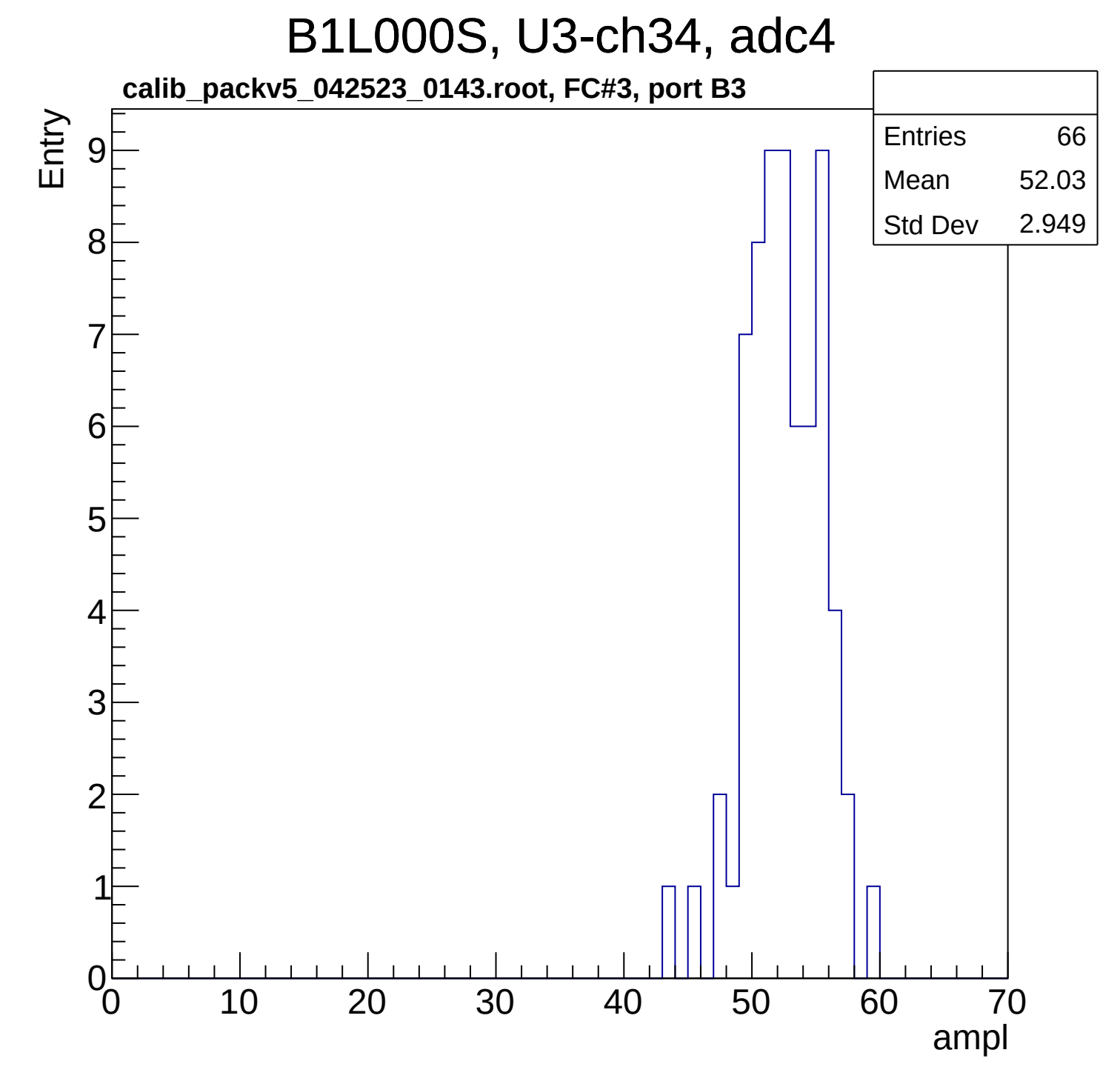
B1L000S, U3-ch34, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 52.03
Std Dev 2.949
Entry
ampl
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3
4
5
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7
8
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0
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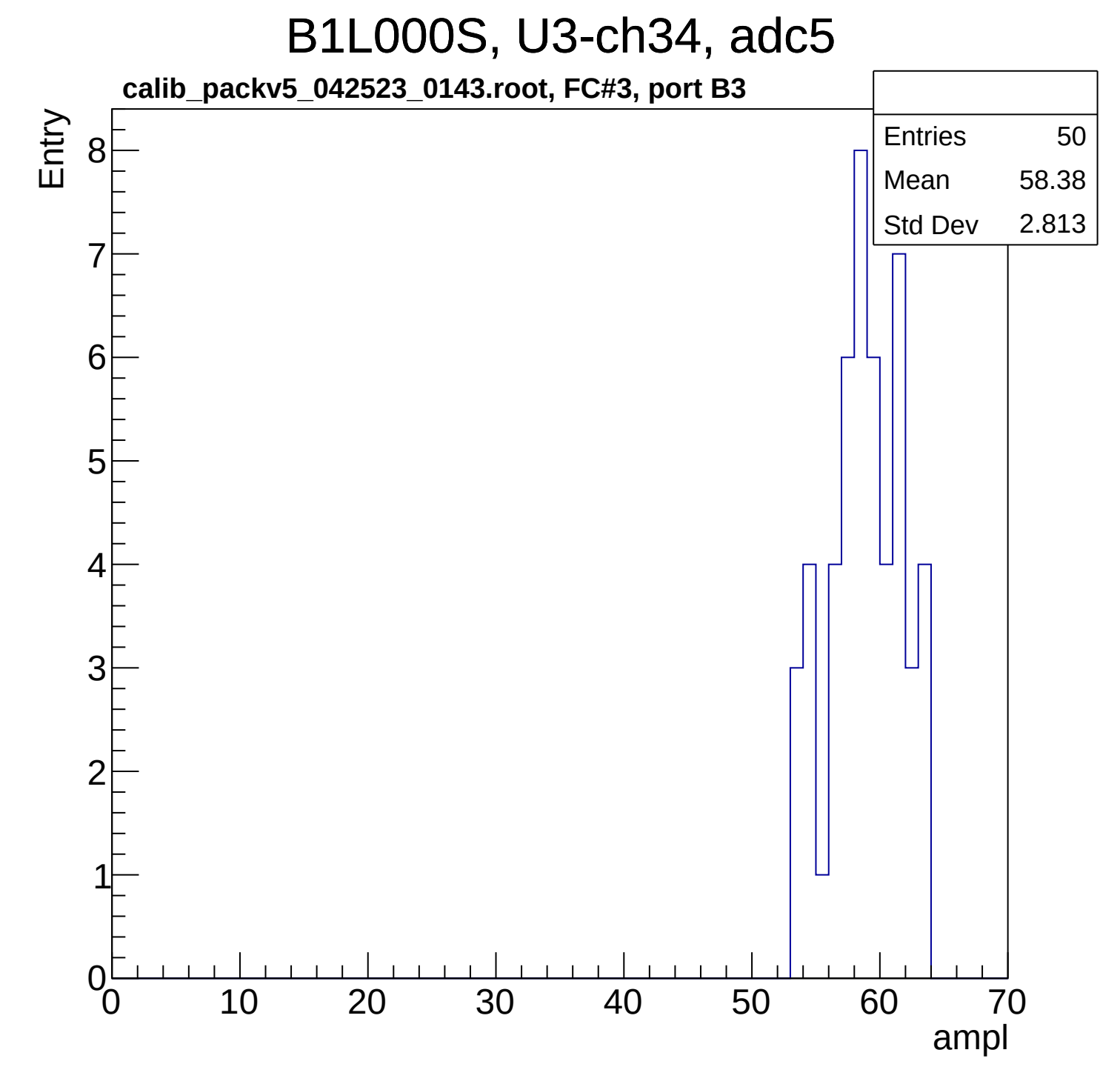
B1L000S, U3-ch34, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.38
Std Dev 2.813
Entry
ampl
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2
3
4
5
6
7
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0
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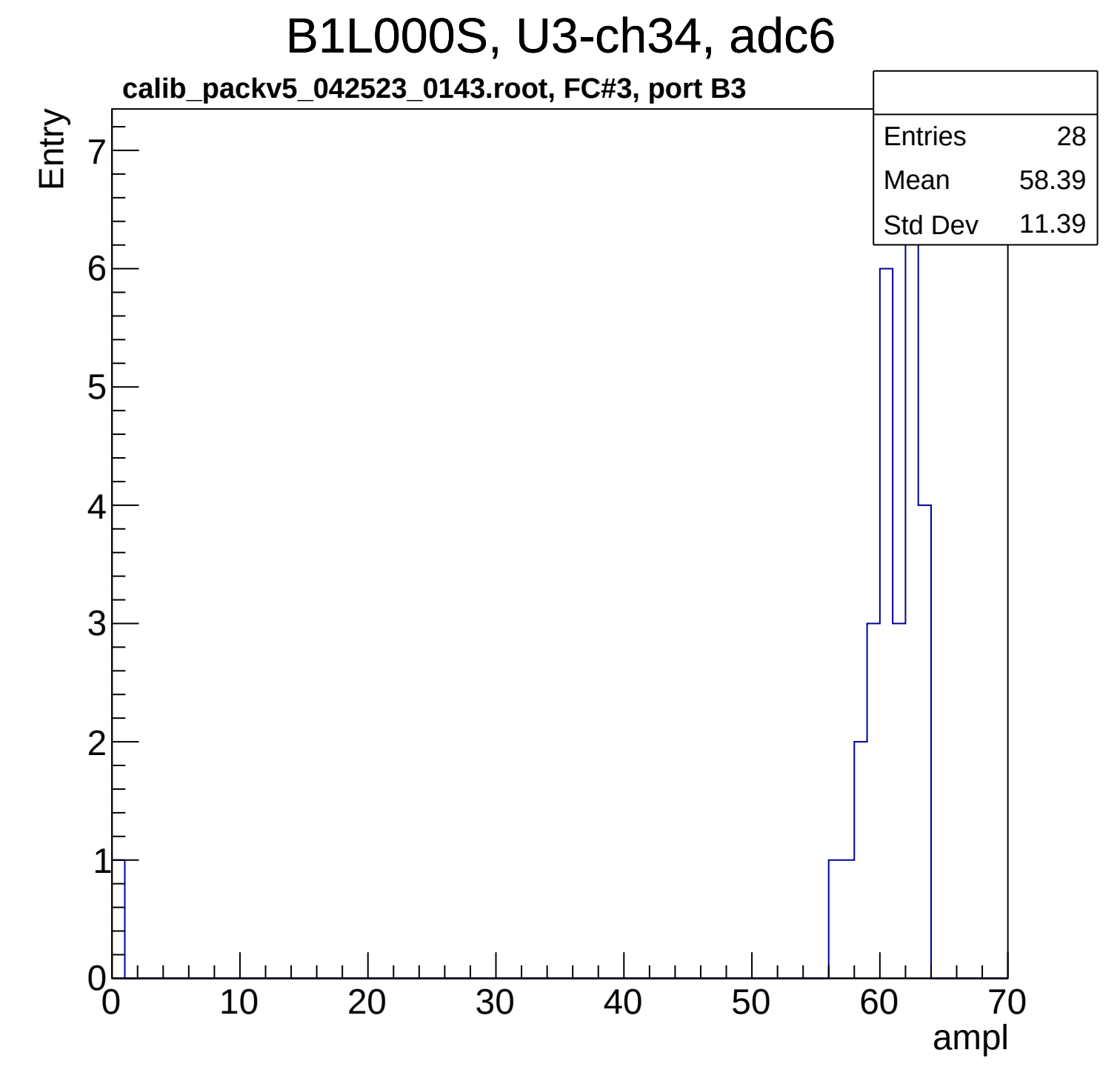
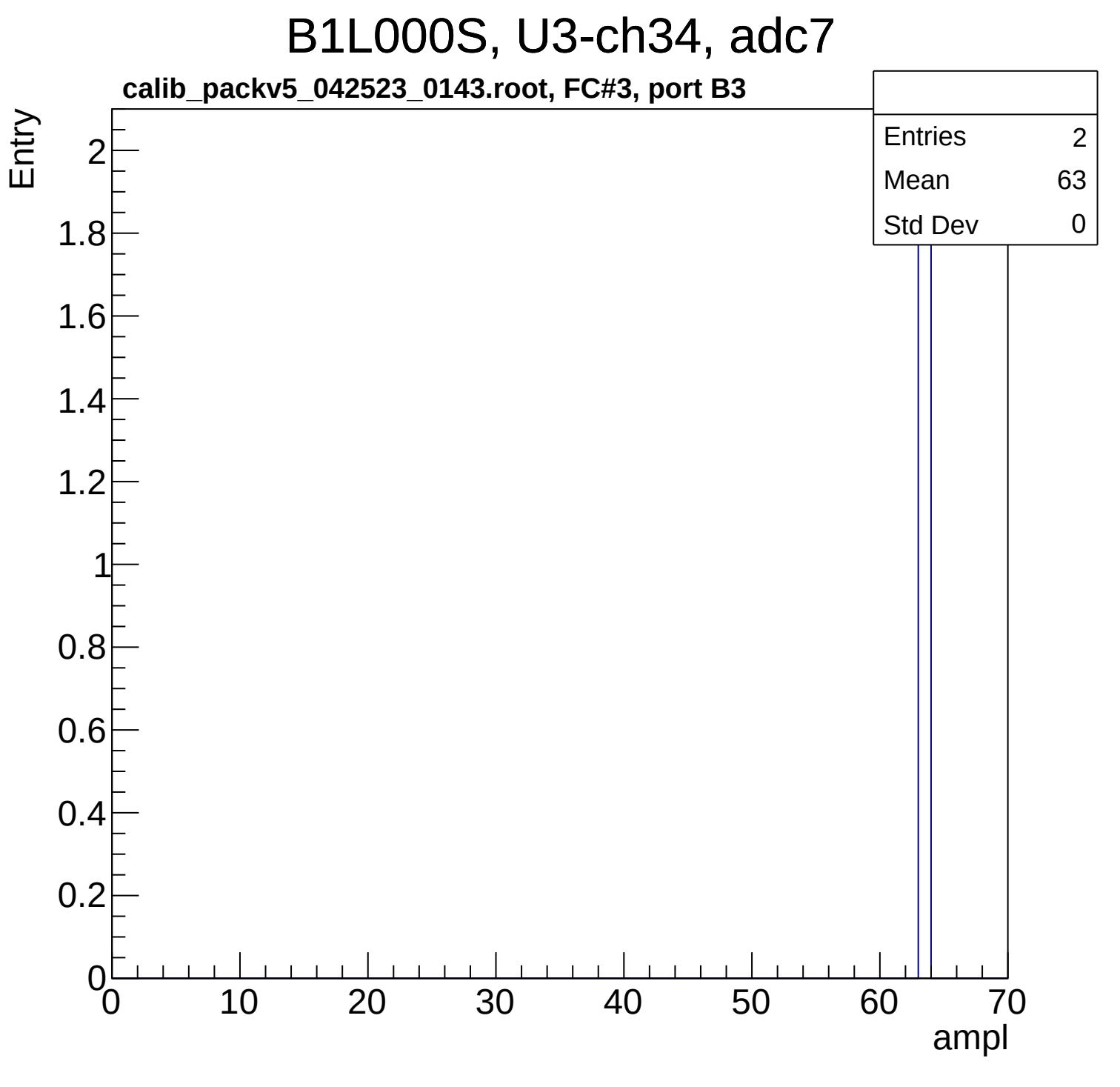

B1L000S, U3-ch34, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 28
Mean 58.39
Std Dev 11.39
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch34, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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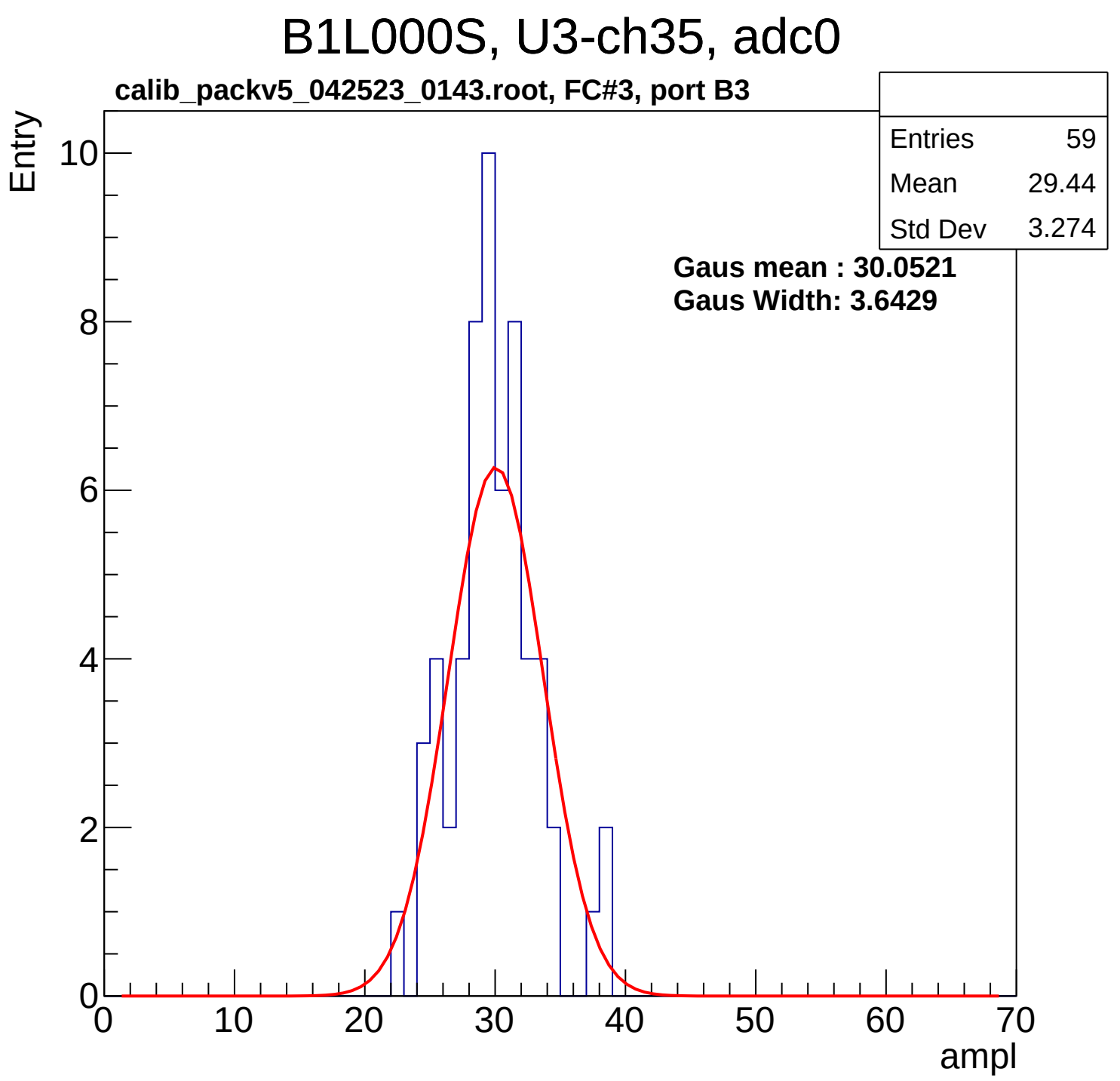
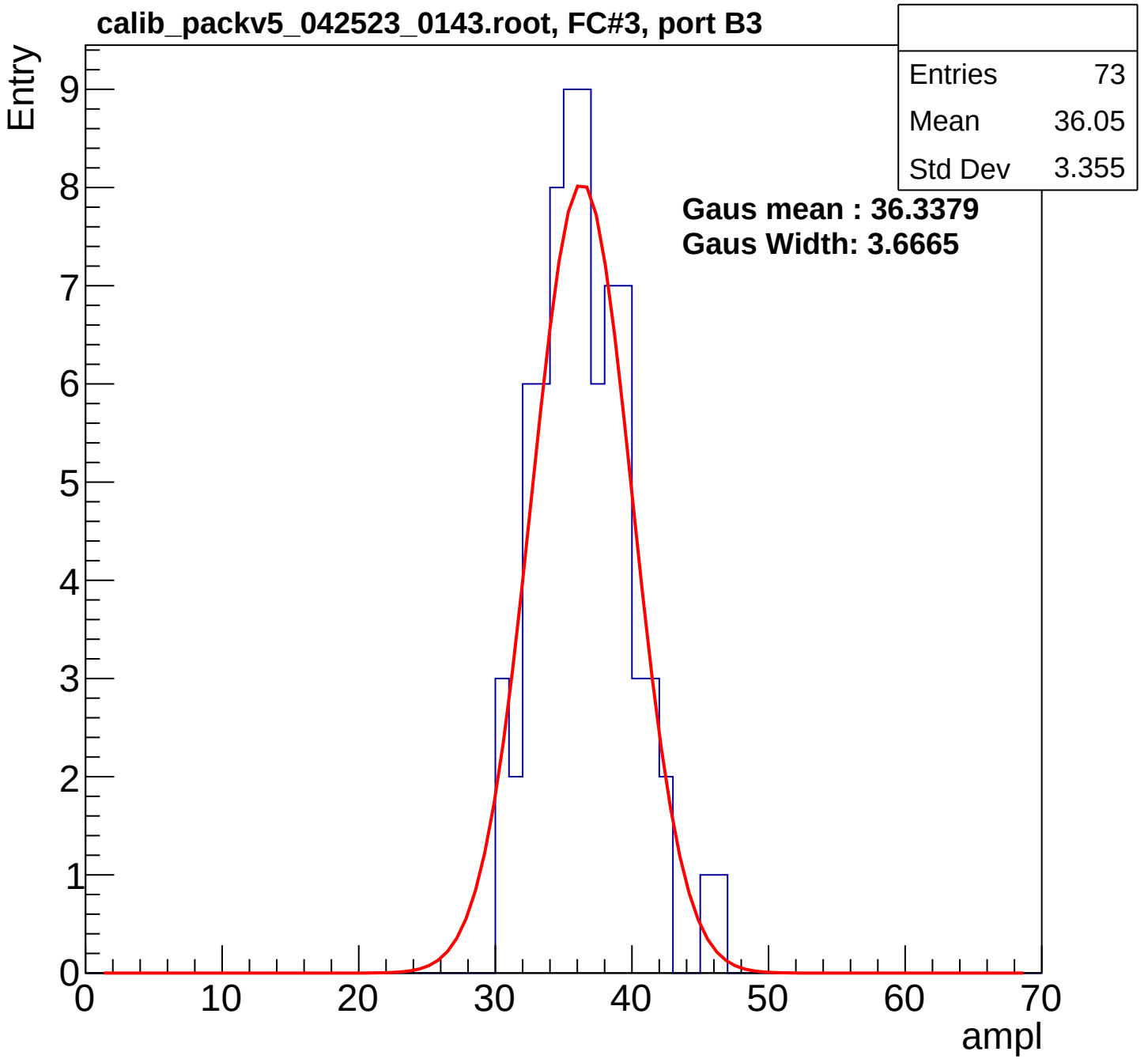

B1L000S, U3-ch35, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 29.44
Std Dev 3.274
Gaus mean : 30.0521
Gaus Width: 3.6429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch35, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 36.05
Std Dev 3.355
Gaus mean : 36.3379
Gaus Width: 3.6665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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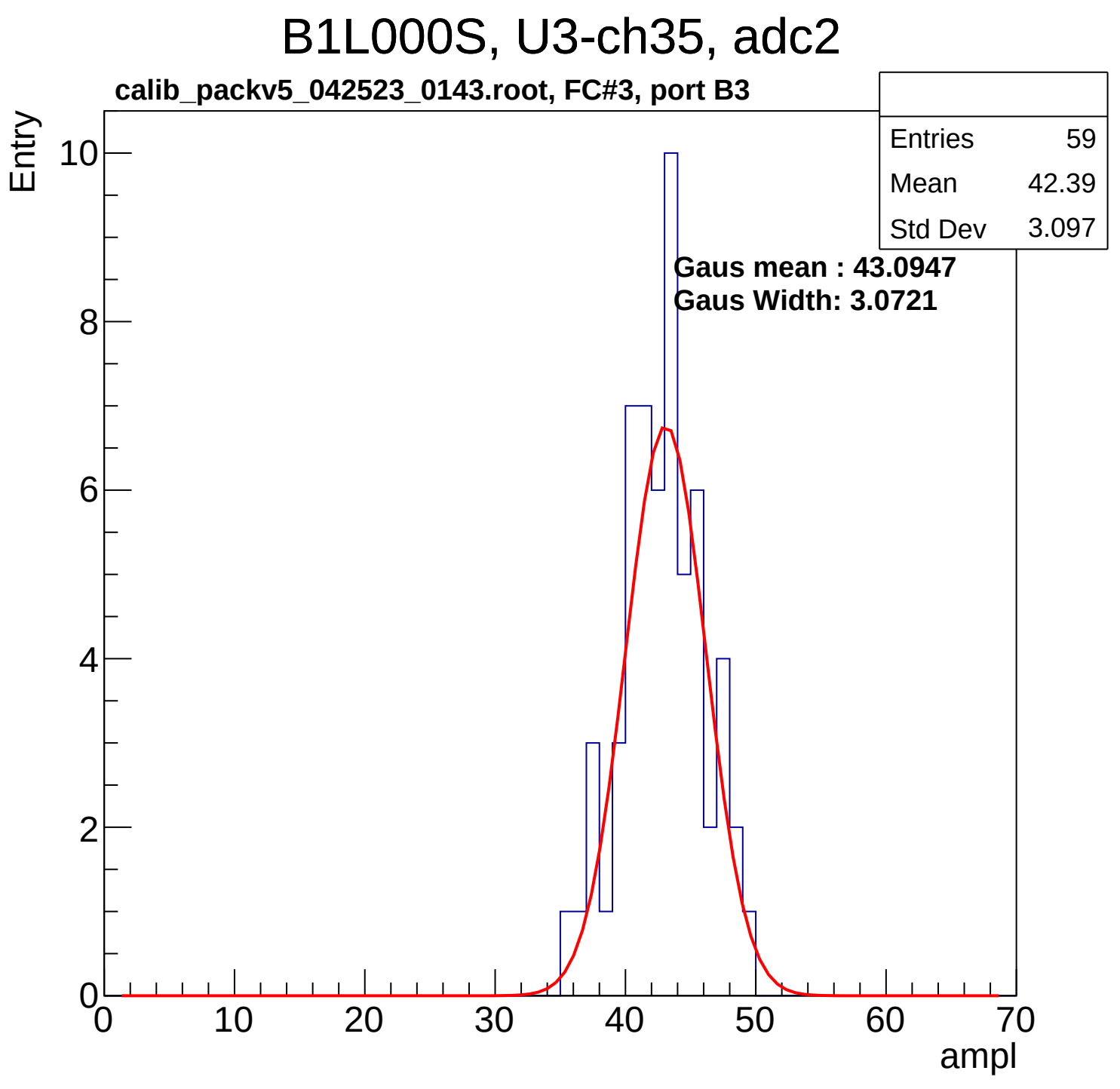
B1L000S, U3-ch35, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 42.39
Std Dev 3.097
Gaus mean : 43.0947
Gaus Width: 3.0721
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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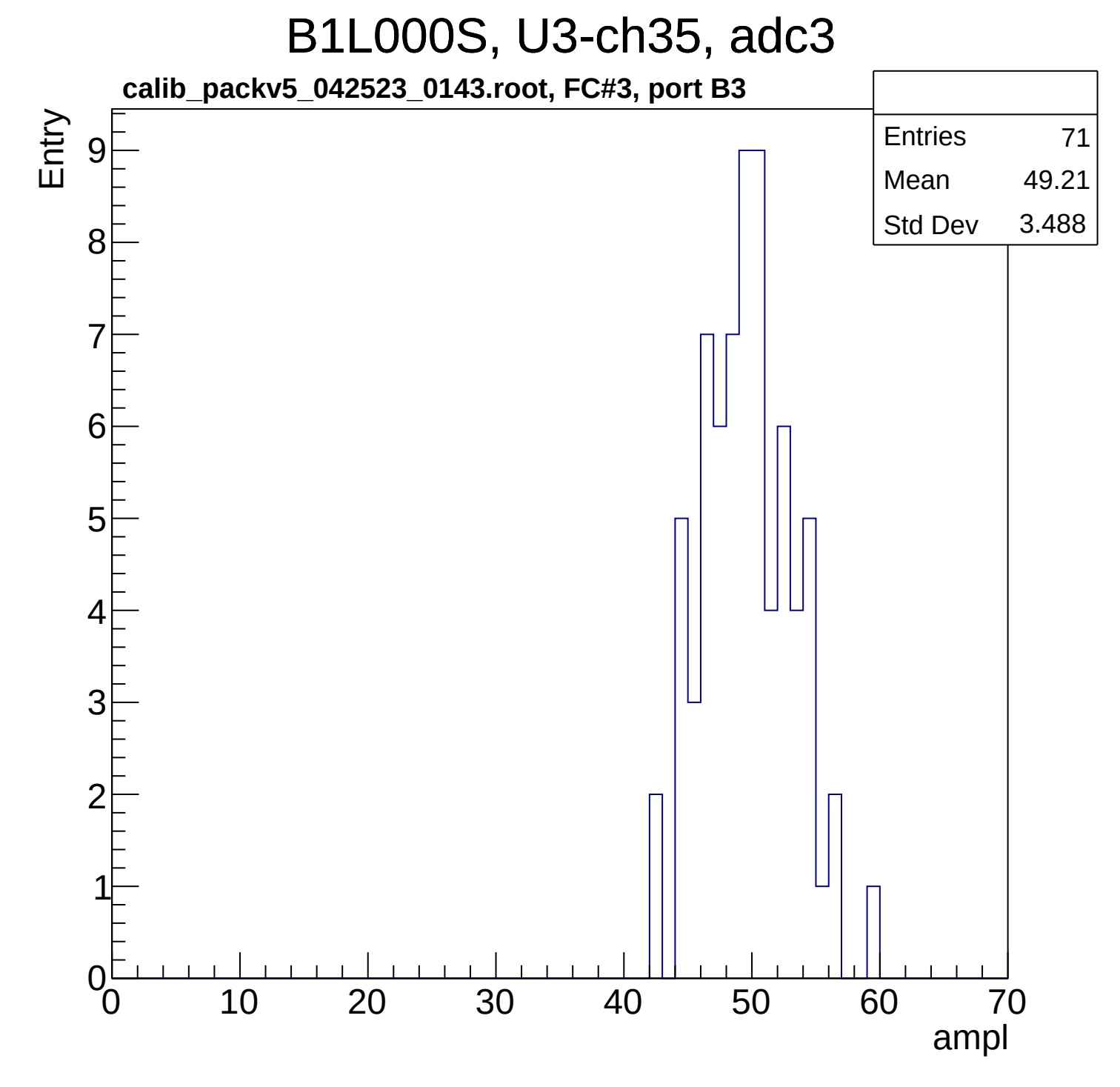
B1L000S, U3-ch35, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 49.21
Std Dev 3.488
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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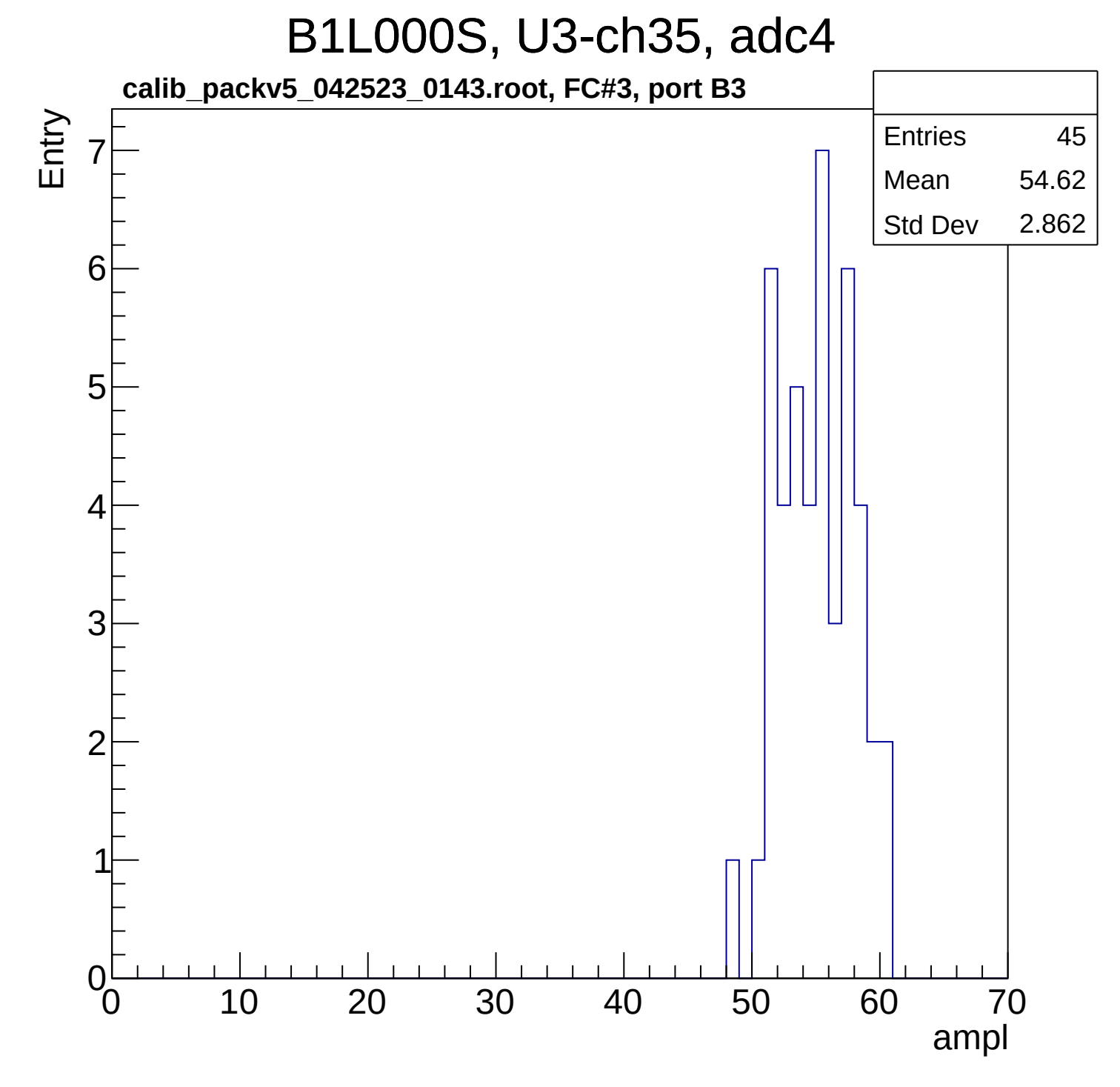
B1L000S, U3-ch35, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 54.62
Std Dev 2.862
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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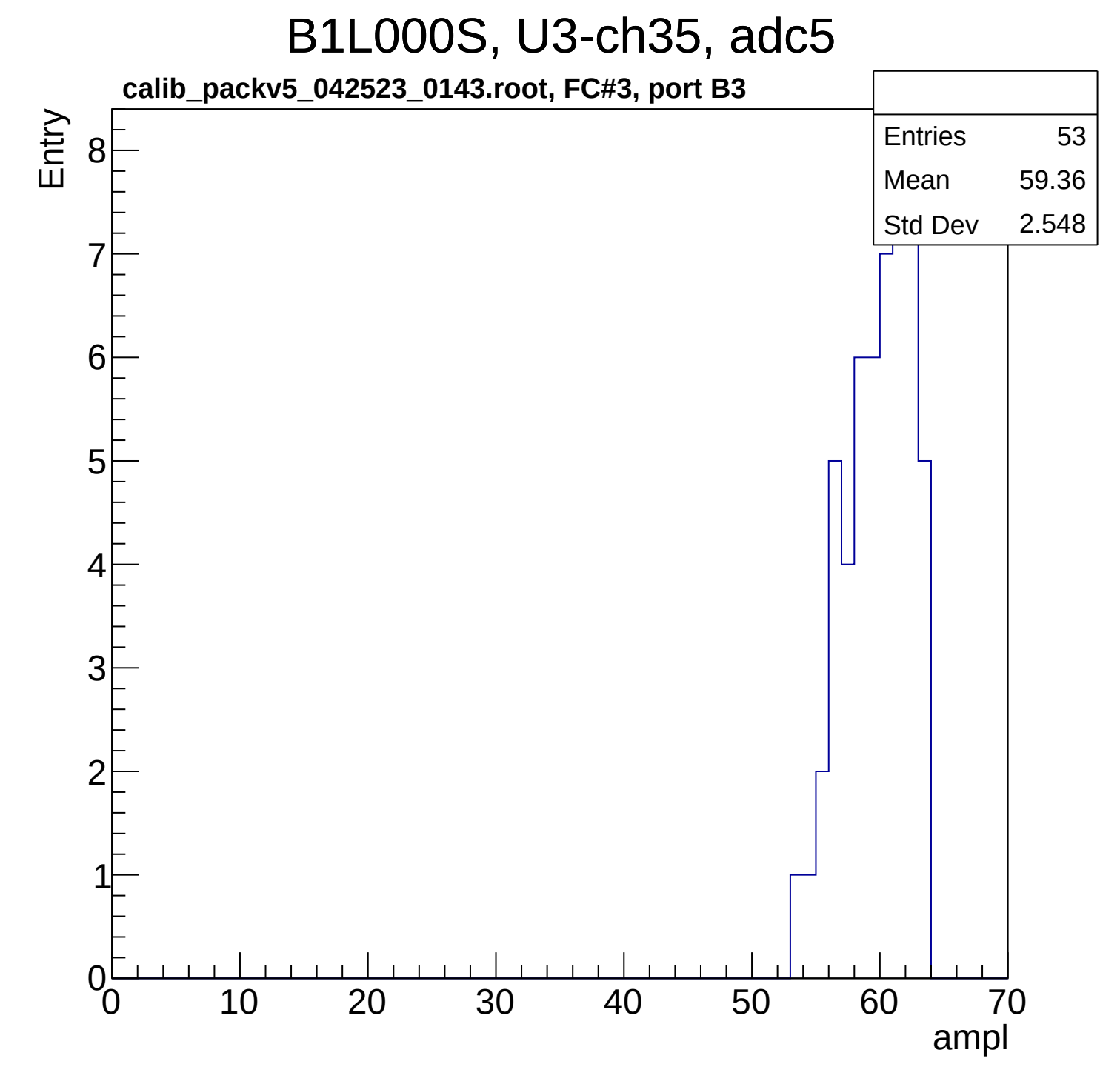
B1L000S, U3-ch35, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 59.36
Std Dev 2.548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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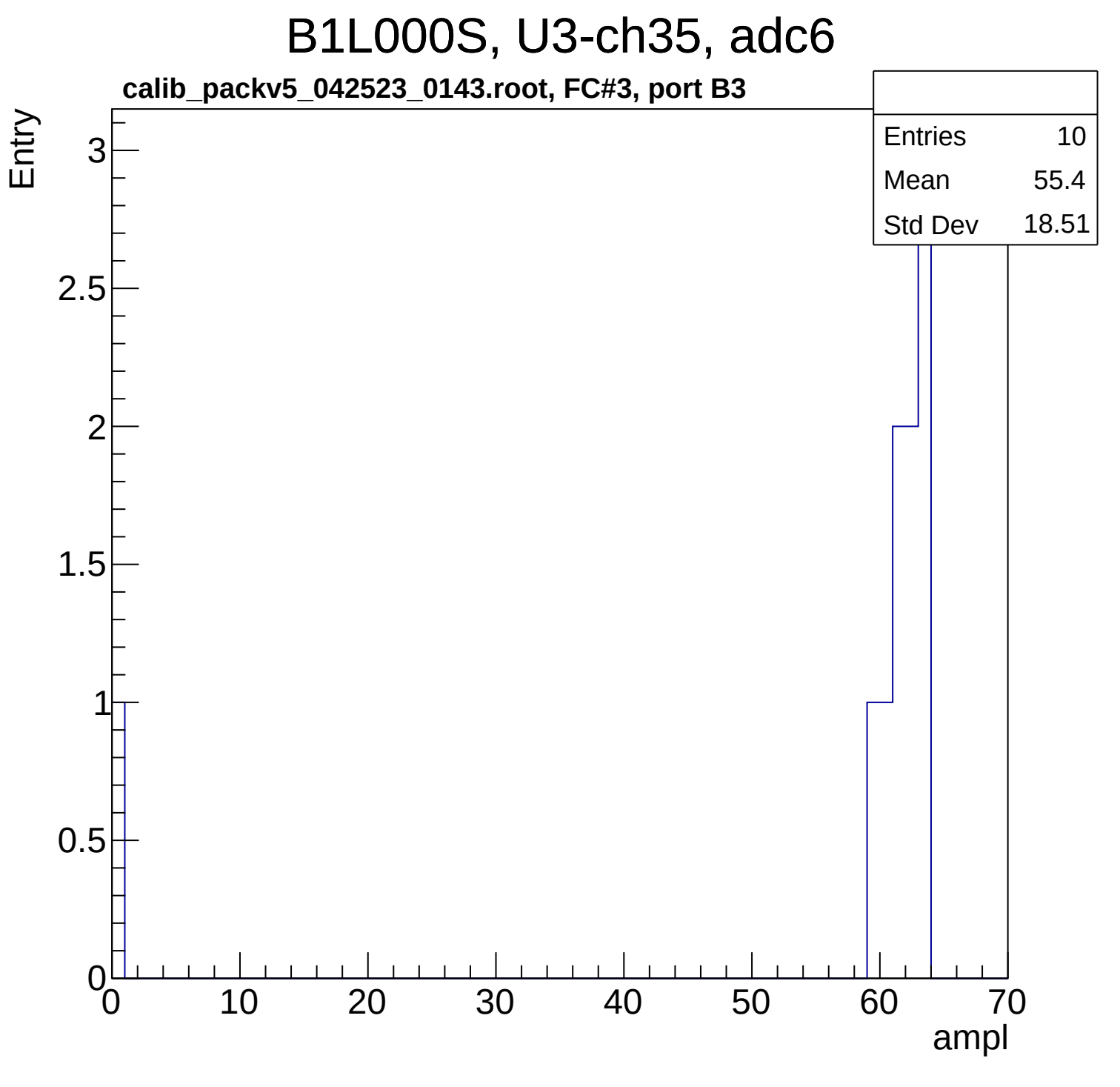
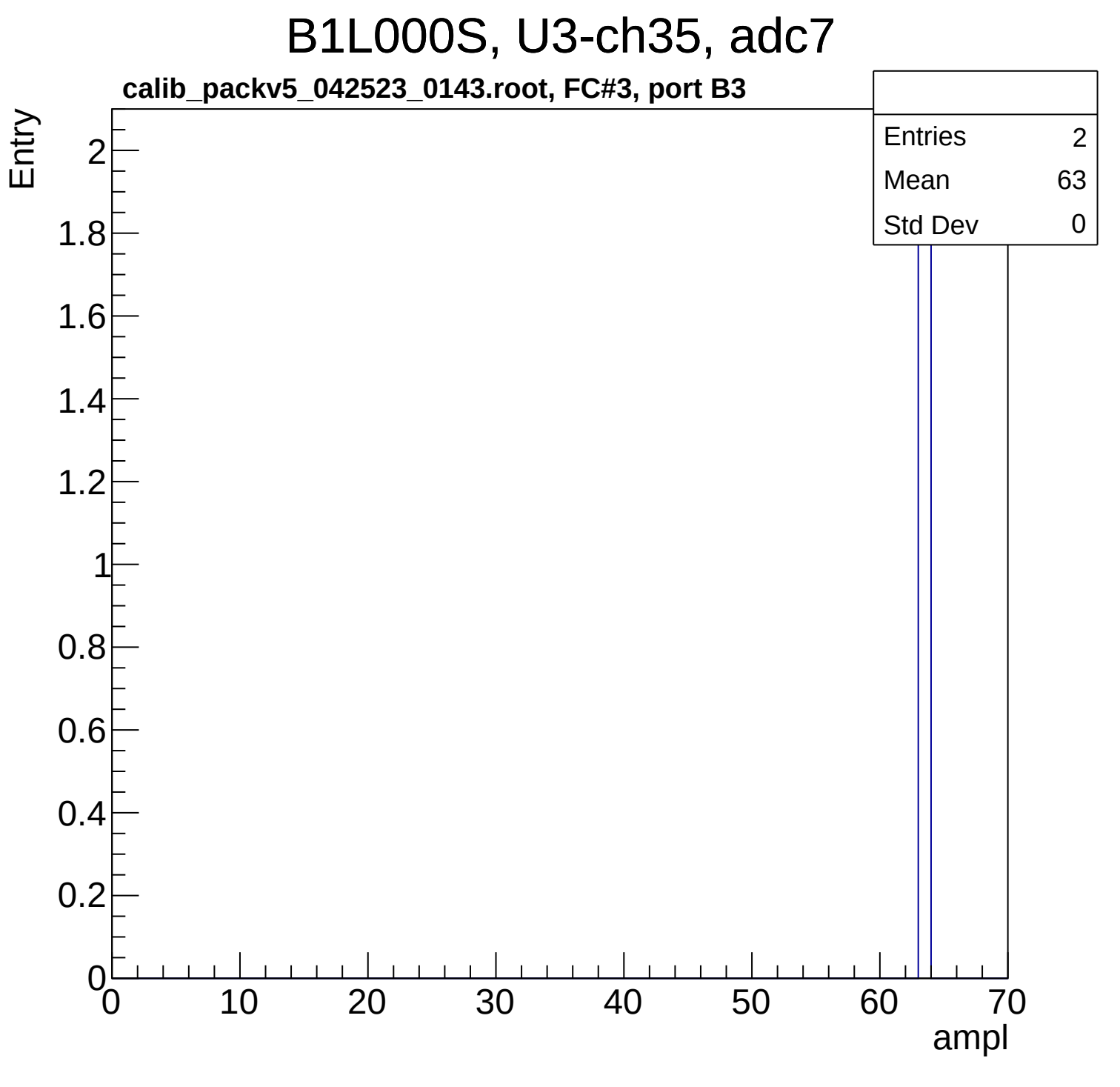

B1L000S, U3-ch35, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 55.4
Std Dev 18.51
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch35, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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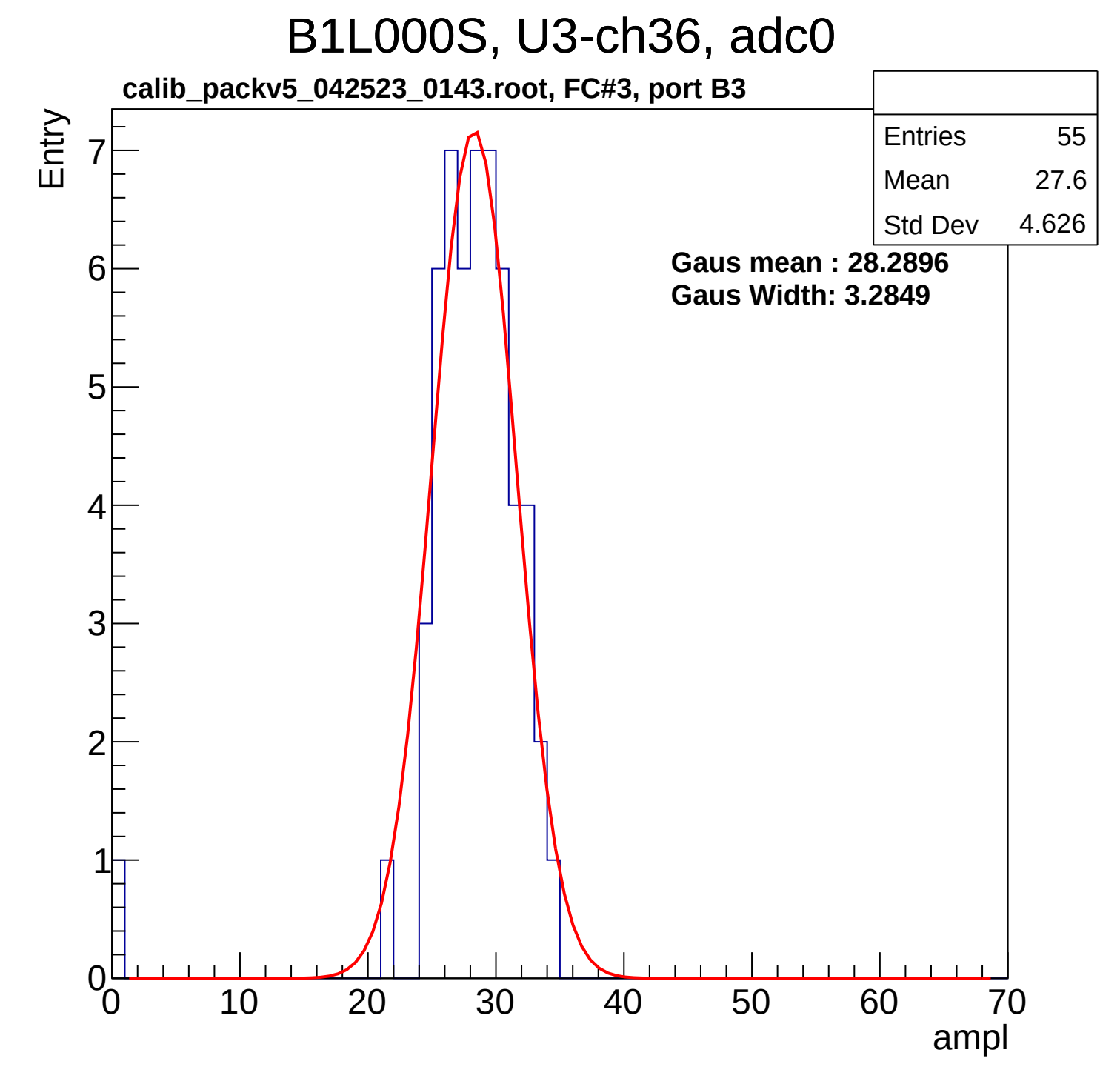
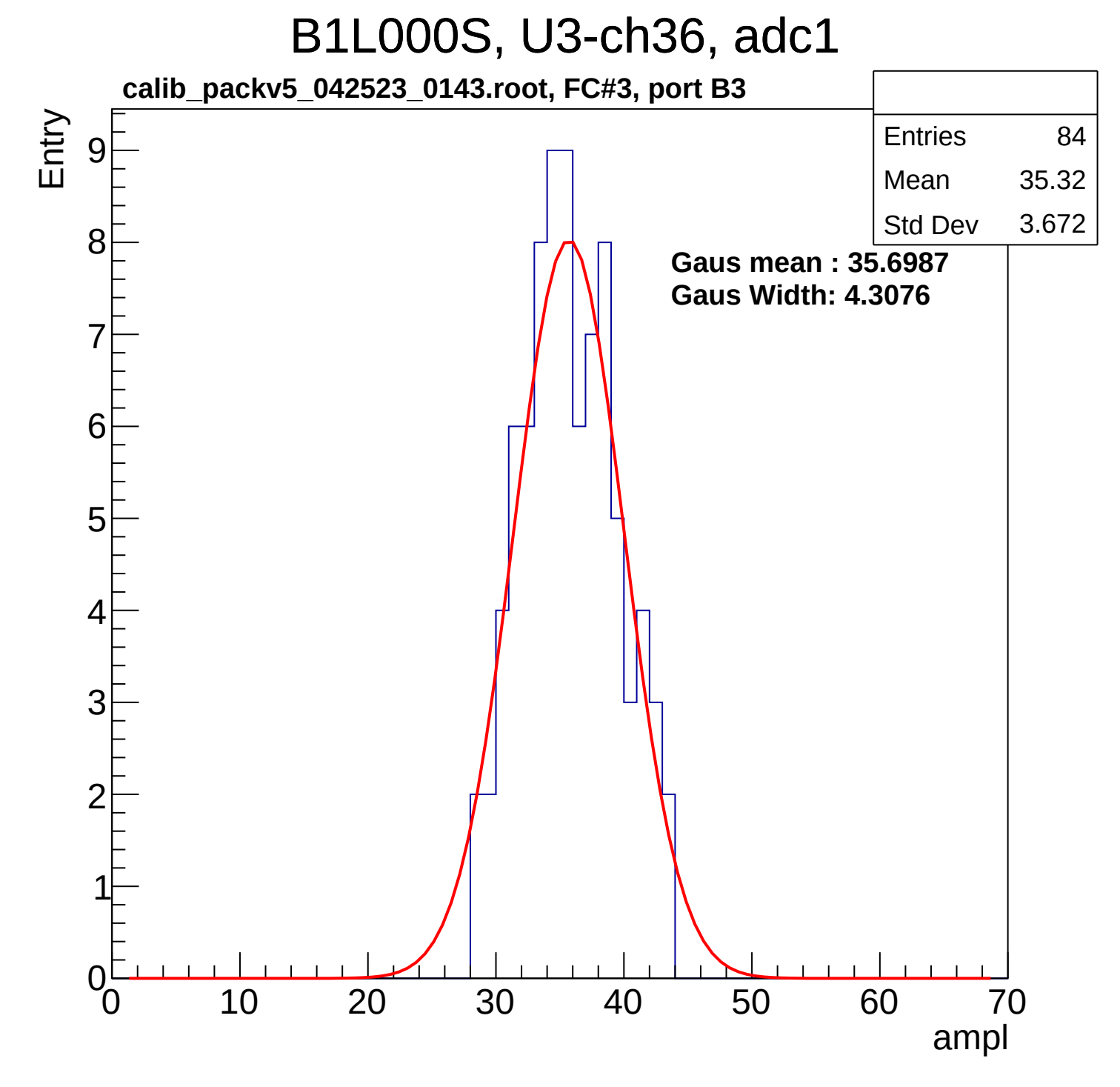
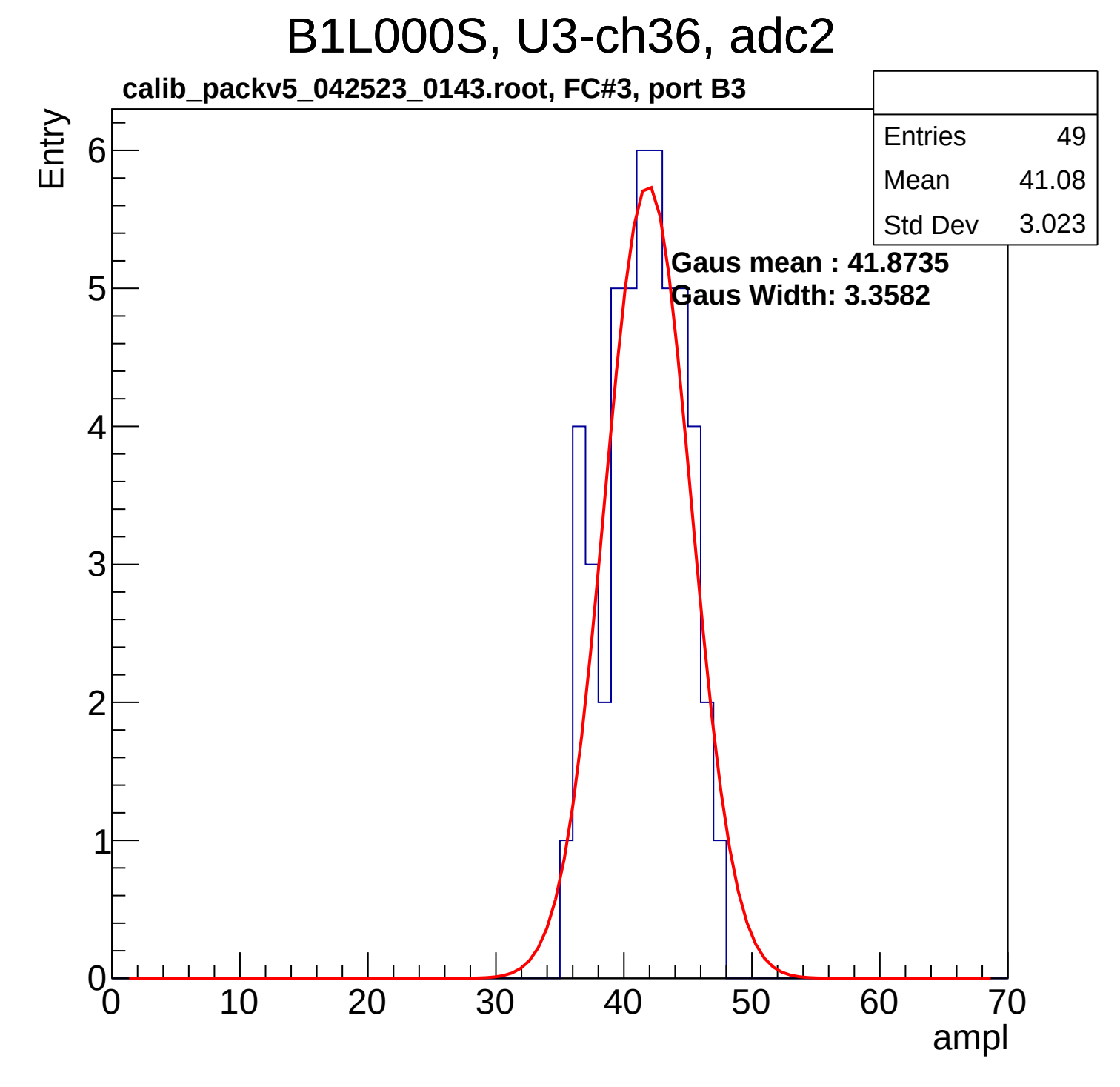
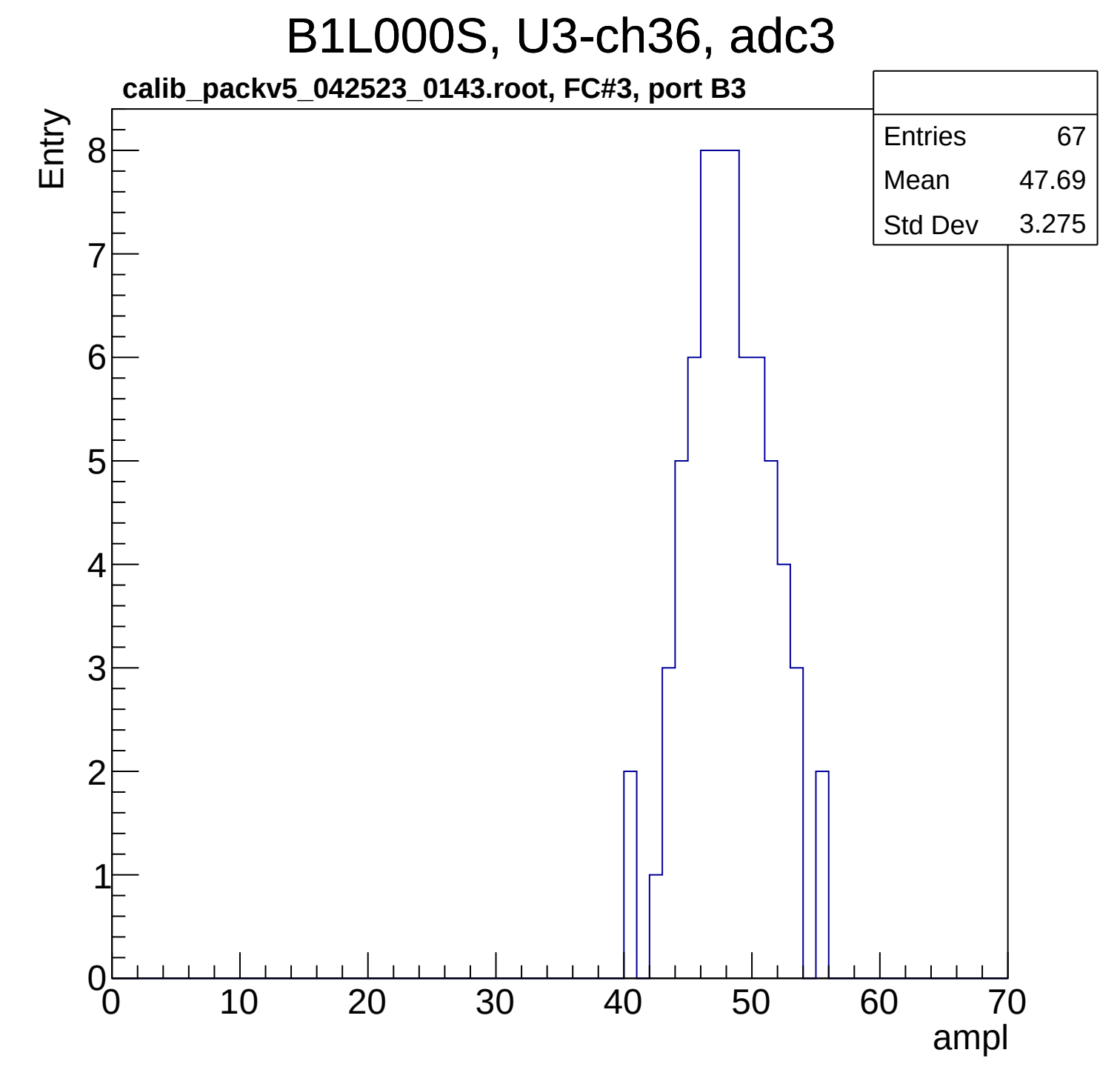
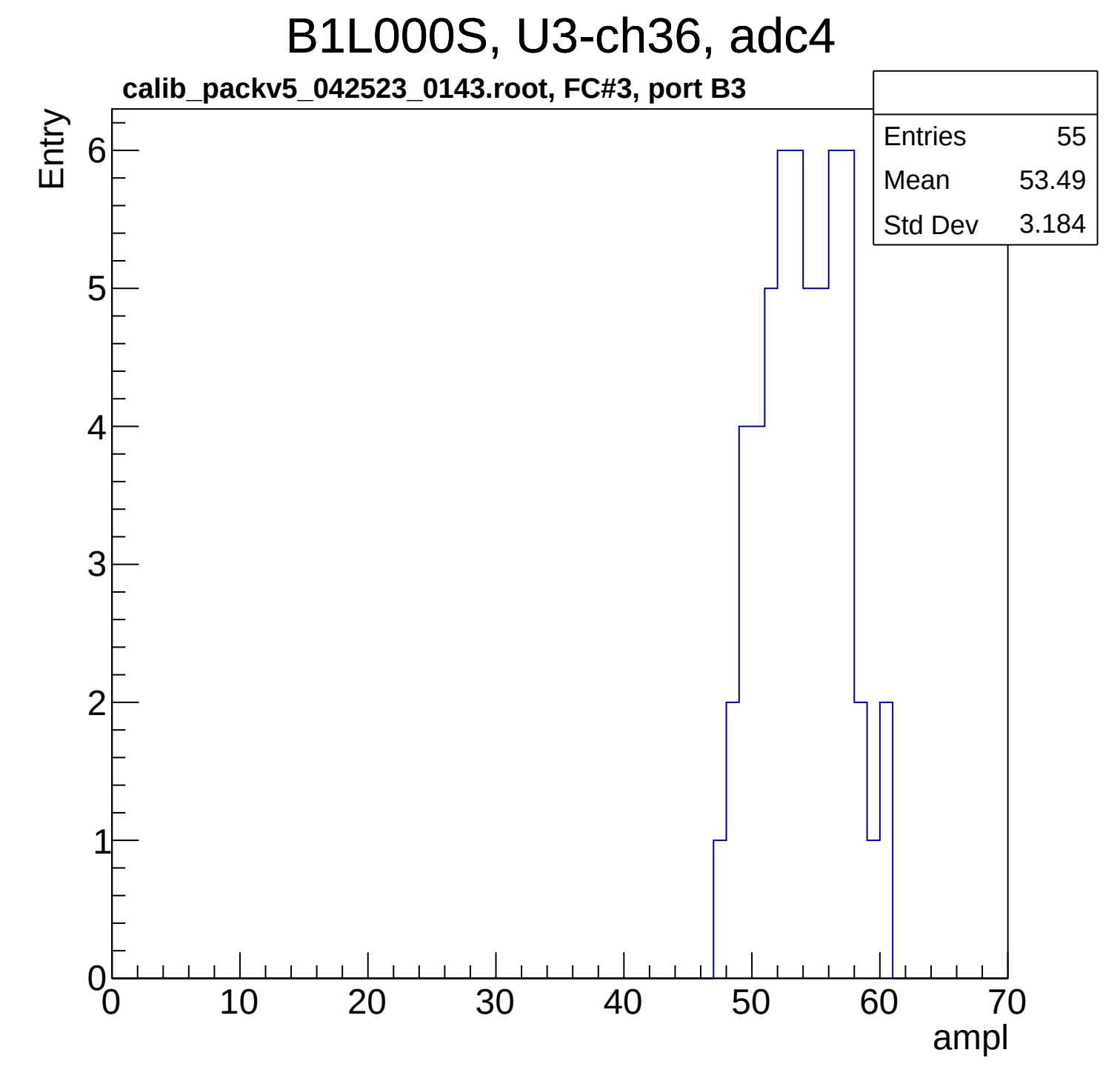
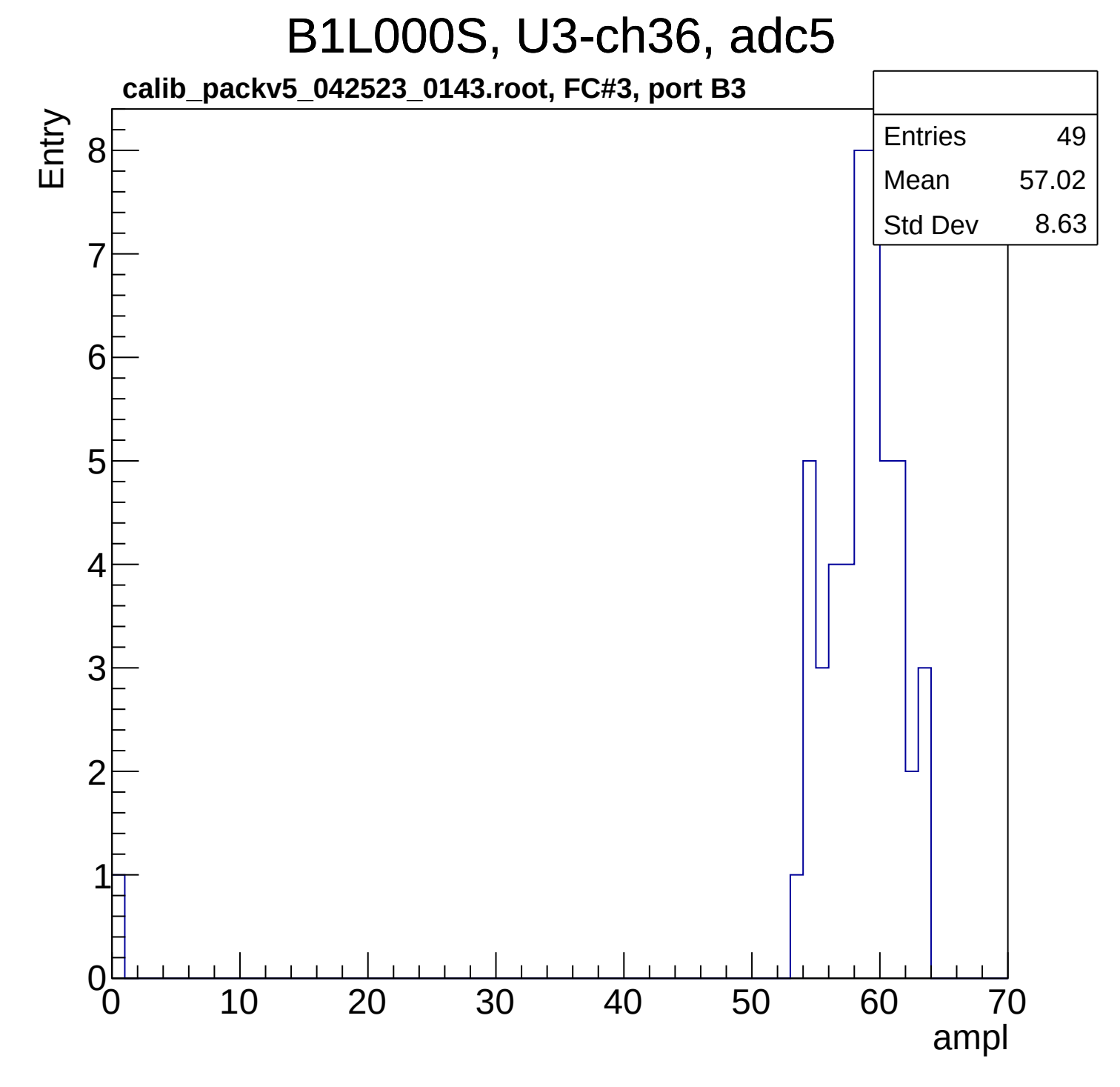

B1L000S, U3-ch36, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 27.6
Std Dev 4.626
Gaus mean : 28.2896
Gaus Width: 3.2849
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch36, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 35.32
Std Dev 3.672
Gaus mean : 35.6987
Gaus Width: 4.3076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch36, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 41.08
Std Dev 3.023
Gaus mean : 41.8735
Gaus Width: 3.3582
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U3-ch36, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 47.69
Std Dev 3.275
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch36, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.49
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U3-ch36, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 57.02
Std Dev 8.63
Entry
ampl
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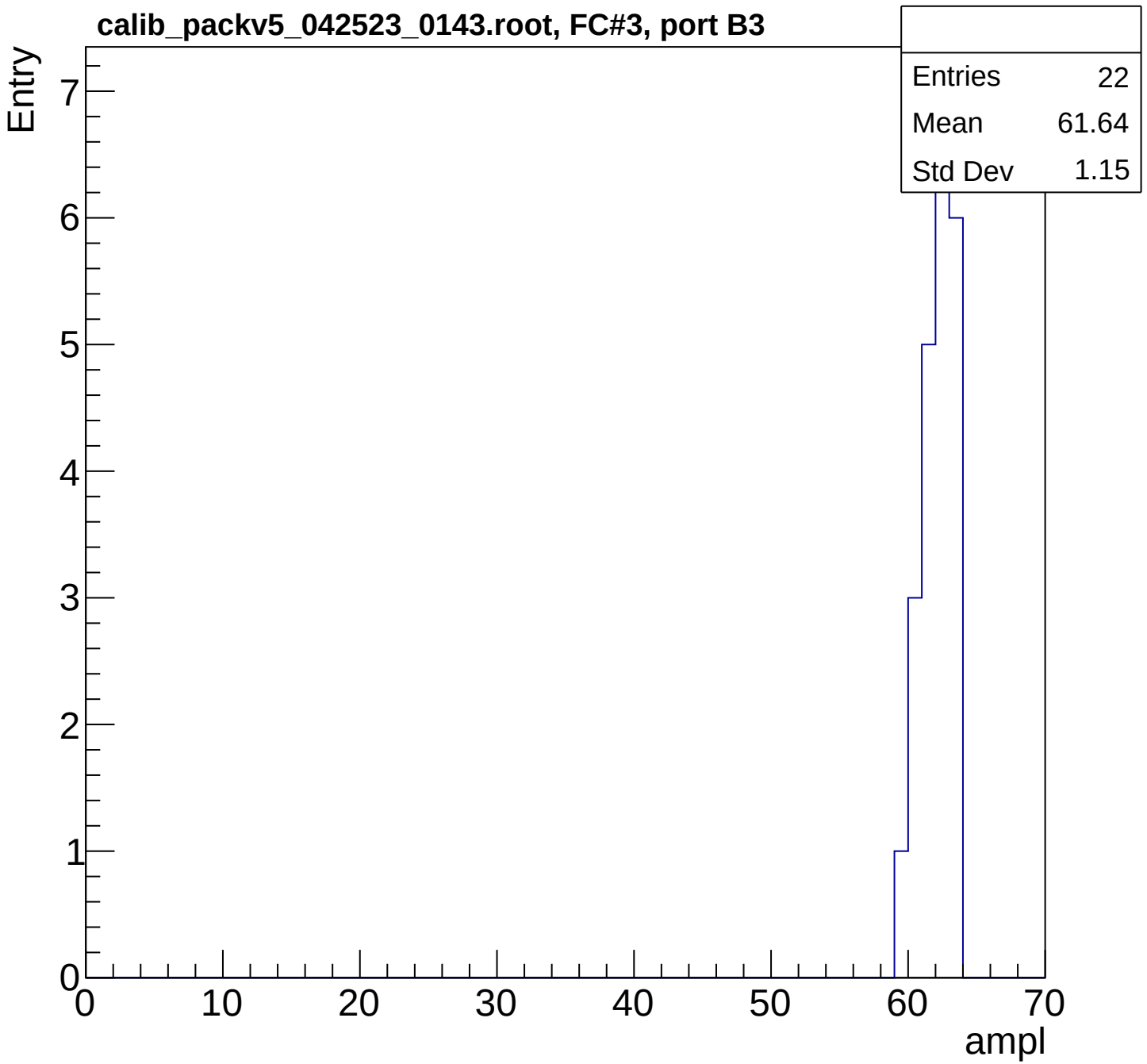
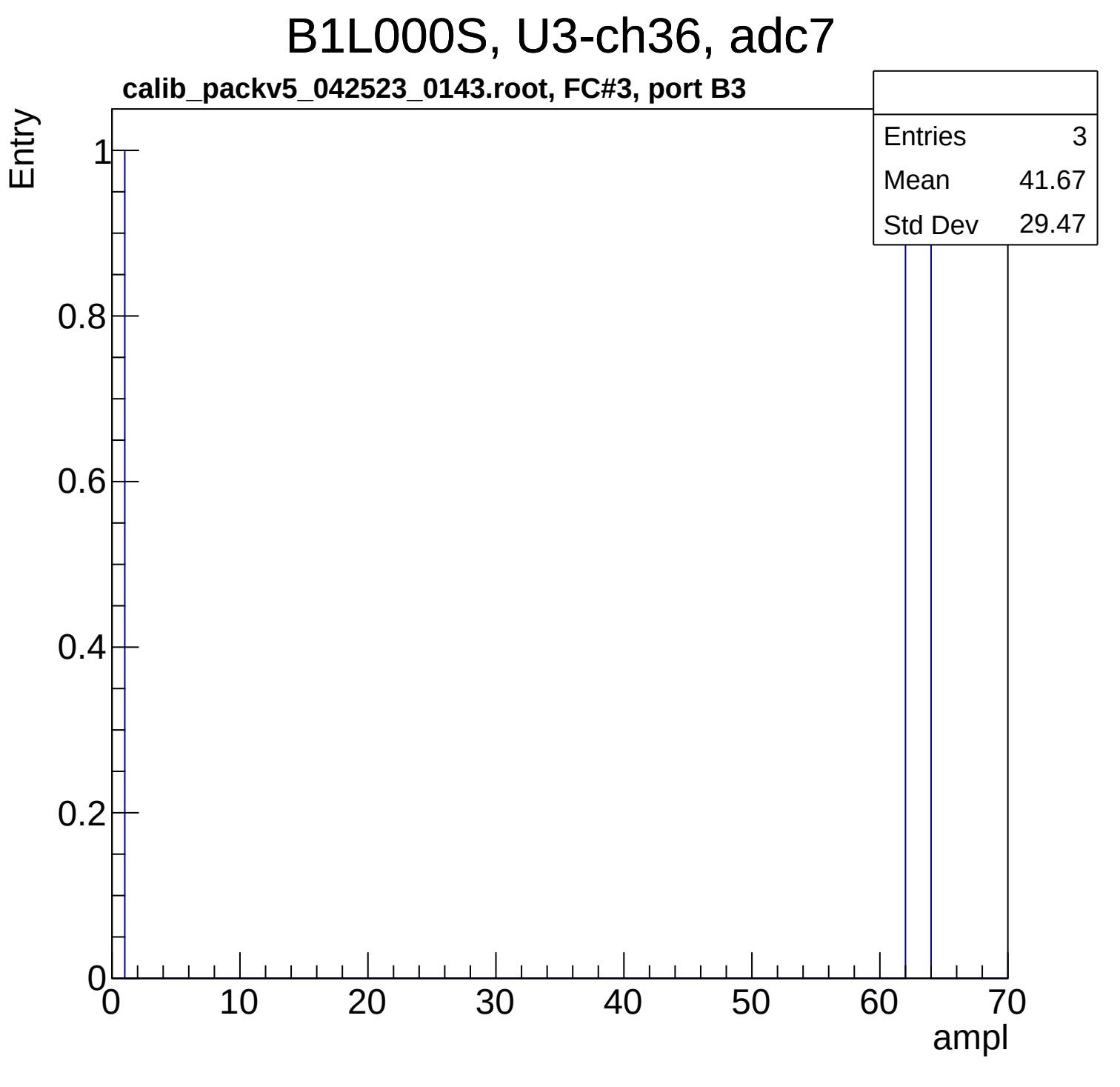
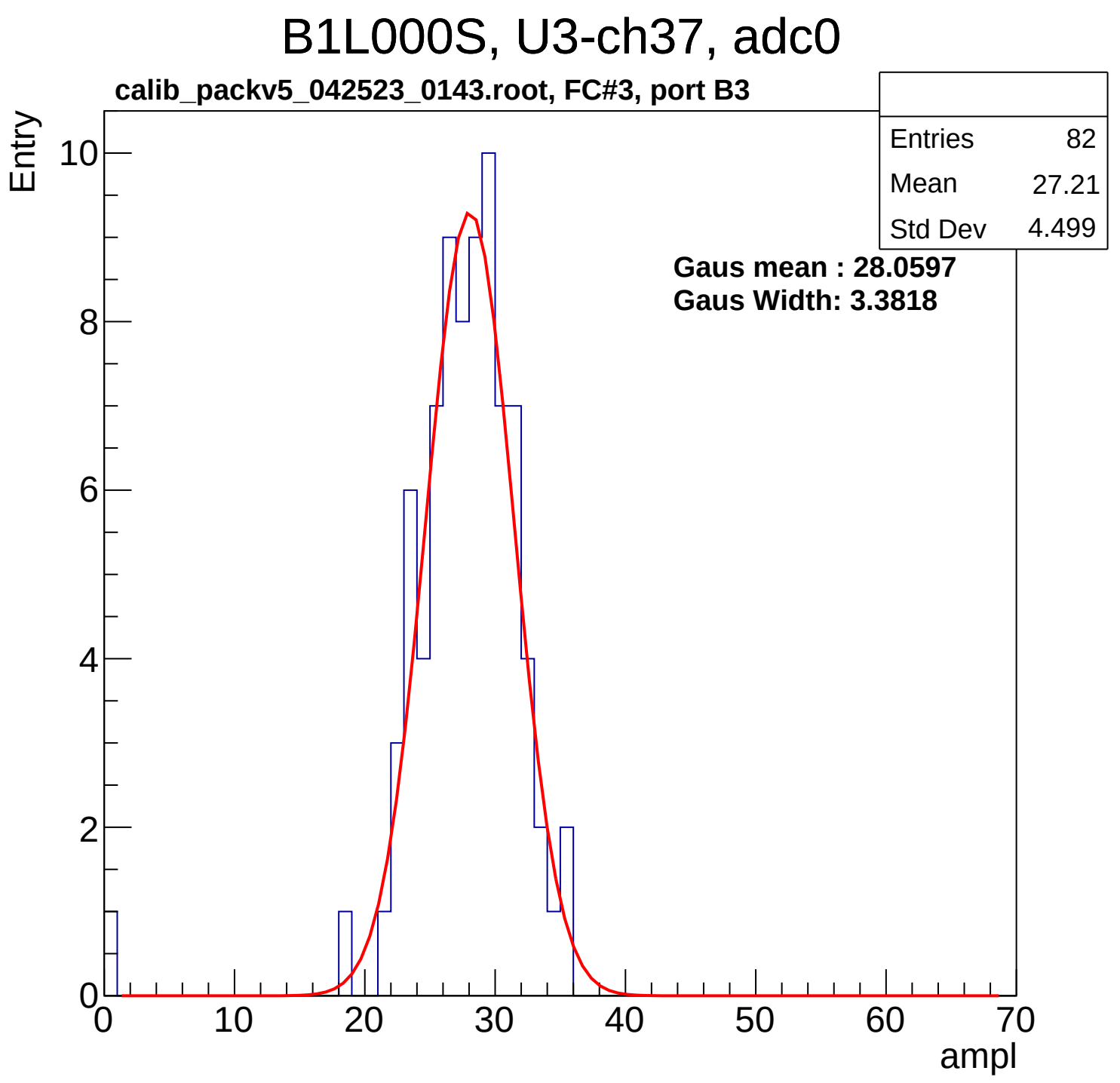
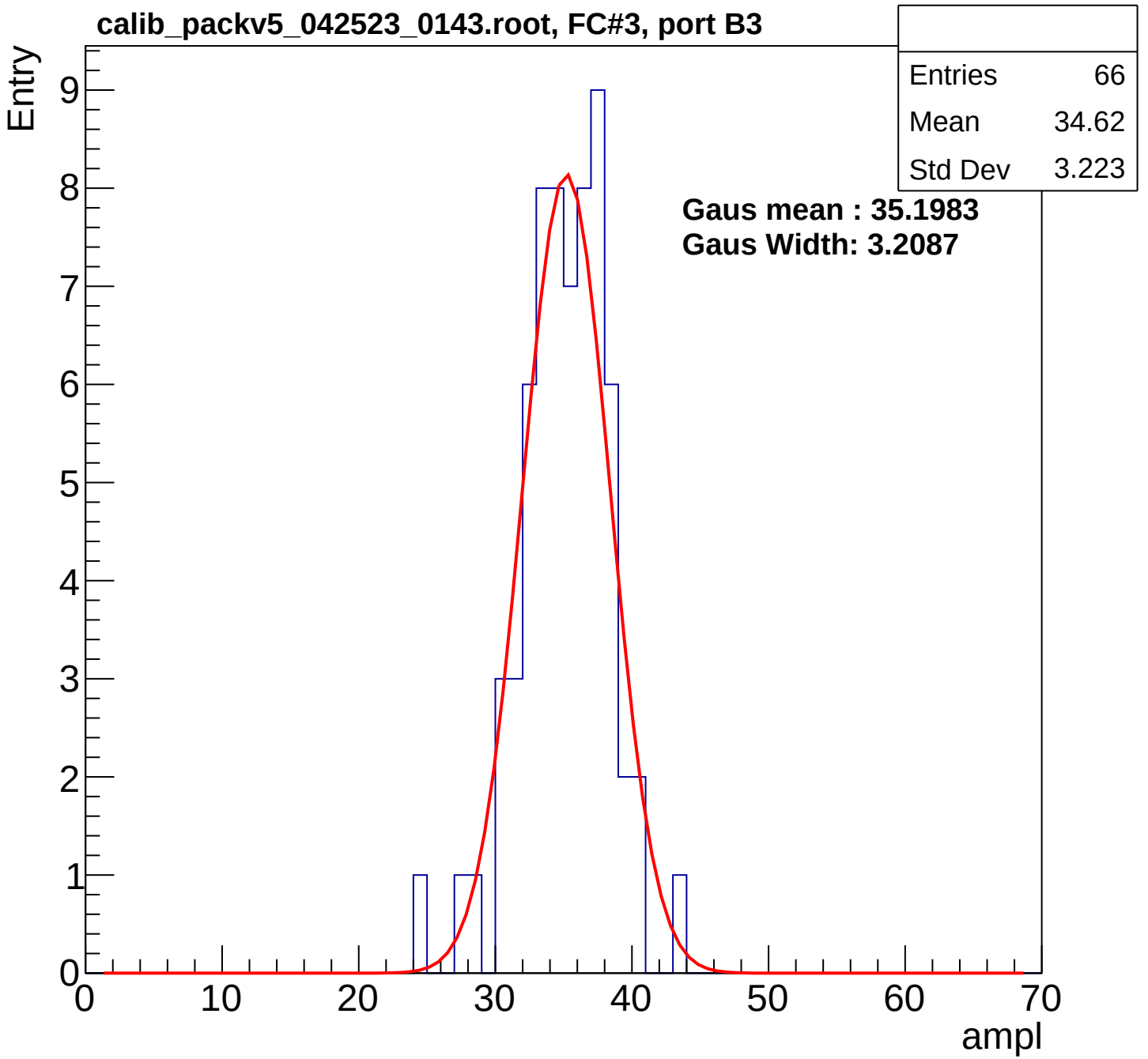
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch36, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 61.64
Std Dev 1.15
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch36, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch37, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 27.21
Std Dev 4.499
Gaus mean : 28.0597
Gaus Width: 3.3818
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch37, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 34.62
Std Dev 3.223
Gaus mean : 35.1983
Gaus Width: 3.2087
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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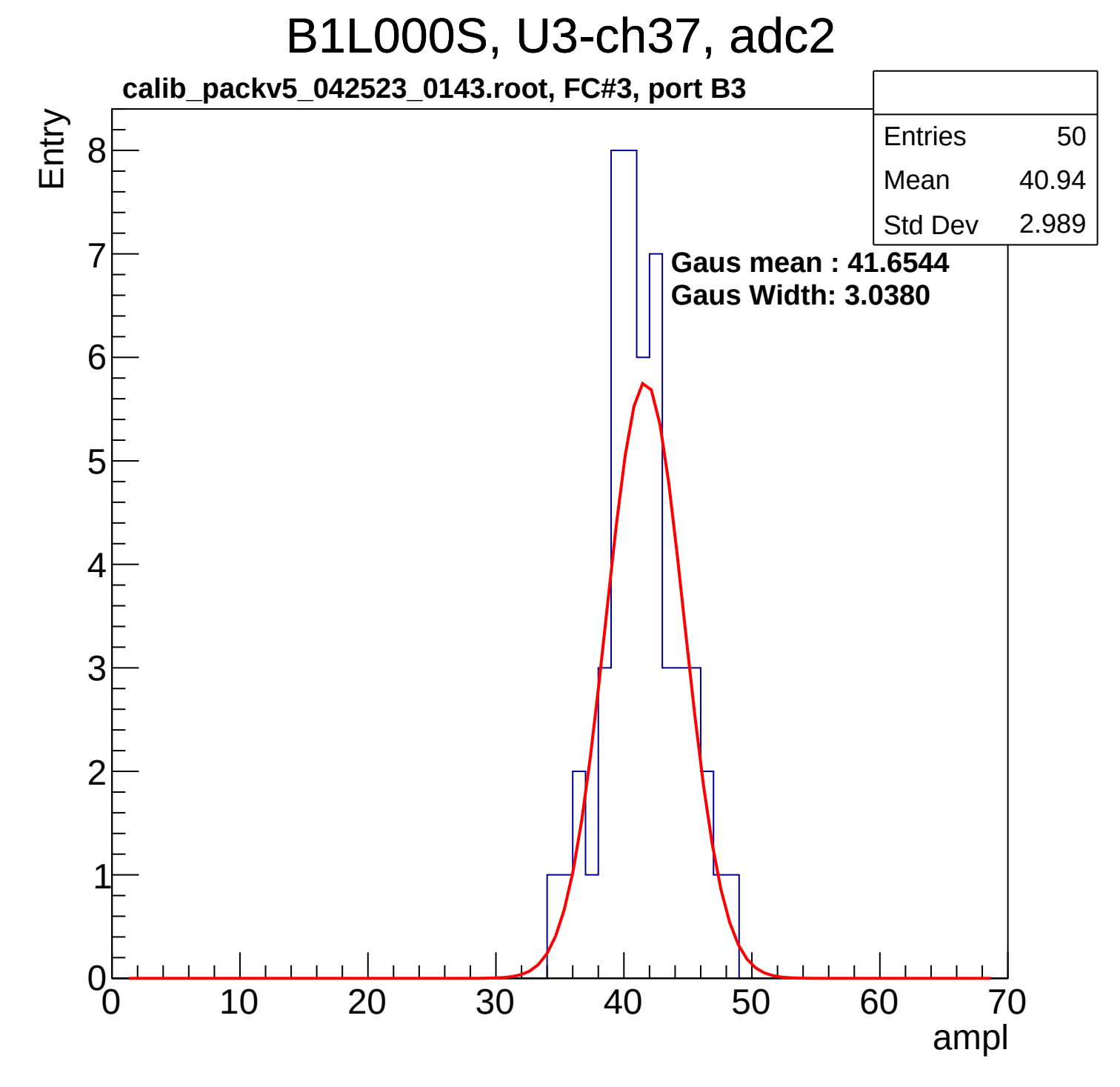
B1L000S, U3-ch37, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 40.94
Std Dev 2.989
Gaus mean : 41.6544
Gaus Width: 3.0380
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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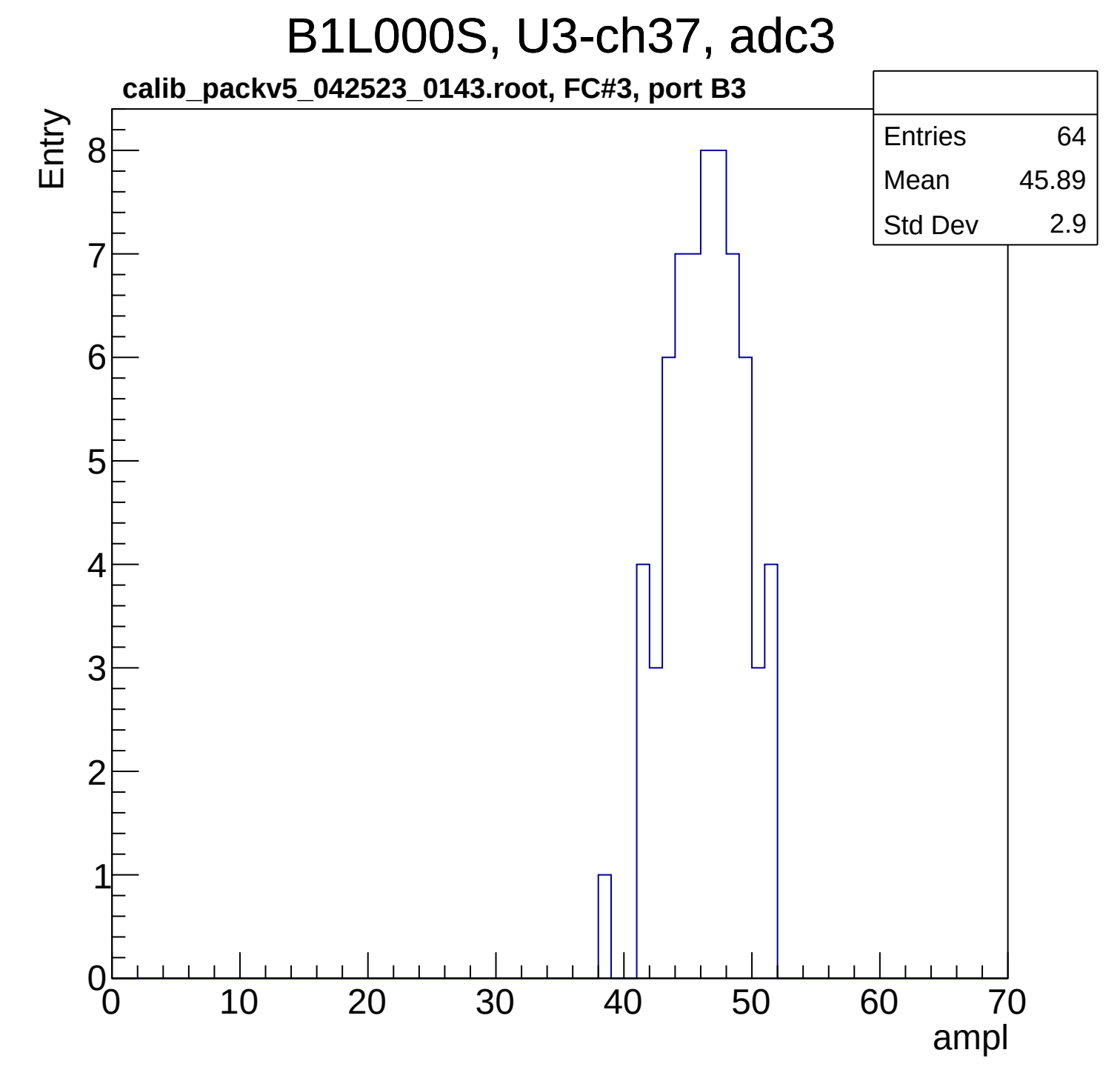
B1L000S, U3-ch37, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 45.89
Std Dev 2.9
Entry
ampl
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1
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5
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7
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0
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20
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60
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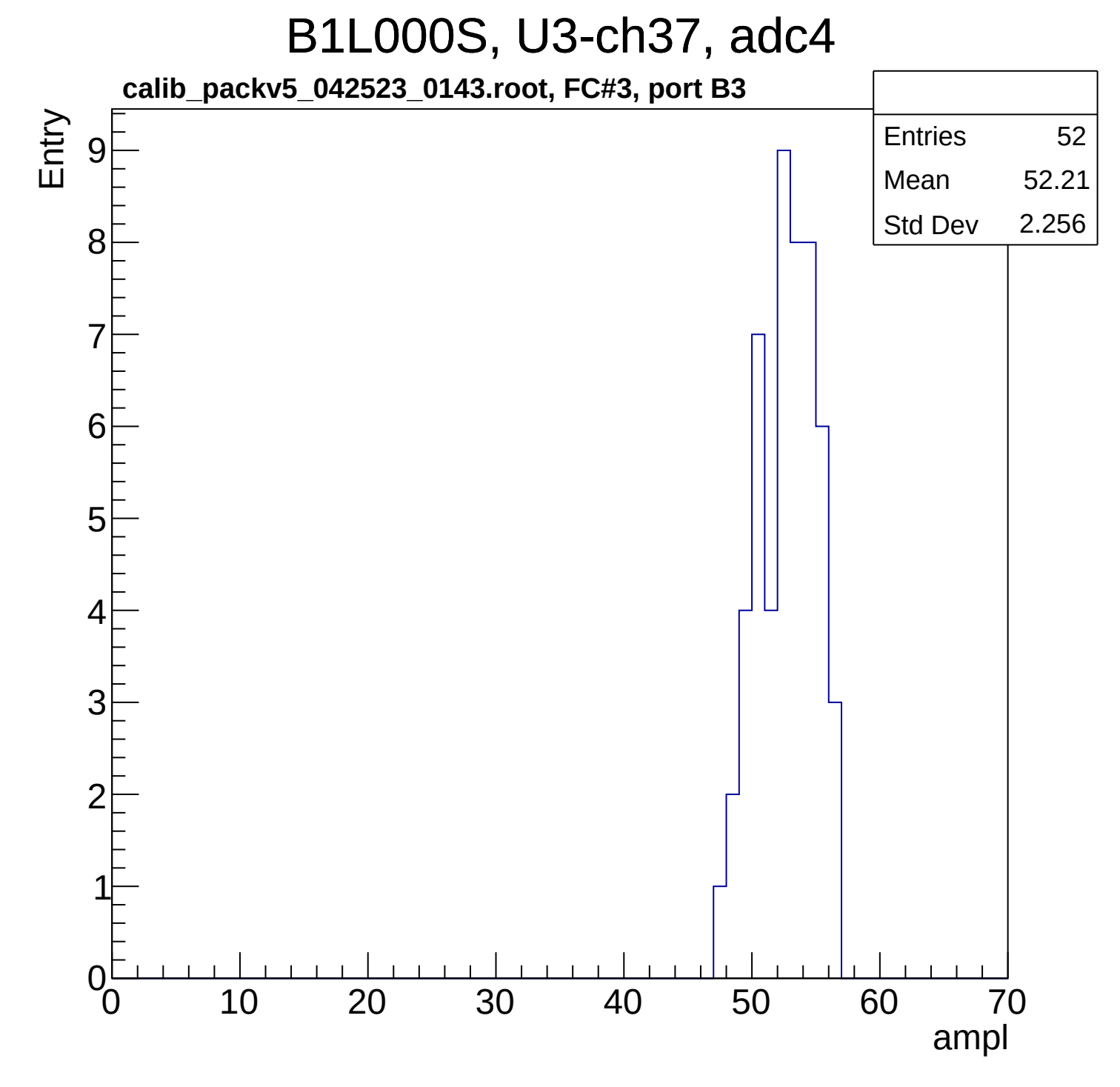
B1L000S, U3-ch37, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 52.21
Std Dev 2.256
Entry
ampl
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2
3
4
5
6
7
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0
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60
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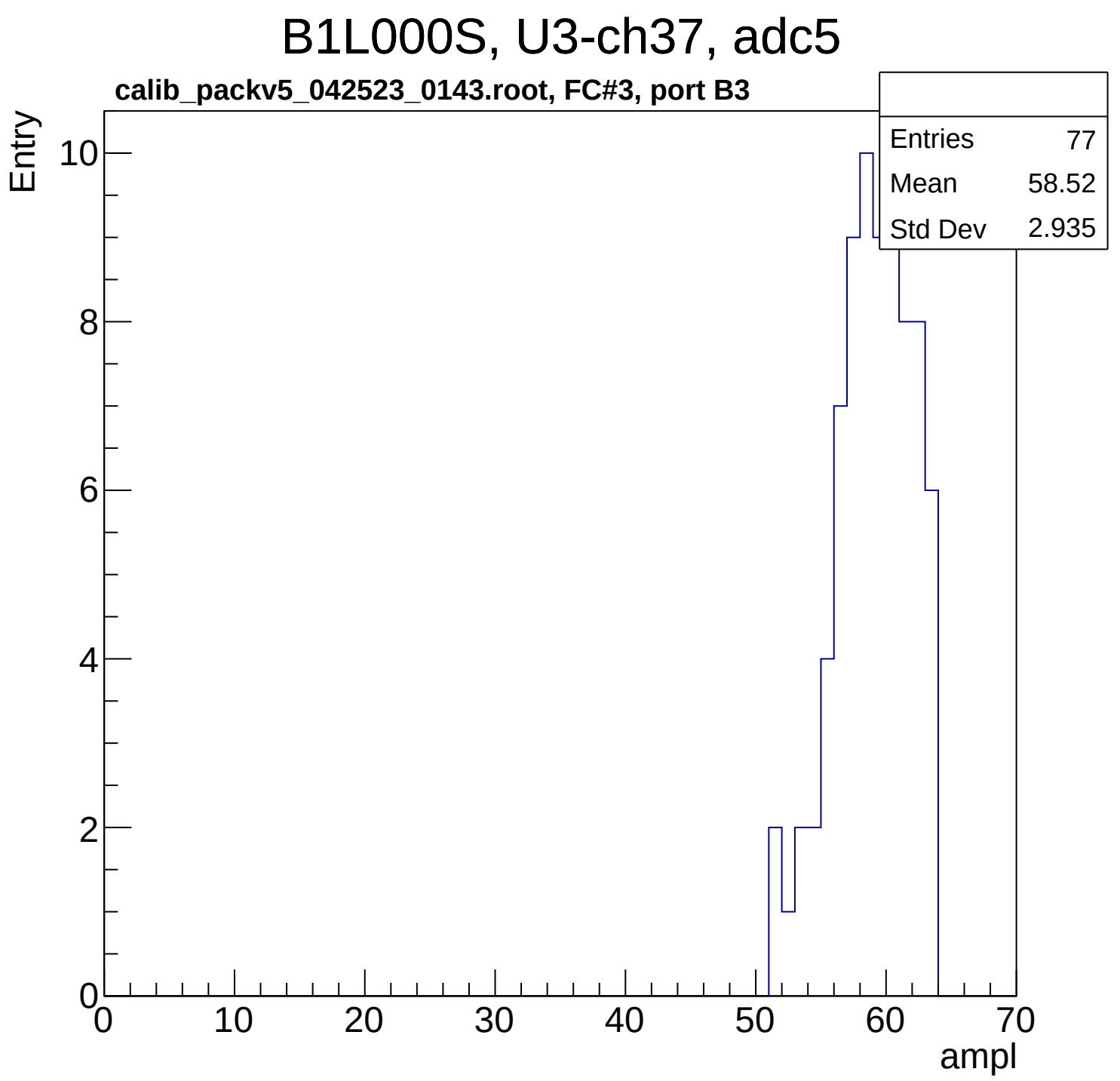
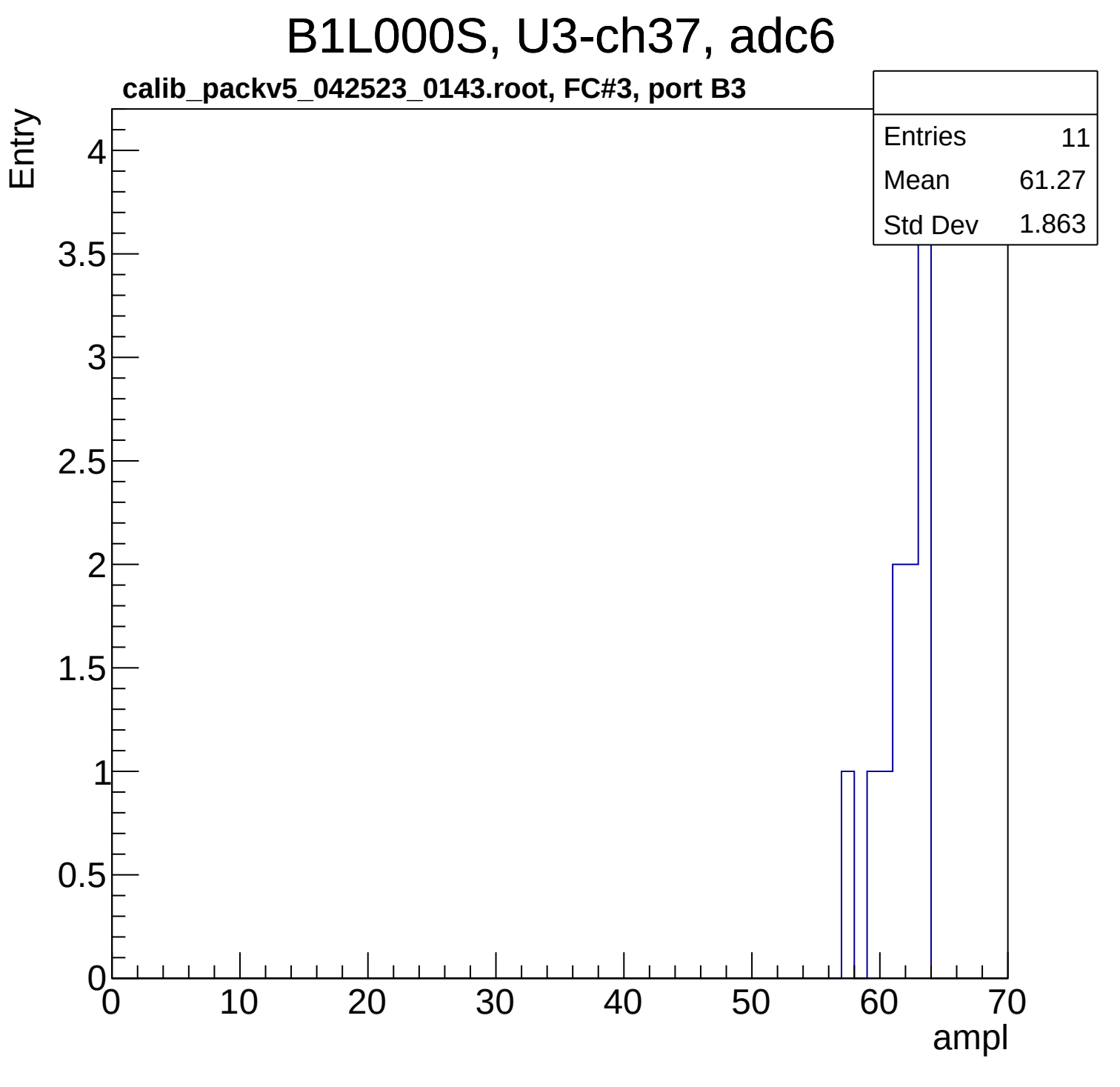

B1L000S, U3-ch37, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 58.52
Std Dev 2.935
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch37, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 61.27
Std Dev 1.863
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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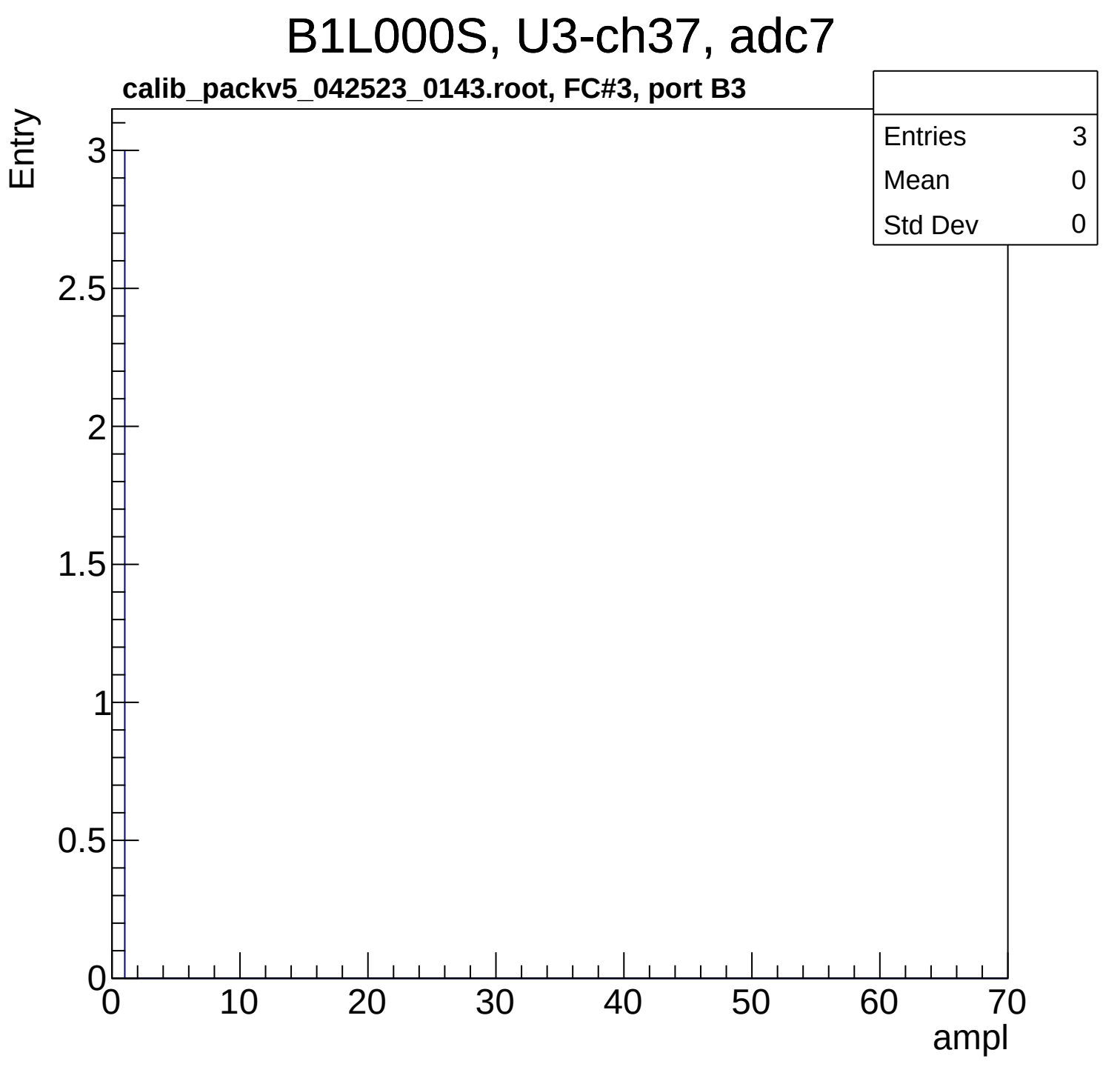
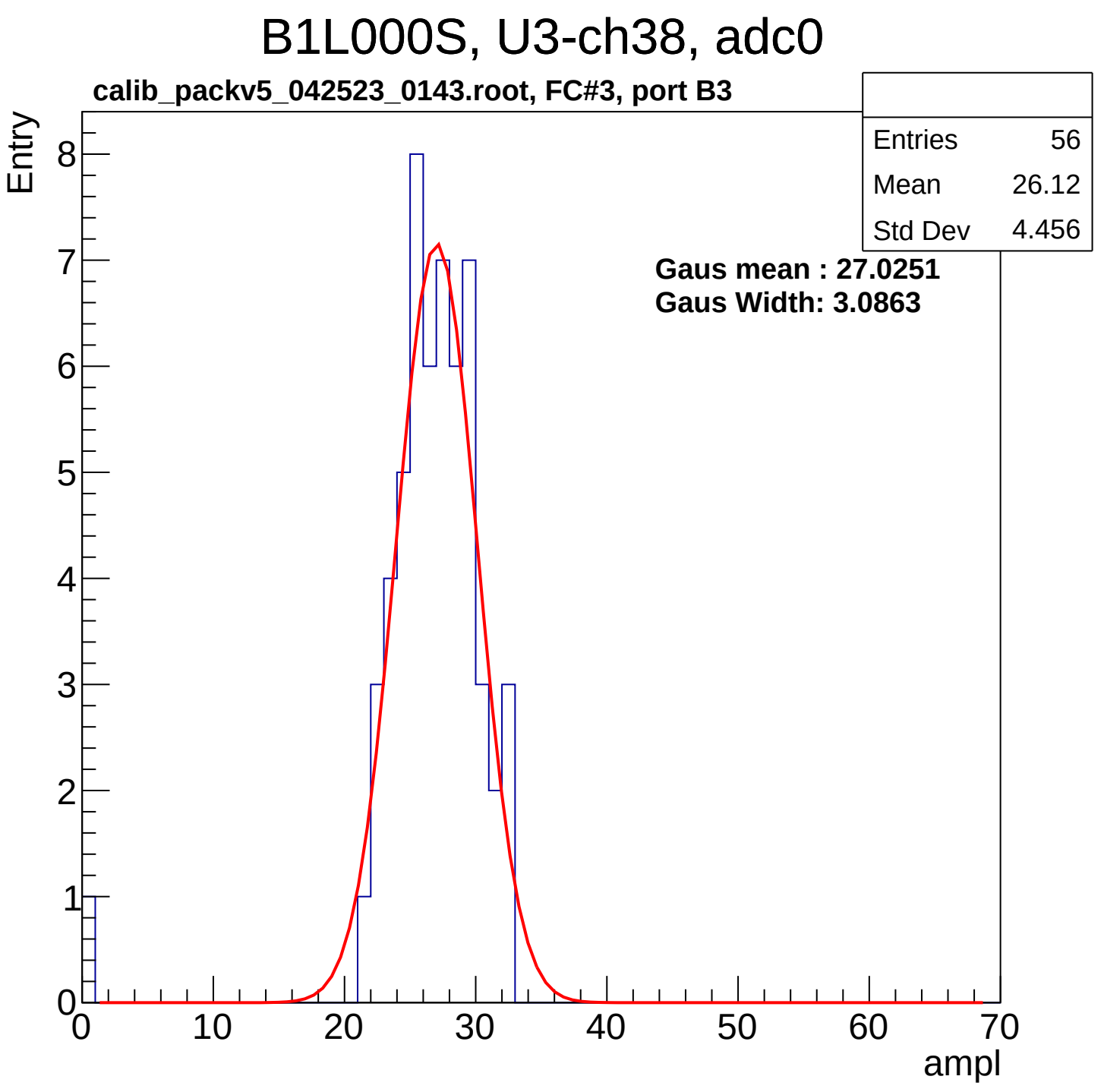
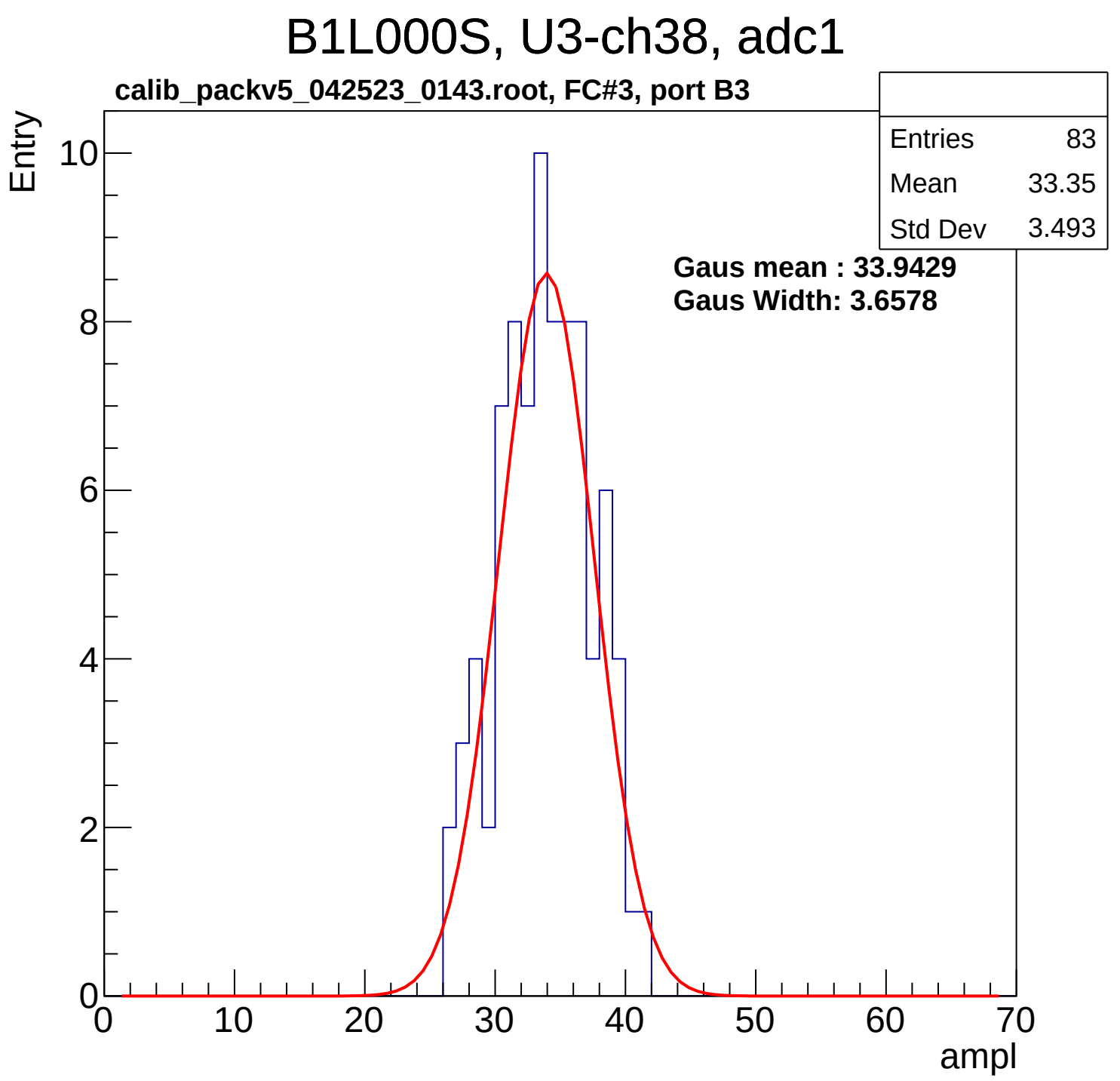
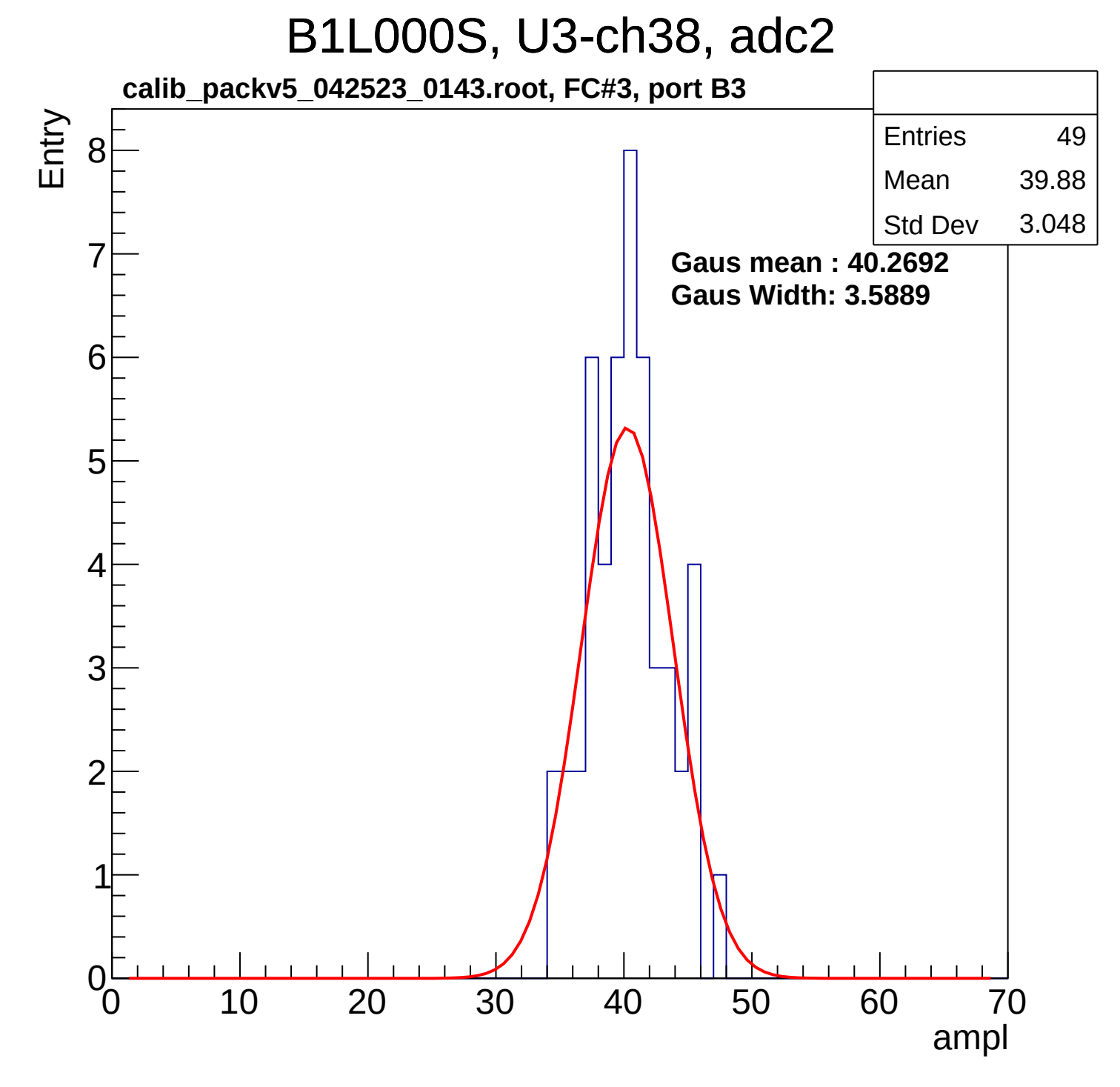
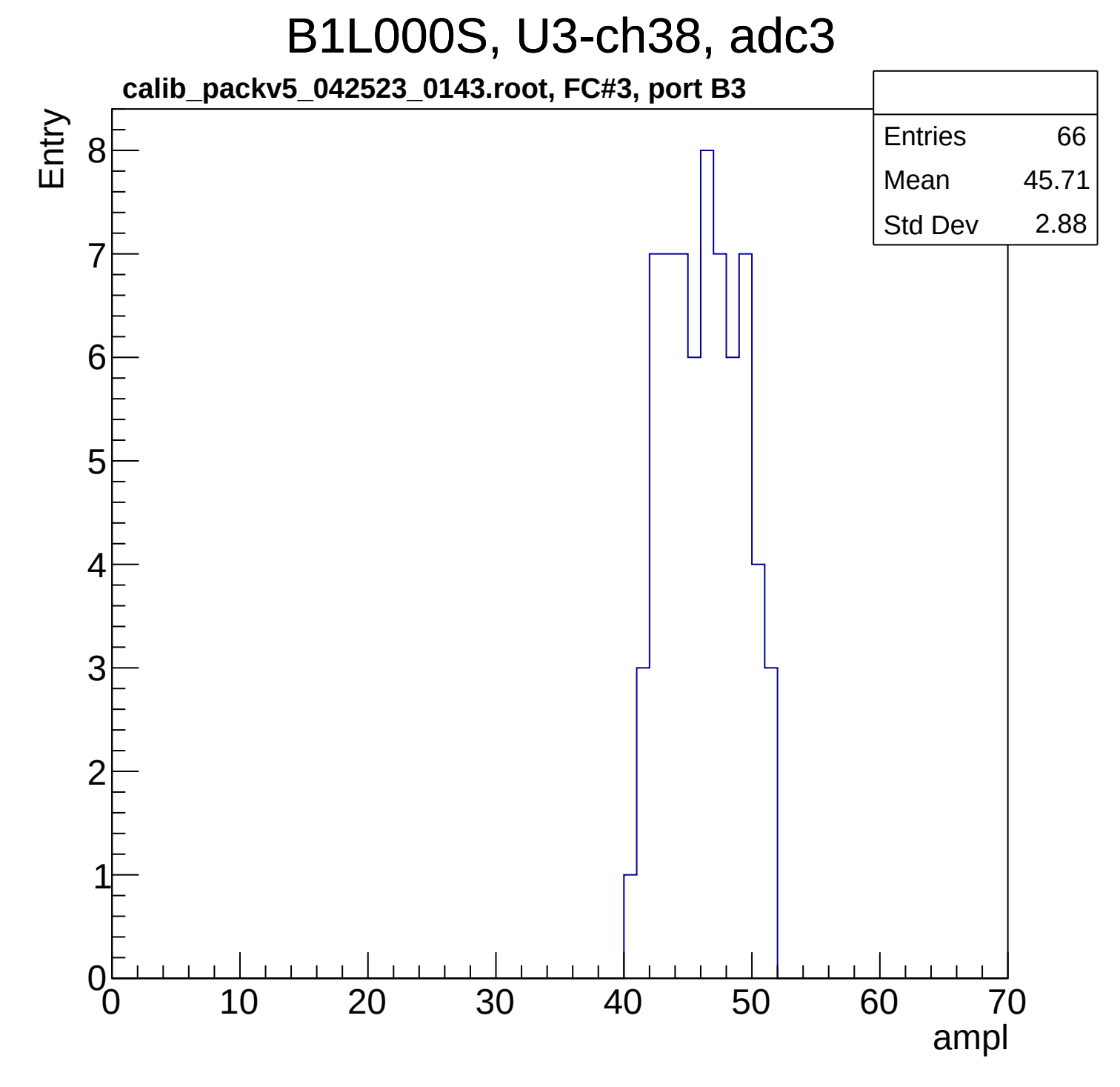

B1L000S, U3-ch37, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch38, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 26.12
Std Dev 4.456
Gaus mean : 27.0251
Gaus Width: 3.0863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch38, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 33.35
Std Dev 3.493
Gaus mean : 33.9429
Gaus Width: 3.6578
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch38, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 39.88
Std Dev 3.048
Gaus mean : 40.2692
Gaus Width: 3.5889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch38, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 45.71
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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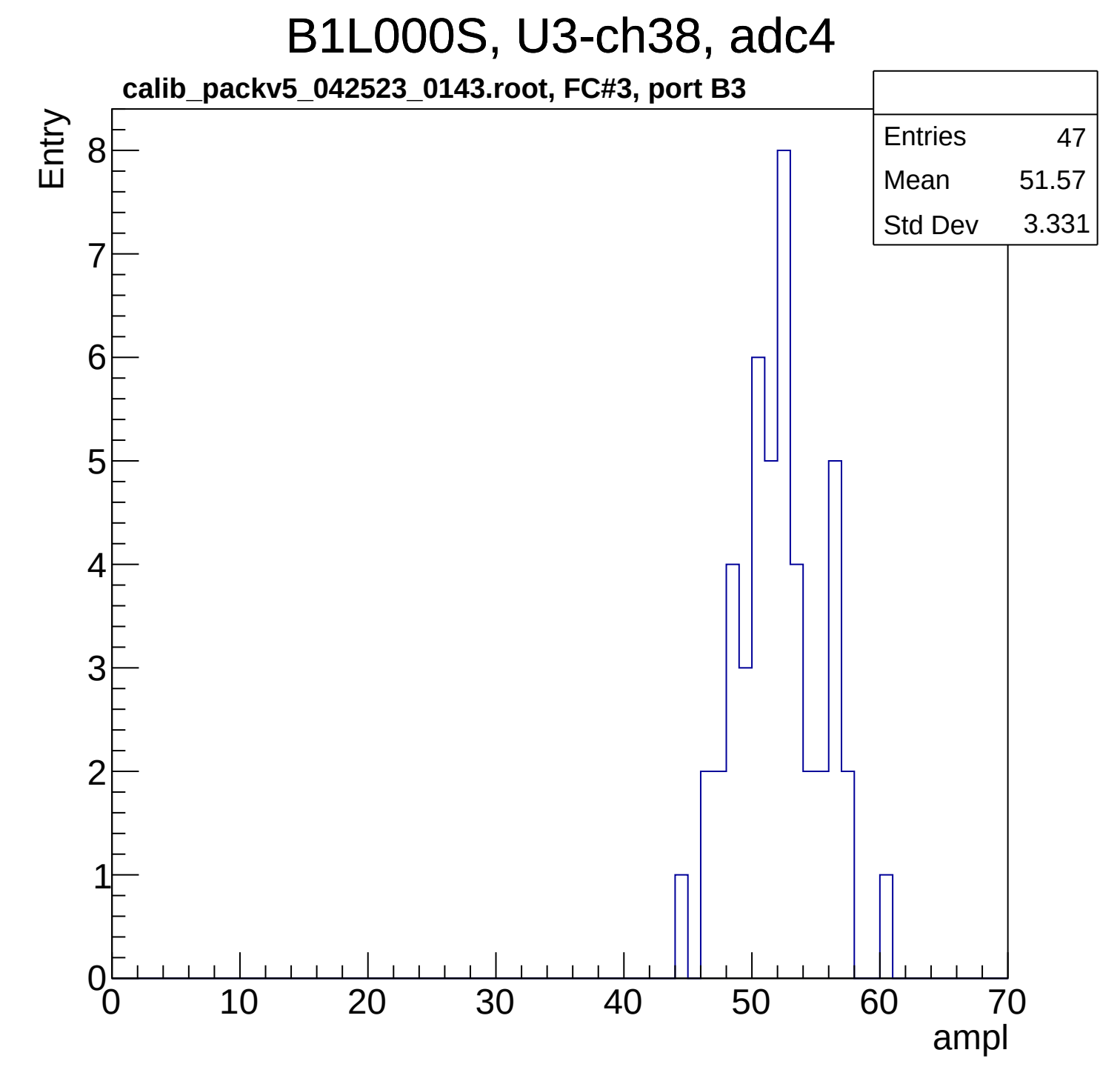
B1L000S, U3-ch38, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 51.57
Std Dev 3.331
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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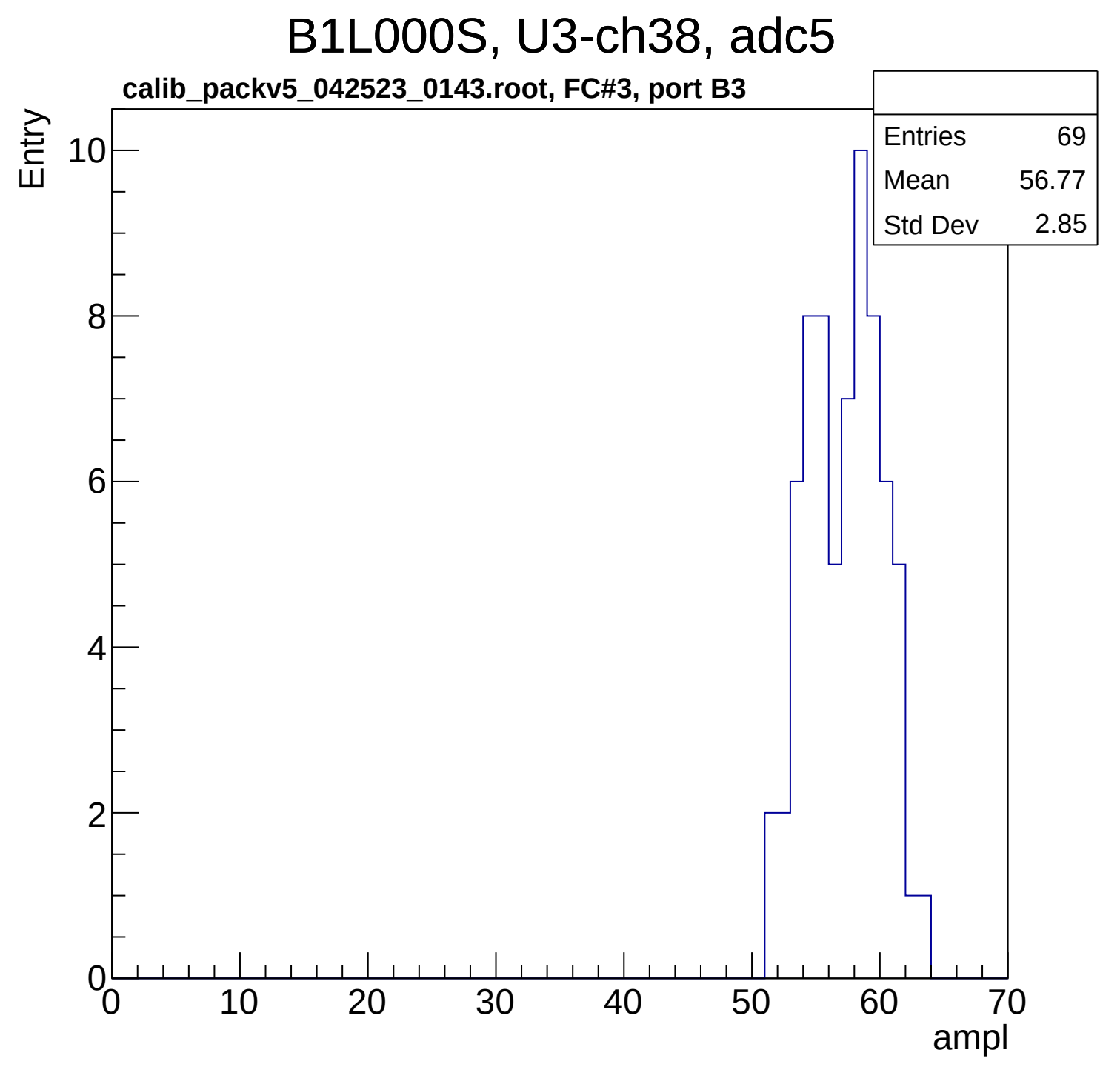
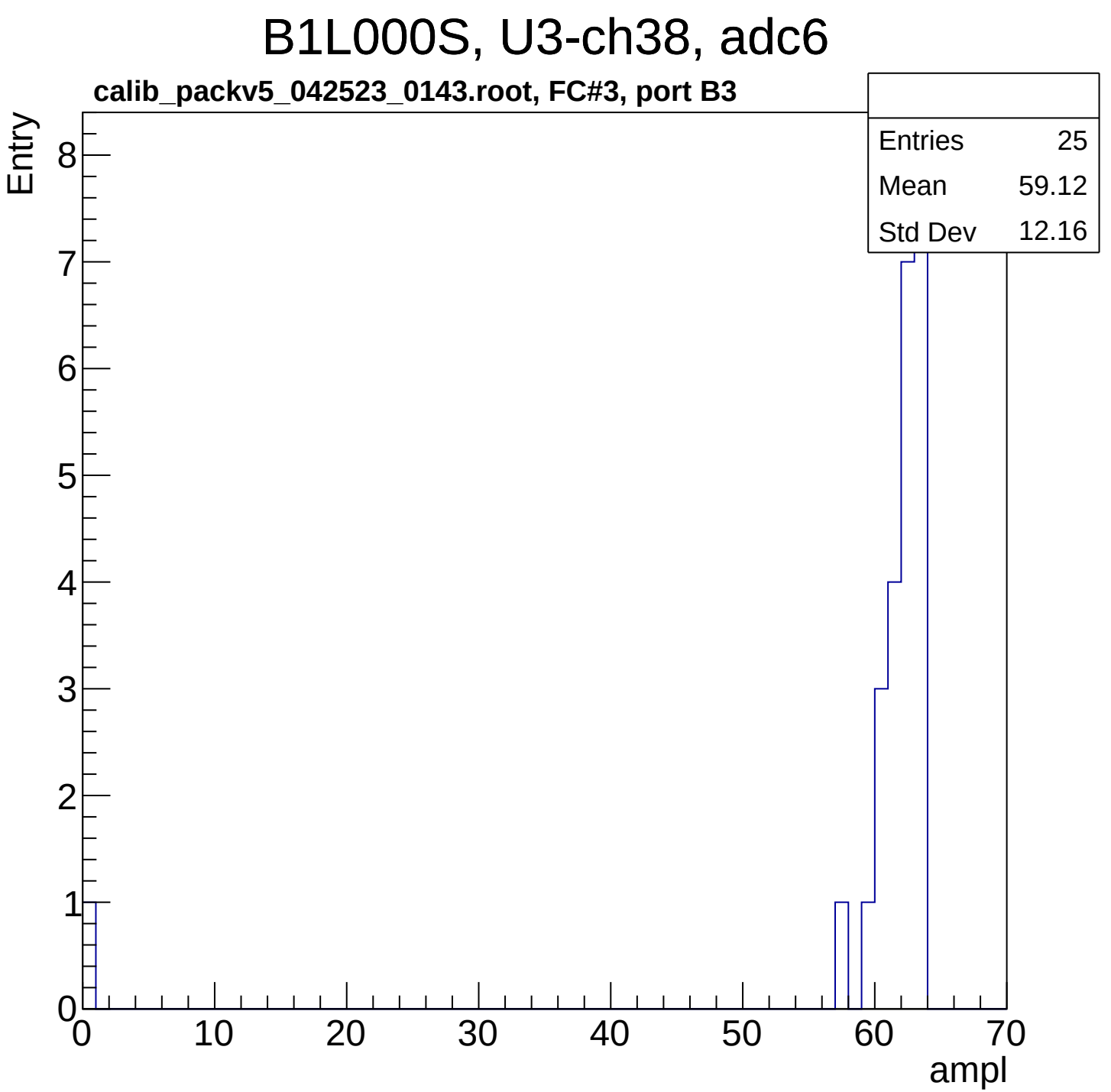
40
50
60
70


B1L000S, U3-ch38, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 56.77
Std Dev 2.85
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch38, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 59.12
Std Dev 12.16
Entry
ampl
0
1
2
3
4
5
6
7
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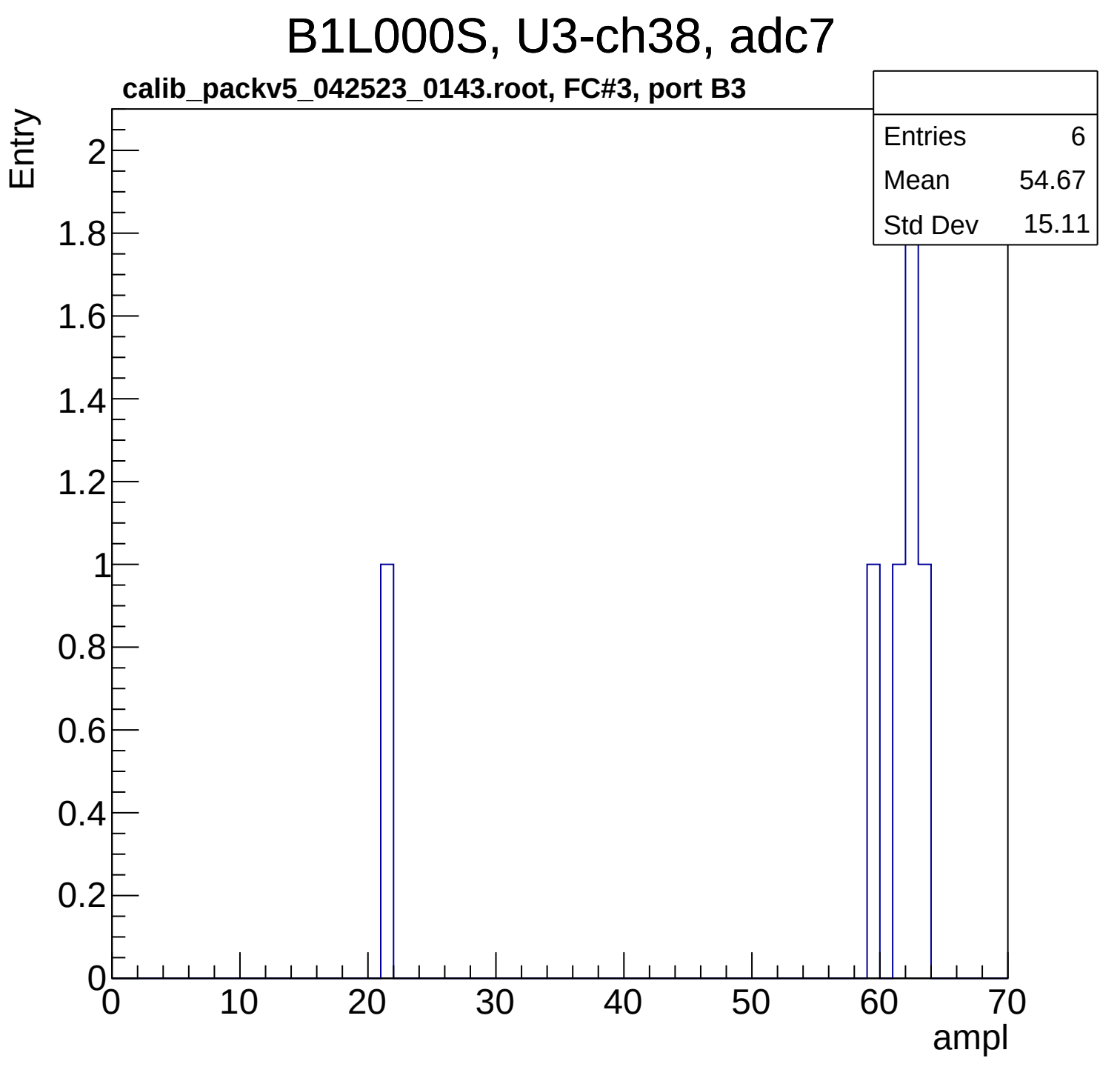
B1L000S, U3-ch38, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 54.67
Std Dev 15.11
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
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0
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60
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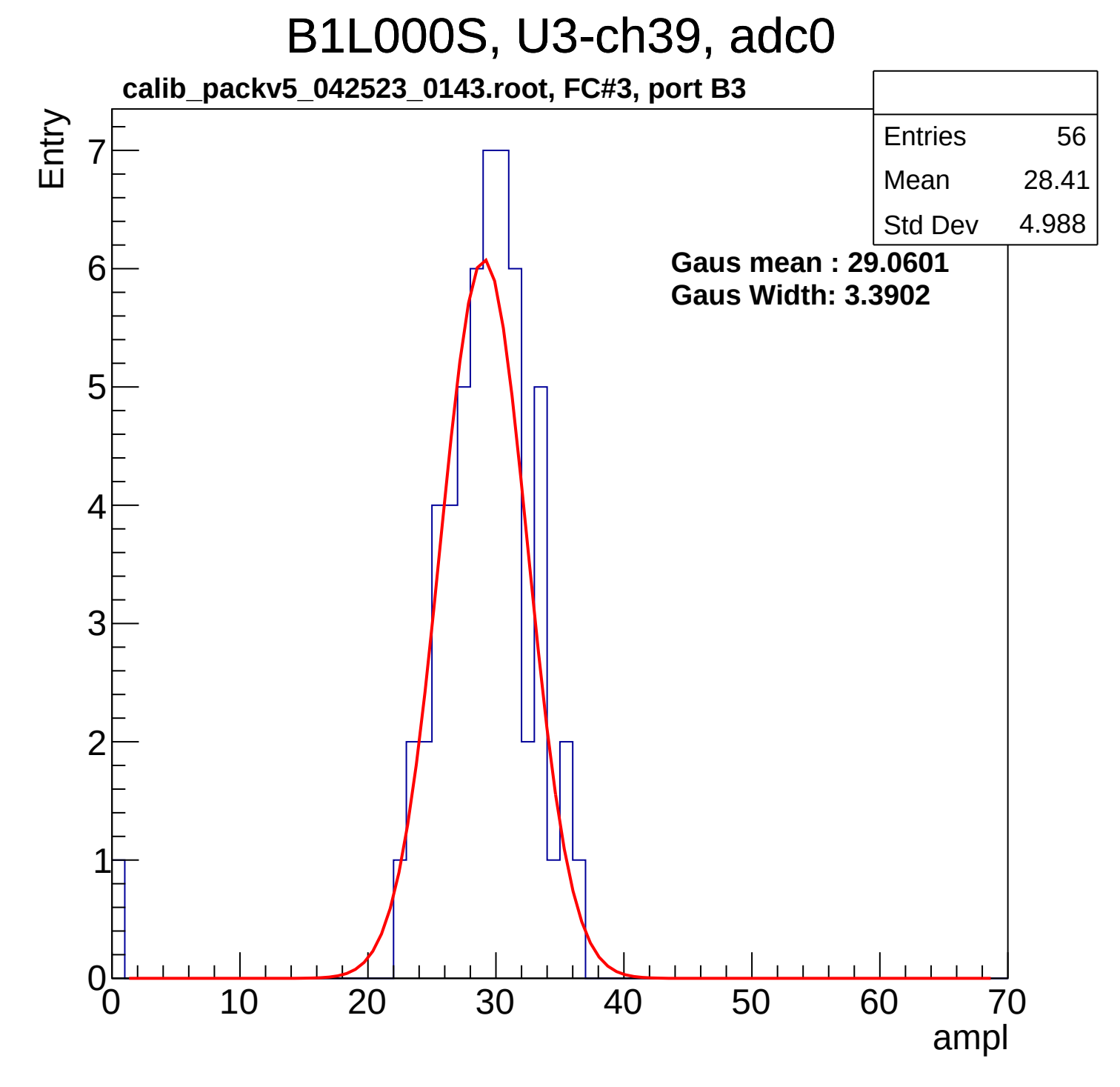
B1L000S, U3-ch39, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 28.41
Std Dev 4.988
Gaus mean : 29.0601
Gaus Width: 3.3902
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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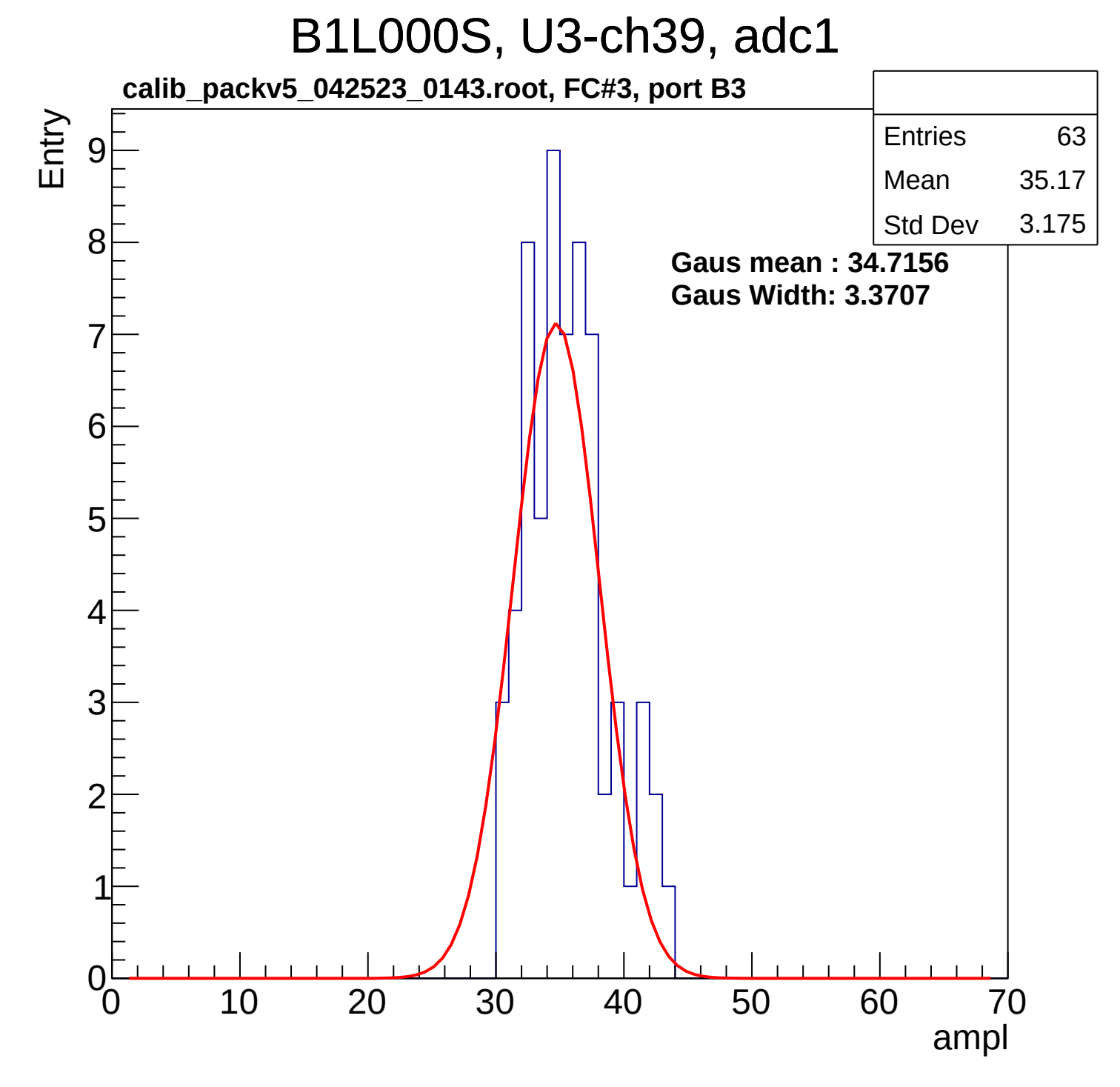
B1L000S, U3-ch39, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 35.17
Std Dev 3.175
Gaus mean : 34.7156
Gaus Width: 3.3707
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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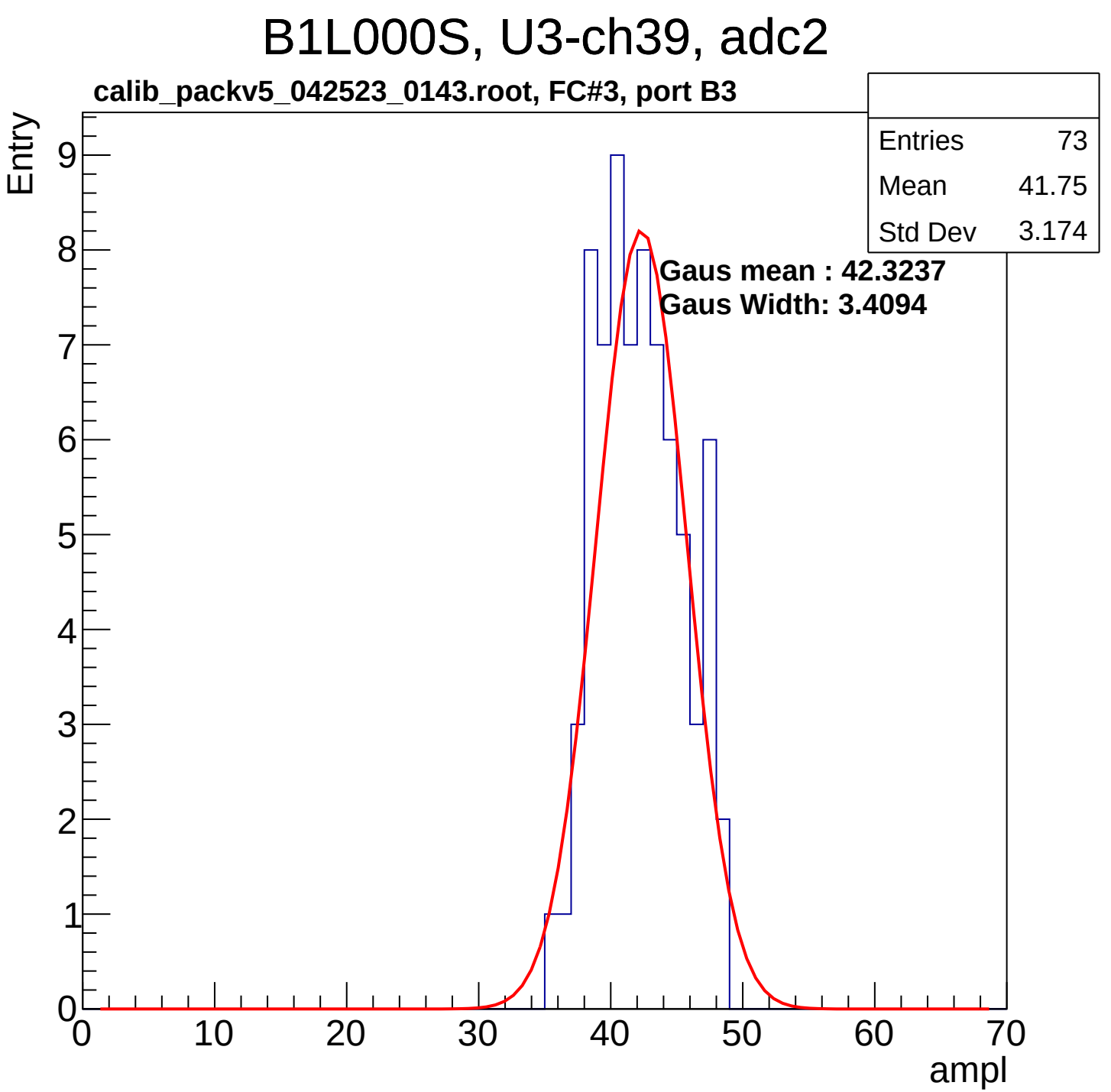
B1L000S, U3-ch39, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 41.75
Std Dev 3.174
Gaus mean : 42.3237
Gaus Width: 3.4094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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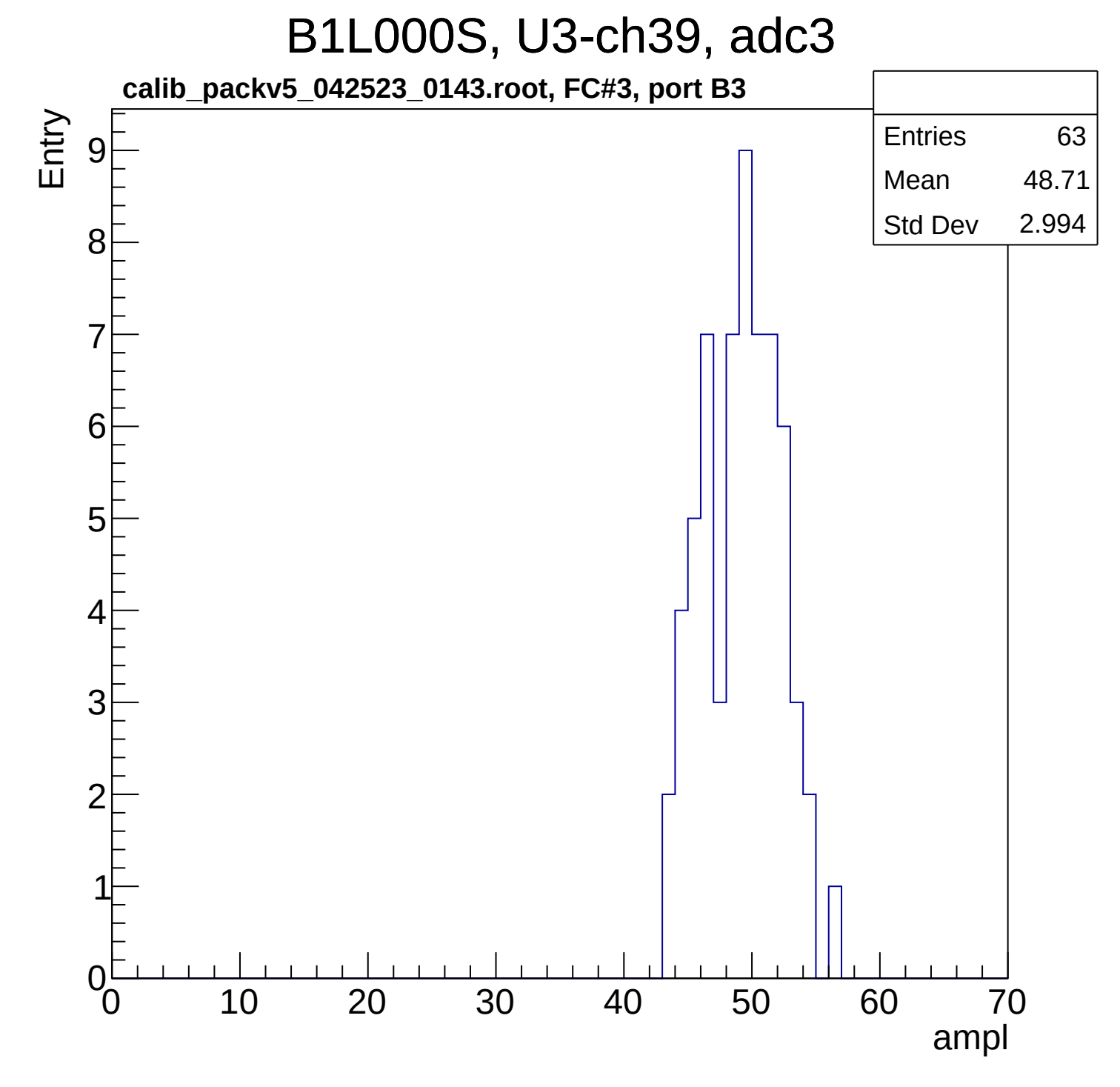
B1L000S, U3-ch39, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 48.71
Std Dev 2.994
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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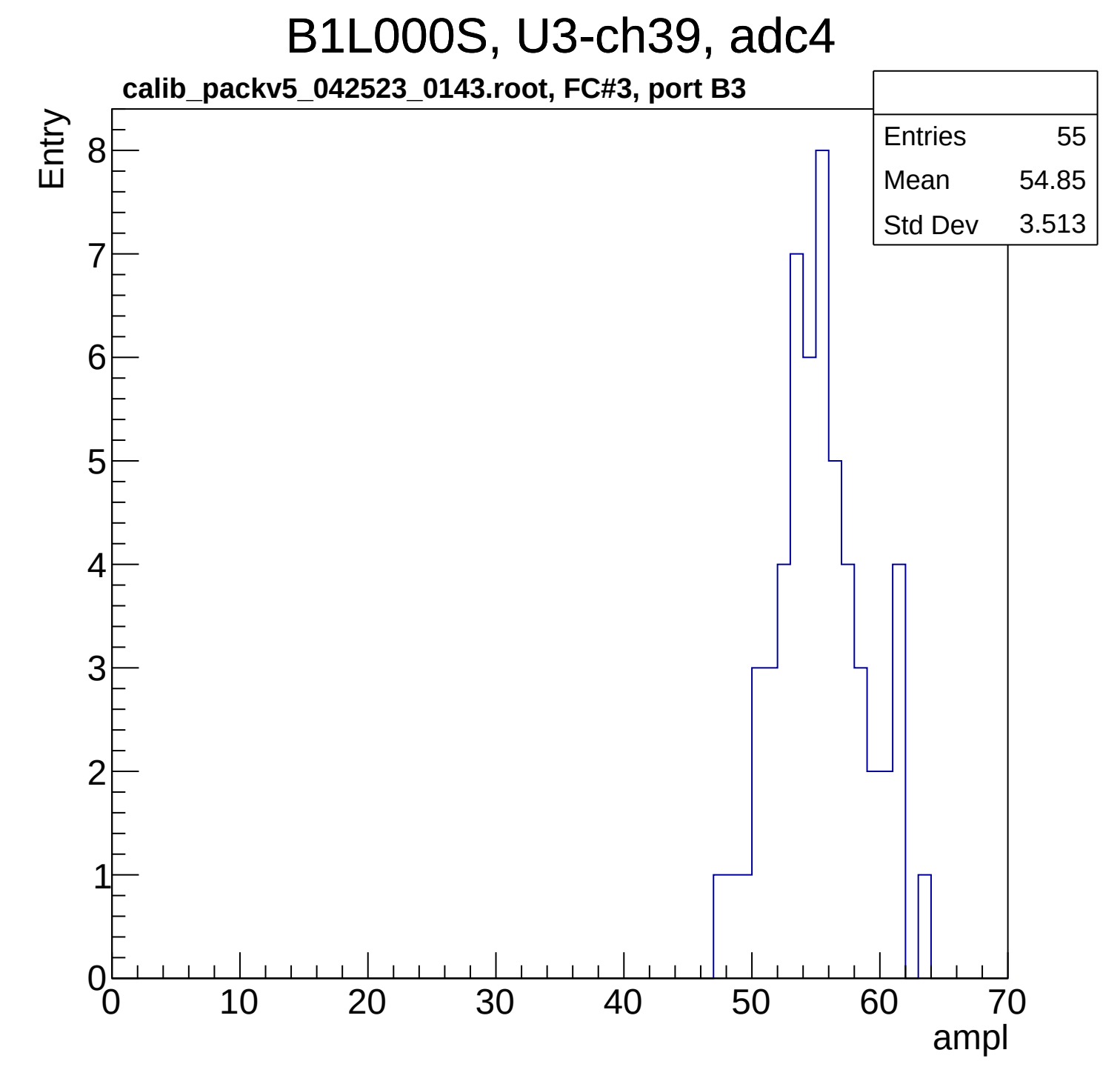
B1L000S, U3-ch39, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 54.85
Std Dev 3.513
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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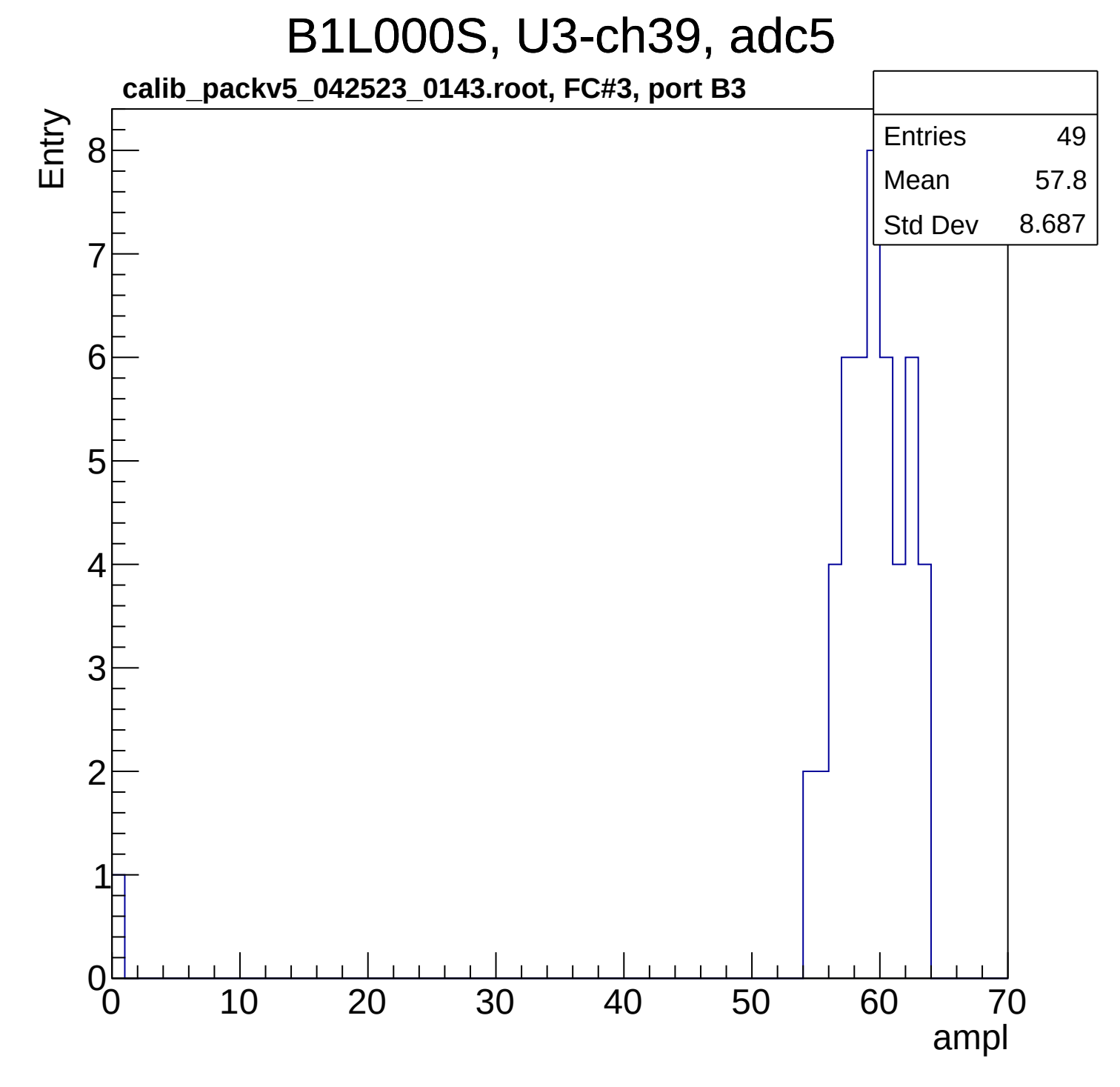
B1L000S, U3-ch39, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 57.8
Std Dev 8.687
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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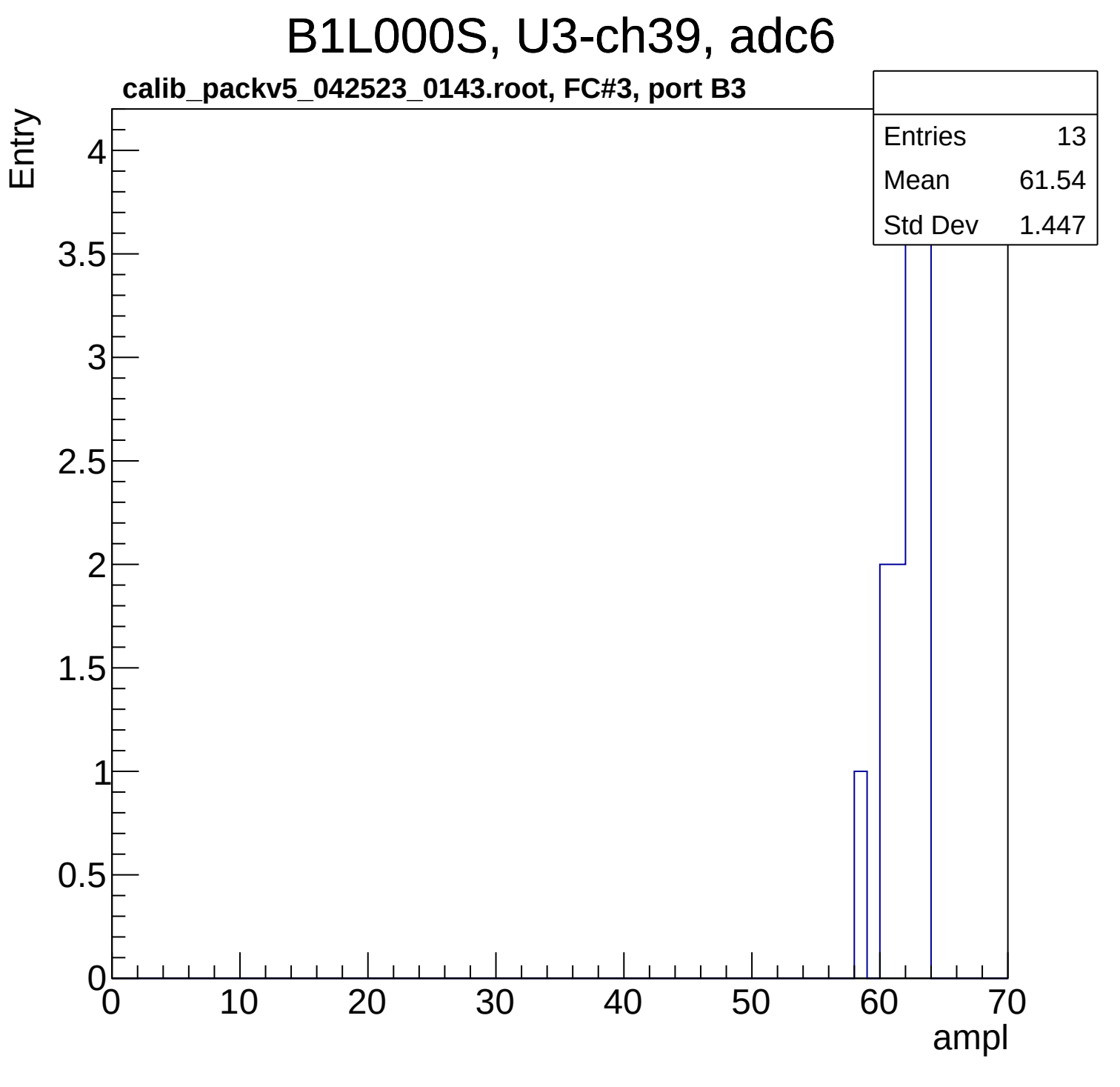
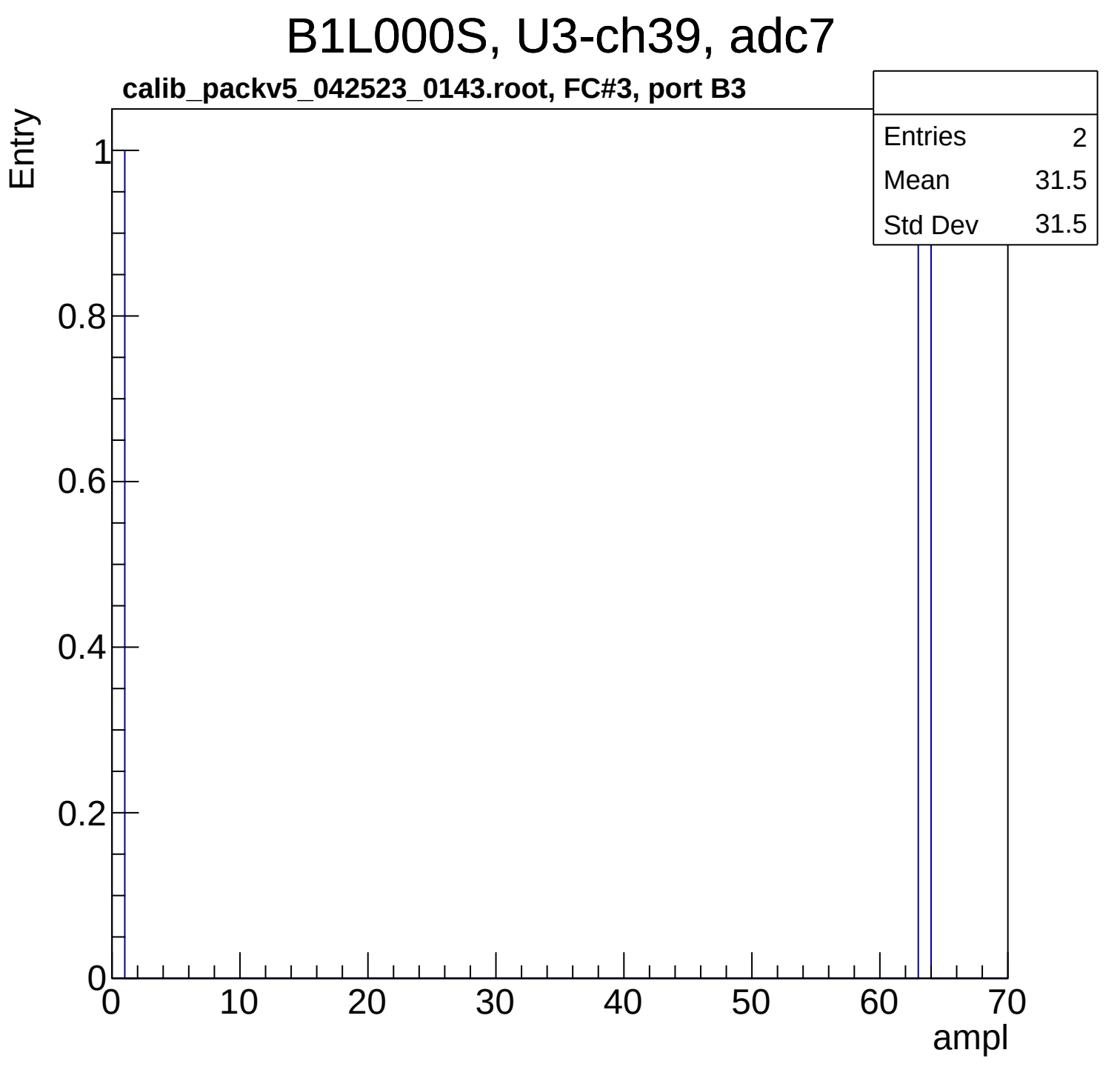

B1L000S, U3-ch39, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.54
Std Dev 1.447
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch39, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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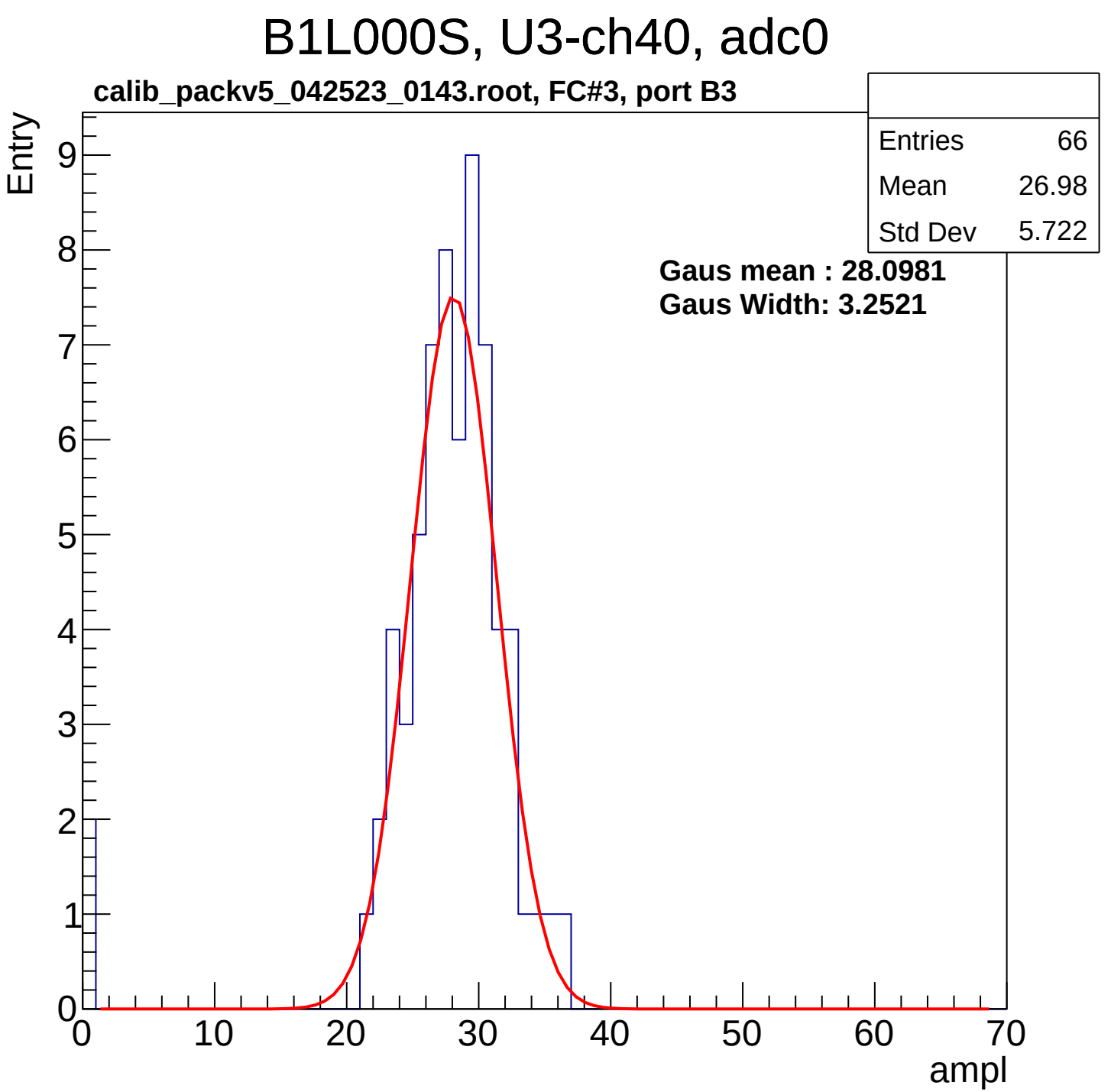
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch40, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 26.98
Std Dev 5.722
Gaus mean : 28.0981
Gaus Width: 3.2521
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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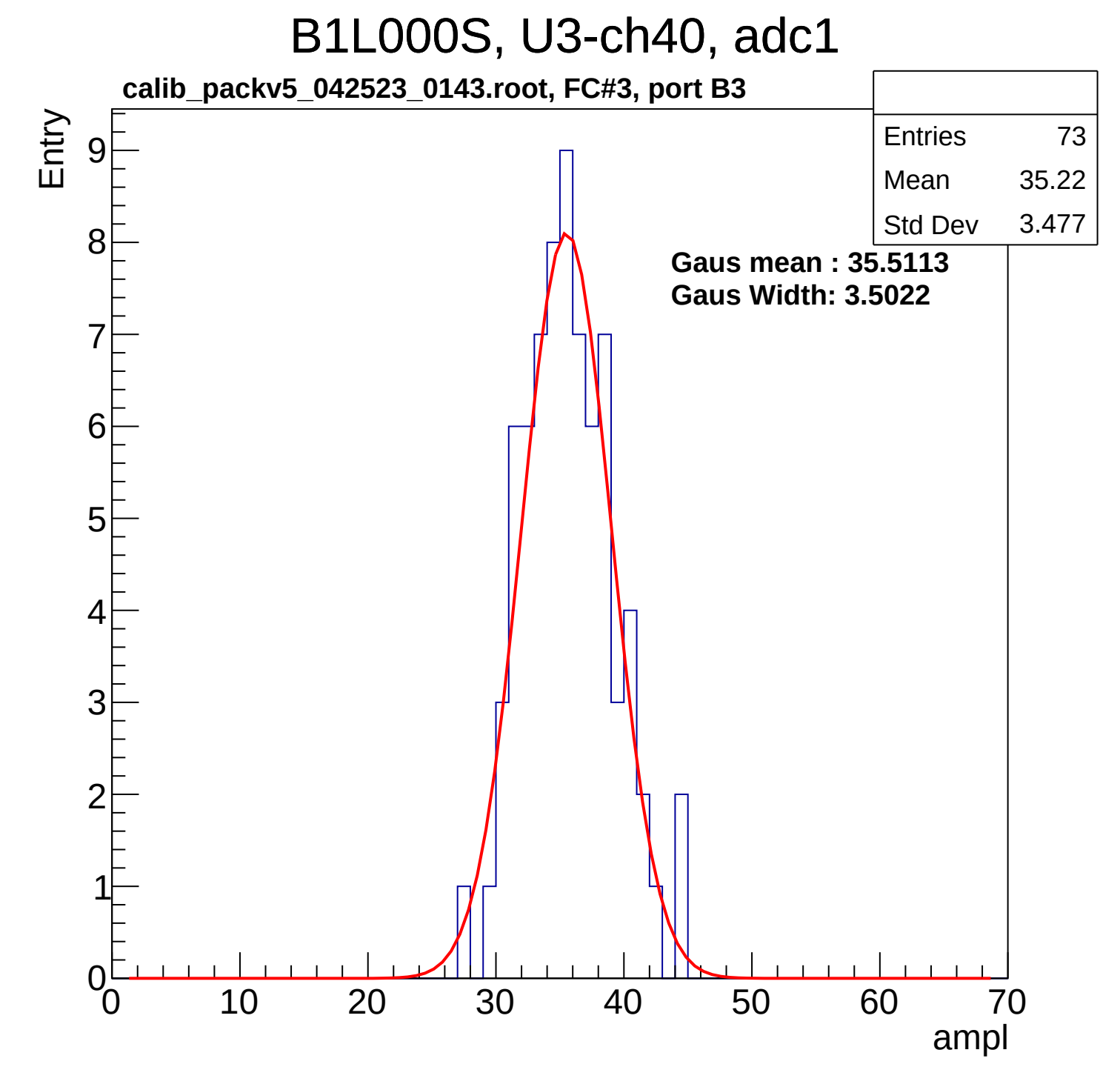
B1L000S, U3-ch40, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 35.22
Std Dev 3.477
Gaus mean : 35.5113
Gaus Width: 3.5022
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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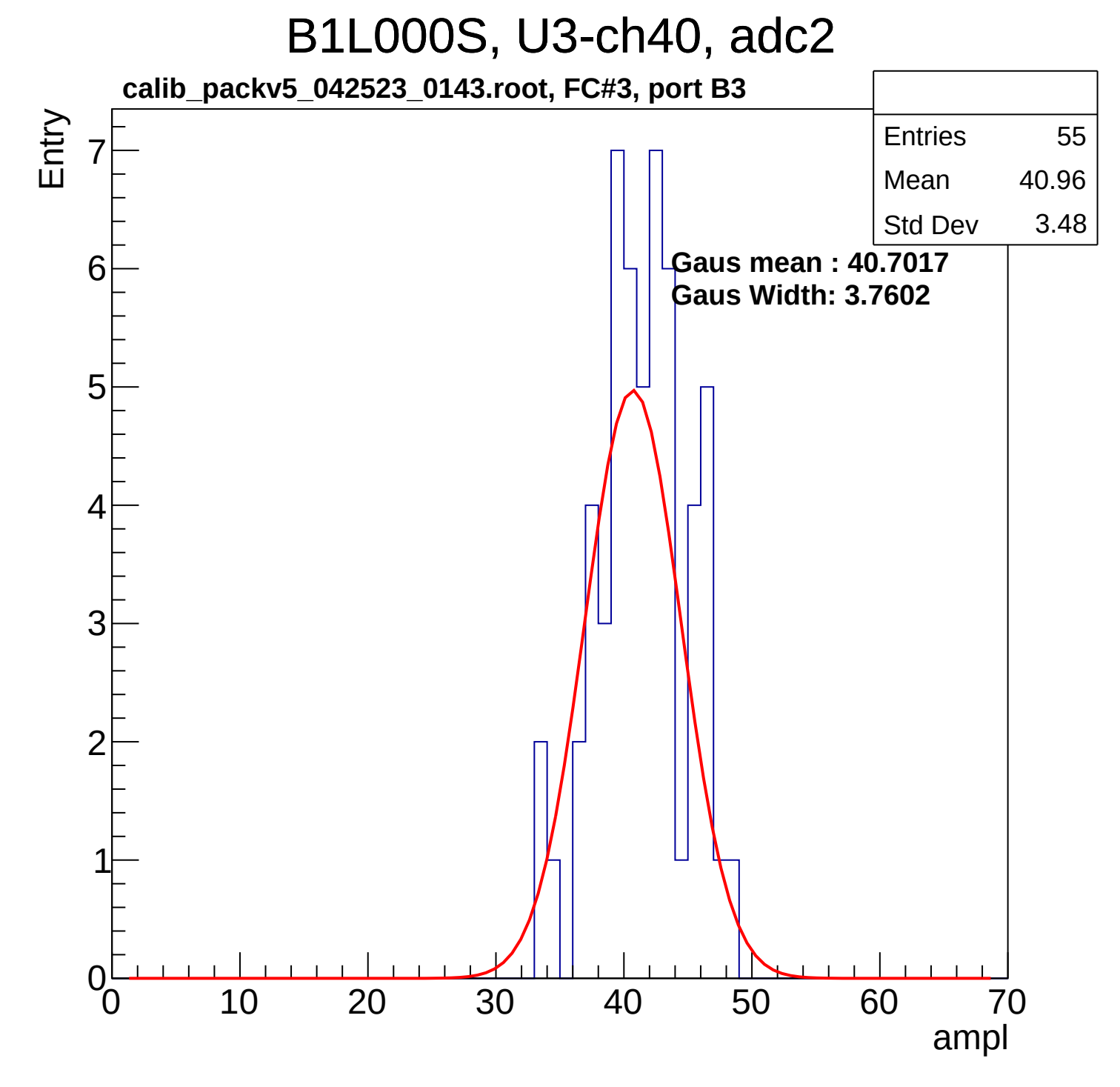
B1L000S, U3-ch40, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 40.96
Std Dev 3.48
Gaus mean : 40.7017
Gaus Width: 3.7602
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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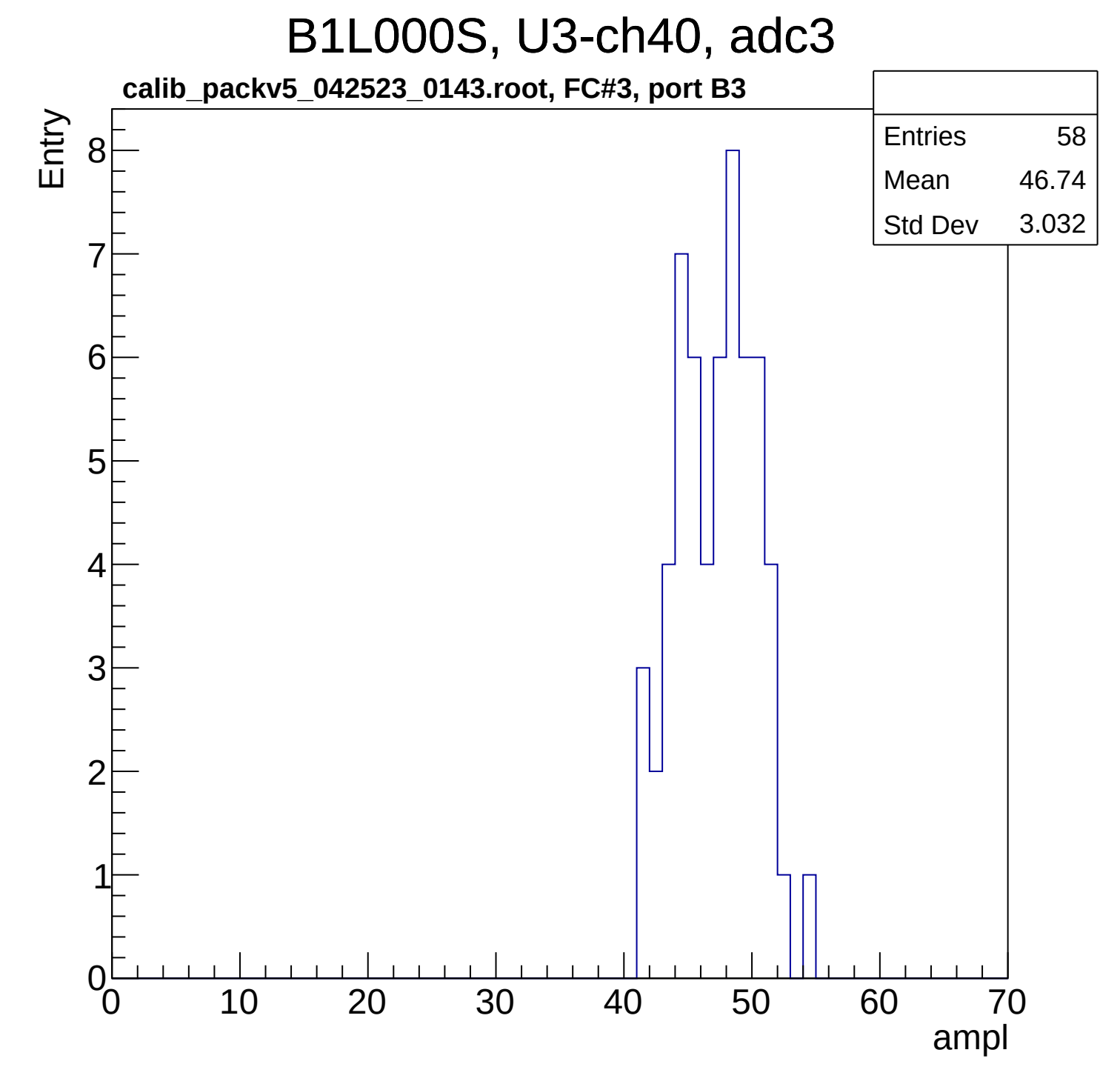
B1L000S, U3-ch40, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 46.74
Std Dev 3.032
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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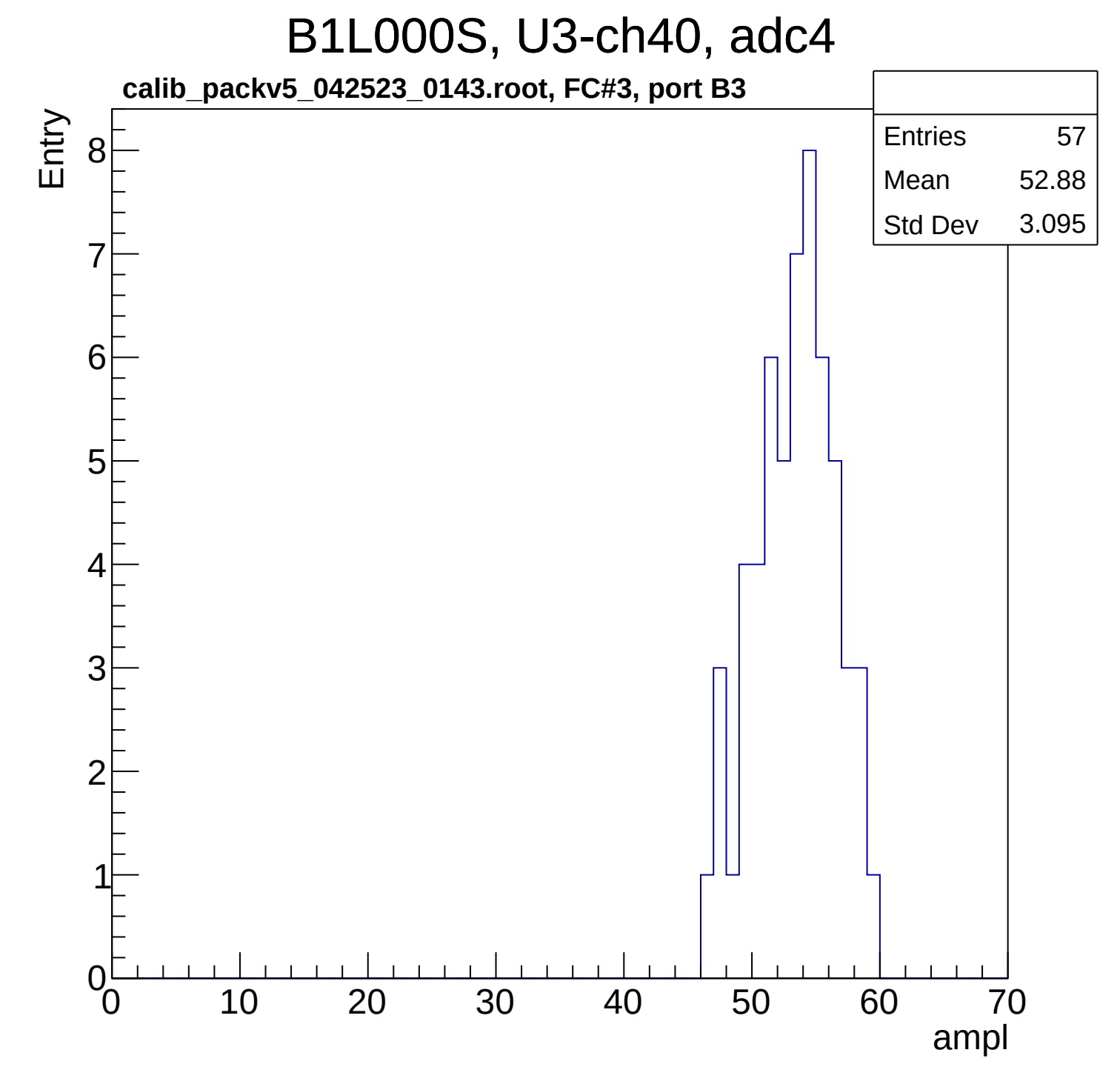
B1L000S, U3-ch40, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 52.88
Std Dev 3.095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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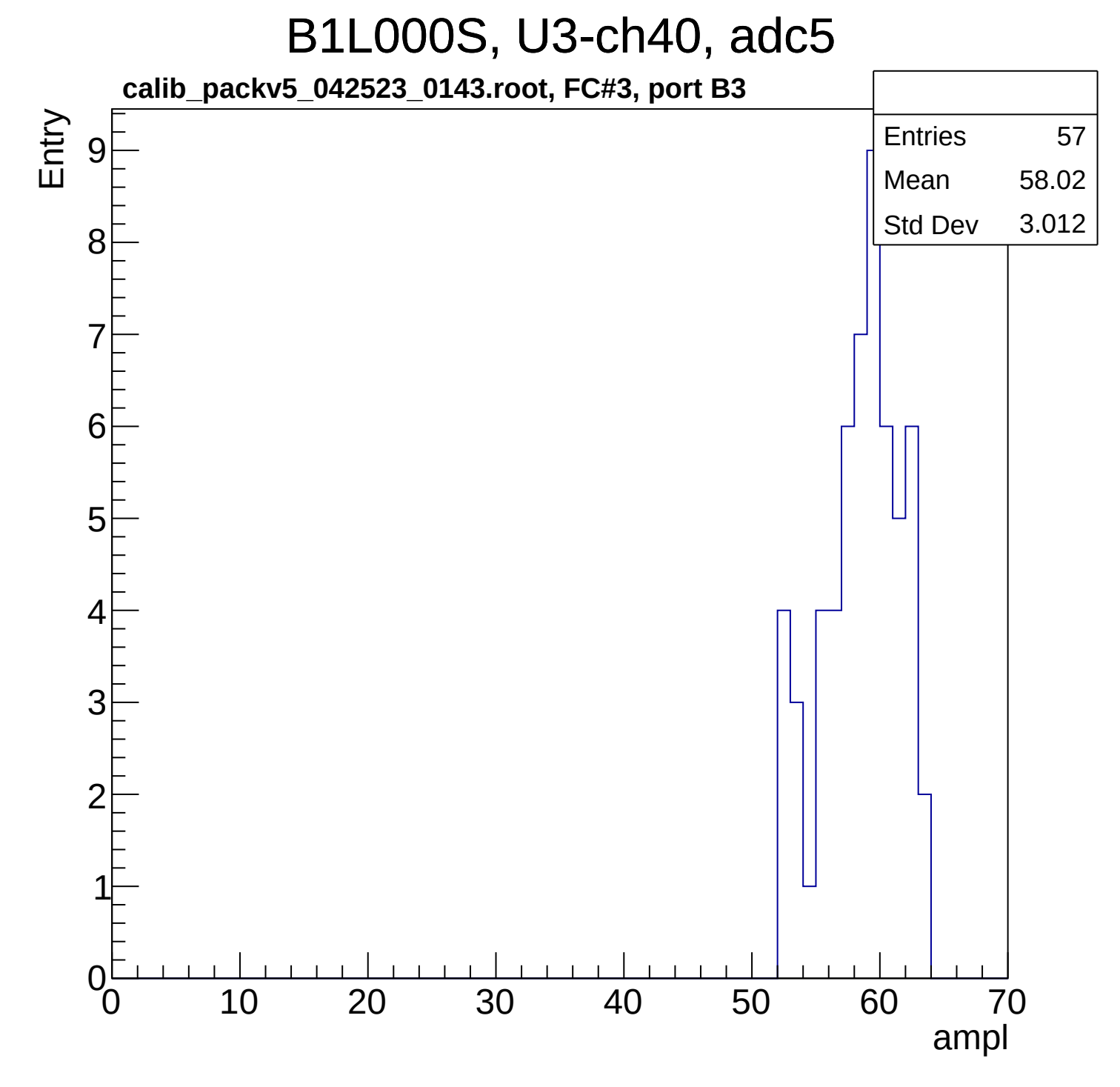
B1L000S, U3-ch40, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.02
Std Dev 3.012
Entry
ampl
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2
3
4
5
6
7
8
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0
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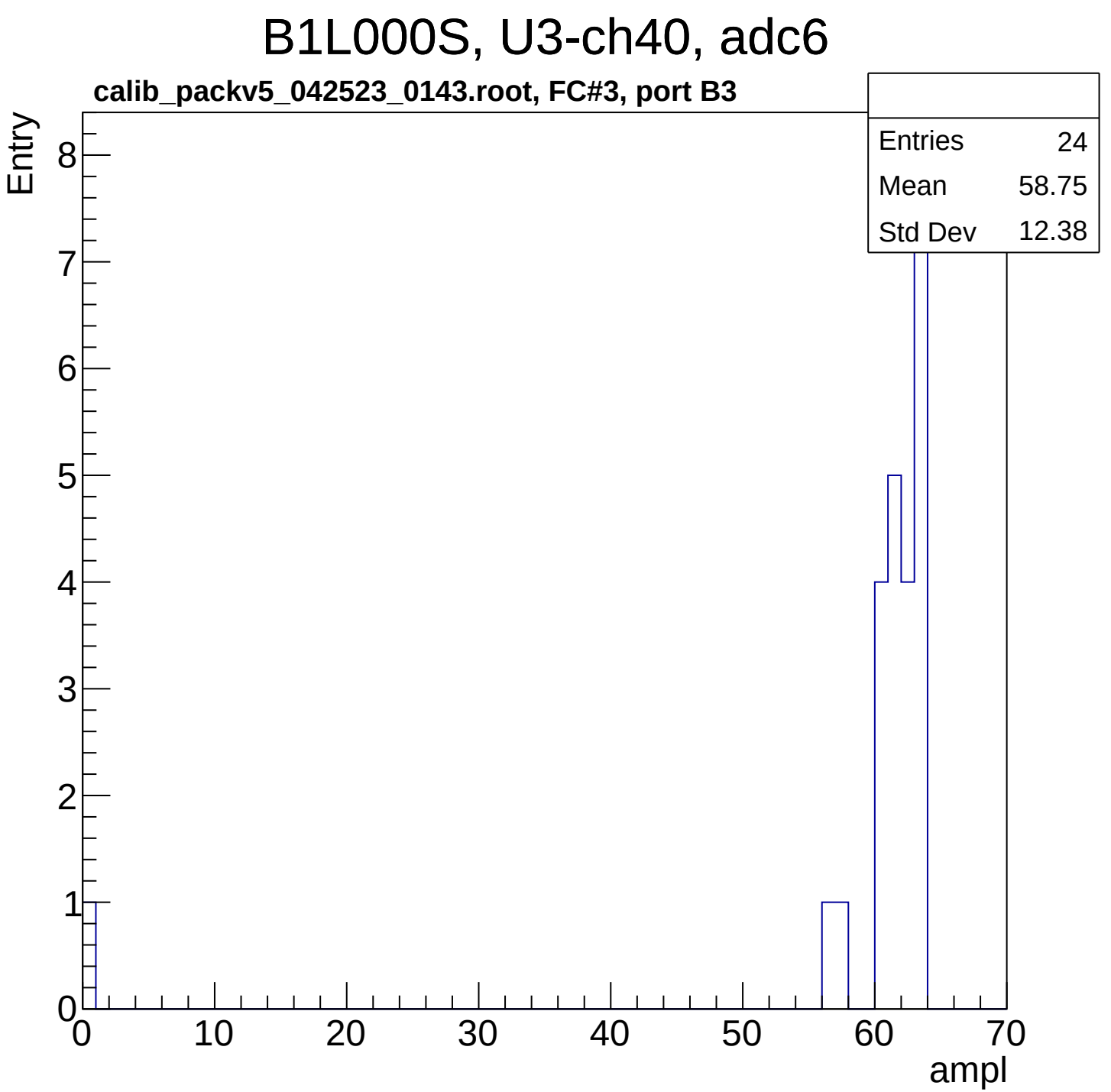
B1L000S, U3-ch40, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 24
Mean 58.75
Std Dev 12.38
Entry
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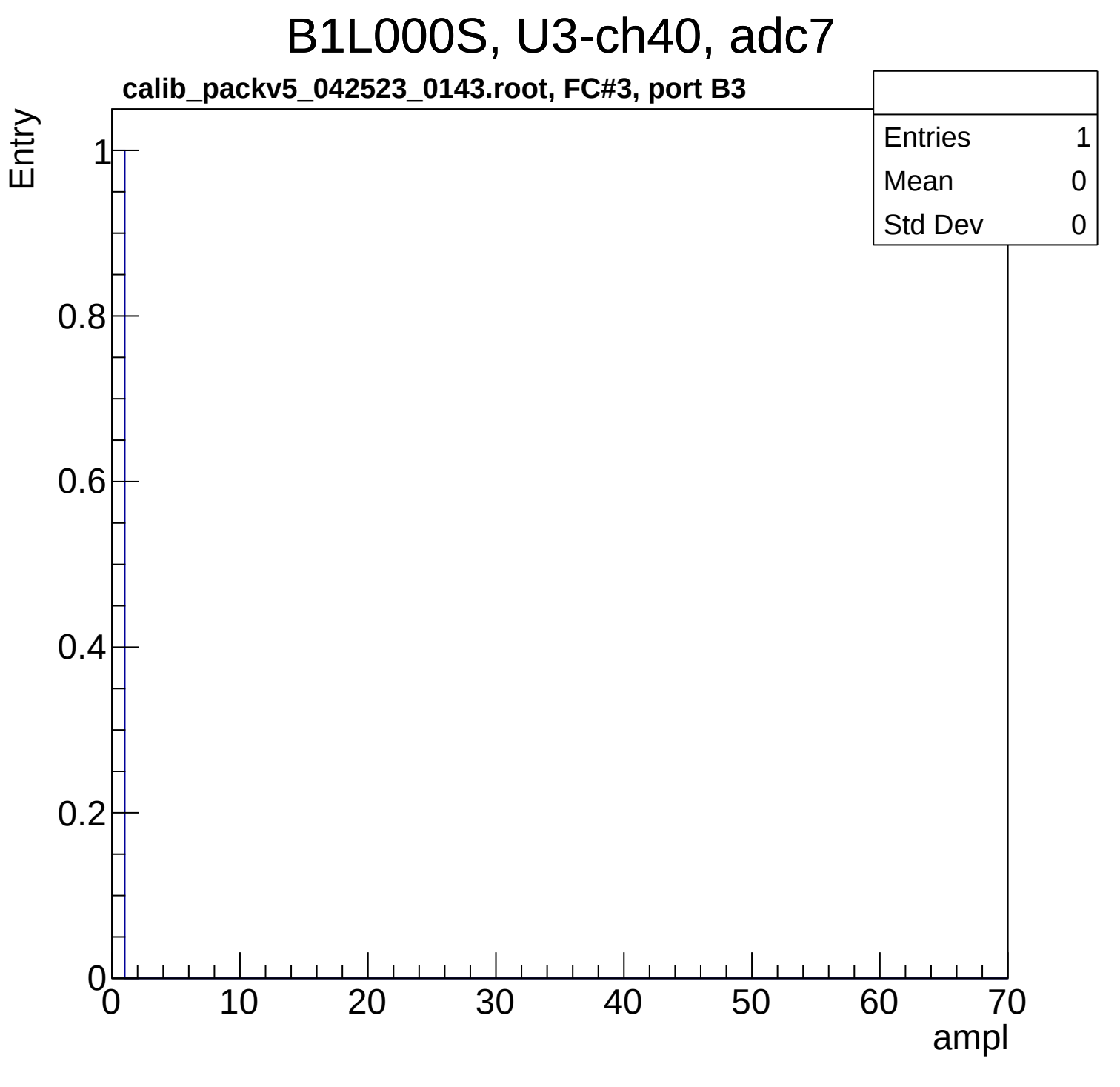
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch40, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
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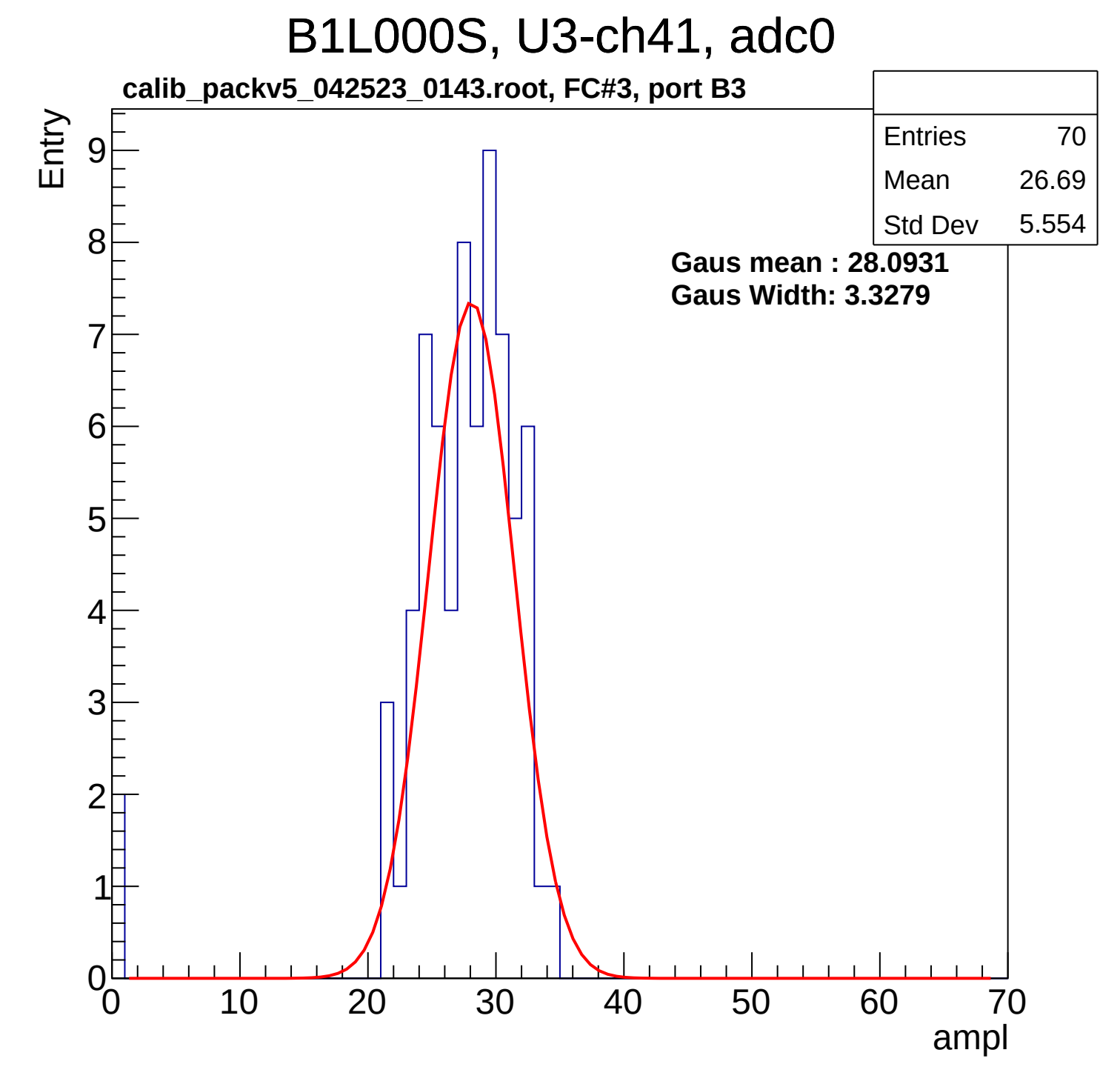
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch41, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 26.69
Std Dev 5.554
Gaus mean : 28.0931
Gaus Width: 3.3279
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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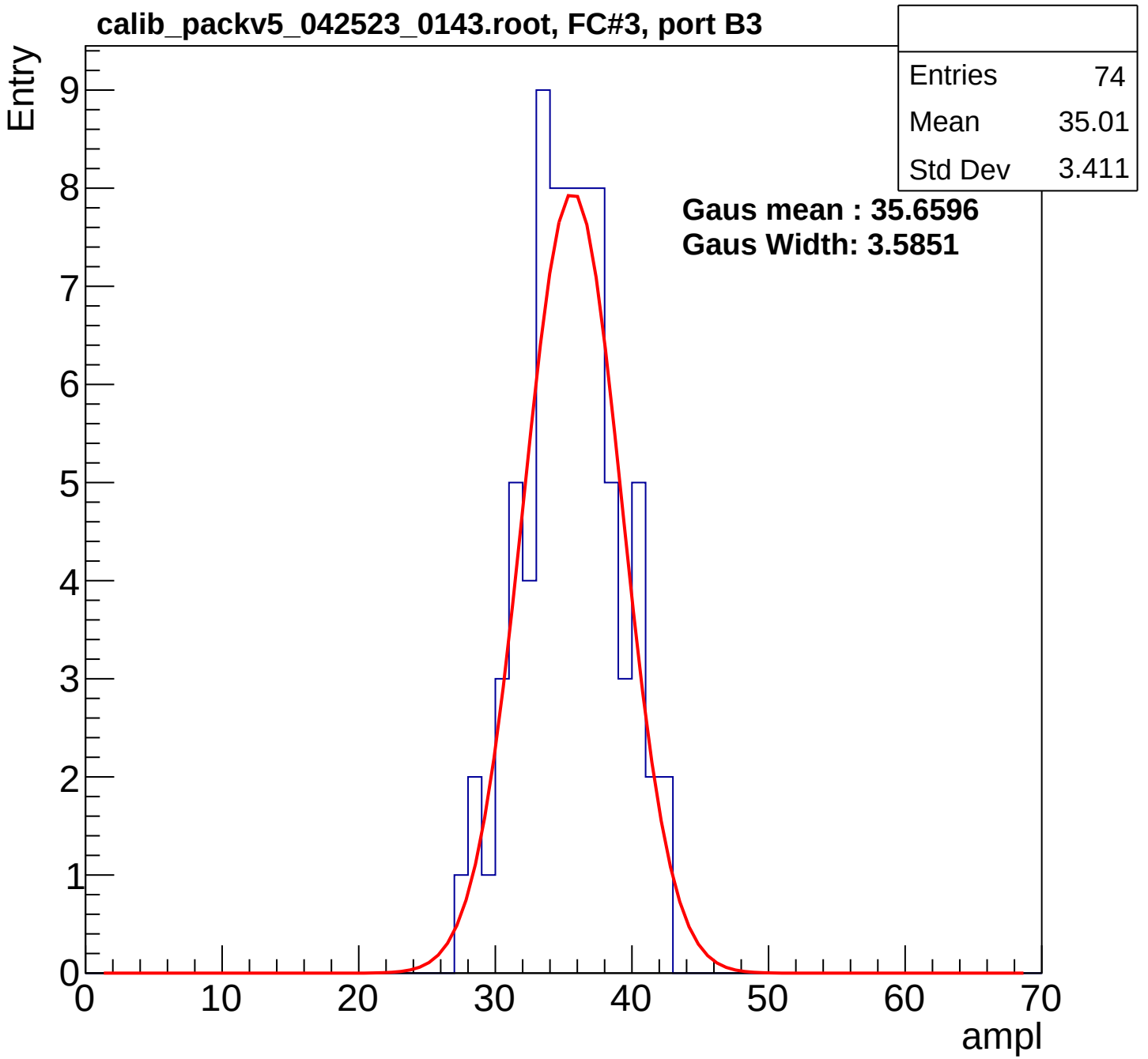
B1L000S, U3-ch41, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 35.01
Std Dev 3.411
Gaus mean : 35.6596
Gaus Width: 3.5851
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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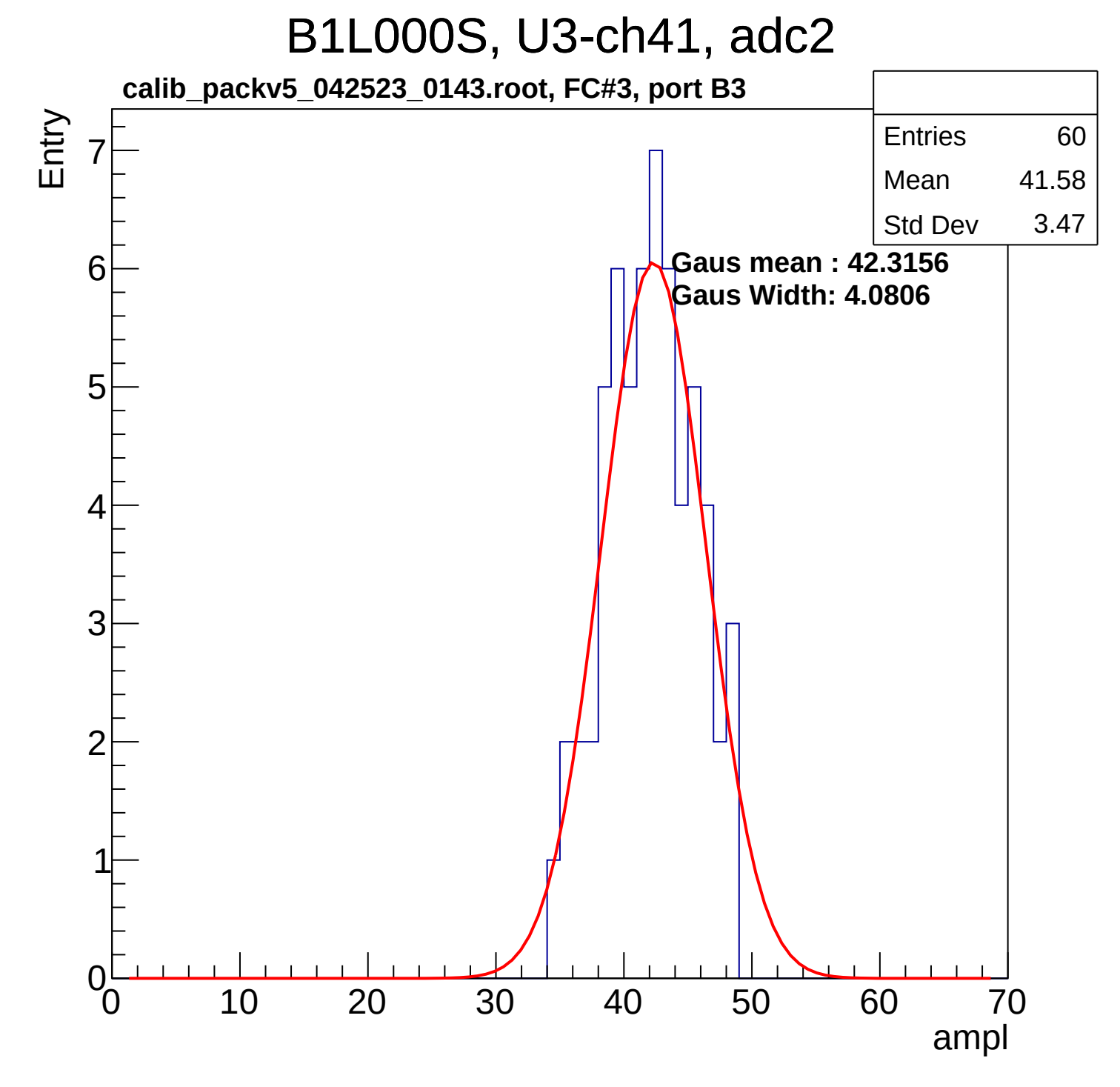
B1L000S, U3-ch41, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 41.58
Std Dev 3.47
Gaus mean : 42.3156
Gaus Width: 4.0806
Entry
ampl
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1
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3
4
5
6
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0
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60
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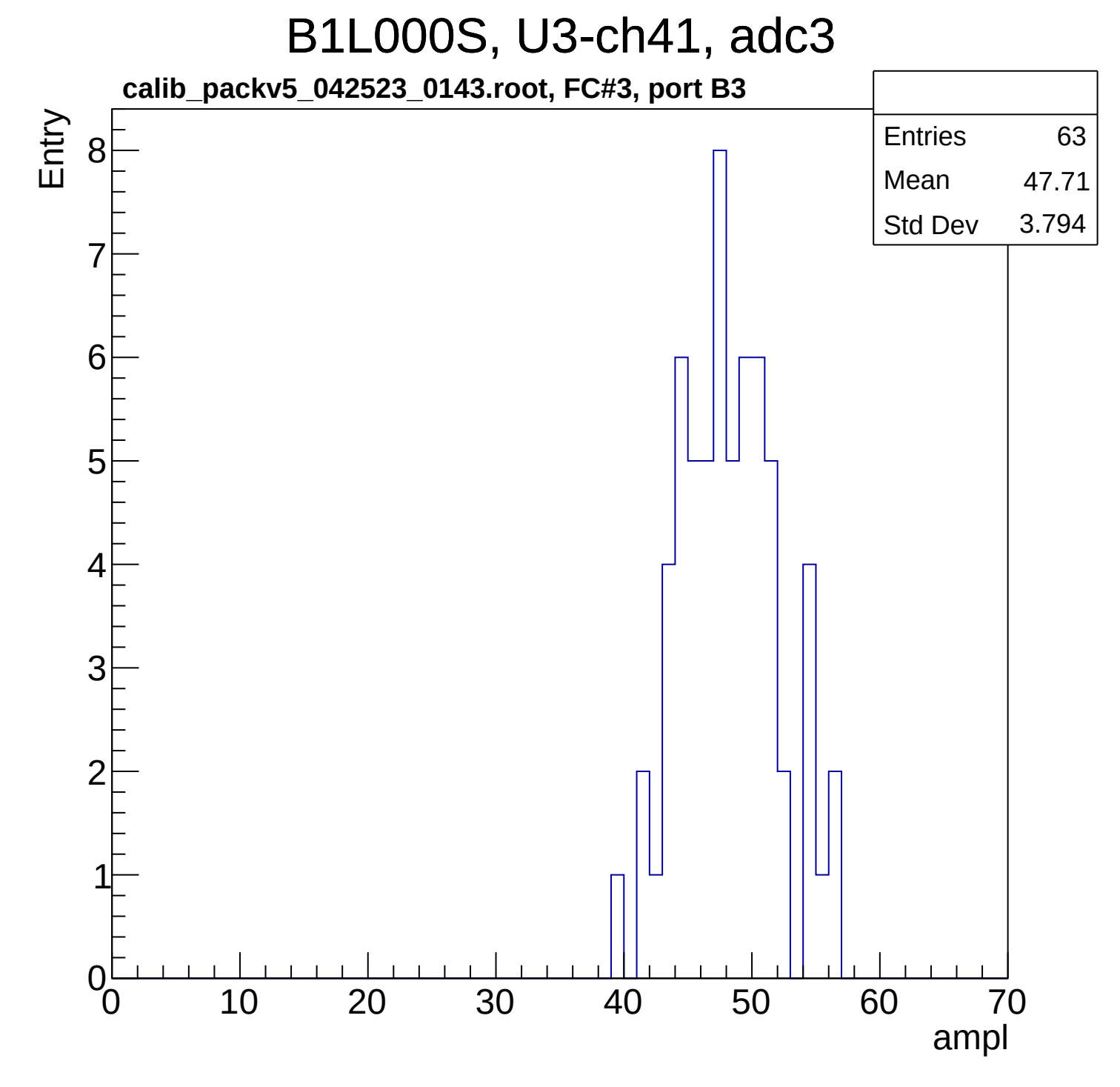
B1L000S, U3-ch41, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 47.71
Std Dev 3.794
Entry
ampl
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2
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4
5
6
7
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0
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20
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40
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60
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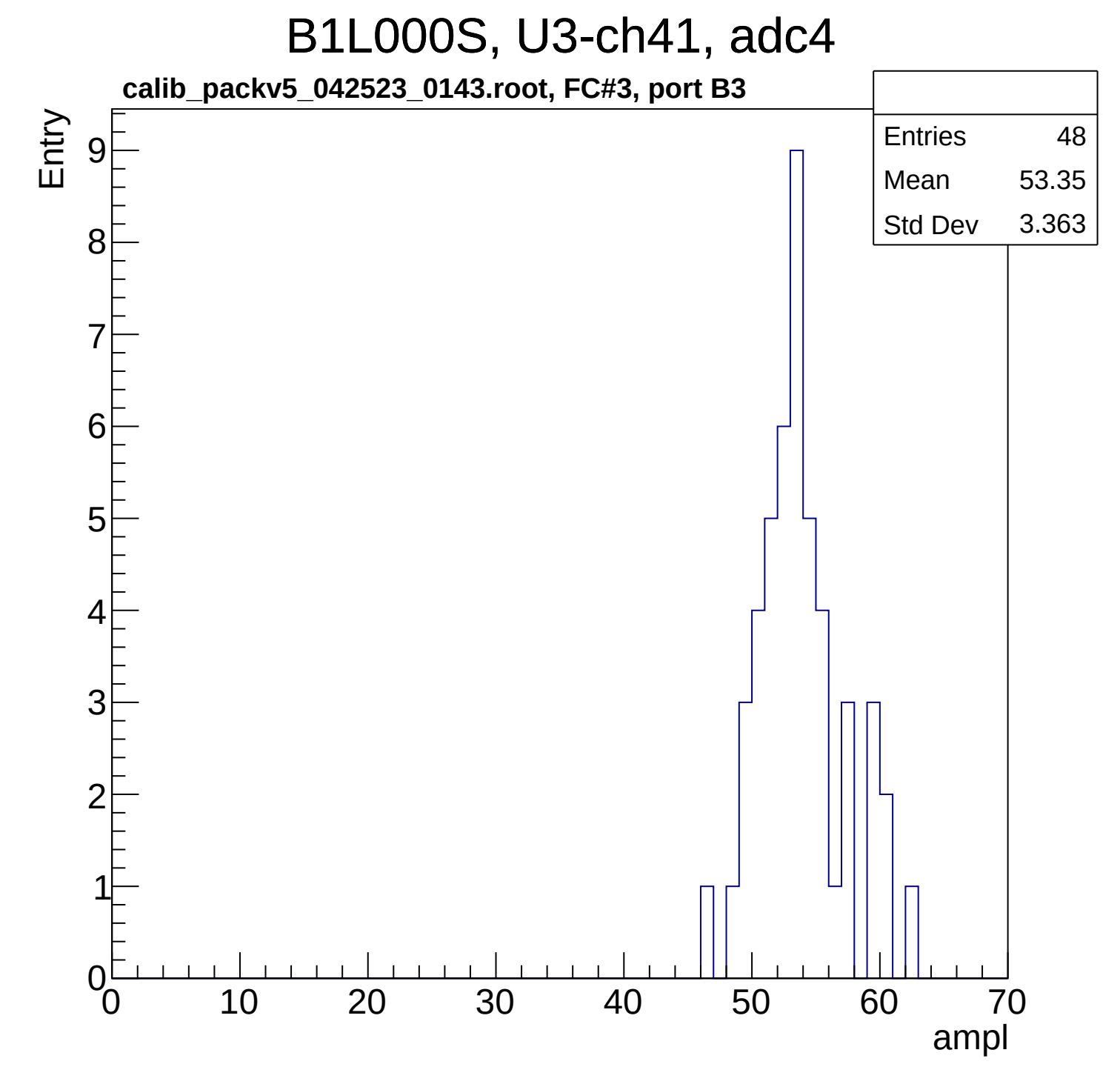
B1L000S, U3-ch41, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 53.35
Std Dev 3.363
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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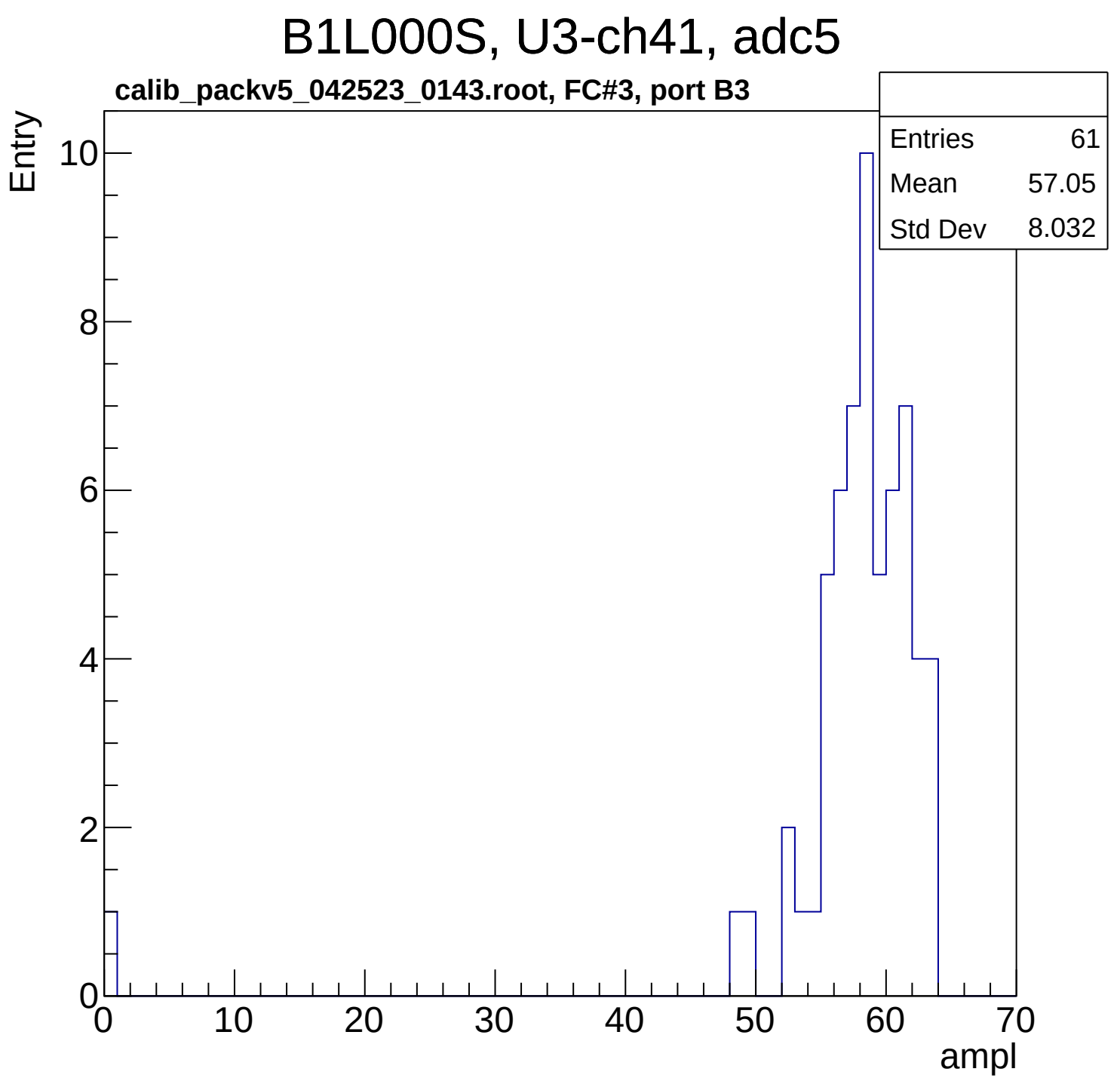
B1L000S, U3-ch41, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 57.05
Std Dev 8.032
Entry
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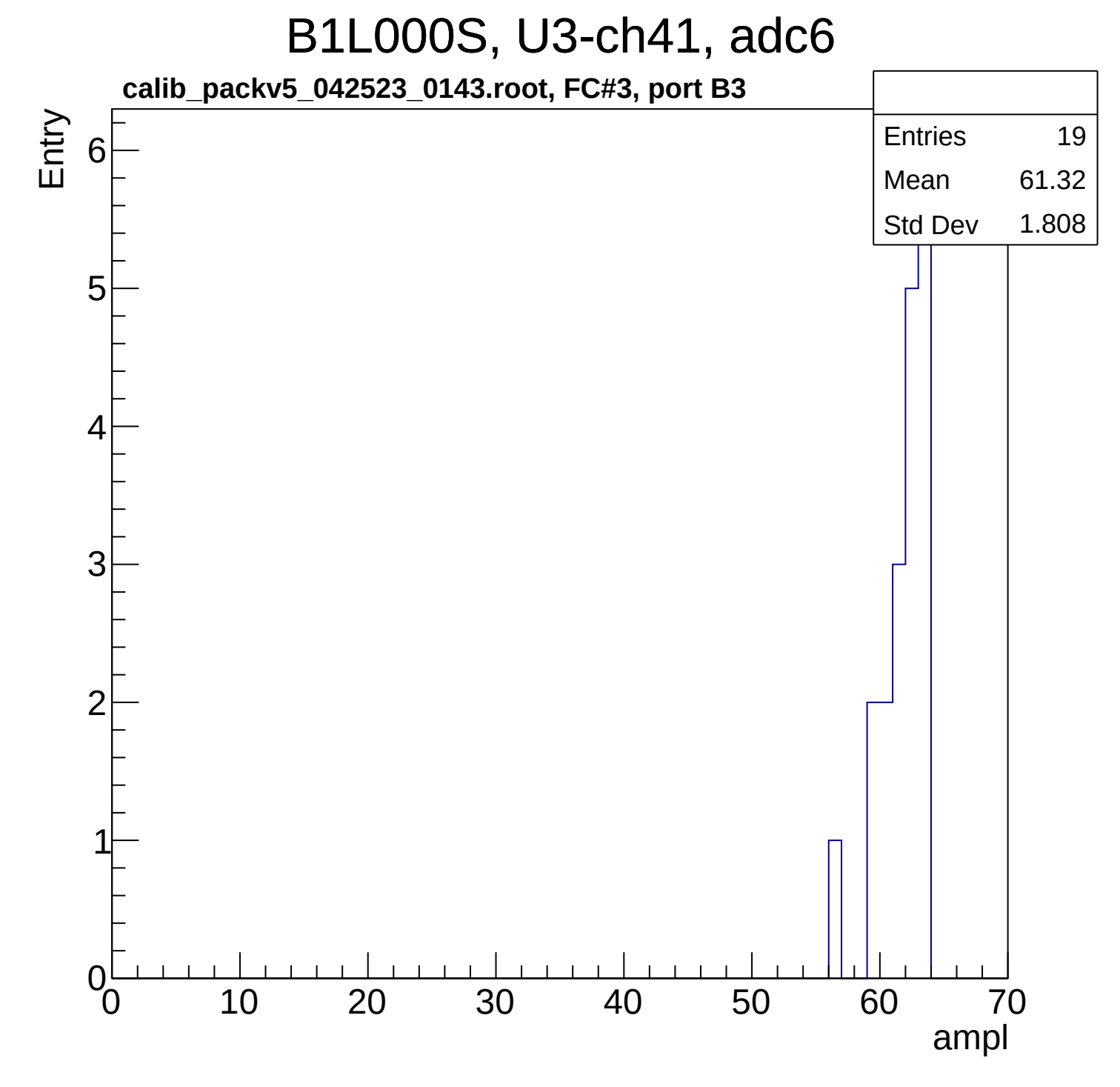
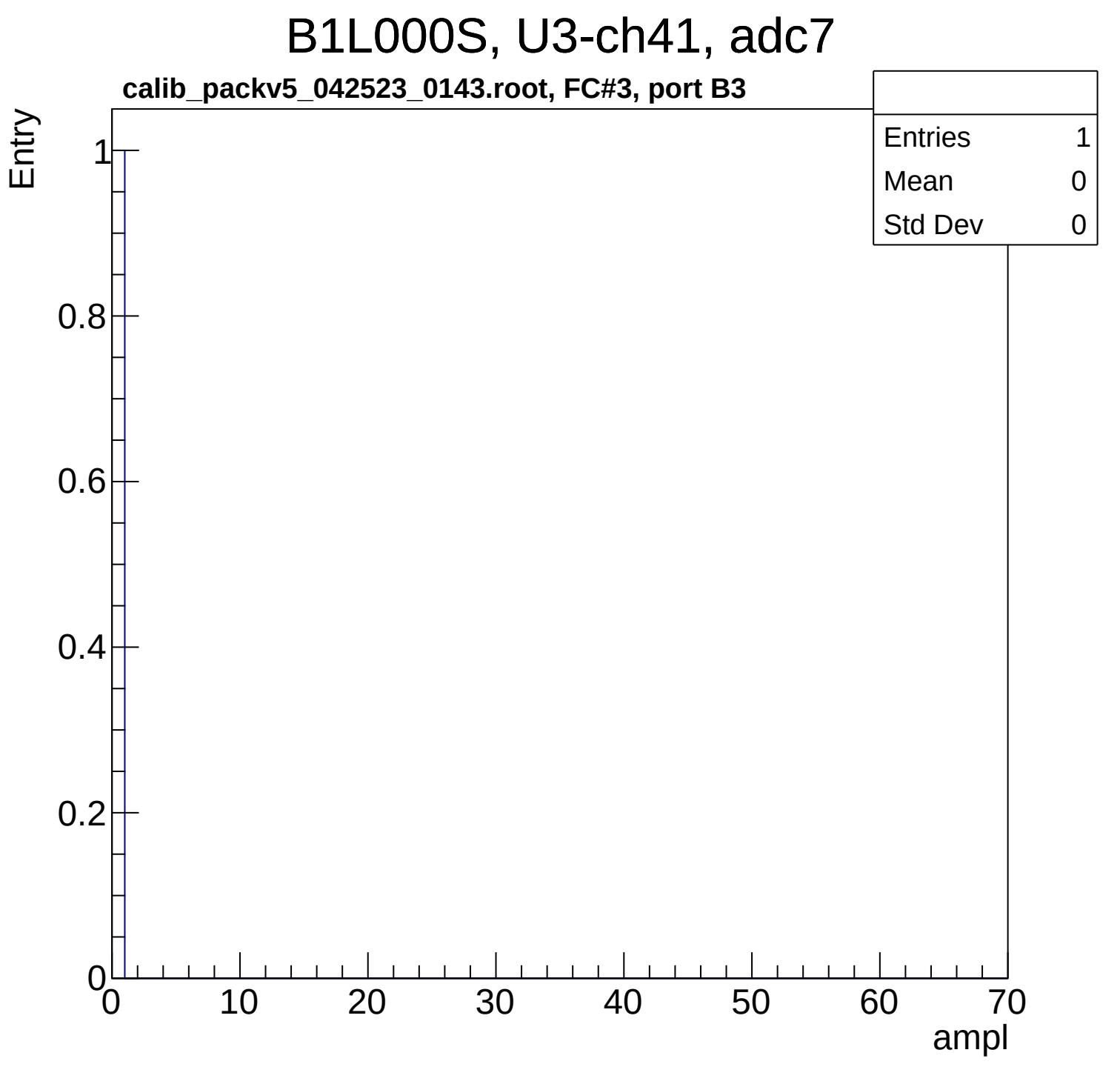
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch41, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 61.32
Std Dev 1.808
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch41, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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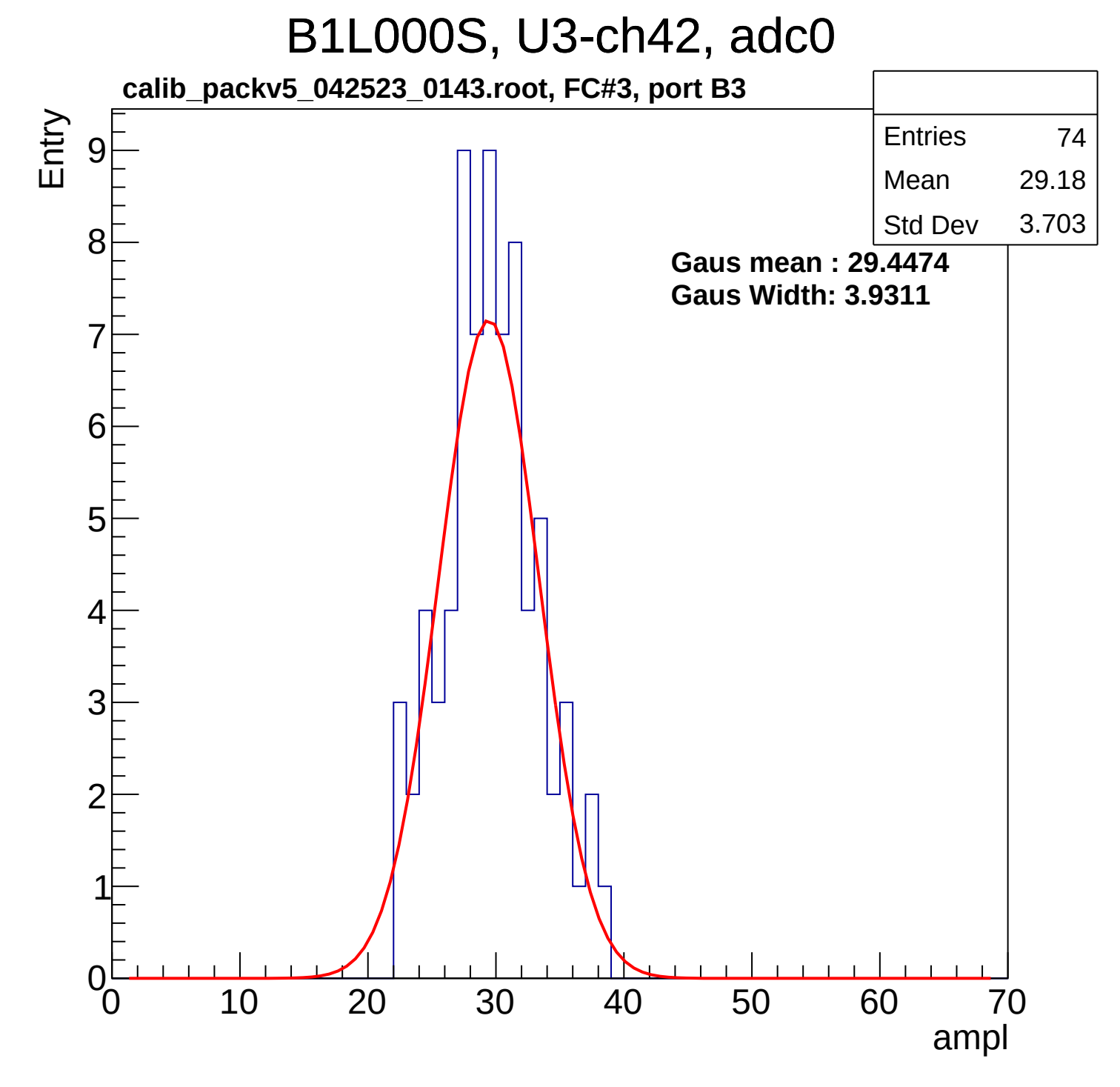
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch42, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 29.18
Std Dev 3.703
Gaus mean : 29.4474
Gaus Width: 3.9311
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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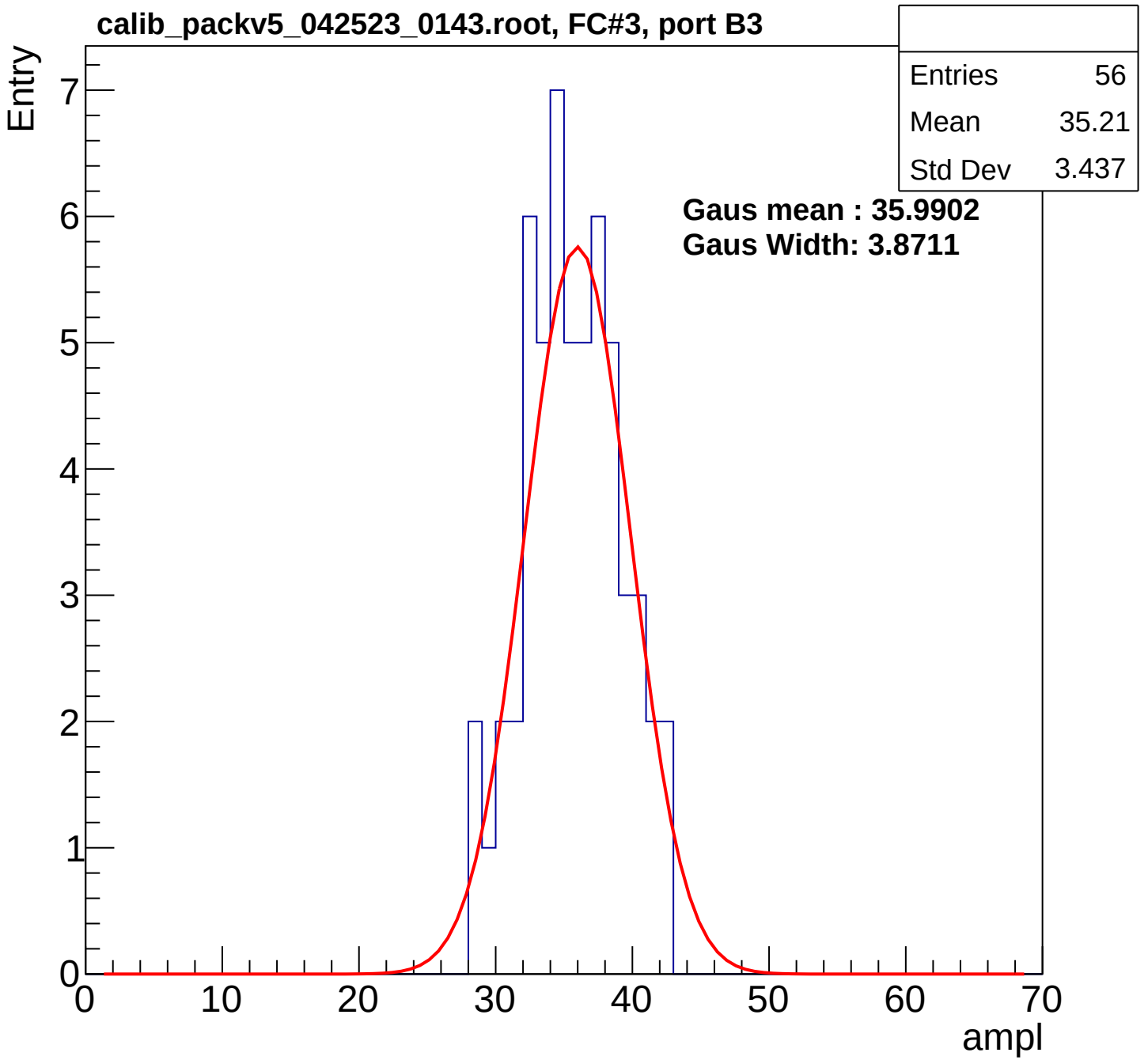
B1L000S, U3-ch42, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 35.21
Std Dev 3.437
Gaus mean : 35.9902
Gaus Width: 3.8711
Entry
ampl
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2
3
4
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6
7
0
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60
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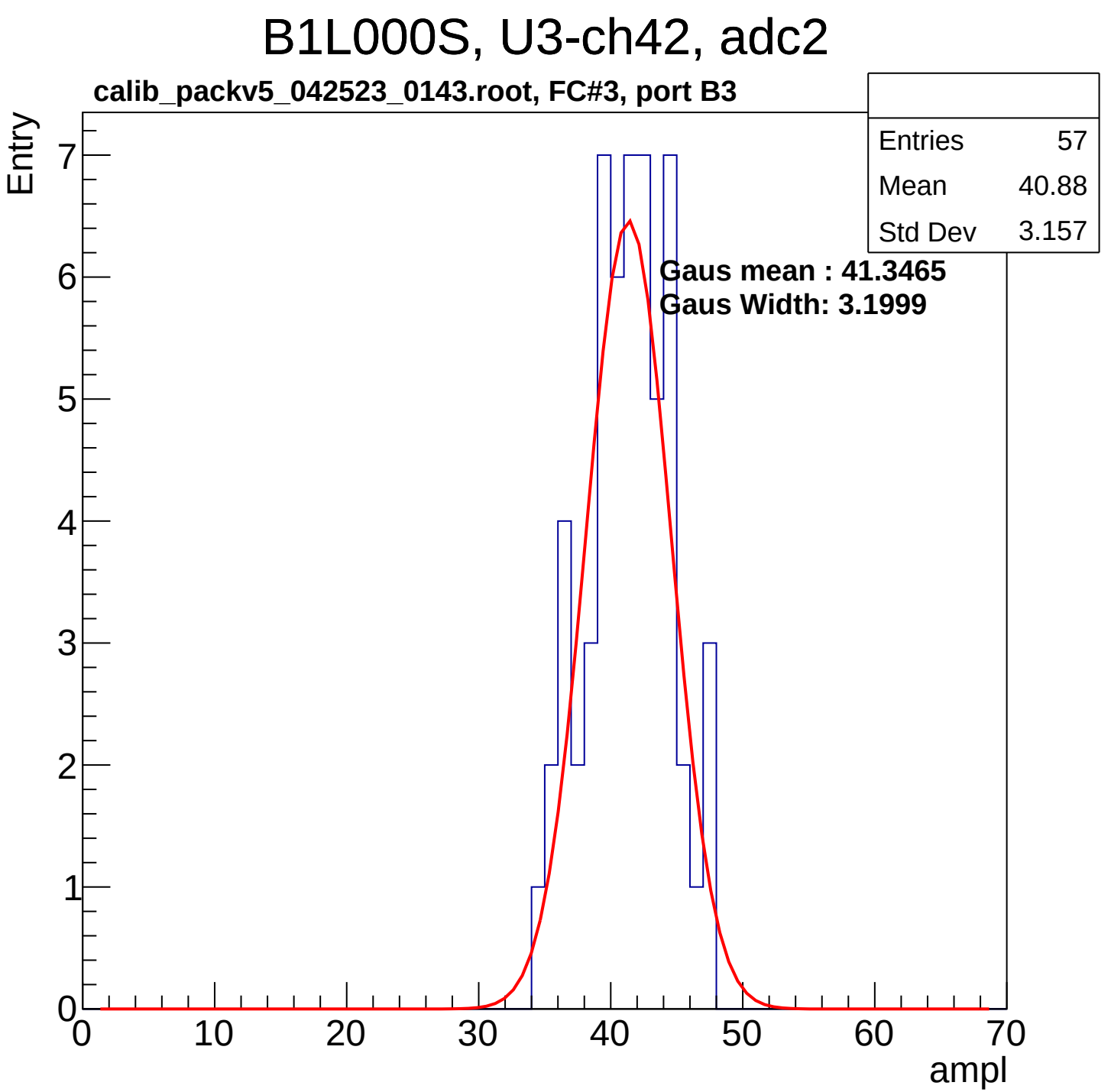
B1L000S, U3-ch42, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 40.88
Std Dev 3.157
Gaus mean : 41.3465
Gaus Width: 3.1999
Entry
ampl
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2
3
4
5
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7
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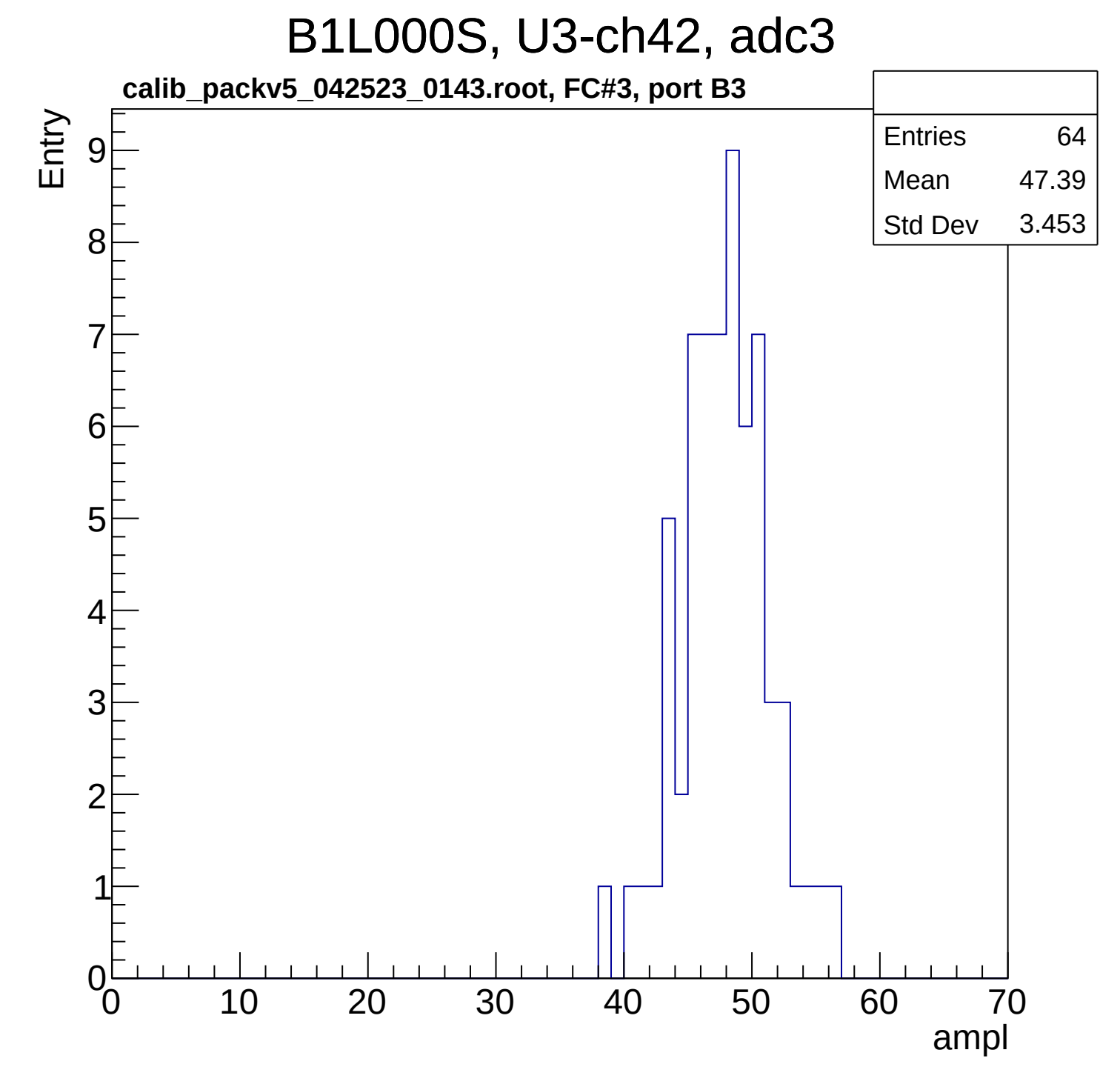
B1L000S, U3-ch42, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 47.39
Std Dev 3.453
Entry
ampl
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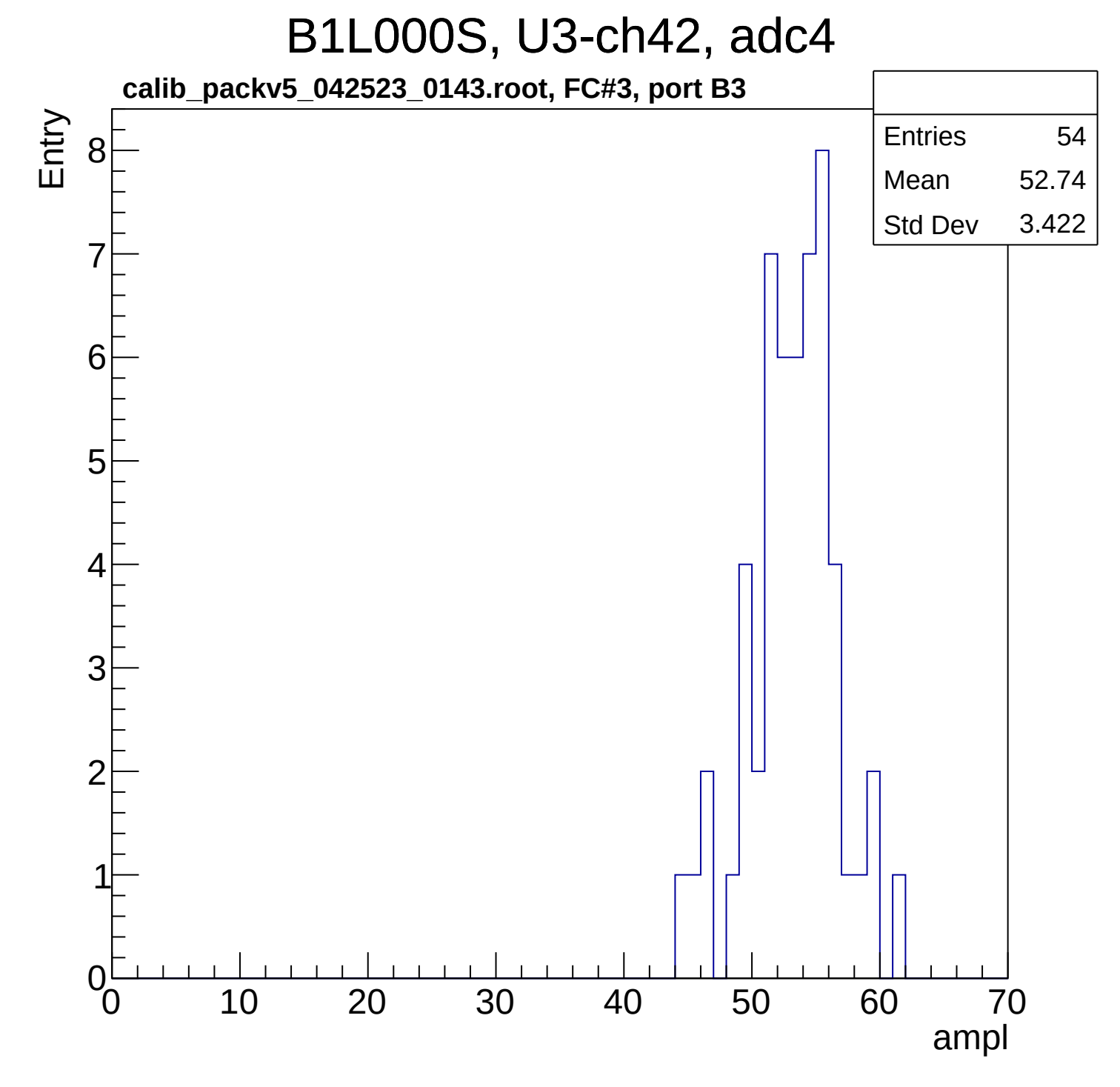
B1L000S, U3-ch42, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 52.74
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
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7
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0
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60
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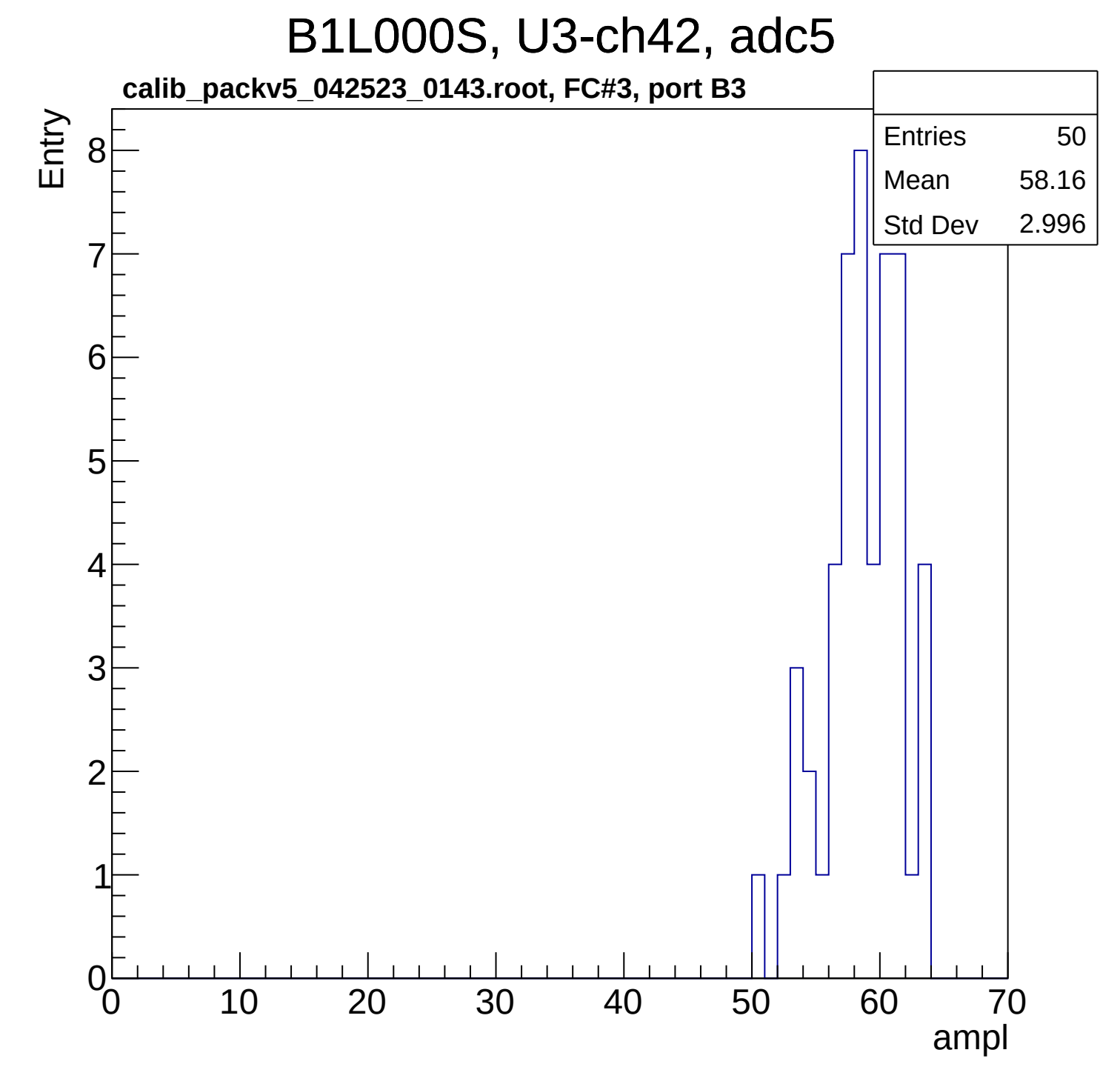
B1L000S, U3-ch42, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.16
Std Dev 2.996
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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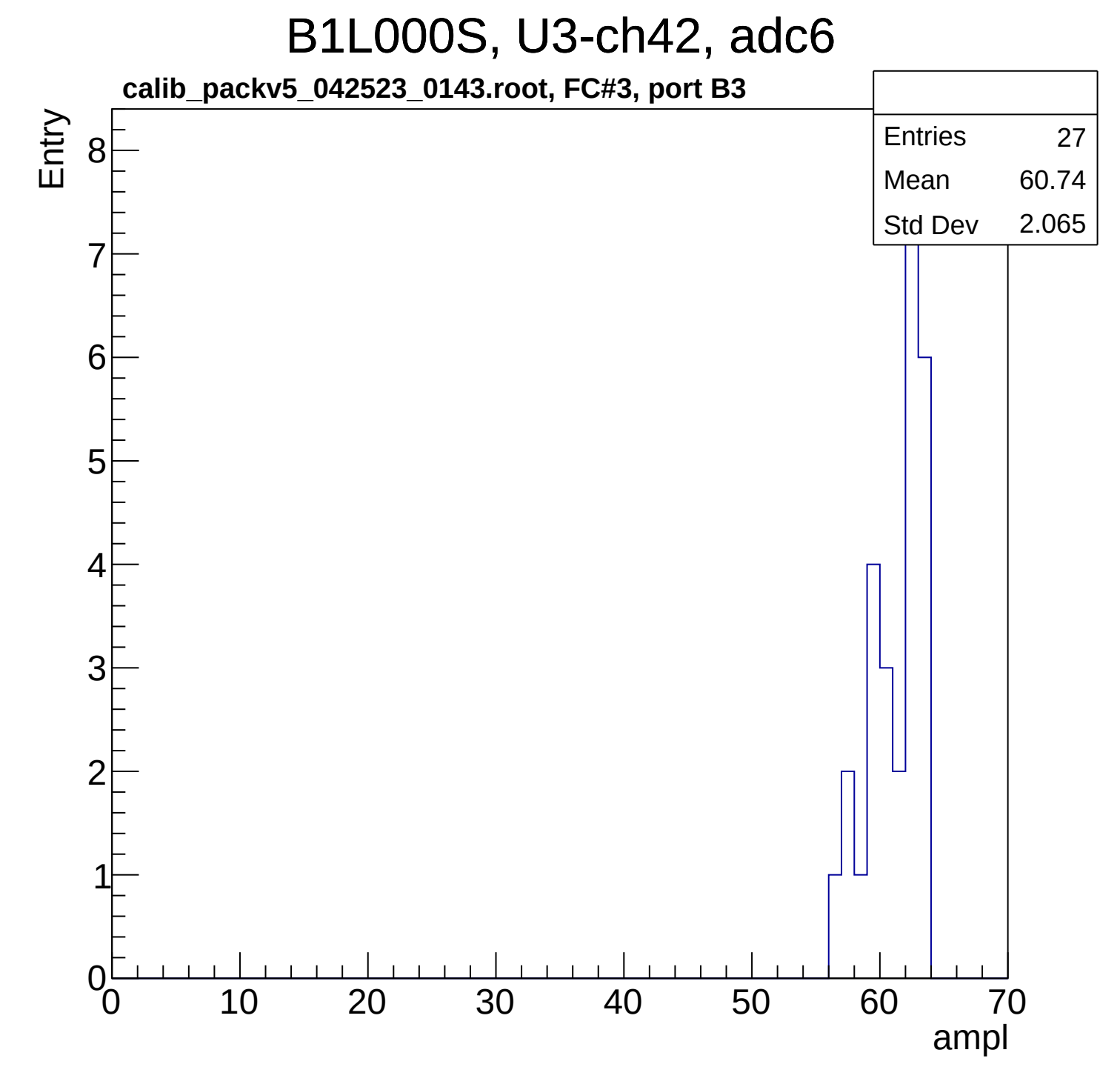
B1L000S, U3-ch42, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 27
Mean 60.74
Std Dev 2.065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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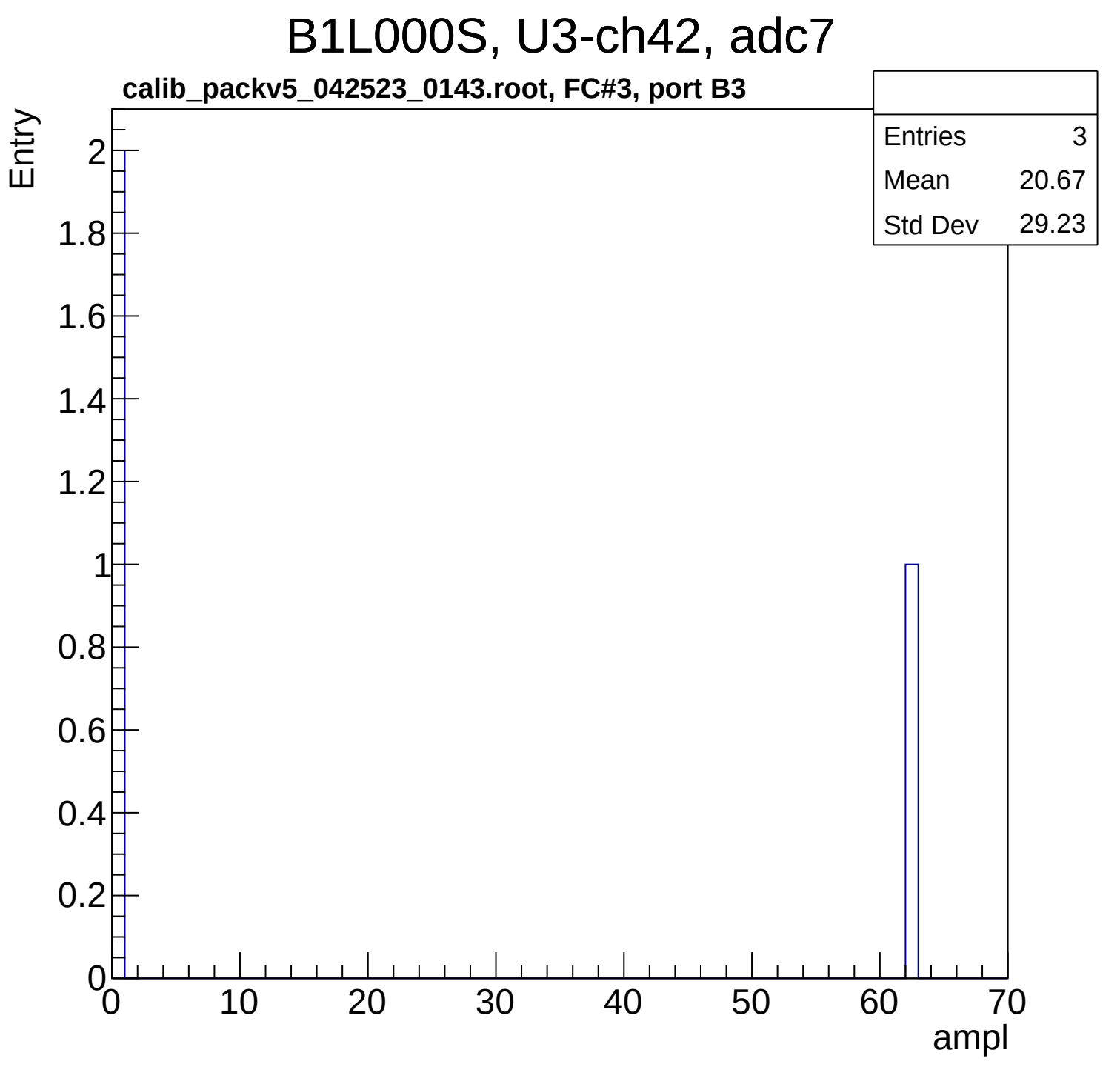
B1L000S, U3-ch42, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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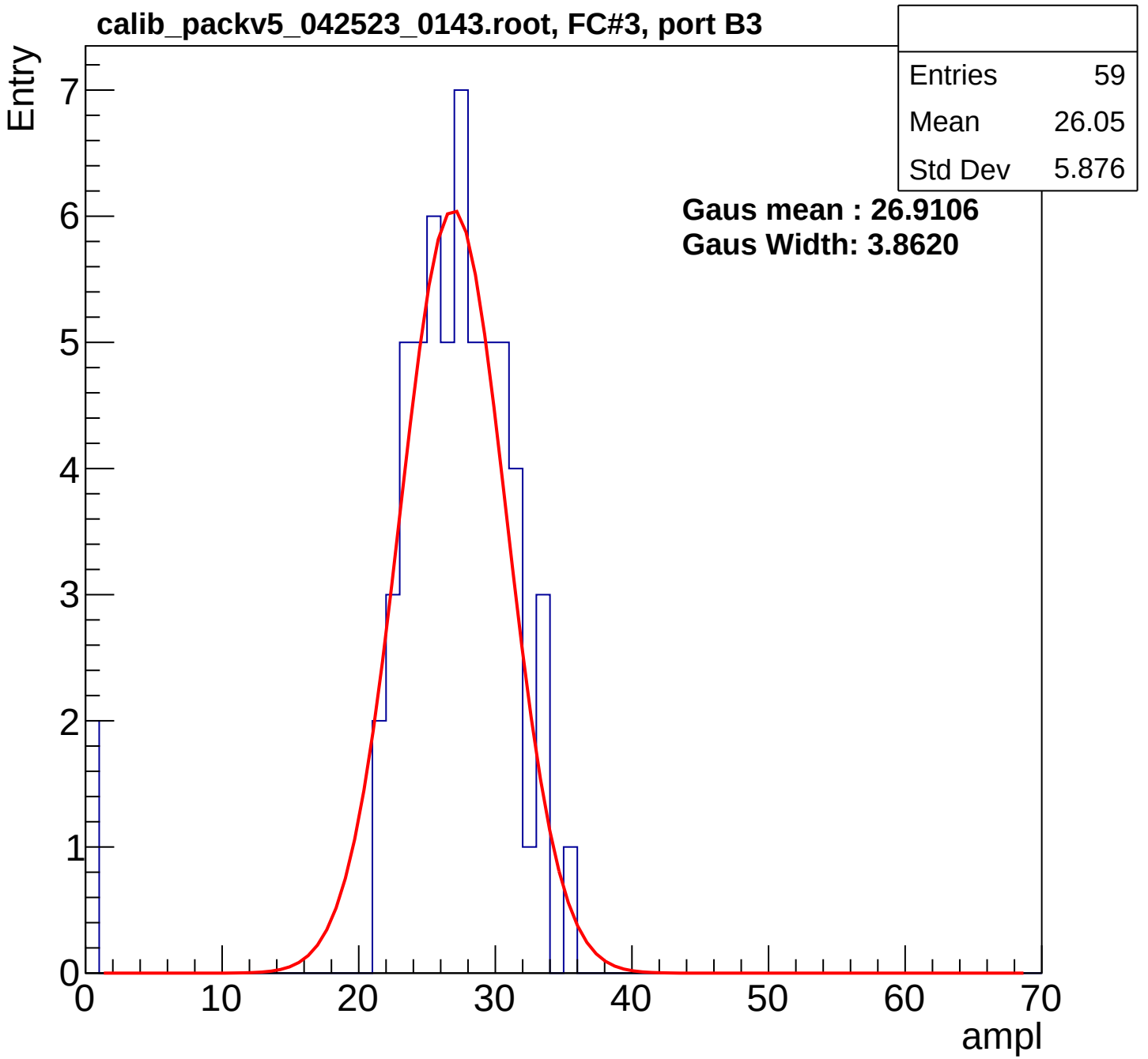
B1L000S, U3-ch43, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 26.05
Std Dev 5.876
Gaus mean : 26.9106
Gaus Width: 3.8620
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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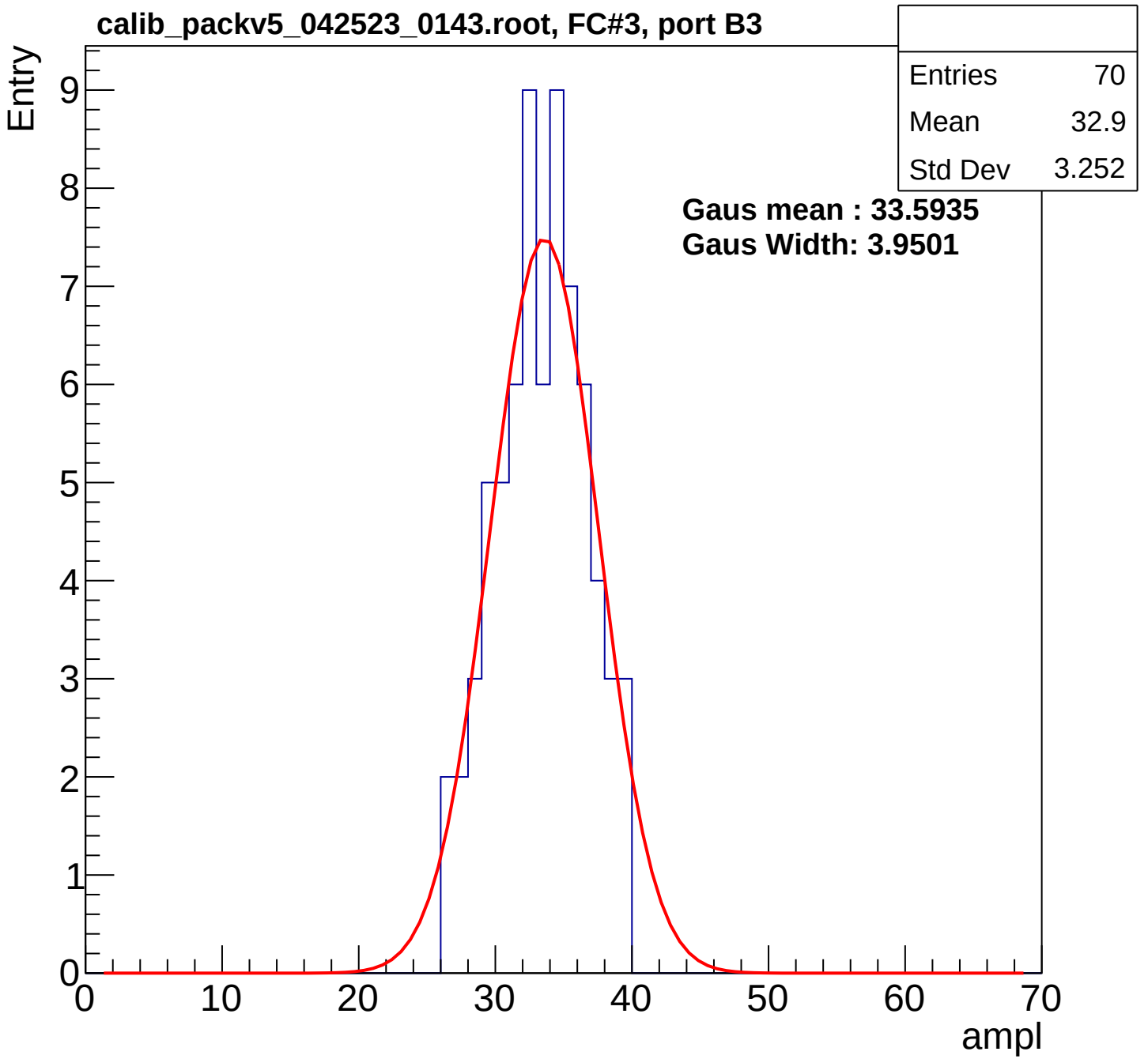
B1L000S, U3-ch43, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 32.9
Std Dev 3.252
Gaus mean : 33.5935
Gaus Width: 3.9501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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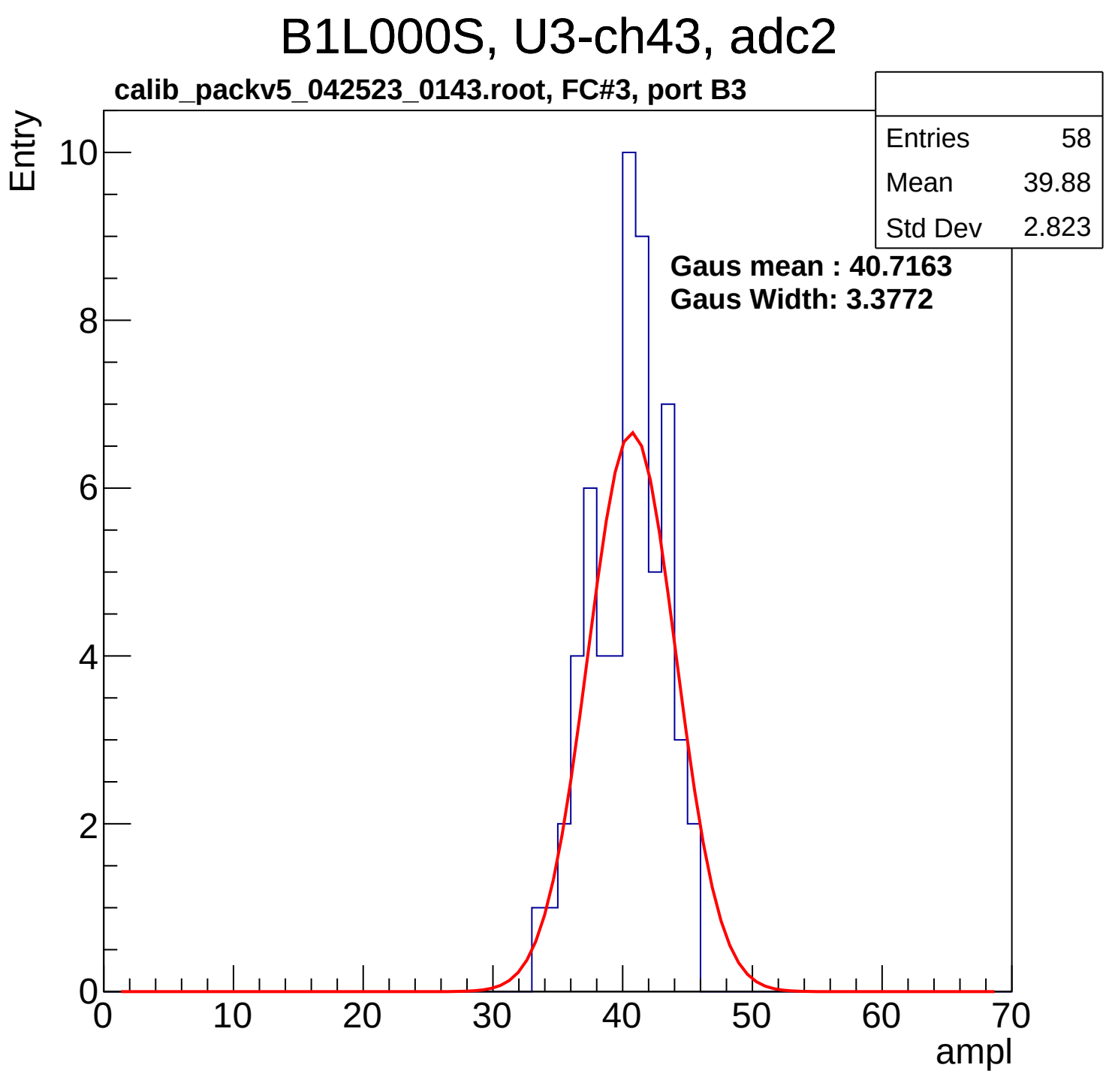
B1L000S, U3-ch43, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 39.88
Std Dev 2.823
Gaus mean : 40.7163
Gaus Width: 3.3772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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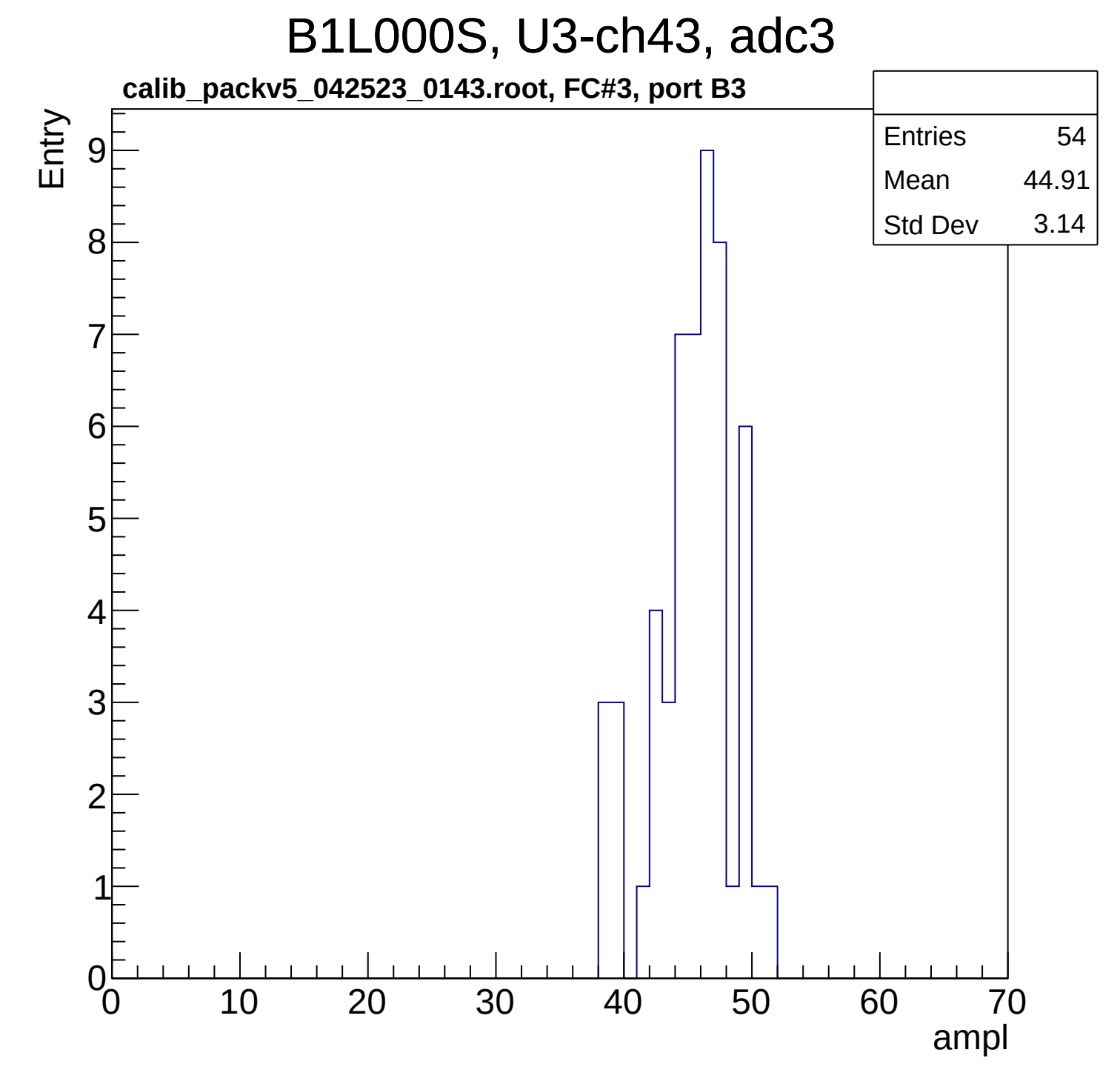
B1L000S, U3-ch43, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 44.91
Std Dev 3.14
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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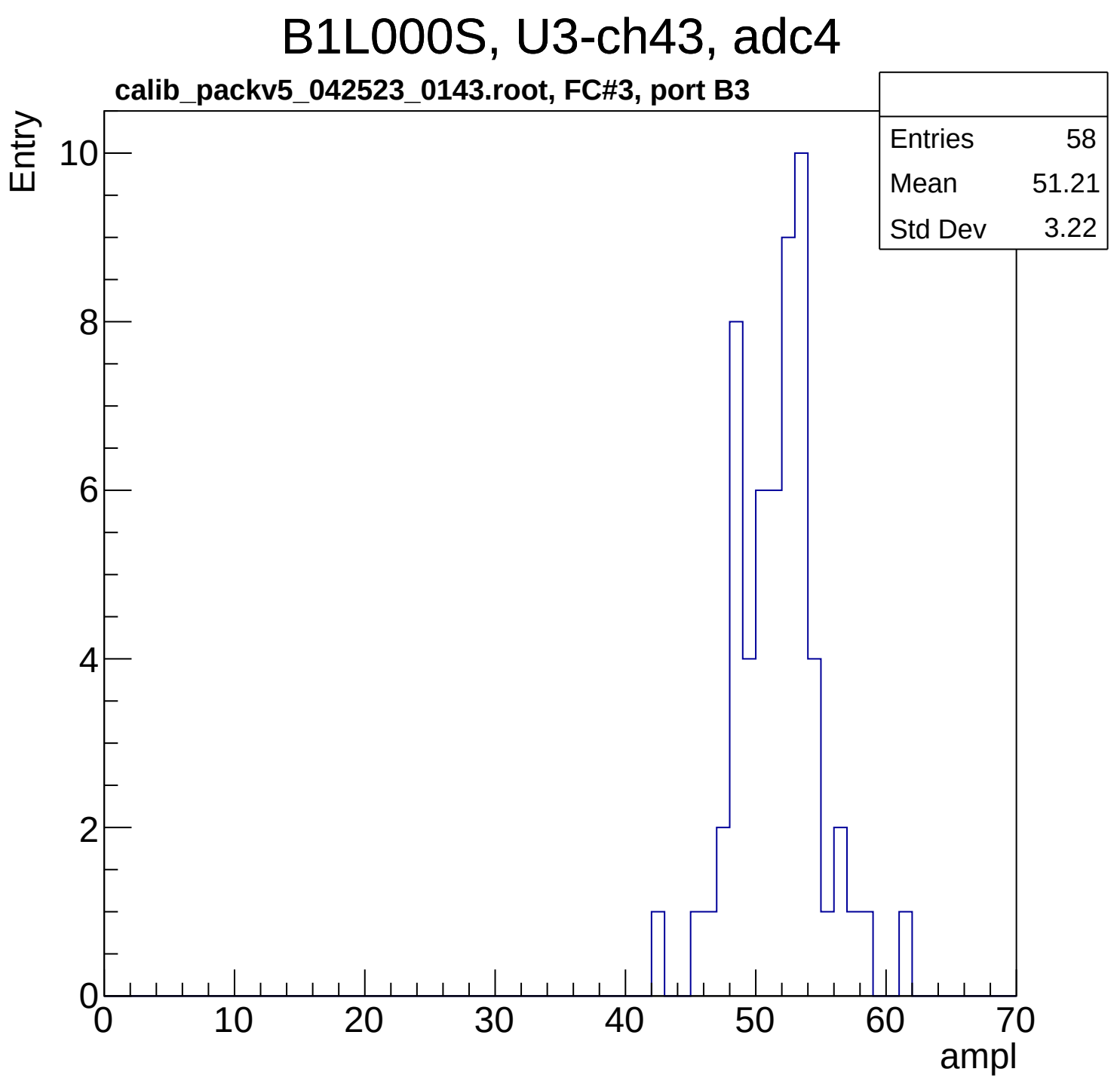
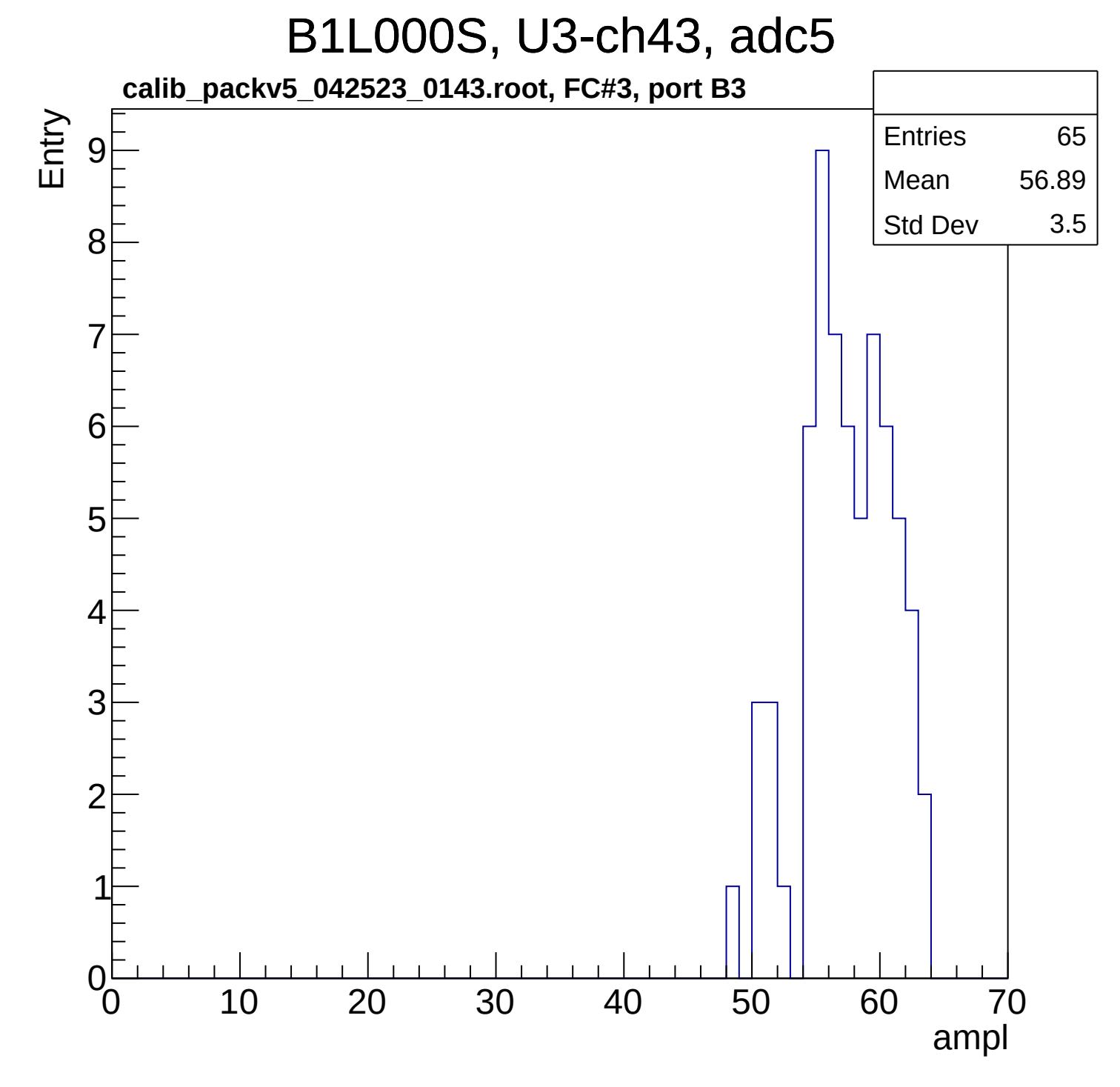

B1L000S, U3-ch43, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 51.21
Std Dev 3.22
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch43, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 56.89
Std Dev 3.5
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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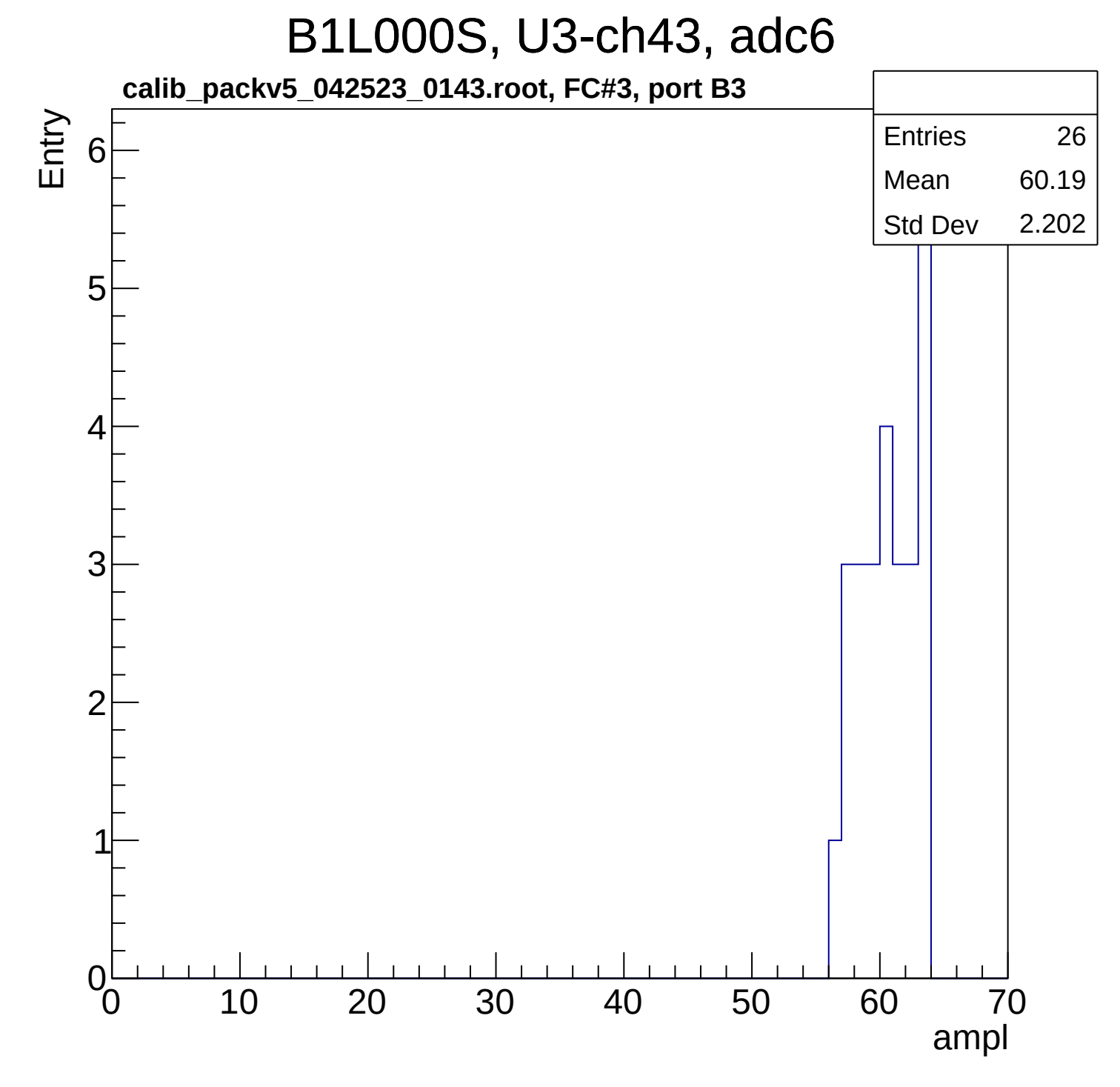
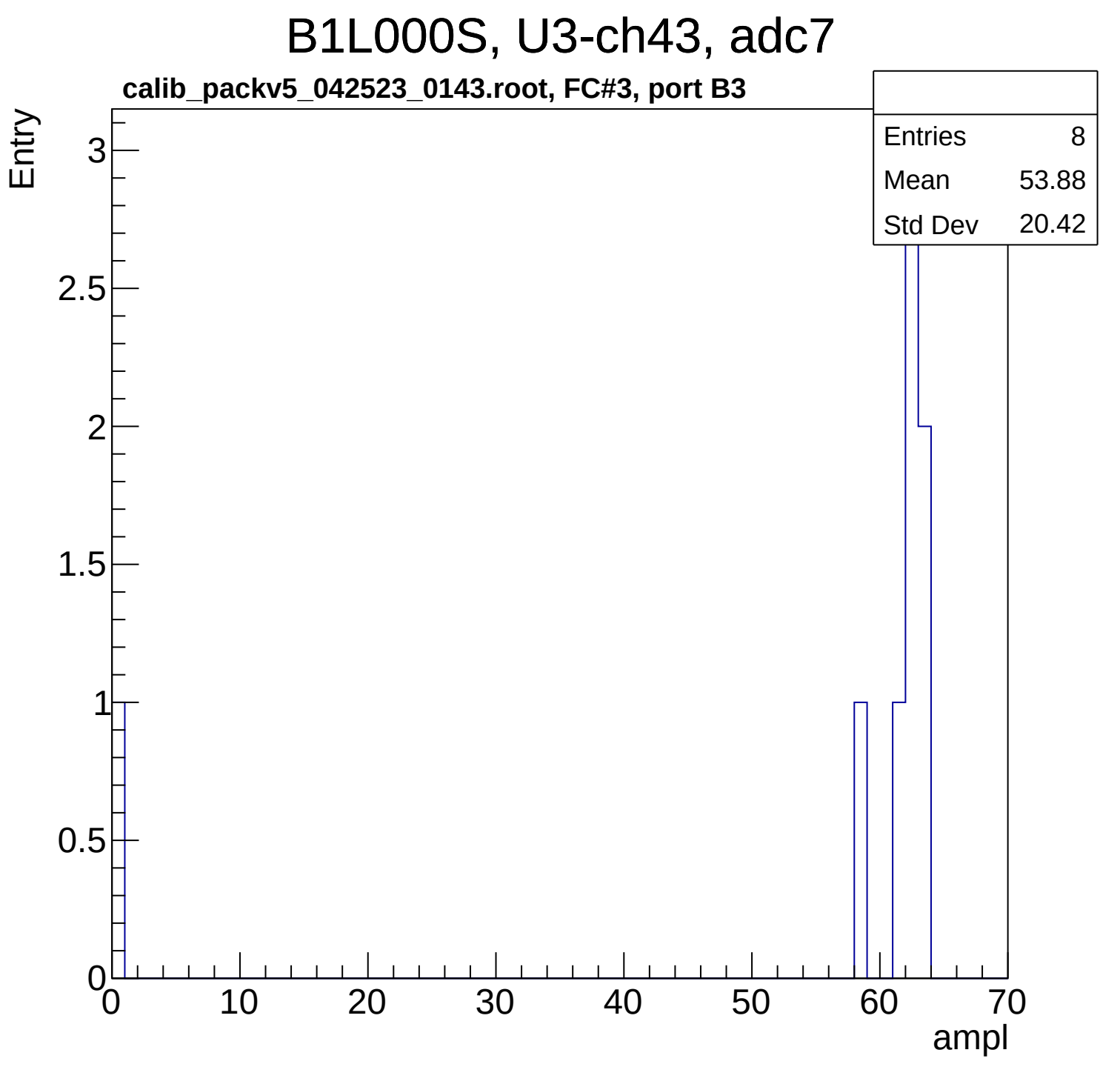

B1L000S, U3-ch43, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 60.19
Std Dev 2.202
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch43, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 53.88
Std Dev 20.42
Entry
ampl
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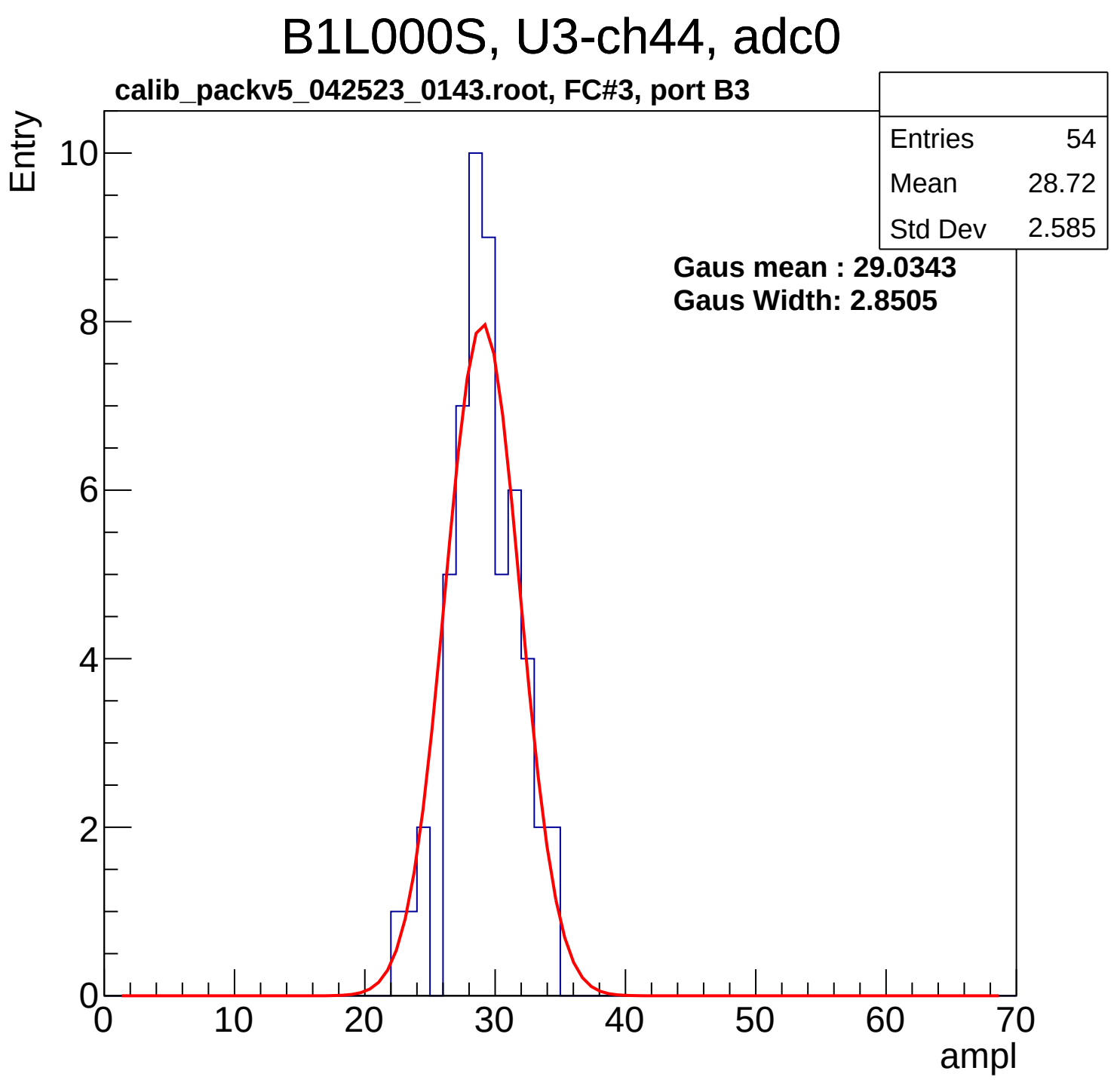
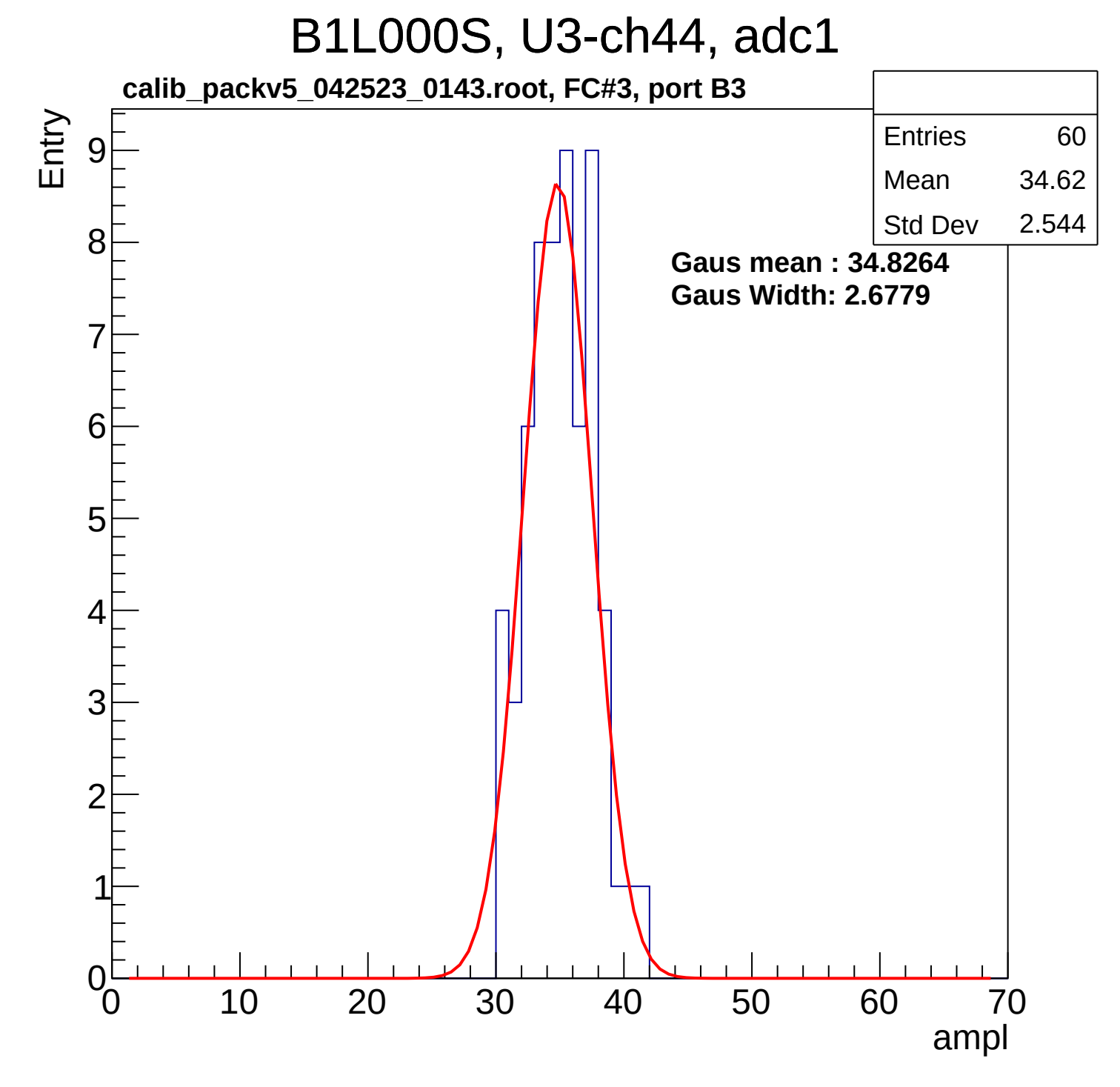
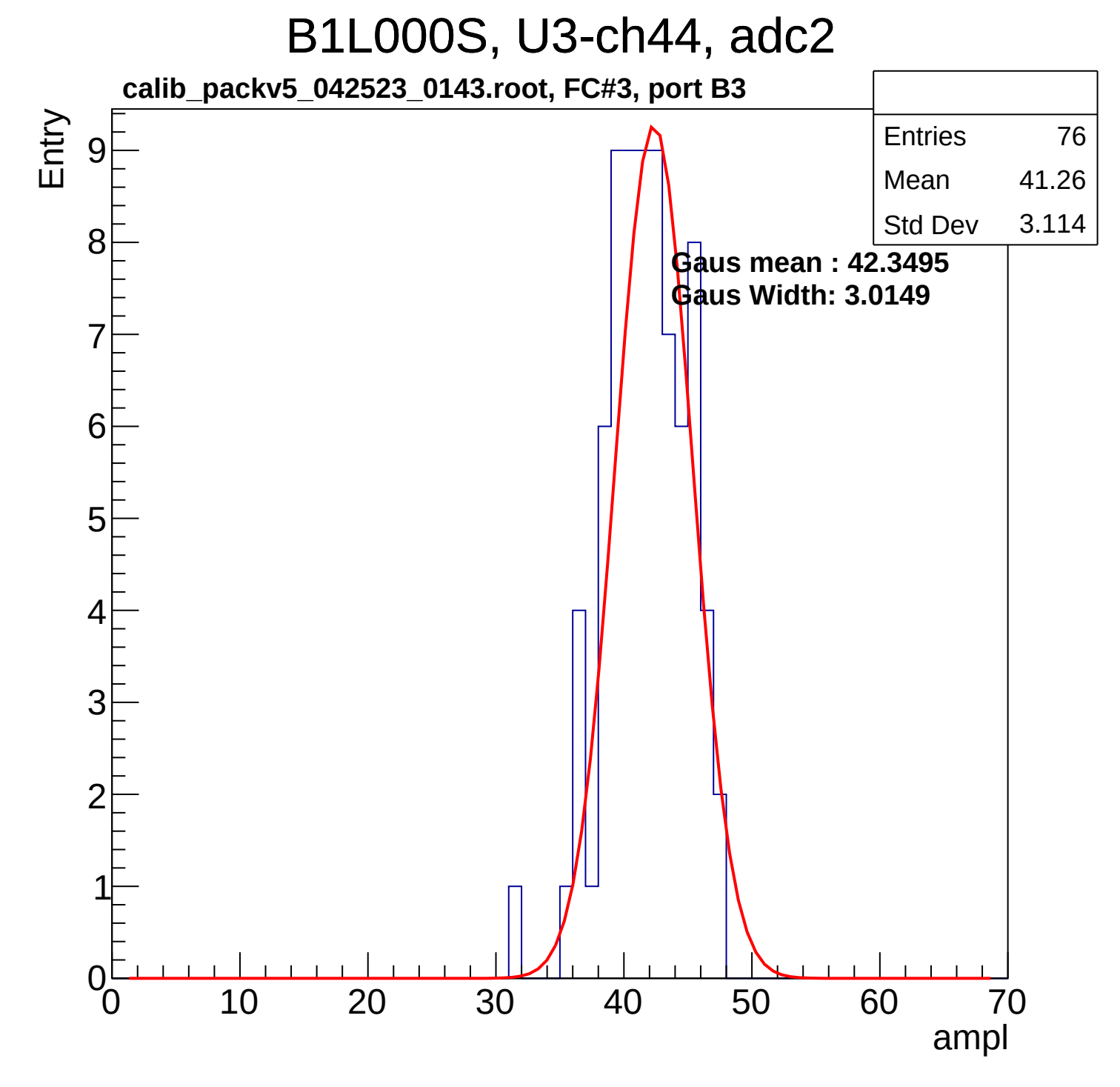
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch44, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 28.72
Std Dev 2.585
Gaus mean : 29.0343
Gaus Width: 2.8505
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch44, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 34.62
Std Dev 2.544
Gaus mean : 34.8264
Gaus Width: 2.6779
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch44, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 41.26
Std Dev 3.114
Gaus mean : 42.3495
Gaus Width: 3.0149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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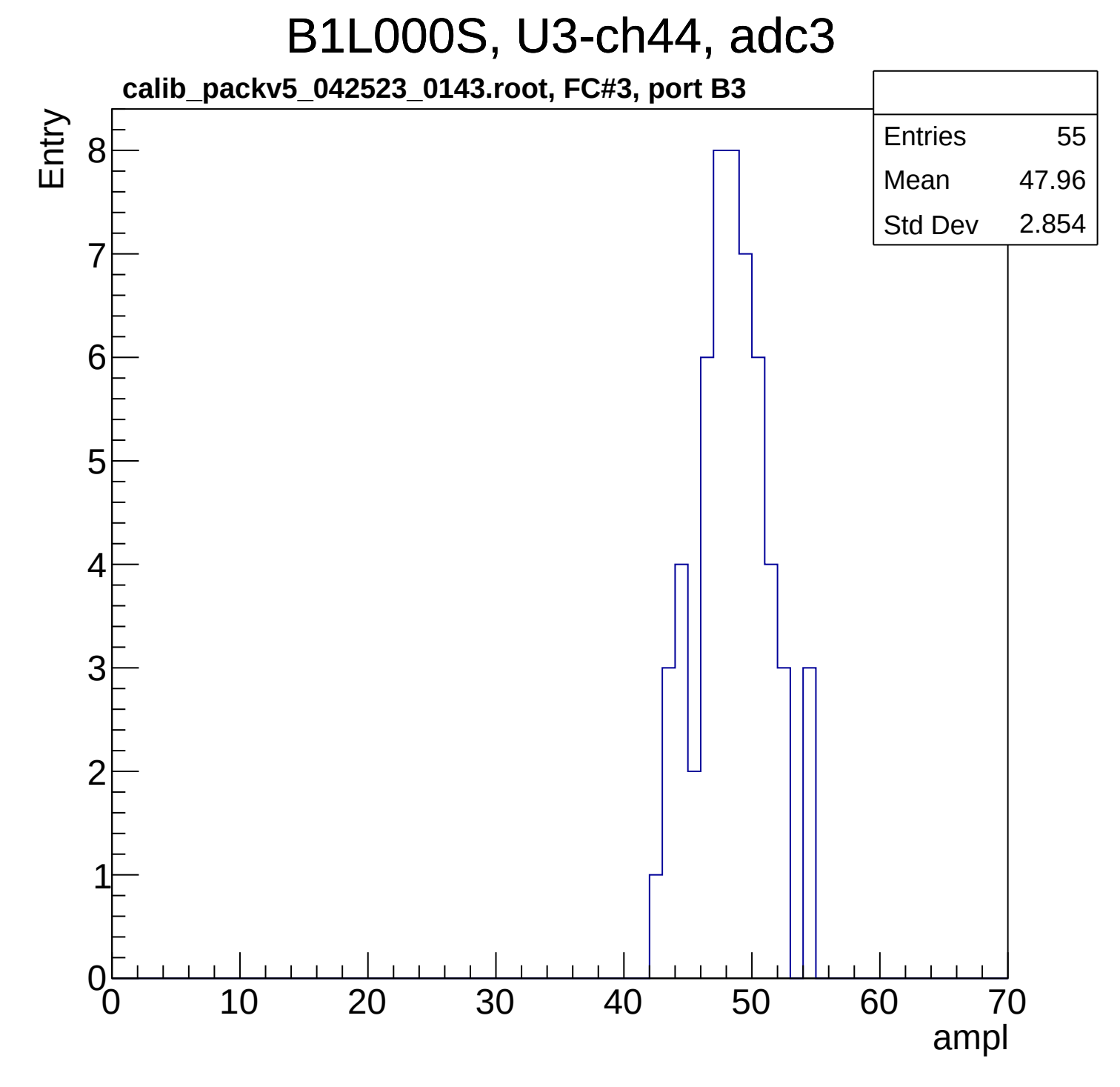
B1L000S, U3-ch44, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 47.96
Std Dev 2.854
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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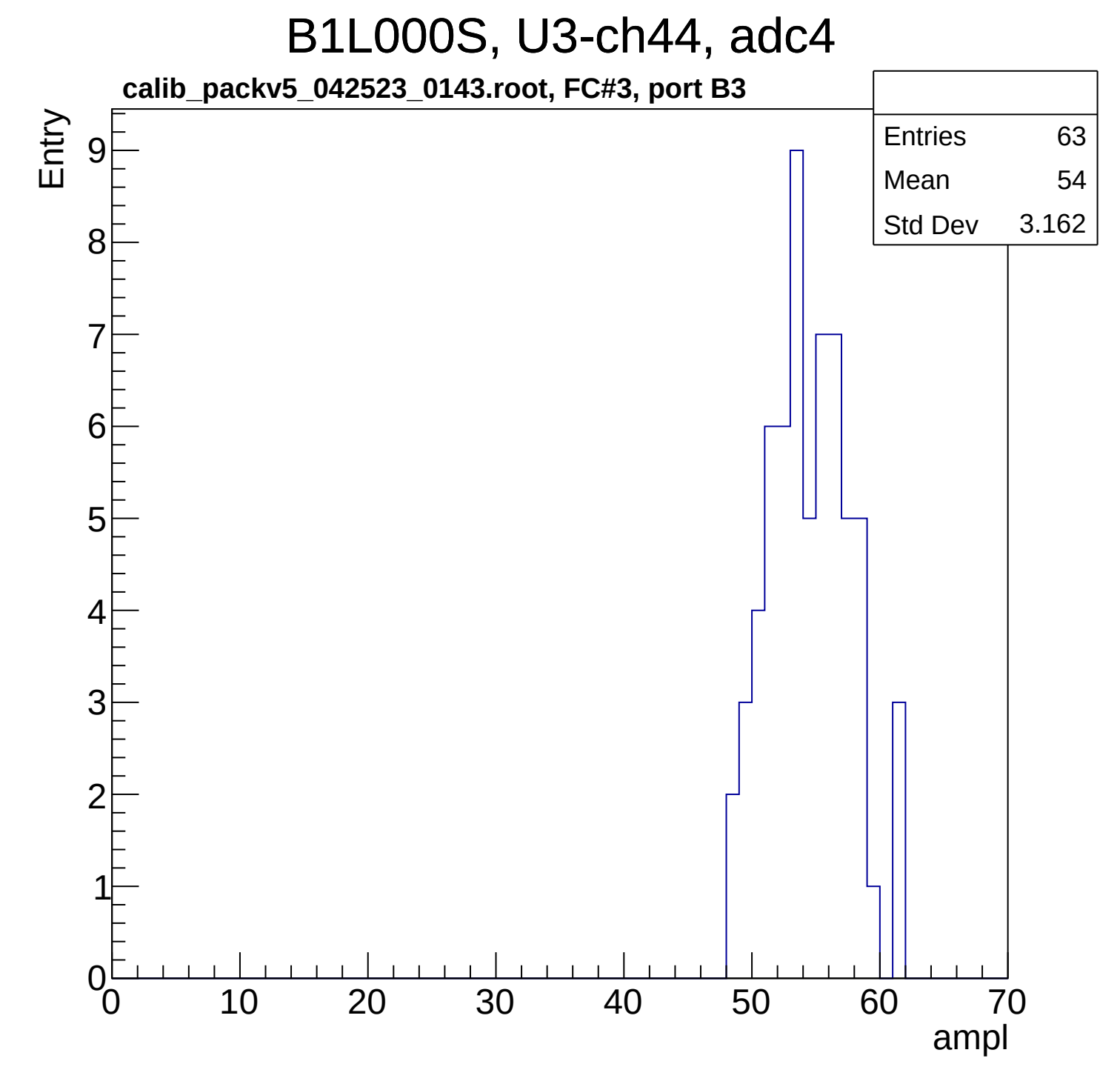
B1L000S, U3-ch44, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 54
Std Dev 3.162
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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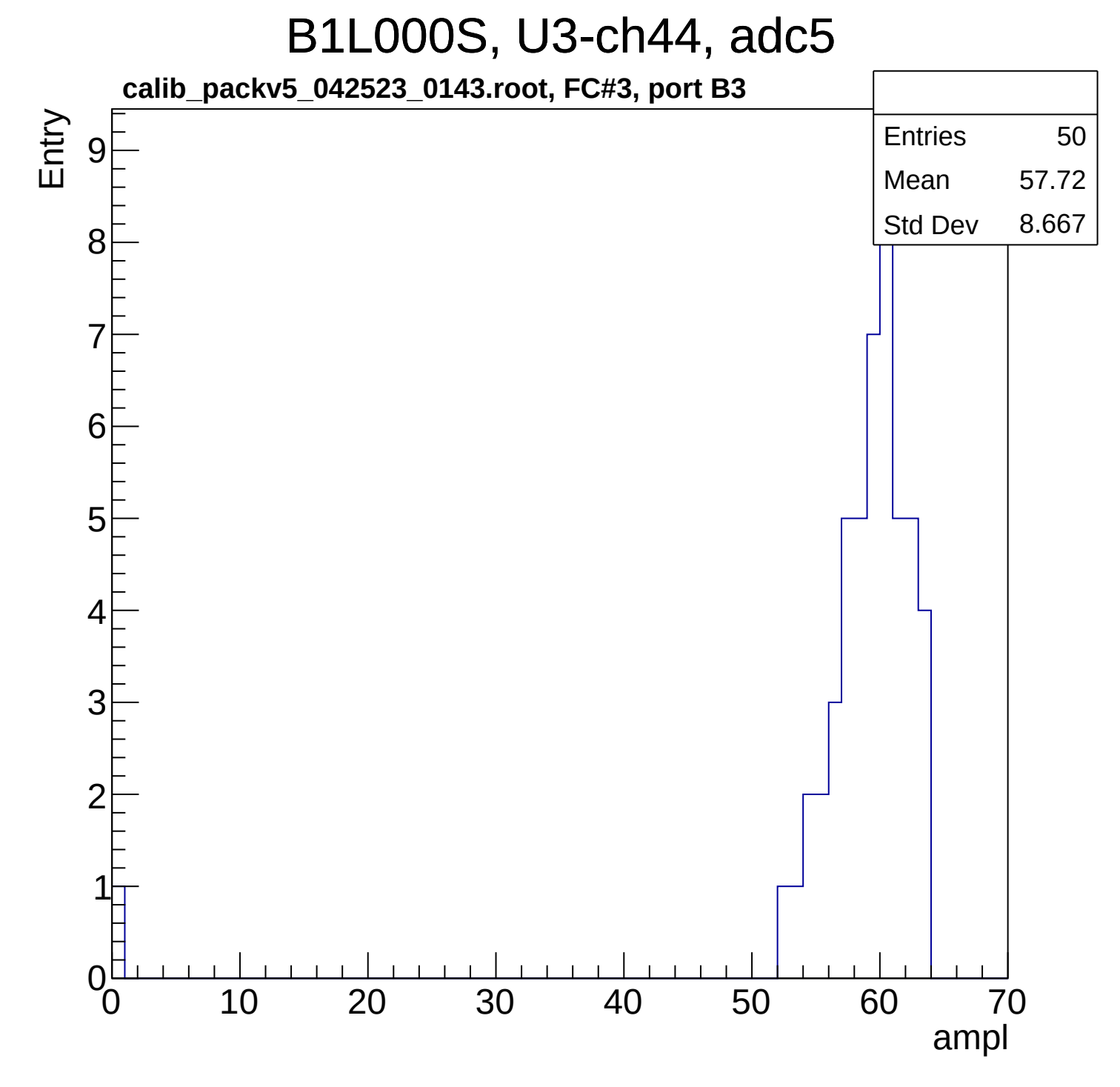
B1L000S, U3-ch44, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 57.72
Std Dev 8.667
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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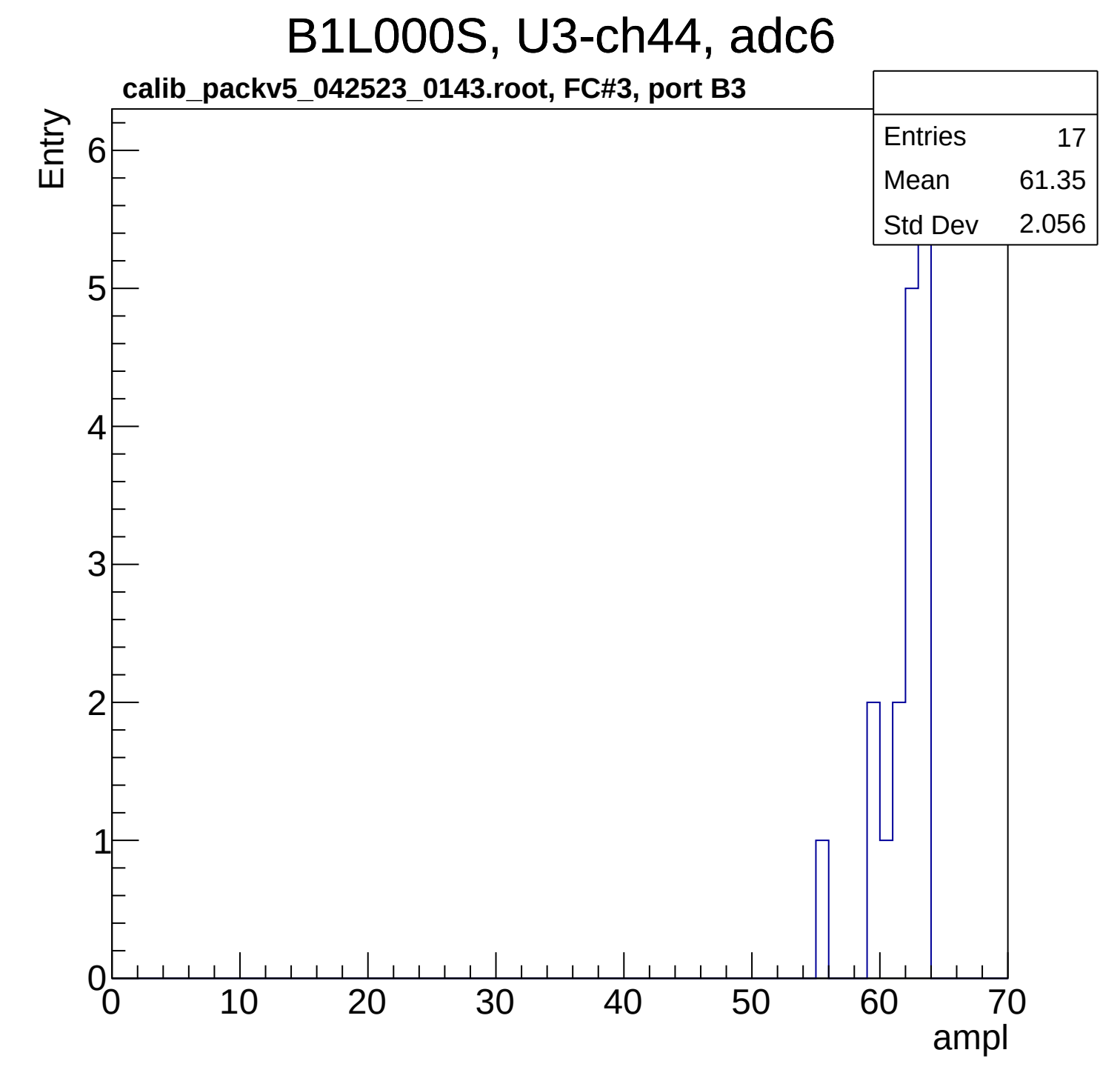
B1L000S, U3-ch44, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.35
Std Dev 2.056
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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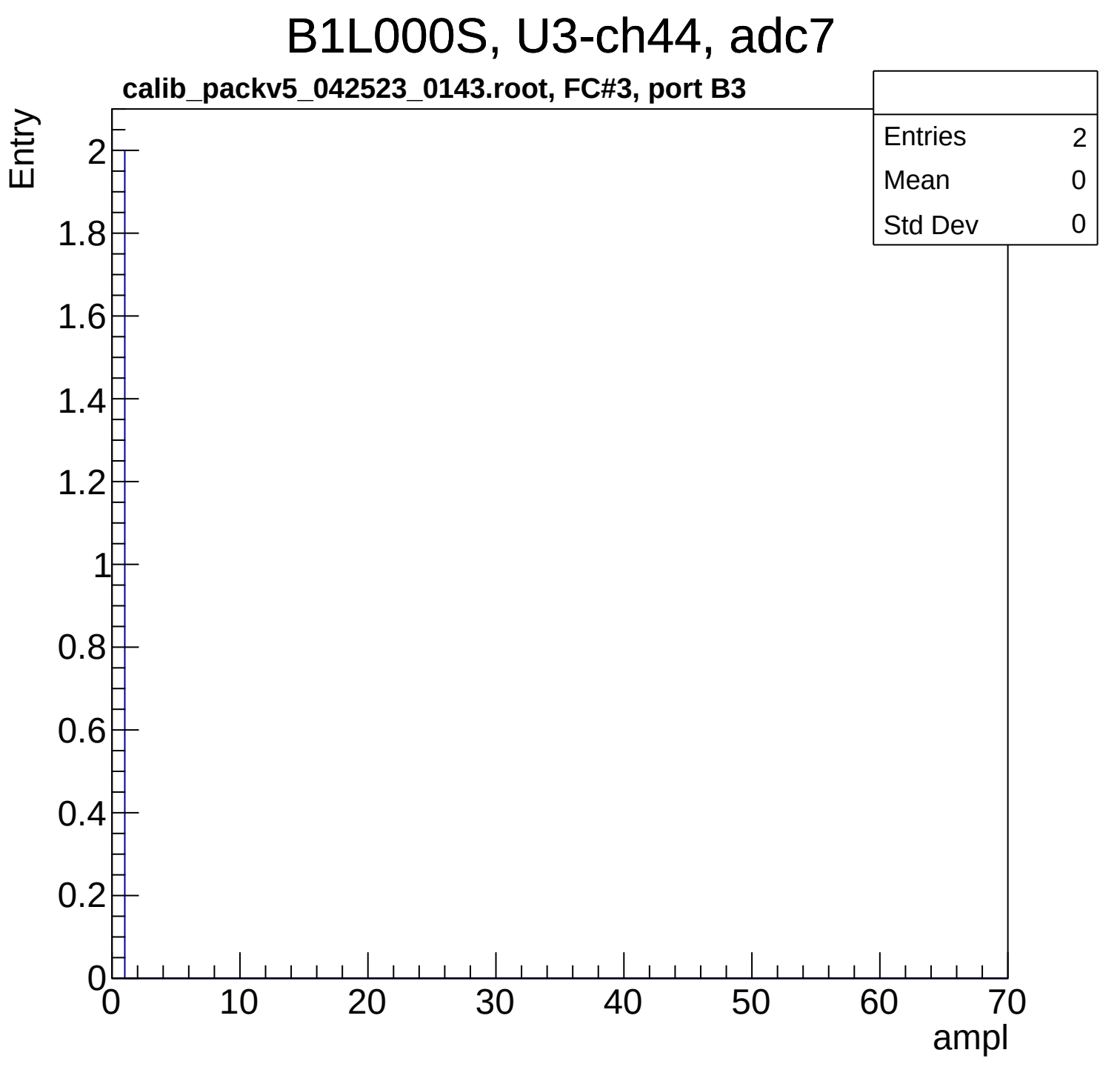
B1L000S, U3-ch44, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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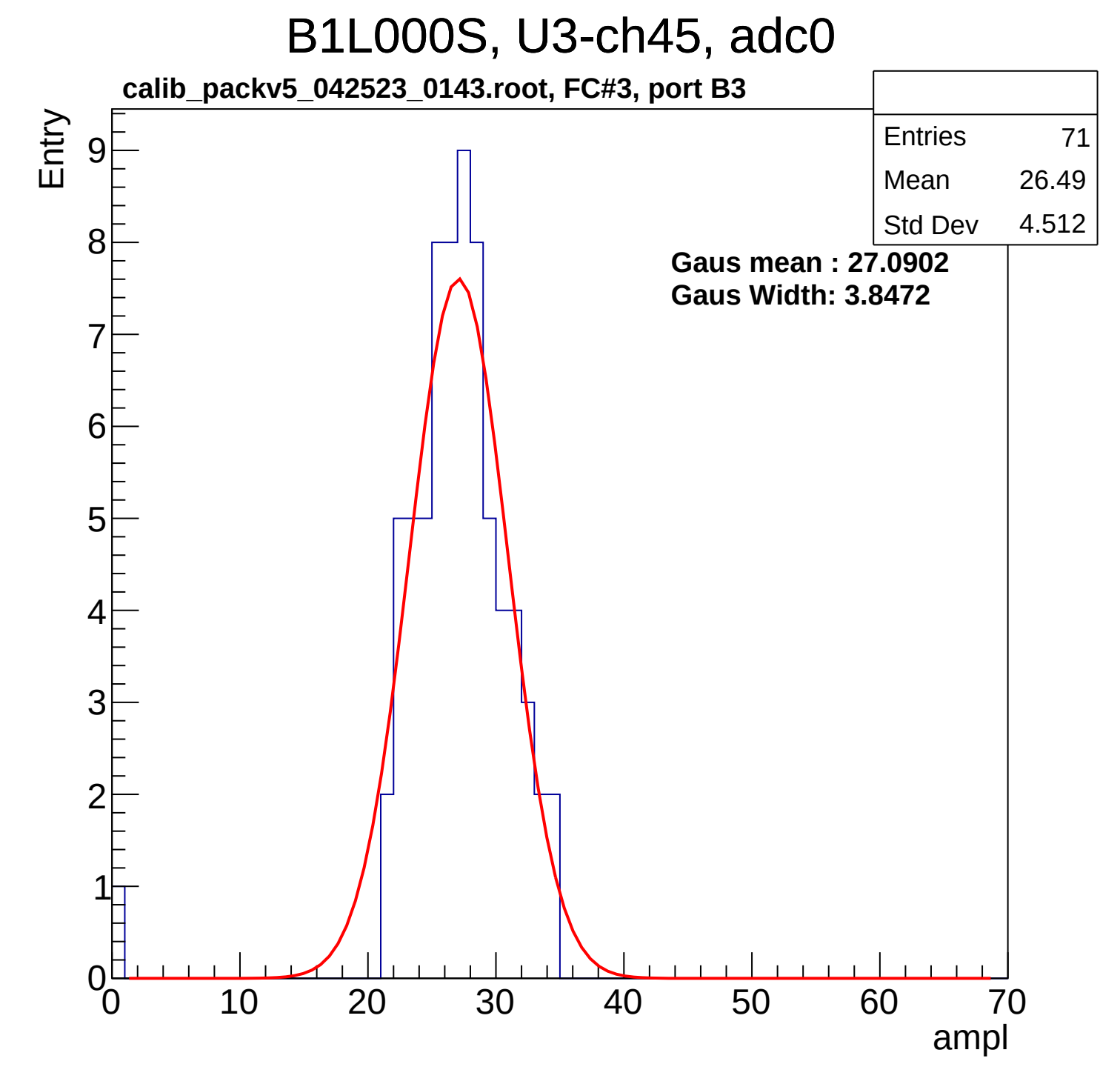
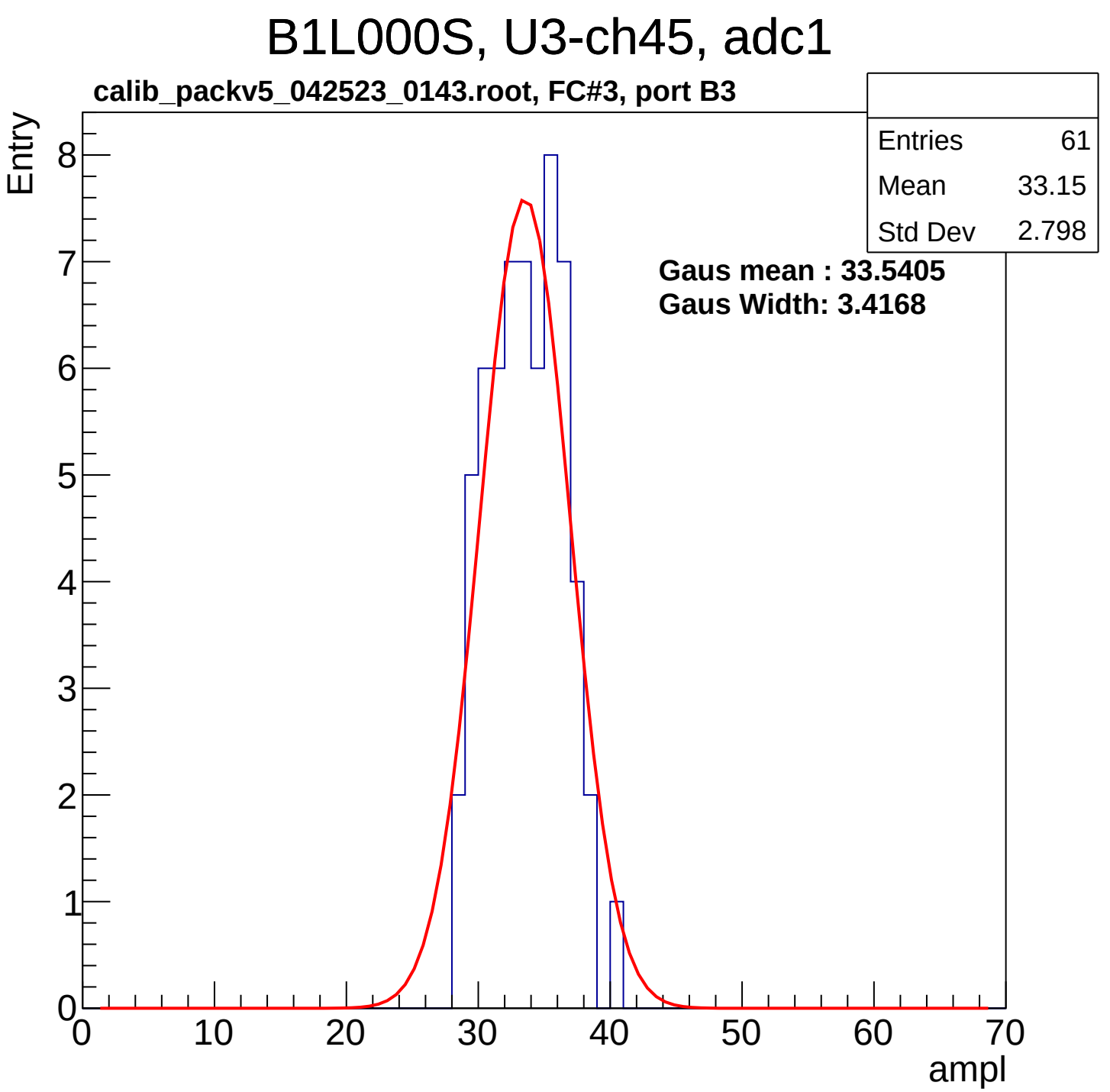

B1L000S, U3-ch45, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 26.49
Std Dev 4.512
Gaus mean : 27.0902
Gaus Width: 3.8472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch45, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 33.15
Std Dev 2.798
Gaus mean : 33.5405
Gaus Width: 3.4168
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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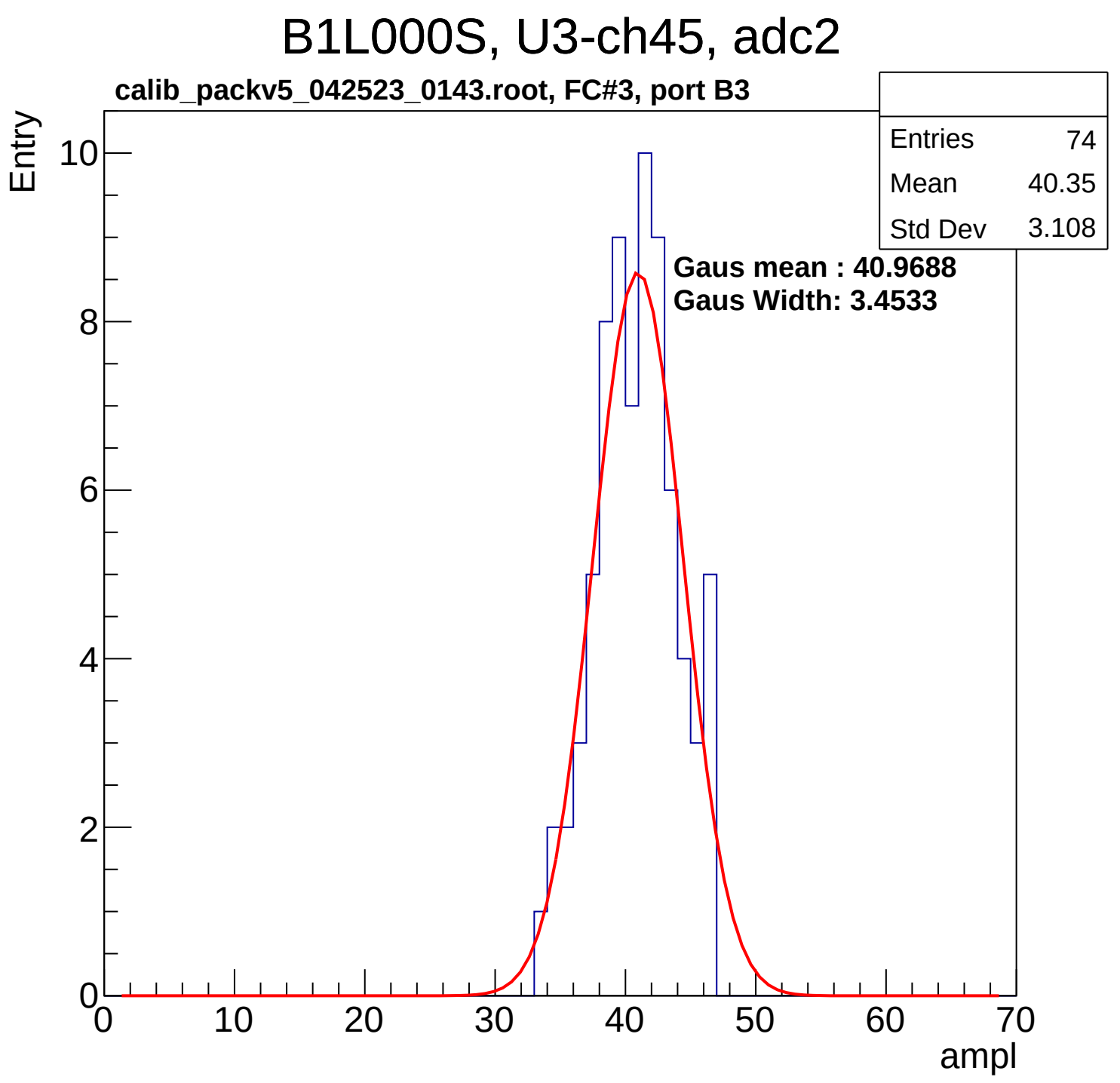
B1L000S, U3-ch45, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 40.35
Std Dev 3.108
Gaus mean : 40.9688
Gaus Width: 3.4533
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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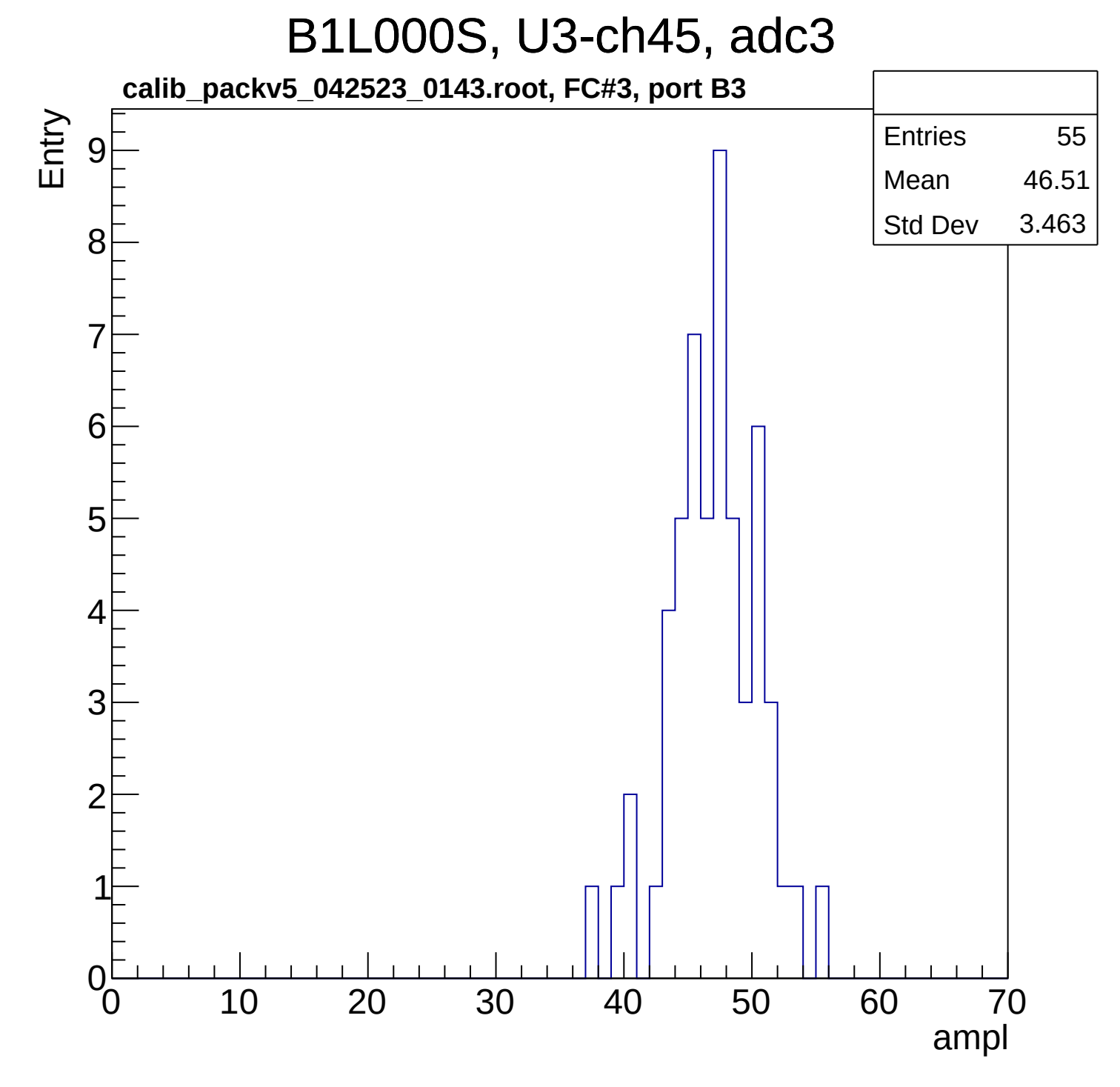
B1L000S, U3-ch45, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 46.51
Std Dev 3.463
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
50
60
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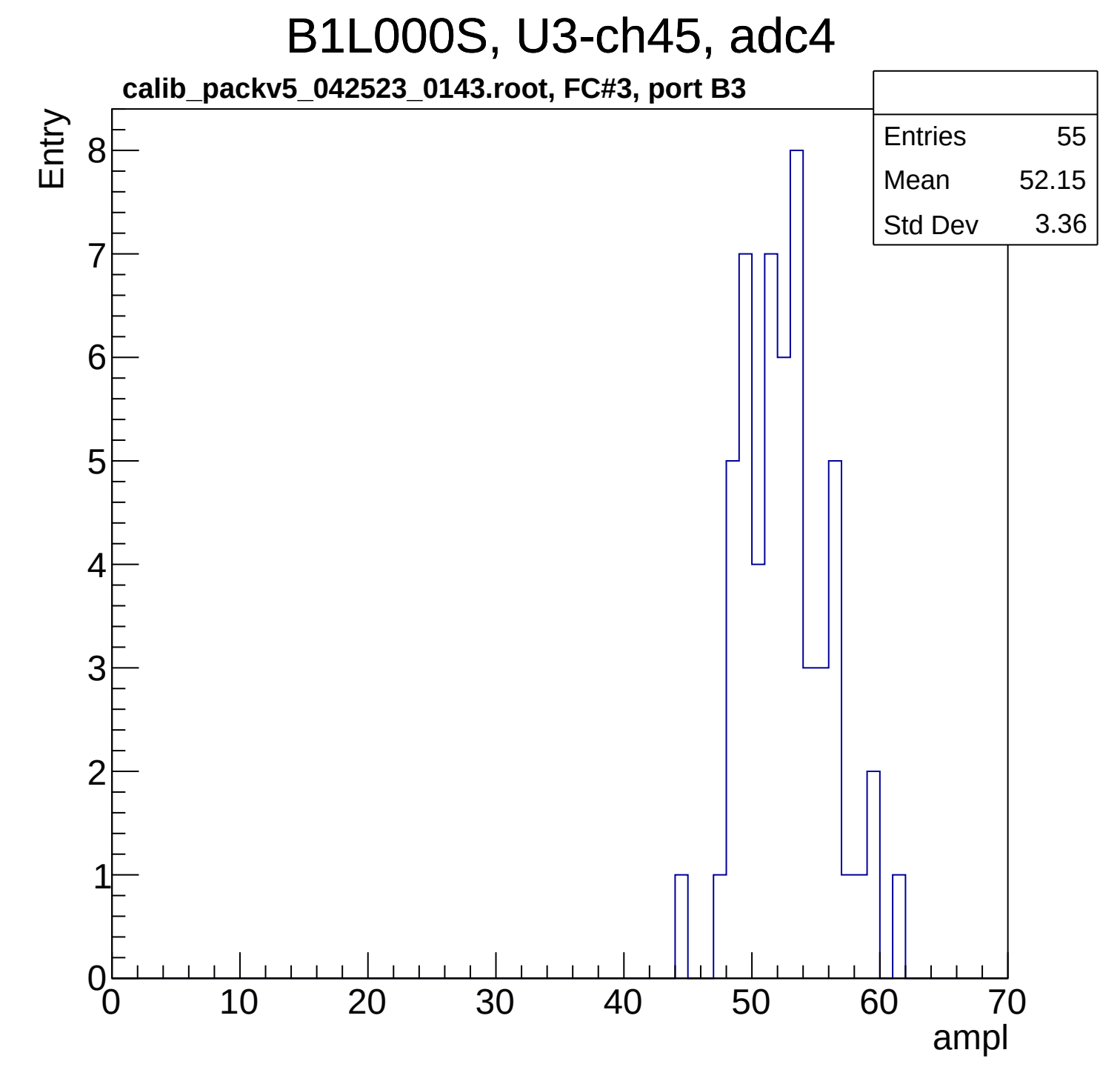
B1L000S, U3-ch45, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 52.15
Std Dev 3.36
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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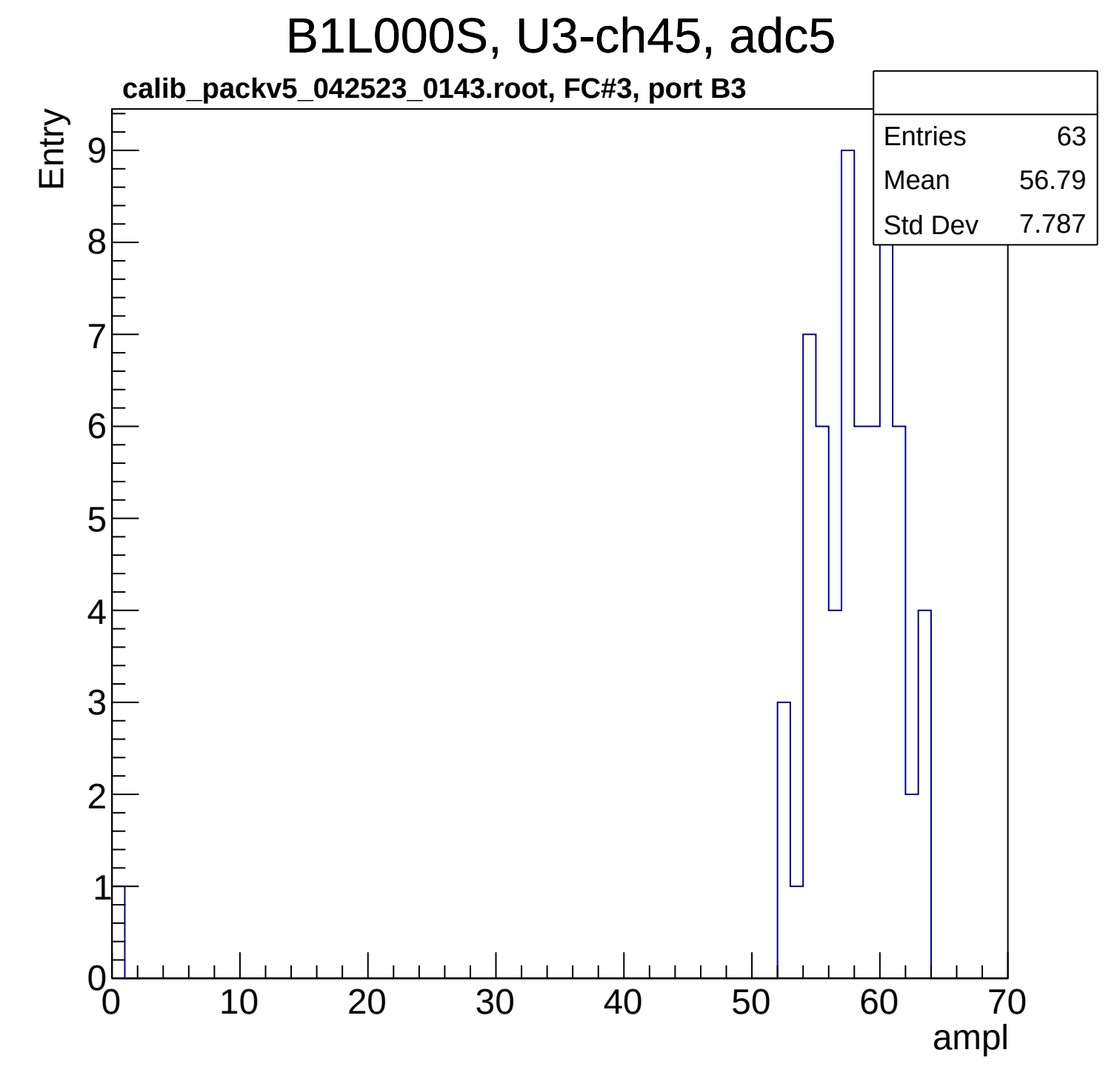
B1L000S, U3-ch45, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 56.79
Std Dev 7.787
Entry
ampl
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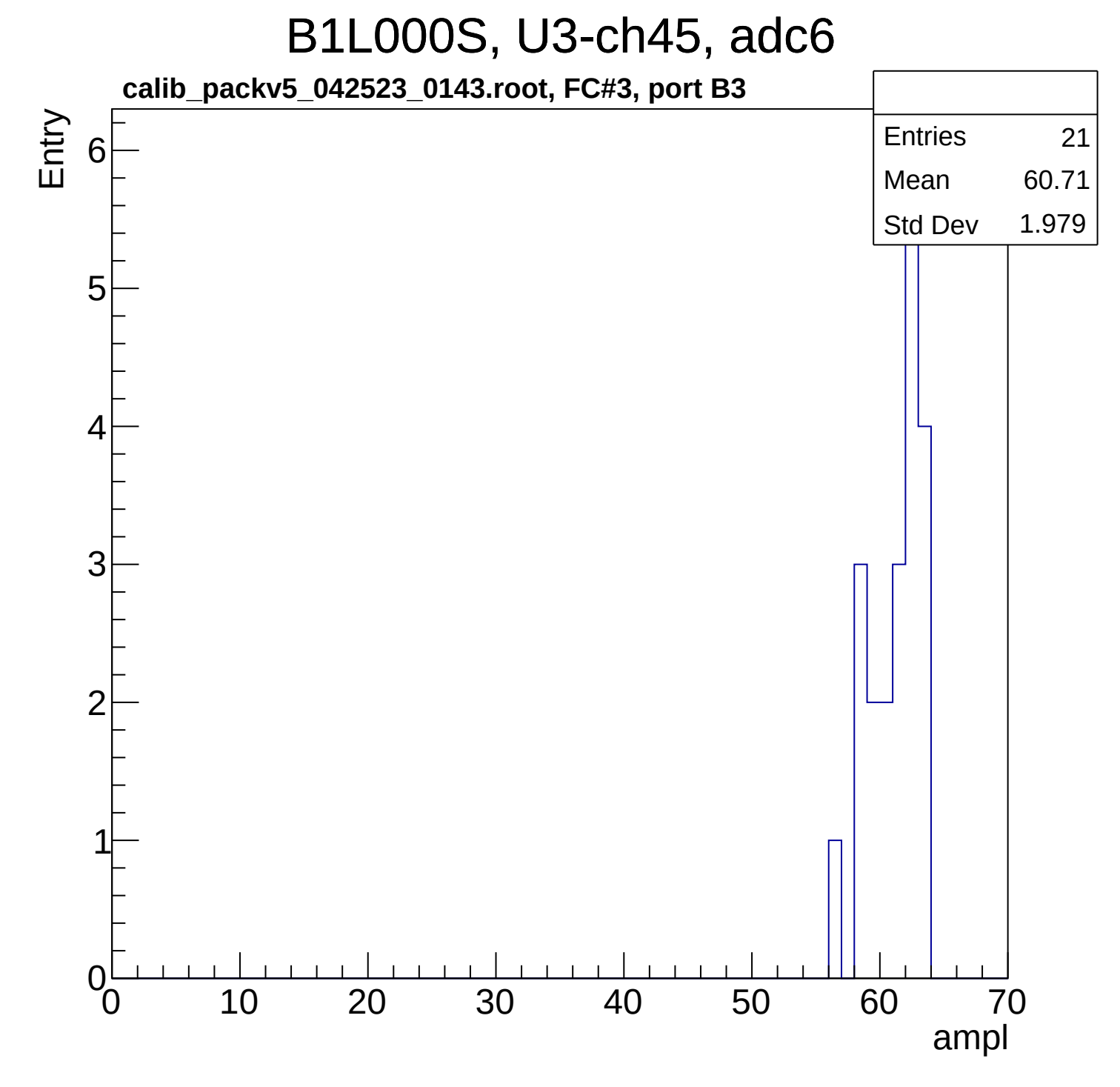
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch45, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 60.71
Std Dev 1.979
Entry
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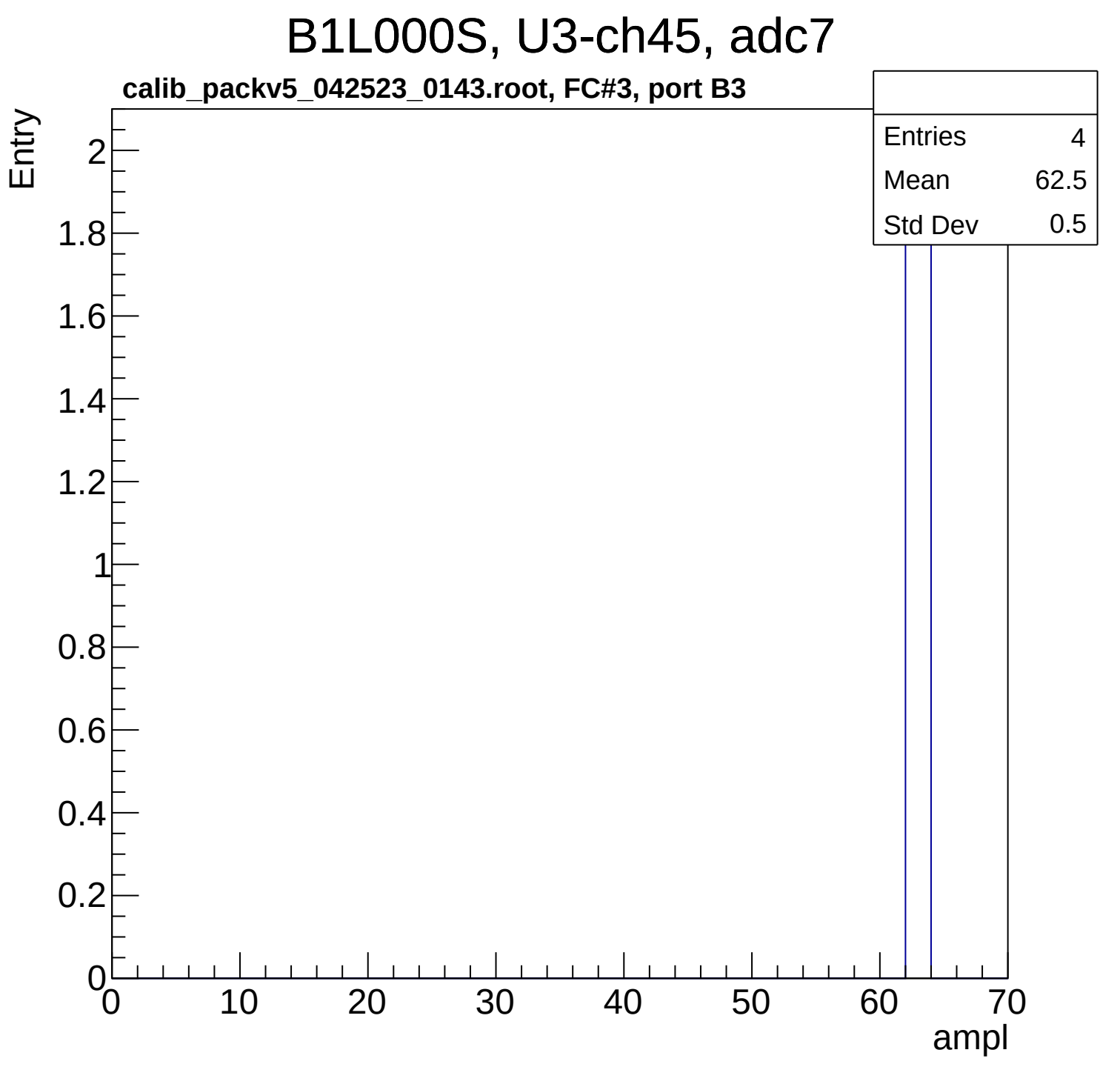
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch45, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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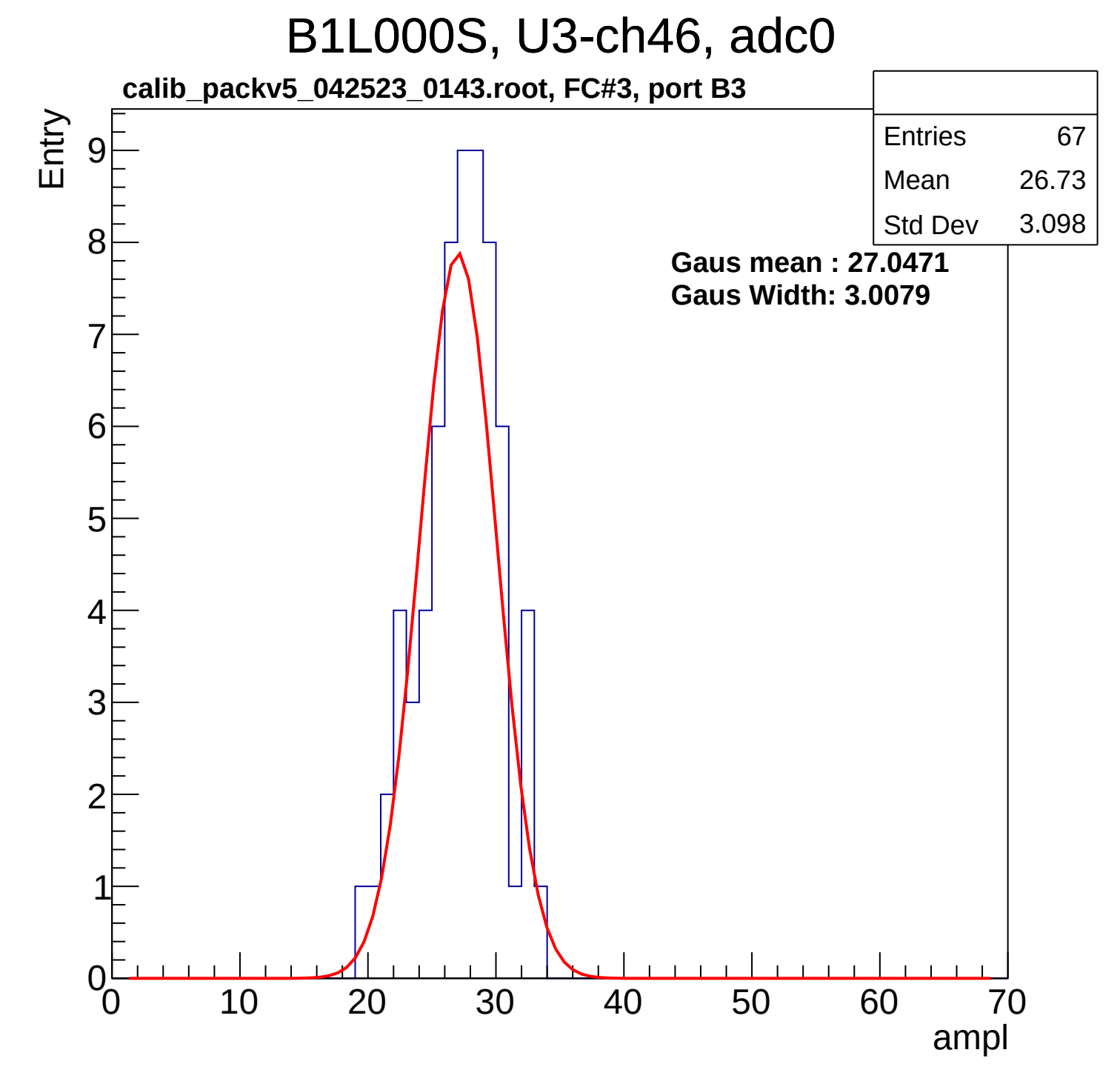
B1L000S, U3-ch46, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 26.73
Std Dev 3.098
Gaus mean : 27.0471
Gaus Width: 3.0079
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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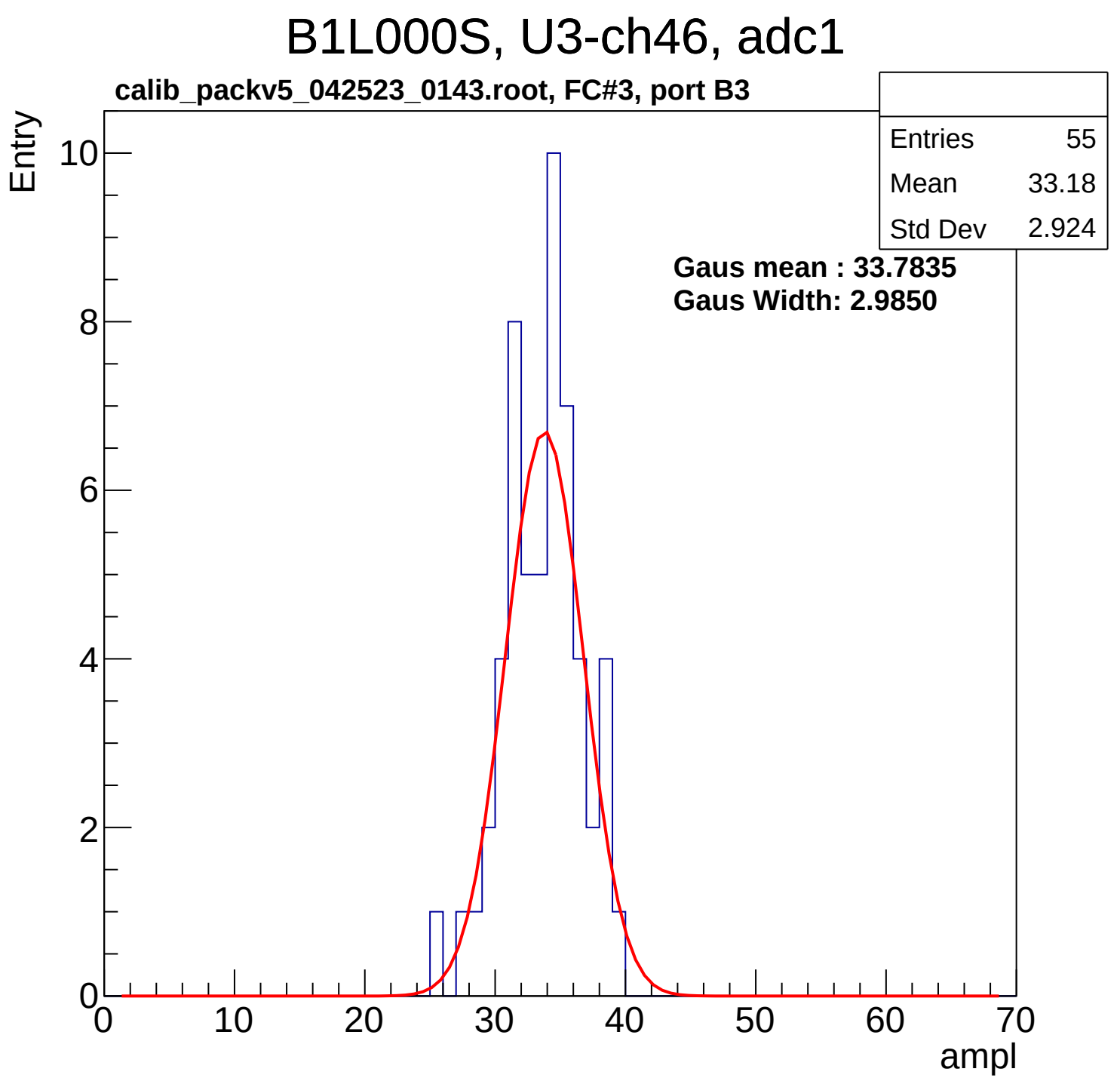
B1L000S, U3-ch46, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 33.18
Std Dev 2.924
Gaus mean : 33.7835
Gaus Width: 2.9850
Entry
ampl
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4
6
8
10
0
10
20
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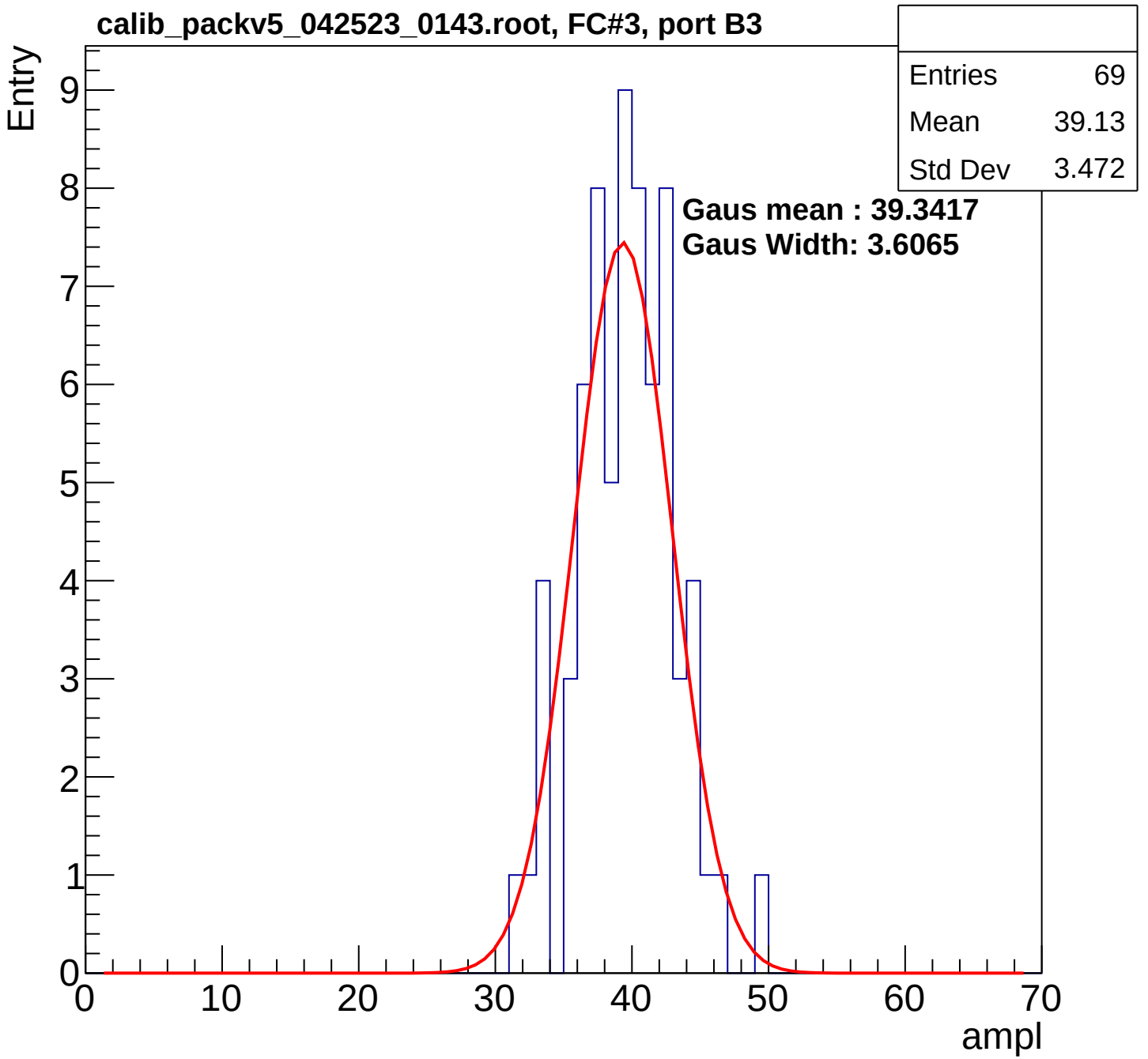
40
50
60
70


B1L000S, U3-ch46, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 39.13
Std Dev 3.472
Gaus mean : 39.3417
Gaus Width: 3.6065
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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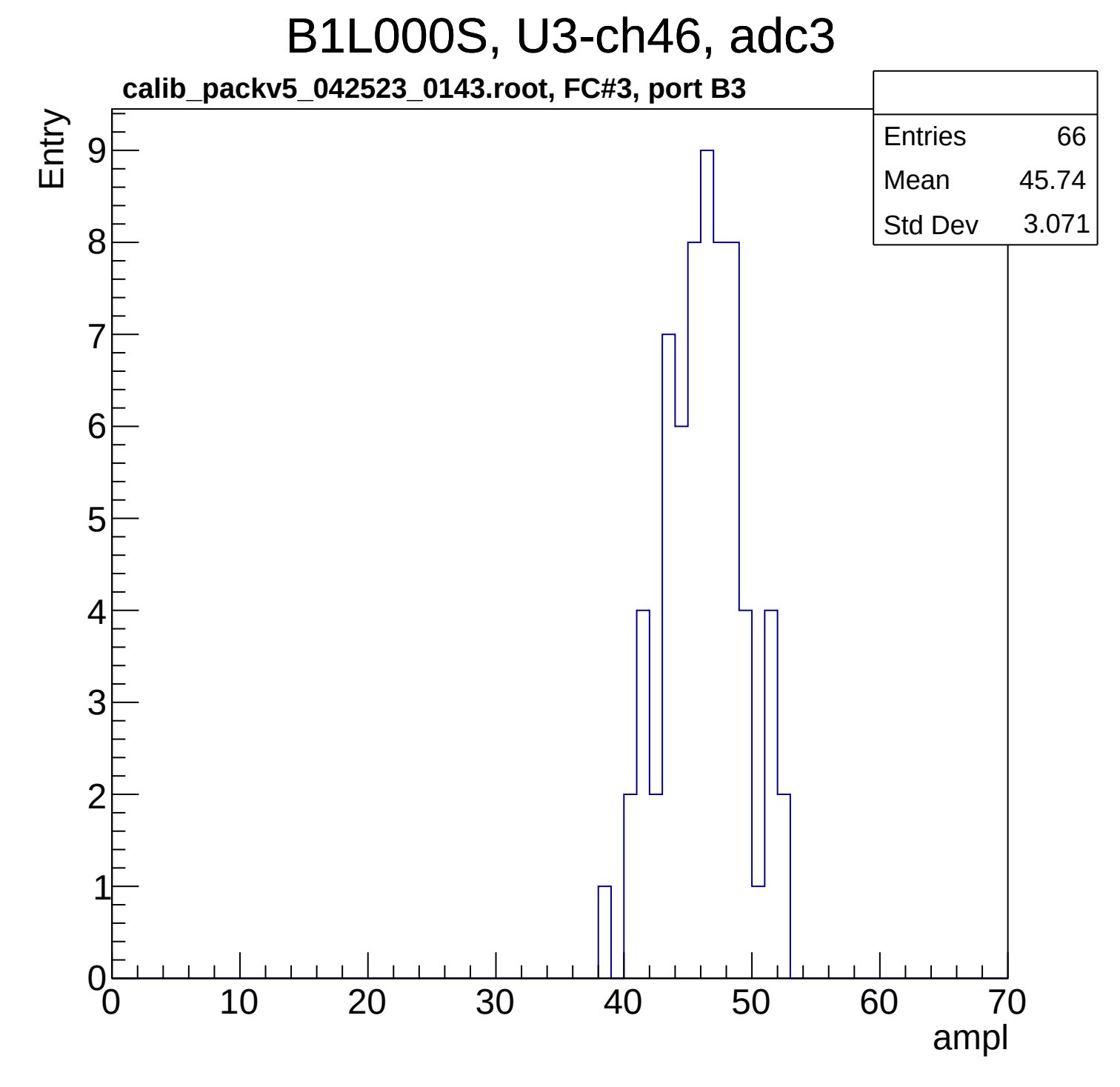
B1L000S, U3-ch46, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 45.74
Std Dev 3.071
Entry
ampl
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6
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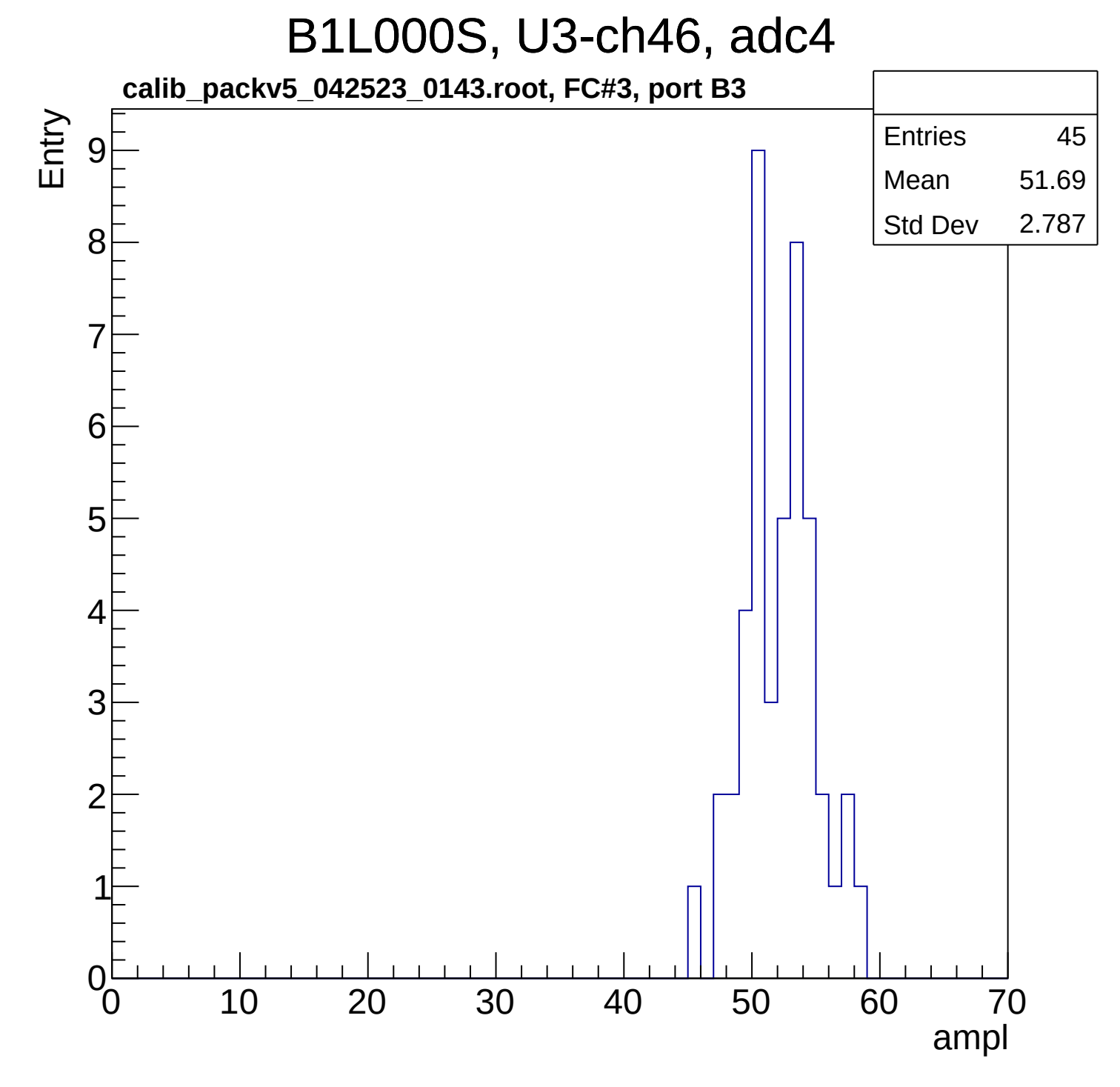
B1L000S, U3-ch46, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 51.69
Std Dev 2.787
Entry
ampl
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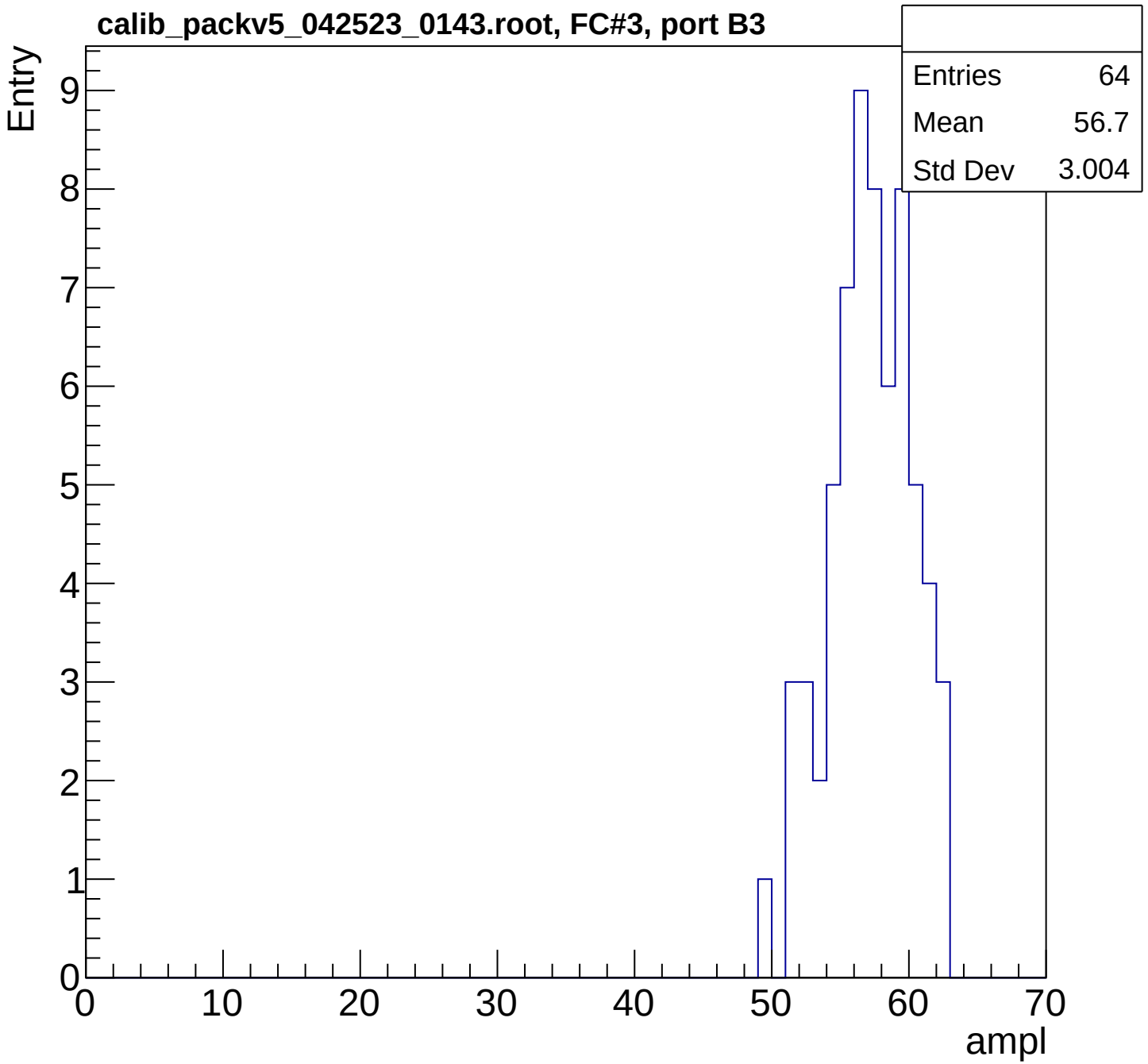
B1L000S, U3-ch46, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 56.7
Std Dev 3.004
Entry
ampl
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4
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6
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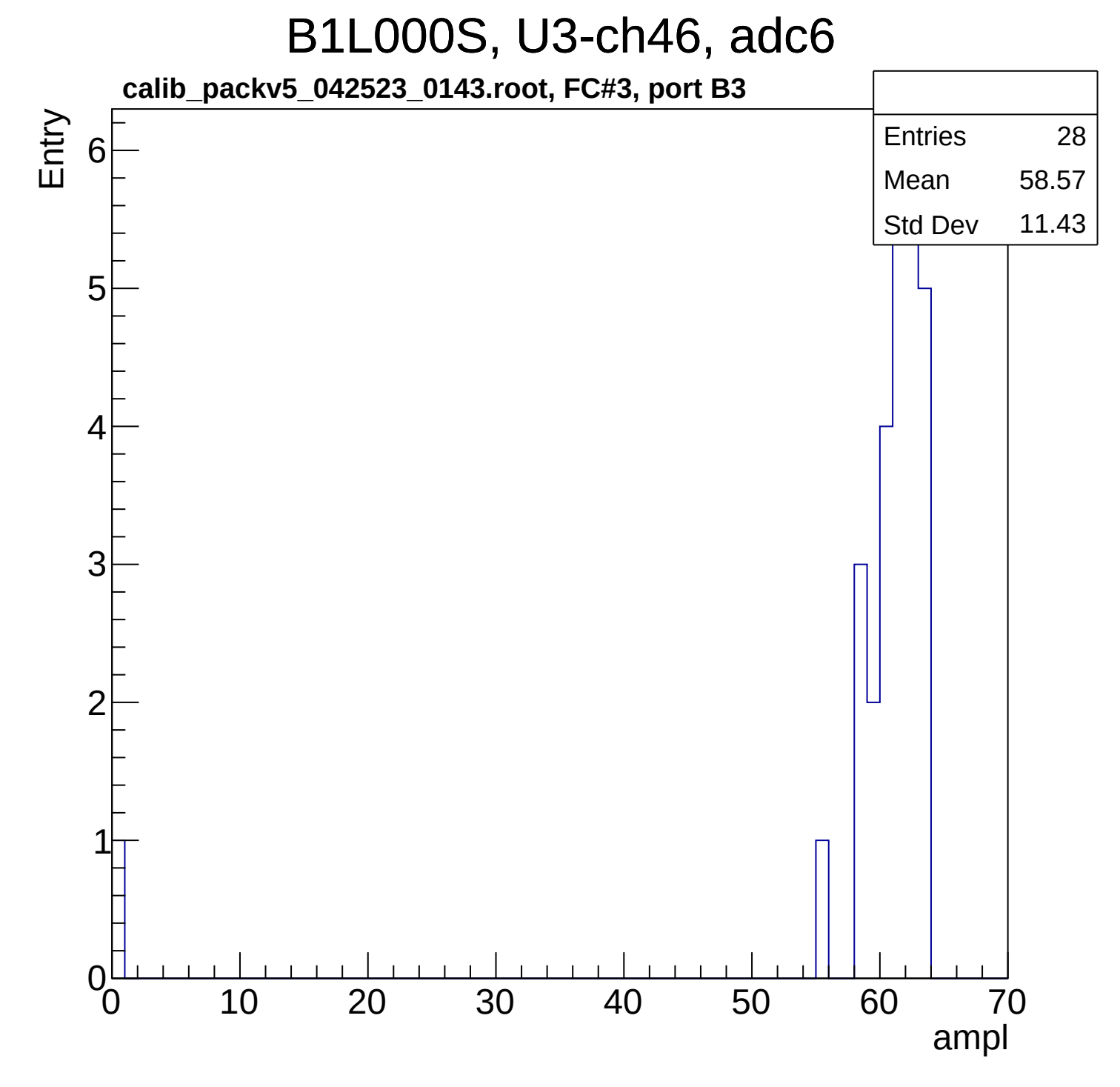
B1L000S, U3-ch46, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 28
Mean 58.57
Std Dev 11.43
Entry
ampl
0
1
2
3
4
5
6
0
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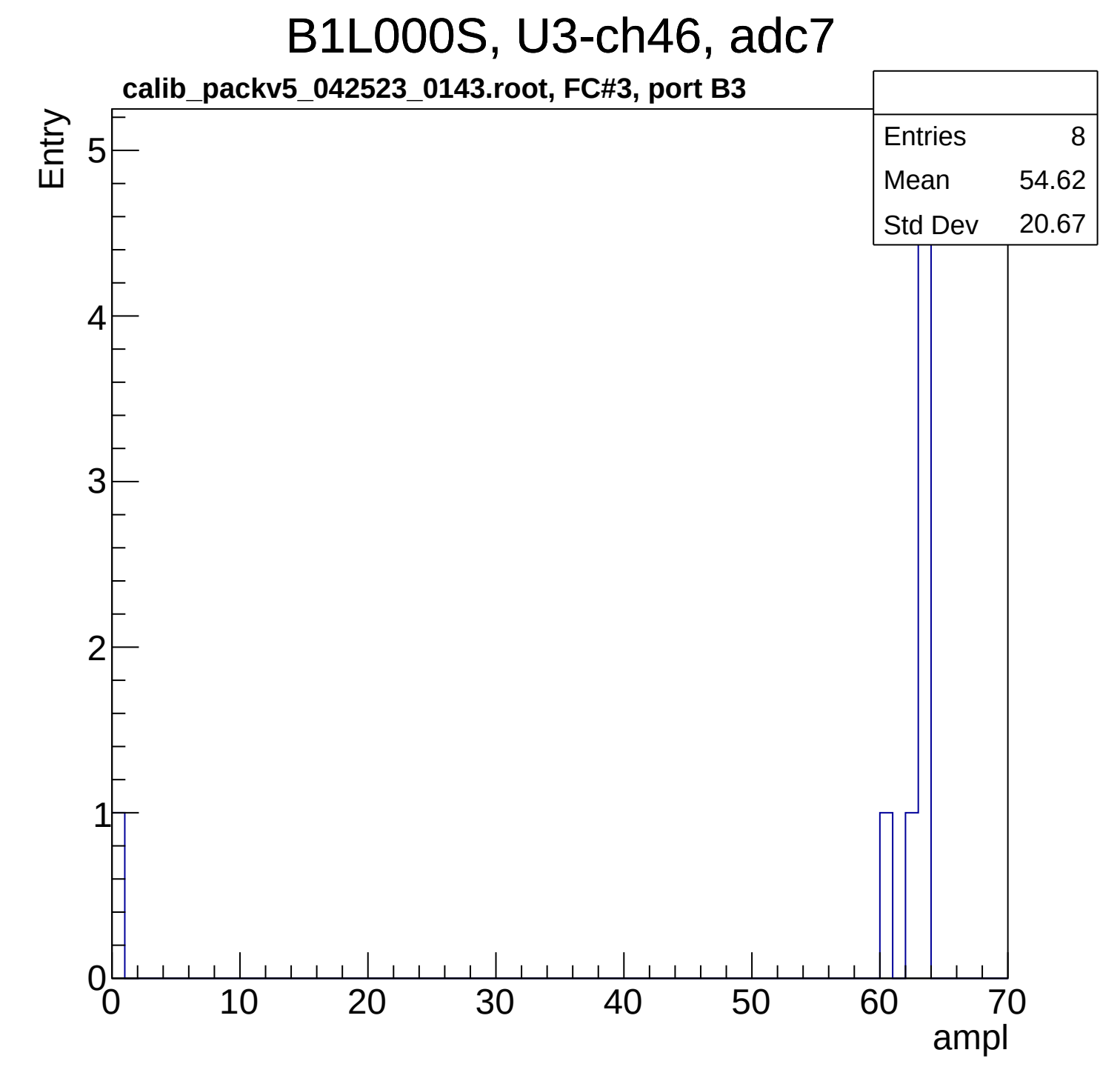
B1L000S, U3-ch46, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 54.62
Std Dev 20.67
Entry
ampl
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2
3
4
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0
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40
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60
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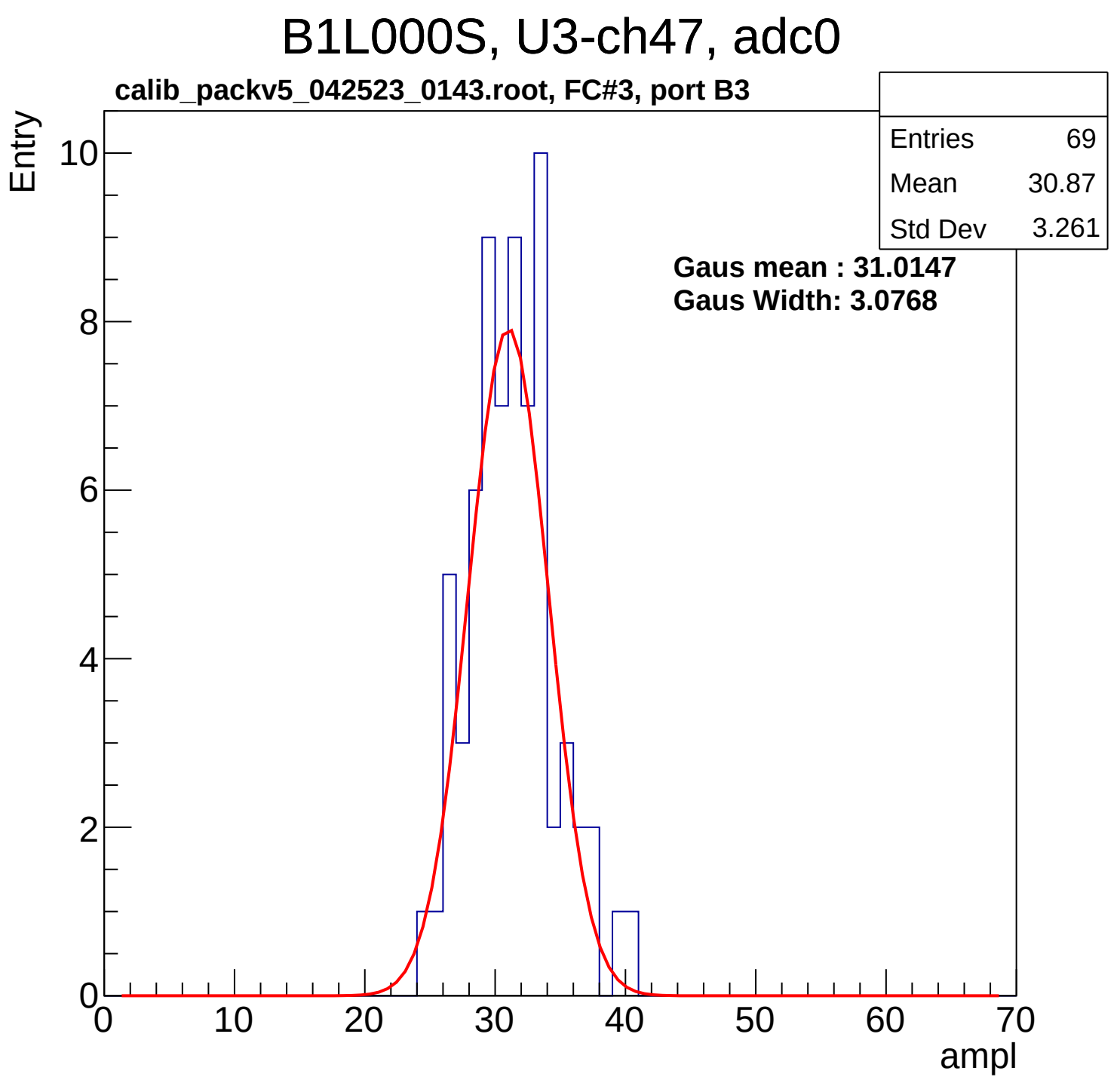
B1L000S, U3-ch47, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 30.87
Std Dev 3.261
Gaus mean : 31.0147
Gaus Width: 3.0768
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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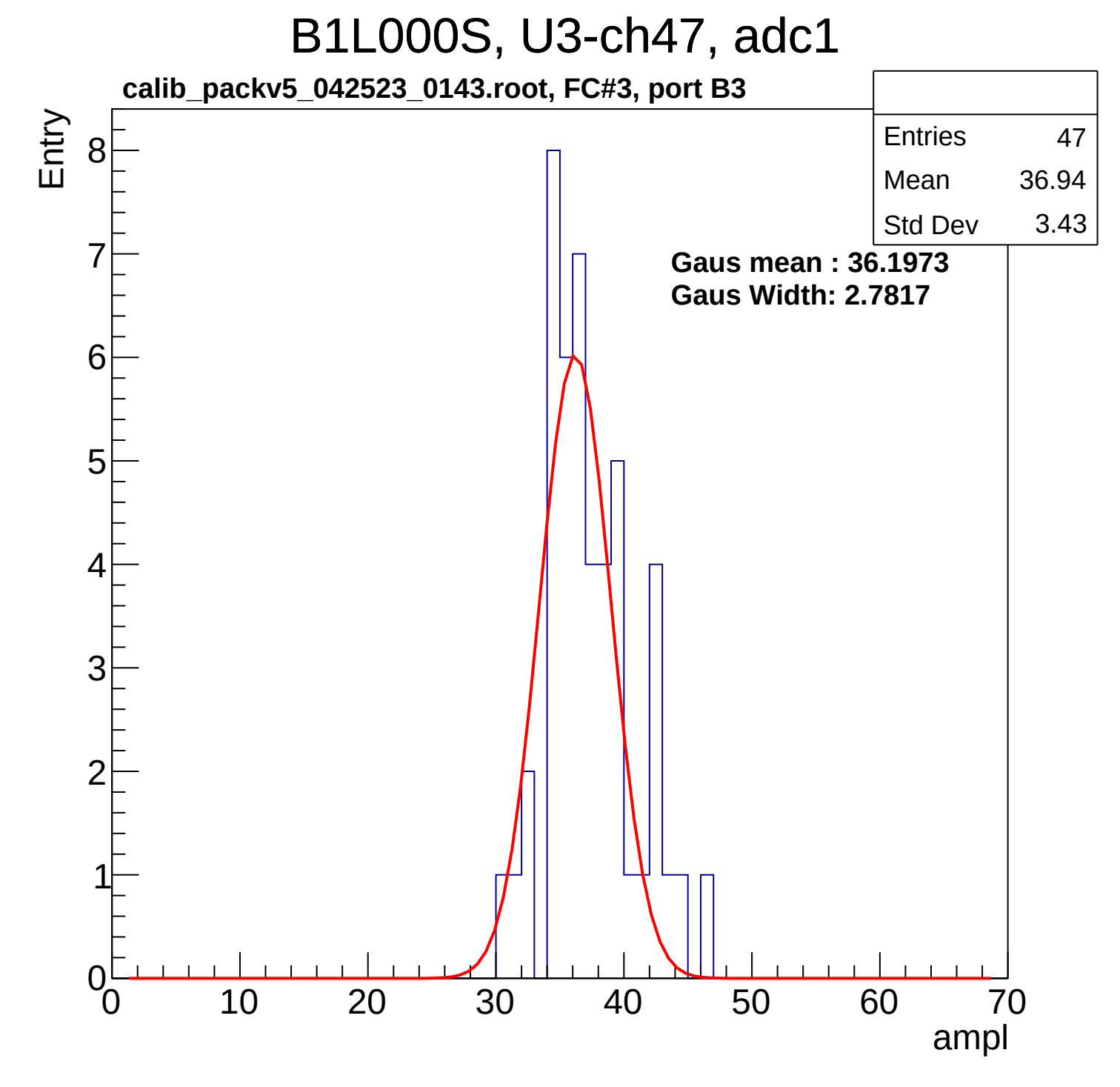
B1L000S, U3-ch47, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 36.94
Std Dev 3.43
Gaus mean : 36.1973
Gaus Width: 2.7817
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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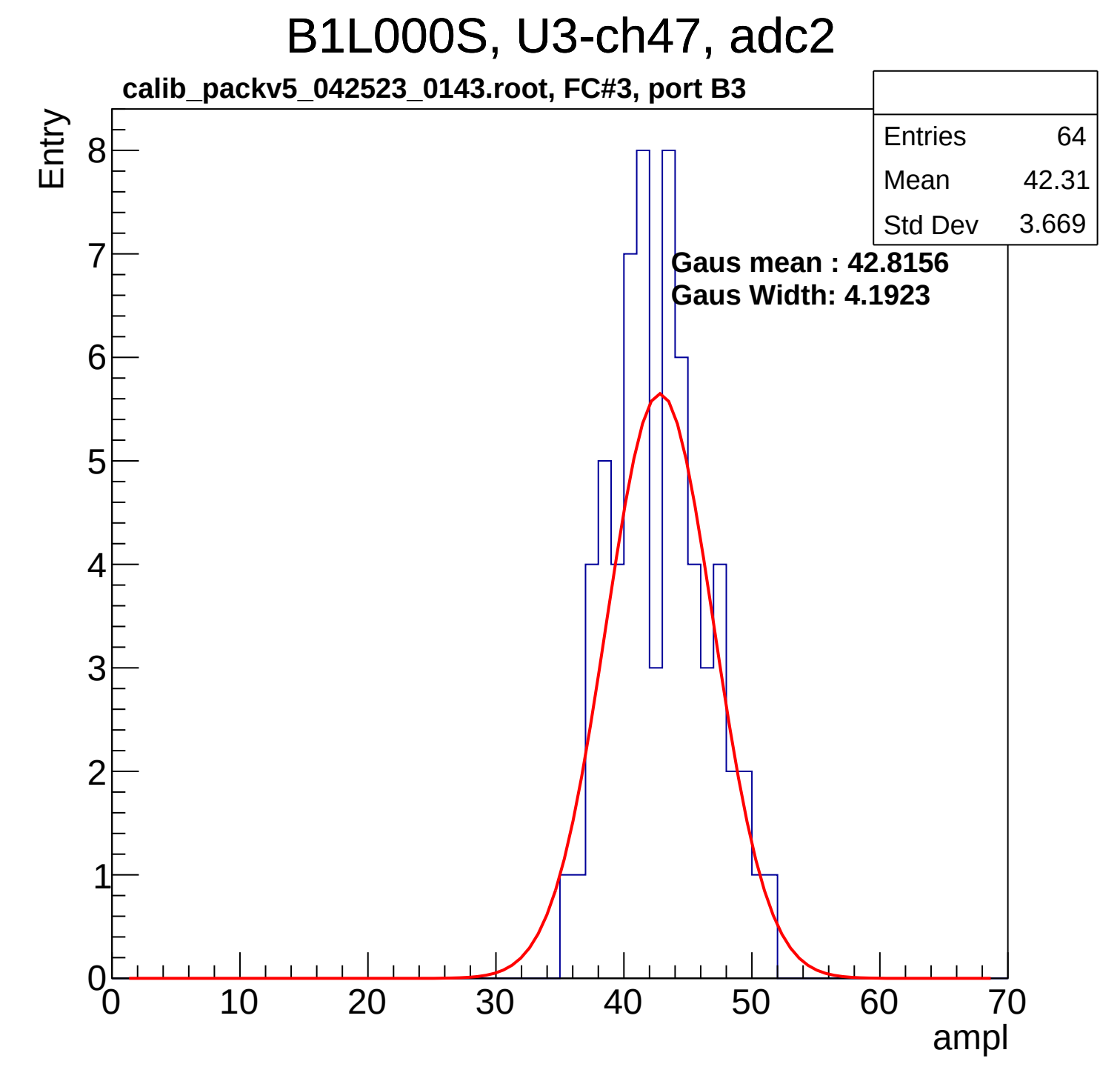
B1L000S, U3-ch47, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42.31
Std Dev 3.669
Gaus mean : 42.8156
Gaus Width: 4.1923
Entry
ampl
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1
2
3
4
5
6
7
8
0
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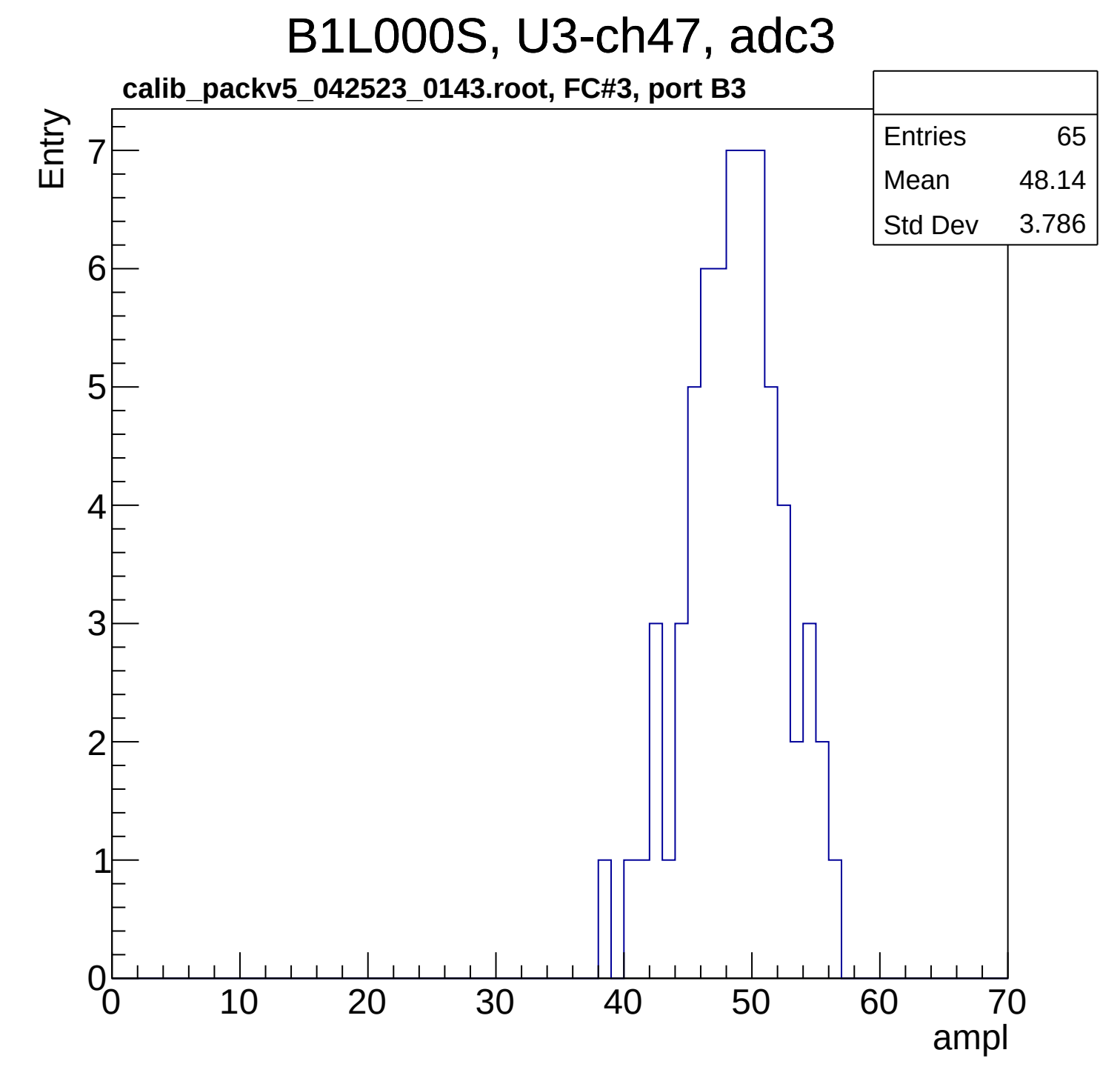
B1L000S, U3-ch47, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 48.14
Std Dev 3.786
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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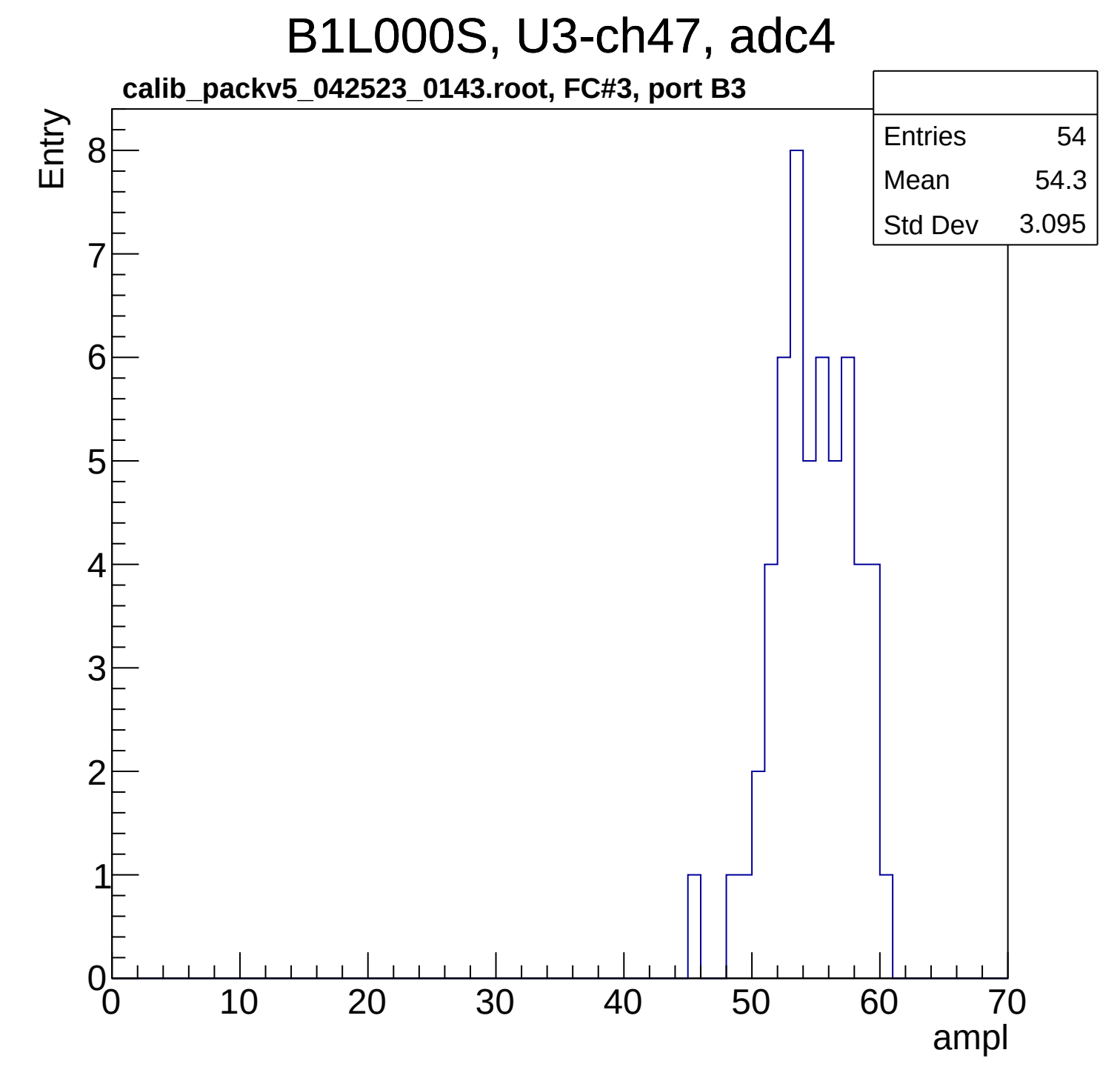
B1L000S, U3-ch47, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.3
Std Dev 3.095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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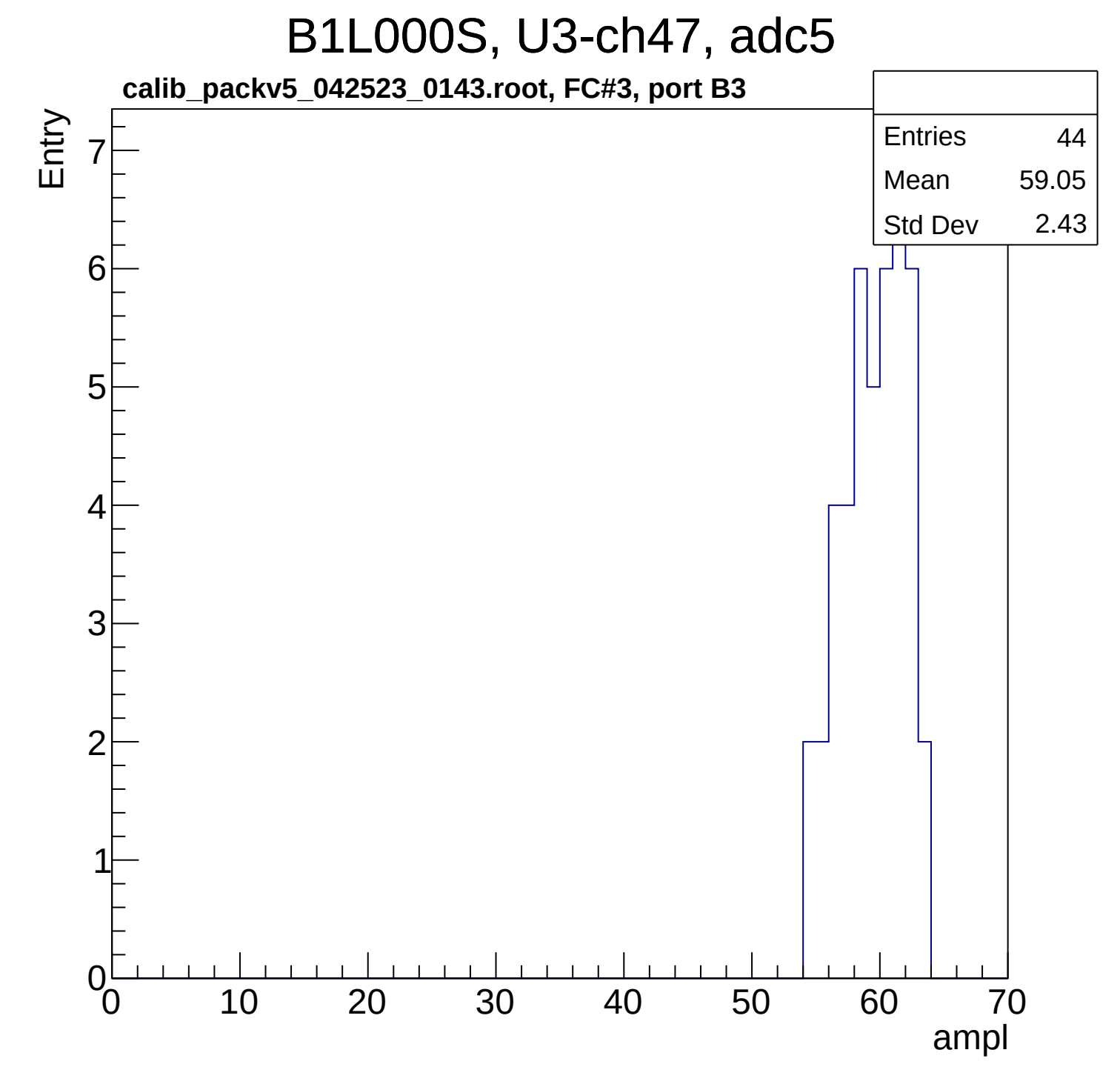
B1L000S, U3-ch47, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 59.05
Std Dev 2.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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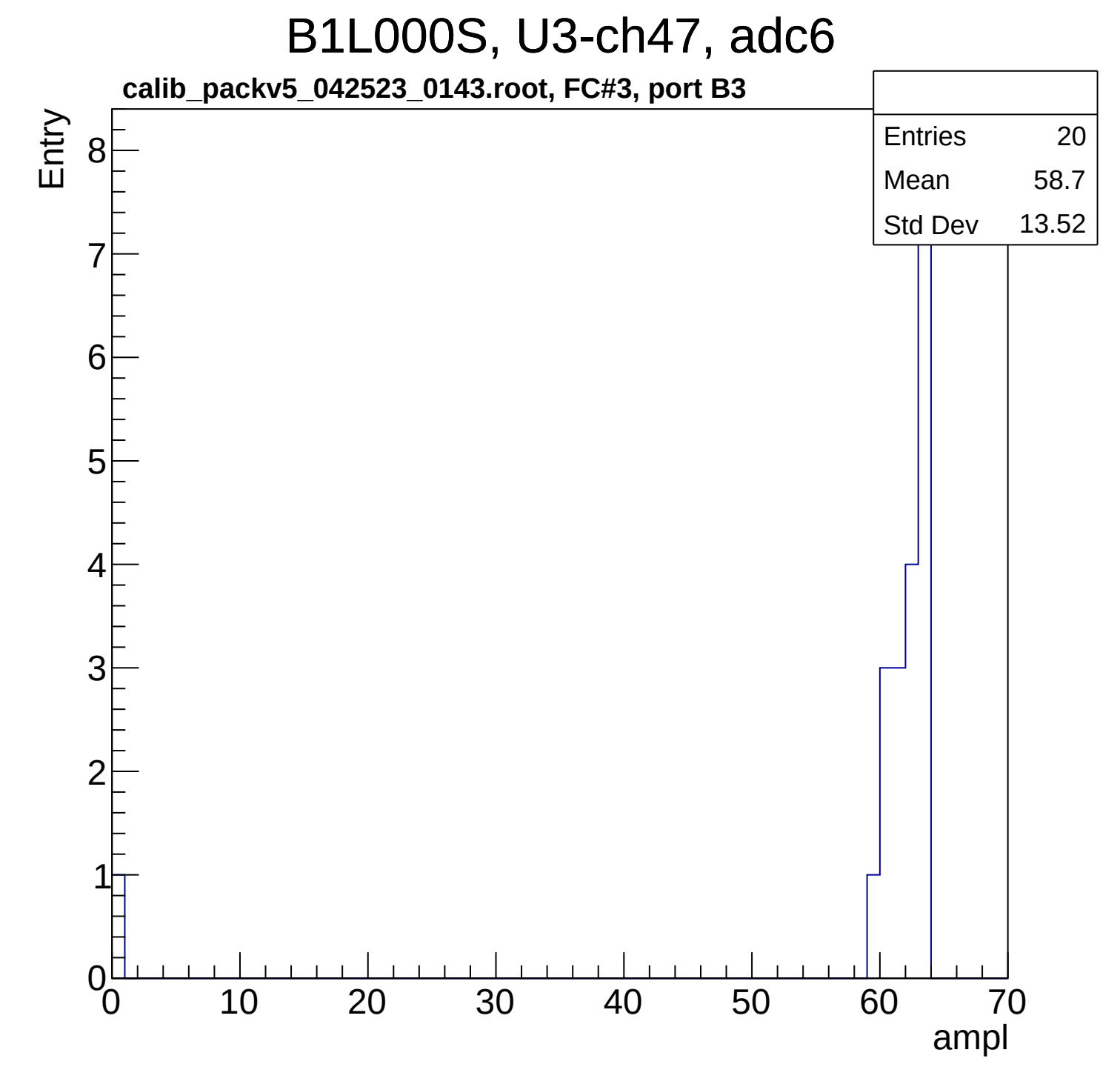
B1L000S, U3-ch47, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58.7
Std Dev 13.52
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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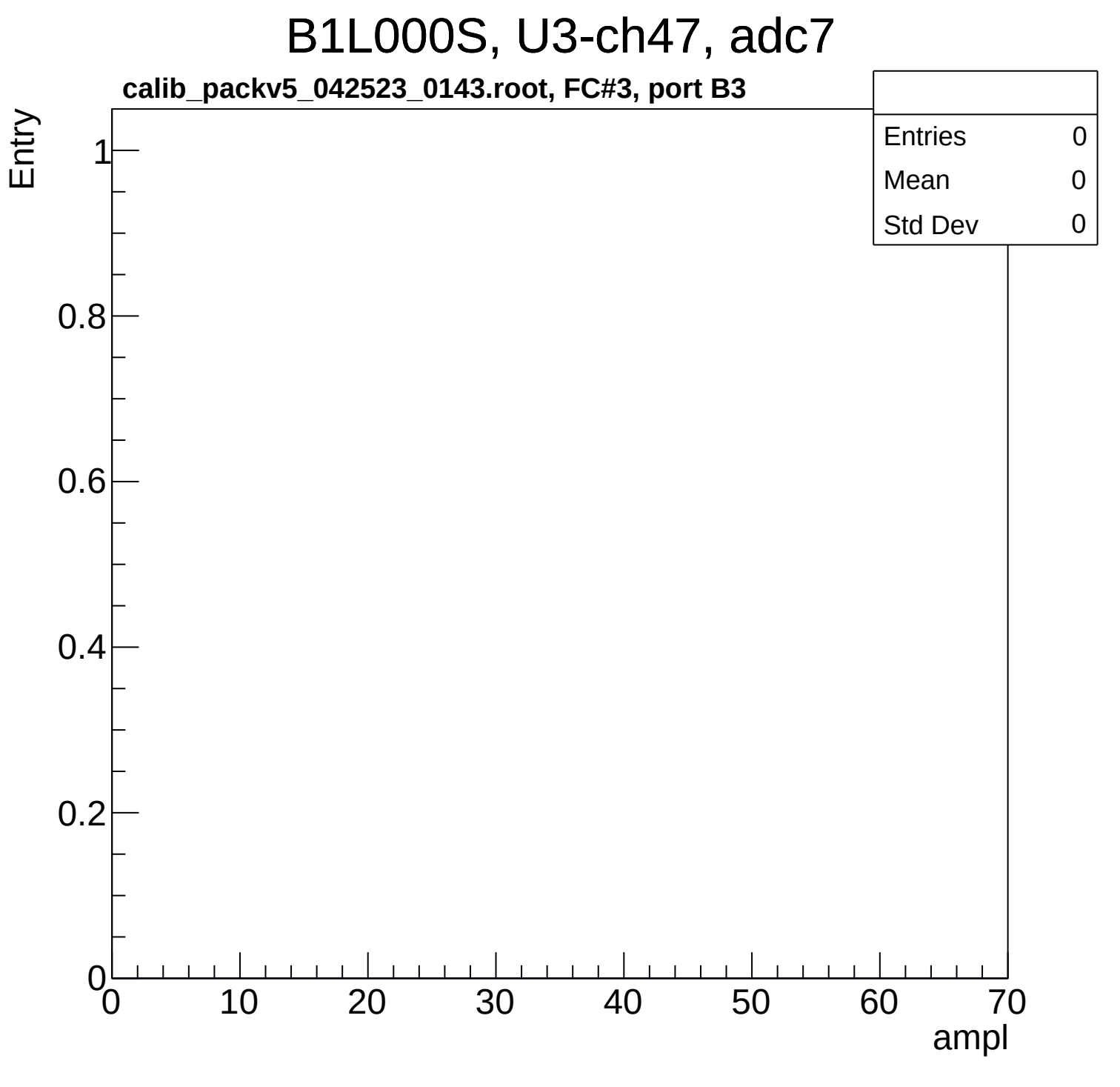
B1L000S, U3-ch47, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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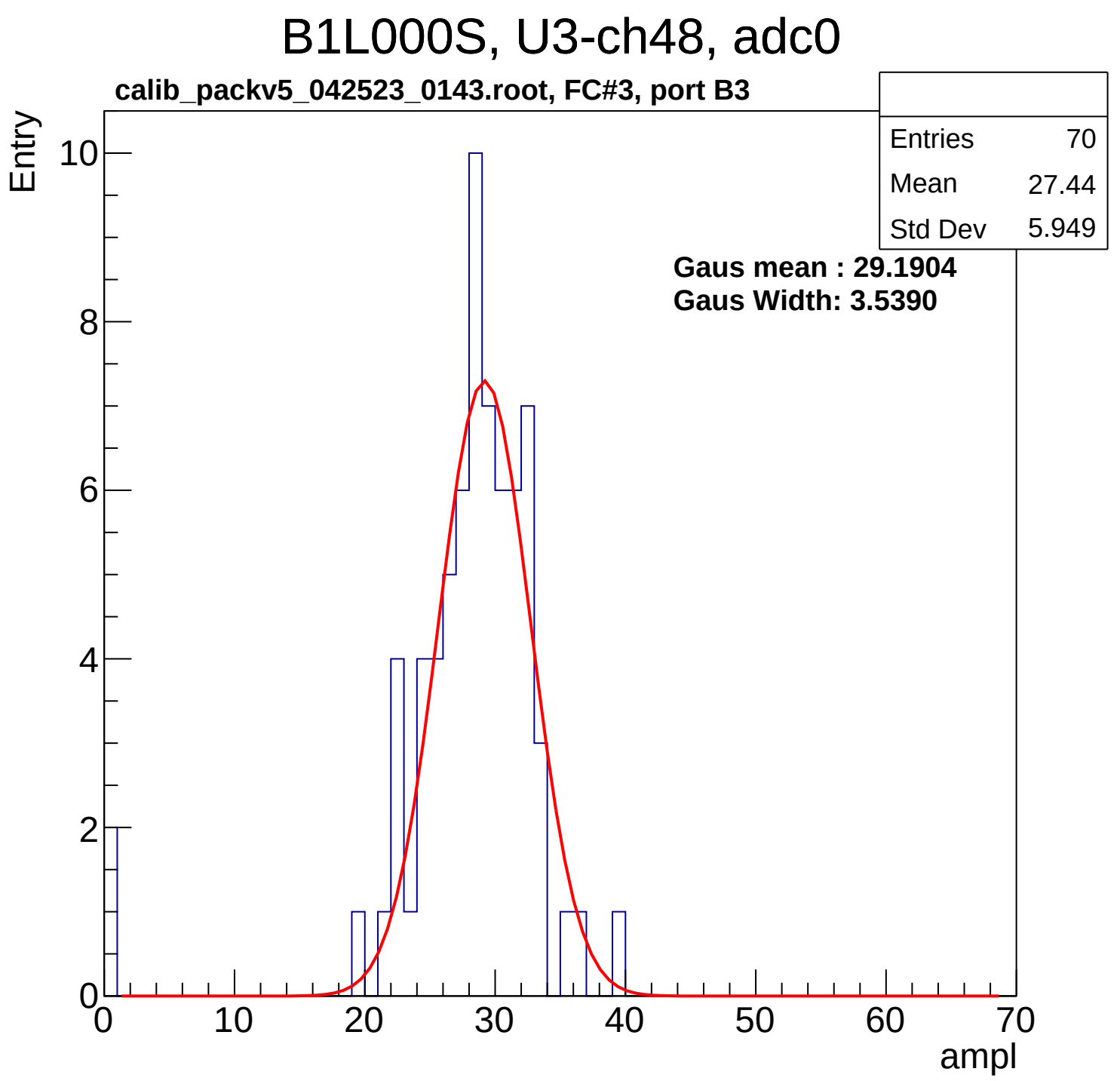
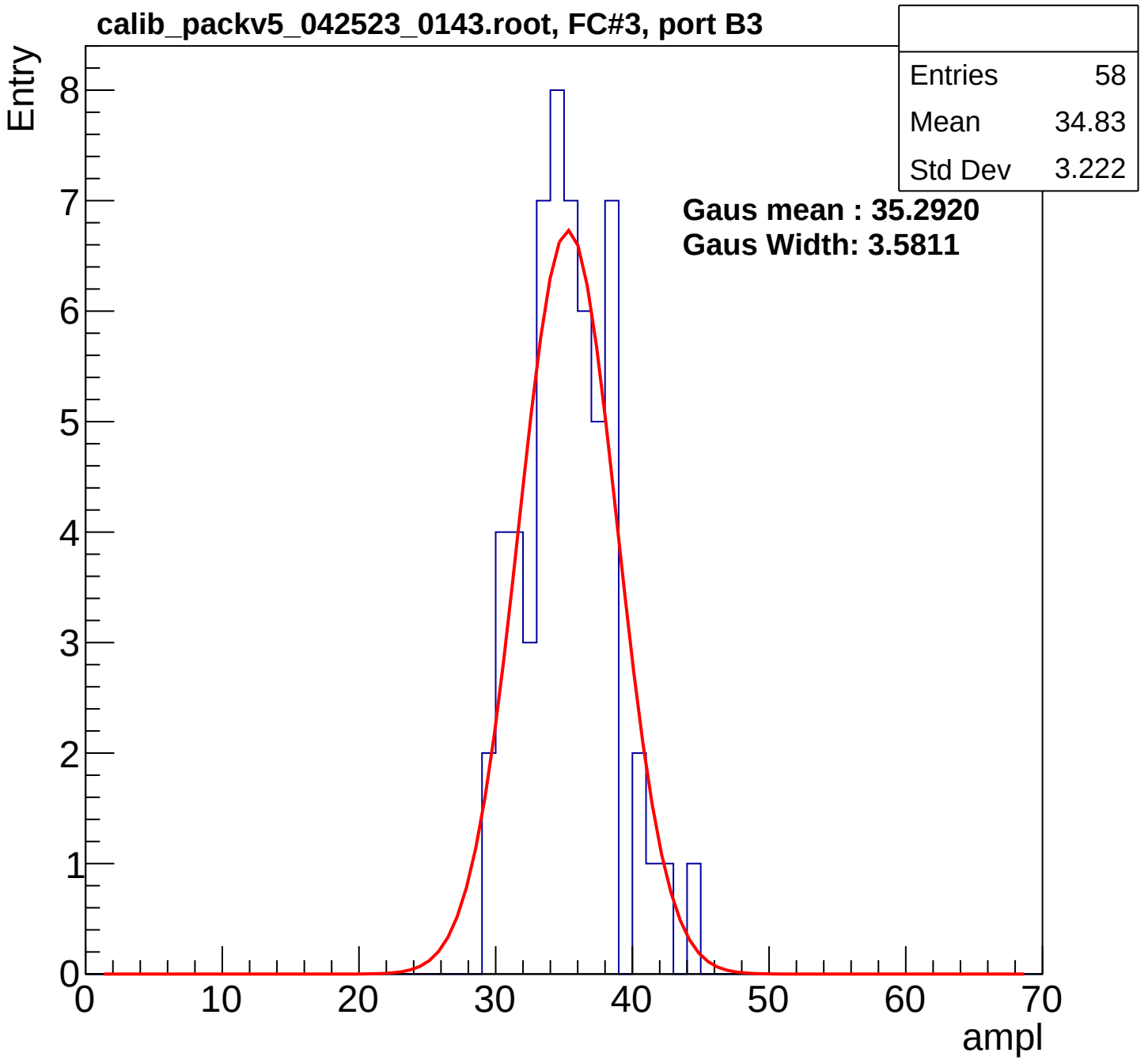
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch48, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 27.44
Std Dev 5.949
Gaus mean : 29.1904
Gaus Width: 3.5390
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch48, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 34.83
Std Dev 3.222
Gaus mean : 35.2920
Gaus Width: 3.5811
Entry
ampl
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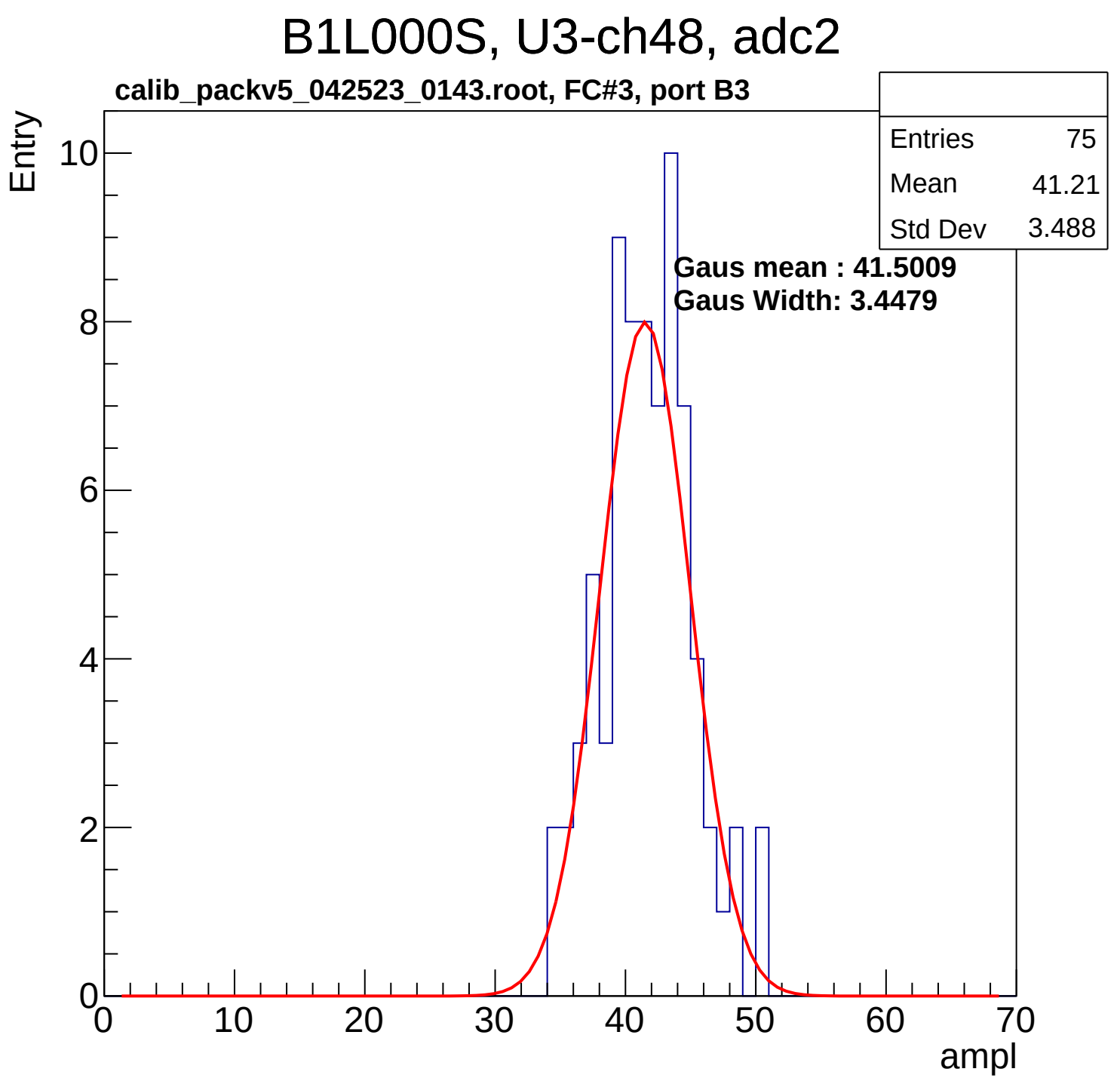
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch48, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 41.21
Std Dev 3.488
Gaus mean : 41.5009
Gaus Width: 3.4479
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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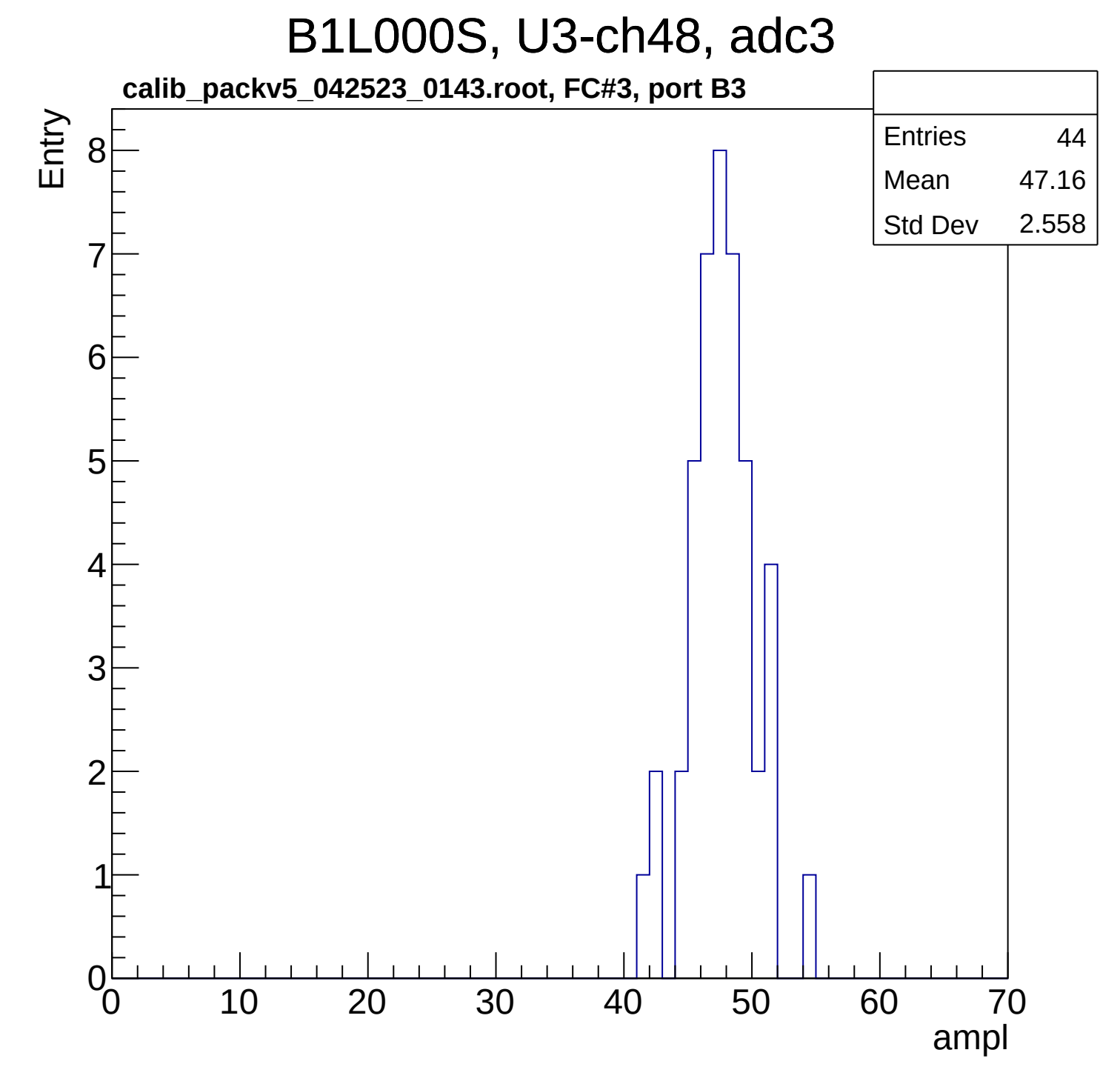
B1L000S, U3-ch48, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 47.16
Std Dev 2.558
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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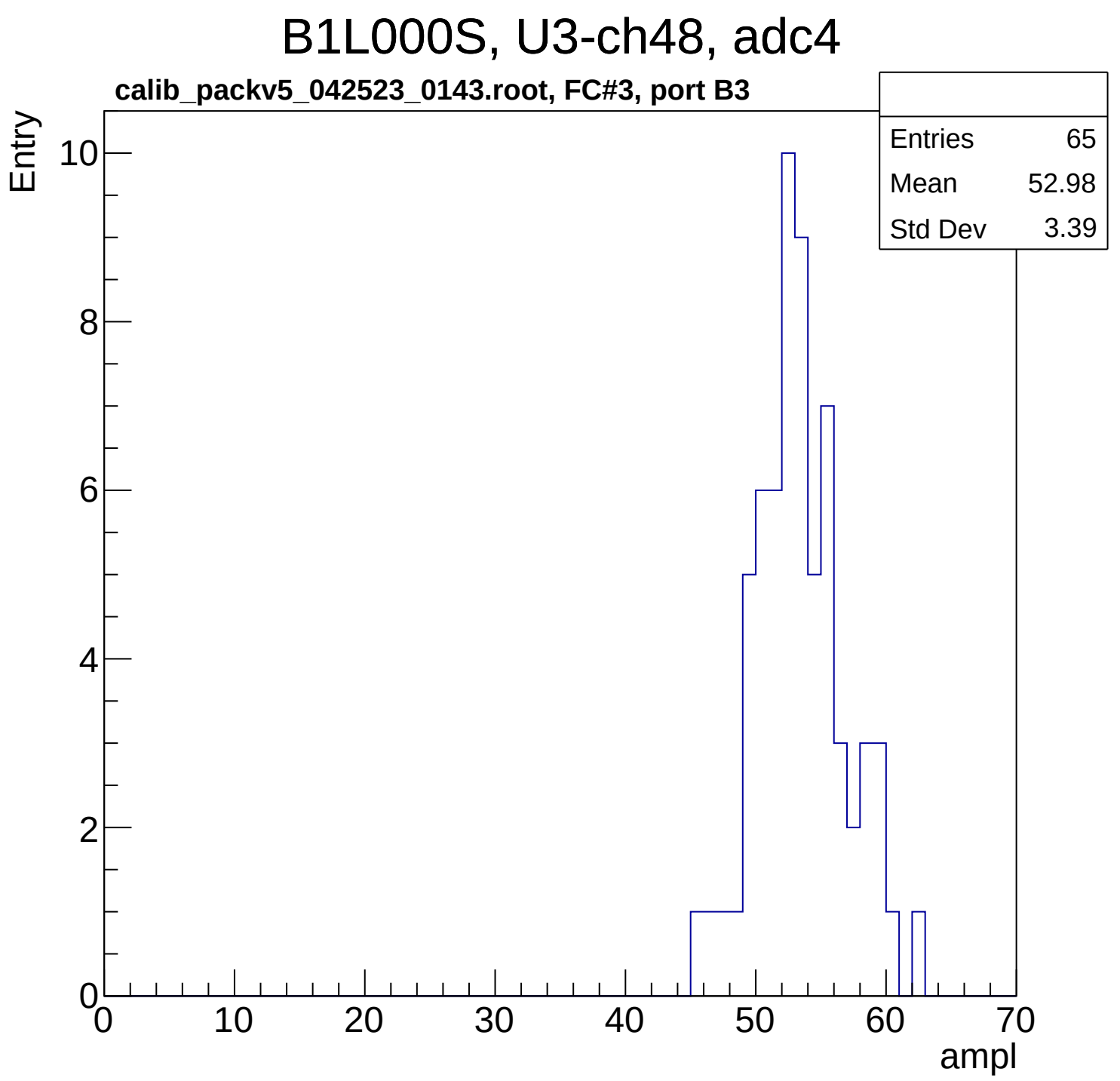
40
50
60
70


B1L000S, U3-ch48, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 52.98
Std Dev 3.39
Entry
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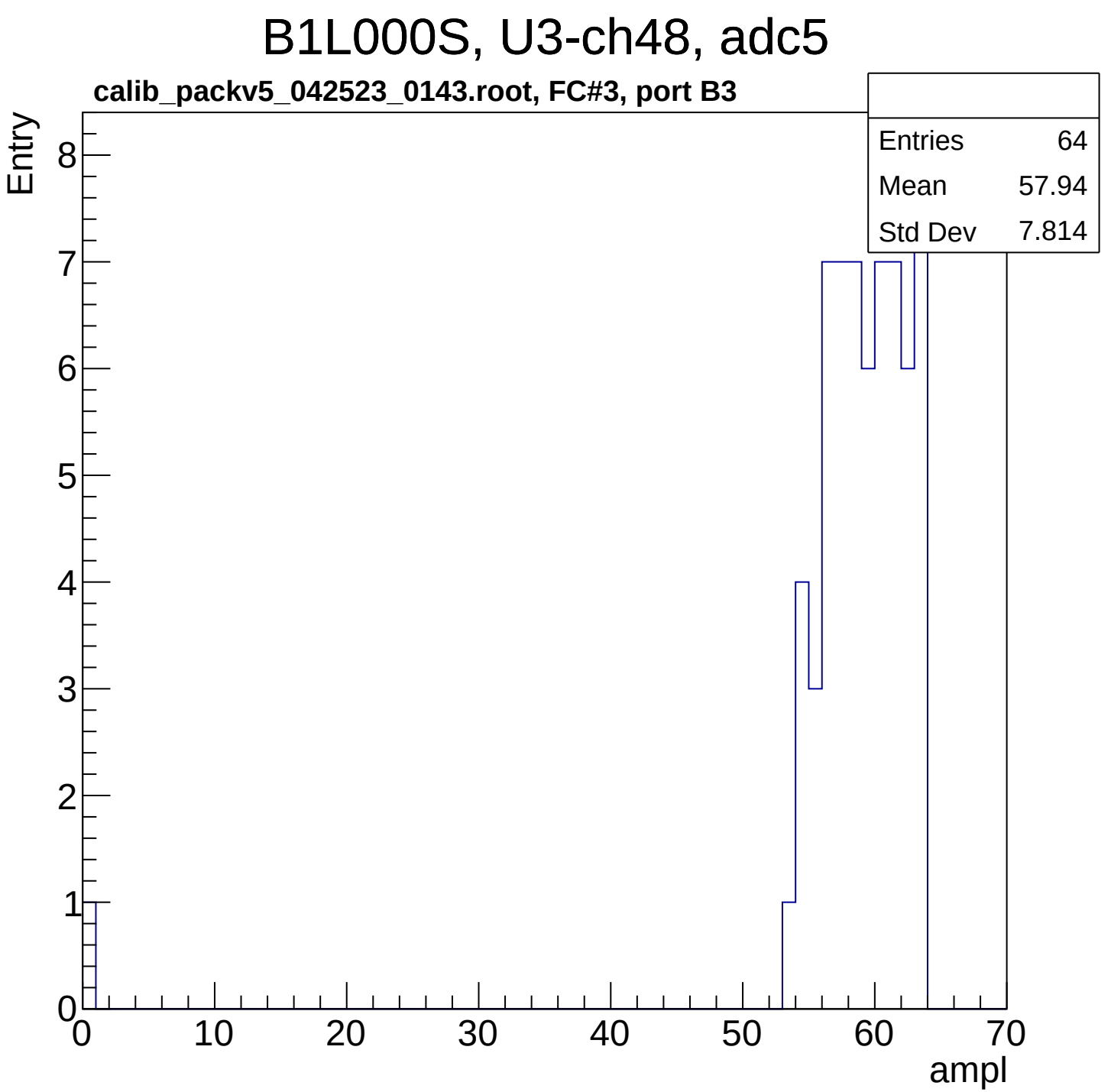
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch48, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 57.94
Std Dev 7.814
Entry
ampl
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4
5
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7
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0
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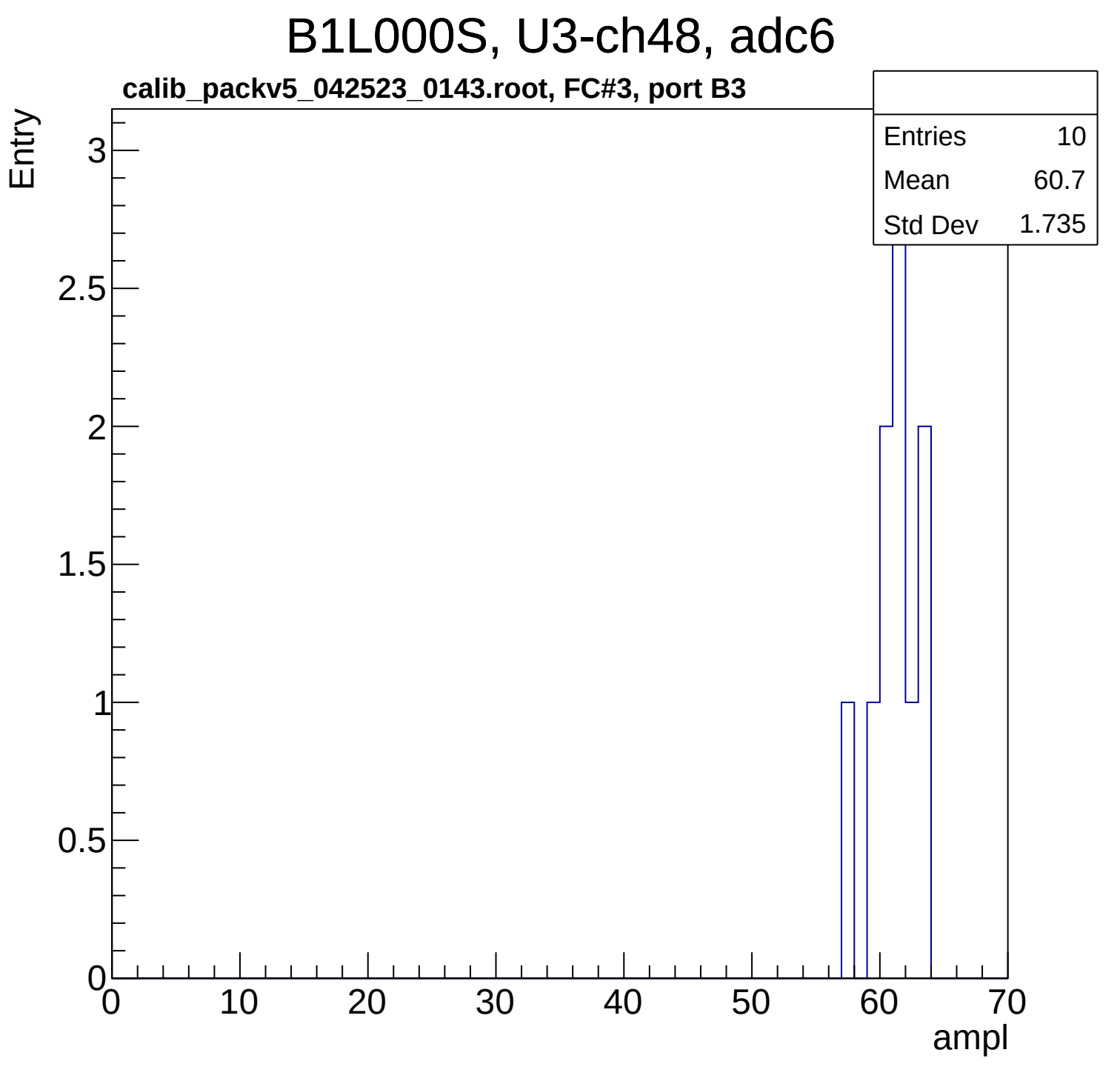
B1L000S, U3-ch48, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 60.7
Std Dev 1.735
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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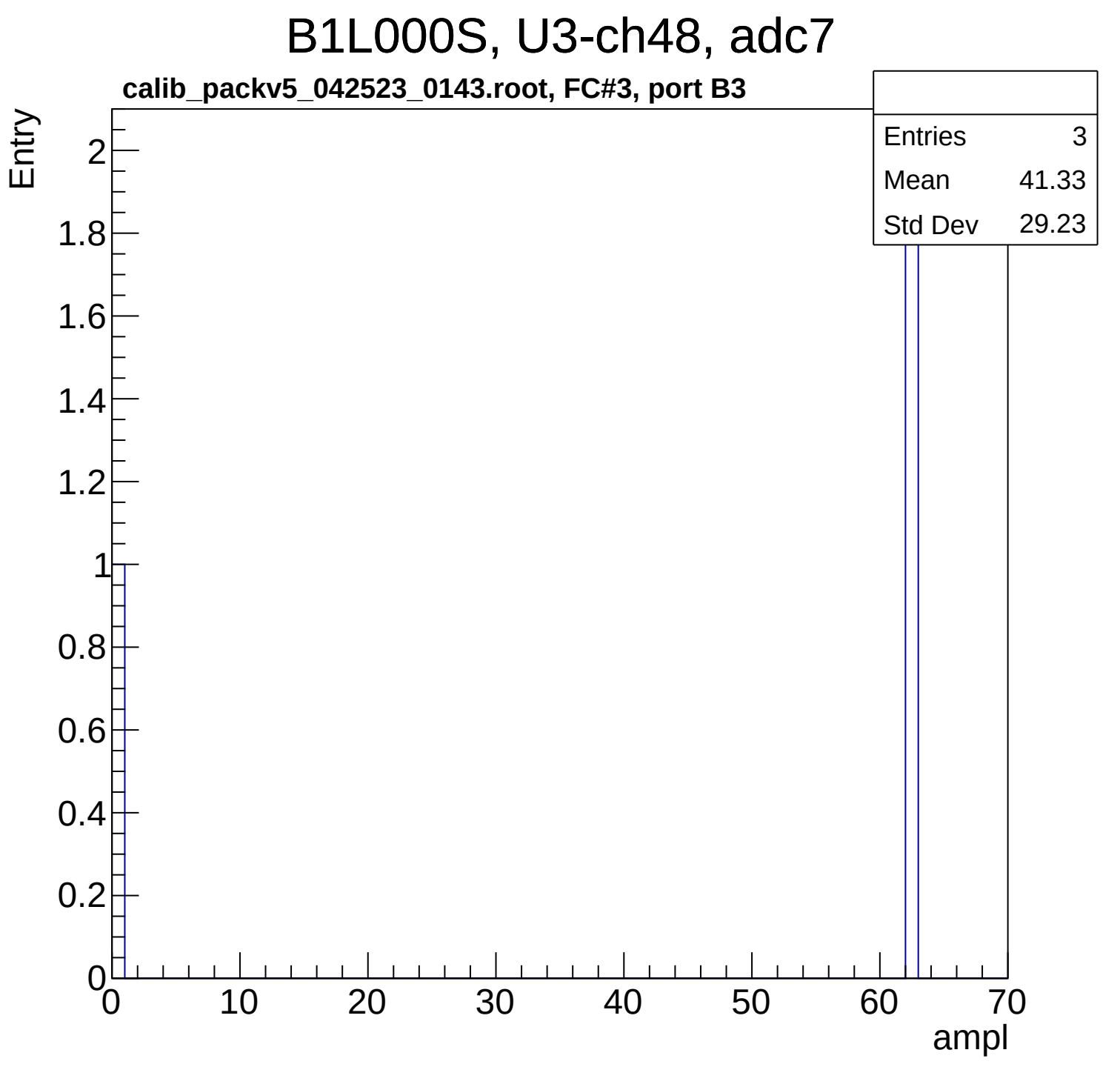
B1L000S, U3-ch48, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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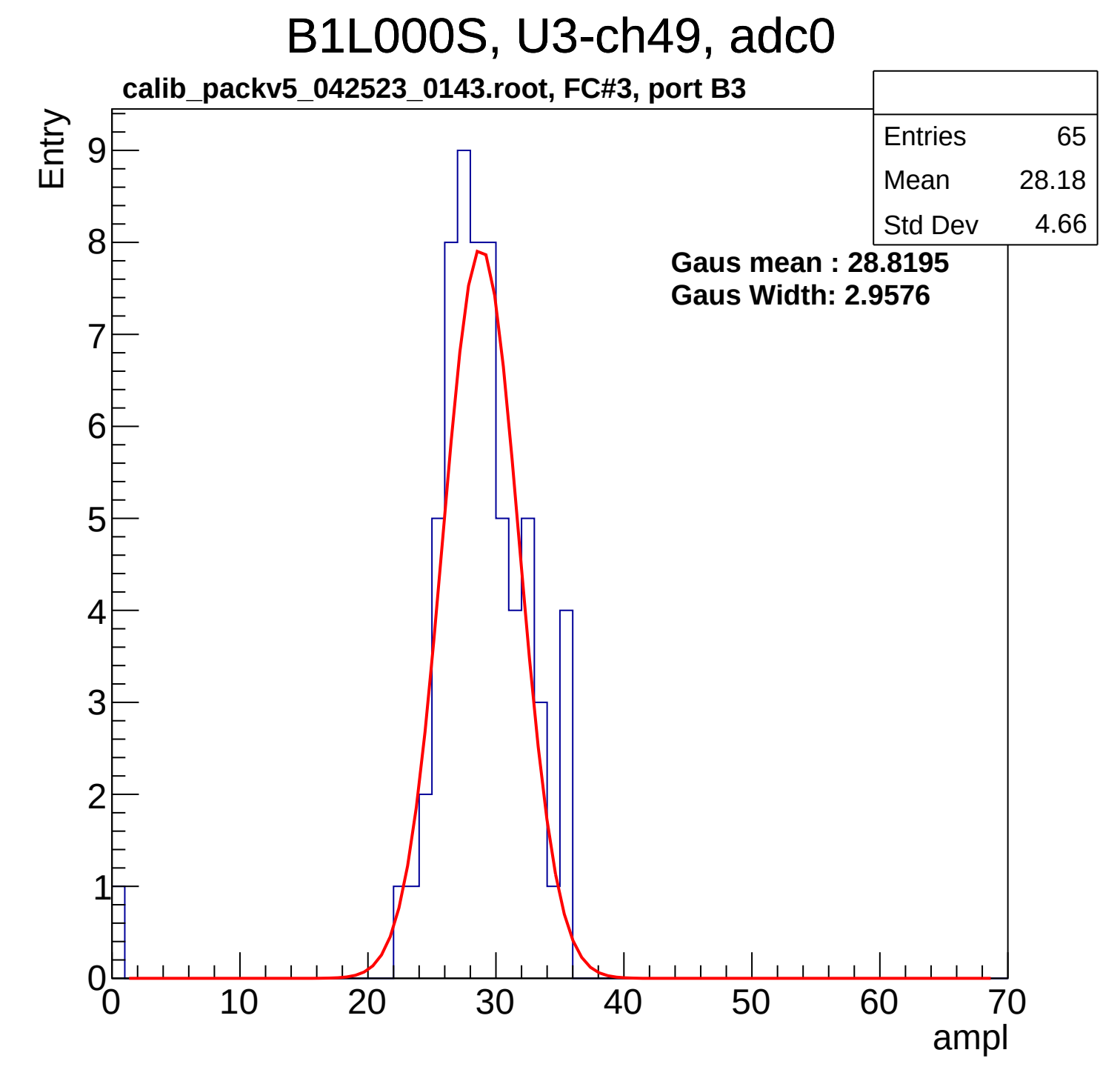
B1L000S, U3-ch49, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 28.18
Std Dev 4.66
Gaus mean : 28.8195
Gaus Width: 2.9576
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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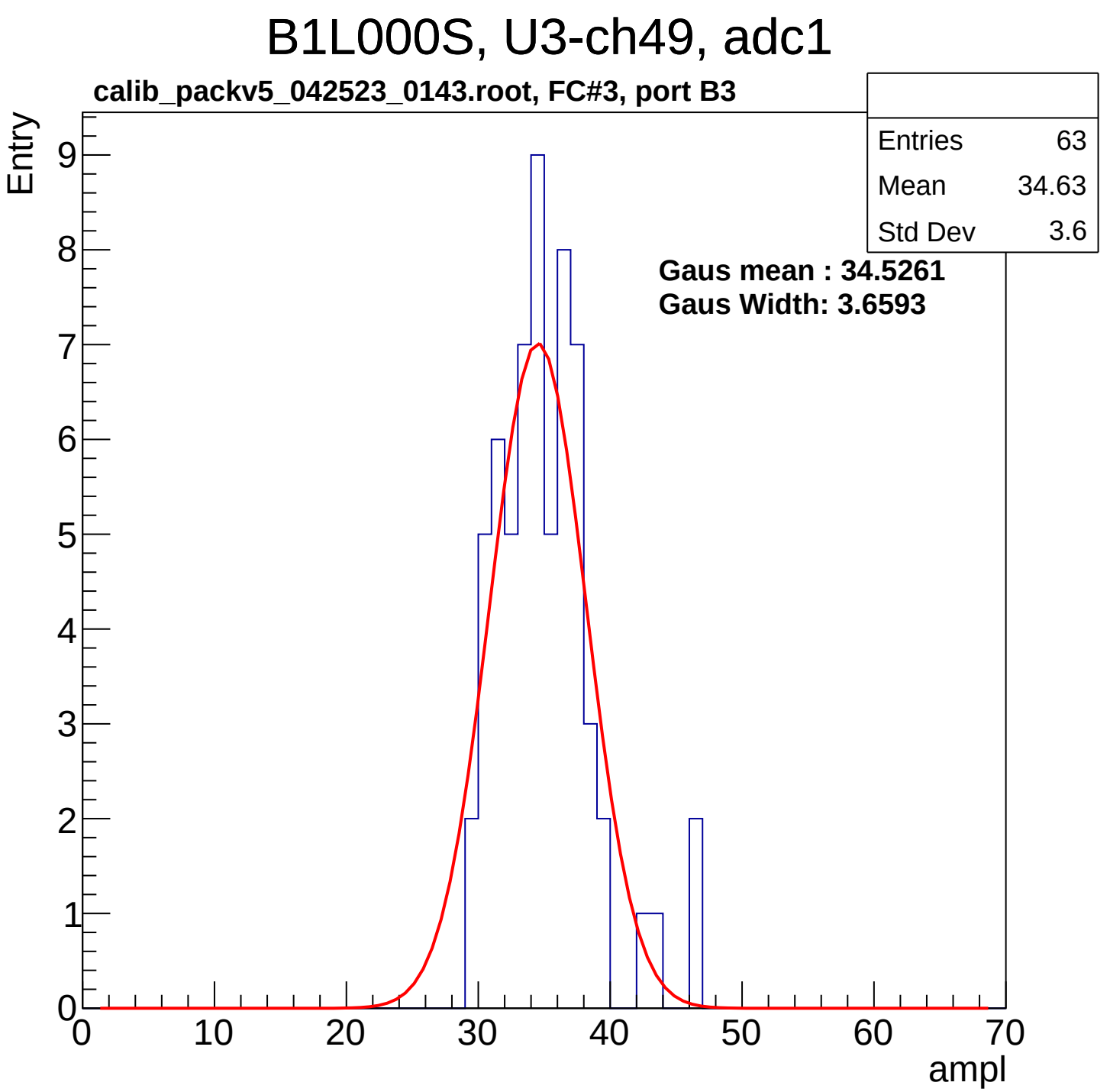
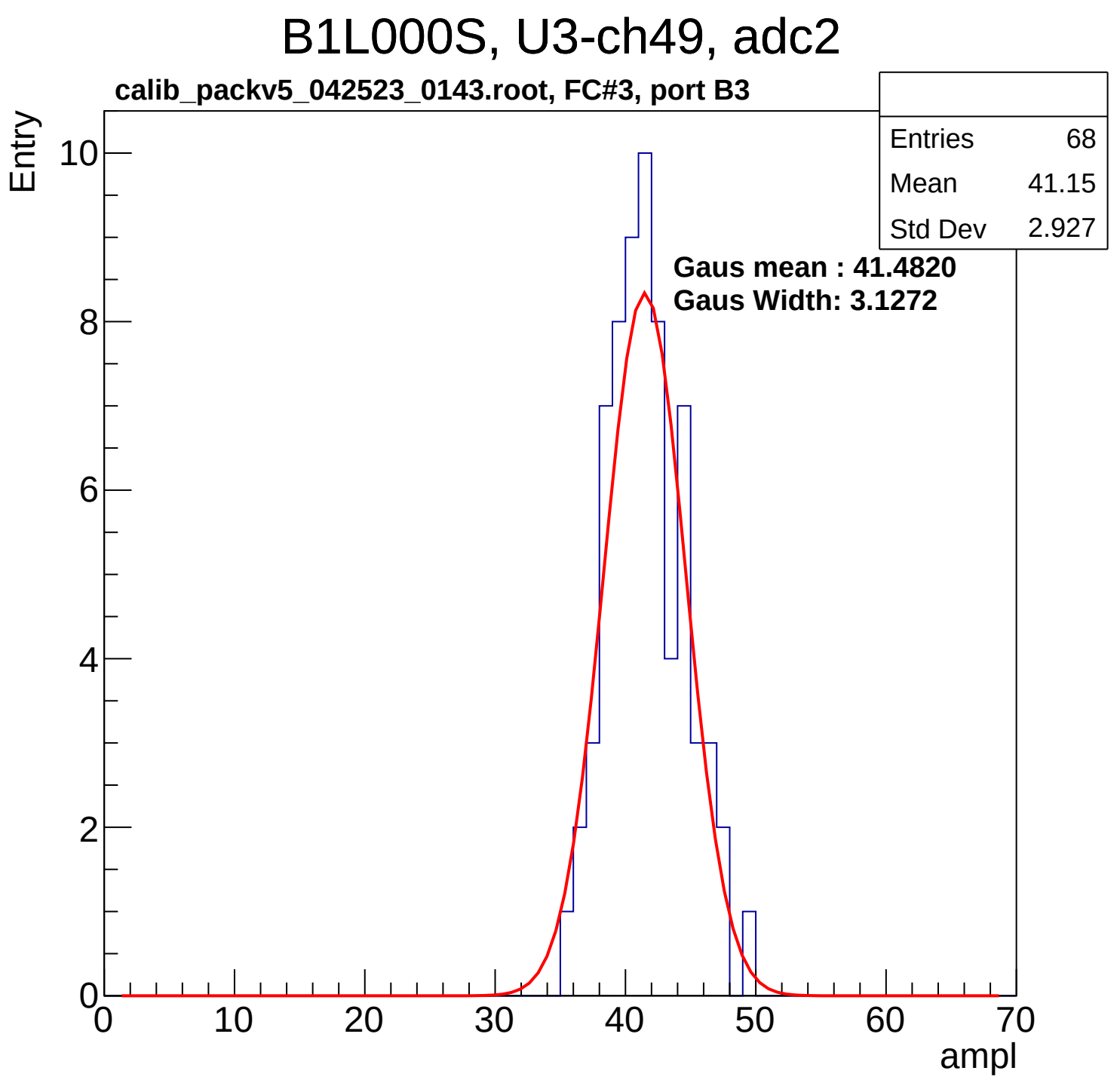
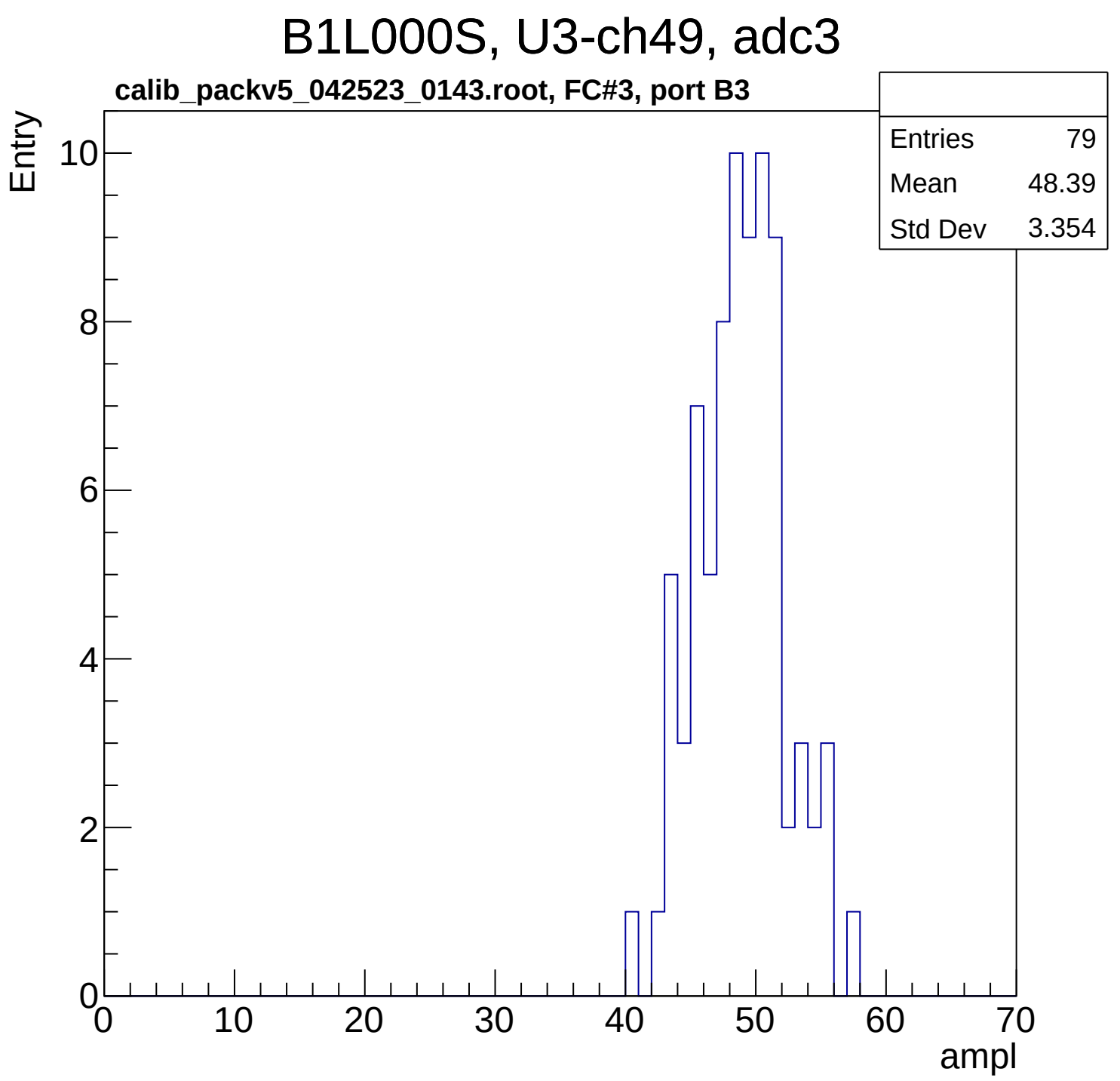

B1L000S, U3-ch49, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.63
Std Dev 3.6
Gaus mean : 34.5261
Gaus Width: 3.6593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch49, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 41.15
Std Dev 2.927
Gaus mean : 41.4820
Gaus Width: 3.1272
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch49, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 48.39
Std Dev 3.354
Entry
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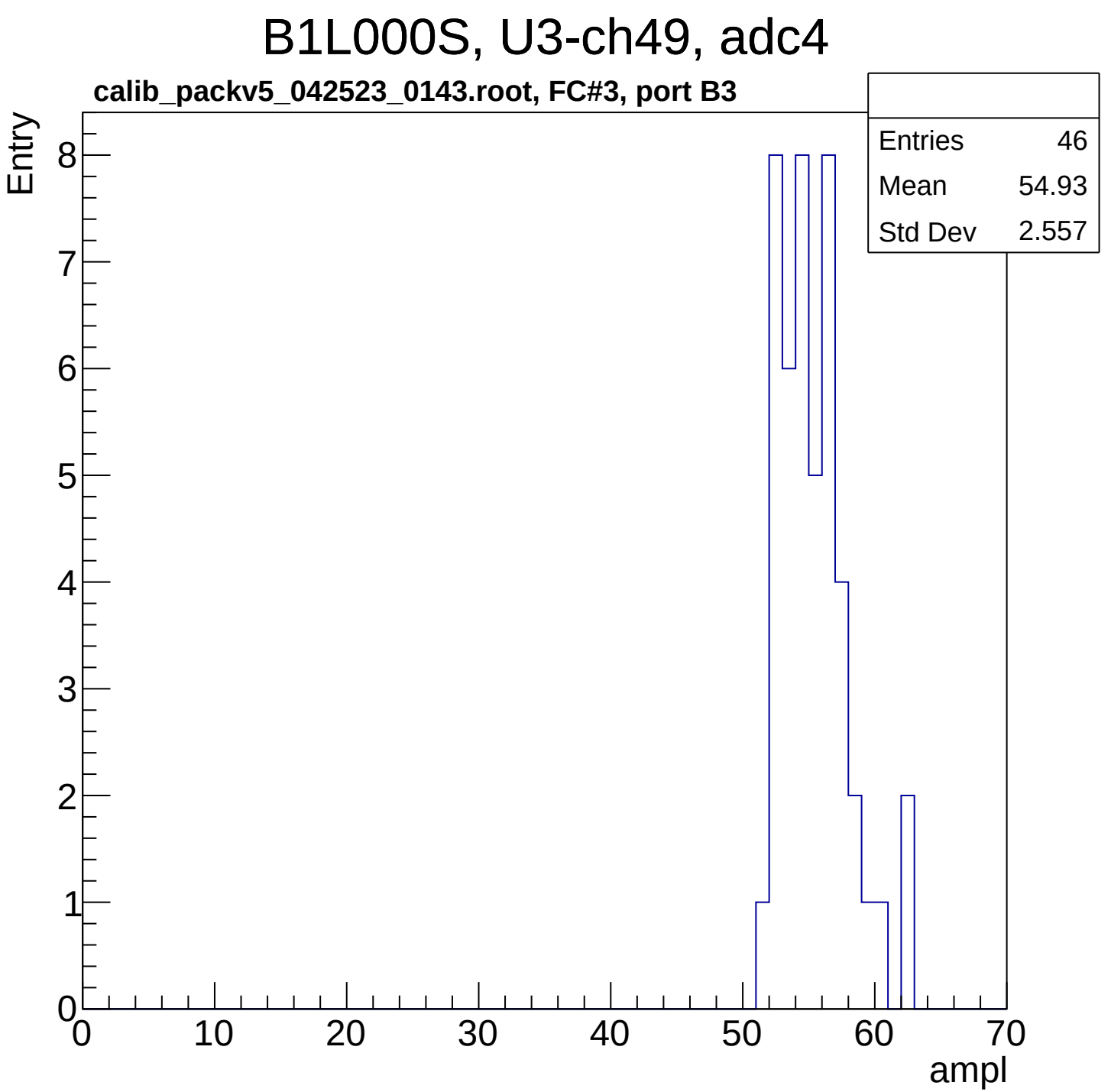
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch49, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 54.93
Std Dev 2.557
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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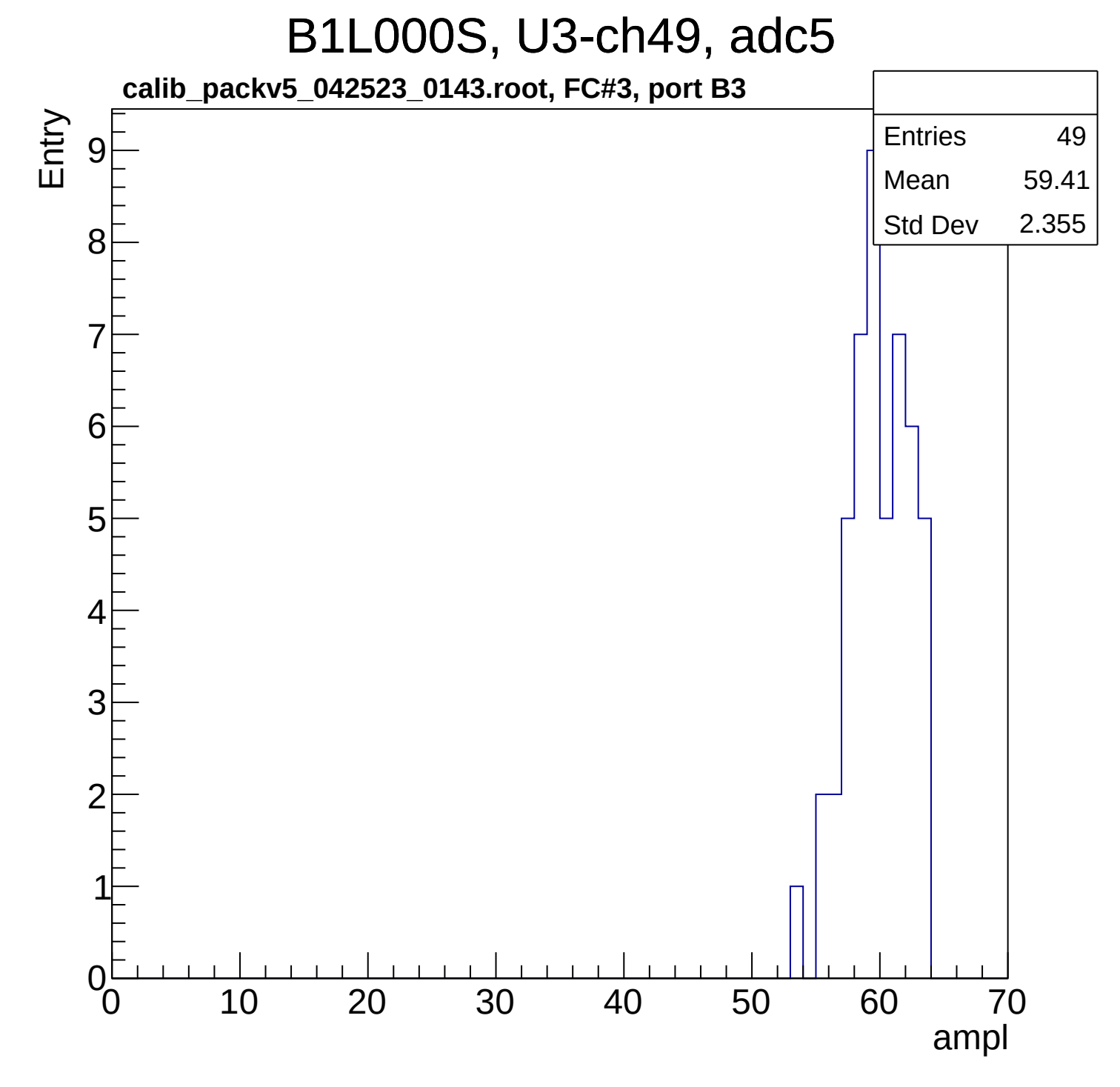
B1L000S, U3-ch49, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 59.41
Std Dev 2.355
Entry
ampl
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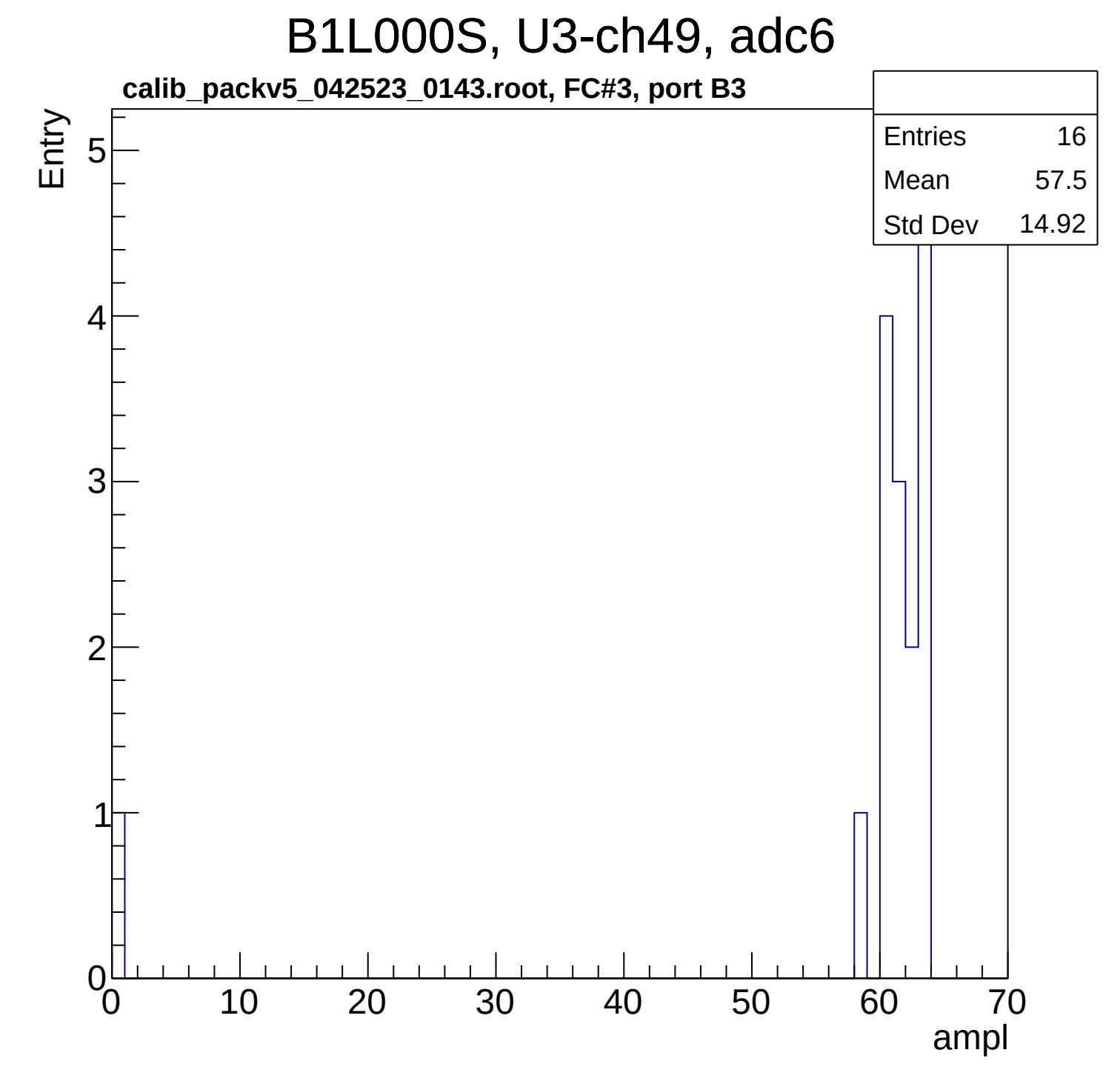
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch49, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.5
Std Dev 14.92
Entry
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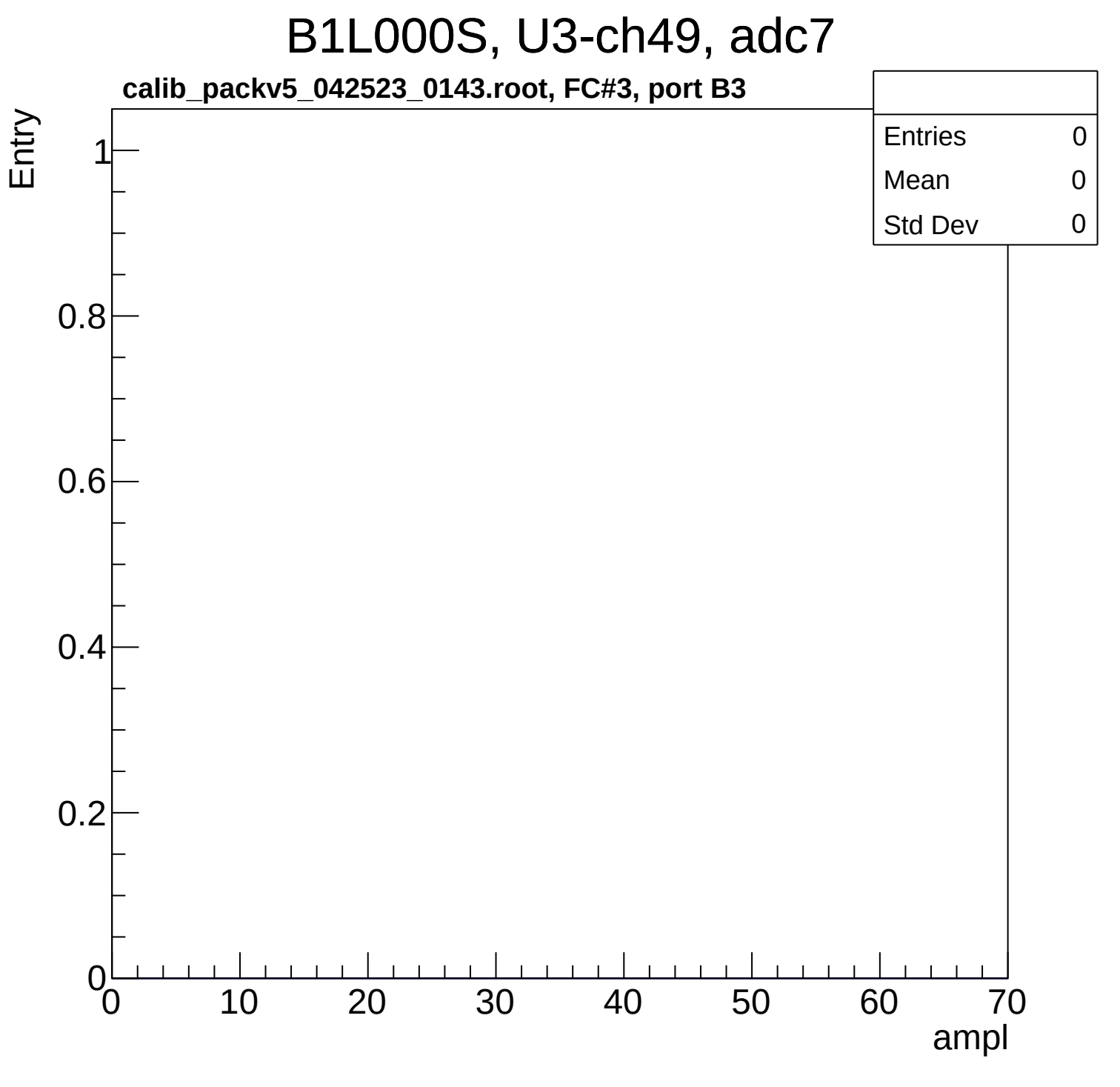
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch49, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.4
0.2
0
0
10
20
30
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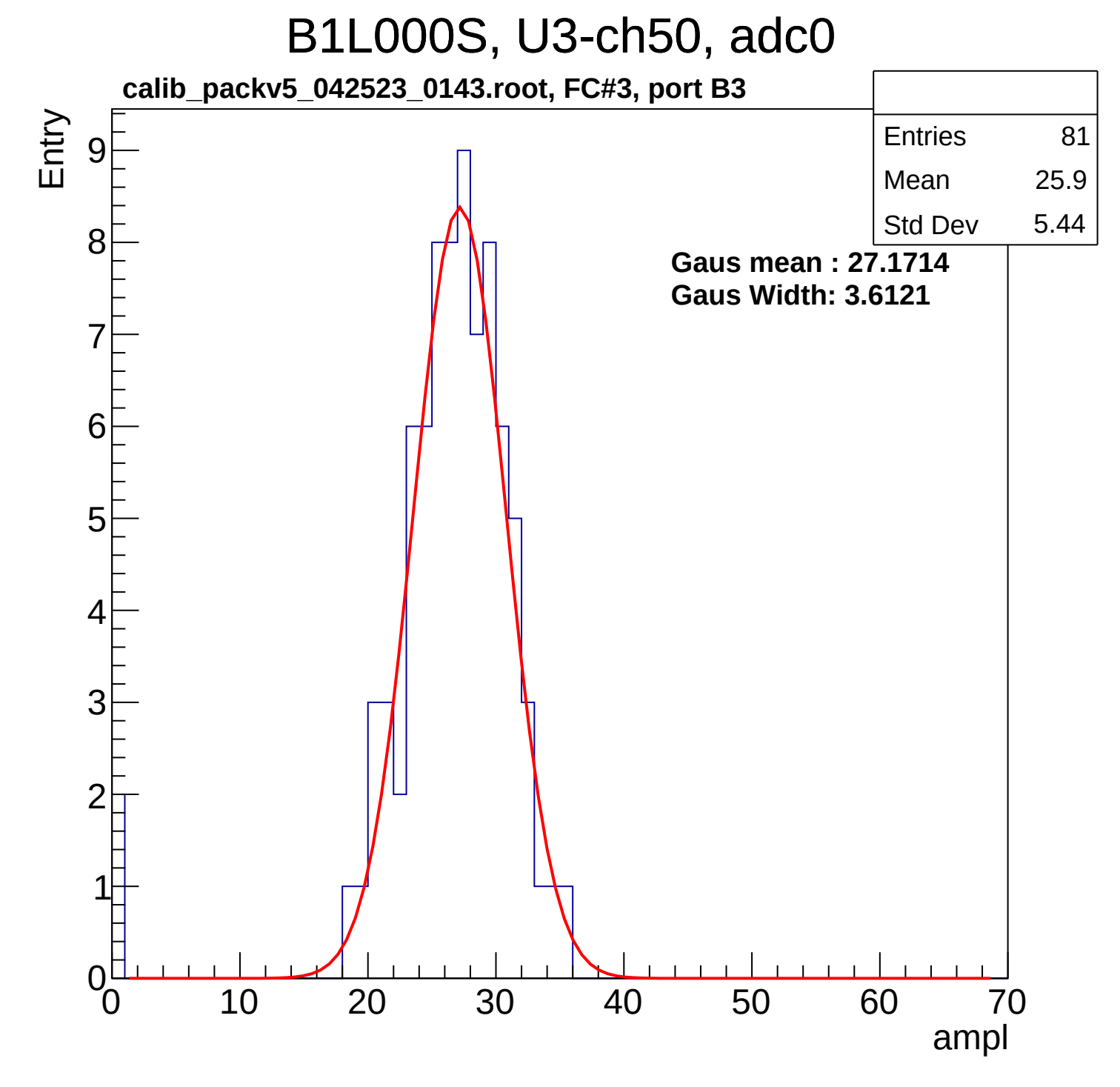
50
60
70
ampl


B1L000S, U3-ch50, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 25.9
Std Dev 5.44
Gaus mean : 27.1714
Gaus Width: 3.6121
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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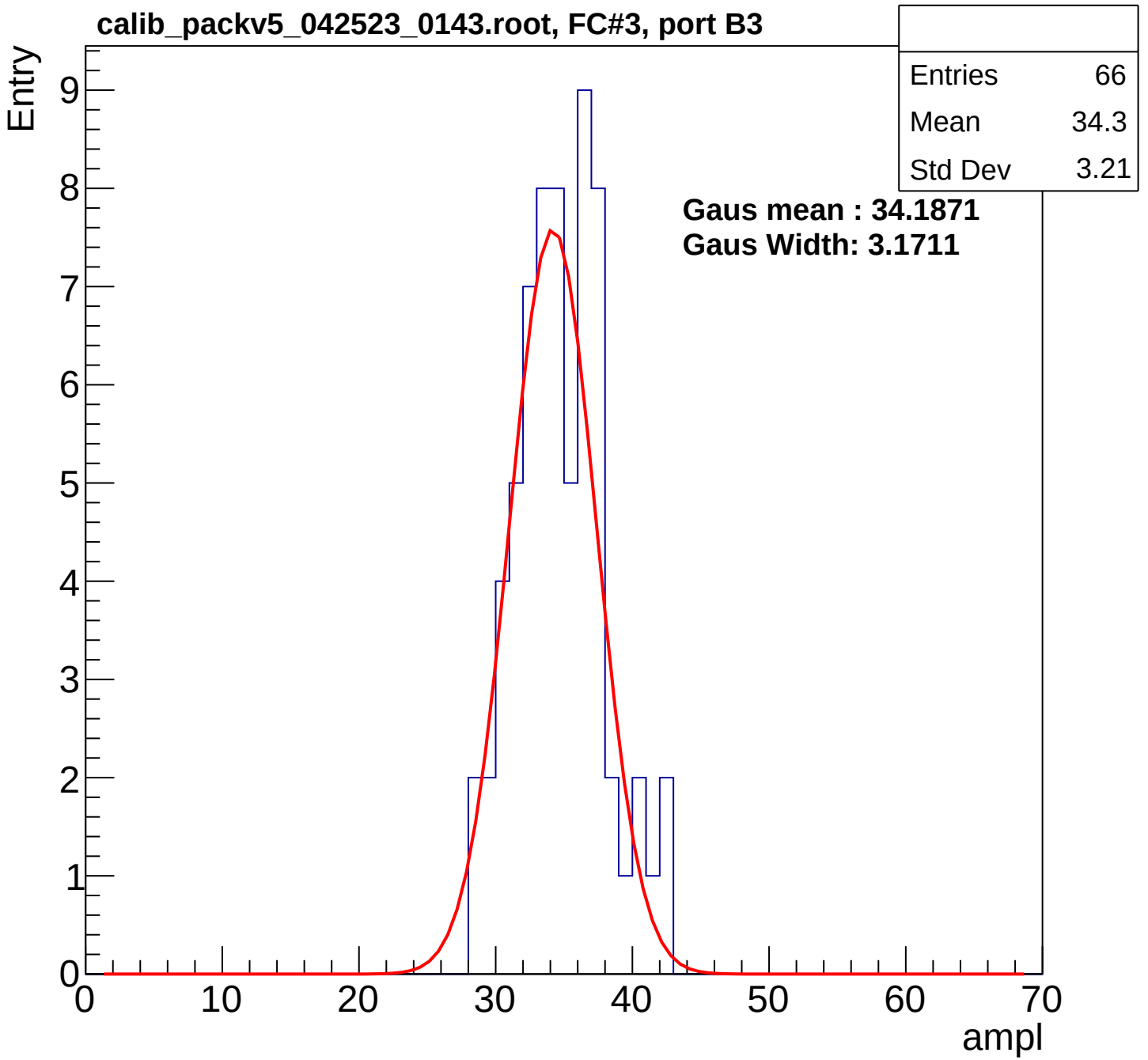
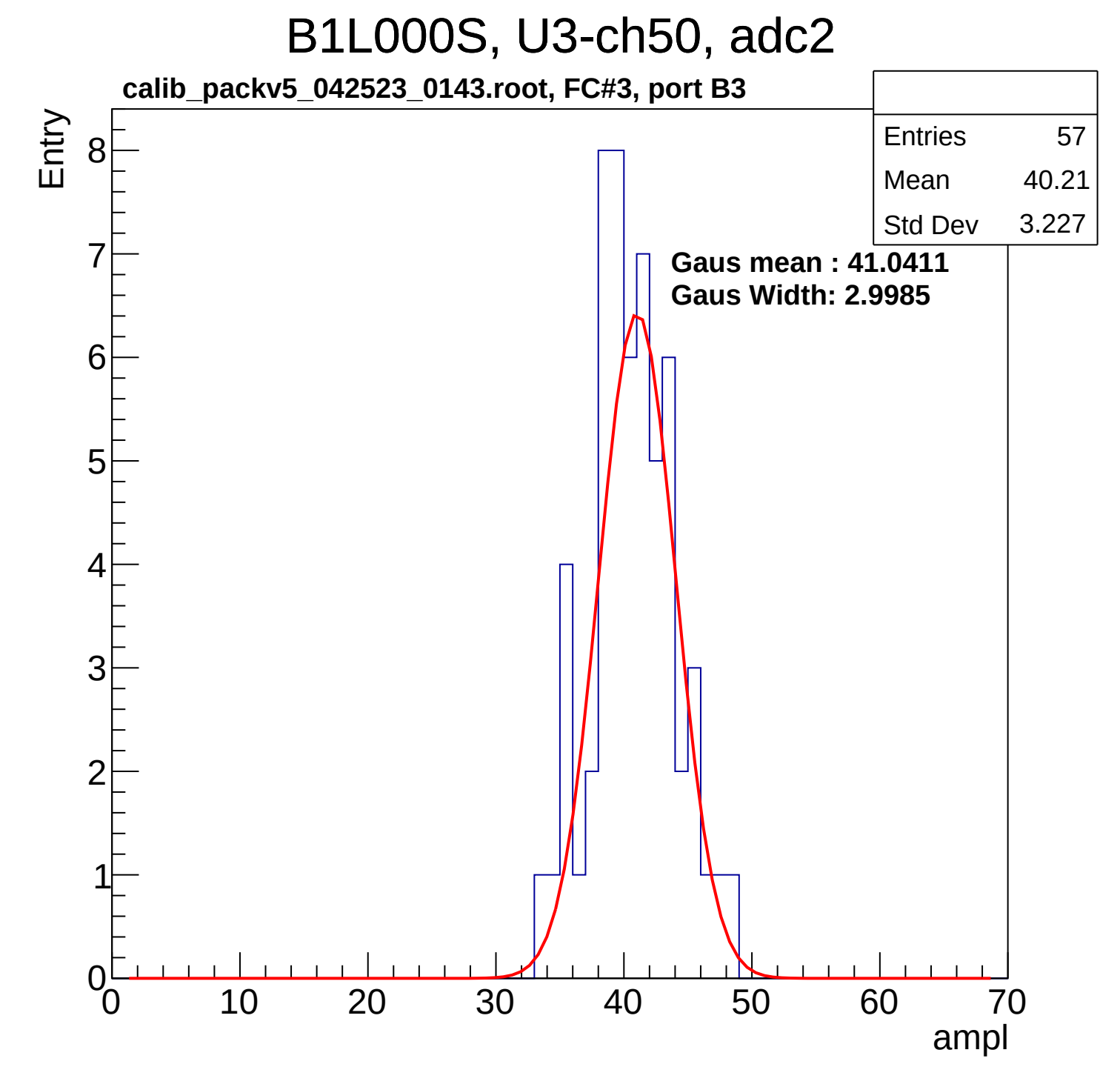

B1L000S, U3-ch50, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 34.3
Std Dev 3.21
Gaus mean : 34.1871
Gaus Width: 3.1711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch50, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 40.21
Std Dev 3.227
Gaus mean : 41.0411
Gaus Width: 2.9985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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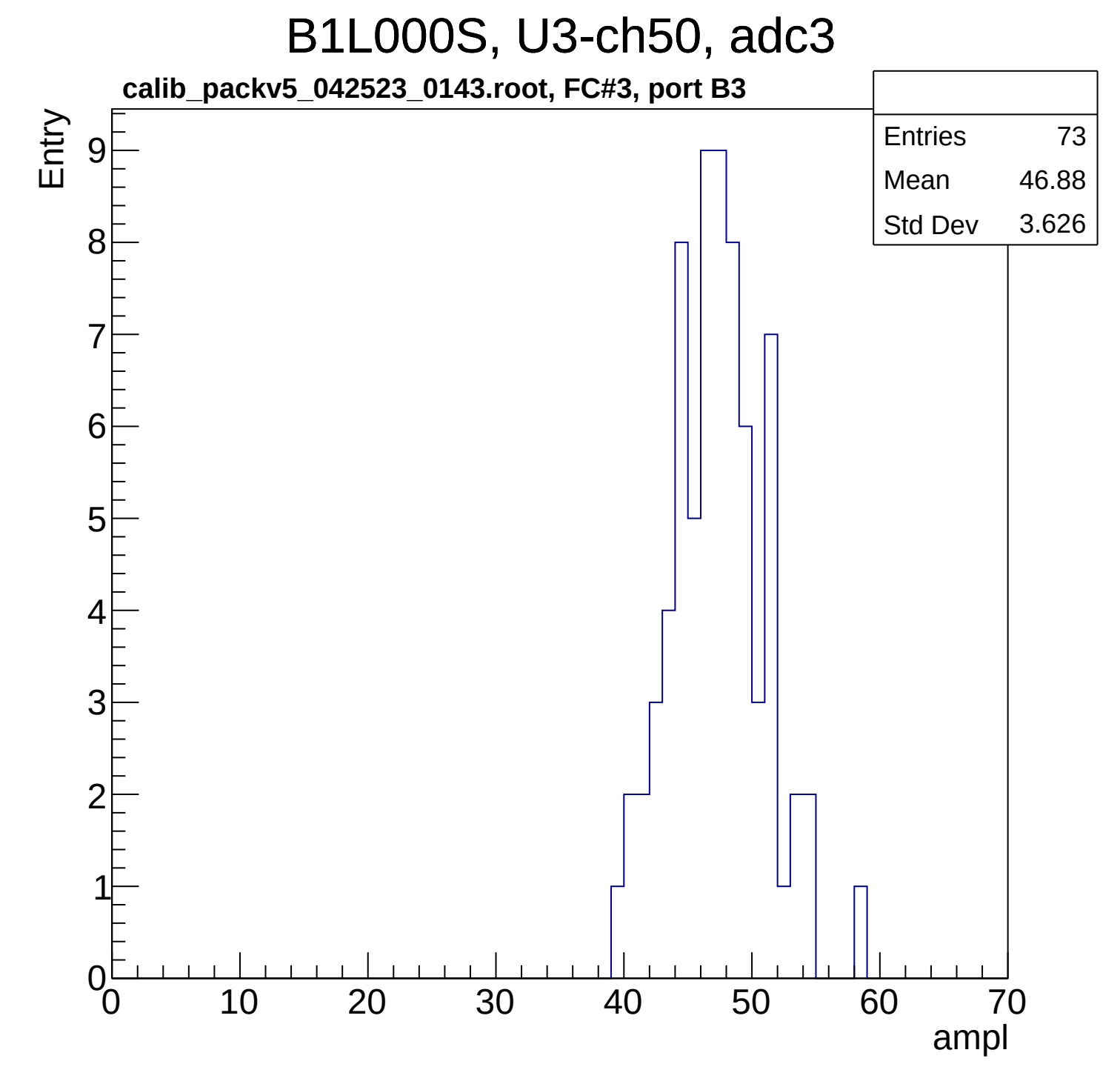
B1L000S, U3-ch50, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 46.88
Std Dev 3.626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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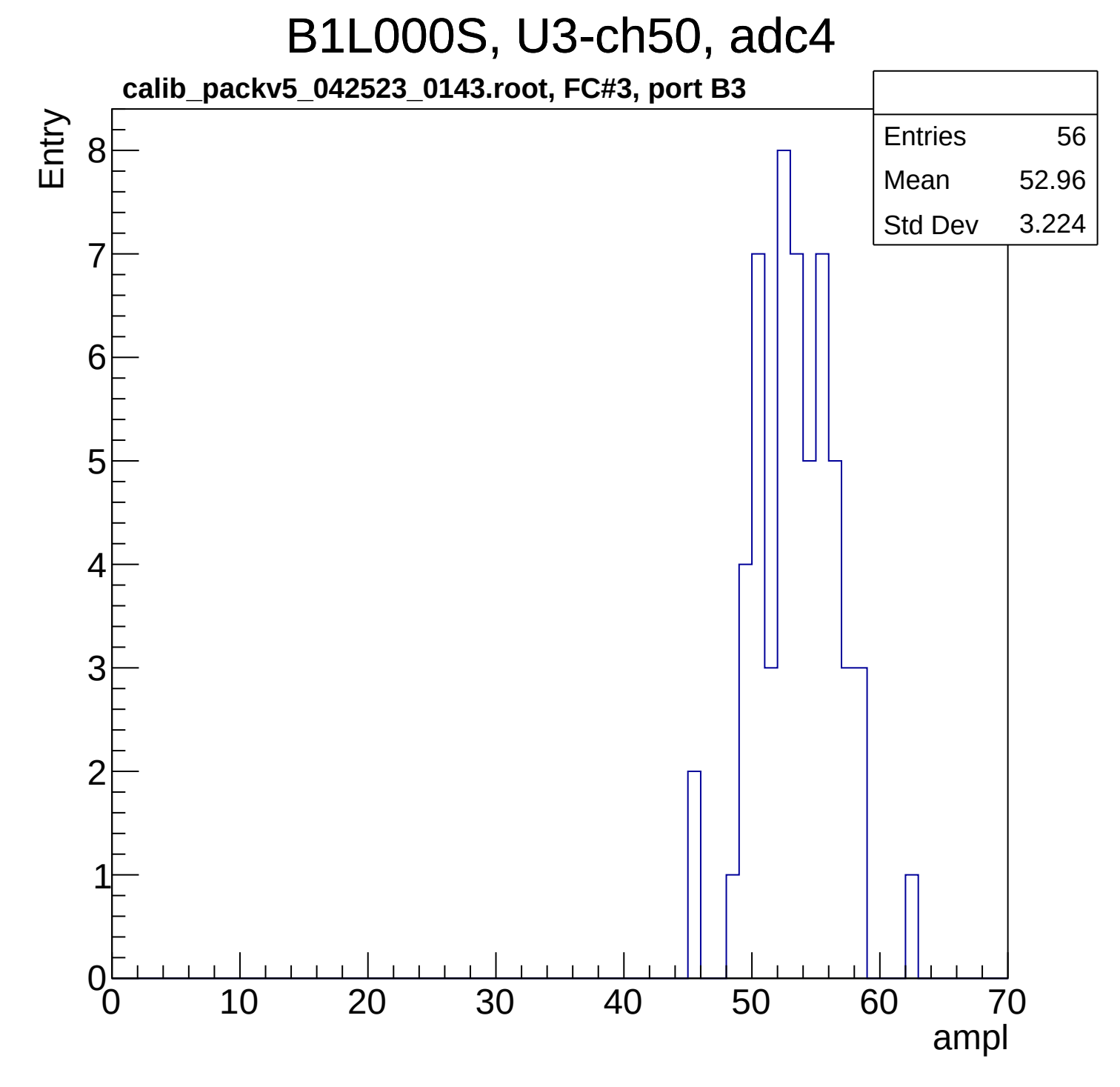
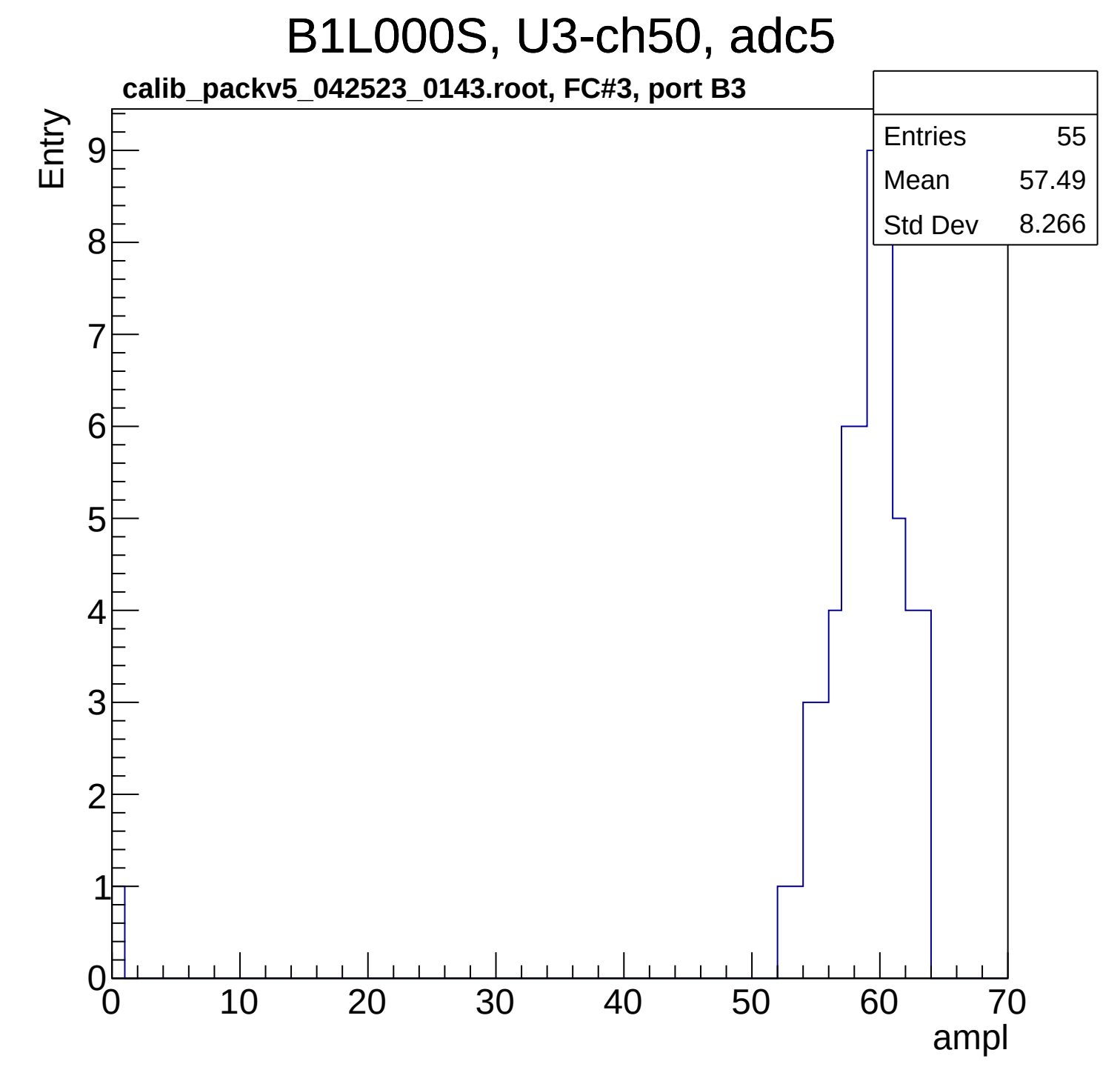

B1L000S, U3-ch50, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 52.96
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch50, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 57.49
Std Dev 8.266
Entry
ampl
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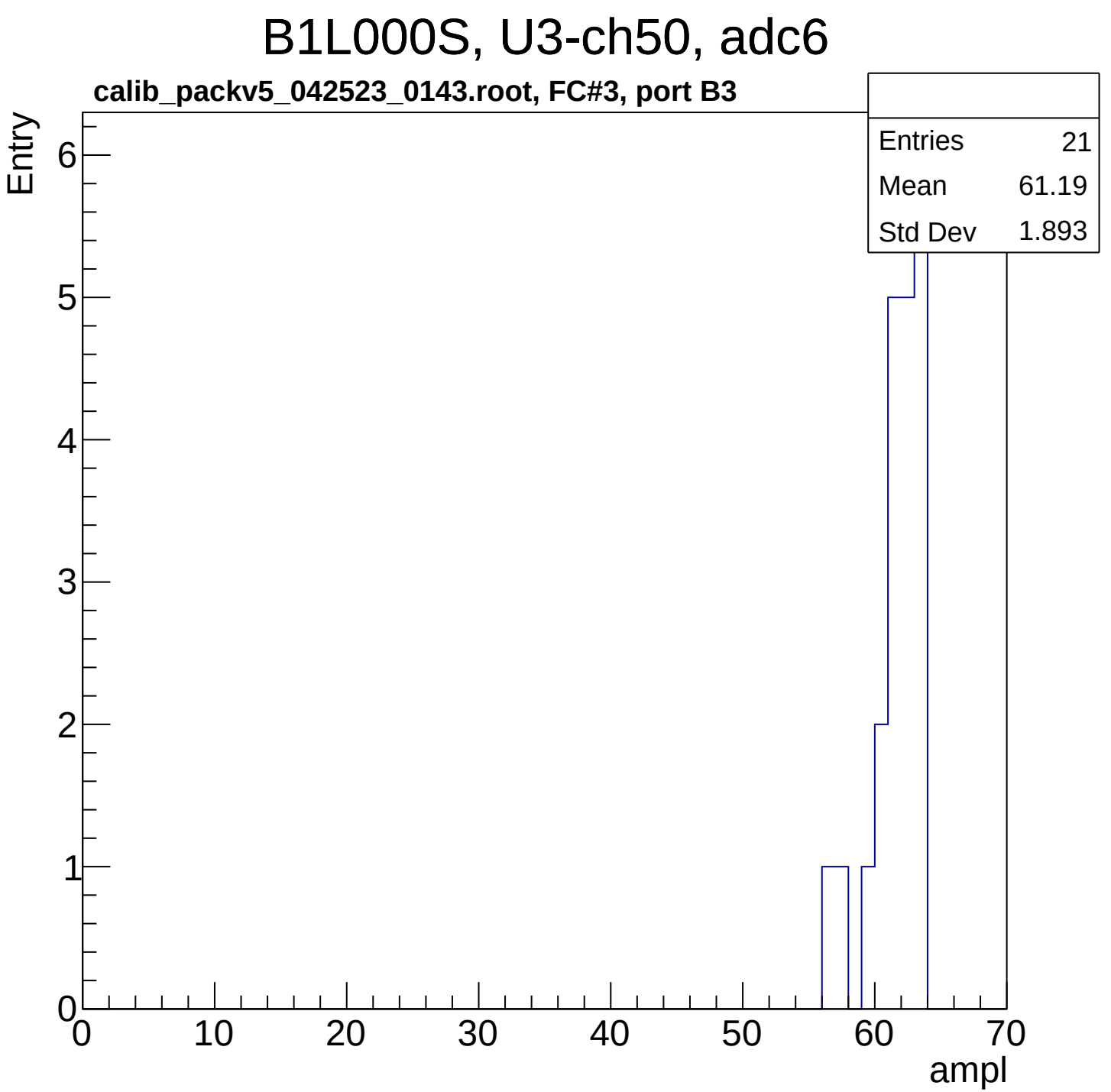
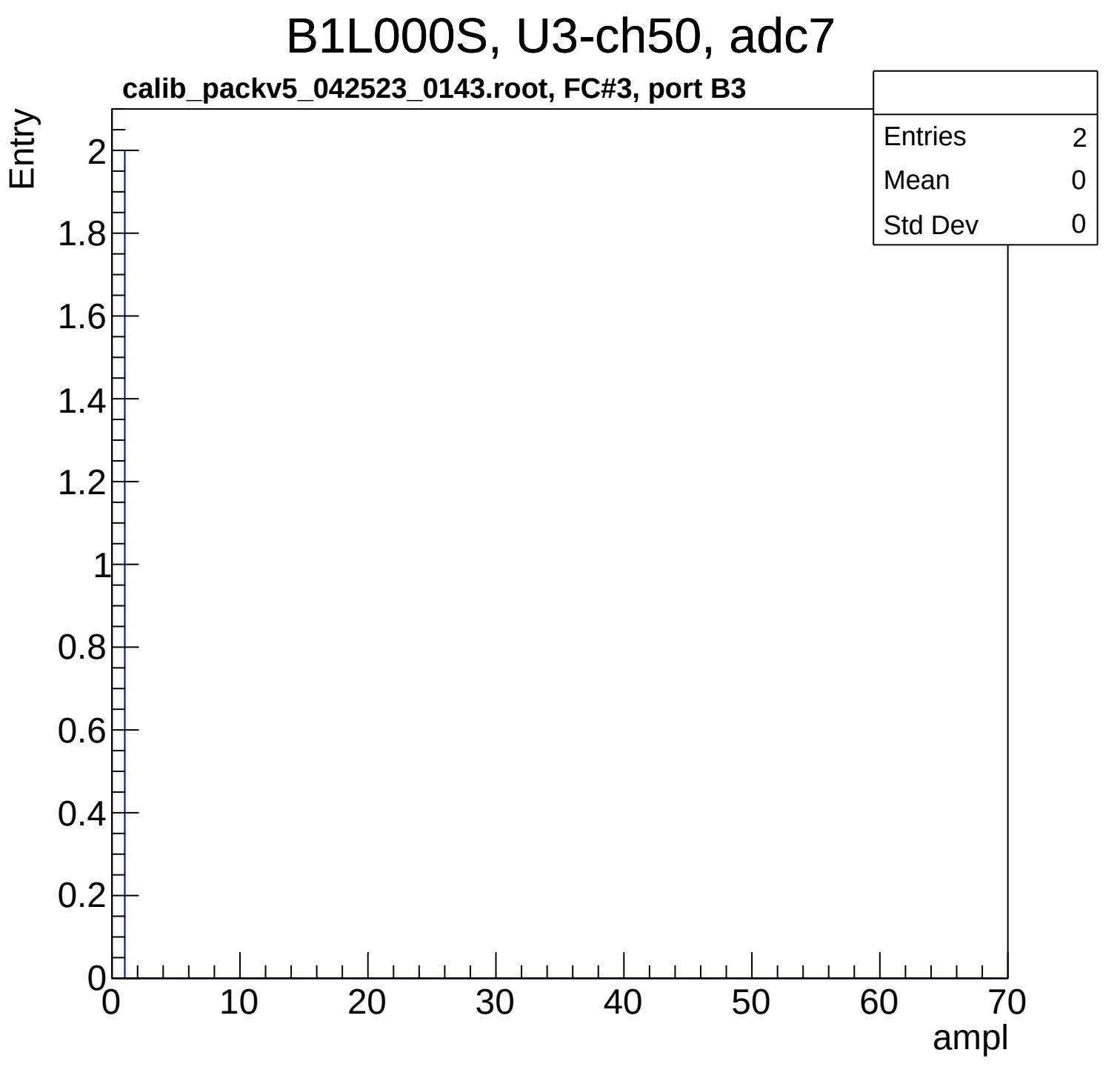
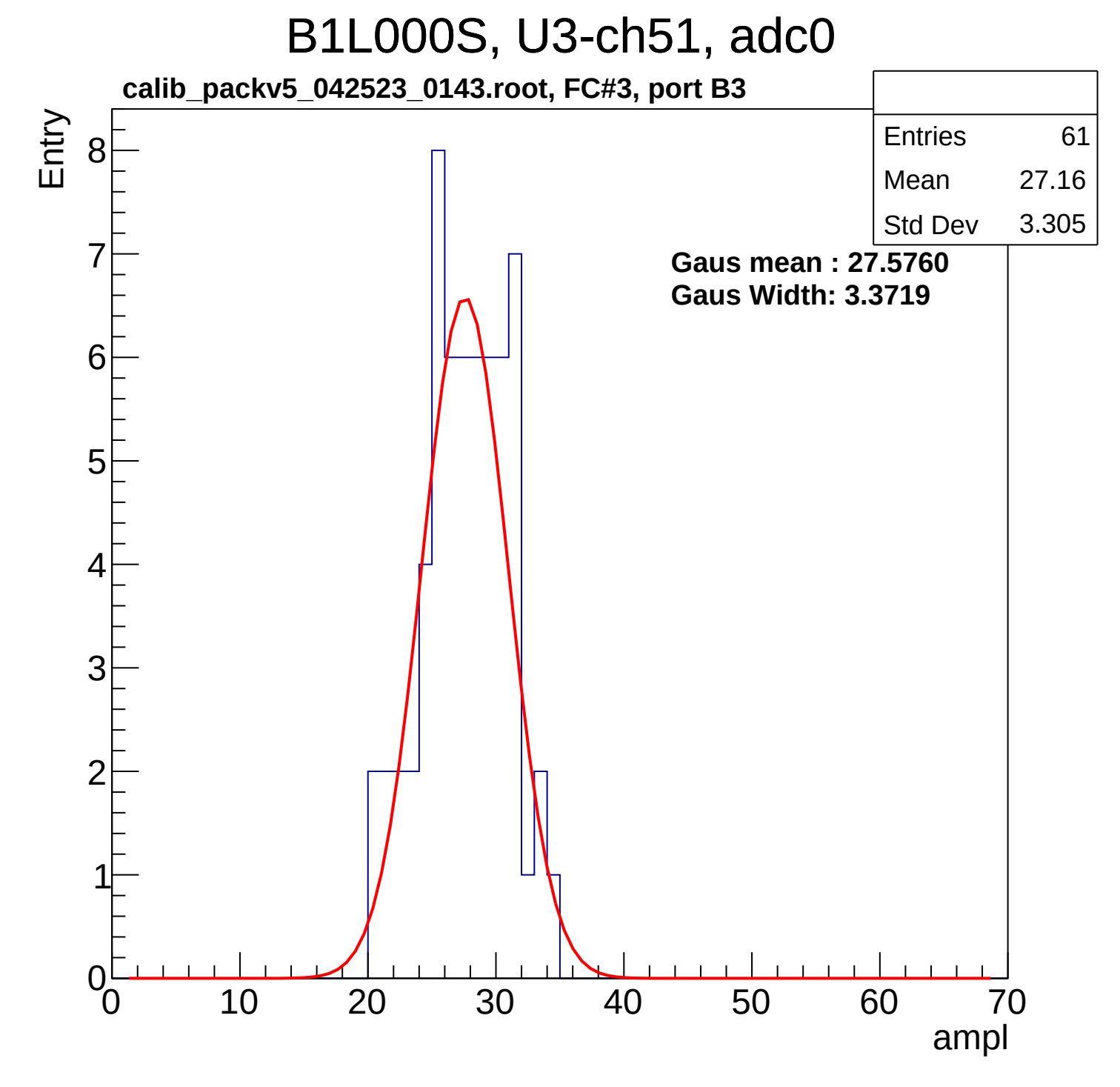
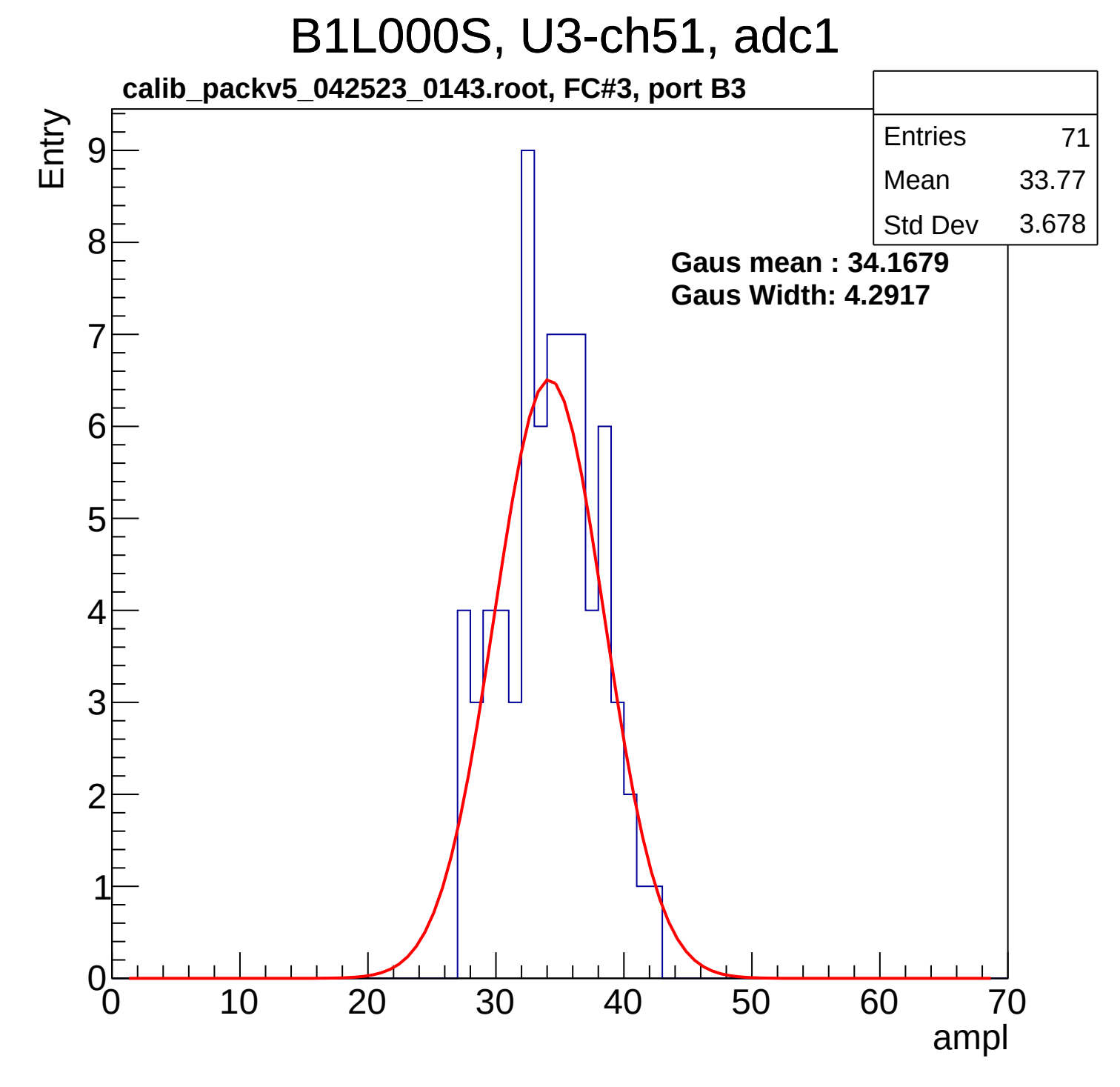
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch50, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 61.19
Std Dev 1.893
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch50, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch51, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 27.16
Std Dev 3.305
Gaus mean : 27.5760
Gaus Width: 3.3719
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch51, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 33.77
Std Dev 3.678
Gaus mean : 34.1679
Gaus Width: 4.2917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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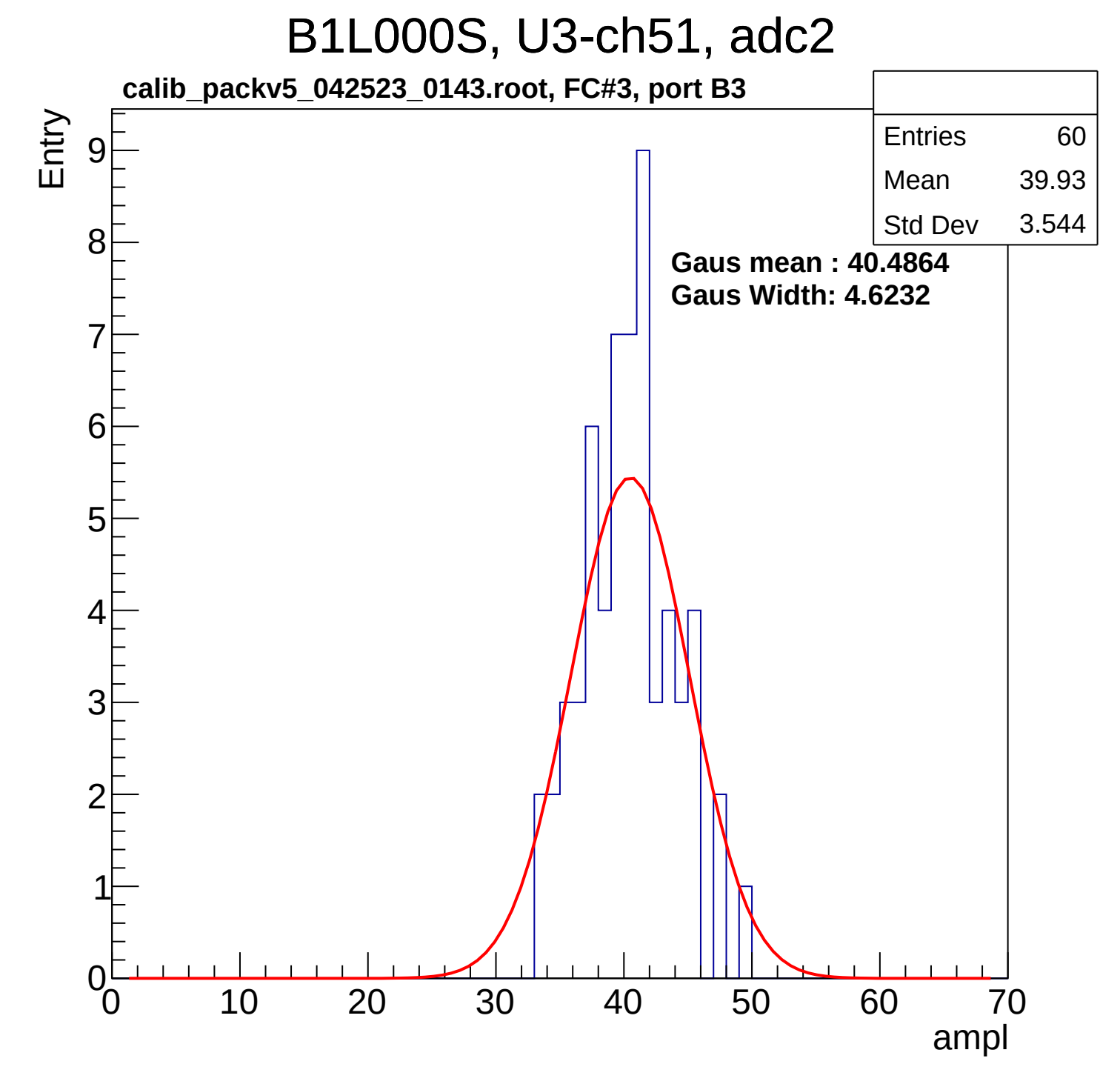
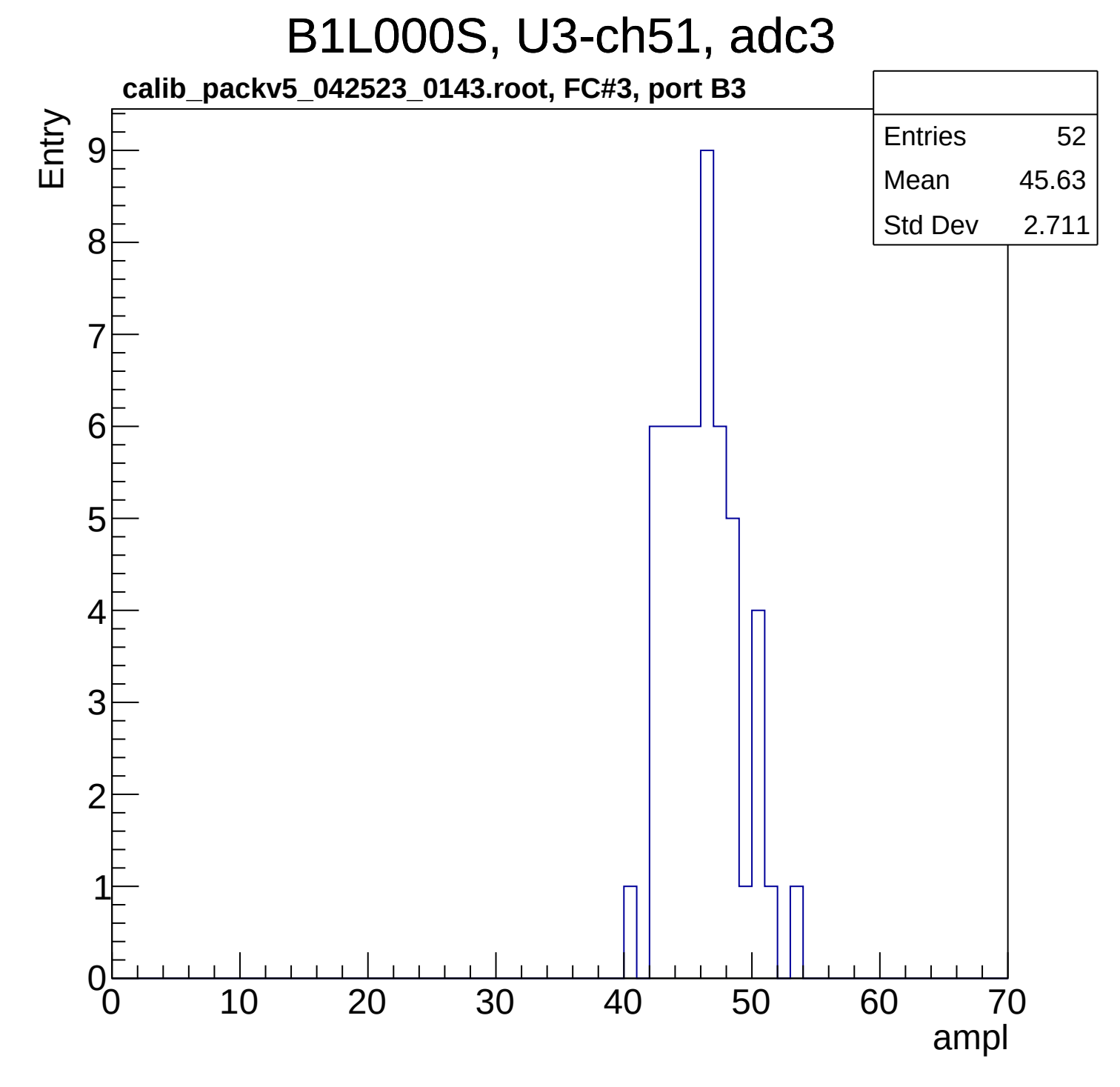
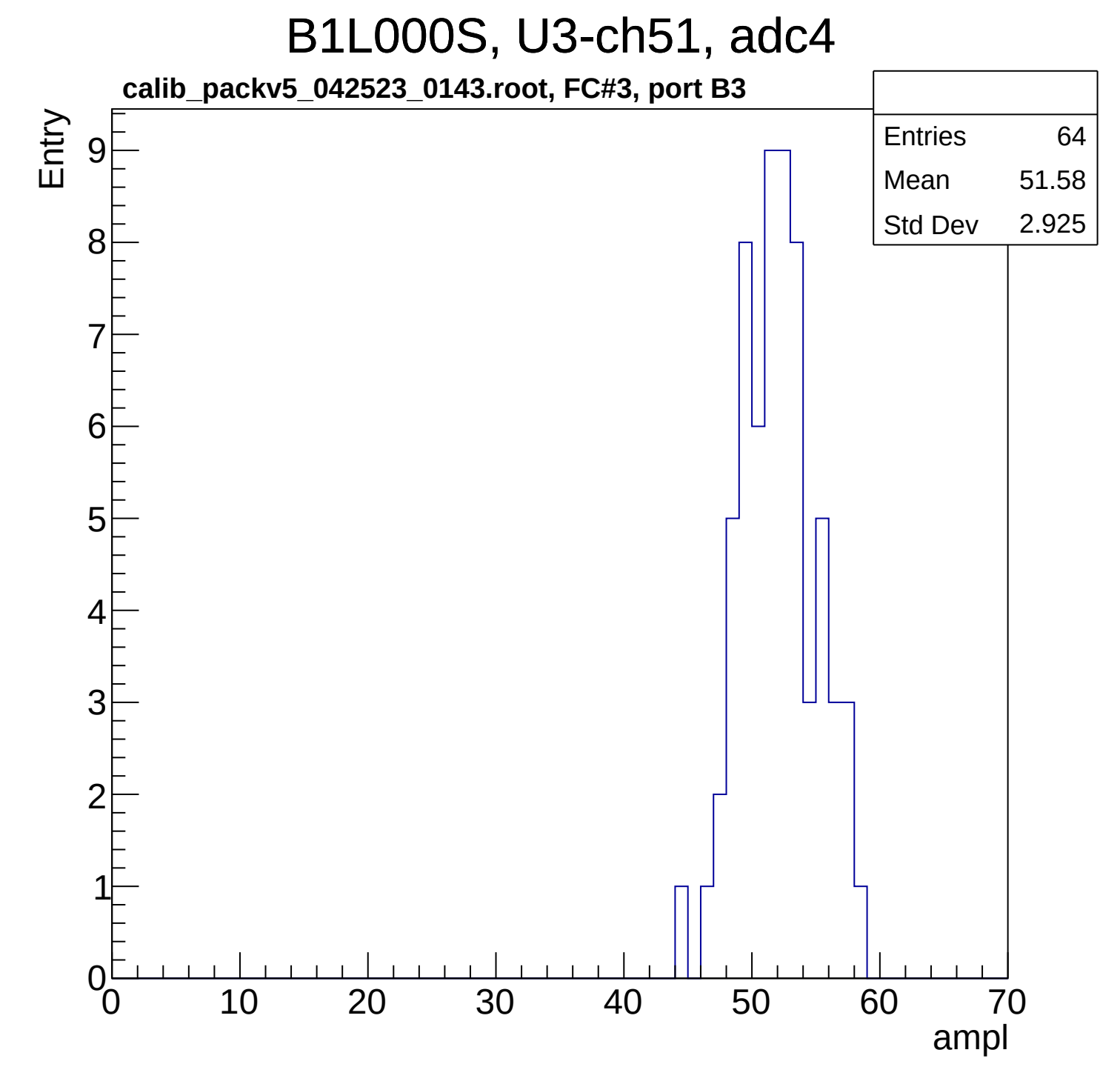
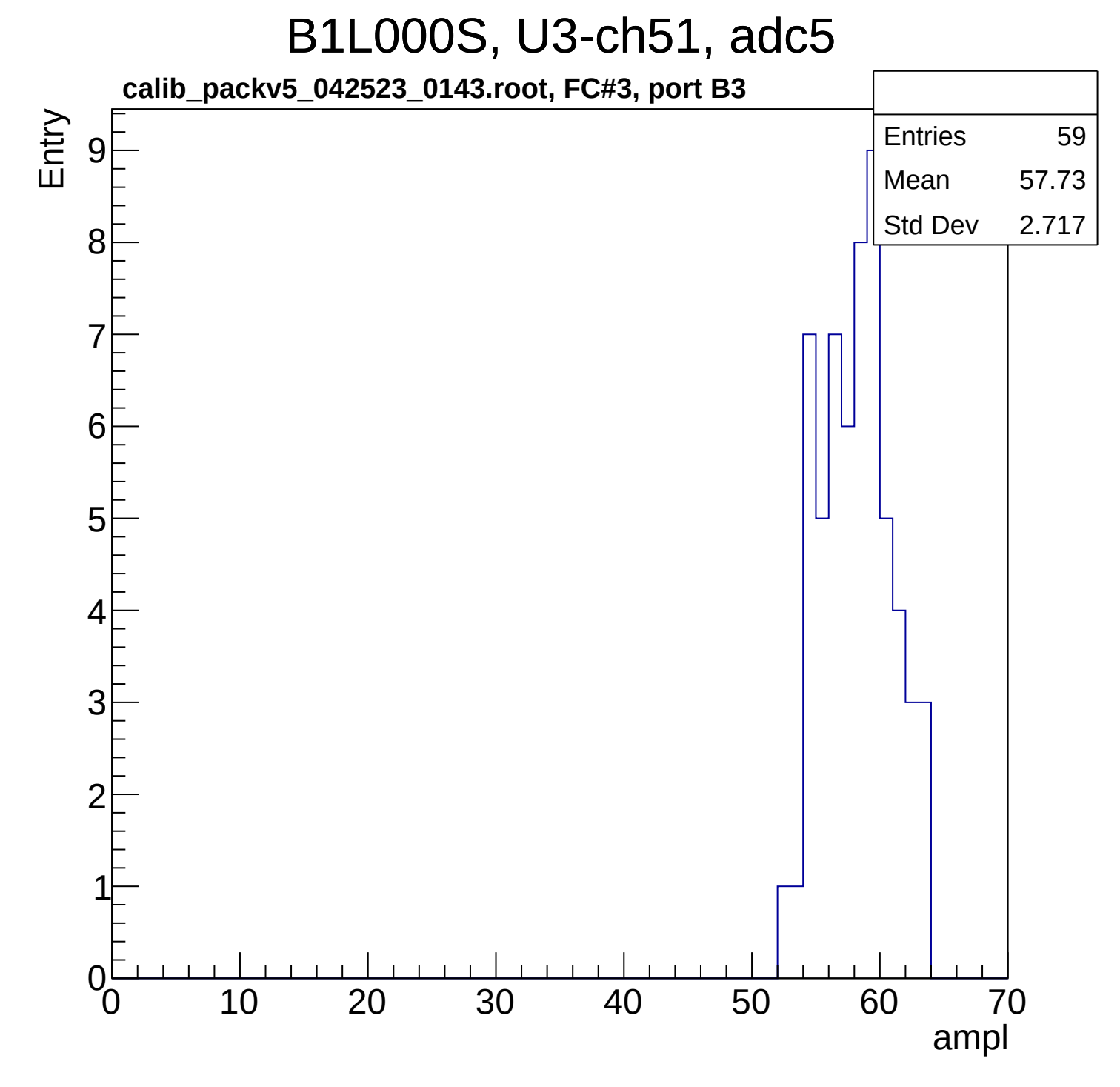
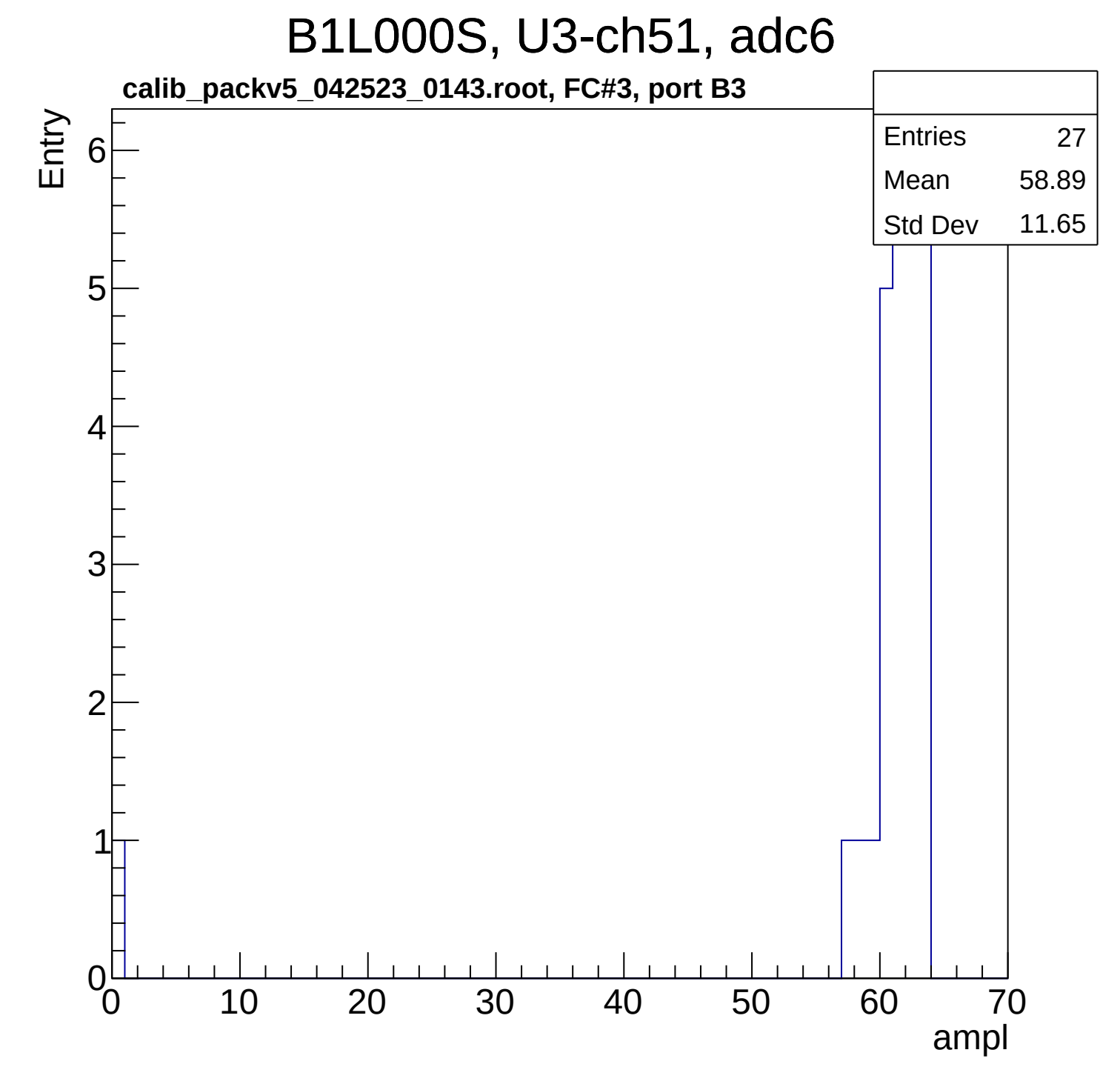
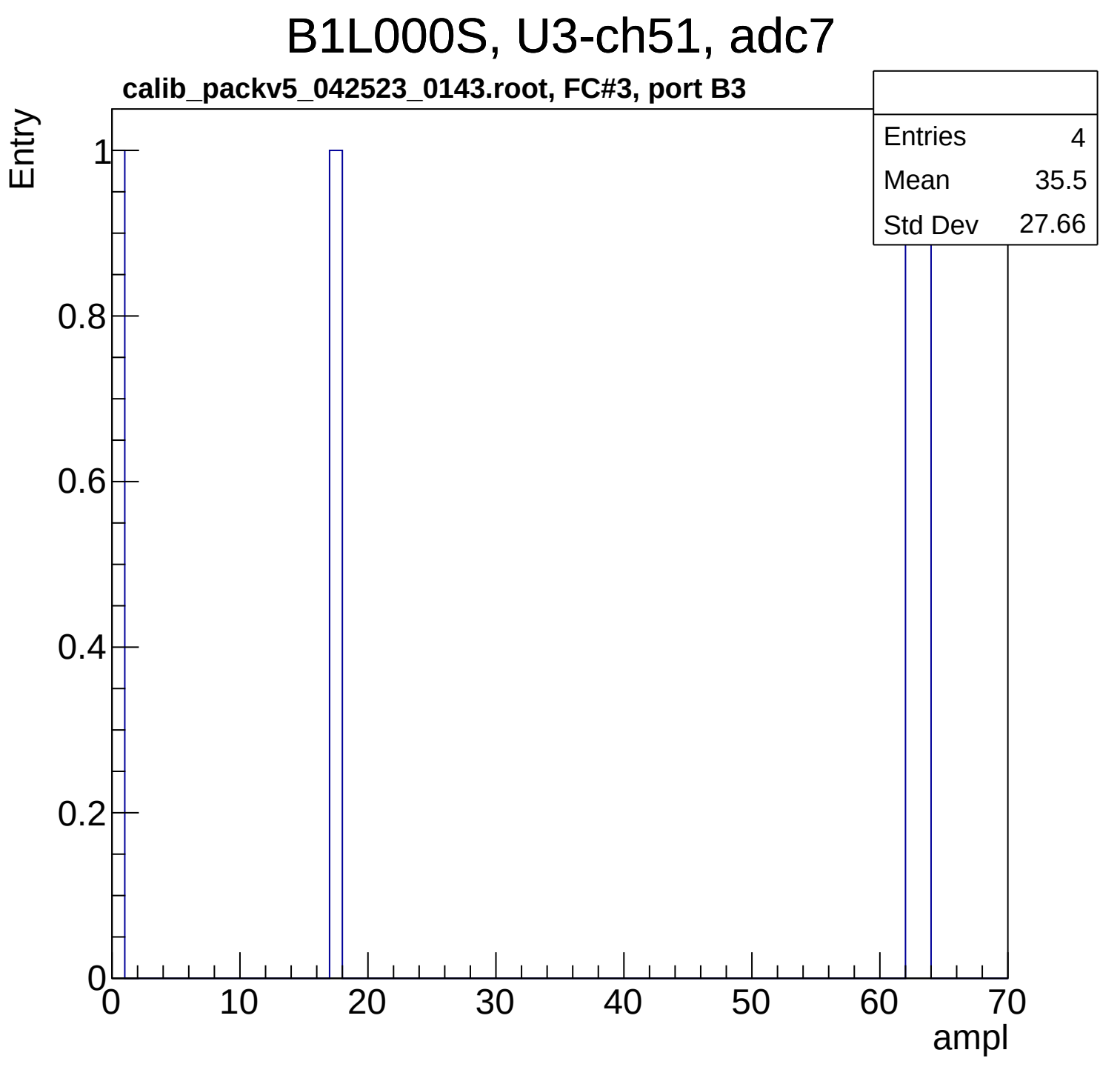

B1L000S, U3-ch51, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 39.93
Std Dev 3.544
Gaus mean : 40.4864
Gaus Width: 4.6232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

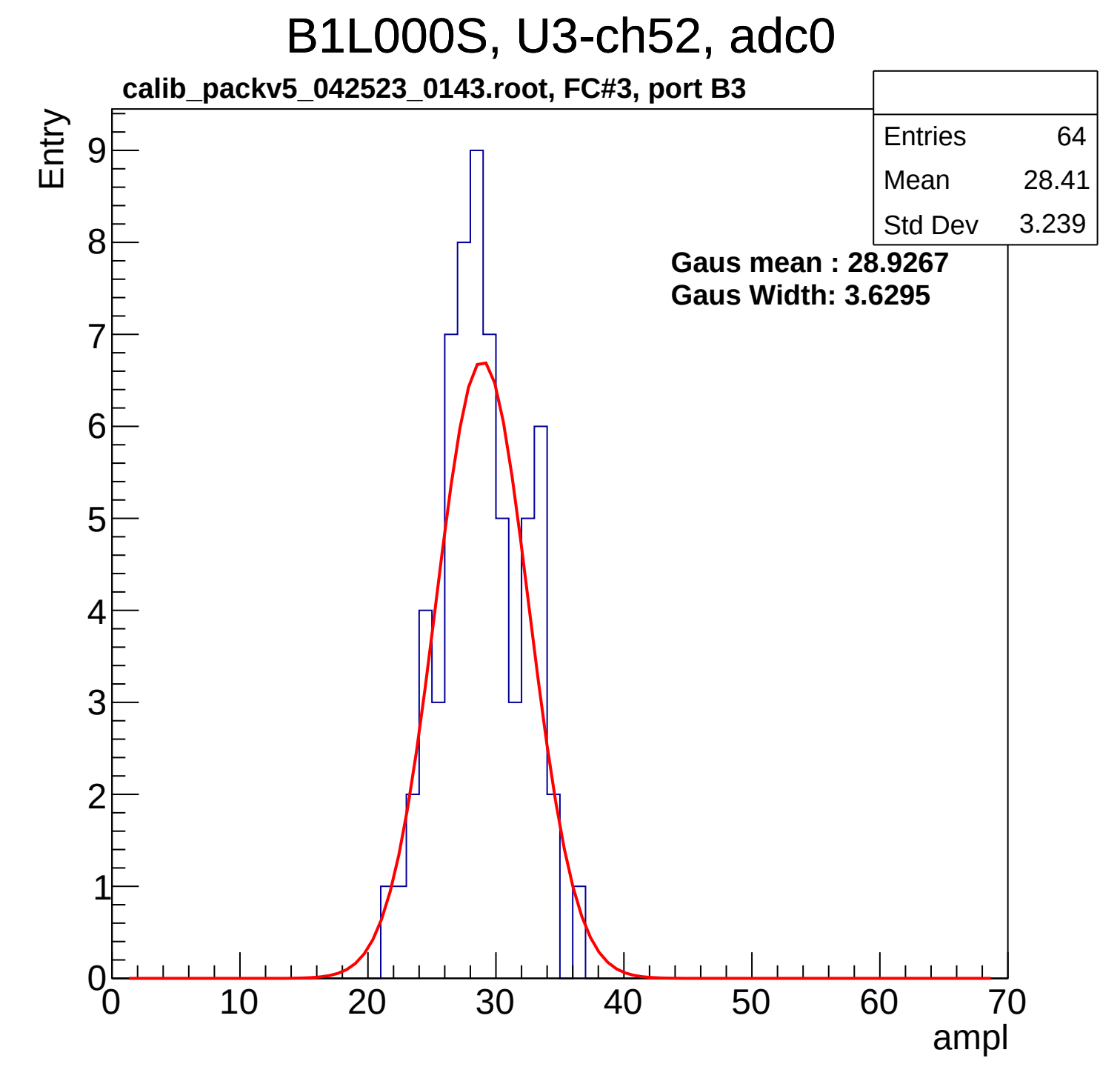

B1L000S, U3-ch51, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 45.63
Std Dev 2.711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

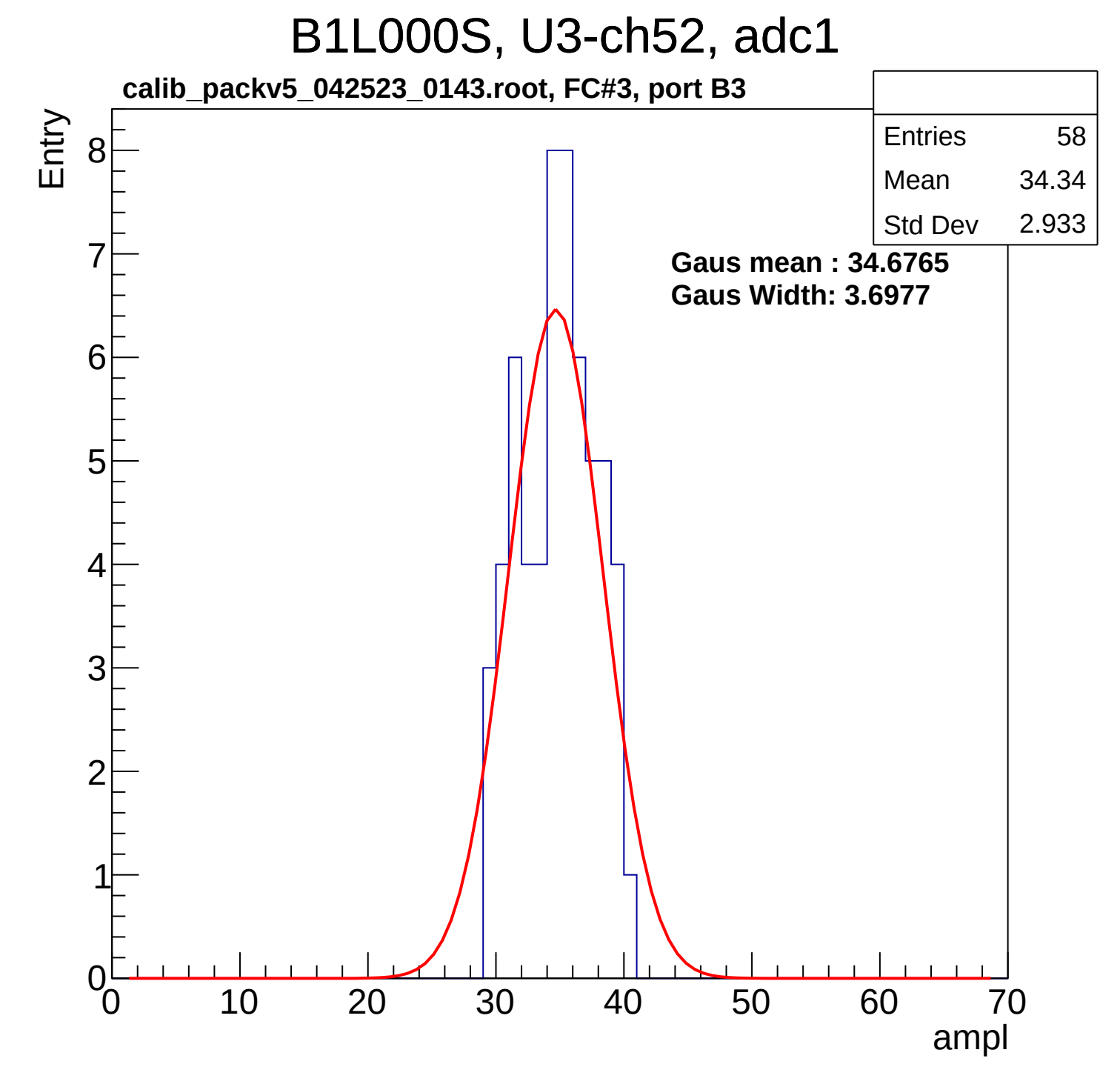

B1L000S, U3-ch51, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 51.58
Std Dev 2.925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

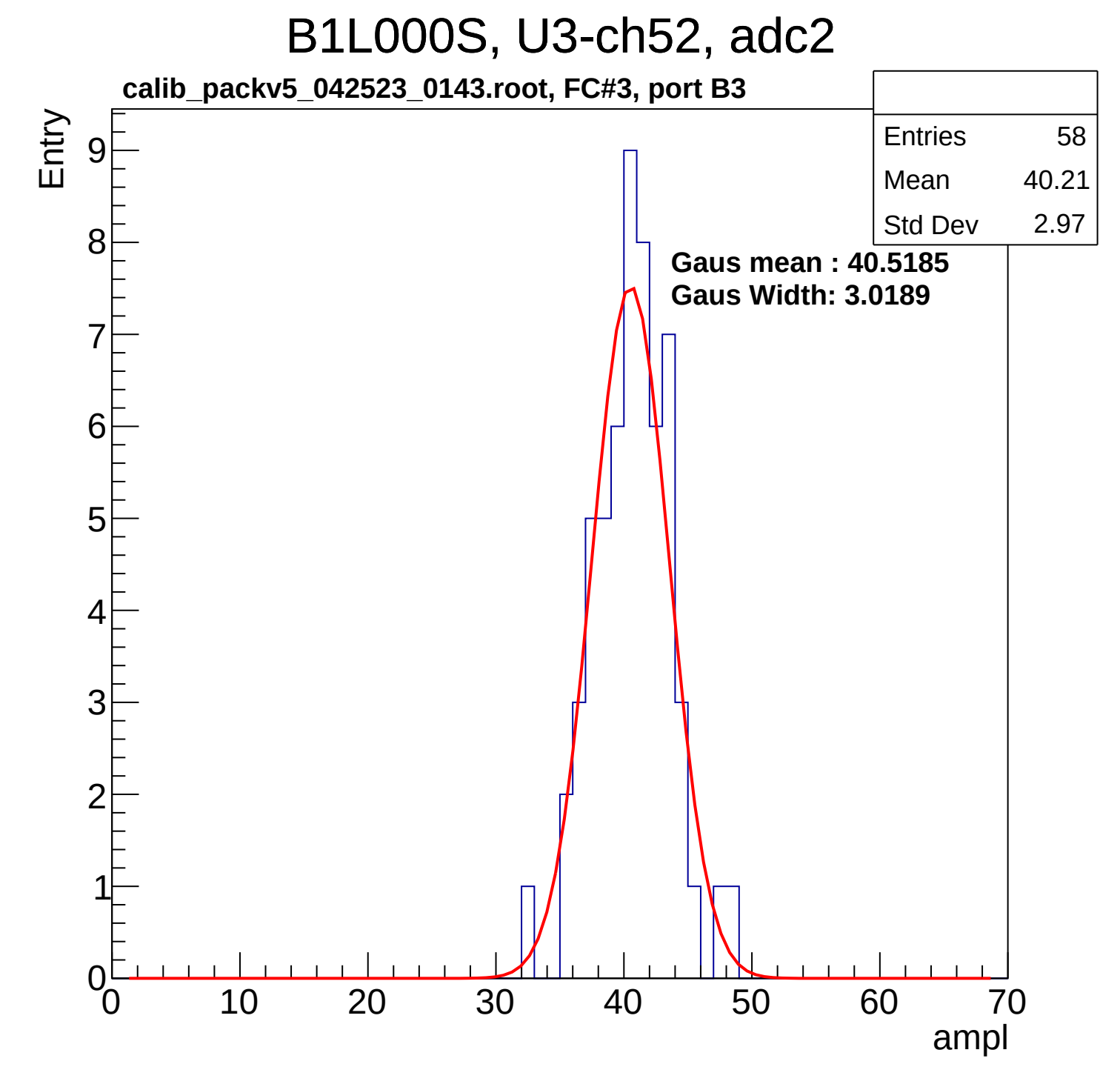

B1L000S, U3-ch51, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 57.73
Std Dev 2.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

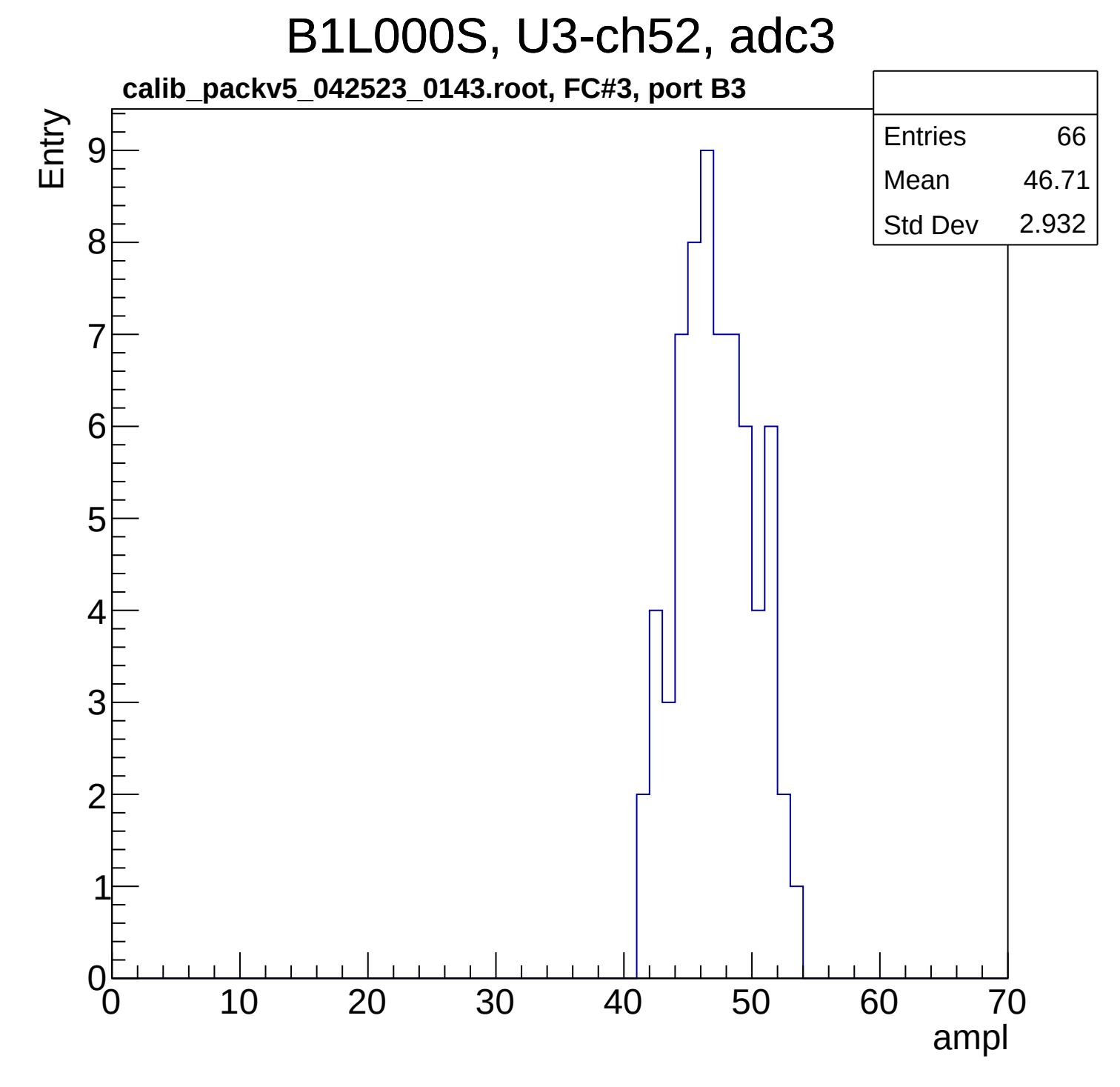

B1L000S, U3-ch51, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 27
Mean 58.89
Std Dev 11.65
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch51, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 35.5
Std Dev 27.66
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U3-ch52, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.41
Std Dev 3.239
Gaus mean : 28.9267
Gaus Width: 3.6295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch52, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 34.34
Std Dev 2.933
Gaus mean : 34.6765
Gaus Width: 3.6977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch52, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 40.21
Std Dev 2.97
Gaus mean : 40.5185
Gaus Width: 3.0189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch52, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 46.71
Std Dev 2.932
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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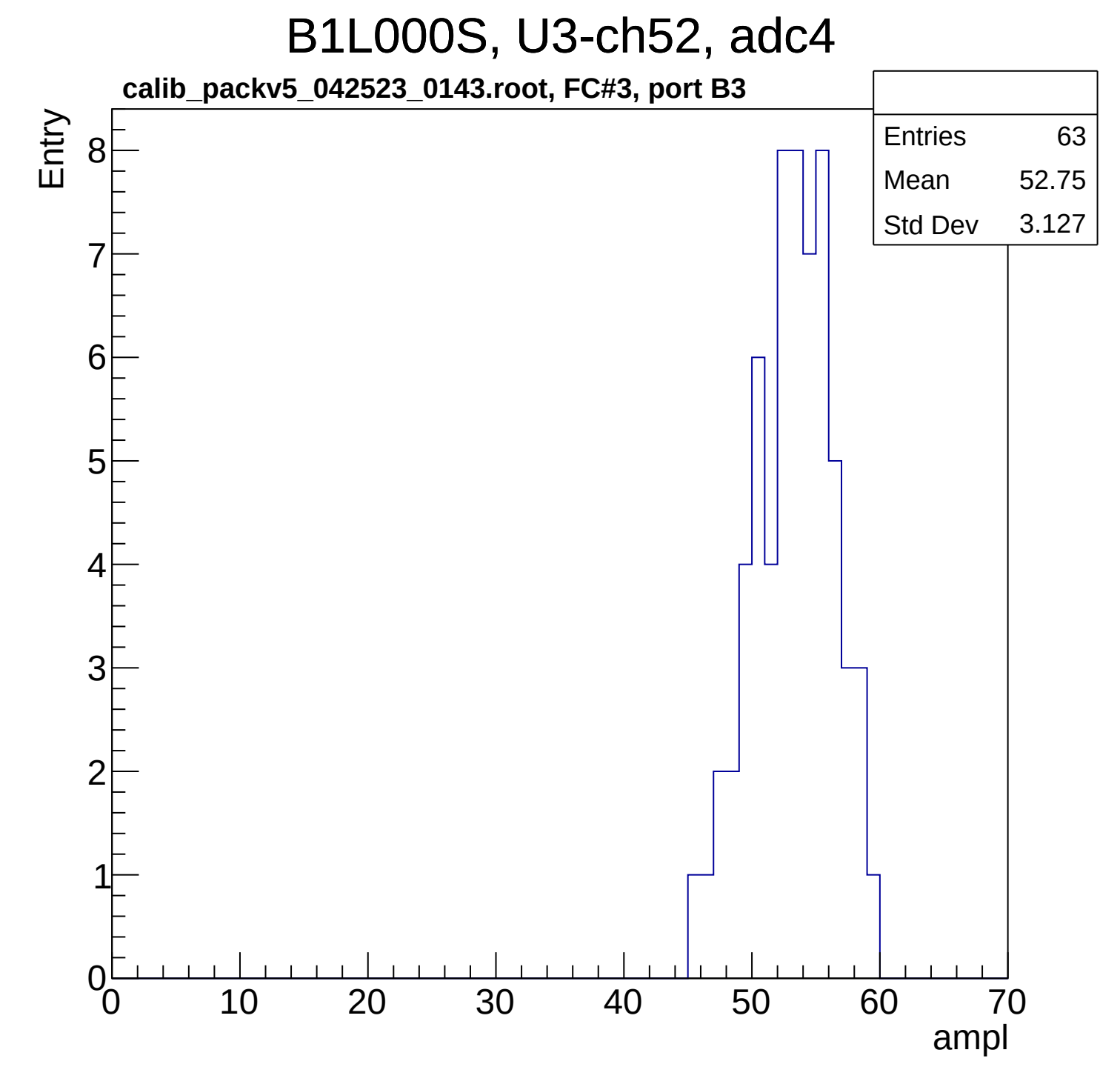
B1L000S, U3-ch52, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 52.75
Std Dev 3.127
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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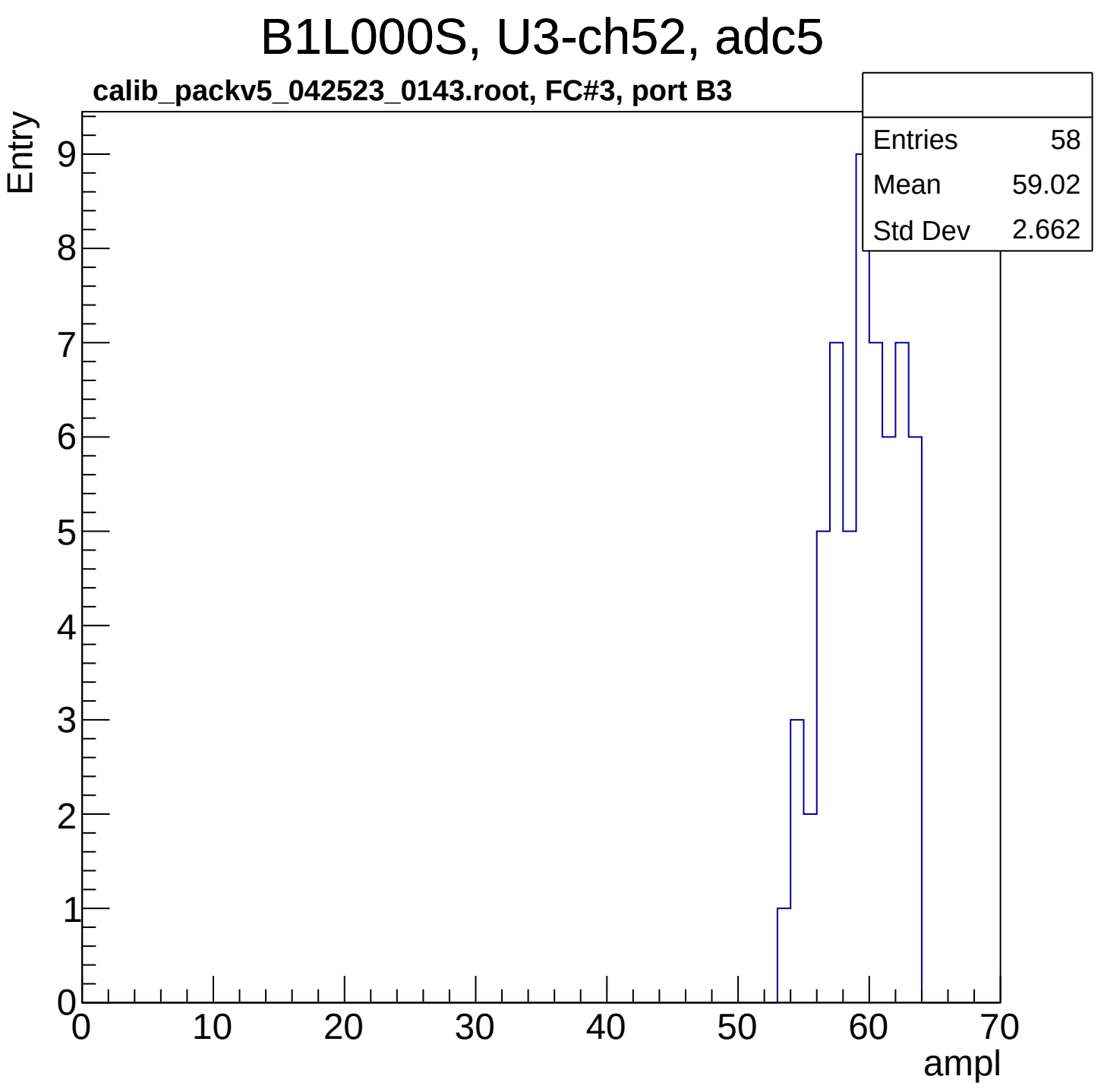
B1L000S, U3-ch52, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 59.02
Std Dev 2.662
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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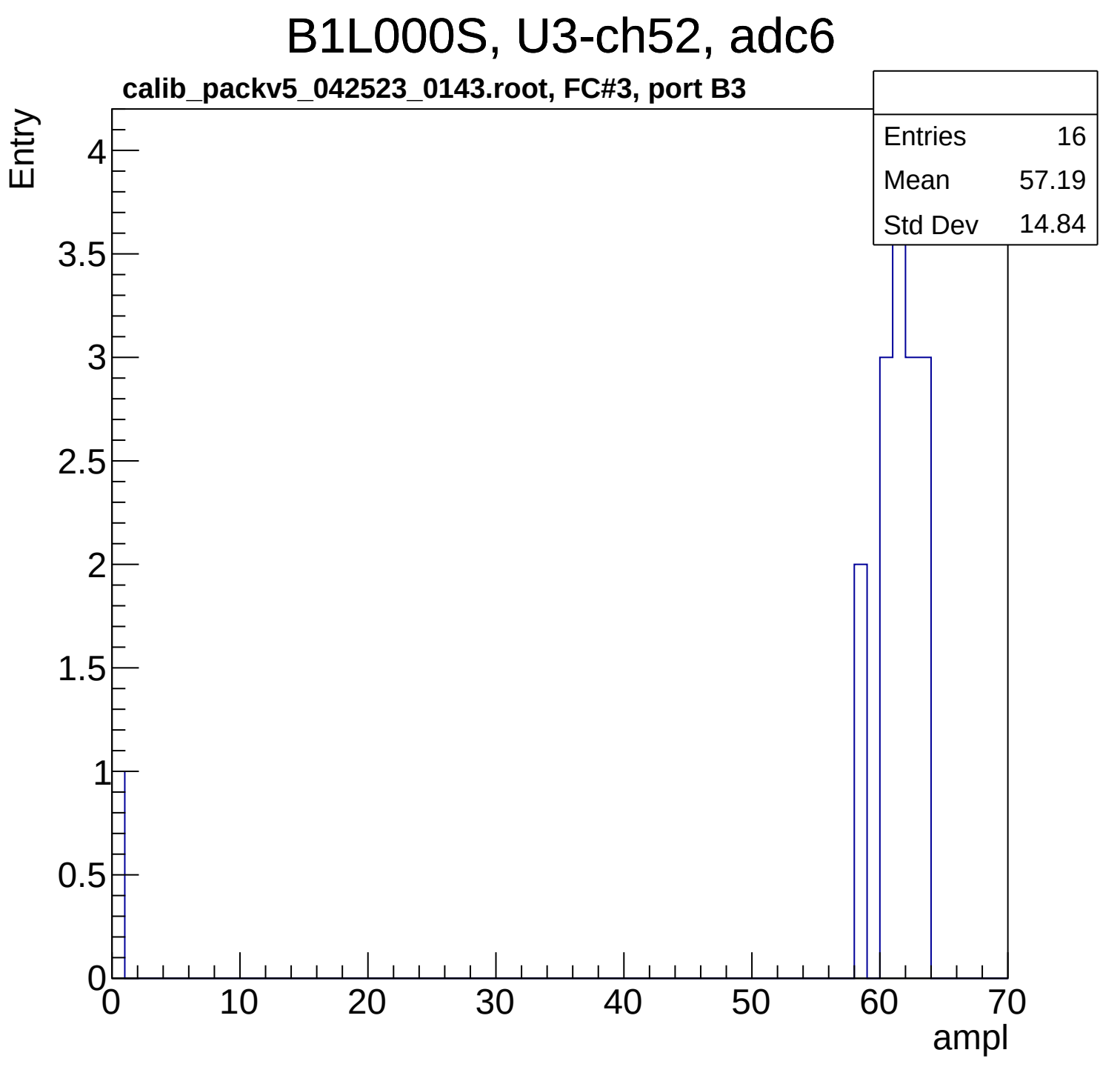
B1L000S, U3-ch52, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.19
Std Dev 14.84
Entry
ampl
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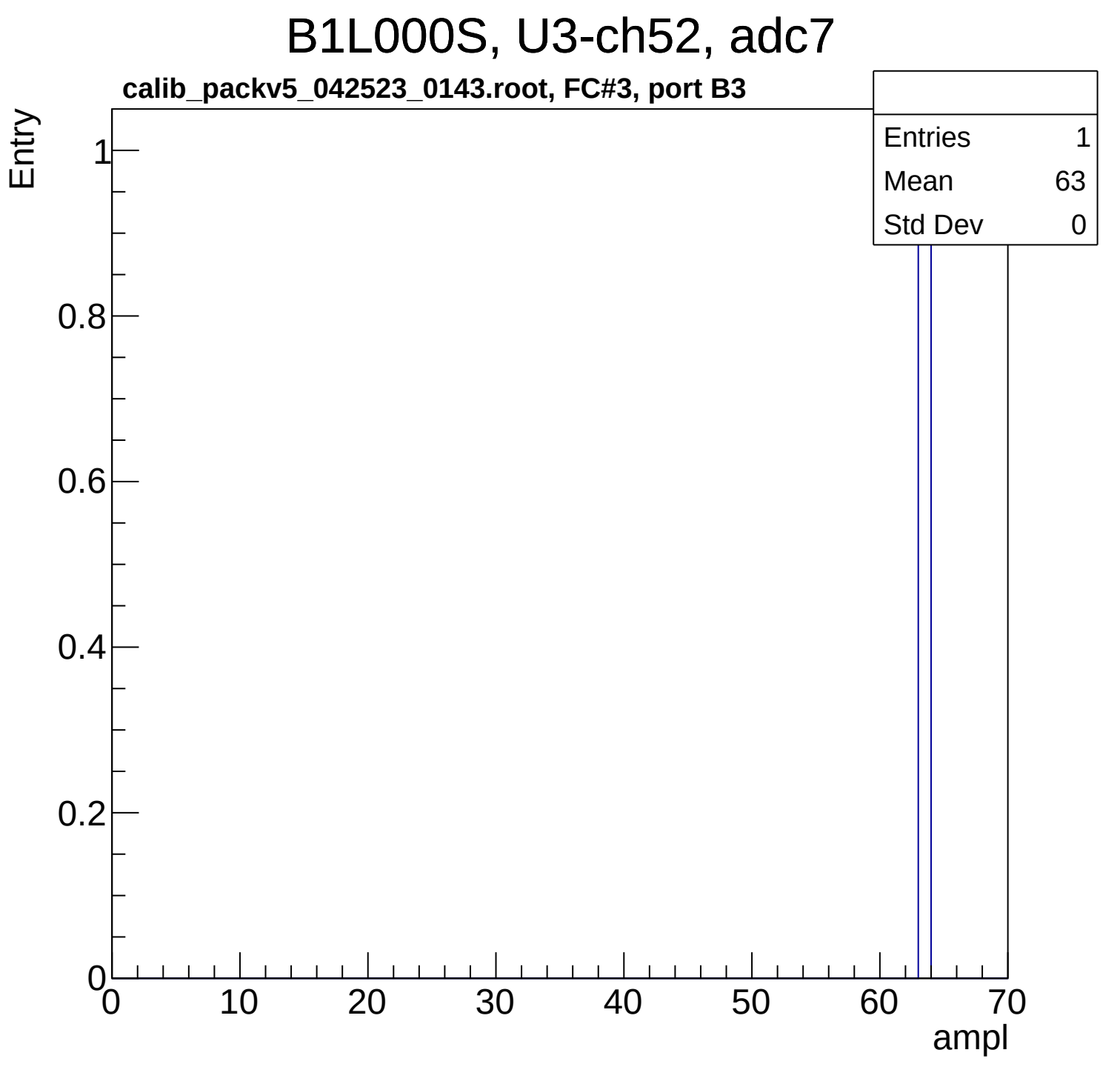
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch52, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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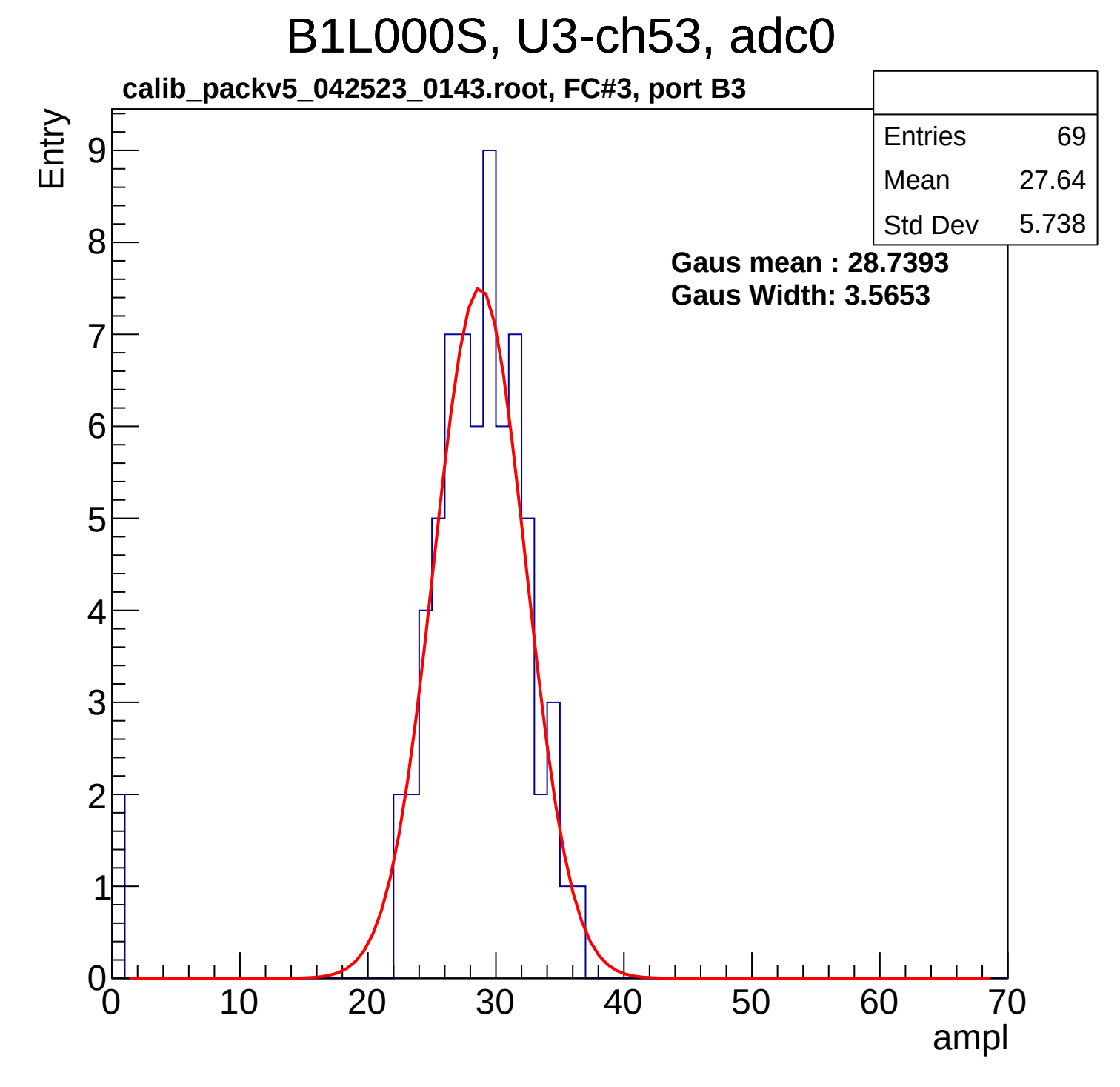
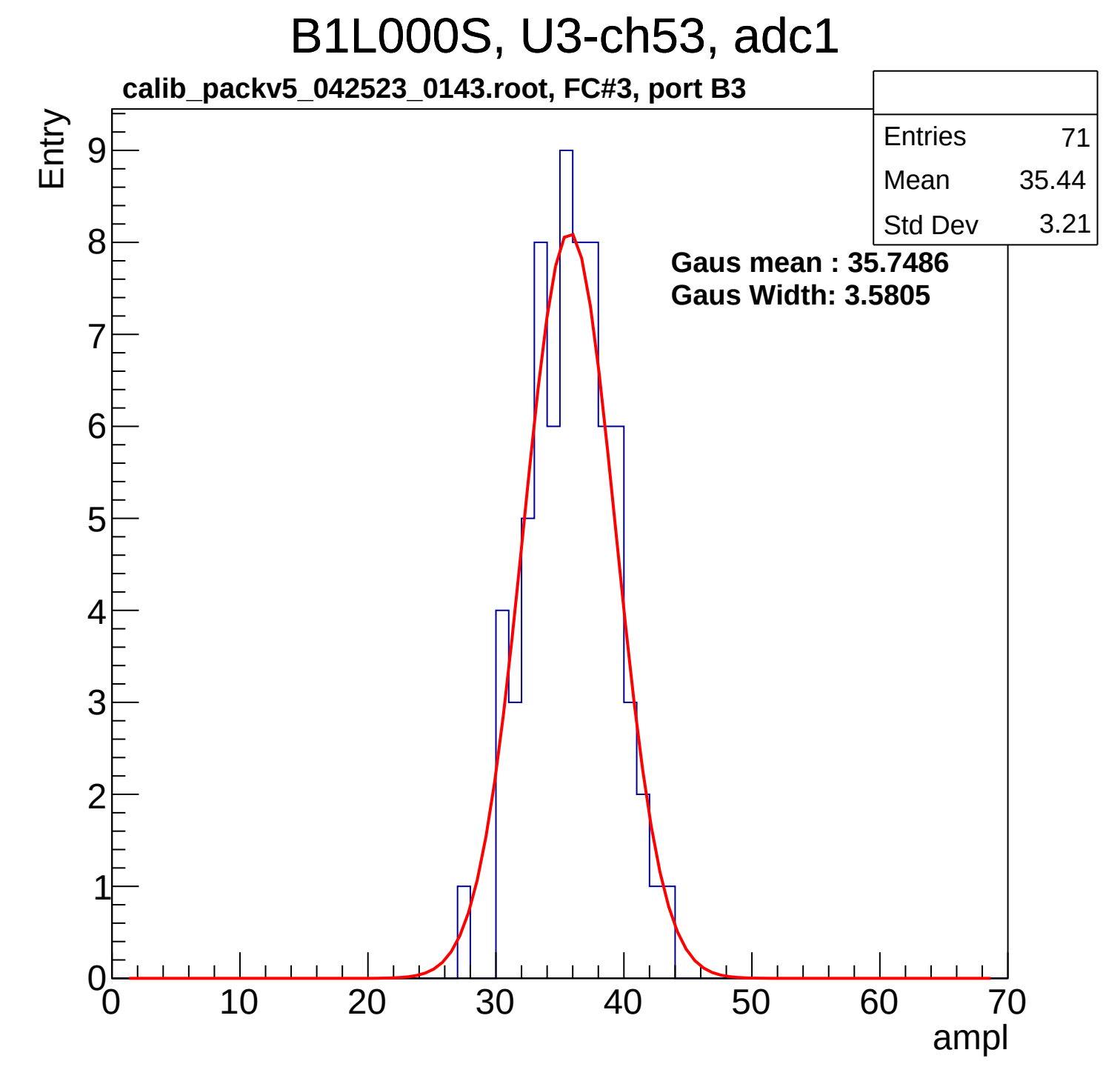
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch53, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 27.64
Std Dev 5.738
Gaus mean : 28.7393
Gaus Width: 3.5653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch53, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.44
Std Dev 3.21
Gaus mean : 35.7486
Gaus Width: 3.5805
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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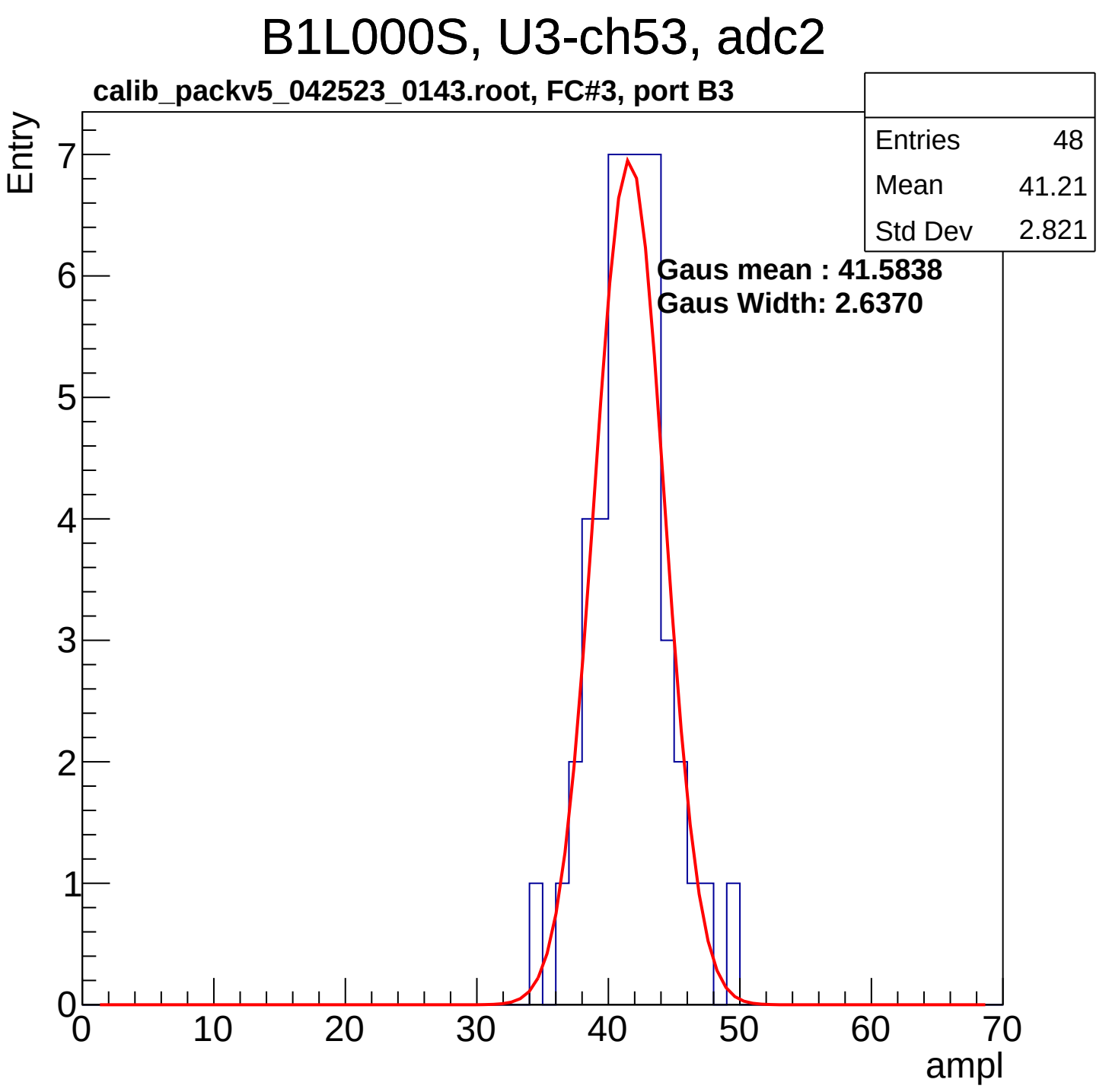
B1L000S, U3-ch53, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 41.21
Std Dev 2.821
Gaus mean : 41.5838
Gaus Width: 2.6370
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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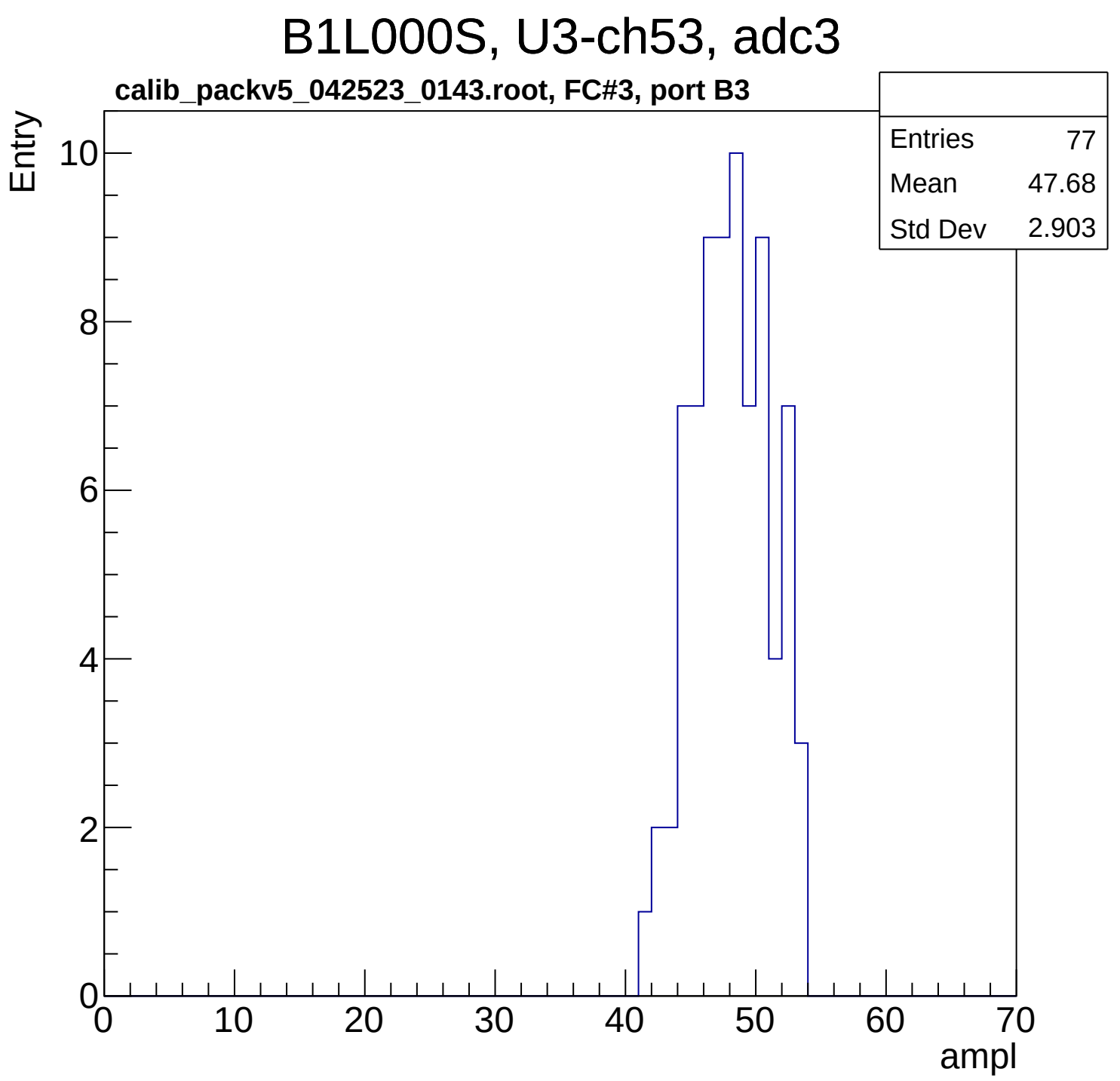
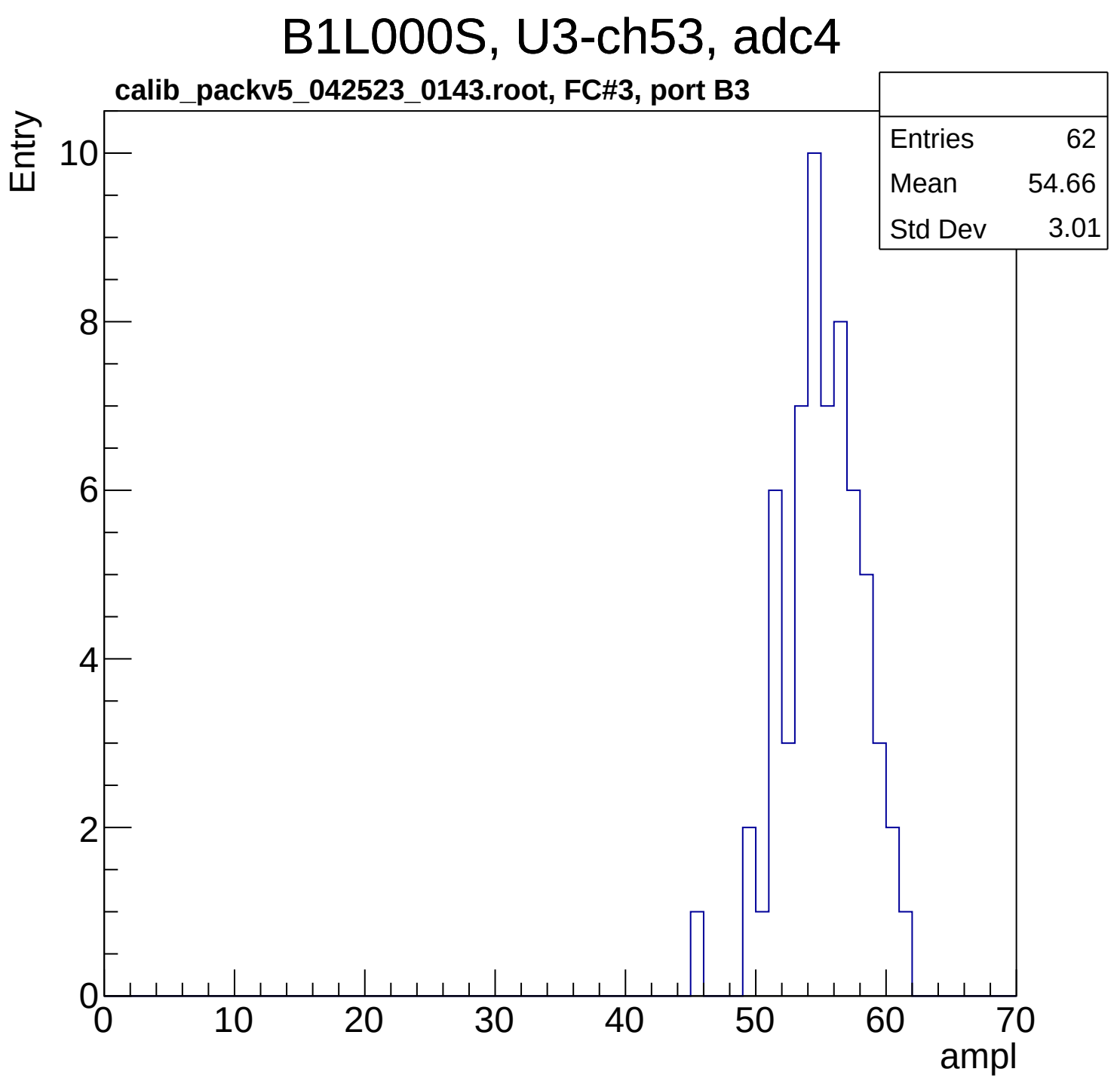
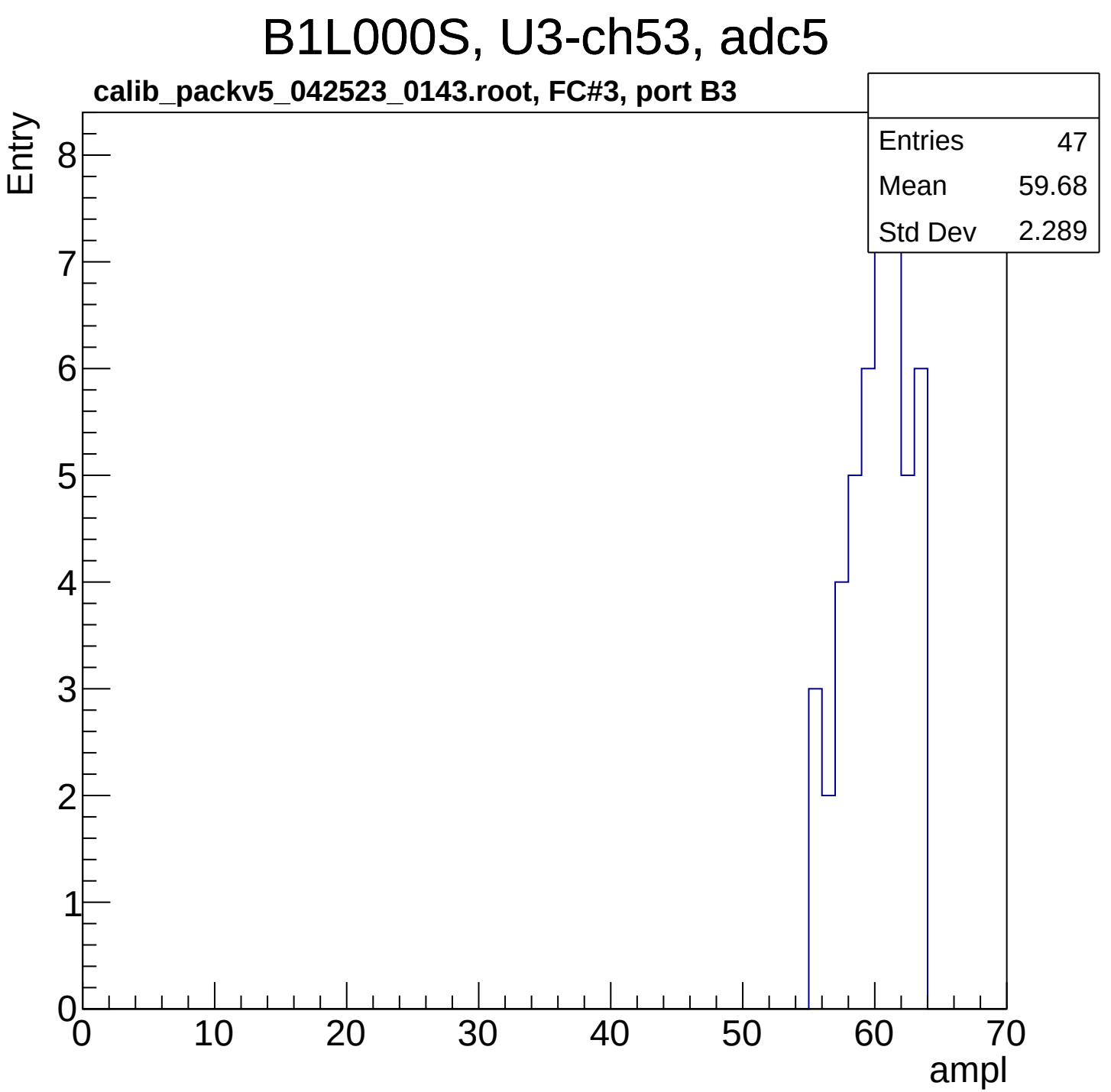

B1L000S, U3-ch53, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 47.68
Std Dev 2.903
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch53, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 54.66
Std Dev 3.01
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch53, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 59.68
Std Dev 2.289
Entry
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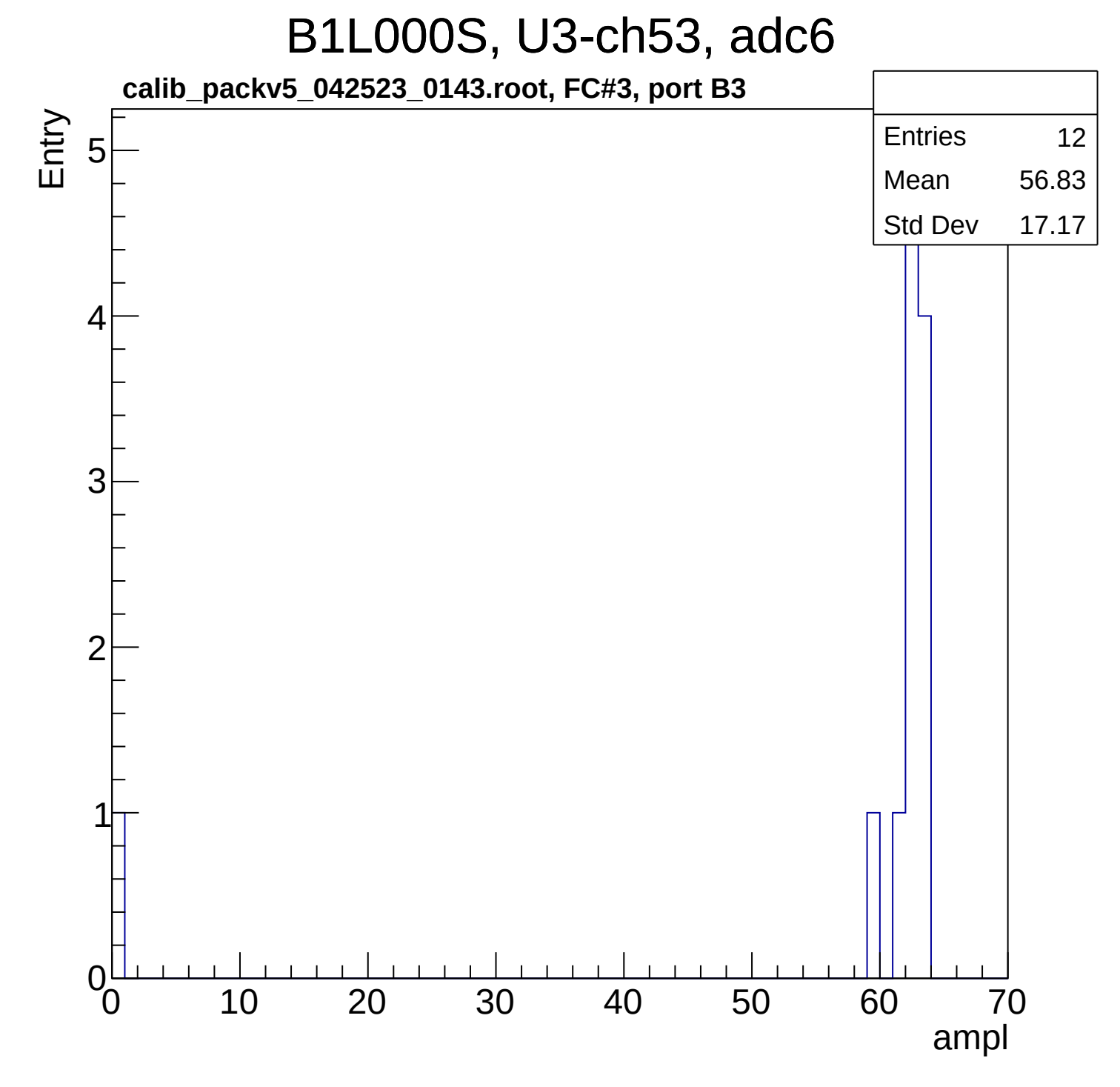
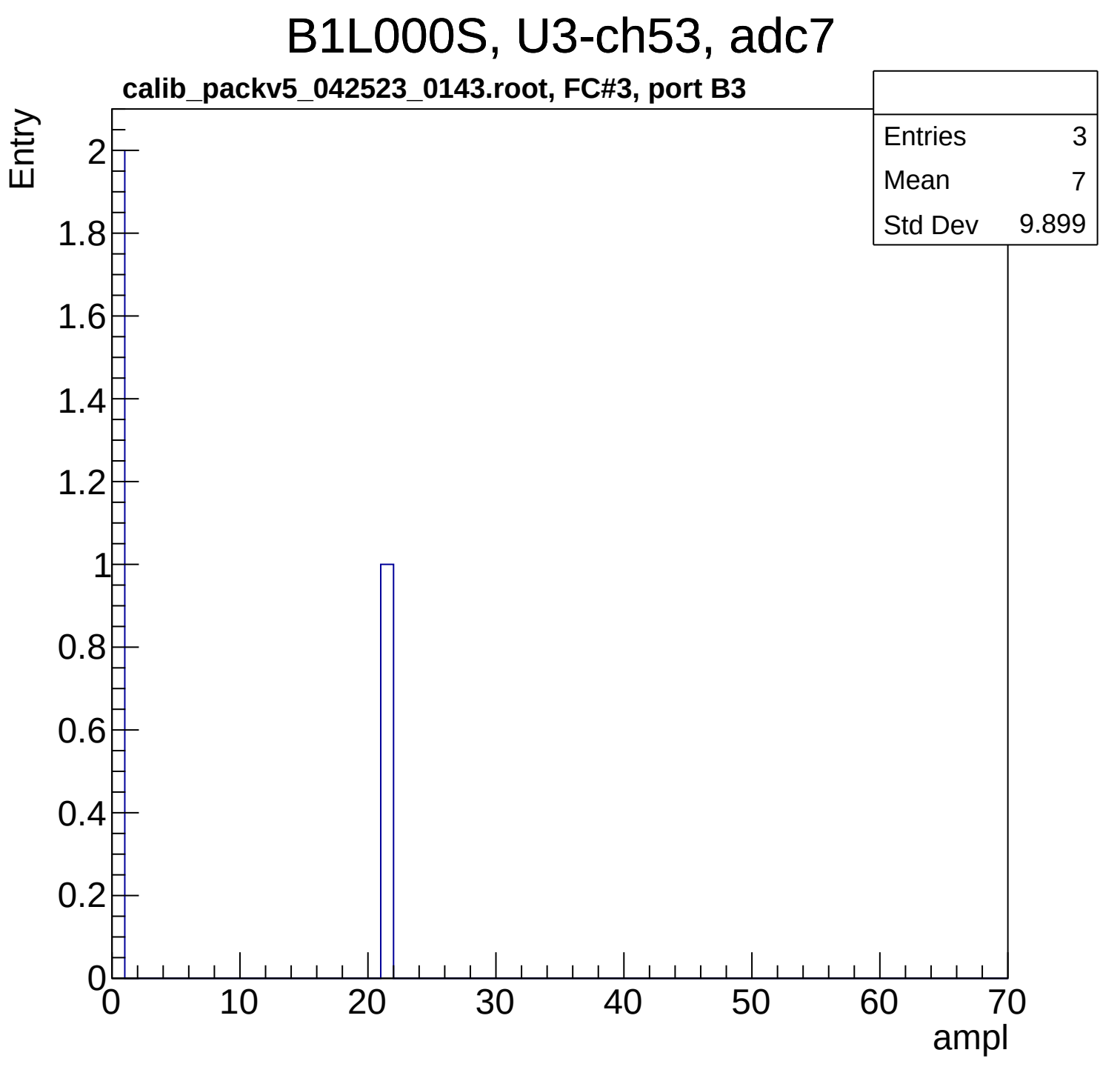
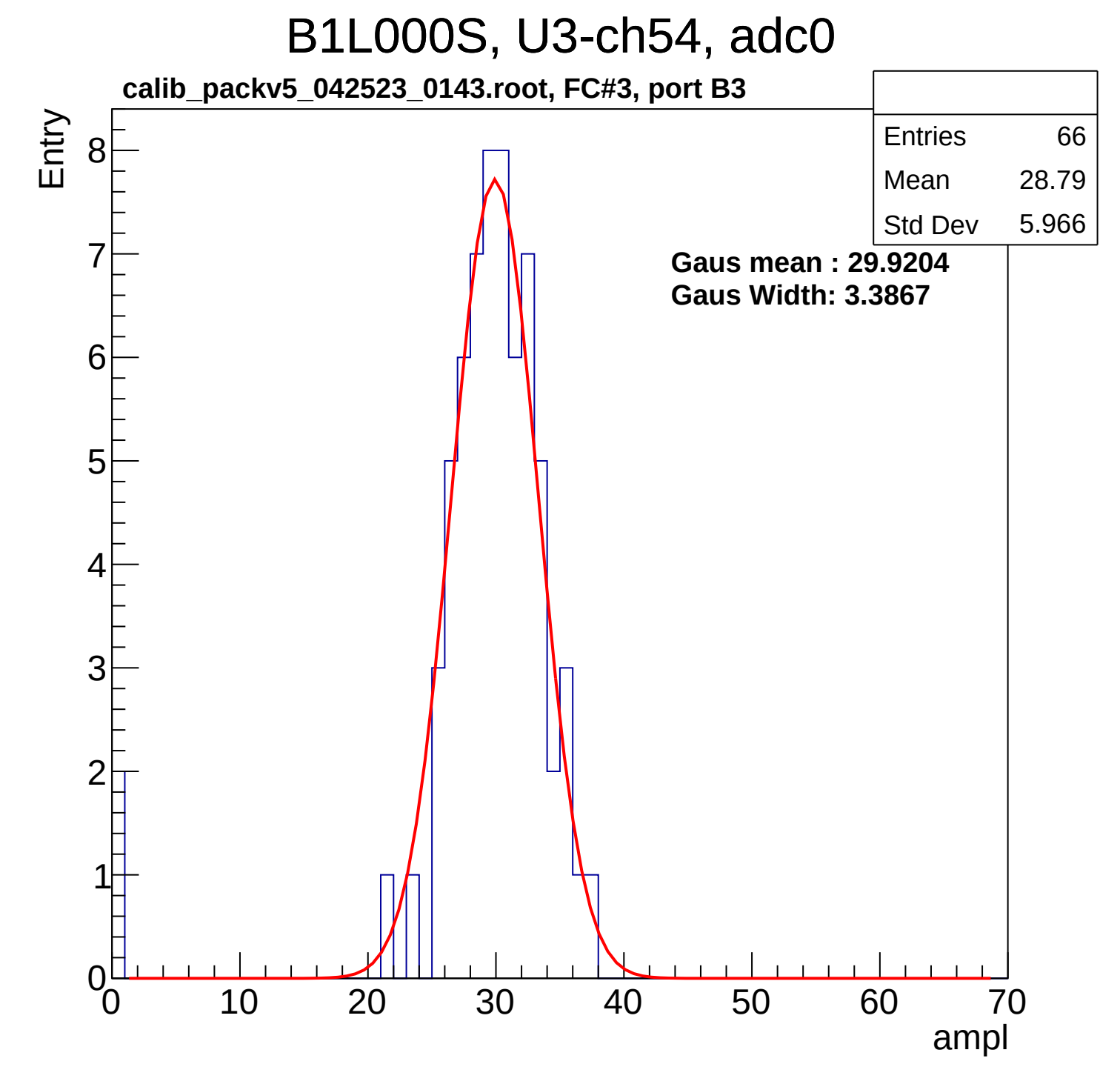
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch53, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch53, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch54, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.79
Std Dev 5.966
Gaus mean : 29.9204
Gaus Width: 3.3867
Entry
ampl
0
1
2
3
4
5
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7
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0
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60
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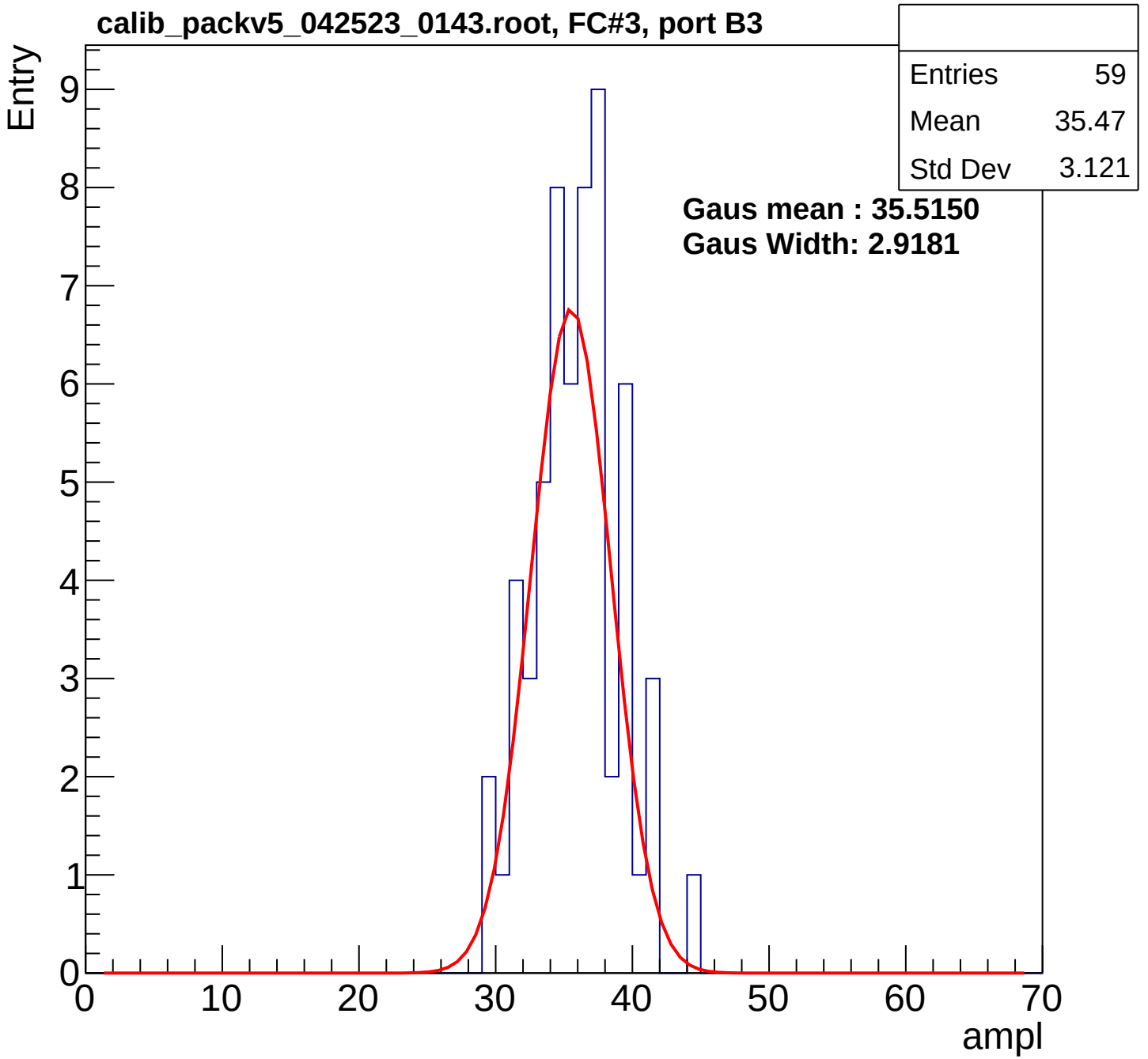
B1L000S, U3-ch54, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 35.47
Std Dev 3.121
Gaus mean : 35.5150
Gaus Width: 2.9181
Entry
ampl
0
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2
3
4
5
6
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8
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0
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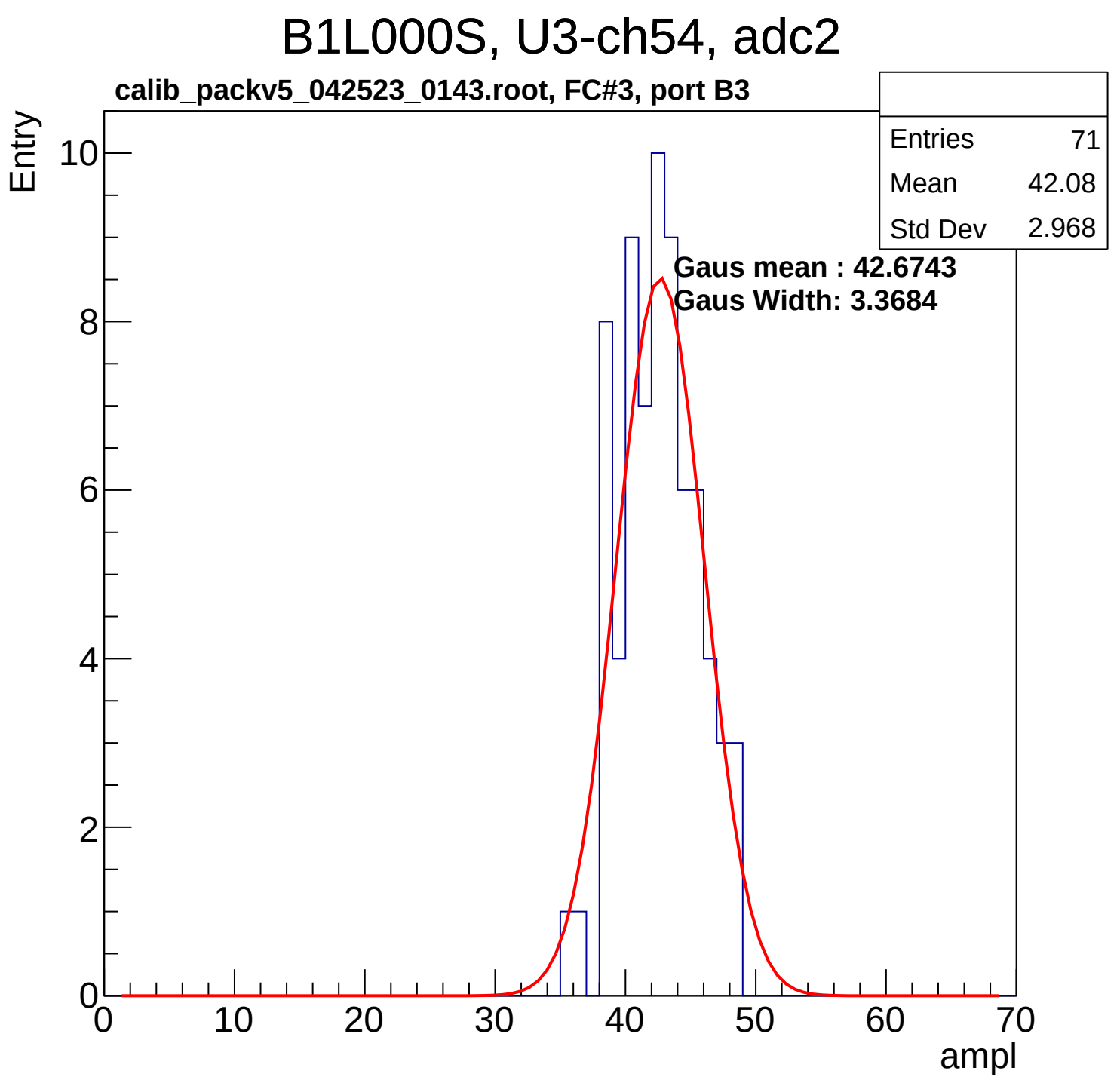
B1L000S, U3-ch54, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 42.08
Std Dev 2.968
Gaus mean : 42.6743
Gaus Width: 3.3684
Entry
ampl
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4
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8
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0
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40
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60
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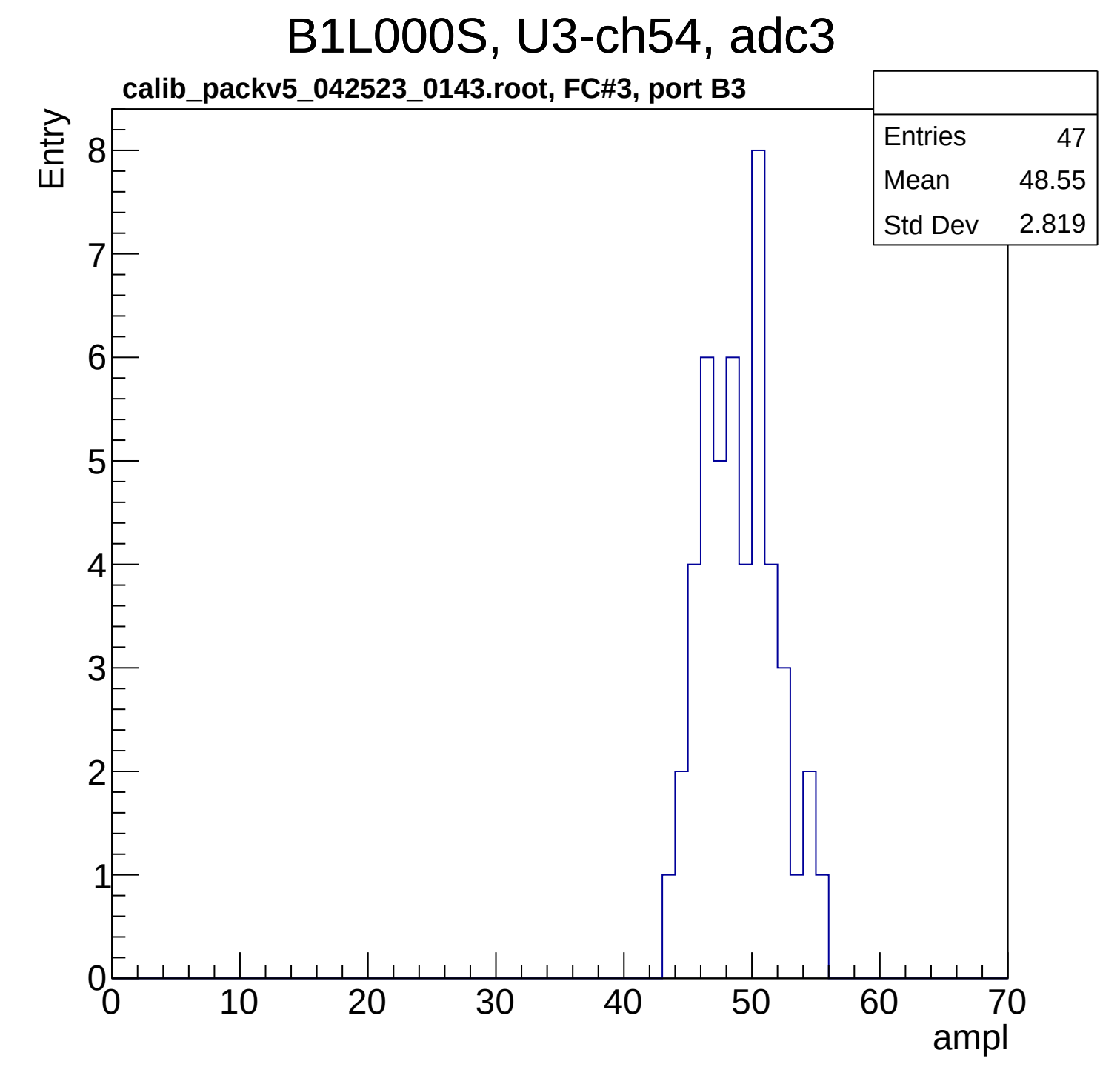
B1L000S, U3-ch54, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 48.55
Std Dev 2.819
Entry
ampl
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1
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3
4
5
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7
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0
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60
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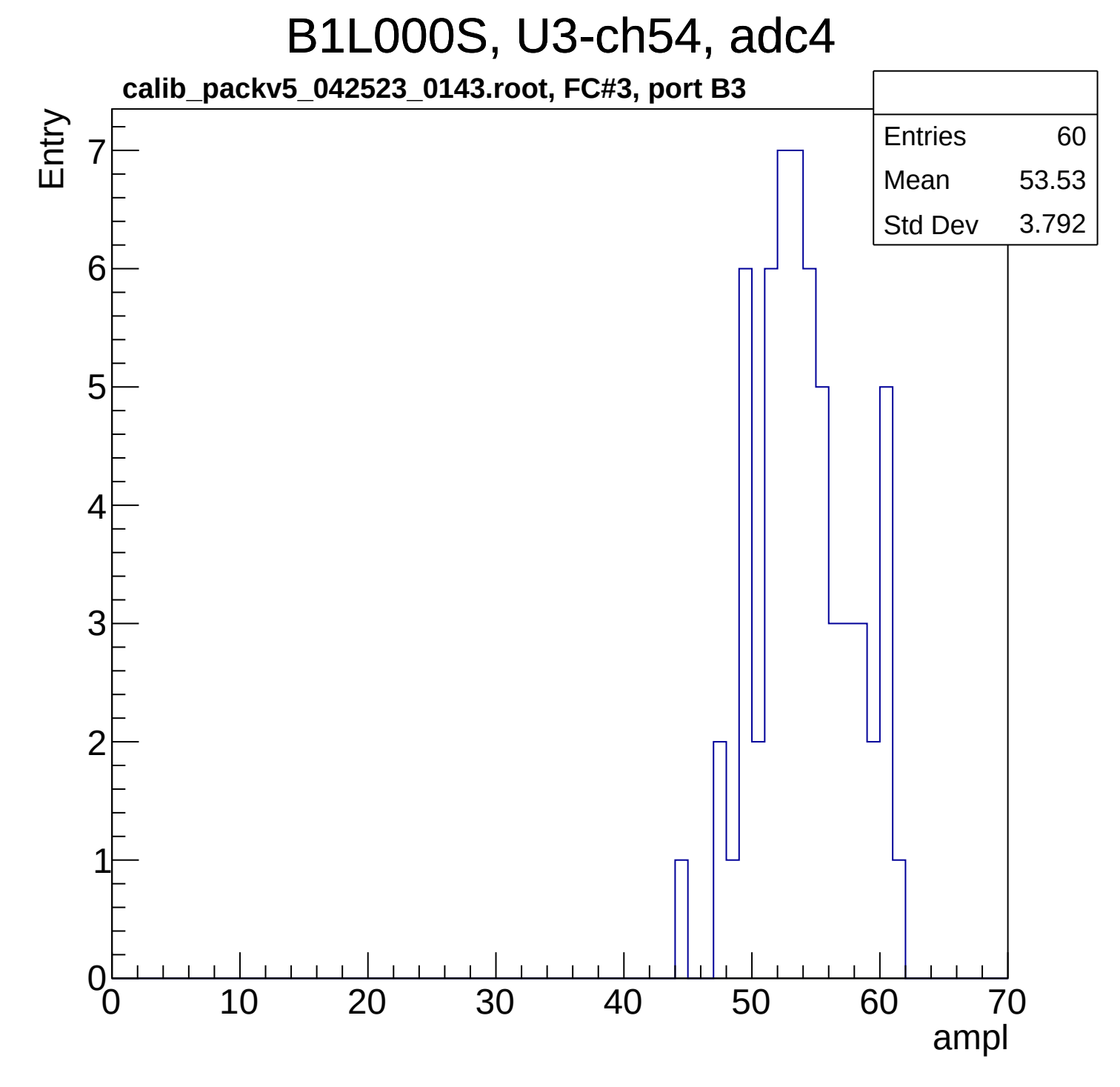
B1L000S, U3-ch54, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 53.53
Std Dev 3.792
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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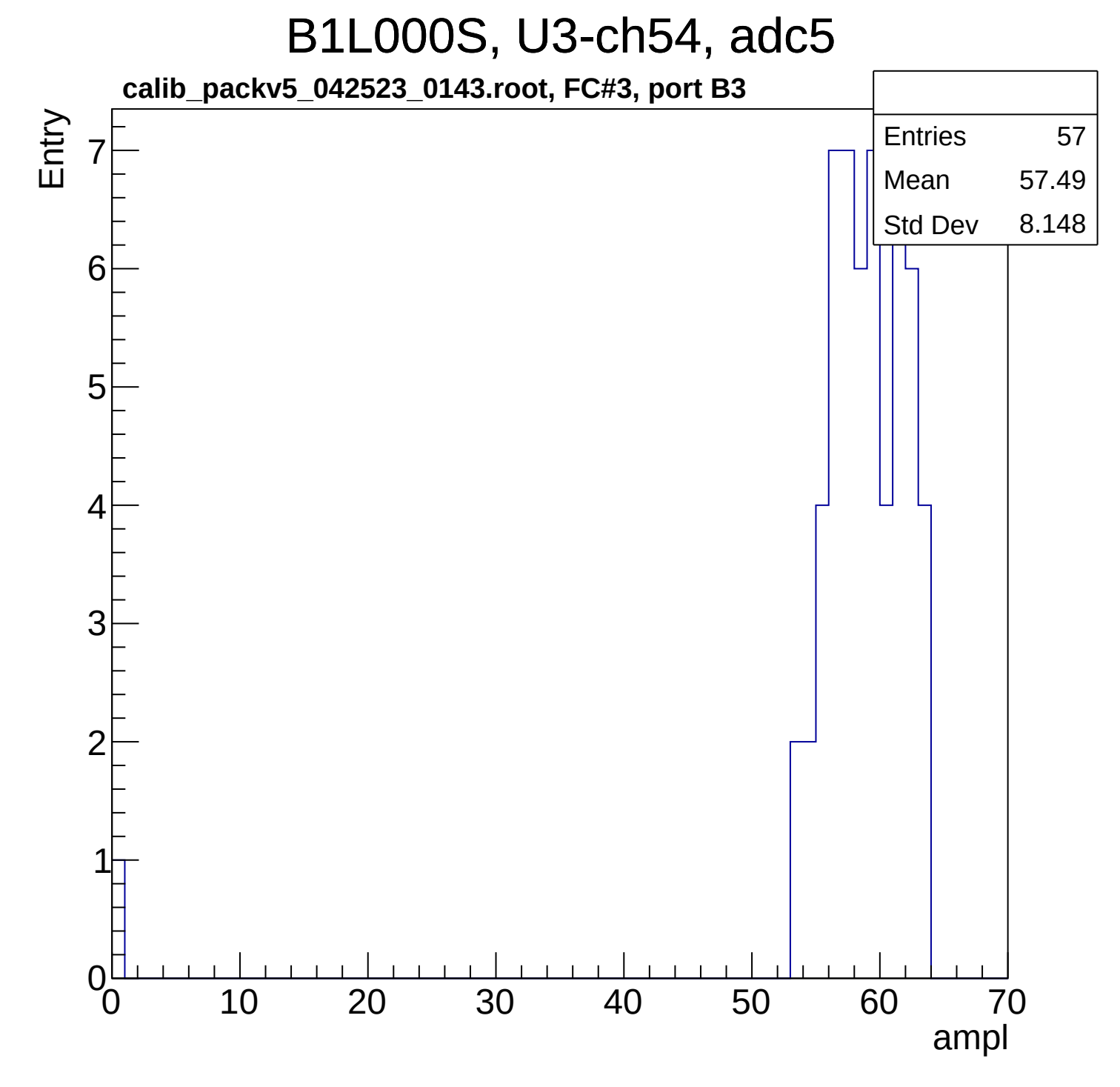
B1L000S, U3-ch54, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 57.49
Std Dev 8.148
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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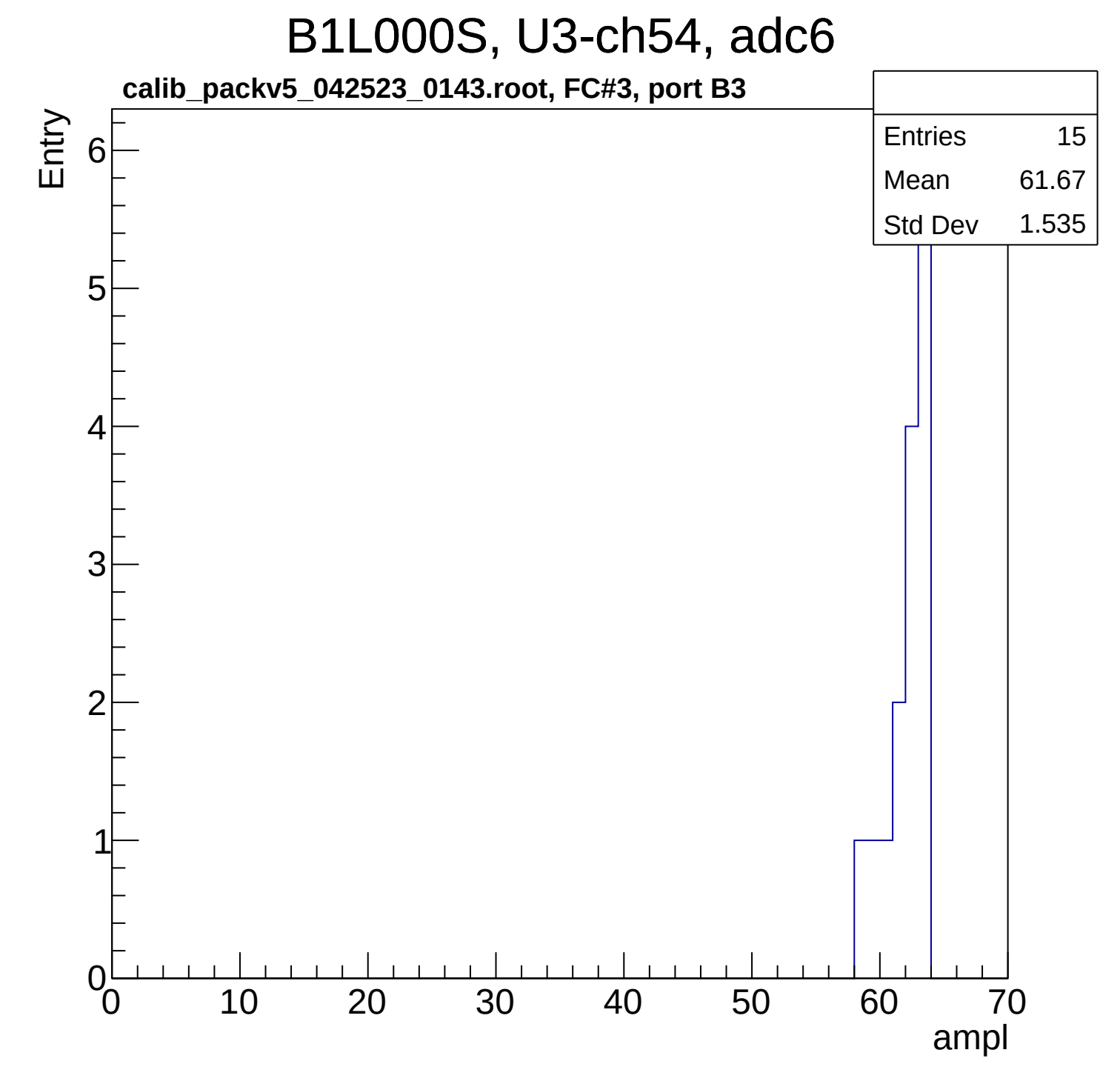
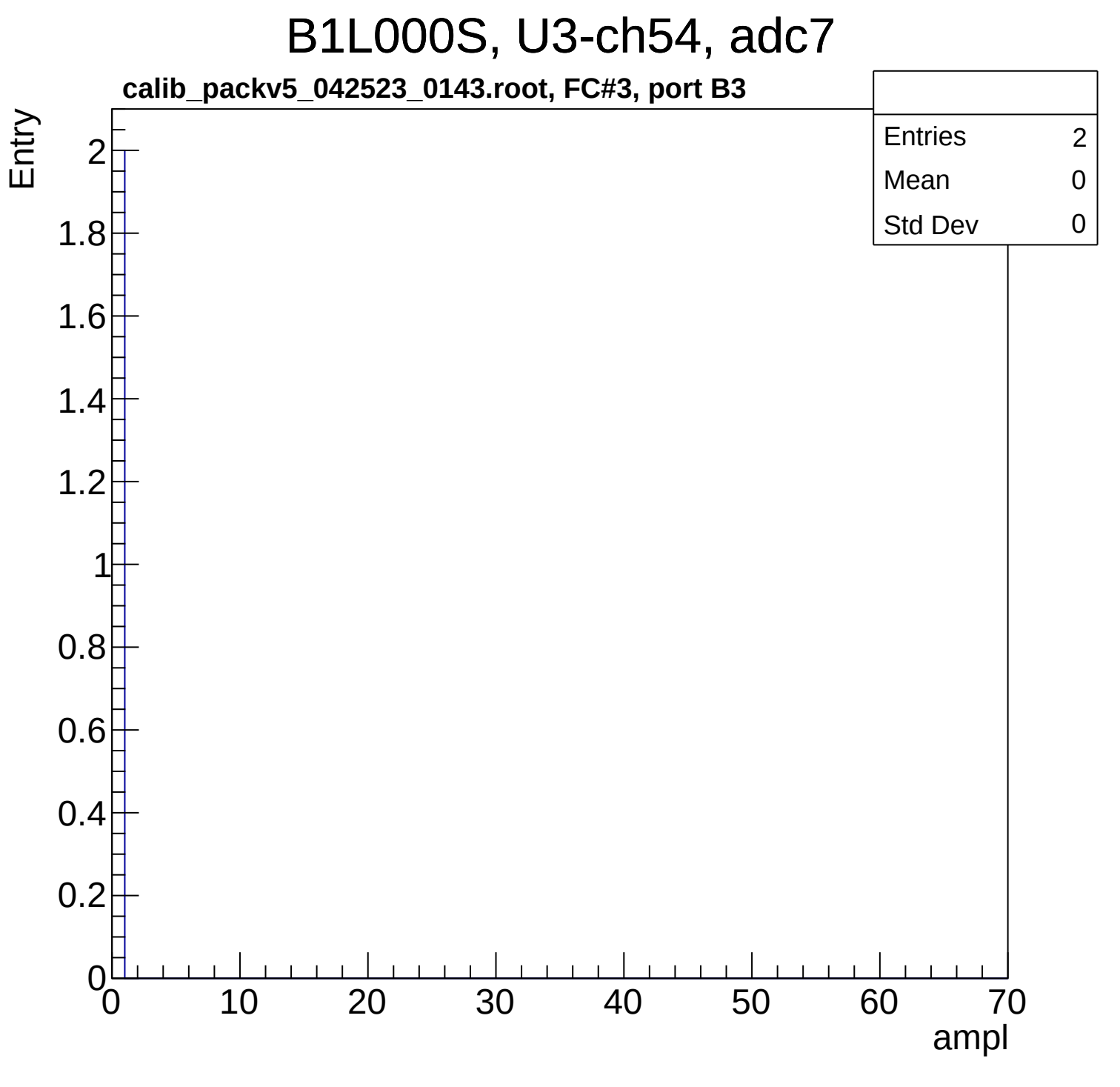
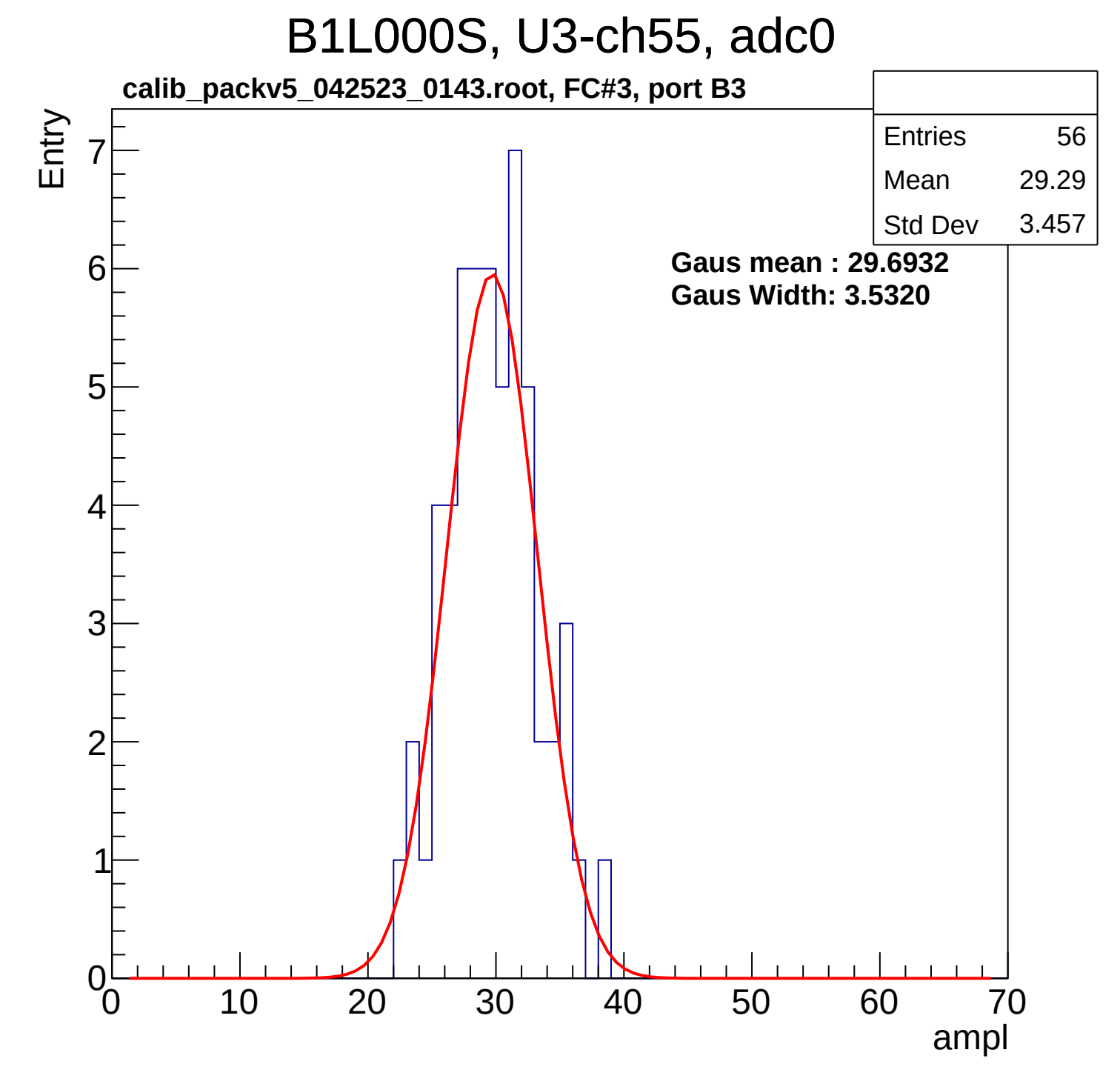

B1L000S, U3-ch54, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 61.67
Std Dev 1.535
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch54, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch55, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 29.29
Std Dev 3.457
Gaus mean : 29.6932
Gaus Width: 3.5320
Entry
ampl
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1
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3
4
5
6
7
0
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20
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60
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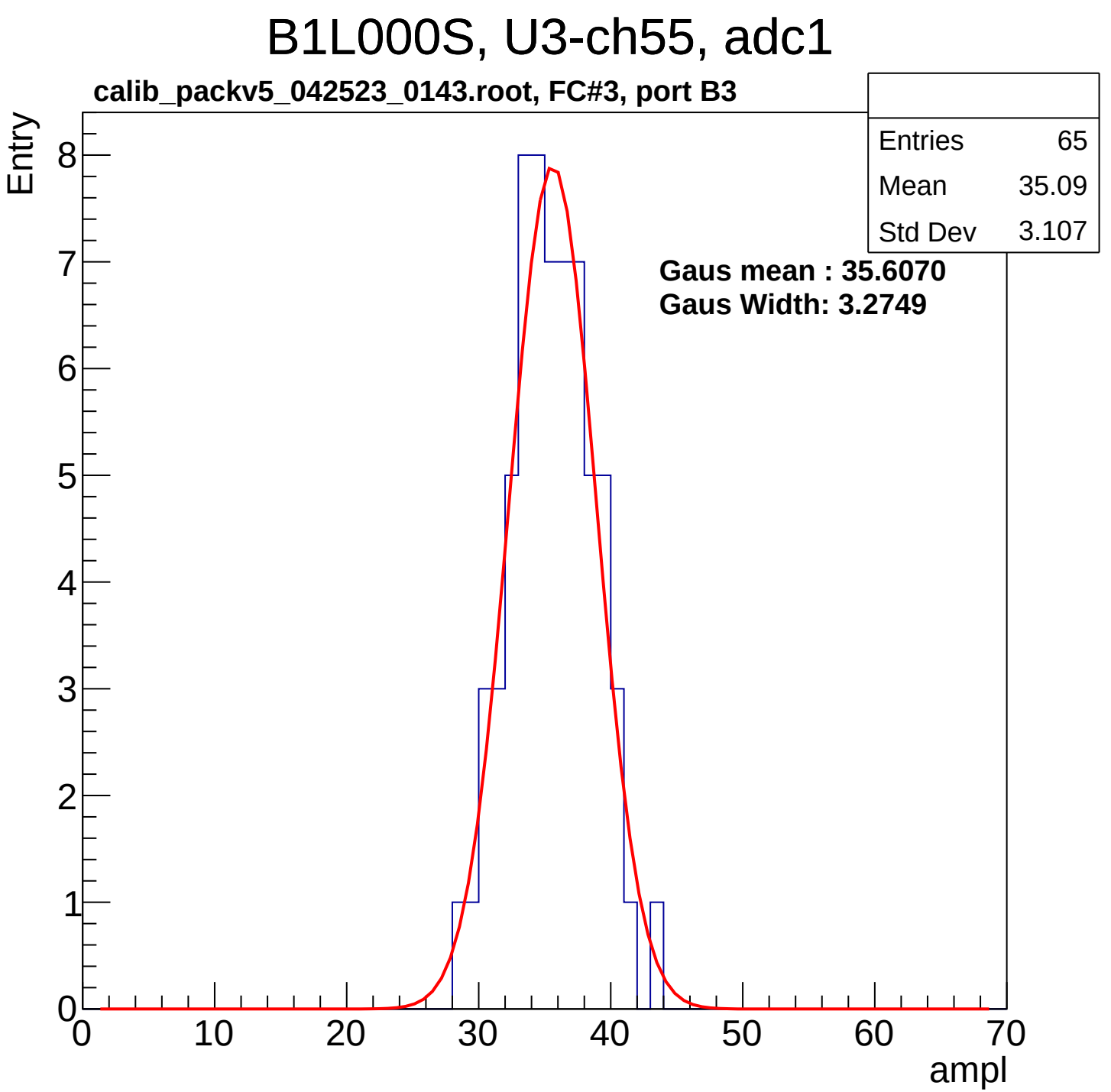
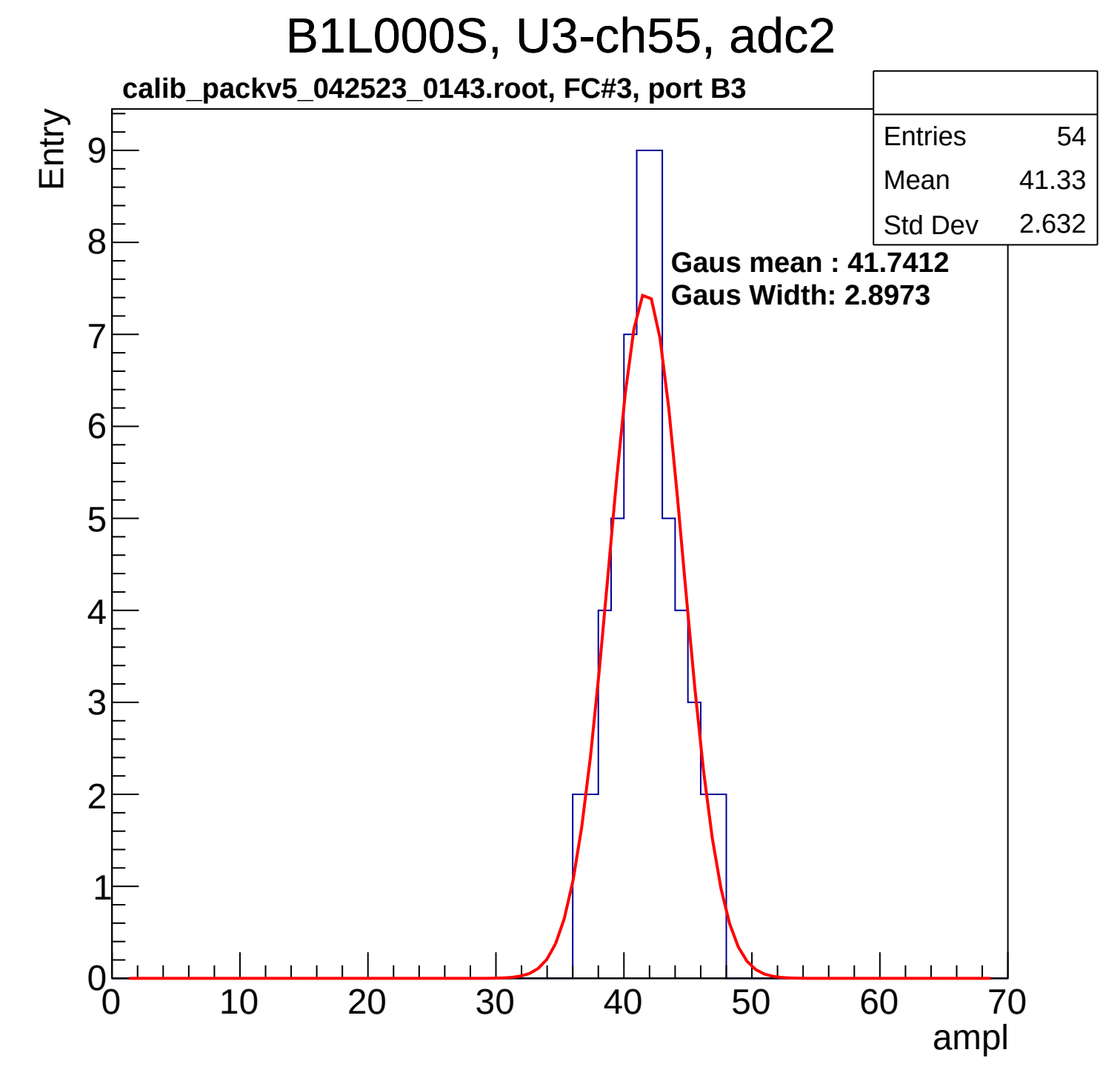

B1L000S, U3-ch55, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.09
Std Dev 3.107
Gaus mean : 35.6070
Gaus Width: 3.2749
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch55, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 41.33
Std Dev 2.632
Gaus mean : 41.7412
Gaus Width: 2.8973
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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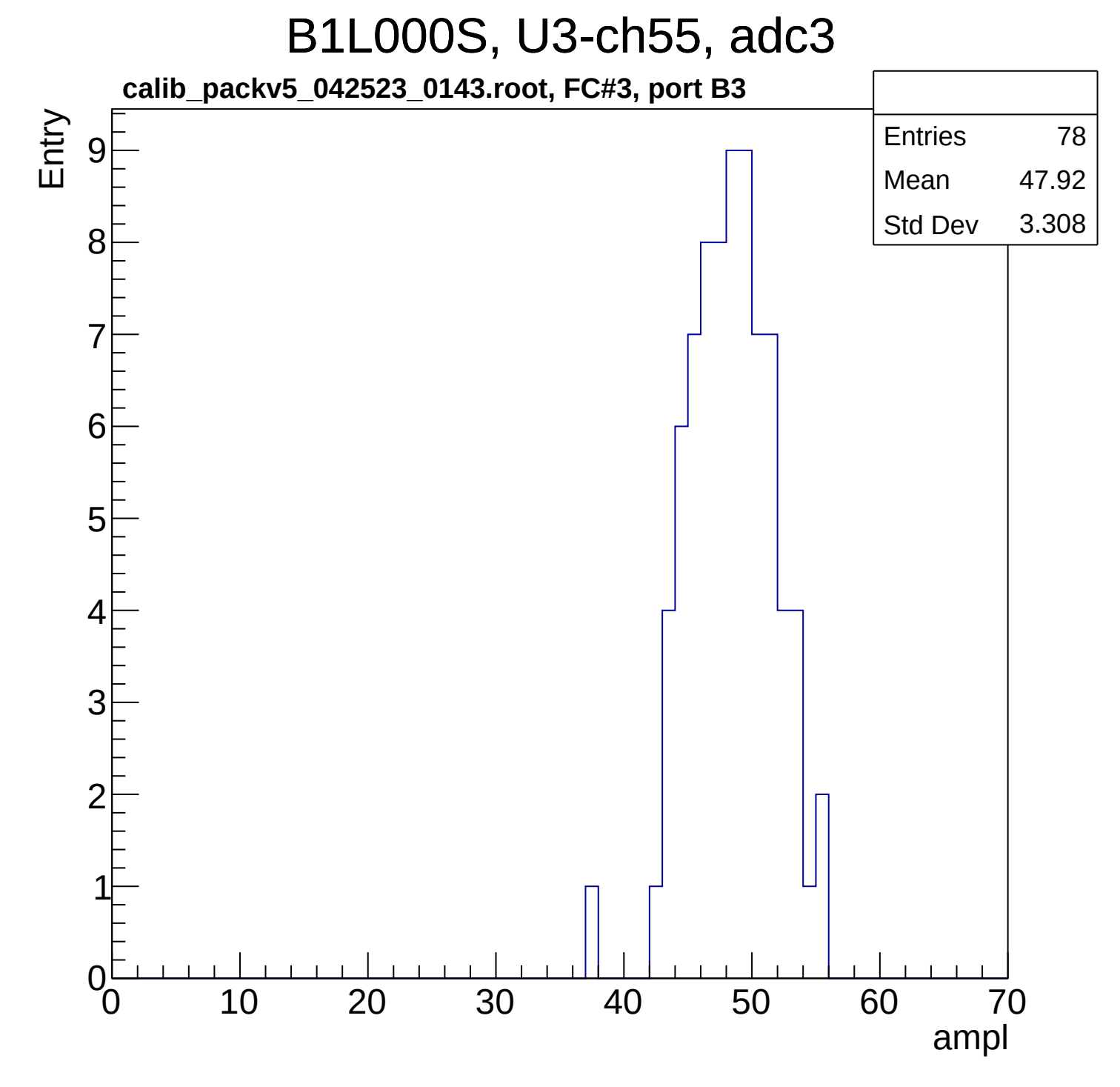
B1L000S, U3-ch55, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 47.92
Std Dev 3.308
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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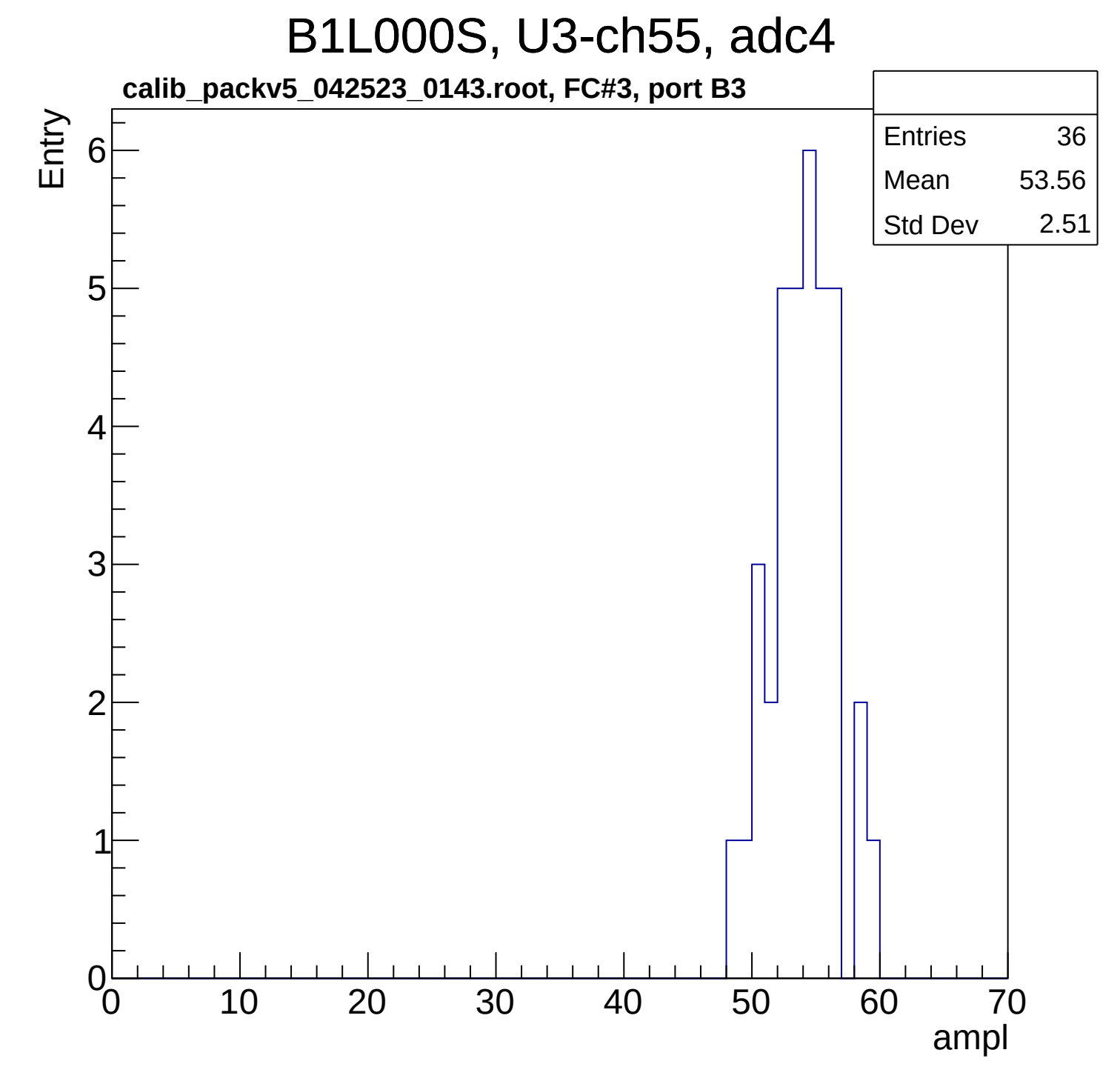
B1L000S, U3-ch55, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 36
Mean 53.56
Std Dev 2.51
Entry
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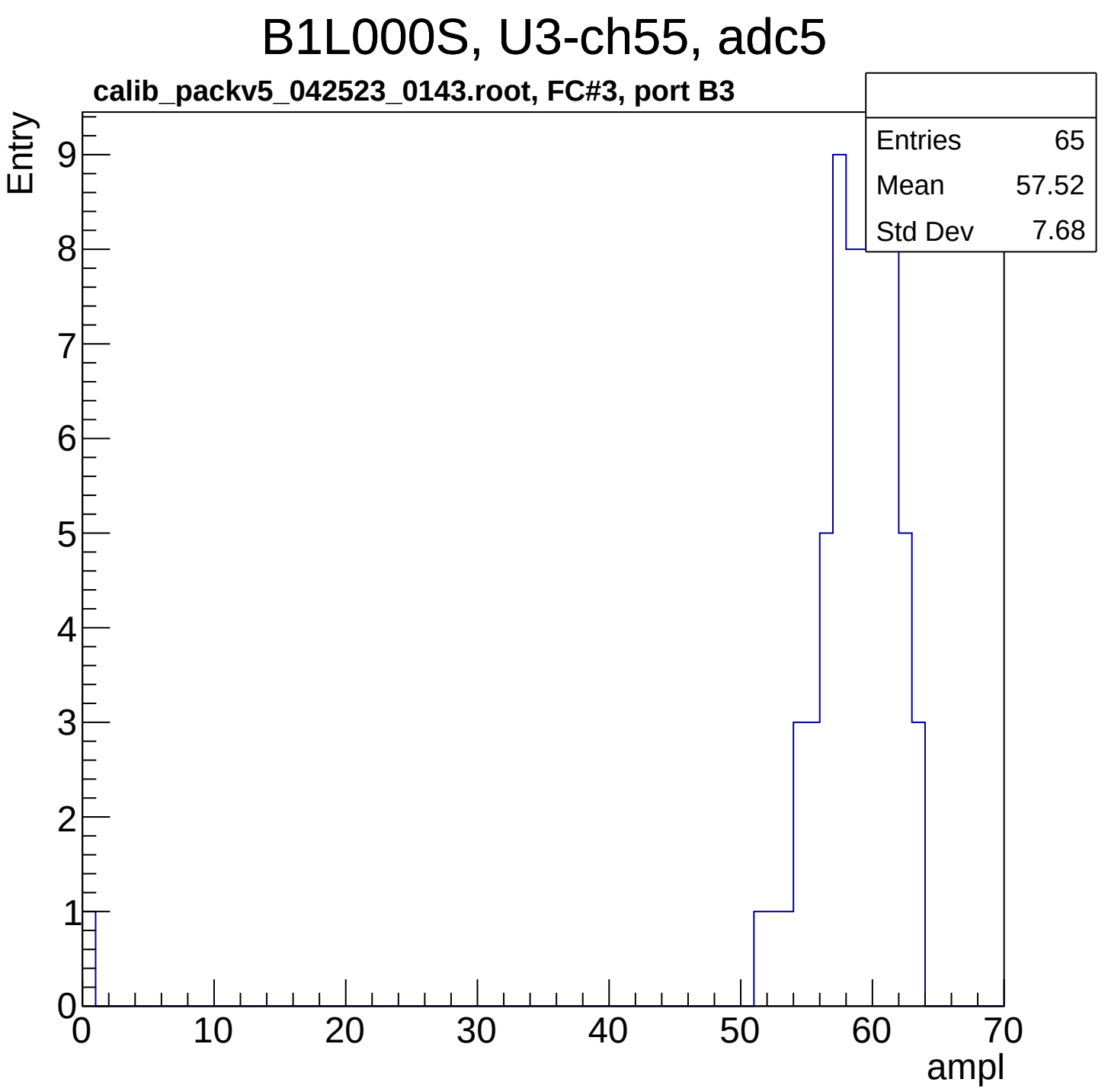
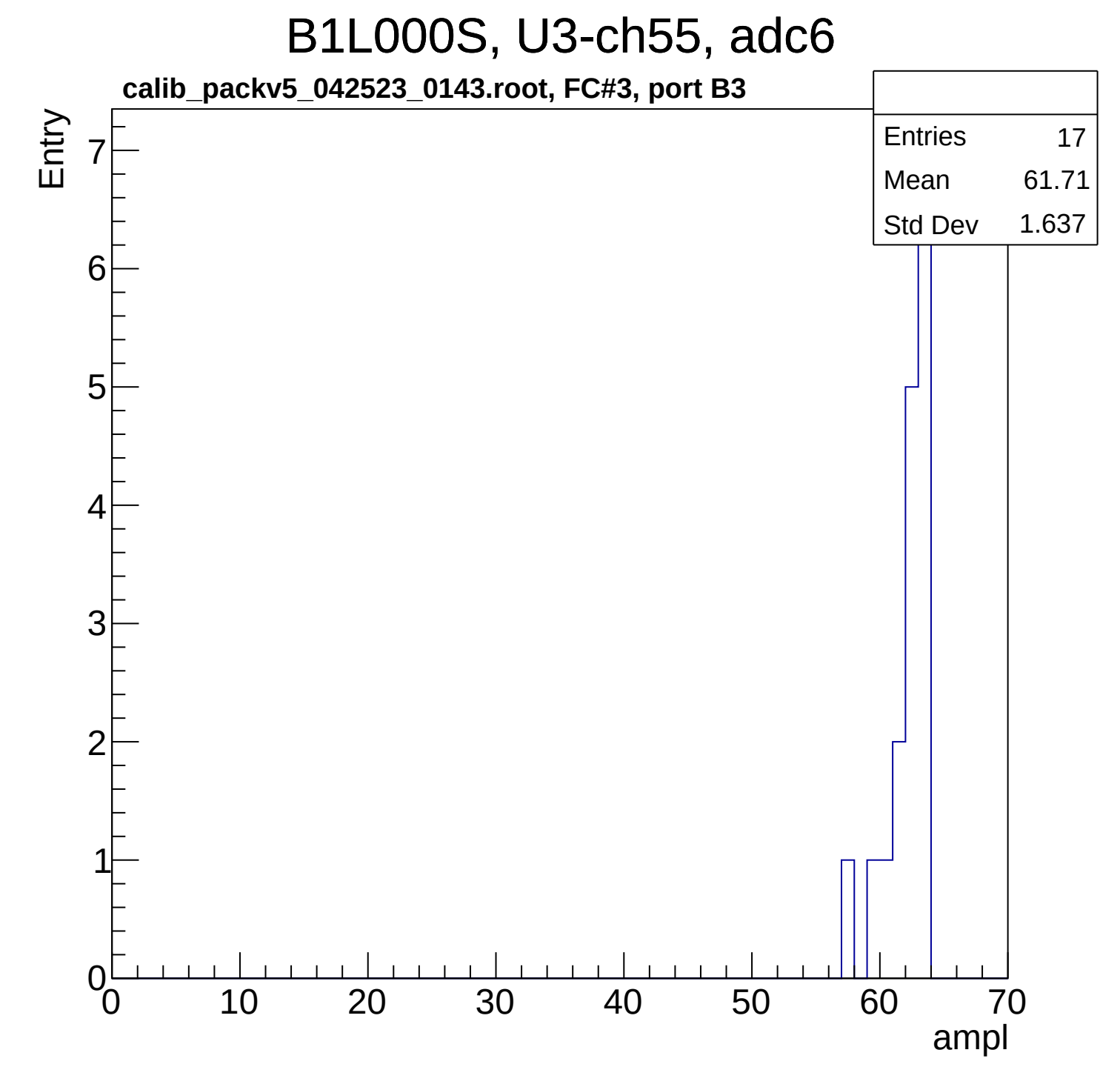
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch55, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 57.52
Std Dev 7.68
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch55, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.71
Std Dev 1.637
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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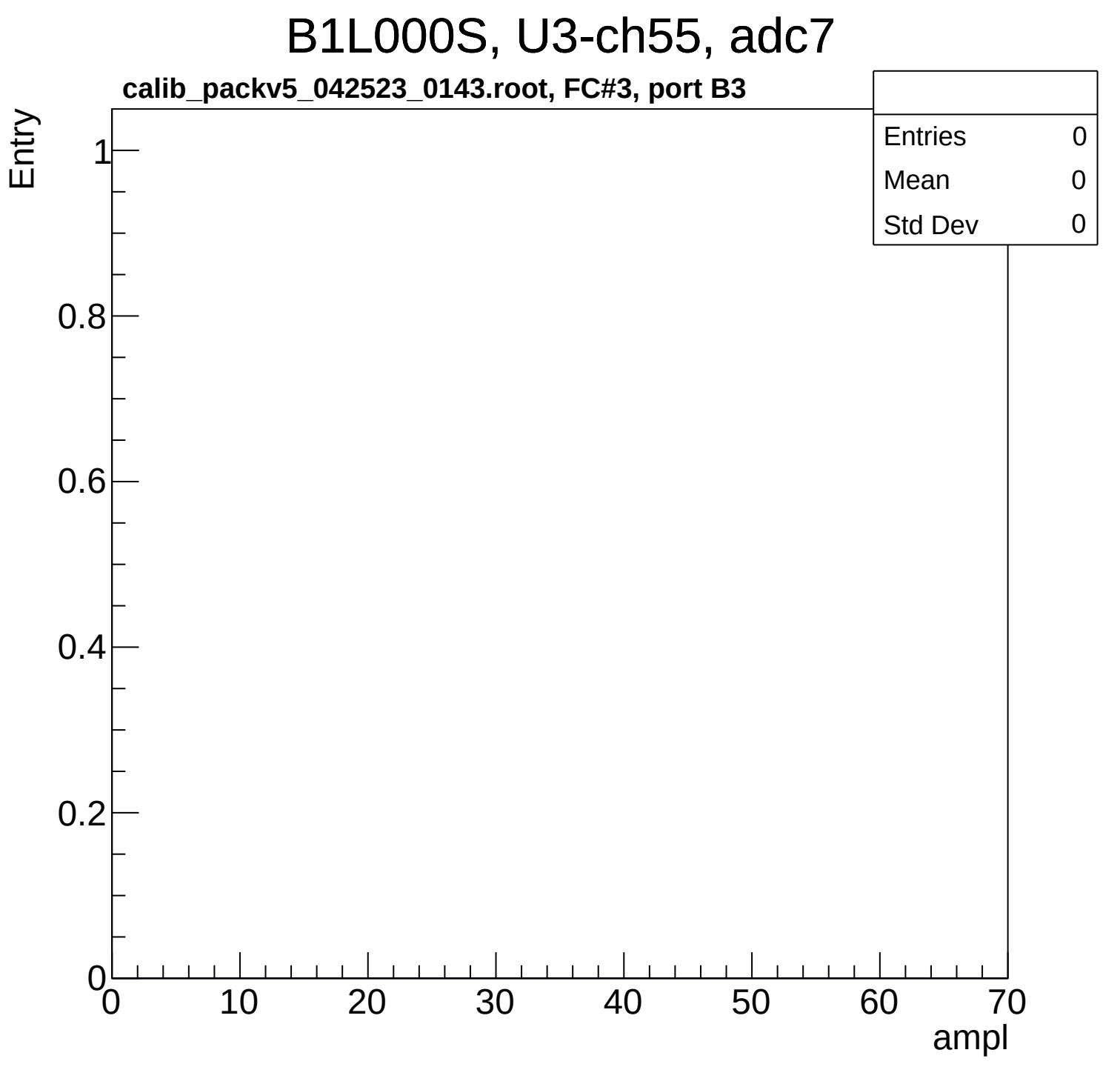
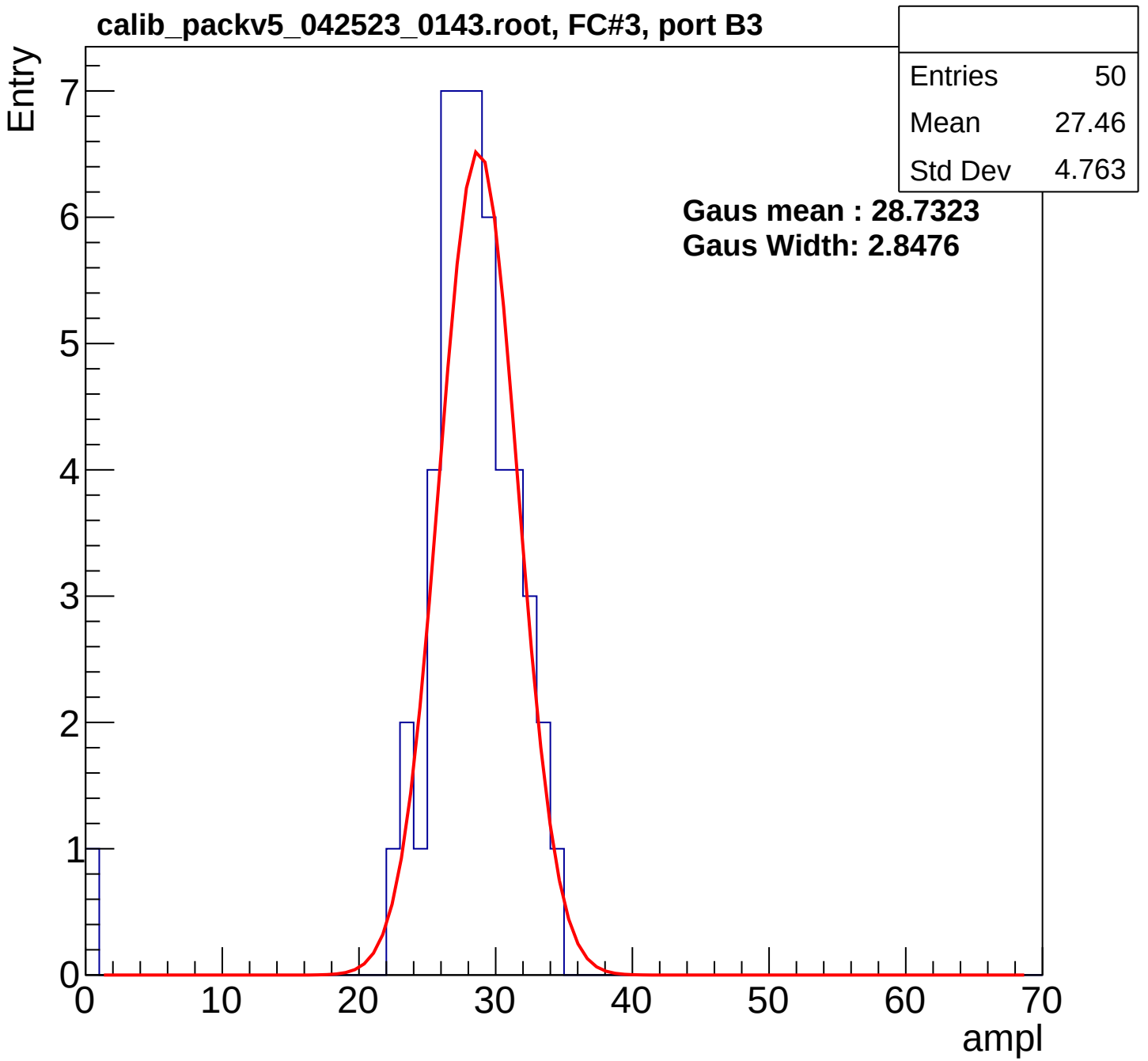

B1L000S, U3-ch55, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch56, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 27.46
Std Dev 4.763
Gaus mean : 28.7323
Gaus Width: 2.8476
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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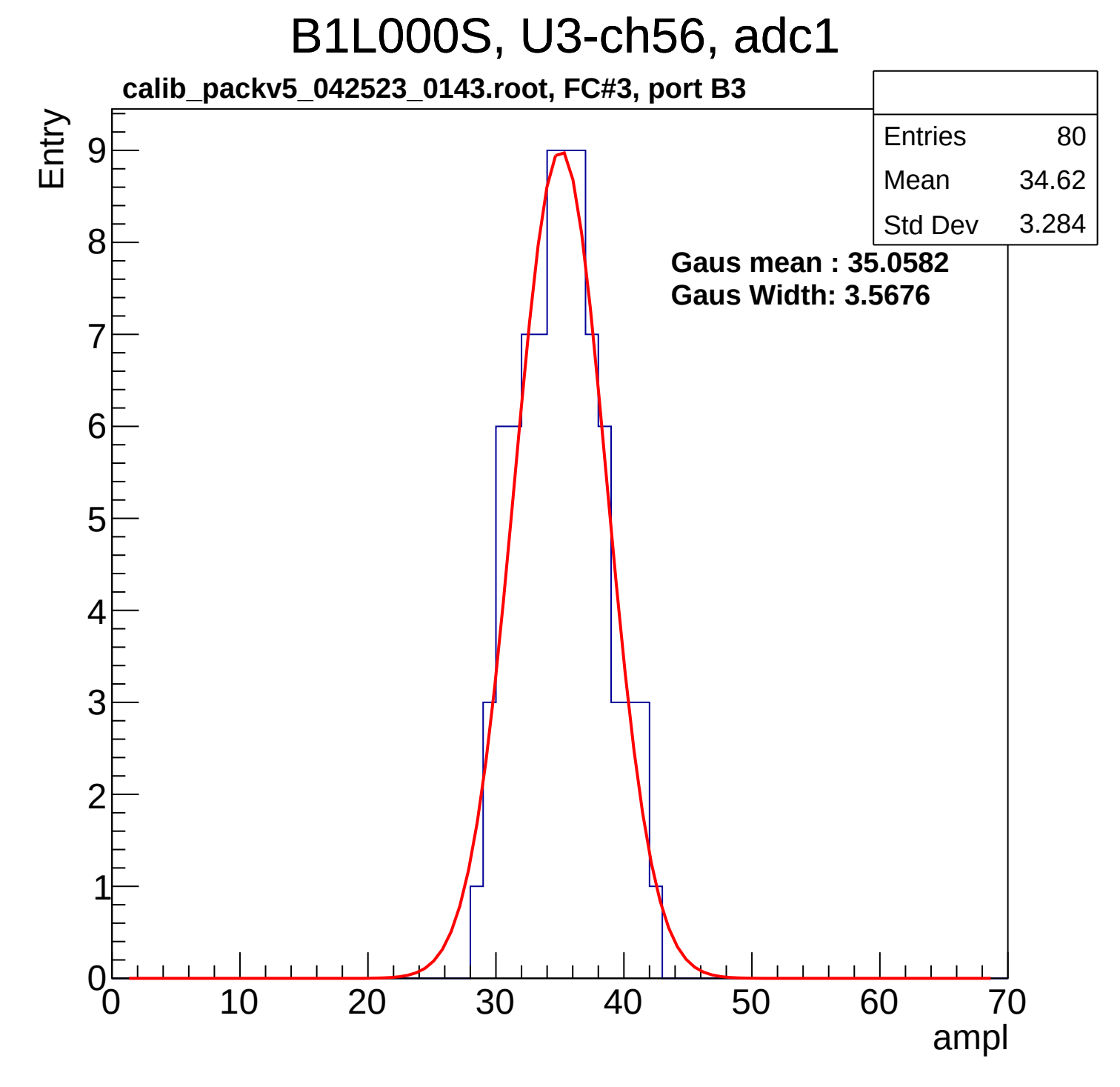
B1L000S, U3-ch56, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 34.62
Std Dev 3.284
Gaus mean : 35.0582
Gaus Width: 3.5676
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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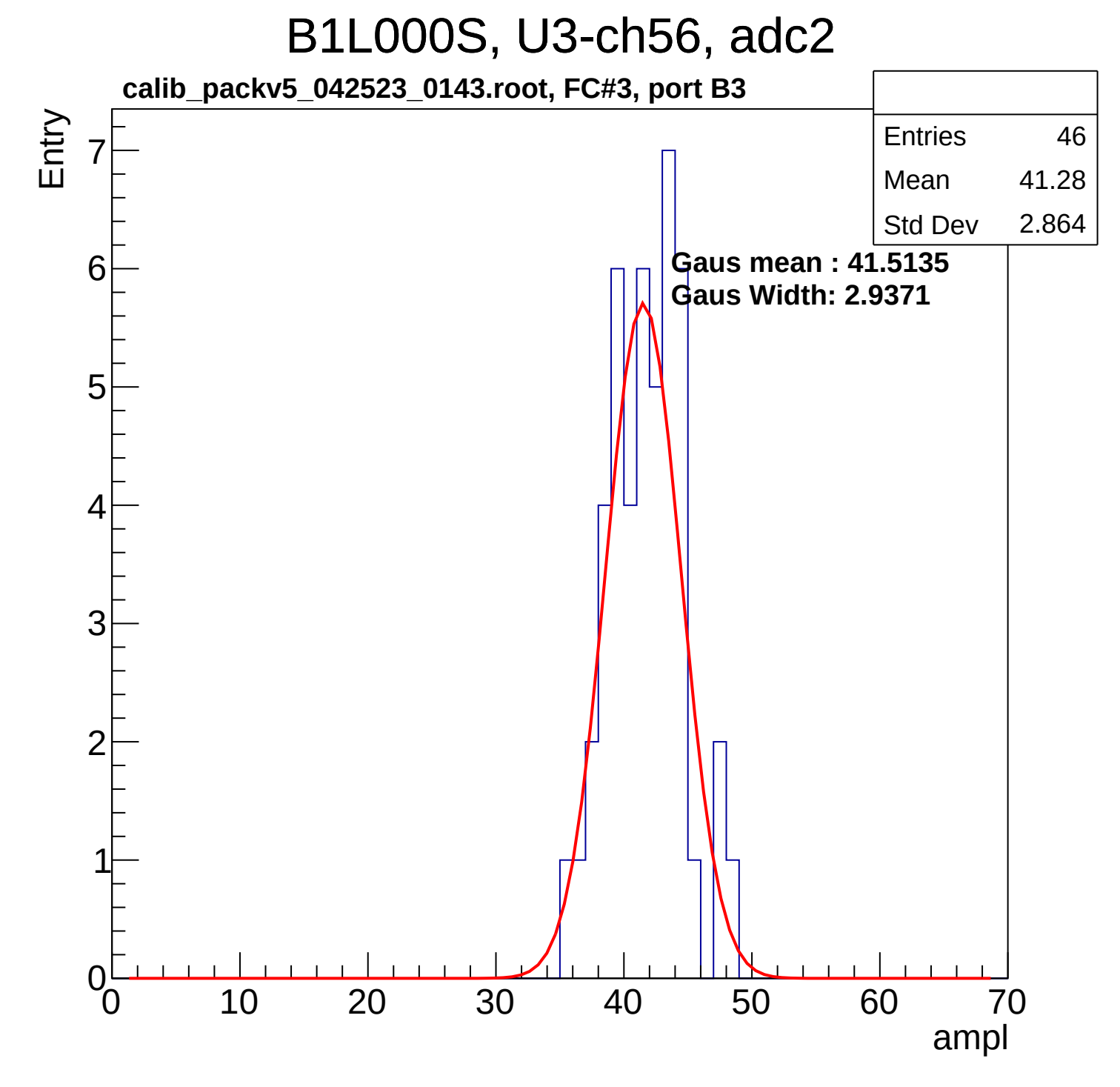
B1L000S, U3-ch56, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 41.28
Std Dev 2.864
Gaus mean : 41.5135
Gaus Width: 2.9371
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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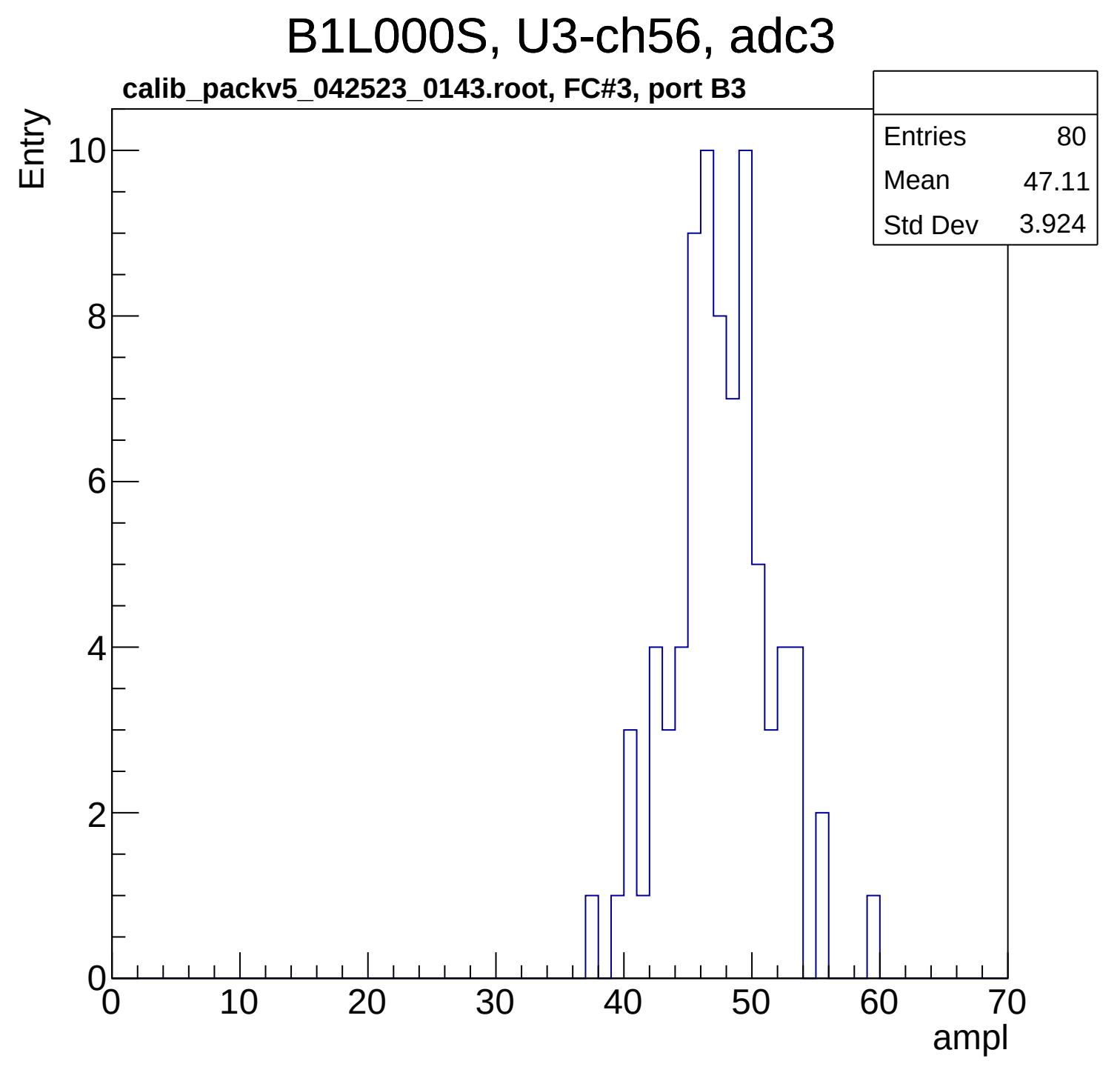
B1L000S, U3-ch56, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 47.11
Std Dev 3.924
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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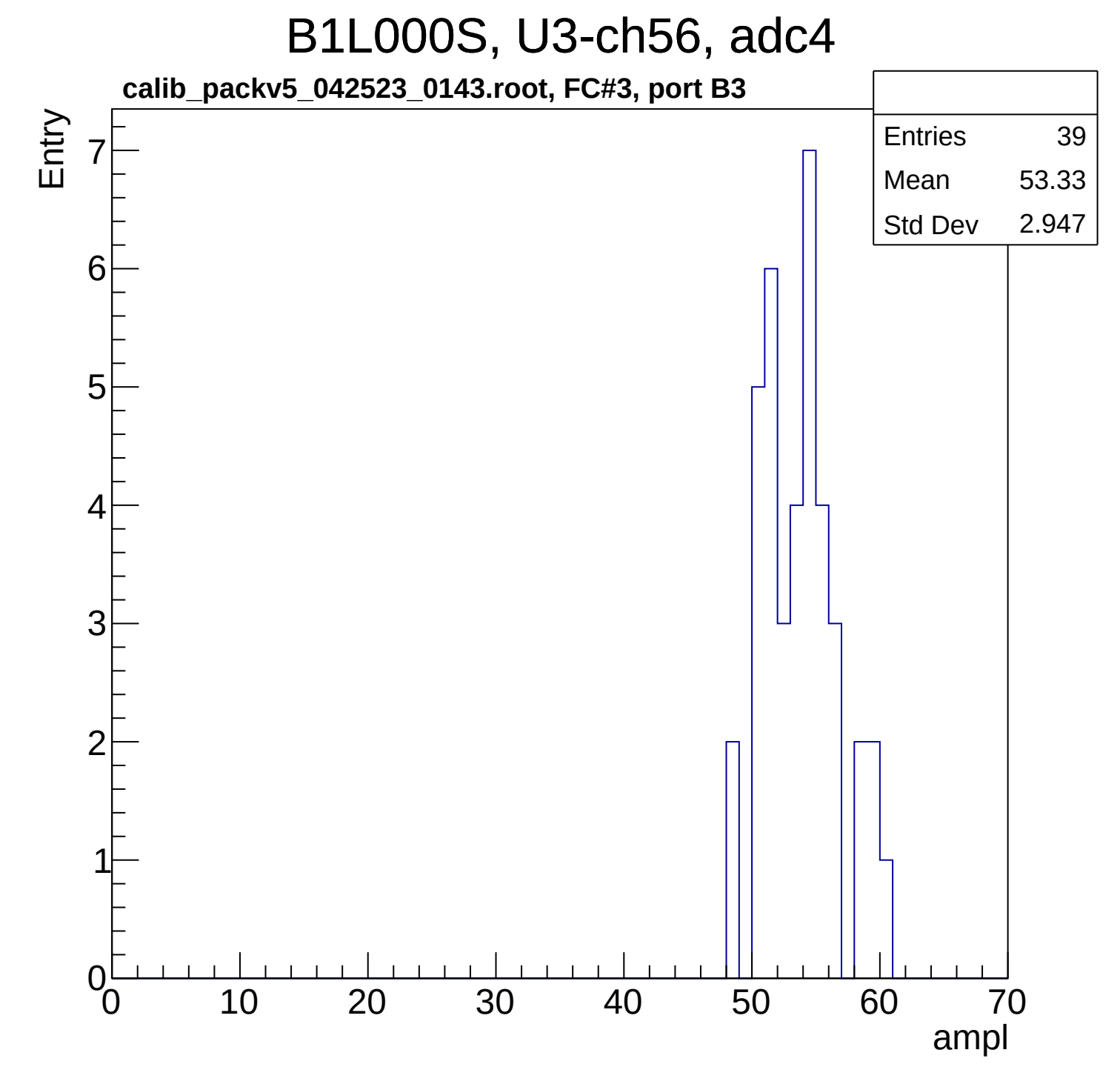
B1L000S, U3-ch56, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 53.33
Std Dev 2.947
Entry
ampl
0
1
2
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4
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6
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0
10
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40
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60
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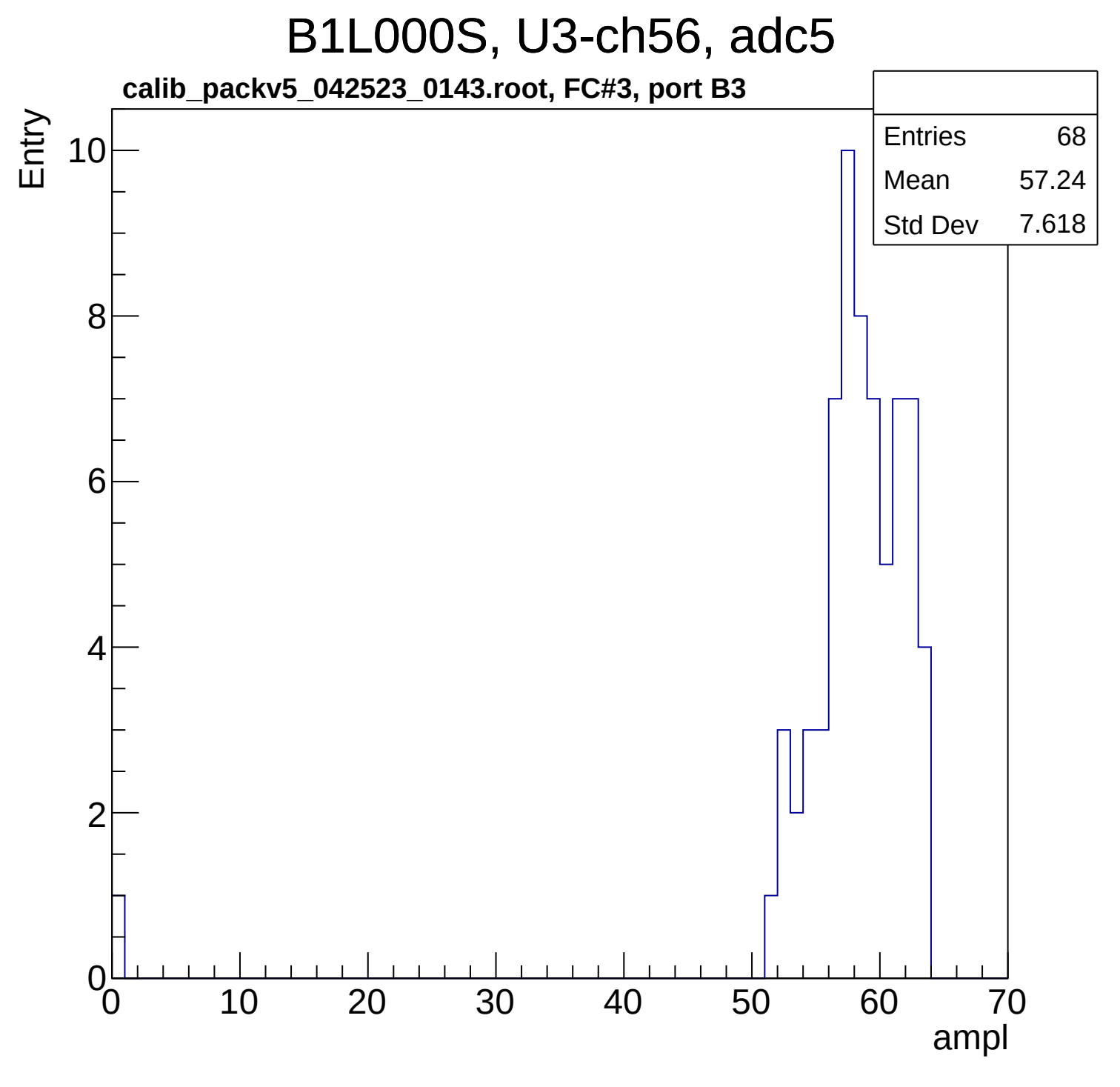
B1L000S, U3-ch56, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 57.24
Std Dev 7.618
Entry
ampl
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4
6
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0
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40
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60
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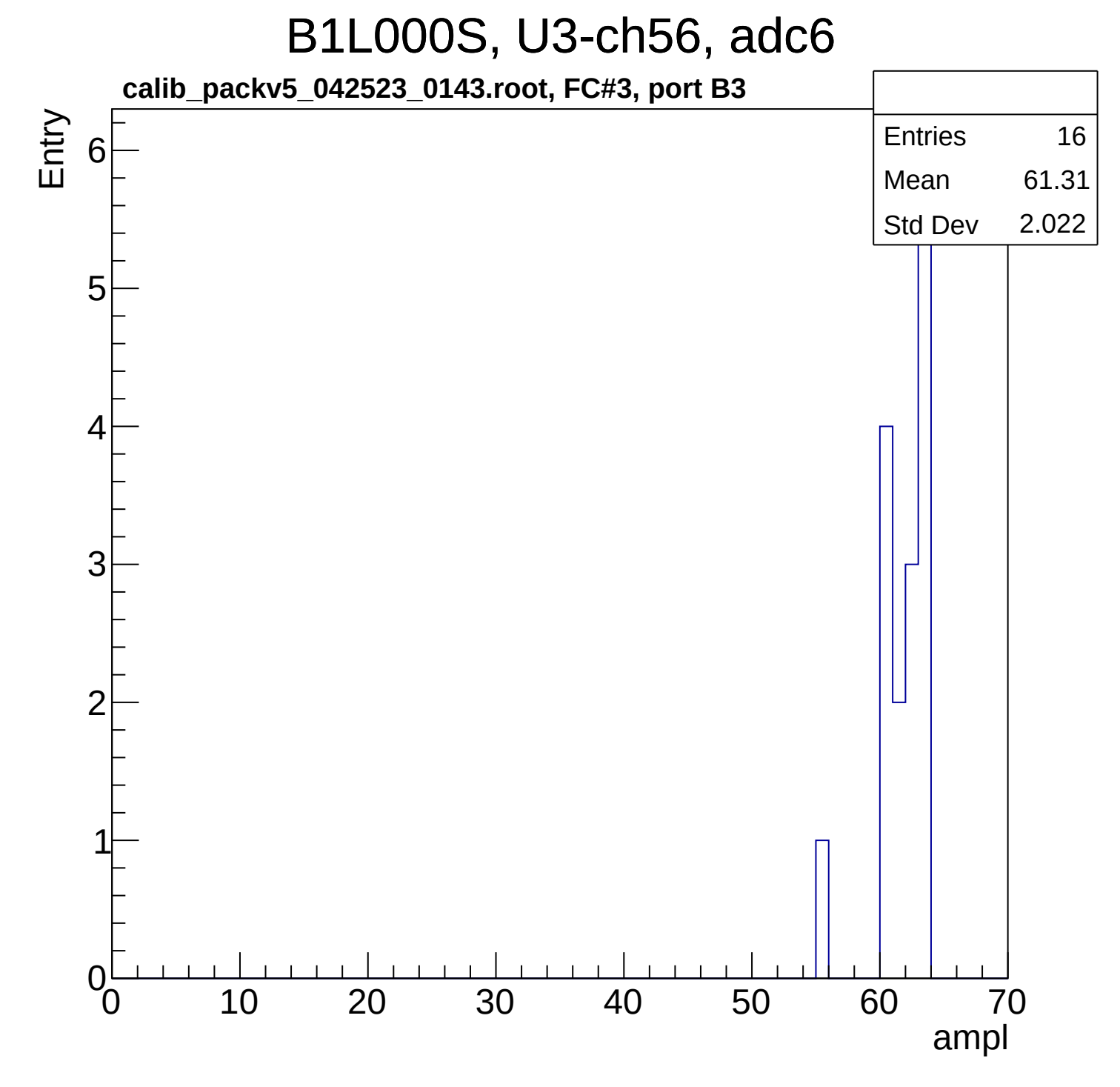
B1L000S, U3-ch56, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 61.31
Std Dev 2.022
Entry
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1
2
3
4
5
6
0
10
20
30
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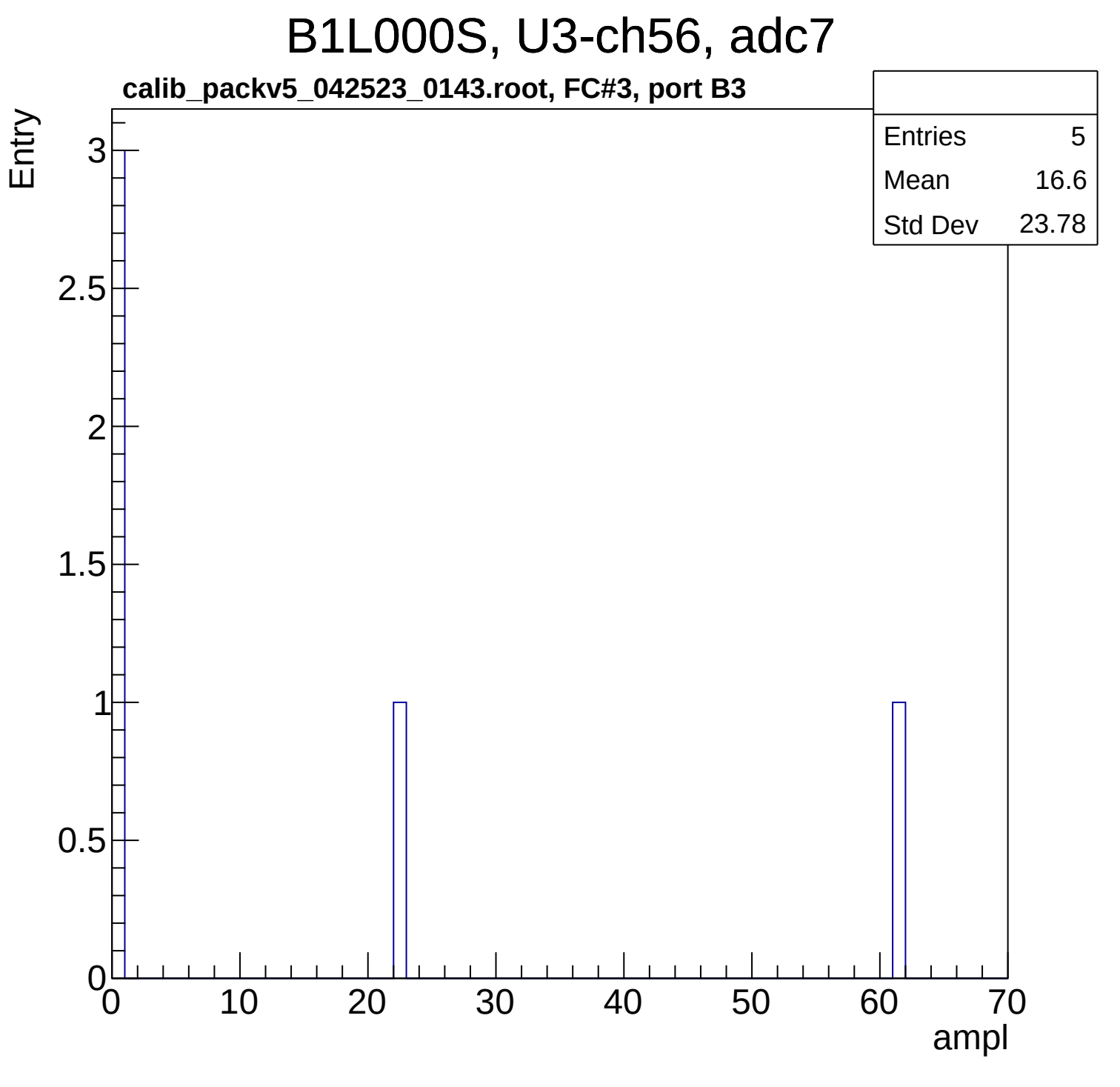
50
60
70
ampl


B1L000S, U3-ch56, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 16.6
Std Dev 23.78
Entry
ampl
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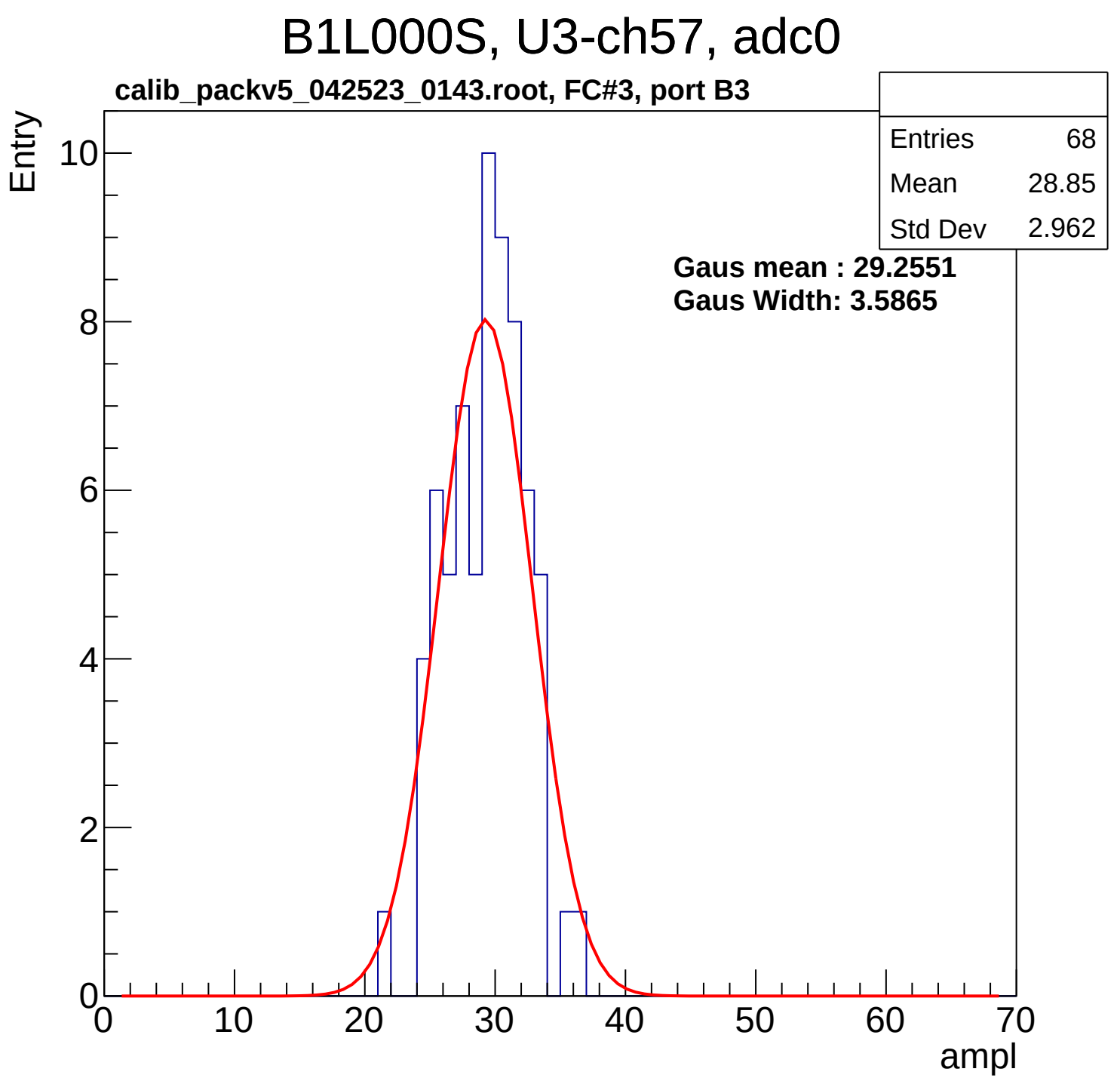
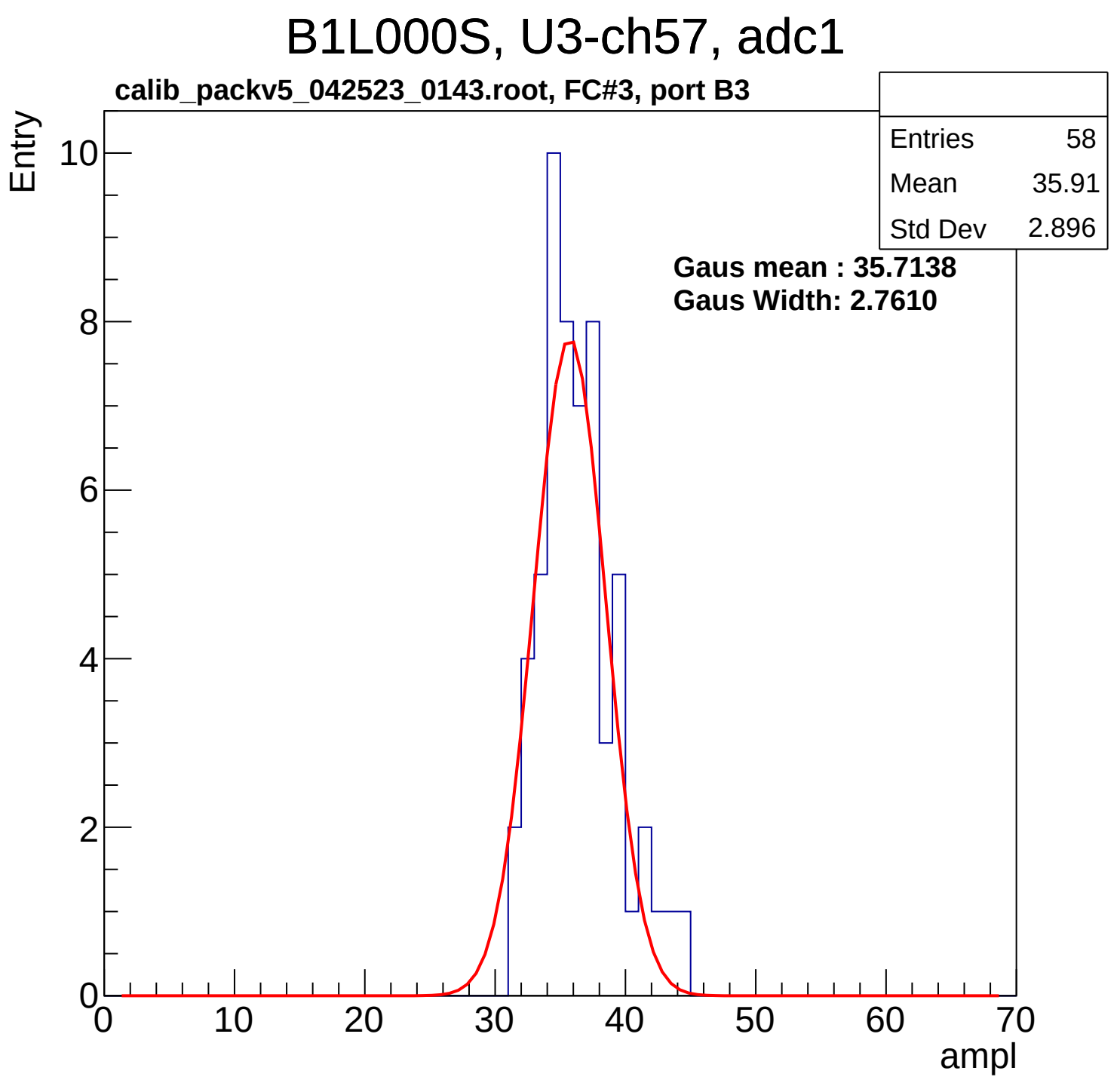
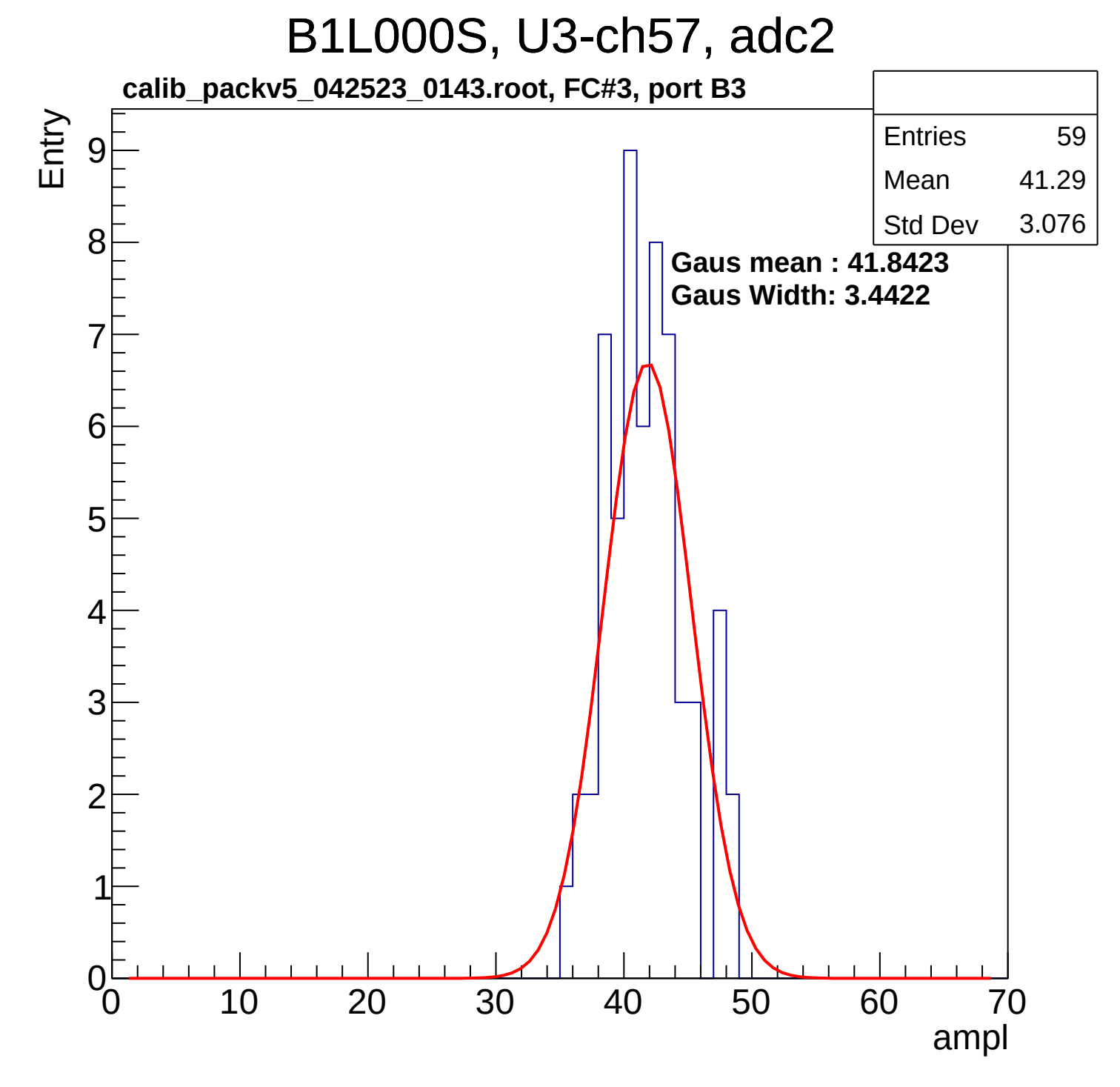
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch57, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 28.85
Std Dev 2.962
Gaus mean : 29.2551
Gaus Width: 3.5865
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch57, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 35.91
Std Dev 2.896
Gaus mean : 35.7138
Gaus Width: 2.7610
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch57, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 41.29
Std Dev 3.076
Gaus mean : 41.8423
Gaus Width: 3.4422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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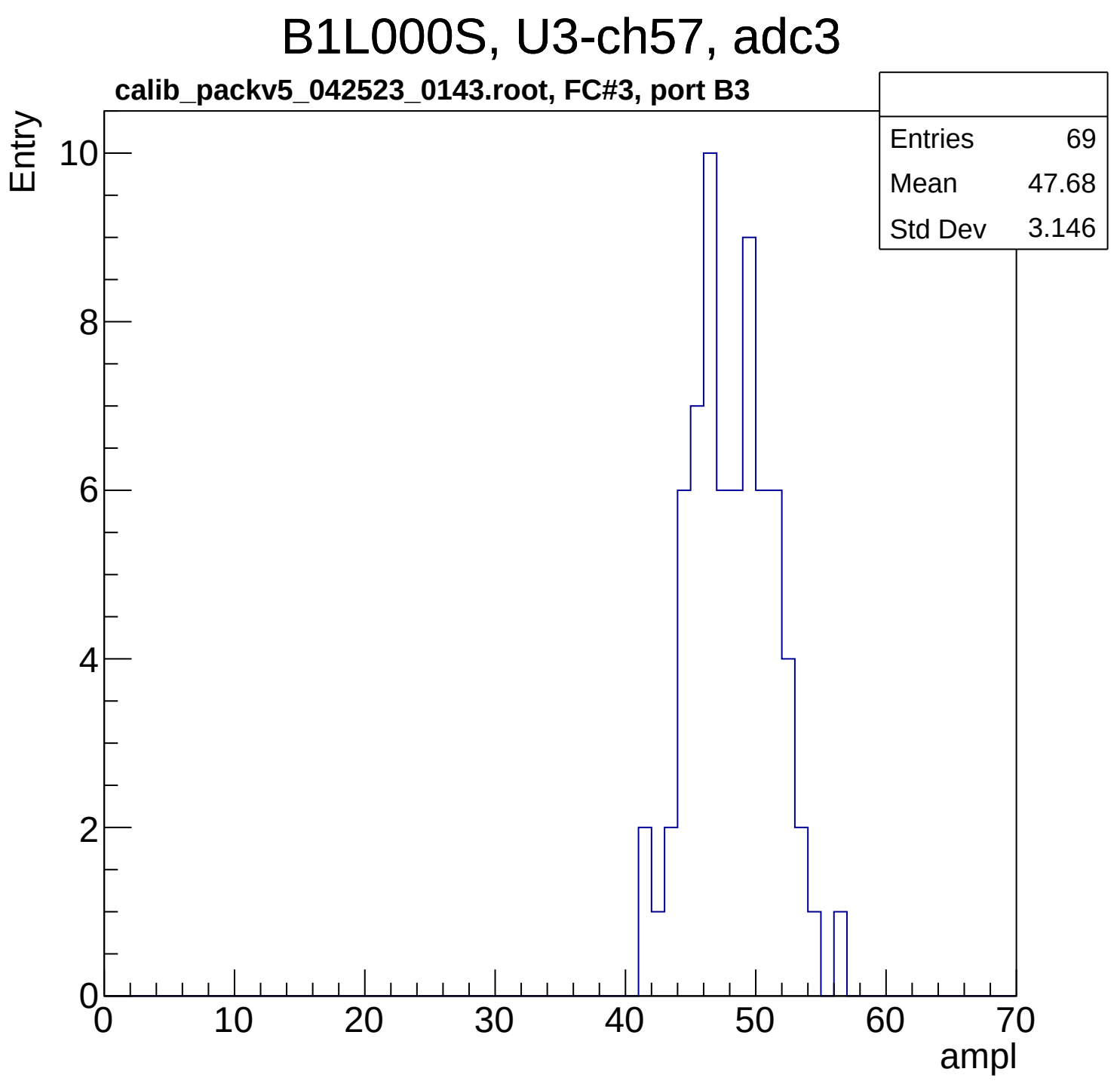
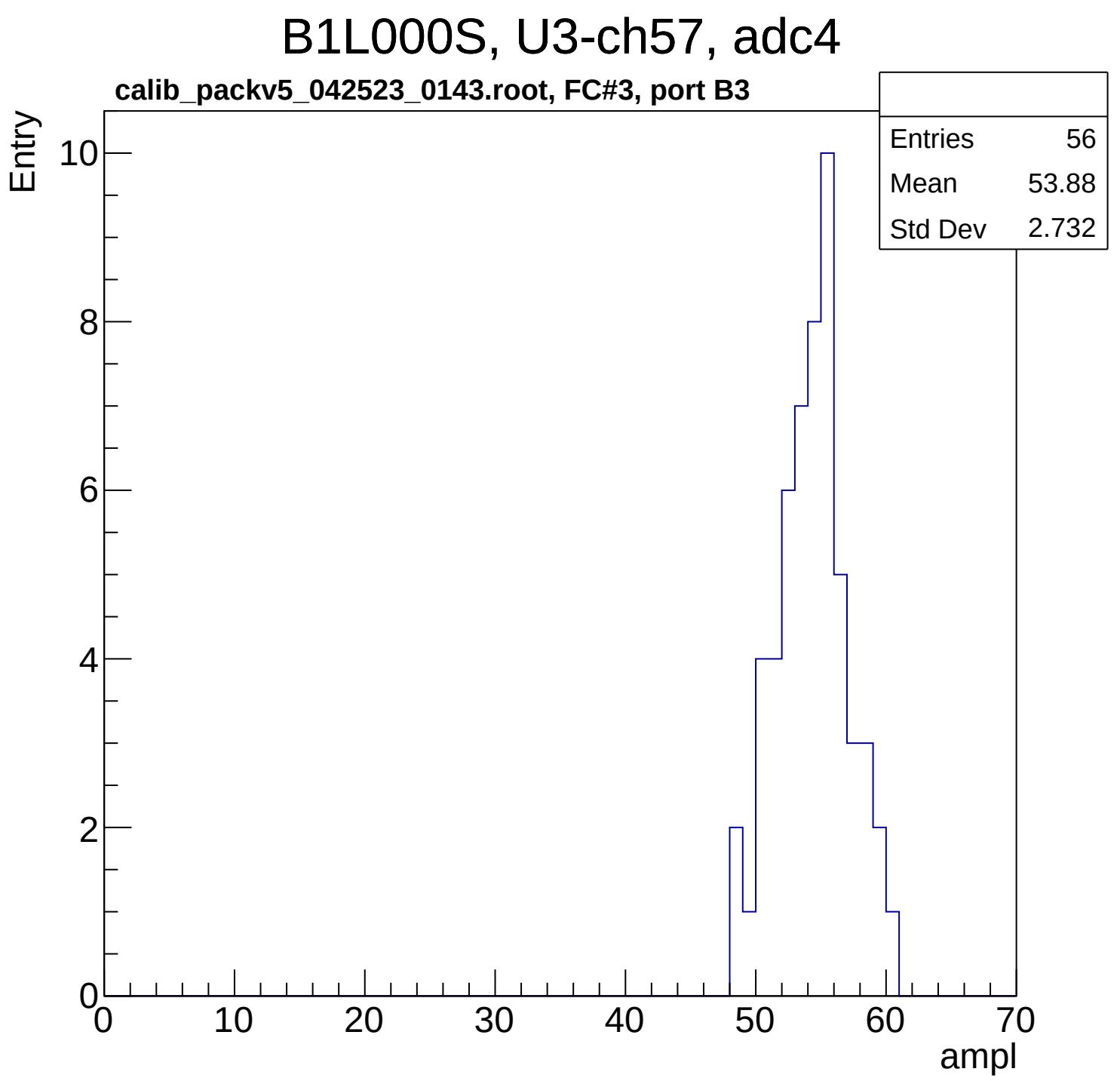
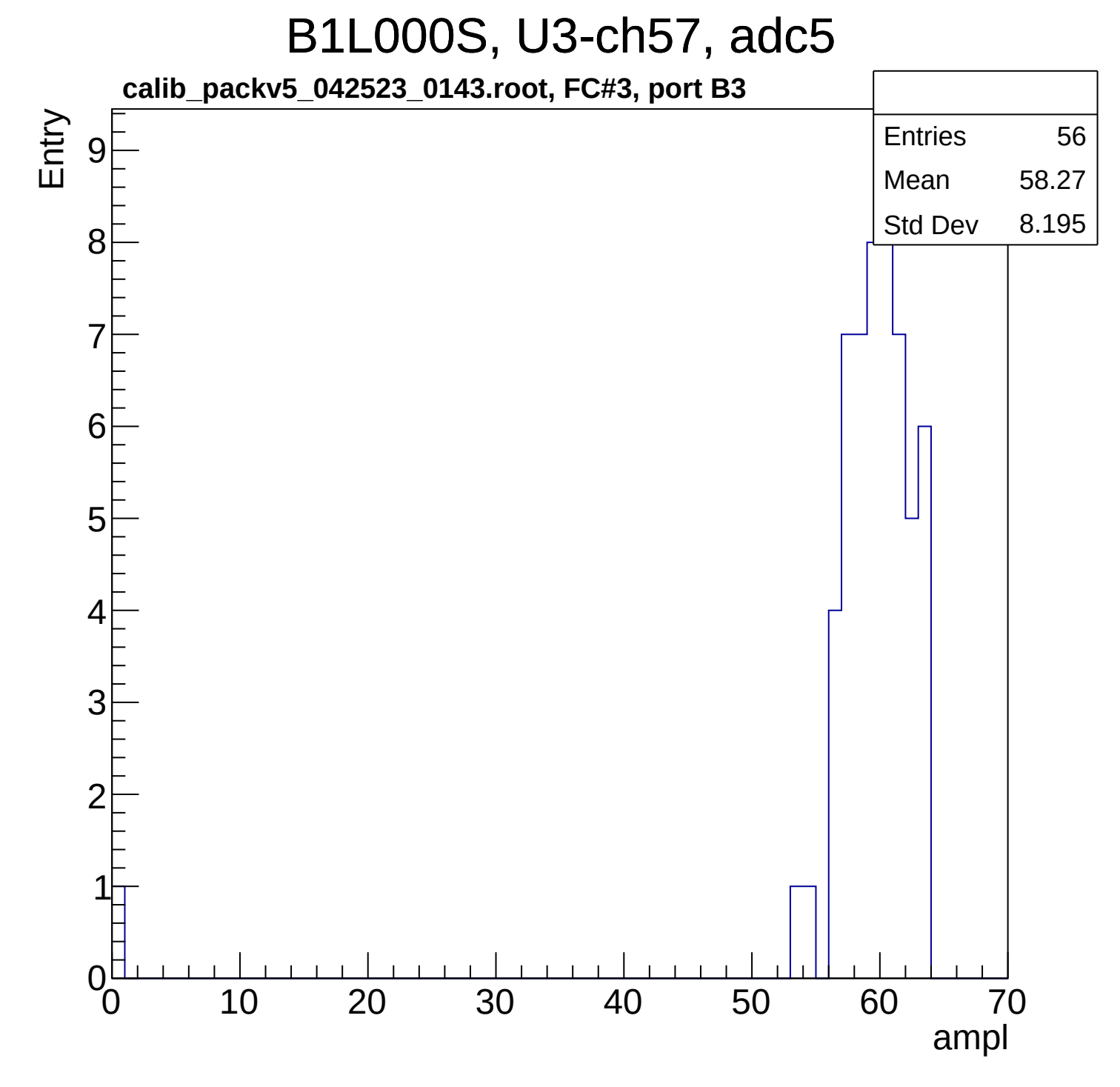

B1L000S, U3-ch57, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 47.68
Std Dev 3.146
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch57, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 53.88
Std Dev 2.732
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch57, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 58.27
Std Dev 8.195
Entry
ampl
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2
3
4
5
6
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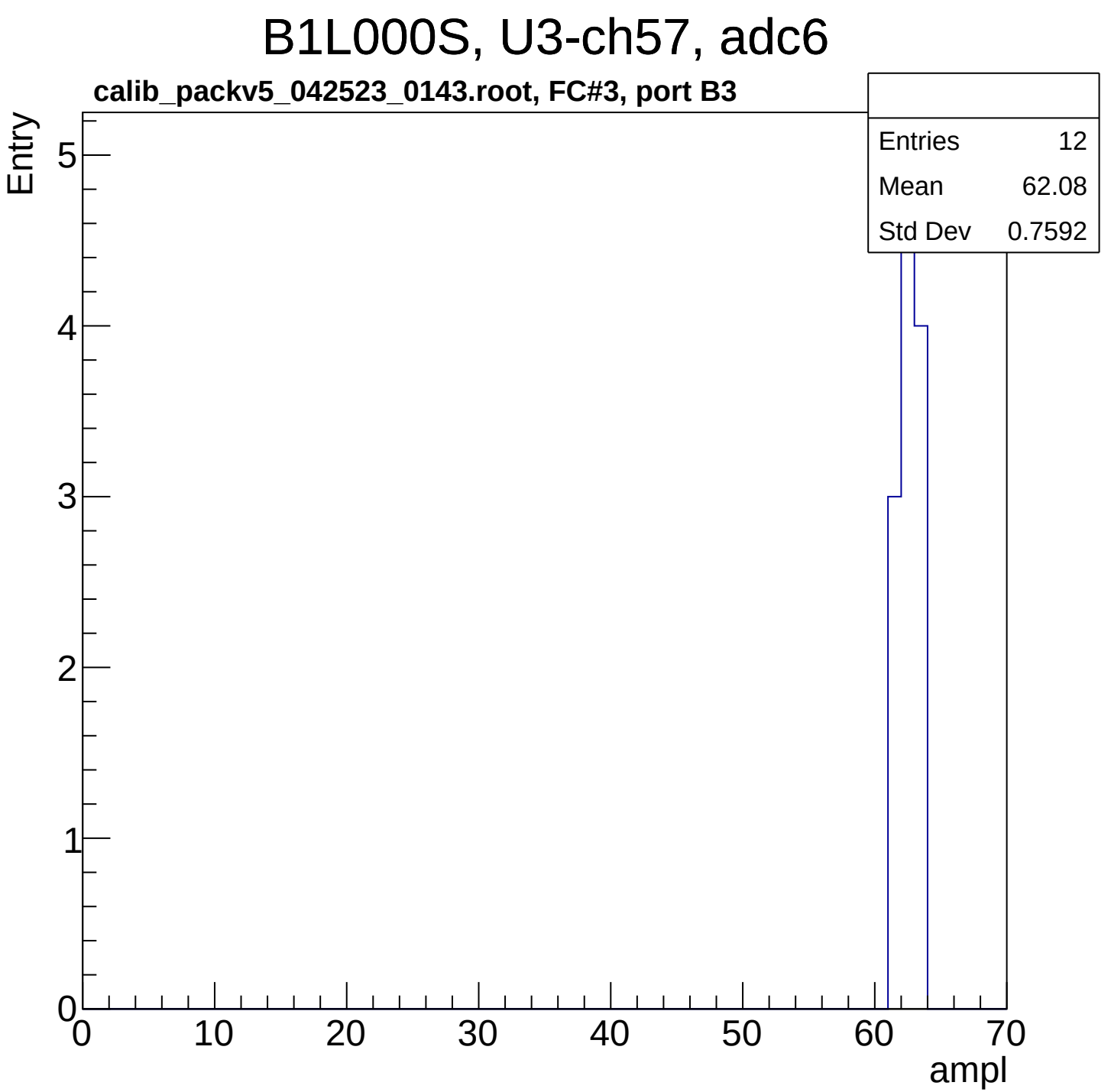
B1L000S, U3-ch57, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 62.08
Std Dev 0.7592
Entry
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3
4
5
0
10
20
30
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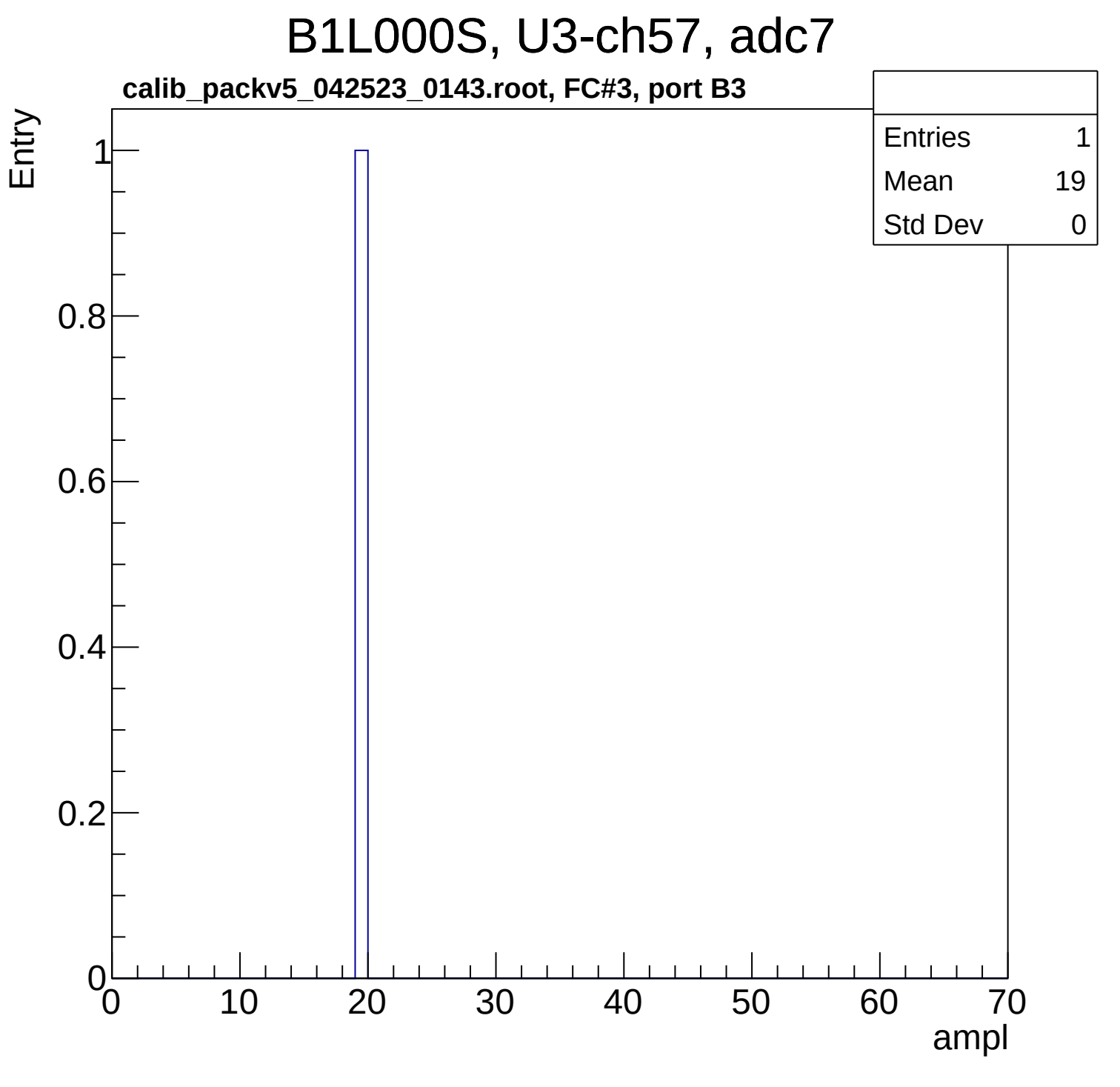
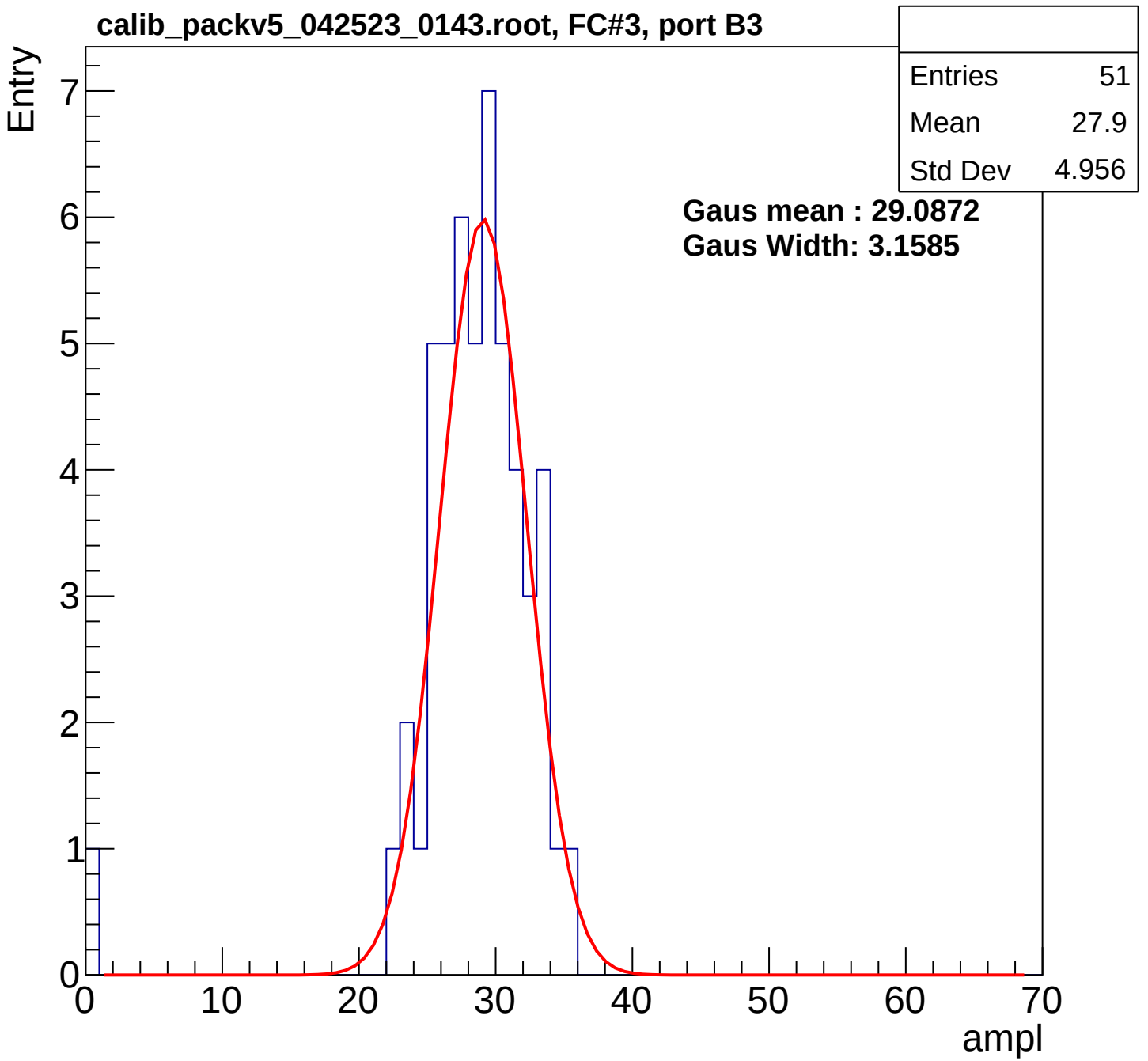
50
60
70
ampl


B1L000S, U3-ch57, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch58, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 27.9
Std Dev 4.956
Gaus mean : 29.0872
Gaus Width: 3.1585
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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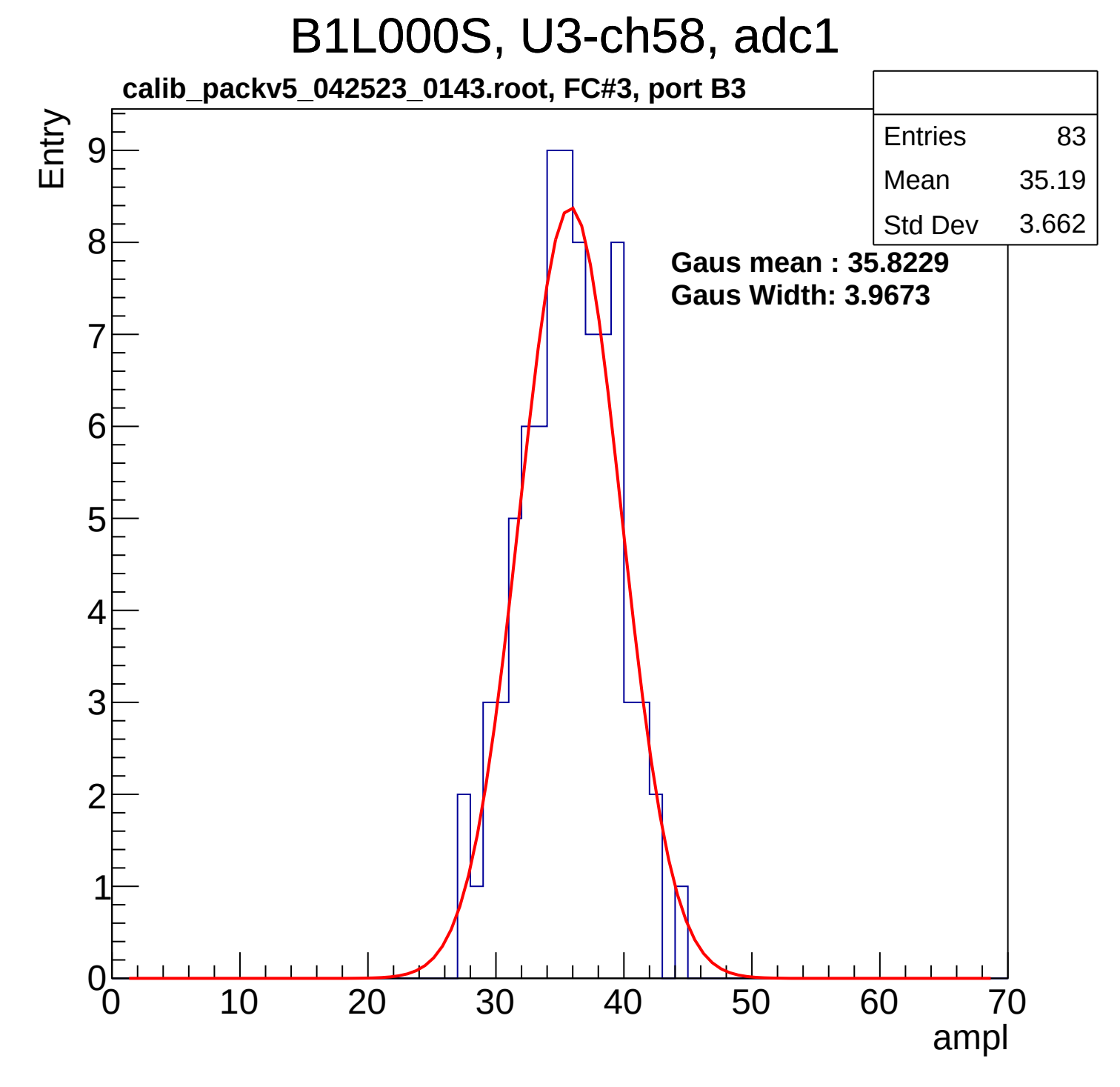
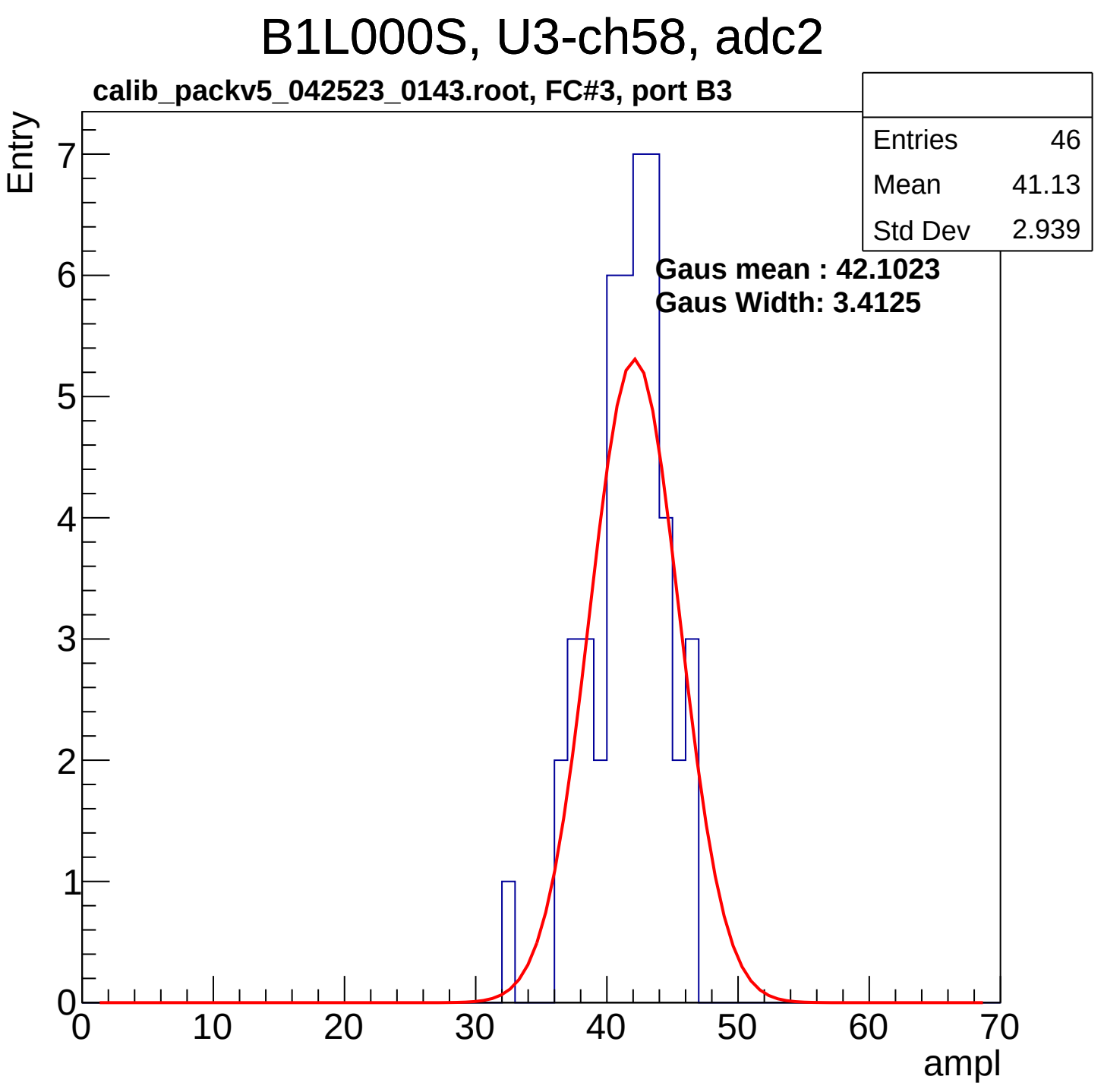

B1L000S, U3-ch58, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 35.19
Std Dev 3.662
Gaus mean : 35.8229
Gaus Width: 3.9673
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch58, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 41.13
Std Dev 2.939
Gaus mean : 42.1023
Gaus Width: 3.4125
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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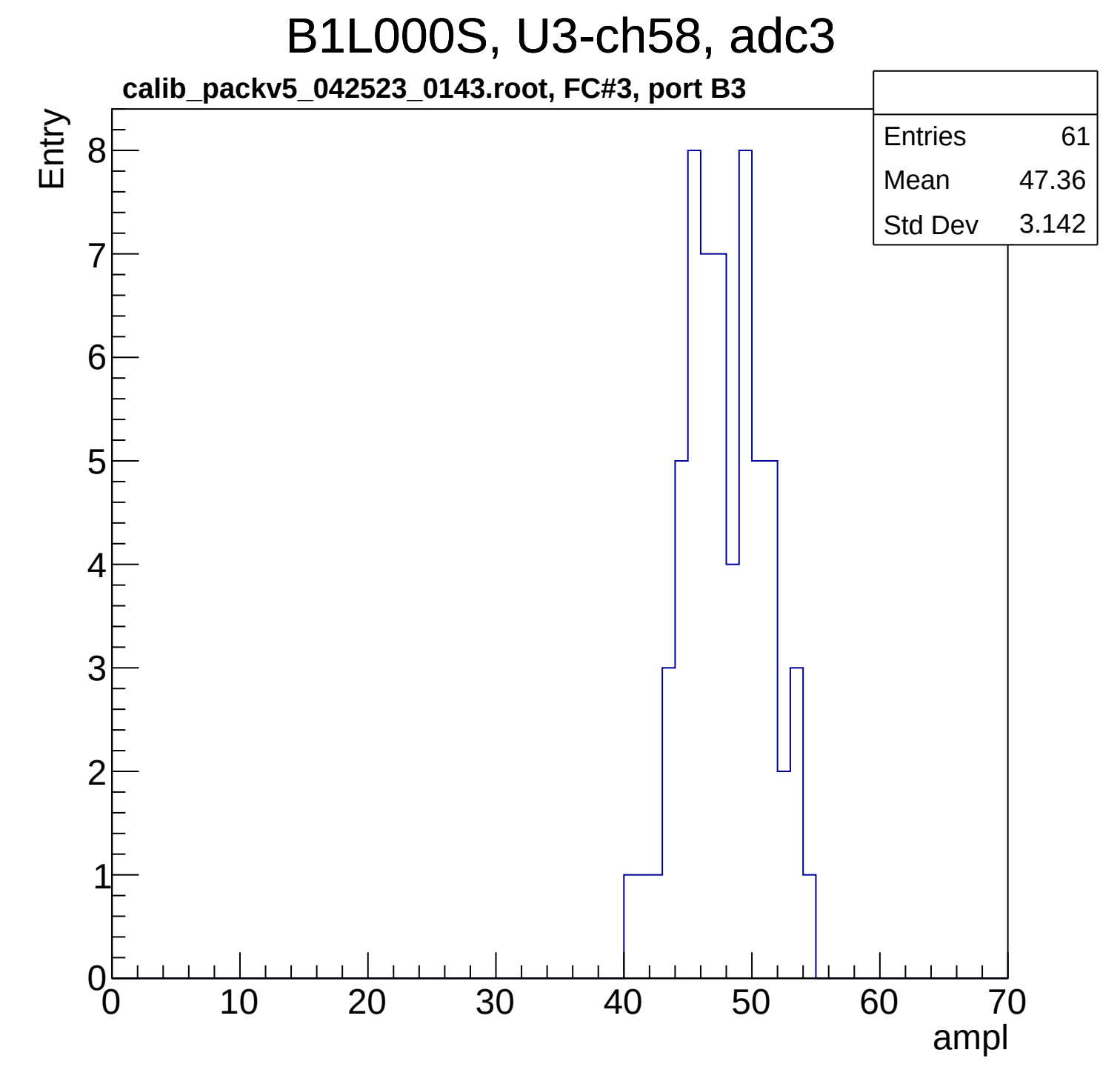
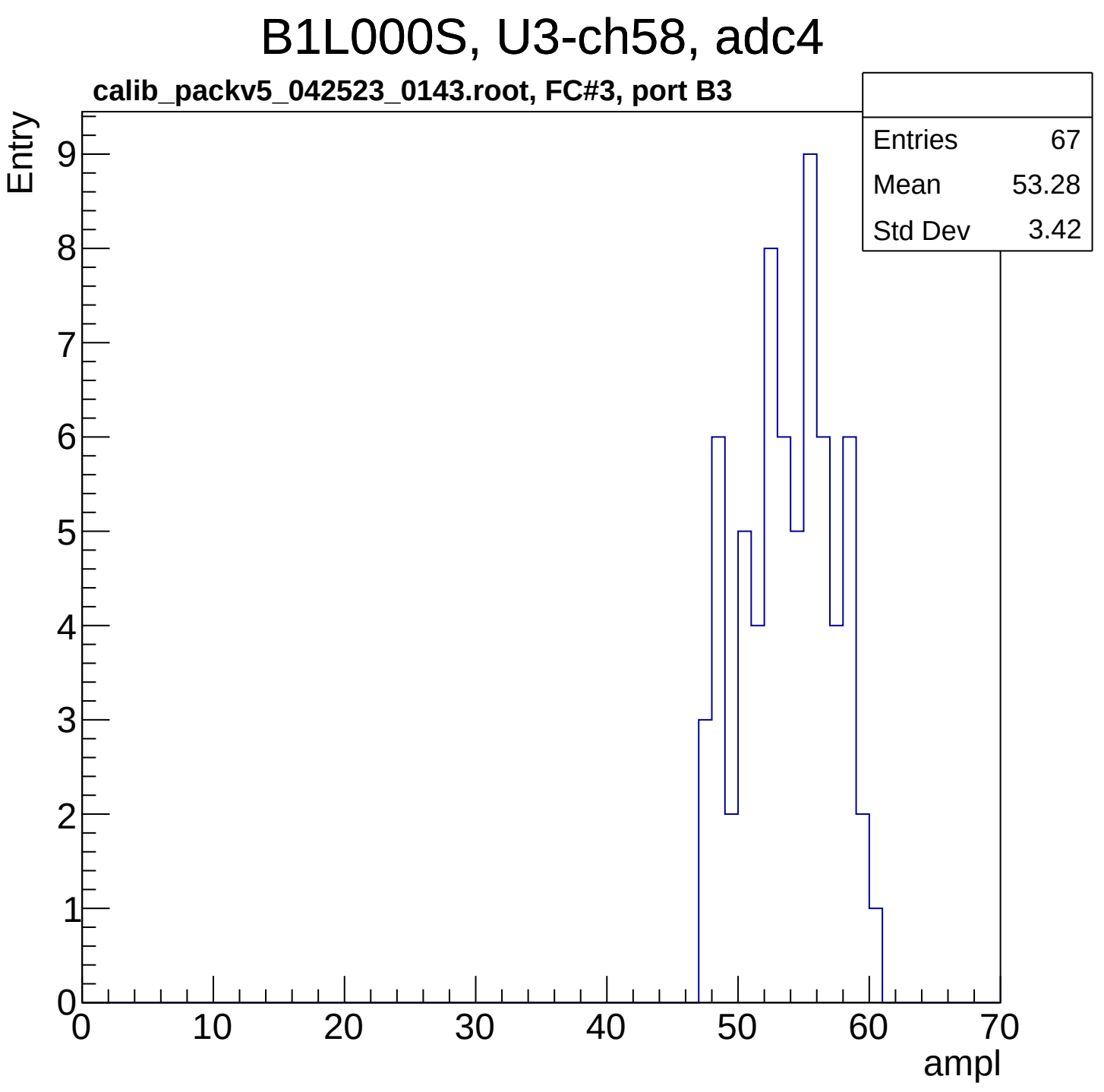
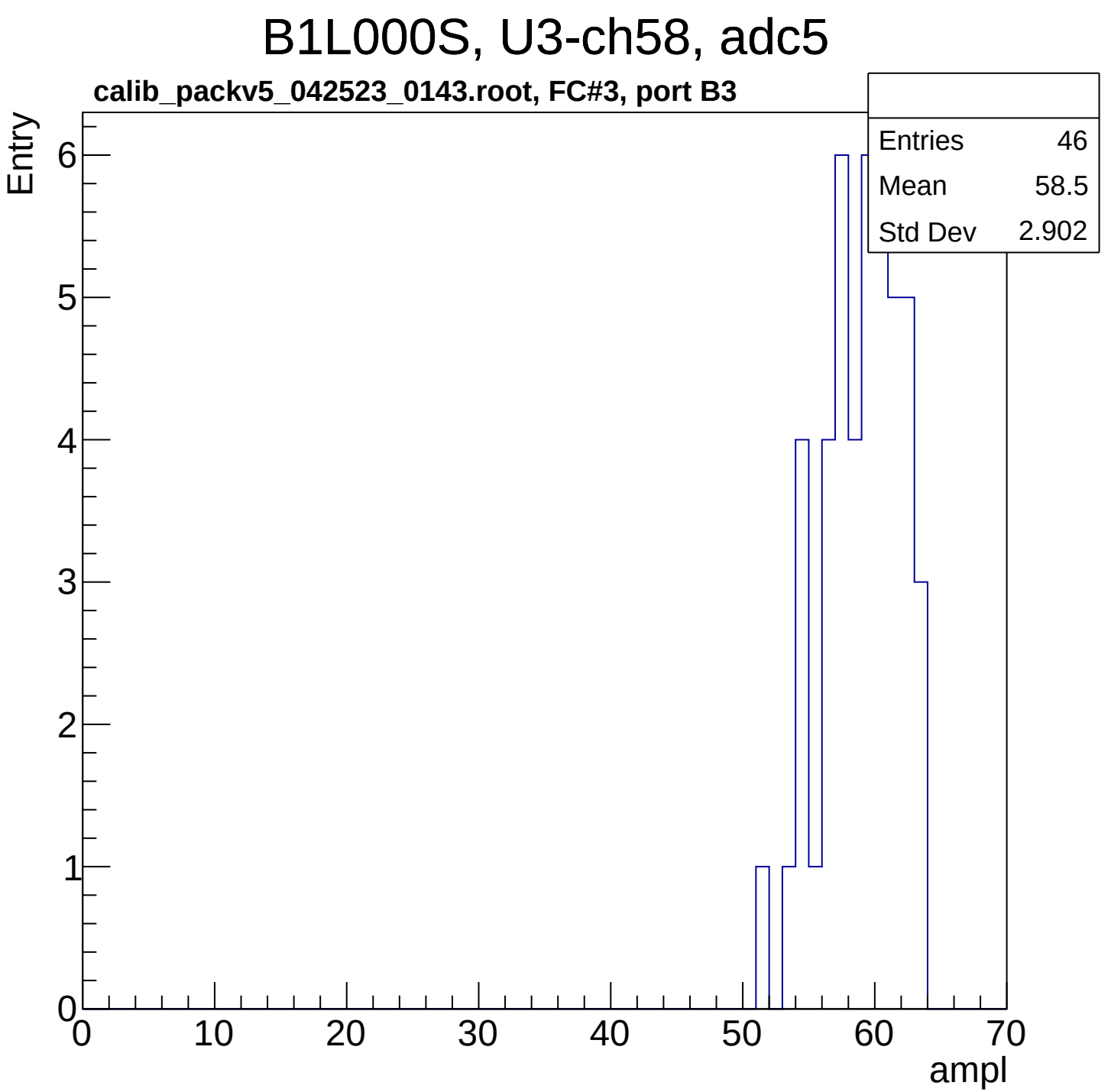
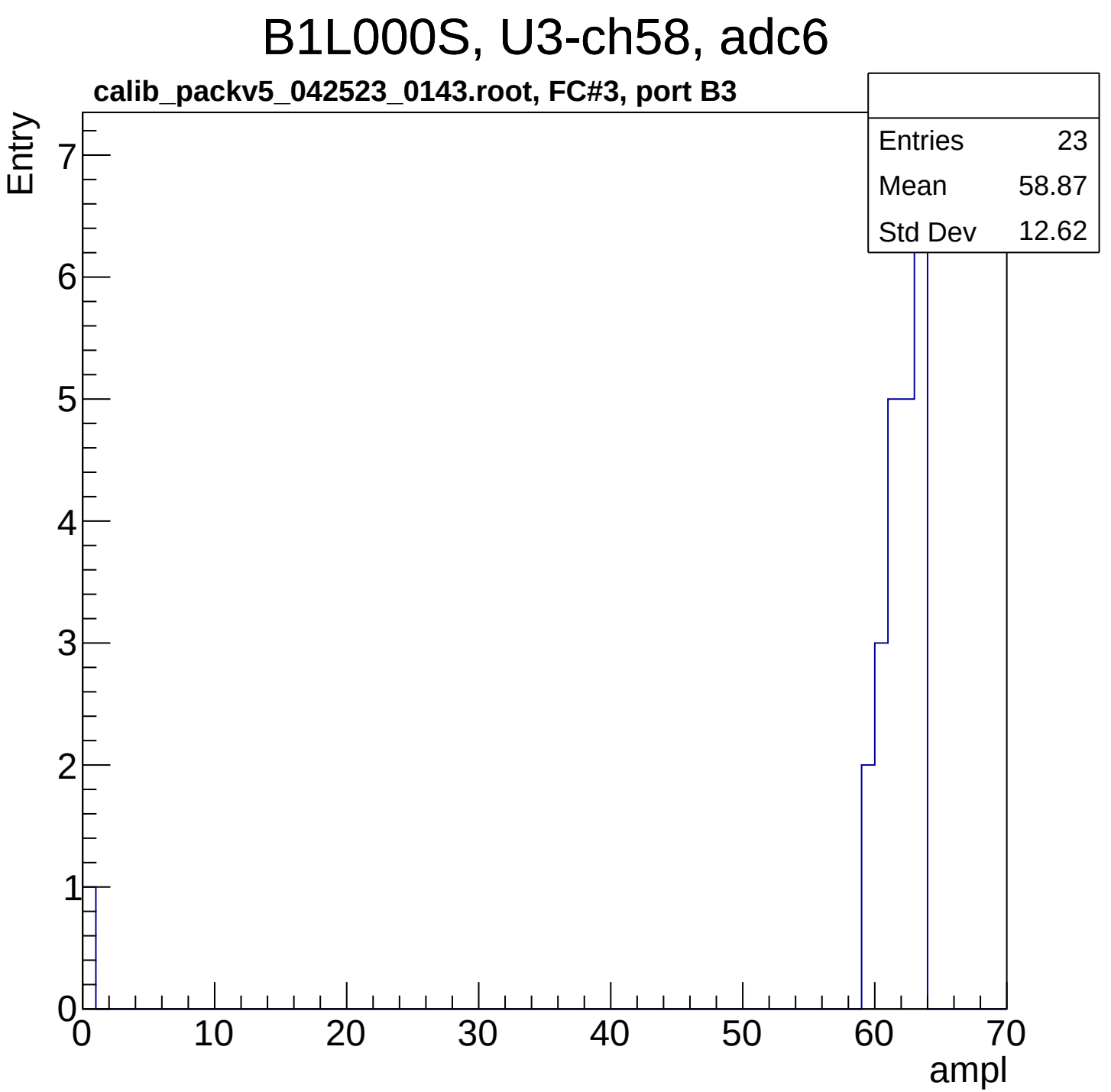
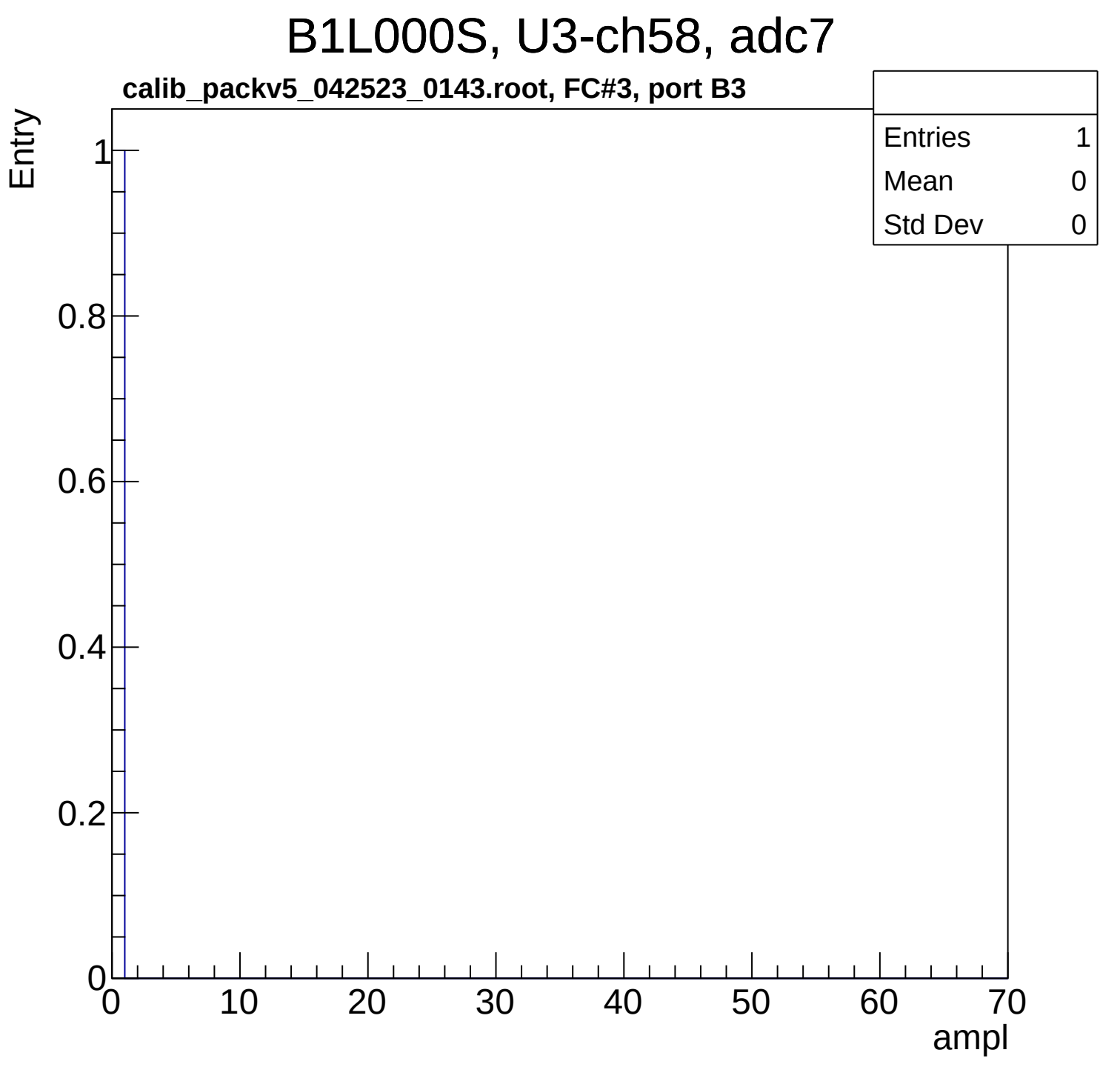
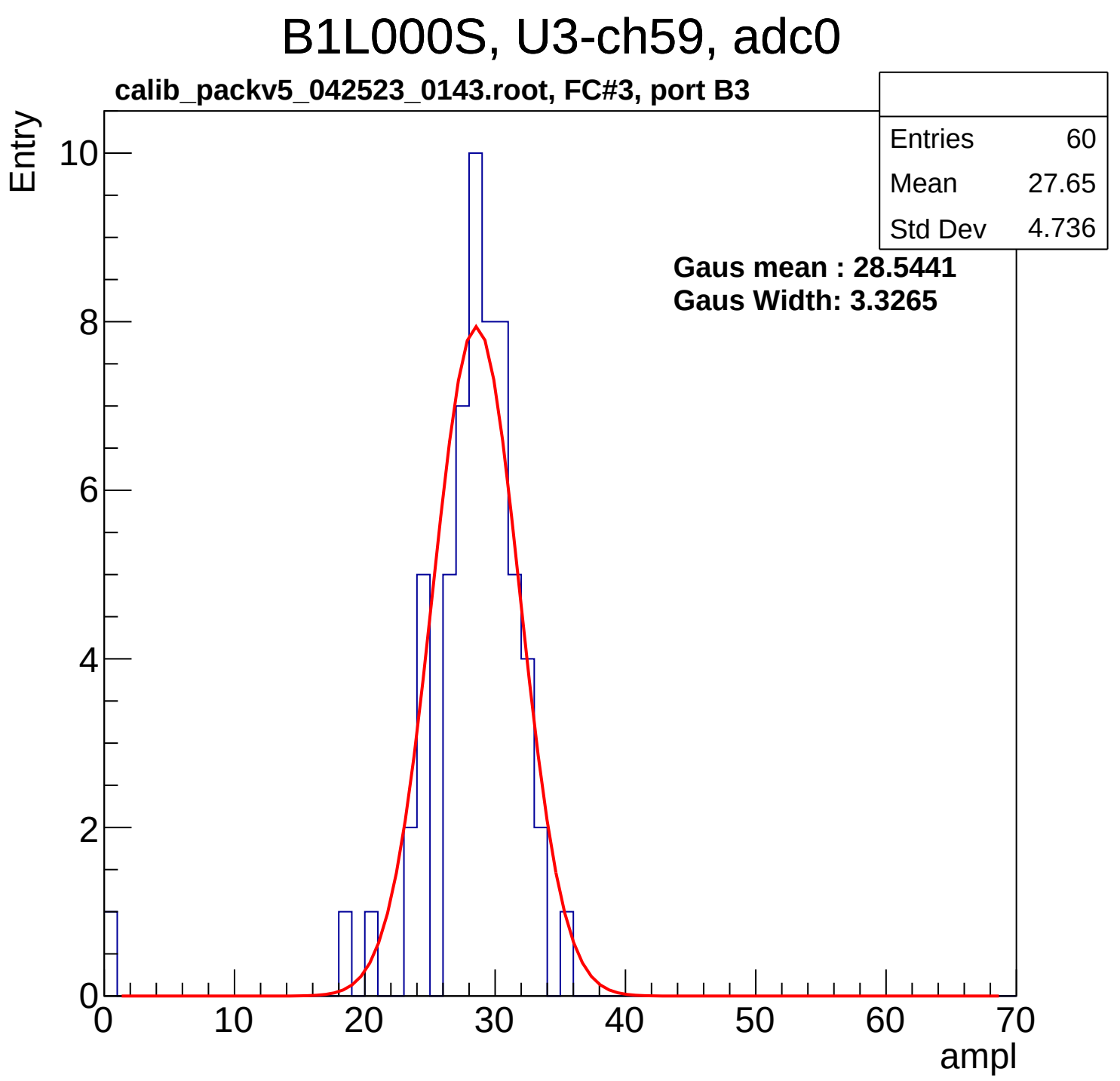

B1L000S, U3-ch58, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 47.36
Std Dev 3.142
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

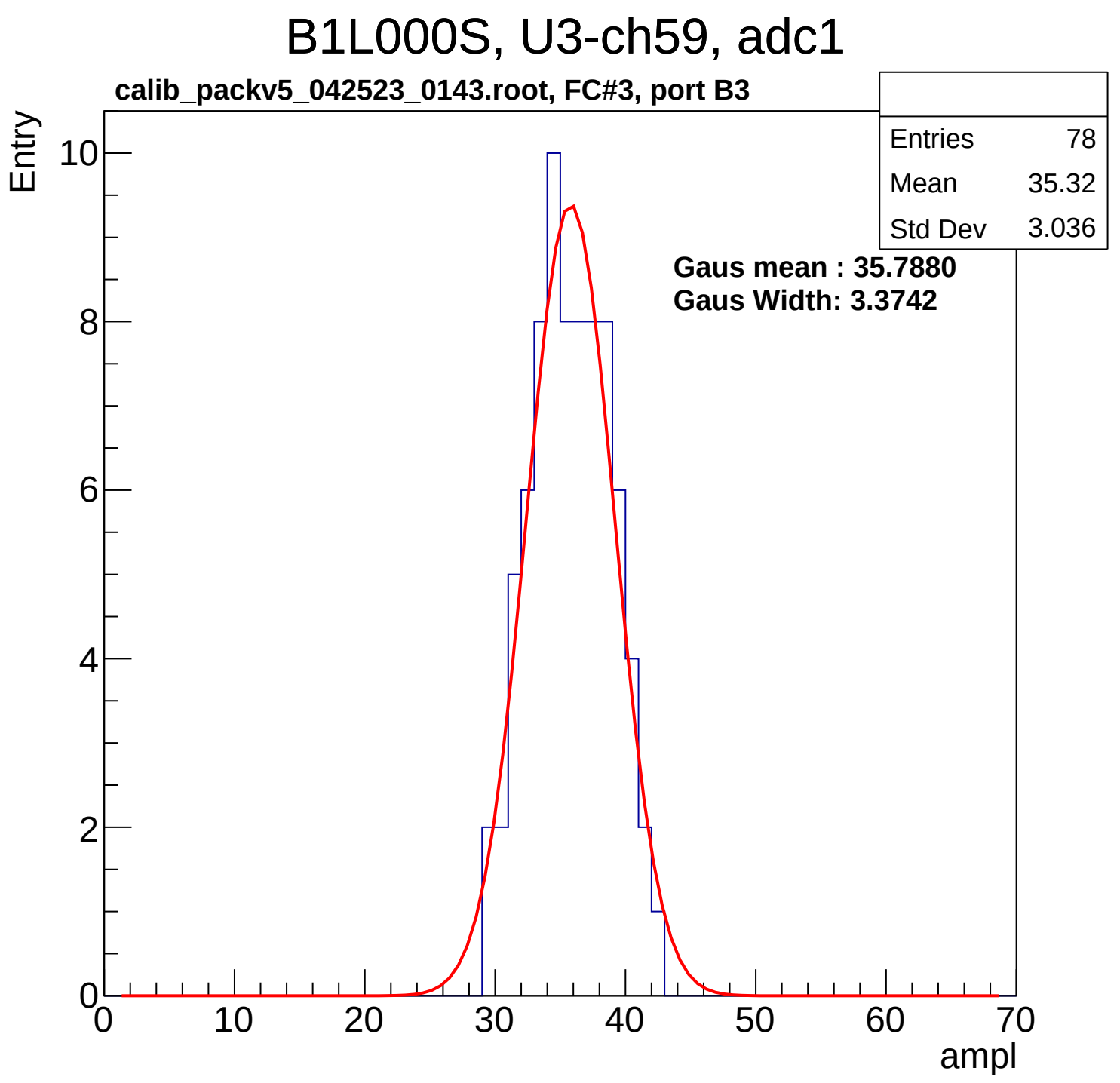

B1L000S, U3-ch58, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 53.28
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

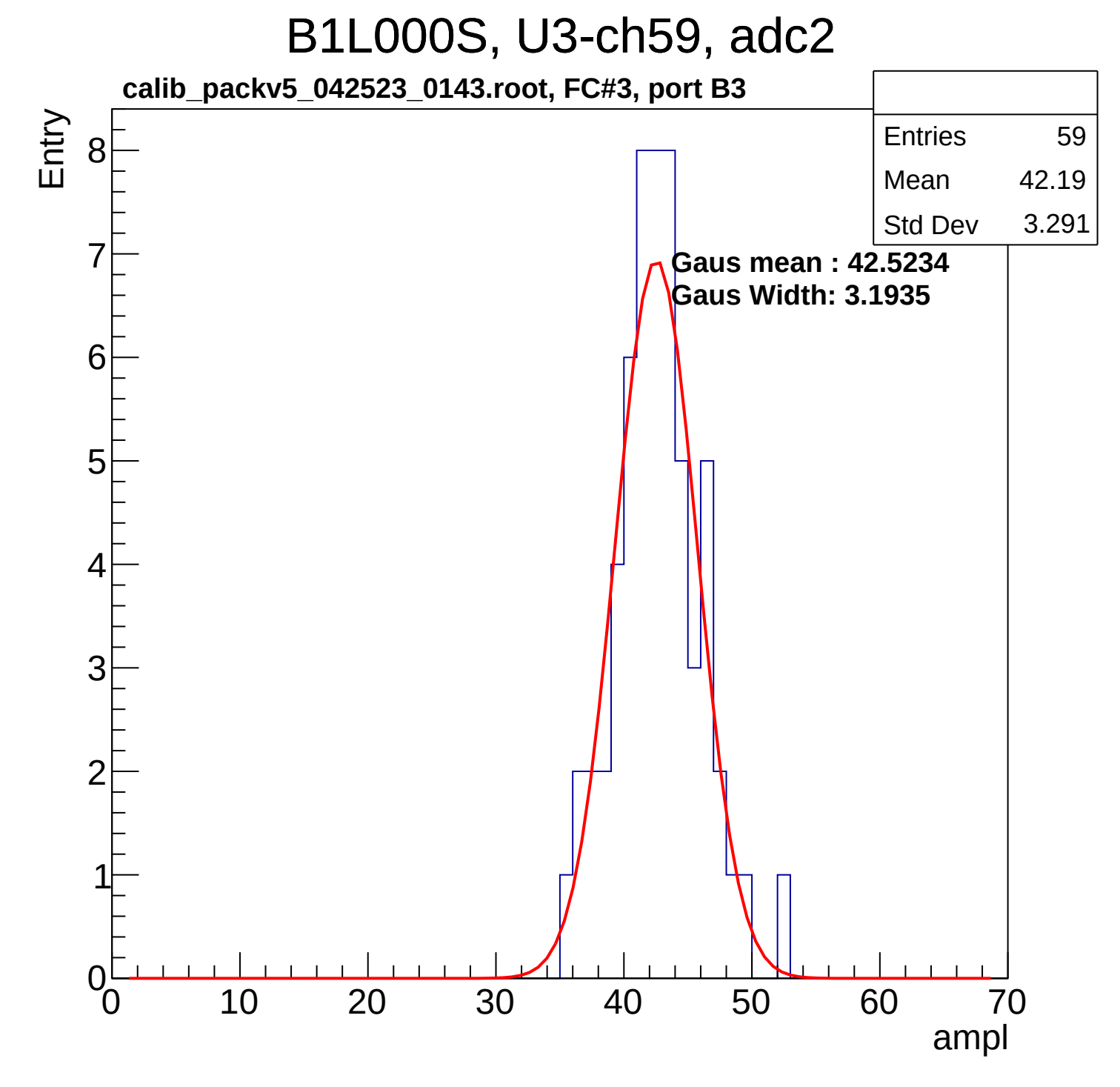

B1L000S, U3-ch58, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 58.5
Std Dev 2.902
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch58, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 58.87
Std Dev 12.62
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch58, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch59, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 27.65
Std Dev 4.736
Gaus mean : 28.5441
Gaus Width: 3.3265
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch59, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 35.32
Std Dev 3.036
Gaus mean : 35.7880
Gaus Width: 3.3742
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch59, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 42.19
Std Dev 3.291
Gaus mean : 42.5234
Gaus Width: 3.1935
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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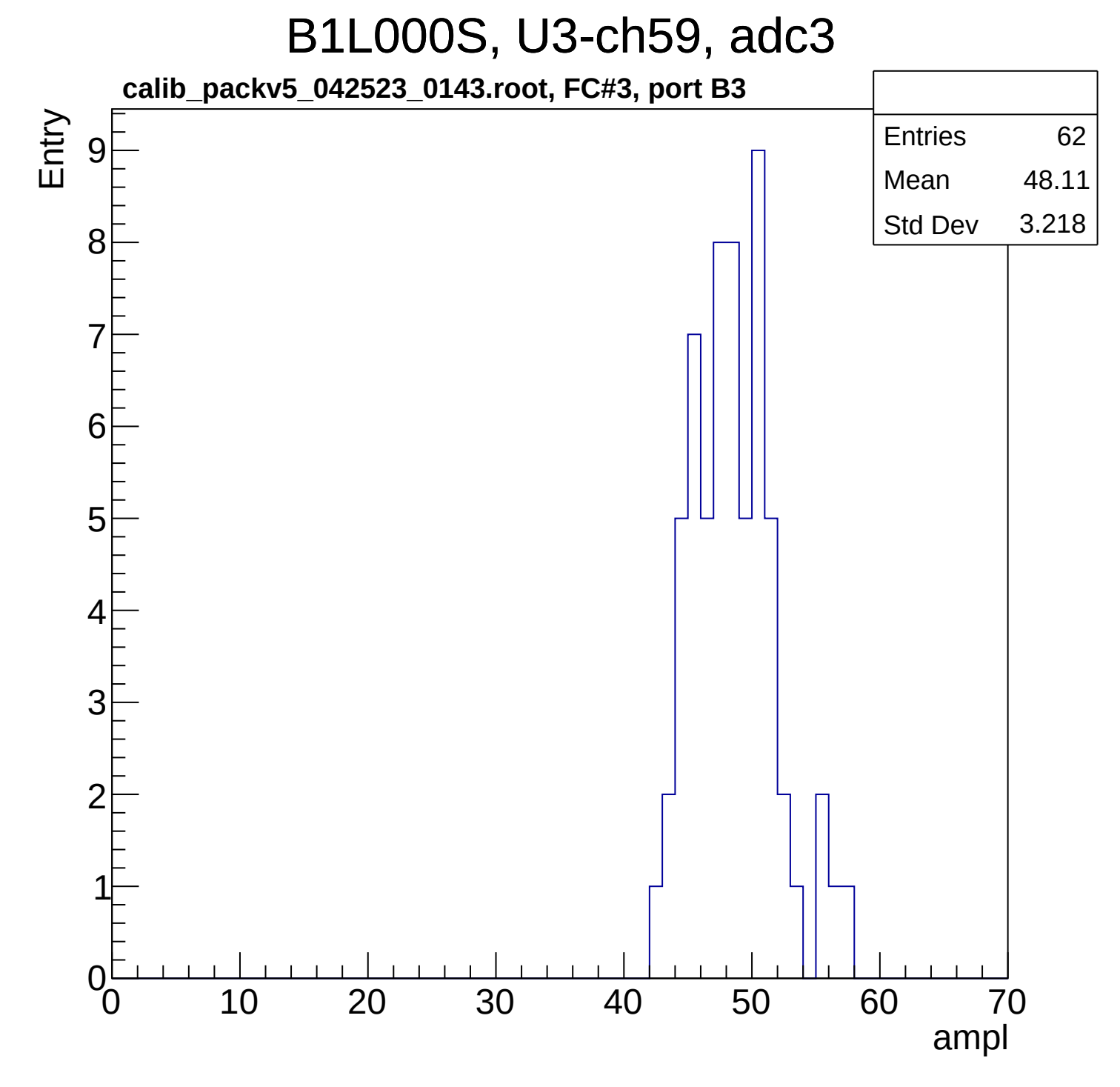
B1L000S, U3-ch59, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 48.11
Std Dev 3.218
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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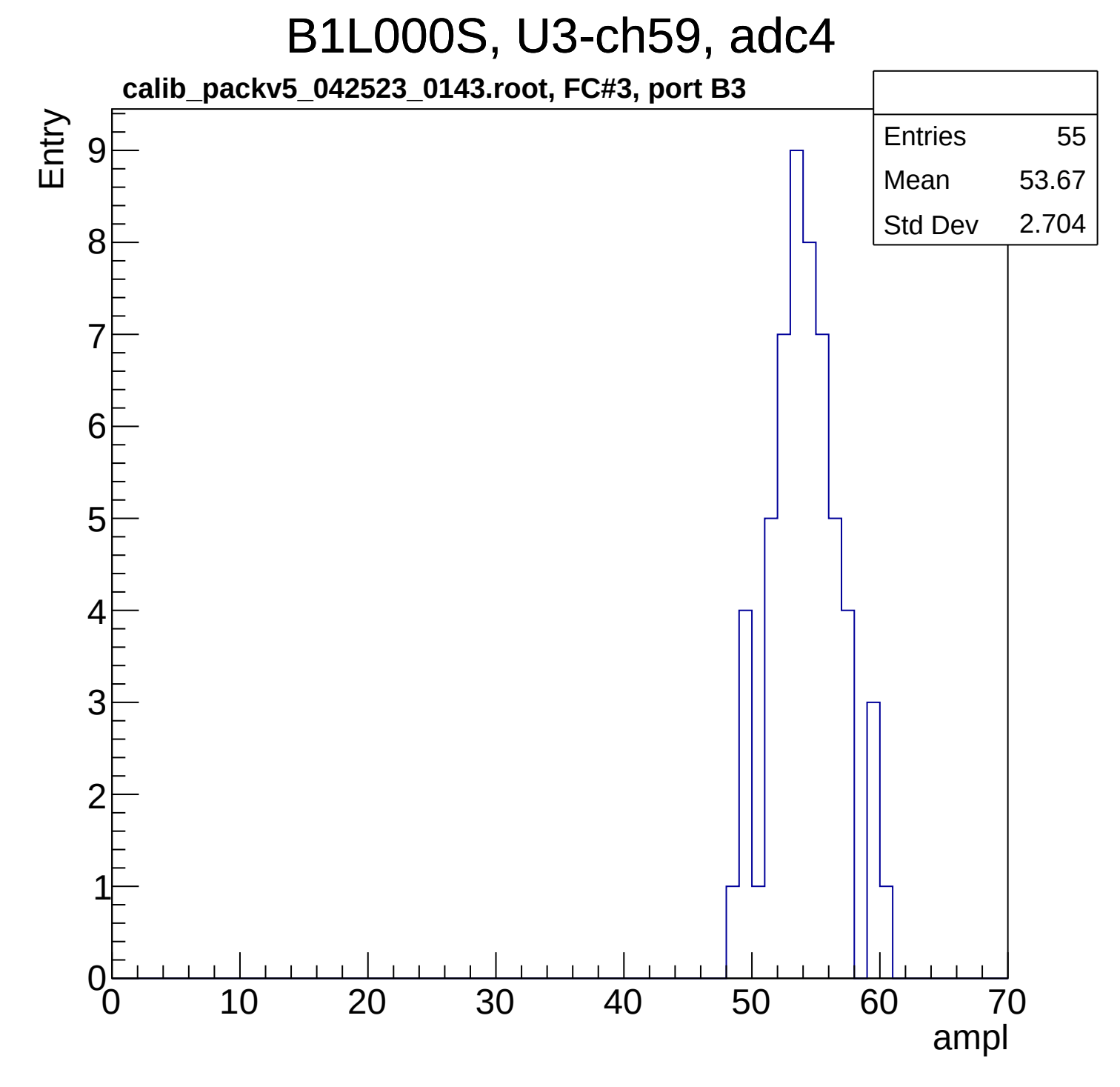
B1L000S, U3-ch59, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.67
Std Dev 2.704
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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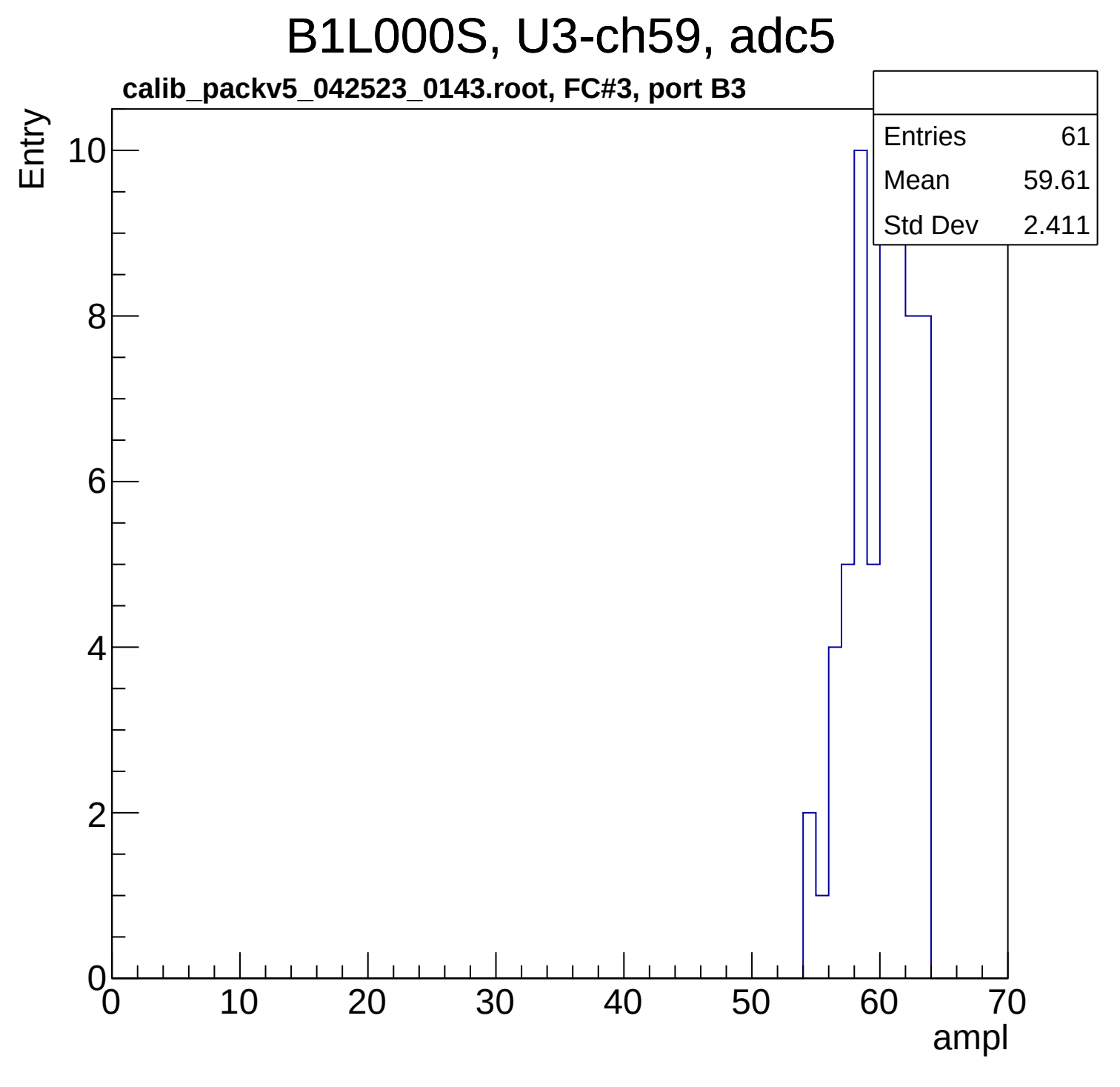
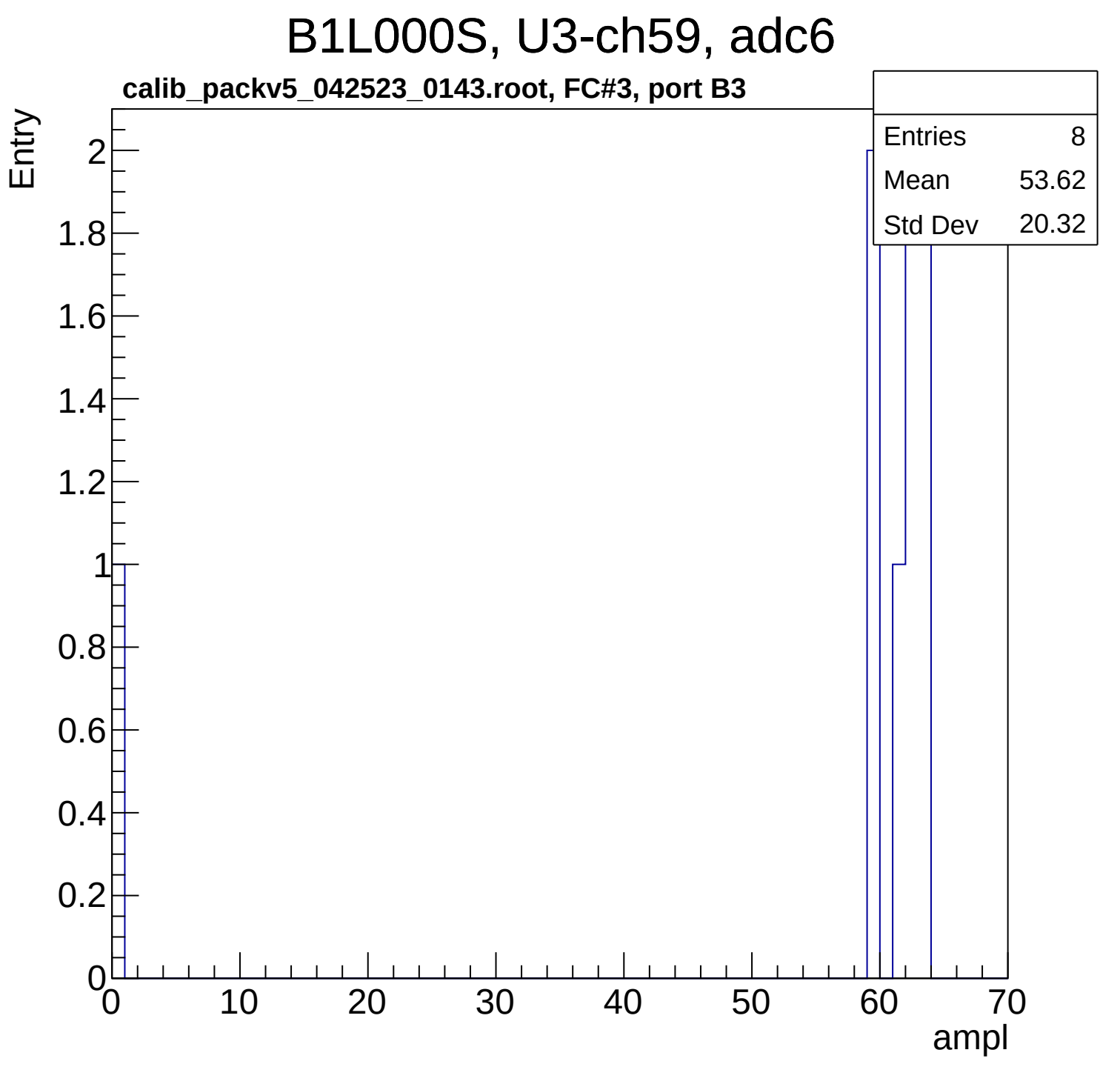
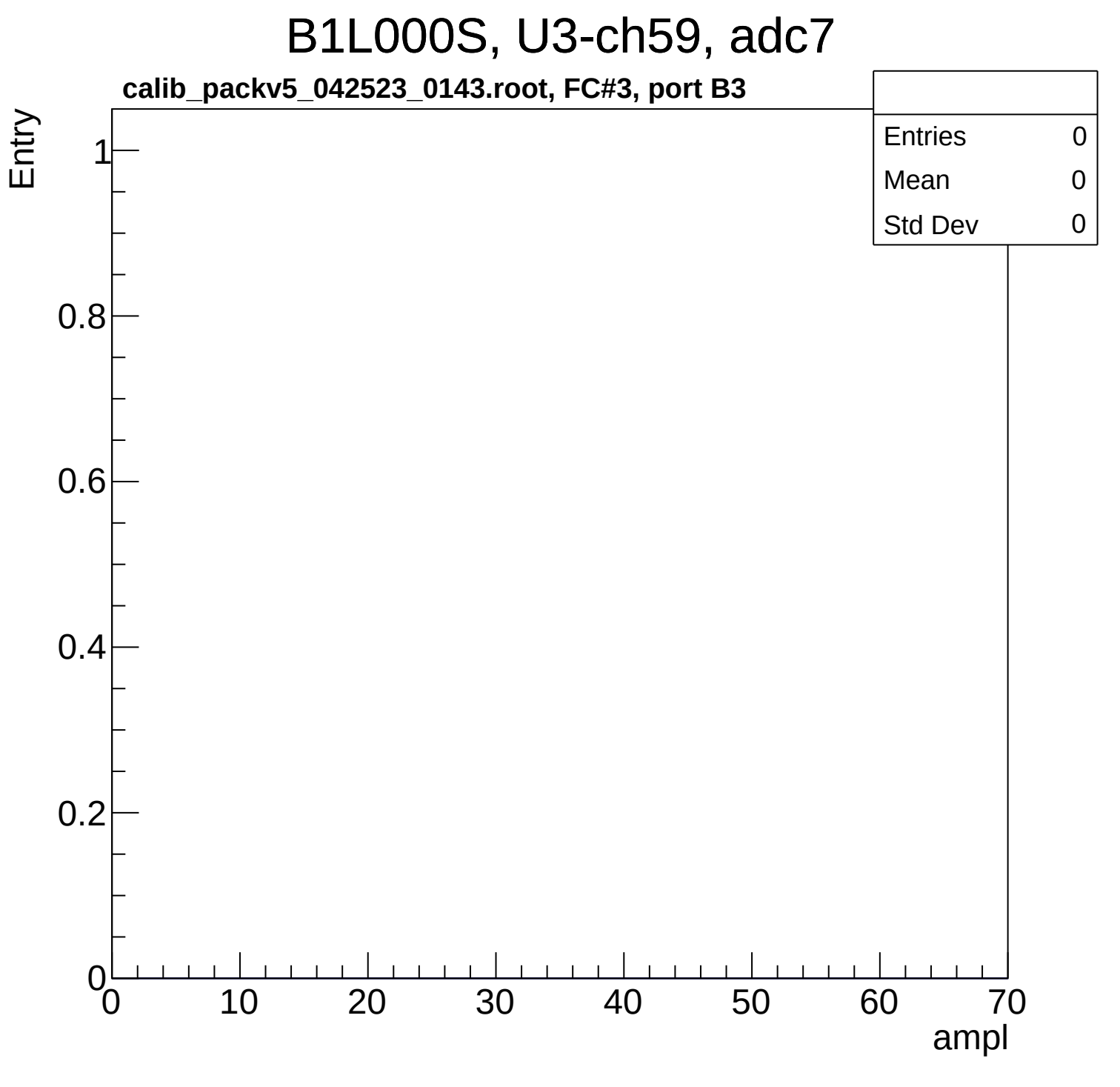
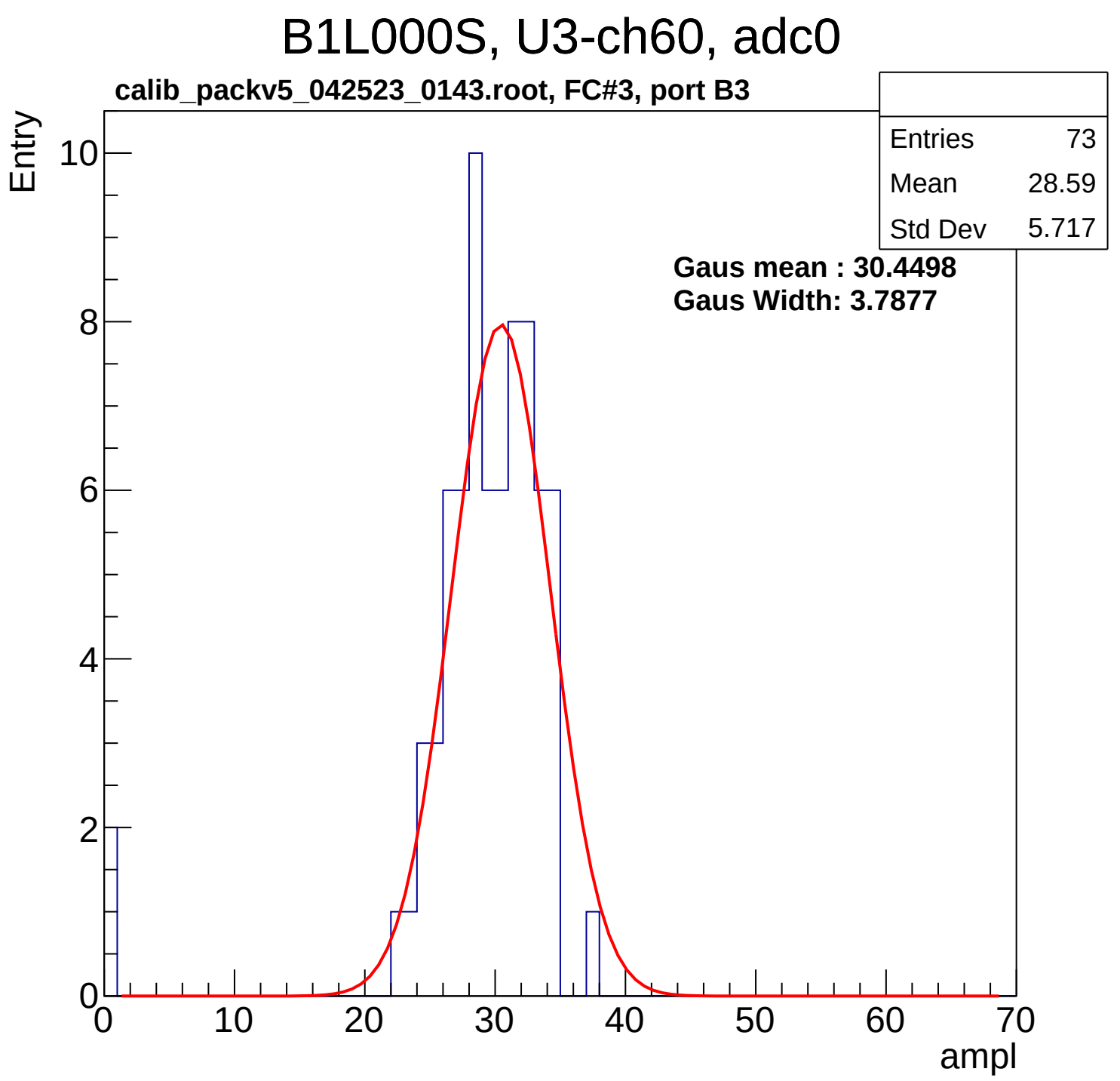
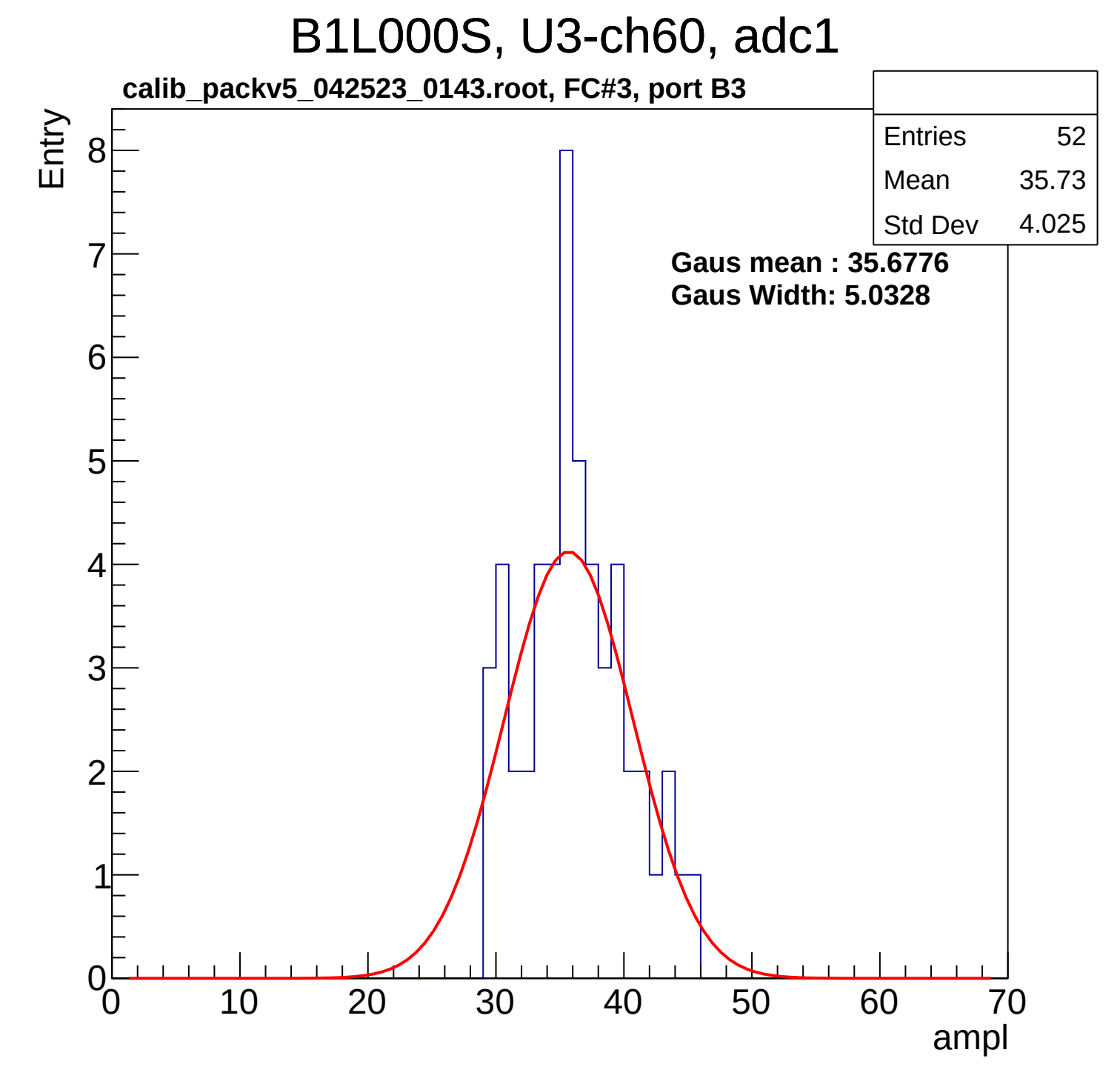

B1L000S, U3-ch59, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 59.61
Std Dev 2.411
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch59, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 53.62
Std Dev 20.32
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch59, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch60, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 28.59
Std Dev 5.717
Gaus mean : 30.4498
Gaus Width: 3.7877
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch60, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 35.73
Std Dev 4.025
Gaus mean : 35.6776
Gaus Width: 5.0328
Entry
ampl
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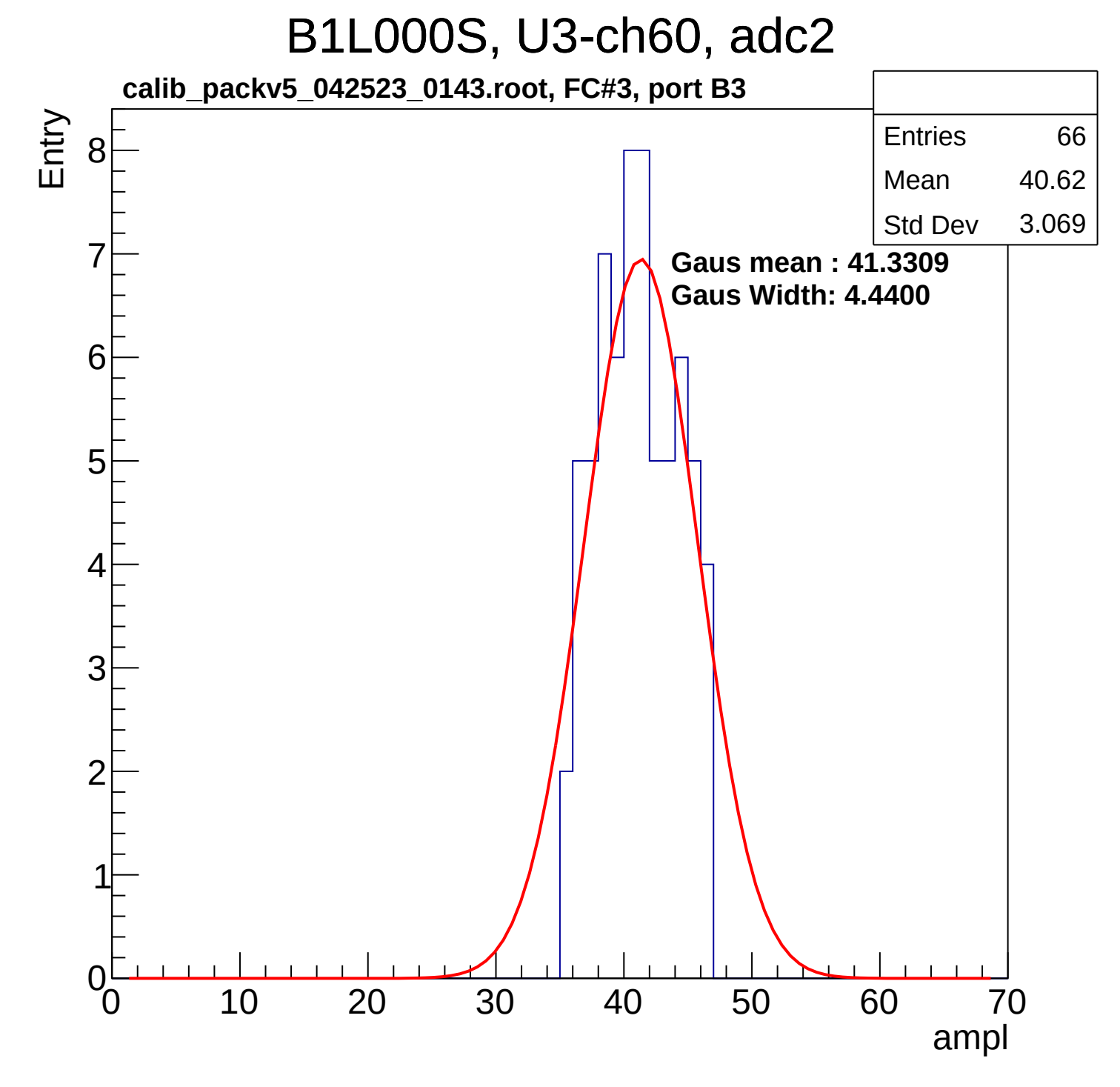
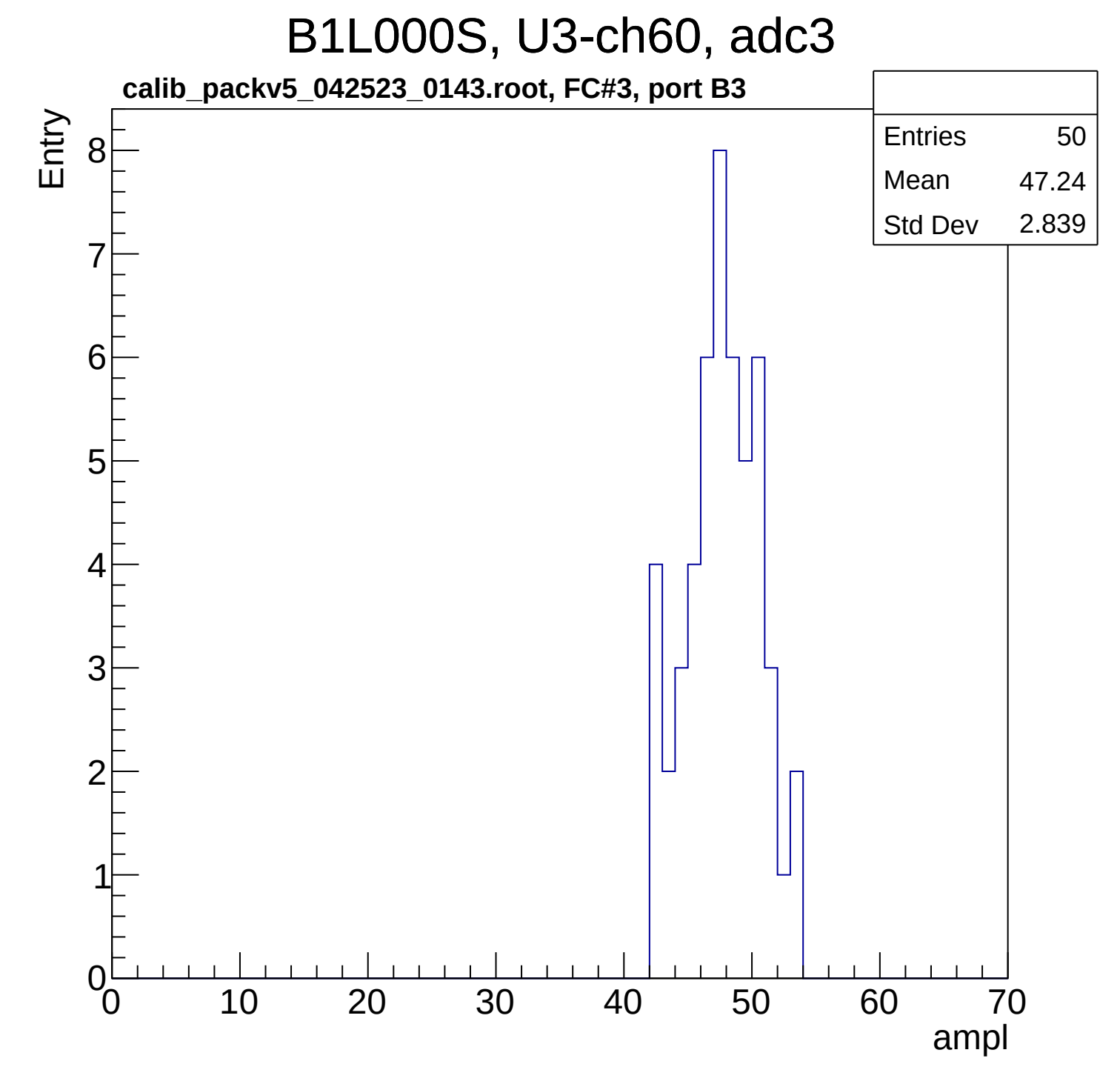
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch60, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 40.62
Std Dev 3.069
Gaus mean : 41.3309
Gaus Width: 4.4400
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch60, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 47.24
Std Dev 2.839
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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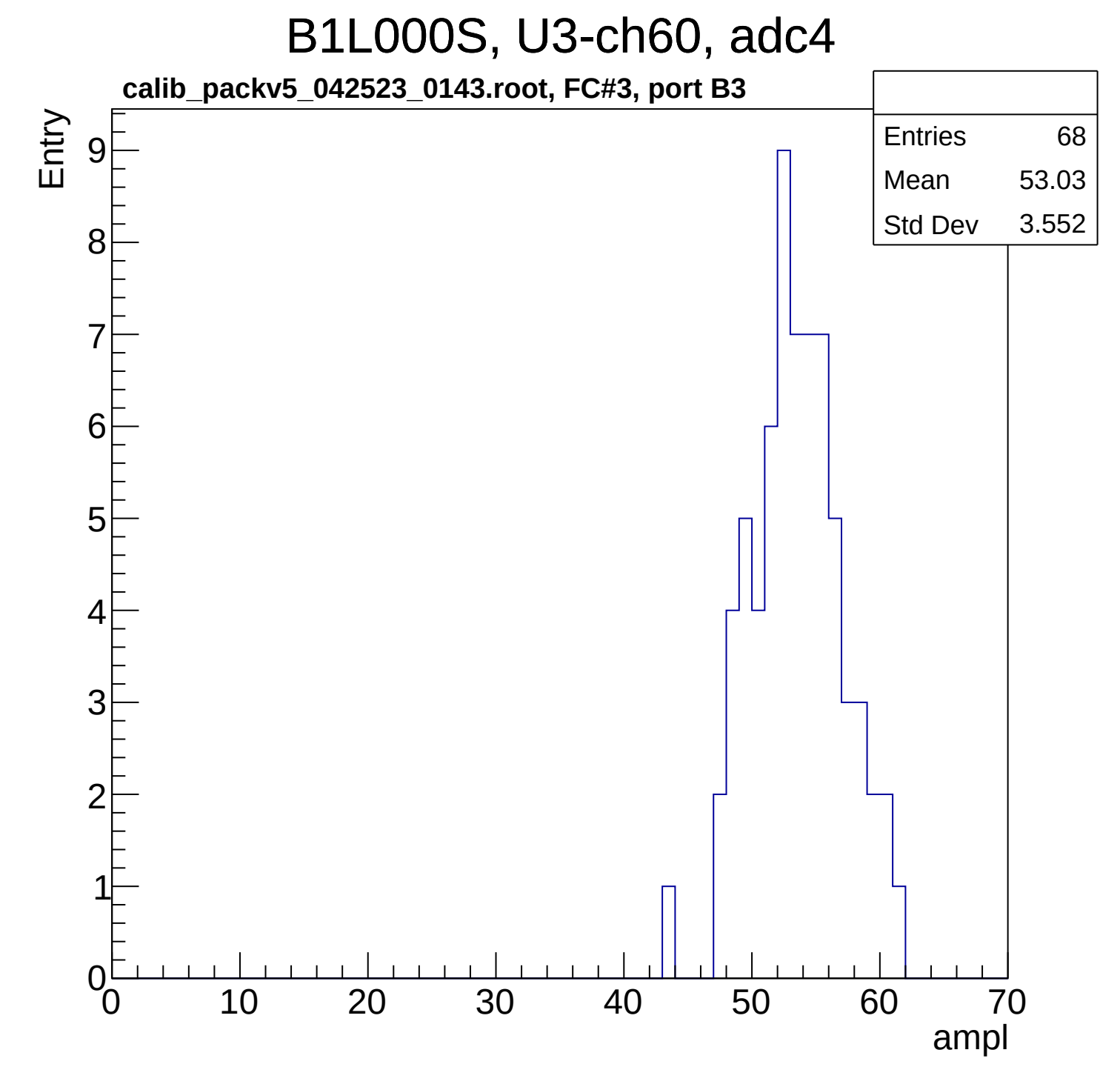
B1L000S, U3-ch60, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 53.03
Std Dev 3.552
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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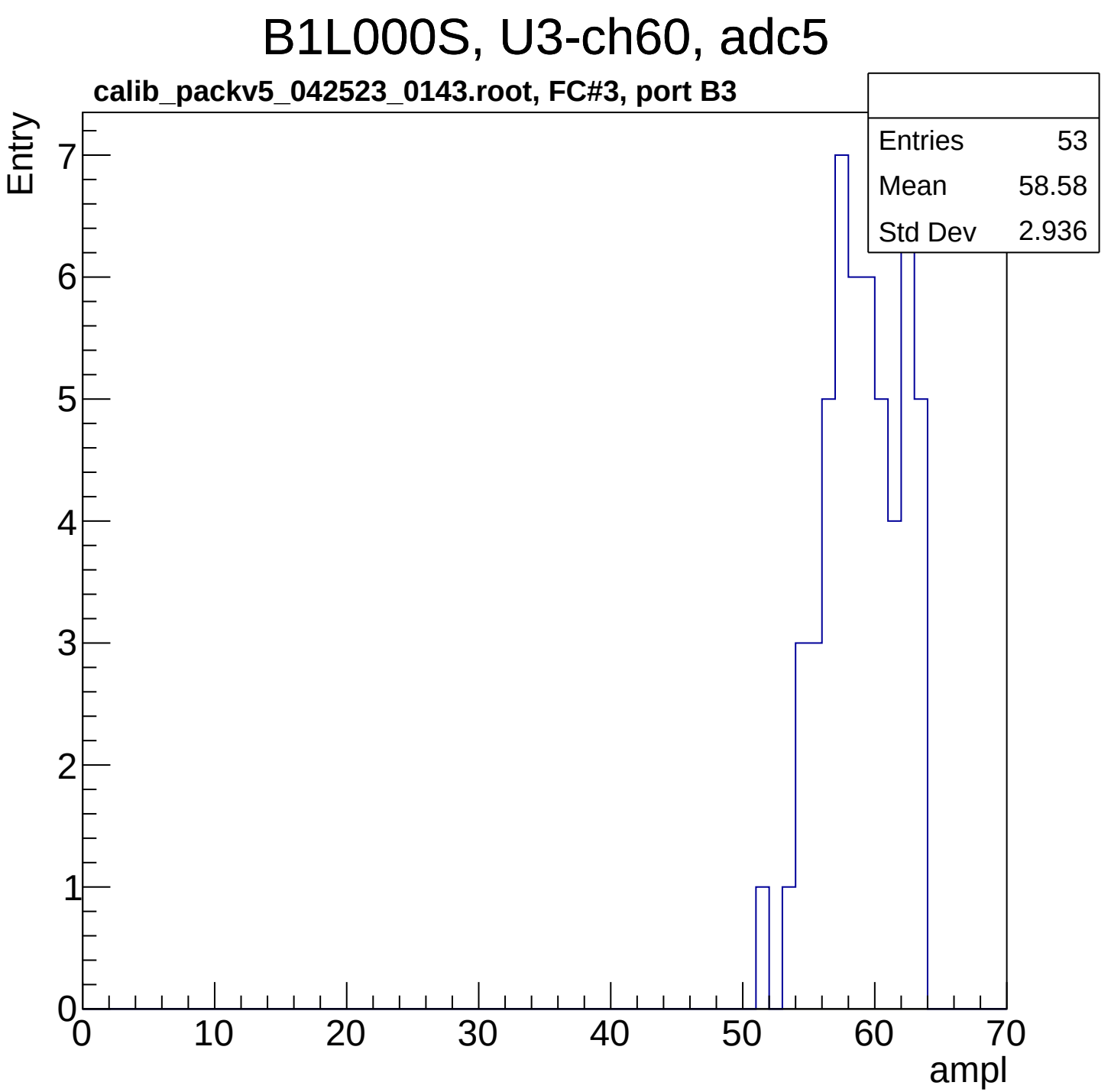
B1L000S, U3-ch60, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.58
Std Dev 2.936
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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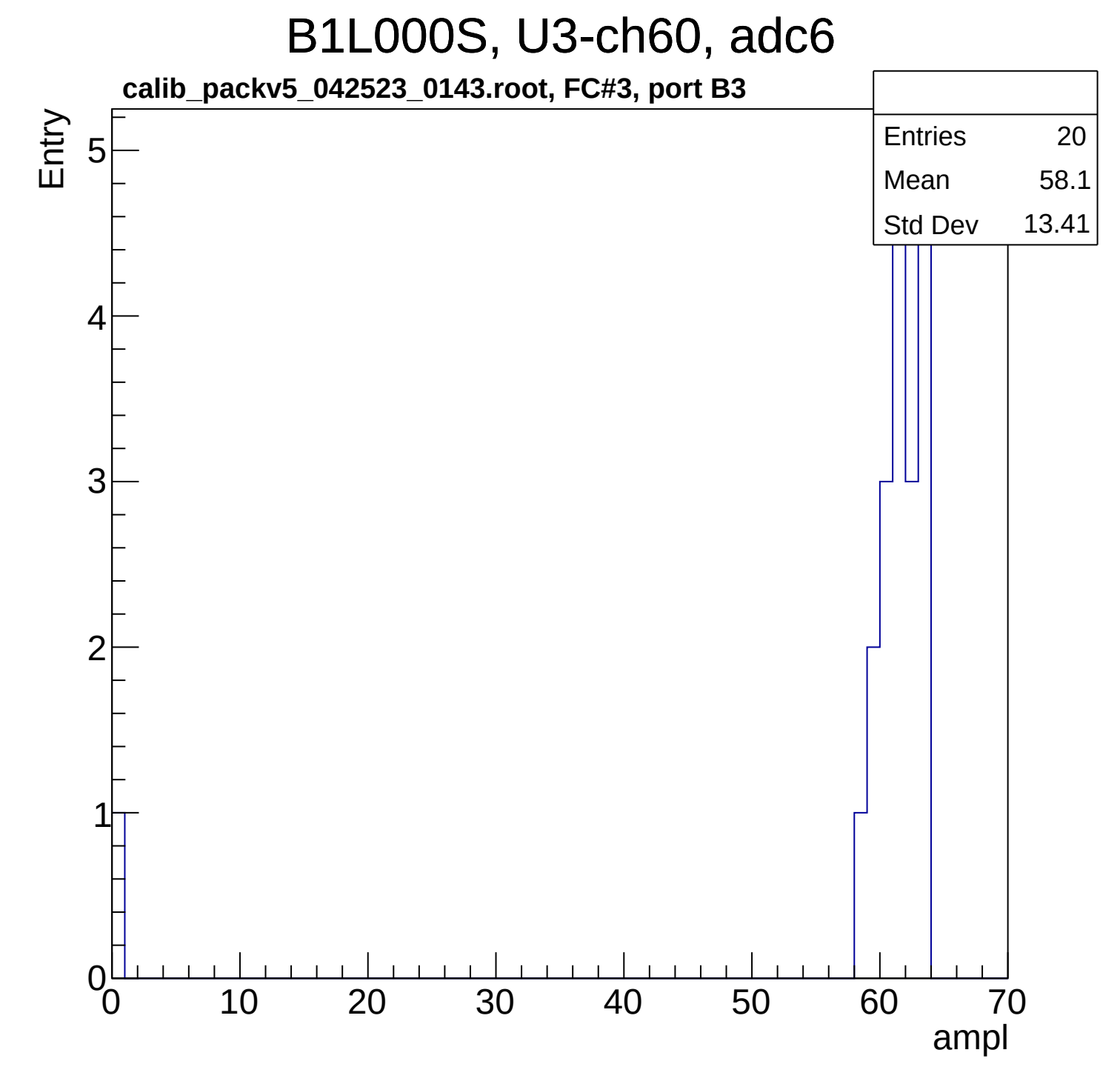
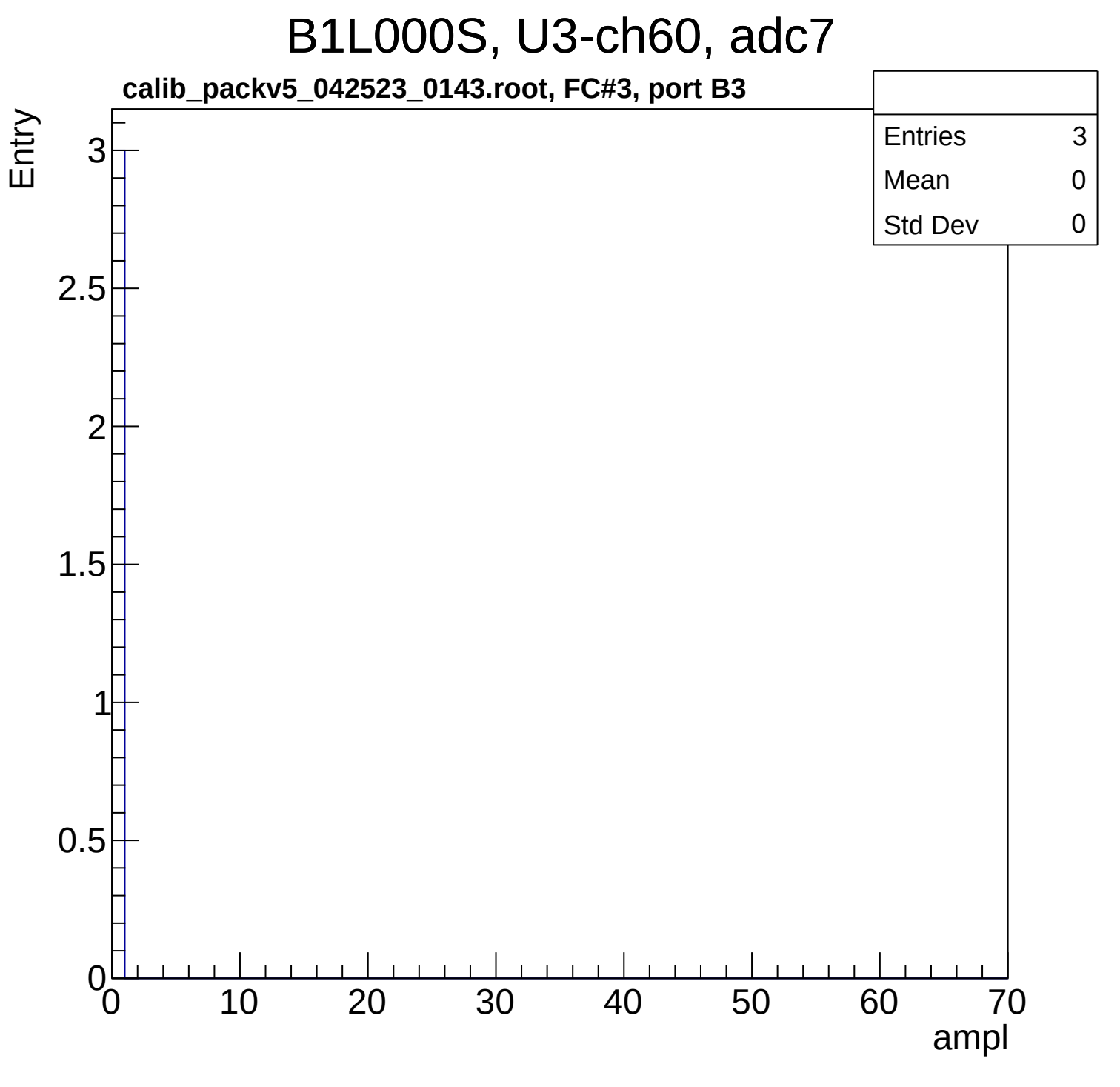

B1L000S, U3-ch60, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58.1
Std Dev 13.41
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L000S, U3-ch60, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
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2.5
2
1.5
1
0.5
0
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10
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30
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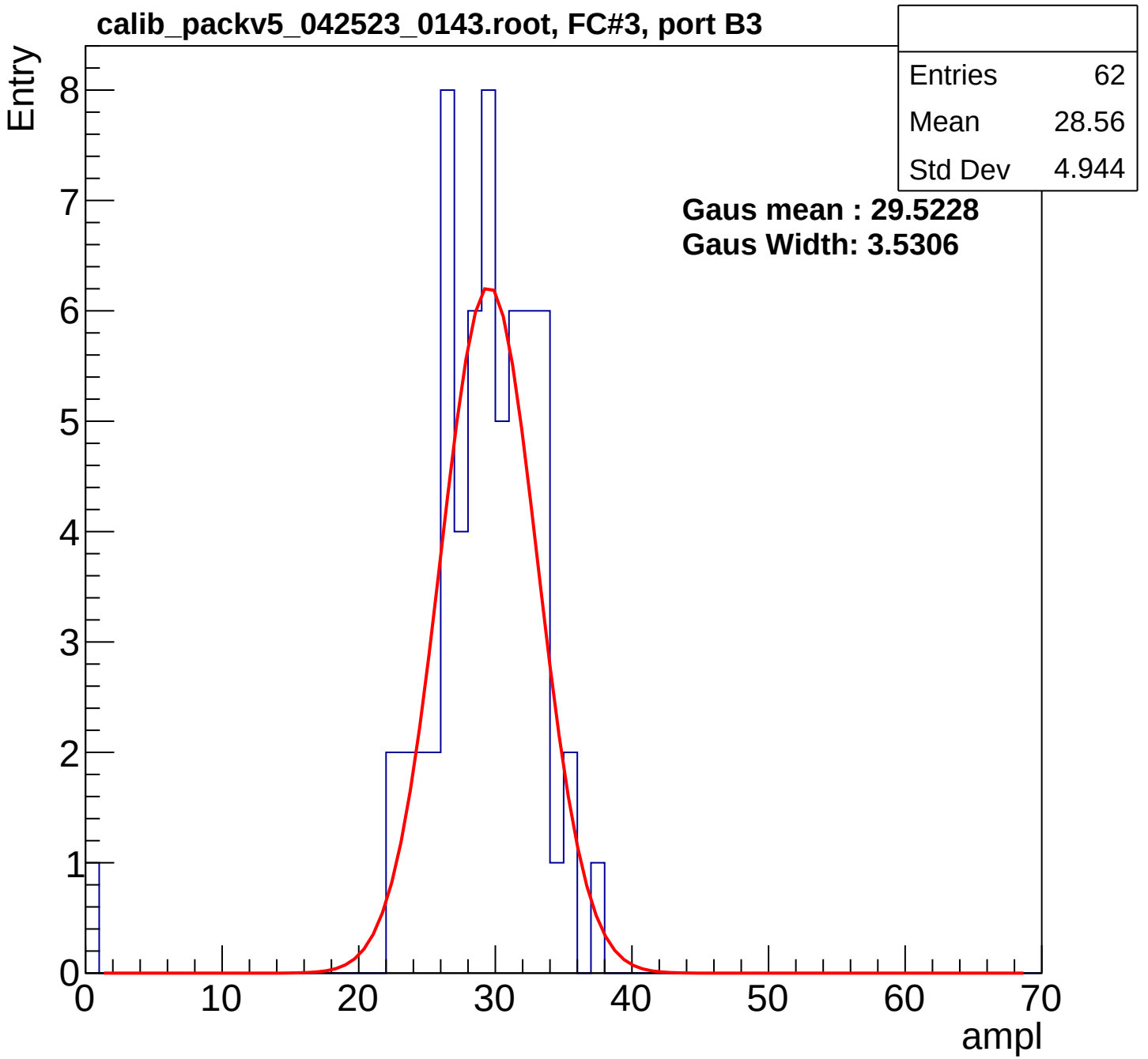
50
60
70
ampl


B1L000S, U3-ch61, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 28.56
Std Dev 4.944
Gaus mean : 29.5228
Gaus Width: 3.5306
Entry
ampl
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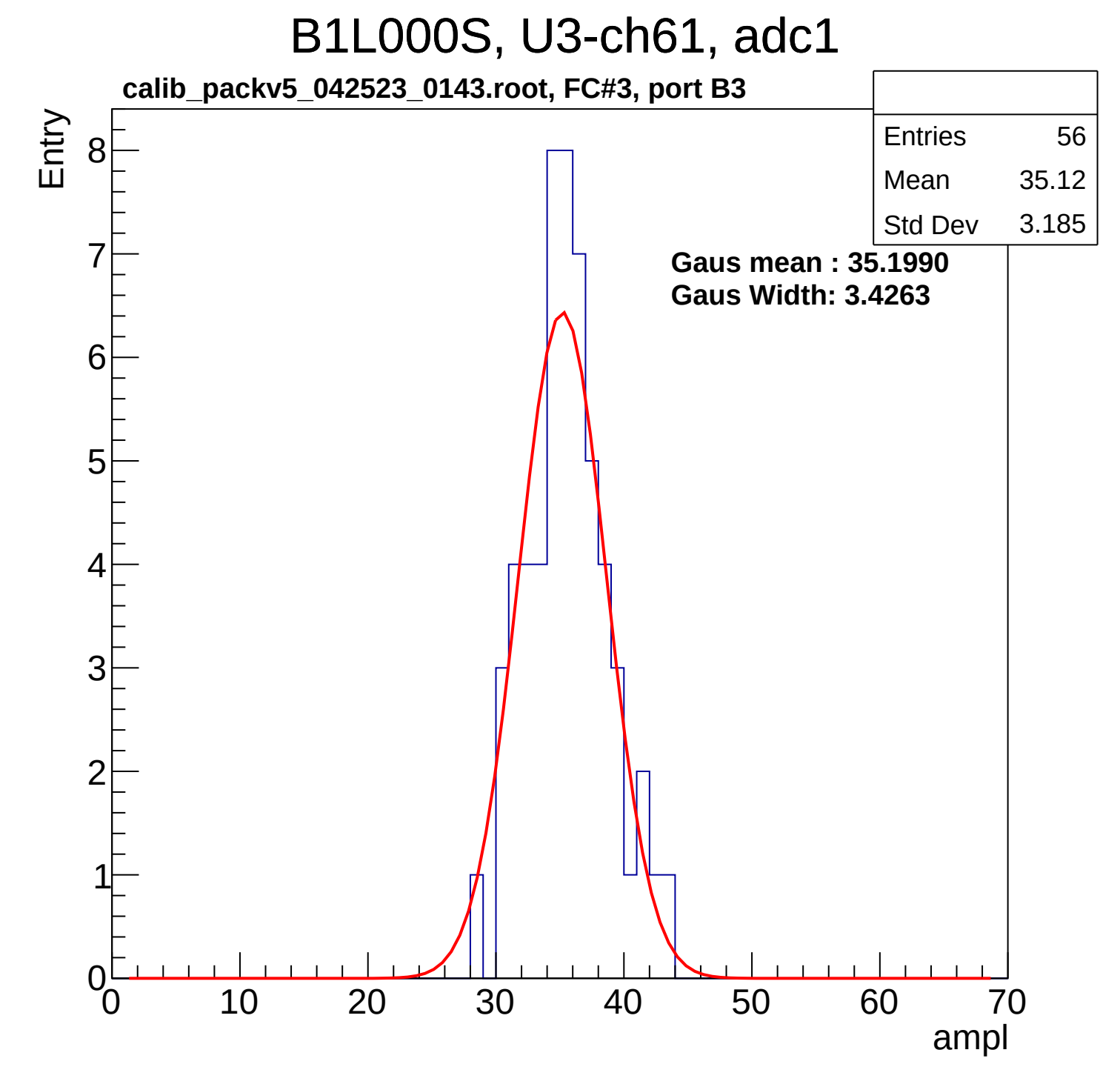
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch61, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 35.12
Std Dev 3.185
Gaus mean : 35.1990
Gaus Width: 3.4263
Entry
ampl
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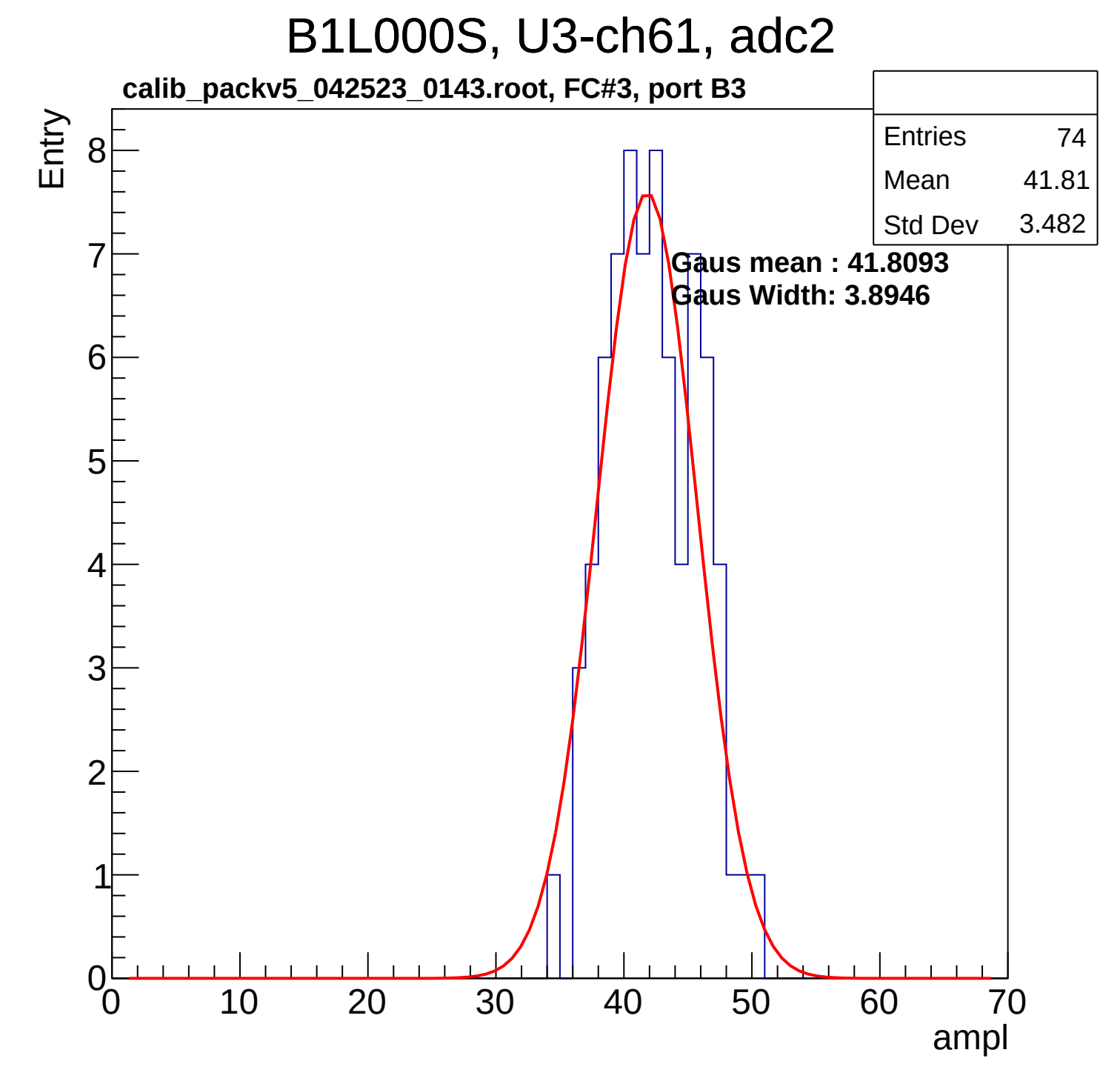
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch61, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 41.81
Std Dev 3.482
Gaus mean : 41.8093
Gaus Width: 3.8946
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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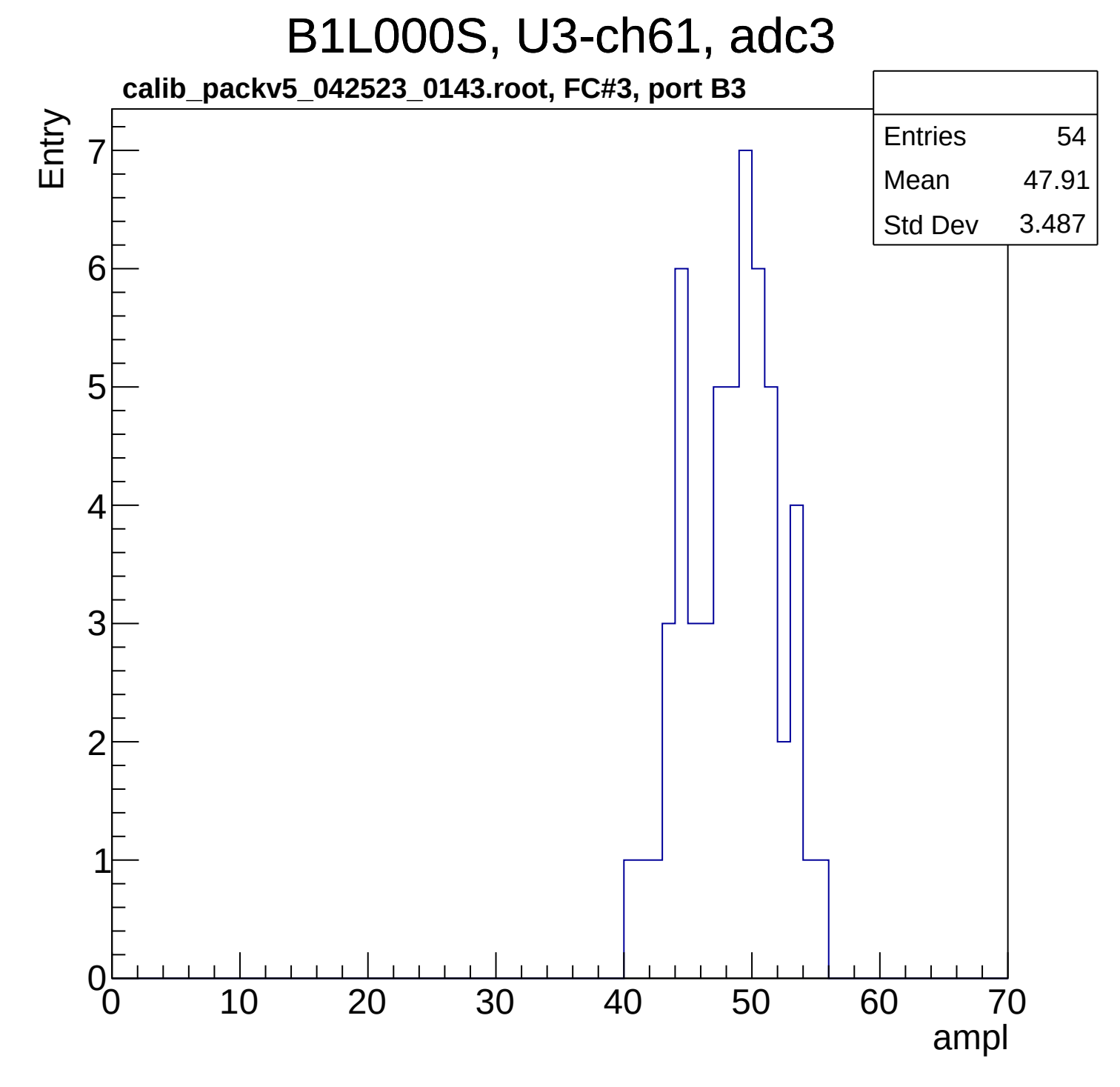
B1L000S, U3-ch61, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 47.91
Std Dev 3.487
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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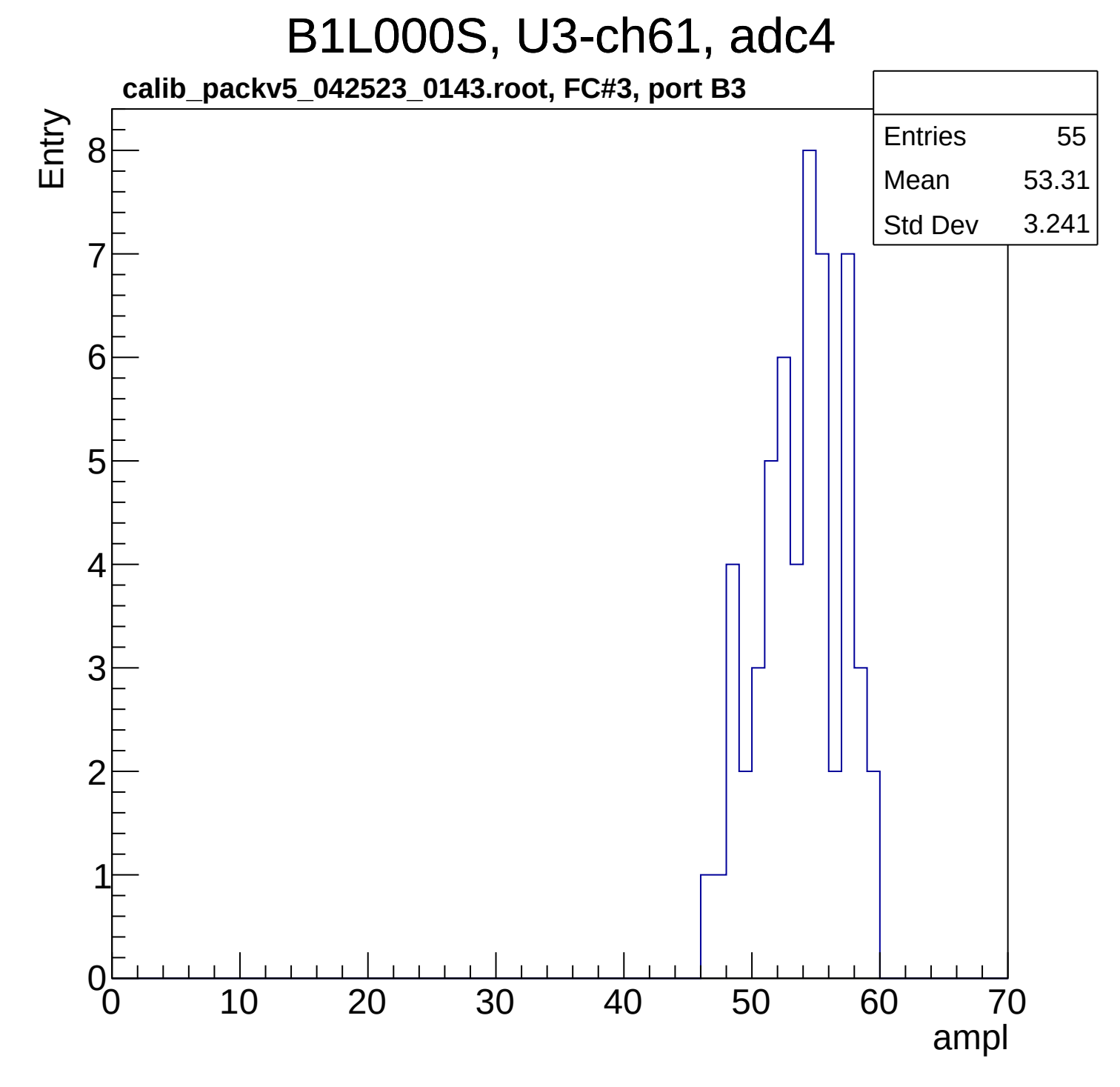
B1L000S, U3-ch61, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.31
Std Dev 3.241
Entry
ampl
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3
4
5
6
7
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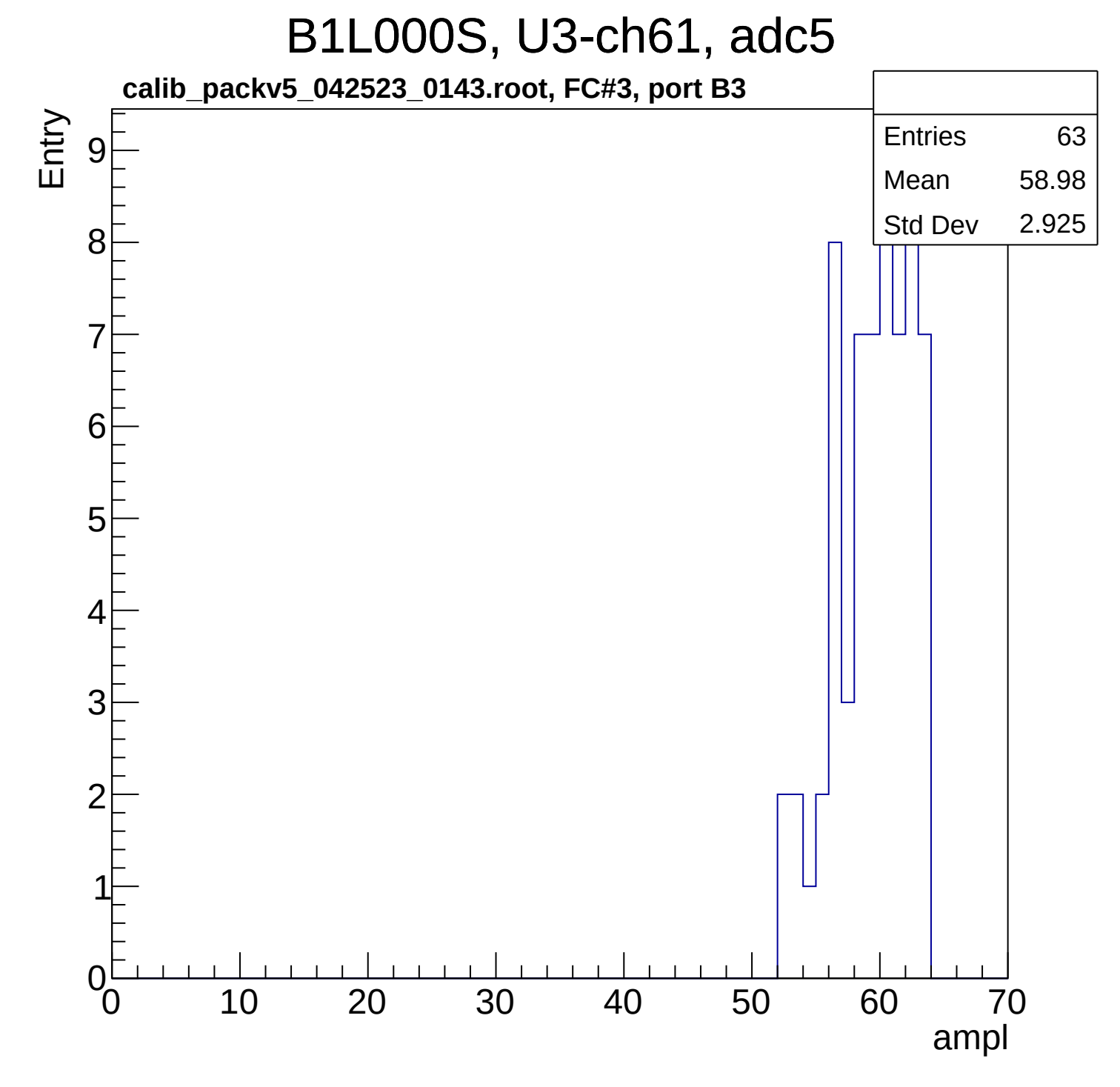
B1L000S, U3-ch61, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 58.98
Std Dev 2.925
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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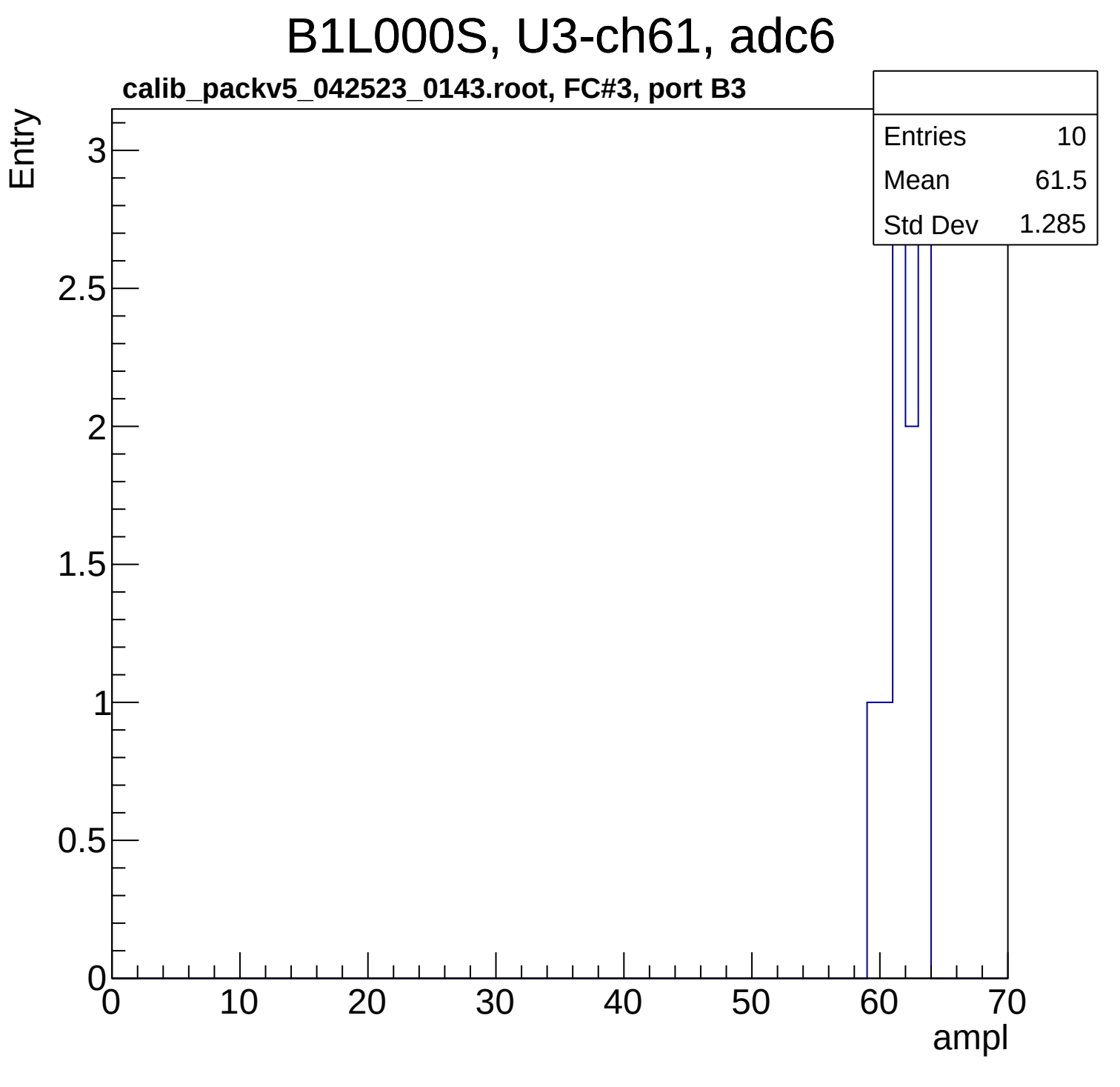
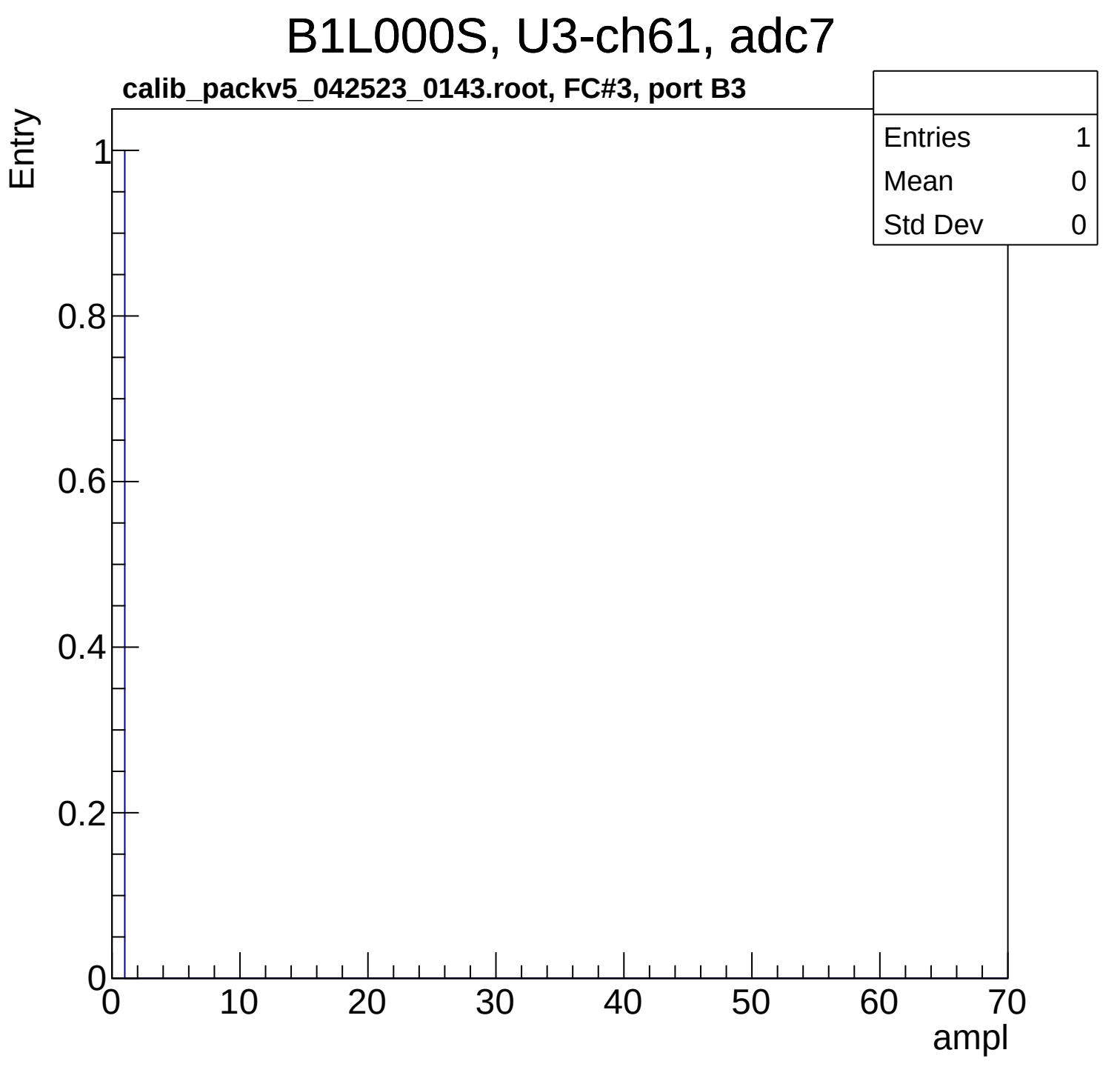

B1L000S, U3-ch61, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.5
Std Dev 1.285
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch61, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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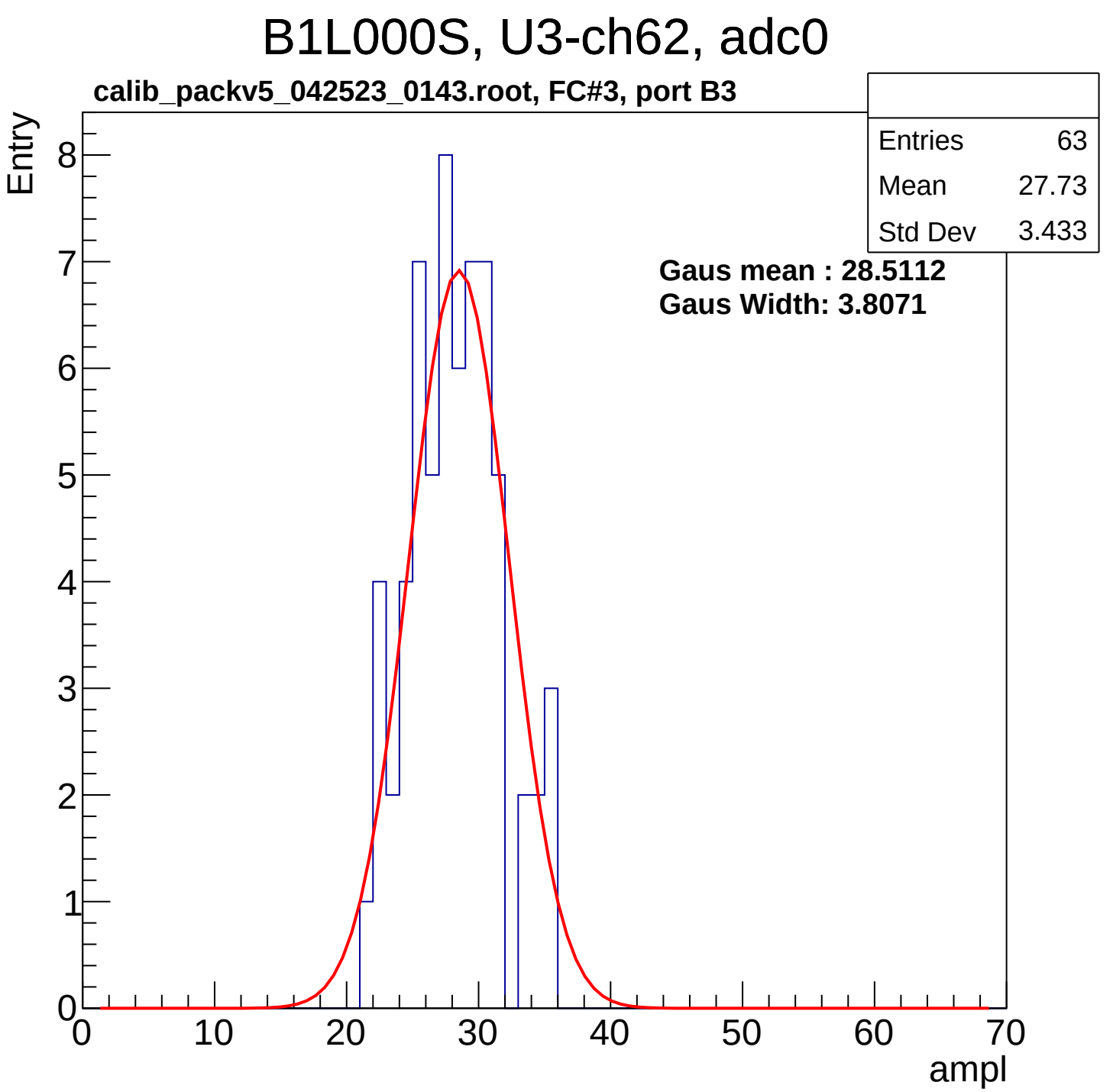
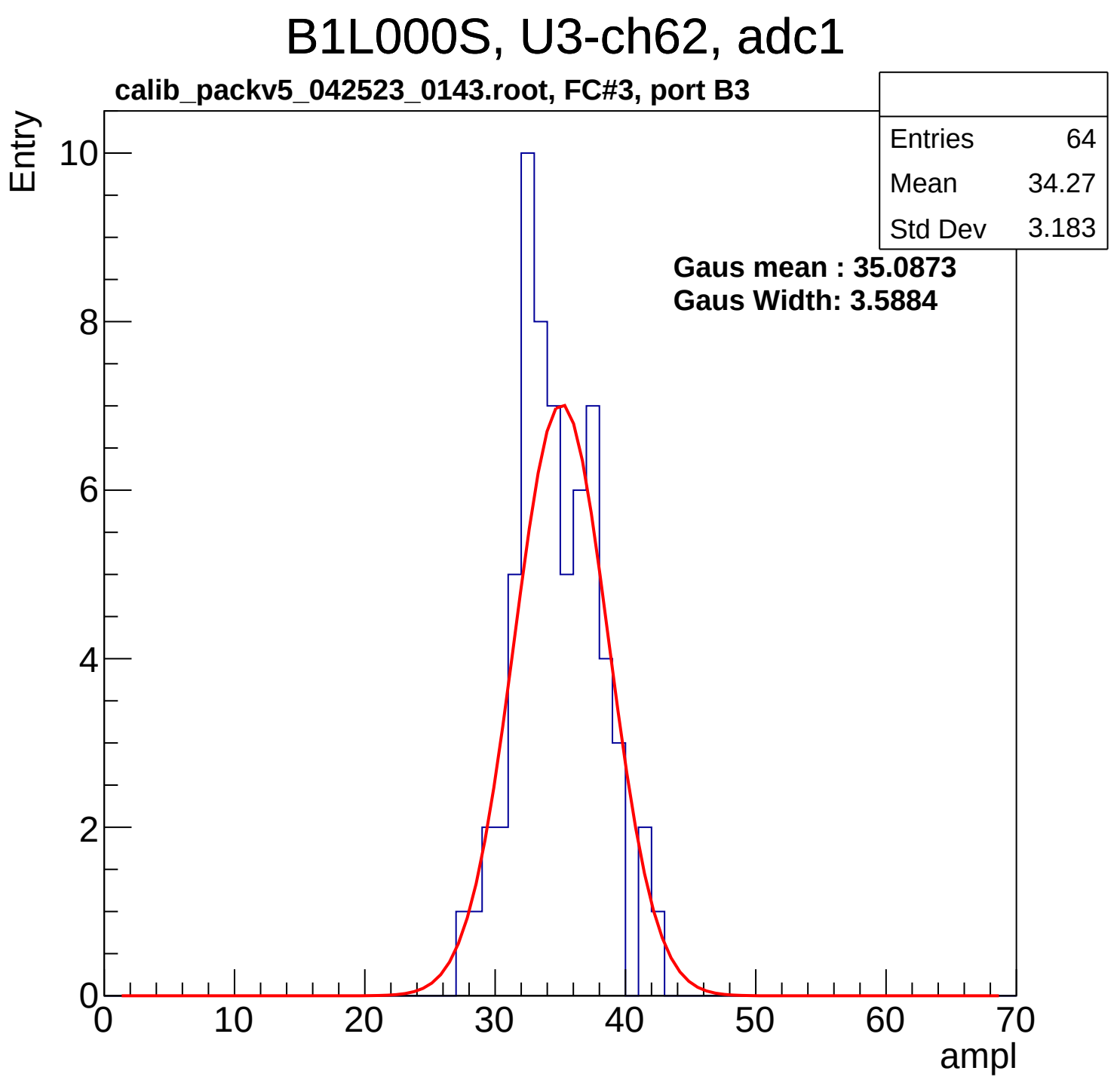
40
50
60
70
ampl


B1L000S, U3-ch62, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 27.73
Std Dev 3.433
Gaus mean : 28.5112
Gaus Width: 3.8071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch62, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 34.27
Std Dev 3.183
Gaus mean : 35.0873
Gaus Width: 3.5884
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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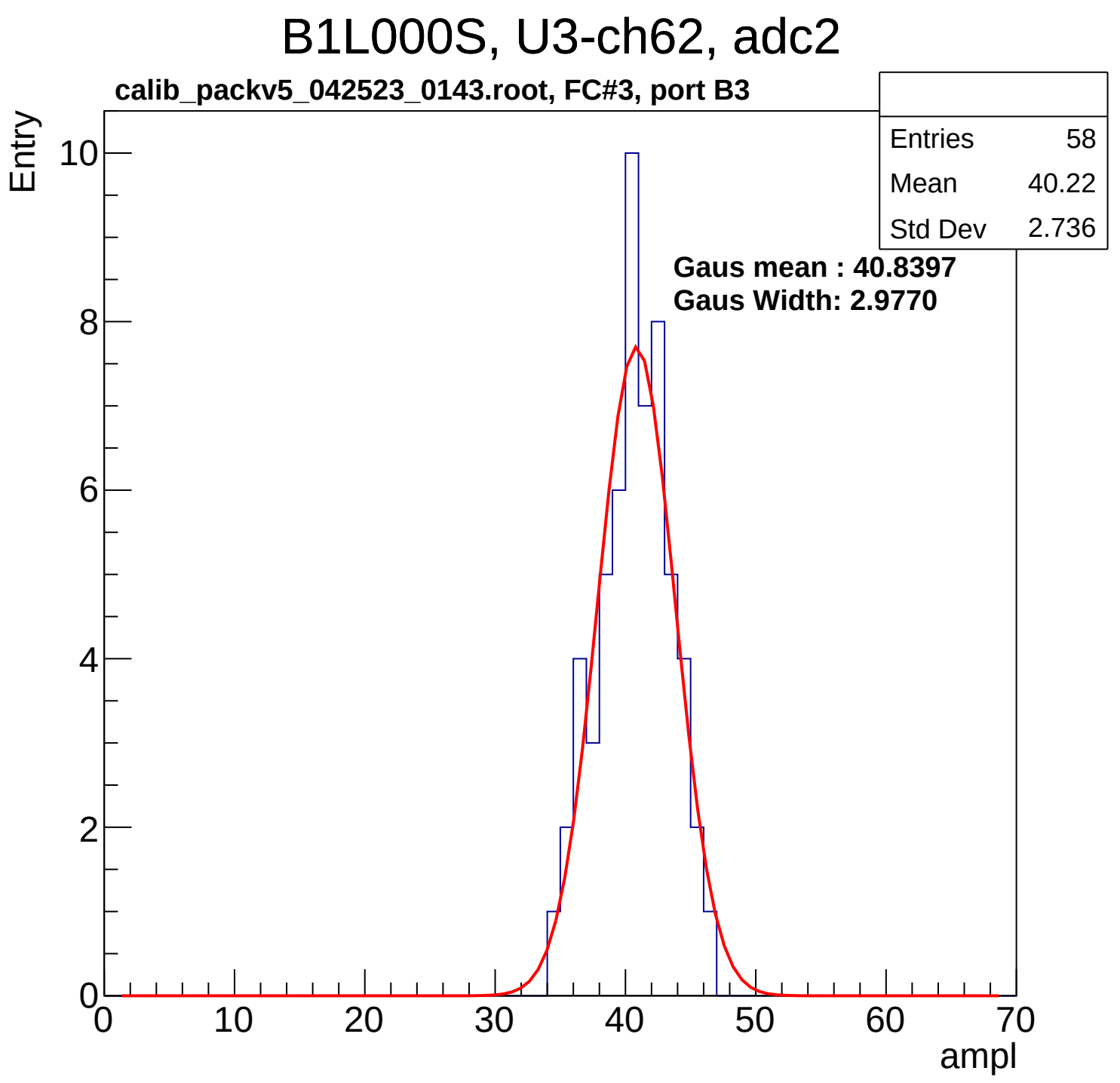
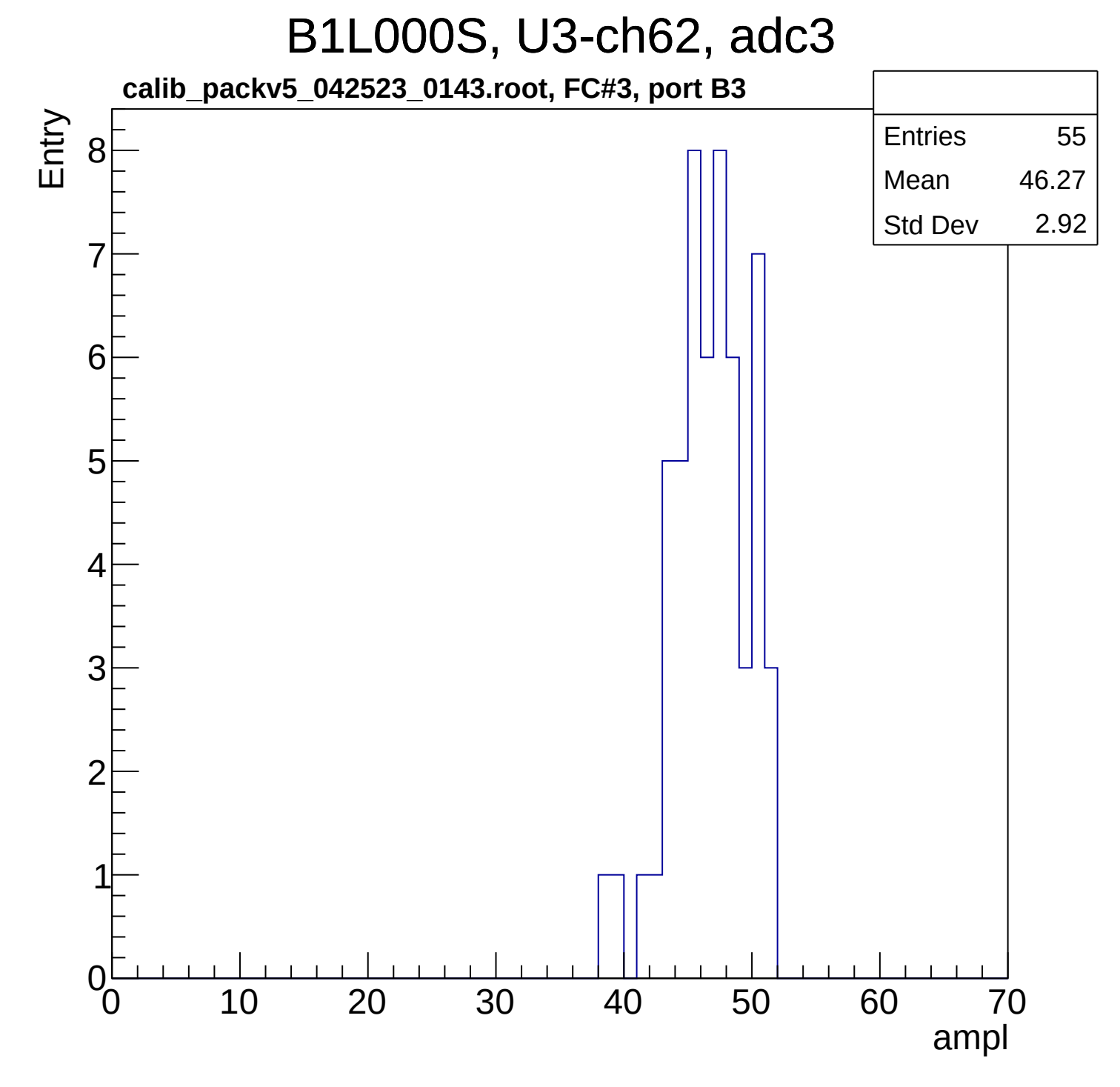

B1L000S, U3-ch62, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 40.22
Std Dev 2.736
Gaus mean : 40.8397
Gaus Width: 2.9770
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch62, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 46.27
Std Dev 2.92
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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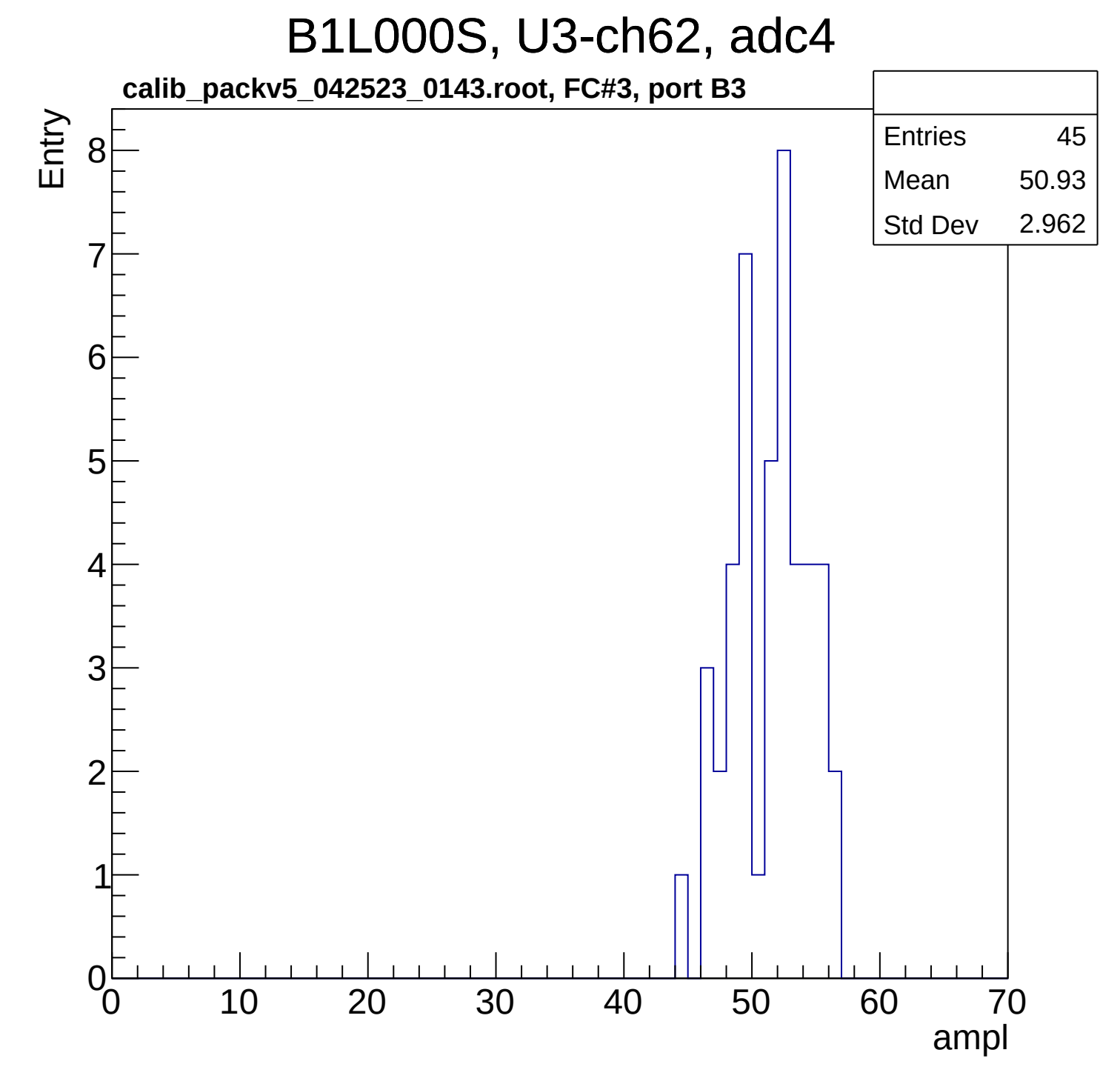
B1L000S, U3-ch62, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 50.93
Std Dev 2.962
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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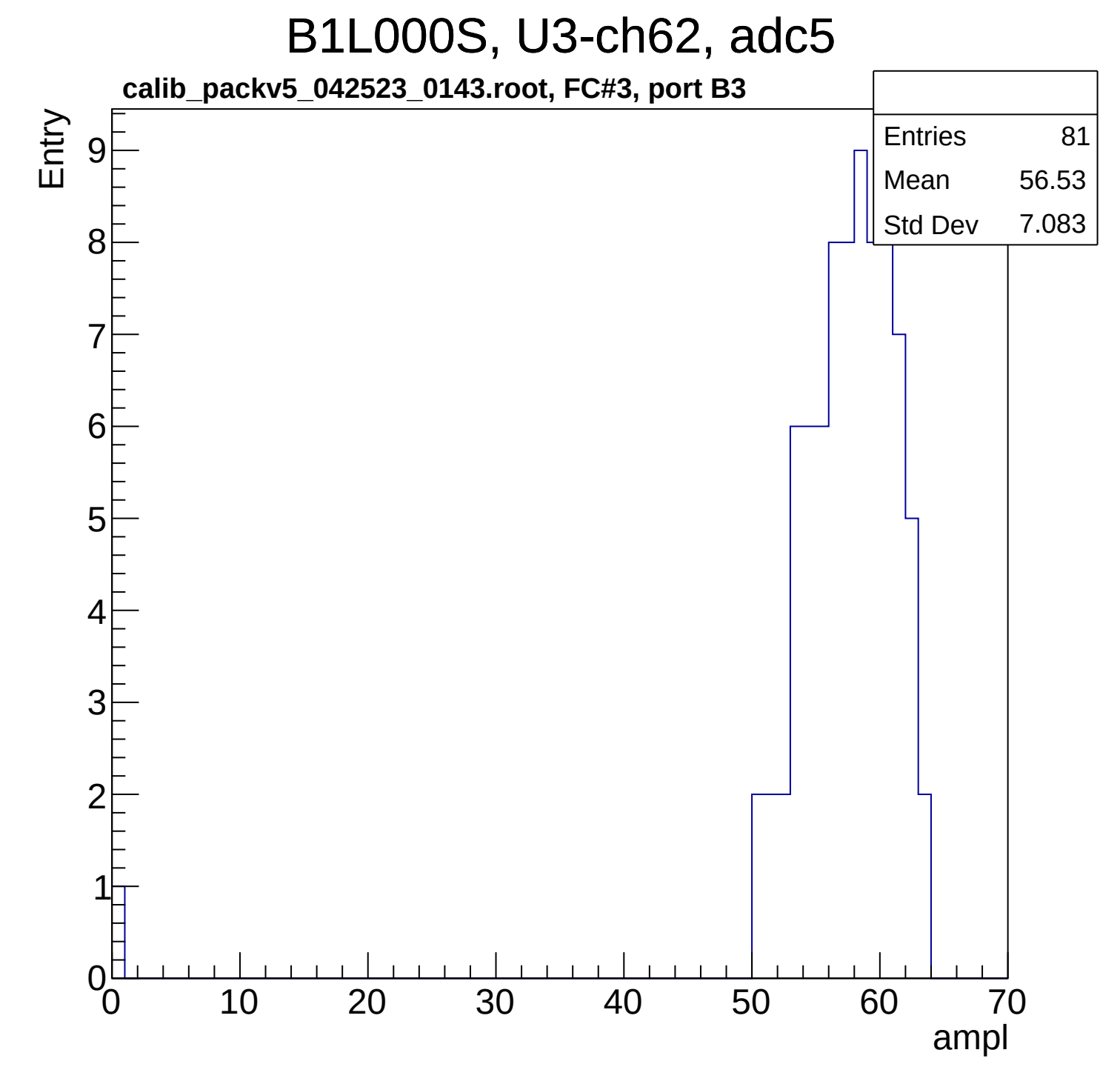
B1L000S, U3-ch62, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 56.53
Std Dev 7.083
Entry
ampl
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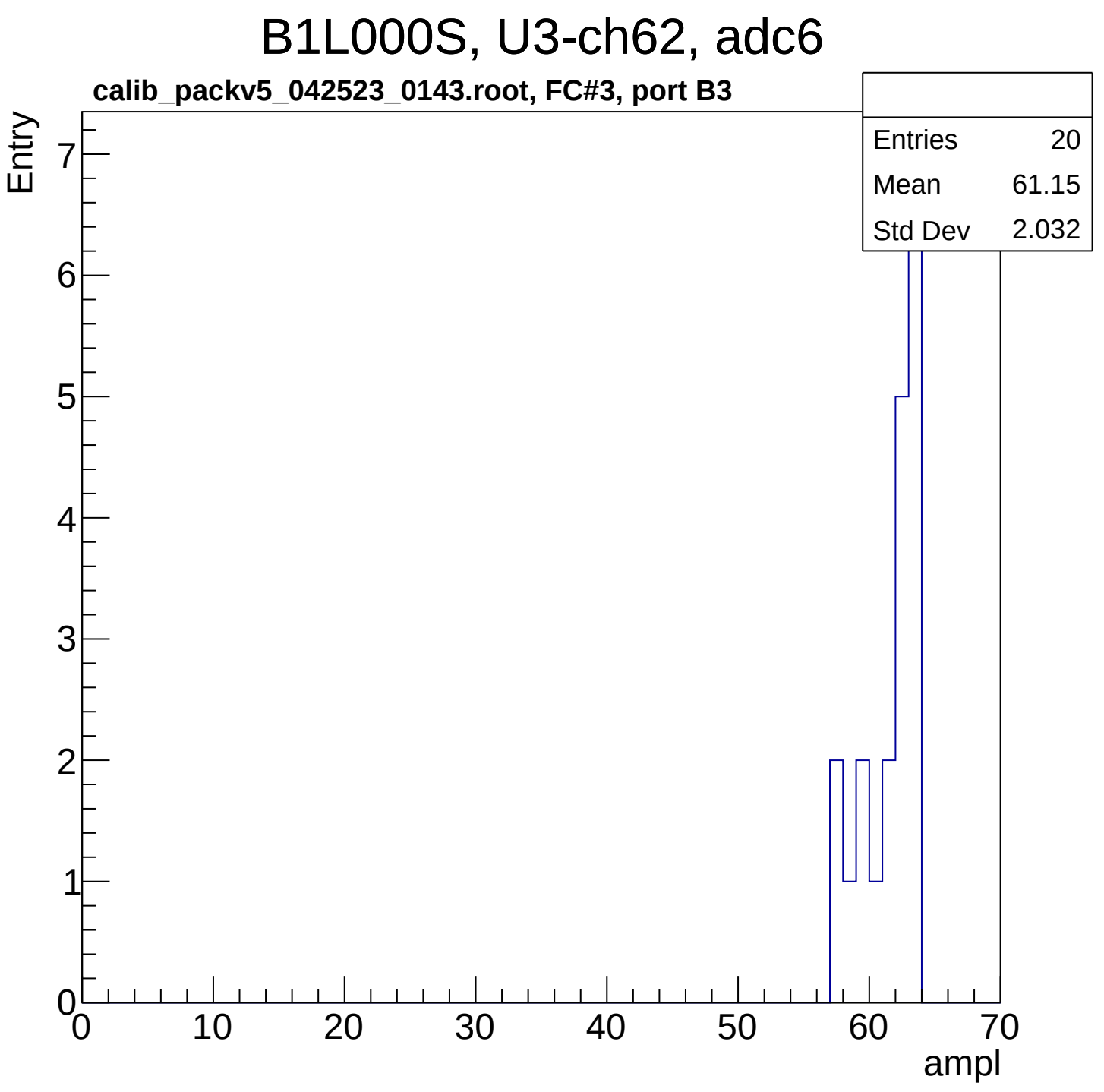
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch62, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 61.15
Std Dev 2.032
Entry
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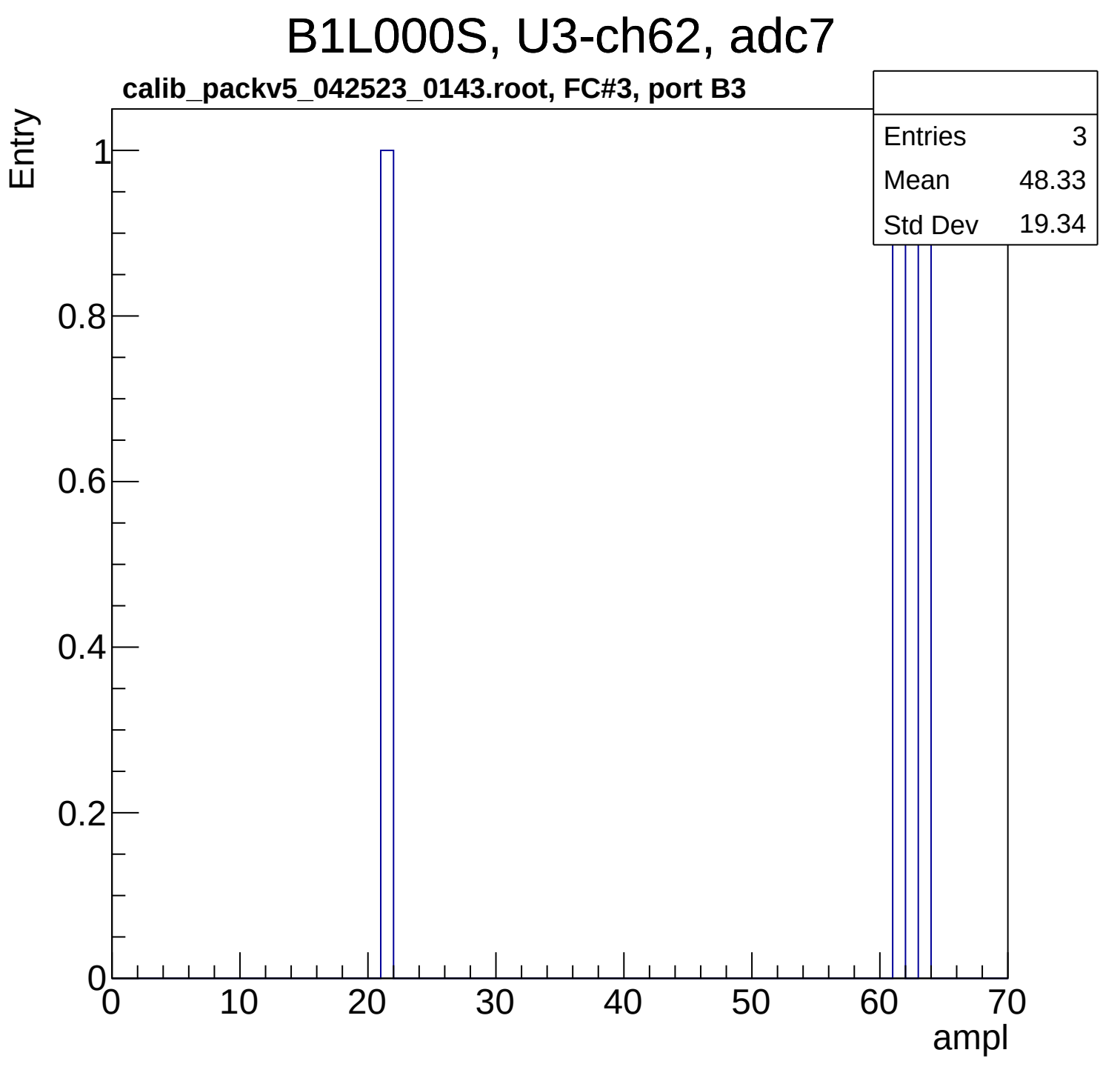
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch62, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 48.33
Std Dev 19.34
Entry
ampl
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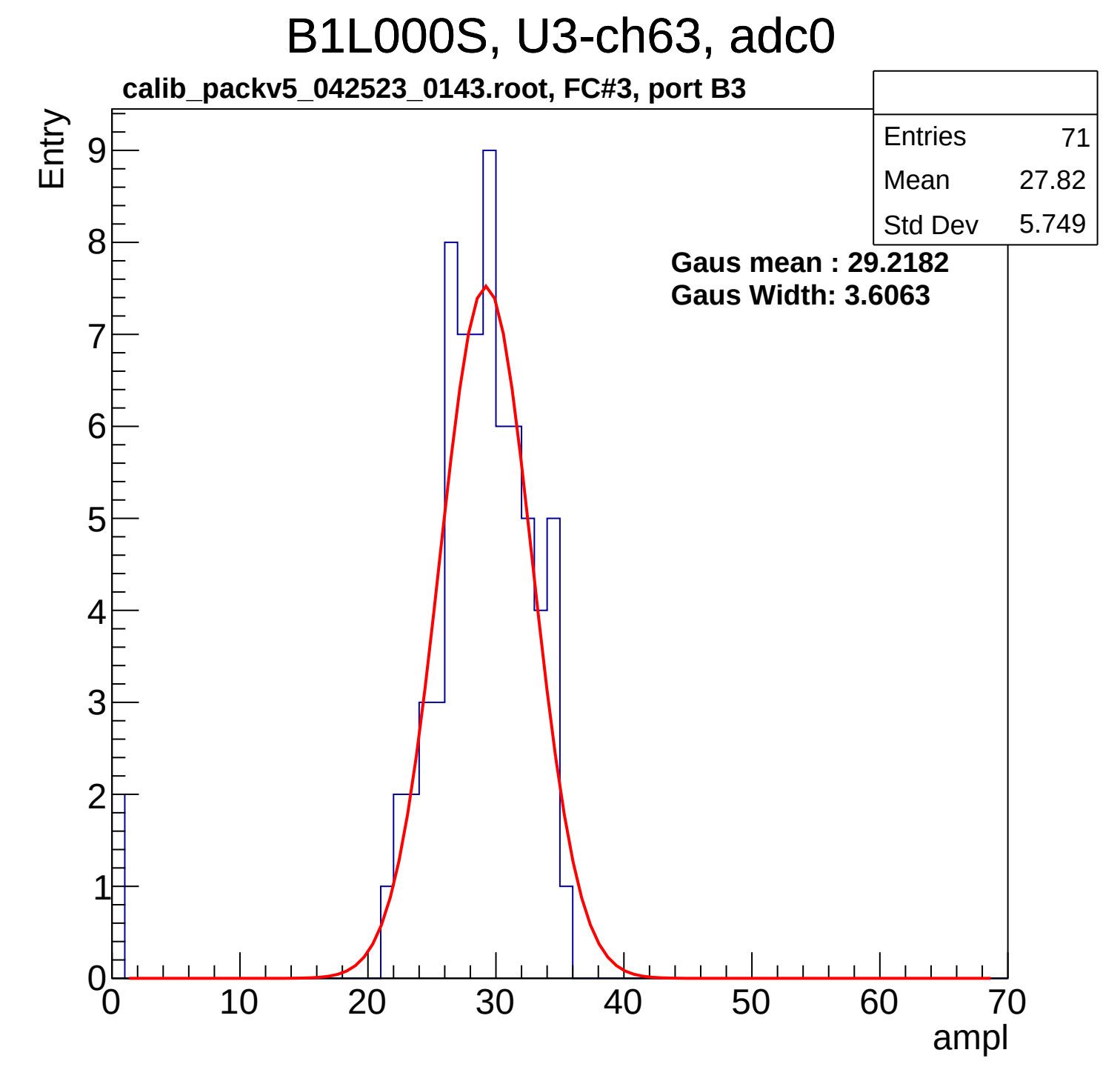
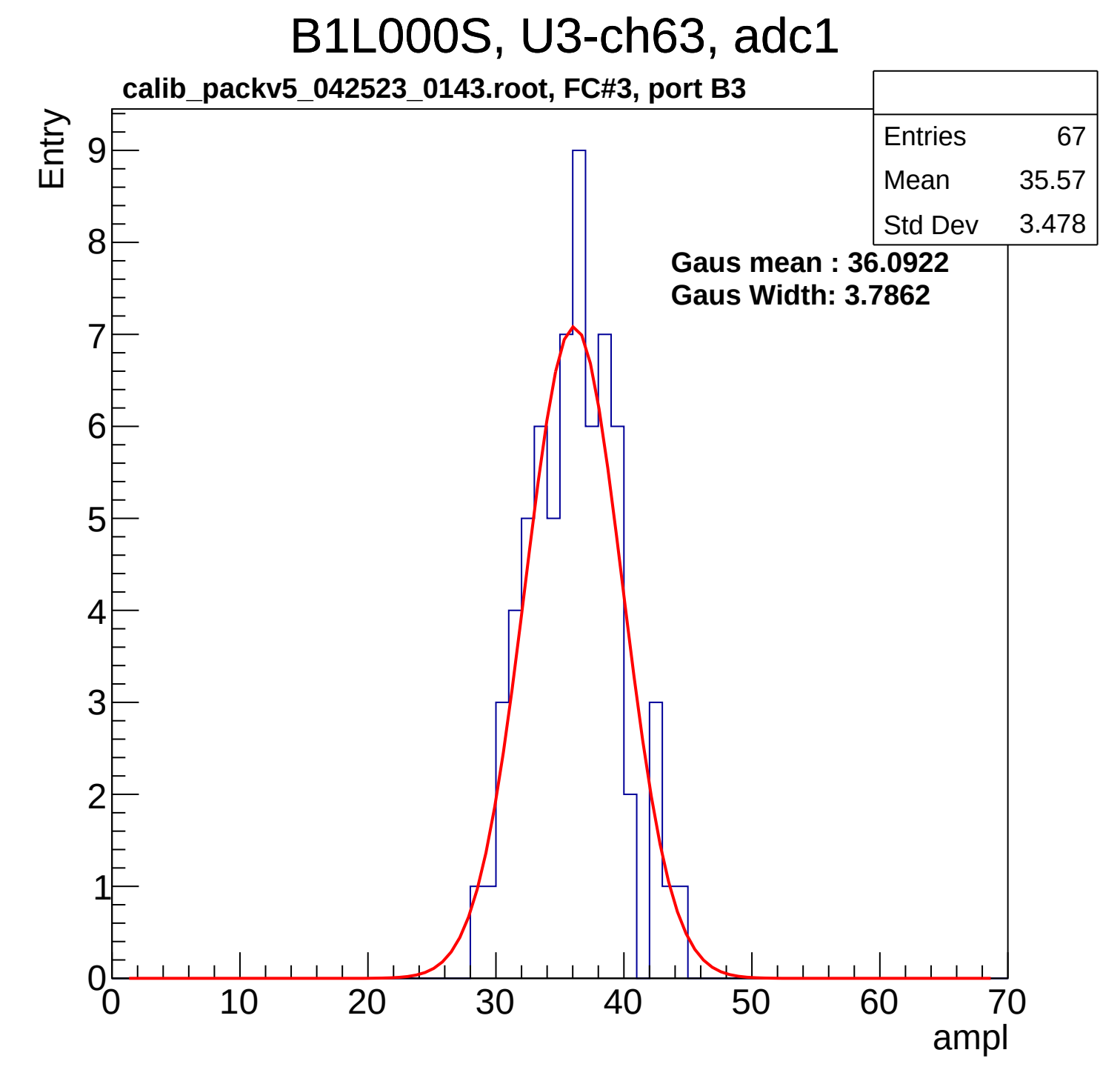
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U3-ch63, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 27.82
Std Dev 5.749
Gaus mean : 29.2182
Gaus Width: 3.6063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch63, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.57
Std Dev 3.478
Gaus mean : 36.0922
Gaus Width: 3.7862
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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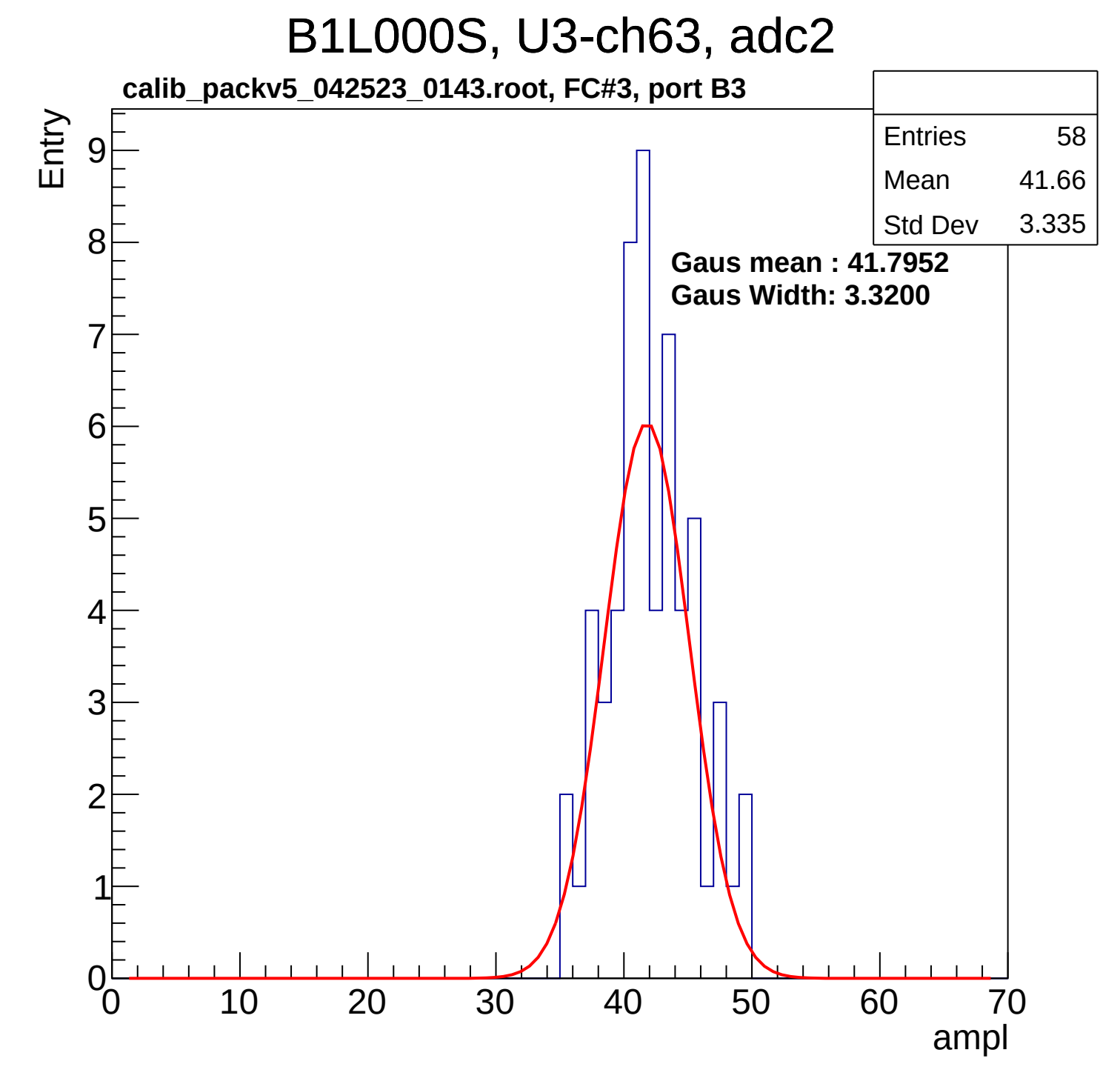
B1L000S, U3-ch63, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 41.66
Std Dev 3.335
Gaus mean : 41.7952
Gaus Width: 3.3200
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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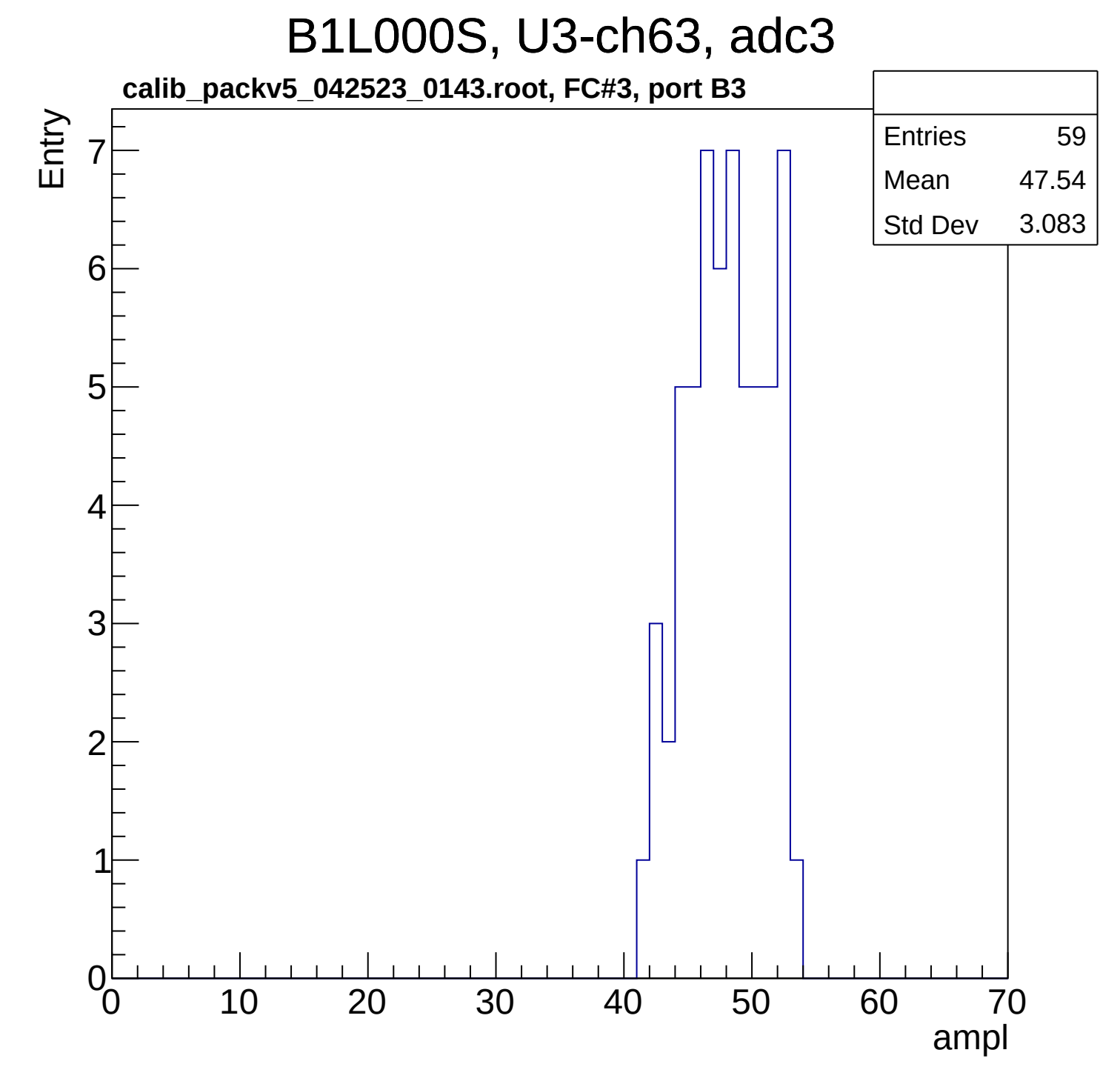
B1L000S, U3-ch63, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 47.54
Std Dev 3.083
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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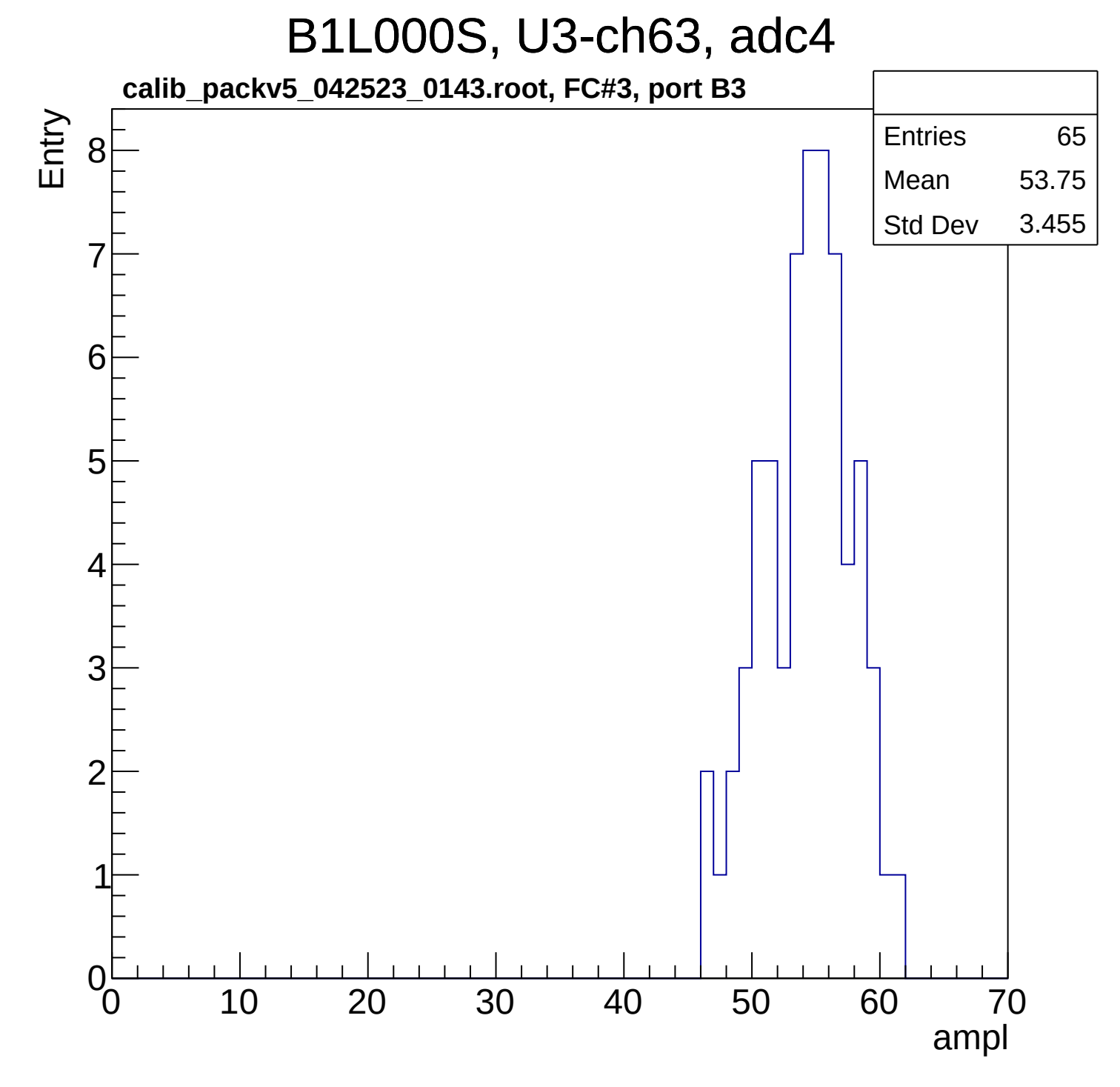
B1L000S, U3-ch63, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 53.75
Std Dev 3.455
Entry
ampl
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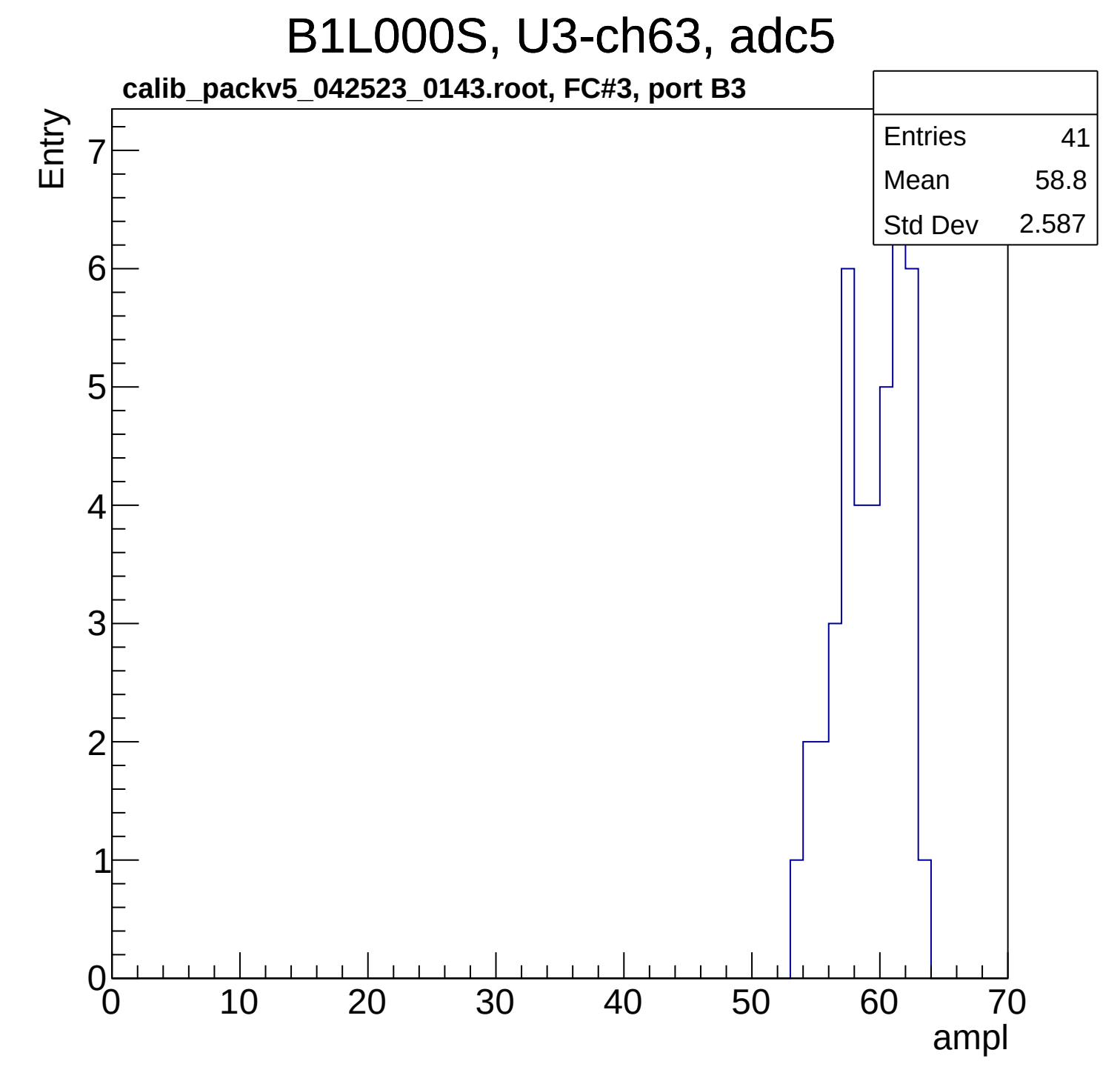
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch63, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 58.8
Std Dev 2.587
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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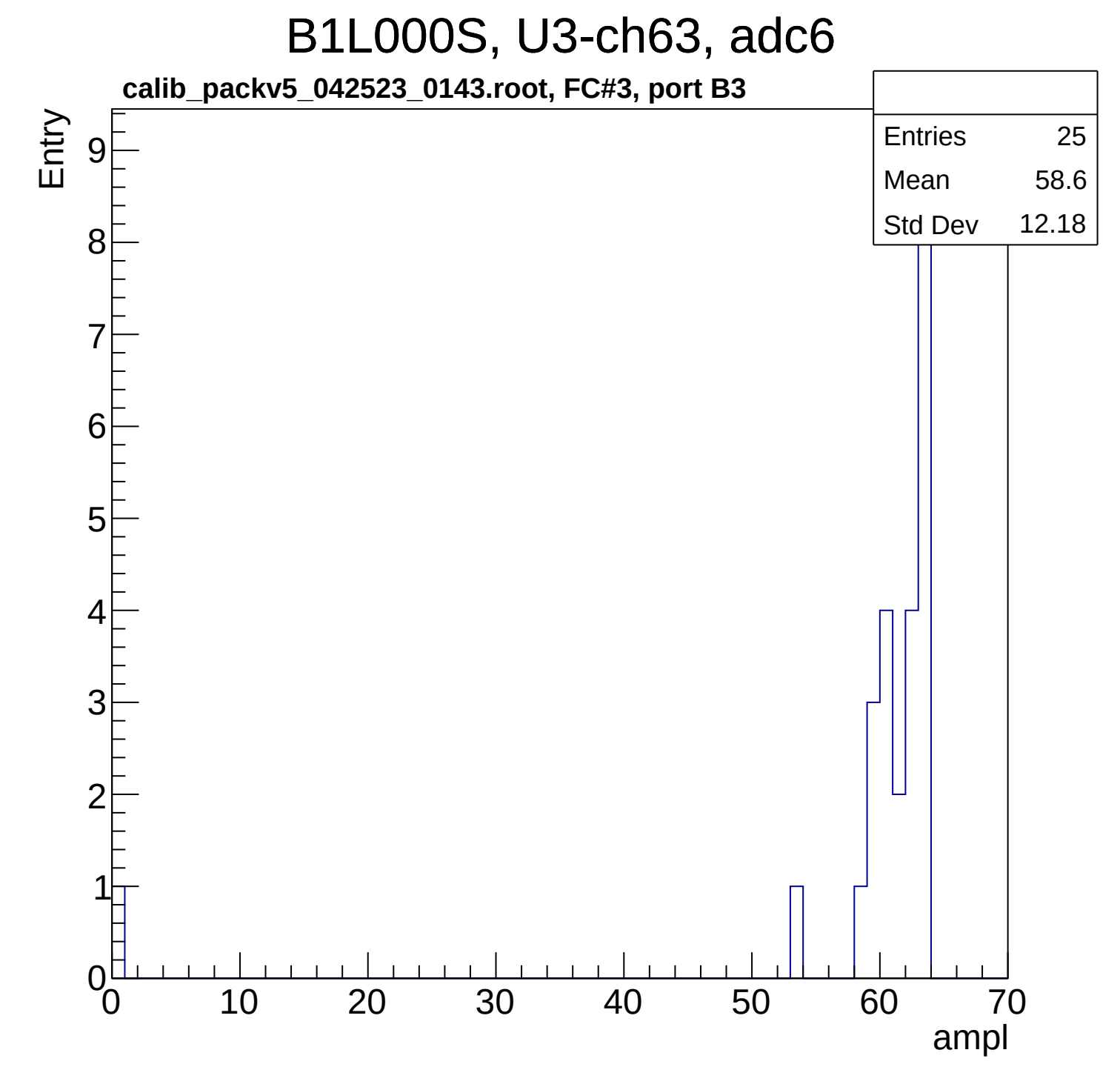
B1L000S, U3-ch63, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 58.6
Std Dev 12.18
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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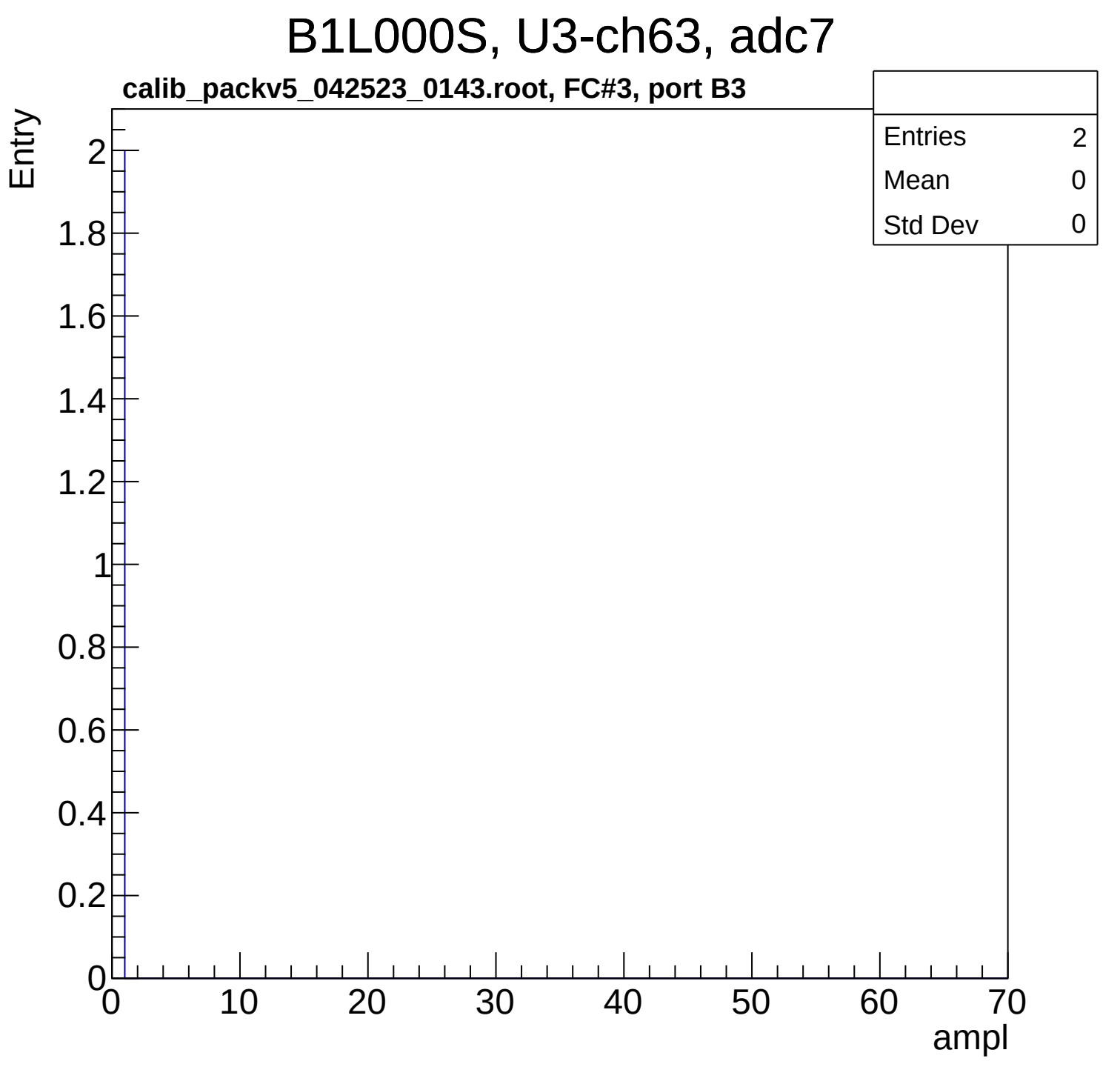
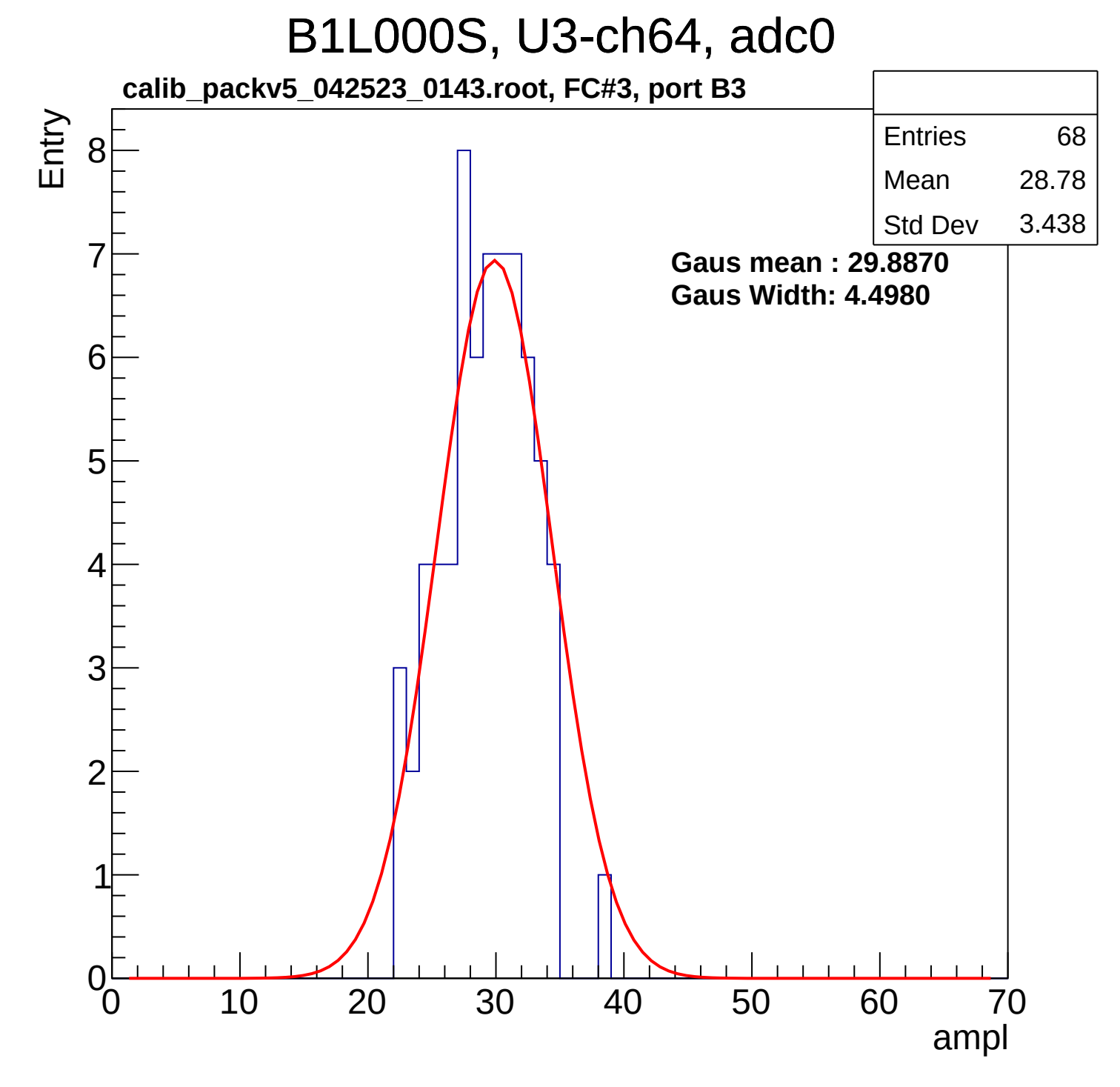
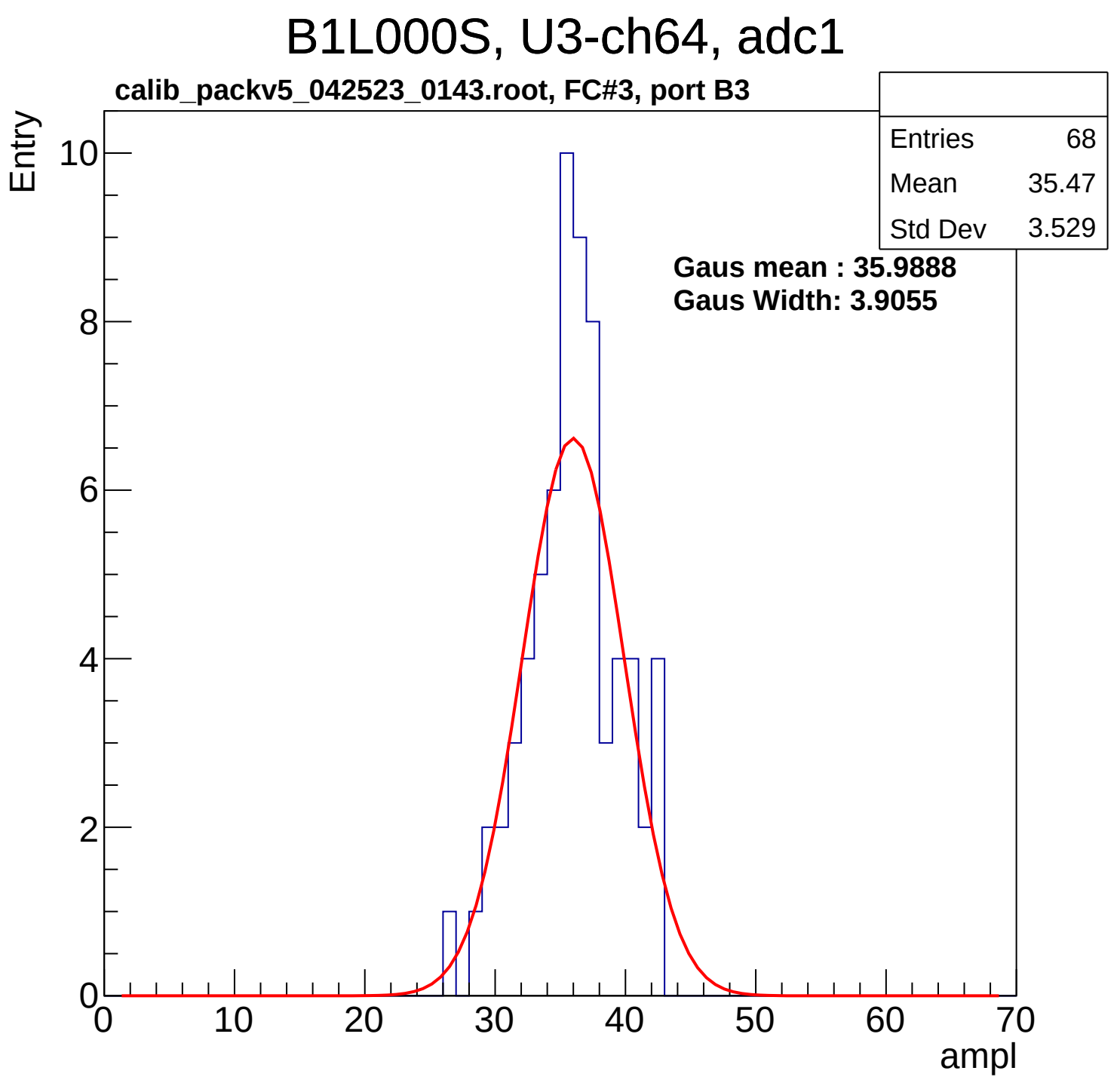
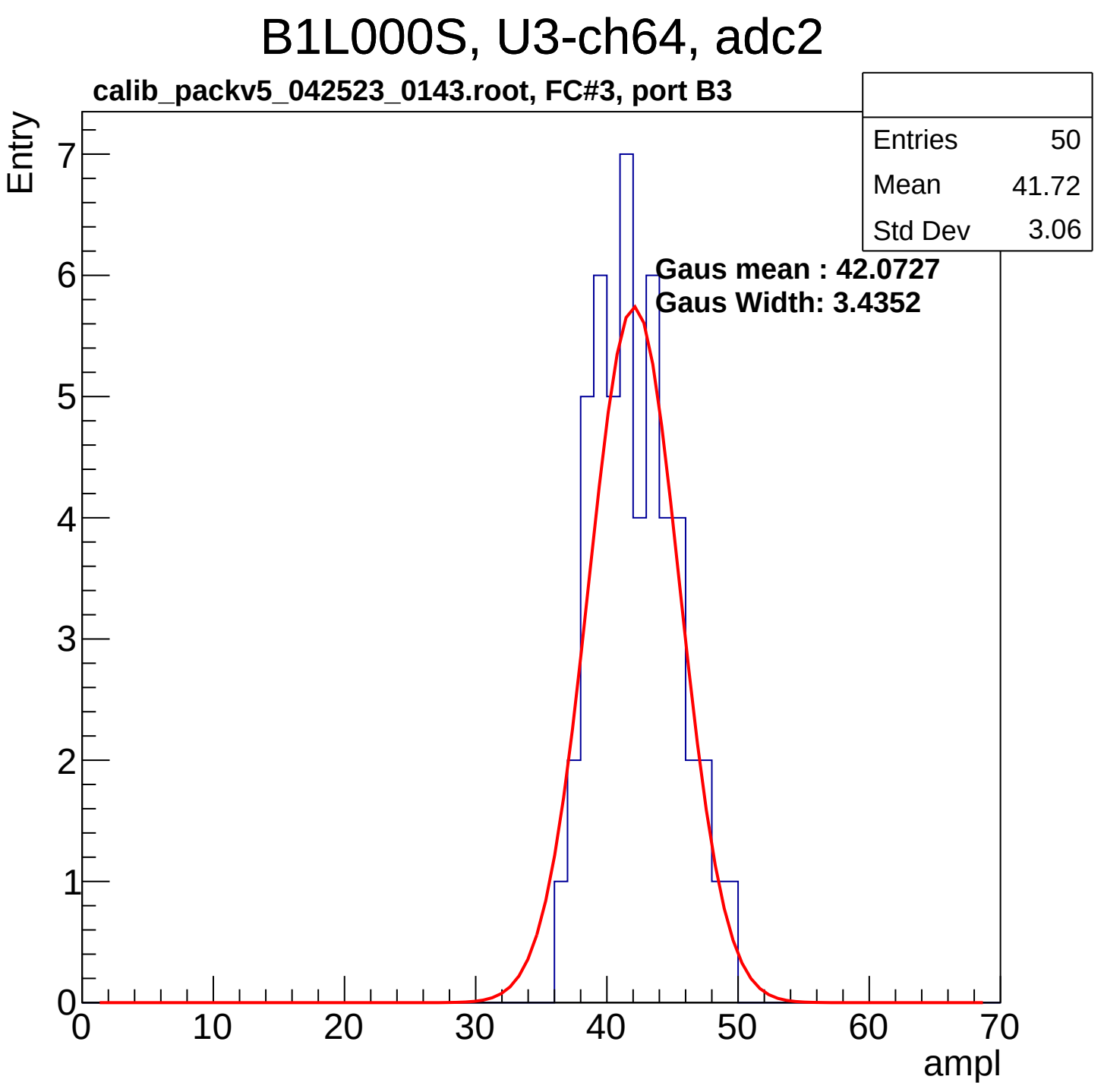
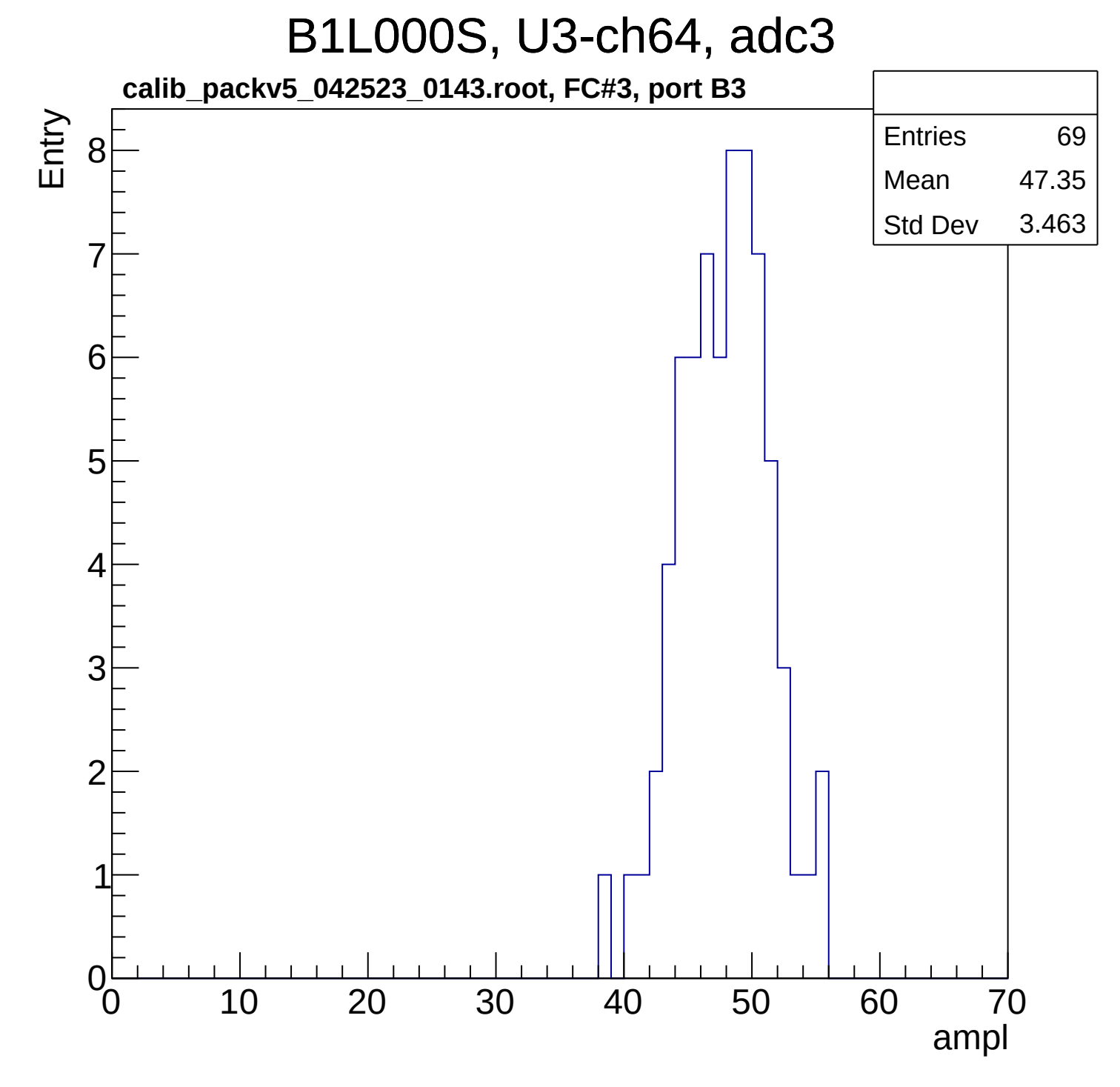

B1L000S, U3-ch63, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch64, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 28.78
Std Dev 3.438
Gaus mean : 29.8870
Gaus Width: 4.4980
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch64, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 35.47
Std Dev 3.529
Gaus mean : 35.9888
Gaus Width: 3.9055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch64, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 41.72
Std Dev 3.06
Gaus mean : 42.0727
Gaus Width: 3.4352
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch64, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 47.35
Std Dev 3.463
Entry
ampl
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1
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4
5
6
7
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0
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20
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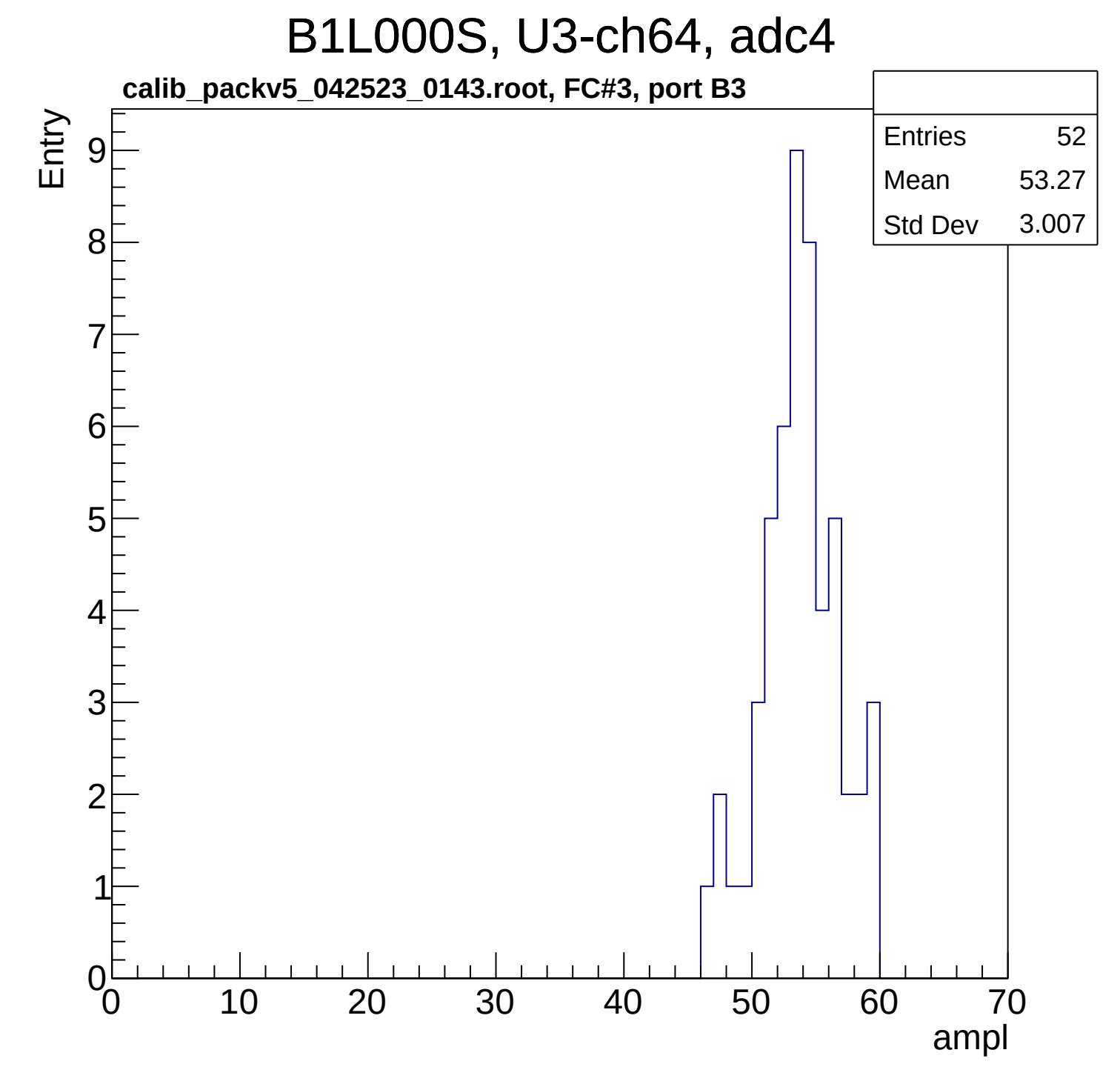
40
50
60
70


B1L000S, U3-ch64, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 53.27
Std Dev 3.007
Entry
ampl
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3
4
5
6
7
8
9
0
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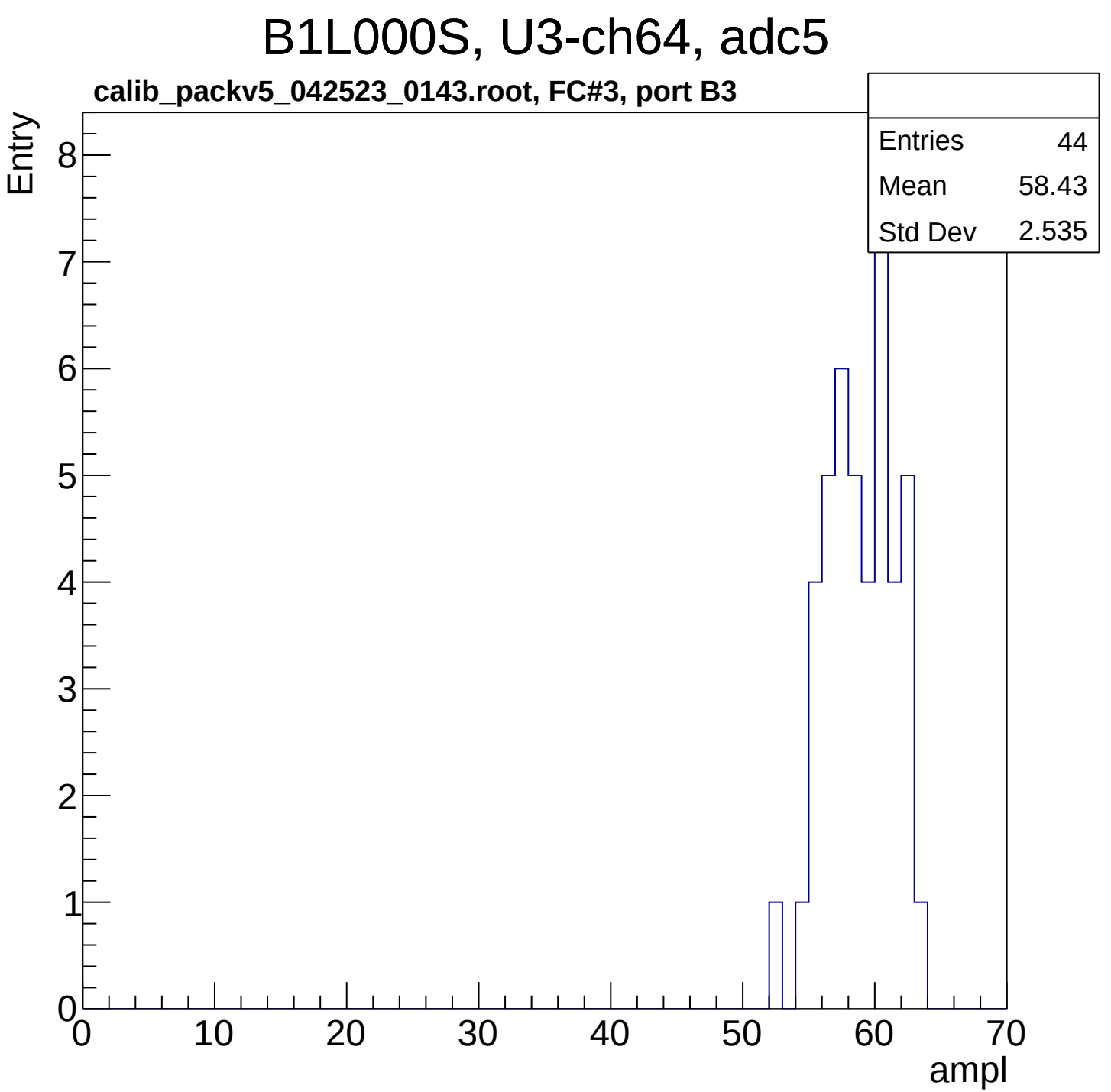
40
50
60
70


B1L000S, U3-ch64, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 58.43
Std Dev 2.535
Entry
ampl
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0
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20
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40
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60
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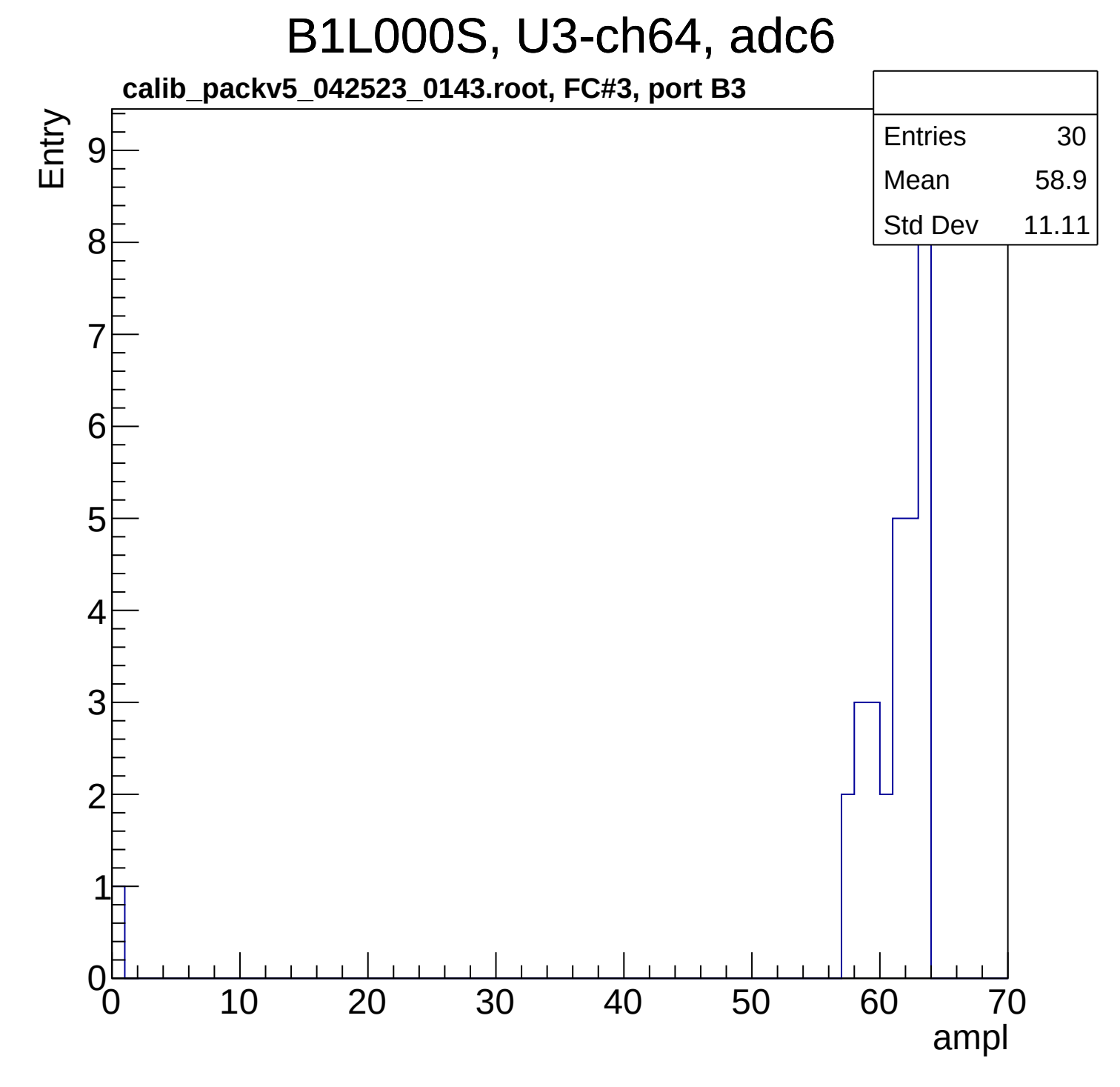
B1L000S, U3-ch64, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 30
Mean 58.9
Std Dev 11.11
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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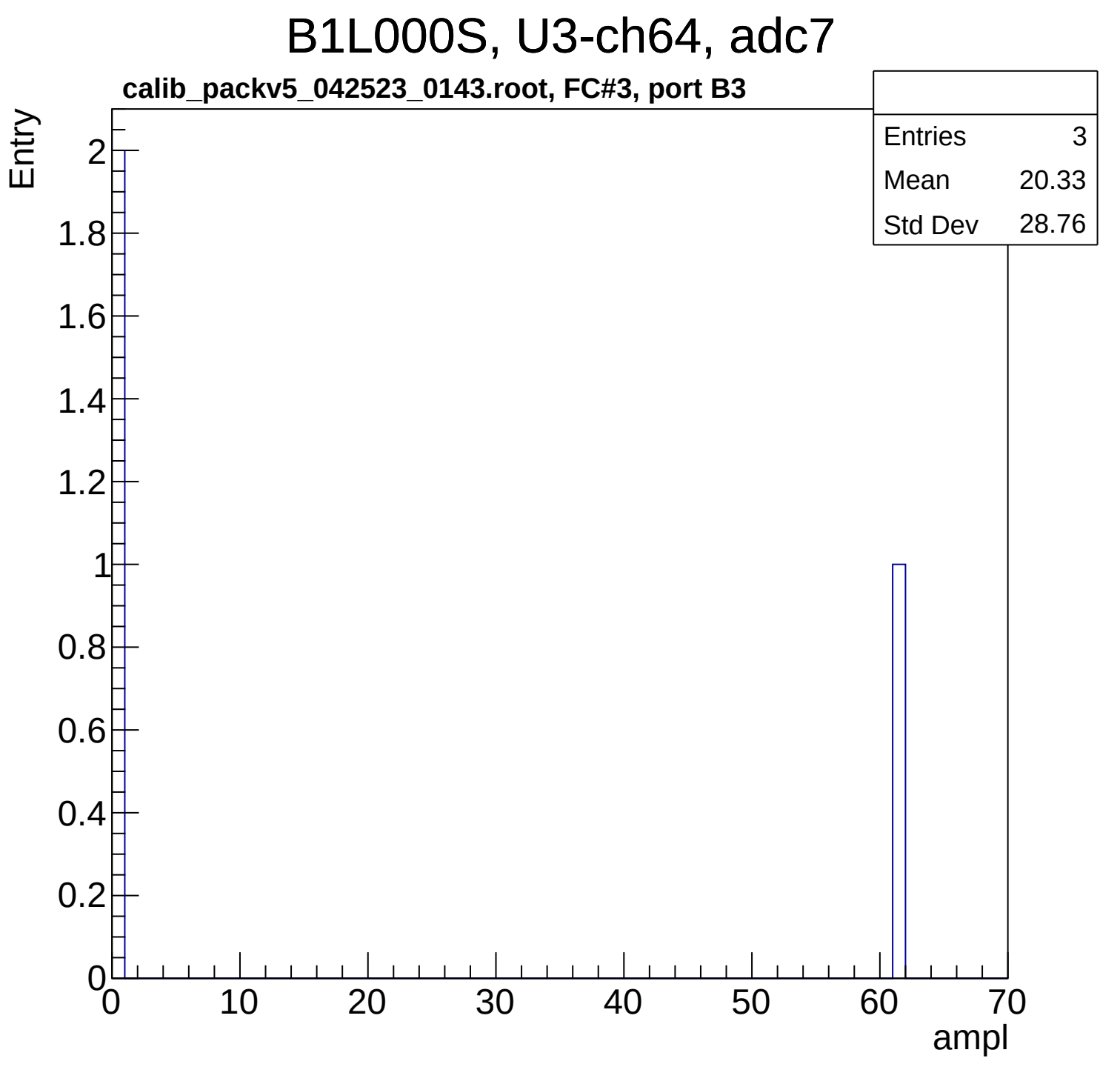
B1L000S, U3-ch64, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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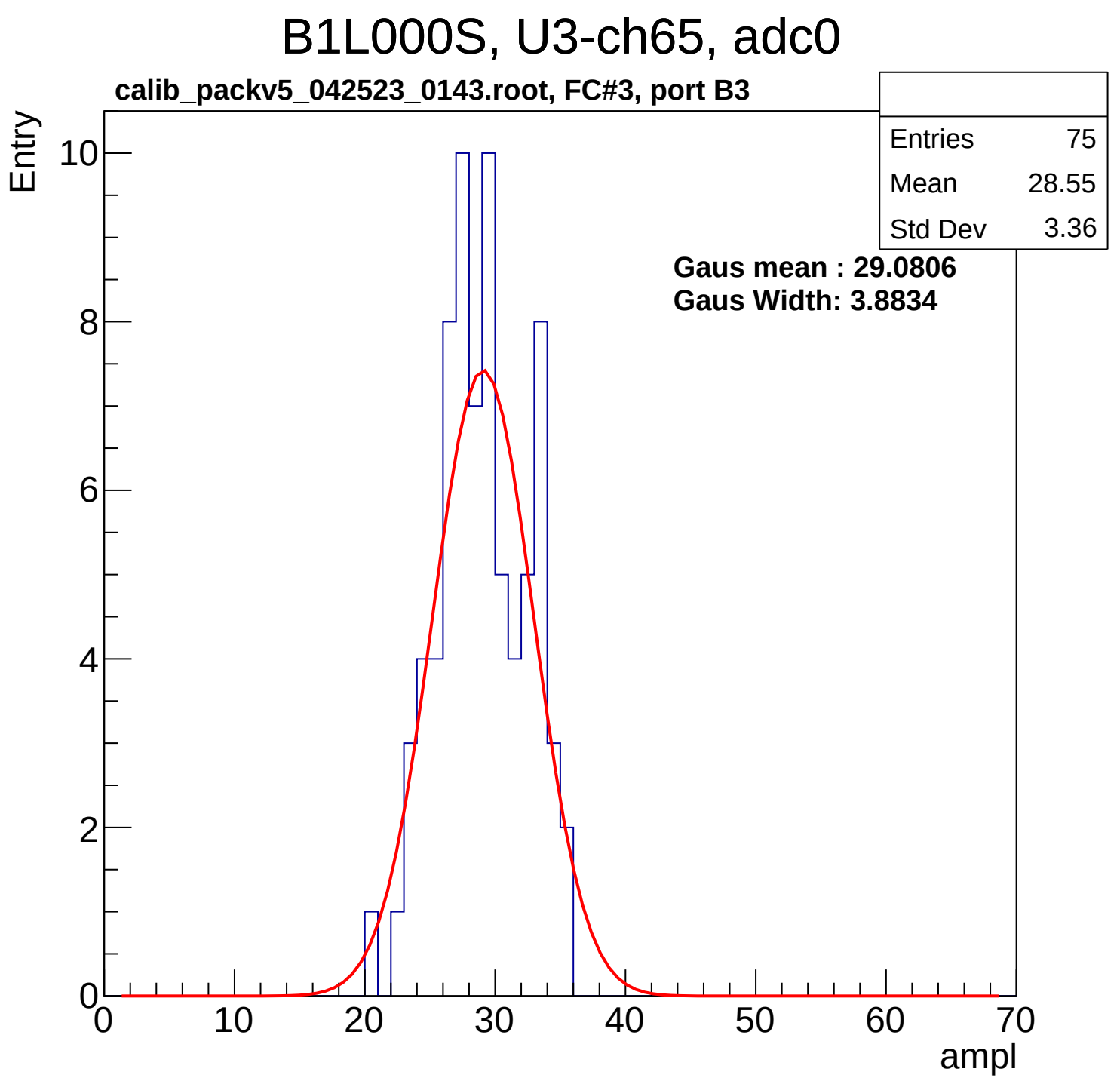
B1L000S, U3-ch65, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 28.55
Std Dev 3.36
Gaus mean : 29.0806
Gaus Width: 3.8834
Entry
ampl
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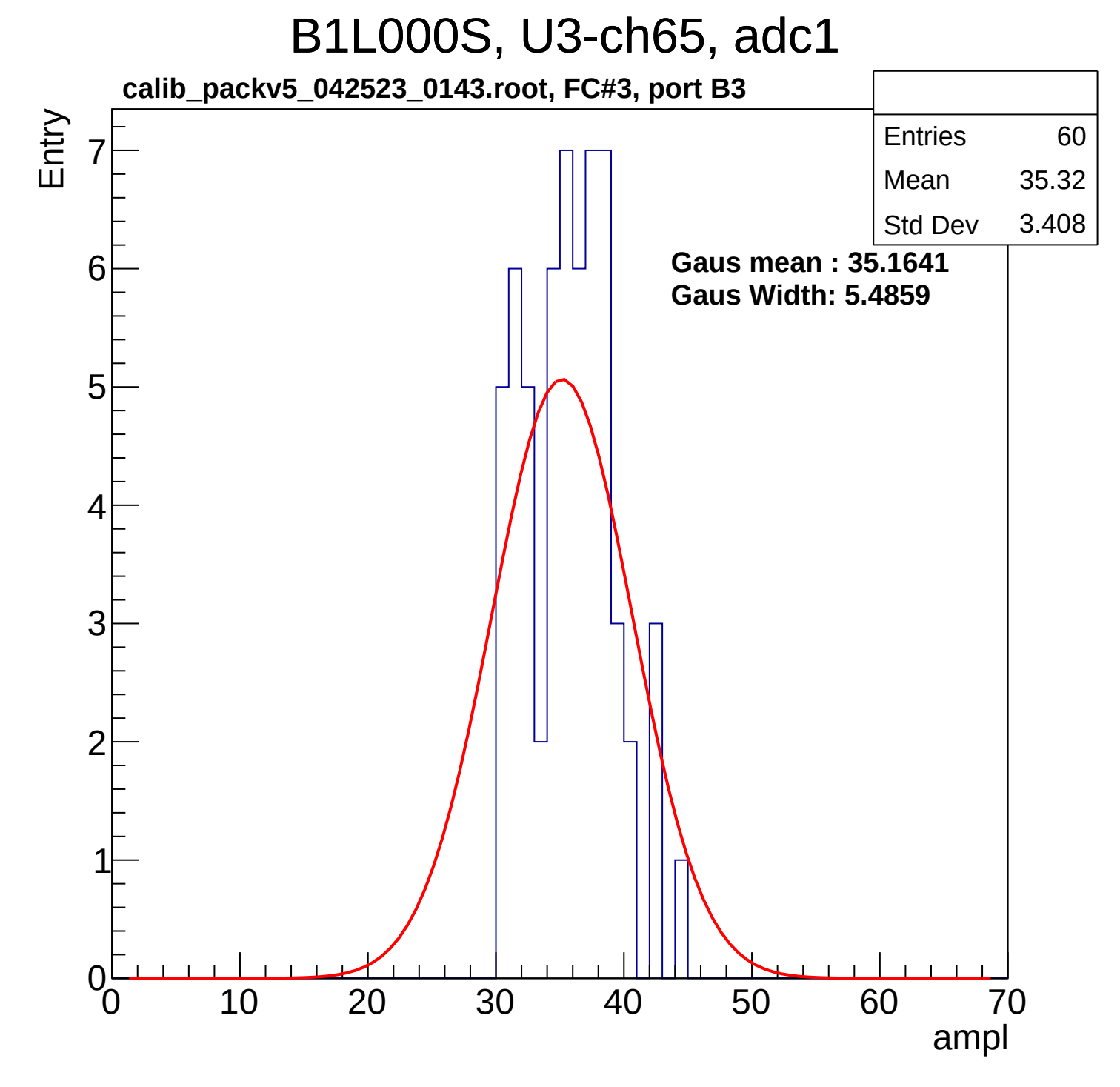
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch65, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 35.32
Std Dev 3.408
Gaus mean : 35.1641
Gaus Width: 5.4859
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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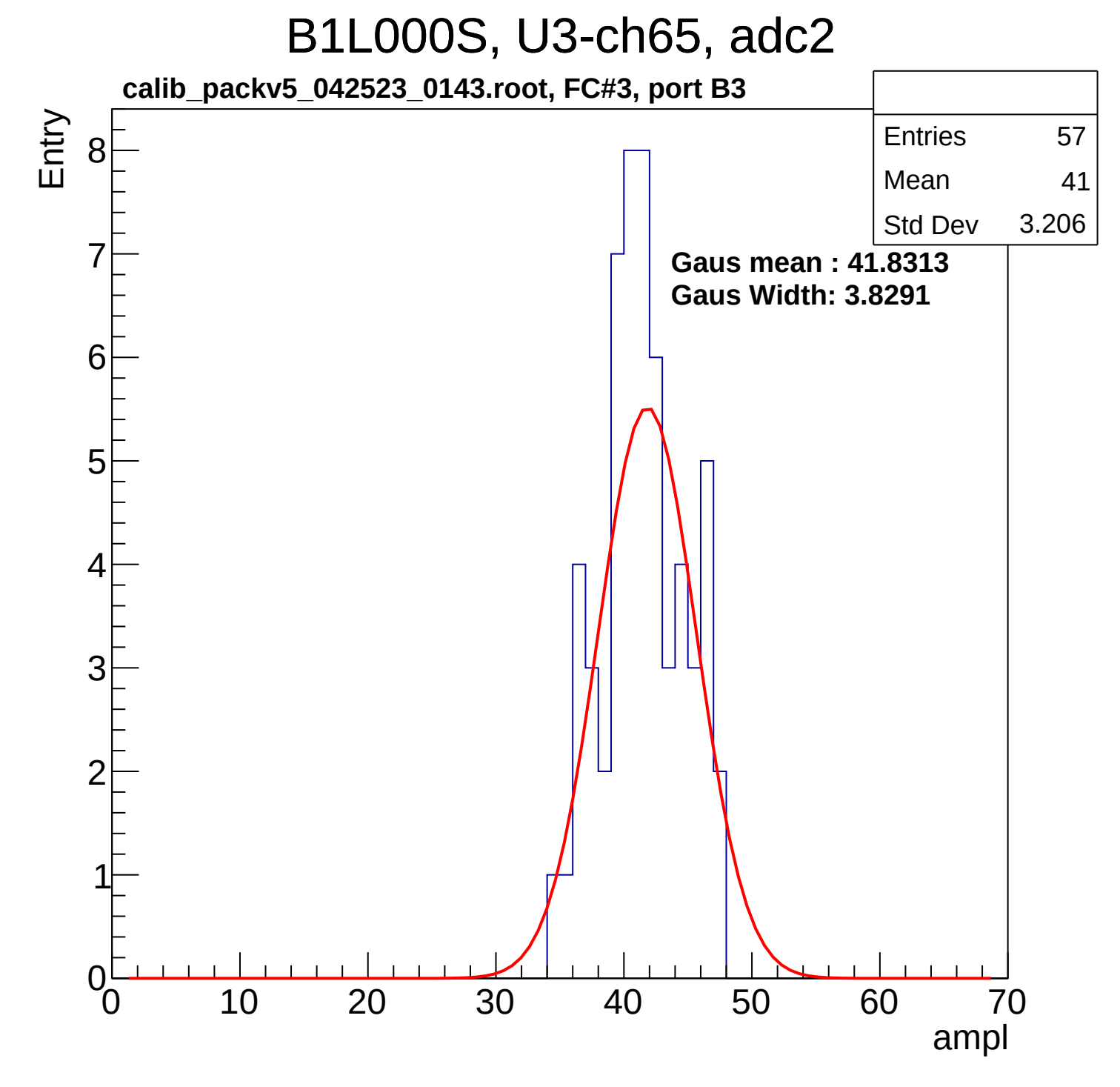
40
50
60
70


B1L000S, U3-ch65, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41
Std Dev 3.206
Gaus mean : 41.8313
Gaus Width: 3.8291
Entry
ampl
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1
2
3
4
5
6
7
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0
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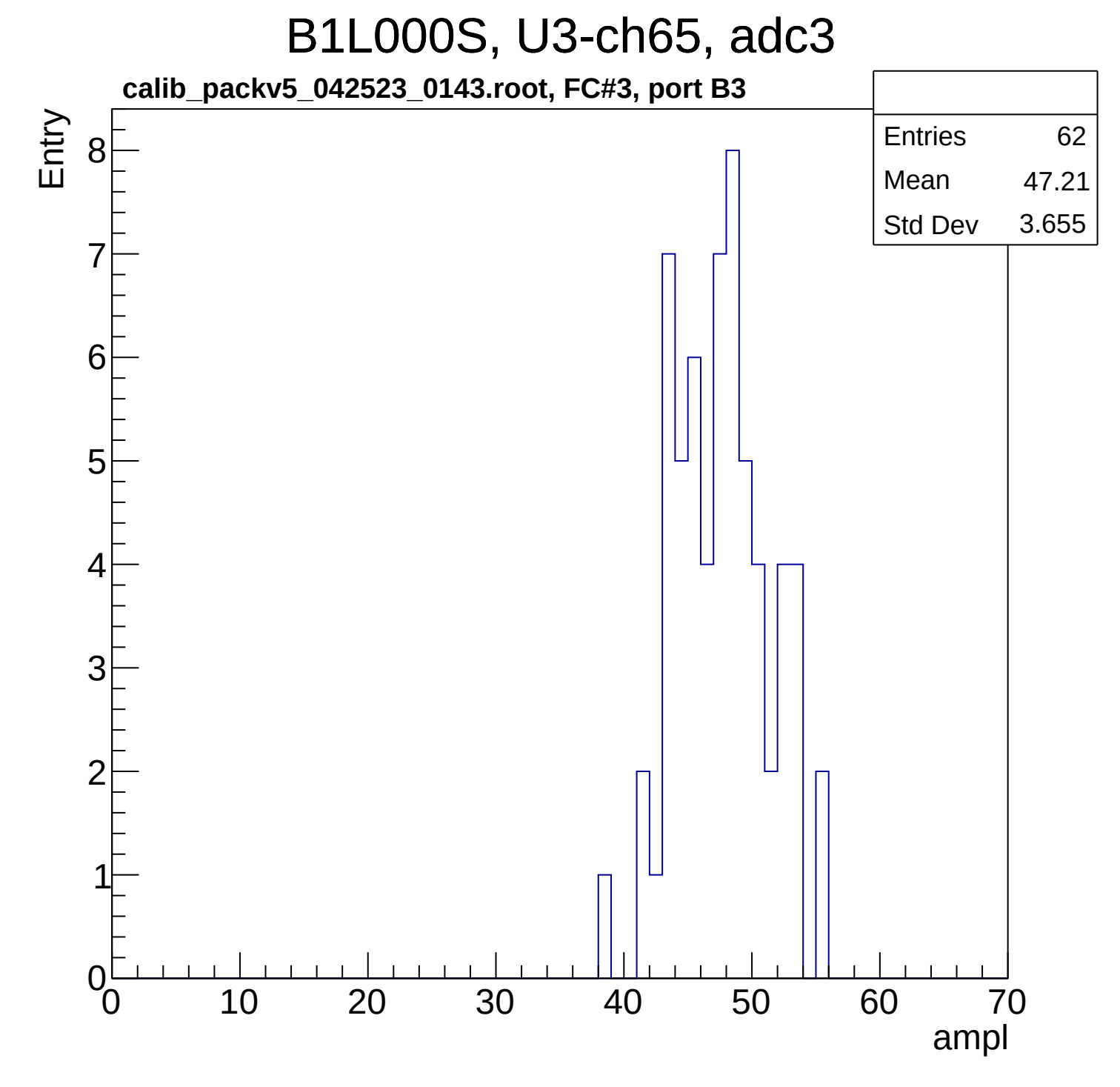
B1L000S, U3-ch65, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 47.21
Std Dev 3.655
Entry
ampl
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4
5
6
7
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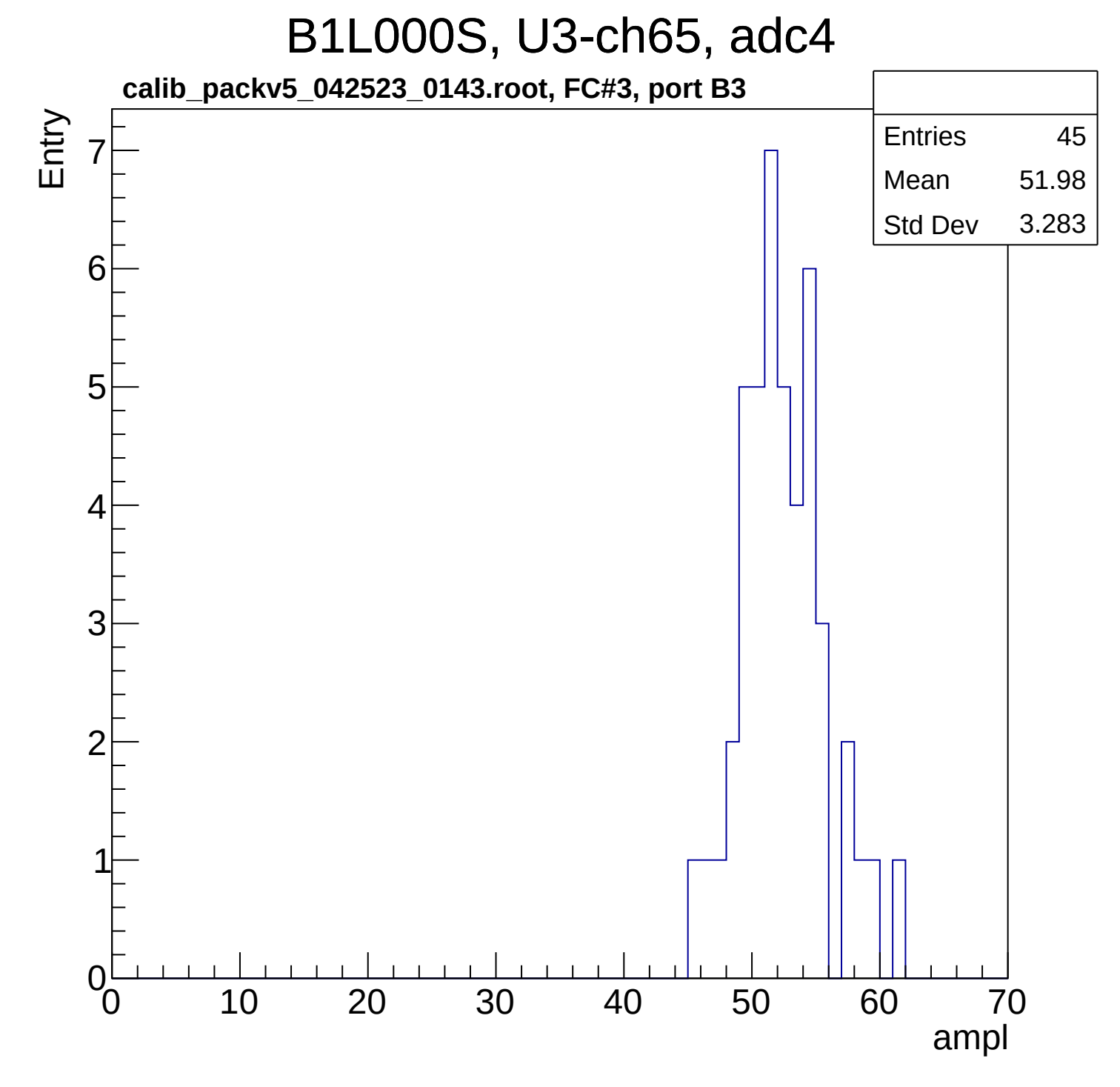
B1L000S, U3-ch65, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 51.98
Std Dev 3.283
Entry
ampl
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0
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60
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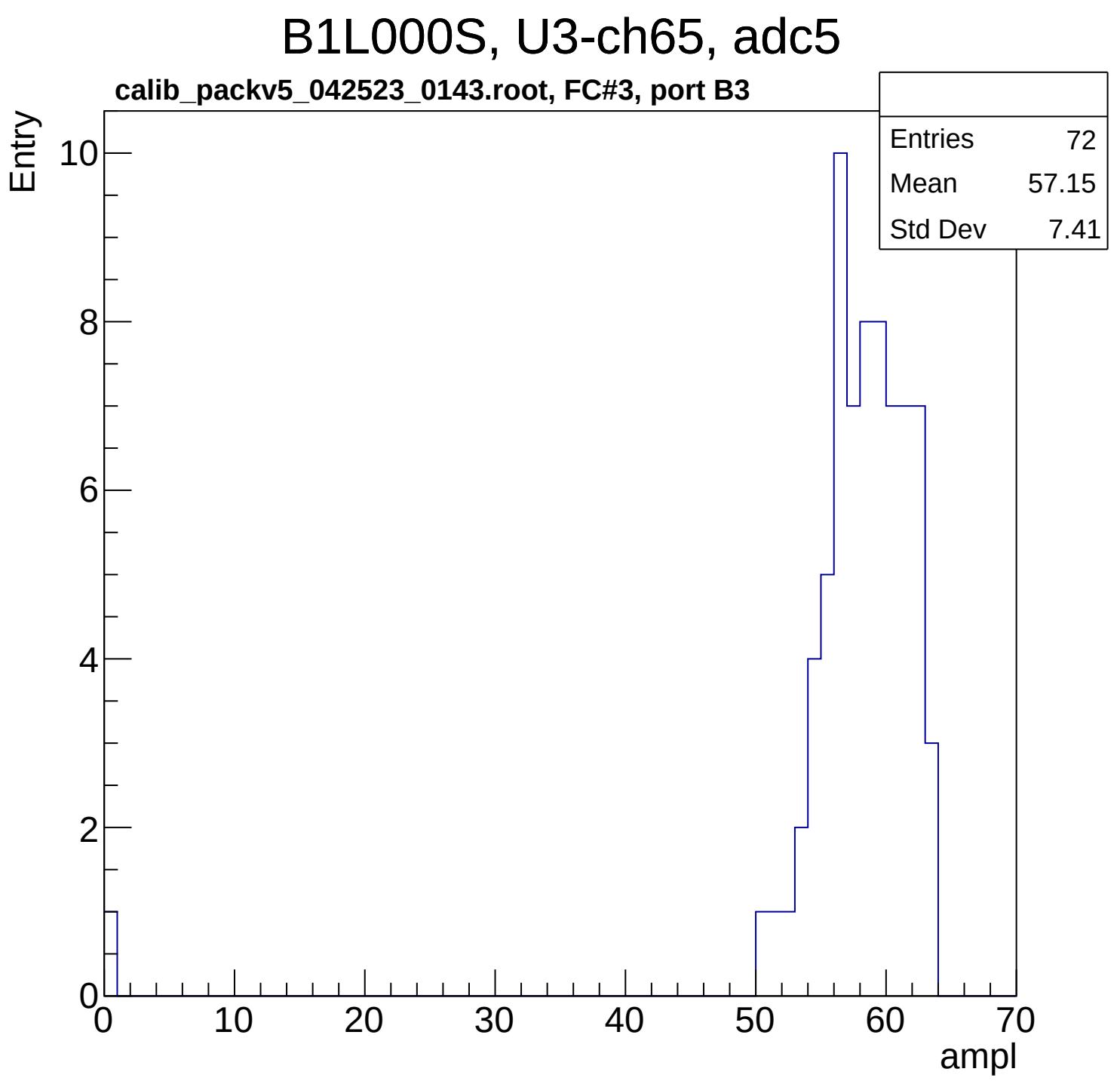
B1L000S, U3-ch65, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 57.15
Std Dev 7.41
Entry
0
2
4
6
8
10
0
10
20
30
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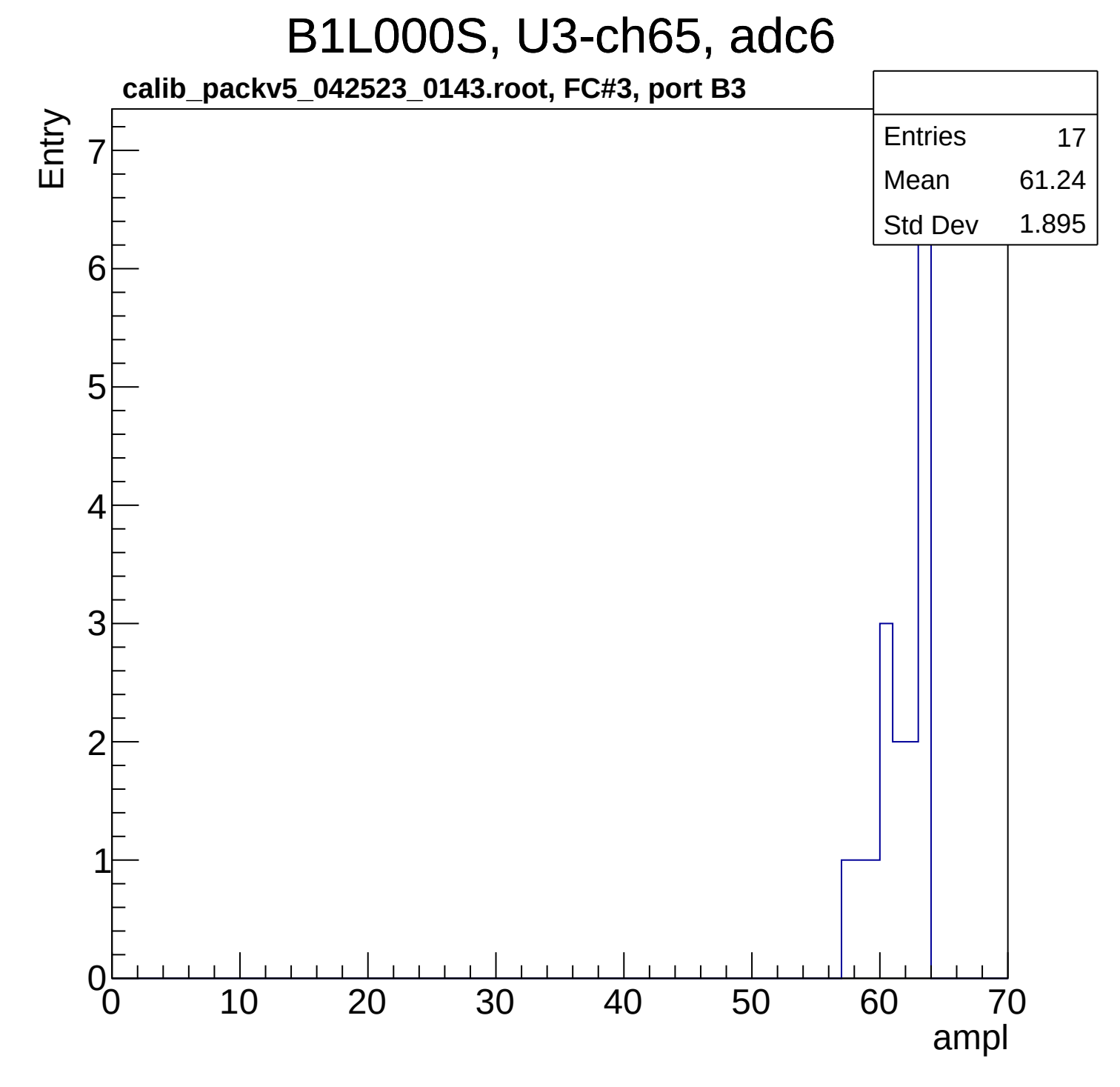
50
60
70
ampl


B1L000S, U3-ch65, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.24
Std Dev 1.895
Entry
0
1
2
3
4
5
6
7
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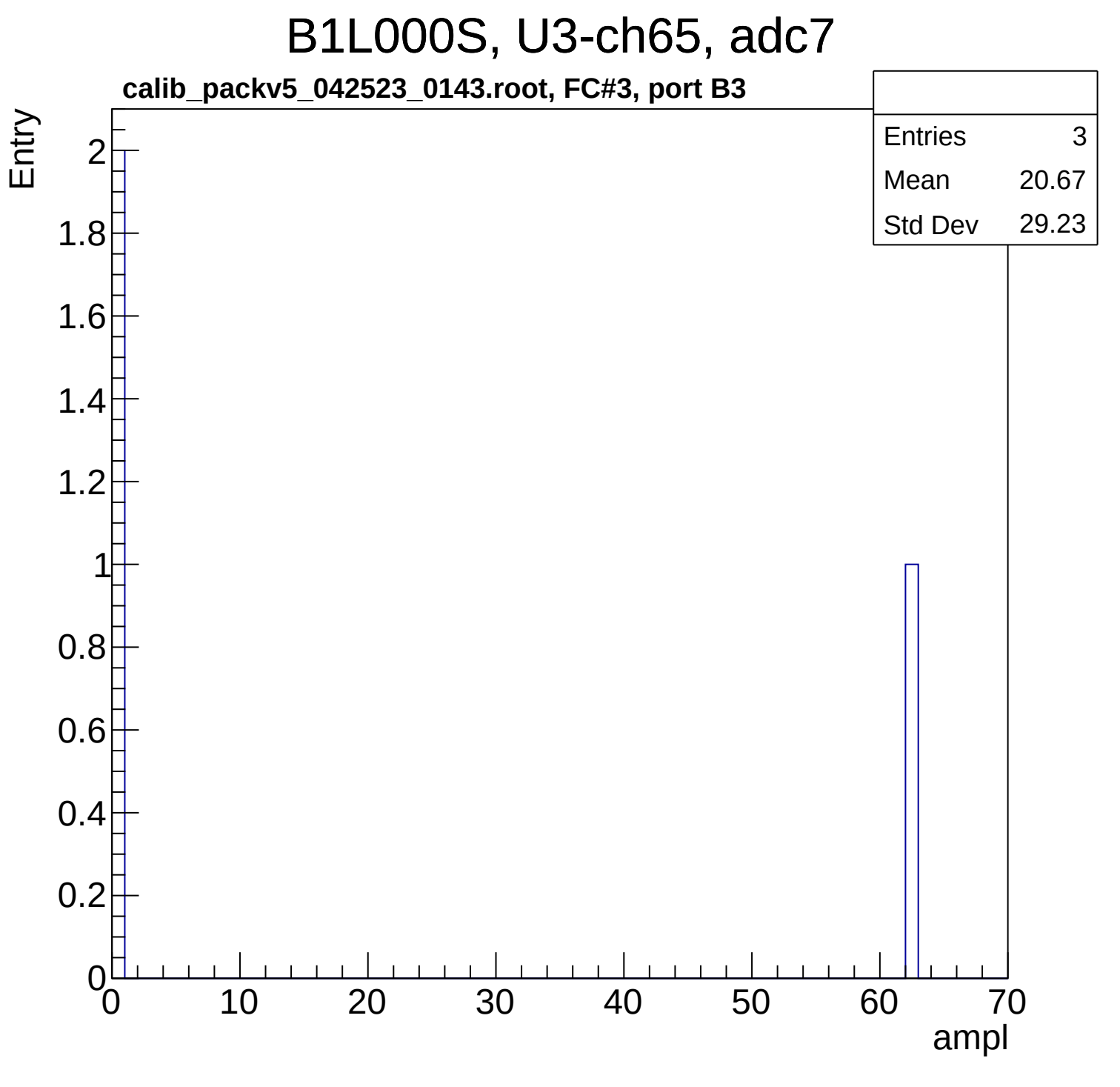
70
ampl


B1L000S, U3-ch65, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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60
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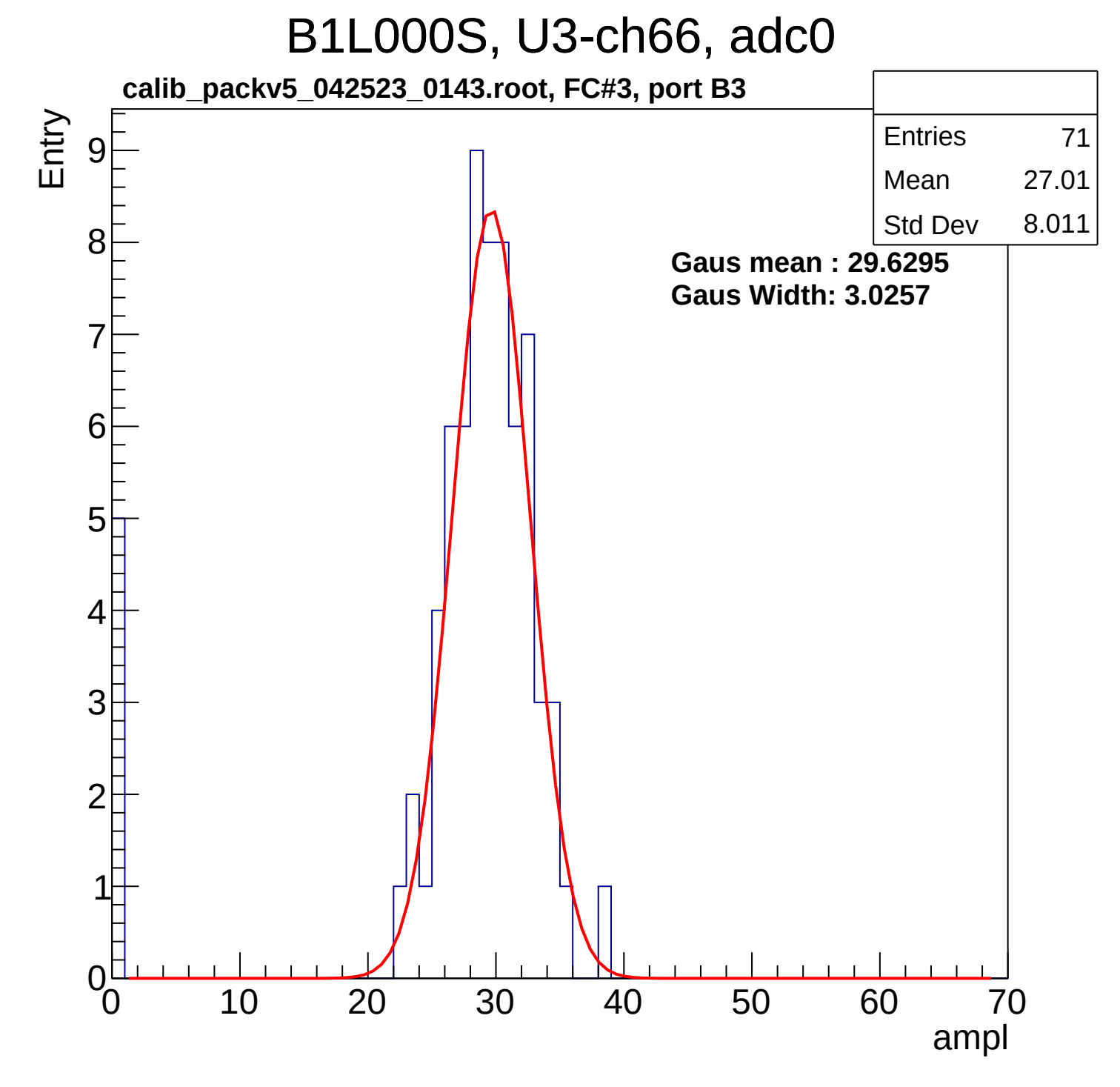
B1L000S, U3-ch66, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 27.01
Std Dev 8.011
Gaus mean : 29.6295
Gaus Width: 3.0257
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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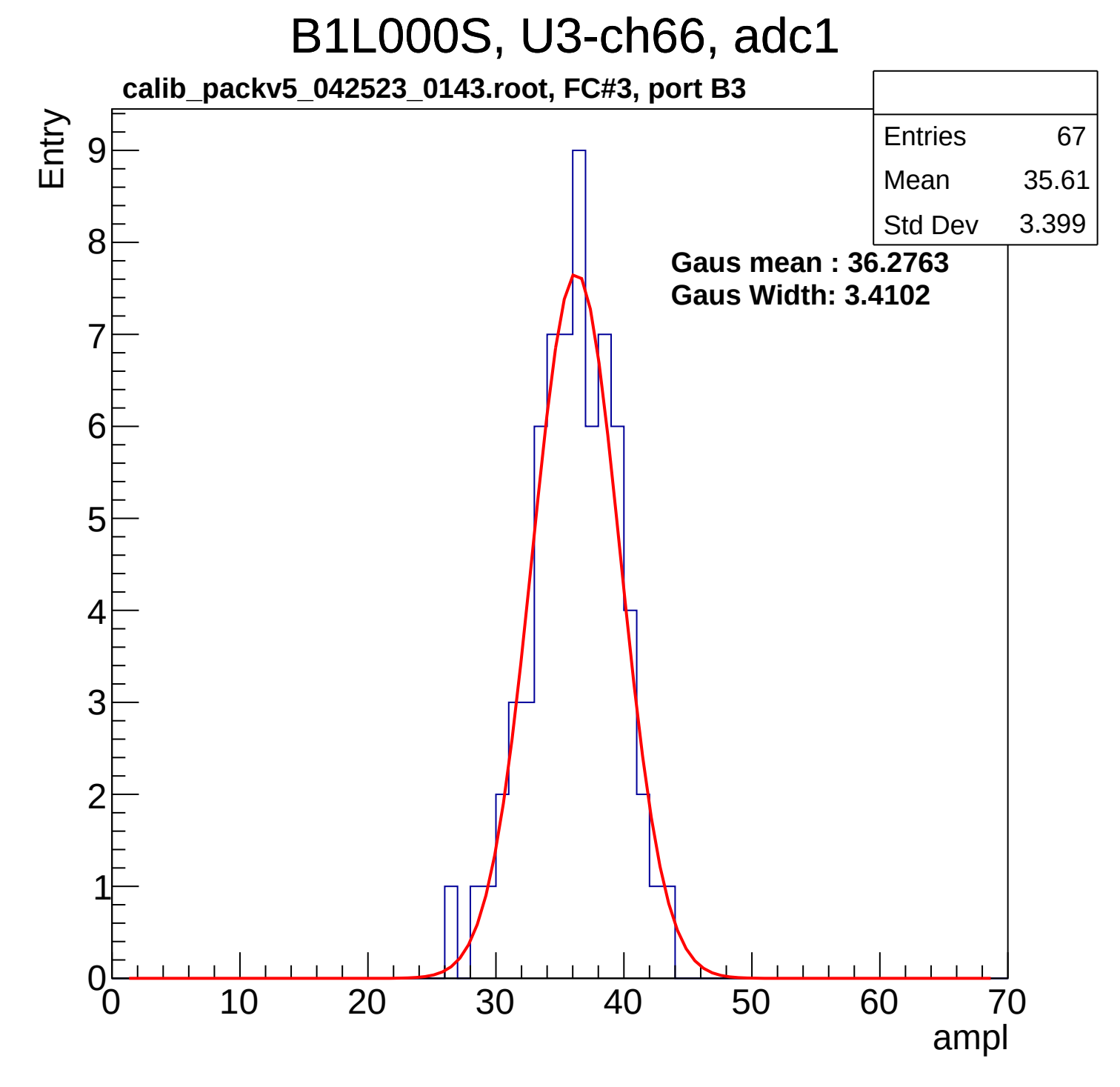
B1L000S, U3-ch66, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.61
Std Dev 3.399
Gaus mean : 36.2763
Gaus Width: 3.4102
Entry
ampl
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2
3
4
5
6
7
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0
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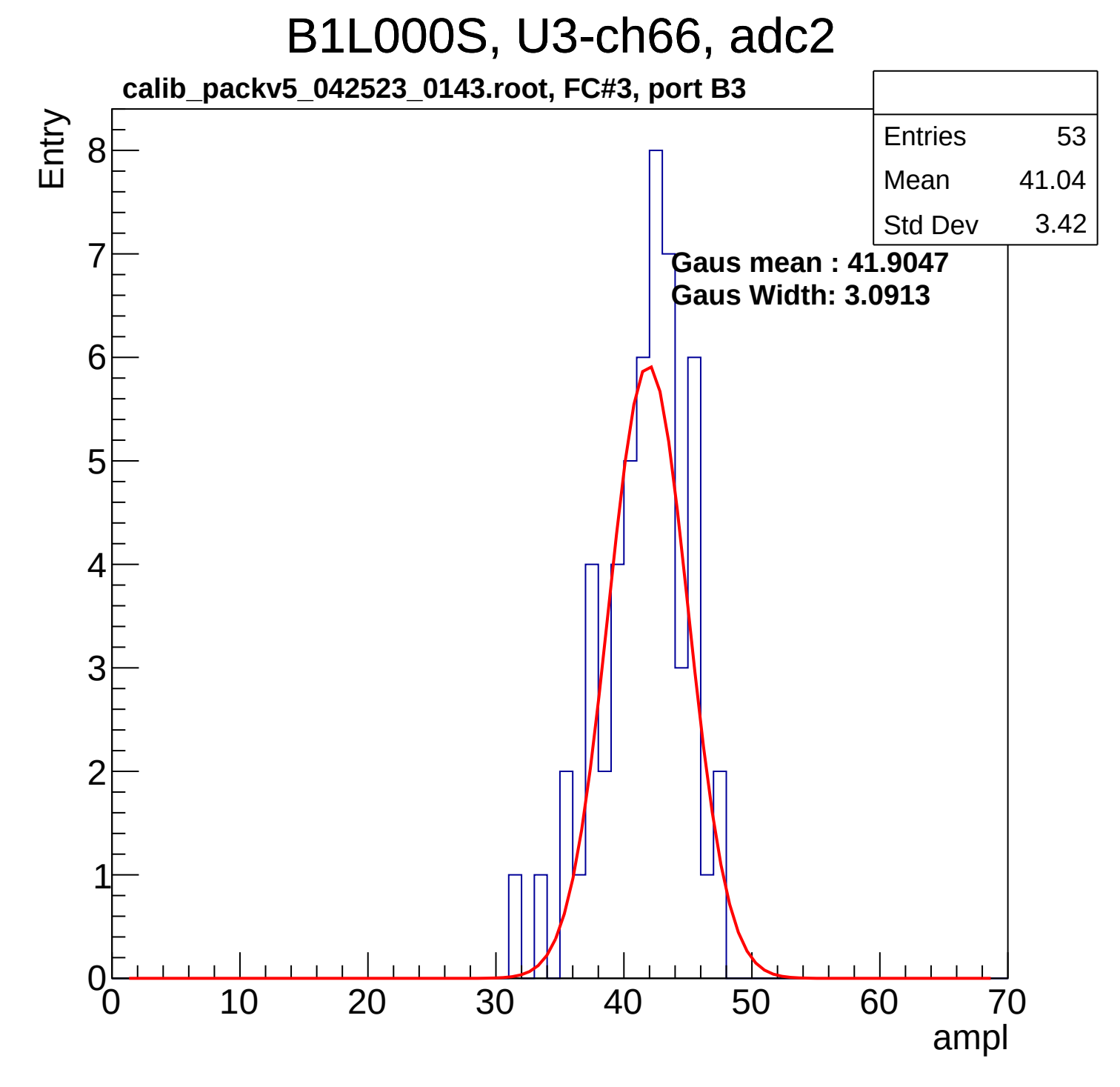
B1L000S, U3-ch66, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 41.04
Std Dev 3.42
Gaus mean : 41.9047
Gaus Width: 3.0913
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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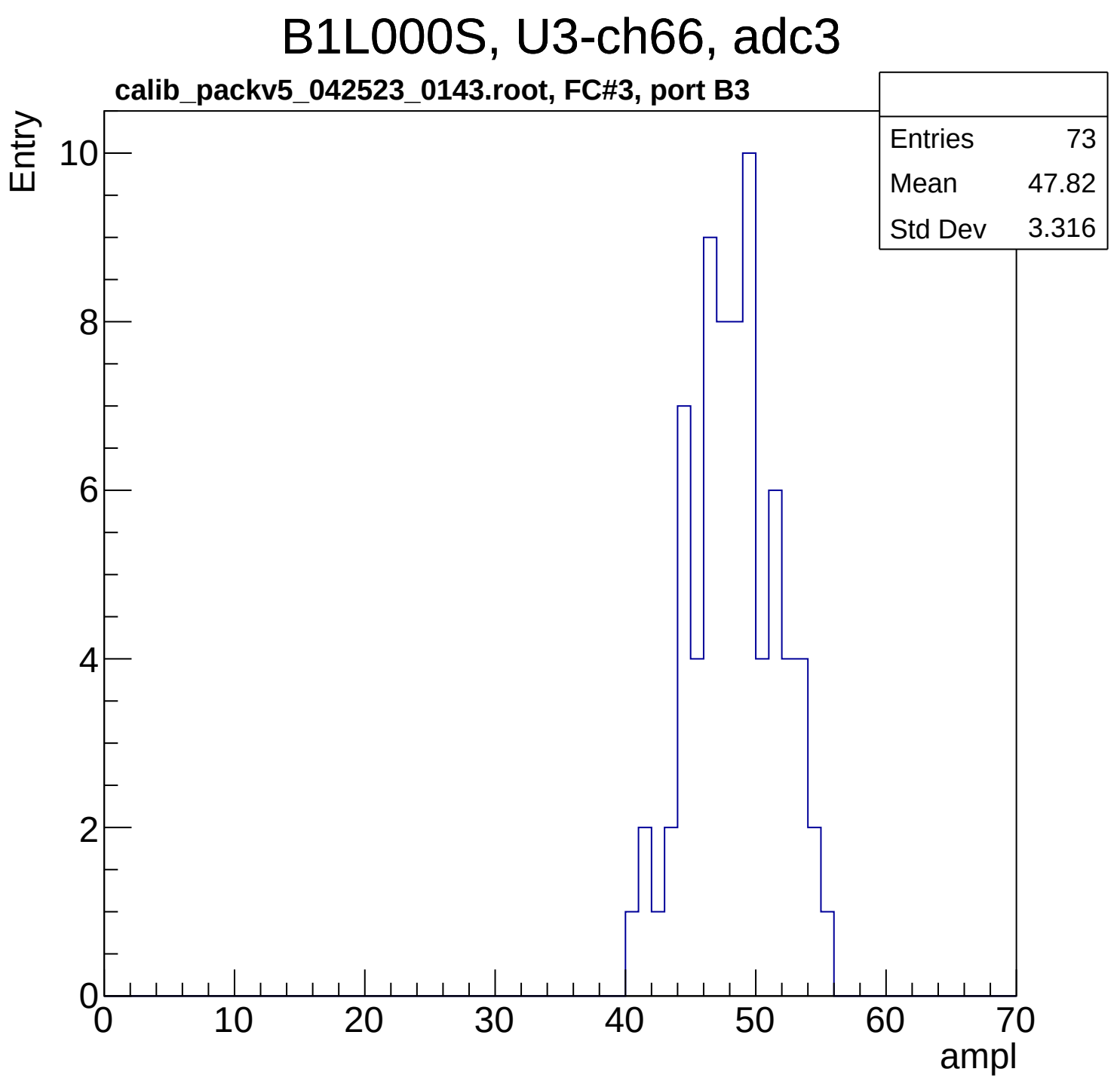
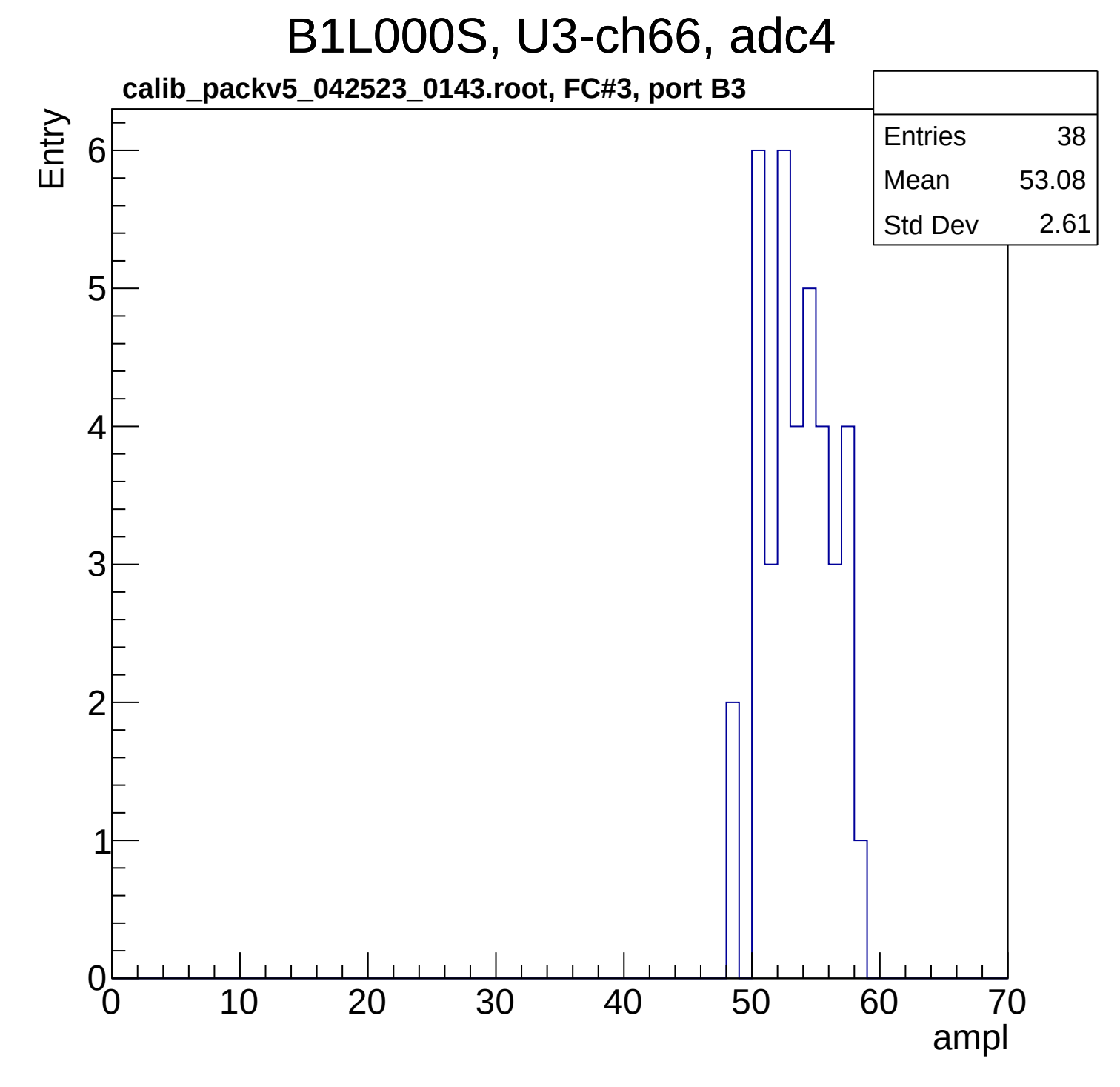
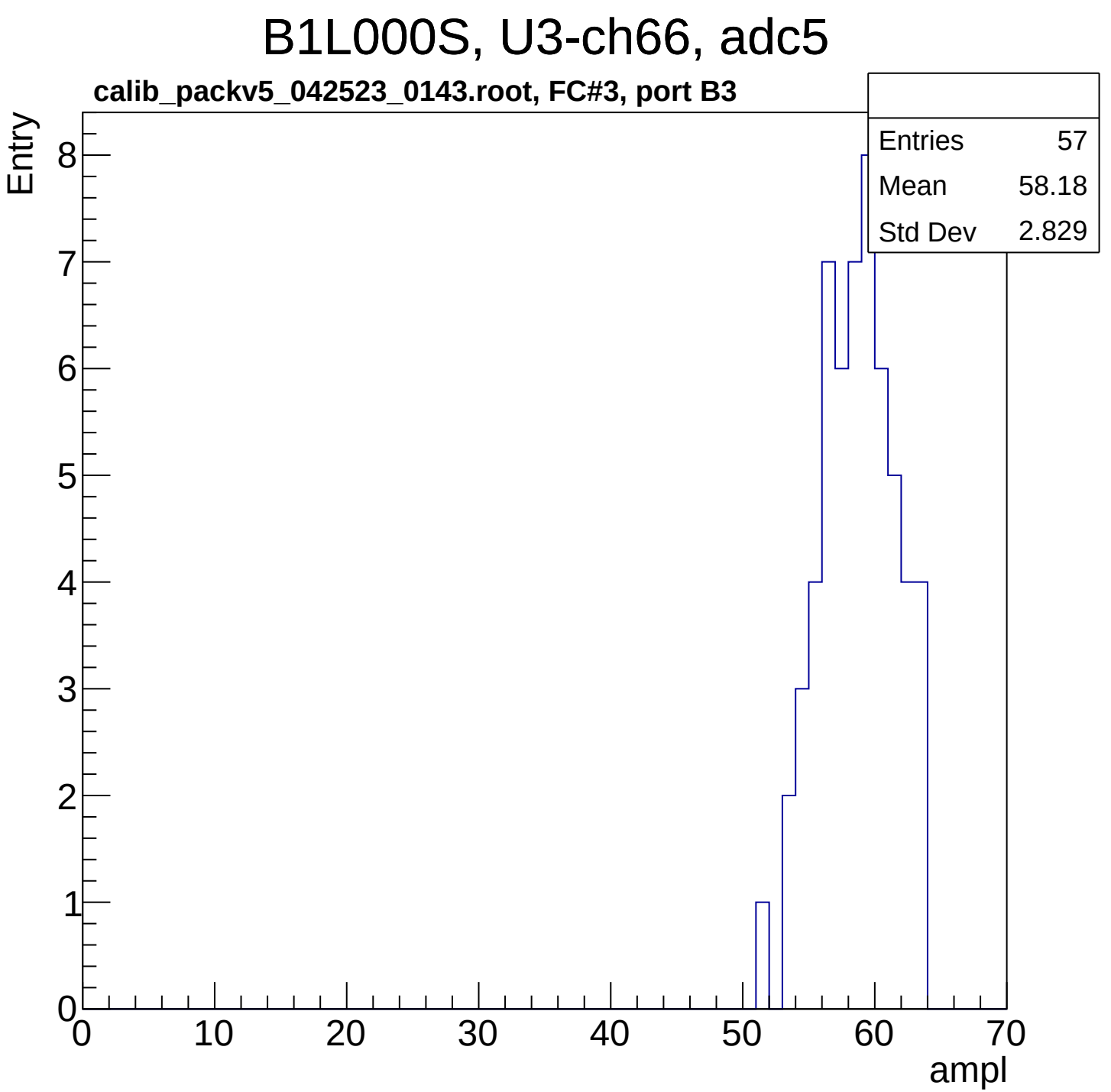

B1L000S, U3-ch66, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 47.82
Std Dev 3.316
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch66, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 38
Mean 53.08
Std Dev 2.61
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch66, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.18
Std Dev 2.829
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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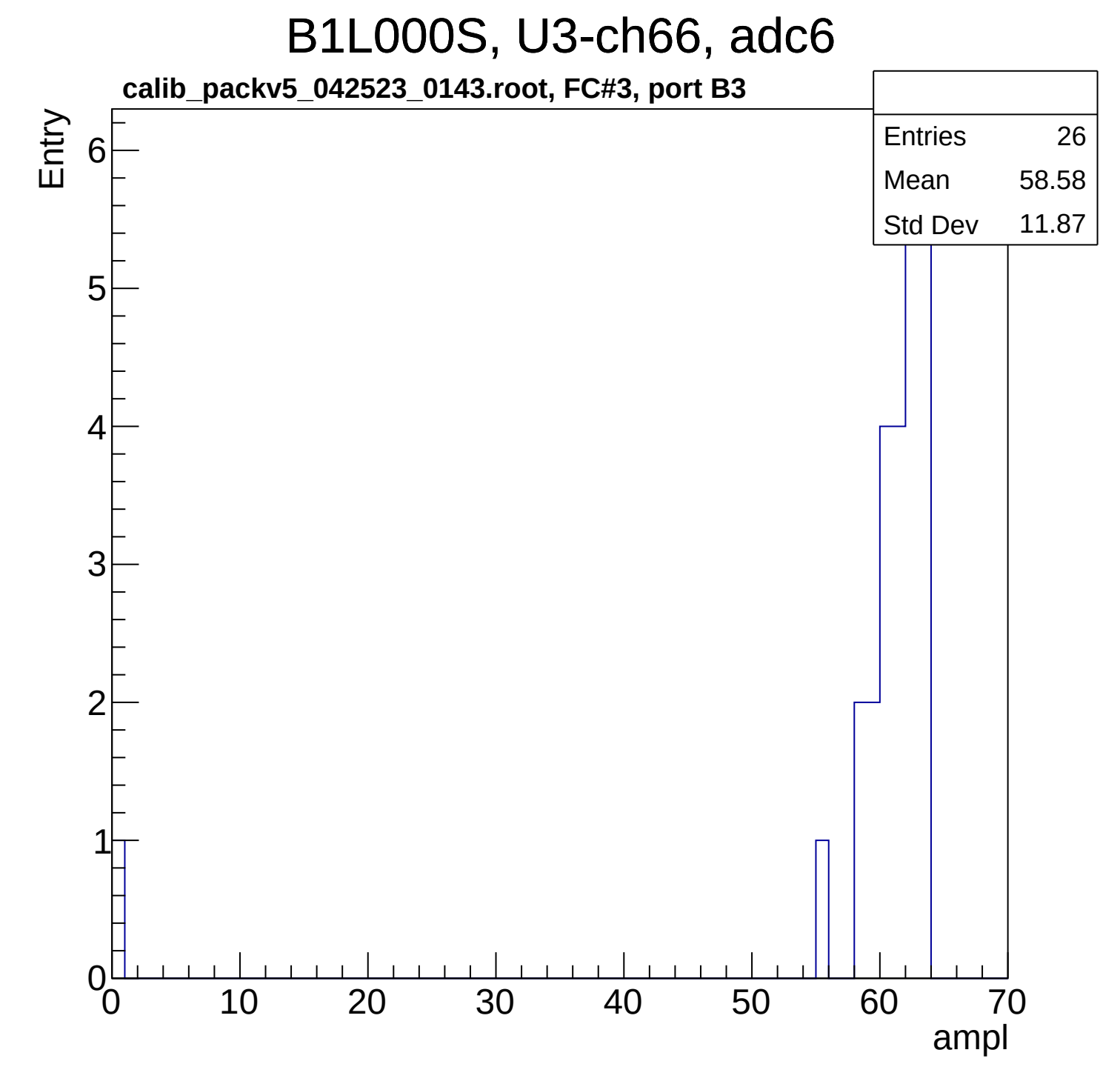
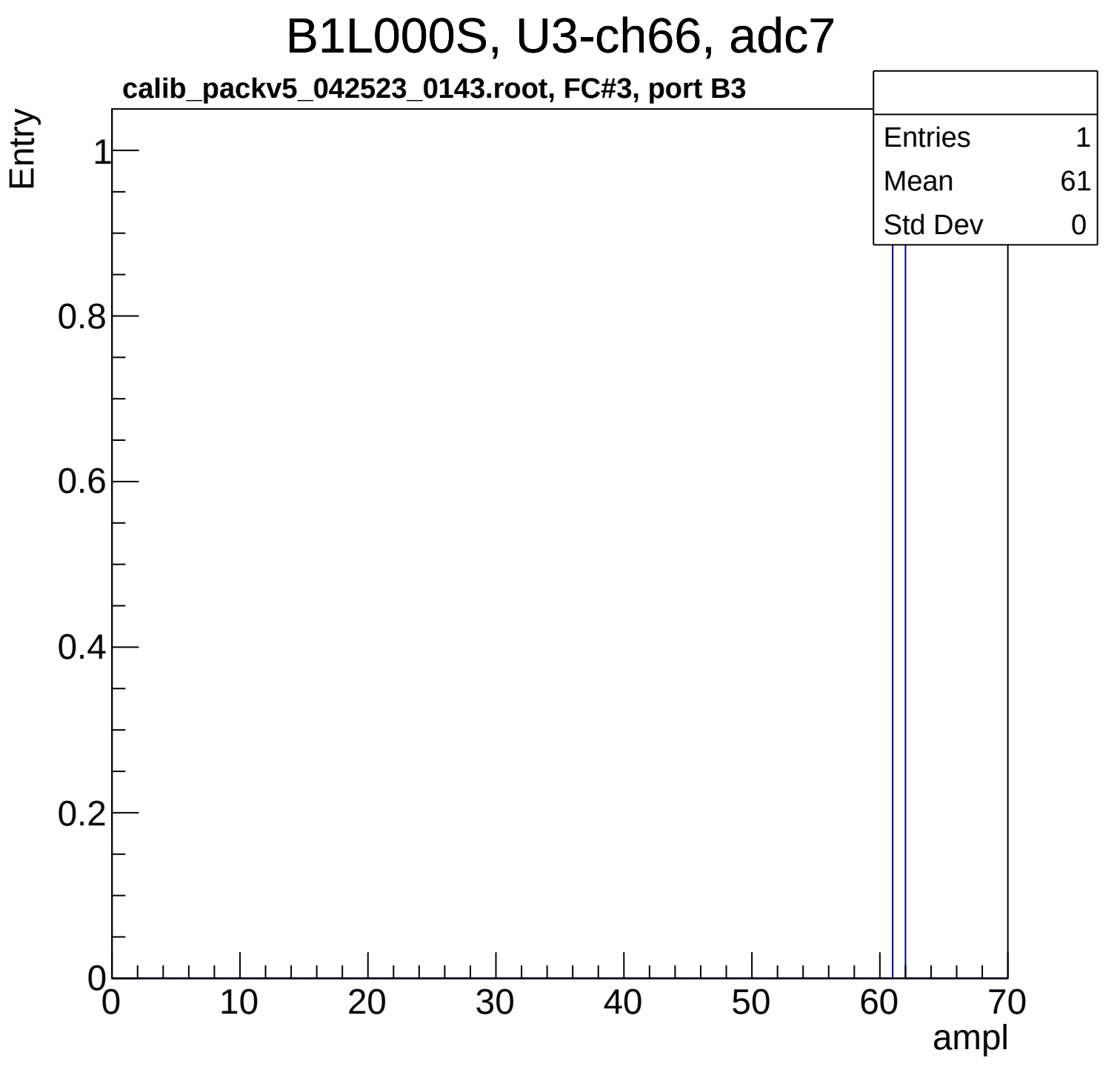
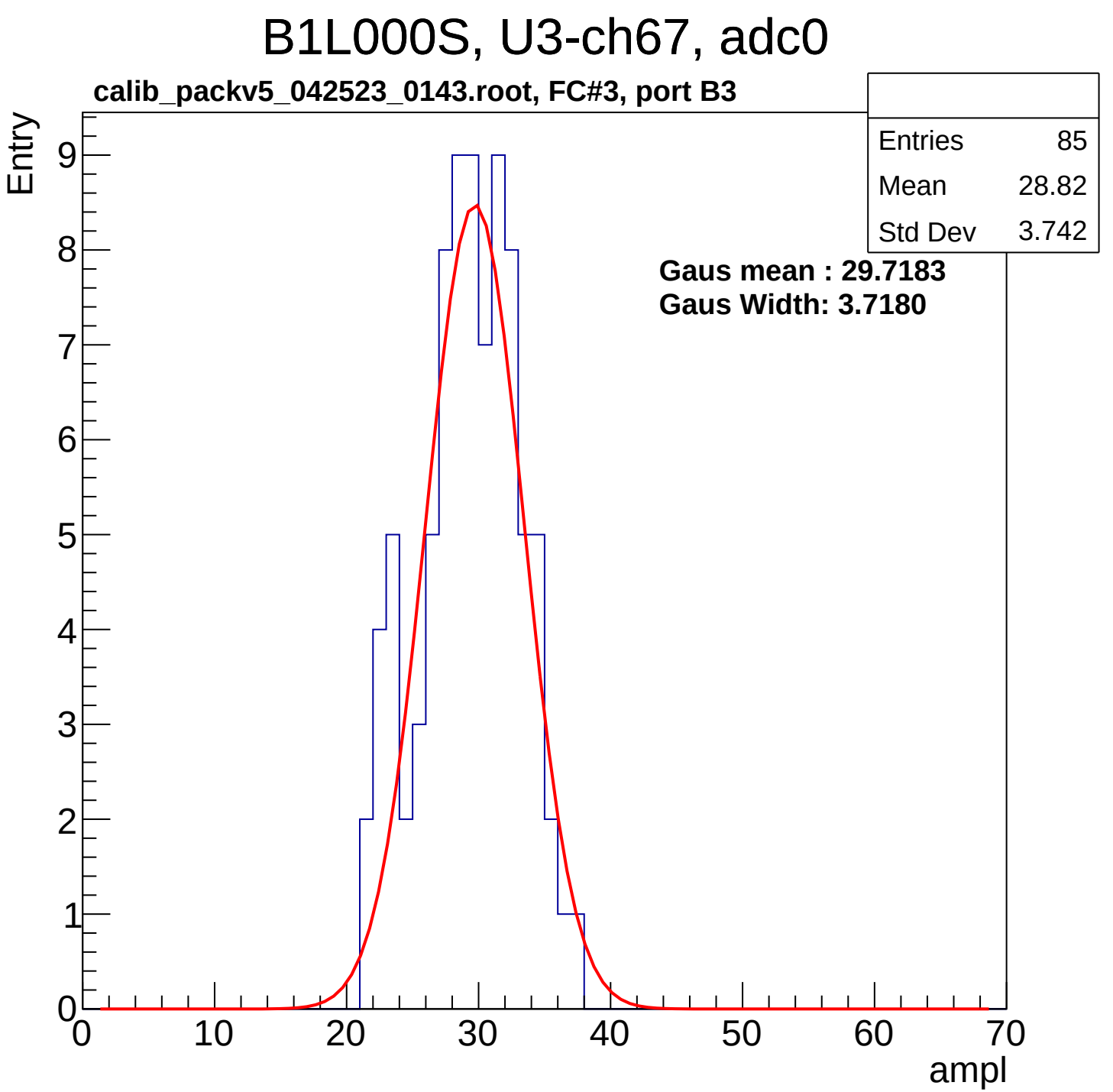

B1L000S, U3-ch66, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 58.58
Std Dev 11.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch66, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch67, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 85
Mean 28.82
Std Dev 3.742
Gaus mean : 29.7183
Gaus Width: 3.7180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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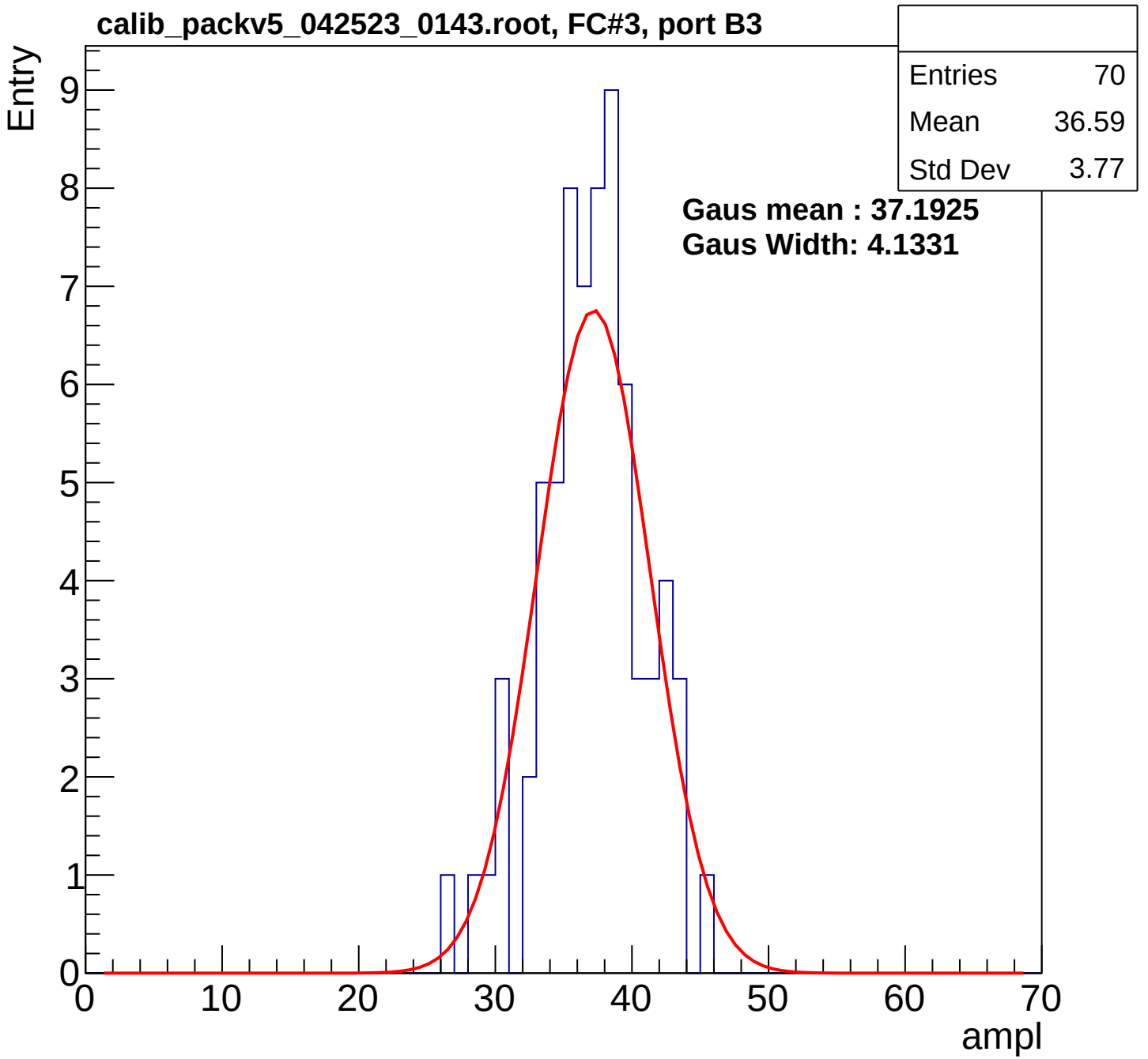
B1L000S, U3-ch67, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 36.59
Std Dev 3.77
Gaus mean : 37.1925
Gaus Width: 4.1331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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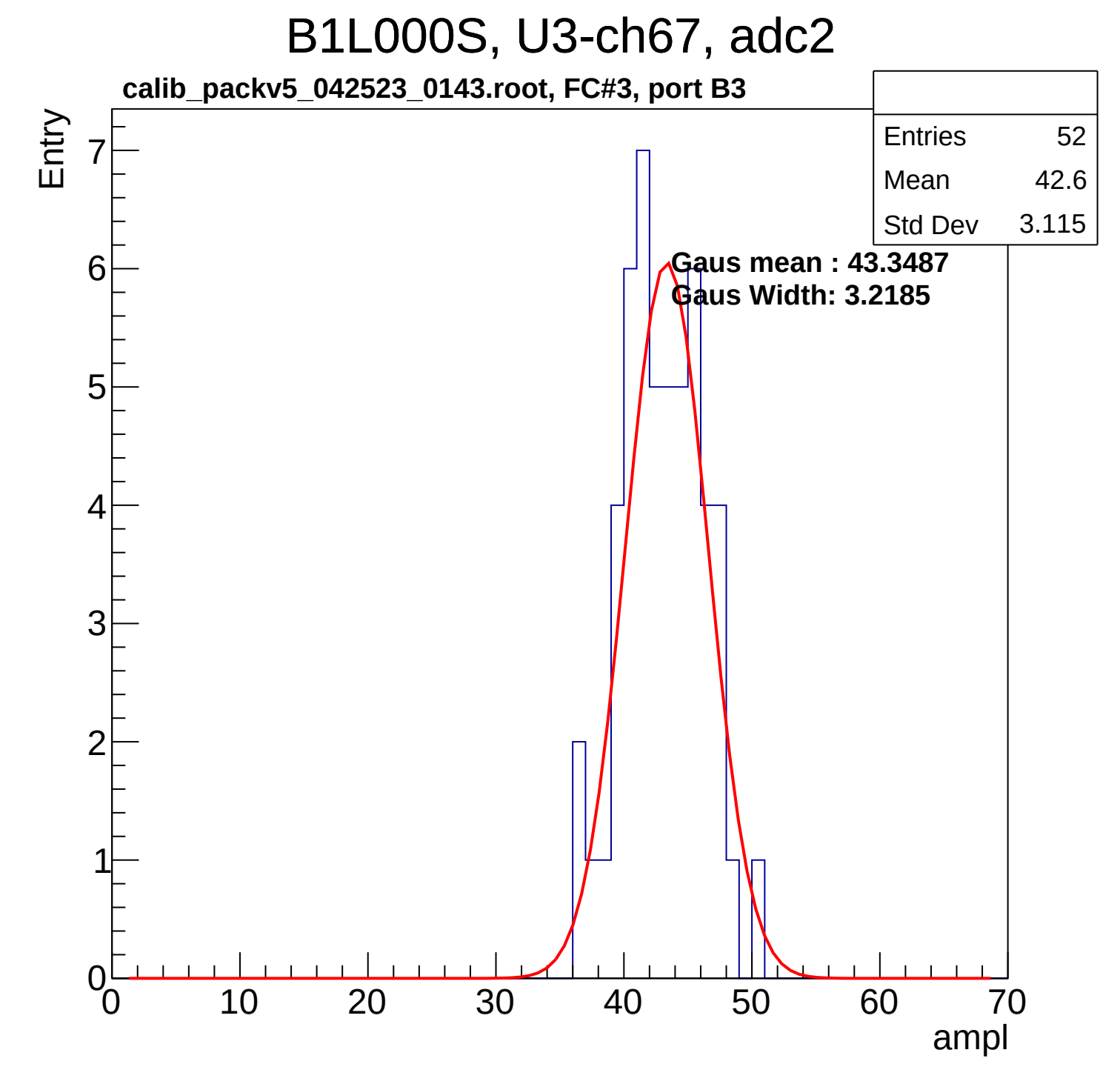
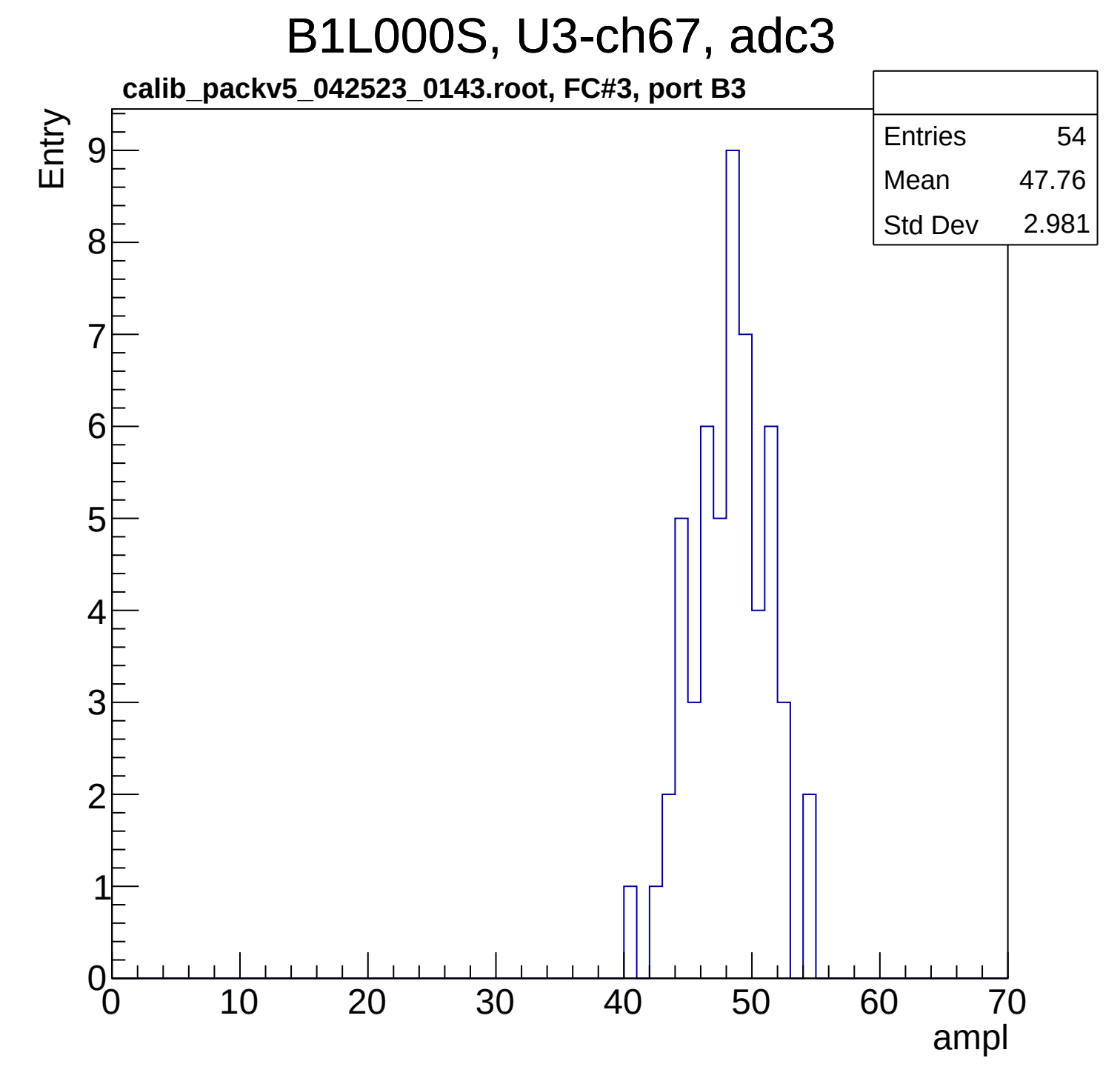

B1L000S, U3-ch67, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 42.6
Std Dev 3.115
Gaus mean : 43.3487
Gaus Width: 3.2185
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch67, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 47.76
Std Dev 2.981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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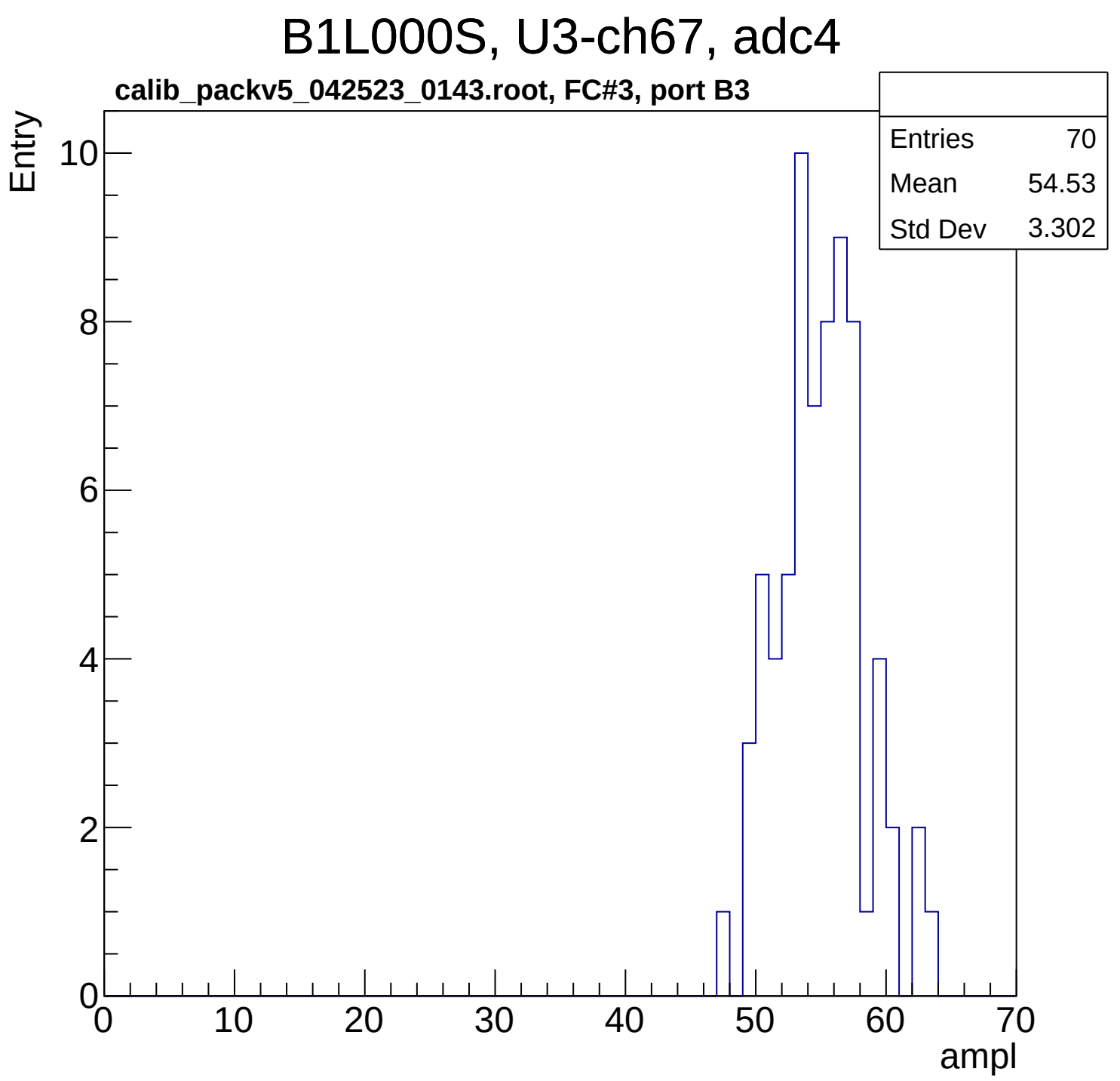
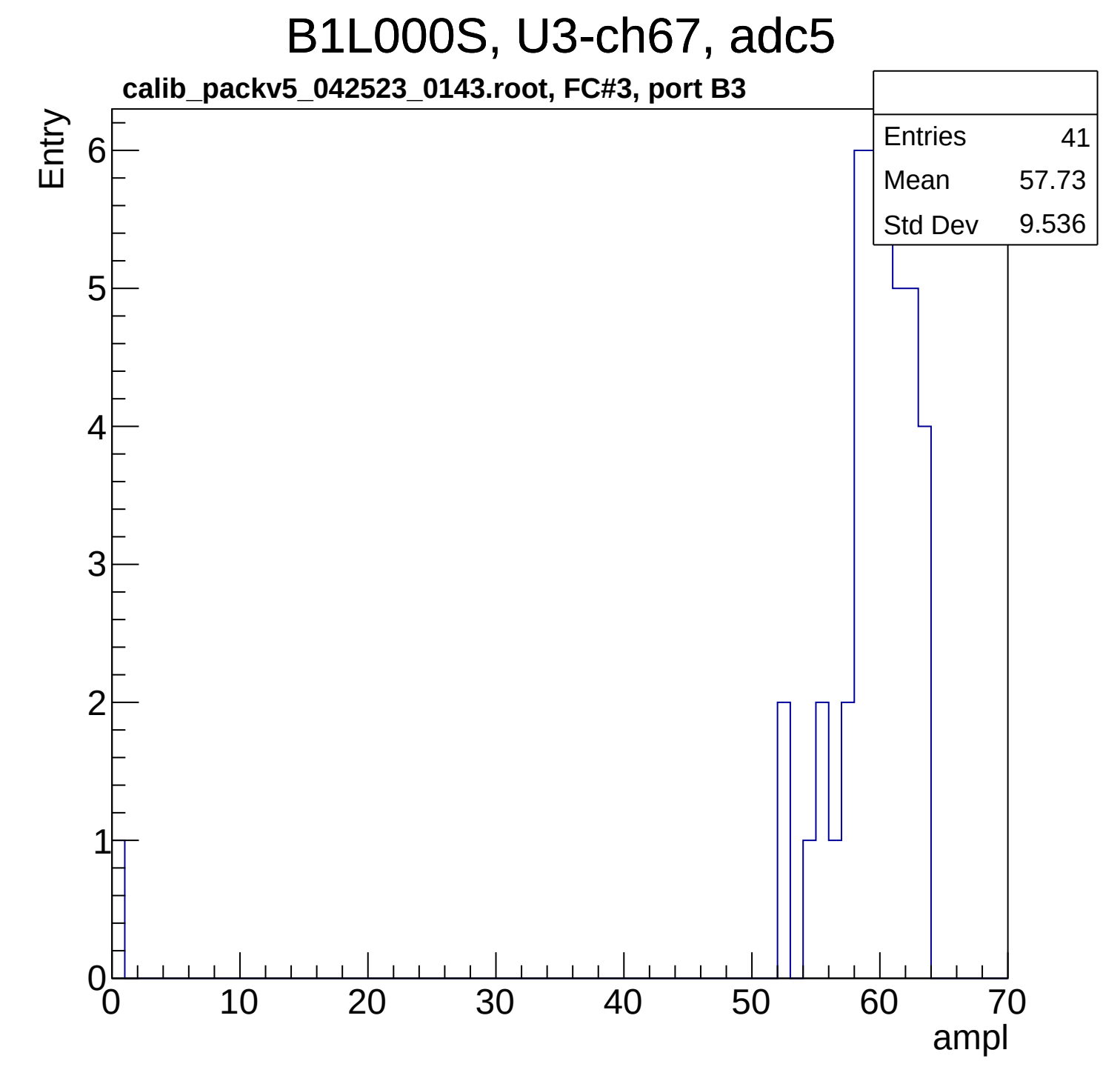
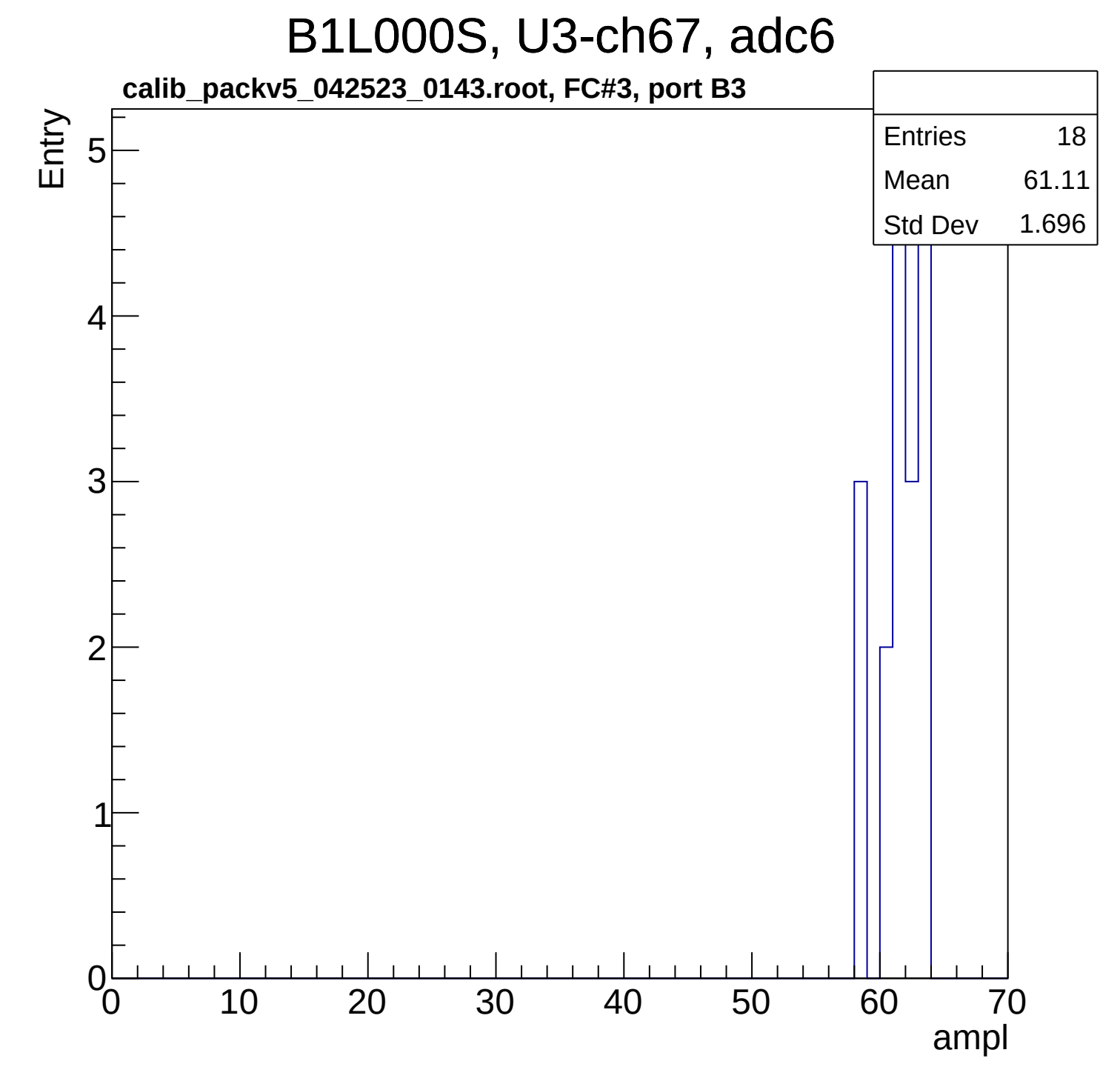
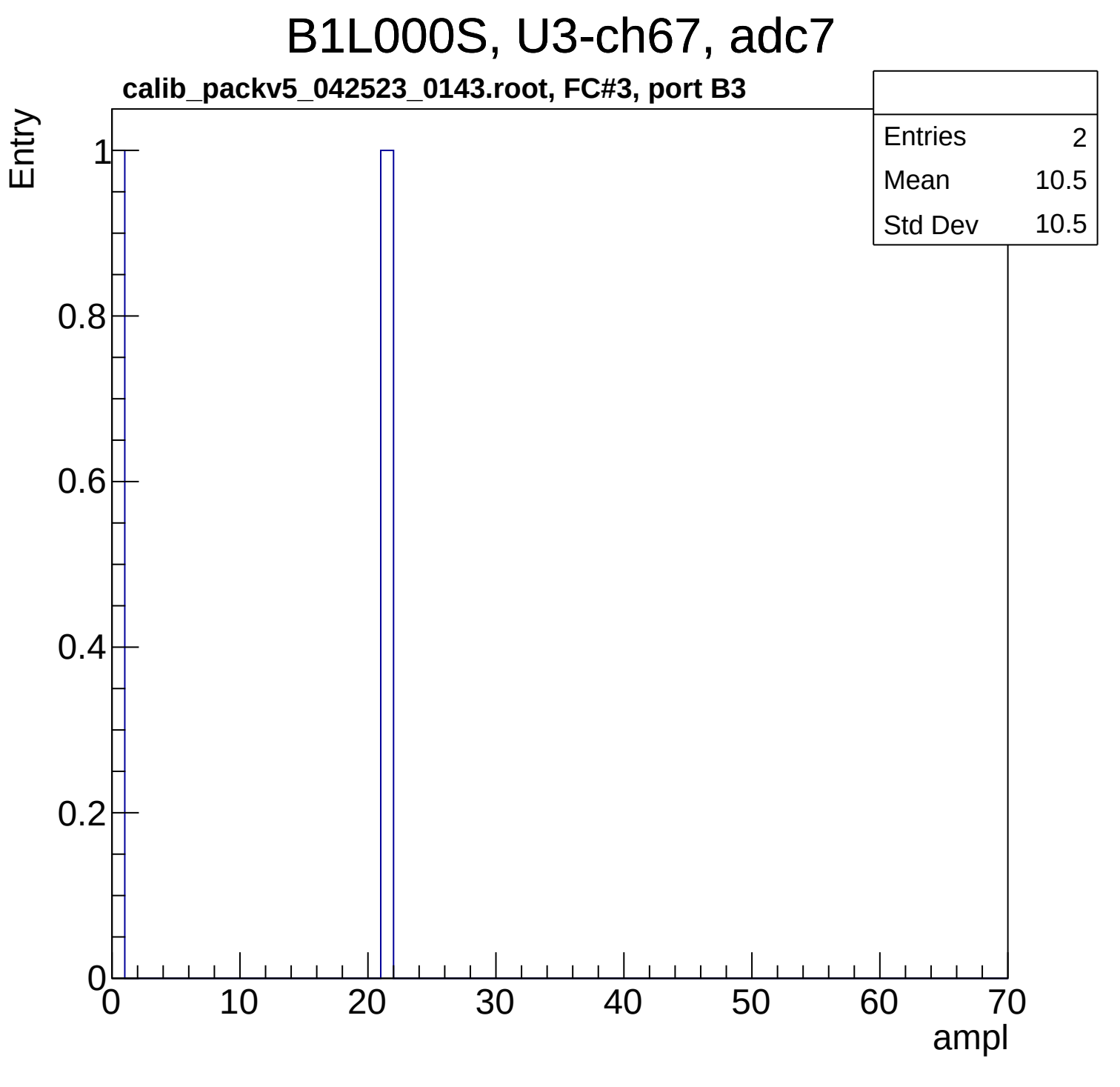
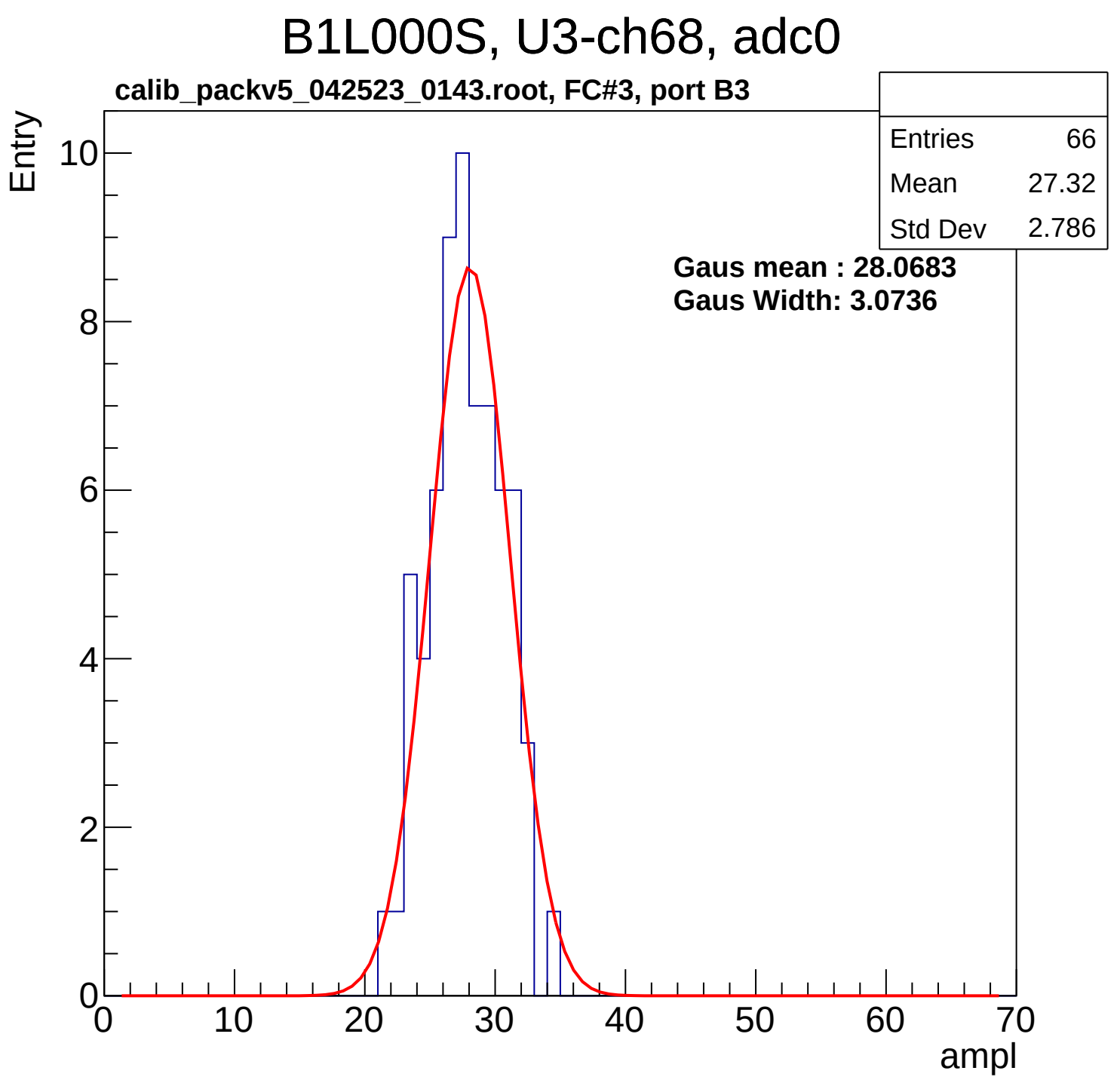
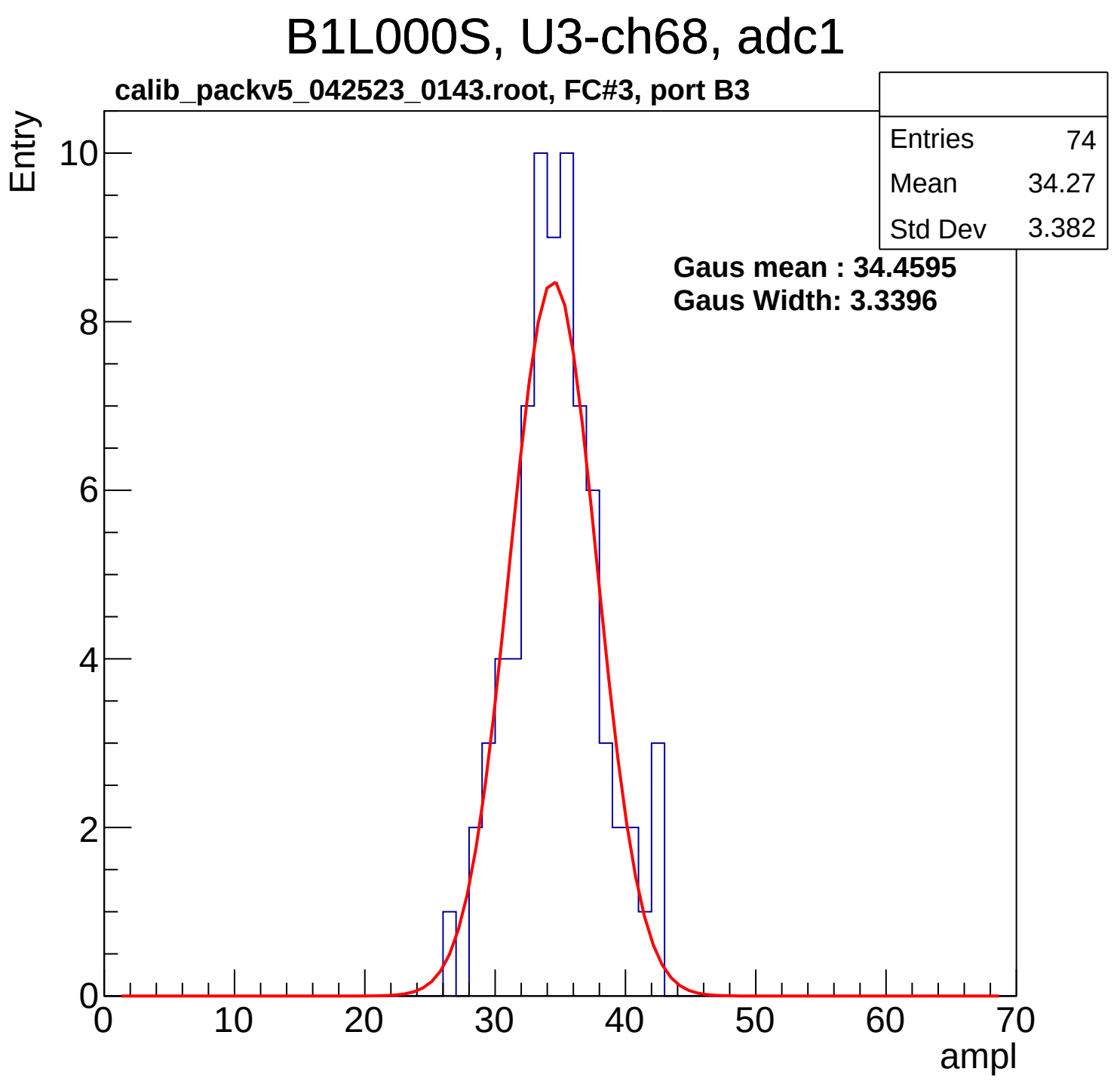

B1L000S, U3-ch67, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 54.53
Std Dev 3.302
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

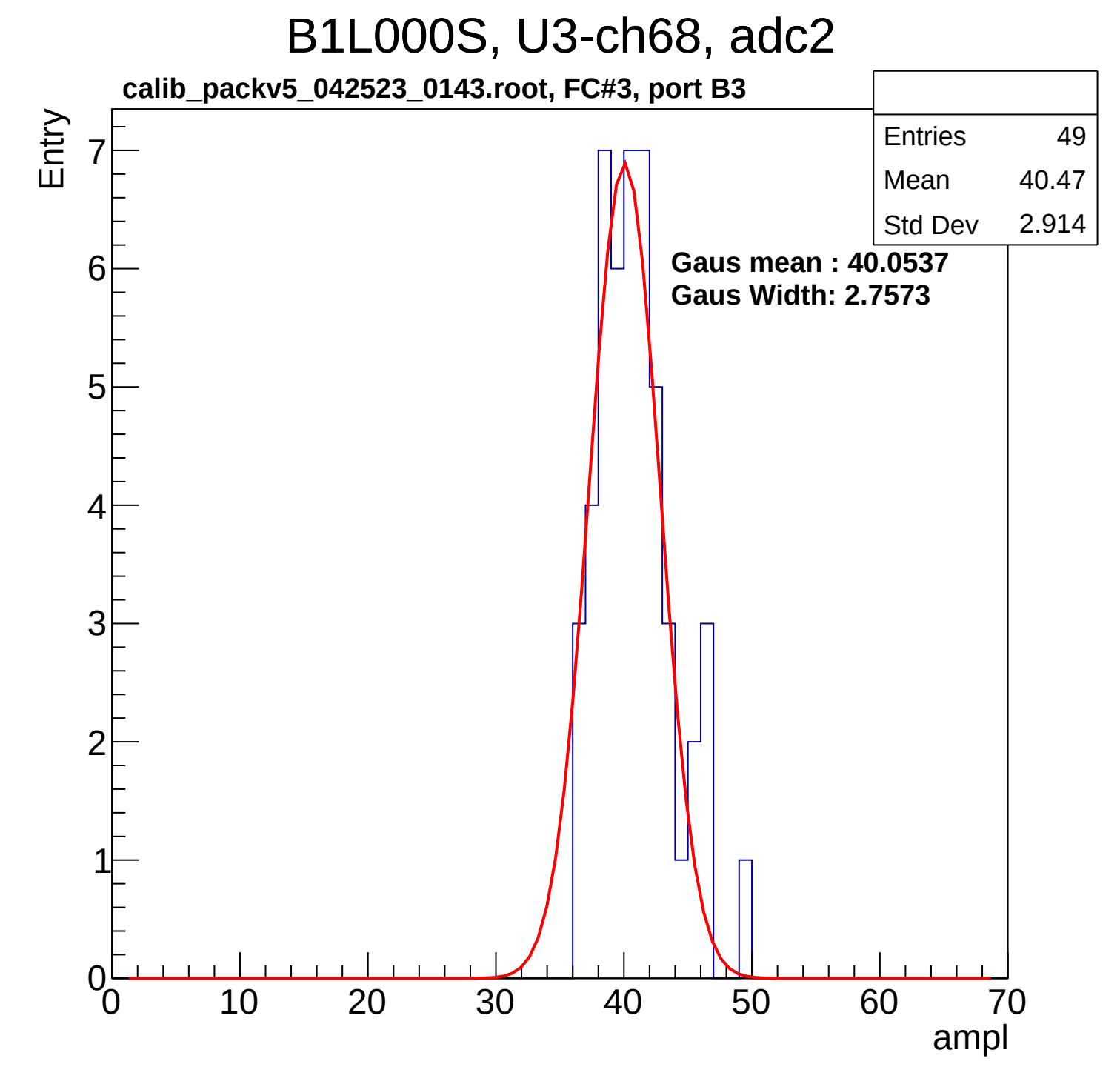

B1L000S, U3-ch67, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 57.73
Std Dev 9.536
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U3-ch67, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 61.11
Std Dev 1.696
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch67, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch68, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 27.32
Std Dev 2.786
Gaus mean : 28.0683
Gaus Width: 3.0736
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch68, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 34.27
Std Dev 3.382
Gaus mean : 34.4595
Gaus Width: 3.3396
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch68, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 40.47
Std Dev 2.914
Gaus mean : 40.0537
Gaus Width: 2.7573
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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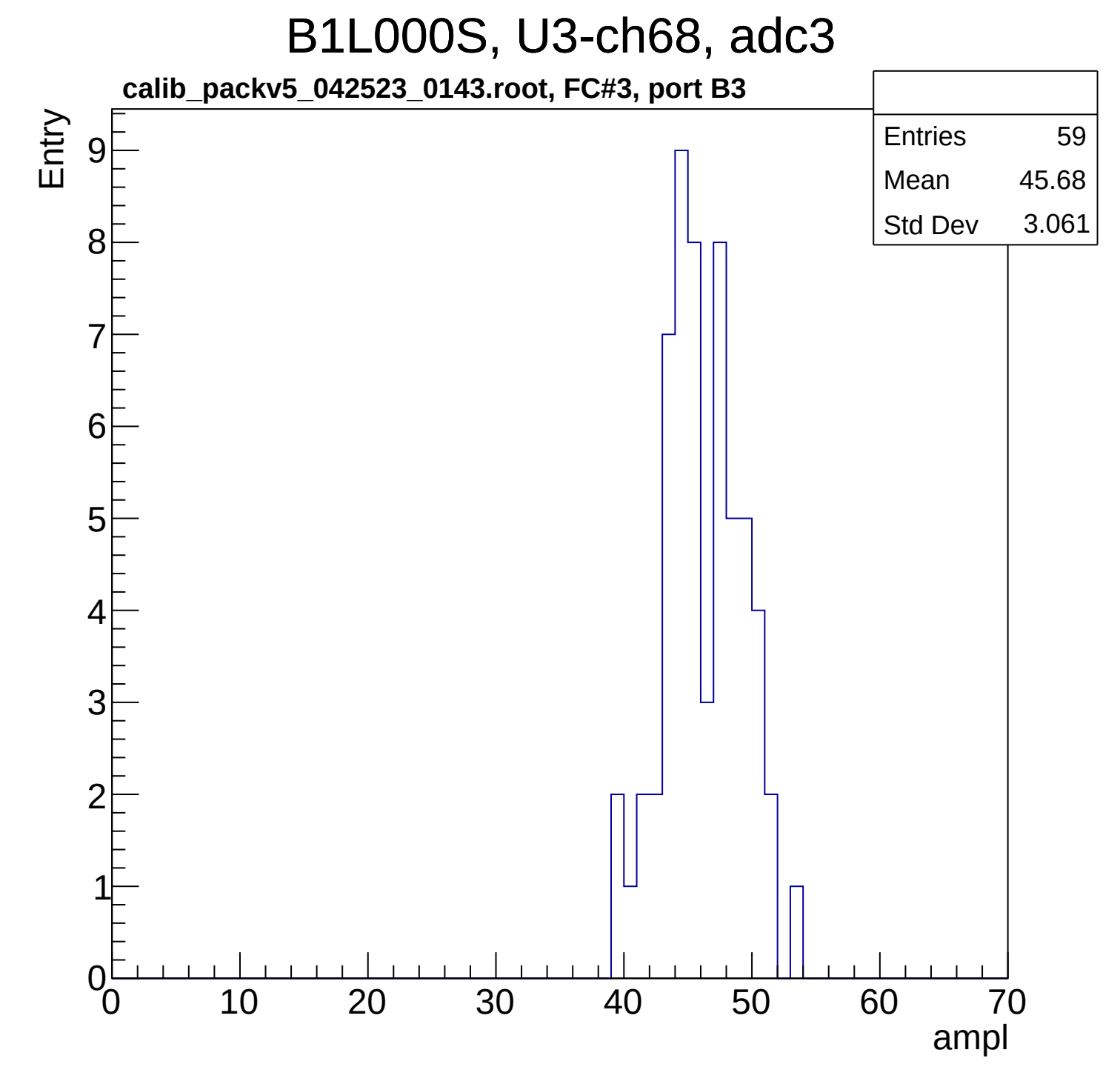
B1L000S, U3-ch68, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 45.68
Std Dev 3.061
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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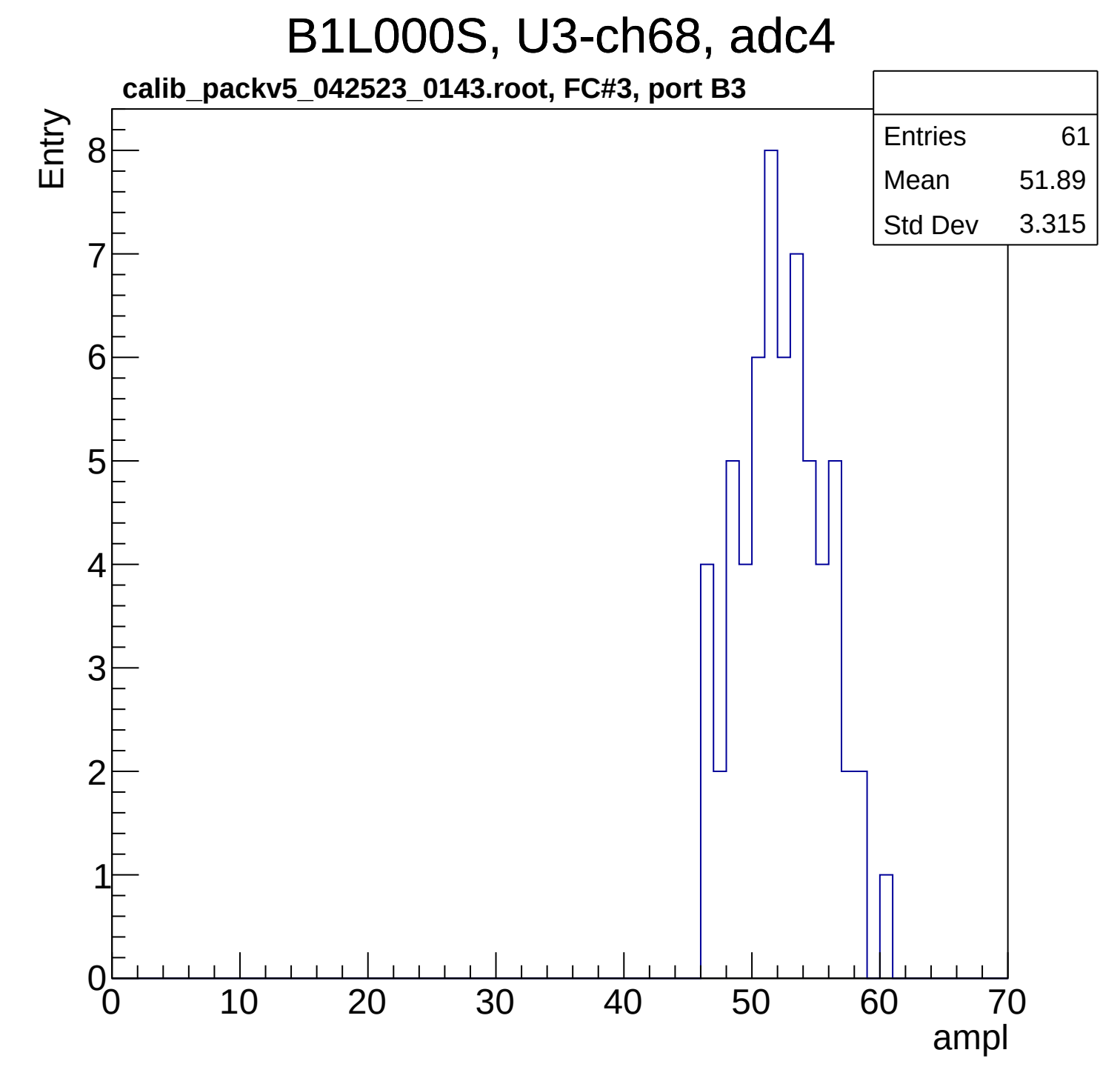
B1L000S, U3-ch68, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 51.89
Std Dev 3.315
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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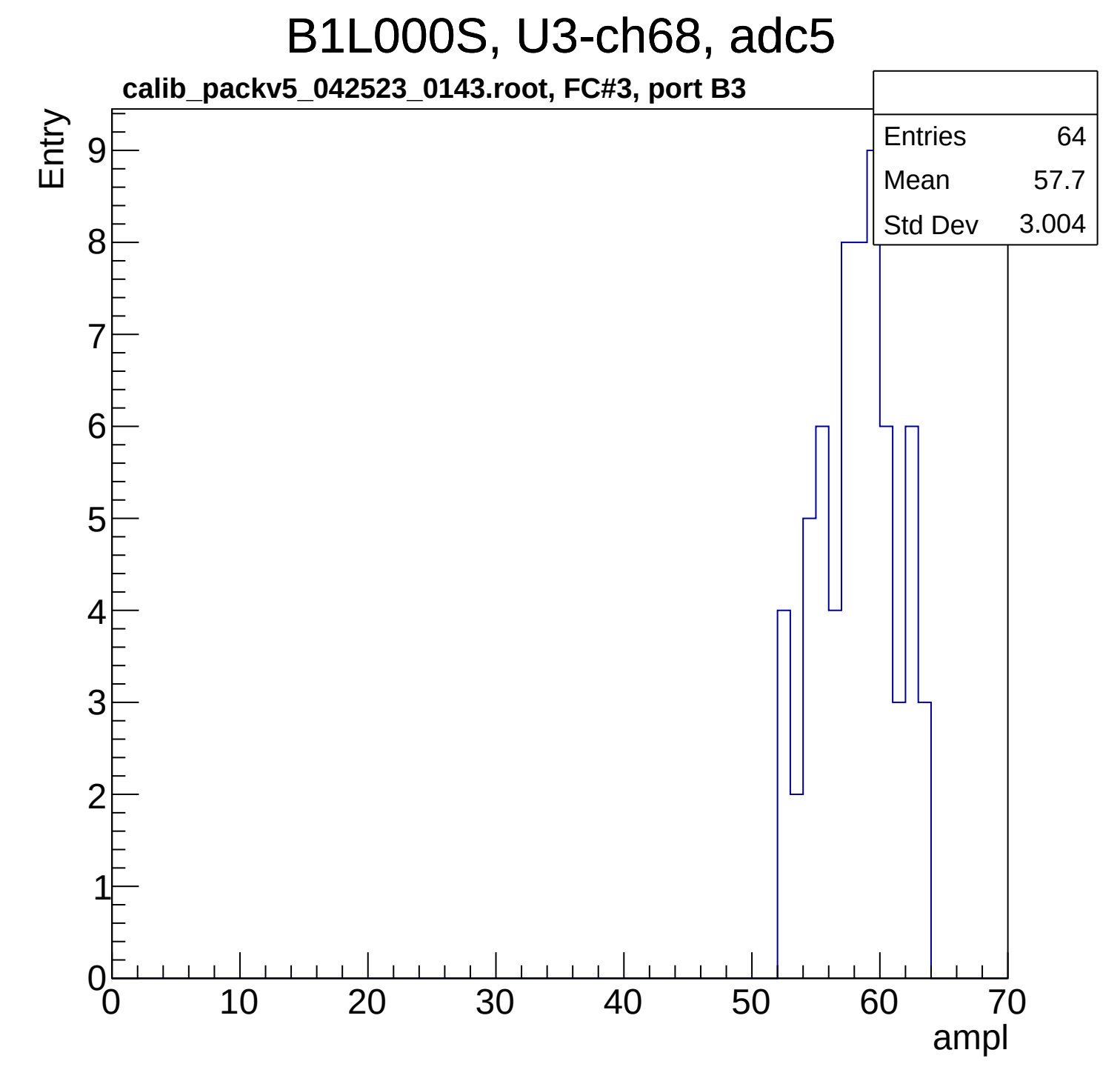
B1L000S, U3-ch68, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 57.7
Std Dev 3.004
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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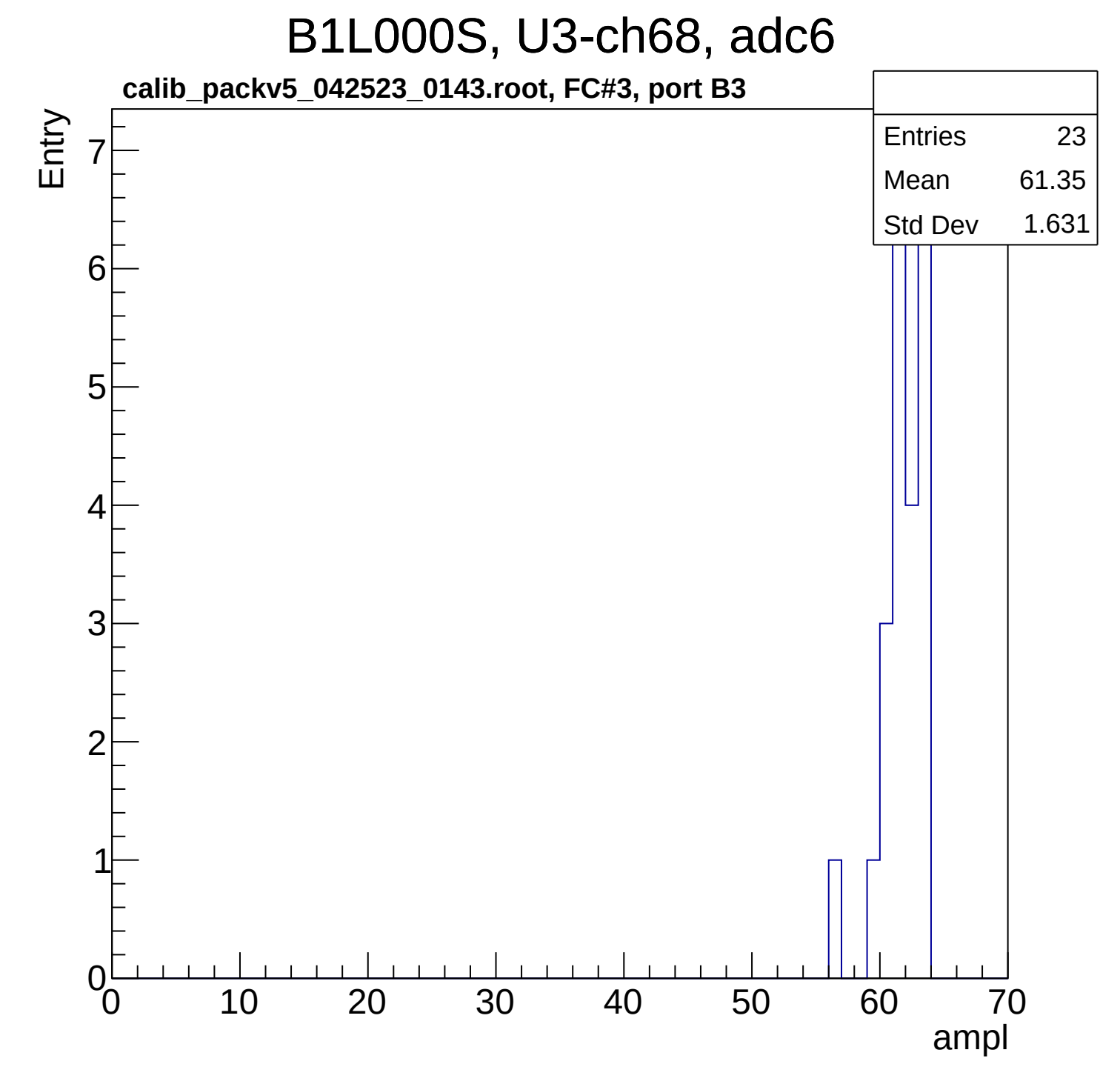
B1L000S, U3-ch68, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 61.35
Std Dev 1.631
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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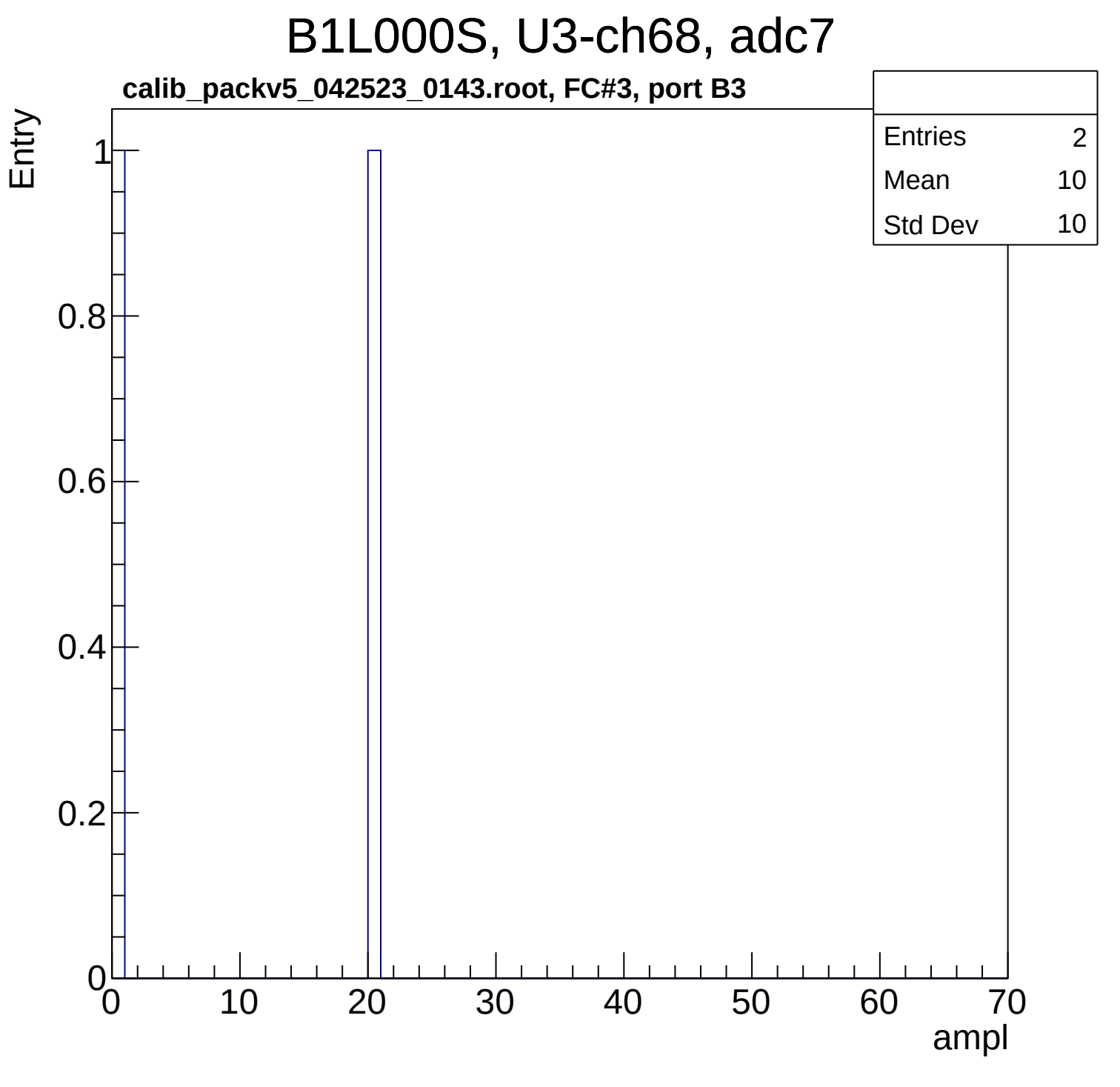
B1L000S, U3-ch68, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10
Std Dev 10
Entry
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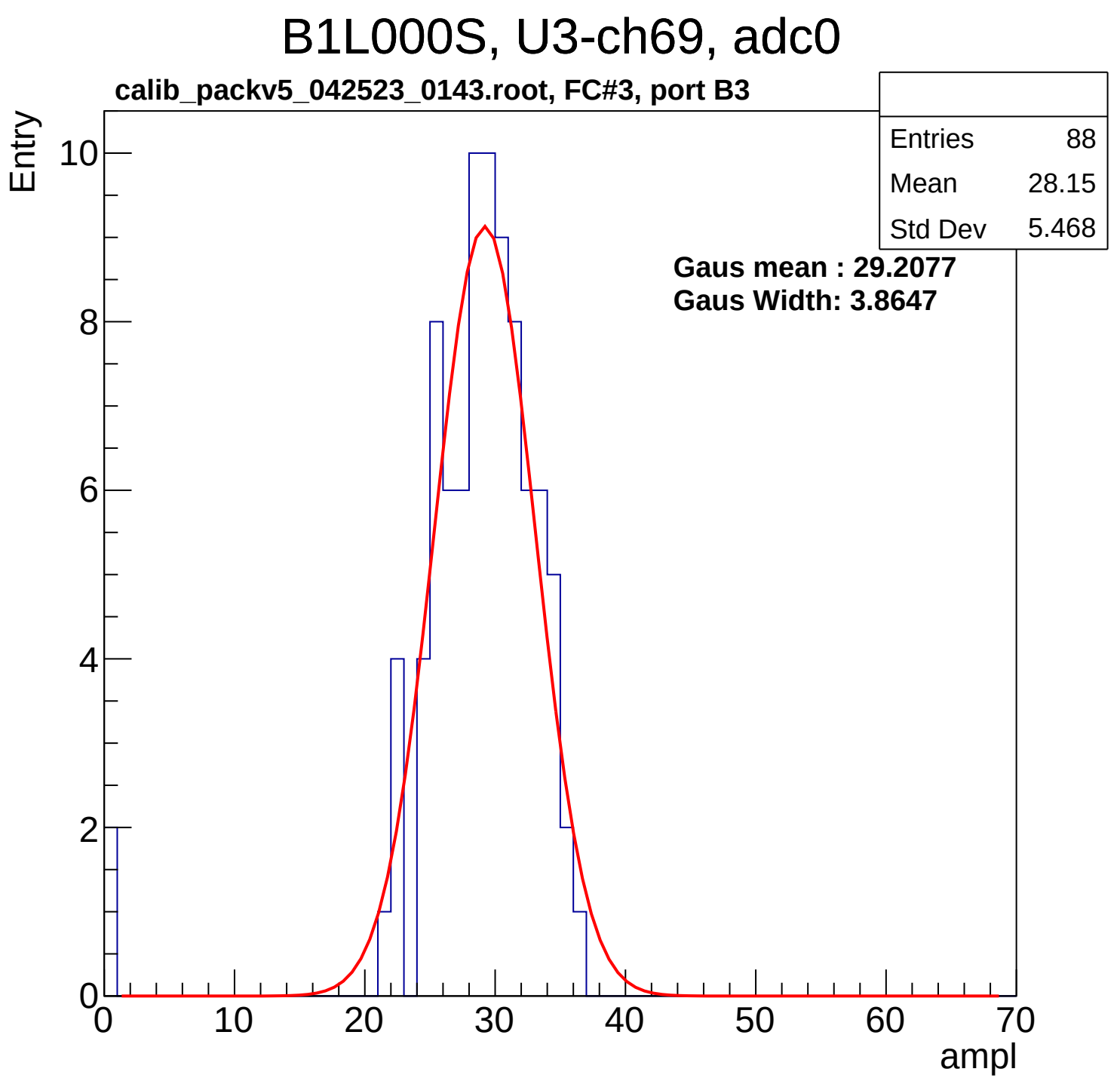
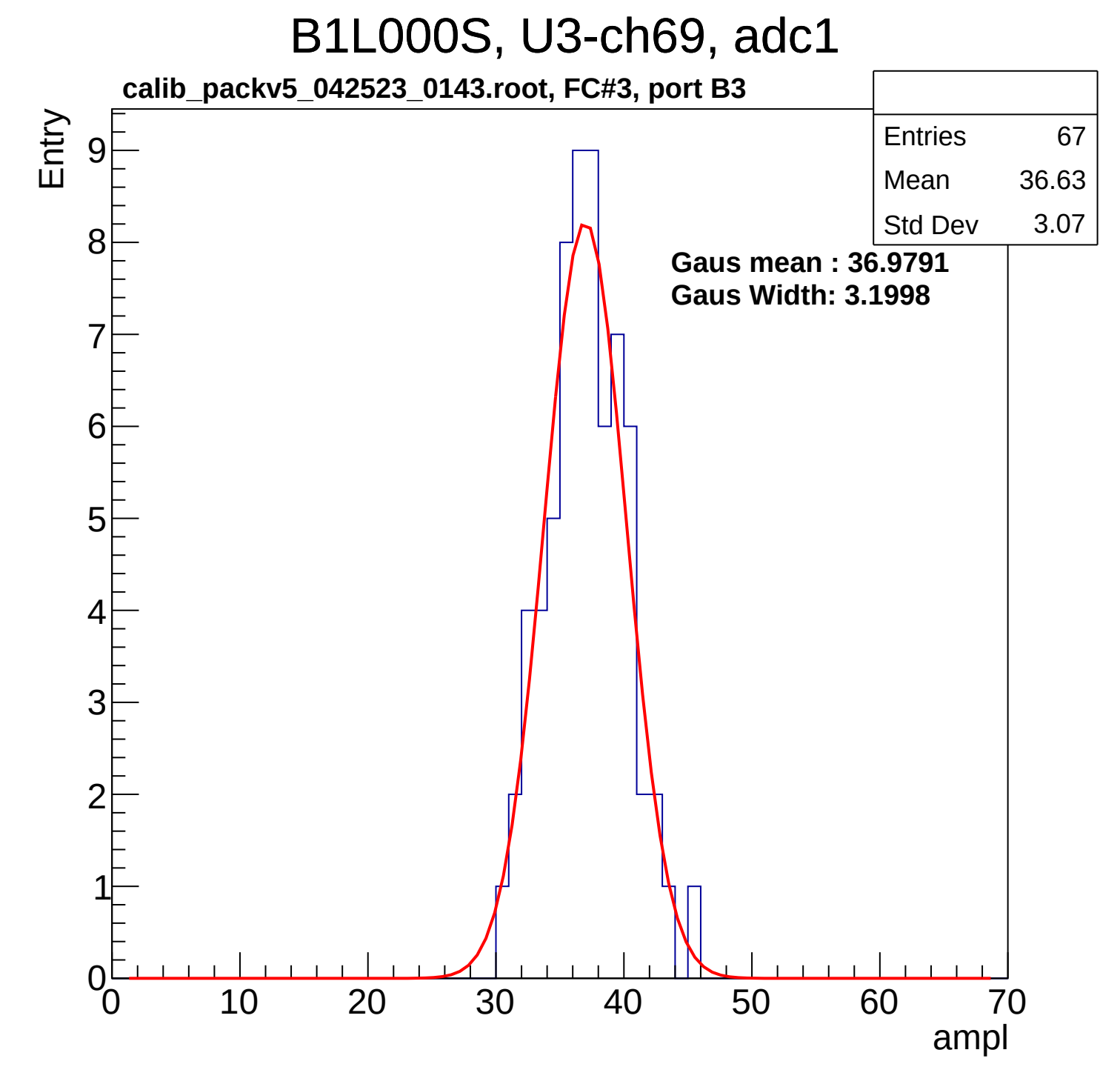
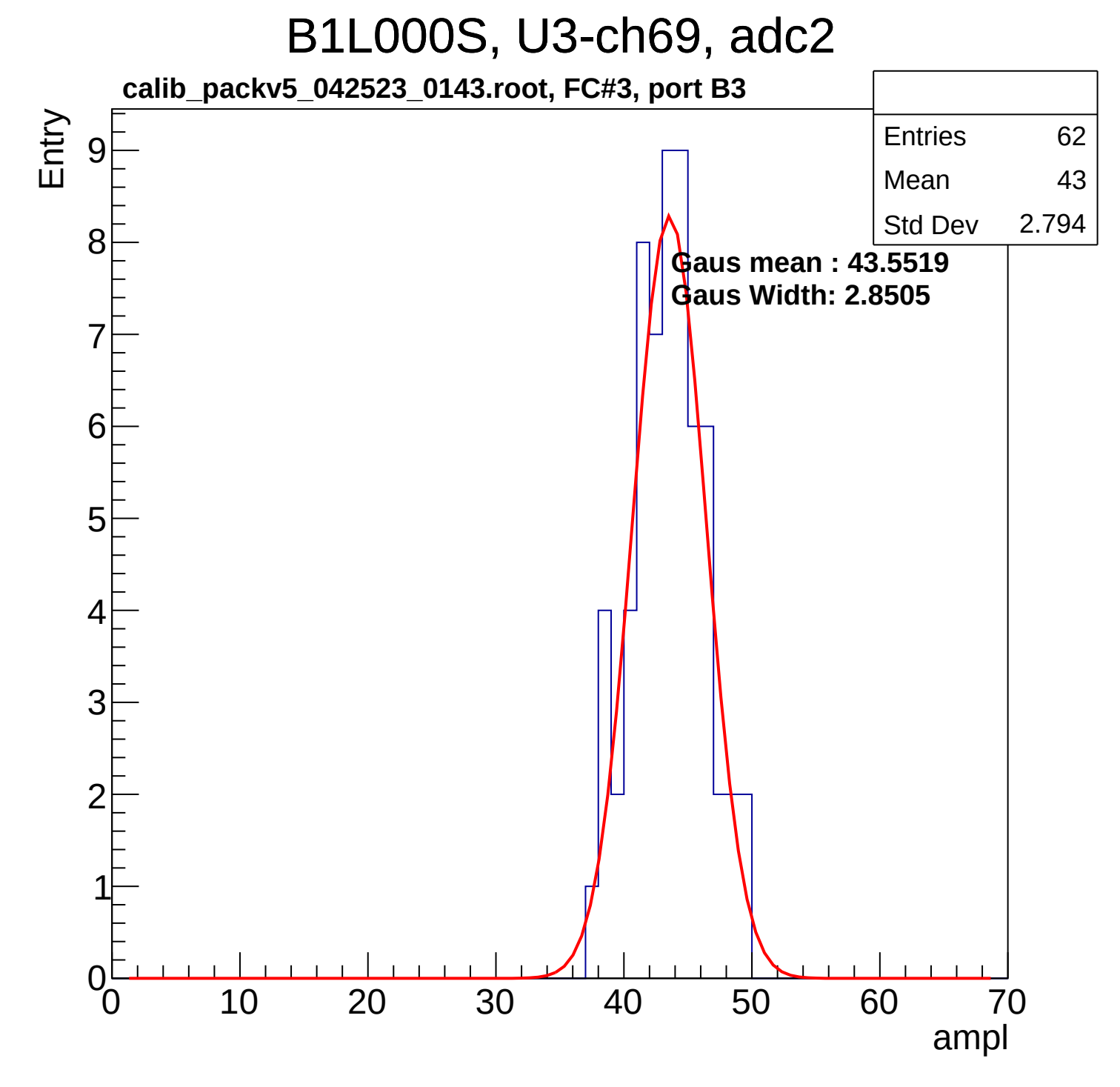
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch69, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 88
Mean 28.15
Std Dev 5.468
Gaus mean : 29.2077
Gaus Width: 3.8647
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch69, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 36.63
Std Dev 3.07
Gaus mean : 36.9791
Gaus Width: 3.1998
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch69, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 43
Std Dev 2.794
Gaus mean : 43.5519
Gaus Width: 2.8505
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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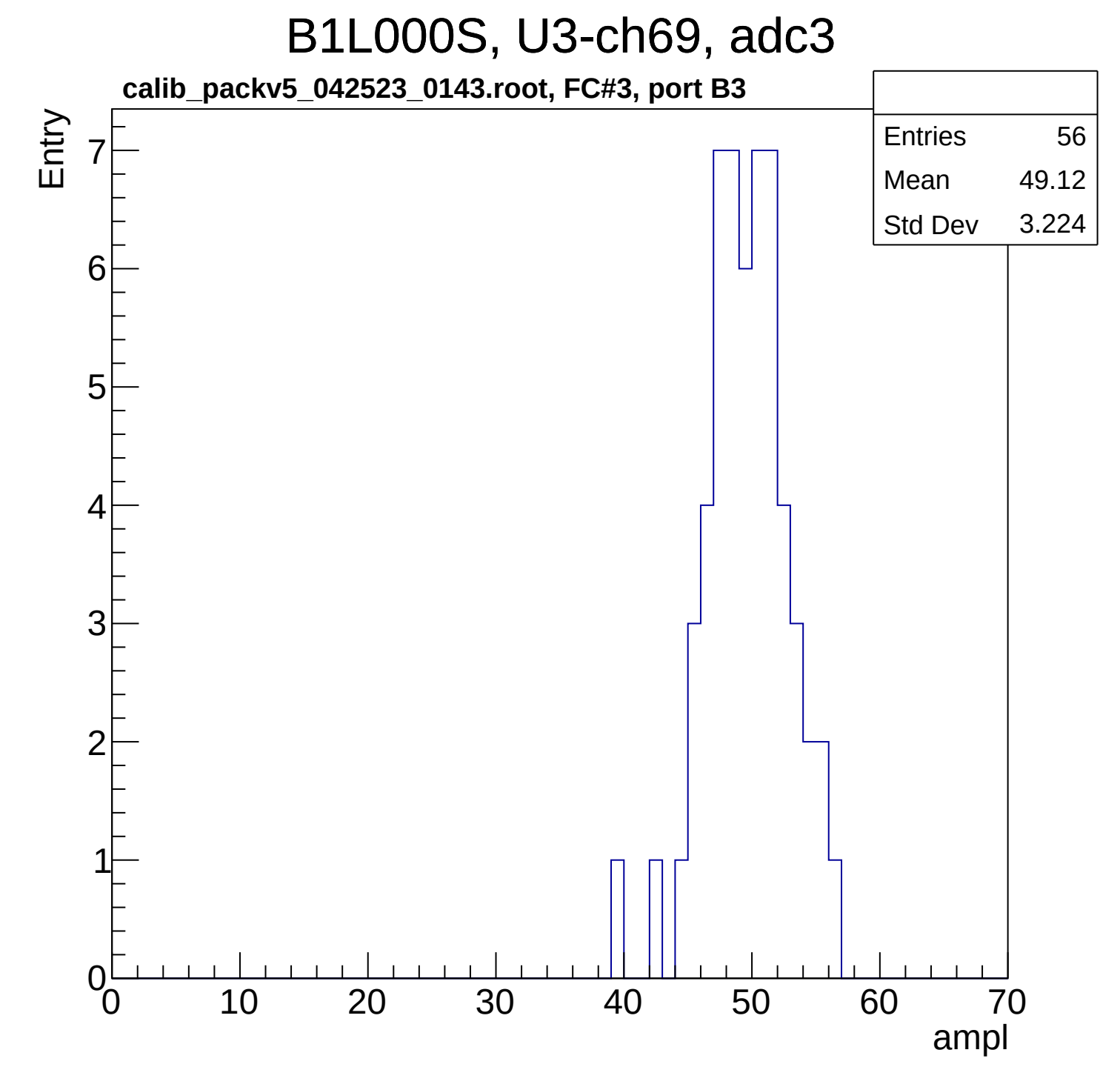
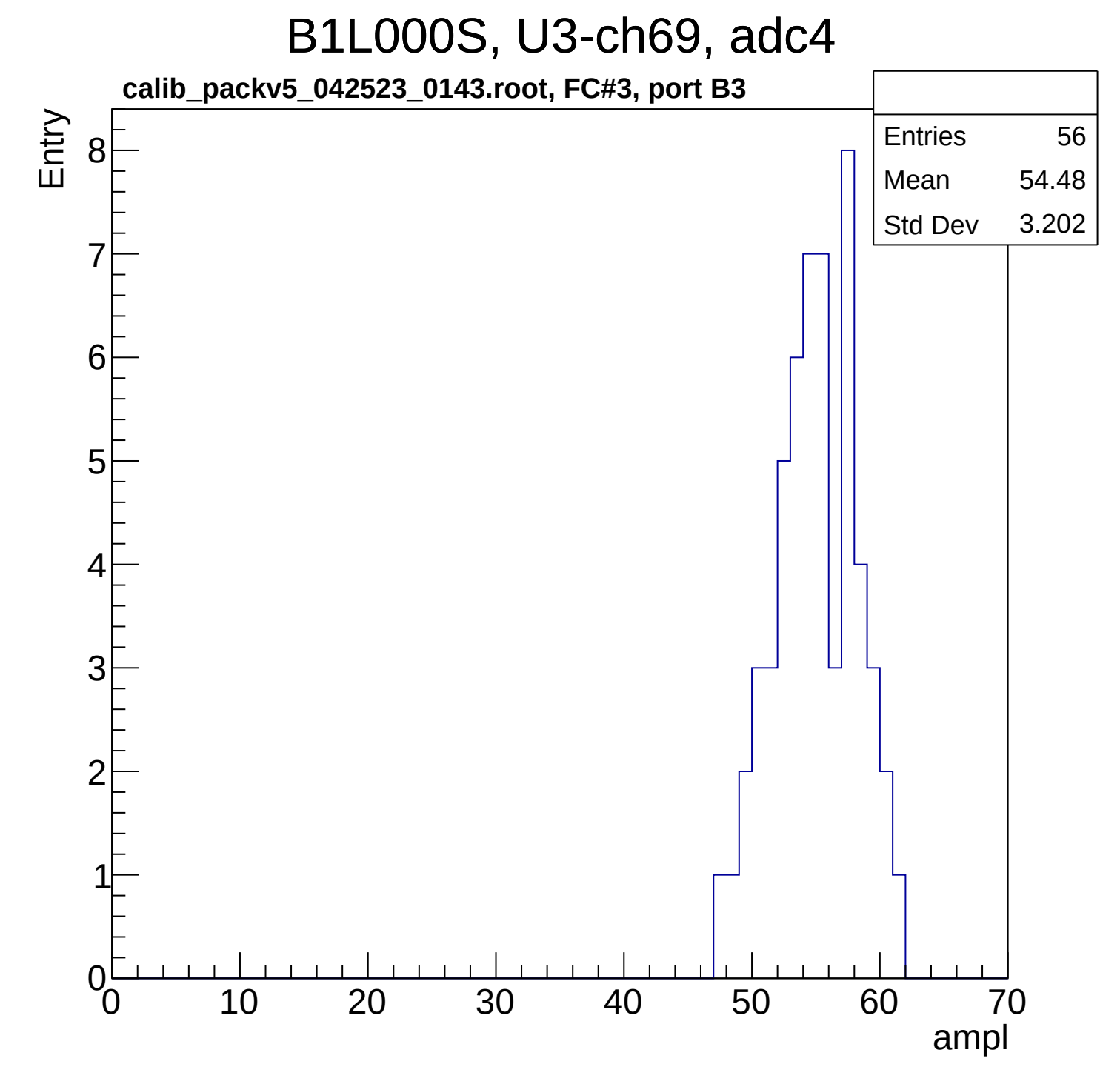

B1L000S, U3-ch69, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 49.12
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch69, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 54.48
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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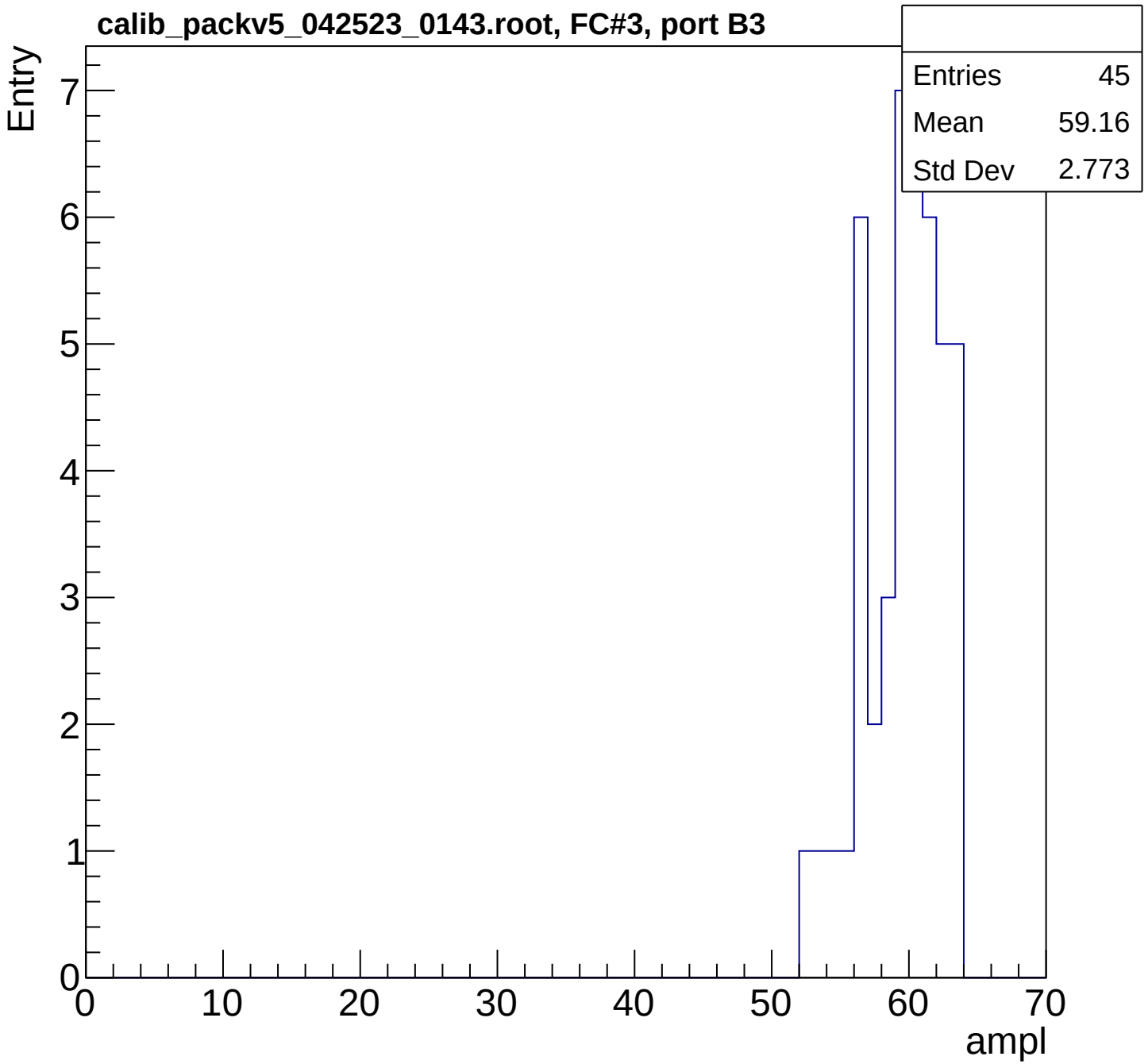
B1L000S, U3-ch69, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 59.16
Std Dev 2.773
Entry
ampl
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1
2
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4
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6
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0
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60
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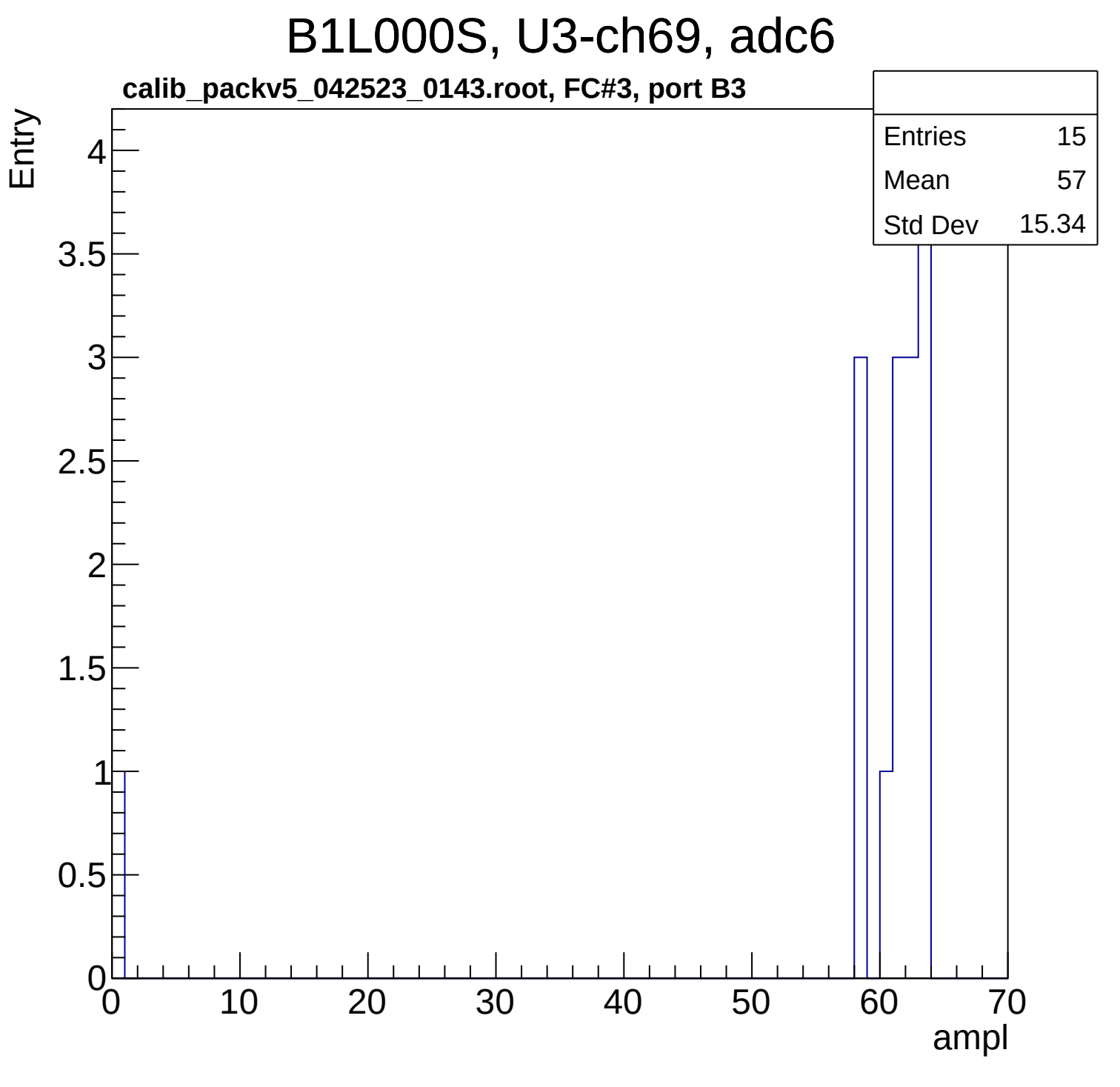
B1L000S, U3-ch69, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57
Std Dev 15.34
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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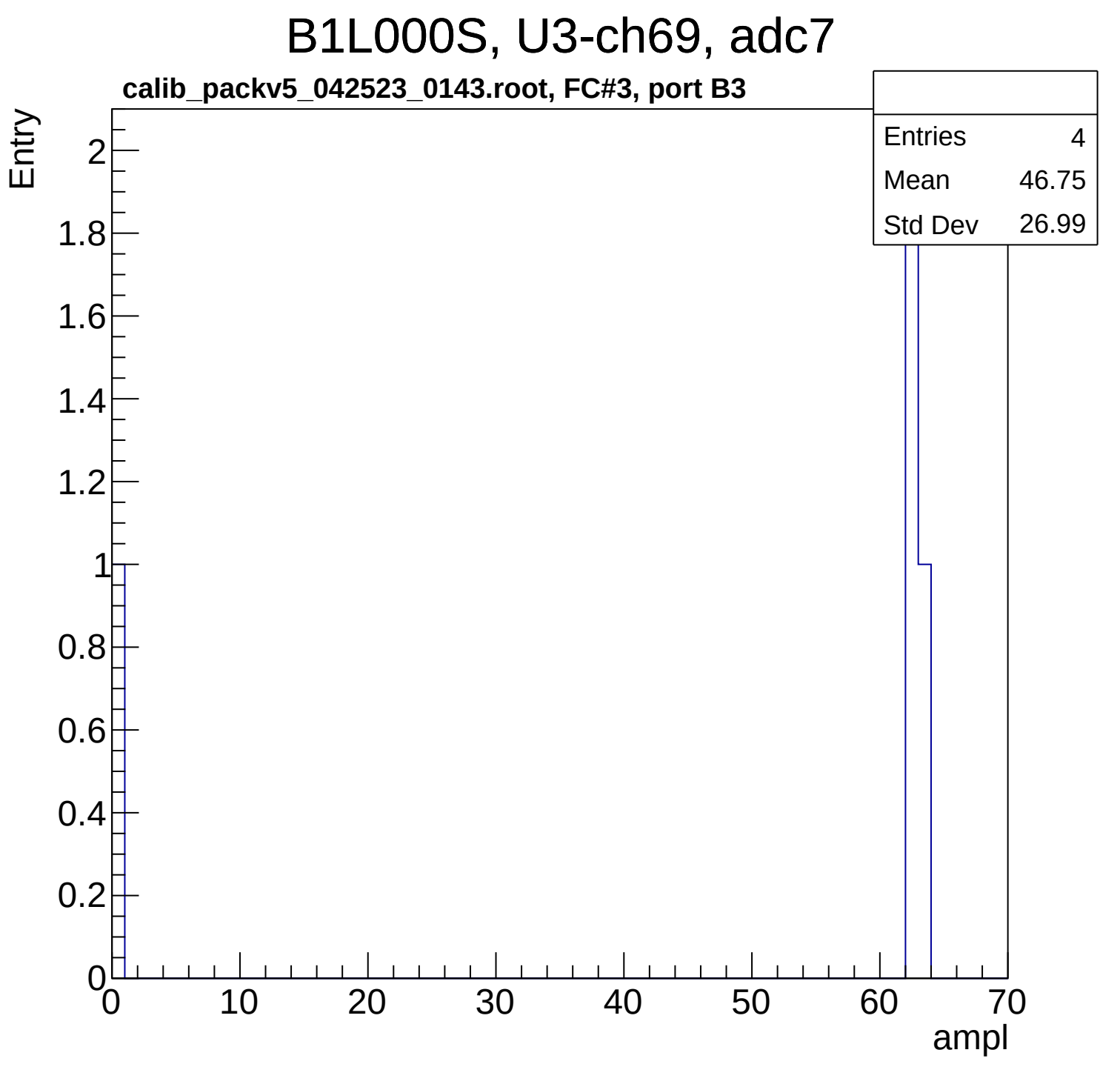
B1L000S, U3-ch69, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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60
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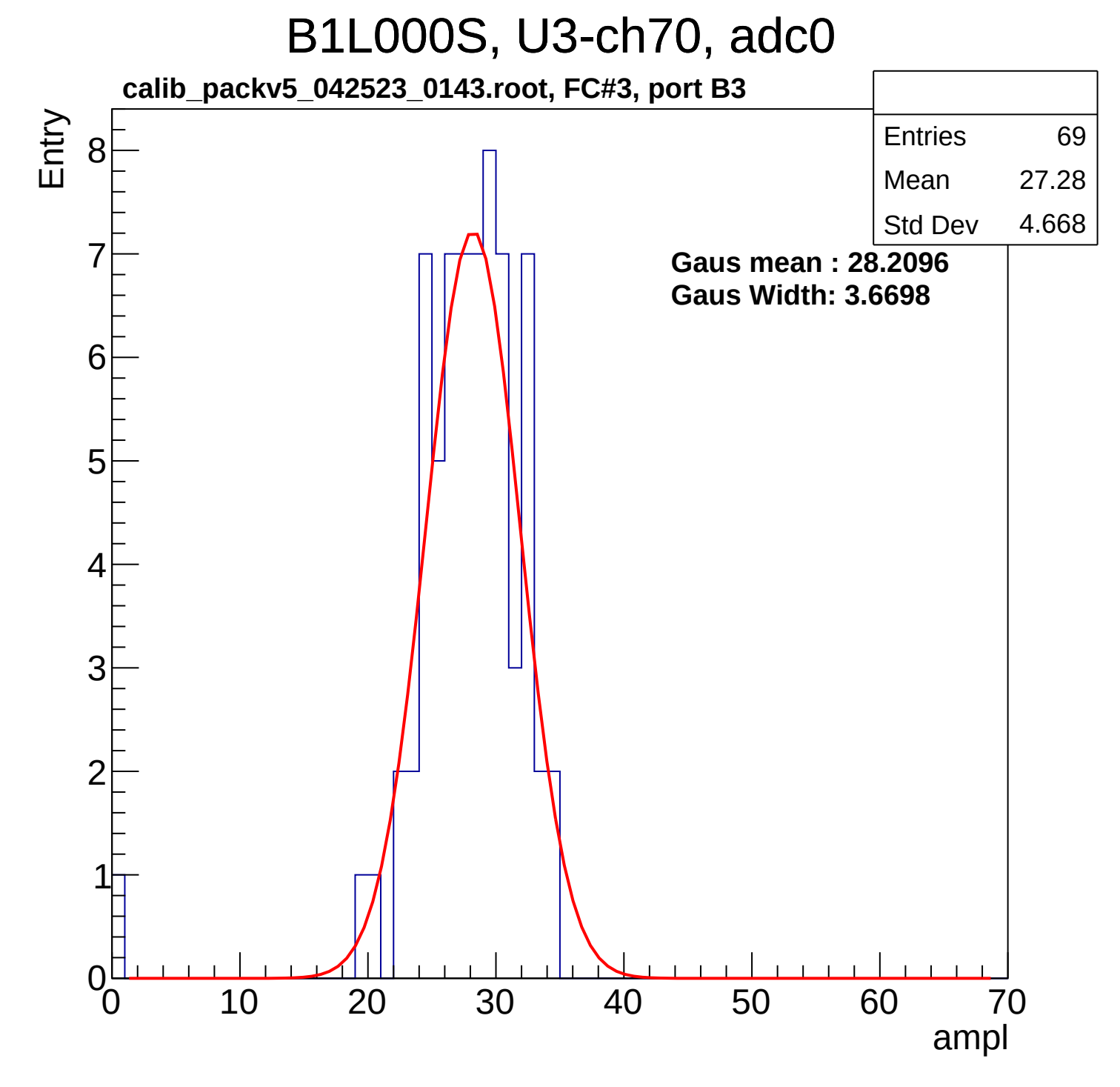
B1L000S, U3-ch70, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 27.28
Std Dev 4.668
Gaus mean : 28.2096
Gaus Width: 3.6698
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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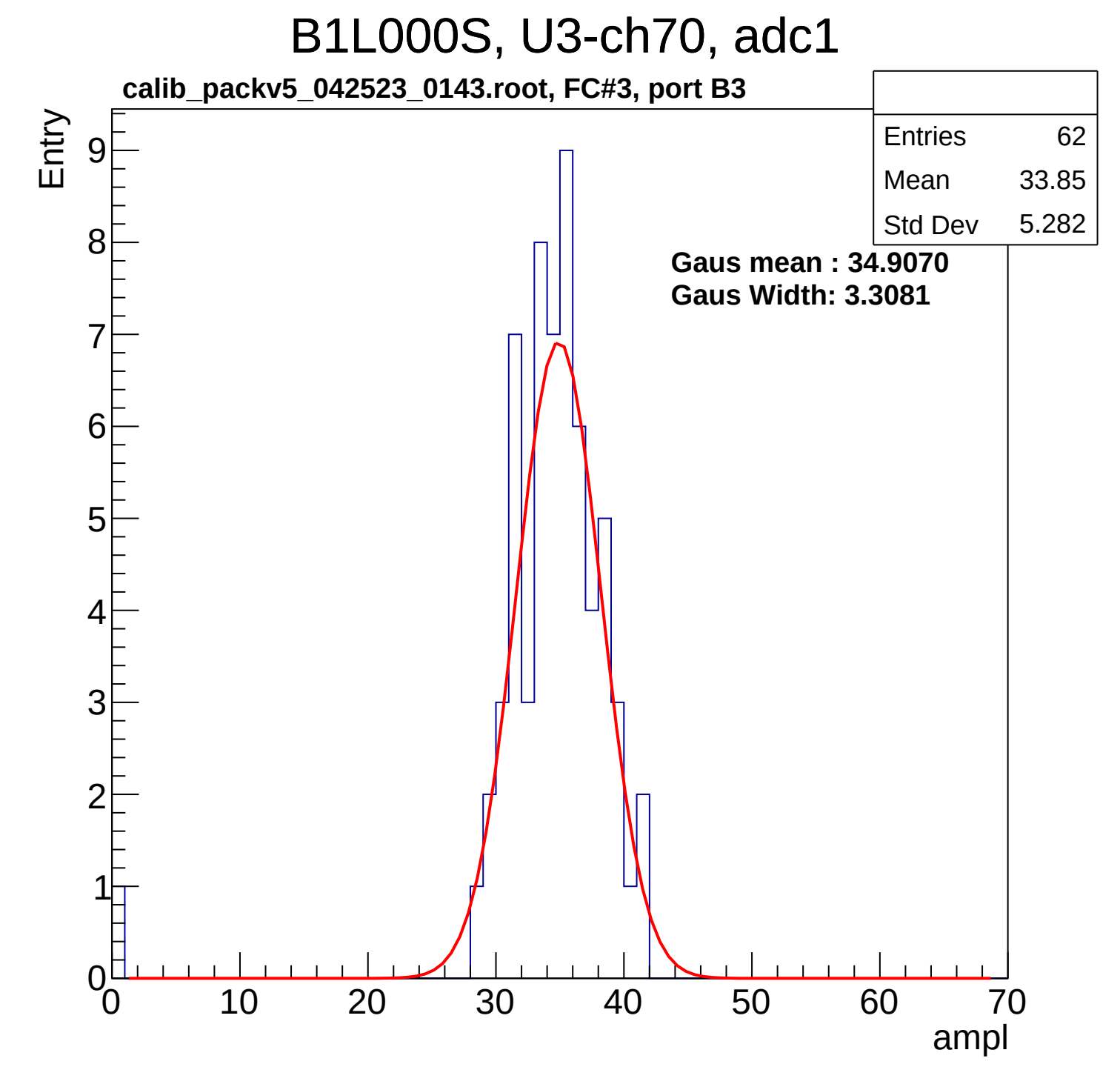
B1L000S, U3-ch70, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 33.85
Std Dev 5.282
Gaus mean : 34.9070
Gaus Width: 3.3081
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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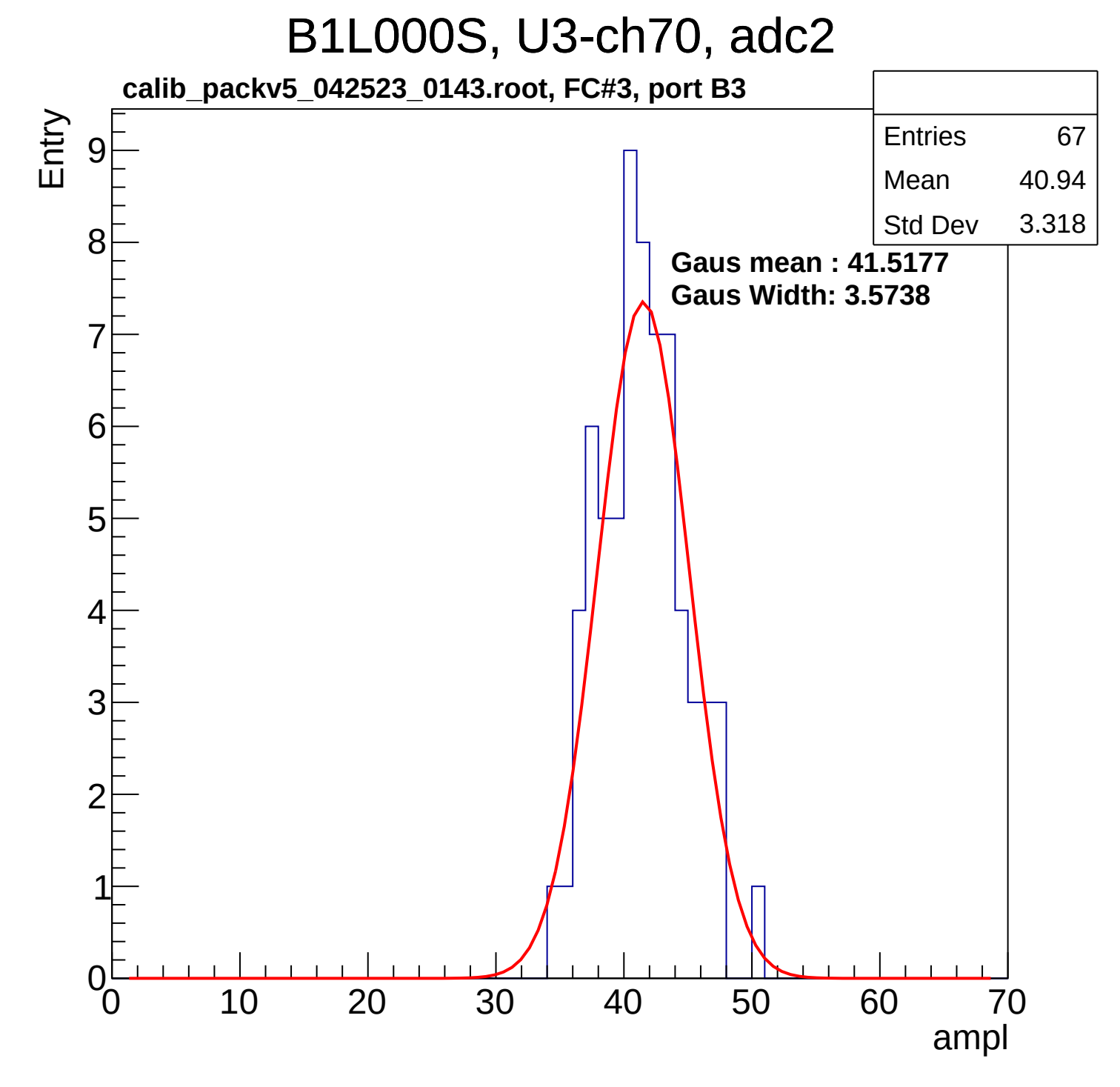
B1L000S, U3-ch70, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 40.94
Std Dev 3.318
Gaus mean : 41.5177
Gaus Width: 3.5738
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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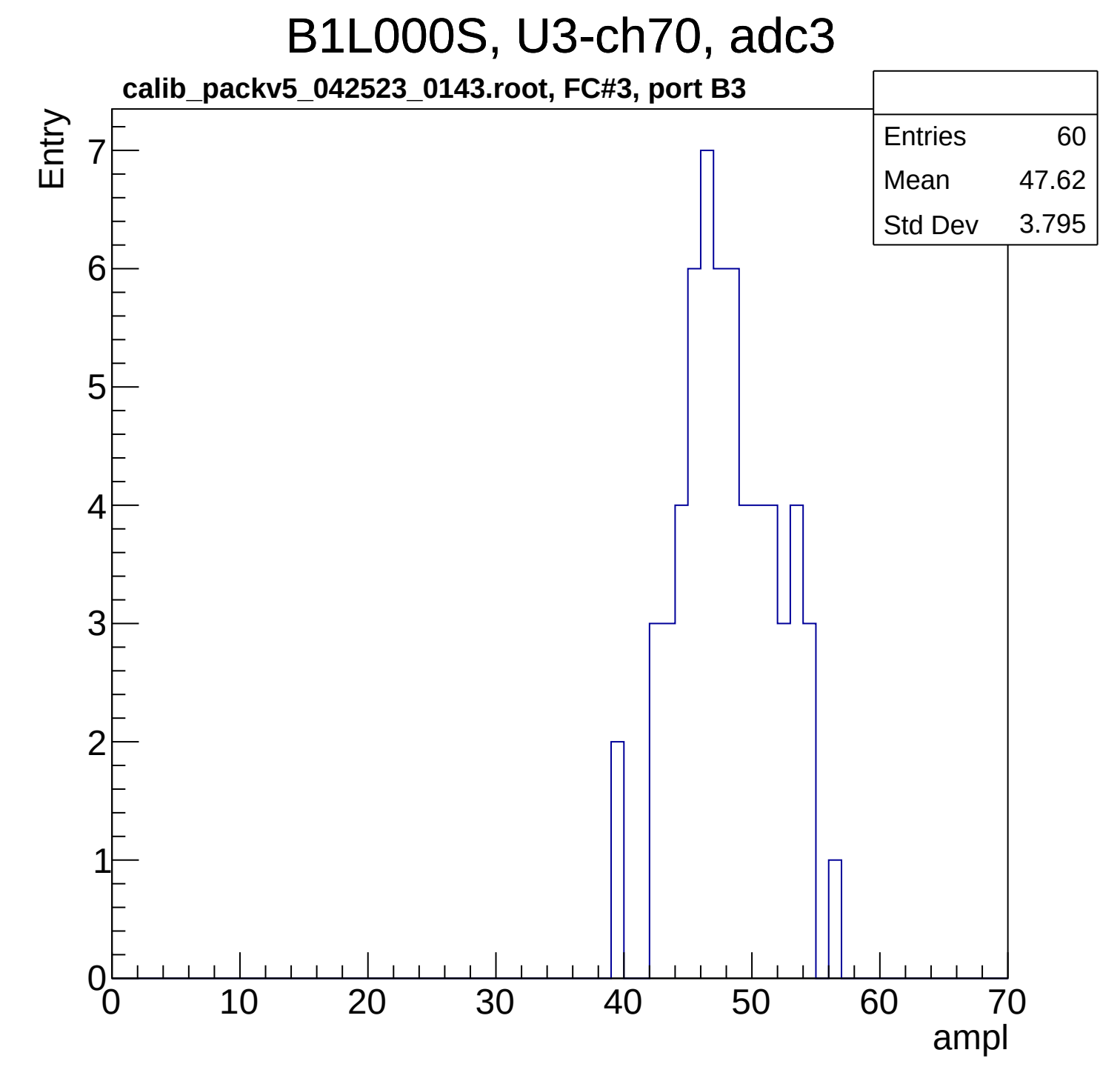
B1L000S, U3-ch70, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 47.62
Std Dev 3.795
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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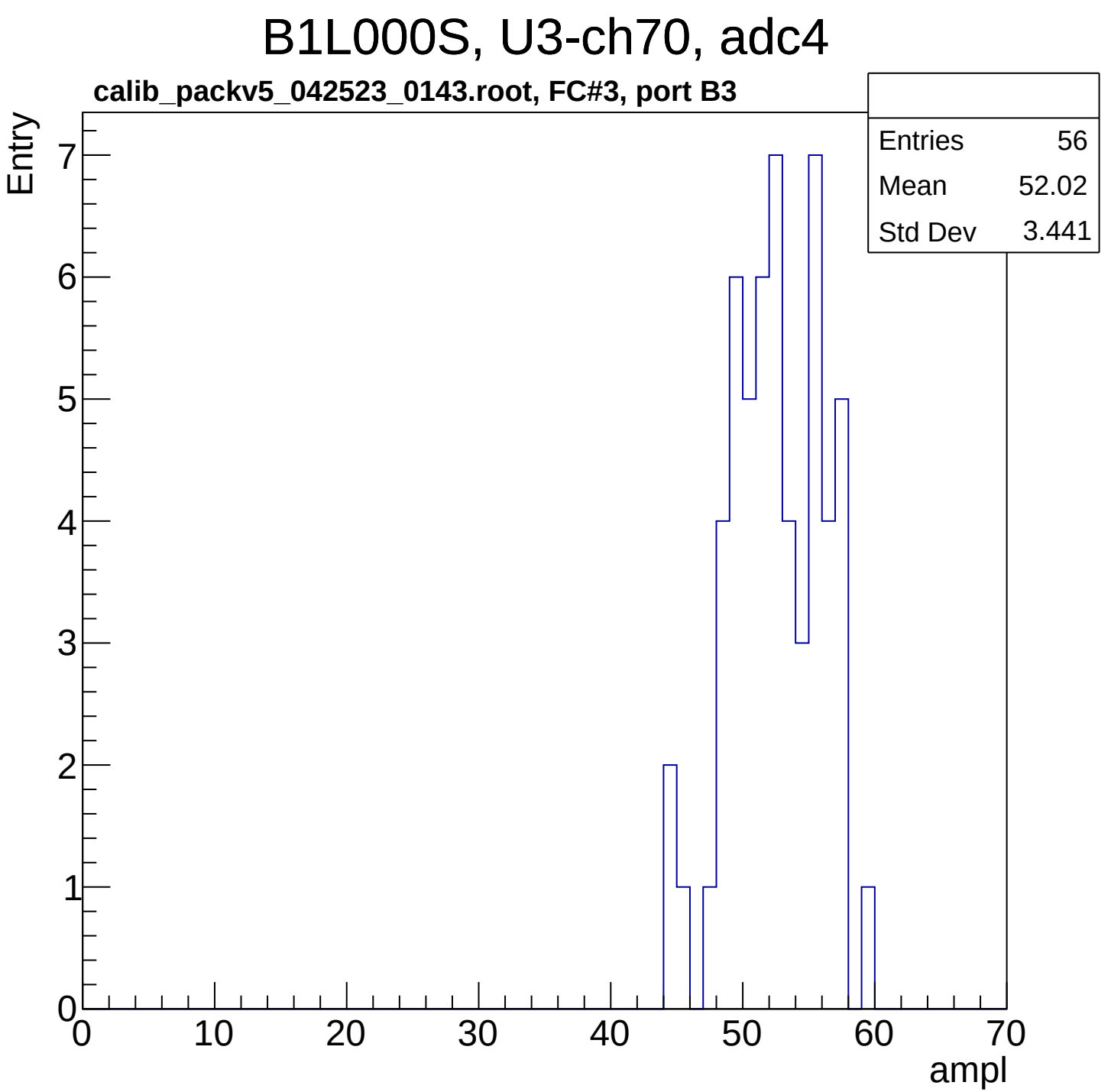
B1L000S, U3-ch70, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 52.02
Std Dev 3.441
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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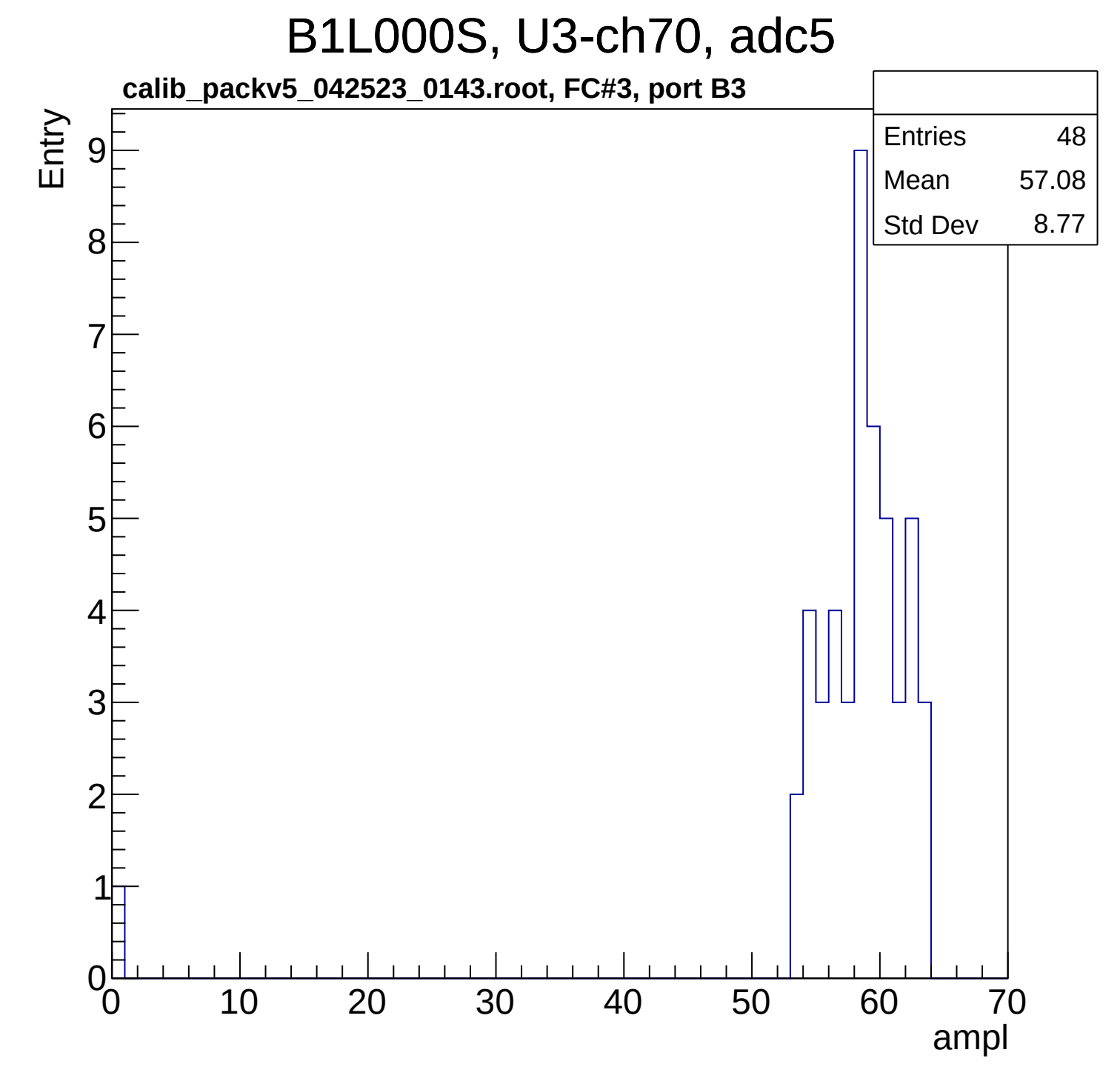
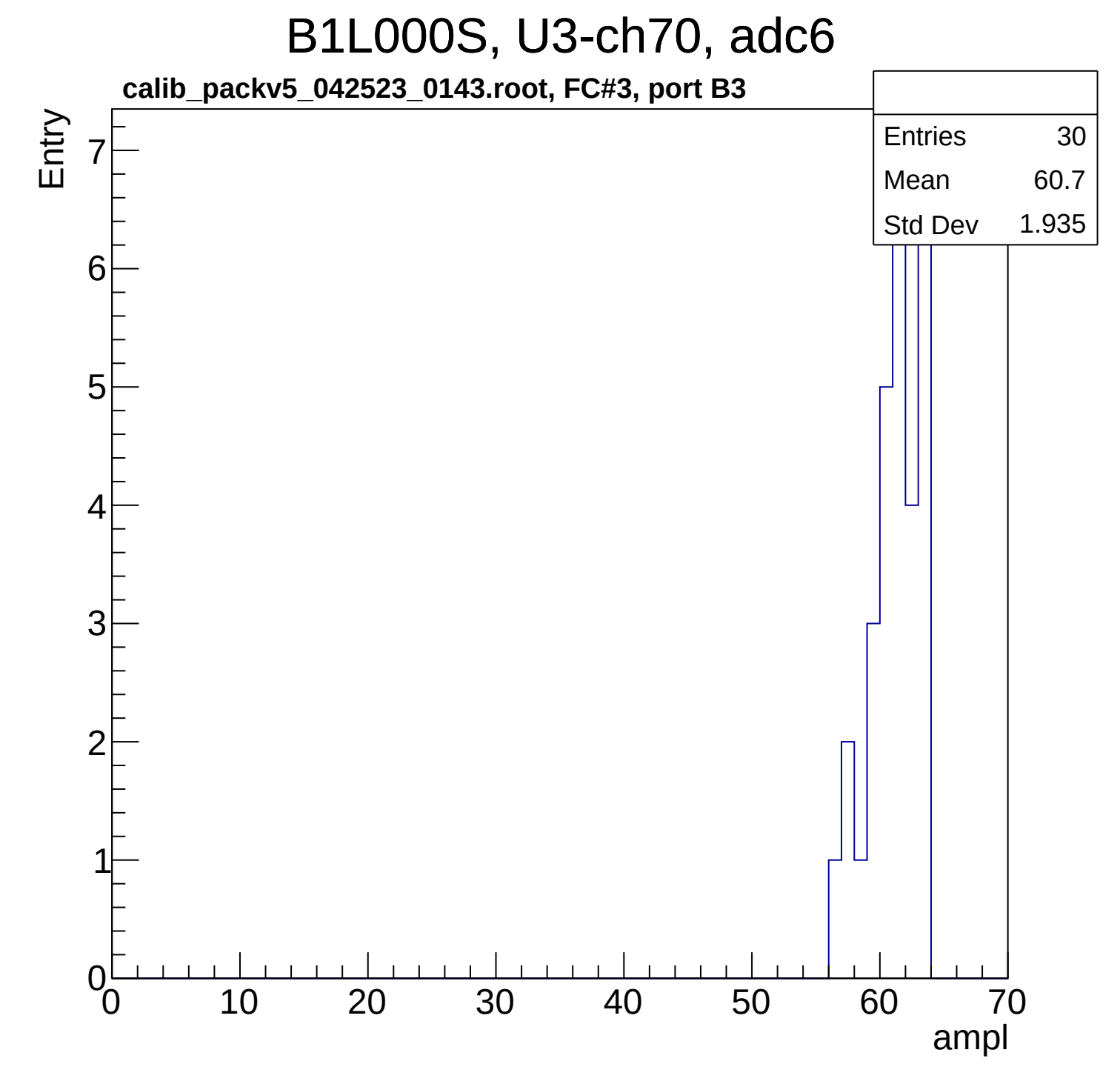
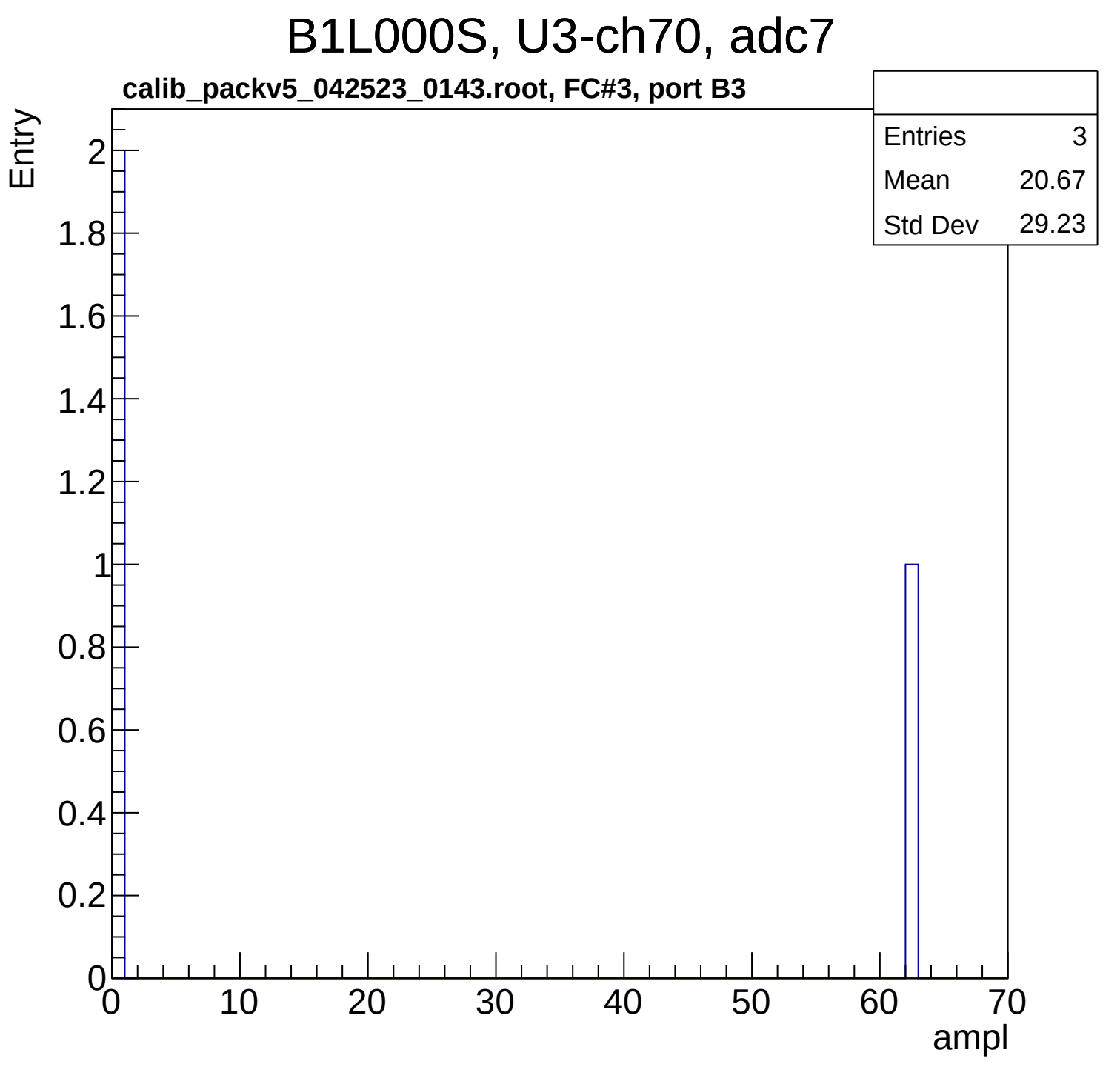
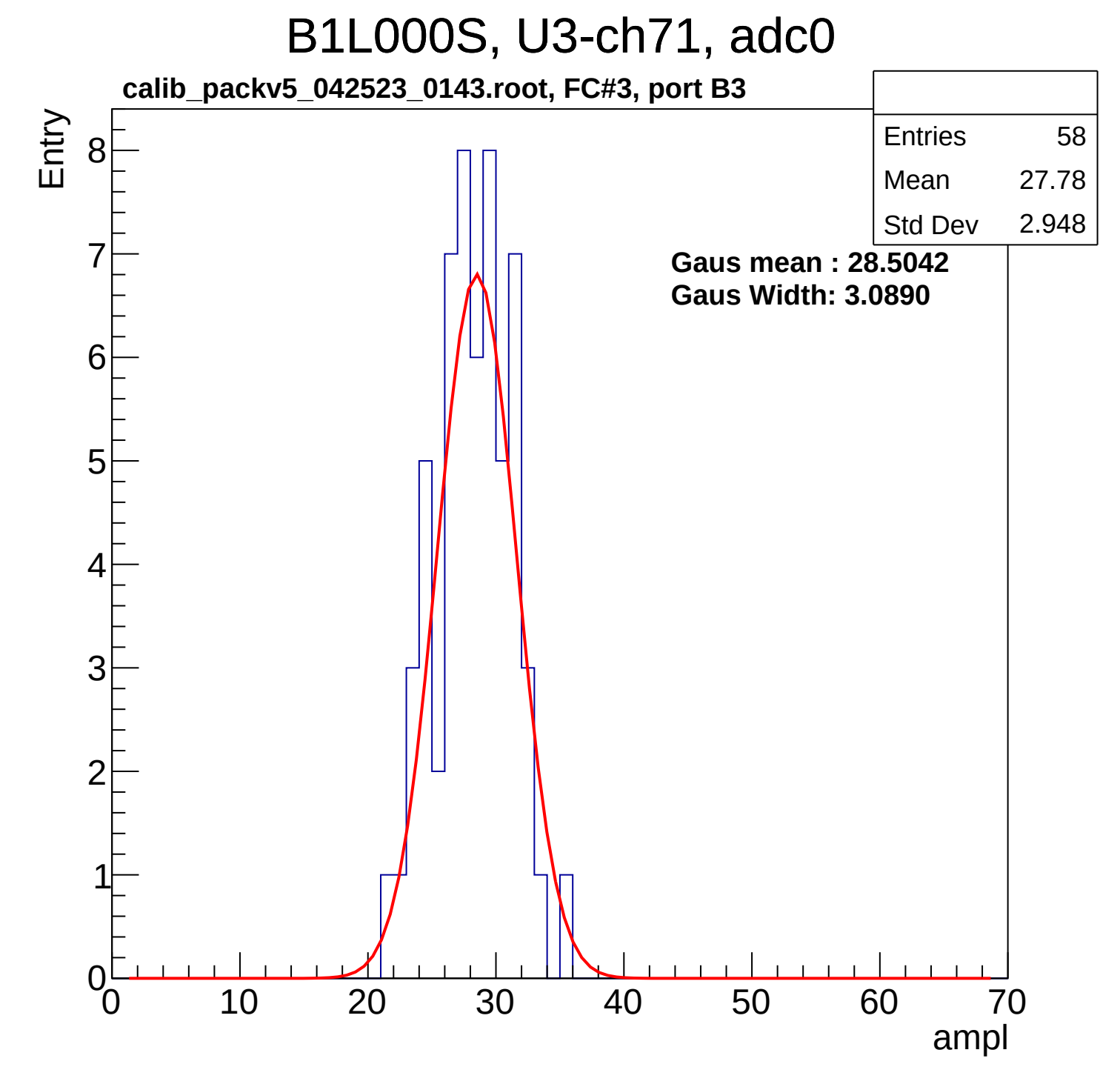

B1L000S, U3-ch70, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 57.08
Std Dev 8.77
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch70, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 30
Mean 60.7
Std Dev 1.935
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch70, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch71, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 27.78
Std Dev 2.948
Gaus mean : 28.5042
Gaus Width: 3.0890
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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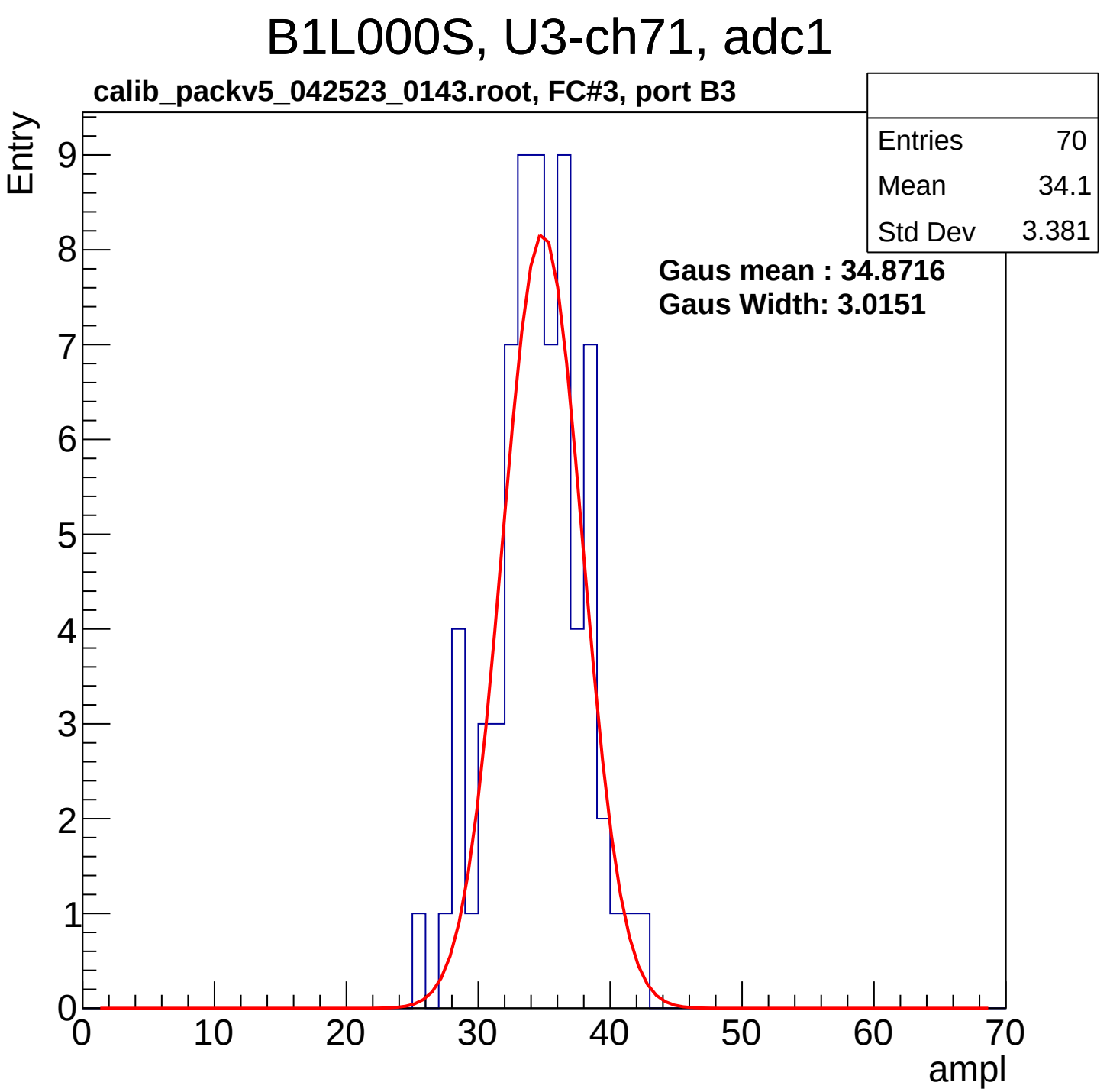
B1L000S, U3-ch71, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 34.1
Std Dev 3.381
Gaus mean : 34.8716
Gaus Width: 3.0151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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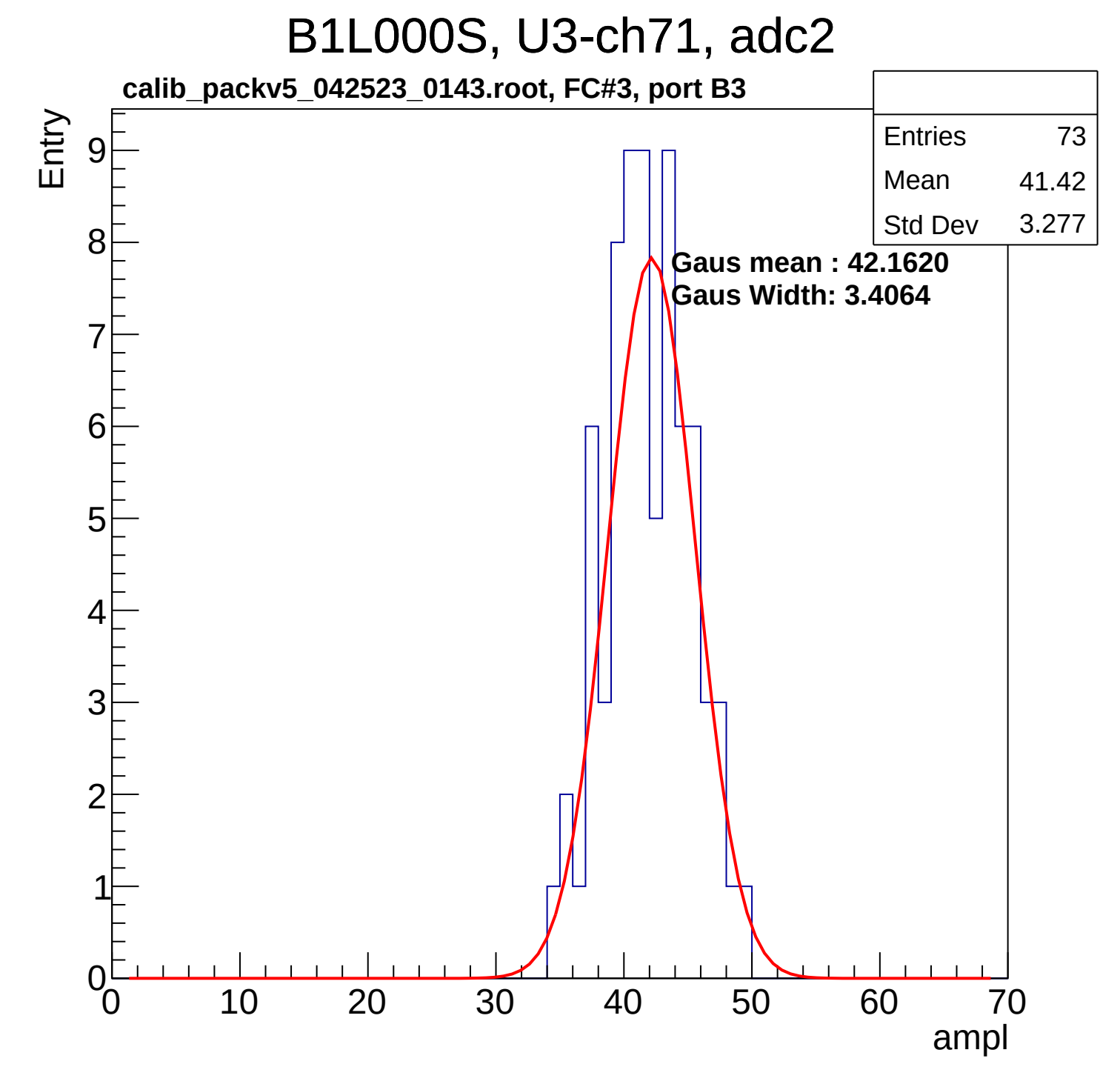
B1L000S, U3-ch71, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 41.42
Std Dev 3.277
Gaus mean : 42.1620
Gaus Width: 3.4064
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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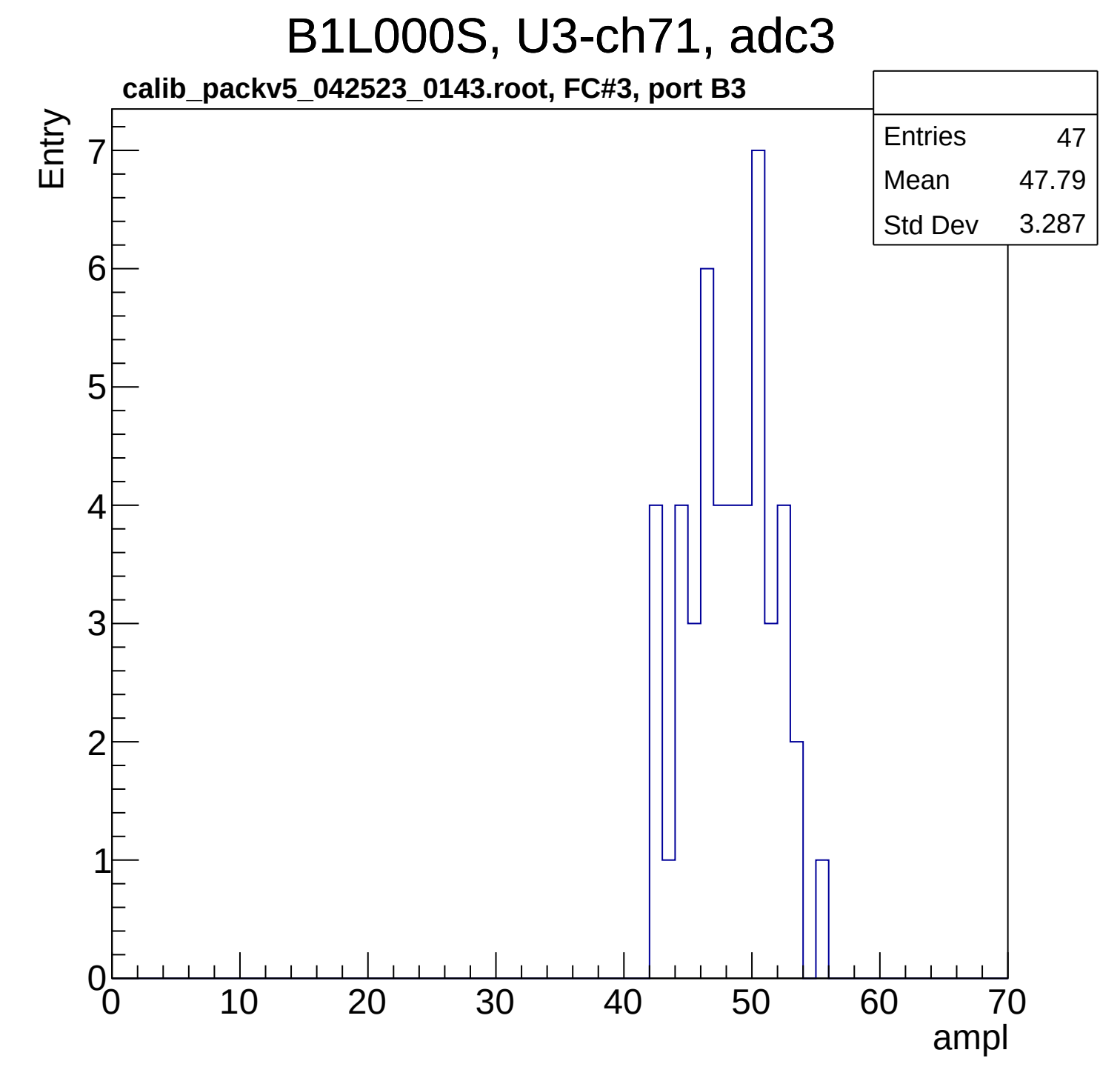
B1L000S, U3-ch71, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 47.79
Std Dev 3.287
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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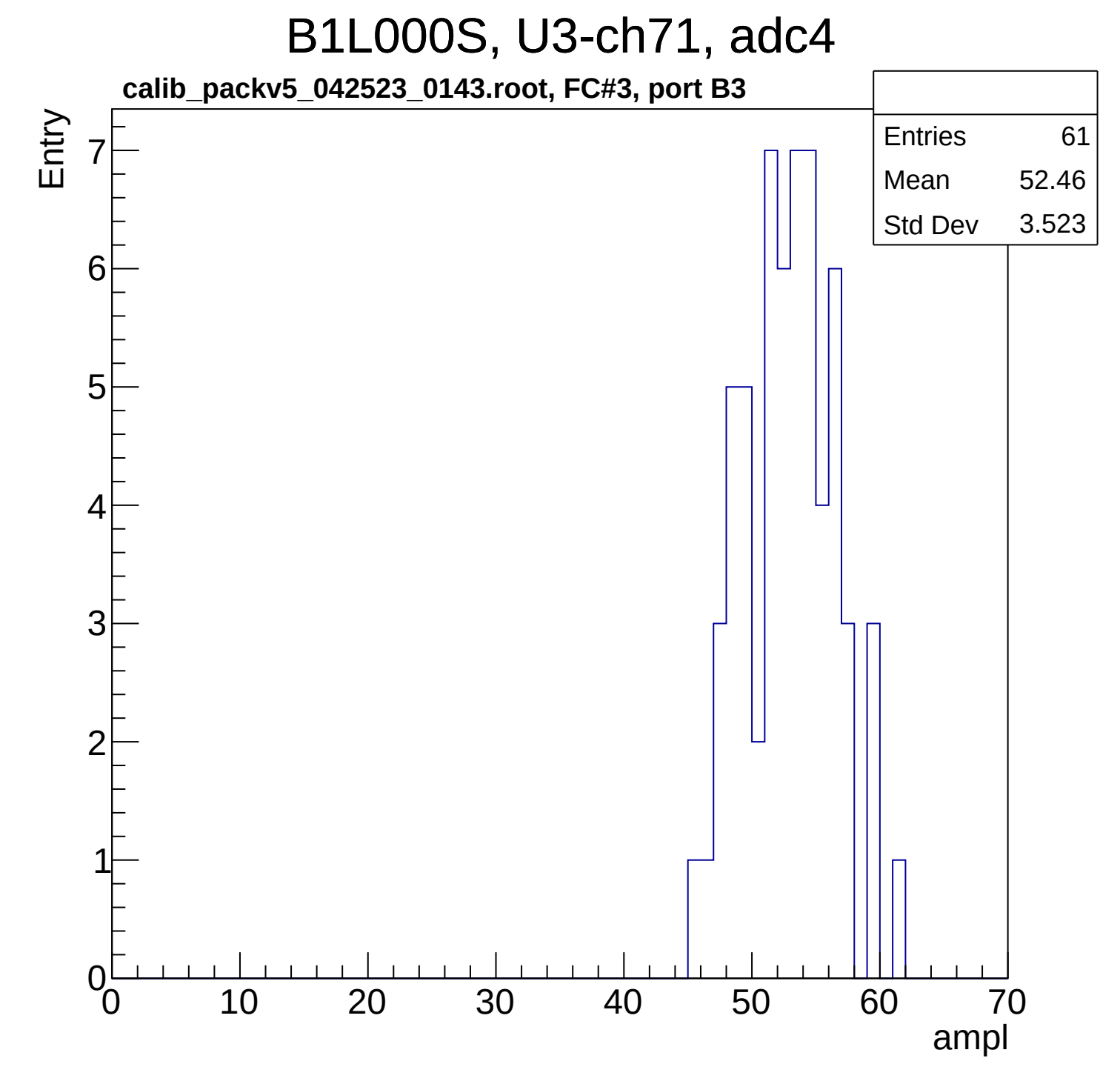
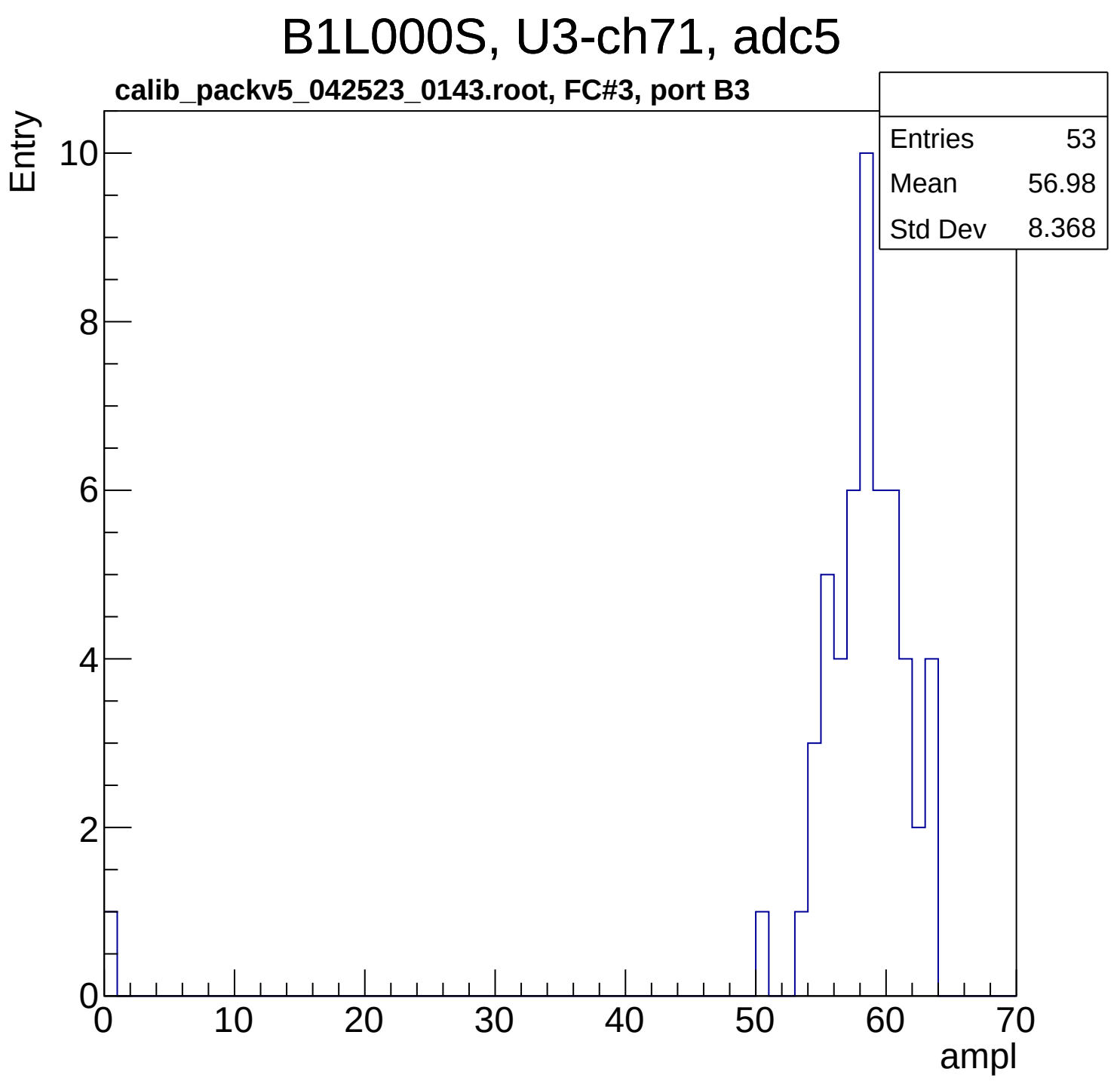
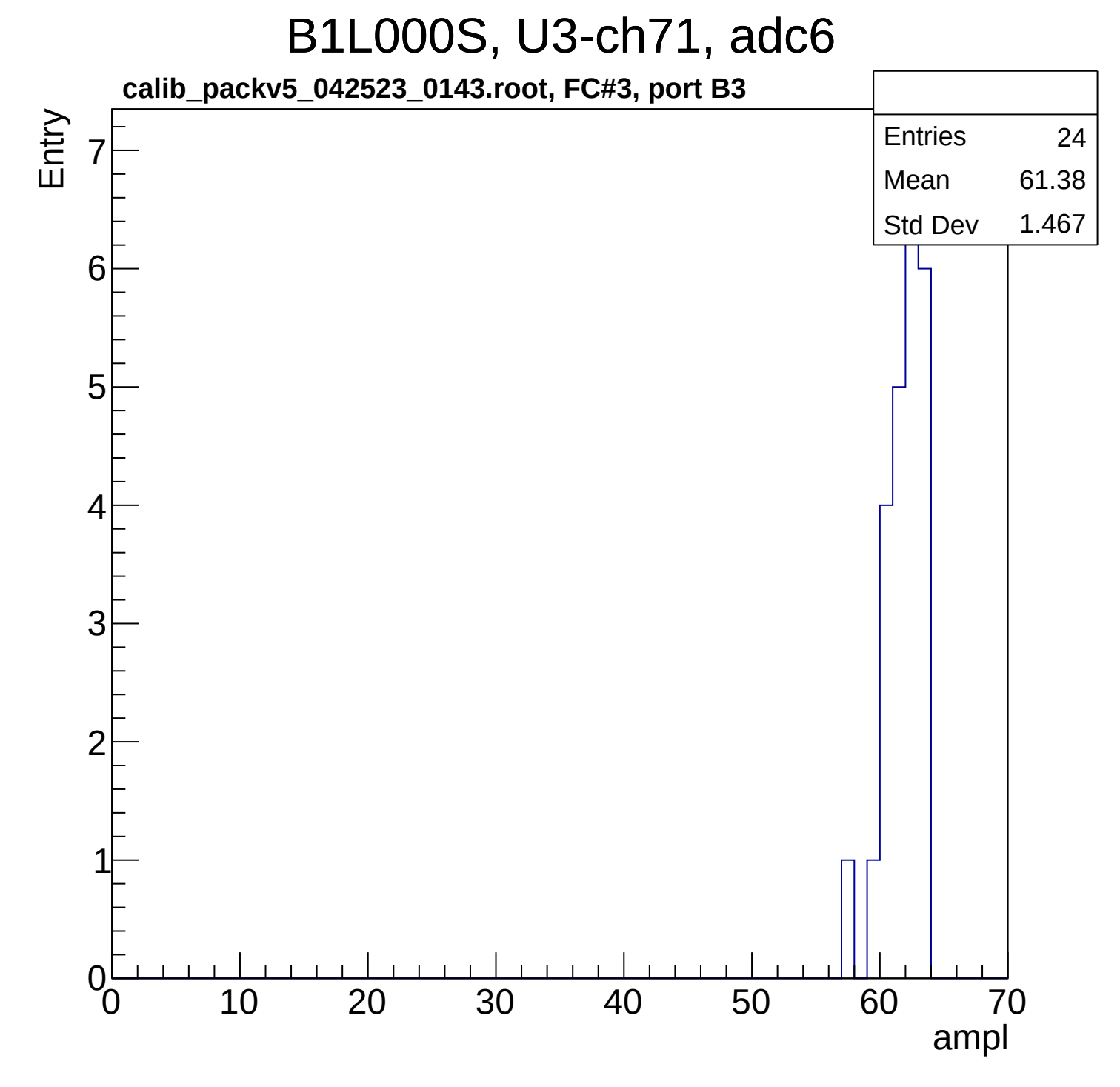

B1L000S, U3-ch71, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 52.46
Std Dev 3.523
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch71, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 56.98
Std Dev 8.368
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch71, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 24
Mean 61.38
Std Dev 1.467
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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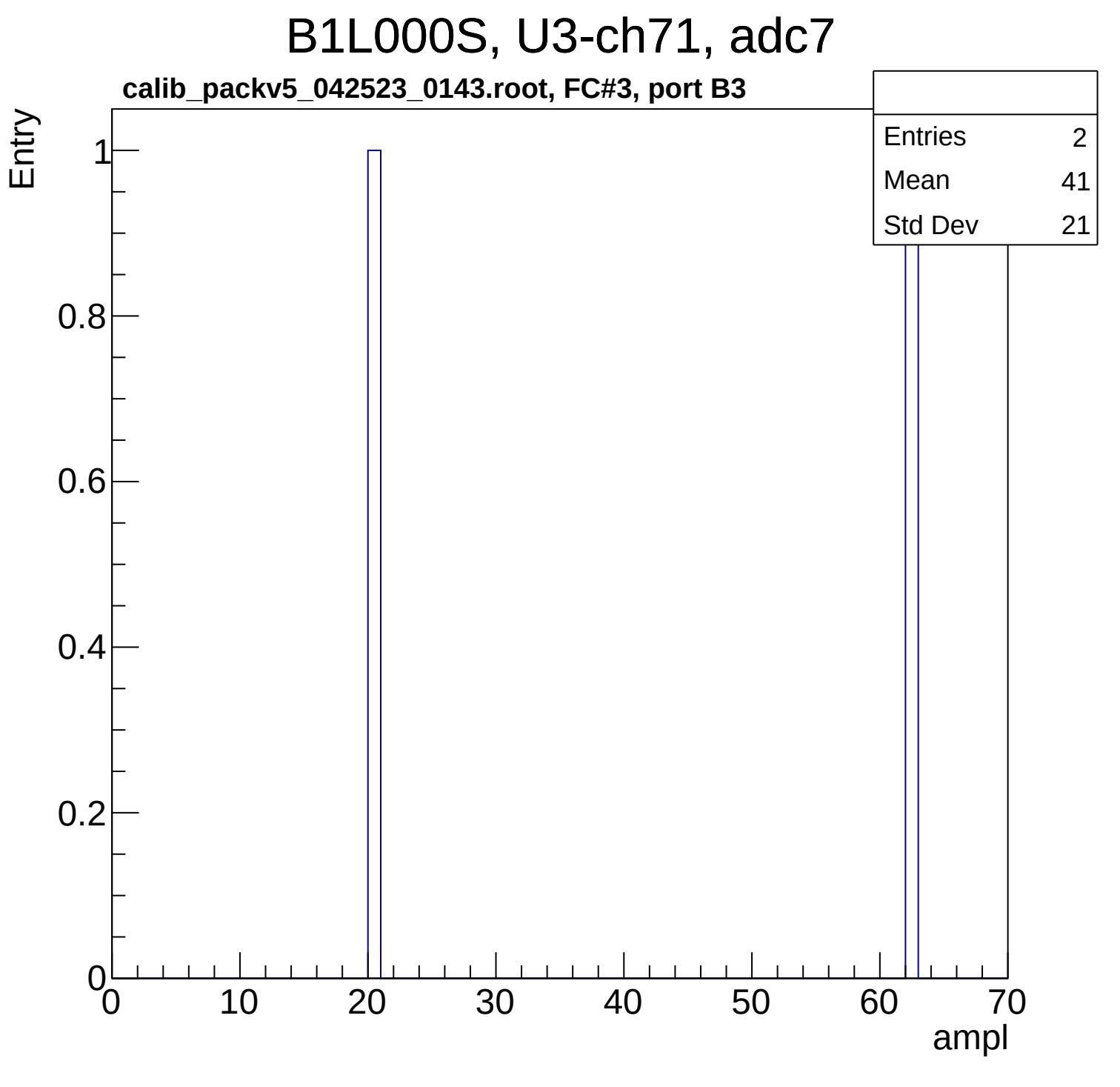
B1L000S, U3-ch71, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 41
Std Dev 21
Entry
ampl
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0.4
0.6
0.8
1
0
10
20
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40
50
60
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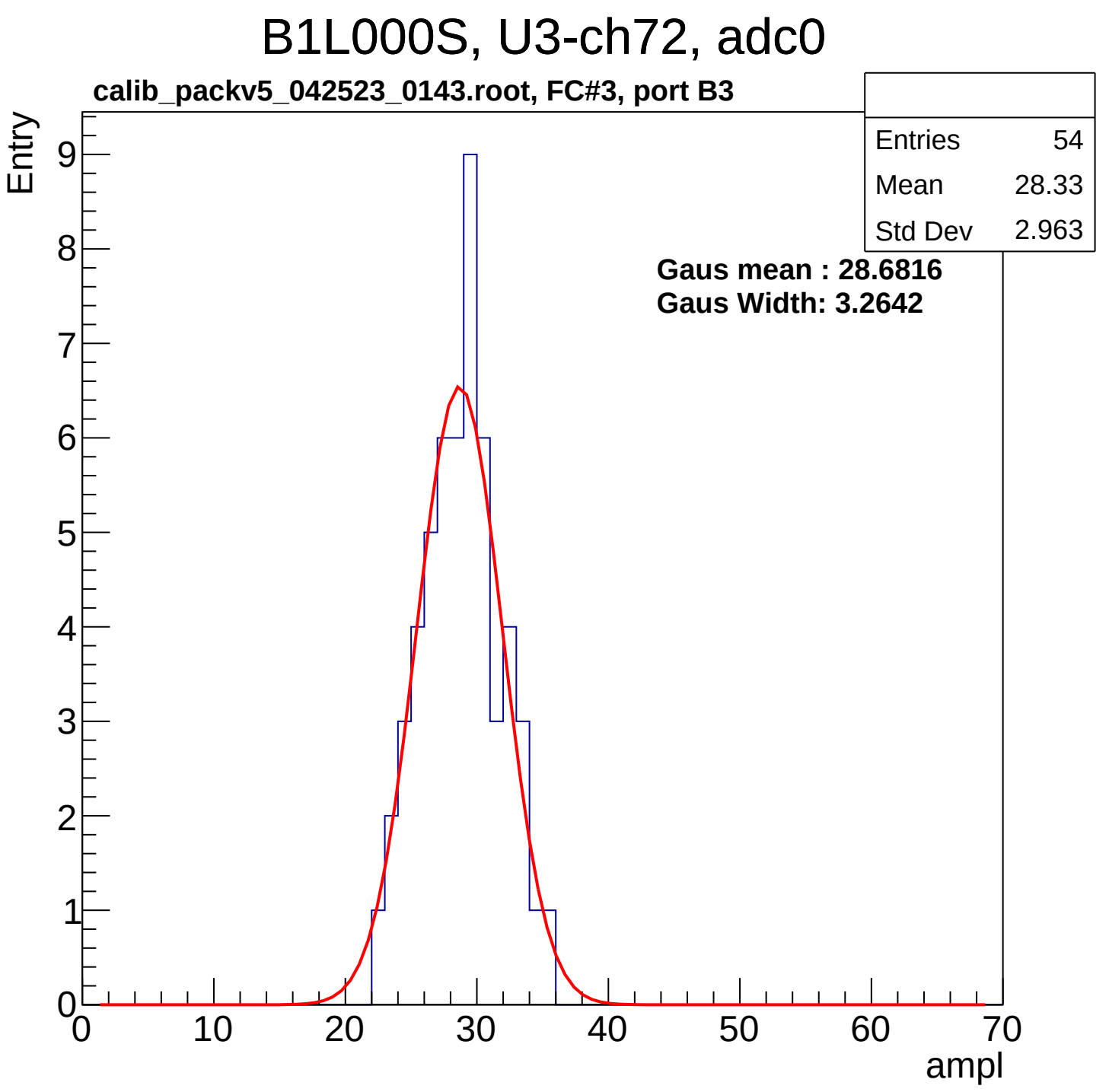
B1L000S, U3-ch72, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 28.33
Std Dev 2.963
Gaus mean : 28.6816
Gaus Width: 3.2642
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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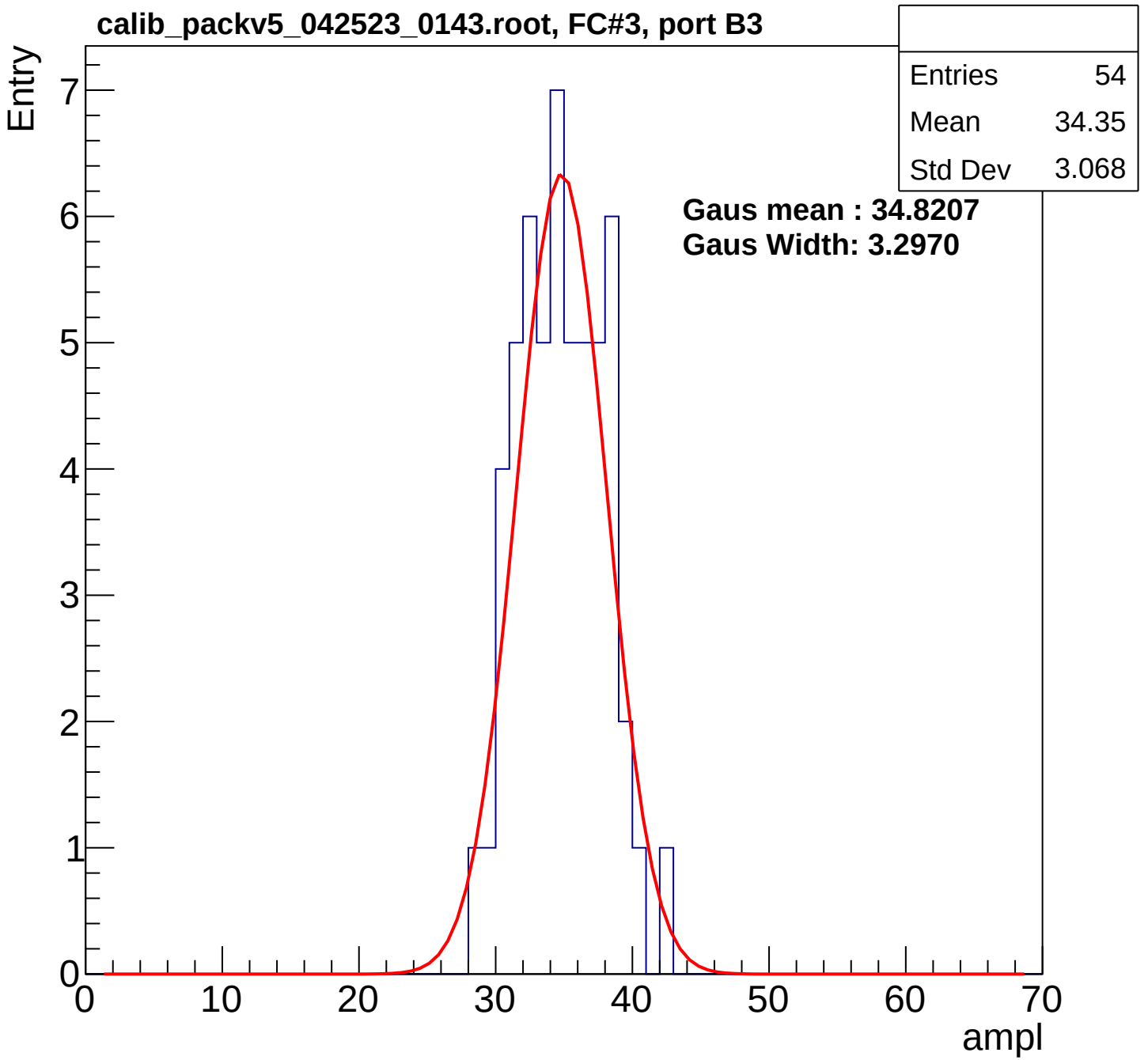
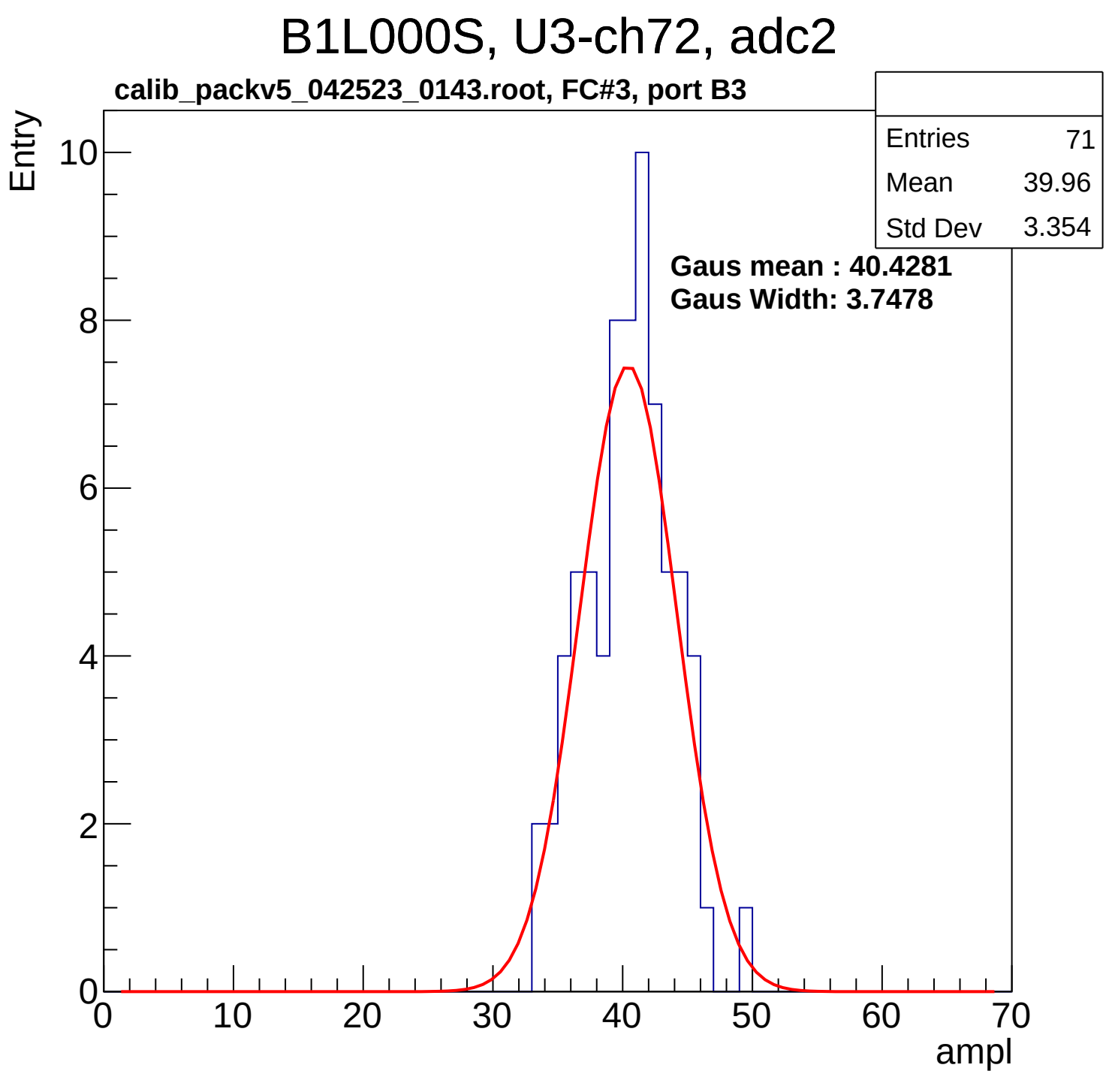
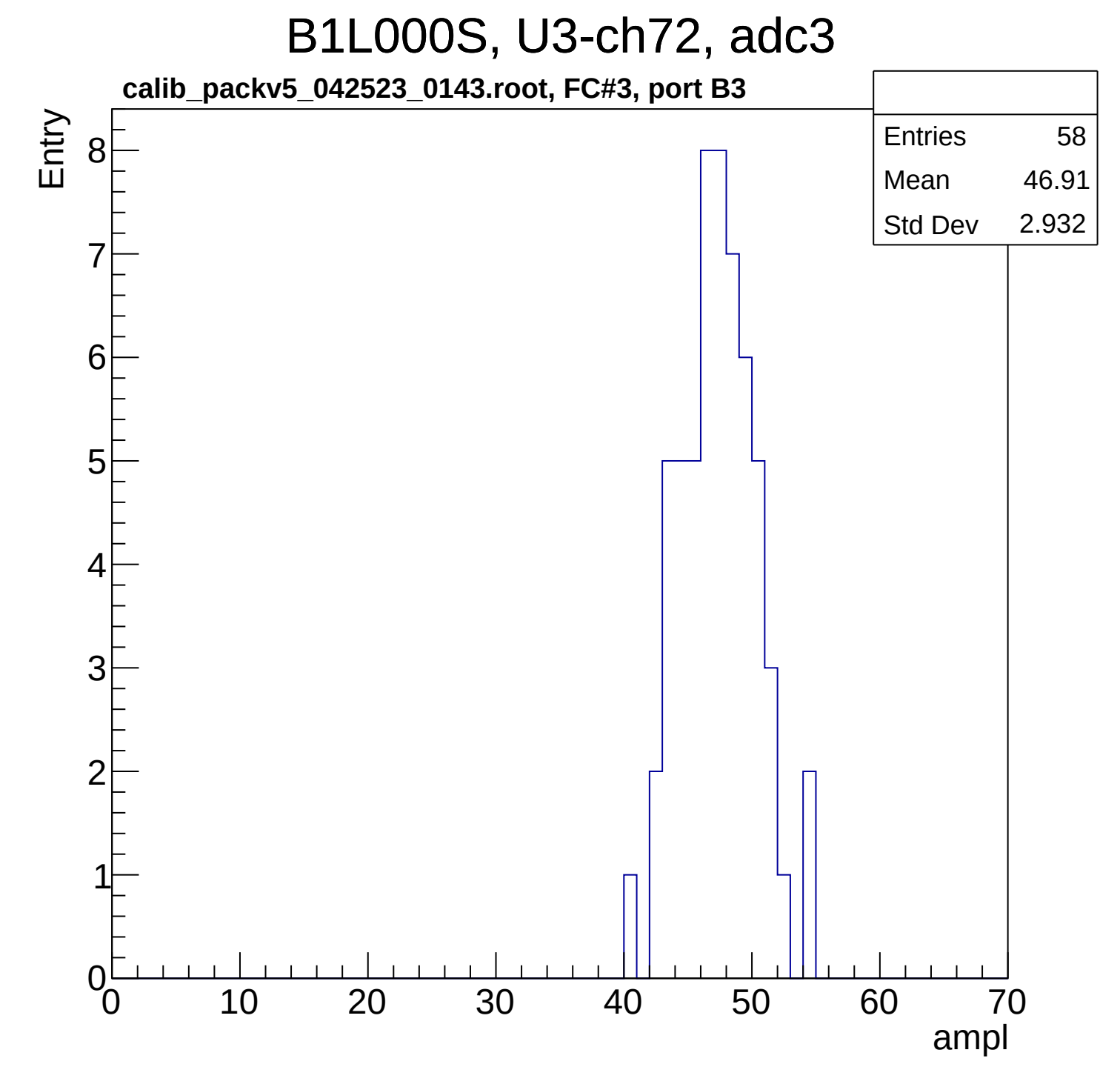

B1L000S, U3-ch72, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 34.35
Std Dev 3.068
Gaus mean : 34.8207
Gaus Width: 3.2970
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch72, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 39.96
Std Dev 3.354
Gaus mean : 40.4281
Gaus Width: 3.7478
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch72, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 46.91
Std Dev 2.932
Entry
ampl
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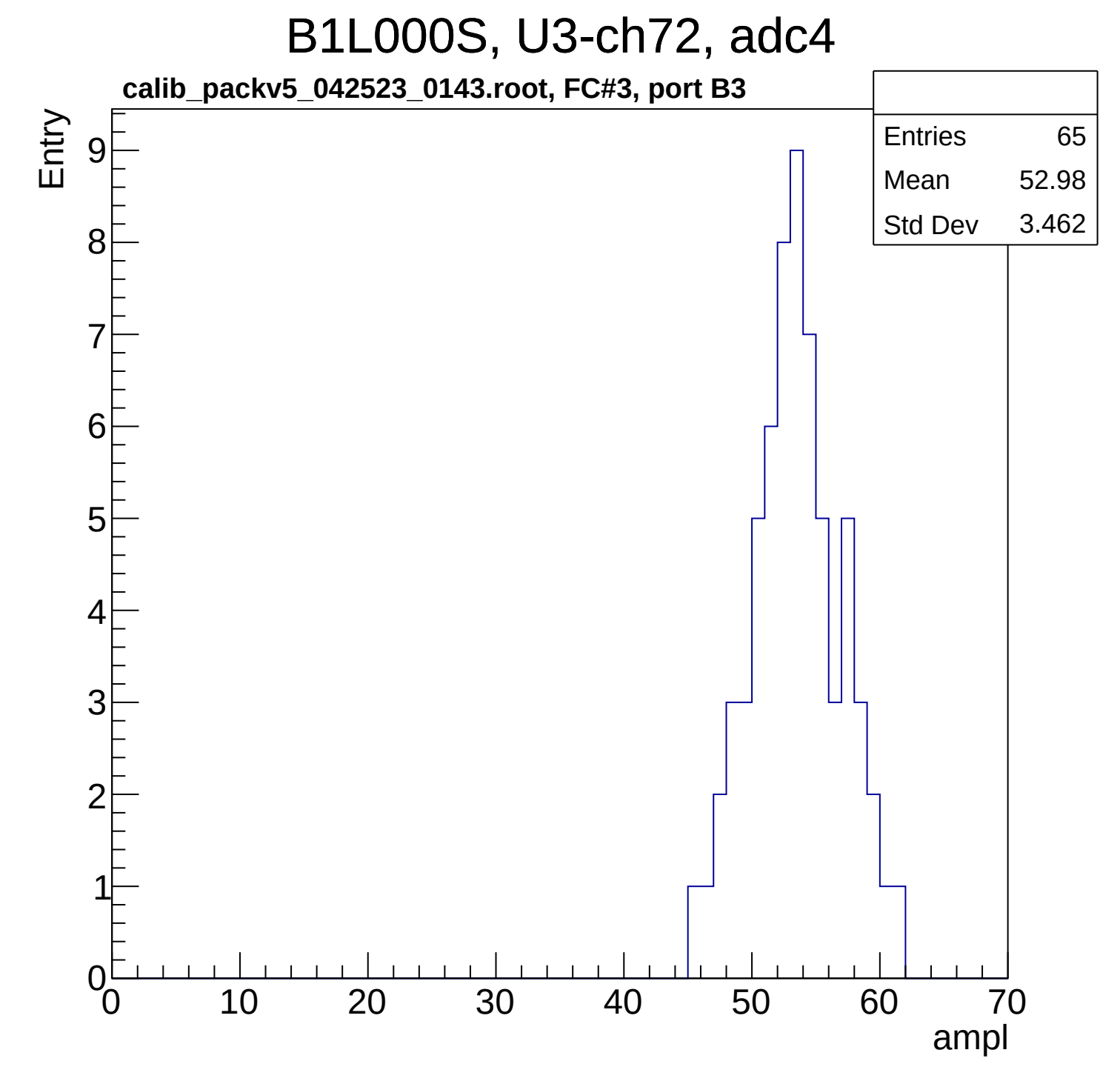
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch72, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 52.98
Std Dev 3.462
Entry
ampl
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3
4
5
6
7
8
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0
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60
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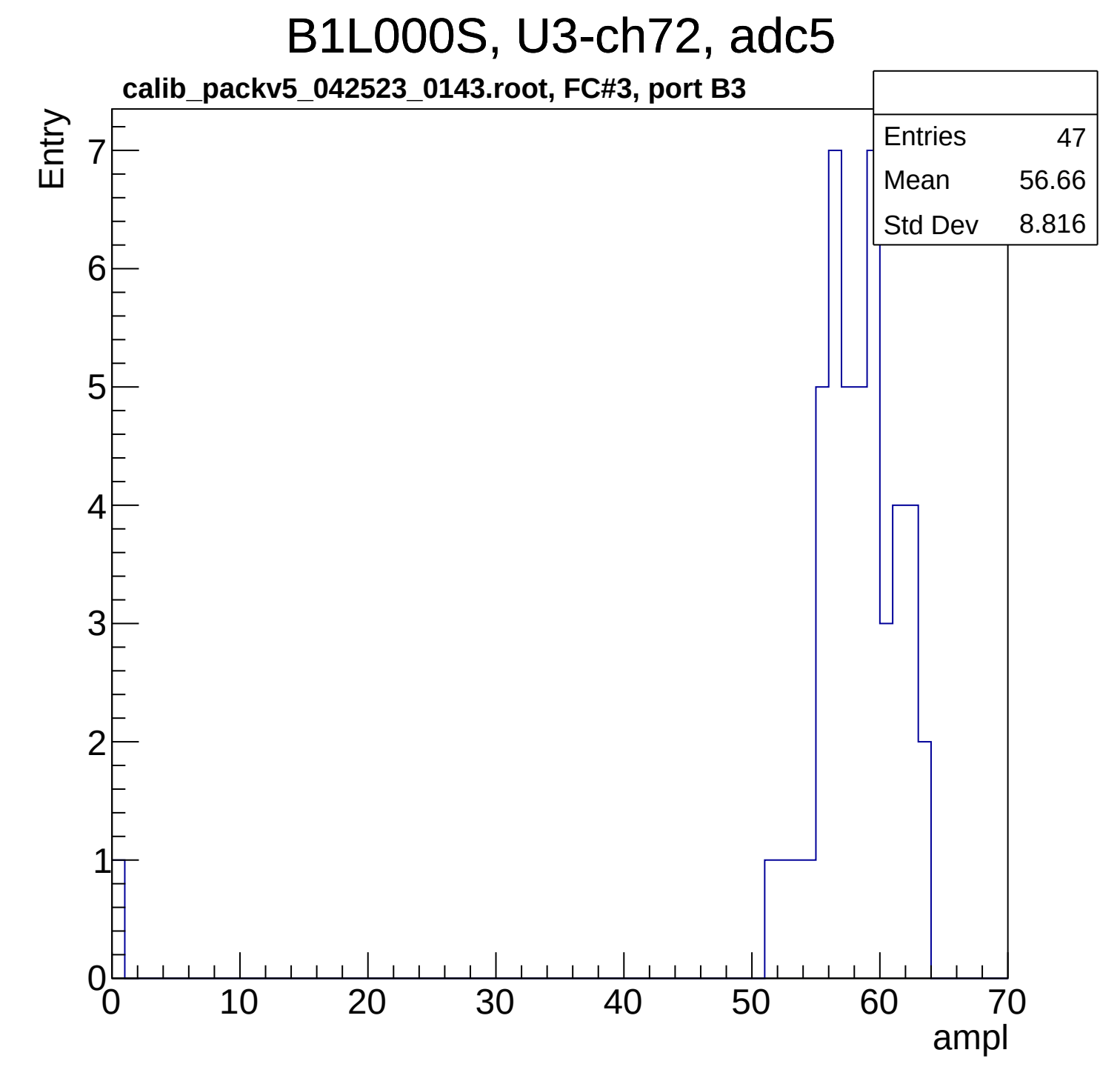
B1L000S, U3-ch72, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 56.66
Std Dev 8.816
Entry
ampl
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1
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3
4
5
6
7
0
10
20
30
40
50
60
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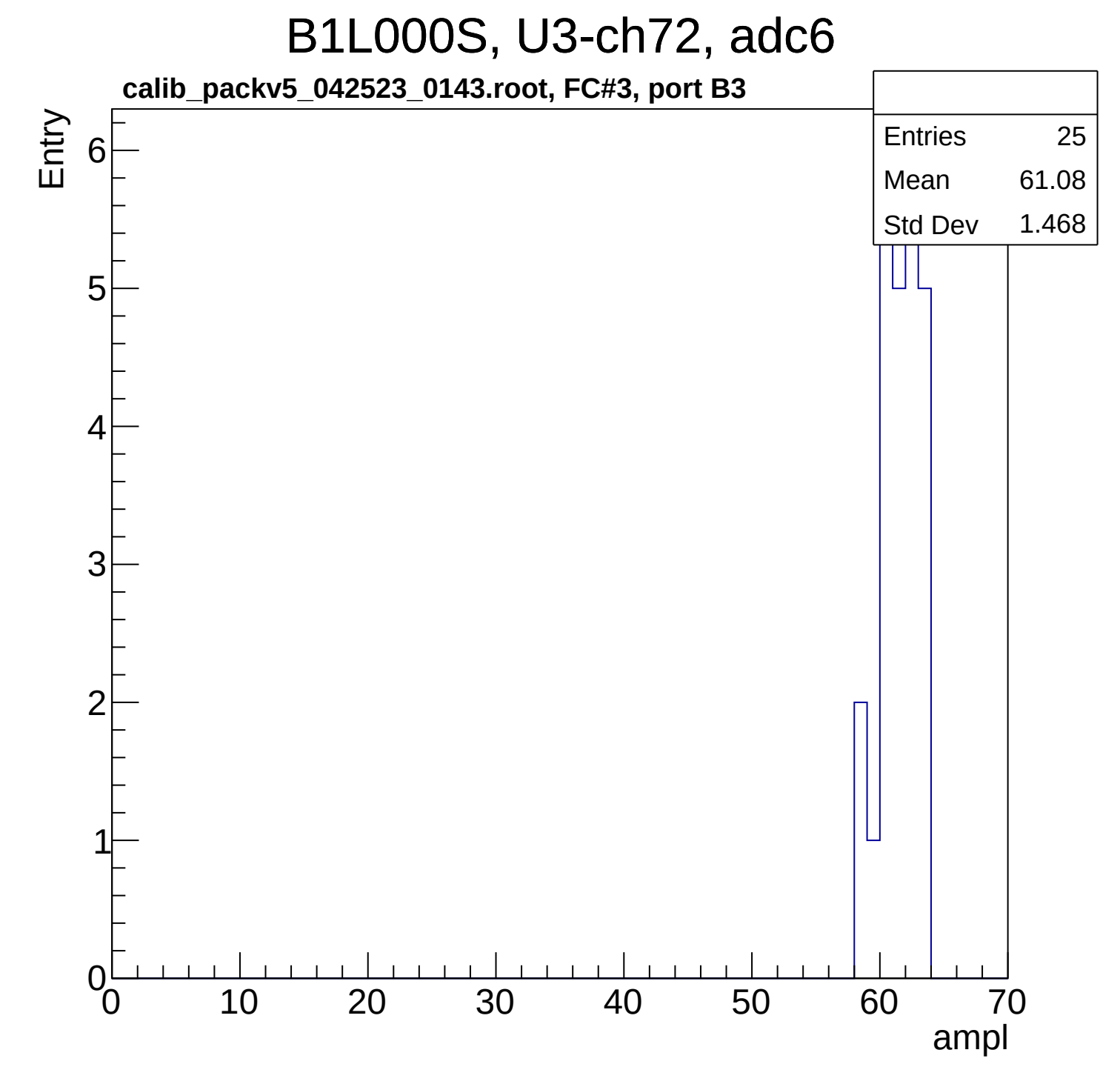
B1L000S, U3-ch72, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 61.08
Std Dev 1.468
Entry
0
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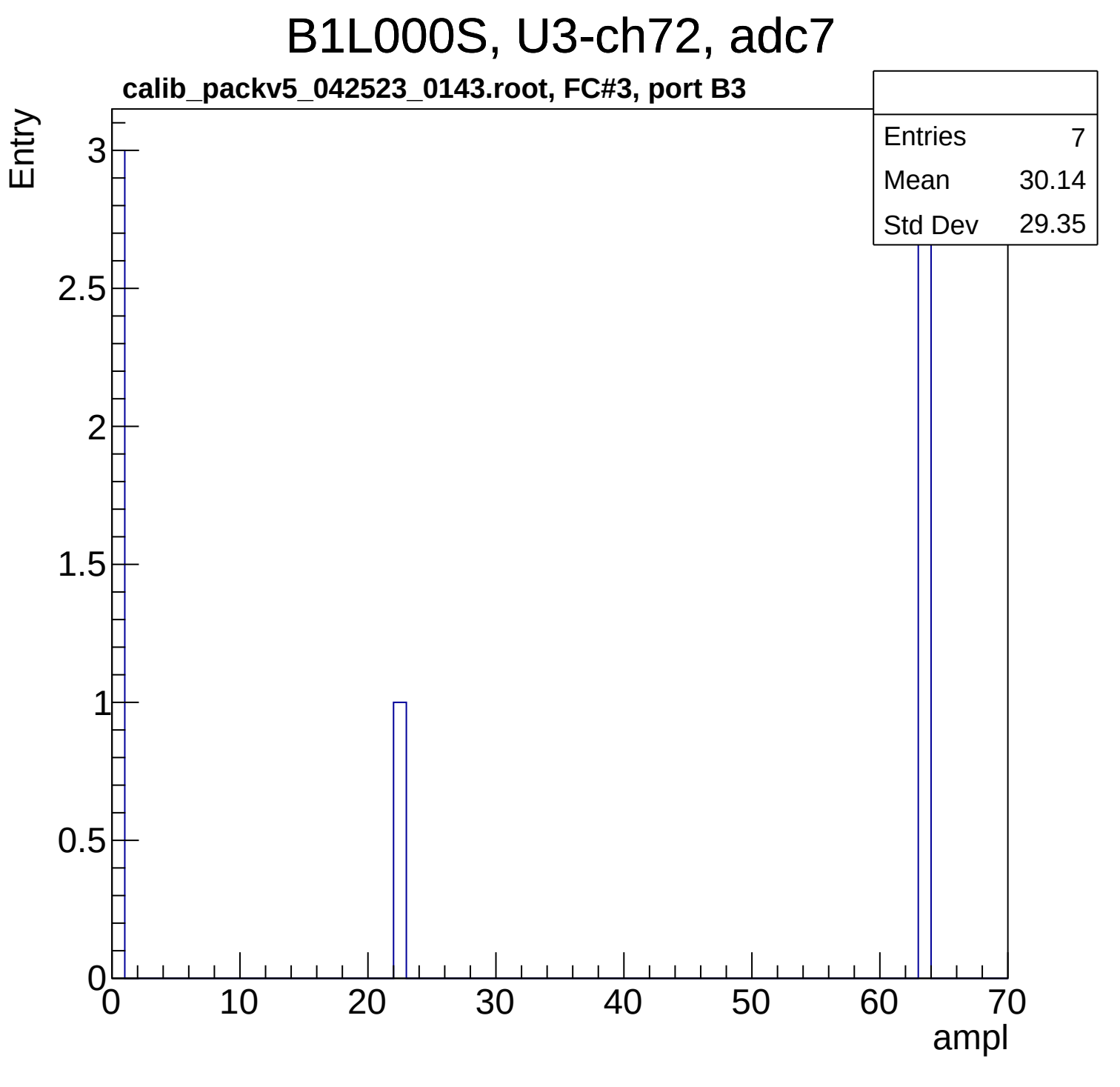
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch72, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 30.14
Std Dev 29.35
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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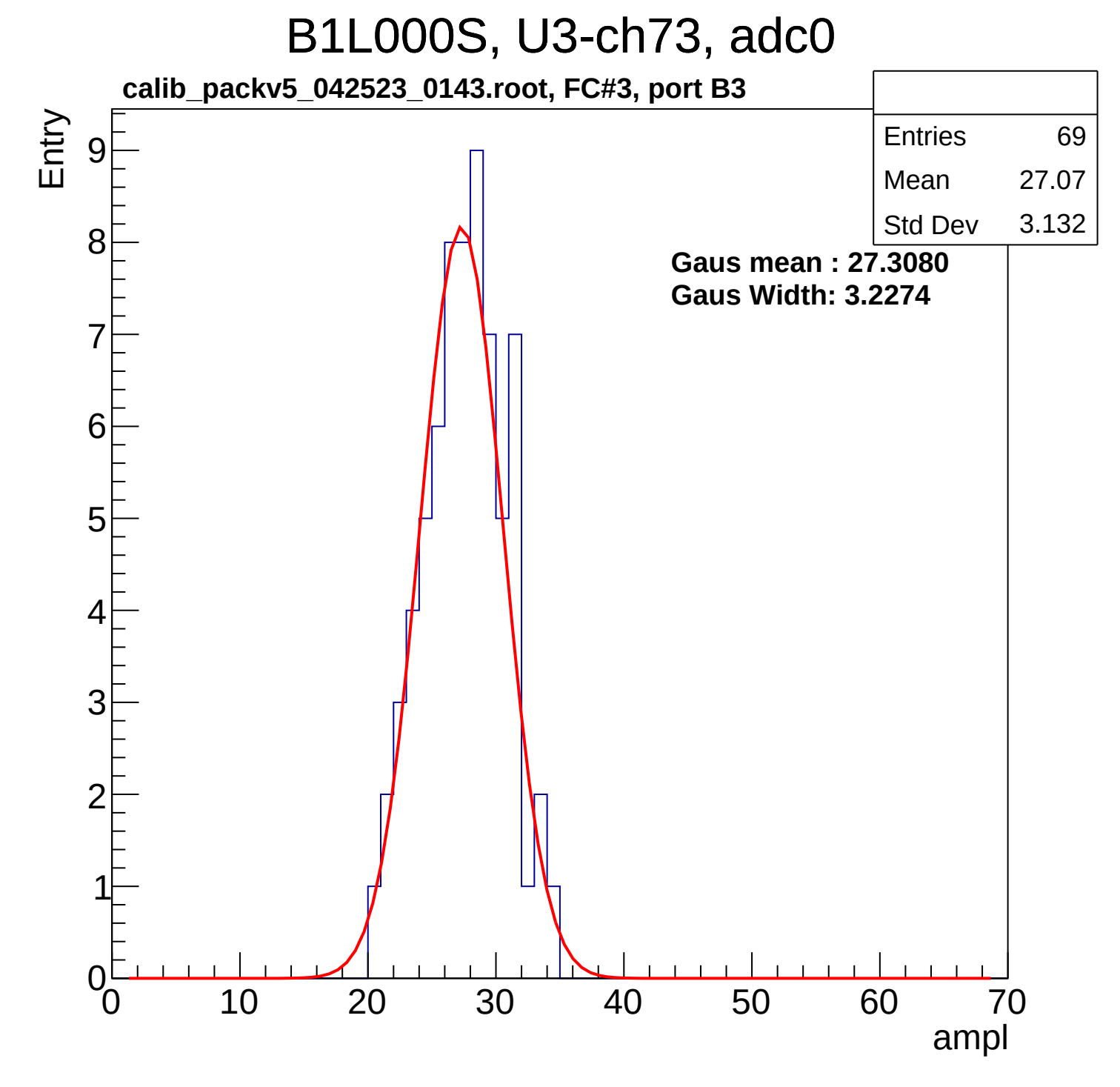
20
30
40
50
60
70


B1L000S, U3-ch73, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 27.07
Std Dev 3.132
Gaus mean : 27.3080
Gaus Width: 3.2274
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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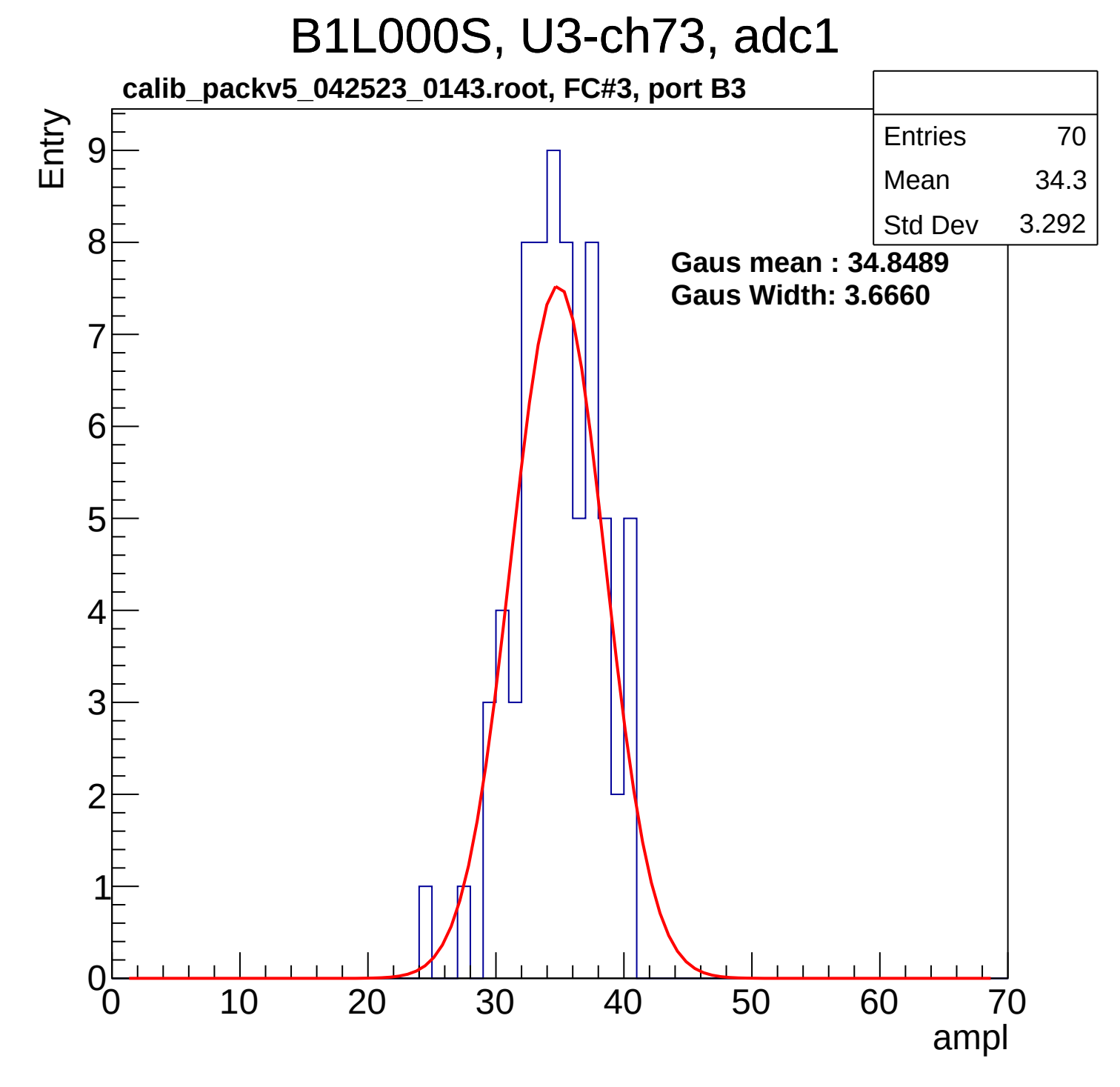
B1L000S, U3-ch73, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 34.3
Std Dev 3.292
Gaus mean : 34.8489
Gaus Width: 3.6660
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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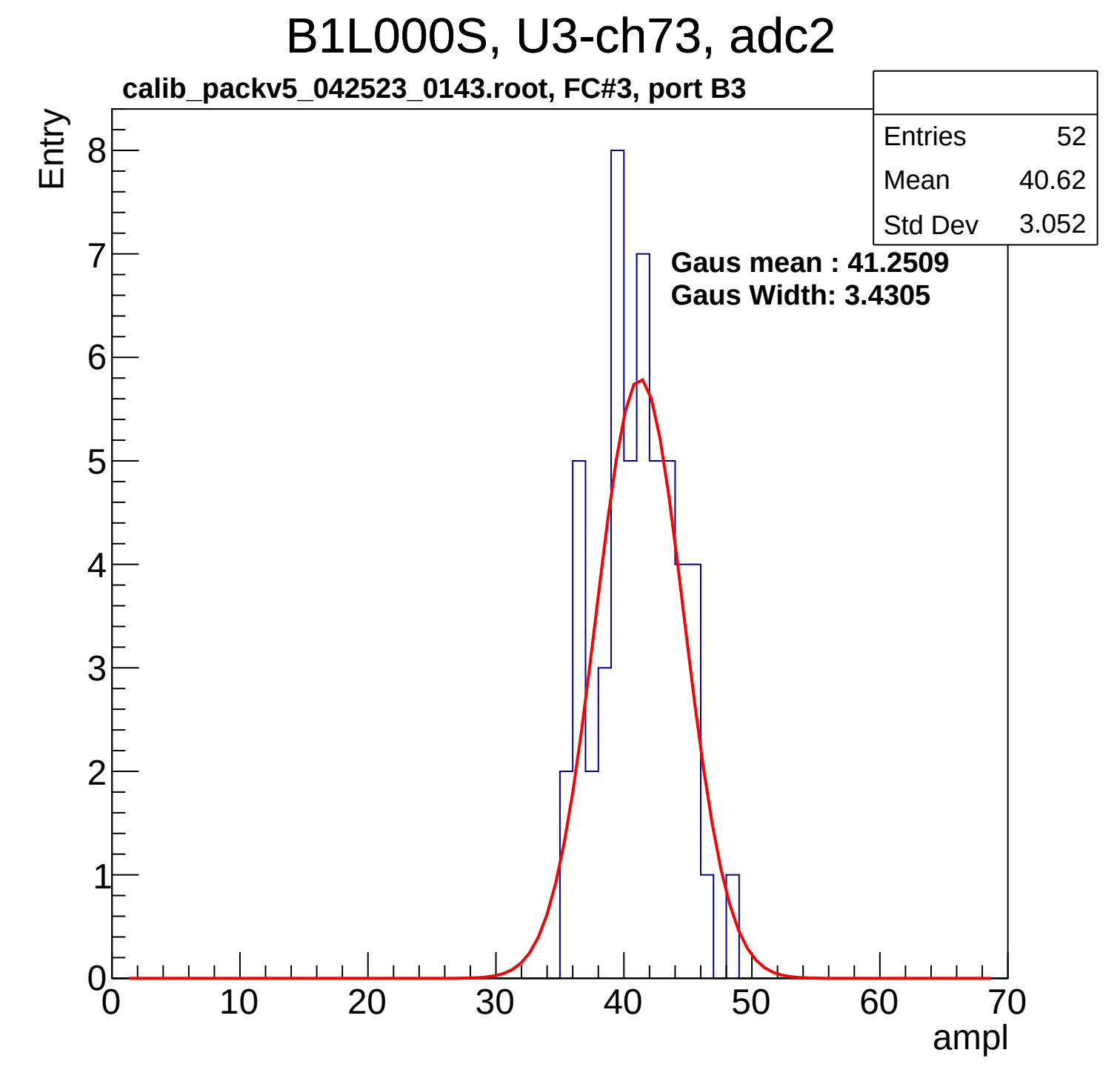
B1L000S, U3-ch73, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 40.62
Std Dev 3.052
Gaus mean : 41.2509
Gaus Width: 3.4305
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
50
60
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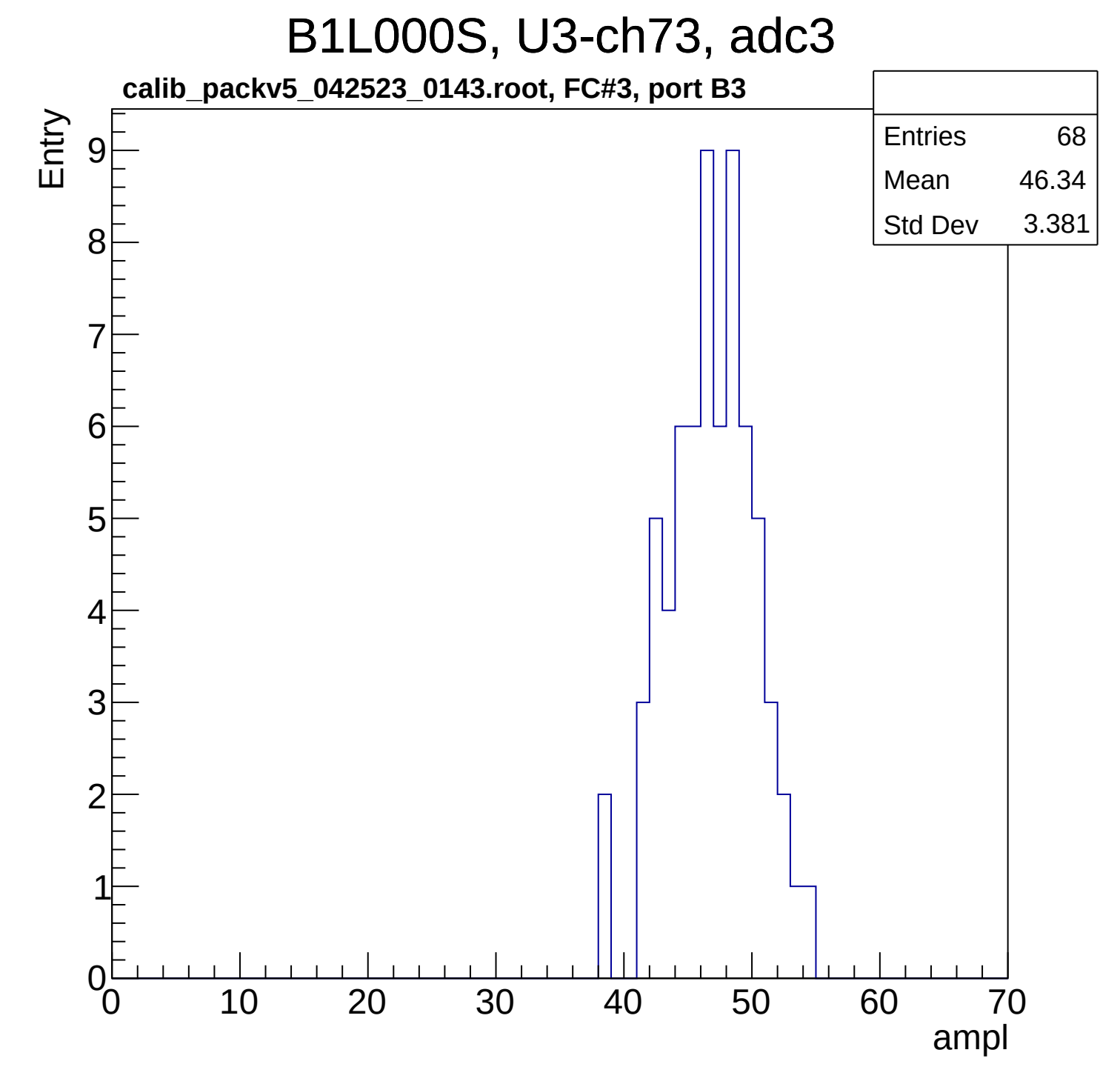
B1L000S, U3-ch73, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 46.34
Std Dev 3.381
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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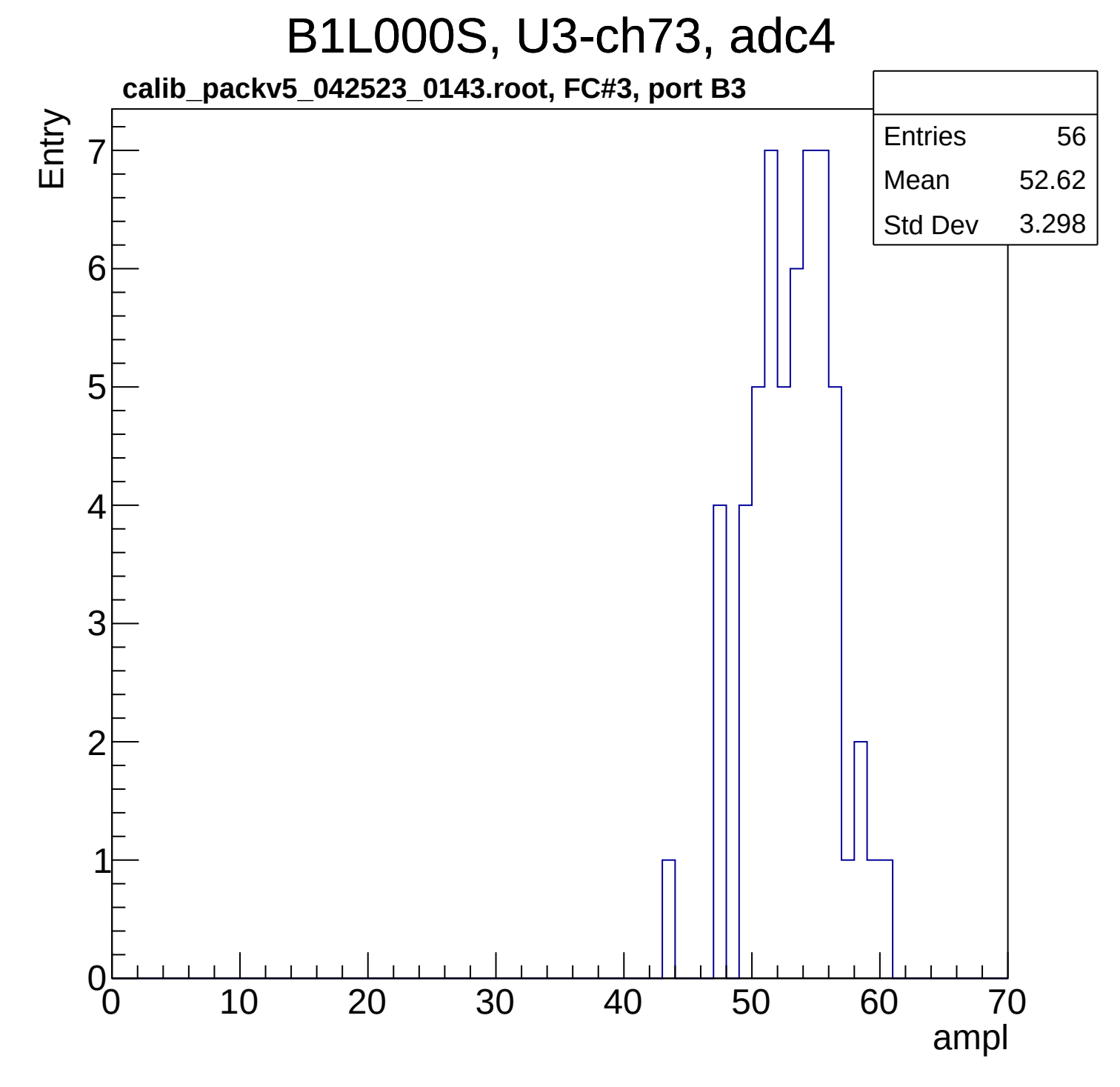
40
50
60
70


B1L000S, U3-ch73, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 52.62
Std Dev 3.298
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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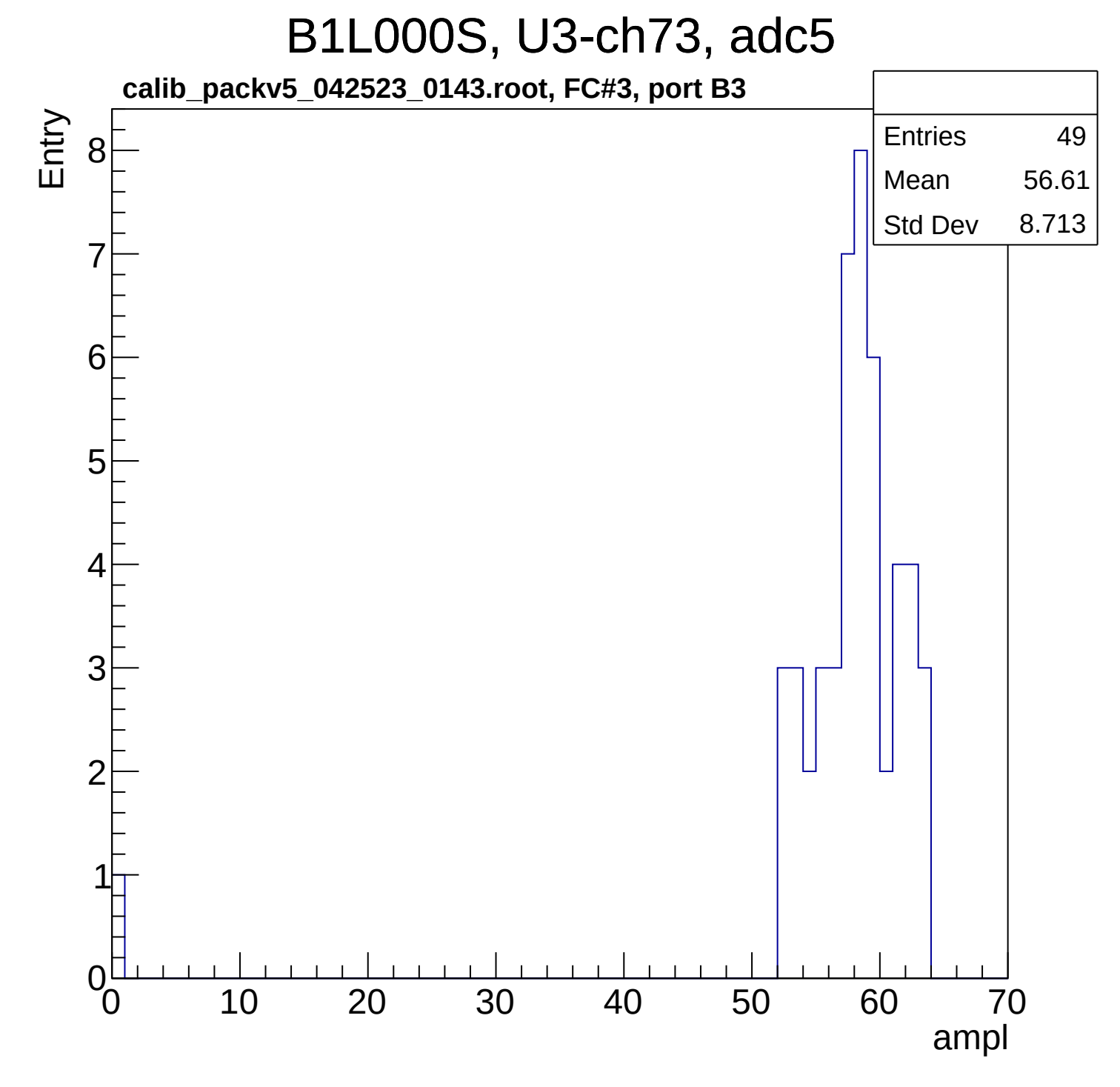
B1L000S, U3-ch73, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 56.61
Std Dev 8.713
Entry
ampl
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1
2
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4
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7
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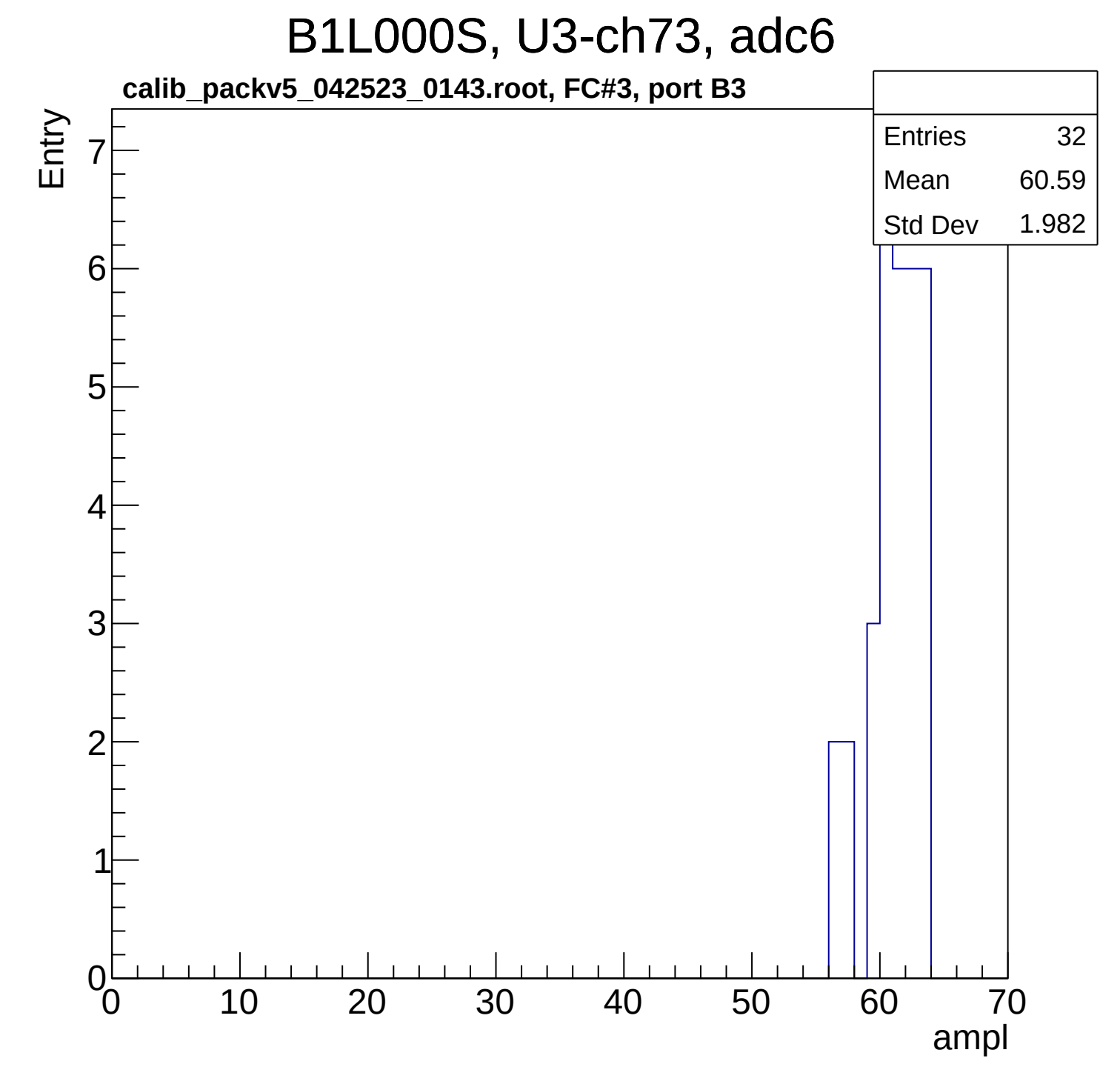
B1L000S, U3-ch73, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 60.59
Std Dev 1.982
Entry
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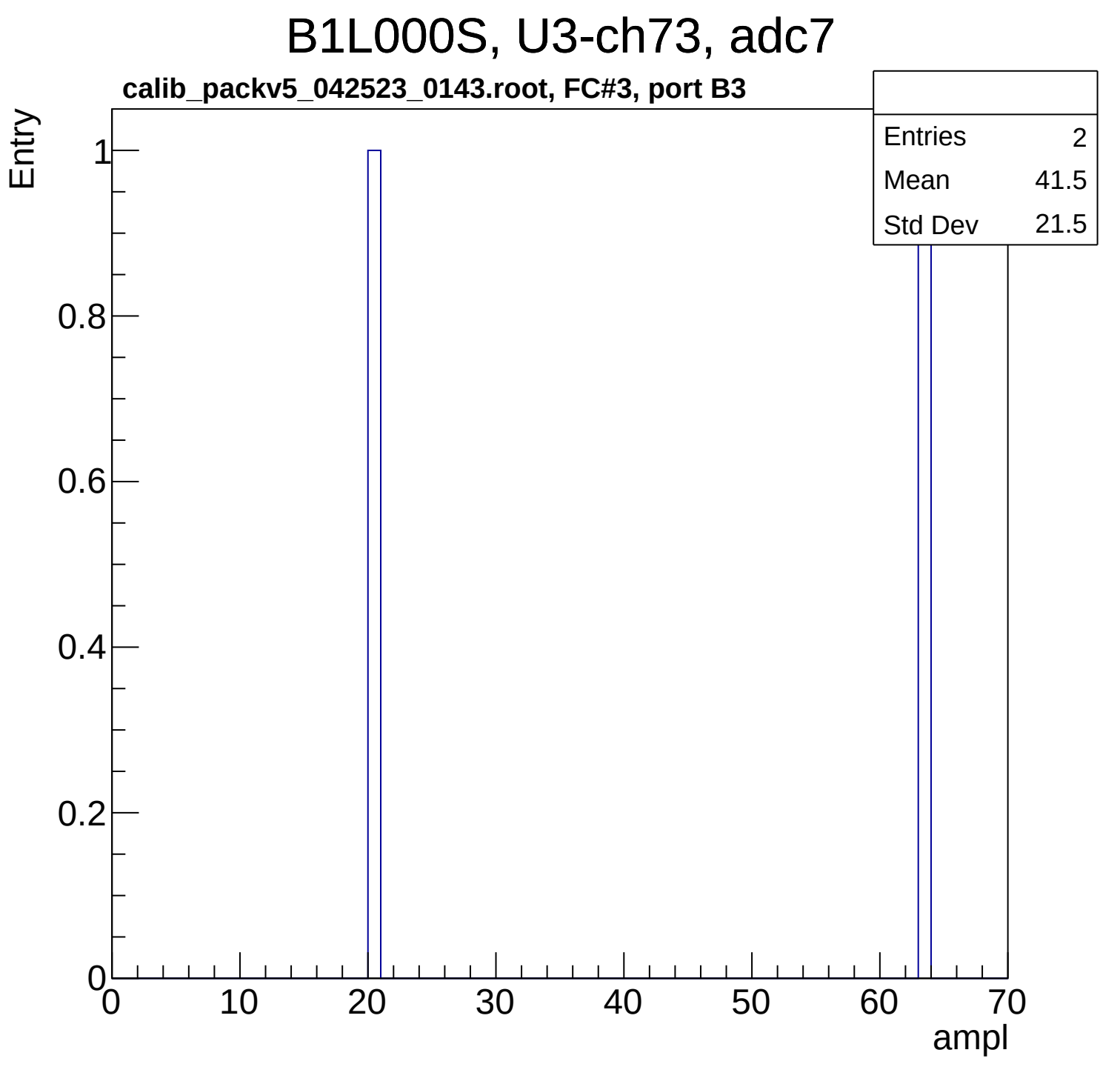
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch73, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 41.5
Std Dev 21.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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40
50
60
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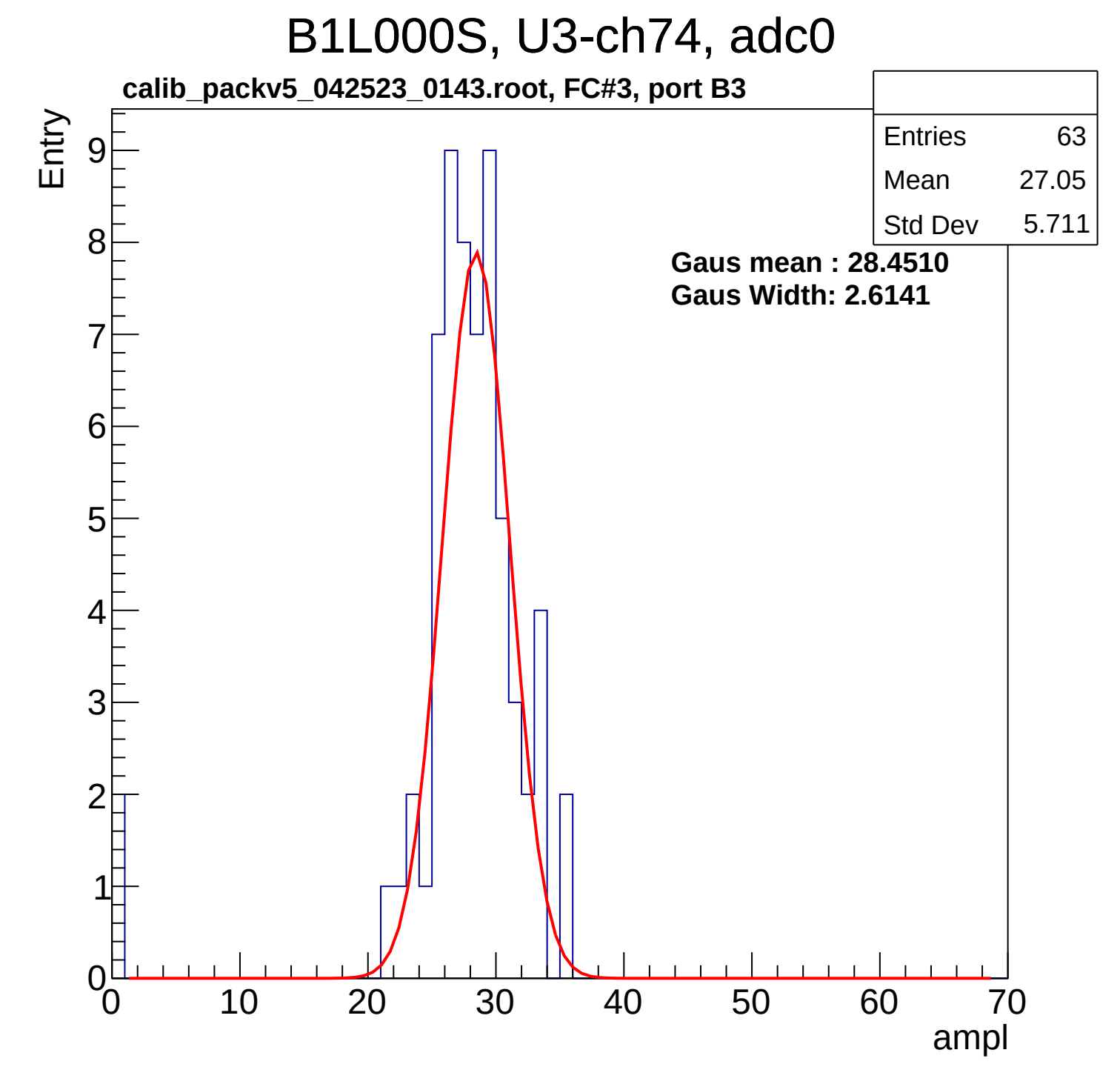
B1L000S, U3-ch74, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 27.05
Std Dev 5.711
Gaus mean : 28.4510
Gaus Width: 2.6141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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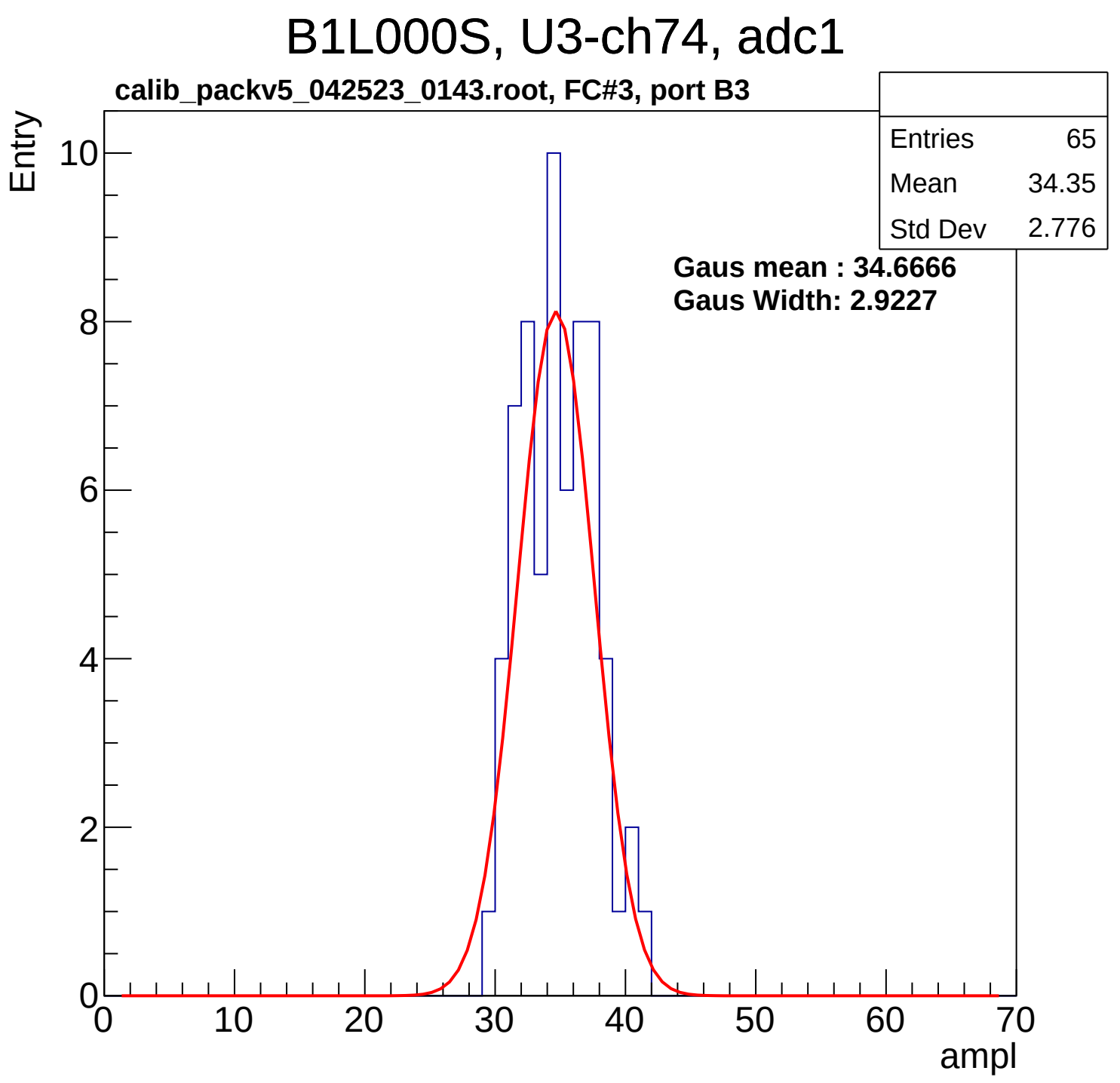
B1L000S, U3-ch74, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 34.35
Std Dev 2.776
Gaus mean : 34.6666
Gaus Width: 2.9227
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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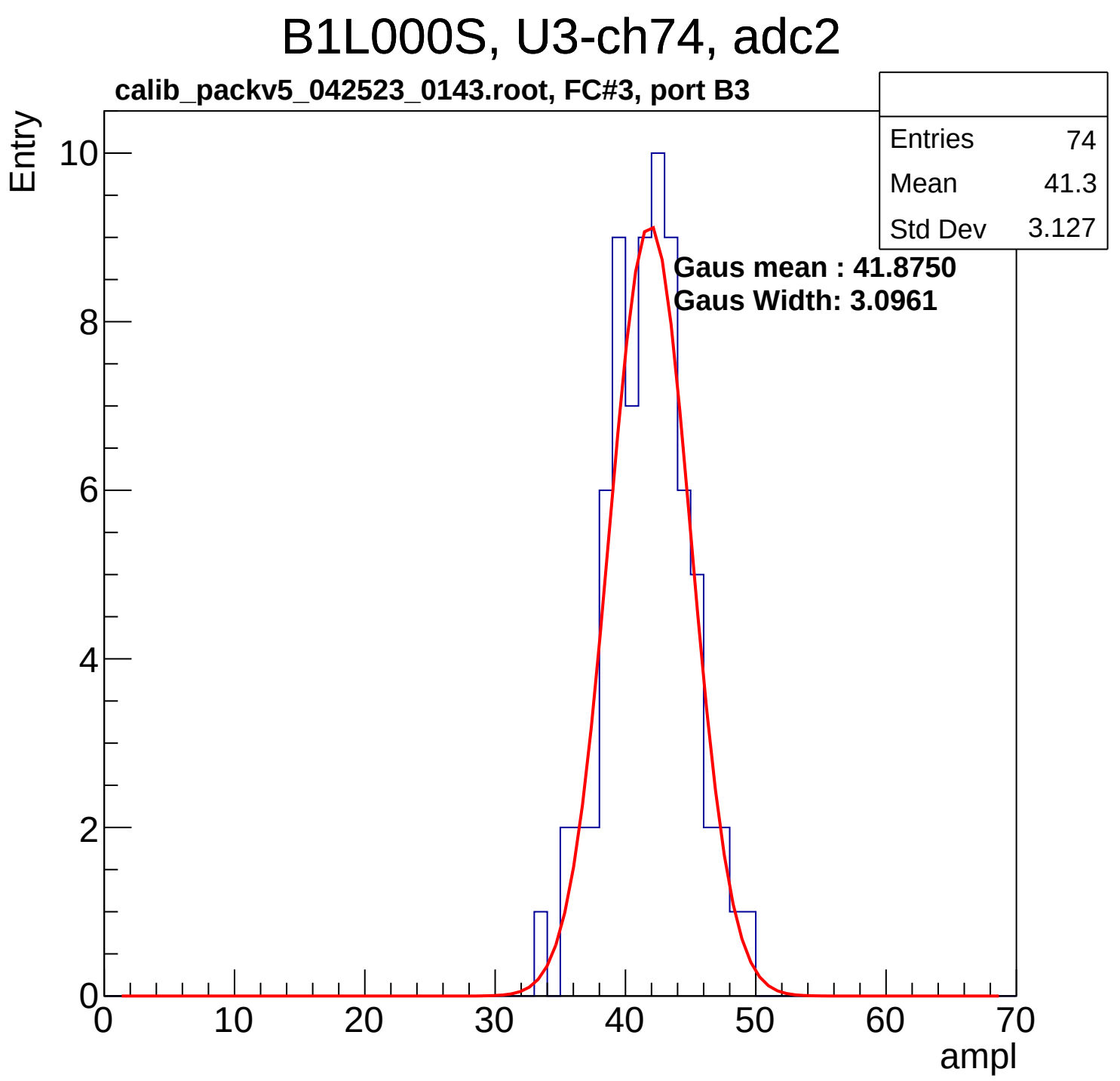
B1L000S, U3-ch74, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 41.3
Std Dev 3.127
Gaus mean : 41.8750
Gaus Width: 3.0961
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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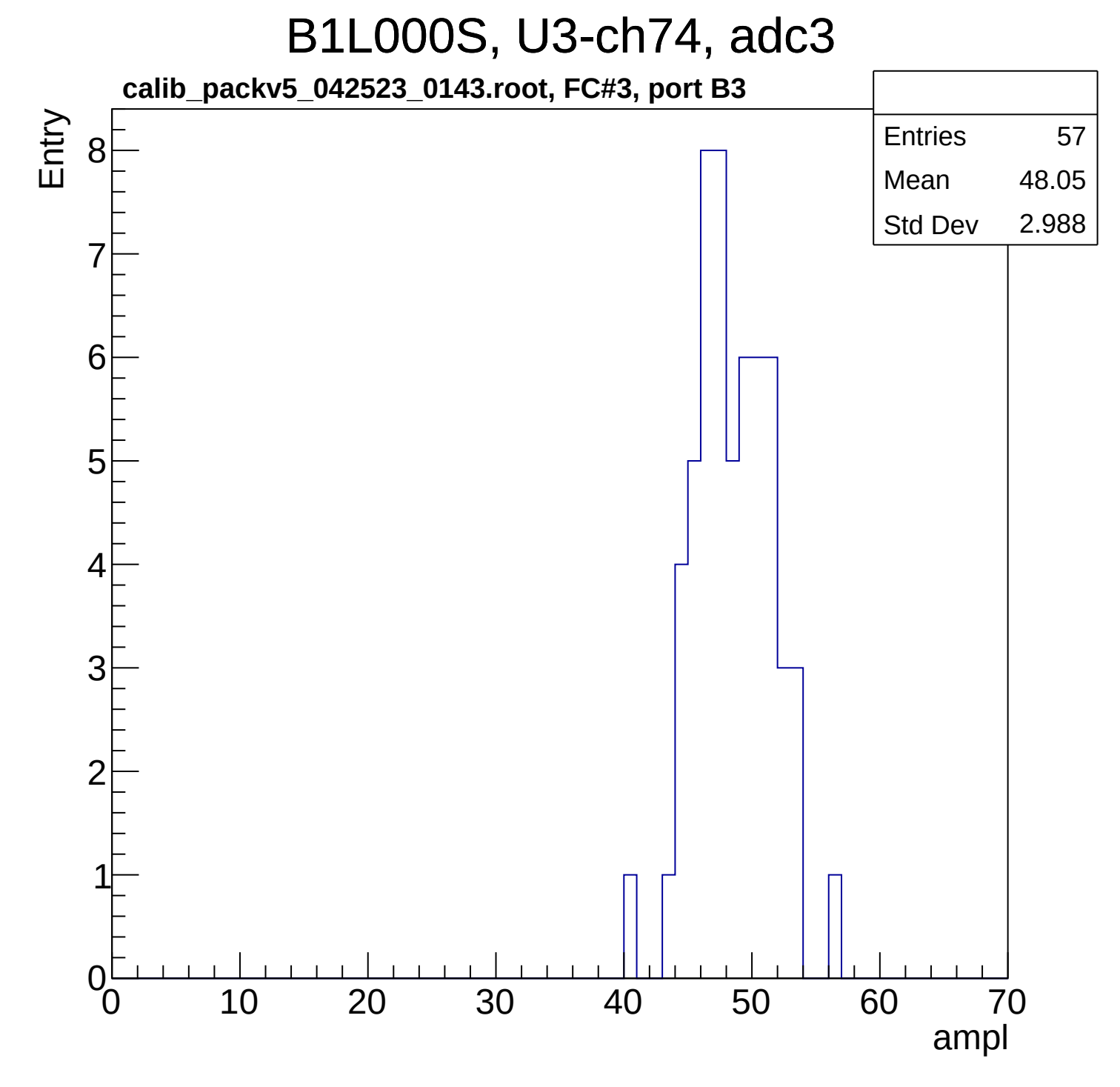
B1L000S, U3-ch74, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 48.05
Std Dev 2.988
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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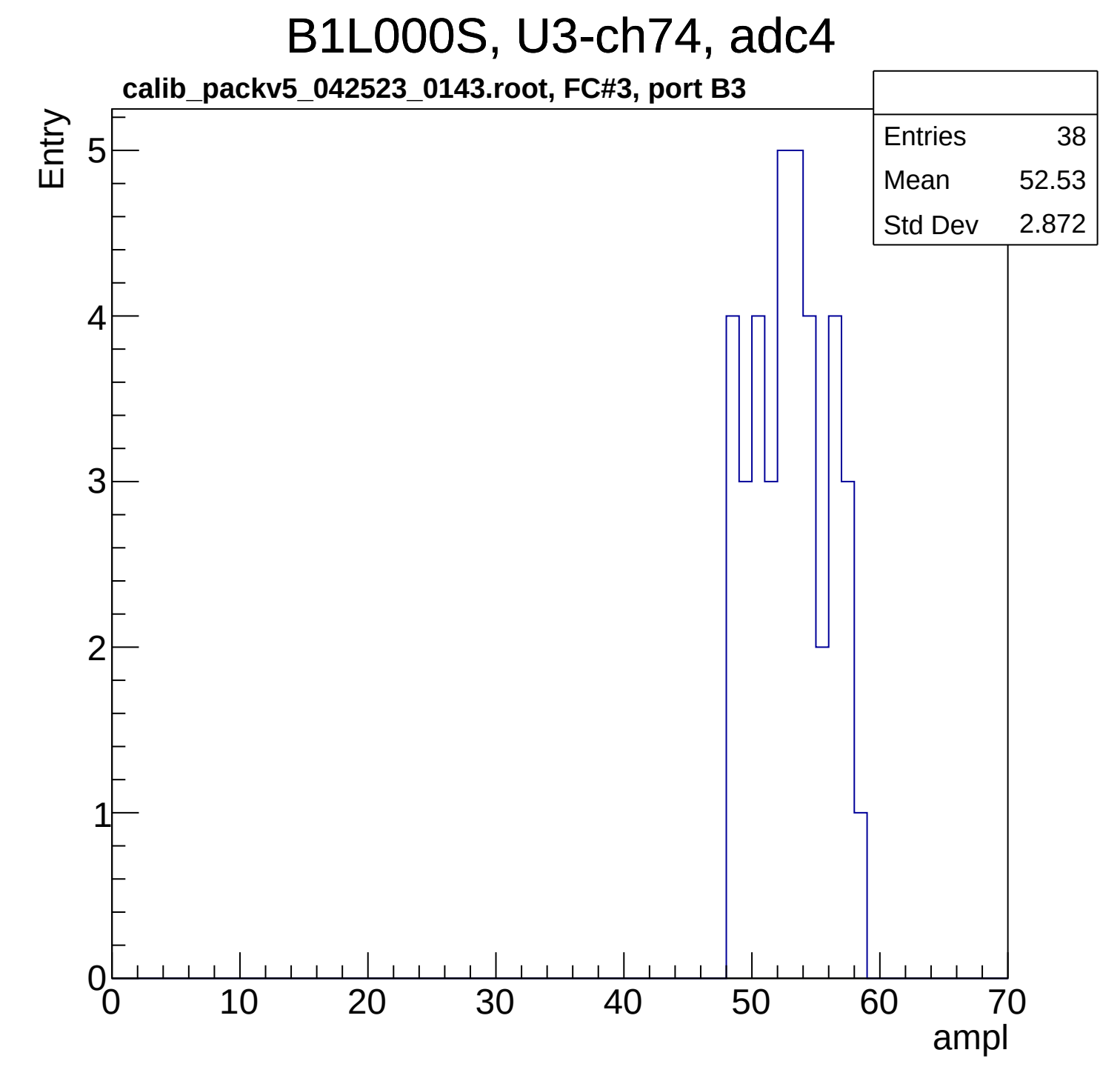
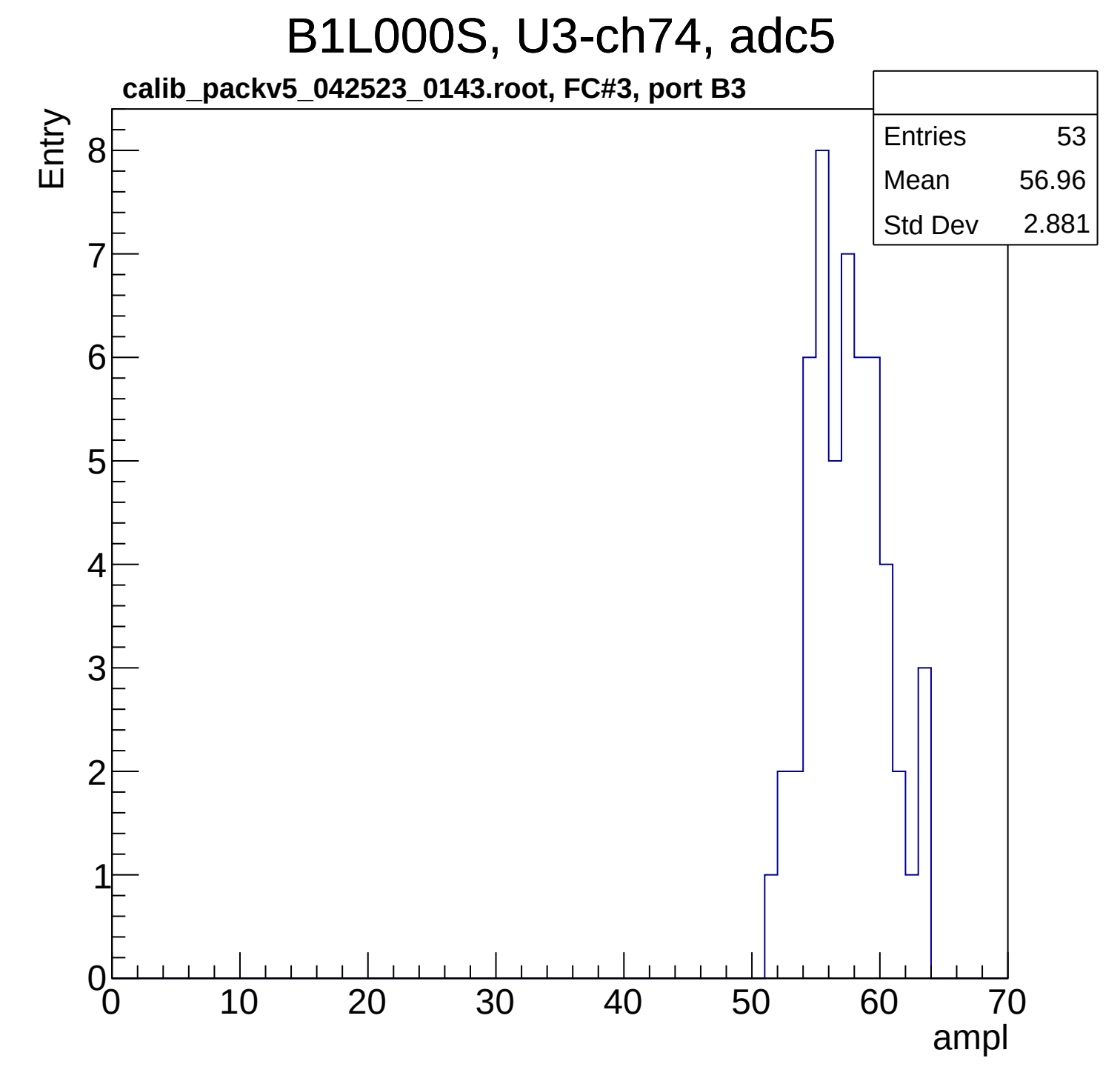

B1L000S, U3-ch74, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 38
Mean 52.53
Std Dev 2.872
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch74, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 56.96
Std Dev 2.881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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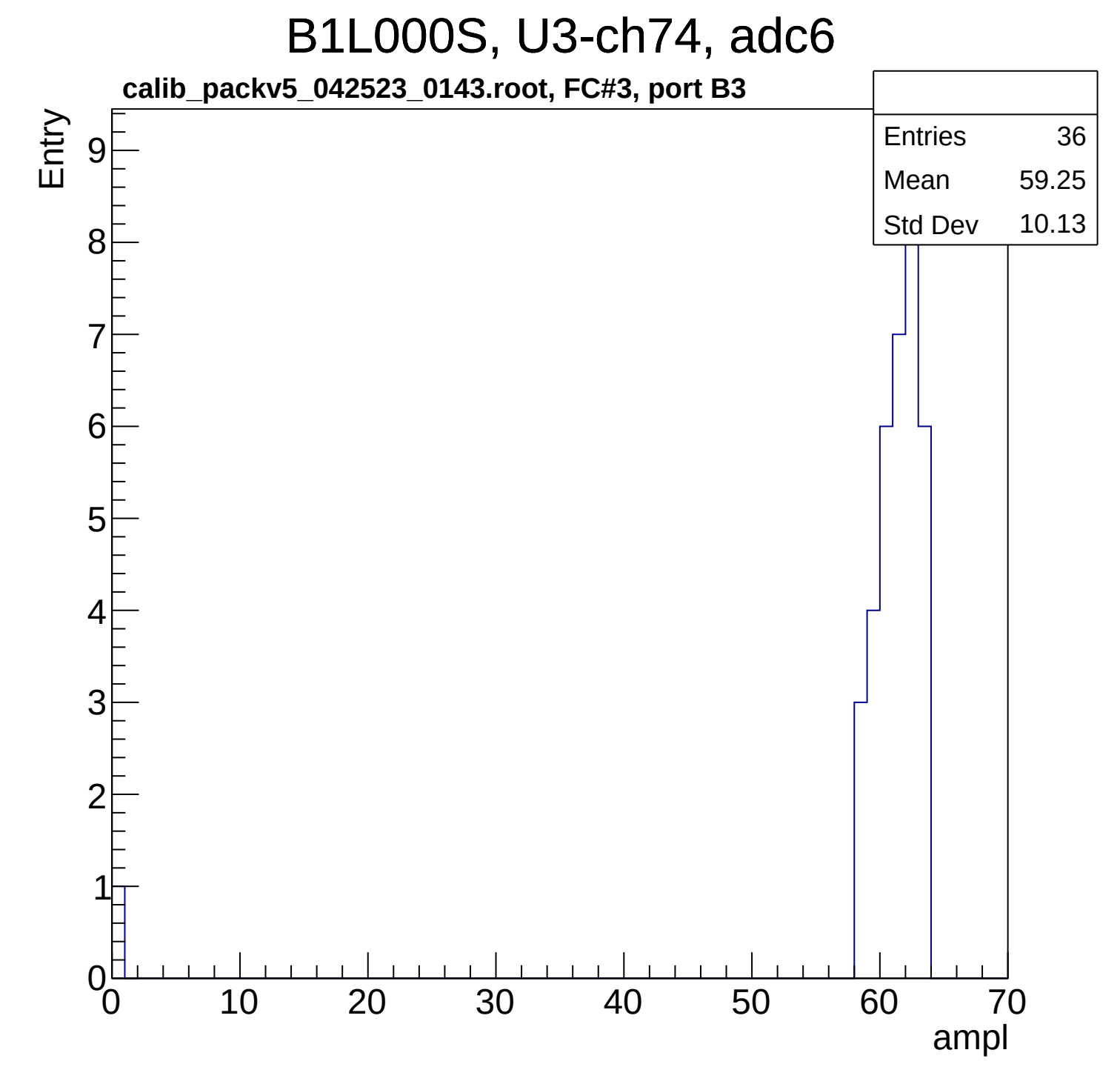
B1L000S, U3-ch74, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 36
Mean 59.25
Std Dev 10.13
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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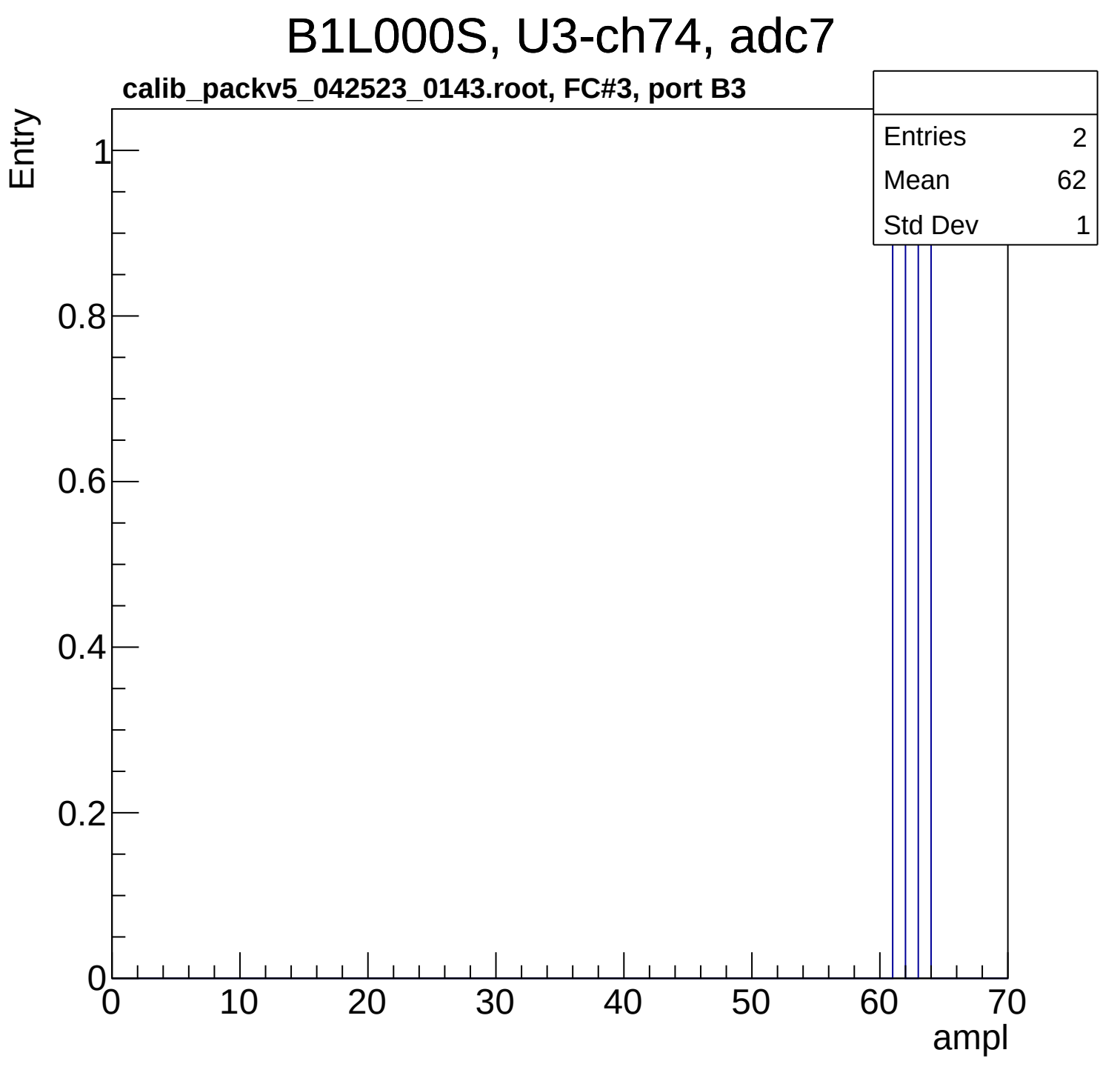
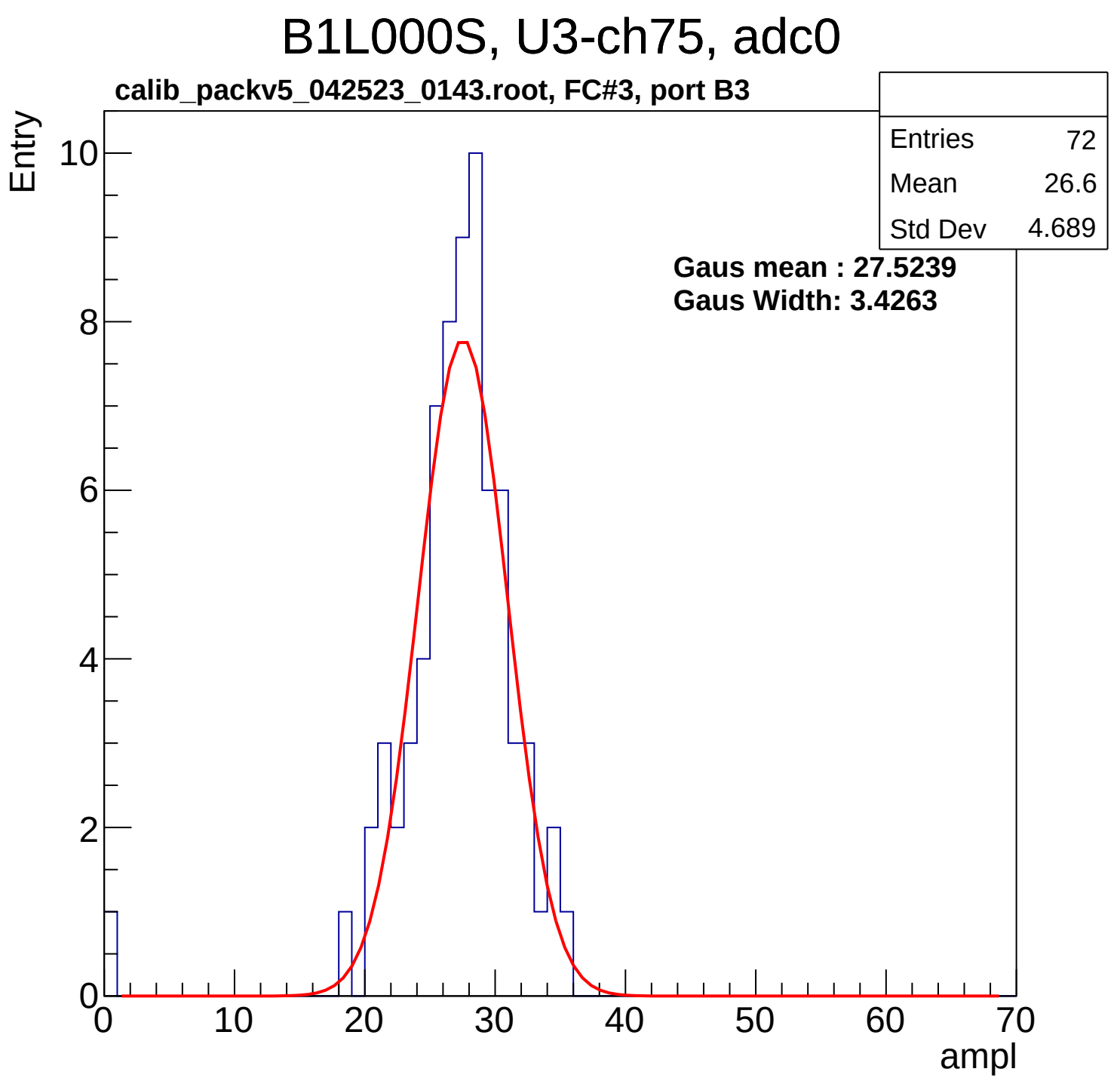
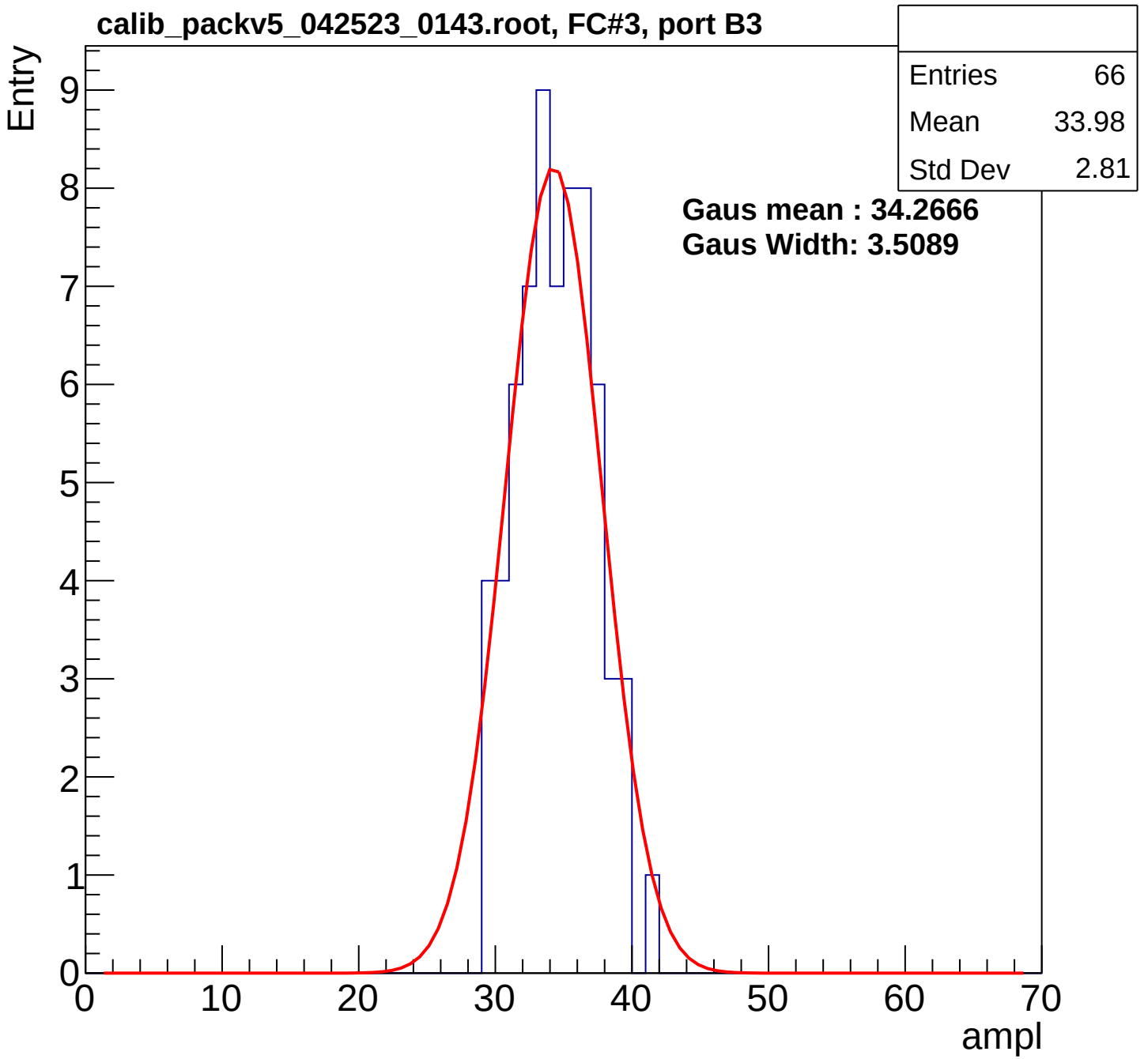
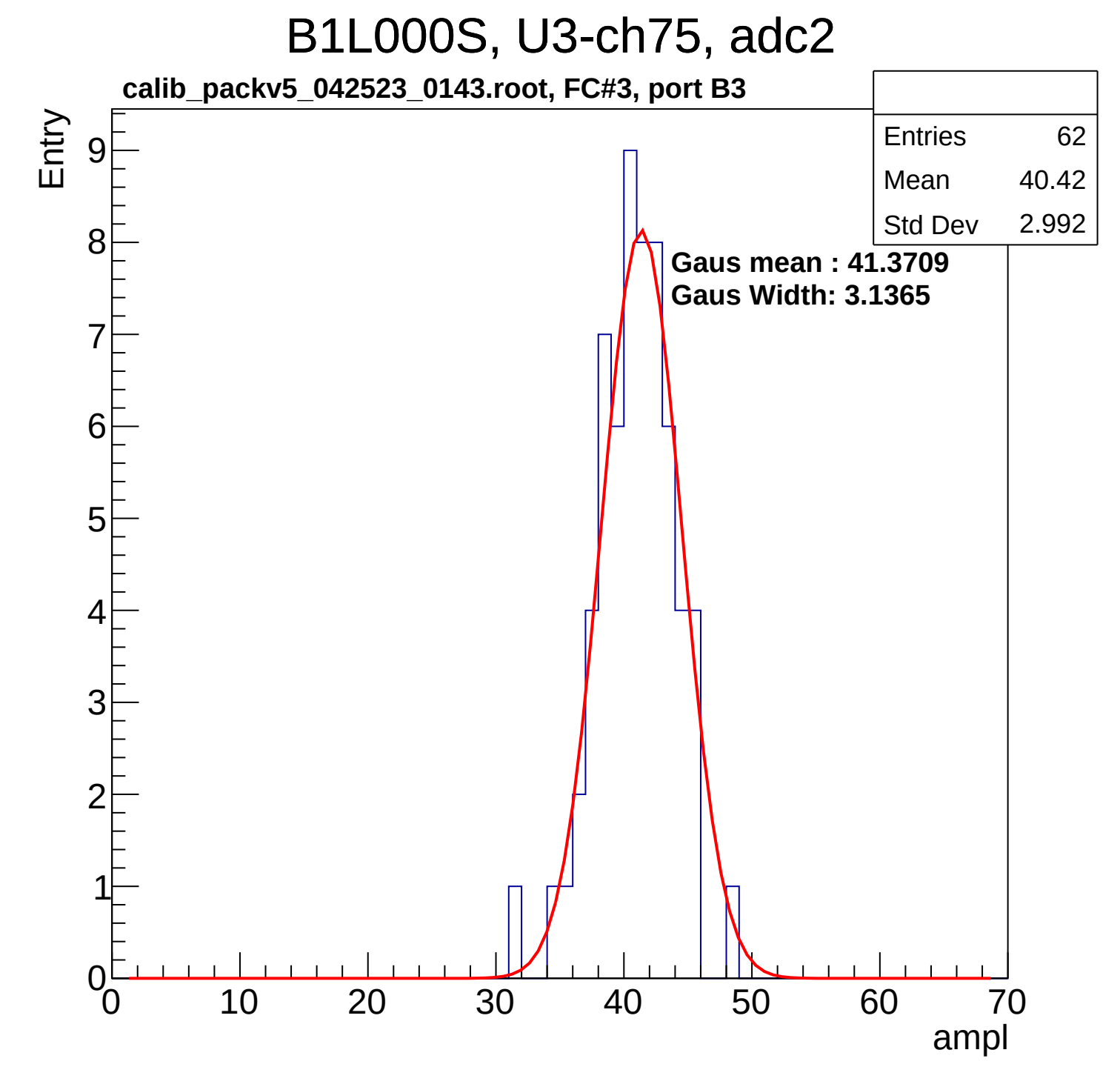
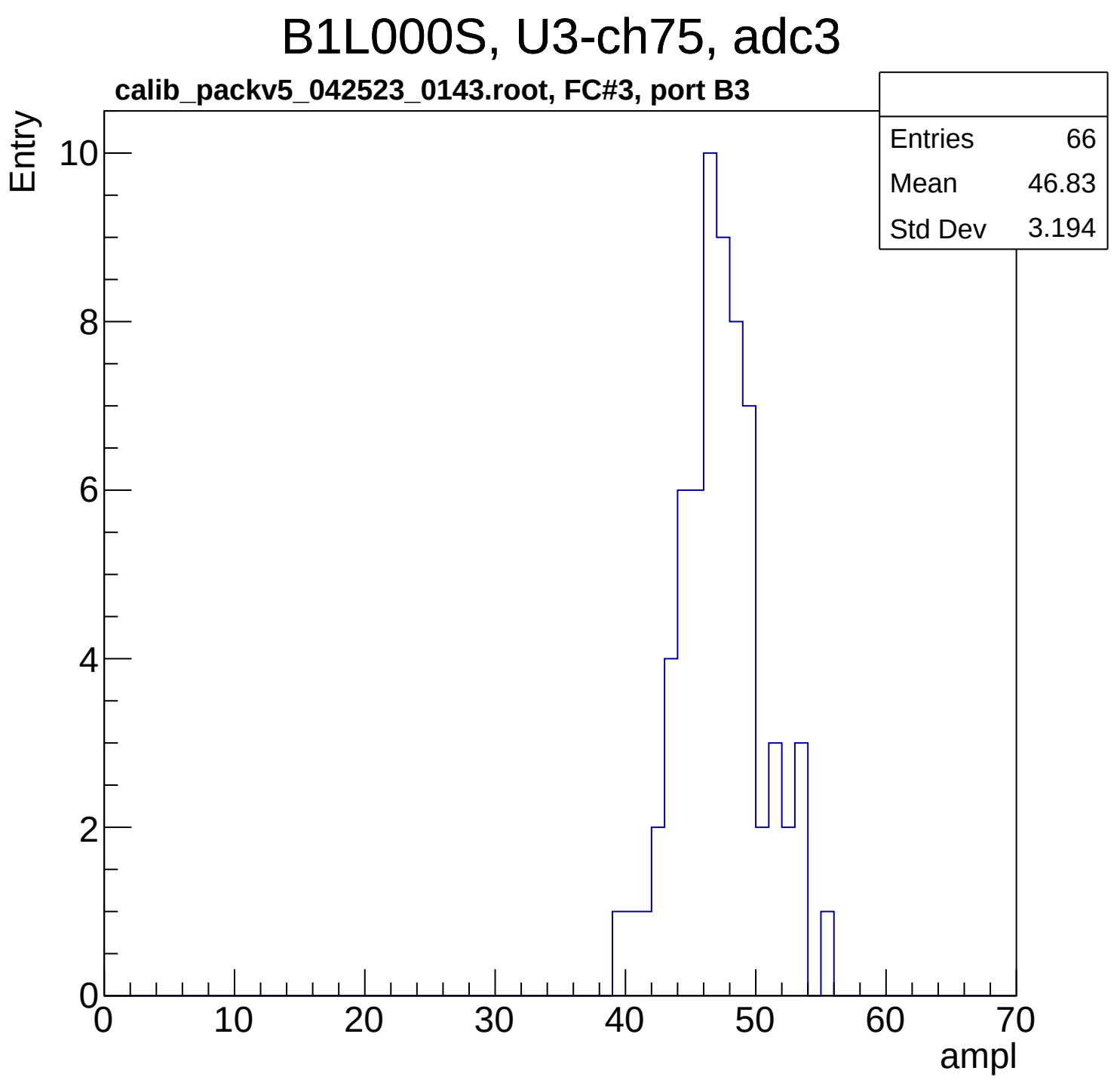
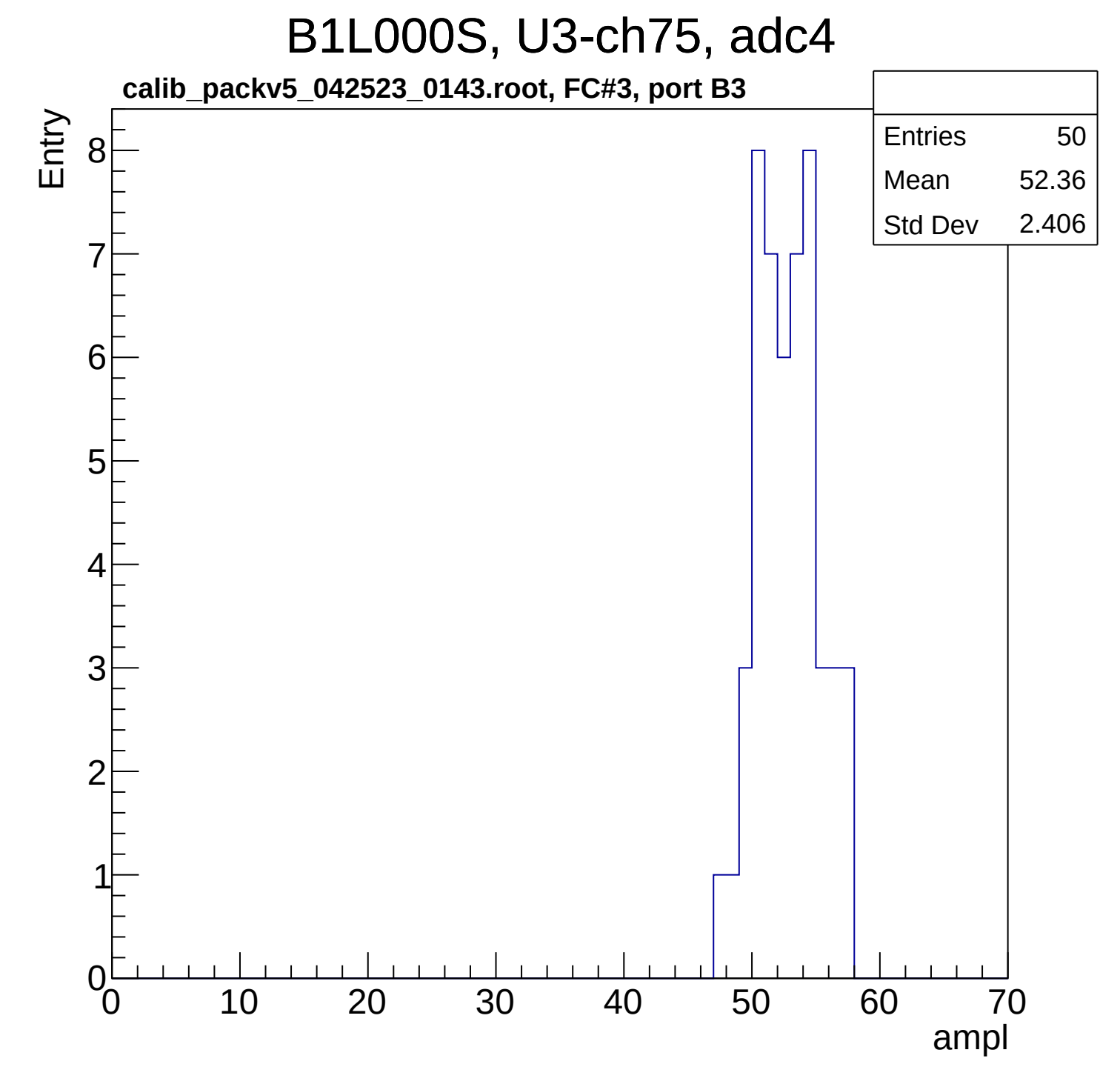

B1L000S, U3-ch74, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

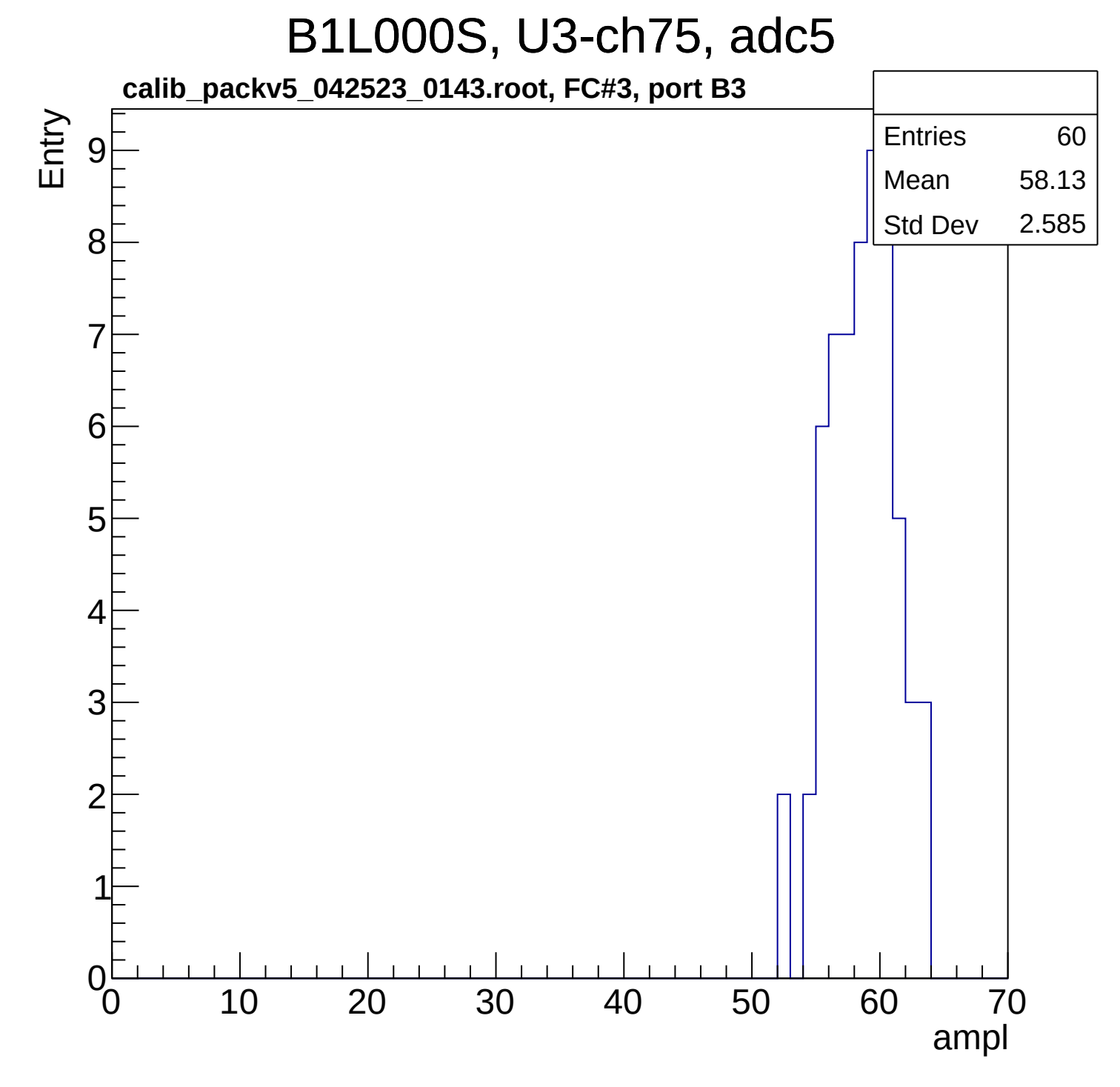

B1L000S, U3-ch75, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 26.6
Std Dev 4.689
Gaus mean : 27.5239
Gaus Width: 3.4263
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch75, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 33.98
Std Dev 2.81
Gaus mean : 34.2666
Gaus Width: 3.5089
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch75, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 40.42
Std Dev 2.992
Gaus mean : 41.3709
Gaus Width: 3.1365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch75, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 46.83
Std Dev 3.194
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch75, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 52.36
Std Dev 2.406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch75, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.13
Std Dev 2.585
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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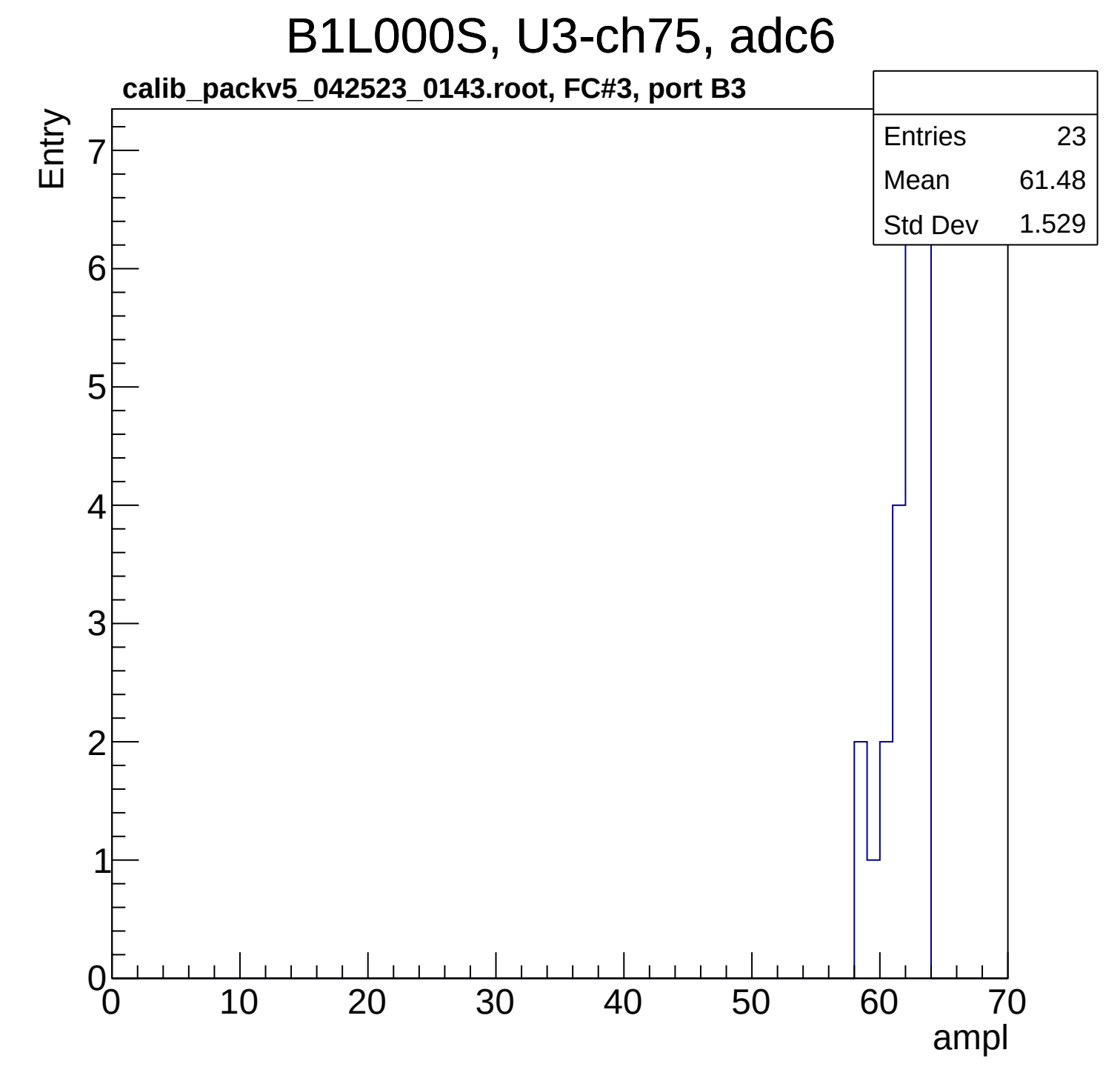
40
50
60
70


B1L000S, U3-ch75, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 61.48
Std Dev 1.529
Entry
ampl
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2
3
4
5
6
7
0
10
20
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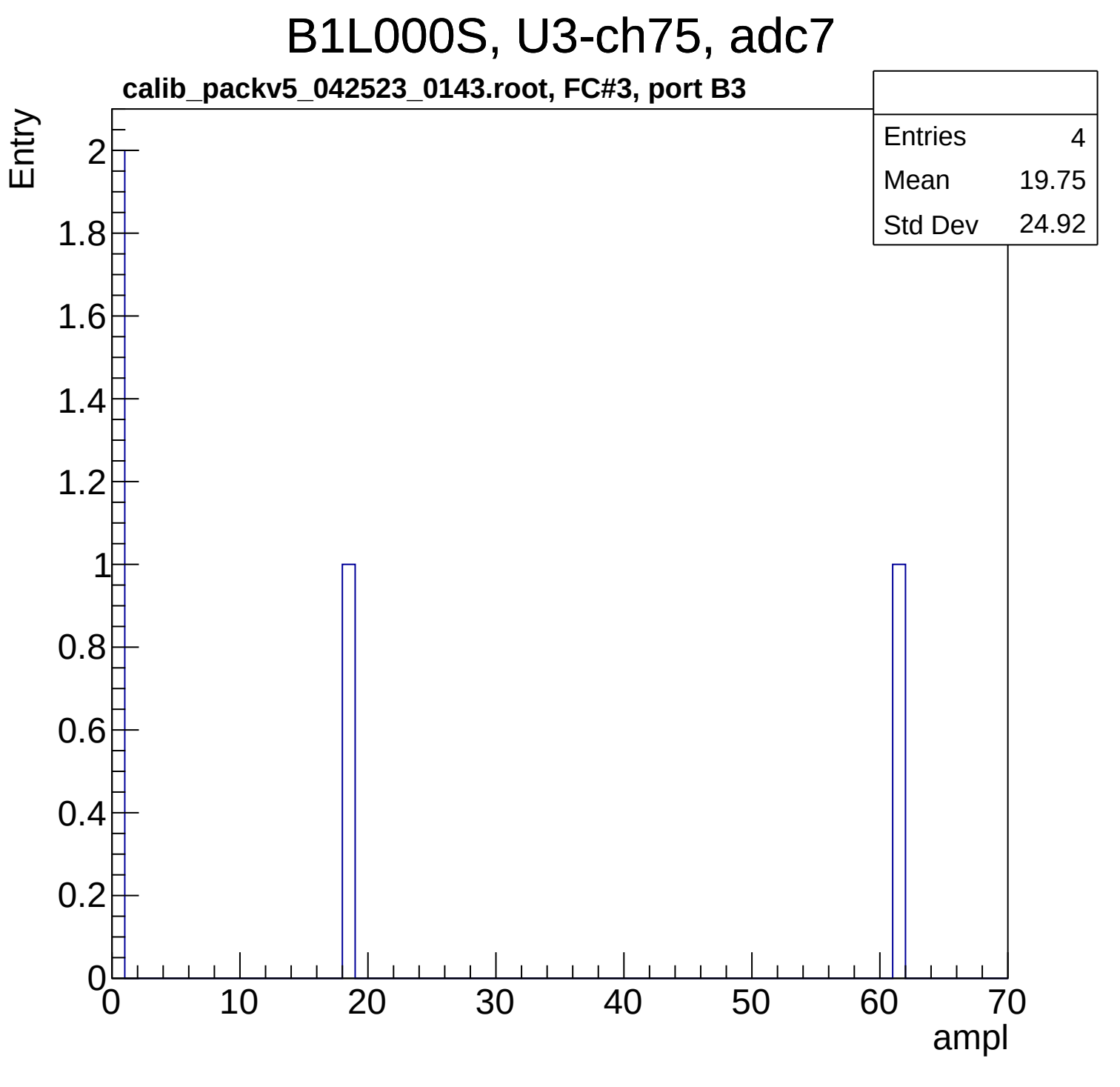
40
50
60
70


B1L000S, U3-ch75, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 19.75
Std Dev 24.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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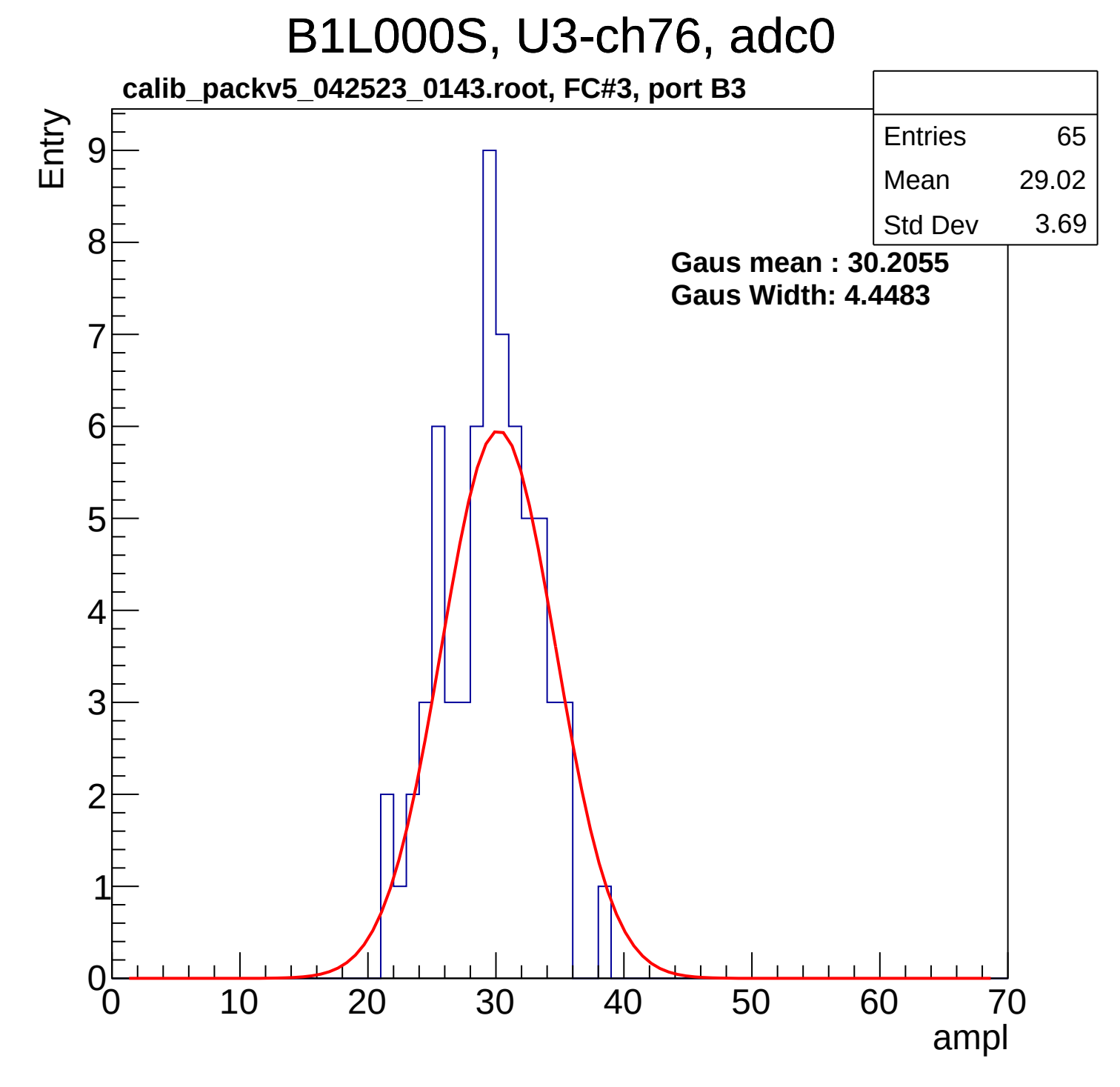
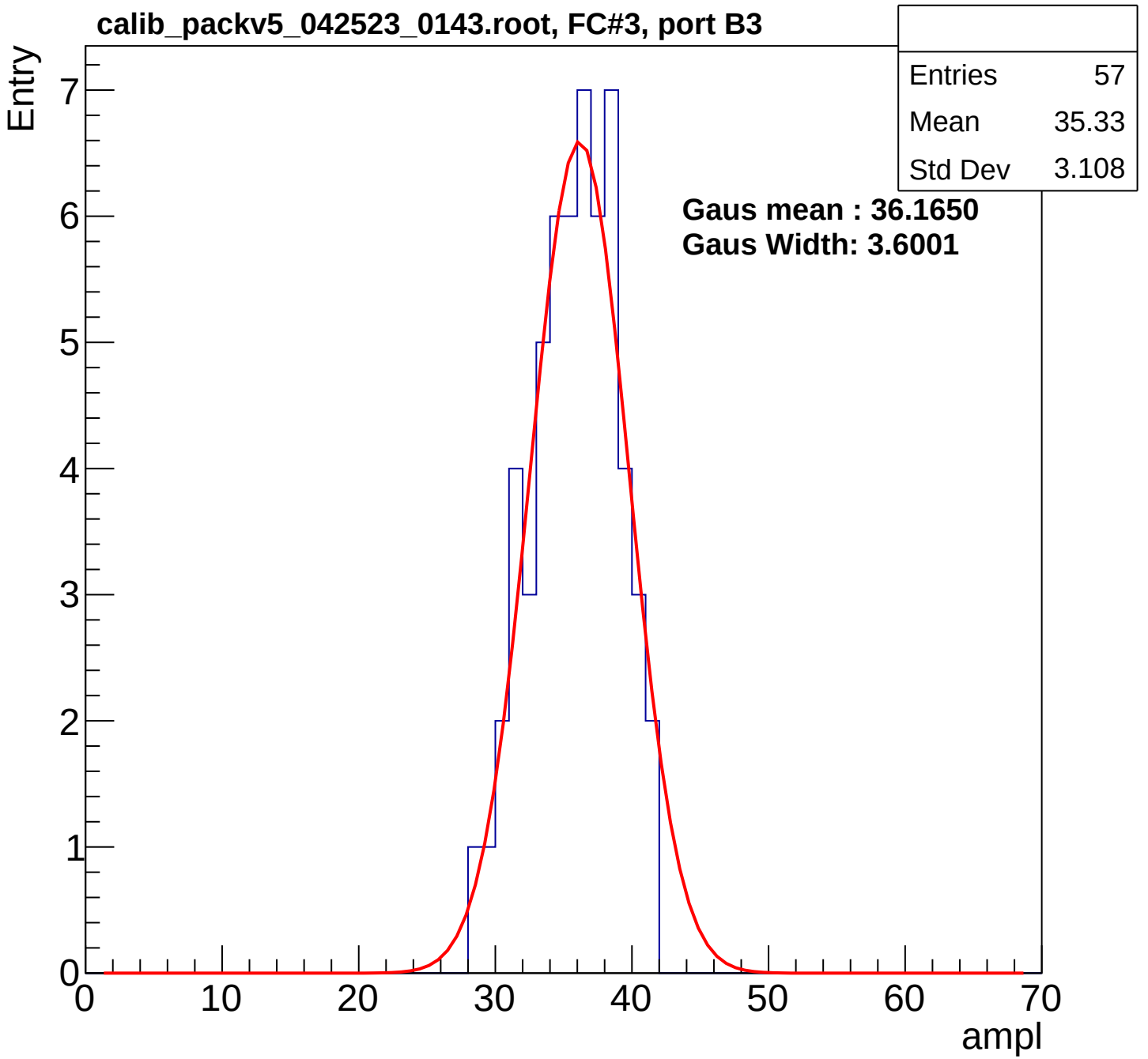
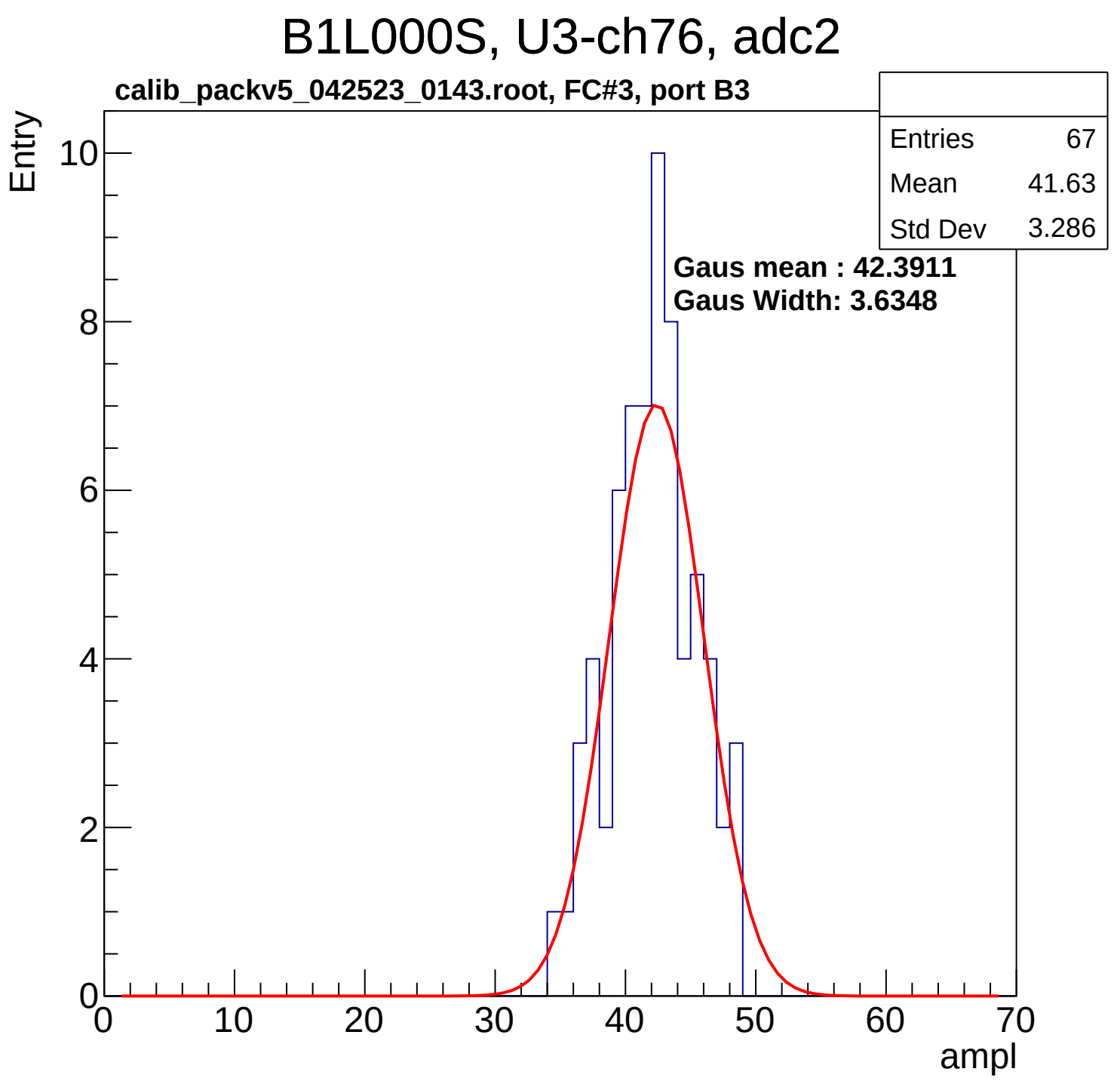
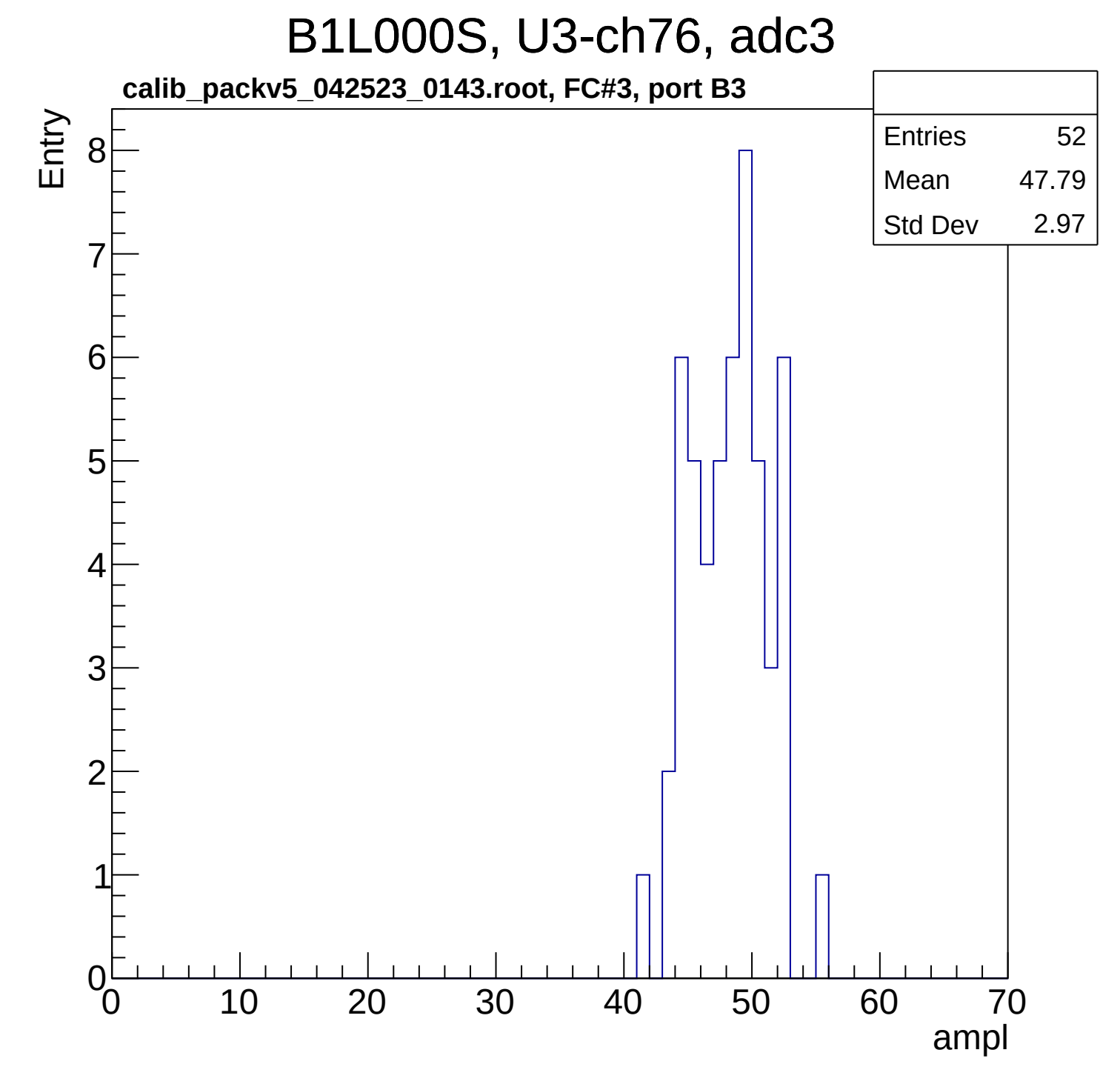

B1L000S, U3-ch76, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 29.02
Std Dev 3.69
Gaus mean : 30.2055
Gaus Width: 4.4483
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch76, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 35.33
Std Dev 3.108
Gaus mean : 36.1650
Gaus Width: 3.6001
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch76, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.63
Std Dev 3.286
Gaus mean : 42.3911
Gaus Width: 3.6348
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch76, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 47.79
Std Dev 2.97
Entry
ampl
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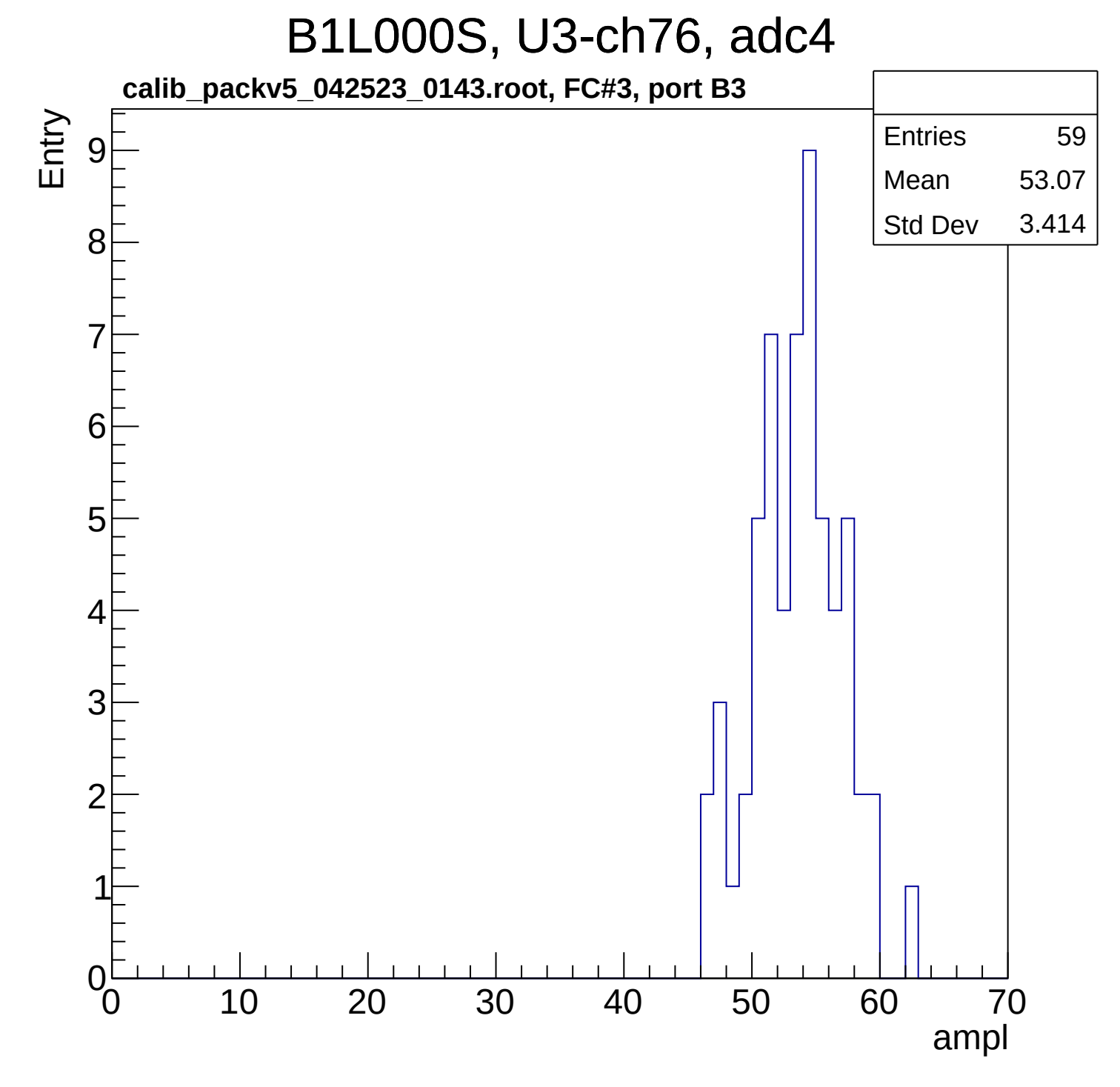
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch76, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 53.07
Std Dev 3.414
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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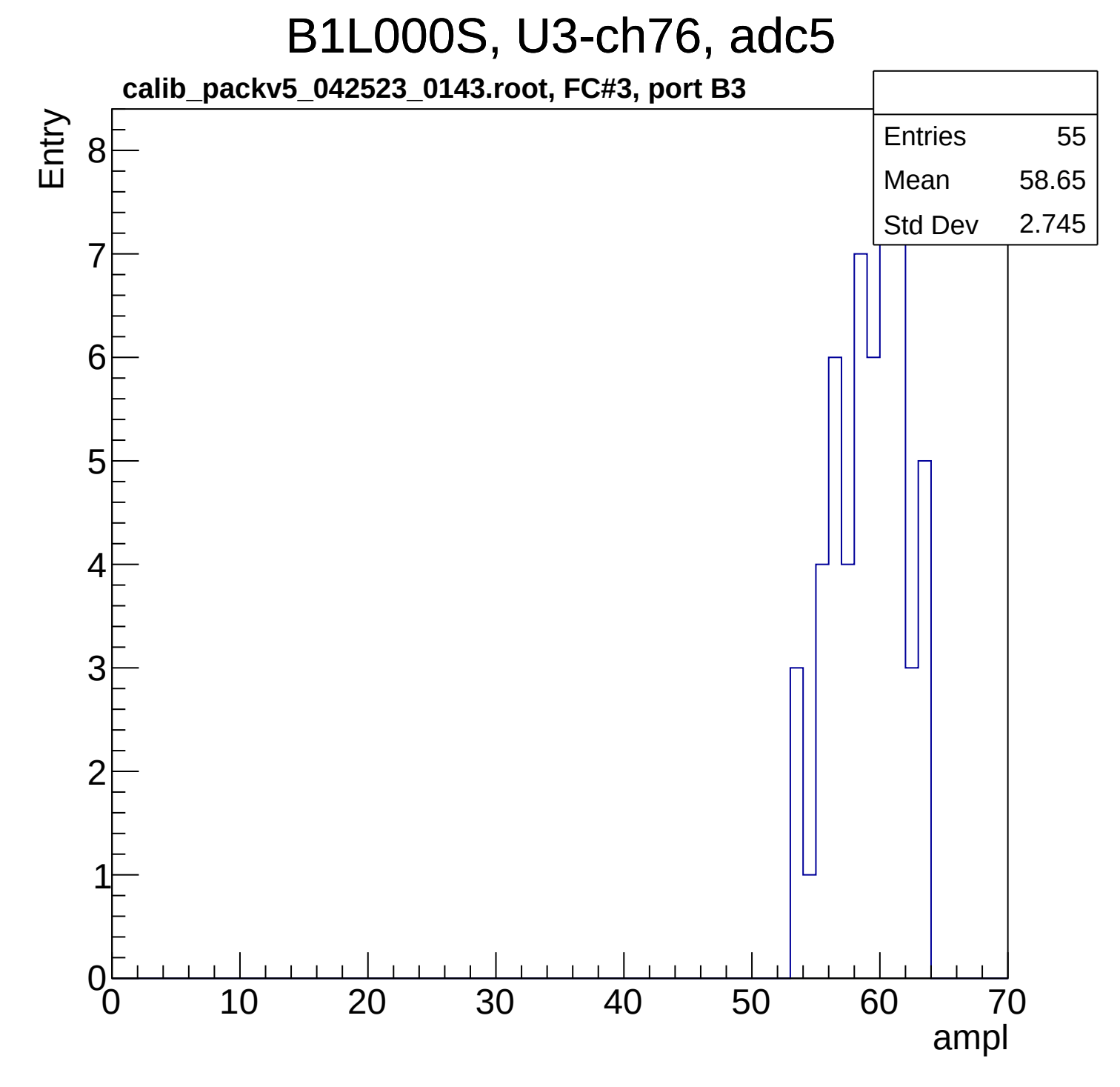
B1L000S, U3-ch76, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.65
Std Dev 2.745
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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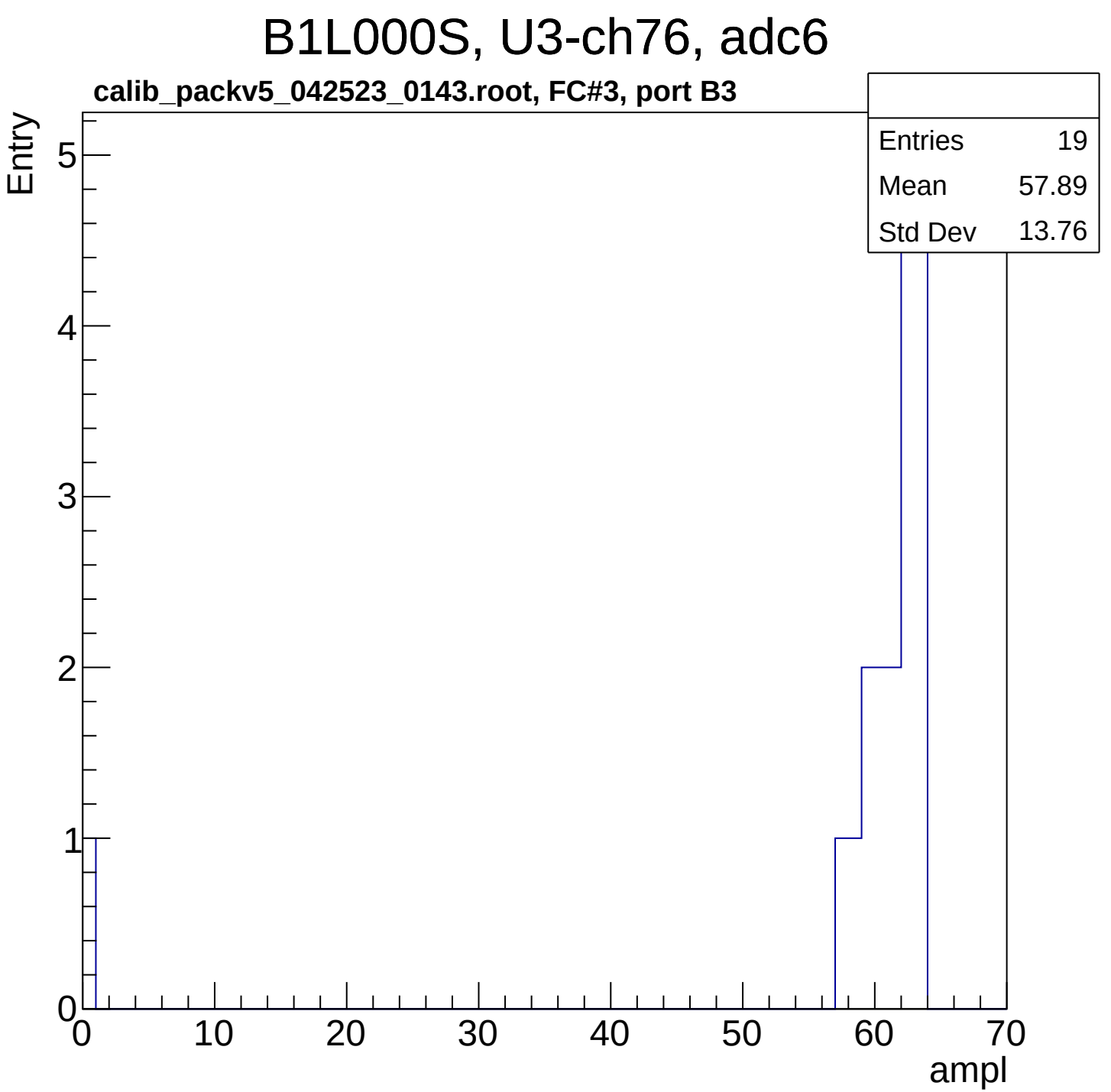
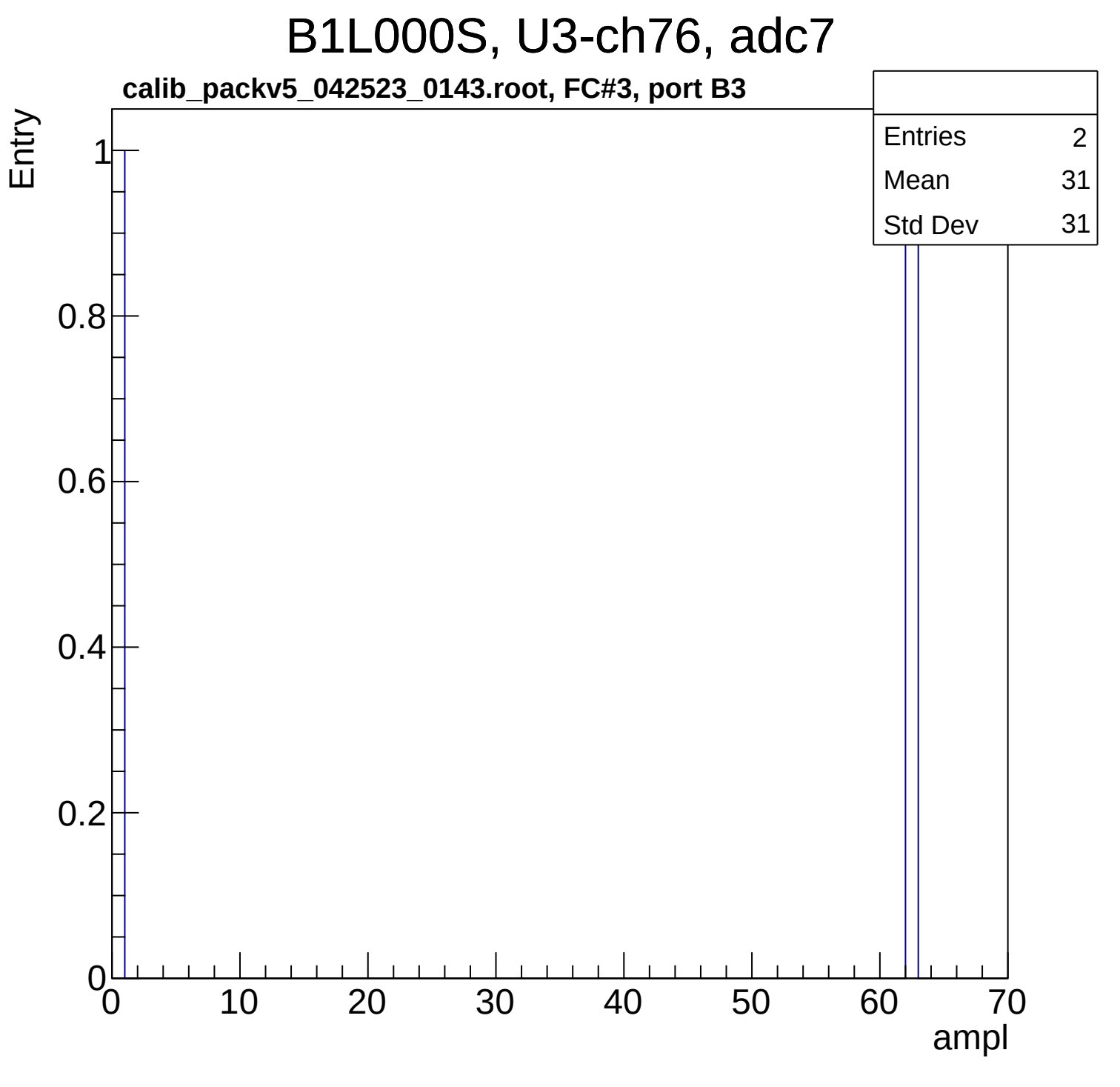
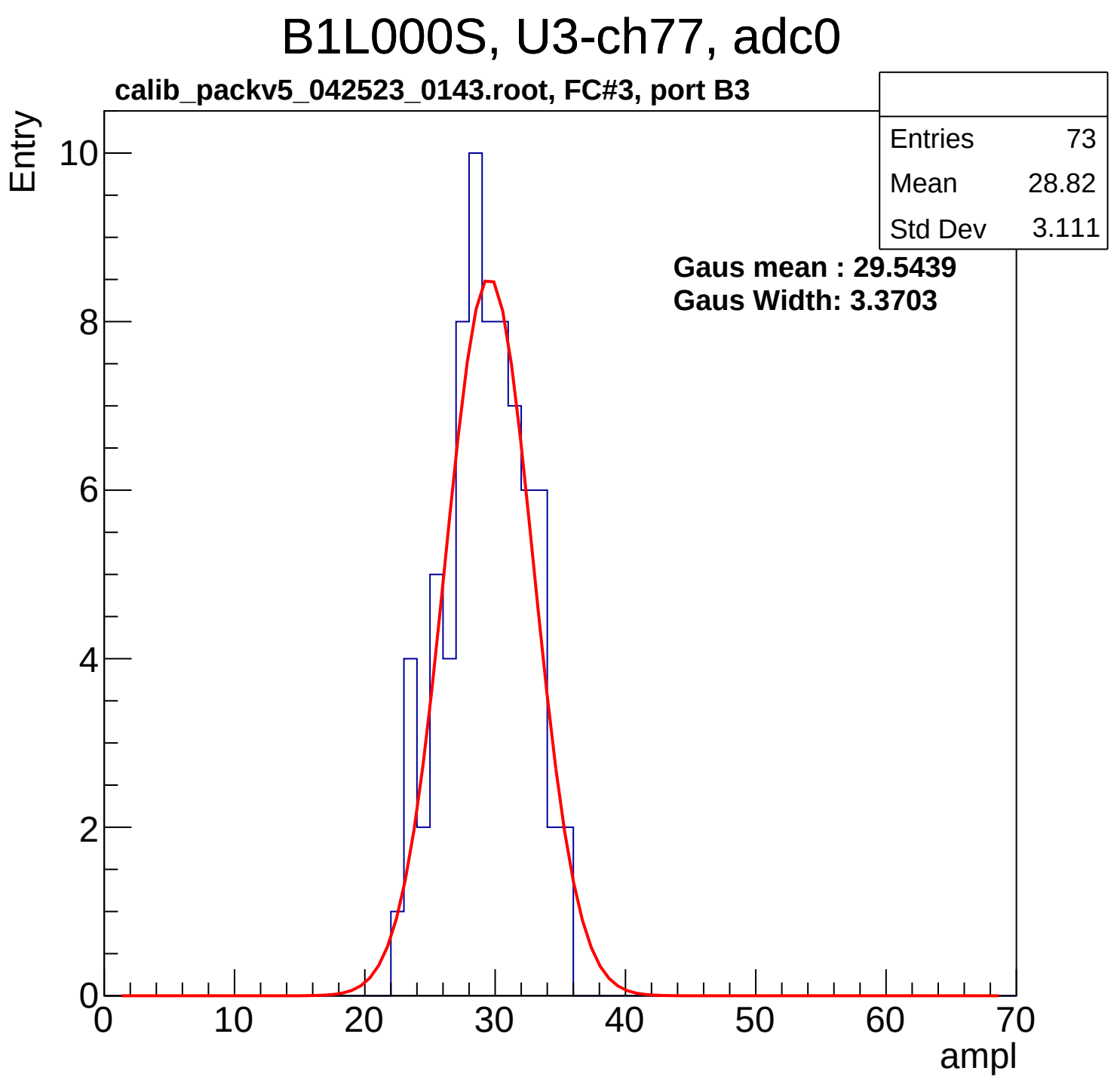
40
50
60
70


B1L000S, U3-ch76, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 57.89
Std Dev 13.76
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch76, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch77, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 28.82
Std Dev 3.111
Gaus mean : 29.5439
Gaus Width: 3.3703
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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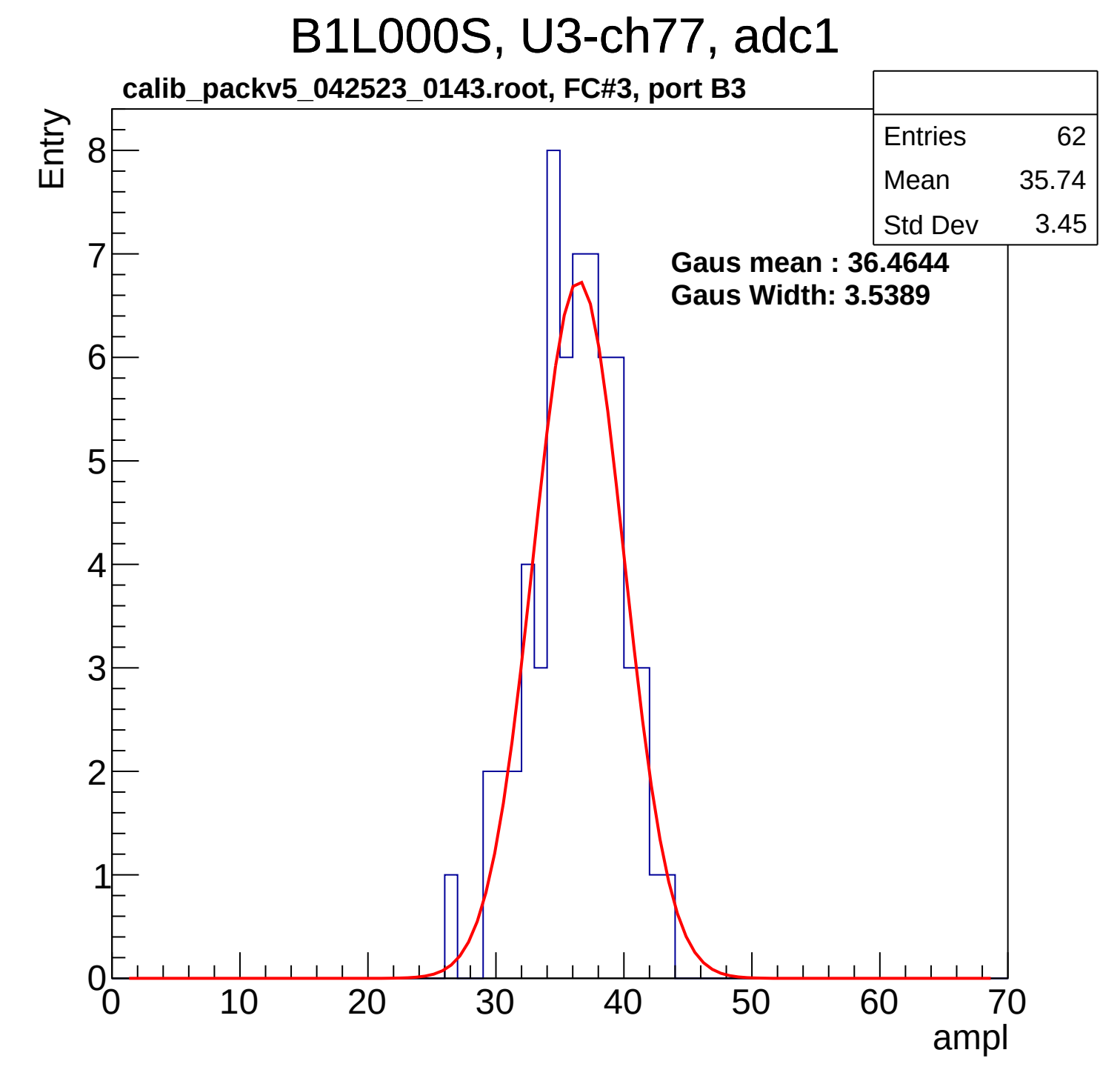
B1L000S, U3-ch77, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 35.74
Std Dev 3.45
Gaus mean : 36.4644
Gaus Width: 3.5389
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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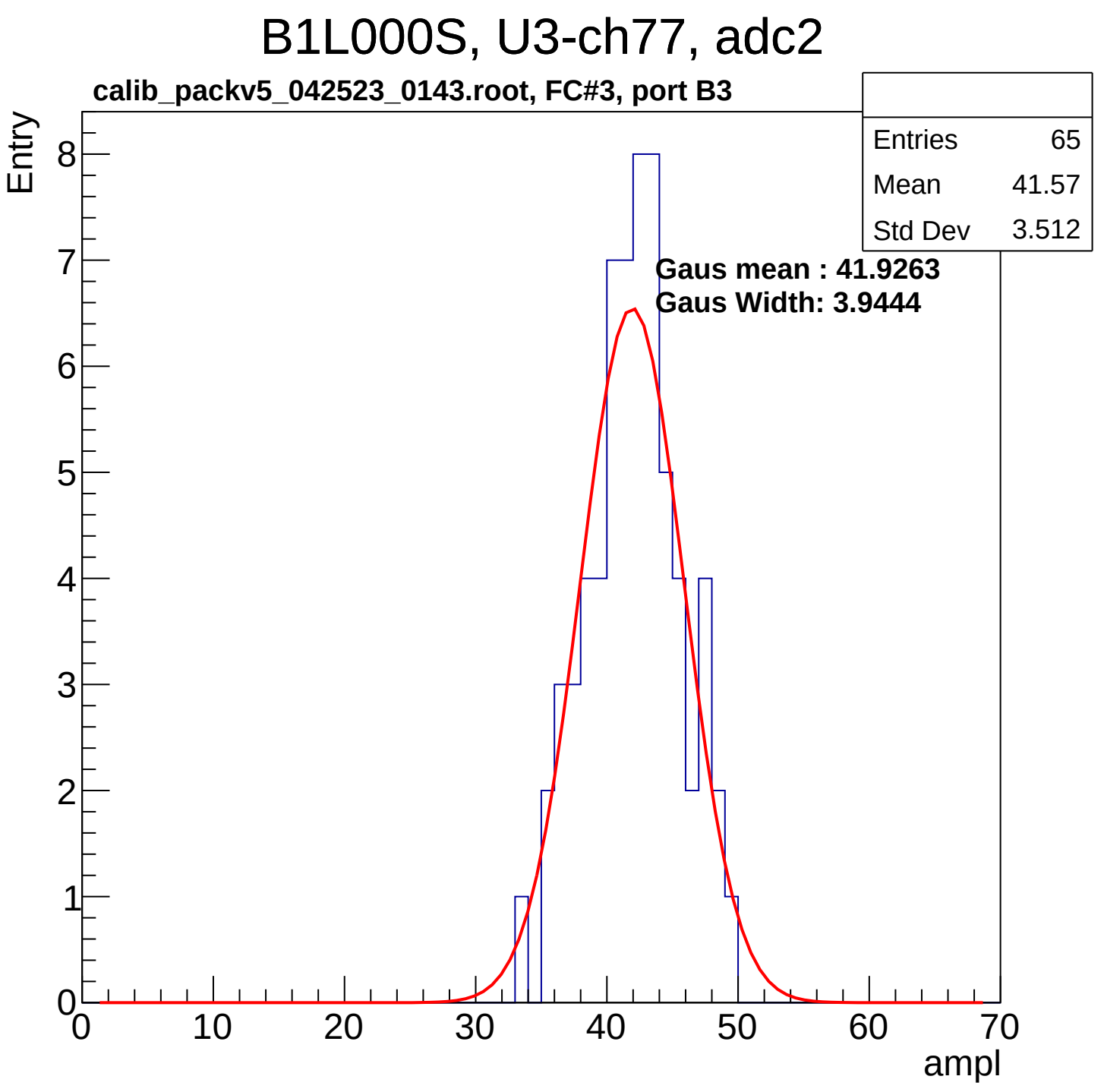
B1L000S, U3-ch77, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 41.57
Std Dev 3.512
Gaus mean : 41.9263
Gaus Width: 3.9444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
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60
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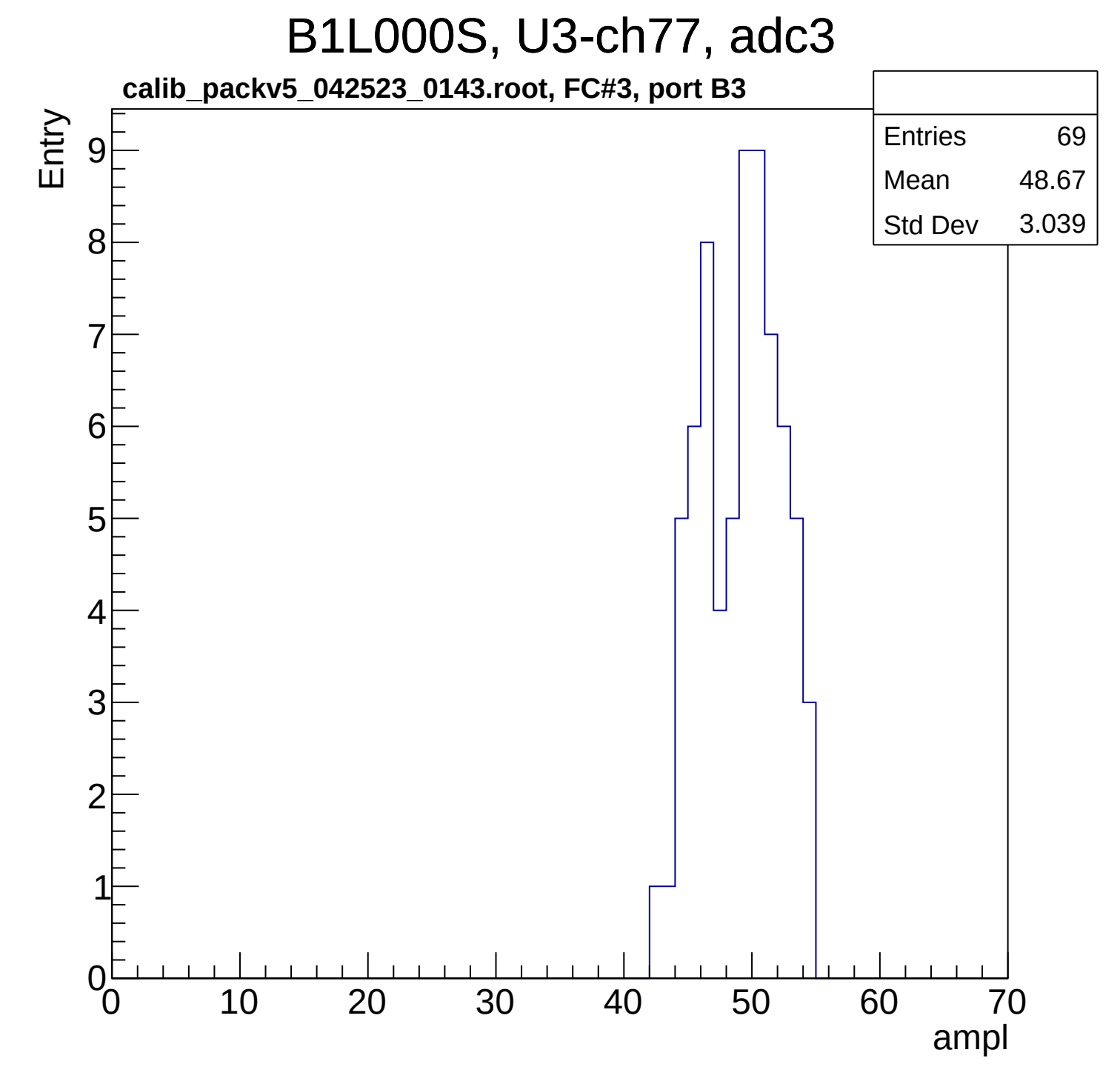
B1L000S, U3-ch77, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 48.67
Std Dev 3.039
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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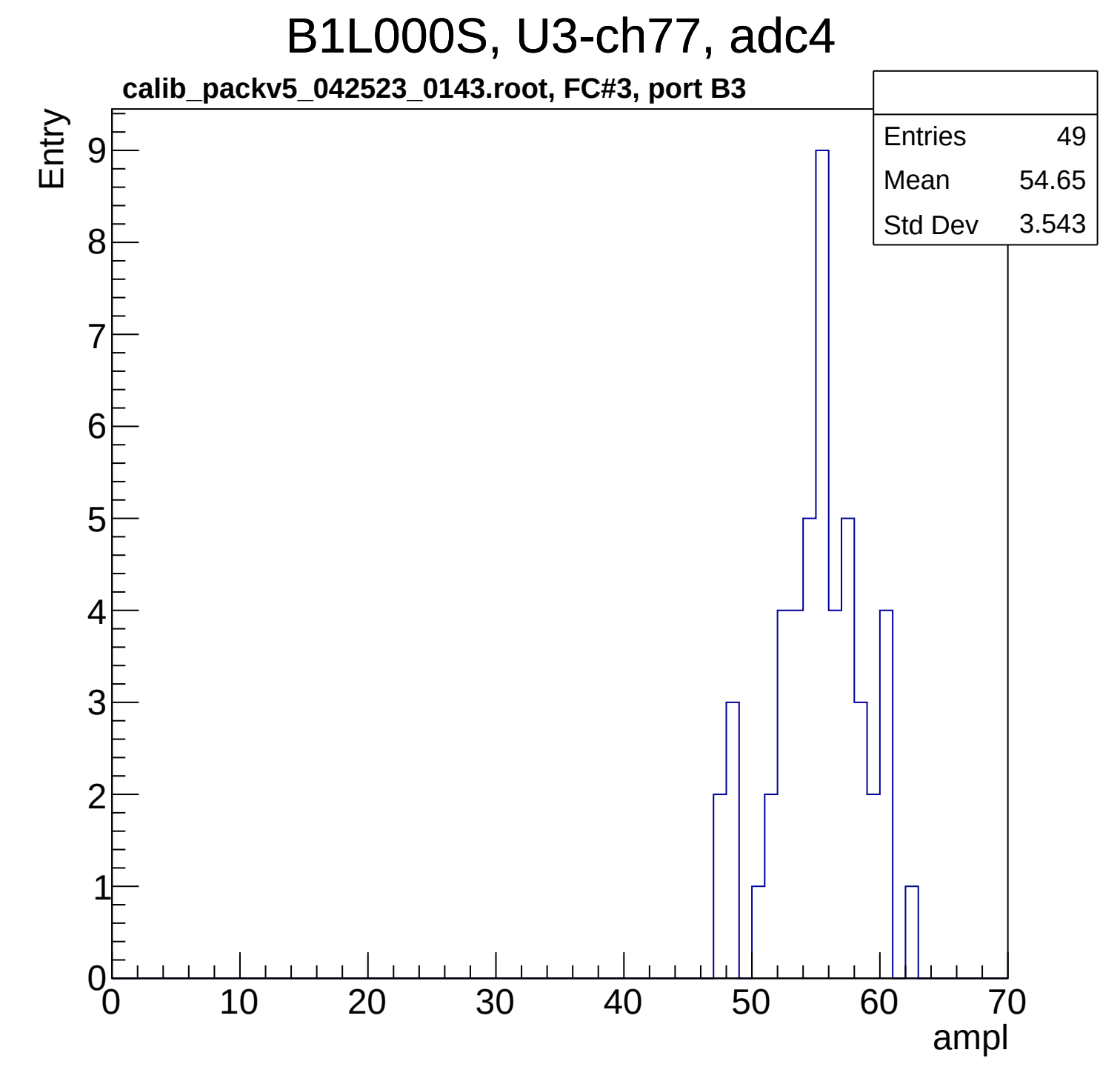
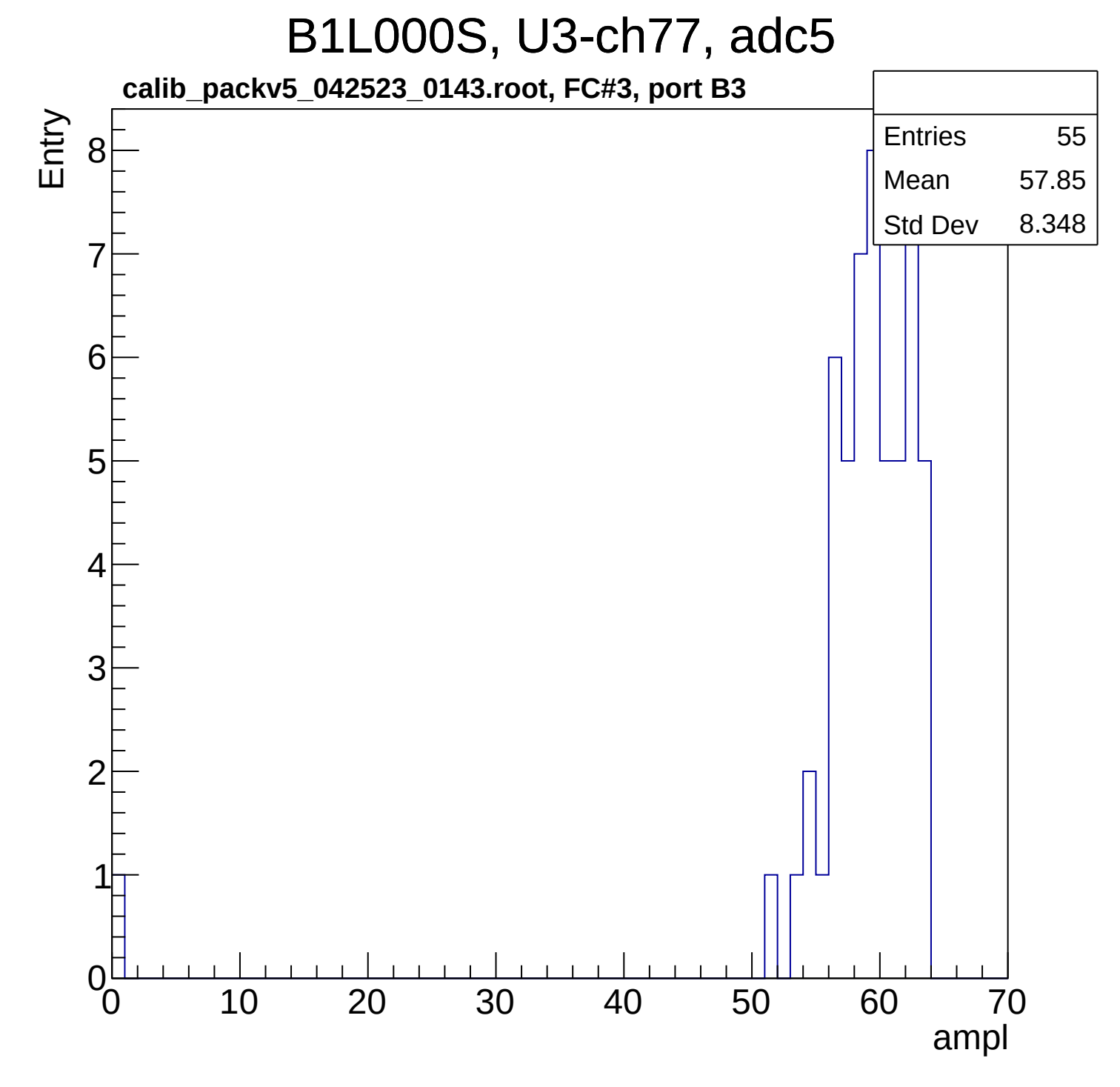
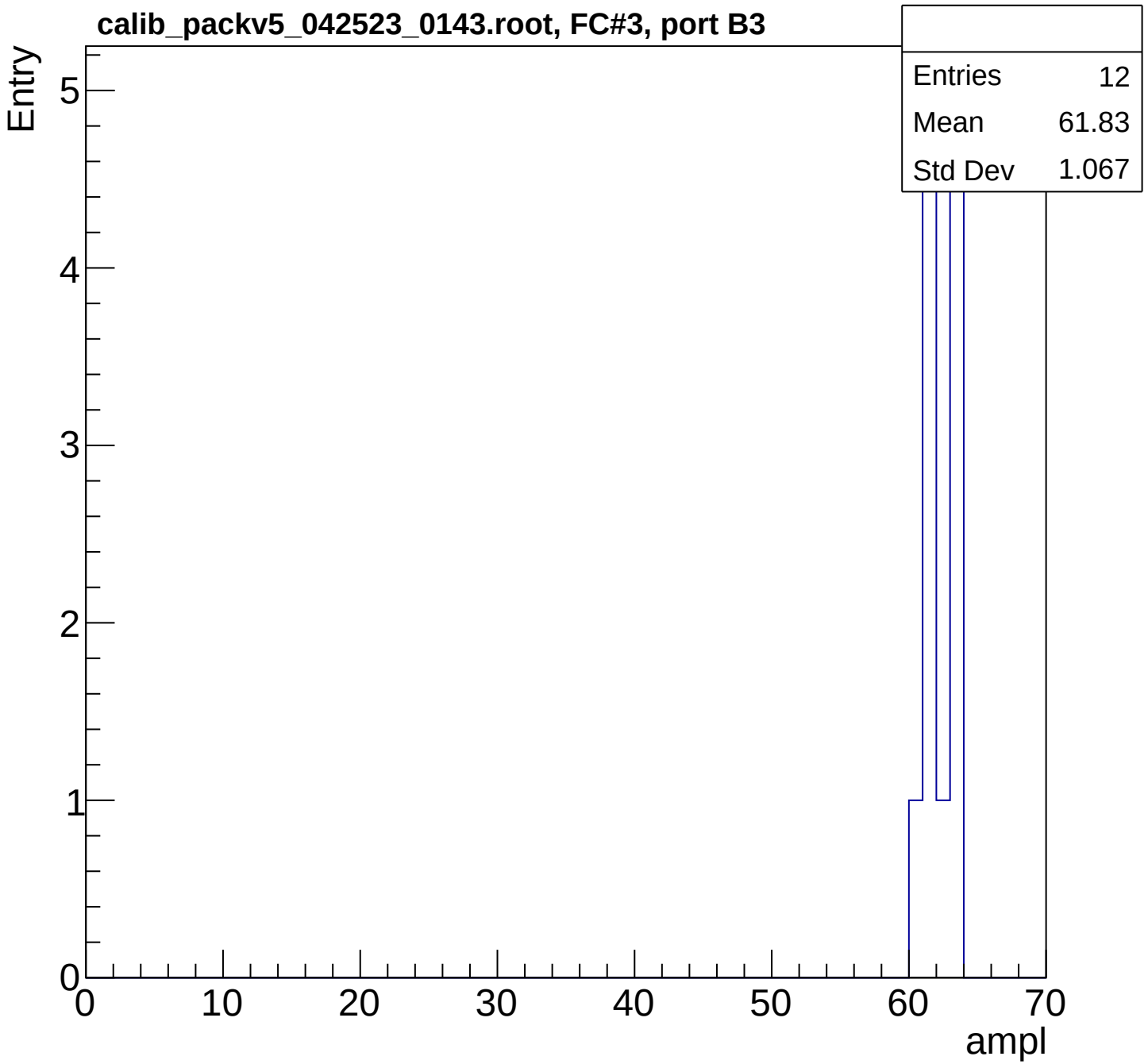
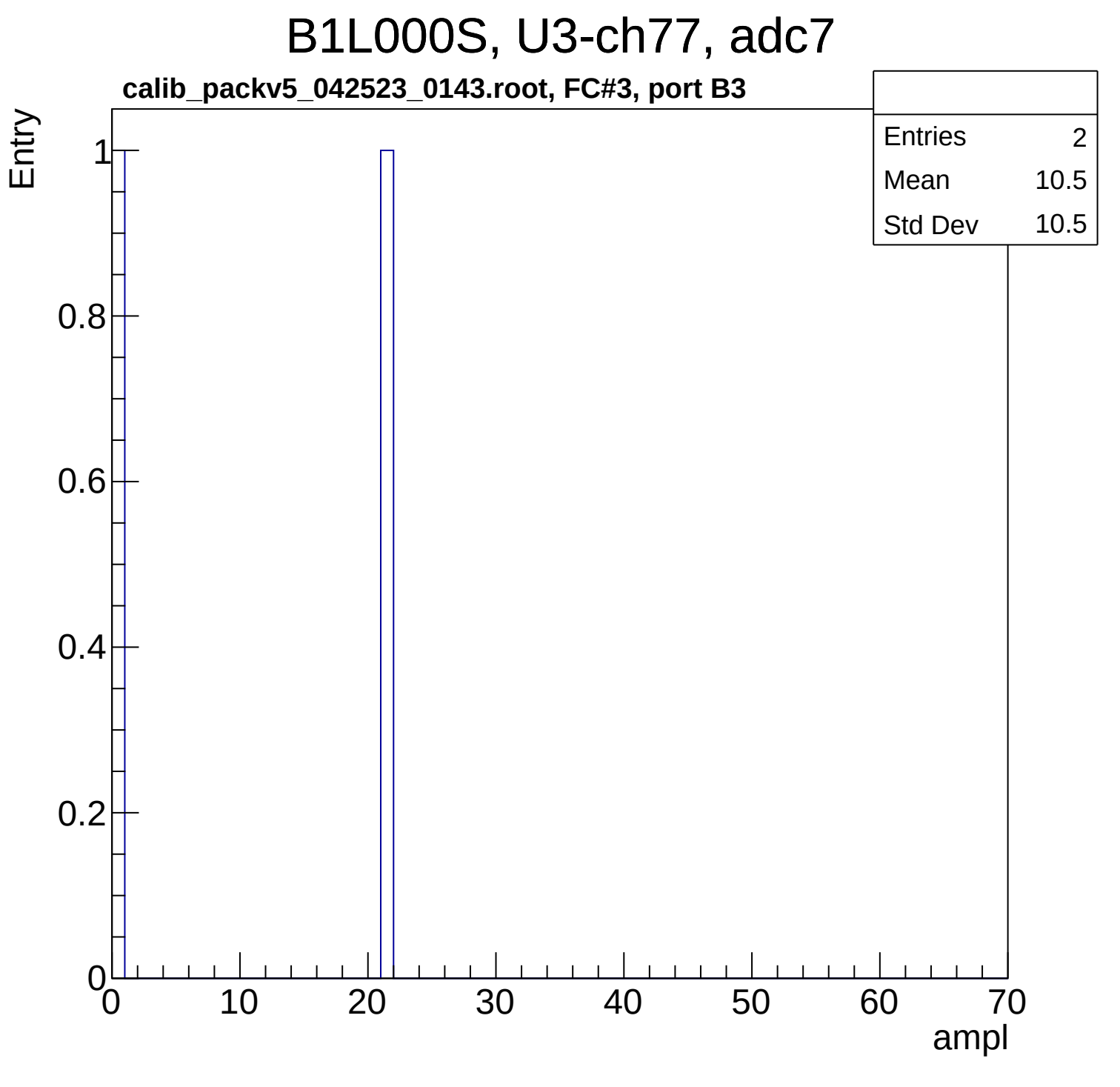
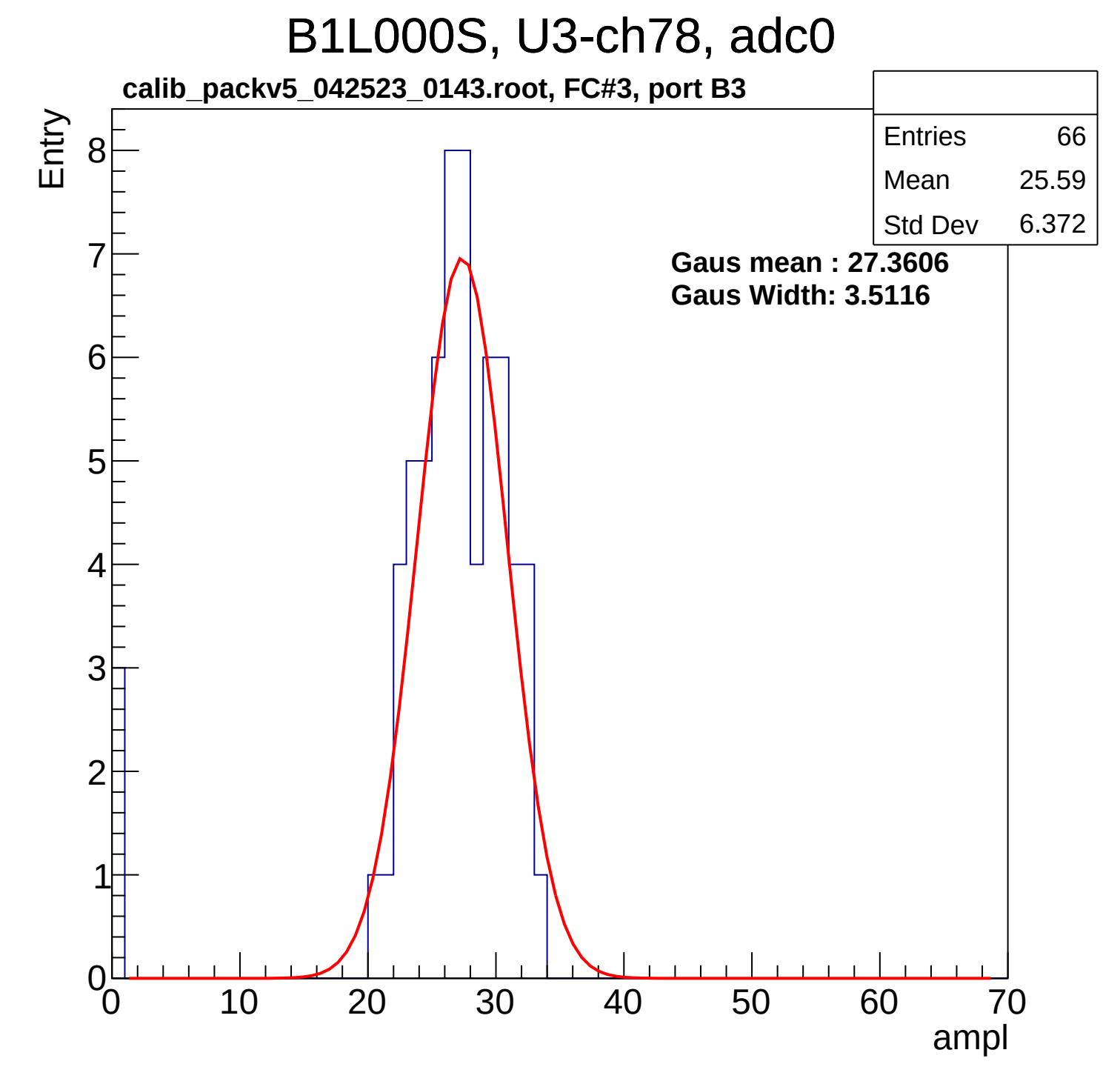
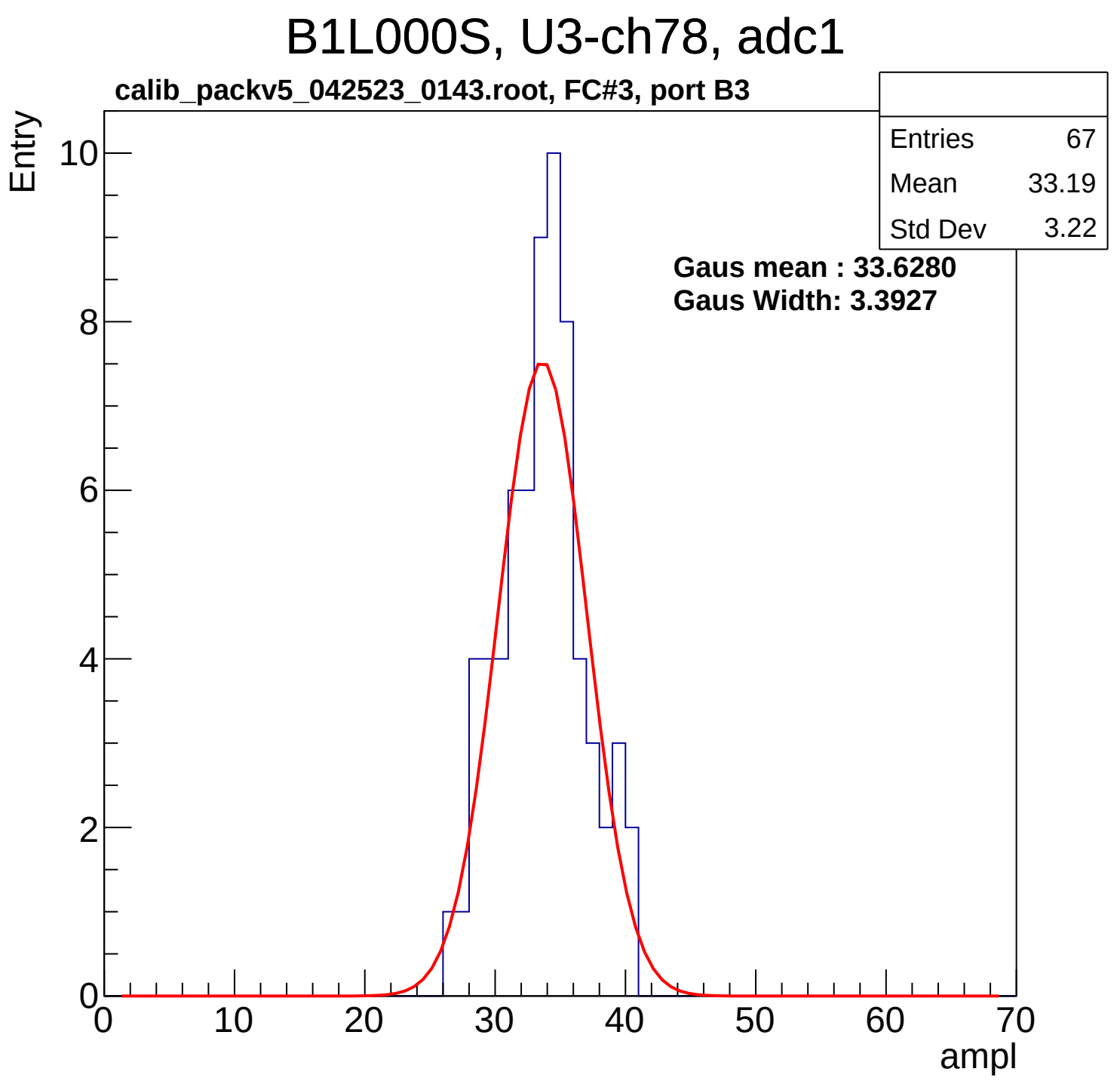

B1L000S, U3-ch77, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 54.65
Std Dev 3.543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

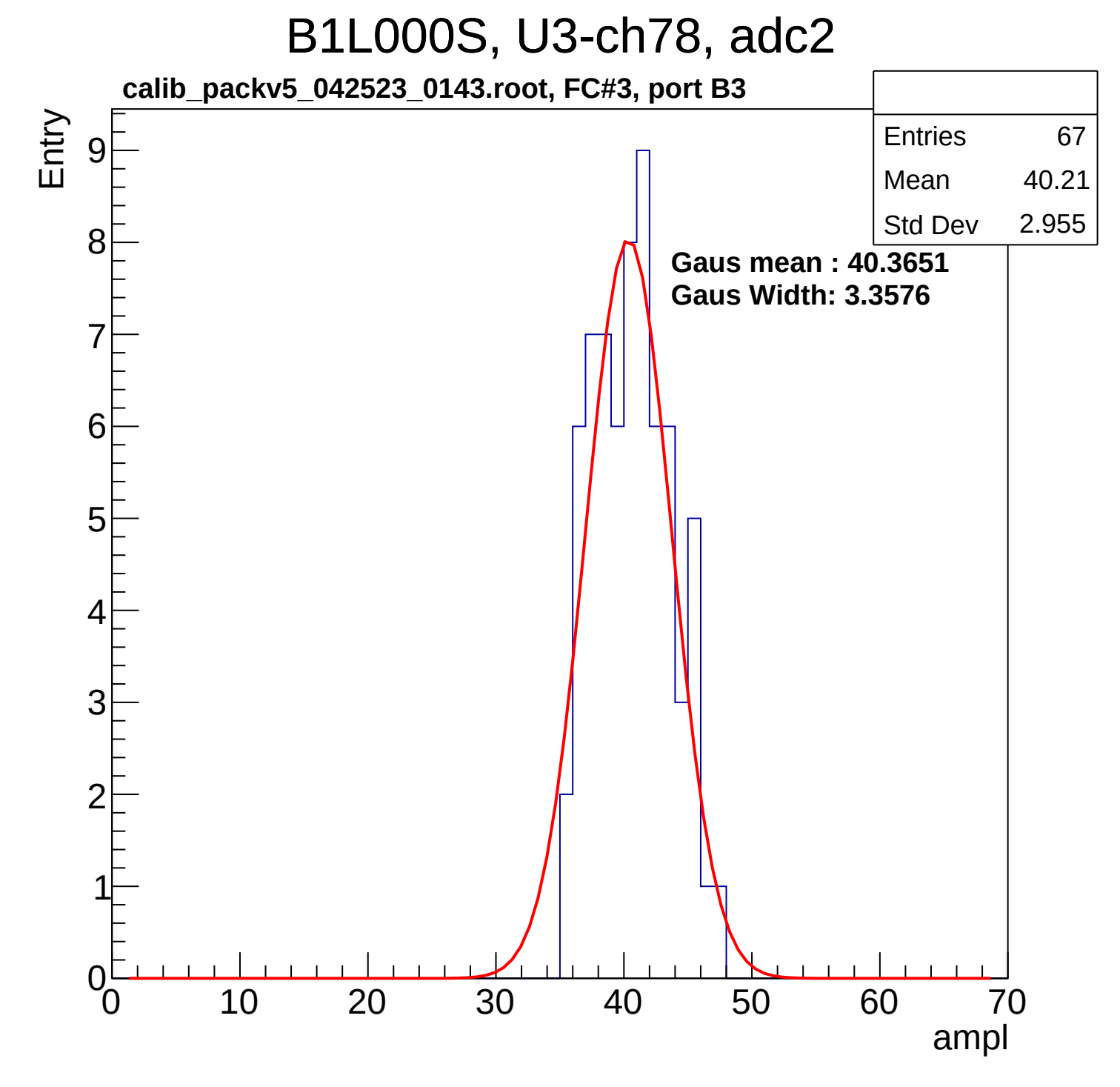

B1L000S, U3-ch77, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 57.85
Std Dev 8.348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

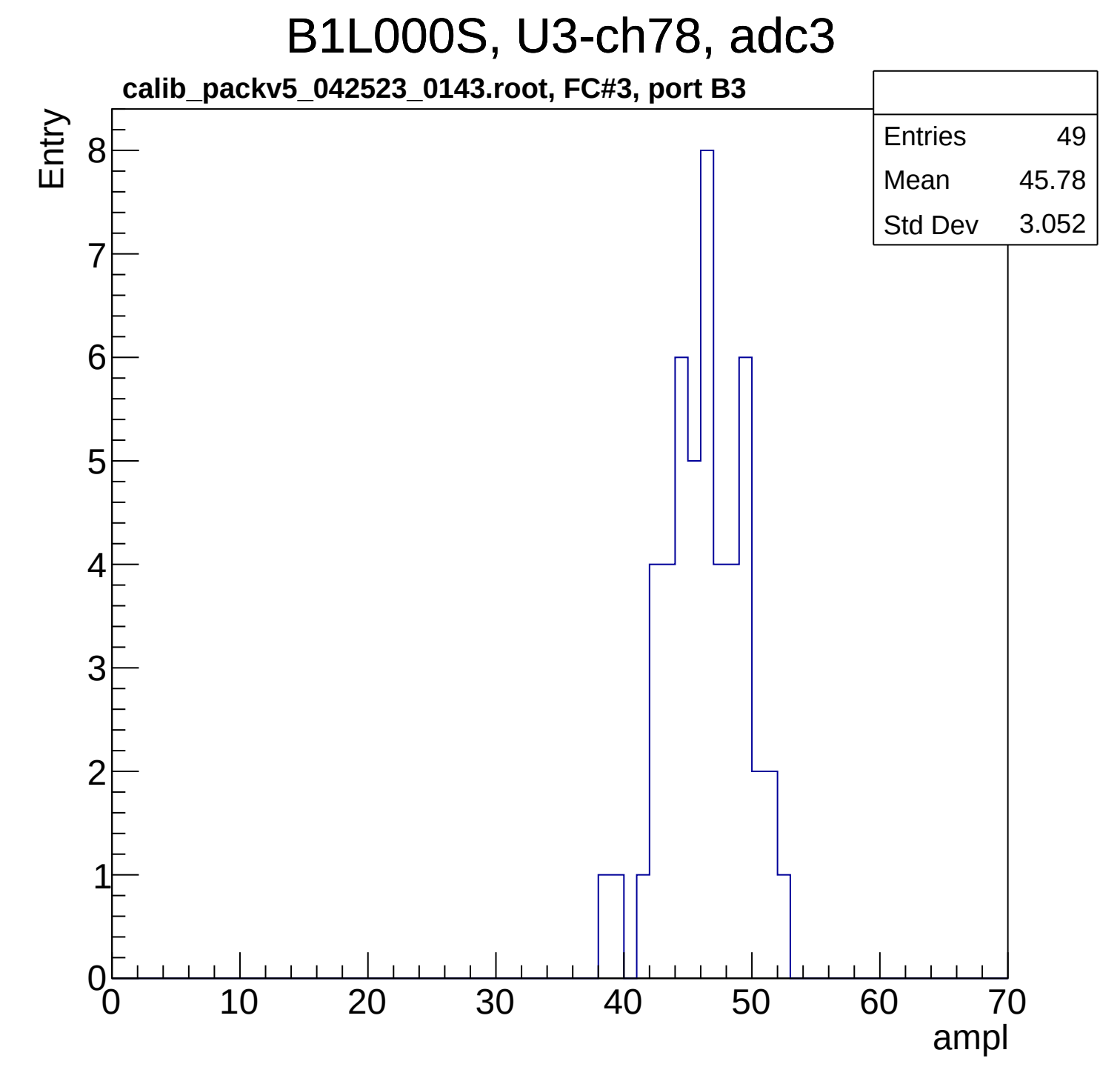

B1L000S, U3-ch77, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 61.83
Std Dev 1.067
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

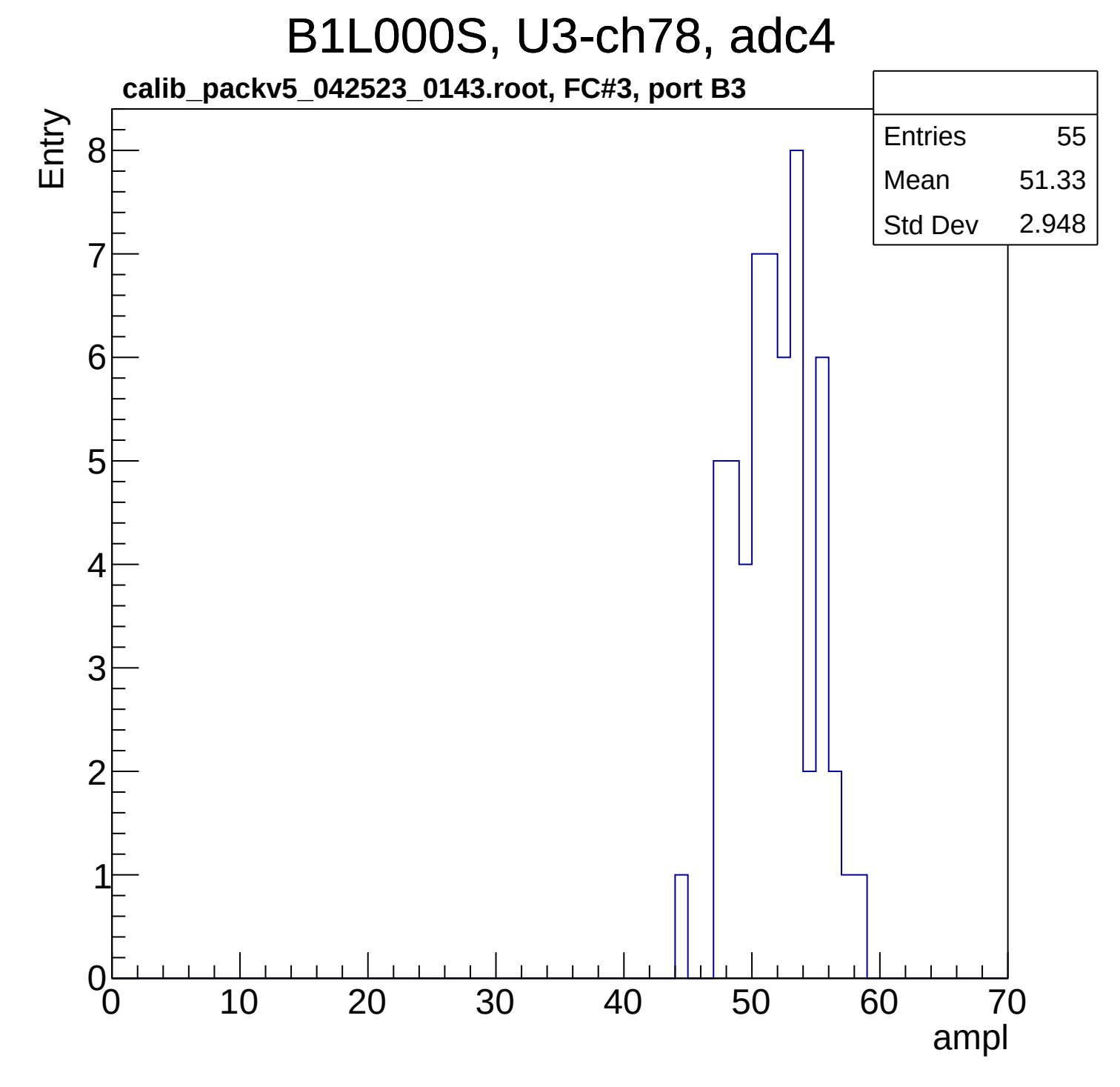

B1L000S, U3-ch77, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

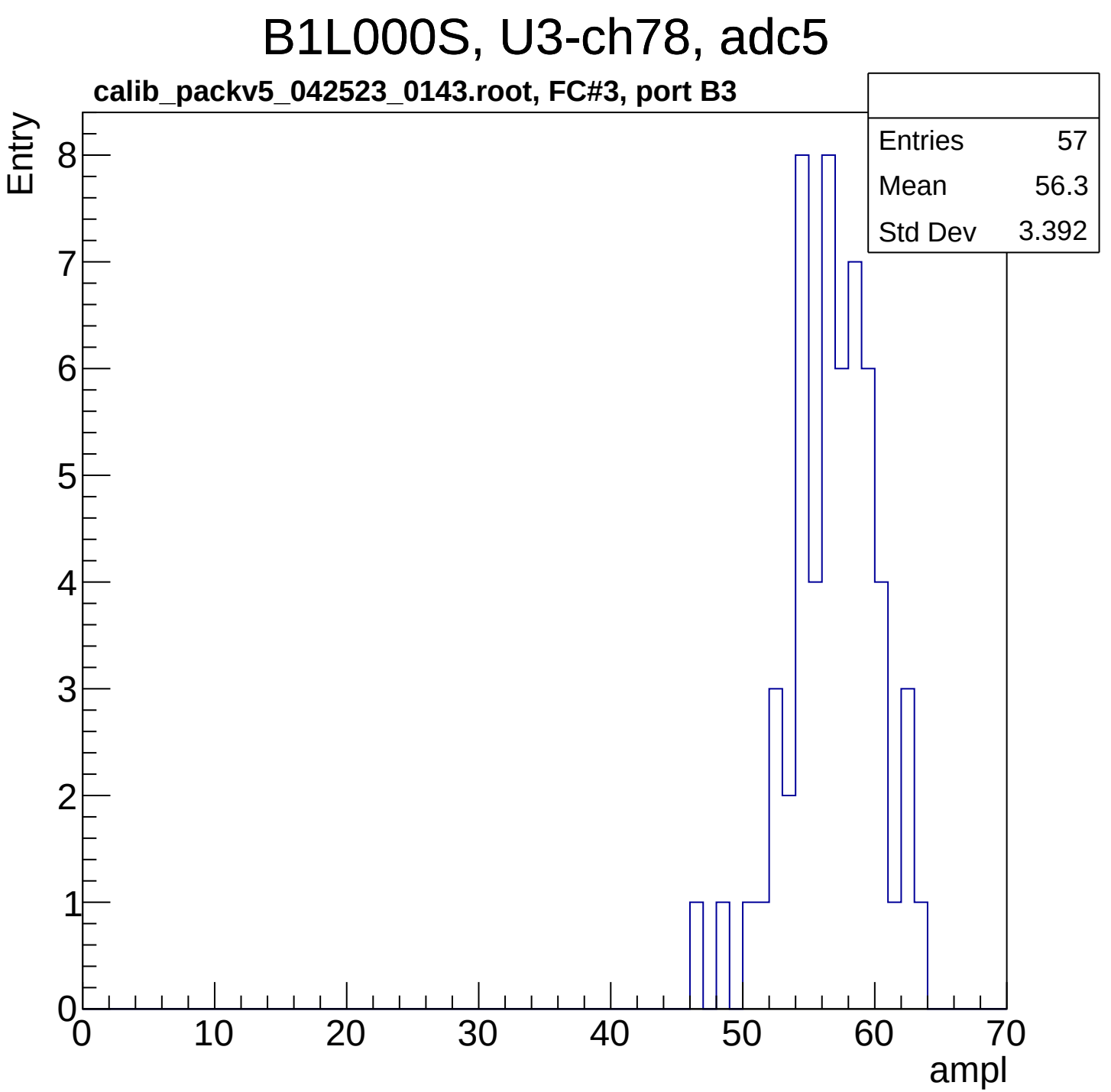

B1L000S, U3-ch78, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 25.59
Std Dev 6.372
Gaus mean : 27.3606
Gaus Width: 3.5116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch78, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 33.19
Std Dev 3.22
Gaus mean : 33.6280
Gaus Width: 3.3927
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch78, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 40.21
Std Dev 2.955
Gaus mean : 40.3651
Gaus Width: 3.3576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch78, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 45.78
Std Dev 3.052
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch78, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 51.33
Std Dev 2.948
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch78, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 56.3
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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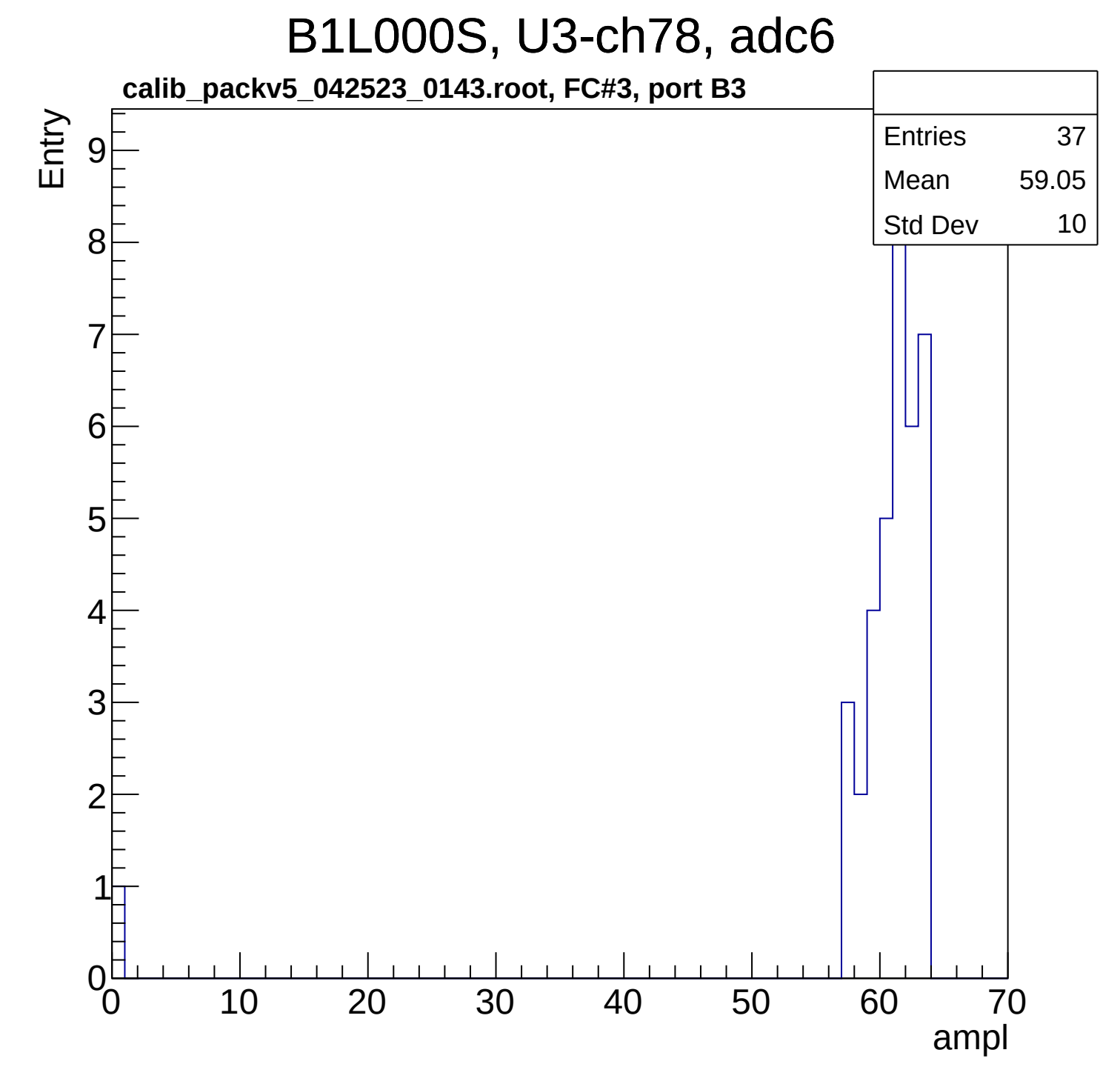
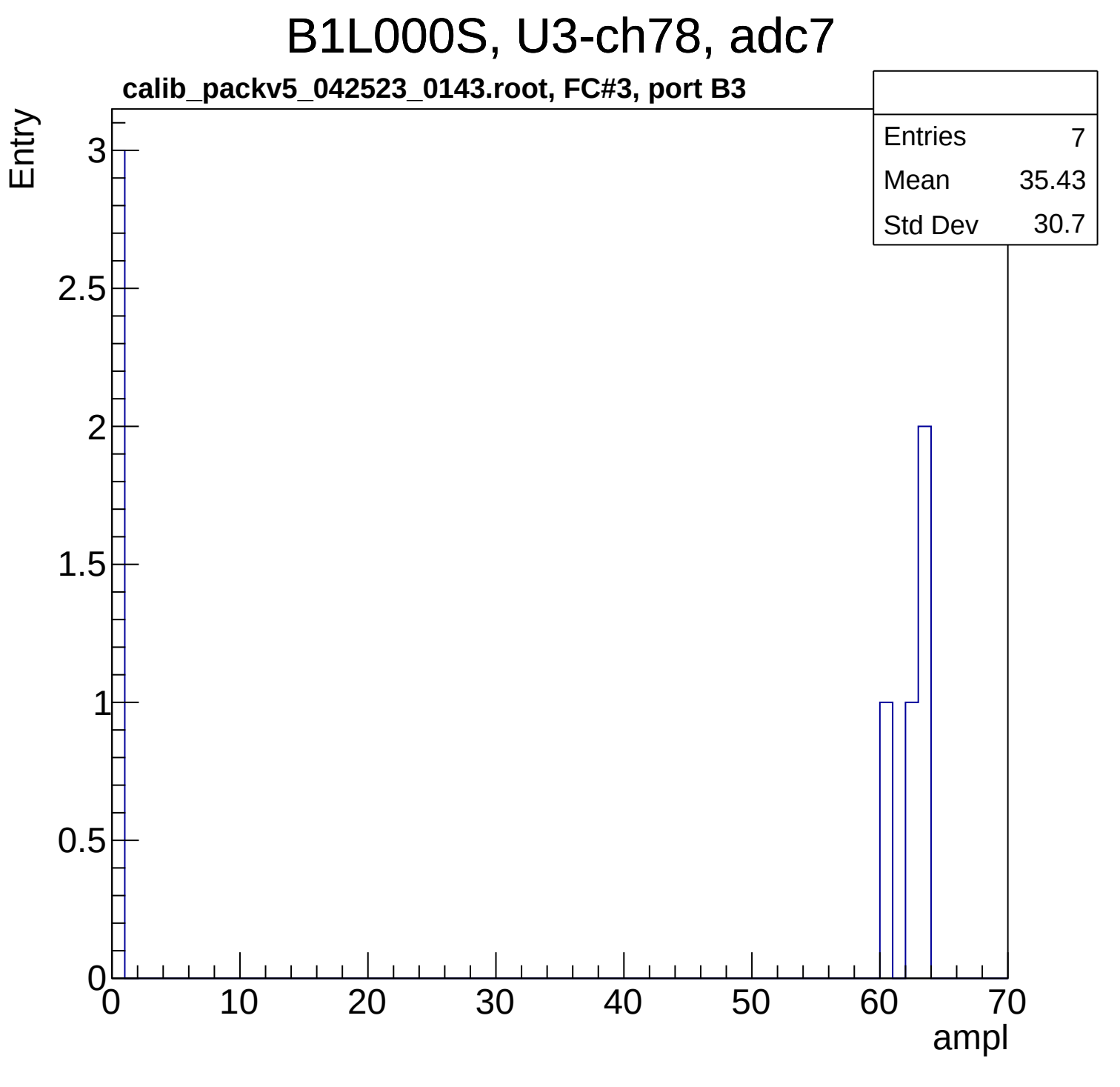
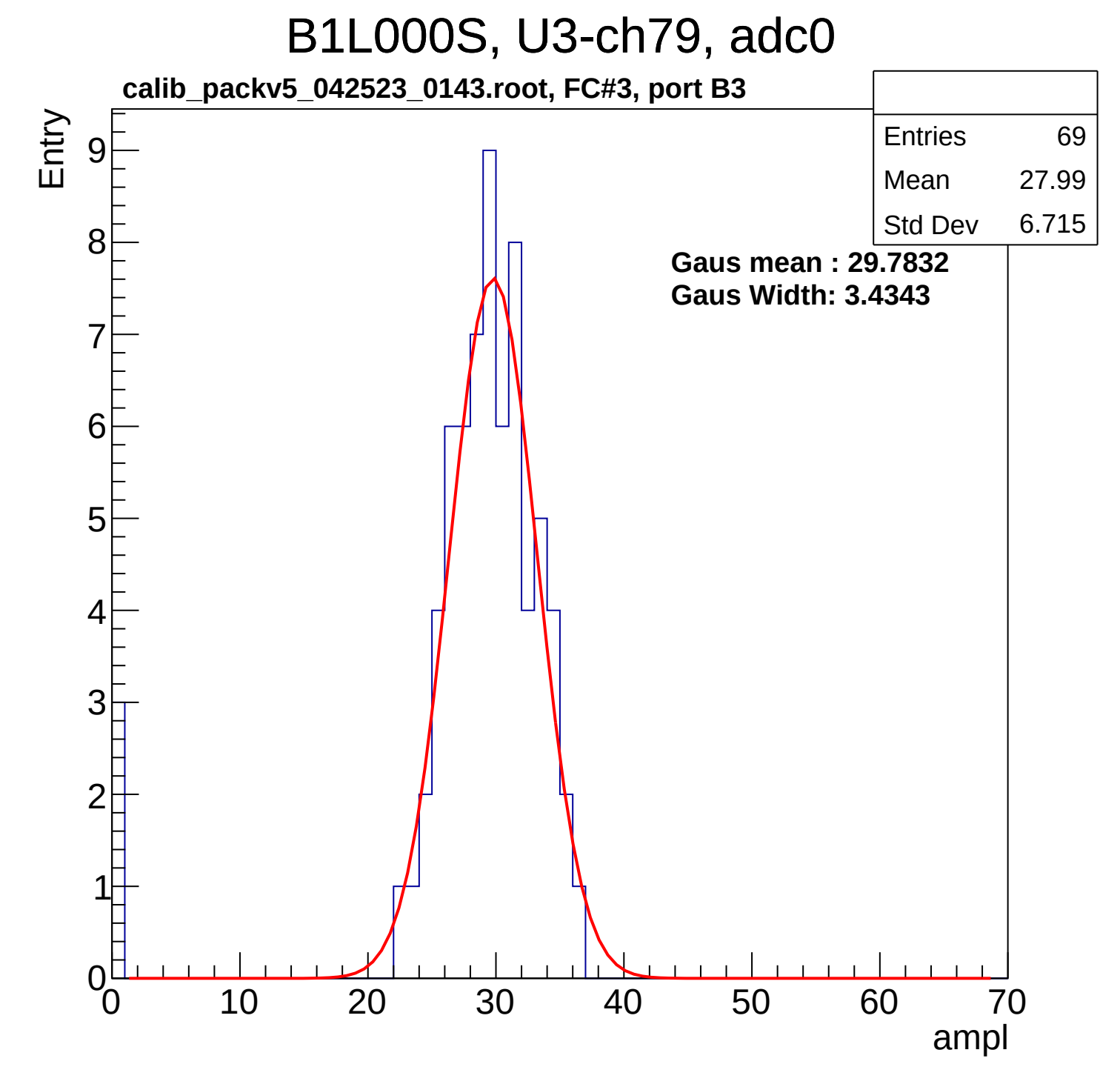
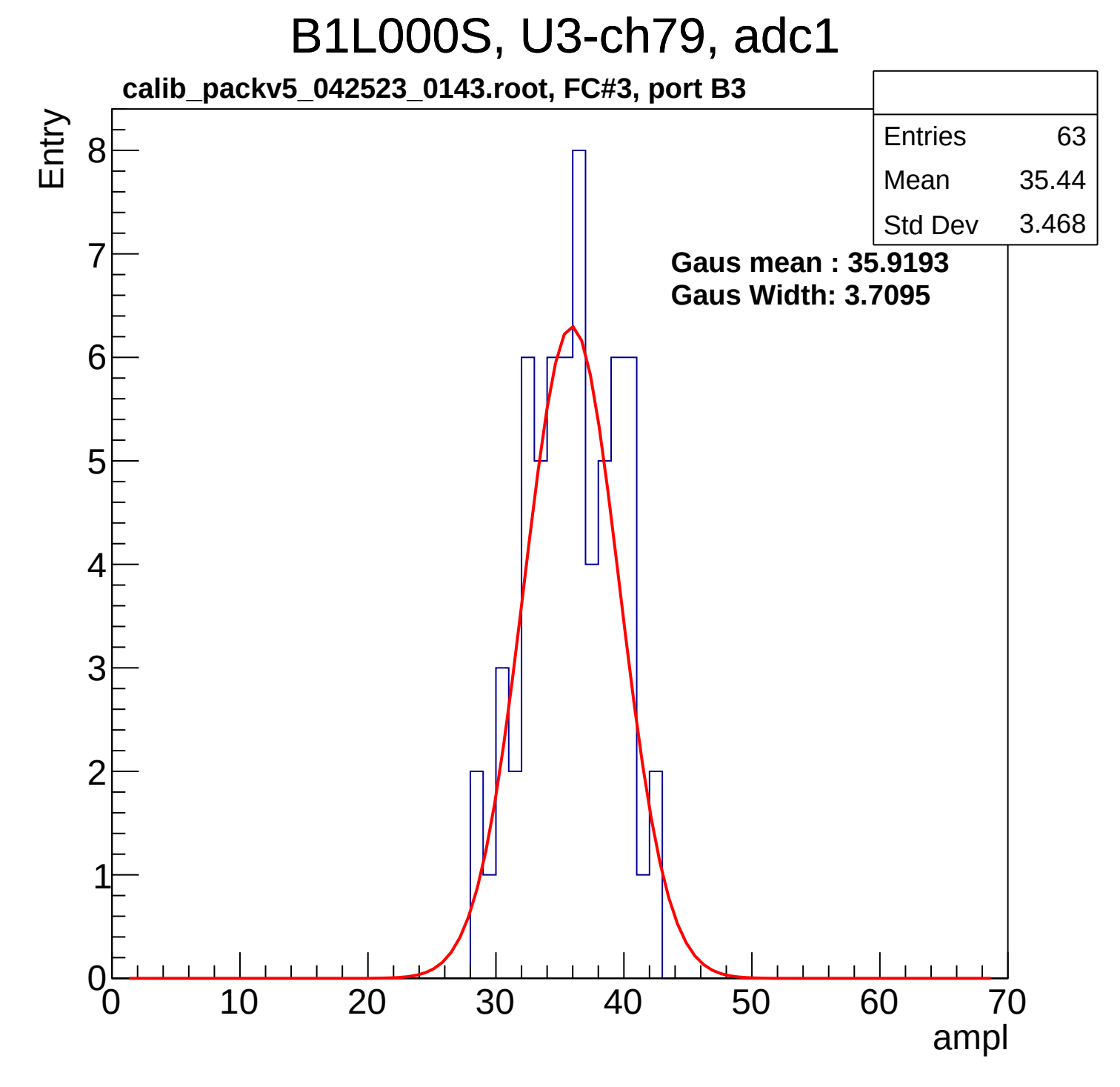
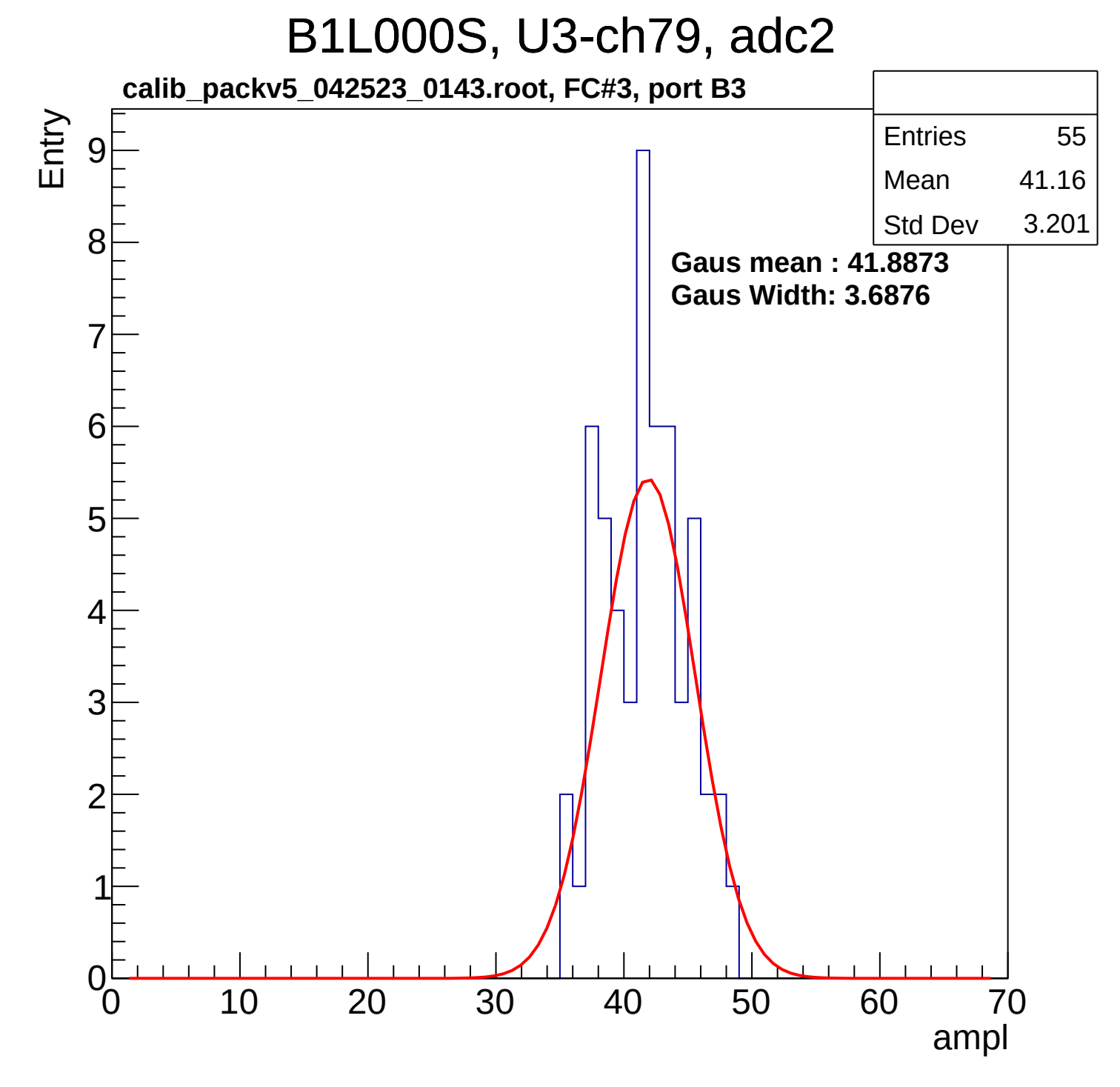

B1L000S, U3-ch78, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 59.05
Std Dev 10
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch78, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 35.43
Std Dev 30.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch79, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 27.99
Std Dev 6.715
Gaus mean : 29.7832
Gaus Width: 3.4343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch79, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 35.44
Std Dev 3.468
Gaus mean : 35.9193
Gaus Width: 3.7095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch79, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 41.16
Std Dev 3.201
Gaus mean : 41.8873
Gaus Width: 3.6876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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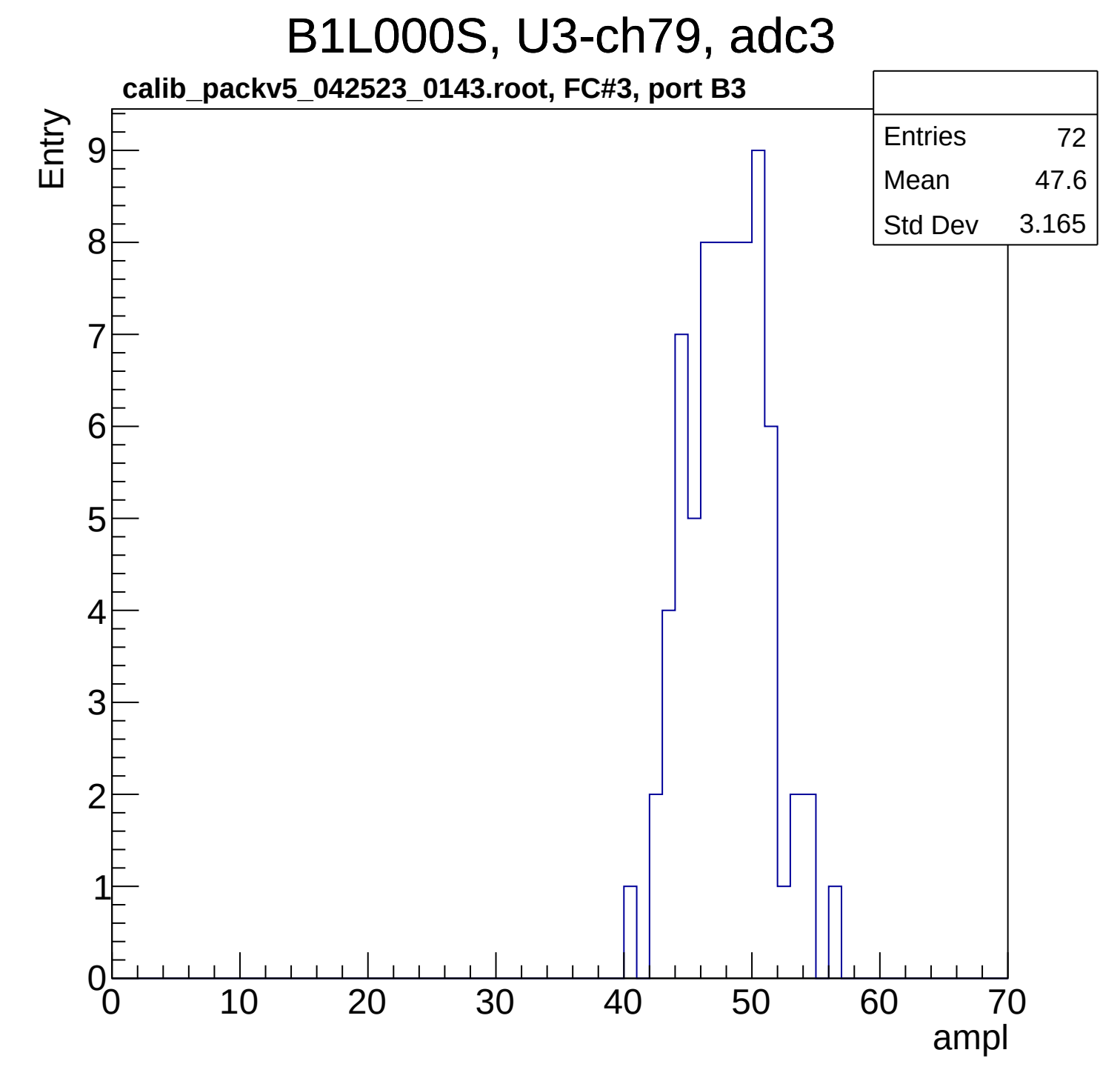
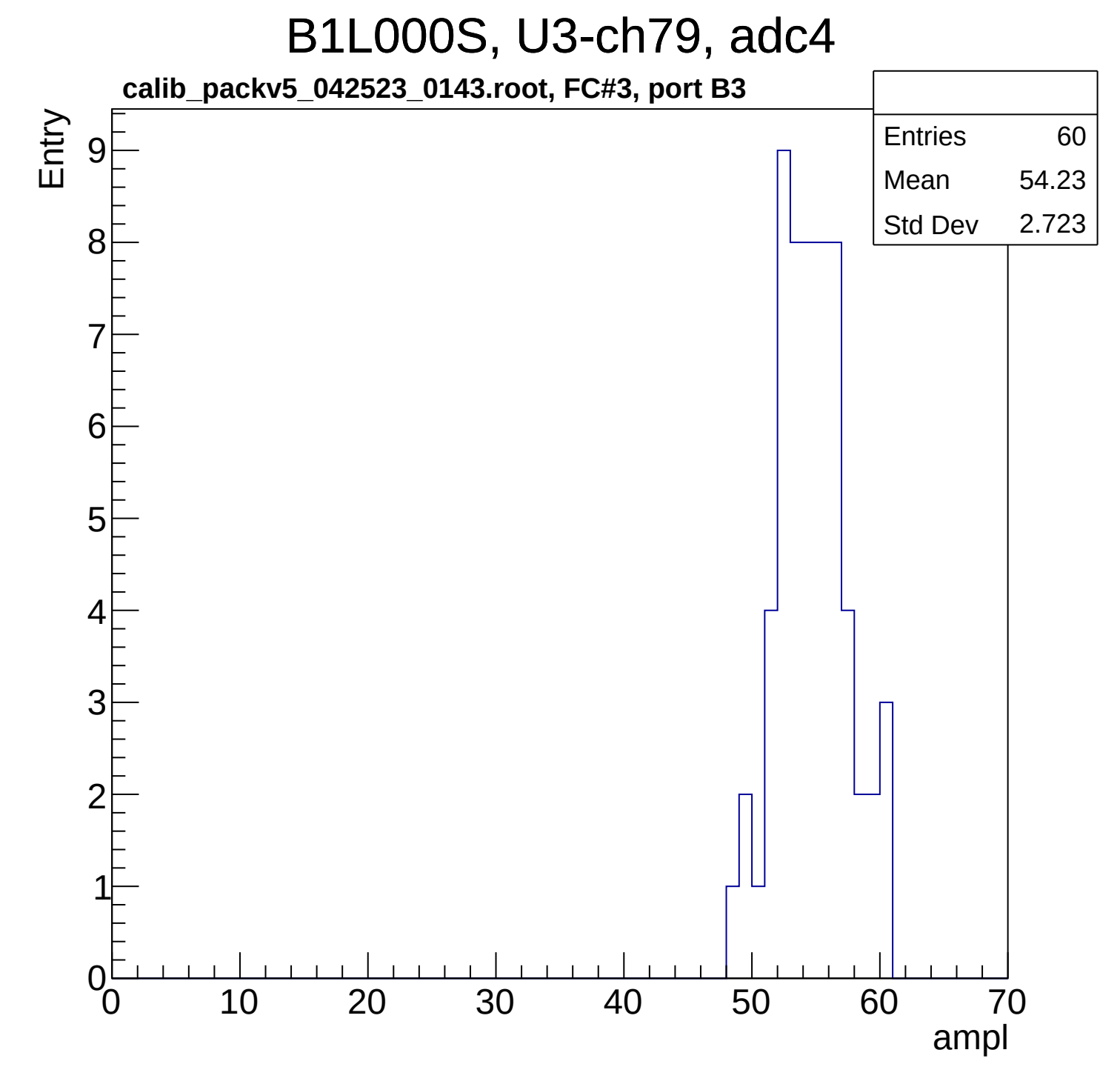
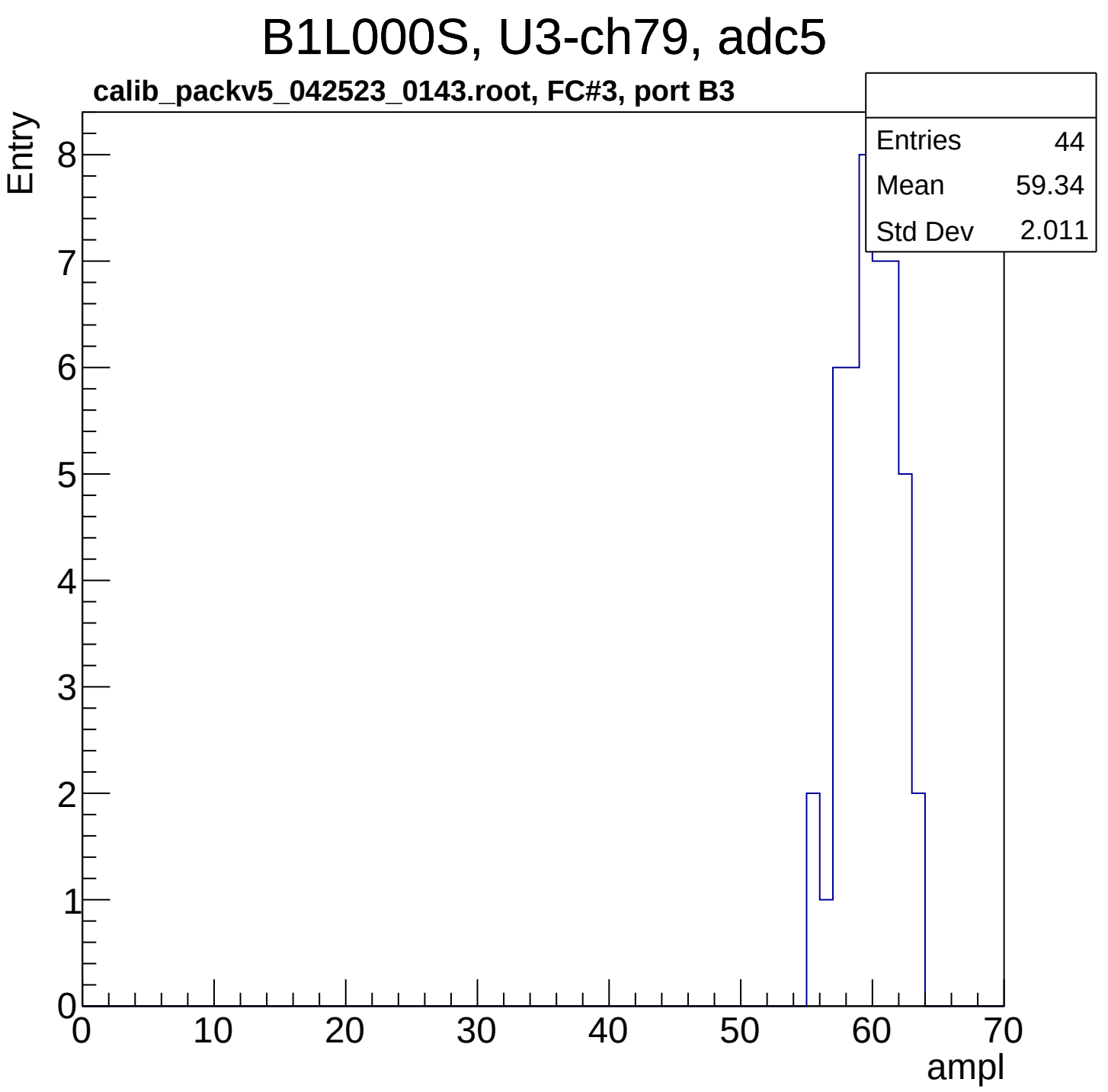

B1L000S, U3-ch79, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 47.6
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch79, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 54.23
Std Dev 2.723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch79, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 59.34
Std Dev 2.011
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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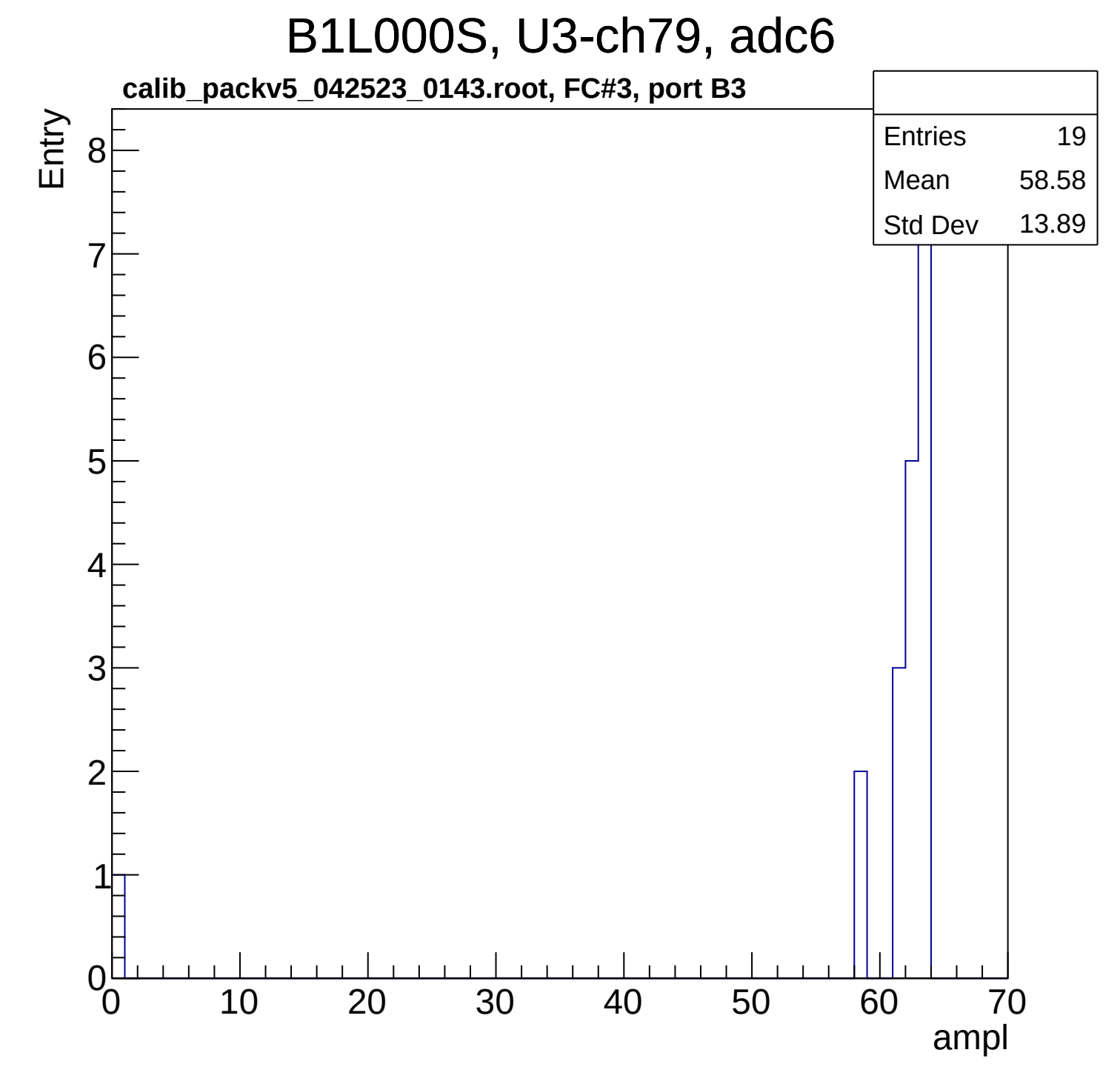
40
50
60
70


B1L000S, U3-ch79, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 58.58
Std Dev 13.89
Entry
ampl
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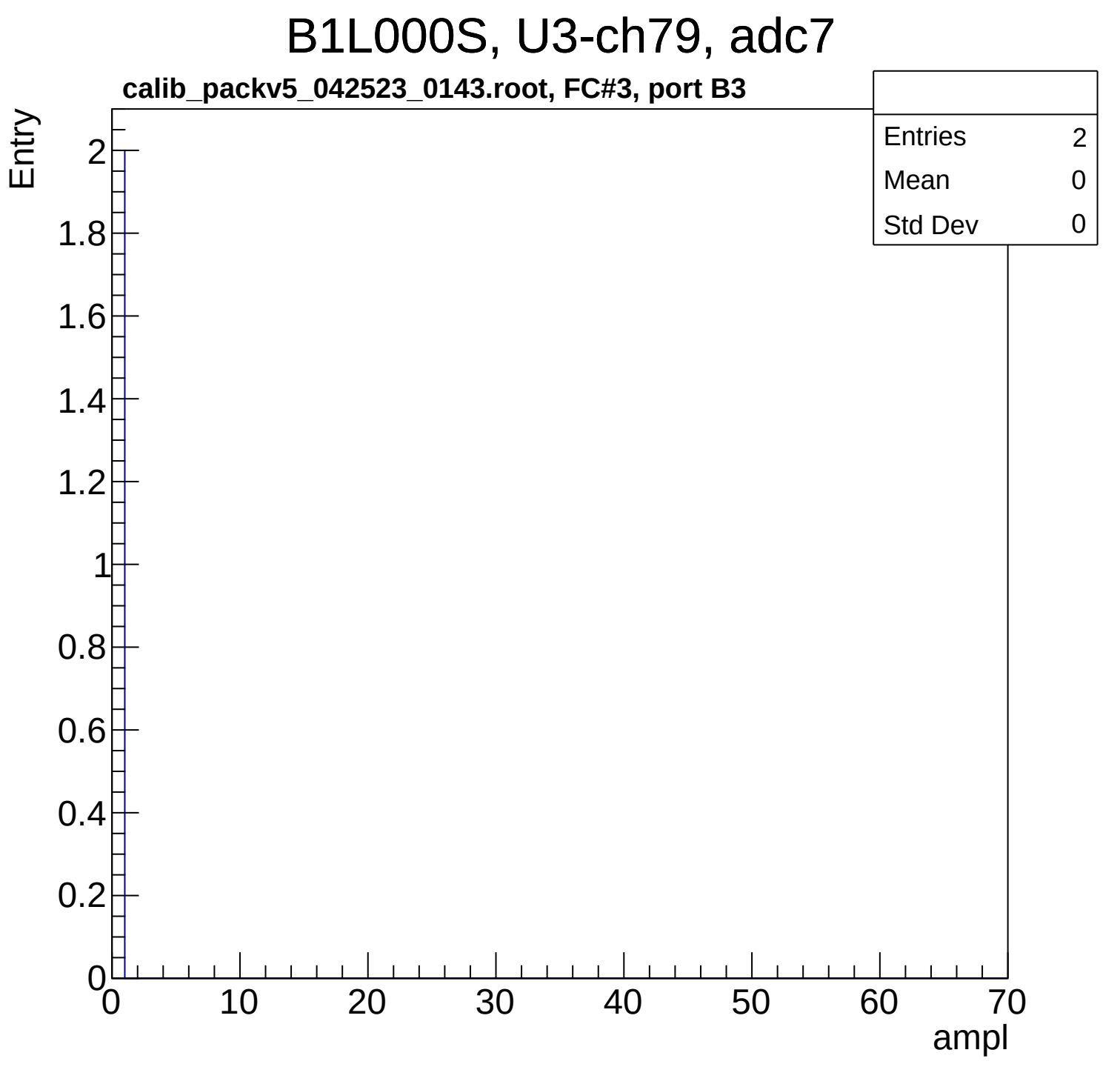
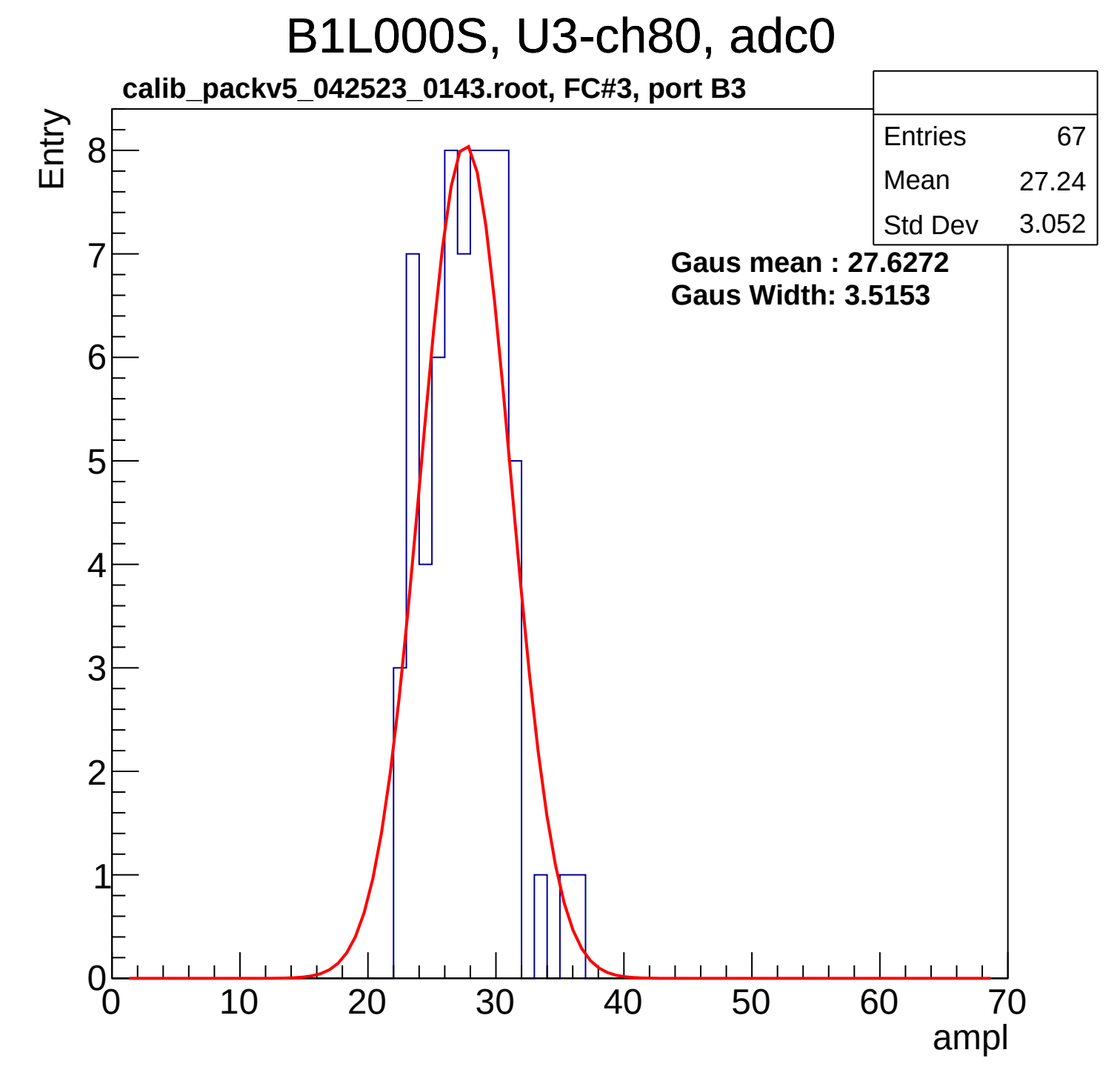
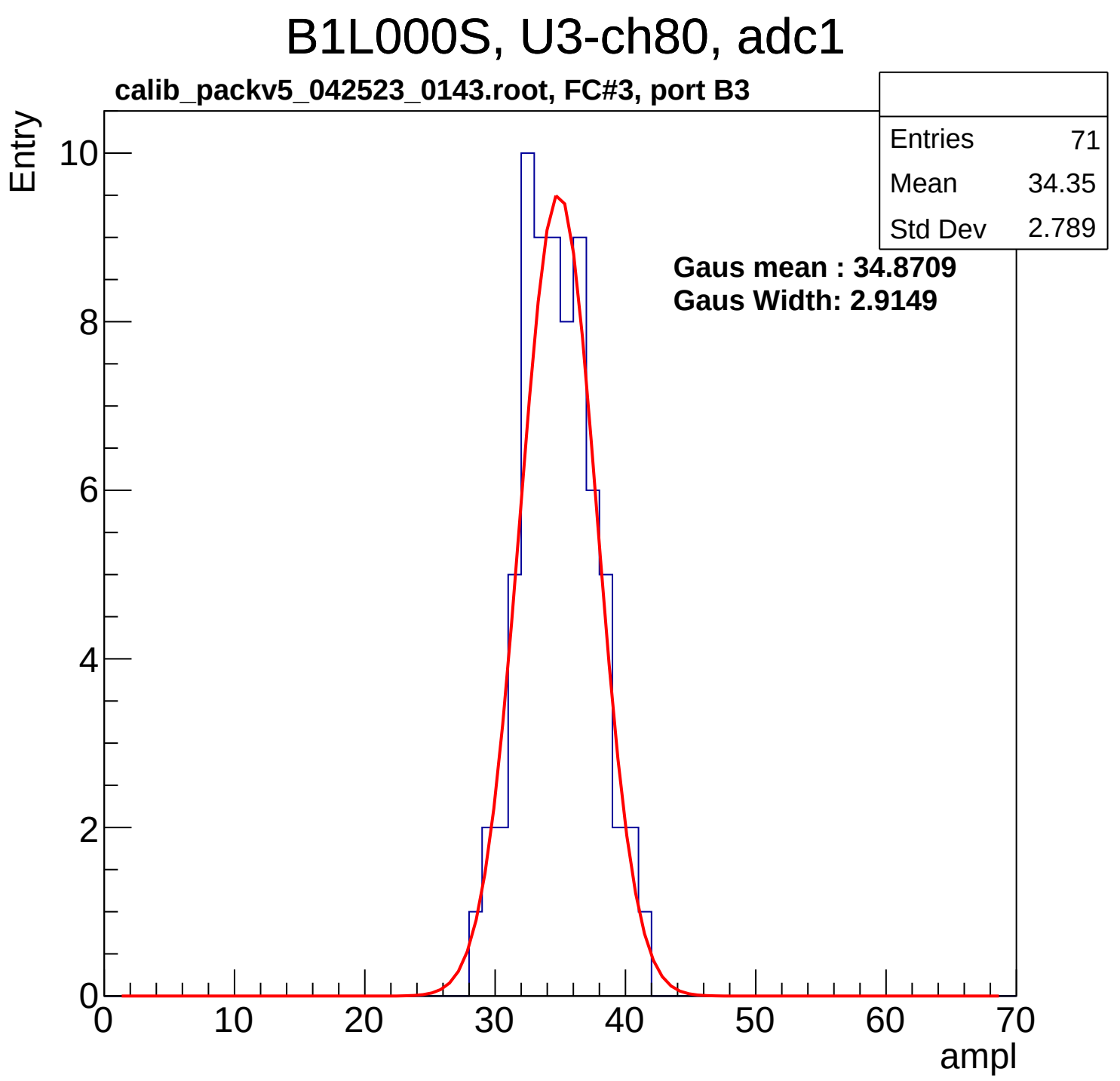
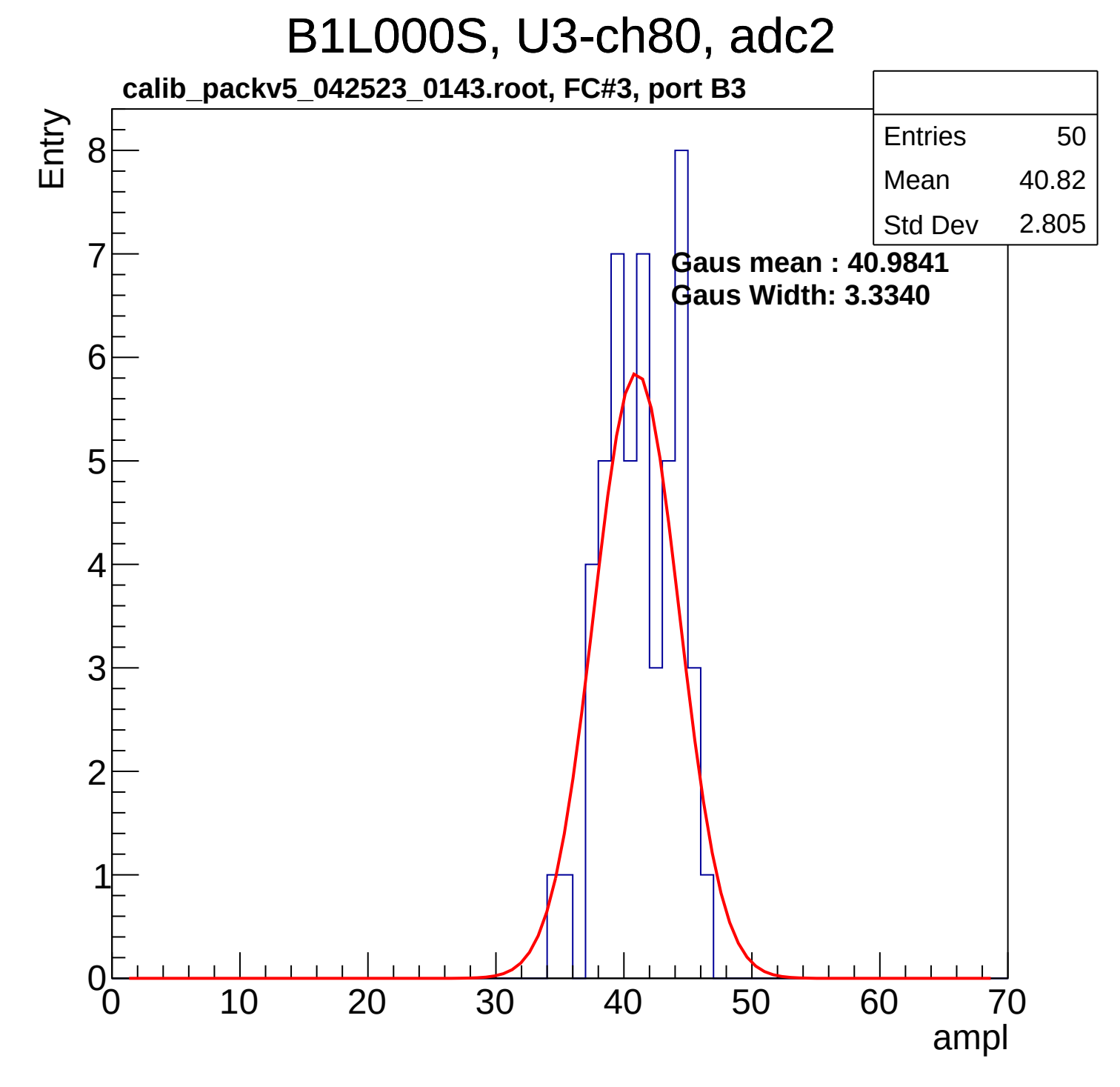
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch79, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch80, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 27.24
Std Dev 3.052
Gaus mean : 27.6272
Gaus Width: 3.5153
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch80, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 34.35
Std Dev 2.789
Gaus mean : 34.8709
Gaus Width: 2.9149
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch80, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 40.82
Std Dev 2.805
Gaus mean : 40.9841
Gaus Width: 3.3340
Entry
ampl
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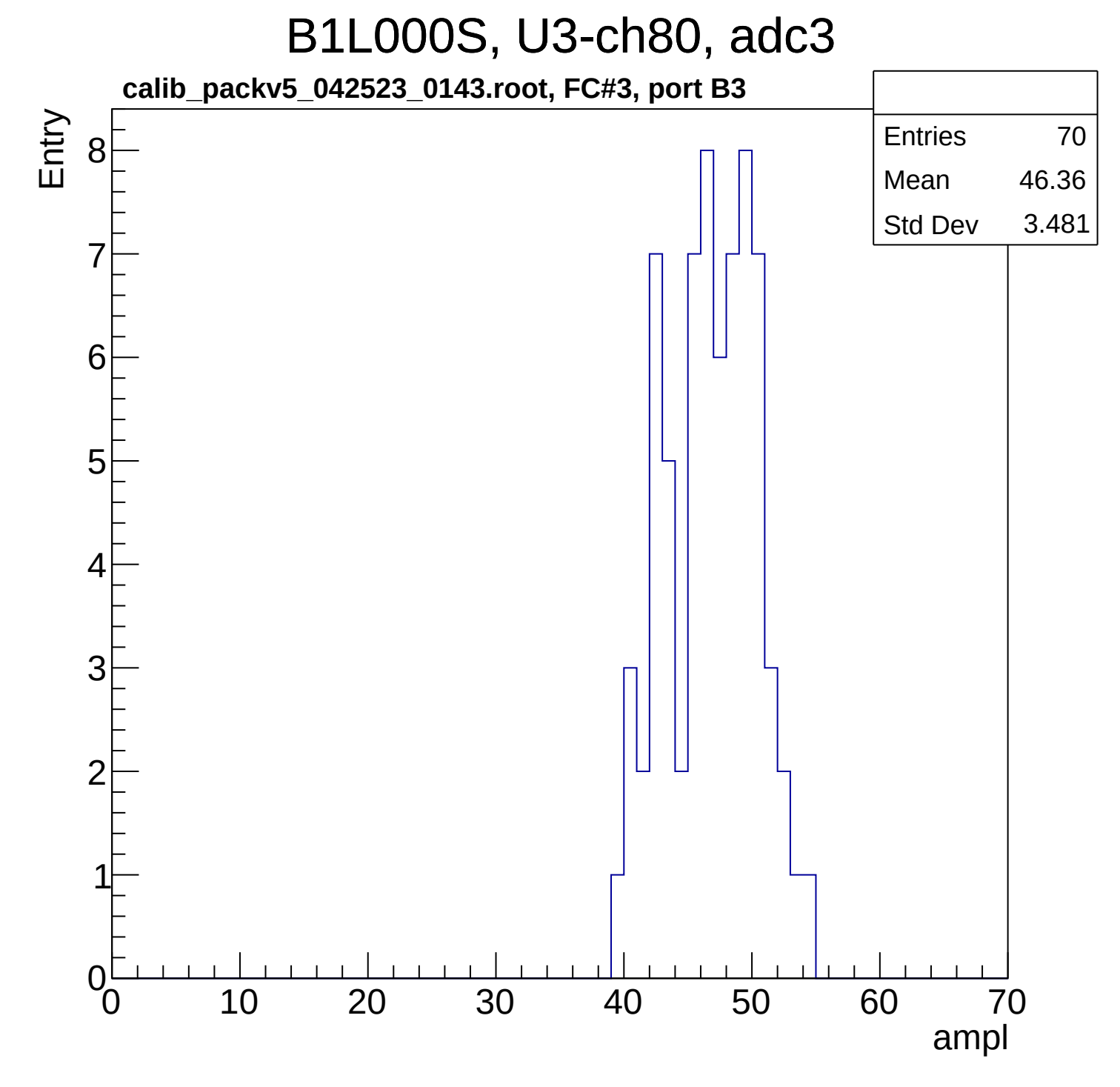
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch80, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 46.36
Std Dev 3.481
Entry
ampl
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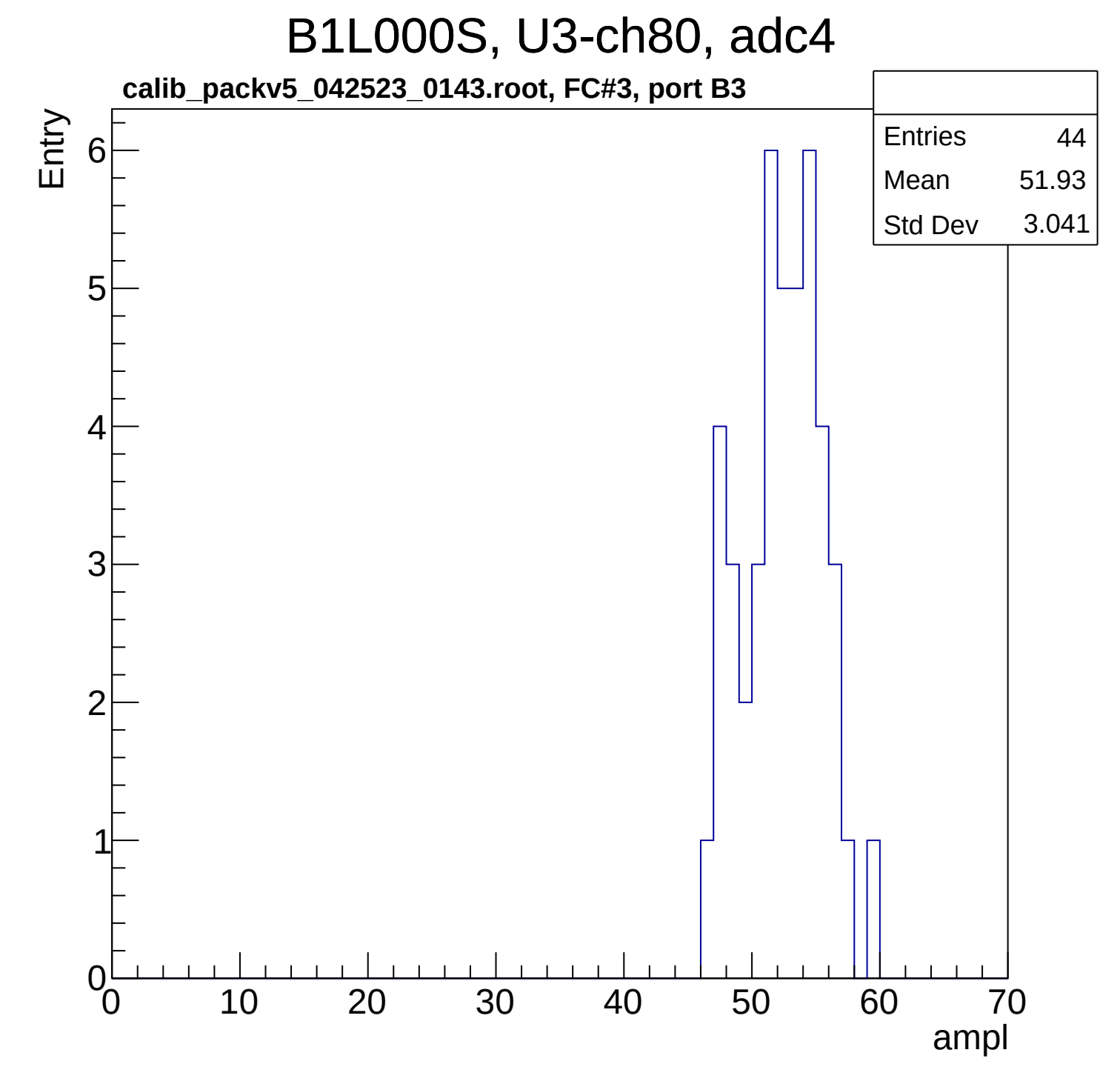
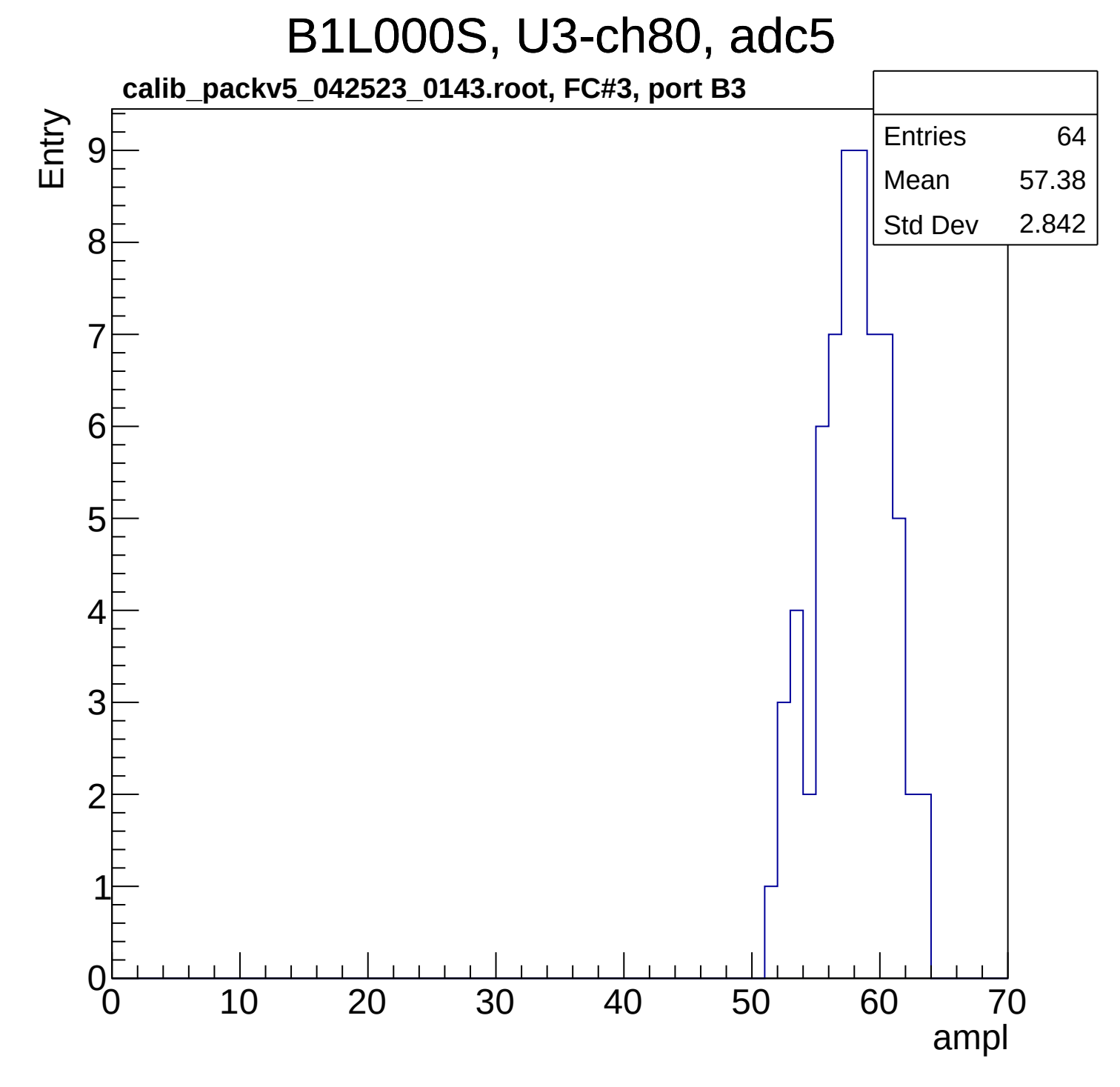
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch80, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 51.93
Std Dev 3.041
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch80, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 57.38
Std Dev 2.842
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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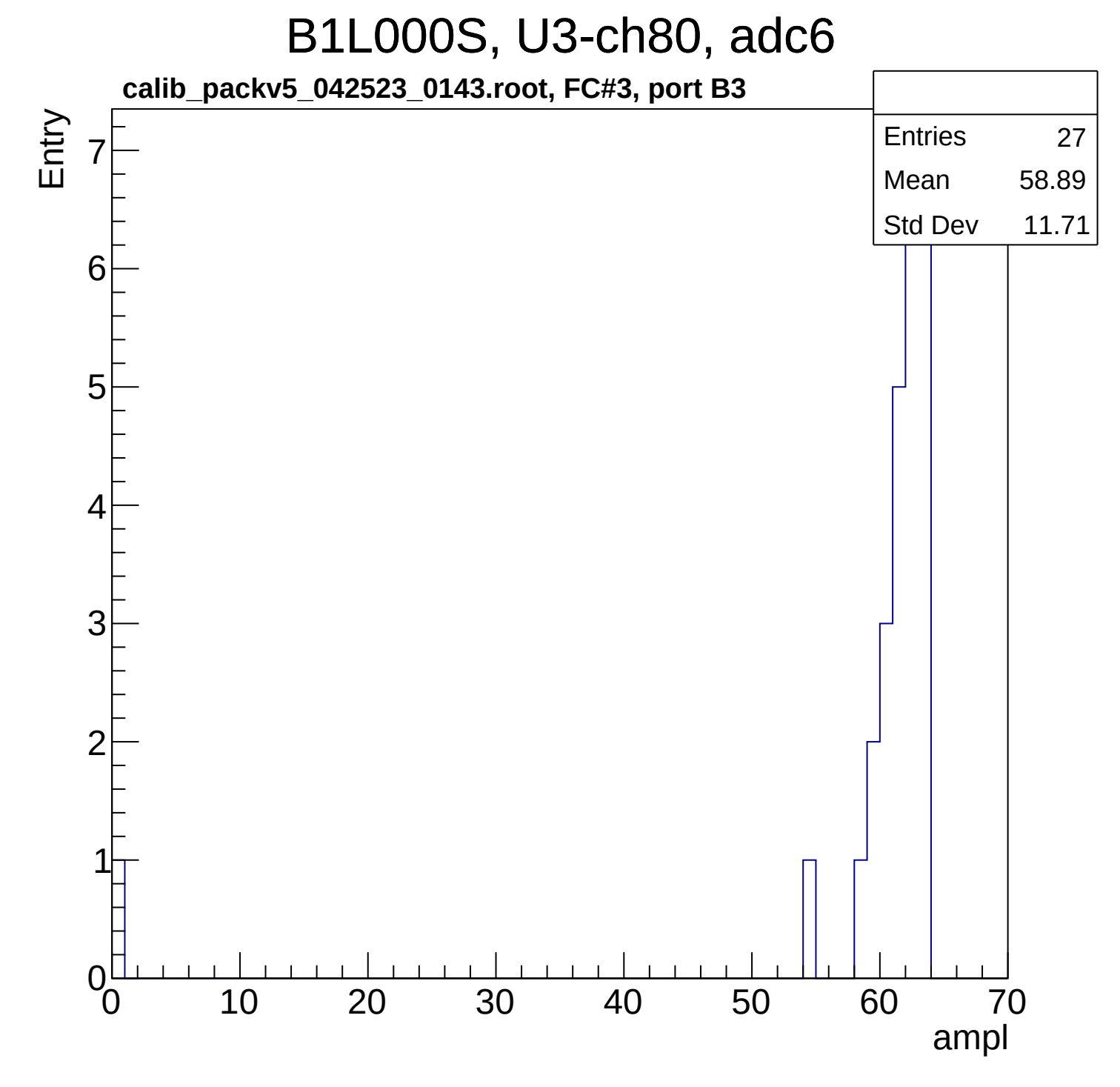
B1L000S, U3-ch80, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 27
Mean 58.89
Std Dev 11.71
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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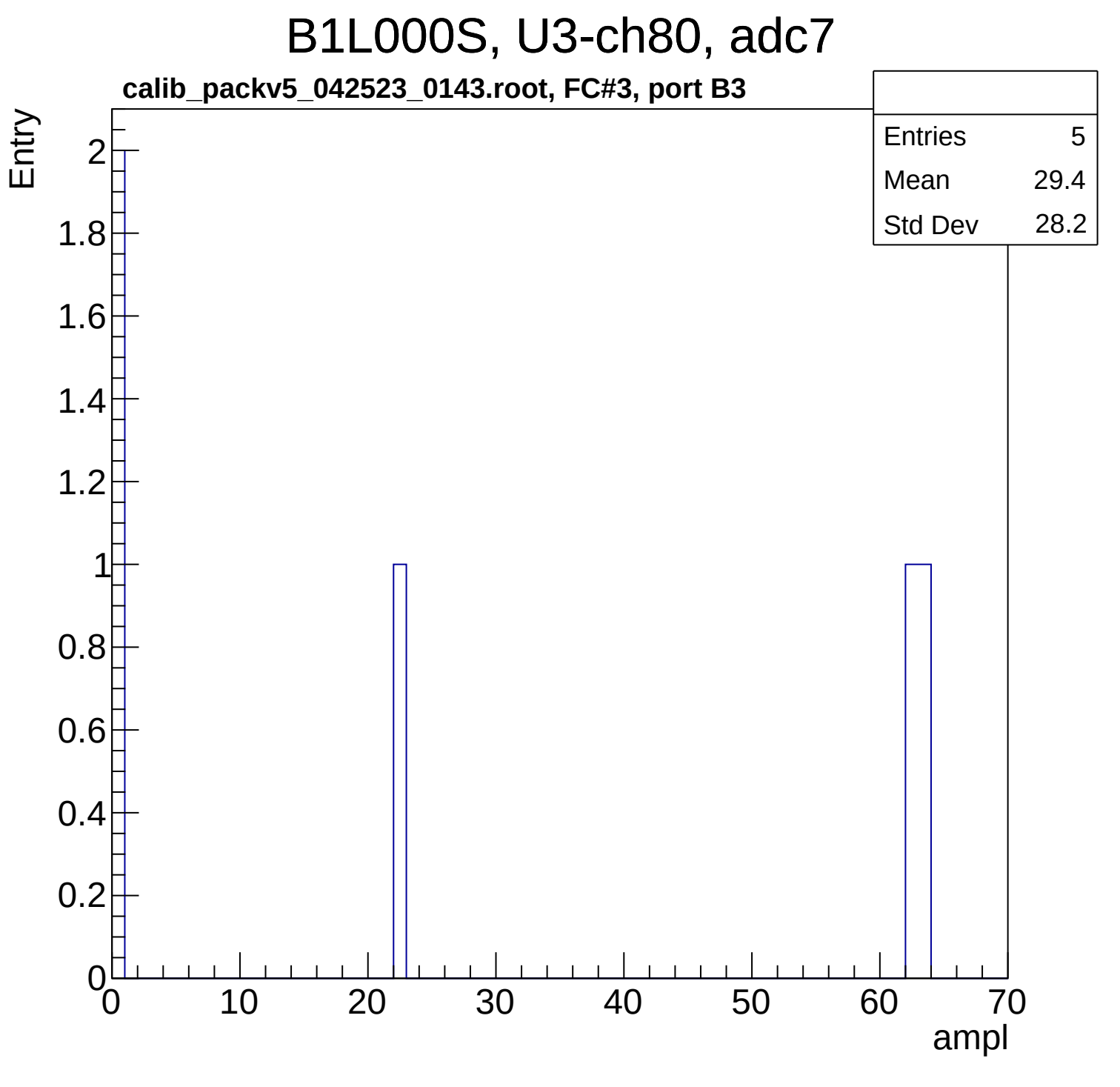
B1L000S, U3-ch80, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 29.4
Std Dev 28.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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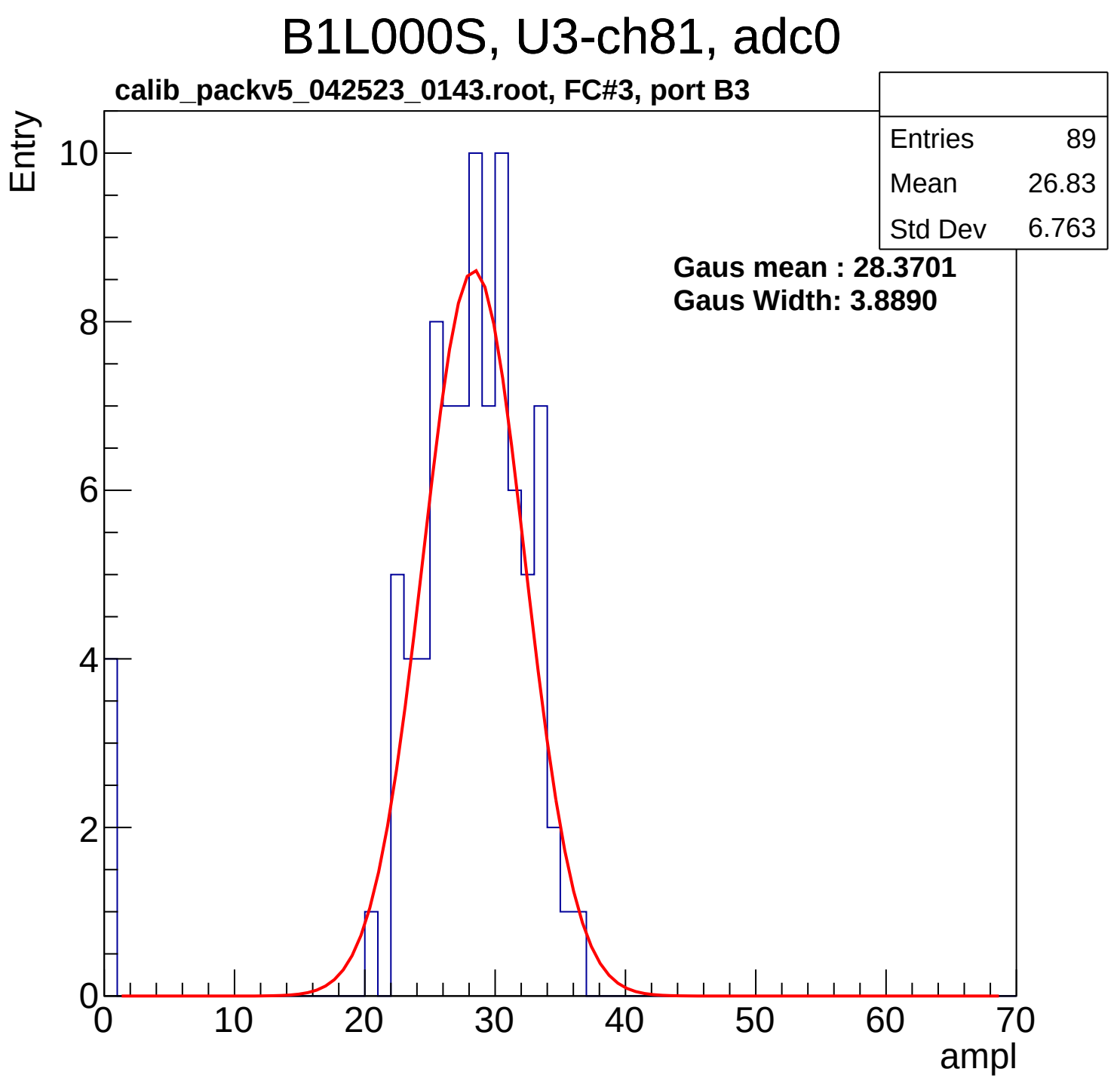
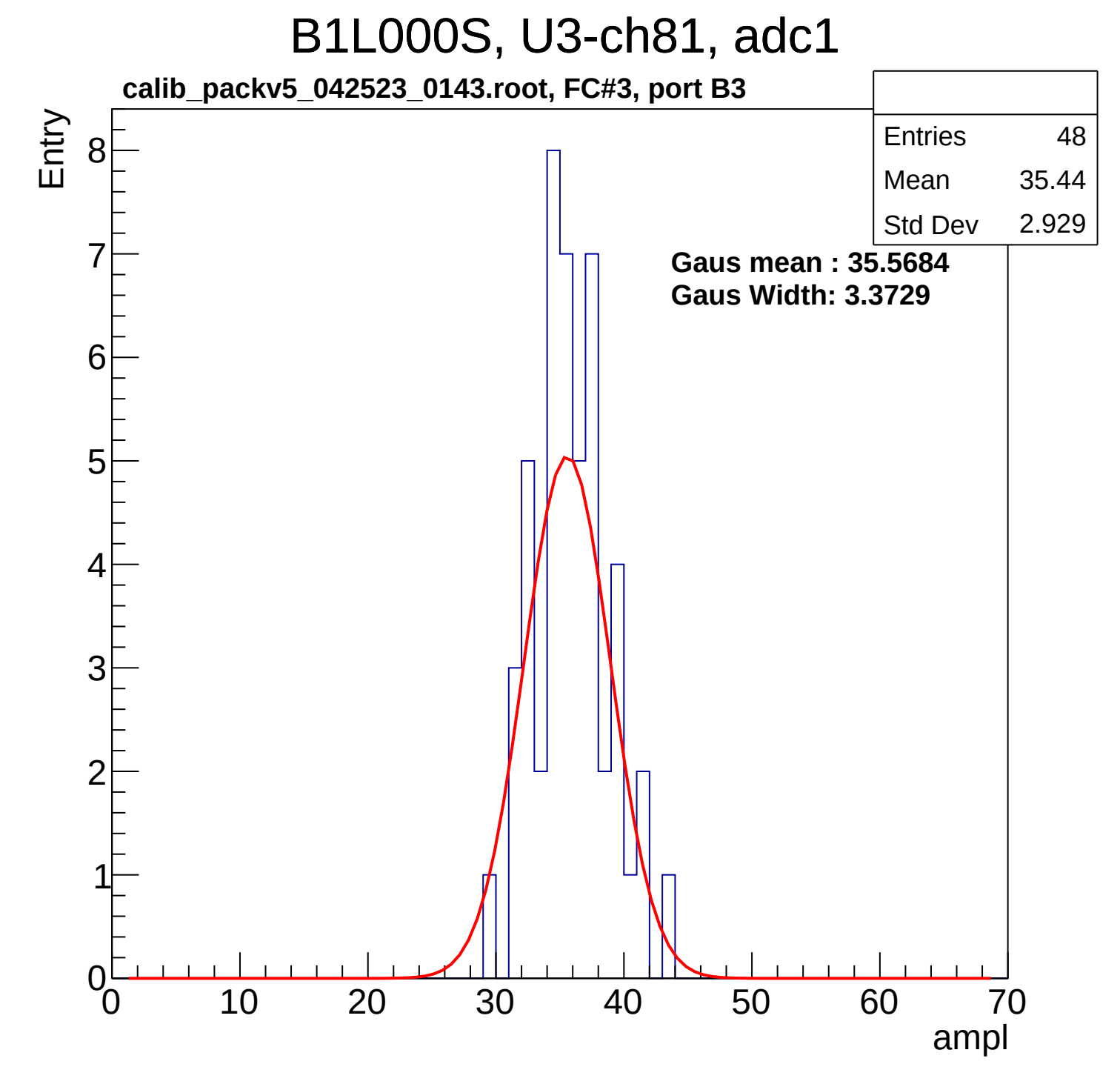

B1L000S, U3-ch81, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 89
Mean 26.83
Std Dev 6.763
Gaus mean : 28.3701
Gaus Width: 3.8890
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch81, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 35.44
Std Dev 2.929
Gaus mean : 35.5684
Gaus Width: 3.3729
Entry
ampl
0
1
2
3
4
5
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0
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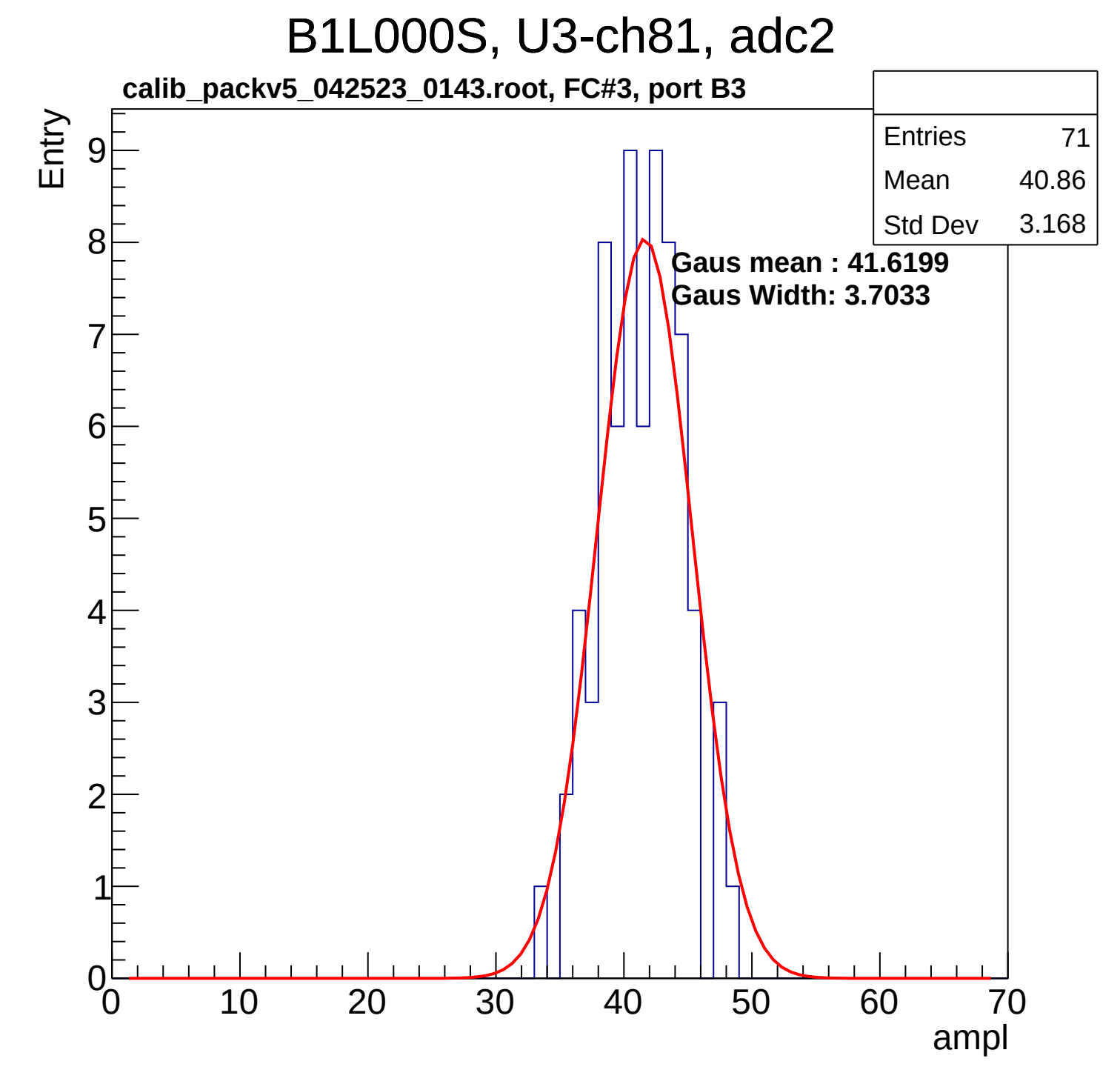
B1L000S, U3-ch81, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 40.86
Std Dev 3.168
Gaus mean : 41.6199
Gaus Width: 3.7033
Entry
ampl
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3
4
5
6
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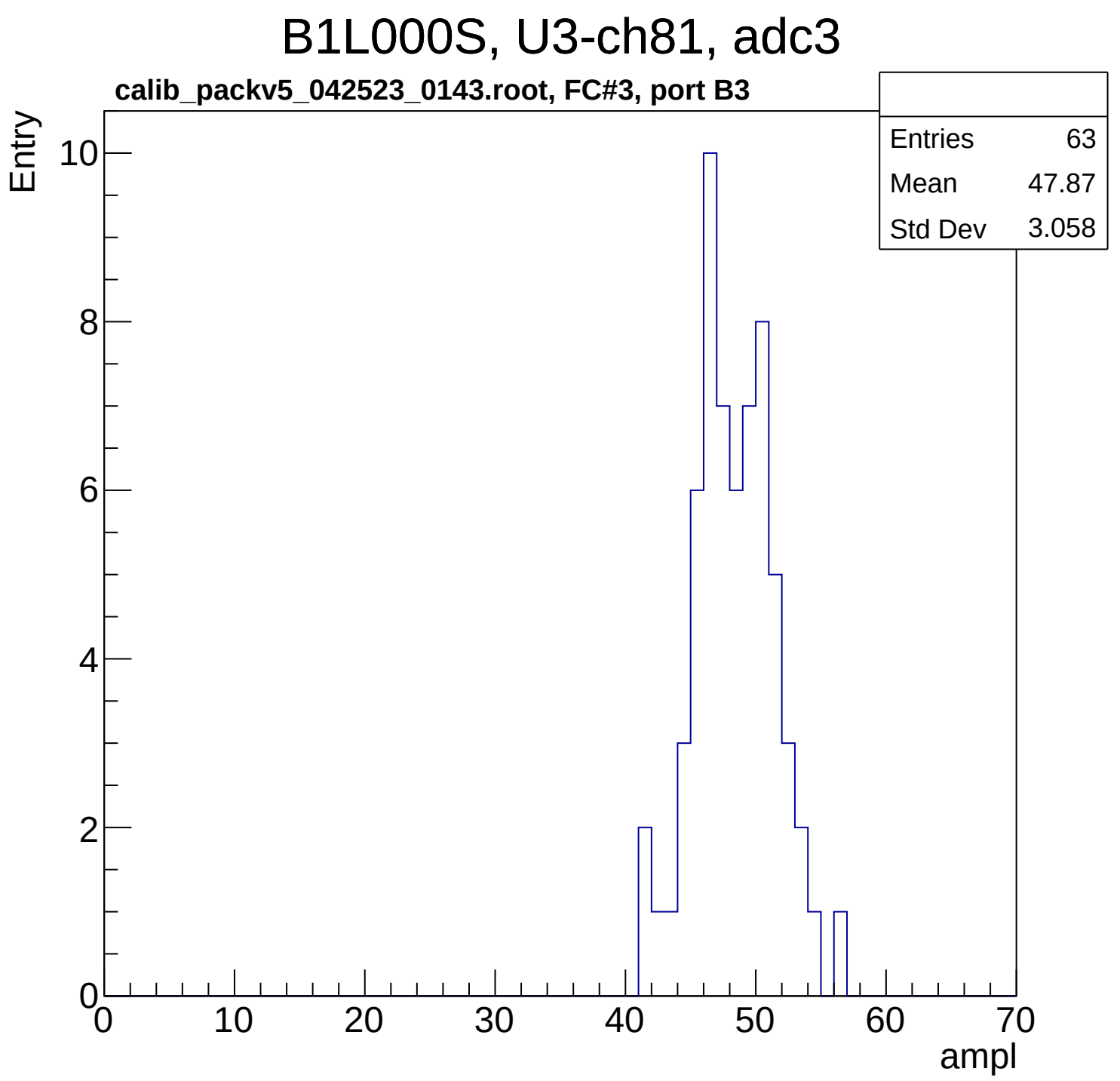
B1L000S, U3-ch81, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 47.87
Std Dev 3.058
Entry
ampl
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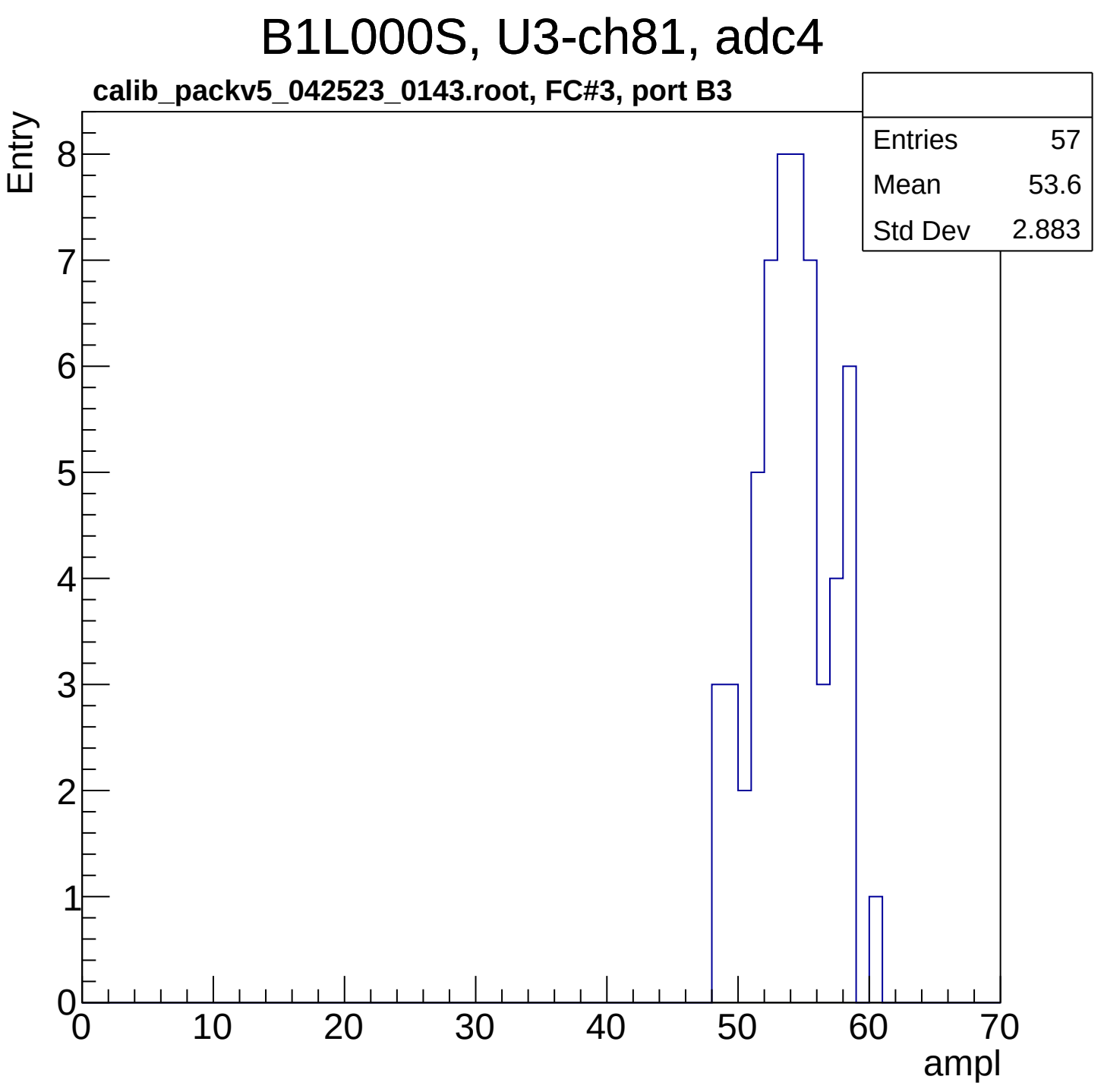
B1L000S, U3-ch81, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 53.6
Std Dev 2.883
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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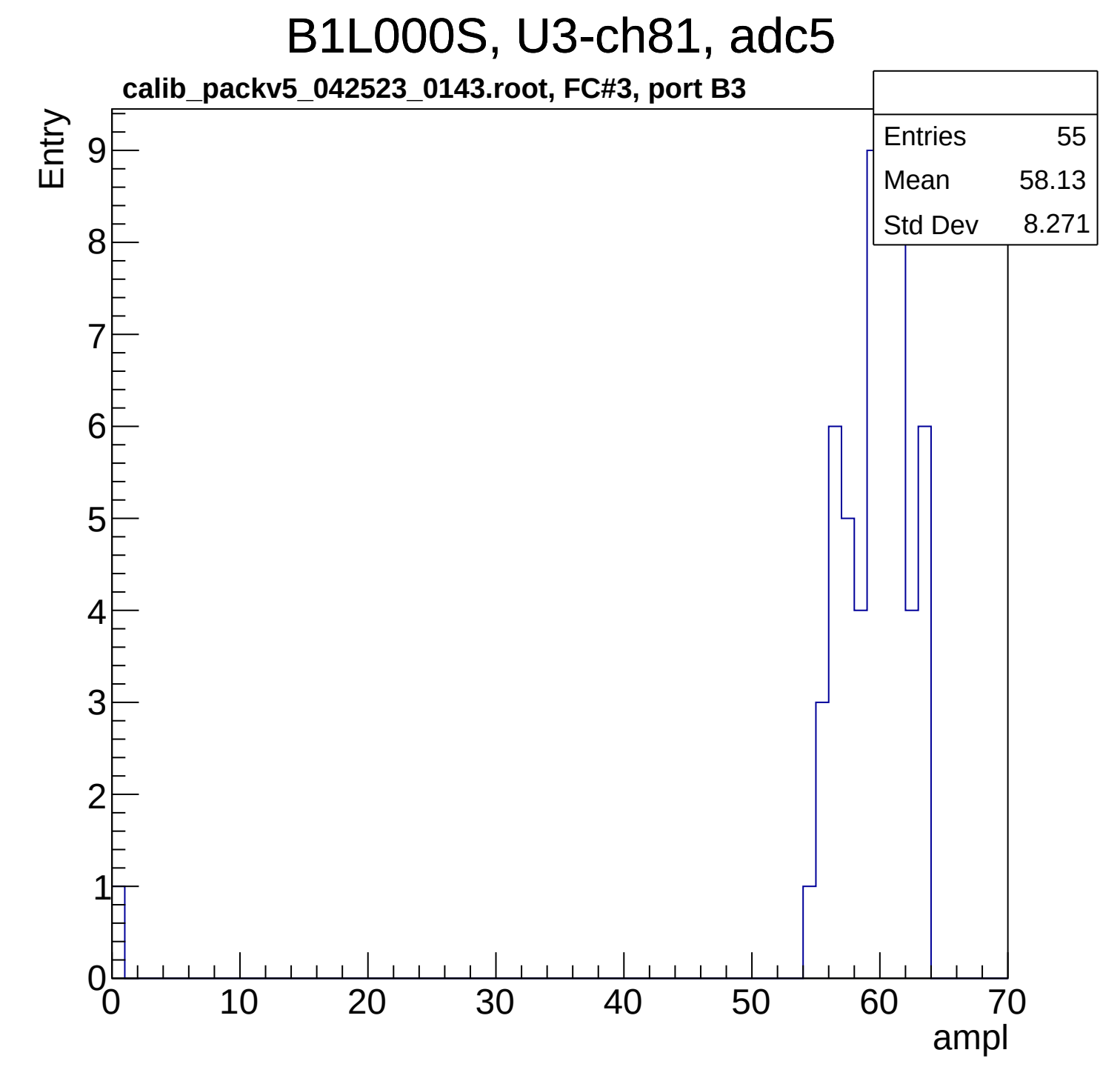
B1L000S, U3-ch81, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.13
Std Dev 8.271
Entry
ampl
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2
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4
5
6
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0
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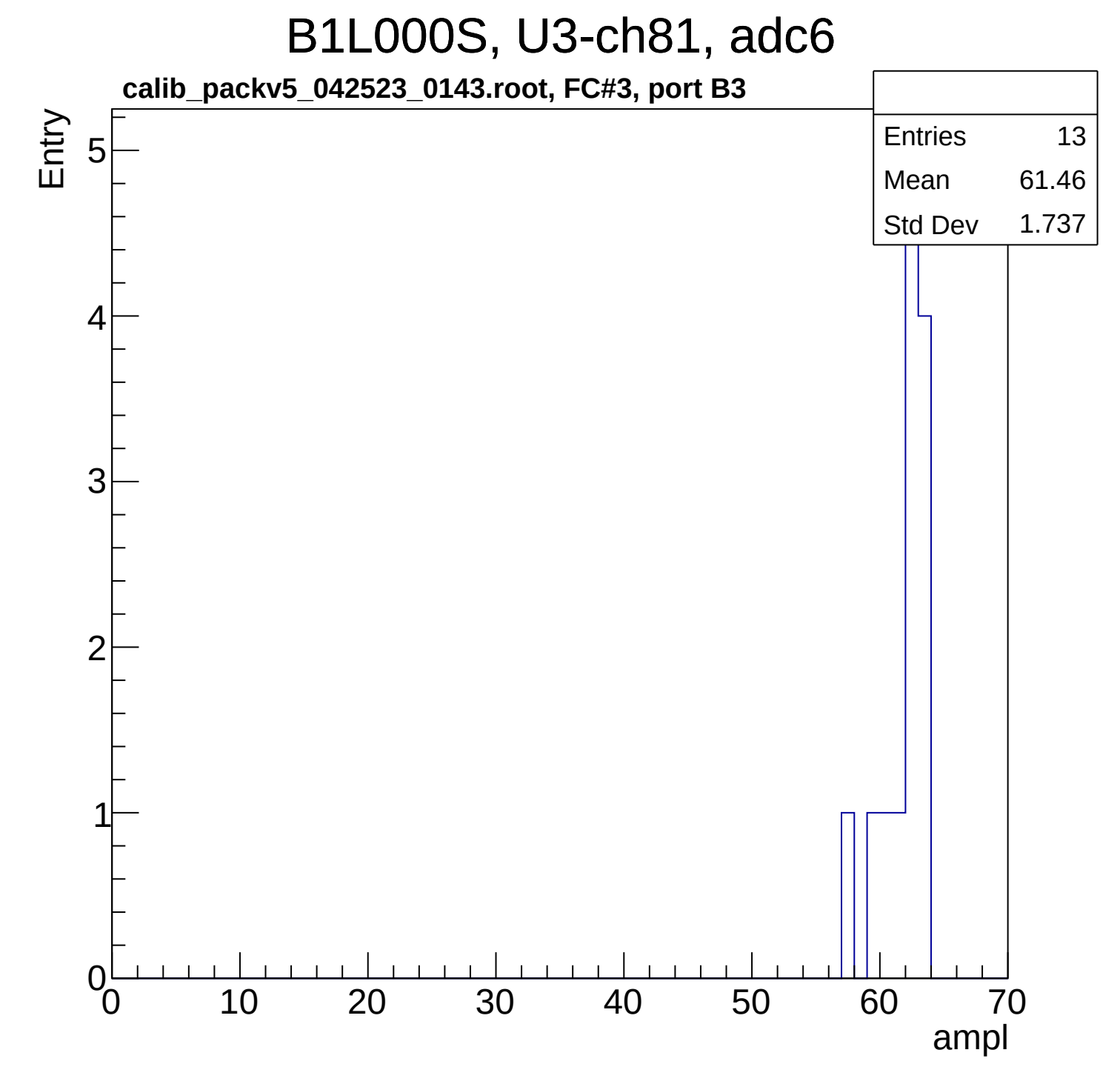
B1L000S, U3-ch81, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.46
Std Dev 1.737
Entry
ampl
0
1
2
3
4
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0
10
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40
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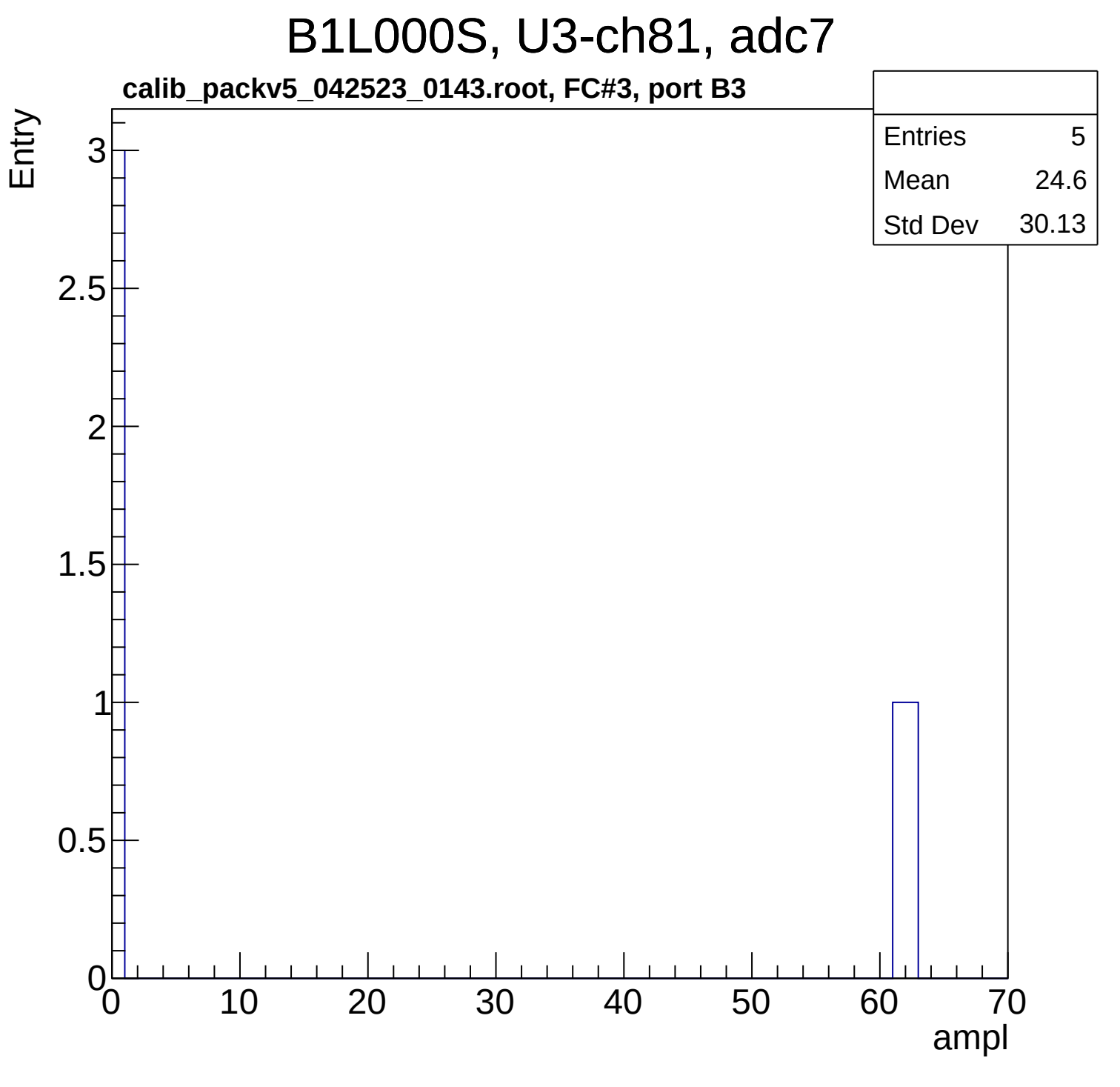
B1L000S, U3-ch81, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 24.6
Std Dev 30.13
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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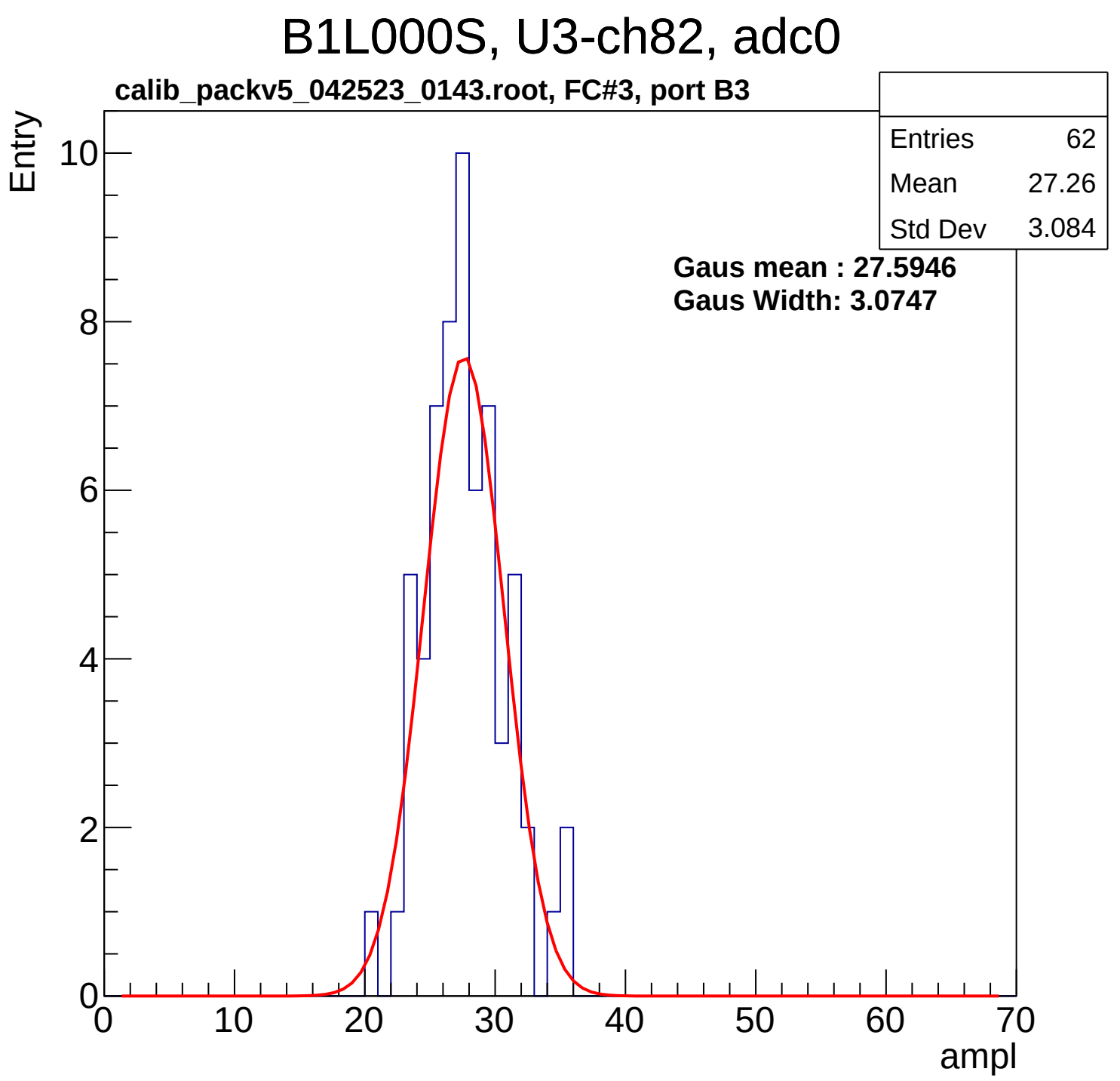
B1L000S, U3-ch82, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.26
Std Dev 3.084
Gaus mean : 27.5946
Gaus Width: 3.0747
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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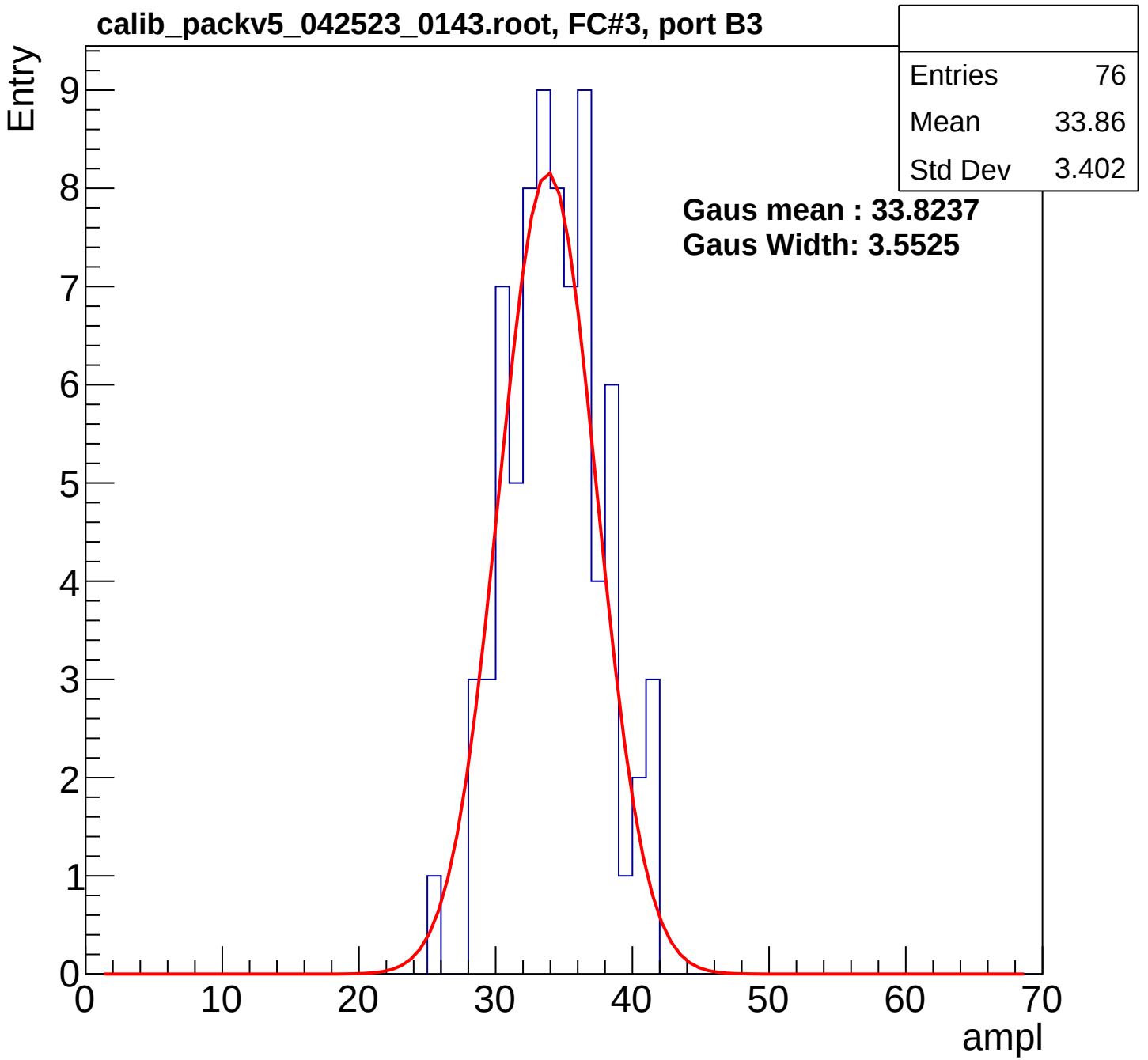
B1L000S, U3-ch82, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 33.86
Std Dev 3.402
Gaus mean : 33.8237
Gaus Width: 3.5525
Entry
ampl
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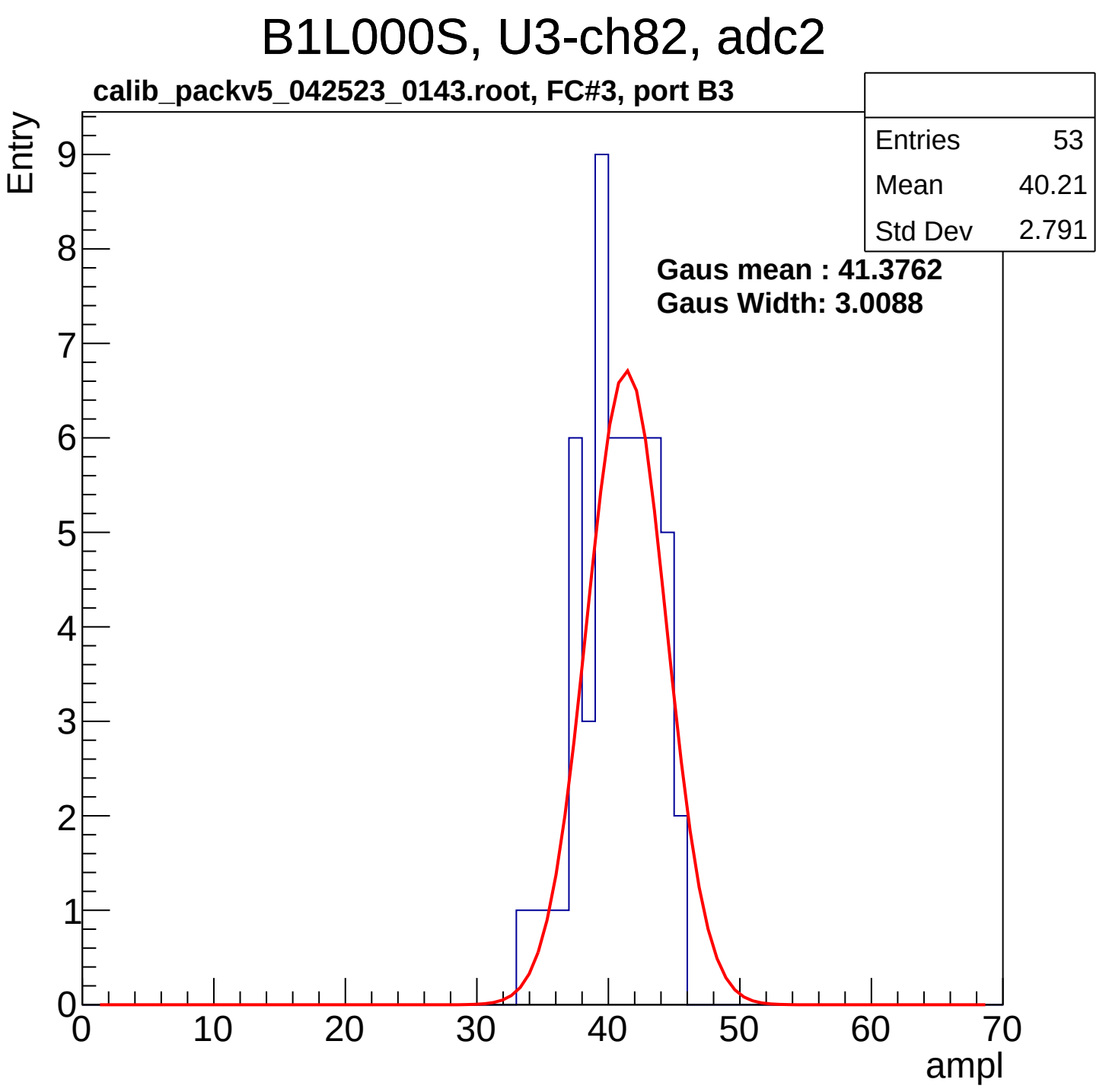
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch82, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 40.21
Std Dev 2.791
Gaus mean : 41.3762
Gaus Width: 3.0088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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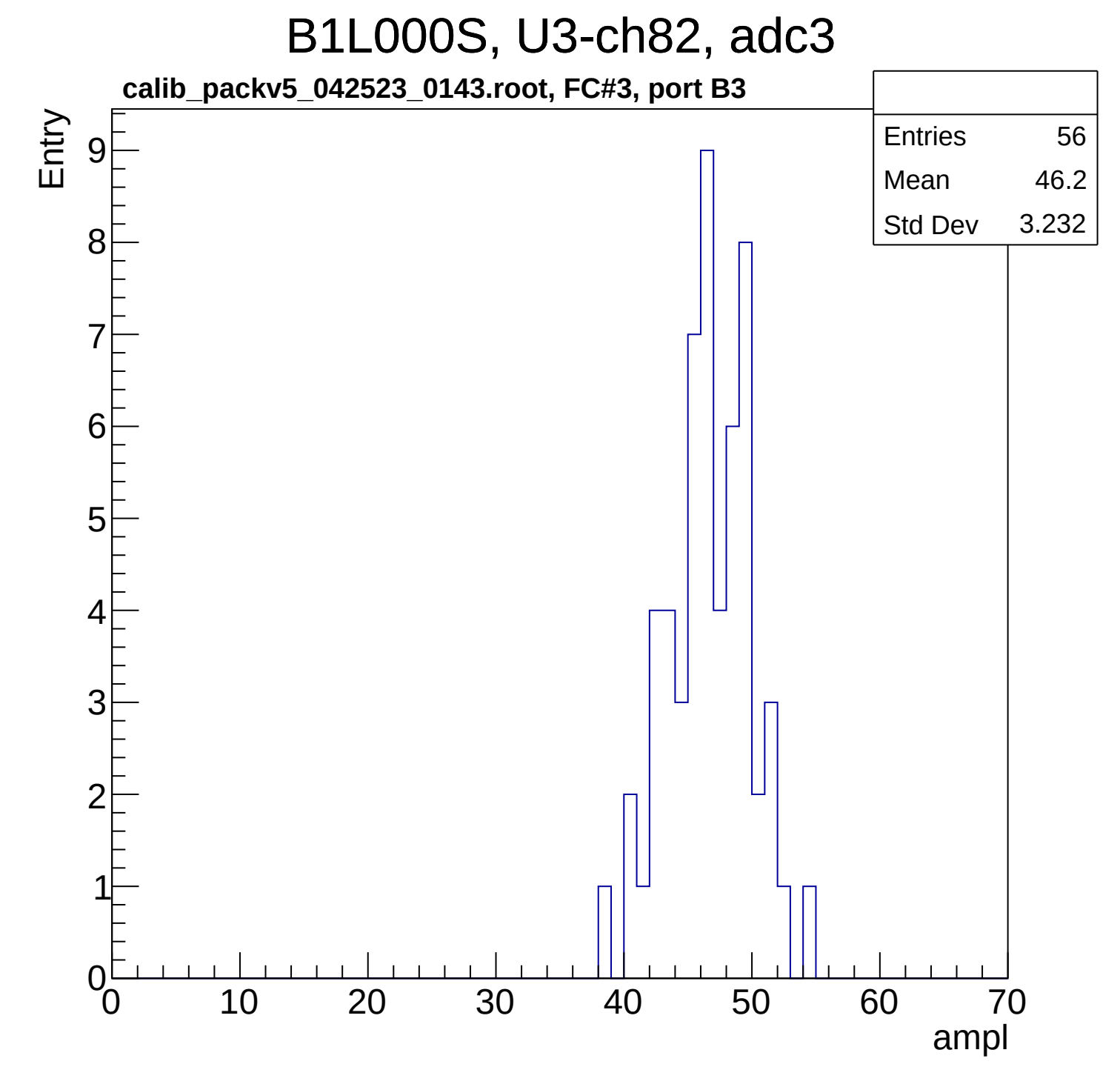
B1L000S, U3-ch82, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 46.2
Std Dev 3.232
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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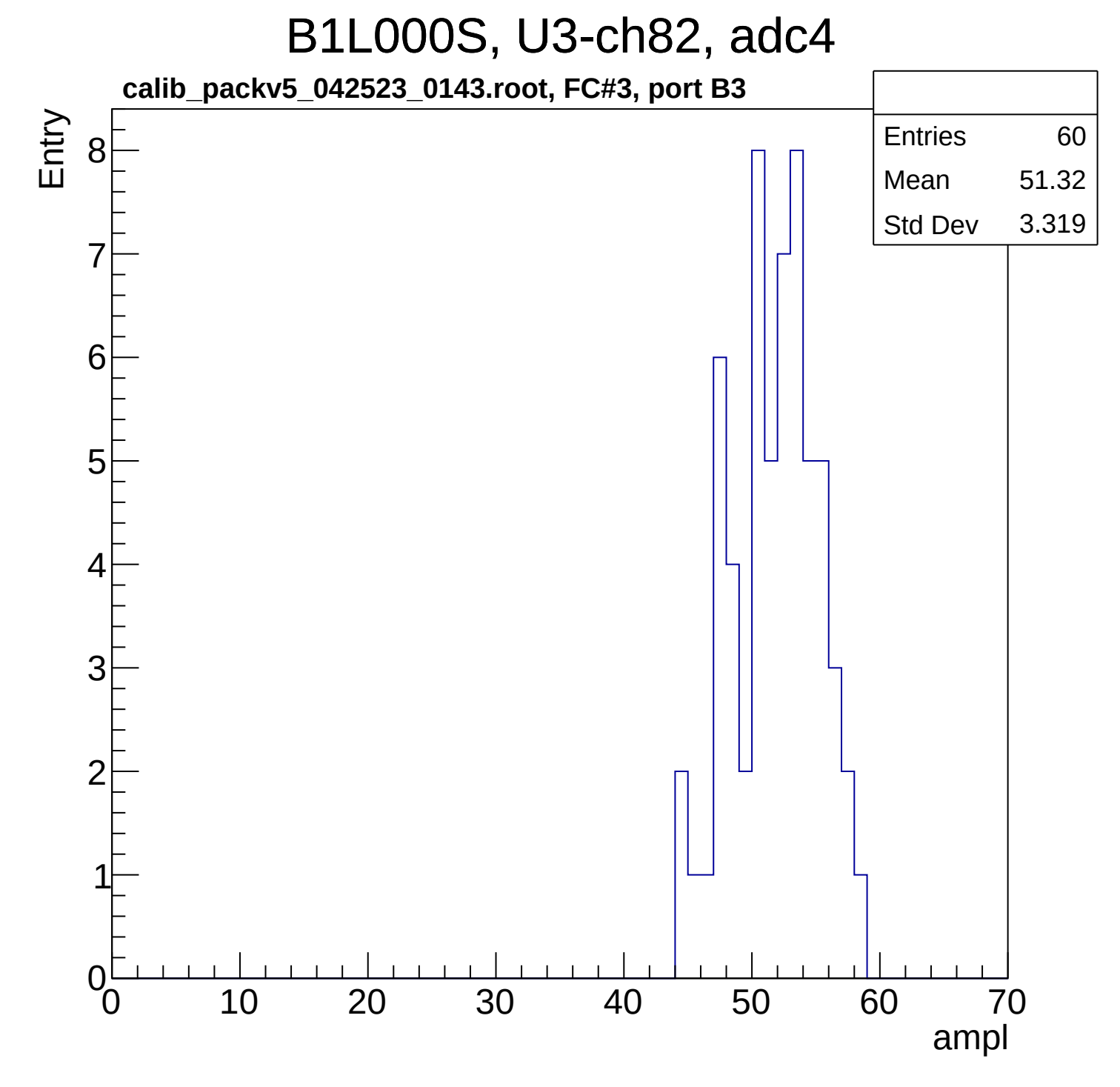
B1L000S, U3-ch82, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 51.32
Std Dev 3.319
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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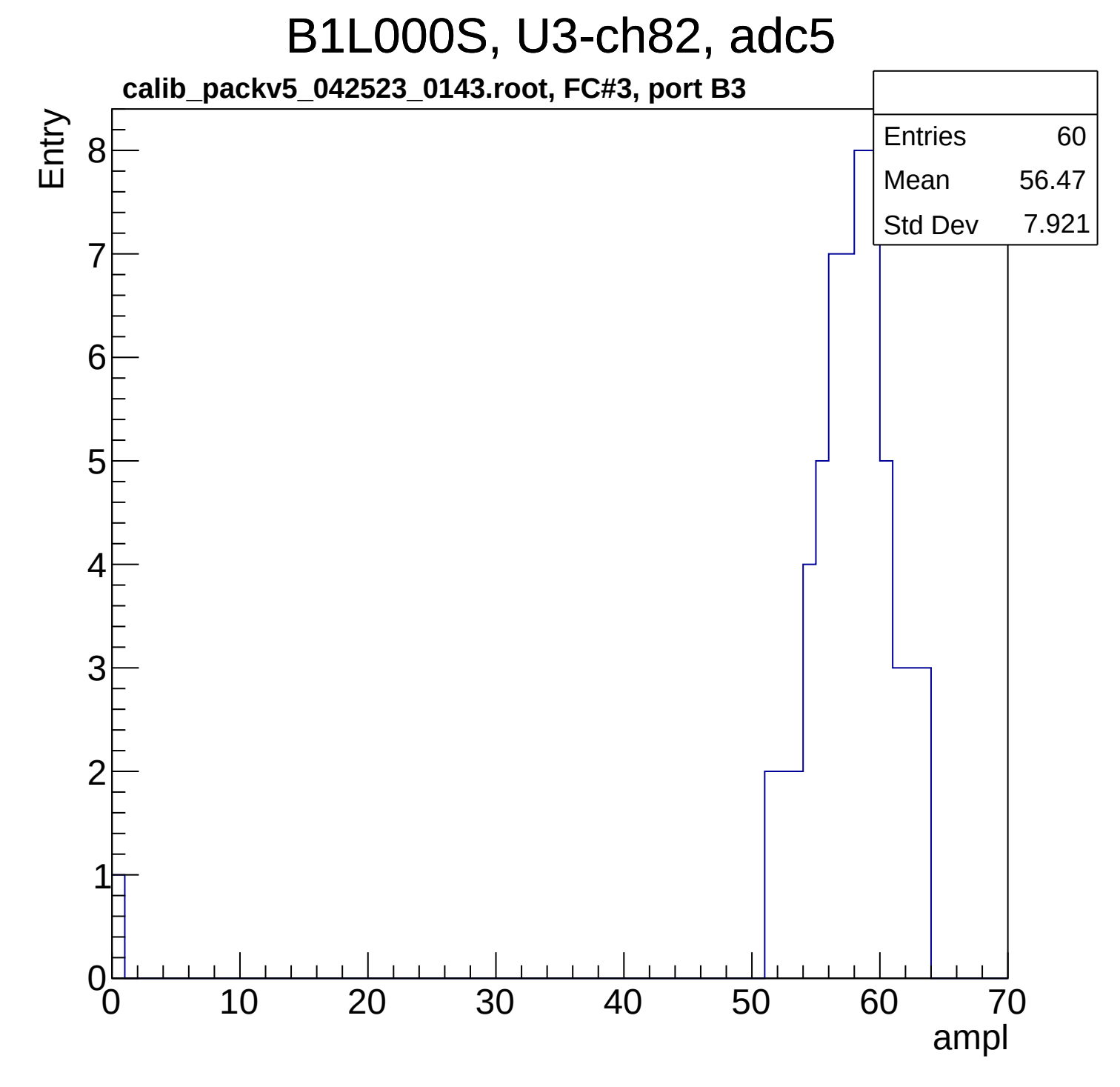
B1L000S, U3-ch82, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 56.47
Std Dev 7.921
Entry
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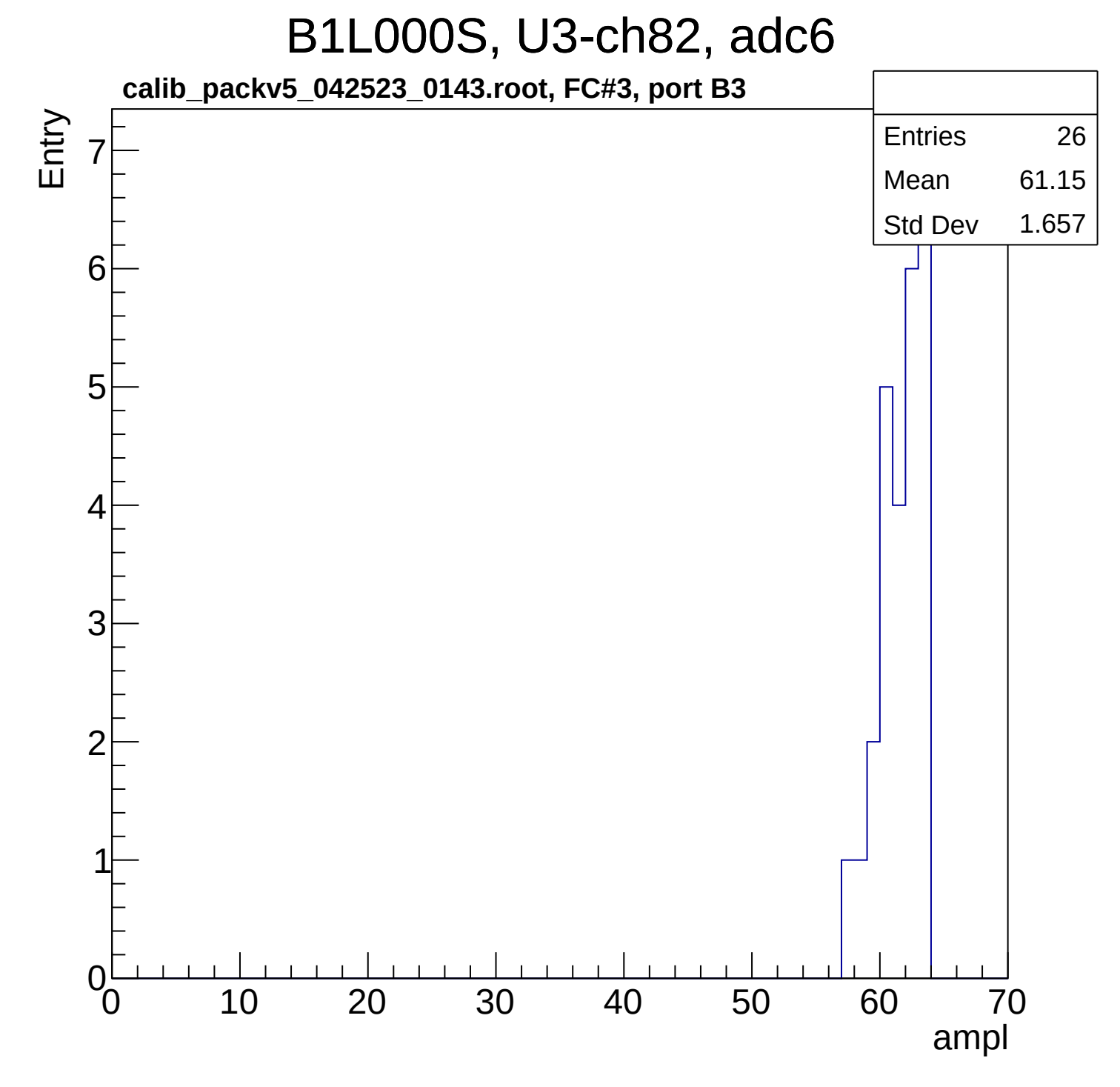
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch82, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 61.15
Std Dev 1.657
Entry
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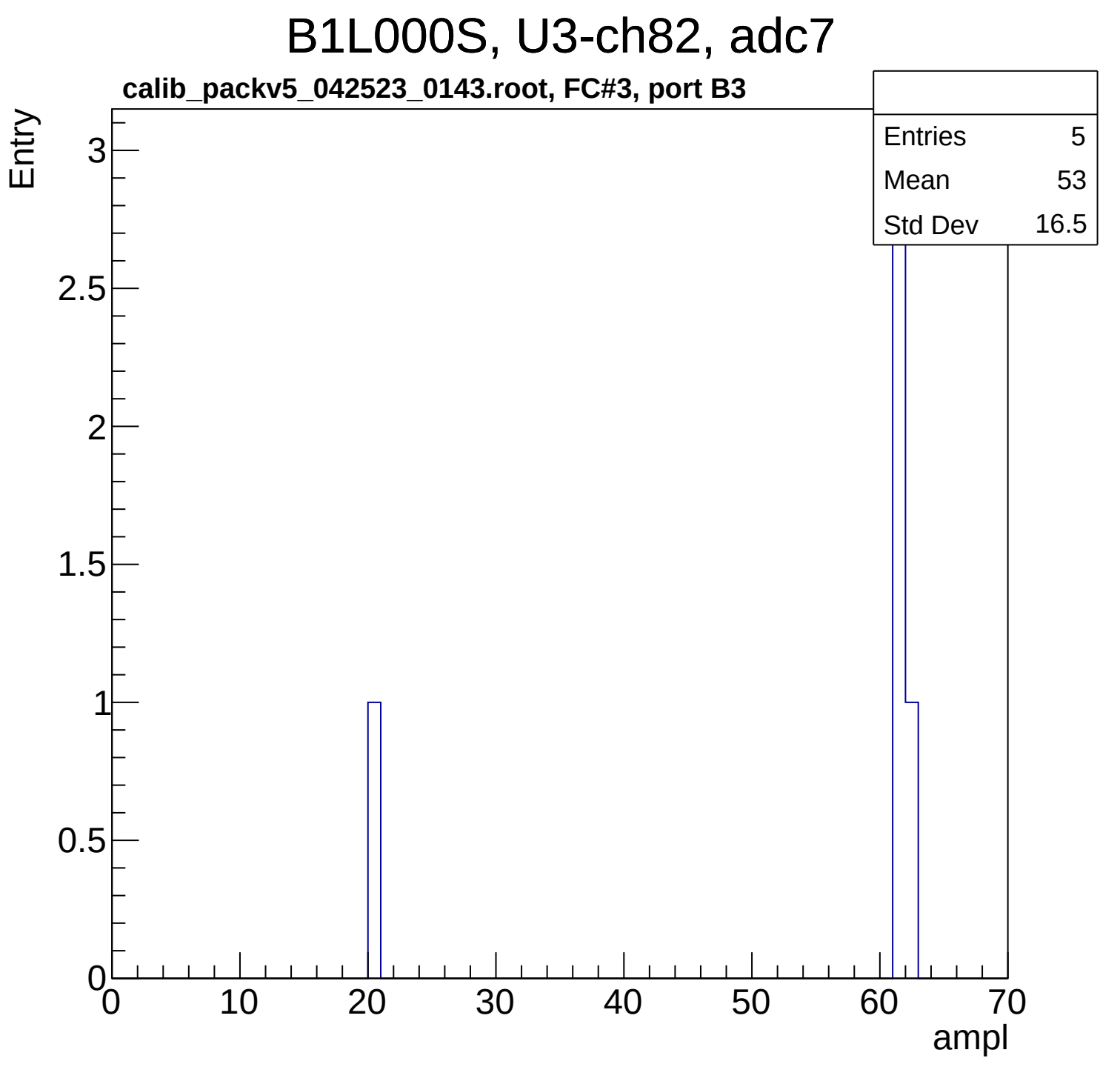
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch82, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 53
Std Dev 16.5
Entry
ampl
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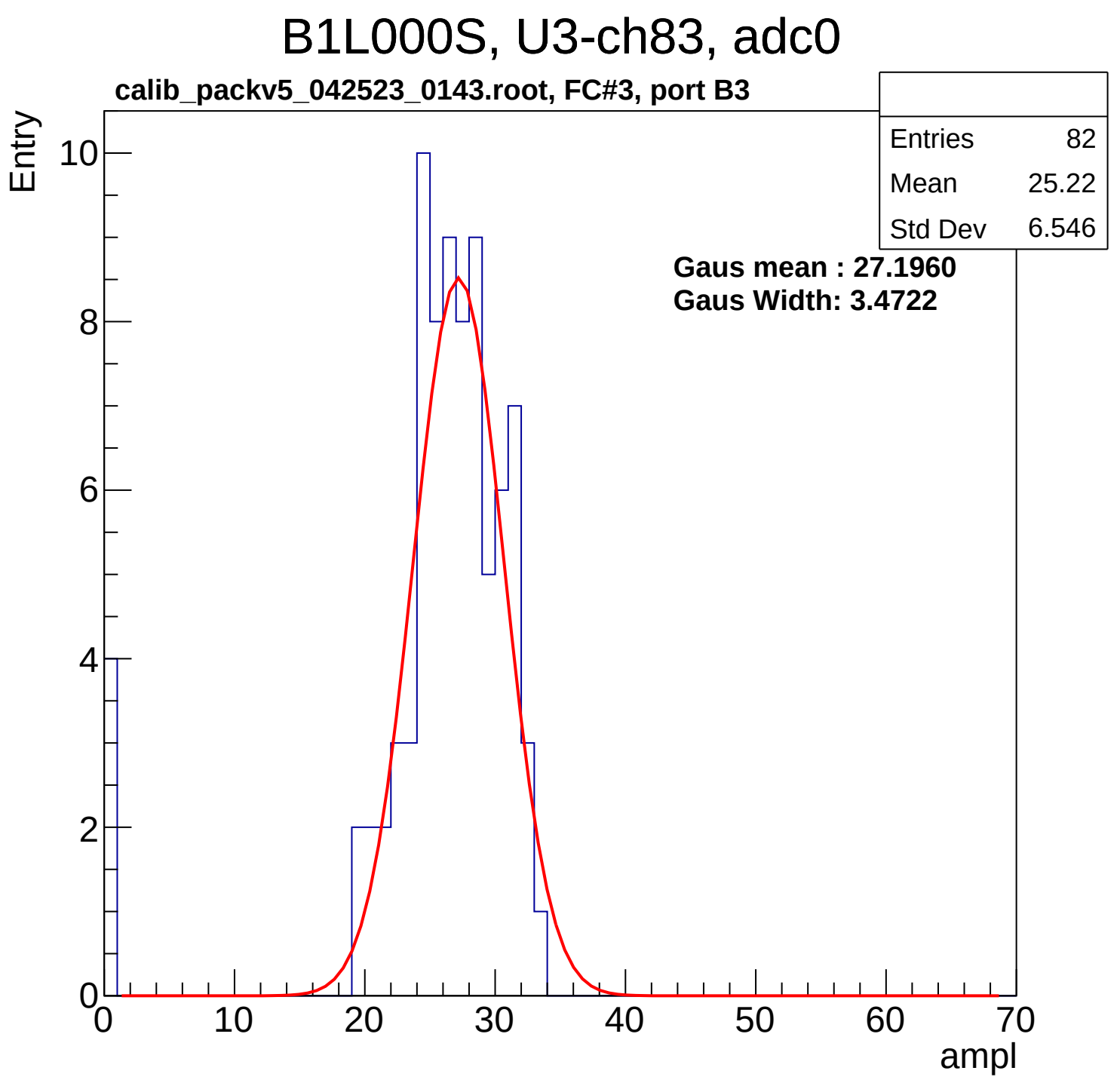
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch83, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 25.22
Std Dev 6.546
Gaus mean : 27.1960
Gaus Width: 3.4722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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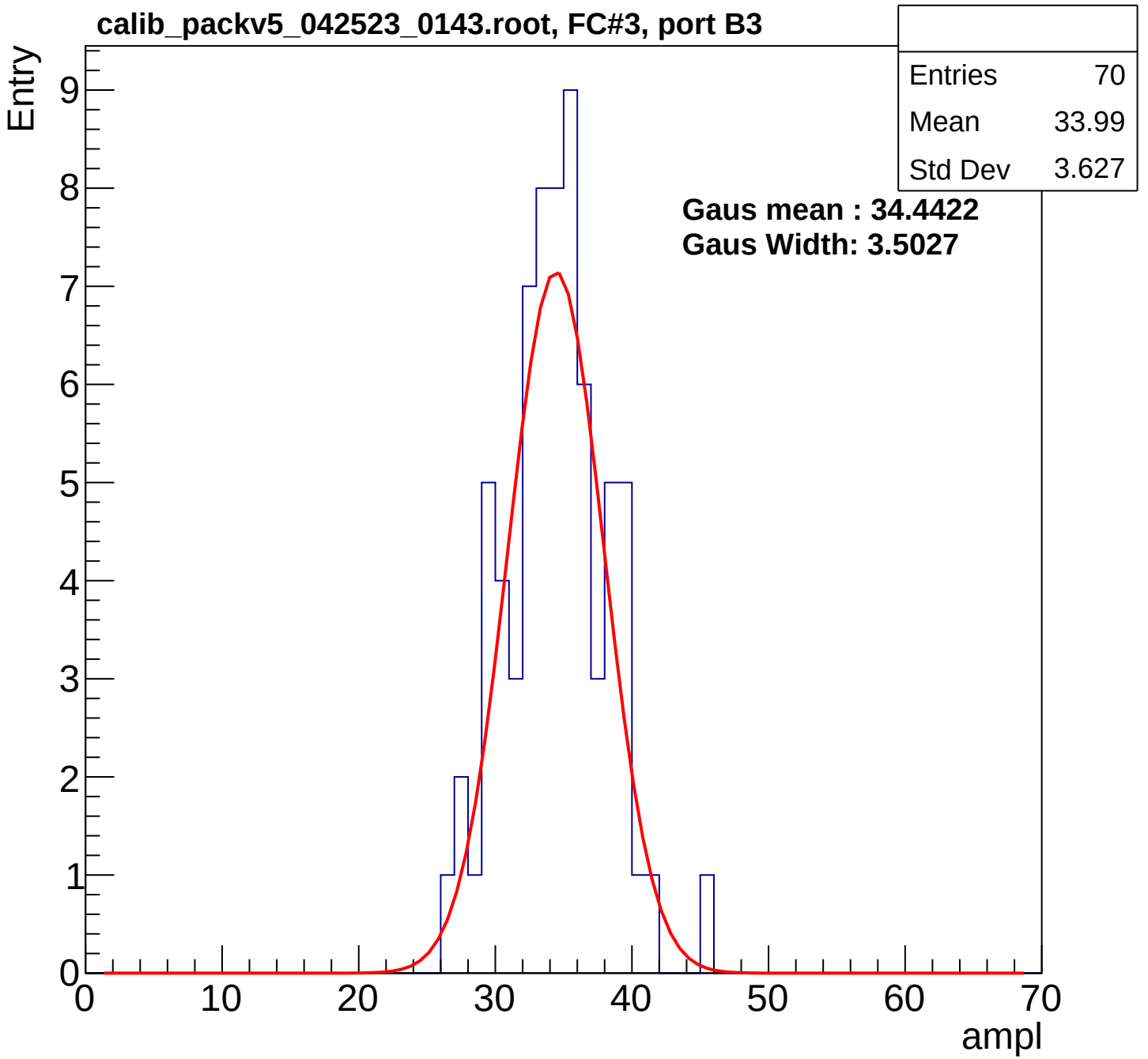
B1L000S, U3-ch83, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 33.99
Std Dev 3.627
Gaus mean : 34.4422
Gaus Width: 3.5027
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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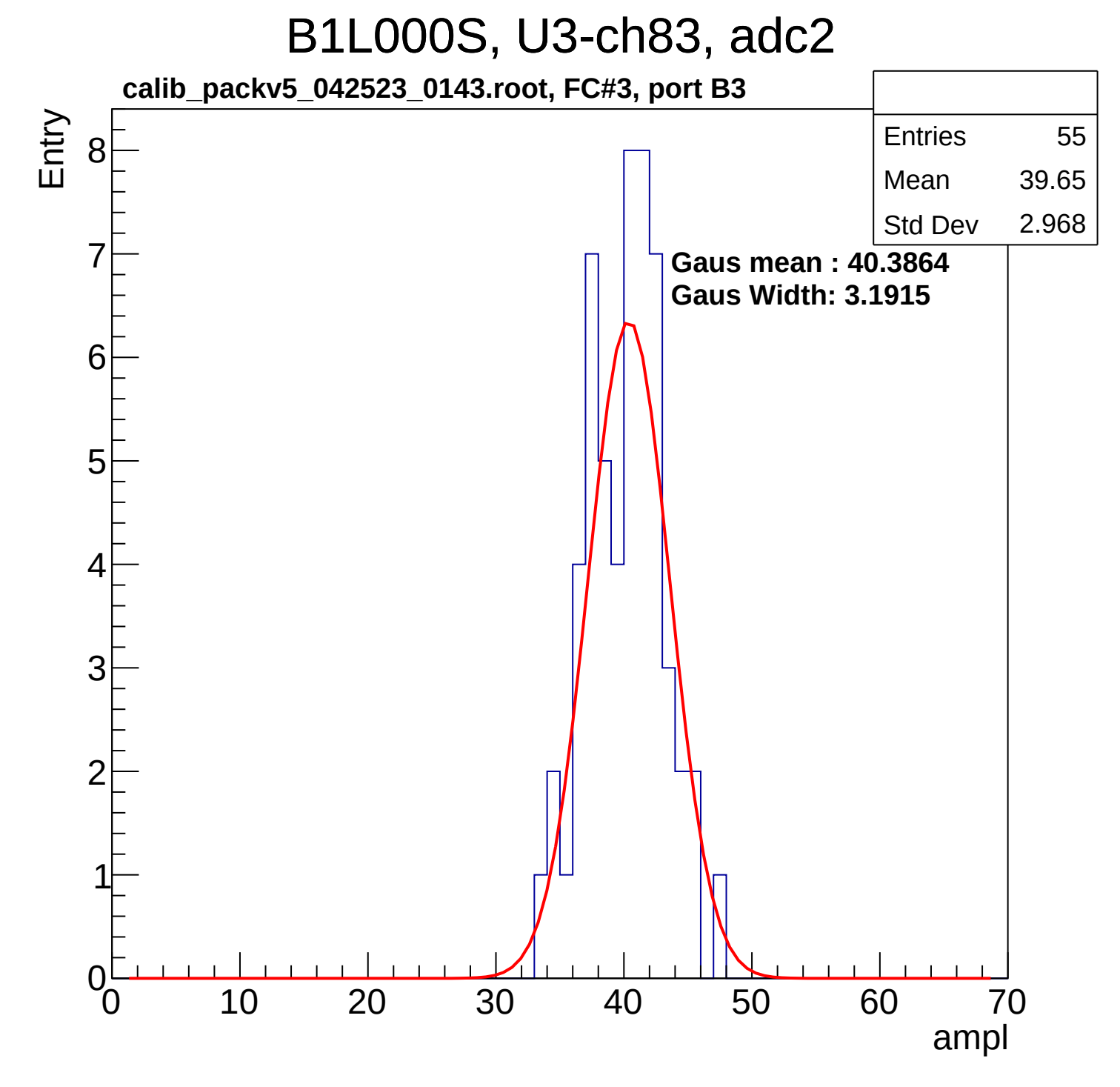
B1L000S, U3-ch83, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 39.65
Std Dev 2.968
Gaus mean : 40.3864
Gaus Width: 3.1915
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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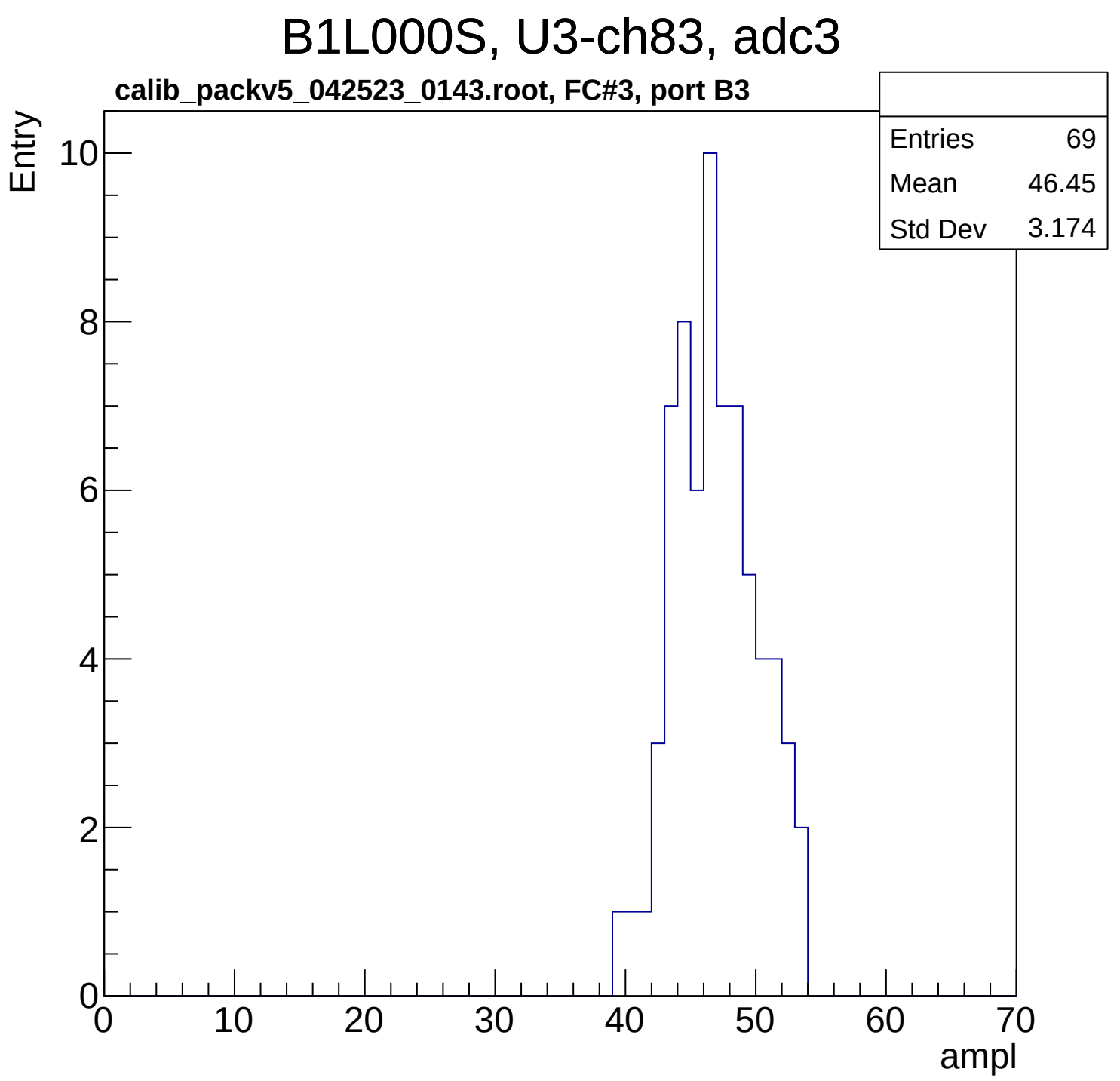
B1L000S, U3-ch83, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 46.45
Std Dev 3.174
Entry
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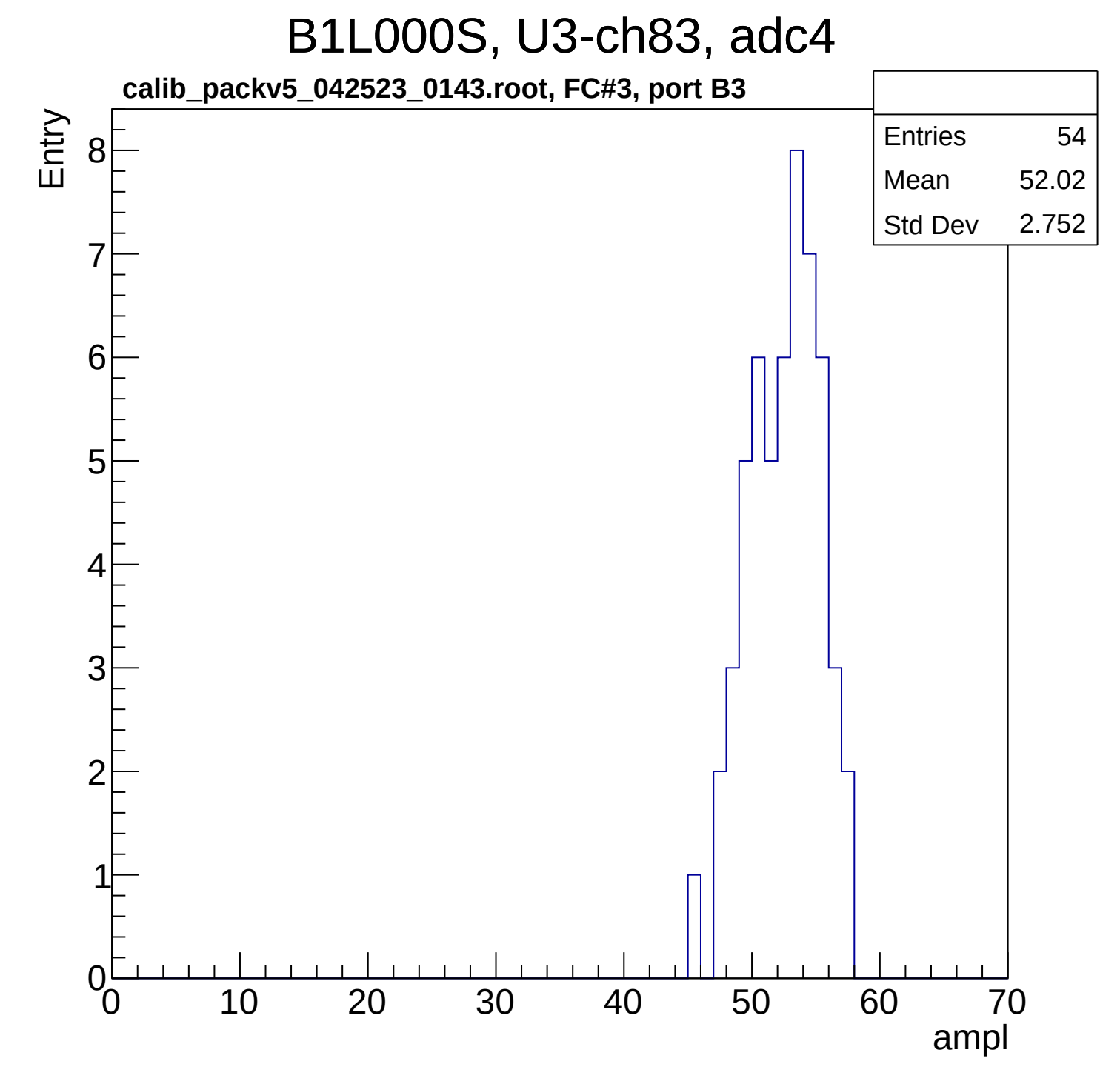
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch83, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 52.02
Std Dev 2.752
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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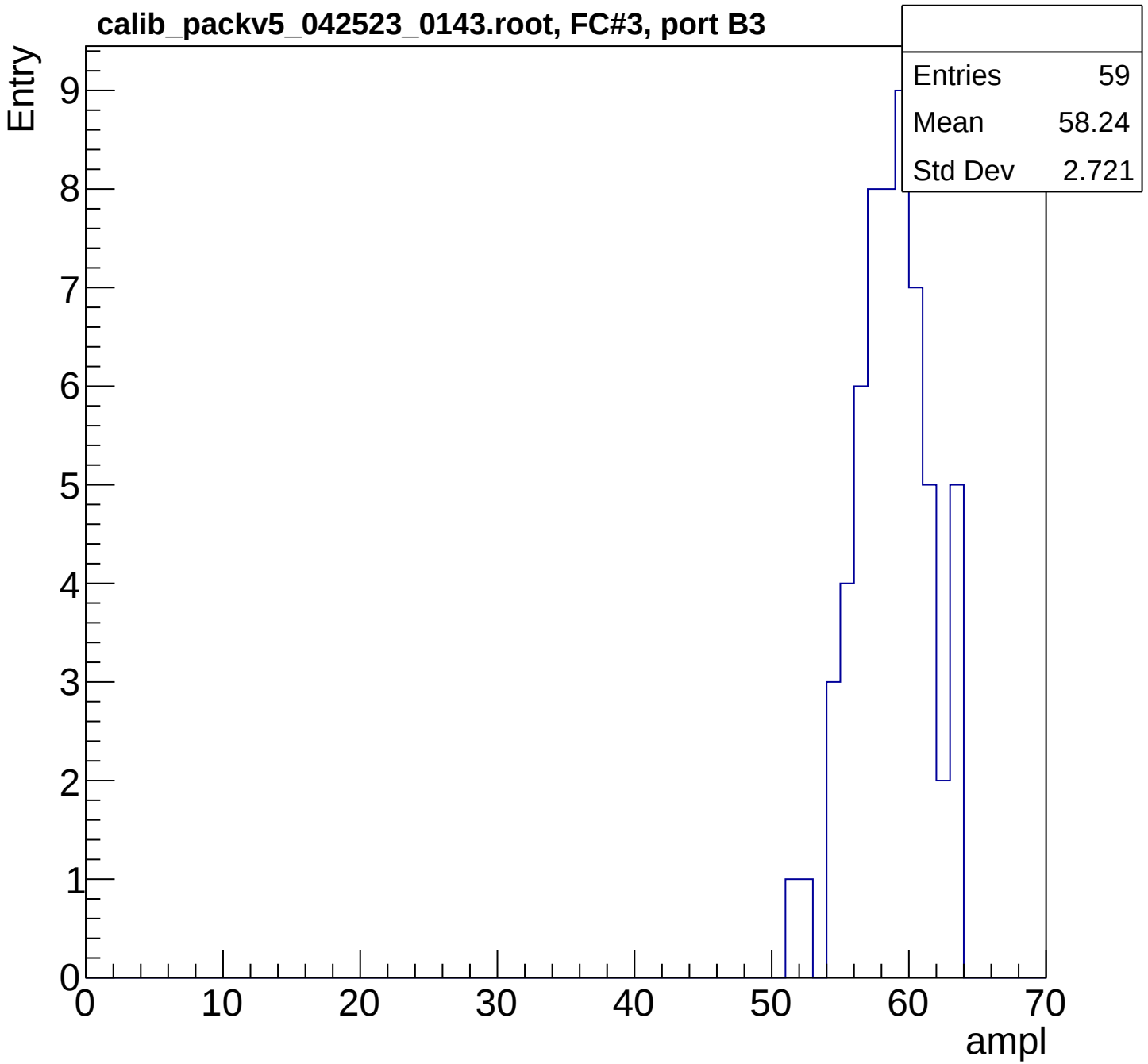
B1L000S, U3-ch83, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.24
Std Dev 2.721
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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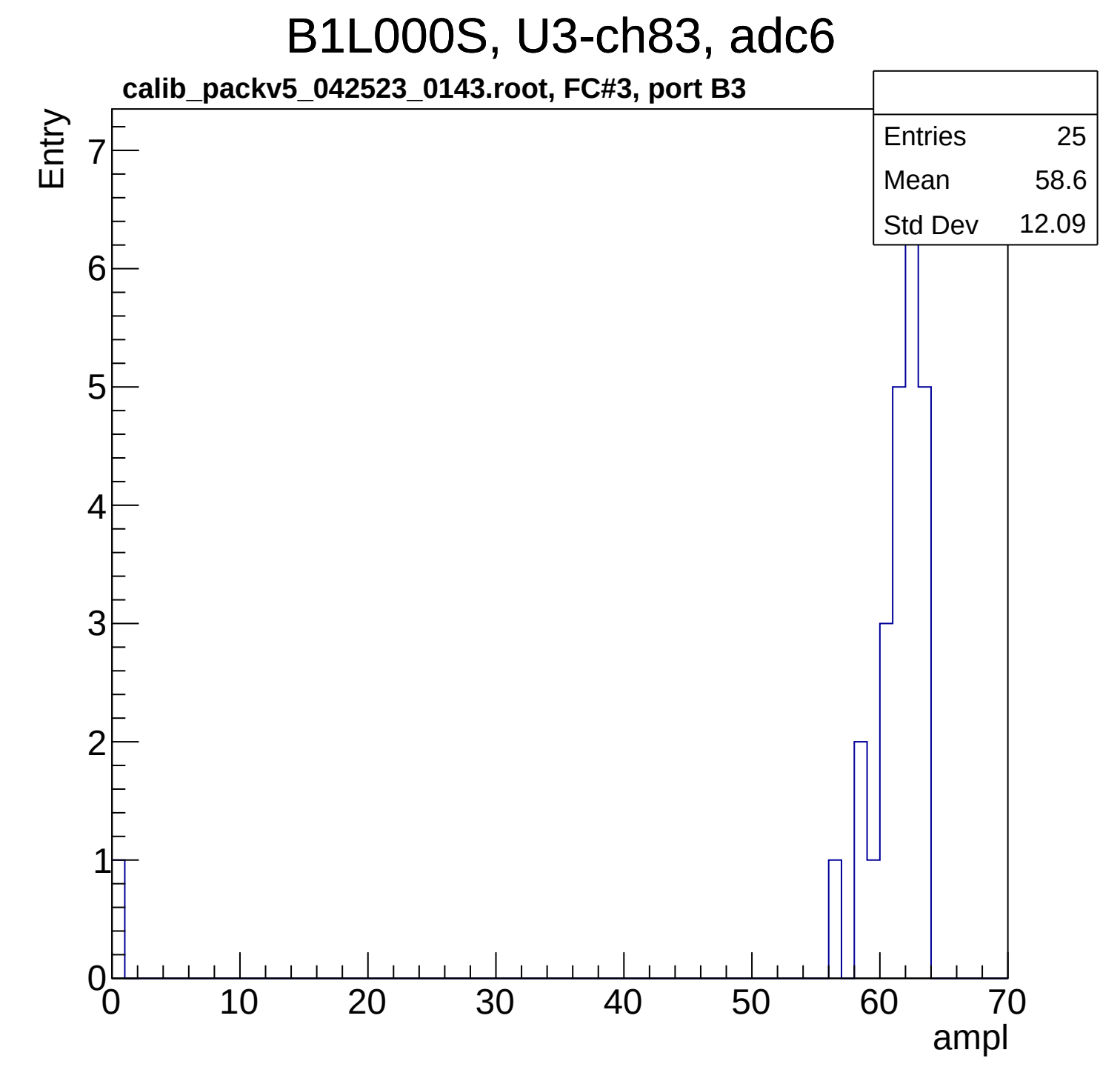
B1L000S, U3-ch83, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 58.6
Std Dev 12.09
Entry
ampl
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3
4
5
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0
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40
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60
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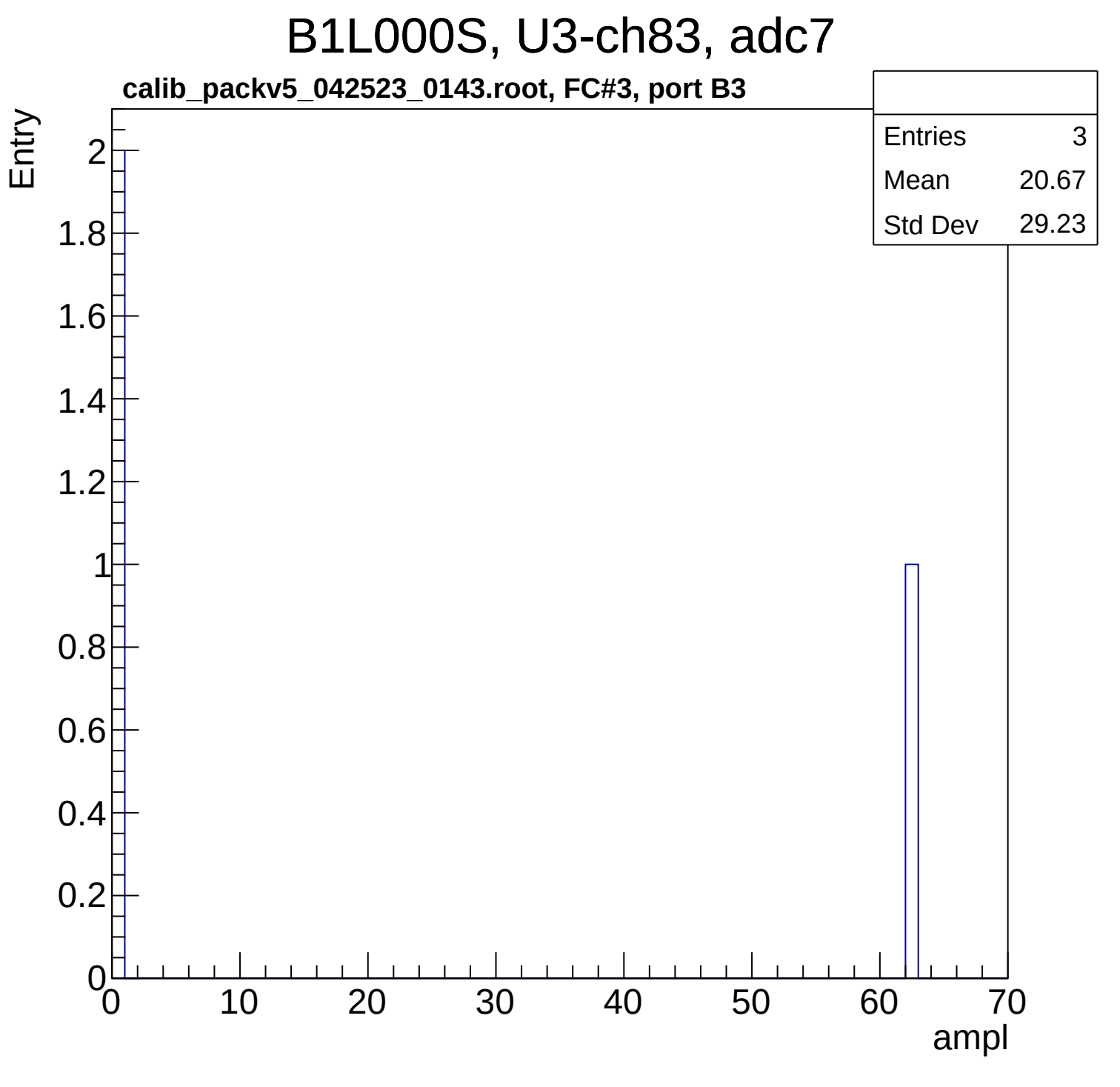
B1L000S, U3-ch83, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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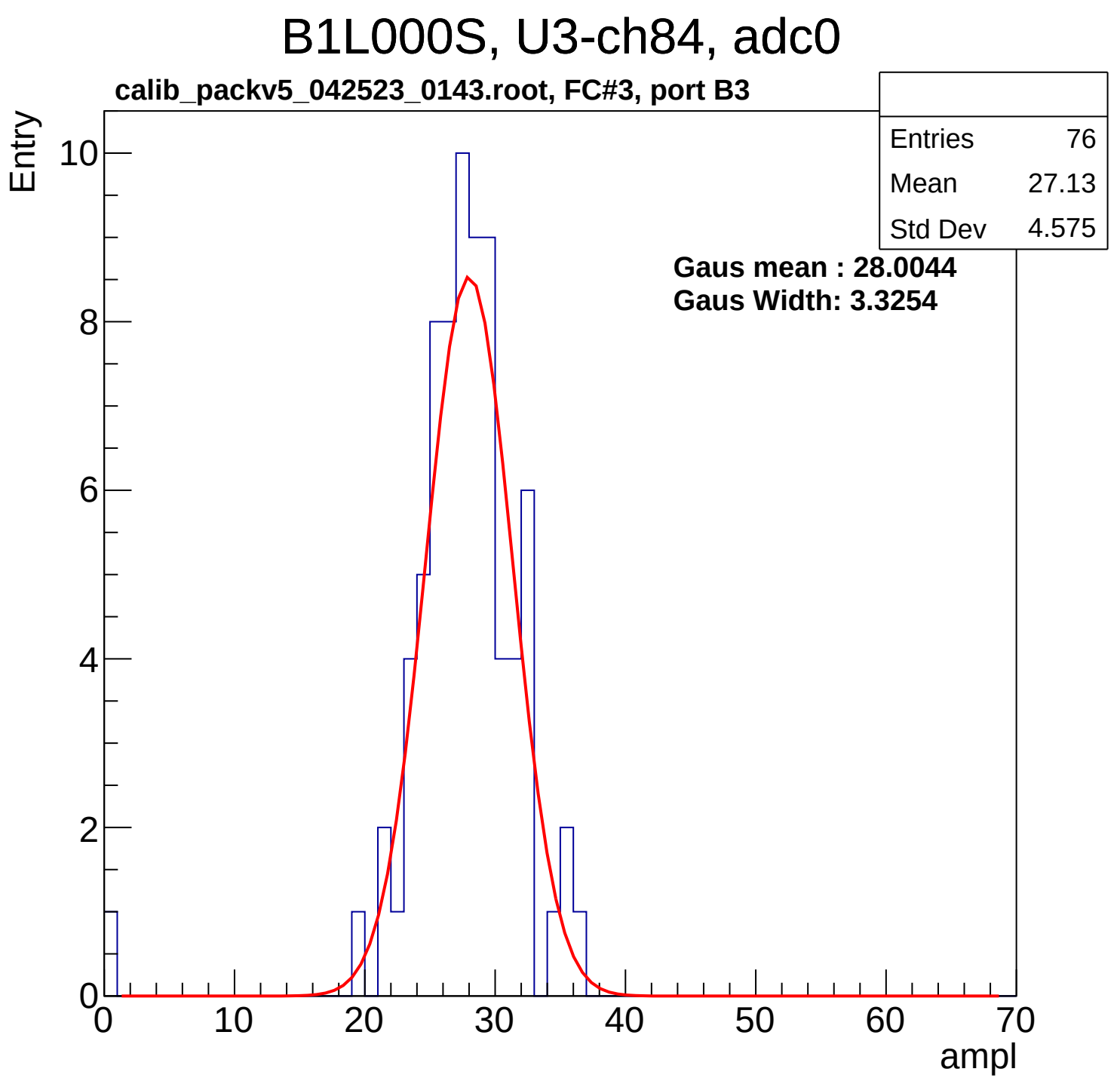
B1L000S, U3-ch84, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 27.13
Std Dev 4.575
Gaus mean : 28.0044
Gaus Width: 3.3254
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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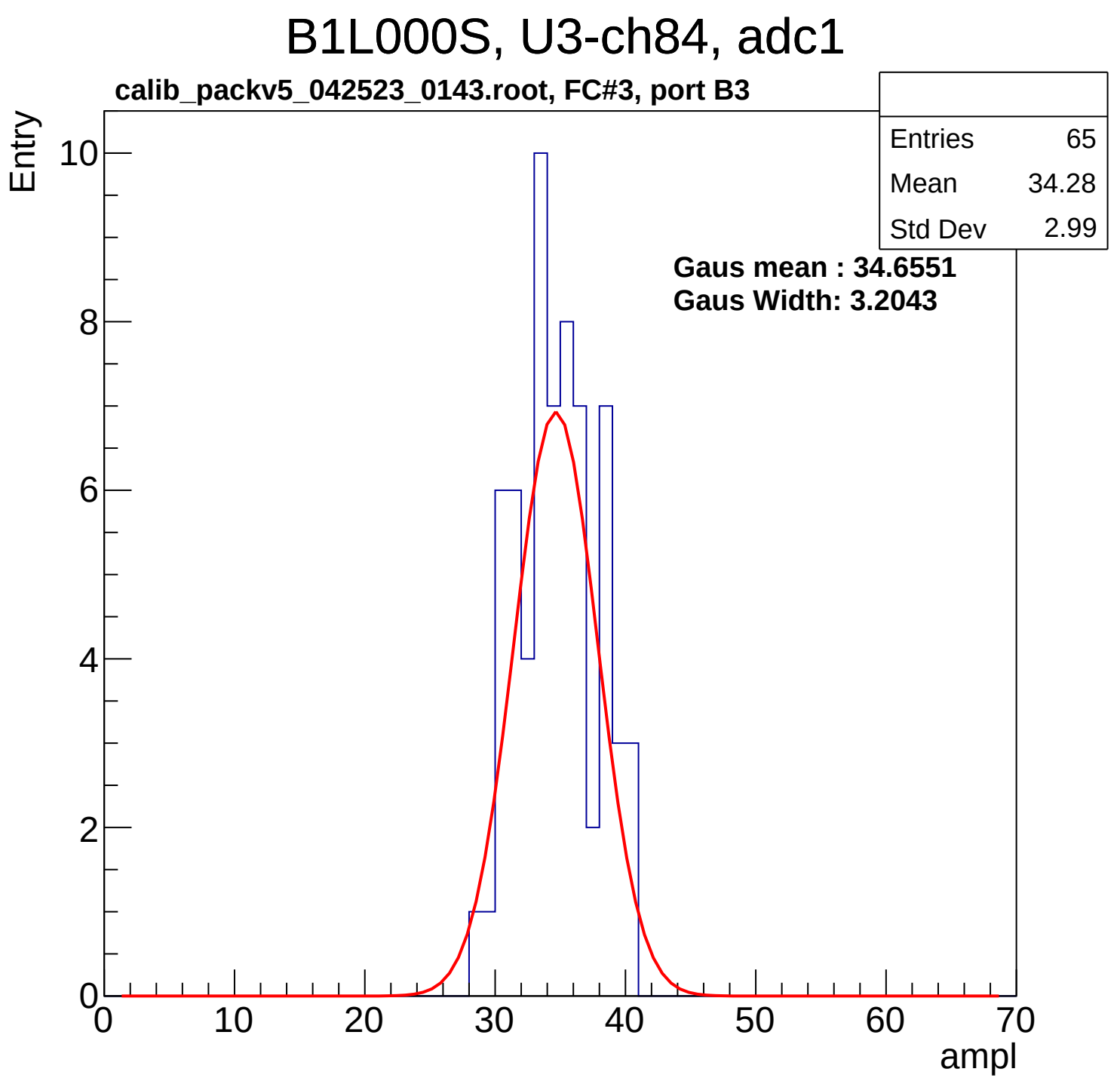
B1L000S, U3-ch84, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 34.28
Std Dev 2.99
Gaus mean : 34.6551
Gaus Width: 3.2043
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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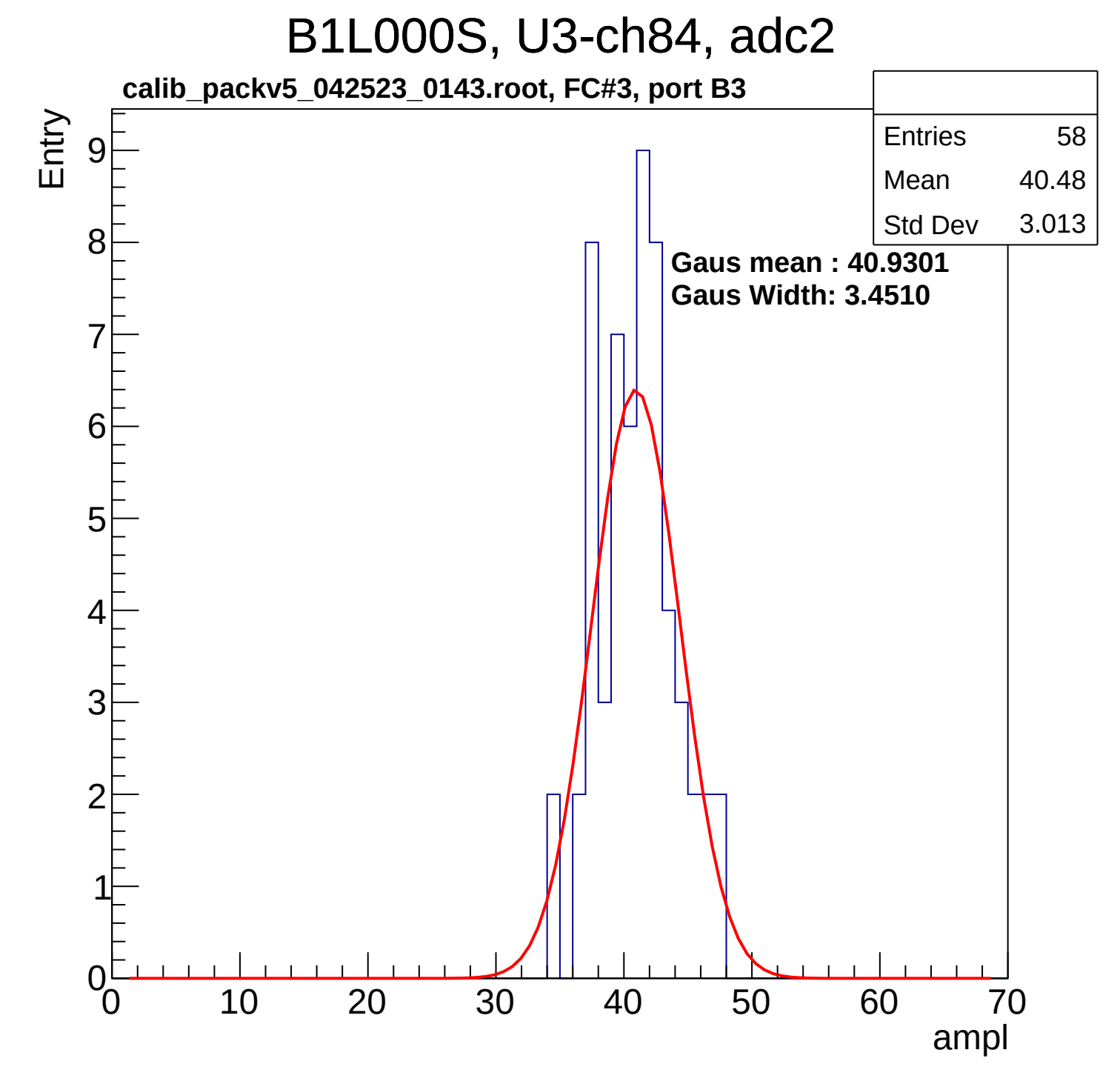
B1L000S, U3-ch84, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 40.48
Std Dev 3.013
Gaus mean : 40.9301
Gaus Width: 3.4510
Entry
ampl
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10
20
30
40
50
60
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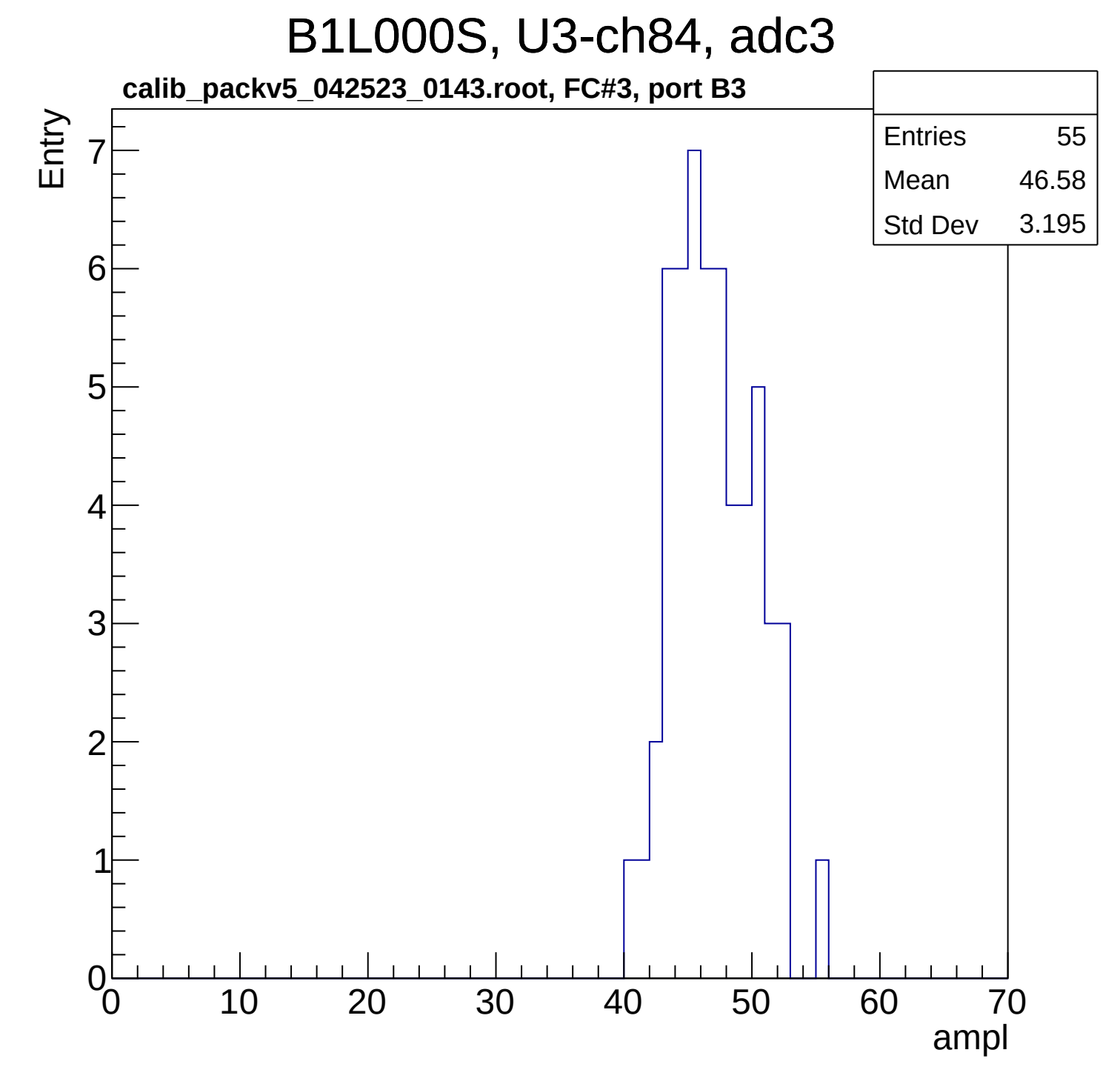
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch84, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 46.58
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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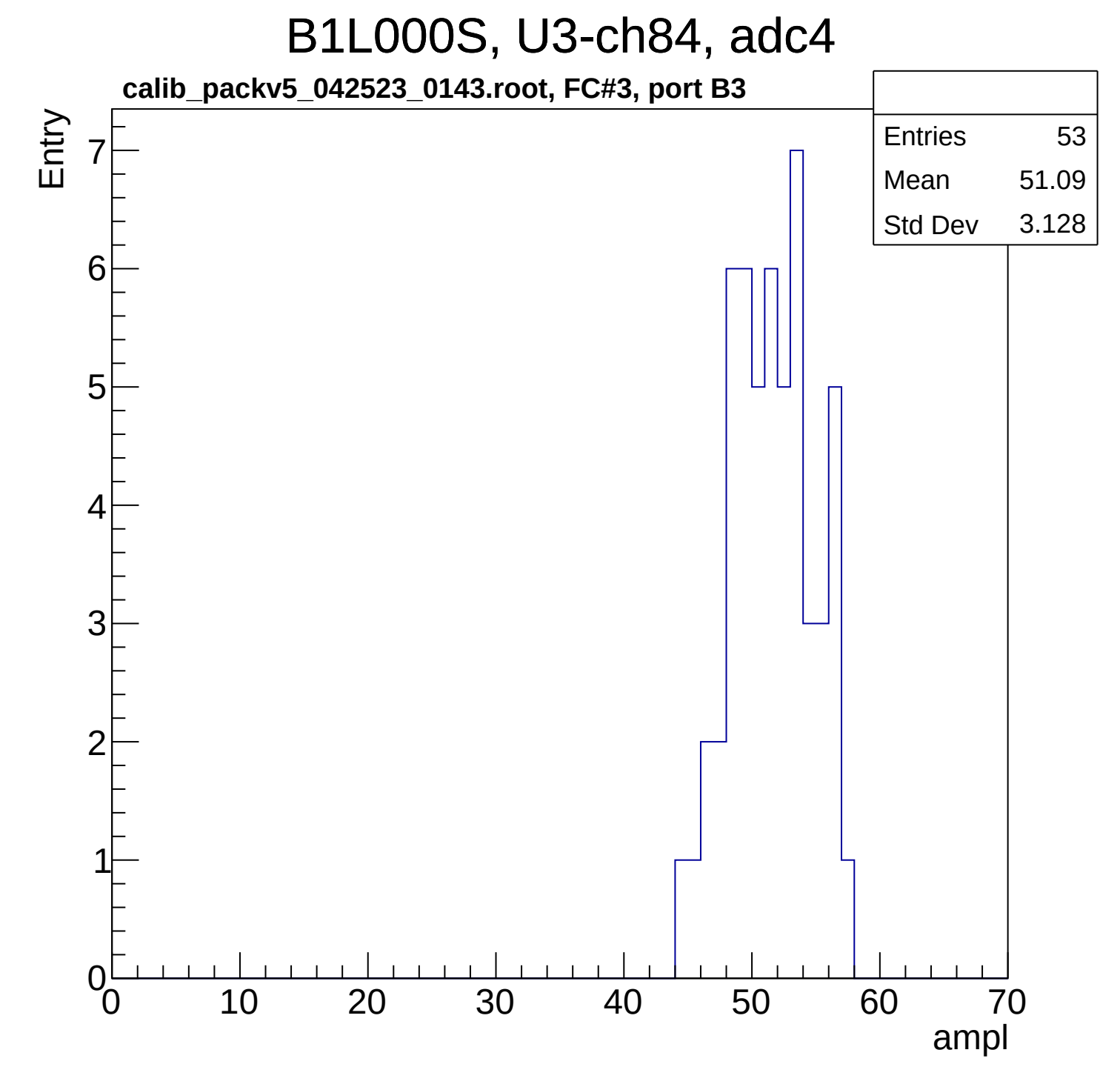
B1L000S, U3-ch84, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 51.09
Std Dev 3.128
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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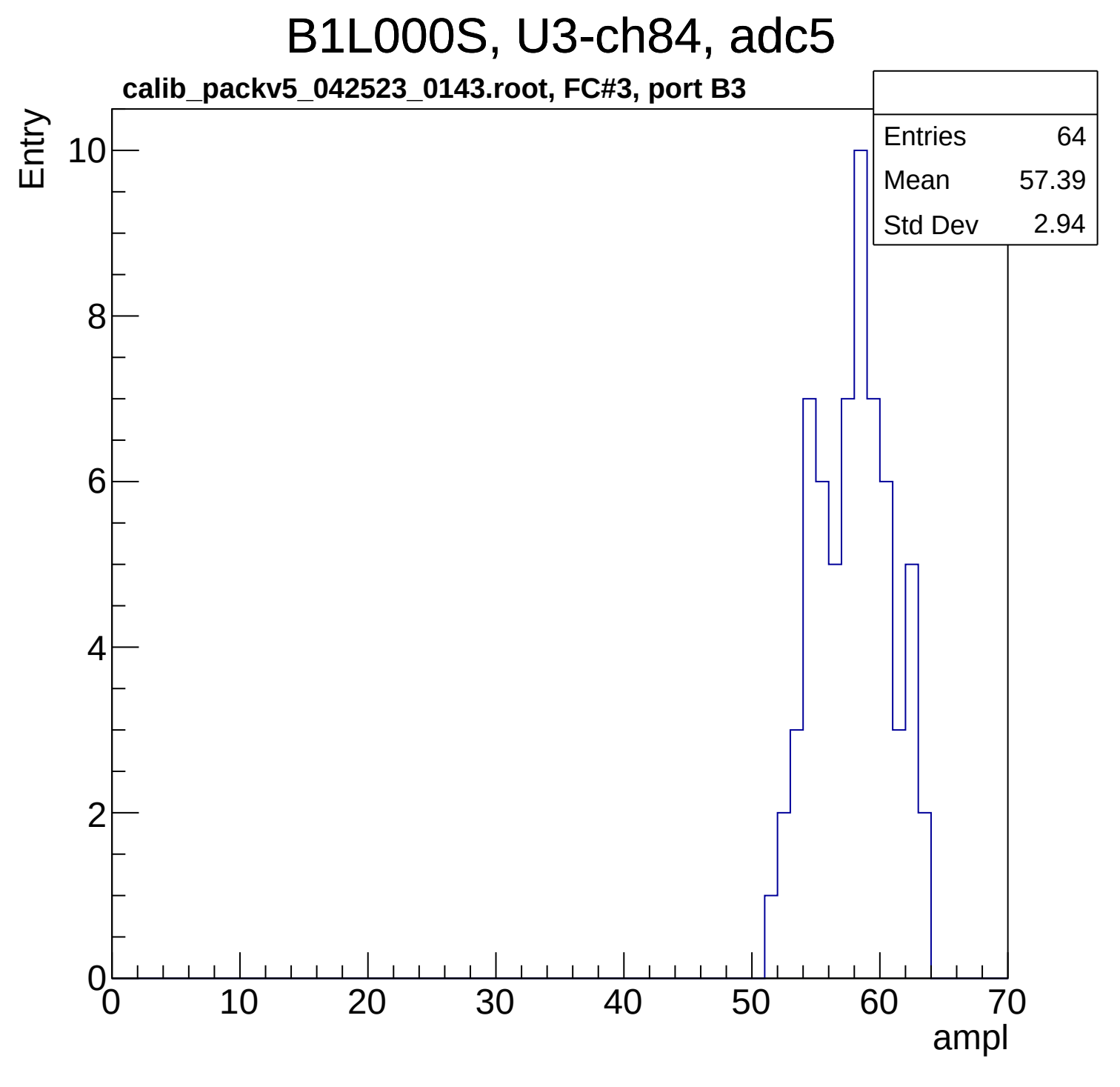
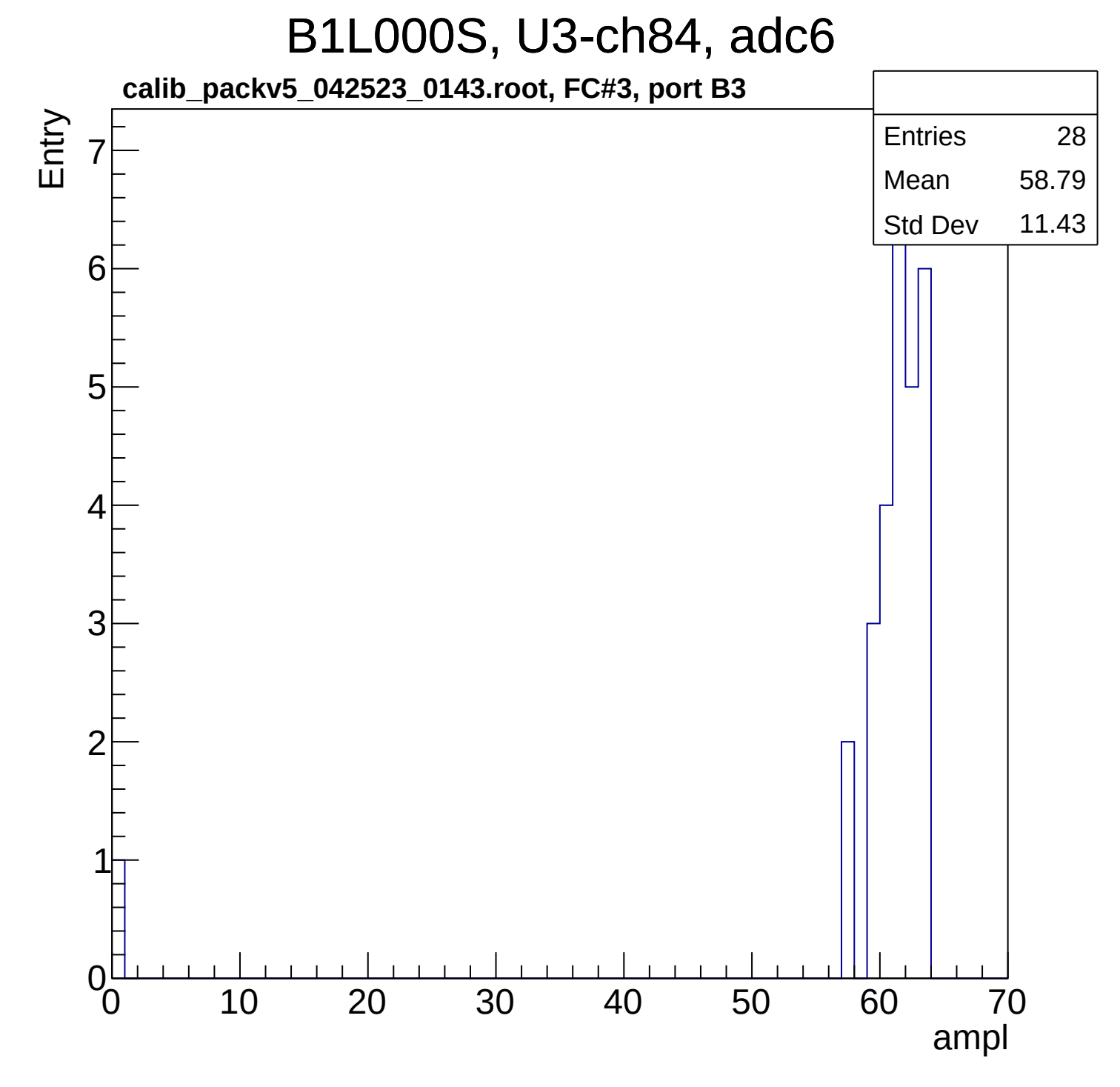

B1L000S, U3-ch84, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 57.39
Std Dev 2.94
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch84, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 28
Mean 58.79
Std Dev 11.43
Entry
ampl
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3
4
5
6
7
0
10
20
30
40
50
60
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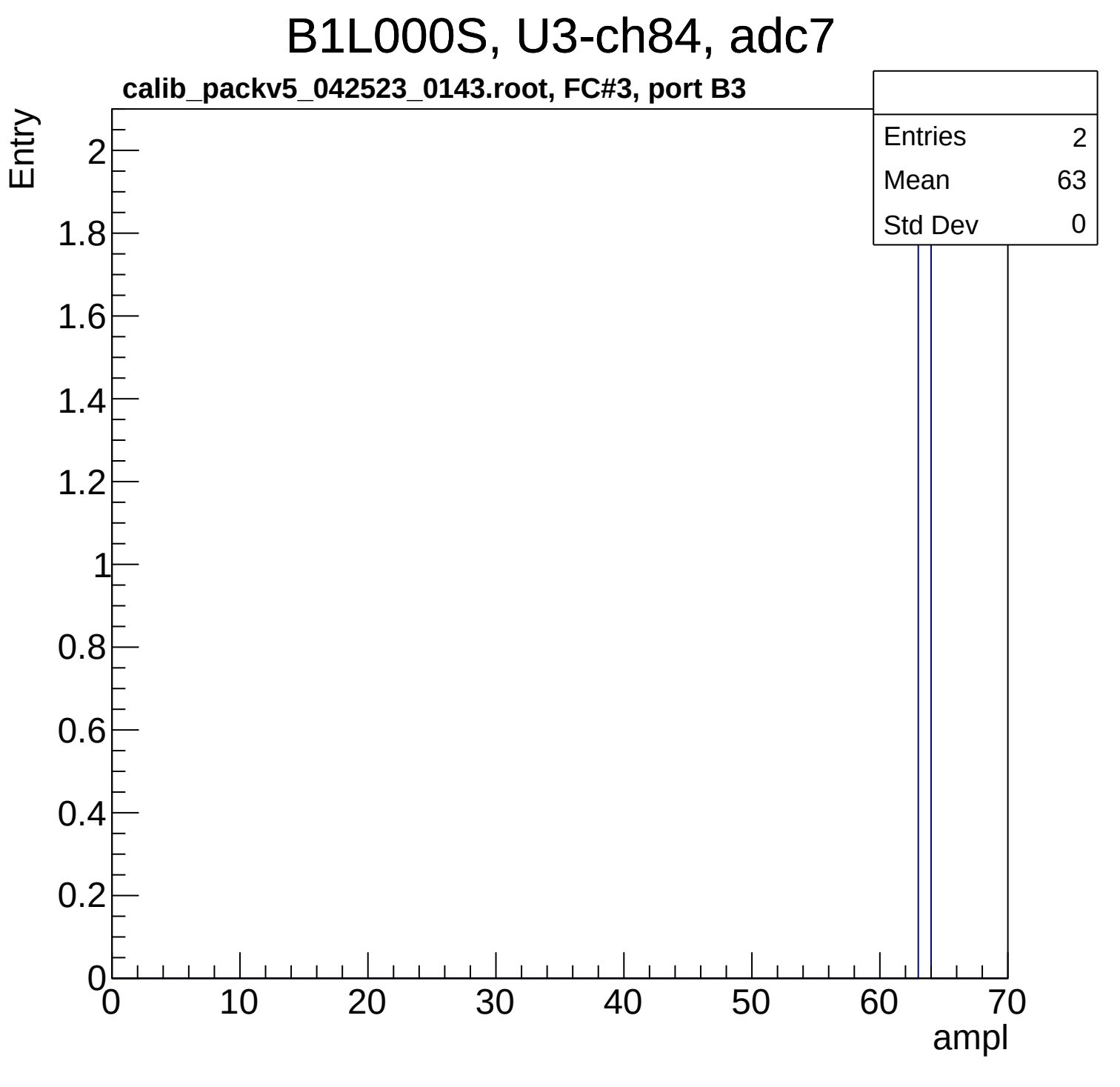
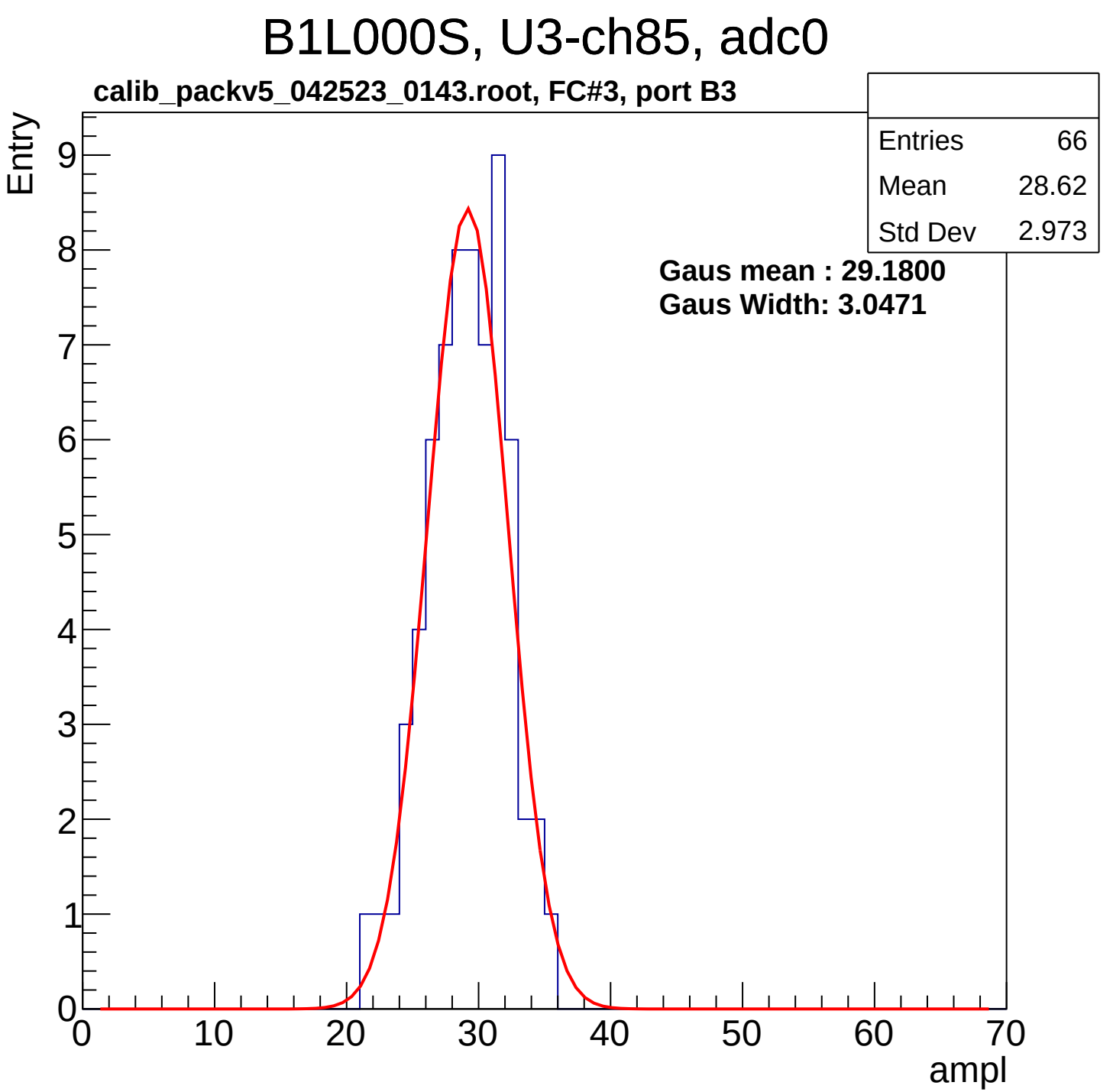

B1L000S, U3-ch84, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch85, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.62
Std Dev 2.973
Gaus mean : 29.1800
Gaus Width: 3.0471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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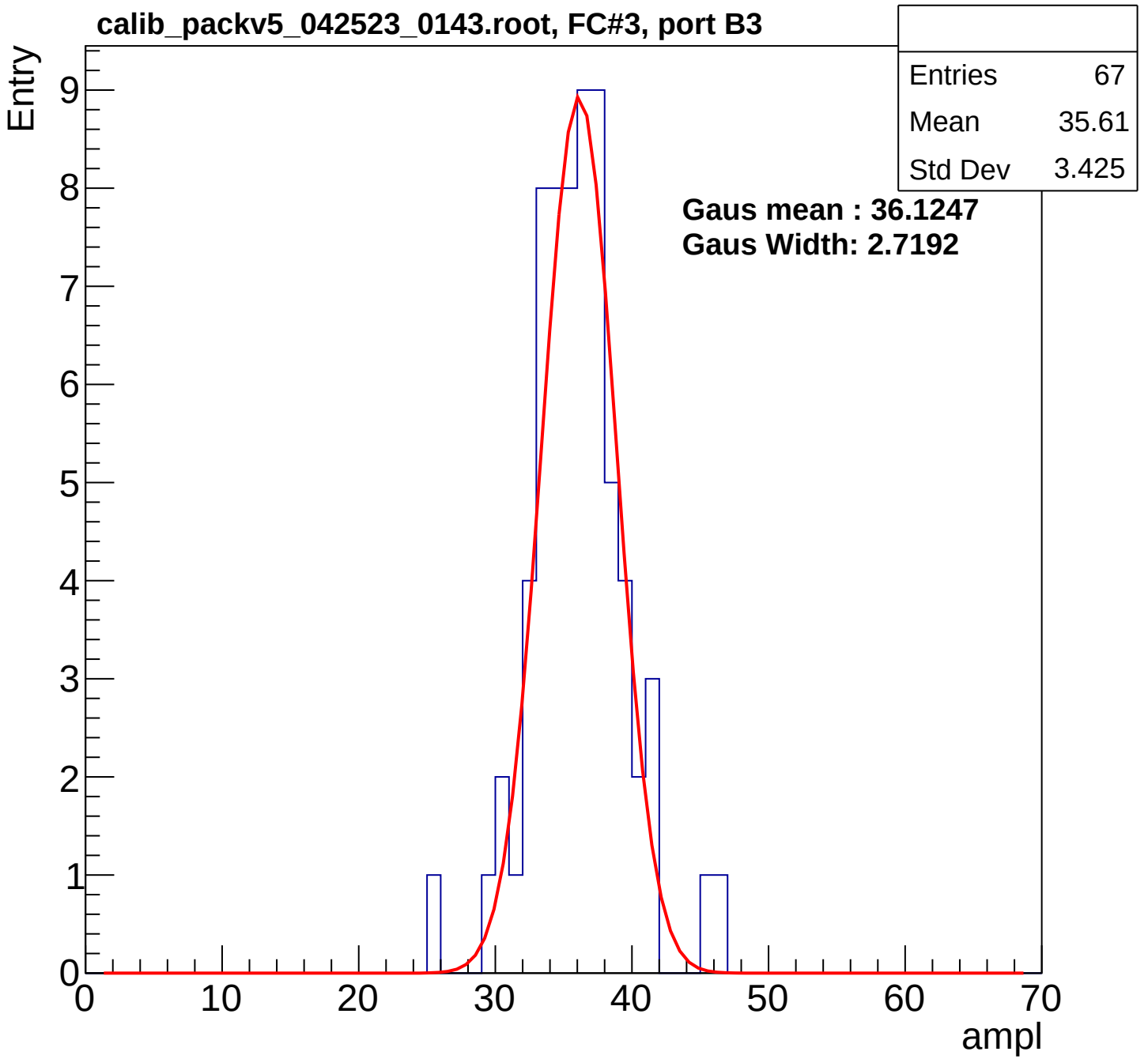
B1L000S, U3-ch85, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.61
Std Dev 3.425
Gaus mean : 36.1247
Gaus Width: 2.7192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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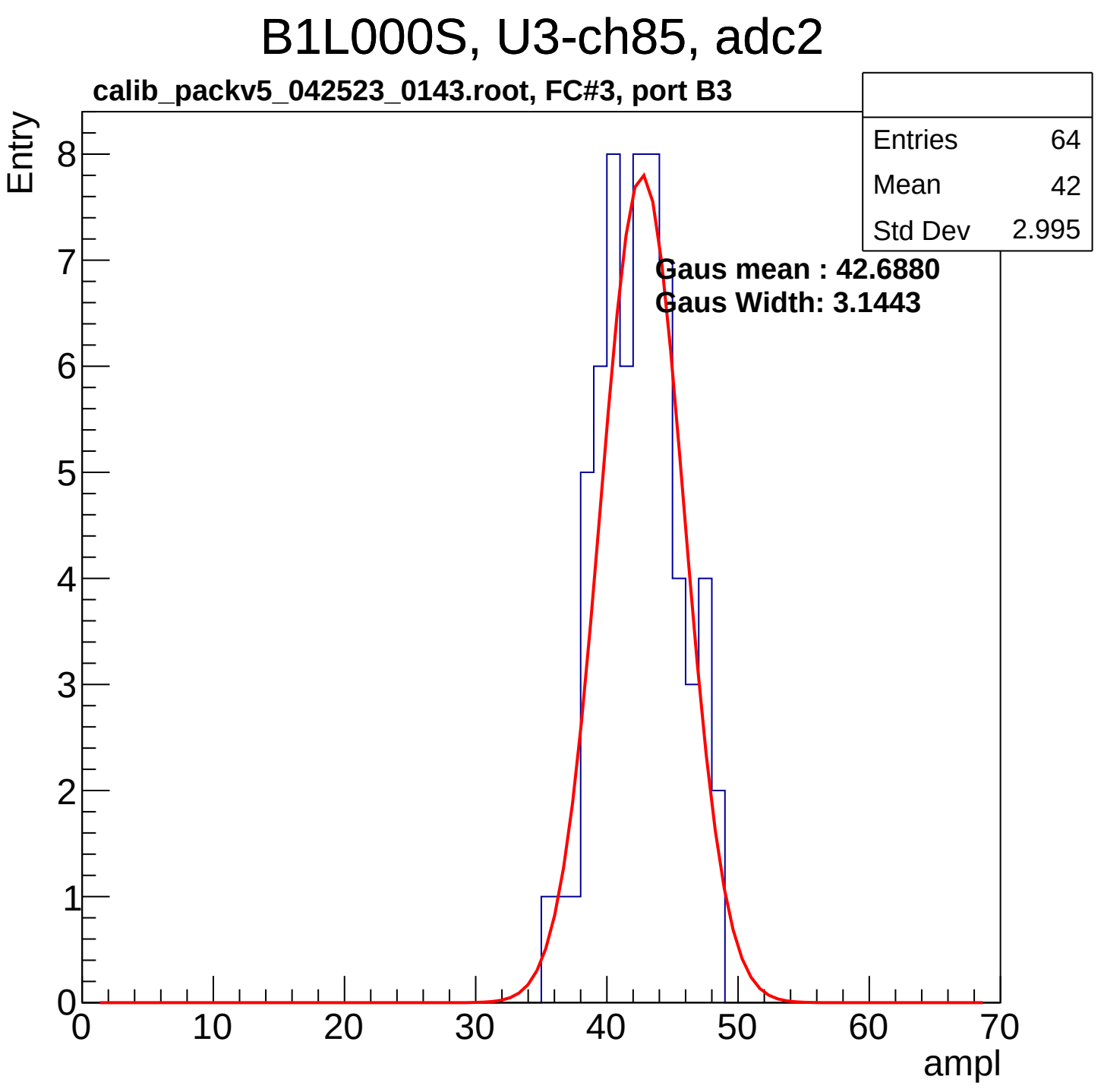
B1L000S, U3-ch85, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42
Std Dev 2.995
Gaus mean : 42.6880
Gaus Width: 3.1443
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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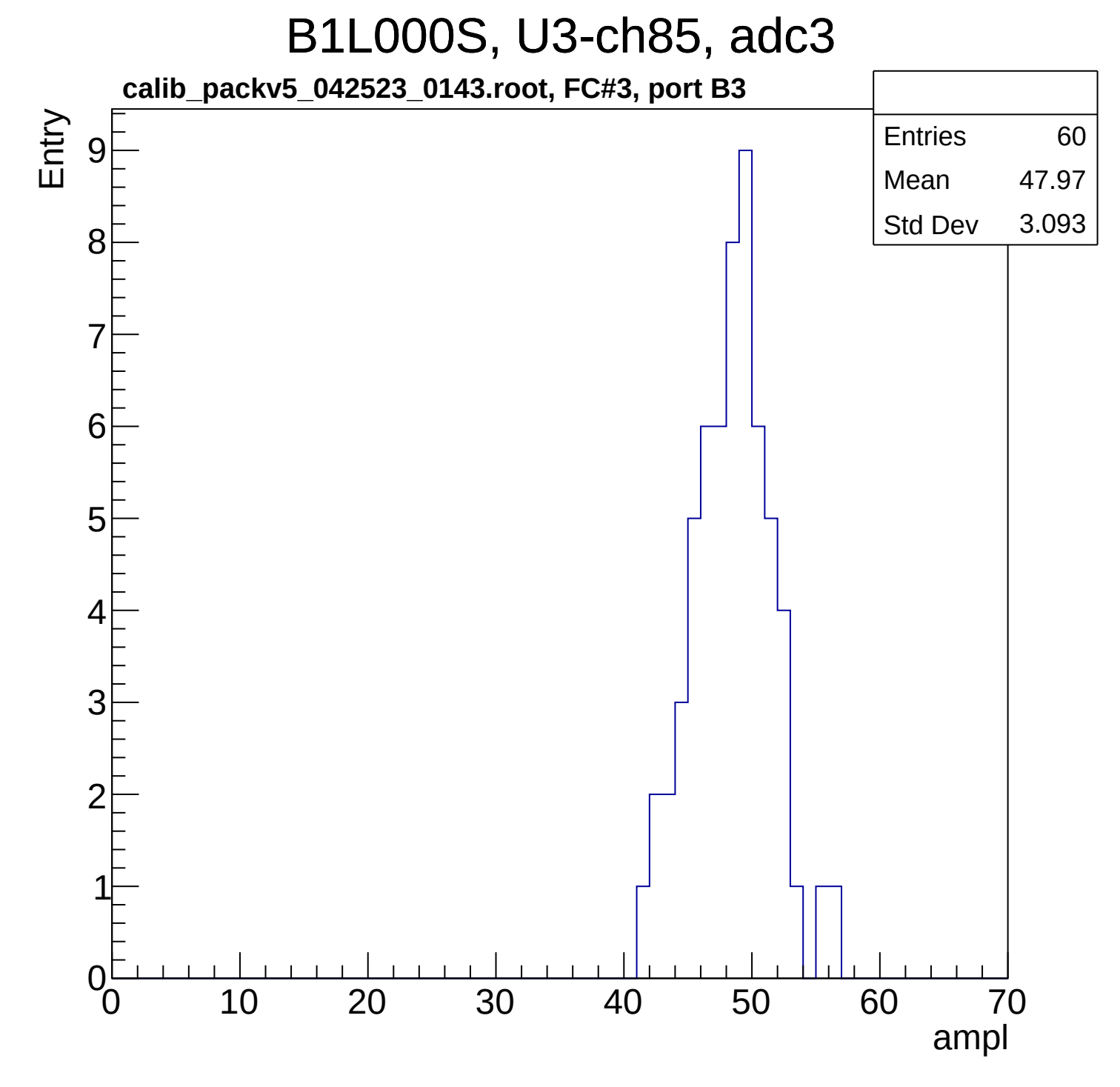
B1L000S, U3-ch85, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 47.97
Std Dev 3.093
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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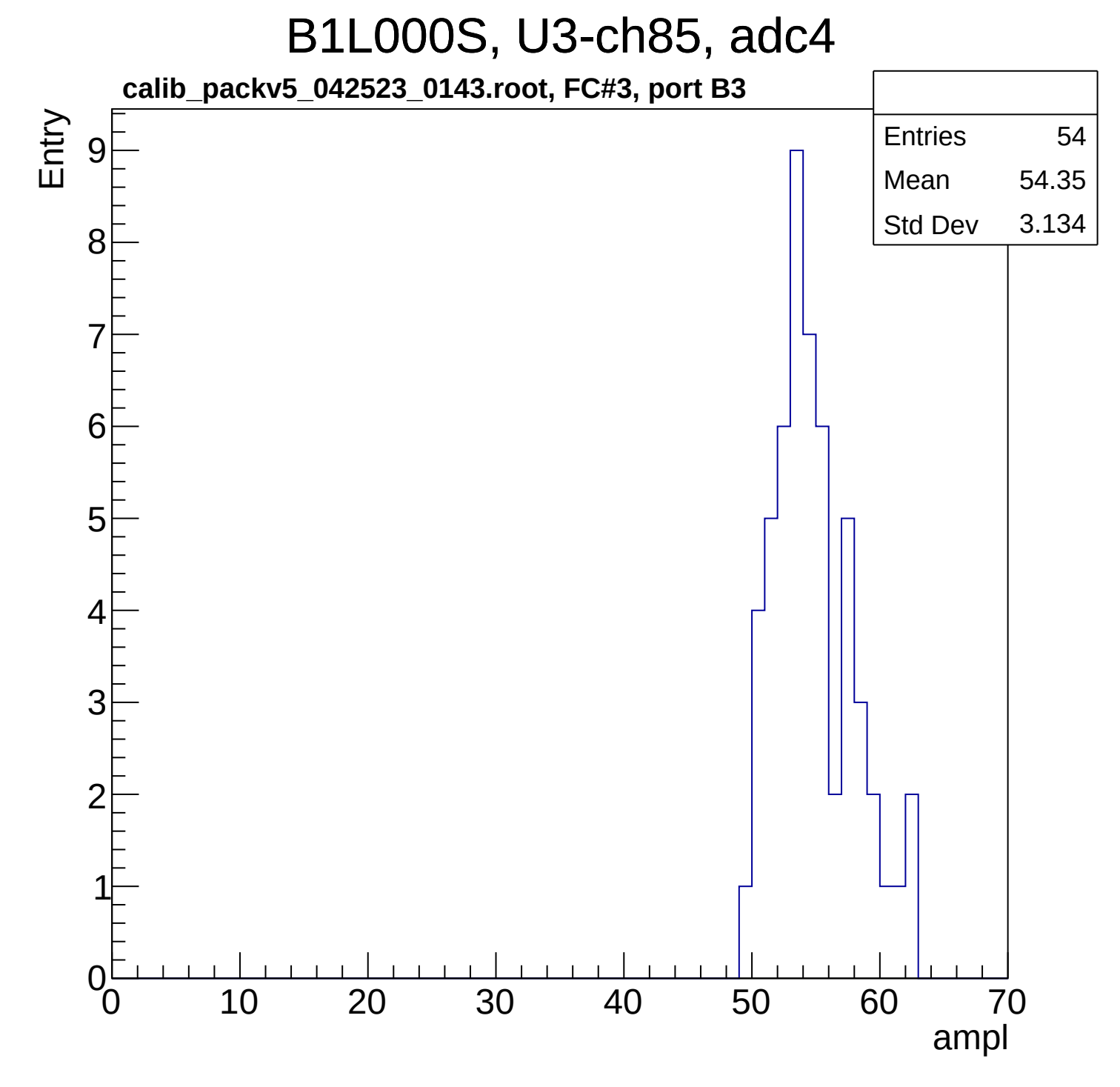
B1L000S, U3-ch85, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.35
Std Dev 3.134
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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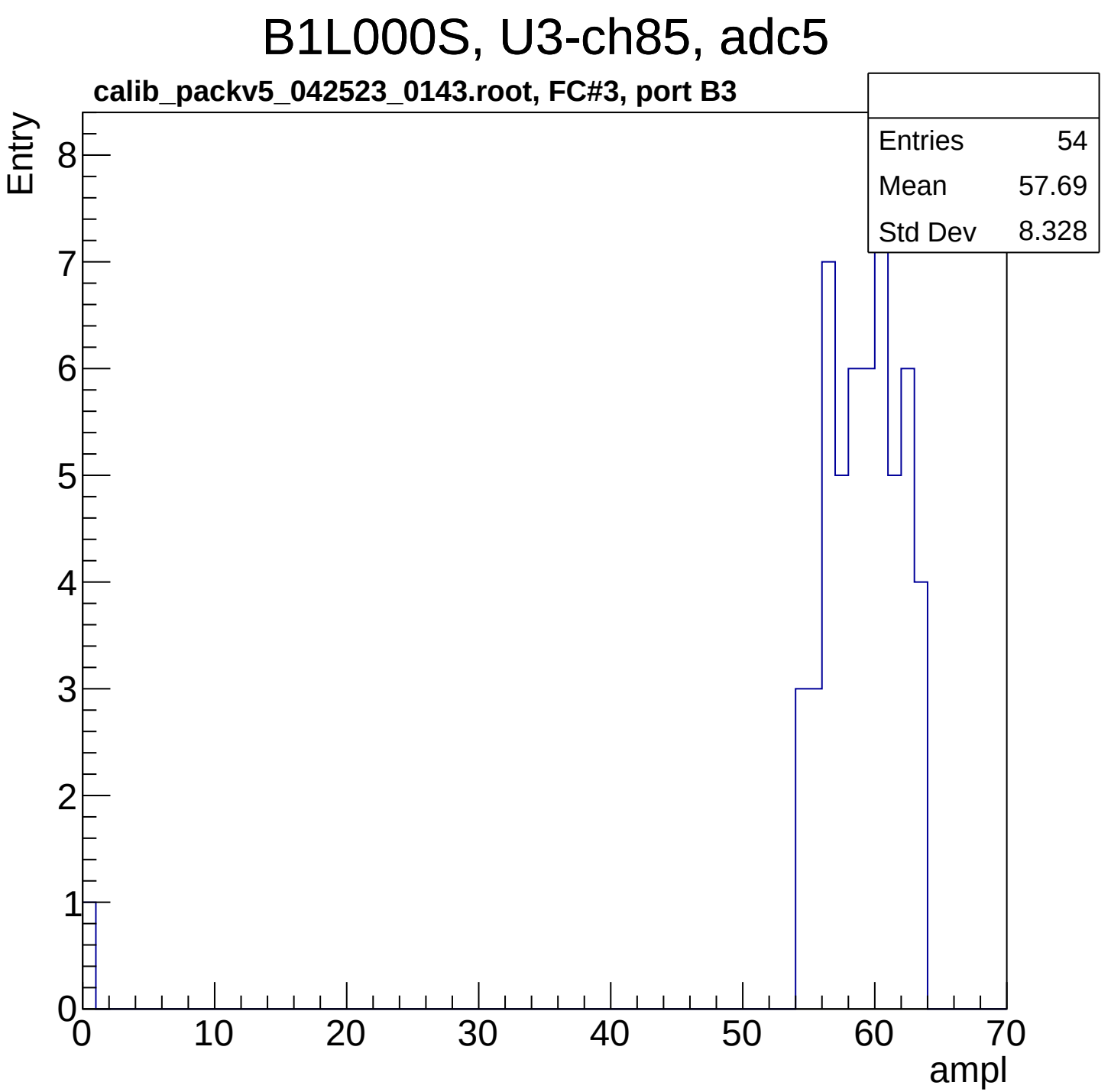
B1L000S, U3-ch85, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 57.69
Std Dev 8.328
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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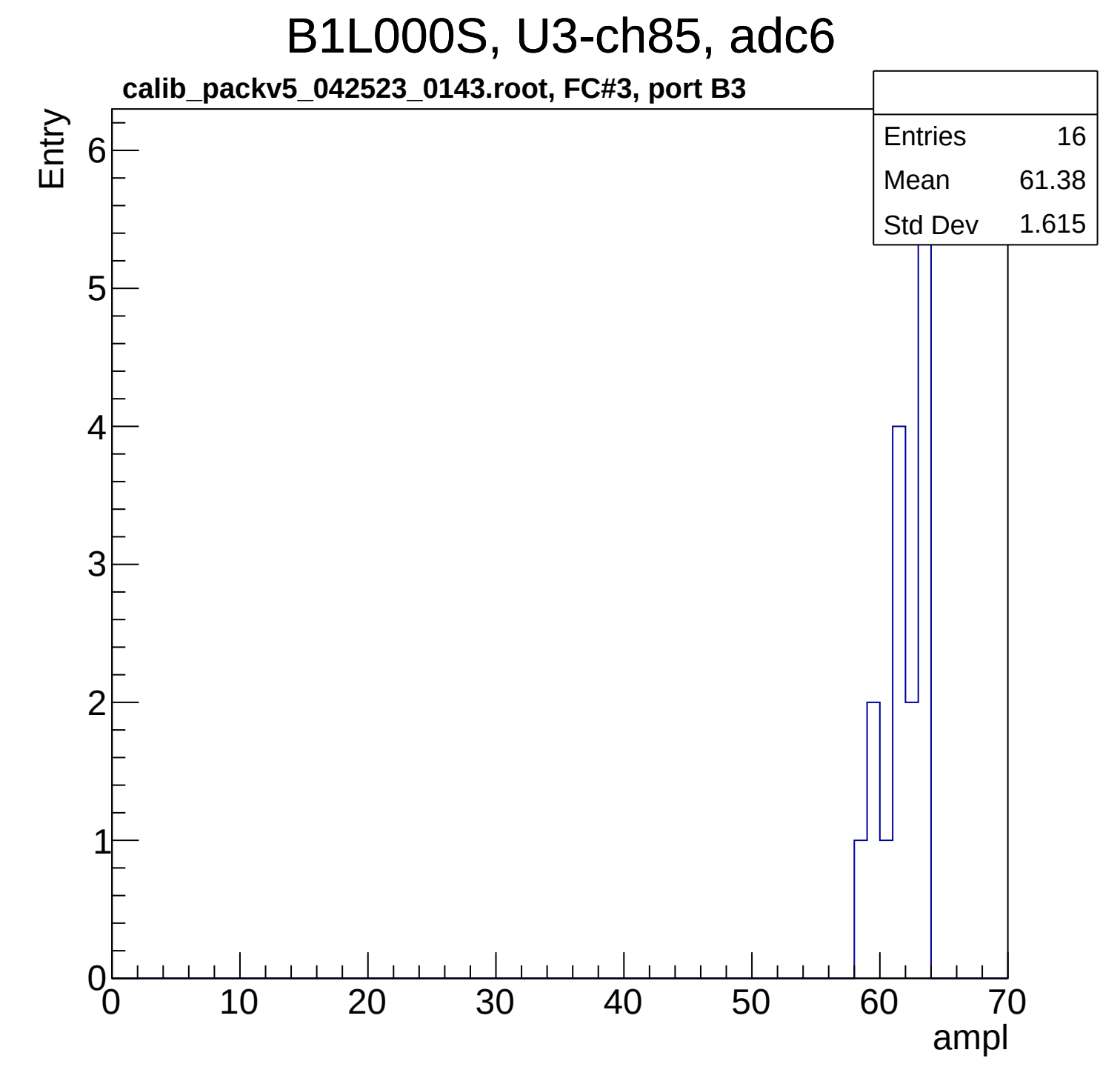
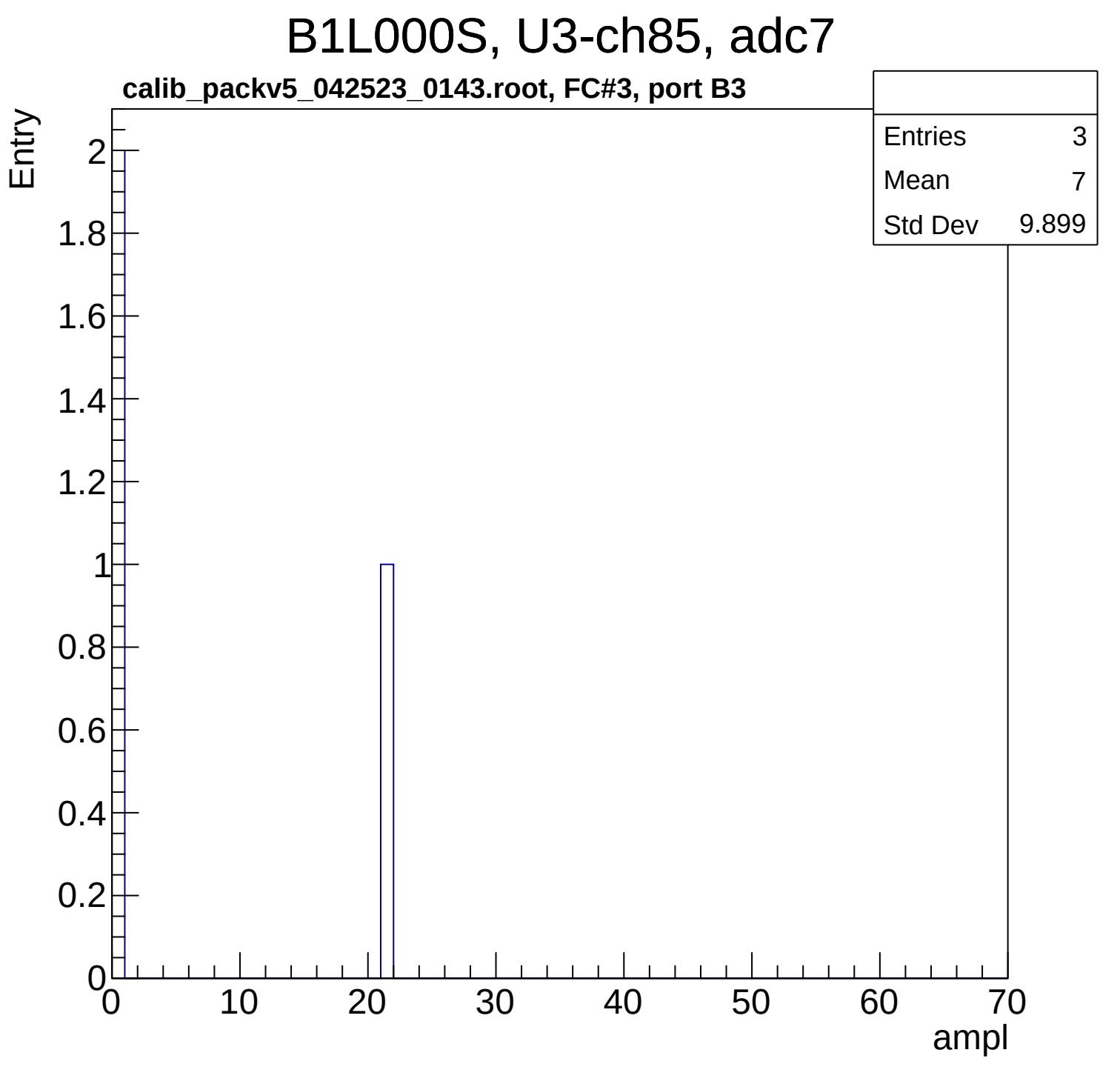

B1L000S, U3-ch85, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 61.38
Std Dev 1.615
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch85, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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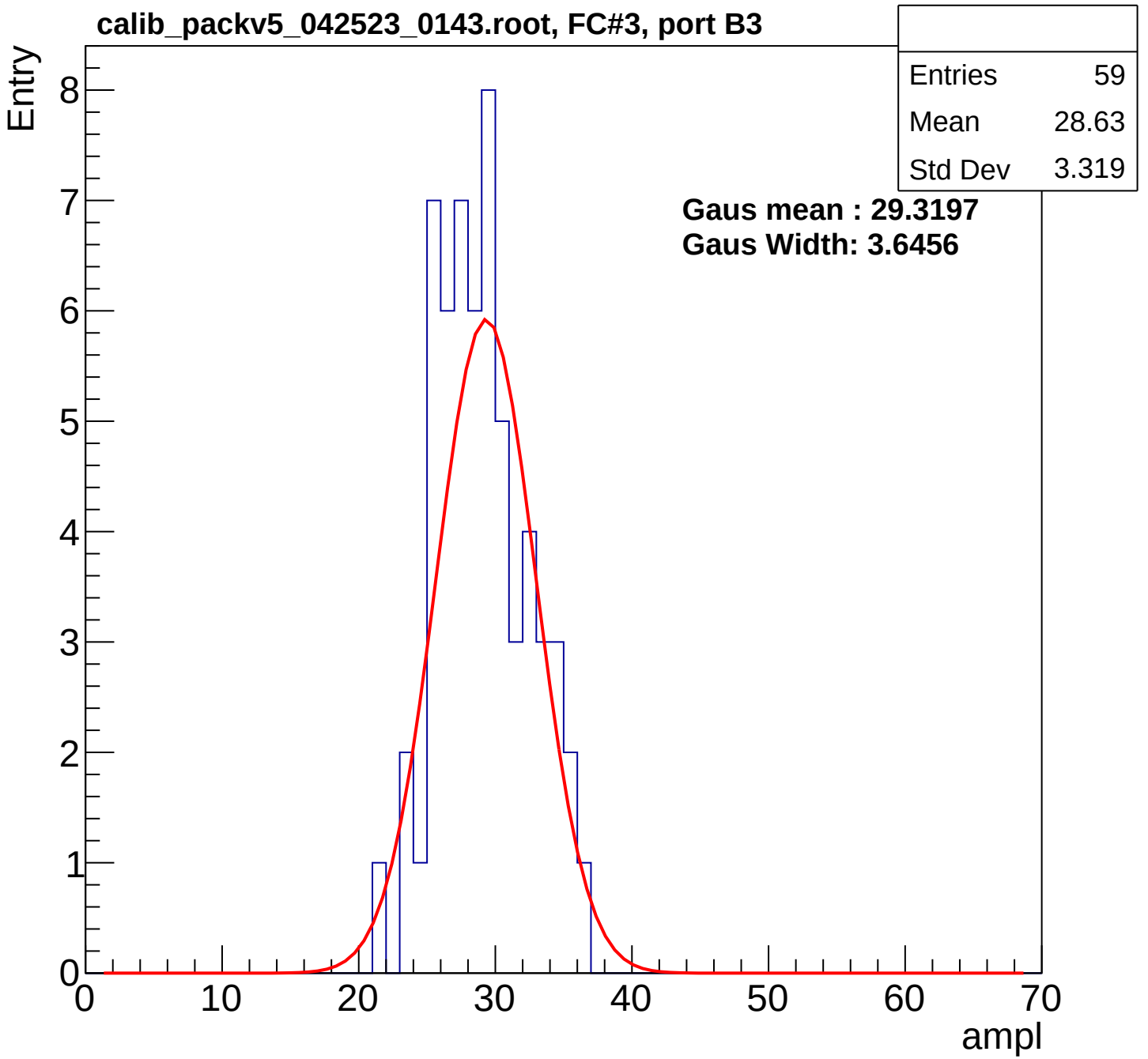
B1L000S, U3-ch86, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 28.63
Std Dev 3.319
Gaus mean : 29.3197
Gaus Width: 3.6456
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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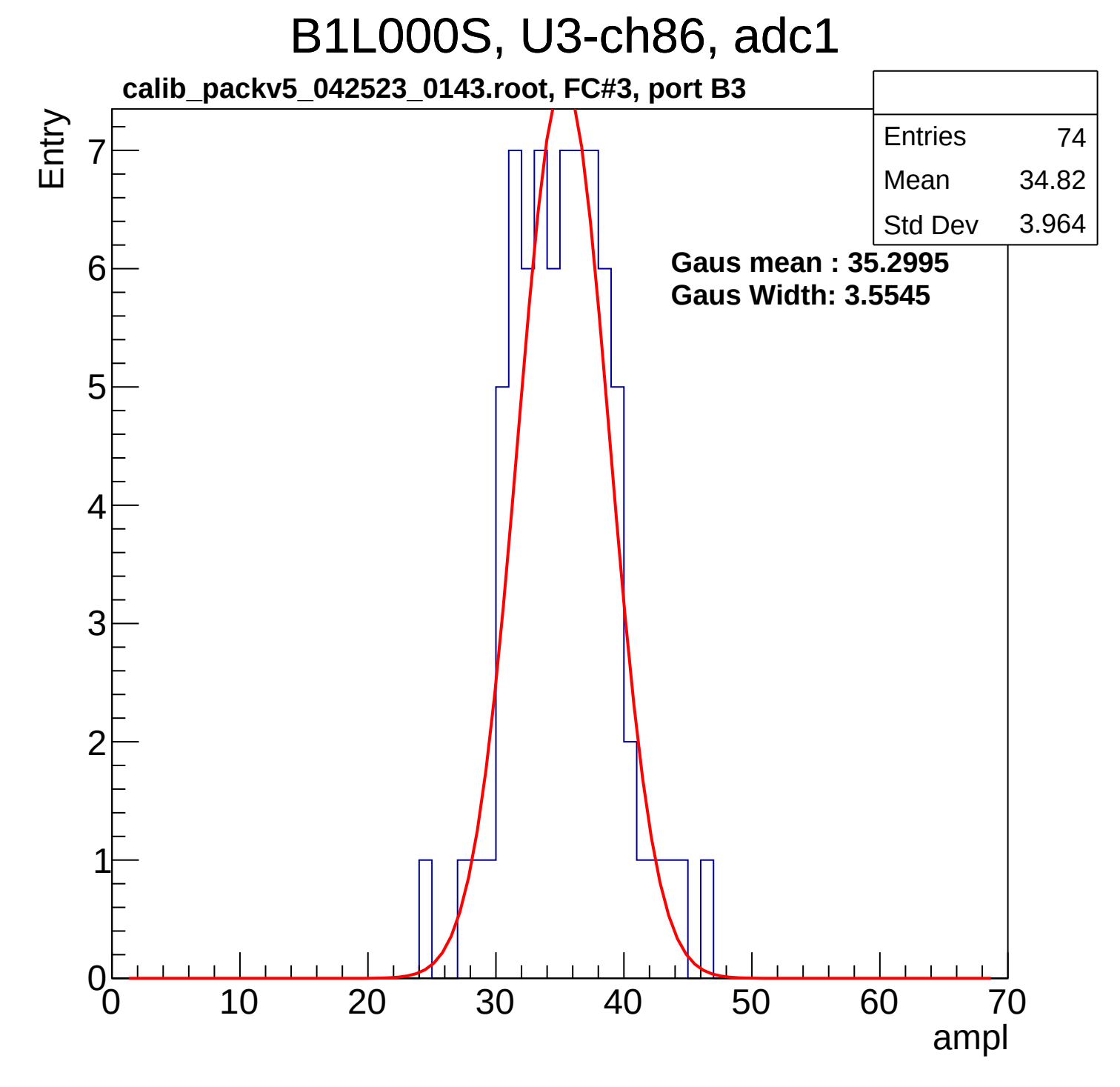
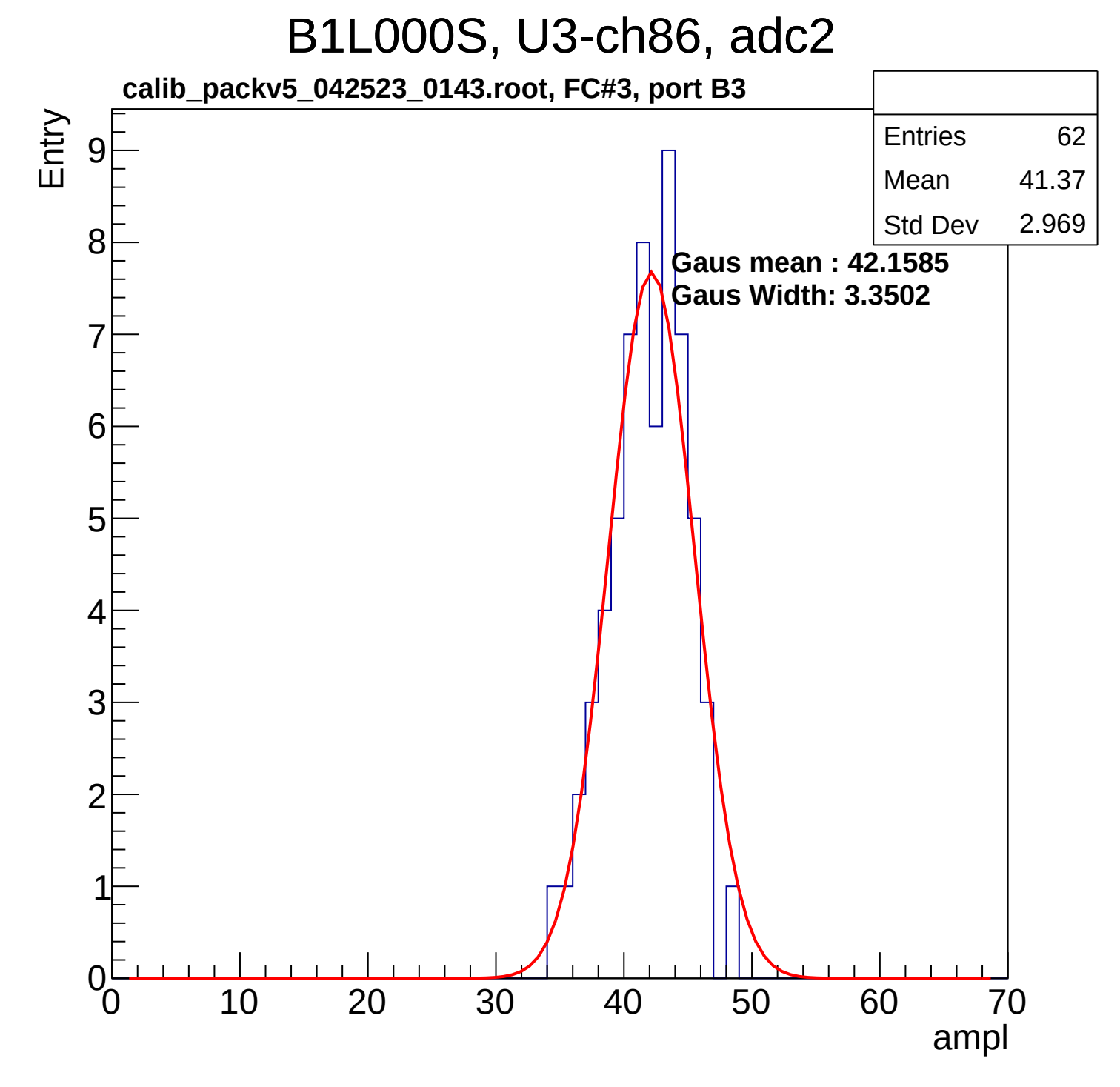

B1L000S, U3-ch86, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 34.82
Std Dev 3.964
Gaus mean : 35.2995
Gaus Width: 3.5545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch86, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 41.37
Std Dev 2.969
Gaus mean : 42.1585
Gaus Width: 3.3502
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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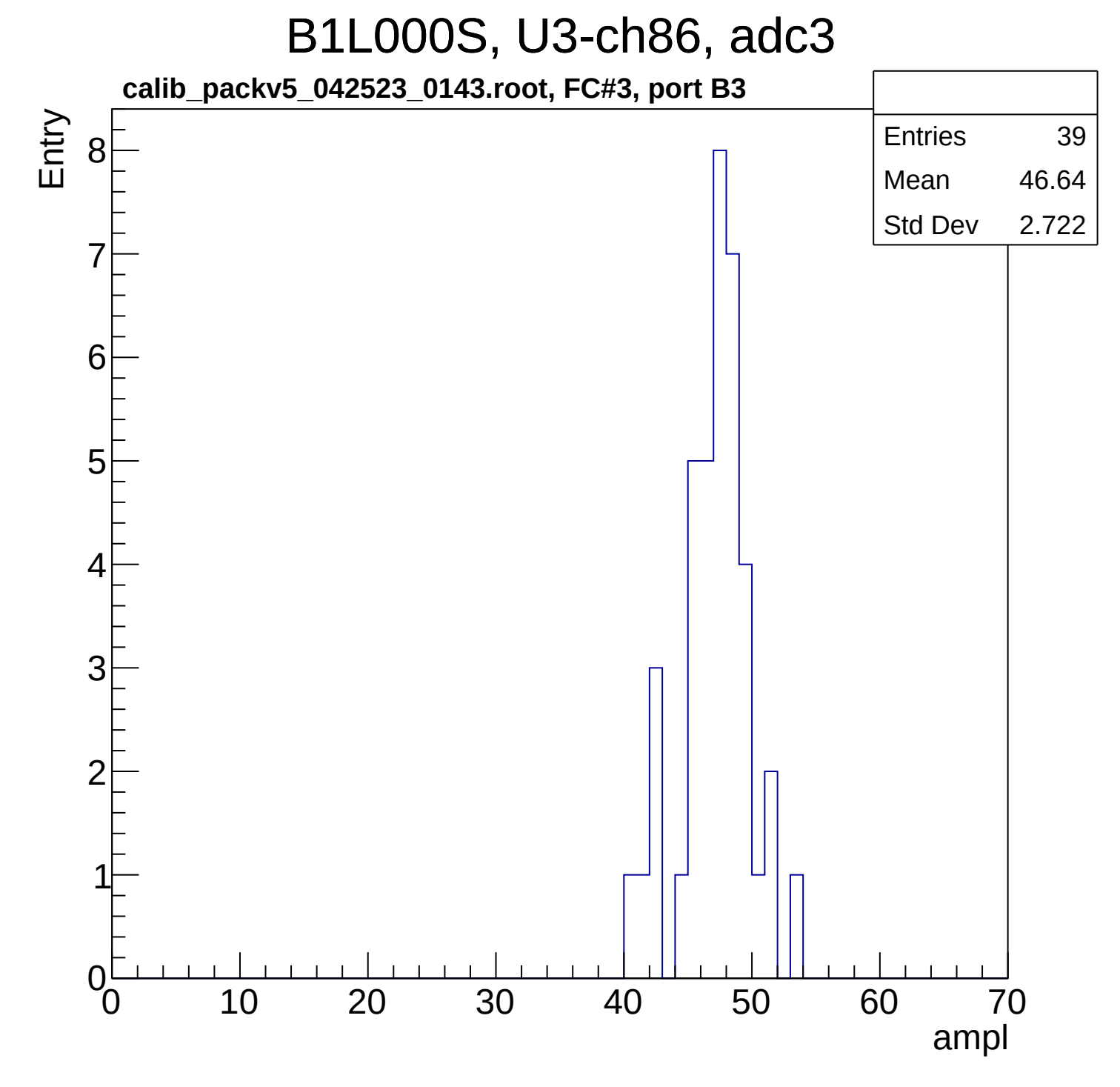
B1L000S, U3-ch86, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 46.64
Std Dev 2.722
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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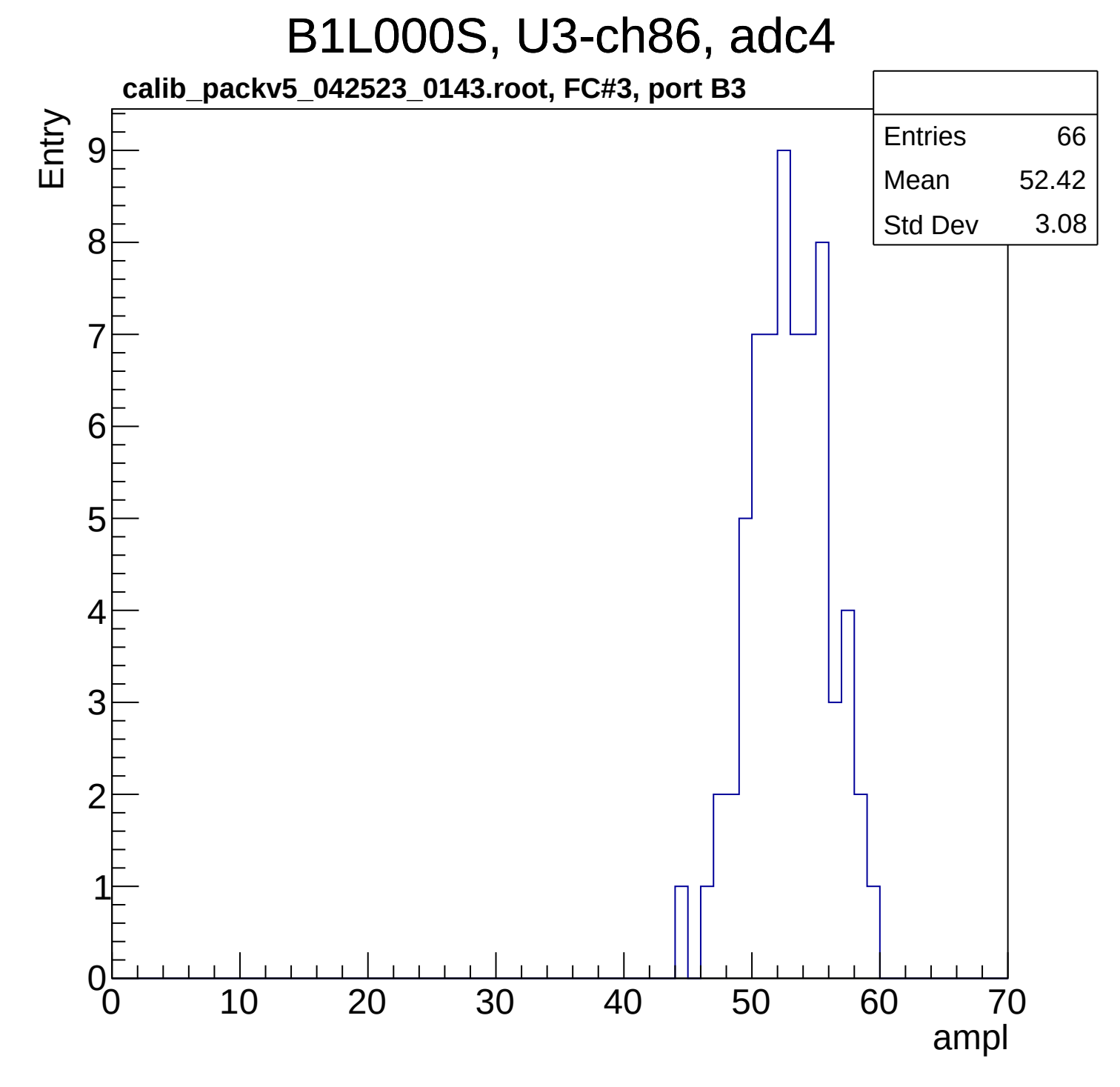
B1L000S, U3-ch86, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 52.42
Std Dev 3.08
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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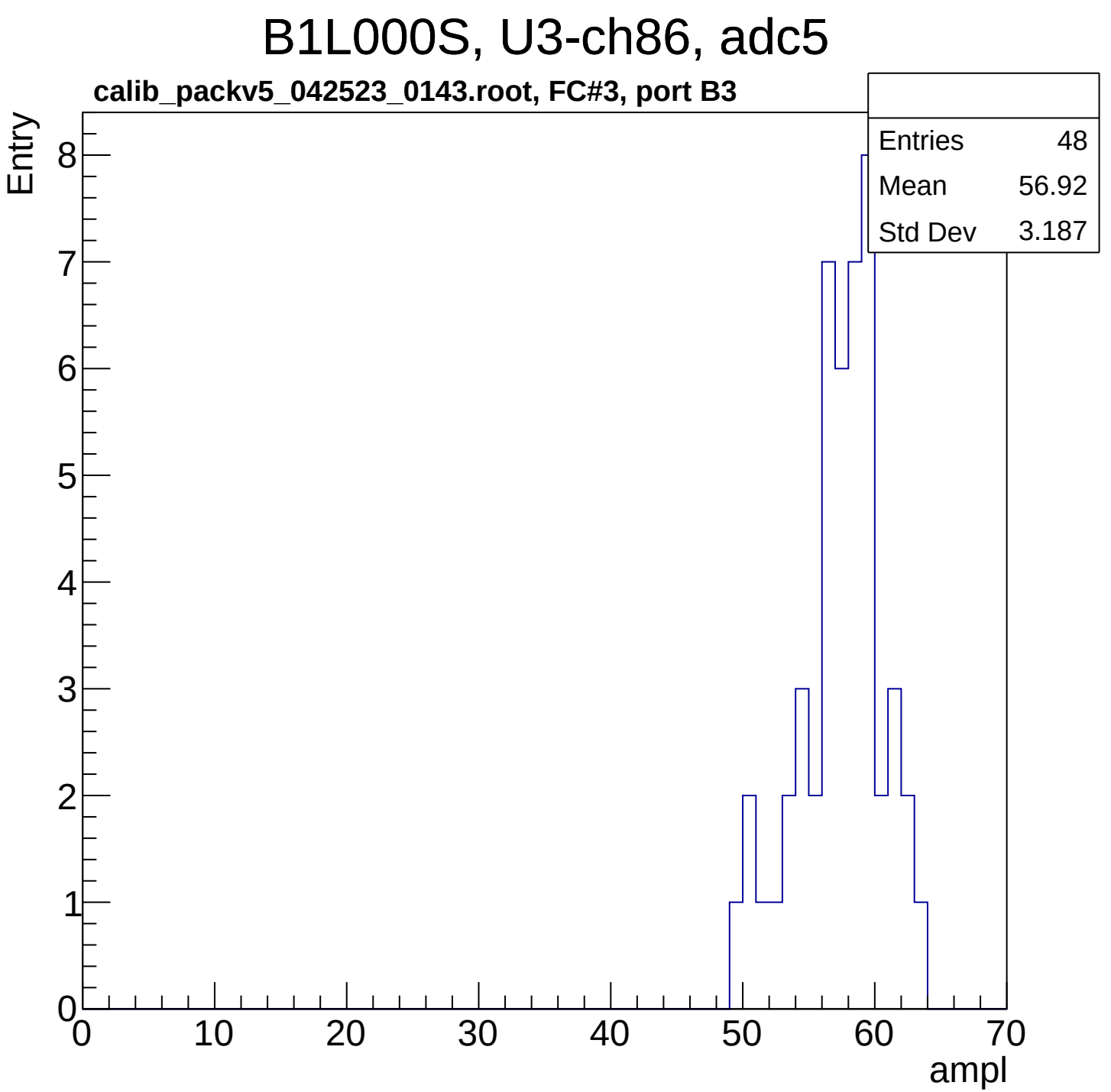
B1L000S, U3-ch86, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 56.92
Std Dev 3.187
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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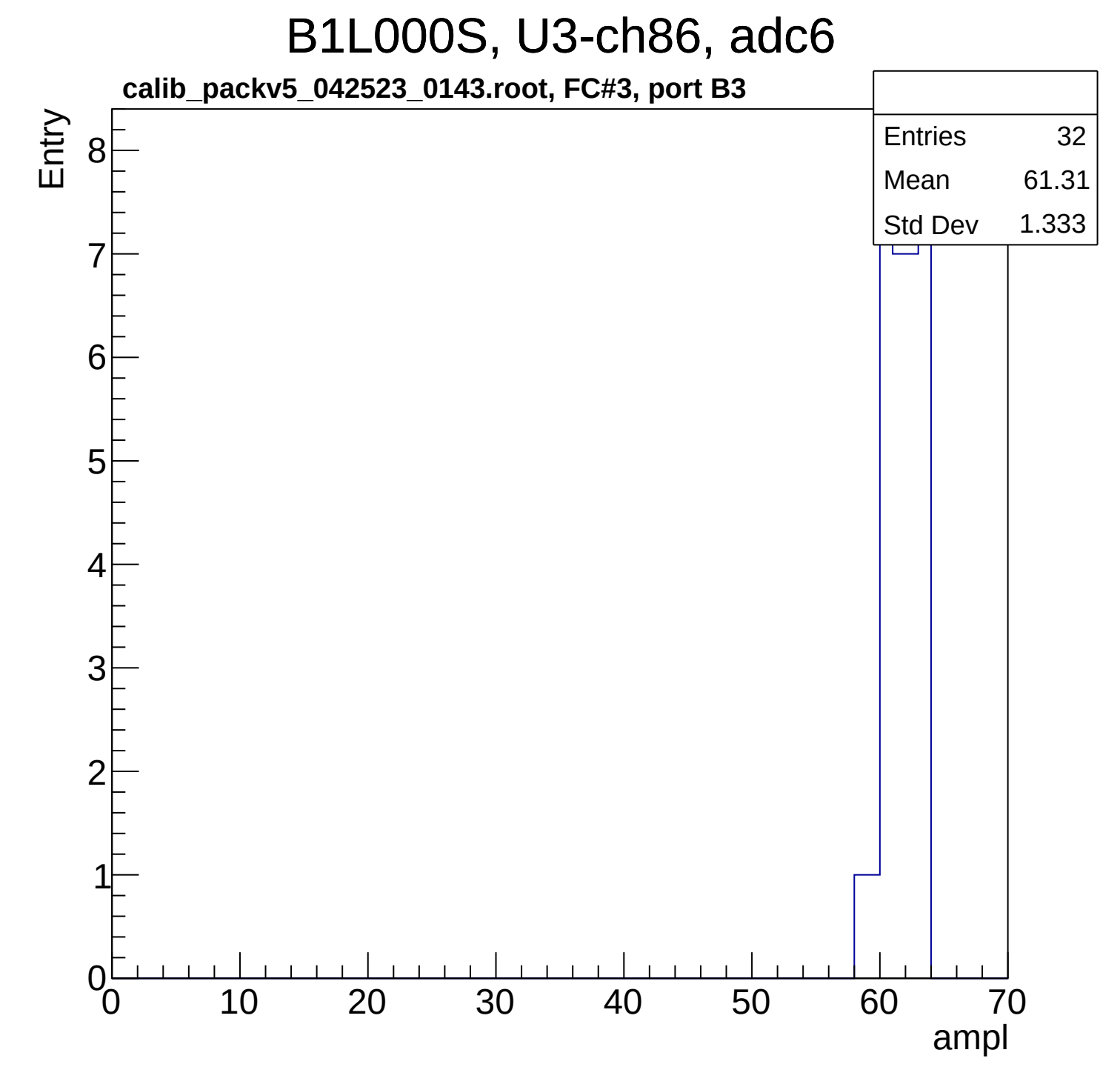
B1L000S, U3-ch86, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 61.31
Std Dev 1.333
Entry
ampl
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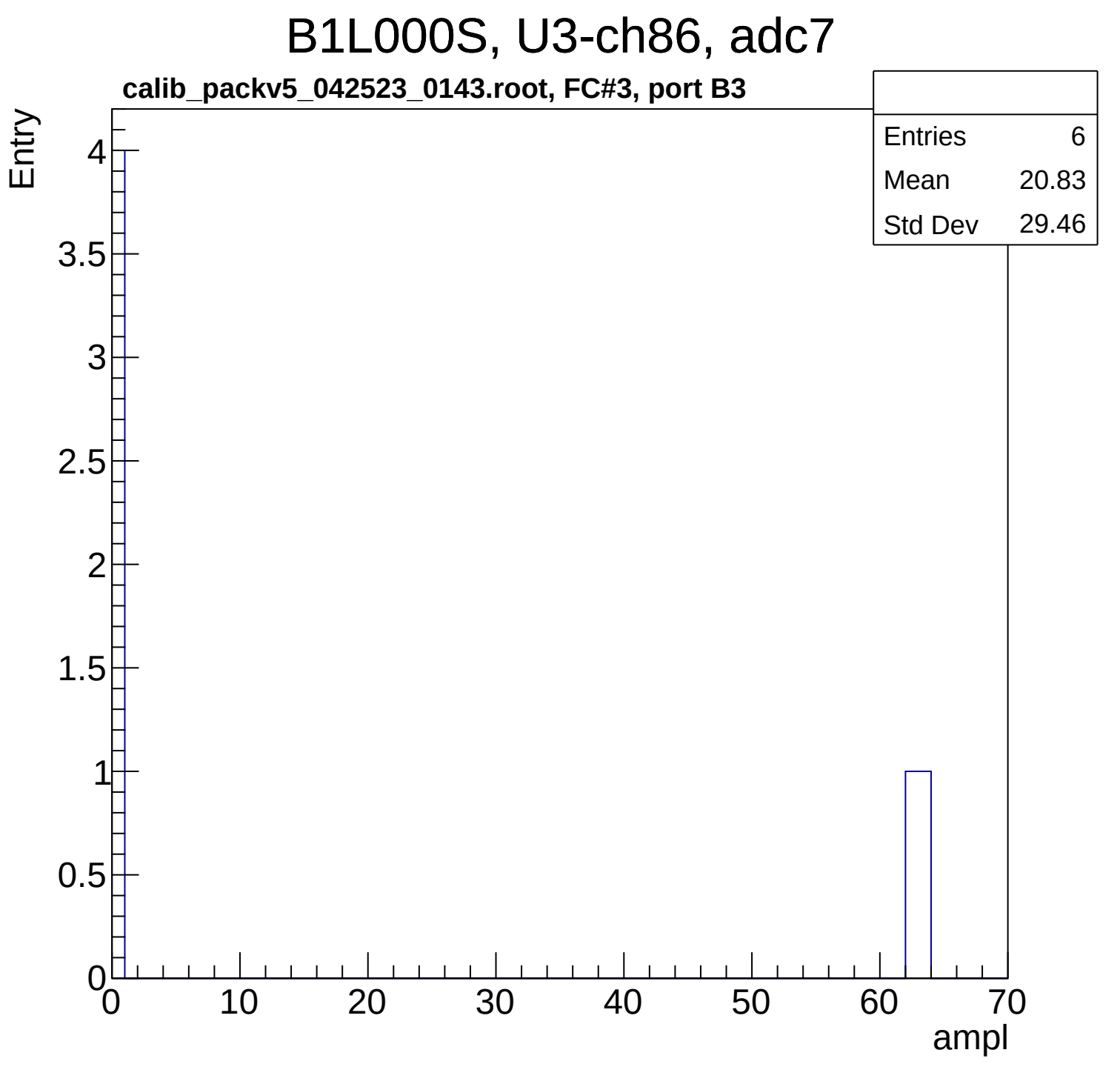
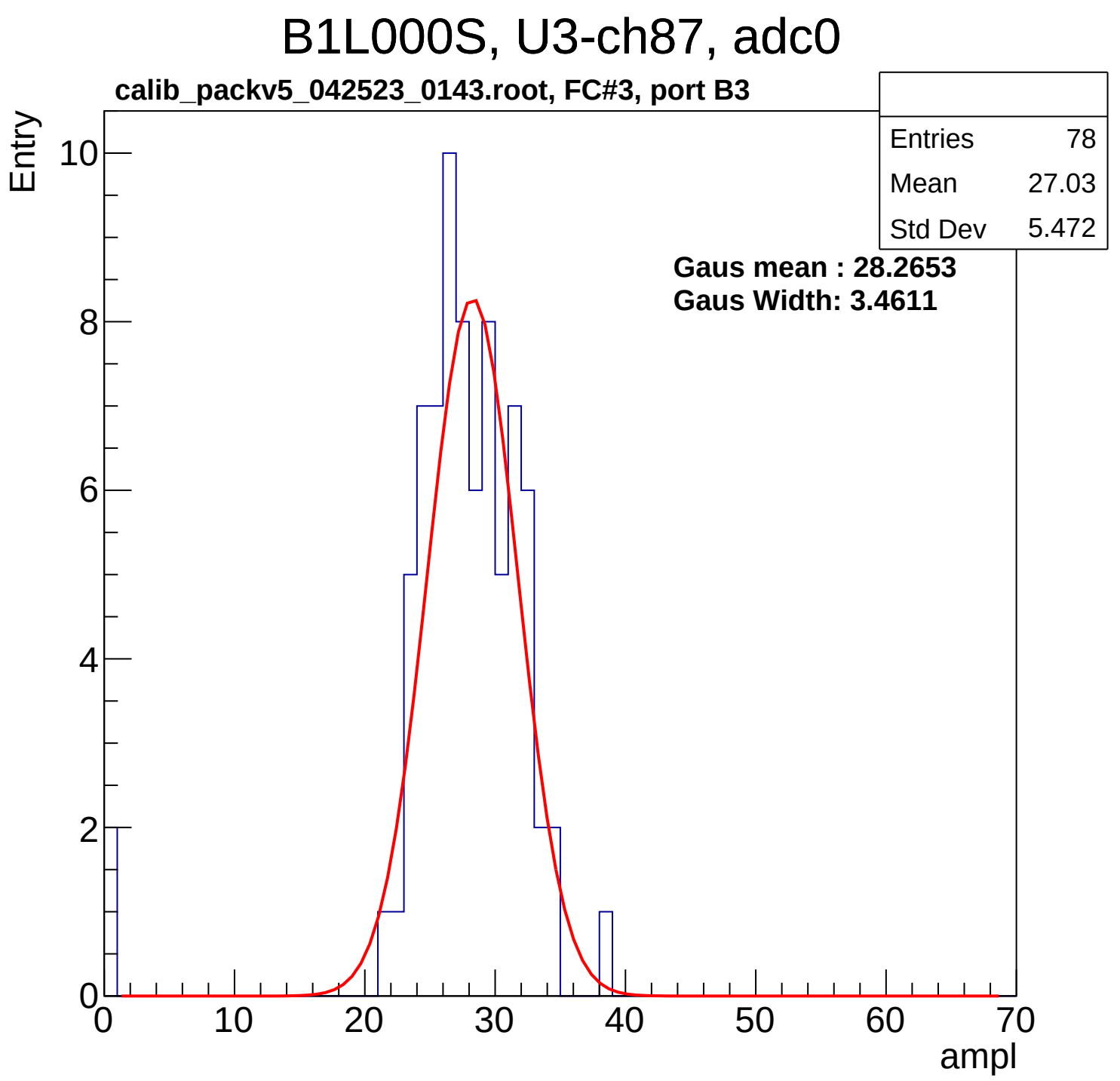
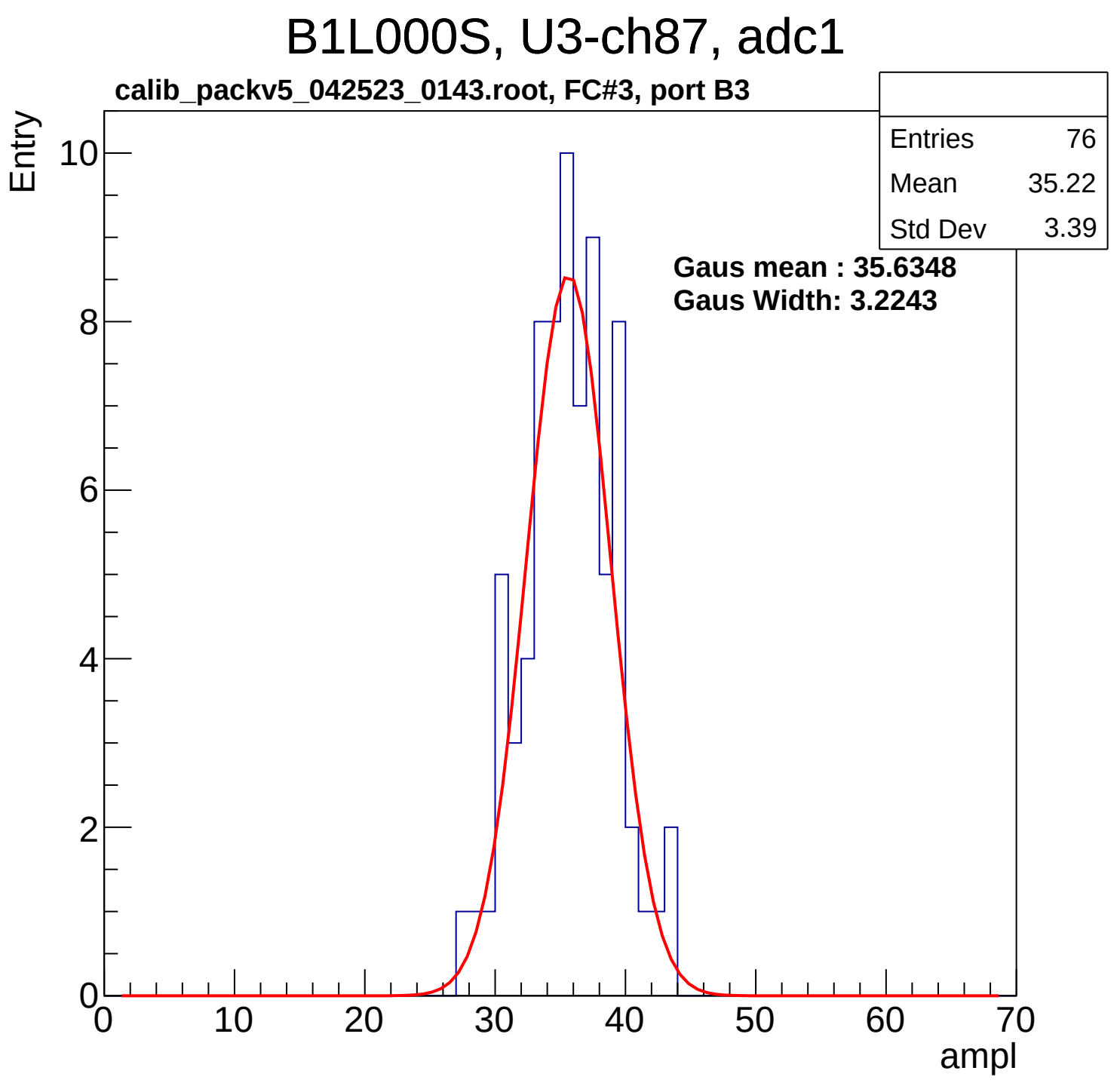
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch86, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 20.83
Std Dev 29.46
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch87, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 27.03
Std Dev 5.472
Gaus mean : 28.2653
Gaus Width: 3.4611
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch87, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 35.22
Std Dev 3.39
Gaus mean : 35.6348
Gaus Width: 3.2243
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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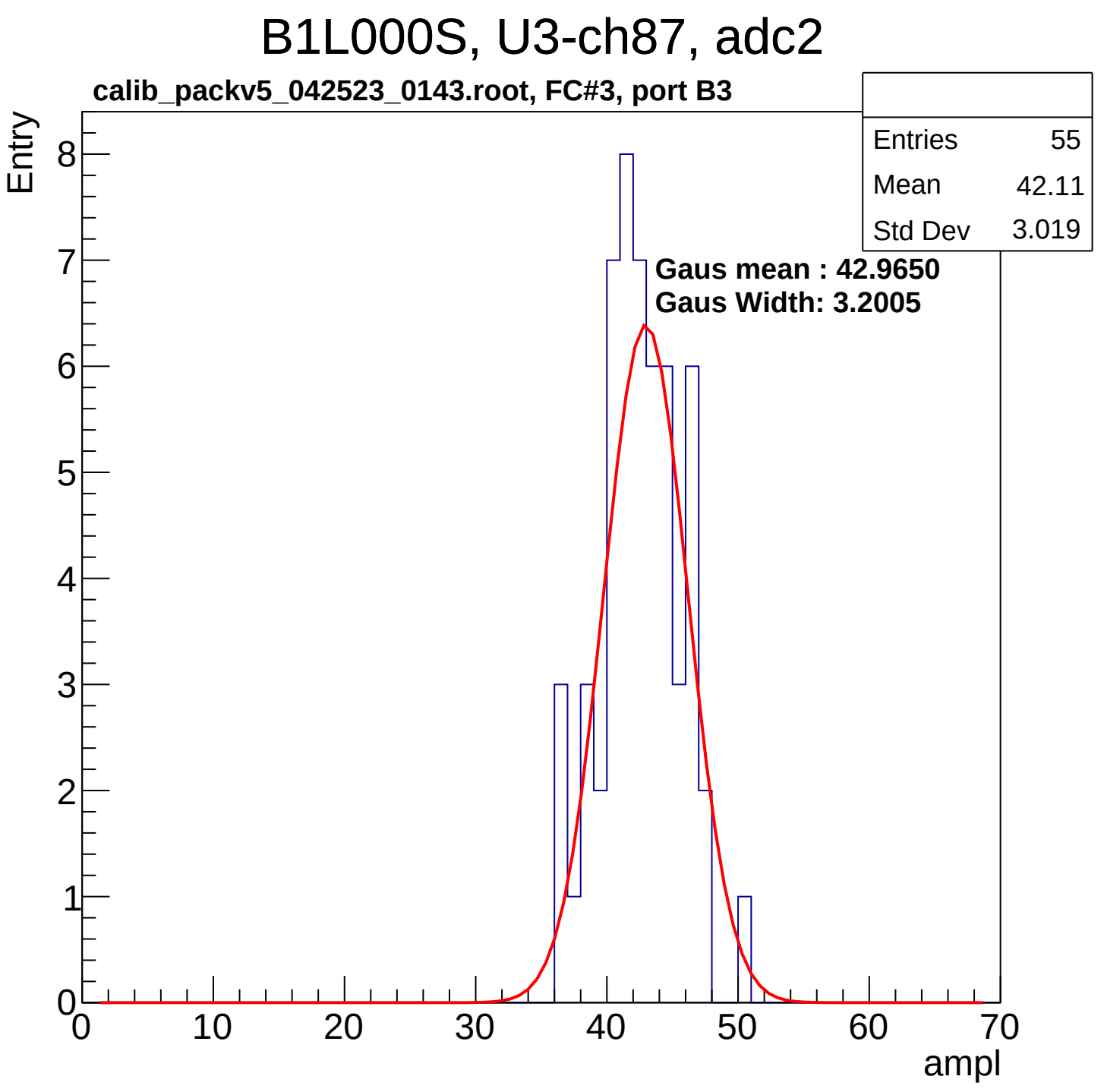
B1L000S, U3-ch87, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 42.11
Std Dev 3.019
Gaus mean : 42.9650
Gaus Width: 3.2005
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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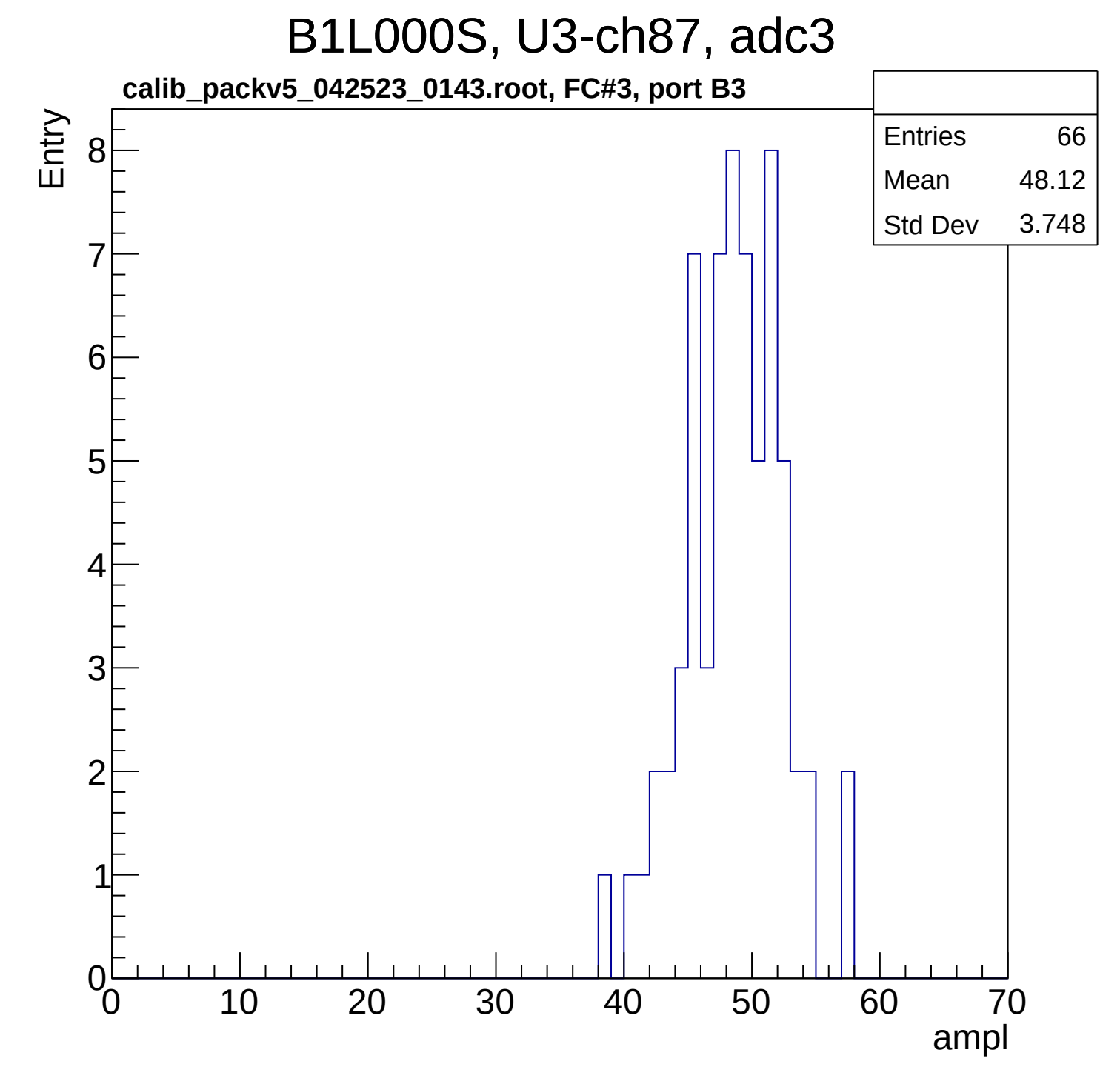
B1L000S, U3-ch87, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 48.12
Std Dev 3.748
Entry
ampl
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3
4
5
6
7
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0
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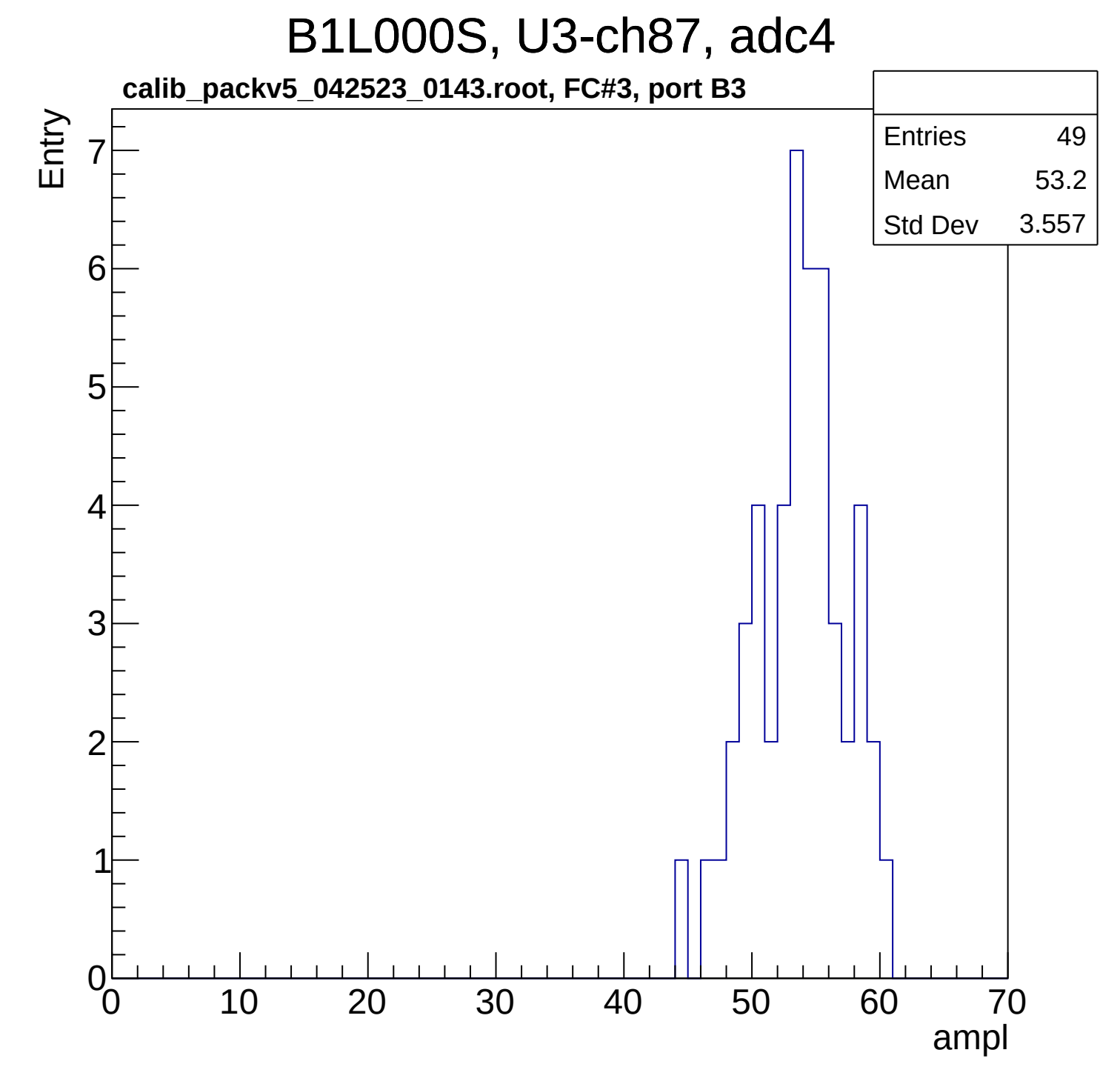
B1L000S, U3-ch87, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 53.2
Std Dev 3.557
Entry
ampl
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4
5
6
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0
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60
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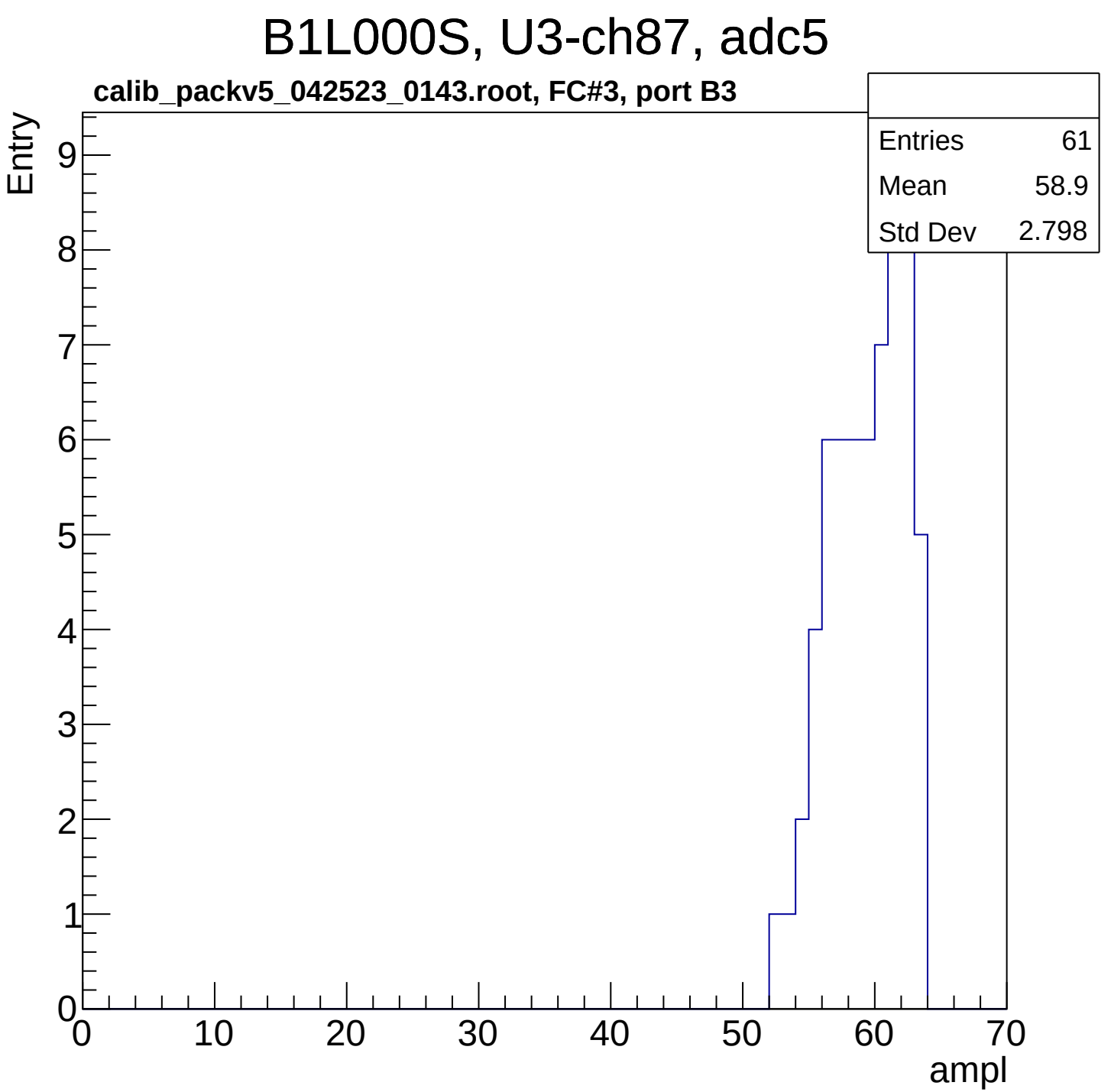
B1L000S, U3-ch87, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.9
Std Dev 2.798
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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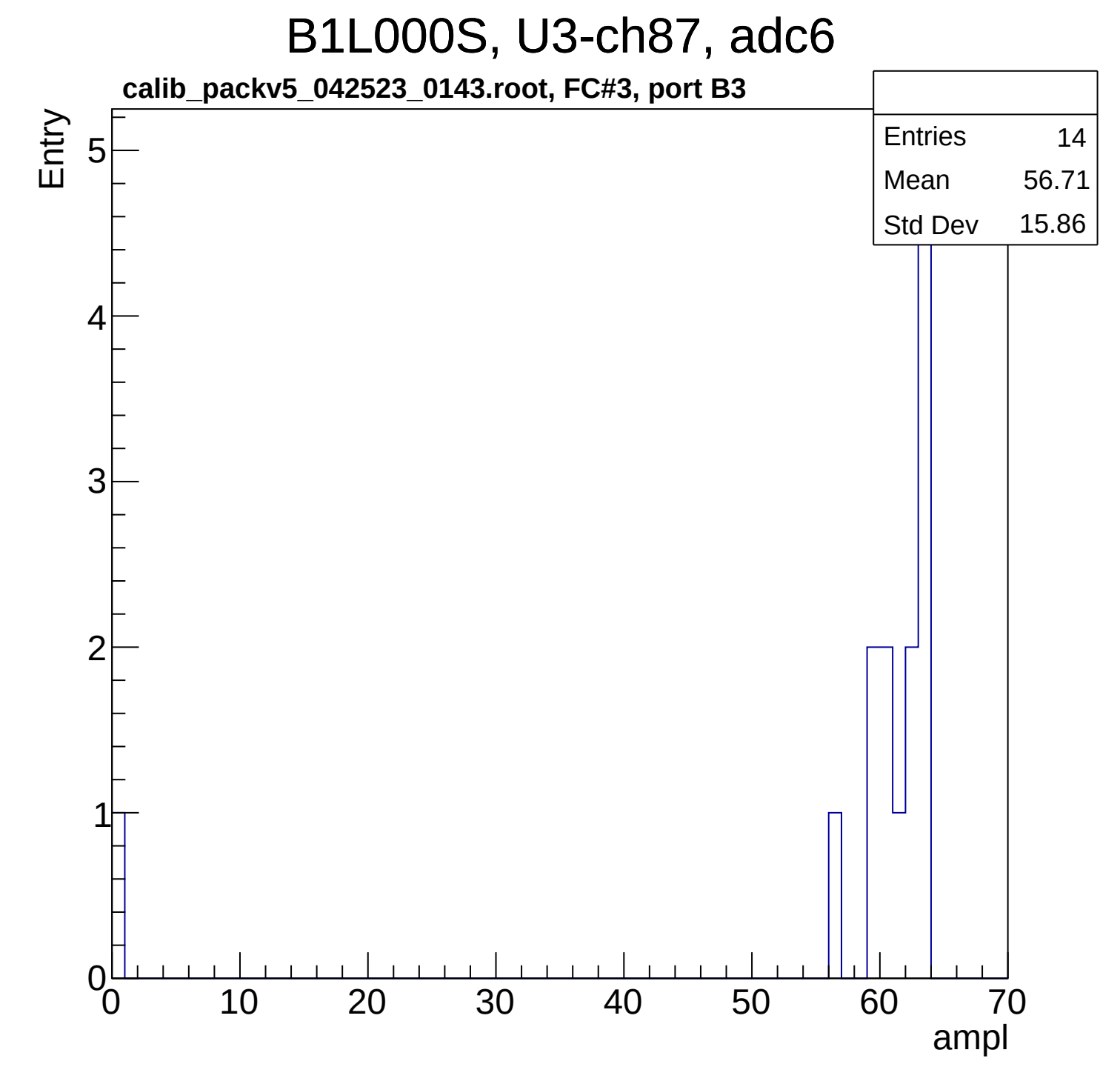
40
50
60
70


B1L000S, U3-ch87, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 56.71
Std Dev 15.86
Entry
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3
4
5
0
10
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30
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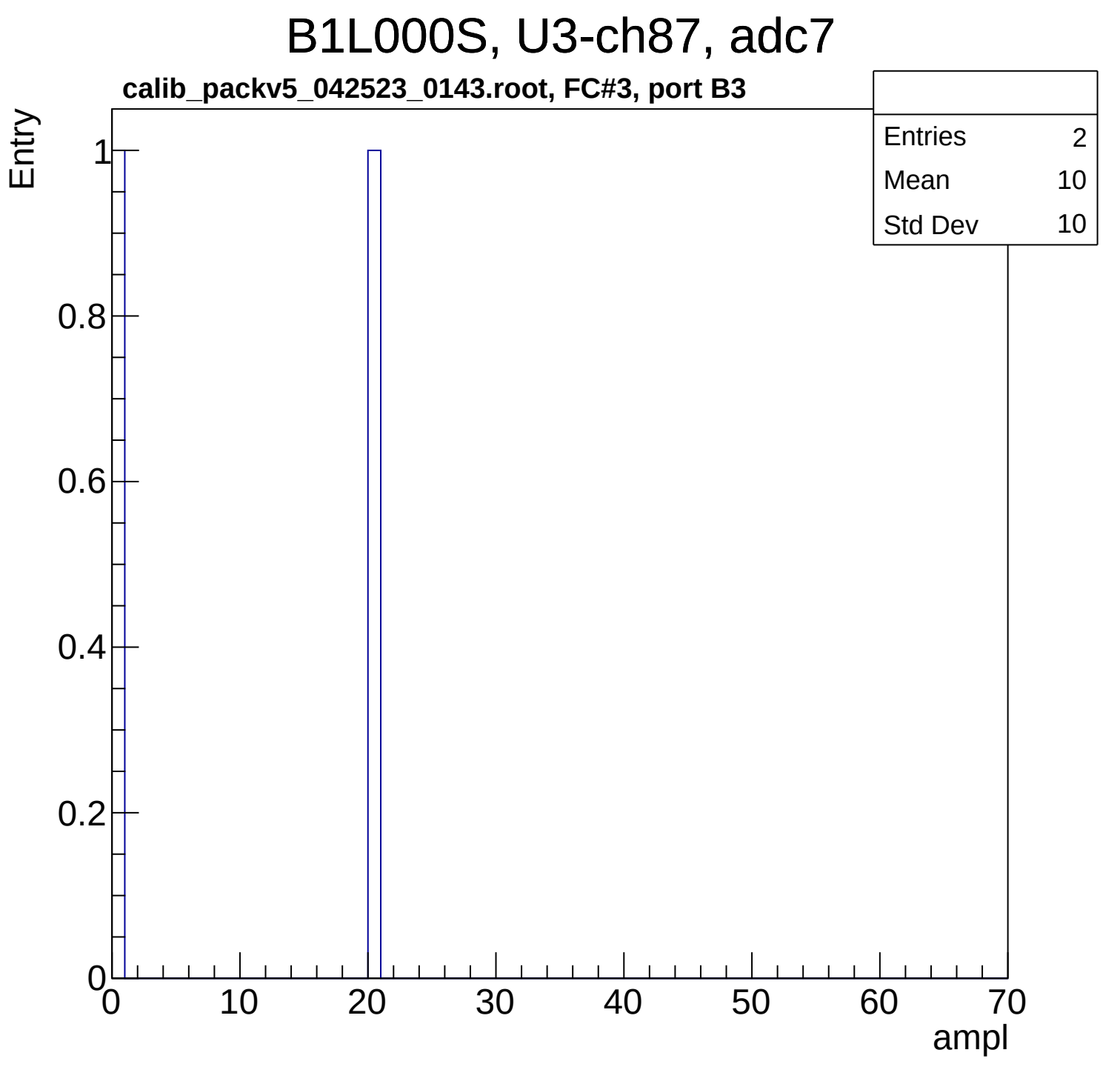
50
60
70
ampl


B1L000S, U3-ch87, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10
Std Dev 10
Entry
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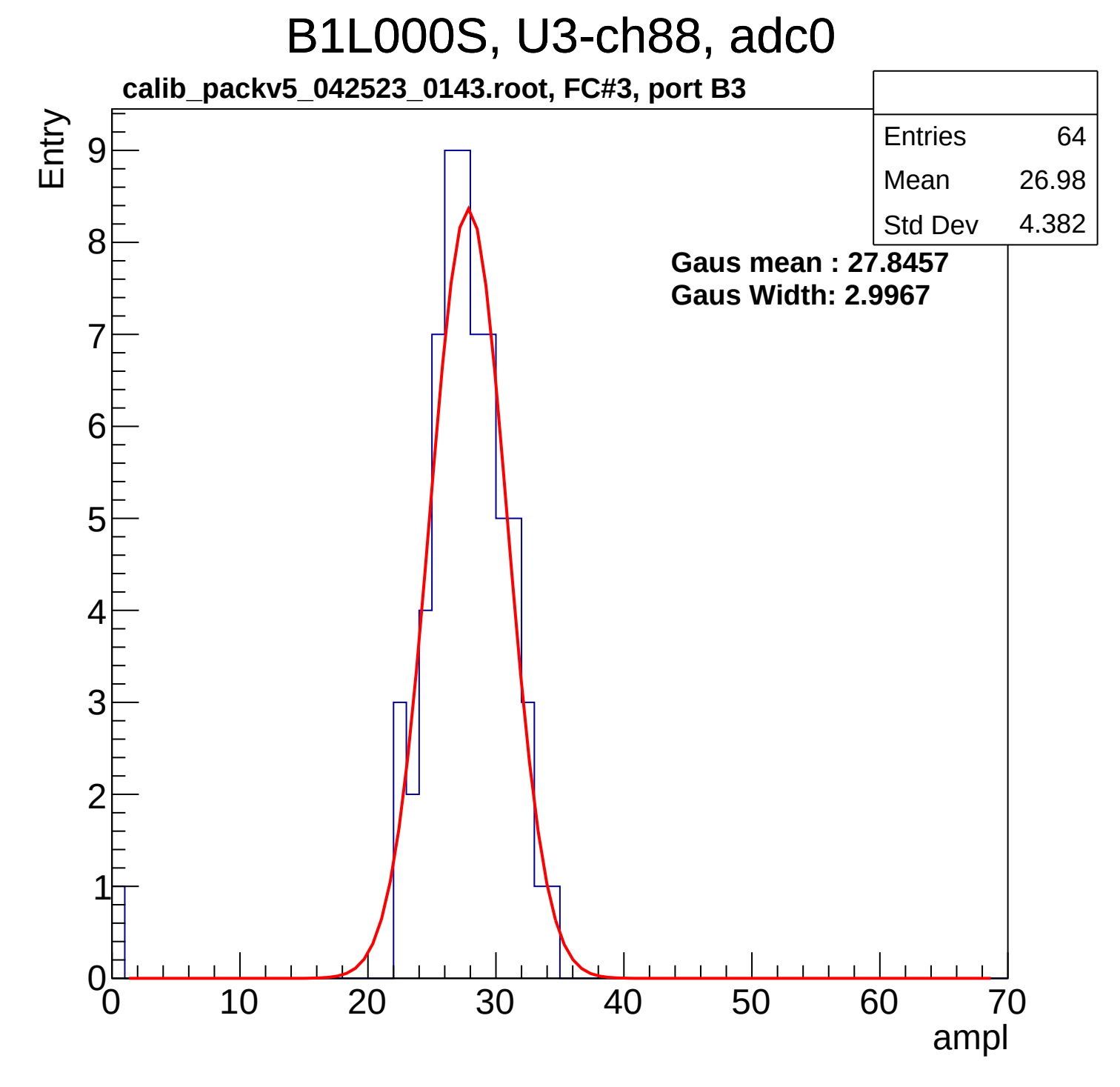
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch88, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 26.98
Std Dev 4.382
Gaus mean : 27.8457
Gaus Width: 2.9967
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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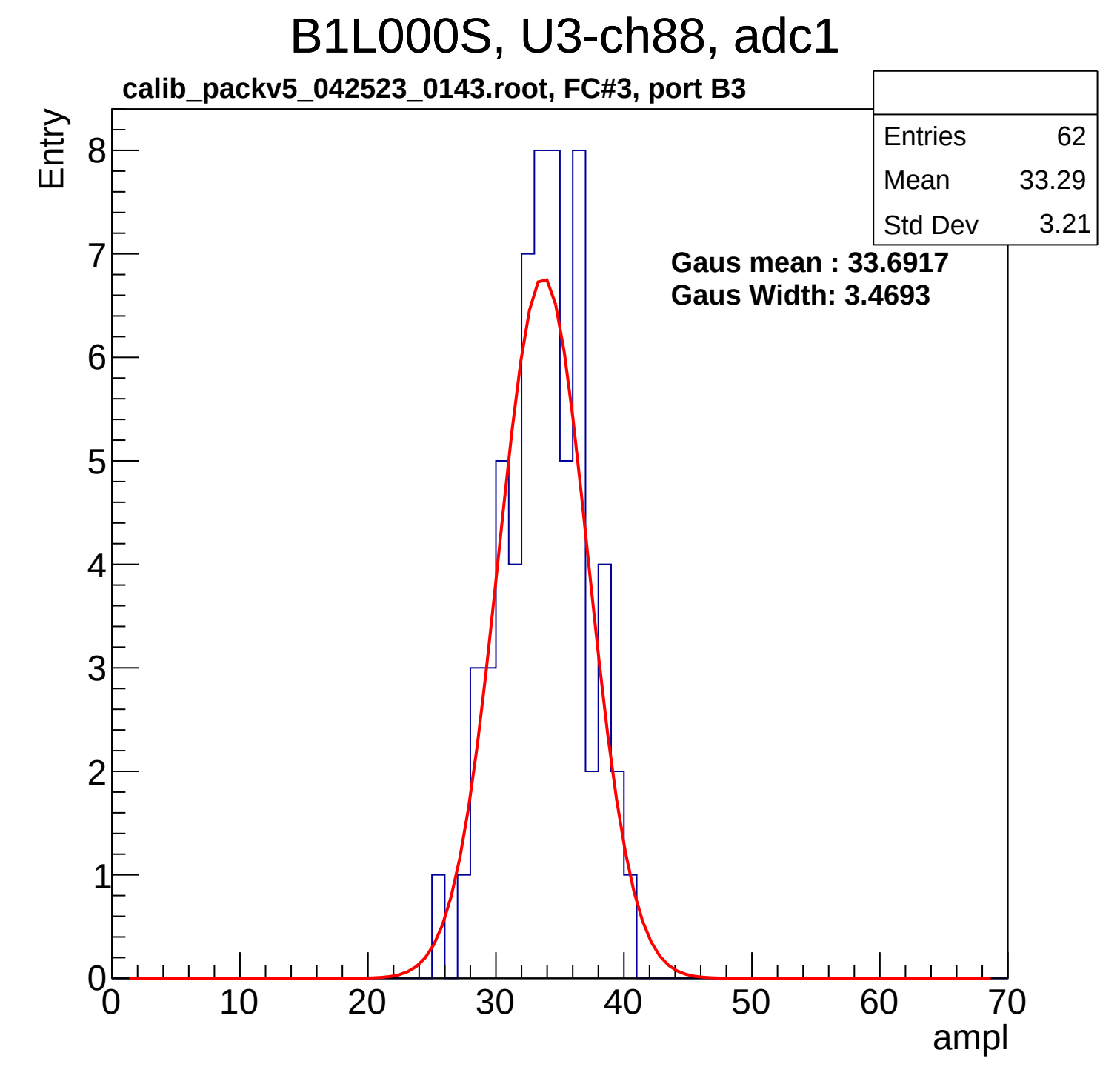
B1L000S, U3-ch88, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 33.29
Std Dev 3.21
Gaus mean : 33.6917
Gaus Width: 3.4693
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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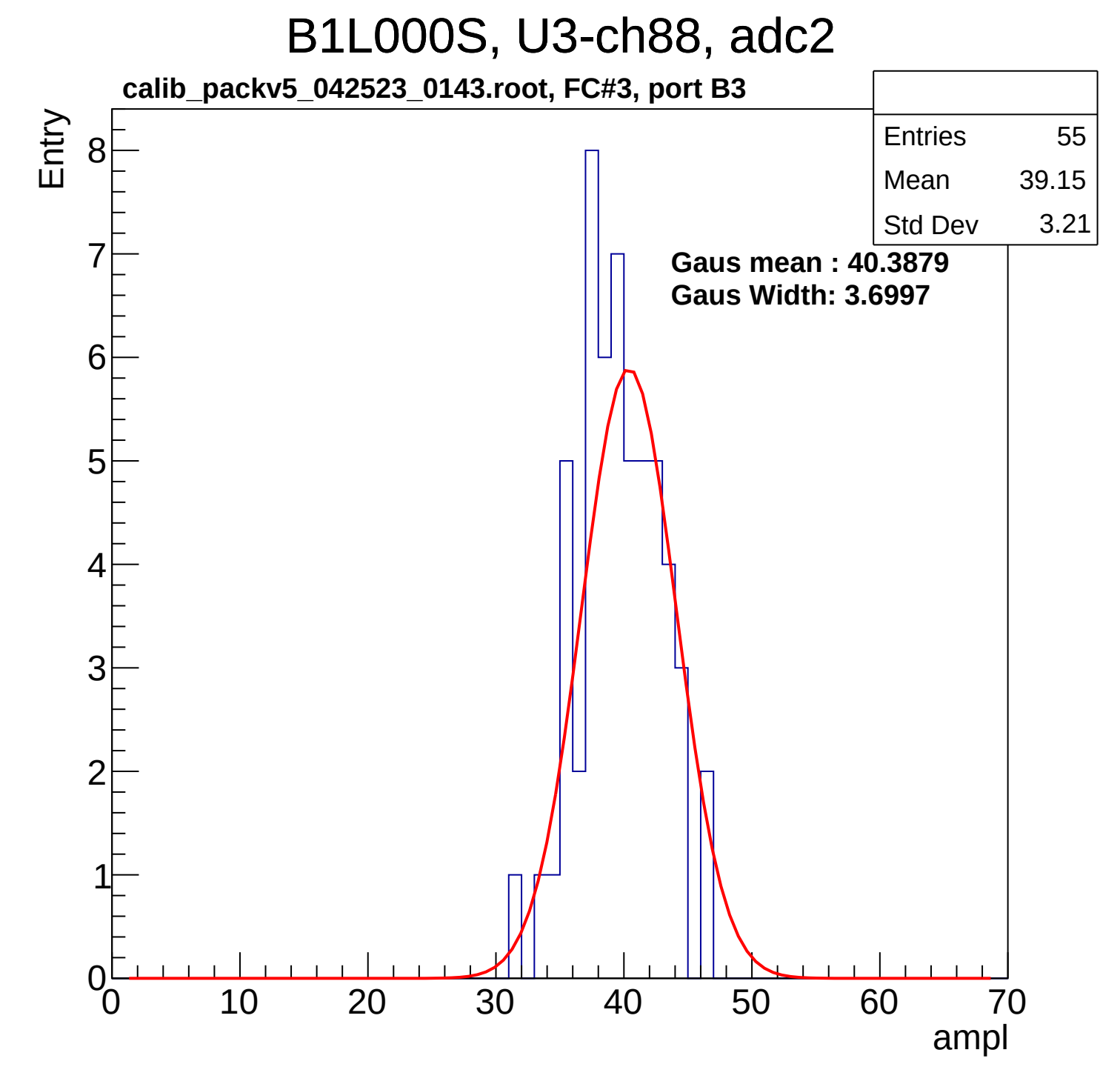
B1L000S, U3-ch88, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 39.15
Std Dev 3.21
Gaus mean : 40.3879
Gaus Width: 3.6997
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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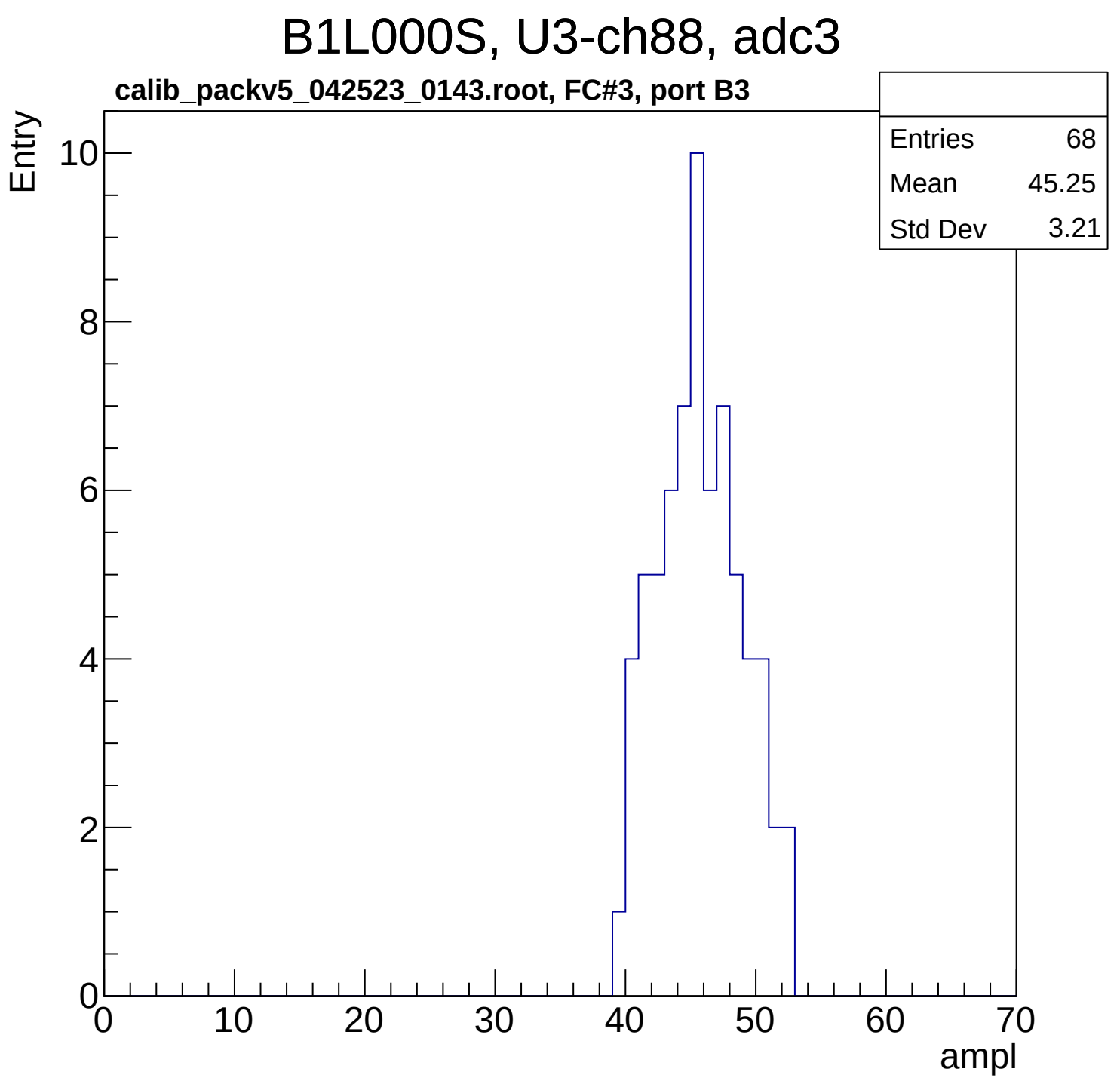
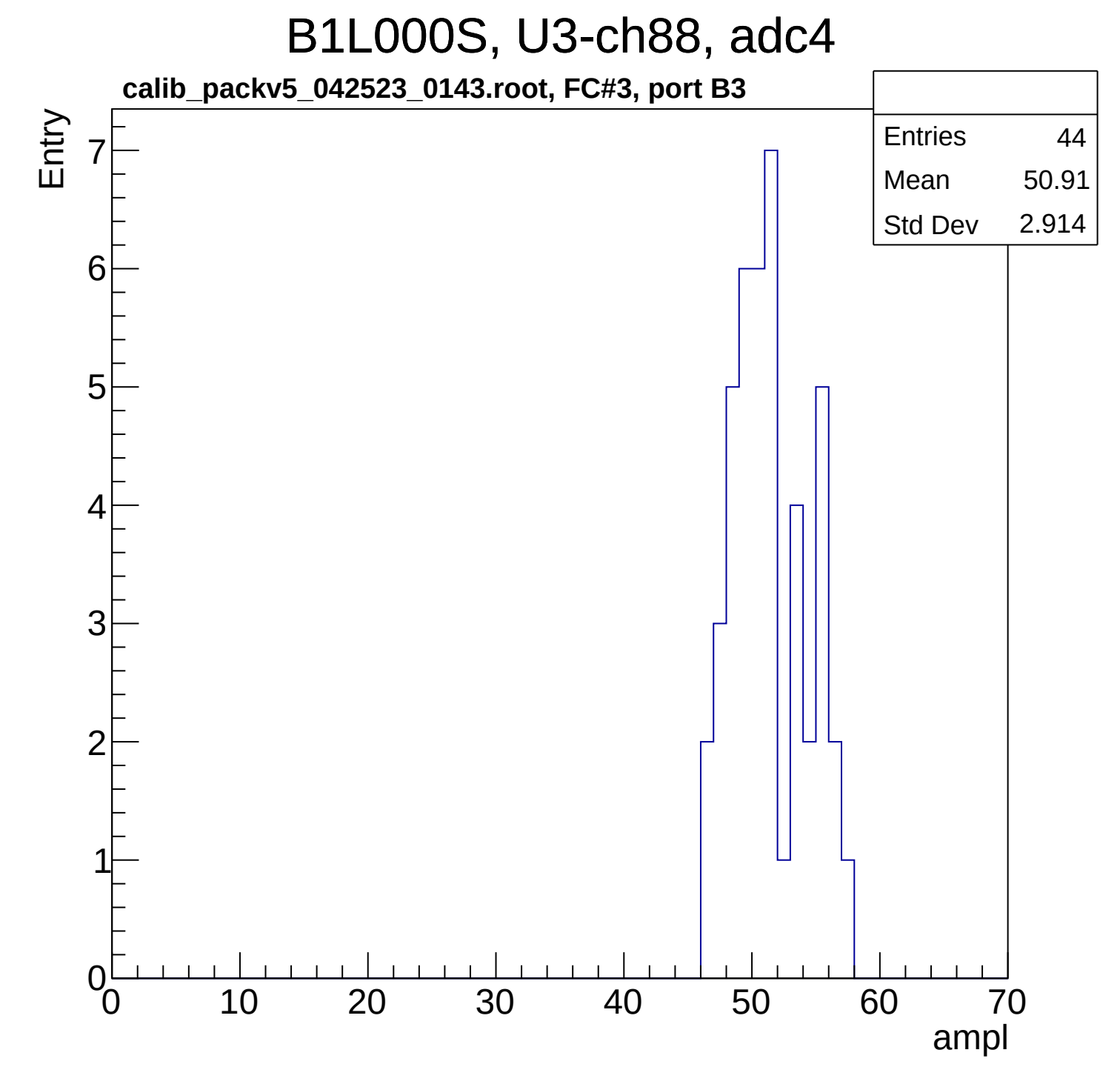

B1L000S, U3-ch88, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 45.25
Std Dev 3.21
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch88, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 50.91
Std Dev 2.914
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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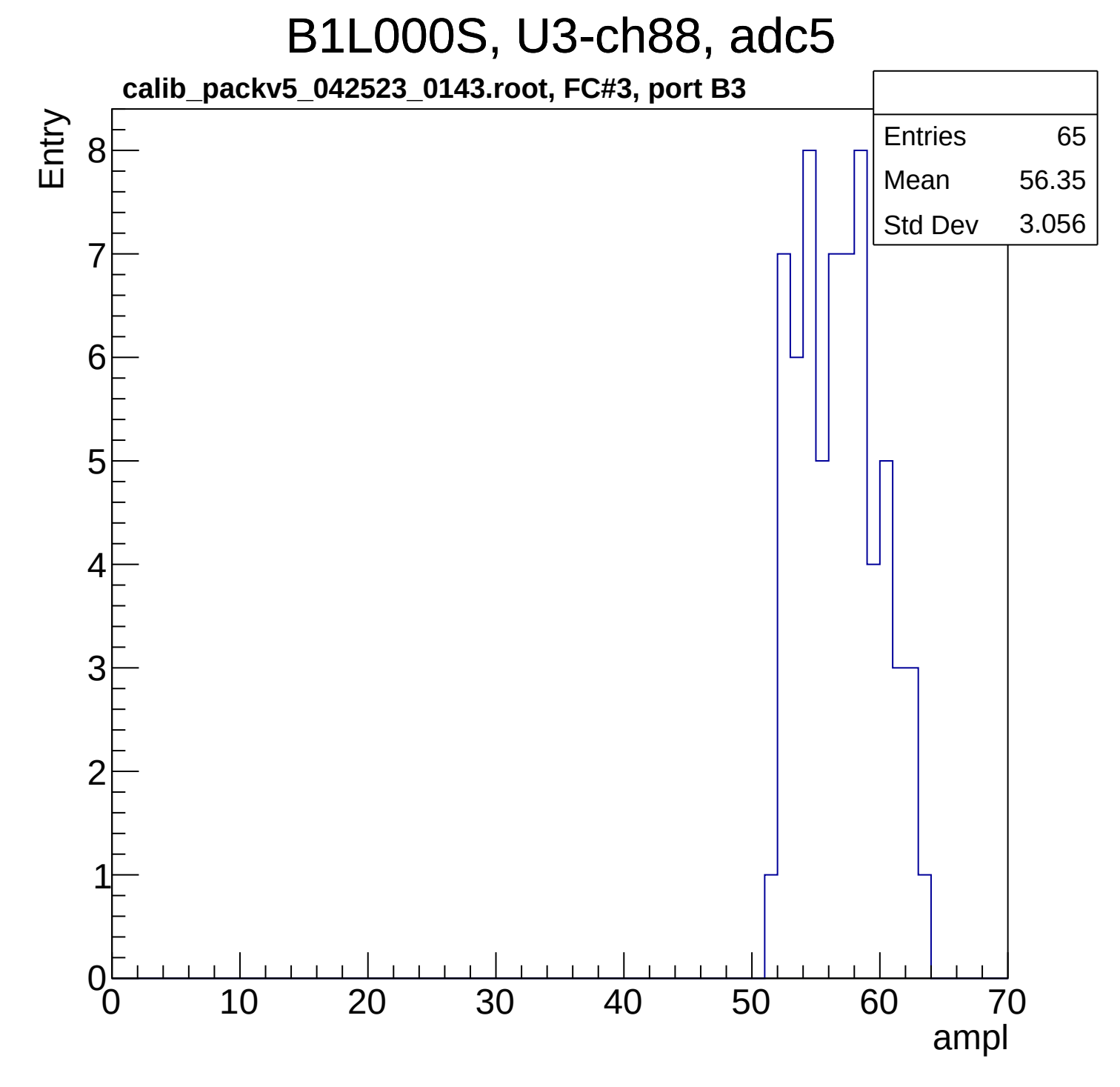
B1L000S, U3-ch88, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 56.35
Std Dev 3.056
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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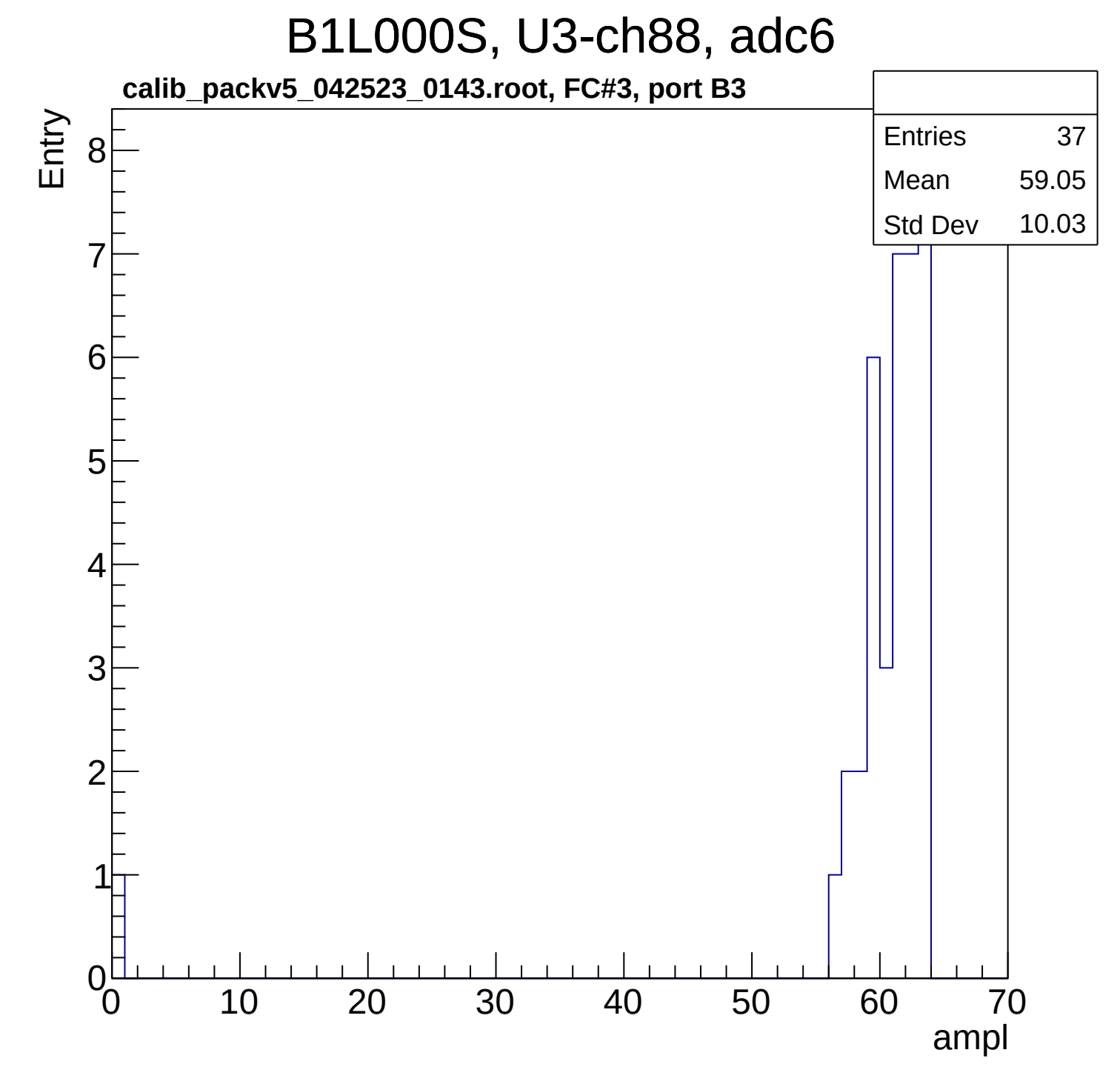
B1L000S, U3-ch88, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 59.05
Std Dev 10.03
Entry
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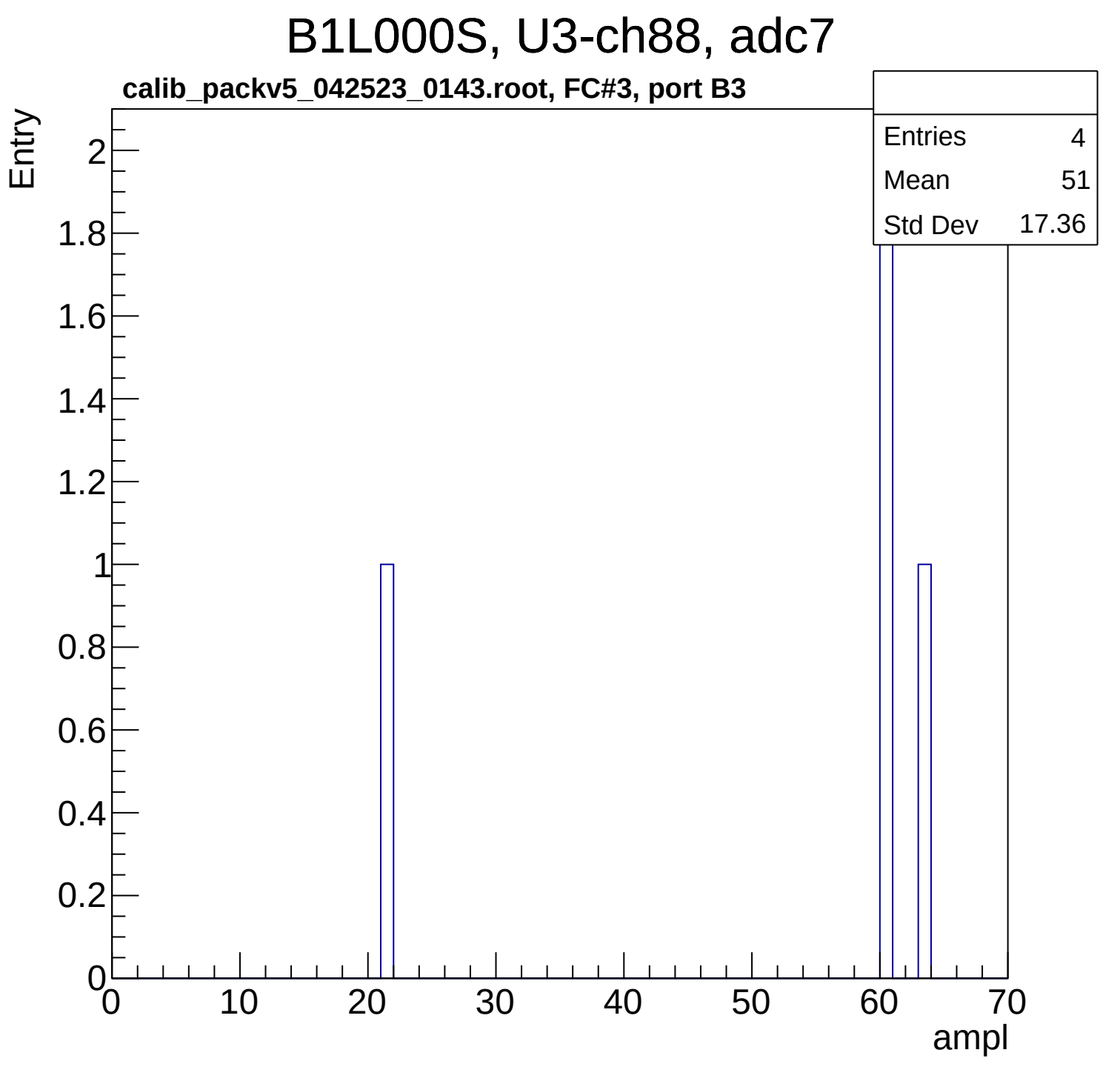
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch88, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 51
Std Dev 17.36
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
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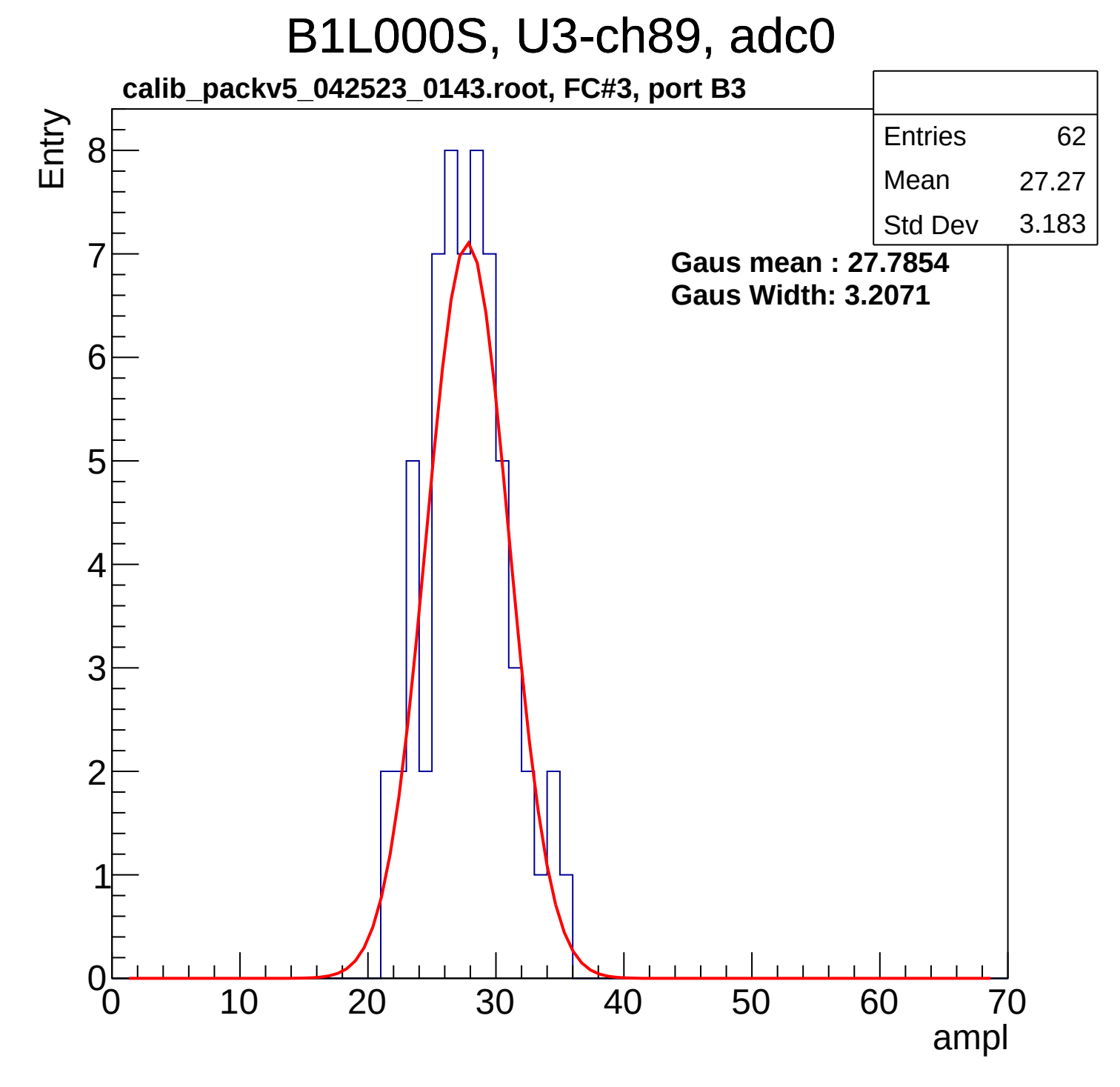
B1L000S, U3-ch89, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.27
Std Dev 3.183
Gaus mean : 27.7854
Gaus Width: 3.2071
Entry
ampl
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2
3
4
5
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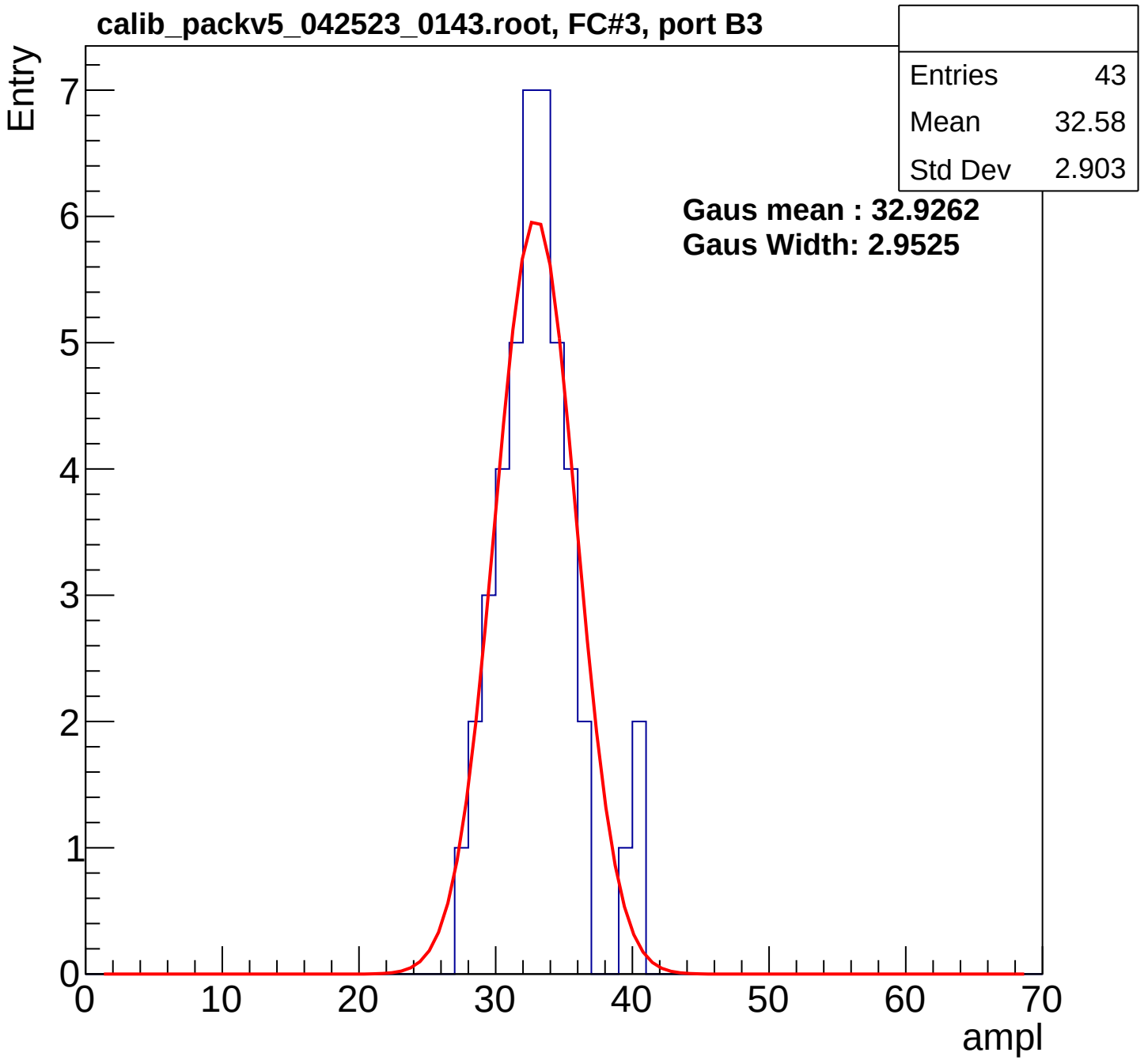
B1L000S, U3-ch89, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 32.58
Std Dev 2.903
Gaus mean : 32.9262
Gaus Width: 2.9525
Entry
ampl
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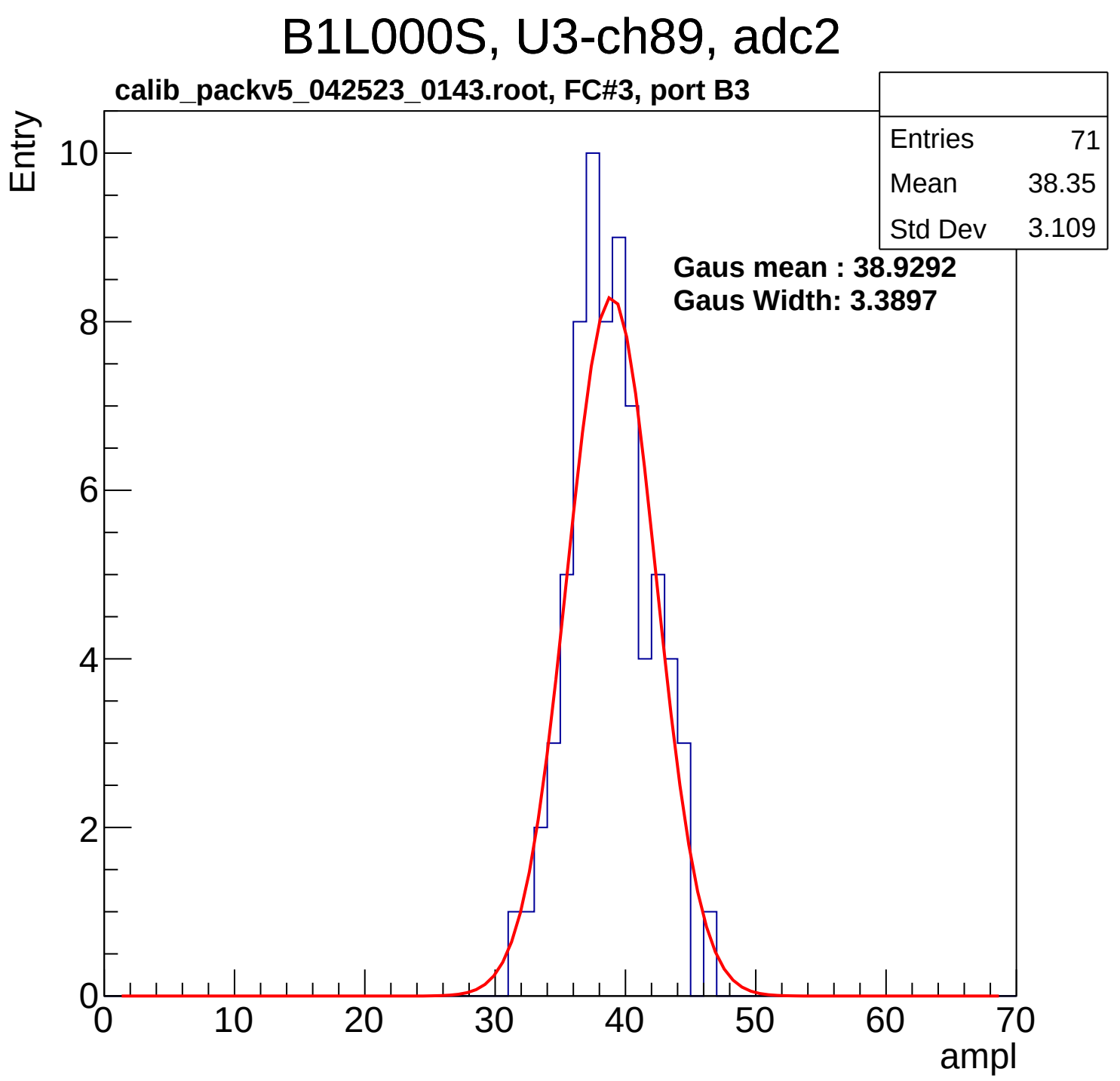
B1L000S, U3-ch89, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 38.35
Std Dev 3.109
Gaus mean : 38.9292
Gaus Width: 3.3897
Entry
ampl
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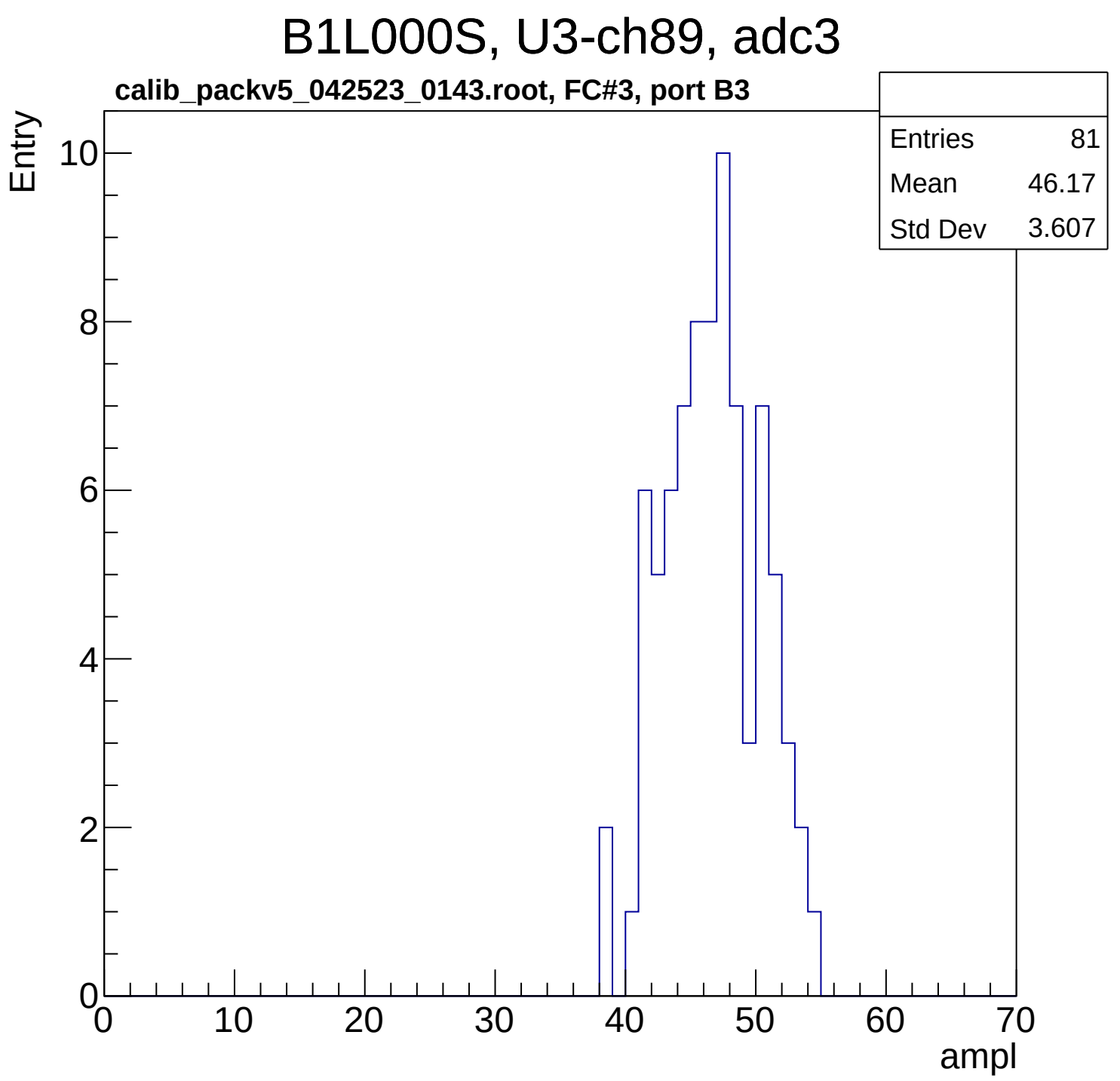
B1L000S, U3-ch89, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 46.17
Std Dev 3.607
Entry
ampl
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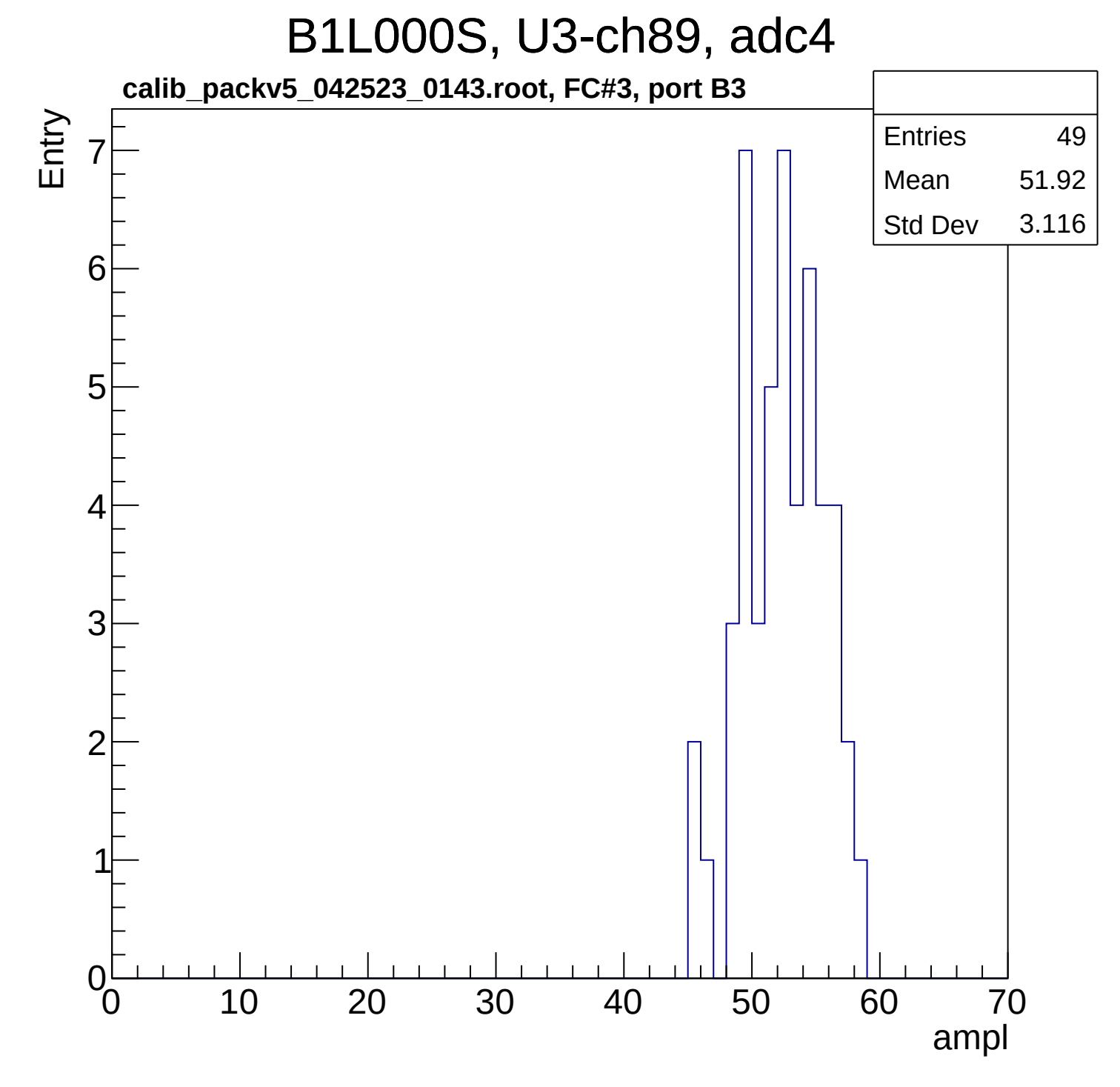
B1L000S, U3-ch89, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 51.92
Std Dev 3.116
Entry
ampl
0
1
2
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4
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0
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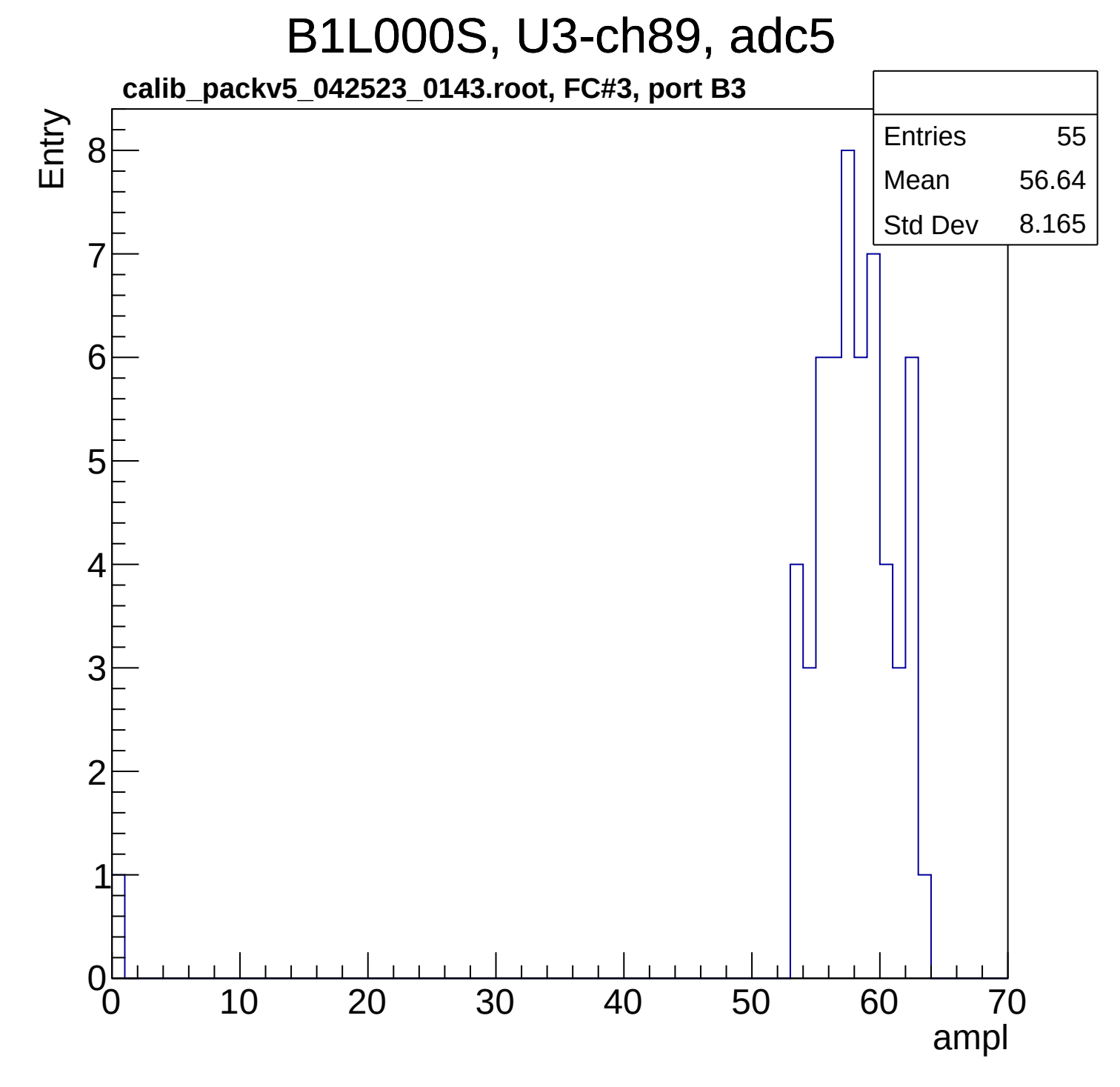
B1L000S, U3-ch89, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 56.64
Std Dev 8.165
Entry
ampl
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2
3
4
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6
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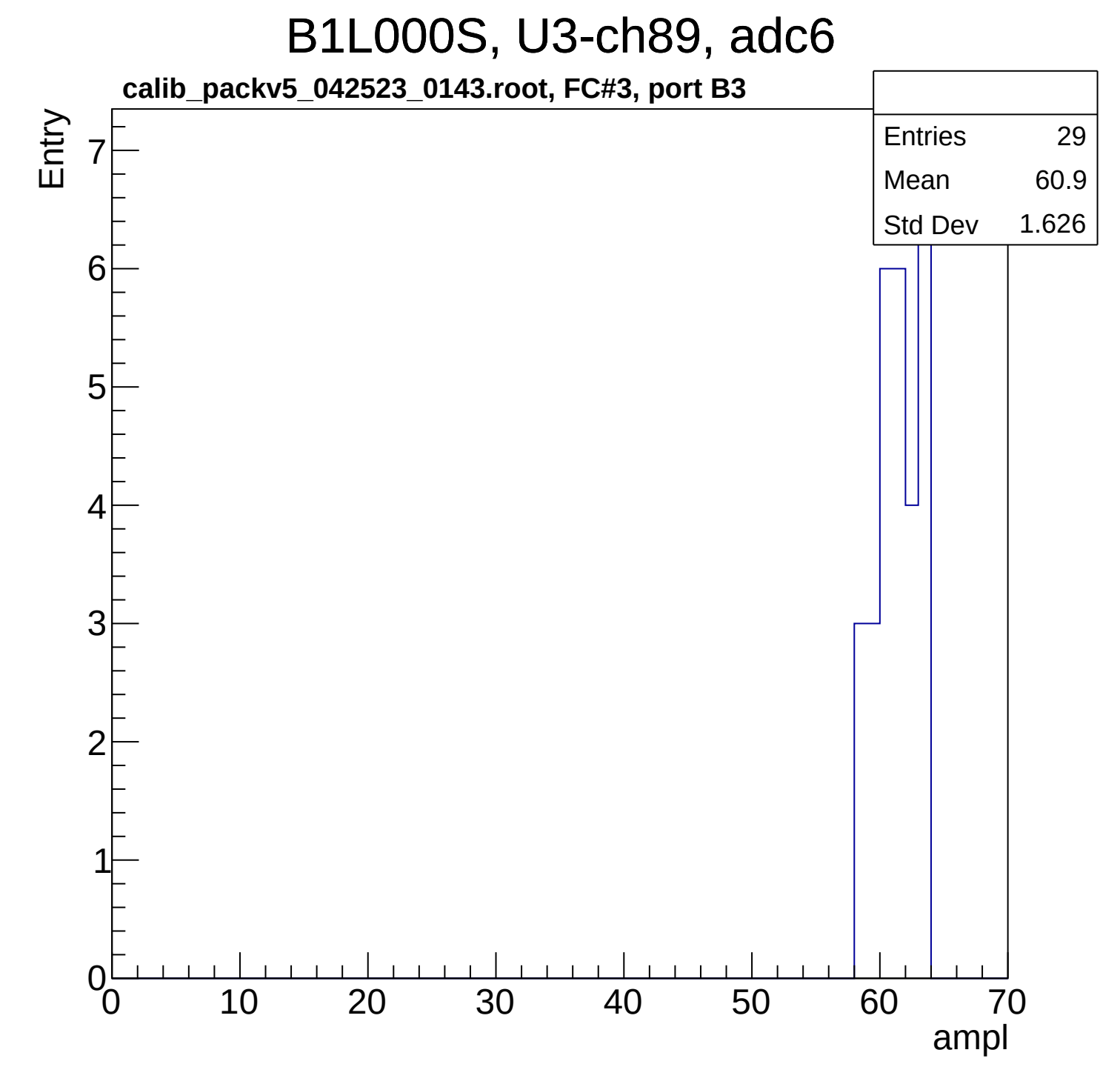
B1L000S, U3-ch89, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 29
Mean 60.9
Std Dev 1.626
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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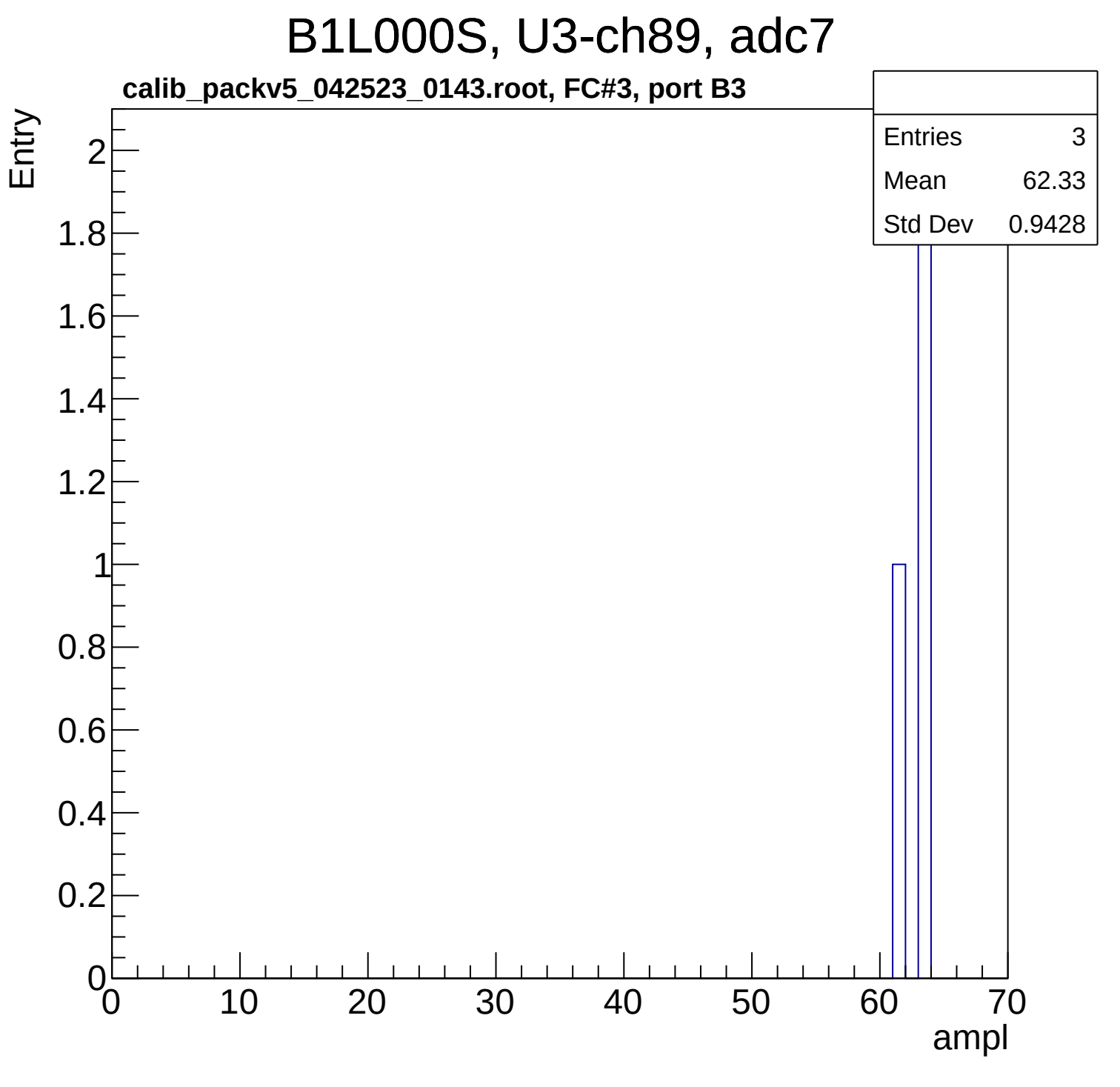
B1L000S, U3-ch89, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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40
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60
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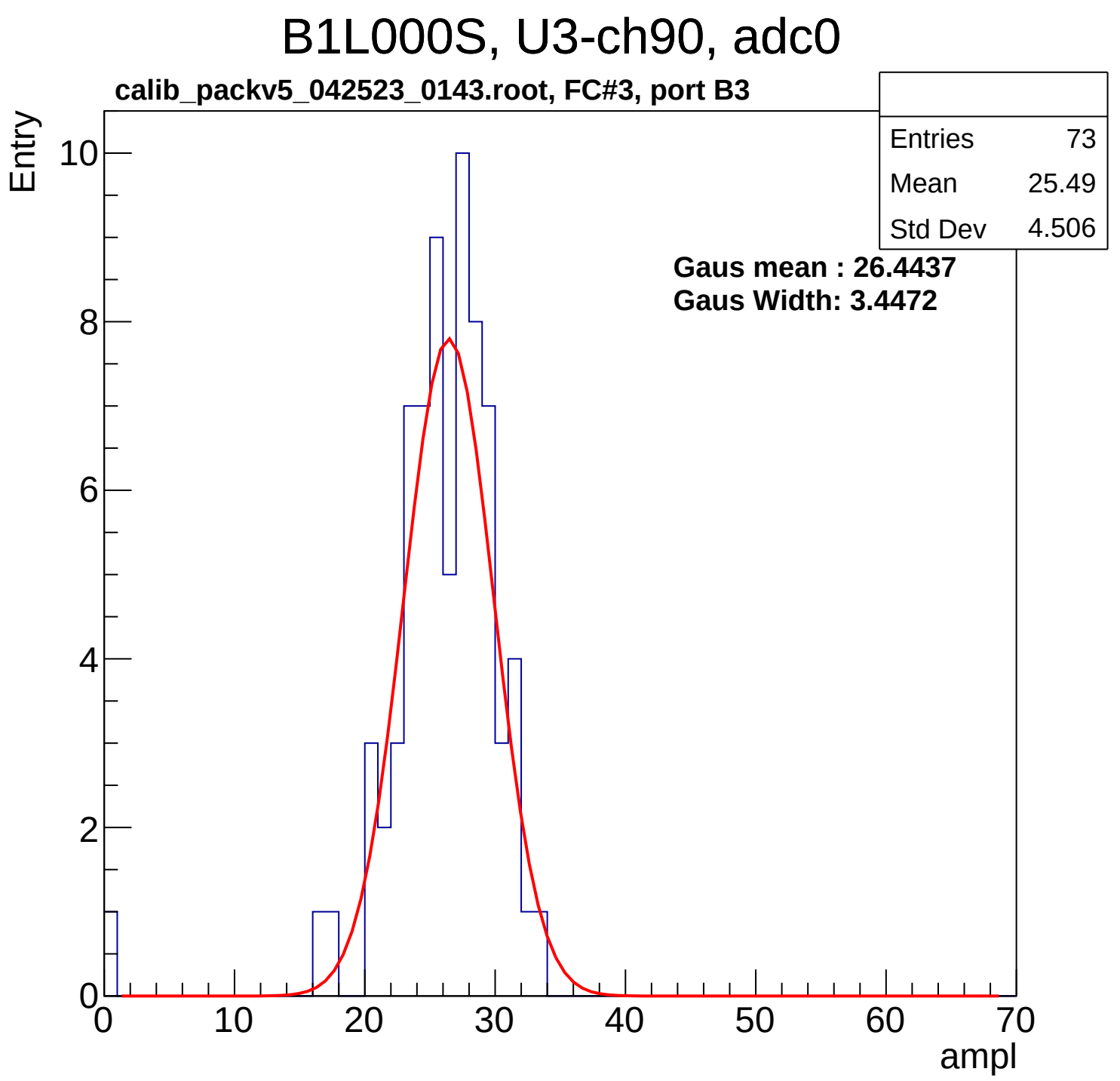
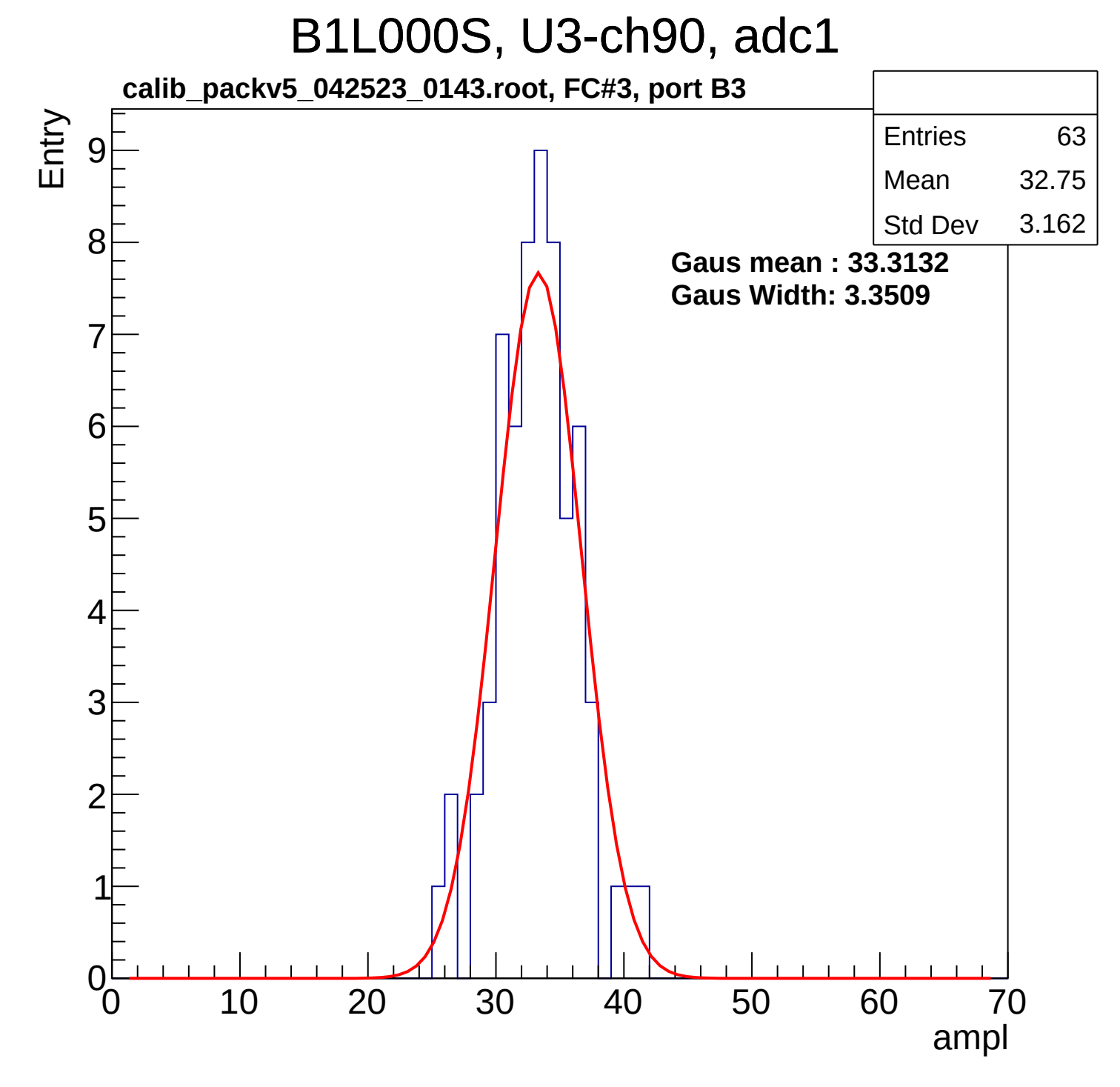

B1L000S, U3-ch90, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 25.49
Std Dev 4.506
Gaus mean : 26.4437
Gaus Width: 3.4472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch90, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 32.75
Std Dev 3.162
Gaus mean : 33.3132
Gaus Width: 3.3509
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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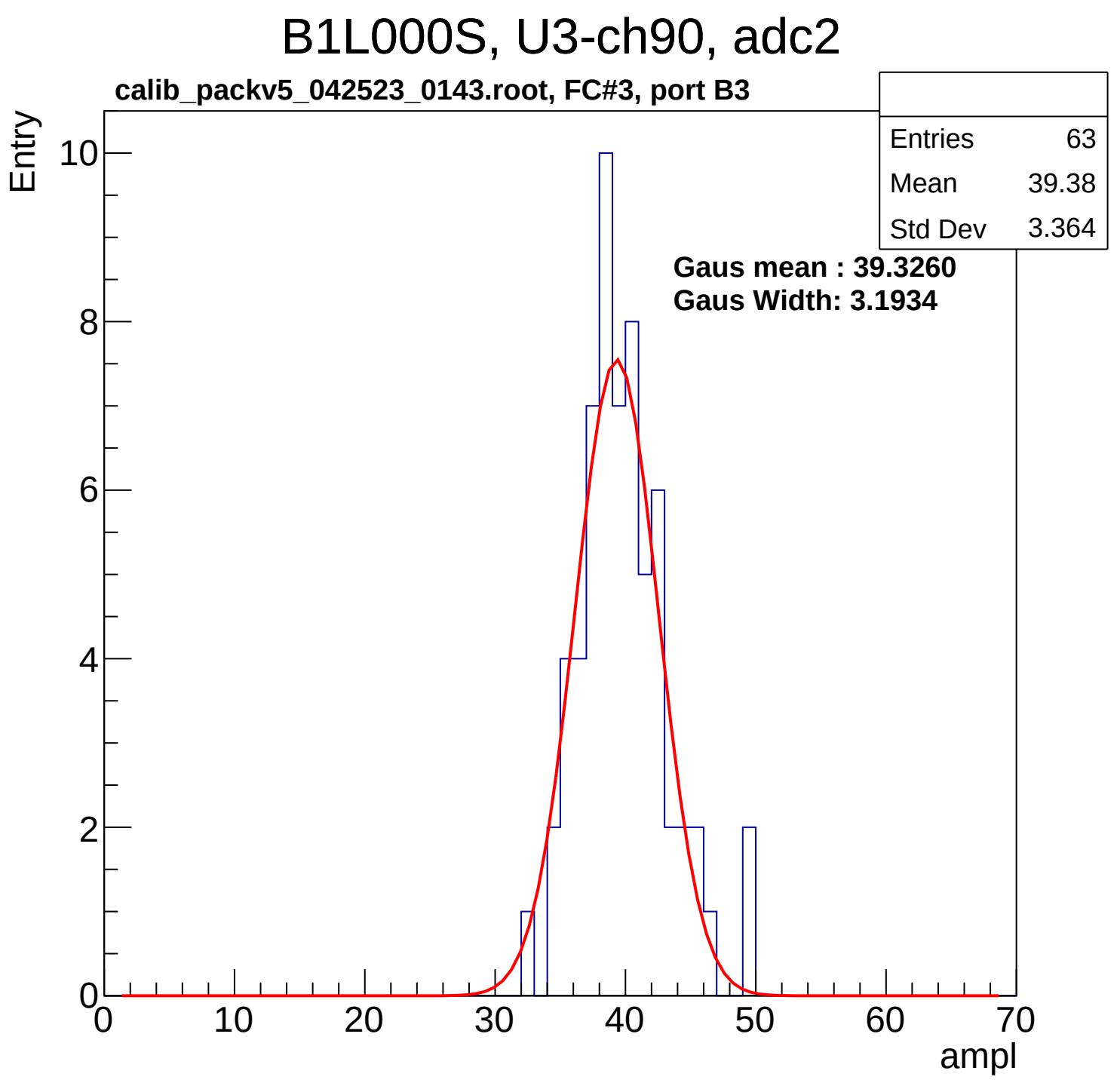
B1L000S, U3-ch90, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 39.38
Std Dev 3.364
Gaus mean : 39.3260
Gaus Width: 3.1934
Entry
ampl
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2
4
6
8
10
0
10
20
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40
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60
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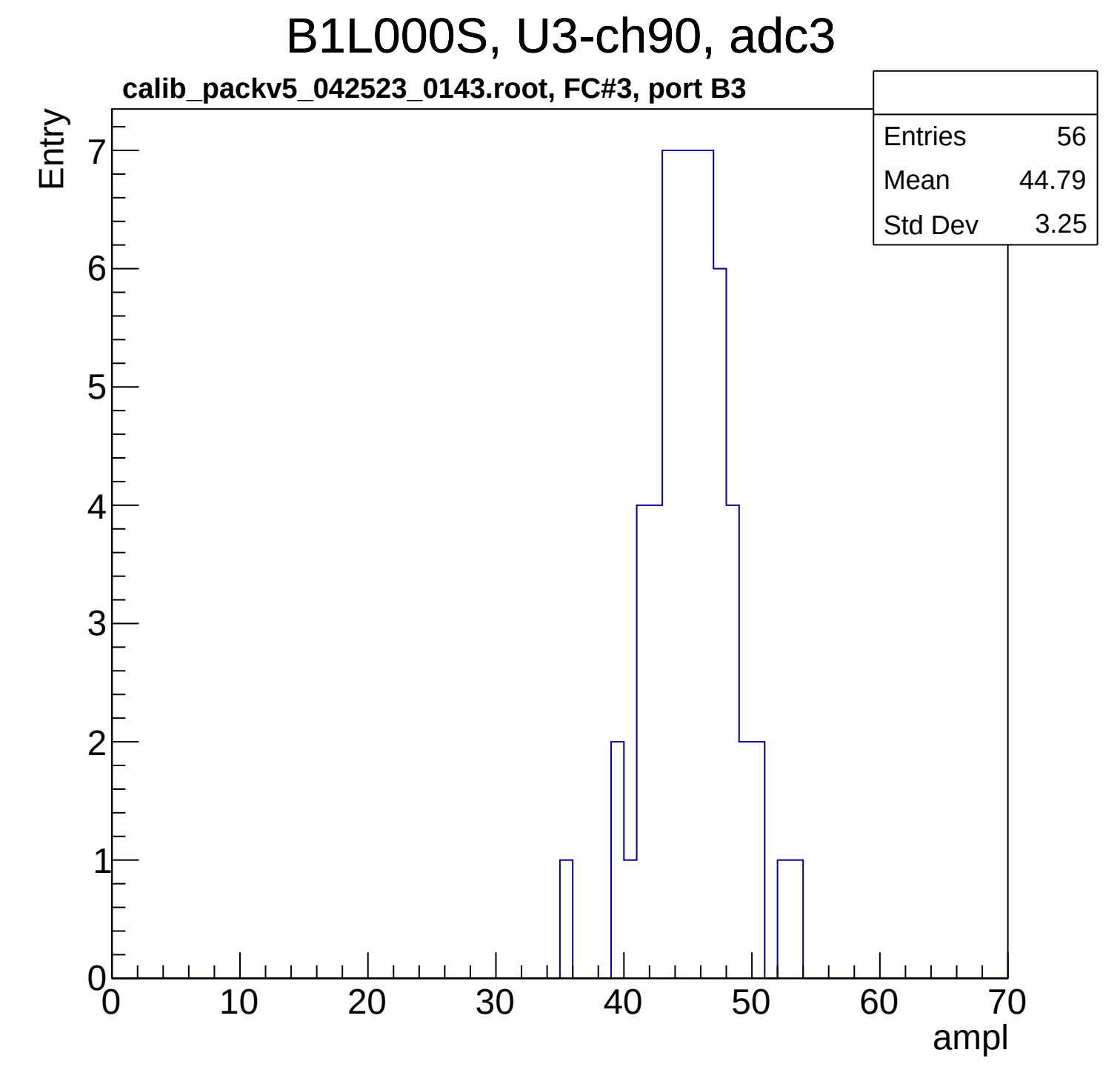
B1L000S, U3-ch90, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 44.79
Std Dev 3.25
Entry
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1
2
3
4
5
6
7
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10
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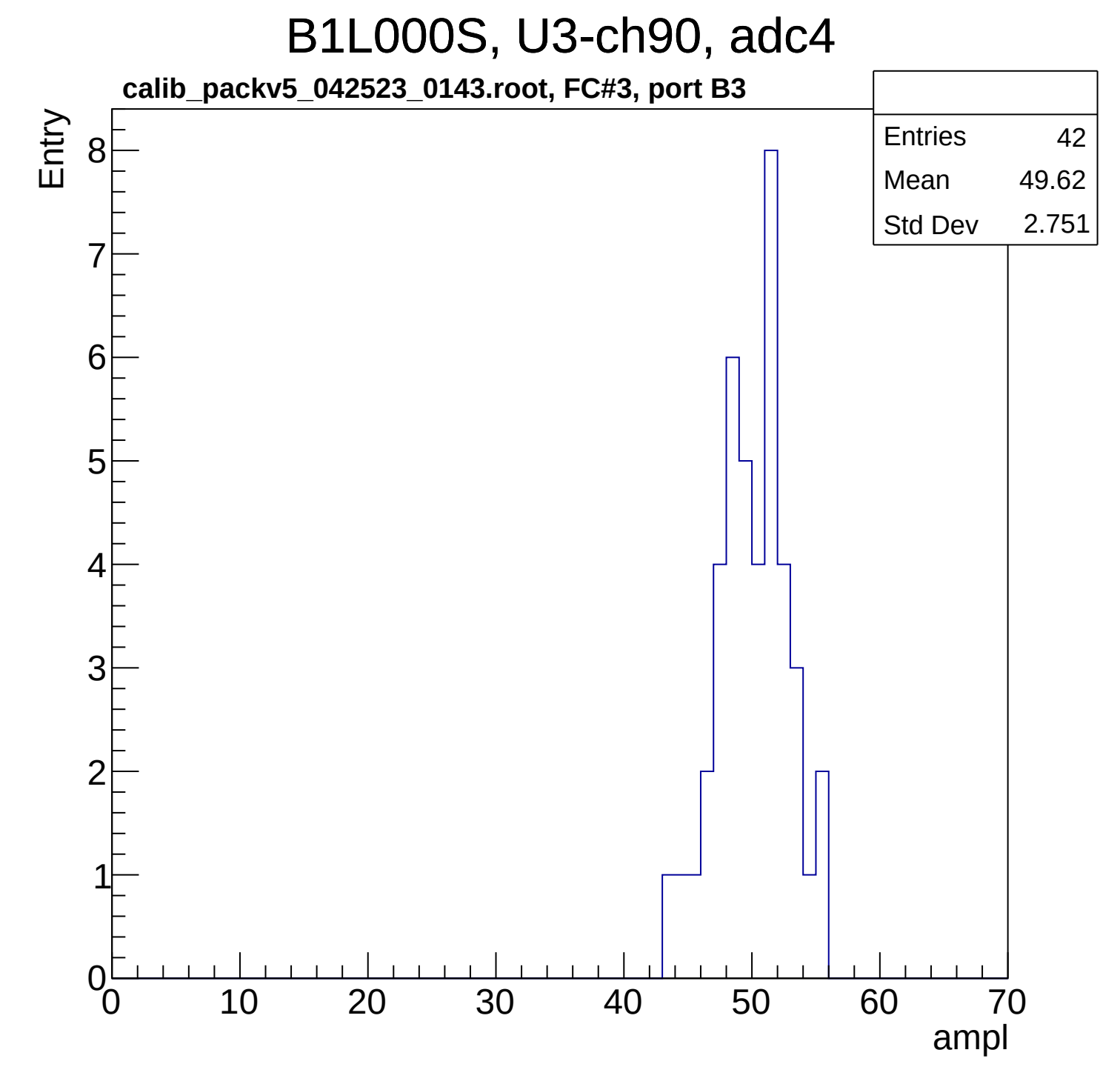
50
60
70
ampl


B1L000S, U3-ch90, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 49.62
Std Dev 2.751
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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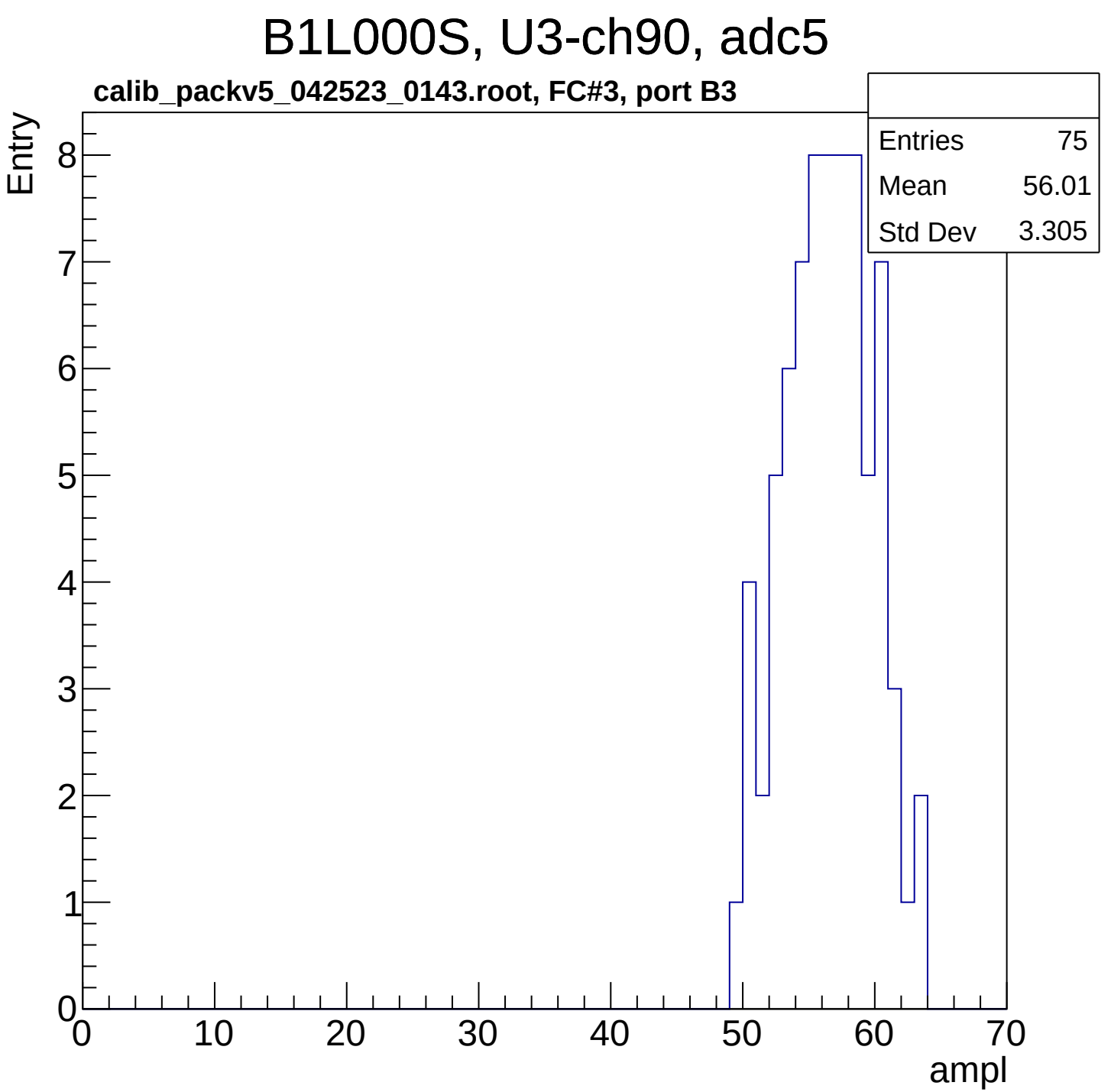
B1L000S, U3-ch90, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 56.01
Std Dev 3.305
Entry
ampl
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1
2
3
4
5
6
7
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0
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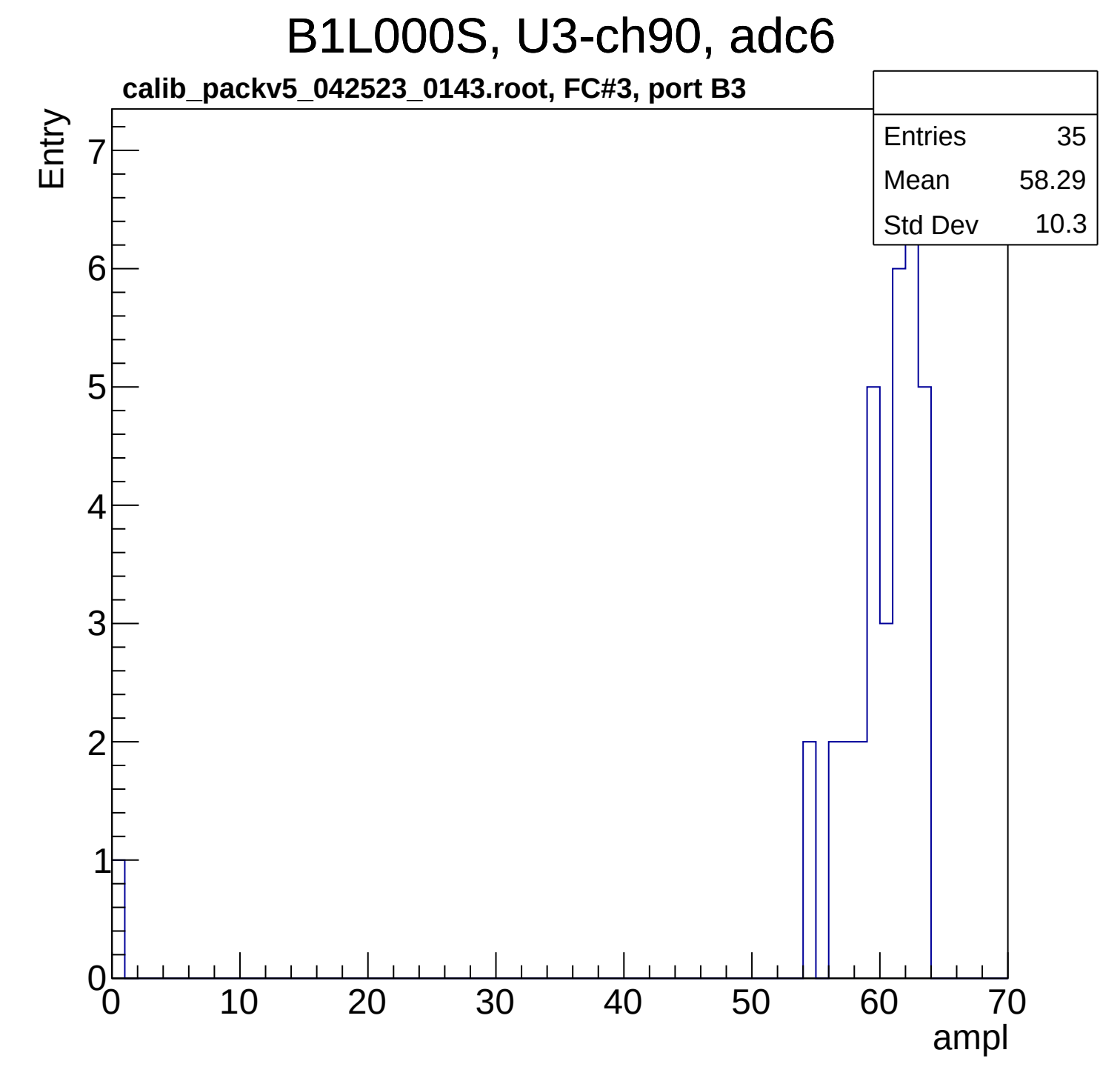
B1L000S, U3-ch90, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 35
Mean 58.29
Std Dev 10.3
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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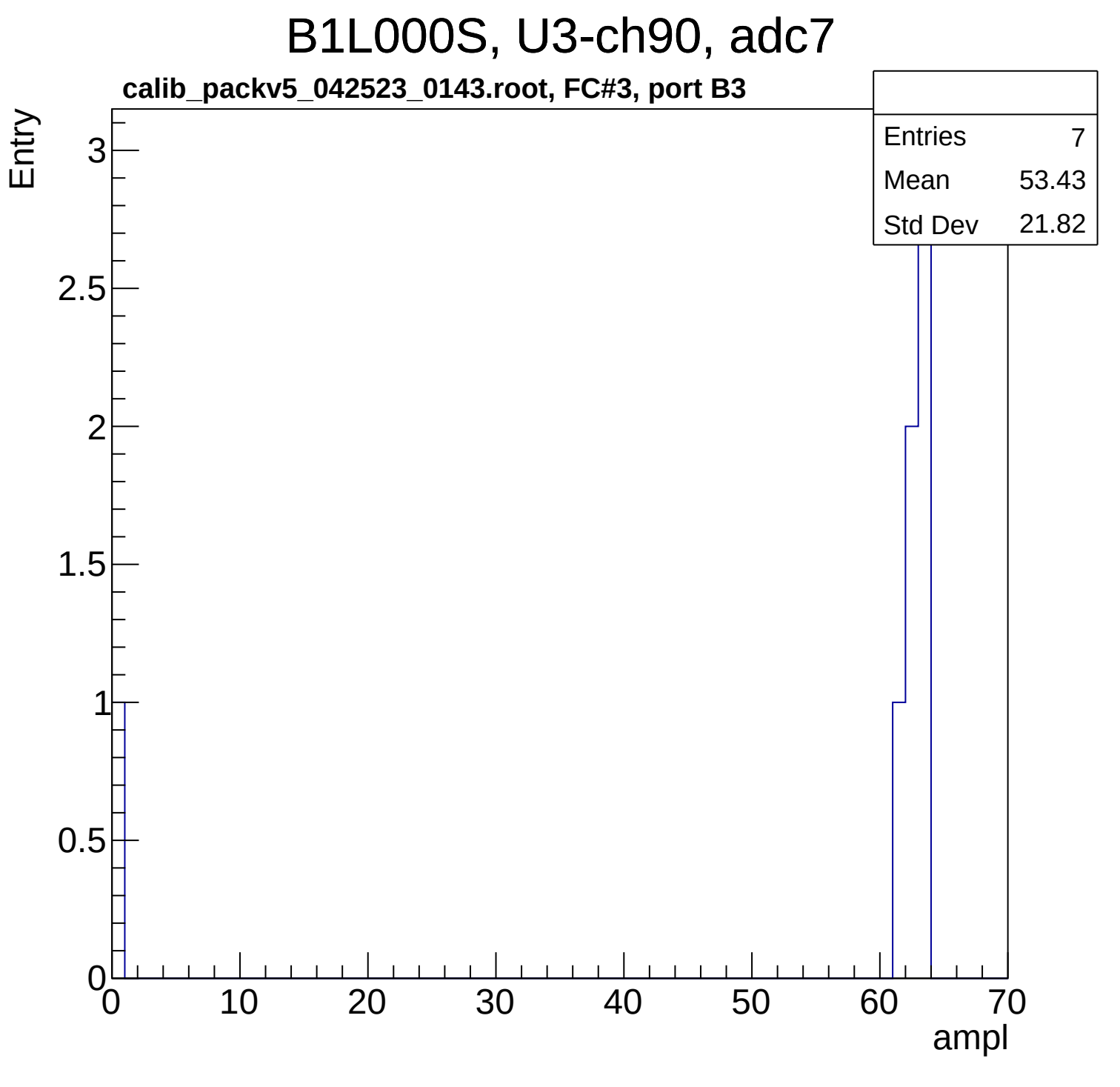
40
50
60
70


B1L000S, U3-ch90, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 53.43
Std Dev 21.82
Entry
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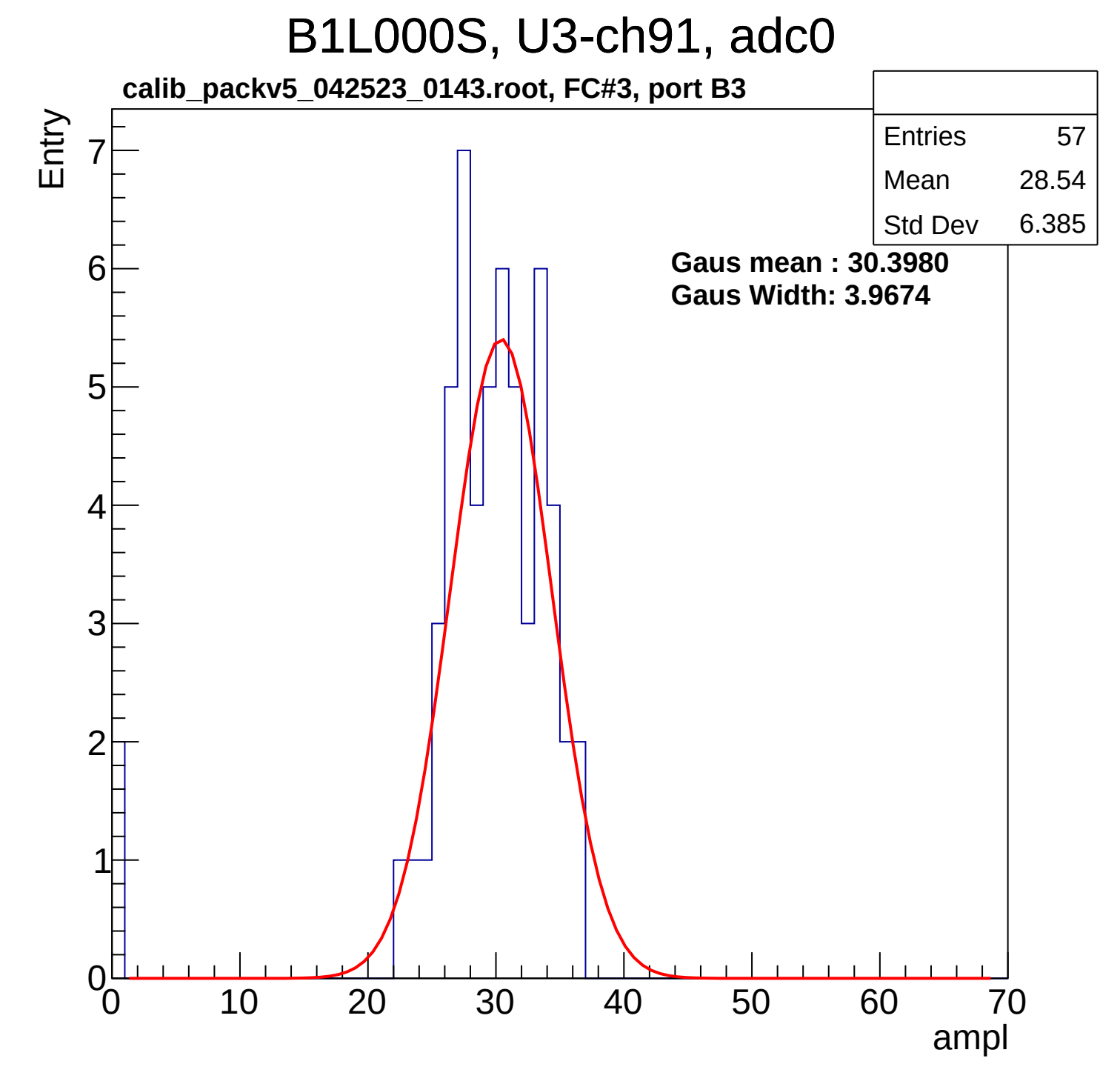
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch91, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 28.54
Std Dev 6.385
Gaus mean : 30.3980
Gaus Width: 3.9674
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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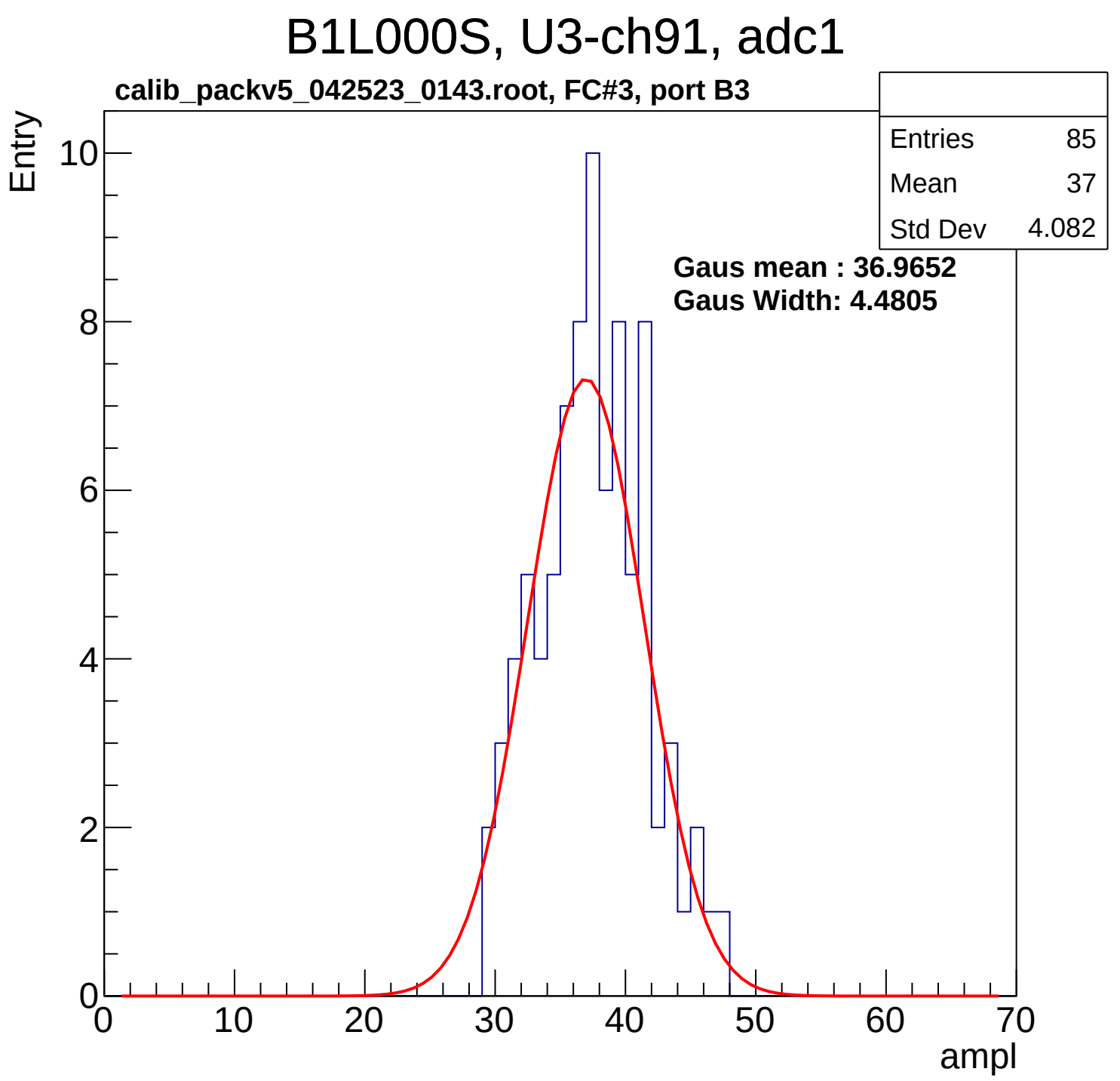
B1L000S, U3-ch91, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 85
Mean 37
Std Dev 4.082
Gaus mean : 36.9652
Gaus Width: 4.4805
Entry
ampl
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4
6
8
10
0
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20
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60
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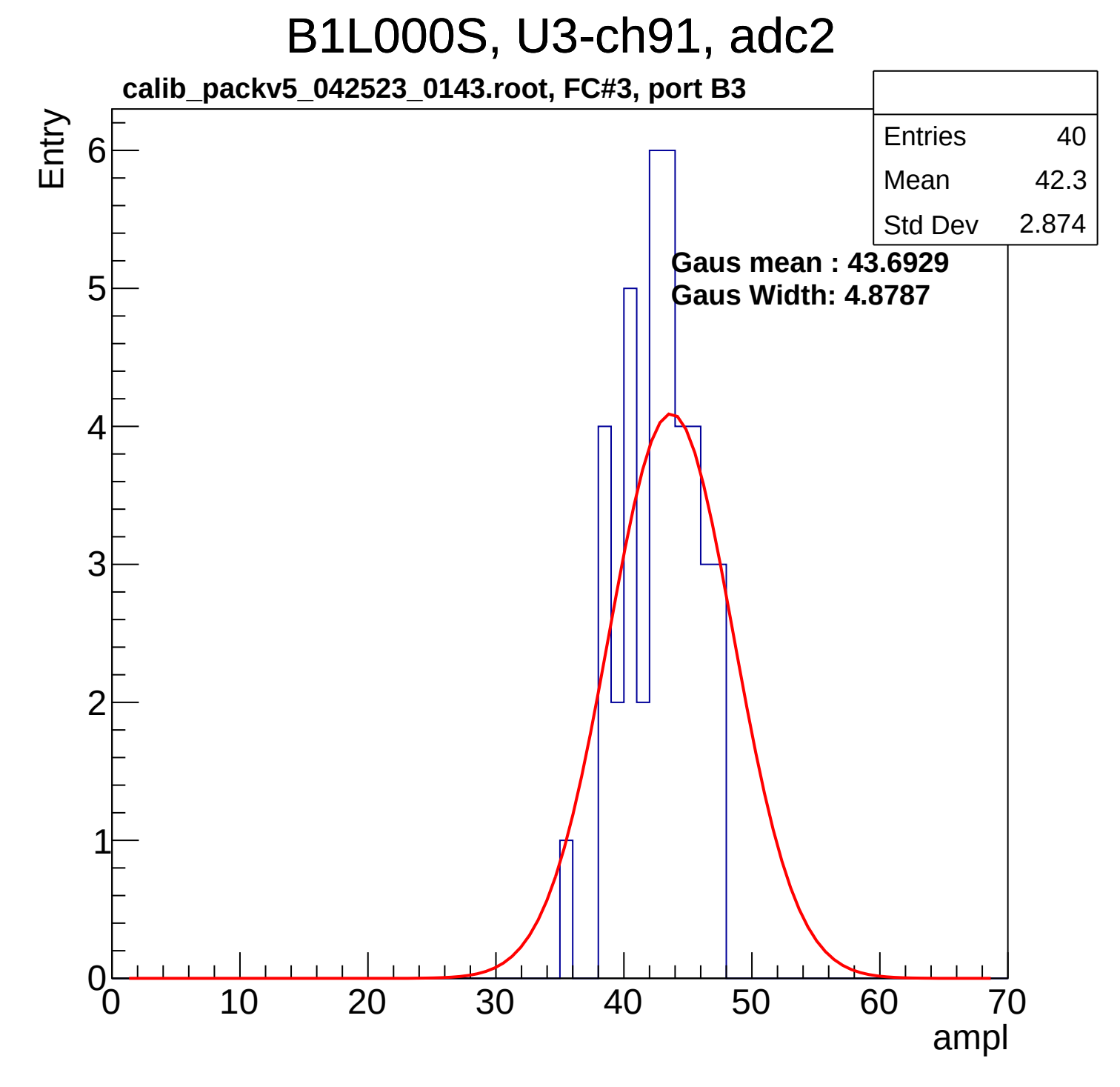
B1L000S, U3-ch91, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 42.3
Std Dev 2.874
Gaus mean : 43.6929
Gaus Width: 4.8787
Entry
ampl
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1
2
3
4
5
6
0
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60
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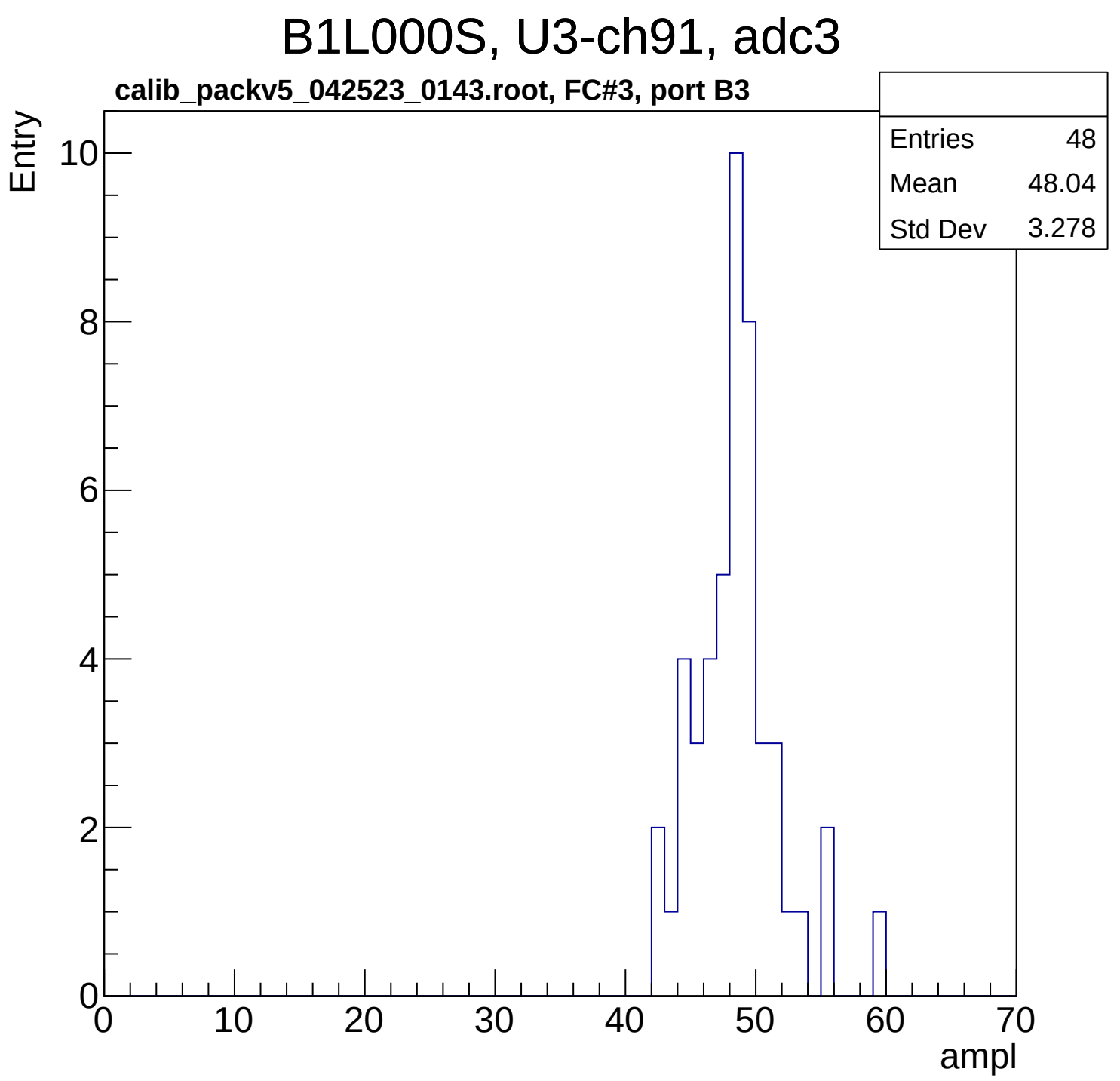
B1L000S, U3-ch91, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 48.04
Std Dev 3.278
Entry
ampl
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8
10
0
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60
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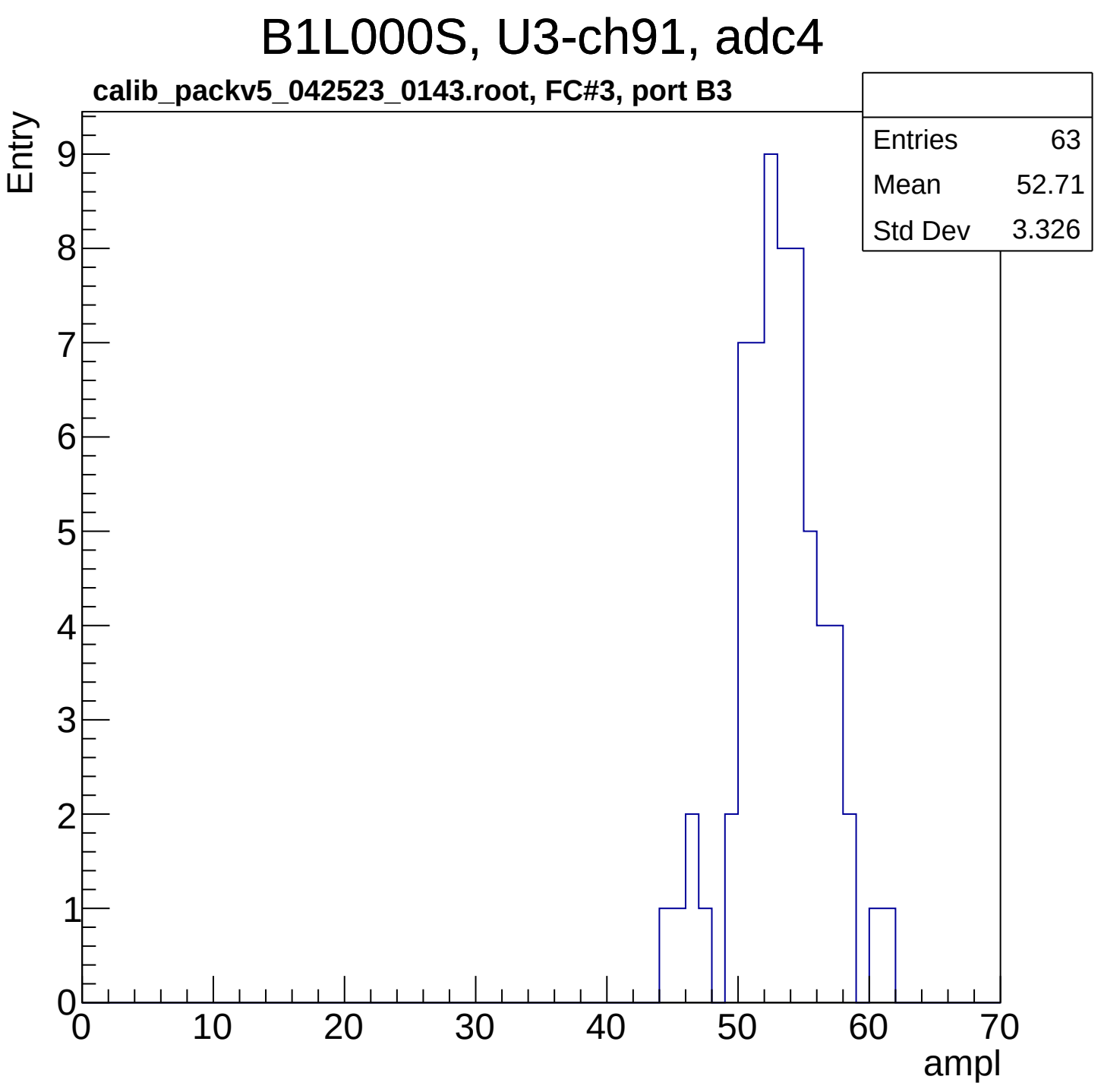
B1L000S, U3-ch91, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 52.71
Std Dev 3.326
Entry
ampl
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4
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6
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0
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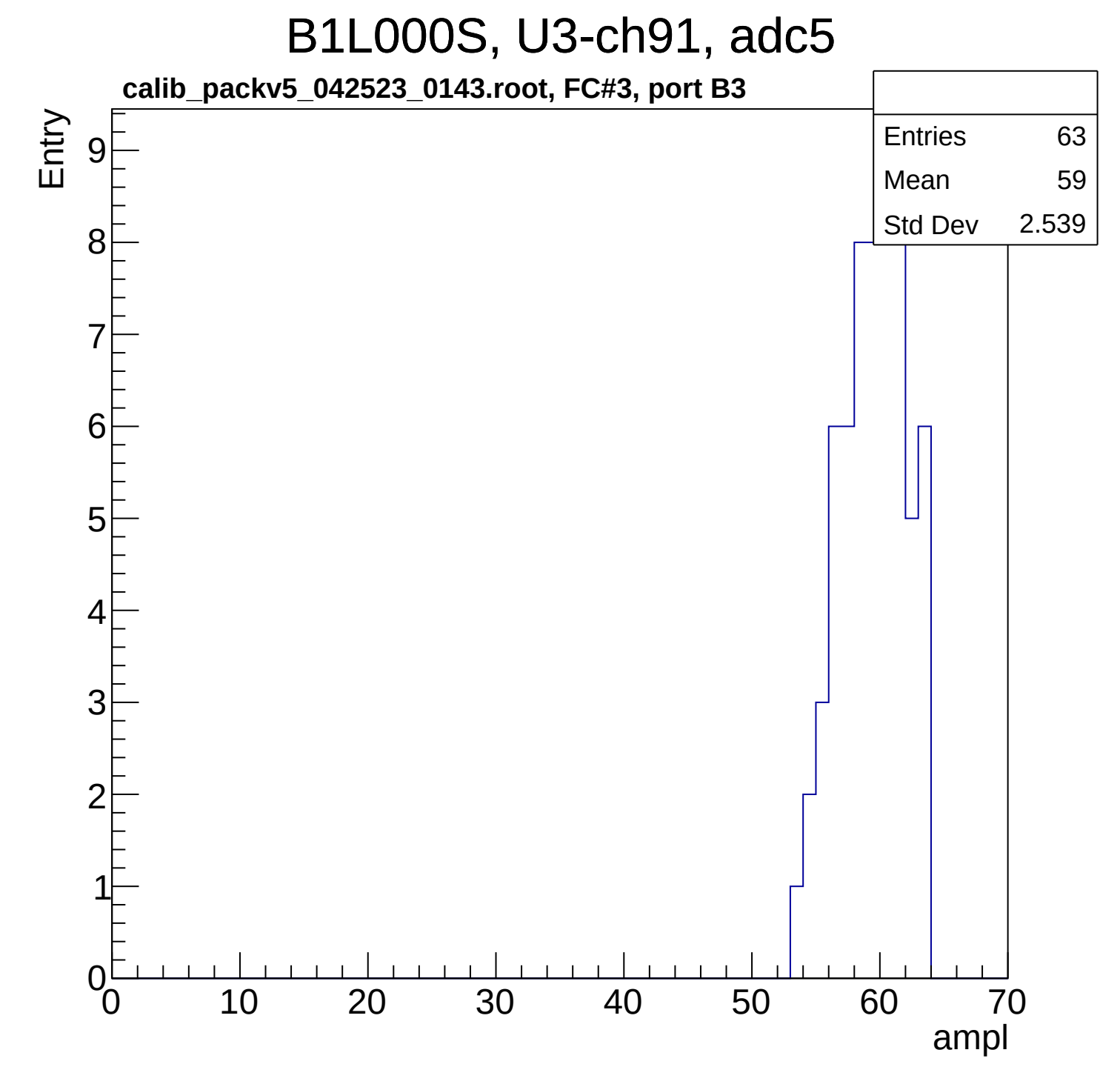
B1L000S, U3-ch91, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 59
Std Dev 2.539
Entry
ampl
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3
4
5
6
7
8
9
0
10
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60
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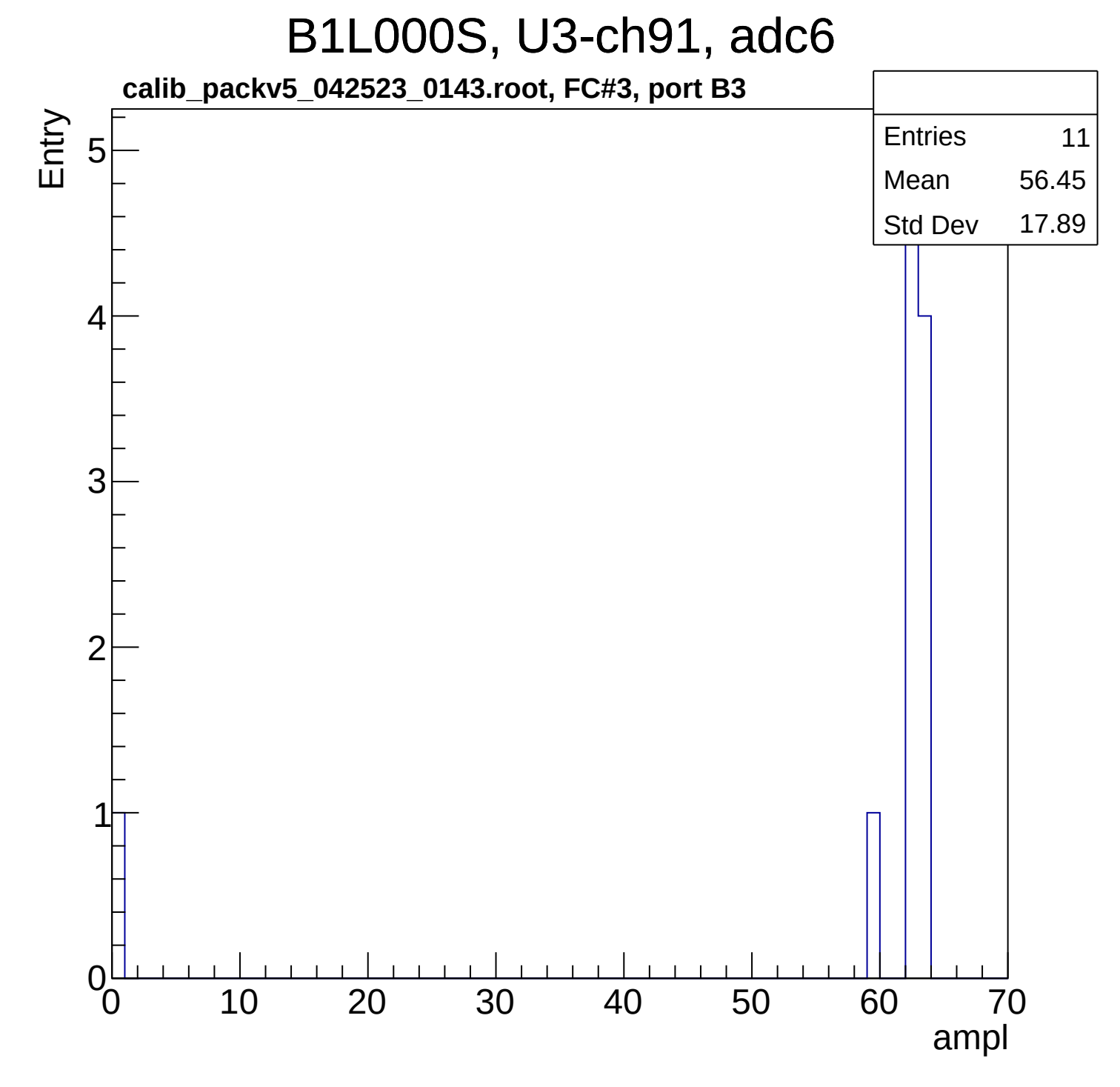
B1L000S, U3-ch91, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 56.45
Std Dev 17.89
Entry
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3
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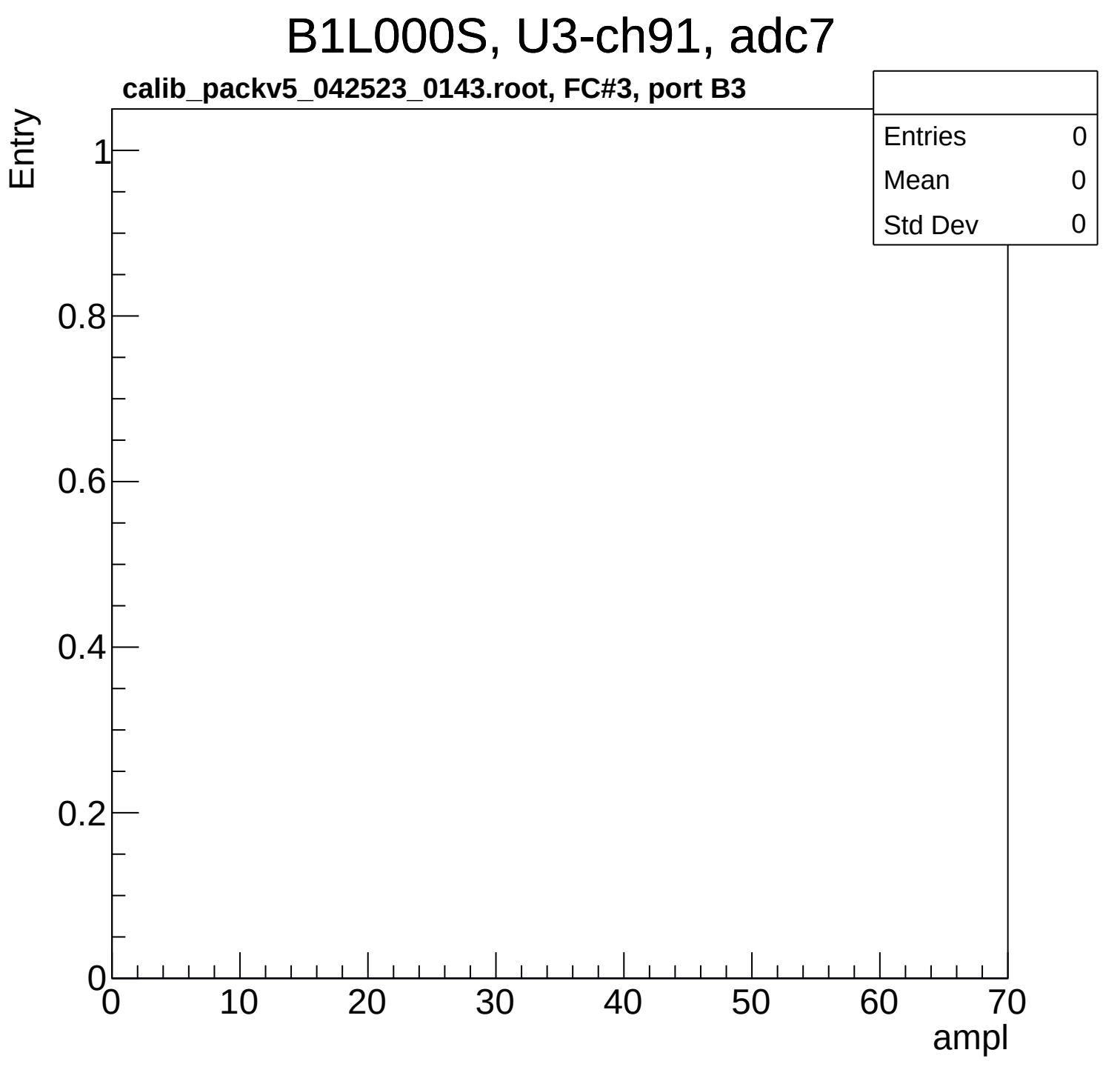
50
60
70
ampl


B1L000S, U3-ch91, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
20
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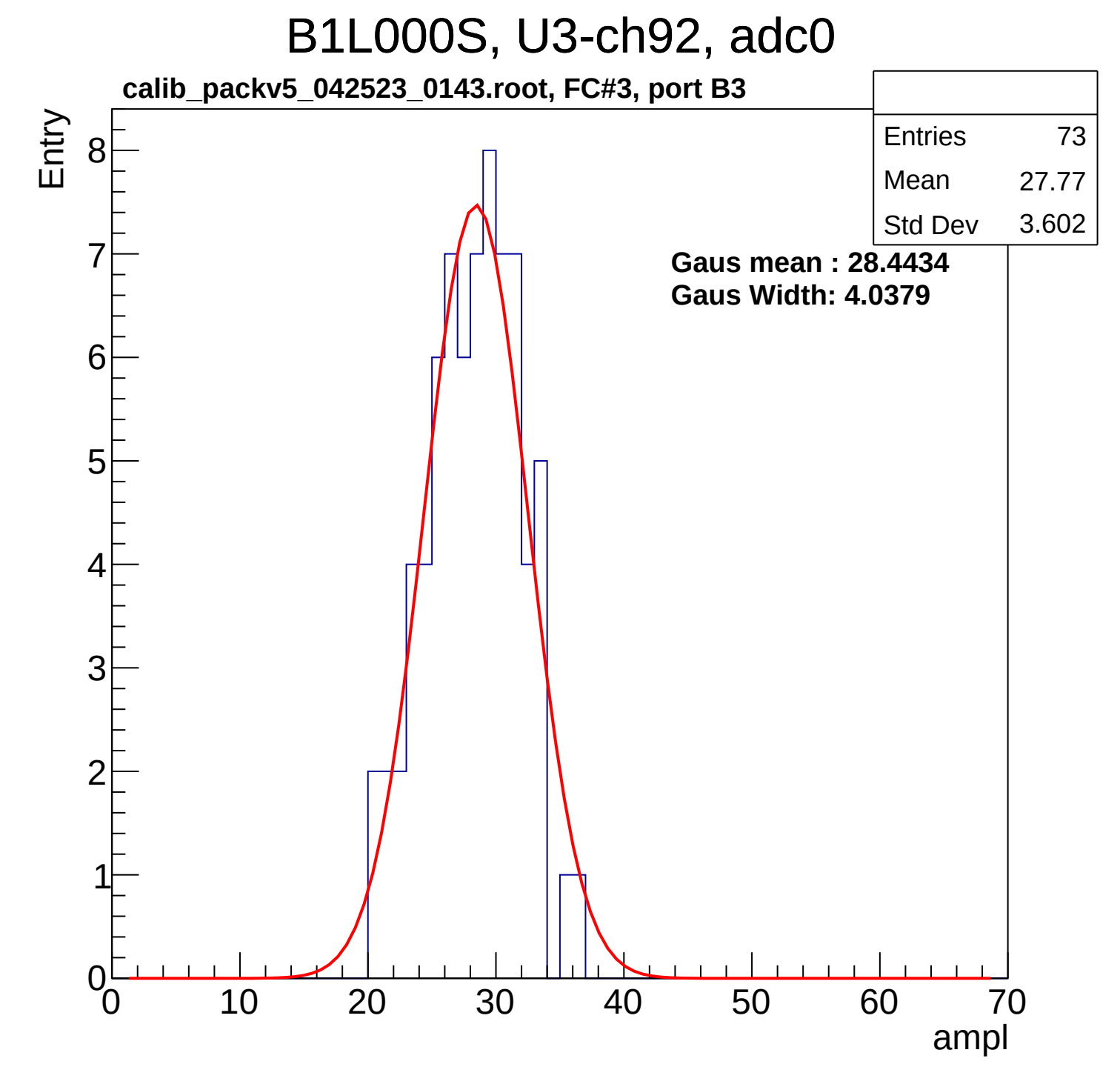
40
50
60
70
ampl


B1L000S, U3-ch92, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 27.77
Std Dev 3.602
Gaus mean : 28.4434
Gaus Width: 4.0379
Entry
ampl
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2
3
4
5
6
7
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0
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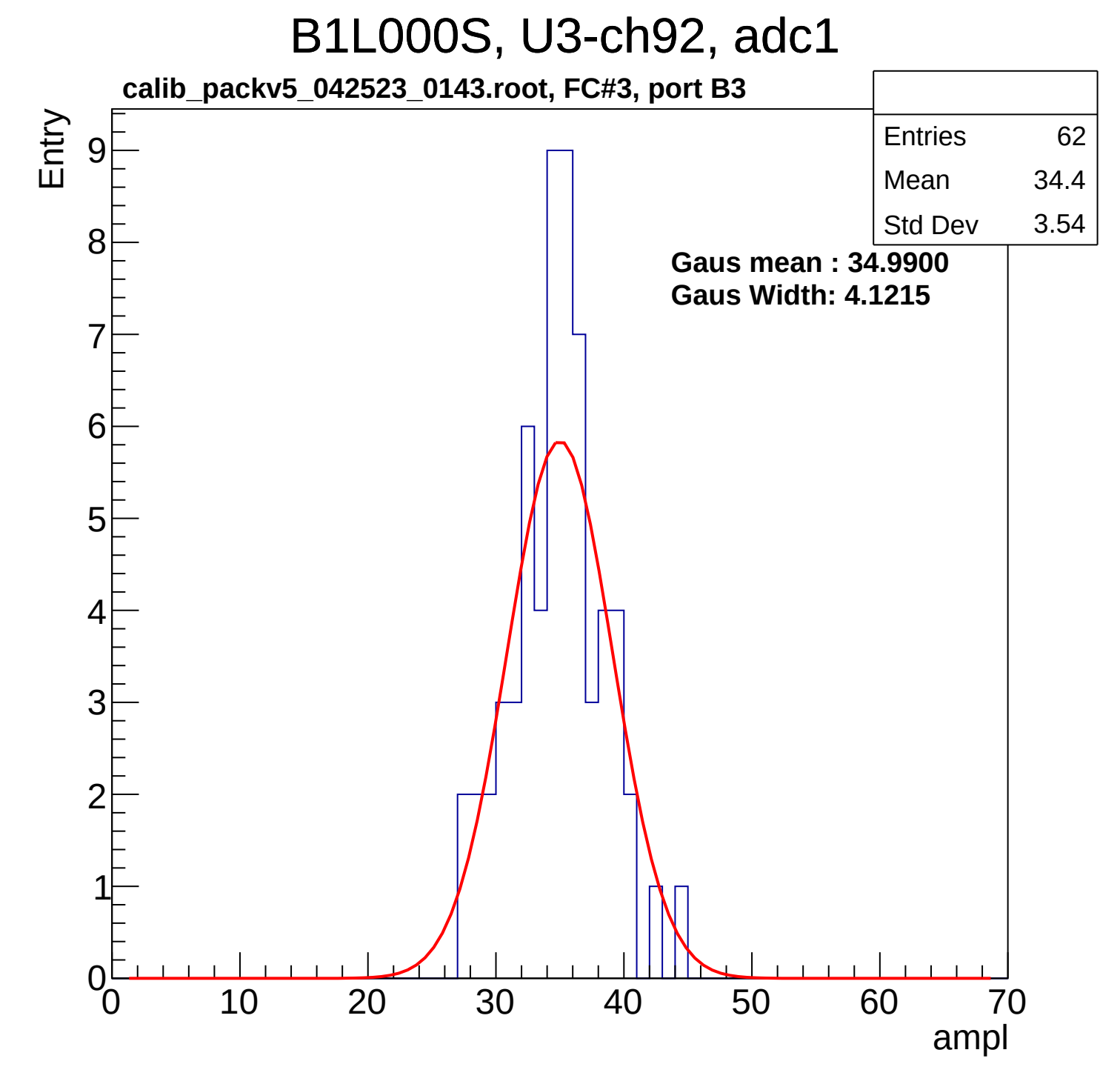
B1L000S, U3-ch92, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 34.4
Std Dev 3.54
Gaus mean : 34.9900
Gaus Width: 4.1215
Entry
ampl
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3
4
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6
7
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0
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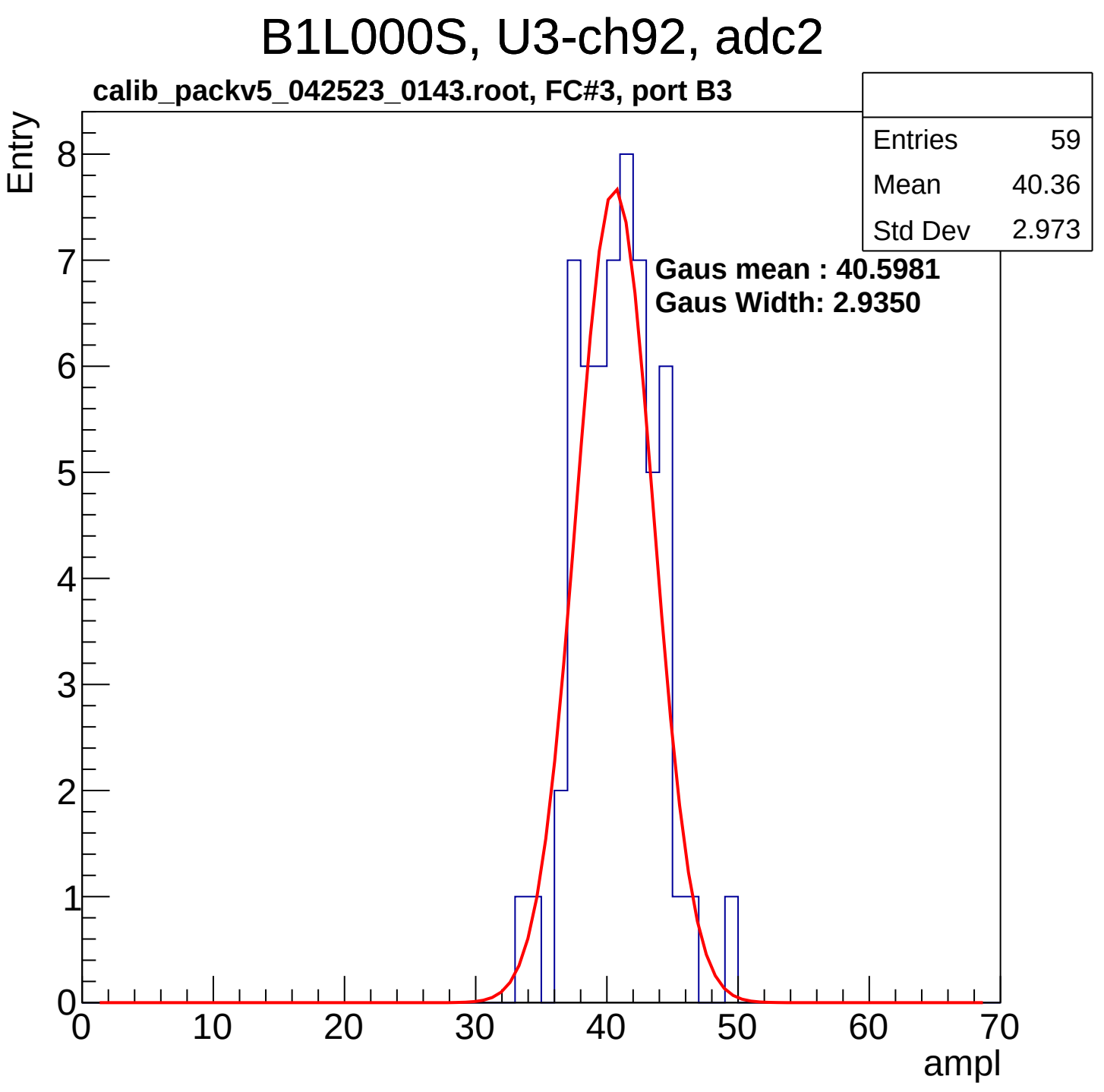
B1L000S, U3-ch92, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 40.36
Std Dev 2.973
Gaus mean : 40.5981
Gaus Width: 2.9350
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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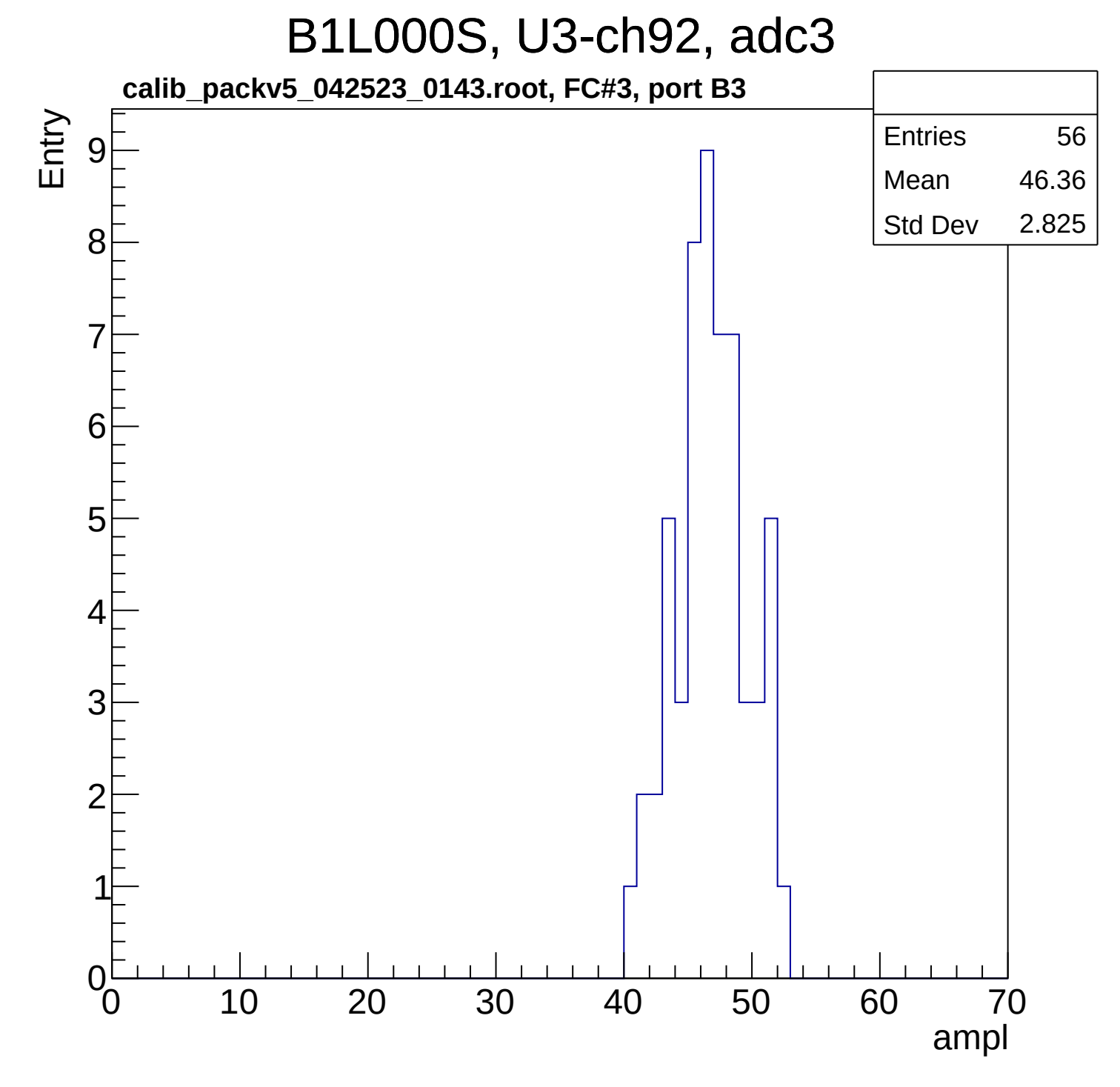
B1L000S, U3-ch92, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 46.36
Std Dev 2.825
Entry
ampl
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4
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7
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0
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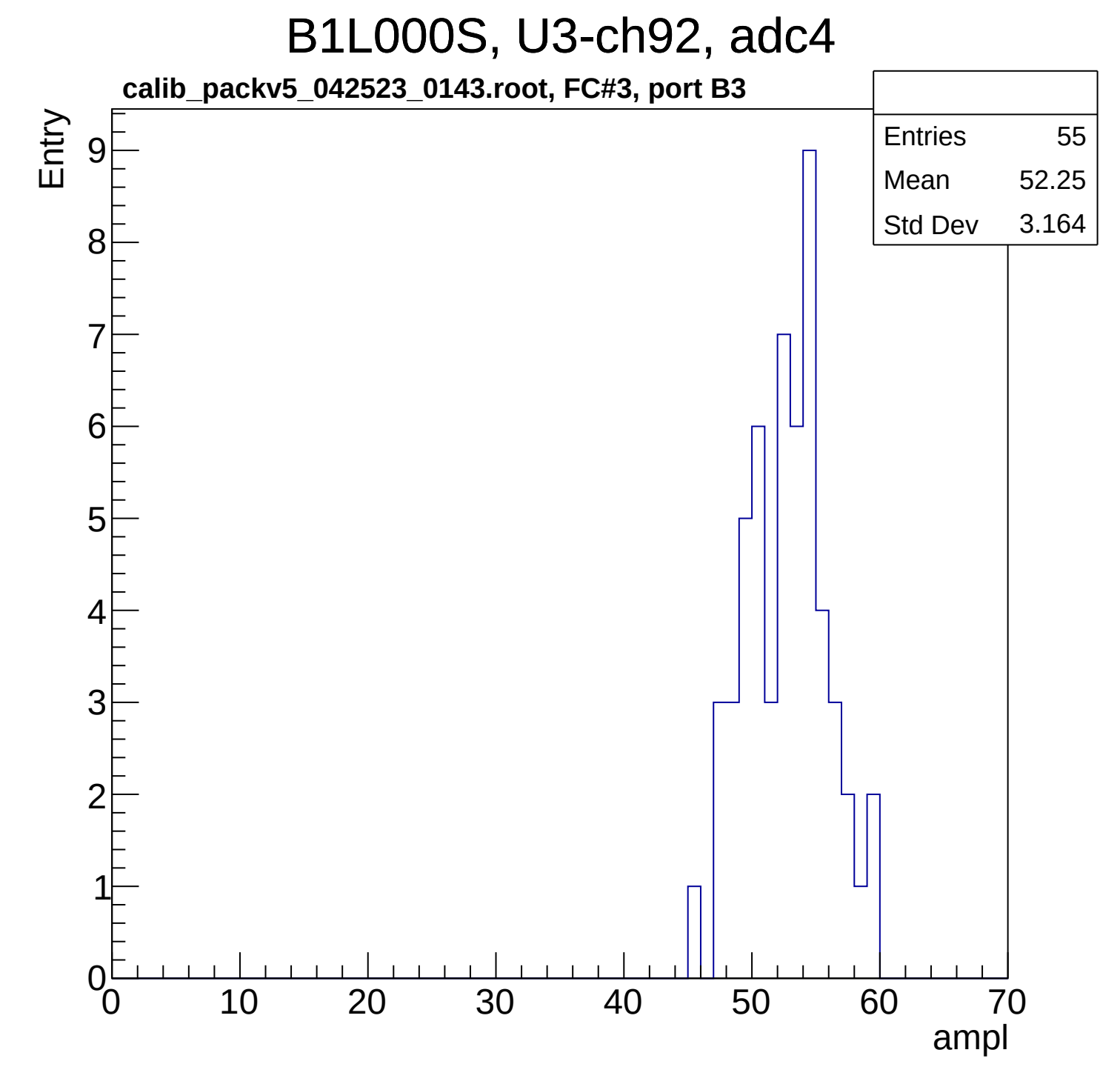
B1L000S, U3-ch92, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 52.25
Std Dev 3.164
Entry
ampl
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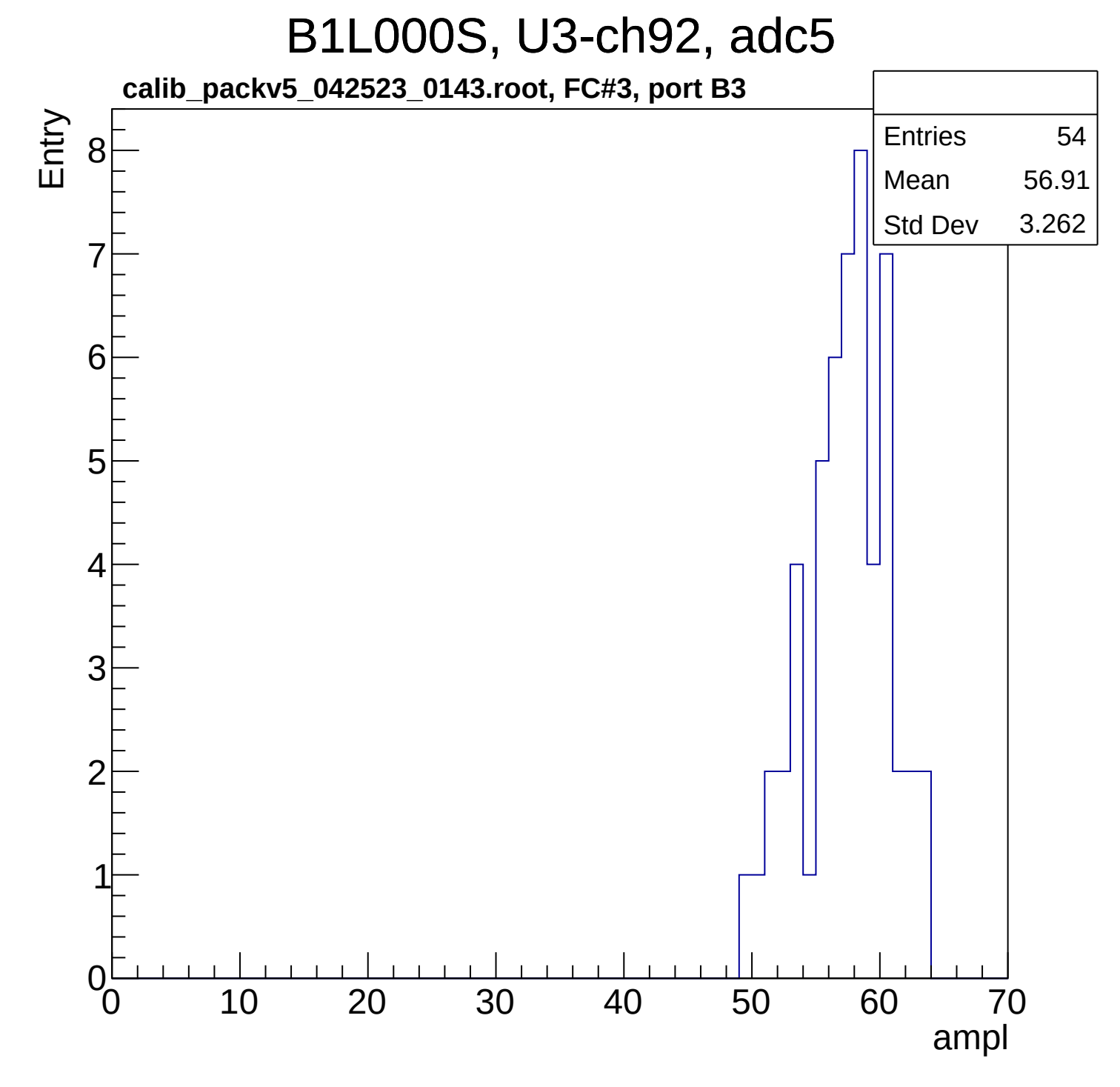
B1L000S, U3-ch92, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 56.91
Std Dev 3.262
Entry
ampl
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3
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5
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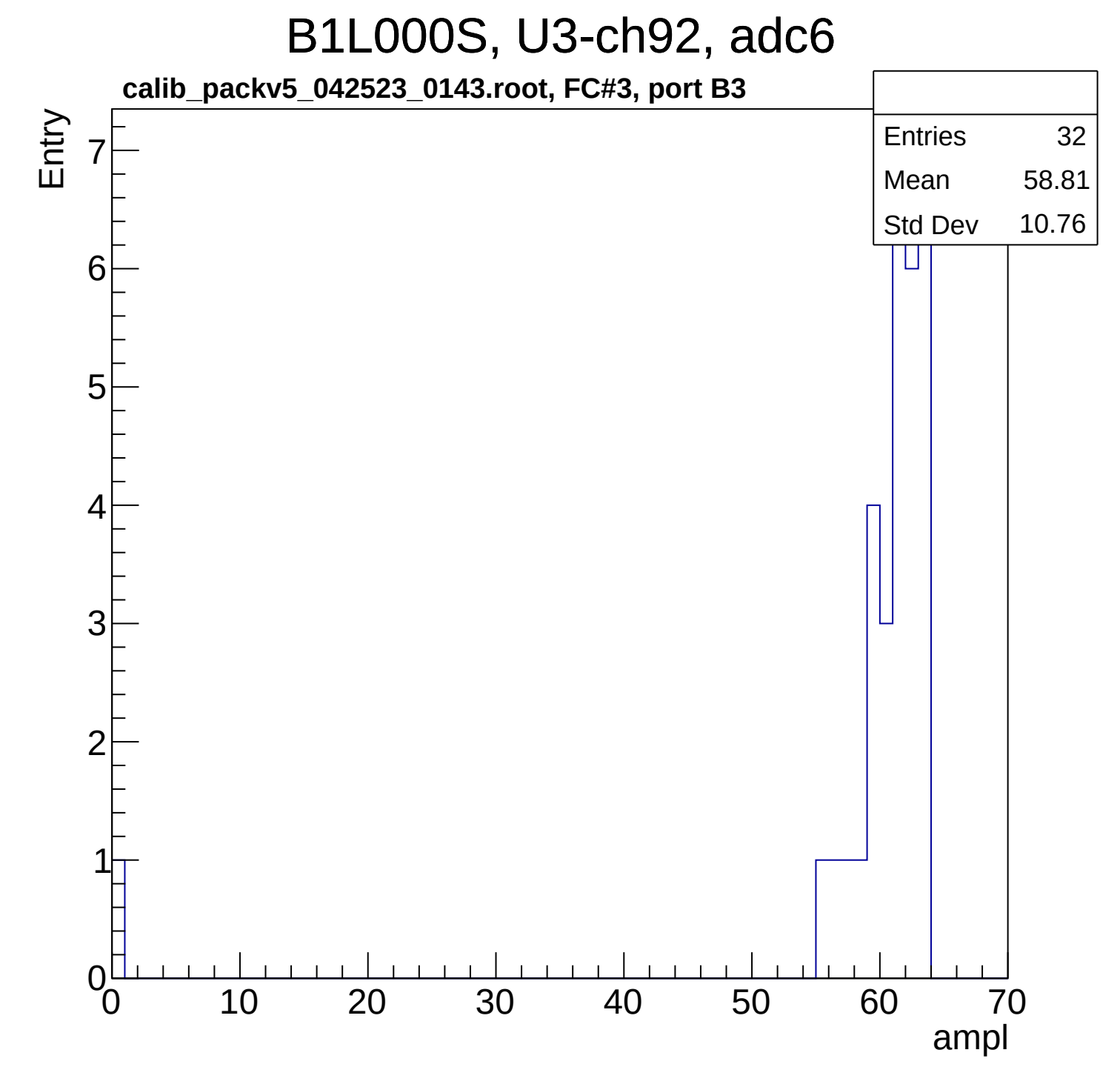
B1L000S, U3-ch92, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 58.81
Std Dev 10.76
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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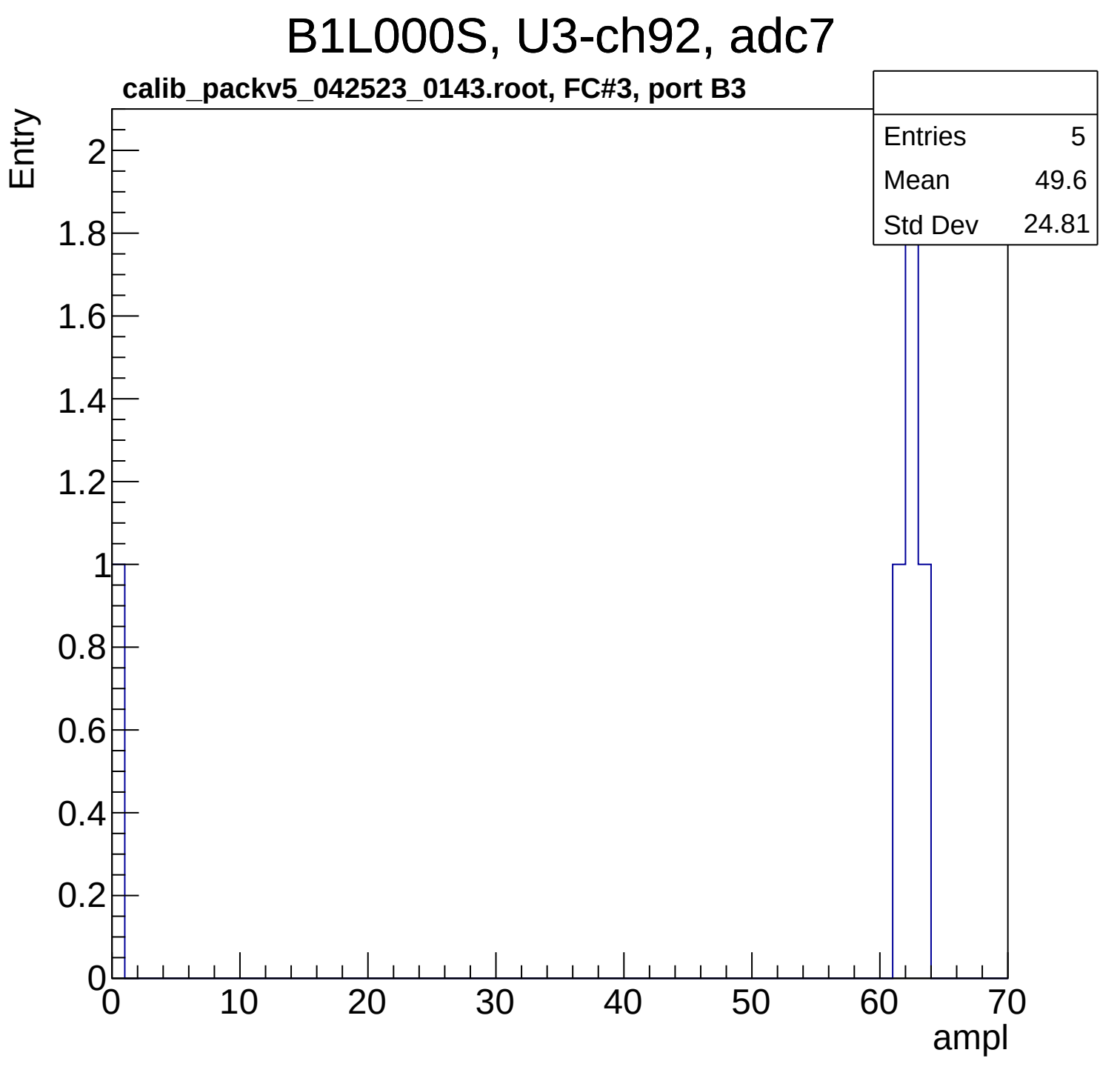
40
50
60
70


B1L000S, U3-ch92, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 49.6
Std Dev 24.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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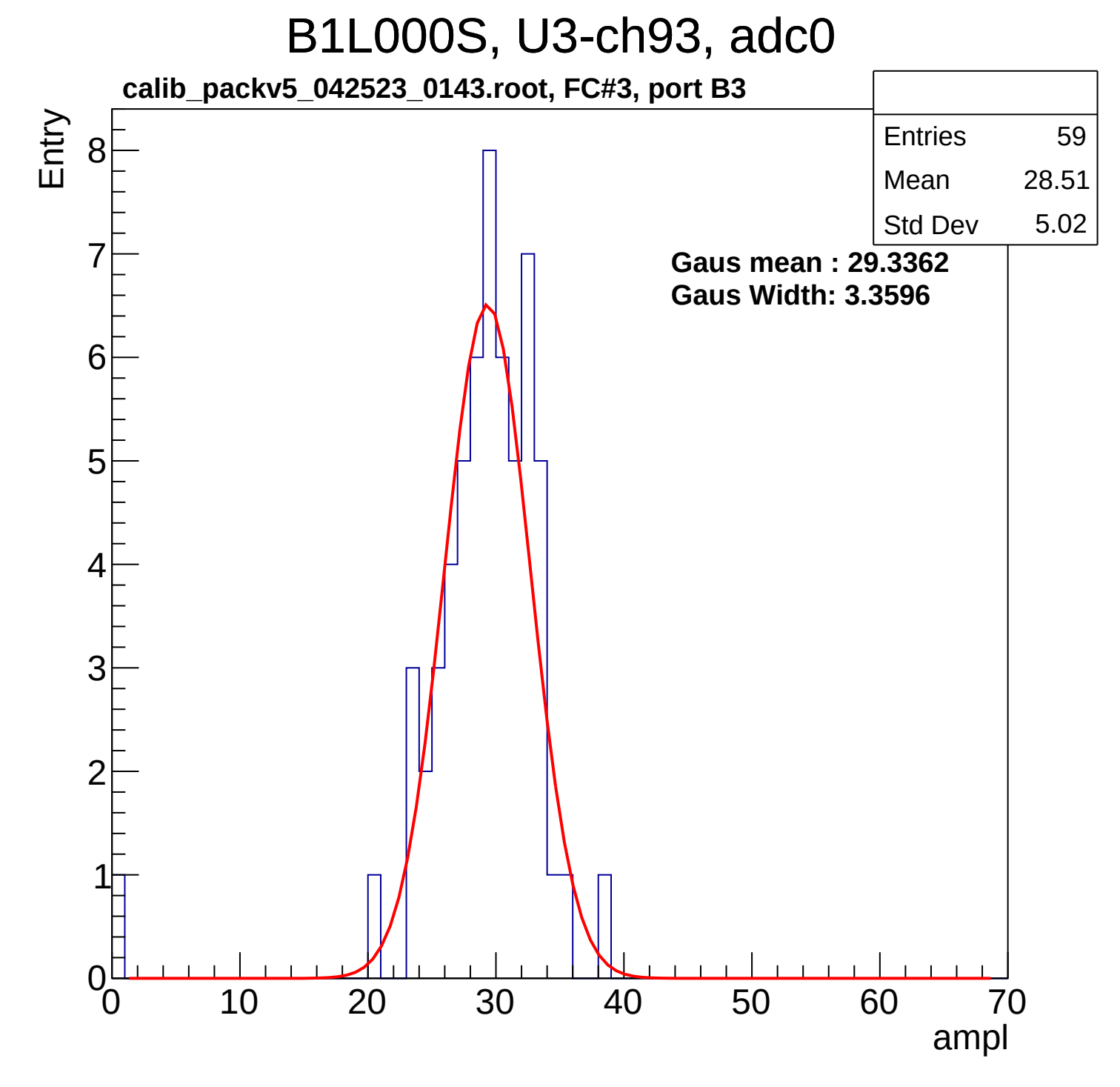
B1L000S, U3-ch93, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 28.51
Std Dev 5.02
Gaus mean : 29.3362
Gaus Width: 3.3596
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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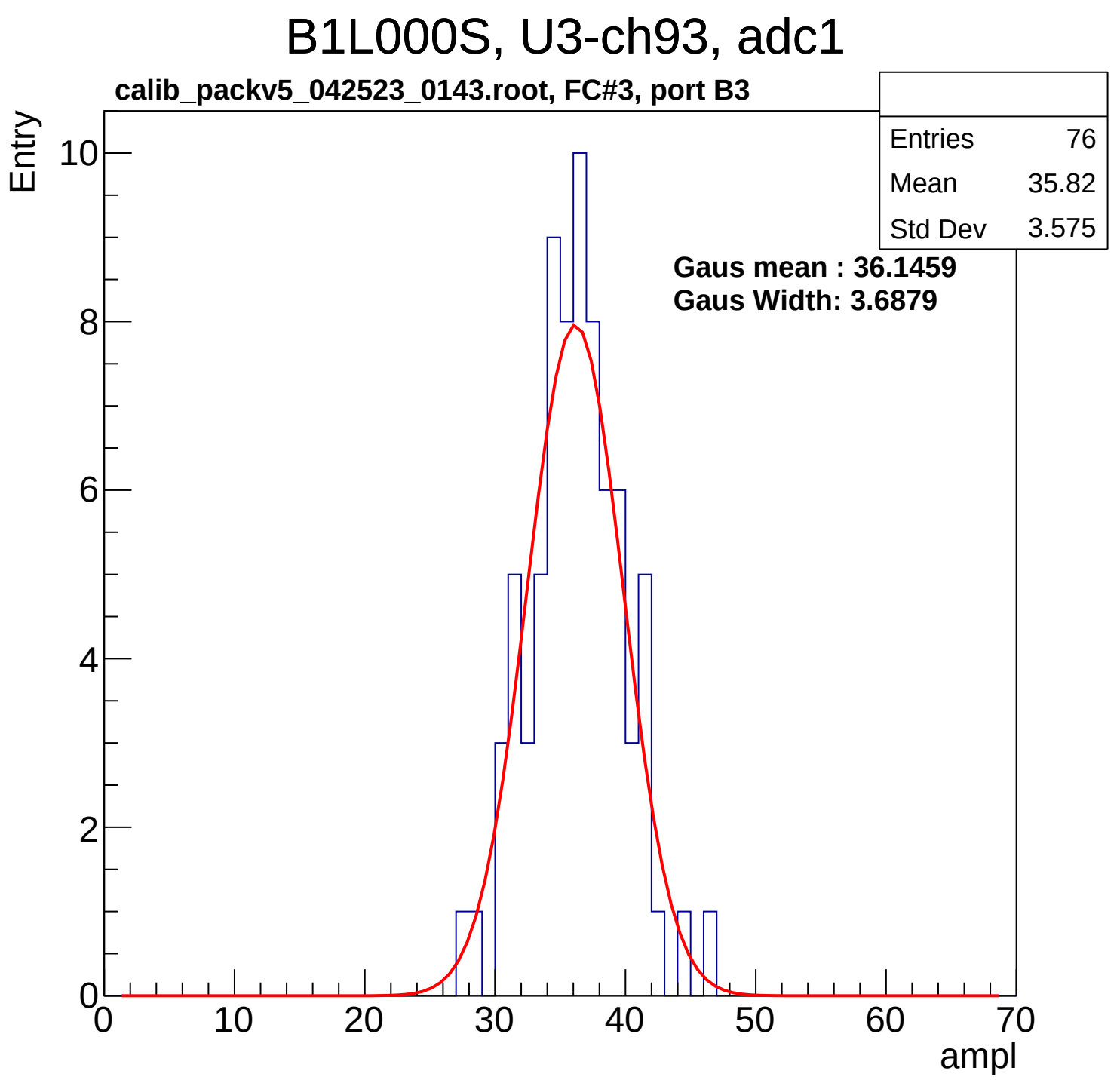
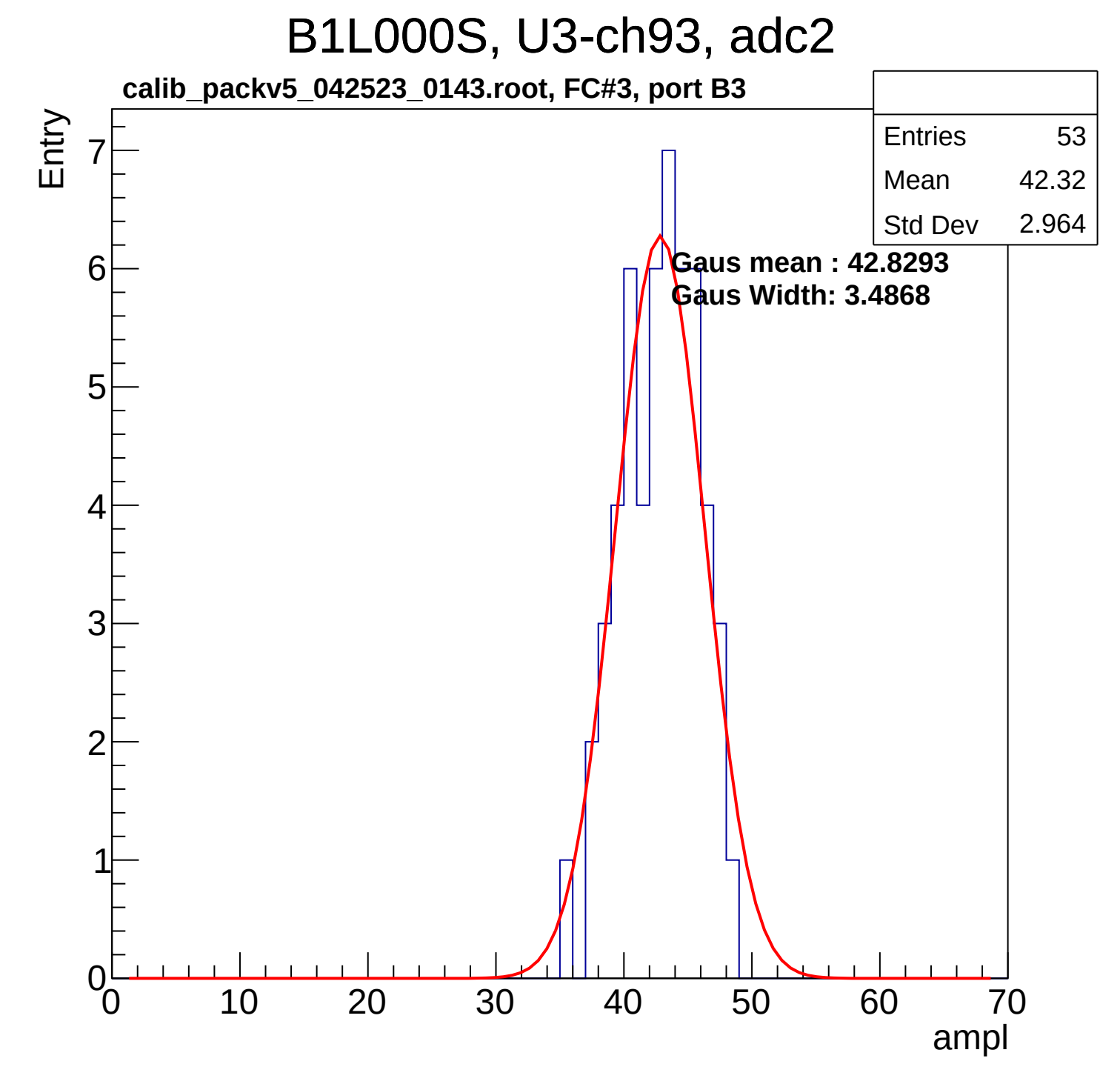
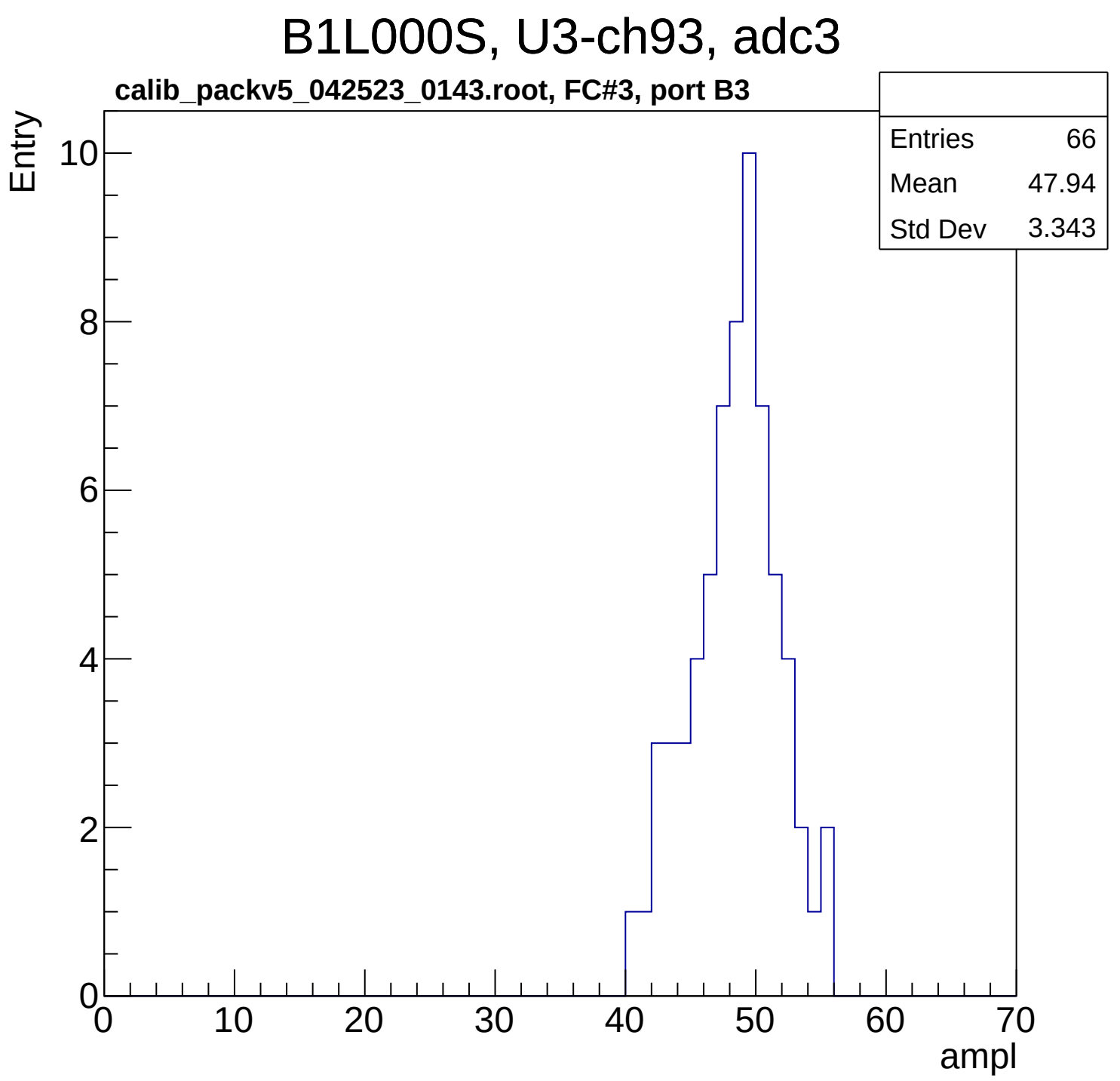
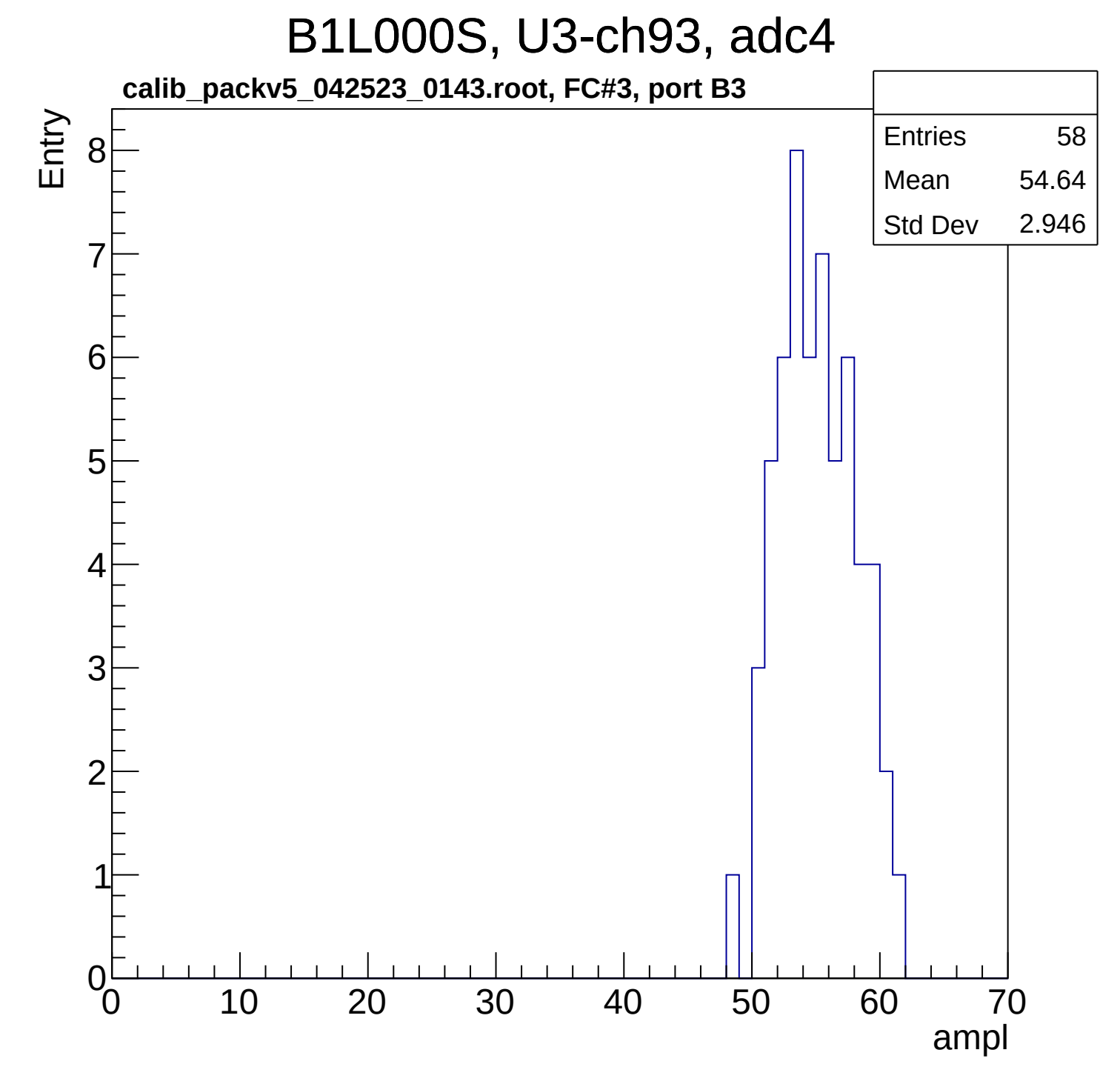
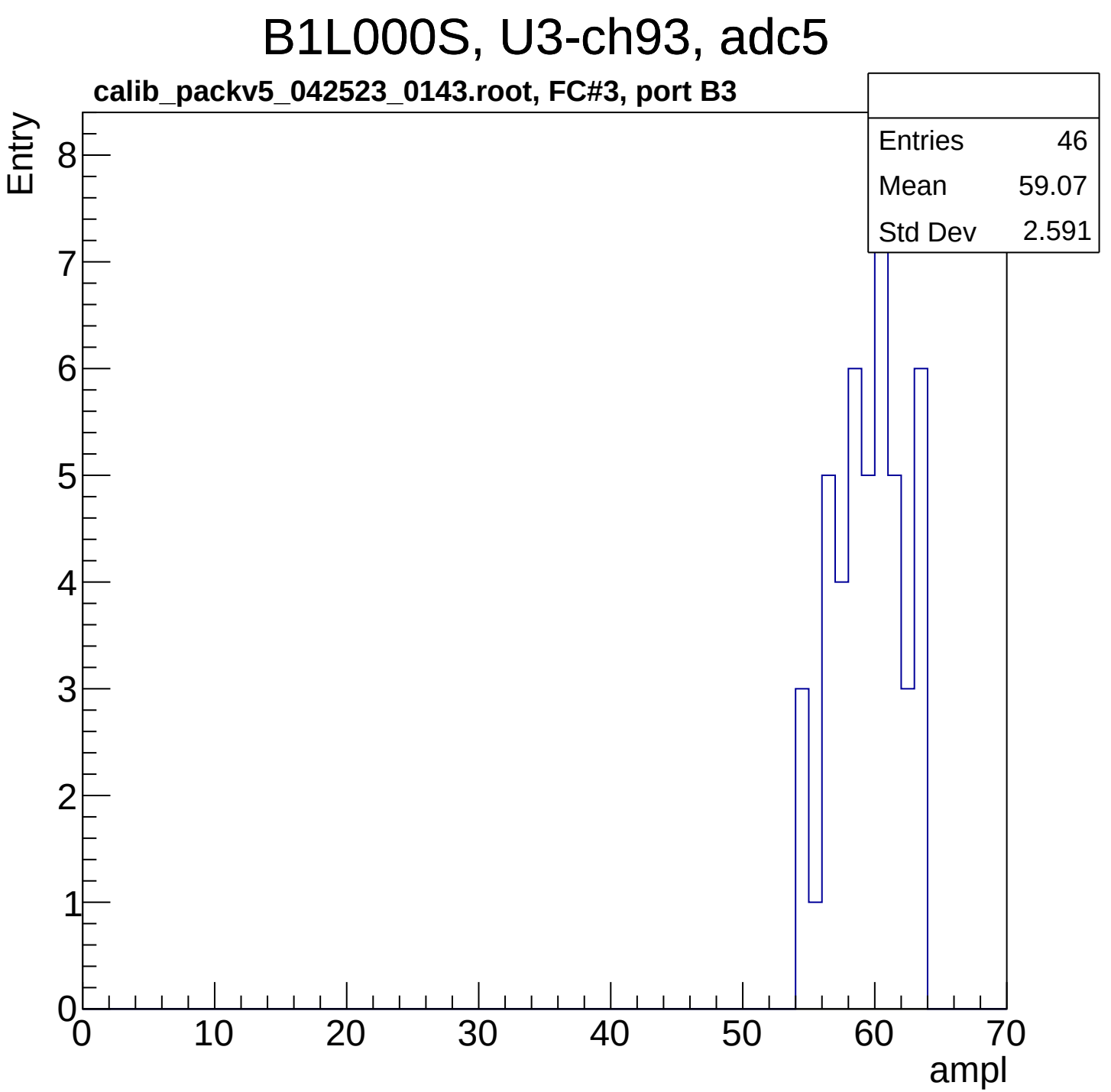
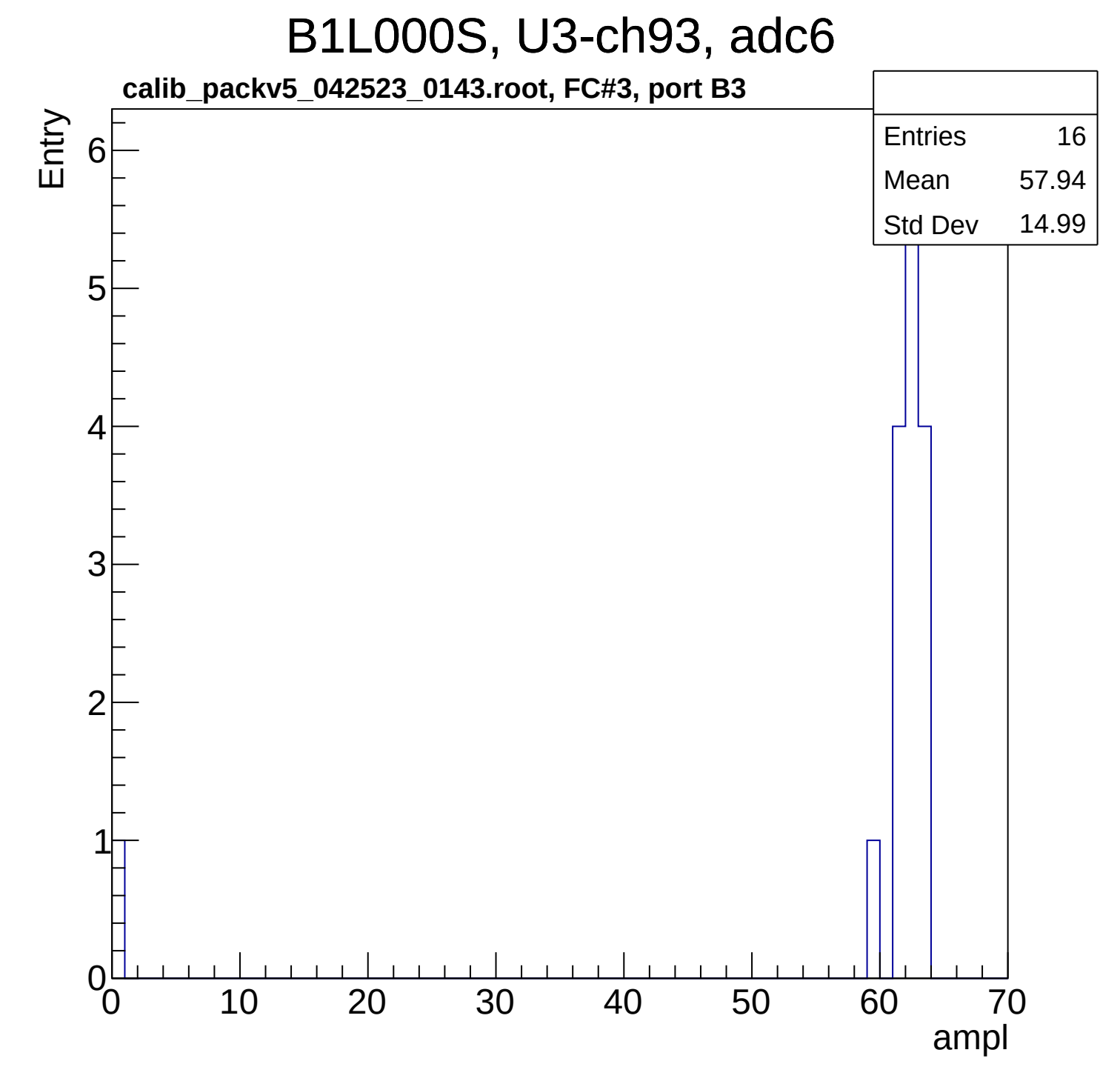

B1L000S, U3-ch93, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 35.82
Std Dev 3.575
Gaus mean : 36.1459
Gaus Width: 3.6879
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

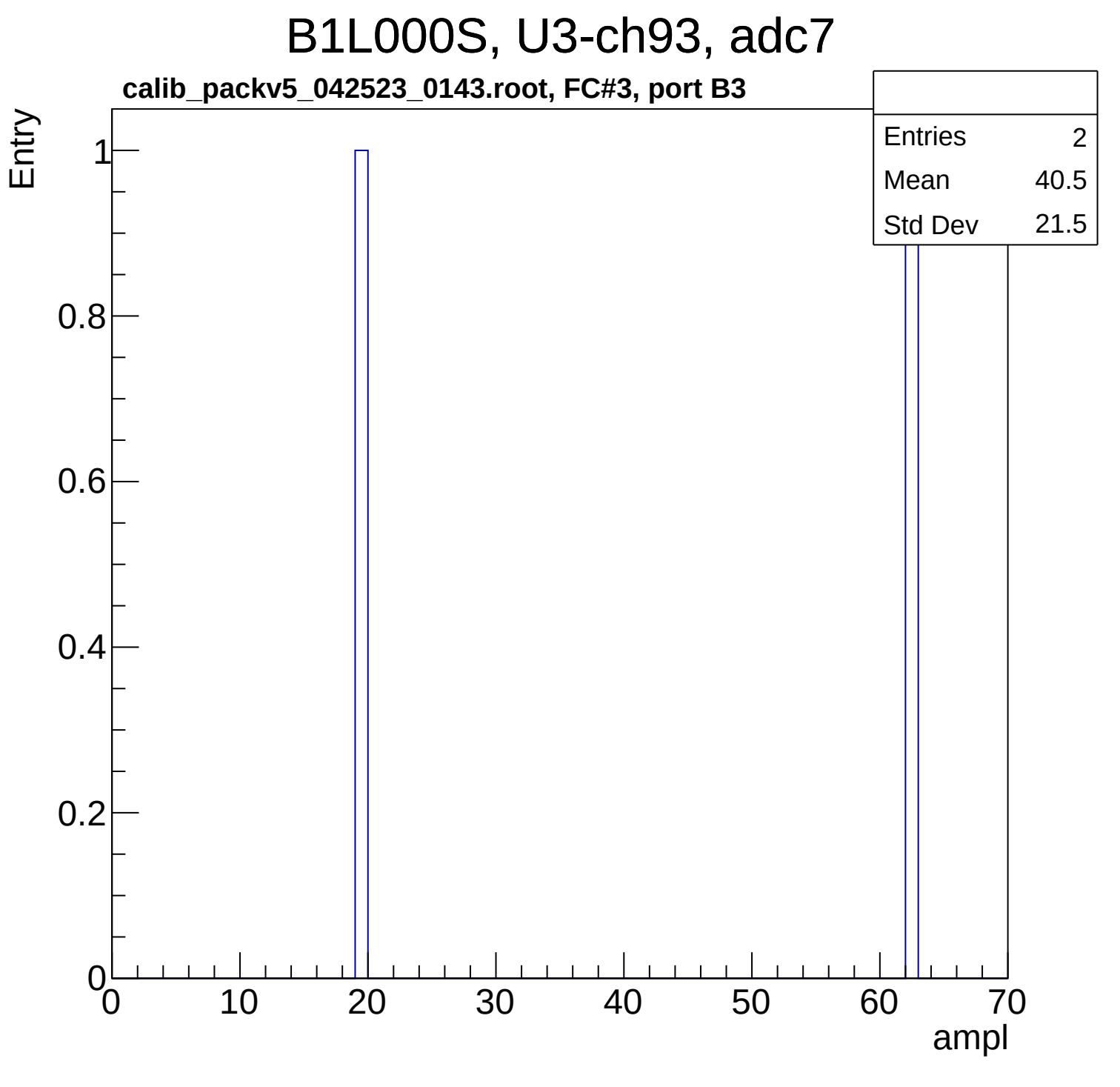

B1L000S, U3-ch93, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 42.32
Std Dev 2.964
Gaus mean : 42.8293
Gaus Width: 3.4868
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

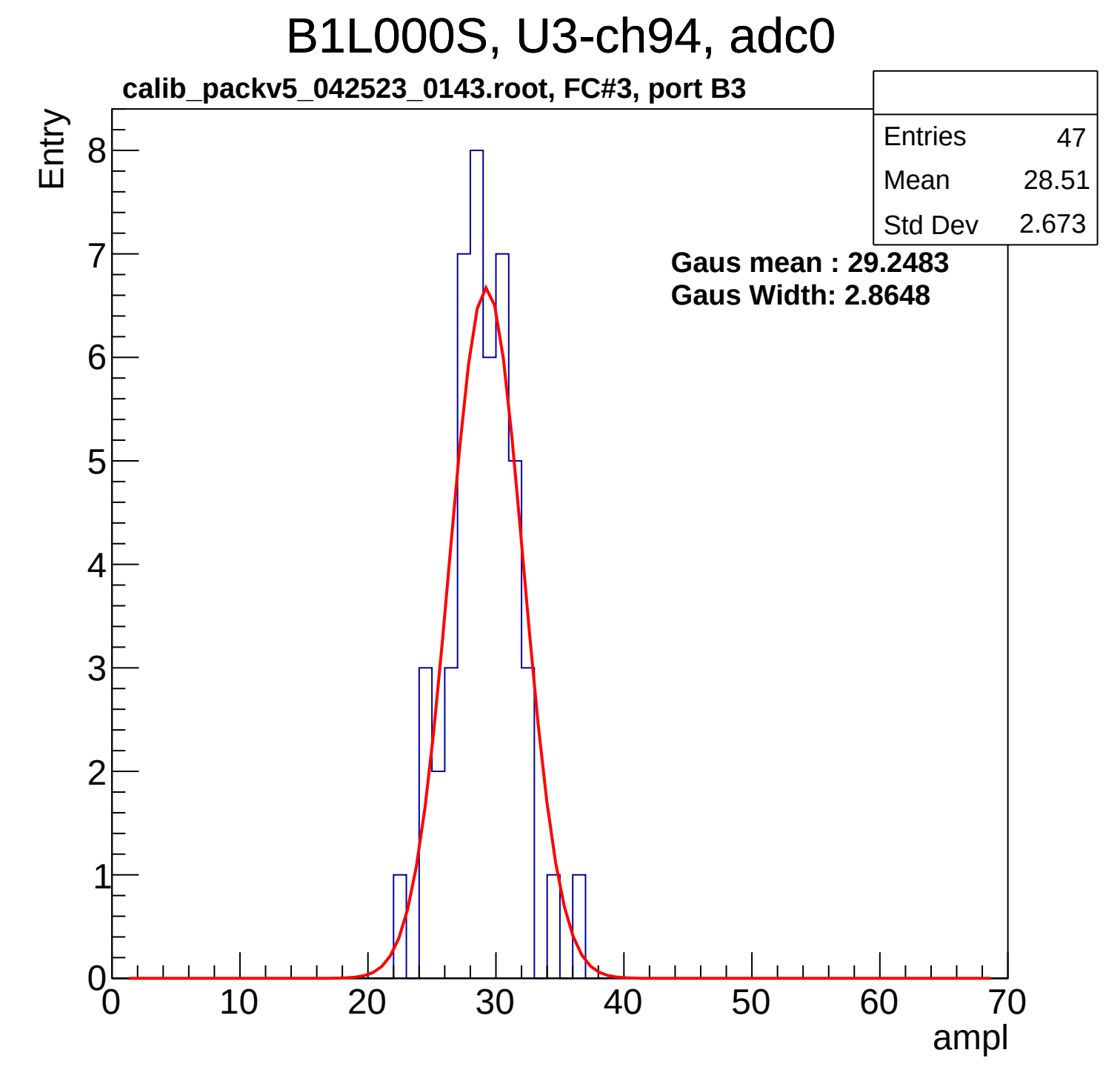

B1L000S, U3-ch93, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.94
Std Dev 3.343
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

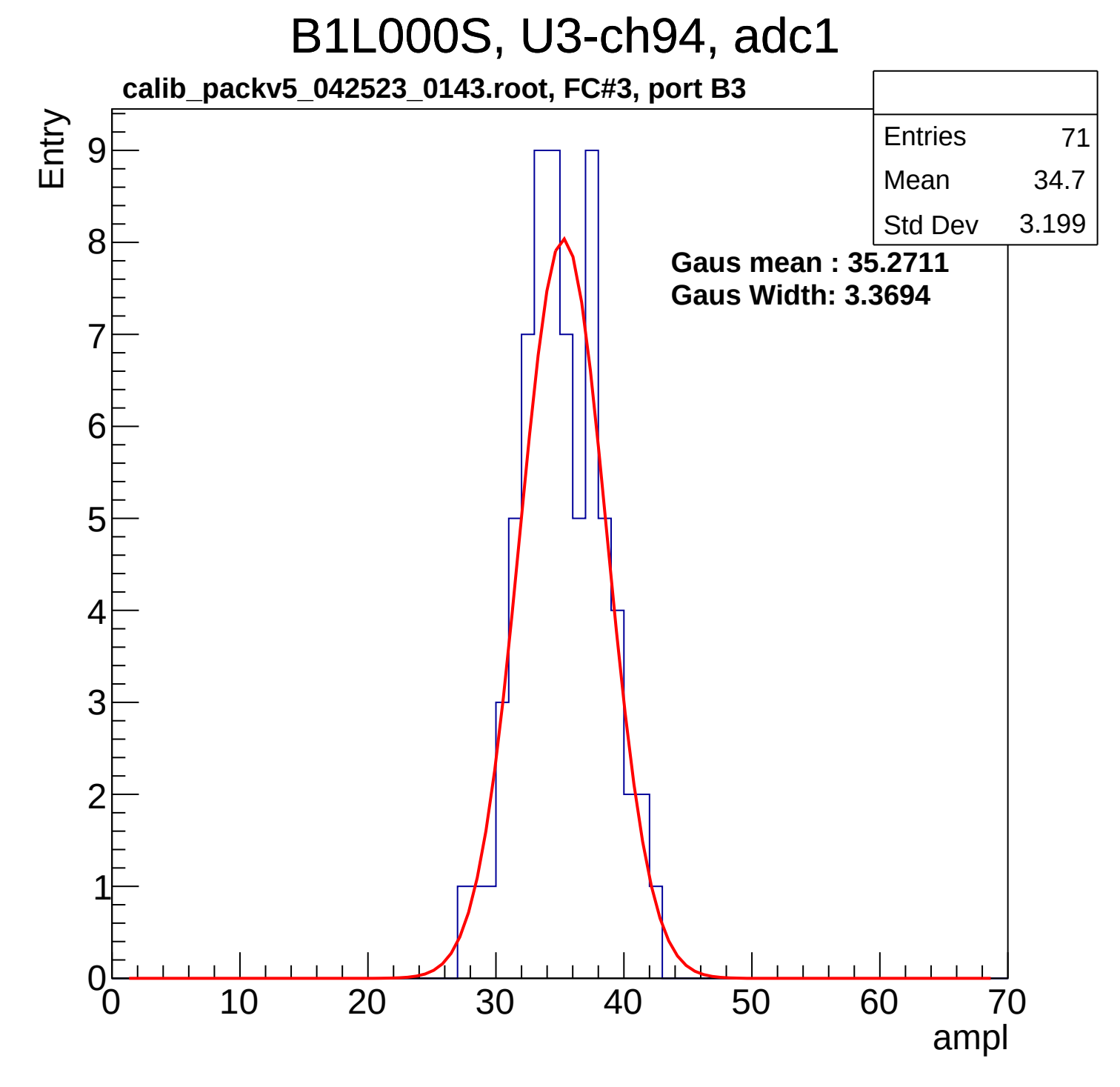

B1L000S, U3-ch93, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.64
Std Dev 2.946
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch93, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 59.07
Std Dev 2.591
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch93, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.94
Std Dev 14.99
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch93, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 40.5
Std Dev 21.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch94, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 28.51
Std Dev 2.673
Gaus mean : 29.2483
Gaus Width: 2.8648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch94, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 34.7
Std Dev 3.199
Gaus mean : 35.2711
Gaus Width: 3.3694
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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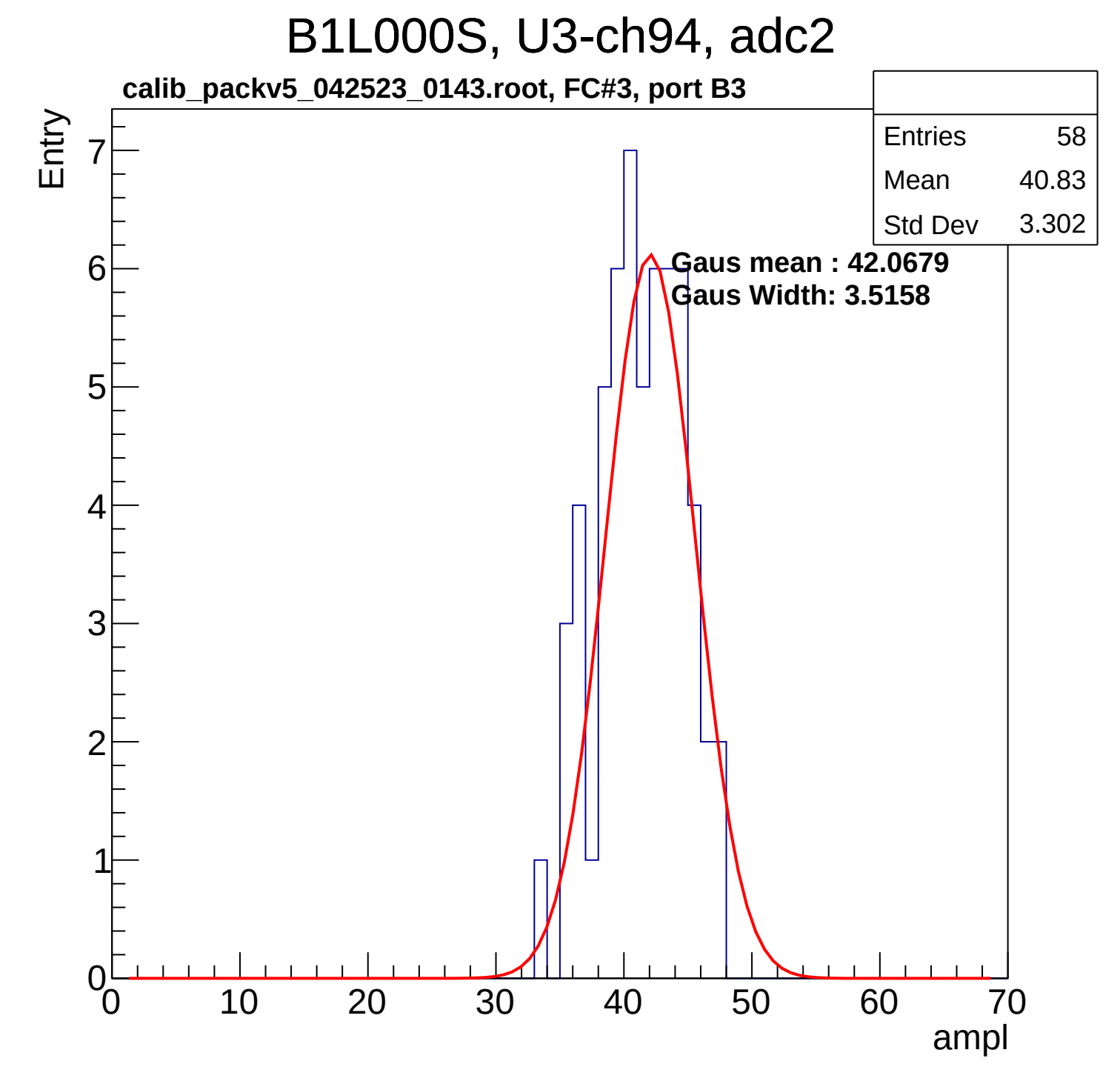
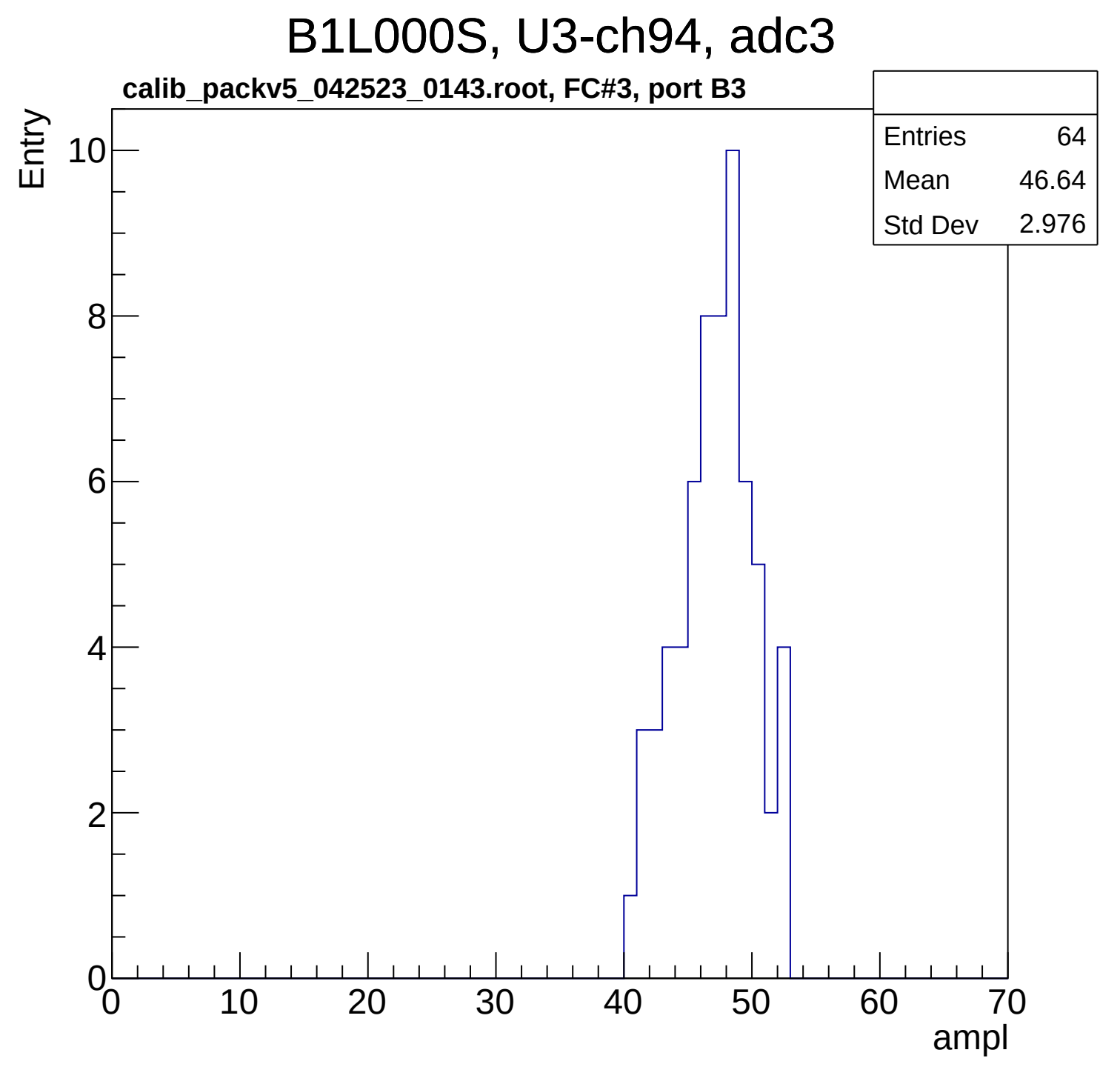
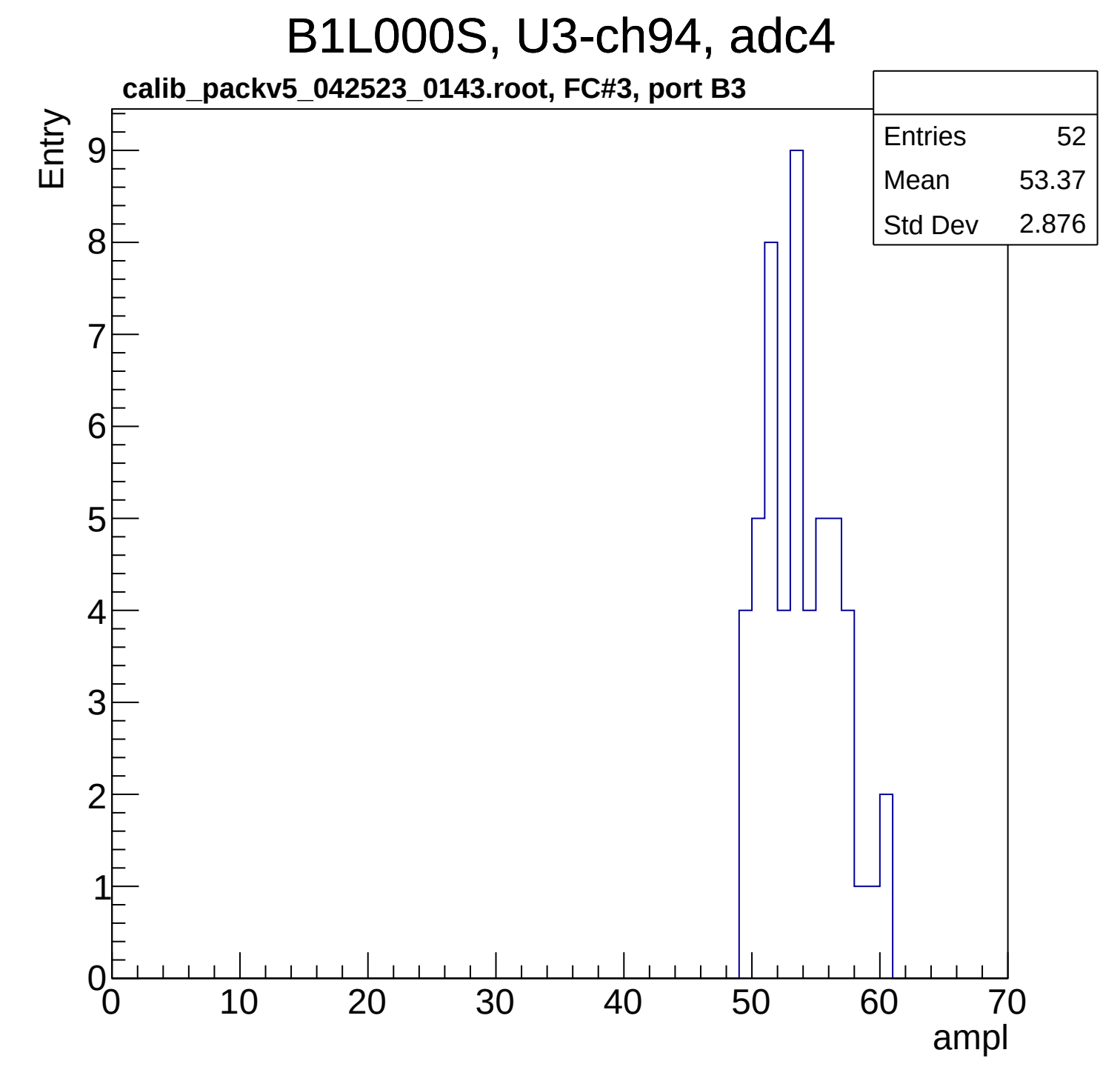
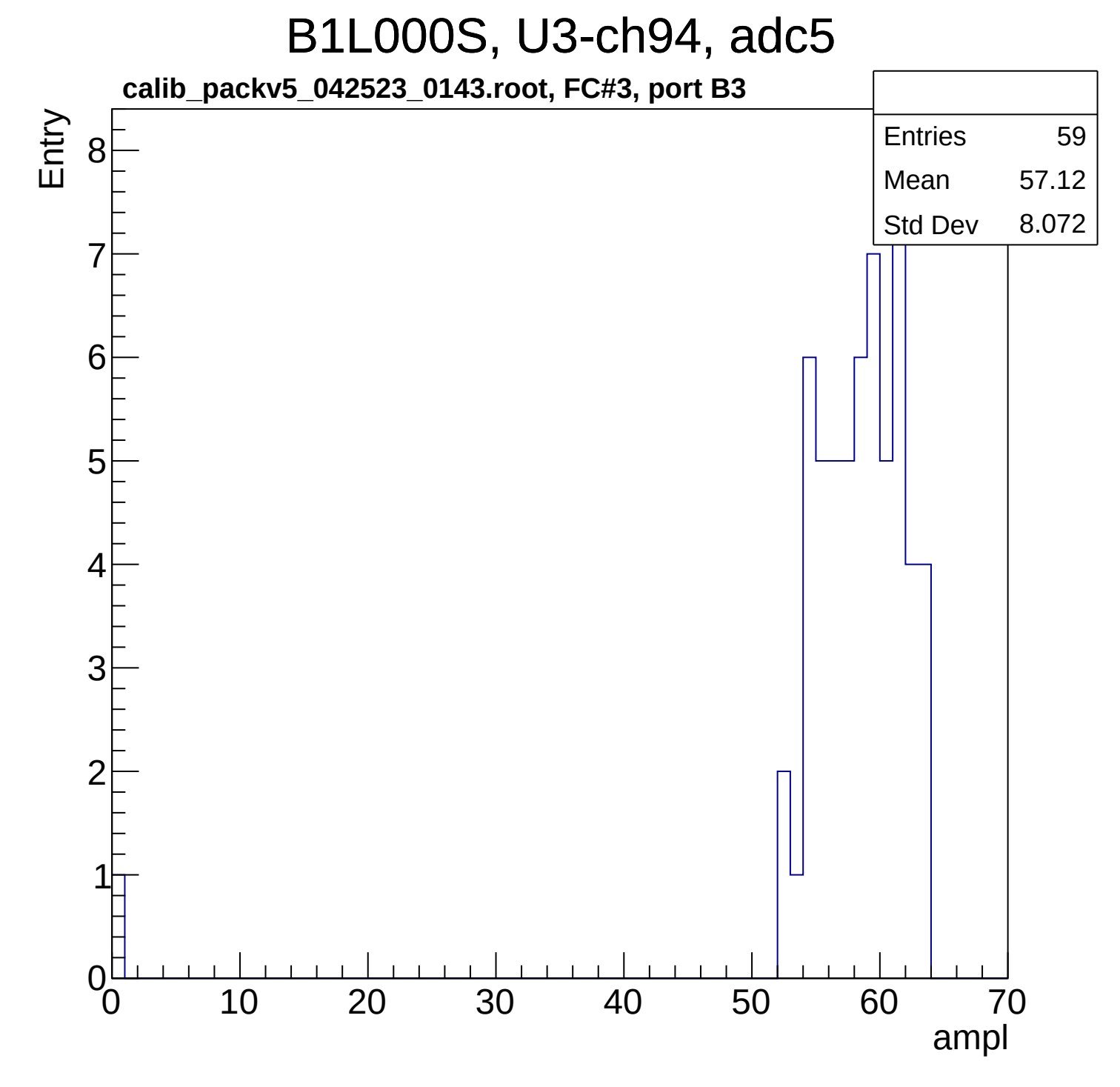
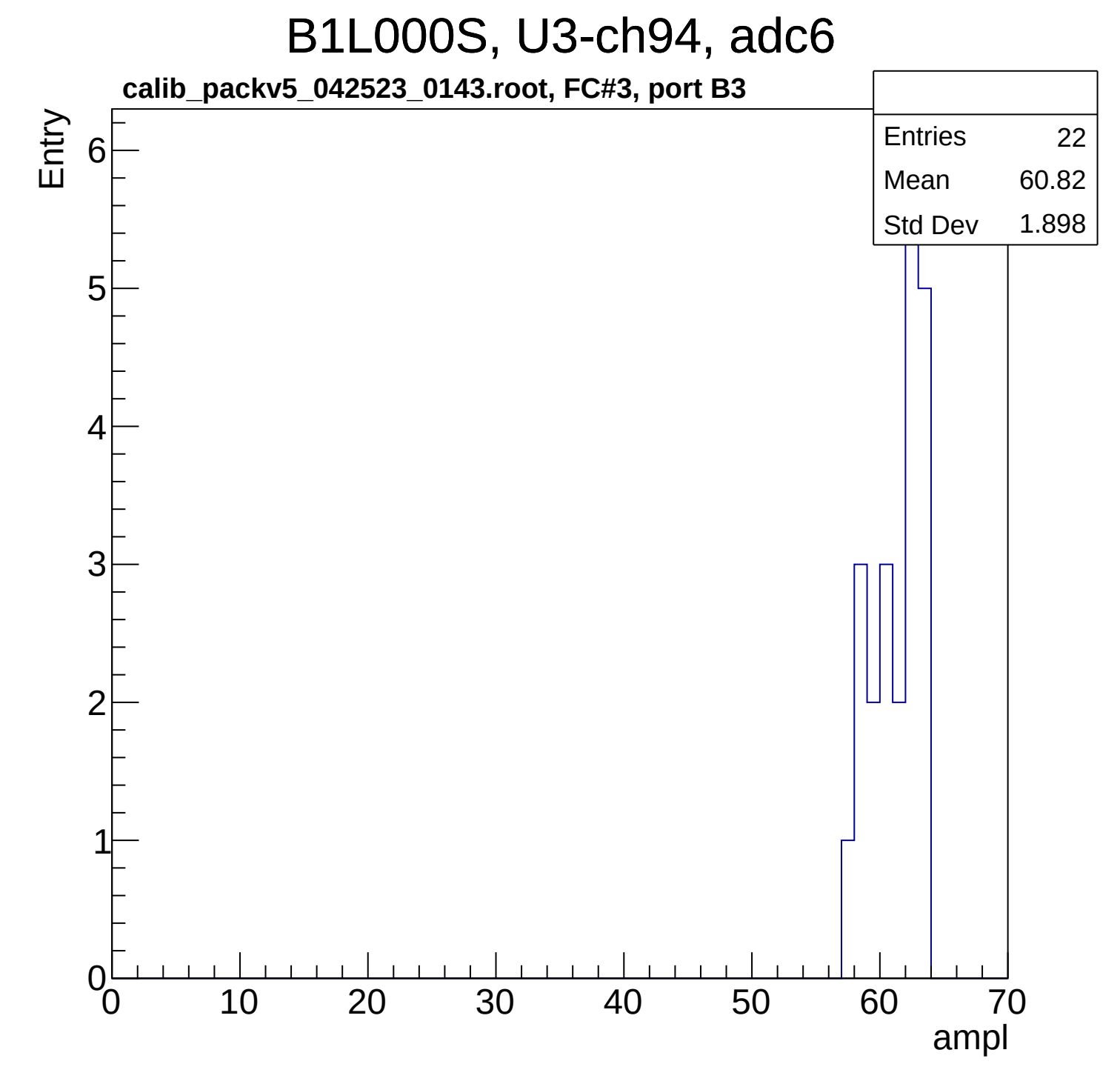
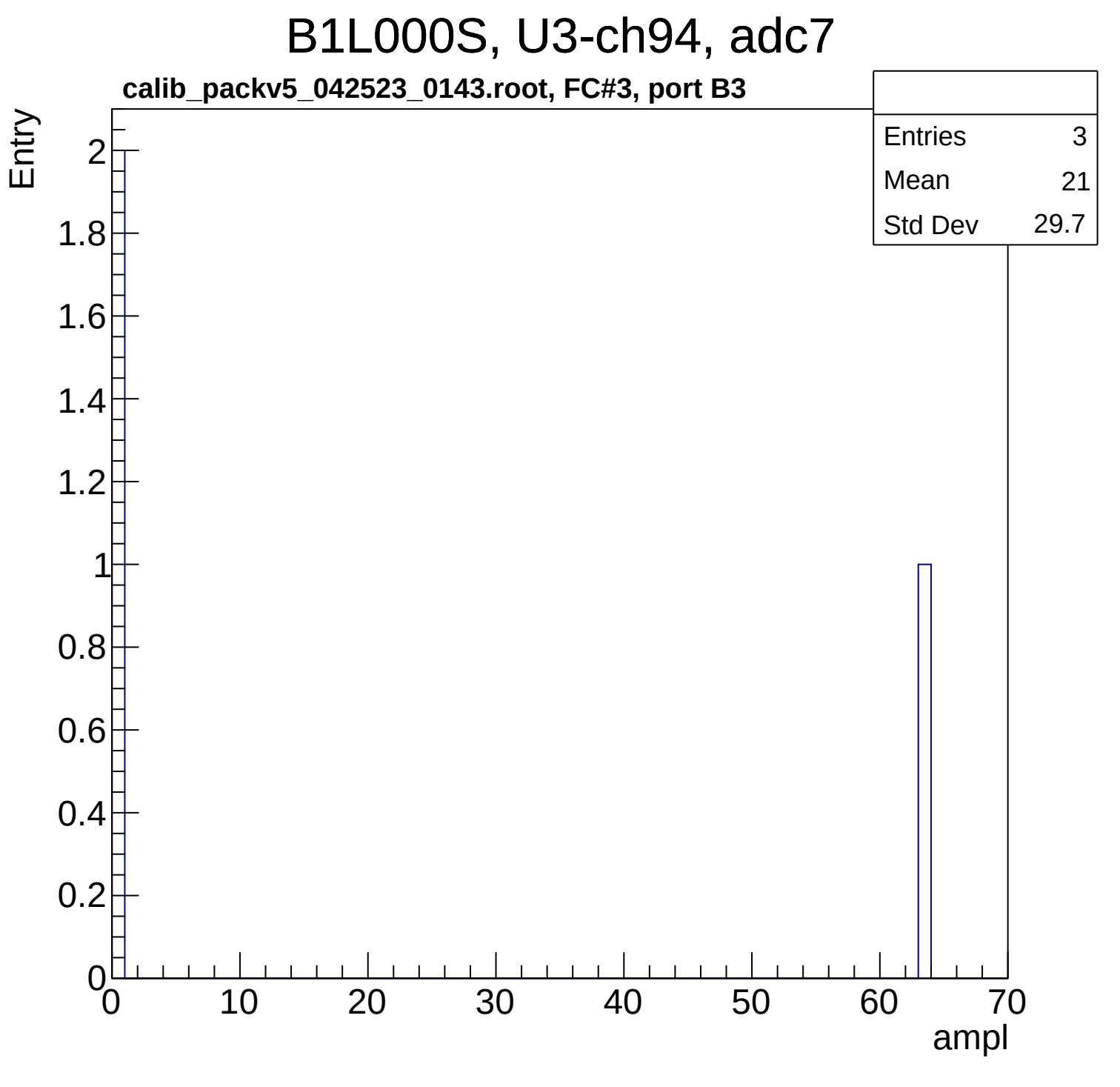

B1L000S, U3-ch94, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 40.83
Std Dev 3.302
Gaus mean : 42.0679
Gaus Width: 3.5158
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

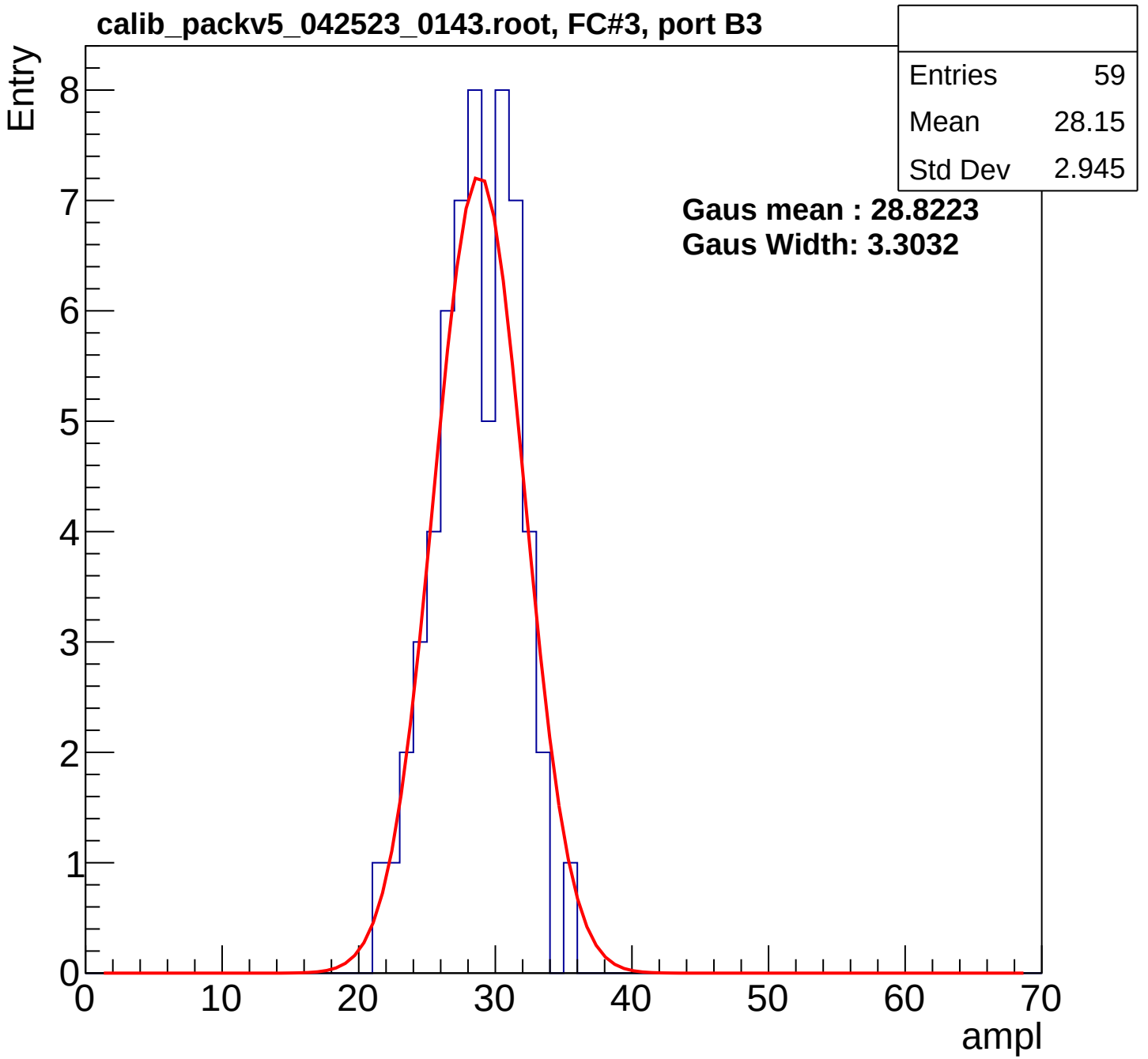

B1L000S, U3-ch94, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 46.64
Std Dev 2.976
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

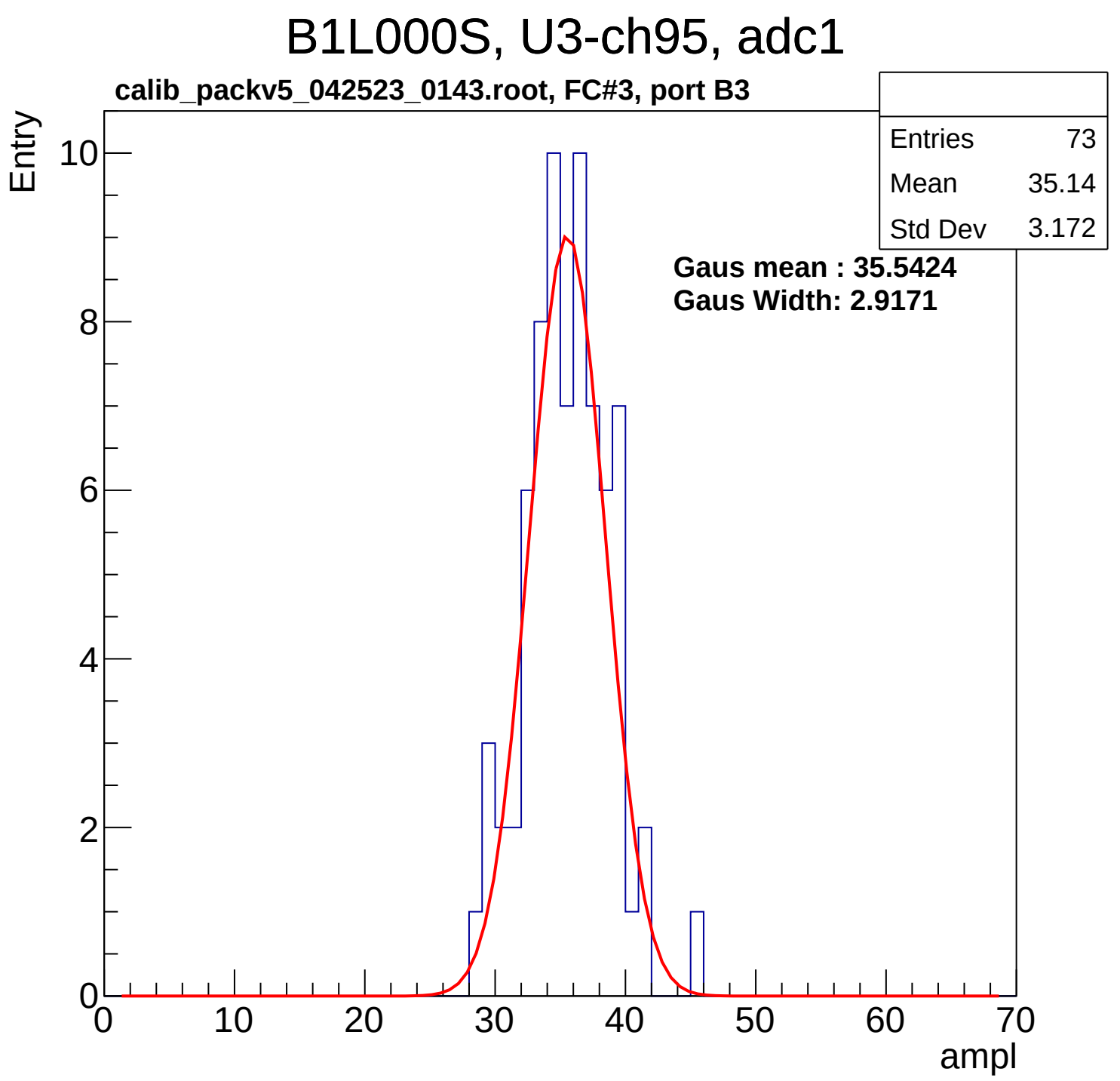

B1L000S, U3-ch94, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 53.37
Std Dev 2.876
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

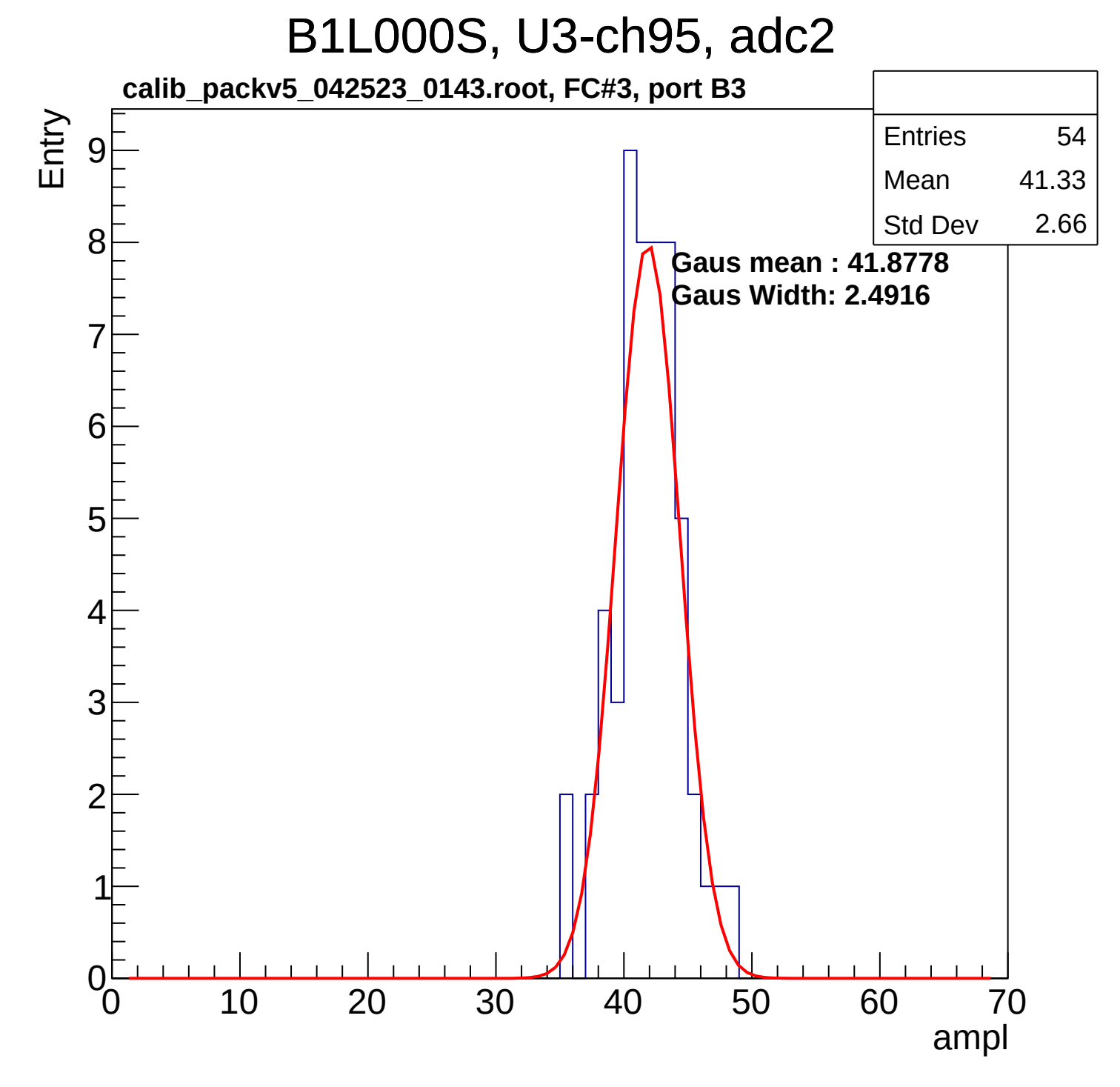

B1L000S, U3-ch94, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 57.12
Std Dev 8.072
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

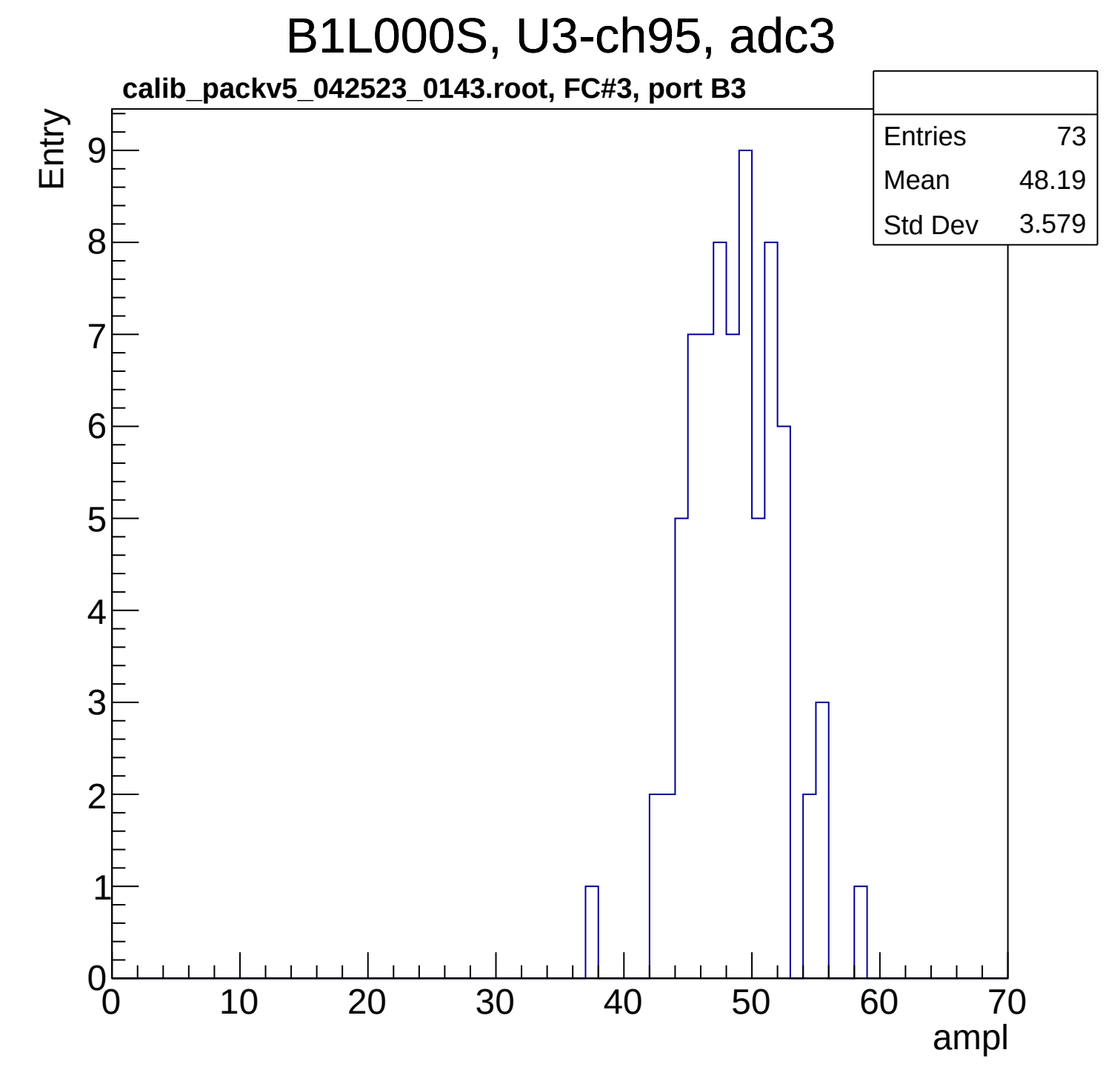

B1L000S, U3-ch94, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 60.82
Std Dev 1.898
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch94, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch95, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 28.15
Std Dev 2.945
Gaus mean : 28.8223
Gaus Width: 3.3032
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch95, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 35.14
Std Dev 3.172
Gaus mean : 35.5424
Gaus Width: 2.9171
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch95, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 41.33
Std Dev 2.66
Gaus mean : 41.8778
Gaus Width: 2.4916
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch95, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 48.19
Std Dev 3.579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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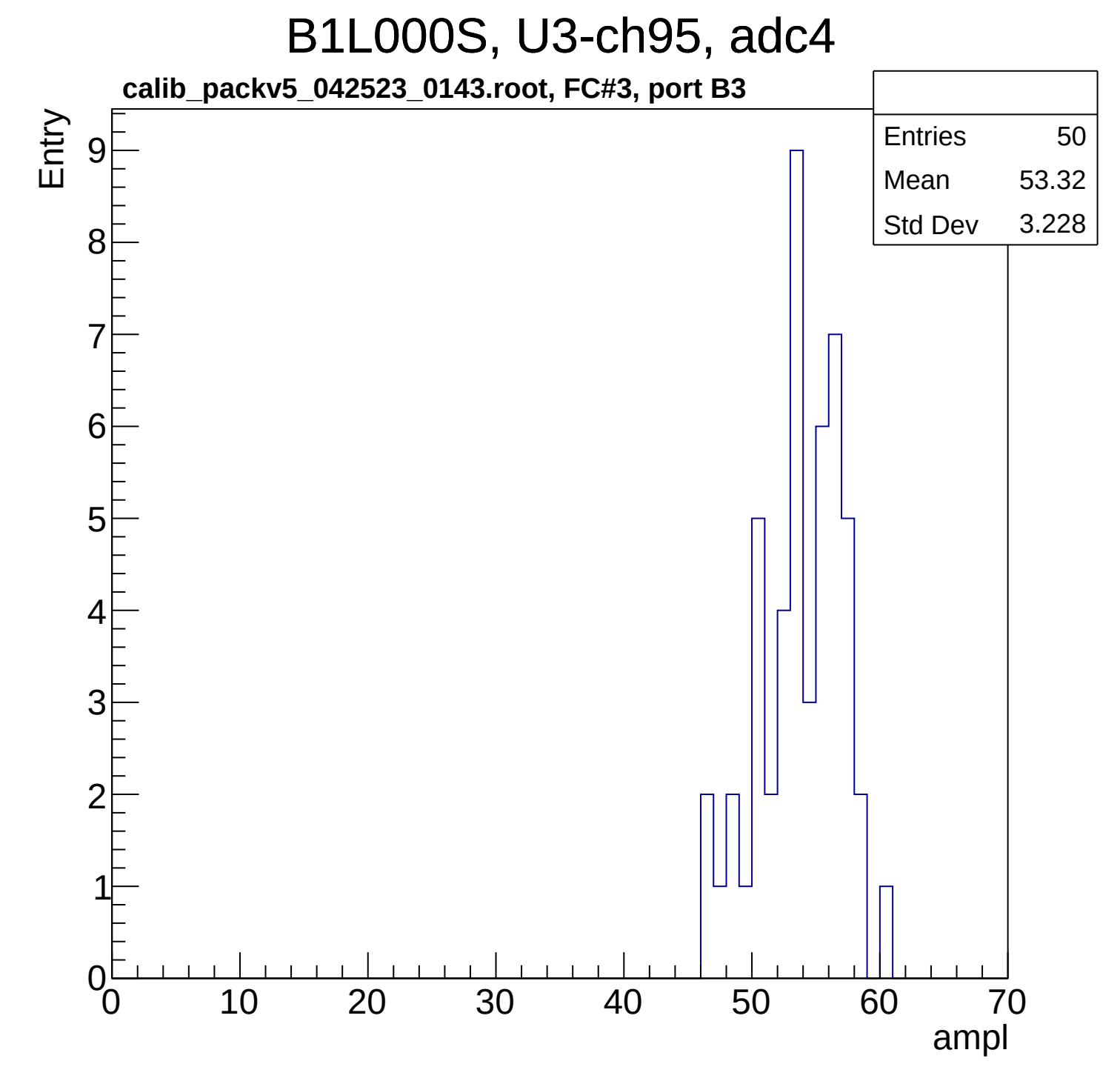
B1L000S, U3-ch95, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 53.32
Std Dev 3.228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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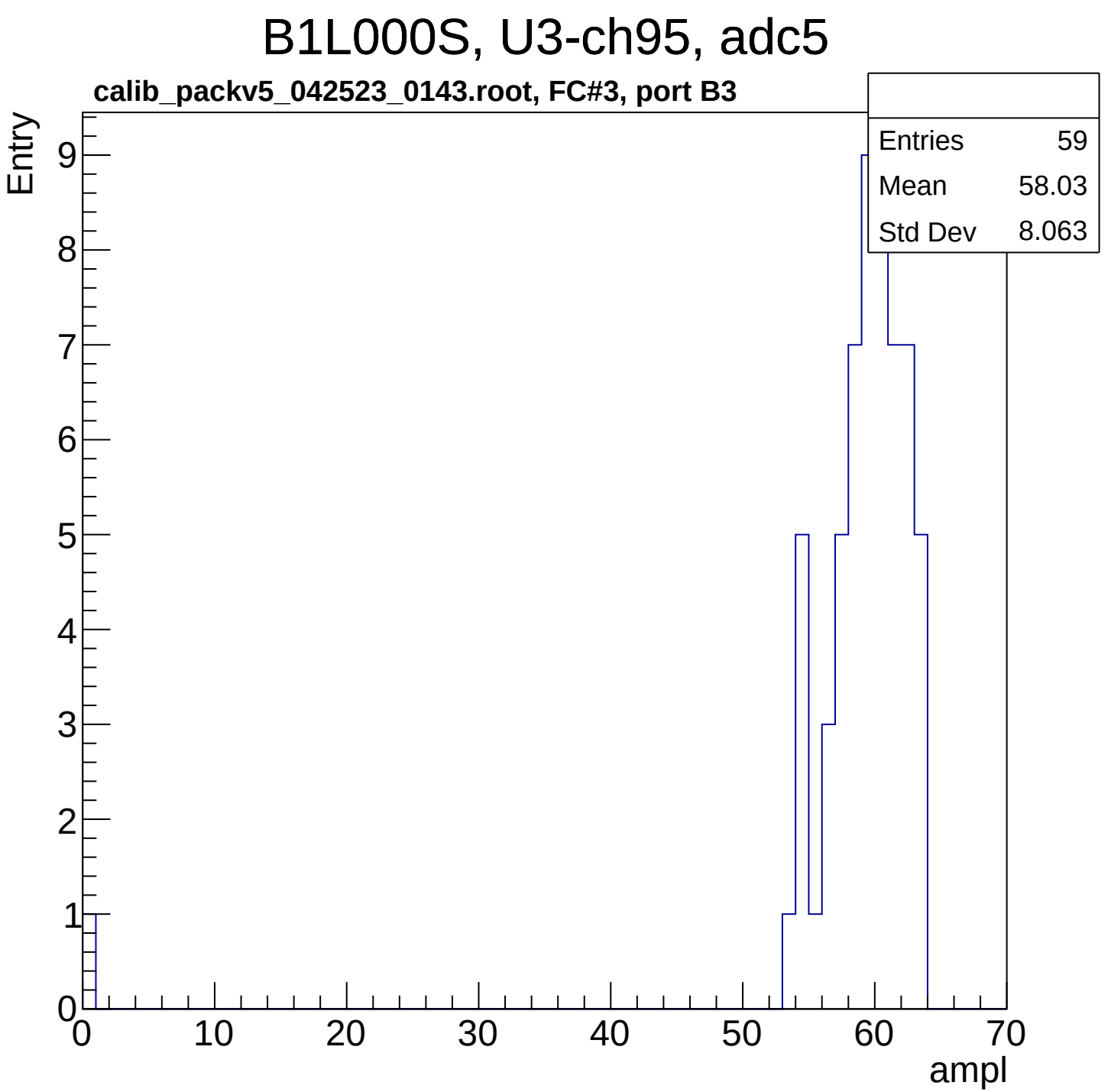
B1L000S, U3-ch95, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.03
Std Dev 8.063
Entry
ampl
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2
3
4
5
6
7
8
9
0
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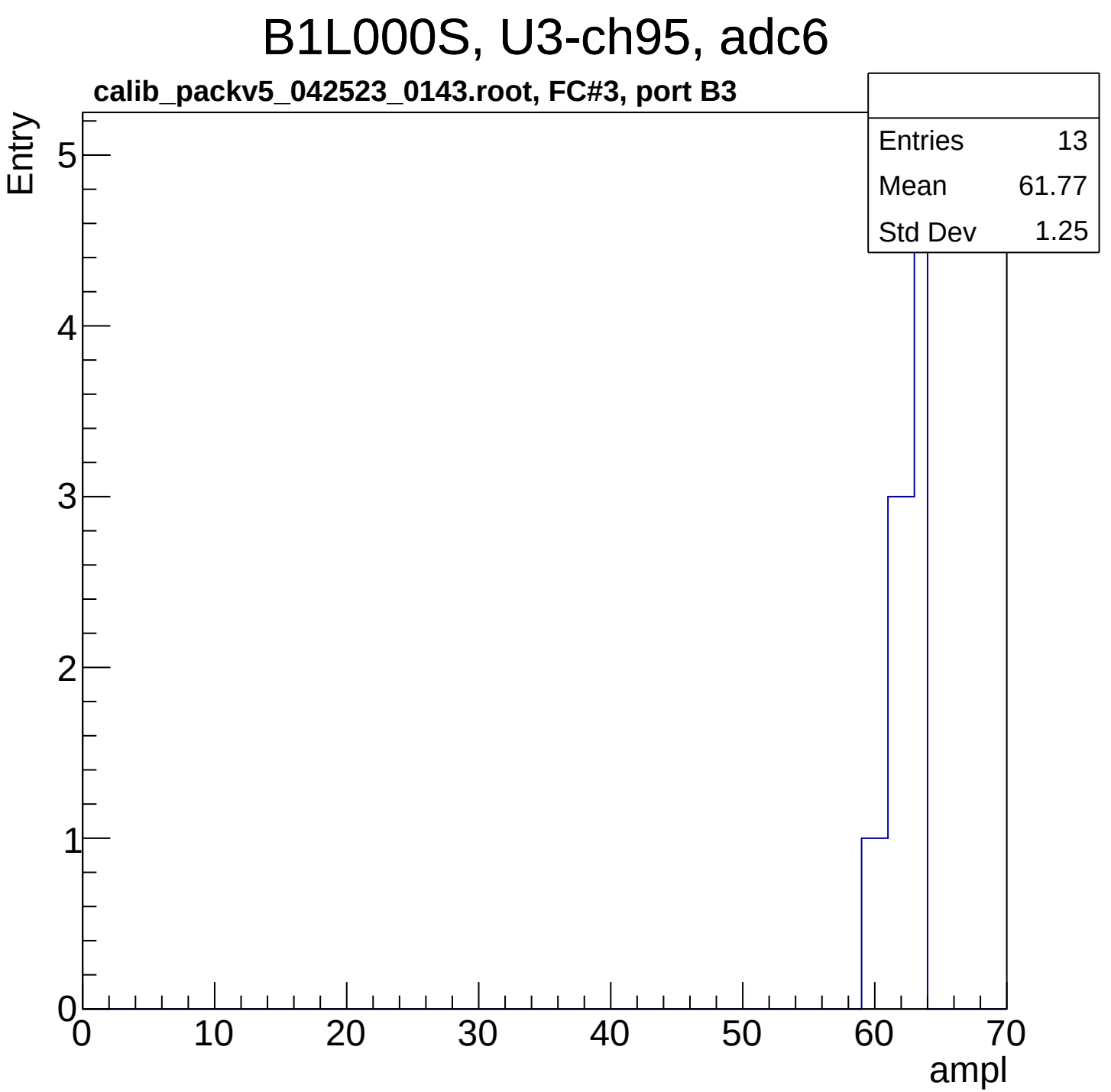
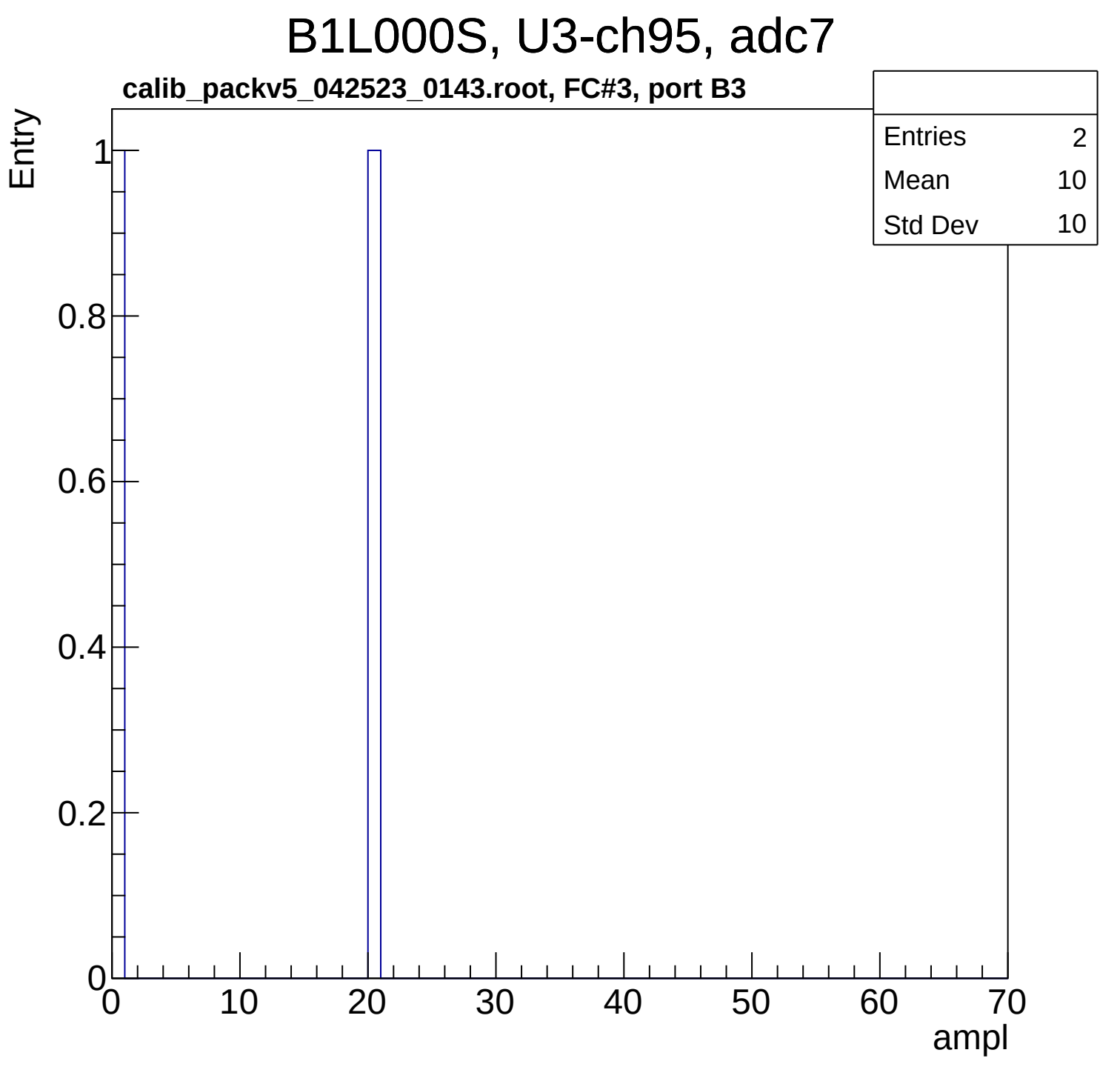
20
30
40
50
60
70


B1L000S, U3-ch95, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.77
Std Dev 1.25
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch95, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10
Std Dev 10
Entry
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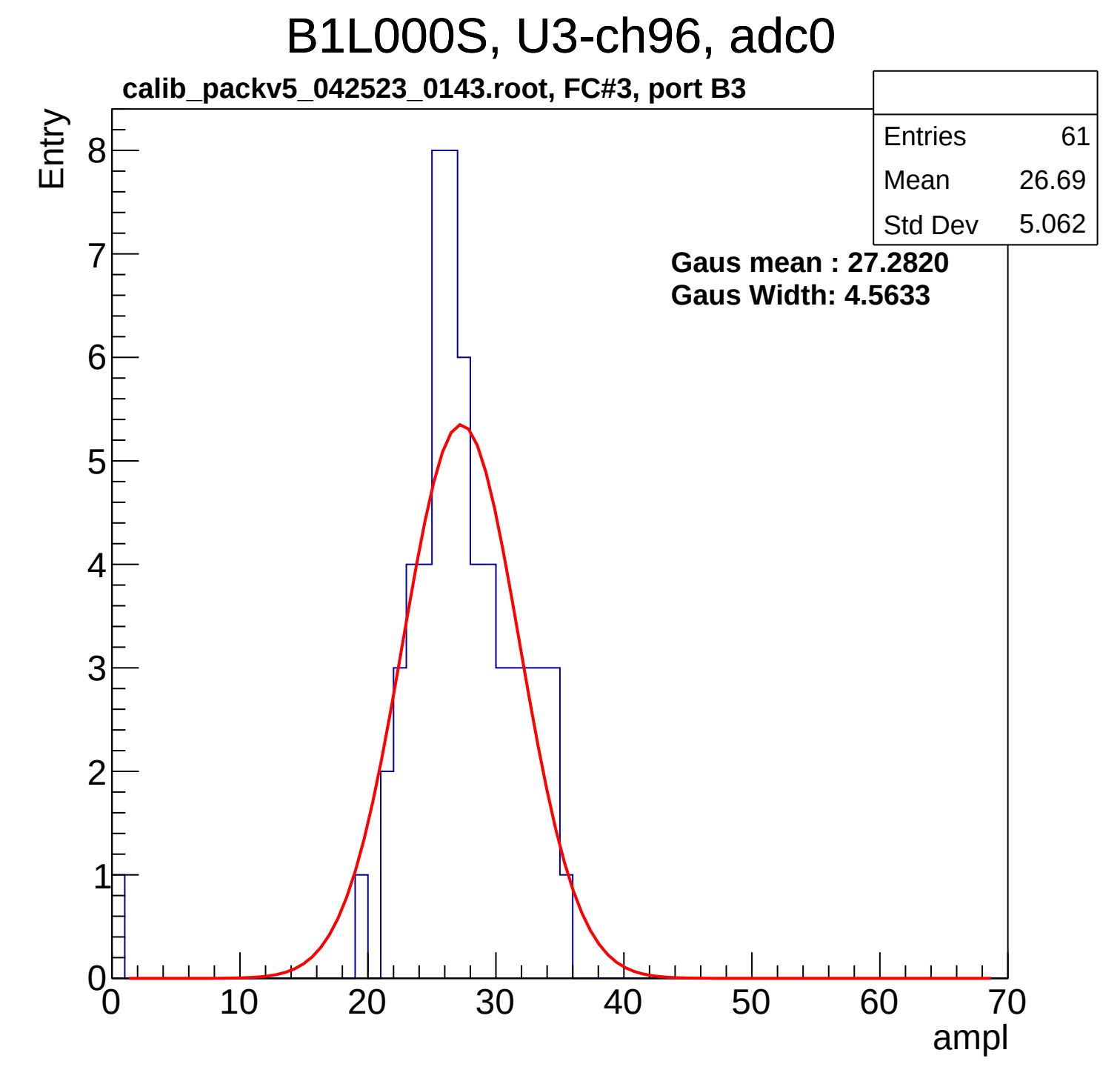
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch96, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 26.69
Std Dev 5.062
Gaus mean : 27.2820
Gaus Width: 4.5633
Entry
ampl
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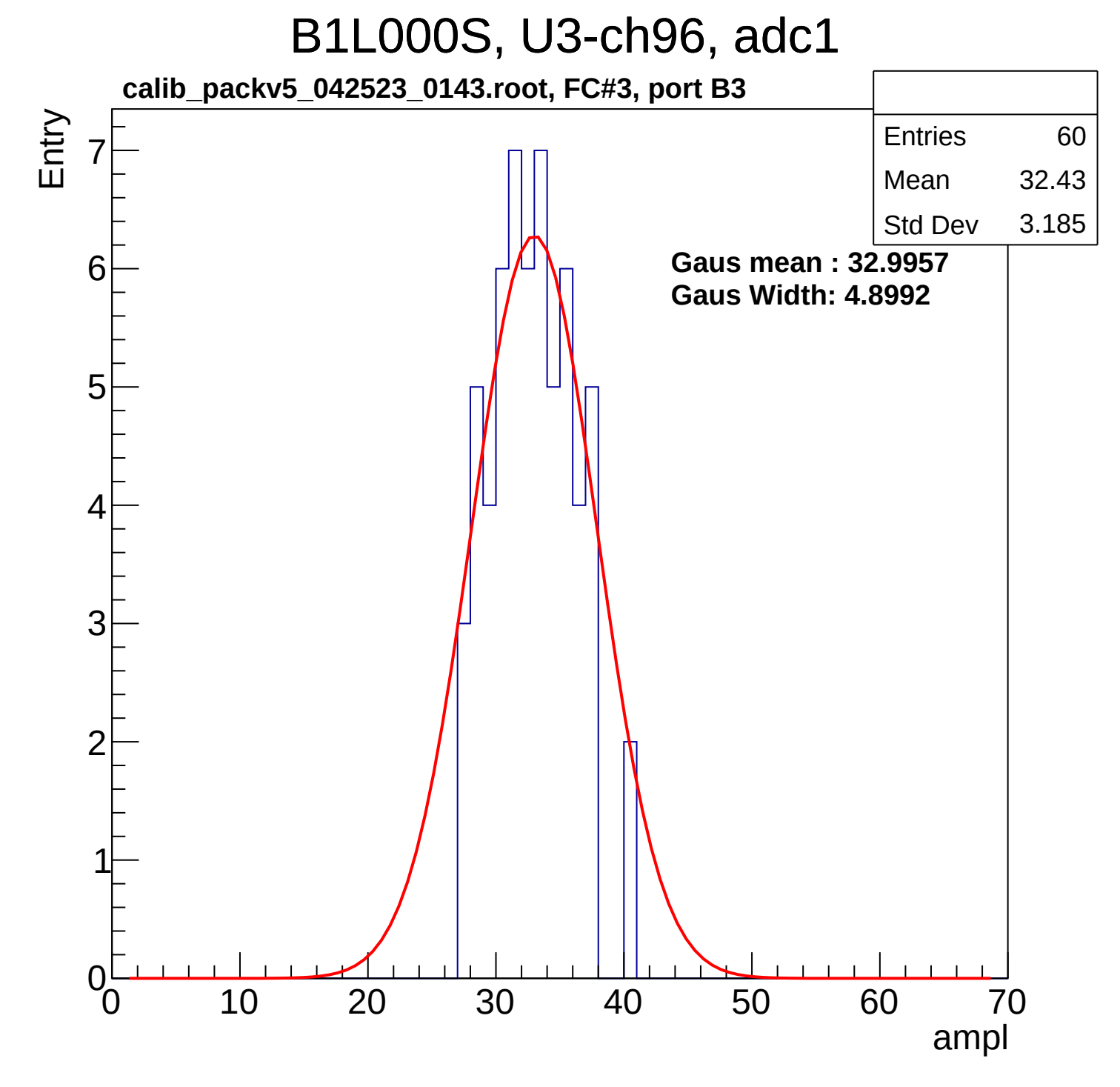
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch96, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 32.43
Std Dev 3.185
Gaus mean : 32.9957
Gaus Width: 4.8992
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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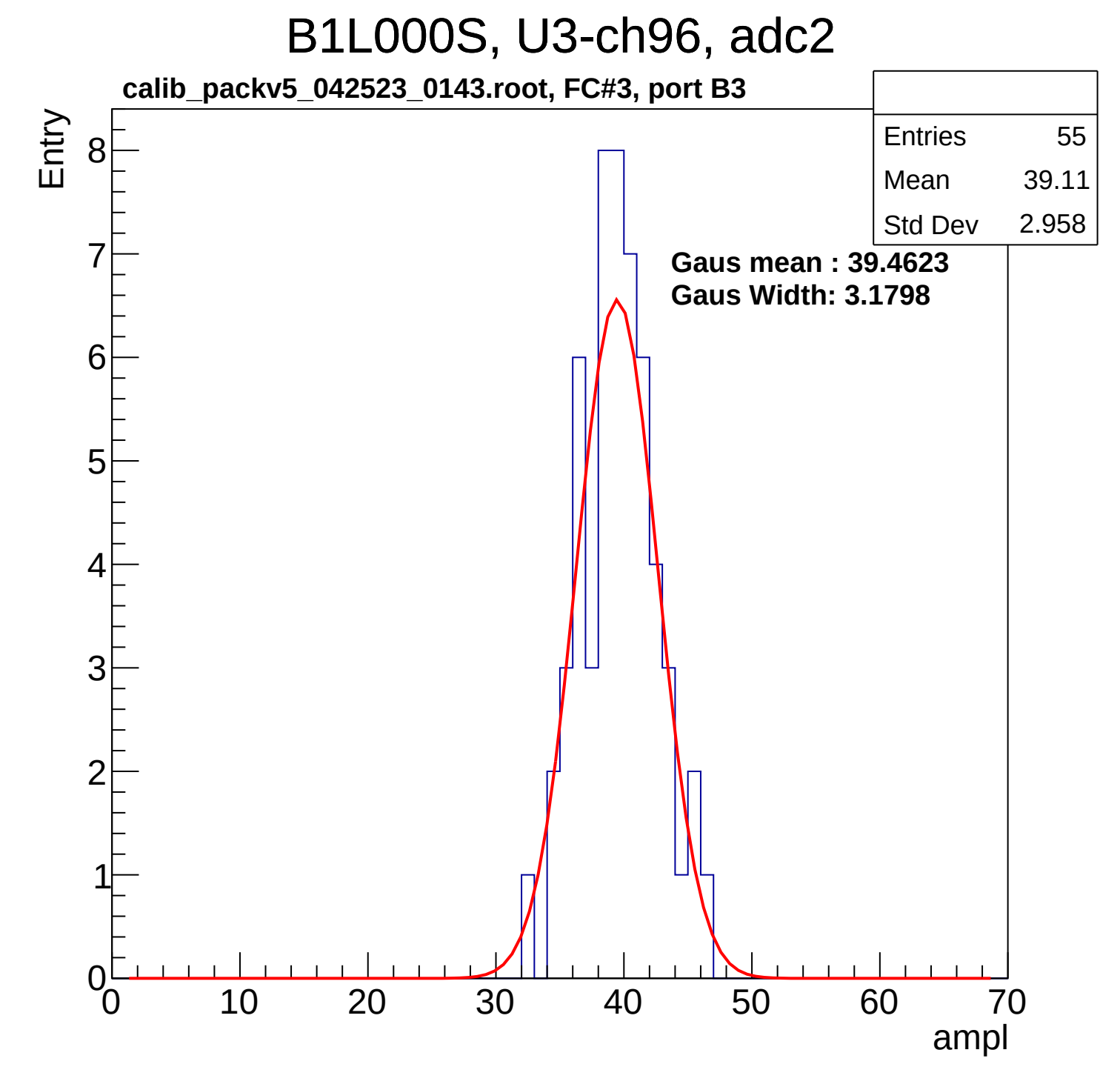
B1L000S, U3-ch96, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 39.11
Std Dev 2.958
Gaus mean : 39.4623
Gaus Width: 3.1798
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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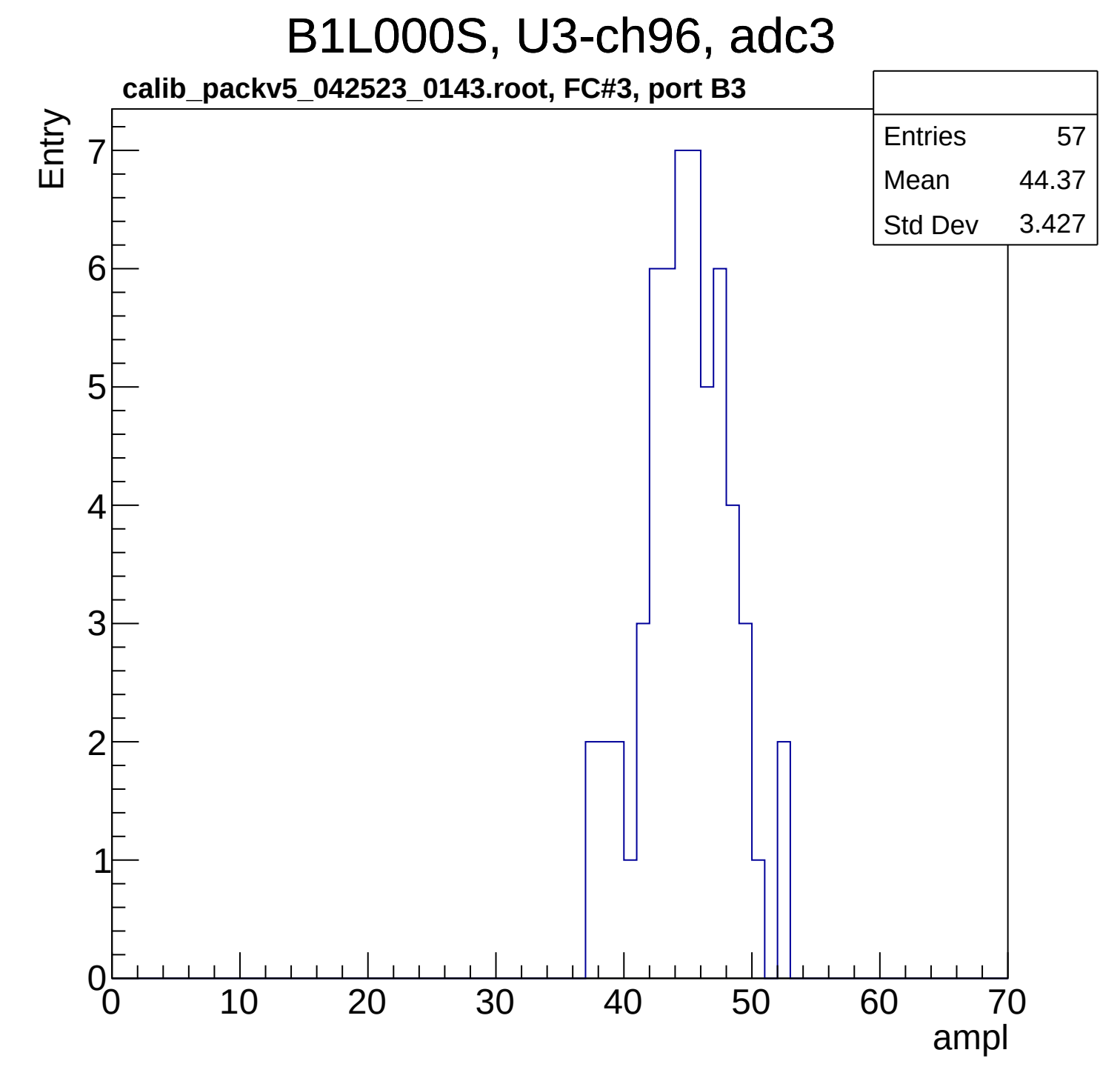
B1L000S, U3-ch96, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 44.37
Std Dev 3.427
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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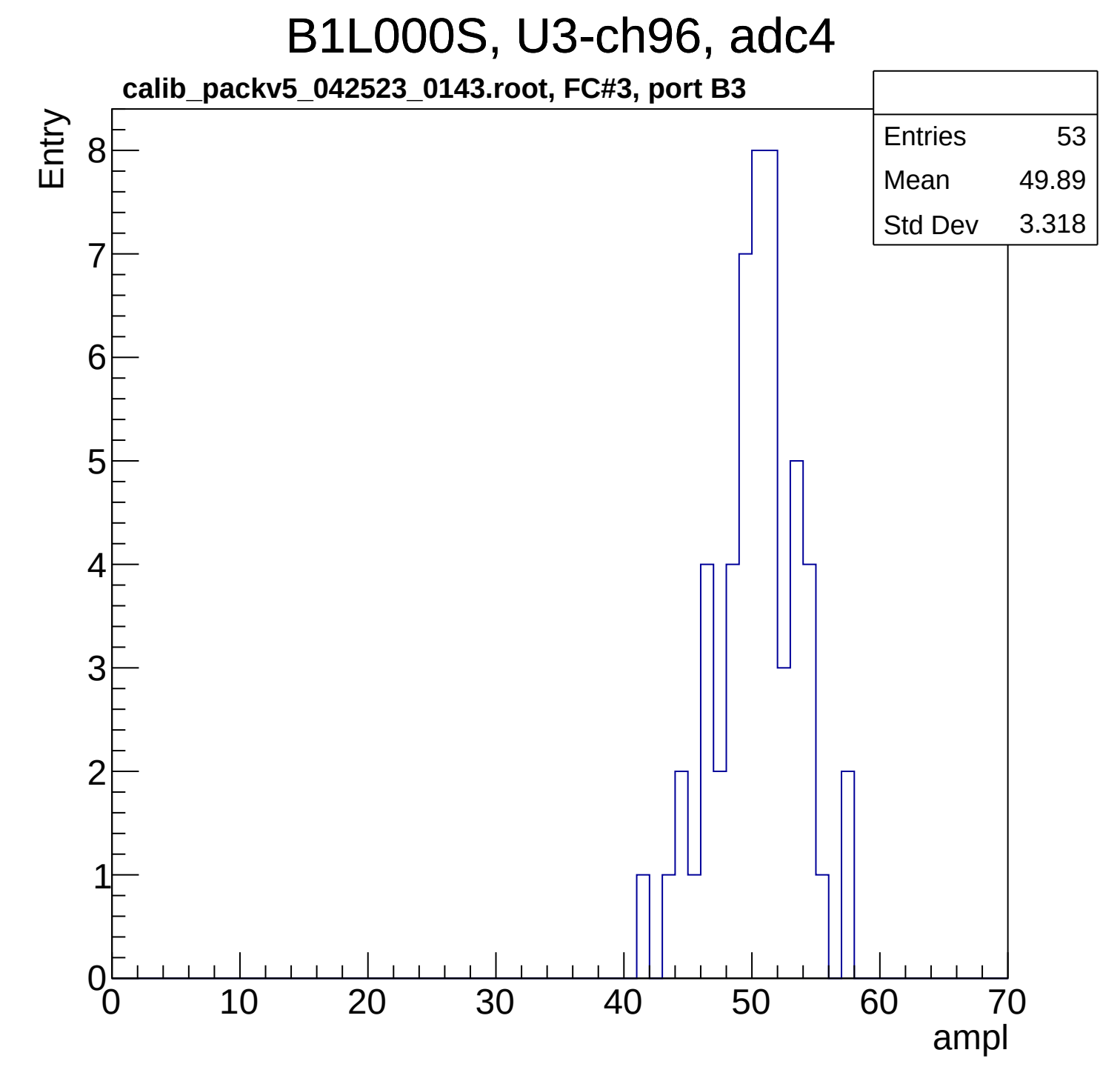
B1L000S, U3-ch96, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 49.89
Std Dev 3.318
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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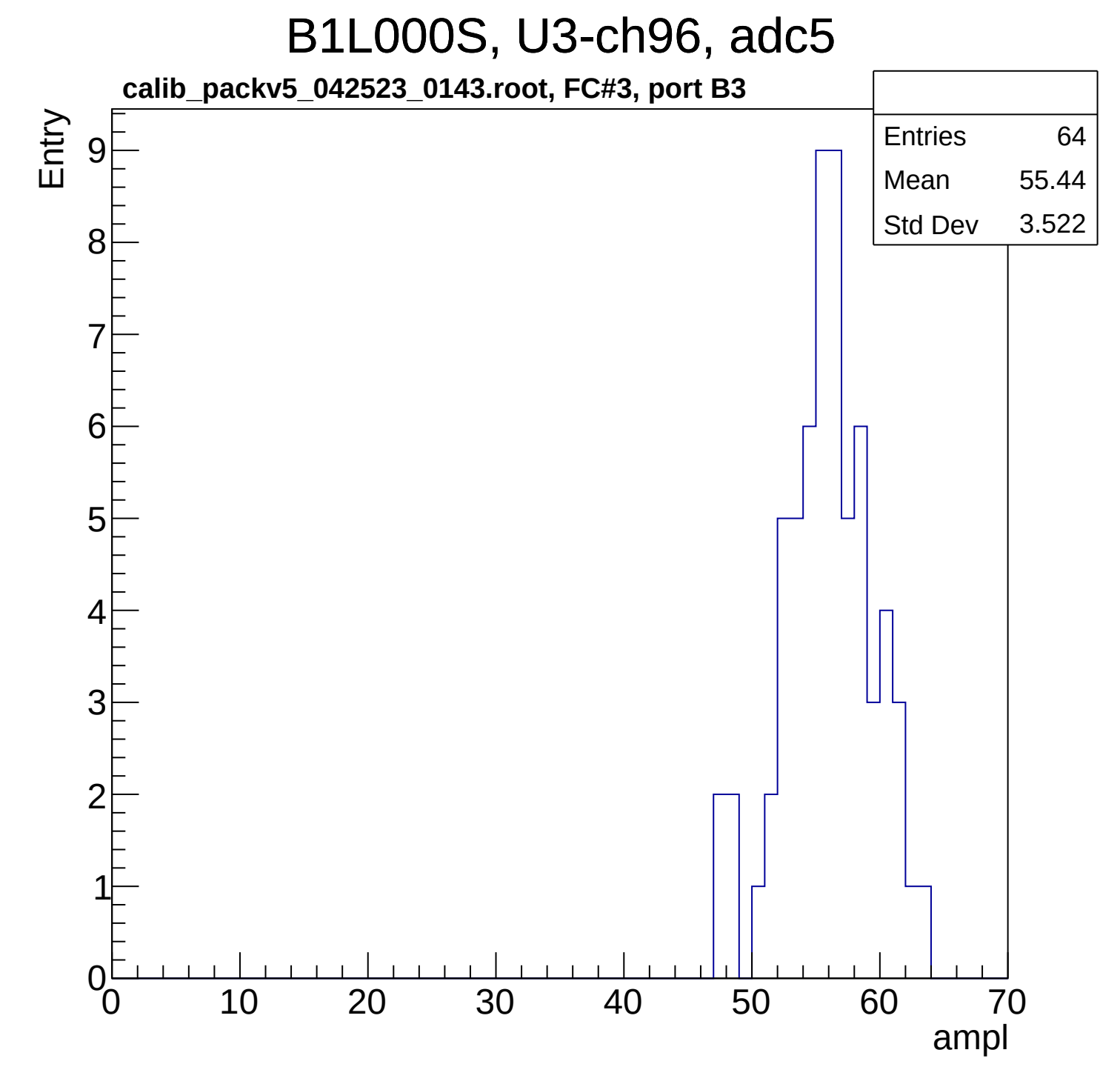
B1L000S, U3-ch96, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 55.44
Std Dev 3.522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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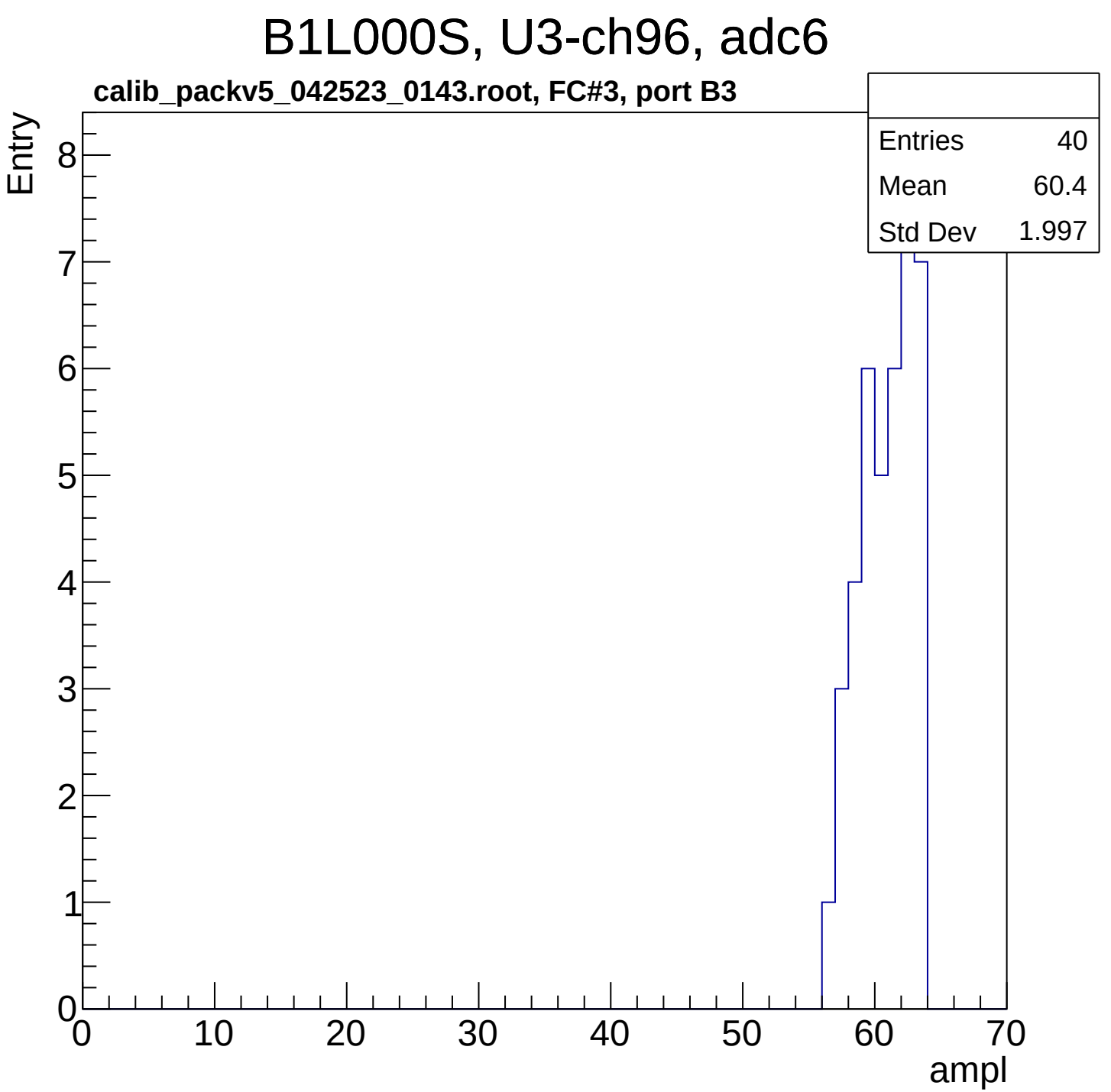
B1L000S, U3-ch96, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 60.4
Std Dev 1.997
Entry
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2
3
4
5
6
7
8
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20
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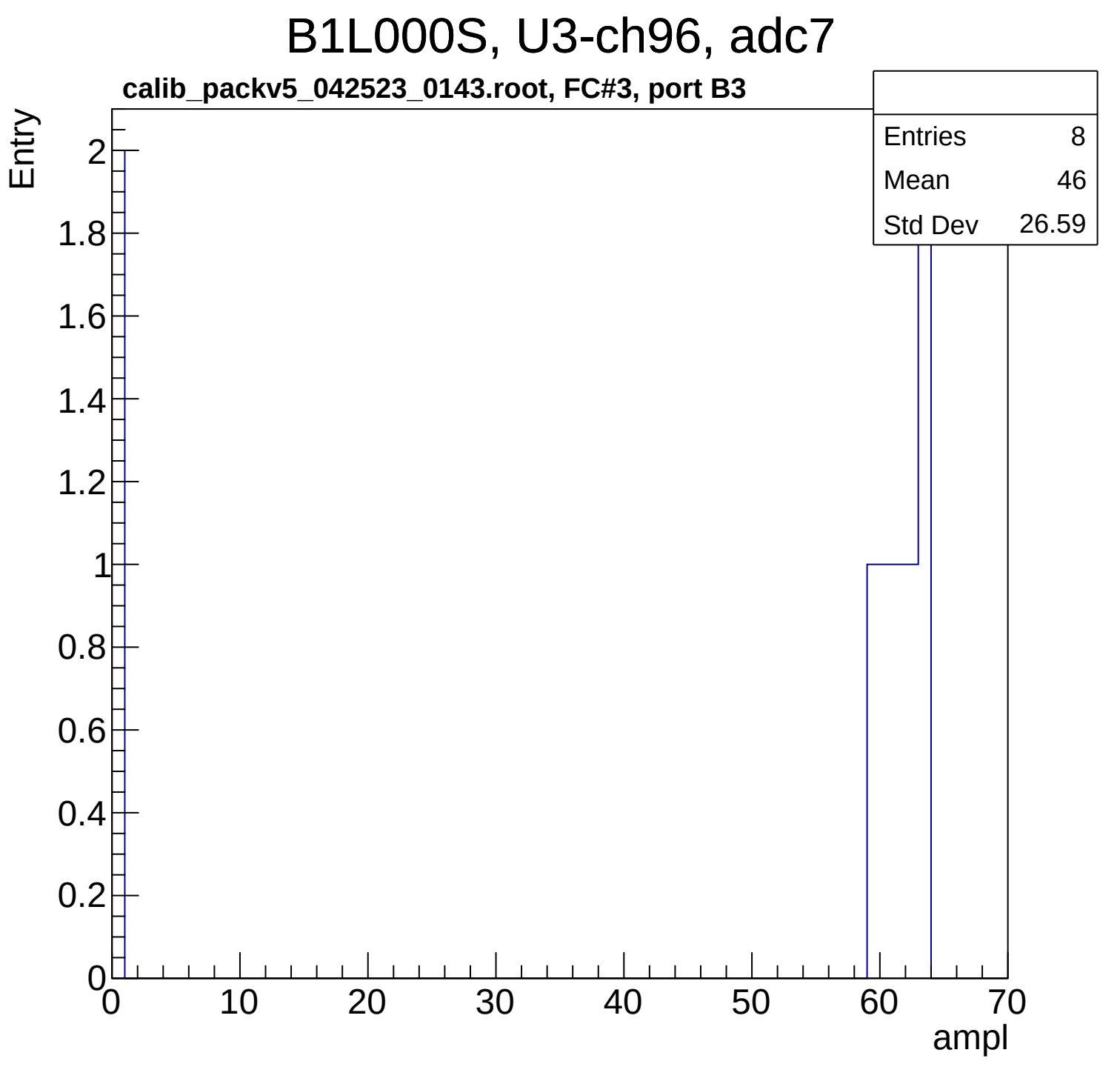
50
60
70
ampl


B1L000S, U3-ch96, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 46
Std Dev 26.59
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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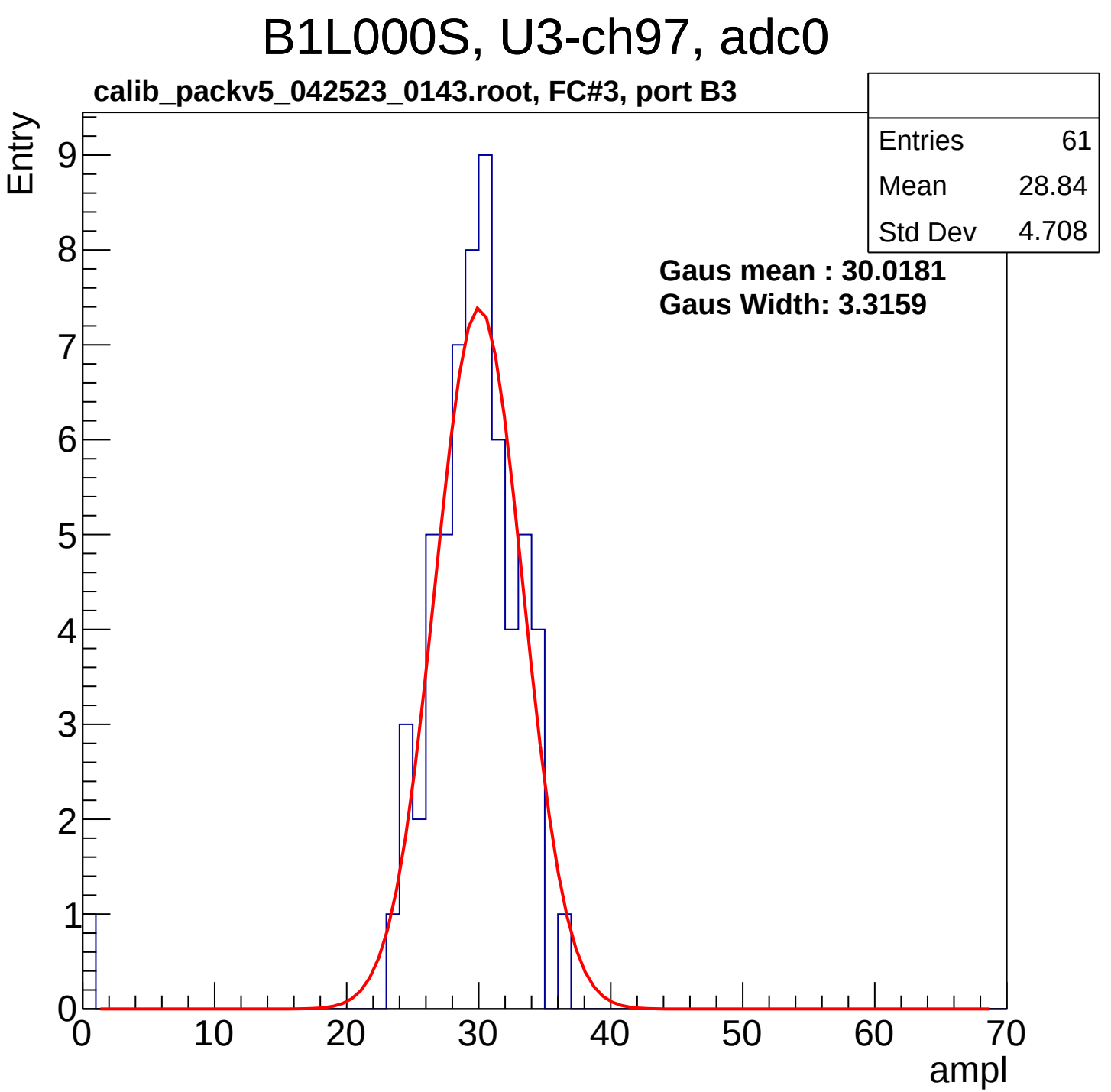
B1L000S, U3-ch97, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 28.84
Std Dev 4.708
Gaus mean : 30.0181
Gaus Width: 3.3159
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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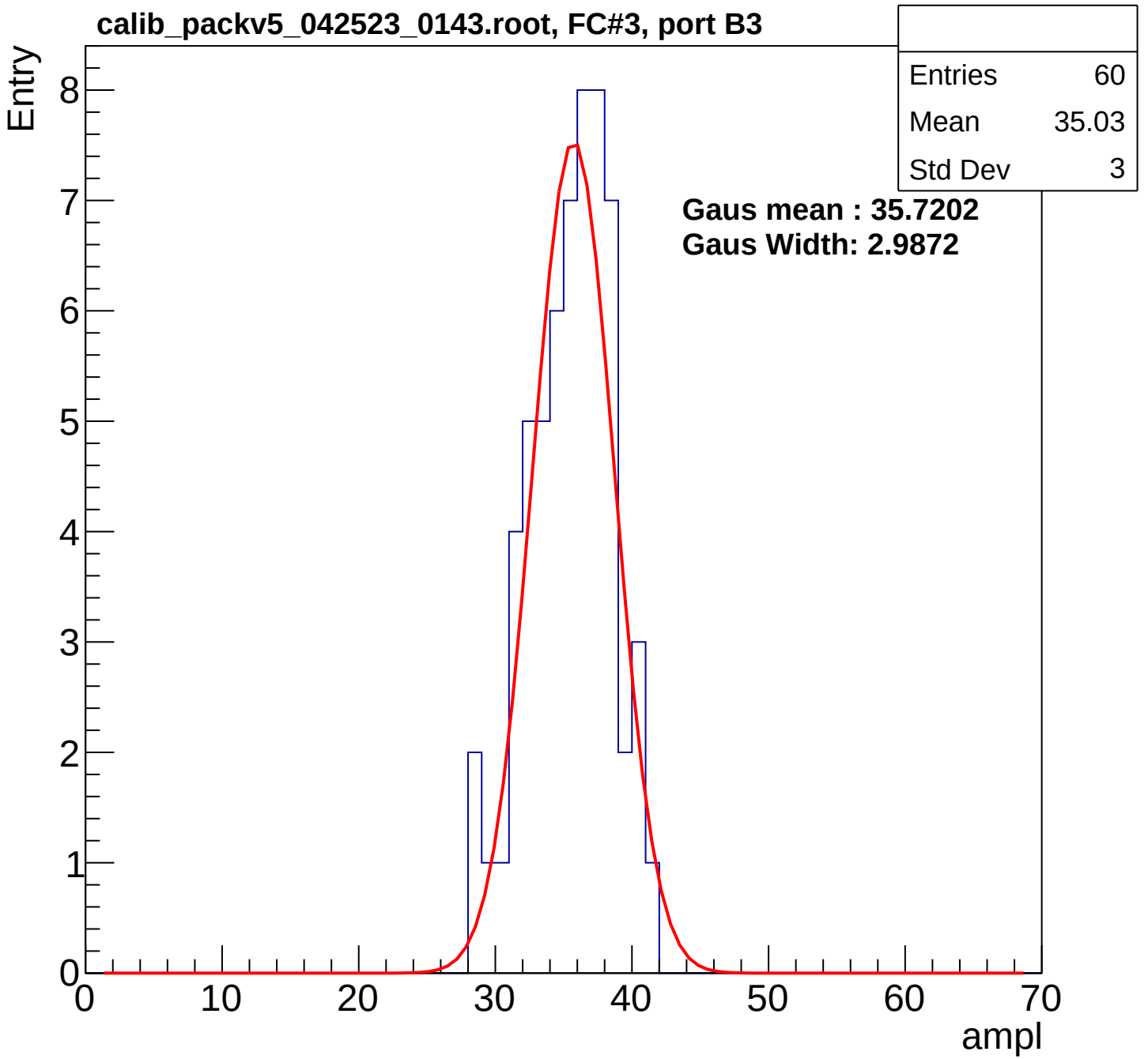
B1L000S, U3-ch97, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 35.03
Std Dev 3
Gaus mean : 35.7202
Gaus Width: 2.9872
Entry
ampl
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1
2
3
4
5
6
7
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0
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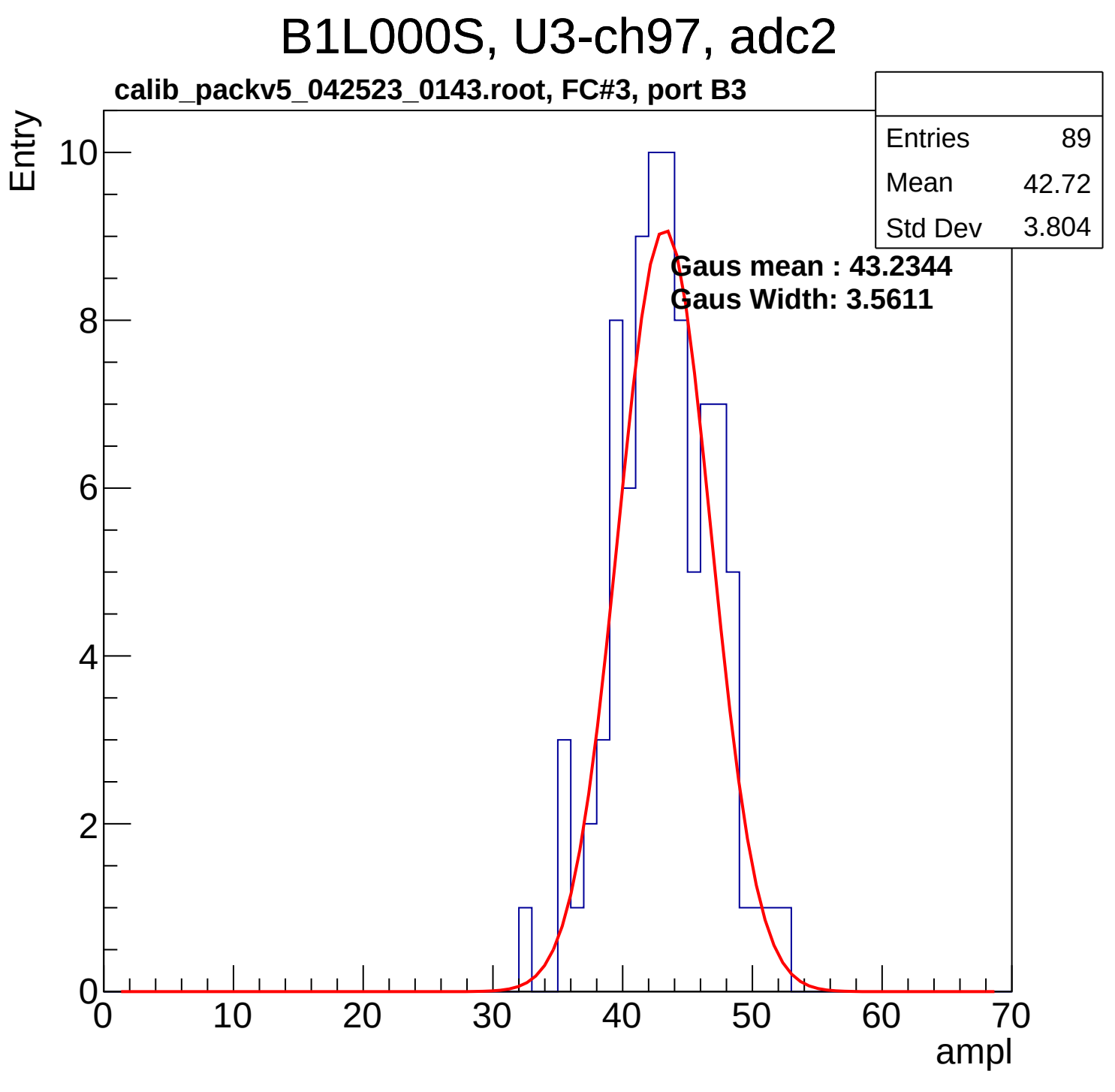
B1L000S, U3-ch97, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 89
Mean 42.72
Std Dev 3.804
Gaus mean : 43.2344
Gaus Width: 3.5611
Entry
ampl
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2
4
6
8
10
0
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20
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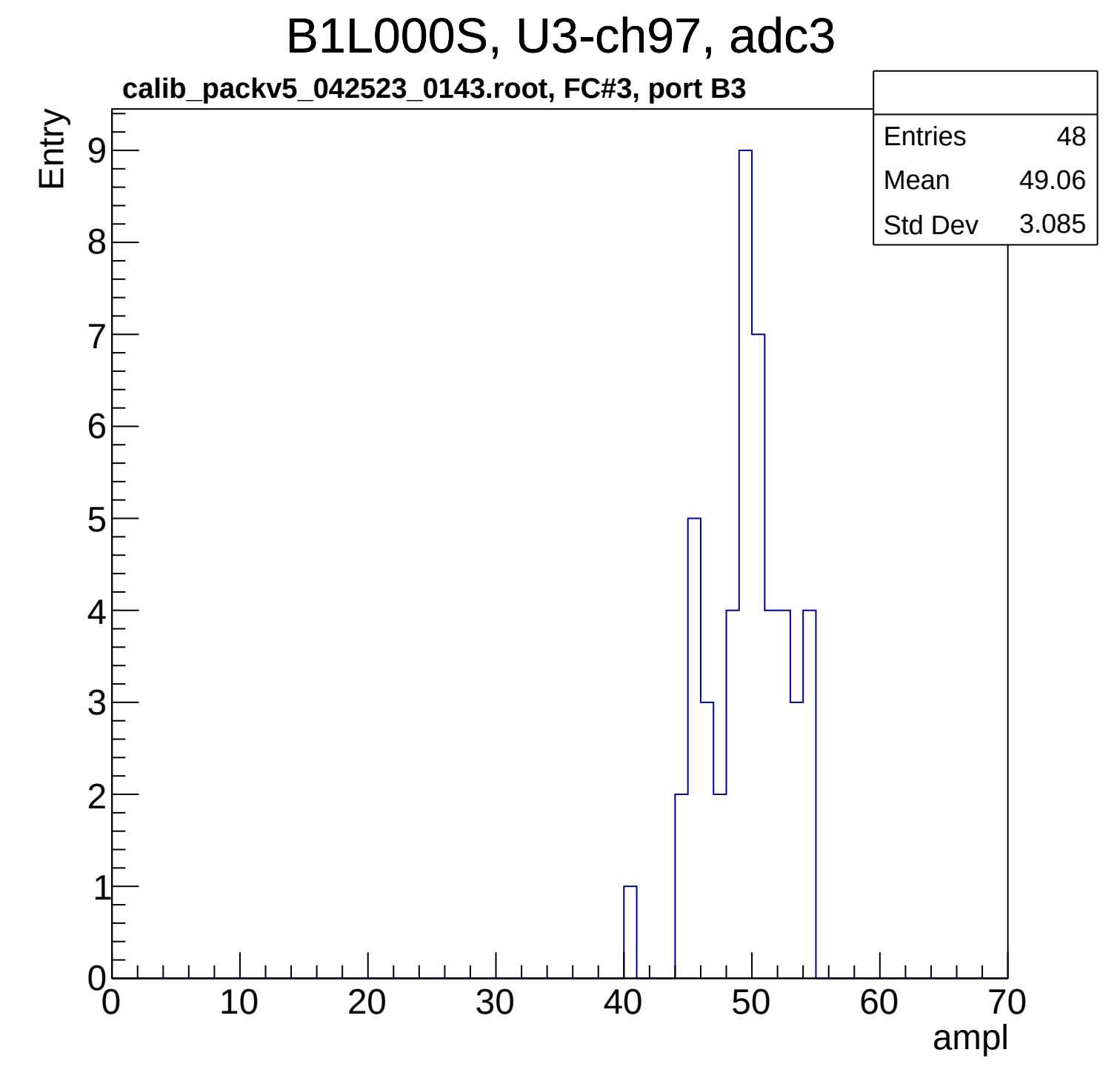
B1L000S, U3-ch97, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 49.06
Std Dev 3.085
Entry
ampl
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2
3
4
5
6
7
8
9
0
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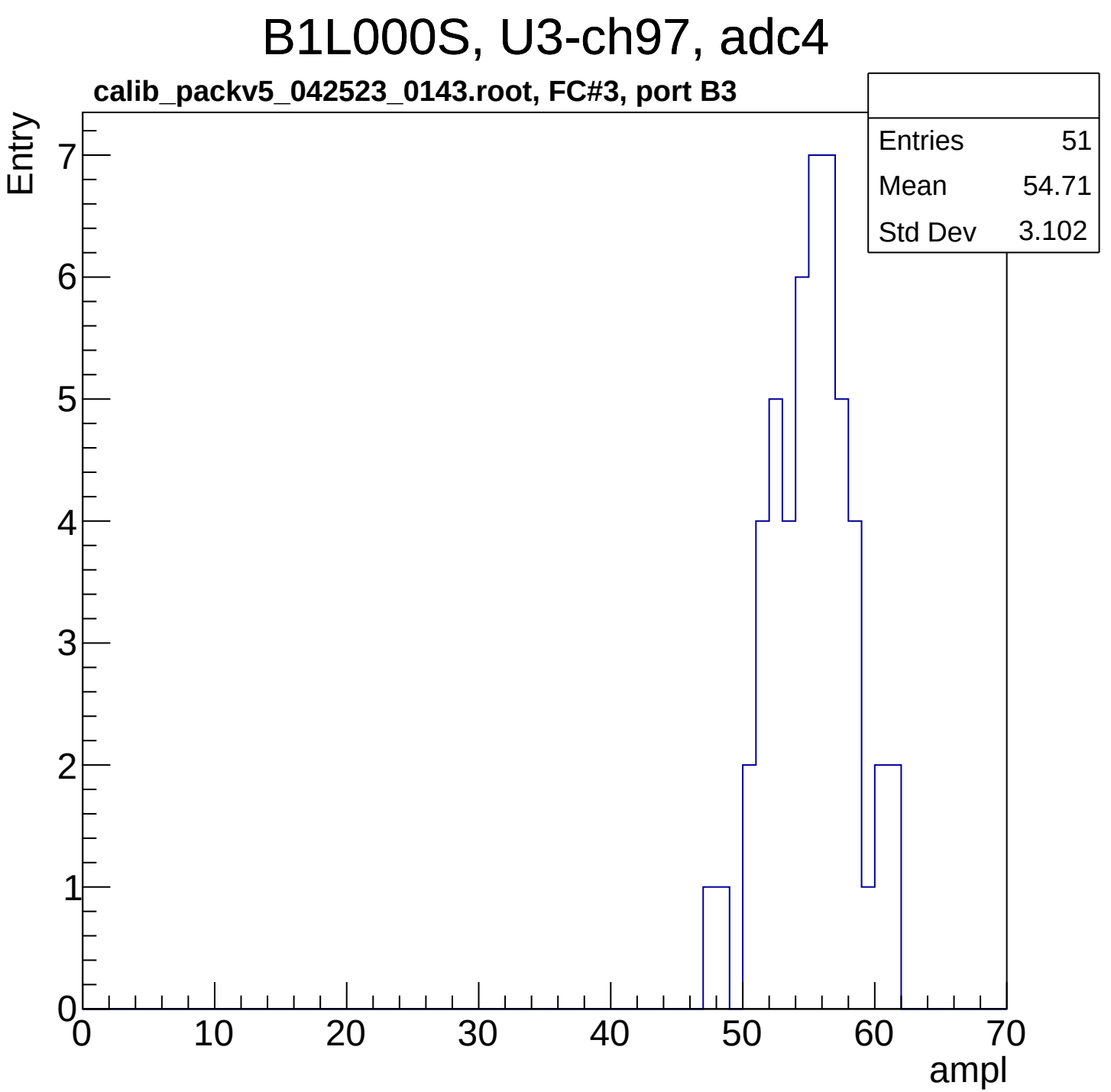
B1L000S, U3-ch97, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.71
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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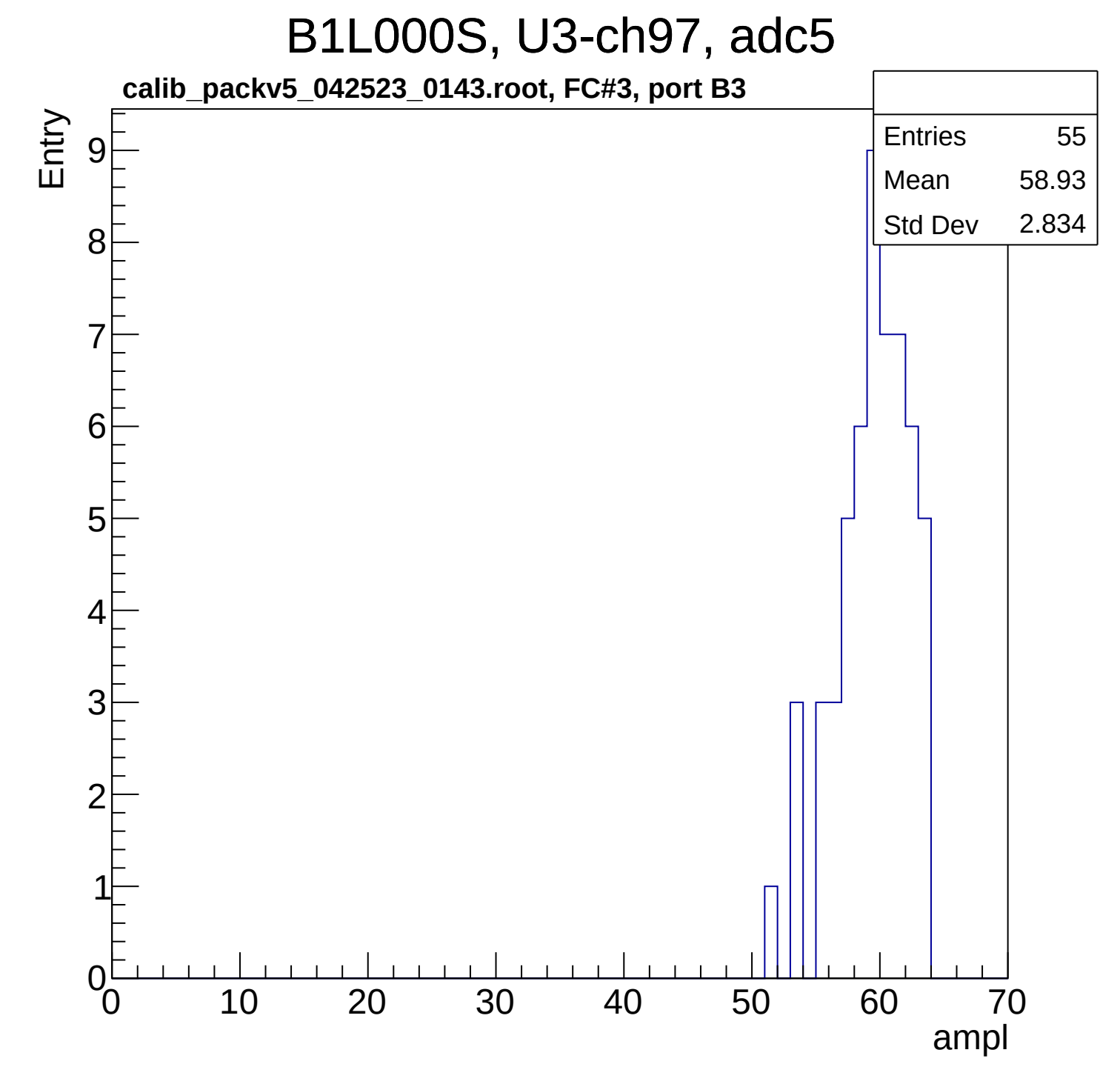
B1L000S, U3-ch97, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.93
Std Dev 2.834
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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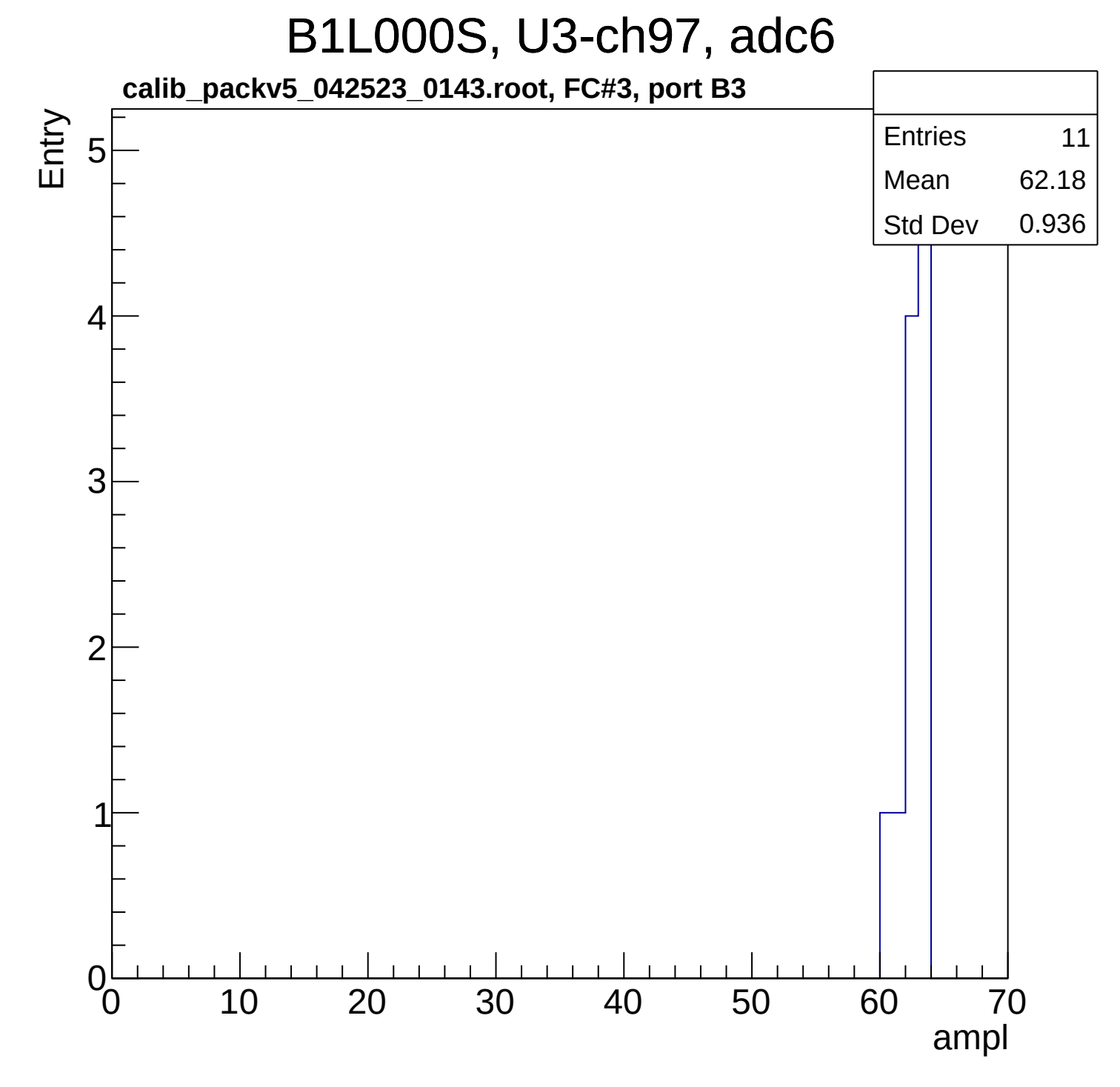
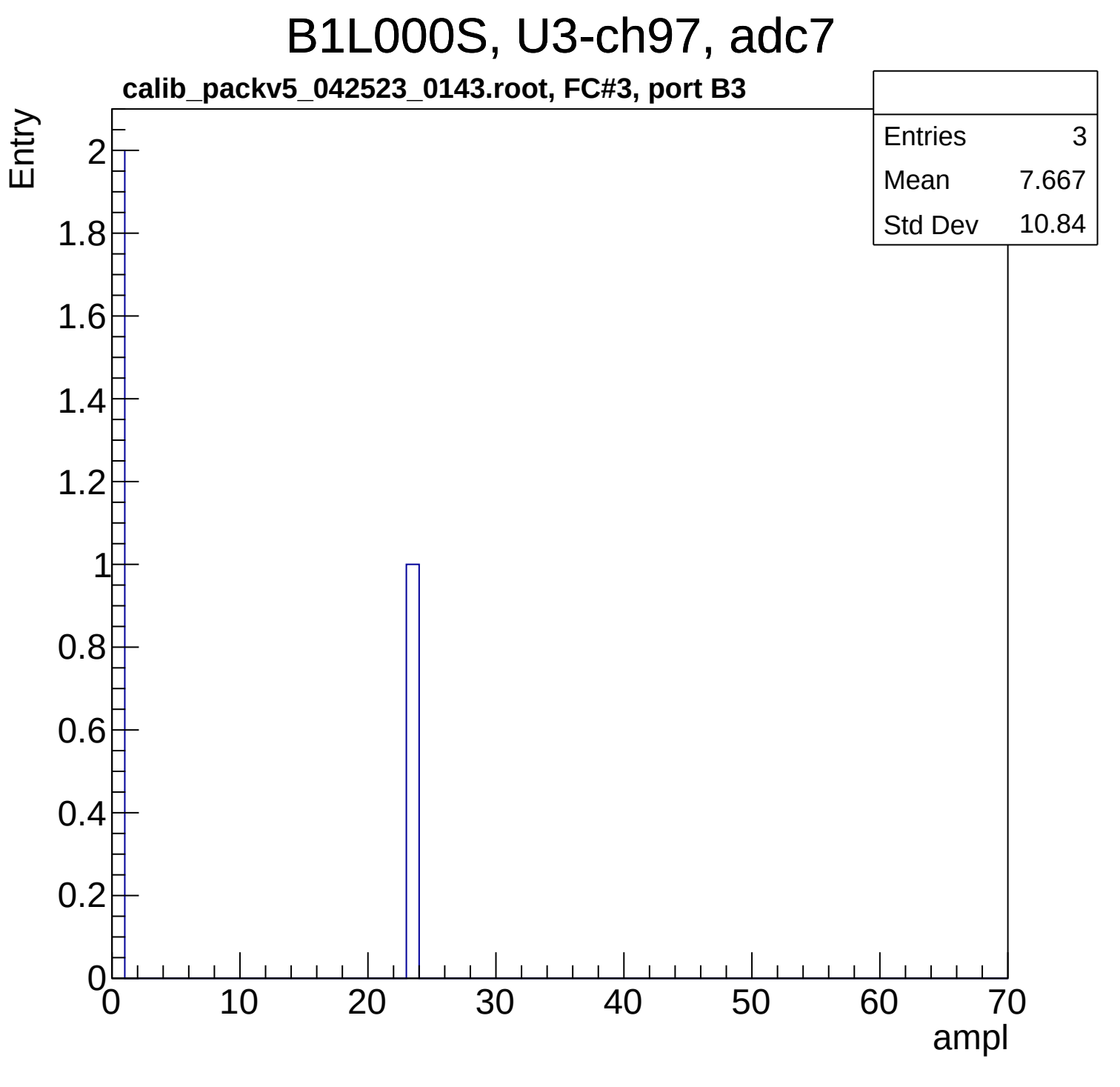

B1L000S, U3-ch97, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 62.18
Std Dev 0.936
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch97, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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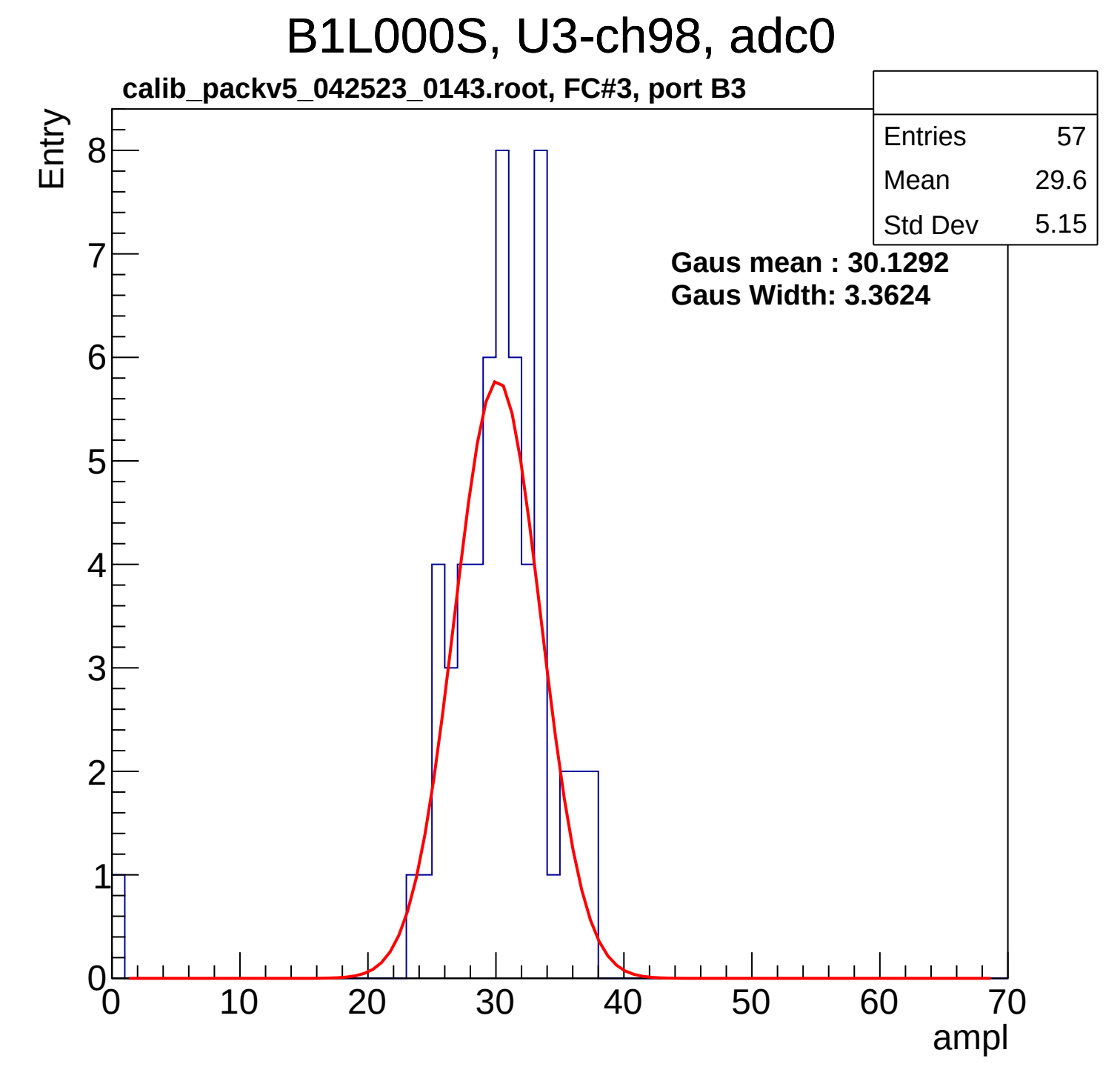
B1L000S, U3-ch98, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 29.6
Std Dev 5.15
Gaus mean : 30.1292
Gaus Width: 3.3624
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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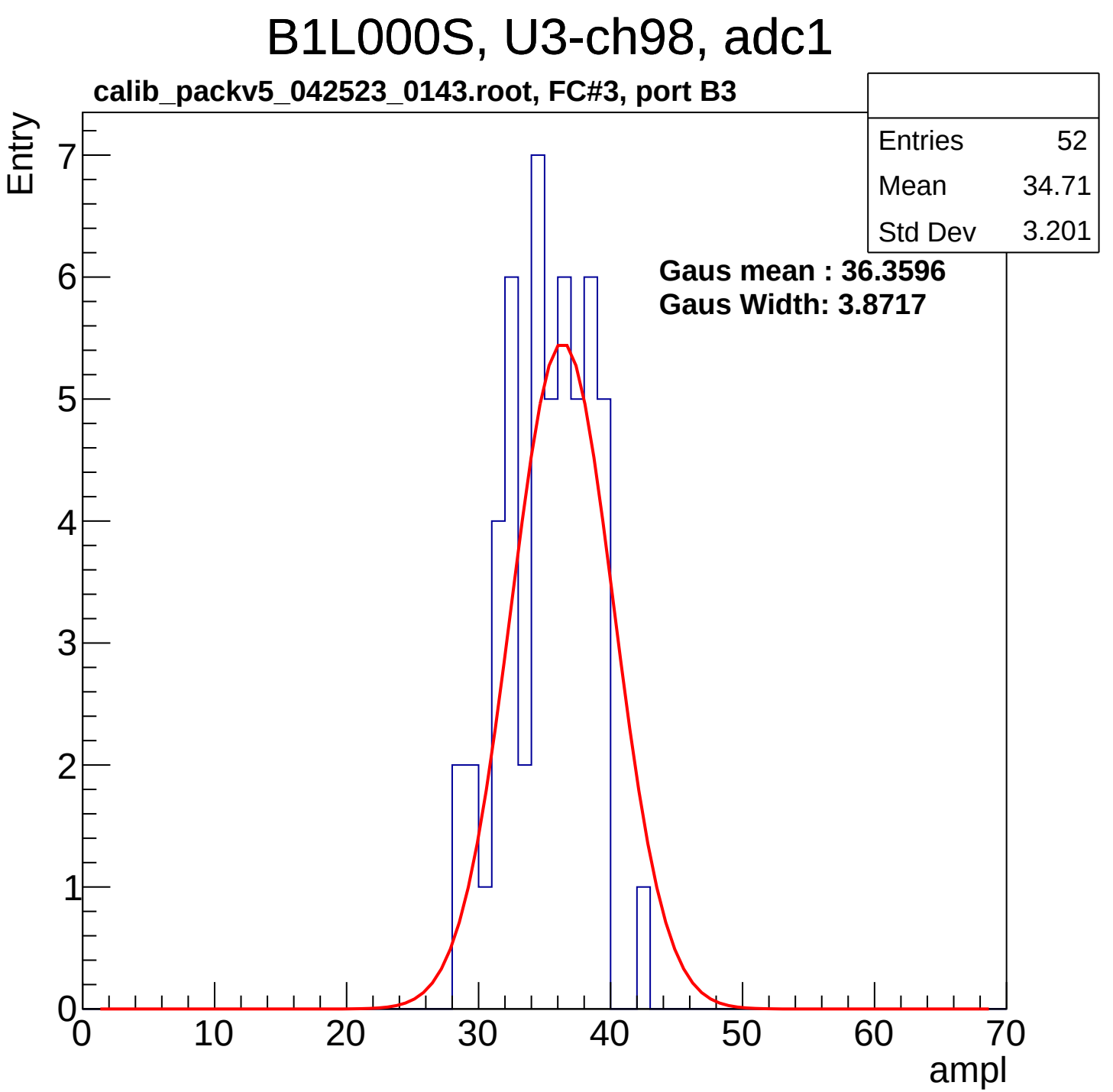
B1L000S, U3-ch98, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 34.71
Std Dev 3.201
Gaus mean : 36.3596
Gaus Width: 3.8717
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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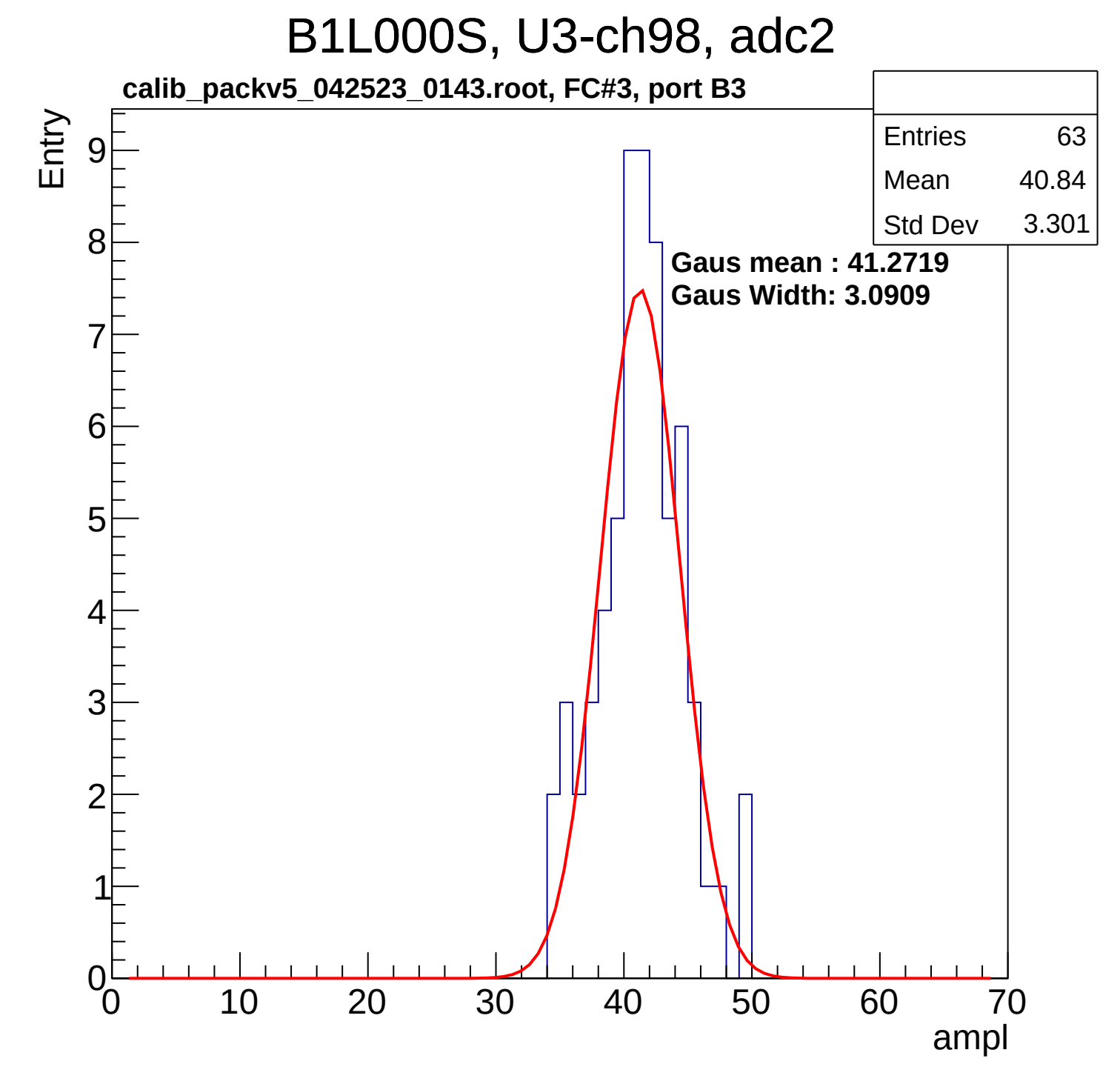
B1L000S, U3-ch98, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 40.84
Std Dev 3.301
Gaus mean : 41.2719
Gaus Width: 3.0909
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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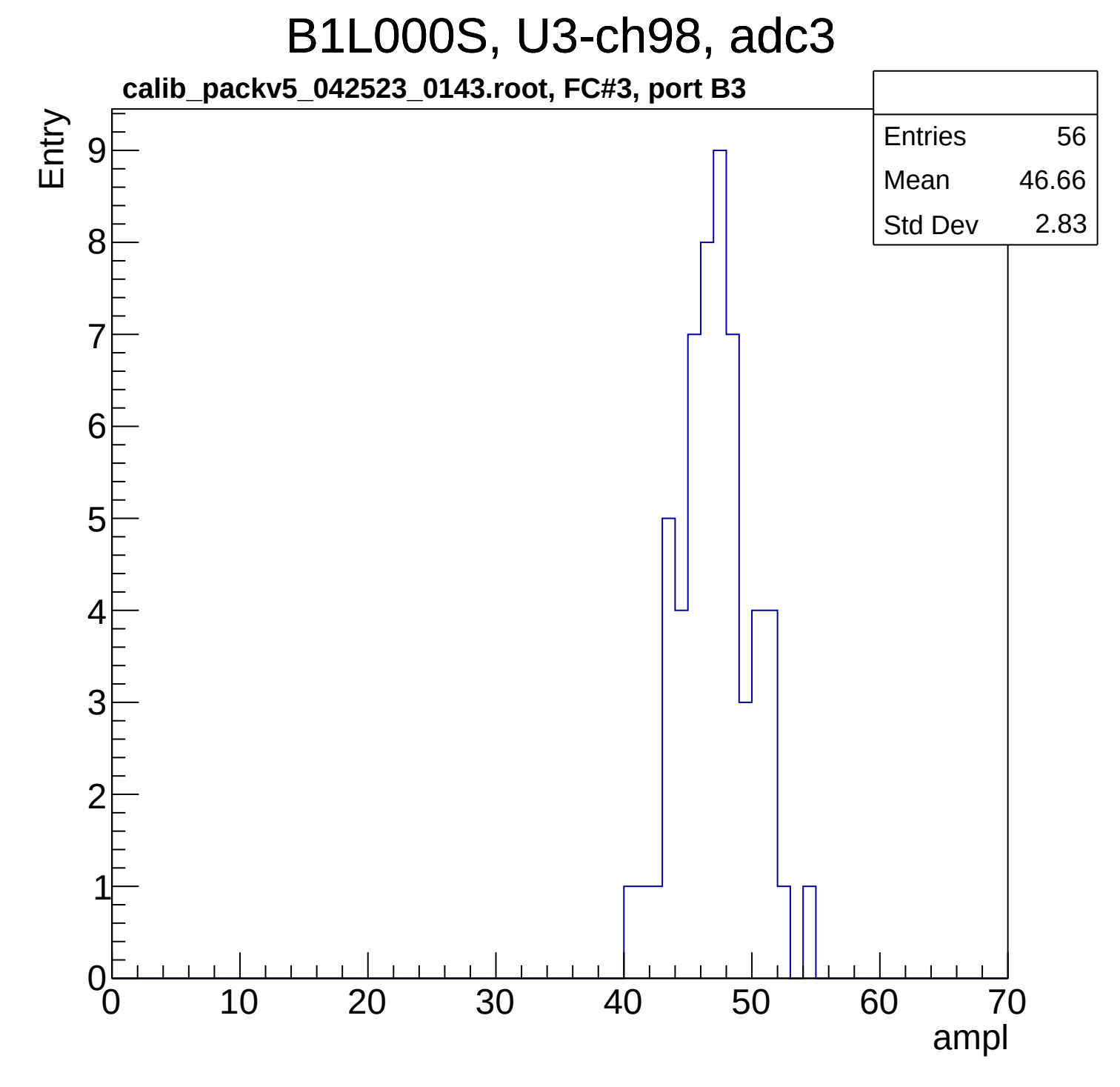
B1L000S, U3-ch98, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 46.66
Std Dev 2.83
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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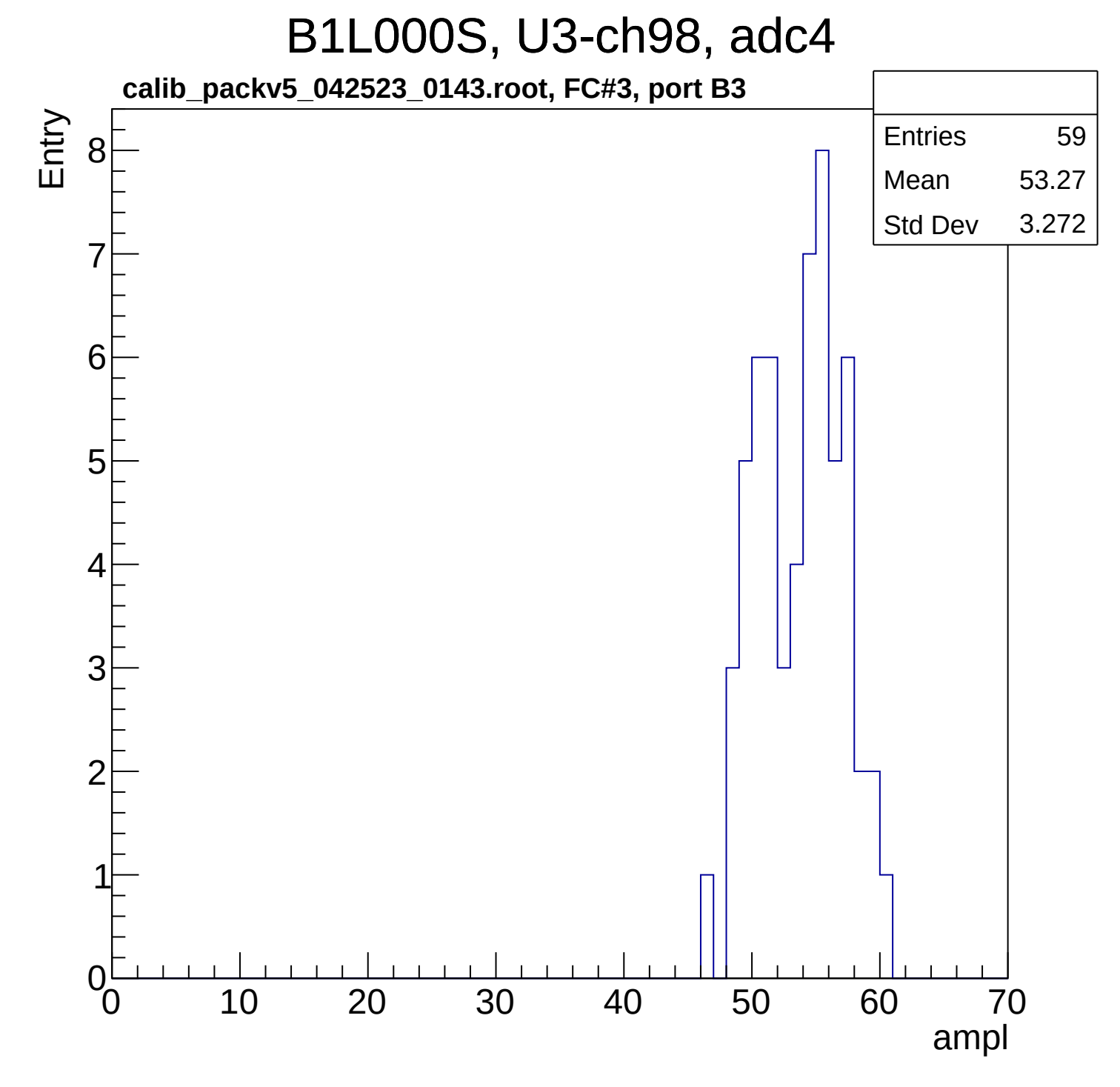
B1L000S, U3-ch98, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 53.27
Std Dev 3.272
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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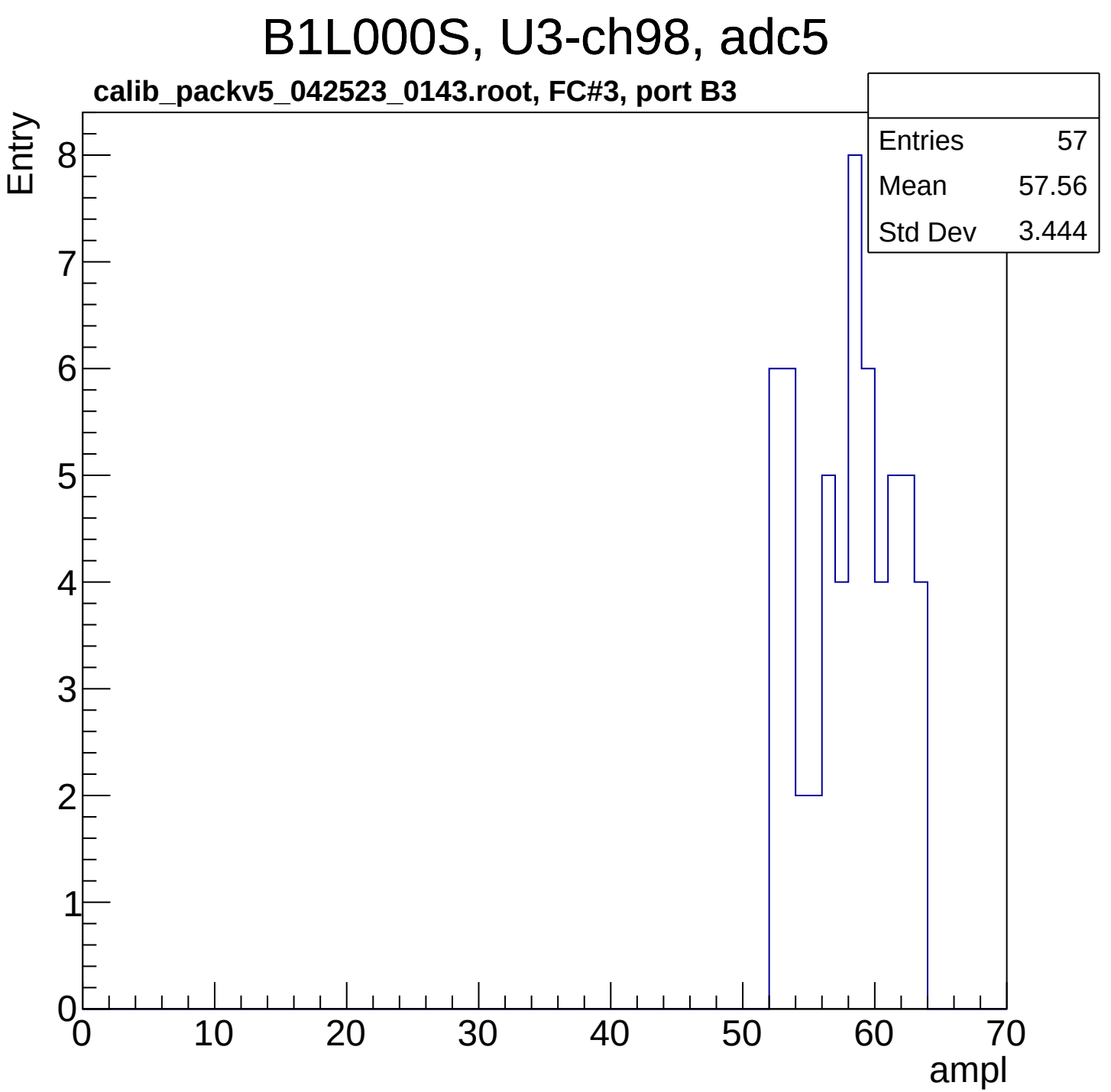
B1L000S, U3-ch98, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 57.56
Std Dev 3.444
Entry
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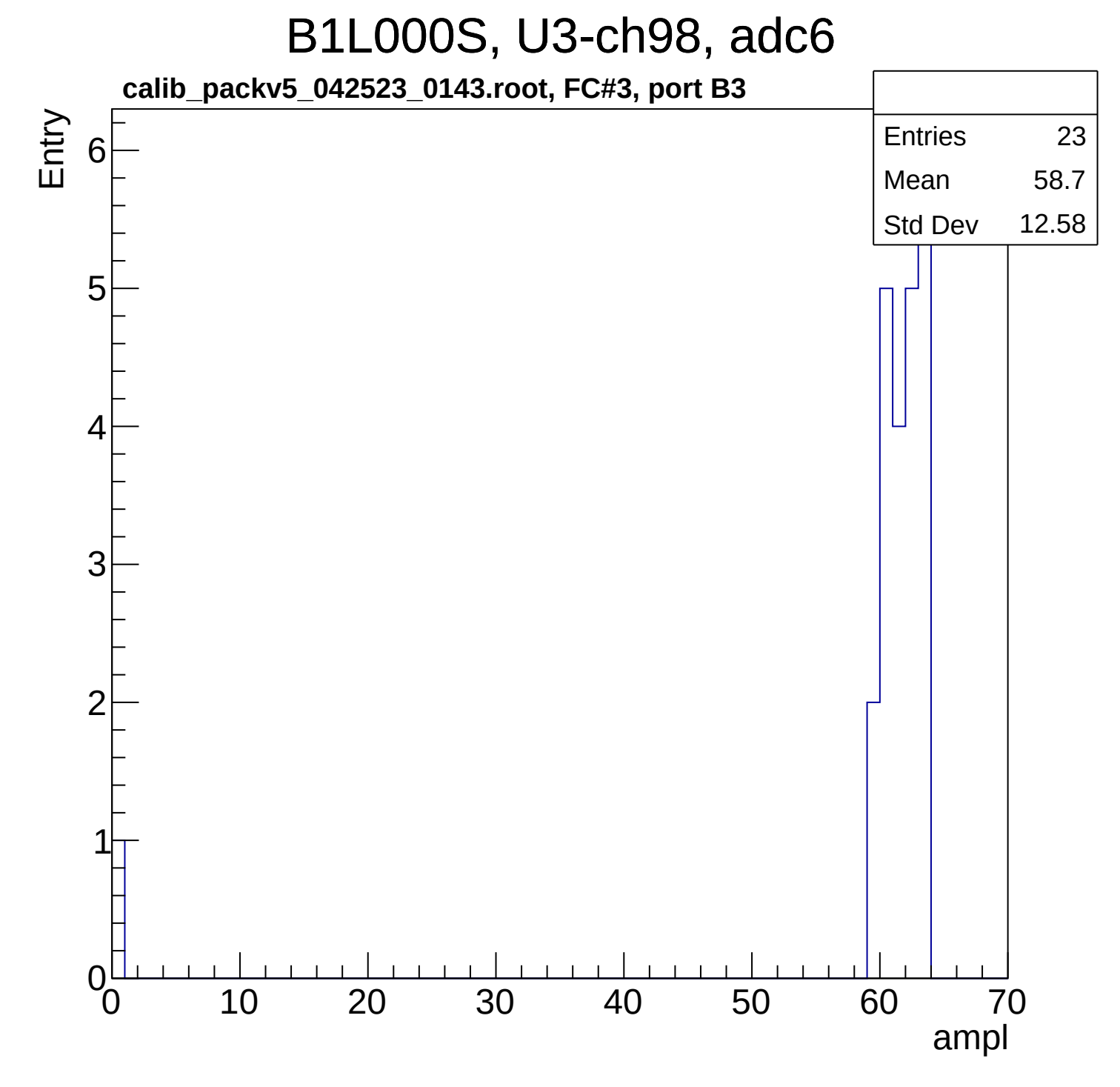
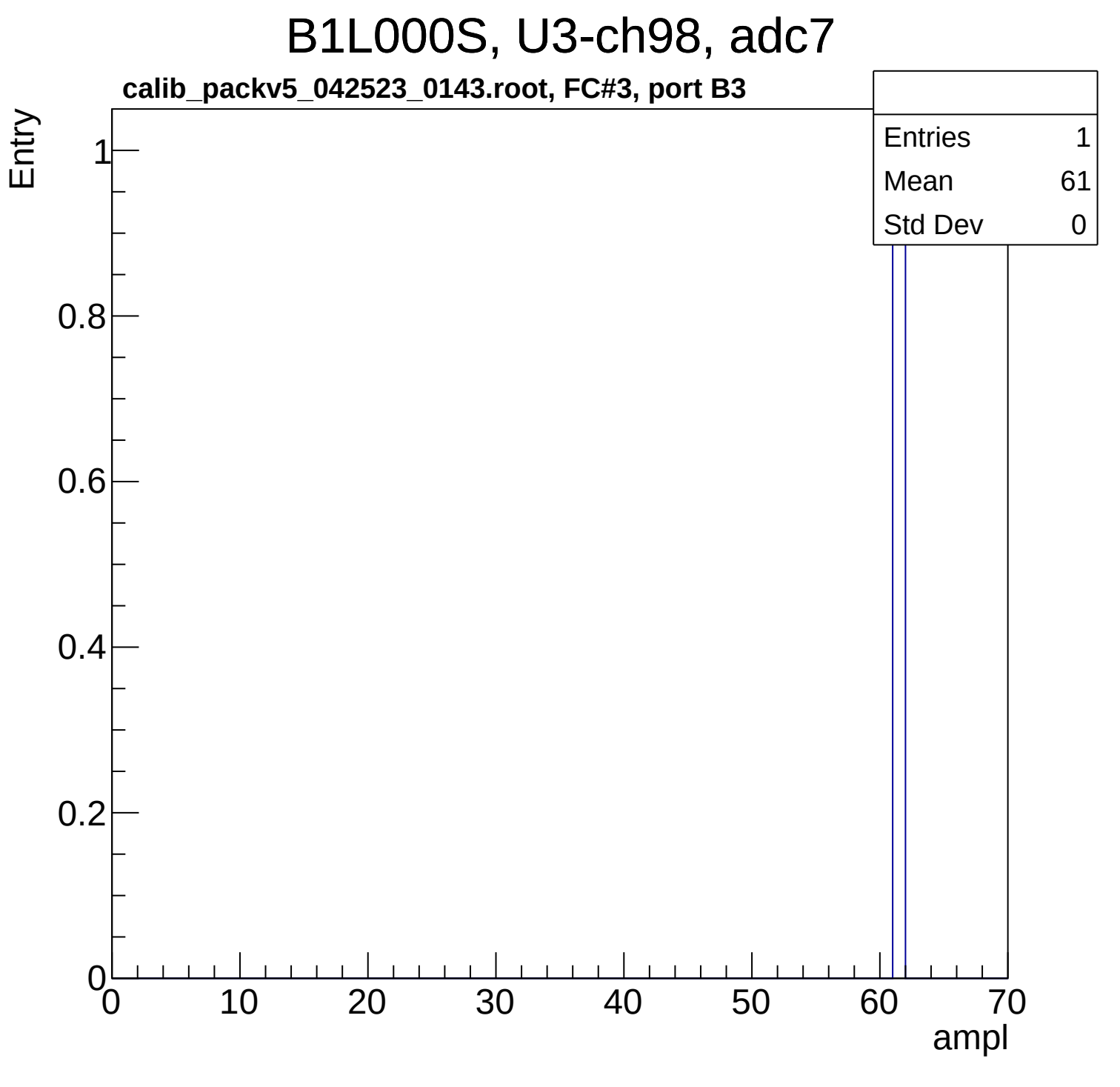
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch98, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 58.7
Std Dev 12.58
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U3-ch98, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 61
Std Dev 0
Entry
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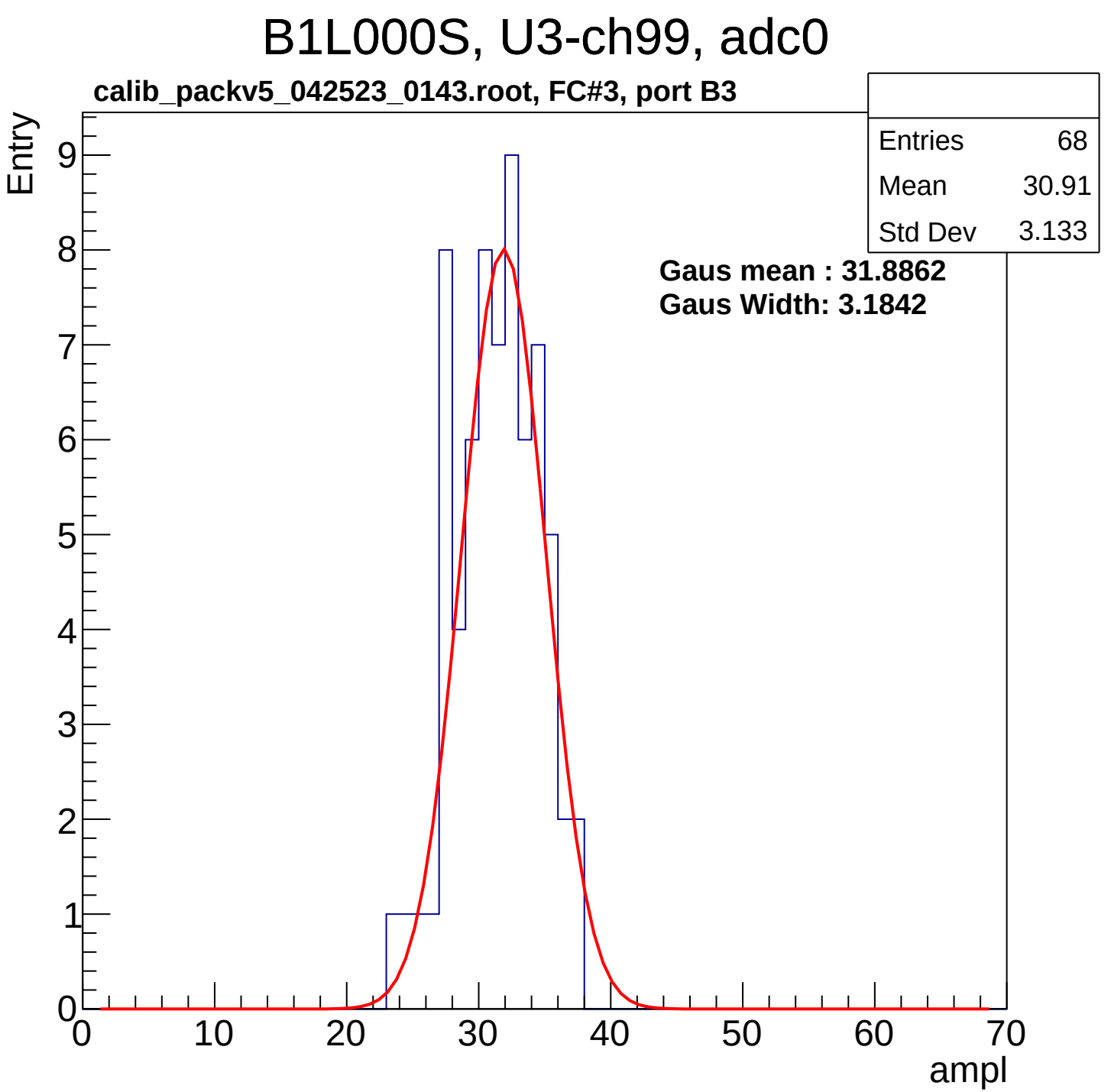
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch99, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 30.91
Std Dev 3.133
Gaus mean : 31.8862
Gaus Width: 3.1842
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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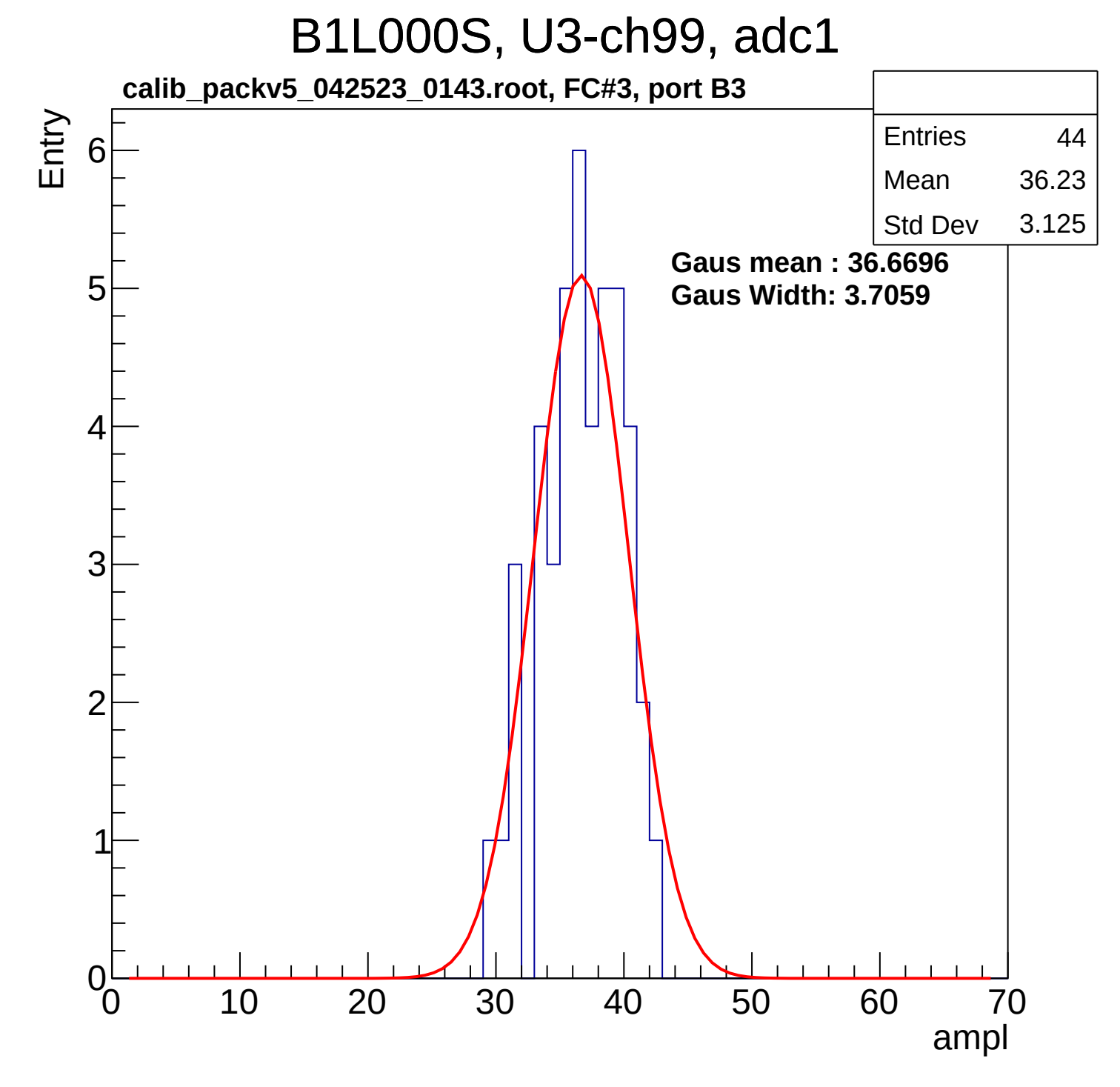
B1L000S, U3-ch99, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 36.23
Std Dev 3.125
Gaus mean : 36.6696
Gaus Width: 3.7059
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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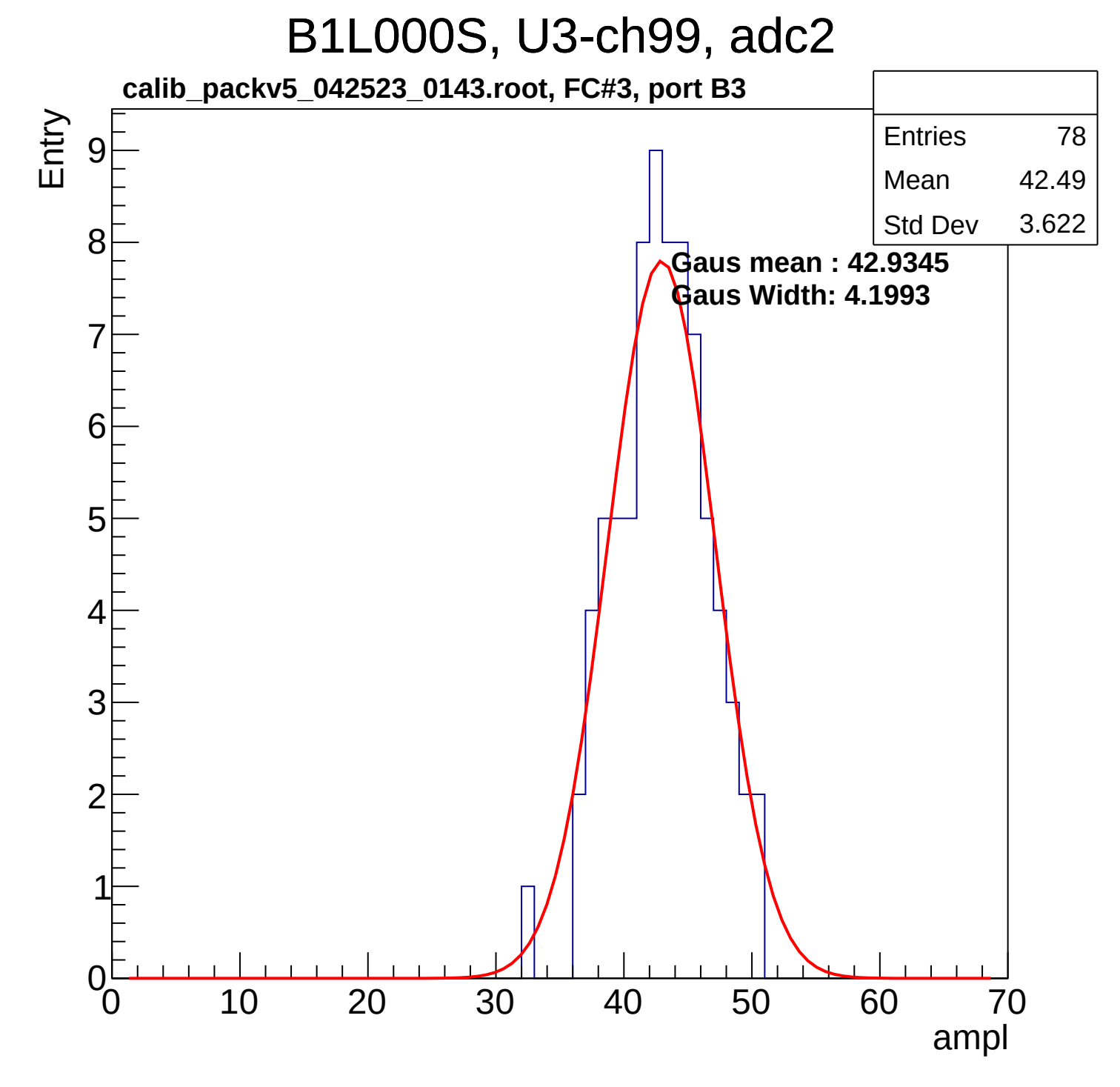
B1L000S, U3-ch99, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 42.49
Std Dev 3.622
Gaus mean : 42.9345
Gaus Width: 4.1993
Entry
ampl
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2
3
4
5
6
7
8
9
0
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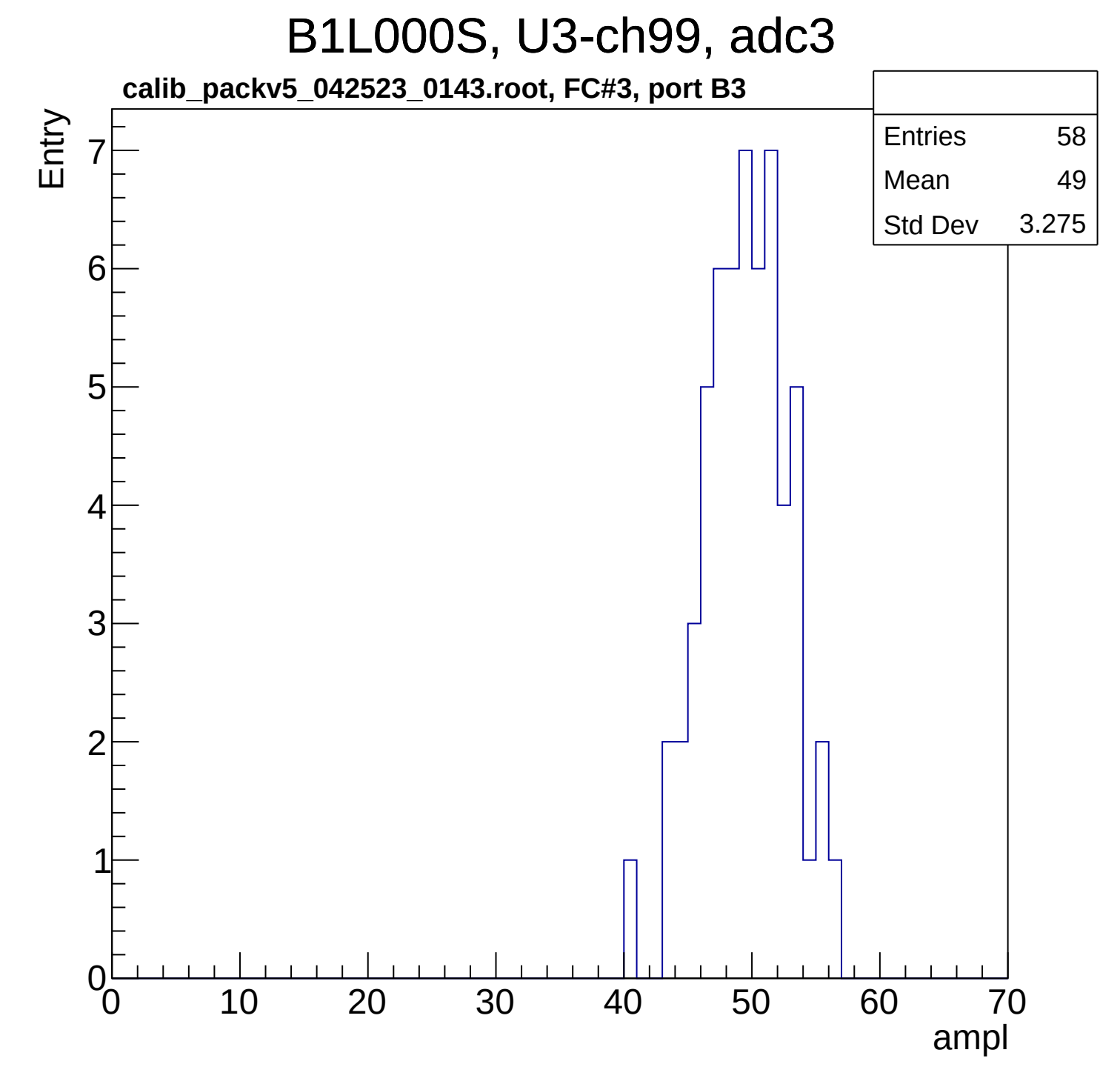
B1L000S, U3-ch99, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 49
Std Dev 3.275
Entry
ampl
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4
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6
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0
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40
50
60
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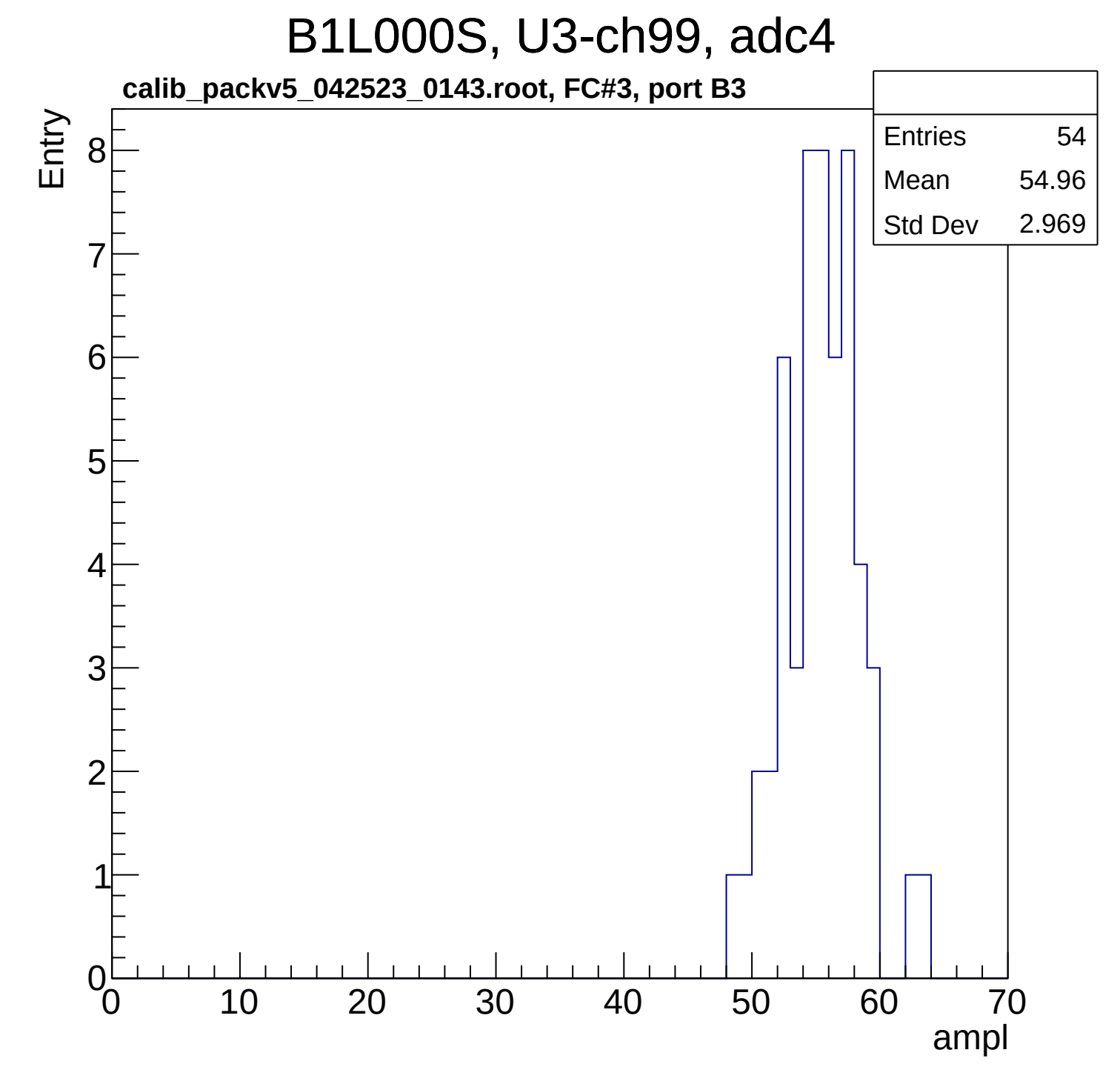
B1L000S, U3-ch99, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.96
Std Dev 2.969
Entry
ampl
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2
3
4
5
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7
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0
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20
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60
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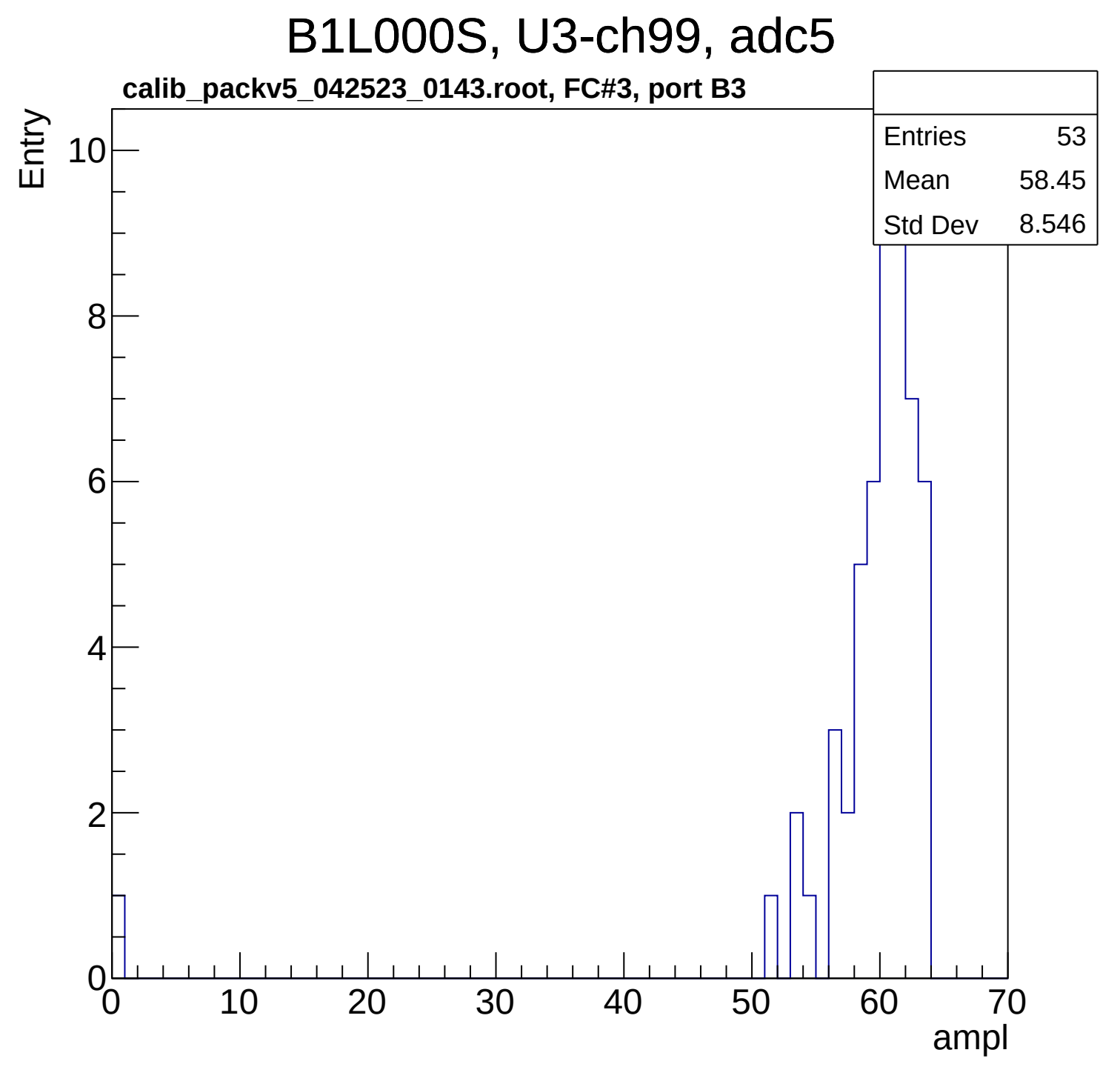
B1L000S, U3-ch99, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.45
Std Dev 8.546
Entry
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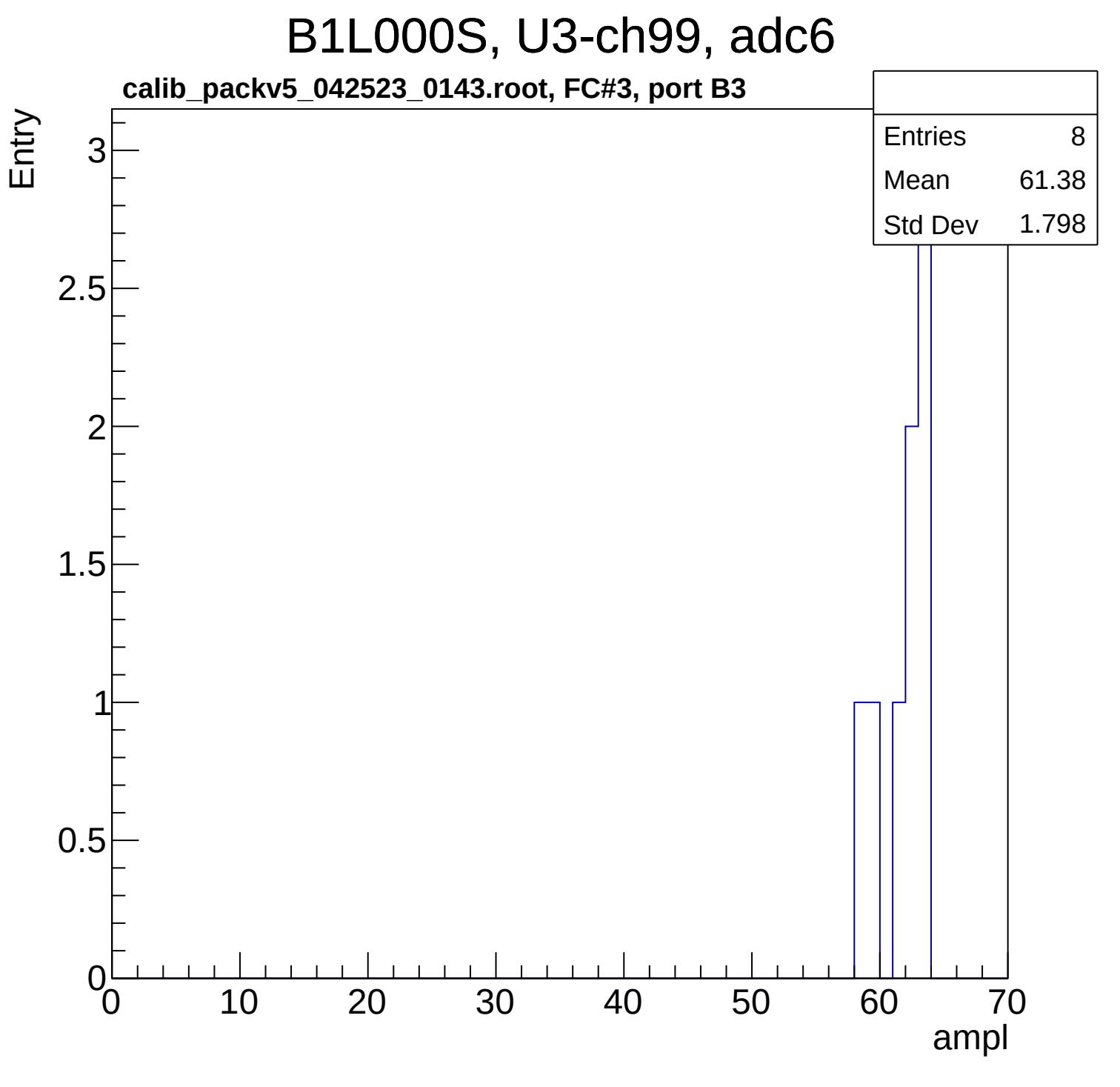
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch99, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.38
Std Dev 1.798
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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20
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60
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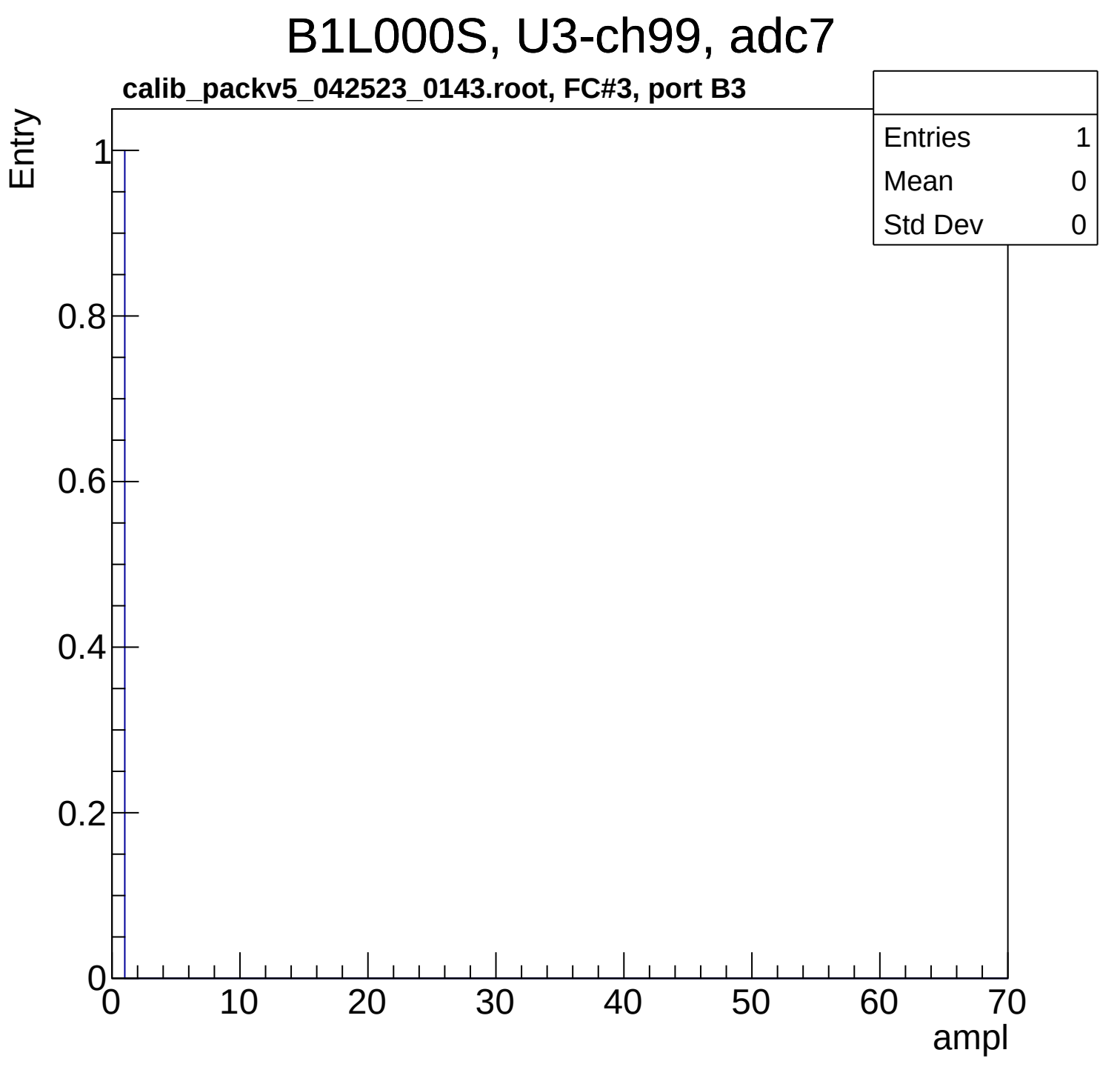
B1L000S, U3-ch99, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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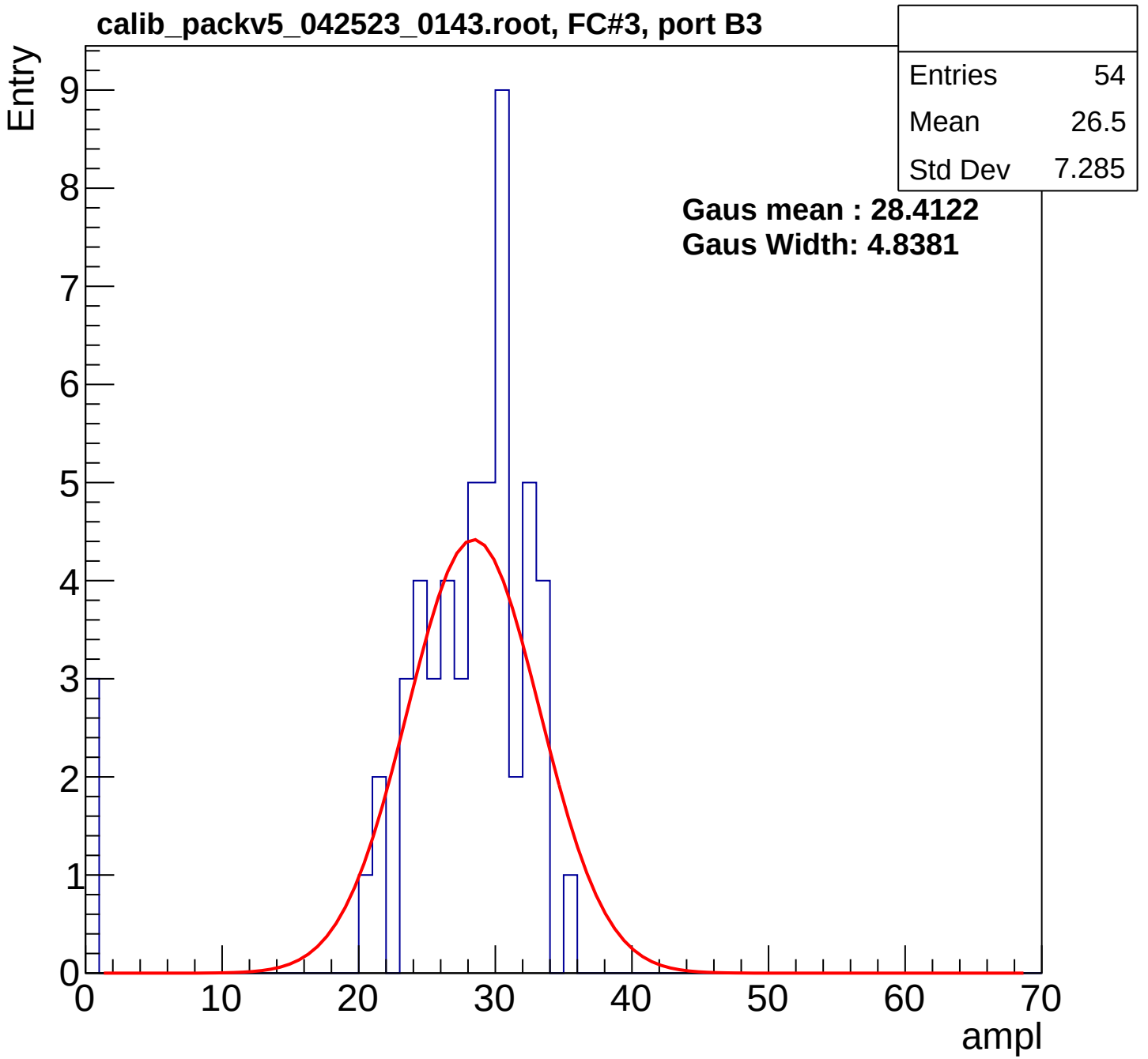
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch100, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 26.5
Std Dev 7.285
Gaus mean : 28.4122
Gaus Width: 4.8381
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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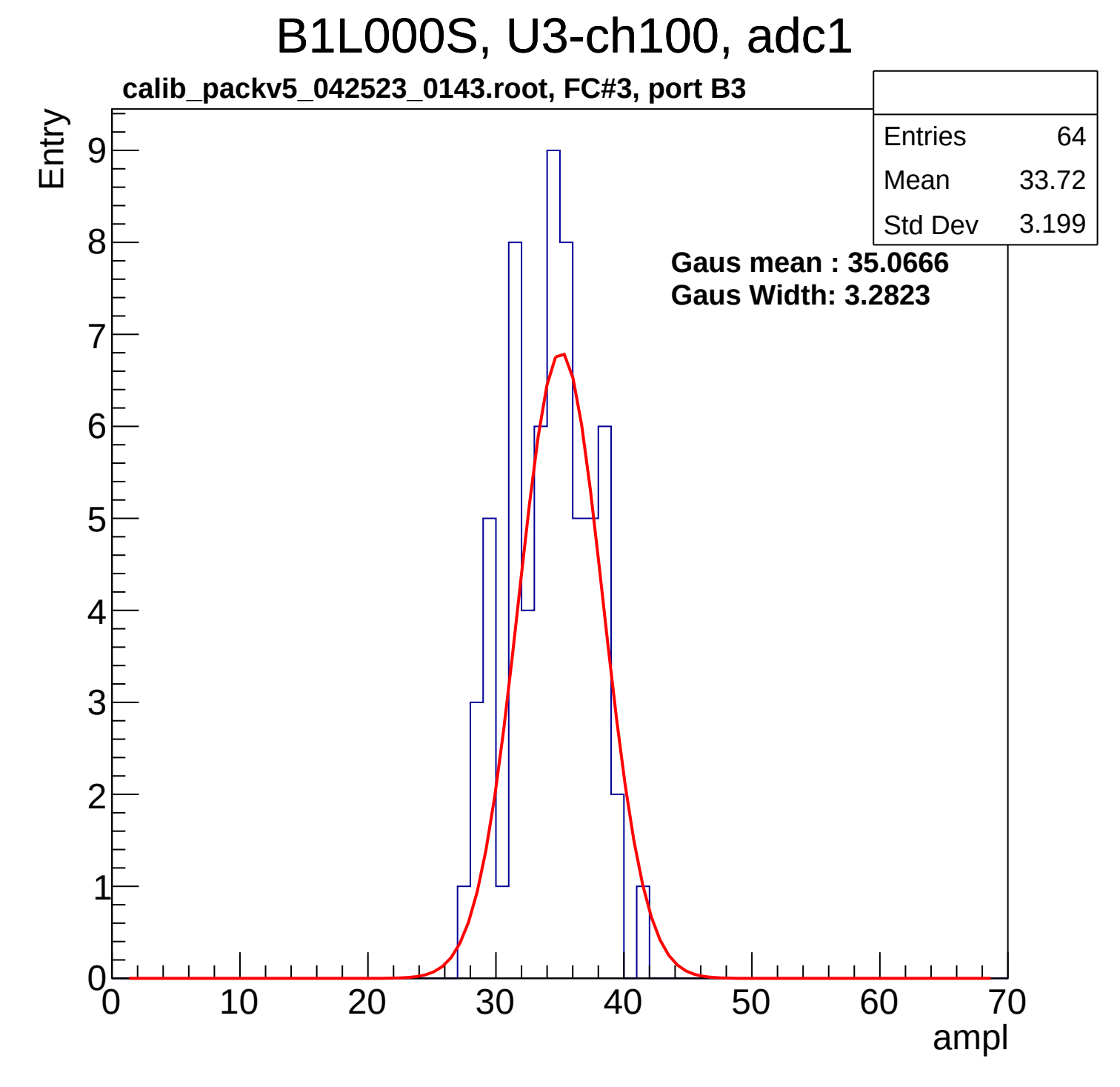
B1L000S, U3-ch100, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 33.72
Std Dev 3.199
Gaus mean : 35.0666
Gaus Width: 3.2823
Entry
ampl
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10
20
30
40
50
60
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1
2
3
4
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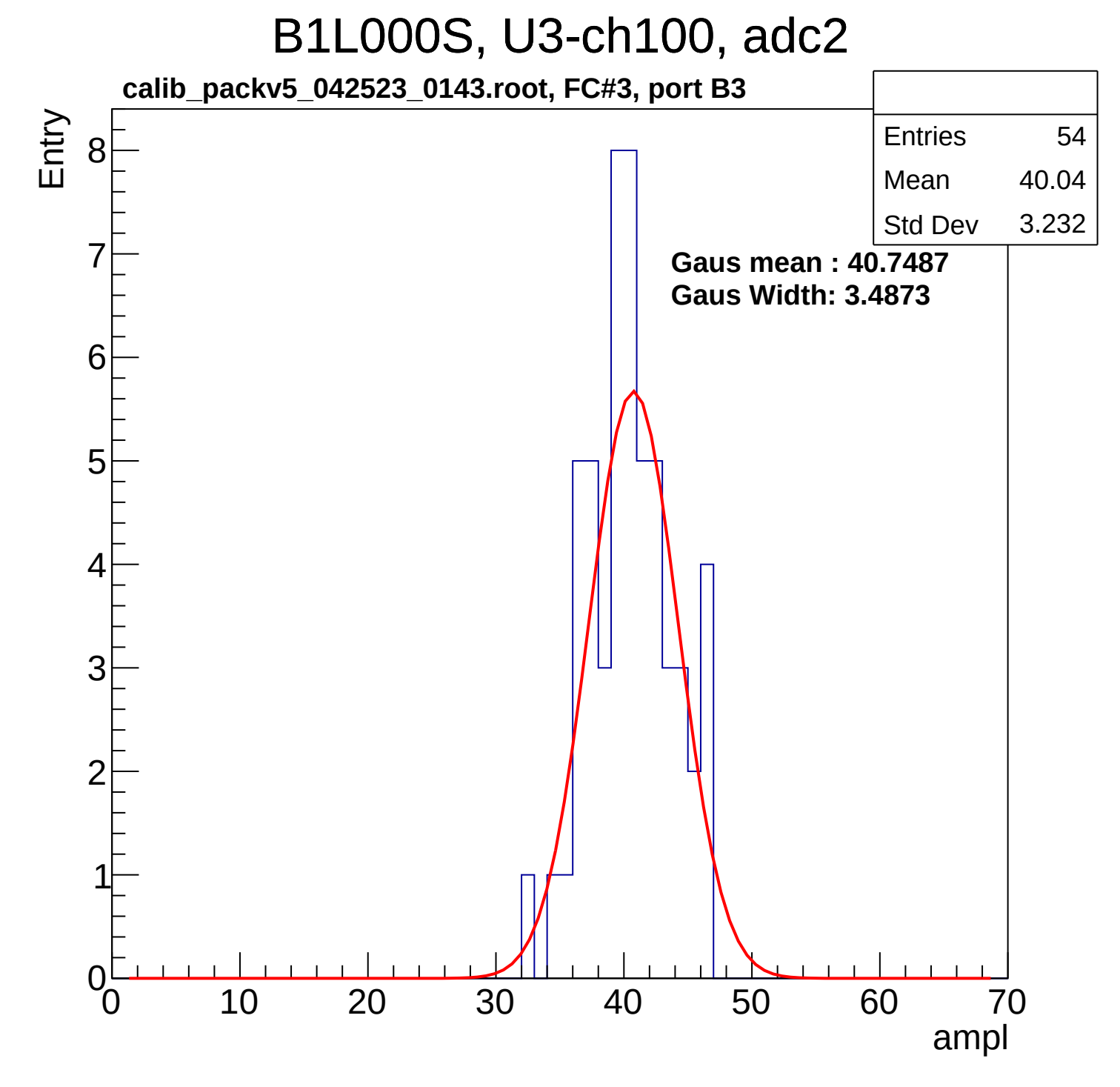
6
7
8
9


B1L000S, U3-ch100, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 40.04
Std Dev 3.232
Gaus mean : 40.7487
Gaus Width: 3.4873
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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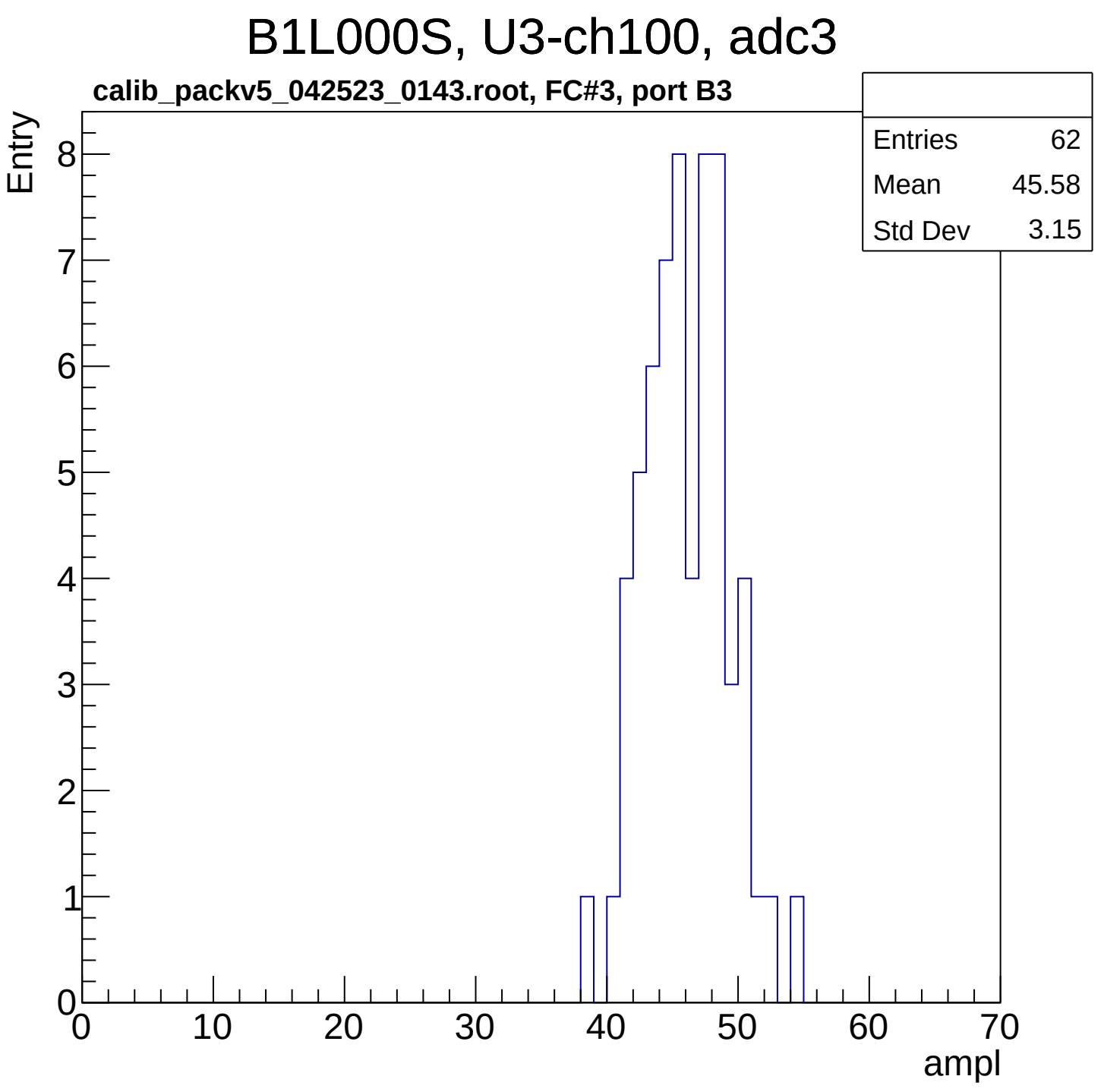
B1L000S, U3-ch100, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 45.58
Std Dev 3.15
Entry
ampl
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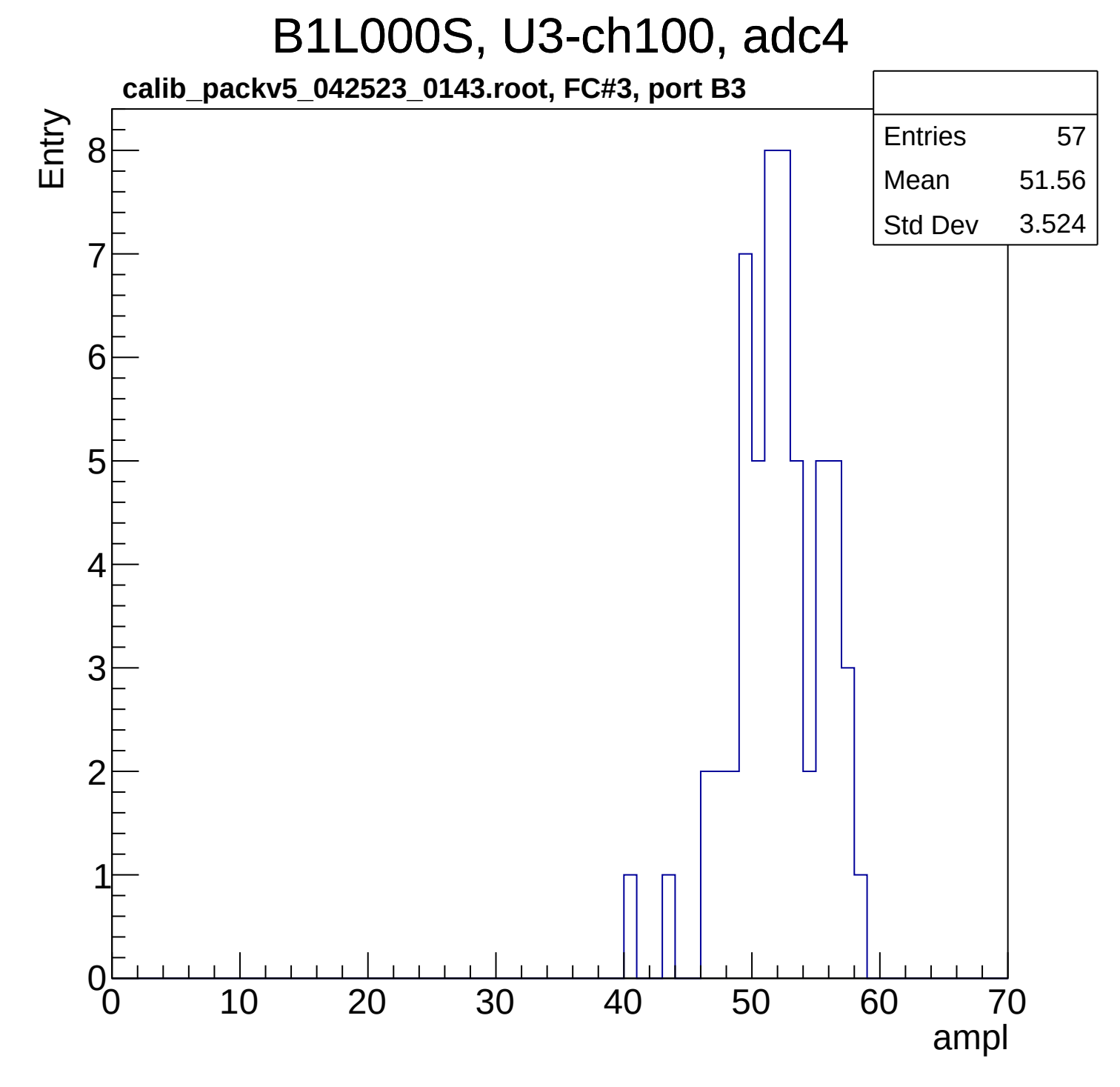
B1L000S, U3-ch100, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 51.56
Std Dev 3.524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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50
60
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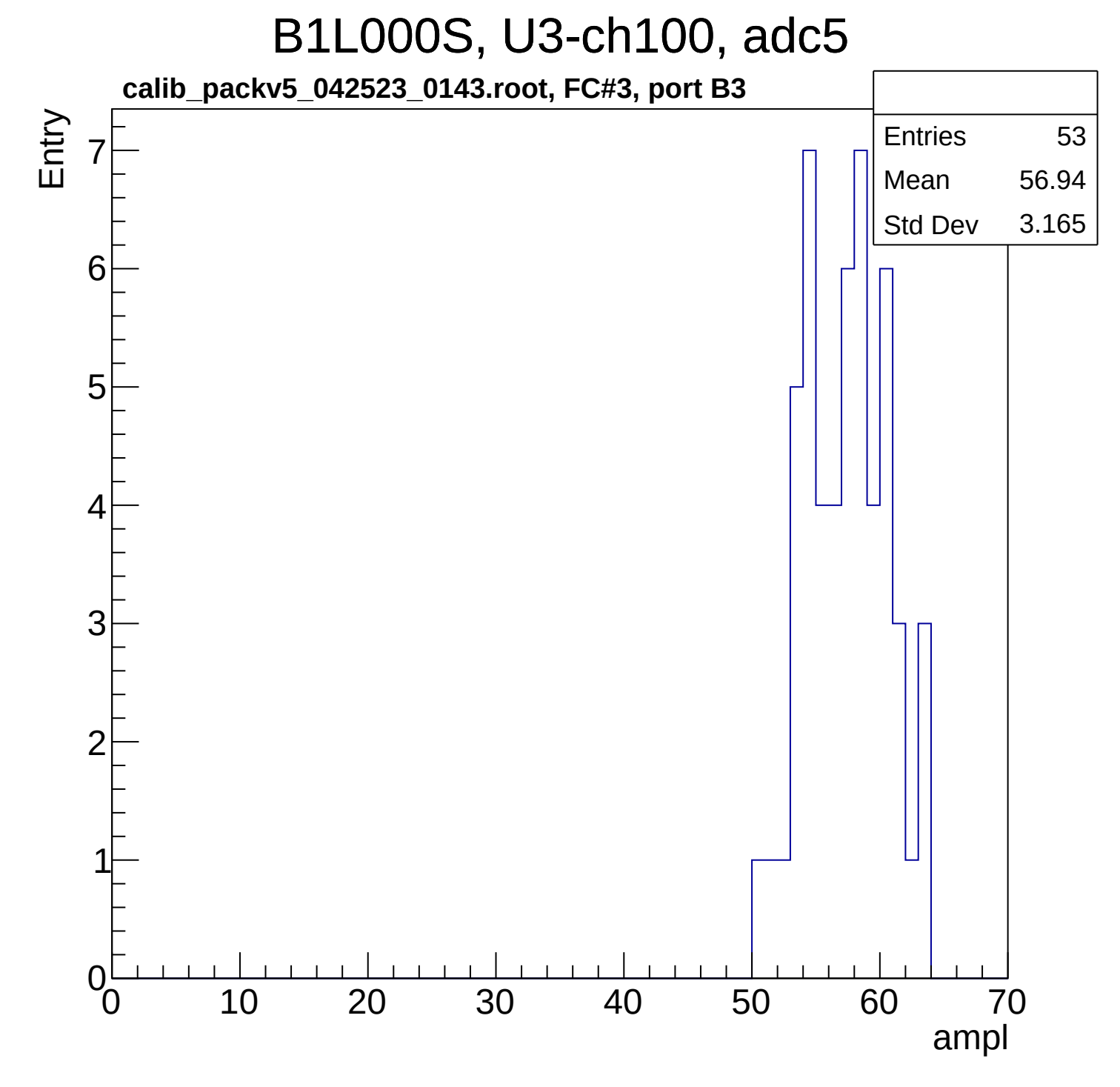
B1L000S, U3-ch100, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 56.94
Std Dev 3.165
Entry
ampl
0
1
2
3
4
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6
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0
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60
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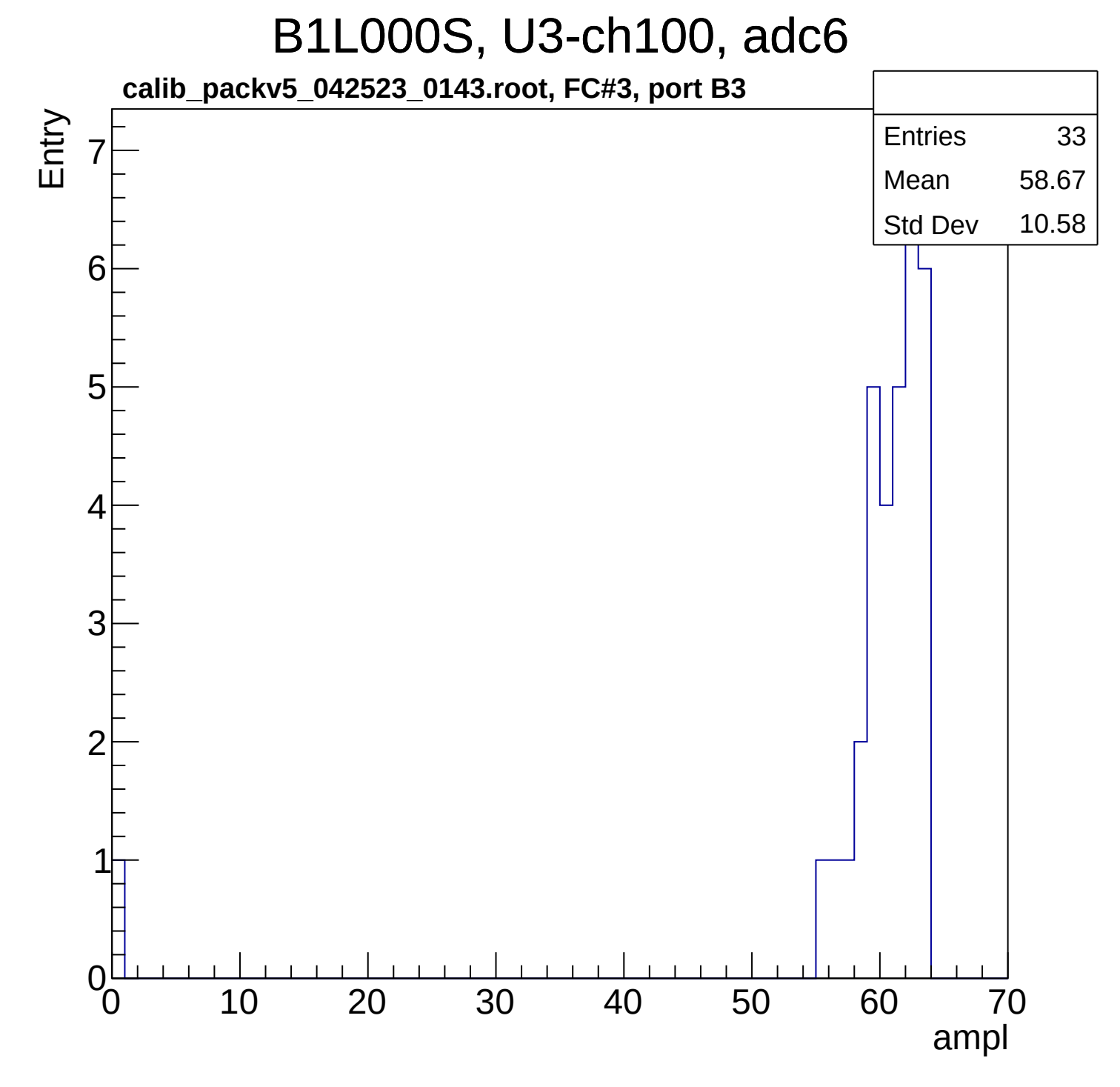
B1L000S, U3-ch100, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 33
Mean 58.67
Std Dev 10.58
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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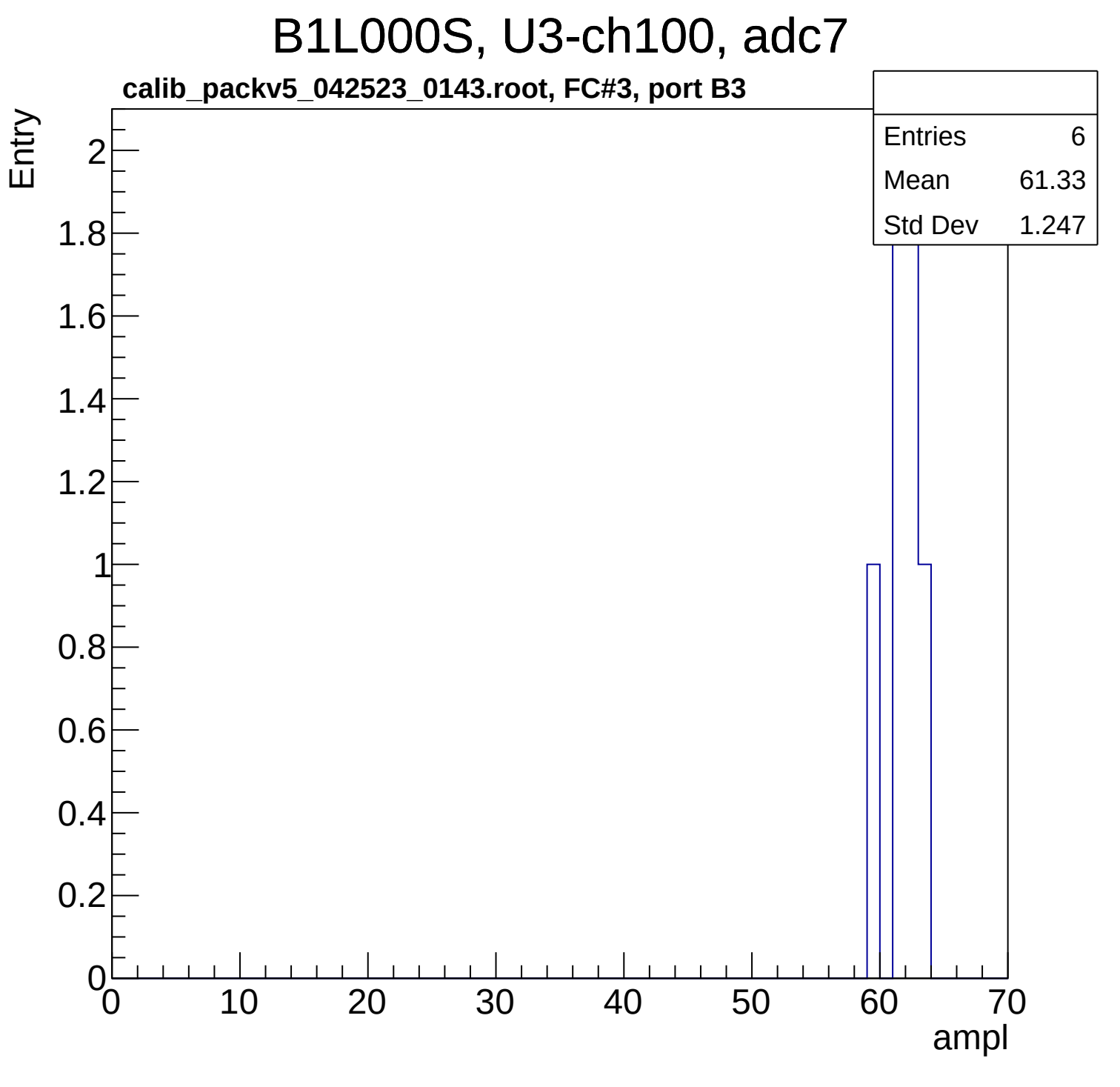
B1L000S, U3-ch100, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 61.33
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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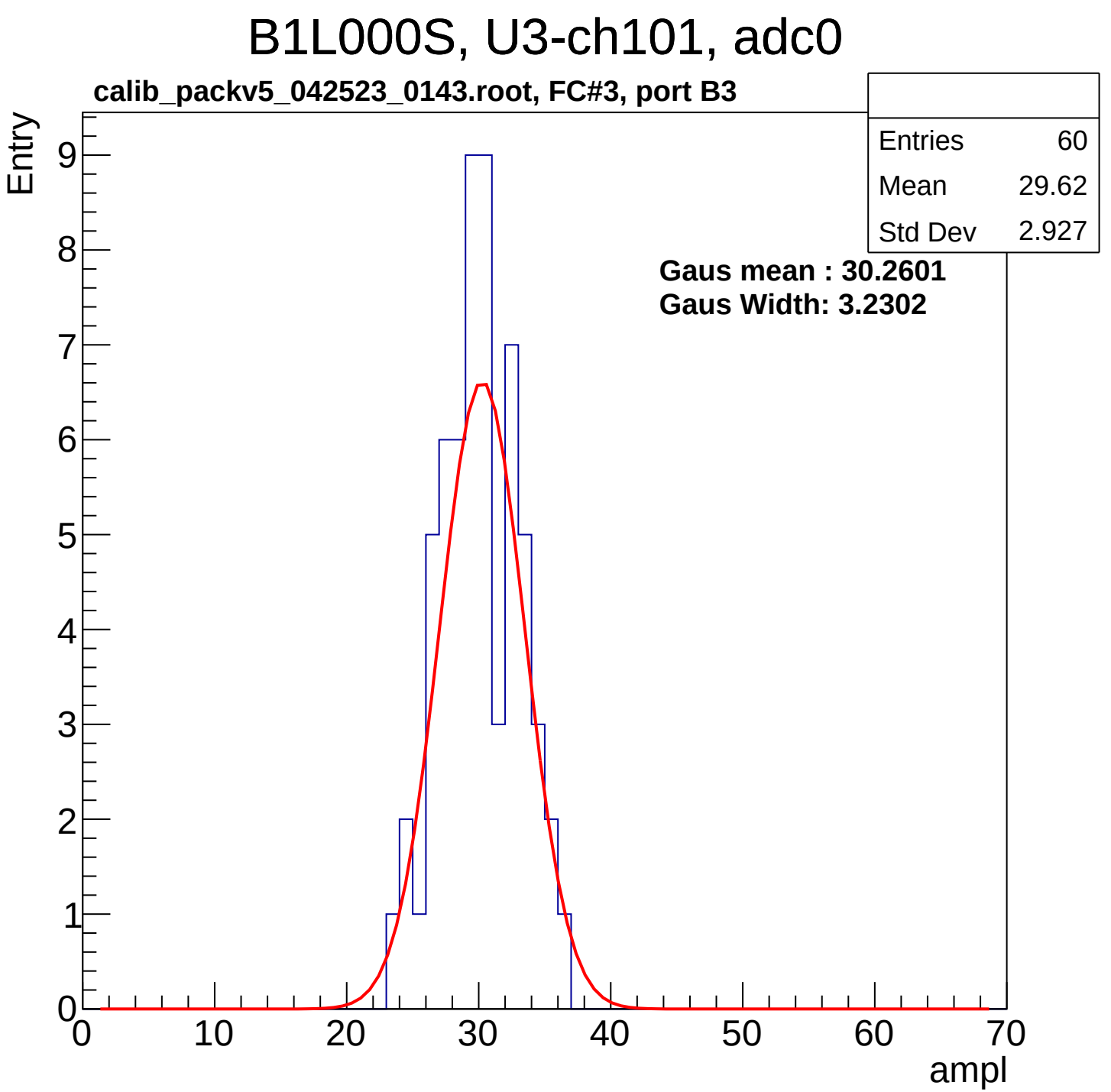
B1L000S, U3-ch101, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 29.62
Std Dev 2.927
Gaus mean : 30.2601
Gaus Width: 3.2302
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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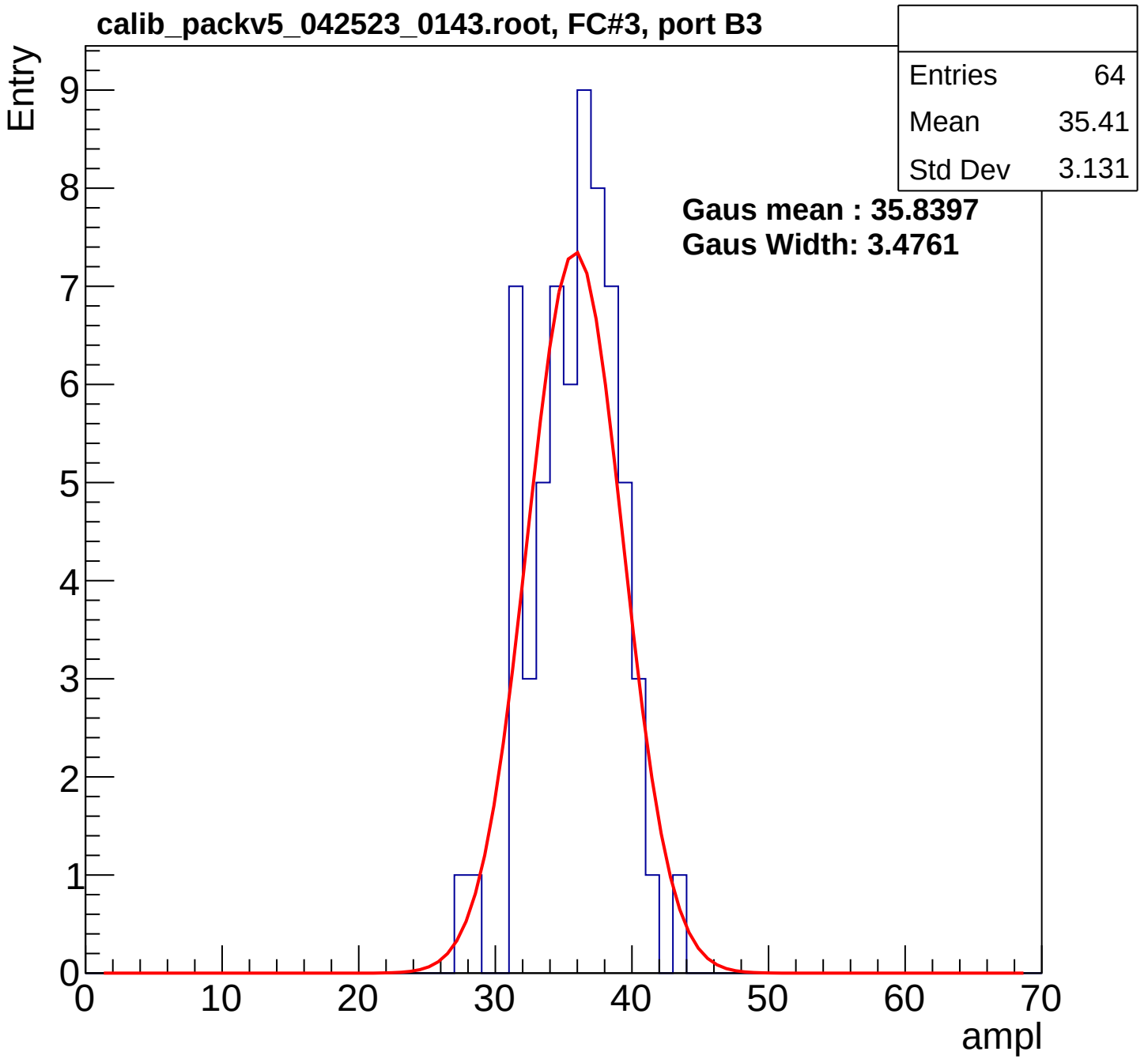
B1L000S, U3-ch101, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 35.41
Std Dev 3.131
Gaus mean : 35.8397
Gaus Width: 3.4761
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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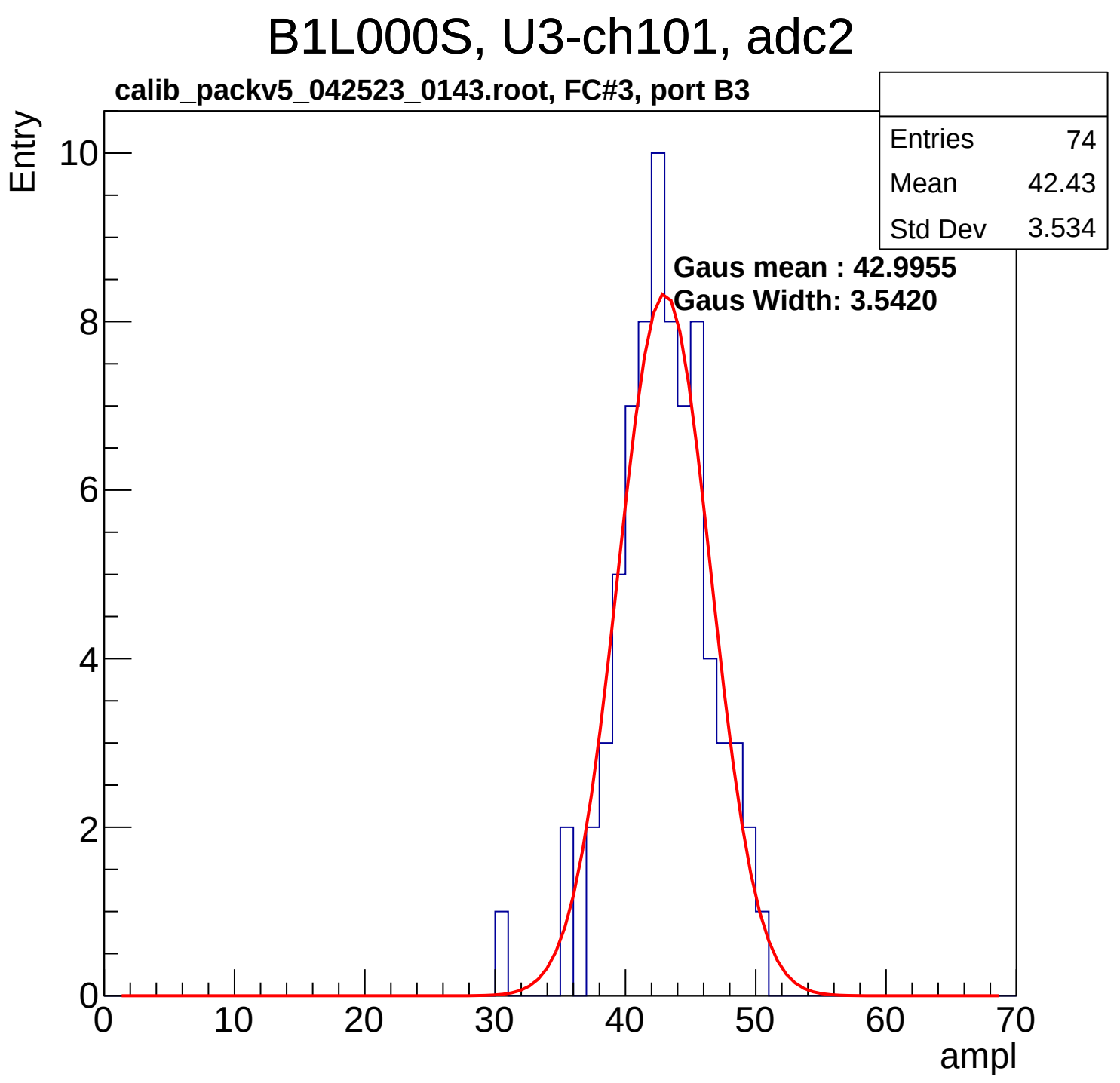
B1L000S, U3-ch101, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 42.43
Std Dev 3.534
Gaus mean : 42.9955
Gaus Width: 3.5420
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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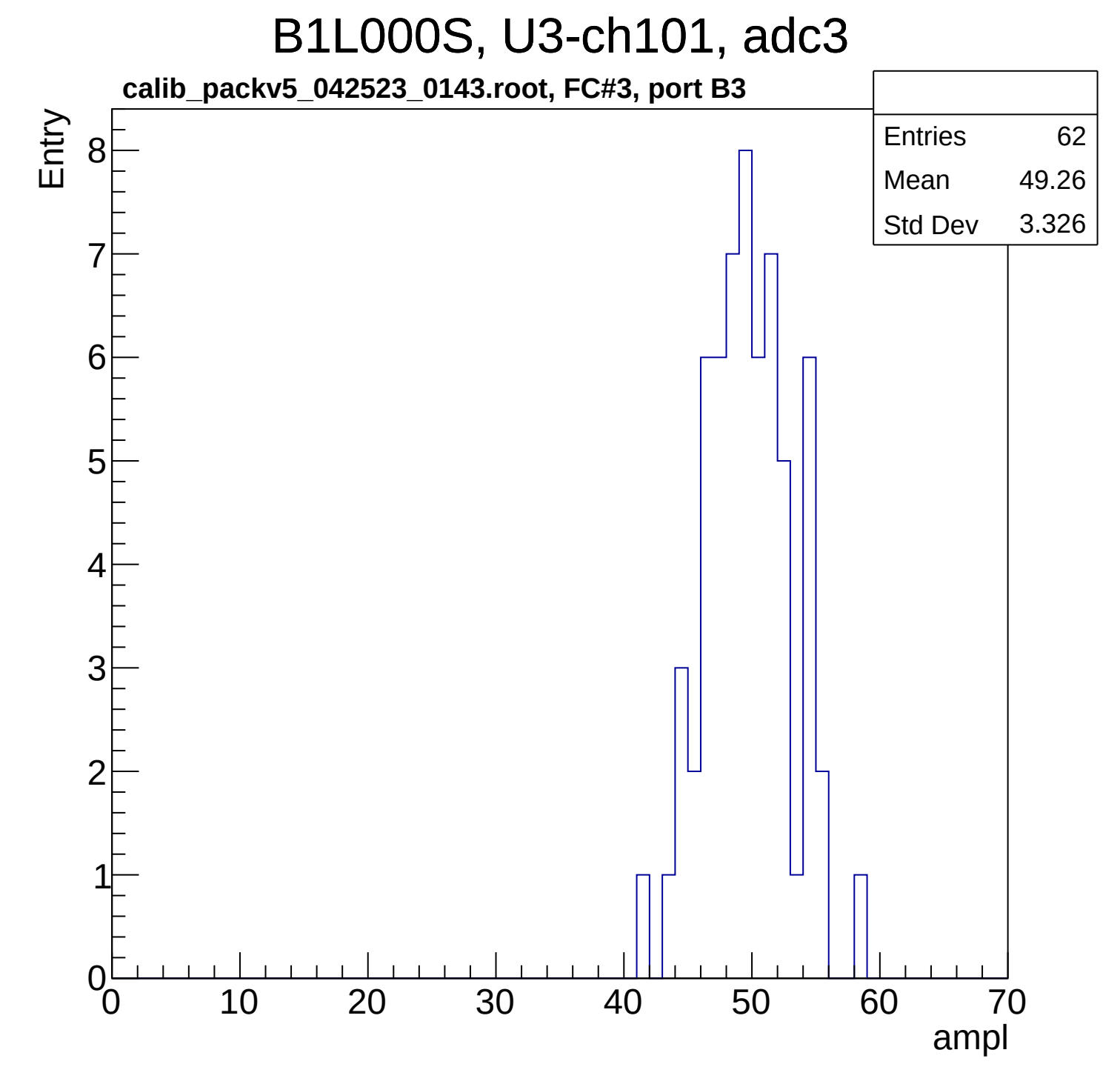
B1L000S, U3-ch101, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 49.26
Std Dev 3.326
Entry
ampl
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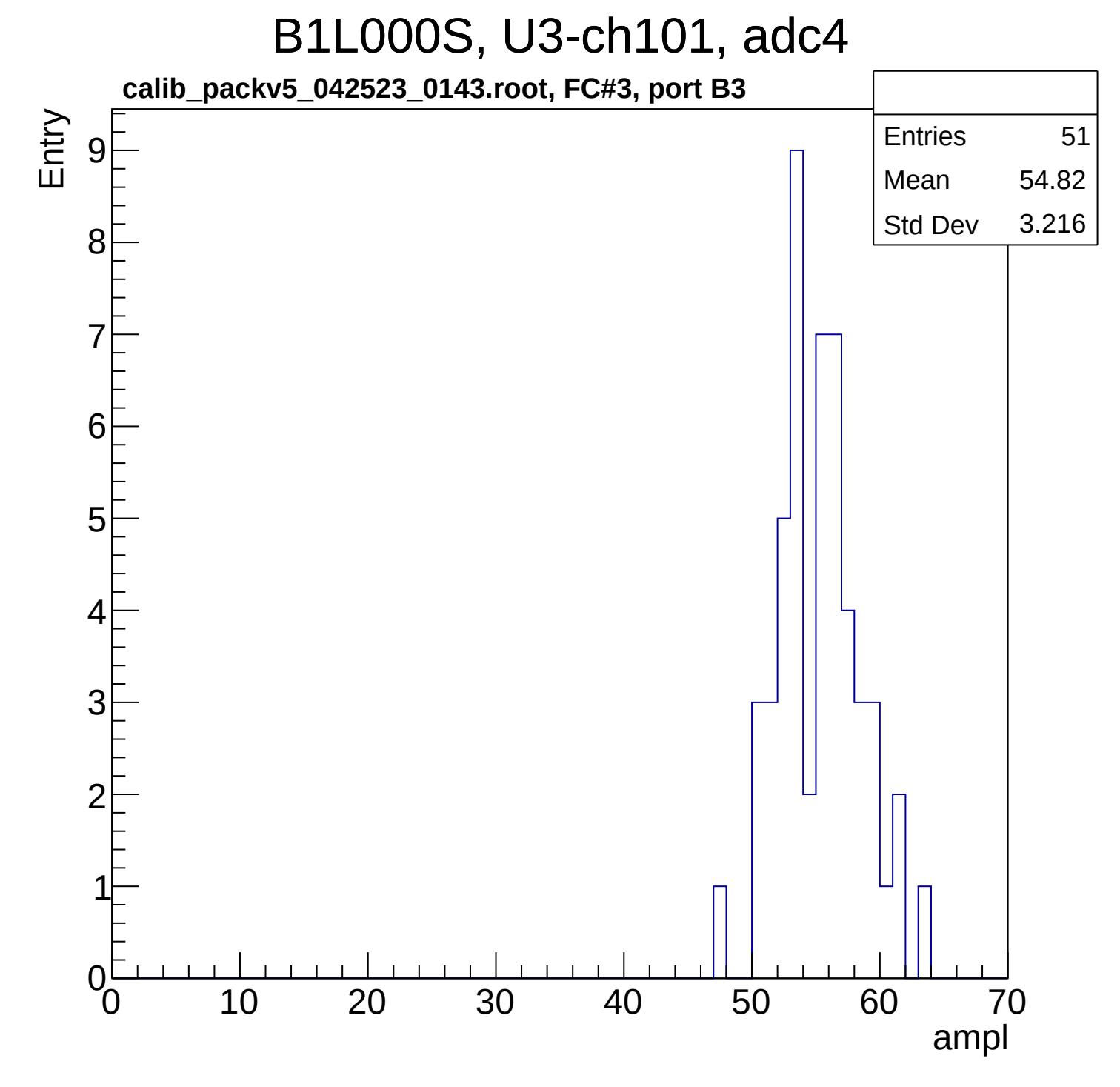
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch101, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.82
Std Dev 3.216
Entry
ampl
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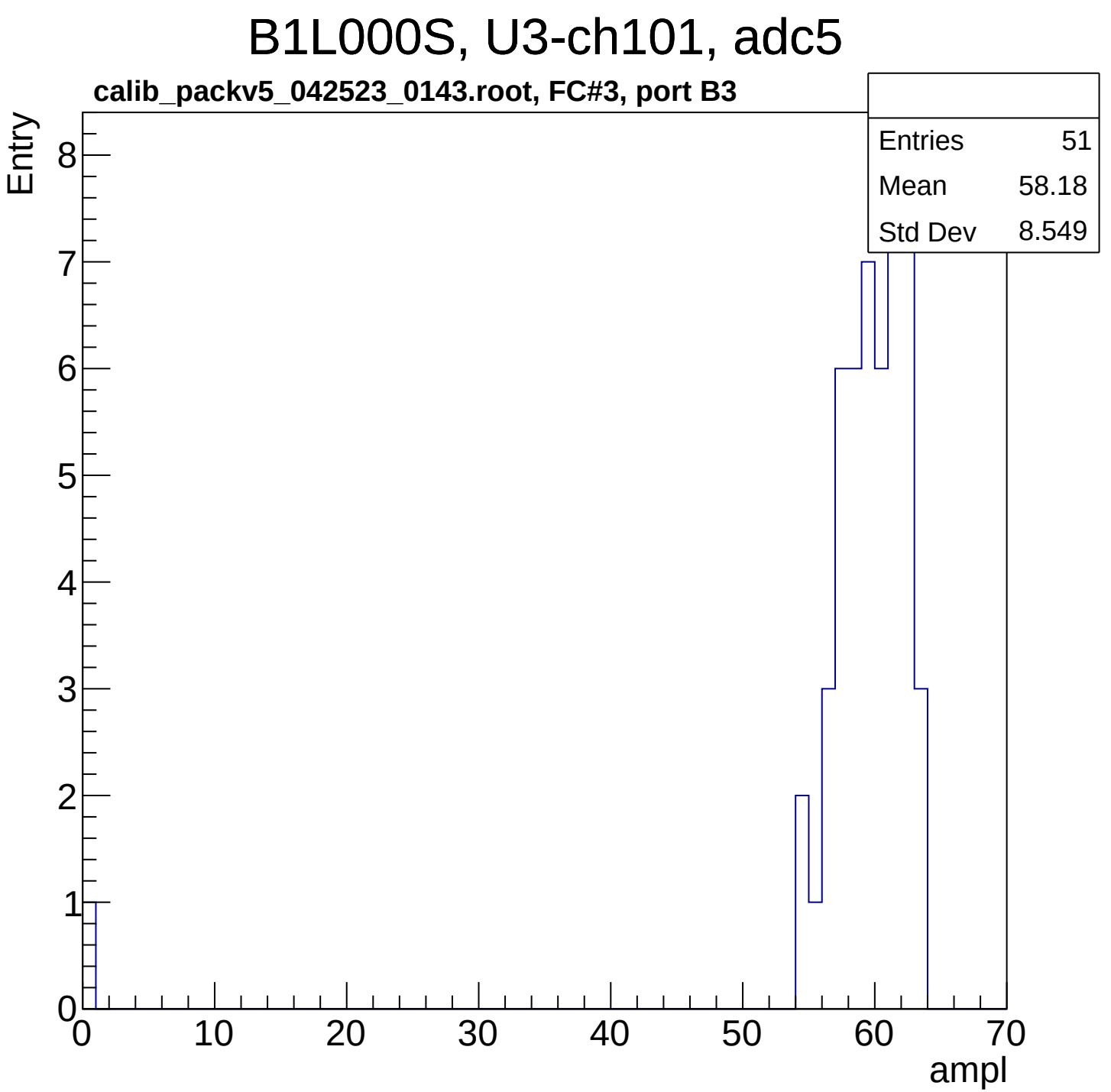
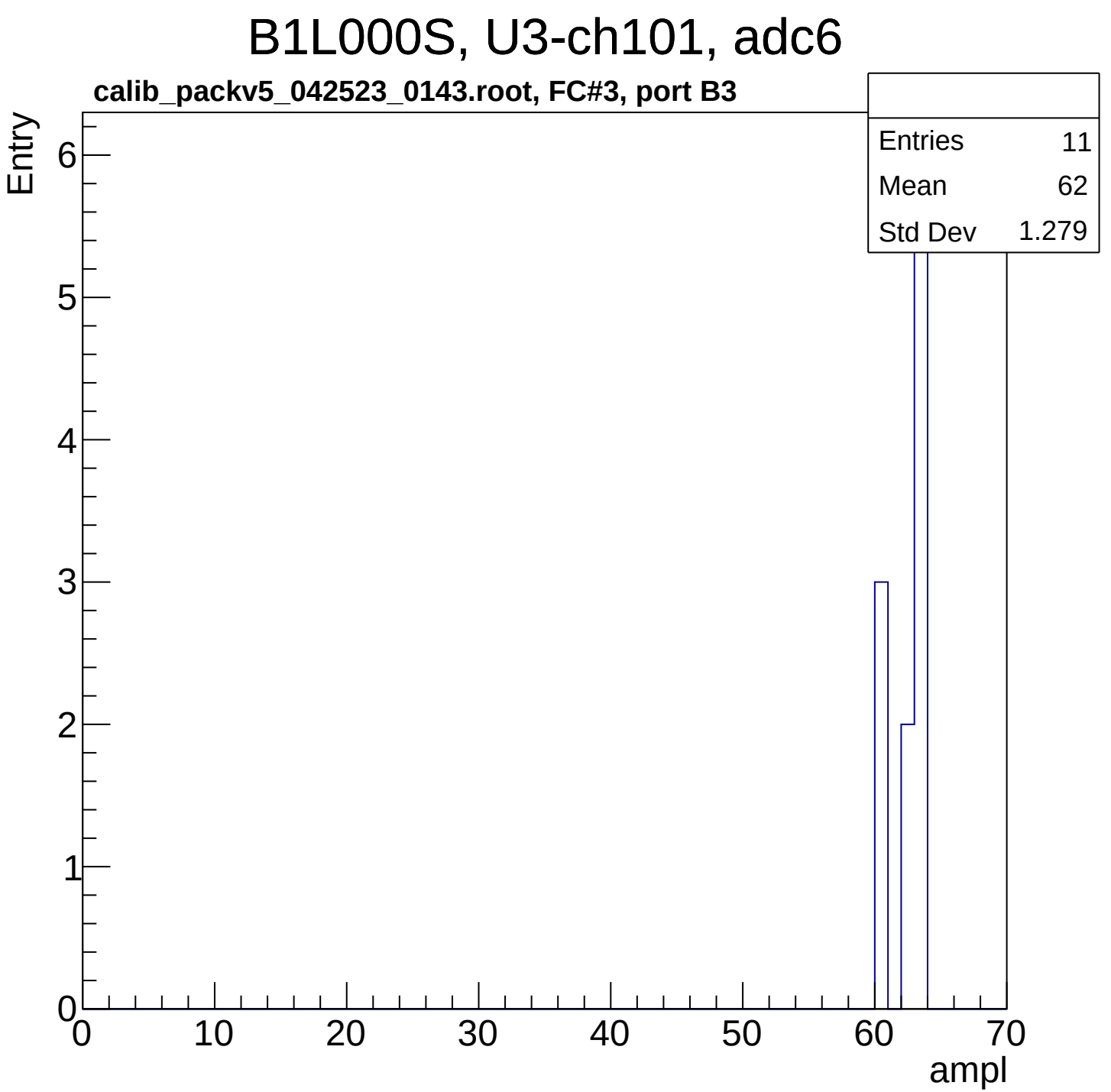
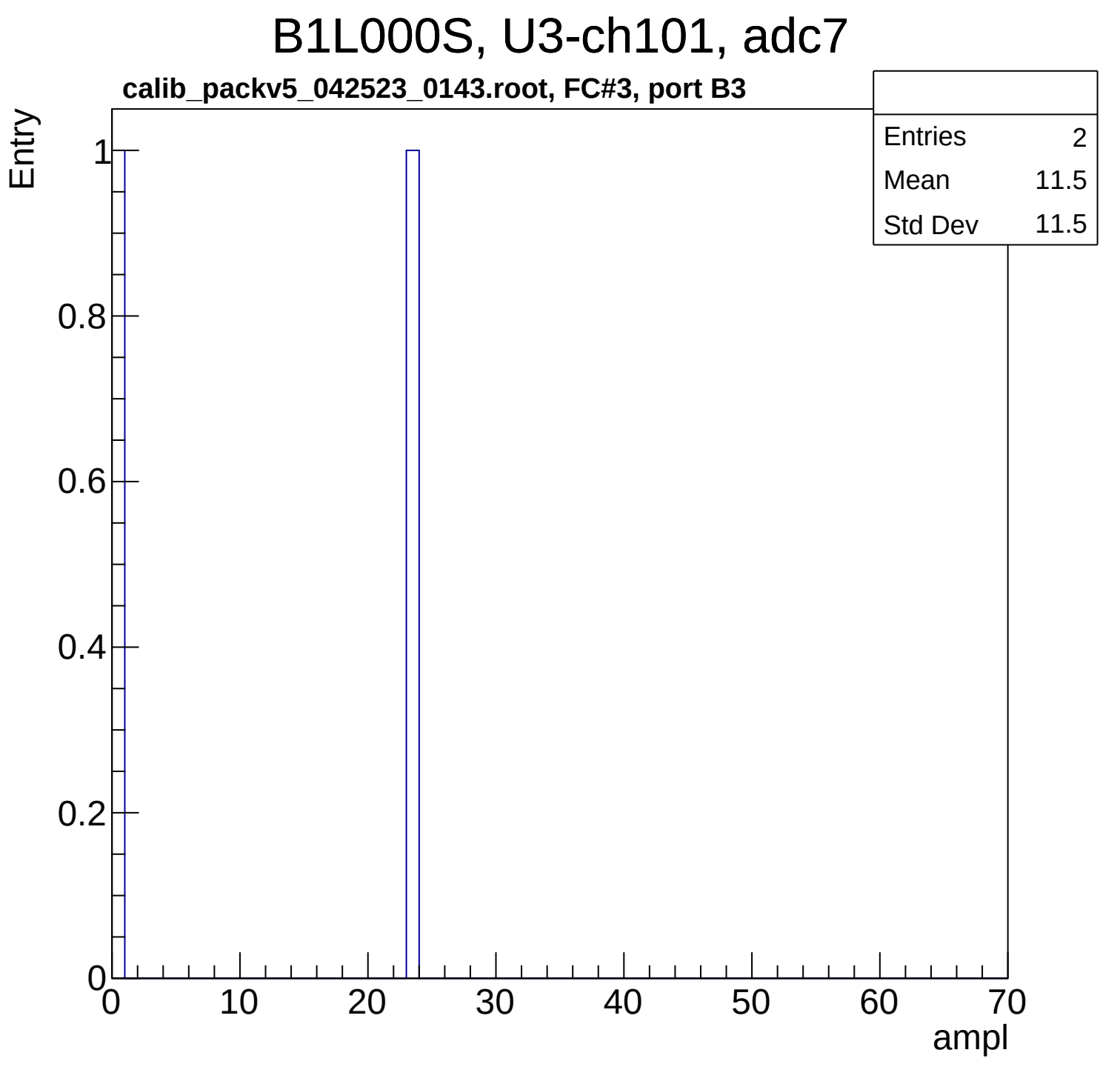
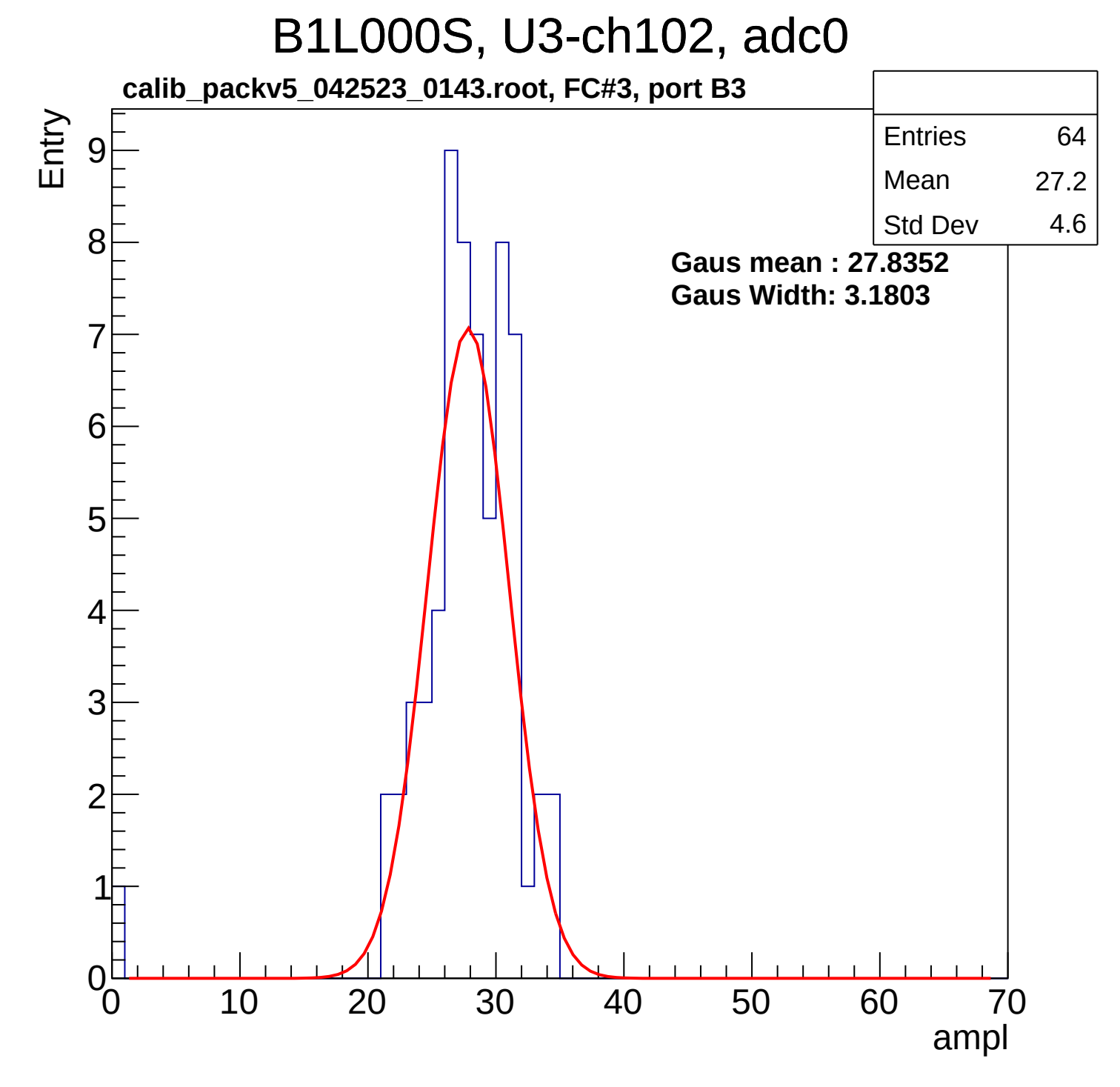
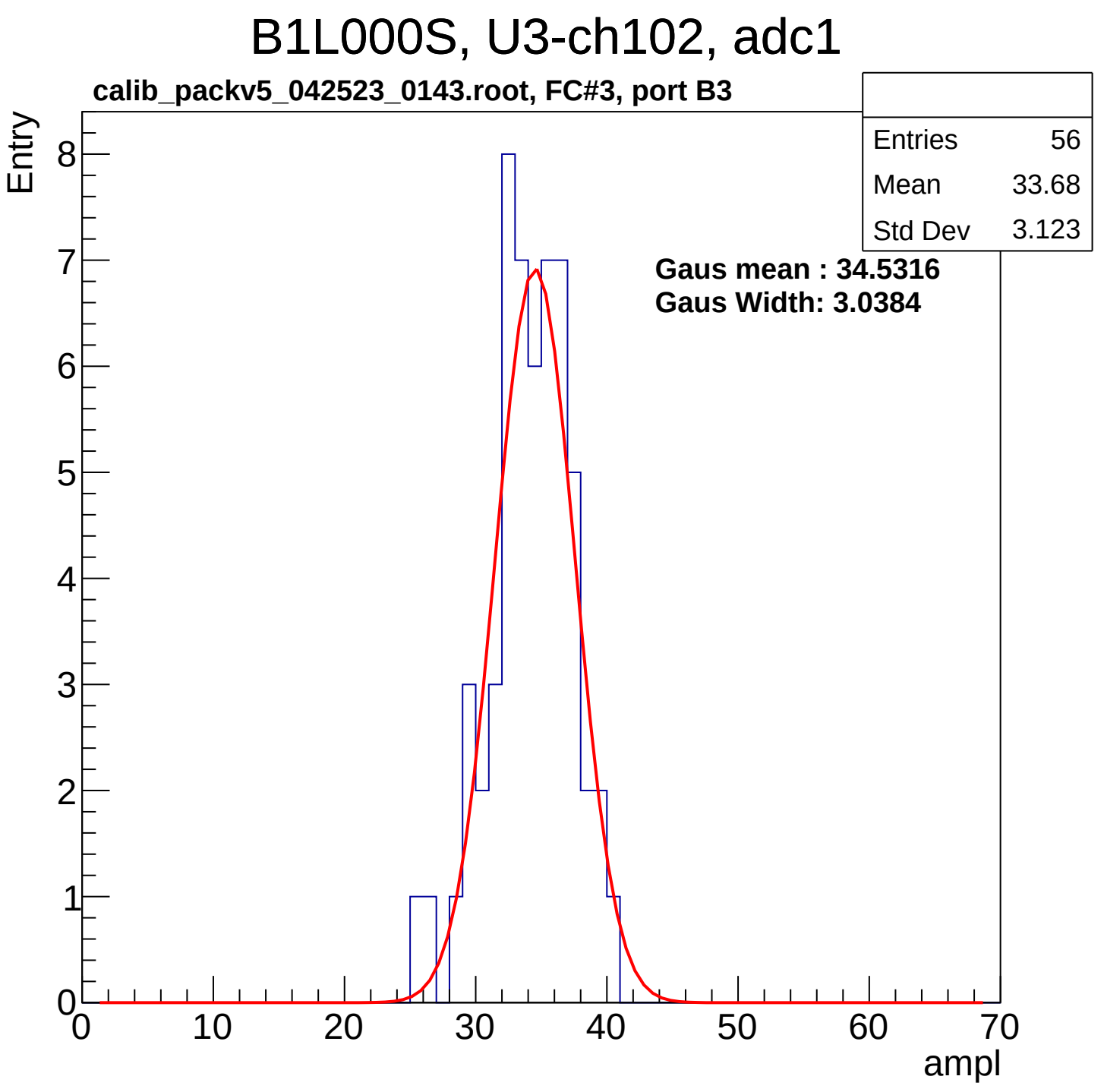
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch101, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 58.18
Std Dev 8.549
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch101, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 62
Std Dev 1.279
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch101, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch102, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 27.2
Std Dev 4.6
Gaus mean : 27.8352
Gaus Width: 3.1803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch102, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 33.68
Std Dev 3.123
Gaus mean : 34.5316
Gaus Width: 3.0384
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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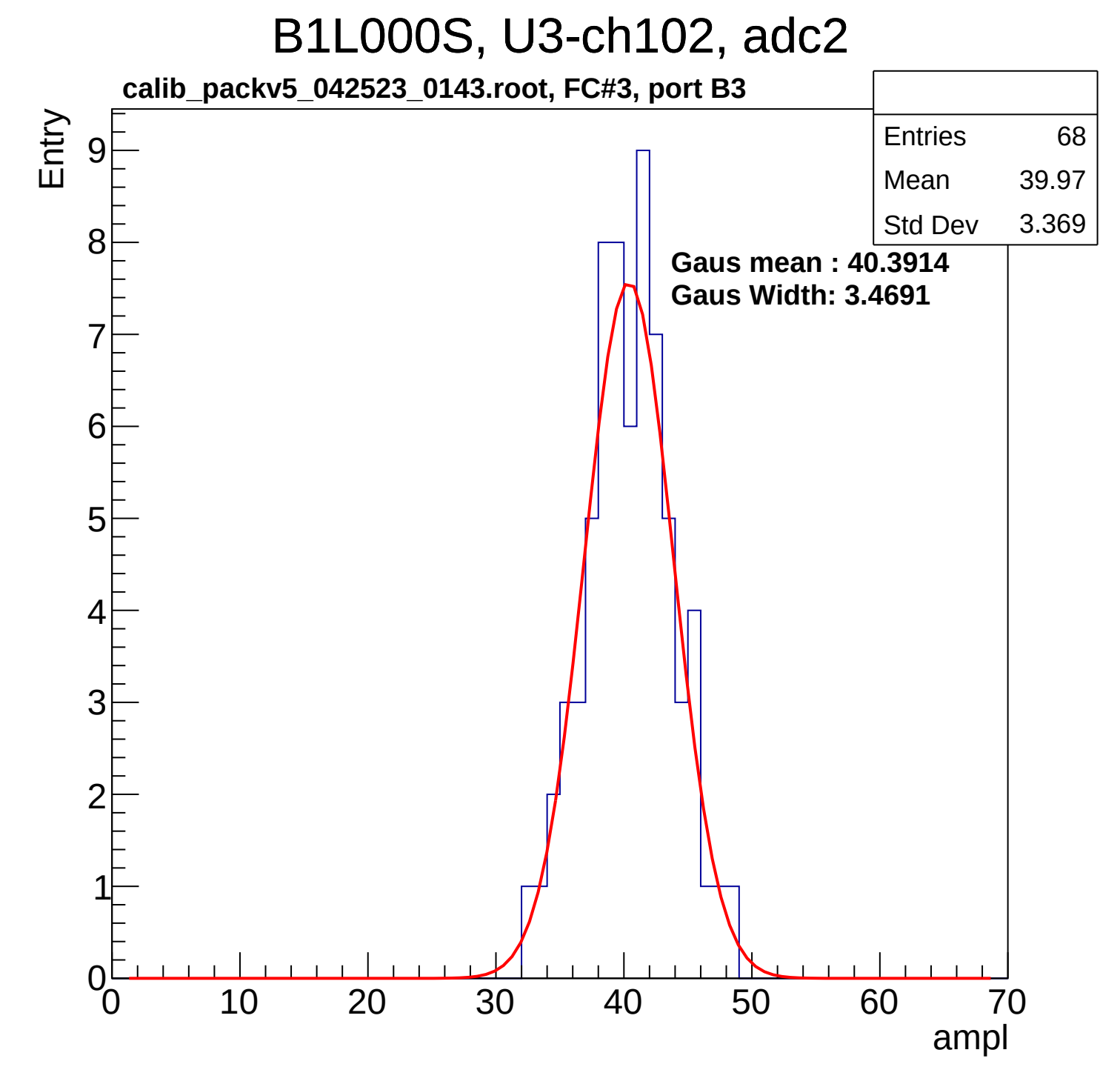
B1L000S, U3-ch102, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 39.97
Std Dev 3.369
Gaus mean : 40.3914
Gaus Width: 3.4691
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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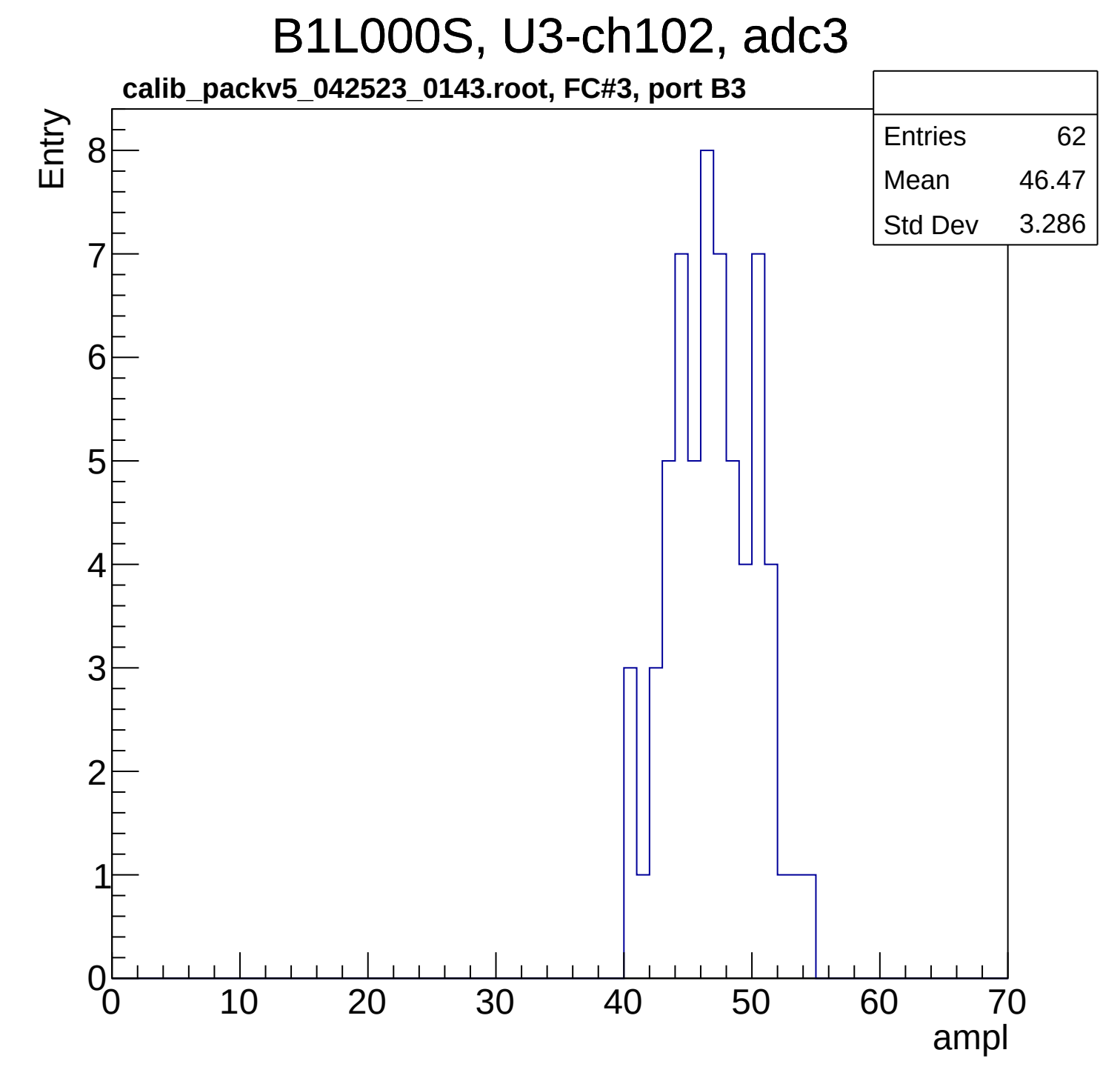
B1L000S, U3-ch102, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 46.47
Std Dev 3.286
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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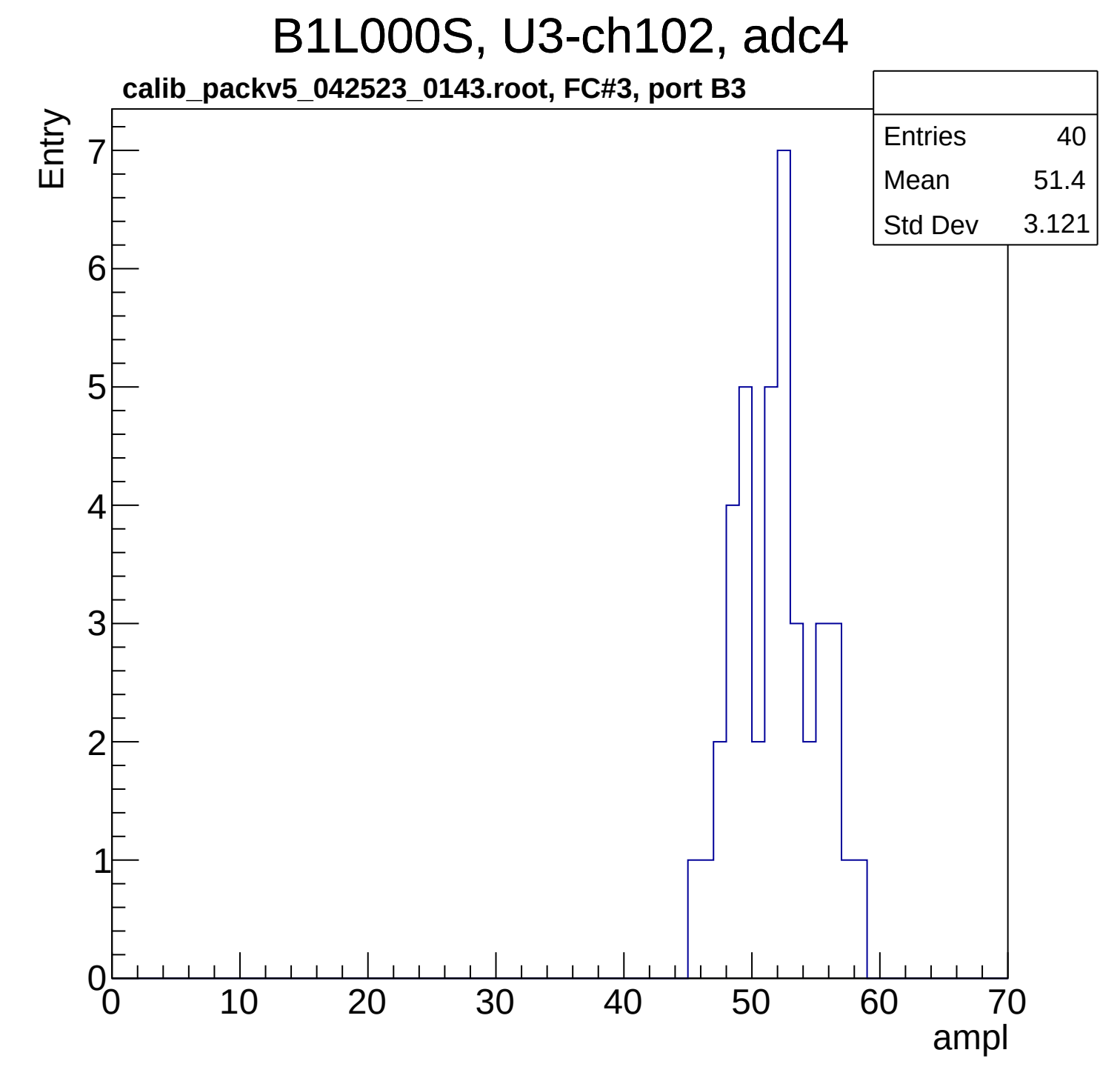
40
50
60
70


B1L000S, U3-ch102, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 51.4
Std Dev 3.121
Entry
ampl
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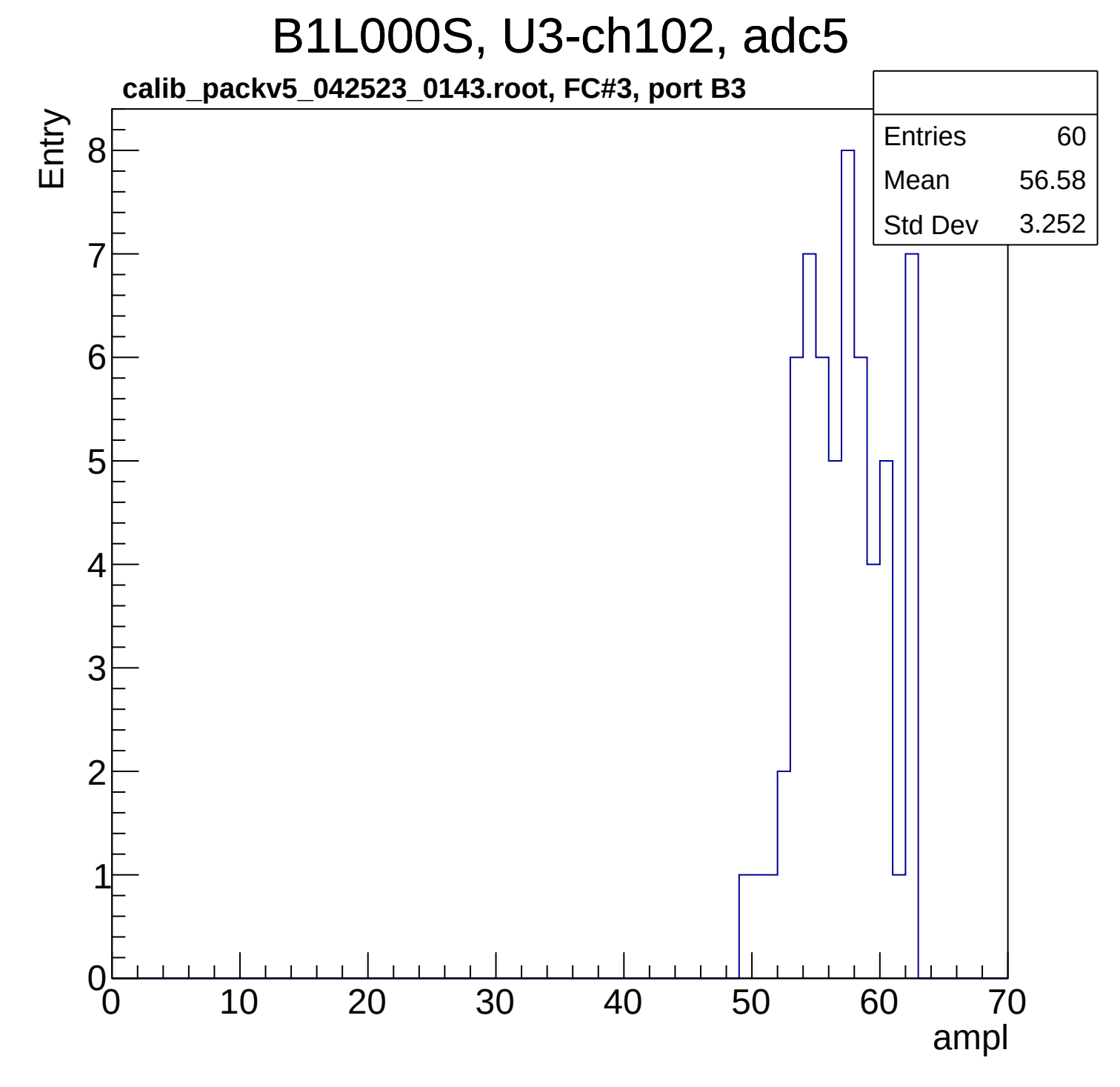
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch102, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 56.58
Std Dev 3.252
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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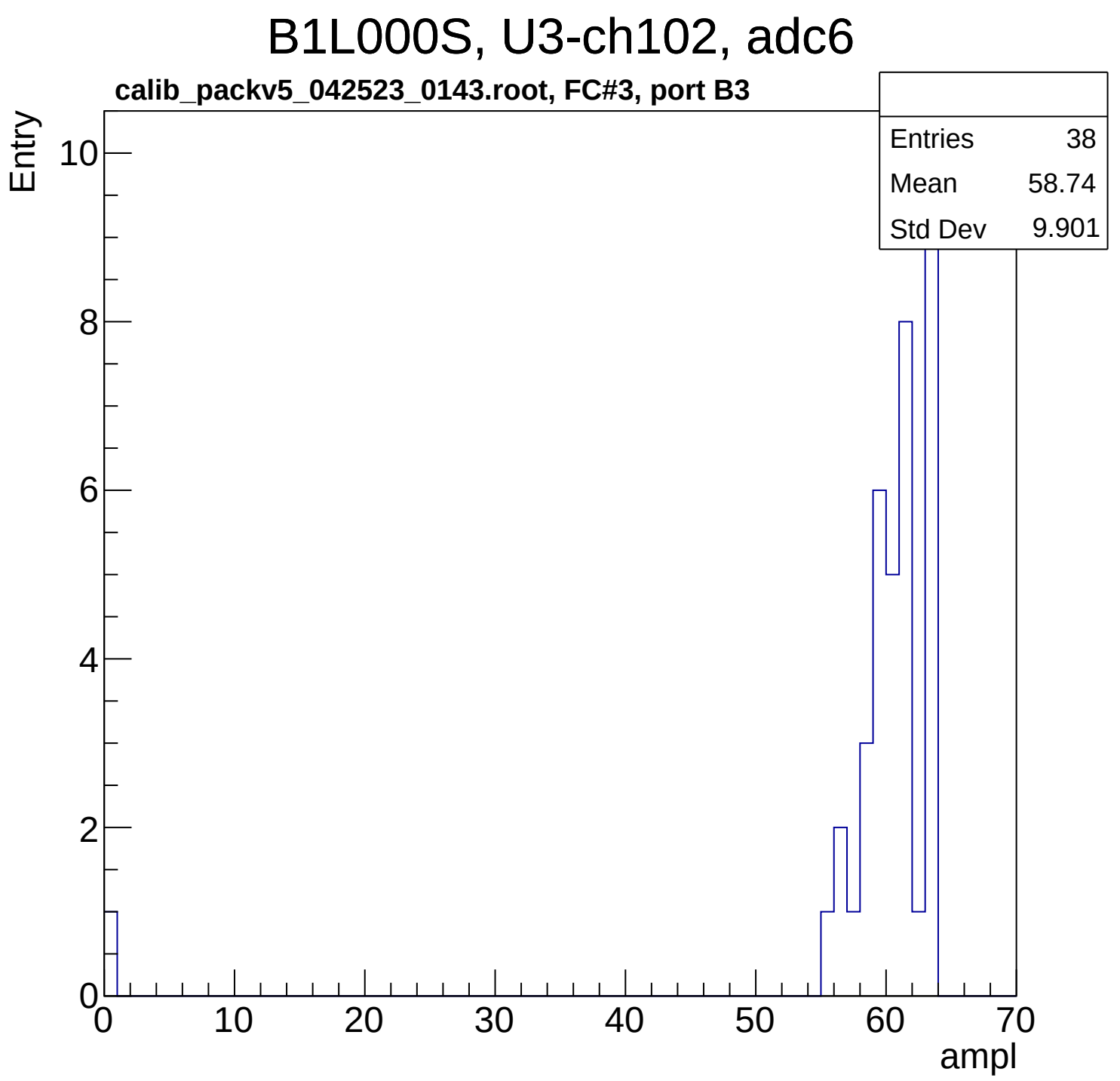
B1L000S, U3-ch102, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 38
Mean 58.74
Std Dev 9.901
Entry
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2
4
6
8
10
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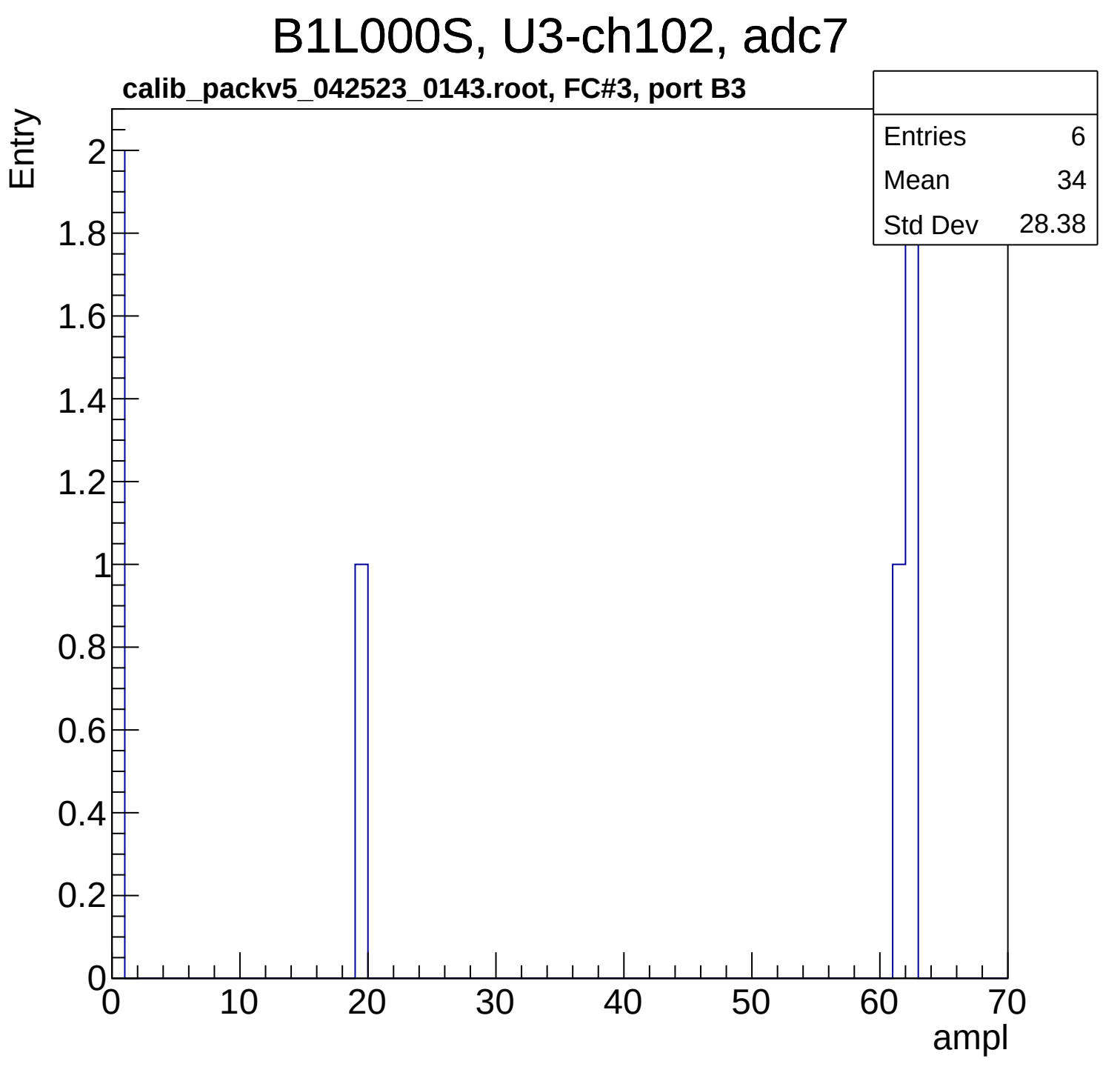
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch102, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 34
Std Dev 28.38
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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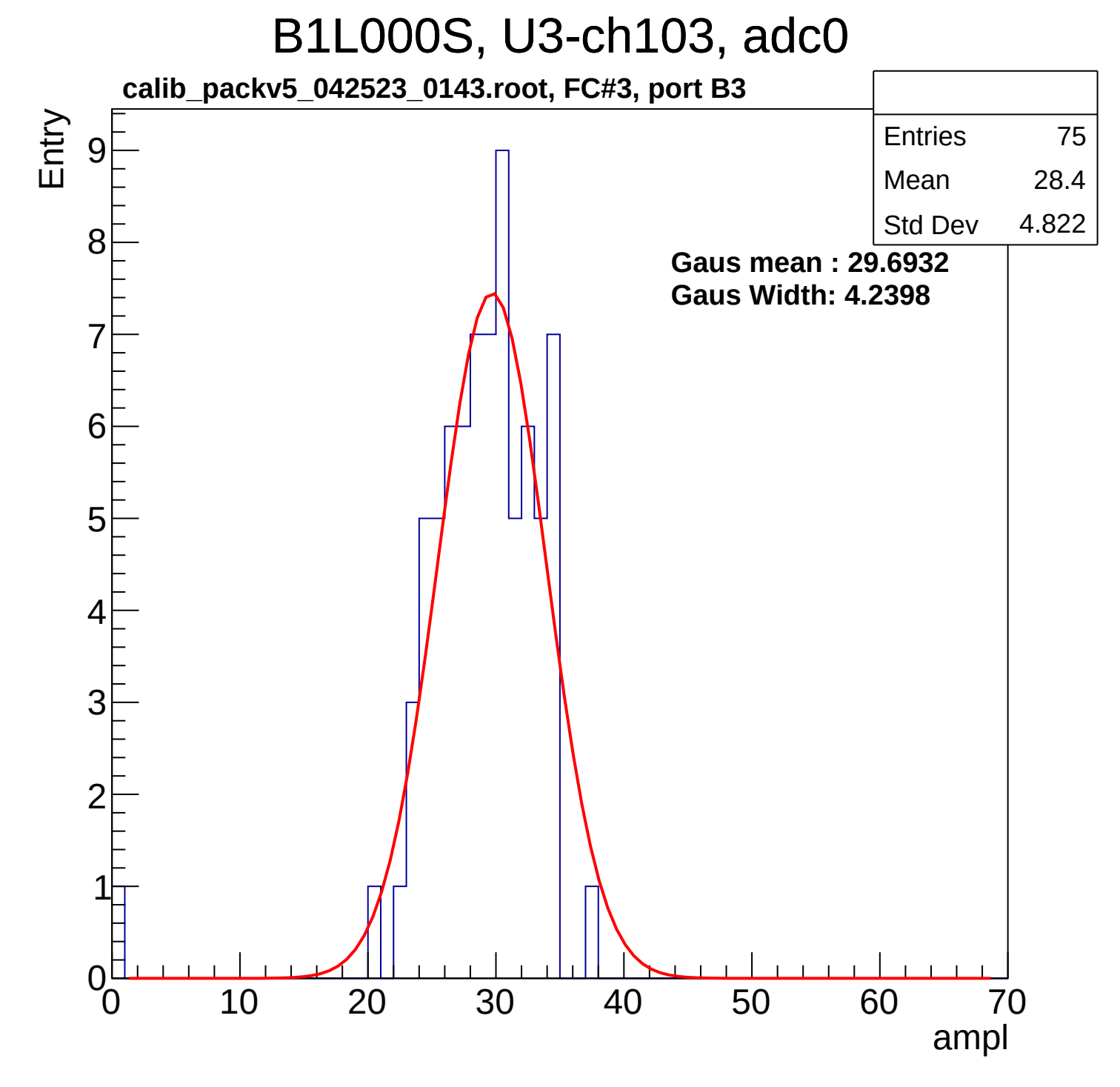
B1L000S, U3-ch103, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 28.4
Std Dev 4.822
Gaus mean : 29.6932
Gaus Width: 4.2398
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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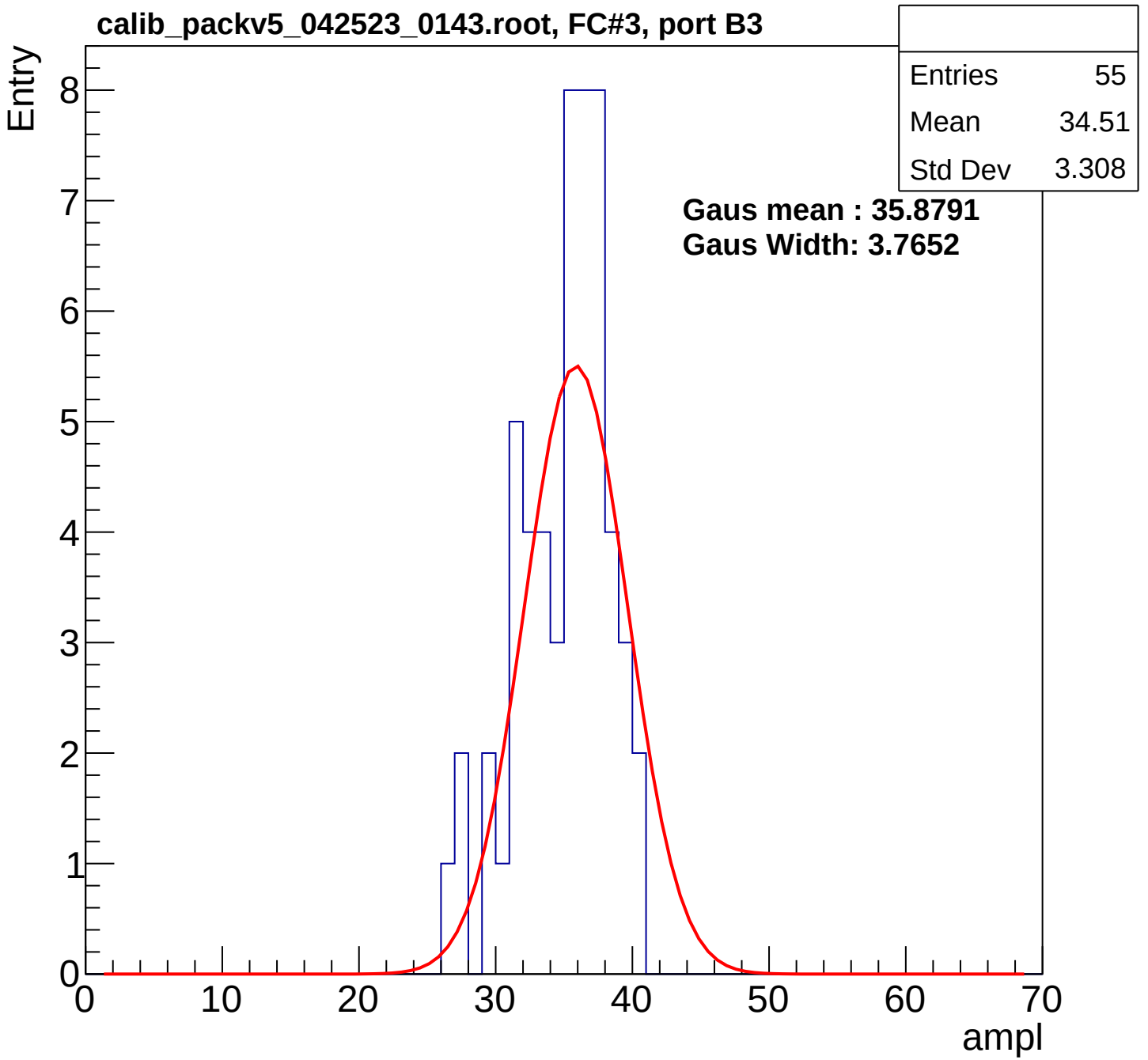
B1L000S, U3-ch103, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 34.51
Std Dev 3.308
Gaus mean : 35.8791
Gaus Width: 3.7652
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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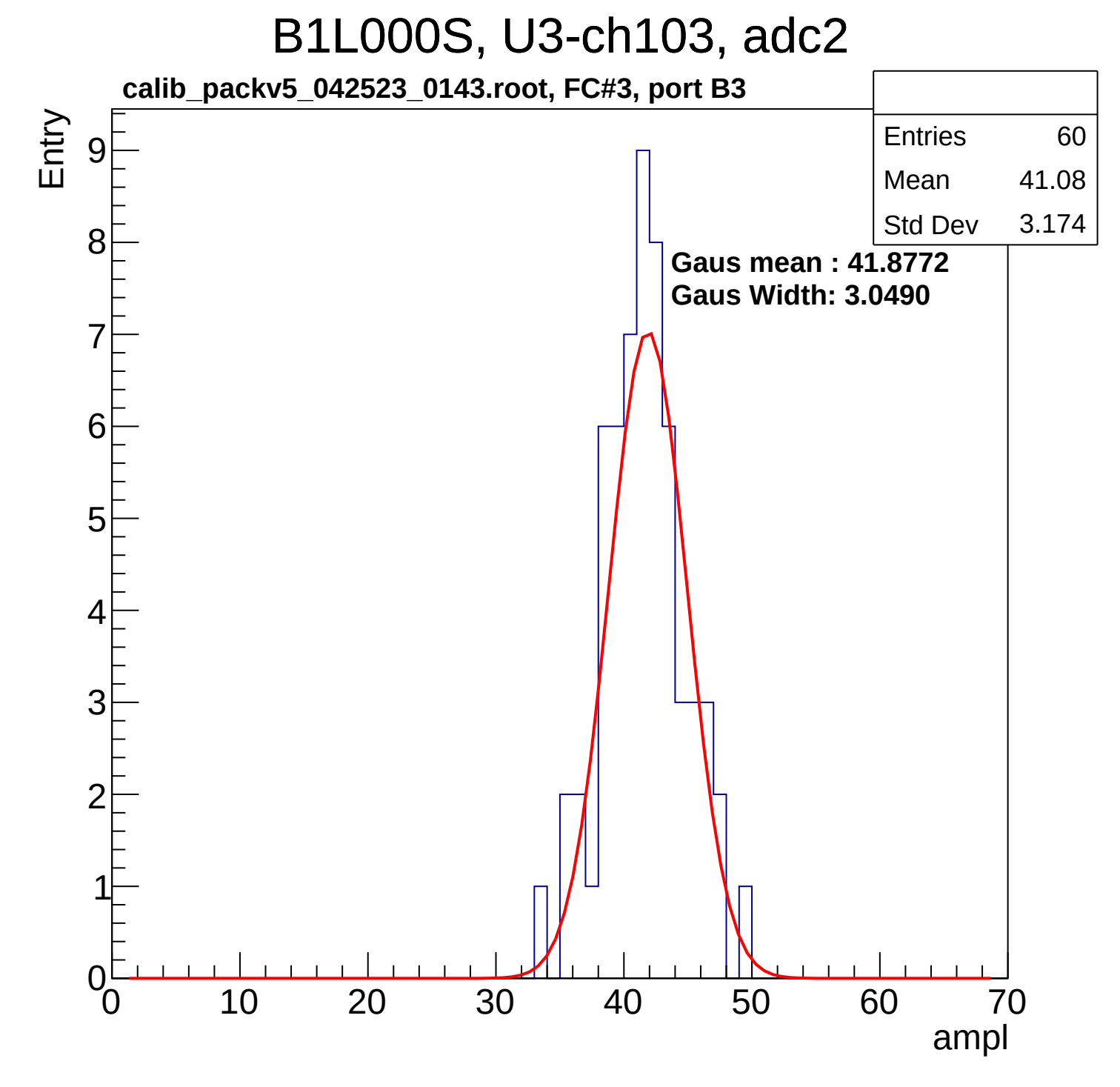
40
50
60
70


B1L000S, U3-ch103, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 41.08
Std Dev 3.174
Gaus mean : 41.8772
Gaus Width: 3.0490
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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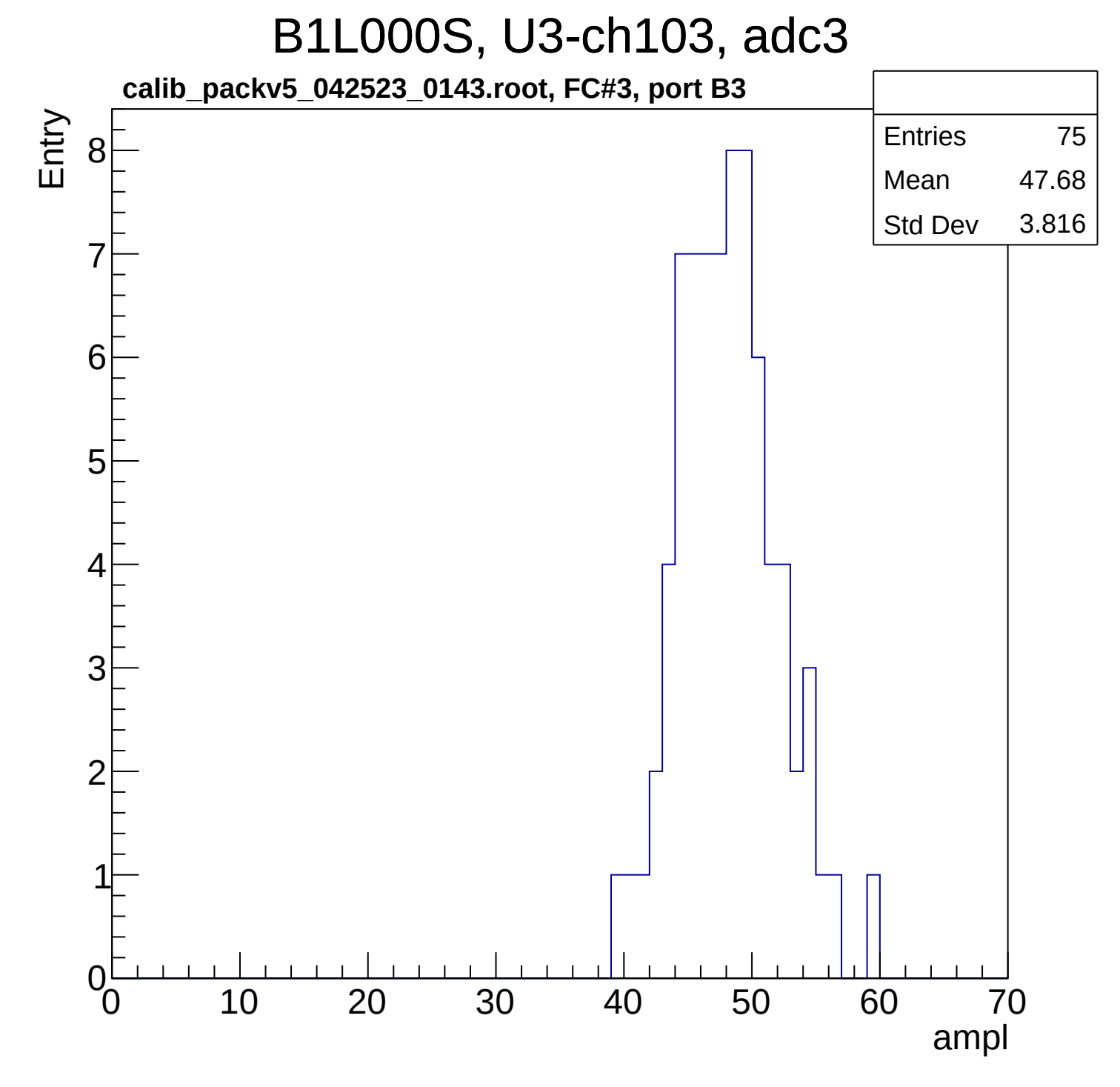
B1L000S, U3-ch103, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 47.68
Std Dev 3.816
Entry
ampl
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3
4
5
6
7
8
0
10
20
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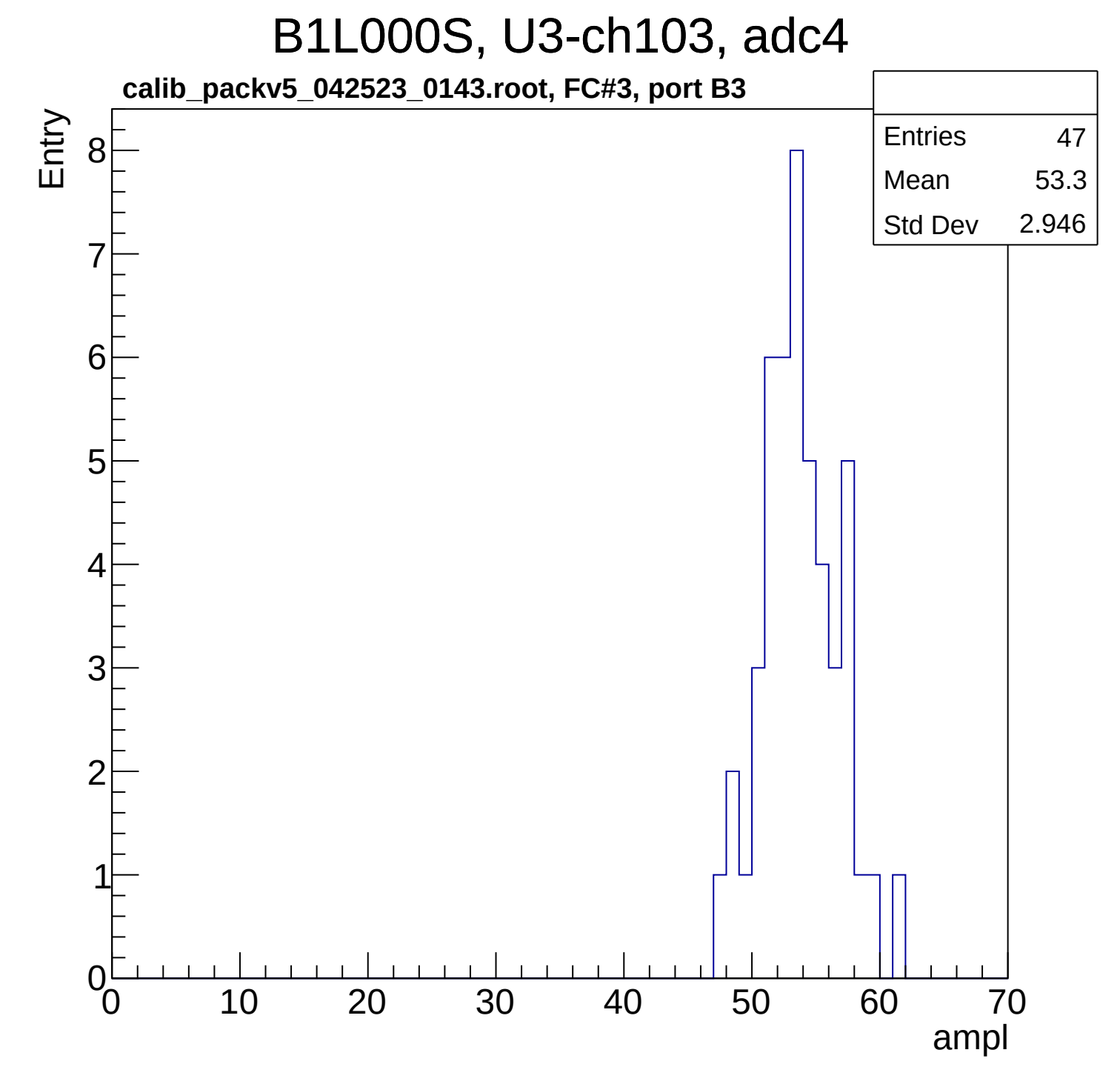
40
50
60
70


B1L000S, U3-ch103, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 53.3
Std Dev 2.946
Entry
ampl
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5
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7
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0
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40
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60
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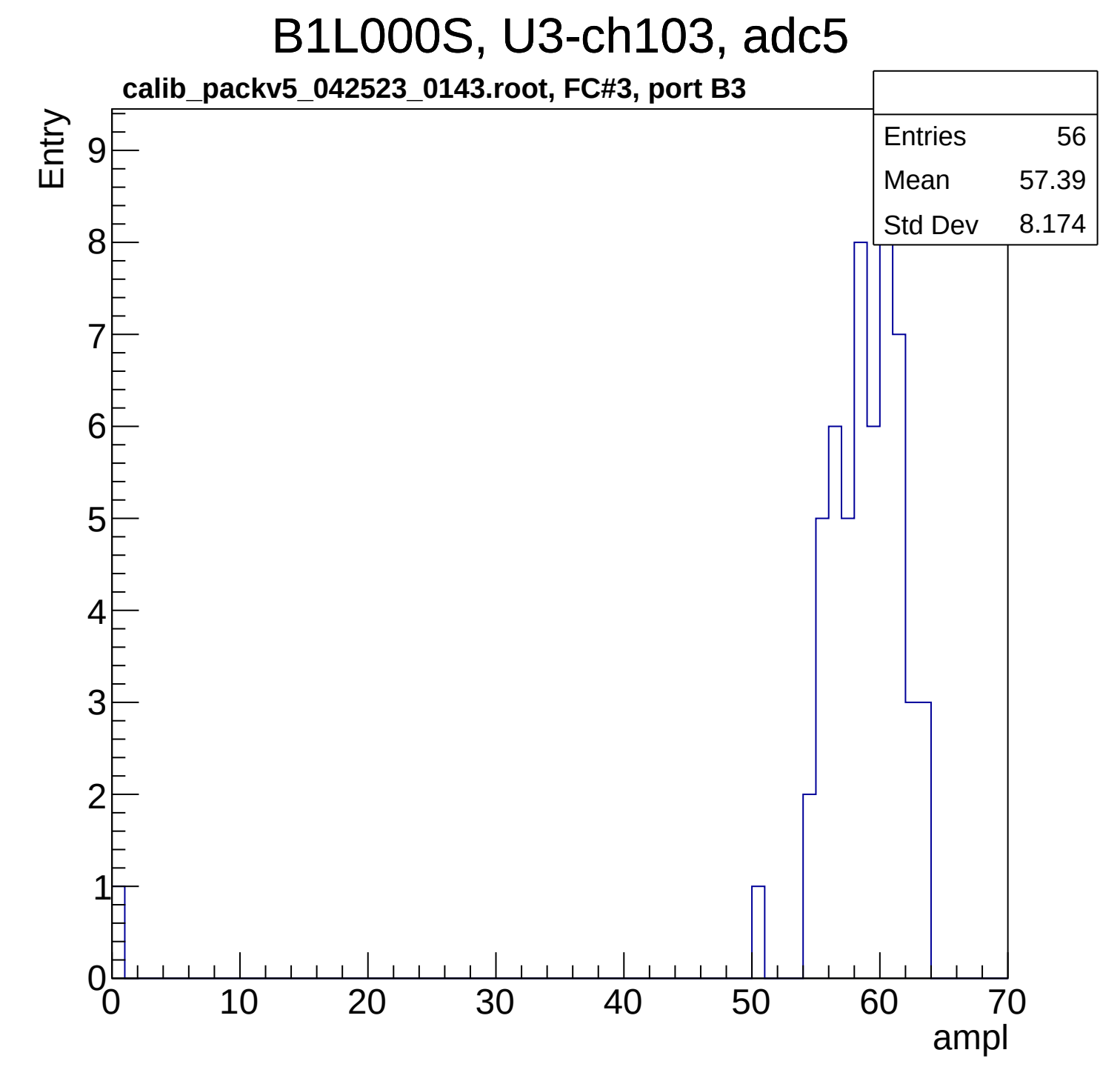
B1L000S, U3-ch103, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 57.39
Std Dev 8.174
Entry
ampl
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2
3
4
5
6
7
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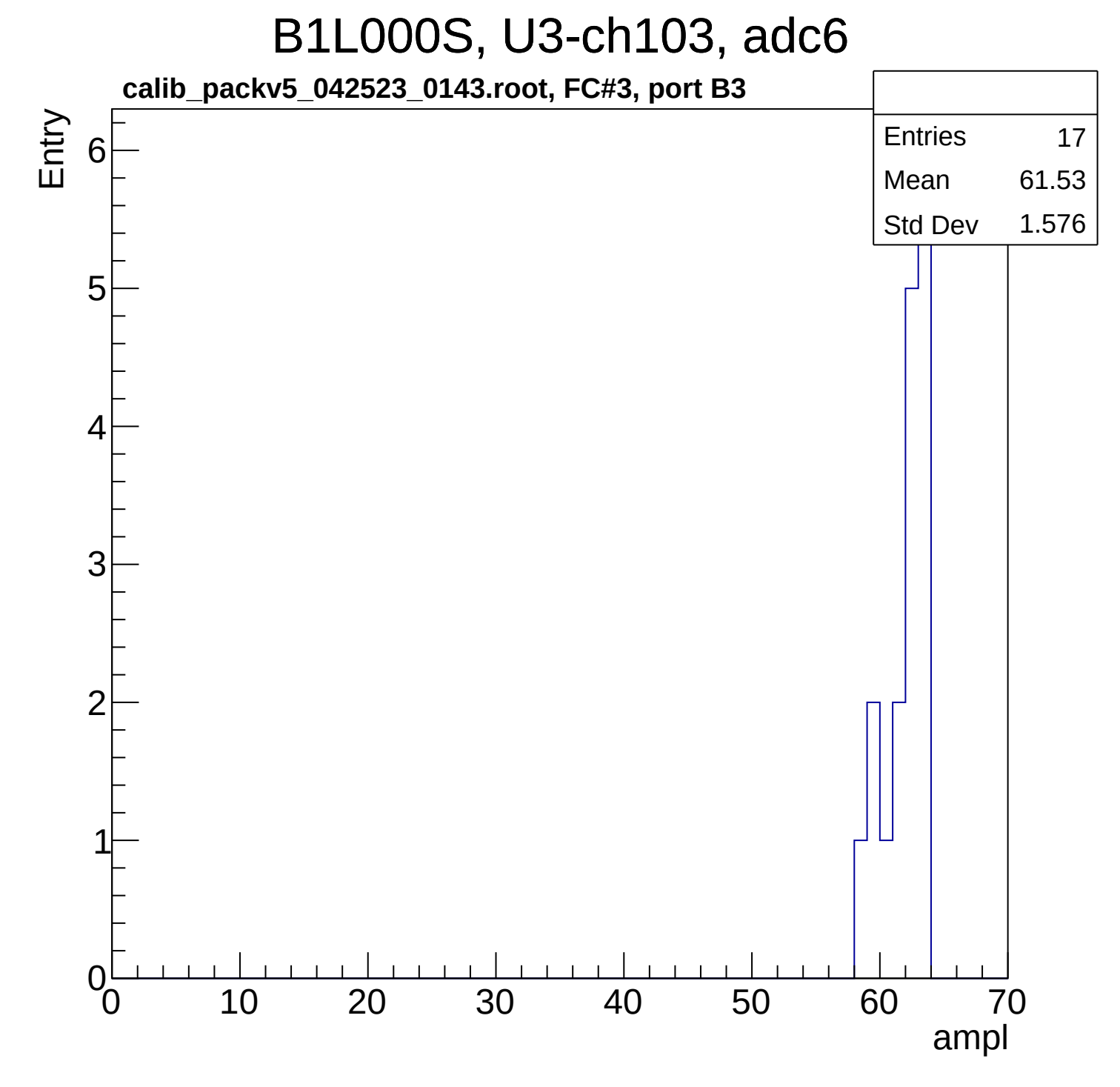
B1L000S, U3-ch103, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.53
Std Dev 1.576
Entry
ampl
0
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3
4
5
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20
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40
50
60
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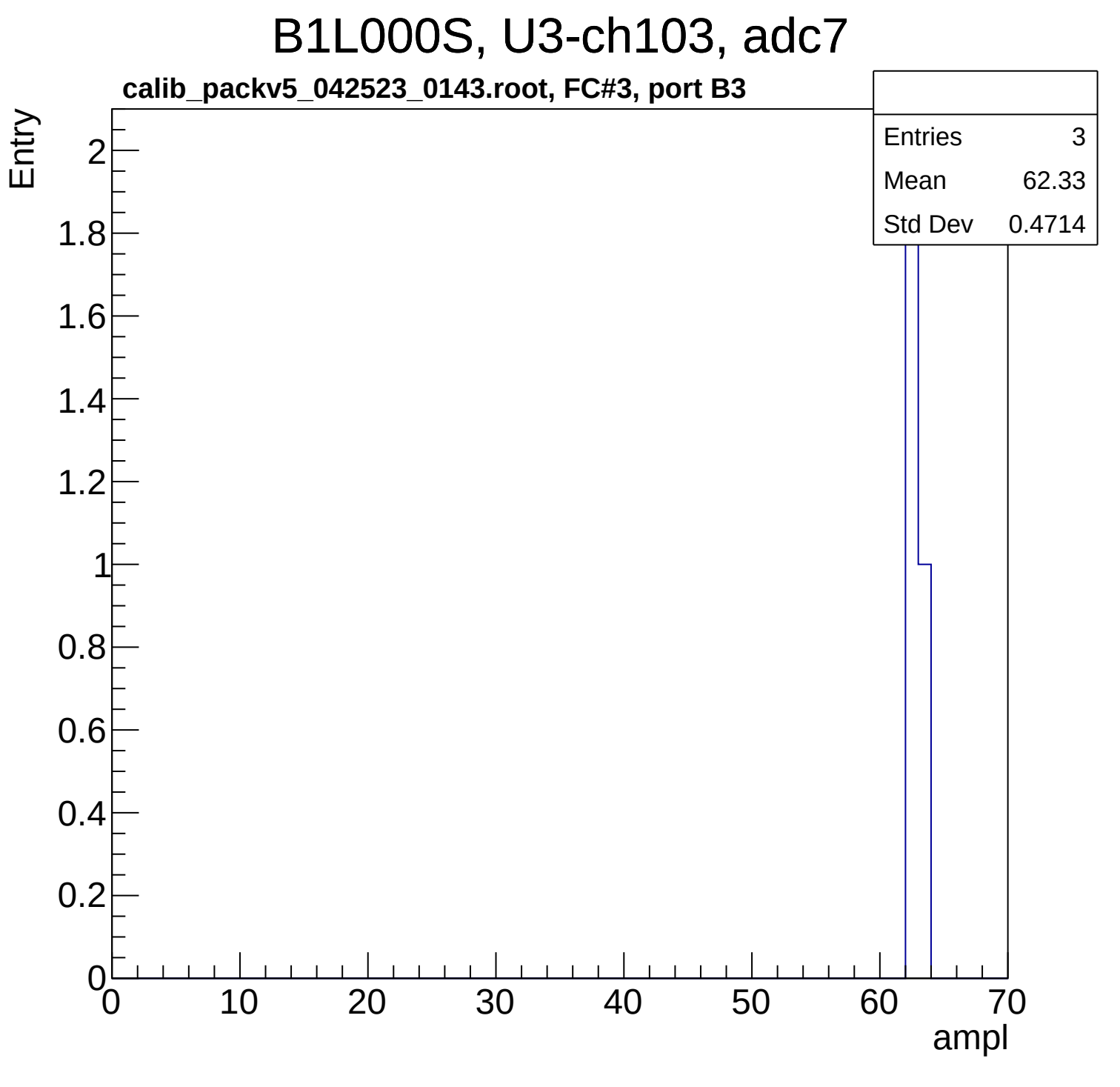
B1L000S, U3-ch103, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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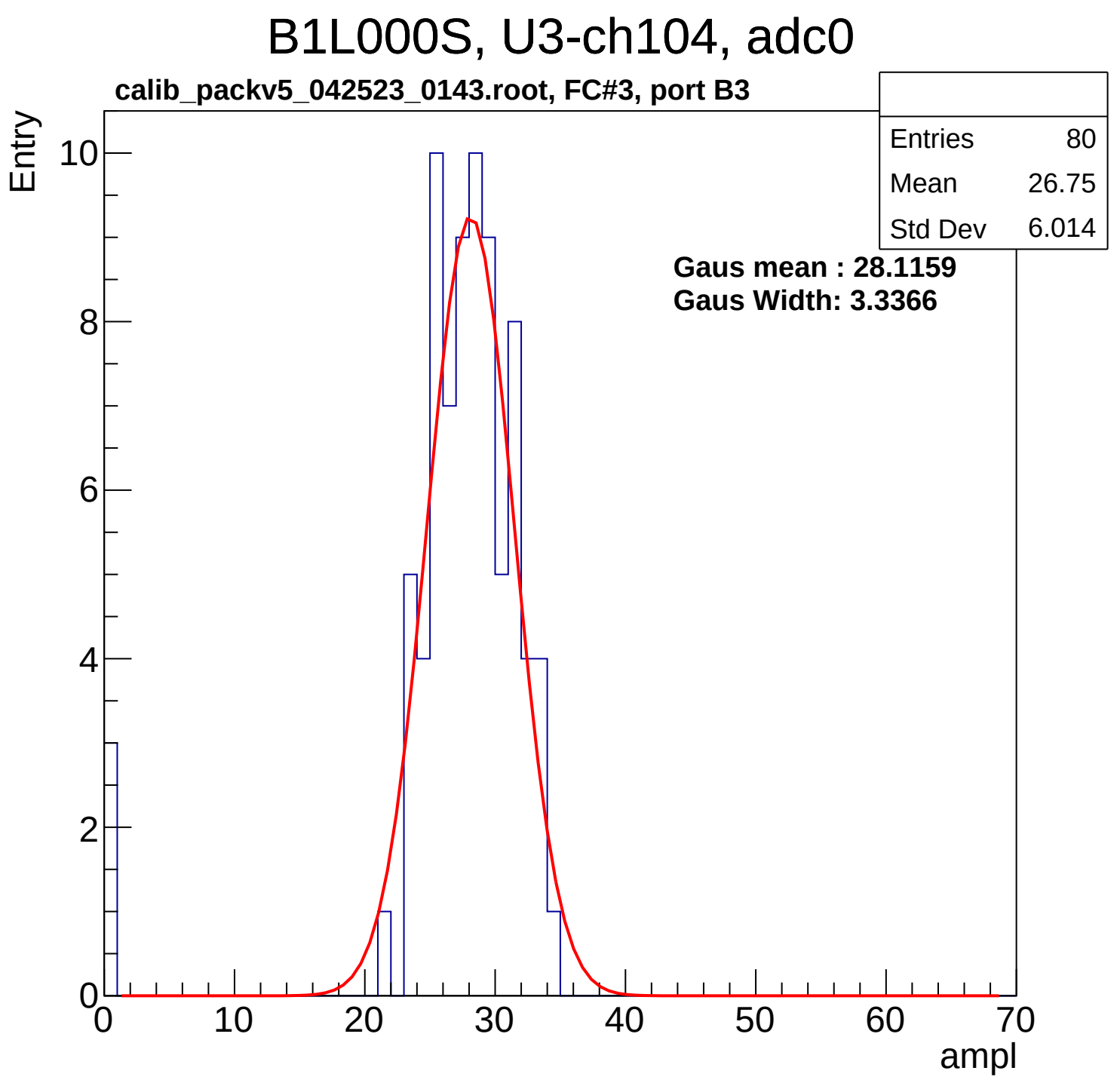
B1L000S, U3-ch104, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 26.75
Std Dev 6.014
Gaus mean : 28.1159
Gaus Width: 3.3366
Entry
ampl
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2
4
6
8
10
0
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20
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40
50
60
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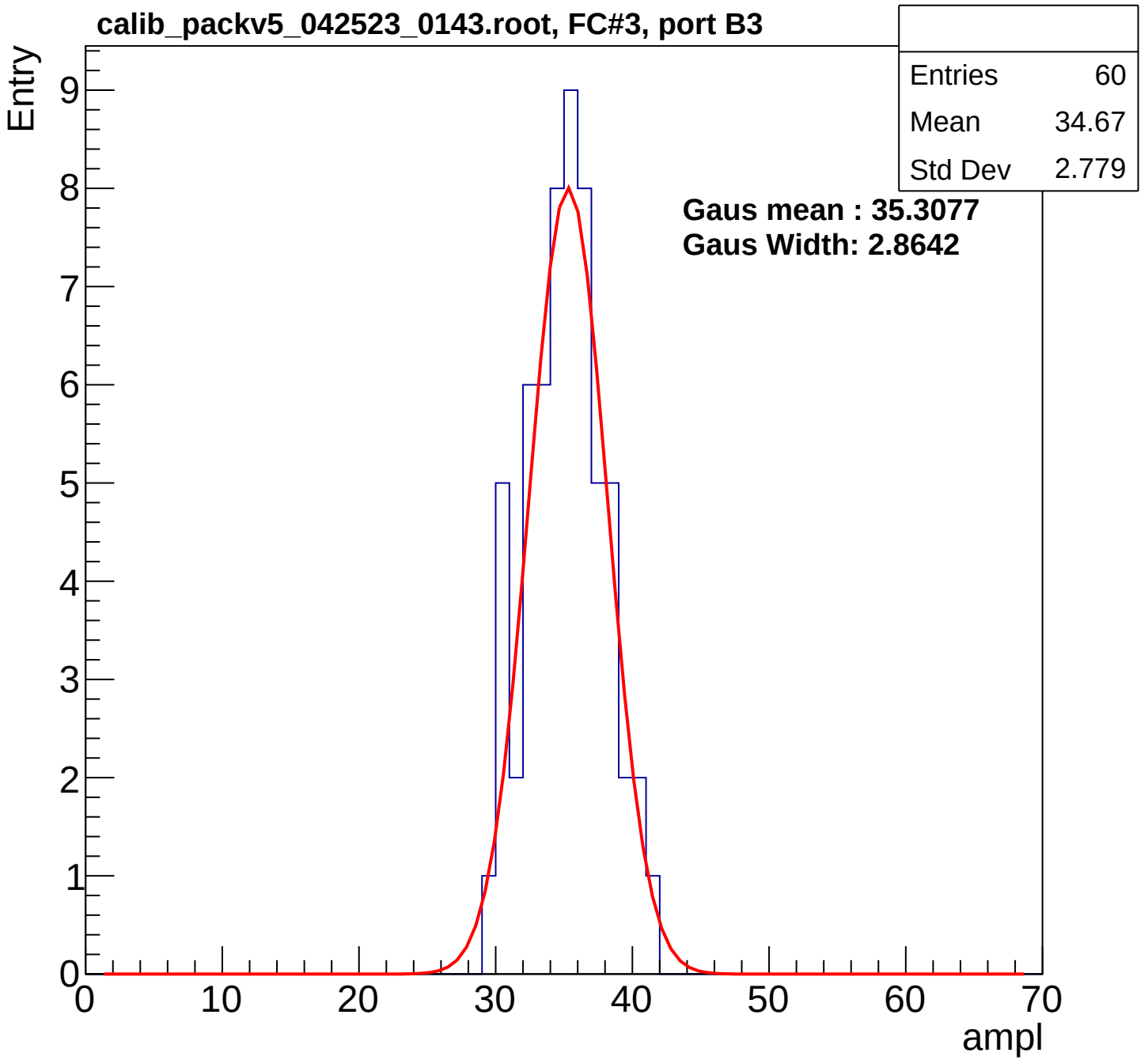
B1L000S, U3-ch104, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 34.67
Std Dev 2.779
Gaus mean : 35.3077
Gaus Width: 2.8642
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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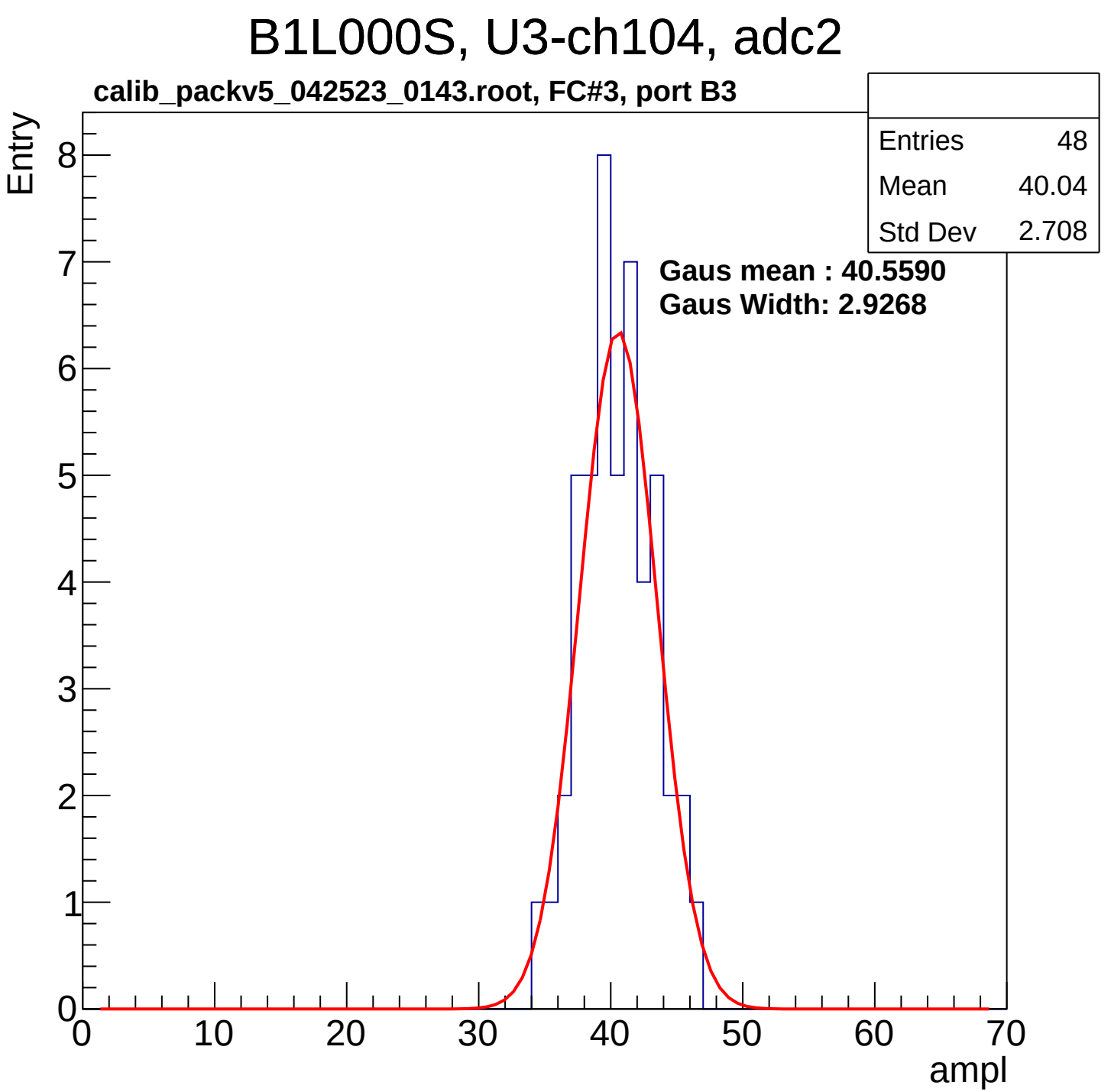
B1L000S, U3-ch104, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 40.04
Std Dev 2.708
Gaus mean : 40.5590
Gaus Width: 2.9268
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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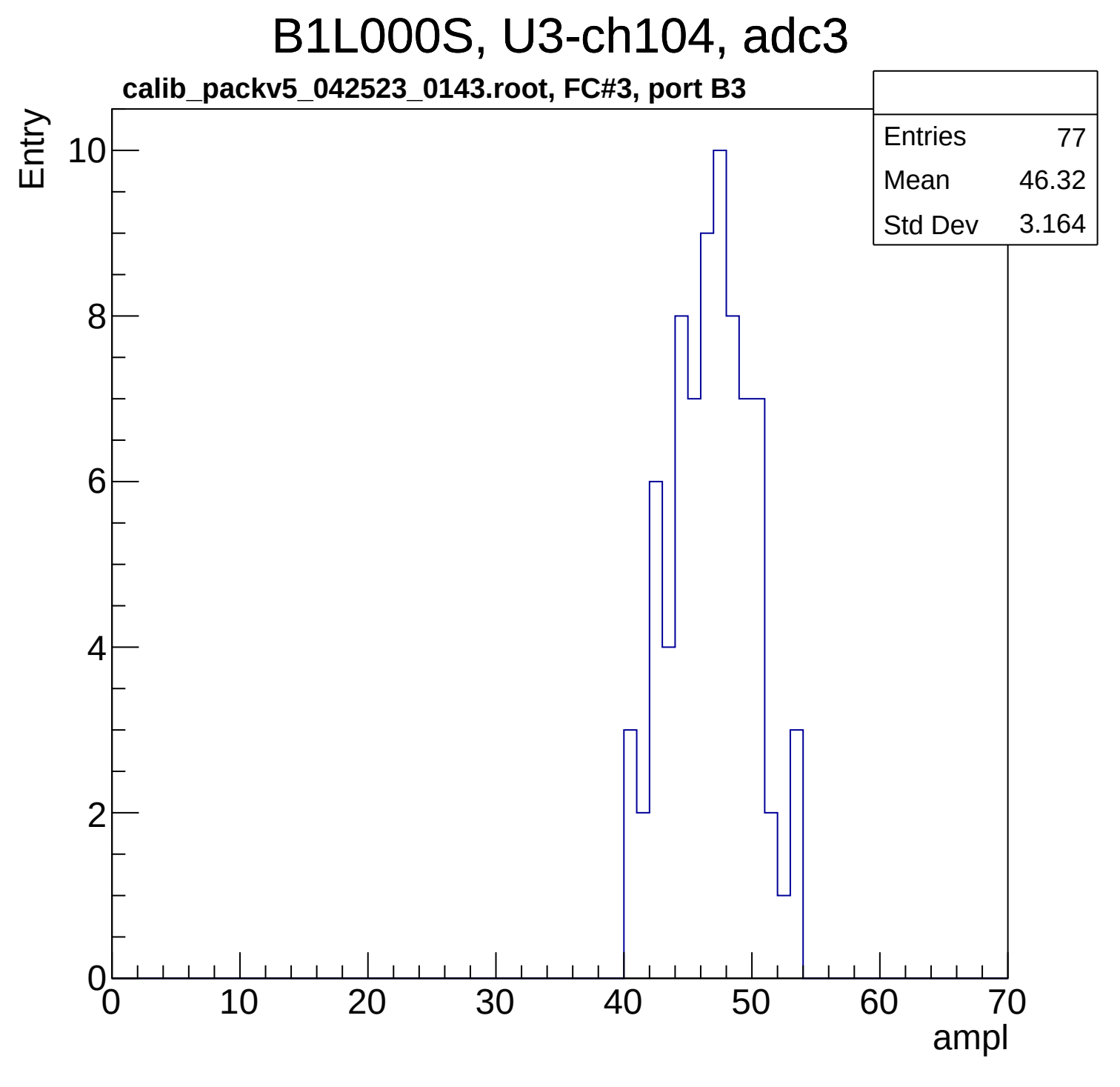
B1L000S, U3-ch104, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 46.32
Std Dev 3.164
Entry
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2
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6
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10
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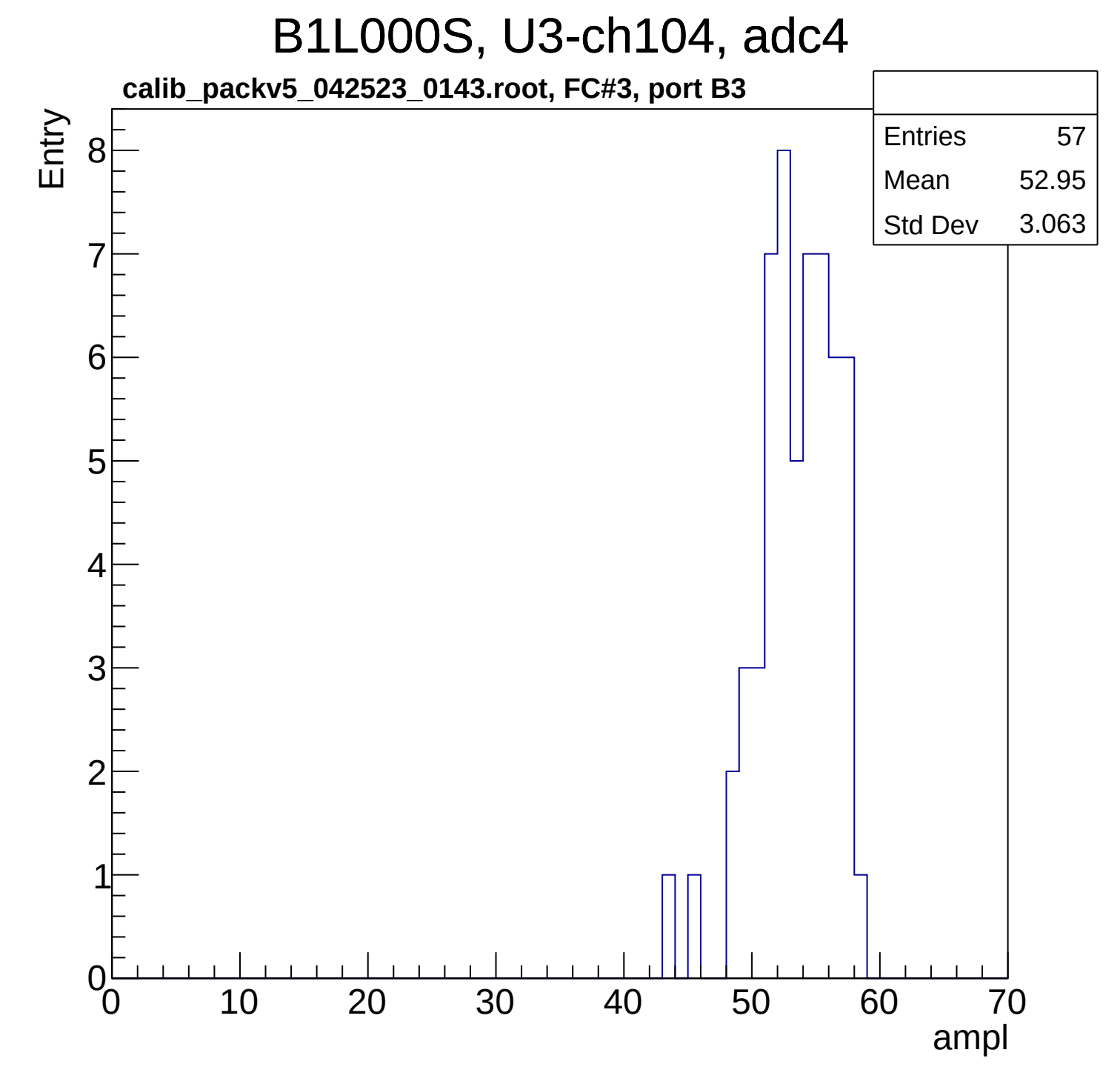
70
ampl


B1L000S, U3-ch104, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 52.95
Std Dev 3.063
Entry
ampl
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1
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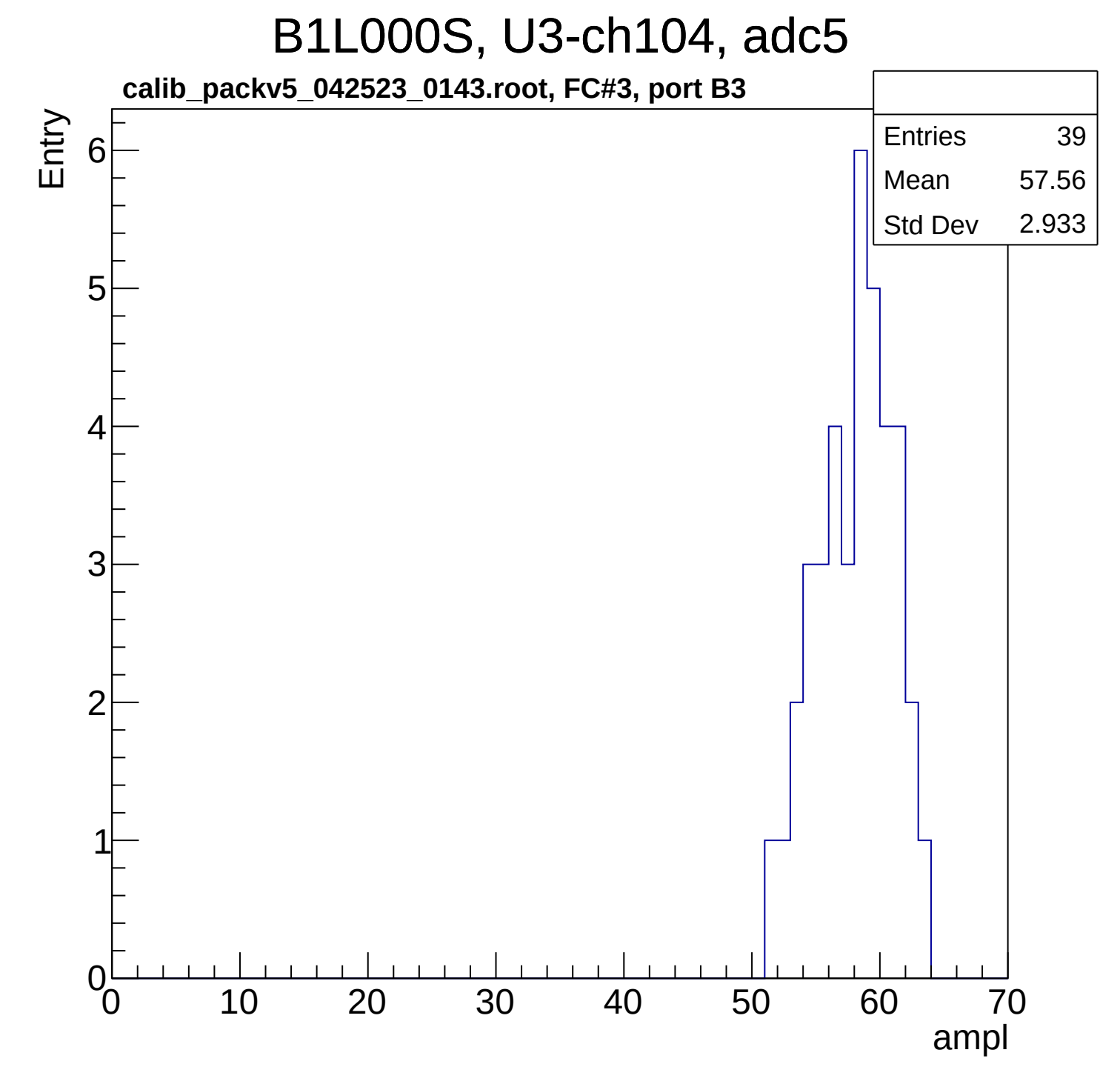
B1L000S, U3-ch104, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 57.56
Std Dev 2.933
Entry
ampl
0
1
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3
4
5
6
0
10
20
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40
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60
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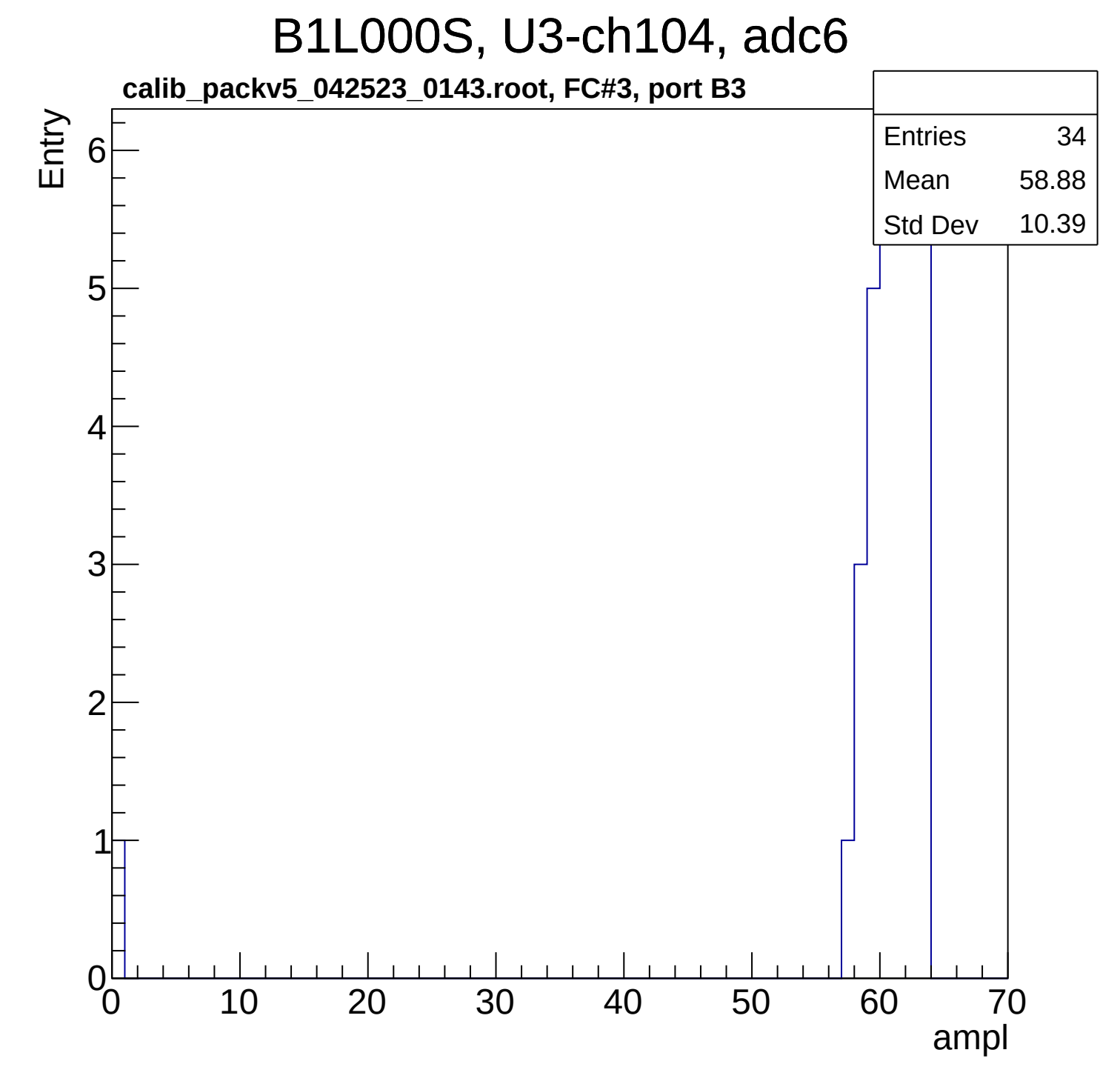
B1L000S, U3-ch104, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 34
Mean 58.88
Std Dev 10.39
Entry
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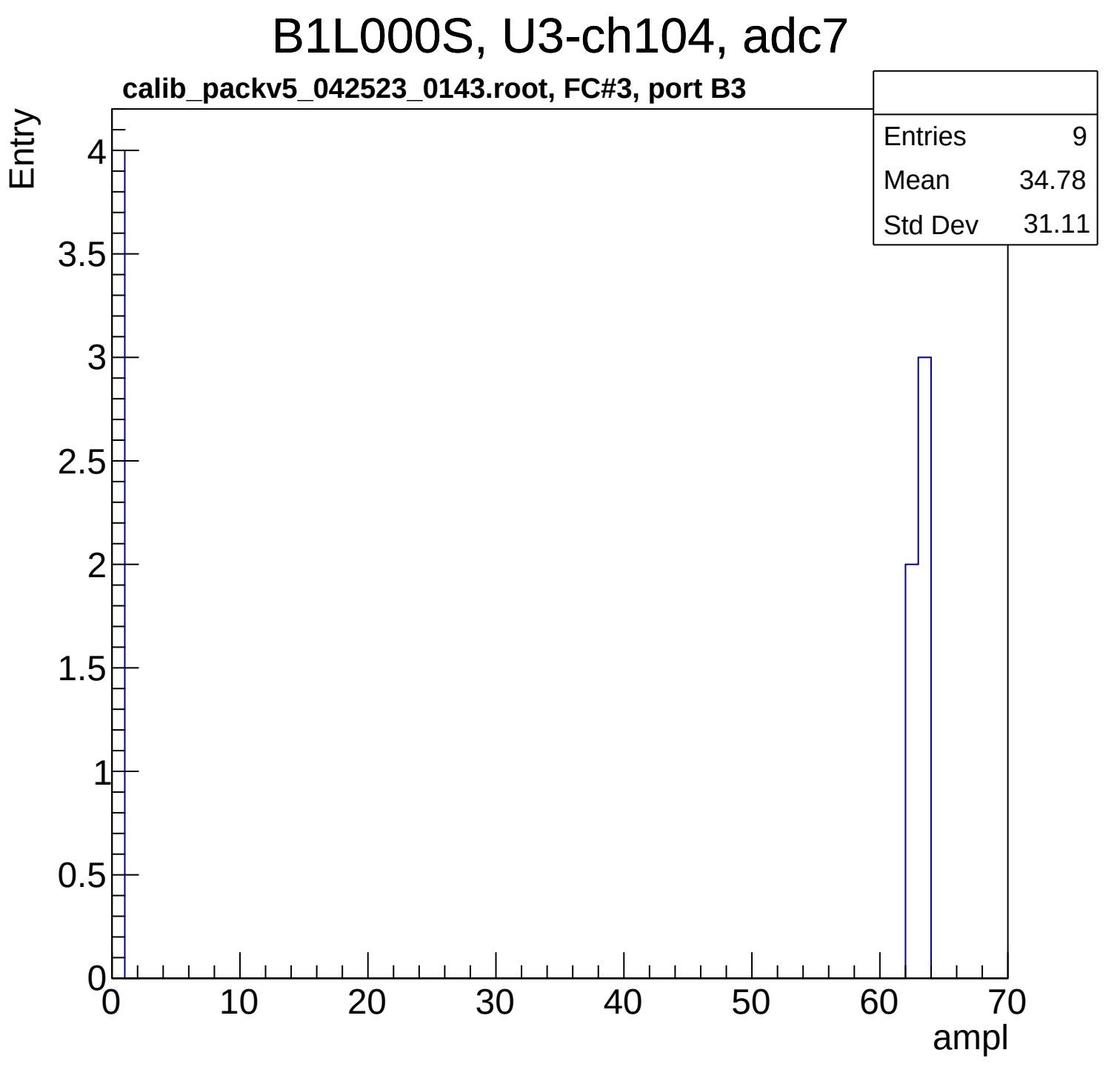
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch104, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 34.78
Std Dev 31.11
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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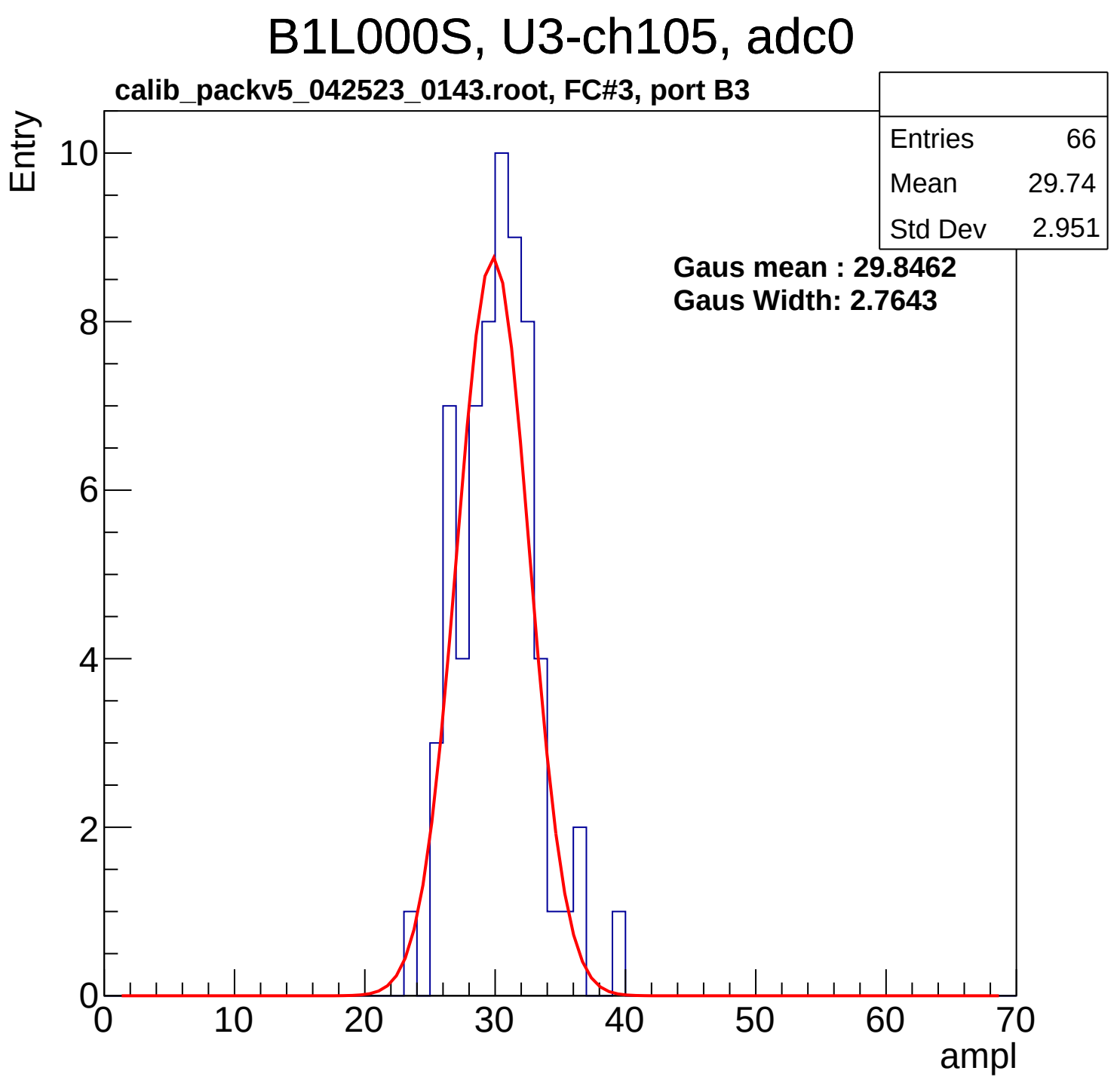
B1L000S, U3-ch105, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 29.74
Std Dev 2.951
Gaus mean : 29.8462
Gaus Width: 2.7643
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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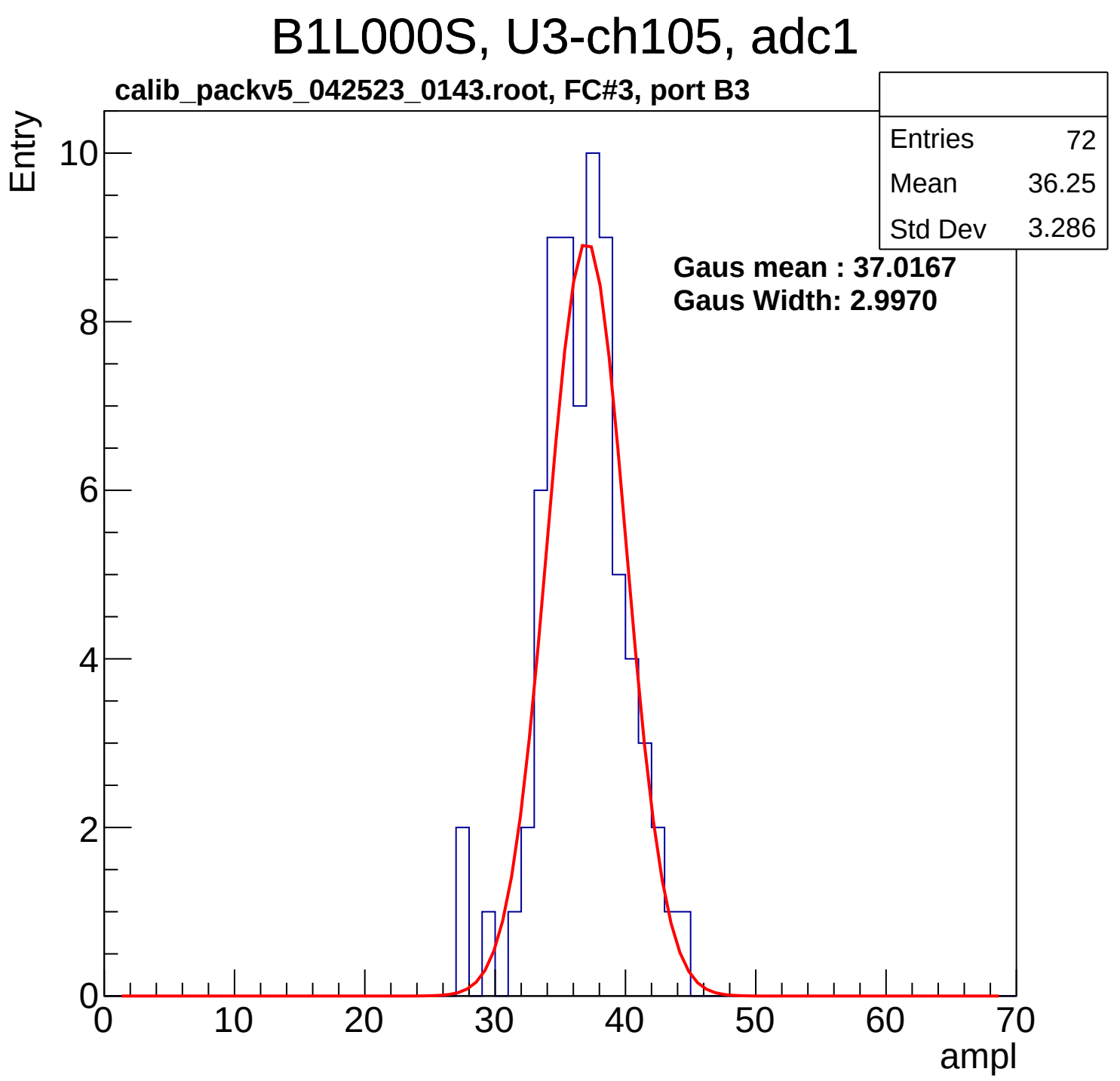
B1L000S, U3-ch105, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 36.25
Std Dev 3.286
Gaus mean : 37.0167
Gaus Width: 2.9970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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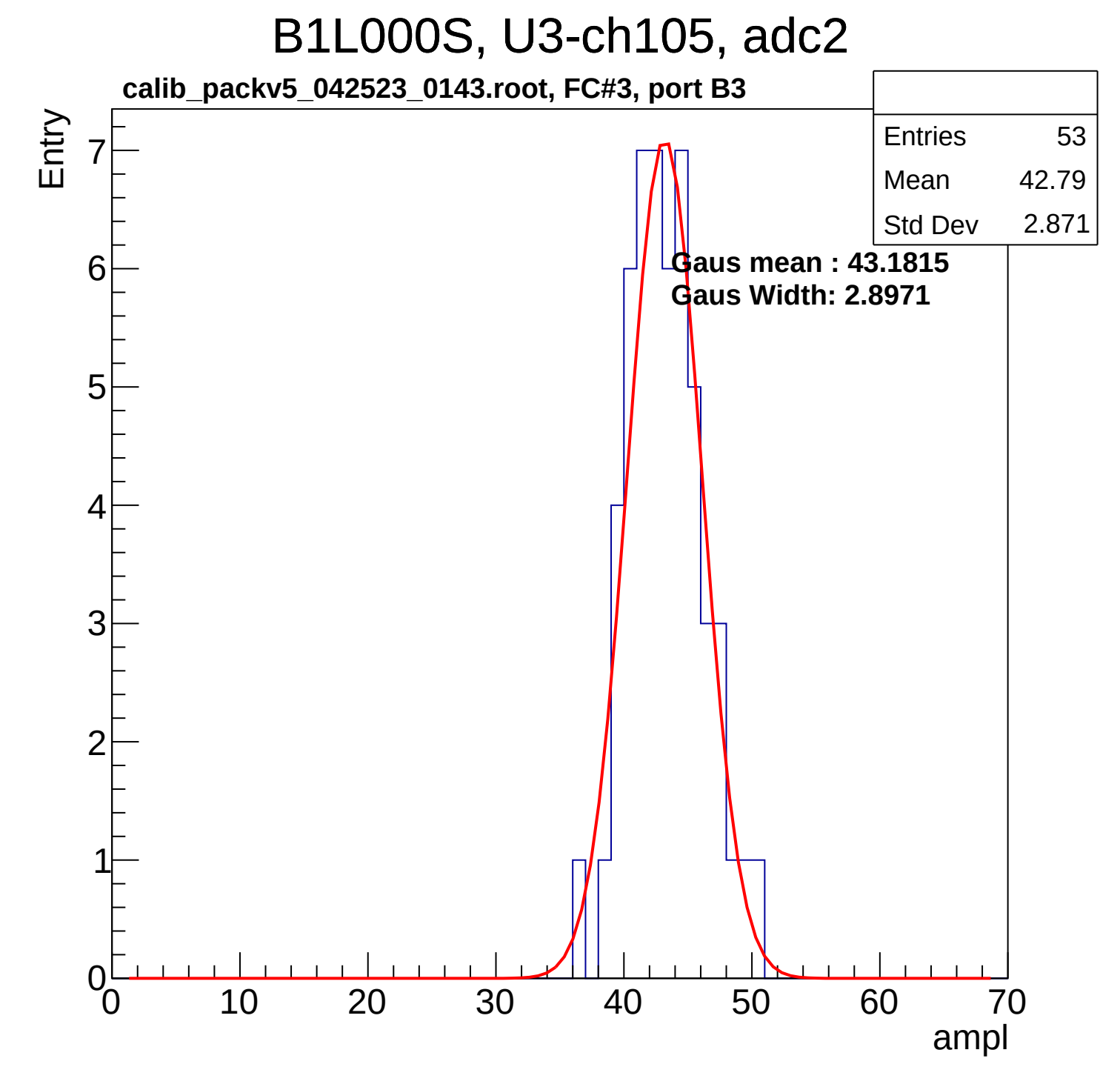
B1L000S, U3-ch105, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 42.79
Std Dev 2.871
Gaus mean : 43.1815
Gaus Width: 2.8971
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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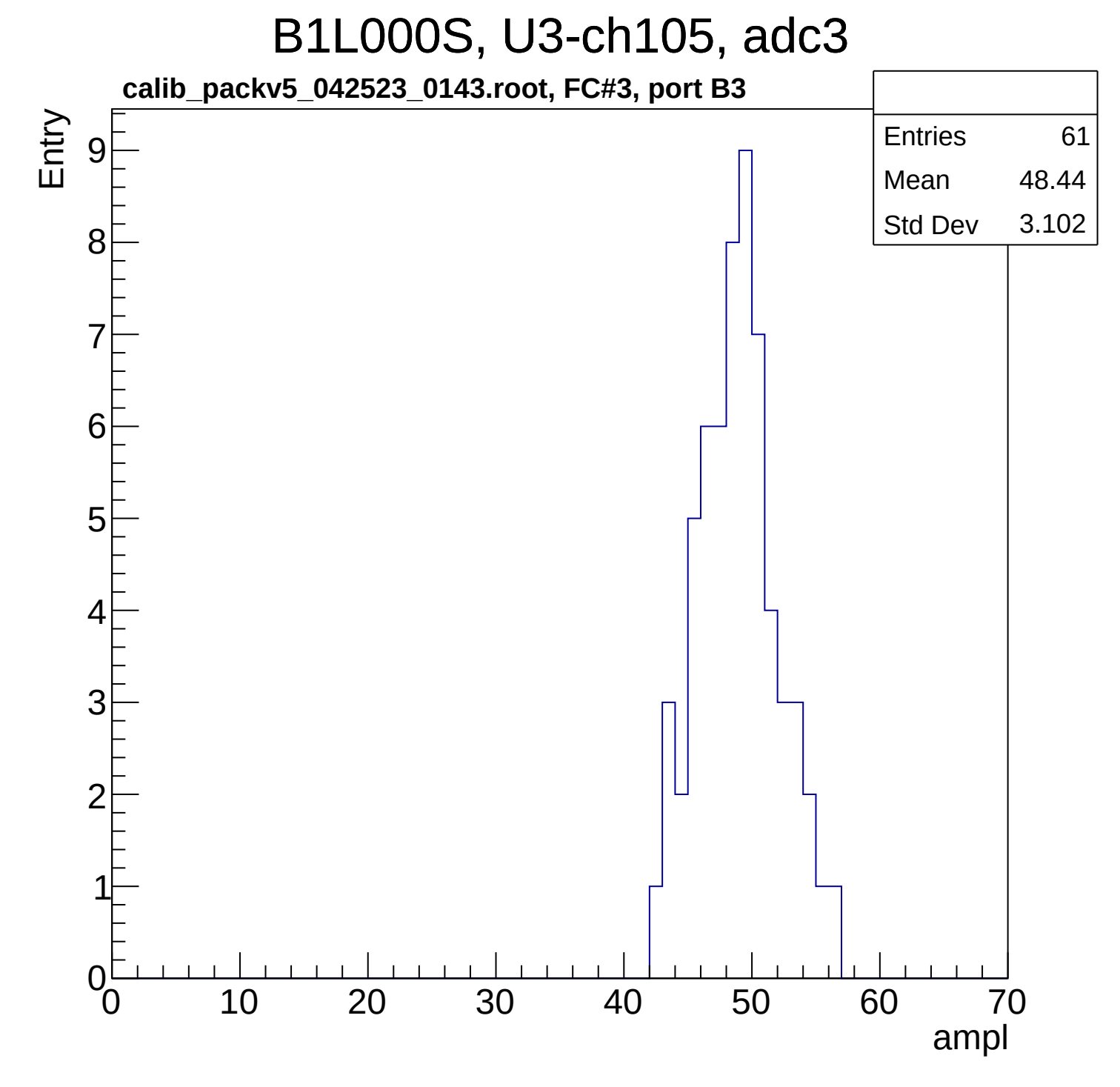
B1L000S, U3-ch105, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 48.44
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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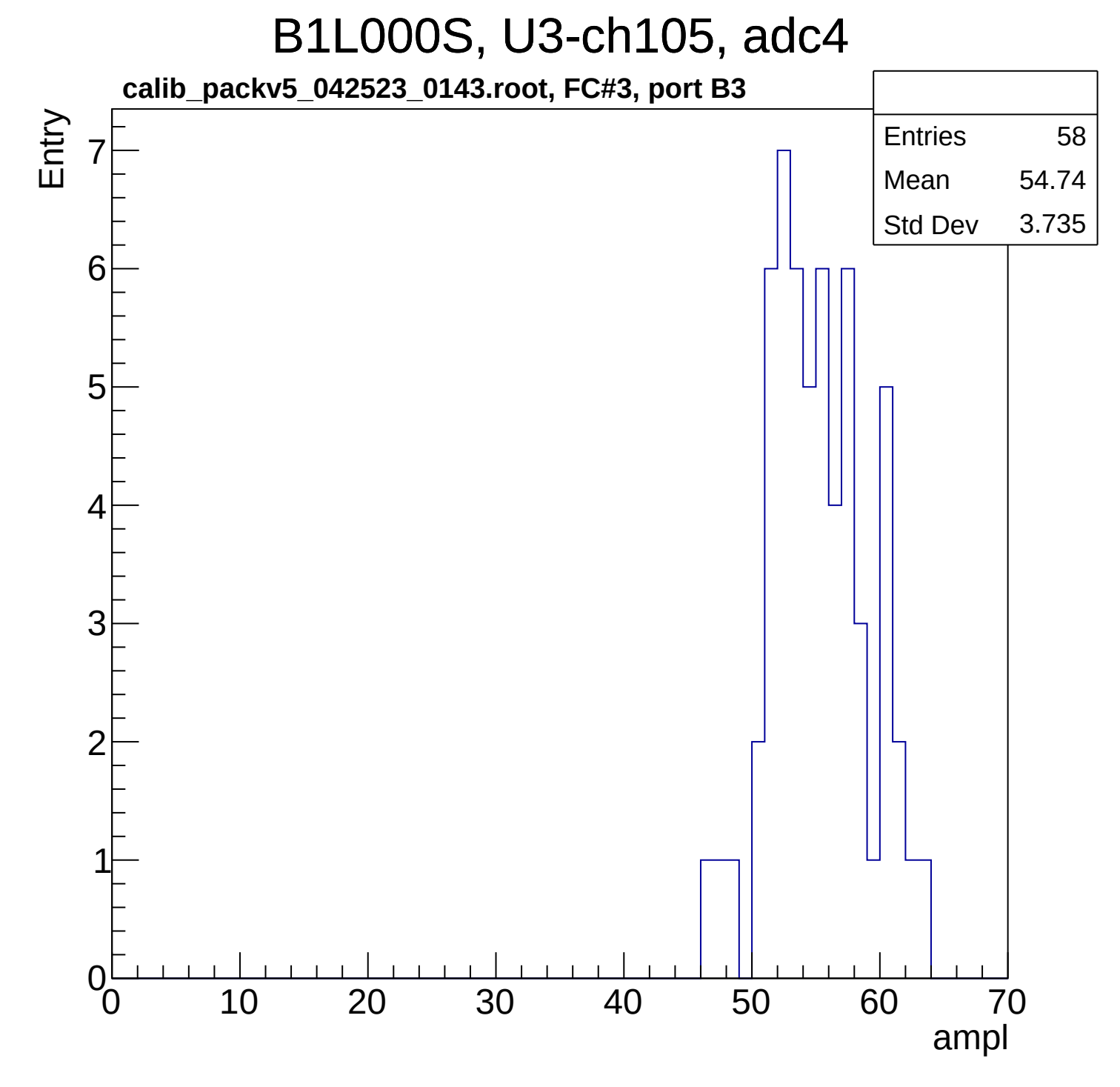
B1L000S, U3-ch105, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.74
Std Dev 3.735
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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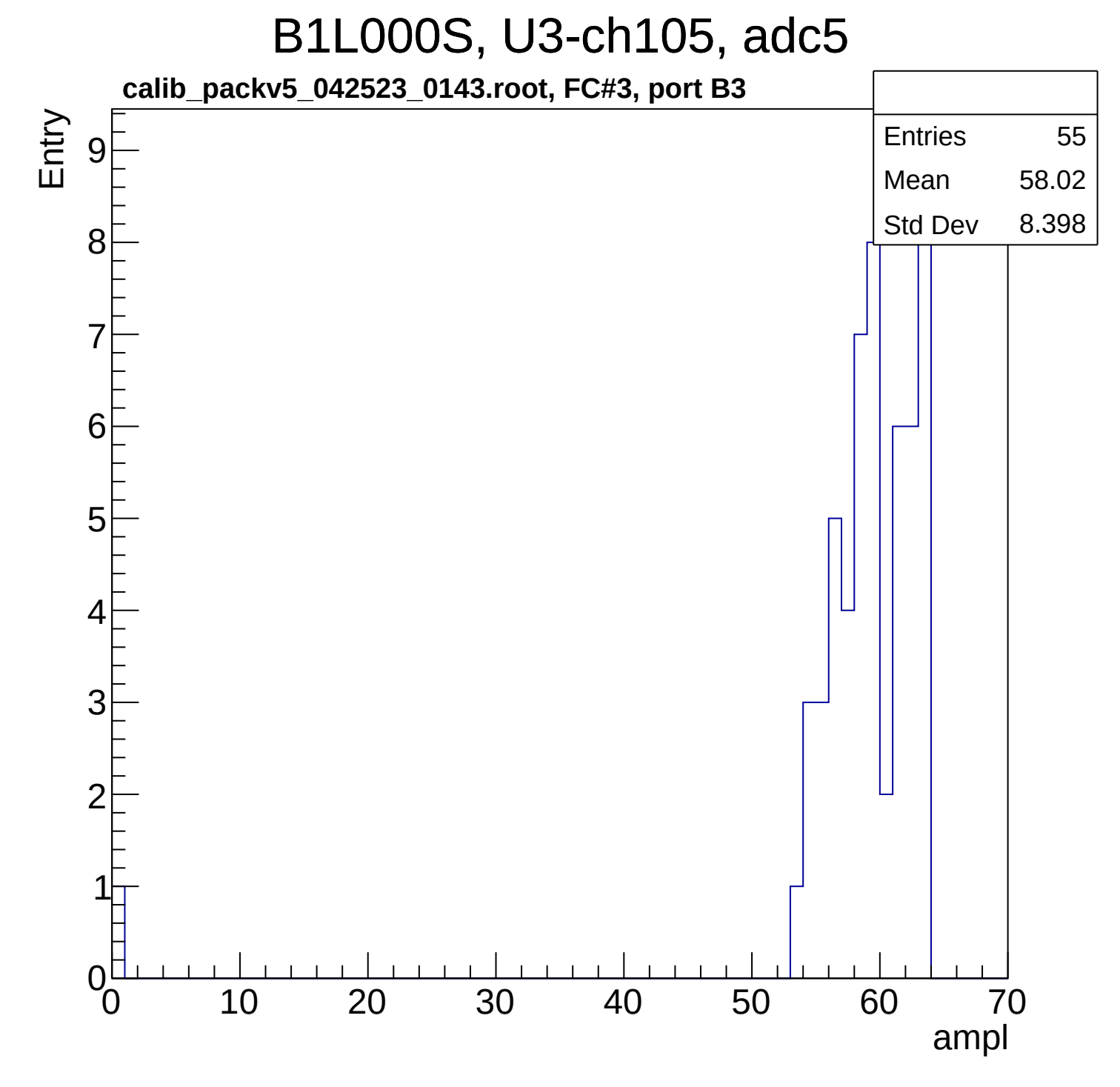
B1L000S, U3-ch105, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.02
Std Dev 8.398
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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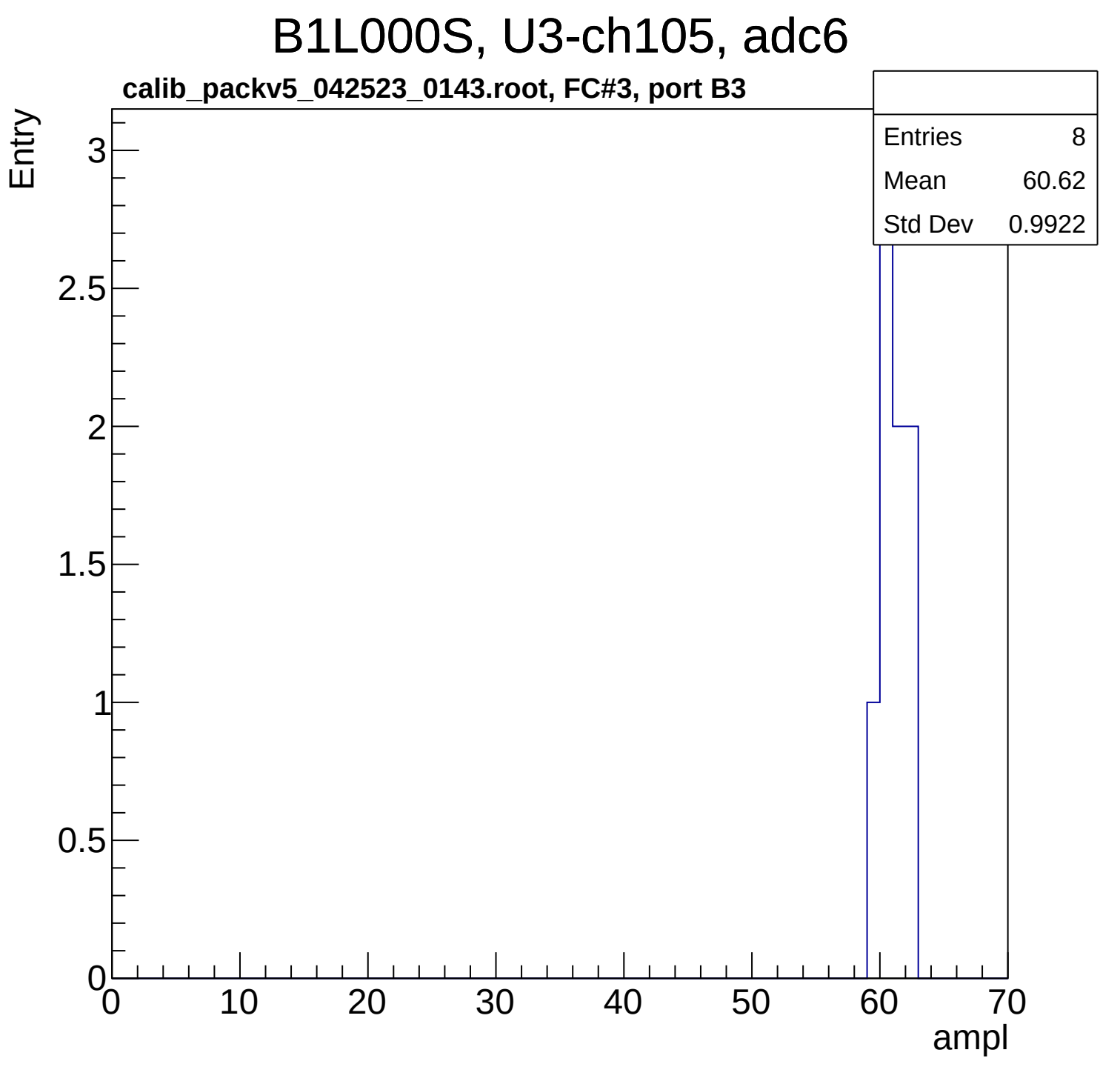
B1L000S, U3-ch105, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 60.62
Std Dev 0.9922
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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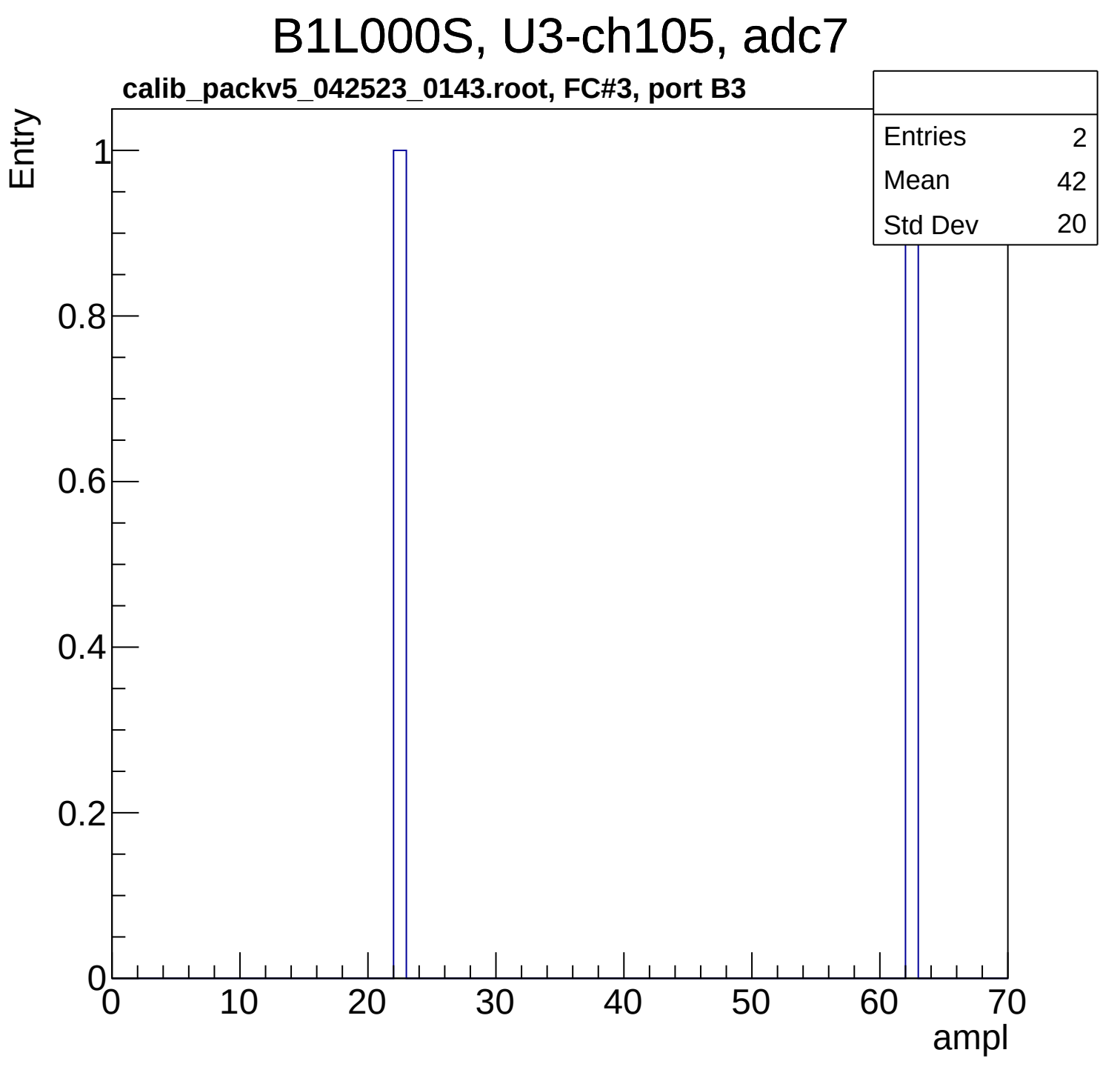
B1L000S, U3-ch105, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 42
Std Dev 20
Entry
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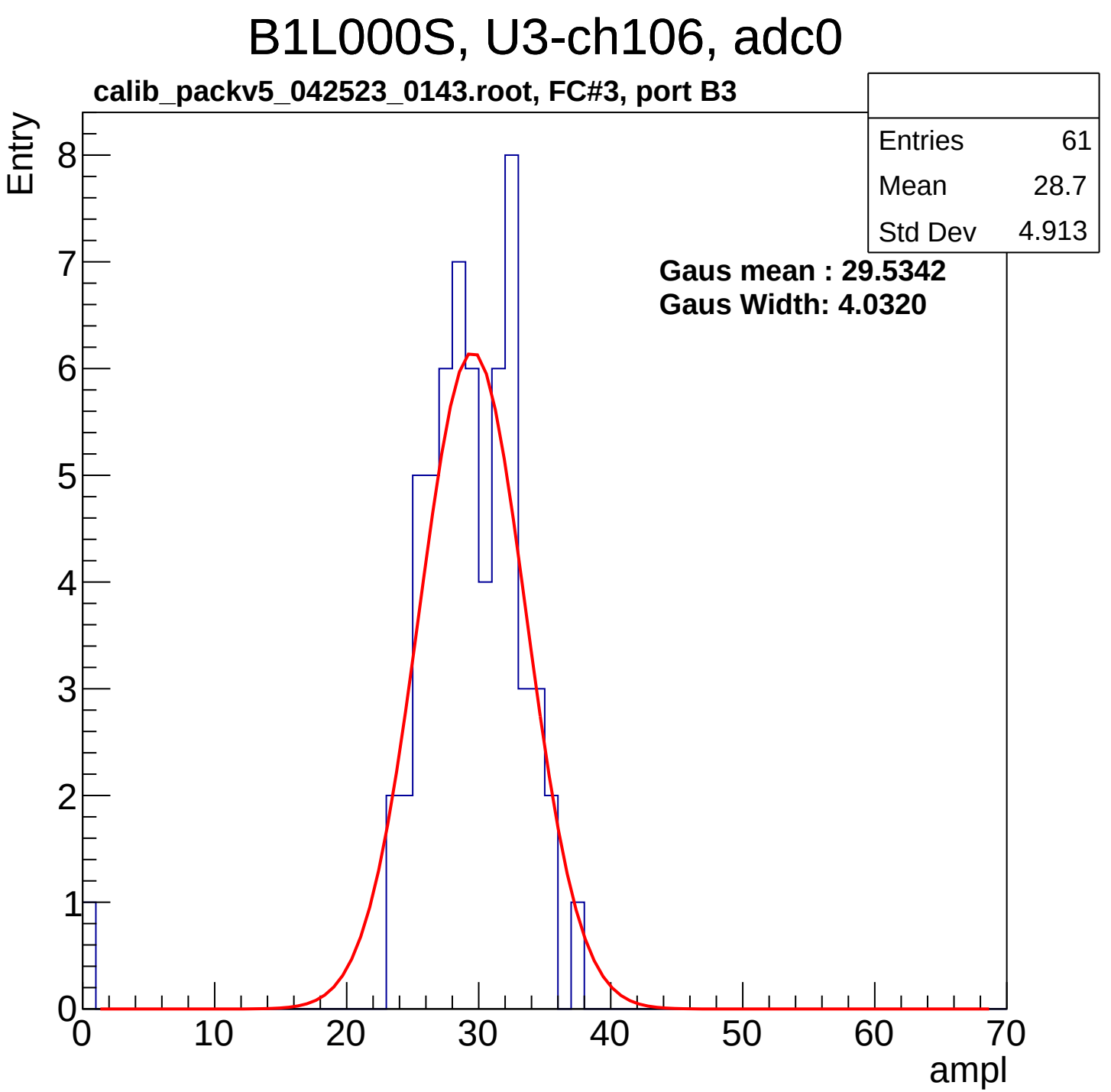
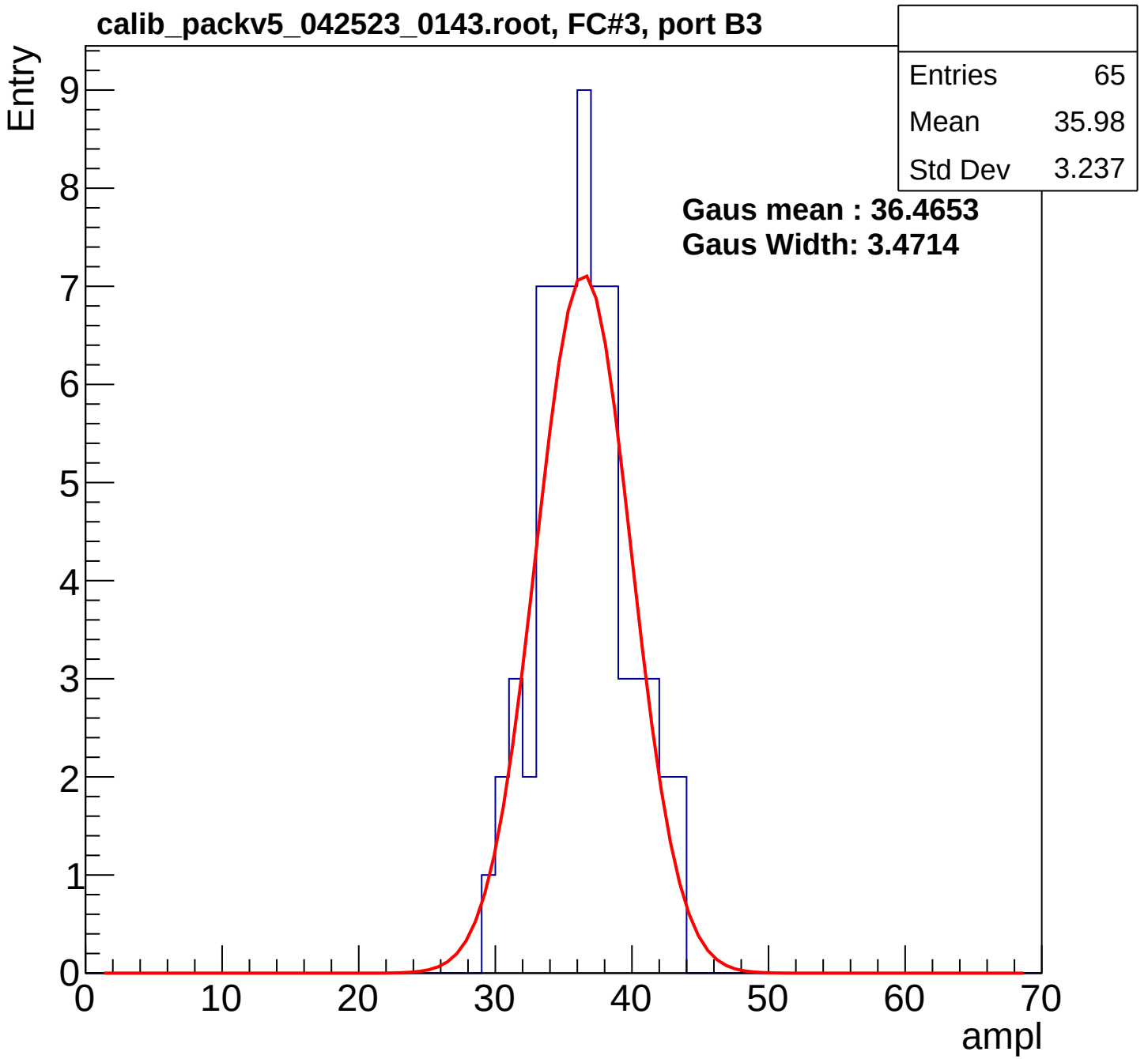
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch106, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 28.7
Std Dev 4.913
Gaus mean : 29.5342
Gaus Width: 4.0320
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch106, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.98
Std Dev 3.237
Gaus mean : 36.4653
Gaus Width: 3.4714
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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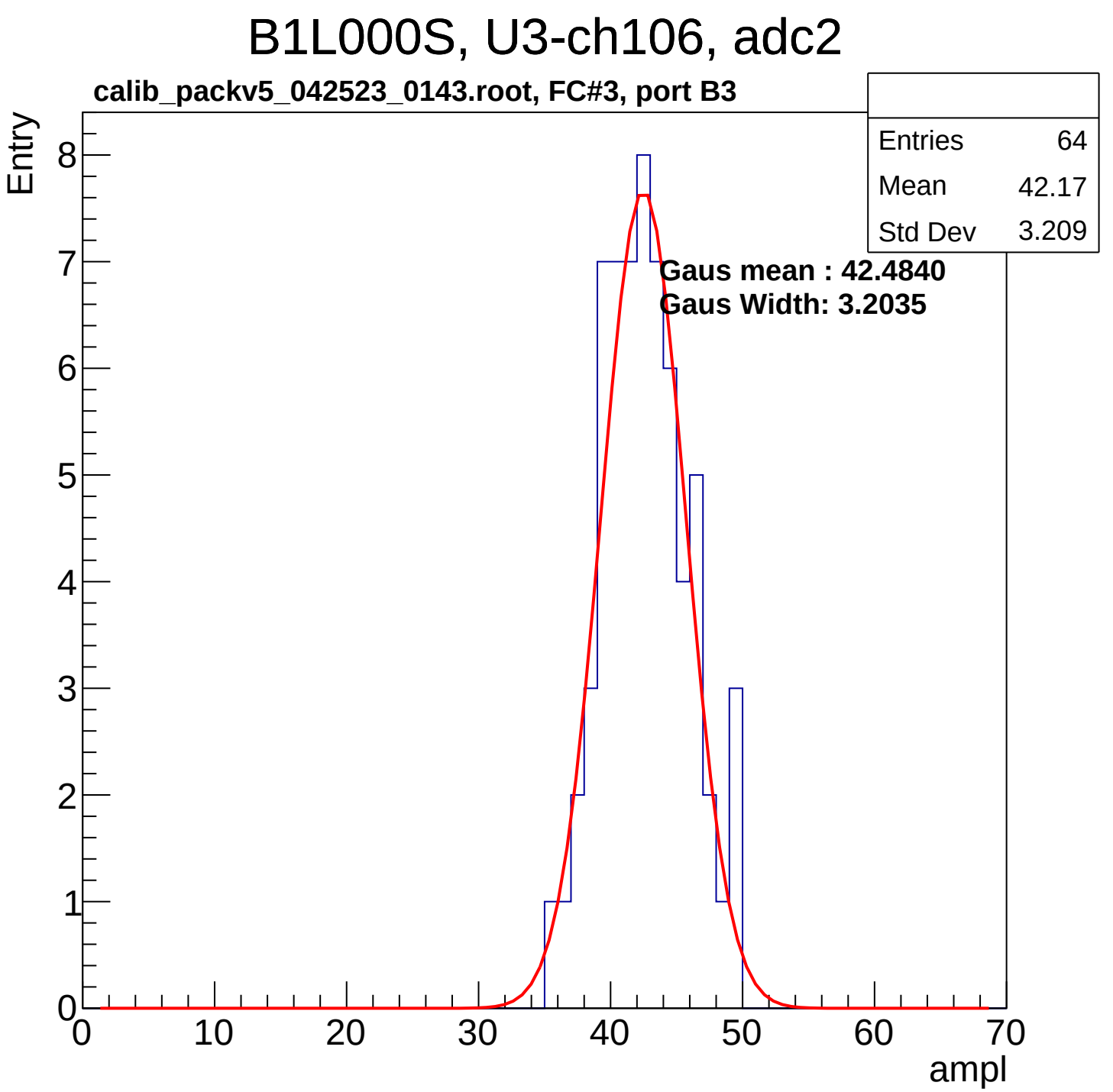
B1L000S, U3-ch106, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42.17
Std Dev 3.209
Gaus mean : 42.4840
Gaus Width: 3.2035
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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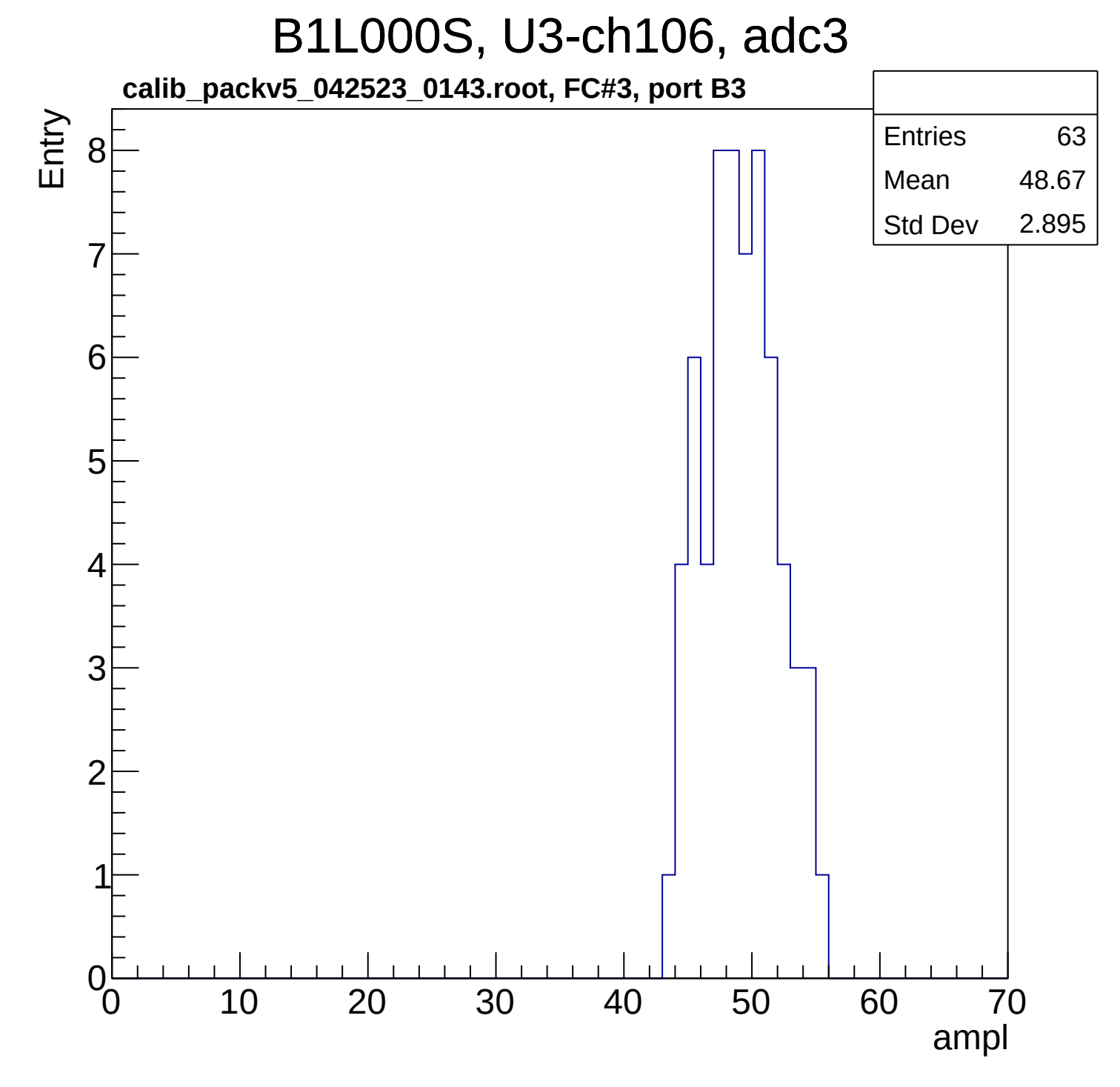
B1L000S, U3-ch106, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 48.67
Std Dev 2.895
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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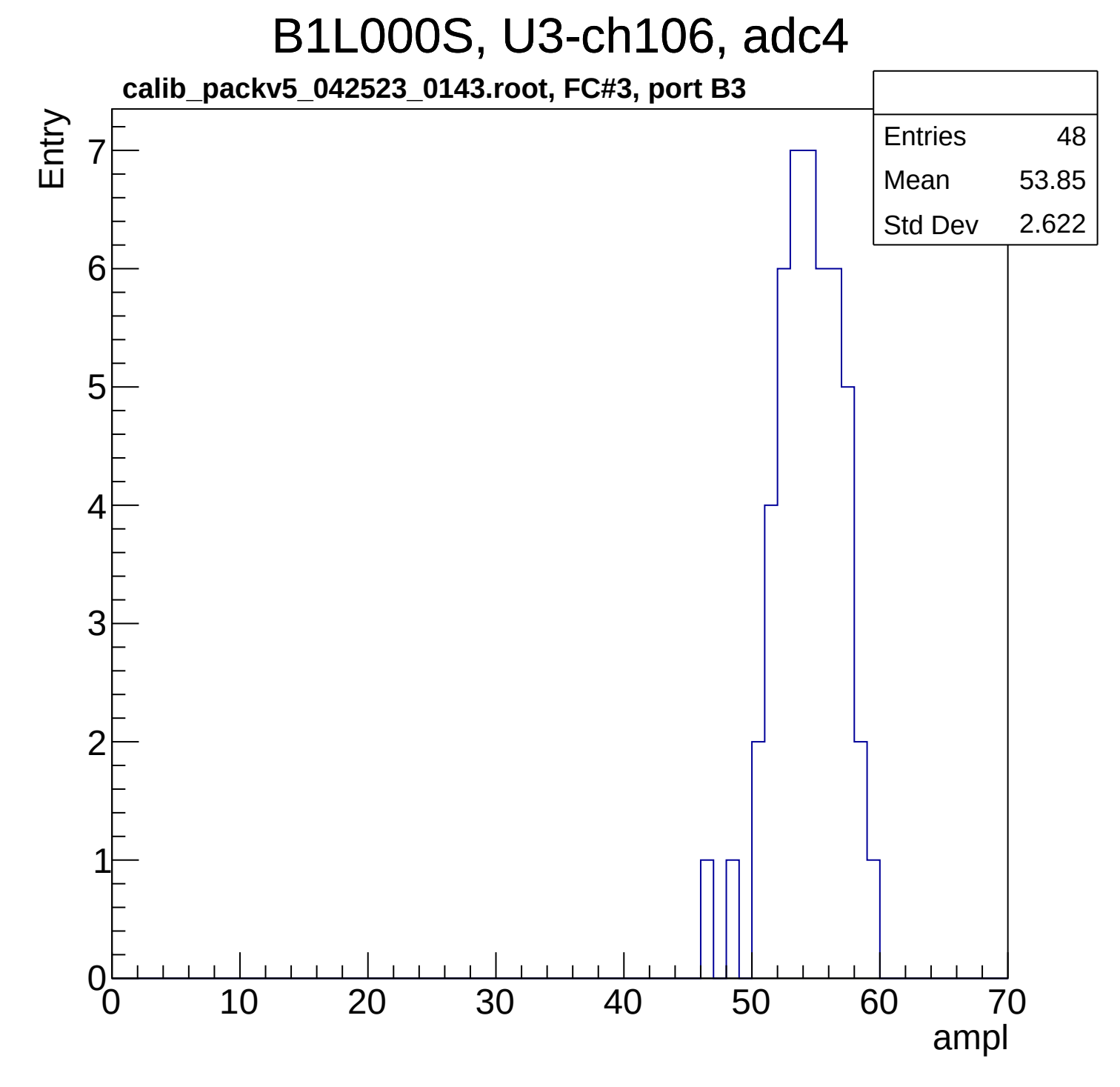
B1L000S, U3-ch106, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 53.85
Std Dev 2.622
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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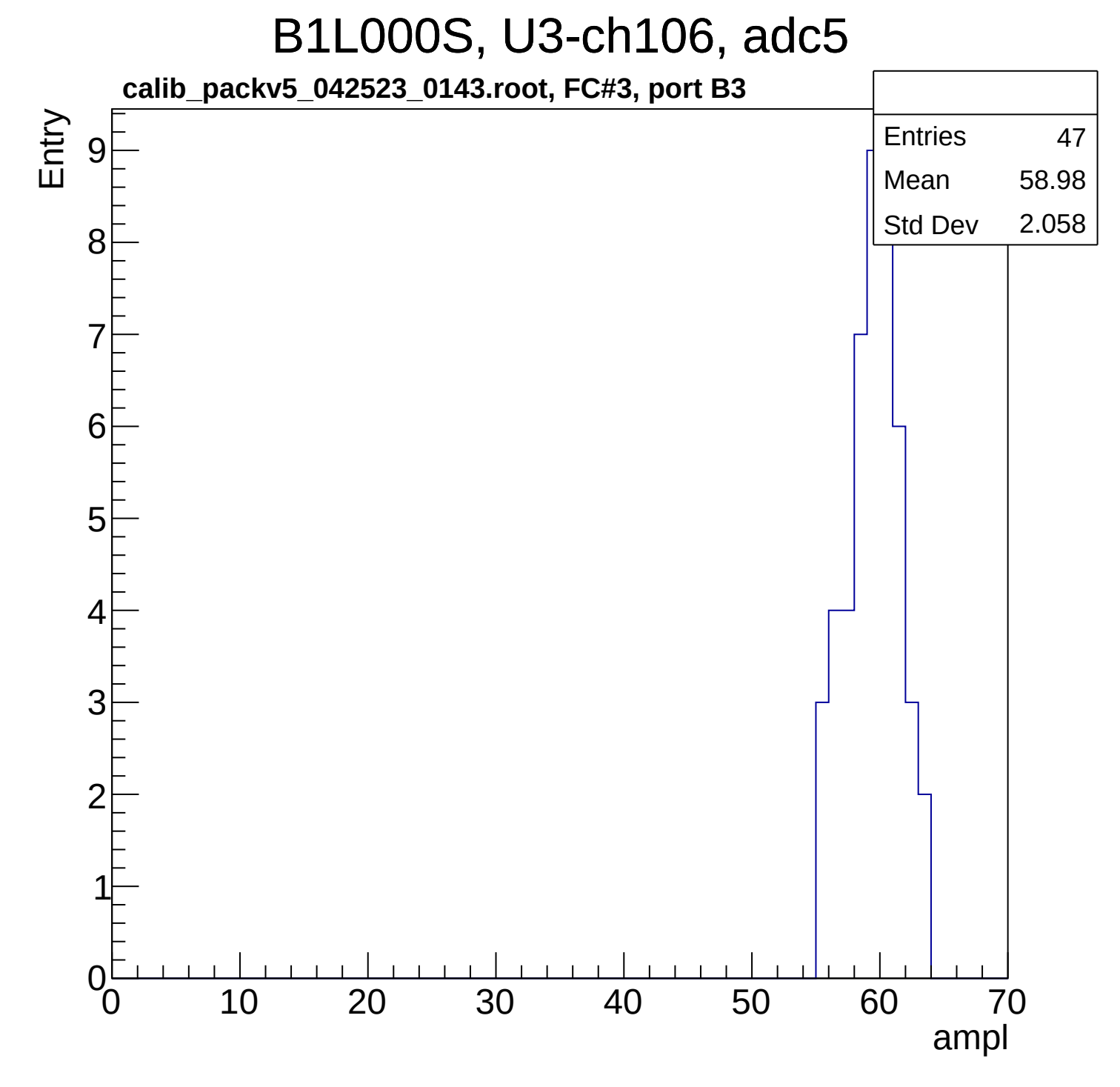
40
50
60
70


B1L000S, U3-ch106, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 58.98
Std Dev 2.058
Entry
ampl
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3
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5
6
7
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0
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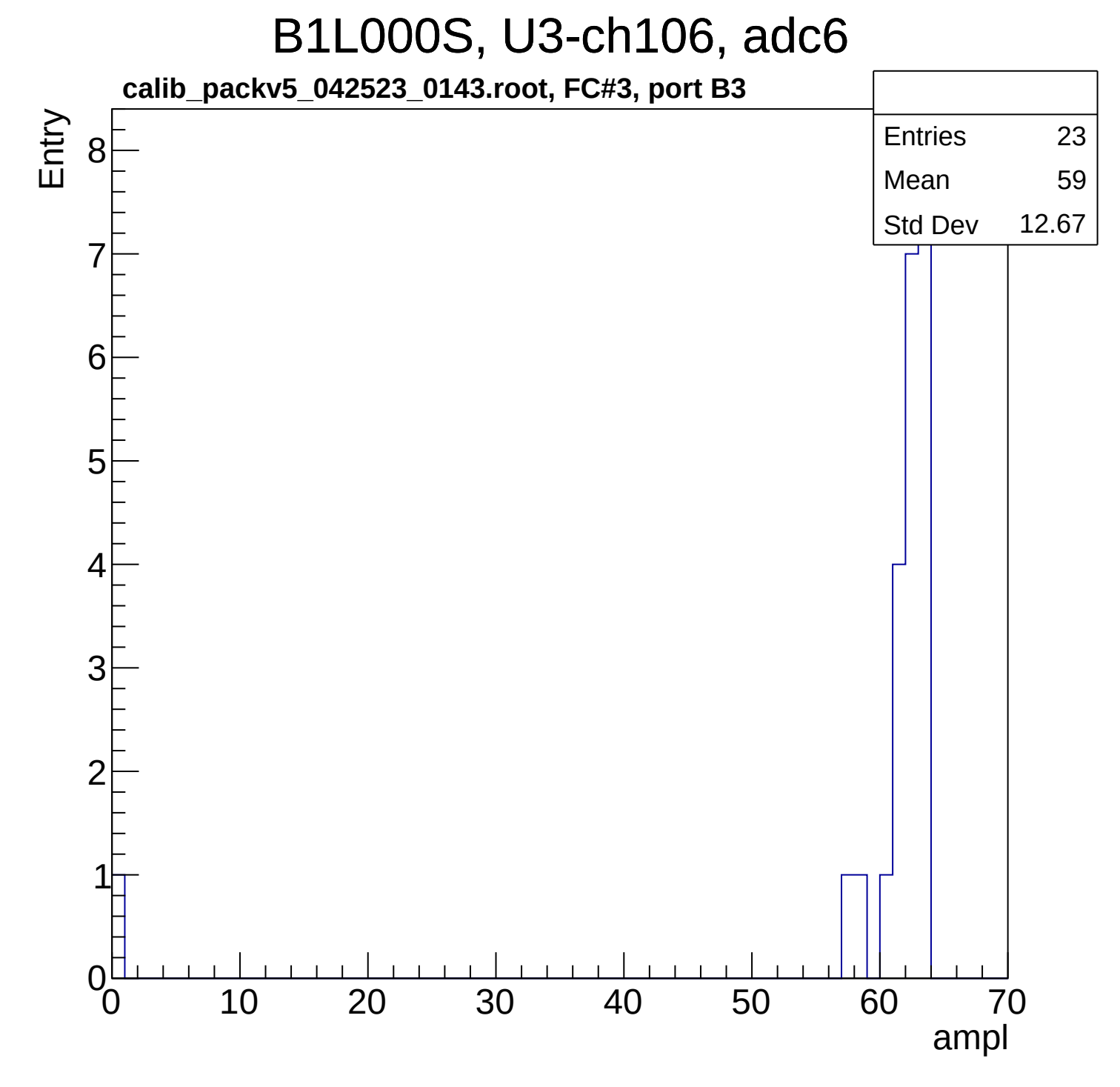
B1L000S, U3-ch106, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 59
Std Dev 12.67
Entry
ampl
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4
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8
0
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20
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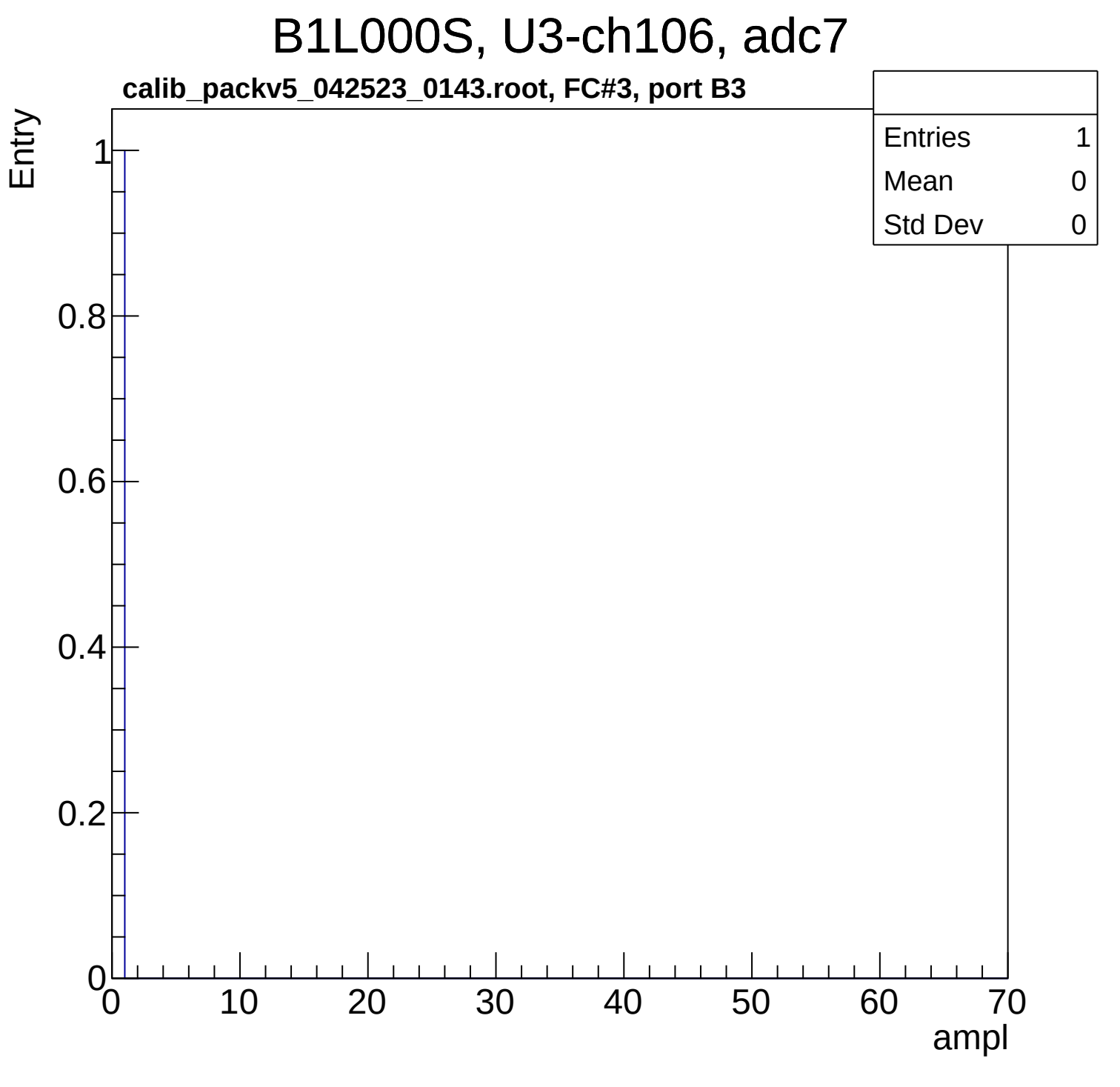
B1L000S, U3-ch106, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
20
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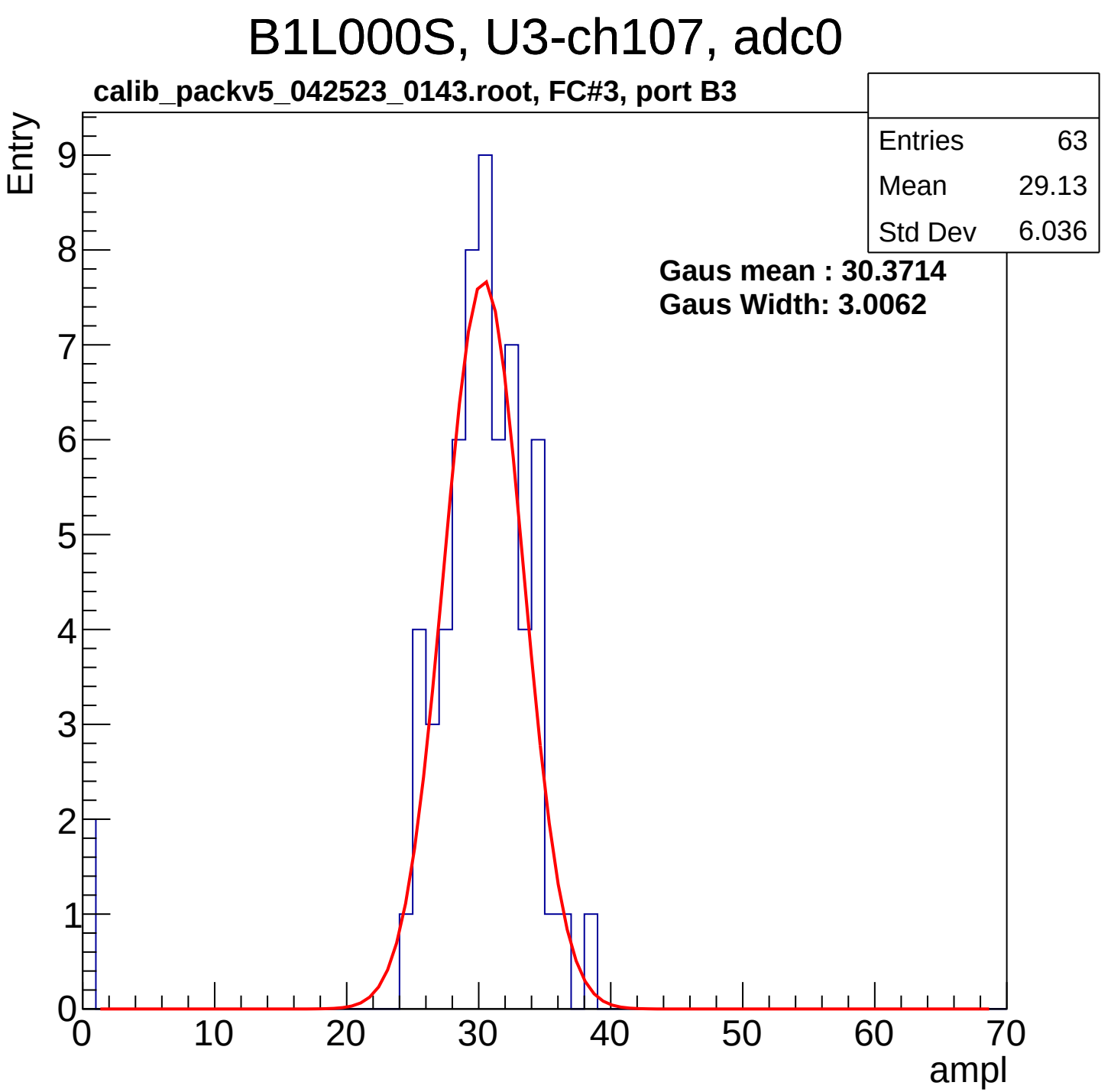
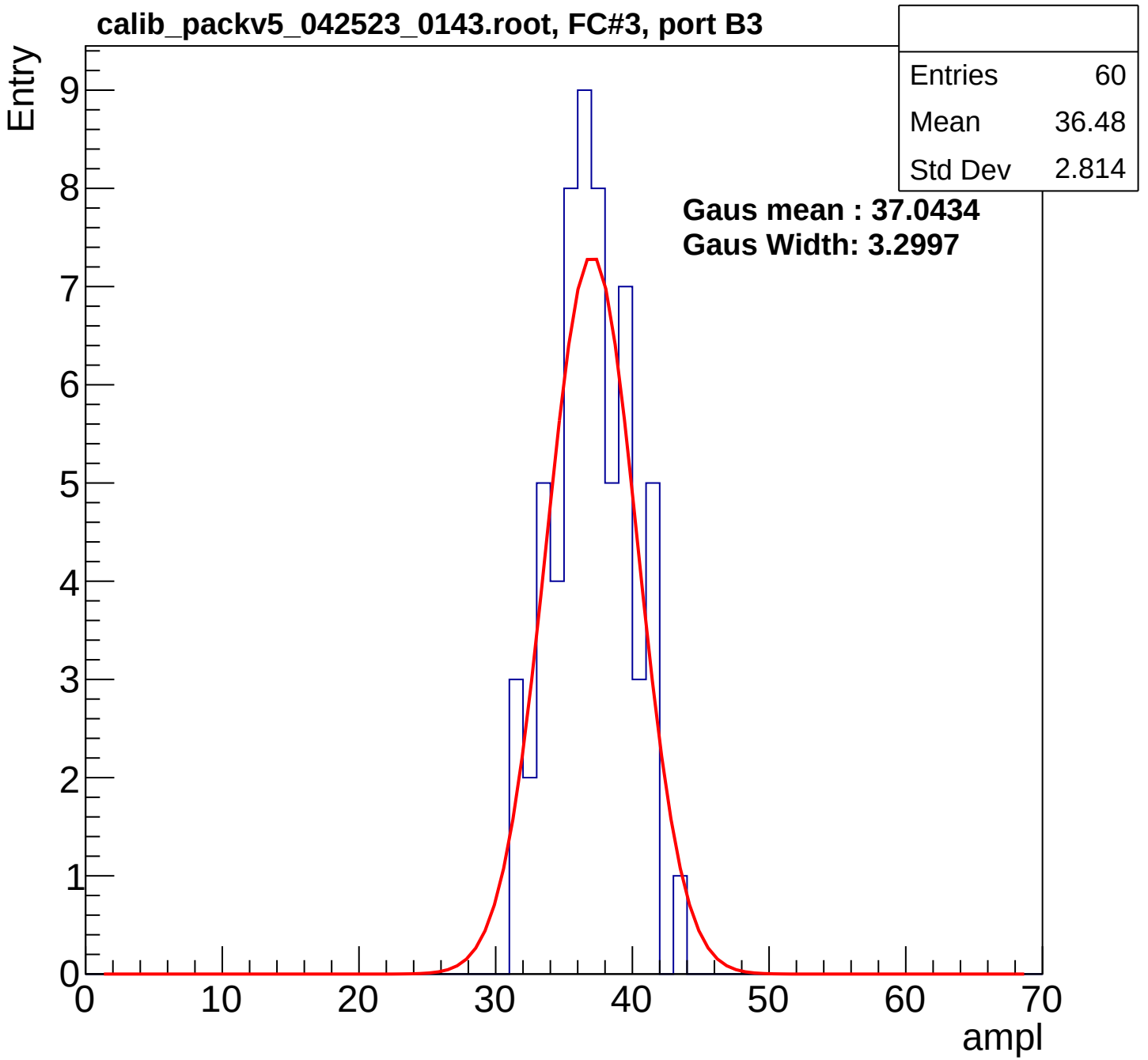
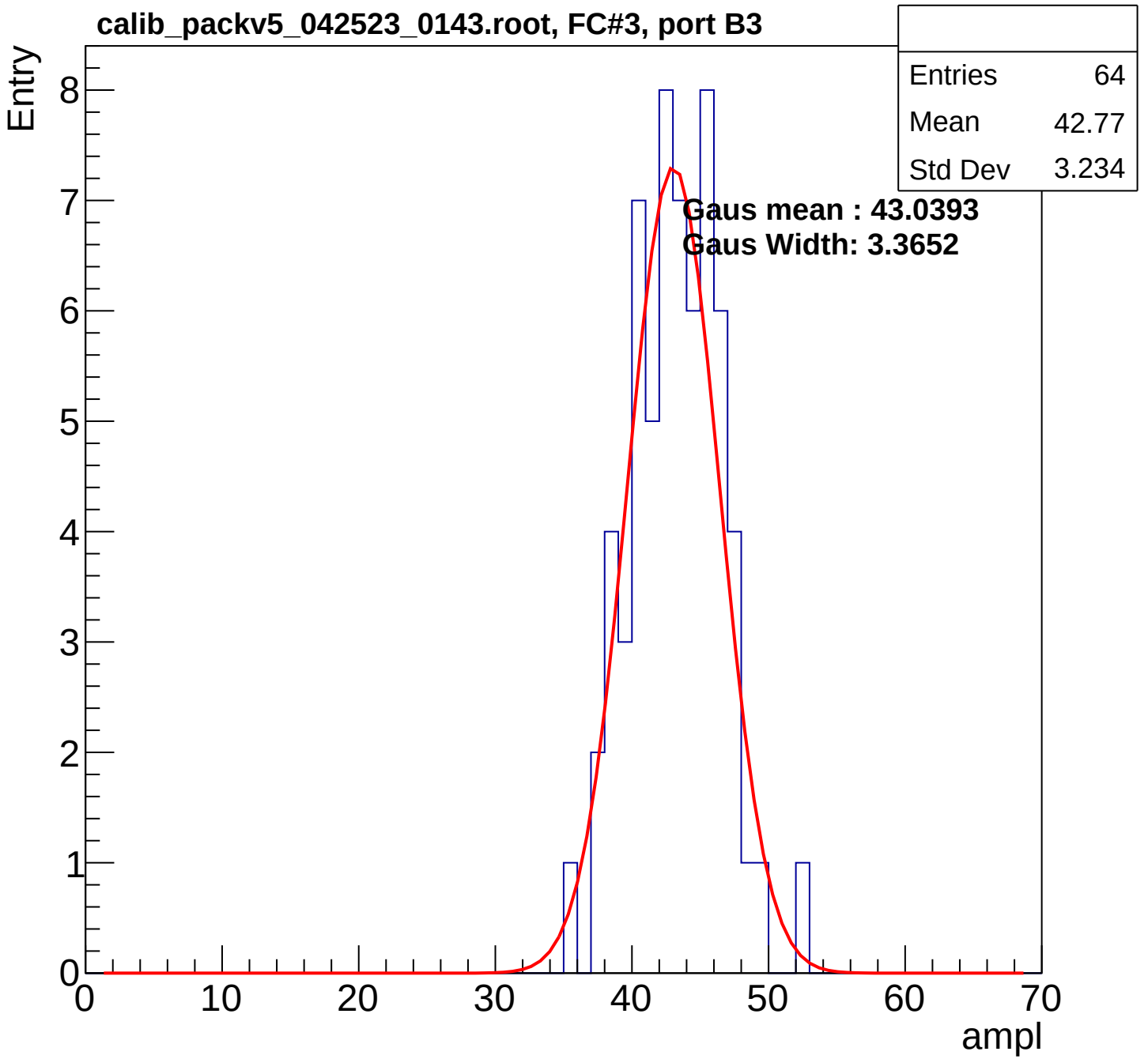
60
70
ampl


B1L000S, U3-ch107, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 29.13
Std Dev 6.036
Gaus mean : 30.3714
Gaus Width: 3.0062
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch107, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 36.48
Std Dev 2.814
Gaus mean : 37.0434
Gaus Width: 3.2997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch107, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42.77
Std Dev 3.234
Gaus mean : 43.0393
Gaus Width: 3.3652
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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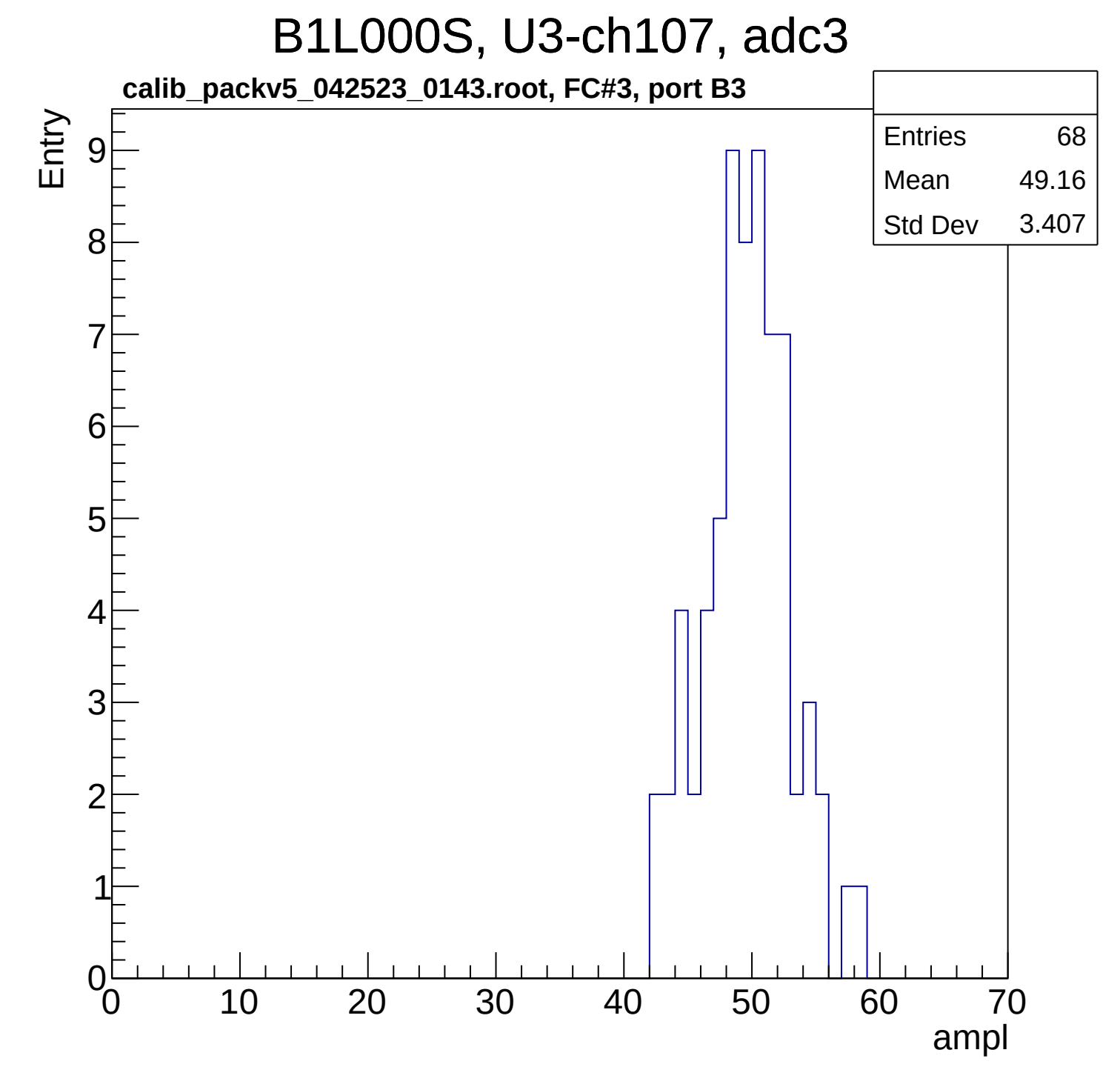
B1L000S, U3-ch107, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 49.16
Std Dev 3.407
Entry
ampl
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2
3
4
5
6
7
8
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0
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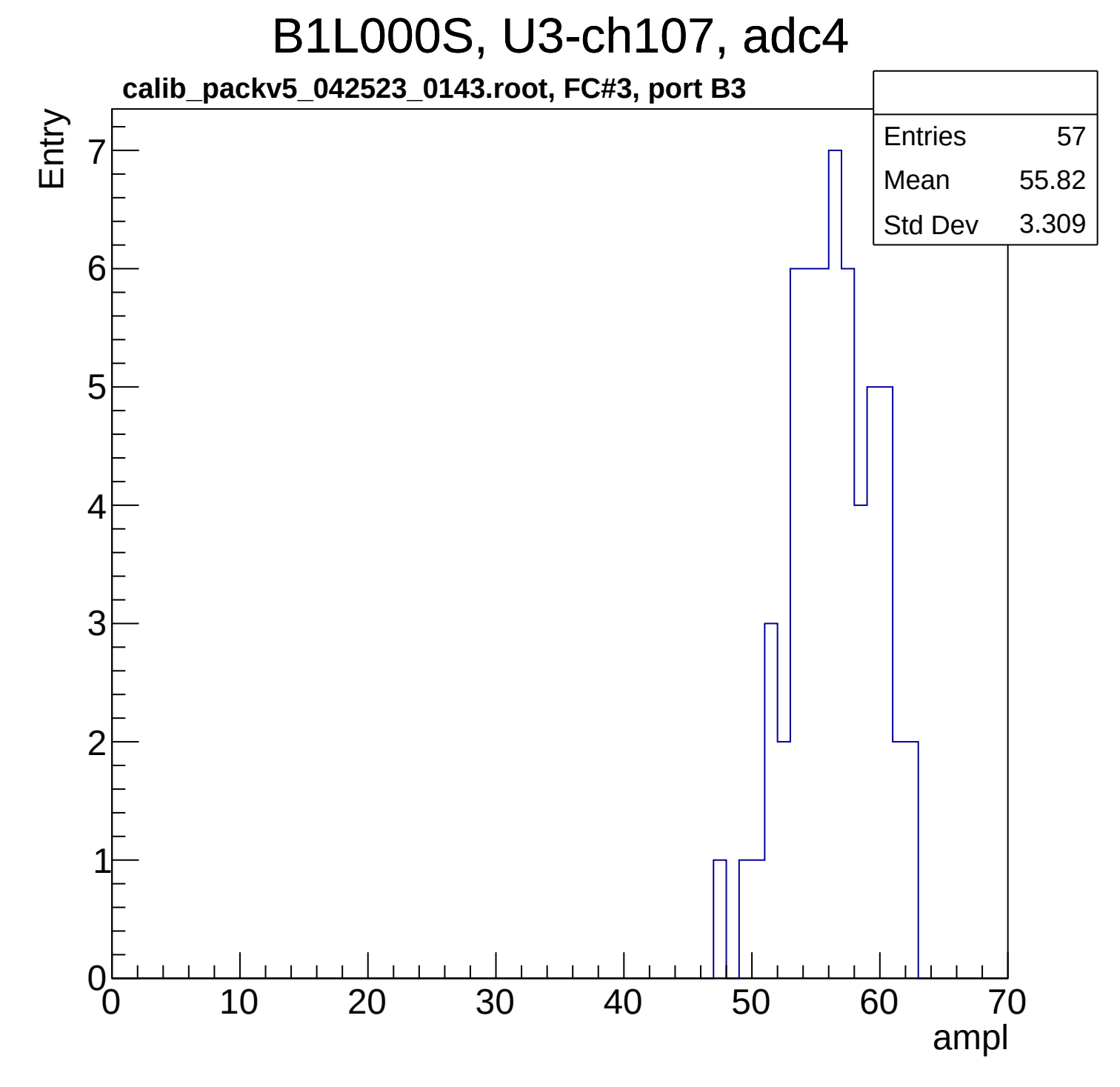
B1L000S, U3-ch107, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 55.82
Std Dev 3.309
Entry
ampl
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4
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6
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0
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60
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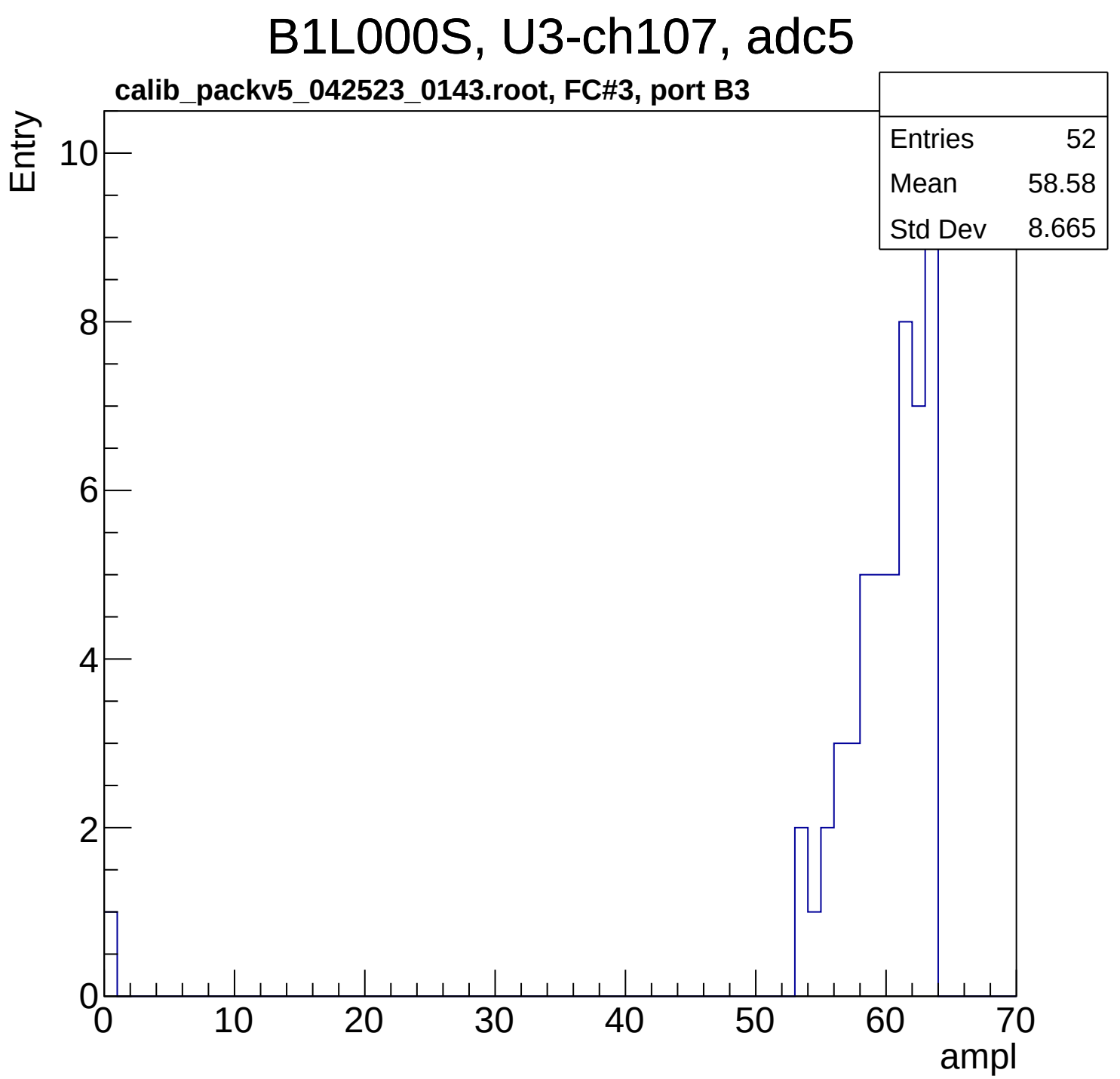
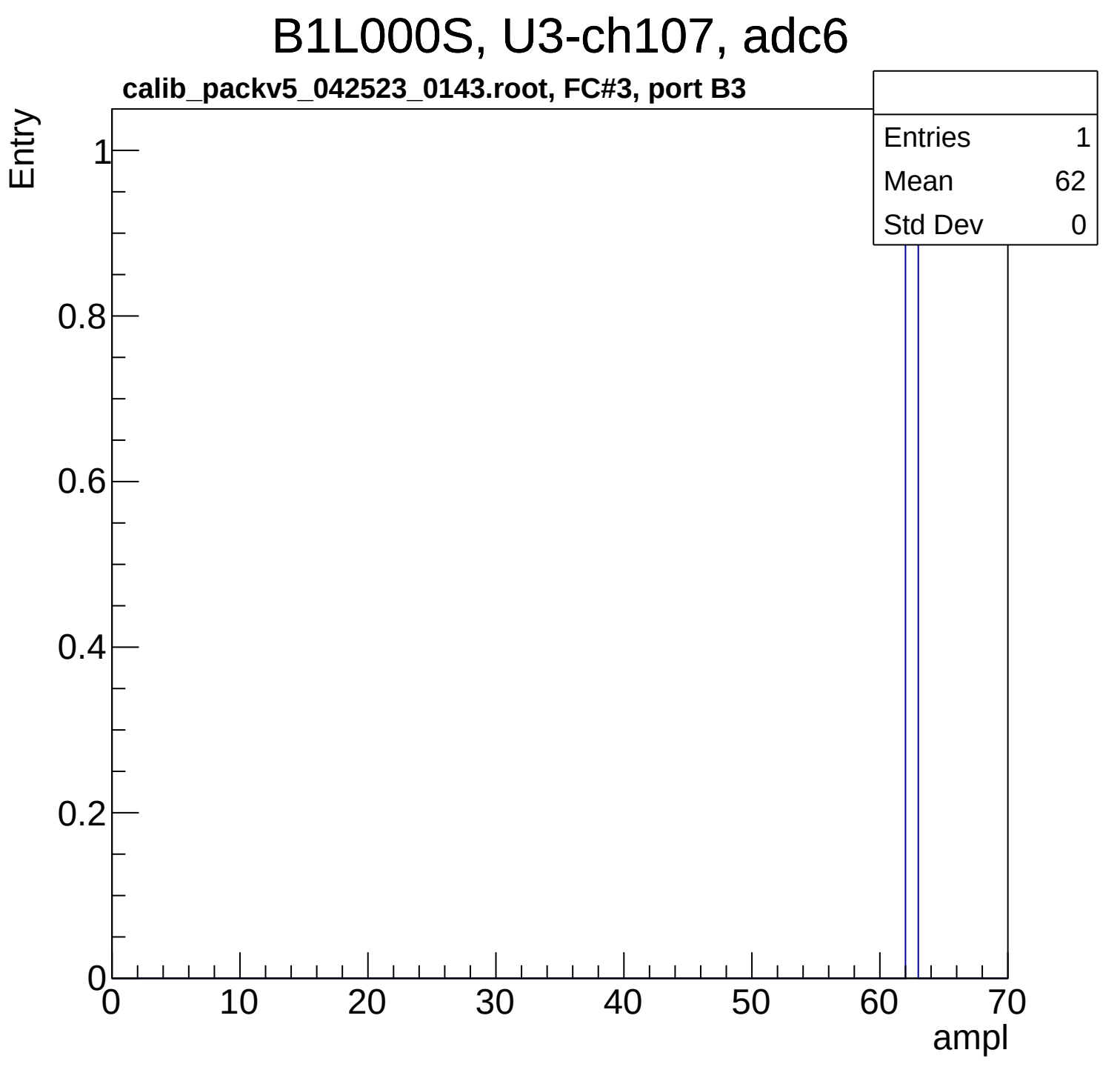

B1L000S, U3-ch107, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.58
Std Dev 8.665
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch107, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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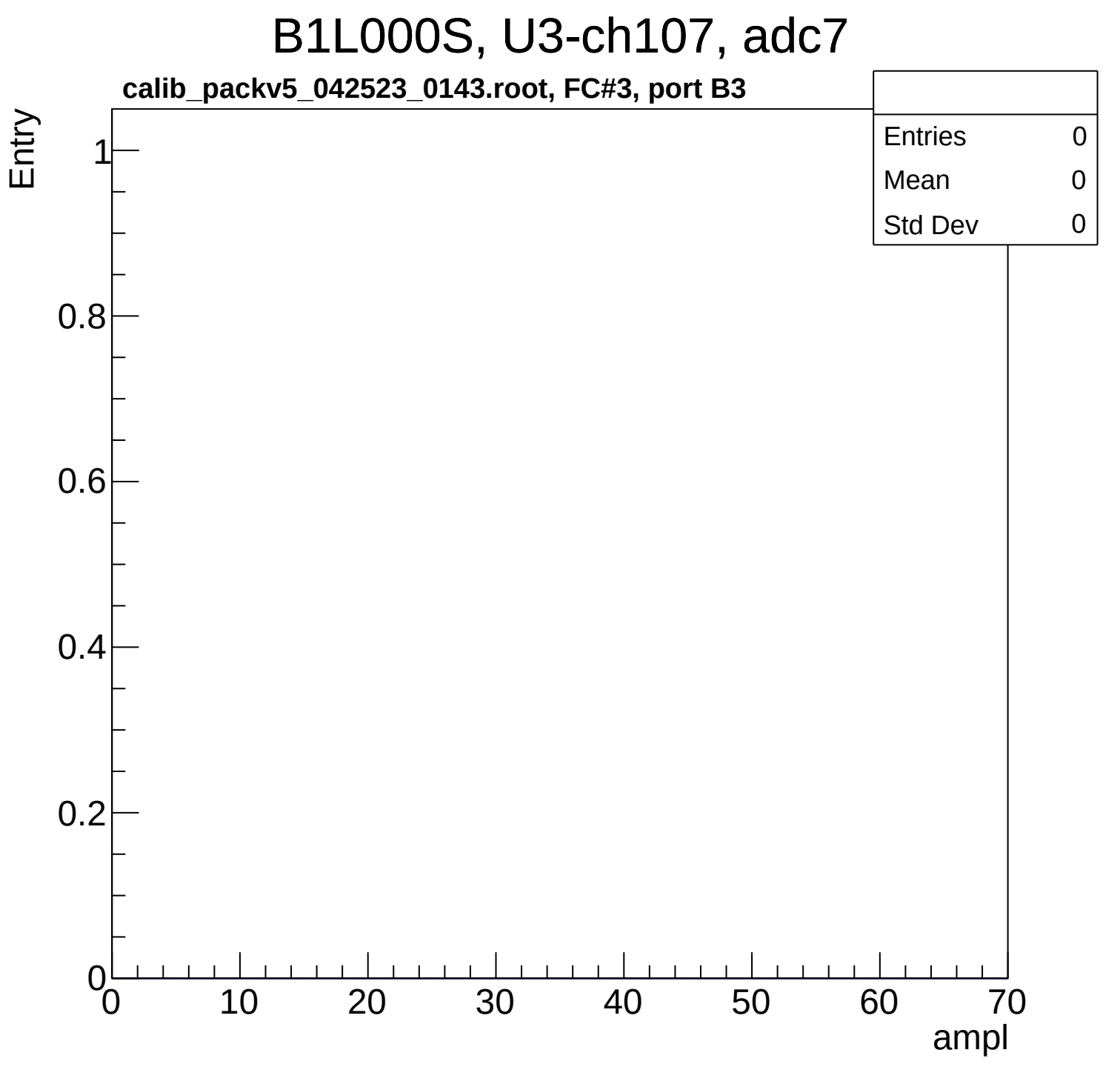
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch107, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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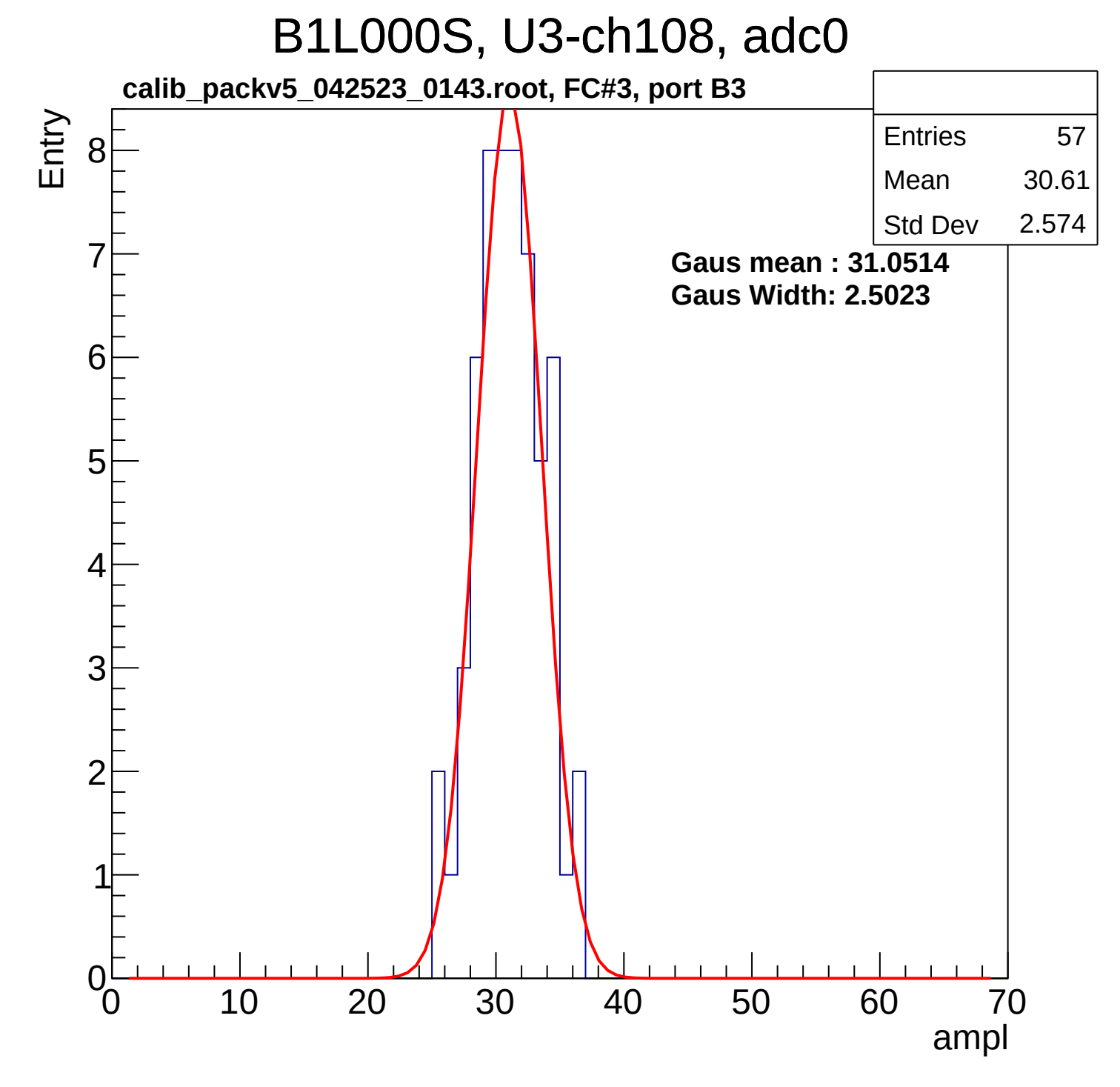
40
50
60
70
ampl


B1L000S, U3-ch108, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 30.61
Std Dev 2.574
Gaus mean : 31.0514
Gaus Width: 2.5023
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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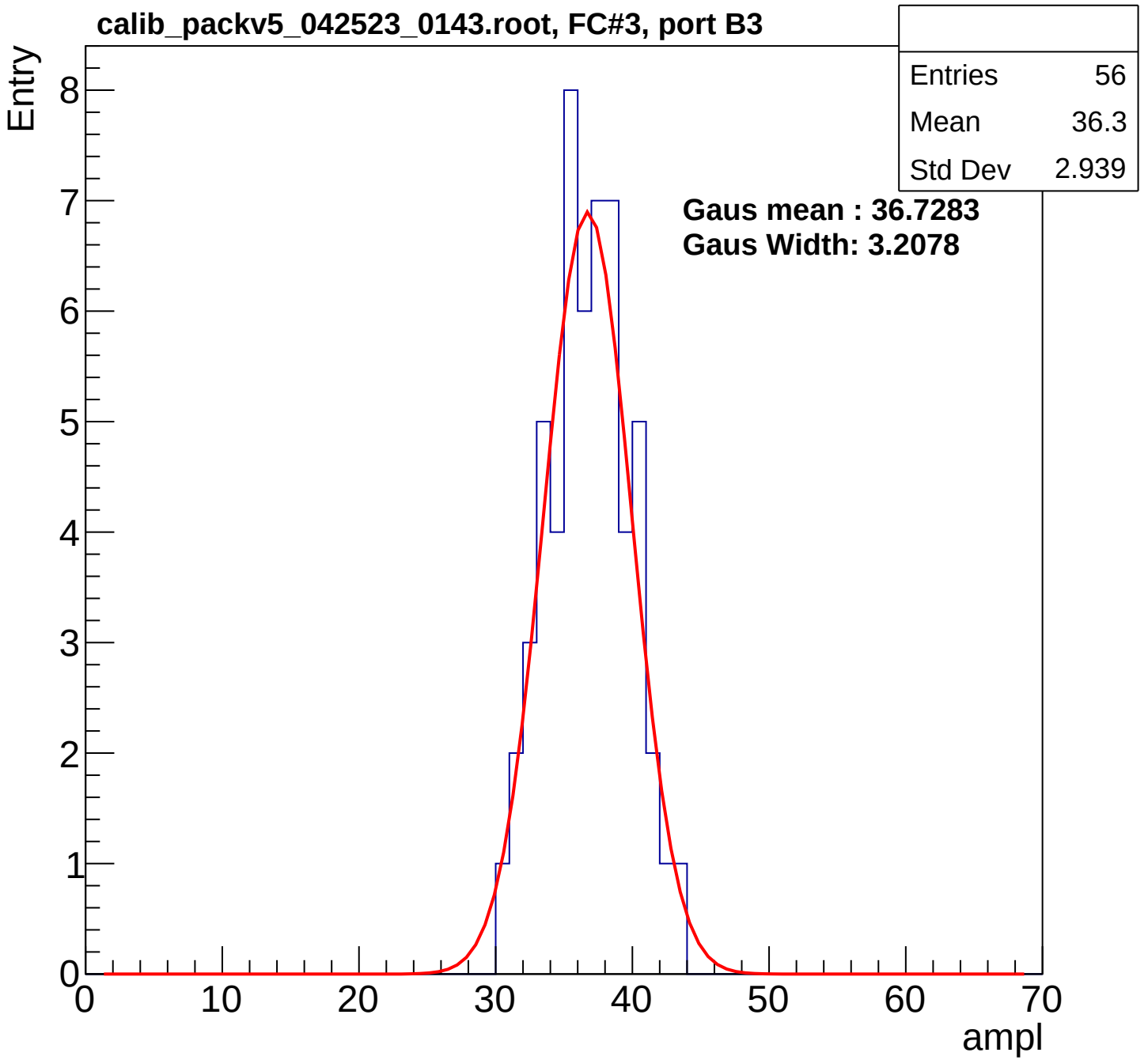
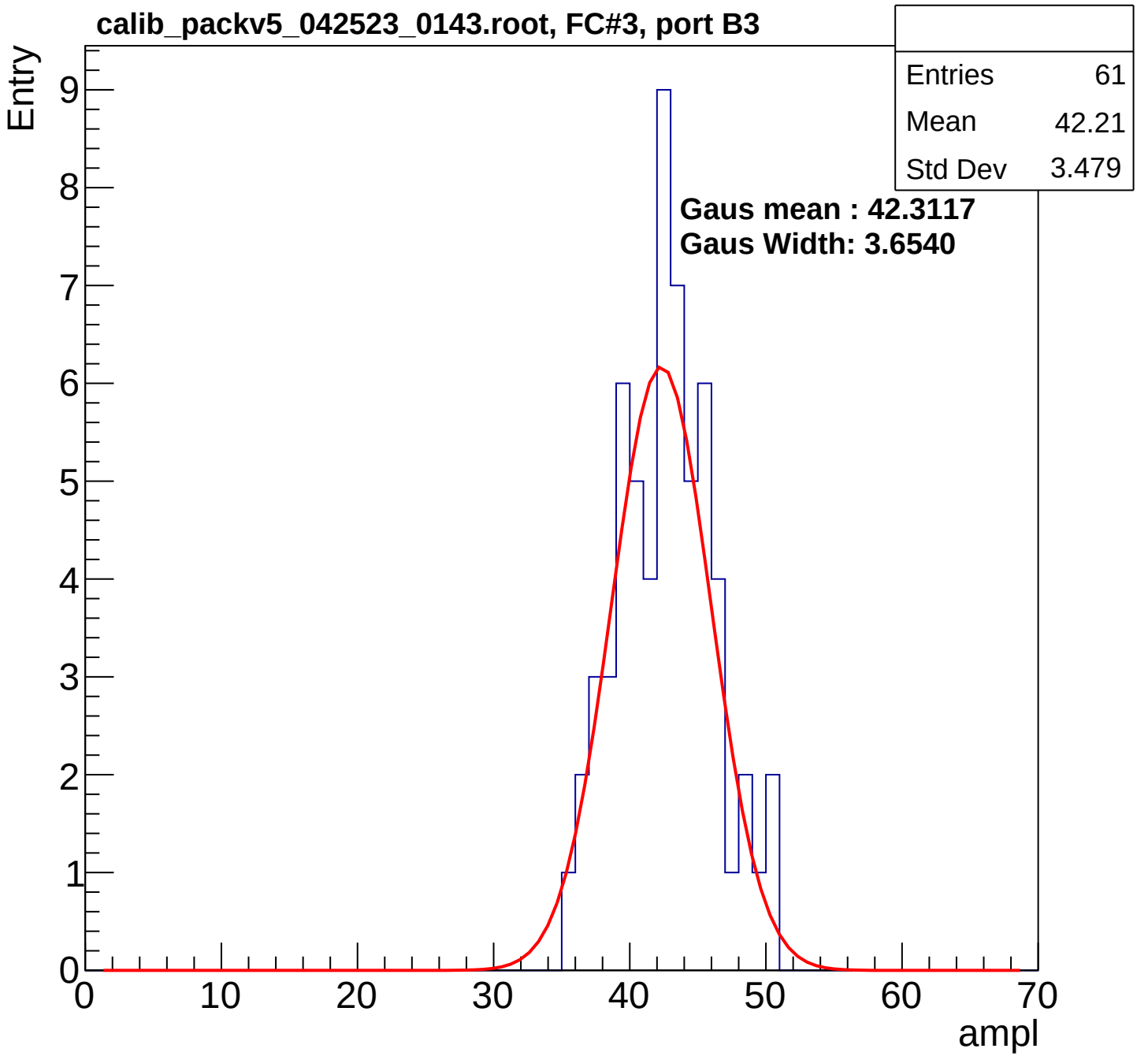

B1L000S, U3-ch108, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 36.3
Std Dev 2.939
Gaus mean : 36.7283
Gaus Width: 3.2078
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch108, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 42.21
Std Dev 3.479
Gaus mean : 42.3117
Gaus Width: 3.6540
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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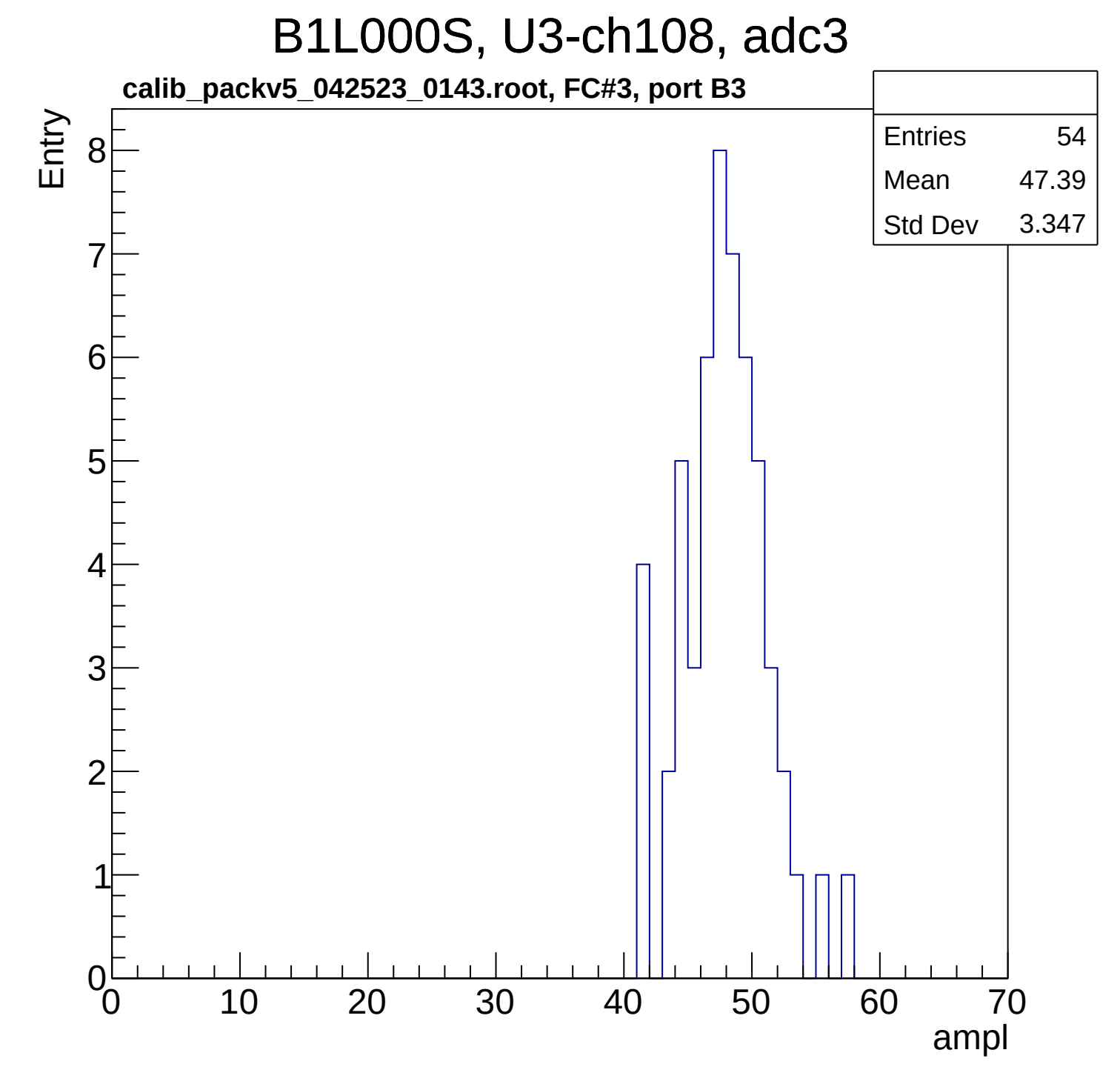
B1L000S, U3-ch108, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 47.39
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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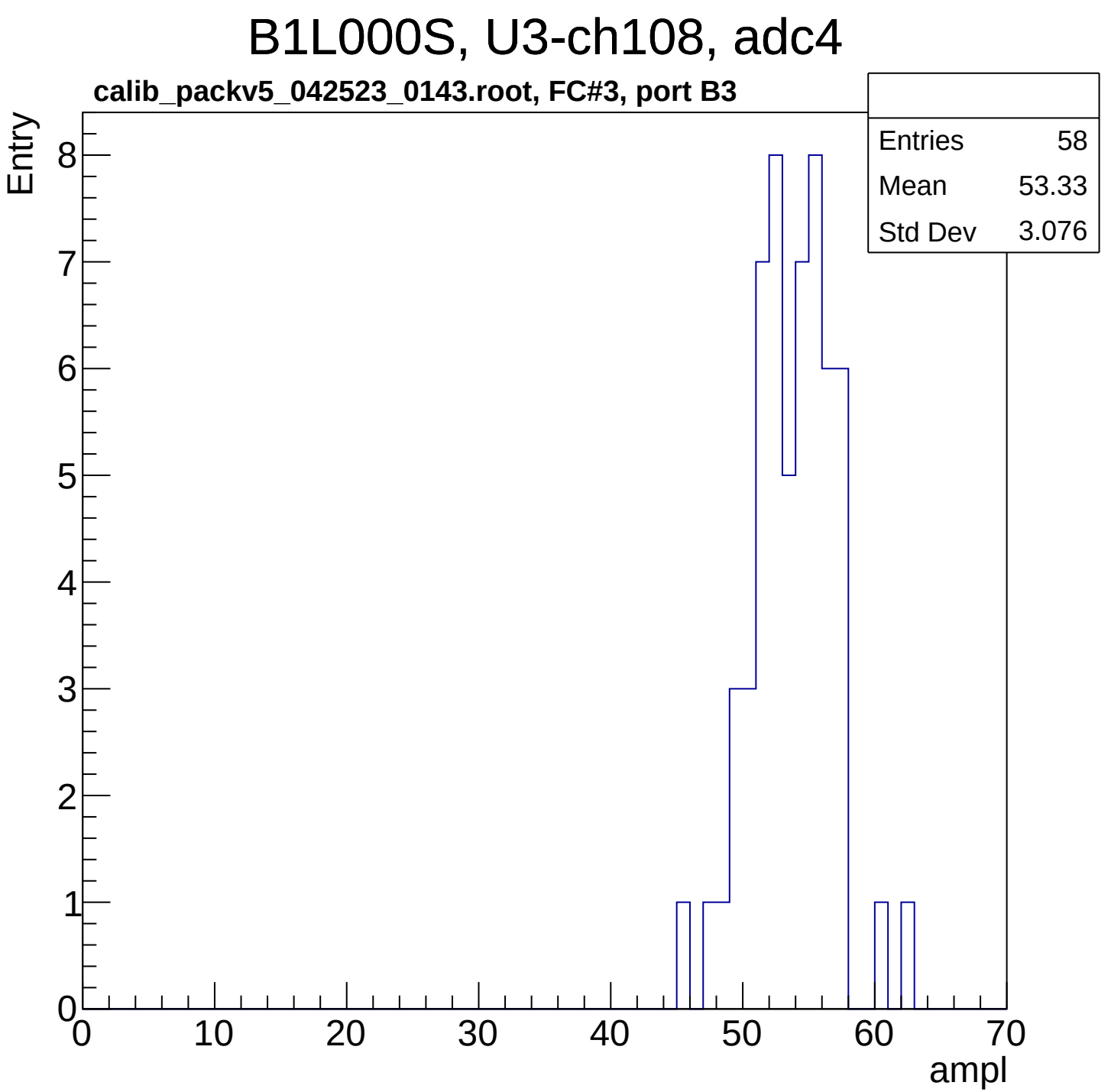
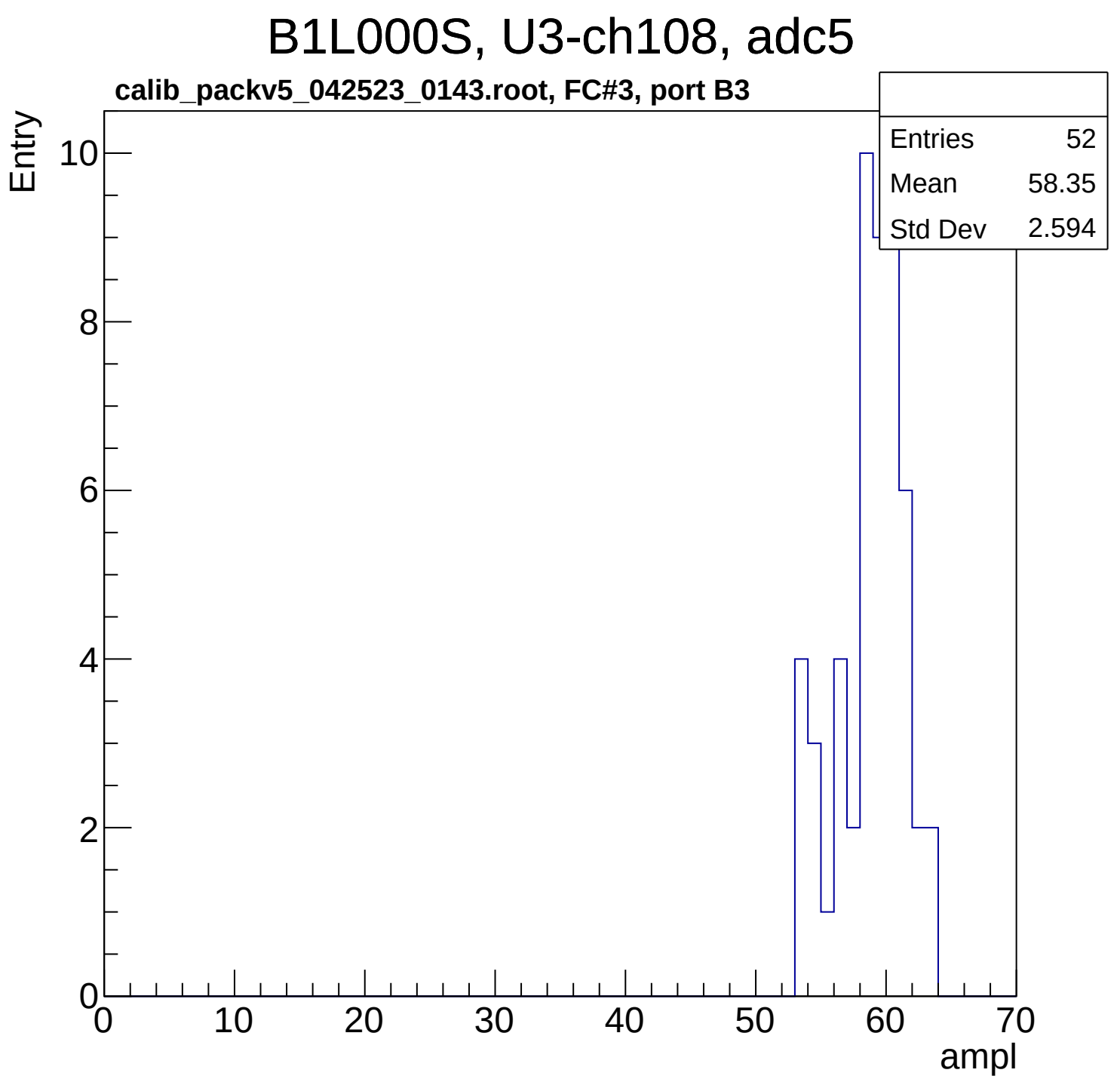
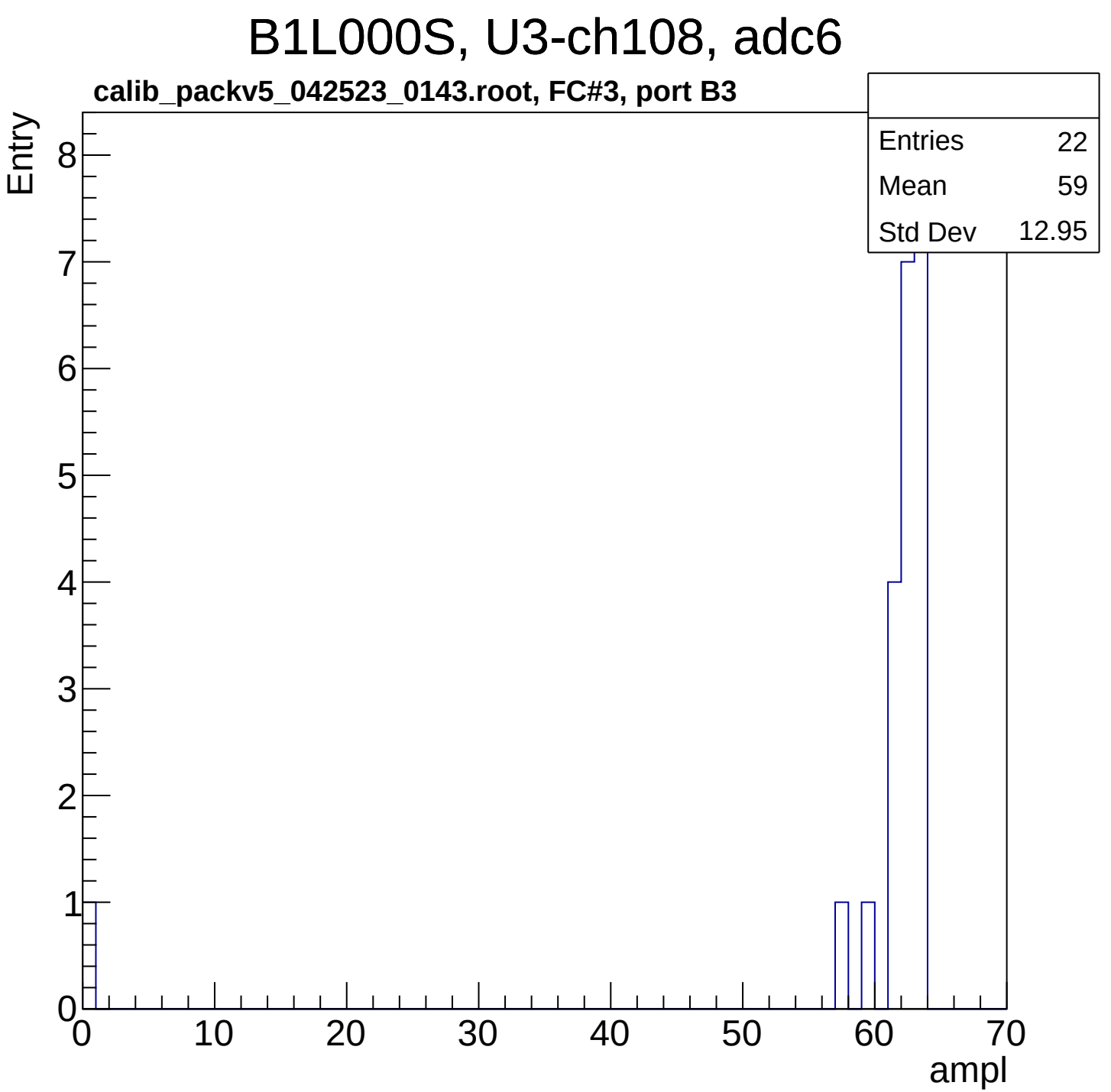
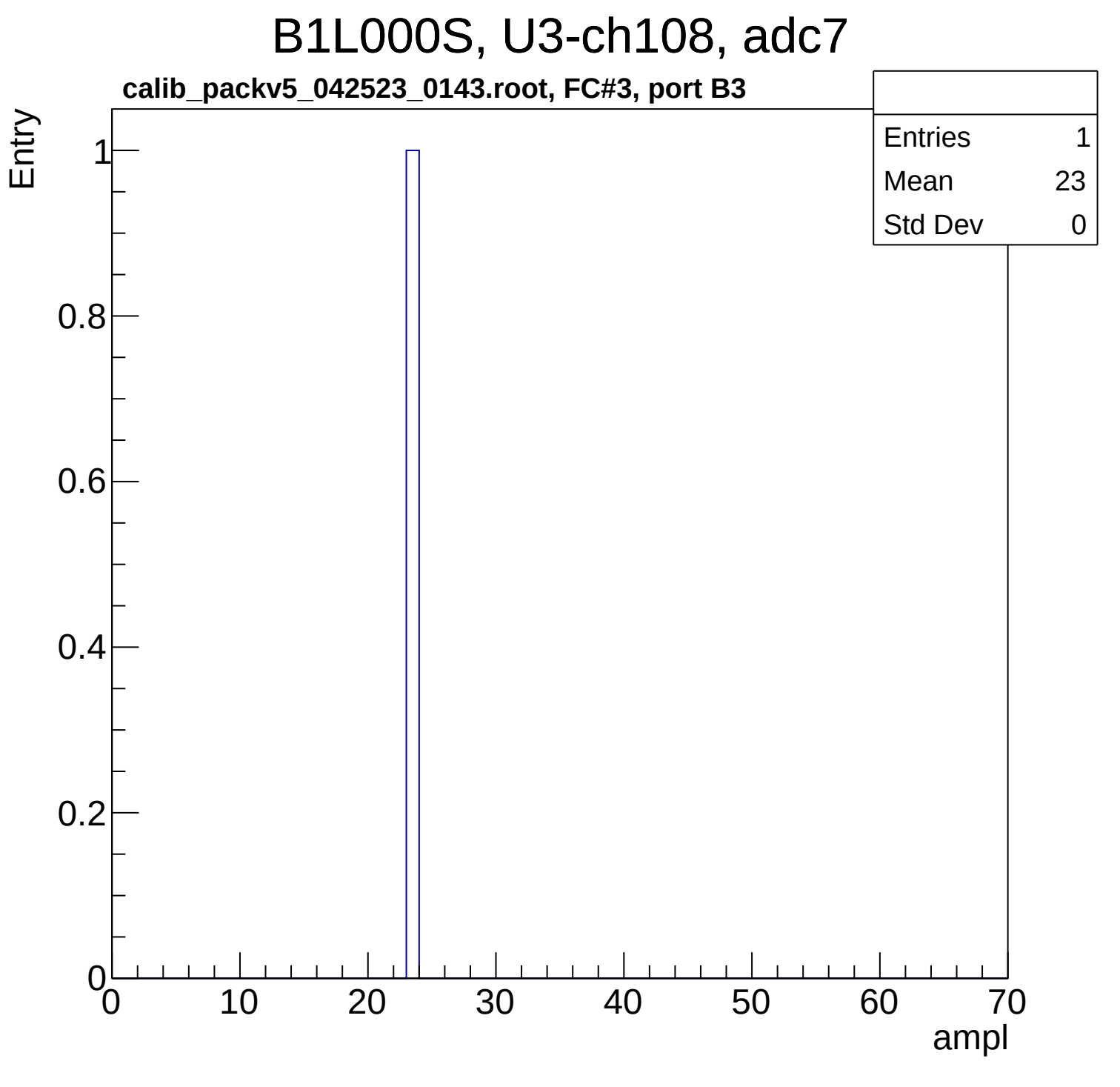
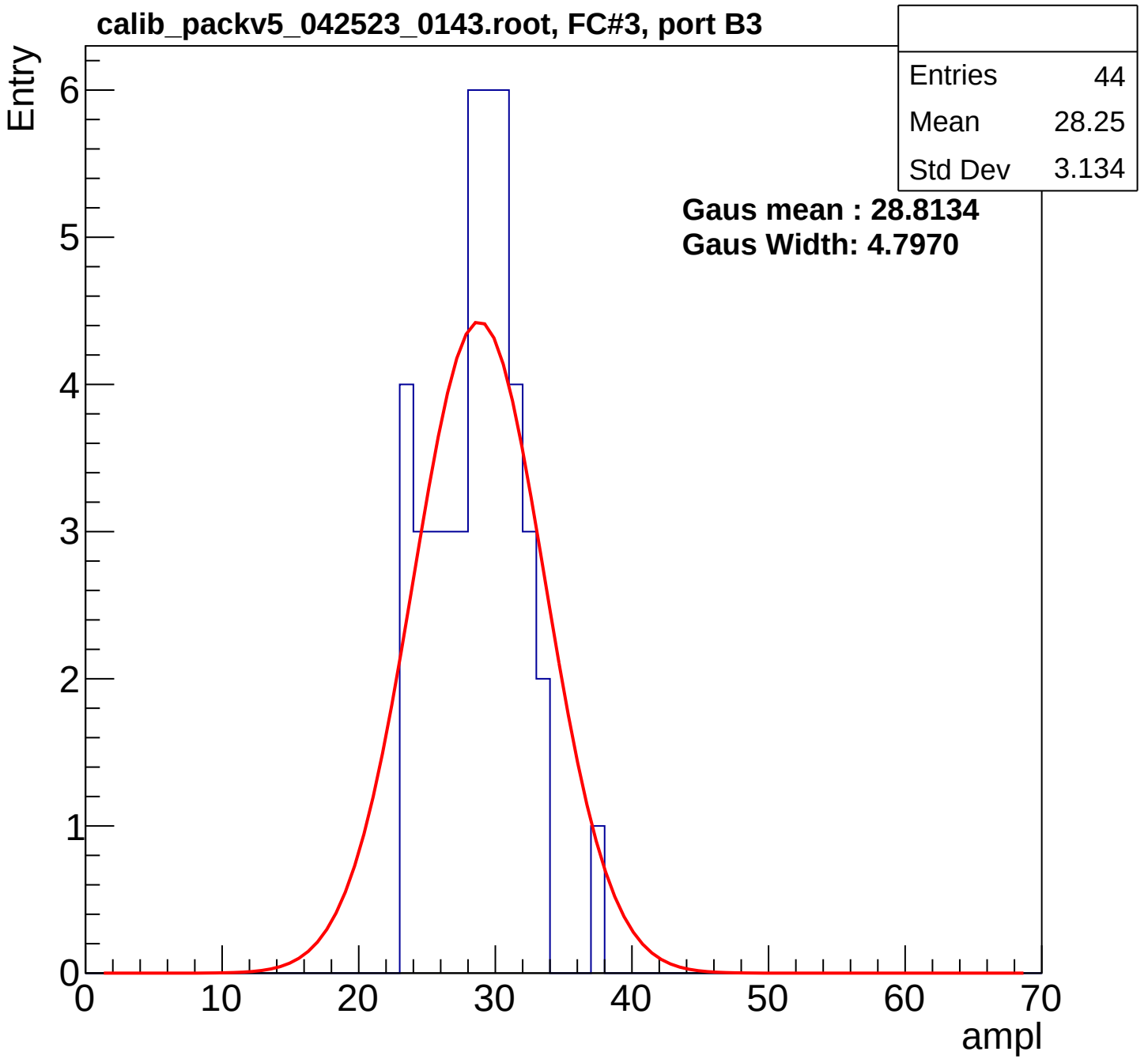
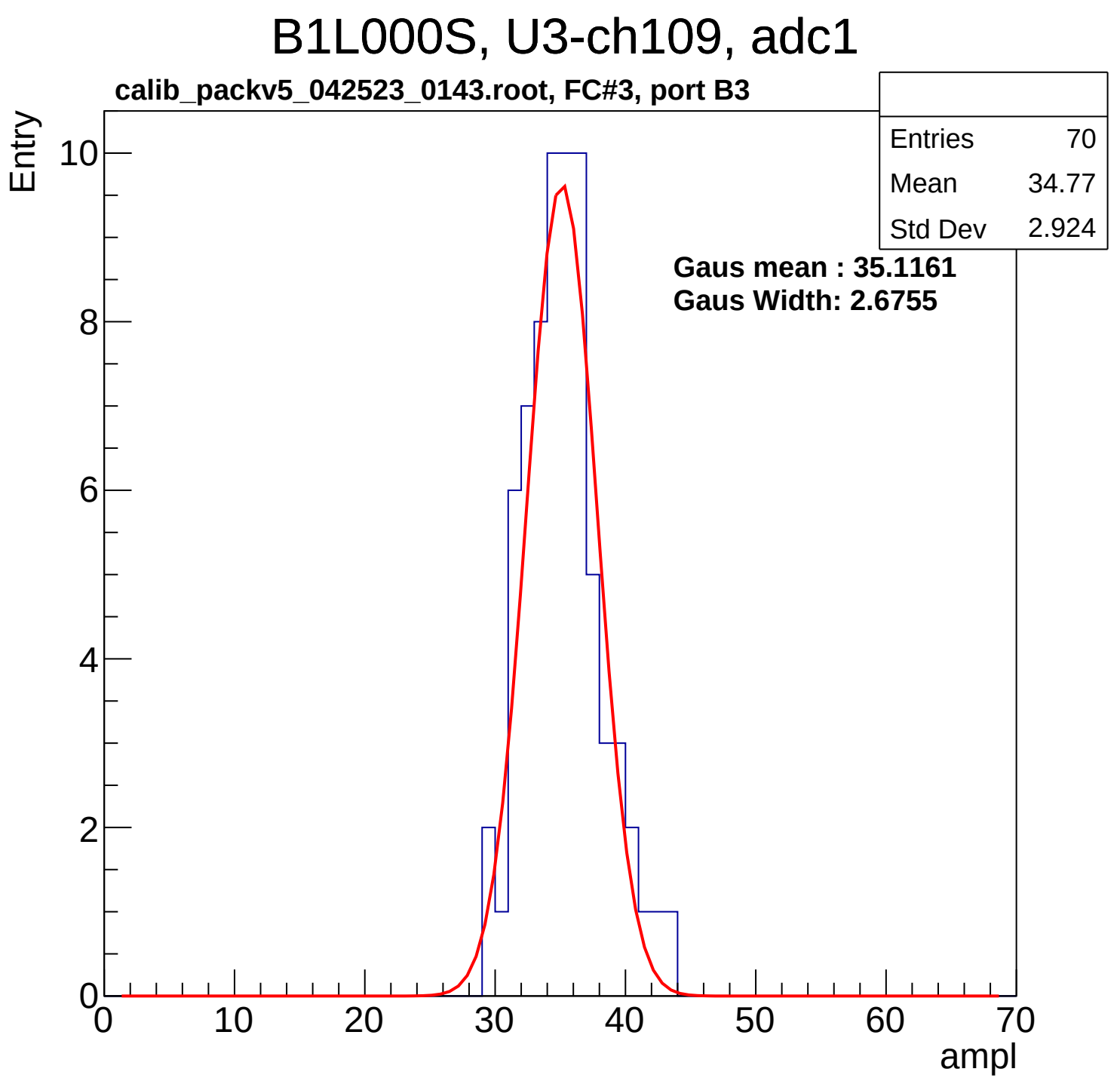

B1L000S, U3-ch108, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 53.33
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

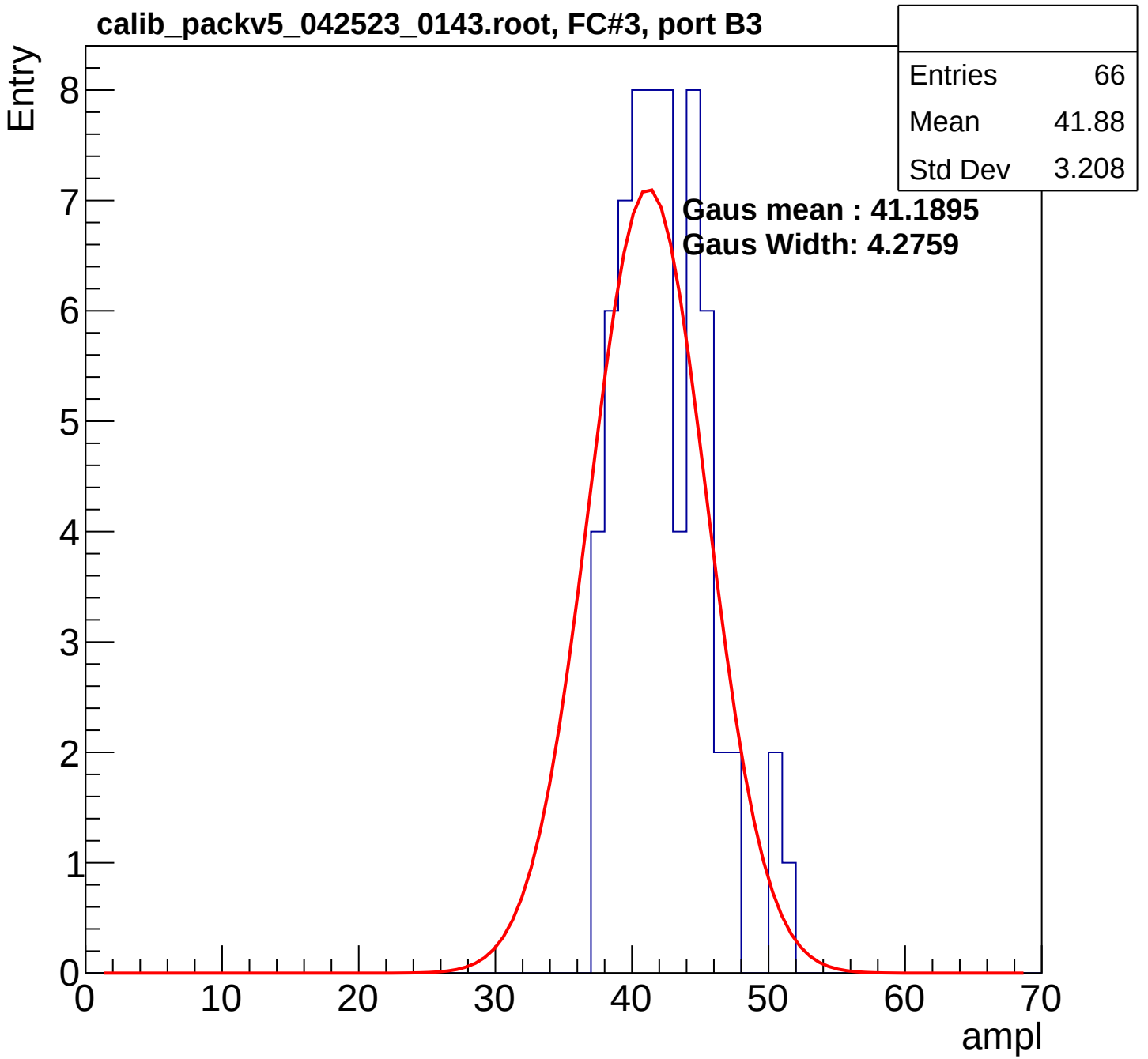

B1L000S, U3-ch108, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.35
Std Dev 2.594
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

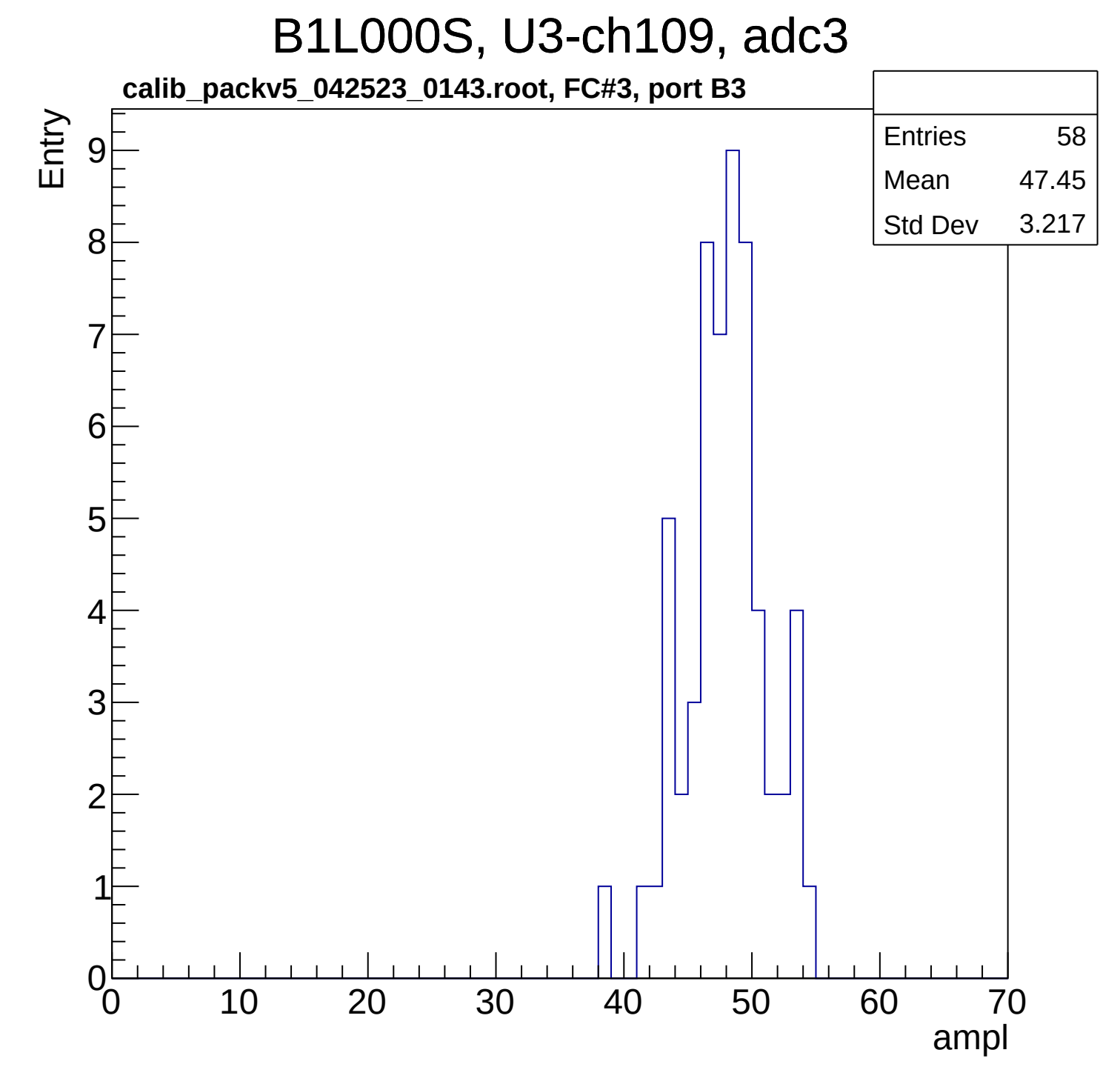

B1L000S, U3-ch108, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 59
Std Dev 12.95
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

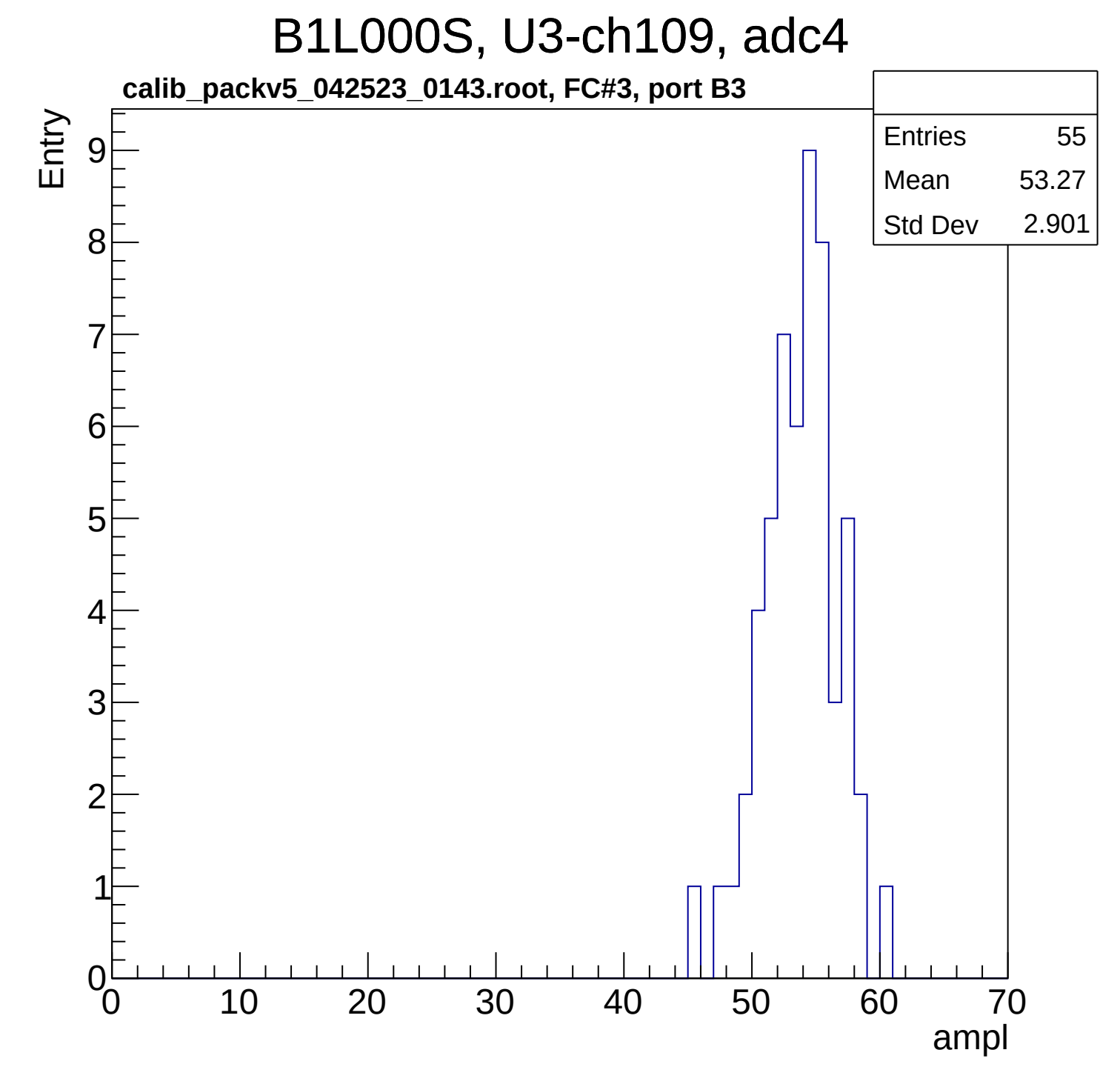

B1L000S, U3-ch108, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch109, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 28.25
Std Dev 3.134
Gaus mean : 28.8134
Gaus Width: 4.7970
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U3-ch109, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 34.77
Std Dev 2.924
Gaus mean : 35.1161
Gaus Width: 2.6755
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch109, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 41.88
Std Dev 3.208
Gaus mean : 41.1895
Gaus Width: 4.2759
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch109, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 47.45
Std Dev 3.217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch109, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.27
Std Dev 2.901
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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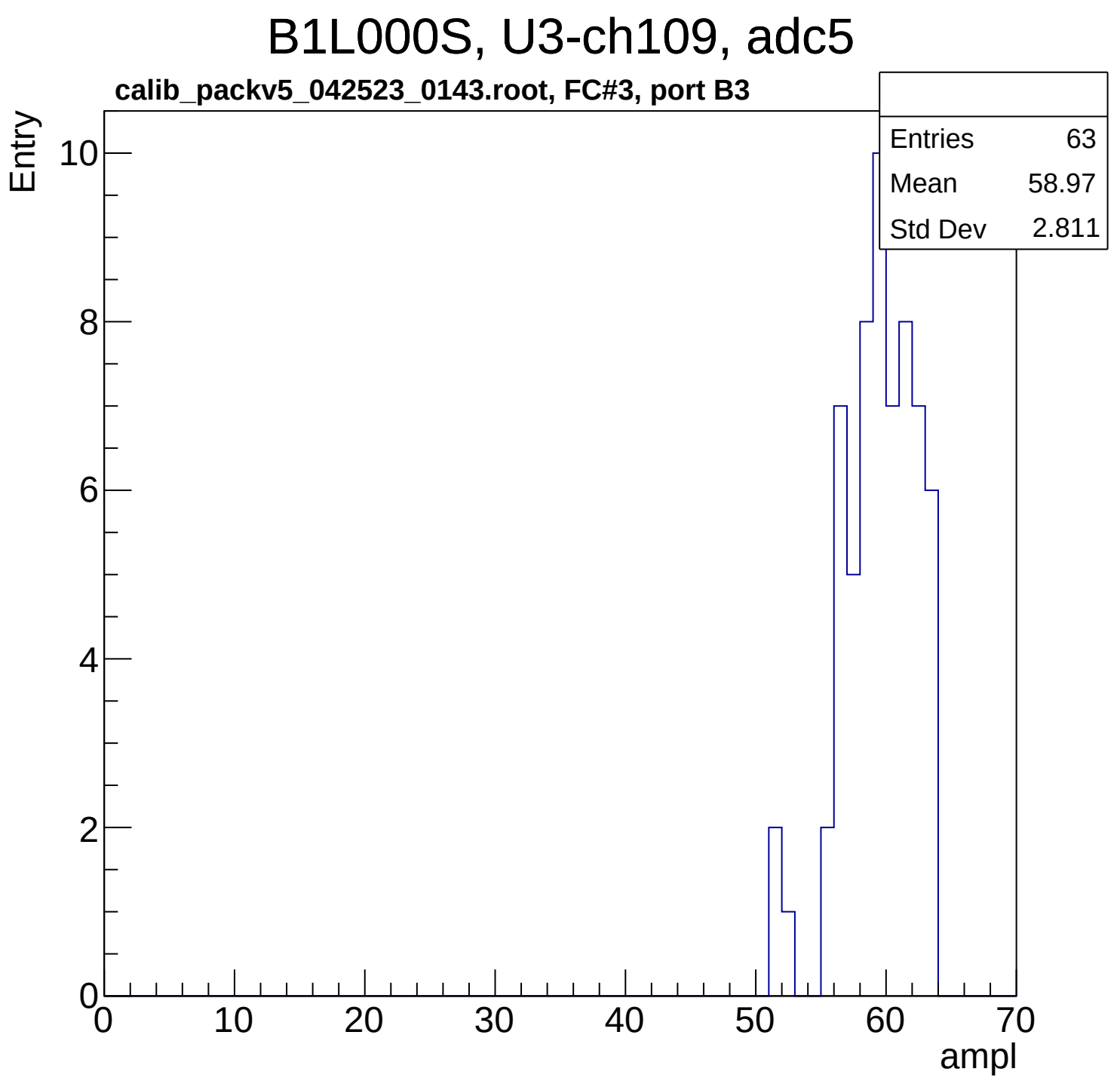
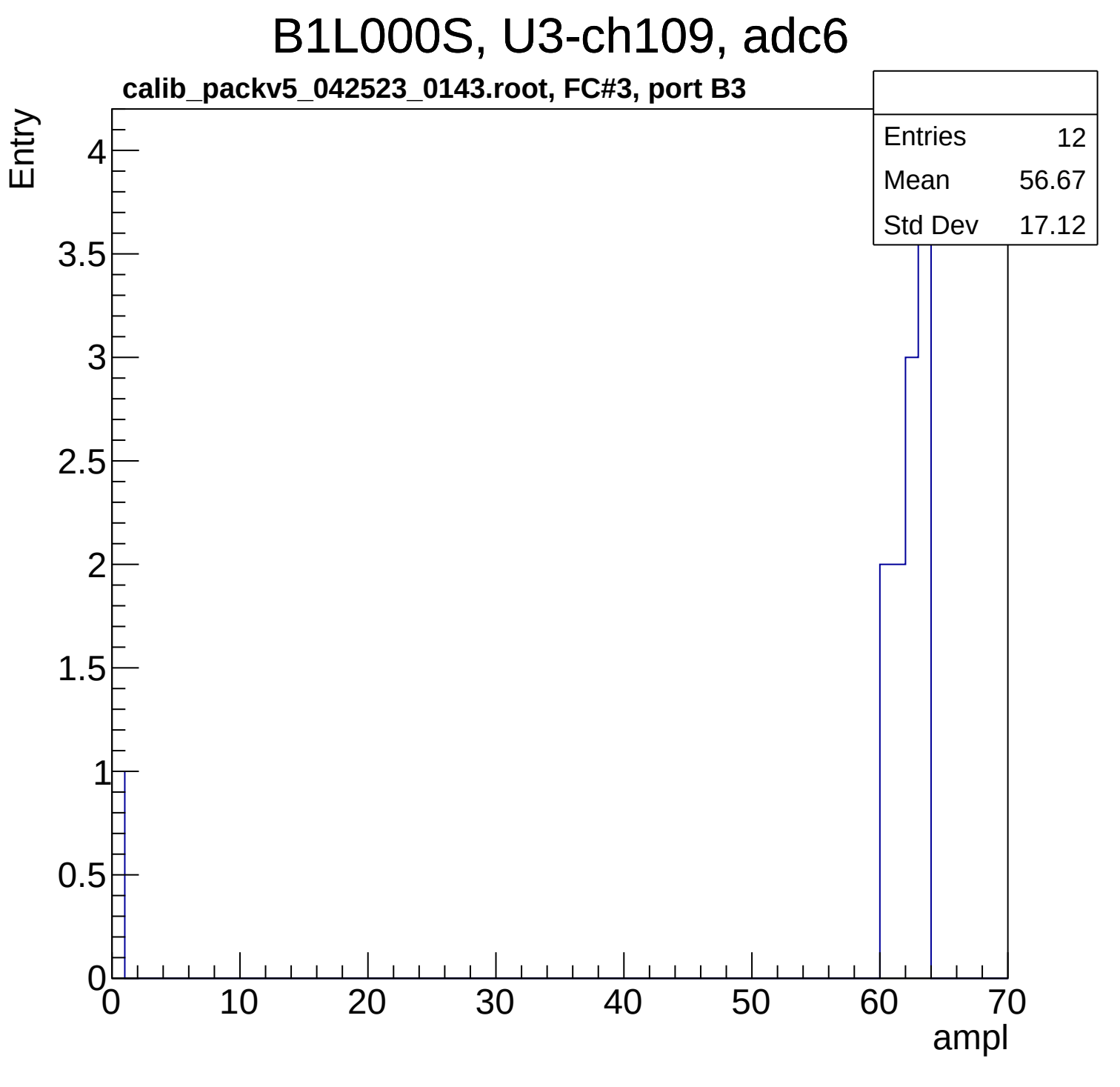
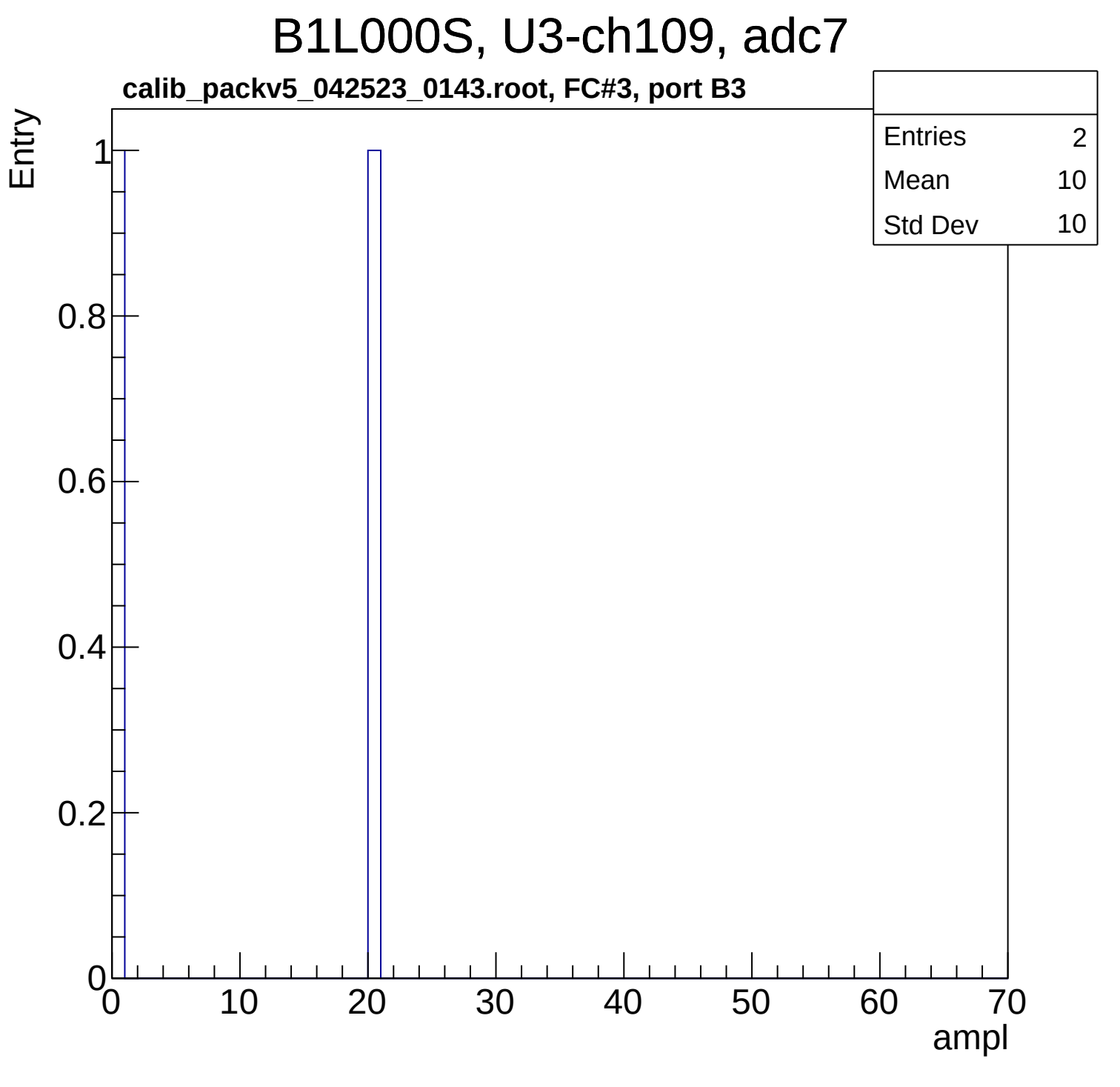
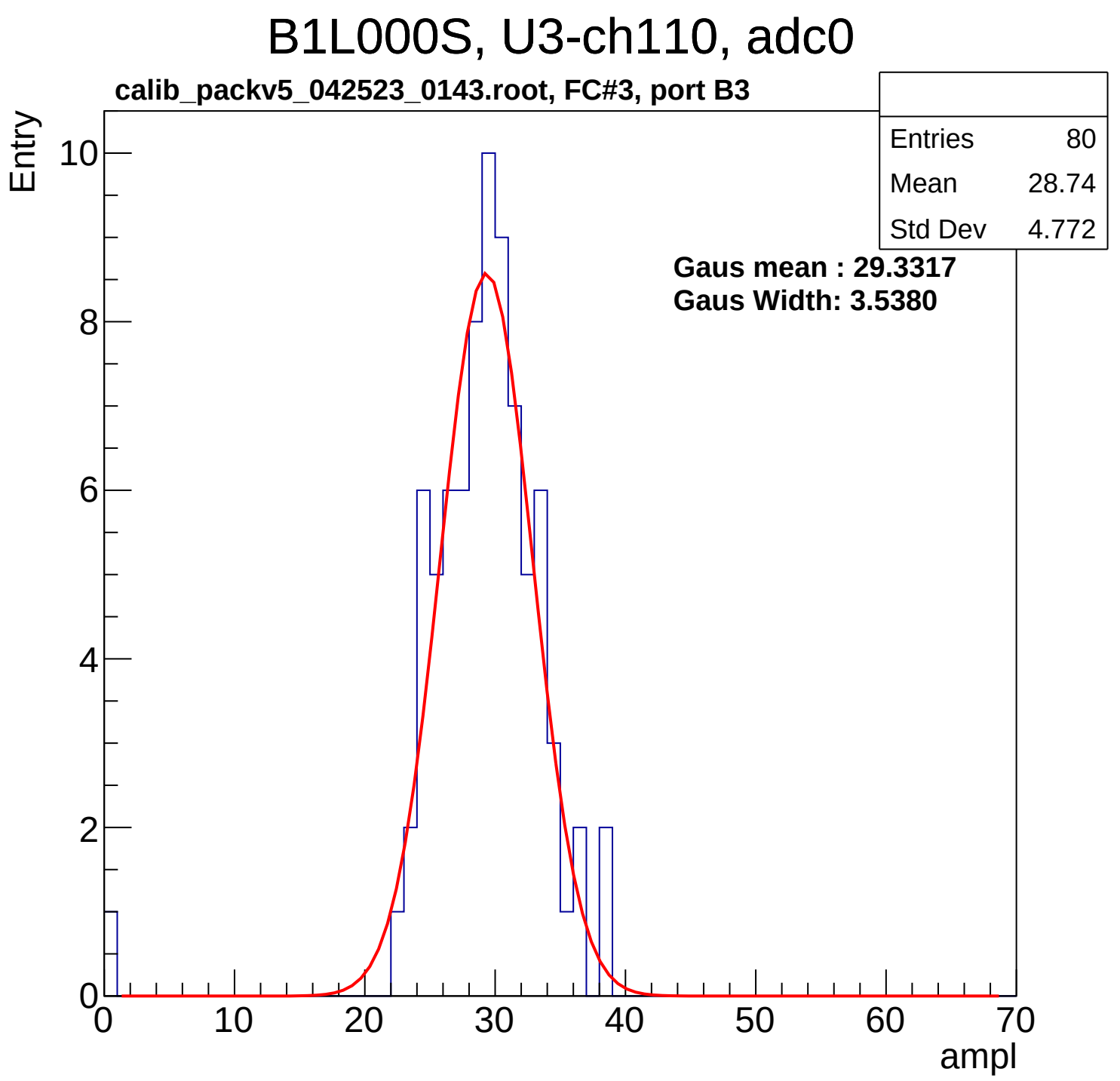
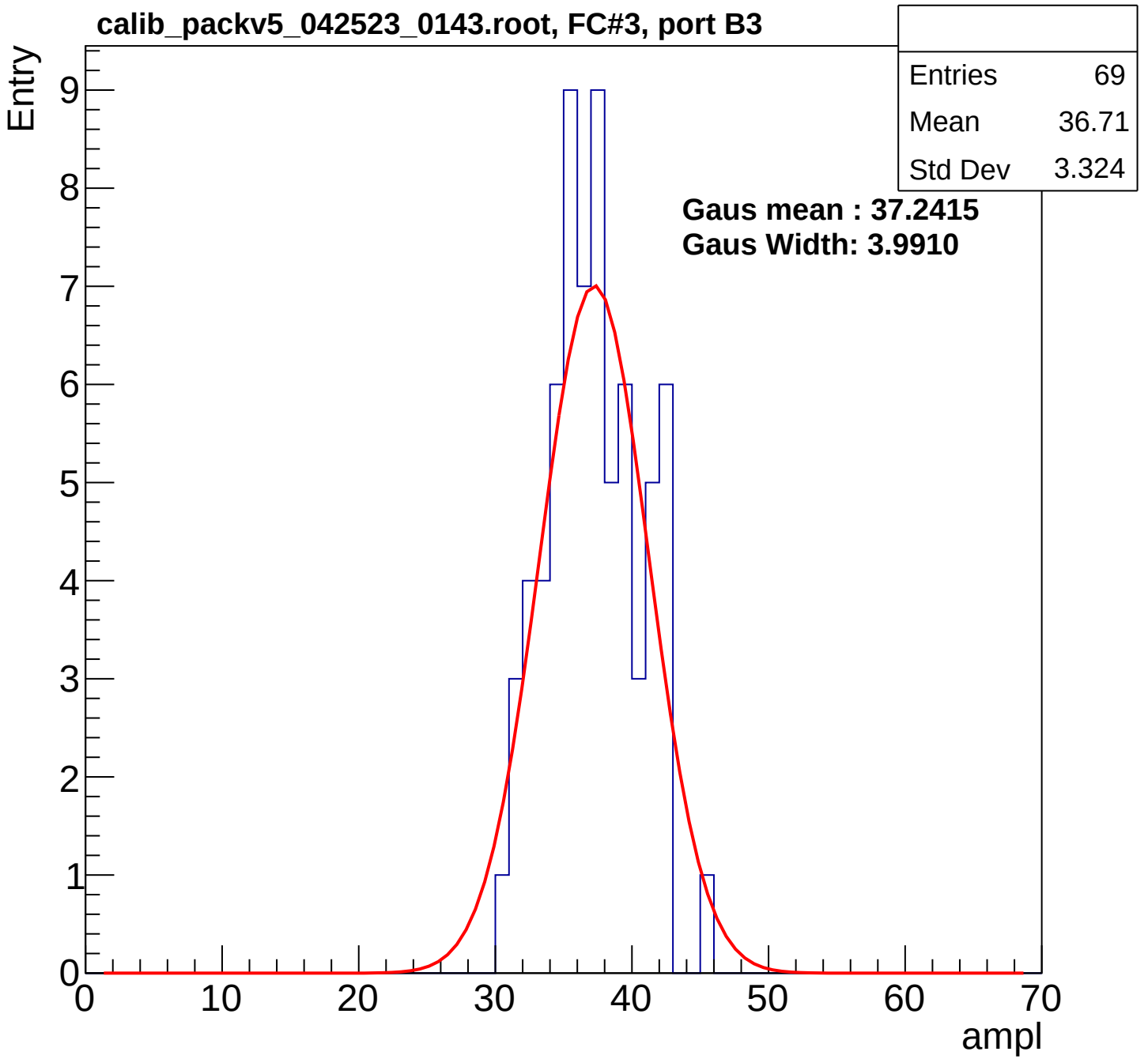
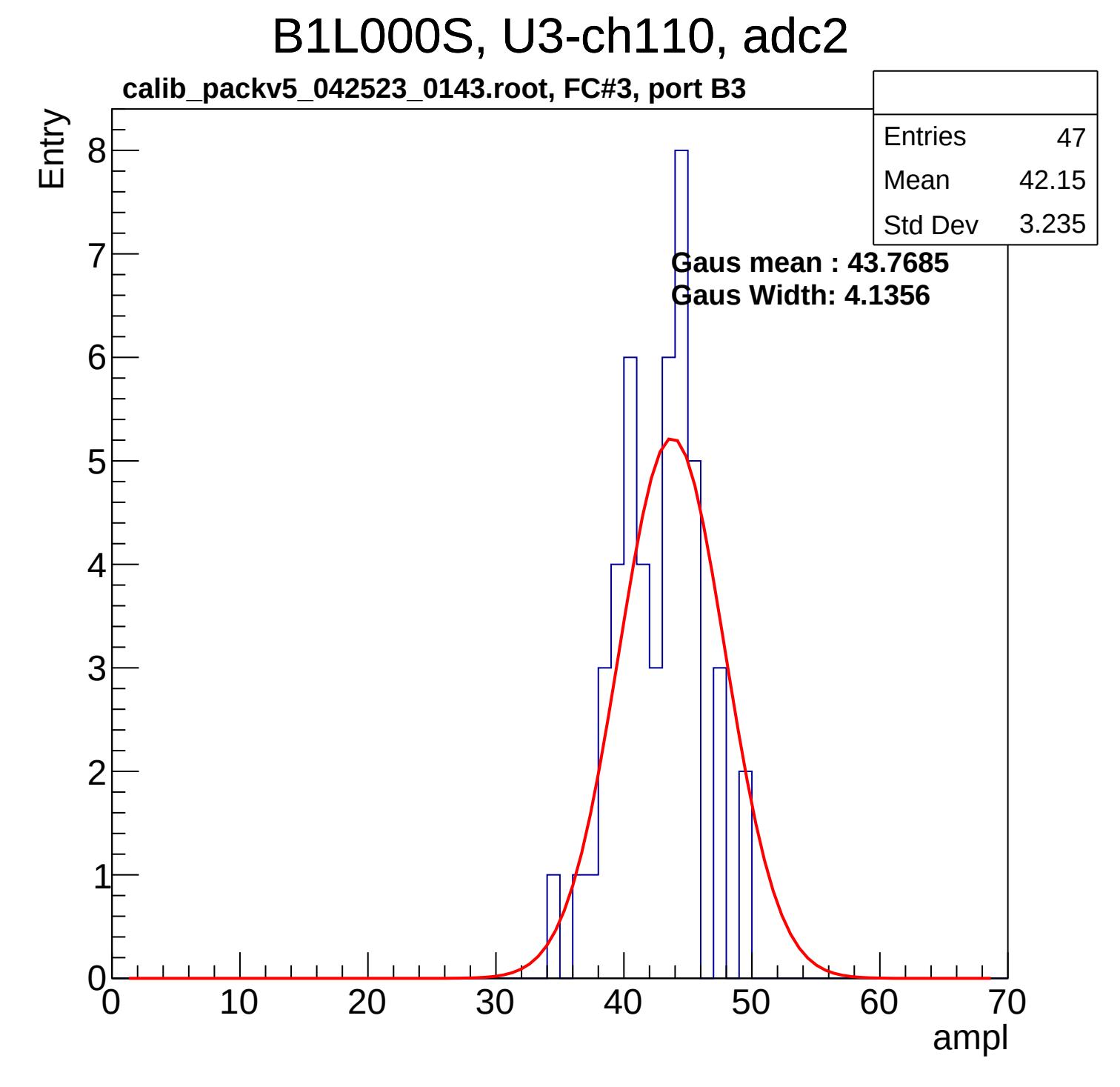

B1L000S, U3-ch109, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 58.97
Std Dev 2.811
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

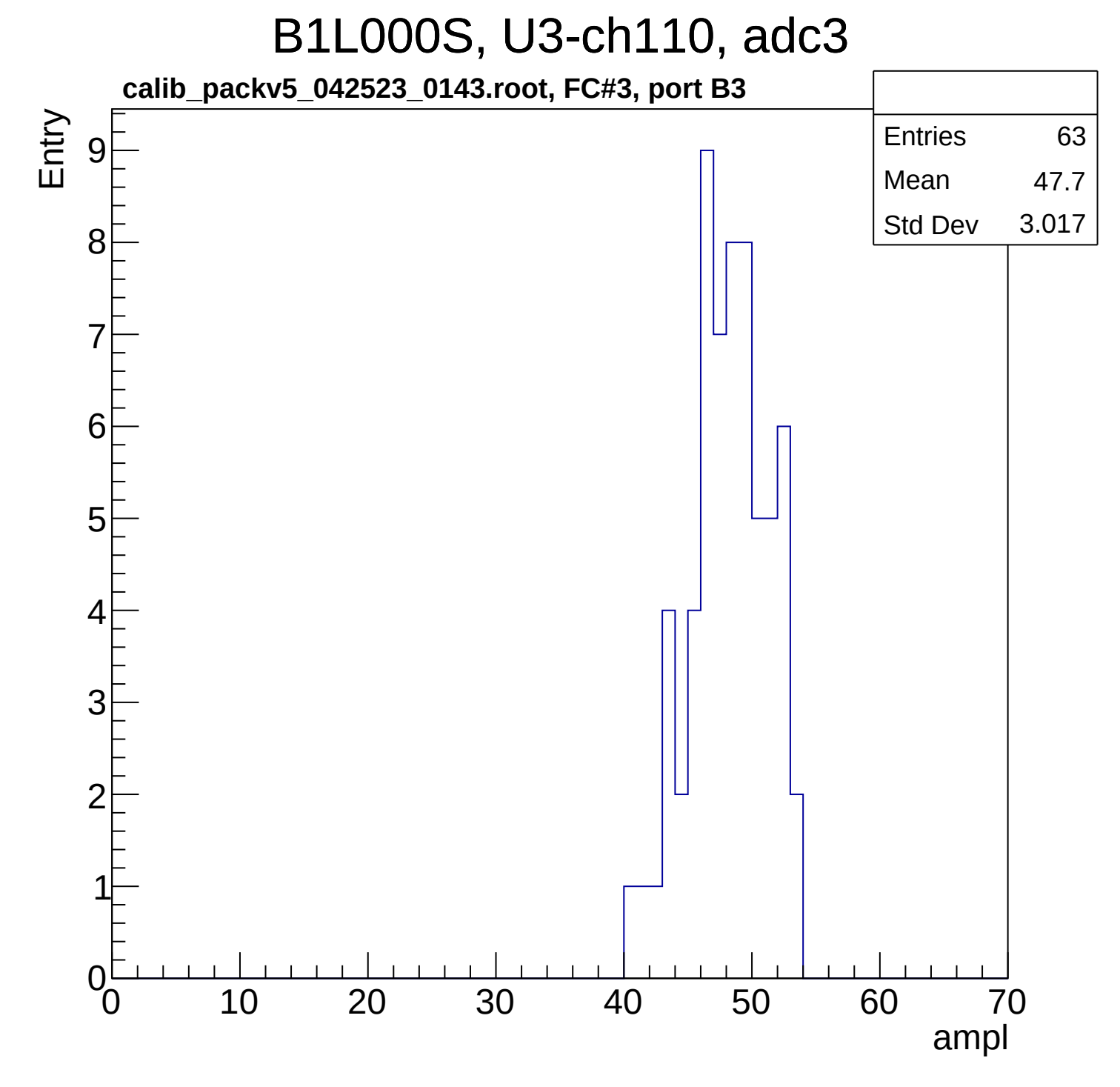

B1L000S, U3-ch109, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.67
Std Dev 17.12
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch109, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch110, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 28.74
Std Dev 4.772
Gaus mean : 29.3317
Gaus Width: 3.5380
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch110, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 36.71
Std Dev 3.324
Gaus mean : 37.2415
Gaus Width: 3.9910
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch110, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 42.15
Std Dev 3.235
Gaus mean : 43.7685
Gaus Width: 4.1356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch110, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 47.7
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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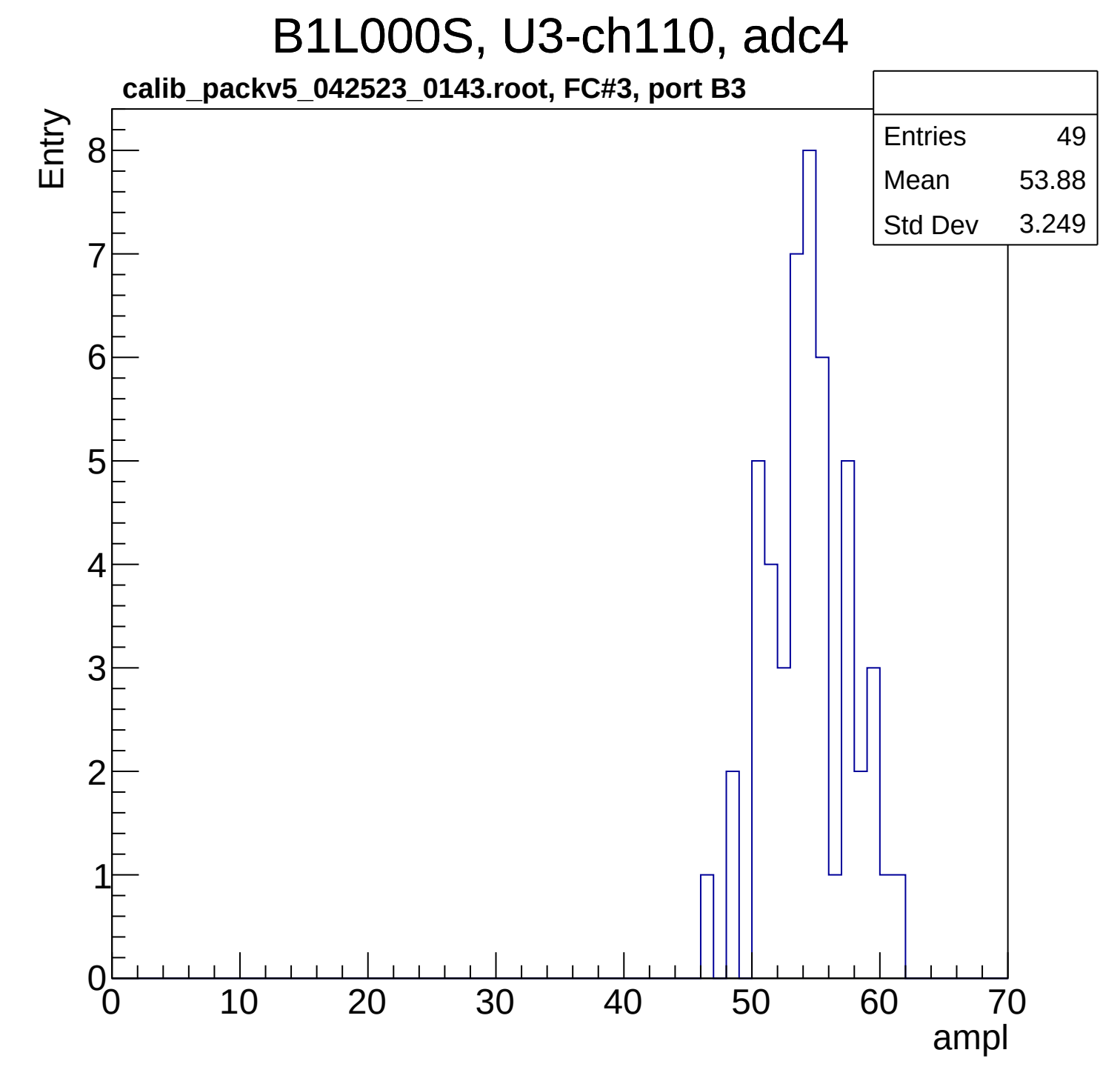
B1L000S, U3-ch110, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 53.88
Std Dev 3.249
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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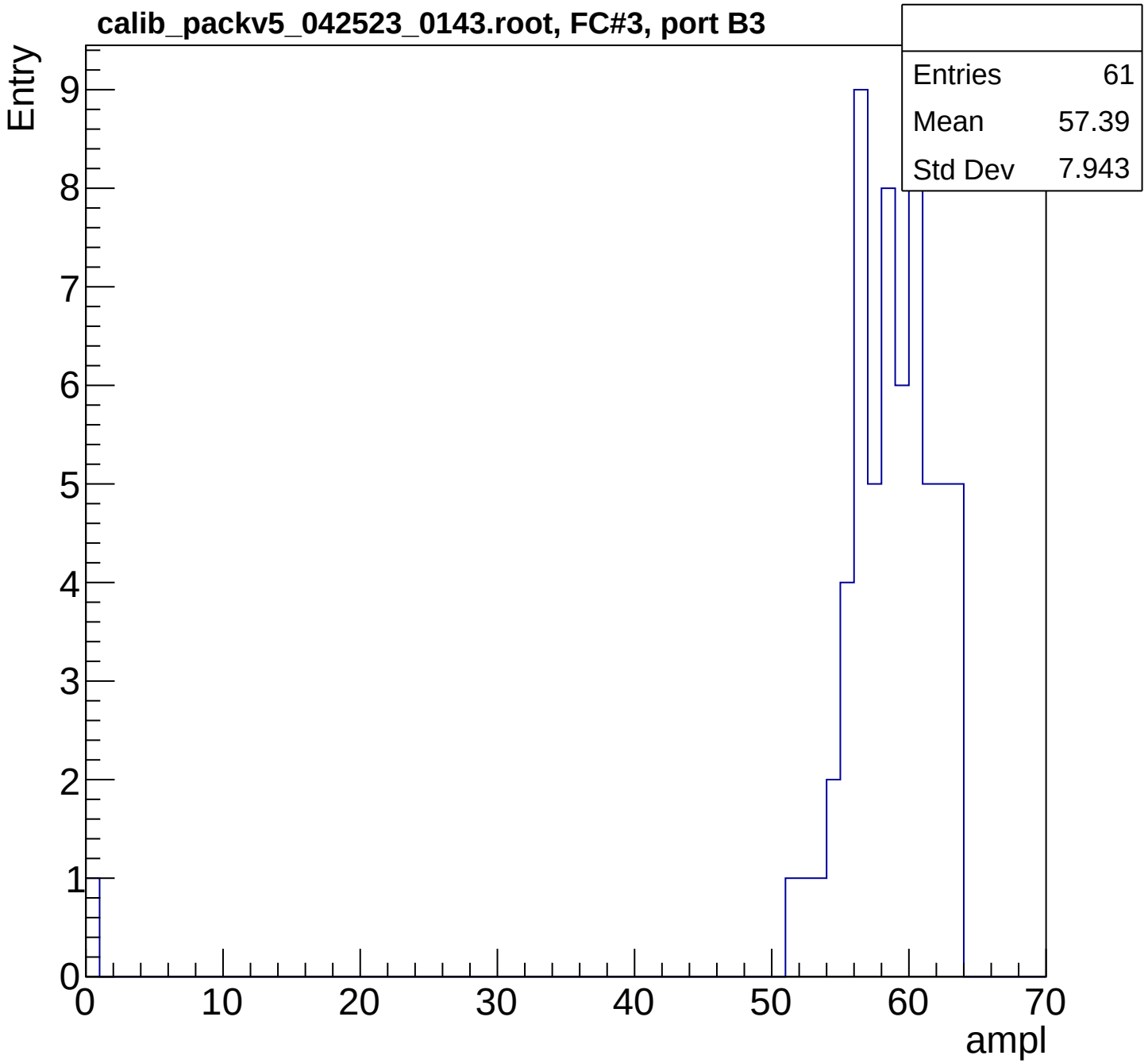
B1L000S, U3-ch110, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 57.39
Std Dev 7.943
Entry
ampl
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3
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5
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7
8
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0
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20
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60
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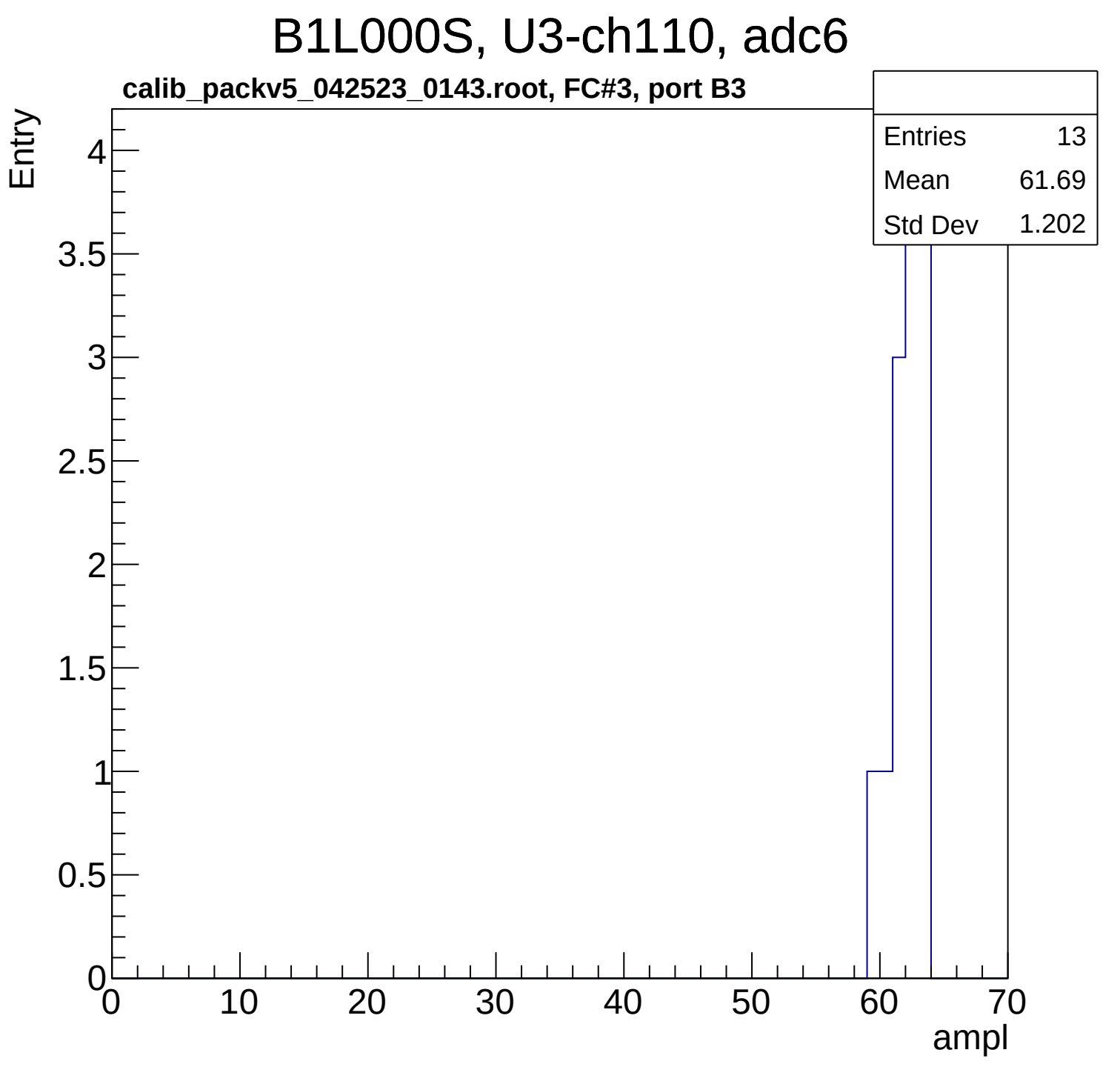
B1L000S, U3-ch110, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.69
Std Dev 1.202
Entry
ampl
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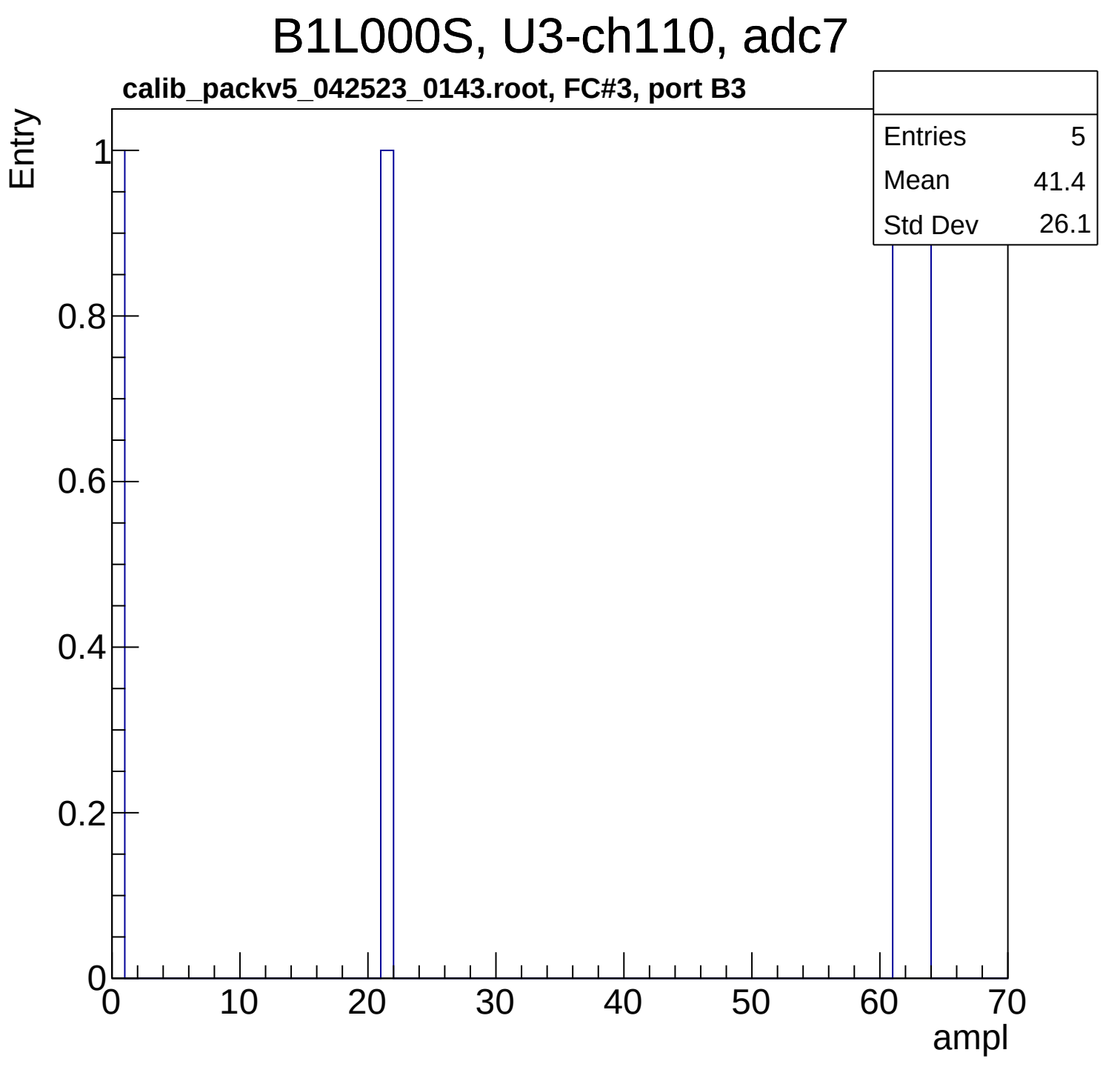
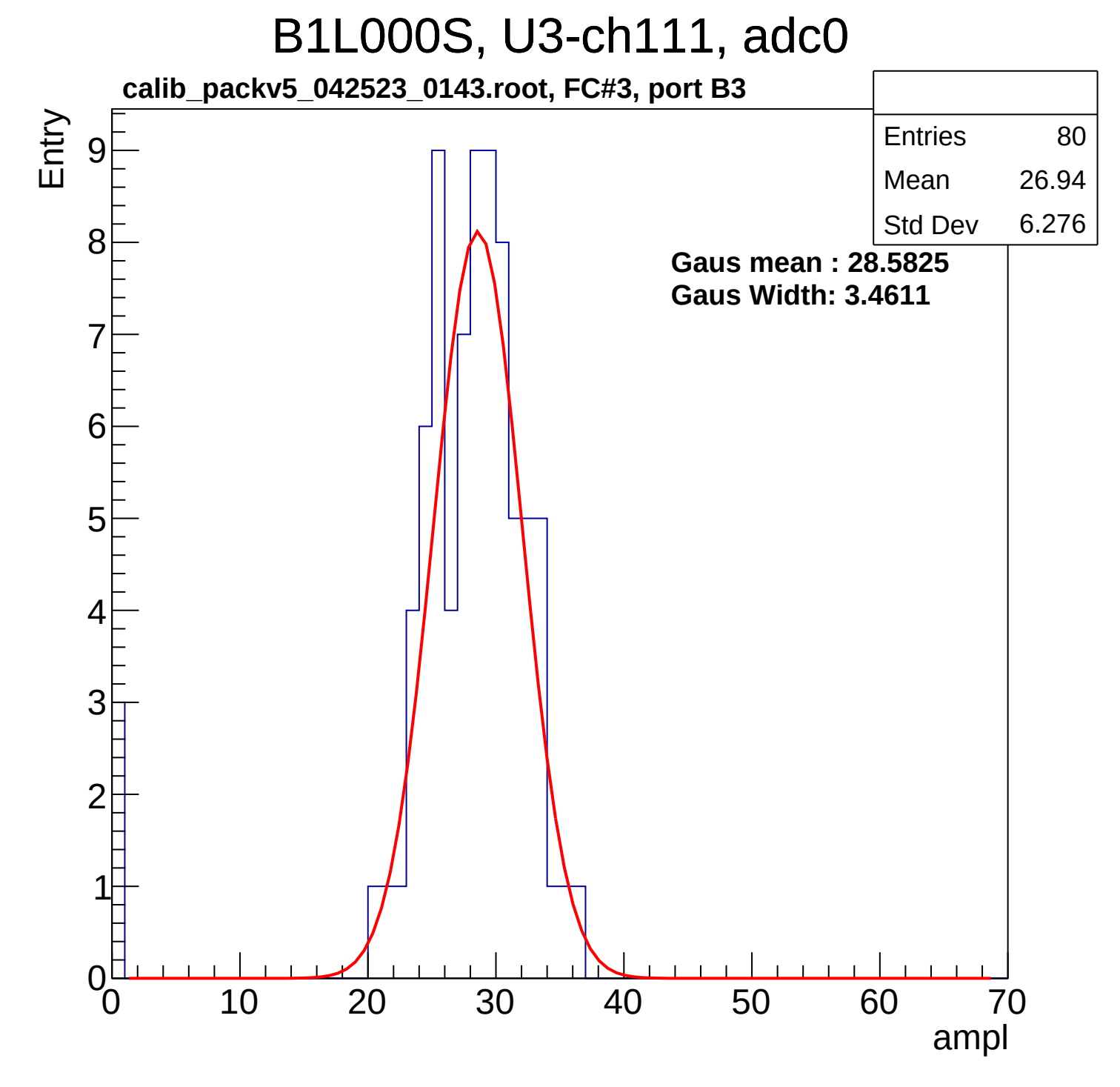
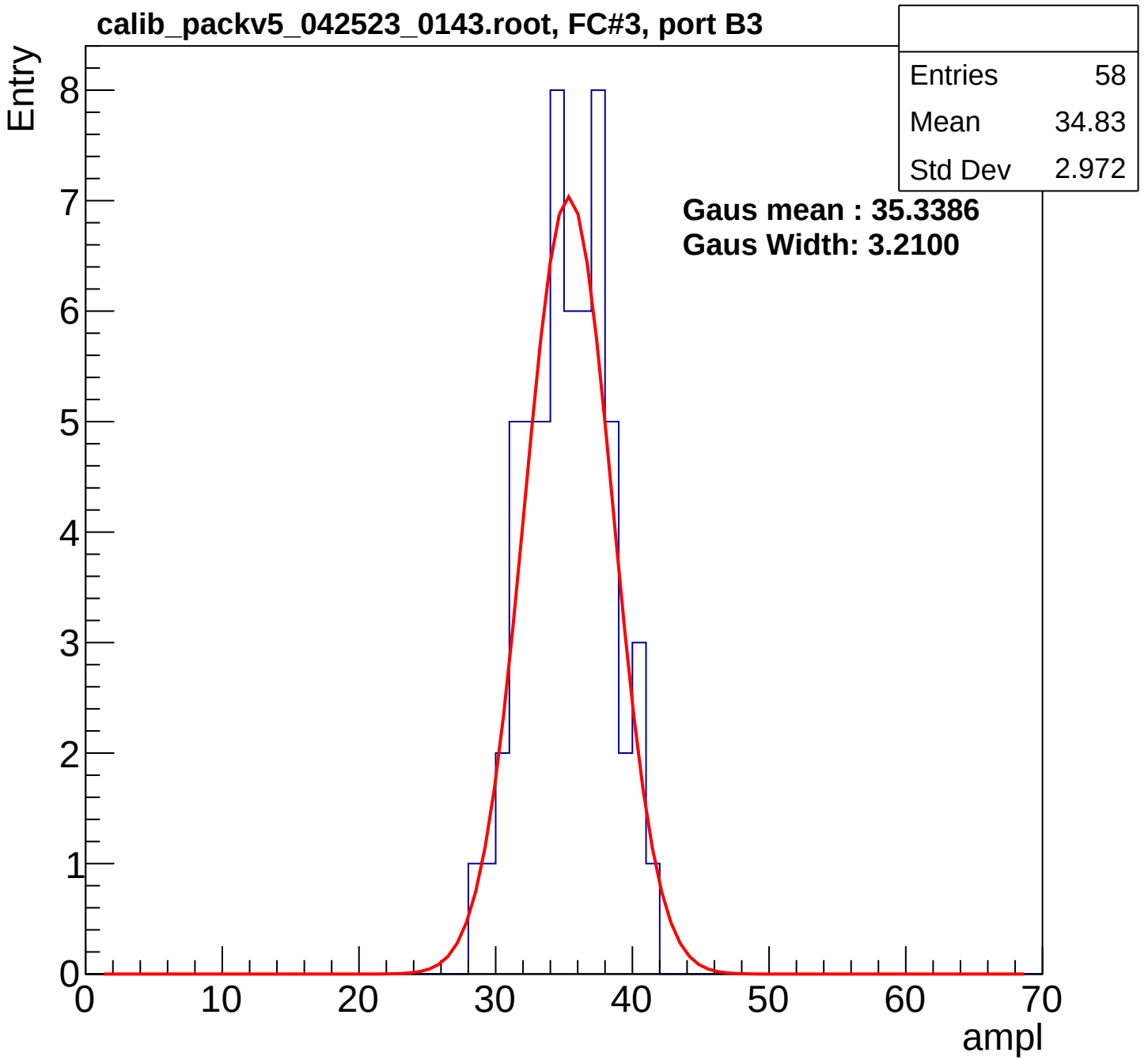
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch110, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 41.4
Std Dev 26.1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U3-ch111, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 26.94
Std Dev 6.276
Gaus mean : 28.5825
Gaus Width: 3.4611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U3-ch111, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 34.83
Std Dev 2.972
Gaus mean : 35.3386
Gaus Width: 3.2100
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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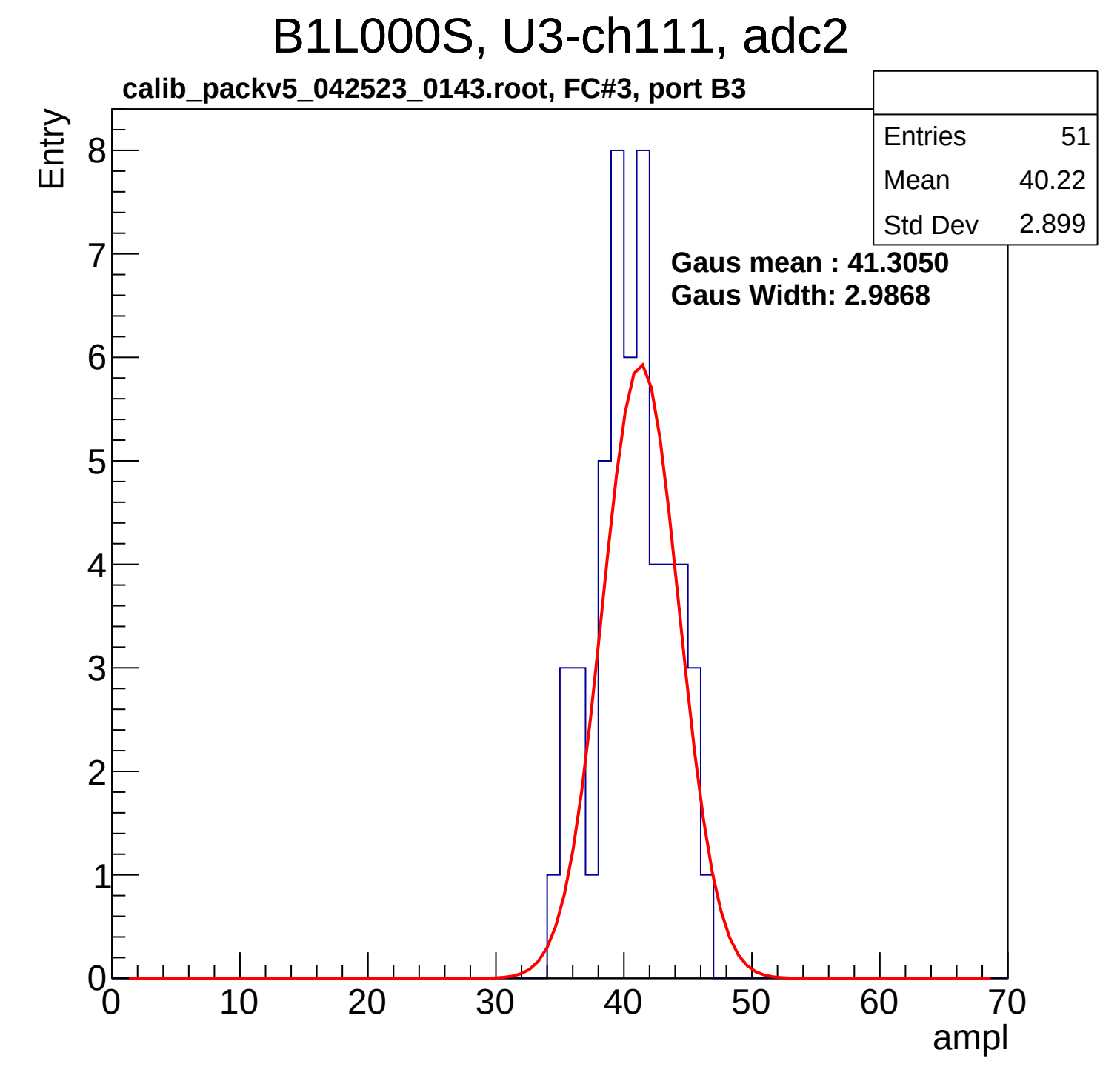
60
70


B1L000S, U3-ch111, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 40.22
Std Dev 2.899
Gaus mean : 41.3050
Gaus Width: 2.9868
Entry
ampl
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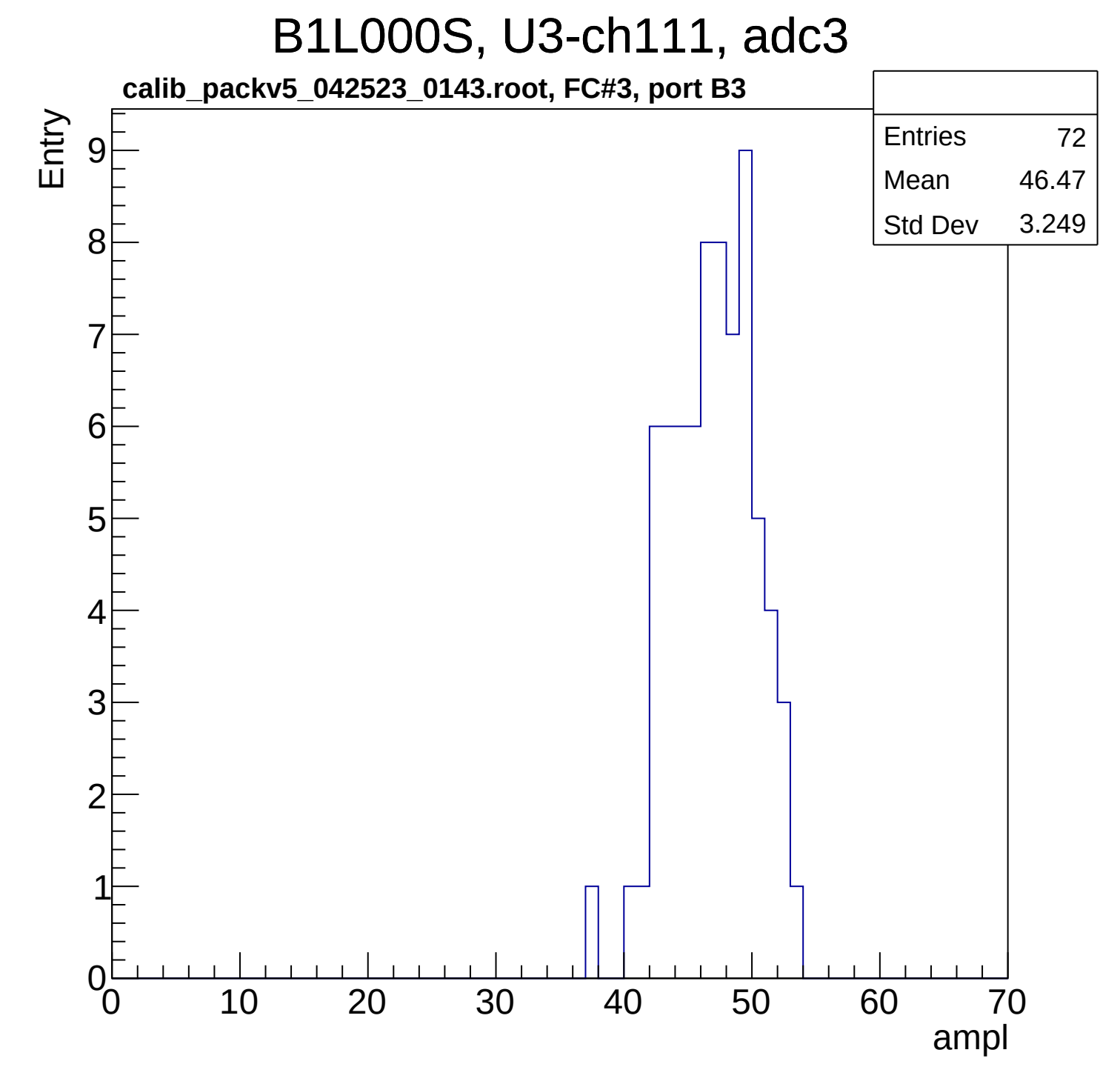
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch111, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 46.47
Std Dev 3.249
Entry
ampl
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3
4
5
6
7
8
9
0
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30
40
50
60
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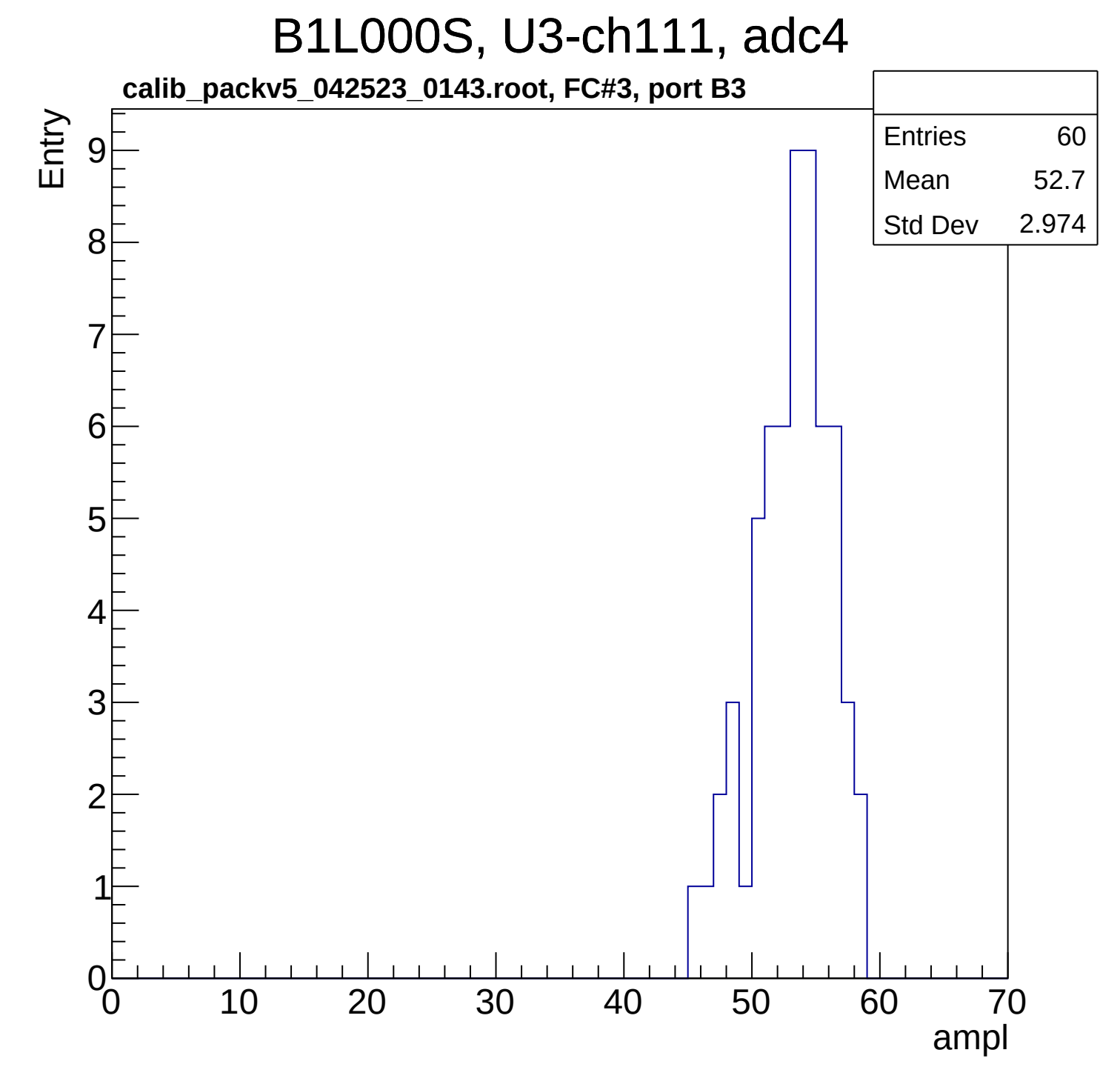
B1L000S, U3-ch111, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 52.7
Std Dev 2.974
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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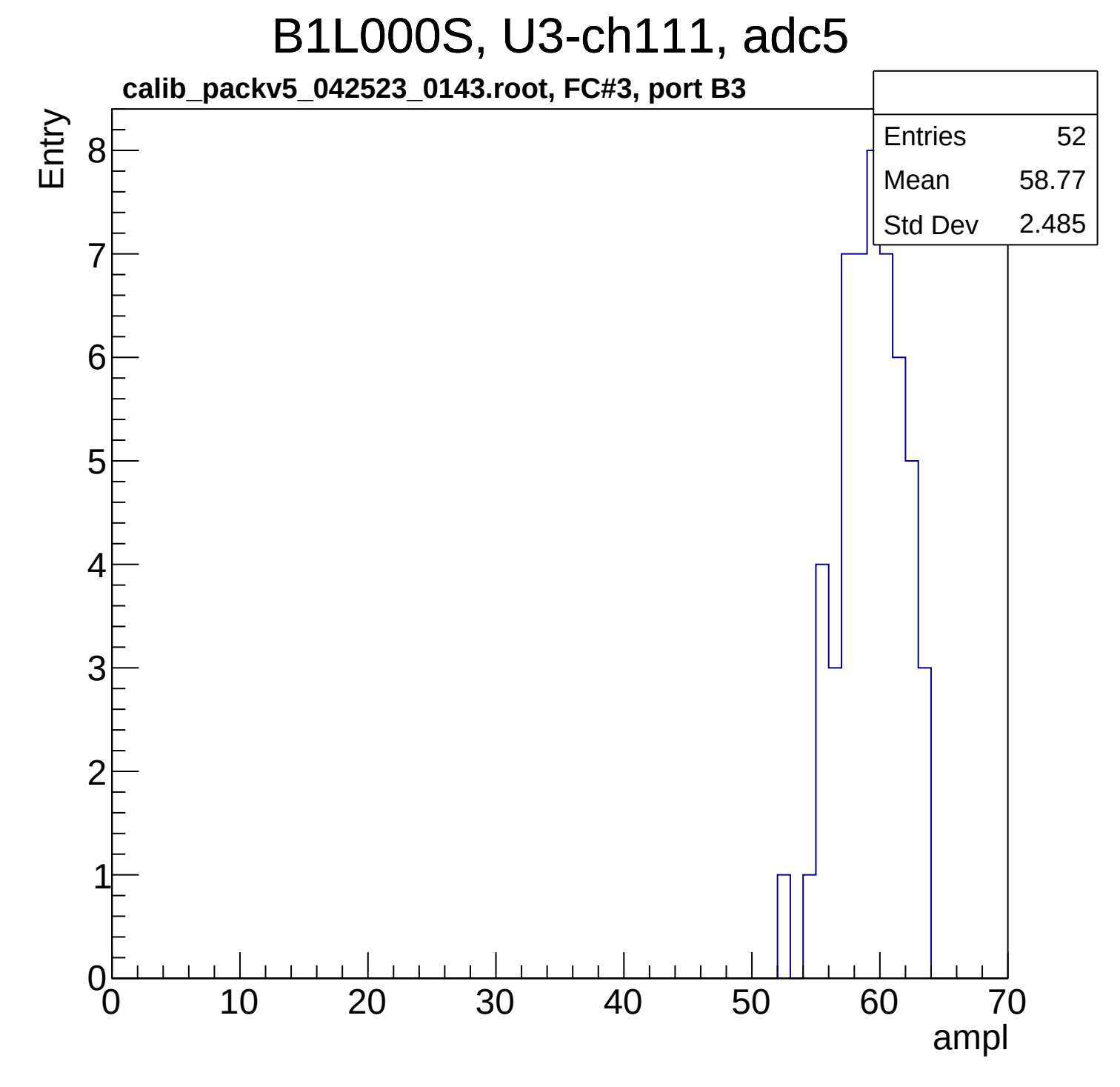
60
70


B1L000S, U3-ch111, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.77
Std Dev 2.485
Entry
ampl
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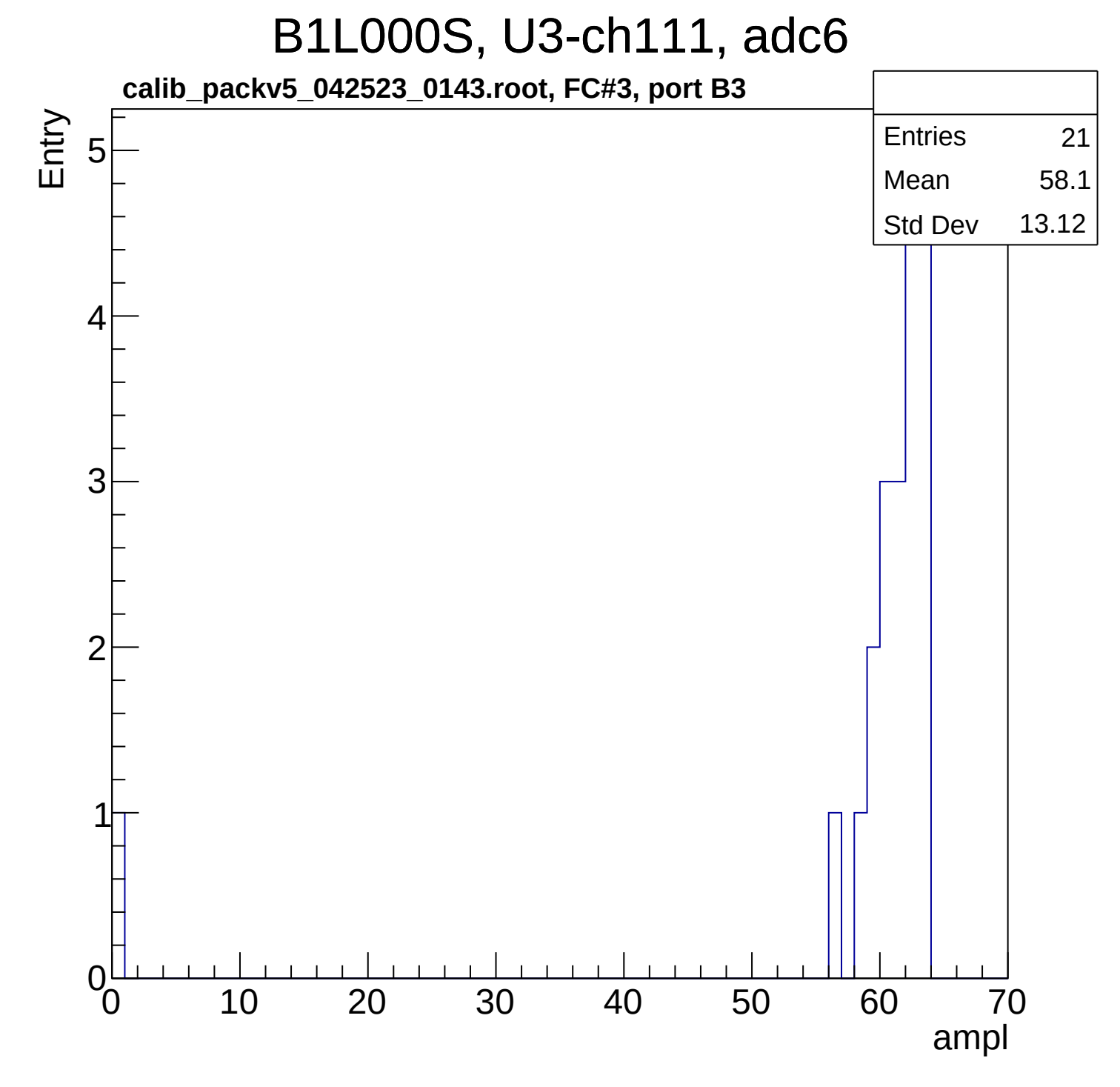
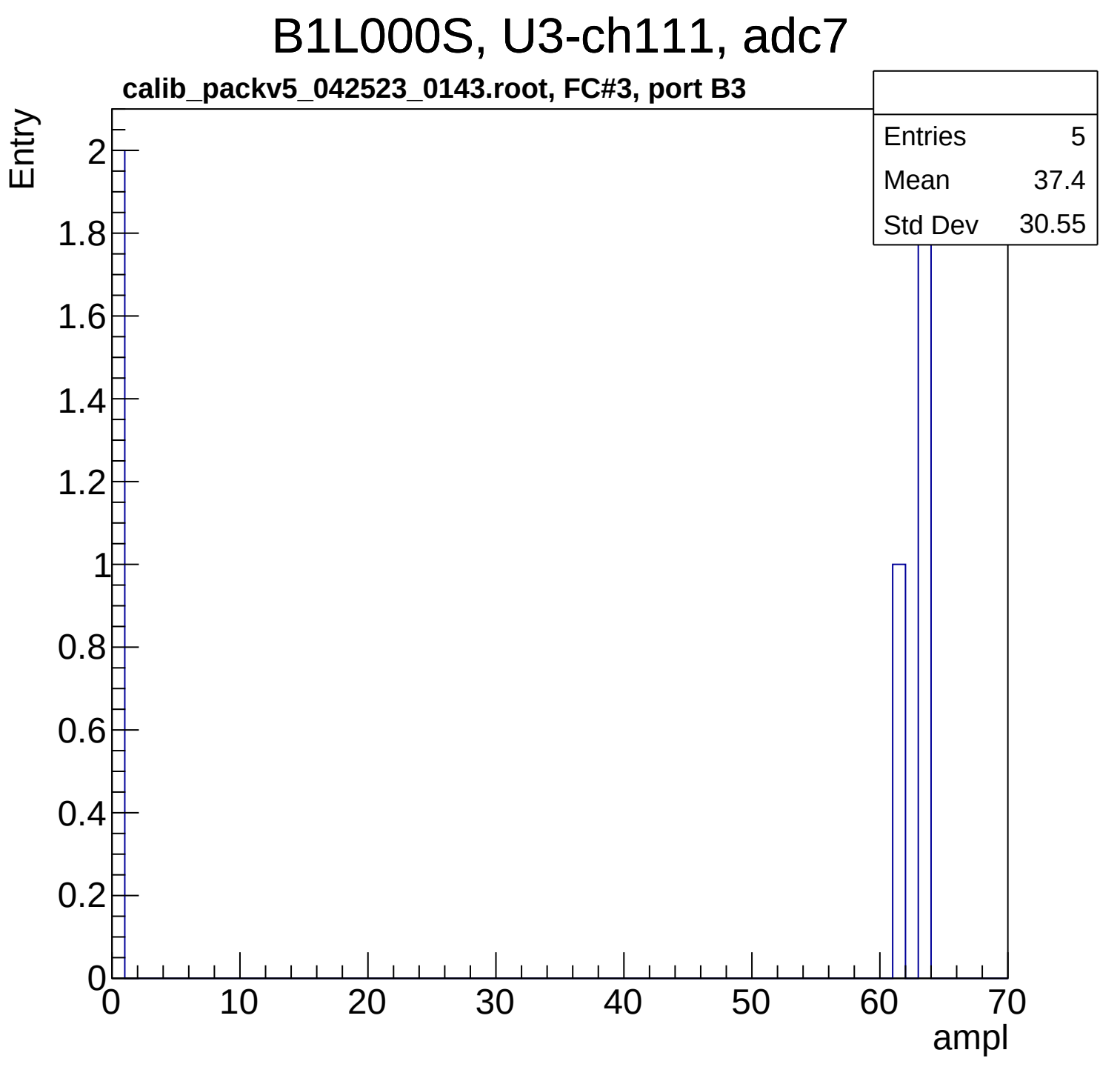
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch111, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 58.1
Std Dev 13.12
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch111, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 37.4
Std Dev 30.55
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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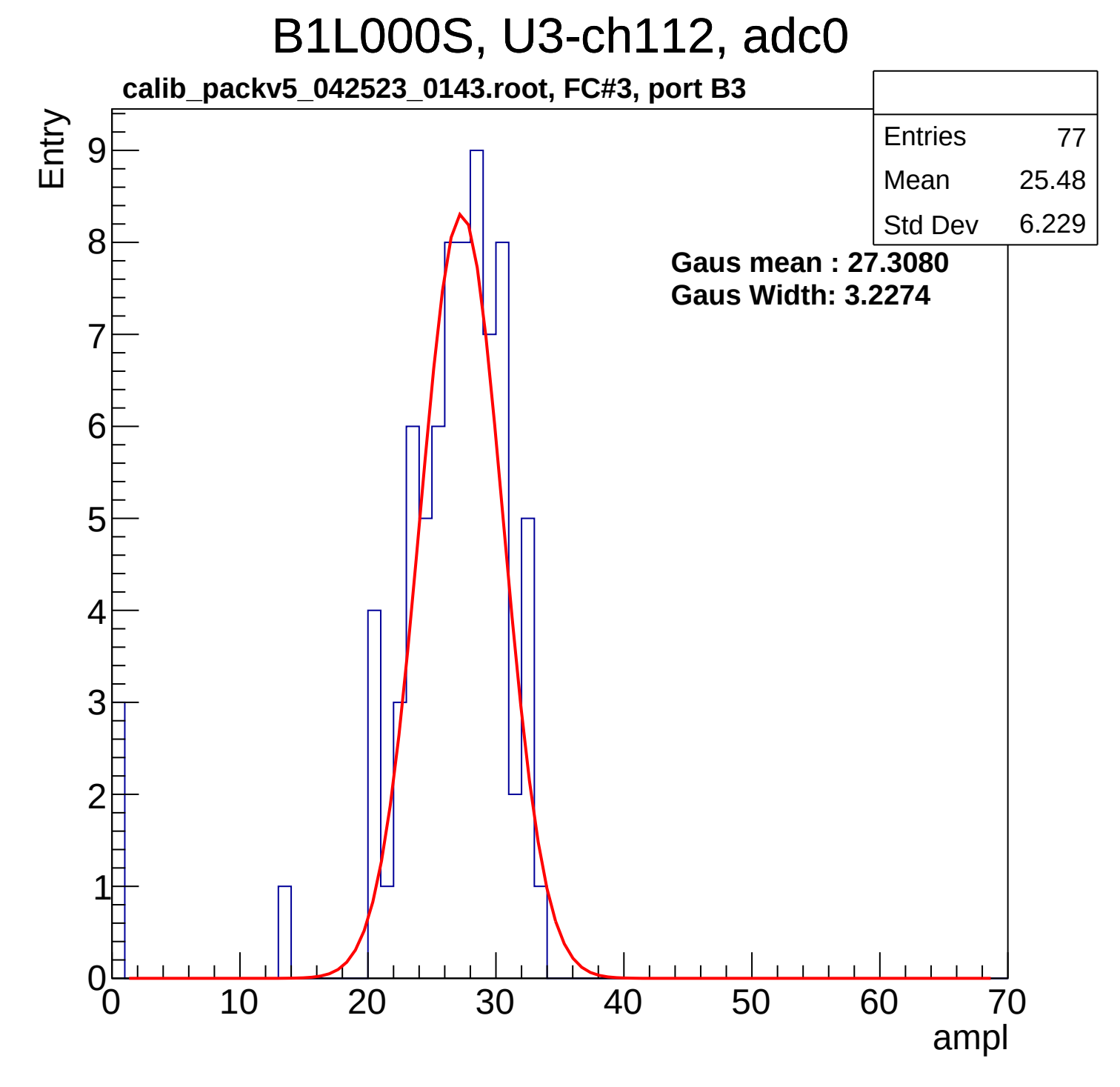
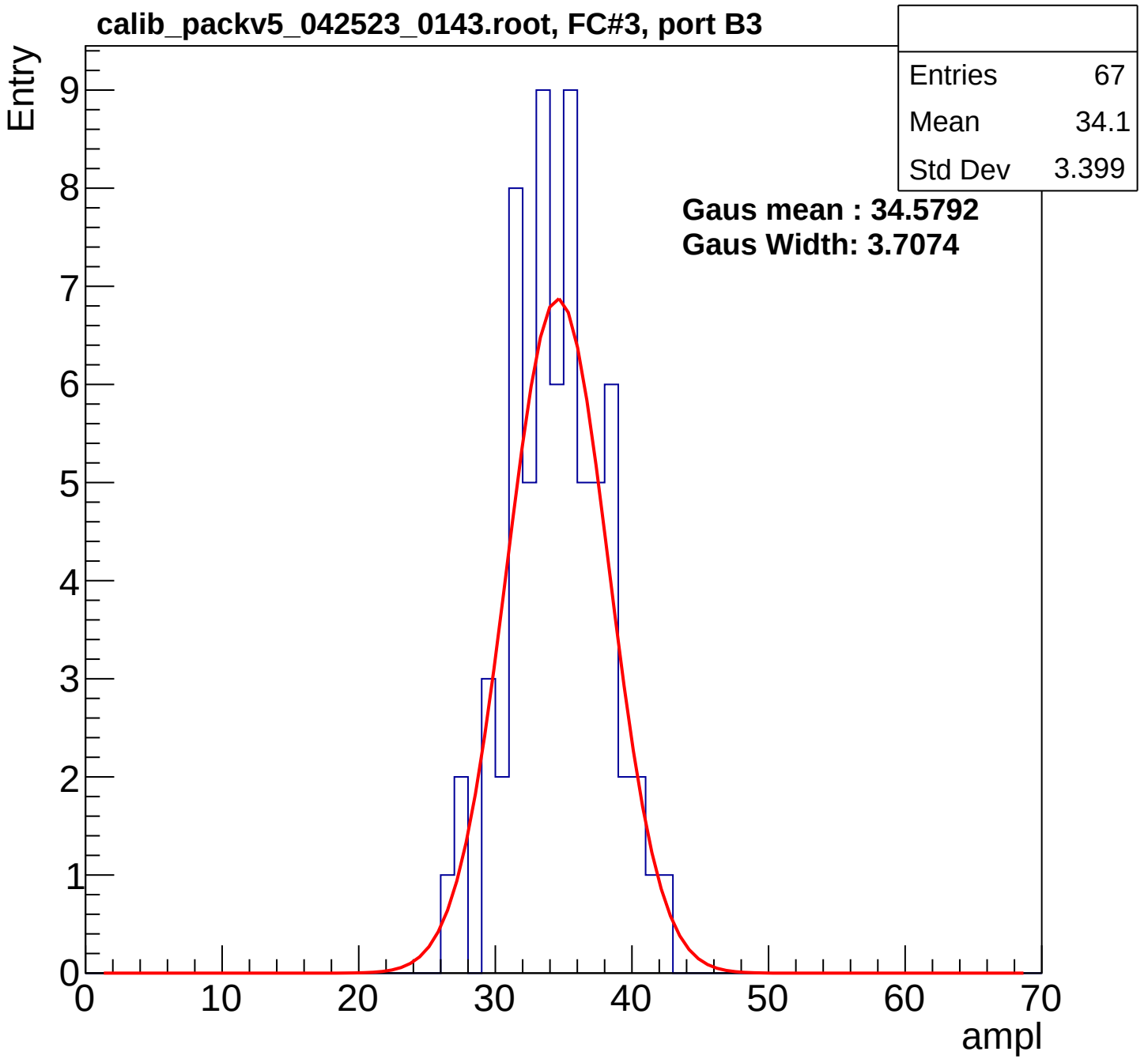

B1L000S, U3-ch112, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 25.48
Std Dev 6.229
Gaus mean : 27.3080
Gaus Width: 3.2274
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch112, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 34.1
Std Dev 3.399
Gaus mean : 34.5792
Gaus Width: 3.7074
Entry
ampl
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2
3
4
5
6
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8
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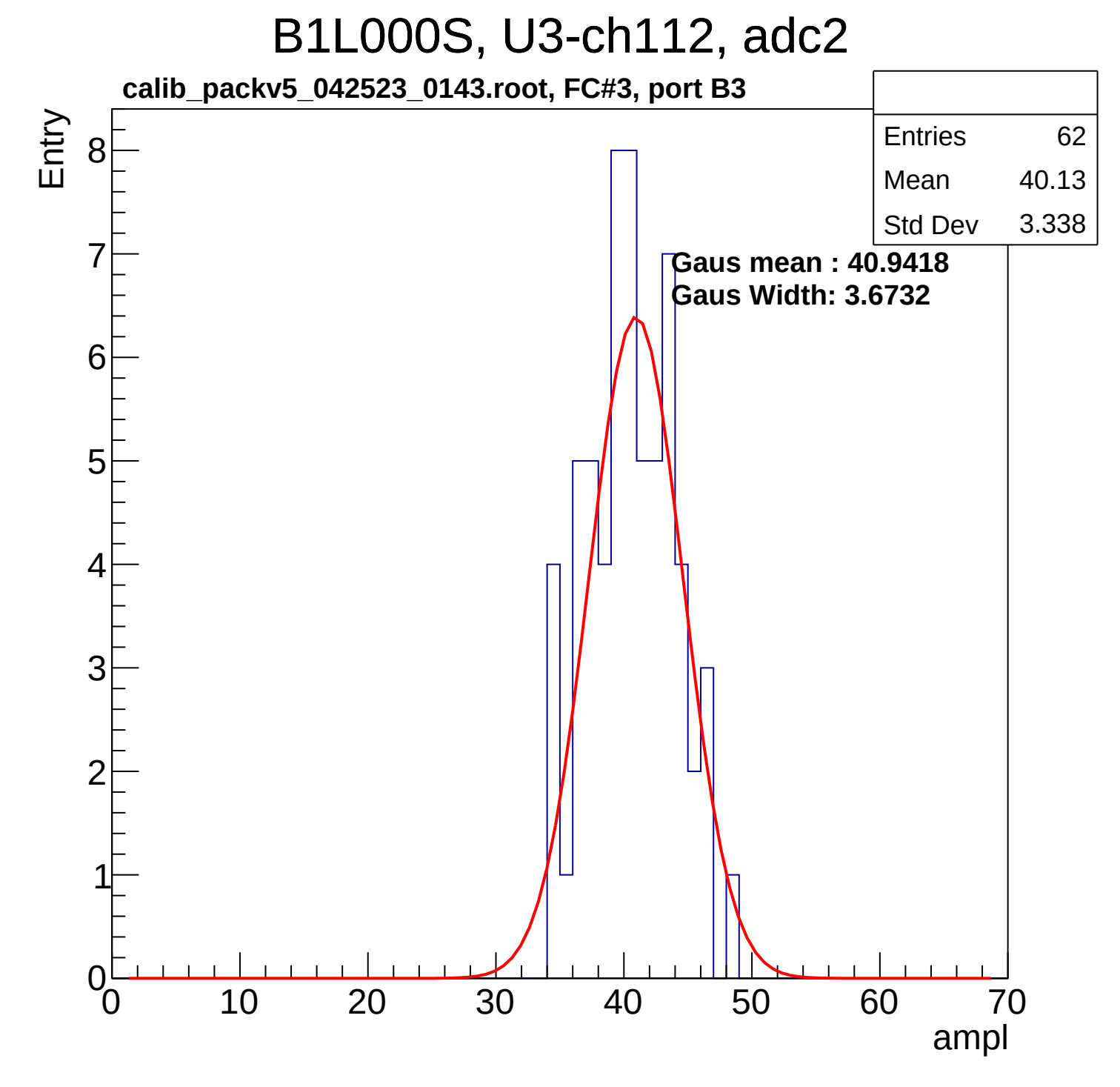
B1L000S, U3-ch112, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 40.13
Std Dev 3.338
Gaus mean : 40.9418
Gaus Width: 3.6732
Entry
ampl
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1
2
3
4
5
6
7
8
0
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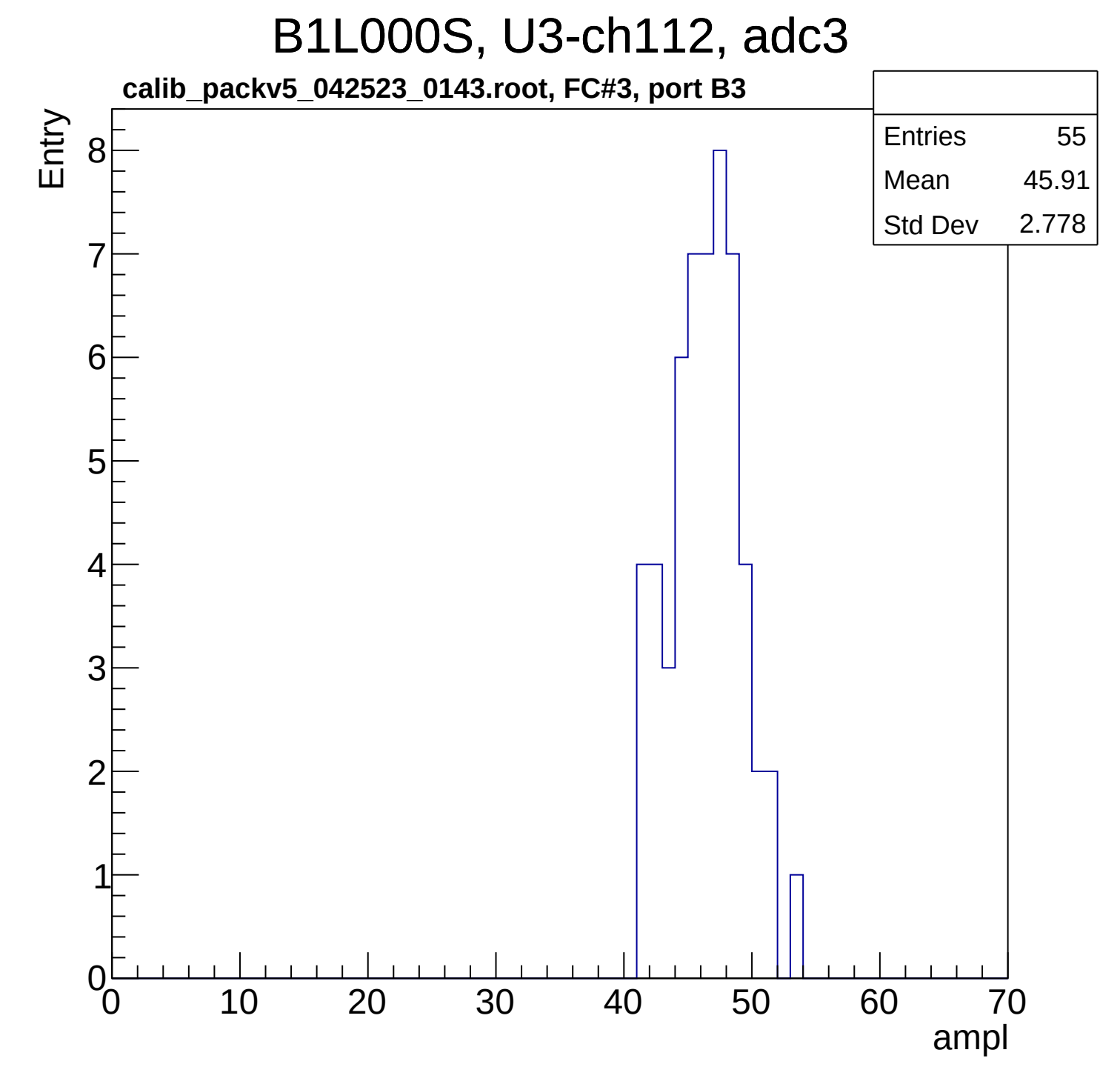
B1L000S, U3-ch112, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 45.91
Std Dev 2.778
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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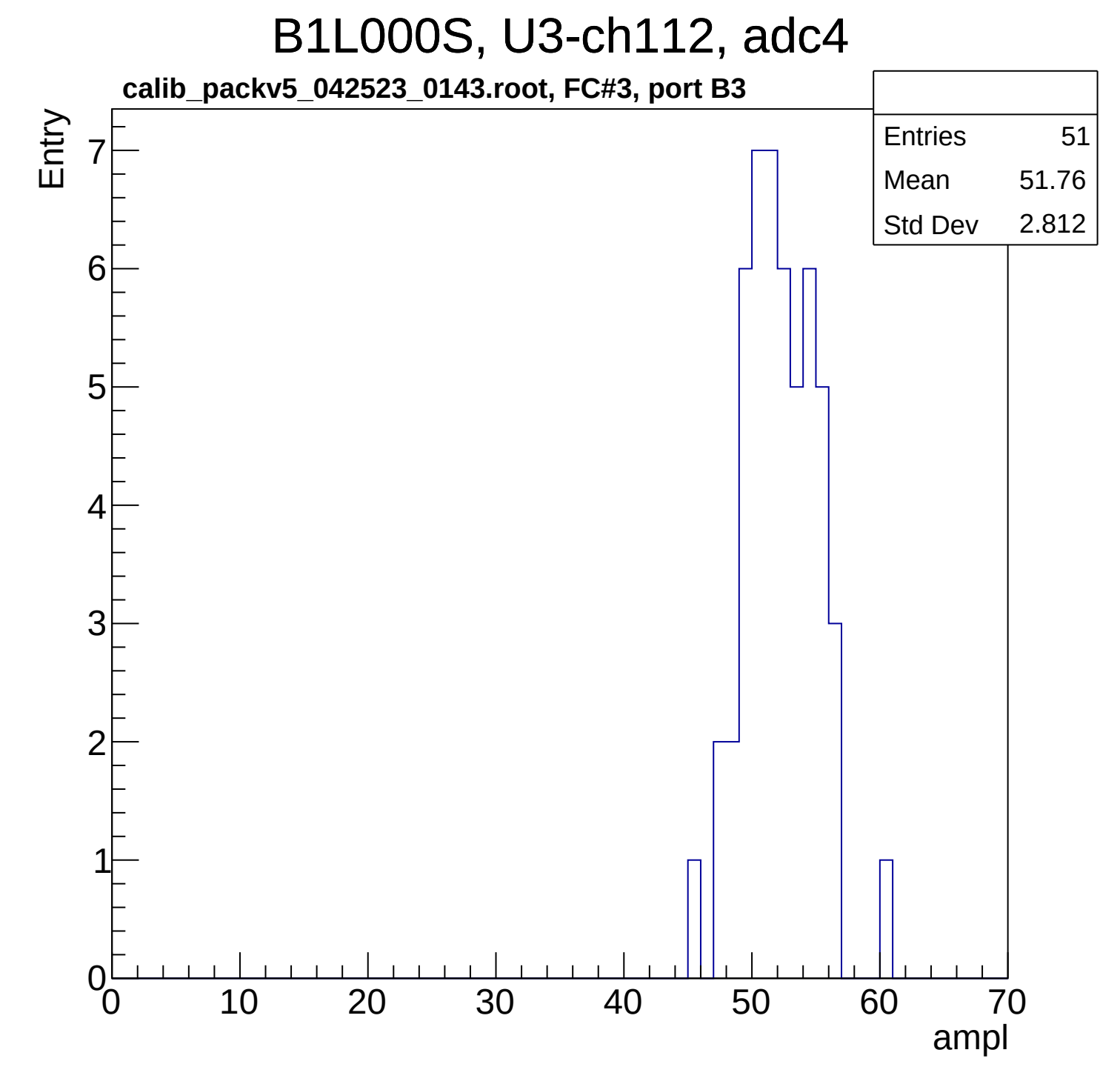
B1L000S, U3-ch112, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 51.76
Std Dev 2.812
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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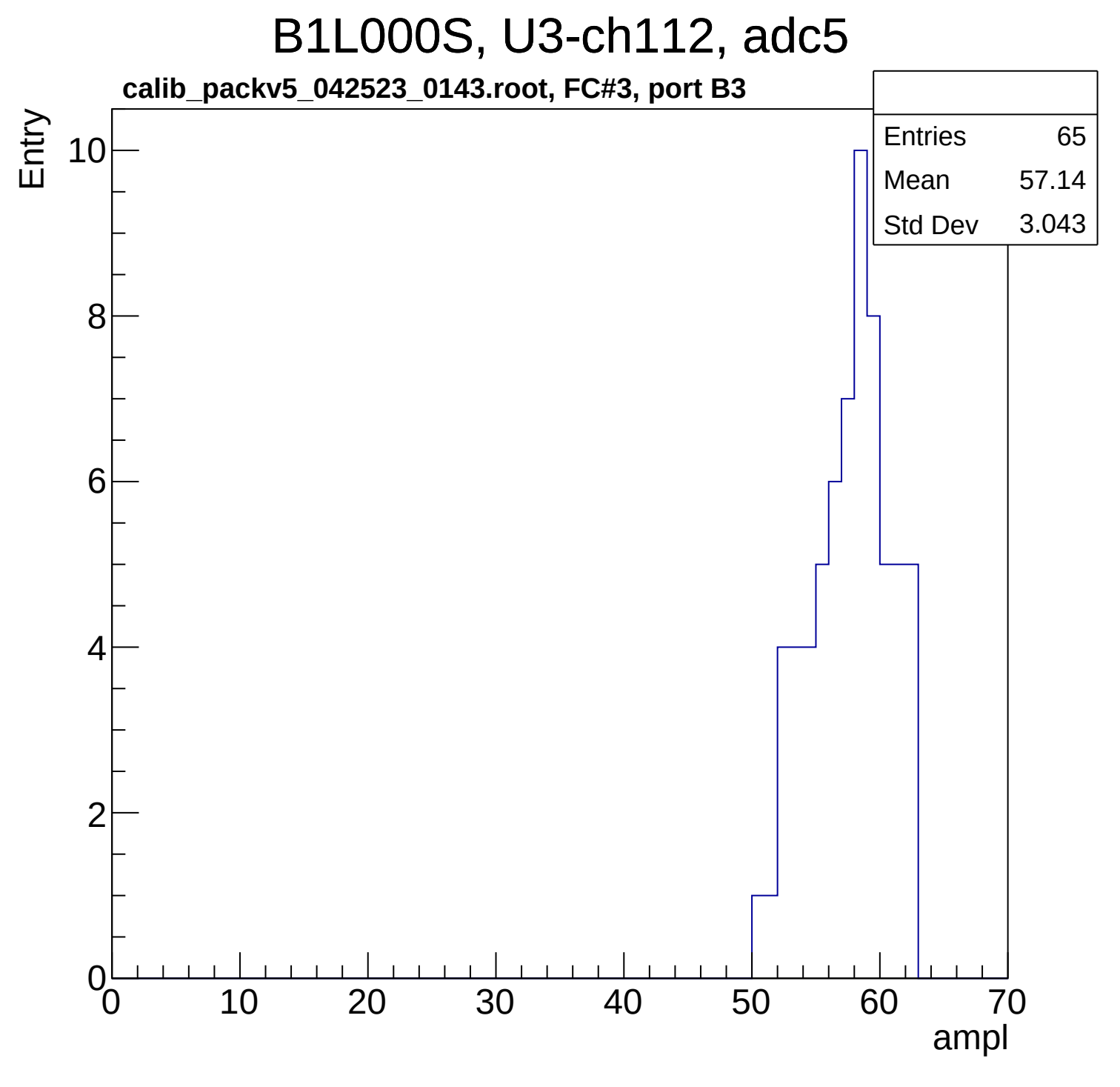
B1L000S, U3-ch112, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 57.14
Std Dev 3.043
Entry
ampl
0
2
4
6
8
10
0
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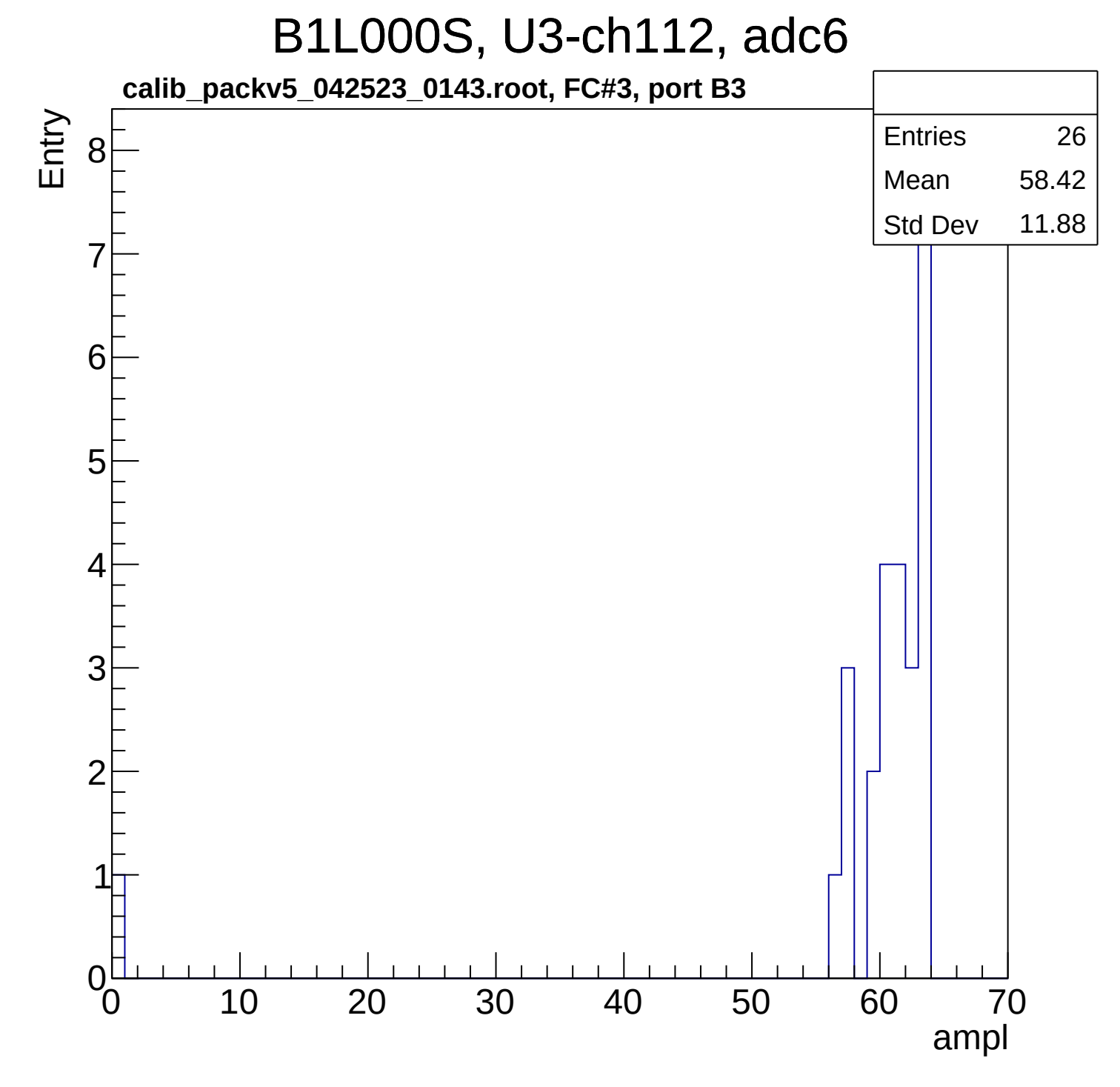
B1L000S, U3-ch112, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 58.42
Std Dev 11.88
Entry
ampl
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1
2
3
4
5
6
7
8
0
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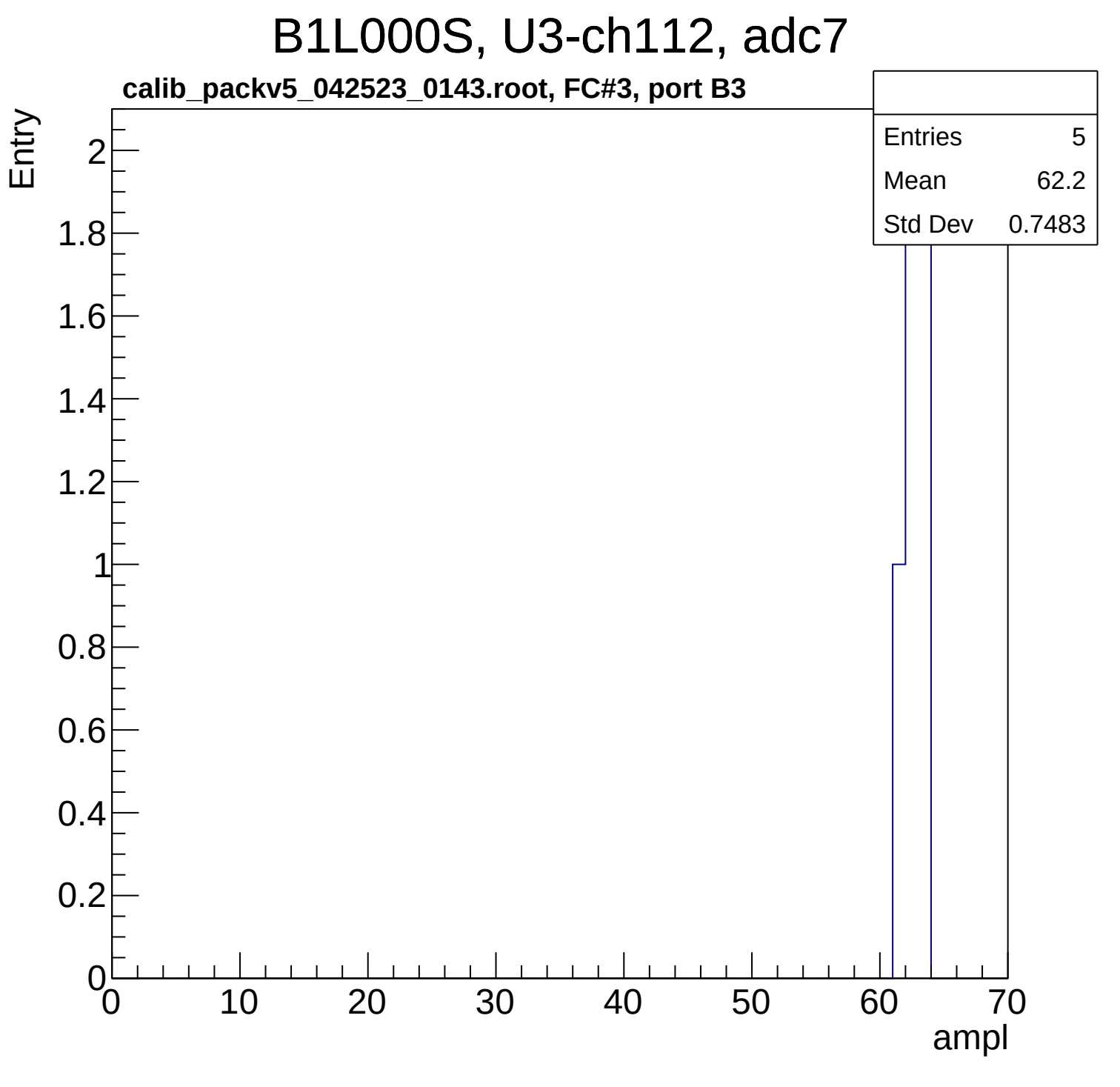
B1L000S, U3-ch112, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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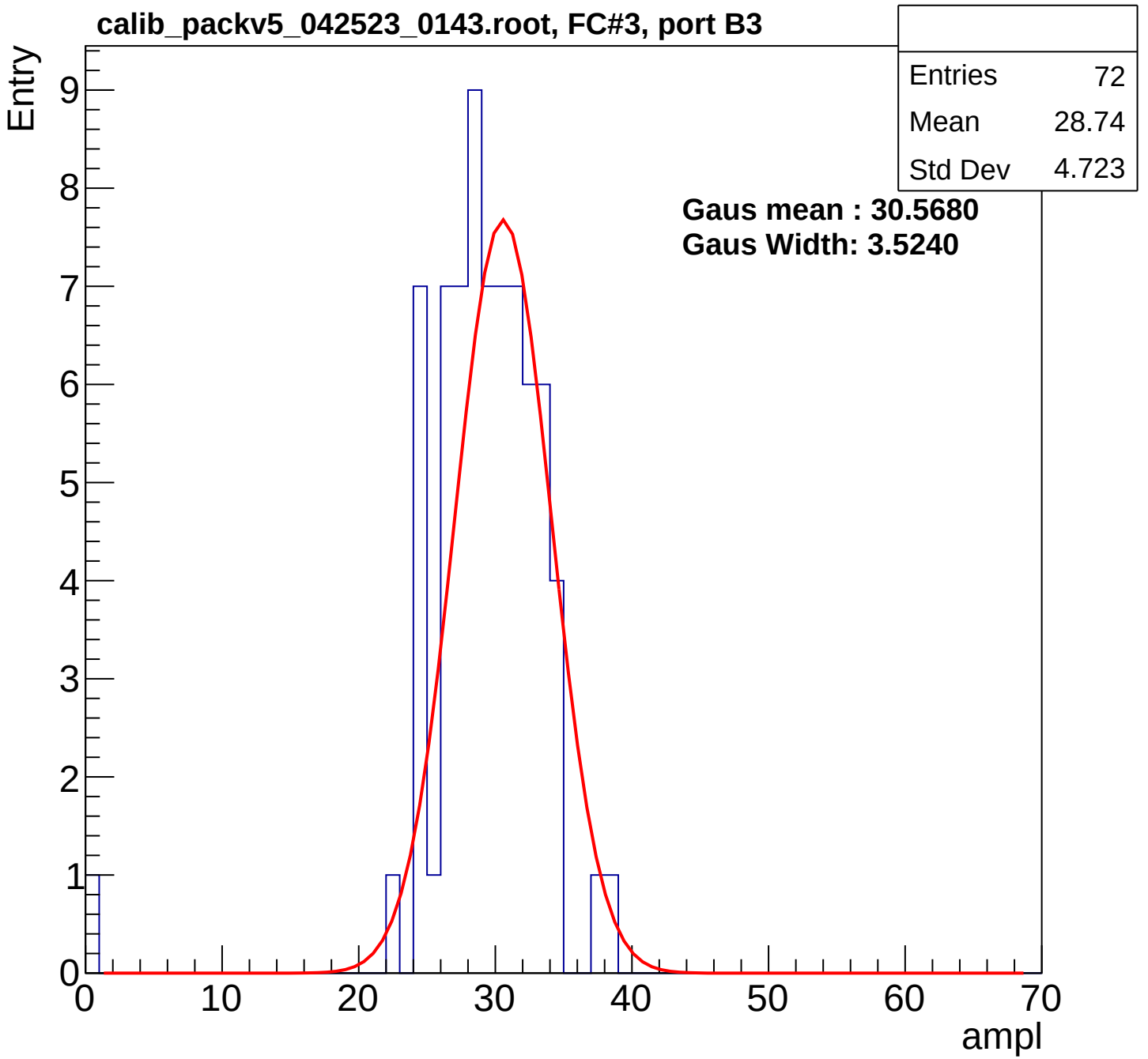
B1L000S, U3-ch113, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 28.74
Std Dev 4.723
Gaus mean : 30.5680
Gaus Width: 3.5240
Entry
ampl
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2
3
4
5
6
7
8
9
0
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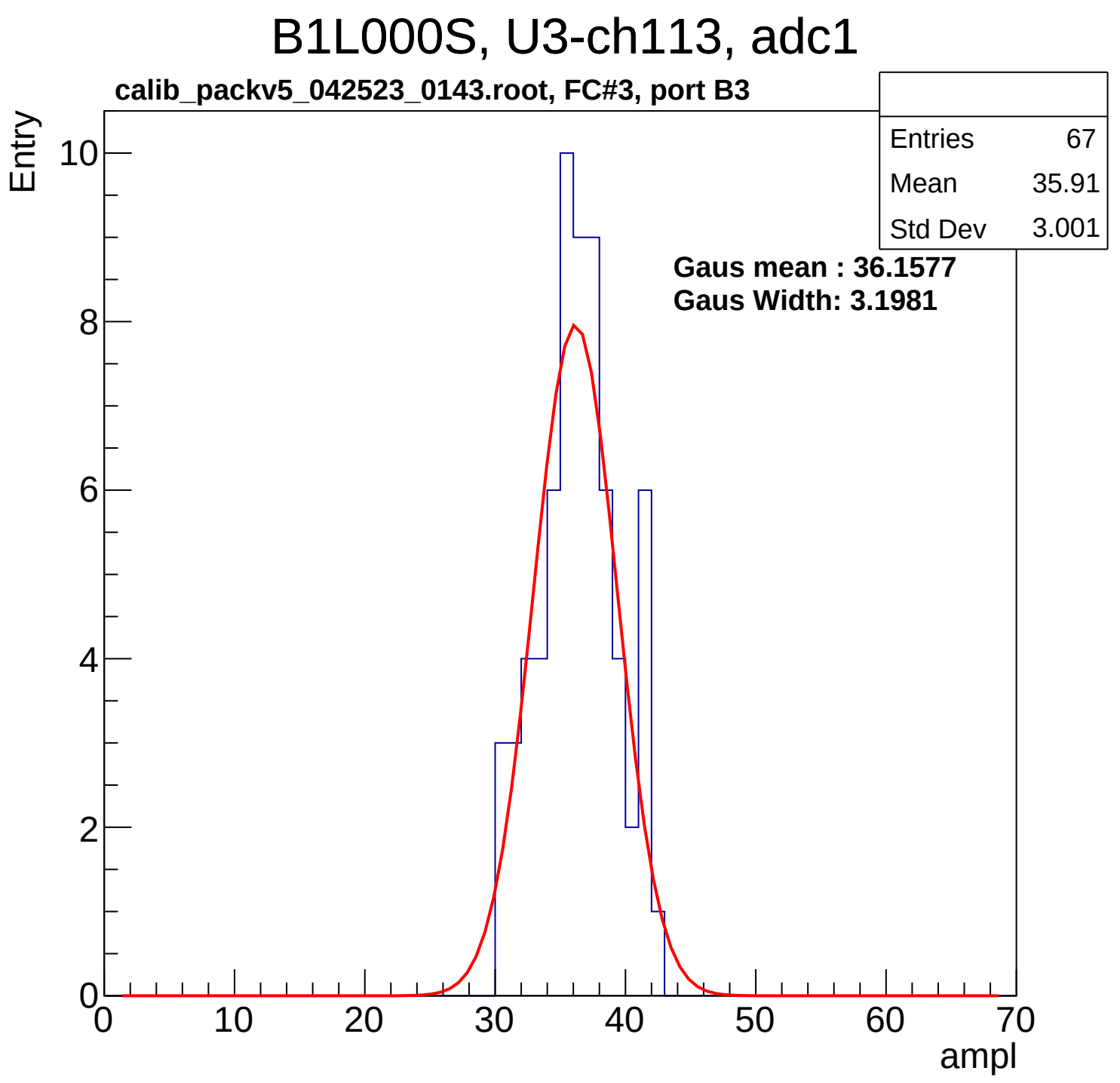
B1L000S, U3-ch113, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.91
Std Dev 3.001
Gaus mean : 36.1577
Gaus Width: 3.1981
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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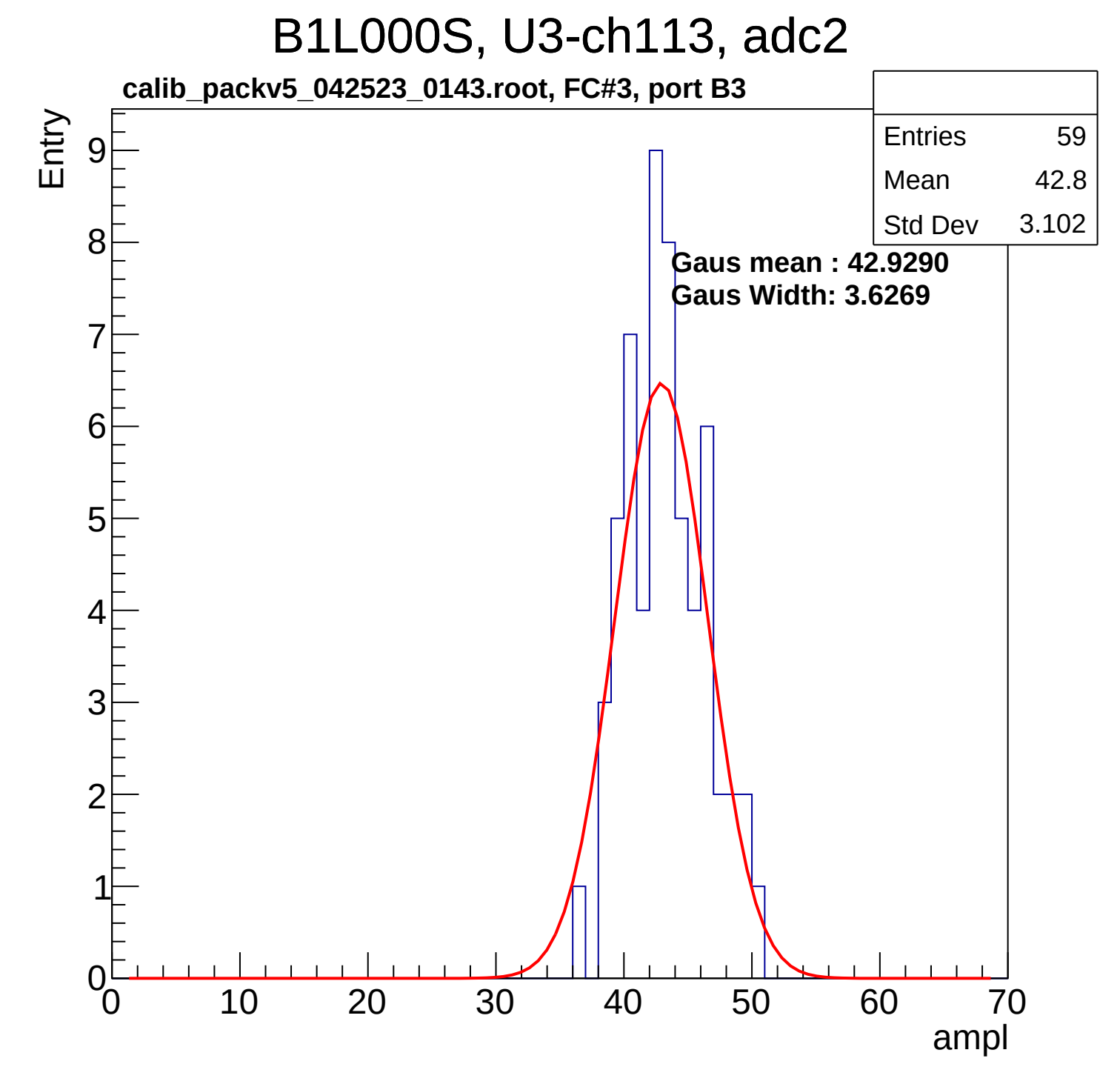
B1L000S, U3-ch113, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 42.8
Std Dev 3.102
Gaus mean : 42.9290
Gaus Width: 3.6269
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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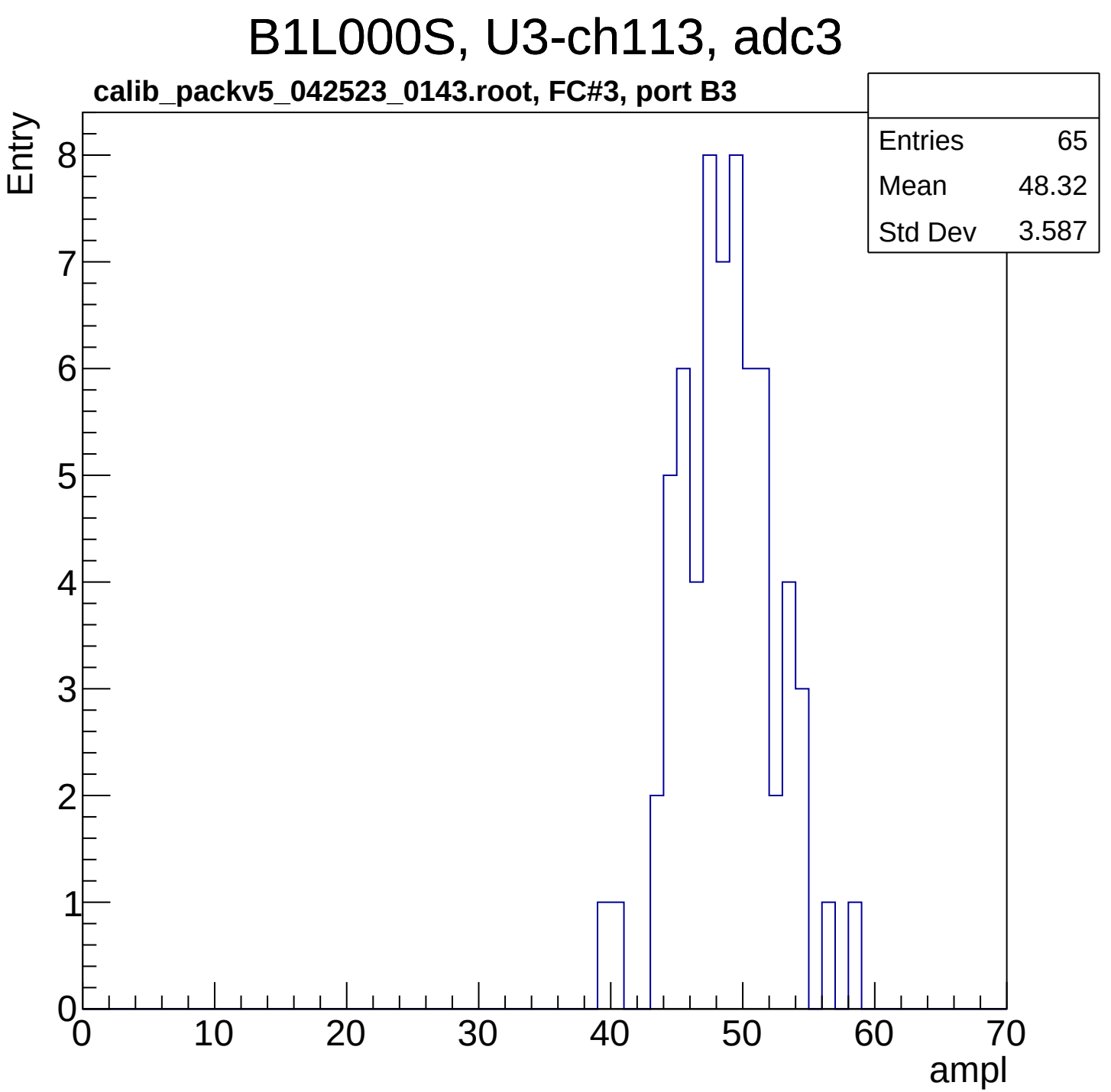
B1L000S, U3-ch113, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 48.32
Std Dev 3.587
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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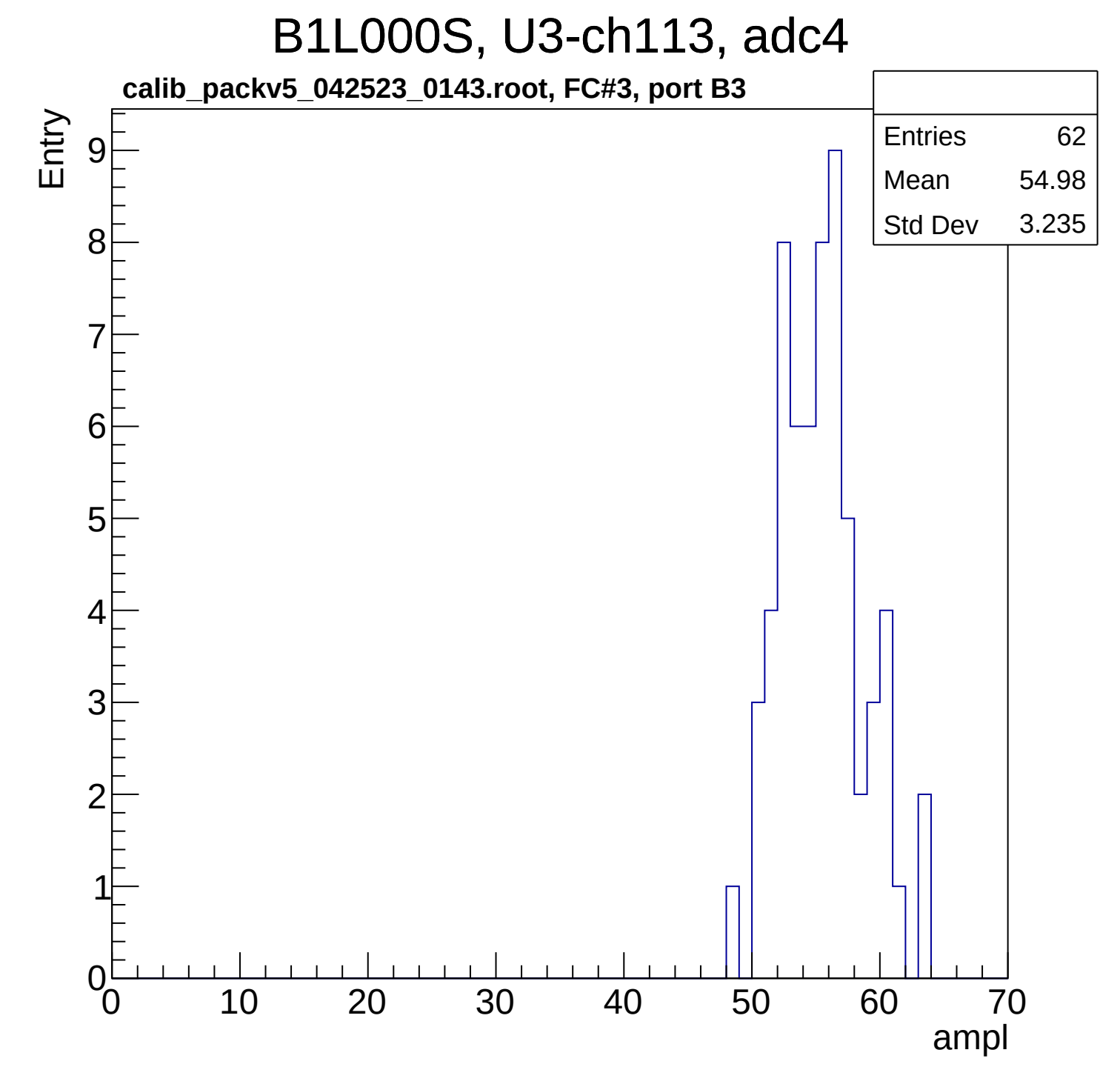
B1L000S, U3-ch113, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 54.98
Std Dev 3.235
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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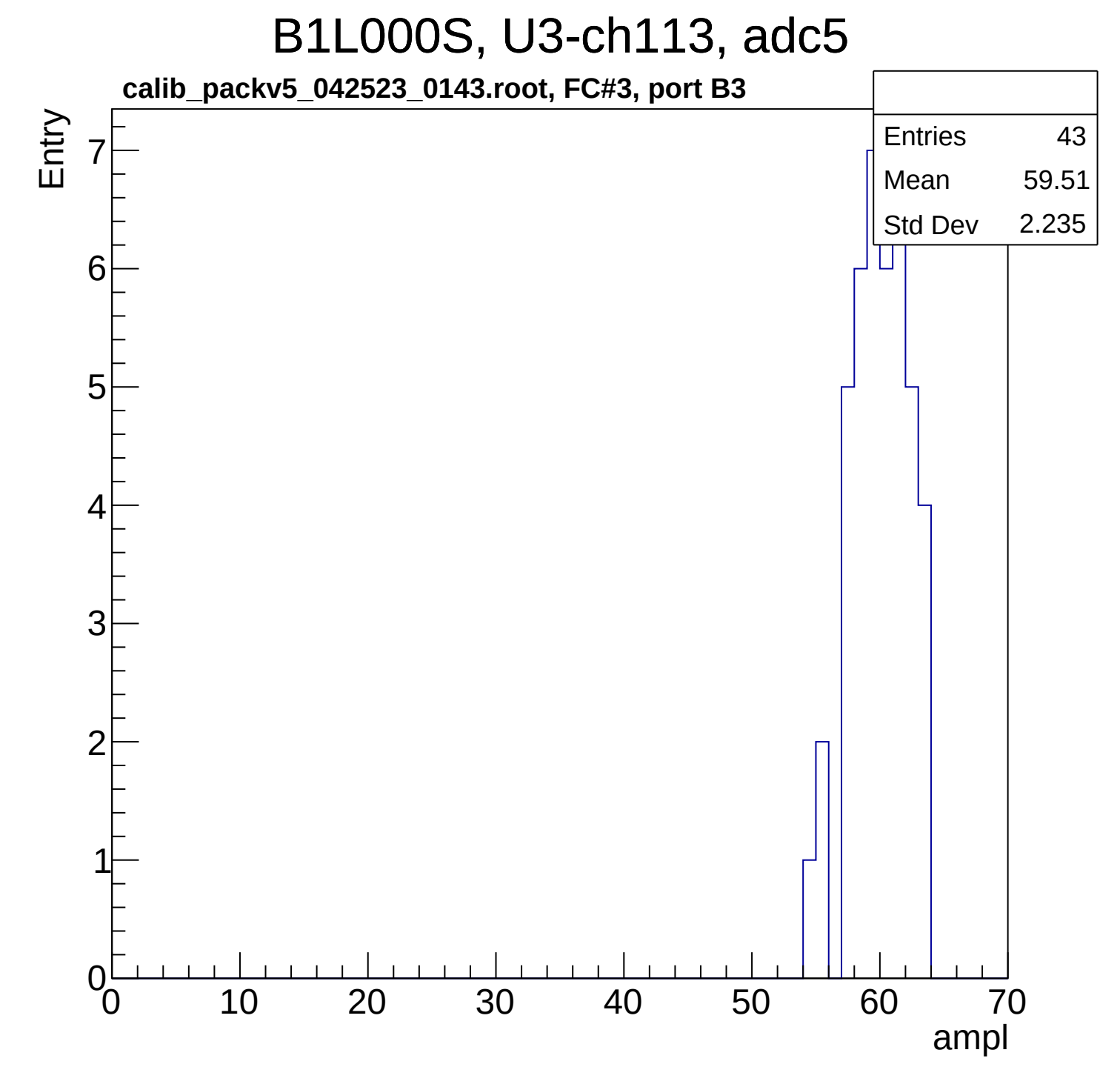
B1L000S, U3-ch113, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 59.51
Std Dev 2.235
Entry
ampl
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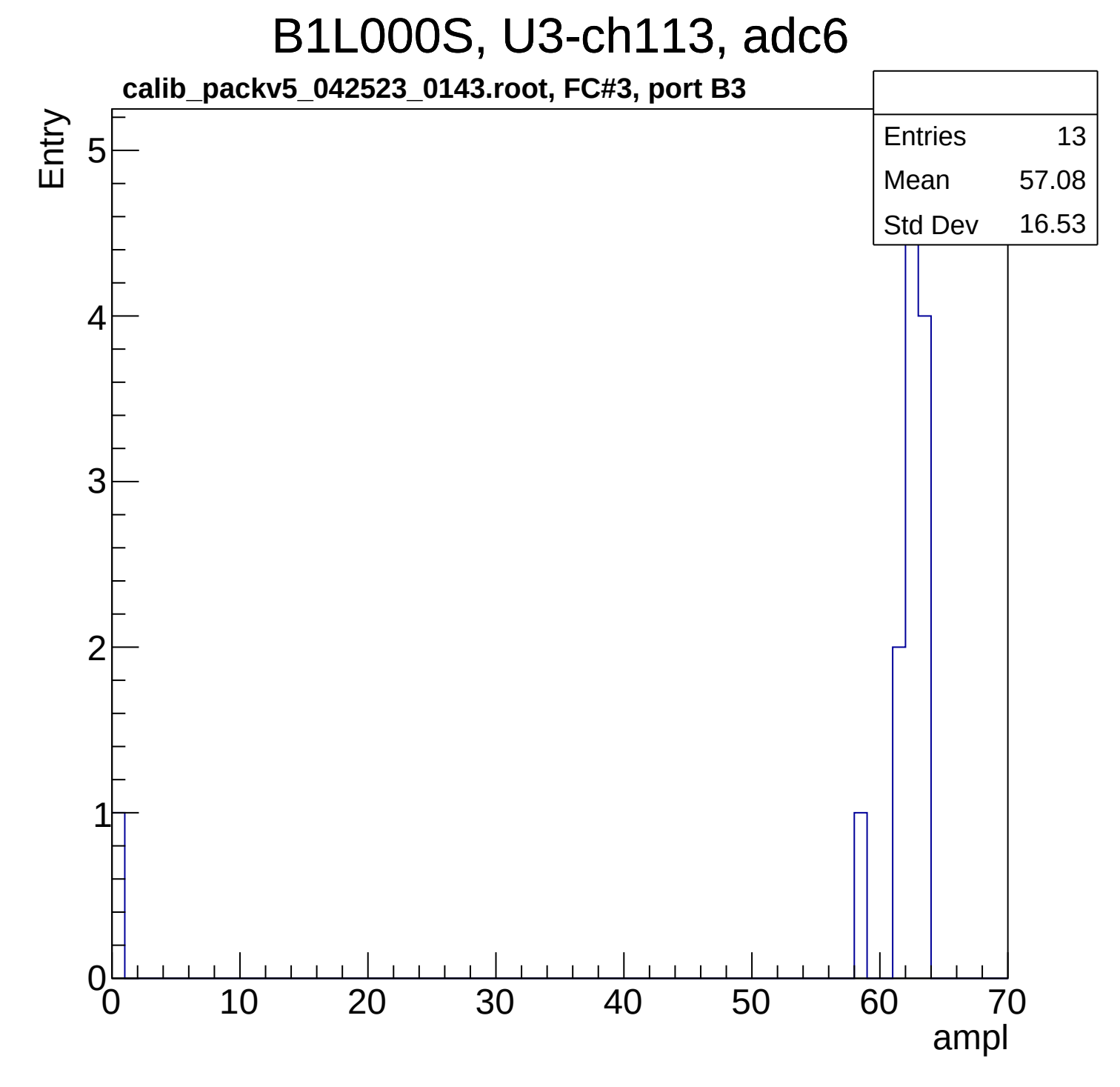
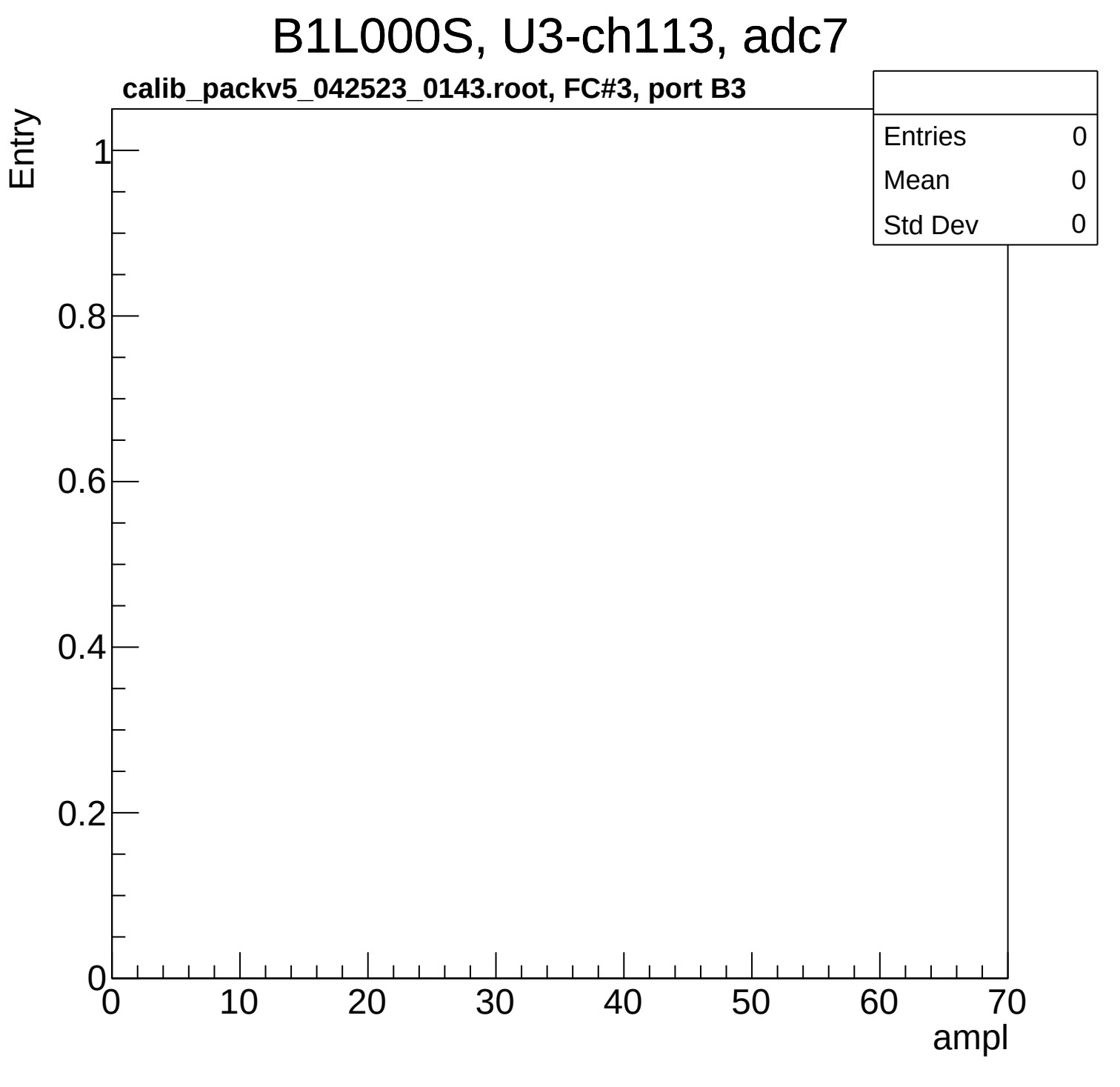
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch113, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.08
Std Dev 16.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch113, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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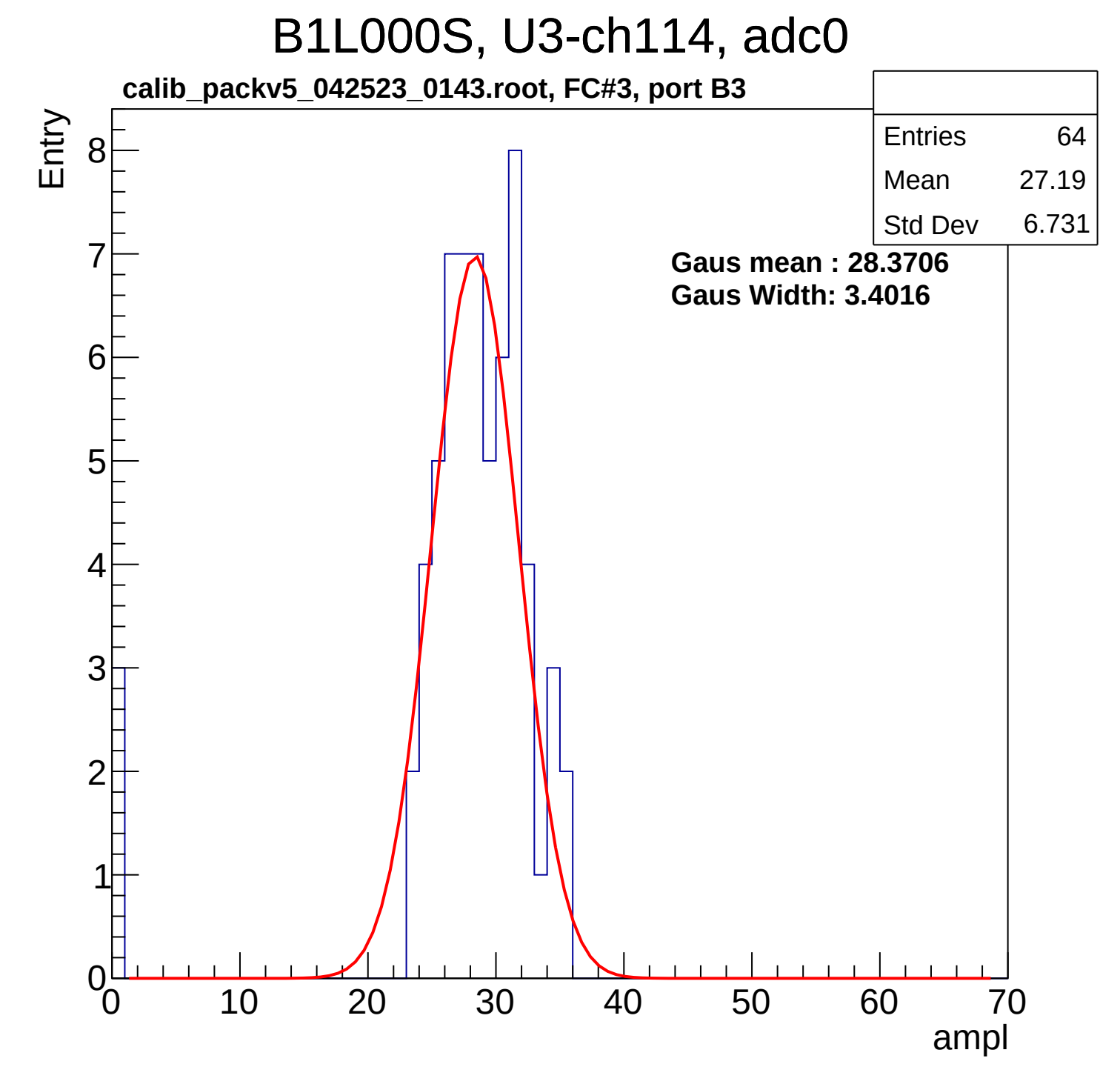
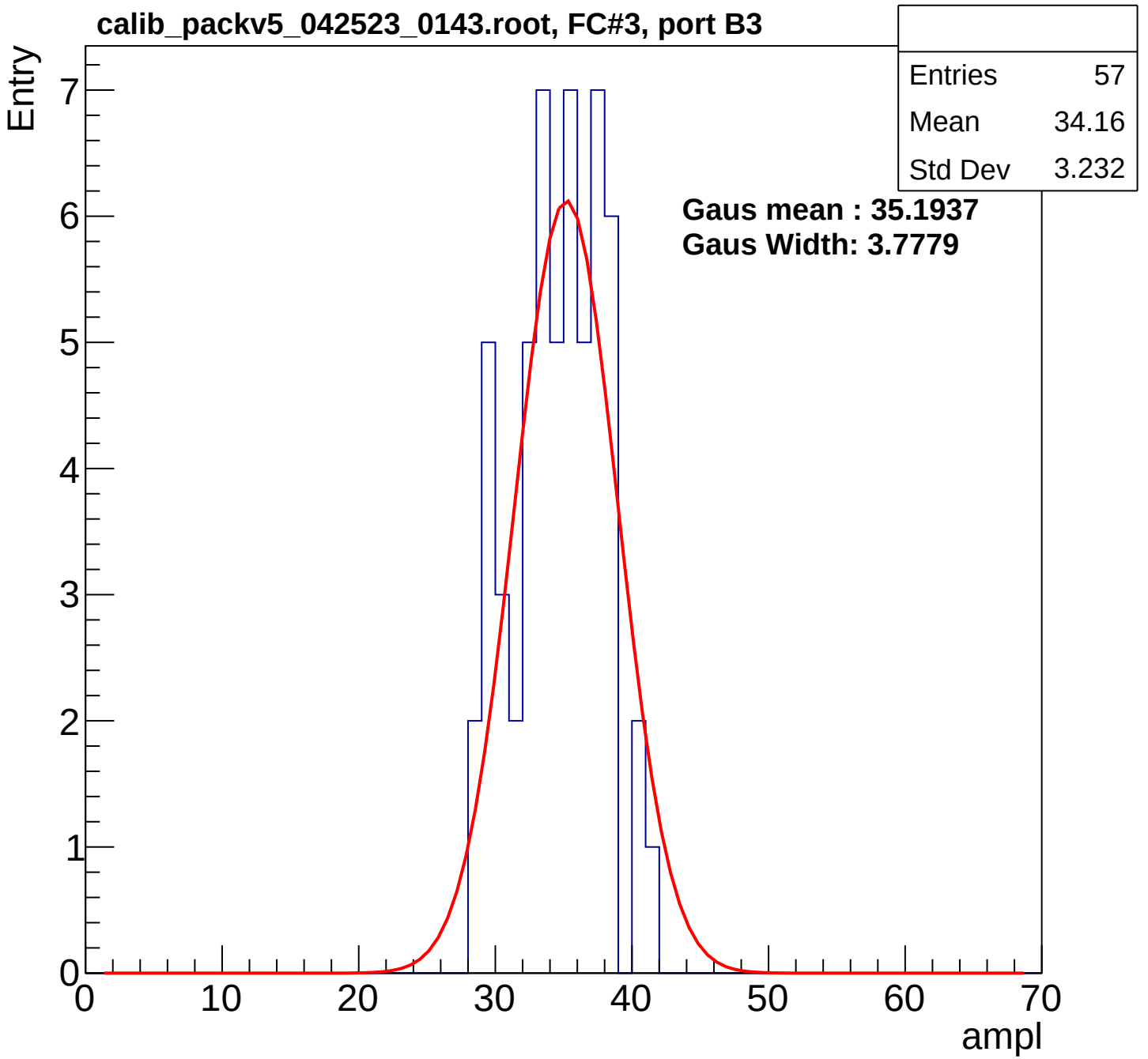
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch114, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 27.19
Std Dev 6.731
Gaus mean : 28.3706
Gaus Width: 3.4016
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch114, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 34.16
Std Dev 3.232
Gaus mean : 35.1937
Gaus Width: 3.7779
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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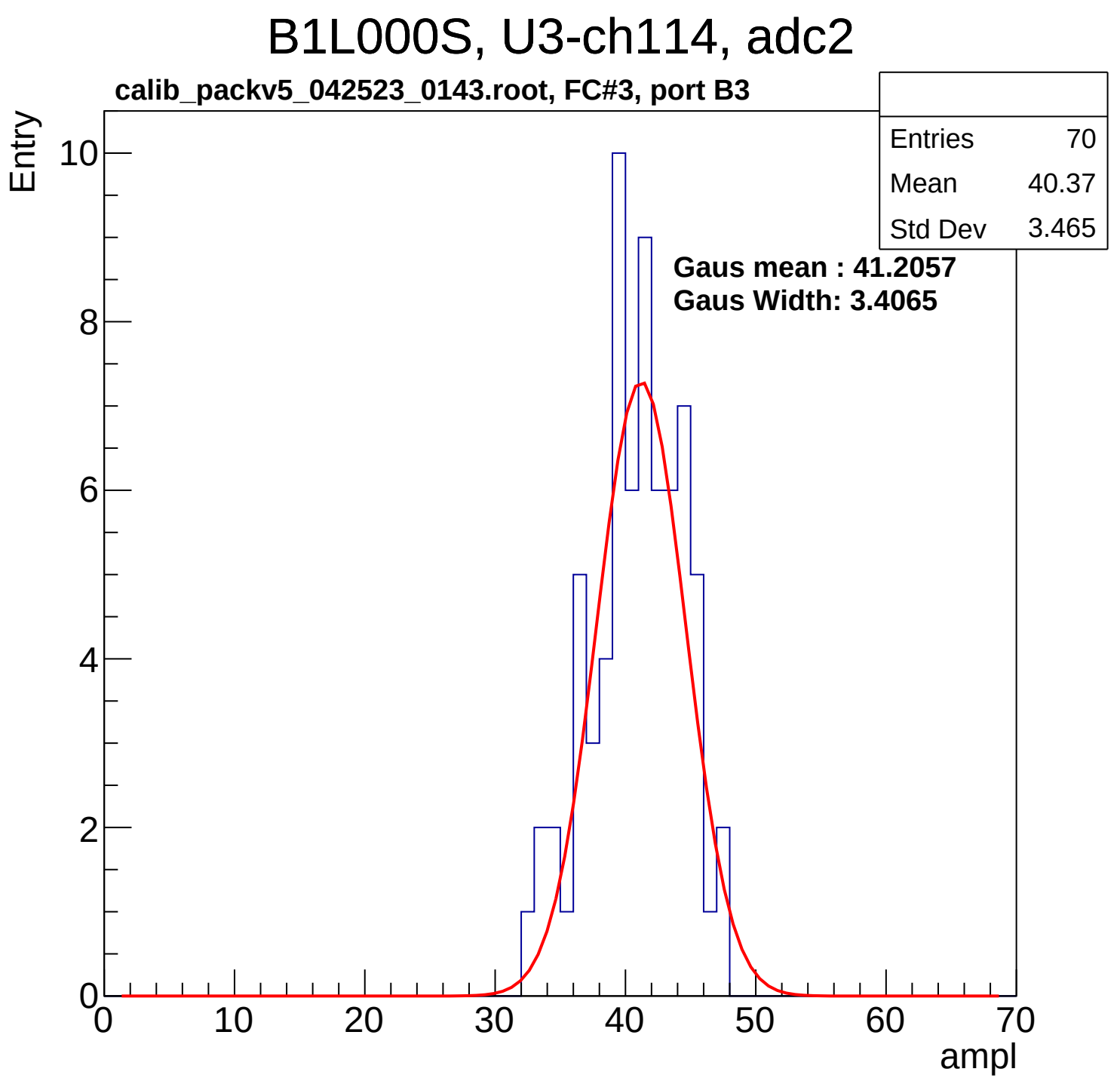
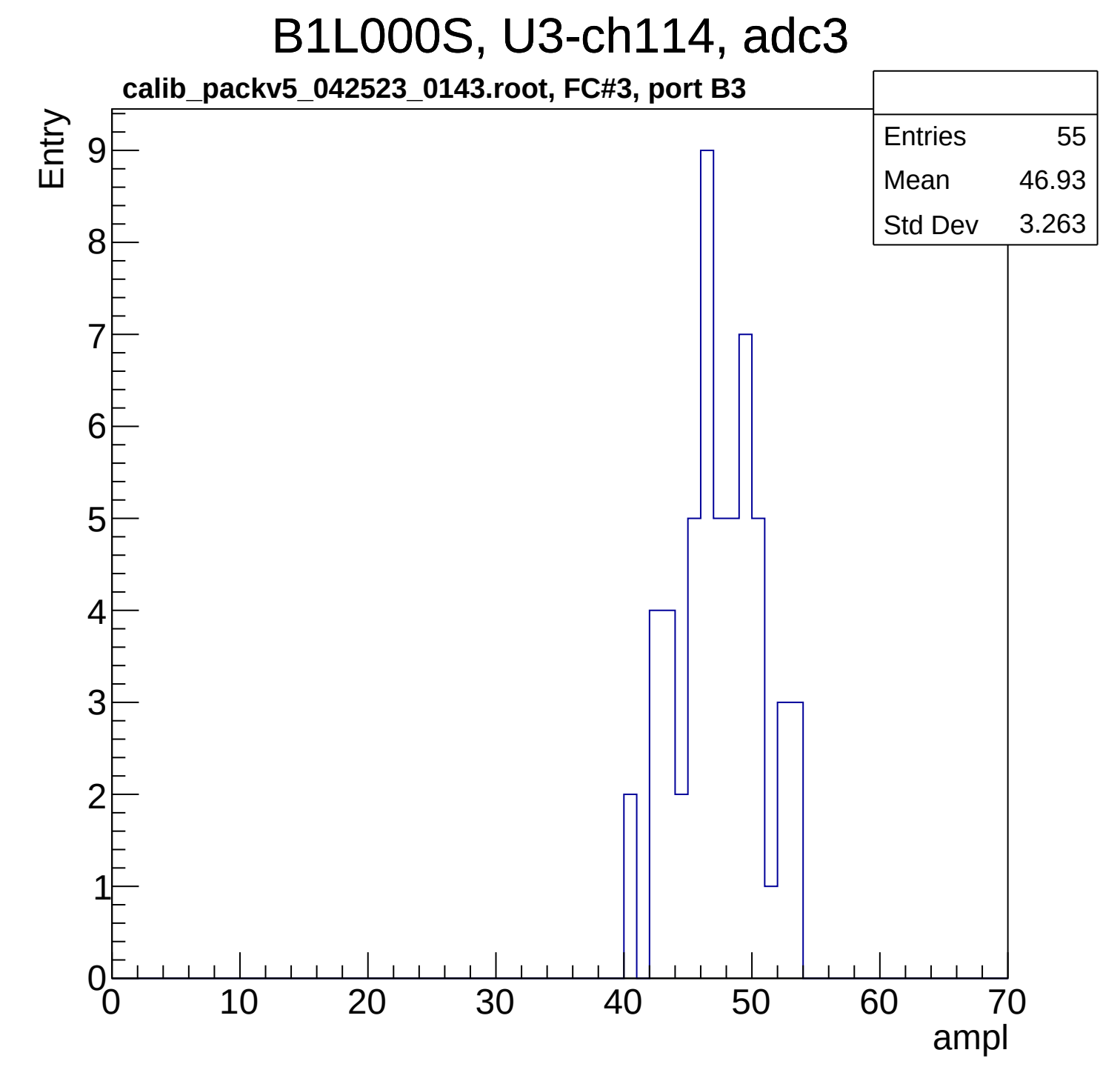

B1L000S, U3-ch114, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 40.37
Std Dev 3.465
Gaus mean : 41.2057
Gaus Width: 3.4065
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch114, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 46.93
Std Dev 3.263
Entry
ampl
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3
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5
6
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8
9
0
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60
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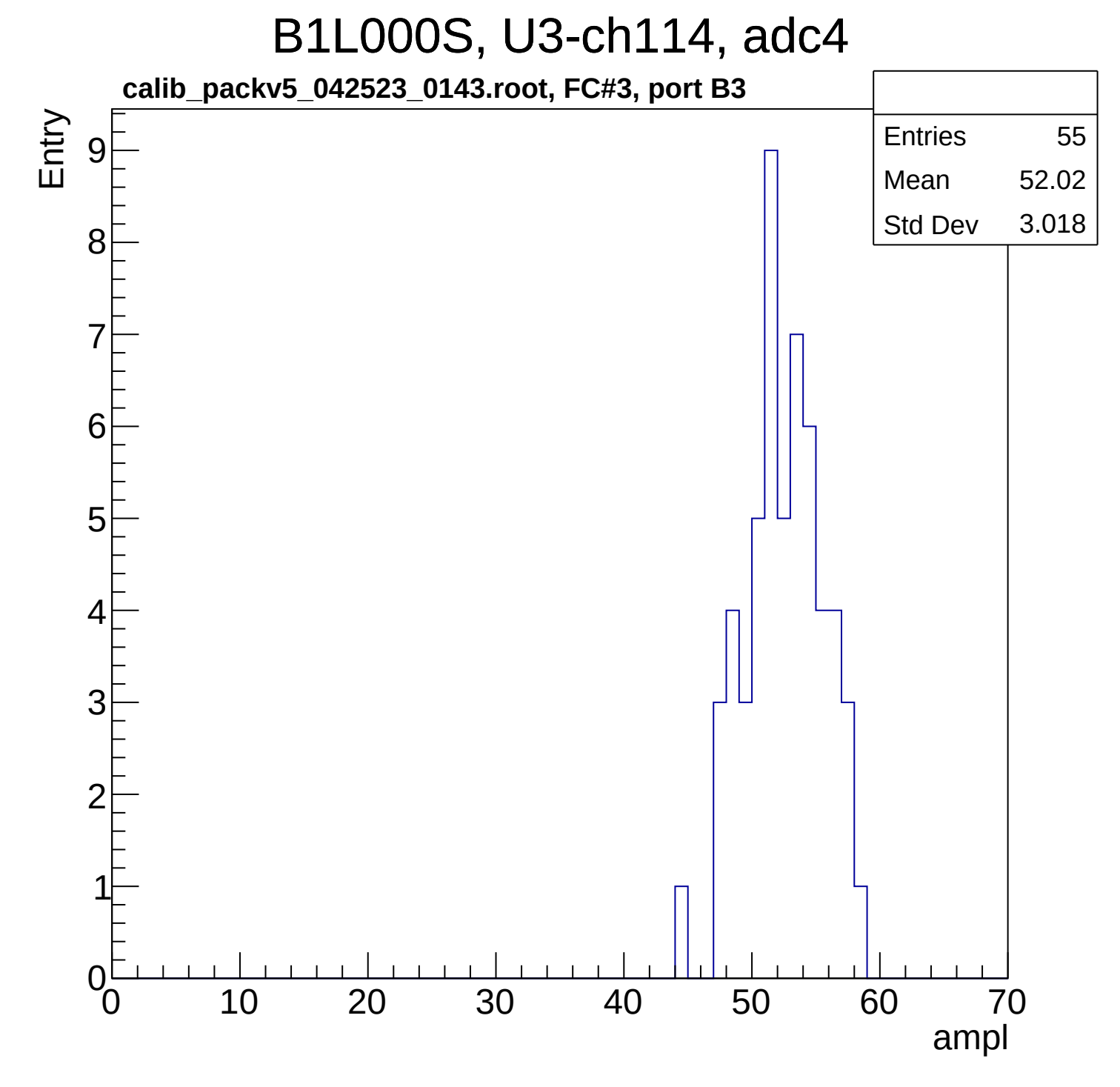
B1L000S, U3-ch114, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 52.02
Std Dev 3.018
Entry
ampl
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3
4
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6
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0
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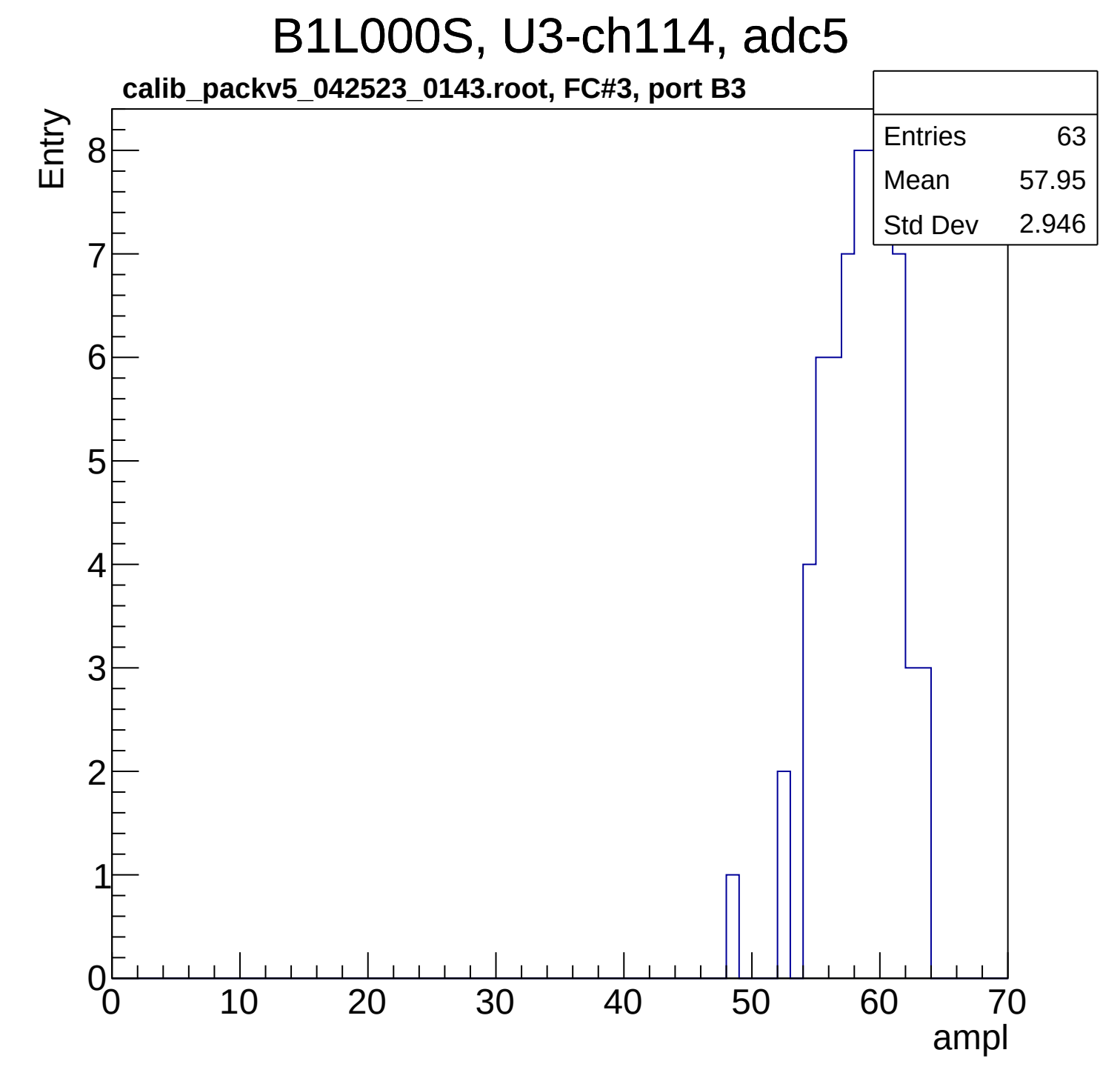
B1L000S, U3-ch114, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 57.95
Std Dev 2.946
Entry
ampl
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3
4
5
6
7
8
0
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60
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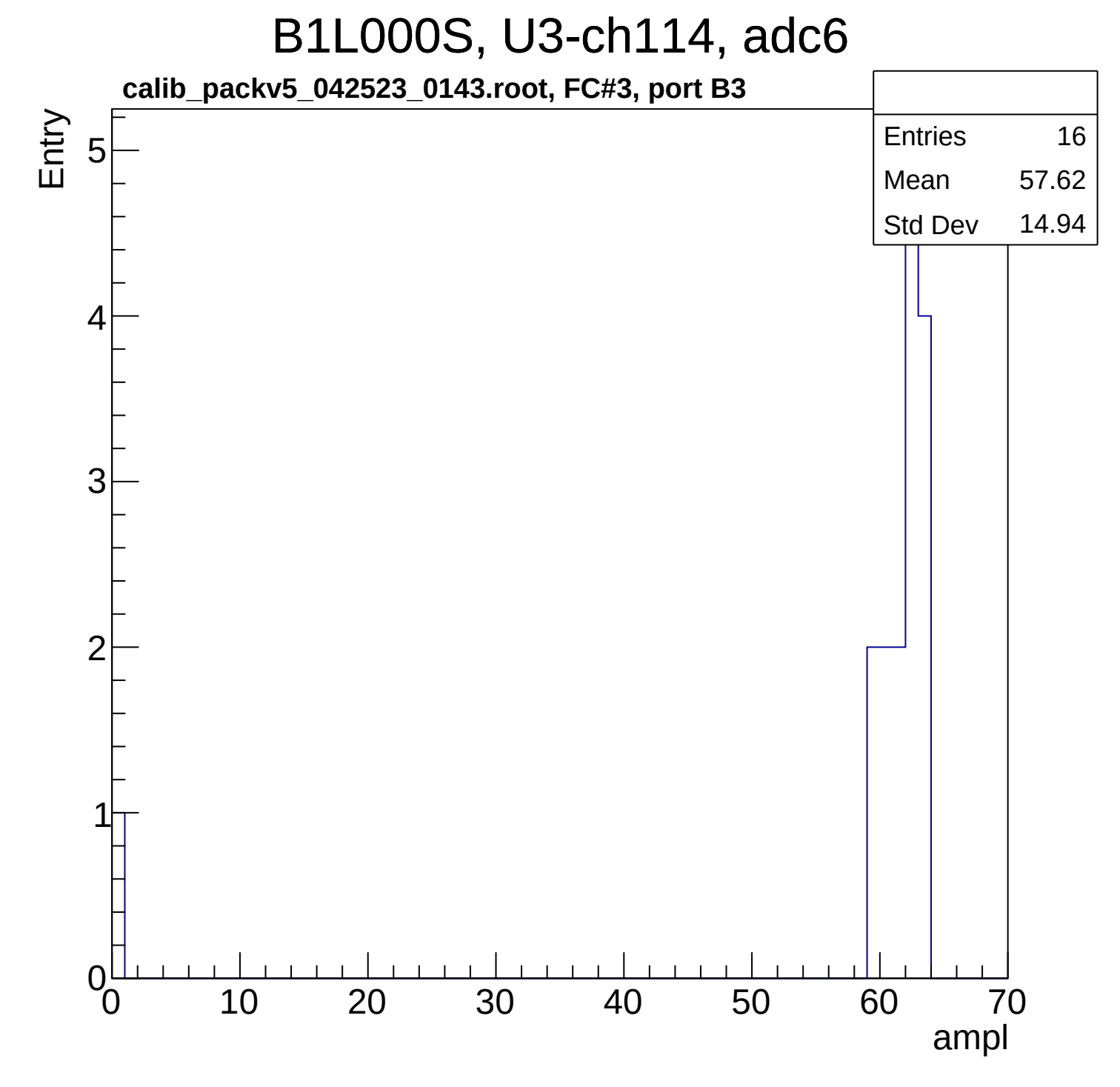
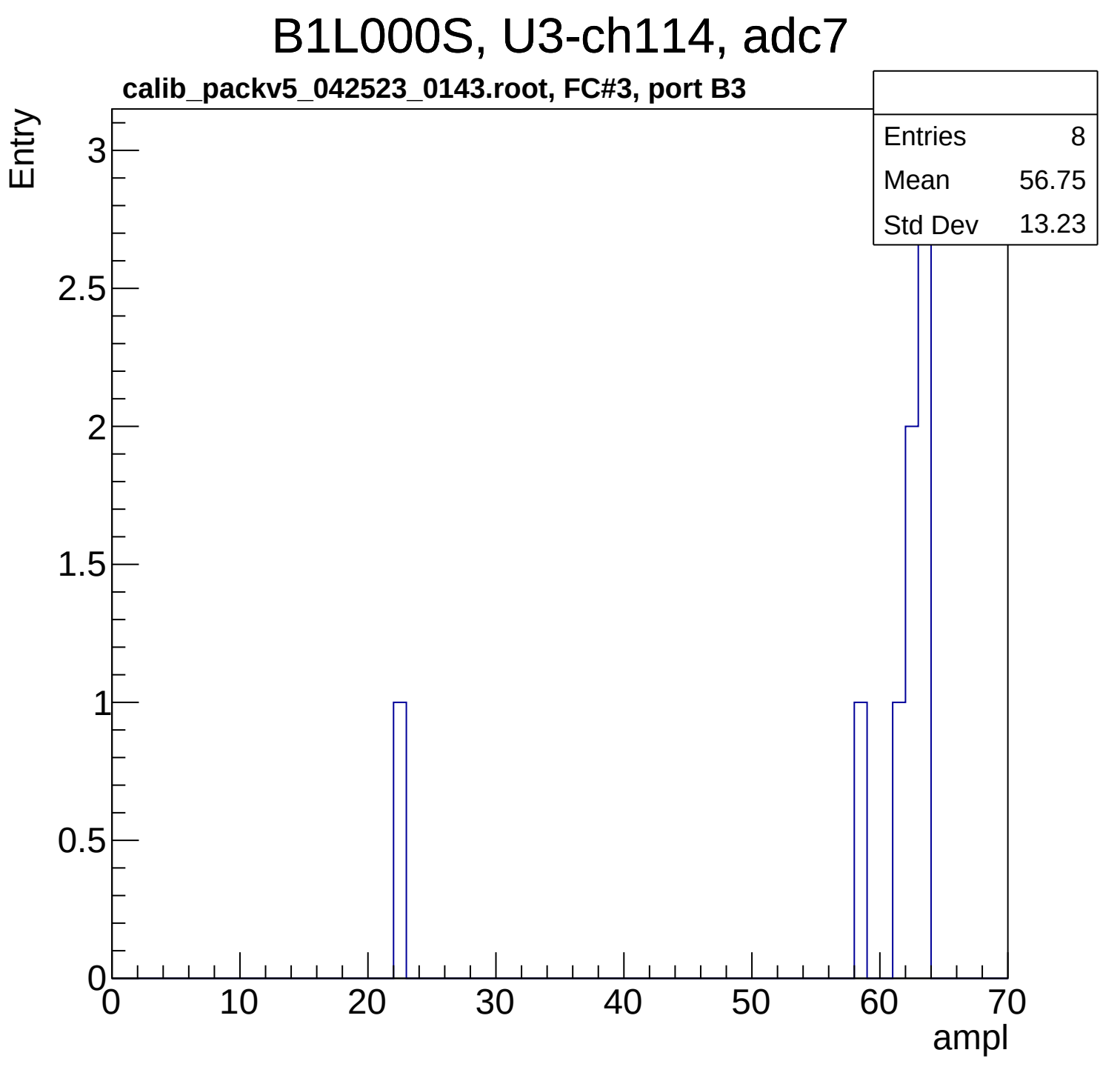

B1L000S, U3-ch114, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.62
Std Dev 14.94
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch114, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 56.75
Std Dev 13.23
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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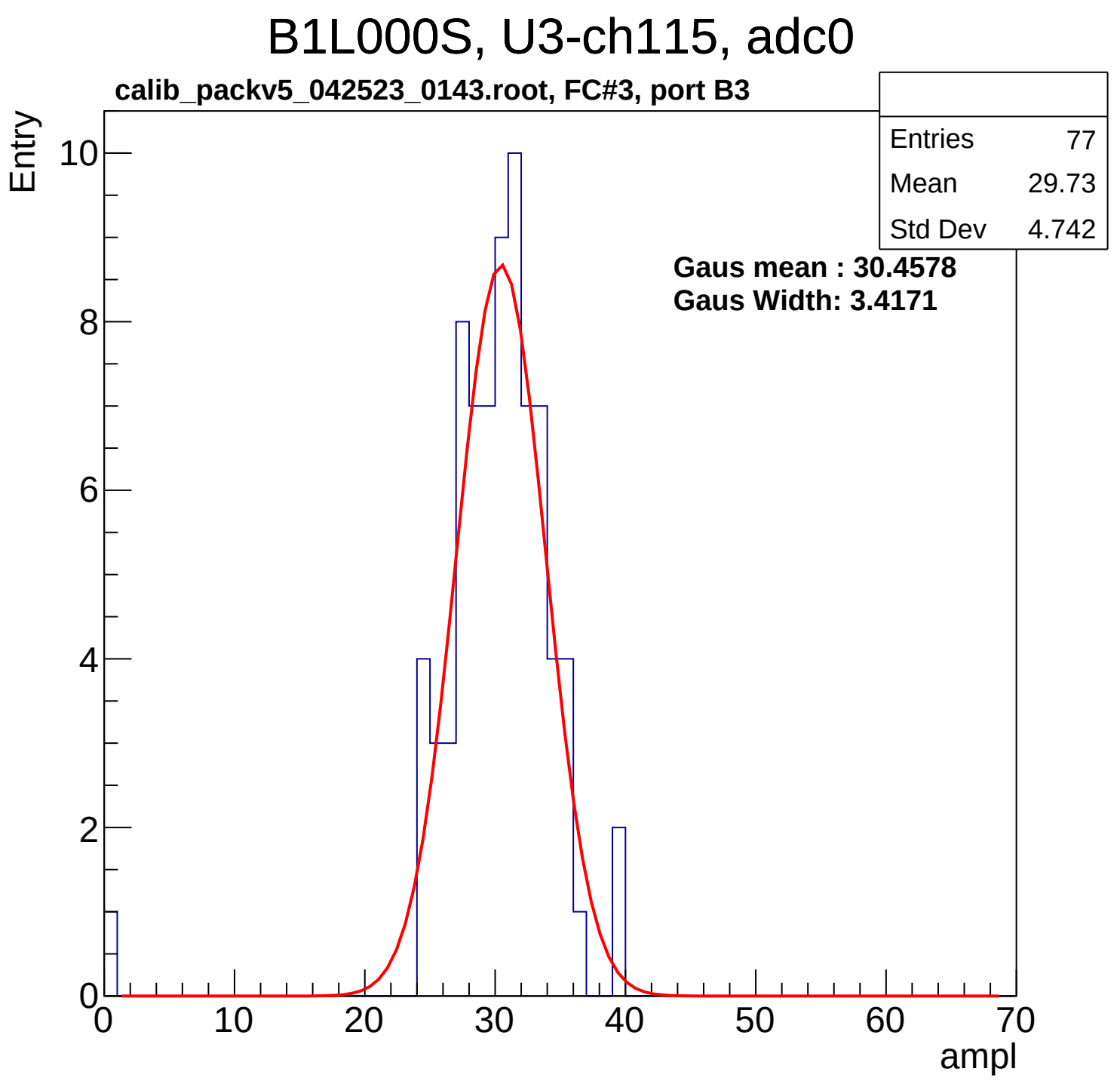
B1L000S, U3-ch115, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 29.73
Std Dev 4.742
Gaus mean : 30.4578
Gaus Width: 3.4171
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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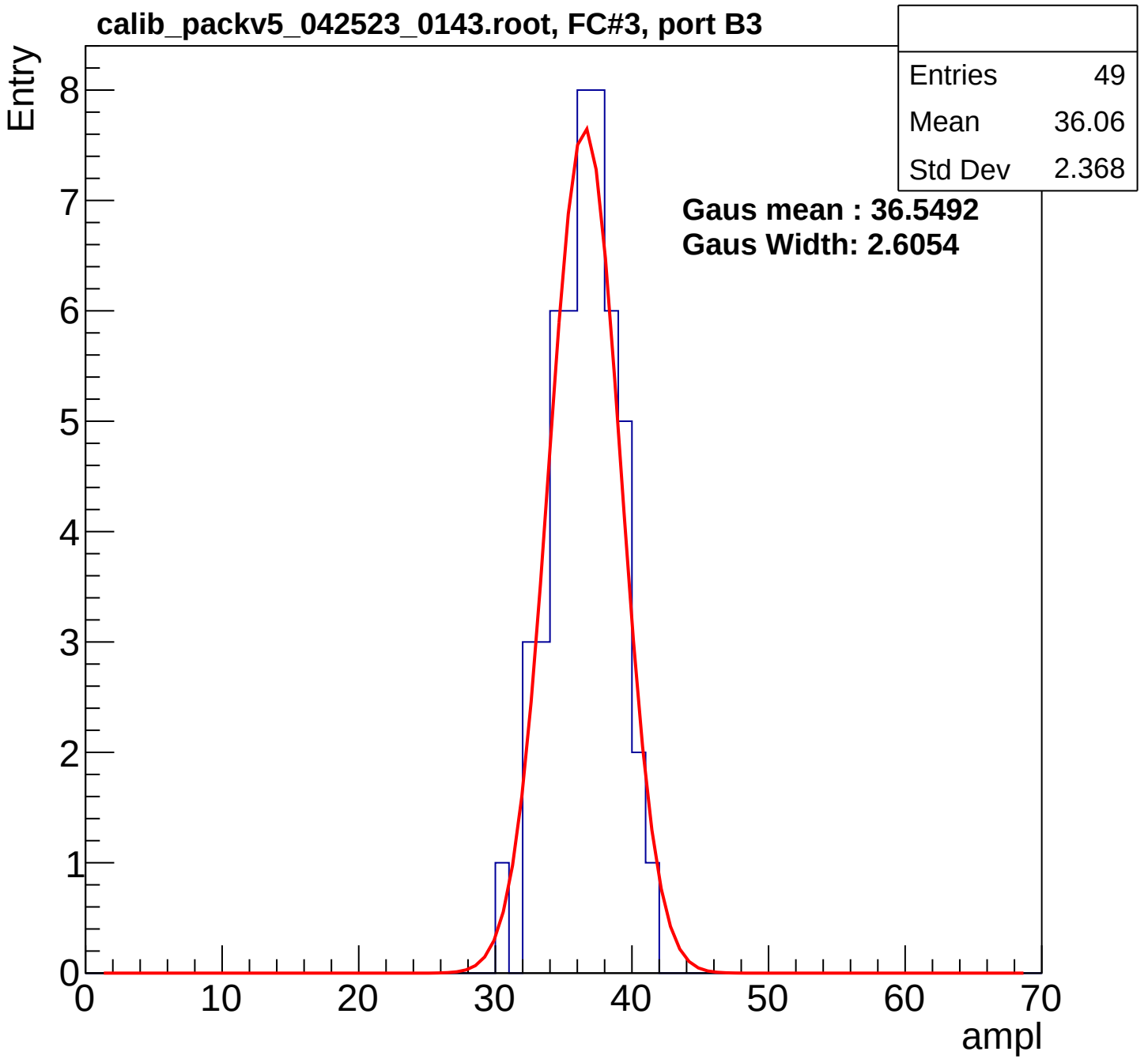
B1L000S, U3-ch115, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 36.06
Std Dev 2.368
Gaus mean : 36.5492
Gaus Width: 2.6054
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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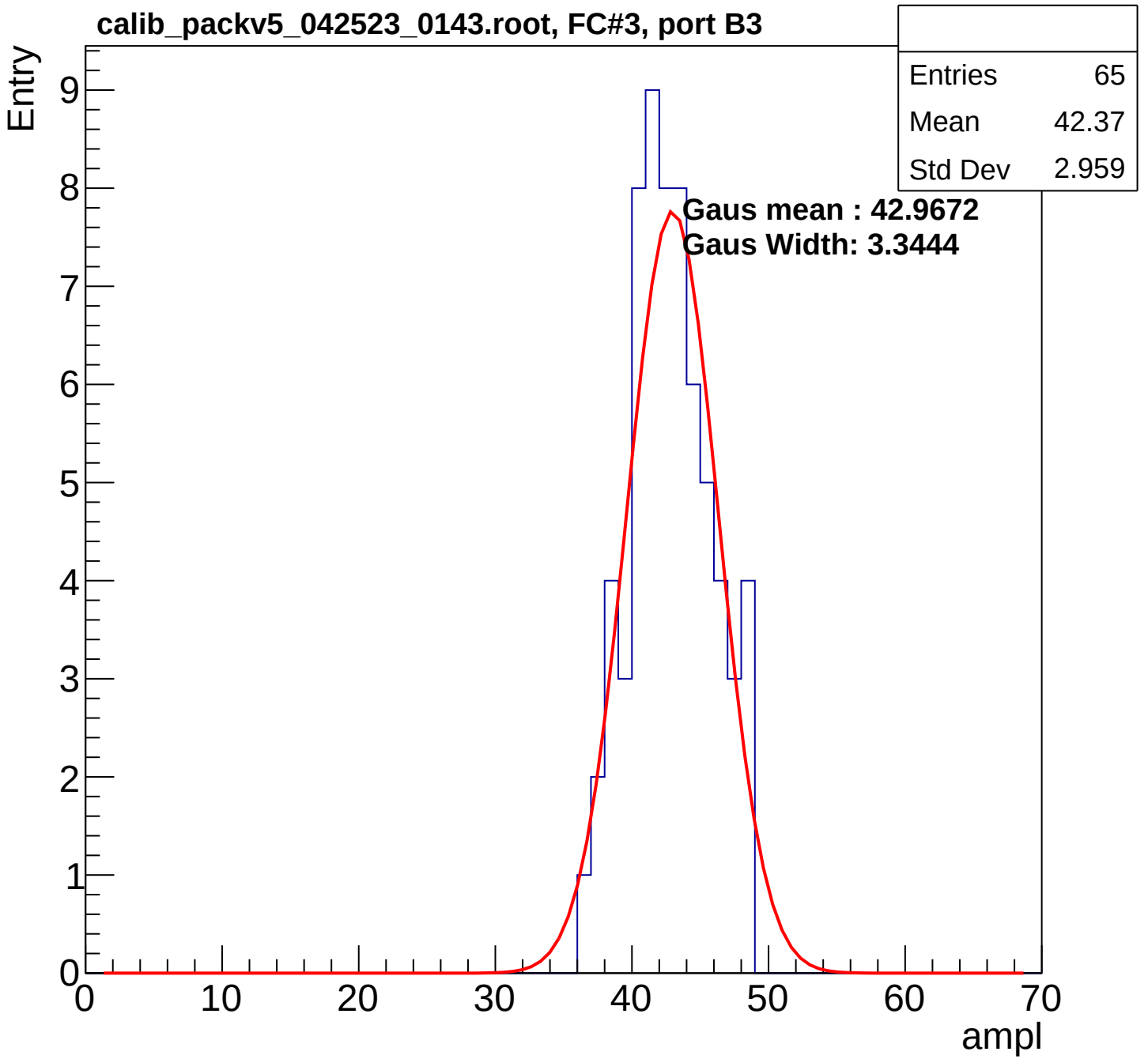
B1L000S, U3-ch115, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 42.37
Std Dev 2.959
Gaus mean : 42.9672
Gaus Width: 3.3444
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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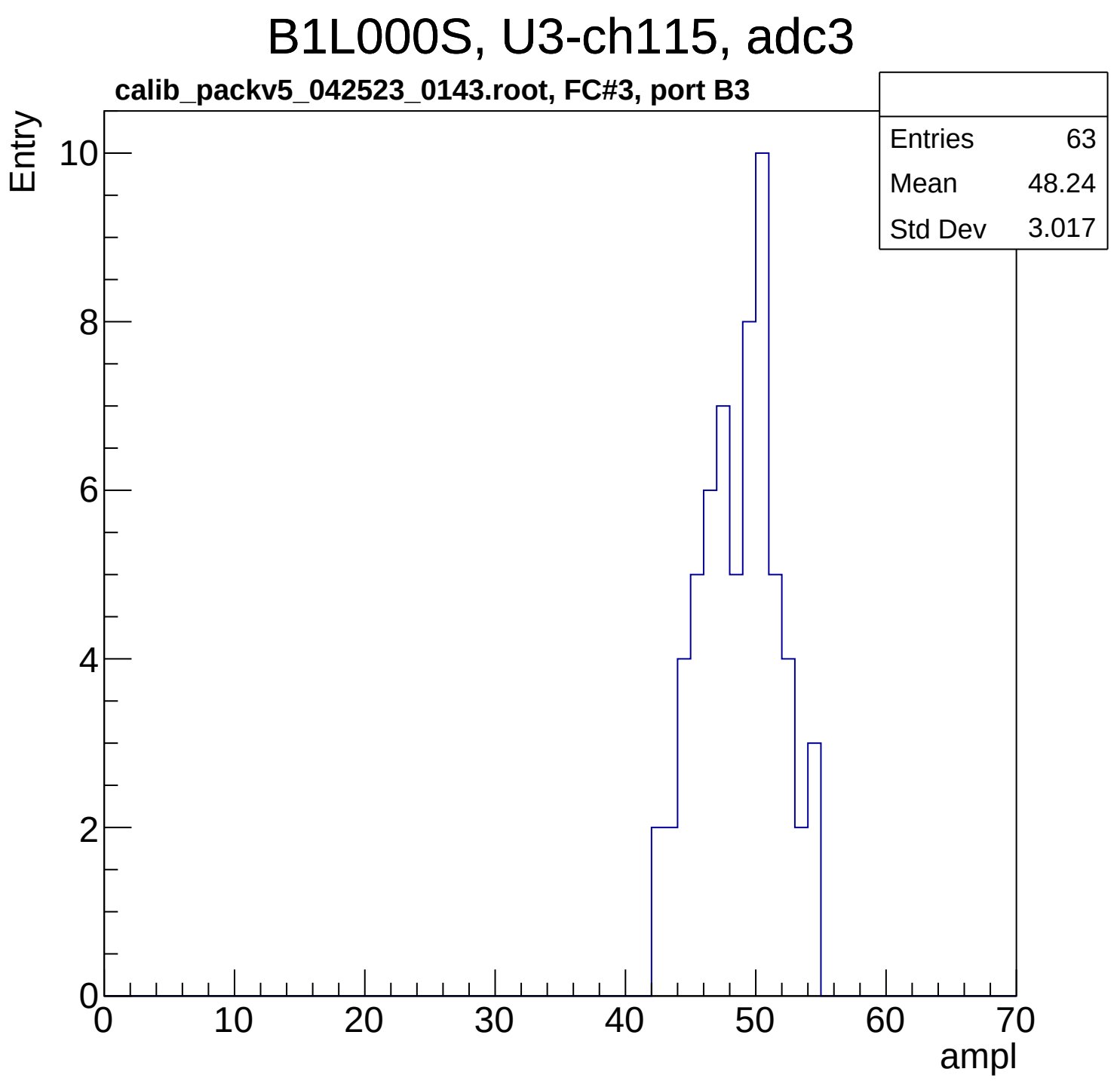
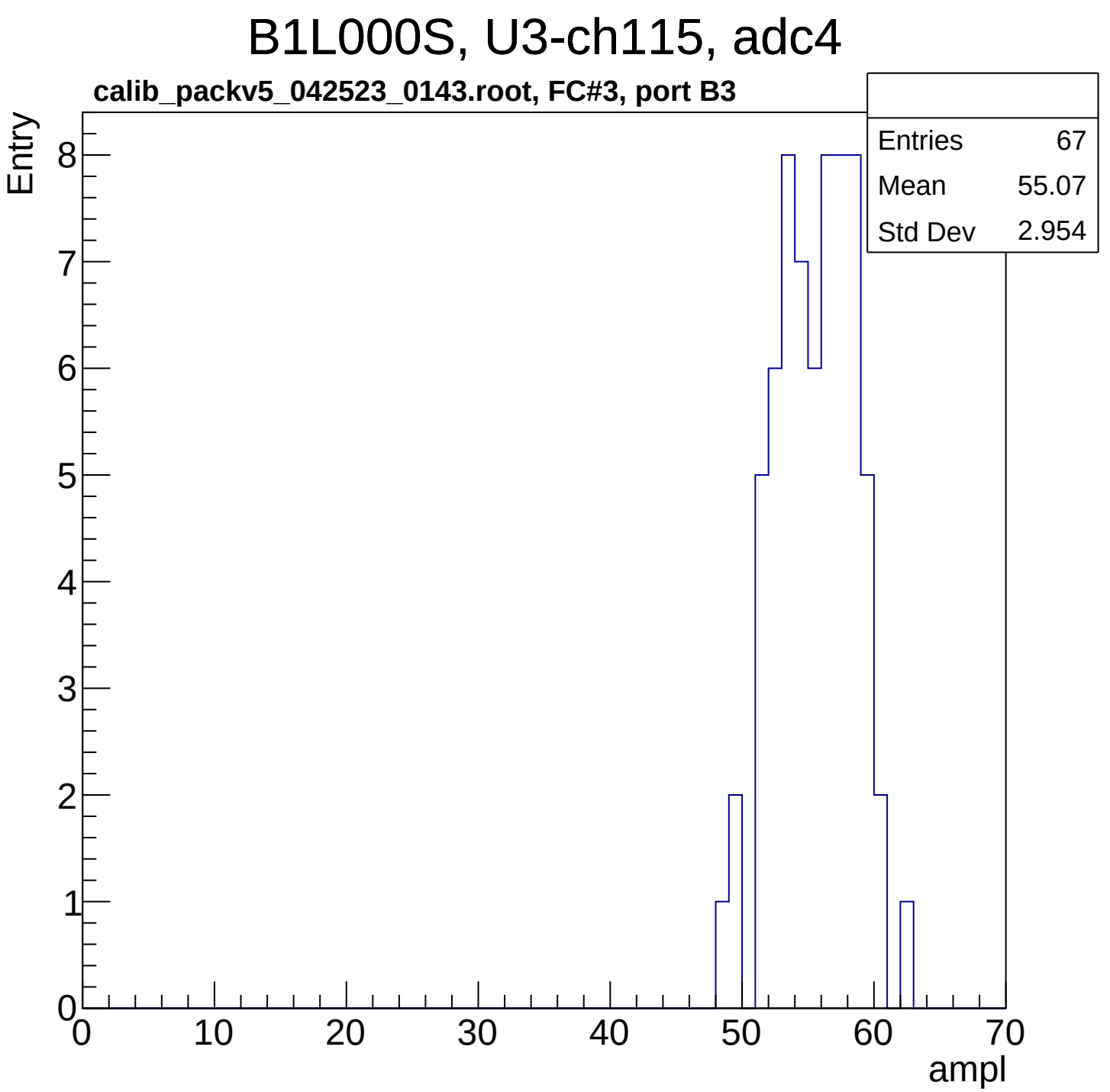

B1L000S, U3-ch115, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 48.24
Std Dev 3.017
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch115, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 55.07
Std Dev 2.954
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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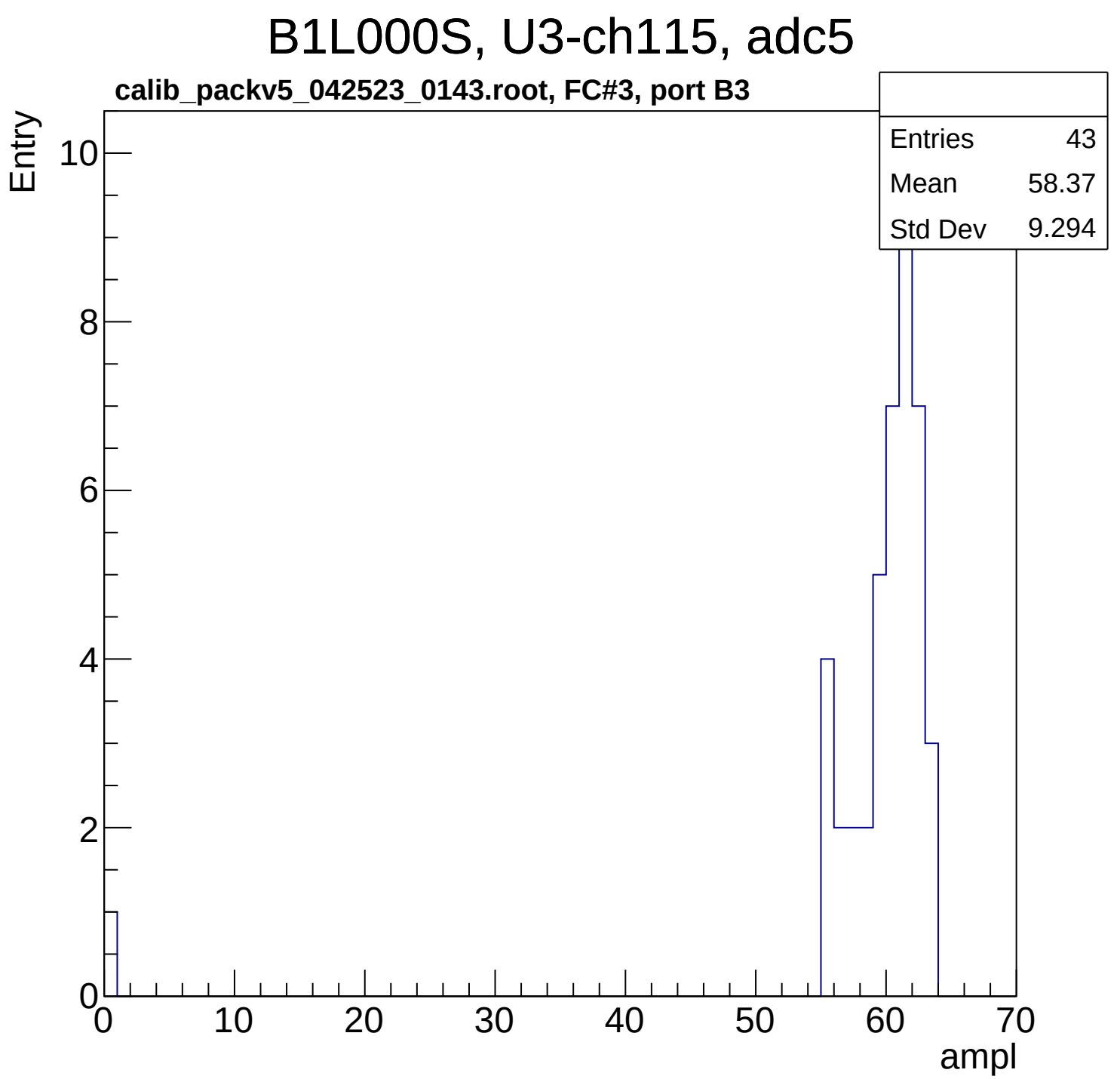
B1L000S, U3-ch115, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 58.37
Std Dev 9.294
Entry
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4
6
8
10
0
10
20
30
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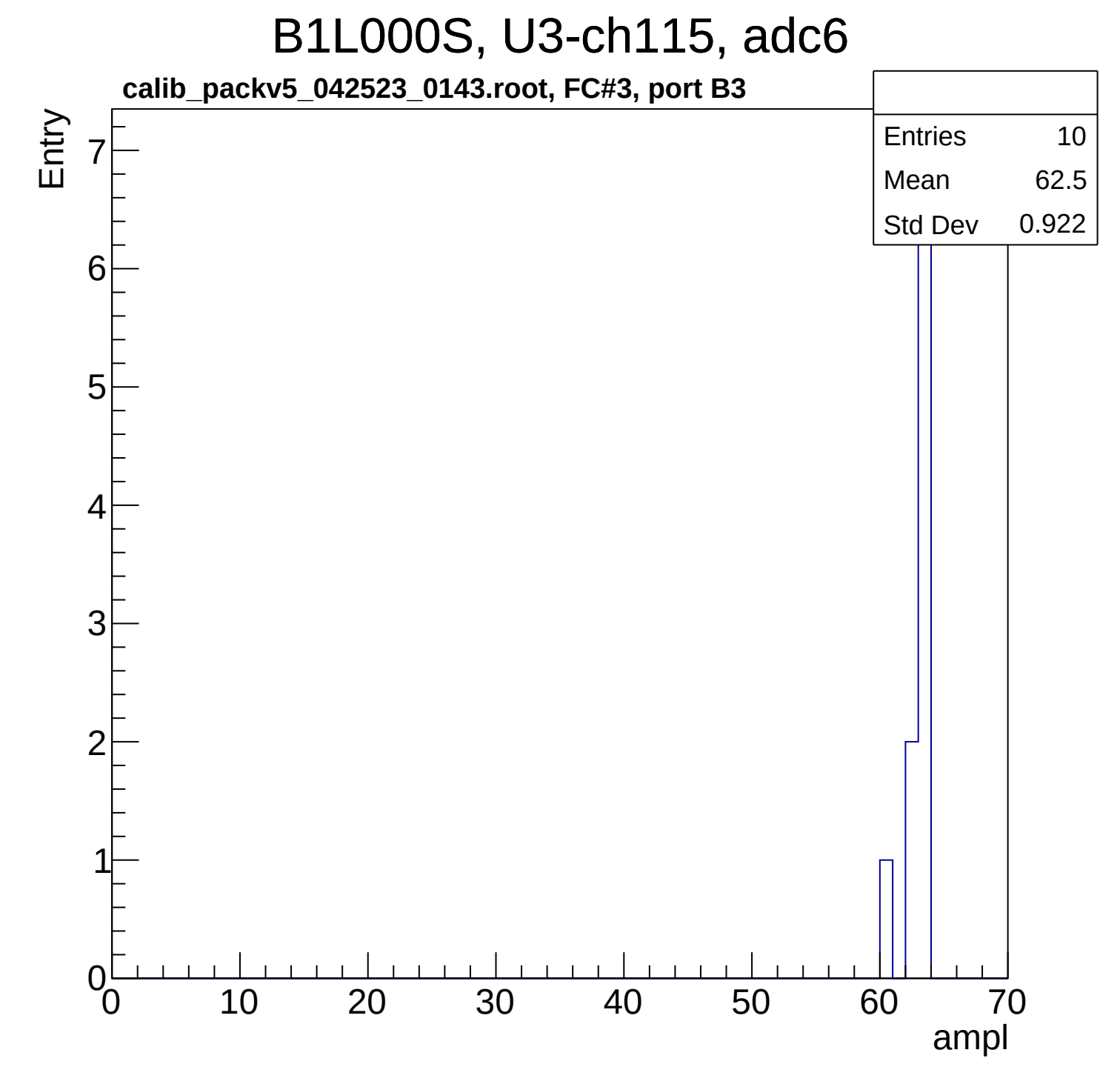
50
60
70
ampl


B1L000S, U3-ch115, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62.5
Std Dev 0.922
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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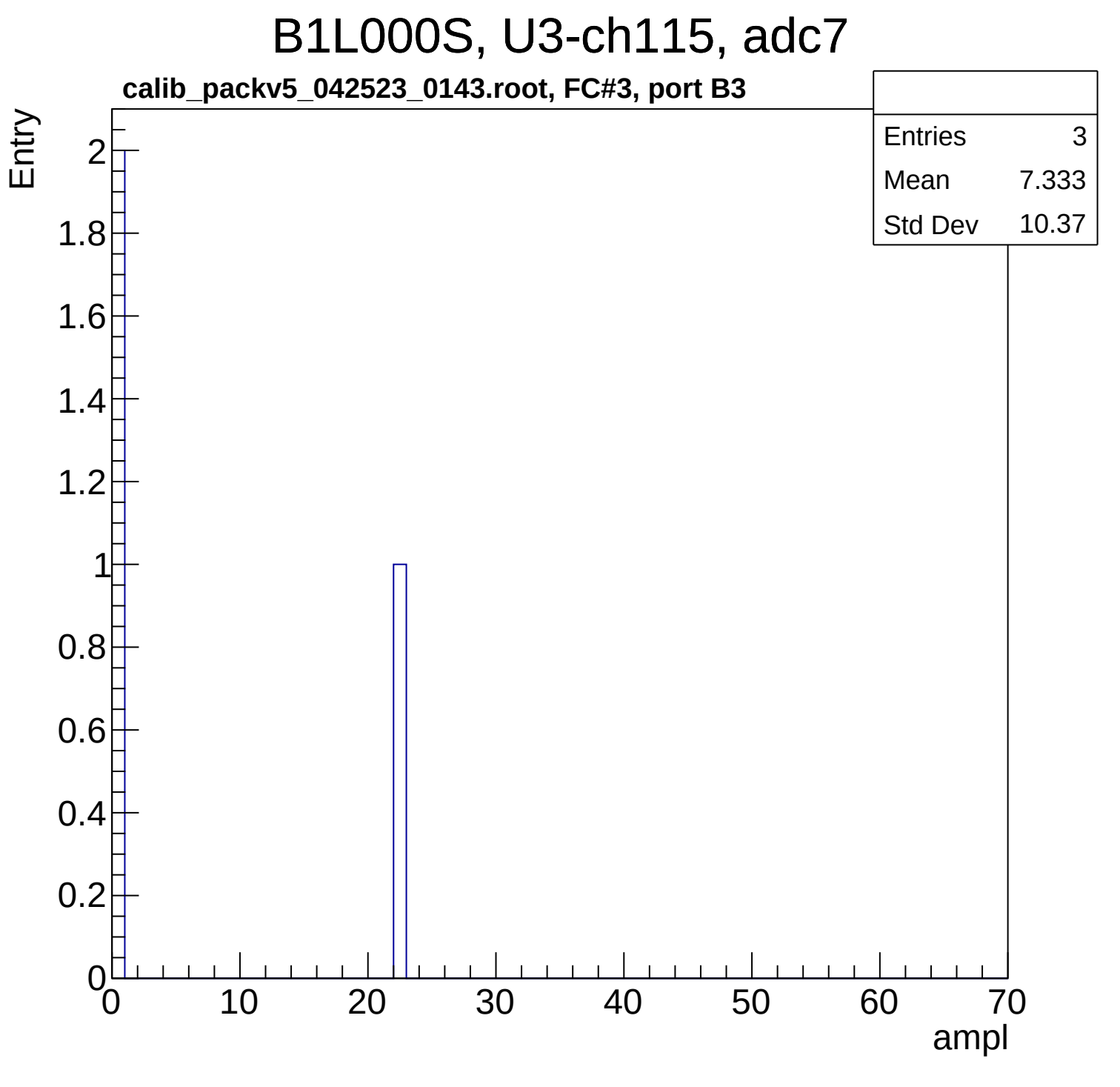
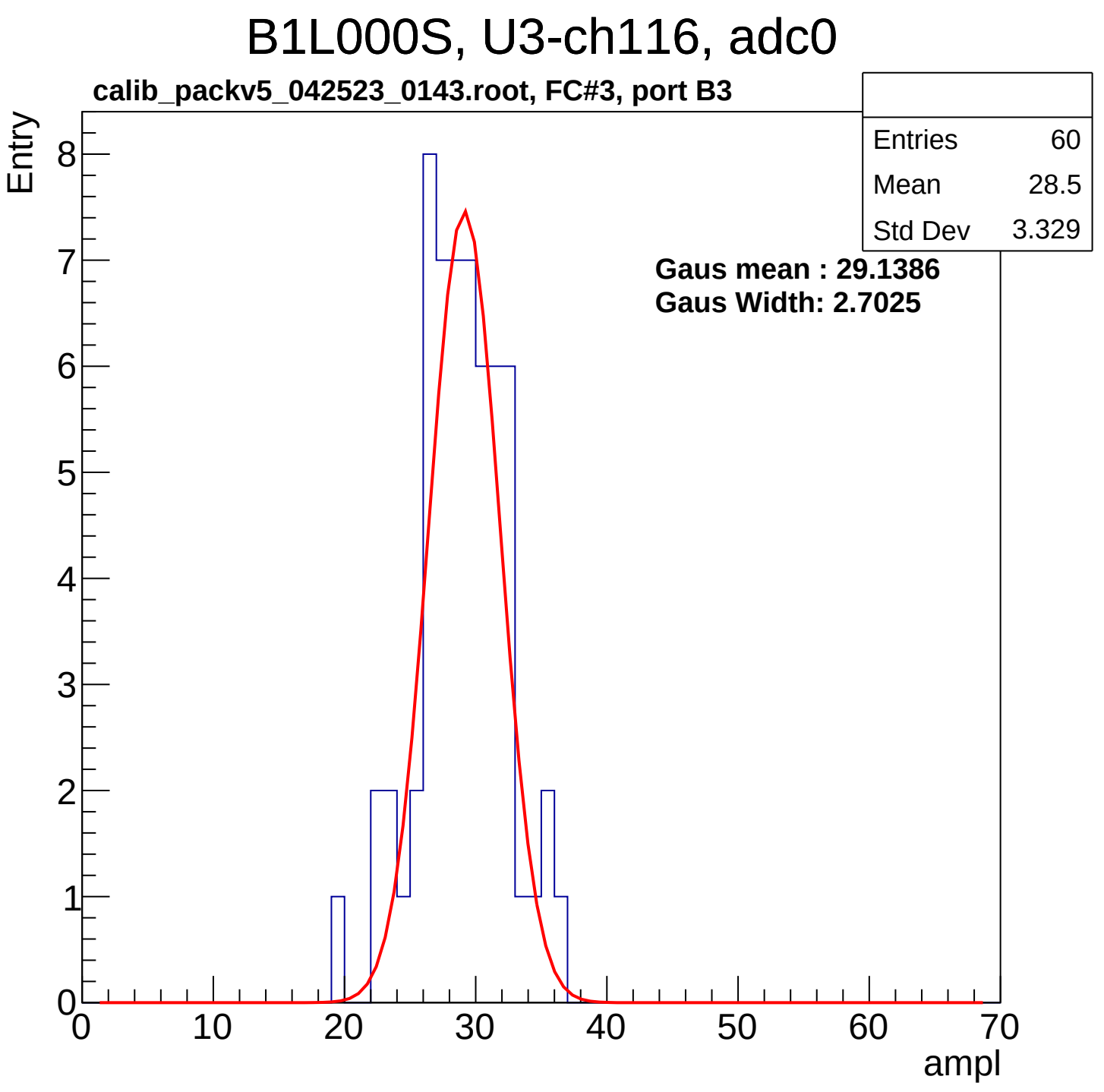
60
70


B1L000S, U3-ch115, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U3-ch116, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 28.5
Std Dev 3.329
Gaus mean : 29.1386
Gaus Width: 2.7025
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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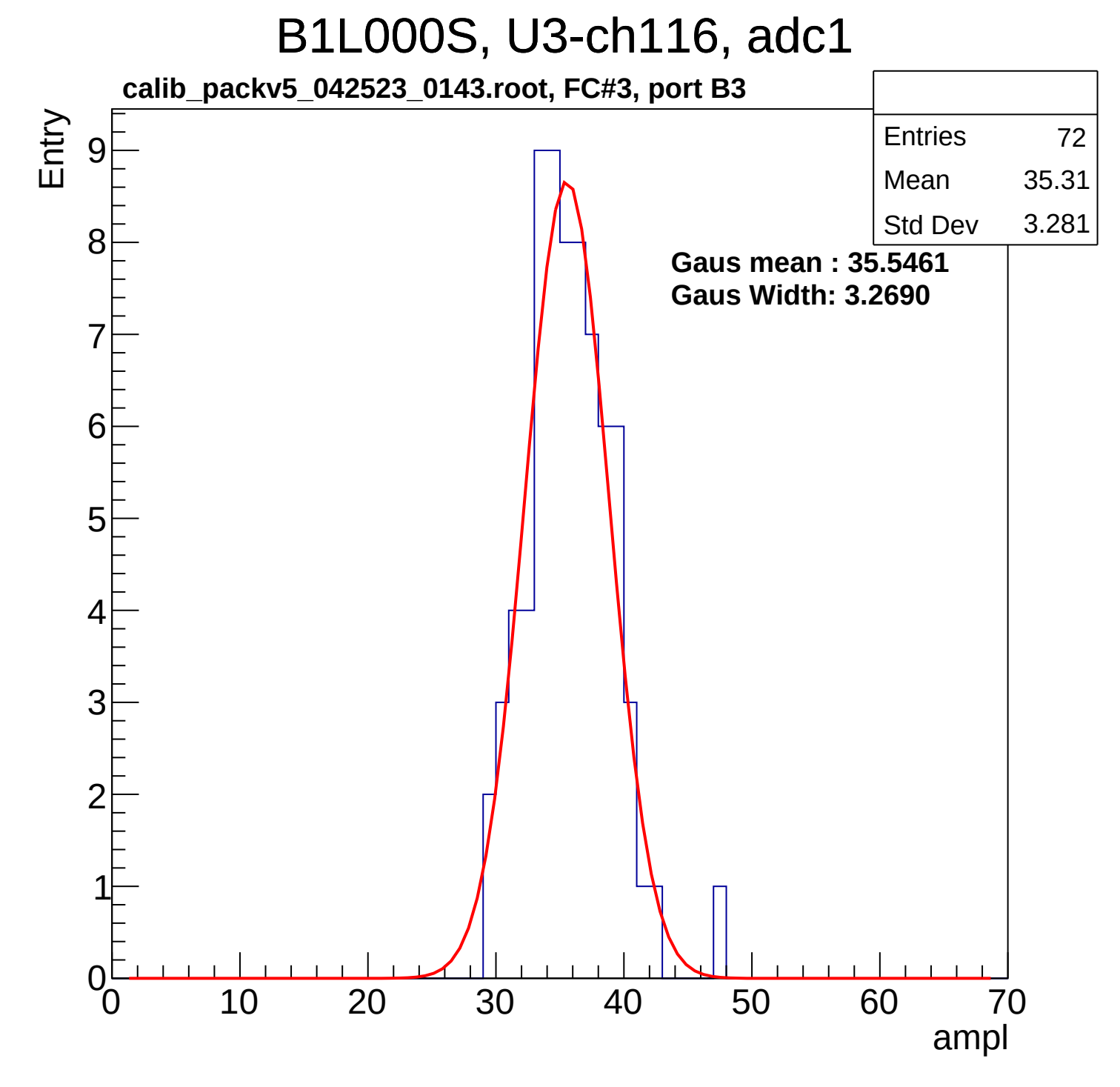
B1L000S, U3-ch116, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 35.31
Std Dev 3.281
Gaus mean : 35.5461
Gaus Width: 3.2690
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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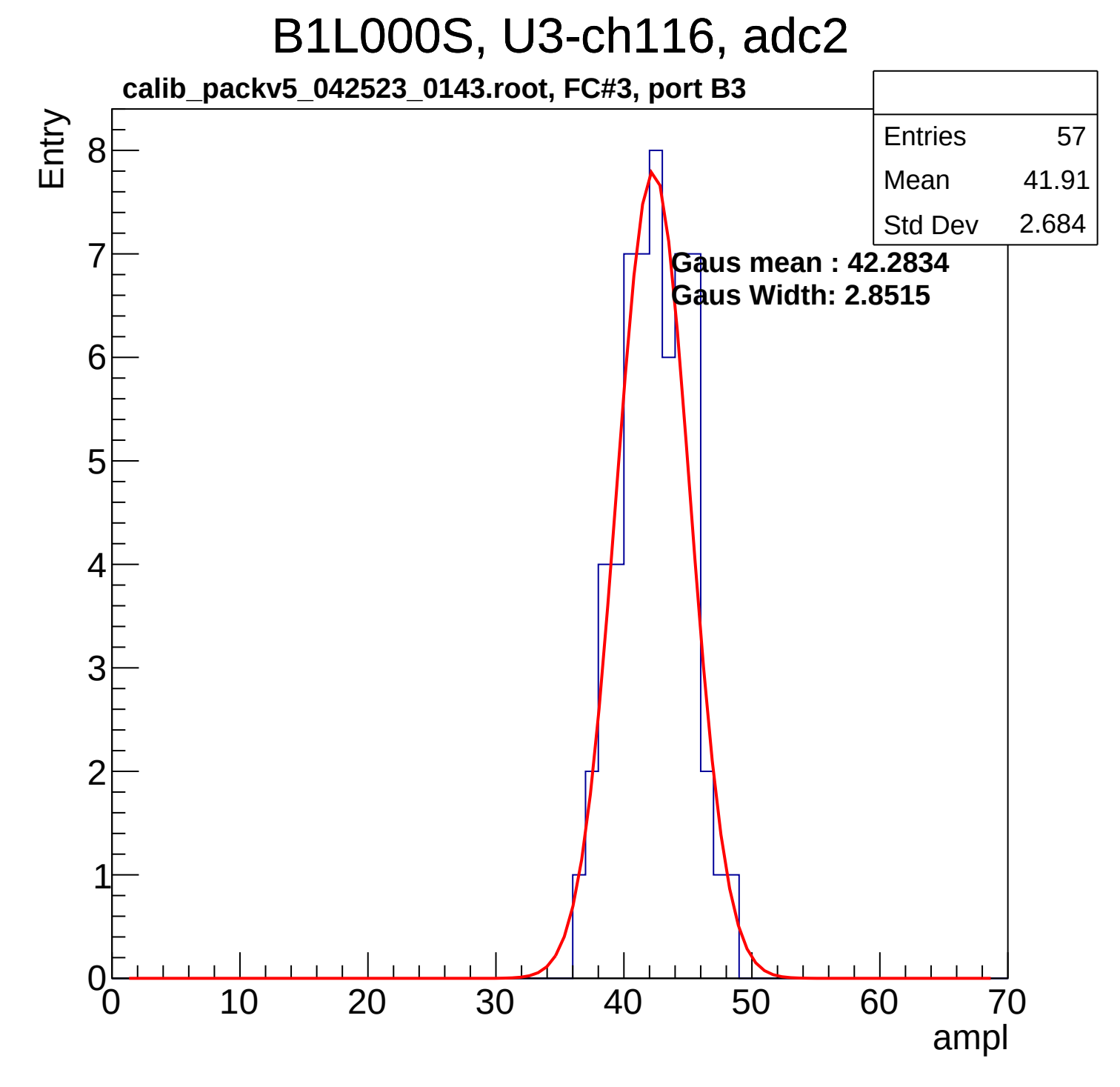
B1L000S, U3-ch116, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41.91
Std Dev 2.684
Gaus mean : 42.2834
Gaus Width: 2.8515
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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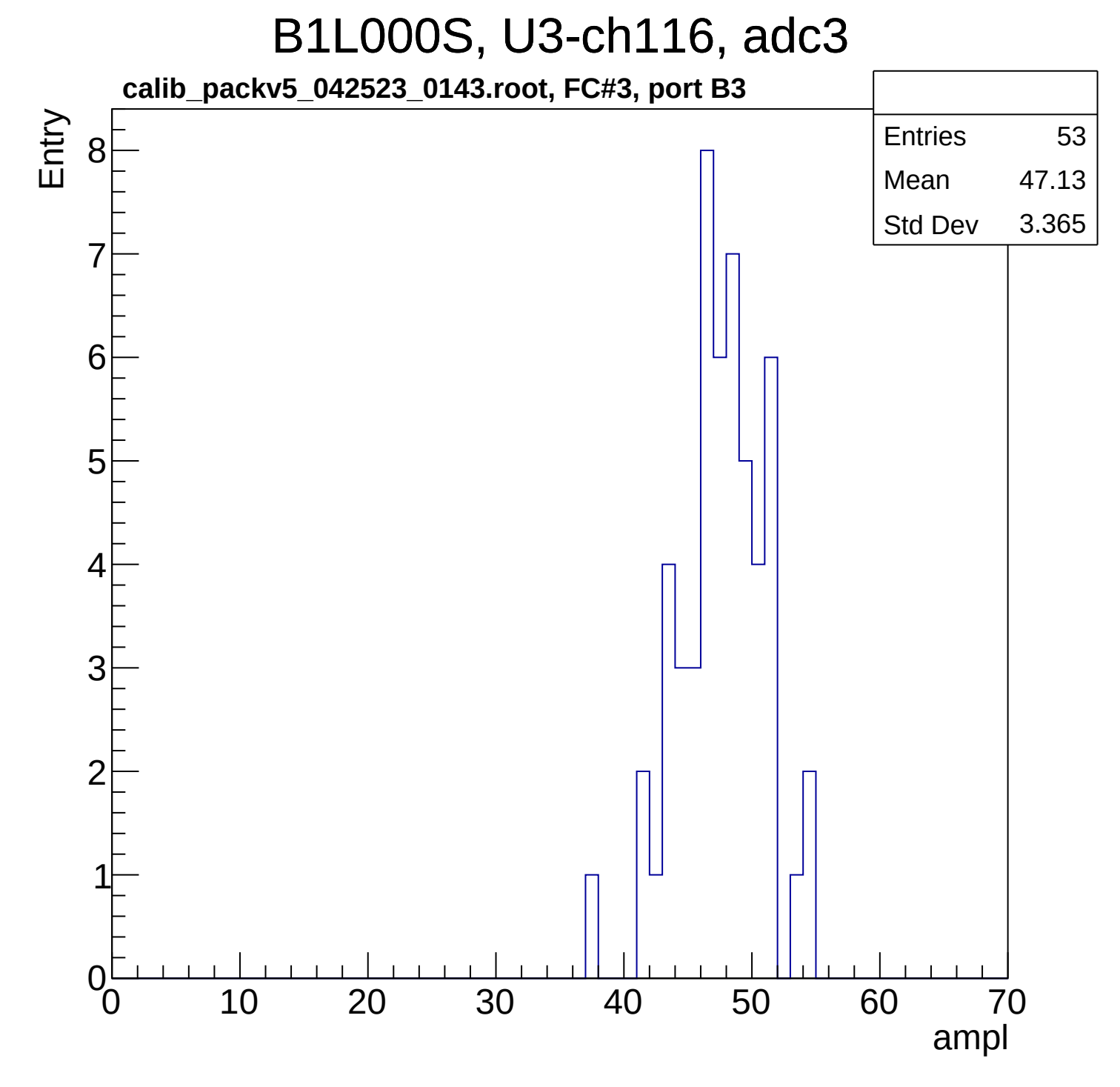
B1L000S, U3-ch116, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 47.13
Std Dev 3.365
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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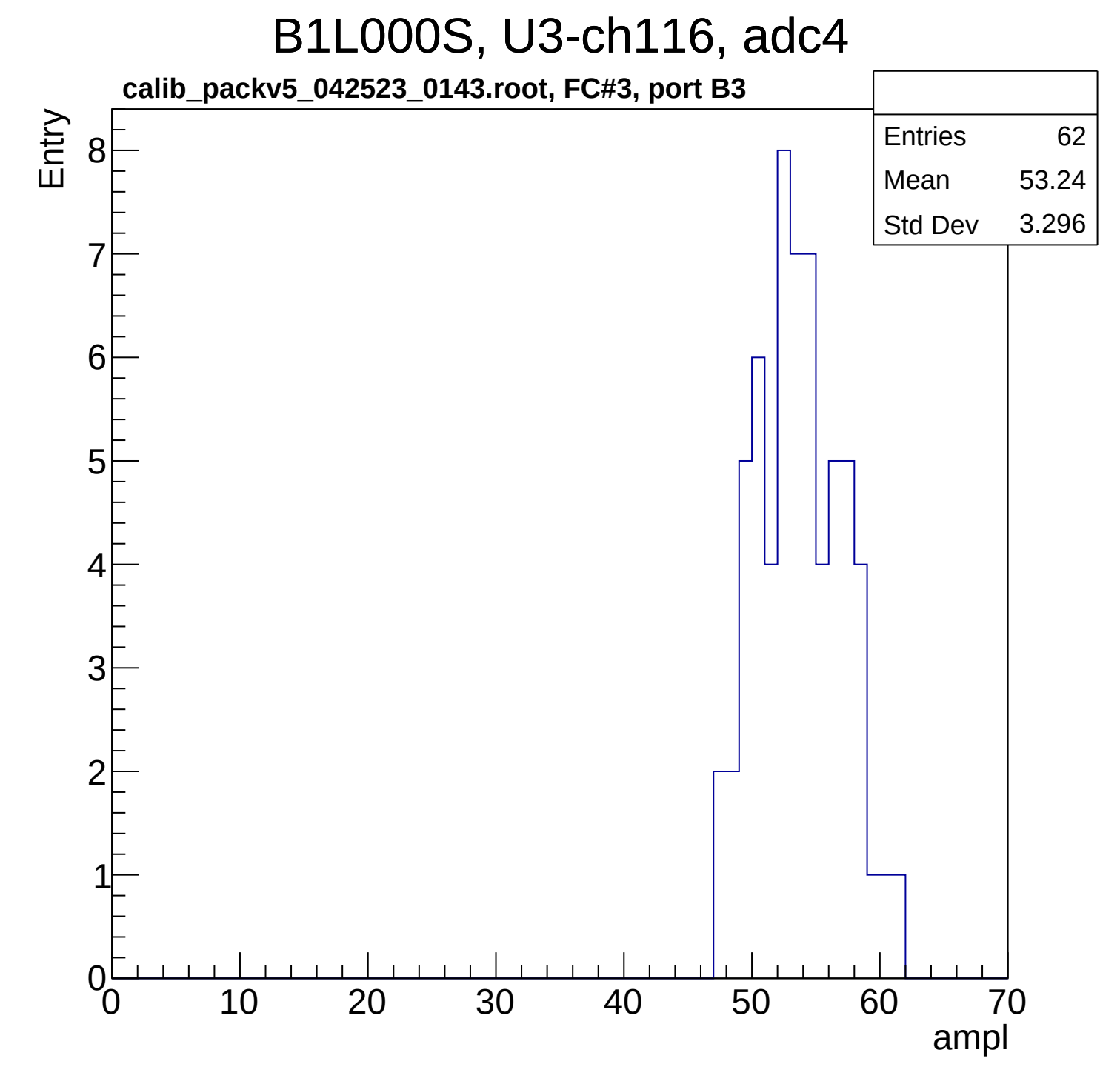
B1L000S, U3-ch116, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 53.24
Std Dev 3.296
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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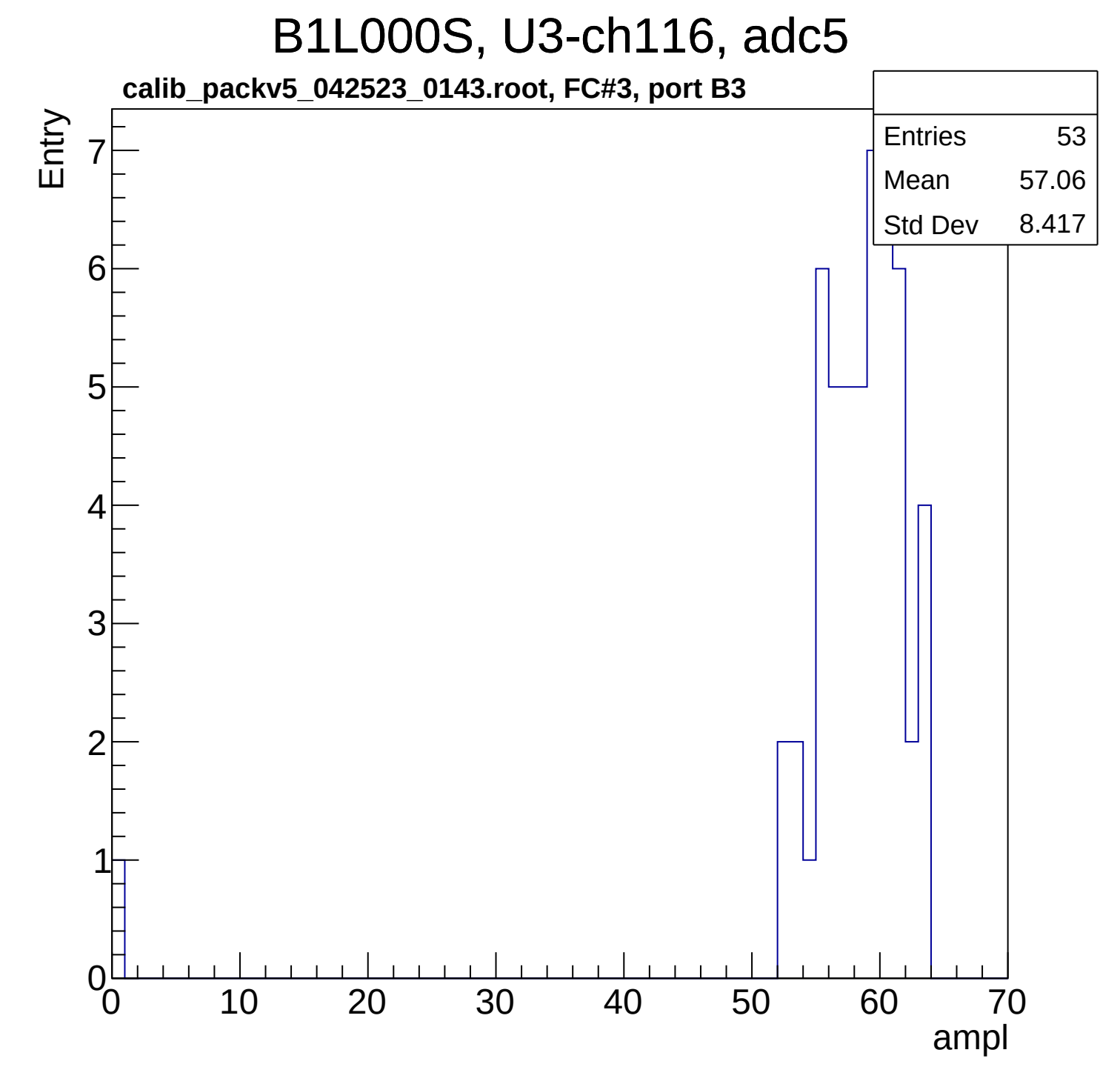
B1L000S, U3-ch116, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 57.06
Std Dev 8.417
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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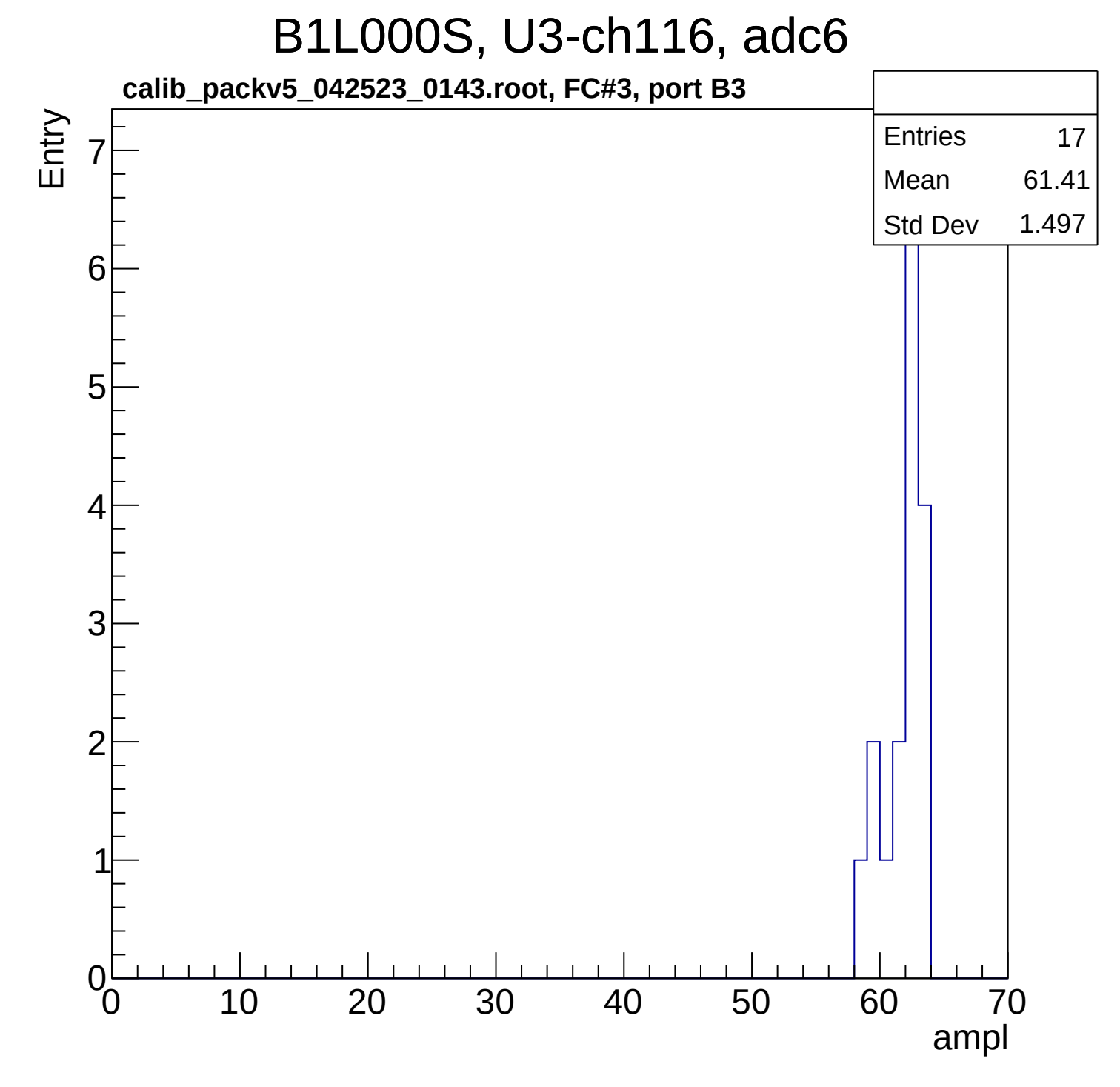
B1L000S, U3-ch116, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.41
Std Dev 1.497
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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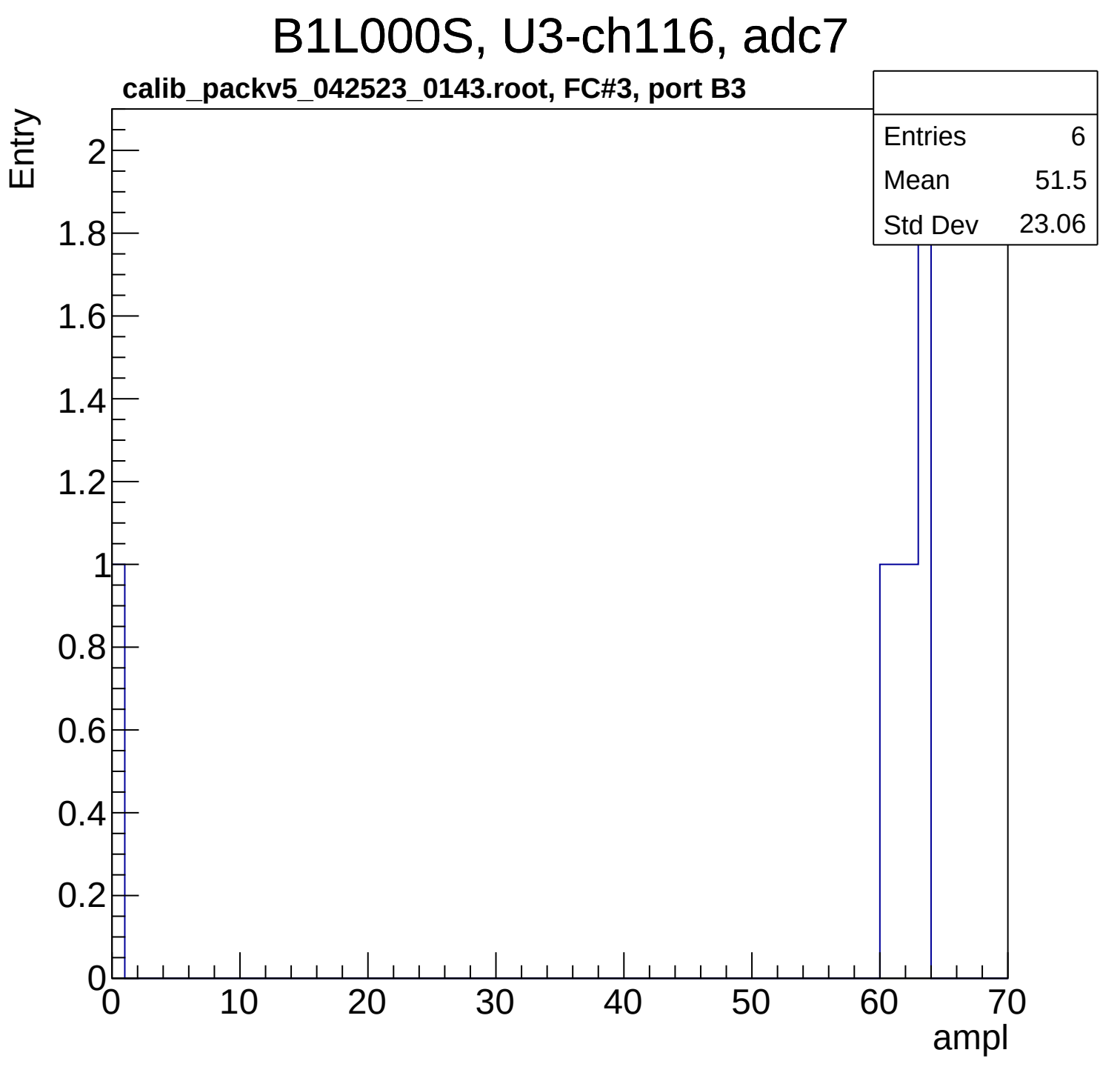
B1L000S, U3-ch116, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 51.5
Std Dev 23.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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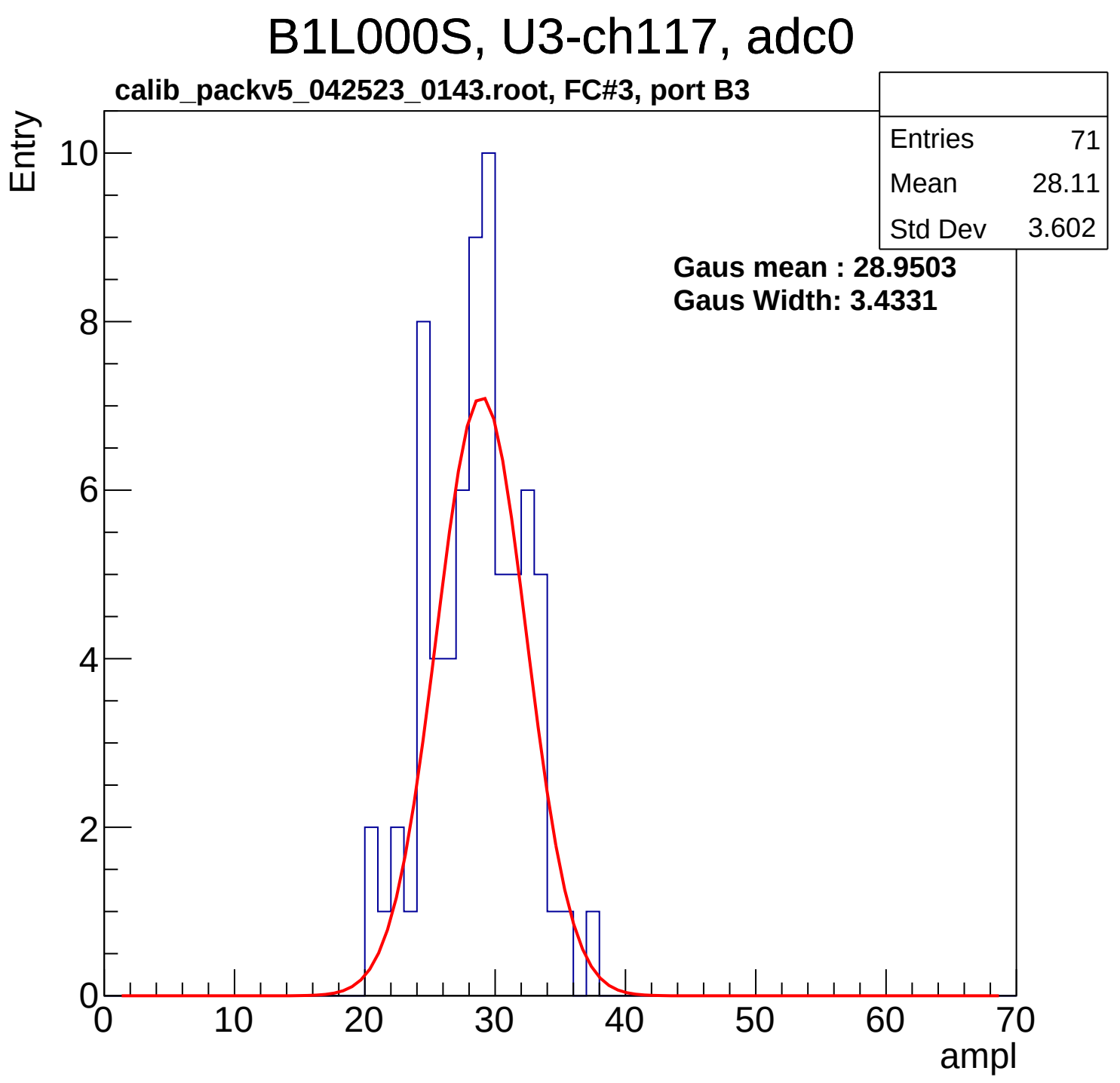
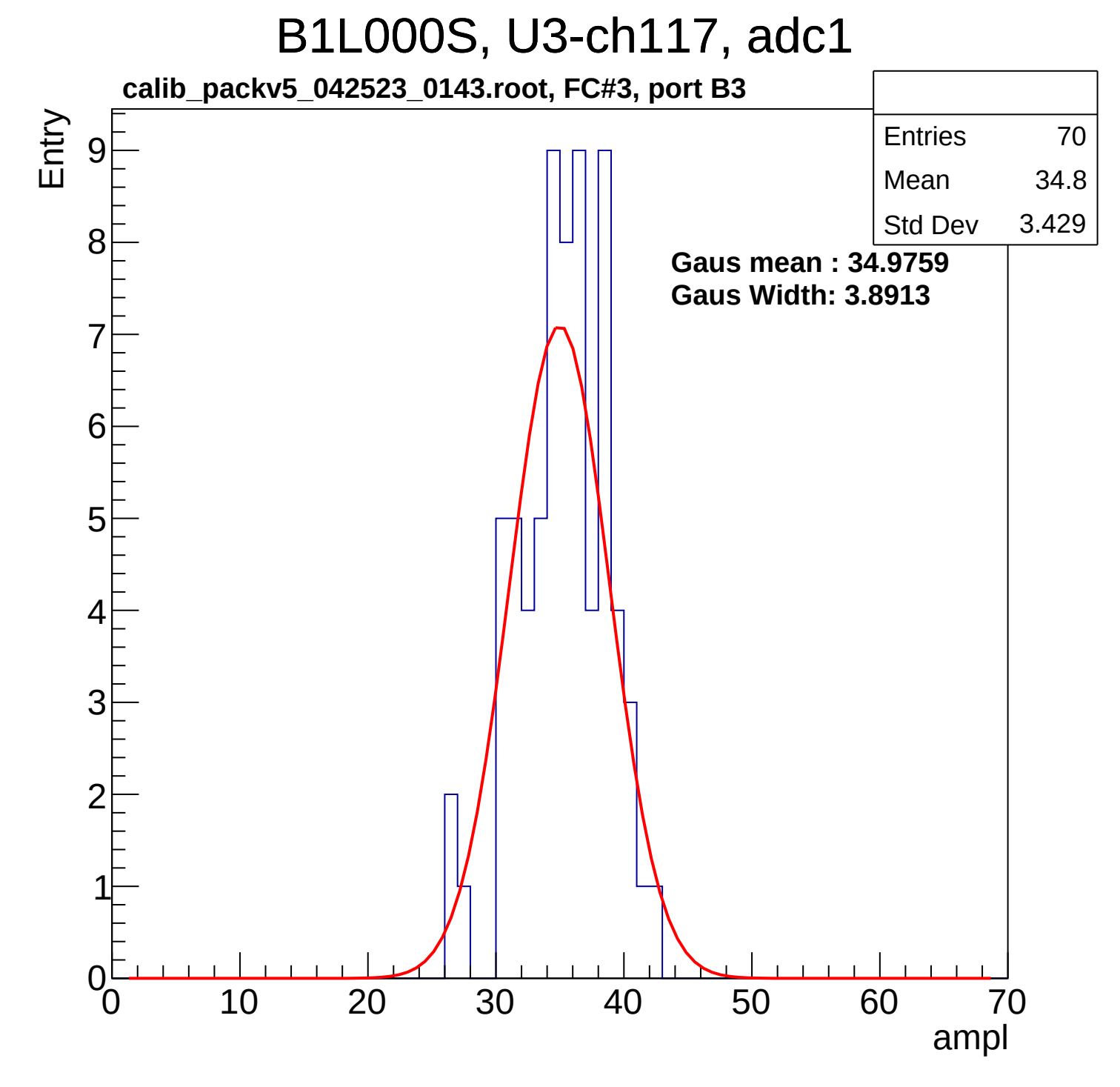

B1L000S, U3-ch117, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 28.11
Std Dev 3.602
Gaus mean : 28.9503
Gaus Width: 3.4331
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch117, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 34.8
Std Dev 3.429
Gaus mean : 34.9759
Gaus Width: 3.8913
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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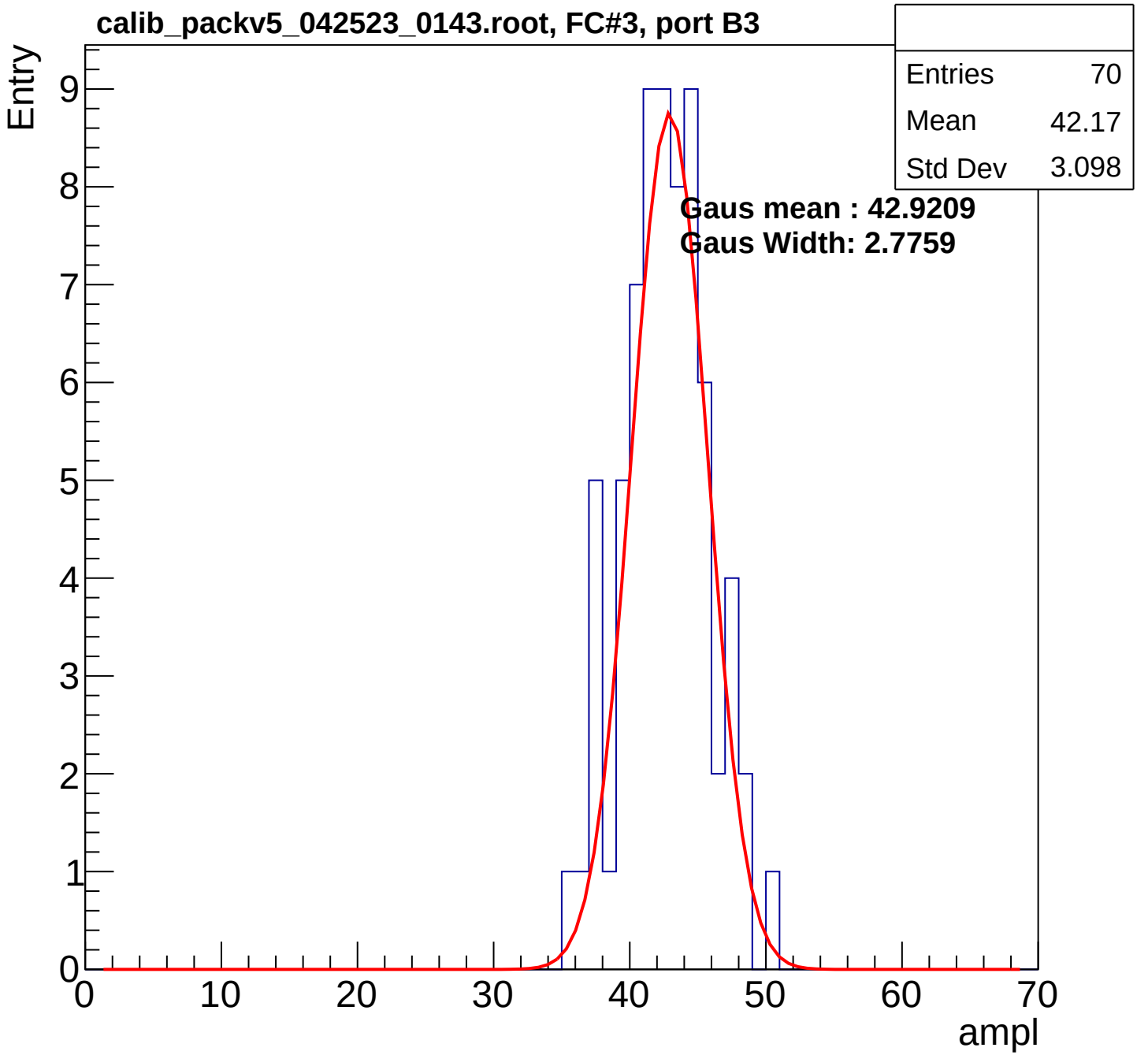
B1L000S, U3-ch117, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 42.17
Std Dev 3.098
Gaus mean : 42.9209
Gaus Width: 2.7759
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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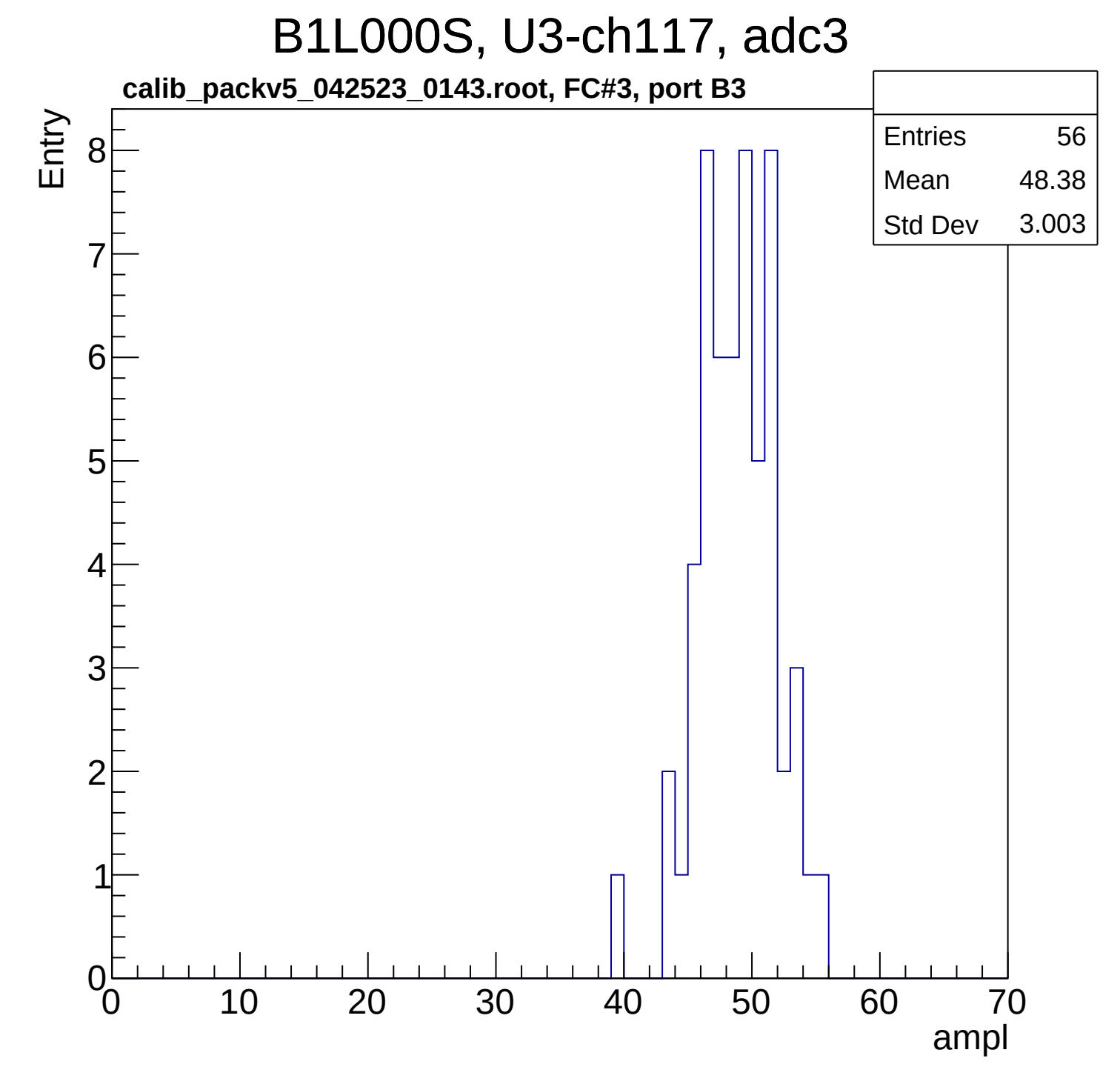
B1L000S, U3-ch117, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 48.38
Std Dev 3.003
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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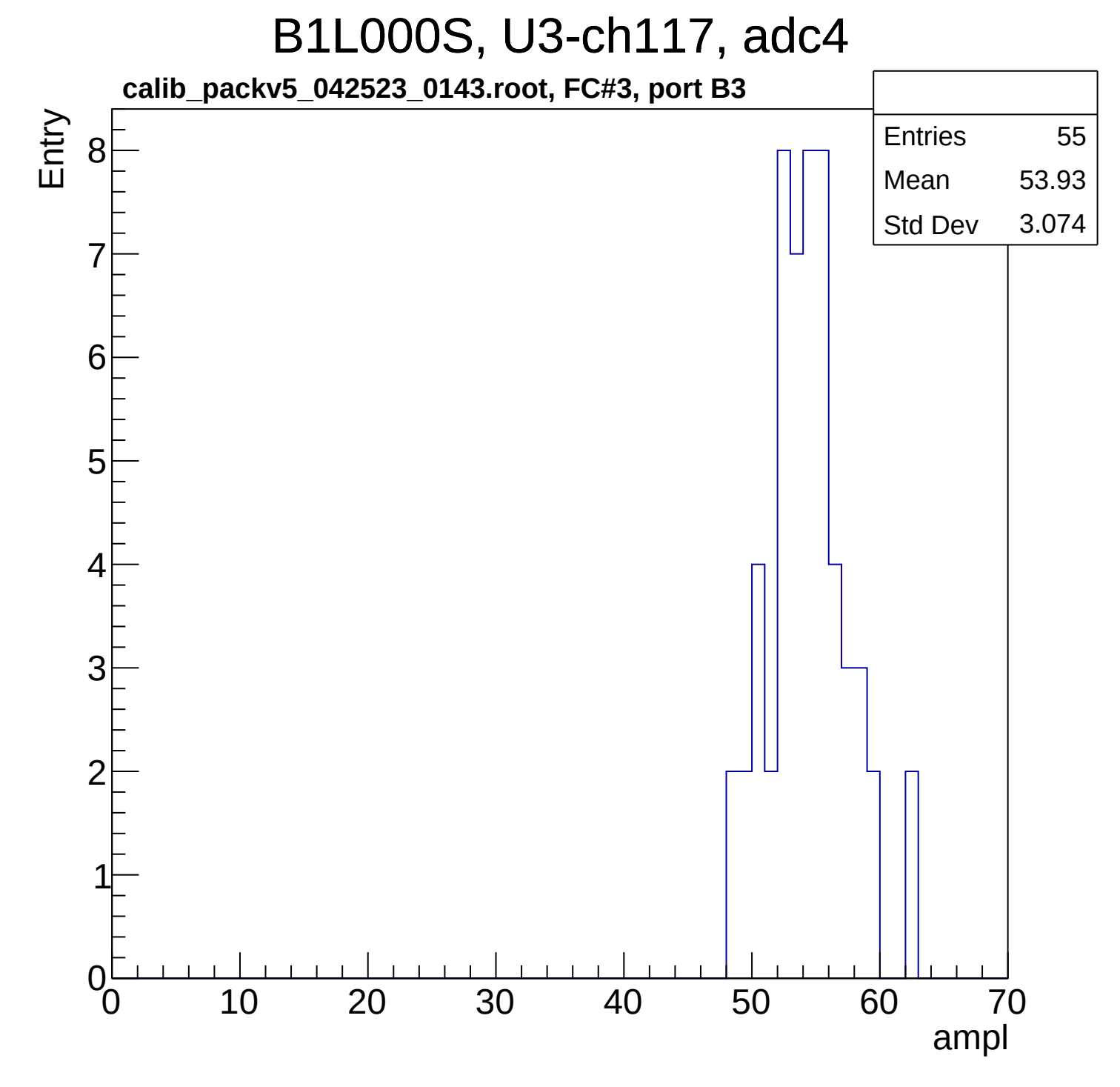
B1L000S, U3-ch117, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.93
Std Dev 3.074
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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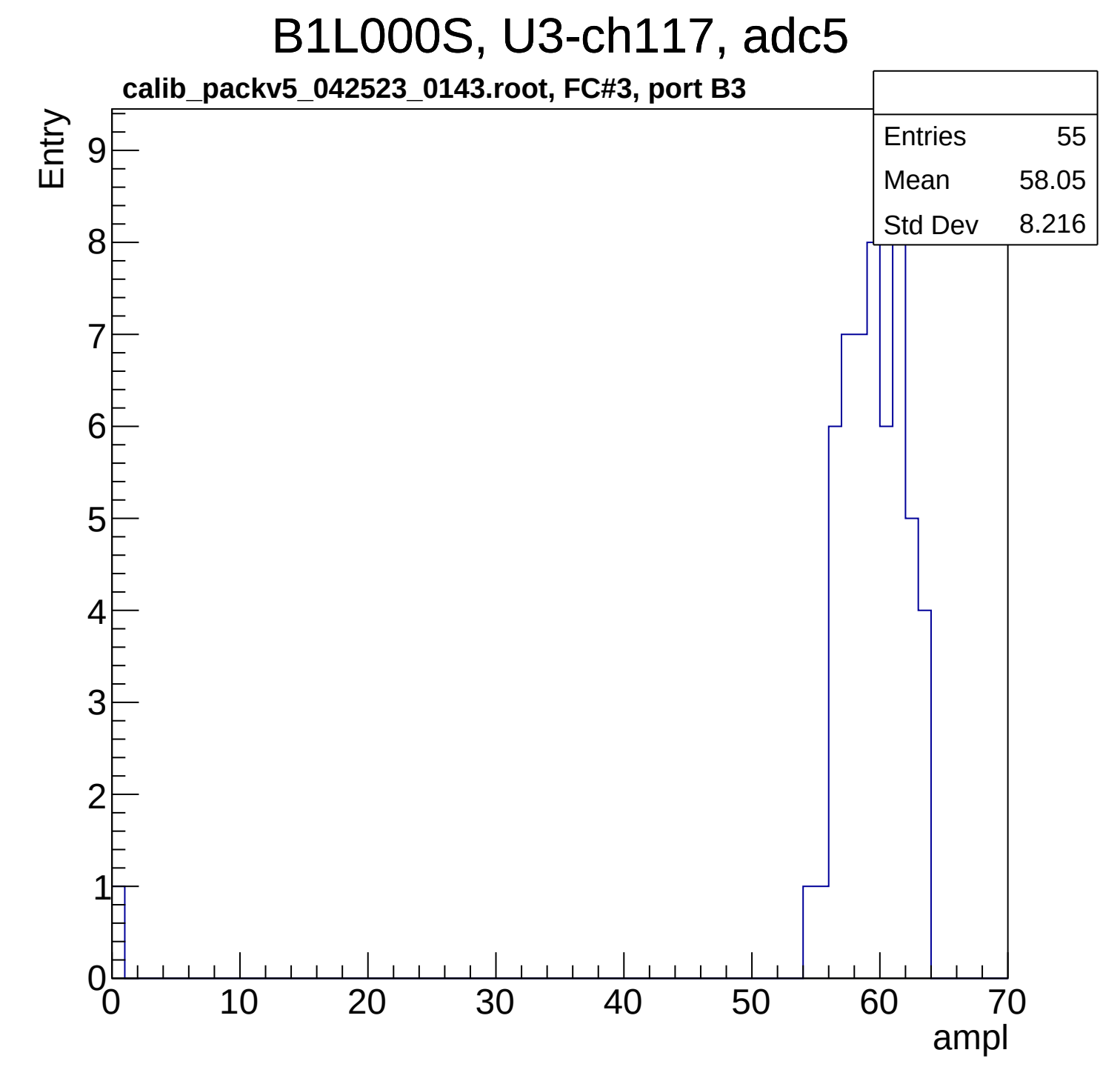
B1L000S, U3-ch117, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.05
Std Dev 8.216
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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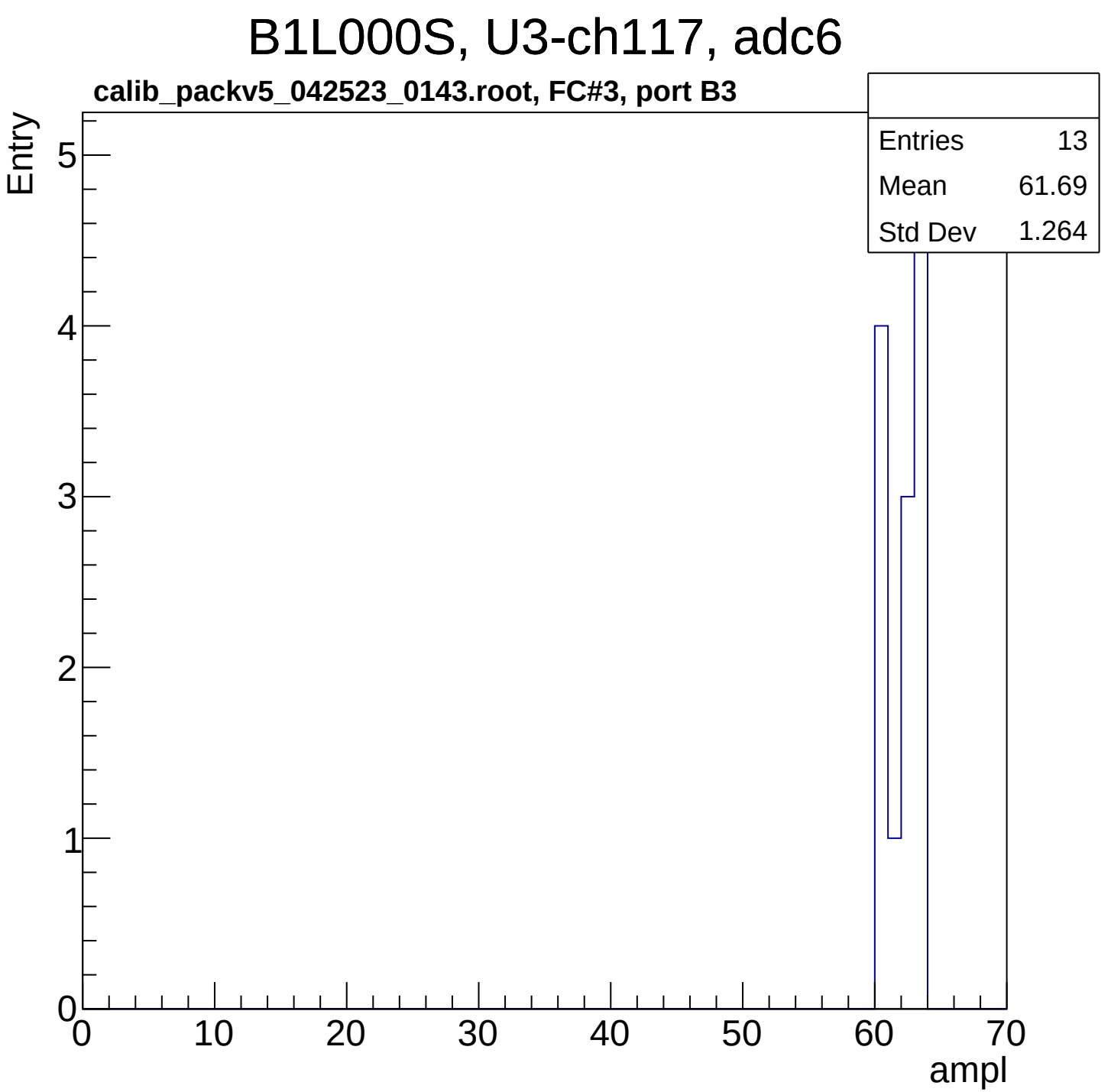
B1L000S, U3-ch117, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.69
Std Dev 1.264
Entry
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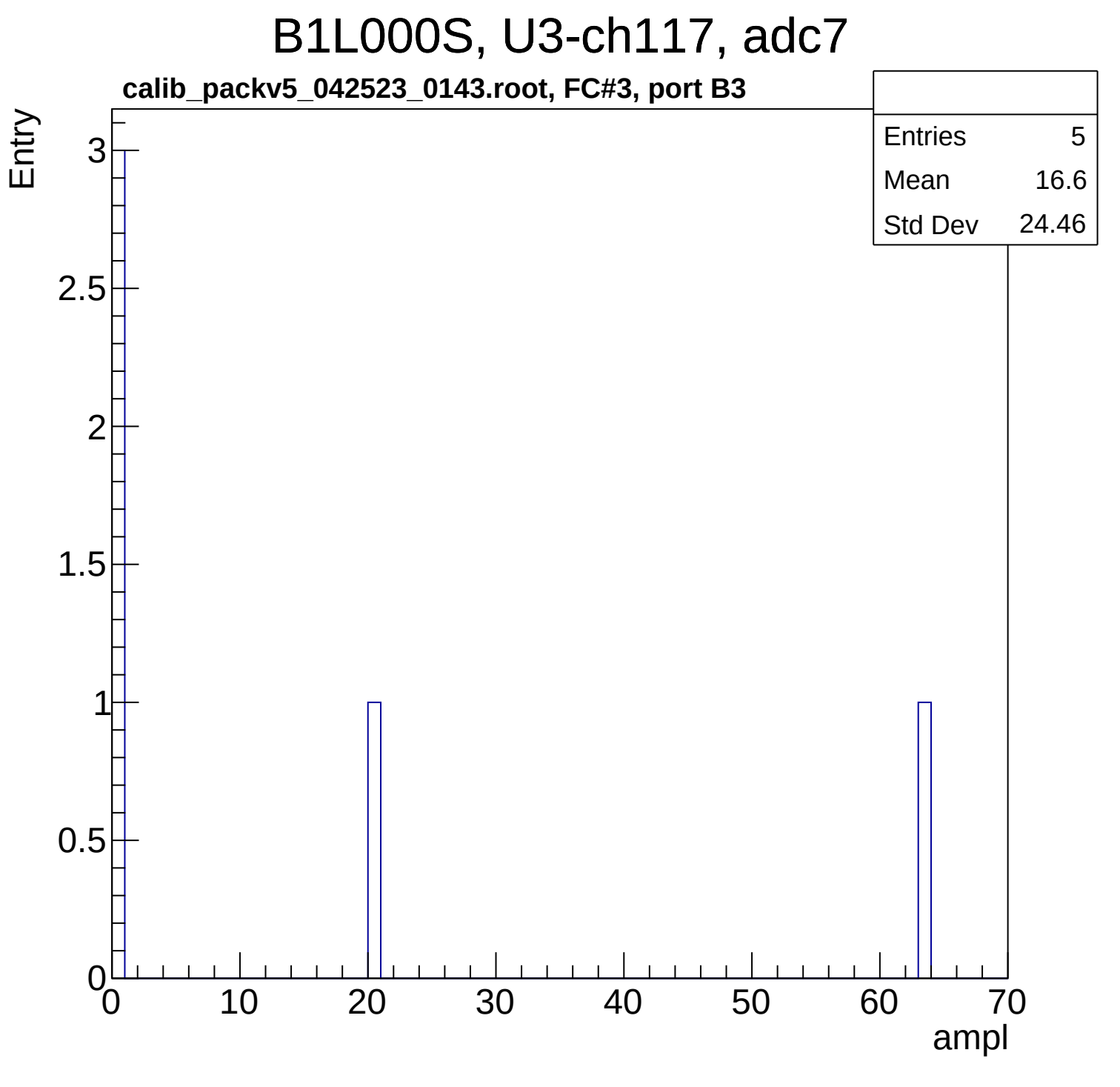
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch117, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 16.6
Std Dev 24.46
Entry
ampl
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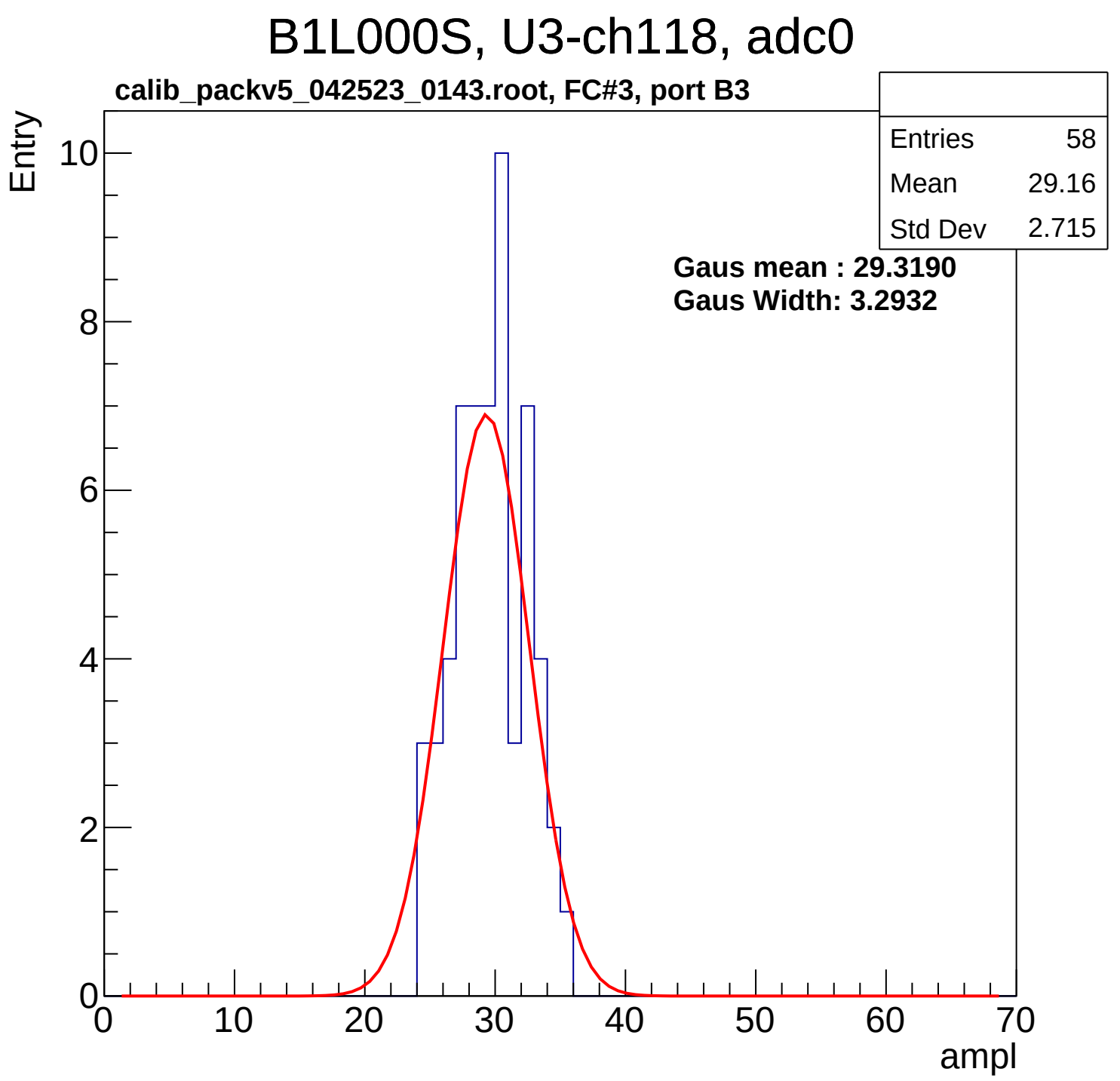
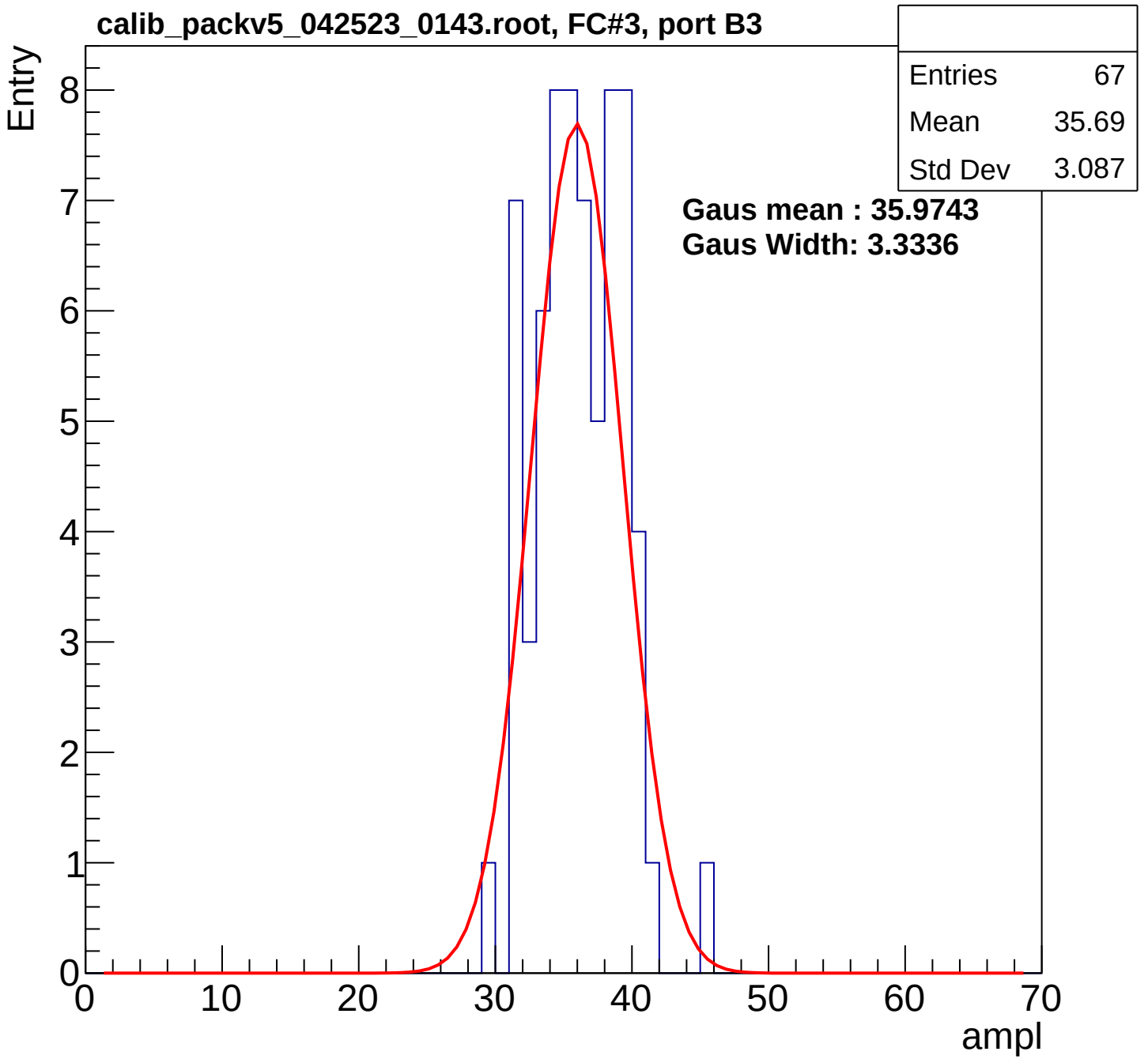
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U3-ch118, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 29.16
Std Dev 2.715
Gaus mean : 29.3190
Gaus Width: 3.2932
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U3-ch118, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.69
Std Dev 3.087
Gaus mean : 35.9743
Gaus Width: 3.3336
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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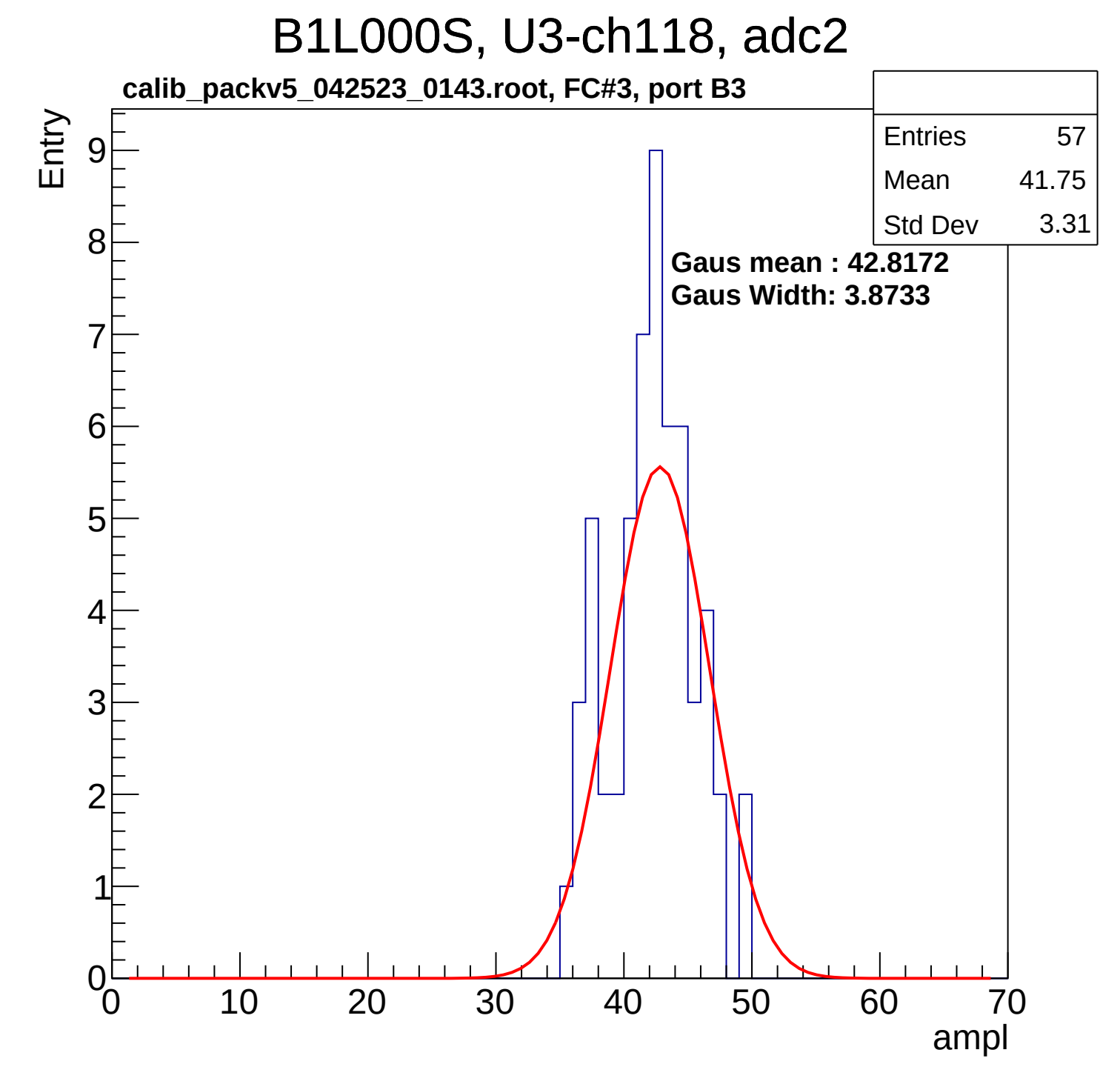
B1L000S, U3-ch118, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41.75
Std Dev 3.31
Gaus mean : 42.8172
Gaus Width: 3.8733
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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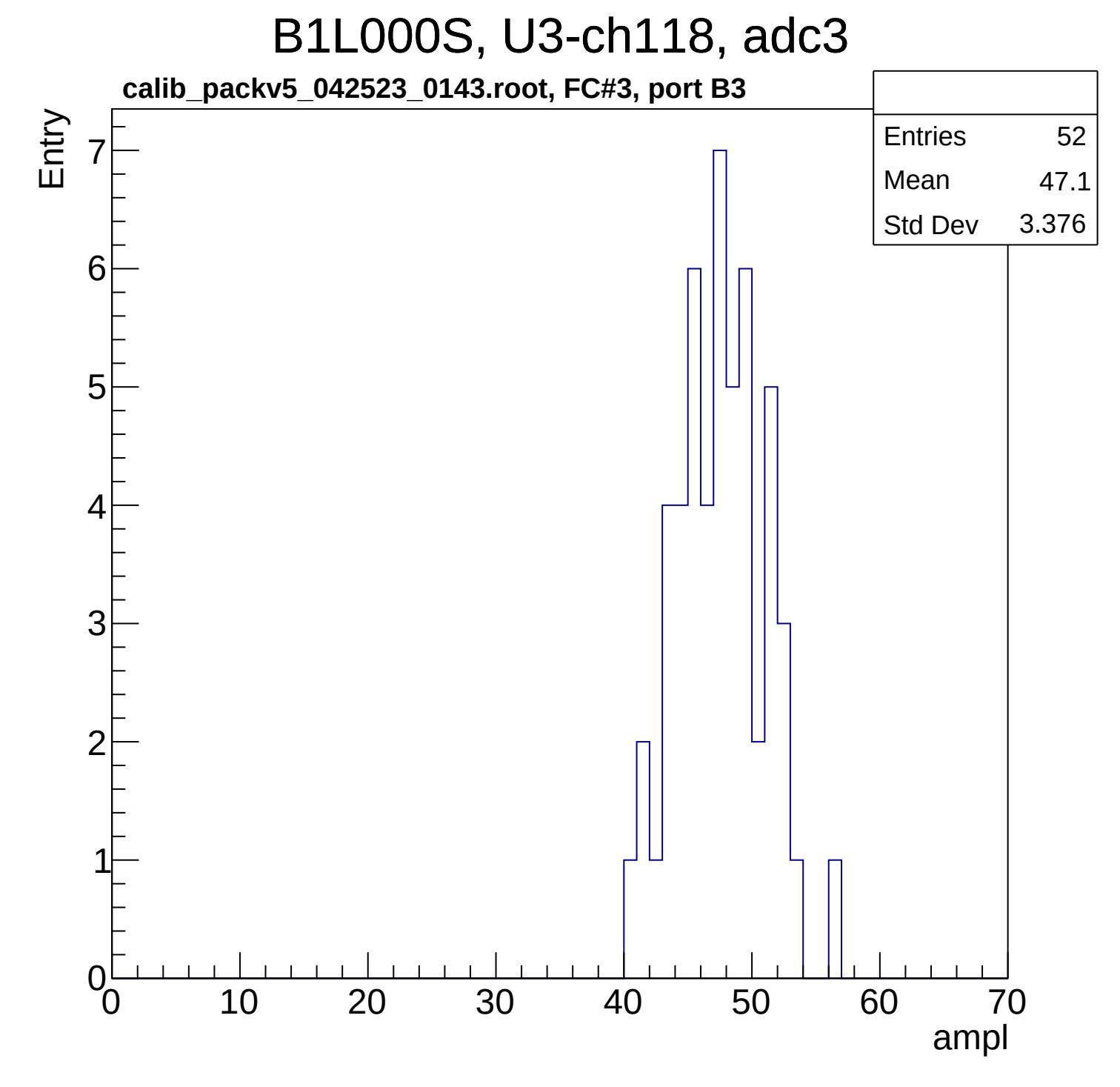
B1L000S, U3-ch118, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 47.1
Std Dev 3.376
Entry
ampl
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60
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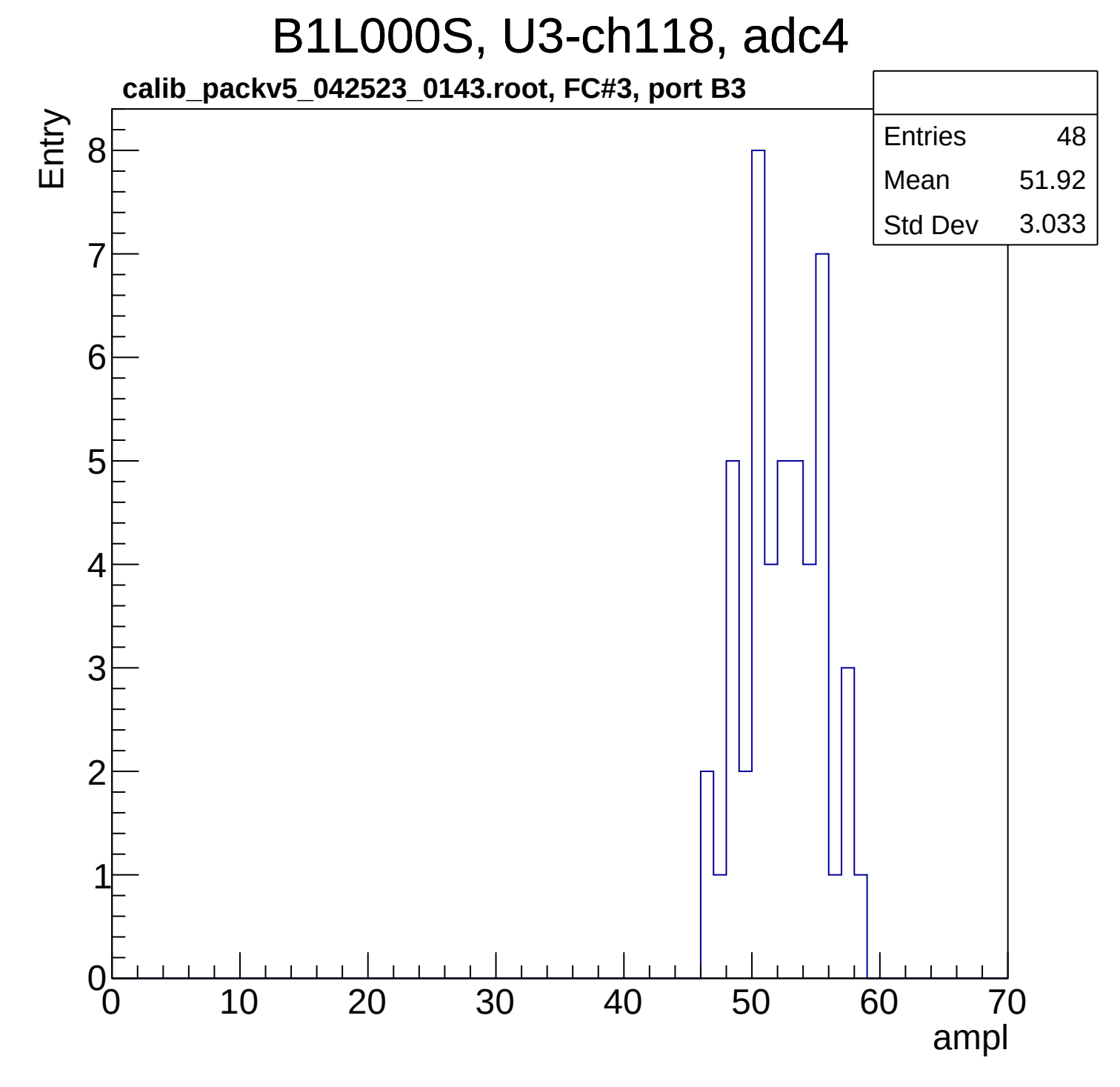
B1L000S, U3-ch118, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 51.92
Std Dev 3.033
Entry
ampl
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3
4
5
6
7
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0
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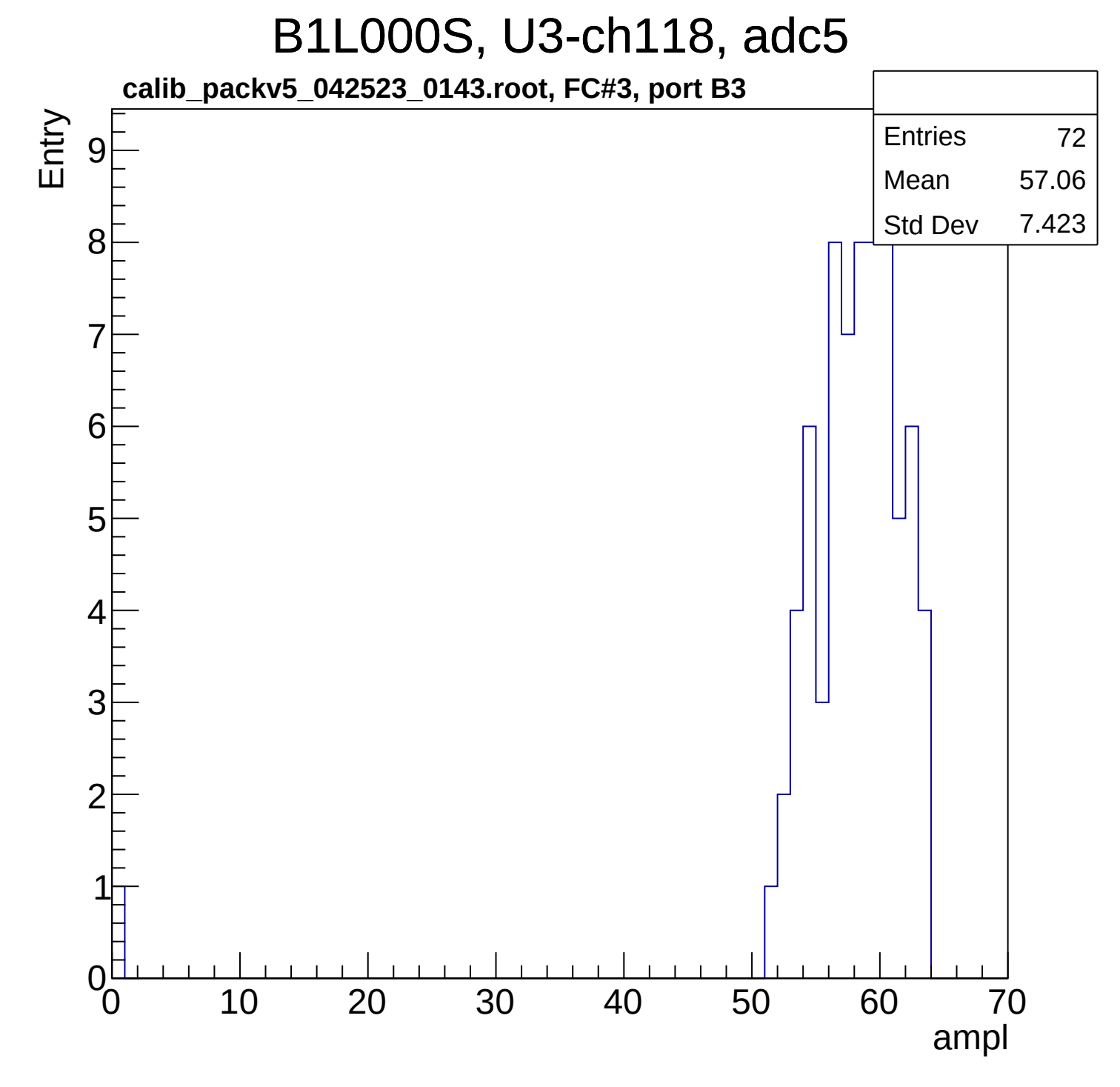
B1L000S, U3-ch118, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 57.06
Std Dev 7.423
Entry
ampl
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3
4
5
6
7
8
9
0
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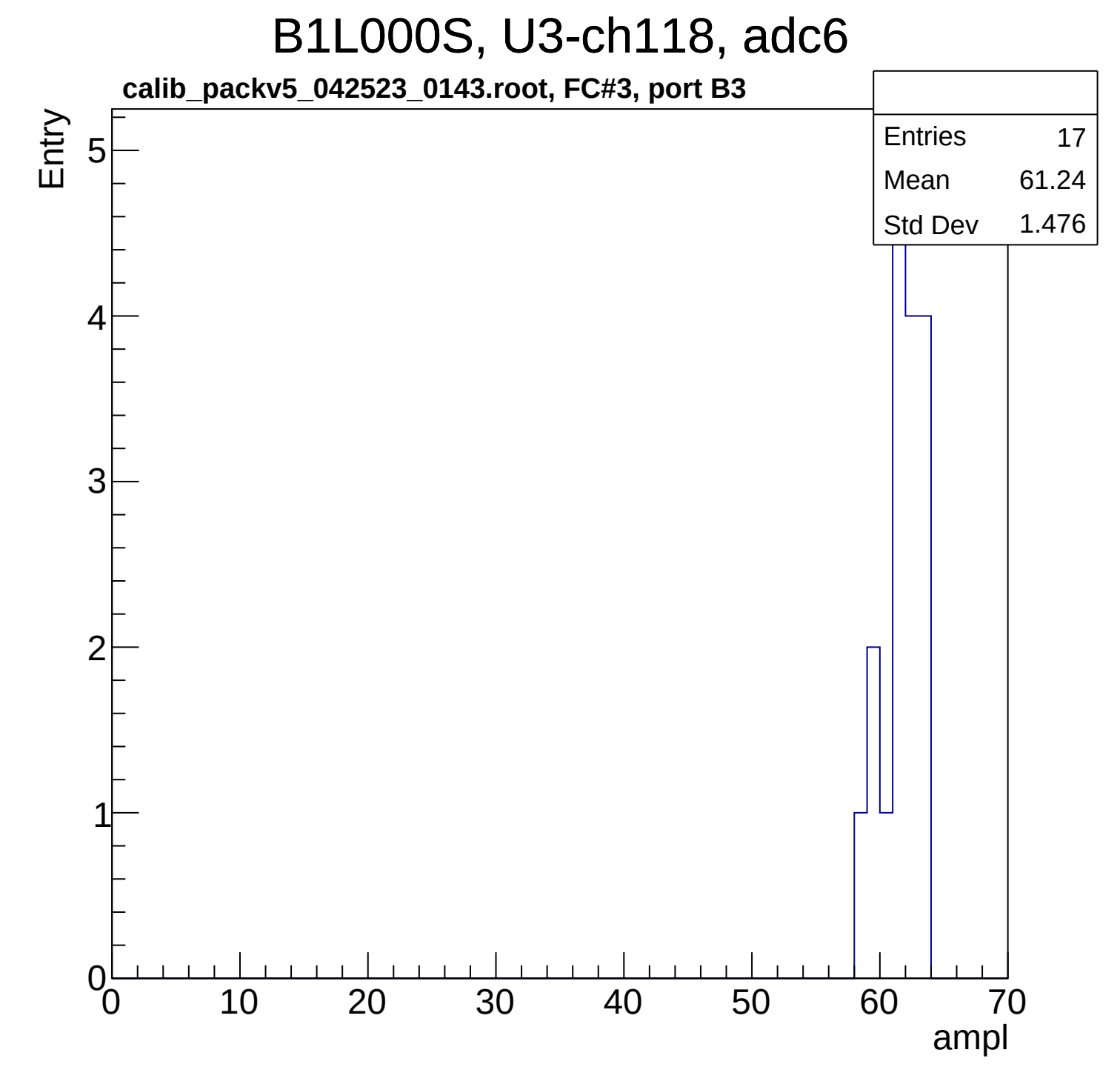
B1L000S, U3-ch118, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.24
Std Dev 1.476
Entry
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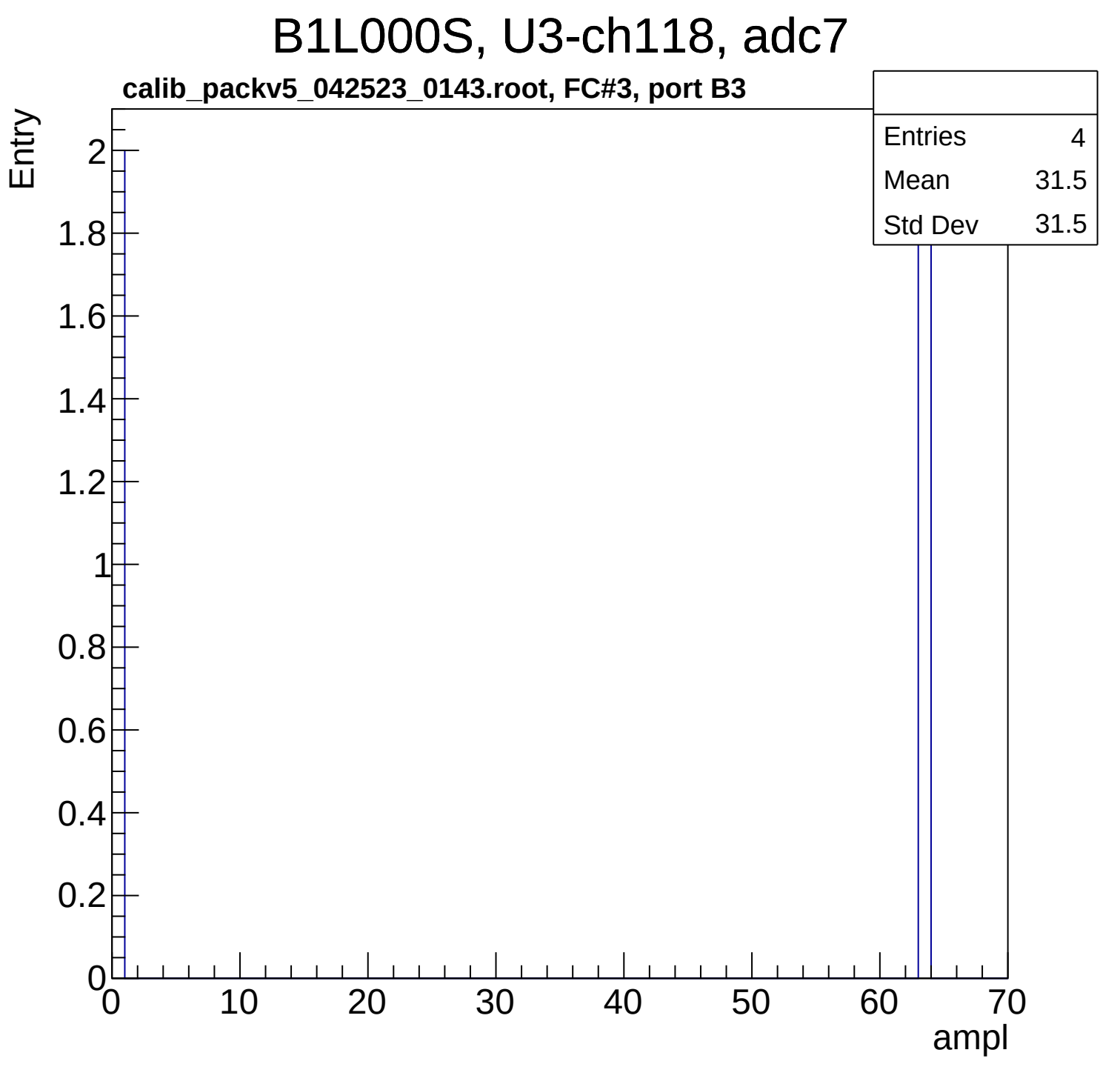
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch118, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
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60
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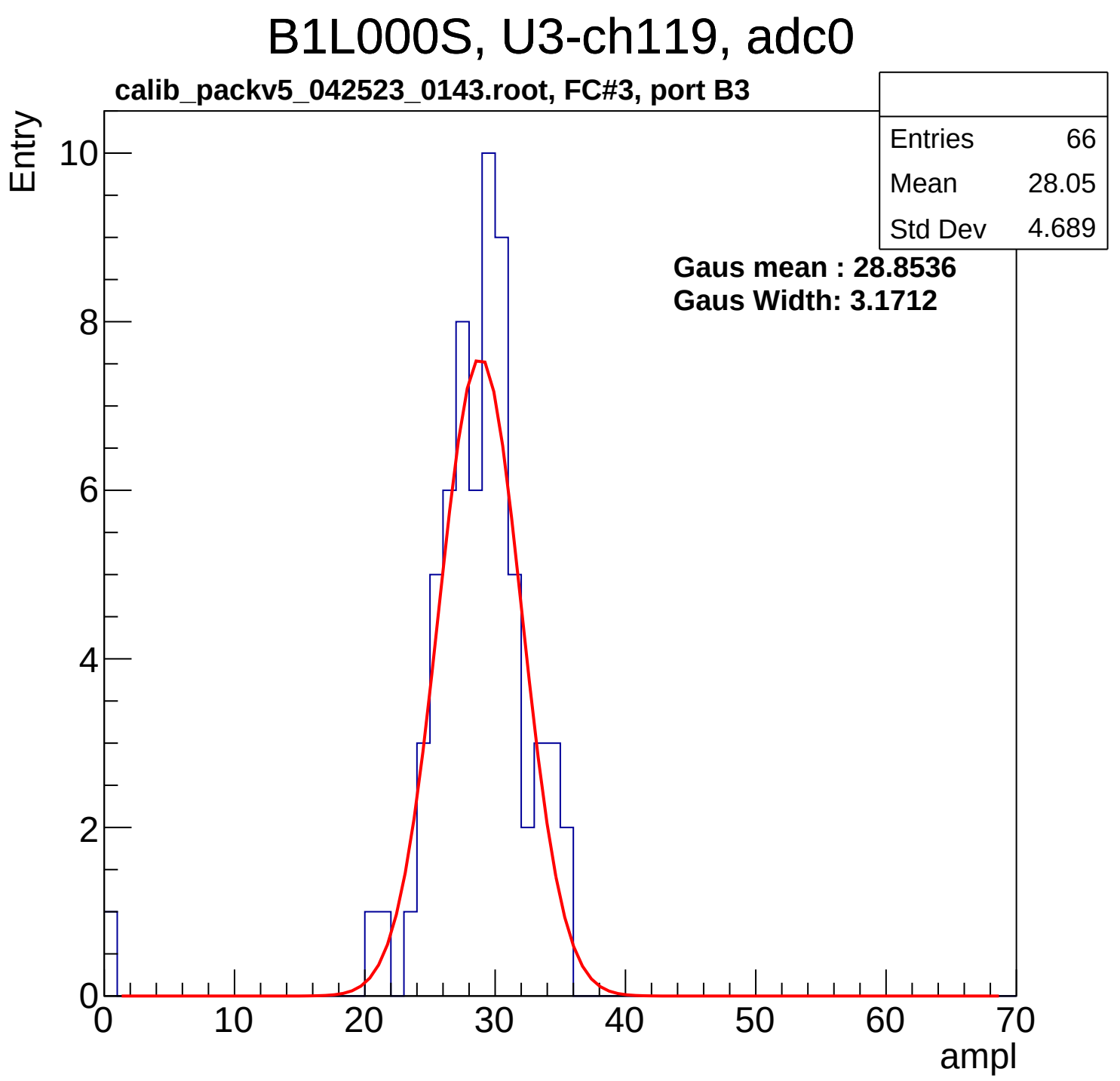
B1L000S, U3-ch119, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.05
Std Dev 4.689
Gaus mean : 28.8536
Gaus Width: 3.1712
Entry
ampl
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6
8
10
0
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20
30
40
50
60
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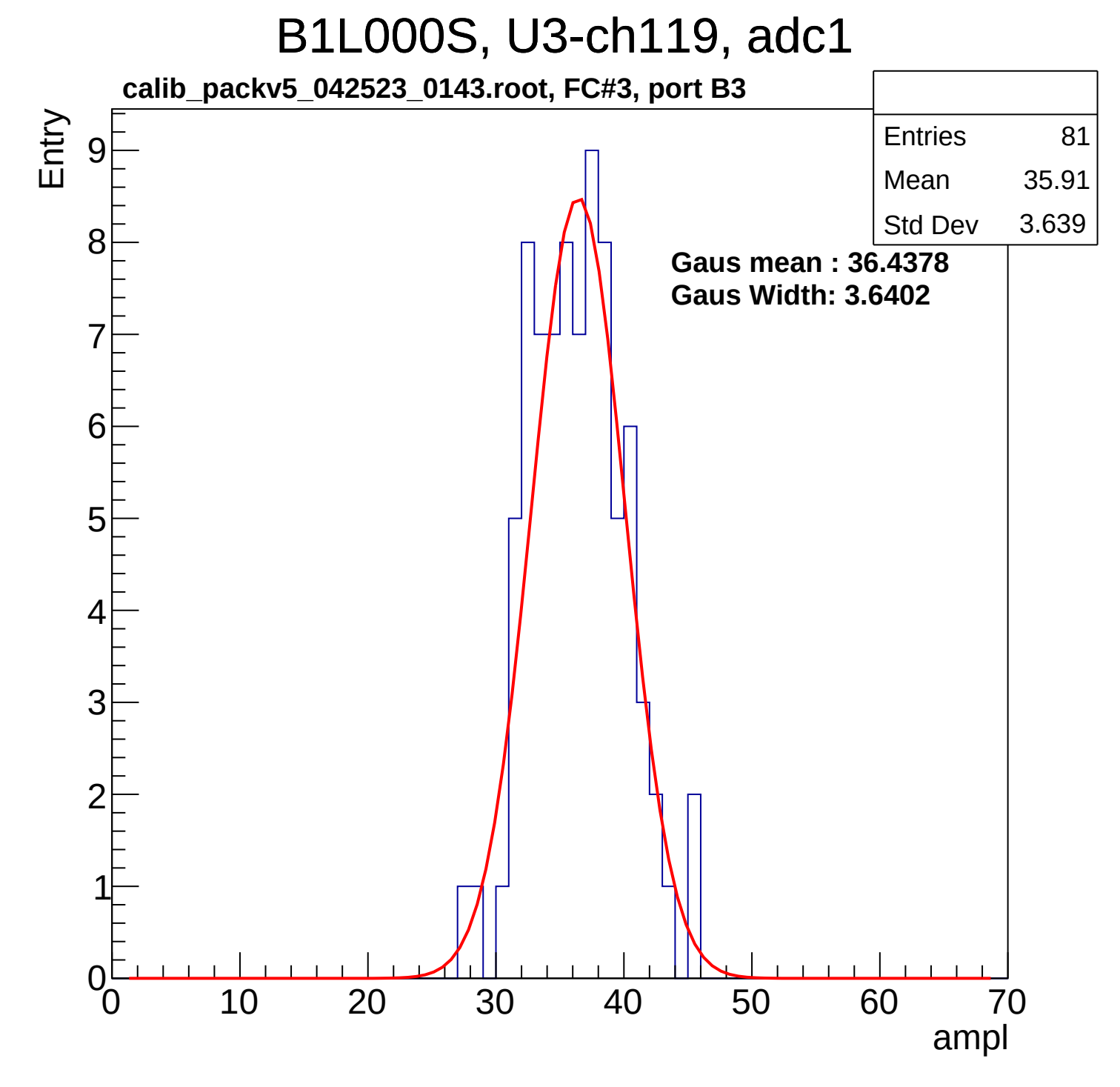
B1L000S, U3-ch119, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 35.91
Std Dev 3.639
Gaus mean : 36.4378
Gaus Width: 3.6402
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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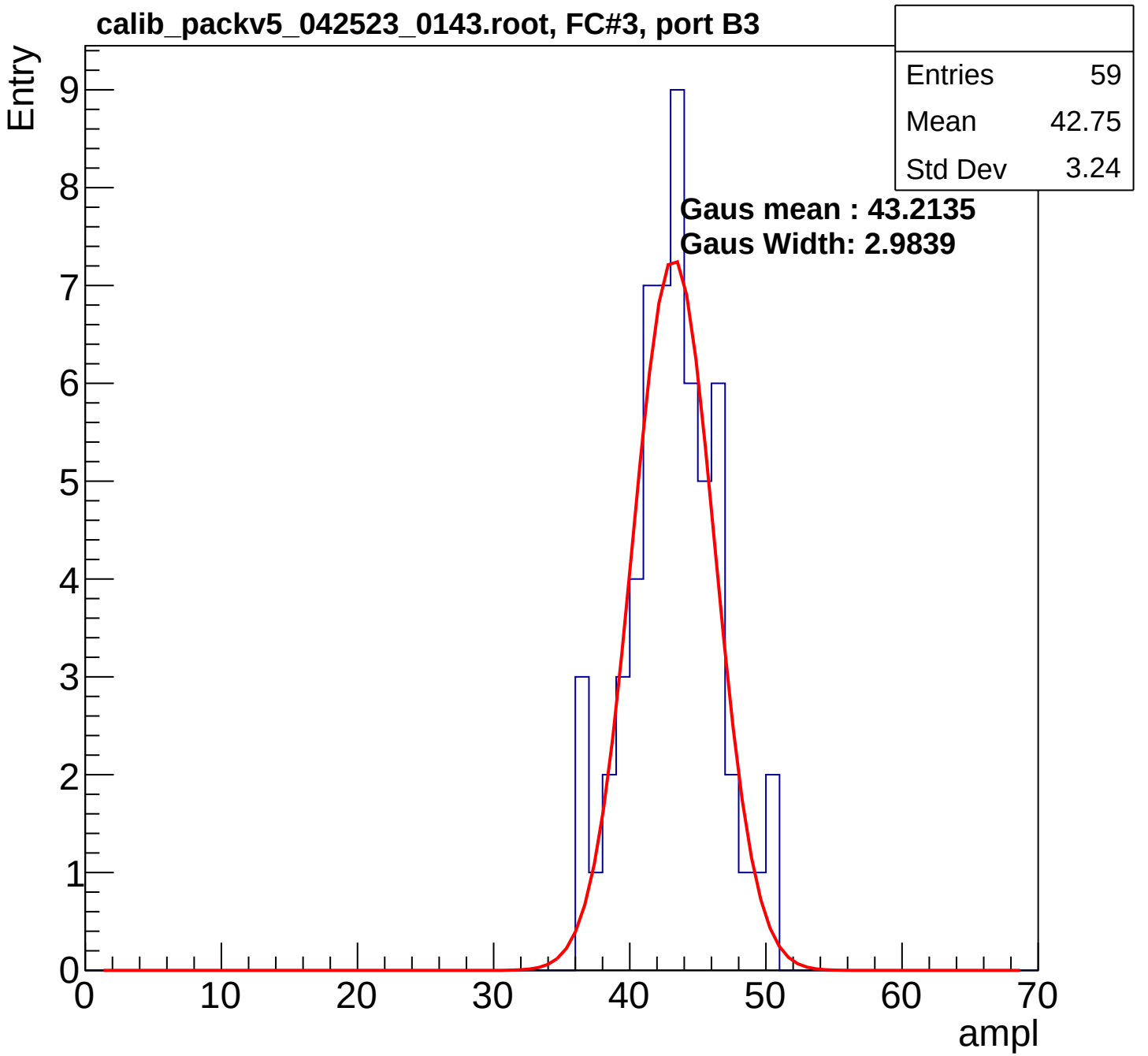
B1L000S, U3-ch119, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 42.75
Std Dev 3.24
Gaus mean : 43.2135
Gaus Width: 2.9839
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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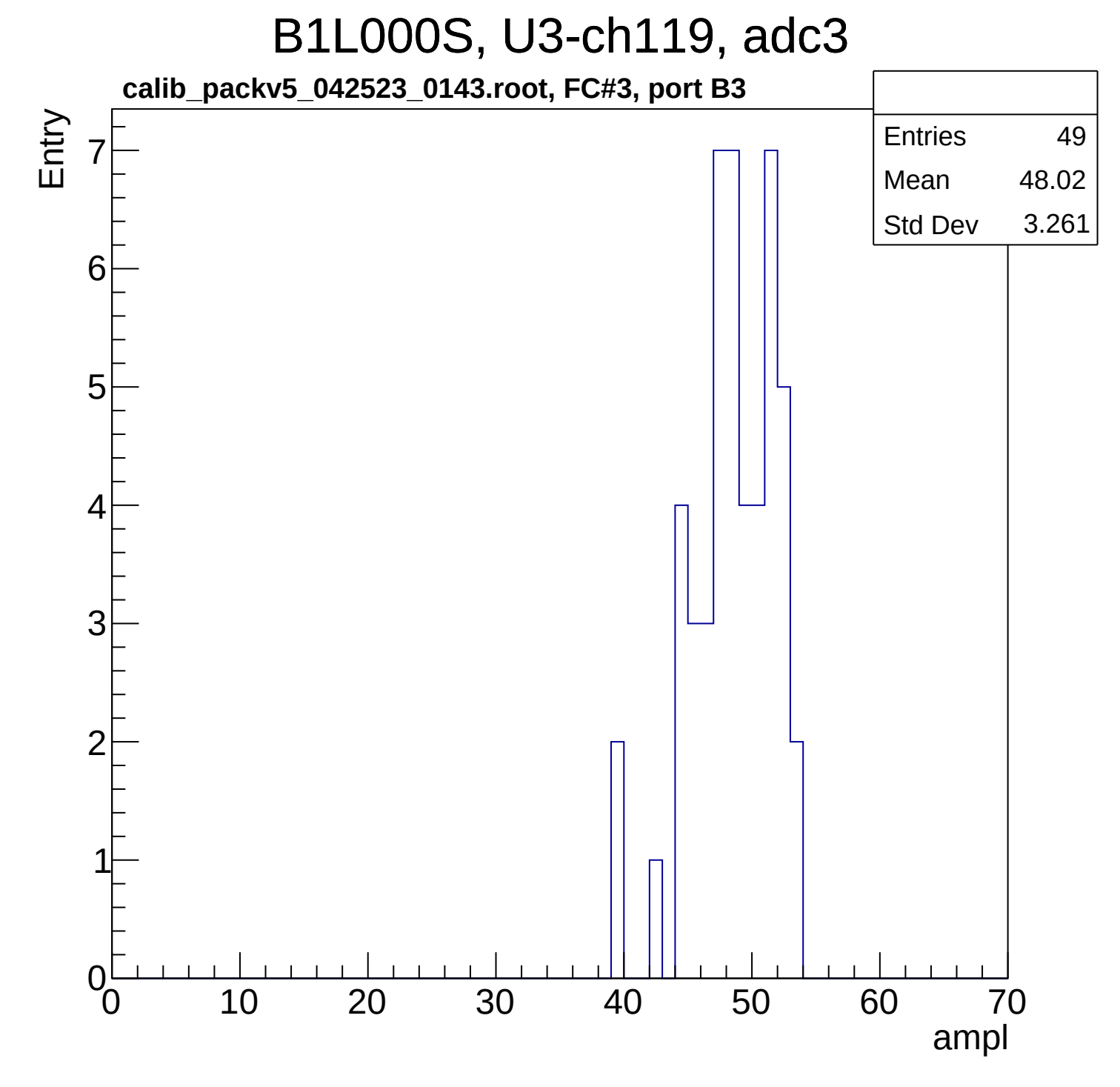
B1L000S, U3-ch119, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 48.02
Std Dev 3.261
Entry
ampl
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2
3
4
5
6
7
0
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20
30
40
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60
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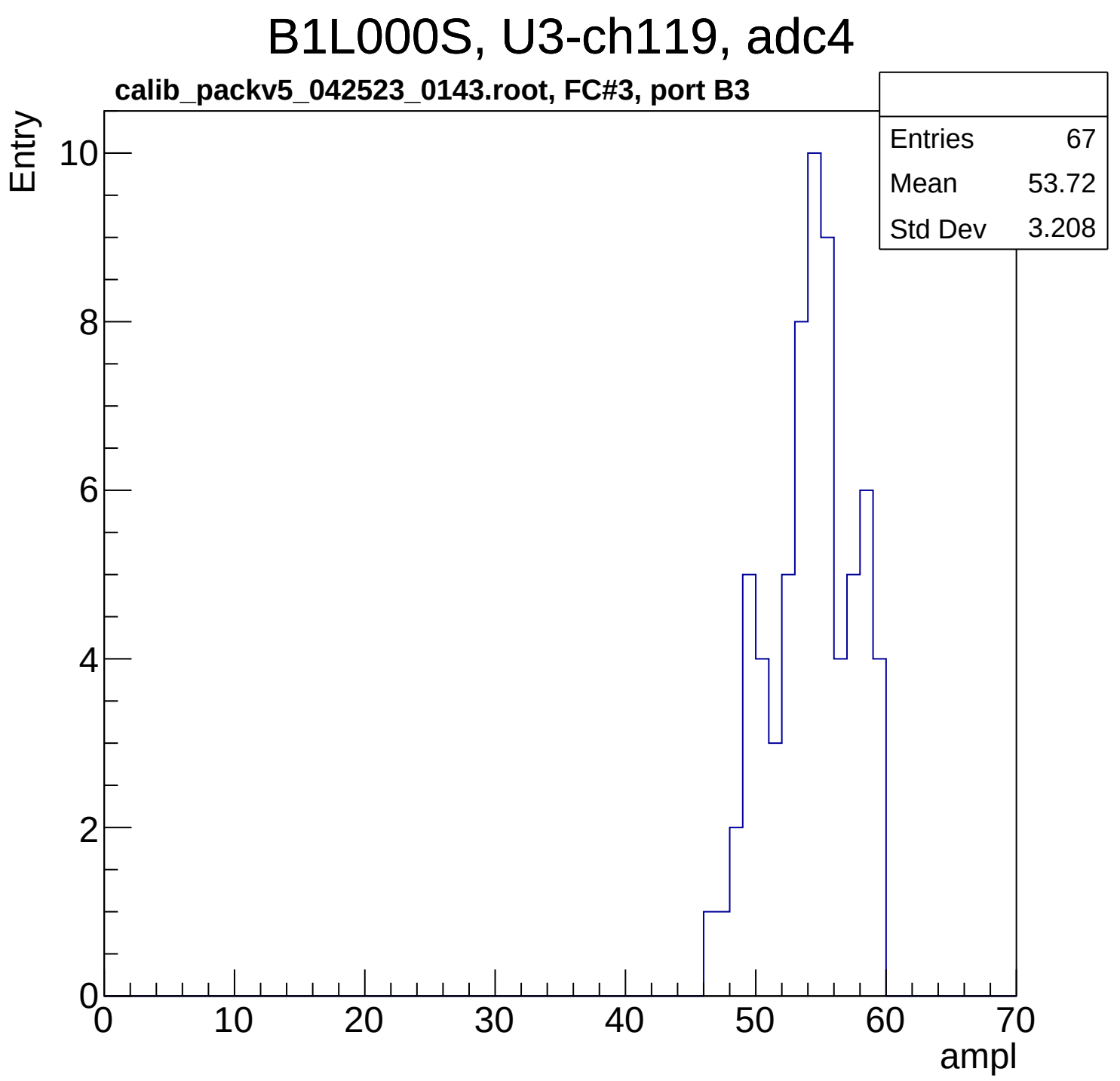
B1L000S, U3-ch119, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 53.72
Std Dev 3.208
Entry
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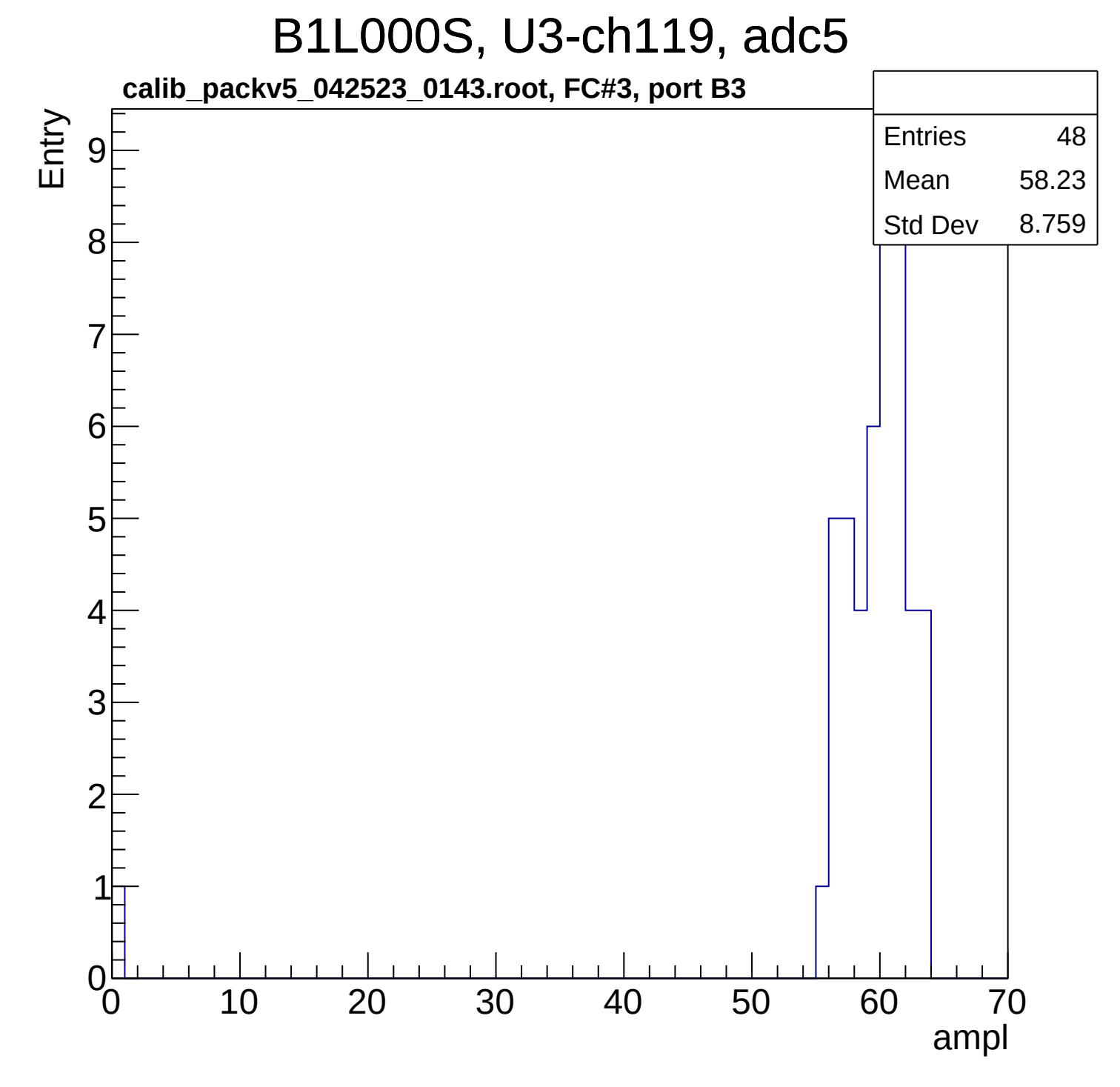
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch119, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.23
Std Dev 8.759
Entry
ampl
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3
4
5
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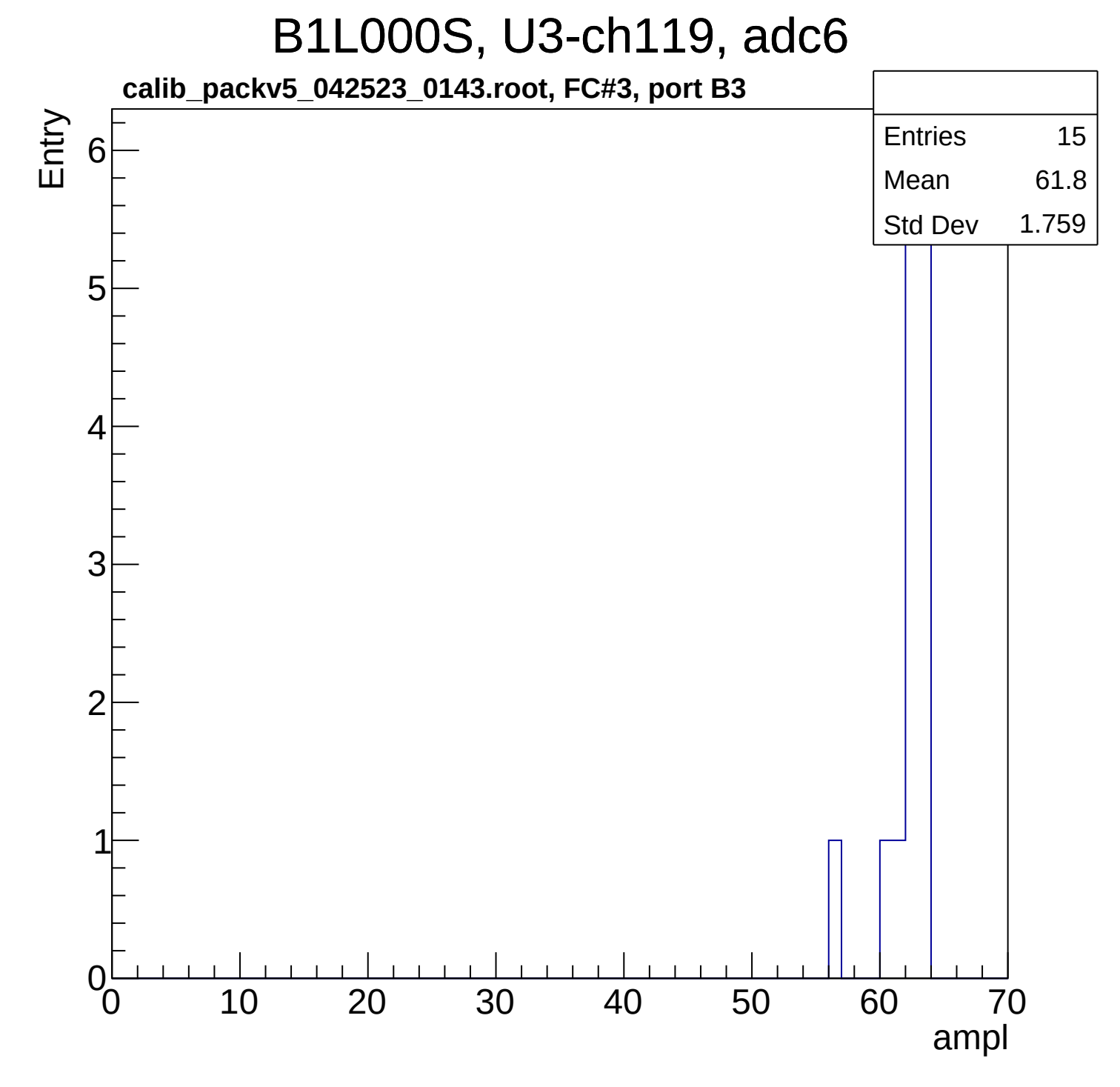
B1L000S, U3-ch119, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 61.8
Std Dev 1.759
Entry
0
1
2
3
4
5
6
0
10
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30
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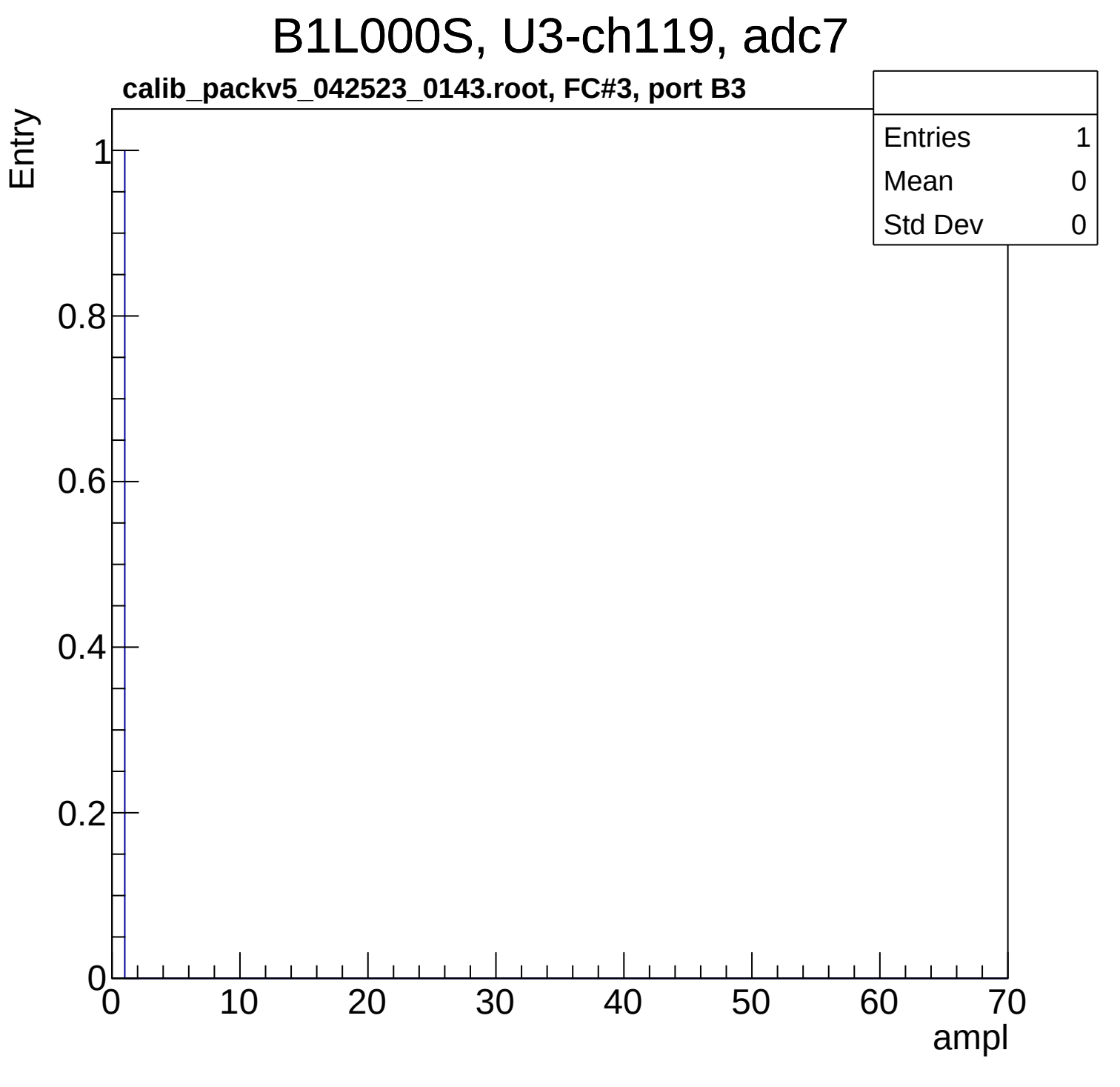
50
60
70
ampl


B1L000S, U3-ch119, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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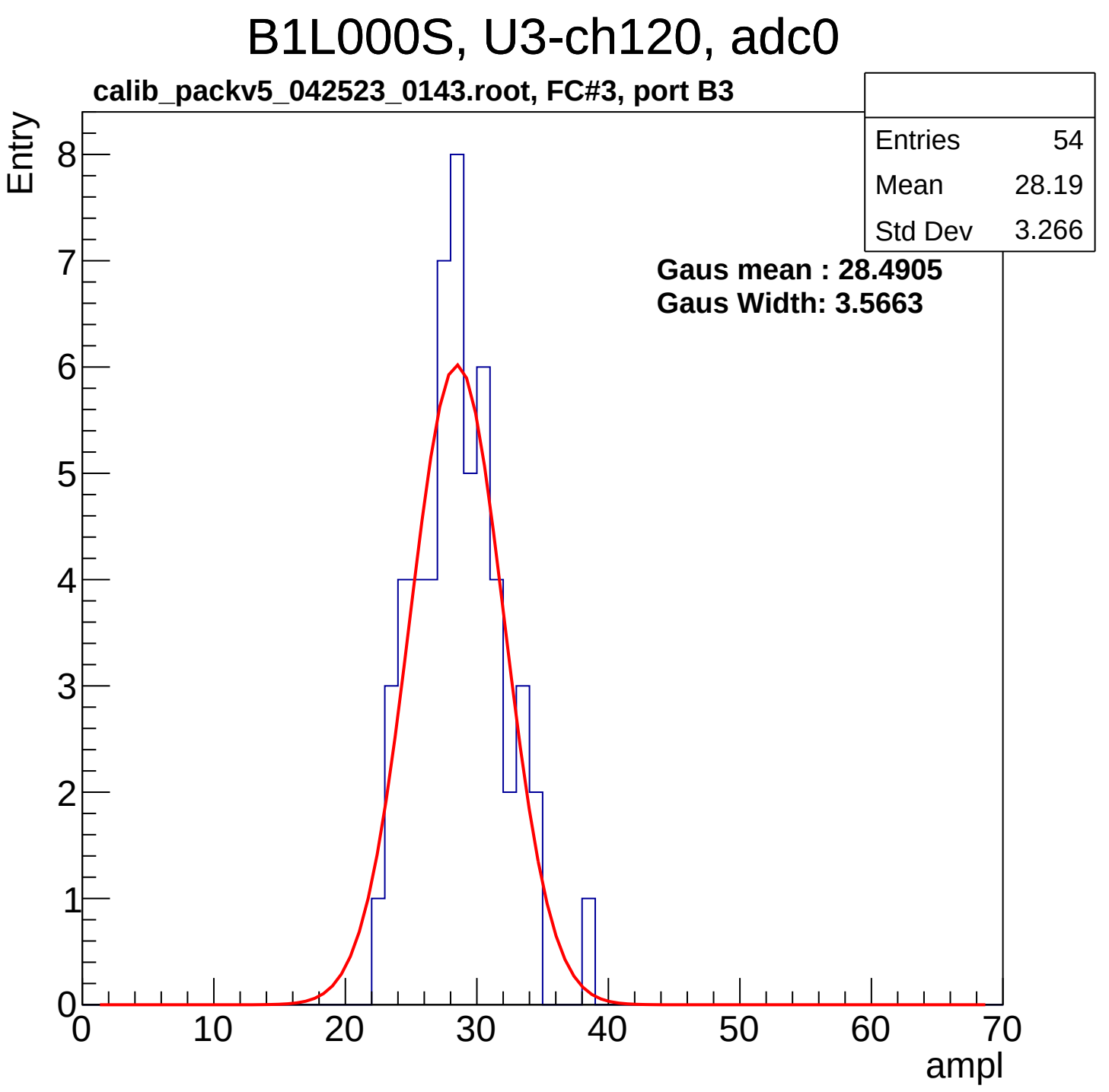
40
50
60
70
ampl


B1L000S, U3-ch120, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 28.19
Std Dev 3.266
Gaus mean : 28.4905
Gaus Width: 3.5663
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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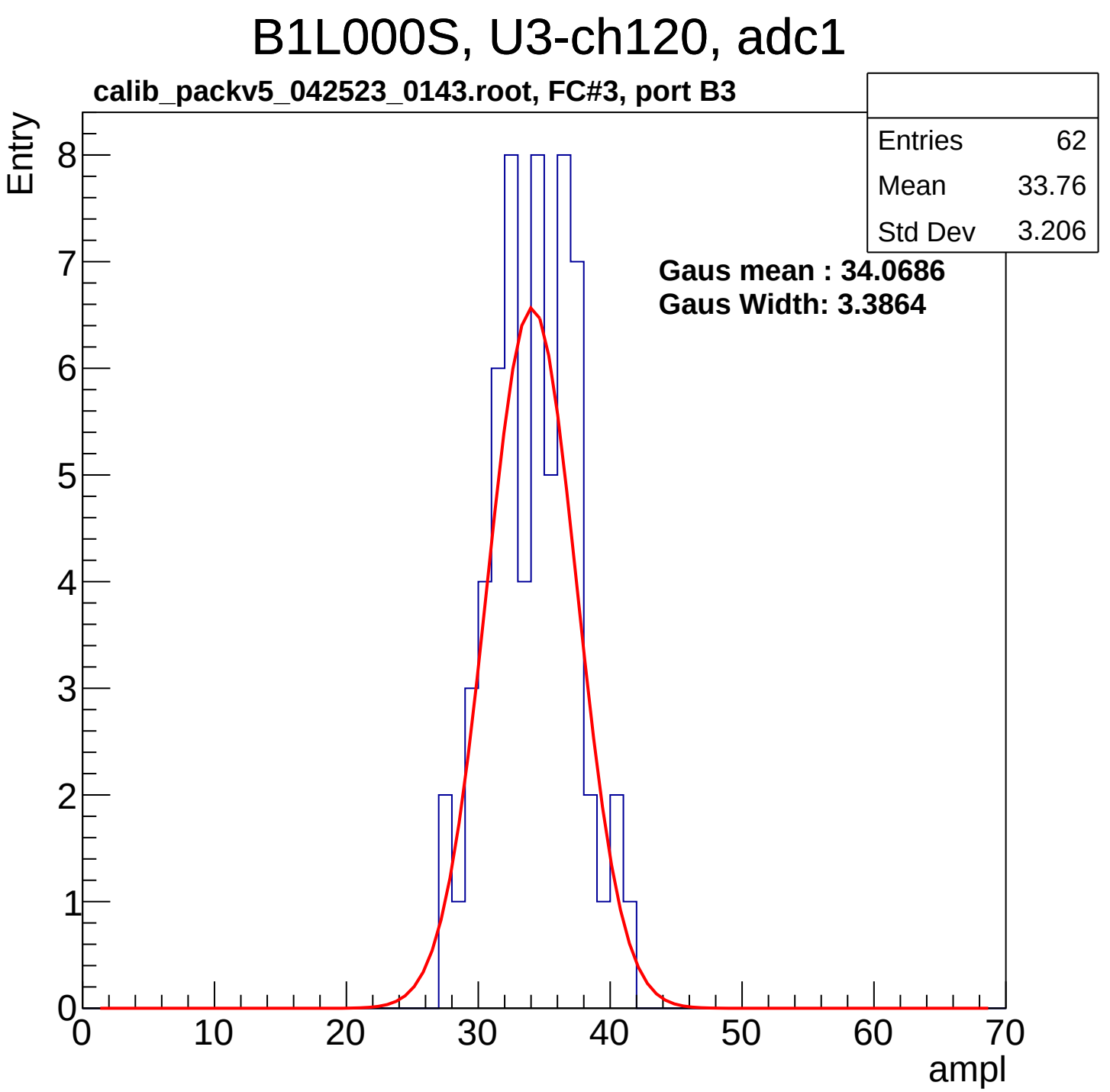
B1L000S, U3-ch120, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 33.76
Std Dev 3.206
Gaus mean : 34.0686
Gaus Width: 3.3864
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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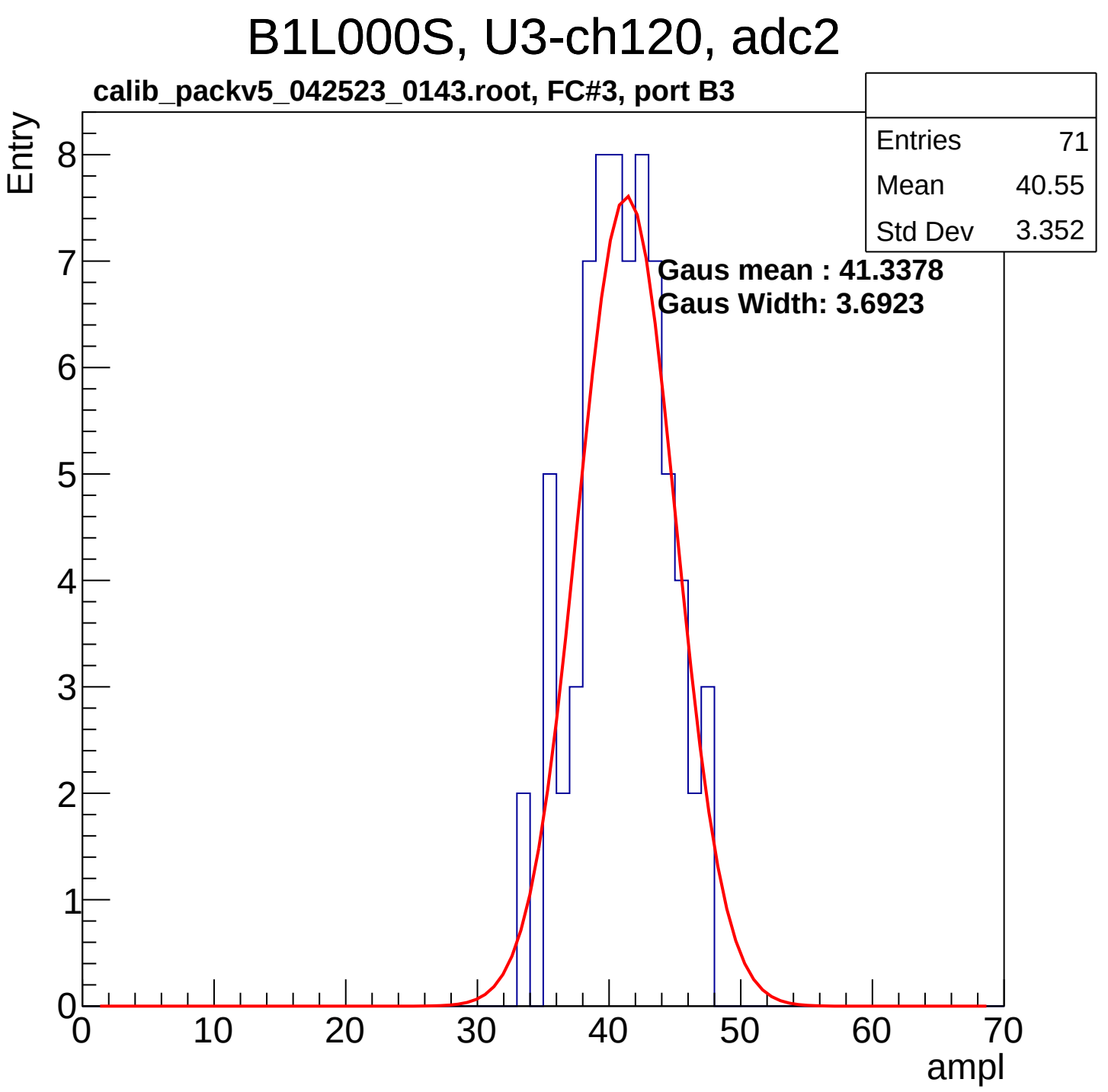
B1L000S, U3-ch120, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 40.55
Std Dev 3.352
Gaus mean : 41.3378
Gaus Width: 3.6923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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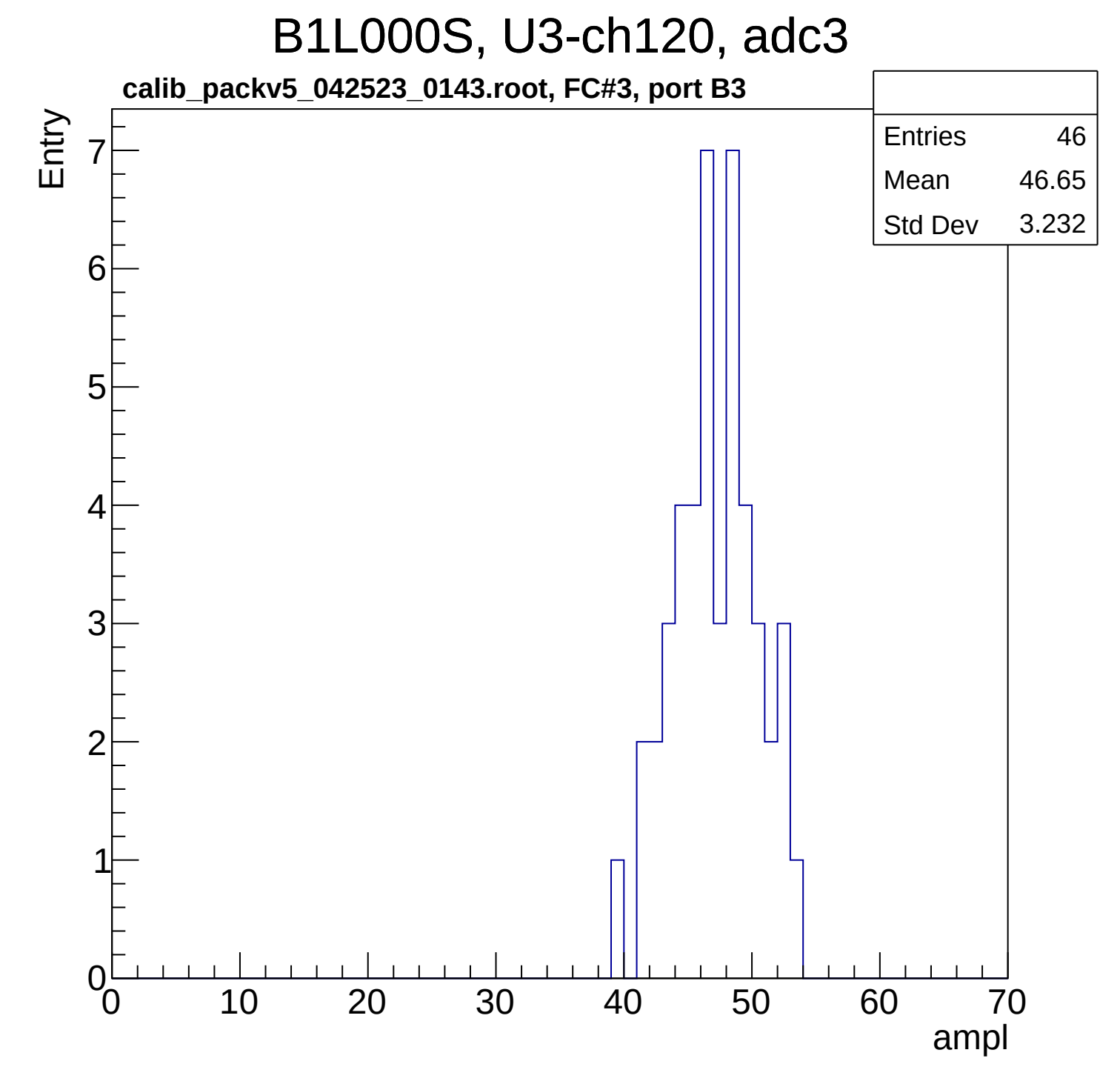
B1L000S, U3-ch120, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 46.65
Std Dev 3.232
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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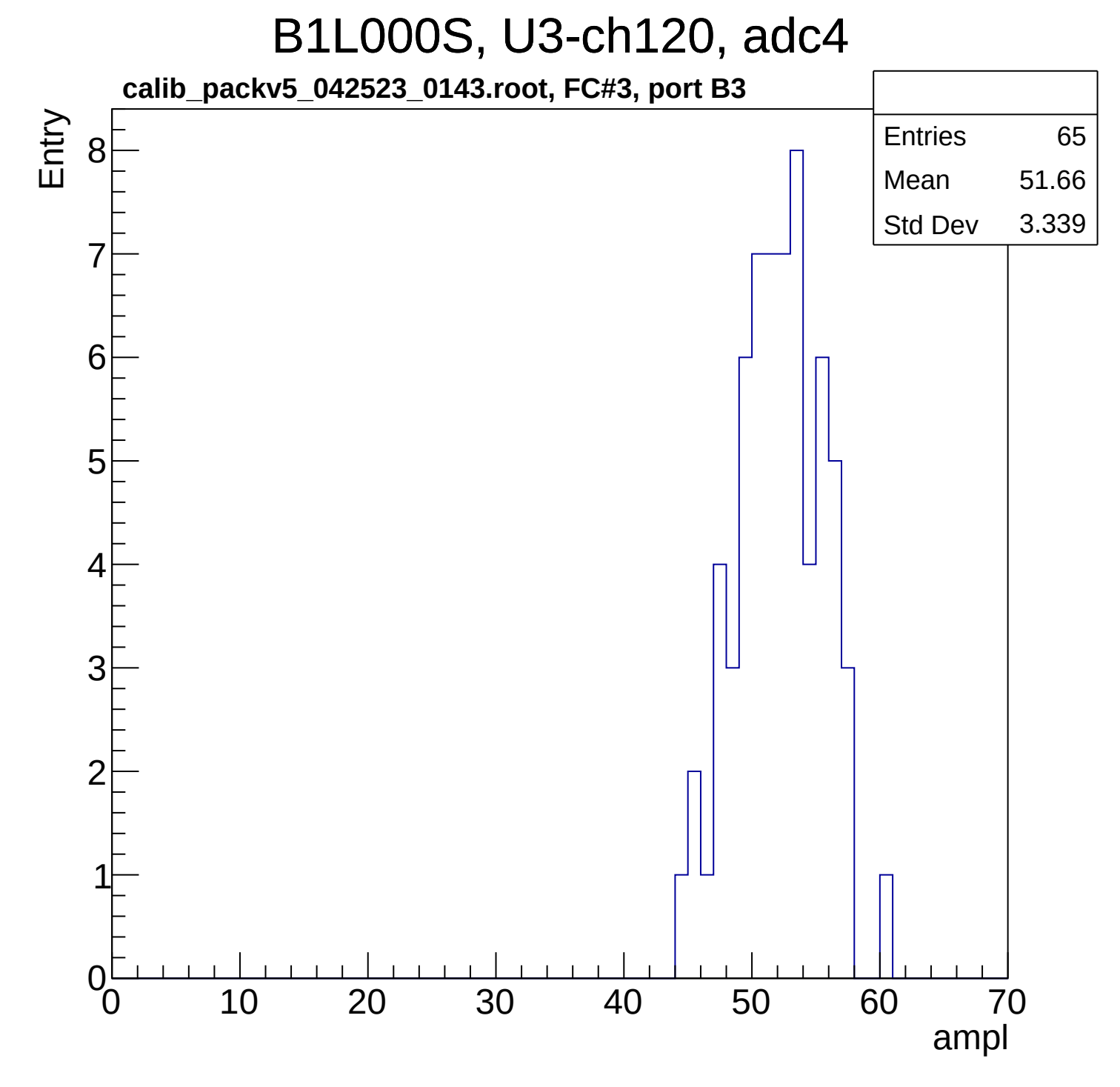
B1L000S, U3-ch120, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 51.66
Std Dev 3.339
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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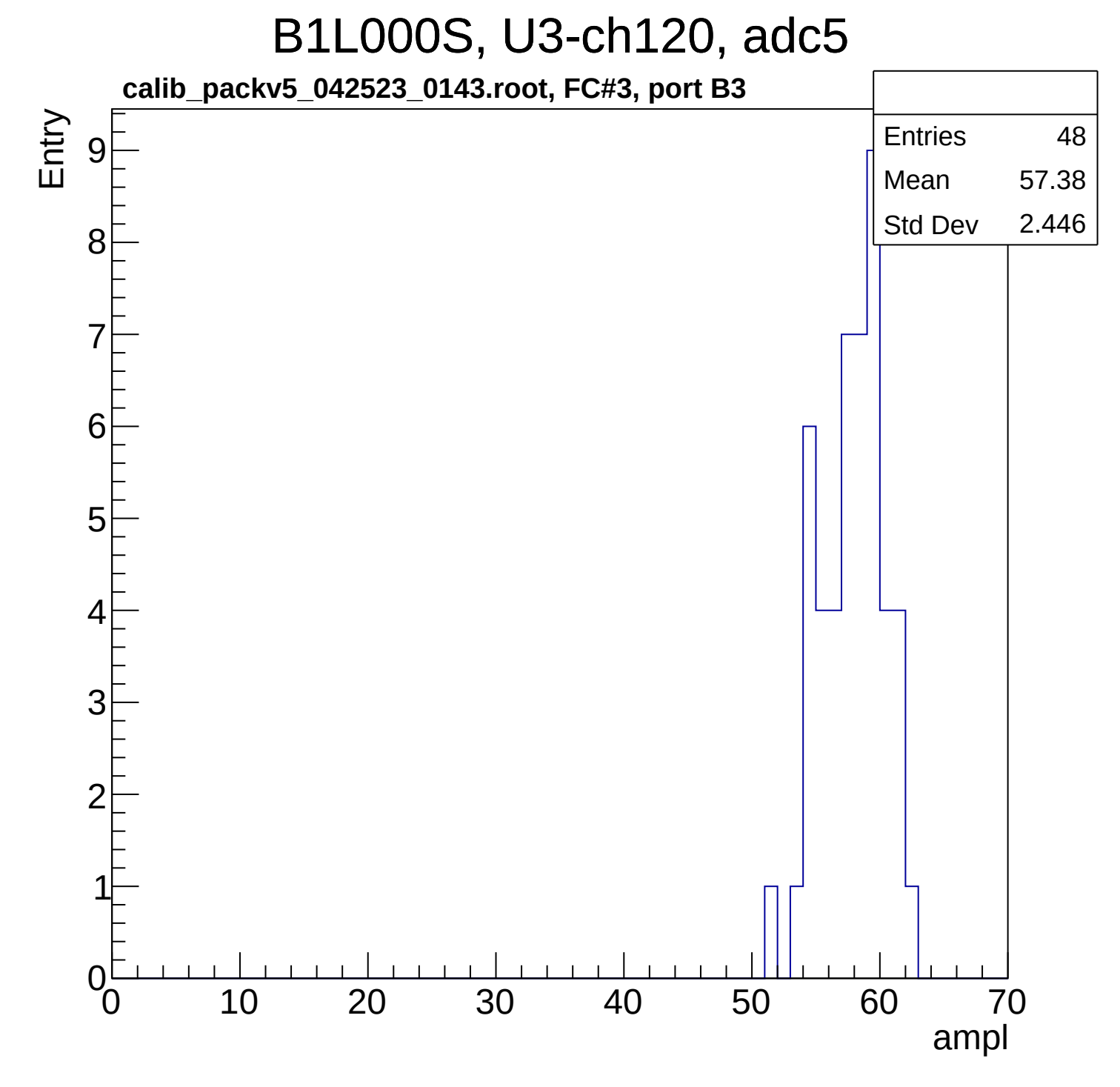
B1L000S, U3-ch120, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 57.38
Std Dev 2.446
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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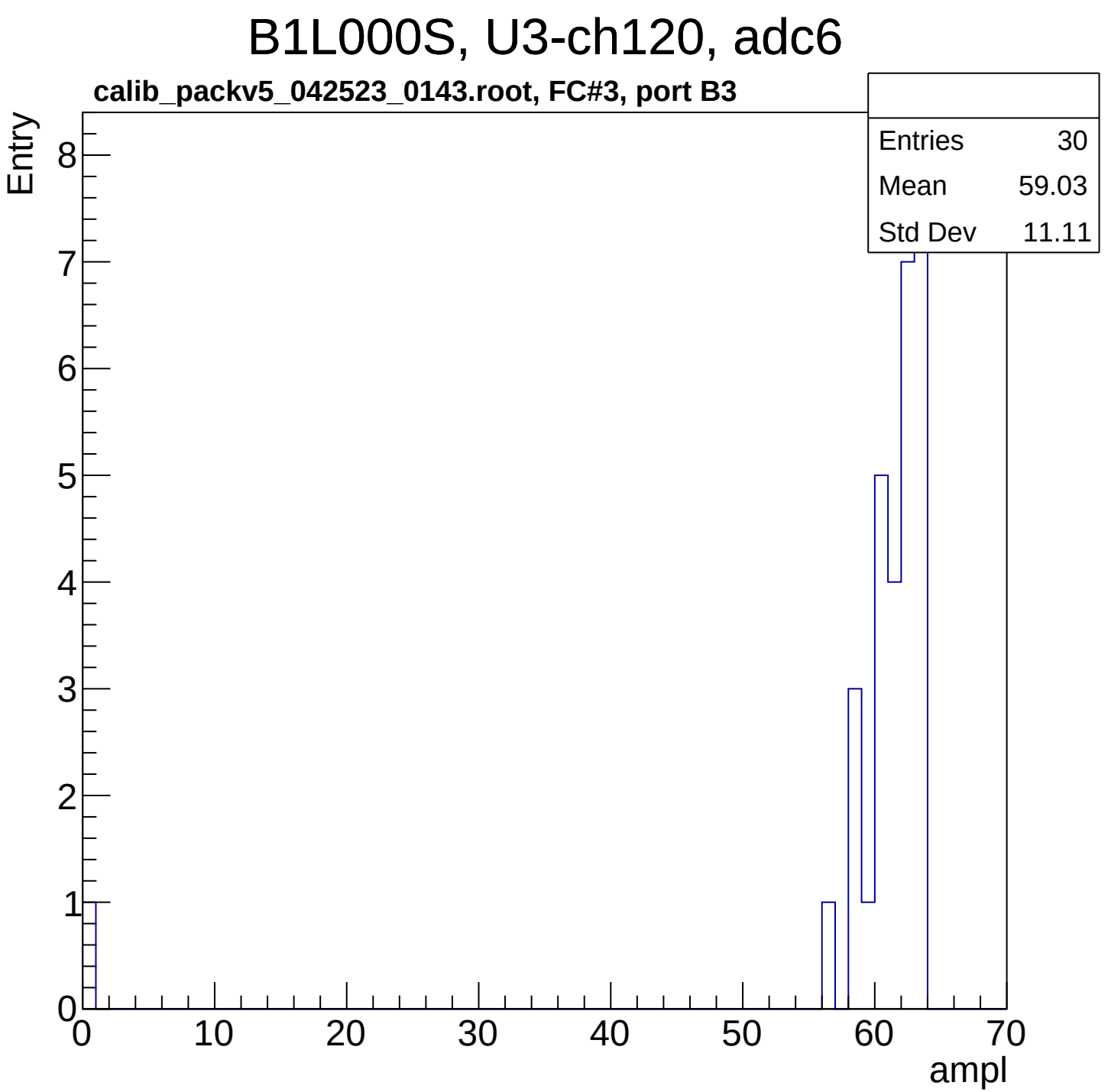
B1L000S, U3-ch120, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 30
Mean 59.03
Std Dev 11.11
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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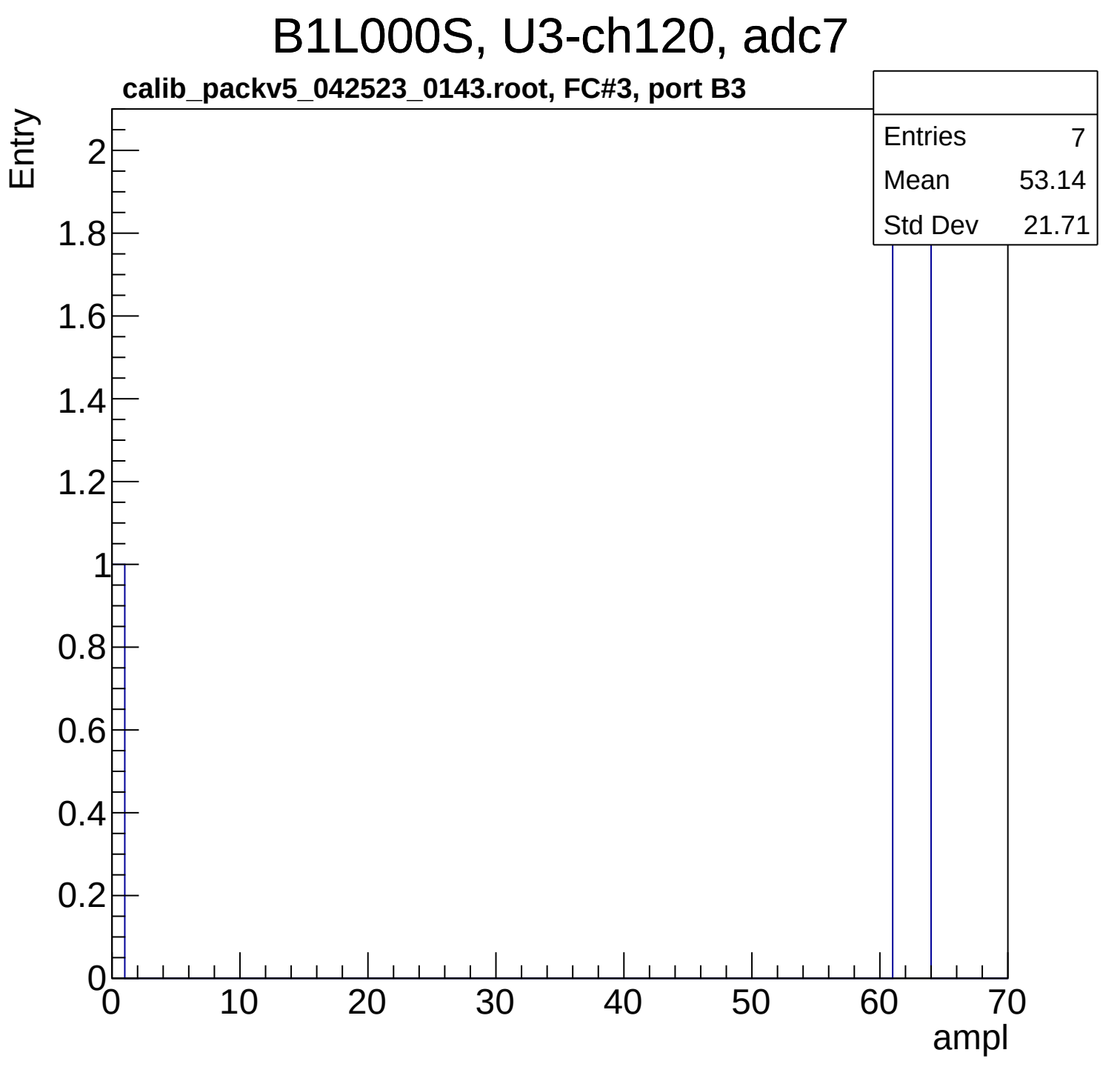
B1L000S, U3-ch120, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 53.14
Std Dev 21.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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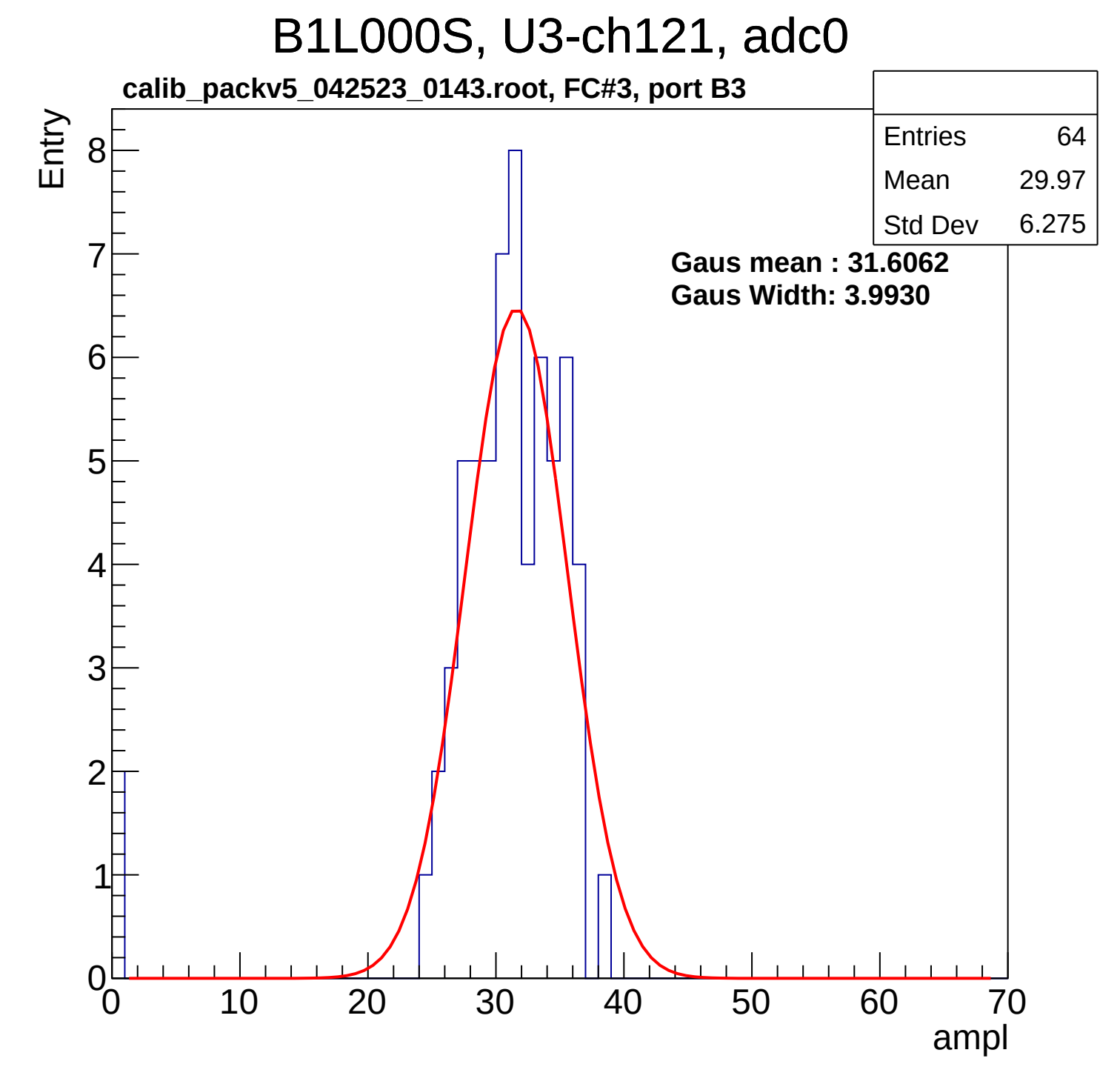
B1L000S, U3-ch121, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 29.97
Std Dev 6.275
Gaus mean : 31.6062
Gaus Width: 3.9930
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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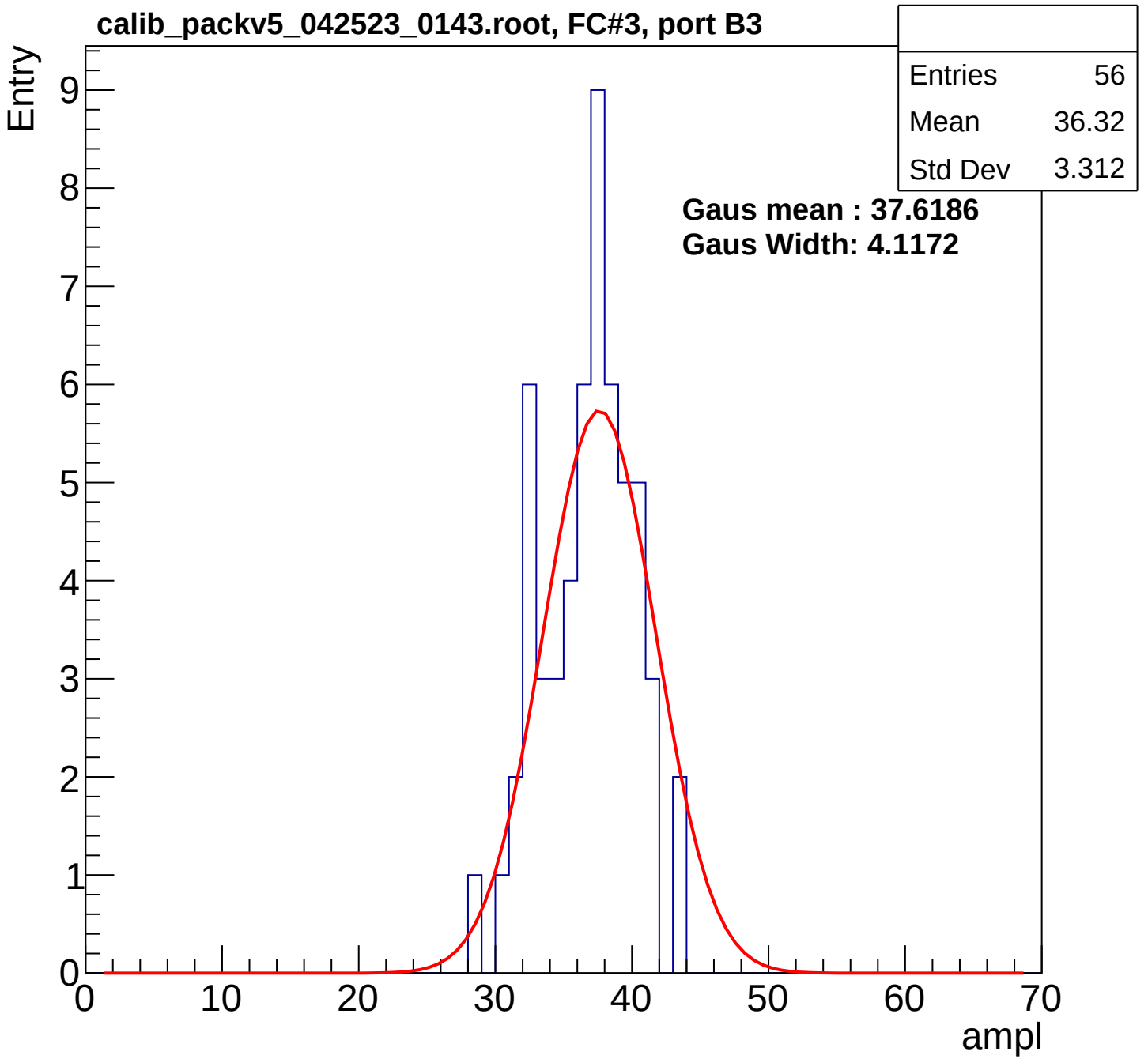
B1L000S, U3-ch121, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 36.32
Std Dev 3.312
Gaus mean : 37.6186
Gaus Width: 4.1172
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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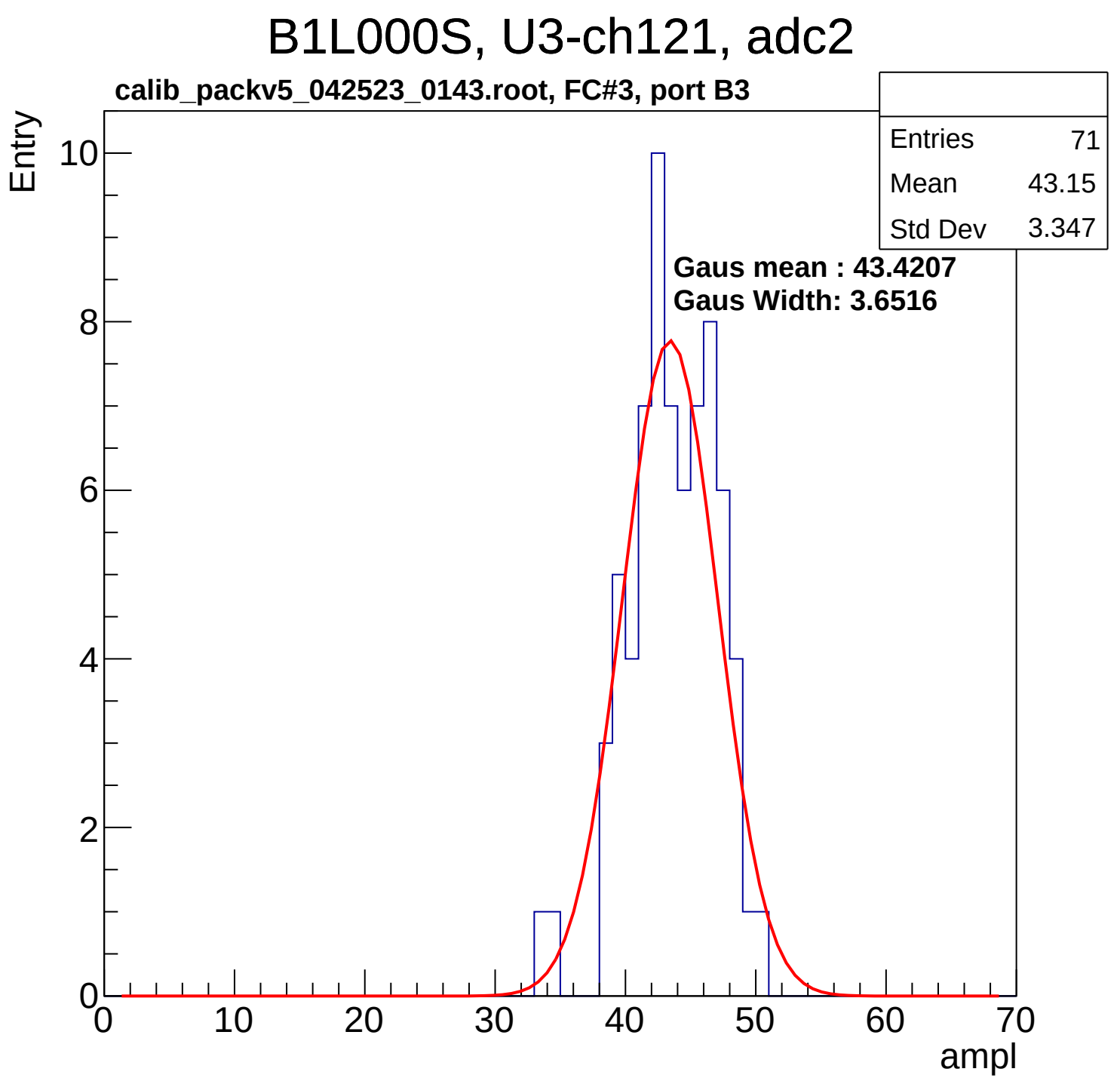
B1L000S, U3-ch121, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 43.15
Std Dev 3.347
Gaus mean : 43.4207
Gaus Width: 3.6516
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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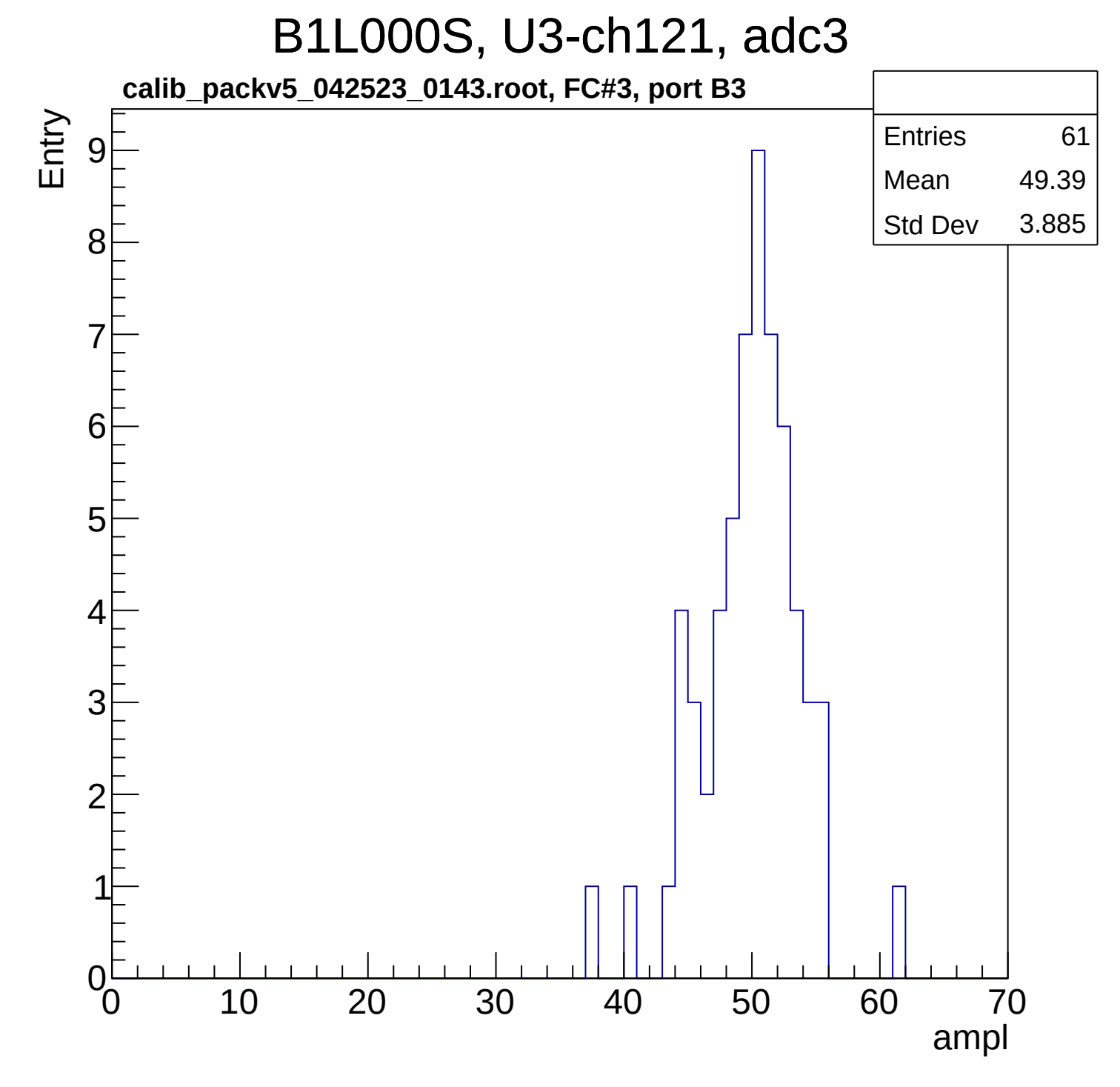
B1L000S, U3-ch121, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 49.39
Std Dev 3.885
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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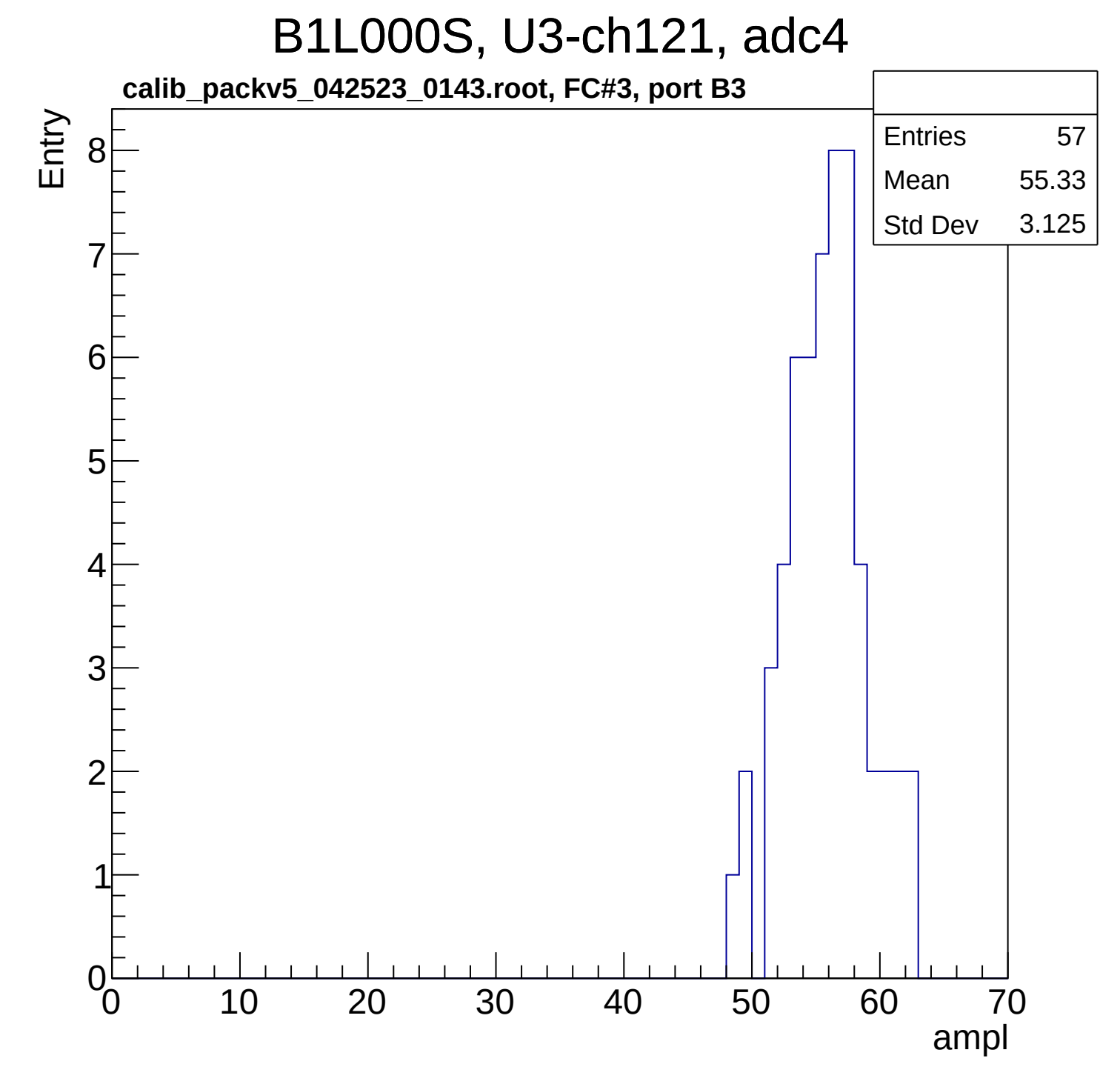
B1L000S, U3-ch121, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 55.33
Std Dev 3.125
Entry
ampl
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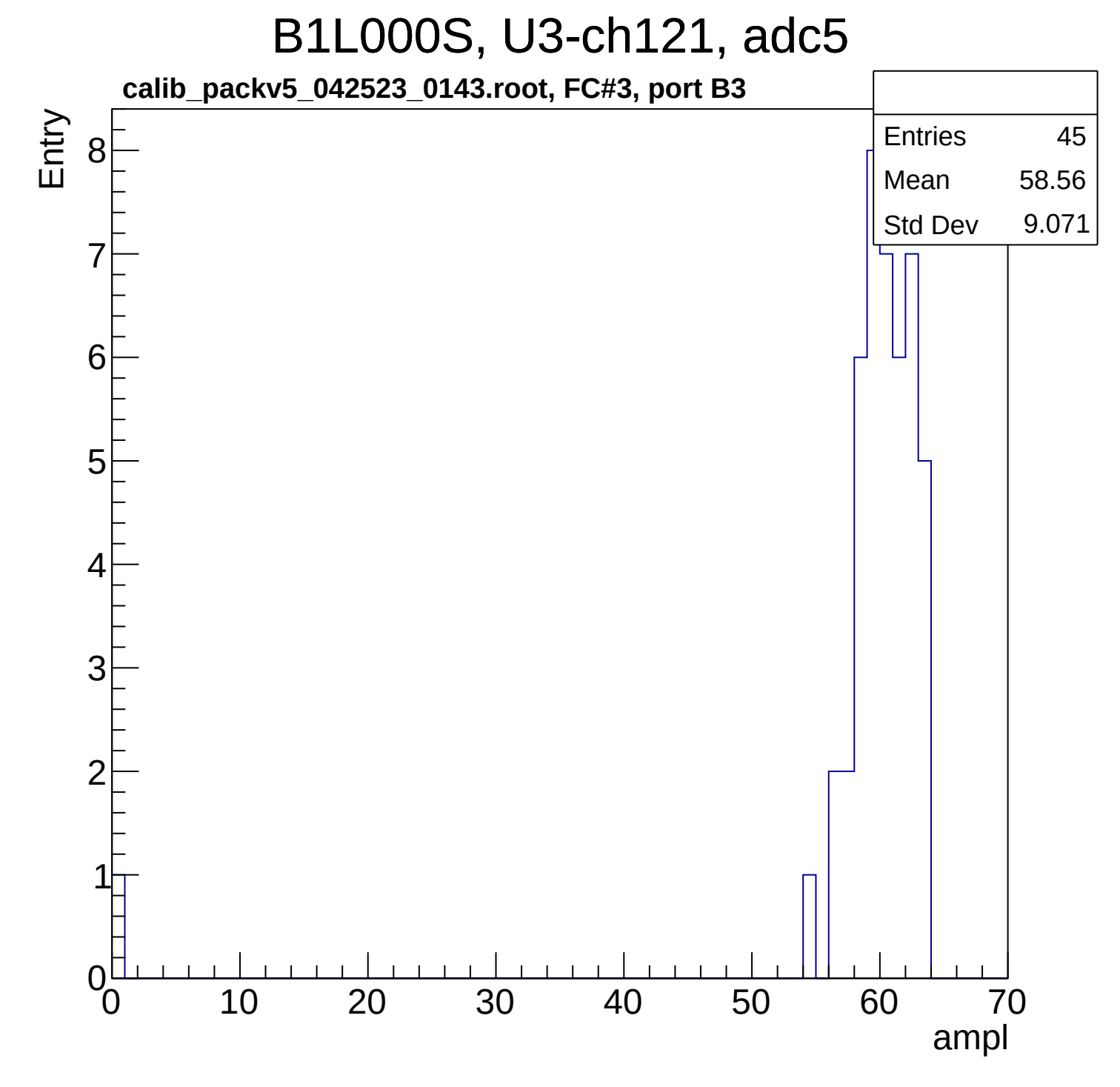
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch121, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 58.56
Std Dev 9.071
Entry
ampl
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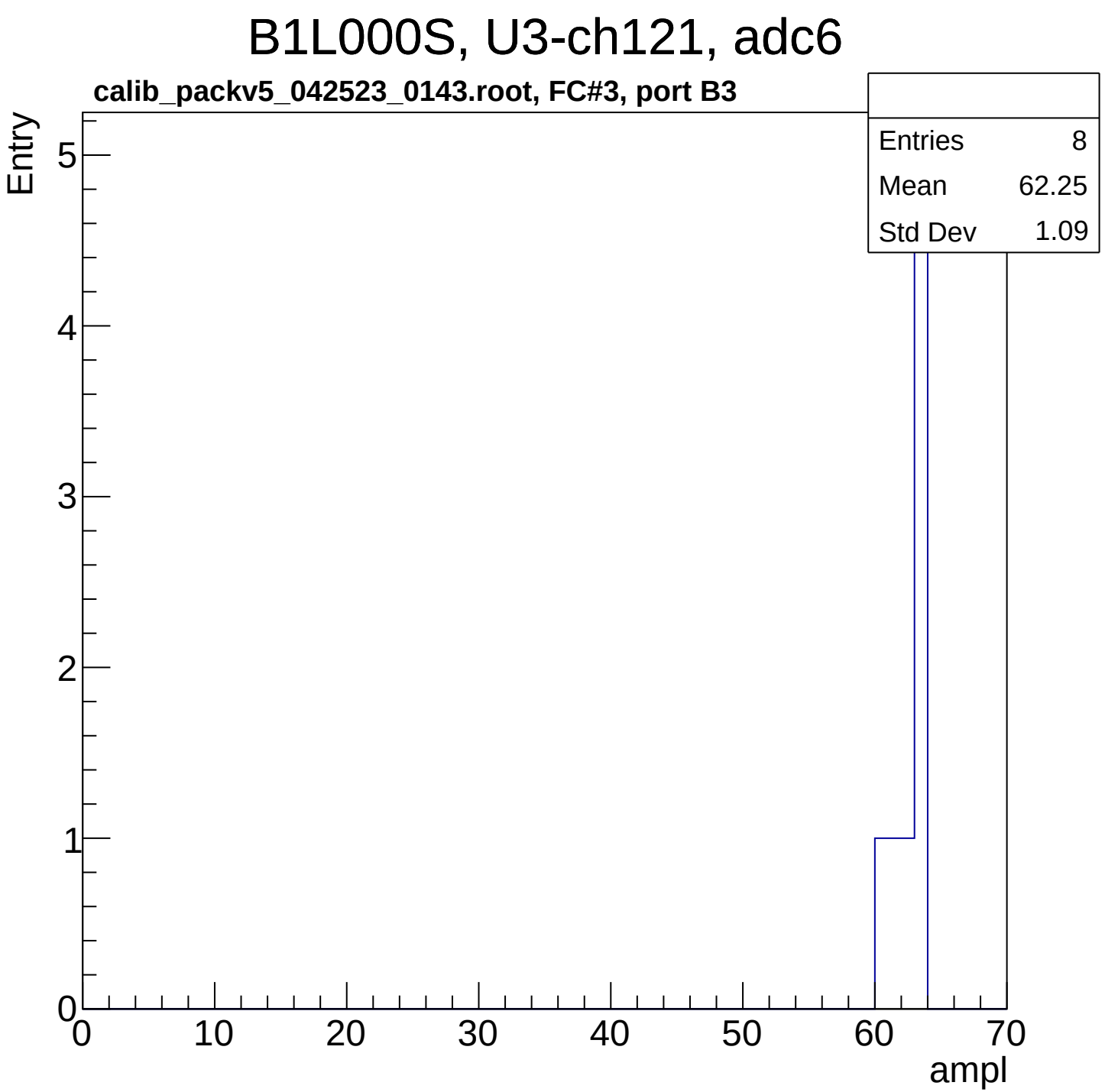
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch121, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62.25
Std Dev 1.09
Entry
0
1
2
3
4
5
0
10
20
30
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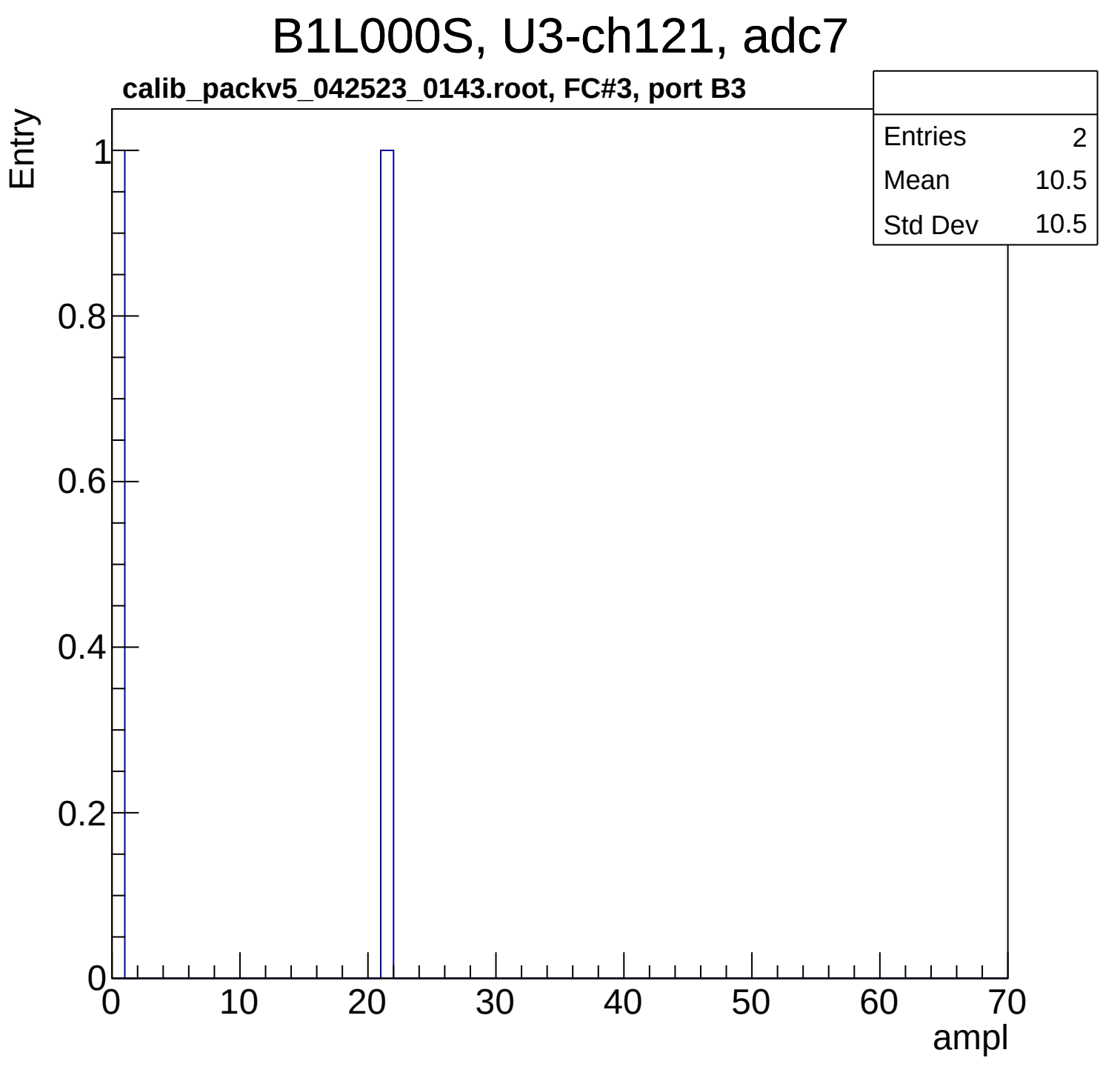
50
60
70
ampl


B1L000S, U3-ch121, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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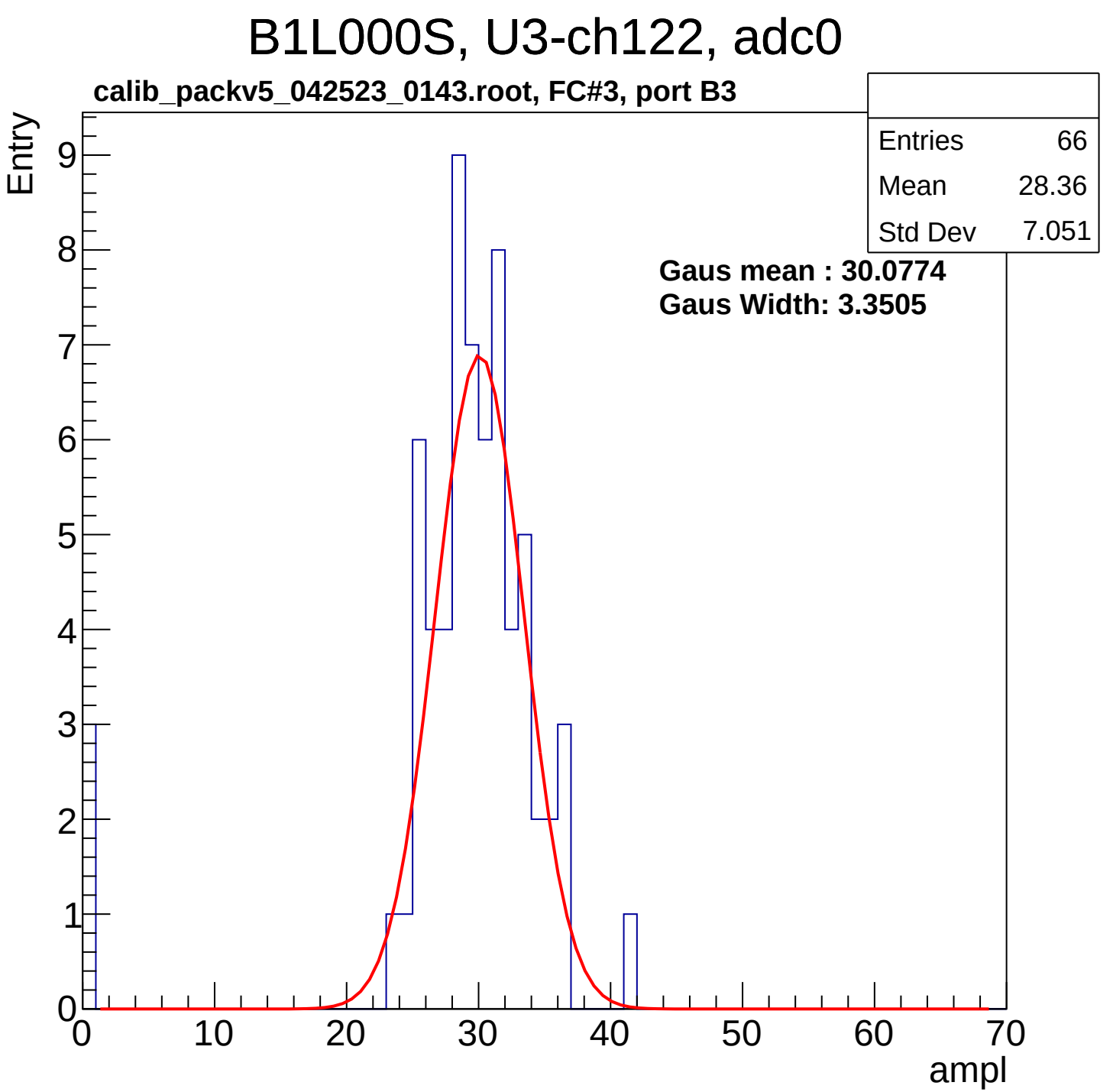
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch122, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.36
Std Dev 7.051
Gaus mean : 30.0774
Gaus Width: 3.3505
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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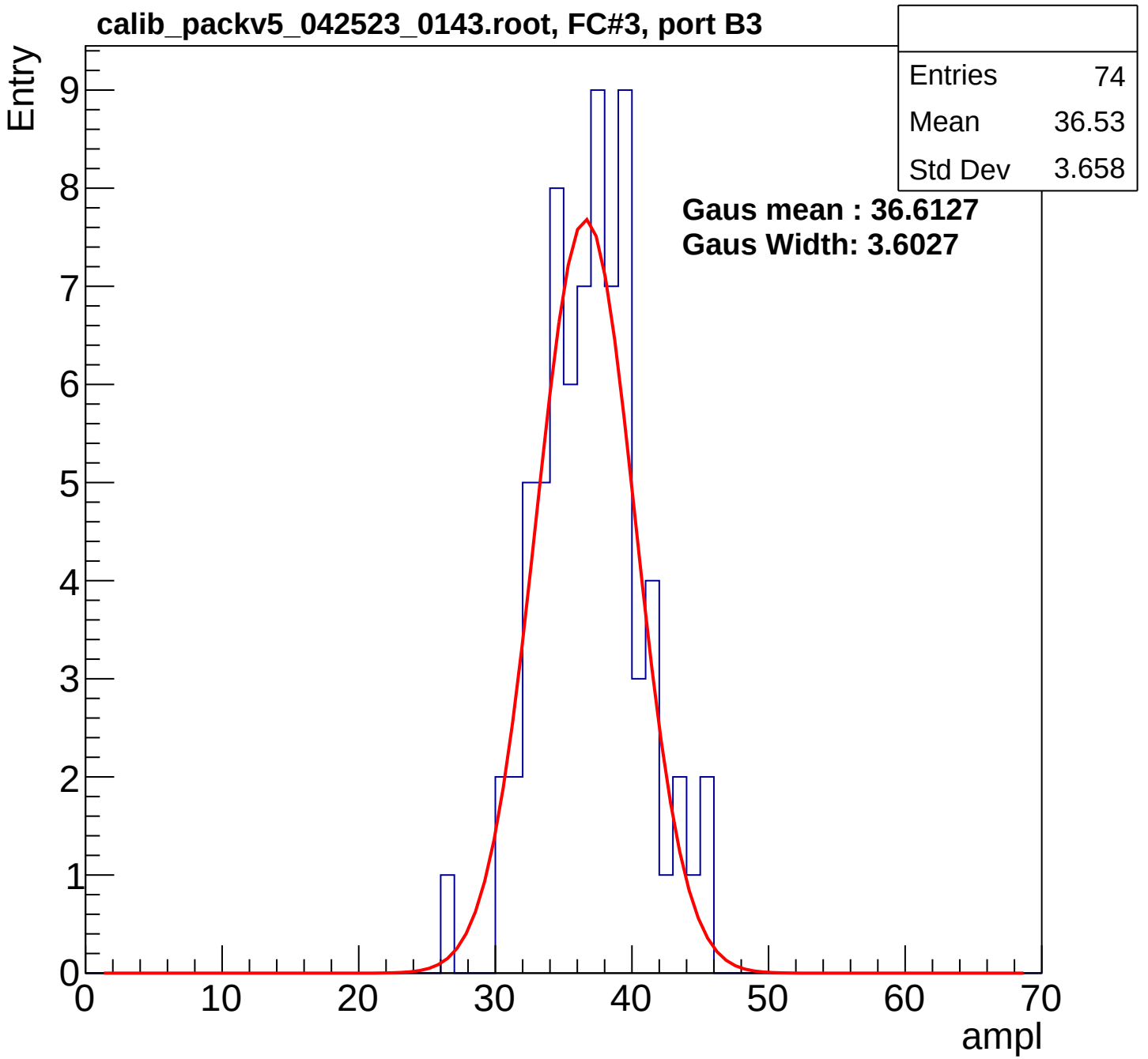
B1L000S, U3-ch122, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 36.53
Std Dev 3.658
Gaus mean : 36.6127
Gaus Width: 3.6027
Entry
ampl
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10
20
30
40
50
60
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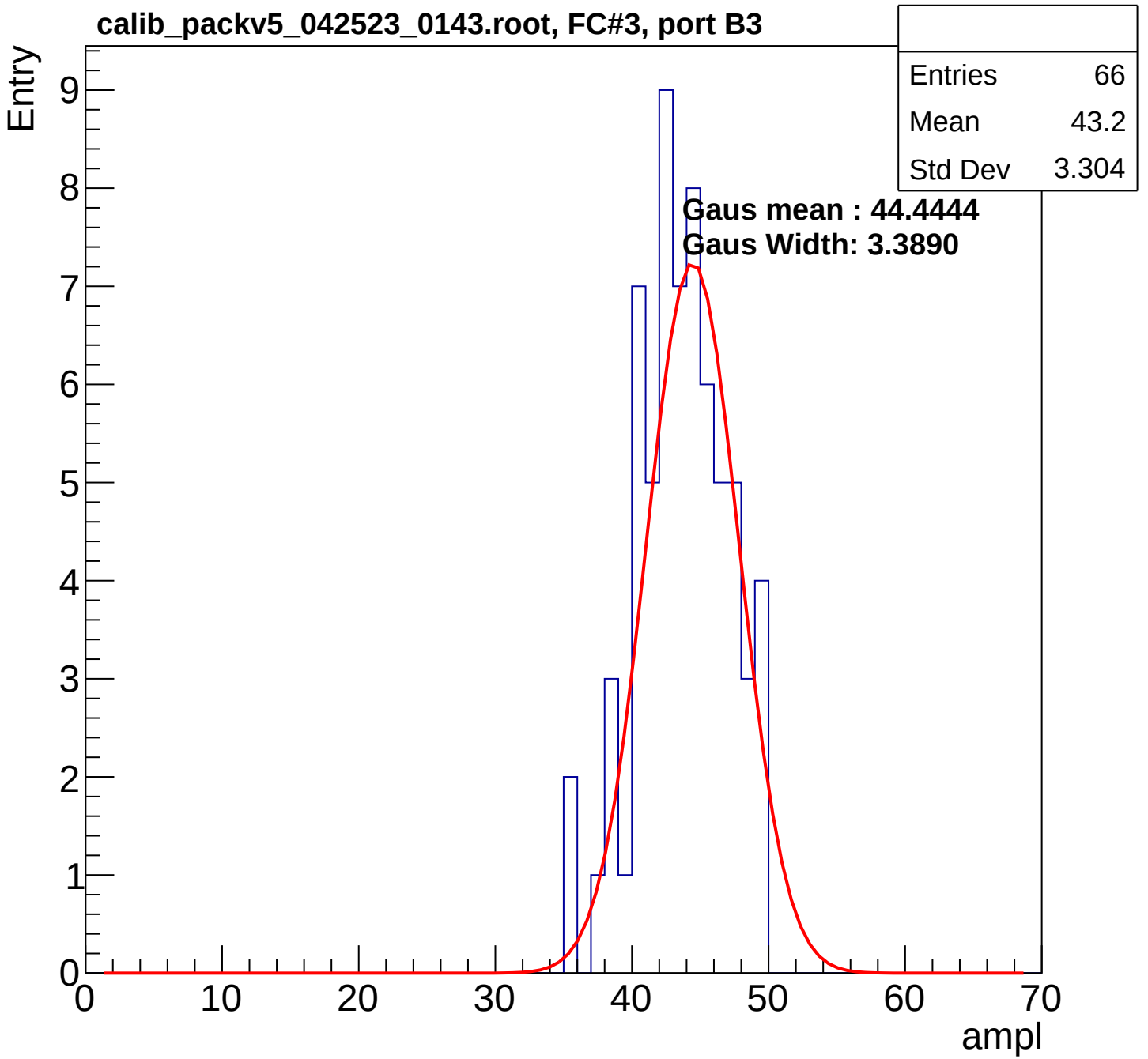
1
2
3
4
5
6
7
8
9


B1L000S, U3-ch122, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 43.2
Std Dev 3.304
Gaus mean : 44.4444
Gaus Width: 3.3890
Entry
ampl
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2
3
4
5
6
7
8
9
0
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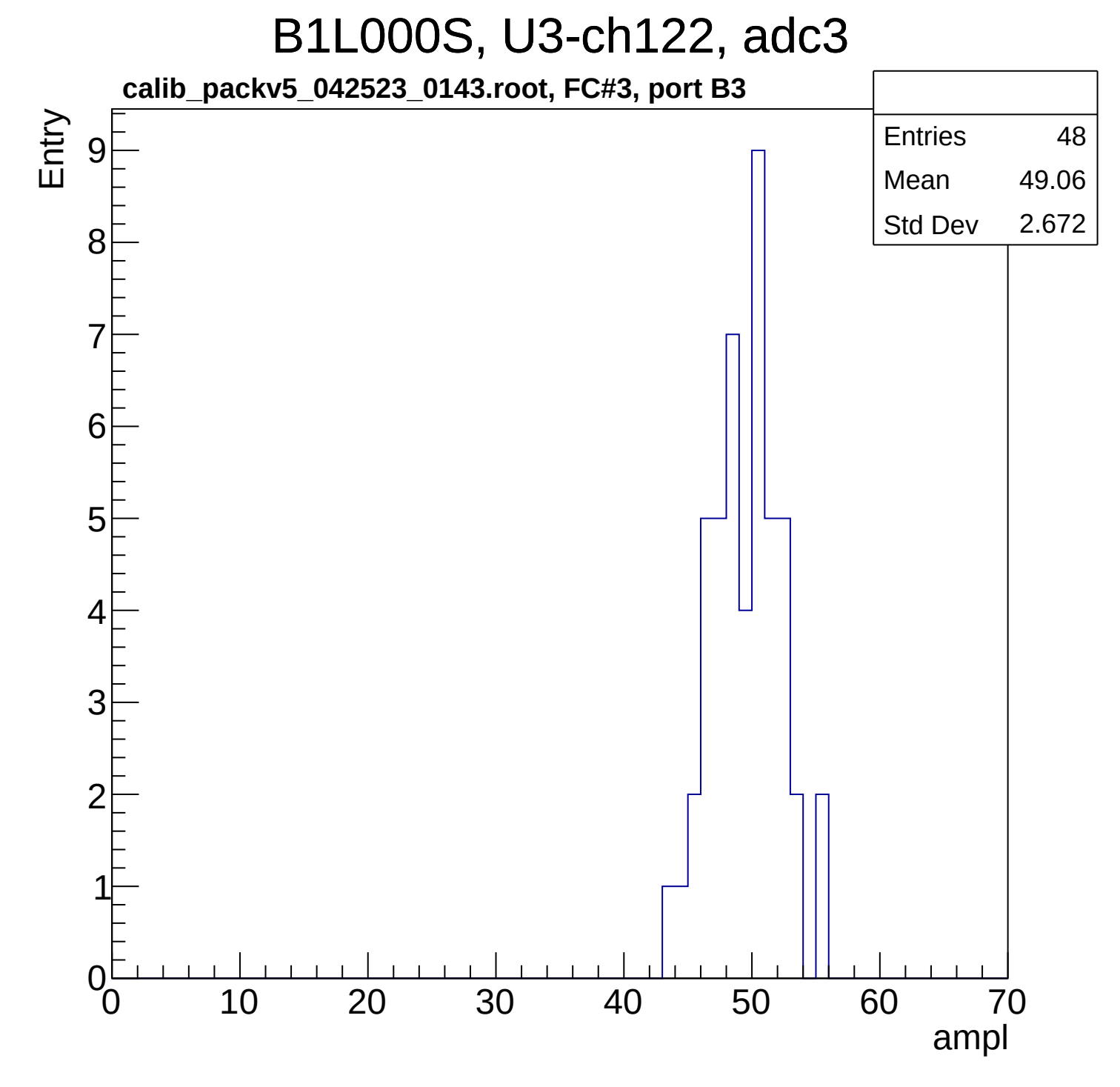
B1L000S, U3-ch122, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 49.06
Std Dev 2.672
Entry
ampl
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3
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5
6
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0
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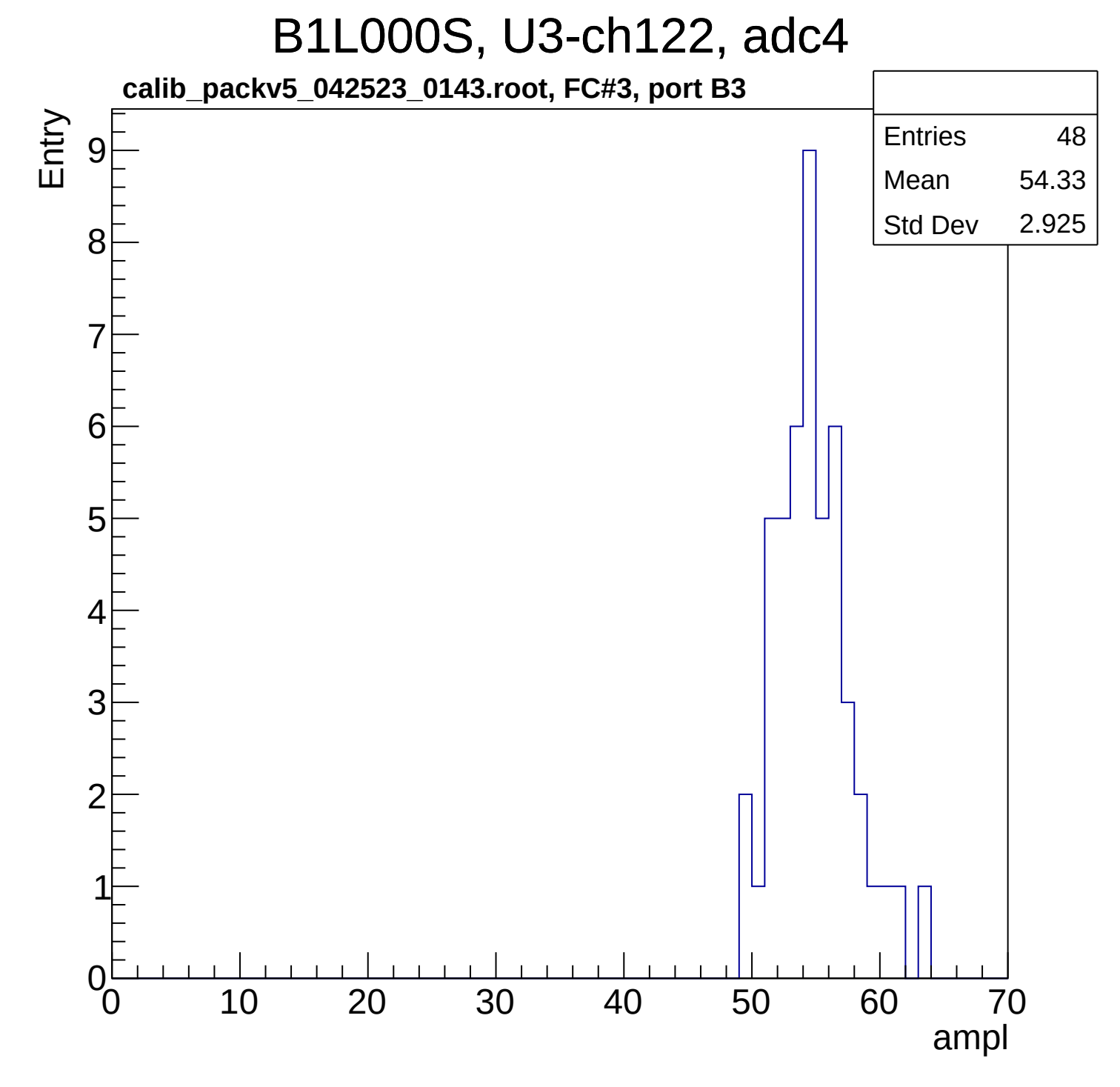
B1L000S, U3-ch122, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 54.33
Std Dev 2.925
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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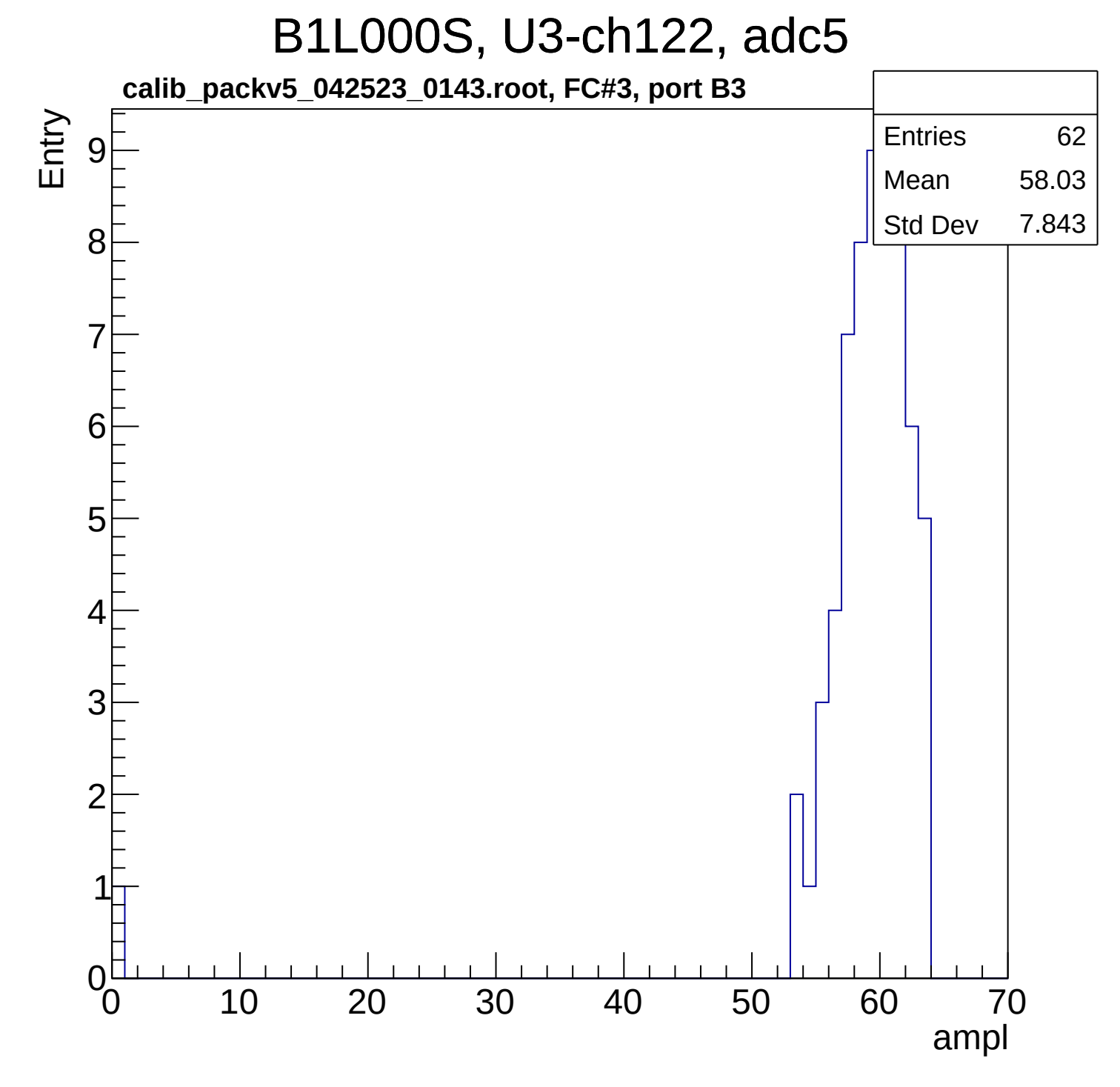
B1L000S, U3-ch122, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 58.03
Std Dev 7.843
Entry
ampl
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2
3
4
5
6
7
8
9
0
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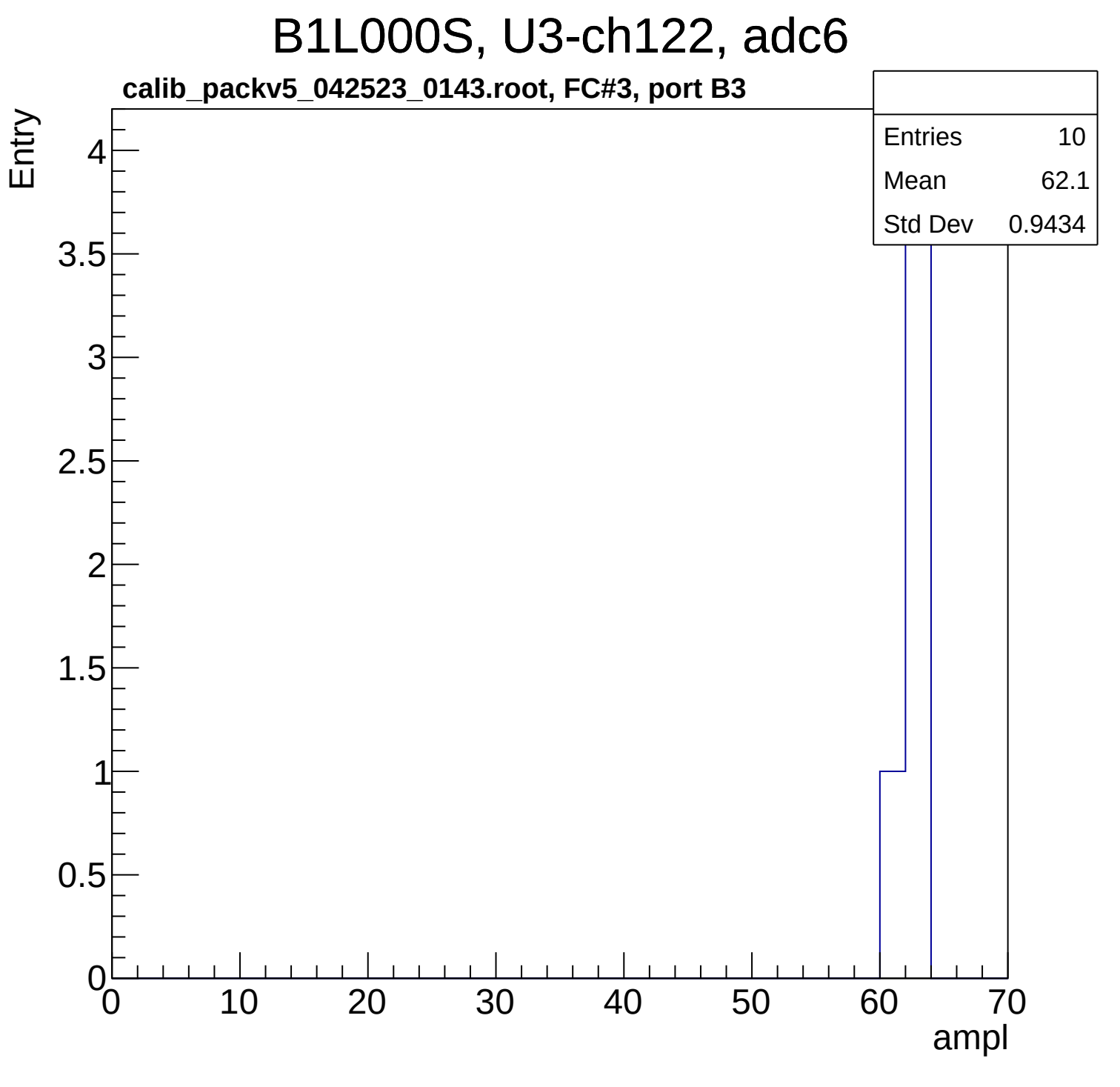
60
70


B1L000S, U3-ch122, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62.1
Std Dev 0.9434
Entry
ampl
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0.5
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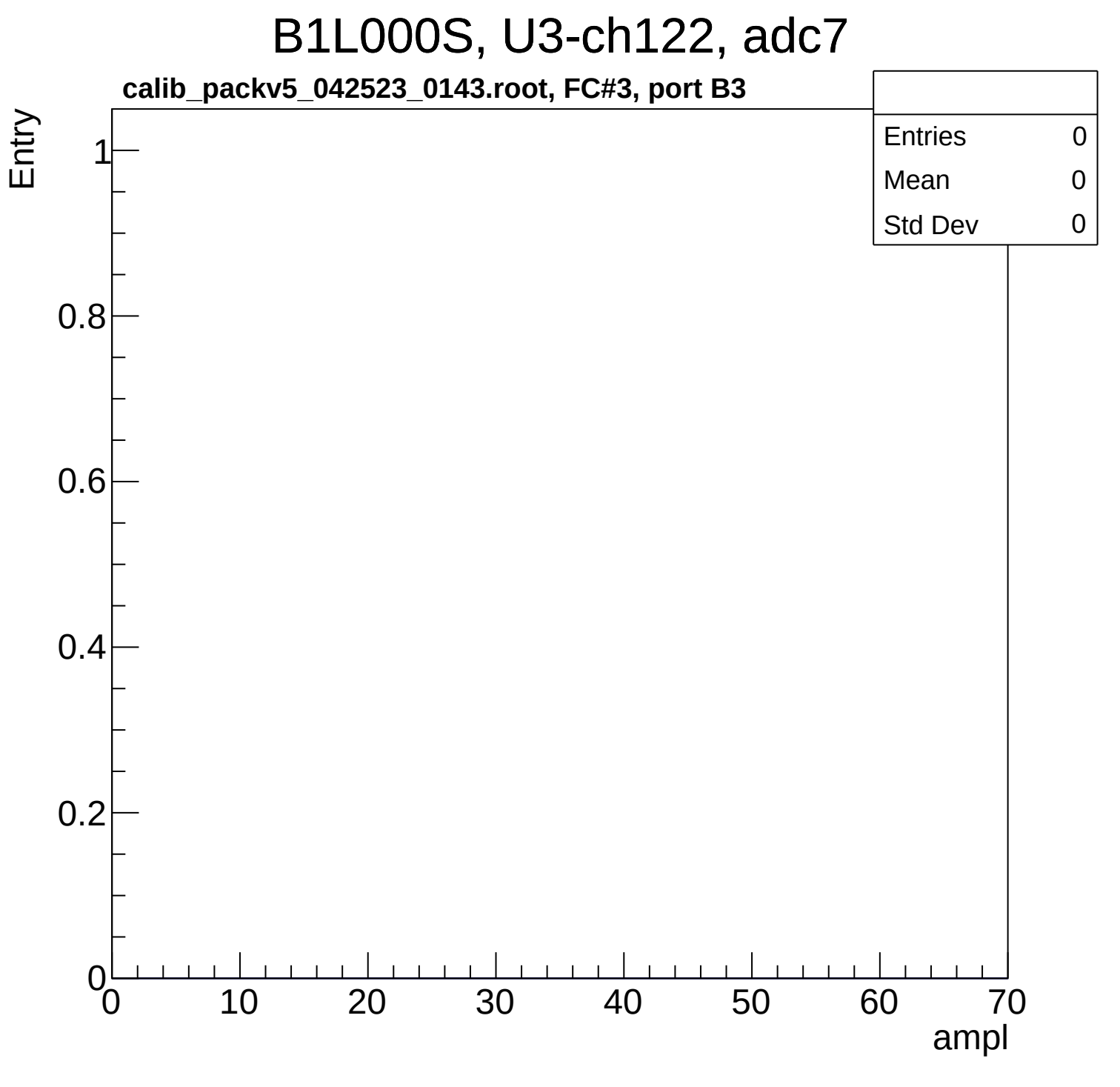
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U3-ch122, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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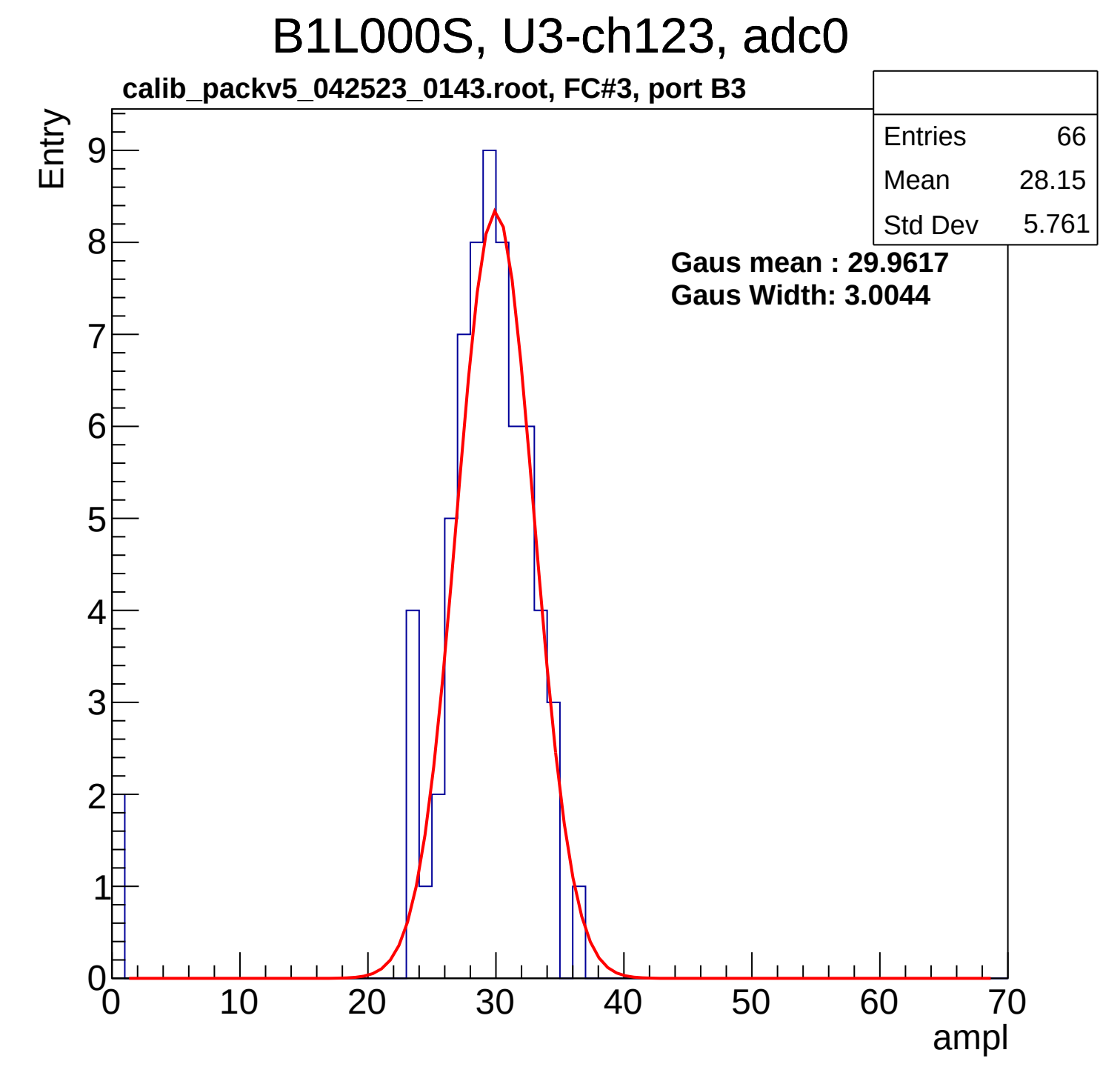
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch123, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.15
Std Dev 5.761
Gaus mean : 29.9617
Gaus Width: 3.0044
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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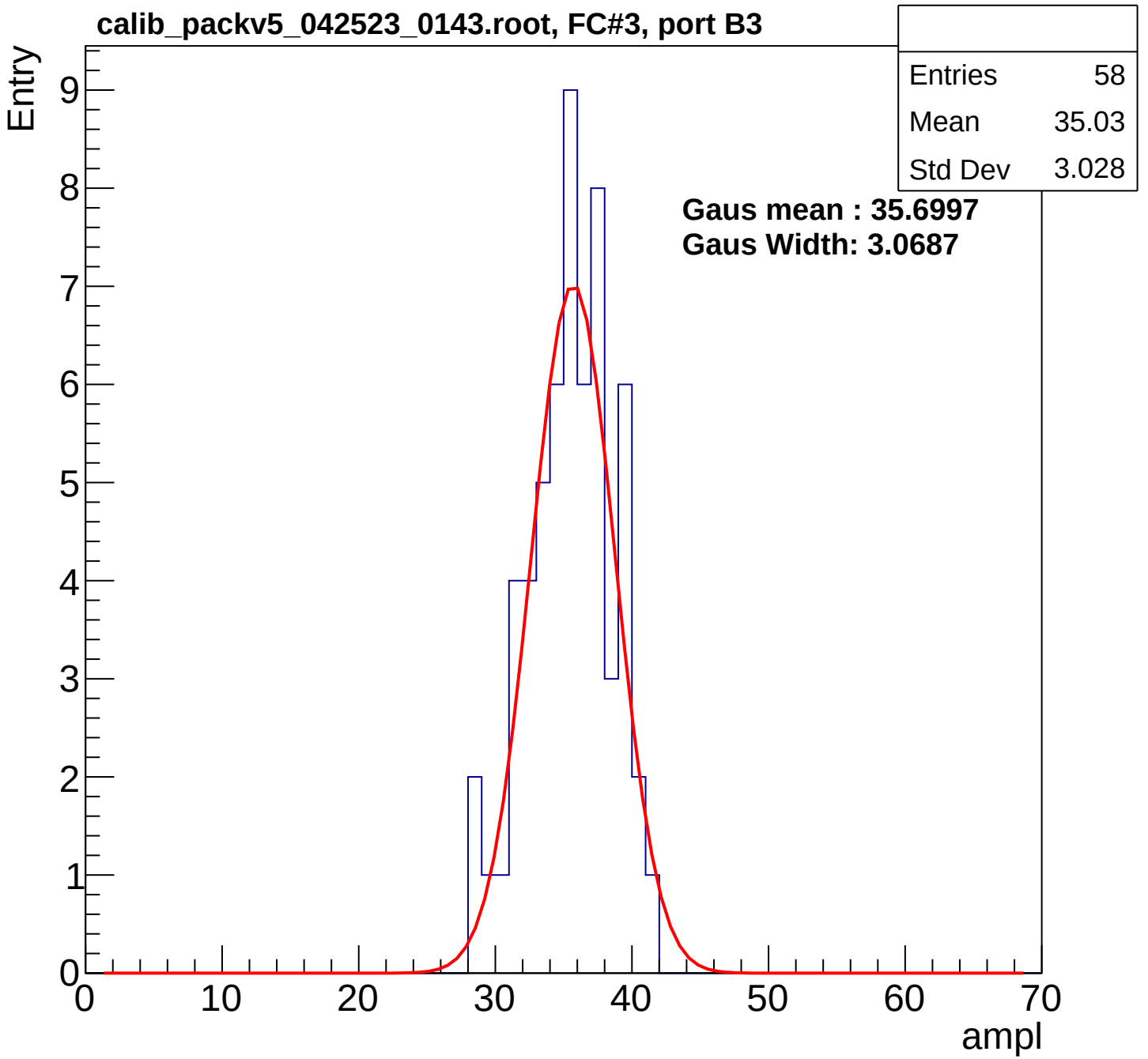
B1L000S, U3-ch123, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 35.03
Std Dev 3.028
Gaus mean : 35.6997
Gaus Width: 3.0687
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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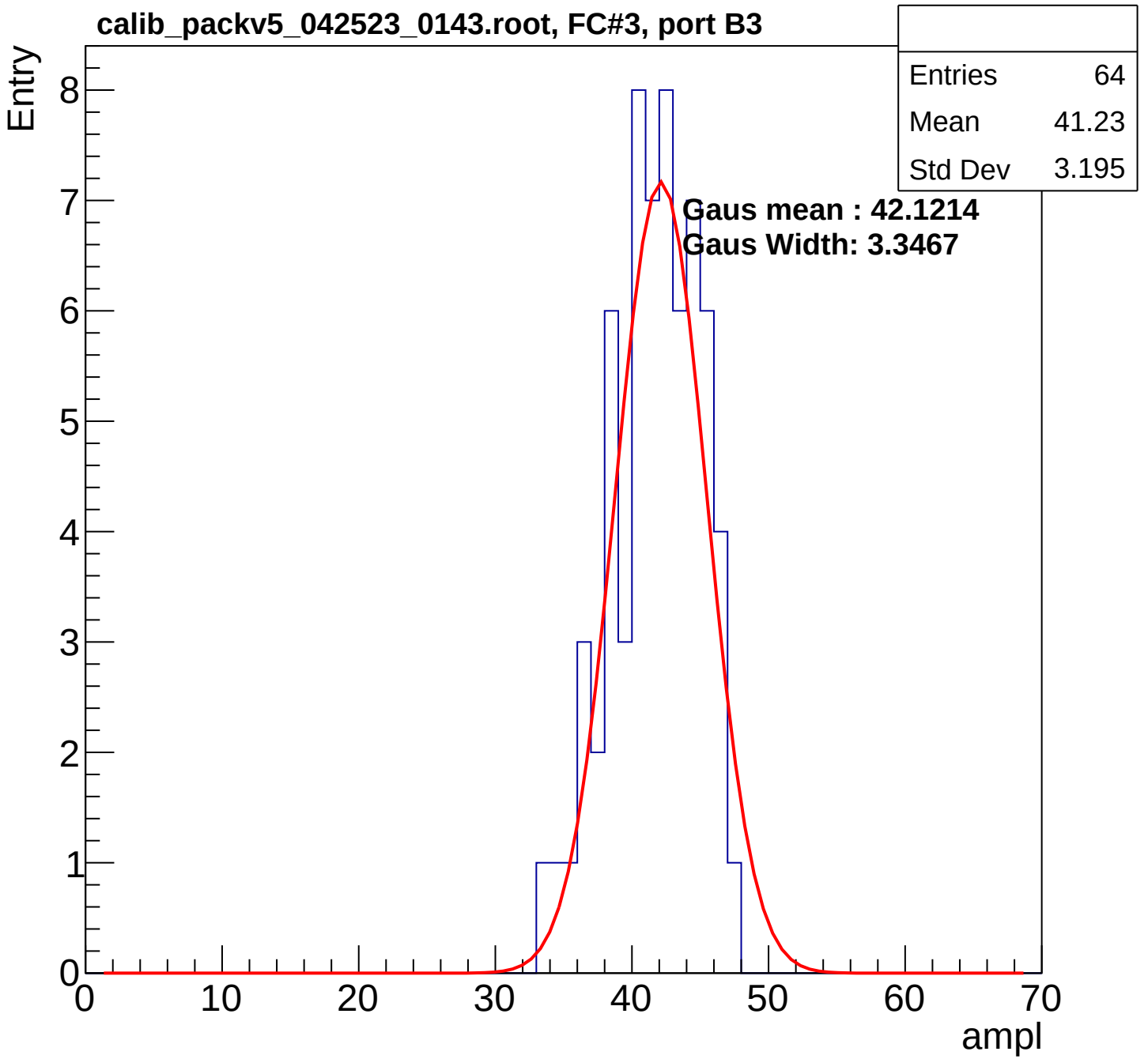
B1L000S, U3-ch123, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 41.23
Std Dev 3.195
Gaus mean : 42.1214
Gaus Width: 3.3467
Entry
ampl
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1
2
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4
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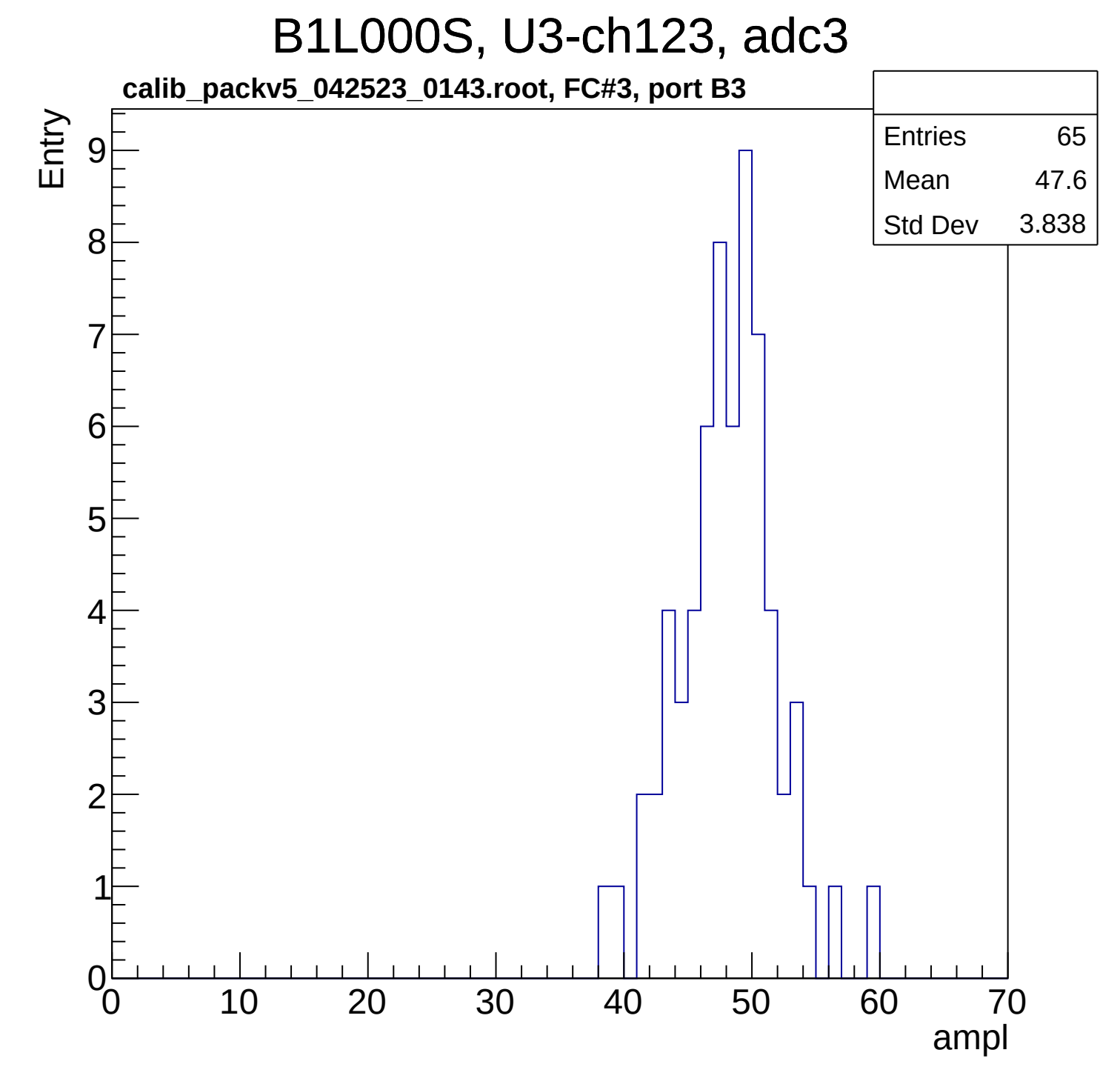
B1L000S, U3-ch123, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 47.6
Std Dev 3.838
Entry
ampl
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3
4
5
6
7
8
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60
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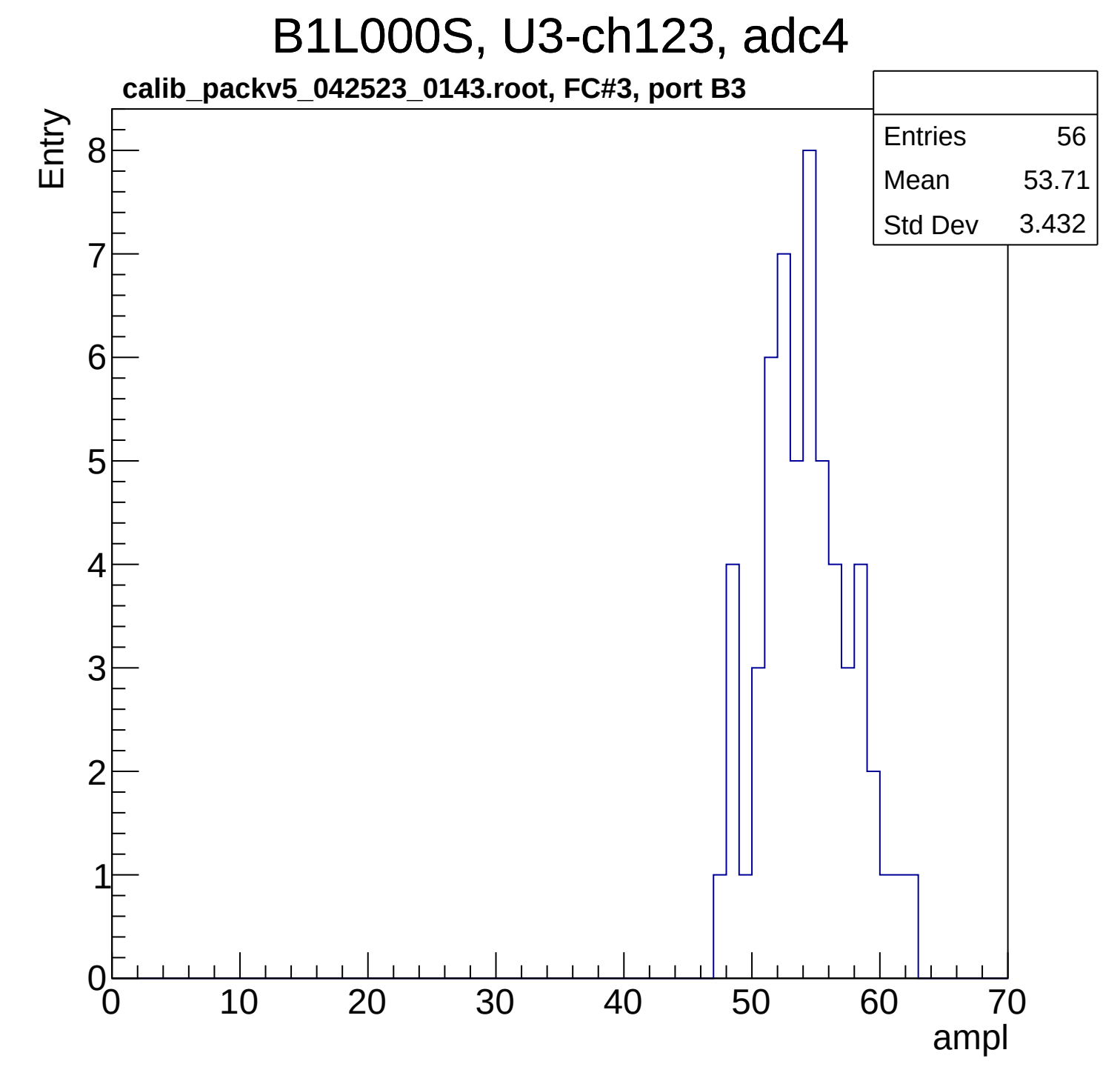
B1L000S, U3-ch123, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 53.71
Std Dev 3.432
Entry
ampl
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1
2
3
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0
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60
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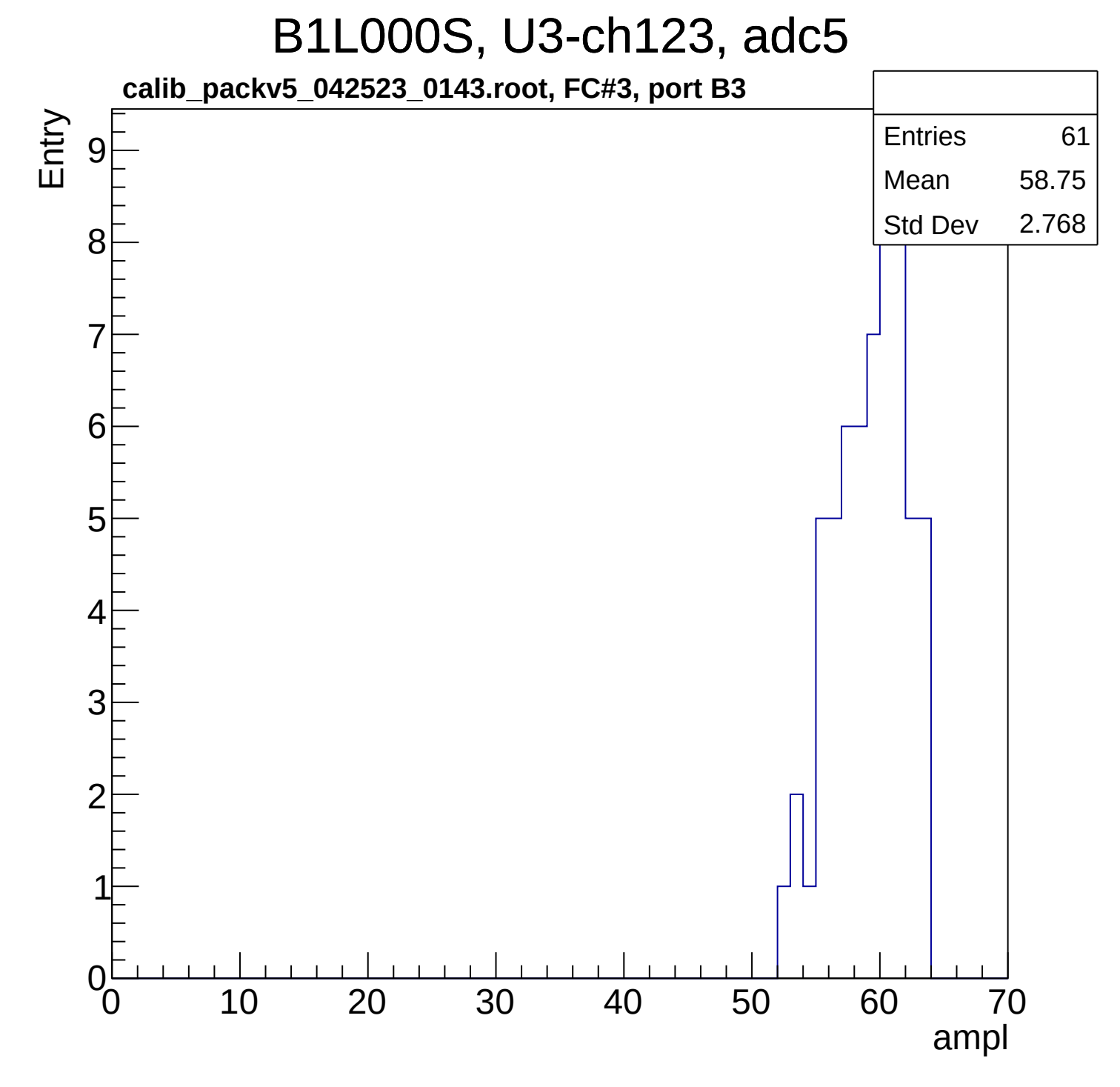
B1L000S, U3-ch123, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.75
Std Dev 2.768
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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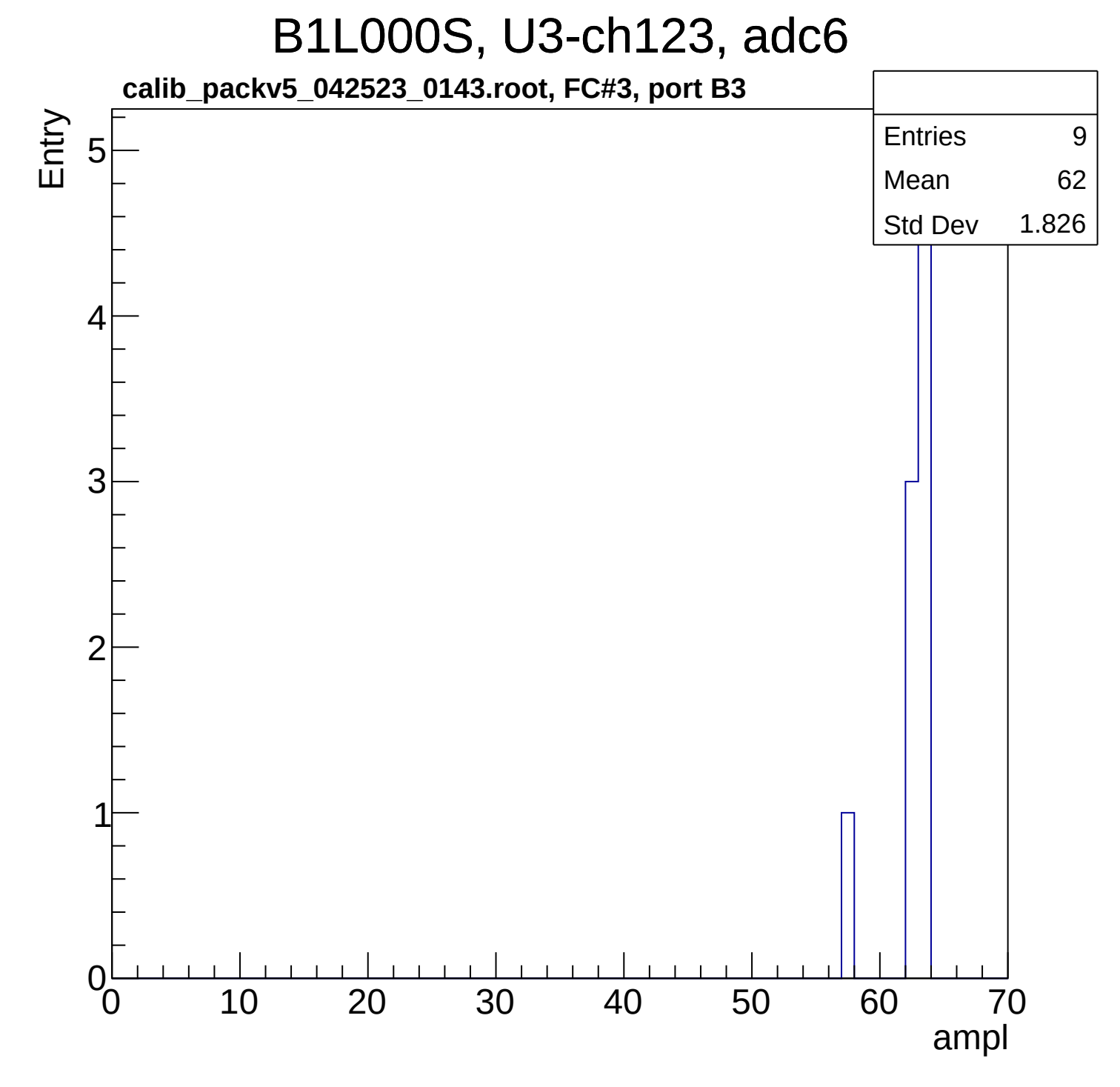
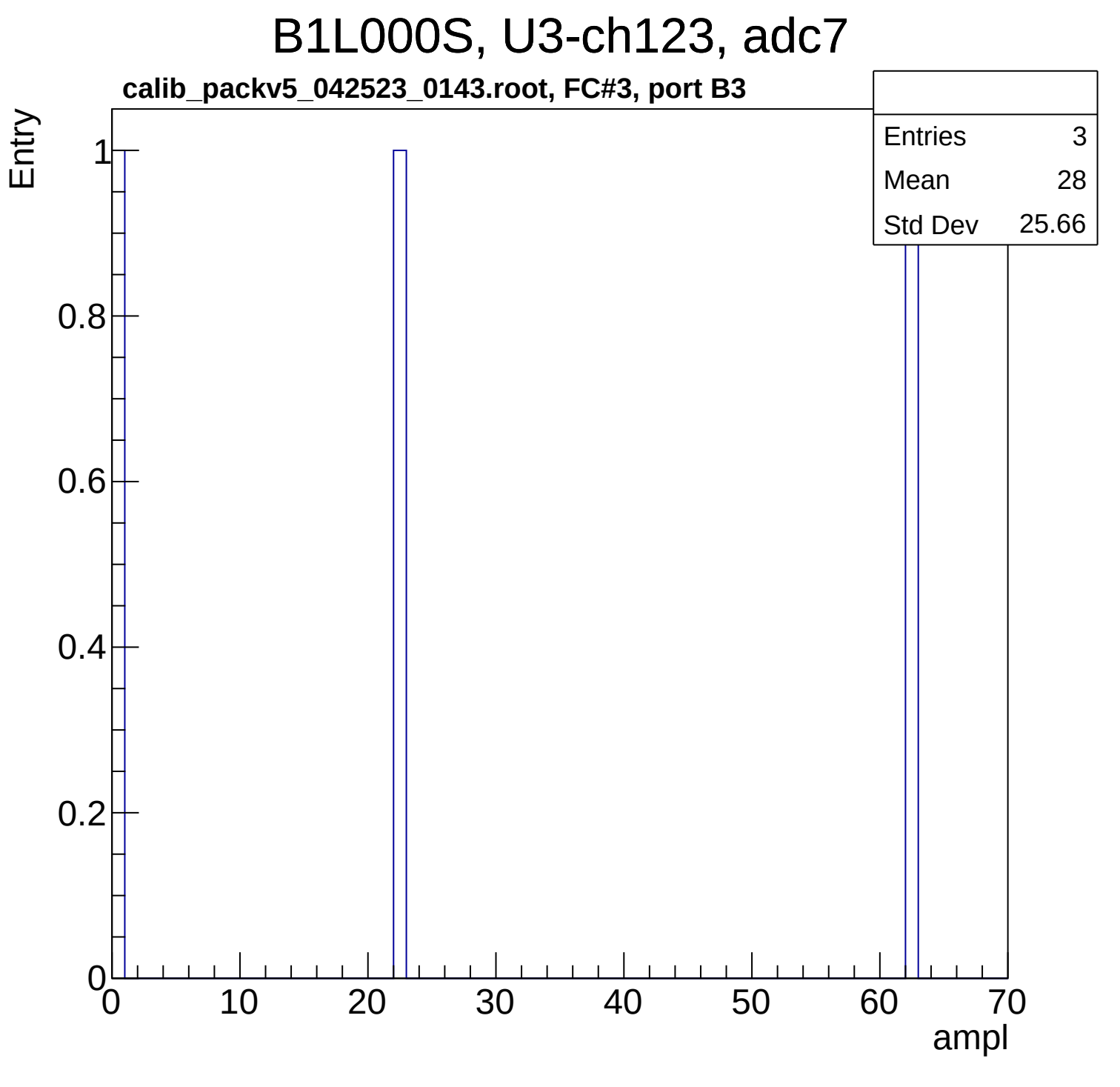

B1L000S, U3-ch123, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 62
Std Dev 1.826
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch123, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 28
Std Dev 25.66
Entry
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0.6
0.4
0.2
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10
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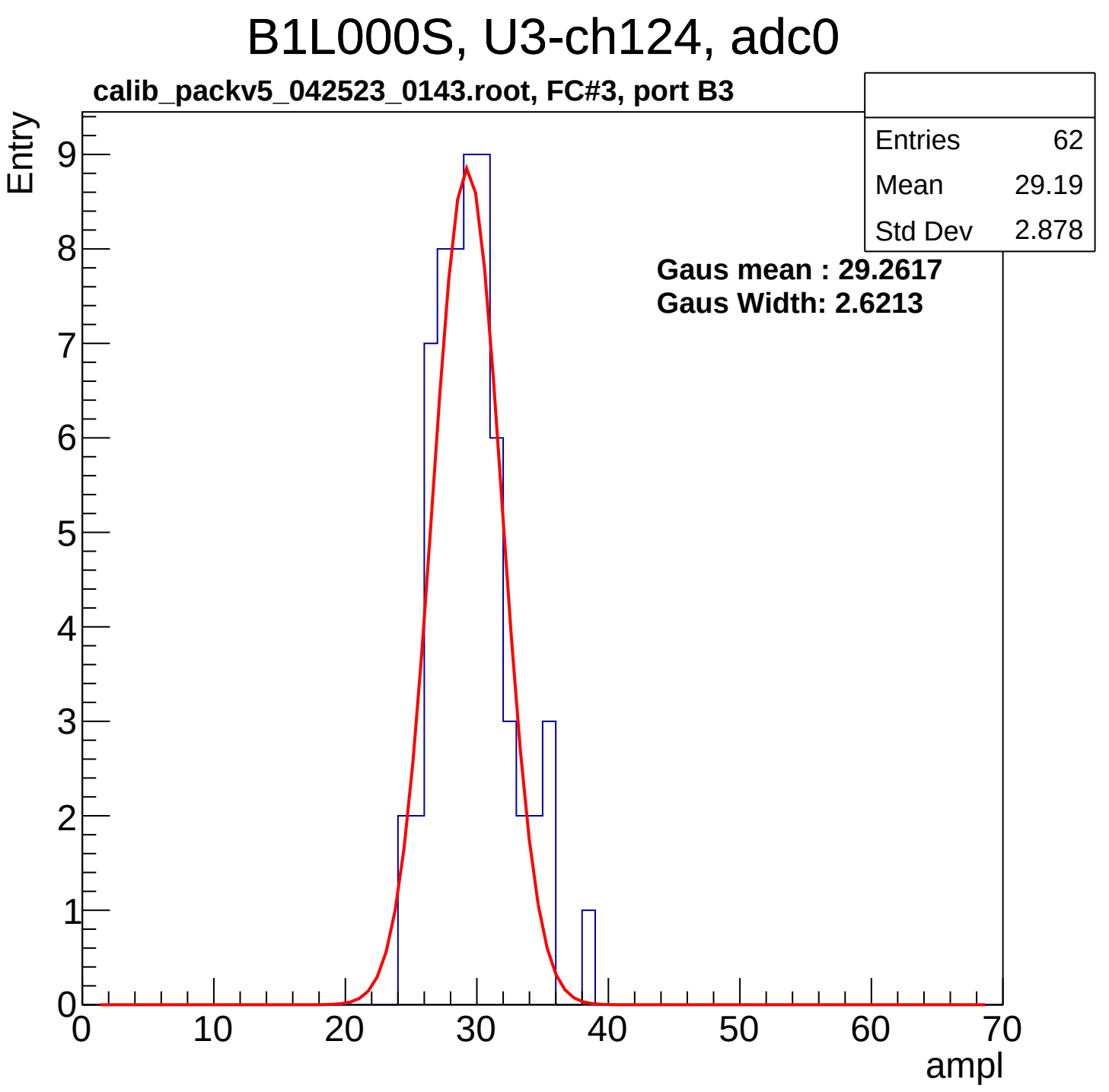
50
60
70
ampl


B1L000S, U3-ch124, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 29.19
Std Dev 2.878
Gaus mean : 29.2617
Gaus Width: 2.6213
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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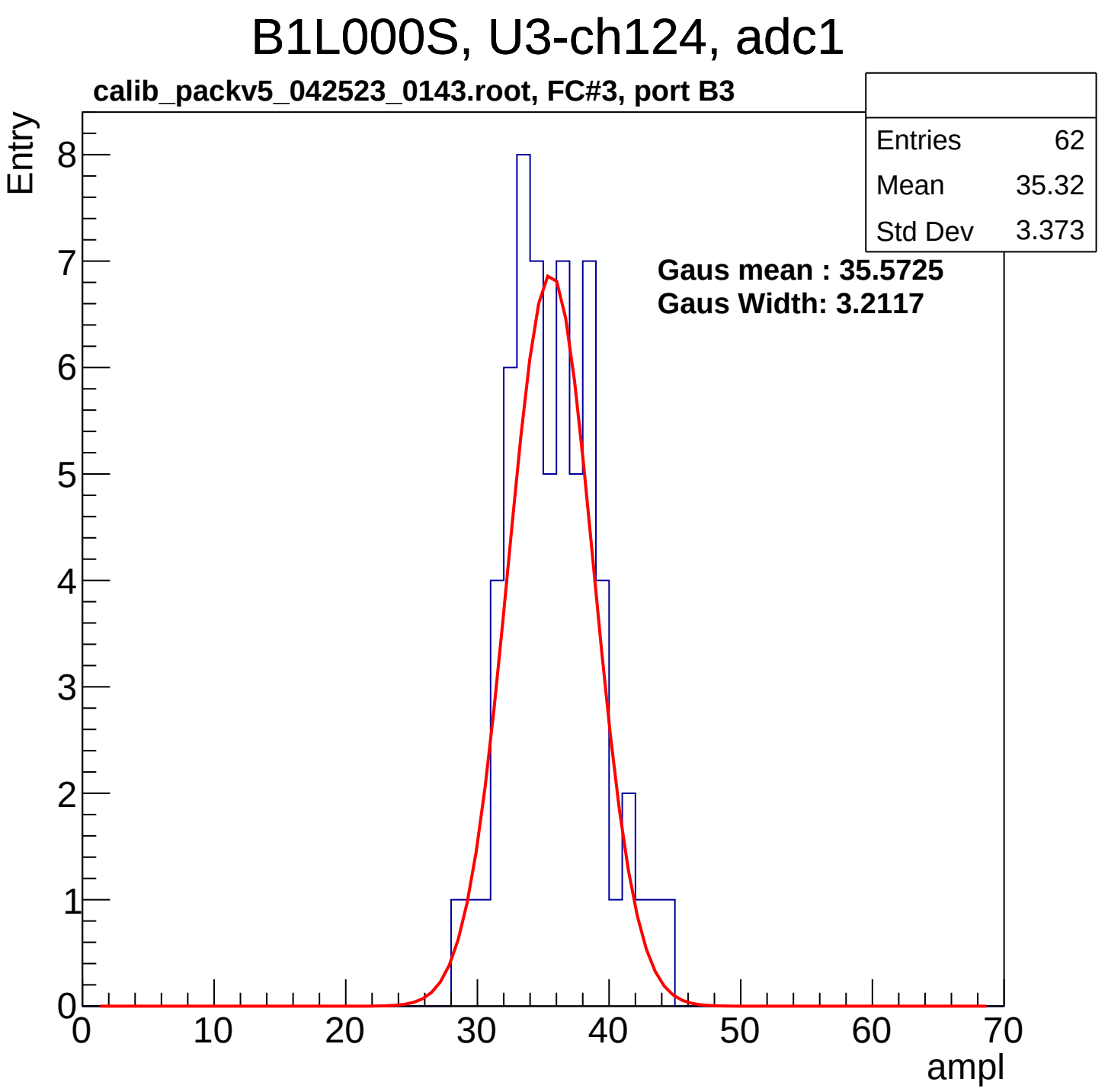
B1L000S, U3-ch124, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 35.32
Std Dev 3.373
Gaus mean : 35.5725
Gaus Width: 3.2117
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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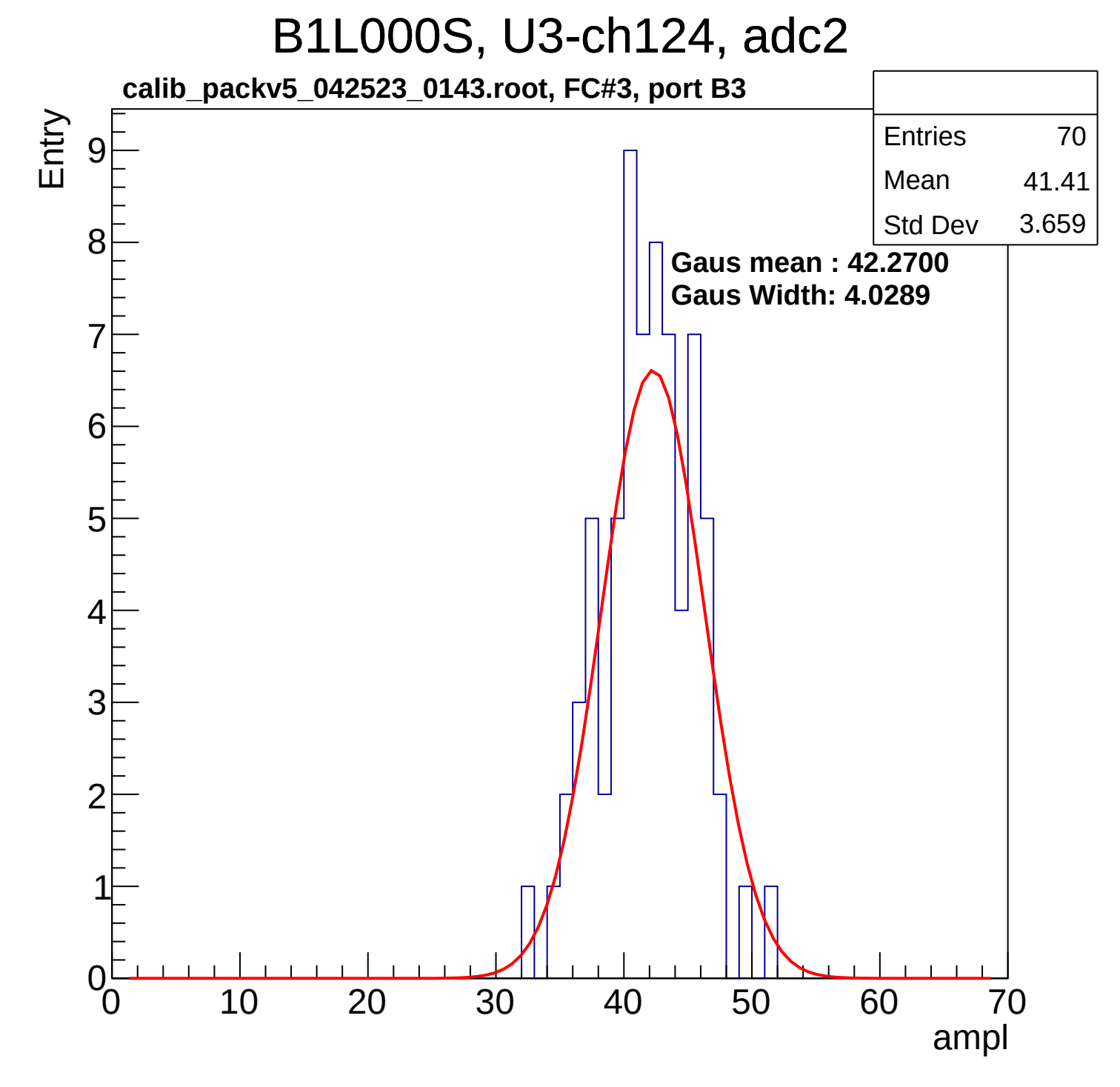
B1L000S, U3-ch124, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 41.41
Std Dev 3.659
Gaus mean : 42.2700
Gaus Width: 4.0289
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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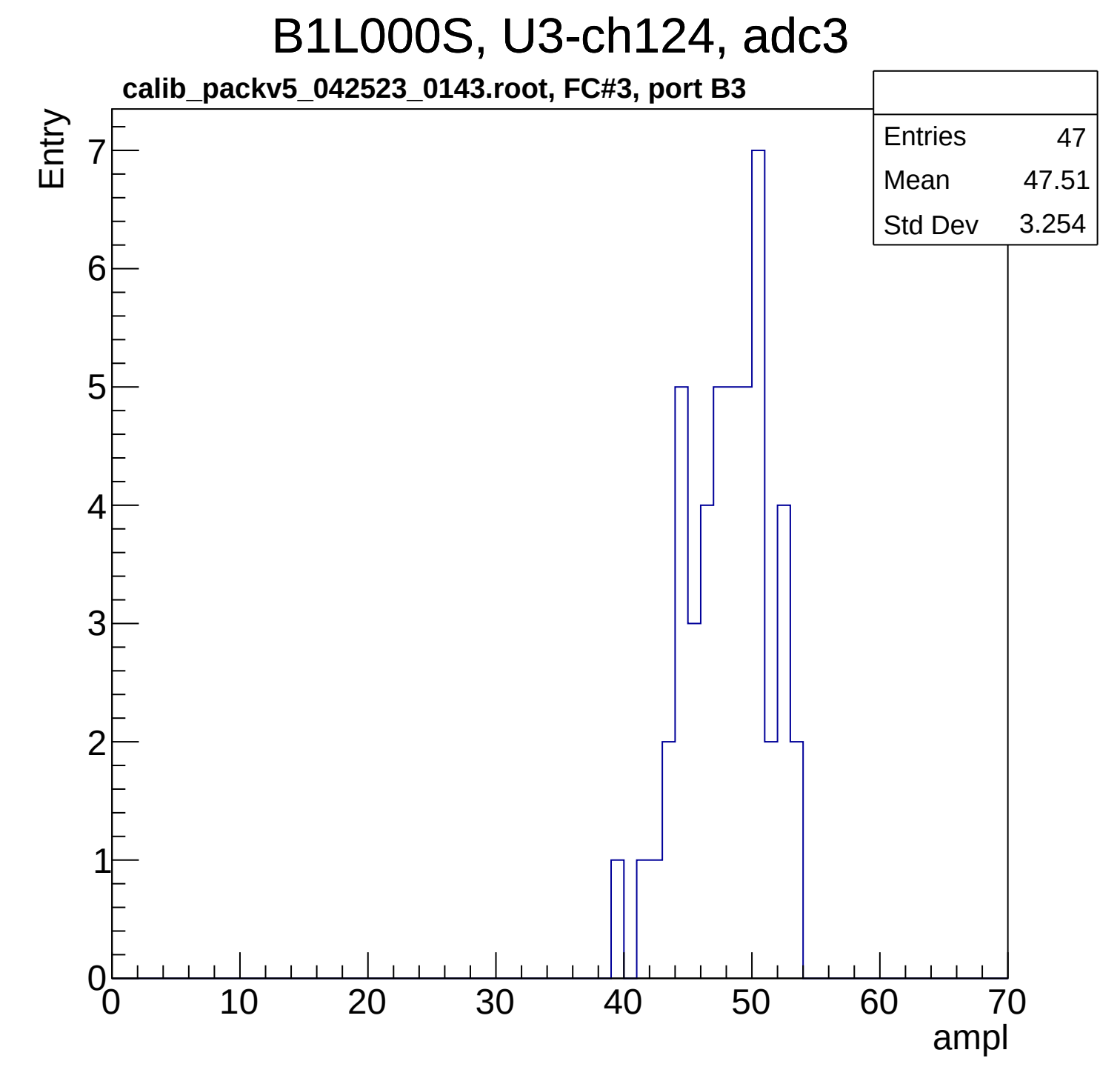
B1L000S, U3-ch124, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 47.51
Std Dev 3.254
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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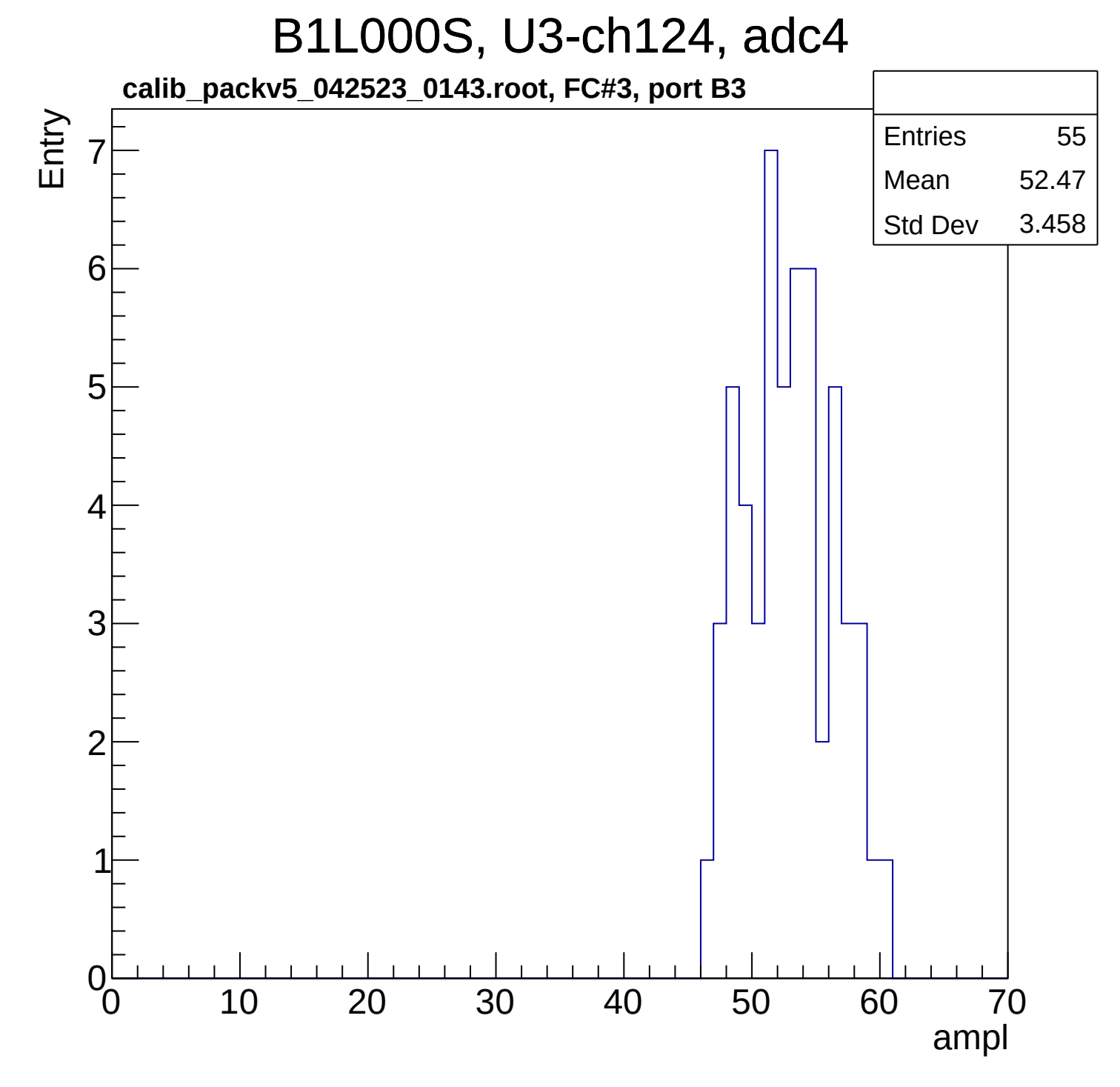
B1L000S, U3-ch124, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 52.47
Std Dev 3.458
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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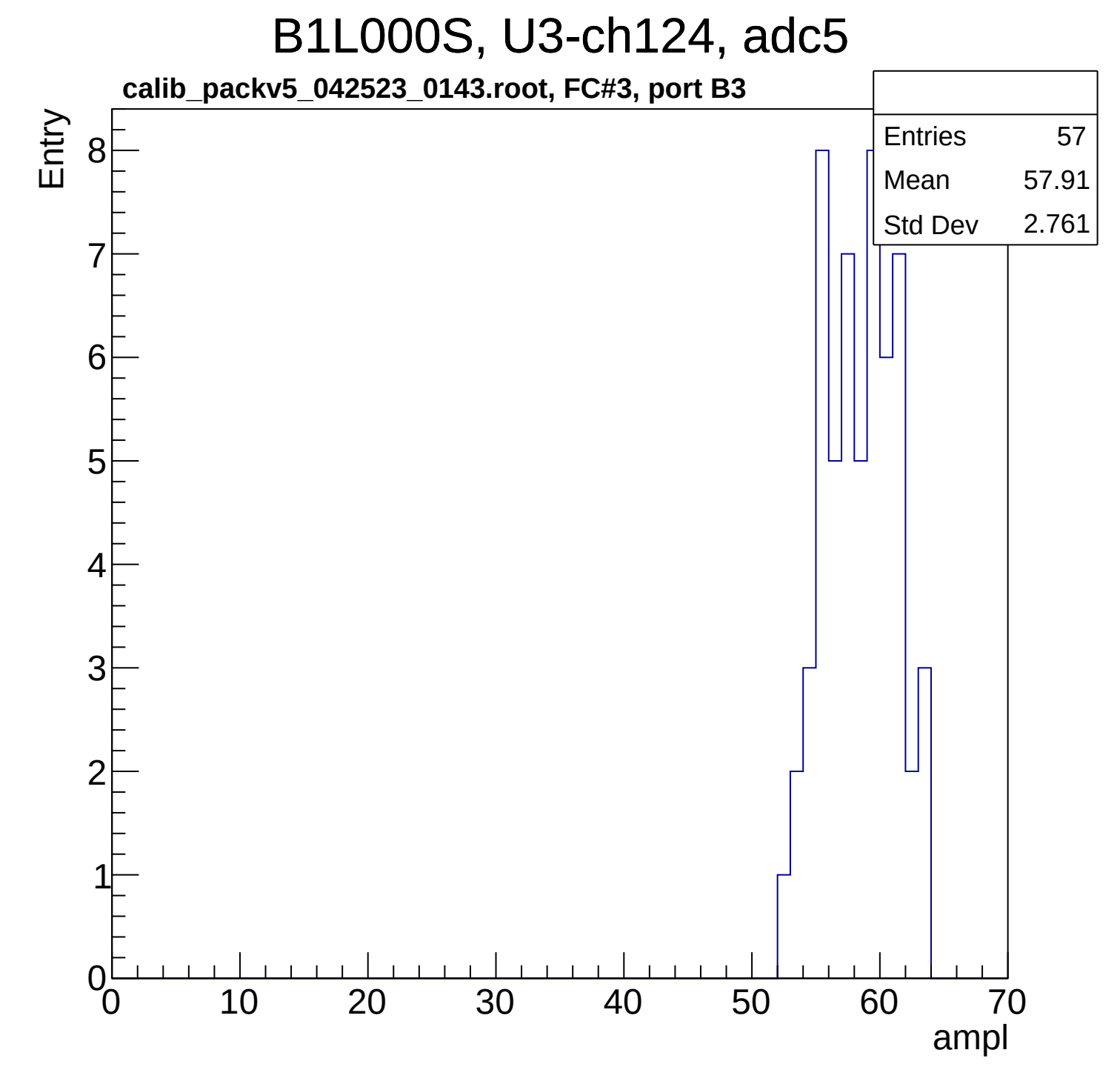
B1L000S, U3-ch124, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 57.91
Std Dev 2.761
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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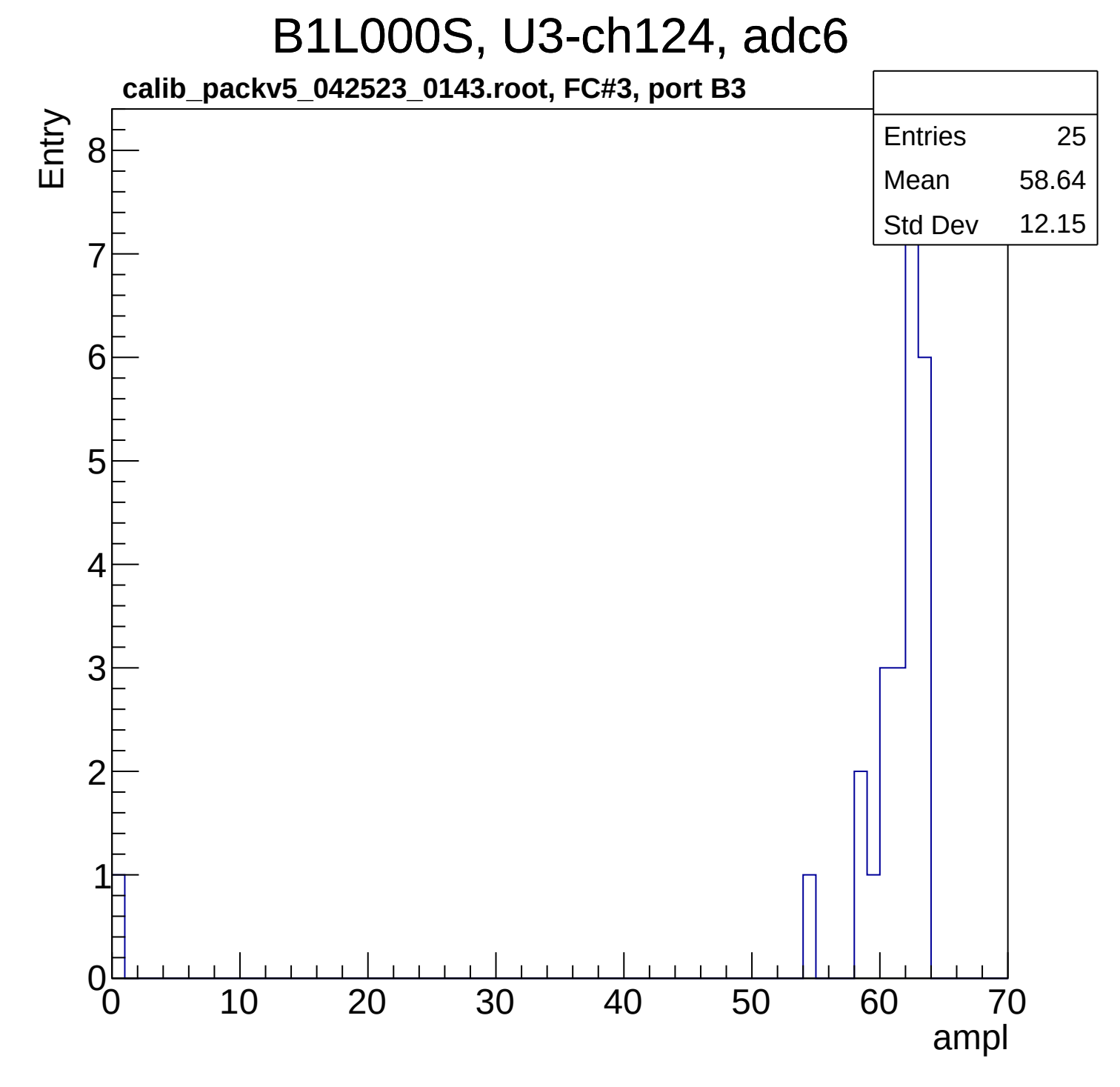
B1L000S, U3-ch124, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 58.64
Std Dev 12.15
Entry
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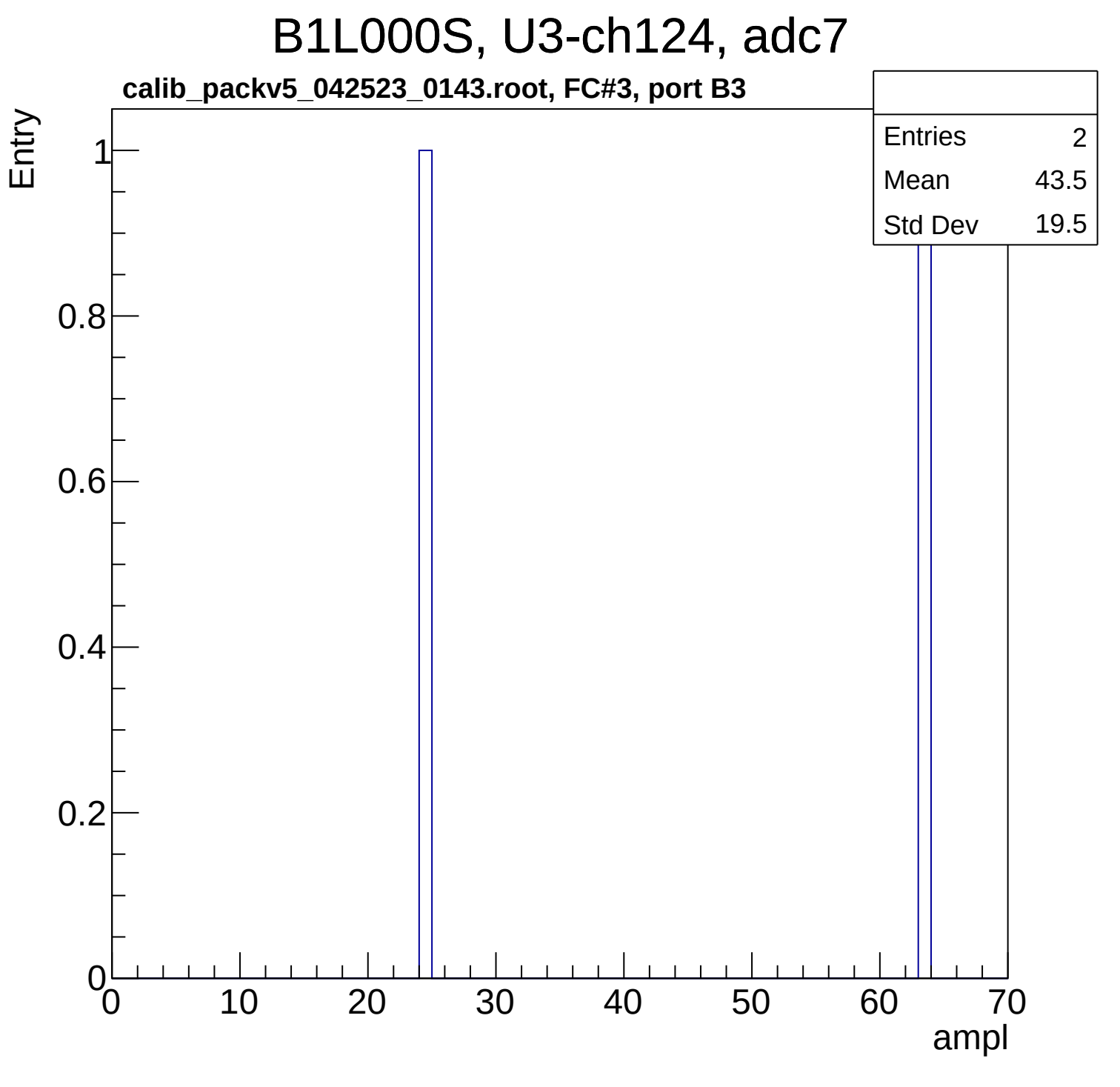
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch124, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 43.5
Std Dev 19.5
Entry
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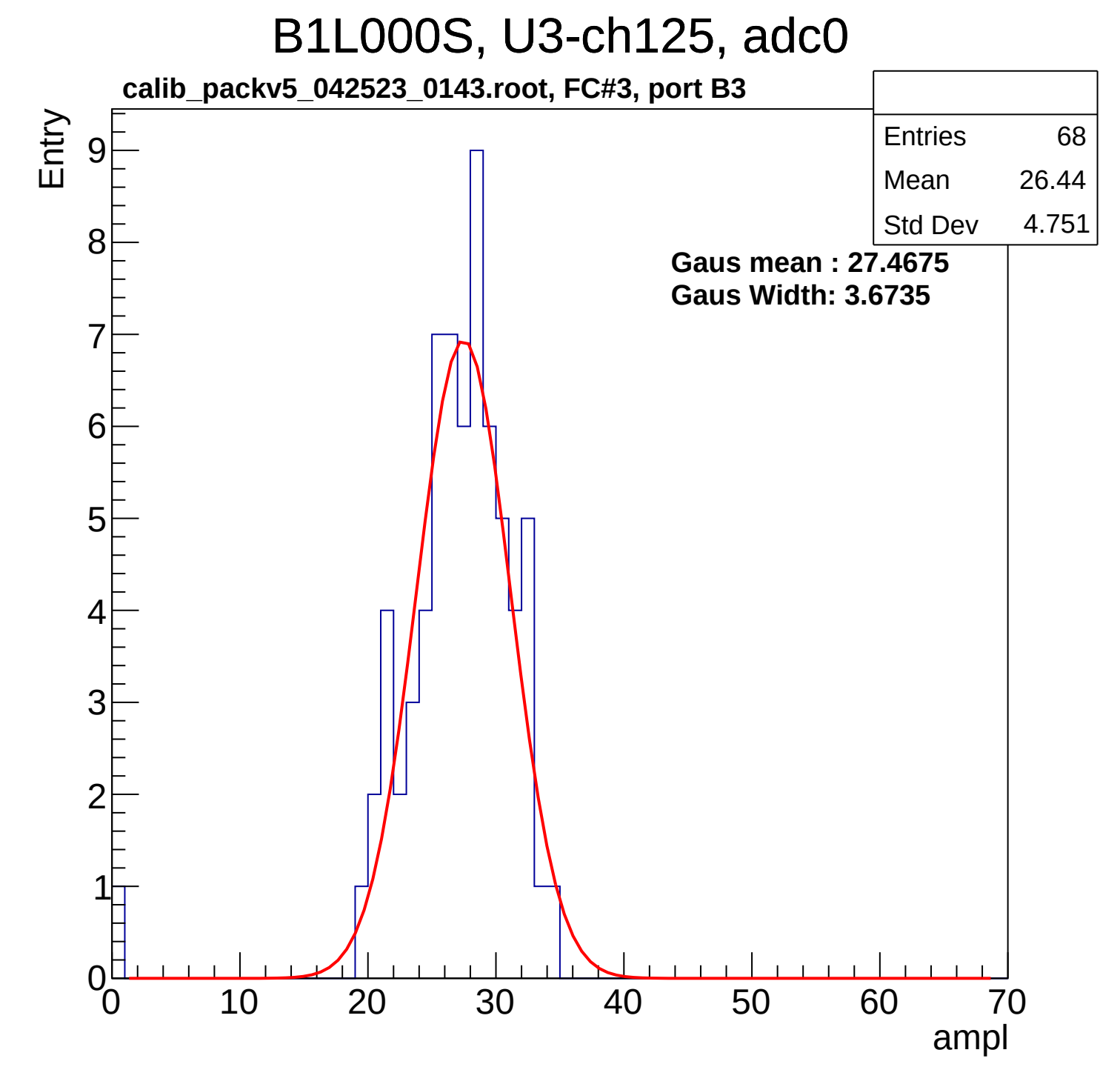
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch125, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 26.44
Std Dev 4.751
Gaus mean : 27.4675
Gaus Width: 3.6735
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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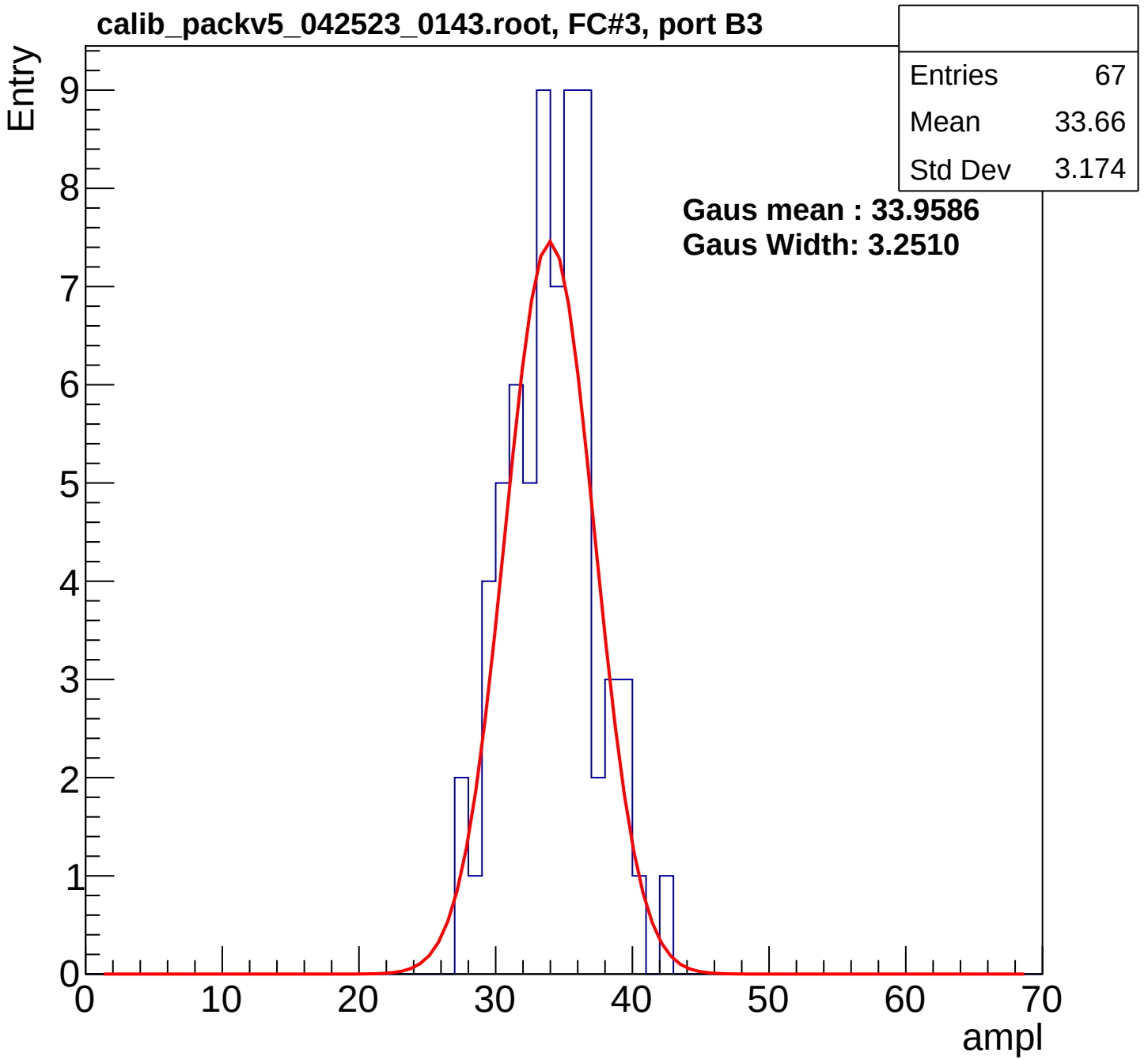
B1L000S, U3-ch125, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 33.66
Std Dev 3.174
Gaus mean : 33.9586
Gaus Width: 3.2510
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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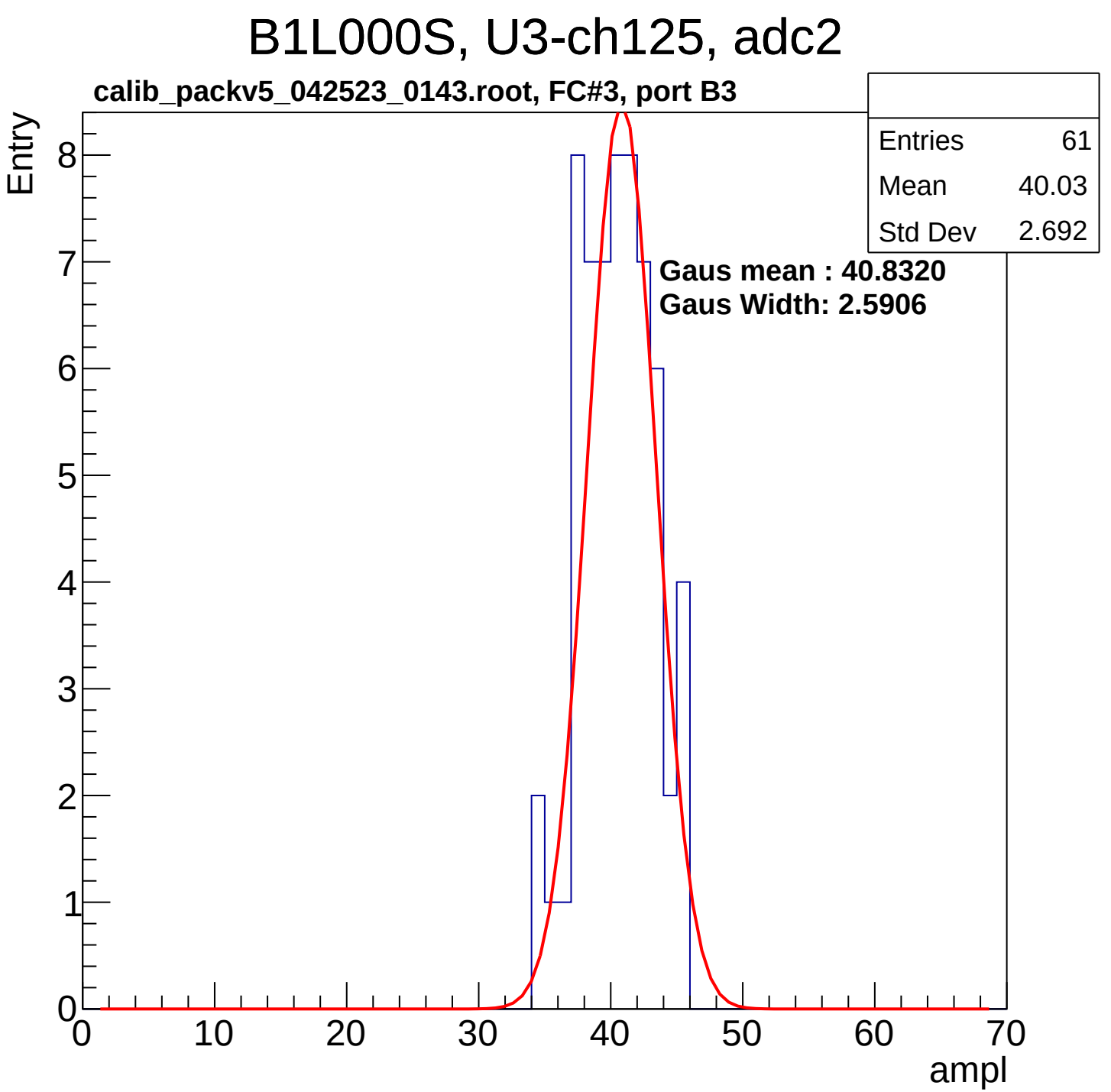
B1L000S, U3-ch125, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 40.03
Std Dev 2.692
Gaus mean : 40.8320
Gaus Width: 2.5906
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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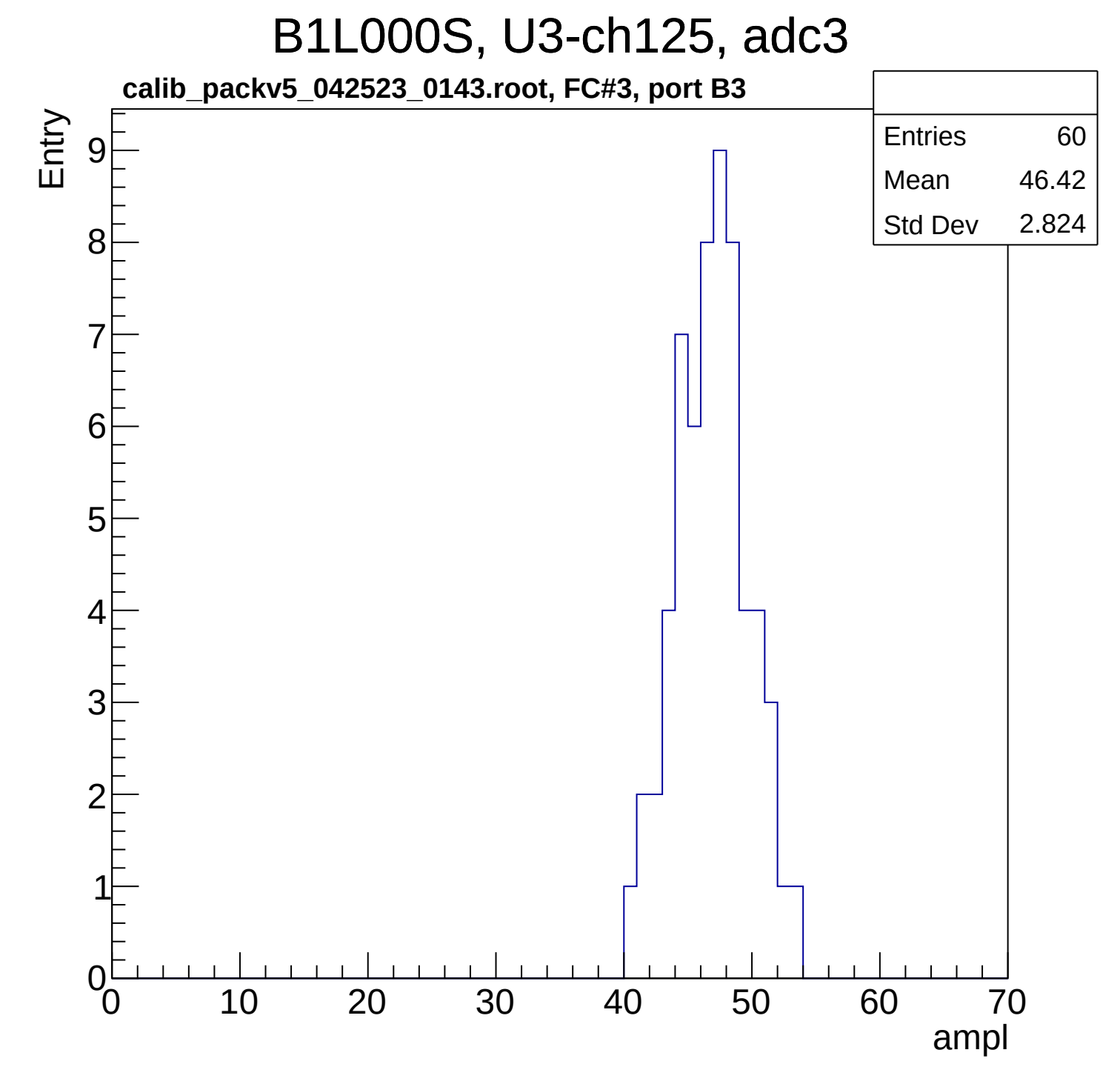
B1L000S, U3-ch125, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 46.42
Std Dev 2.824
Entry
ampl
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4
5
6
7
8
9
0
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20
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60
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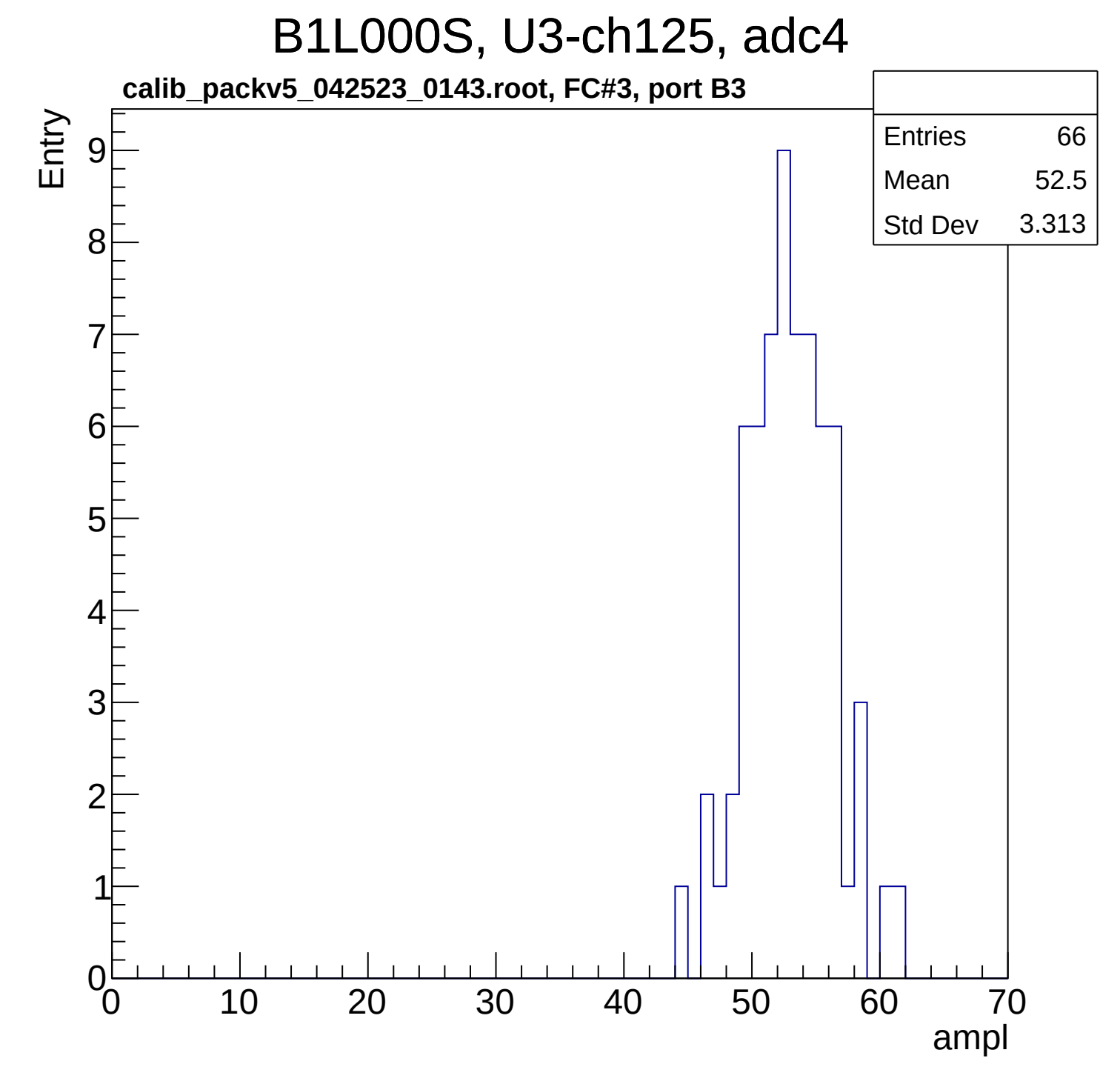
B1L000S, U3-ch125, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 52.5
Std Dev 3.313
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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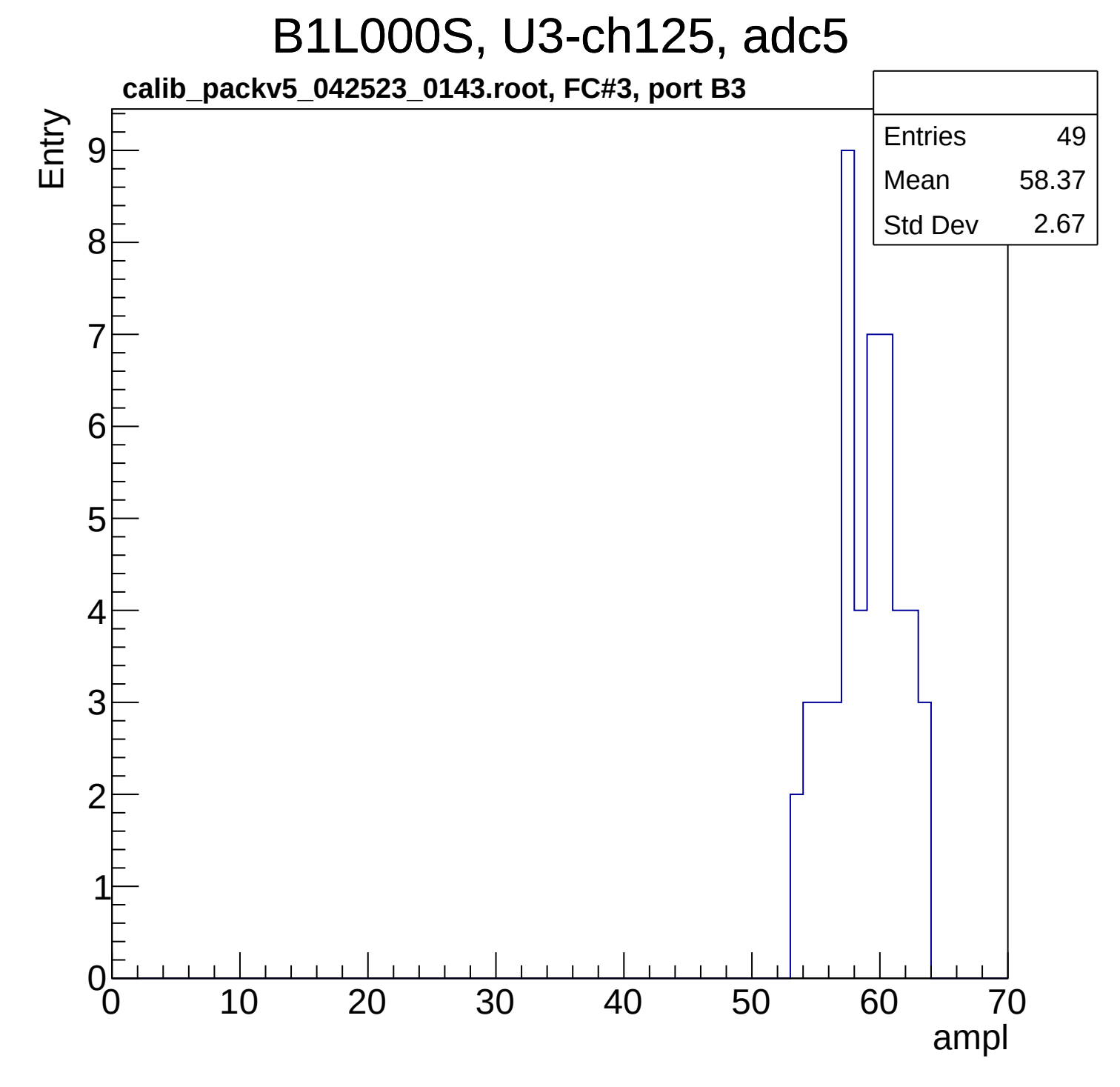
B1L000S, U3-ch125, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 58.37
Std Dev 2.67
Entry
ampl
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3
4
5
6
7
8
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60
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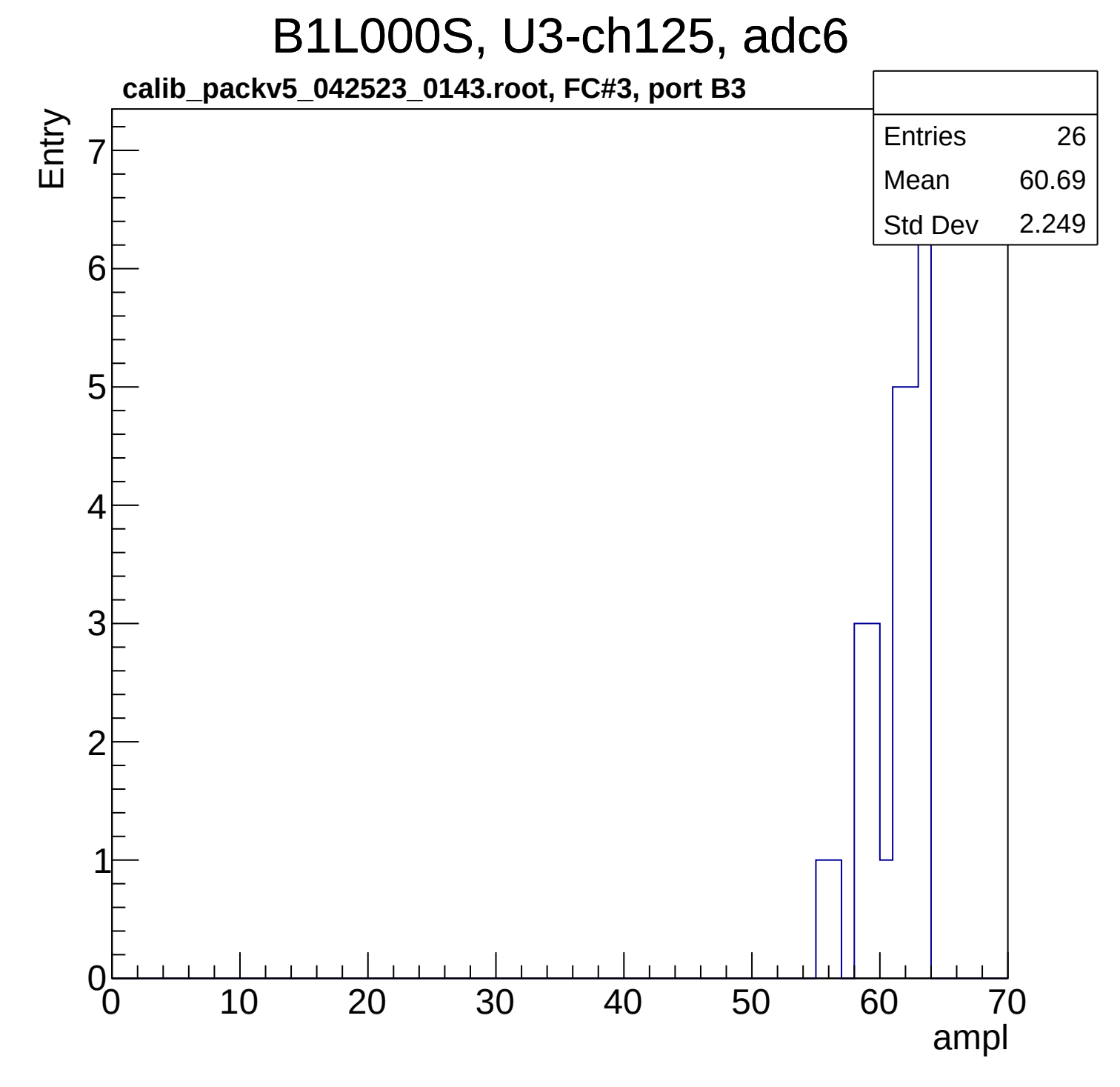
B1L000S, U3-ch125, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 60.69
Std Dev 2.249
Entry
ampl
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0
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60
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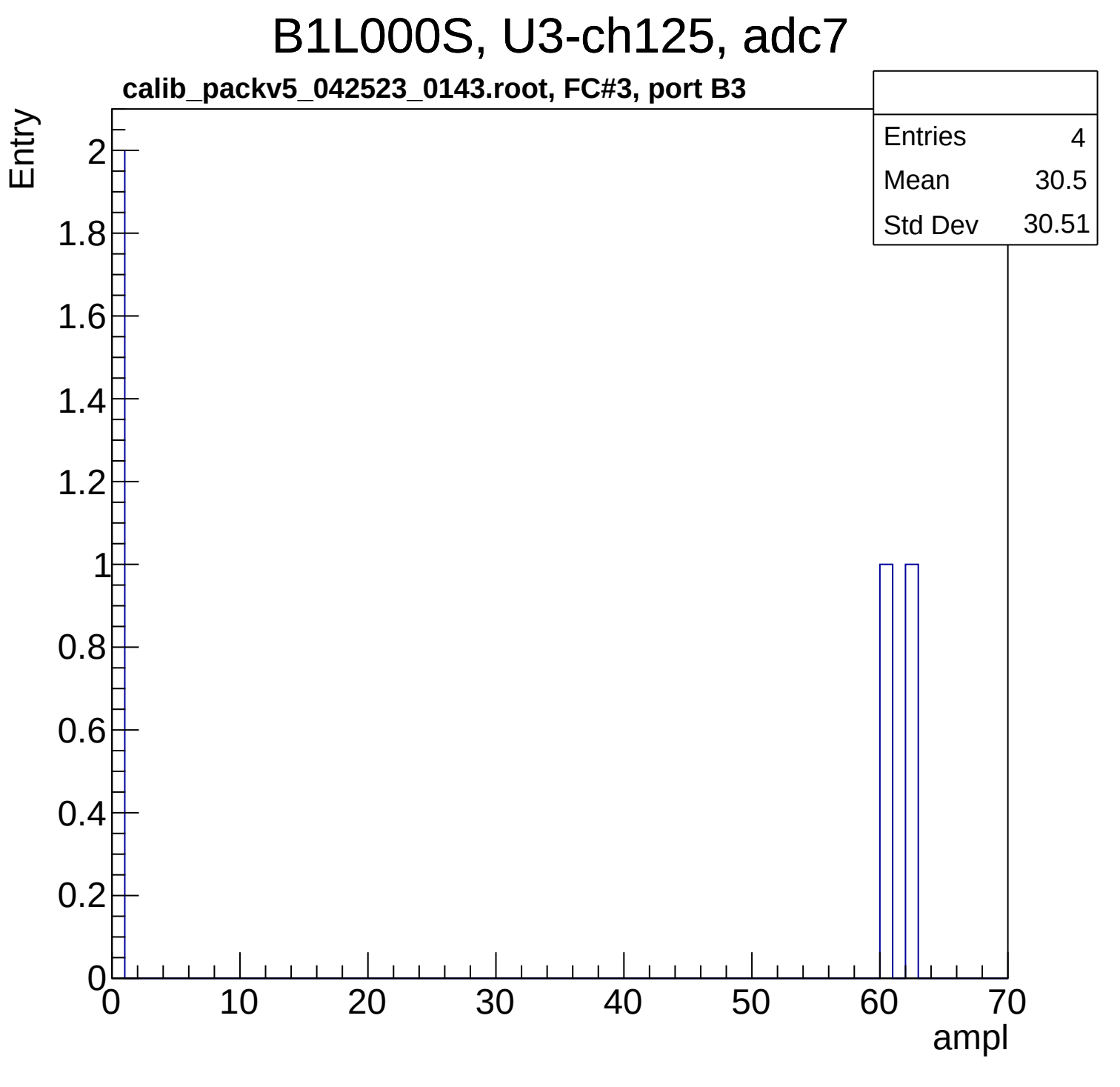
B1L000S, U3-ch125, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 30.5
Std Dev 30.51
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
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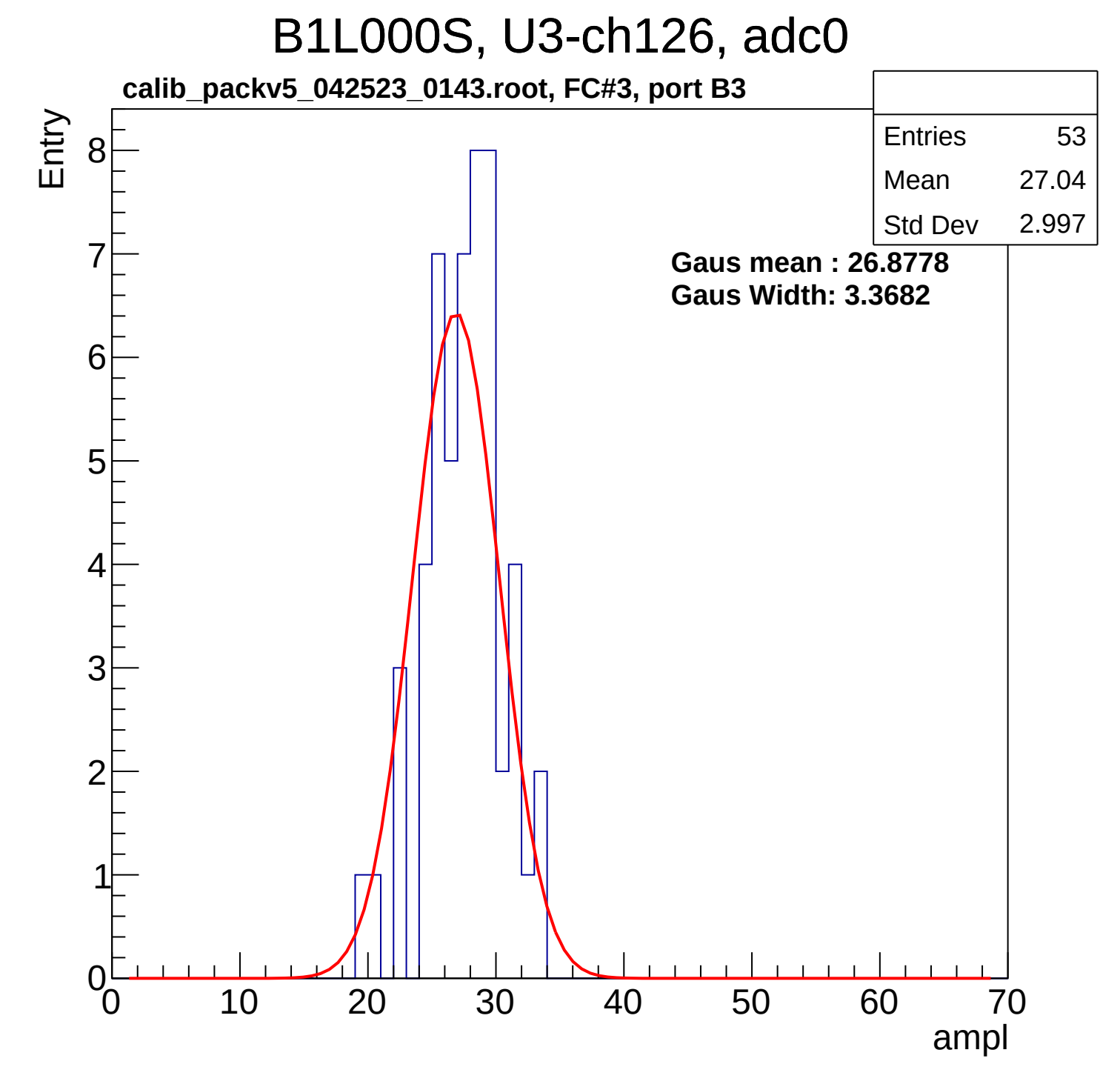
B1L000S, U3-ch126, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 27.04
Std Dev 2.997
Gaus mean : 26.8778
Gaus Width: 3.3682
Entry
ampl
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2
3
4
5
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7
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0
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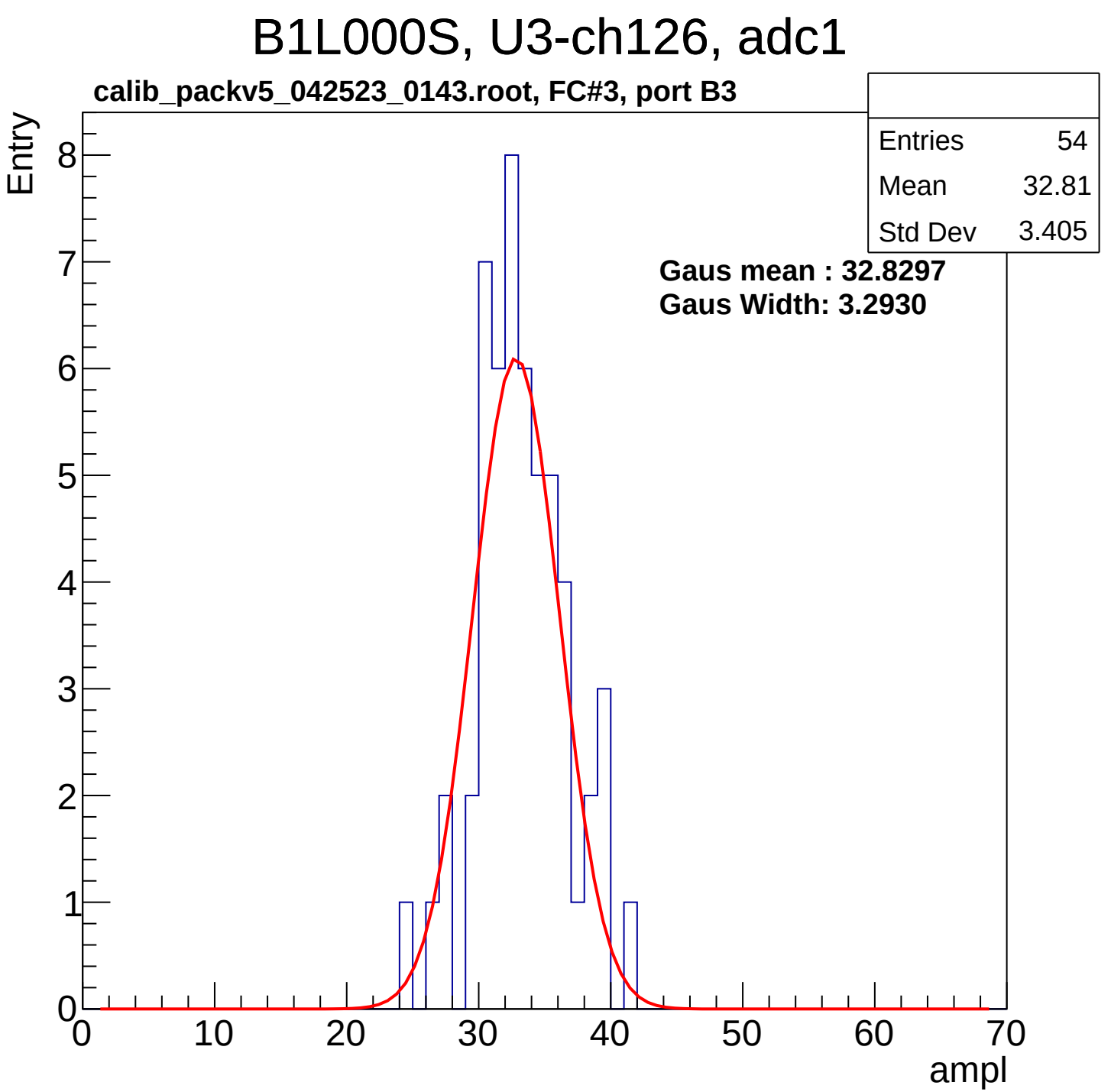
B1L000S, U3-ch126, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 32.81
Std Dev 3.405
Gaus mean : 32.8297
Gaus Width: 3.2930
Entry
ampl
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3
4
5
6
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0
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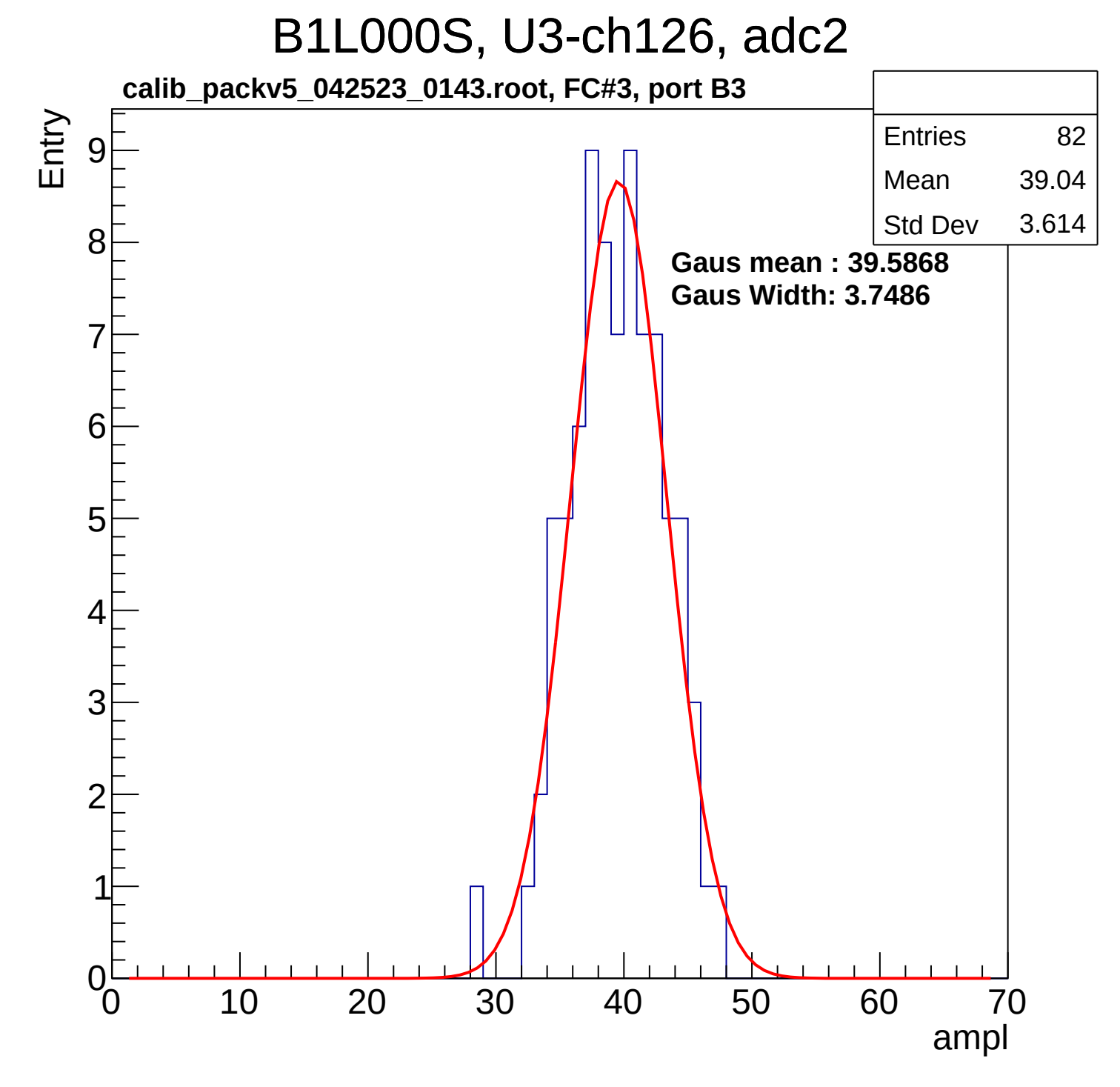
B1L000S, U3-ch126, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 39.04
Std Dev 3.614
Gaus mean : 39.5868
Gaus Width: 3.7486
Entry
ampl
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4
5
6
7
8
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0
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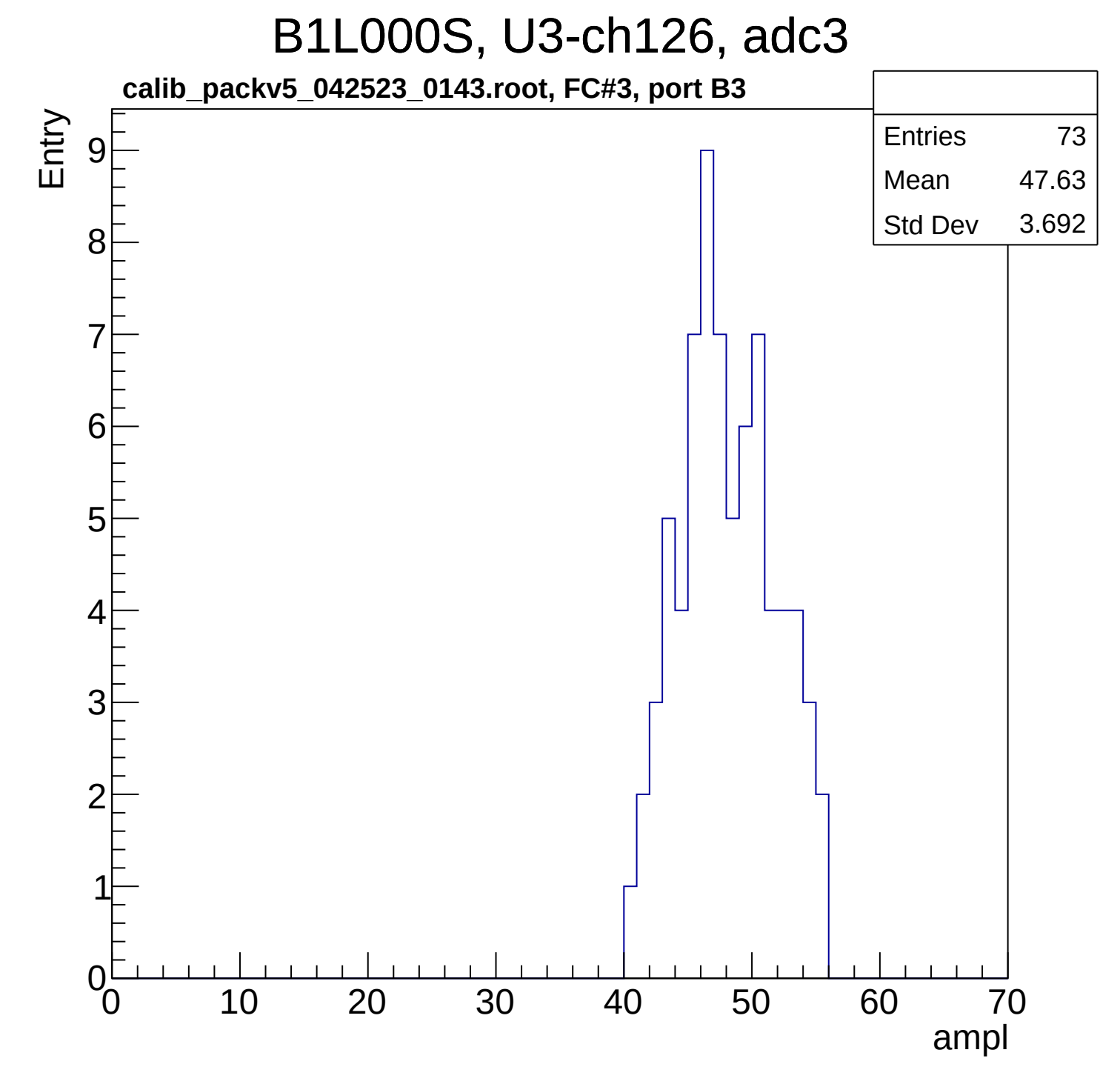
B1L000S, U3-ch126, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 47.63
Std Dev 3.692
Entry
ampl
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0
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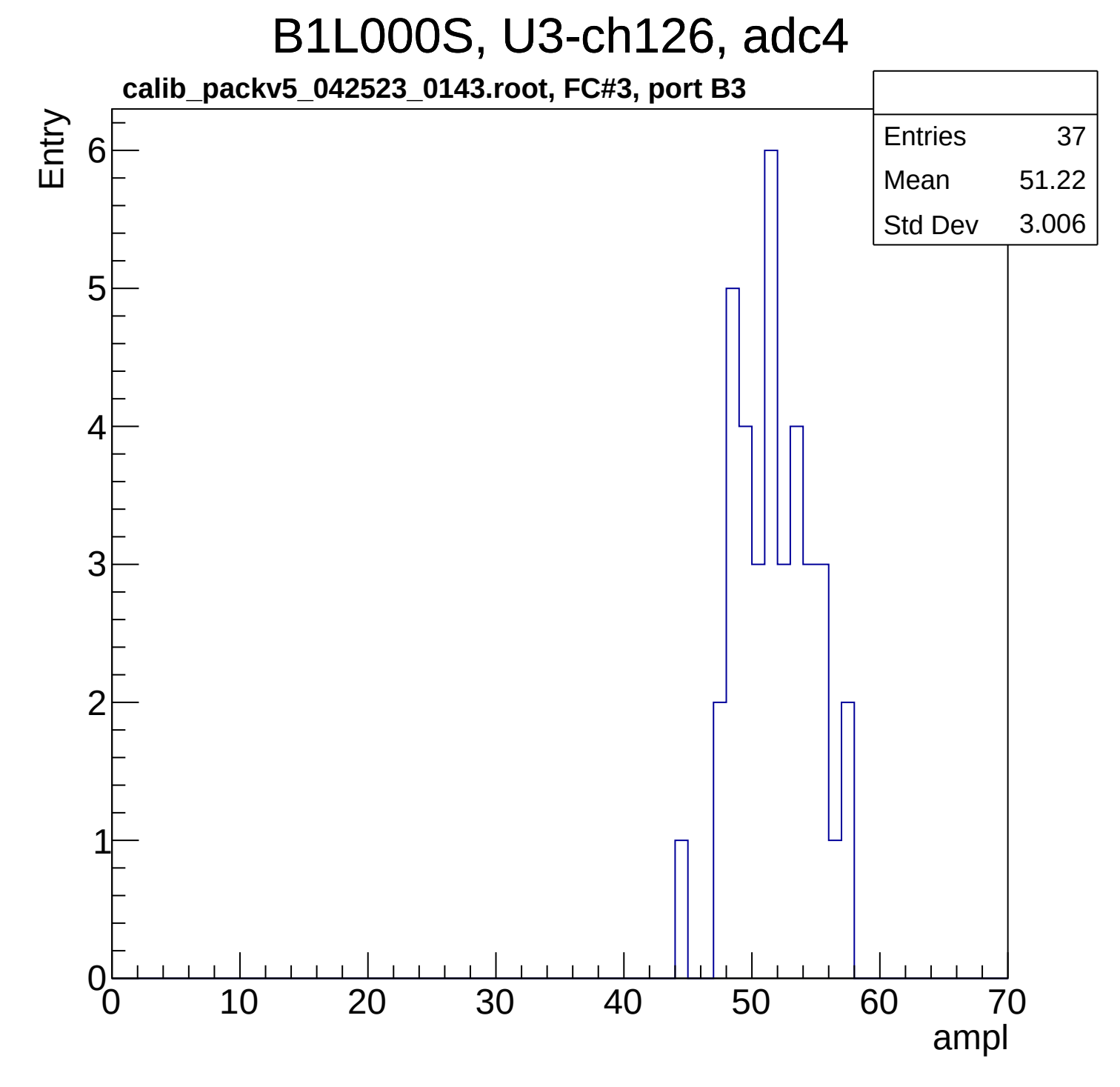
B1L000S, U3-ch126, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 51.22
Std Dev 3.006
Entry
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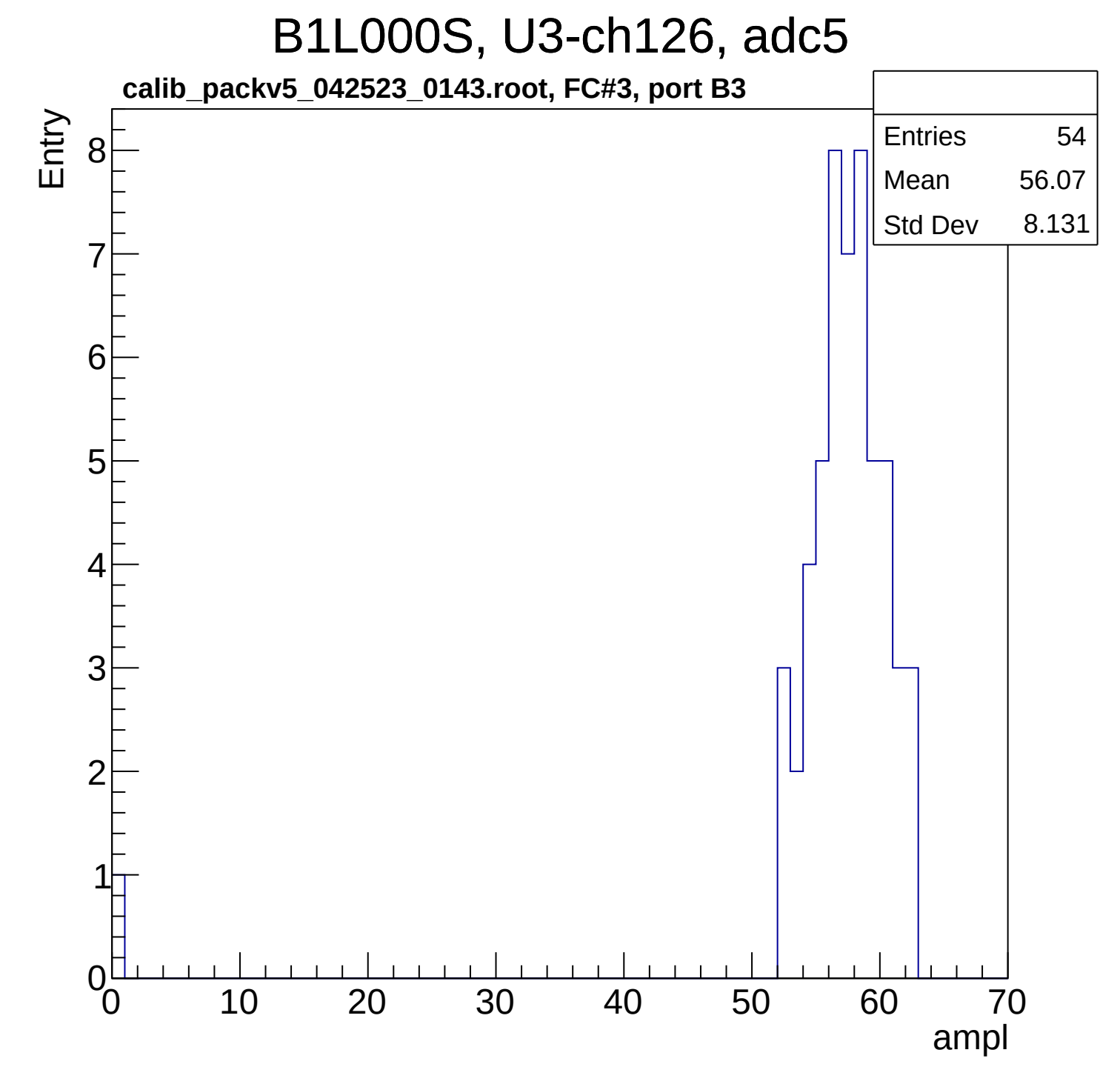
70
ampl


B1L000S, U3-ch126, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 56.07
Std Dev 8.131
Entry
ampl
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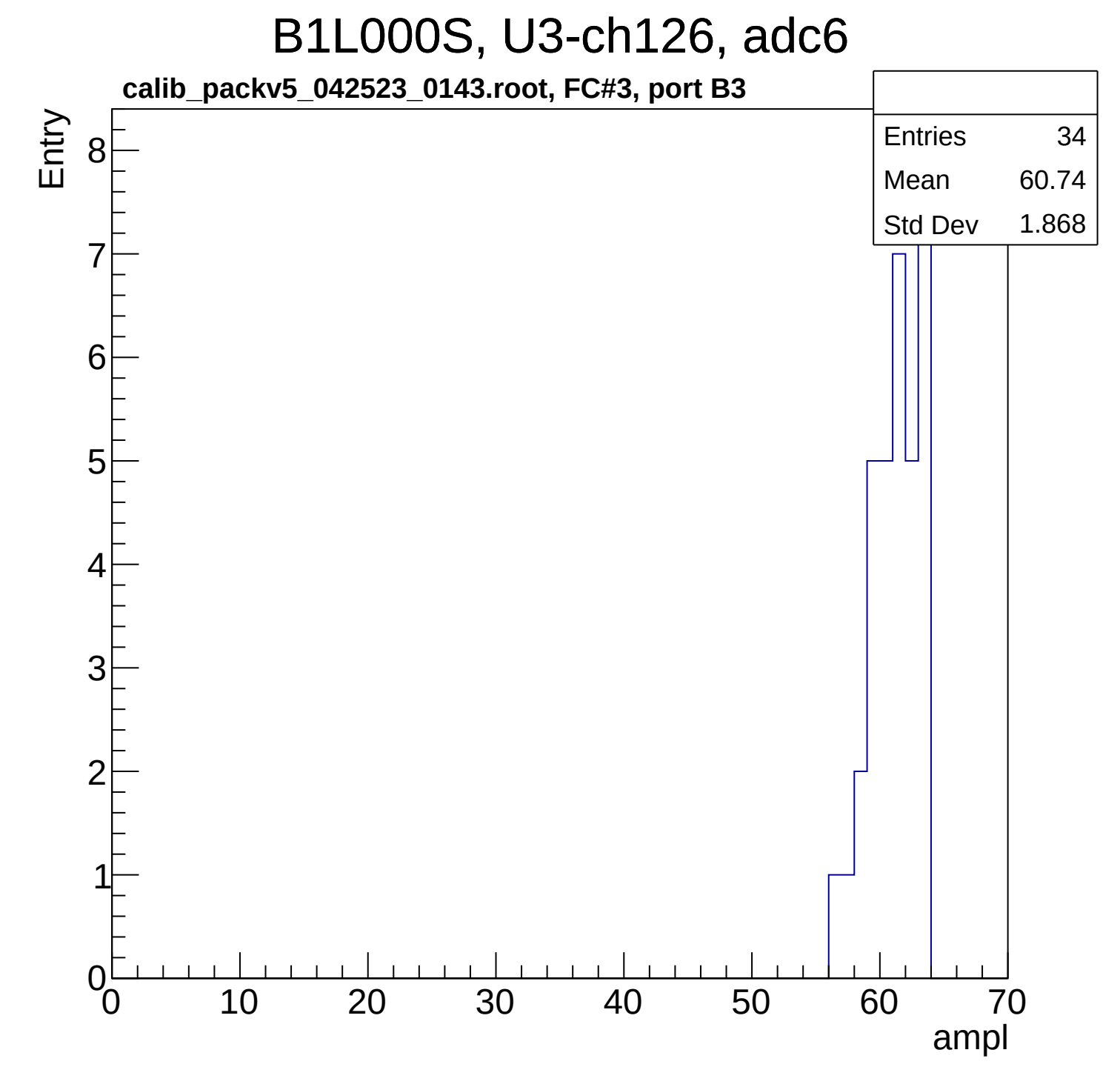
B1L000S, U3-ch126, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 34
Mean 60.74
Std Dev 1.868
Entry
ampl
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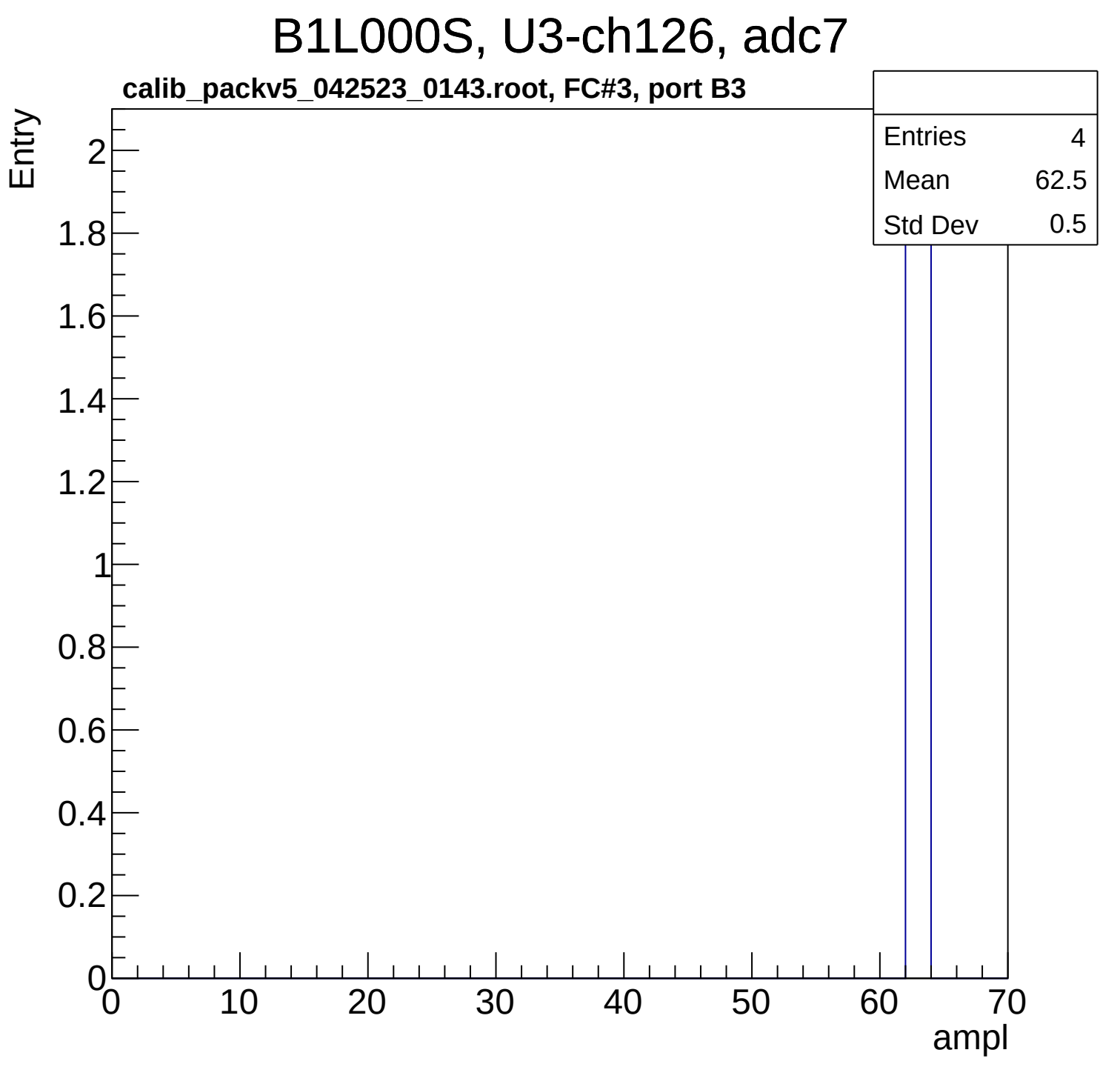
B1L000S, U3-ch126, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
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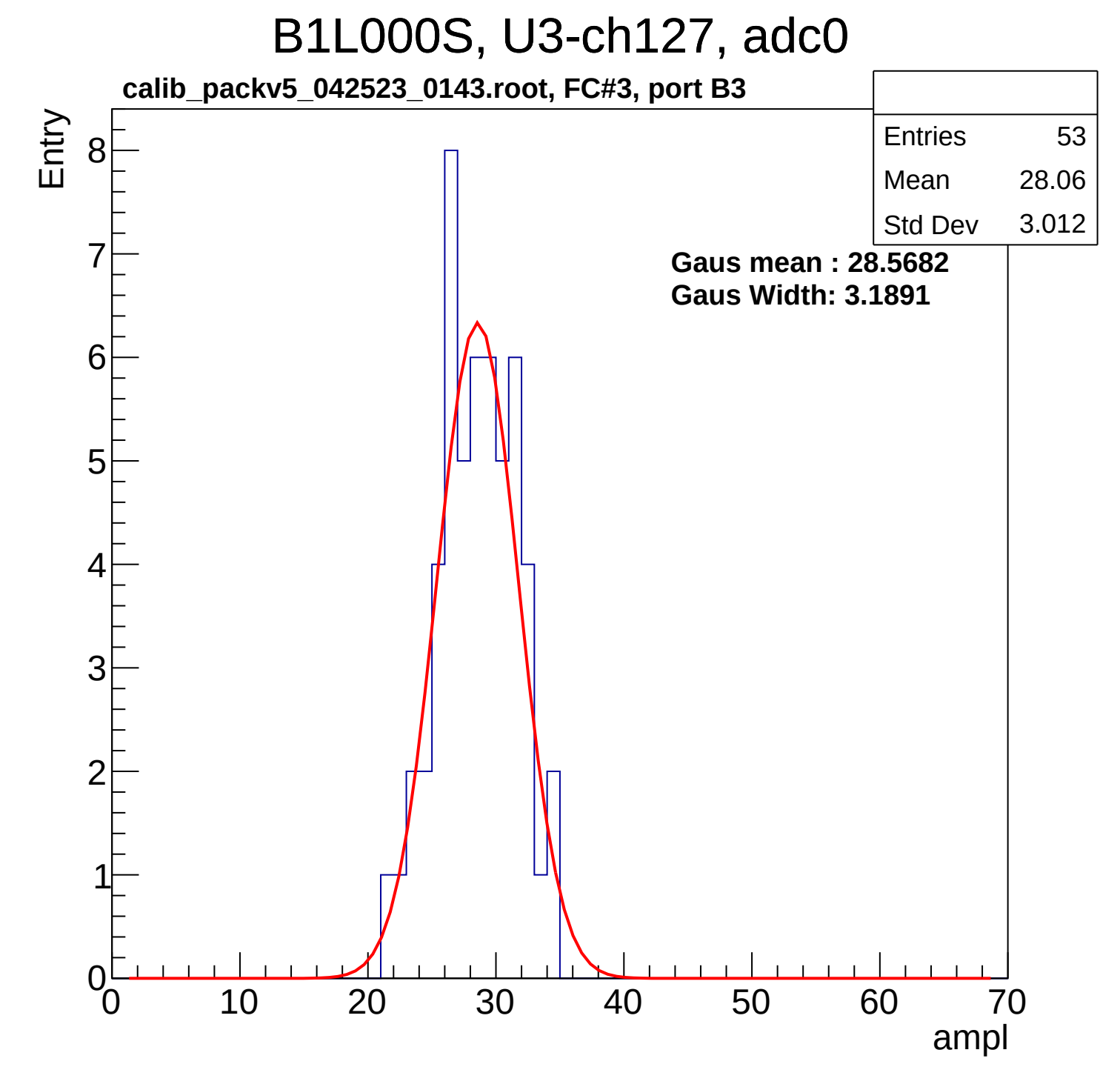
B1L000S, U3-ch127, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 28.06
Std Dev 3.012
Gaus mean : 28.5682
Gaus Width: 3.1891
Entry
ampl
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3
4
5
6
7
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0
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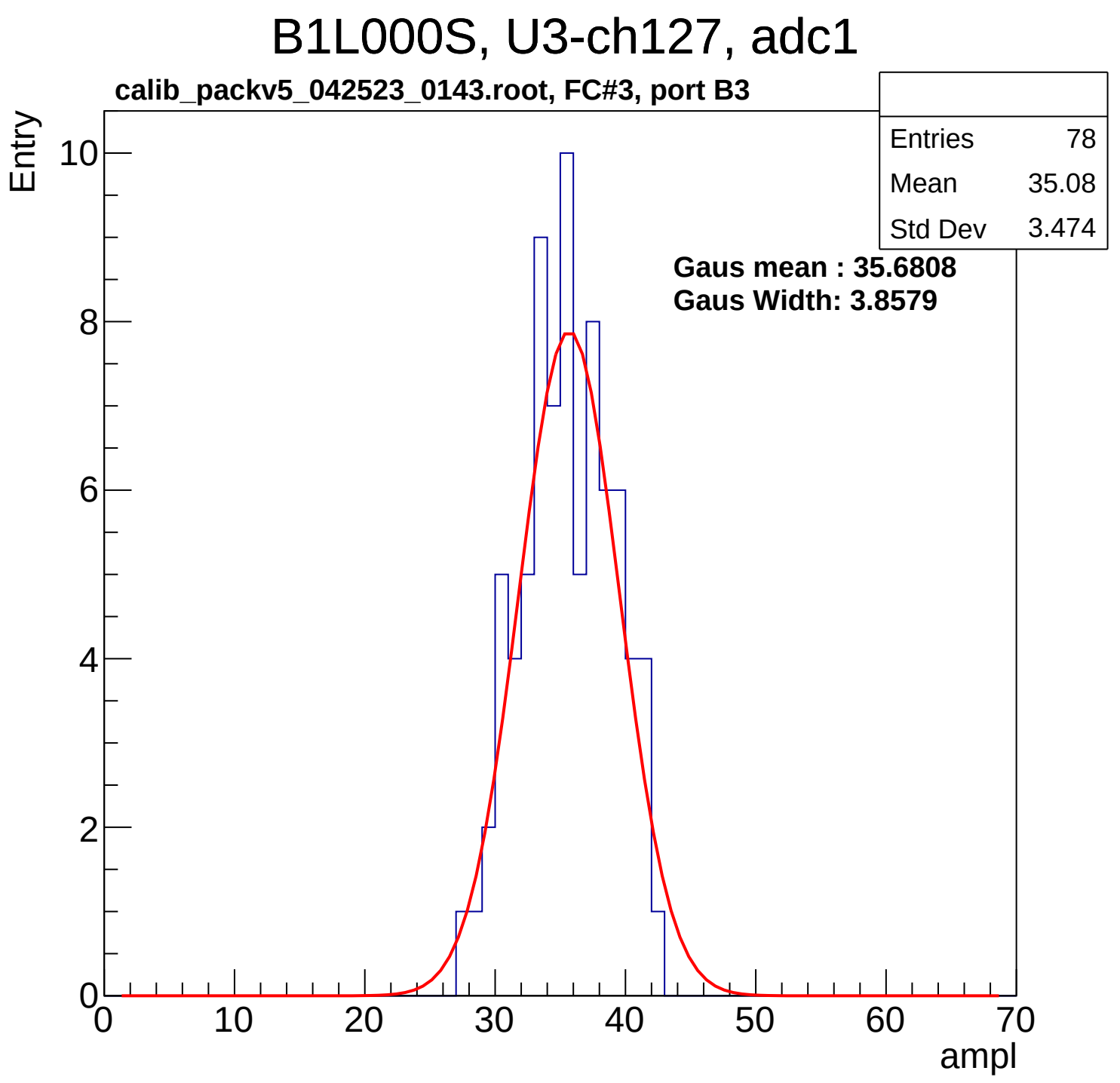
B1L000S, U3-ch127, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 35.08
Std Dev 3.474
Gaus mean : 35.6808
Gaus Width: 3.8579
Entry
ampl
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0
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40
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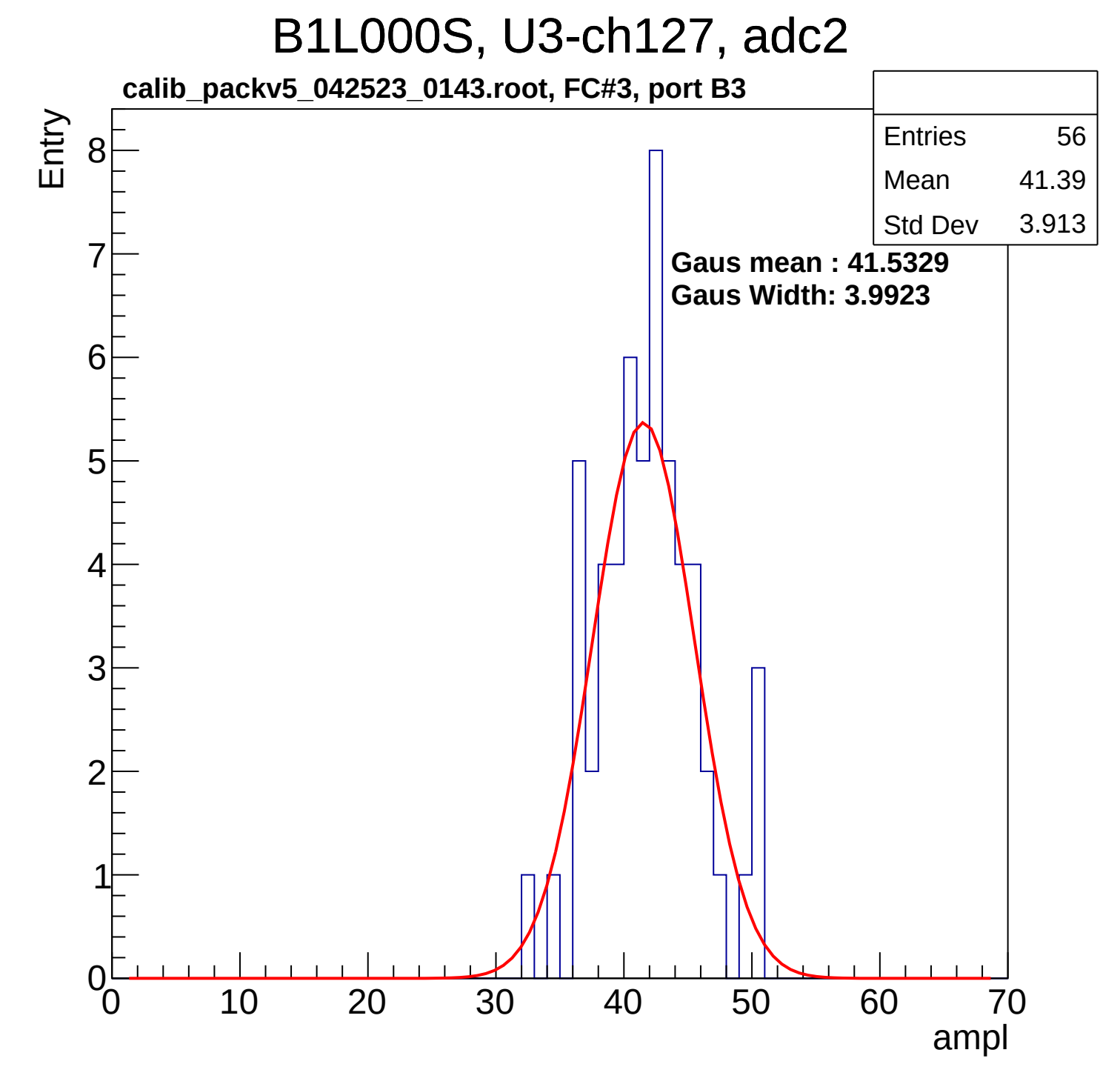
B1L000S, U3-ch127, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 41.39
Std Dev 3.913
Gaus mean : 41.5329
Gaus Width: 3.9923
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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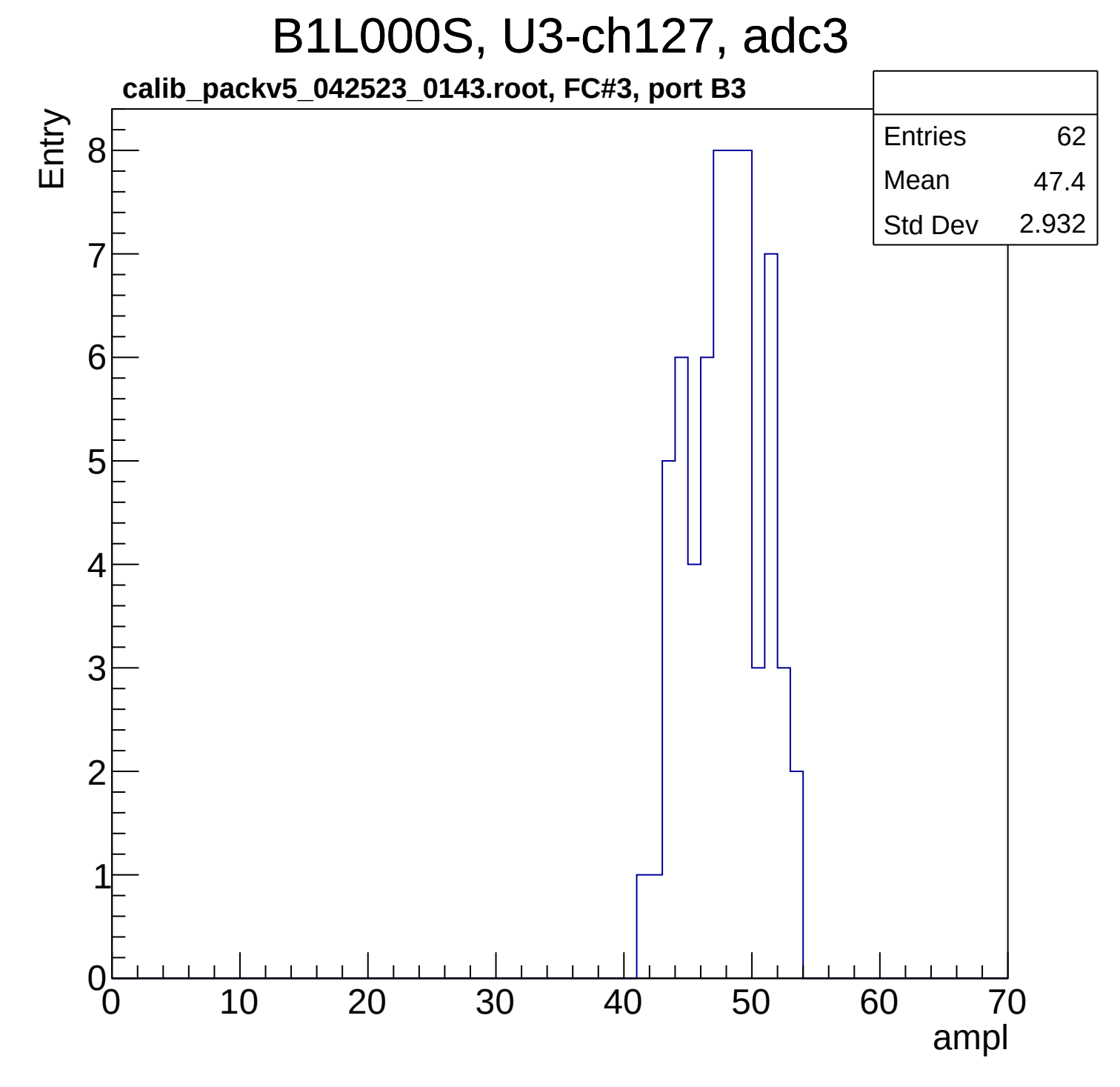
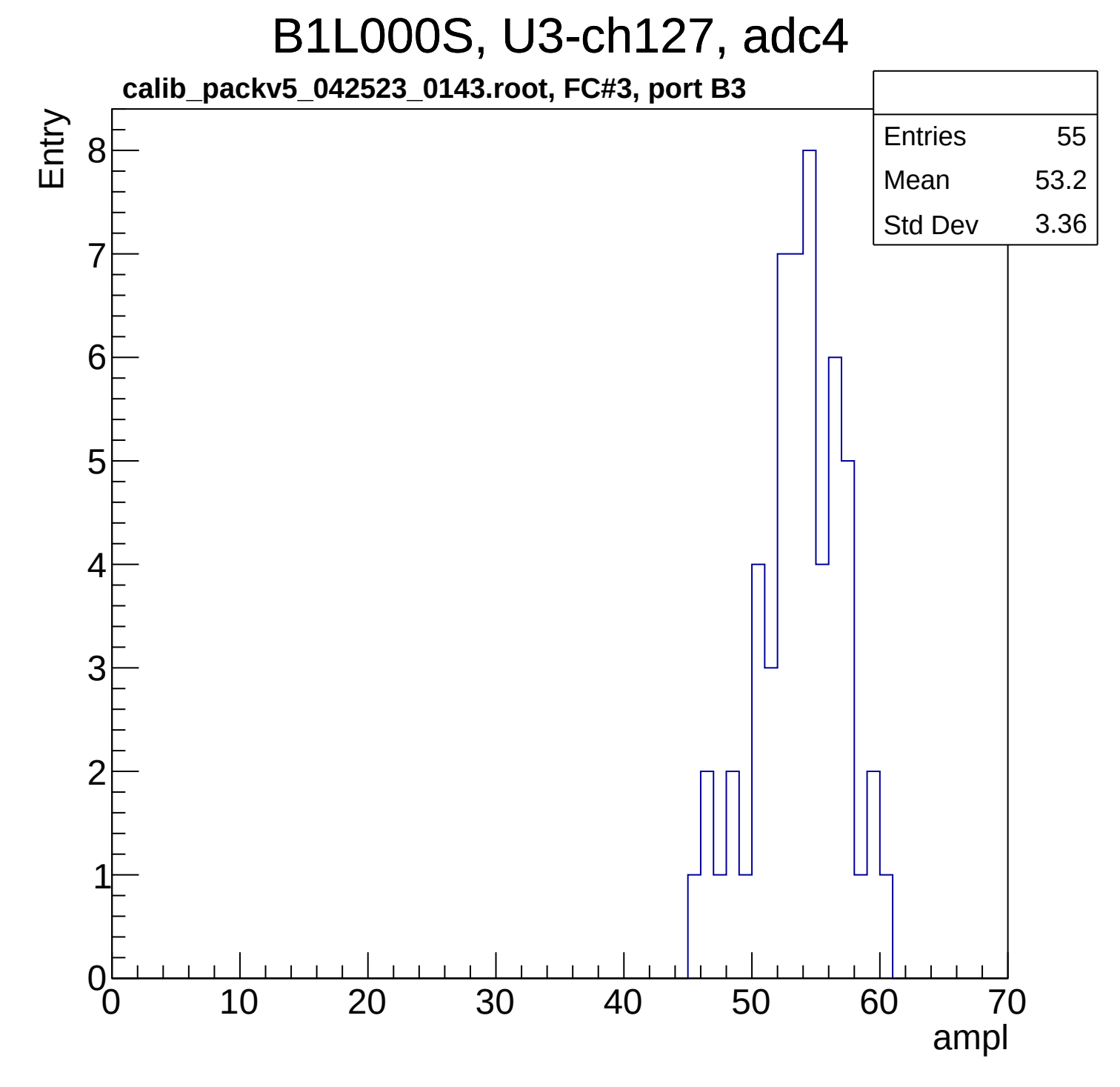
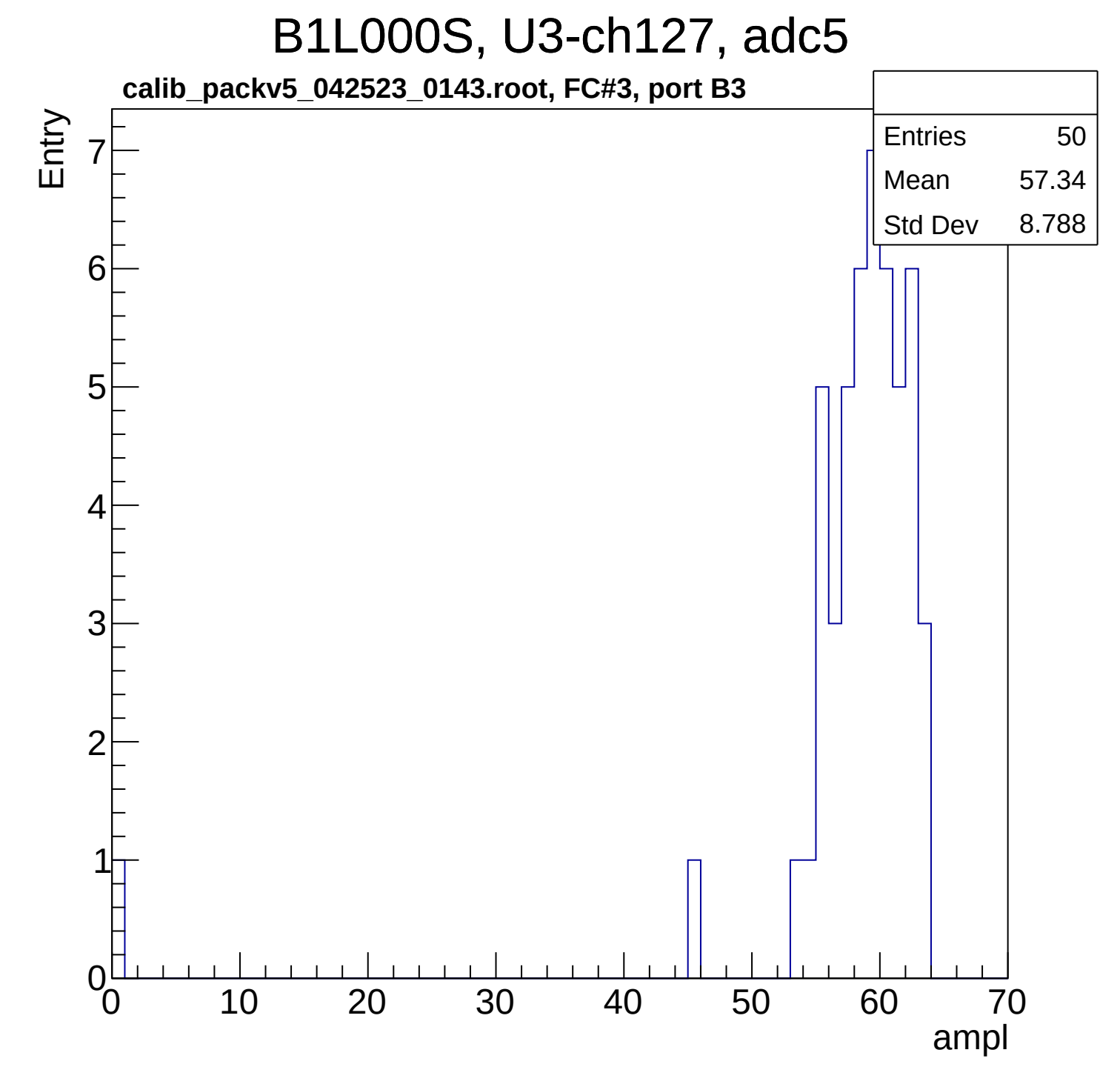
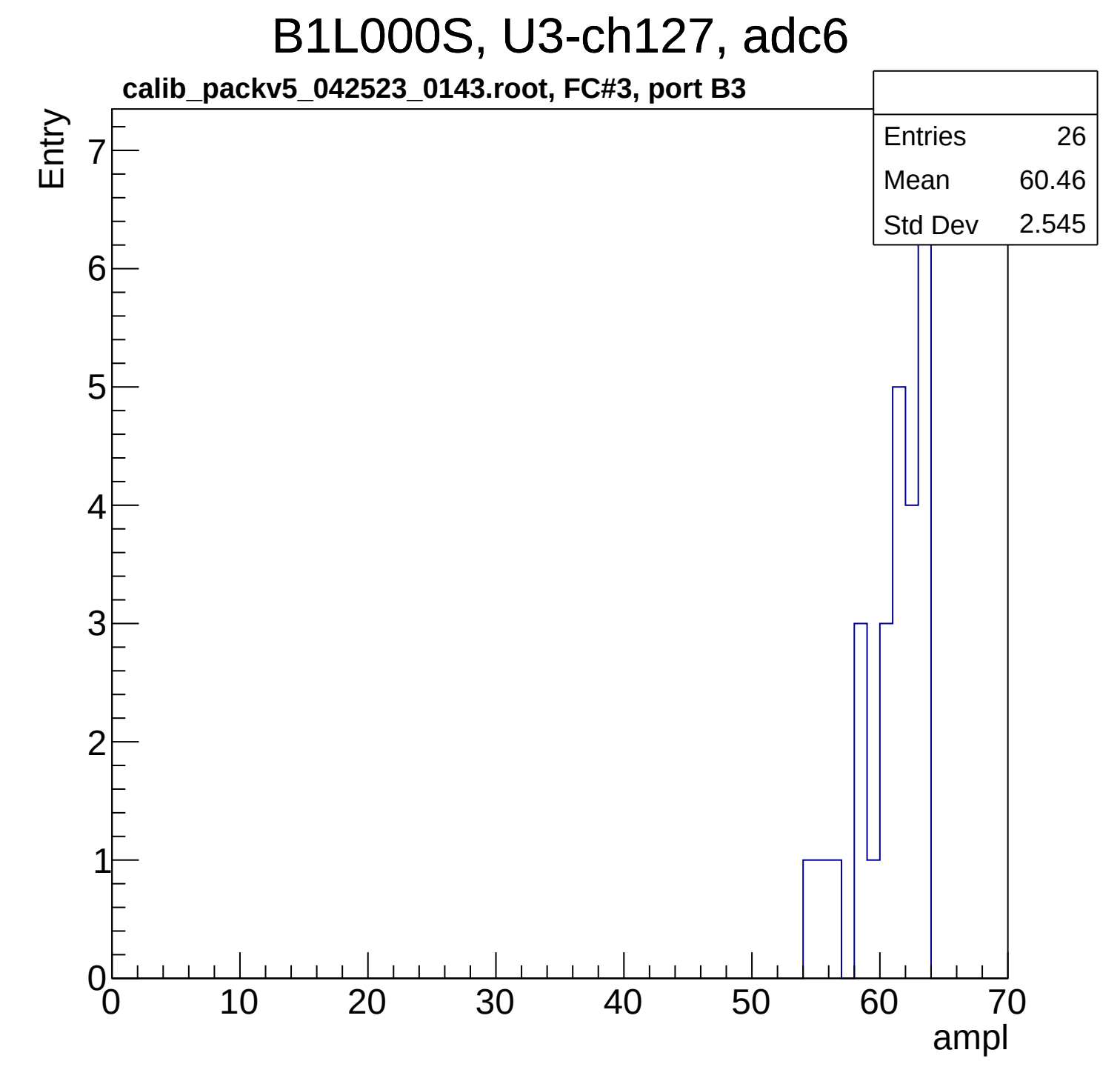
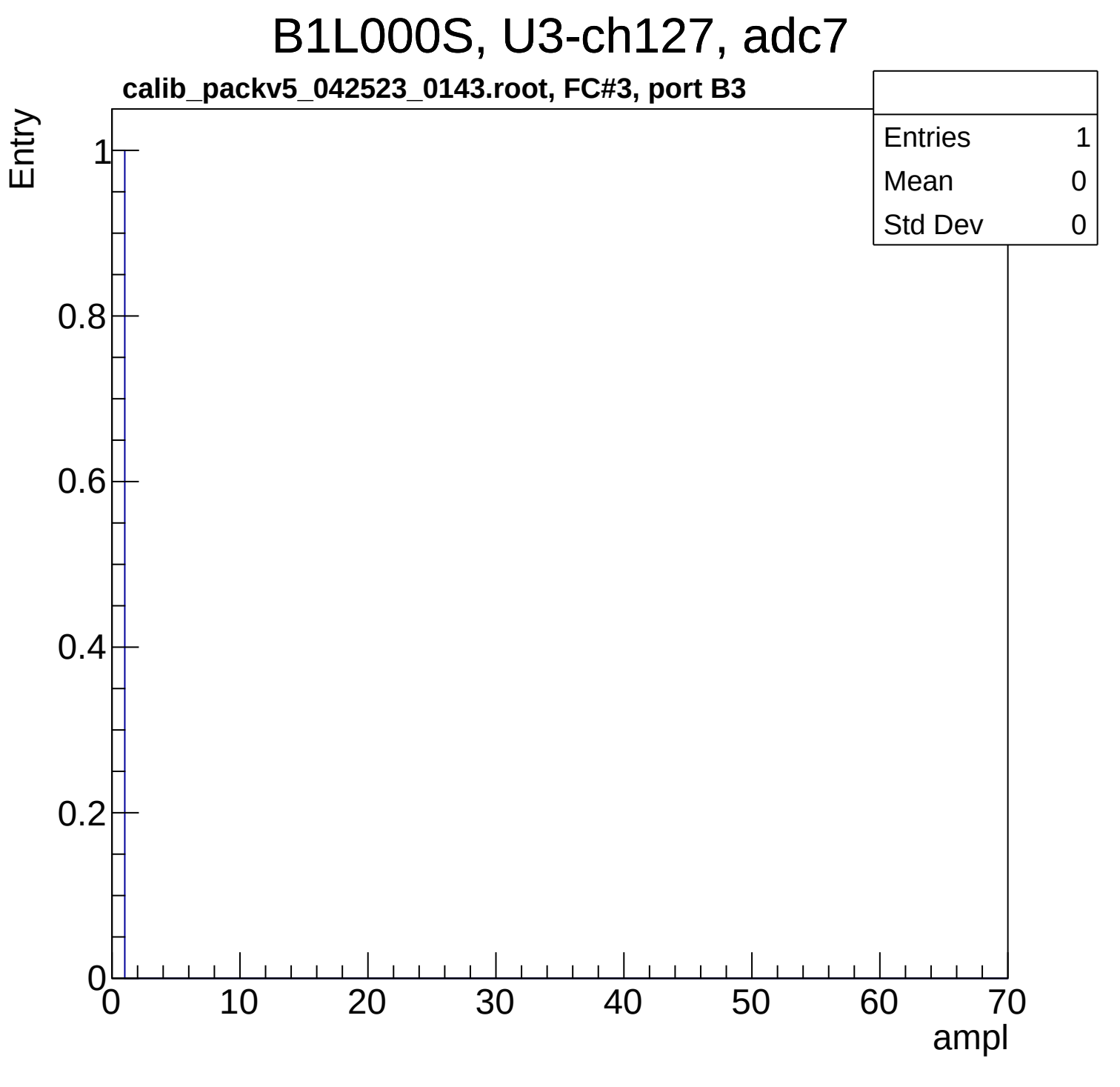
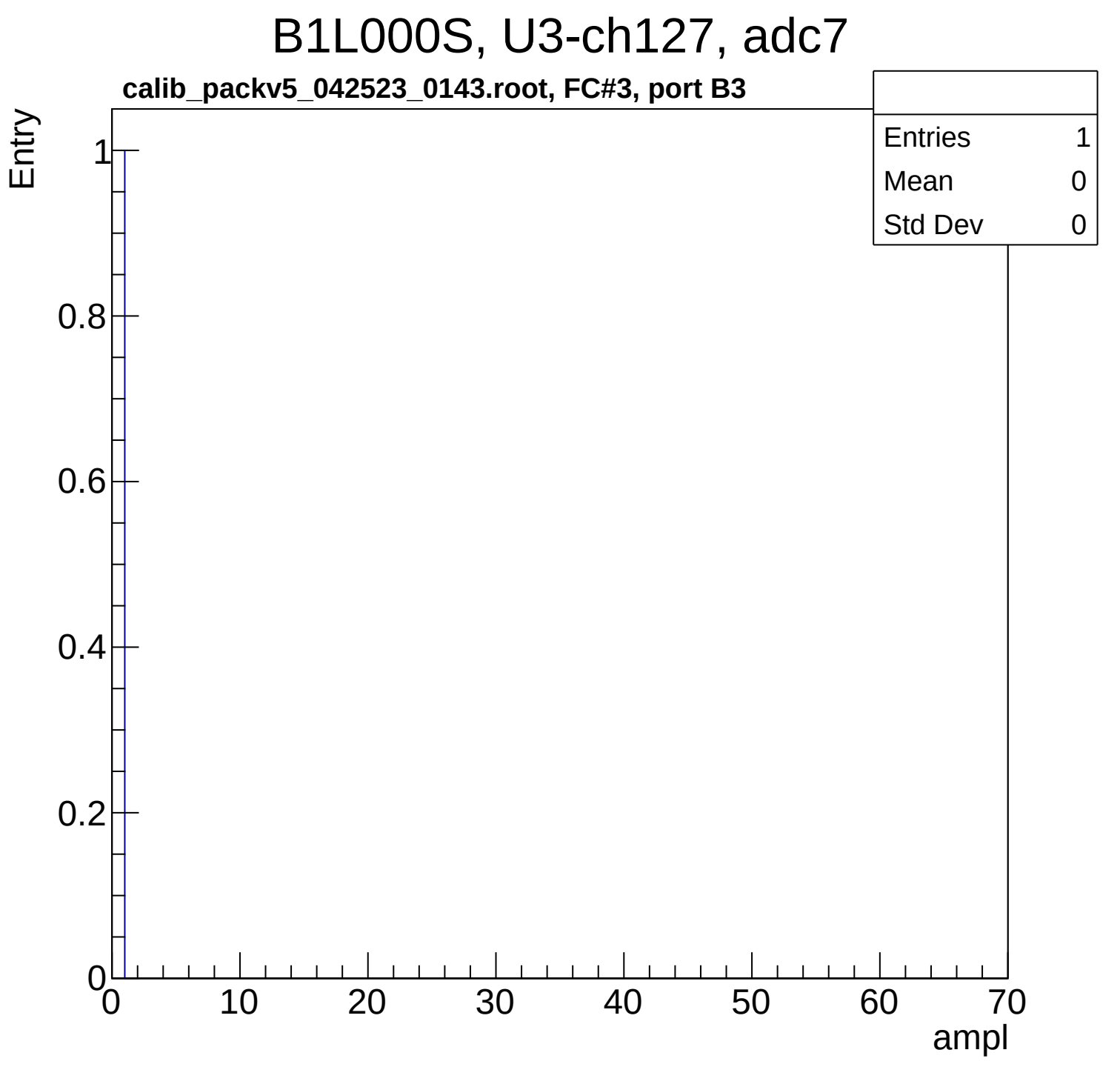

B1L000S, U3-ch127, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 47.4
Std Dev 2.932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch127, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.2
Std Dev 3.36
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U3-ch127, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 57.34
Std Dev 8.788
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch127, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 60.46
Std Dev 2.545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U3-ch127, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U3-ch127, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl