

B1L102S, U17-ch0, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	27.53
Std Dev	5.617

Gaus mean : 28.3041

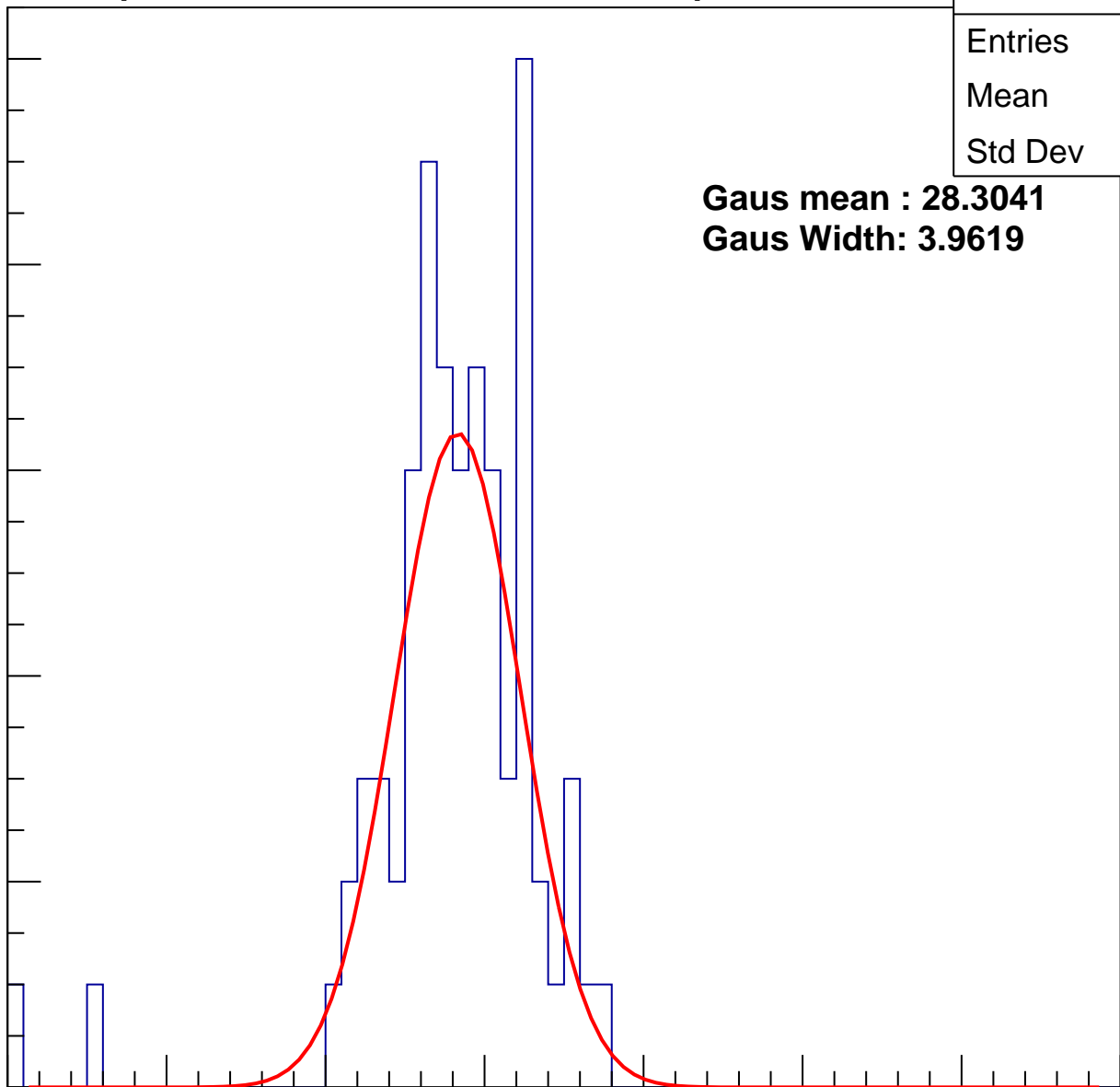
Gaus Width: 3.9619

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch0, adc1

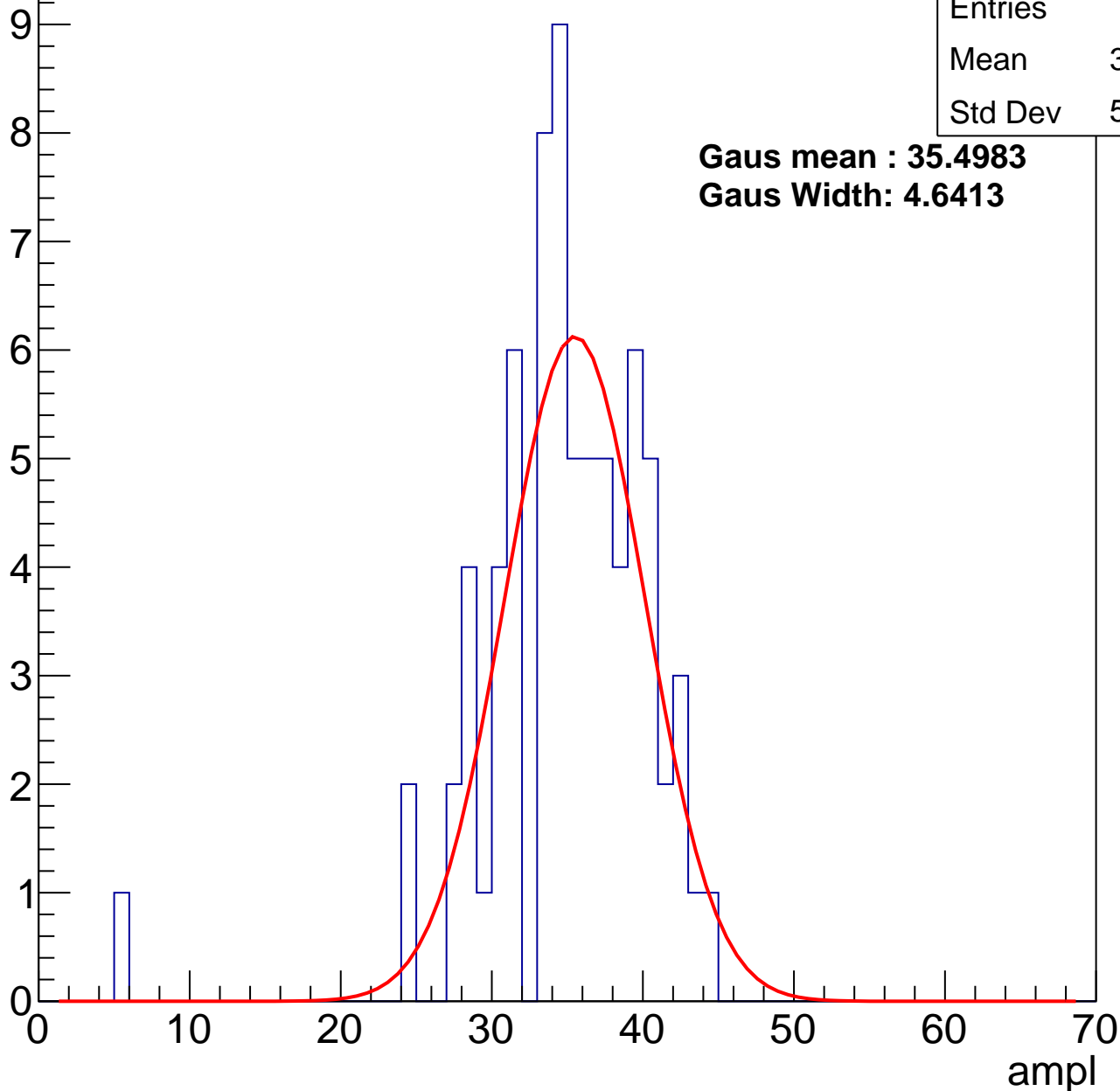
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.39
Std Dev	5.644

Gaus mean : 35.4983

Gaus Width: 4.6413



B1L102S, U17-ch0, adc2

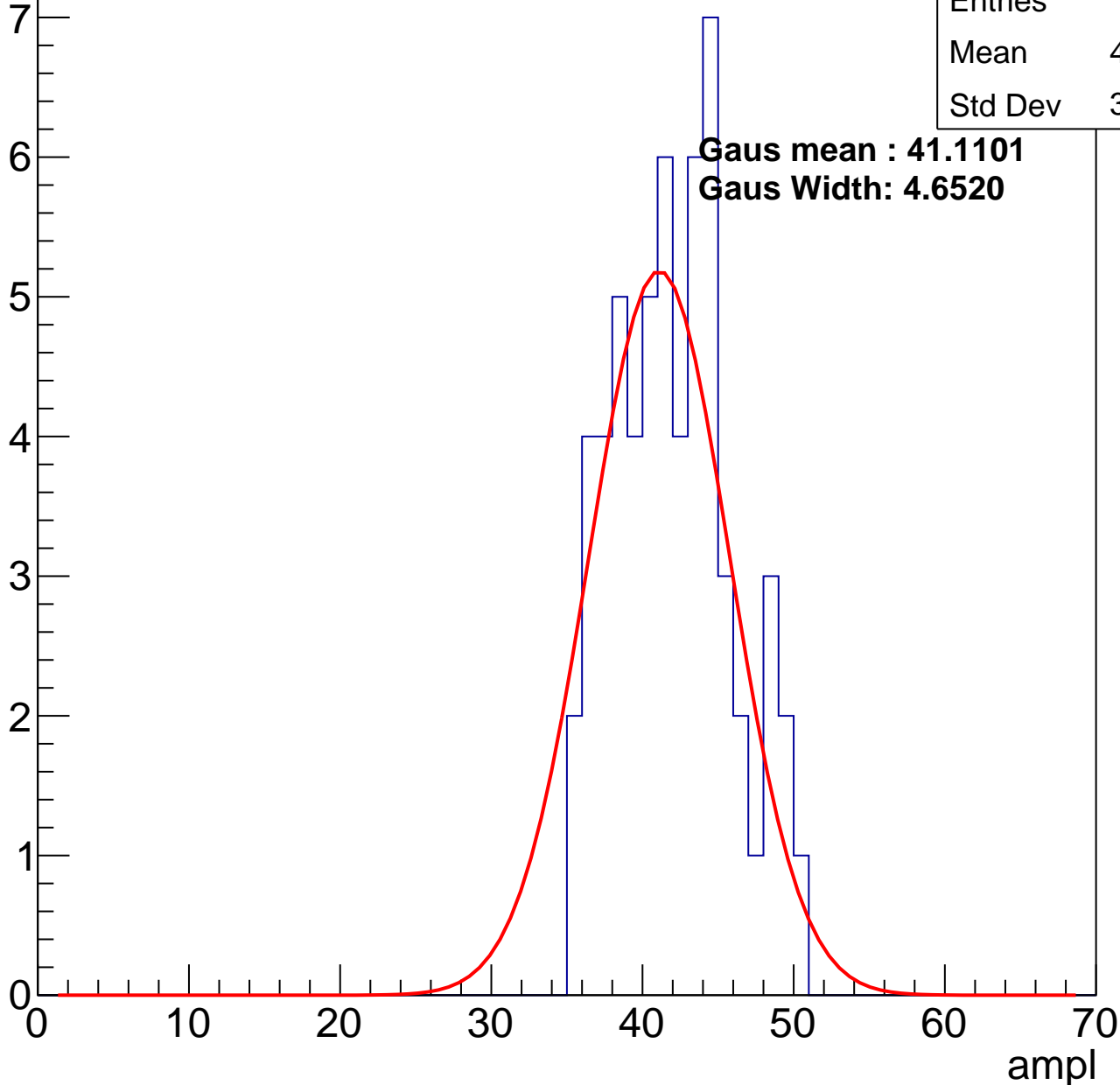
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.59
Std Dev	3.823

Gaus mean : 41.1101

Gaus Width: 4.6520

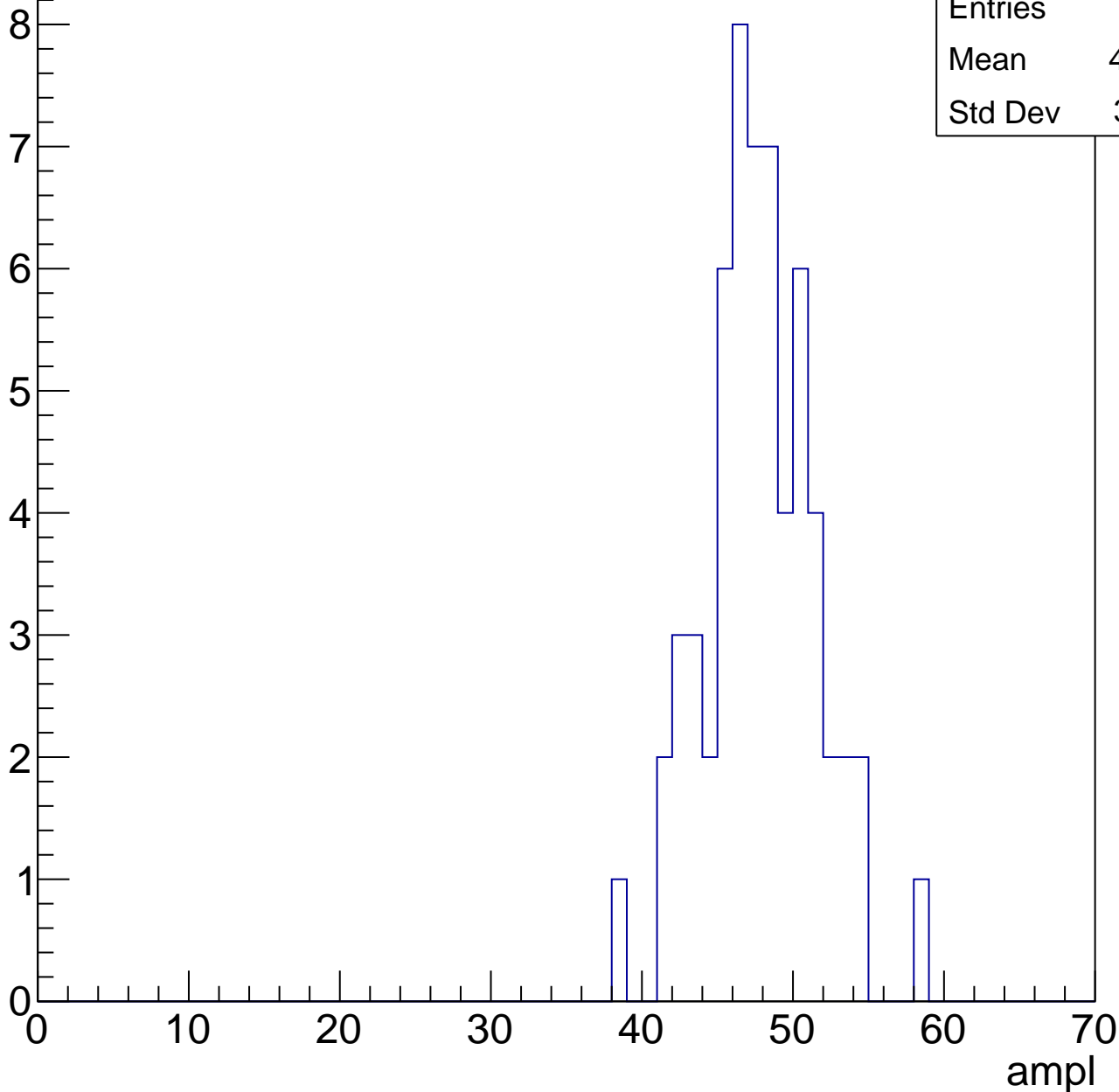


B1L102S, U17-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

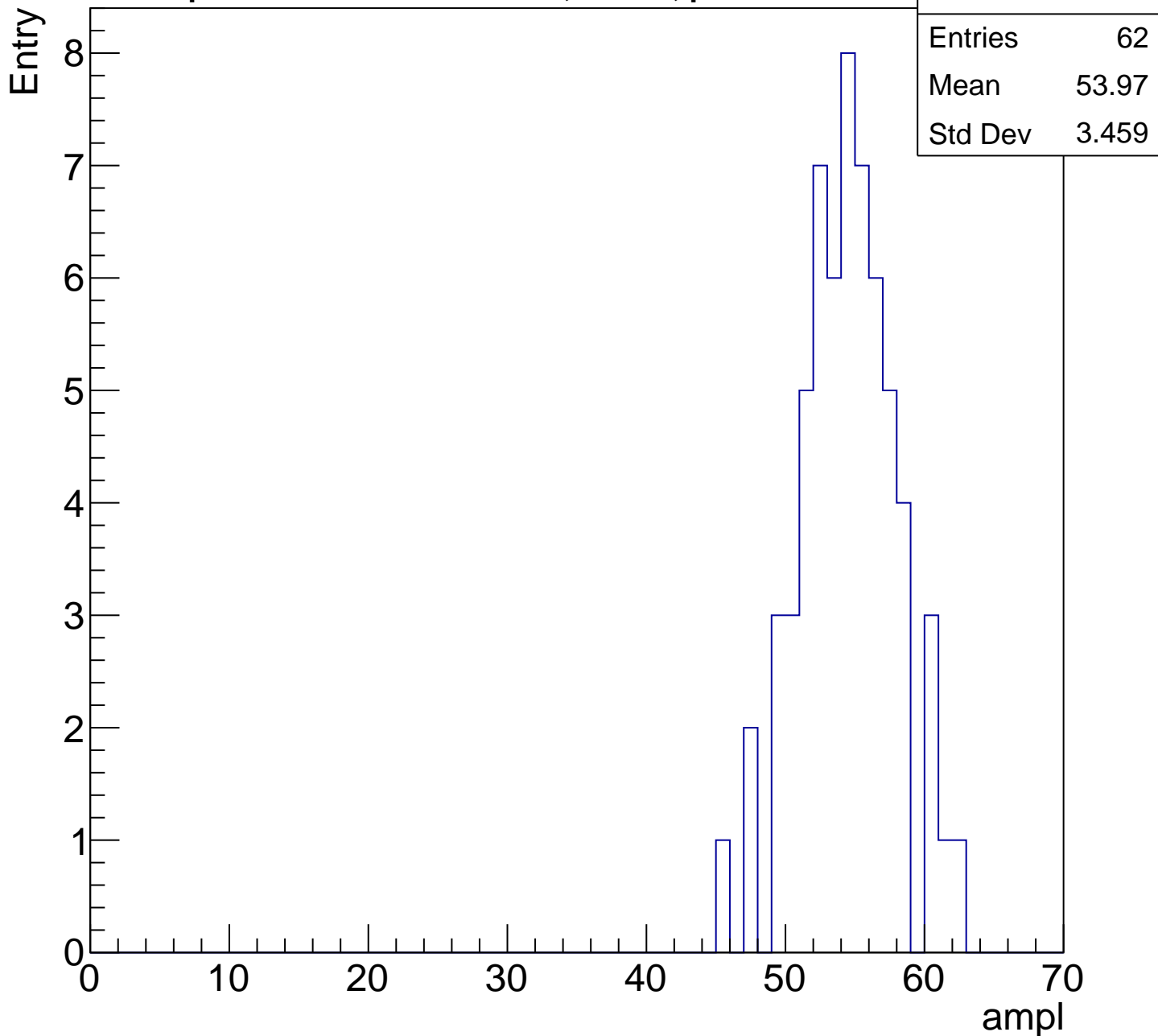
Entry

Entries	60
Mean	47.37
Std Dev	3.651



B1L102S, U17-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U17-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

Entries 58

Mean 57.81

Std Dev 8.303

8

6

4

2

0

0

10

20

30

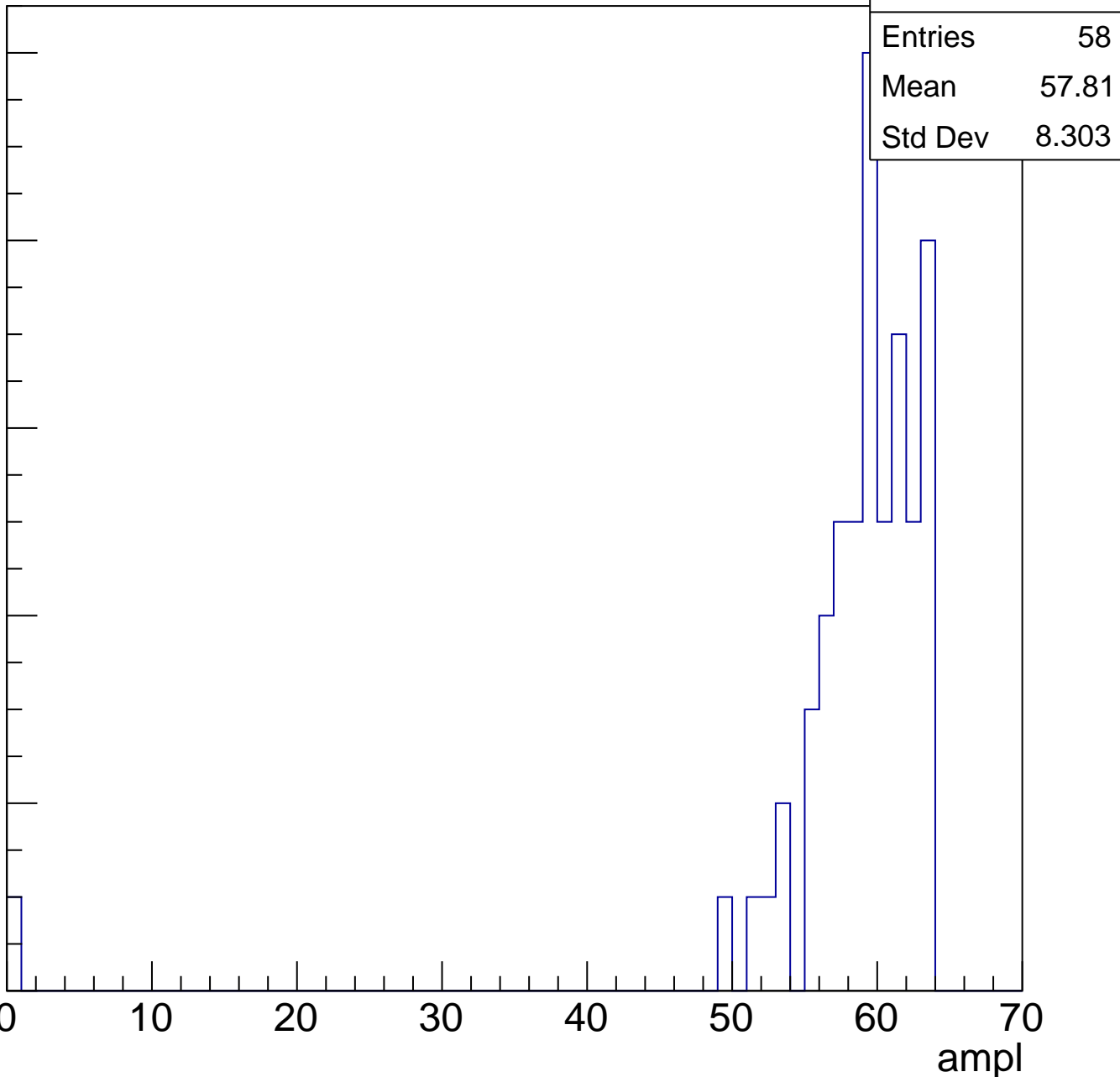
40

50

60

70

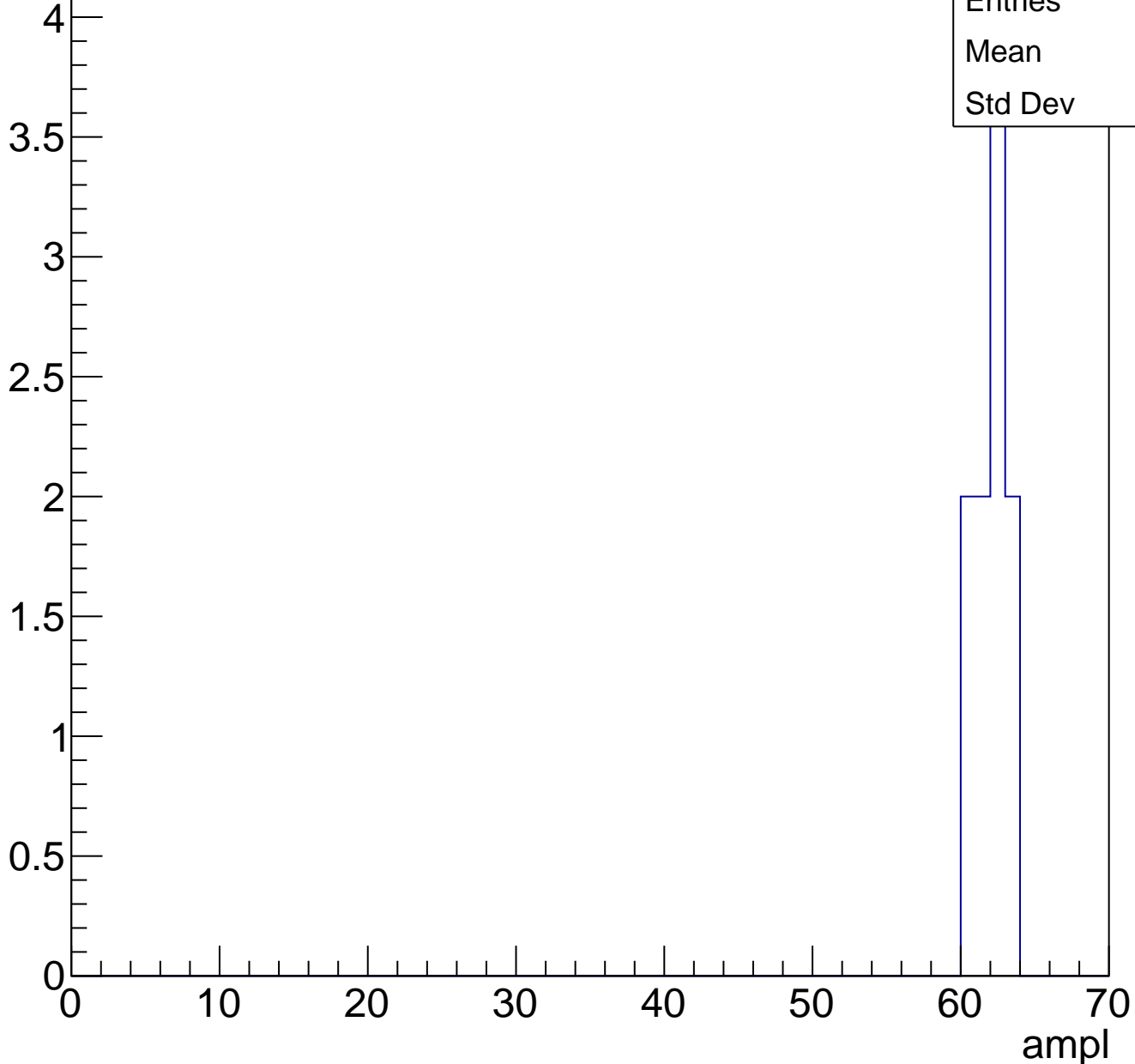
ampl



B1L102S, U17-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	61.6
Std Dev	1.02

B1L102S, U17-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch1, adc0

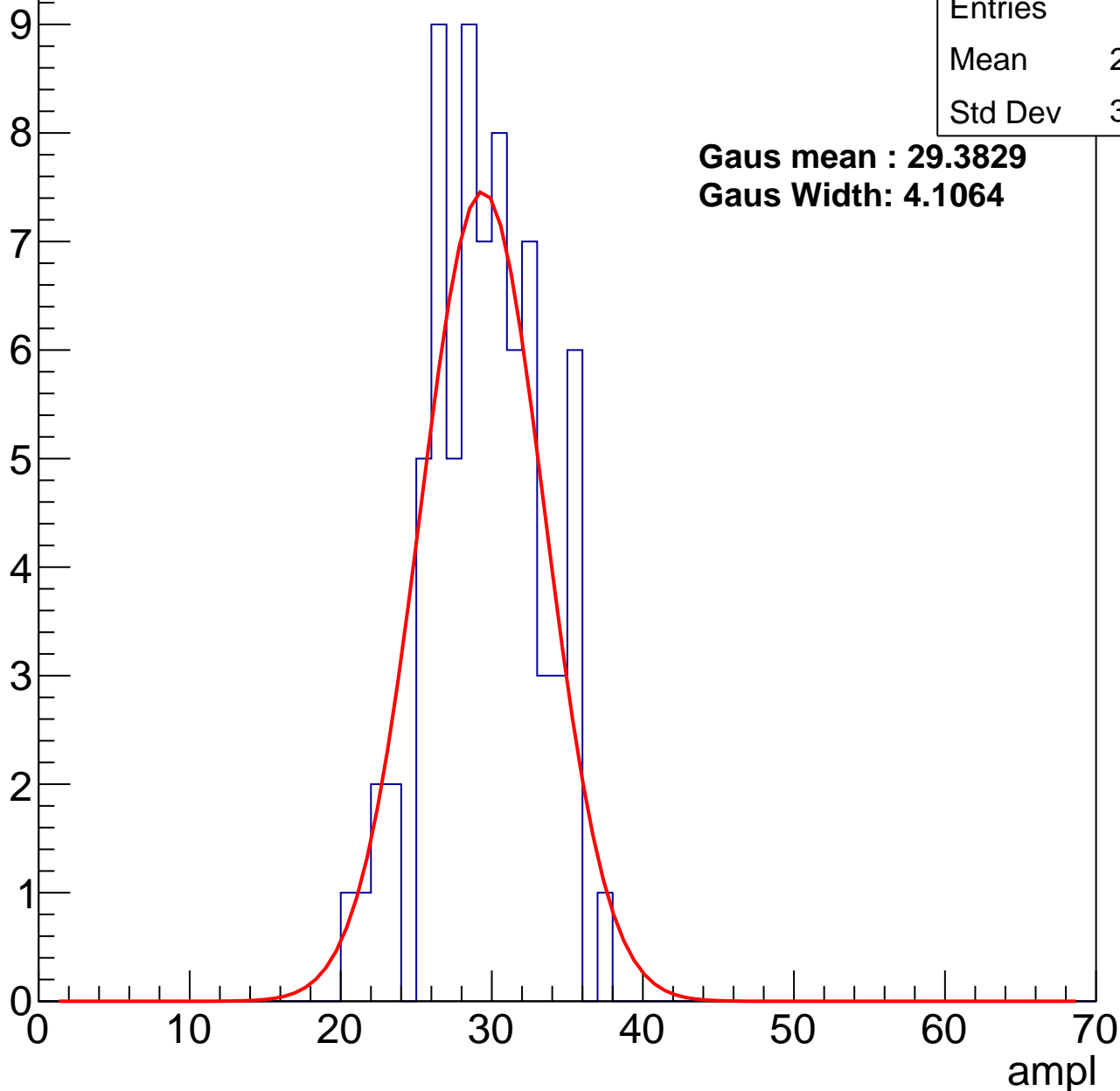
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.04
Std Dev	3.657

Gaus mean : 29.3829

Gaus Width: 4.1064



B1L102S, U17-ch1, adc1

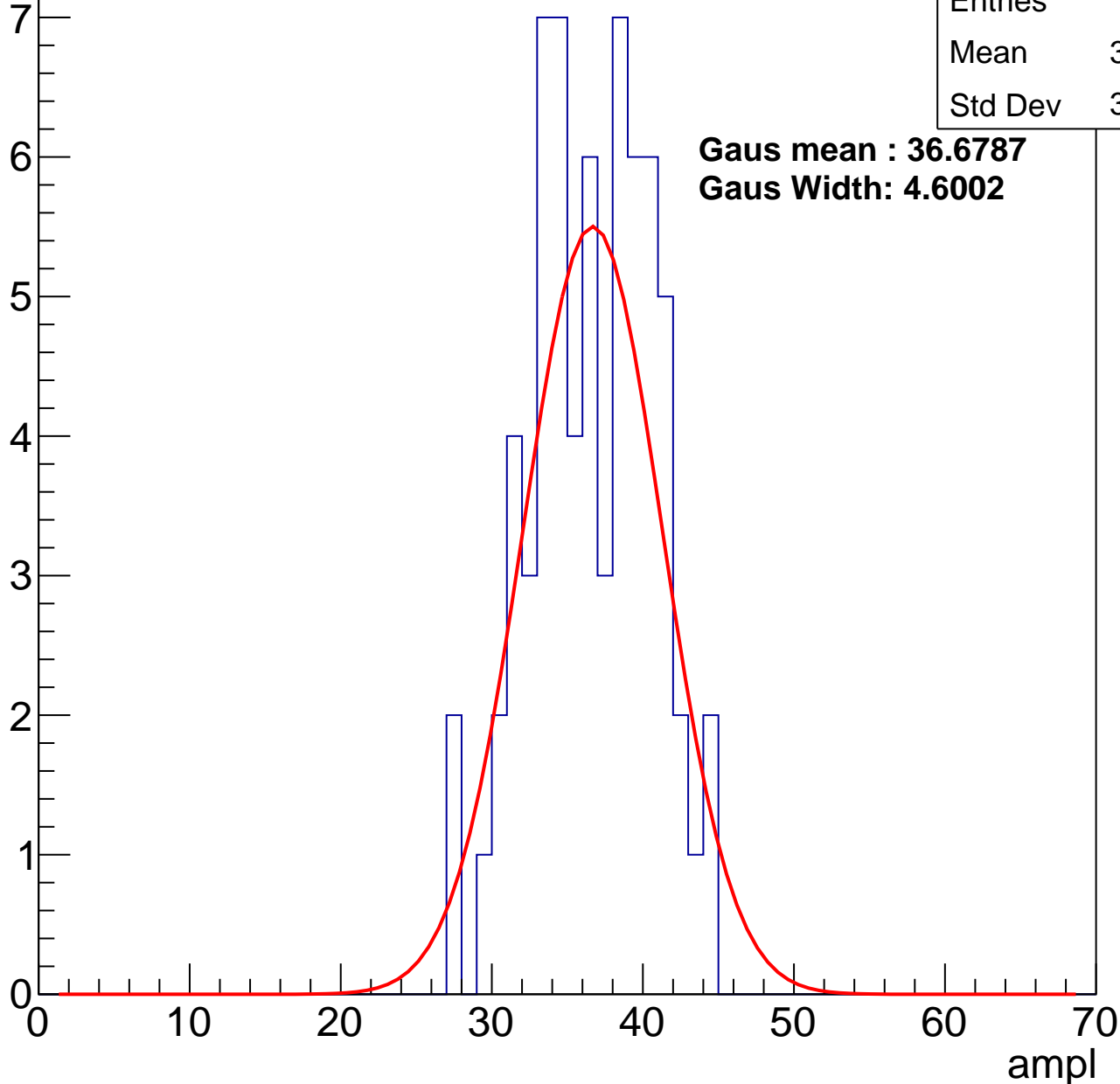
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.16
Std Dev	3.984

Gaus mean : 36.6787

Gaus Width: 4.6002



B1L102S, U17-ch1, adc2

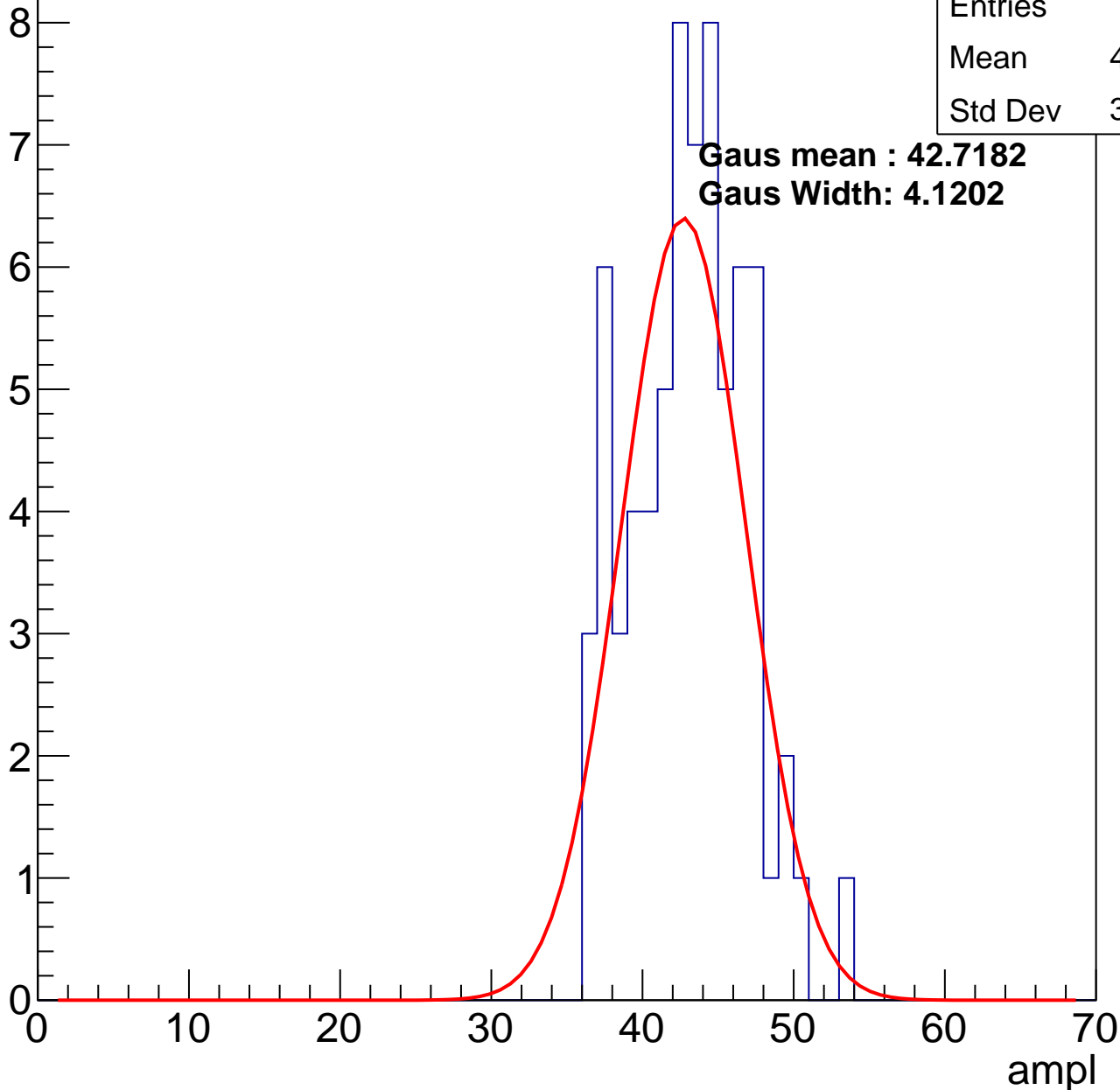
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.66
Std Dev	3.745

Gaus mean : 42.7182

Gaus Width: 4.1202

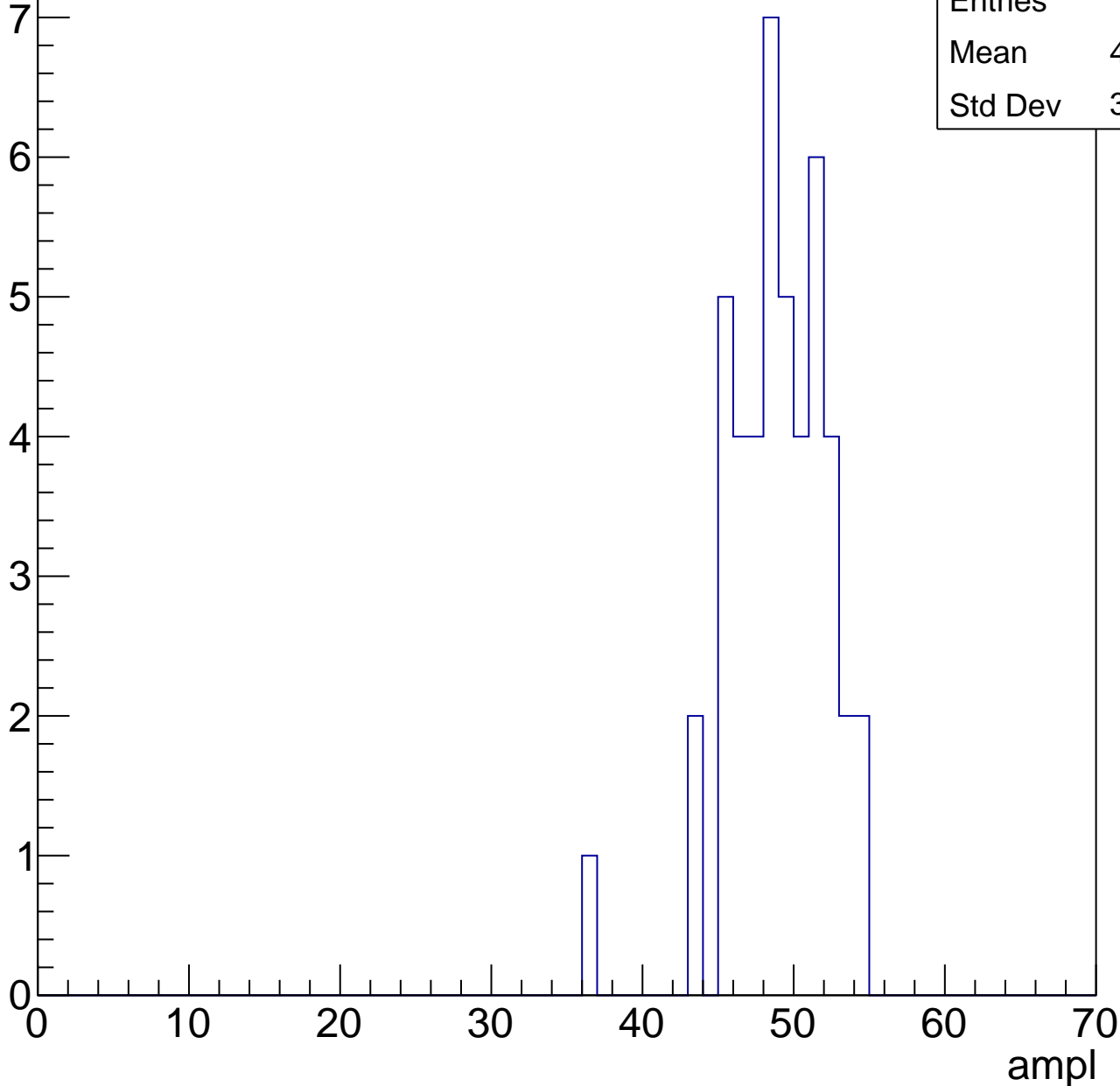


B1L102S, U17-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

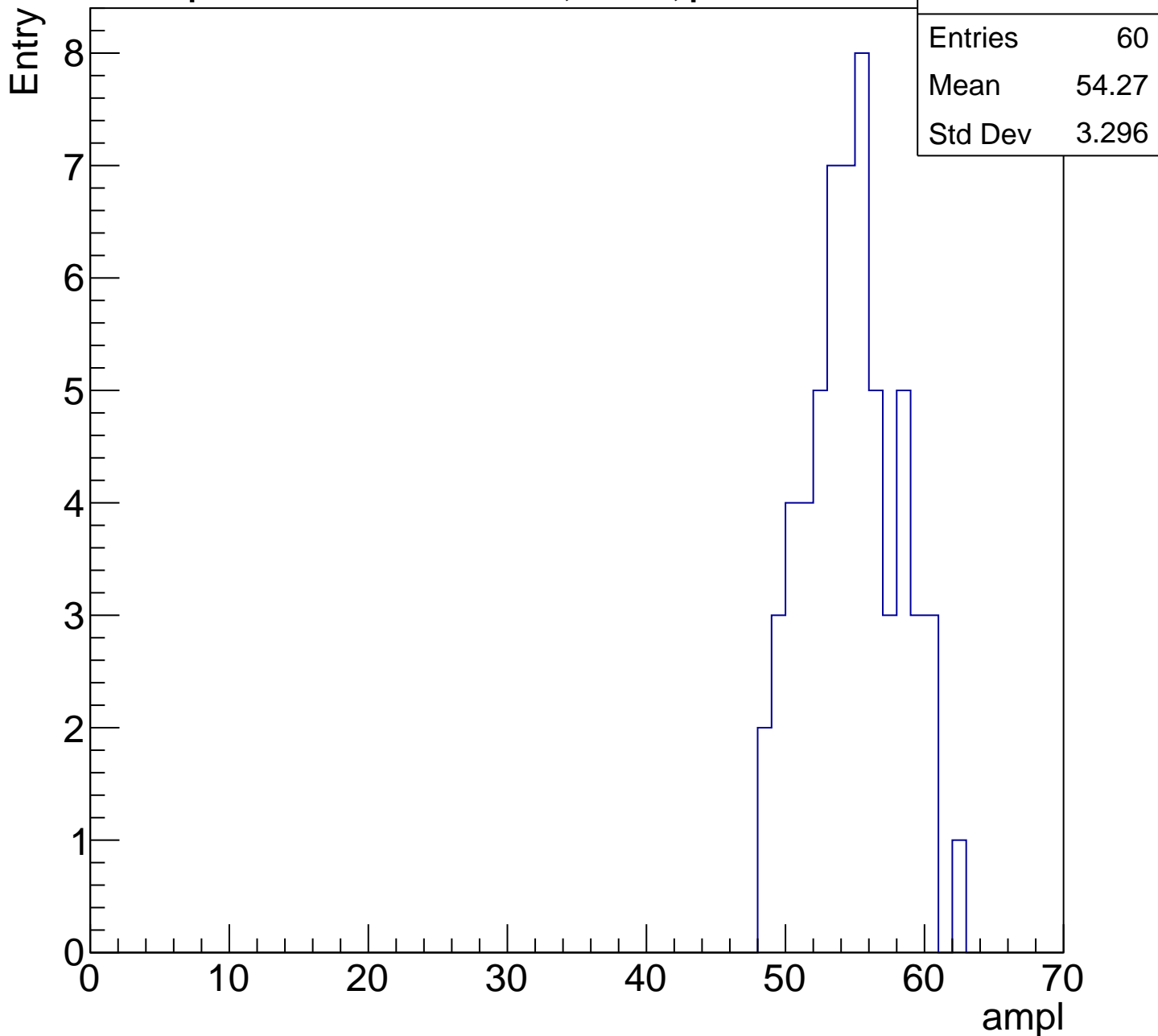
Entry

Entries	46
Mean	48.43
Std Dev	3.334



B1L102S, U17-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

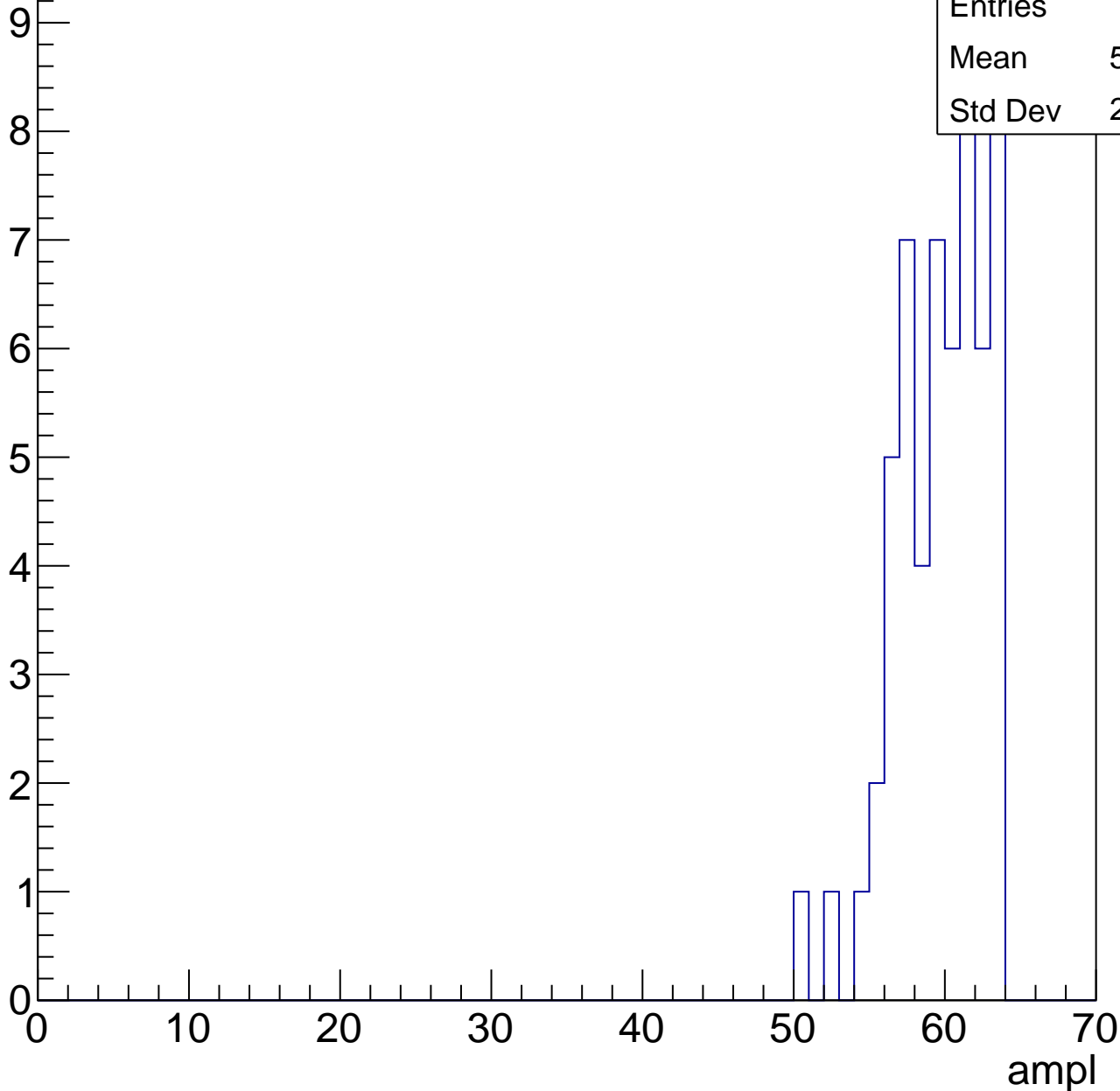


B1L102S, U17-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	59.25
Std Dev	2.952



B1L102S, U17-ch1, adc6

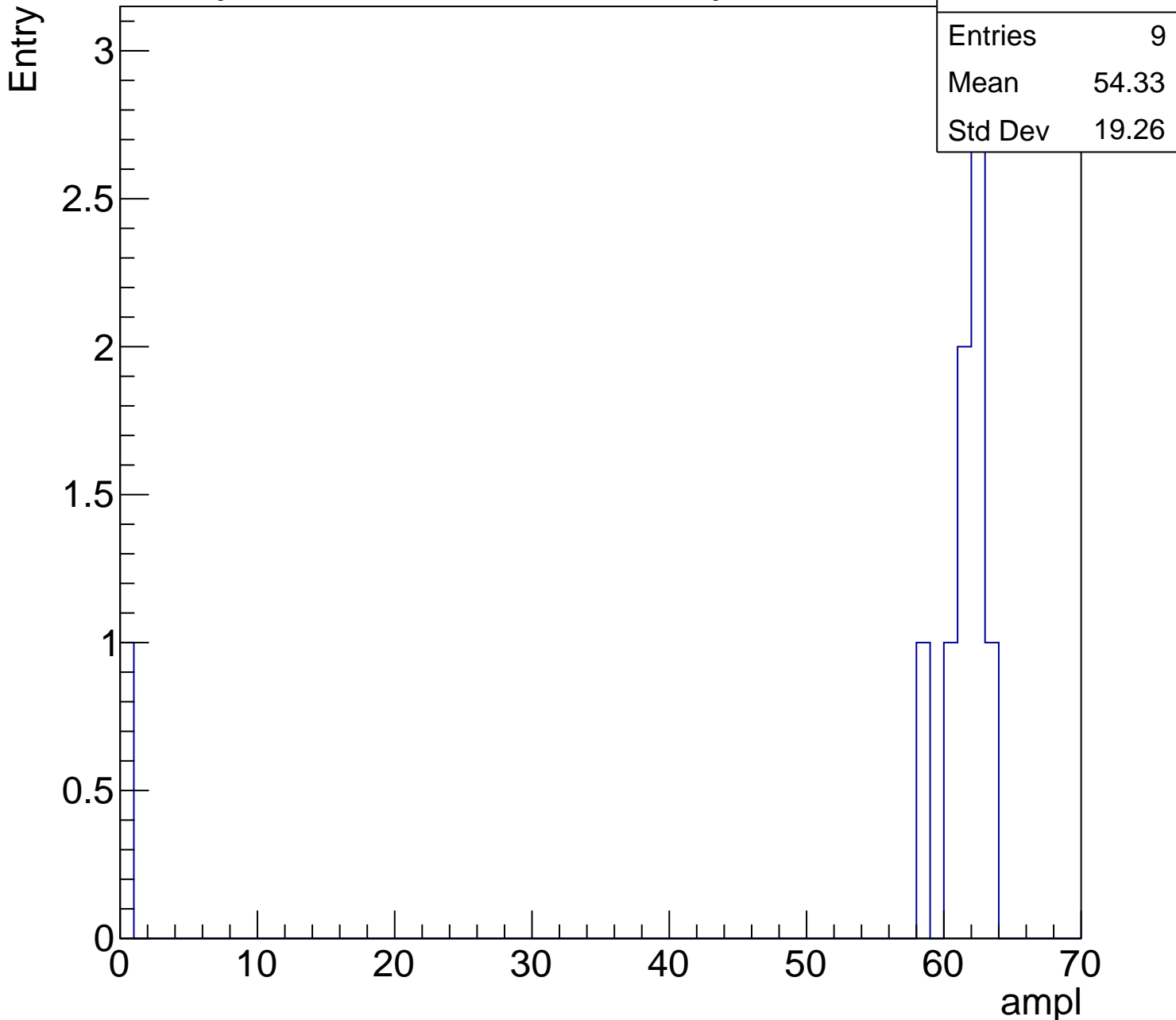
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	54.33
Std Dev	19.26

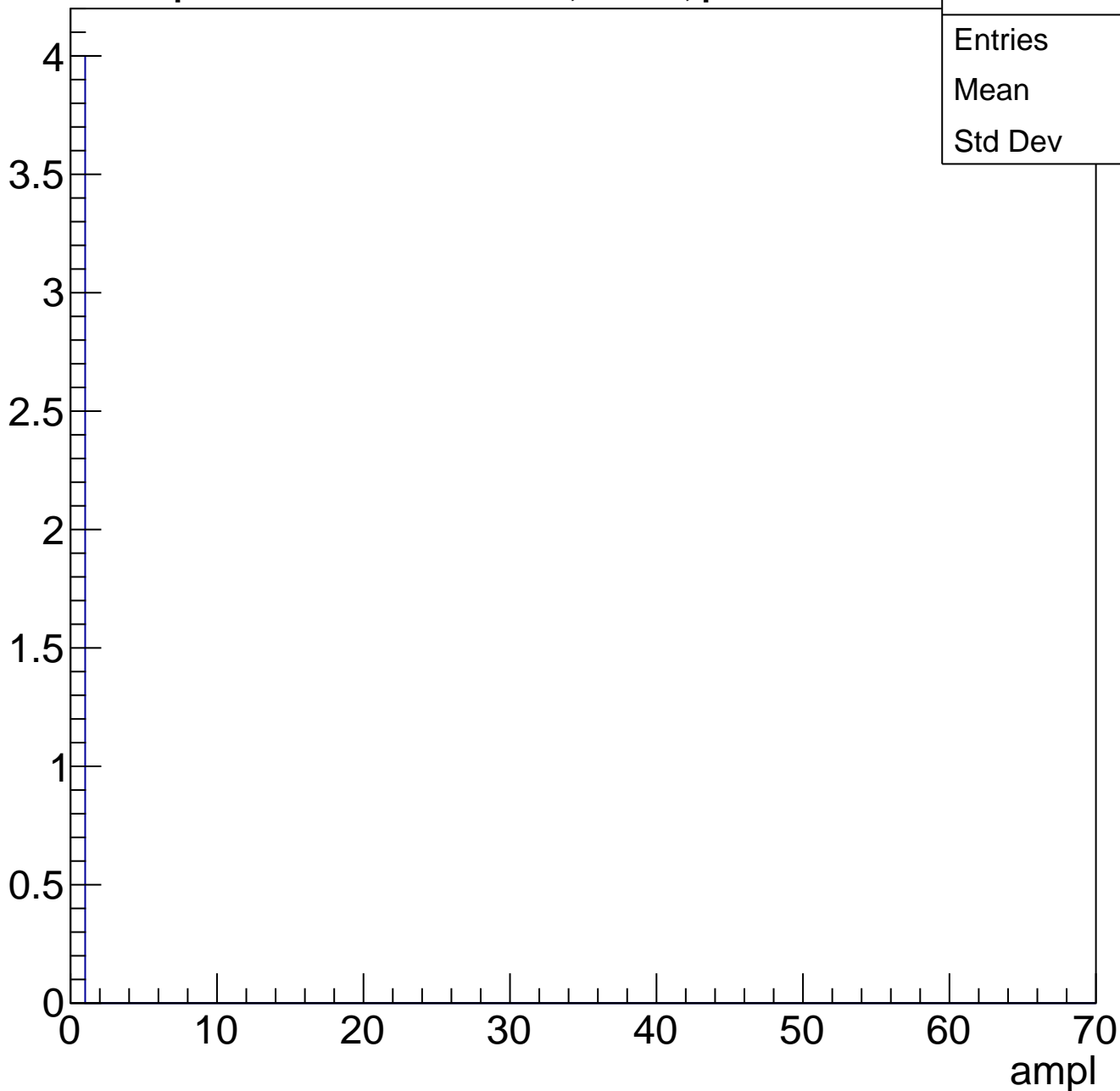
ampl



B1L102S, U17-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L102S, U17-ch2, adc0

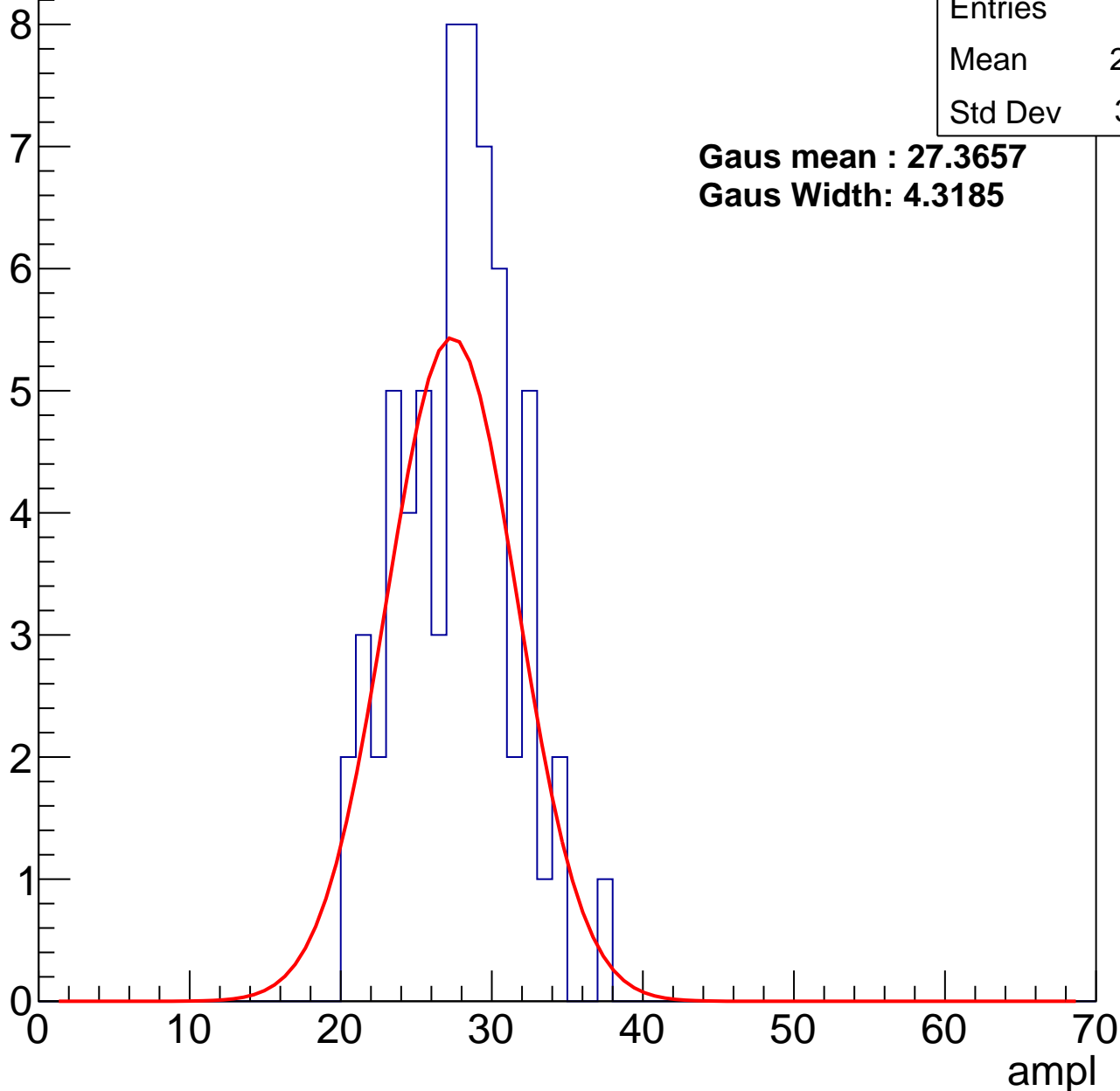
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.25
Std Dev	3.691

Gaus mean : 27.3657

Gaus Width: 4.3185



B1L102S, U17-ch2, adc1

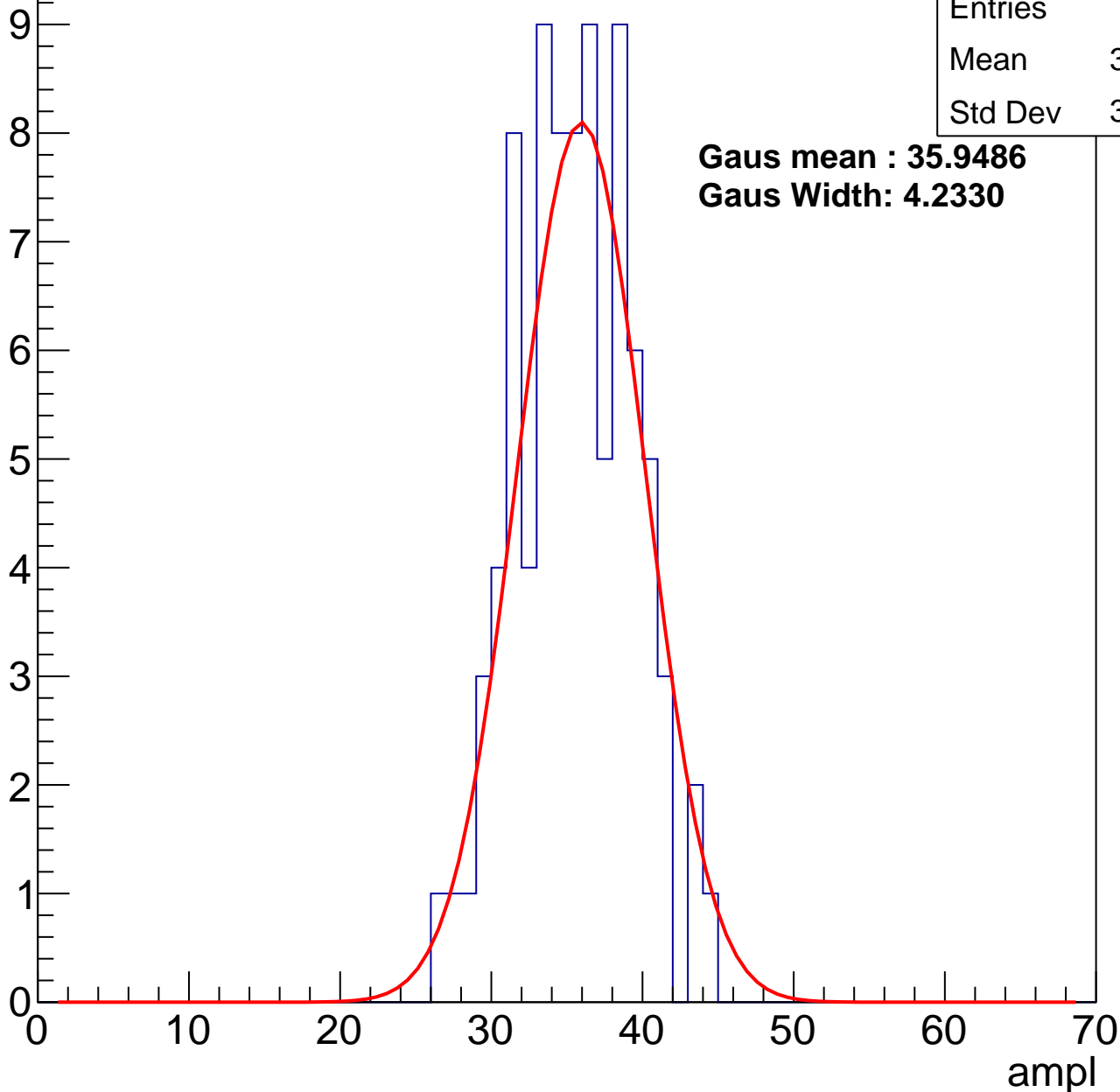
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	35.07
Std Dev	3.802

Gaus mean : 35.9486

Gaus Width: 4.2330



B1L102S, U17-ch2, adc2

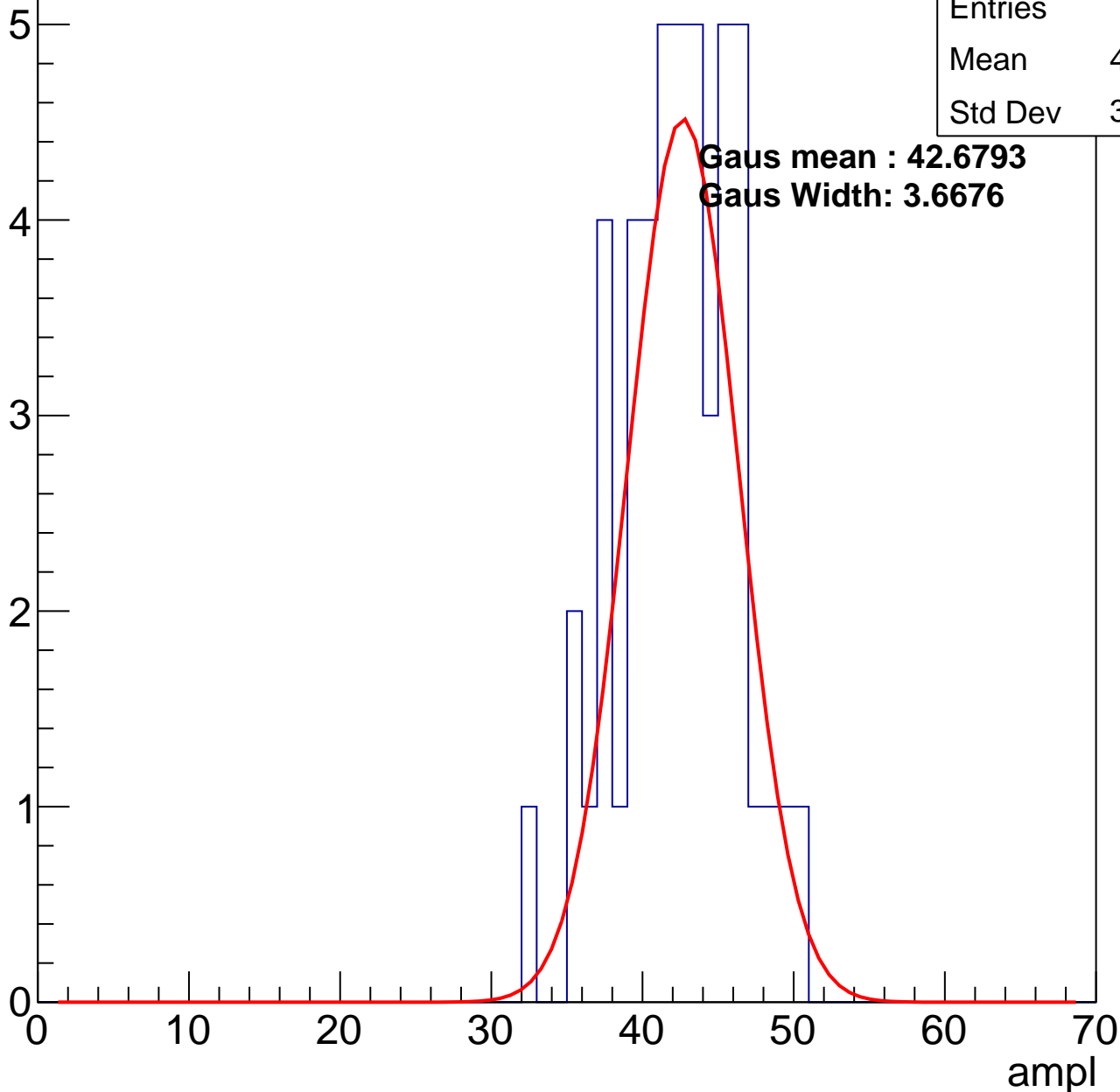
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.86
Std Dev	3.839

Gaus mean : 42.6793

Gaus Width: 3.6676

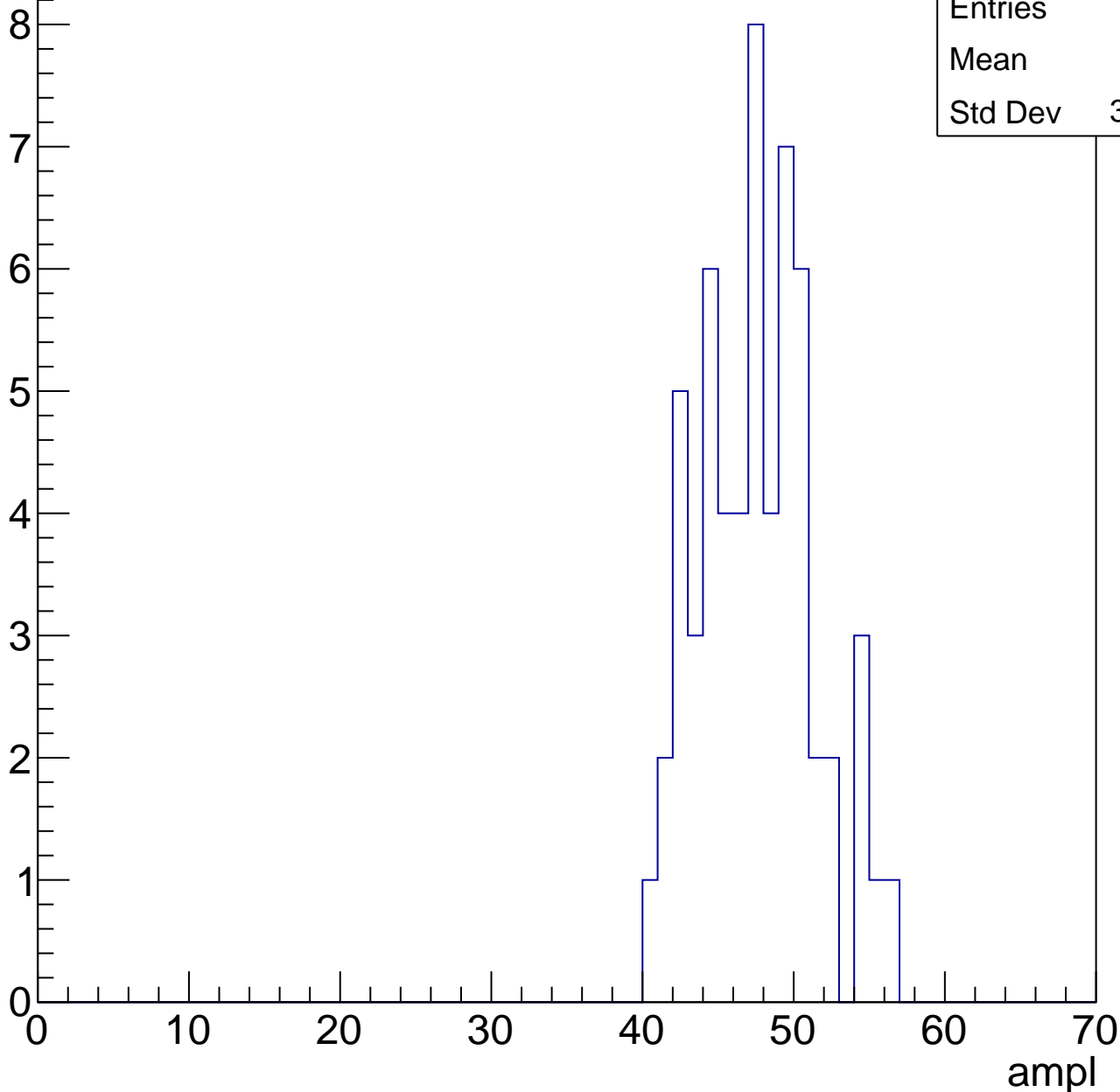


B1L102S, U17-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.1
Std Dev	3.745

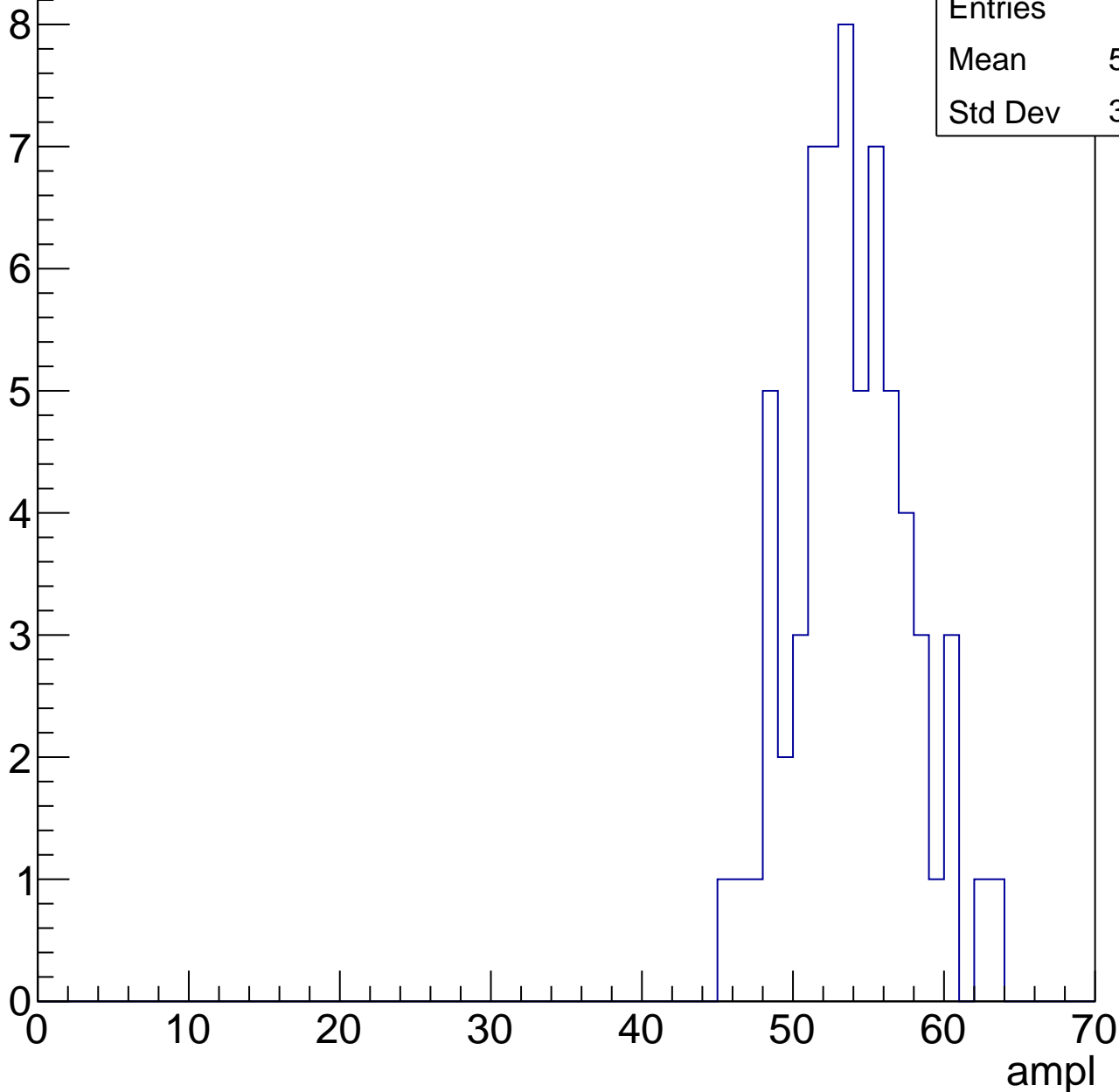


B1L102S, U17-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	53.42
Std Dev	3.798

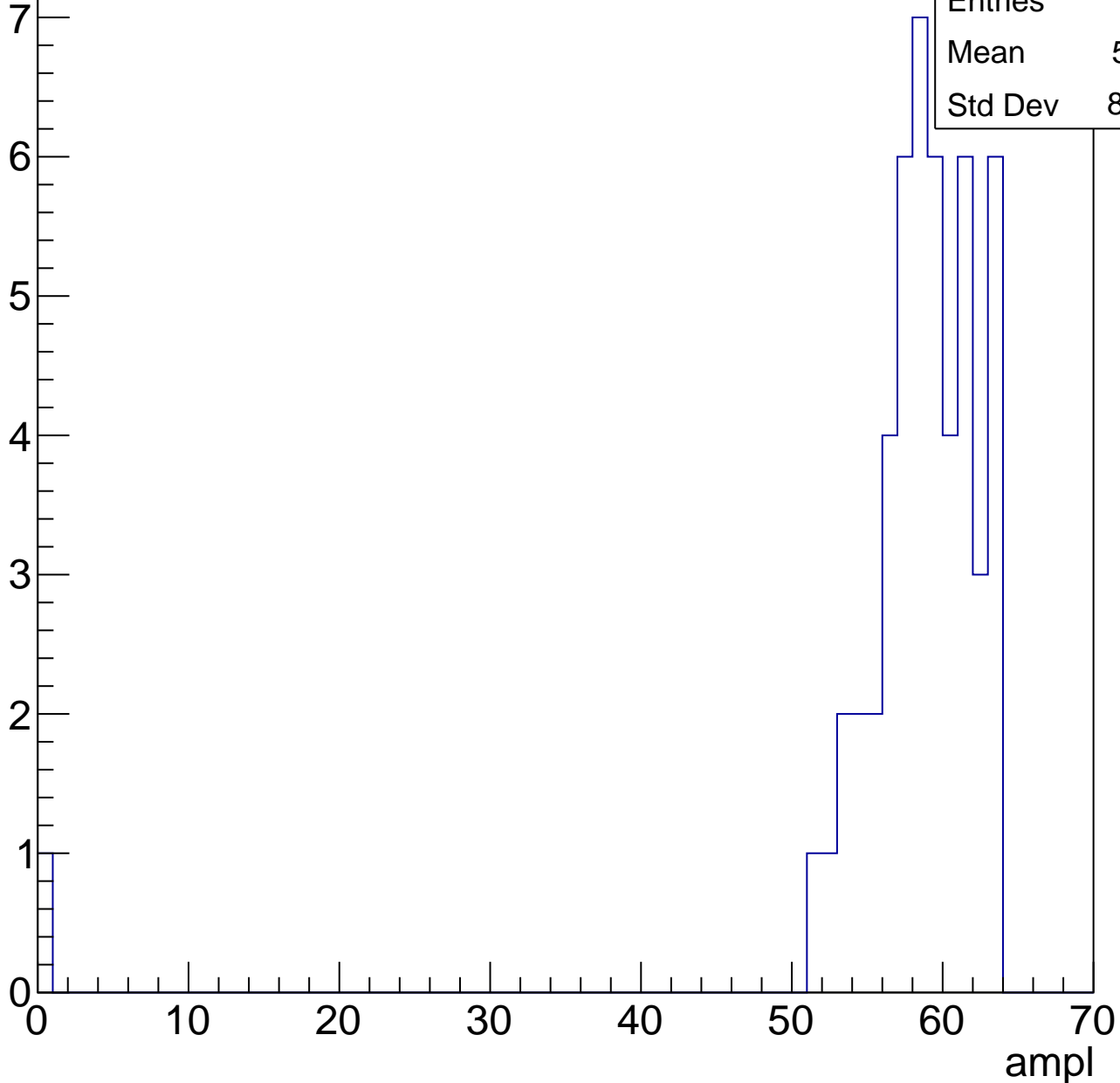


B1L102S, U17-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.31
Std Dev	8.656

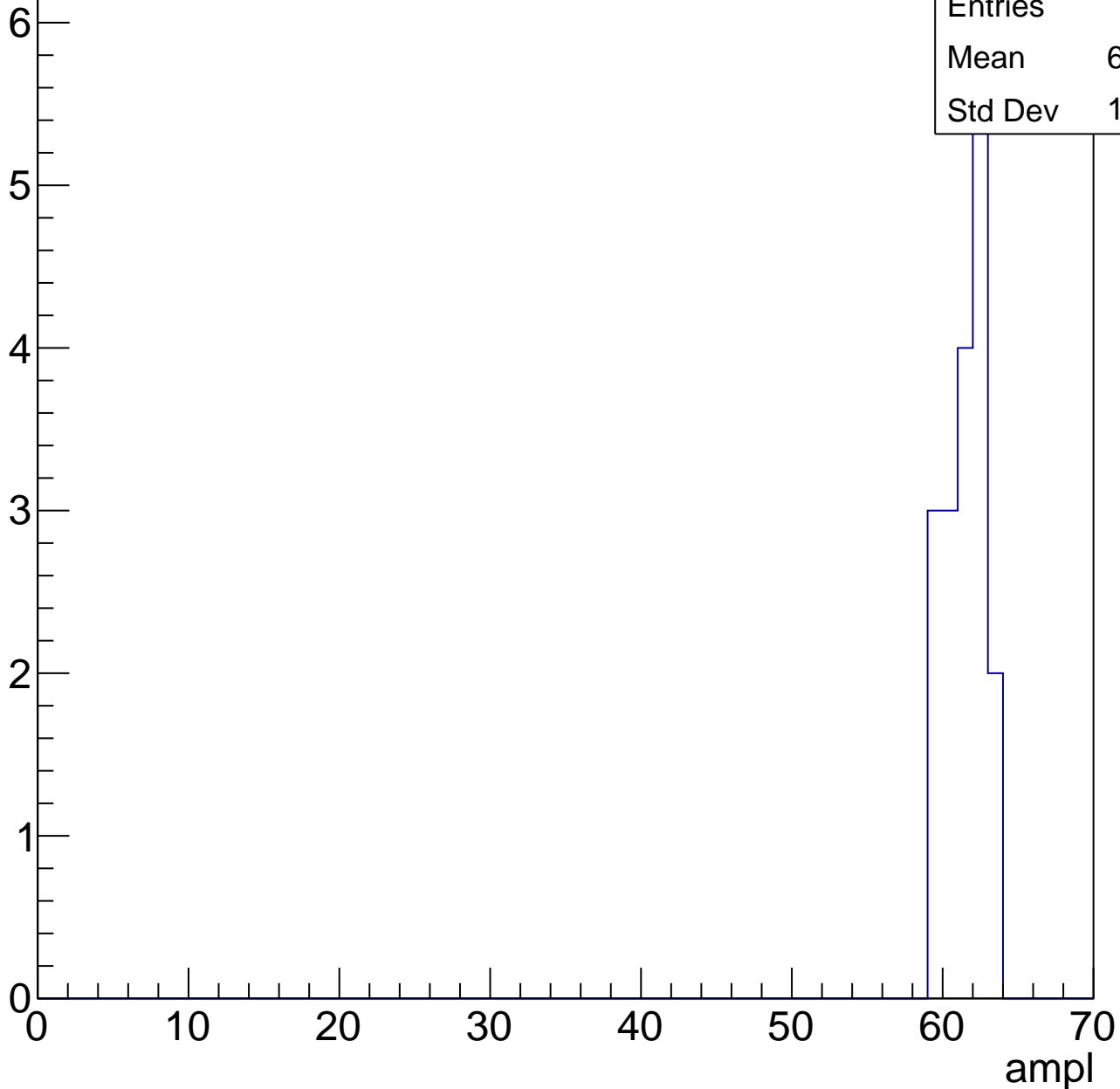


B1L102S, U17-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	61.06
Std Dev	1.268



B1L102S, U17-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L102S, U17-ch3, adc0

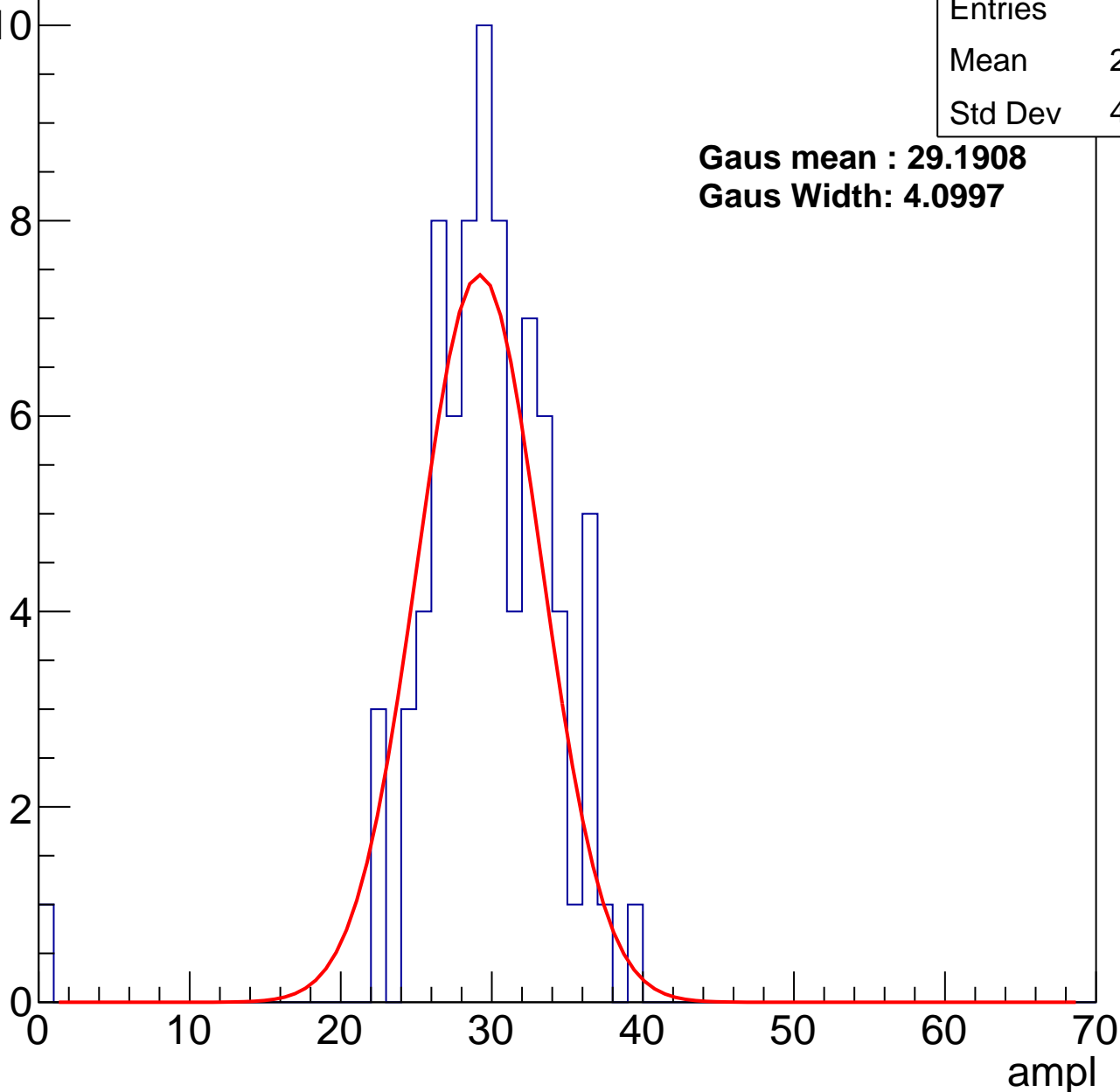
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.19
Std Dev	4.945

Gaus mean : 29.1908

Gaus Width: 4.0997



B1L102S, U17-ch3, adc1

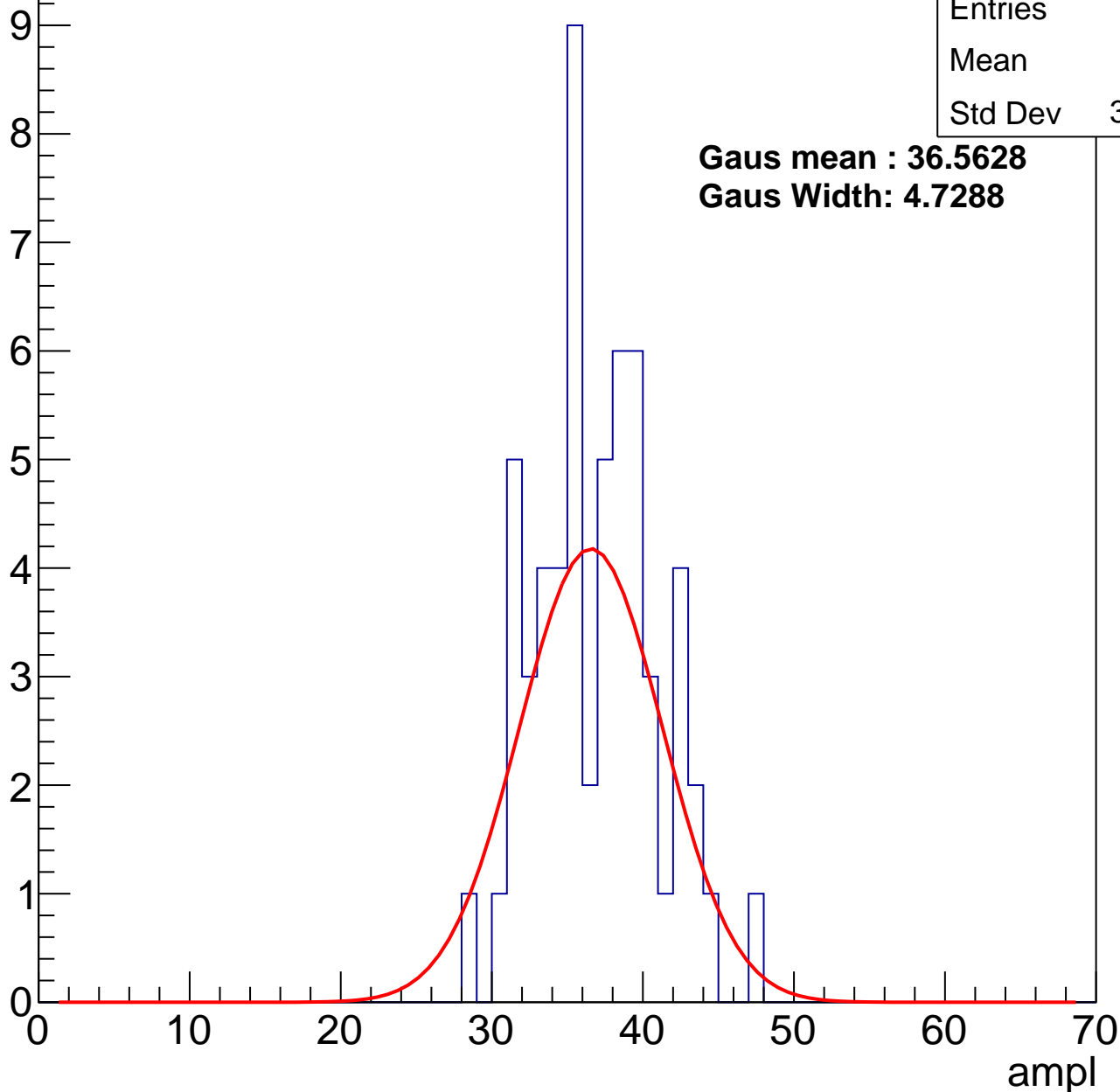
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.5
Std Dev	3.945

Gaus mean : 36.5628

Gaus Width: 4.7288



B1L102S, U17-ch3, adc2

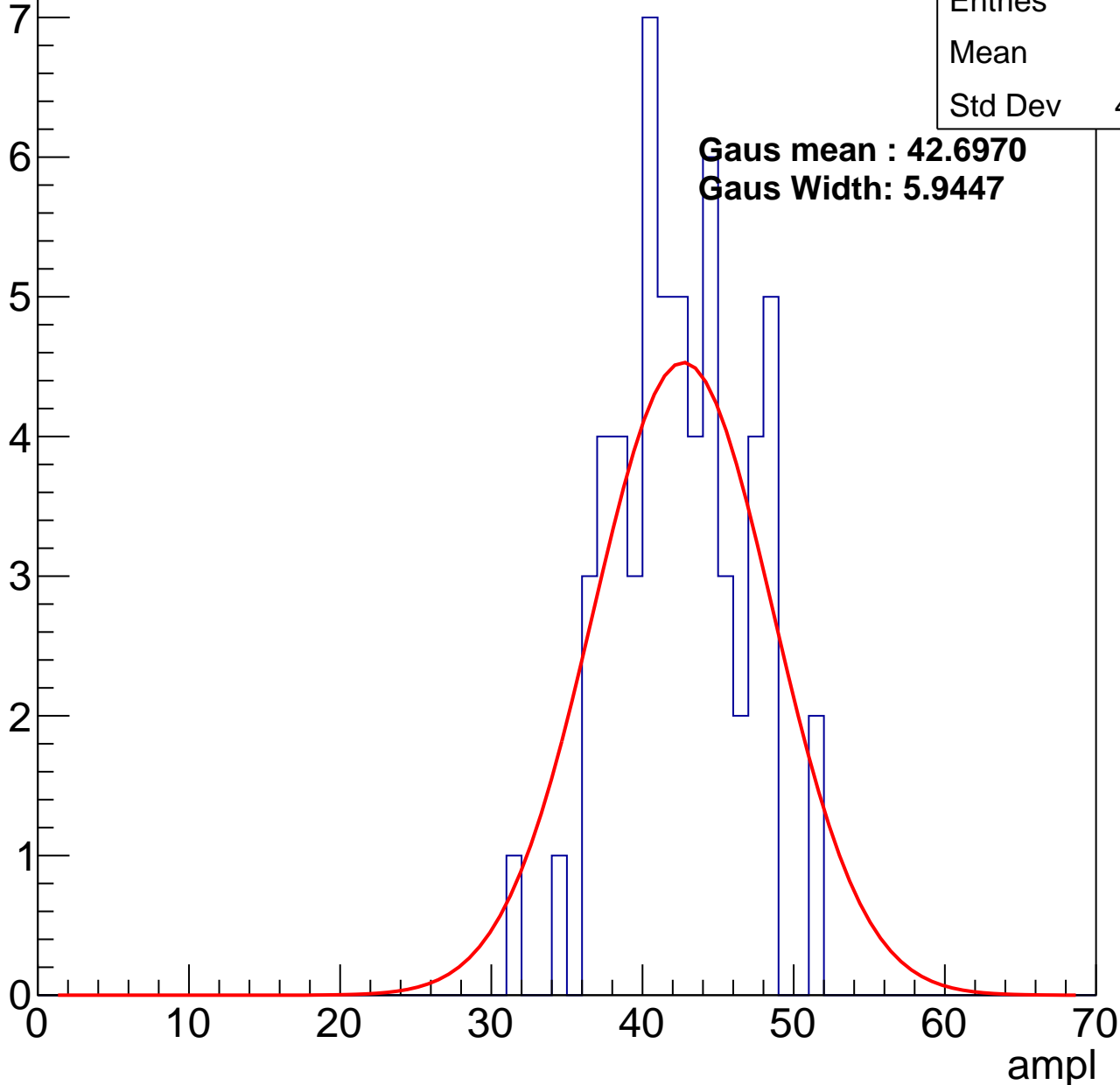
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42
Std Dev	4.211

Gaus mean : 42.6970

Gaus Width: 5.9447

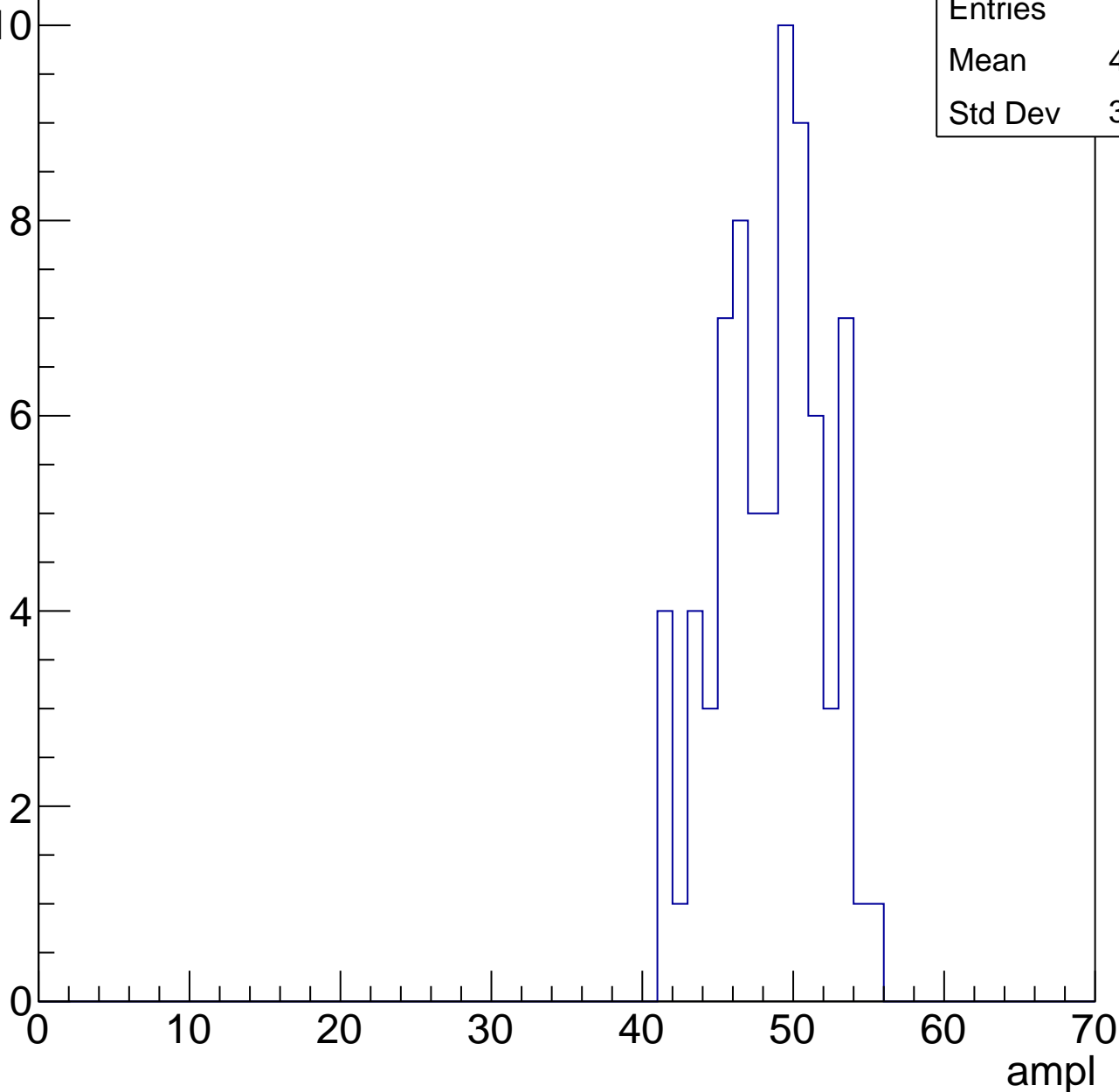


B1L102S, U17-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

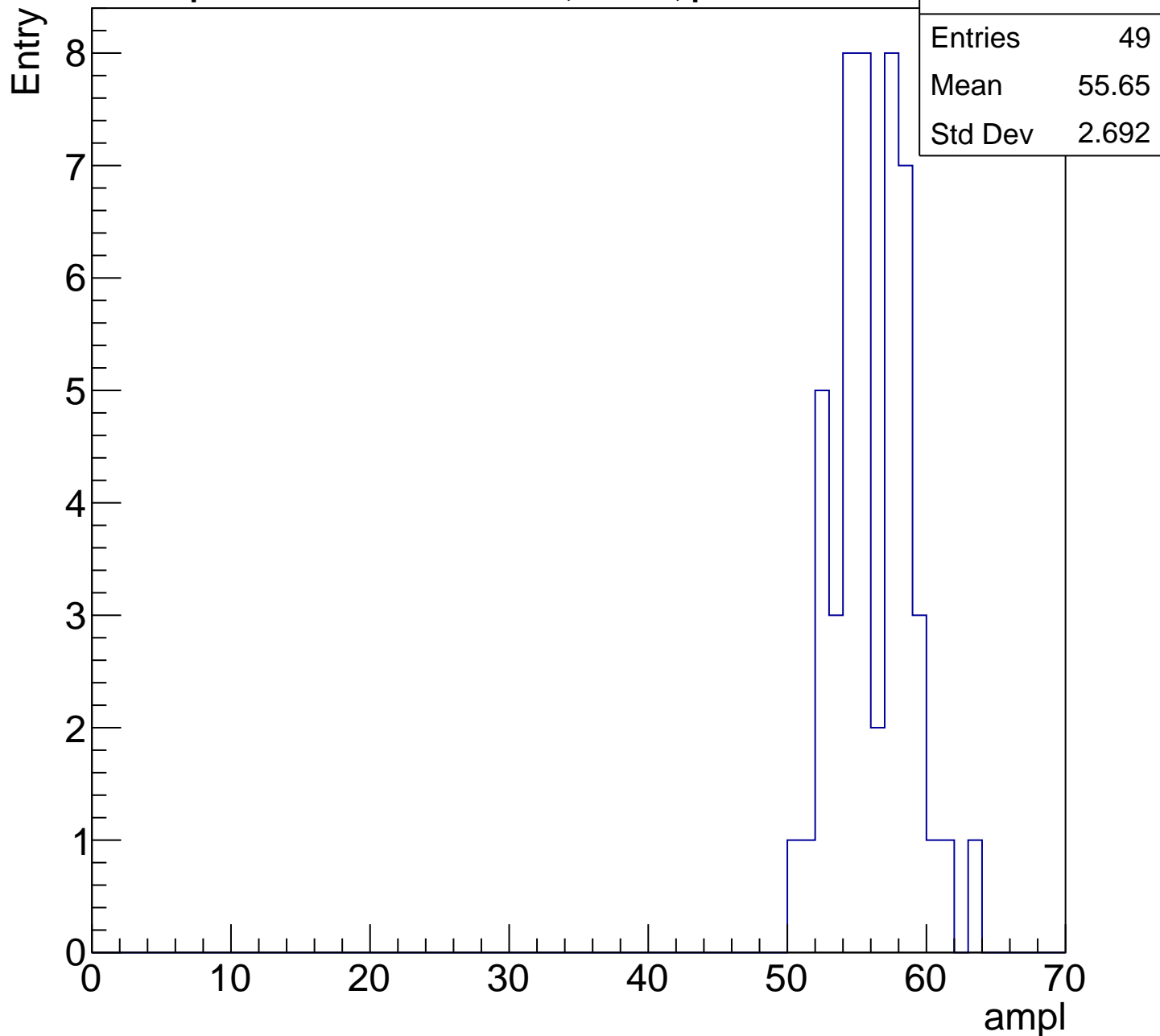
Entry

Entries	74
Mean	47.97
Std Dev	3.464



B1L102S, U17-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

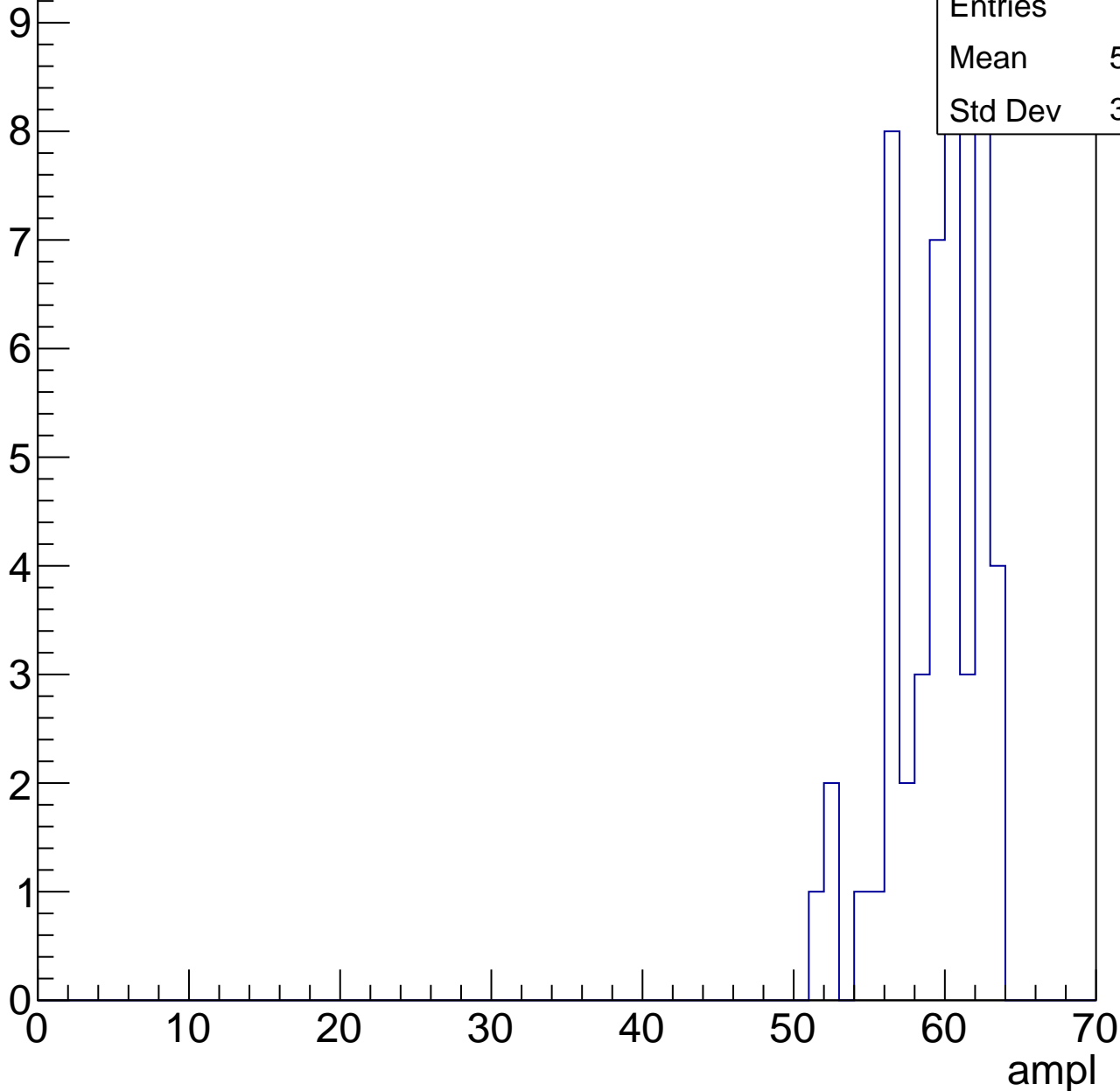


B1L102S, U17-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.86
Std Dev	3.003

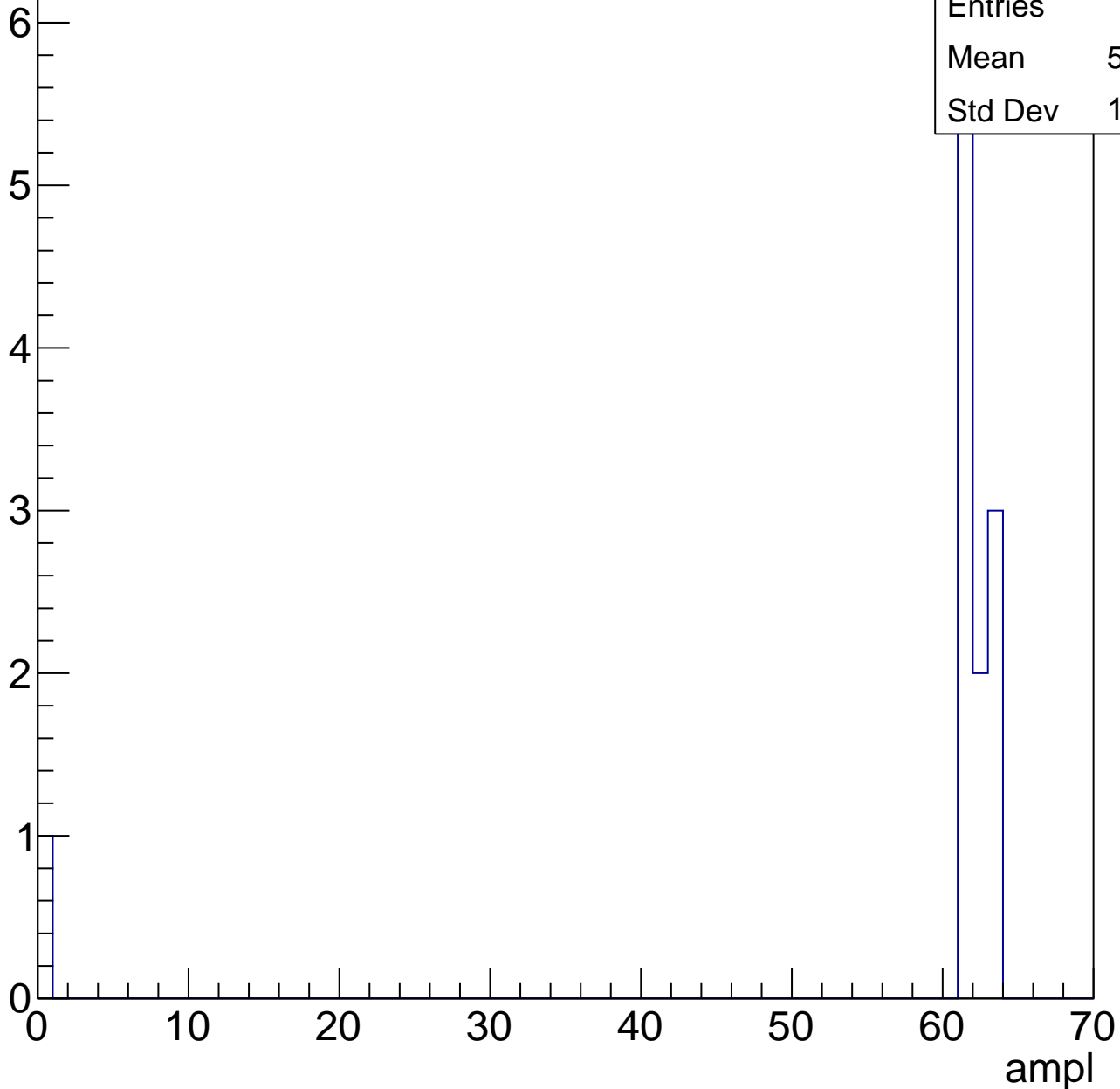


B1L102S, U17-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	56.58
Std Dev	17.08



B1L102S, U17-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch4, adc0

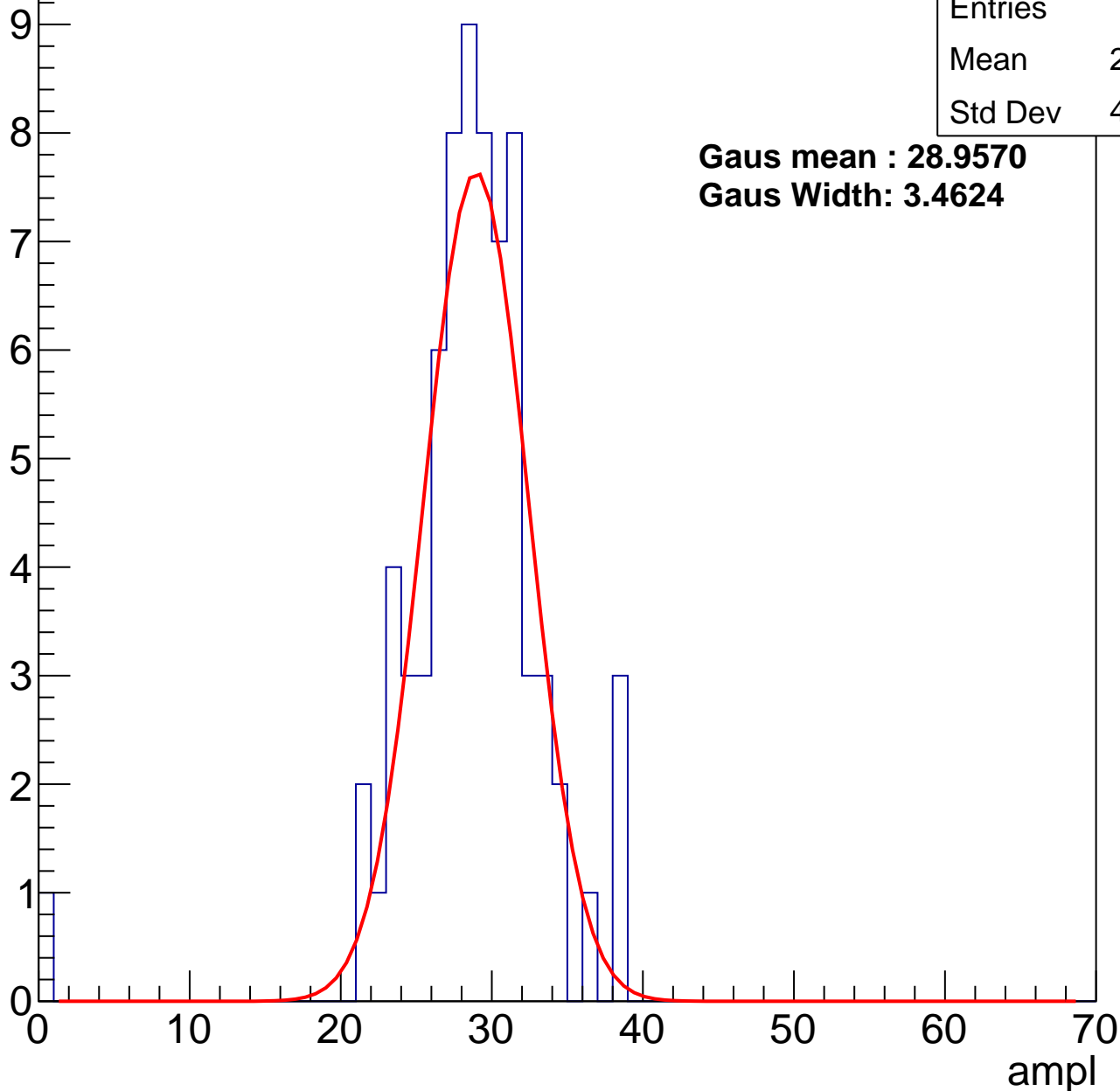
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.19
Std Dev	4.985

Gaus mean : 28.9570

Gaus Width: 3.4624



B1L102S, U17-ch4, adc1

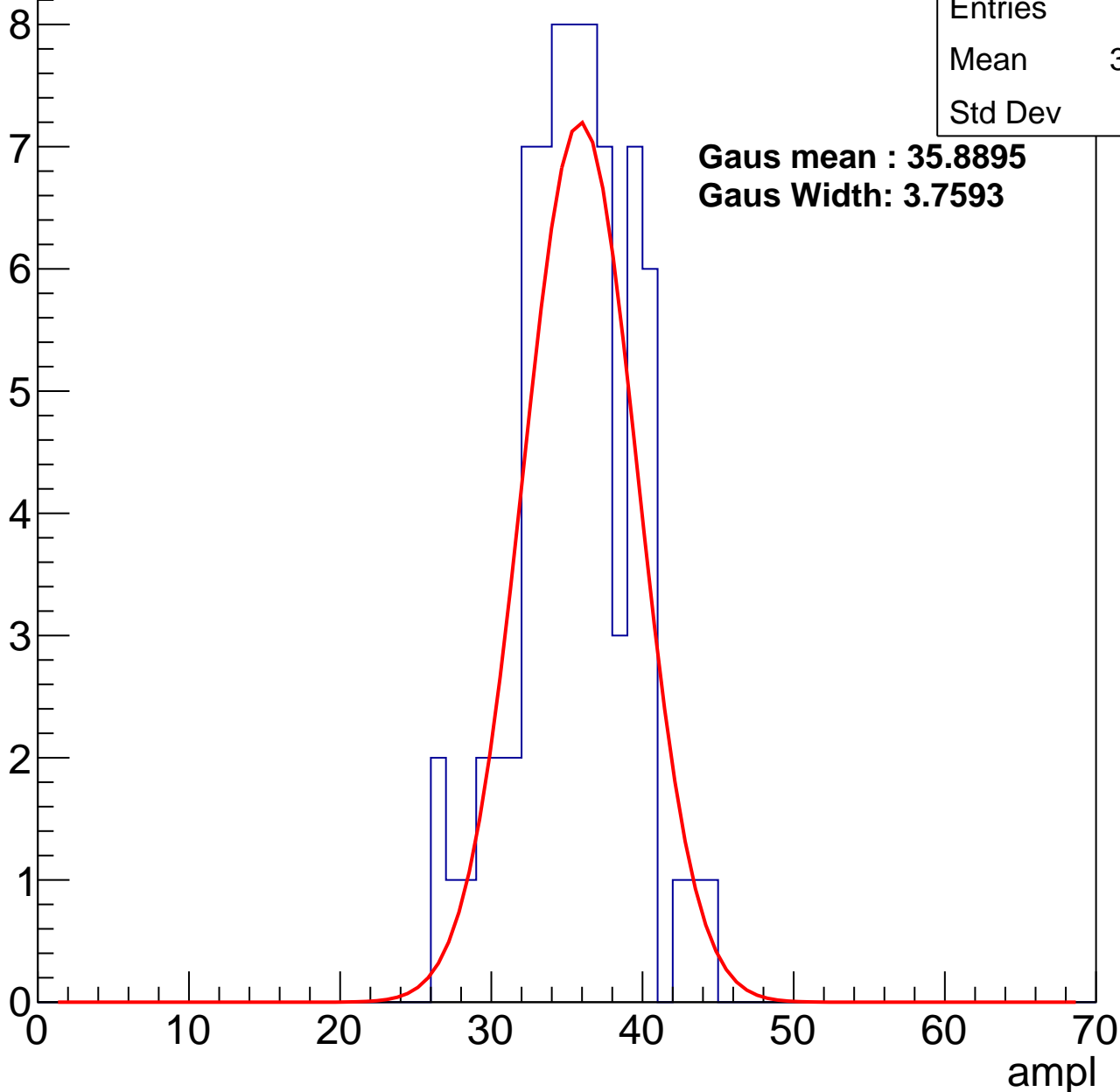
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.09
Std Dev	3.76

Gaus mean : 35.8895

Gaus Width: 3.7593



B1L102S, U17-ch4, adc2

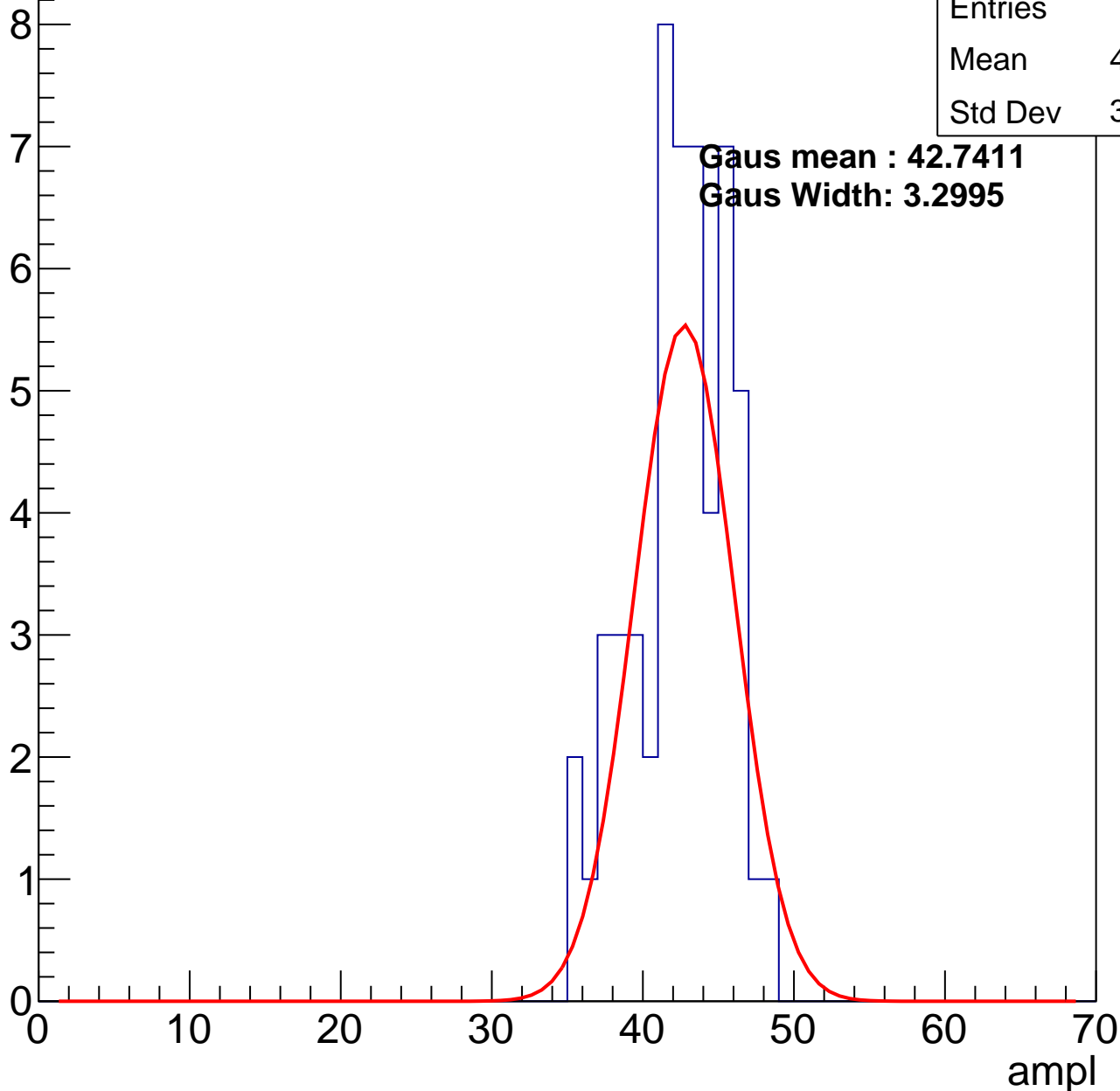
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.98
Std Dev	3.124

Gaus mean : 42.7411

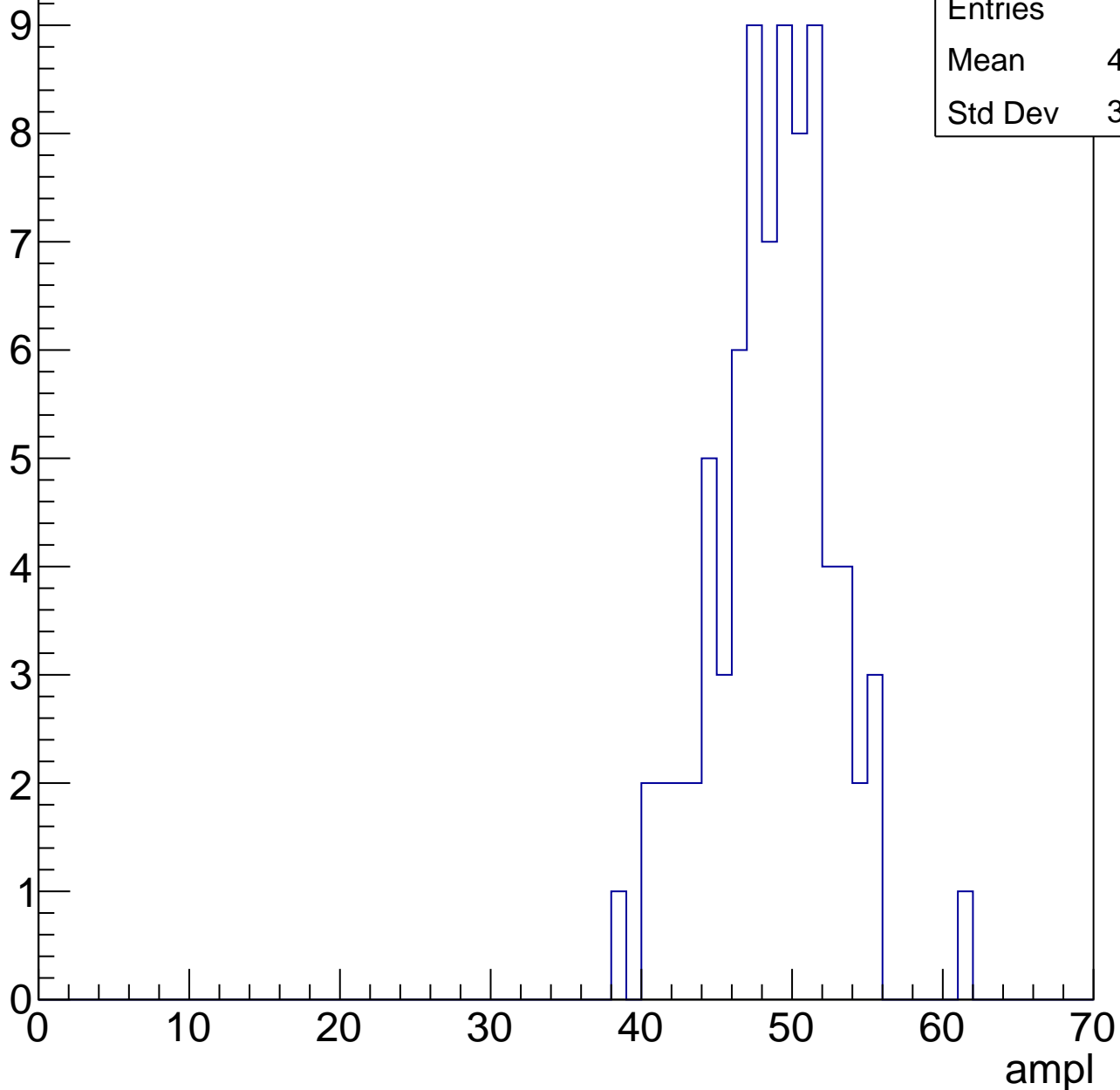
Gaus Width: 3.2995



B1L102S, U17-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

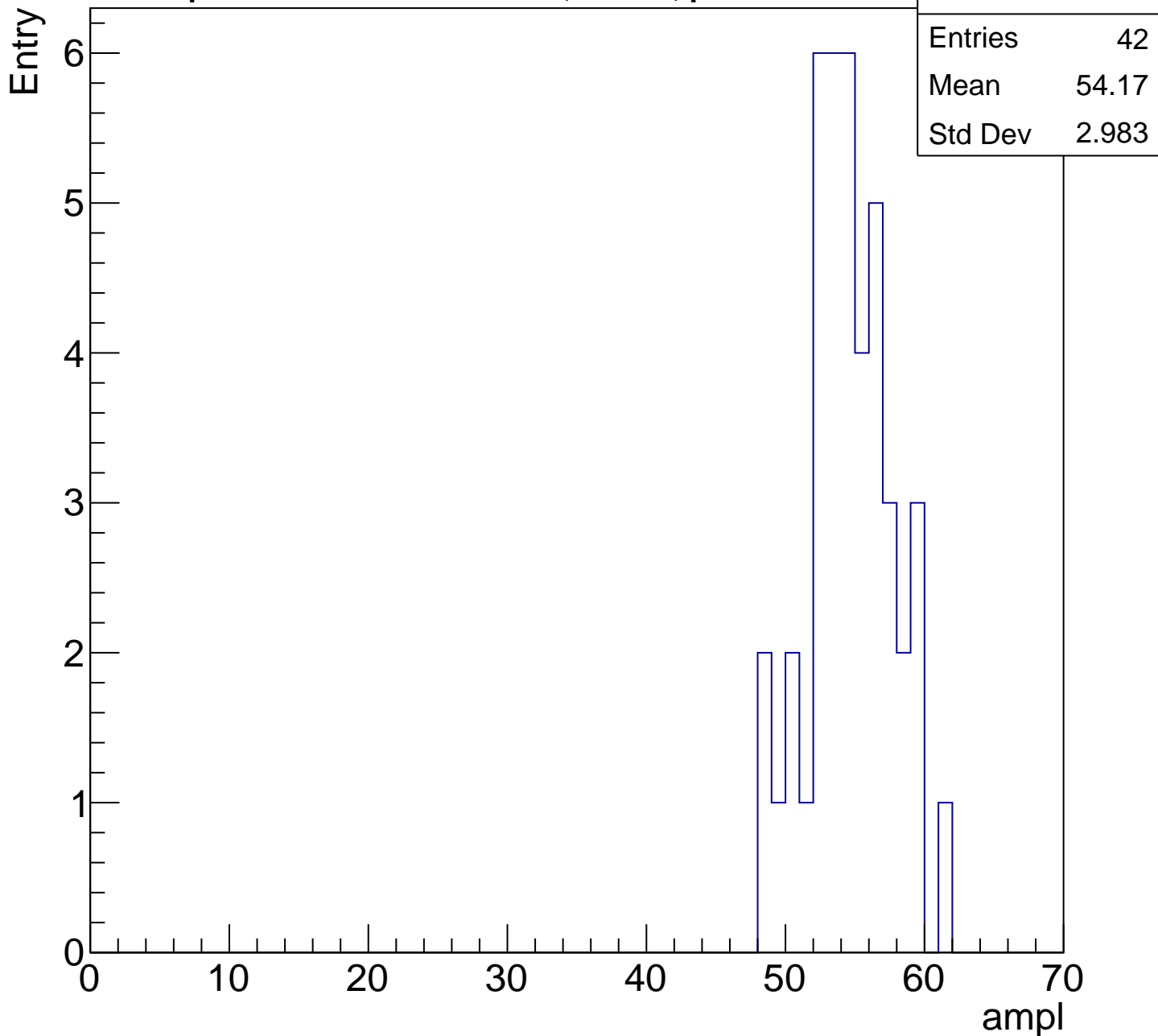
Entry



Entries	79
Mean	48.28
Std Dev	3.987

B1L102S, U17-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

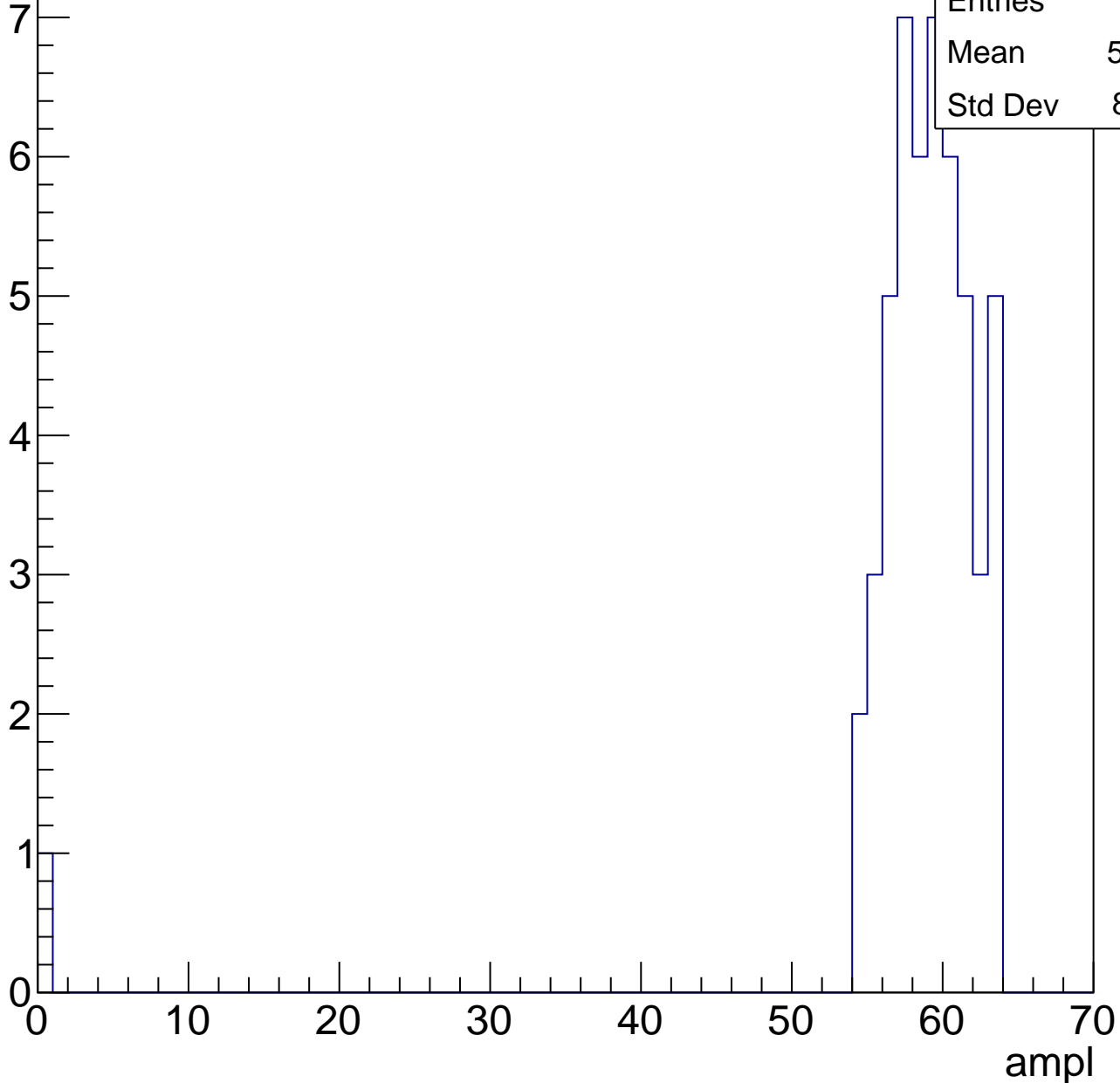


B1L102S, U17-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.58
Std Dev	8.591

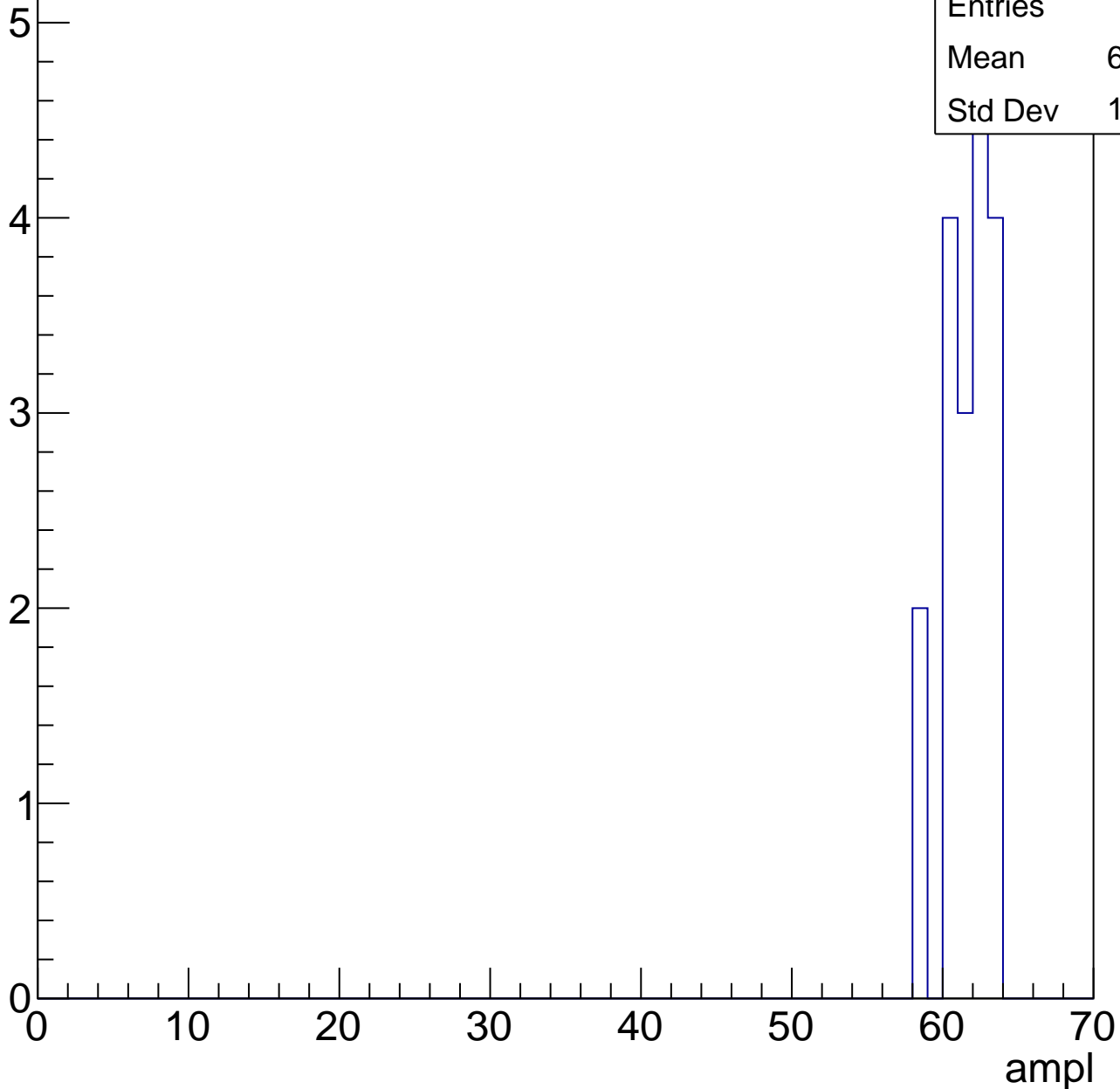


B1L102S, U17-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

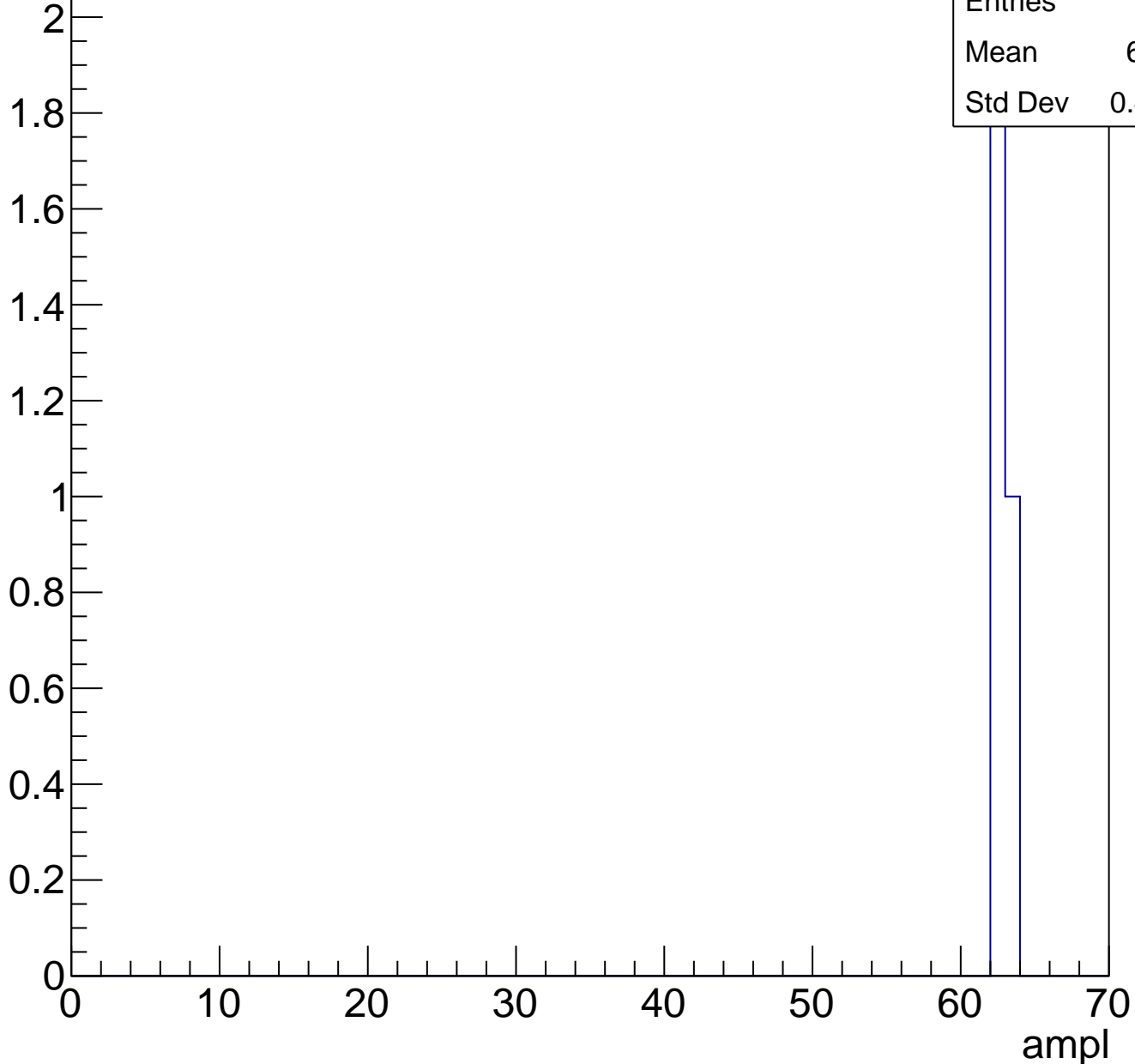
Entries	18
Mean	61.17
Std Dev	1.537



B1L102S, U17-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch5, adc0

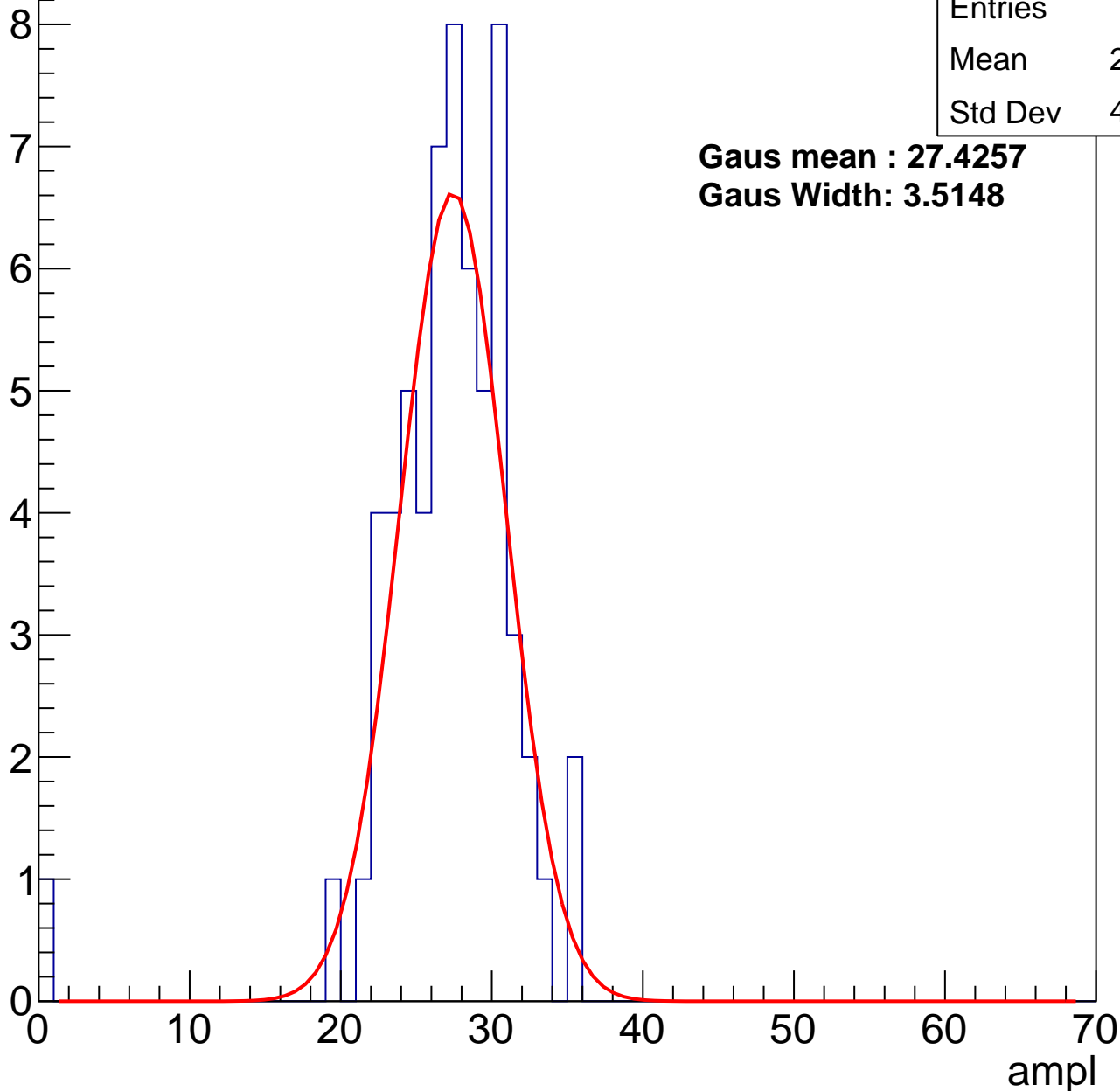
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	26.63
Std Dev	4.773

Gaus mean : 27.4257

Gaus Width: 3.5148



B1L102S, U17-ch5, adc1

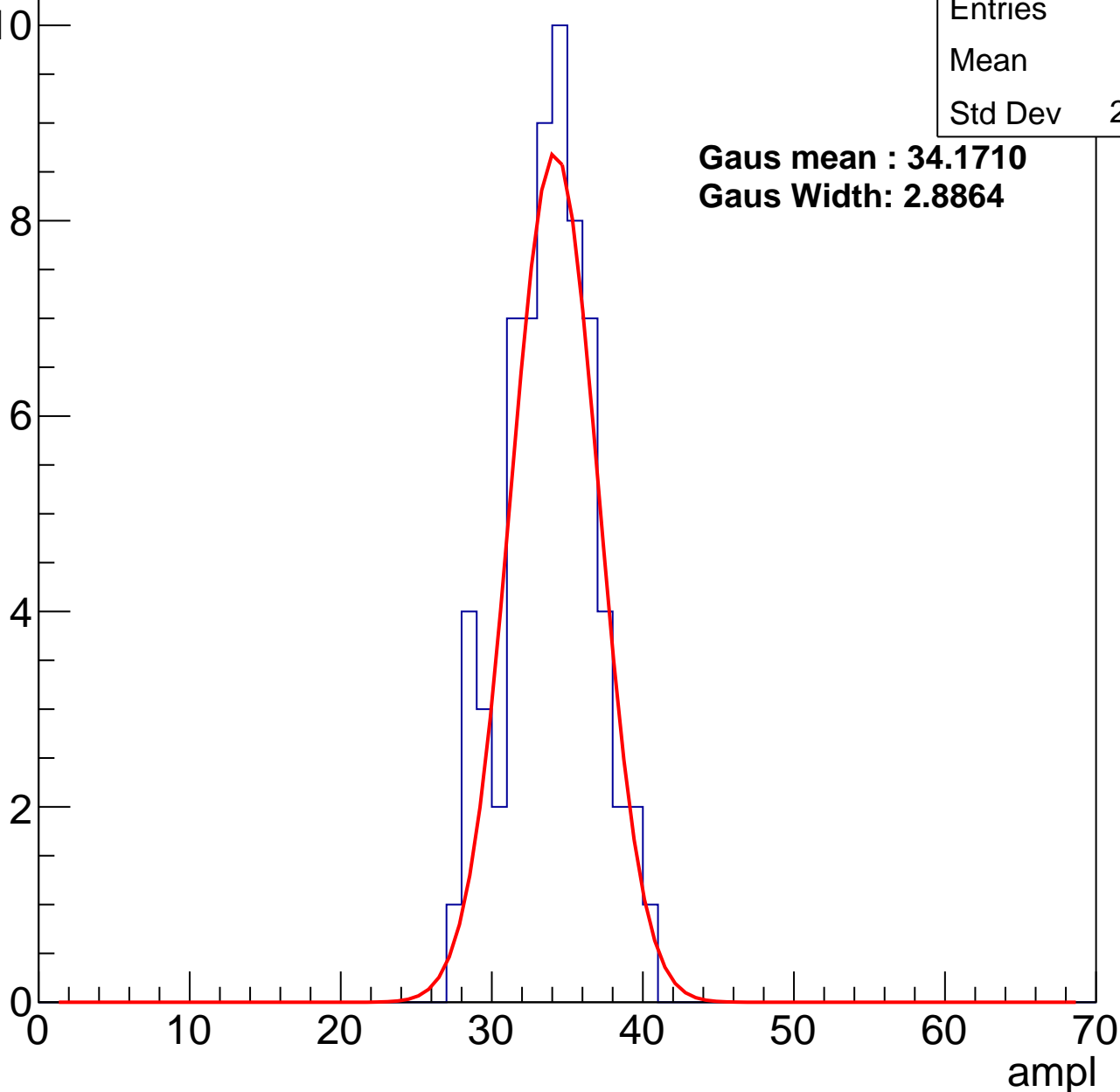
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	33.4
Std Dev	2.907

Gaus mean : 34.1710

Gaus Width: 2.8864



B1L102S, U17-ch5, adc2

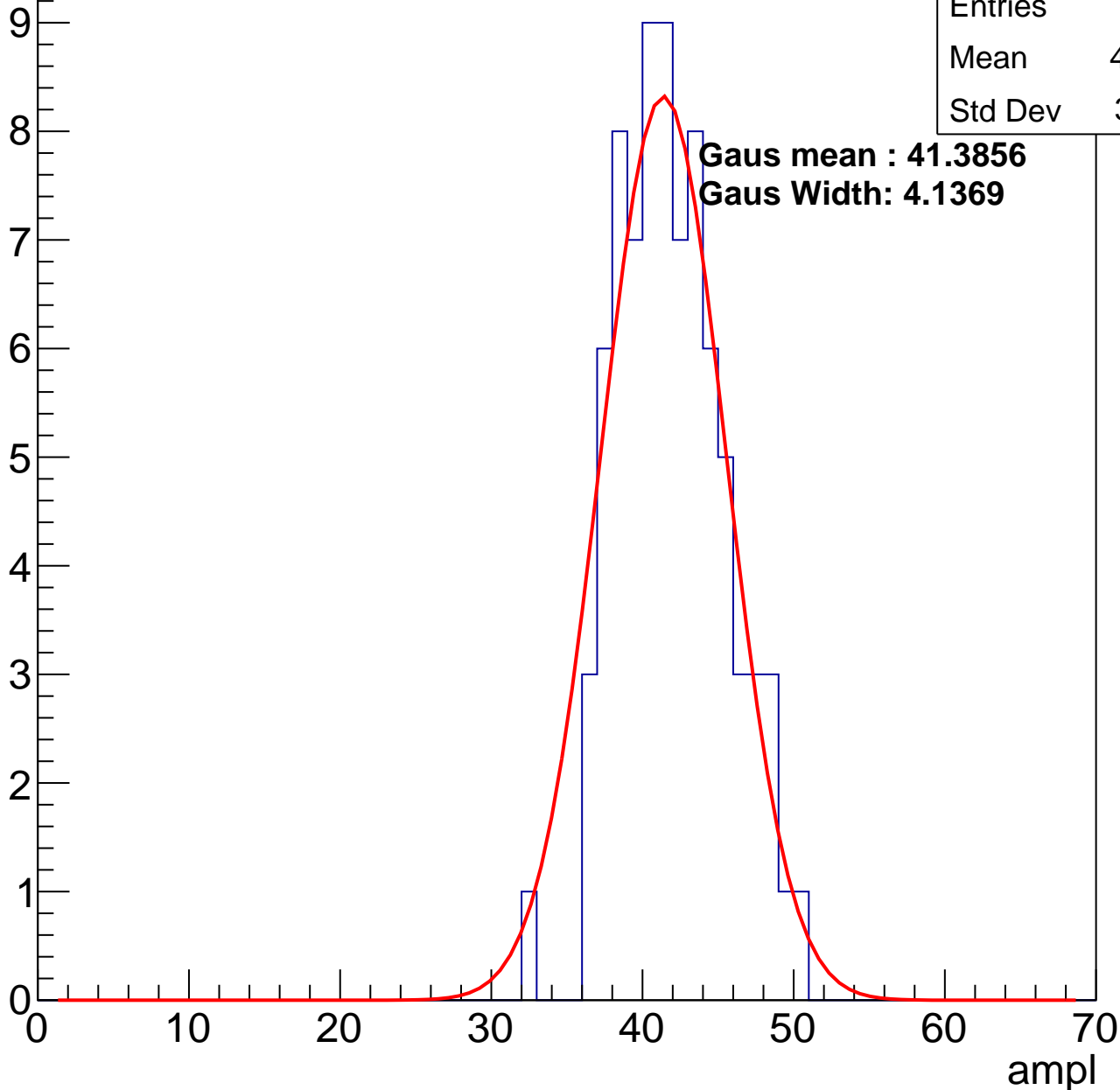
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.46
Std Dev	3.521

Gaus mean : 41.3856

Gaus Width: 4.1369

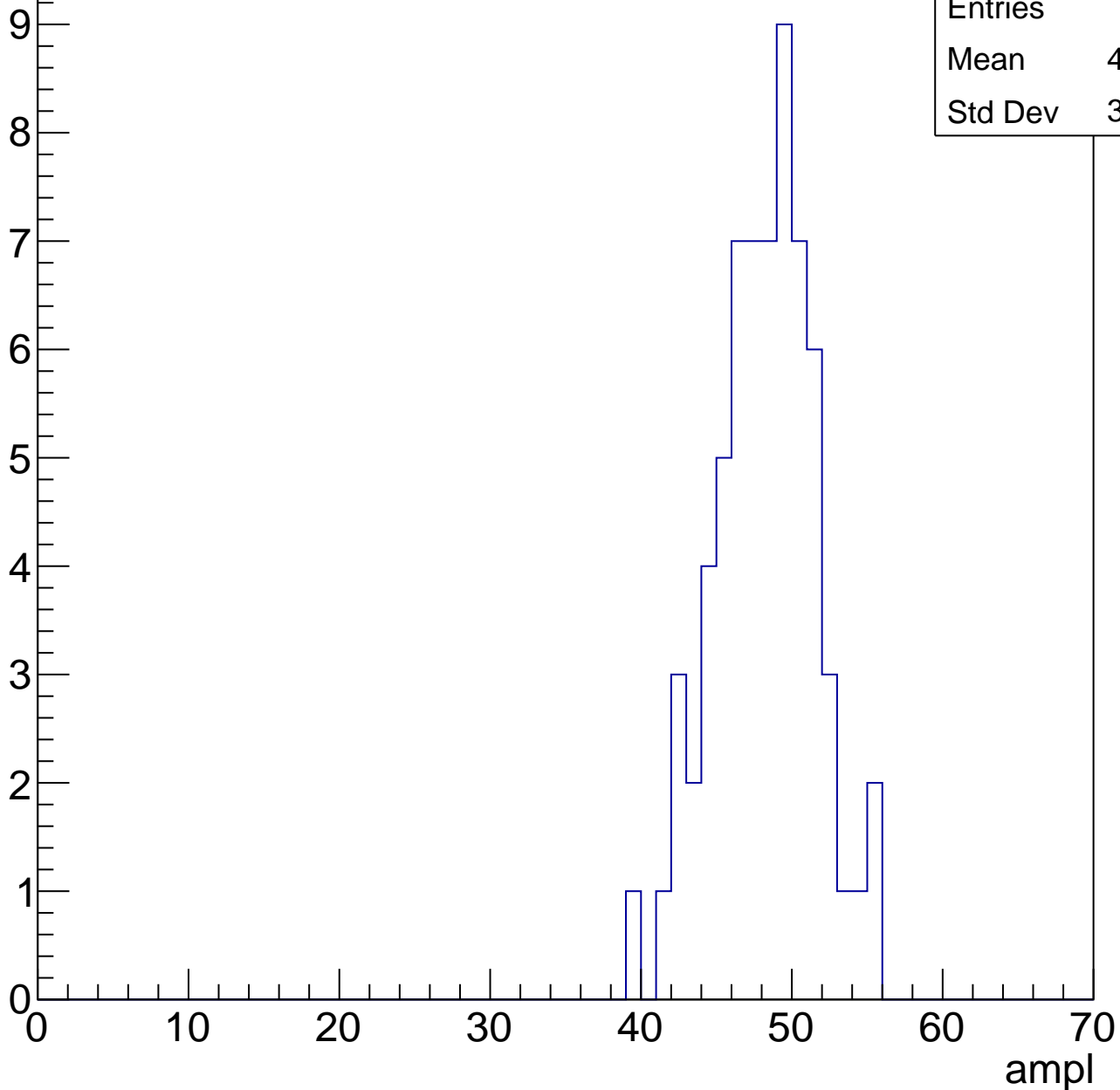


B1L102S, U17-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

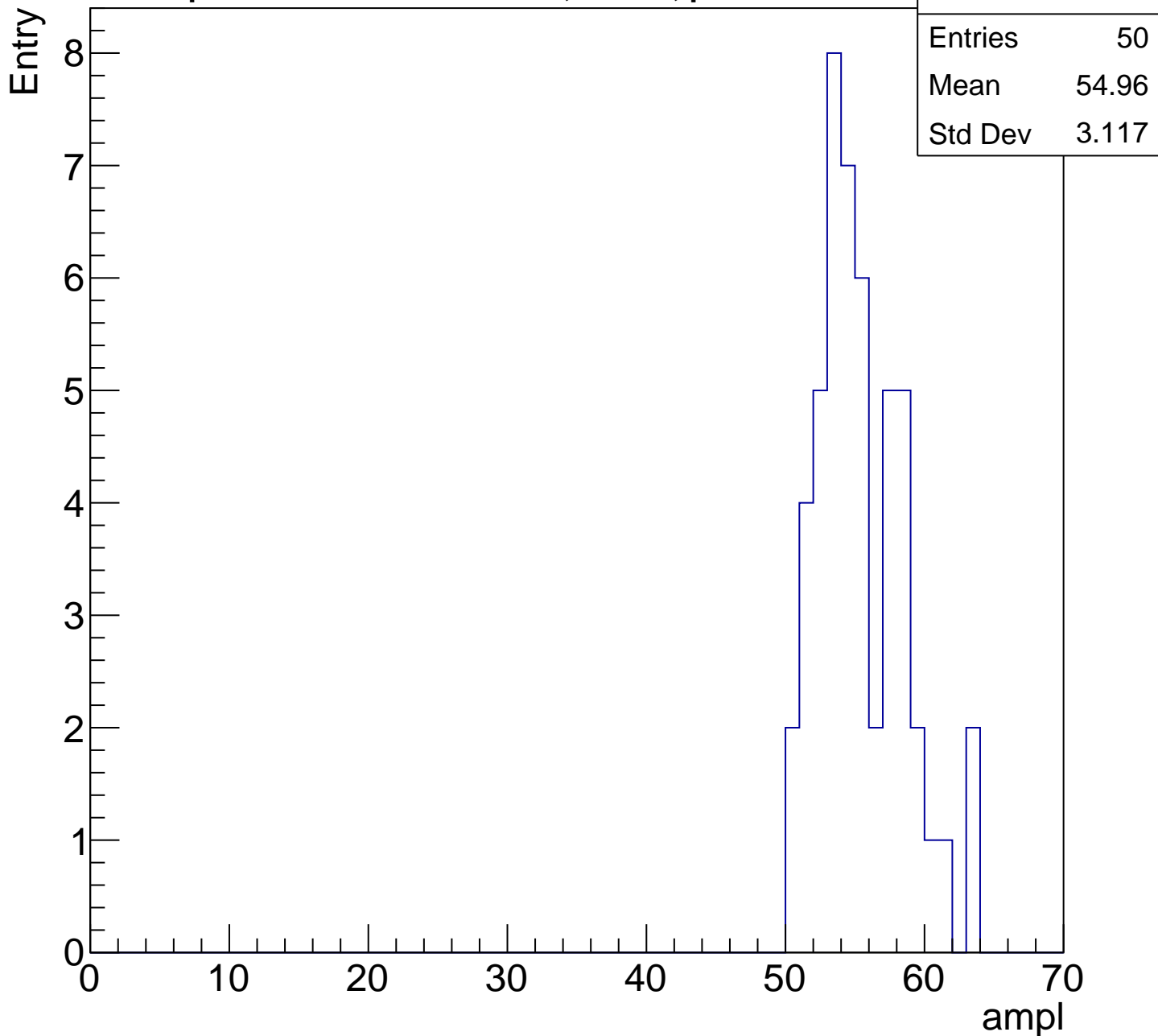
Entry

Entries	66
Mean	47.73
Std Dev	3.315



B1L102S, U17-ch5, adc4

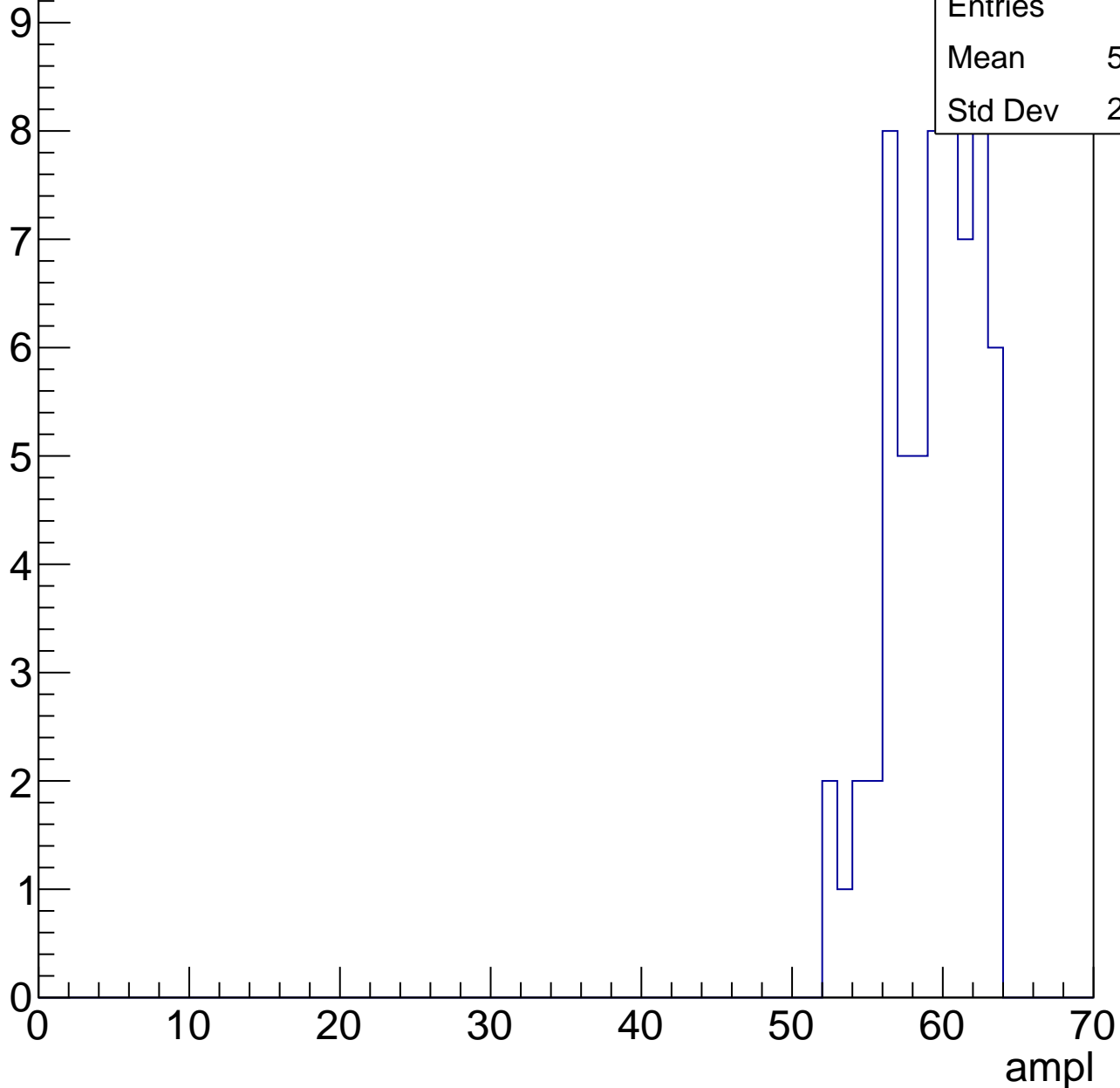
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U17-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

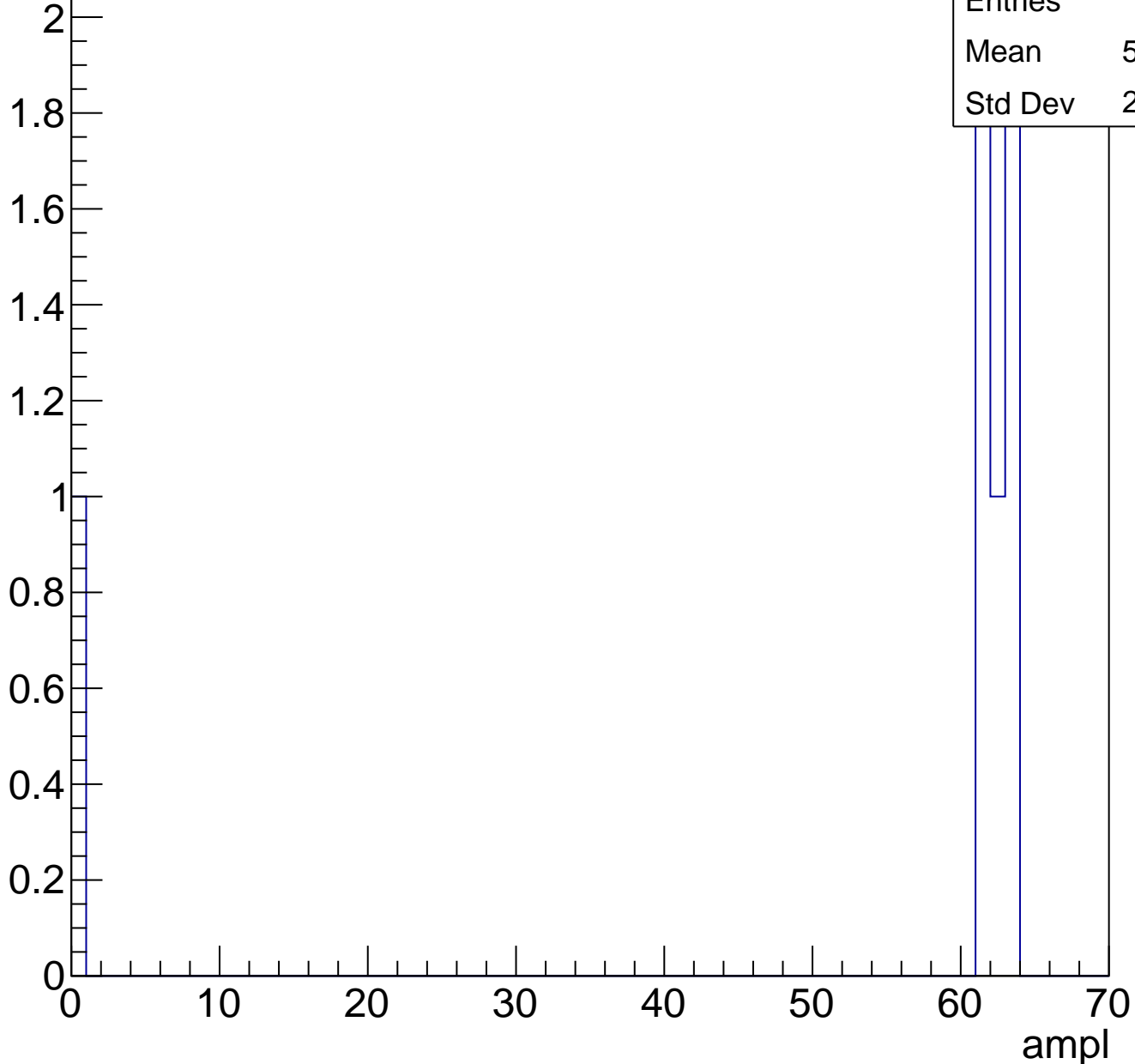


Entries	64
Mean	58.95
Std Dev	2.869

B1L102S, U17-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch6, adc0

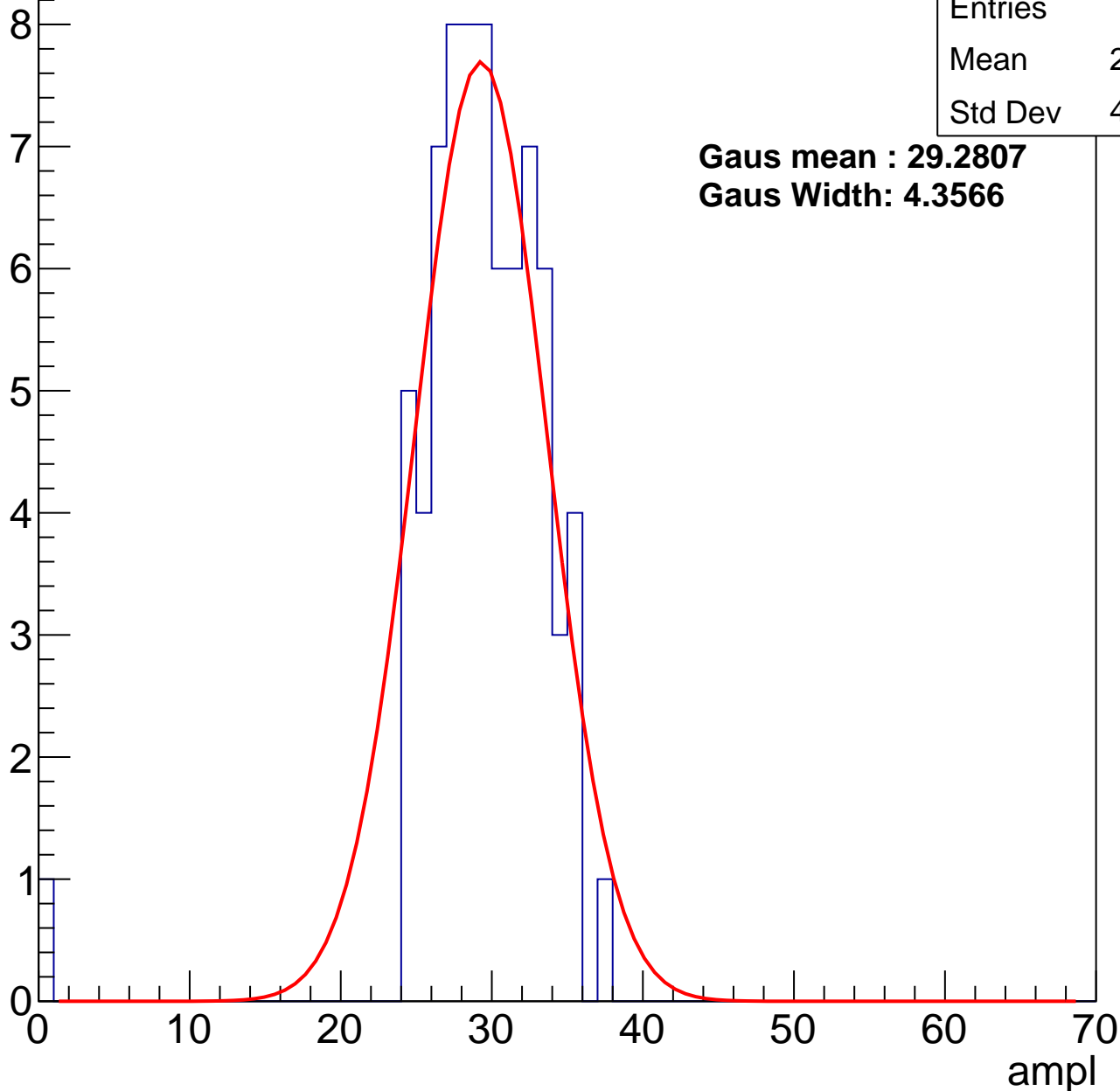
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.93
Std Dev	4.657

Gaus mean : 29.2807

Gaus Width: 4.3566



B1L102S, U17-ch6, adc1

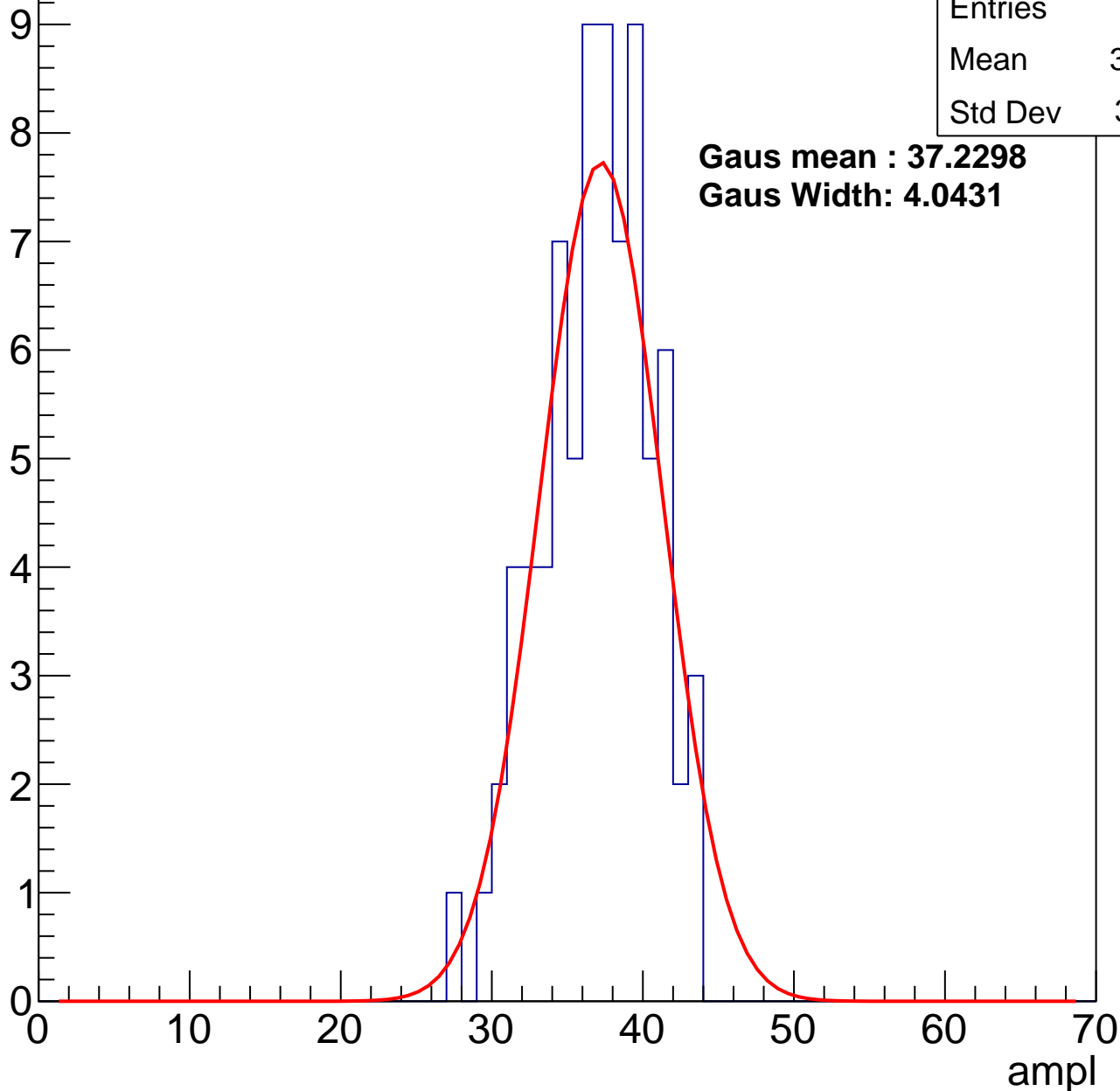
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.49
Std Dev	3.551

Gaus mean : 37.2298

Gaus Width: 4.0431



B1L102S, U17-ch6, adc2

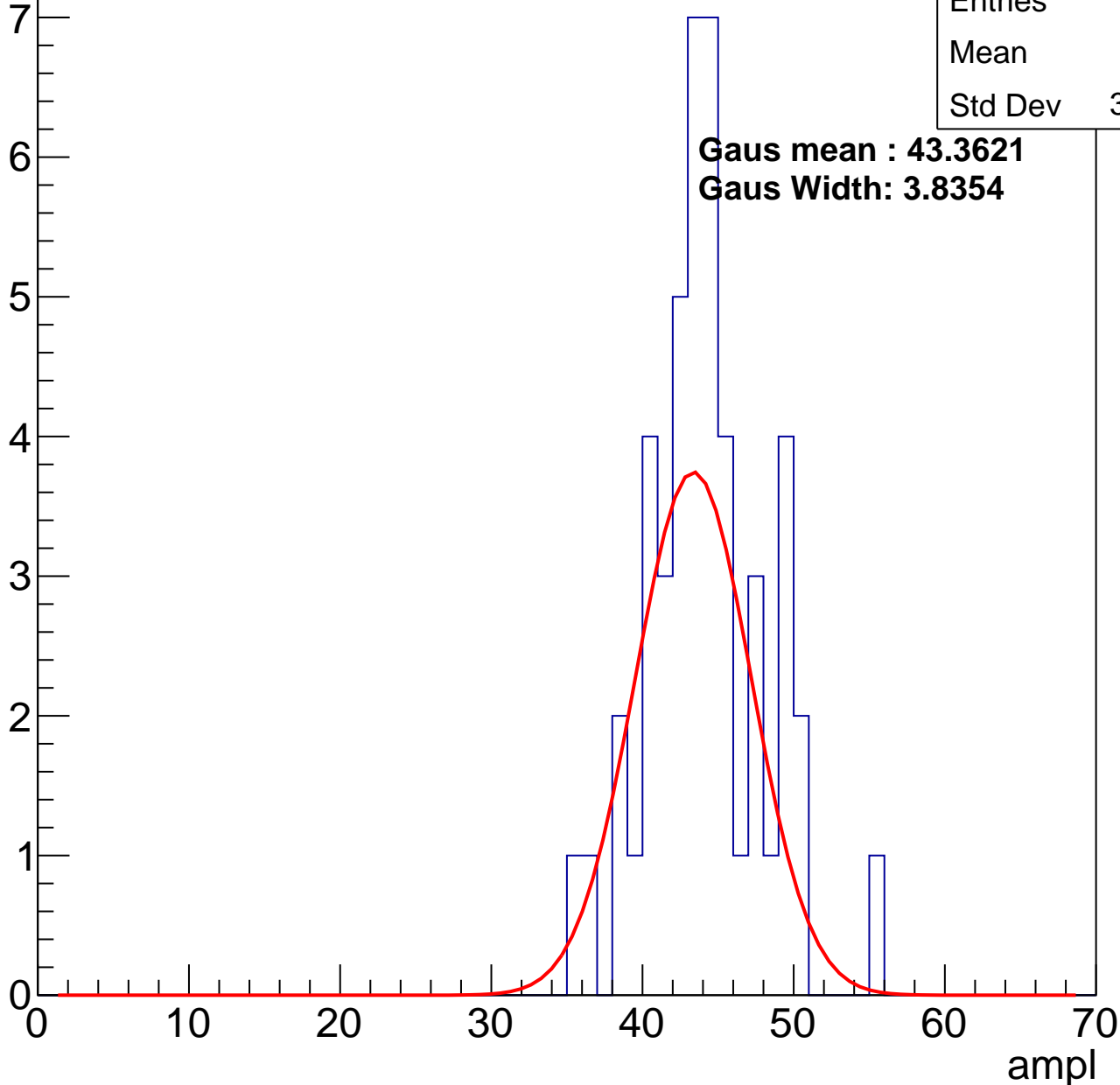
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	43.7
Std Dev	3.859

Gaus mean : 43.3621

Gaus Width: 3.8354

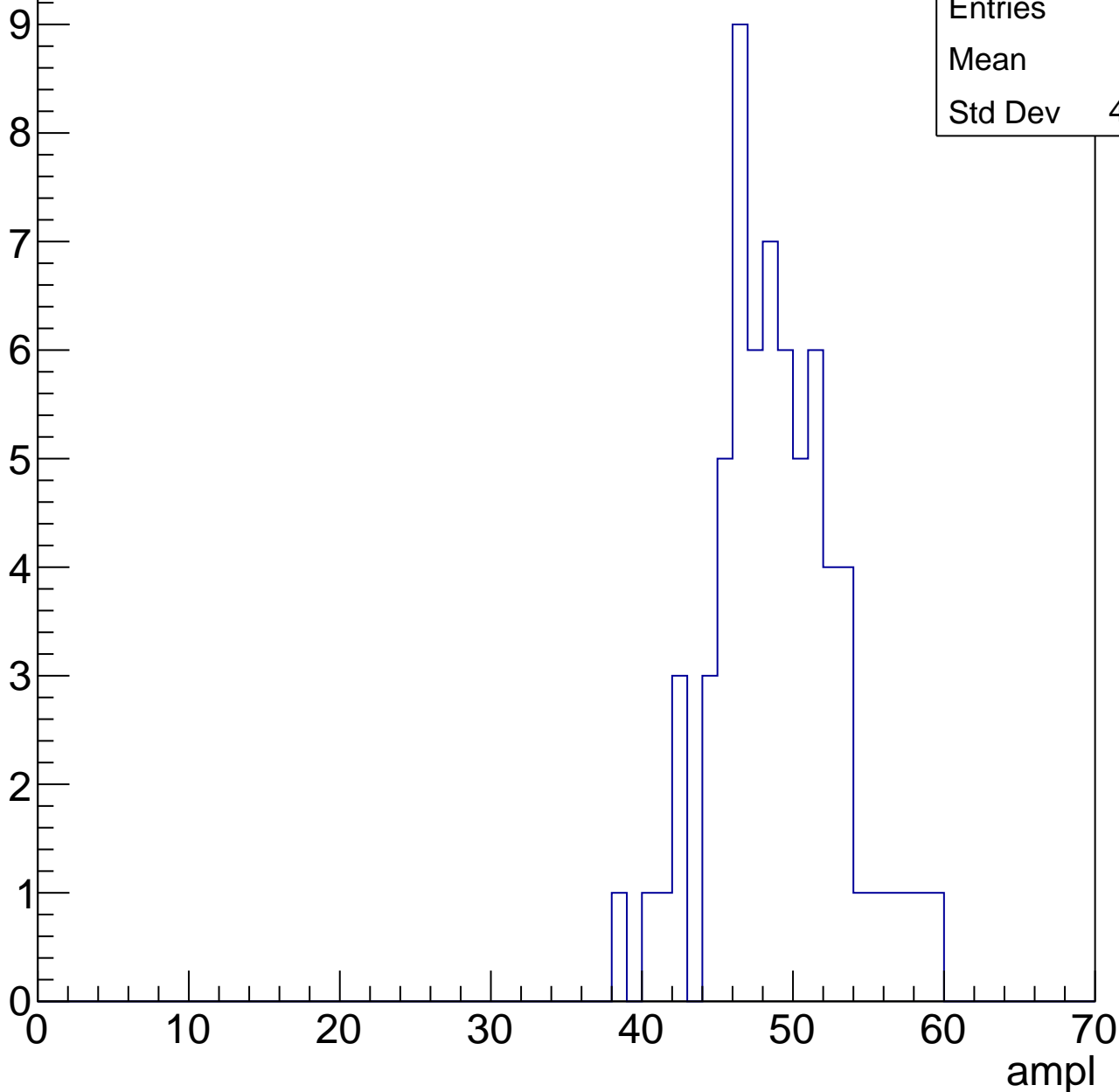


B1L102S, U17-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

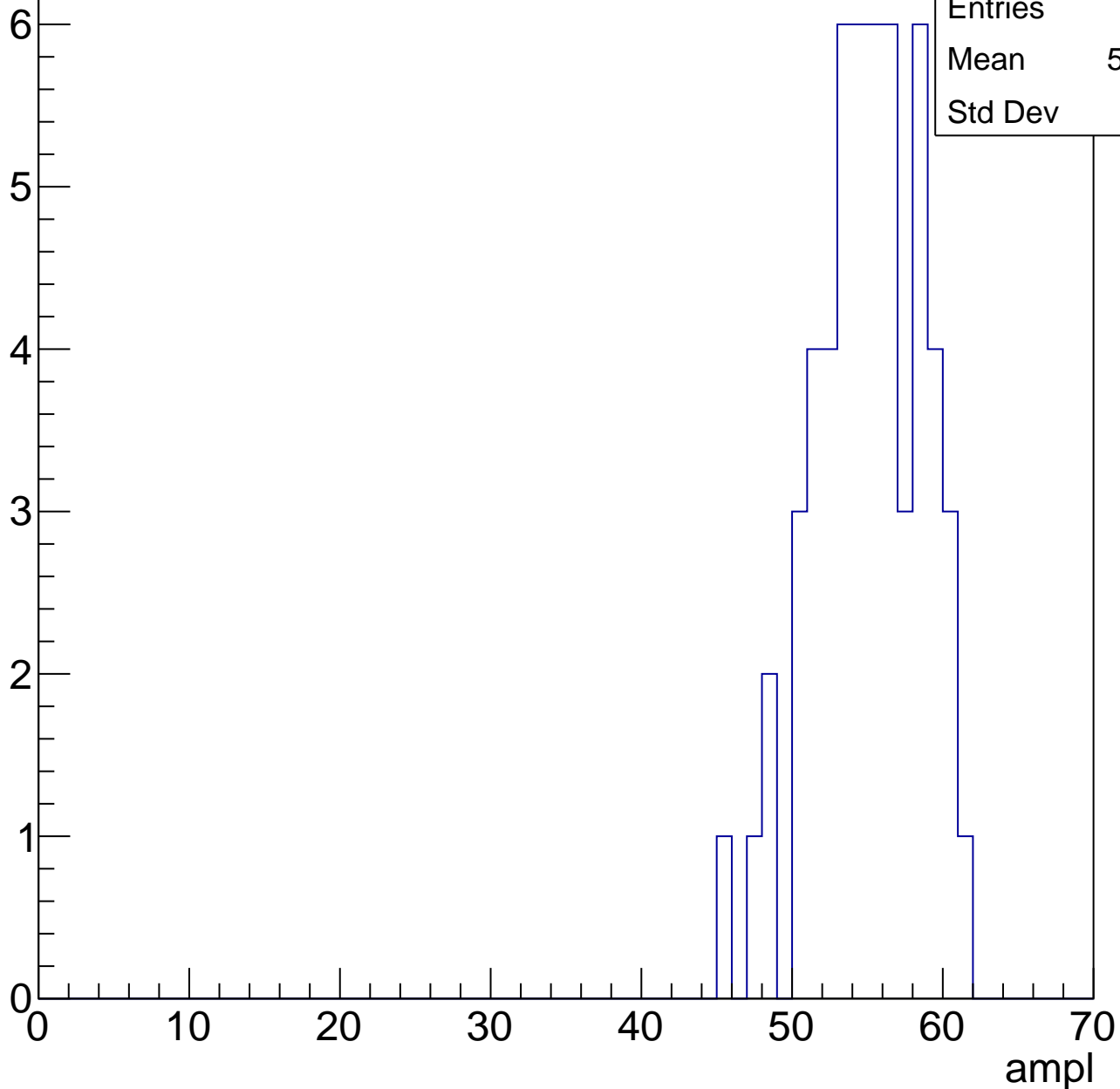
Entries	67
Mean	48.4
Std Dev	4.125



B1L102S, U17-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



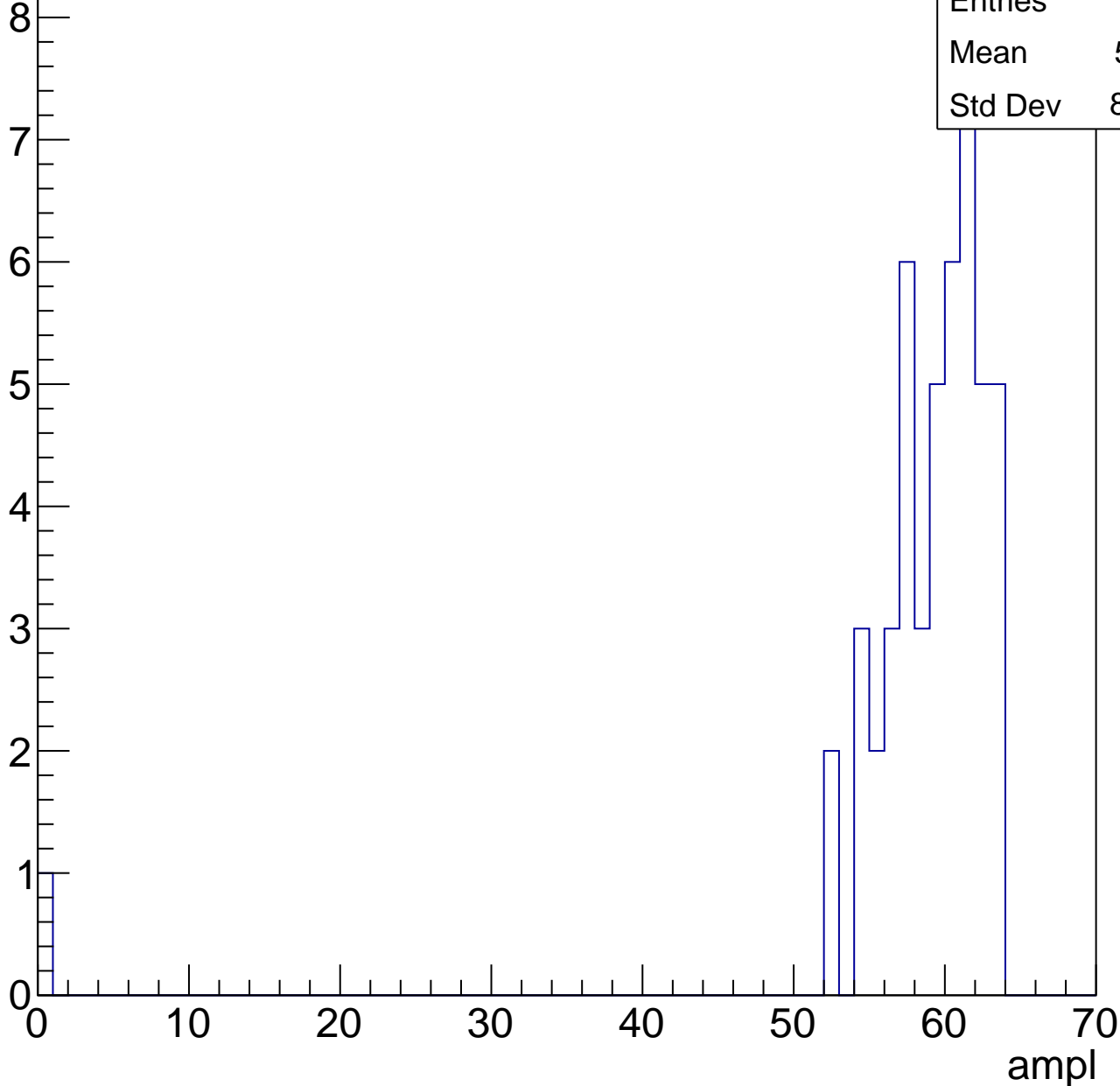
Entries	56
Mean	54.54
Std Dev	3.54

B1L102S, U17-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

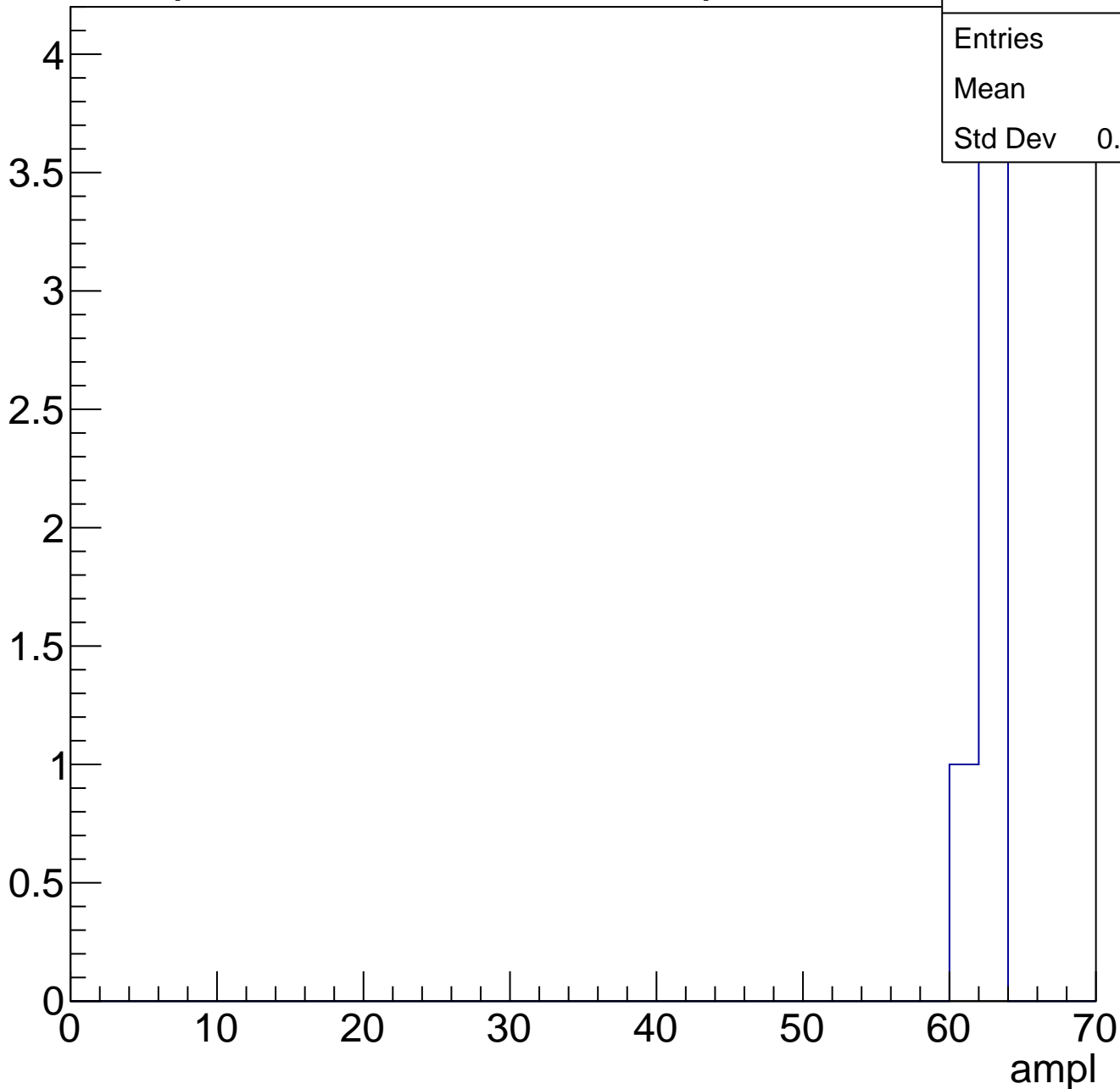
Entries	49
Mean	57.71
Std Dev	8.832



B1L102S, U17-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch7, adc0

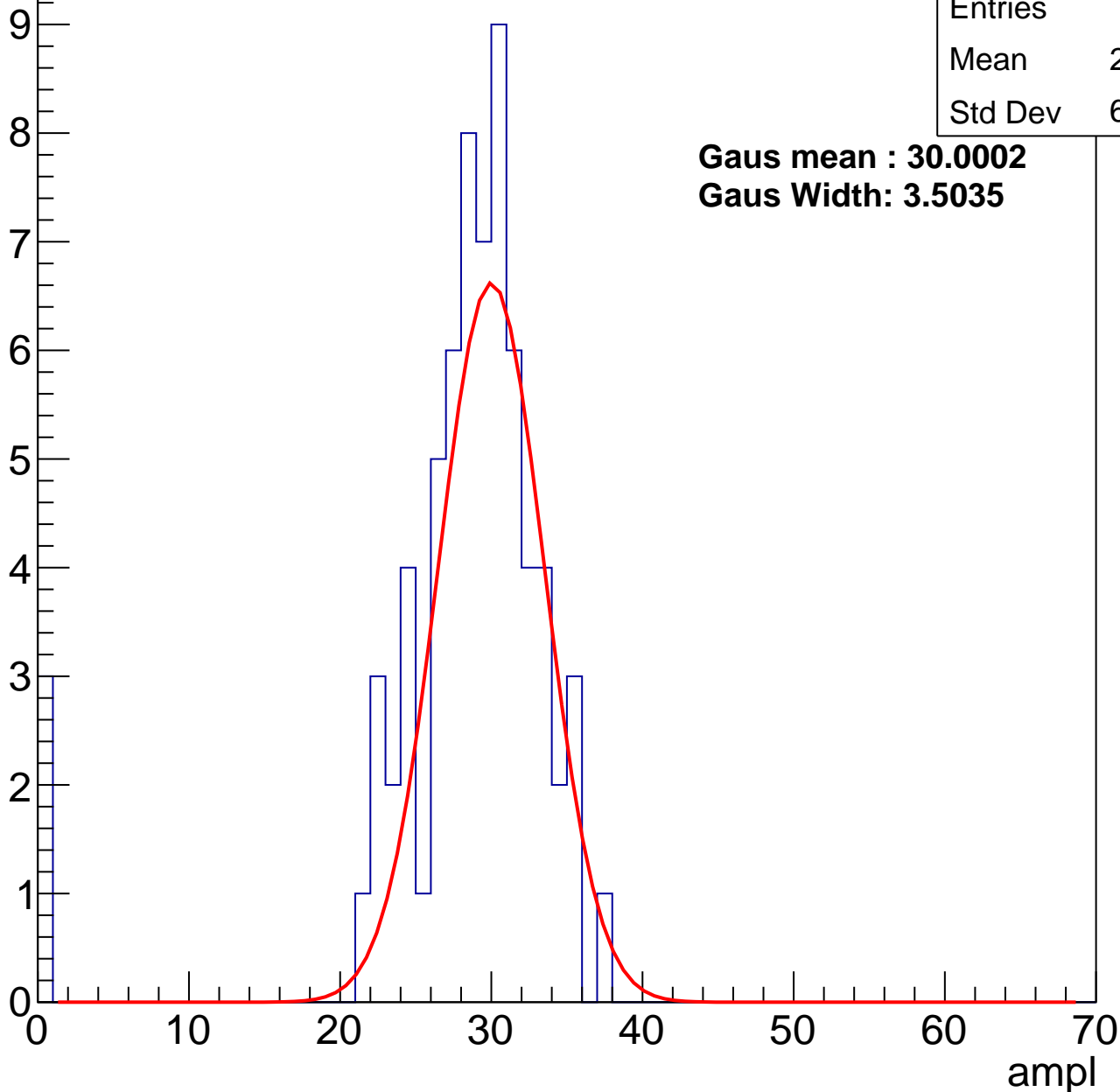
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.52
Std Dev	6.813

Gaus mean : 30.0002

Gaus Width: 3.5035



B1L102S, U17-ch7, adc1

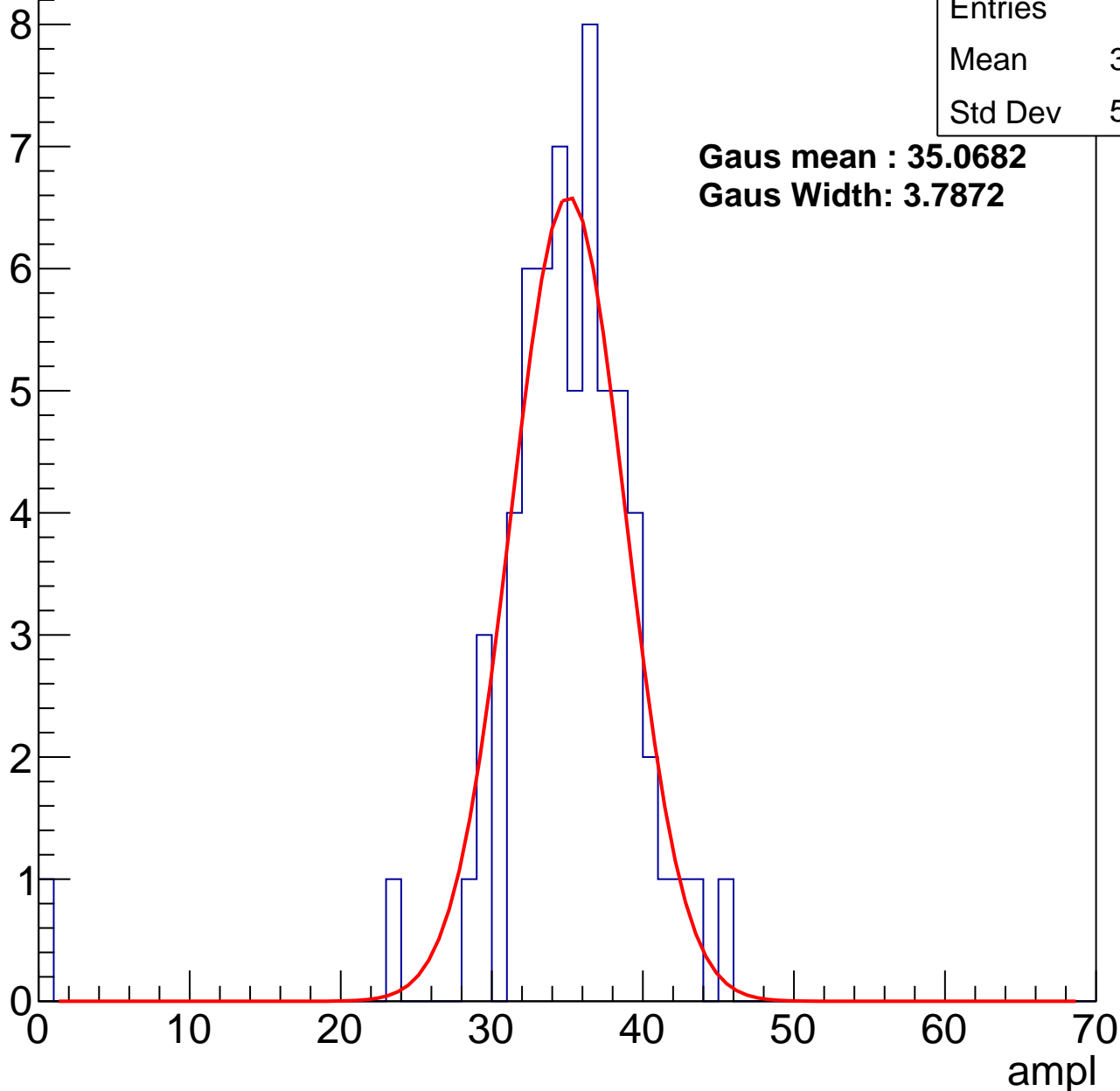
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.44
Std Dev	5.808

Gaus mean : 35.0682

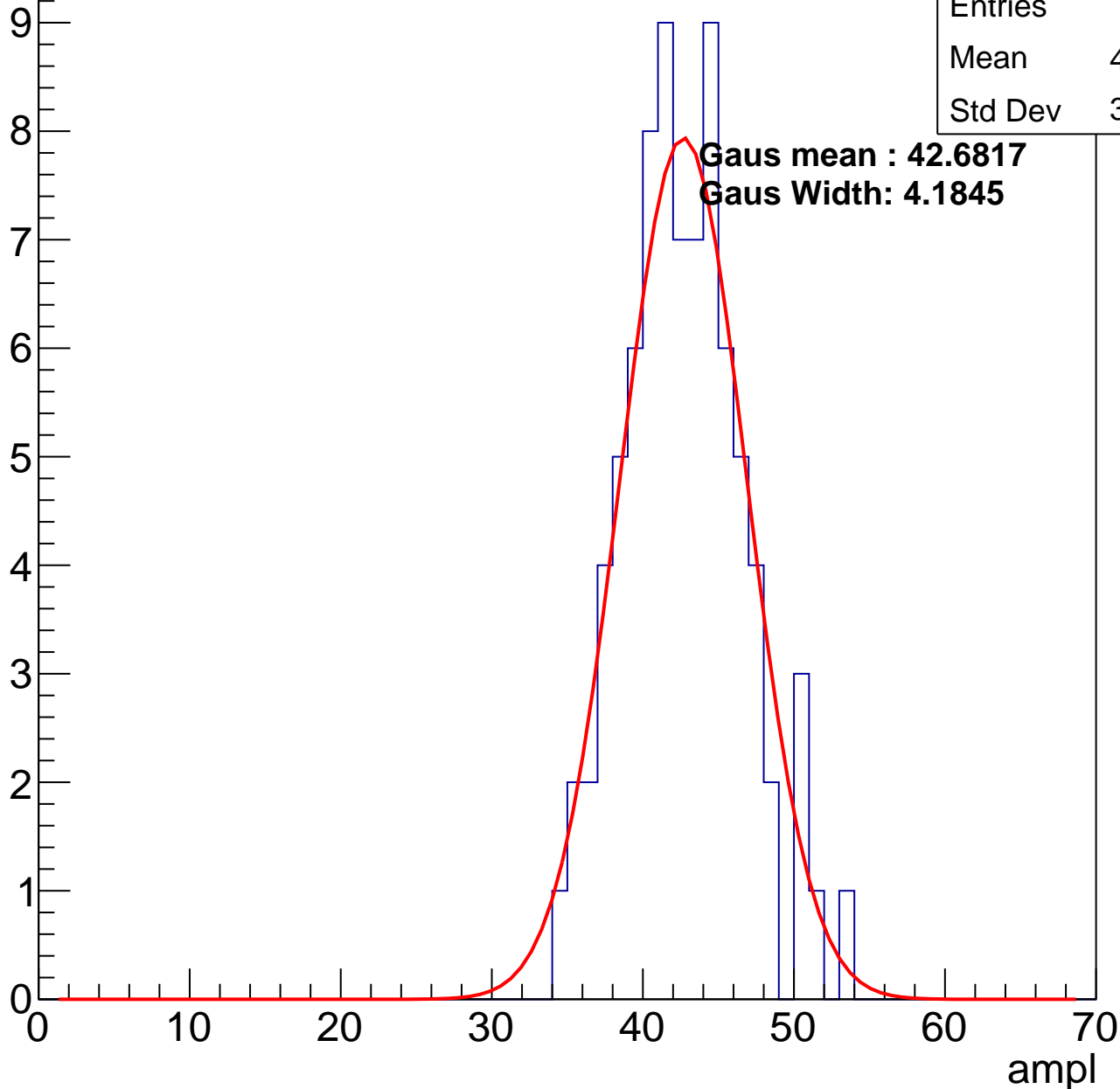
Gaus Width: 3.7872



B1L102S, U17-ch7, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry



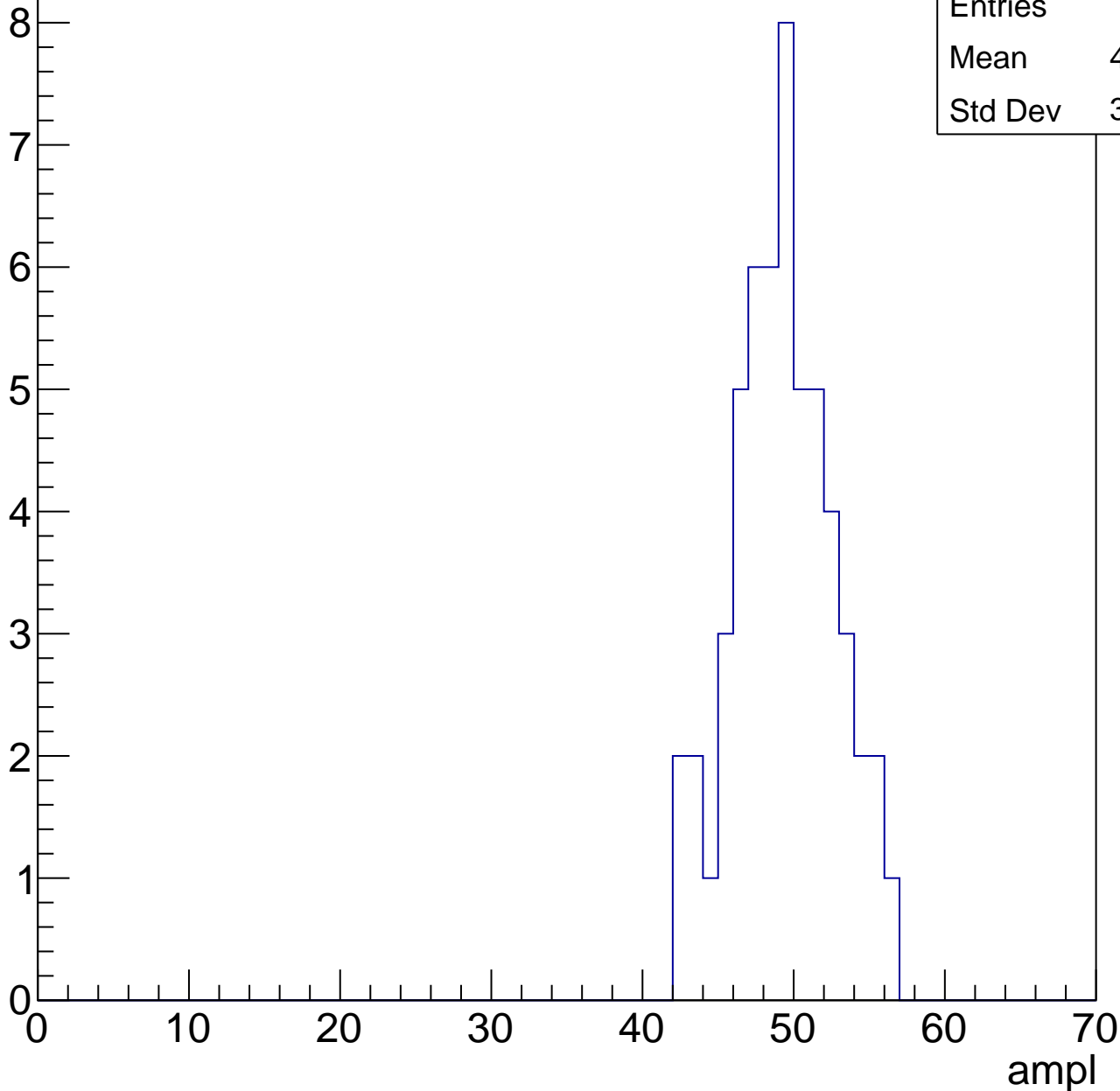
Entries	82
Mean	42.27
Std Dev	3.898

B1L102S, U17-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

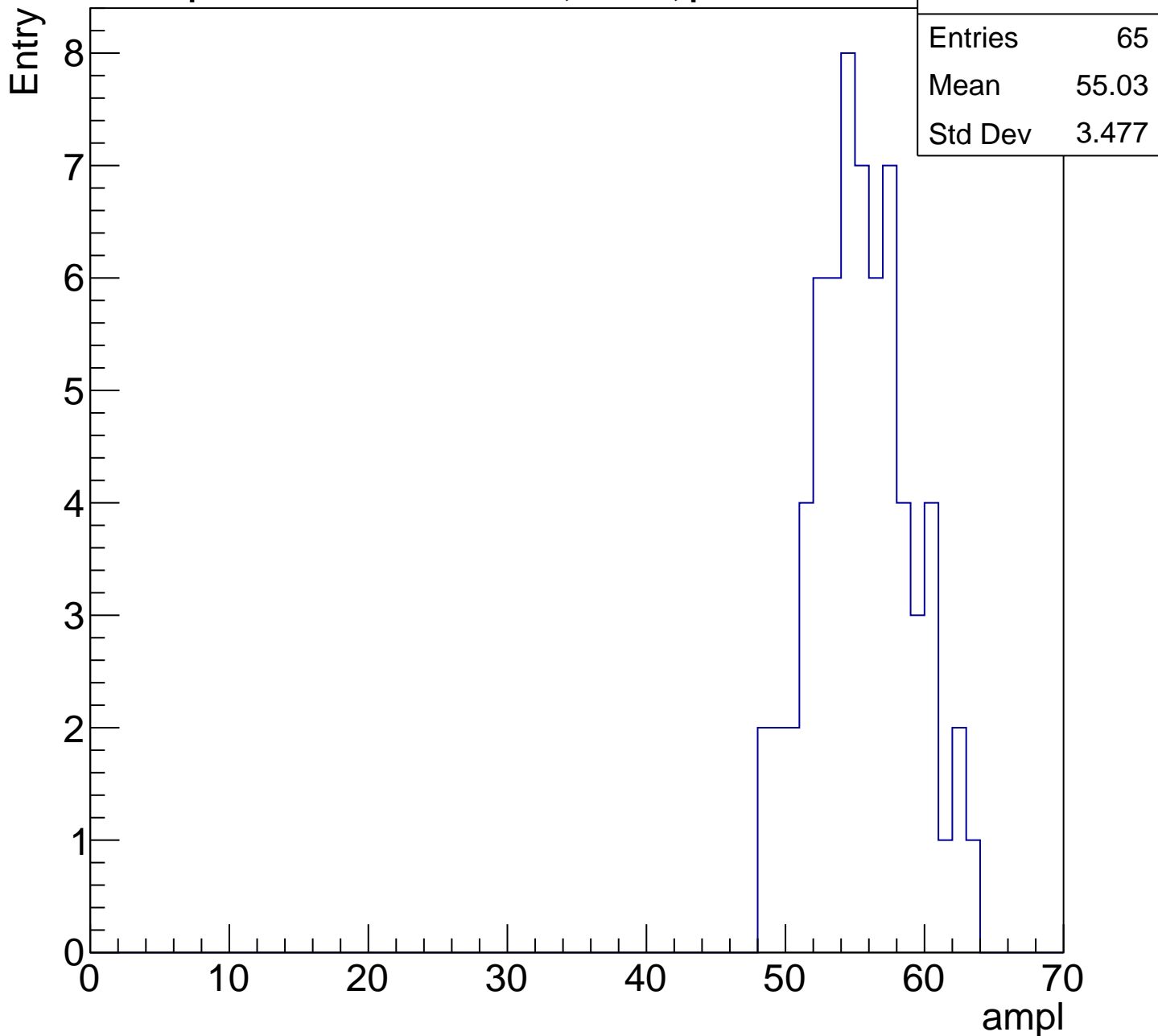
Entry

Entries	55
Mean	48.85
Std Dev	3.294



B1L102S, U17-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

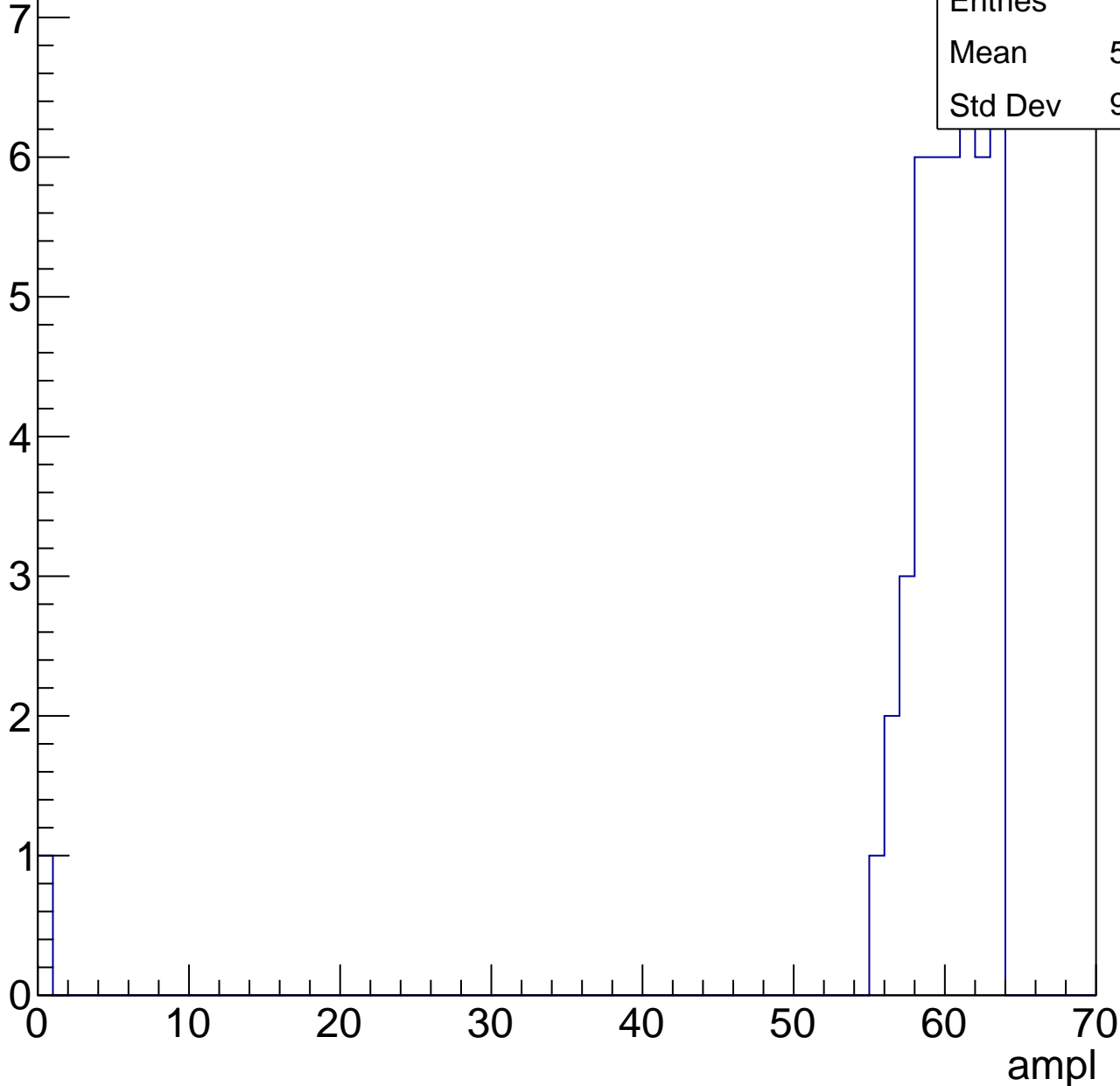


B1L102S, U17-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.67
Std Dev	9.102



B1L102S, U17-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

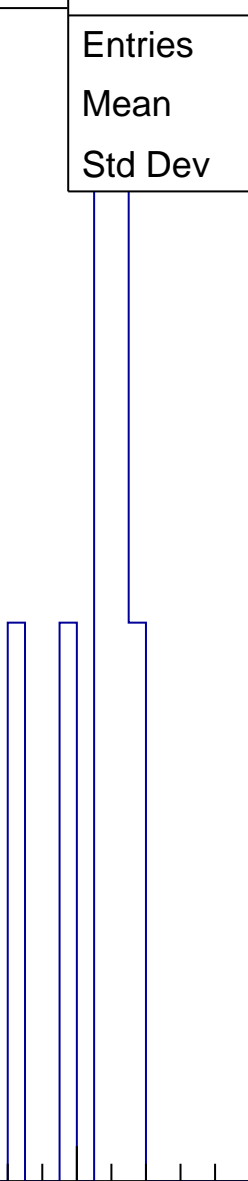
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.57
Std Dev	2.195

0 10 20 30 40 50 60 70

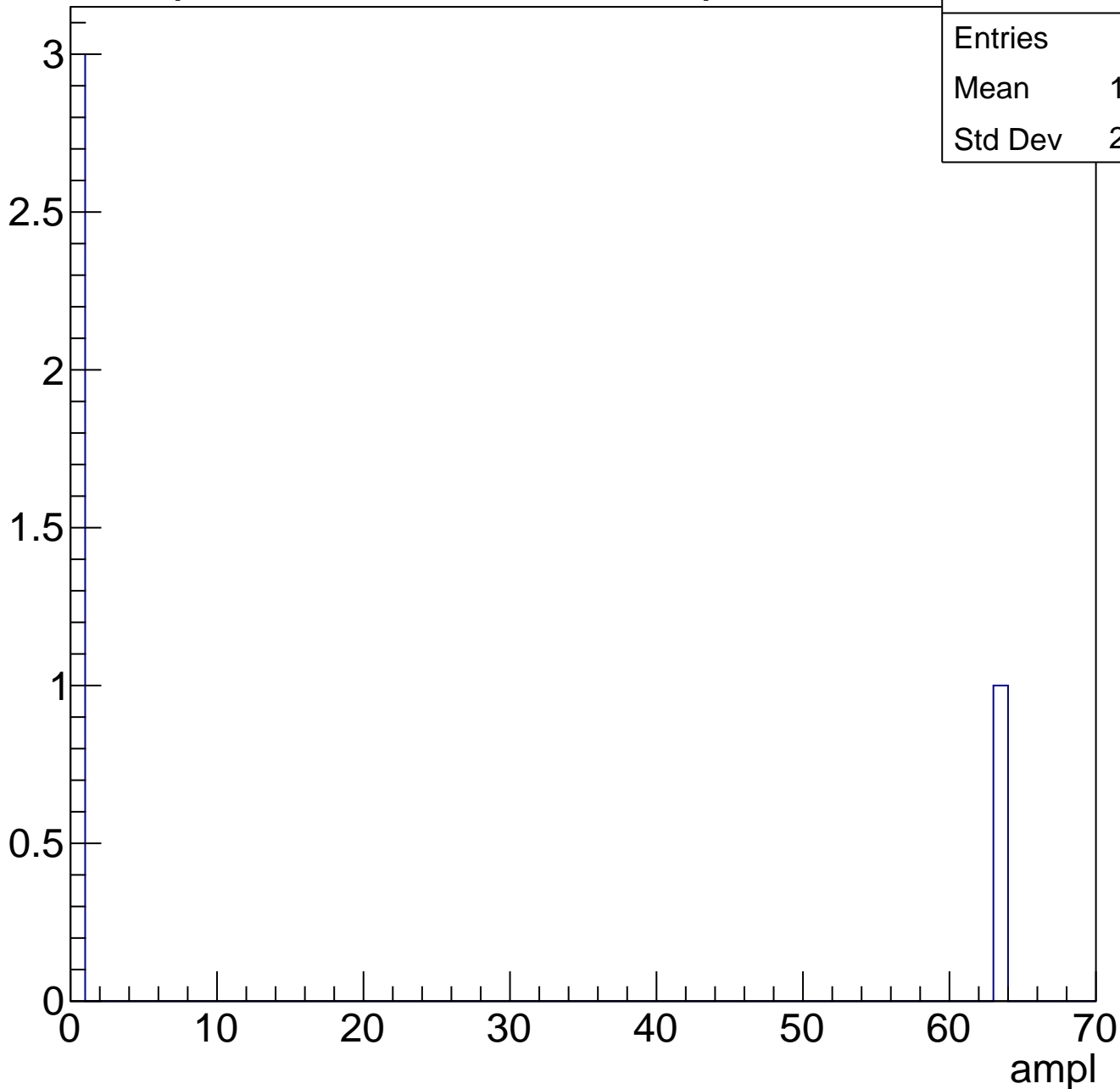
ampl



B1L102S, U17-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch8, adc0

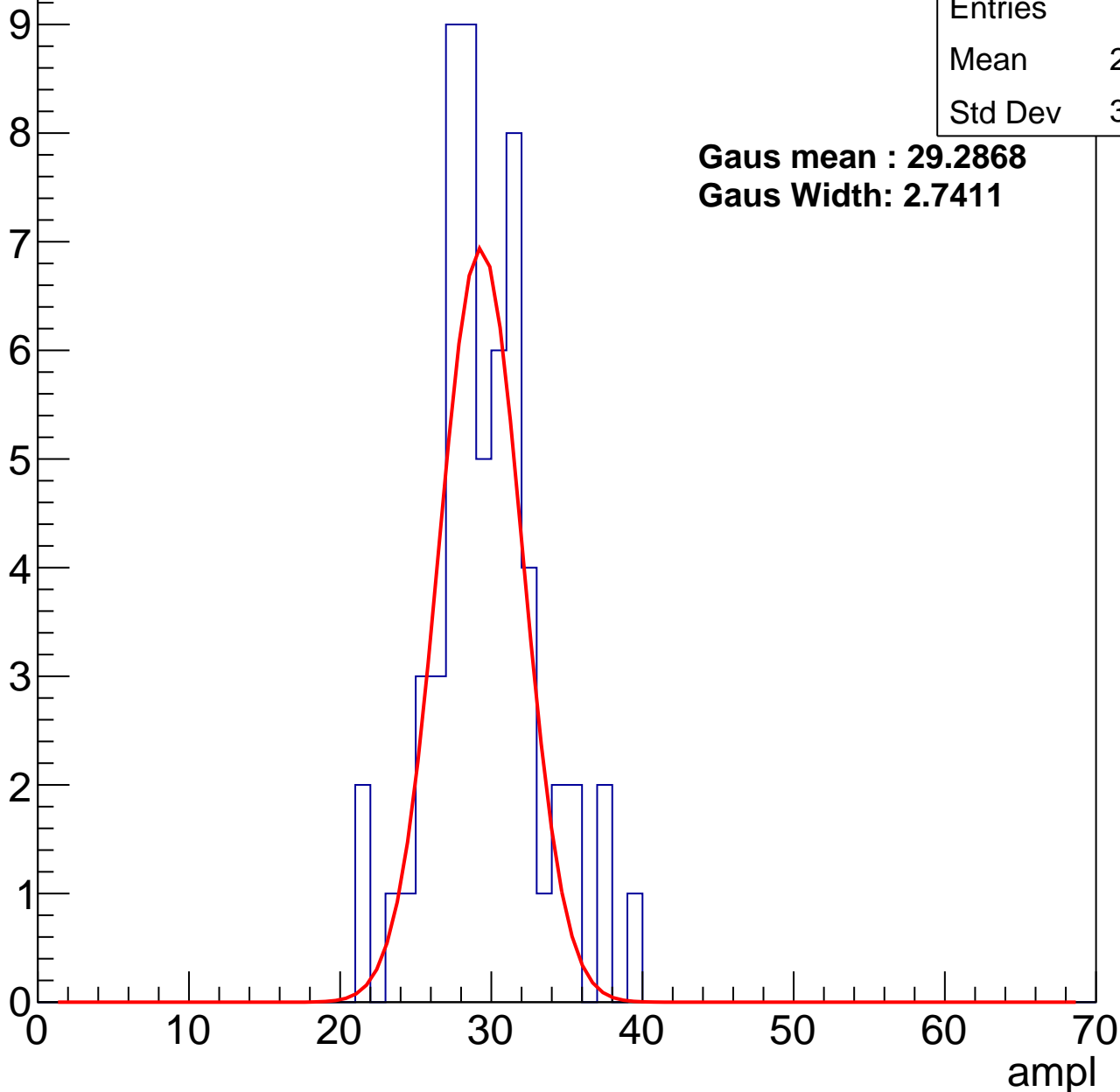
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.19
Std Dev	3.558

Gaus mean : 29.2868

Gaus Width: 2.7411



B1L102S, U17-ch8, adc1

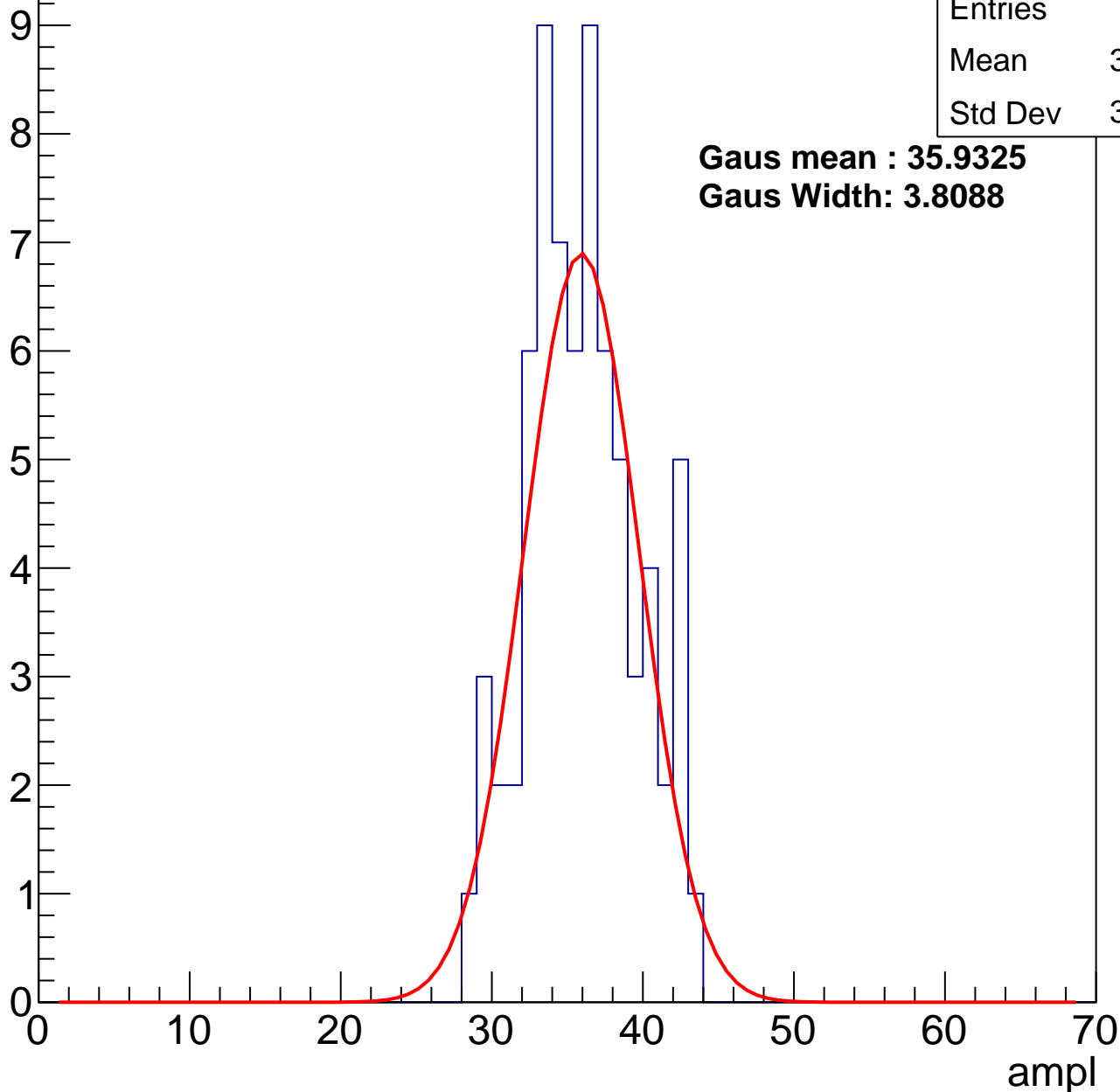
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.52
Std Dev	3.607

Gaus mean : 35.9325

Gaus Width: 3.8088



B1L102S, U17-ch8, adc2

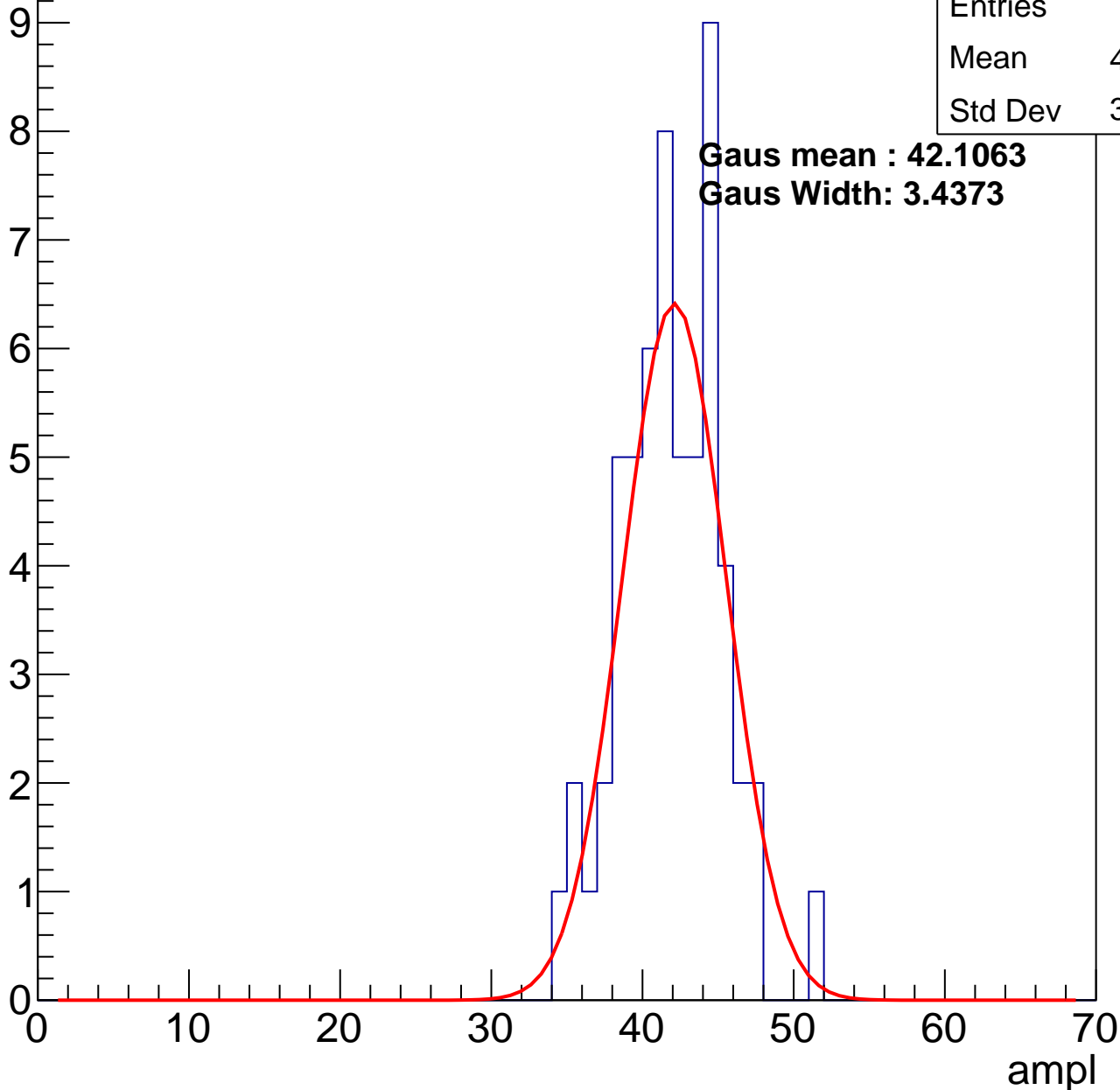
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	41.47
Std Dev	3.292

Gaus mean : 42.1063

Gaus Width: 3.4373

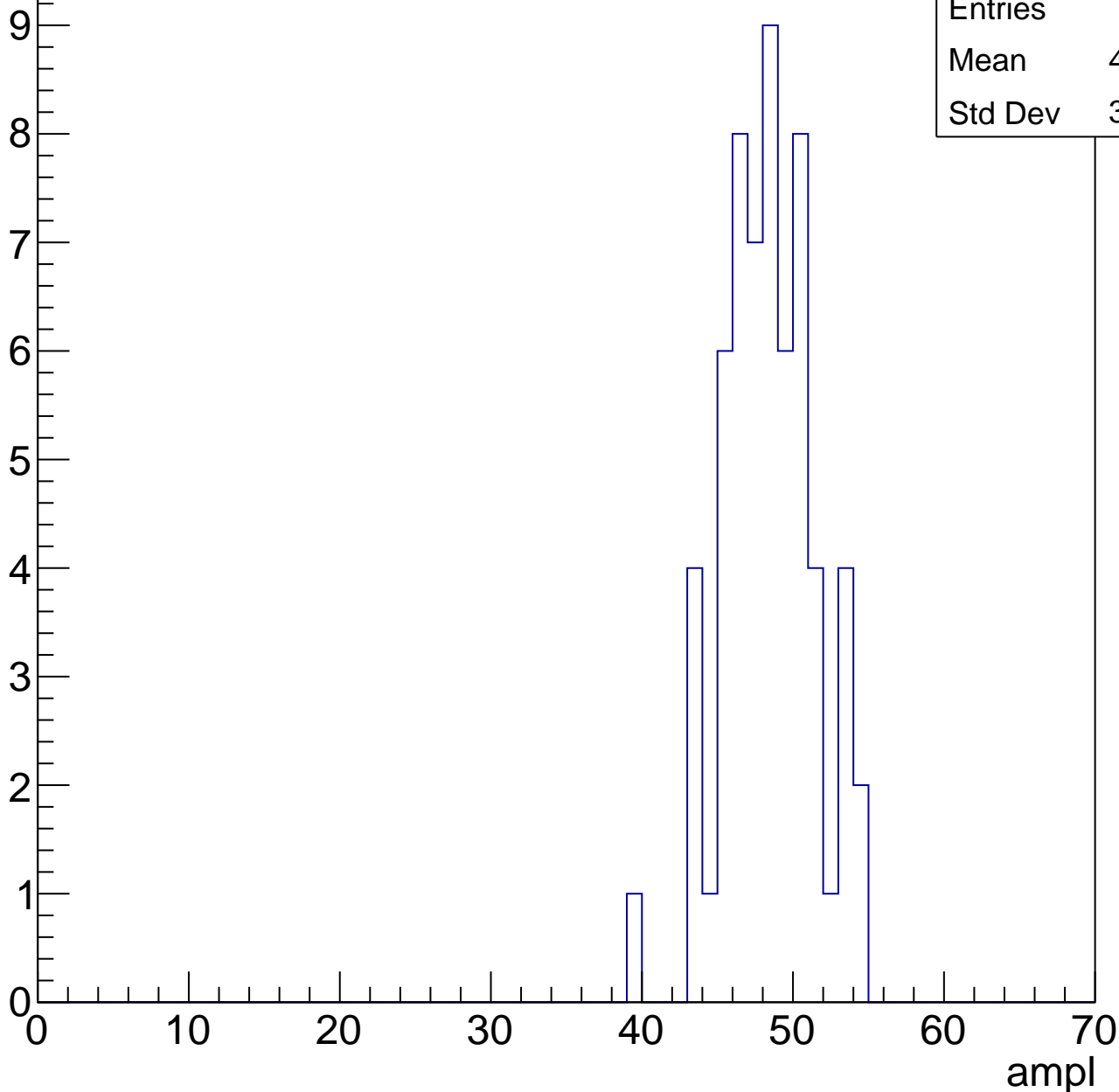


B1L102S, U17-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.93
Std Dev	3.007

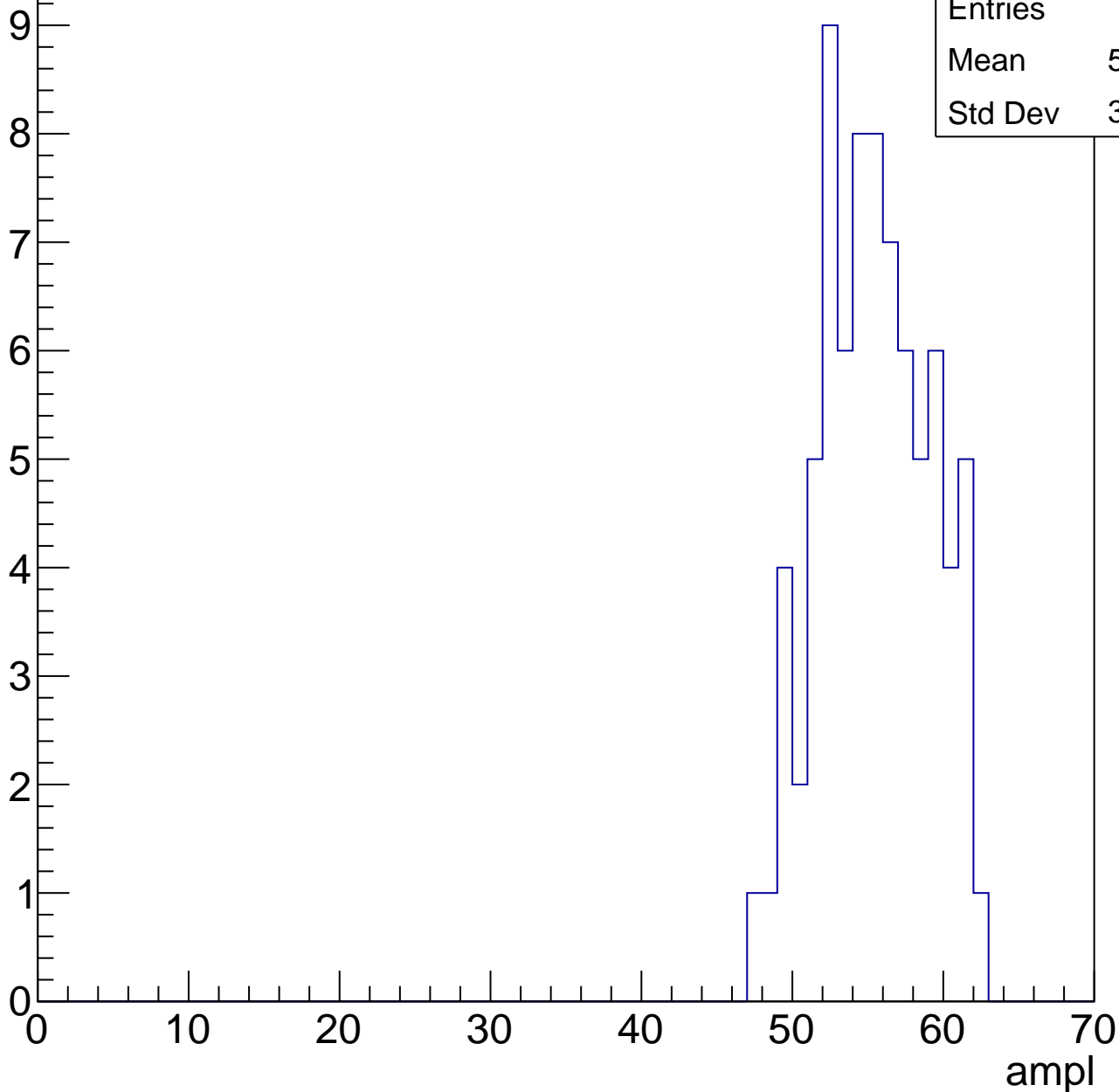


B1L102S, U17-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	54.99
Std Dev	3.582

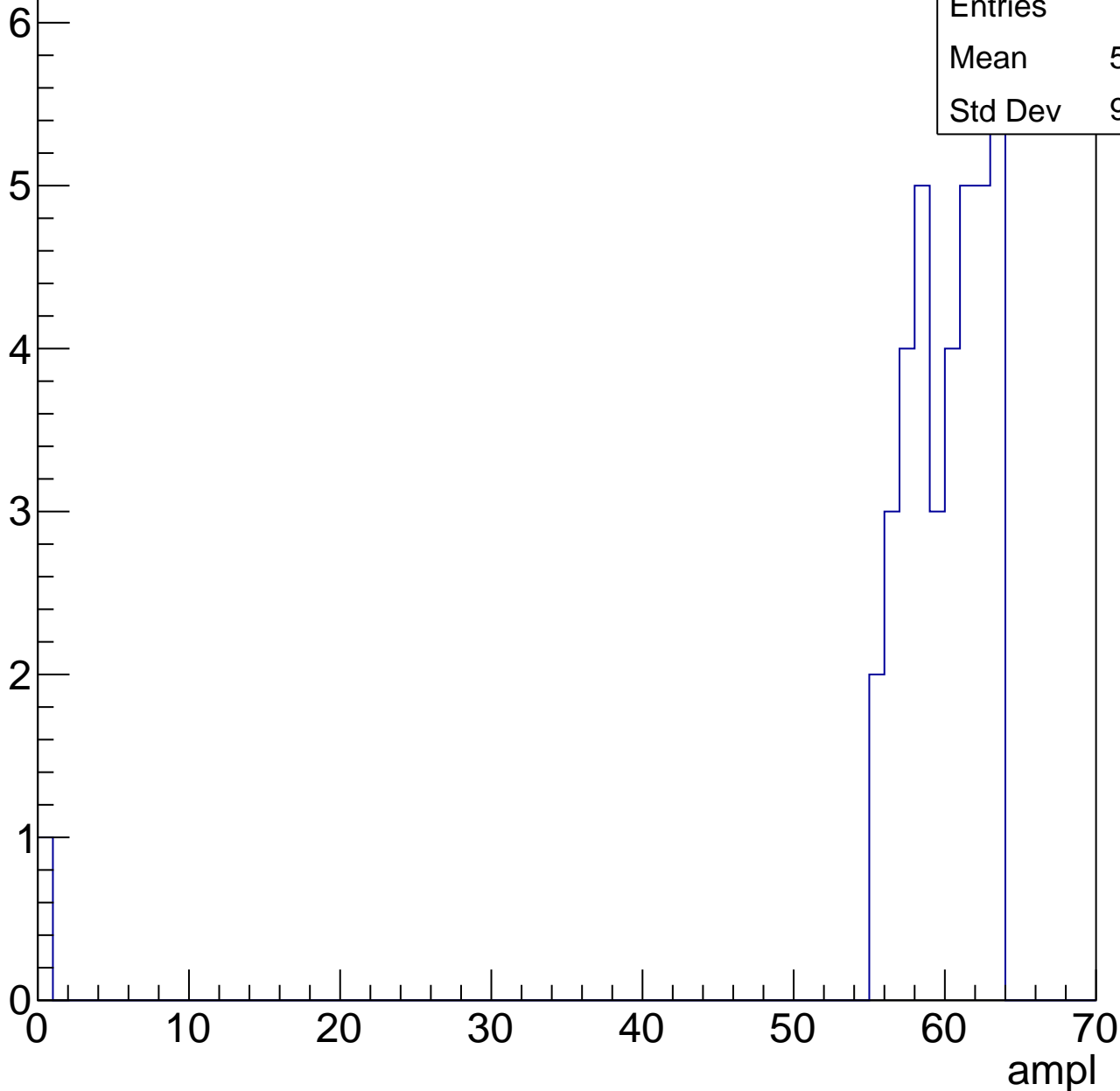


B1L102S, U17-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

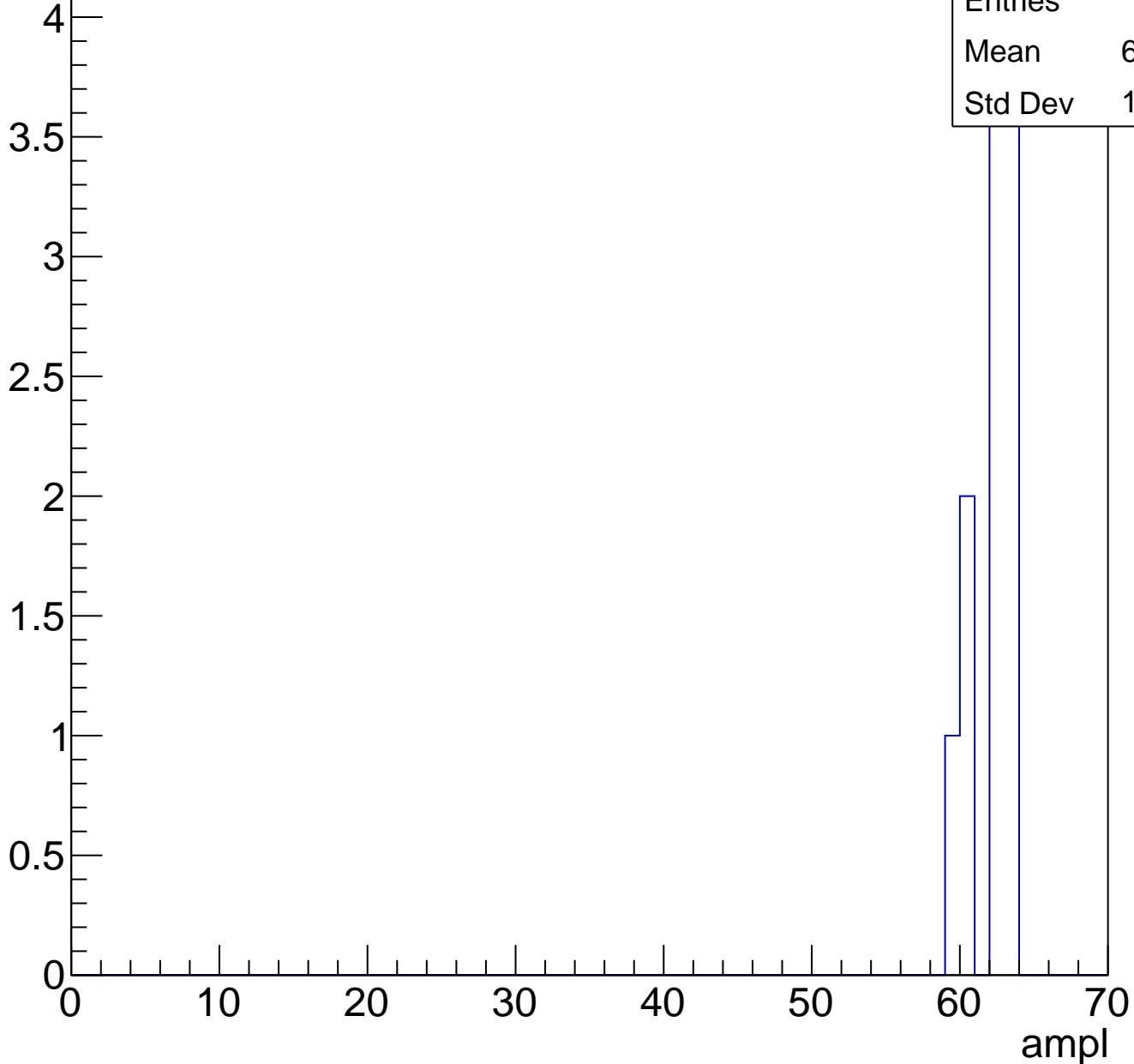
Entries	38
Mean	58.05
Std Dev	9.857



B1L102S, U17-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch9, adc0

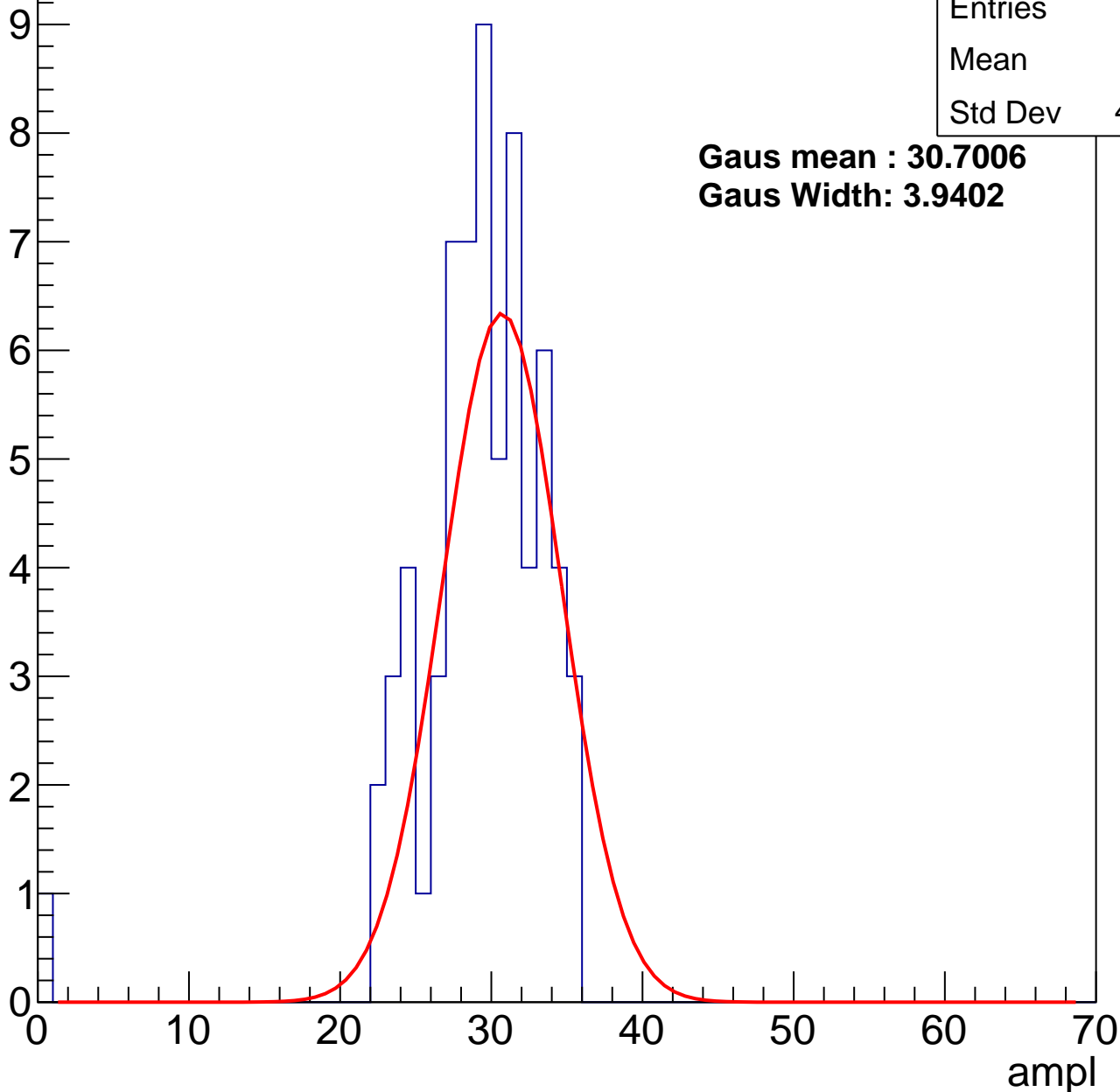
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.7
Std Dev	4.881

Gaus mean : 30.7006

Gaus Width: 3.9402



B1L102S, U17-ch9, adc1

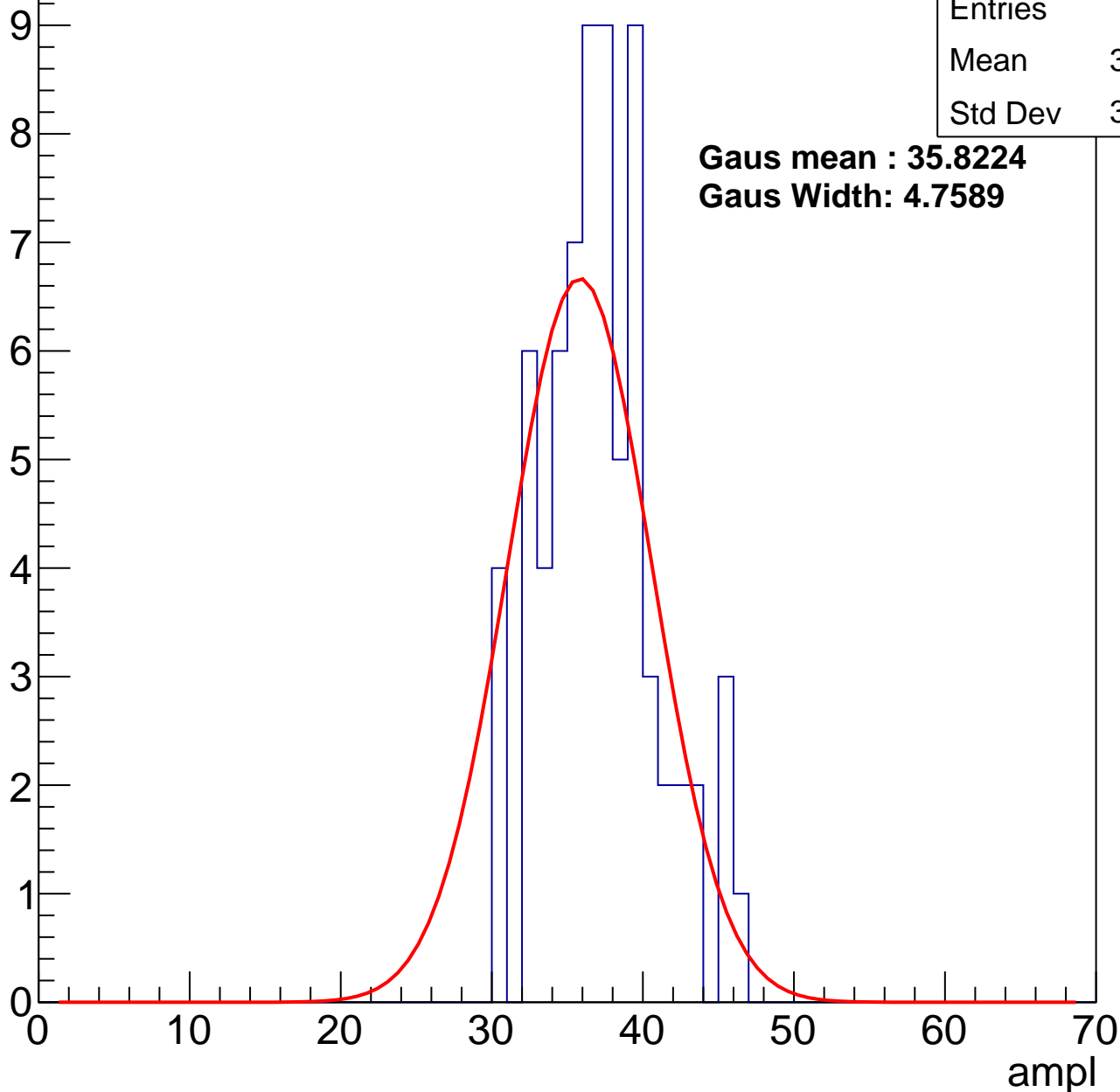
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.72
Std Dev	3.709

Gaus mean : 35.8224

Gaus Width: 4.7589



B1L102S, U17-ch9, adc2

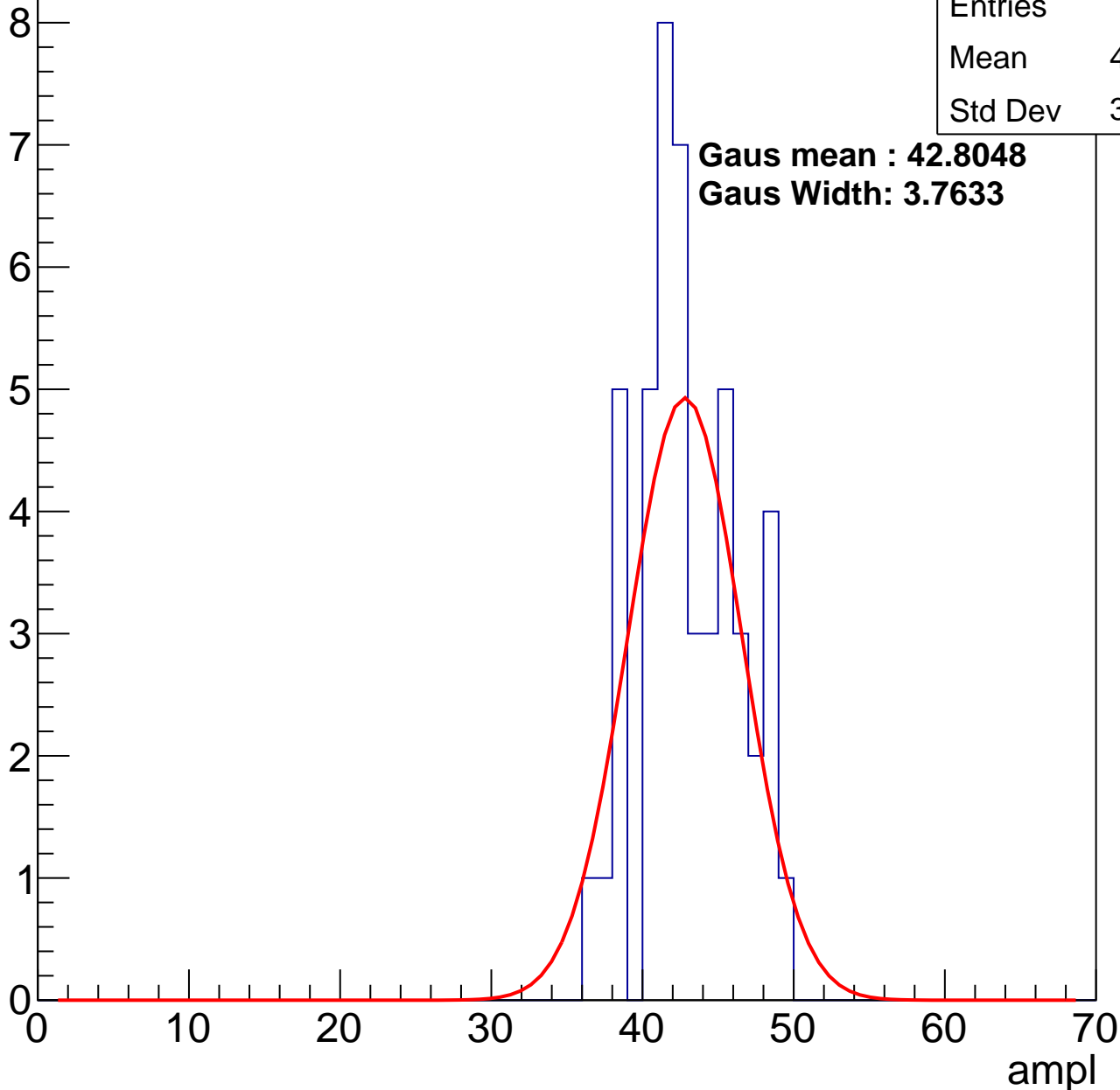
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	42.58
Std Dev	3.239

Gaus mean : 42.8048

Gaus Width: 3.7633

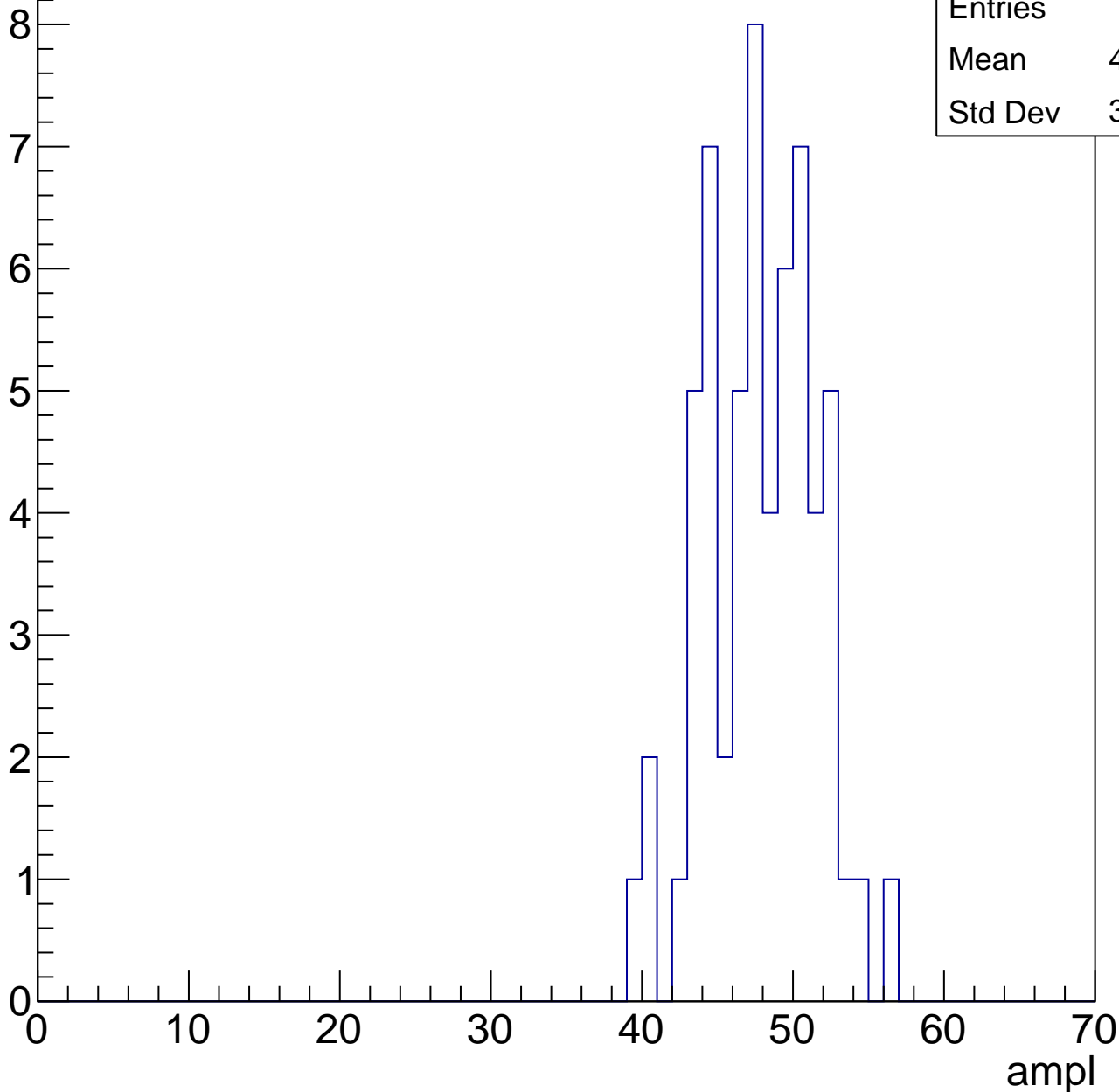


B1L102S, U17-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.38
Std Dev	3.606



B1L102S, U17-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

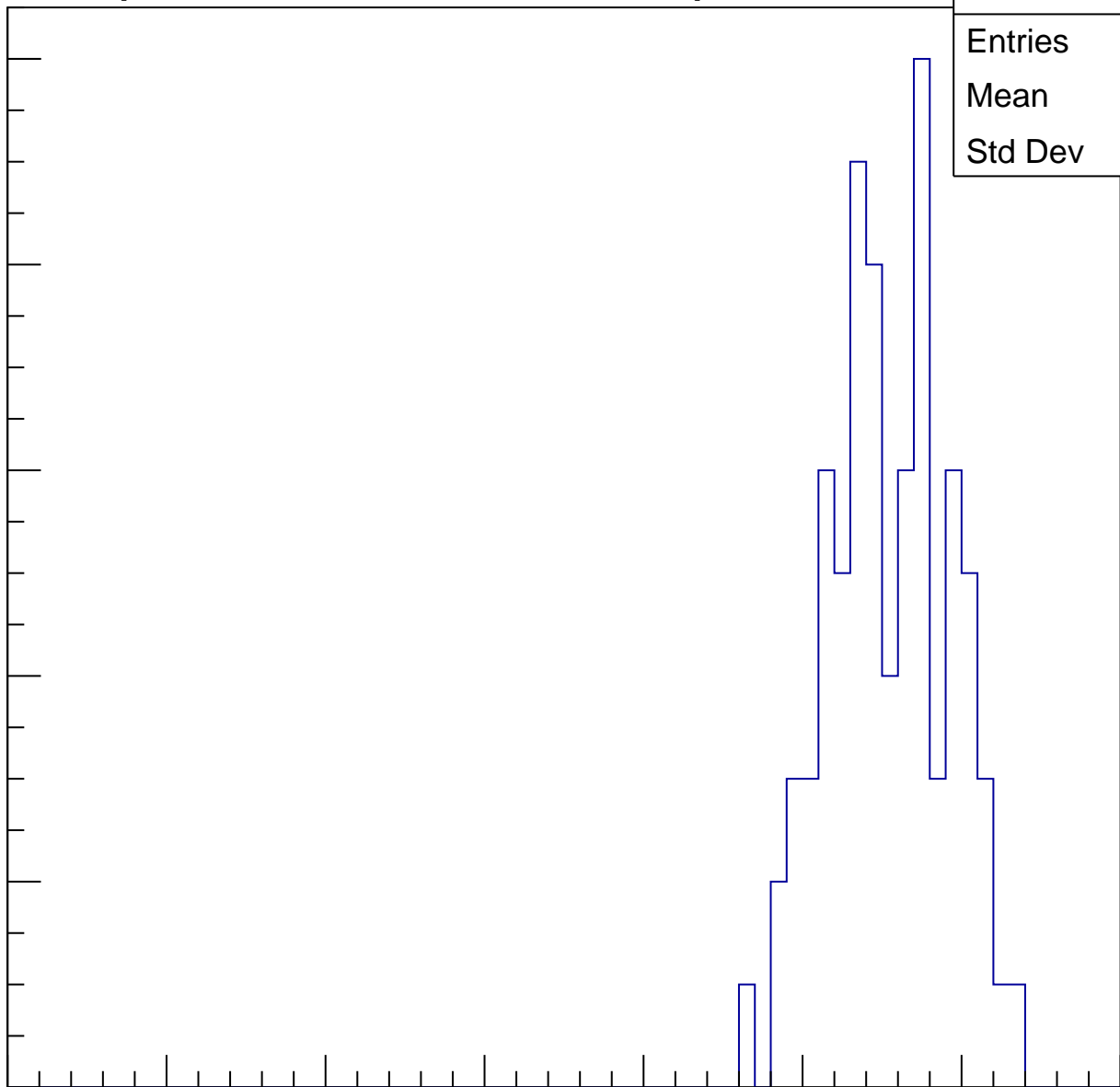
Entries	76
Mean	54.95
Std Dev	3.738

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

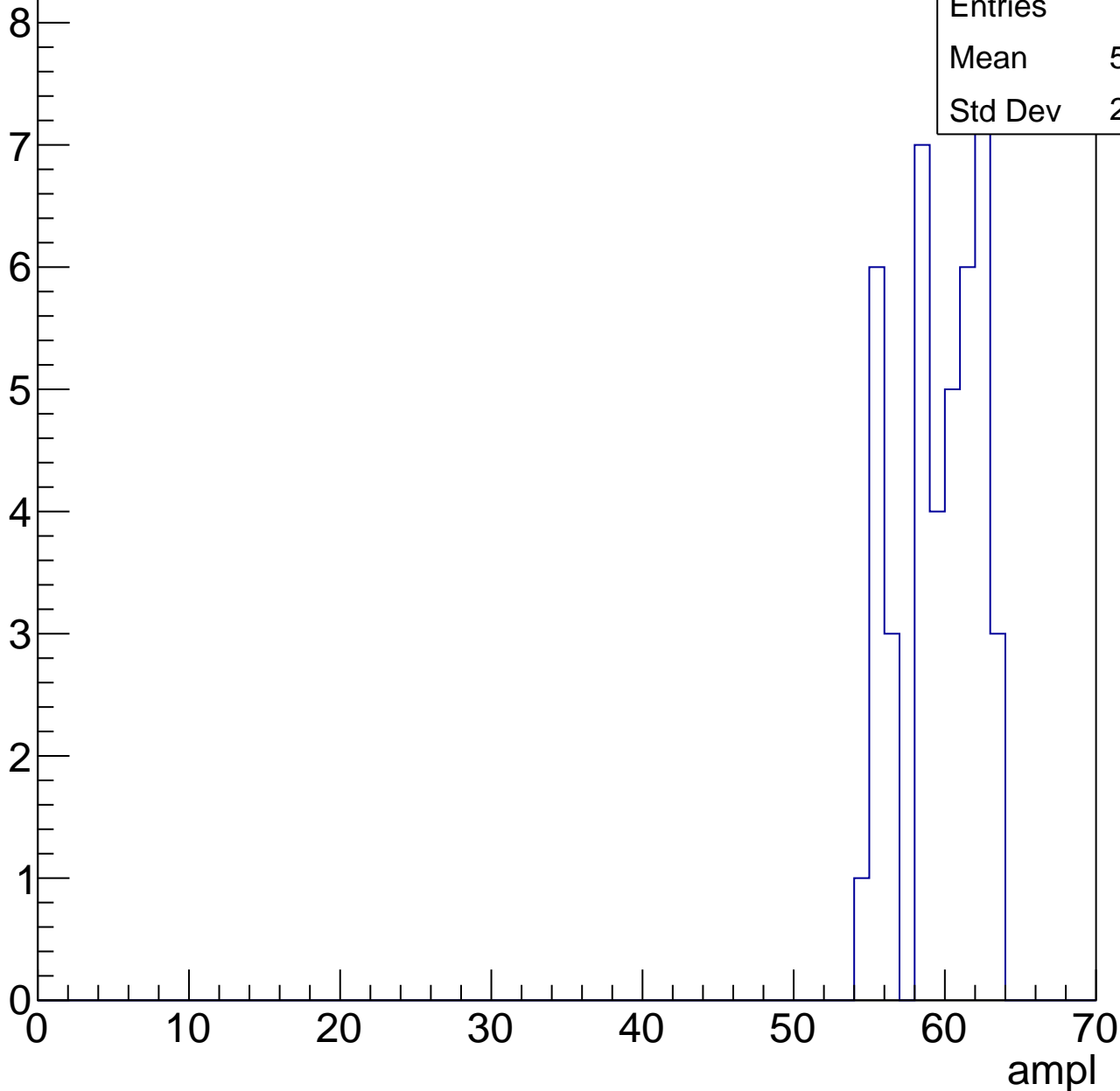


B1L102S, U17-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.19
Std Dev	2.652

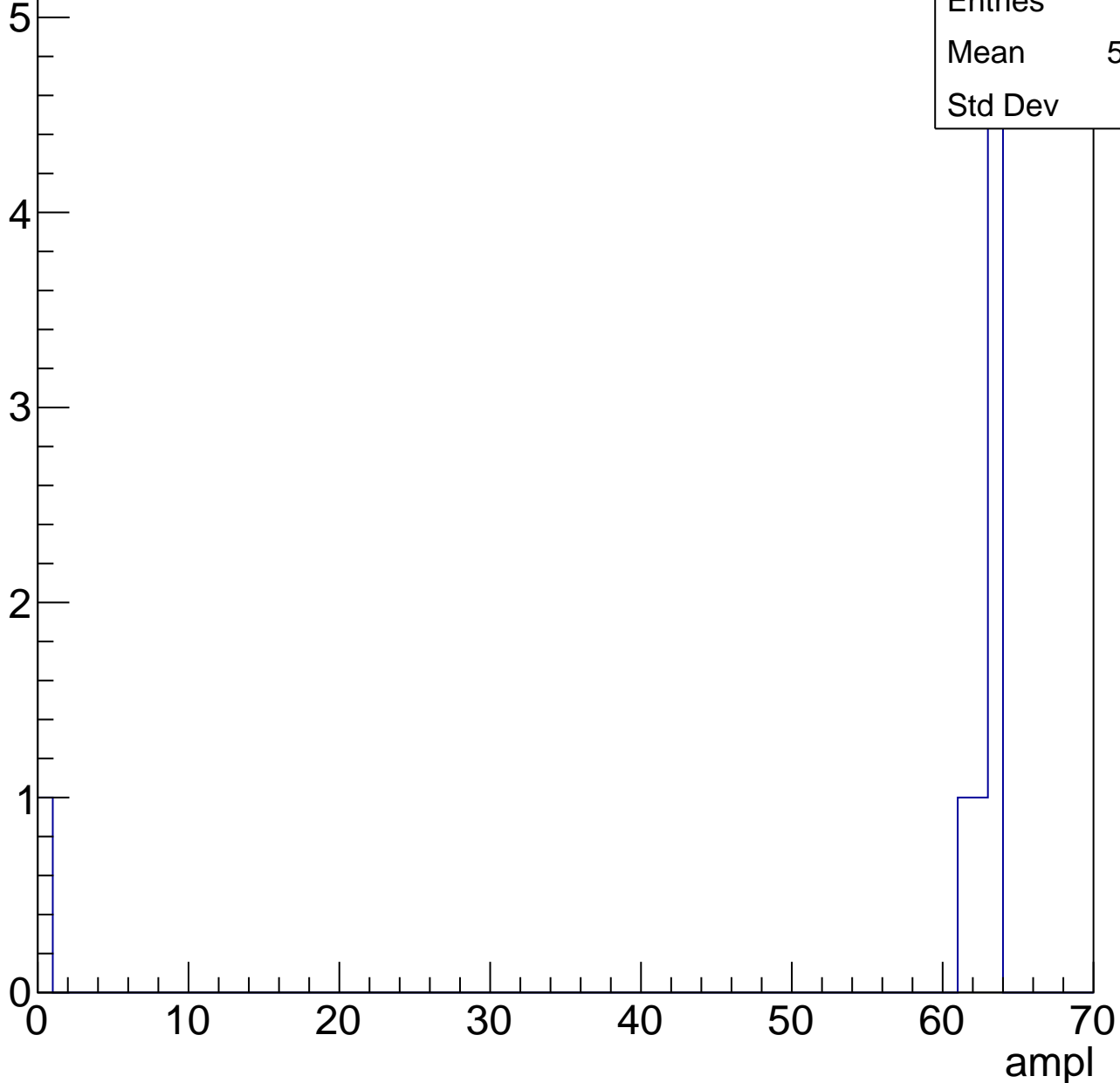


B1L102S, U17-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.75
Std Dev	20.7



B1L102S, U17-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch10, adc0

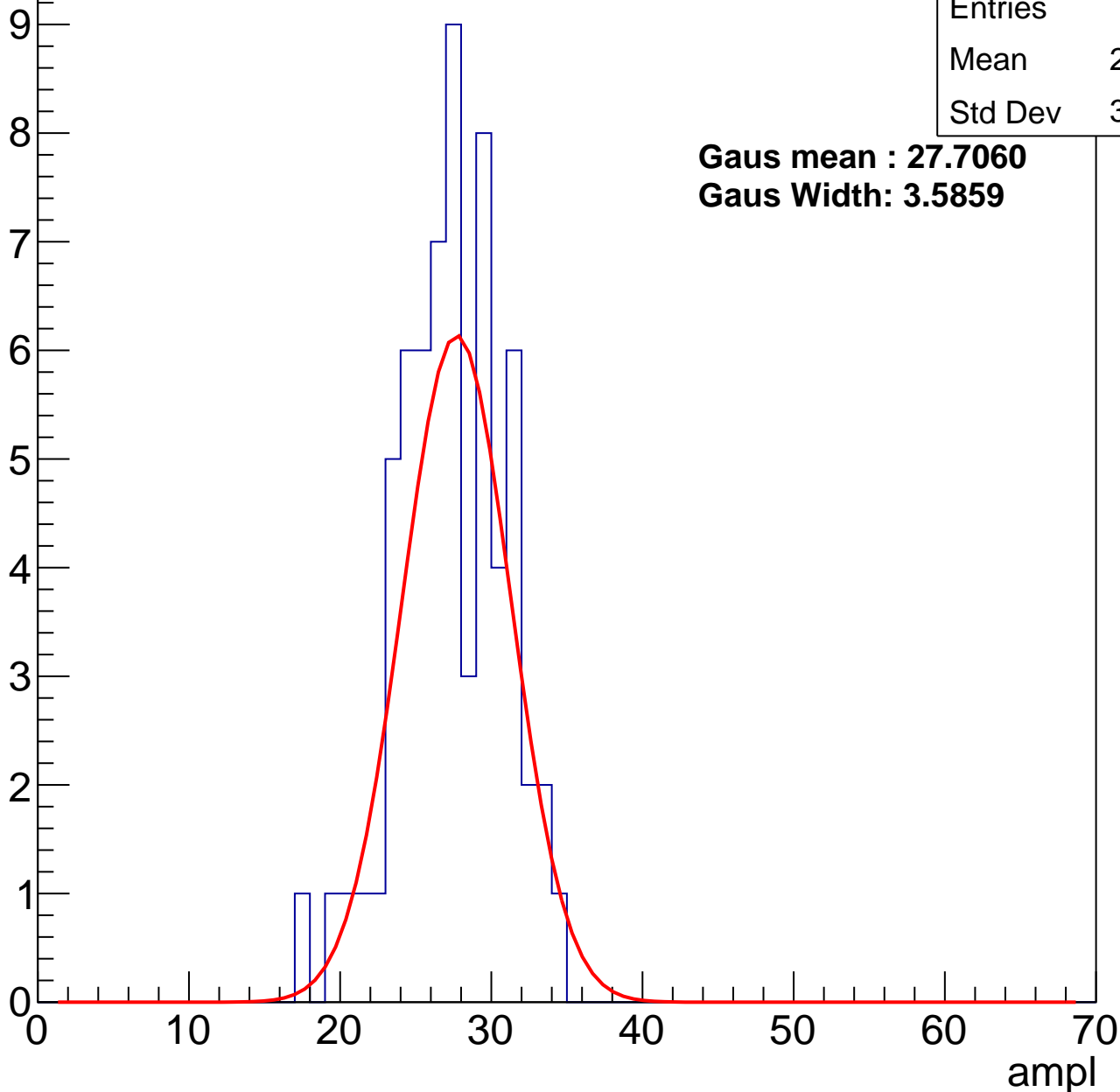
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	26.86
Std Dev	3.482

Gaus mean : 27.7060

Gaus Width: 3.5859



B1L102S, U17-ch10, adc1

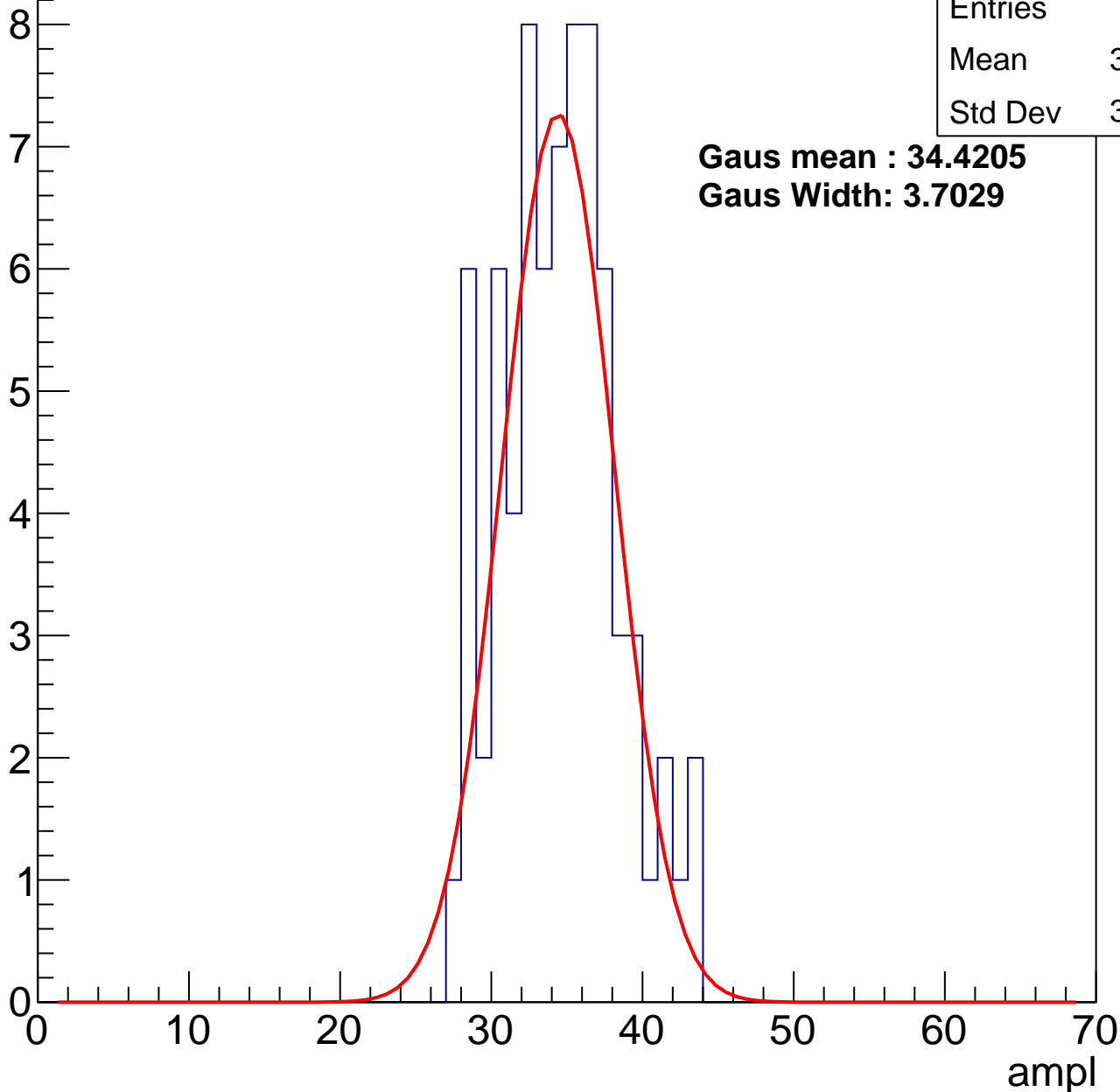
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.05
Std Dev	3.788

Gaus mean : 34.4205

Gaus Width: 3.7029



B1L102S, U17-ch10, adc2

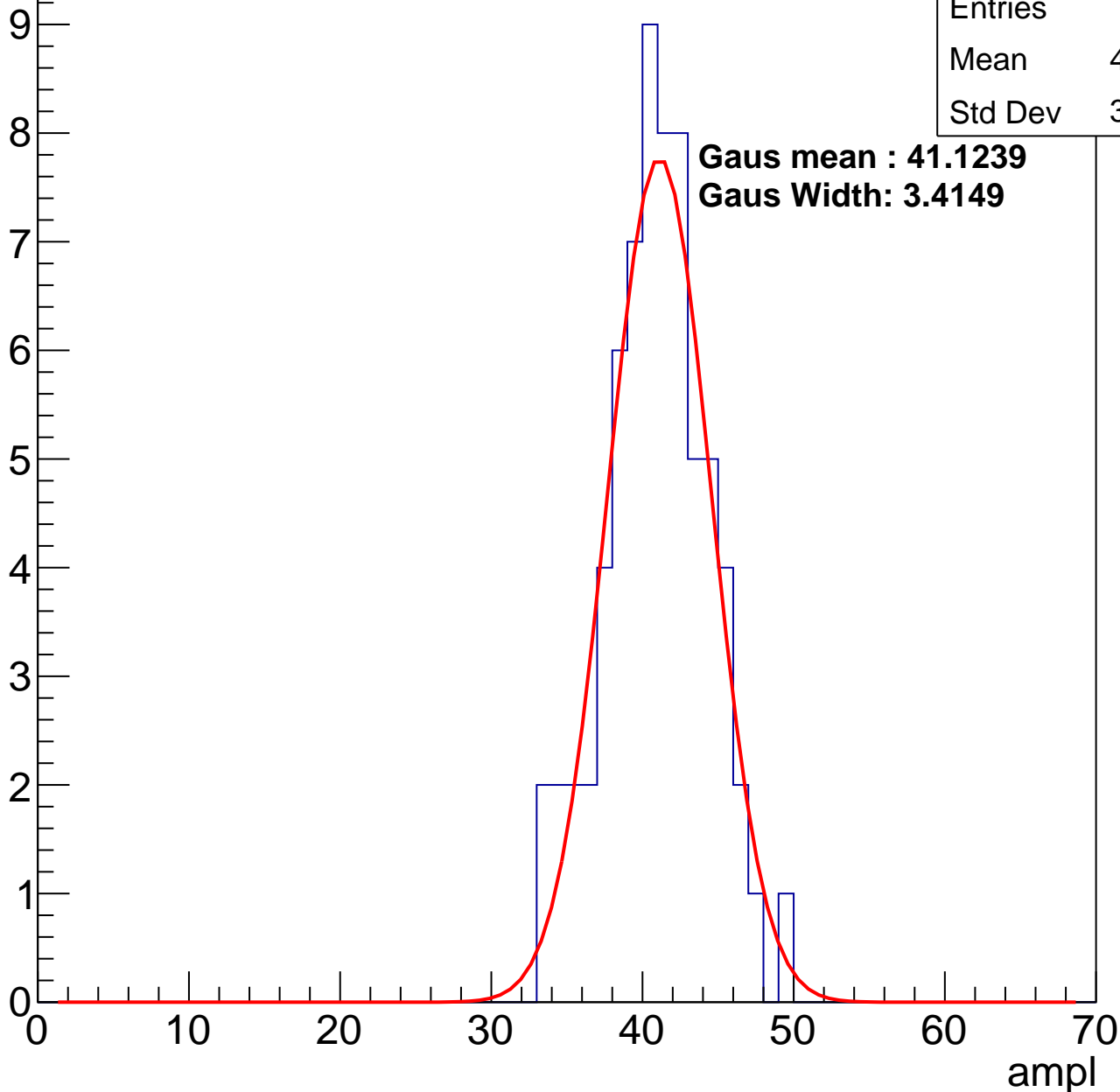
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	40.47
Std Dev	3.367

Gaus mean : 41.1239

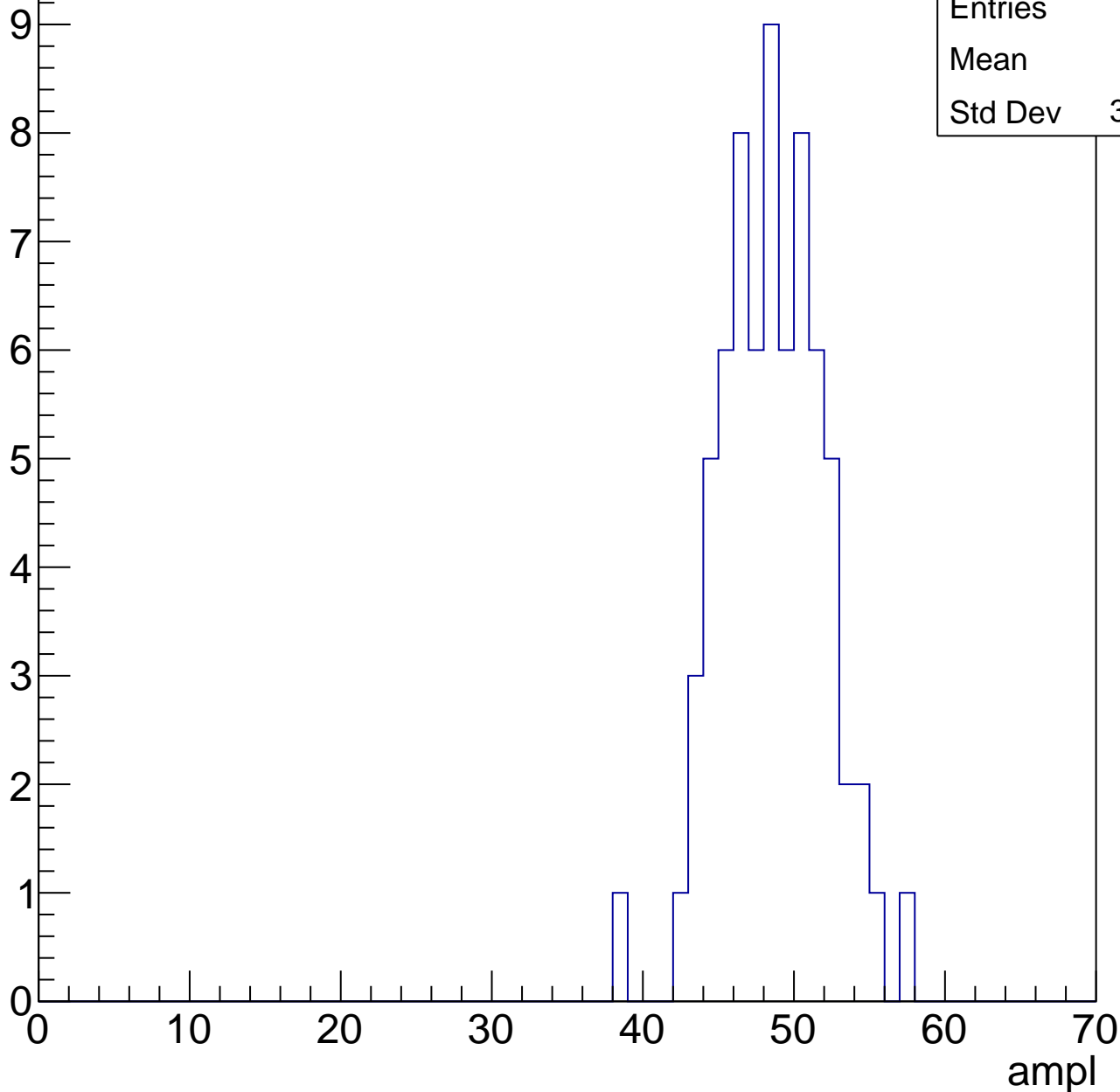
Gaus Width: 3.4149



B1L102S, U17-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

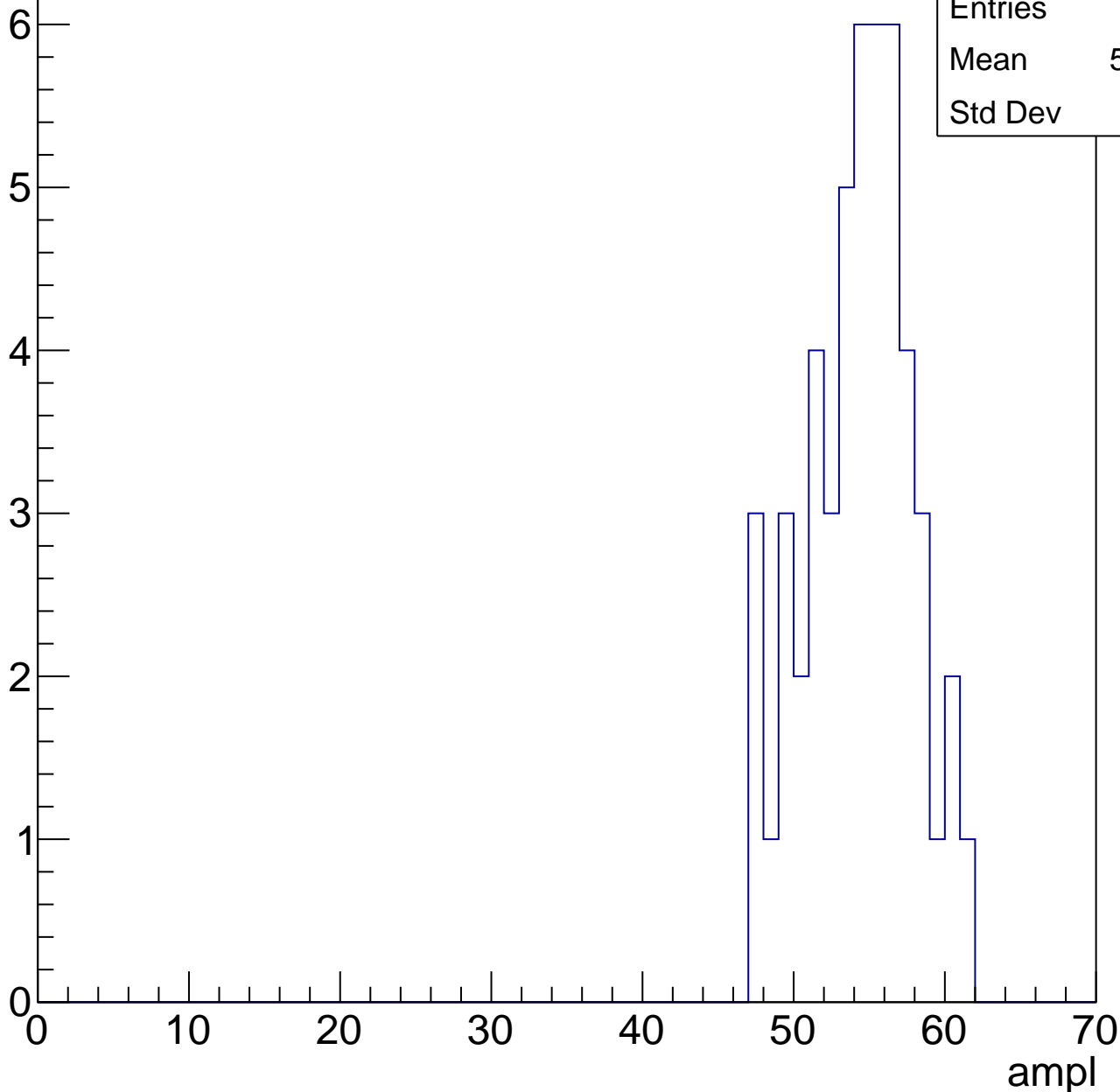


B1L102S, U17-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

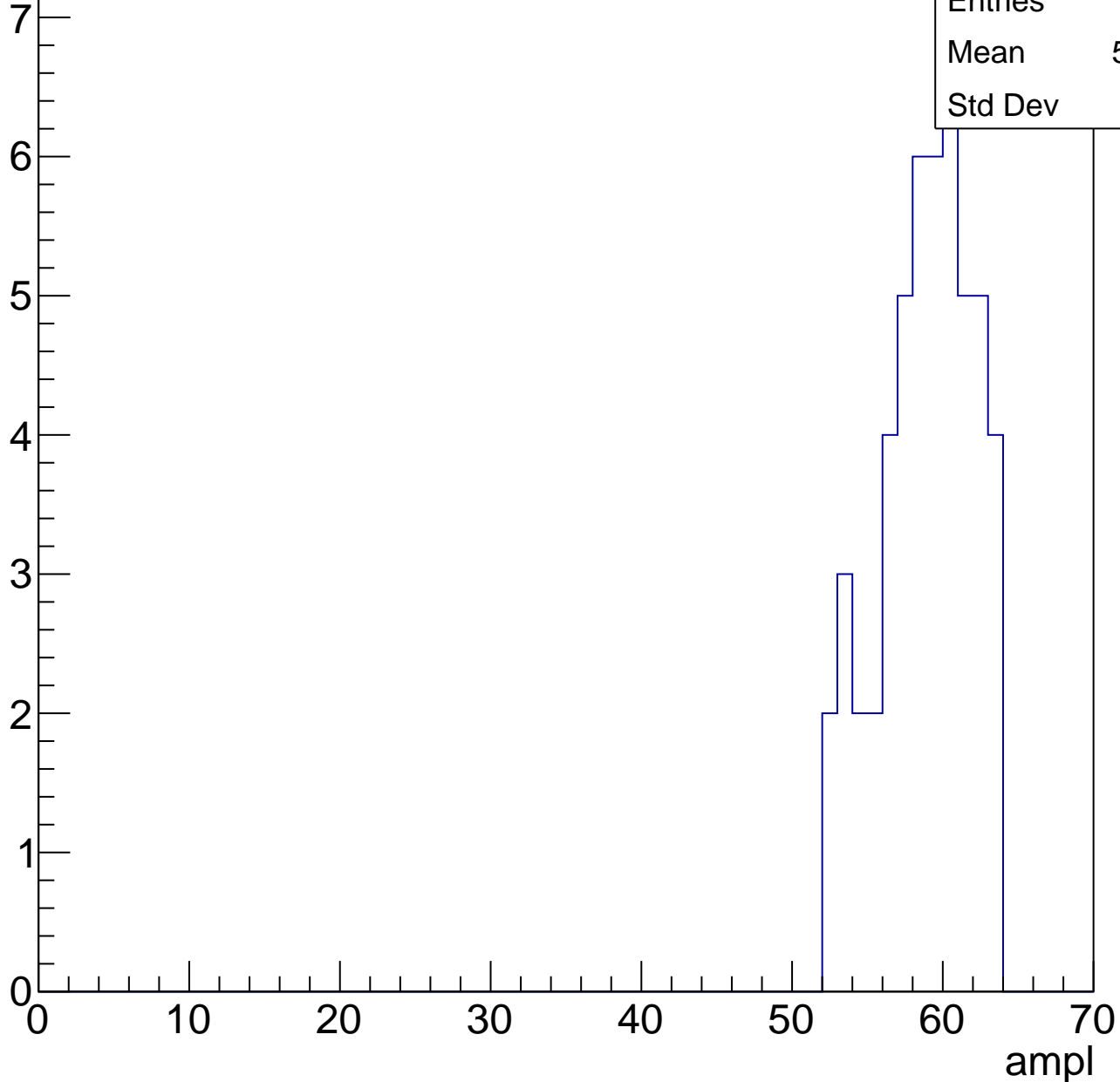
Entries	50
Mean	53.86
Std Dev	3.47



B1L102S, U17-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

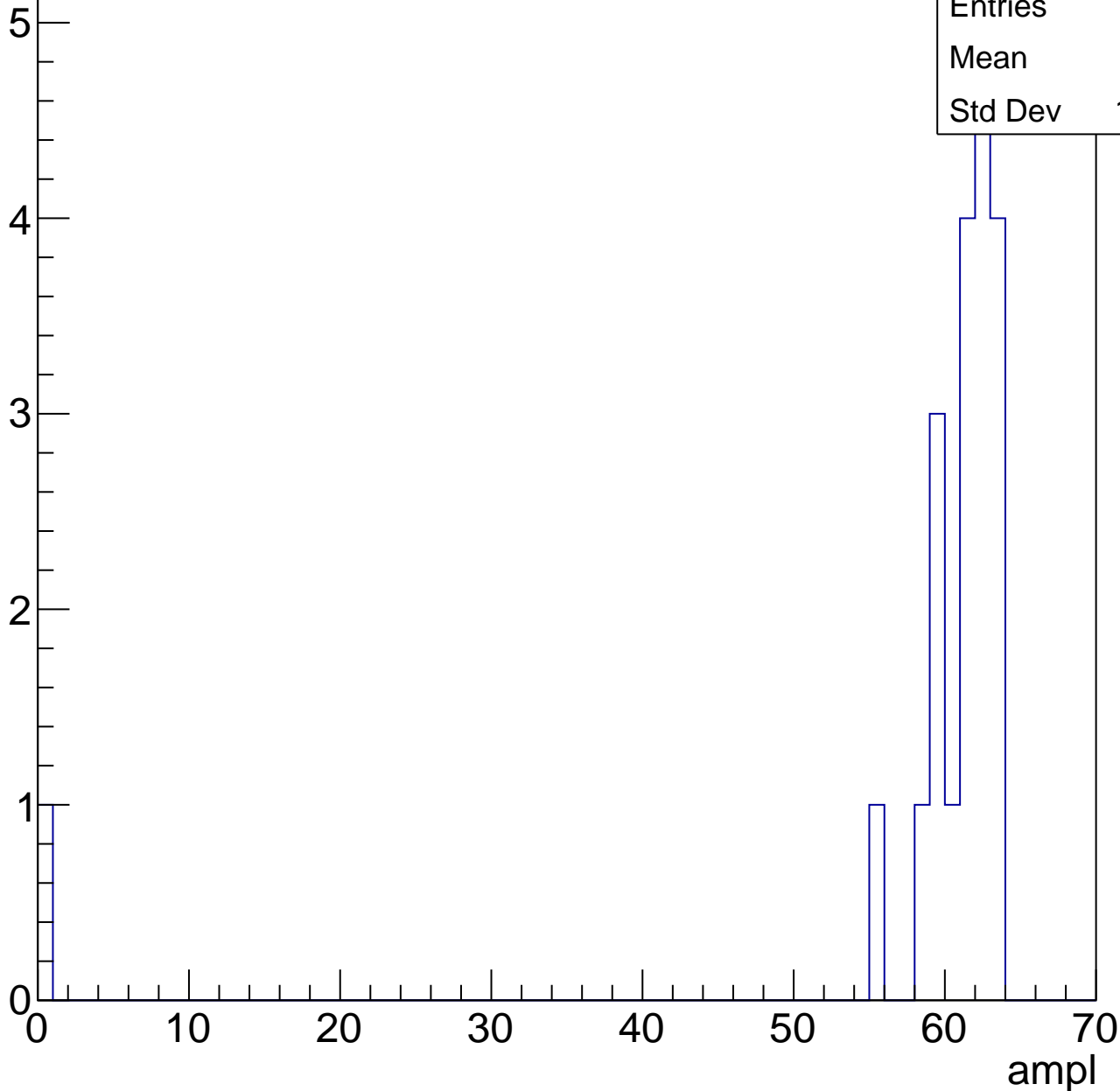


B1L102S, U17-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	57.8
Std Dev	13.41



B1L102S, U17-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	63
Std Dev	0

B1L102S, U17-ch11, adc0

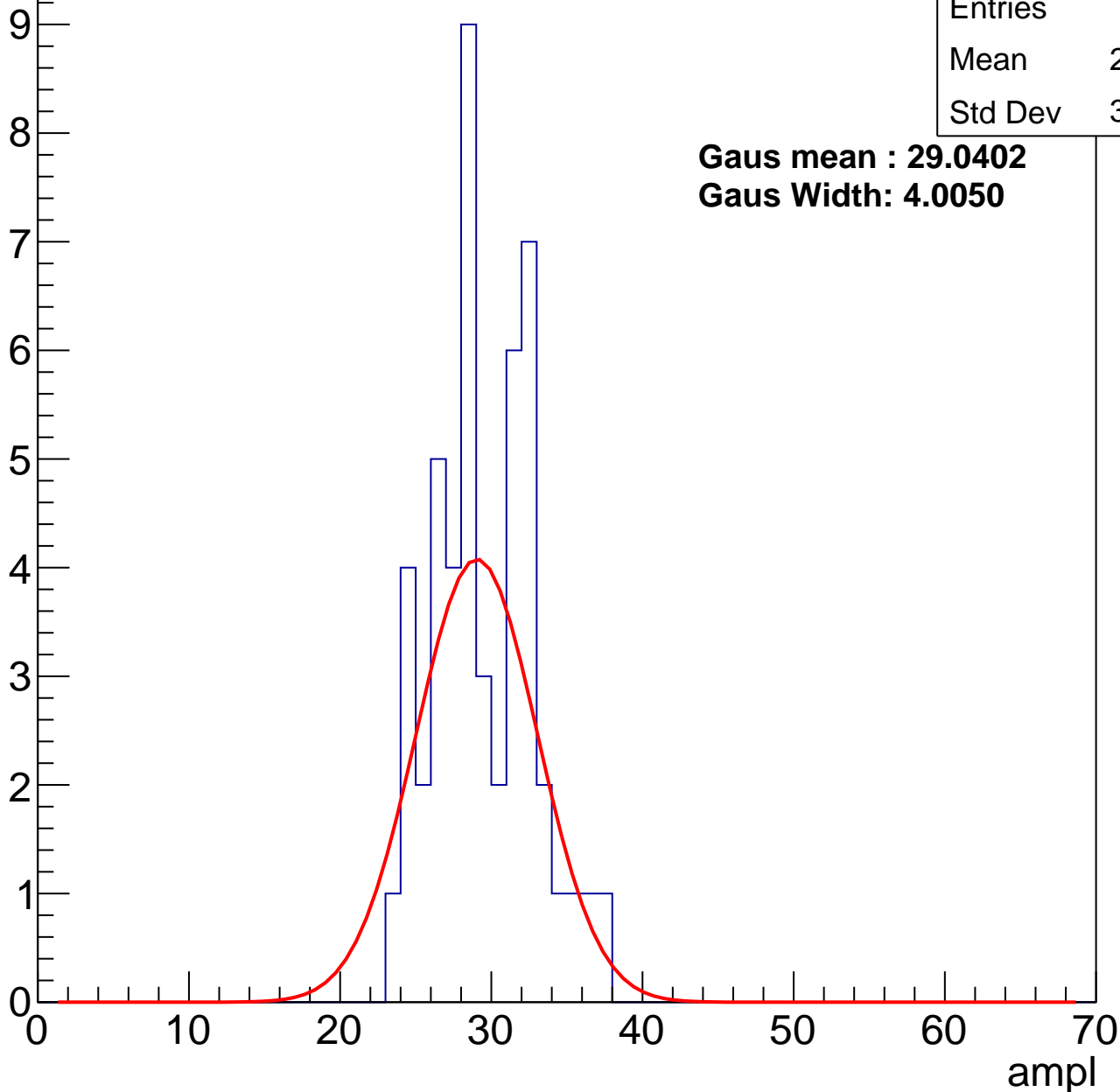
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	29.06
Std Dev	3.298

Gaus mean : 29.0402

Gaus Width: 4.0050



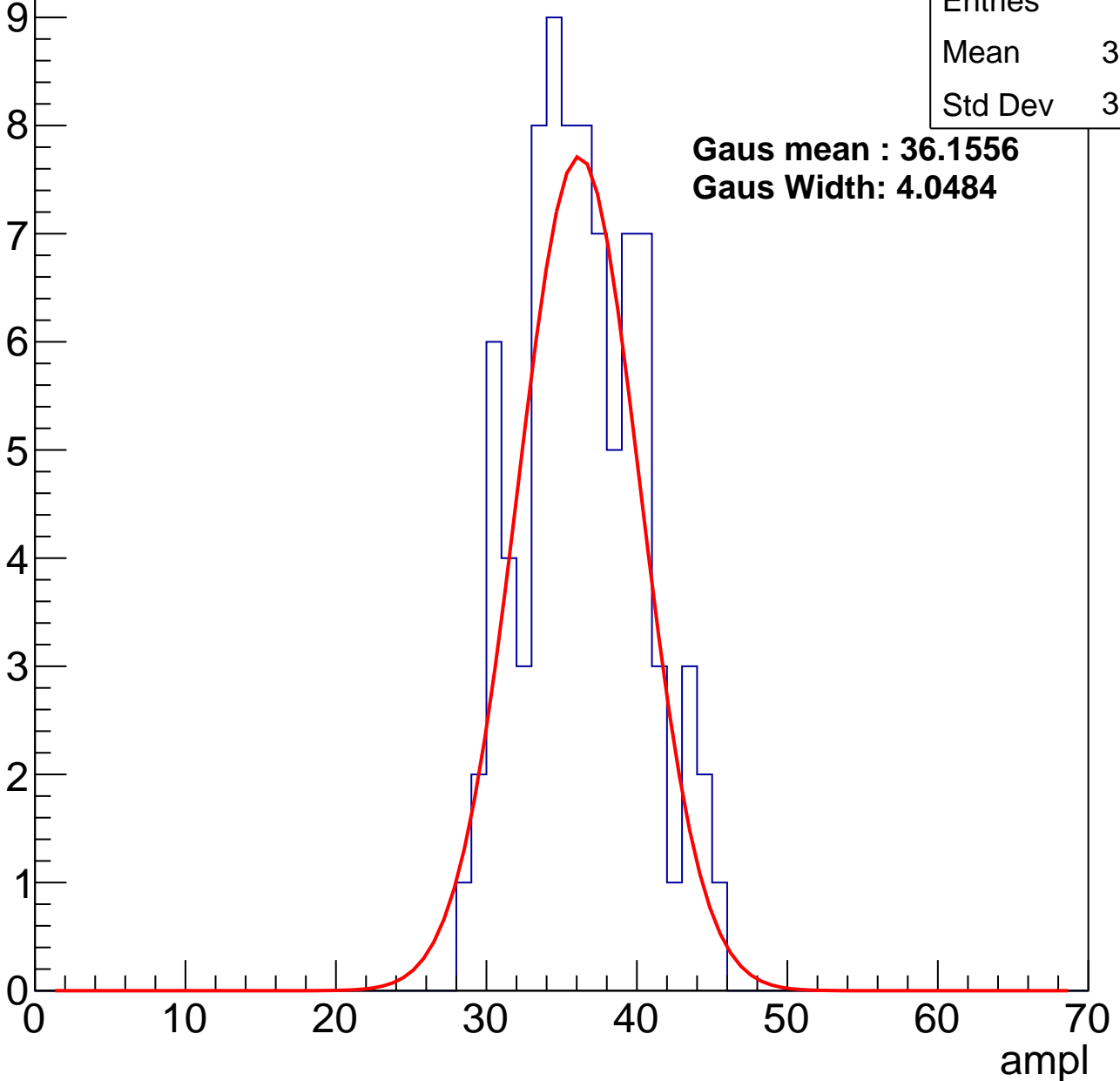
B1L102S, U17-ch11, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	35.92
Std Dev	3.926

Gaus mean : 36.1556
Gaus Width: 4.0484



B1L102S, U17-ch11, adc2

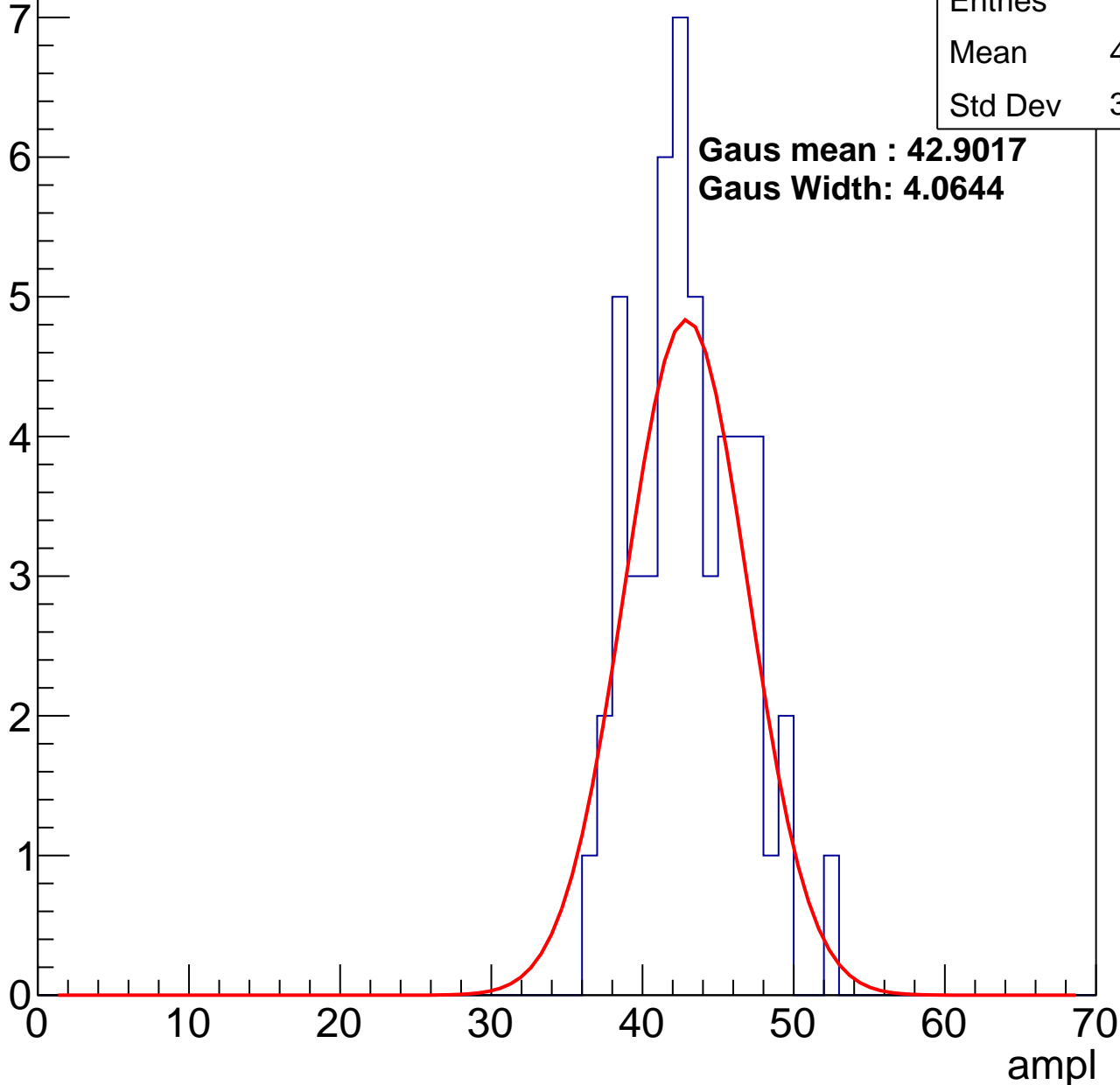
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.63
Std Dev	3.548

Gaus mean : 42.9017

Gaus Width: 4.0644

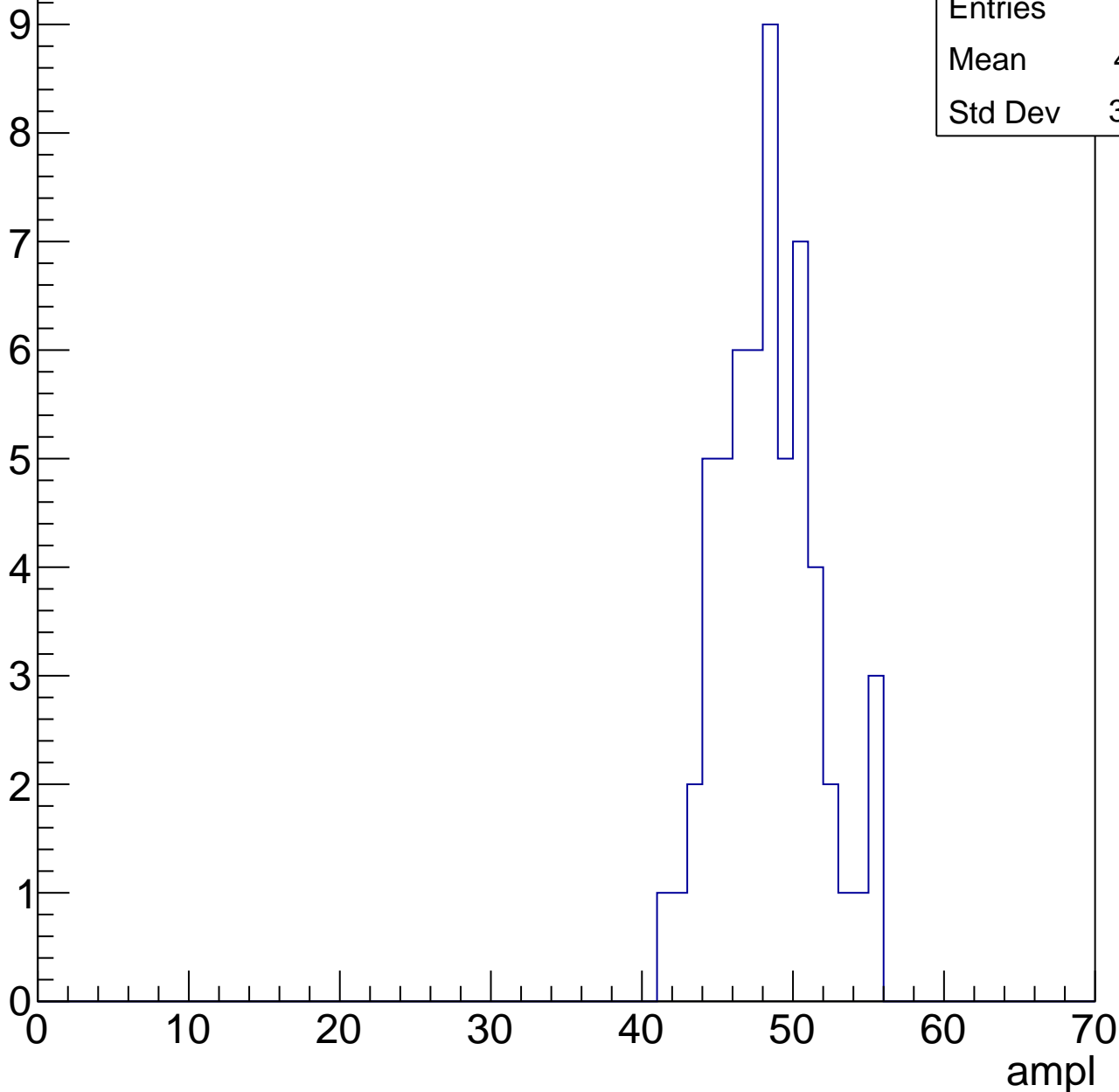


B1L102S, U17-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	47.91
Std Dev	3.212

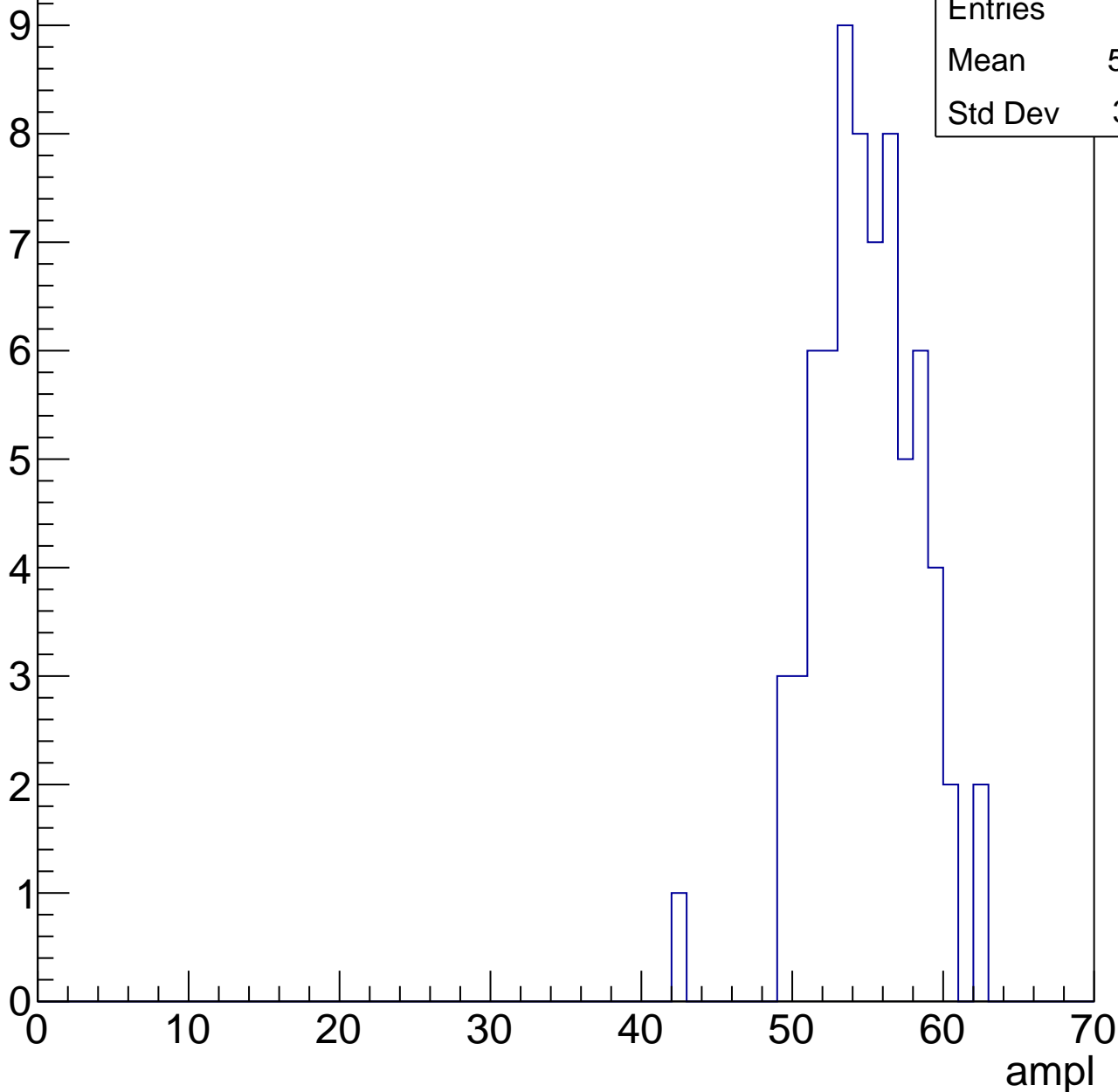


B1L102S, U17-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.46
Std Dev	3.421

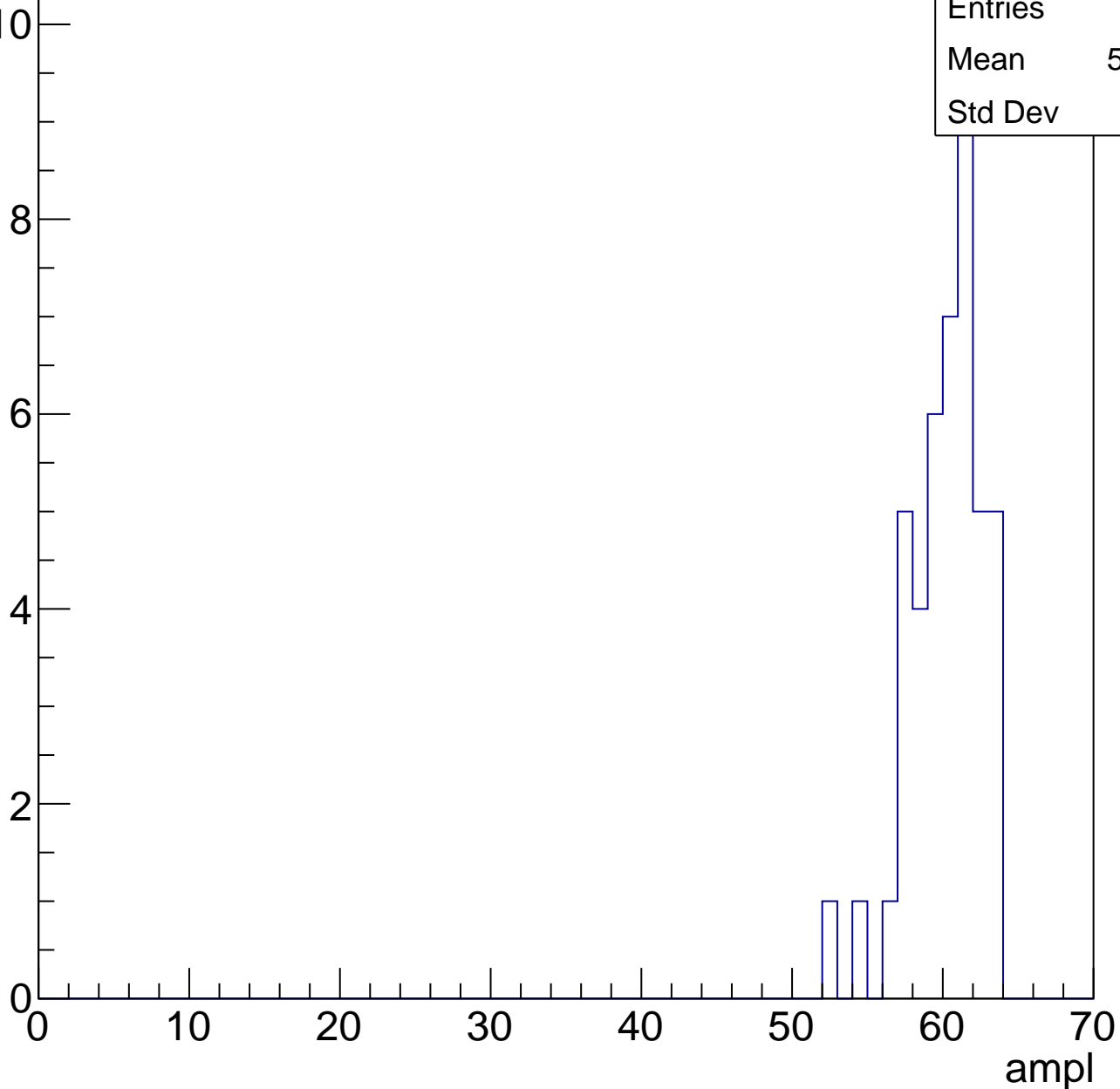


B1L102S, U17-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.73
Std Dev	2.38

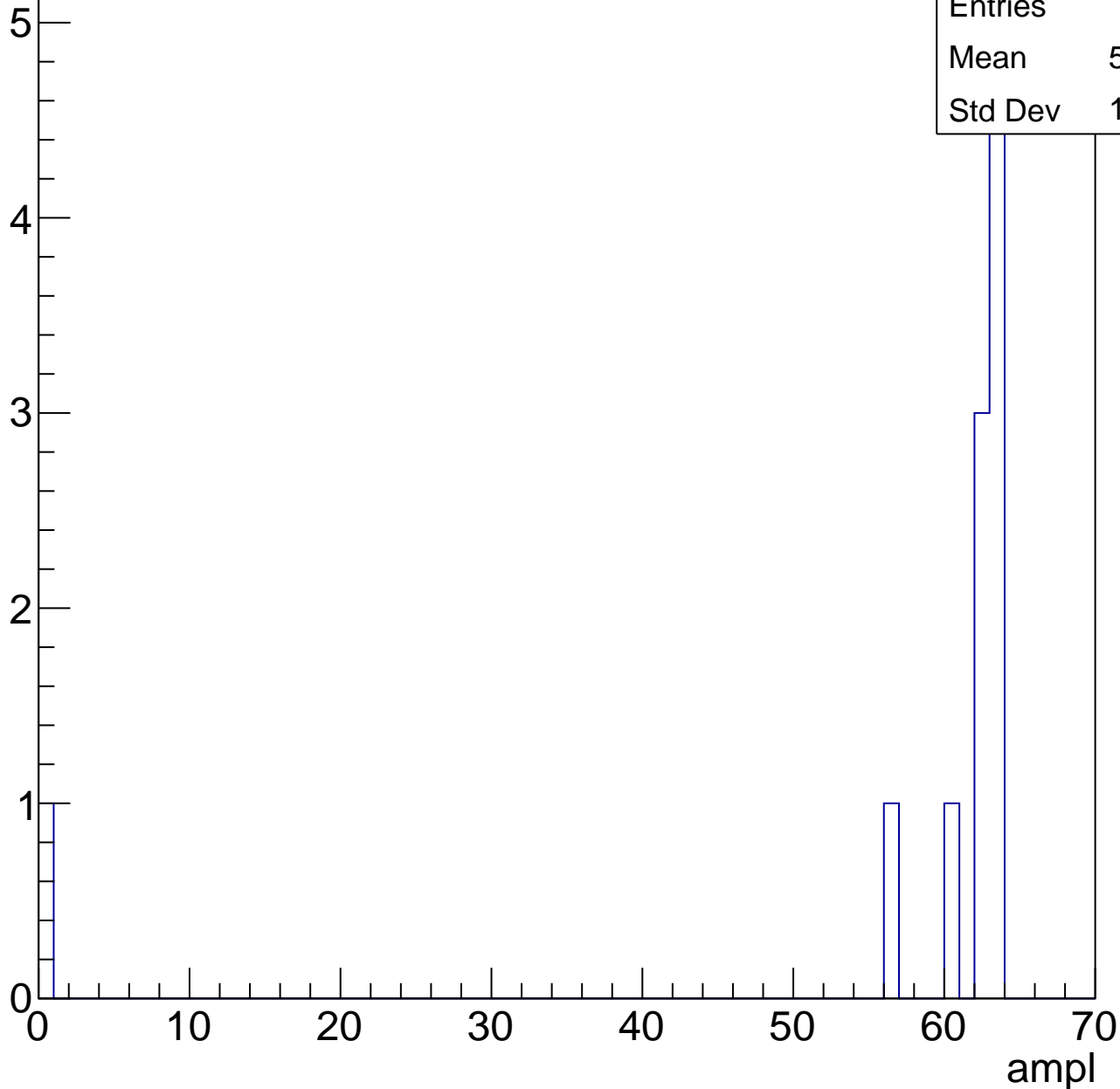


B1L102S, U17-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	56.09
Std Dev	17.85



B1L102S, U17-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch12, adc0

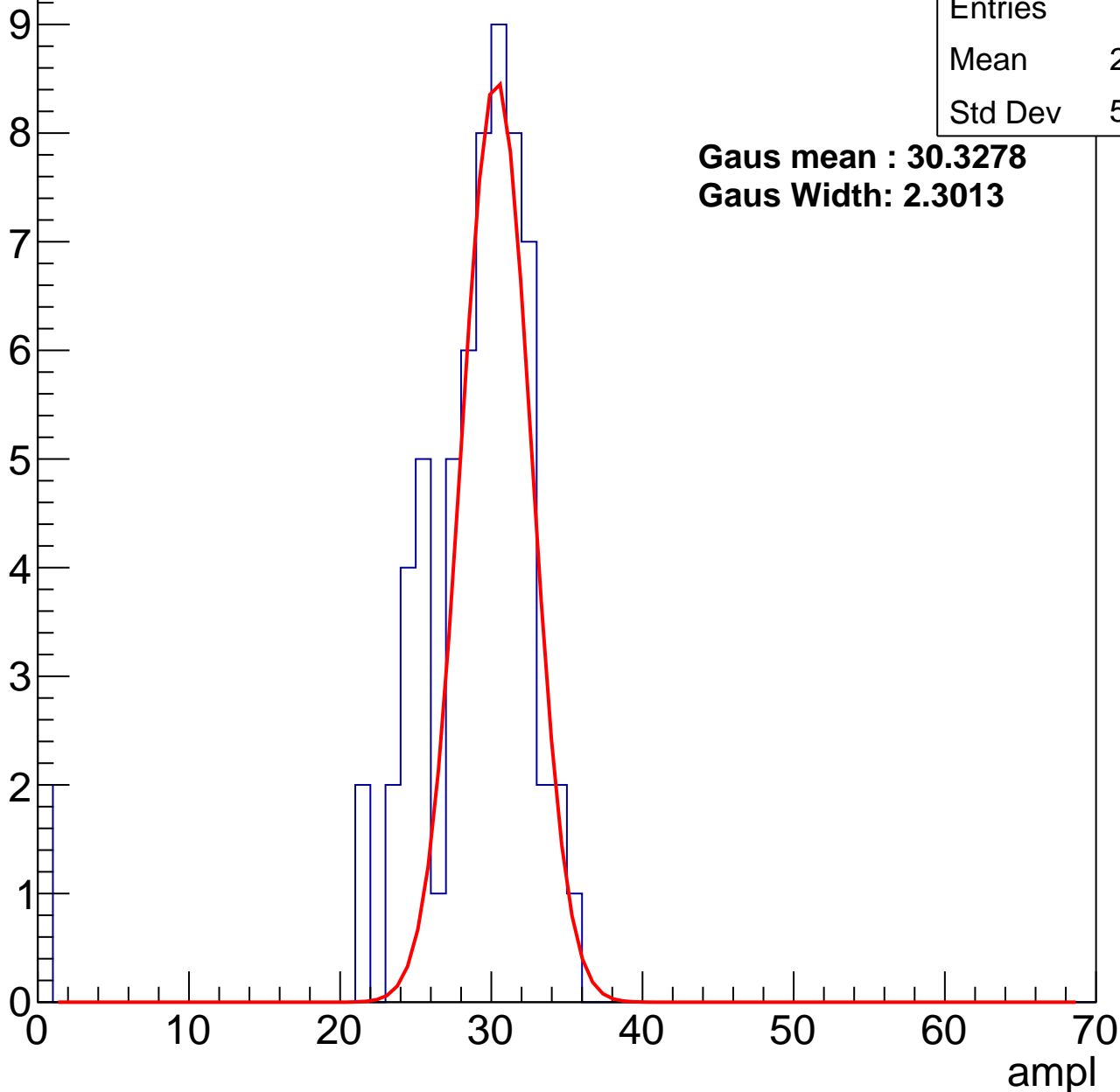
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.83
Std Dev	5.904

Gaus mean : 30.3278

Gaus Width: 2.3013



B1L102S, U17-ch12, adc1

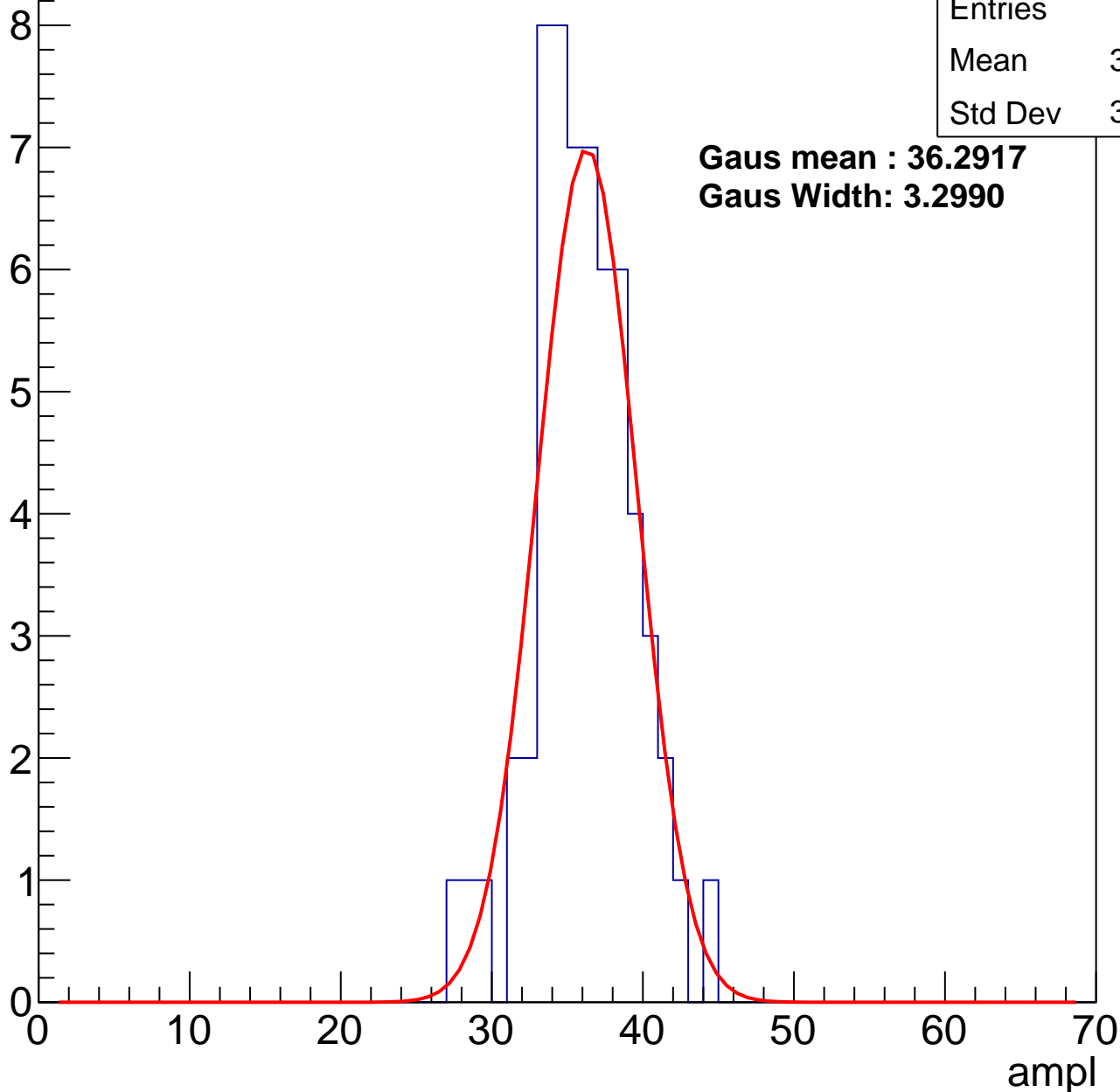
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.62
Std Dev	3.272

Gaus mean : 36.2917

Gaus Width: 3.2990



B1L102S, U17-ch12, adc2

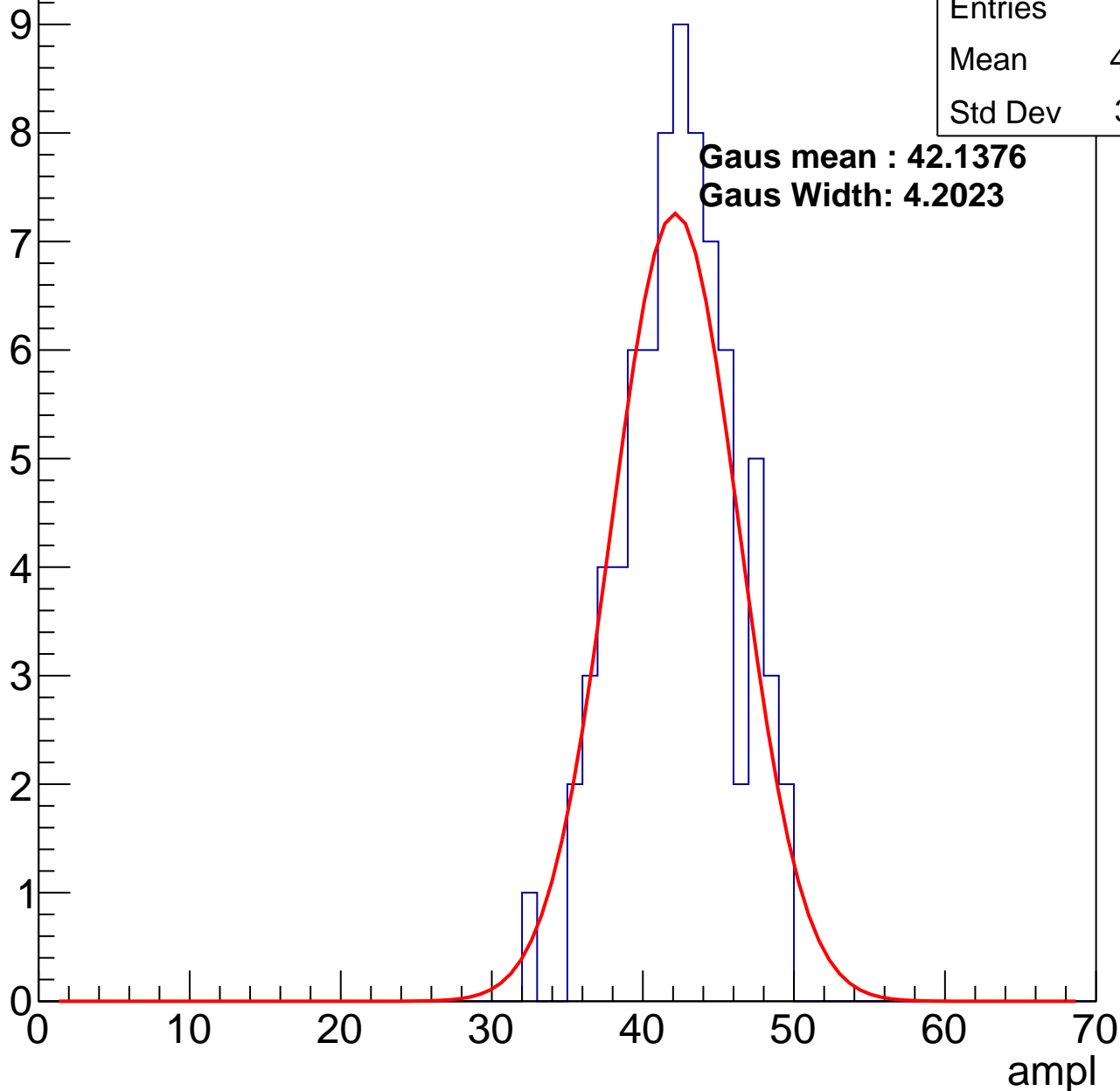
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	41.86
Std Dev	3.641

Gaus mean : 42.1376

Gaus Width: 4.2023

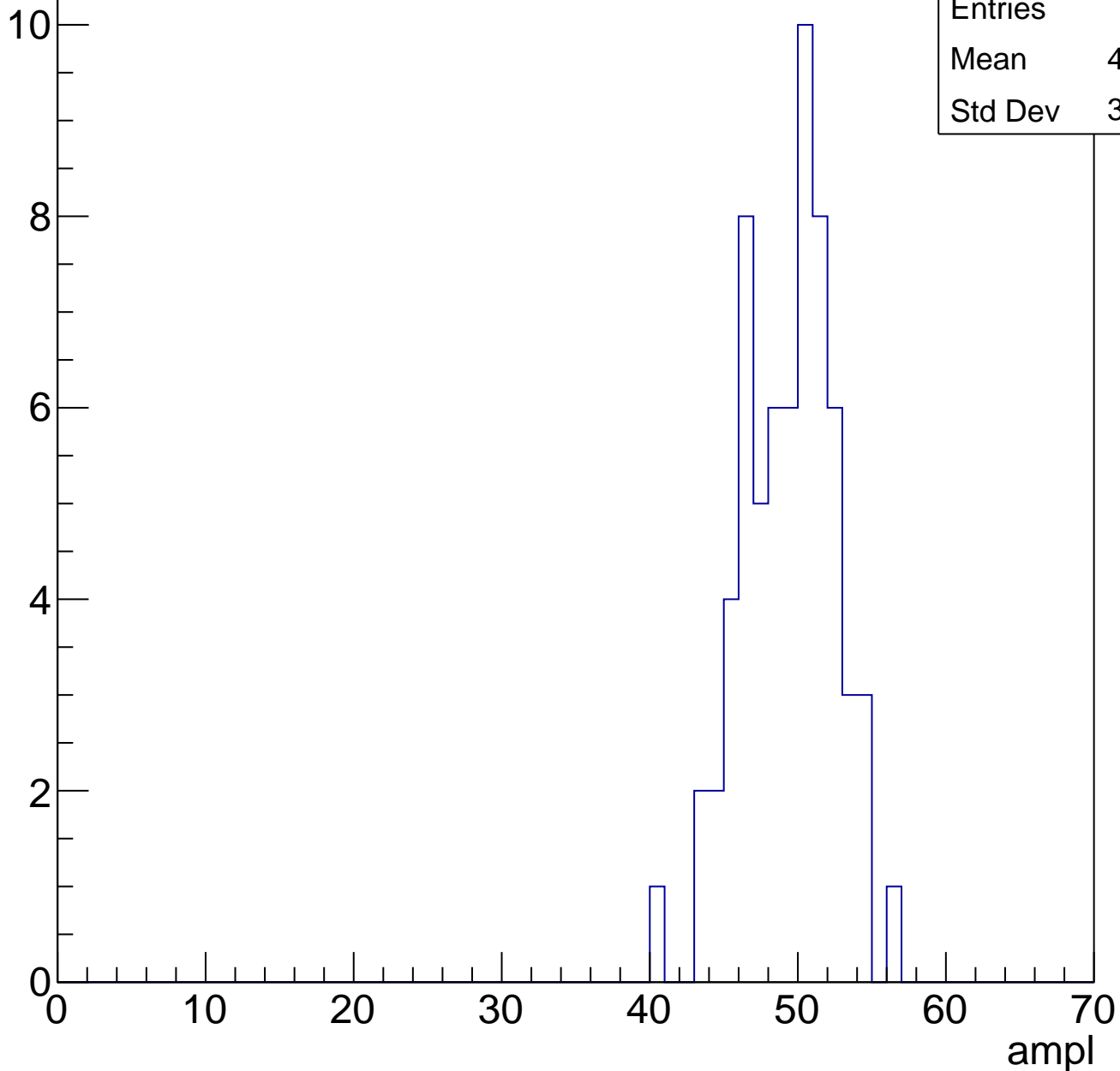


B1L102S, U17-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	48.86
Std Dev	3.118

Entry

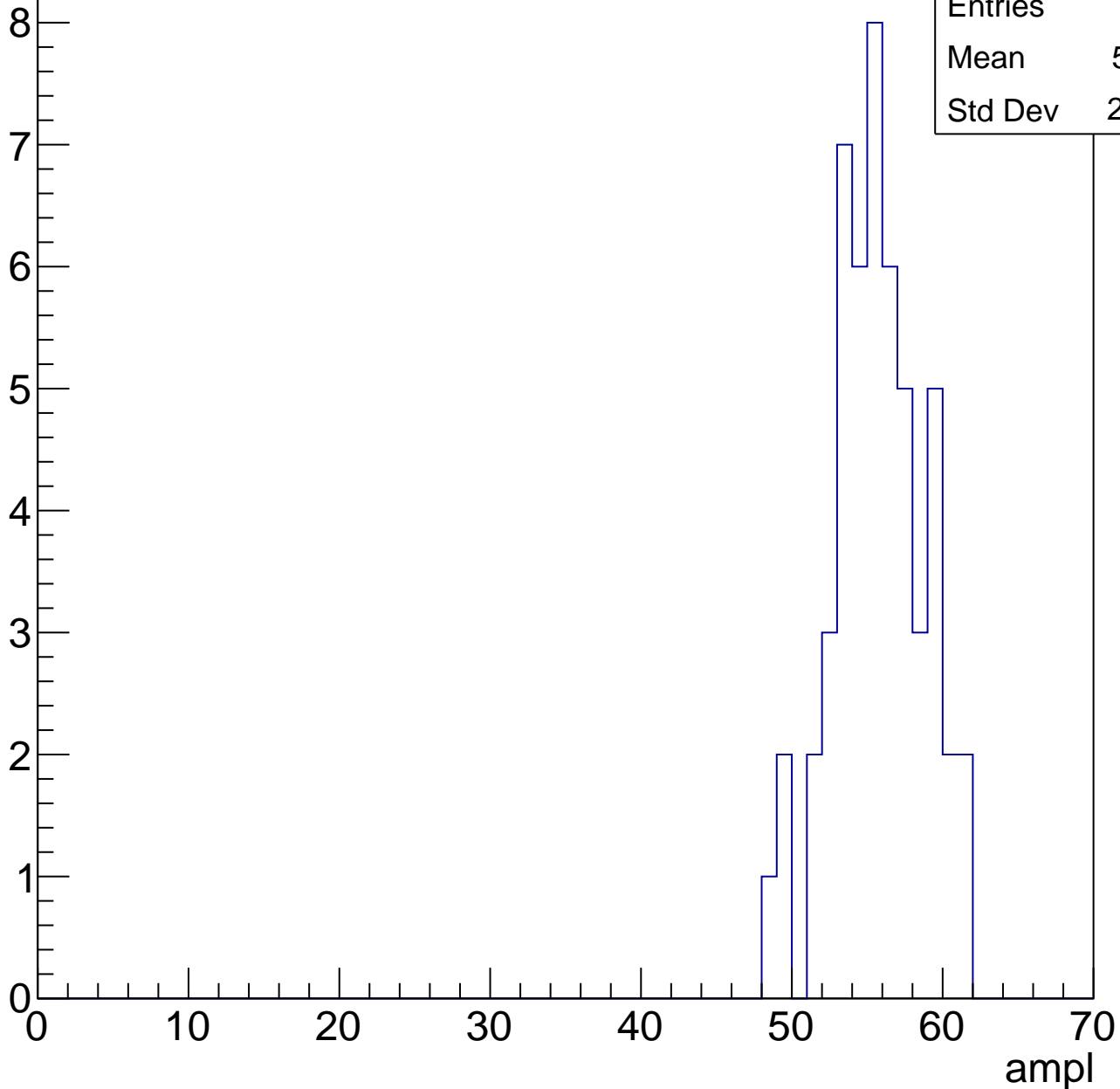


B1L102S, U17-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.21
Std Dev	2.996

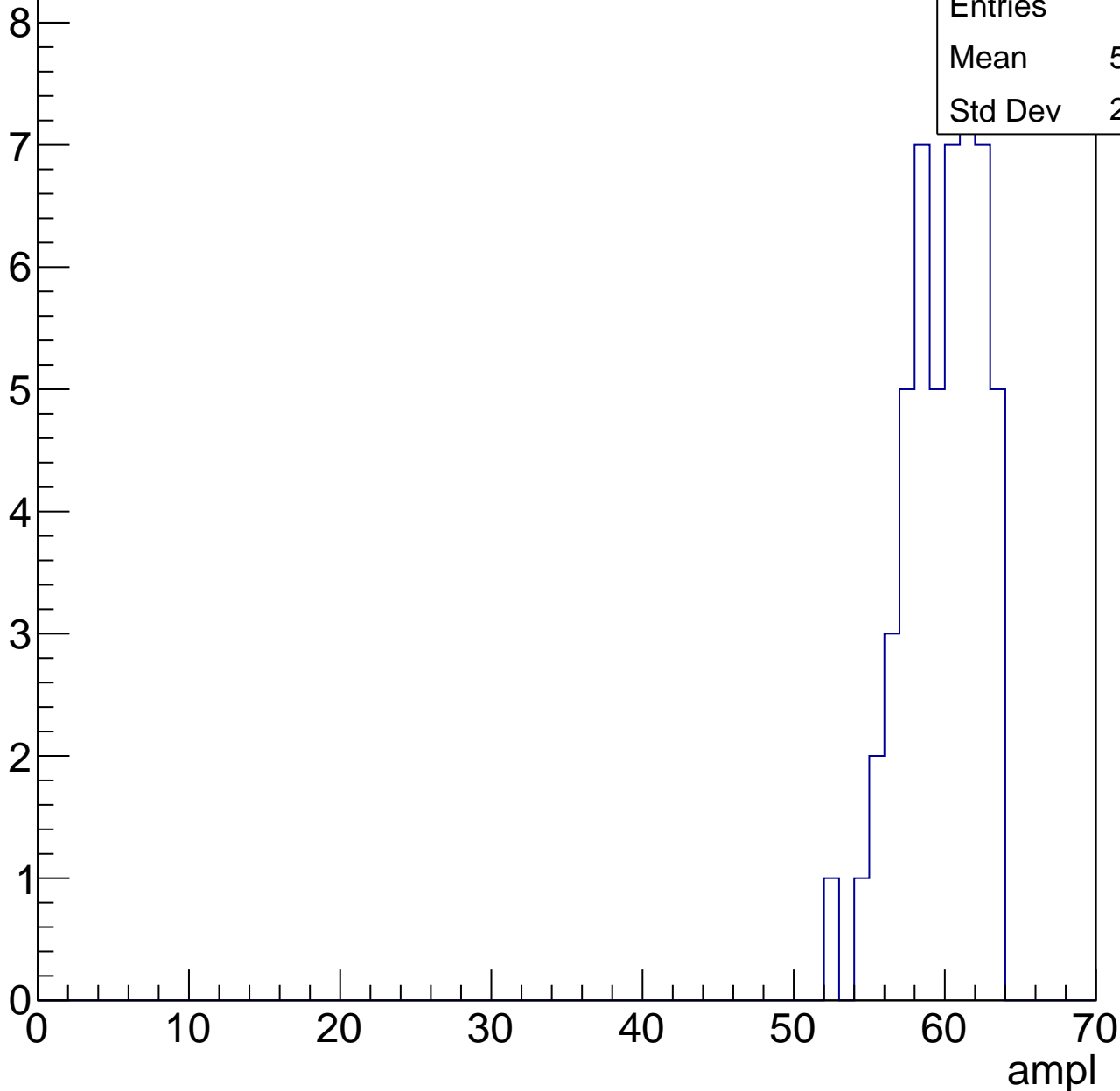


B1L102S, U17-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.35
Std Dev	2.565

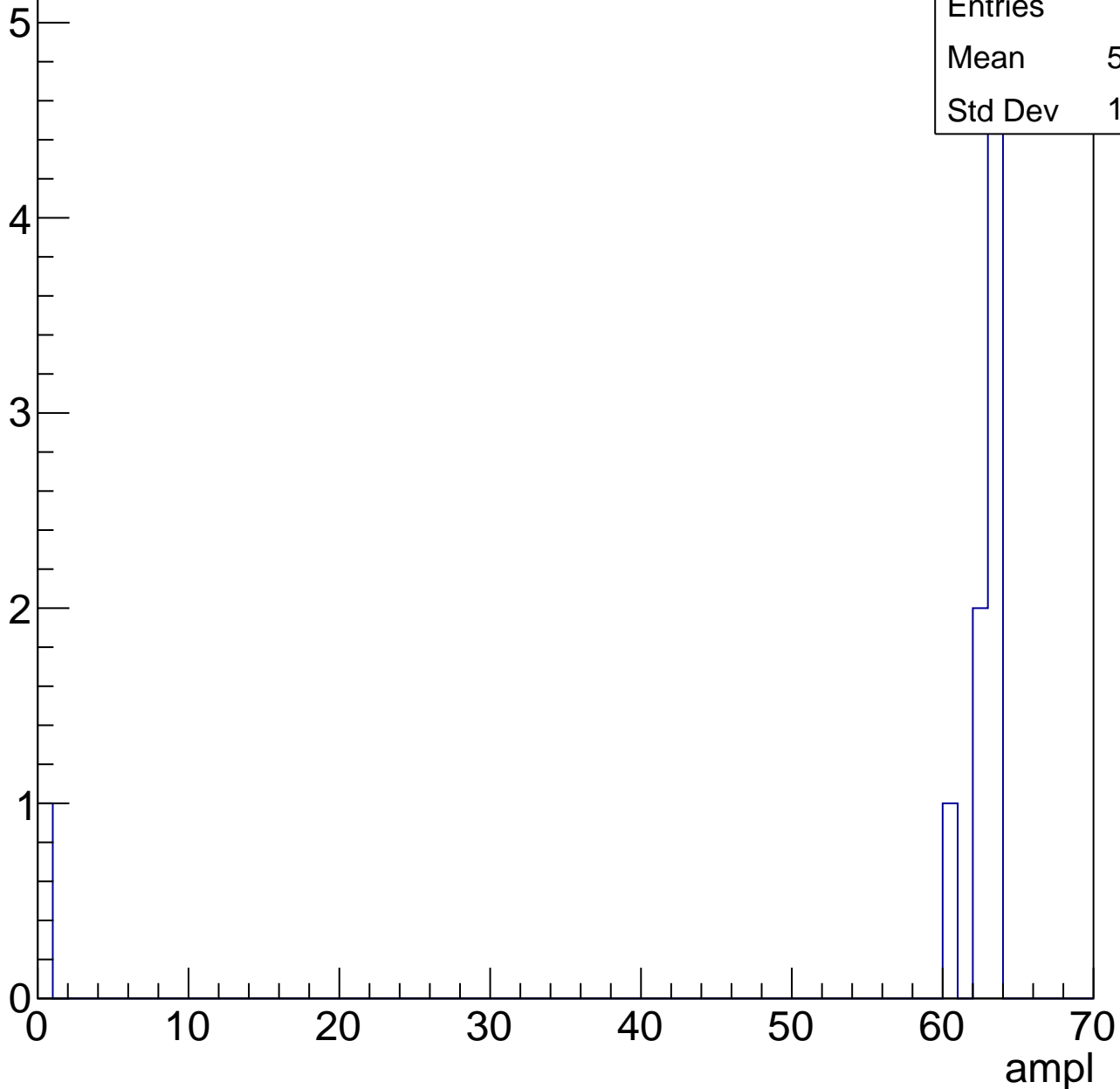


B1L102S, U17-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	55.44
Std Dev	19.62



B1L102S, U17-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch13, adc0

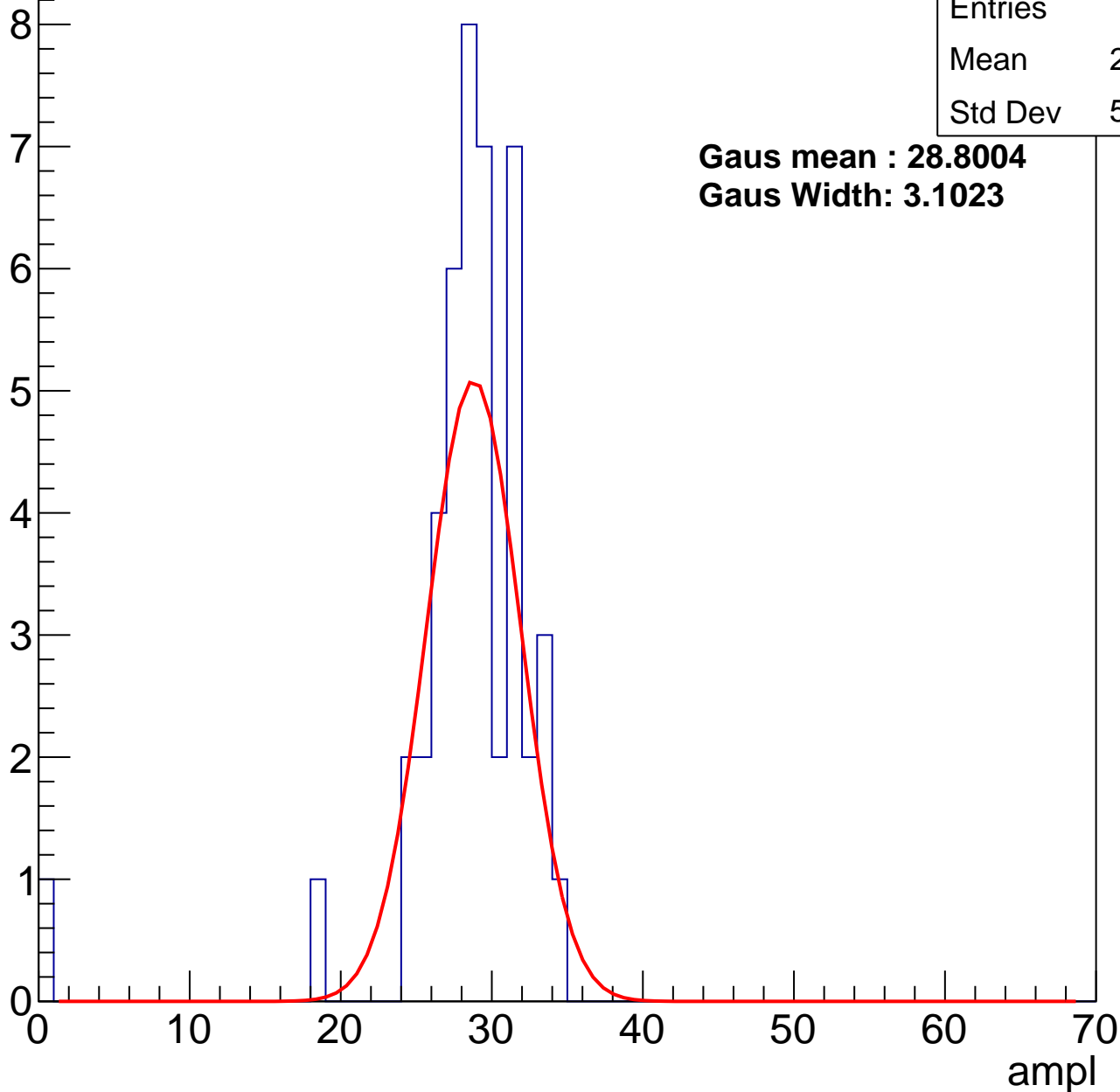
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	27.89
Std Dev	5.057

Gaus mean : 28.8004

Gaus Width: 3.1023



B1L102S, U17-ch13, adc1

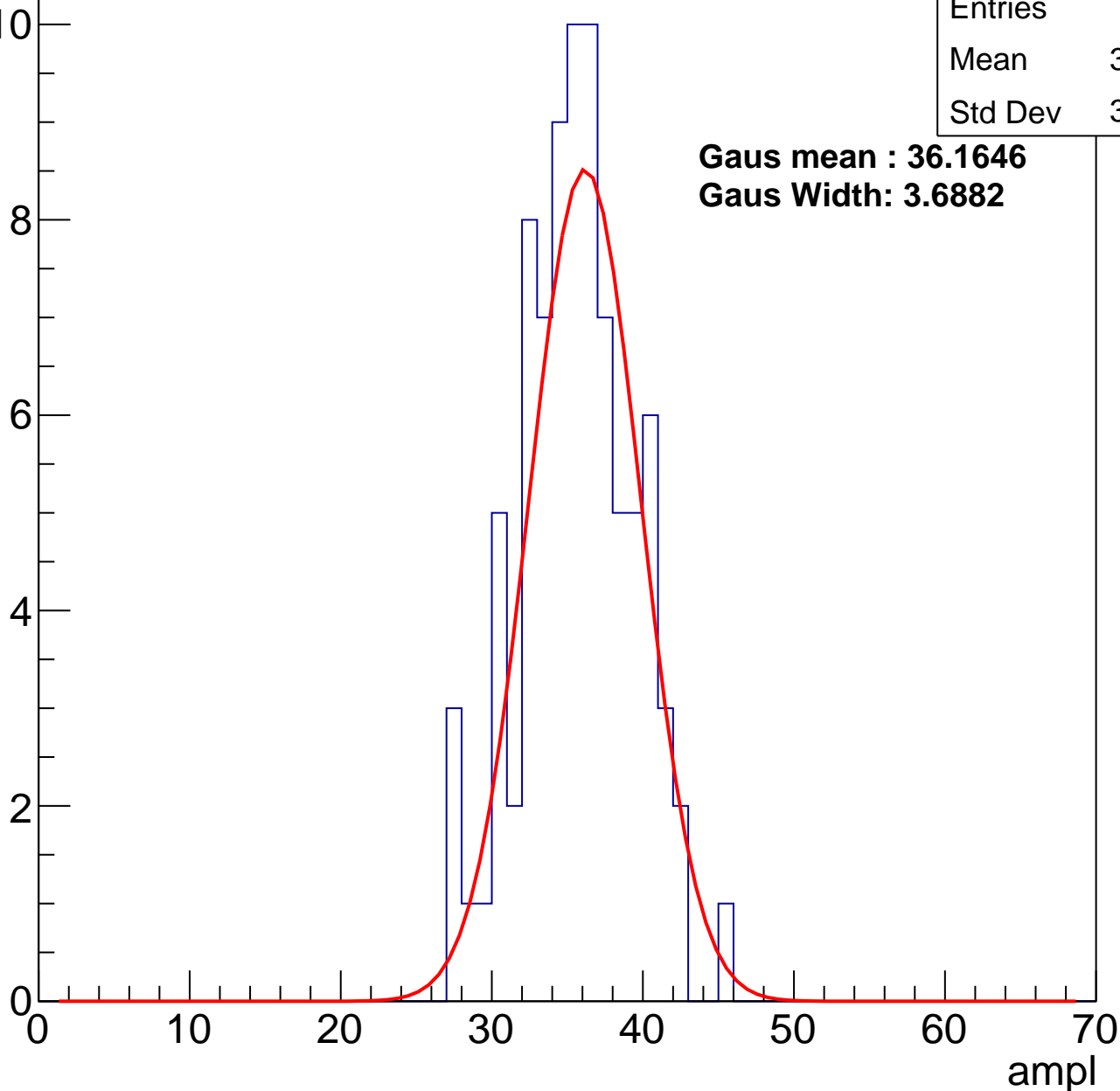
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	35.16
Std Dev	3.694

Gaus mean : 36.1646

Gaus Width: 3.6882



B1L102S, U17-ch13, adc2

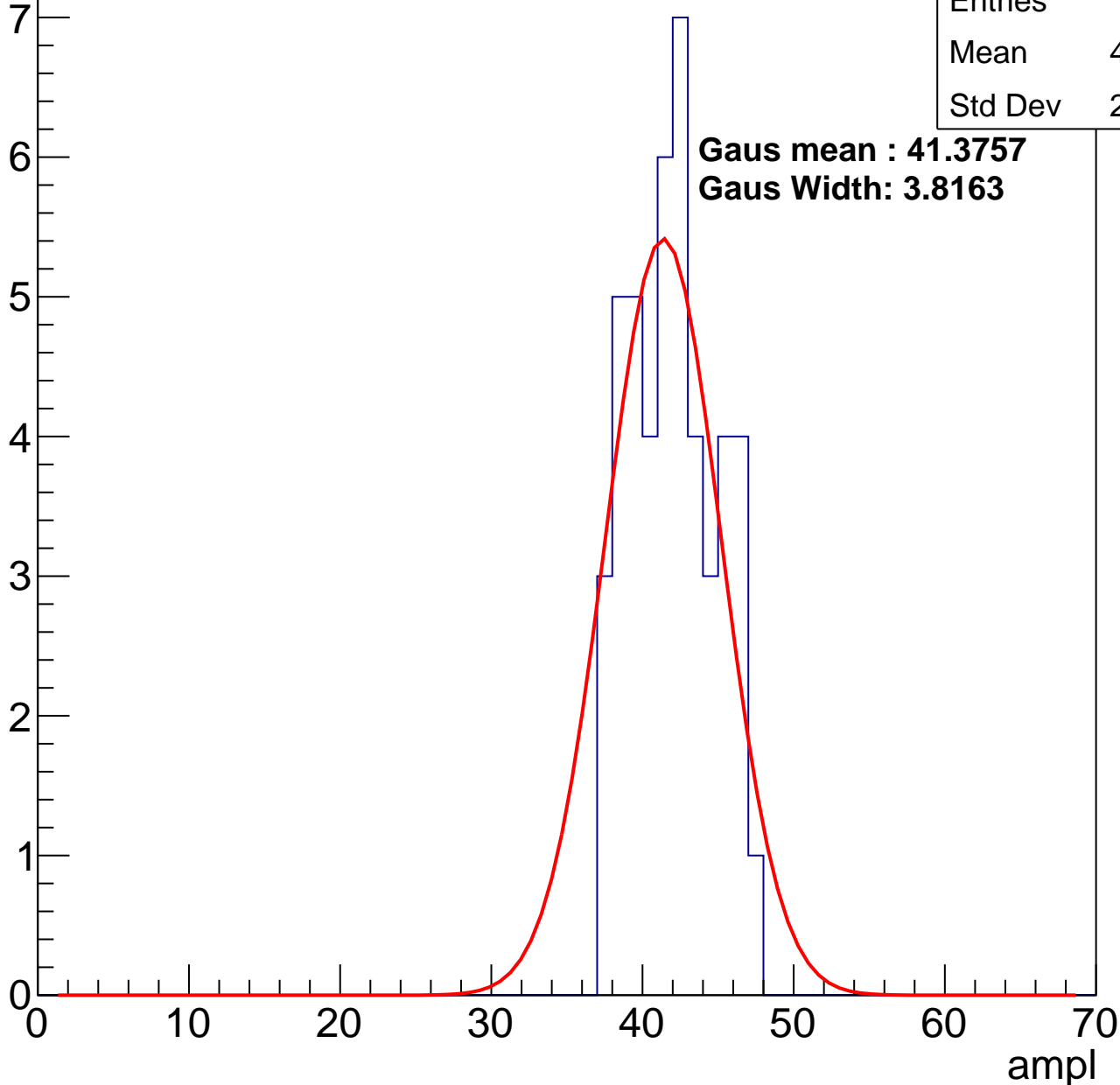
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	41.54
Std Dev	2.772

Gaus mean : 41.3757

Gaus Width: 3.8163



B1L102S, U17-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	48.39
Std Dev	3.404

Entry

10

8

6

4

2

0

0

10

20

30

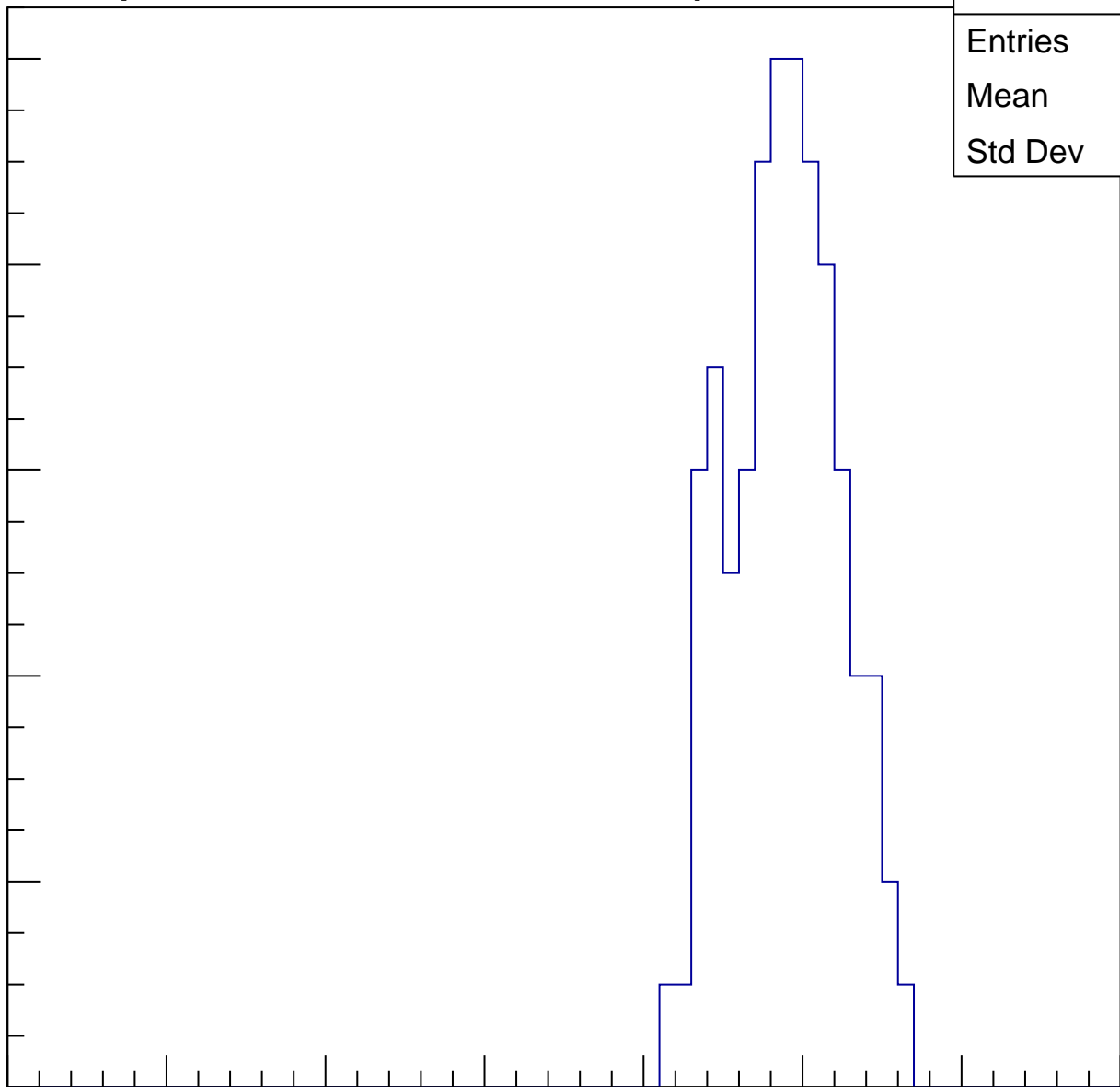
40

50

60

70

ampl

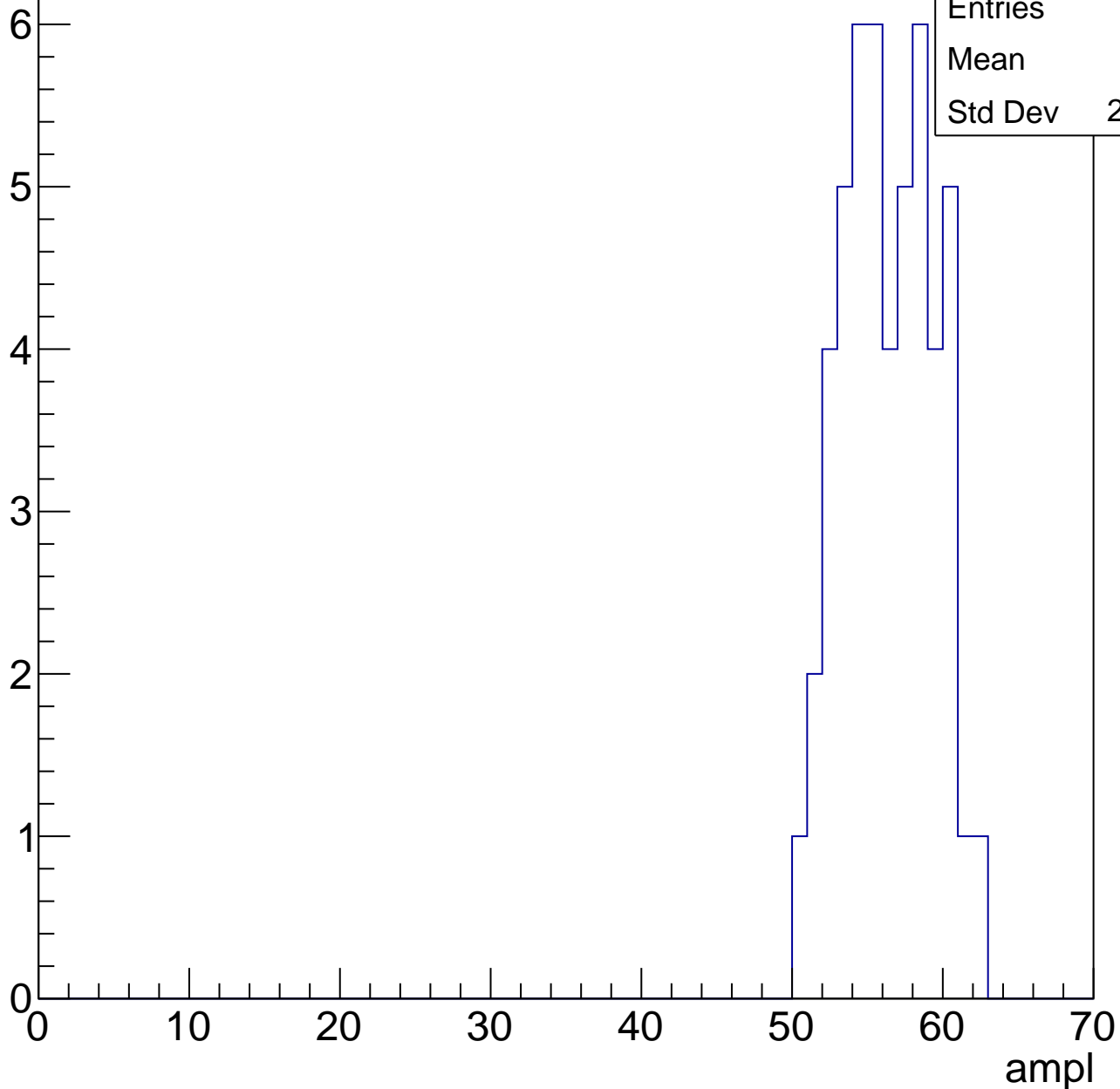


B1L102S, U17-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.9
Std Dev	2.934

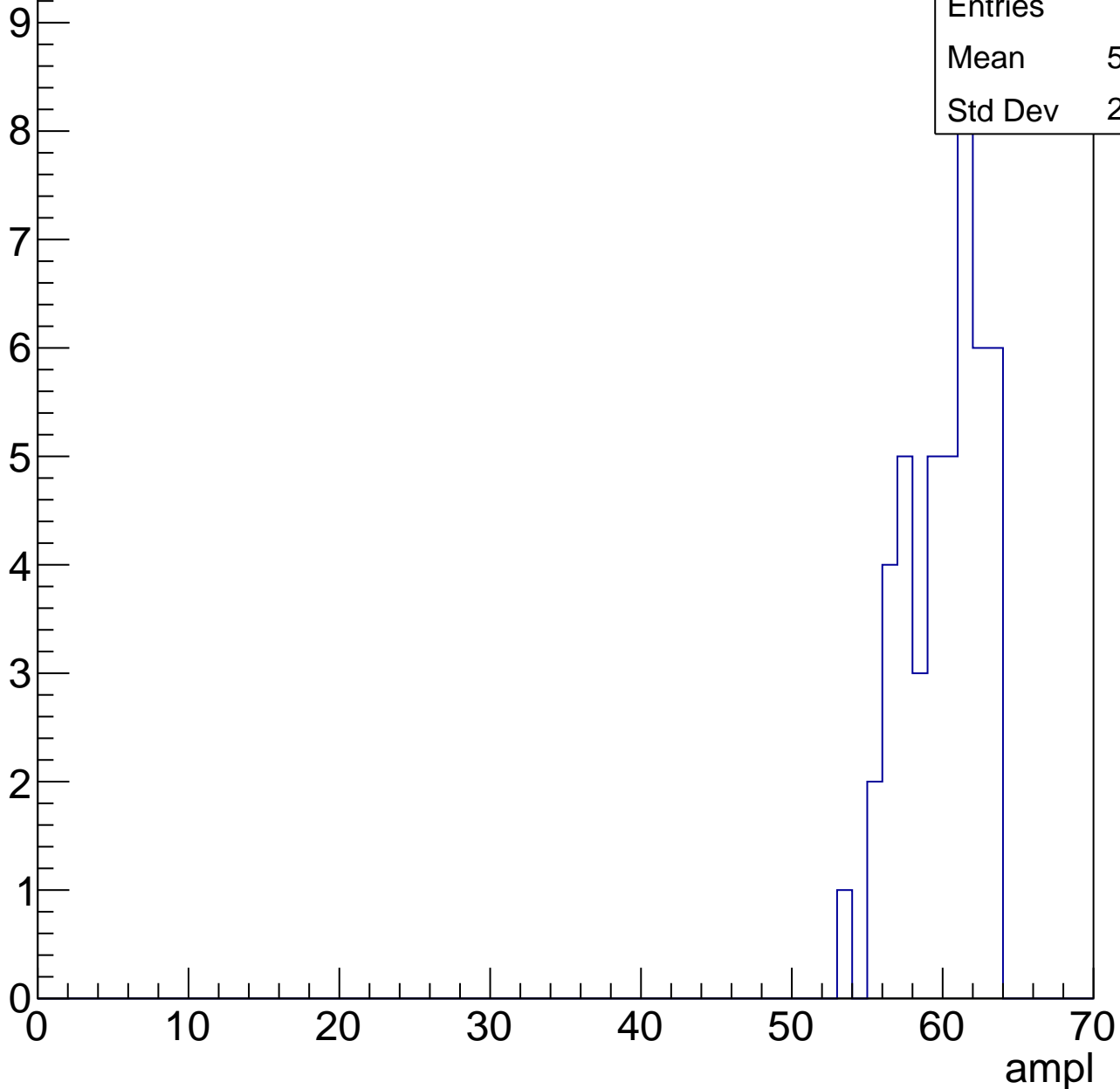


B1L102S, U17-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

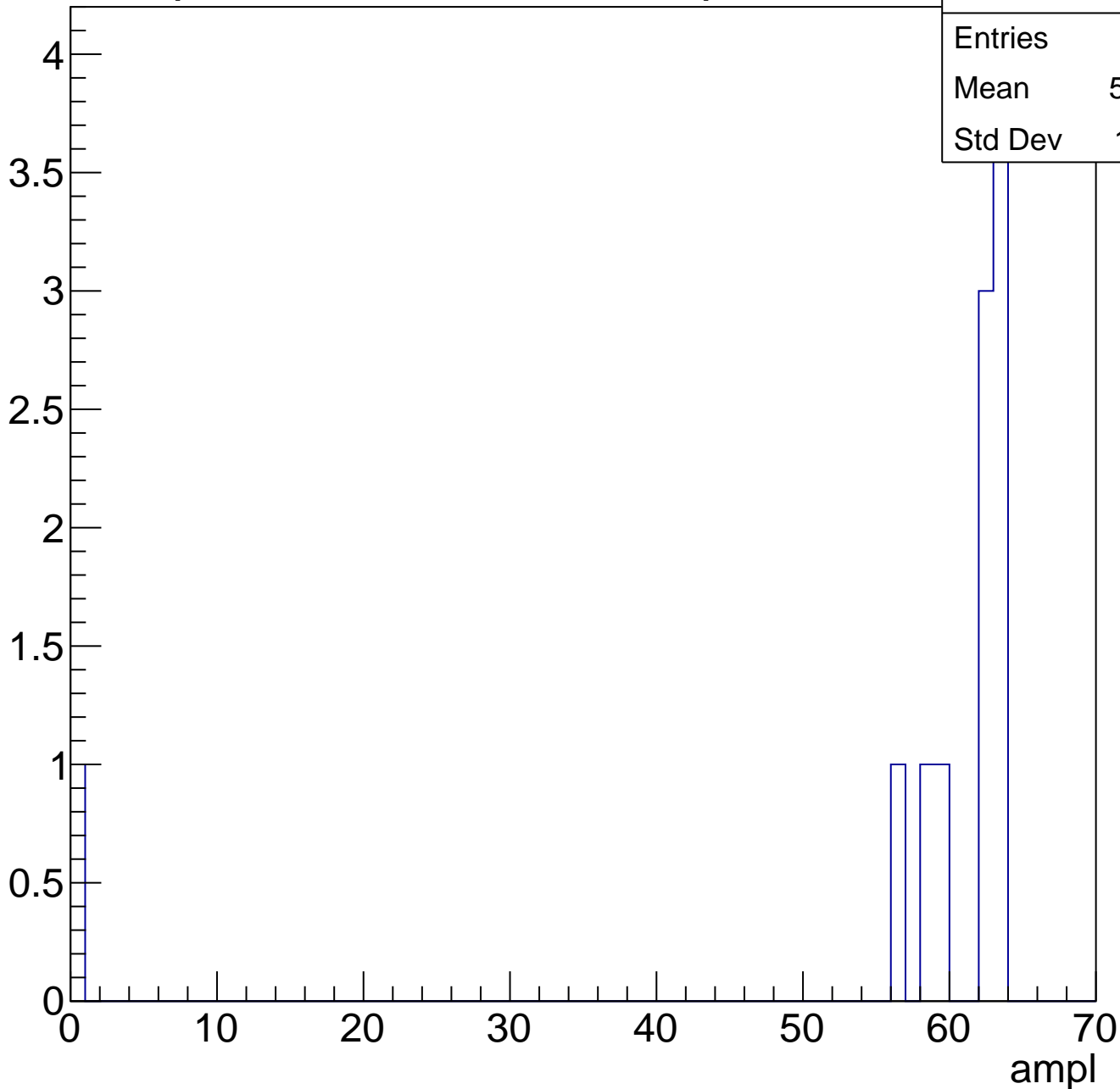
Entries	46
Mean	59.57
Std Dev	2.568



B1L102S, U17-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch14, adc0

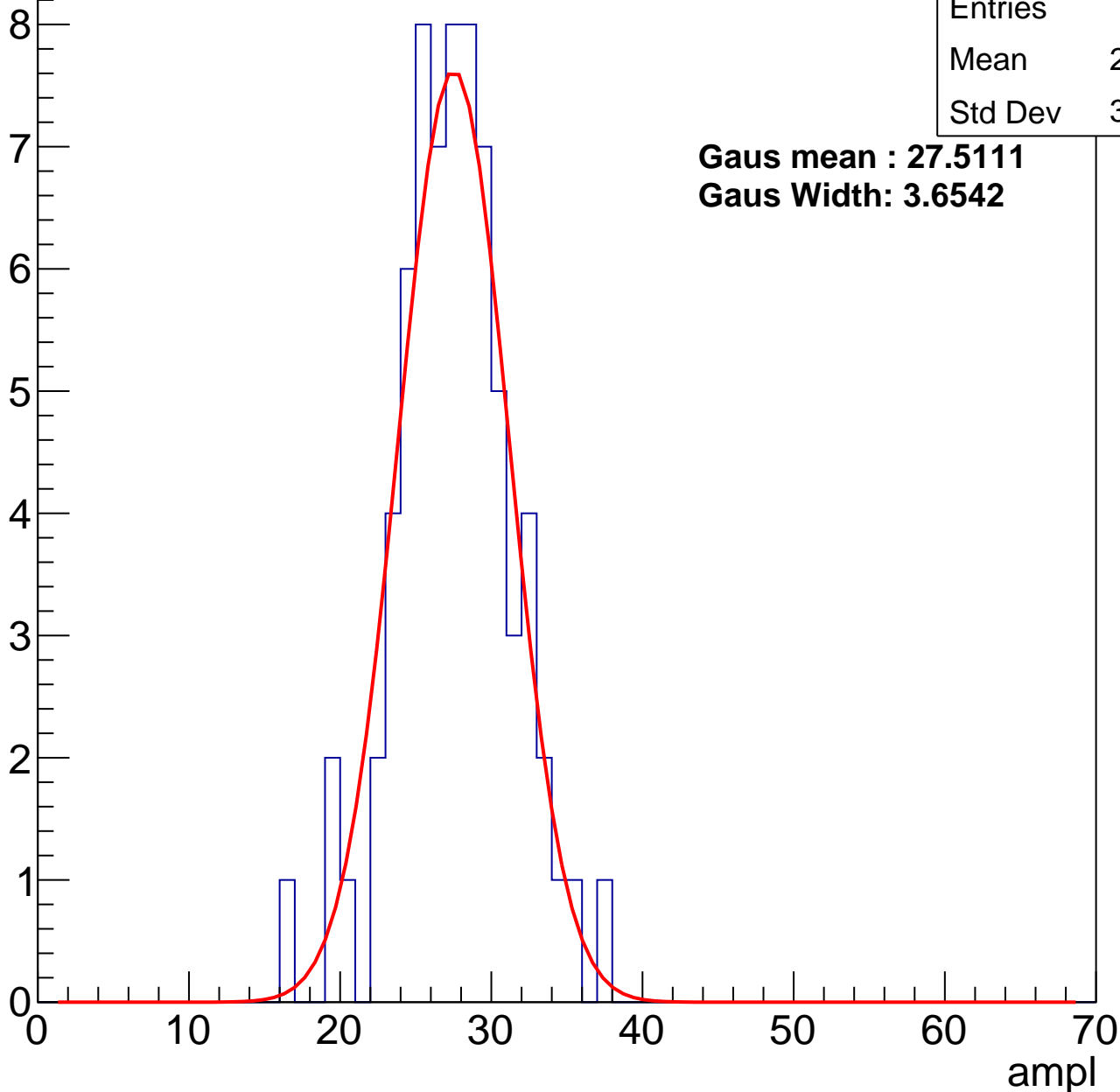
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.07
Std Dev	3.788

Gaus mean : 27.5111

Gaus Width: 3.6542



B1L102S, U17-ch14, adc1

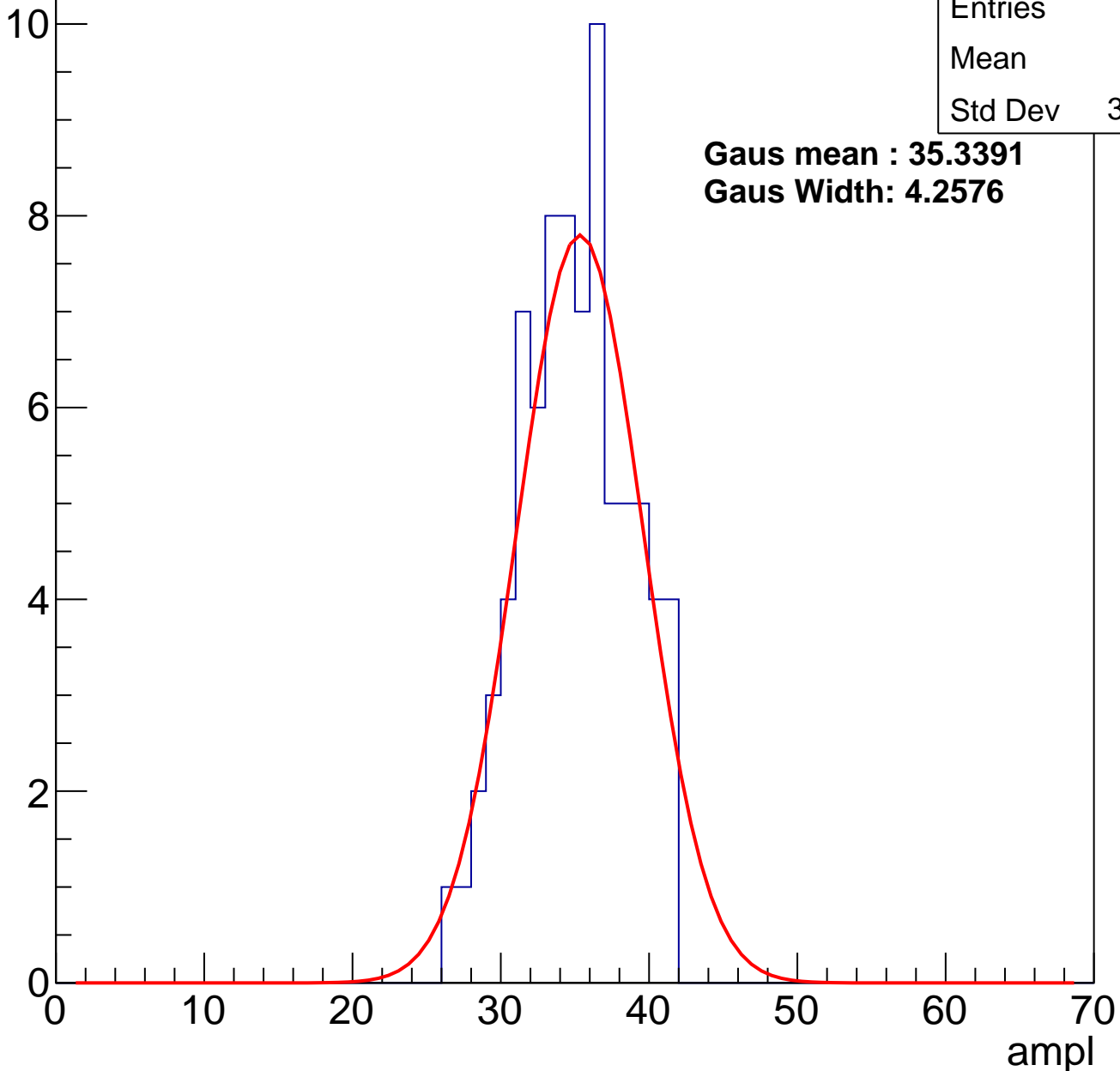
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	34.5
Std Dev	3.599

Gaus mean : 35.3391

Gaus Width: 4.2576

Entry



B1L102S, U17-ch14, adc2

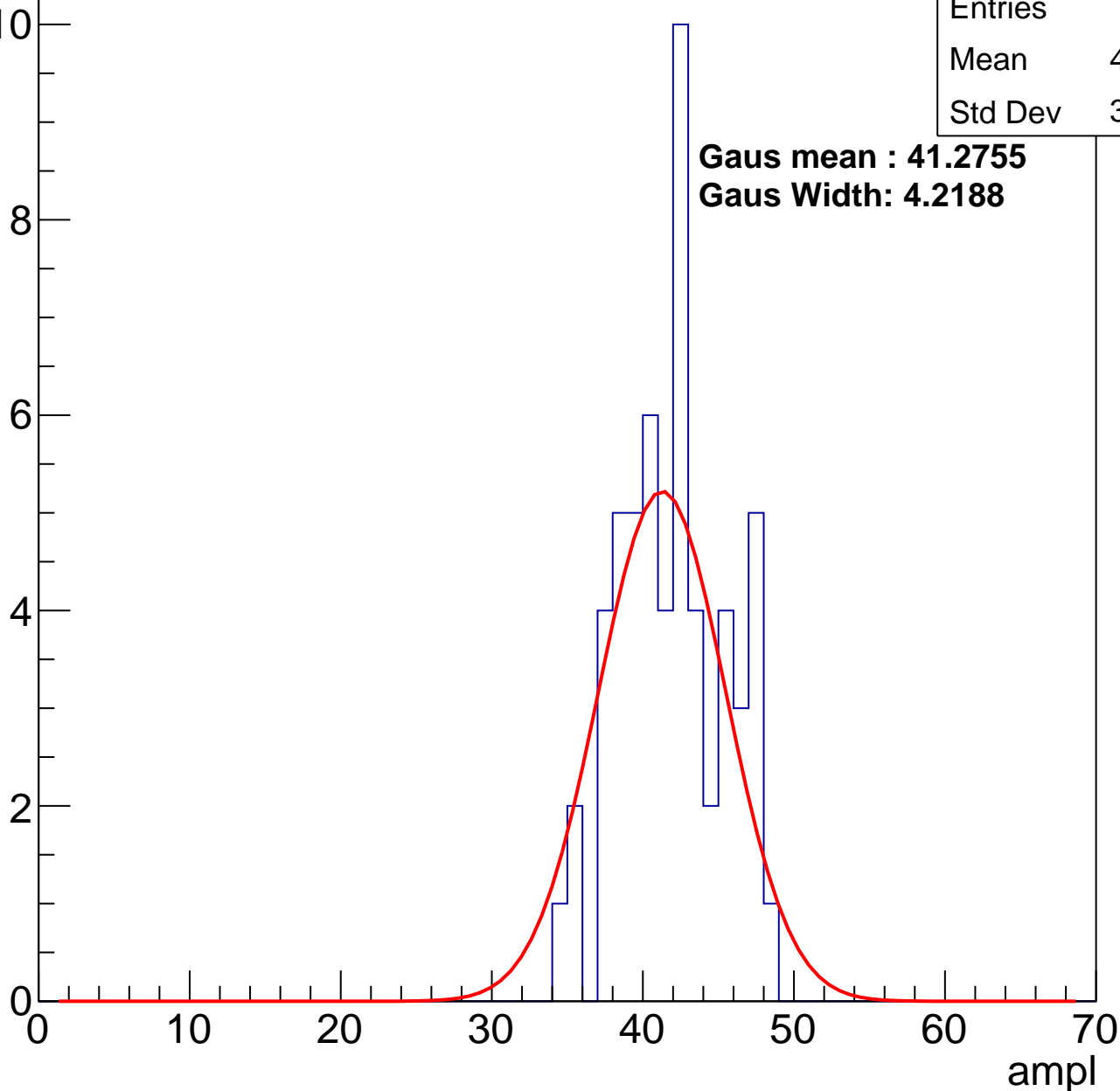
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.46
Std Dev	3.422

Gaus mean : 41.2755

Gaus Width: 4.2188

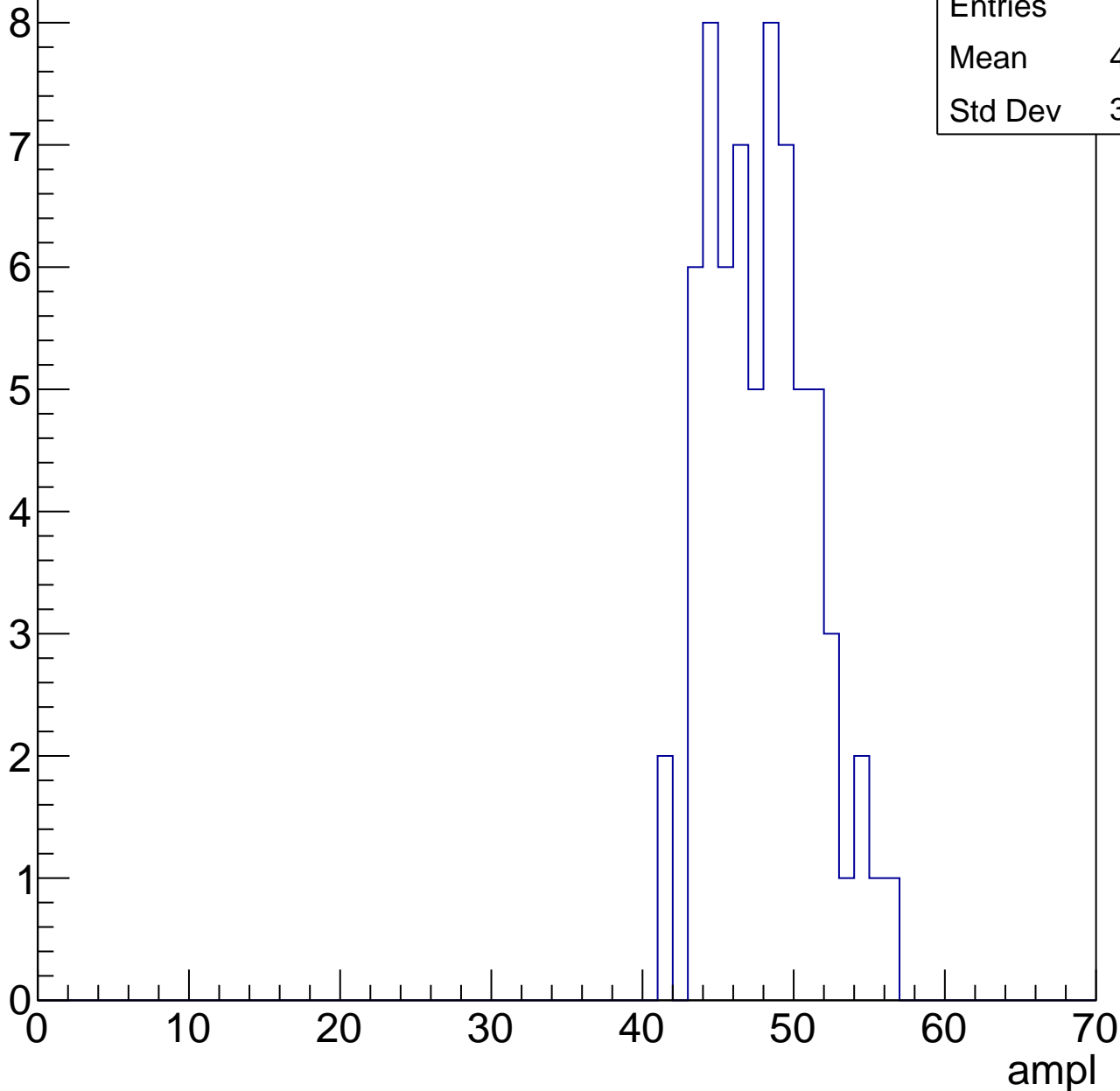


B1L102S, U17-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

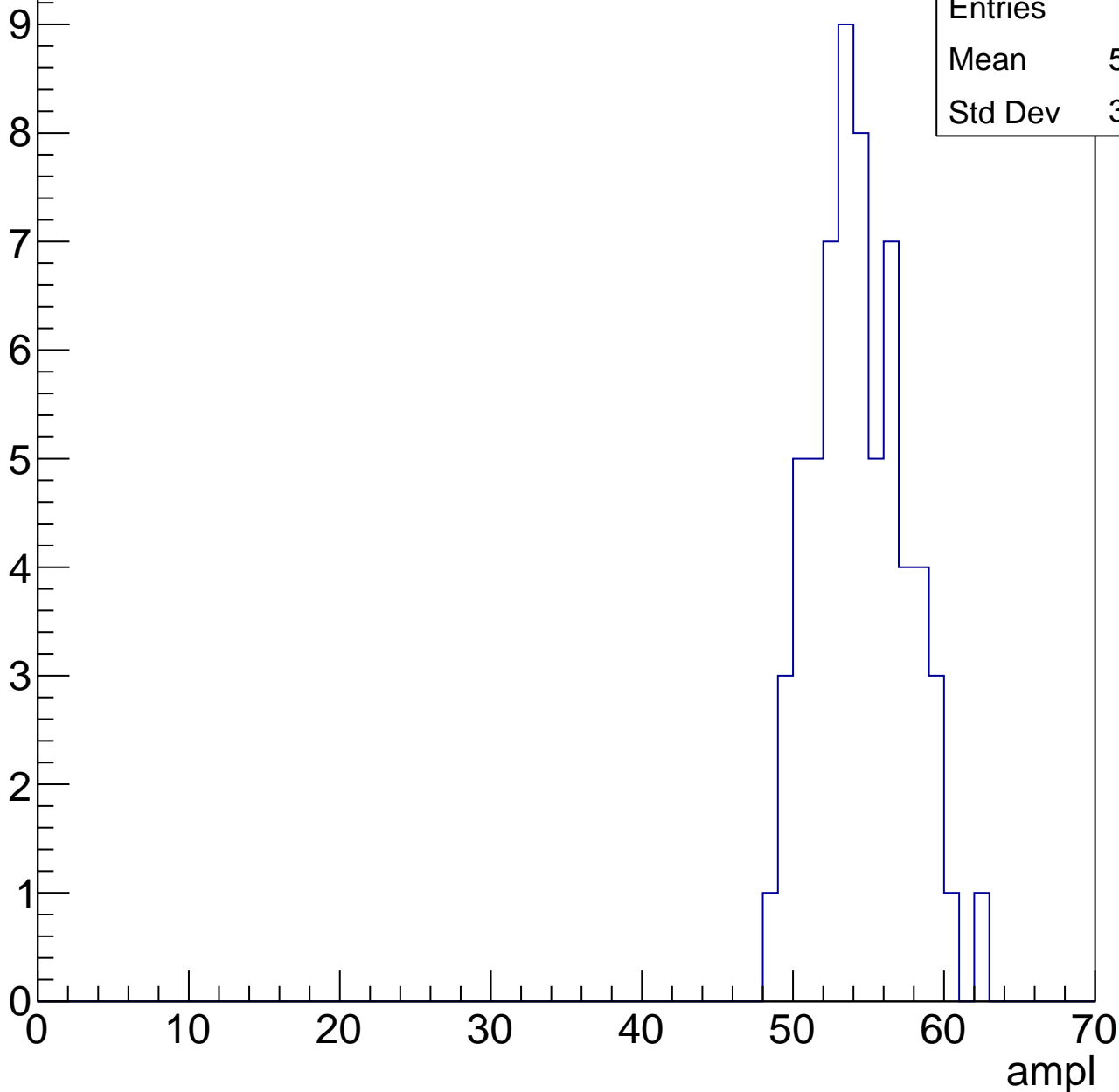
Entries	67
Mean	47.45
Std Dev	3.396



B1L102S, U17-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

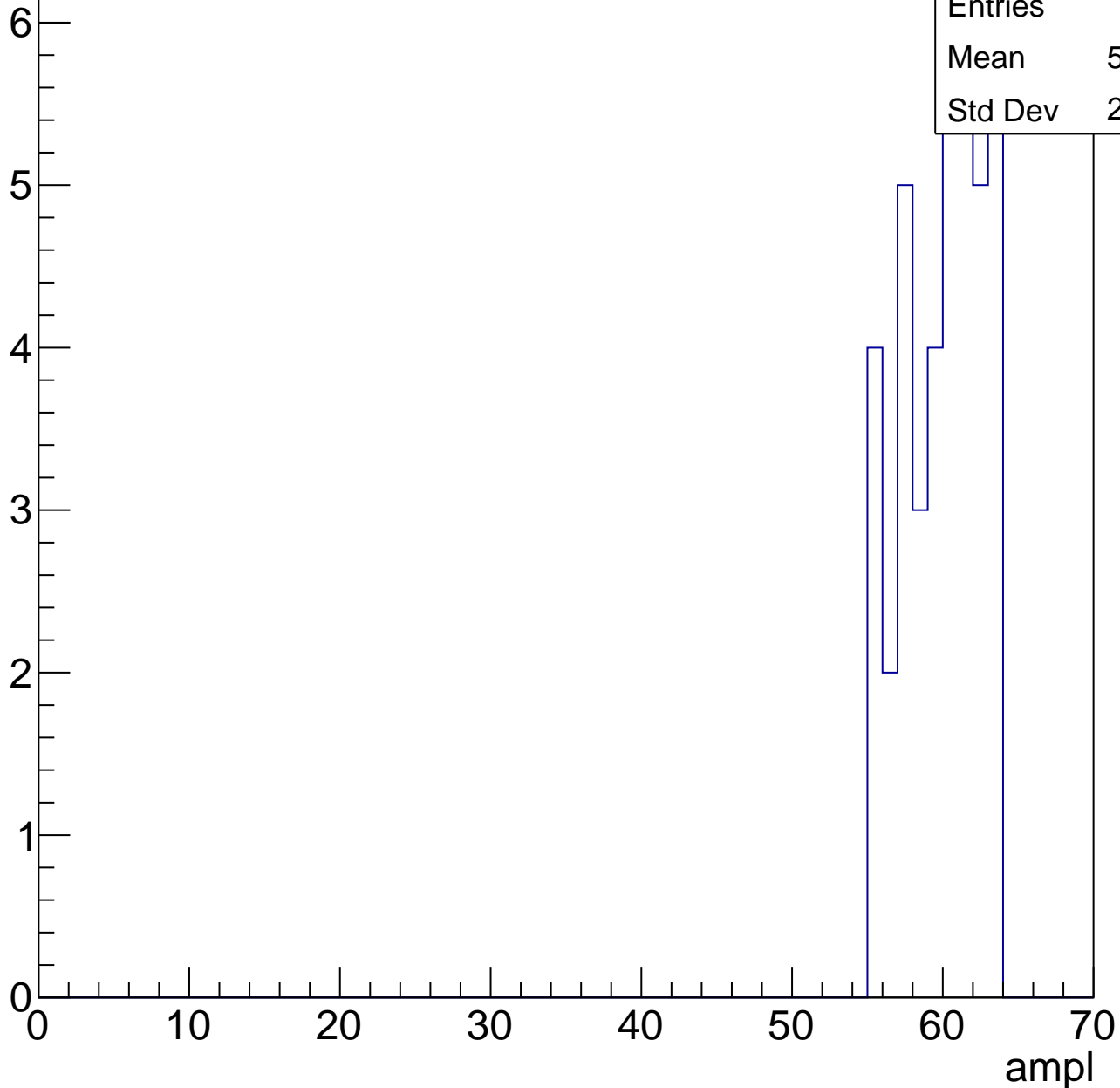


Entries	63
Mean	53.95
Std Dev	3.036

B1L102S, U17-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

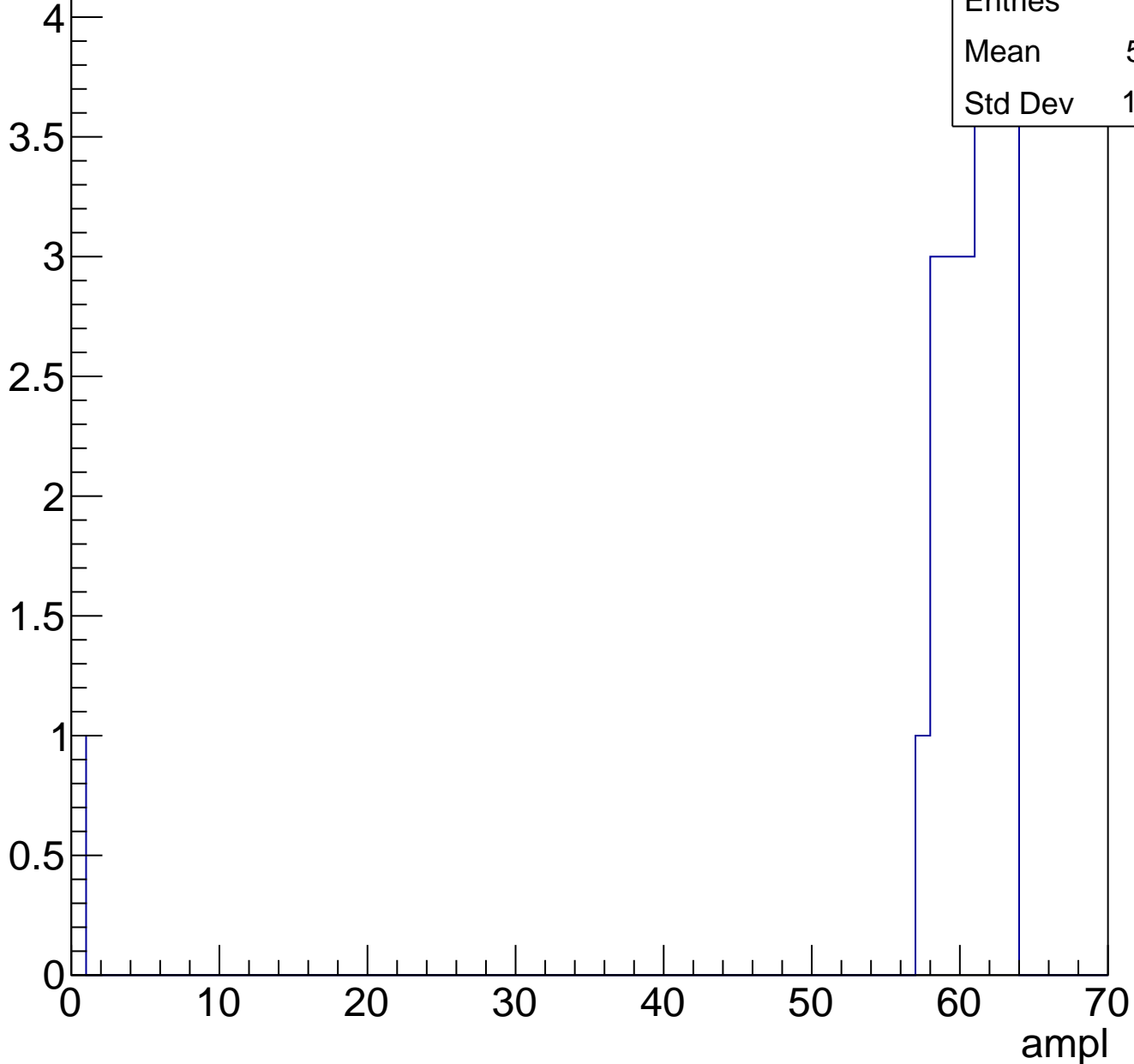


Entries	41
Mean	59.54
Std Dev	2.538

B1L102S, U17-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch15, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	30.39
Std Dev	3.136

Gaus mean : 30.7036

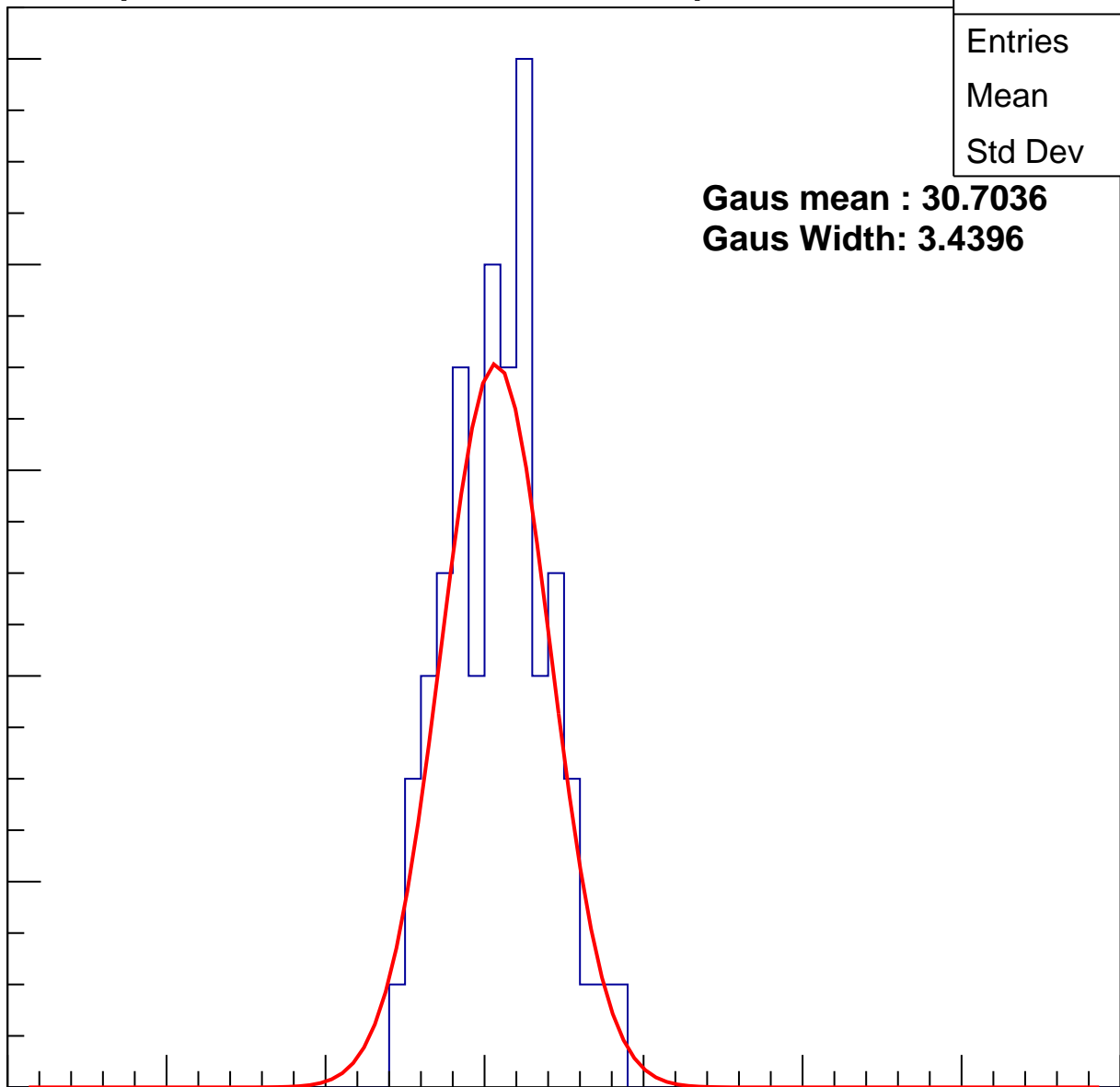
Gaus Width: 3.4396

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



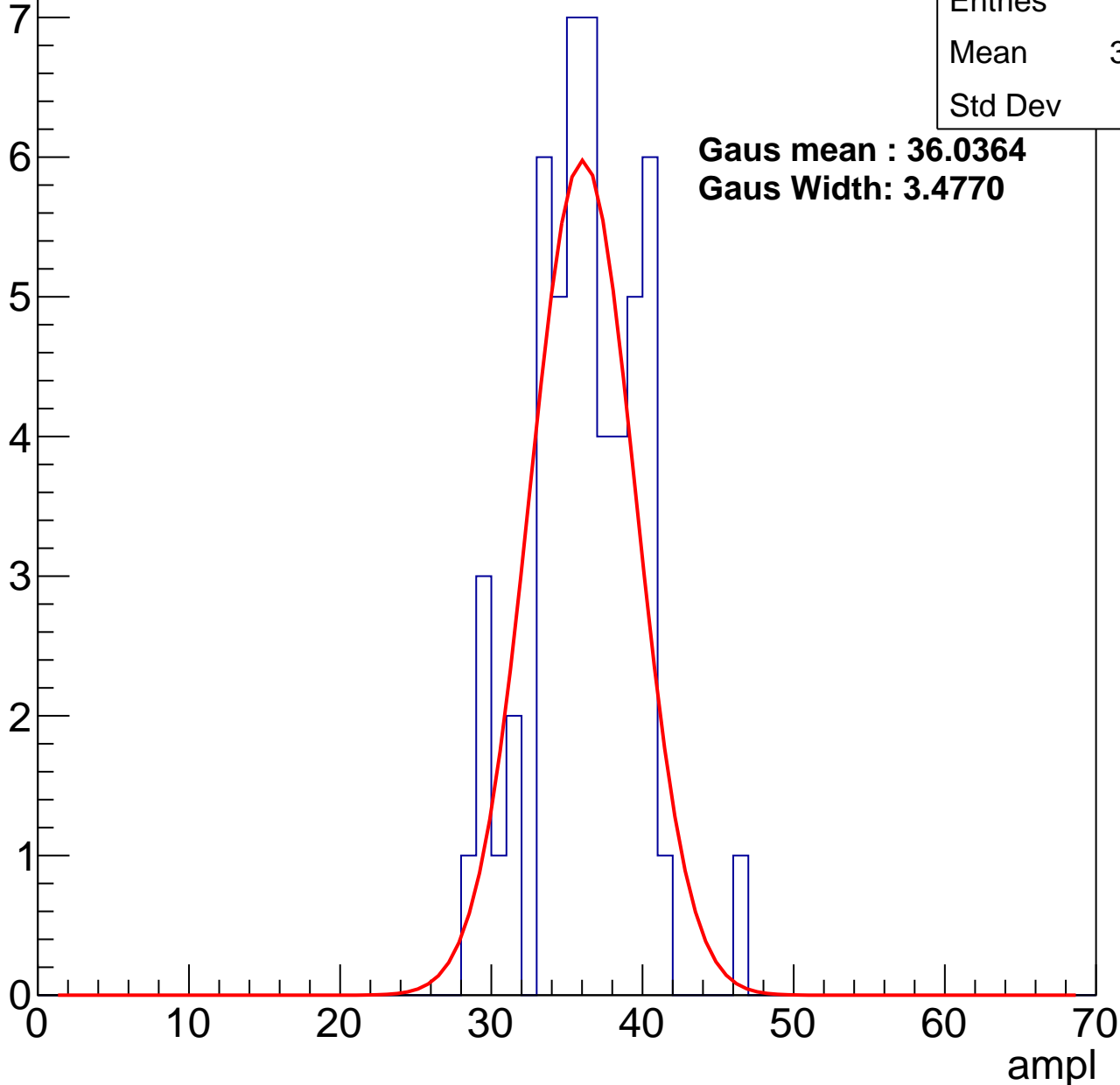
B1L102S, U17-ch15, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	35.74
Std Dev	3.53

Gaus mean : 36.0364
Gaus Width: 3.4770



B1L102S, U17-ch15, adc2

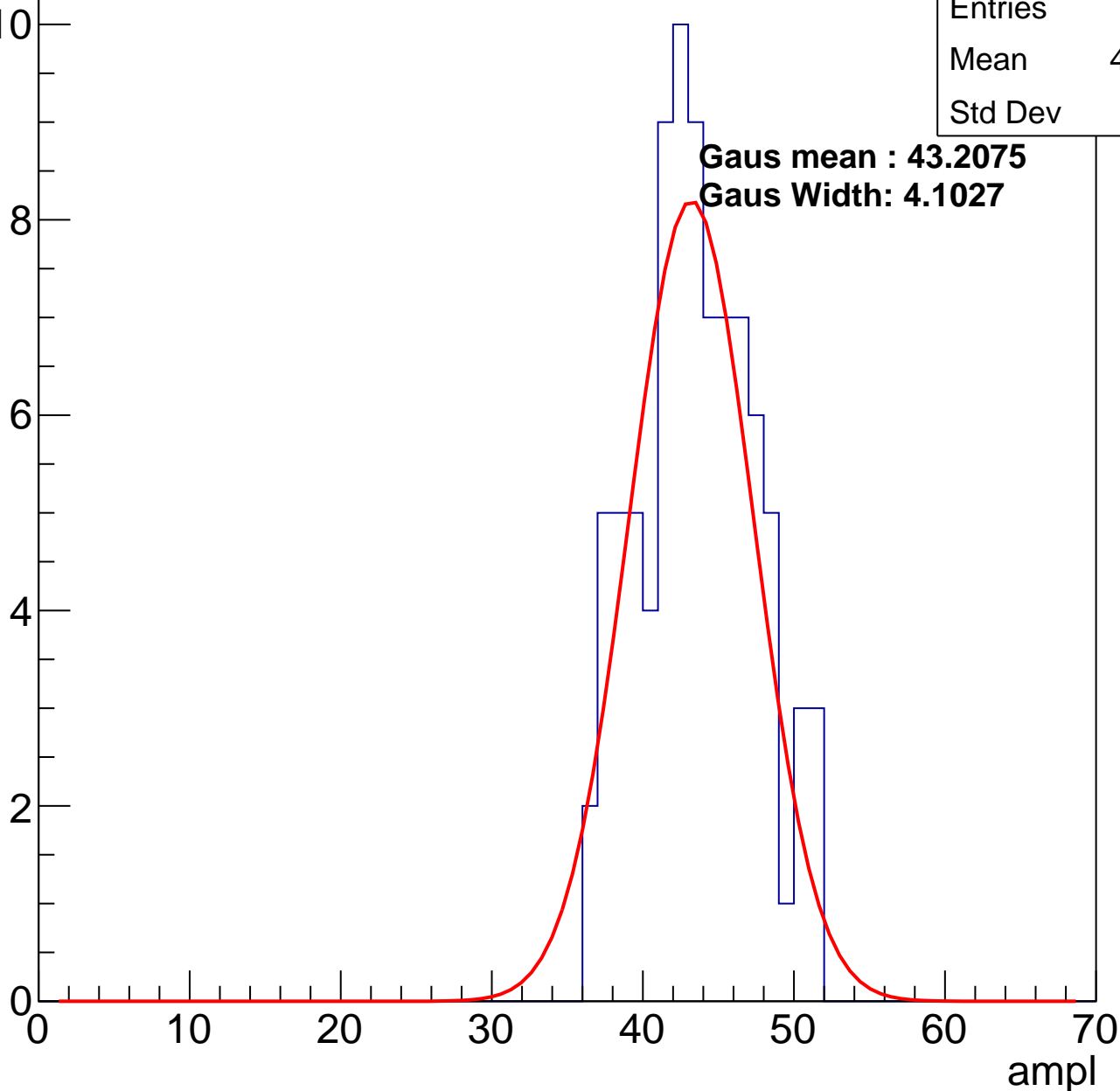
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	43.15
Std Dev	3.78

Gaus mean : 43.2075

Gaus Width: 4.1027

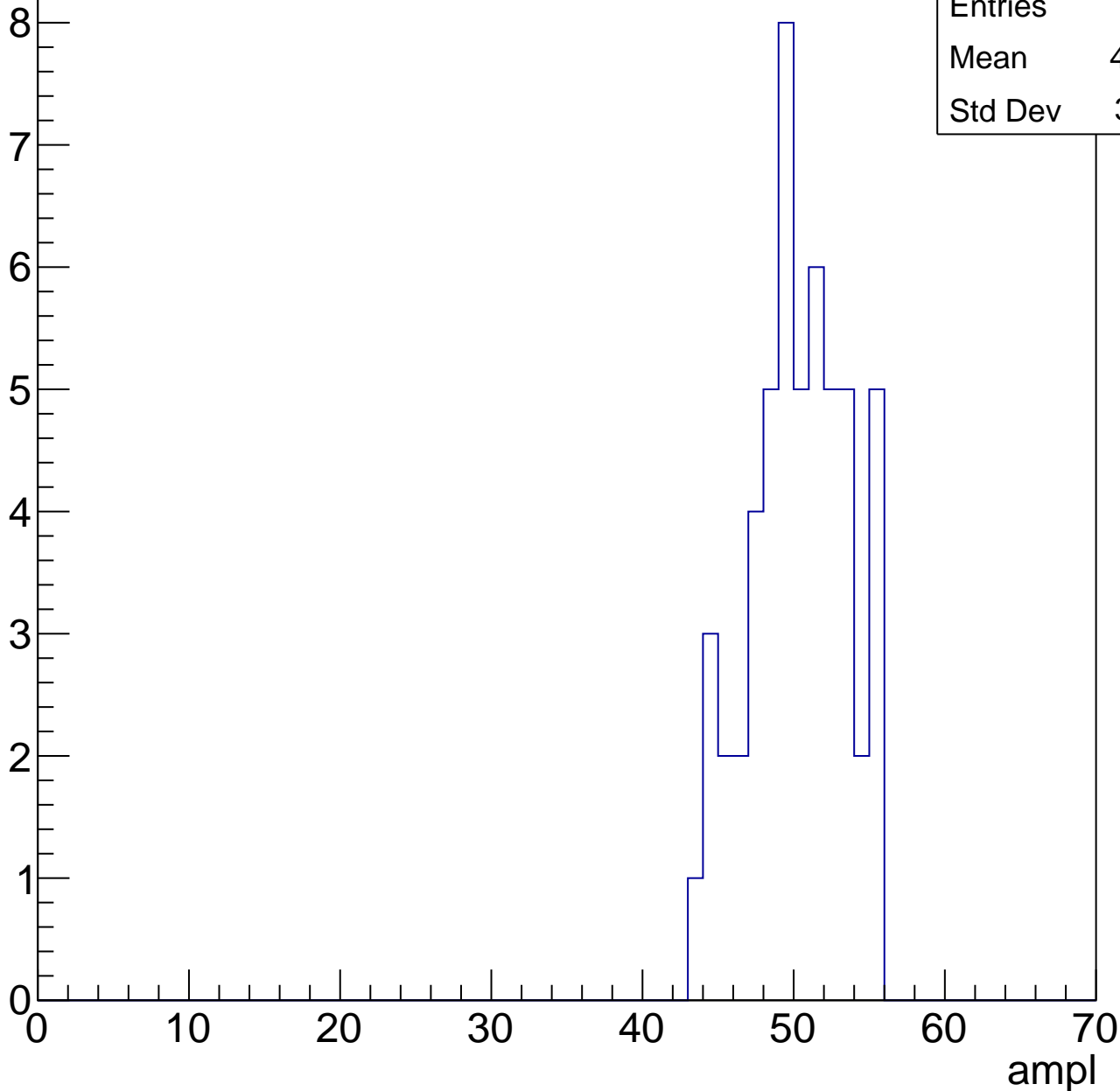


B1L102S, U17-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	49.83
Std Dev	3.161

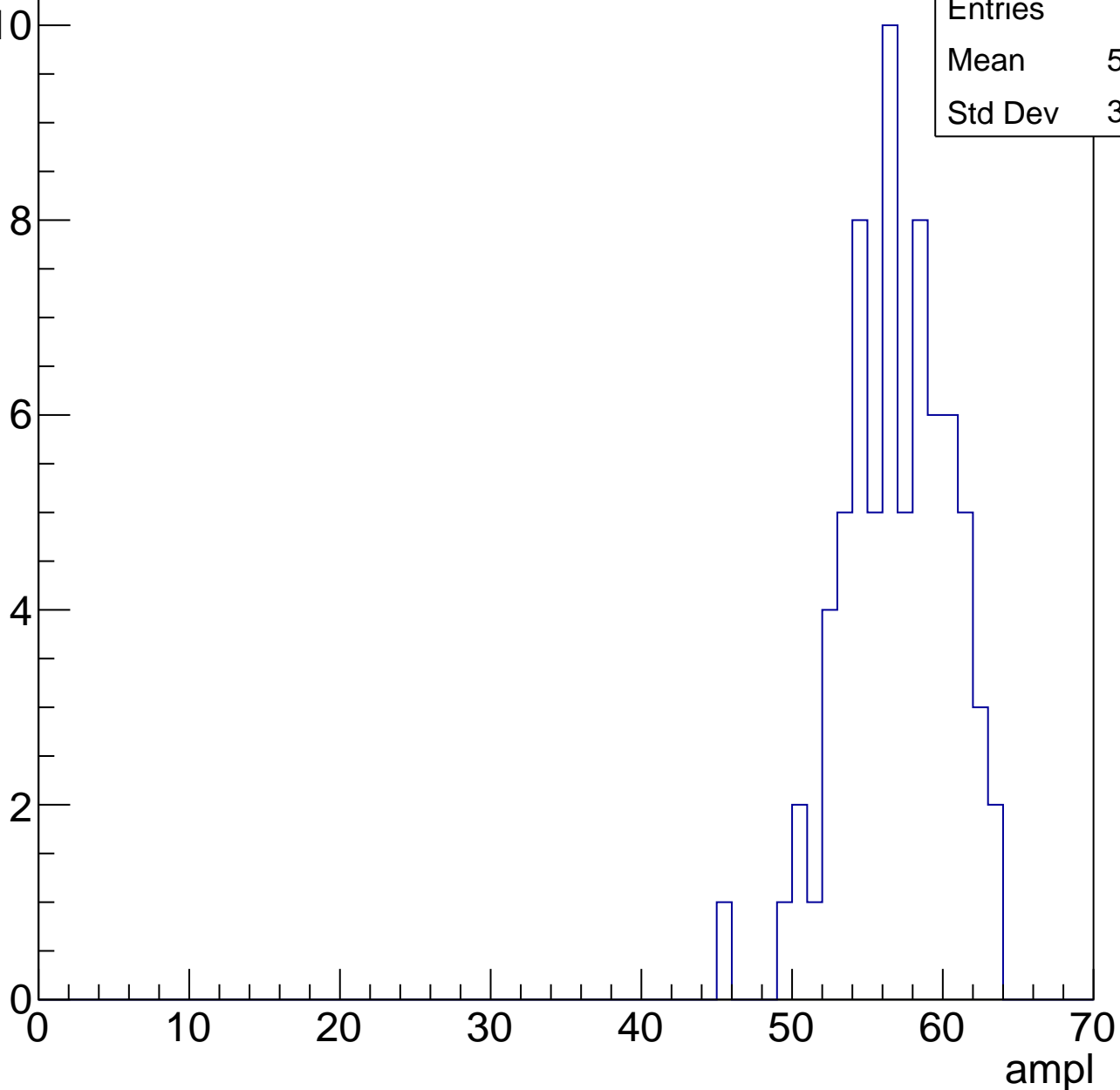


B1L102S, U17-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	56.46
Std Dev	3.567

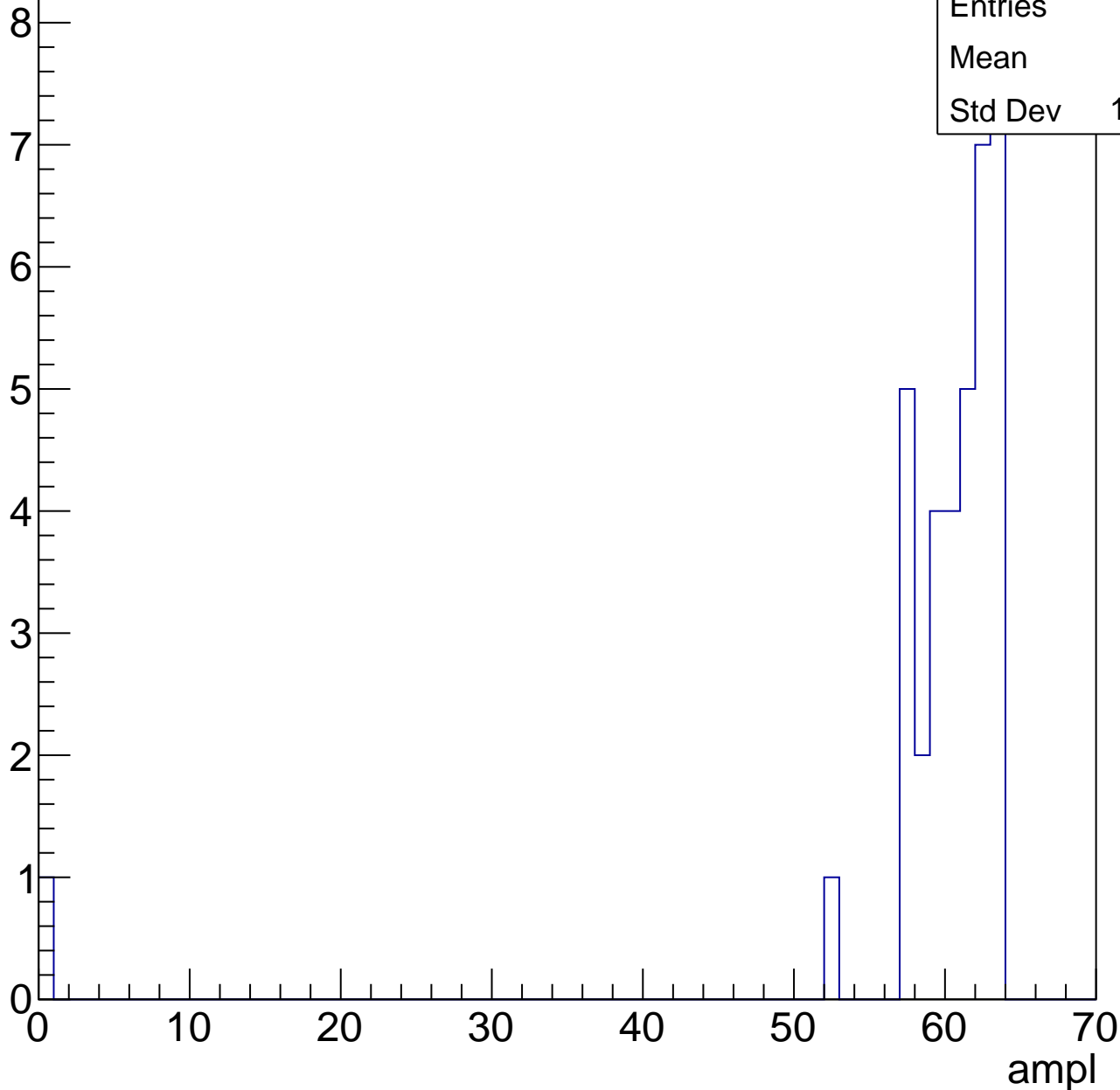


B1L102S, U17-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.7
Std Dev	10.09



B1L102S, U17-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch16, adc0

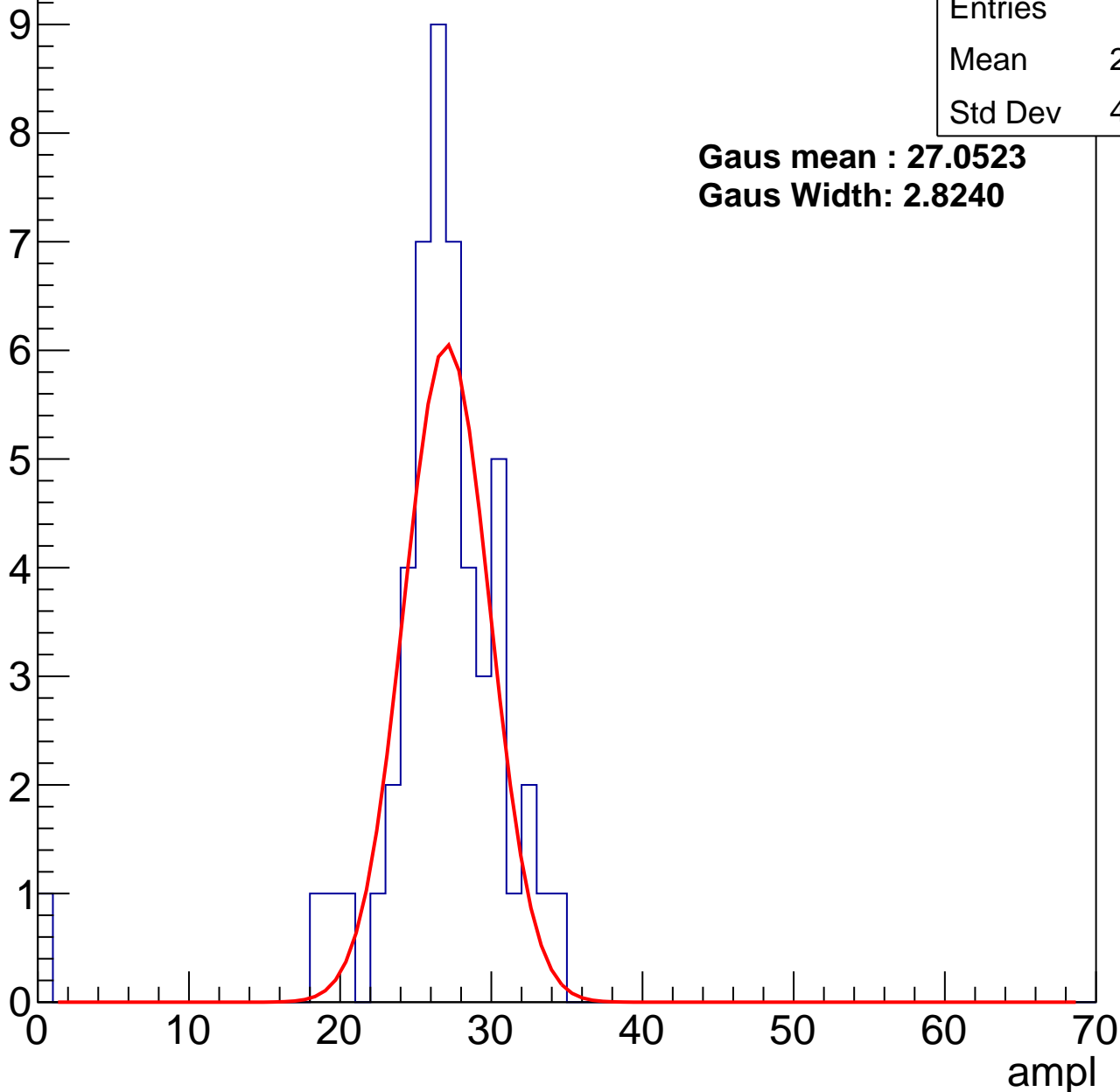
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	26.08
Std Dev	4.894

Gaus mean : 27.0523

Gaus Width: 2.8240



B1L102S, U17-ch16, adc1

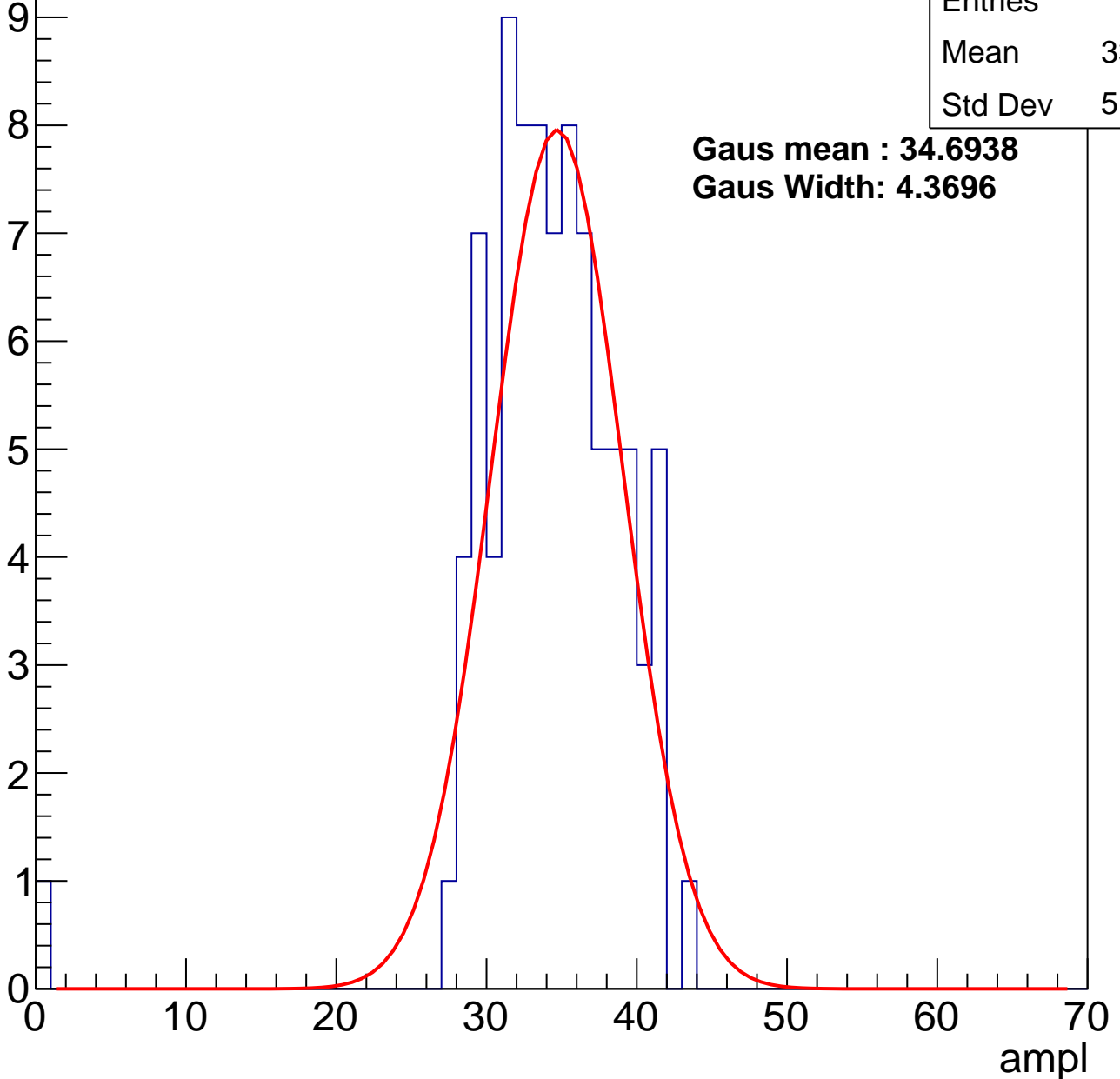
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	33.74
Std Dev	5.247

Gaus mean : 34.6938

Gaus Width: 4.3696



B1L102S, U17-ch16, adc2

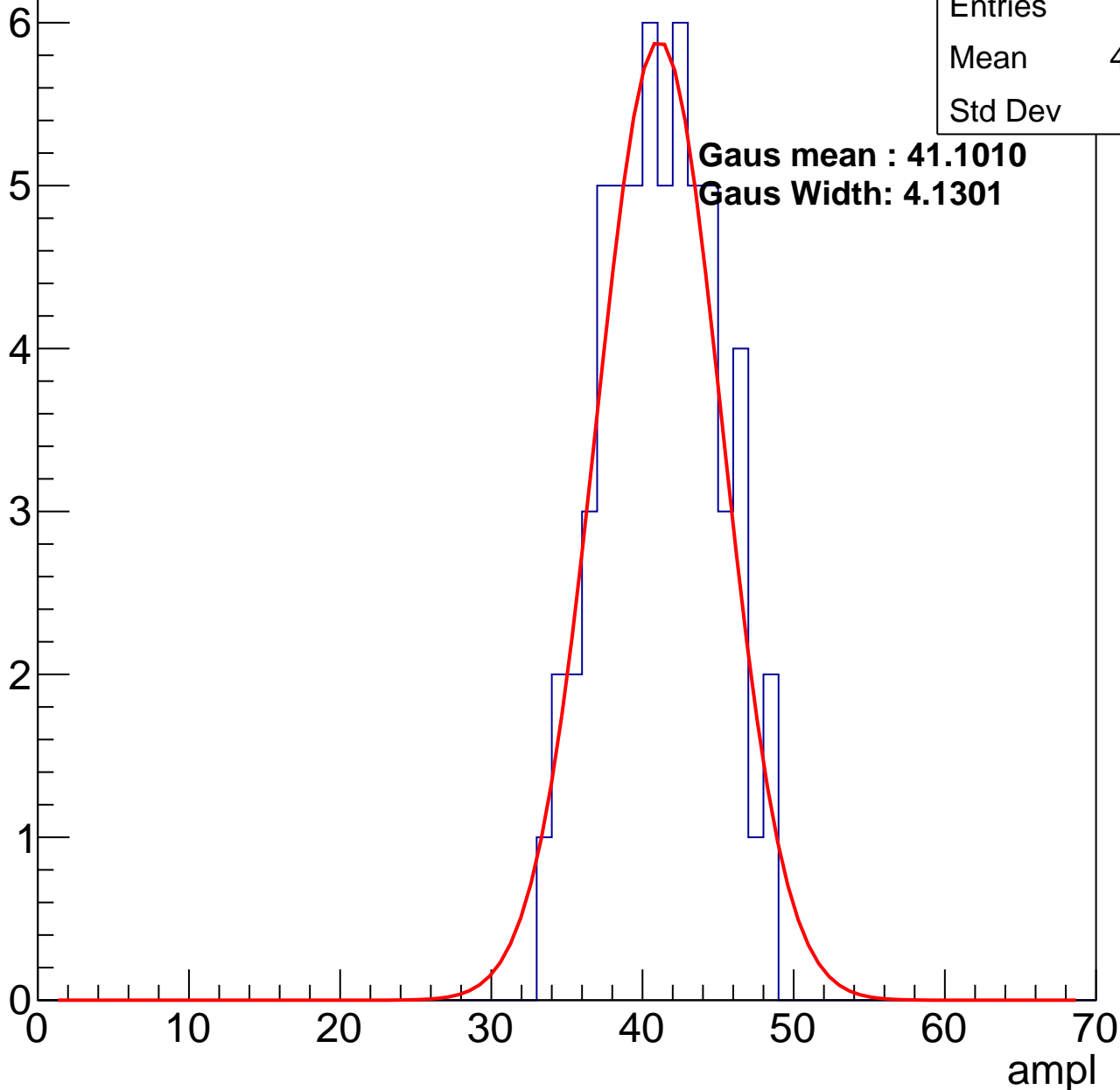
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	40.72
Std Dev	3.67

Gaus mean : 41.1010

Gaus Width: 4.1301

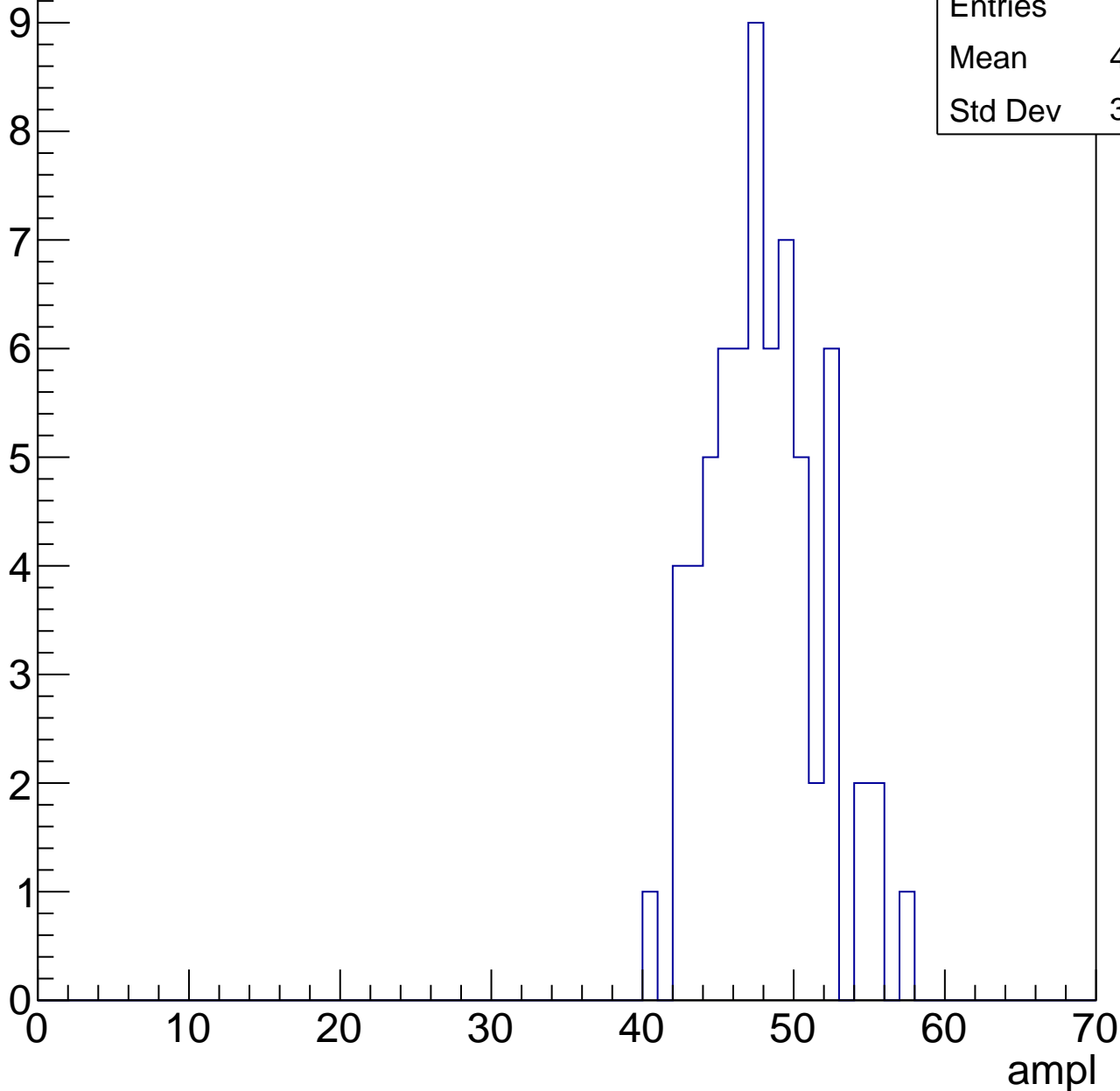


B1L102S, U17-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.56
Std Dev	3.598

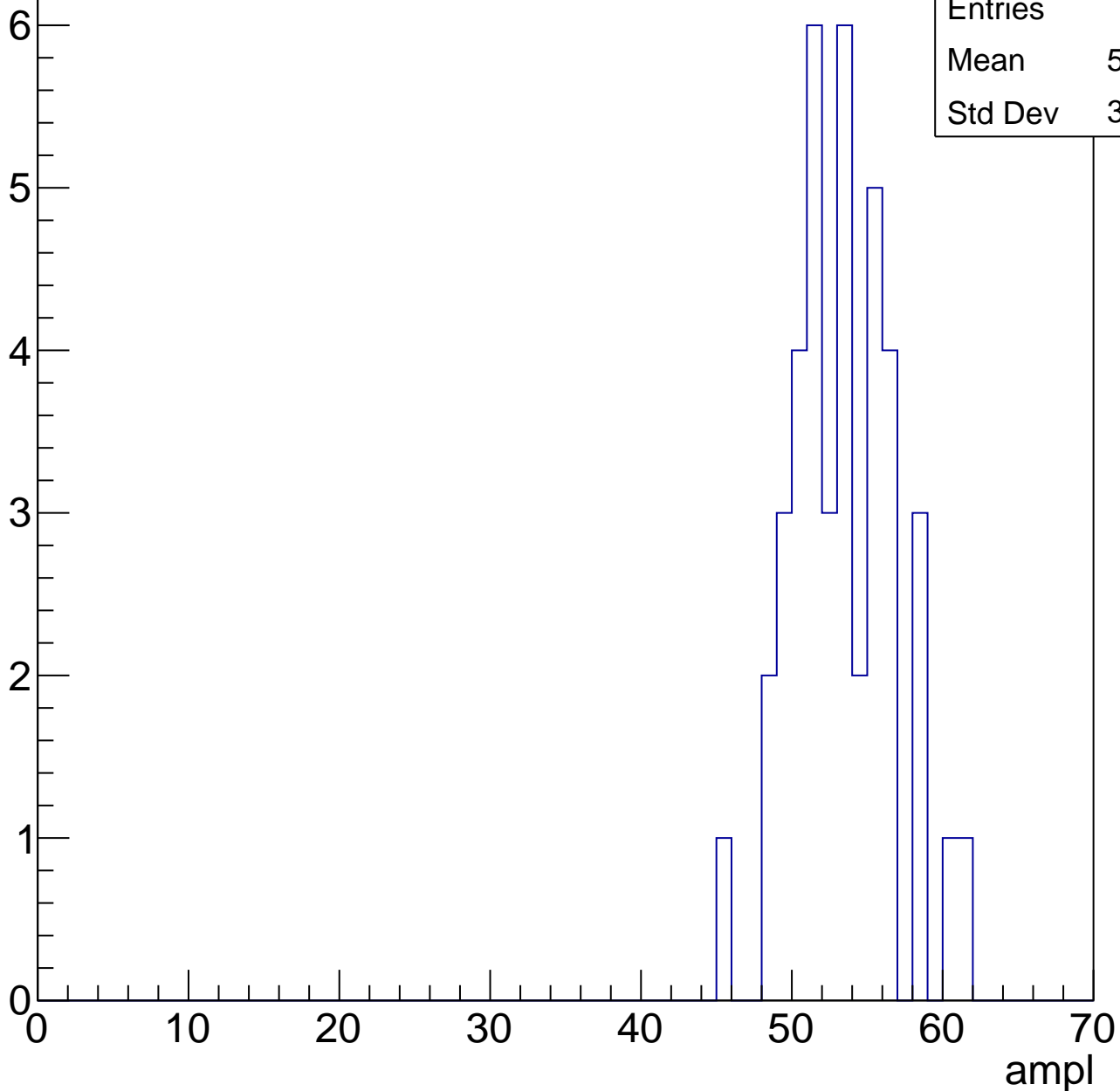


B1L102S, U17-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	52.93
Std Dev	3.389

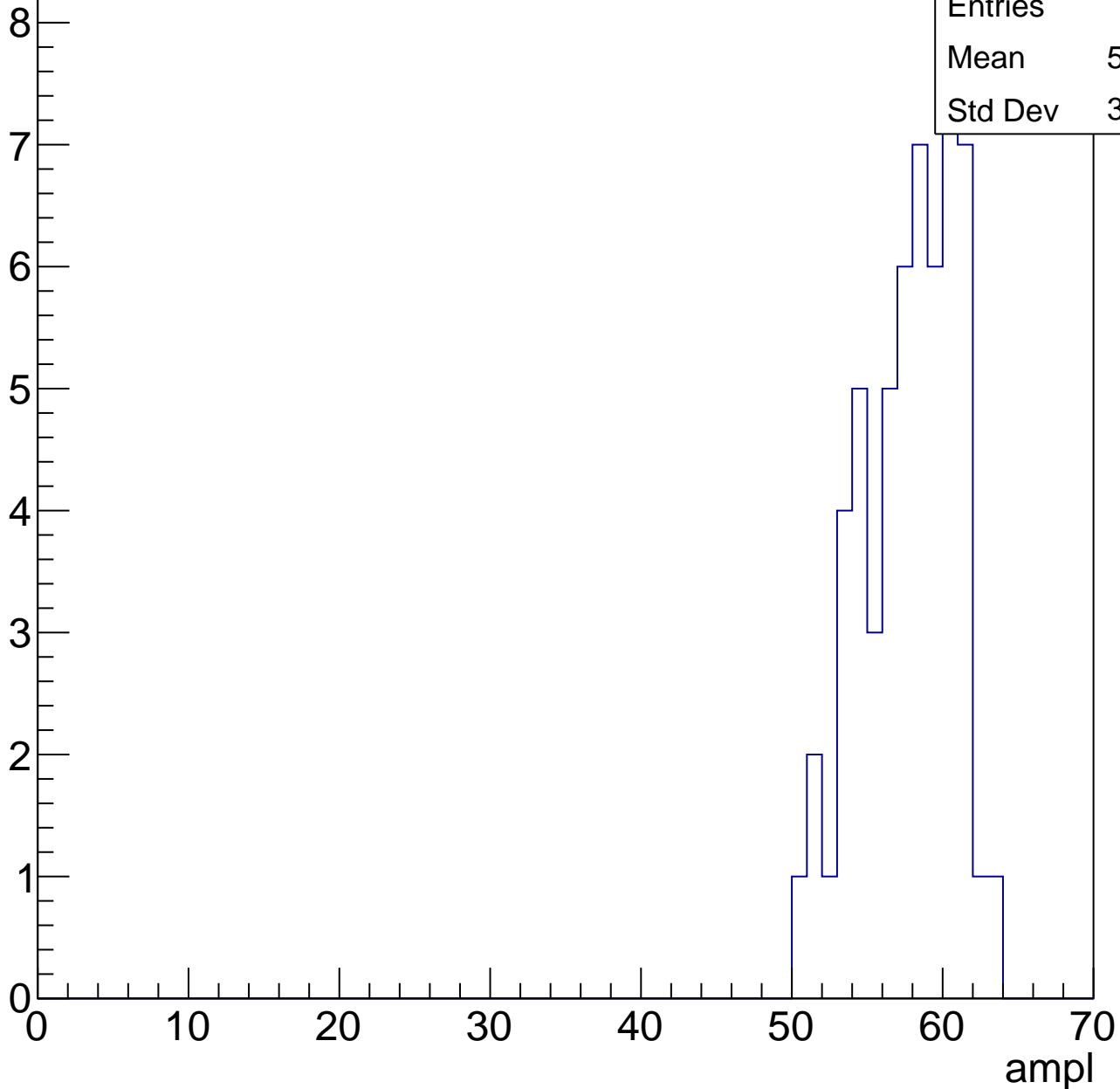


B1L102S, U17-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.28
Std Dev	3.088

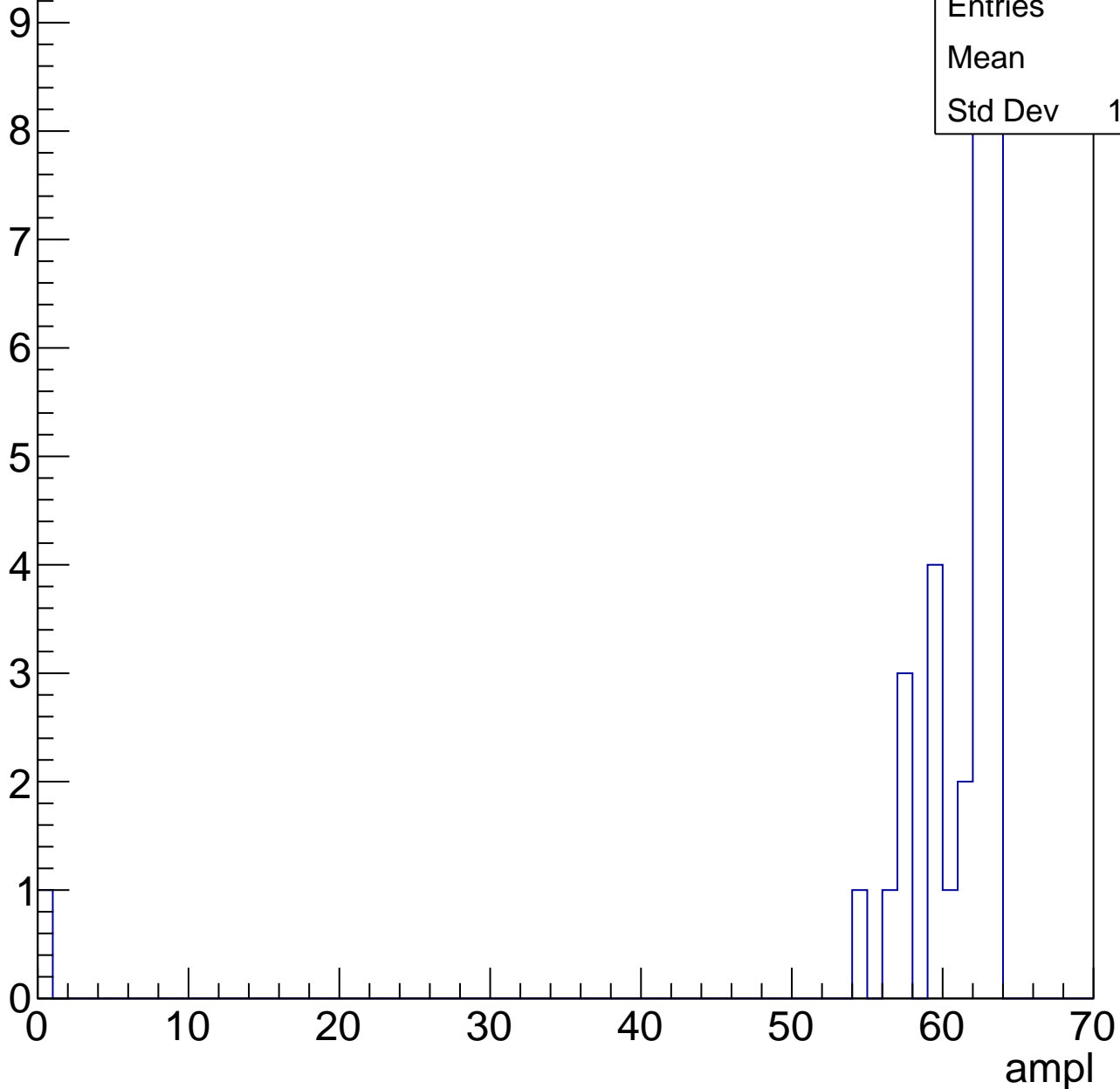


B1L102S, U17-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

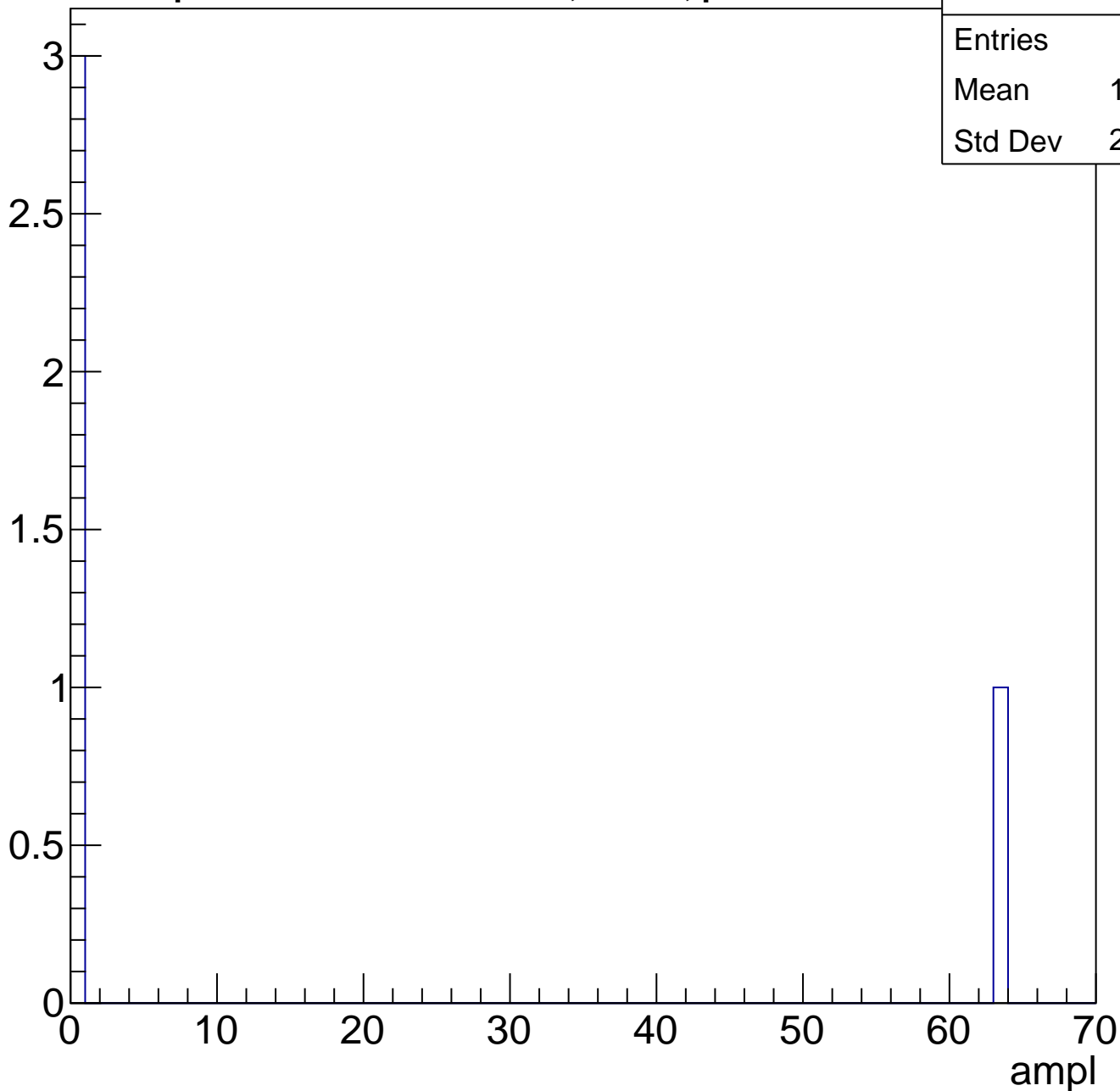
Entries	30
Mean	58.7
Std Dev	11.17



B1L102S, U17-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch17, adc0

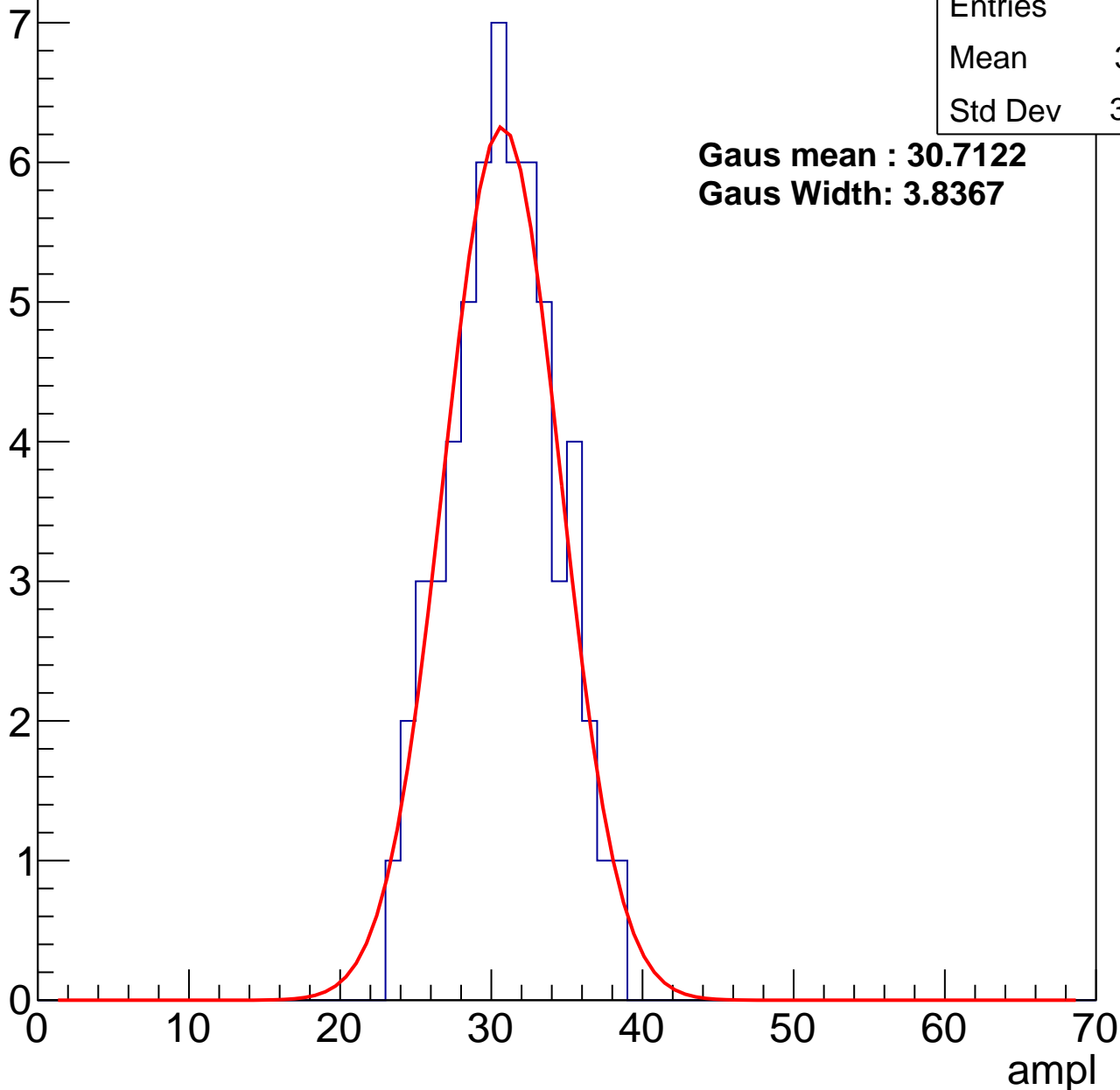
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	30.31
Std Dev	3.465

Gaus mean : 30.7122

Gaus Width: 3.8367



B1L102S, U17-ch17, adc1

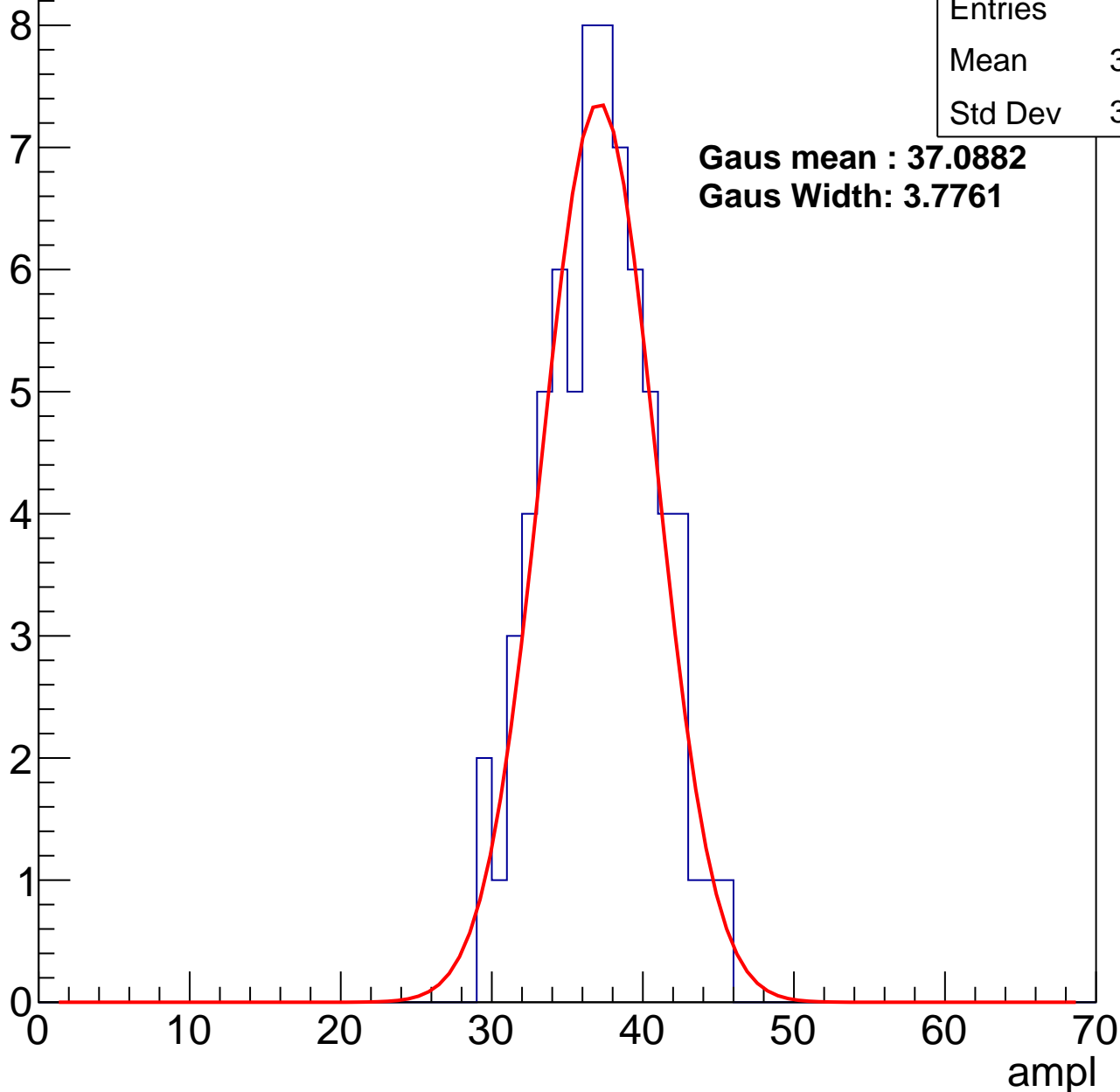
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.63
Std Dev	3.612

Gaus mean : 37.0882

Gaus Width: 3.7761



B1L102S, U17-ch17, adc2

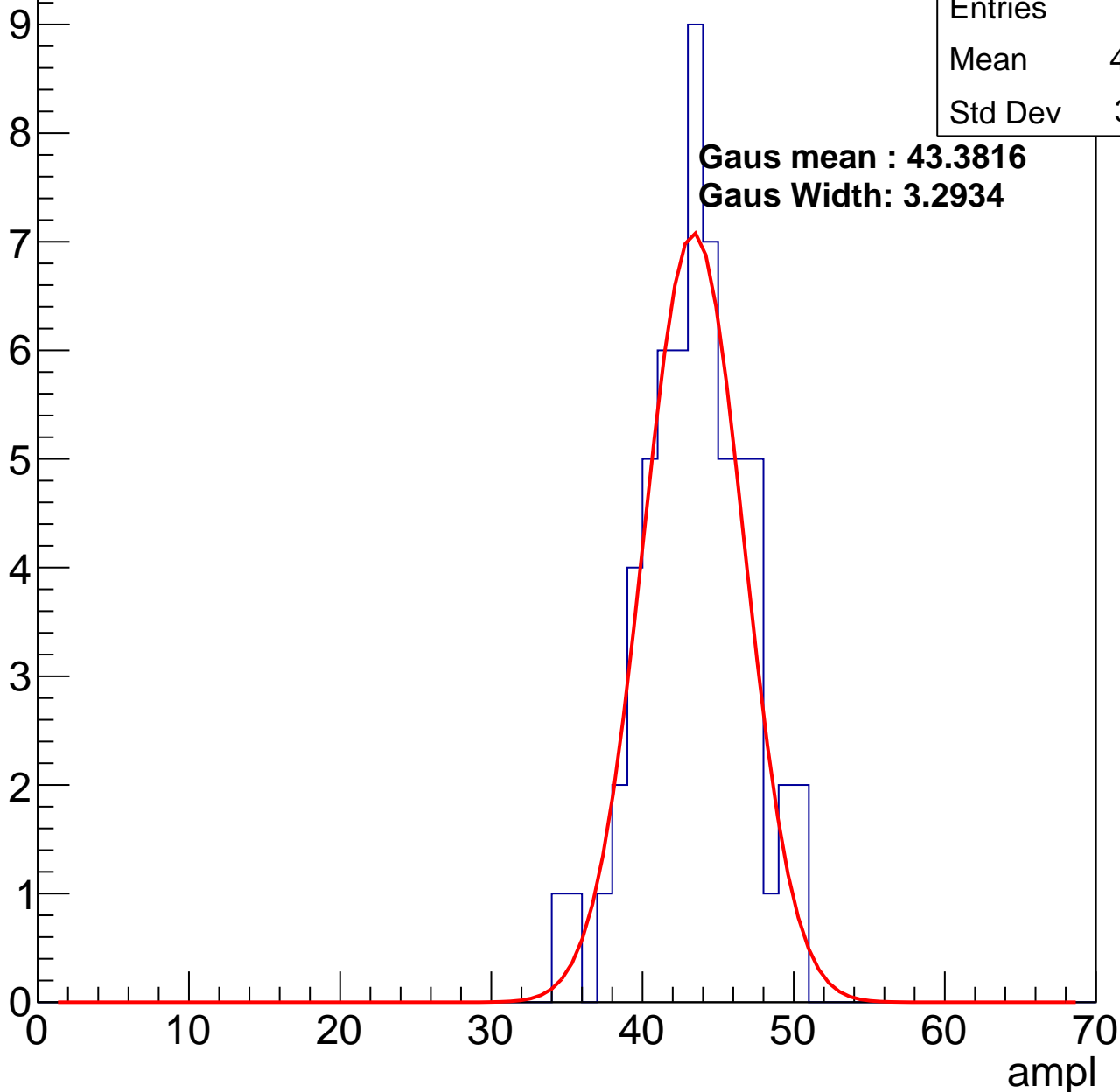
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.02
Std Dev	3.401

Gaus mean : 43.3816

Gaus Width: 3.2934

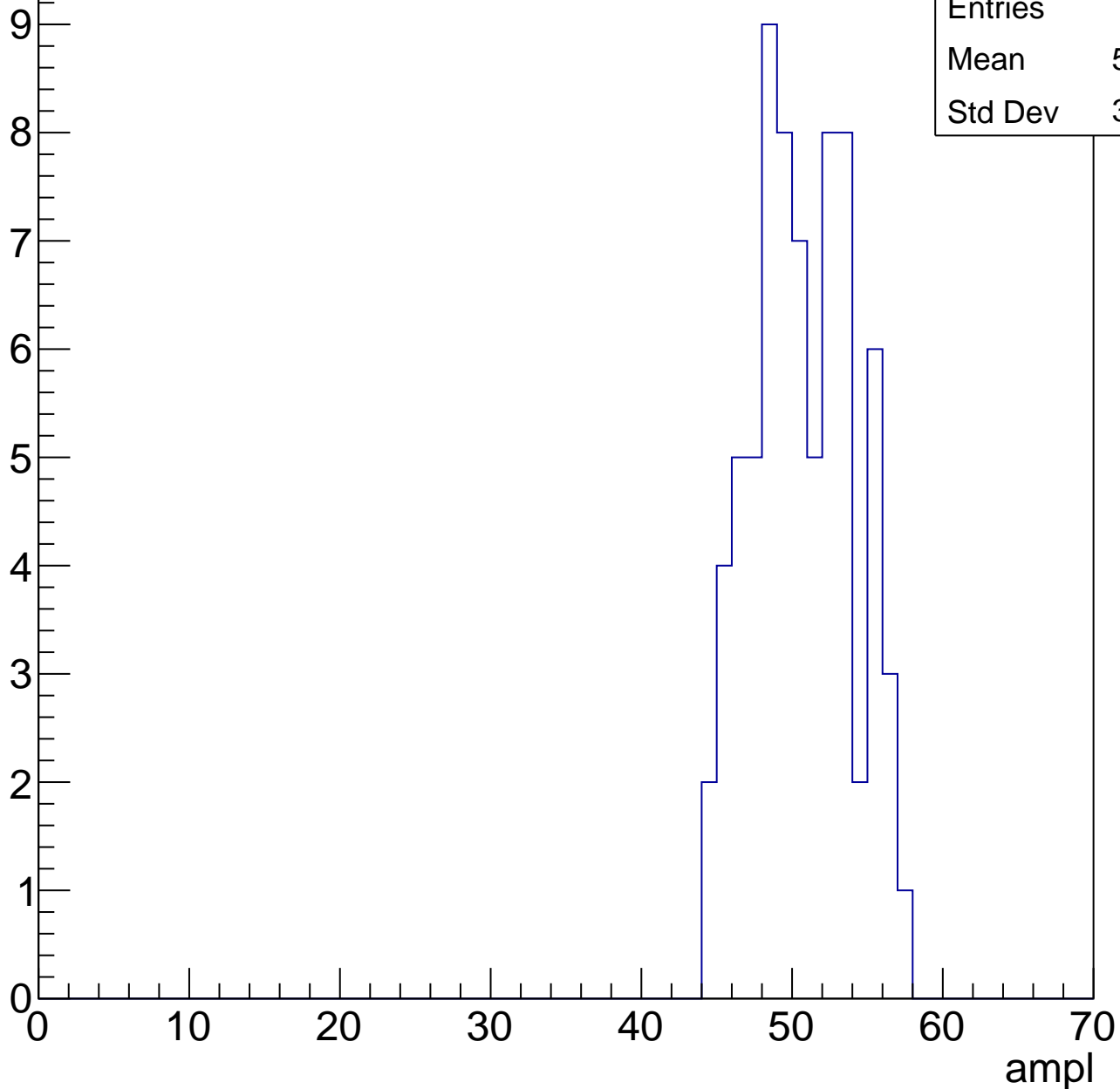


B1L102S, U17-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	50.21
Std Dev	3.281

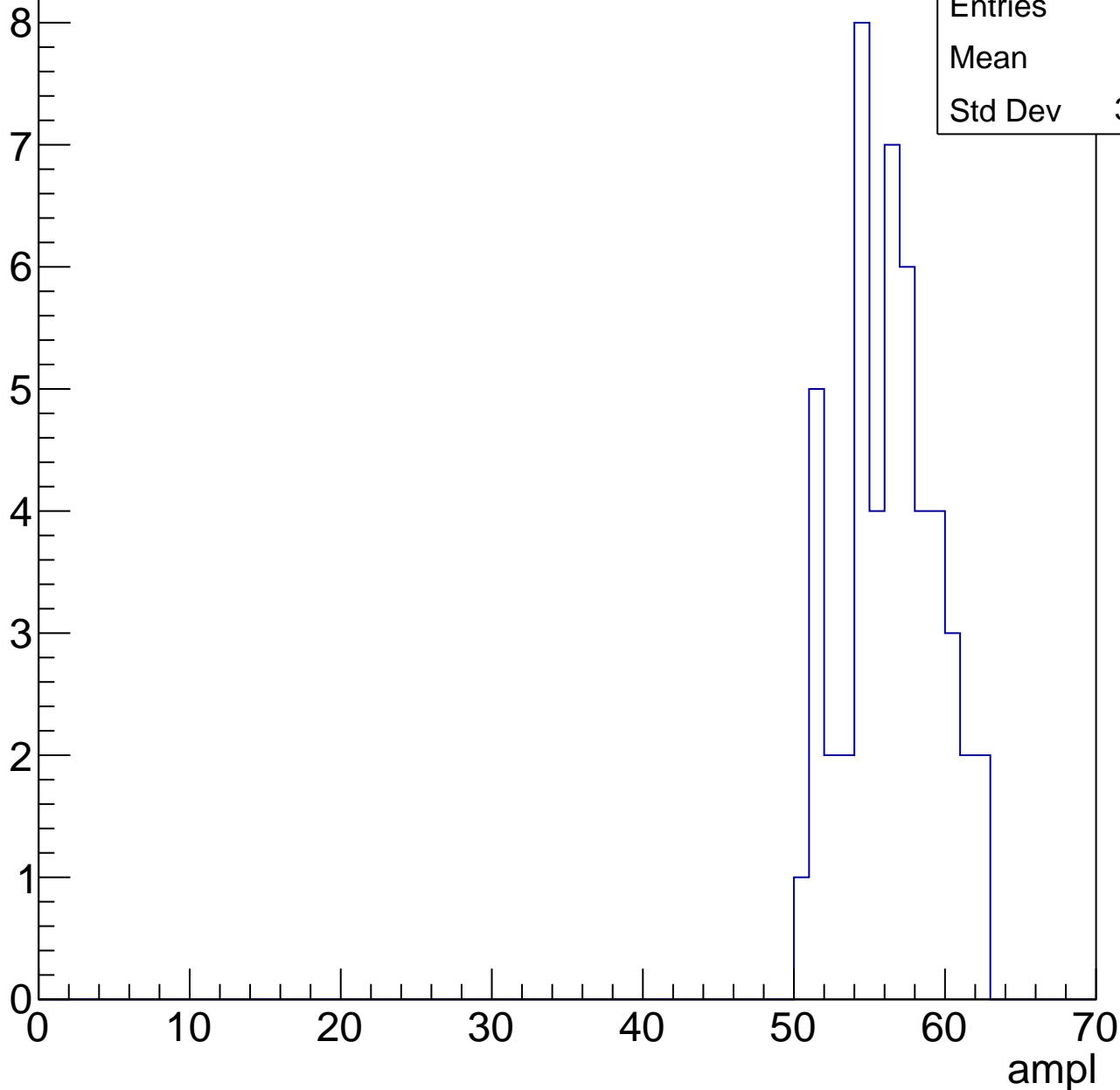


B1L102S, U17-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.9
Std Dev	3.081

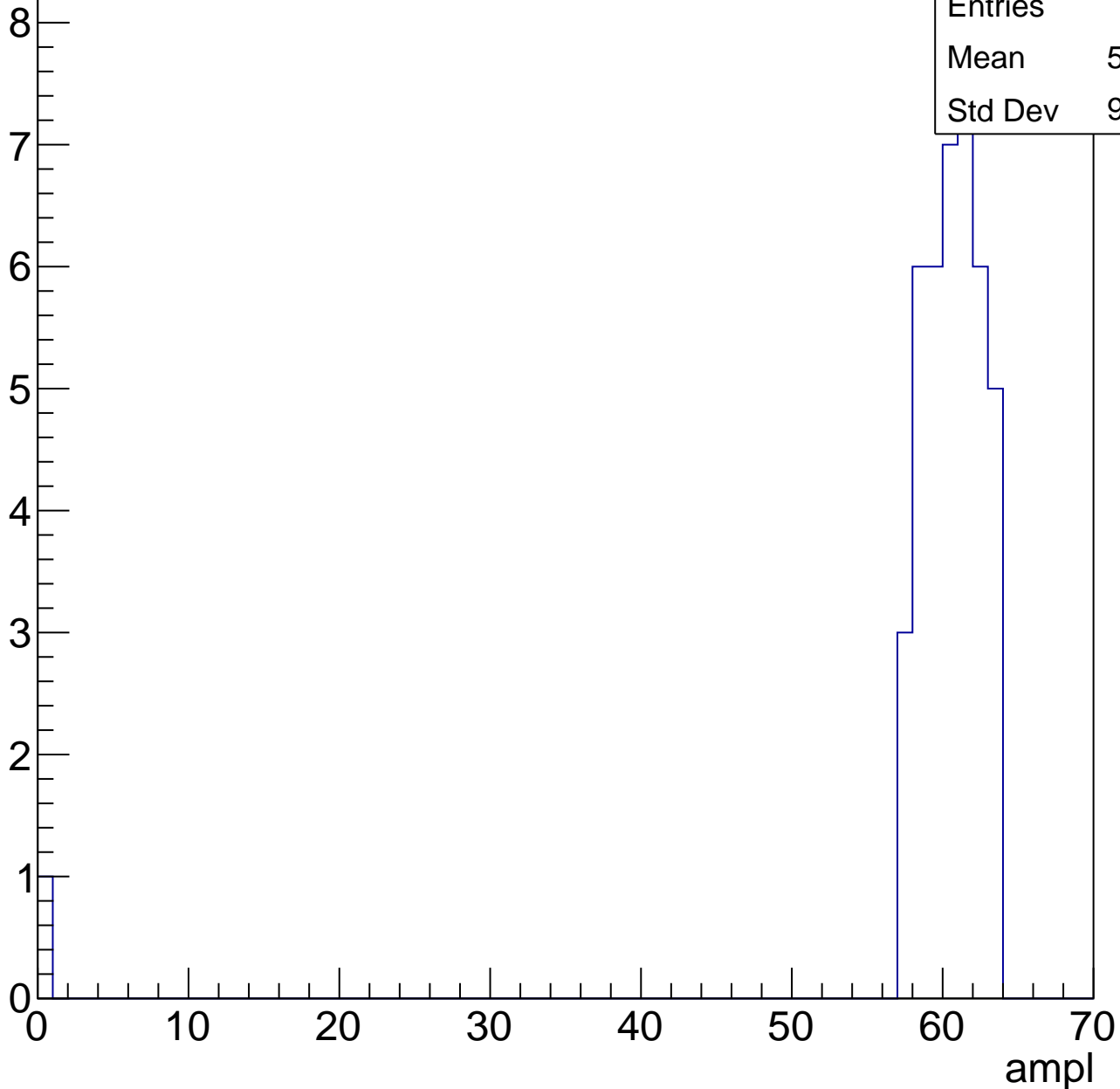


B1L102S, U17-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.76
Std Dev	9.347

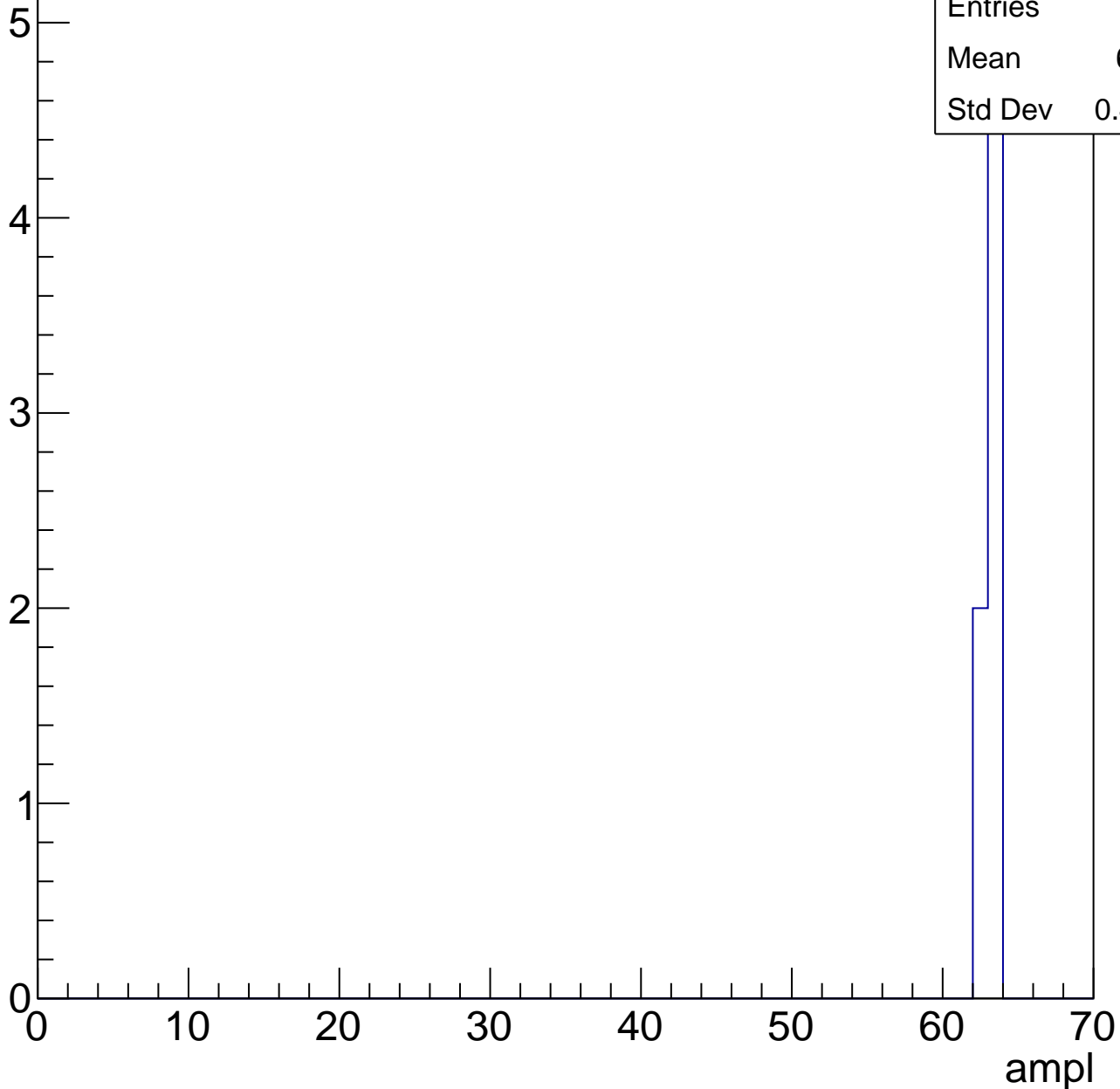


B1L102S, U17-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B1L102S, U17-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch18, adc0

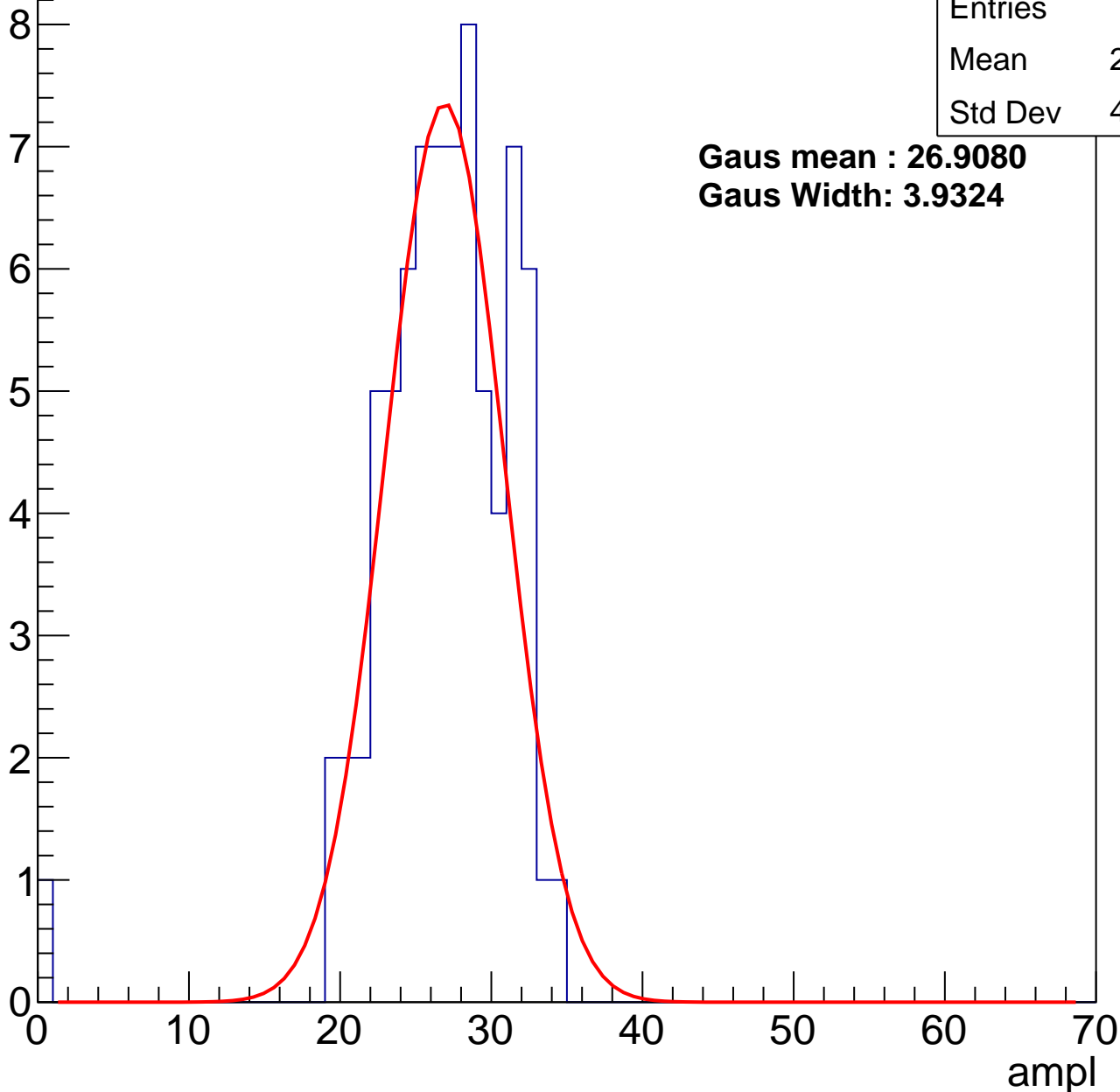
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	26.32
Std Dev	4.725

Gaus mean : 26.9080

Gaus Width: 3.9324



B1L102S, U17-ch18, adc1

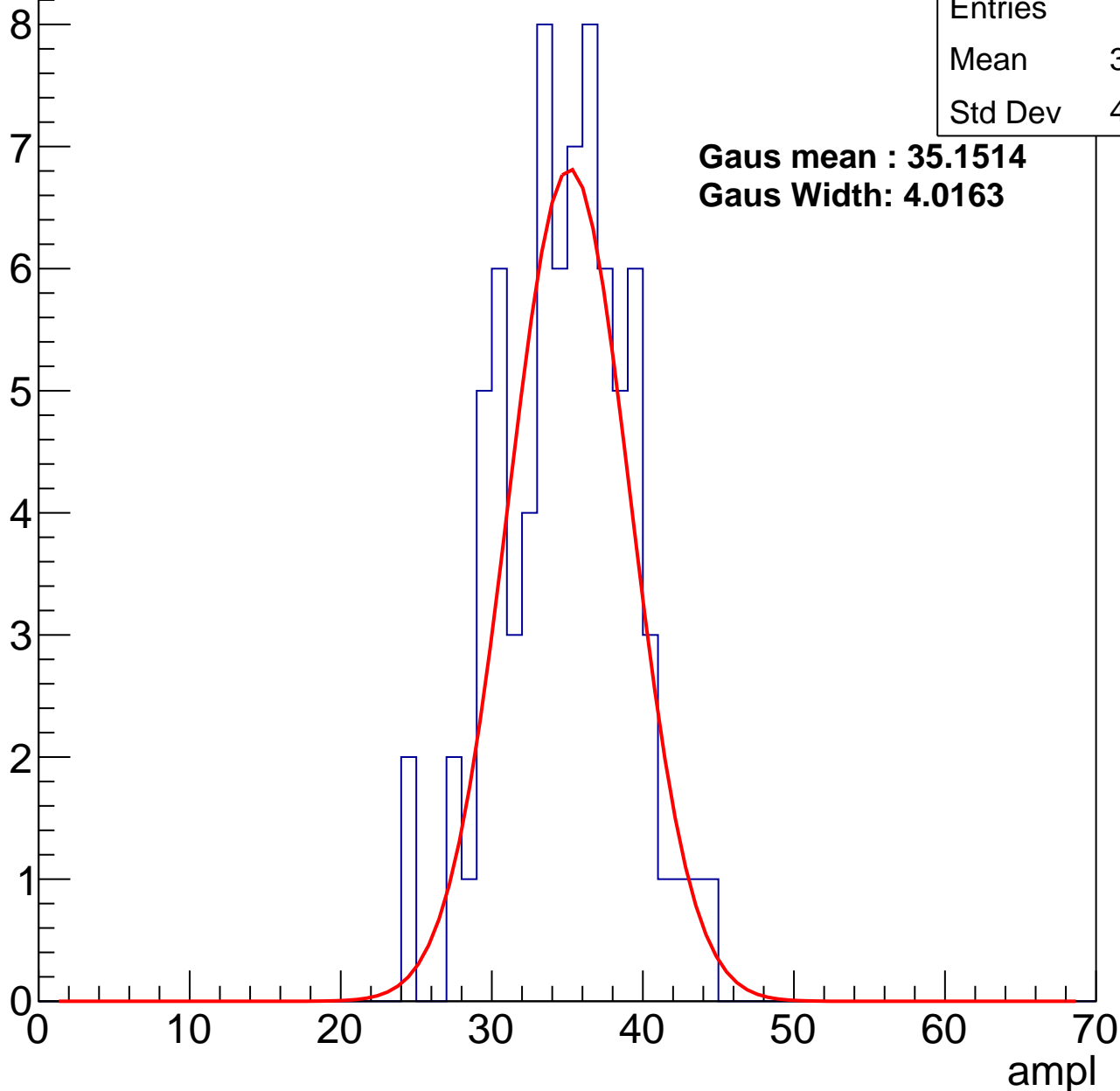
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.38
Std Dev	4.167

Gaus mean : 35.1514

Gaus Width: 4.0163



B1L102S, U17-ch18, adc2

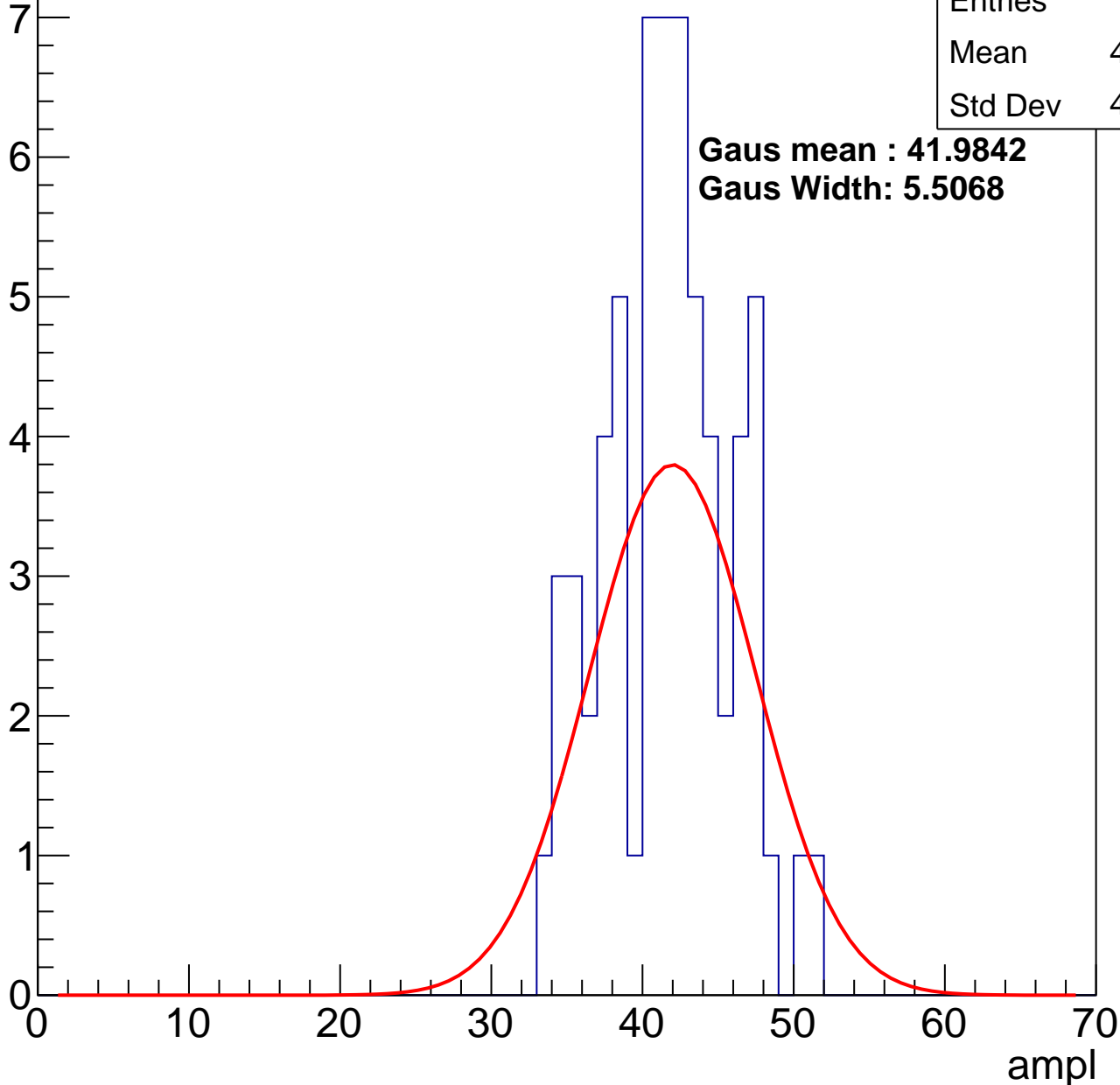
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.25
Std Dev	4.159

Gaus mean : 41.9842

Gaus Width: 5.5068

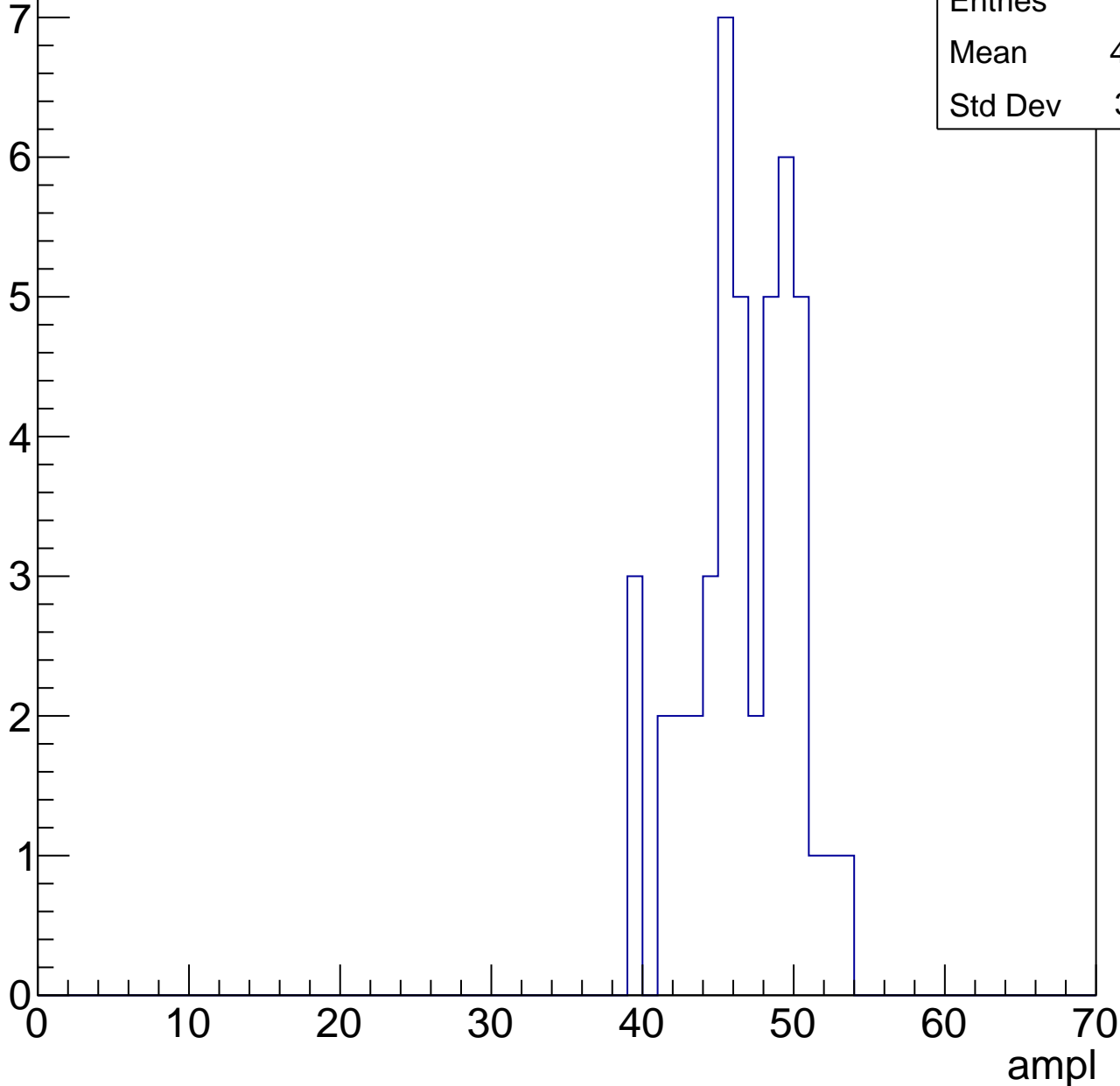


B1L102S, U17-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	46.22
Std Dev	3.431

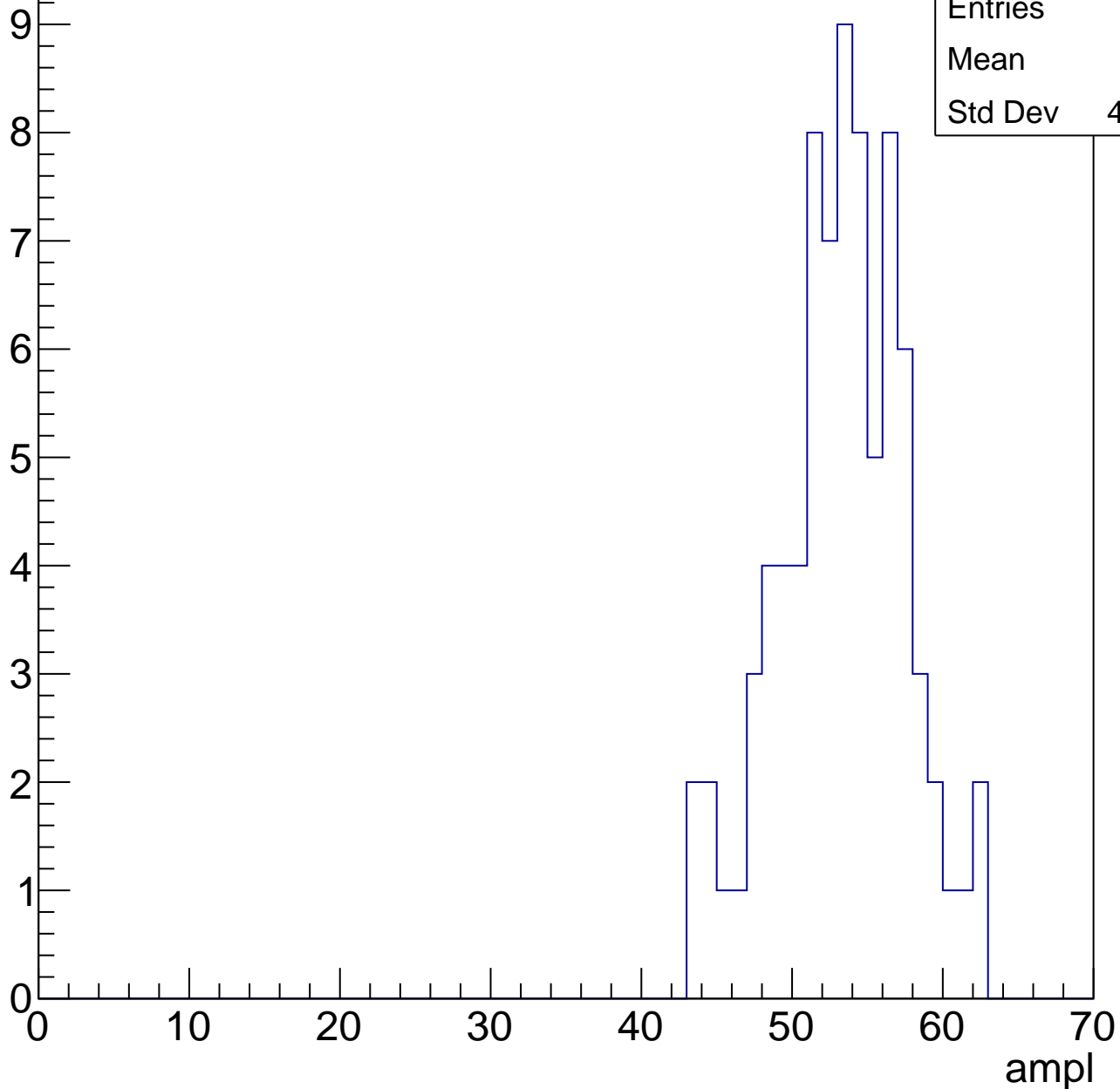


B1L102S, U17-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	52.8
Std Dev	4.212

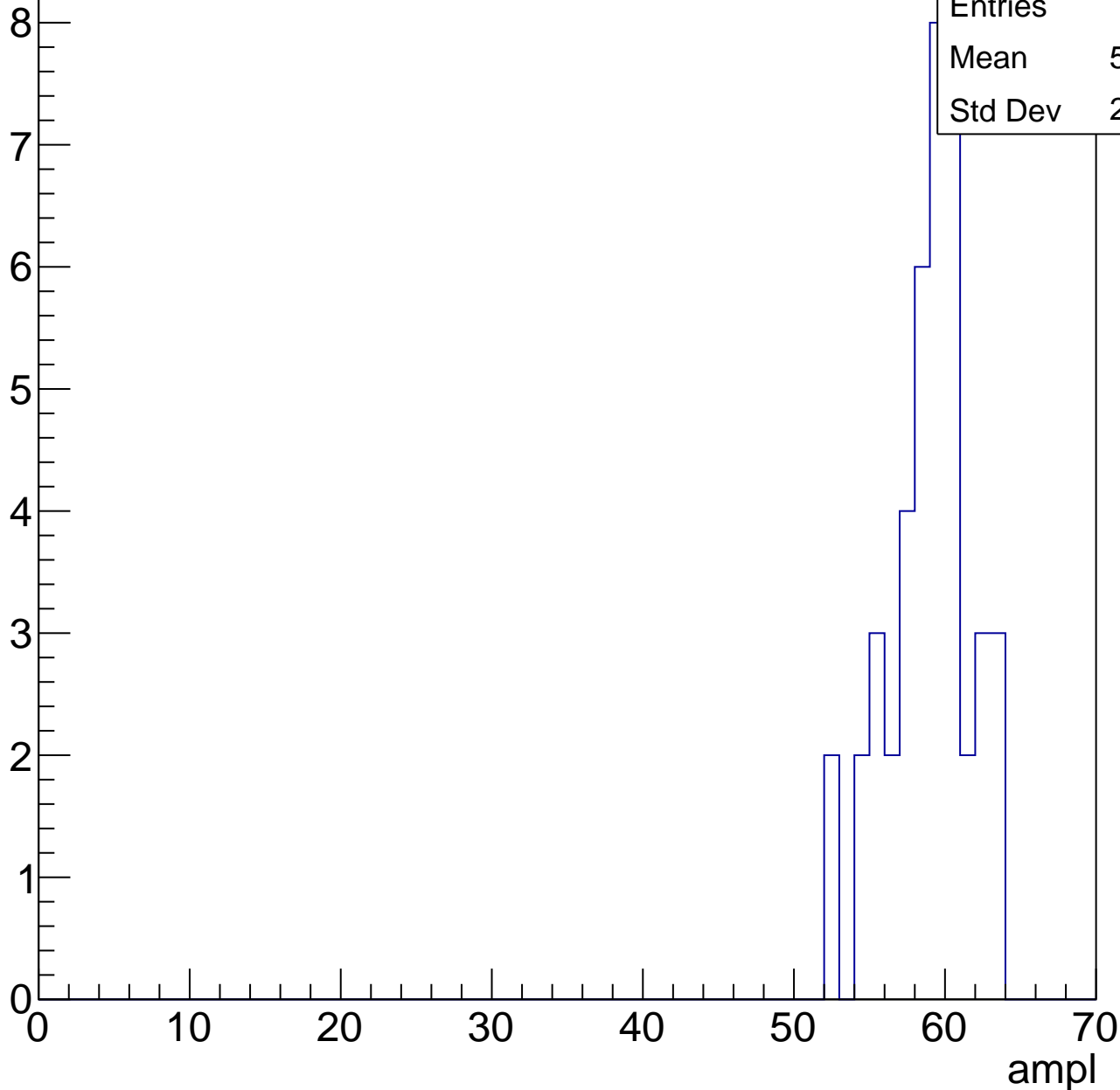


B1L102S, U17-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.47
Std Dev	2.705

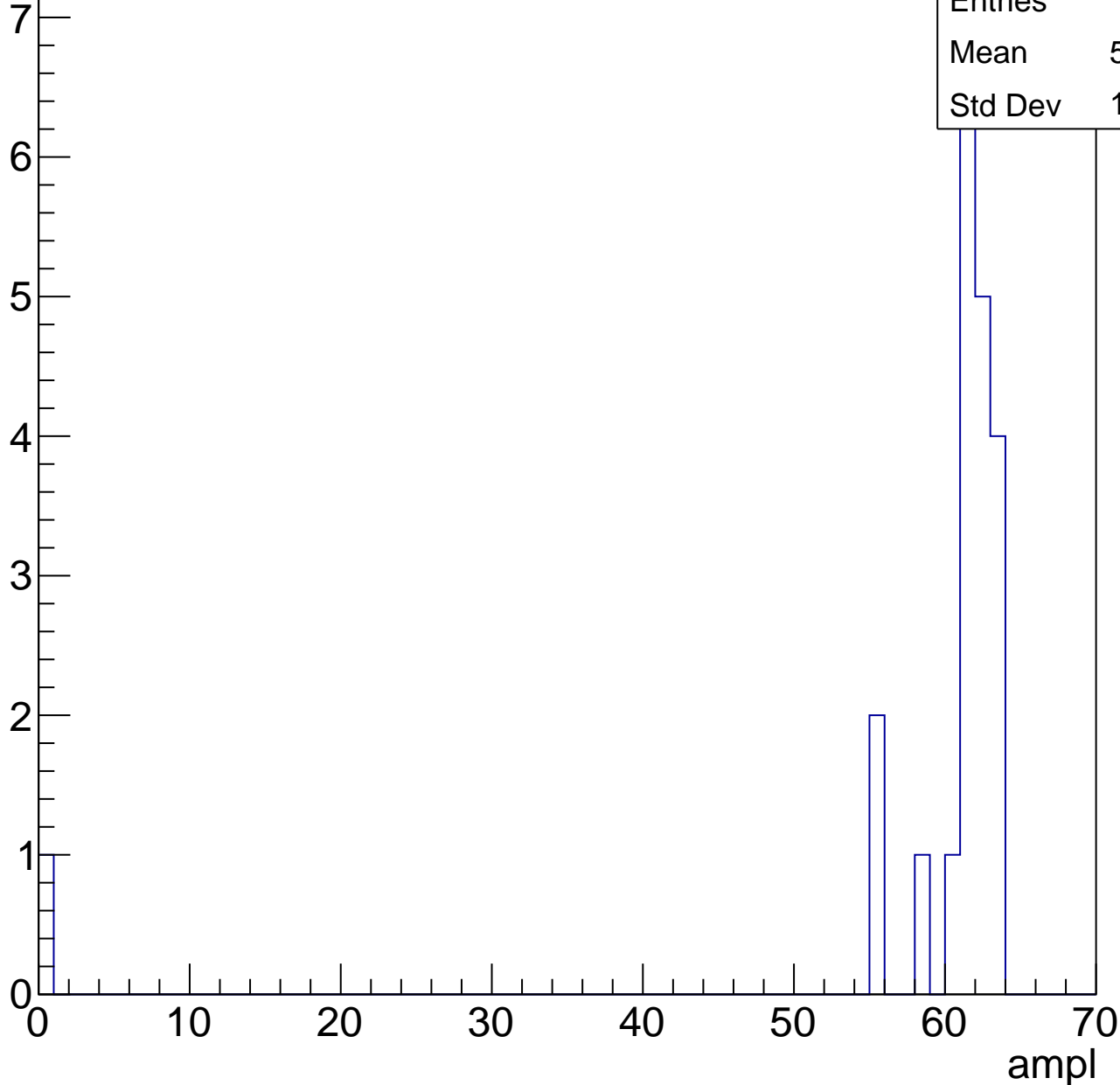


B1L102S, U17-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	57.95
Std Dev	13.15



B1L102S, U17-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch19, adc0

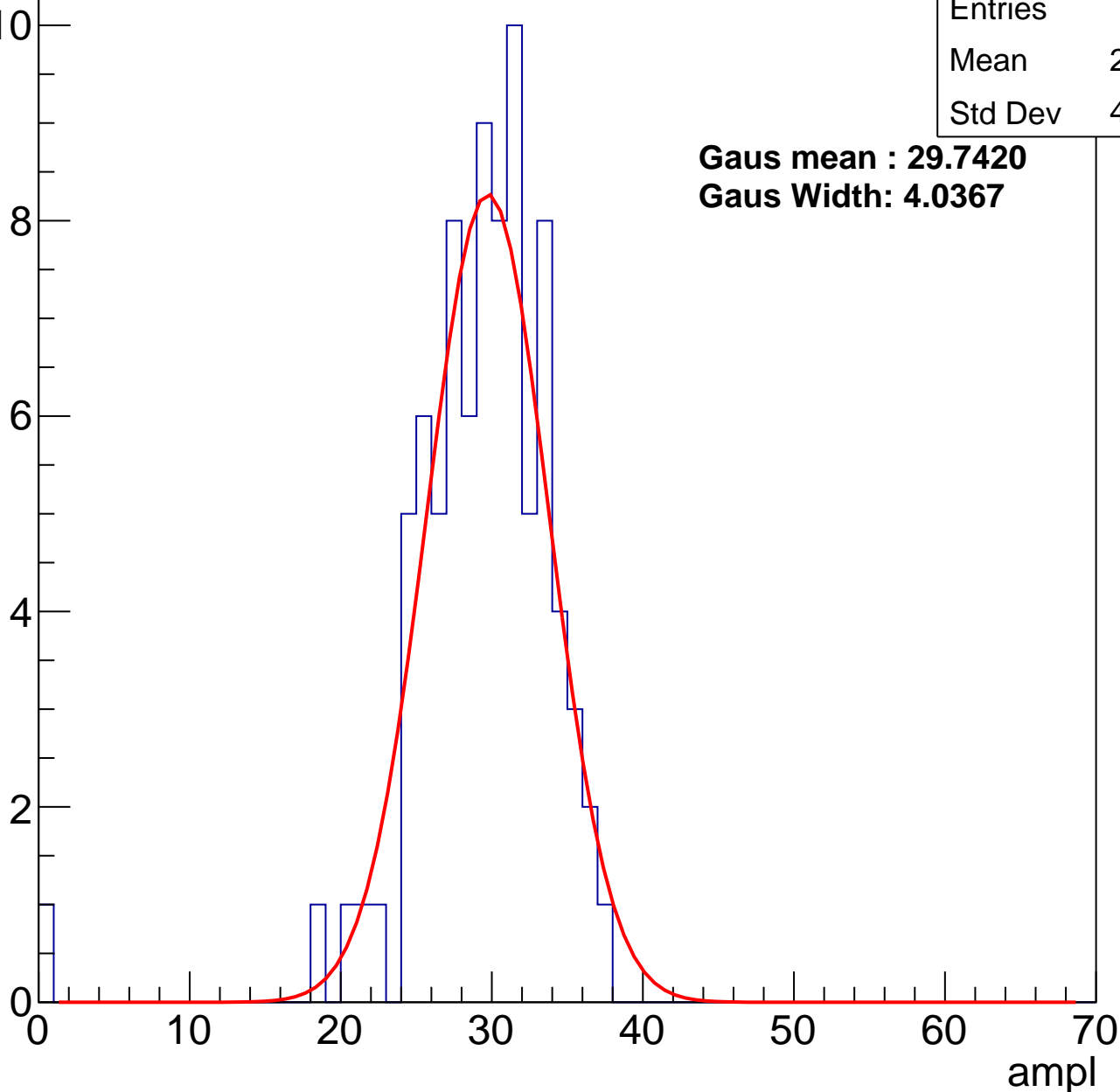
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	28.82
Std Dev	4.909

Gaus mean : 29.7420

Gaus Width: 4.0367



B1L102S, U17-ch19, adc1

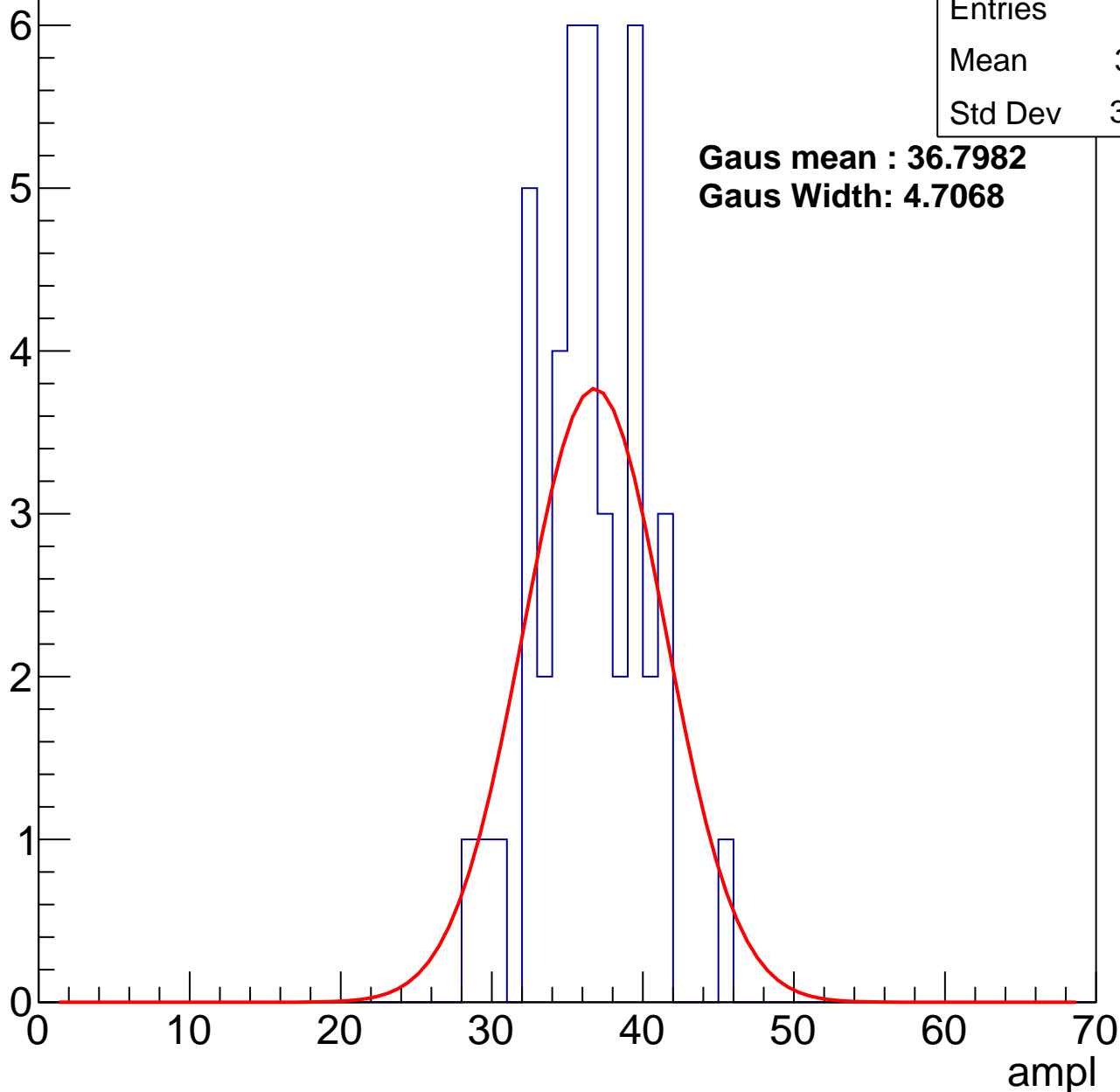
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	35.91
Std Dev	3.483

Gaus mean : 36.7982

Gaus Width: 4.7068



B1L102S, U17-ch19, adc2

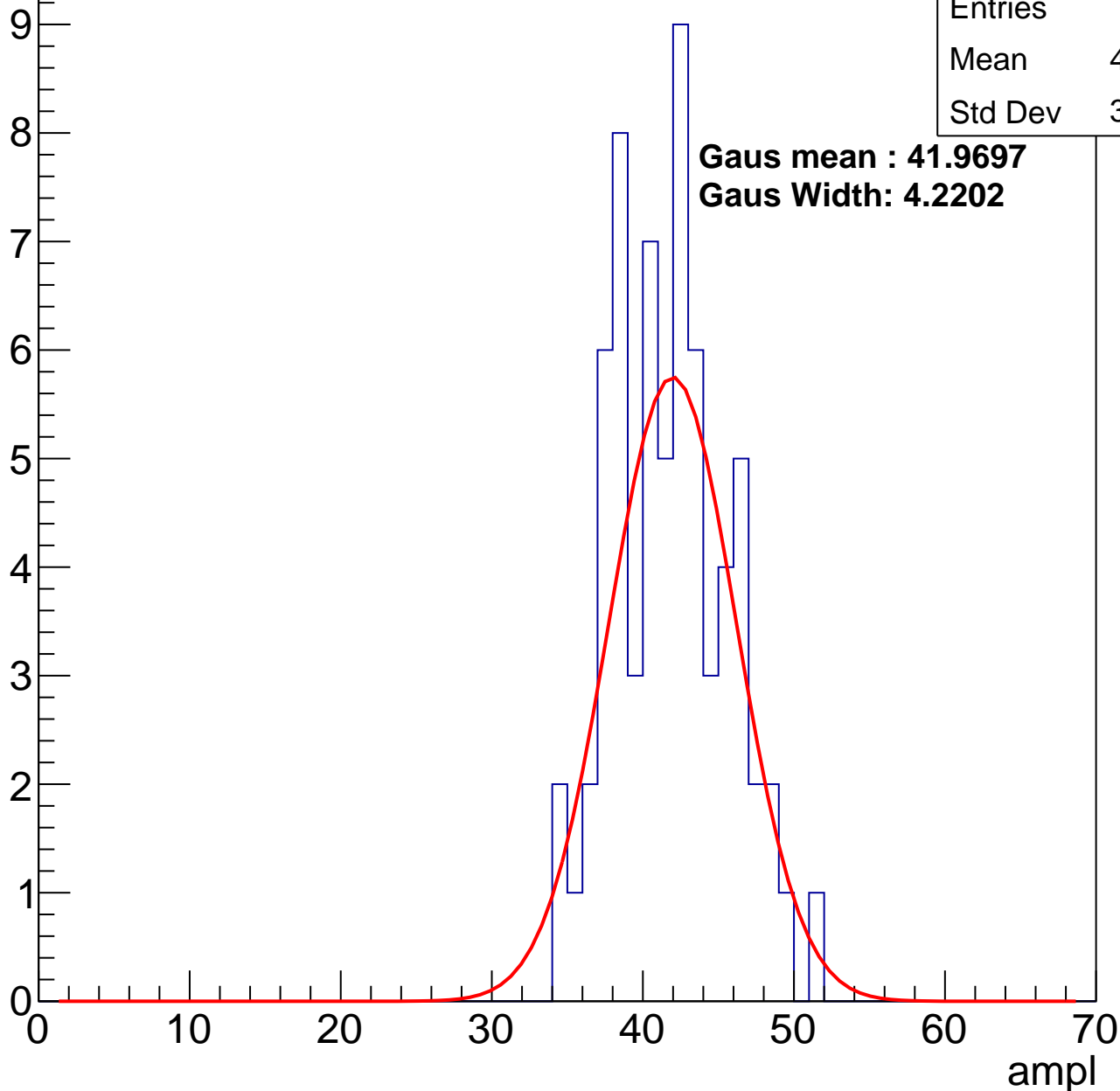
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.36
Std Dev	3.756

Gaus mean : 41.9697

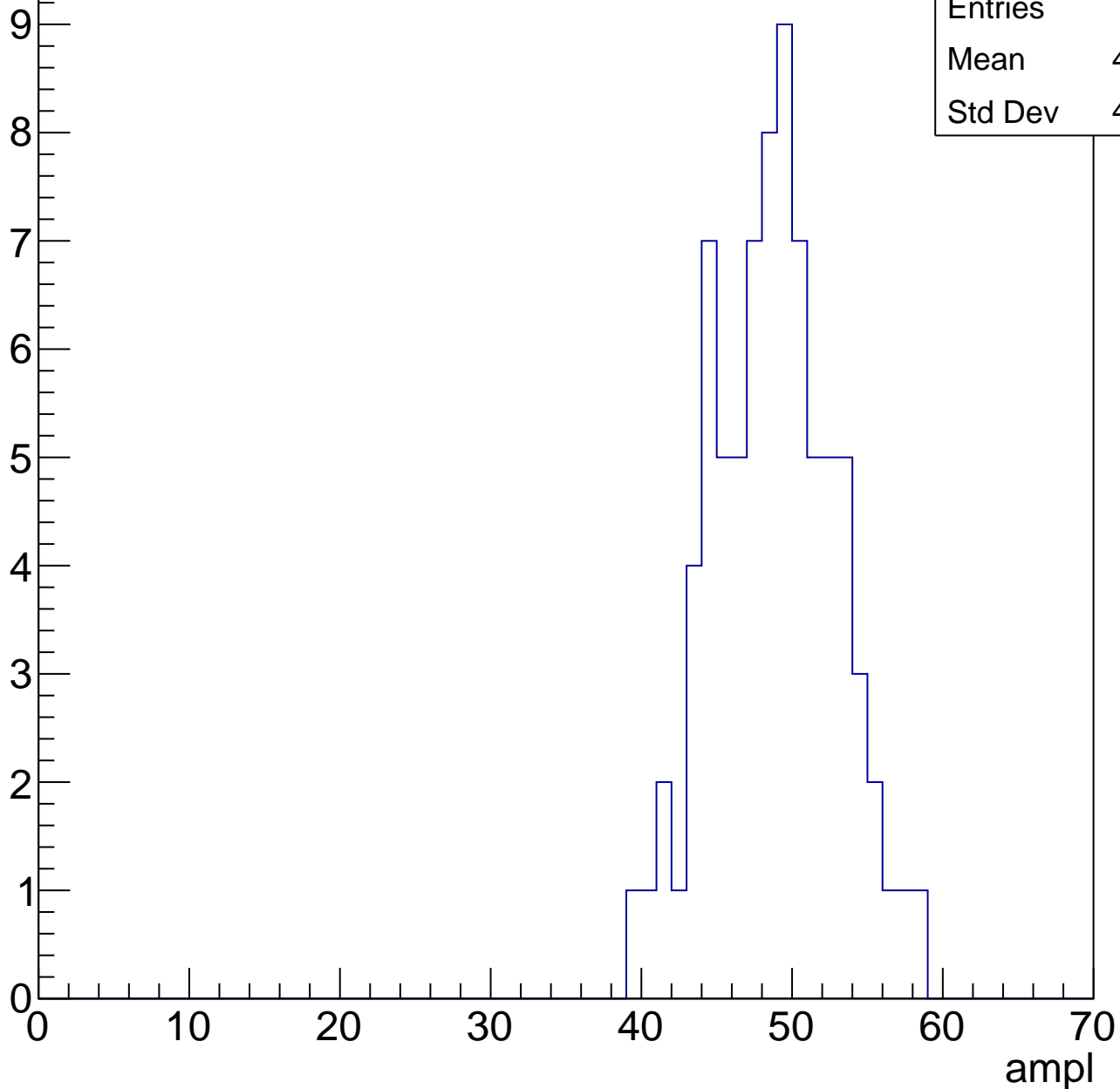
Gaus Width: 4.2202



B1L102S, U17-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



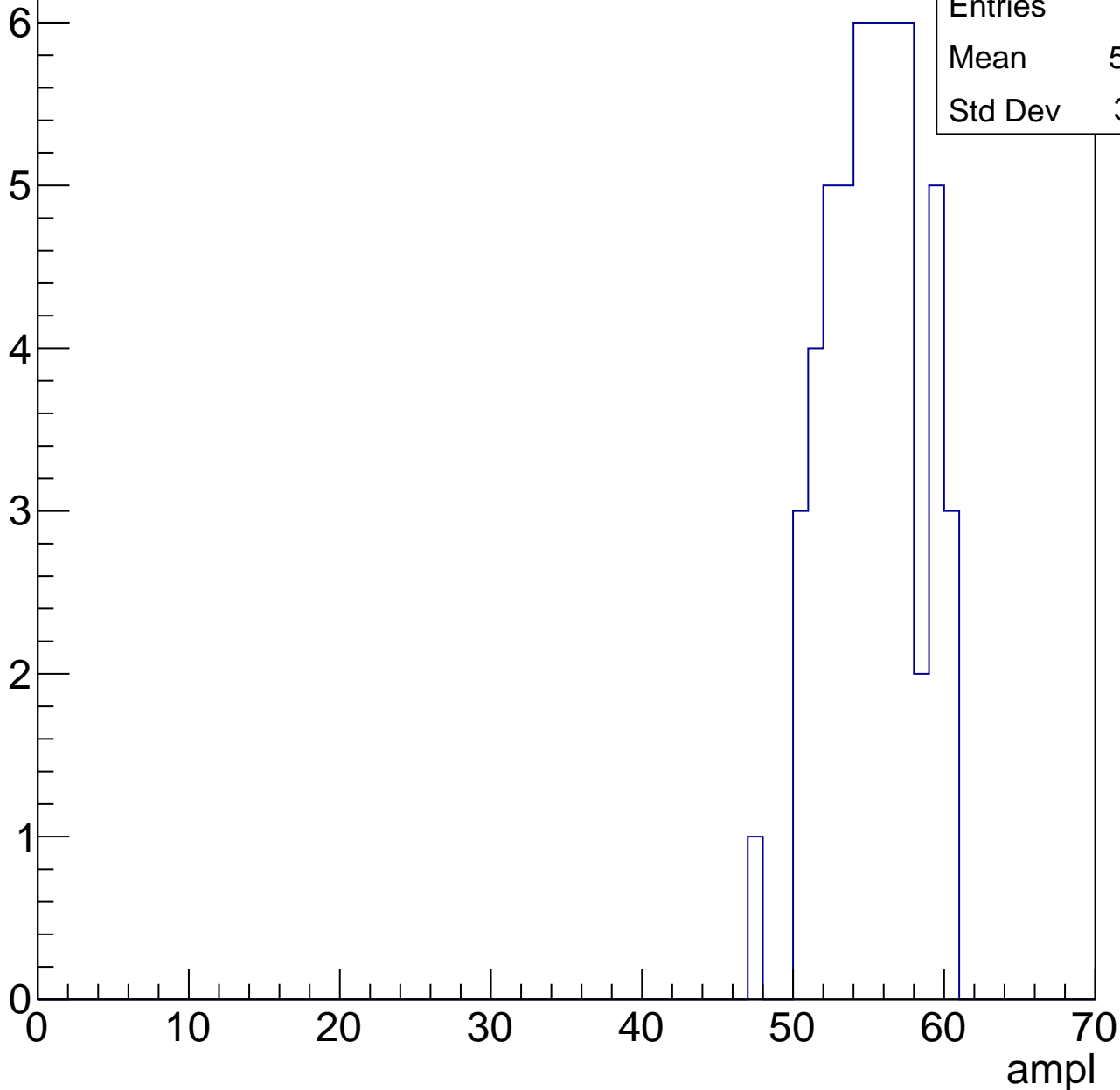
Entries	80
Mean	48.31
Std Dev	4.021

B1L102S, U17-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.79
Std Dev	3.021

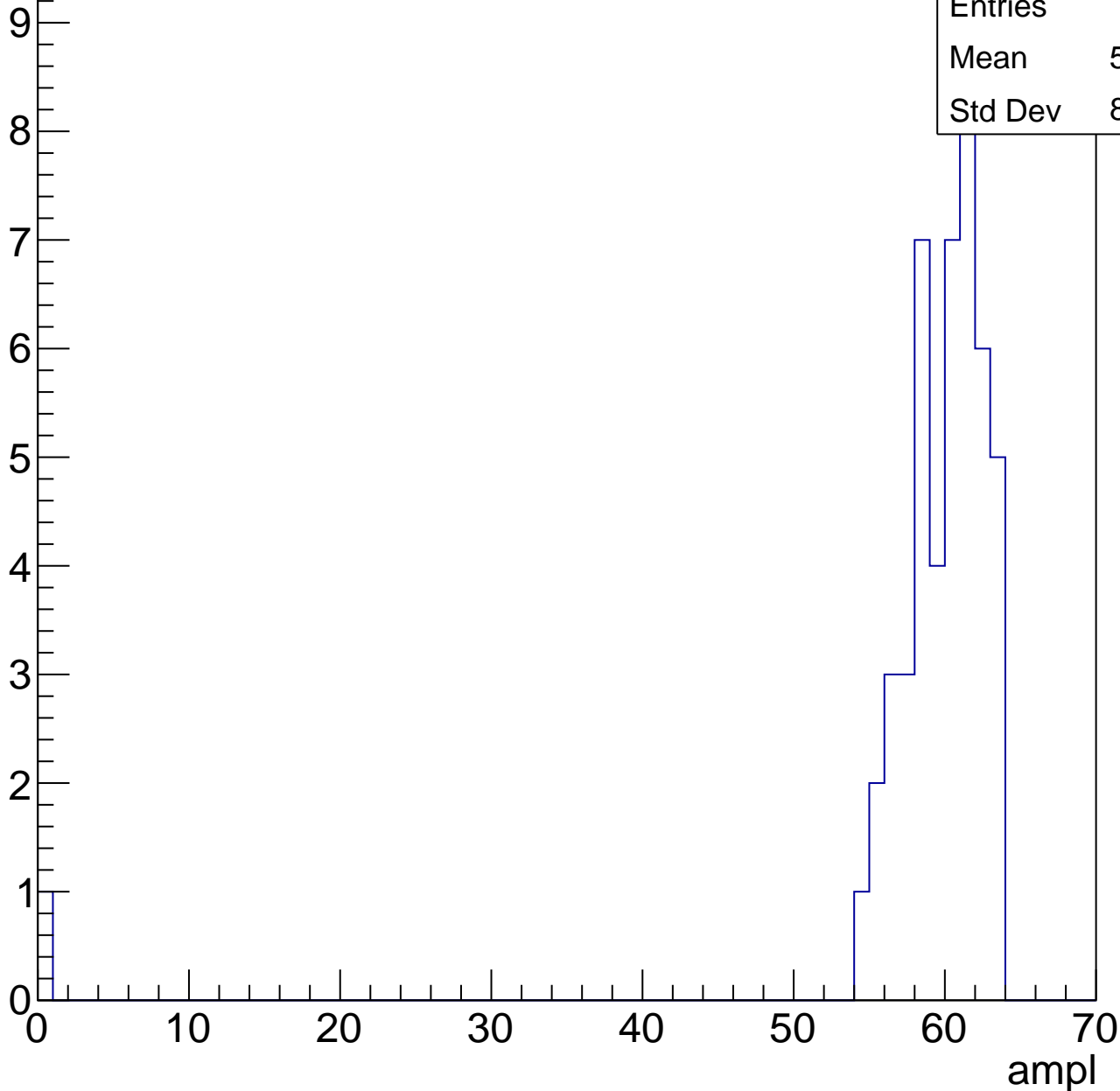


B1L102S, U17-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.35
Std Dev	8.828

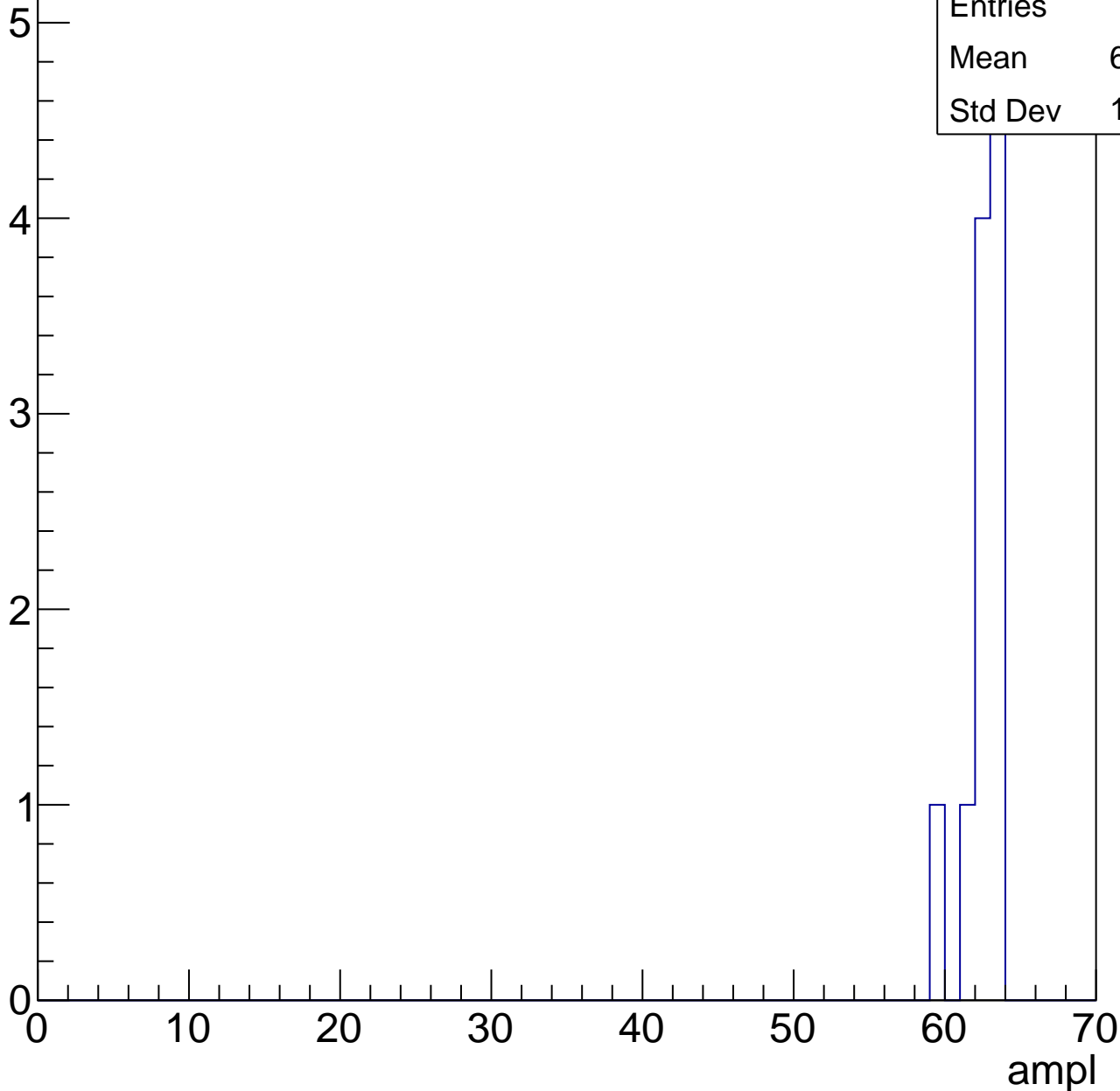


B1L102S, U17-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	62.09
Std Dev	1.164



B1L102S, U17-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch20, adc0

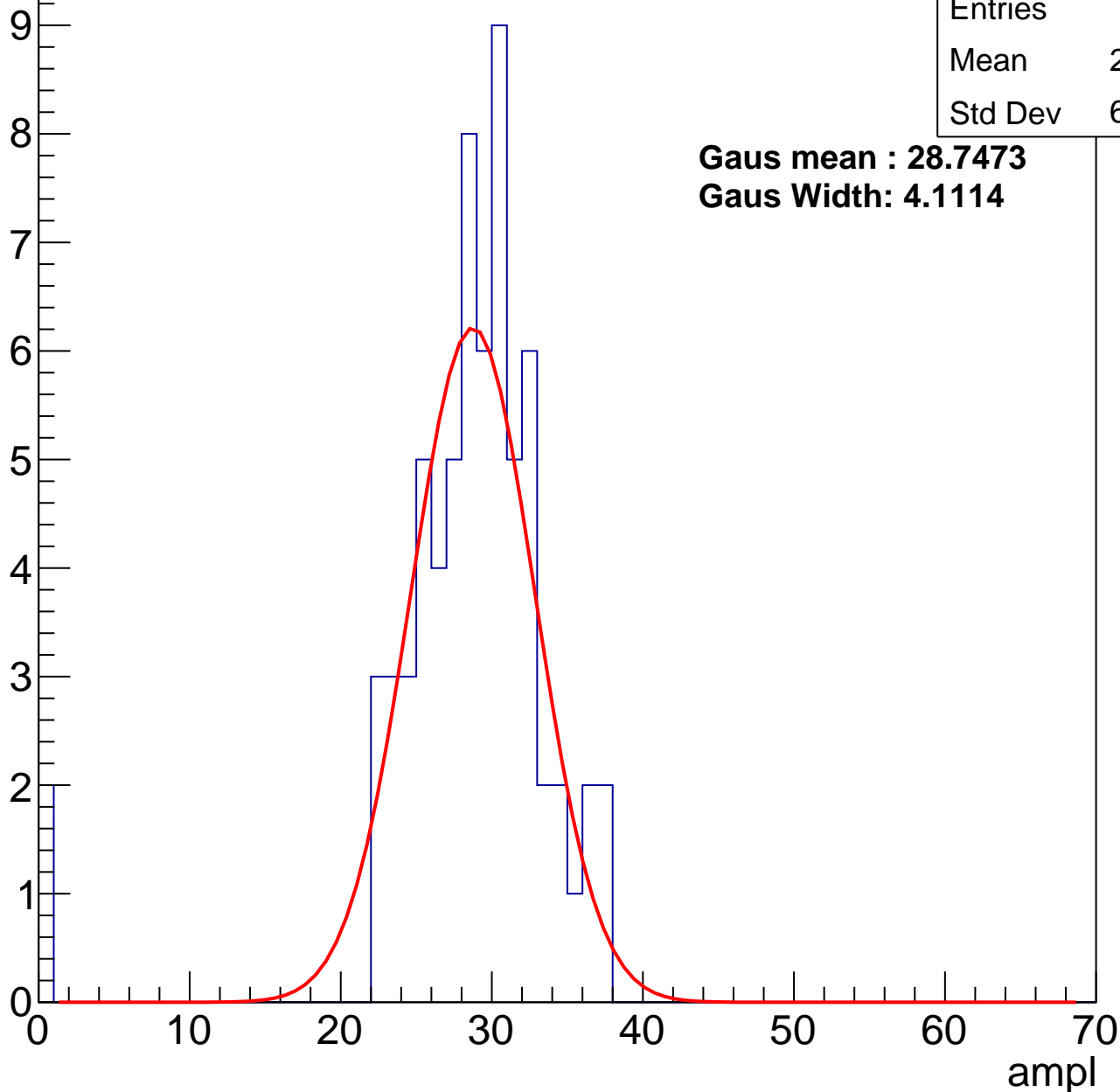
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.96
Std Dev	6.074

Gaus mean : 28.7473

Gaus Width: 4.1114



B1L102S, U17-ch20, adc1

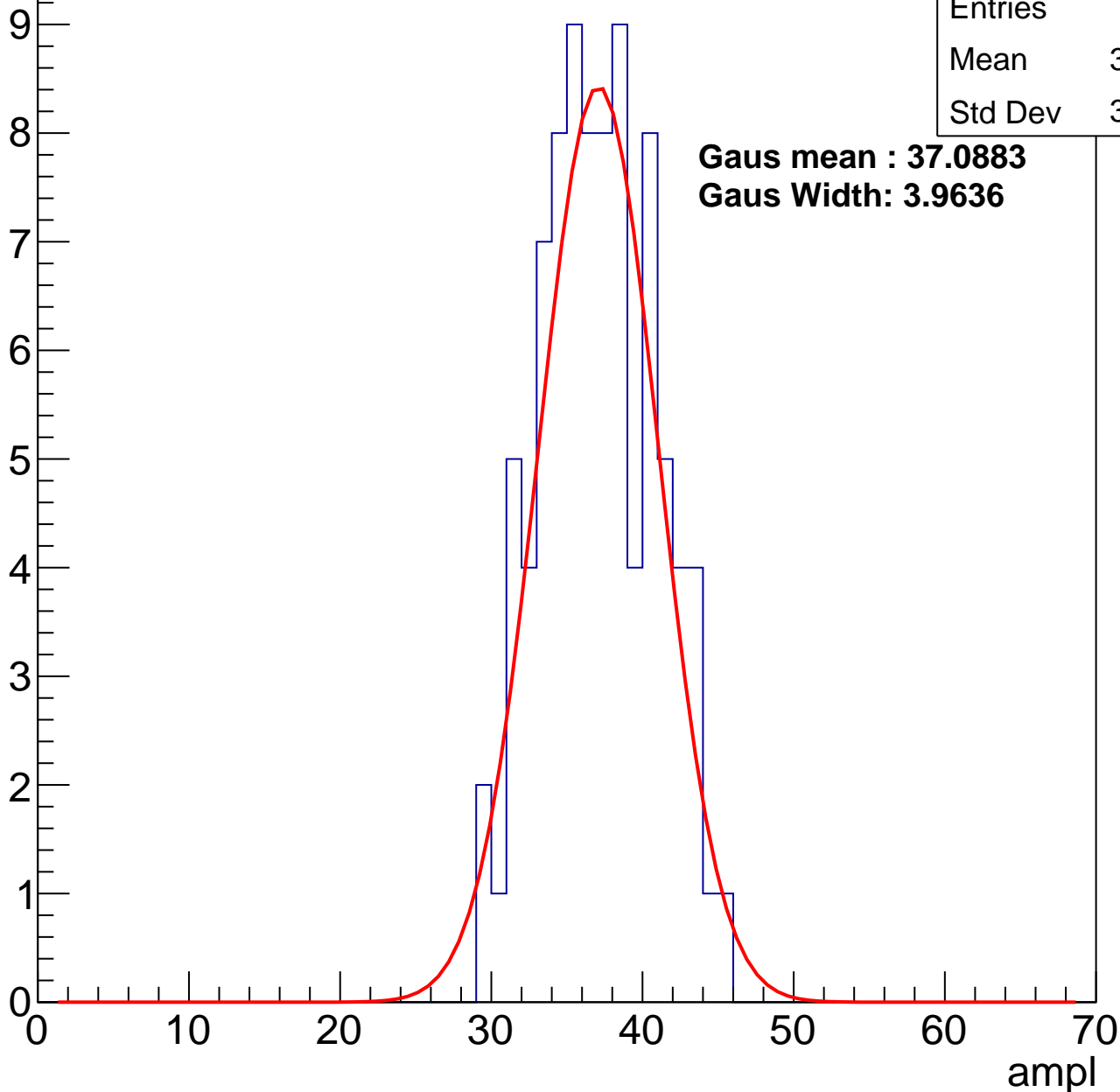
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	36.65
Std Dev	3.705

Gaus mean : 37.0883

Gaus Width: 3.9636



B1L102S, U17-ch20, adc2

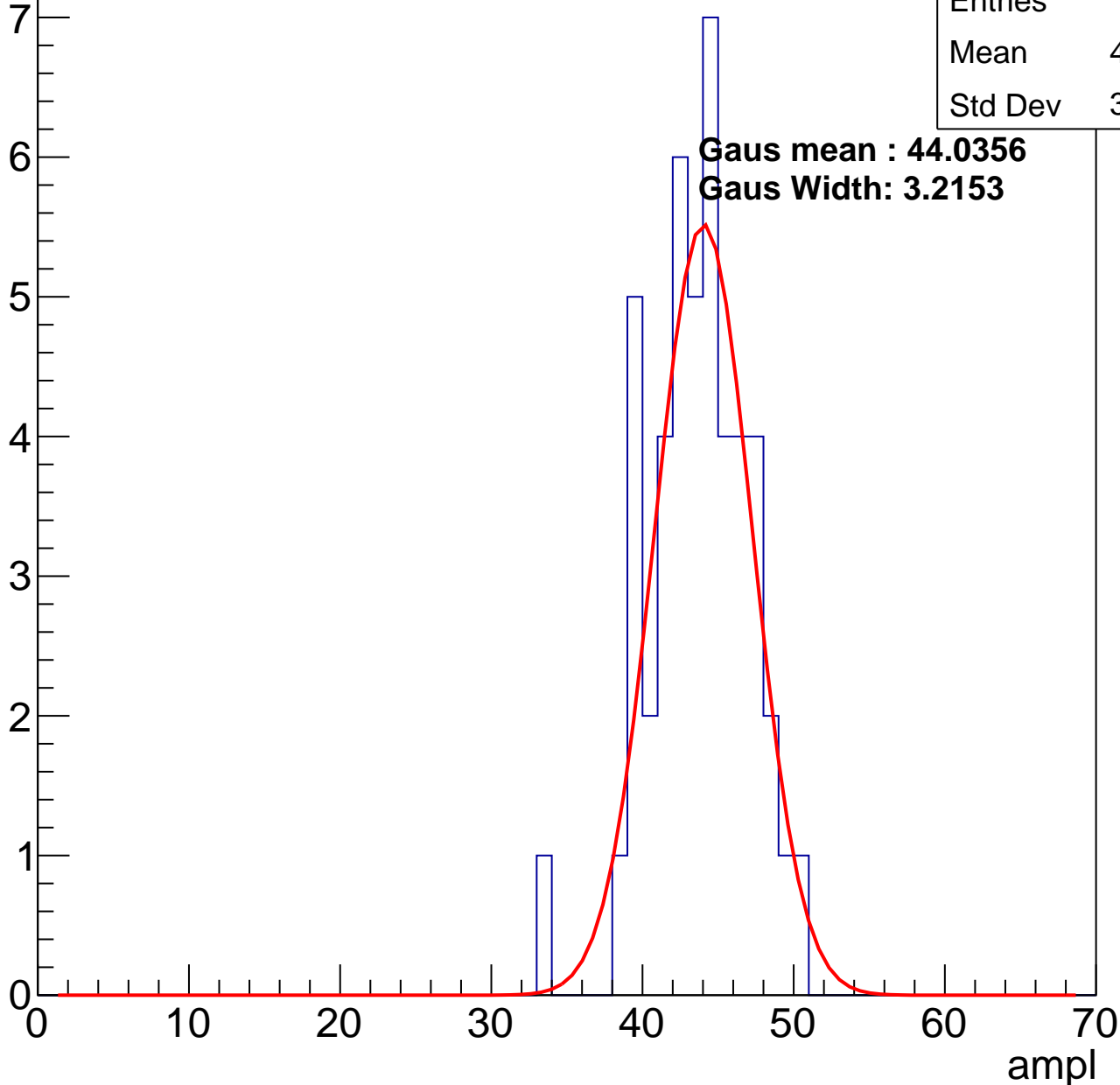
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	43.23
Std Dev	3.263

Gaus mean : 44.0356

Gaus Width: 3.2153

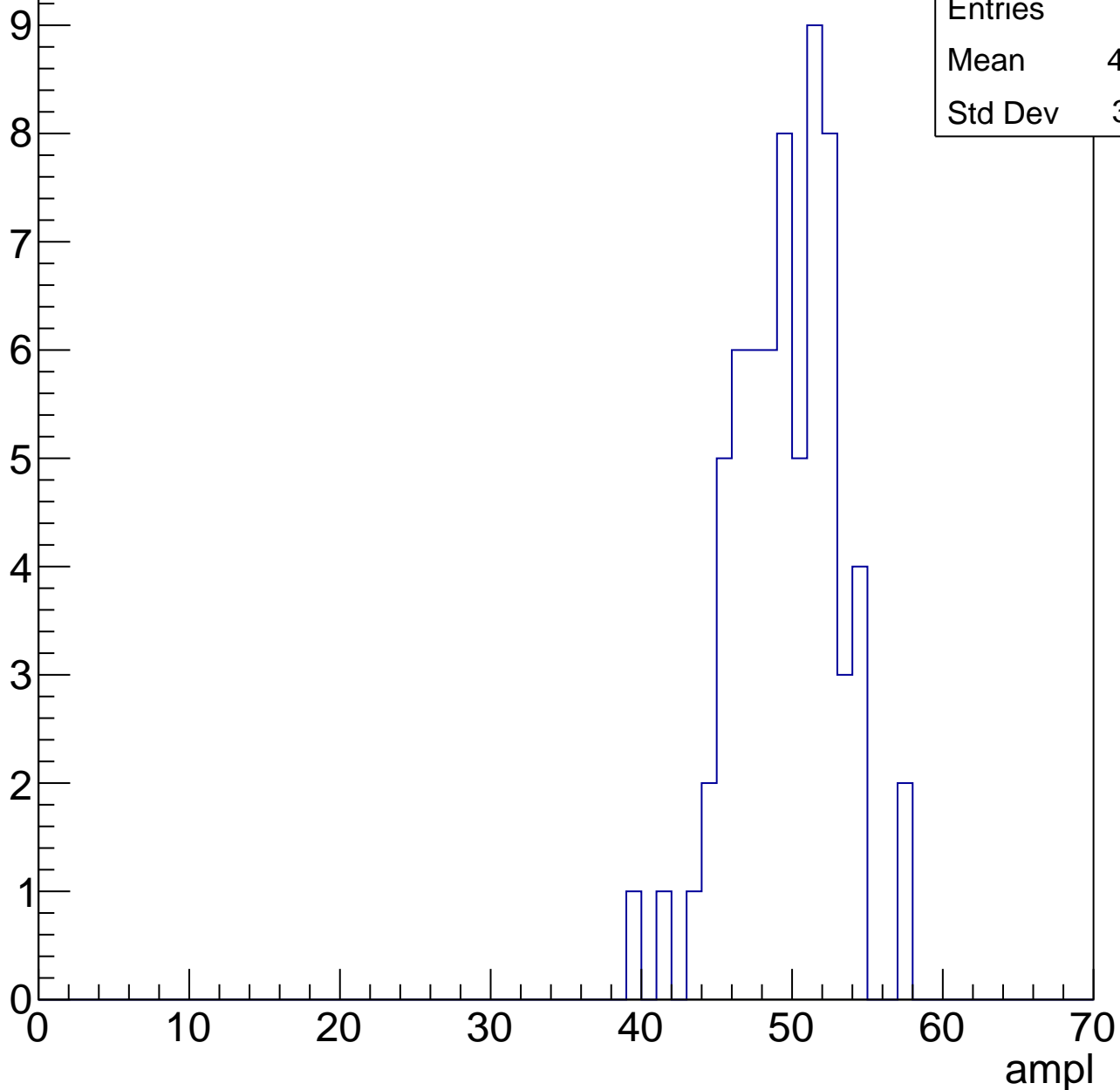


B1L102S, U17-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

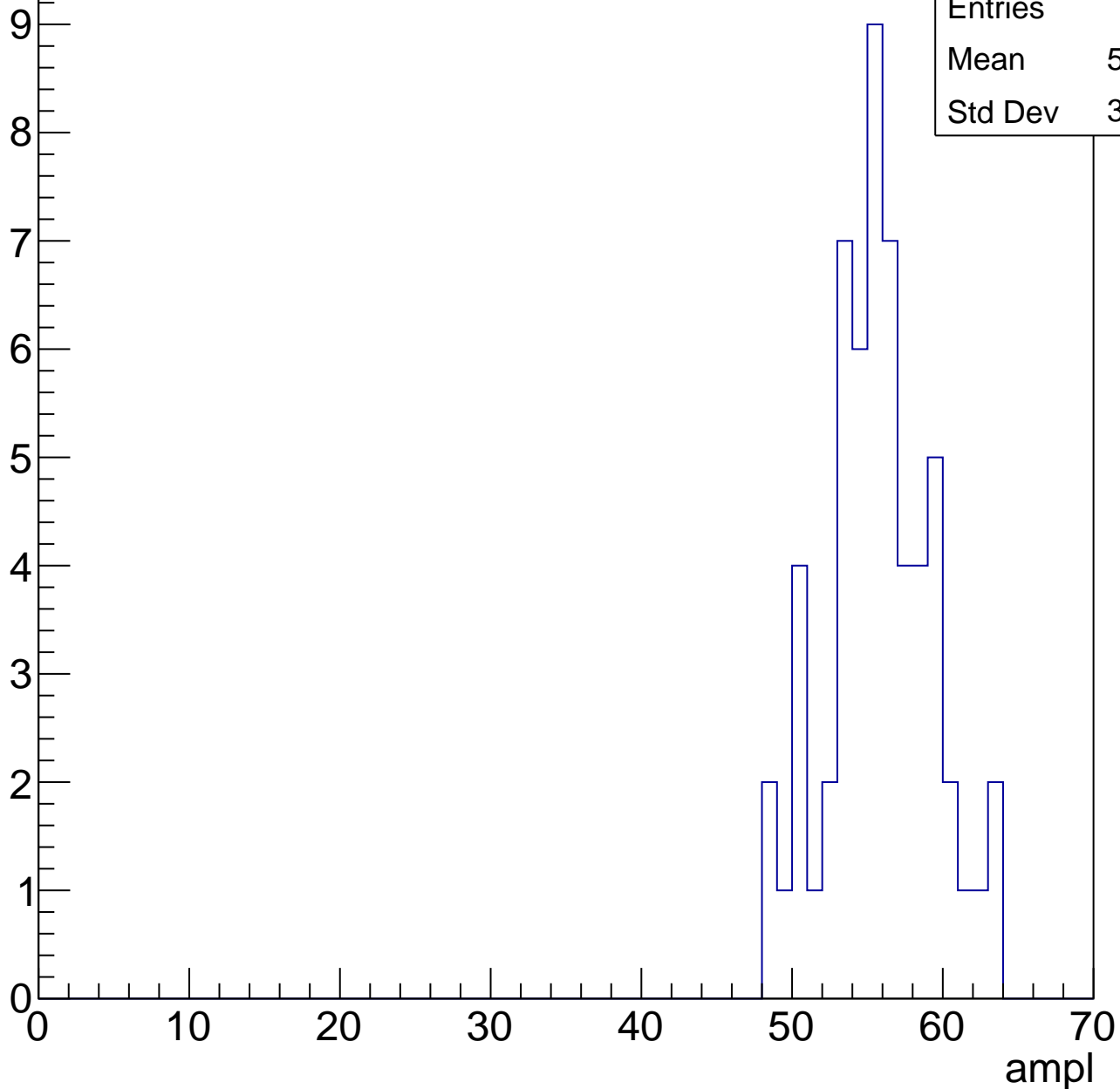
Entries	67
Mean	49.07
Std Dev	3.461



B1L102S, U17-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



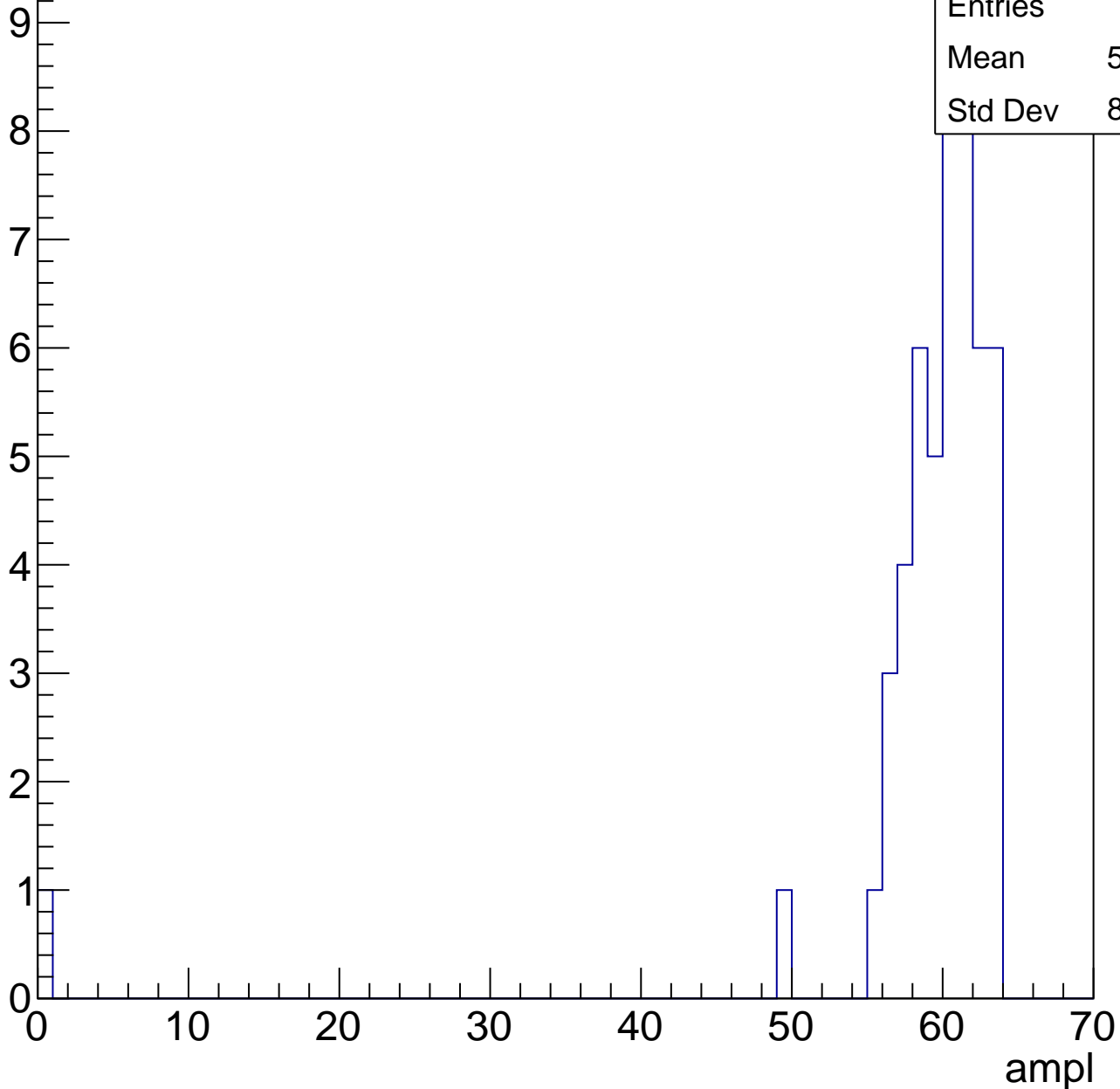
Entries	58
Mean	55.28
Std Dev	3.473

B1L102S, U17-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.44
Std Dev	8.746



B1L102S, U17-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U17-ch21, adc0

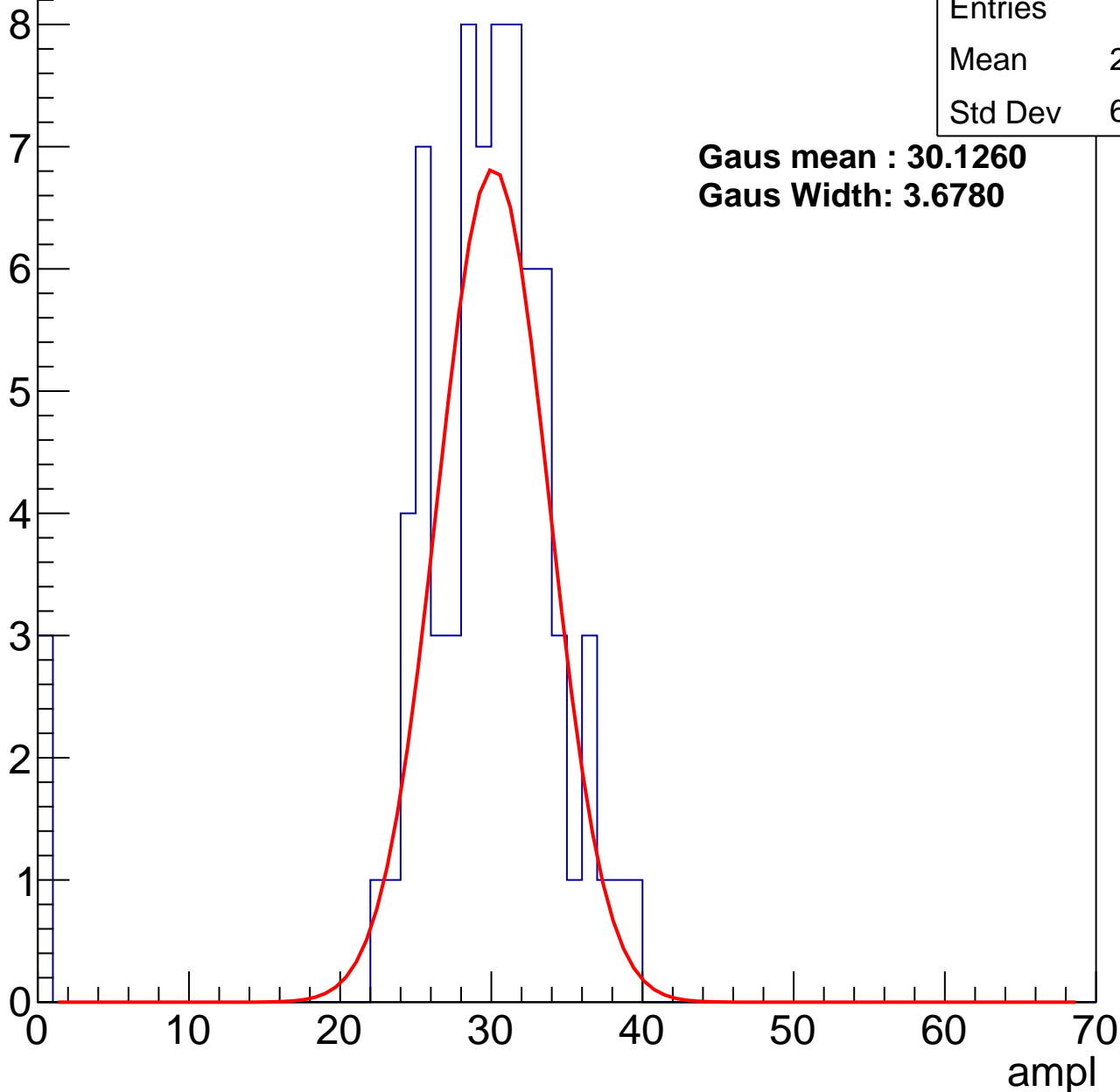
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.52
Std Dev	6.873

Gaus mean : 30.1260

Gaus Width: 3.6780



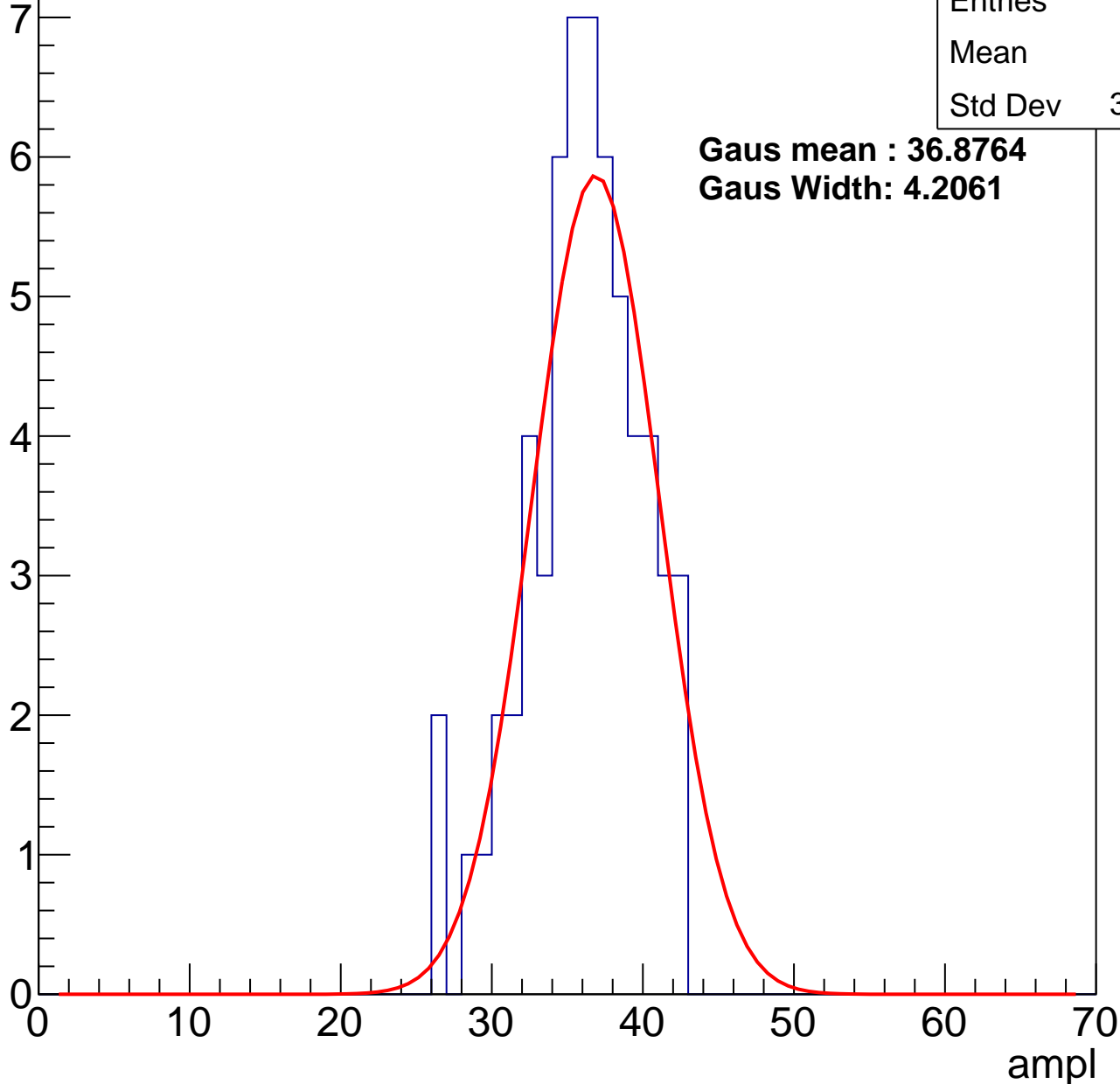
B1L102S, U17-ch21, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.6
Std Dev	3.778

Gaus mean : 36.8764
Gaus Width: 4.2061



B1L102S, U17-ch21, adc2

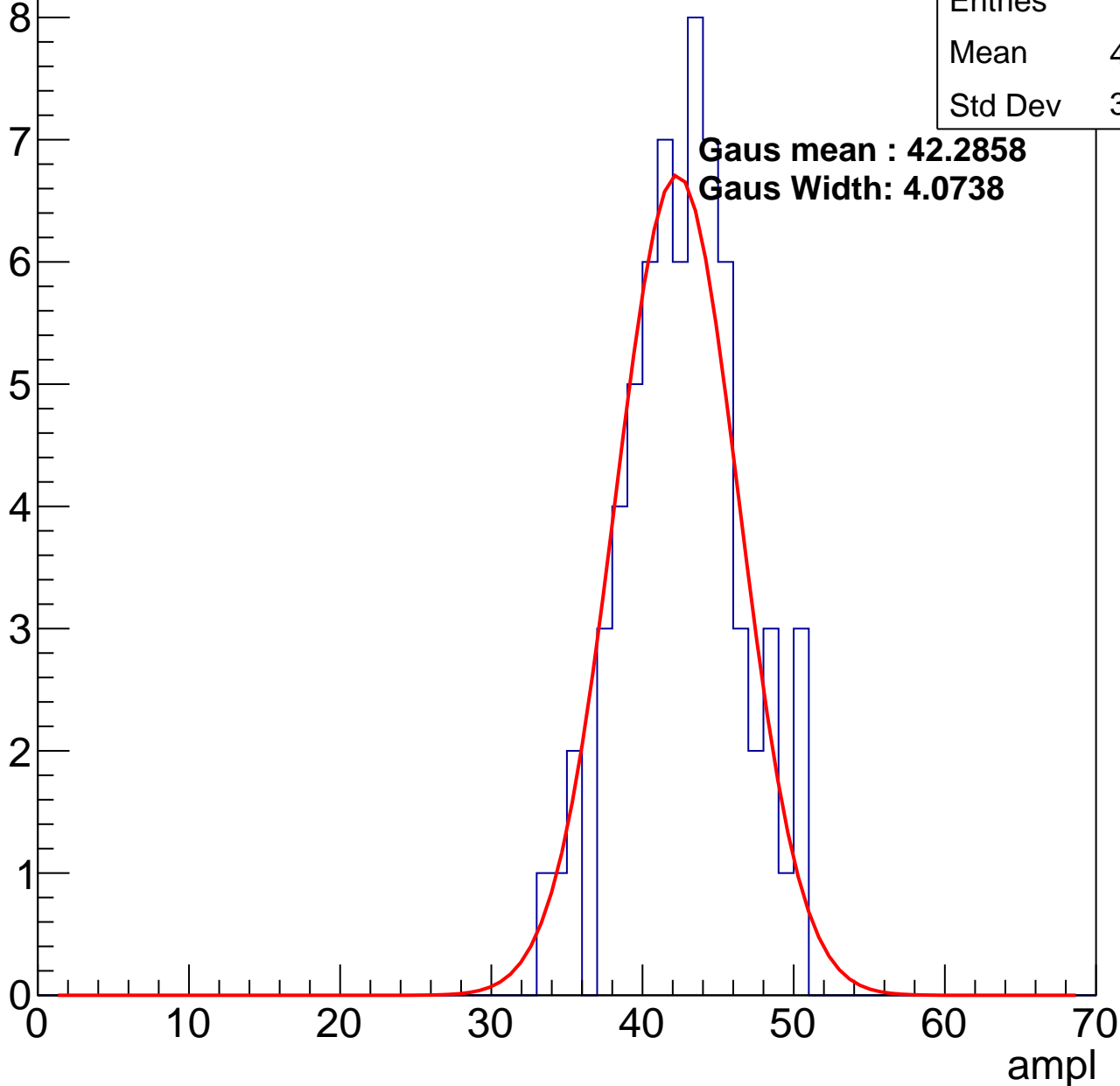
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.22
Std Dev	3.819

Gaus mean : 42.2858

Gaus Width: 4.0738

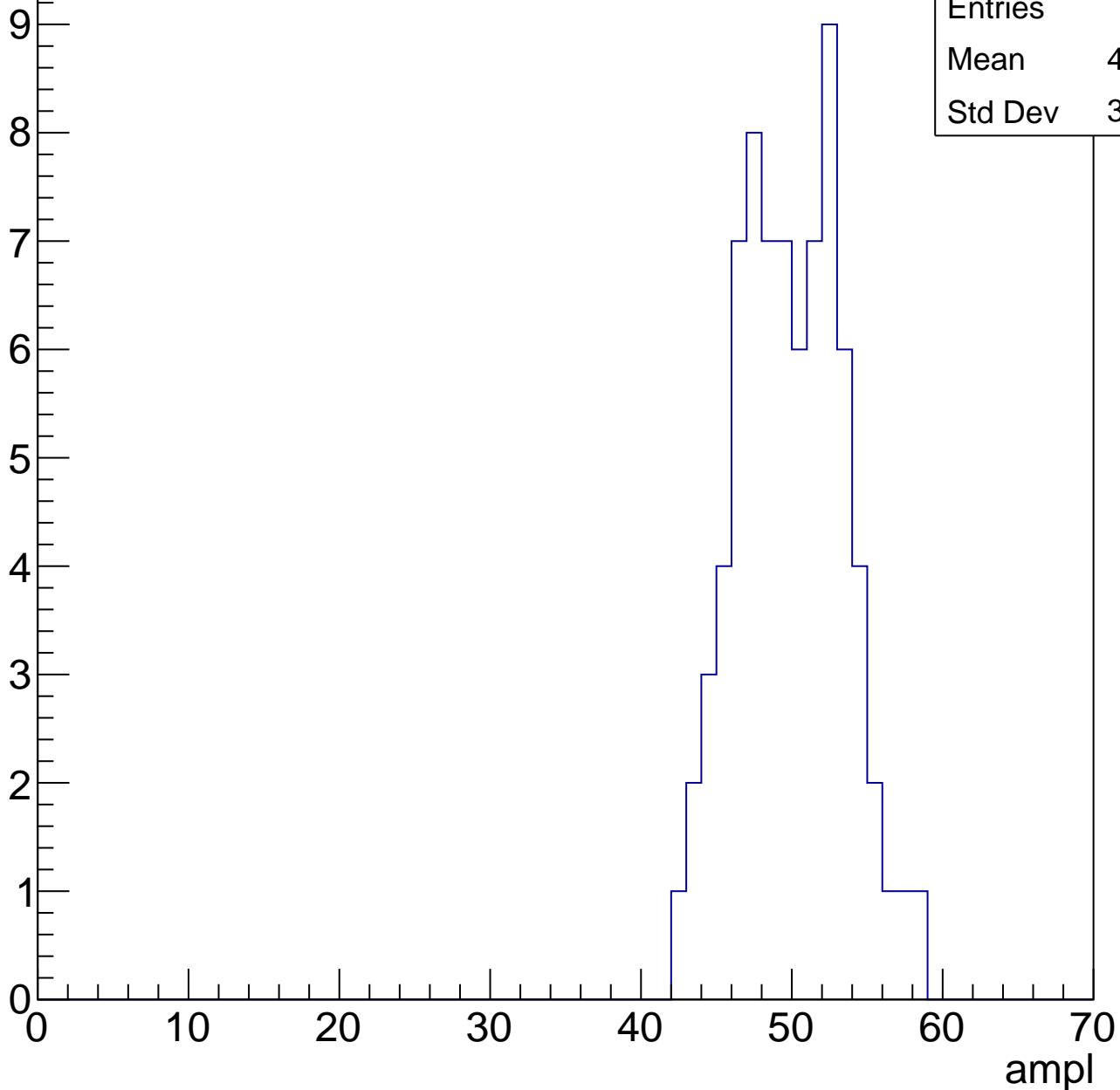


B1L102S, U17-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	49.43
Std Dev	3.488

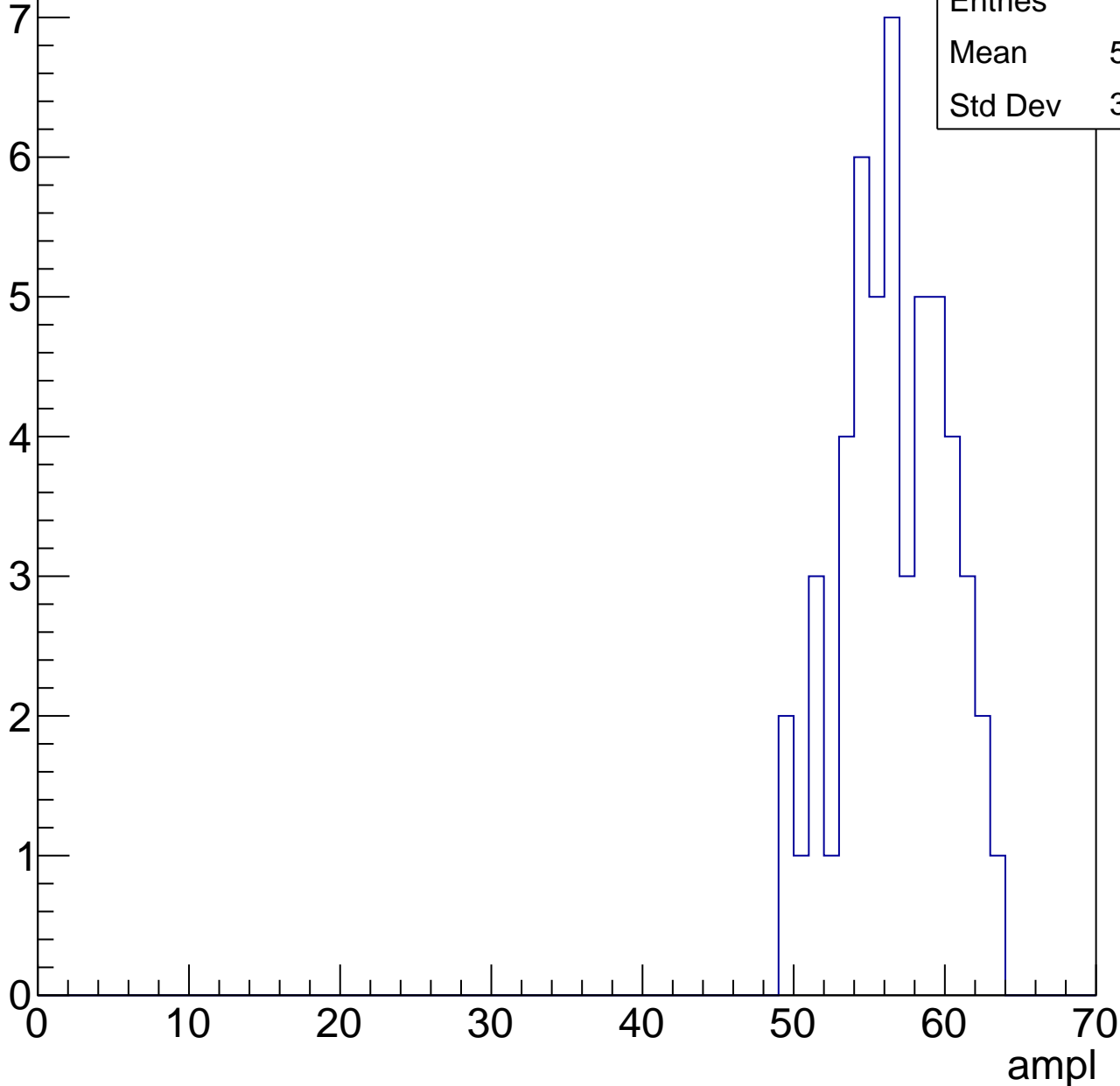


B1L102S, U17-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.19
Std Dev	3.442

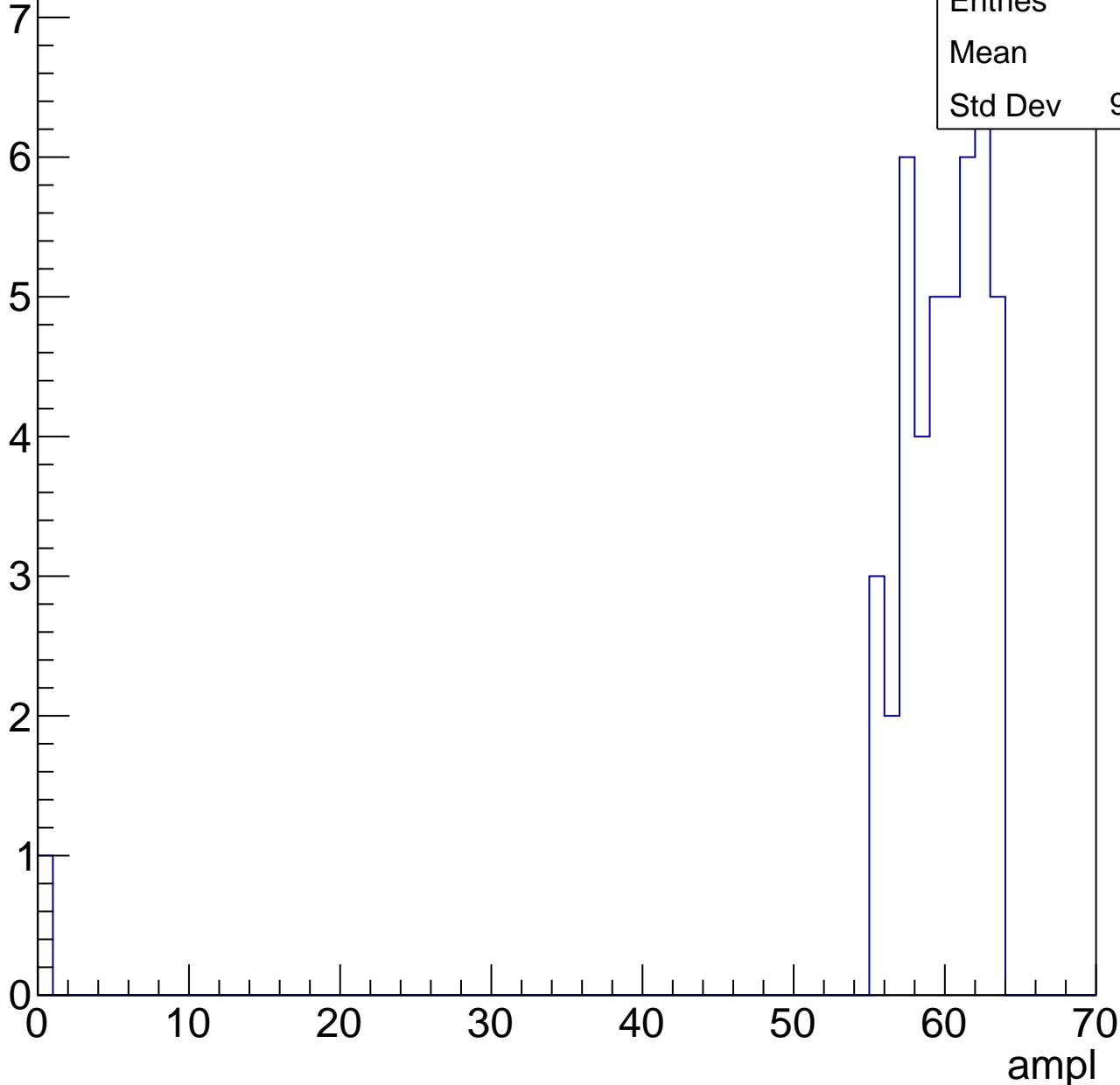


B1L102S, U17-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

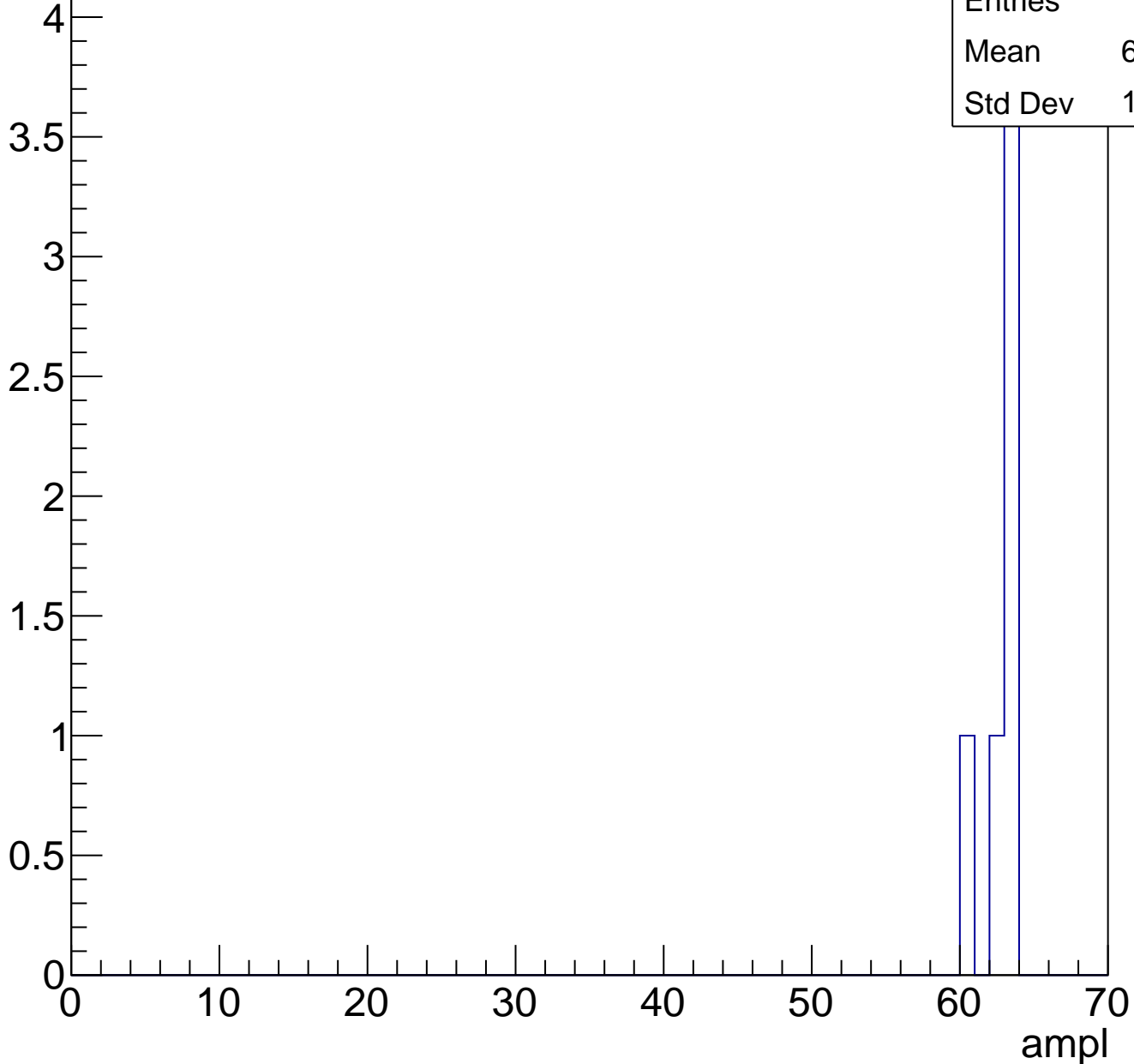
Entries	44
Mean	58.2
Std Dev	9.194



B1L102S, U17-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

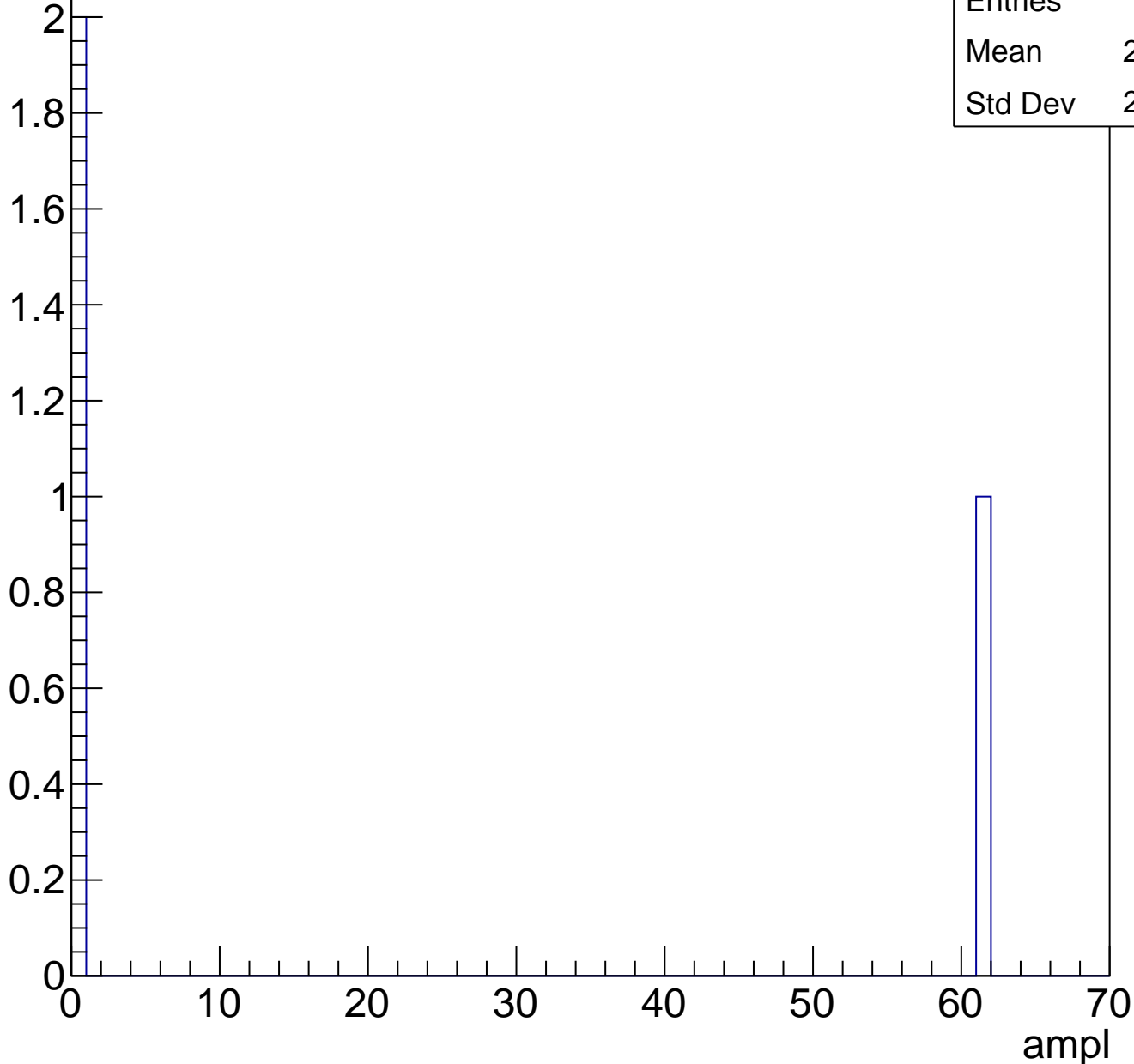
Entry



B1L102S, U17-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch22, adc0

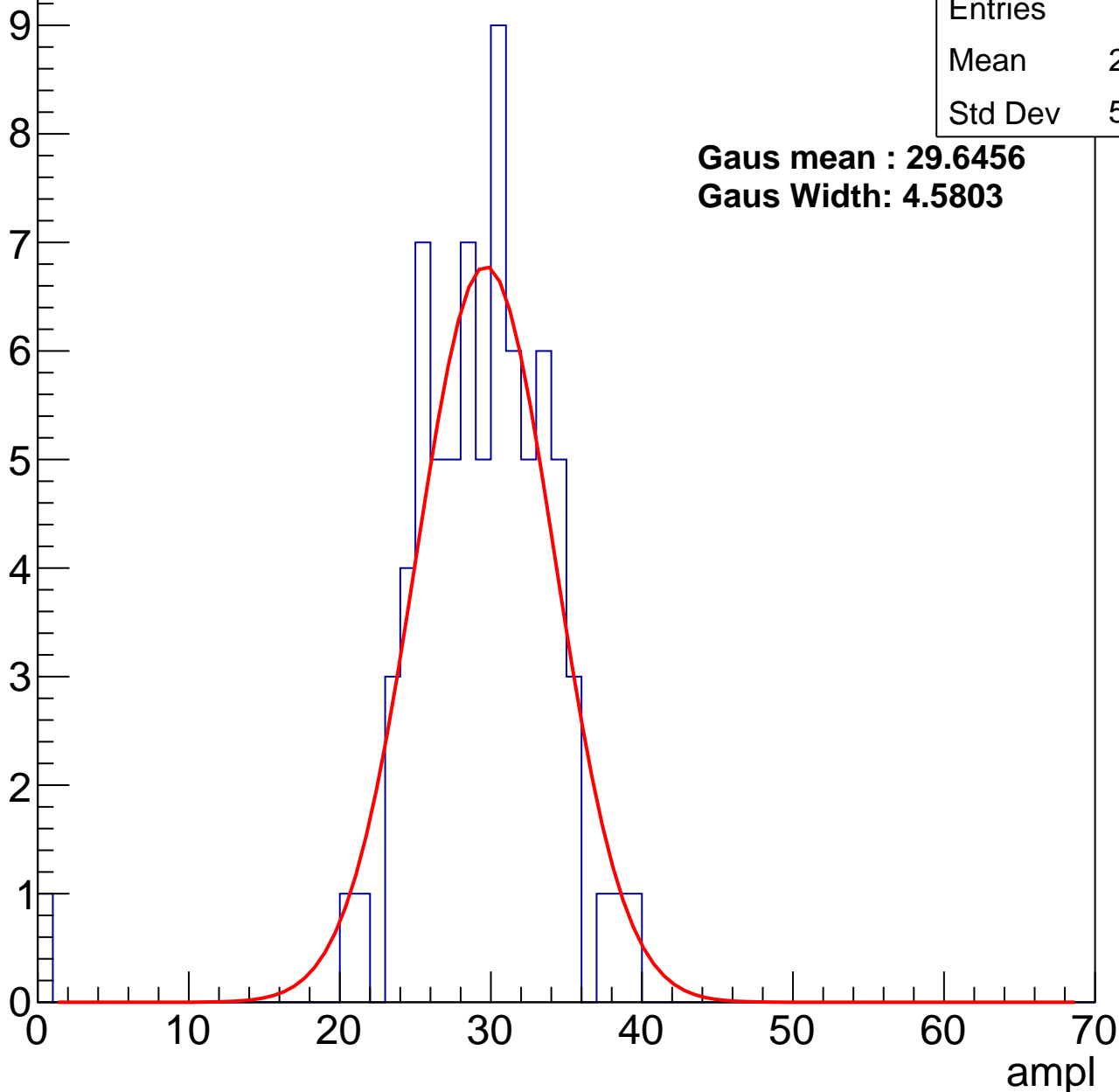
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.82
Std Dev	5.165

Gaus mean : 29.6456

Gaus Width: 4.5803



B1L102S, U17-ch22, adc1

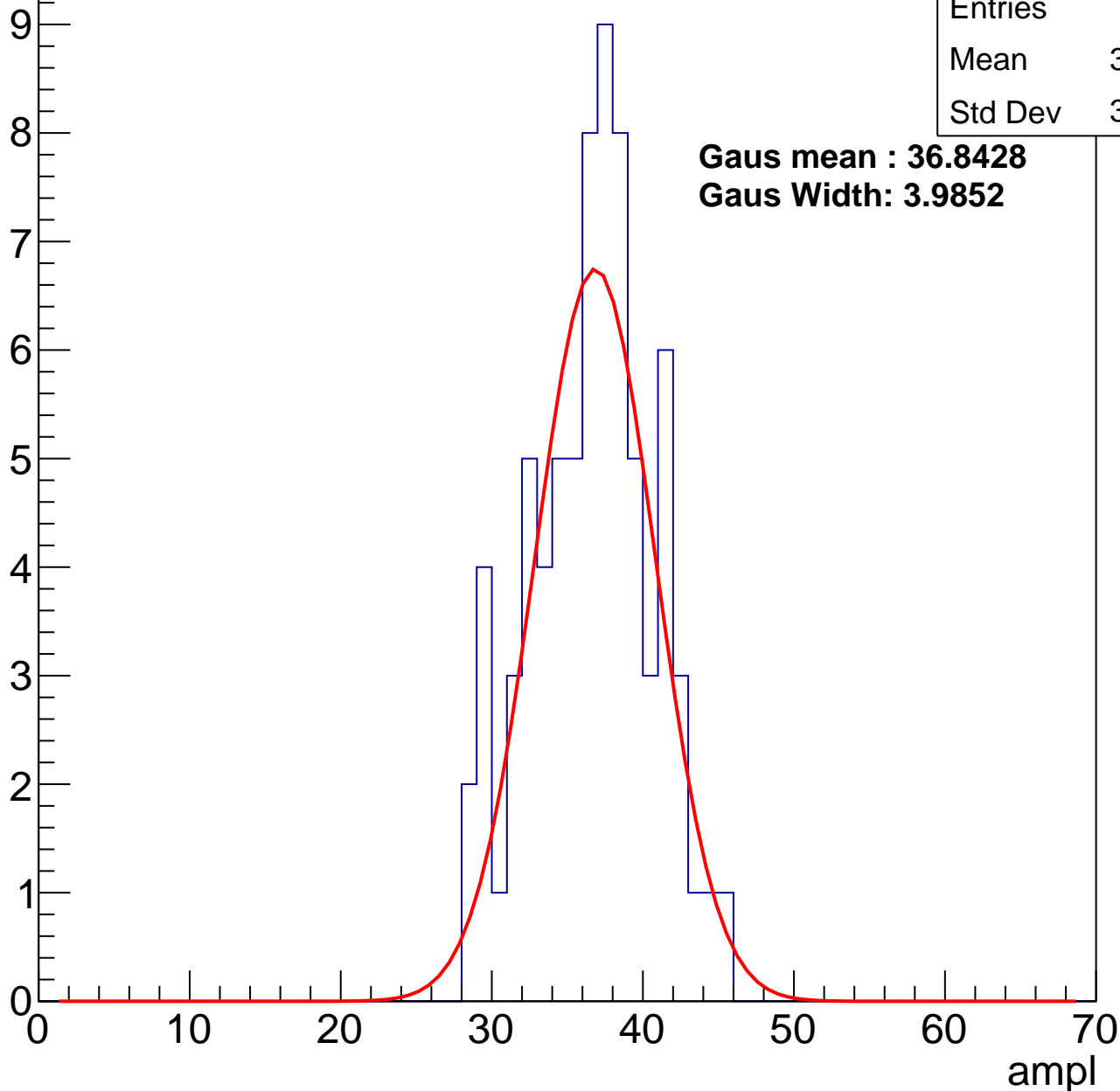
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.16
Std Dev	3.959

Gaus mean : 36.8428

Gaus Width: 3.9852



B1L102S, U17-ch22, adc2

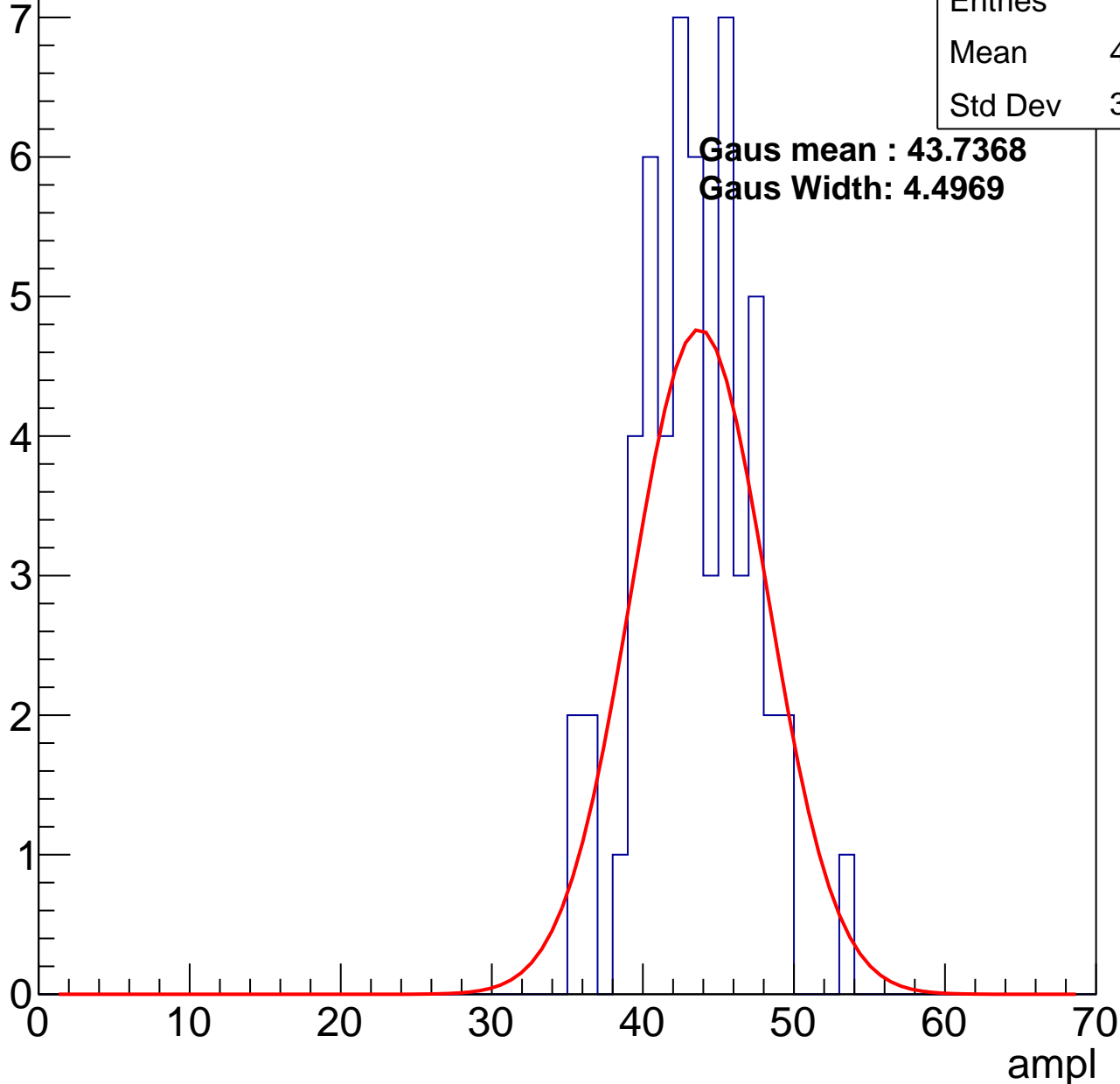
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.89
Std Dev	3.696

Gaus mean : 43.7368

Gaus Width: 4.4969

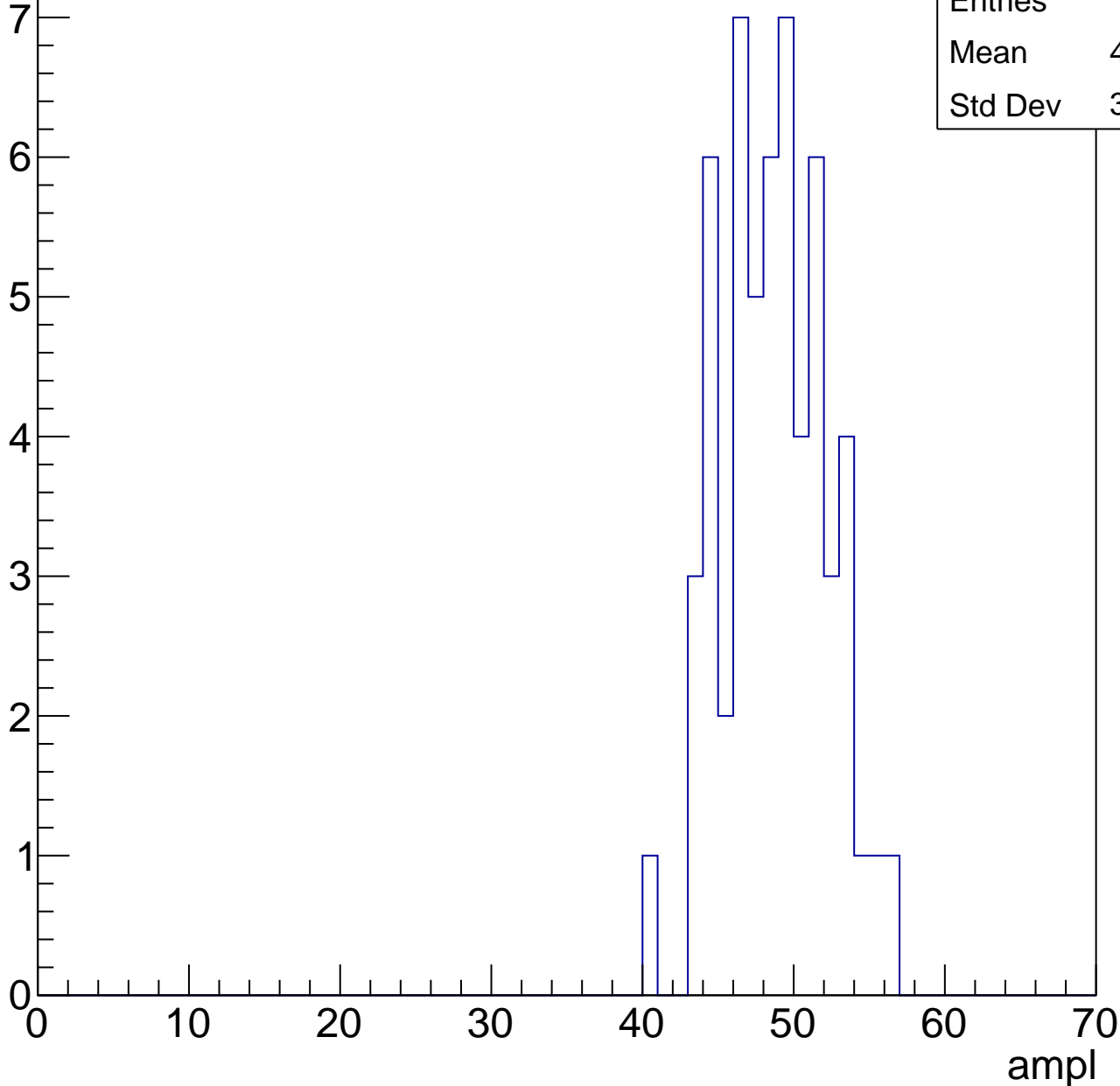


B1L102S, U17-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.25
Std Dev	3.394

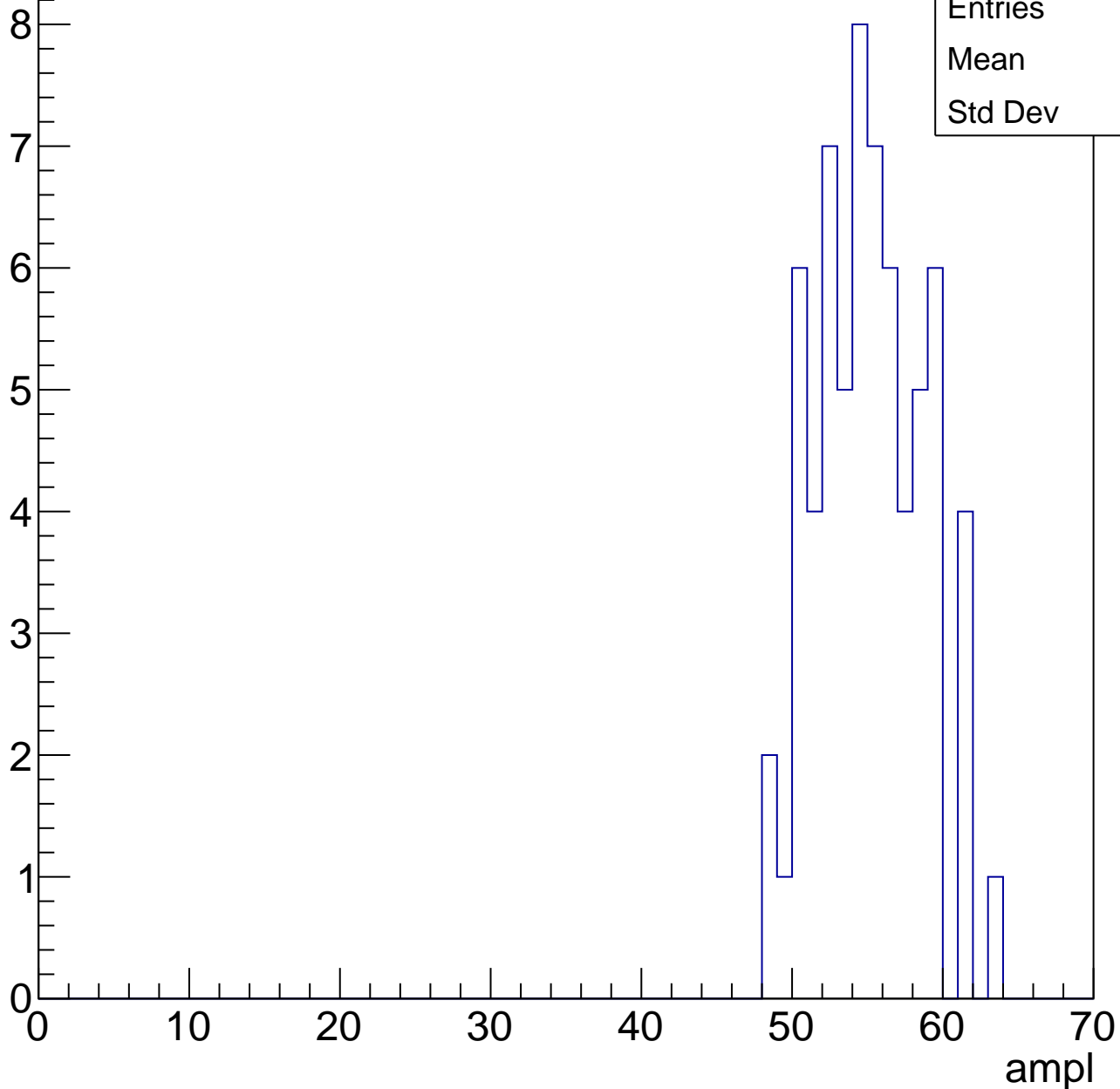


B1L102S, U17-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.7
Std Dev	3.49

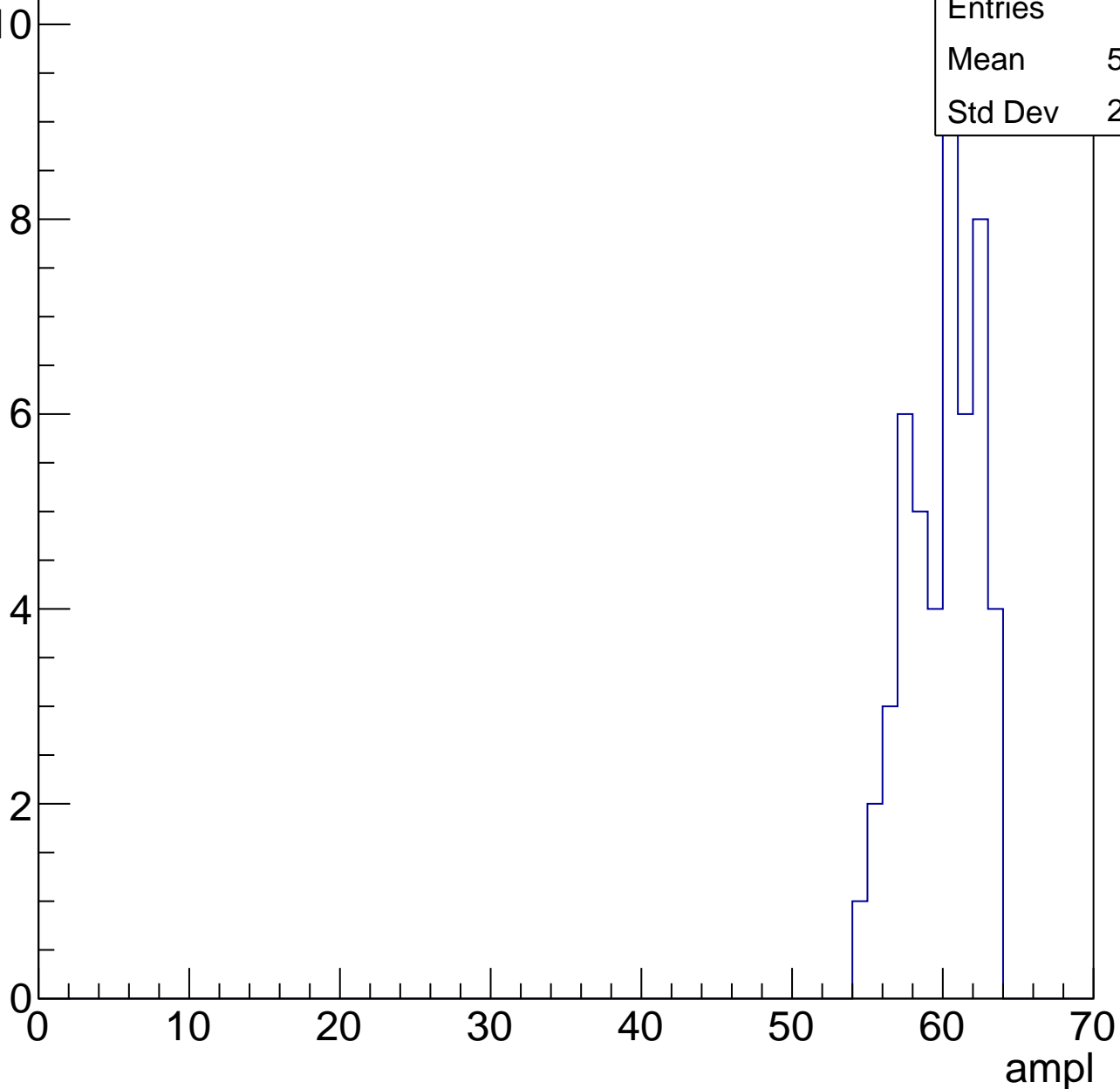


B1L102S, U17-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59.47
Std Dev	2.357

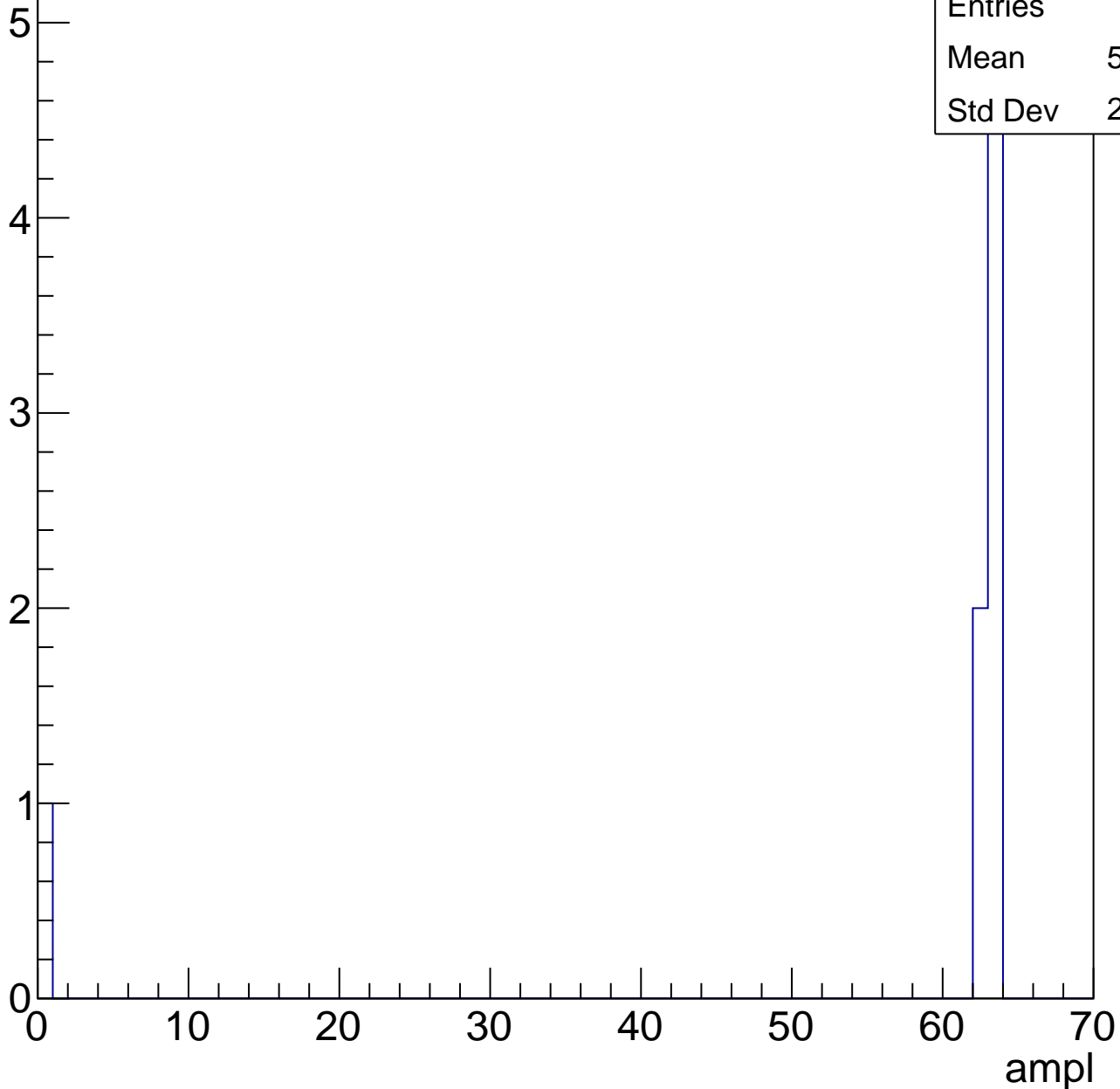


B1L102S, U17-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.88
Std Dev	20.75



B1L102S, U17-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch23, adc0

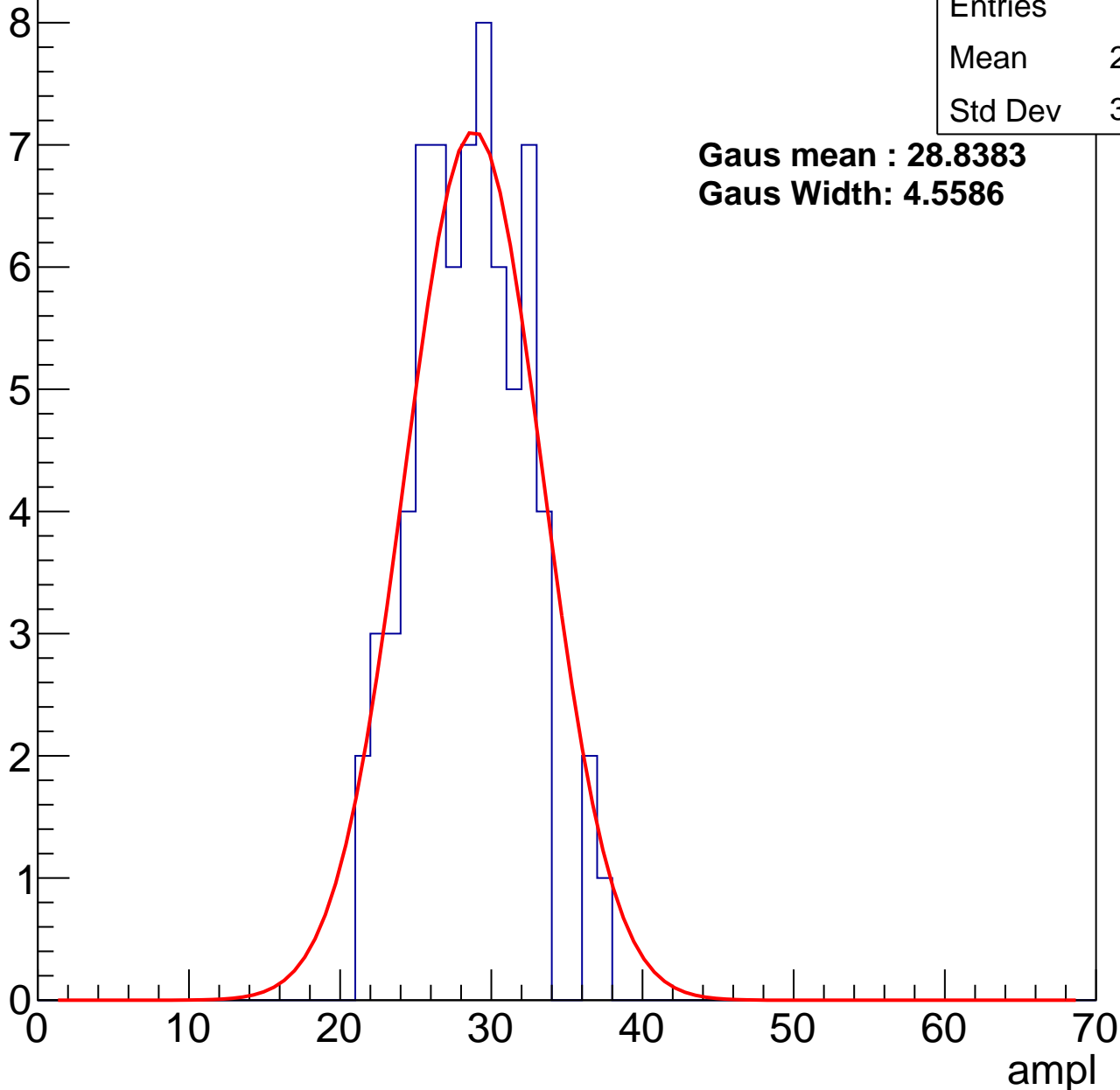
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.06
Std Dev	3.613

Gaus mean : 28.8383

Gaus Width: 4.5586



B1L102S, U17-ch23, adc1

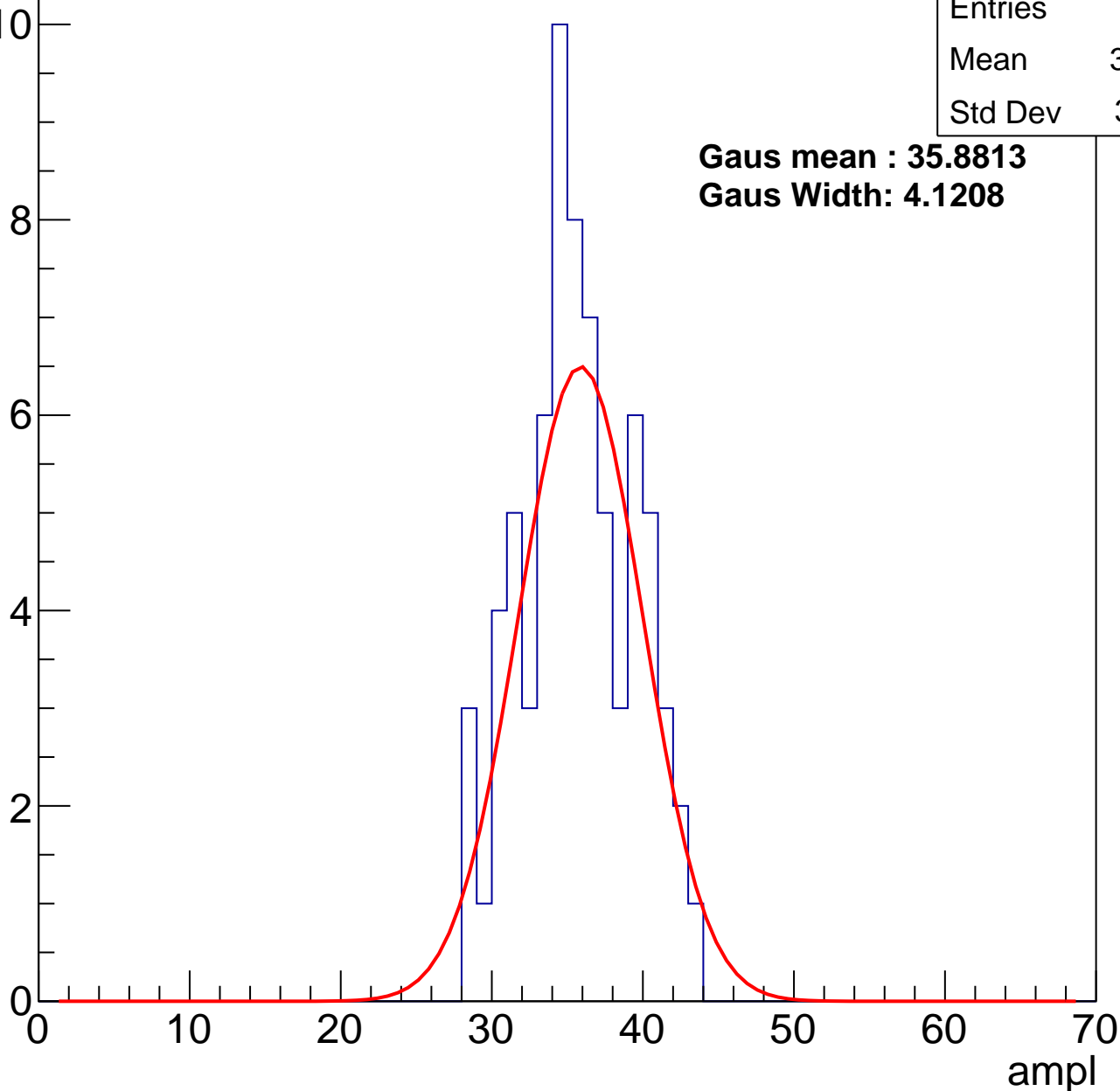
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.24
Std Dev	3.661

Gaus mean : 35.8813

Gaus Width: 4.1208



B1L102S, U17-ch23, adc2

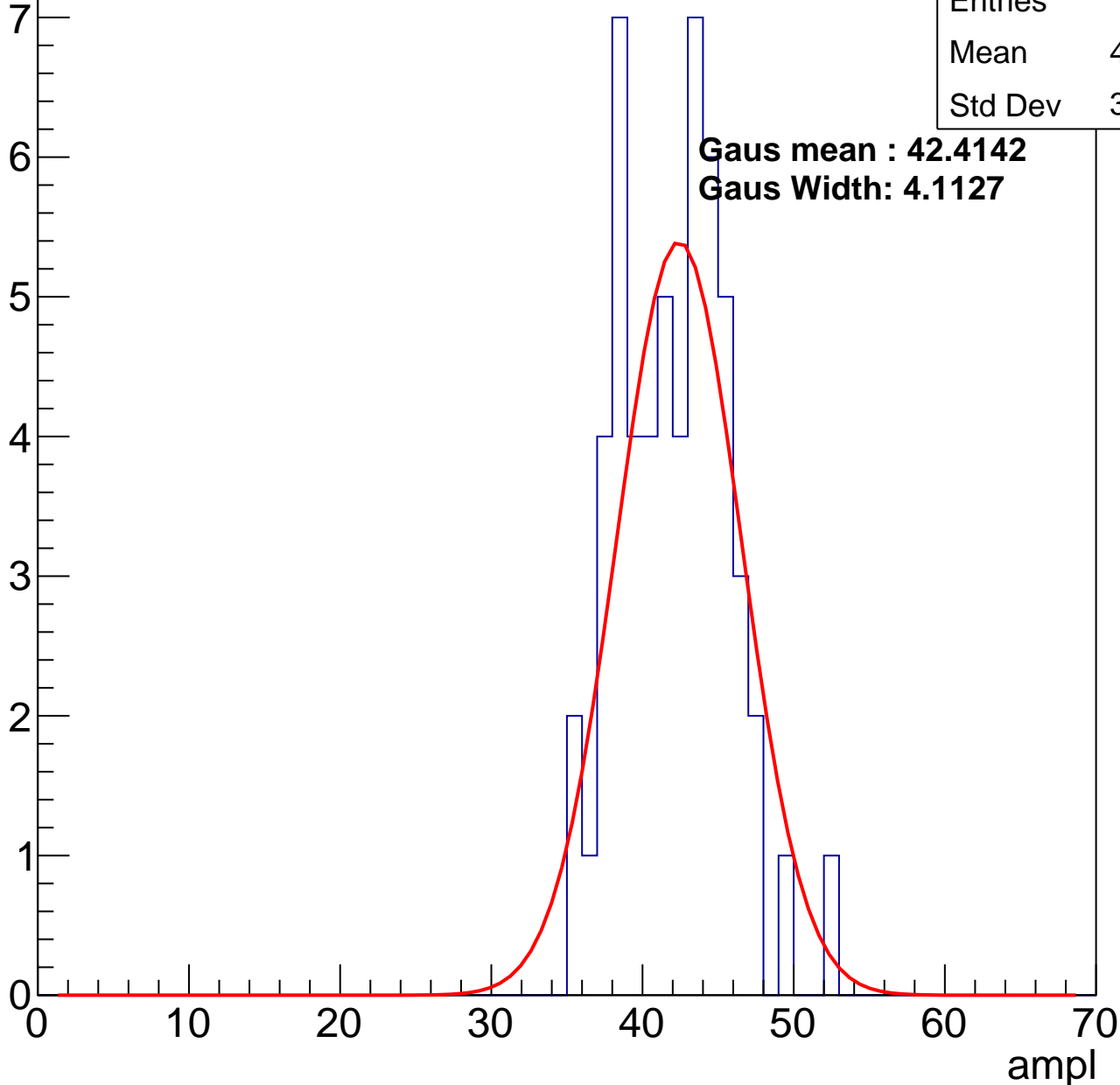
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.64
Std Dev	3.583

Gaus mean : 42.4142

Gaus Width: 4.1127

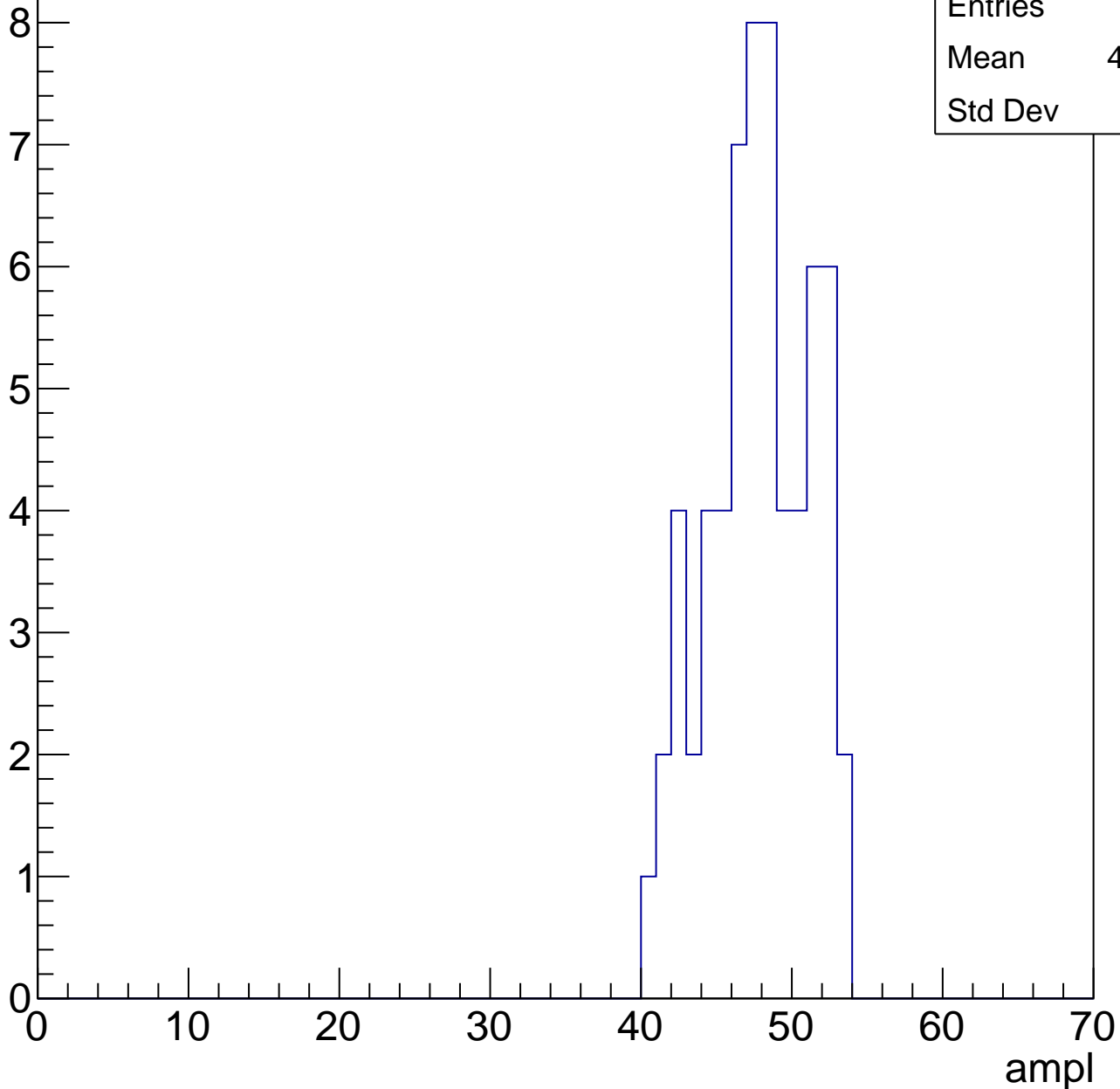


B1L102S, U17-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	47.32
Std Dev	3.32

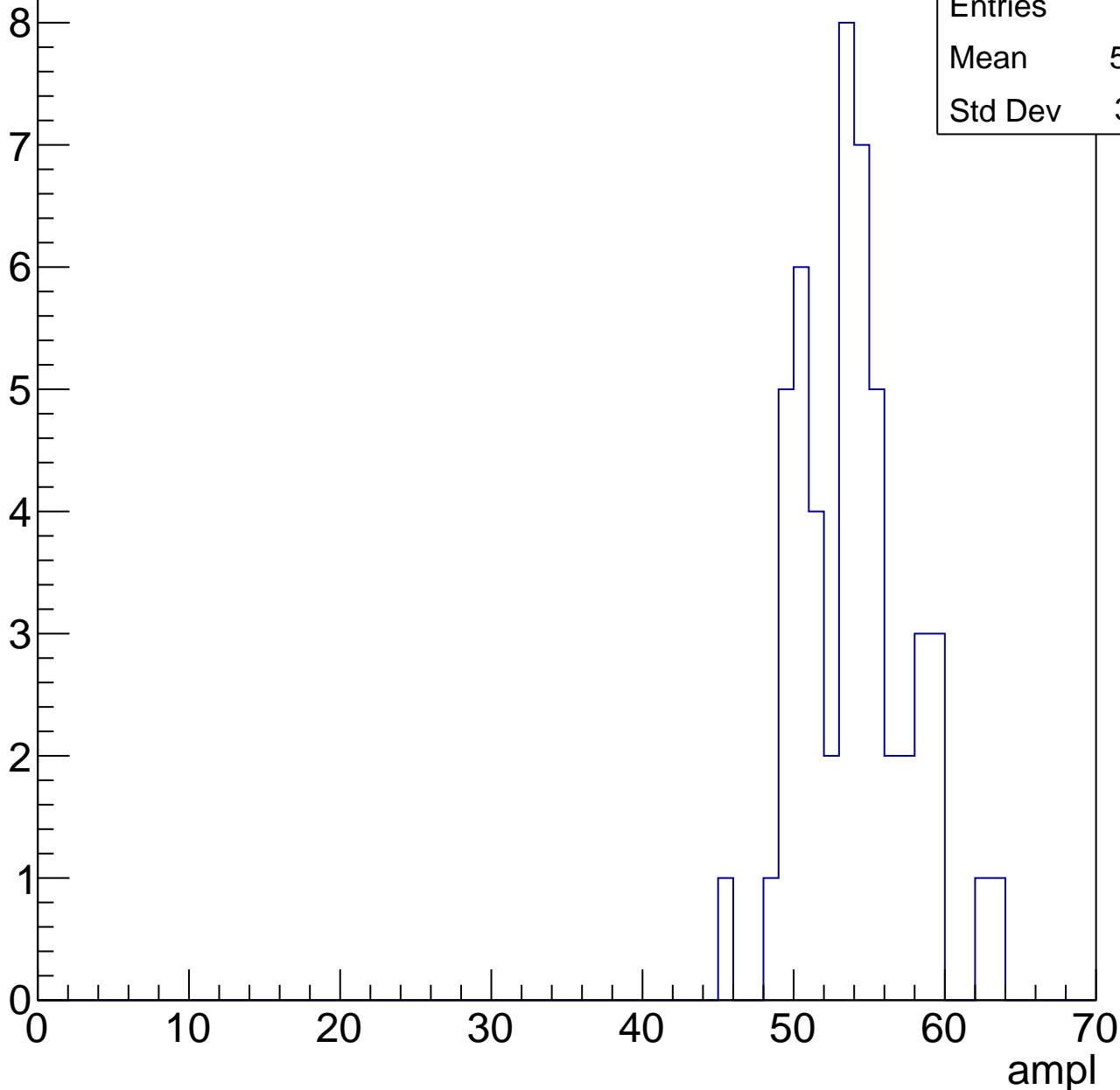


B1L102S, U17-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	53.43
Std Dev	3.631

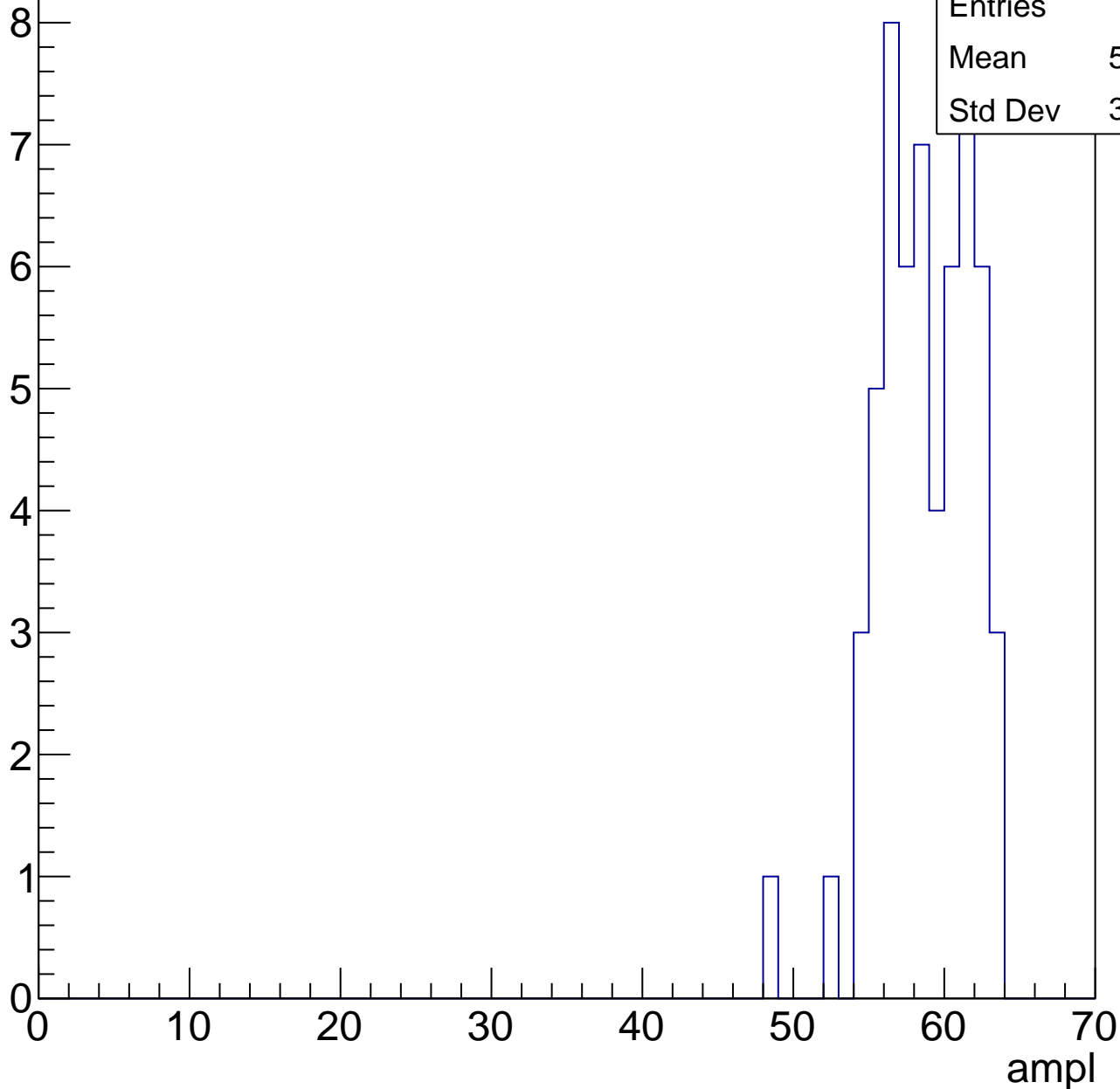


B1L102S, U17-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.24
Std Dev	3.036

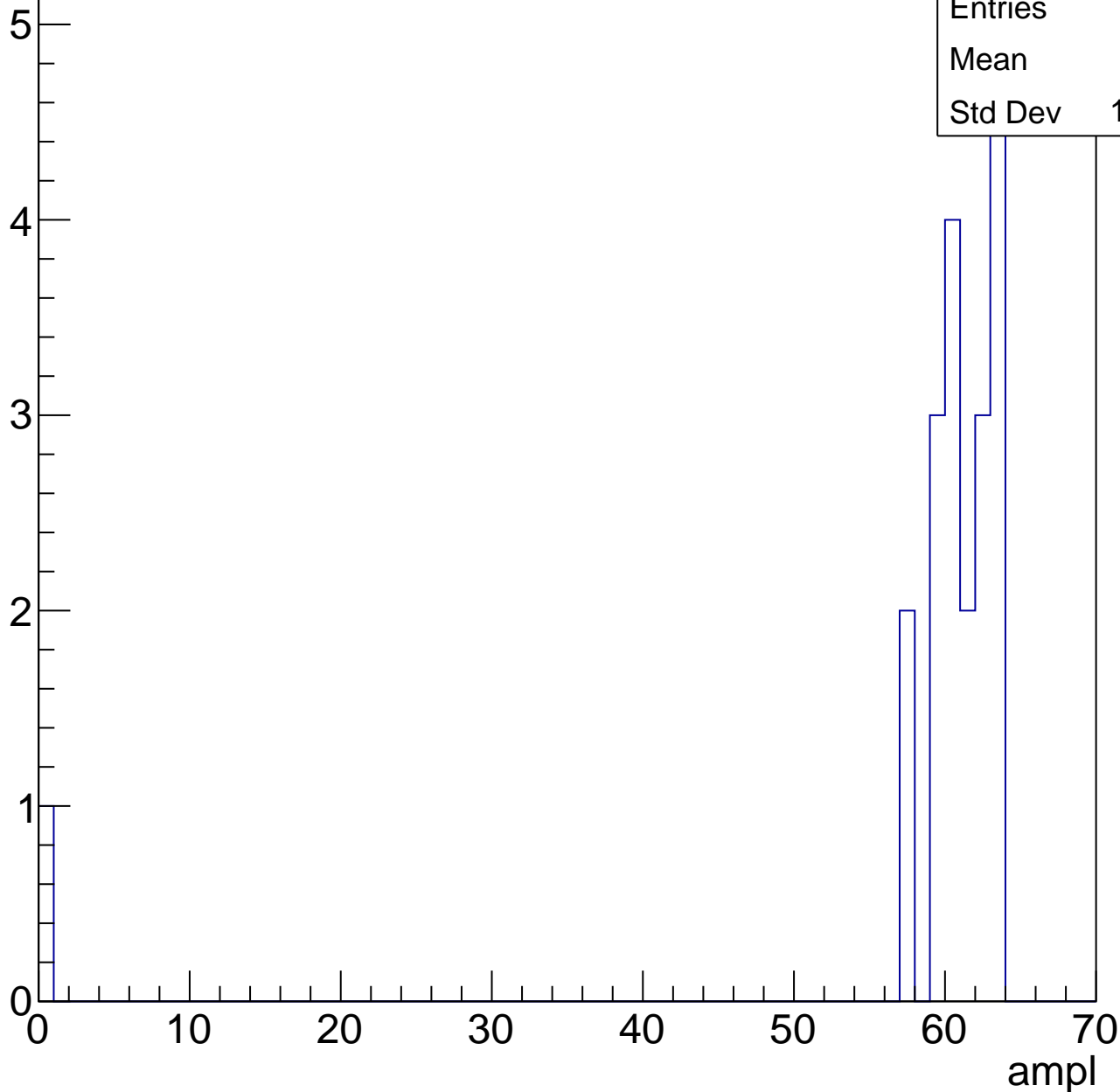


B1L102S, U17-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	57.7
Std Dev	13.37



B1L102S, U17-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch24, adc0

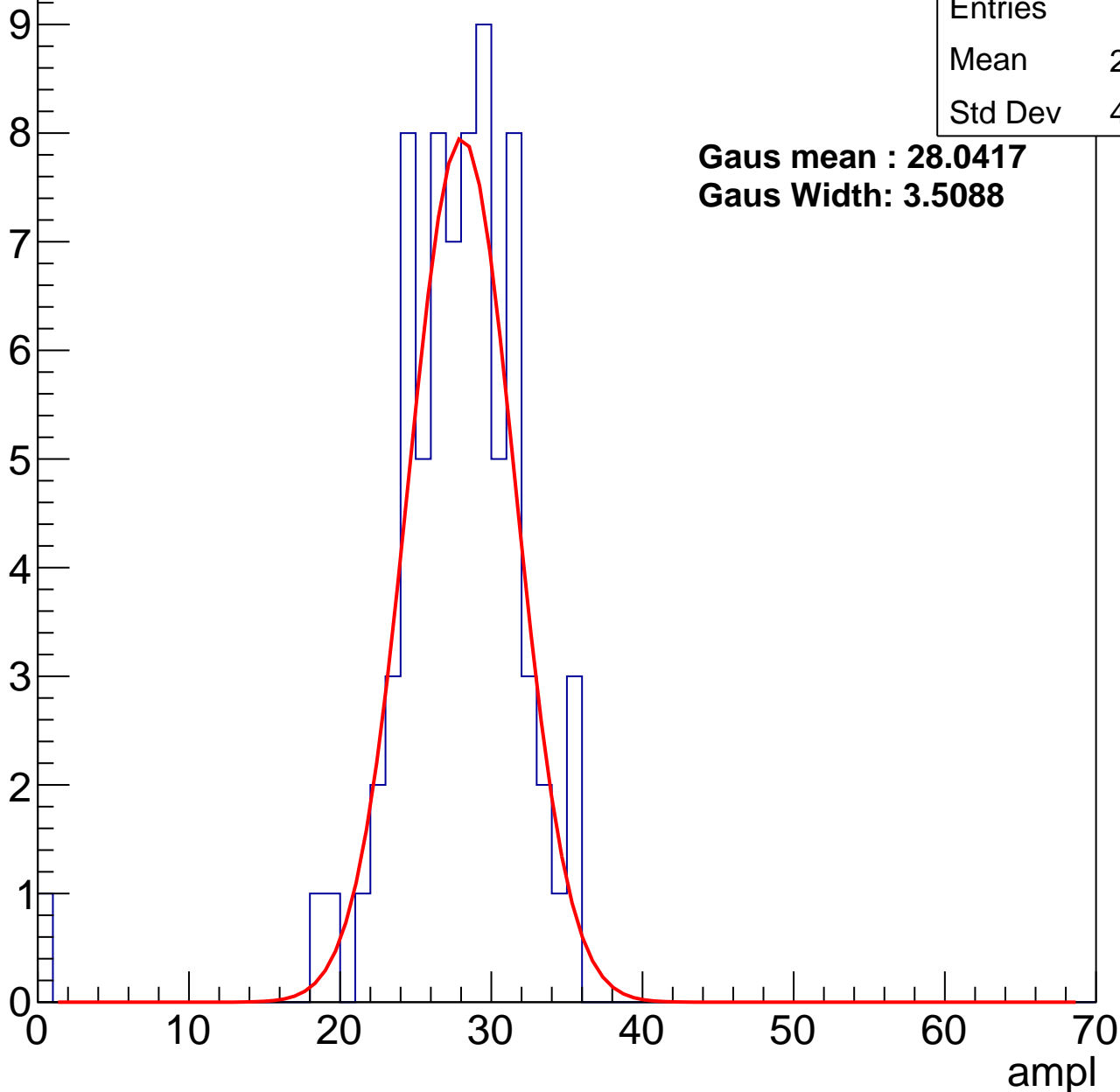
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.22
Std Dev	4.742

Gaus mean : 28.0417

Gaus Width: 3.5088



B1L102S, U17-ch24, adc1

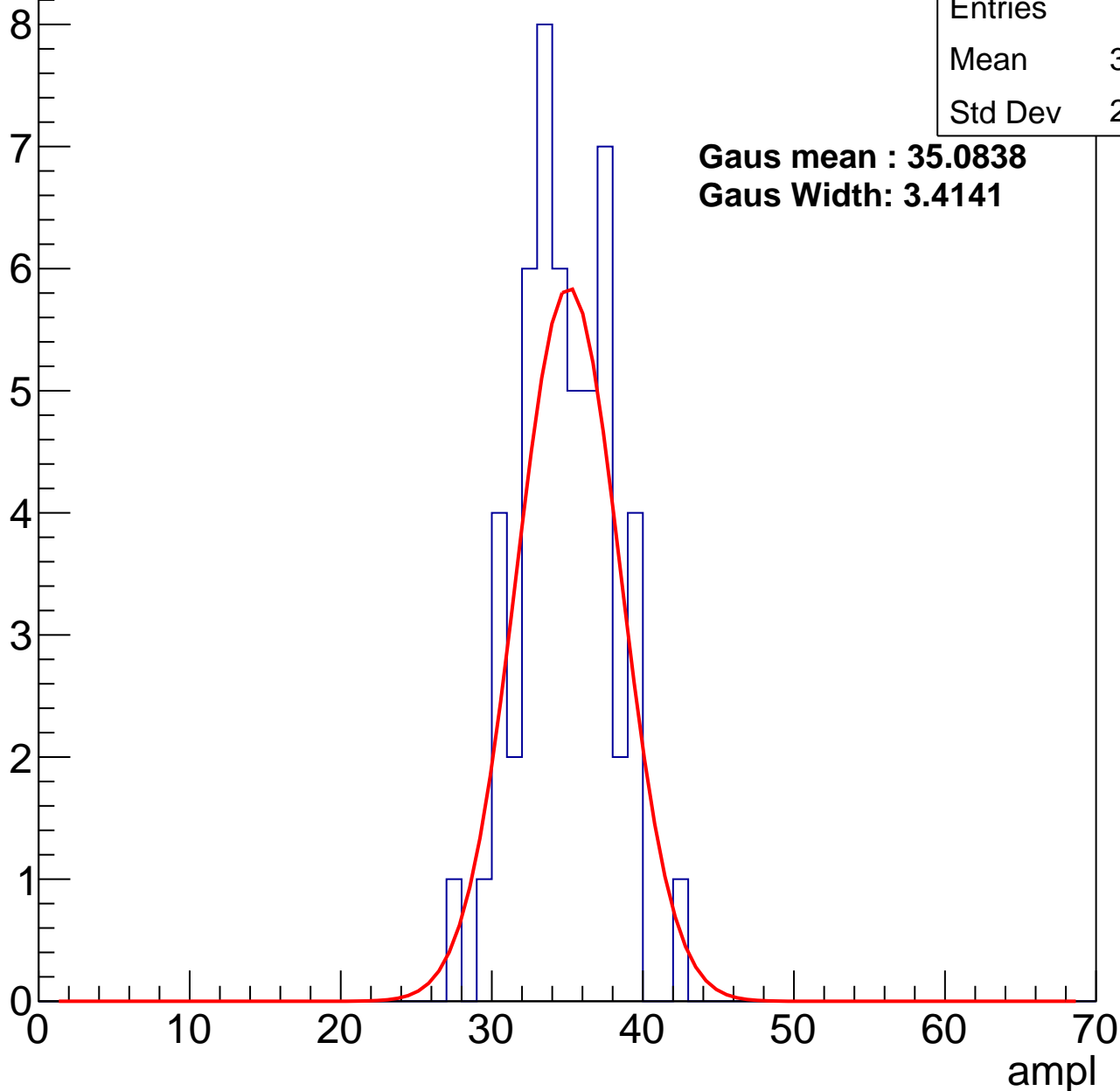
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.35
Std Dev	2.993

Gaus mean : 35.0838

Gaus Width: 3.4141



B1L102S, U17-ch24, adc2

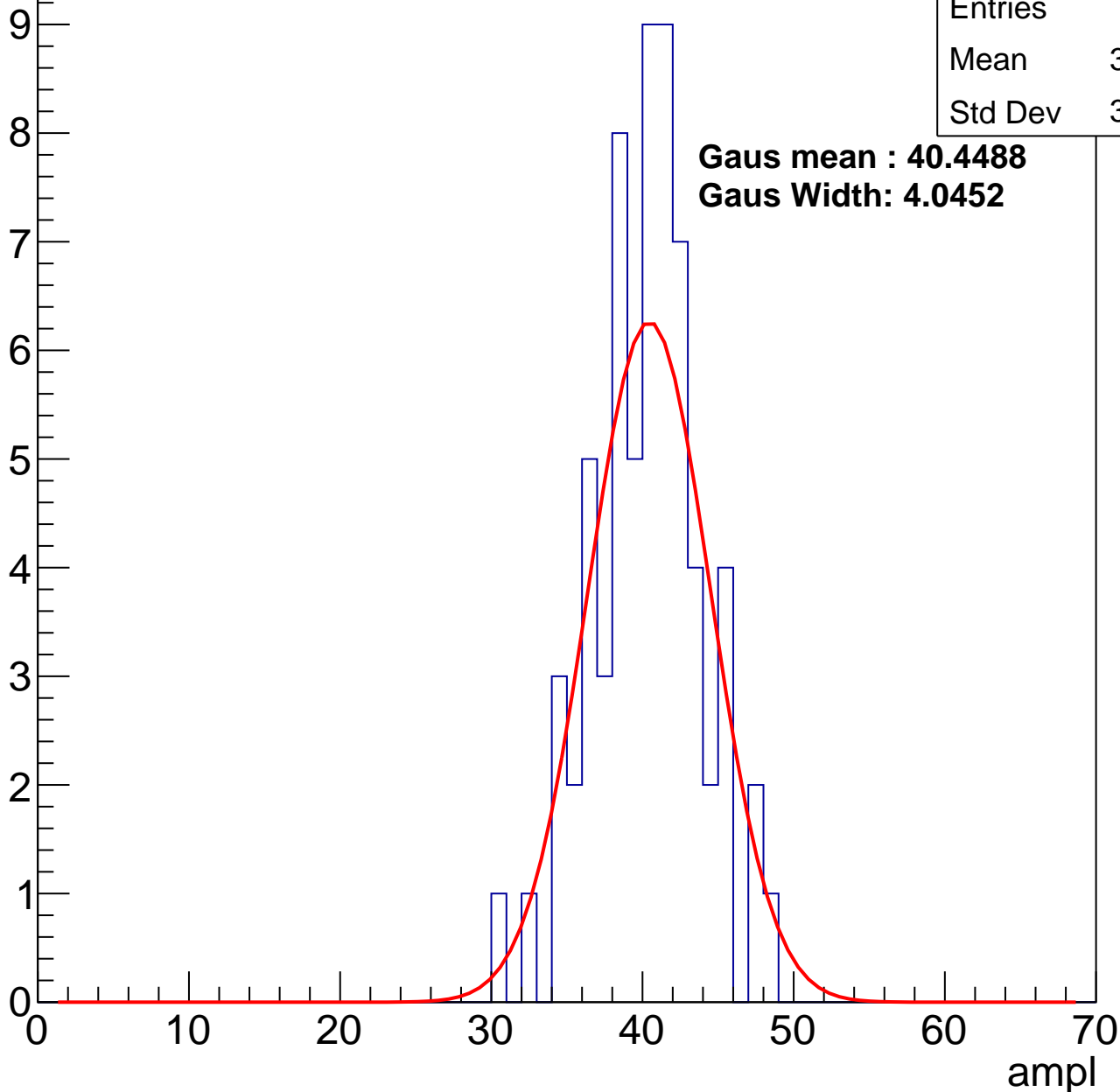
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	39.83
Std Dev	3.549

Gaus mean : 40.4488

Gaus Width: 4.0452

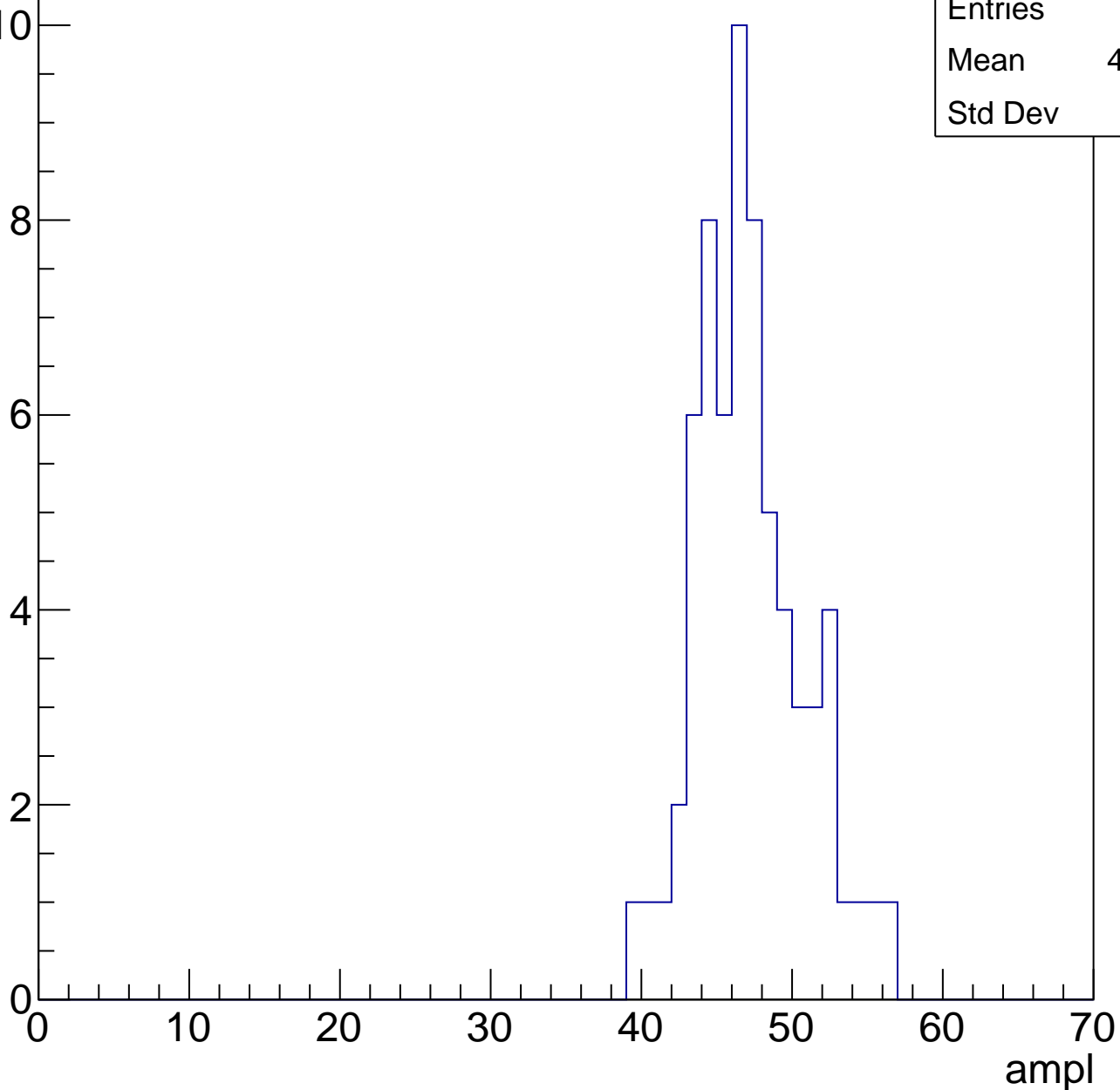


B1L102S, U17-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.74
Std Dev	3.53

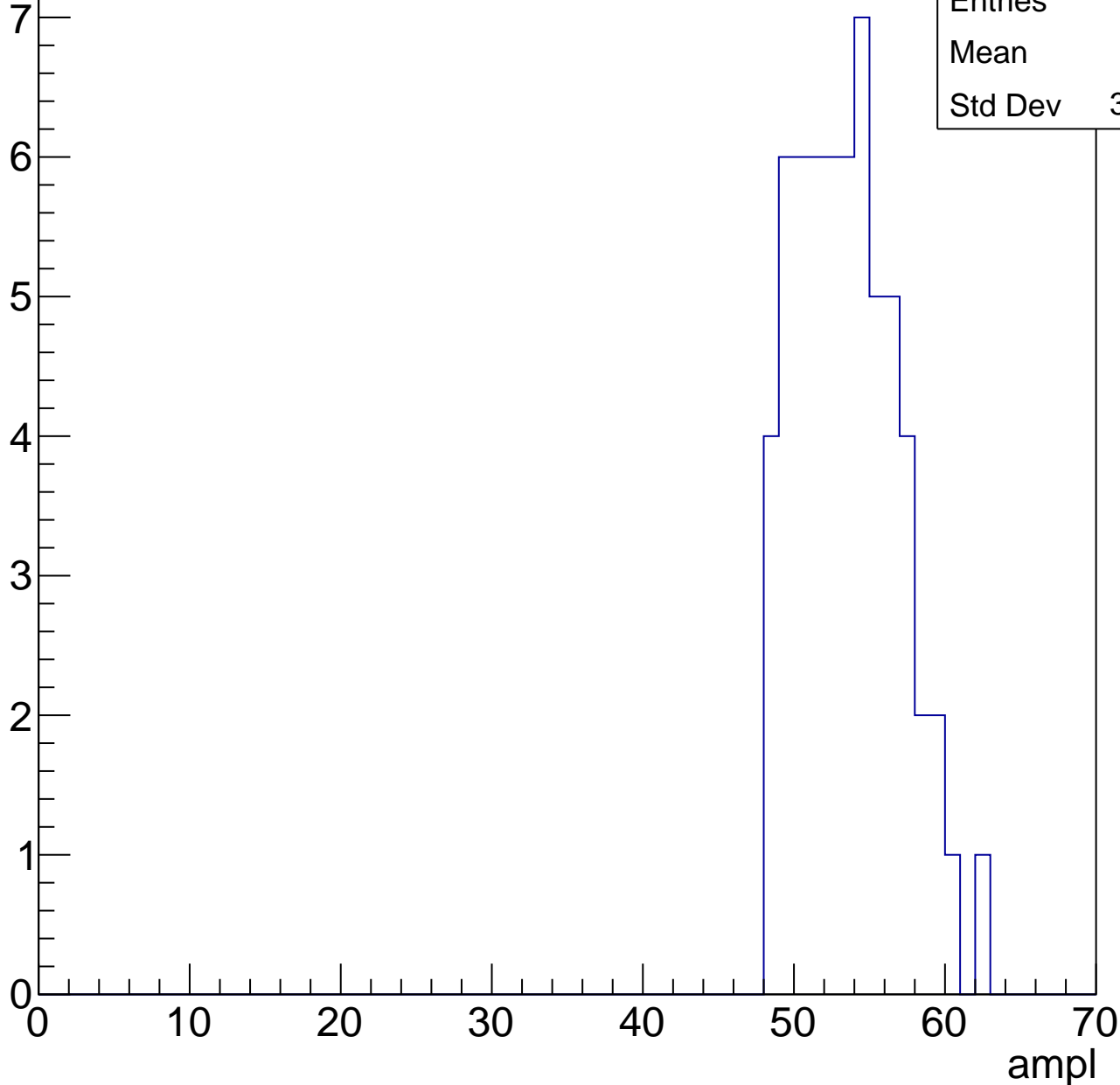


B1L102S, U17-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.1
Std Dev	3.308

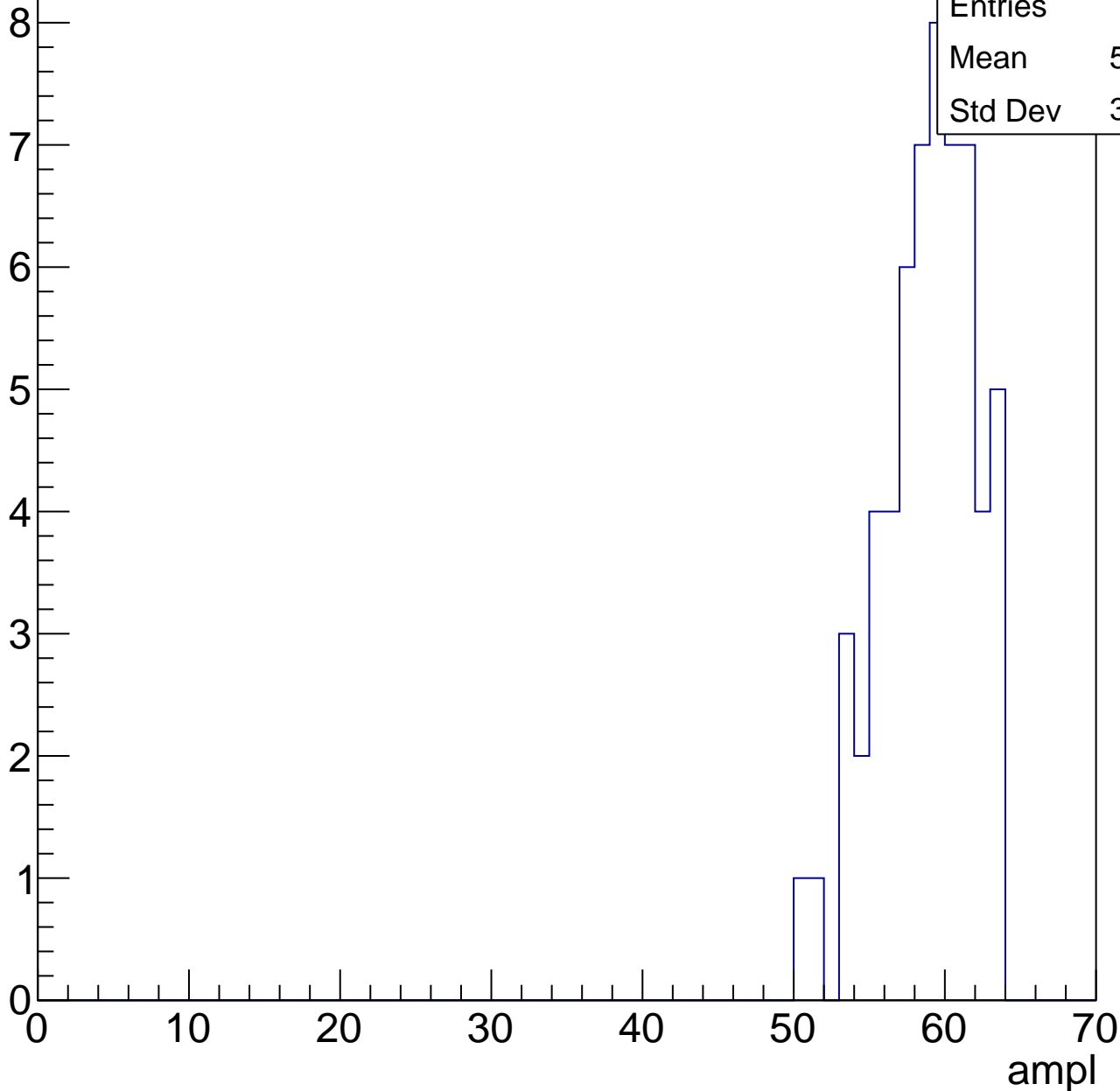


B1L102S, U17-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	58.34
Std Dev	3.079

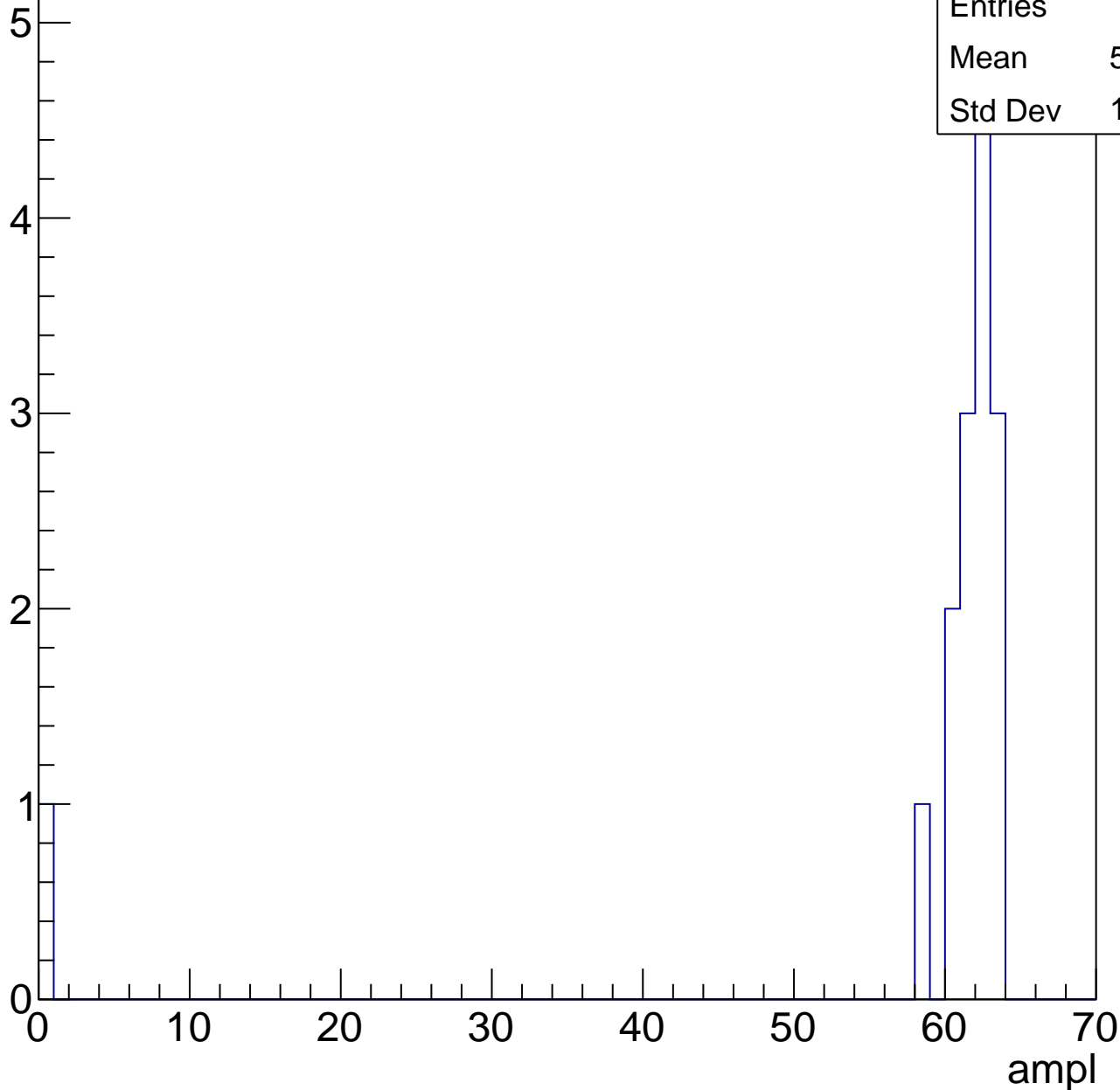


B1L102S, U17-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.33
Std Dev	15.38



B1L102S, U17-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch25, adc0

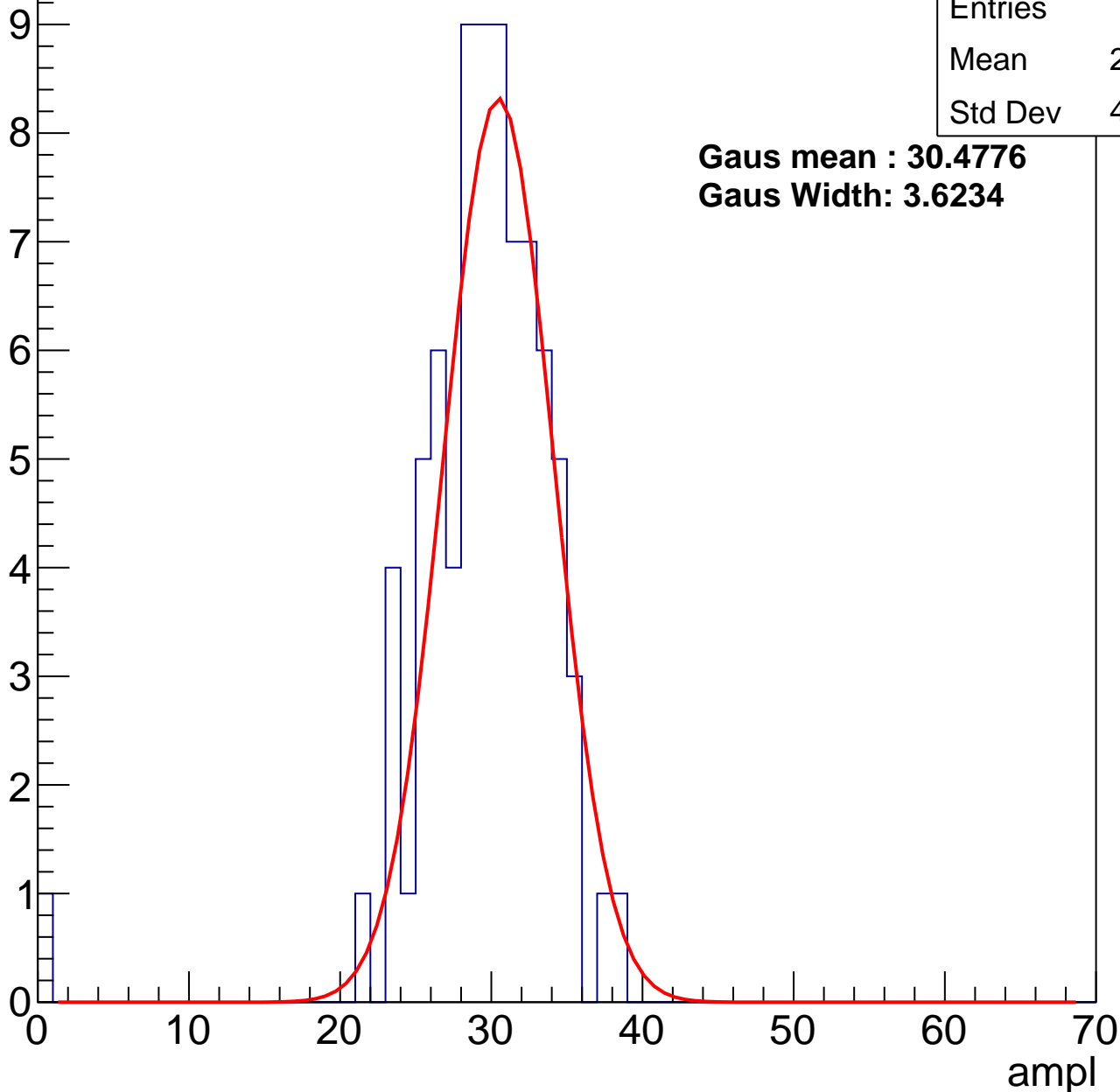
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29.09
Std Dev	4.782

Gaus mean : 30.4776

Gaus Width: 3.6234



B1L102S, U17-ch25, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	36.61
Std Dev	3.181

Gaus mean : 37.0681

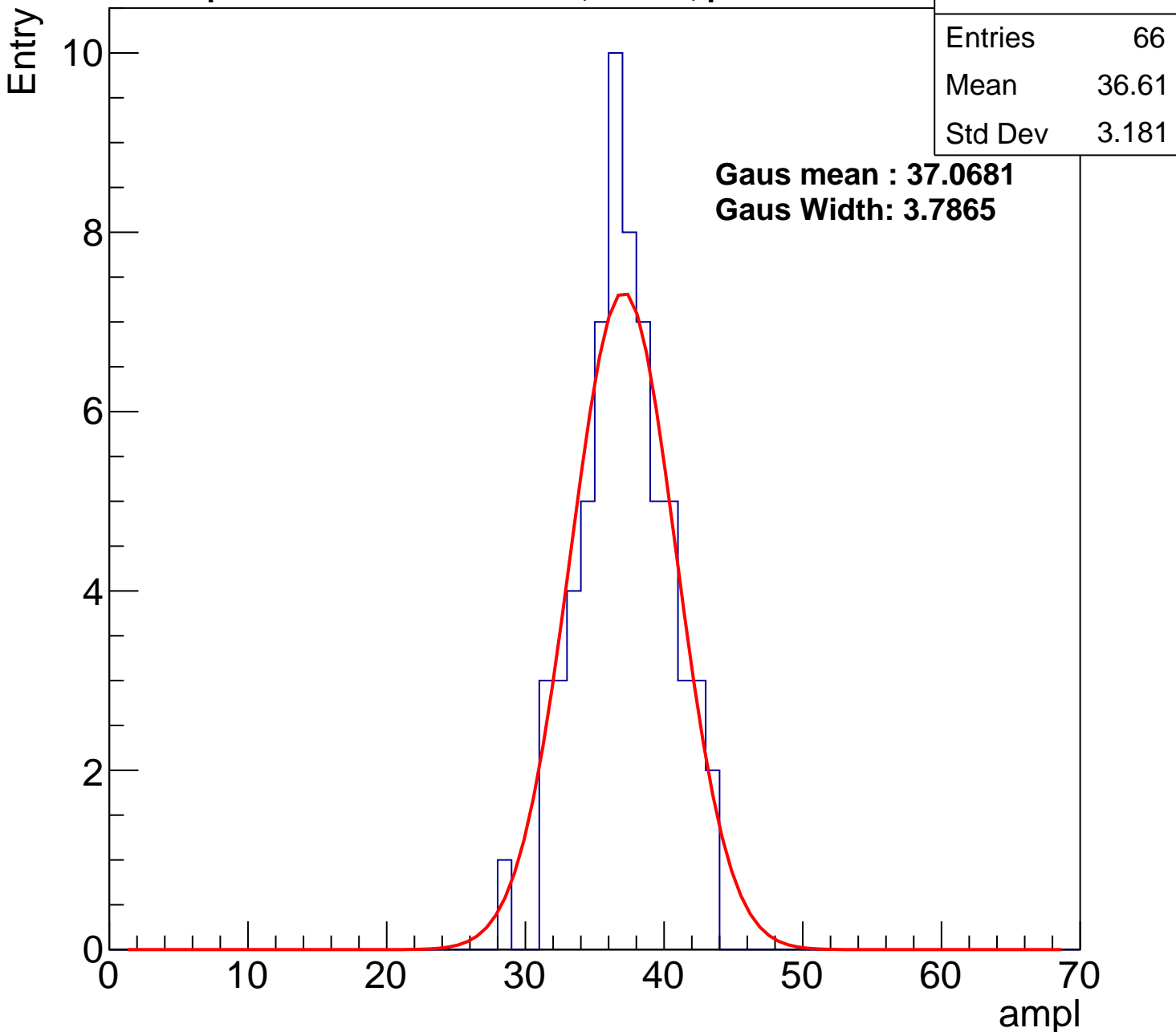
Gaus Width: 3.7865

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

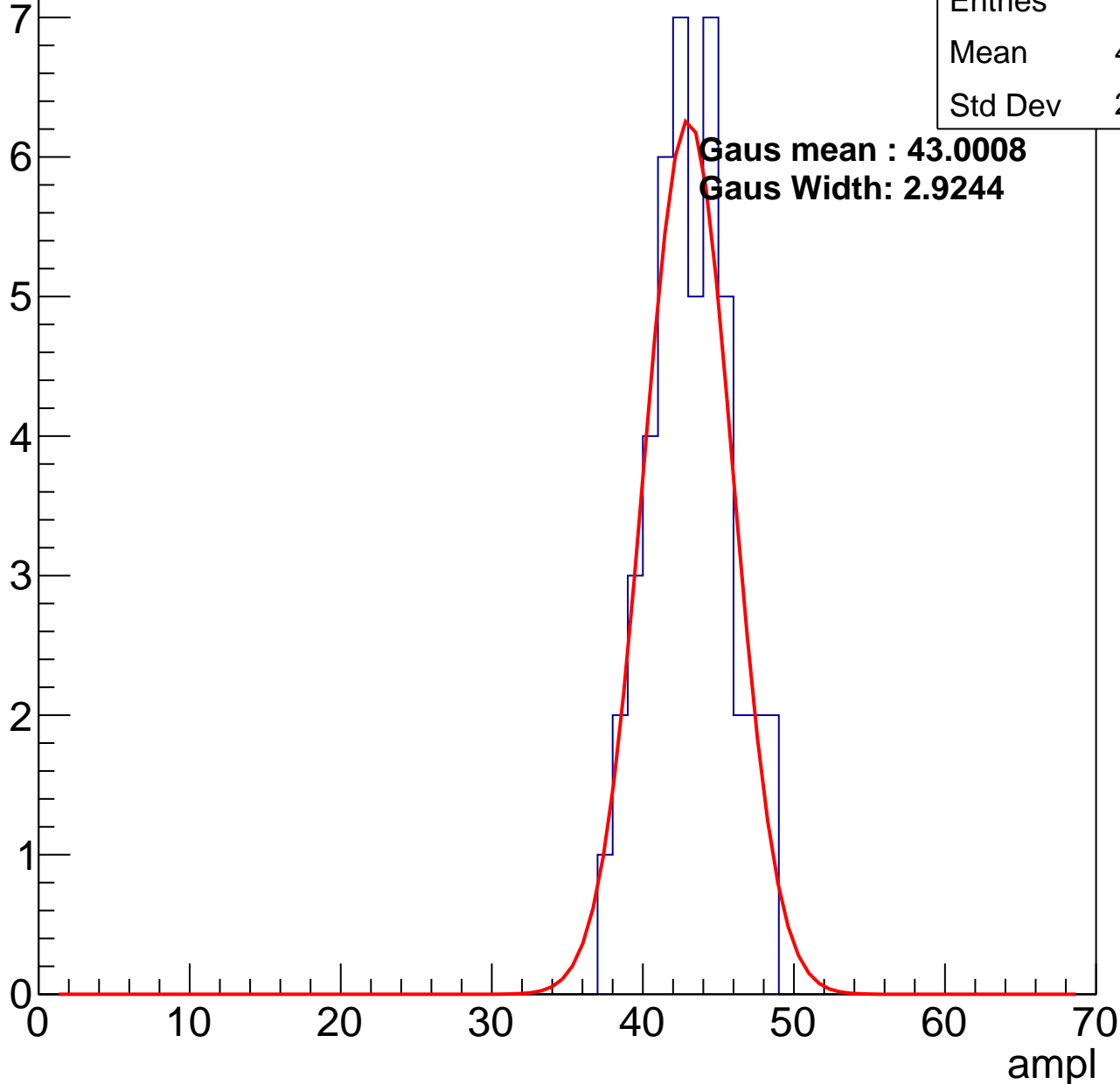


B1L102S, U17-ch25, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	42.61
Std Dev	2.641

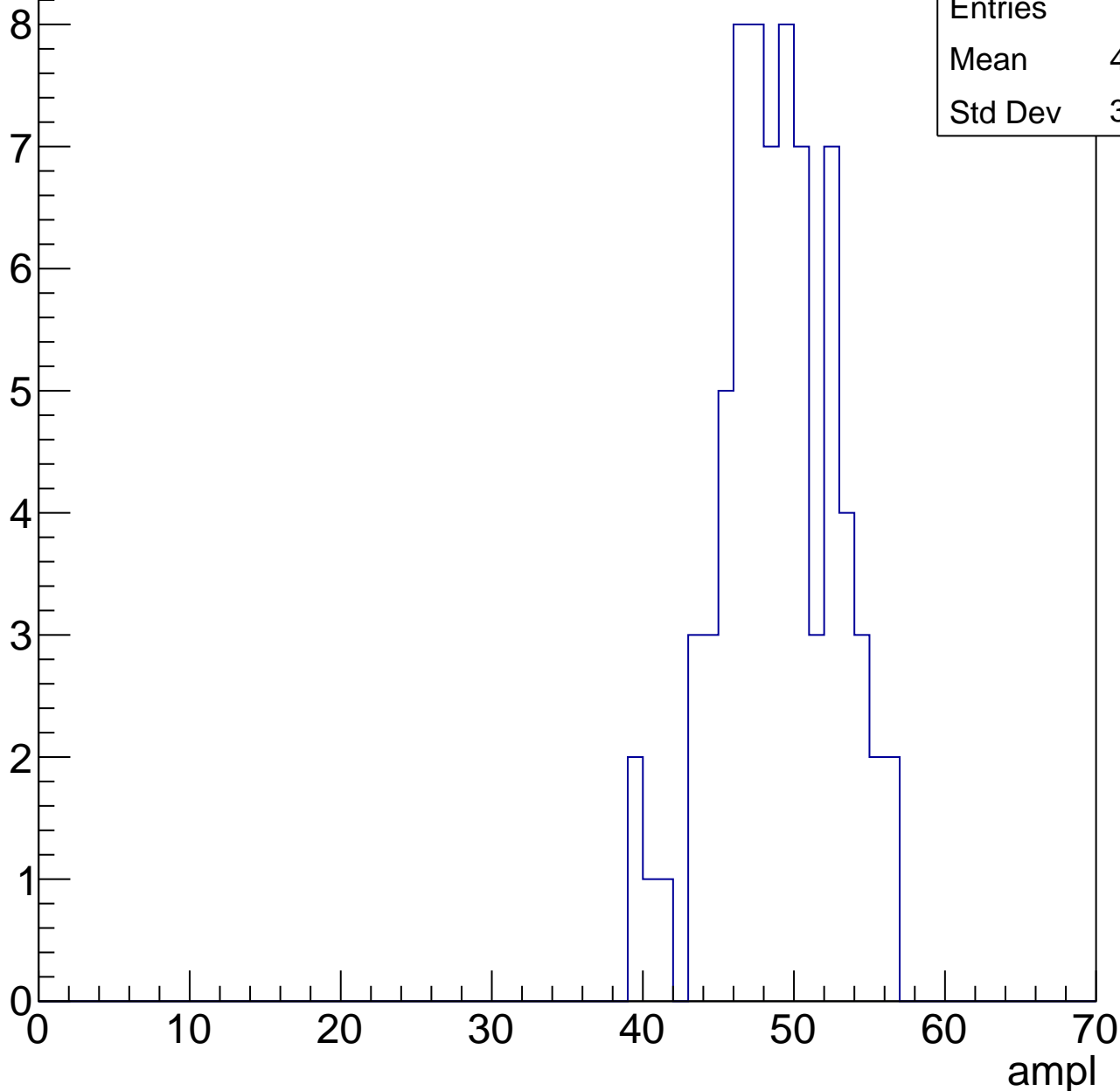


B1L102S, U17-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.38
Std Dev	3.823

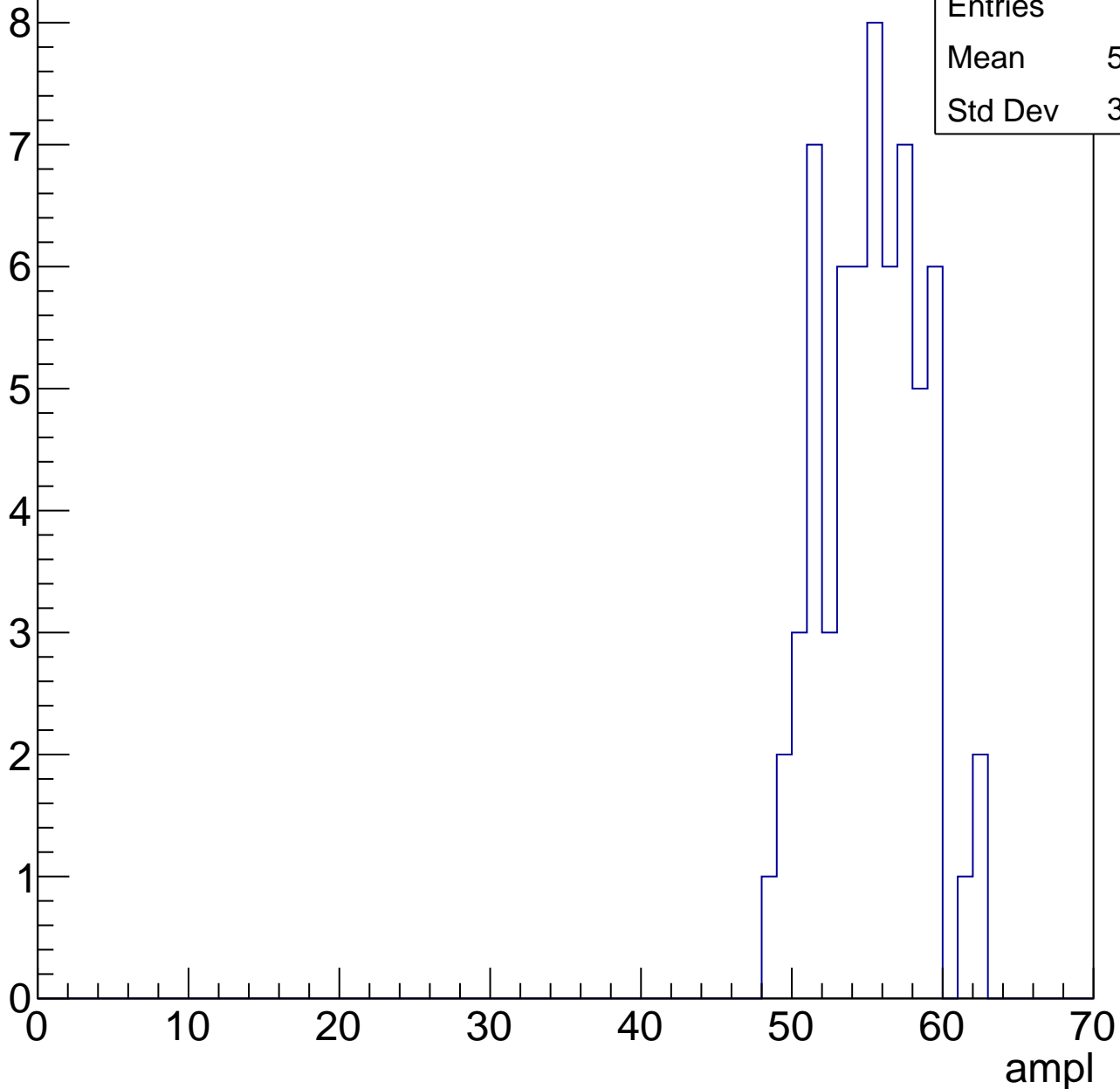


B1L102S, U17-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.84
Std Dev	3.267

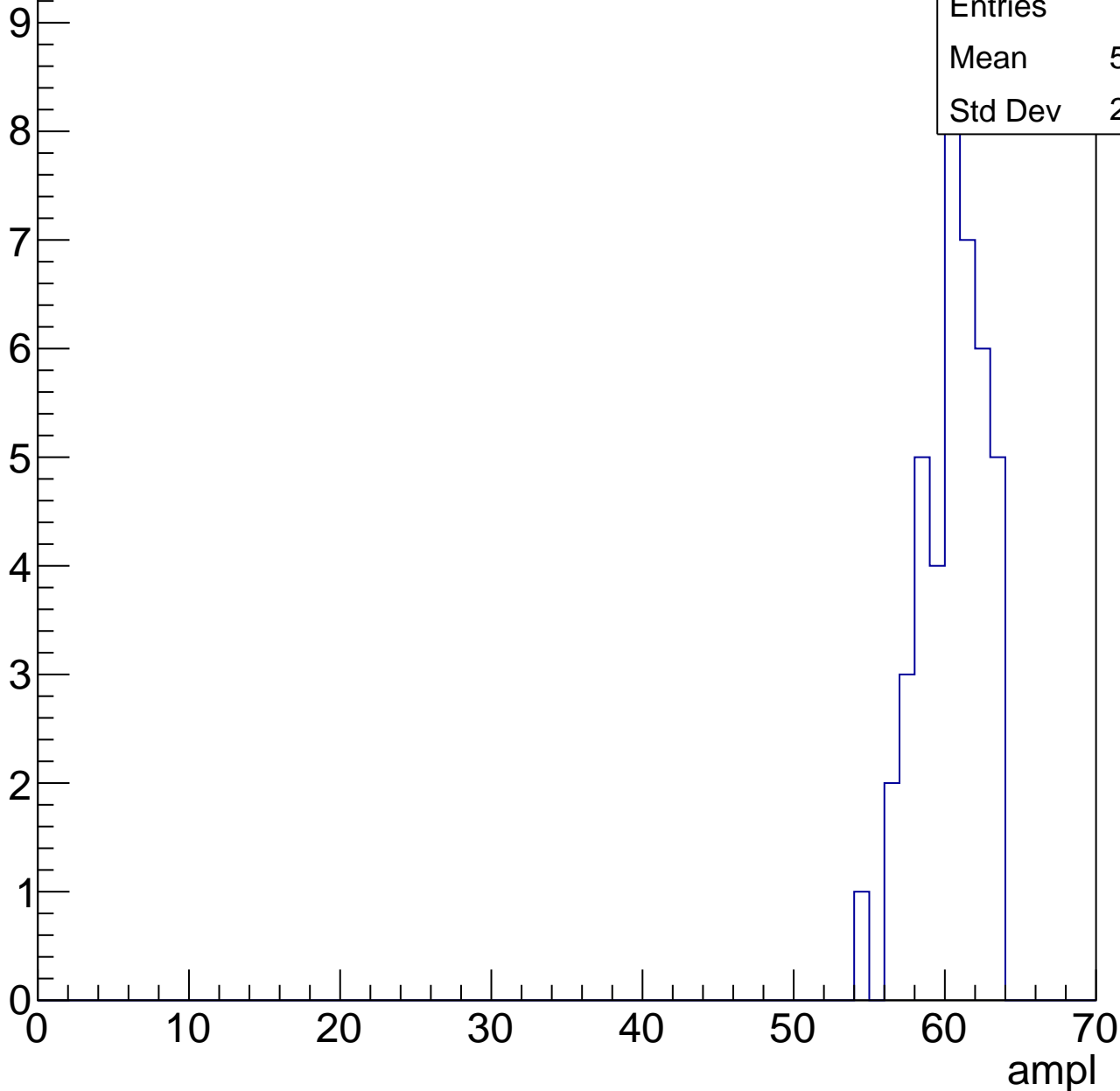


B1L102S, U17-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	59.93
Std Dev	2.154

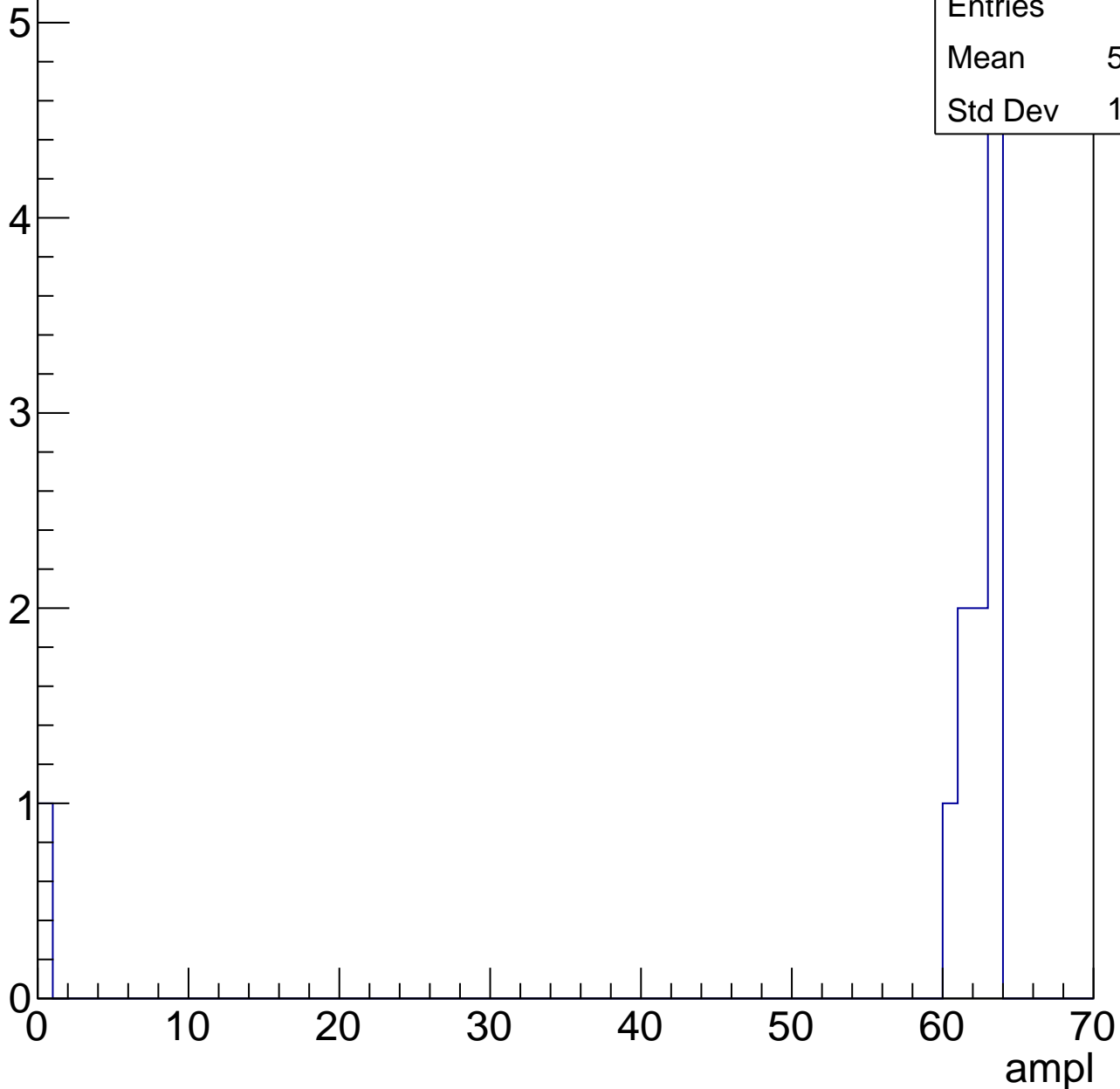


B1L102S, U17-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	56.45
Std Dev	17.88



B1L102S, U17-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch26, adc0

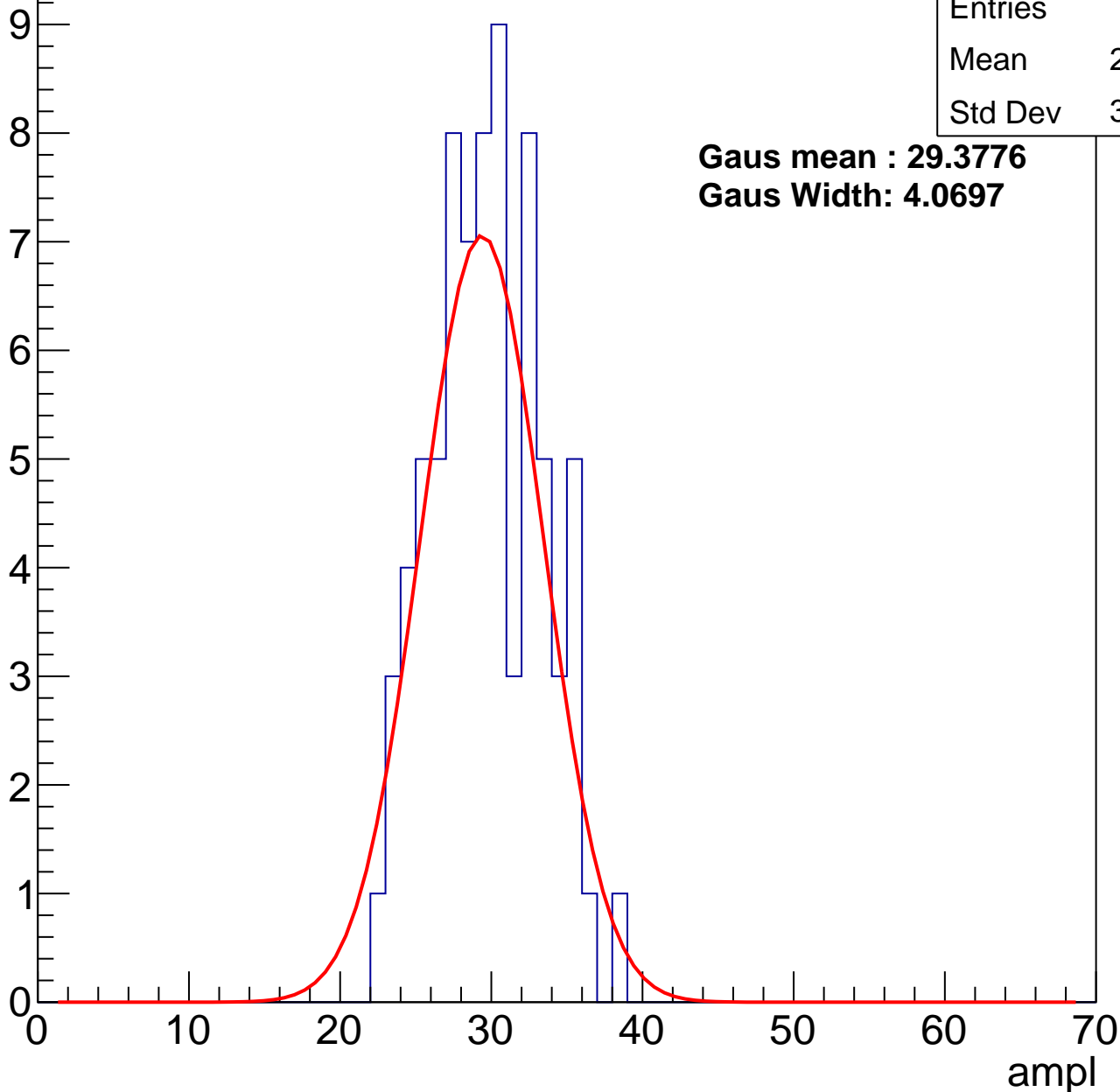
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.22
Std Dev	3.582

Gaus mean : 29.3776

Gaus Width: 4.0697



B1L102S, U17-ch26, adc1

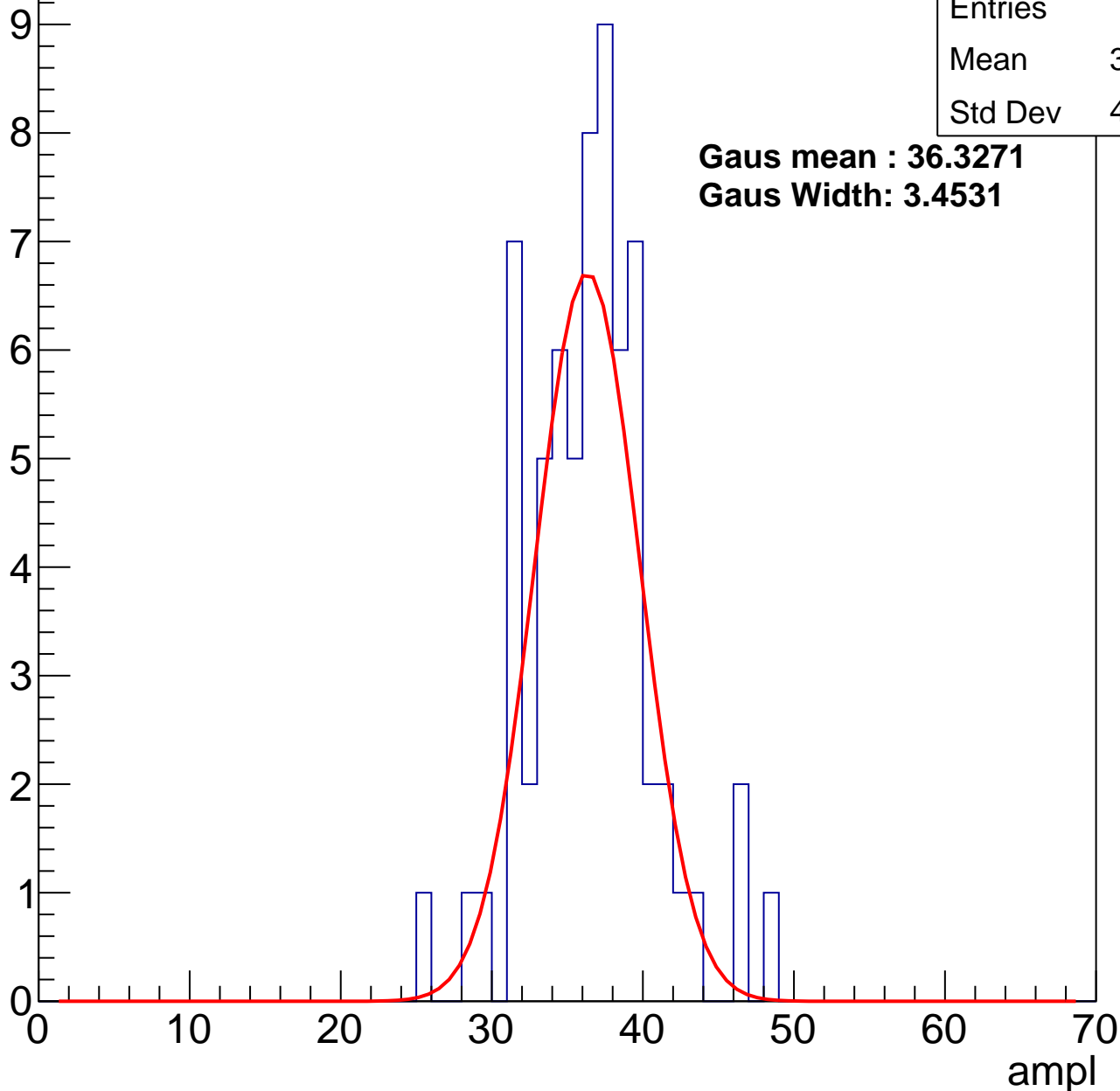
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.06
Std Dev	4.092

Gaus mean : 36.3271

Gaus Width: 3.4531



B1L102S, U17-ch26, adc2

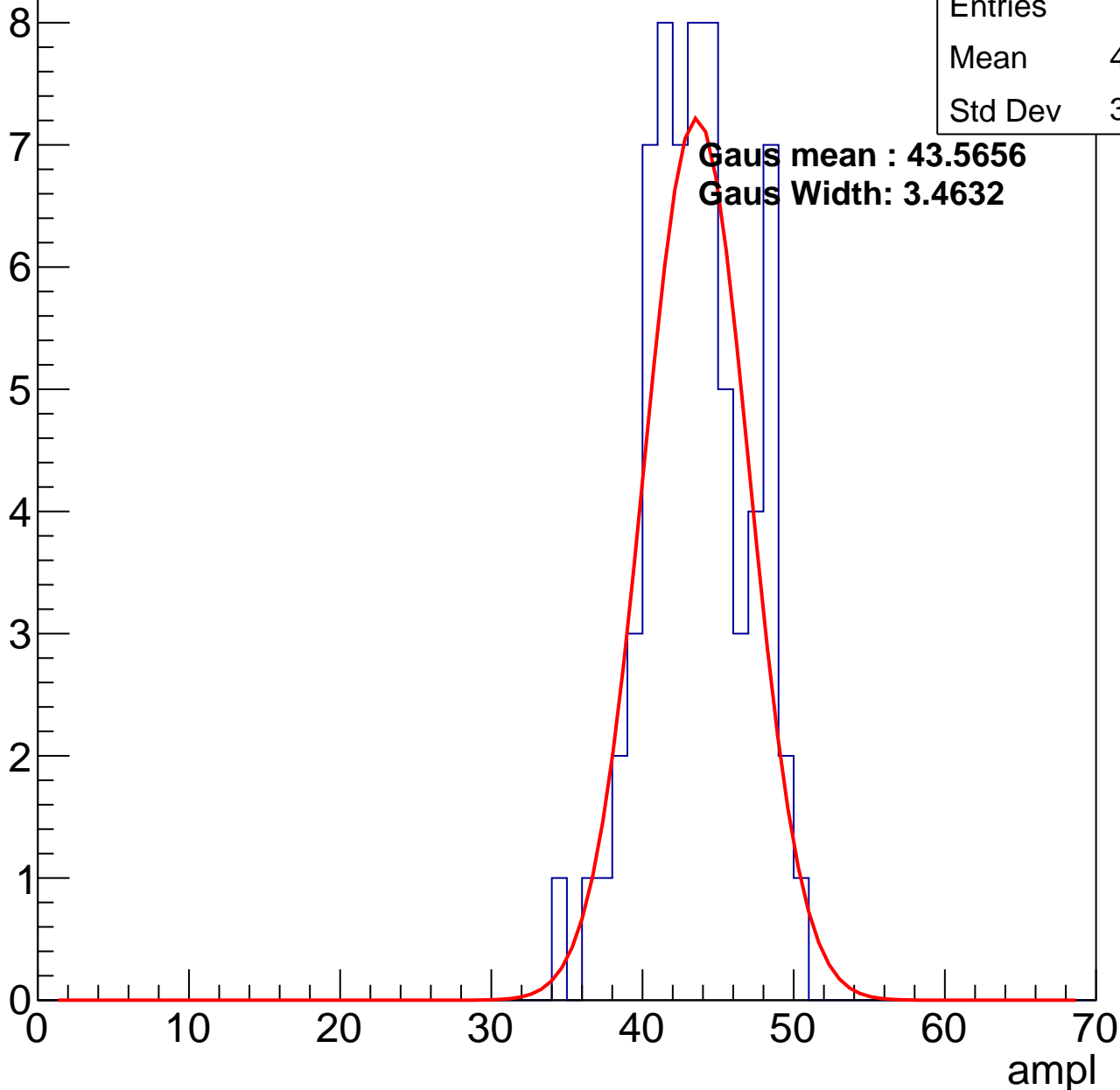
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.13
Std Dev	3.386

Gaus mean : 43.5656

Gaus Width: 3.4632

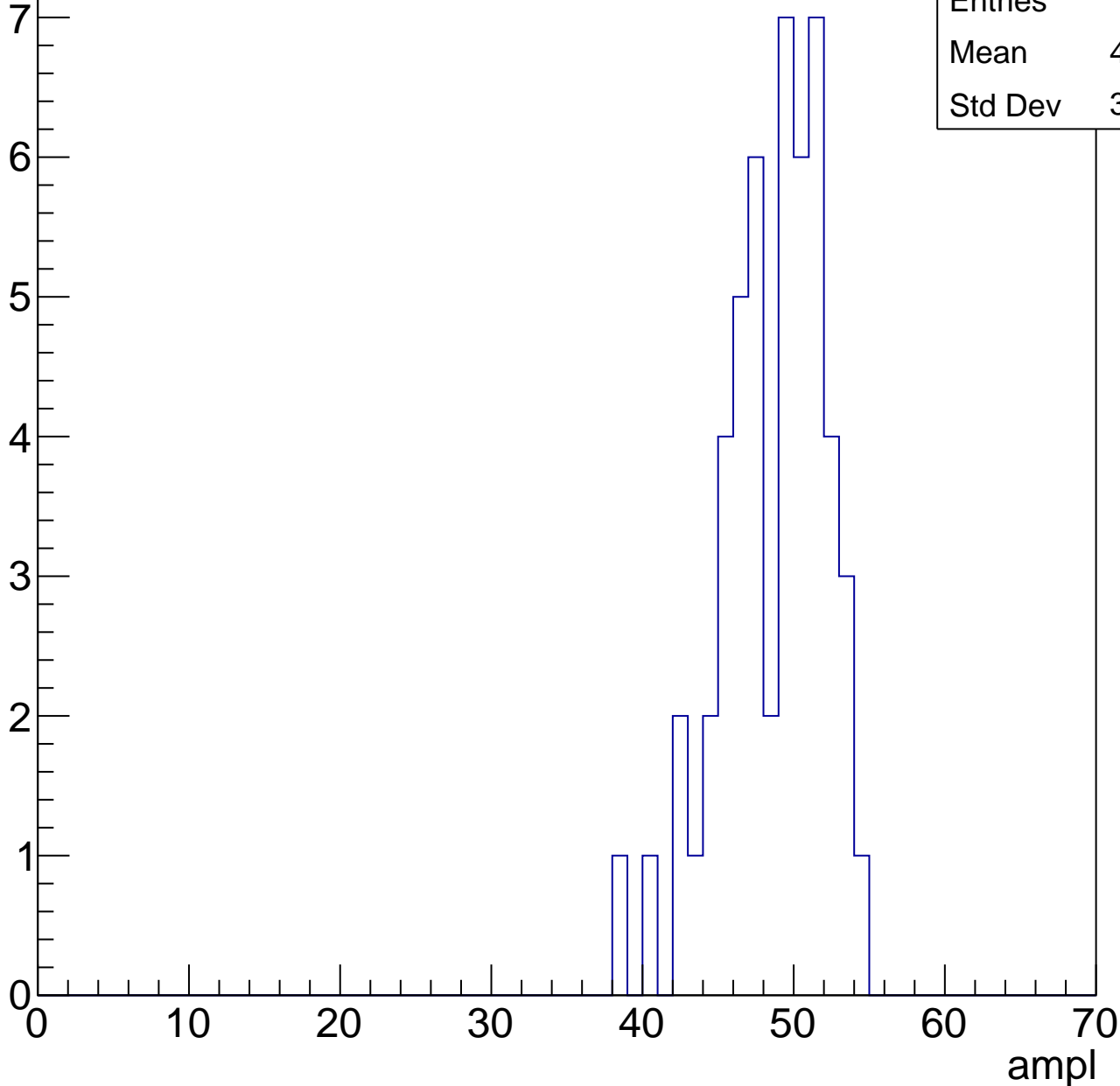


B1L102S, U17-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

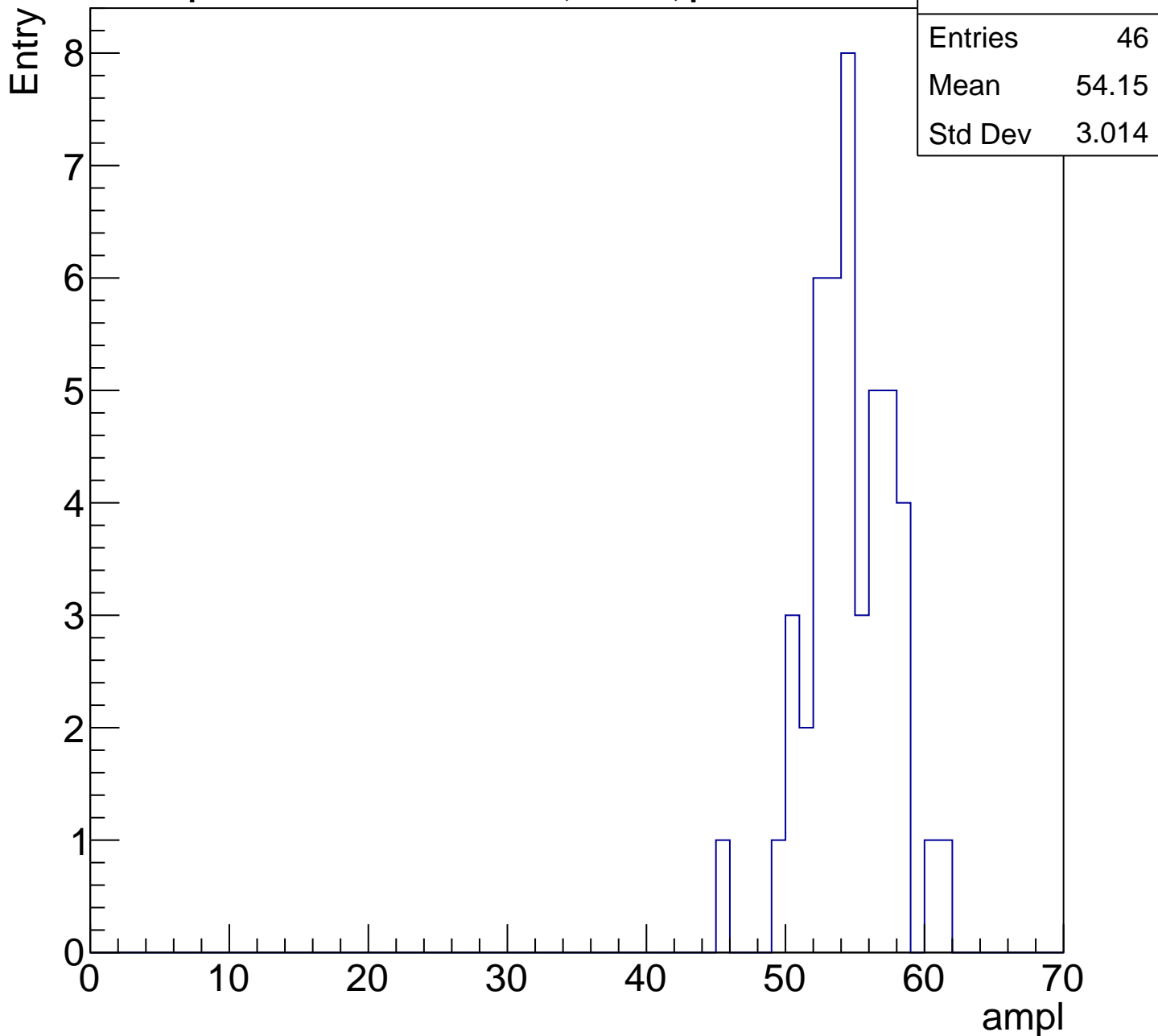
Entry

Entries	52
Mean	48.12
Std Dev	3.462



B1L102S, U17-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

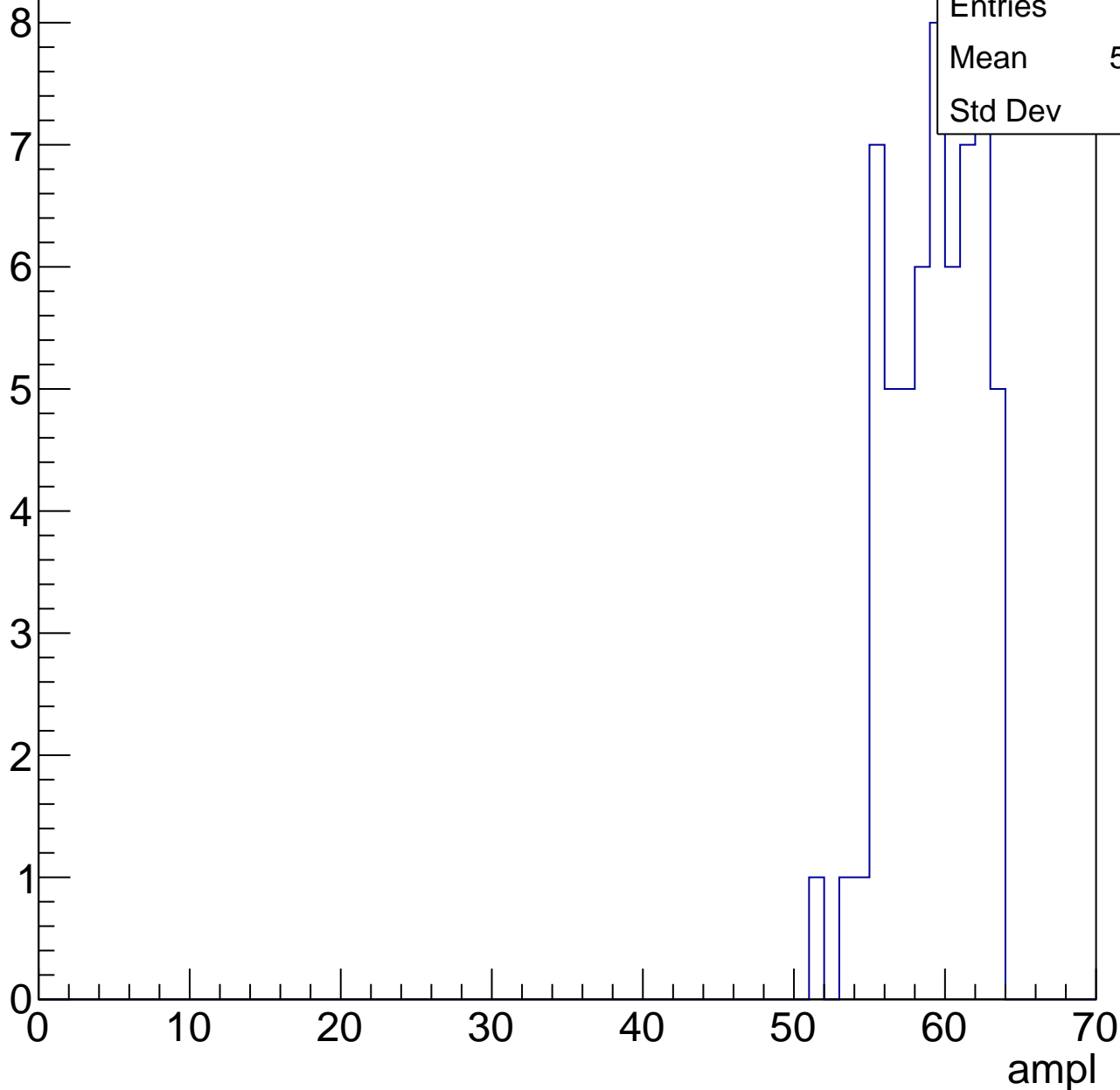


B1L102S, U17-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.77
Std Dev	2.86

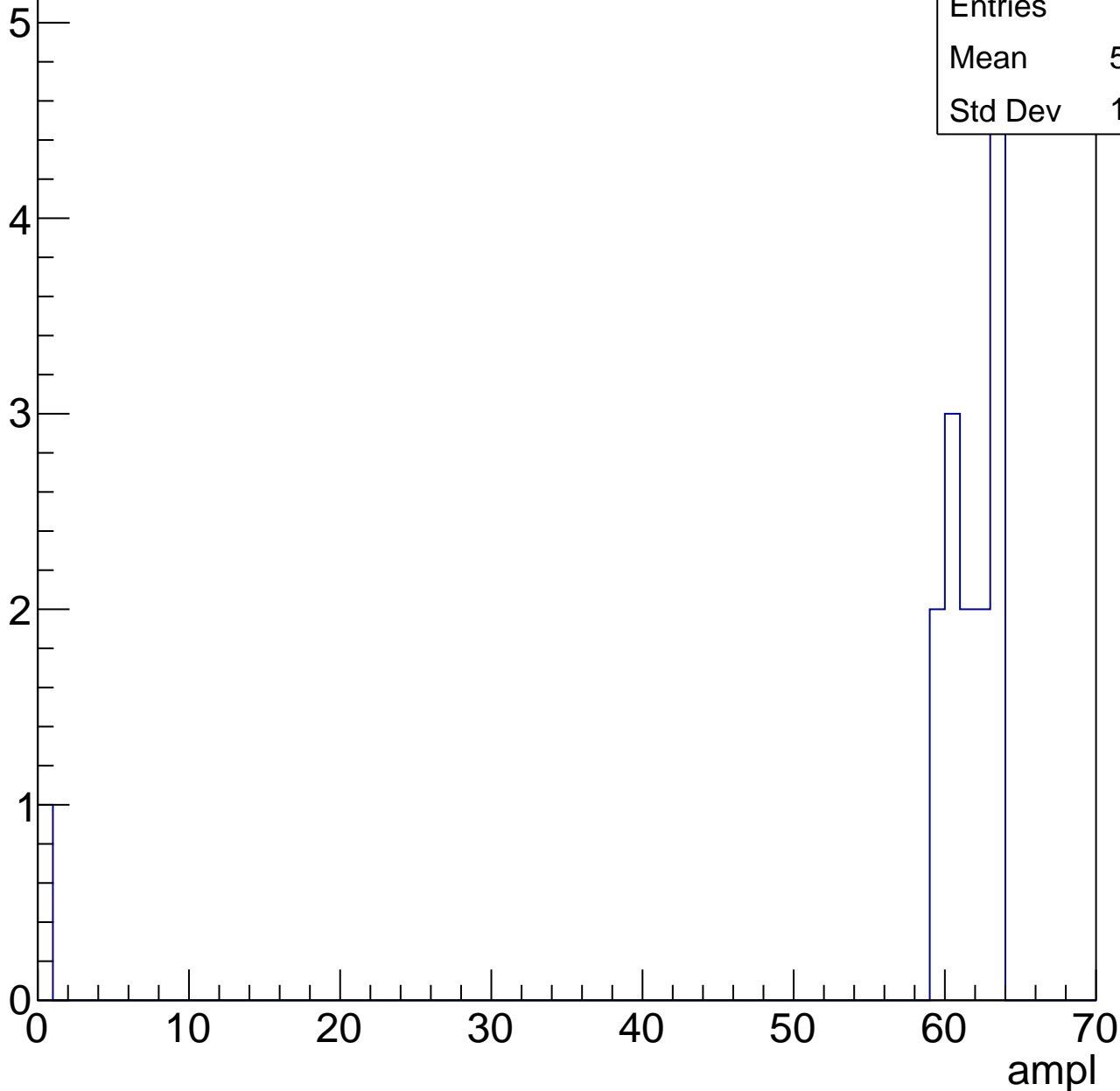


B1L102S, U17-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.27
Std Dev	15.37



B1L102S, U17-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch27, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	29.18
Std Dev	5.114

Gaus mean : 29.7685

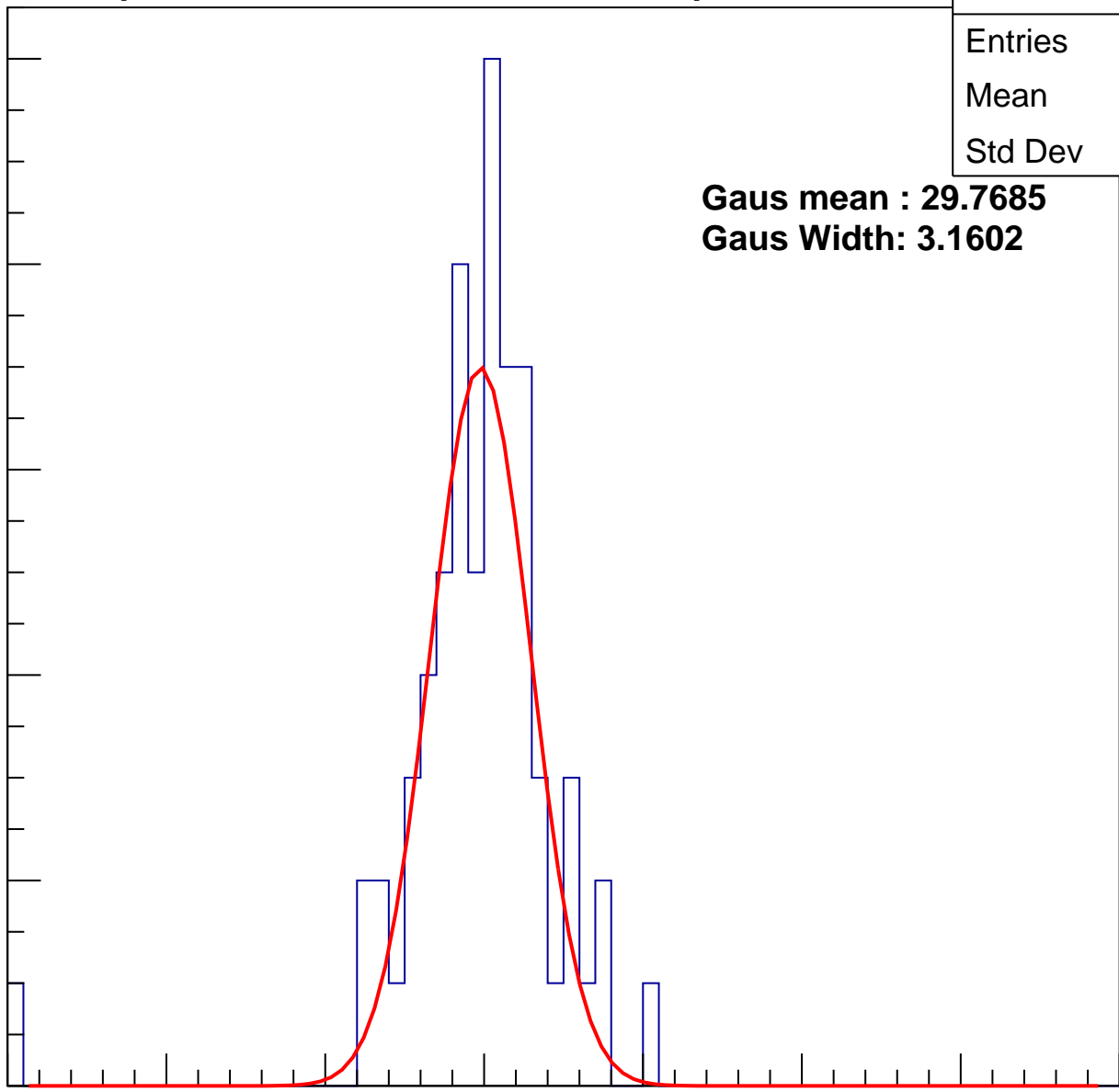
Gaus Width: 3.1602

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch27, adc1

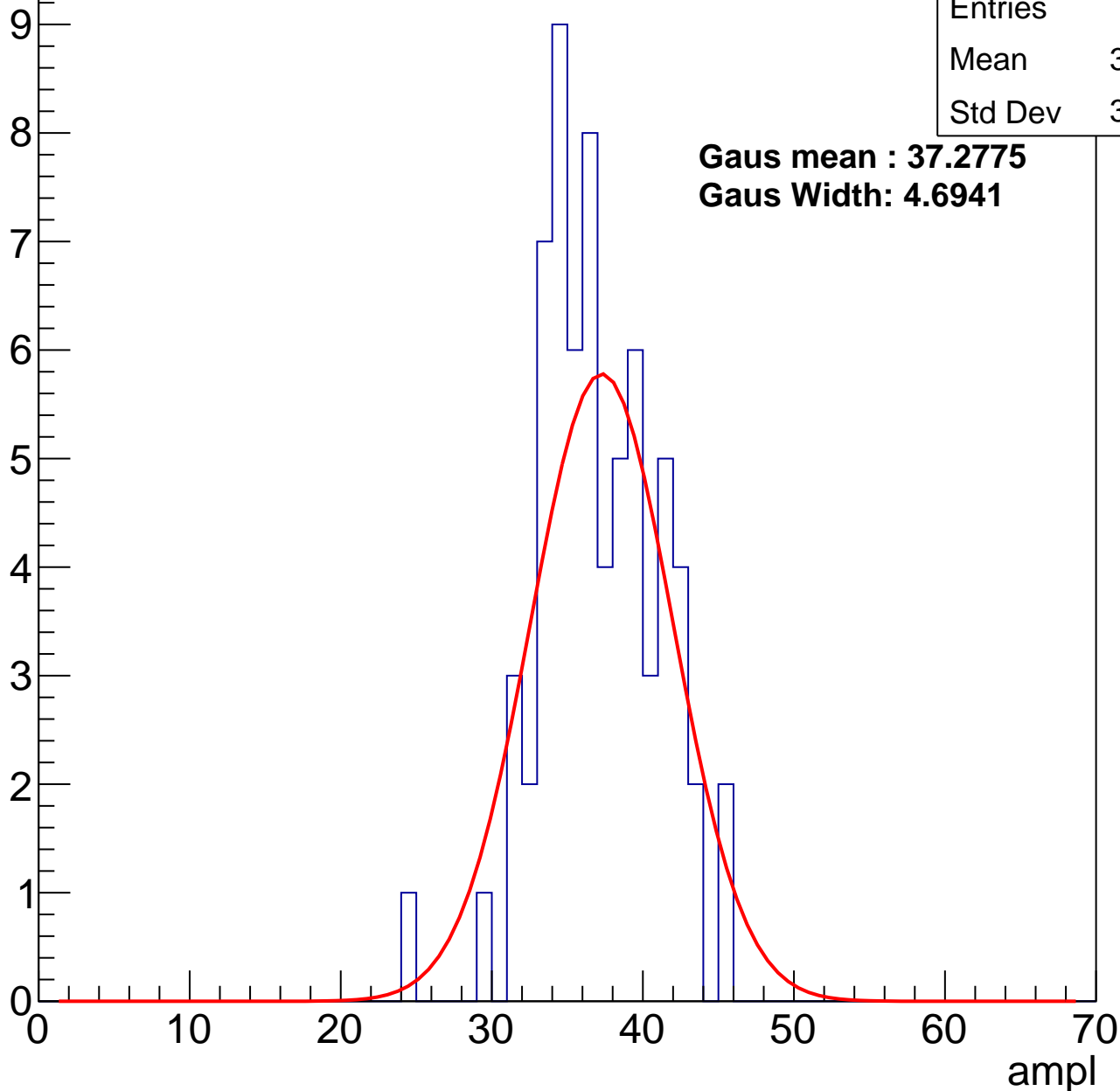
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.56
Std Dev	3.908

Gaus mean : 37.2775

Gaus Width: 4.6941



B1L102S, U17-ch27, adc2

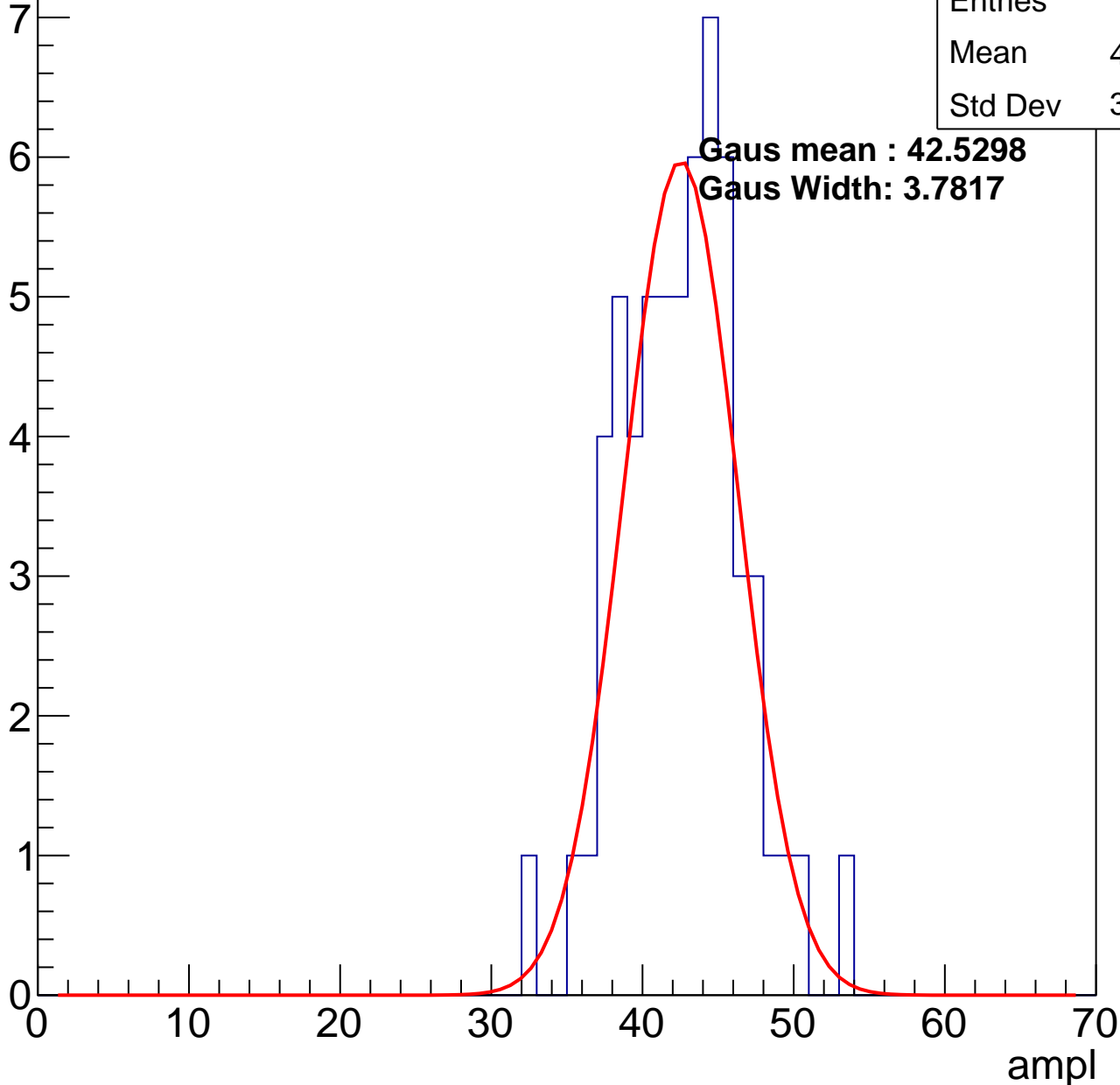
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.12
Std Dev	3.886

Gaus mean : 42.5298

Gaus Width: 3.7817

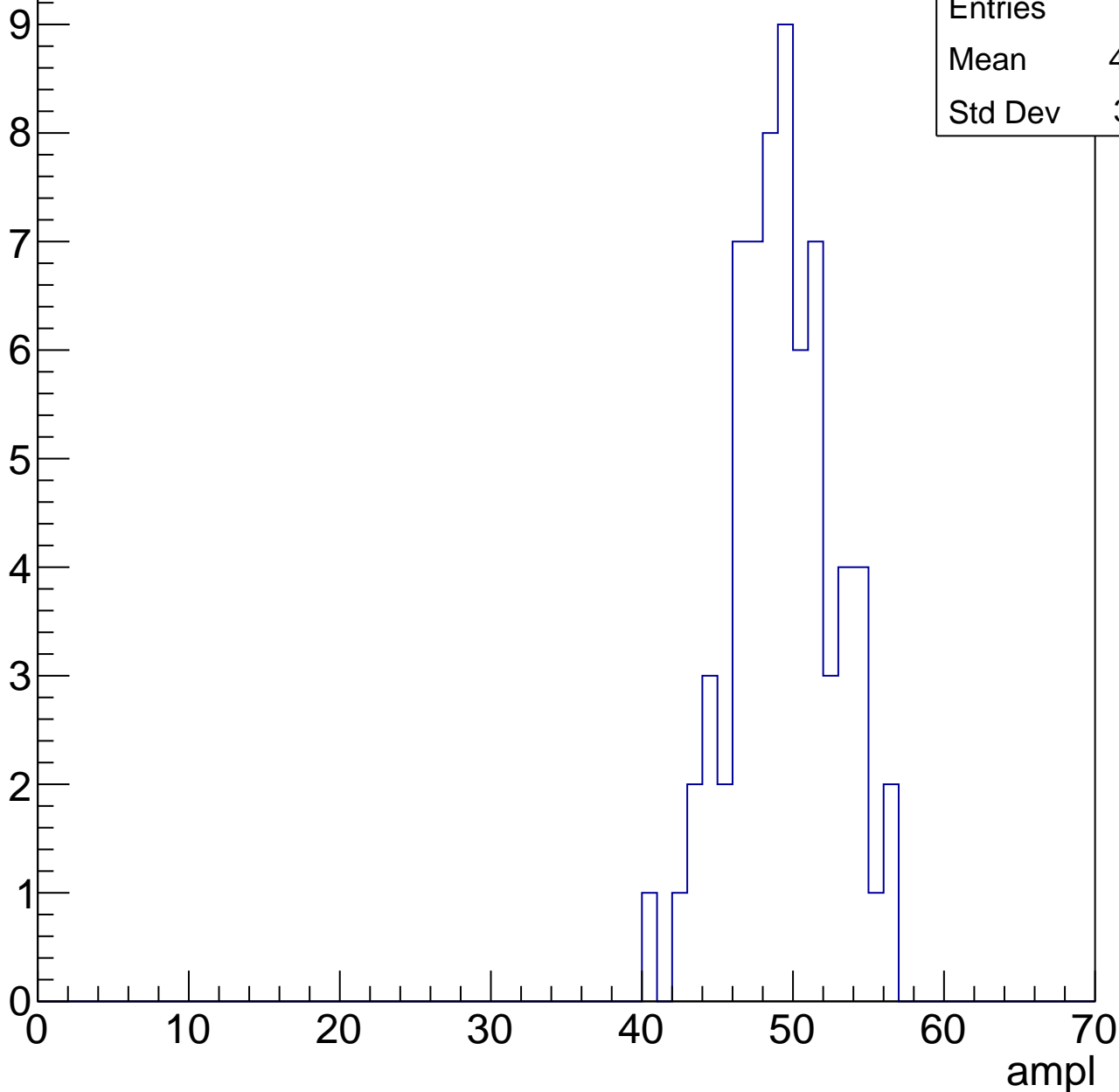


B1L102S, U17-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.87
Std Dev	3.381

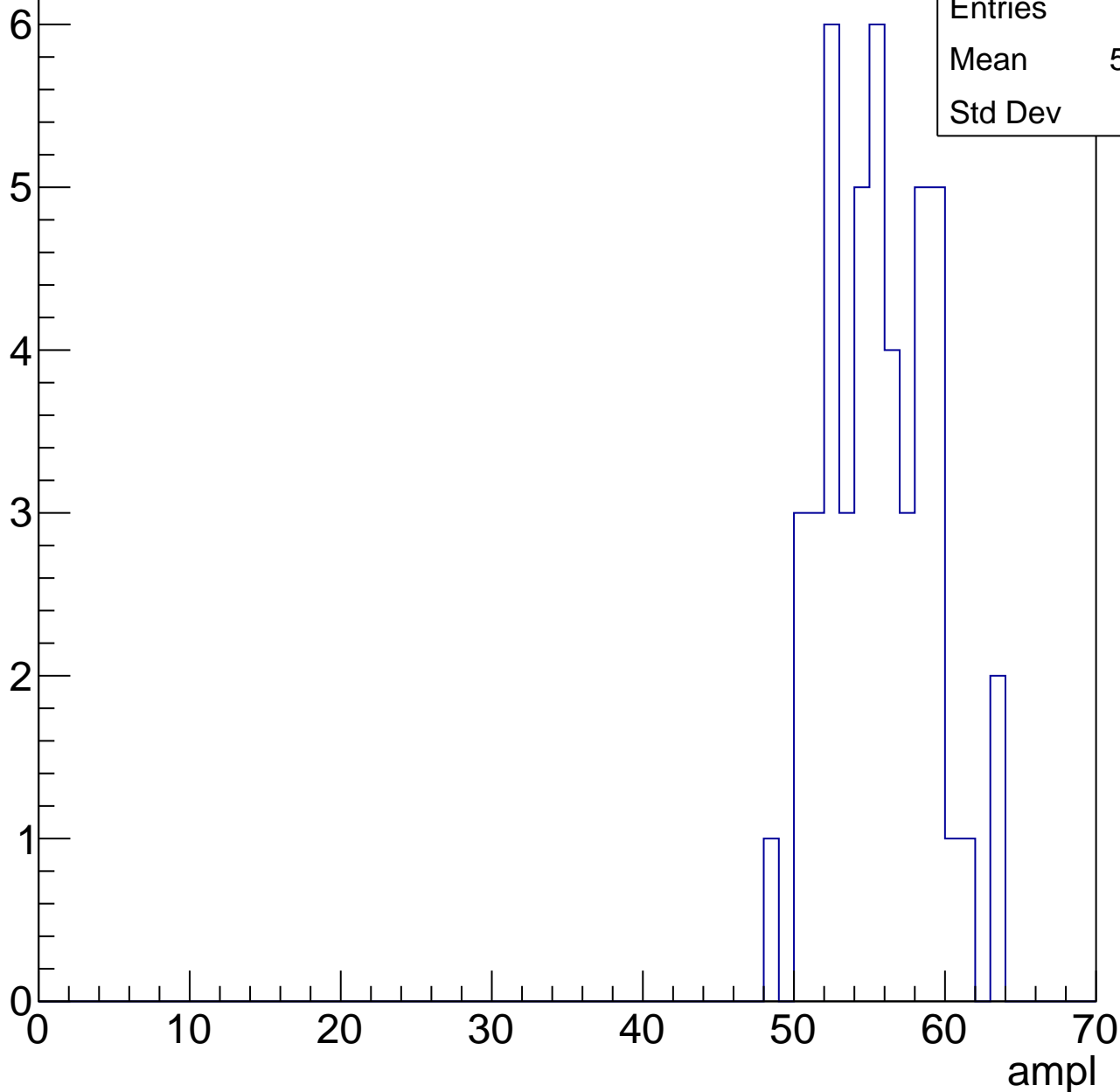


B1L102S, U17-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.19
Std Dev	3.45

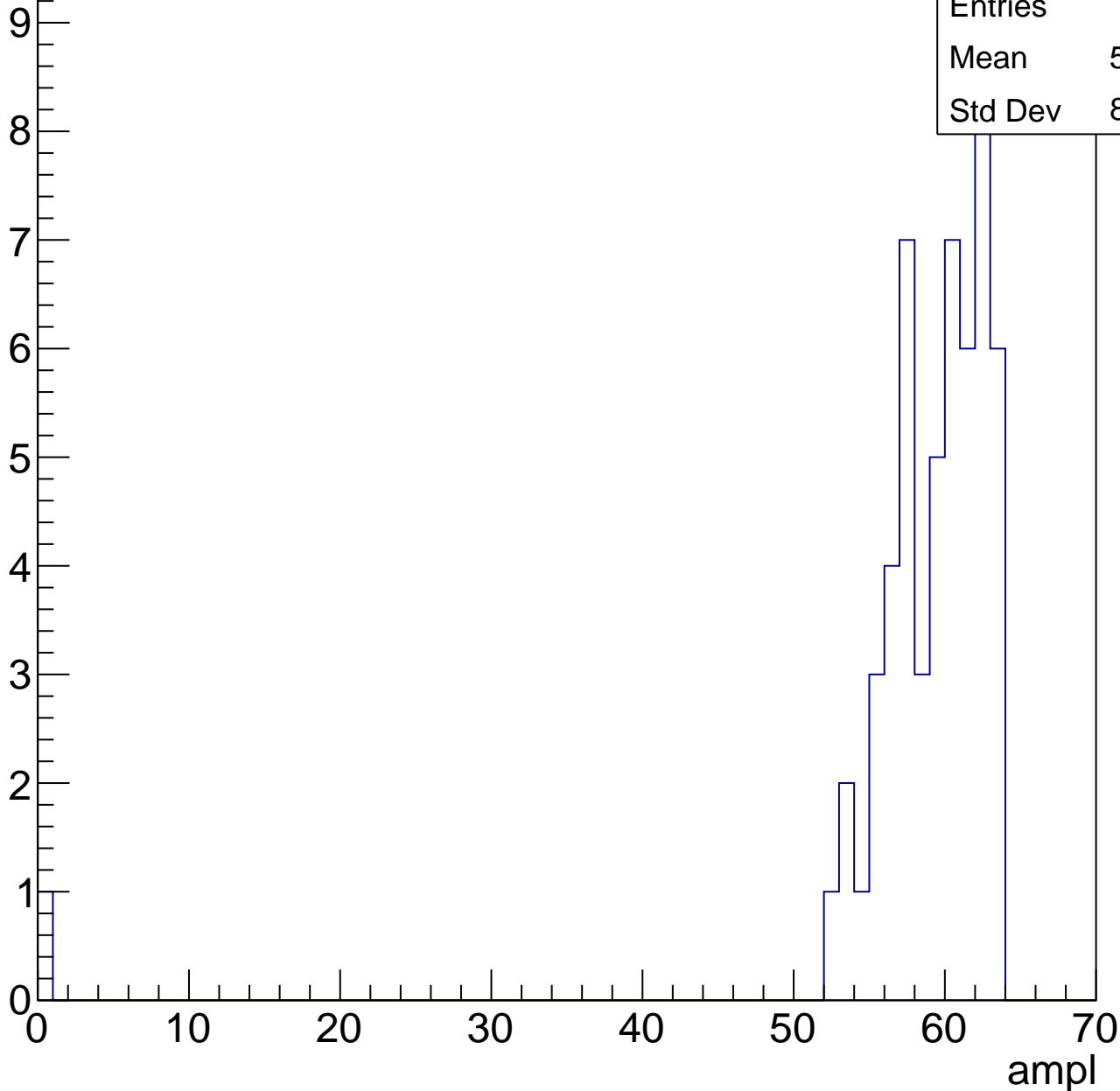


B1L102S, U17-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.02
Std Dev	8.417



B1L102S, U17-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

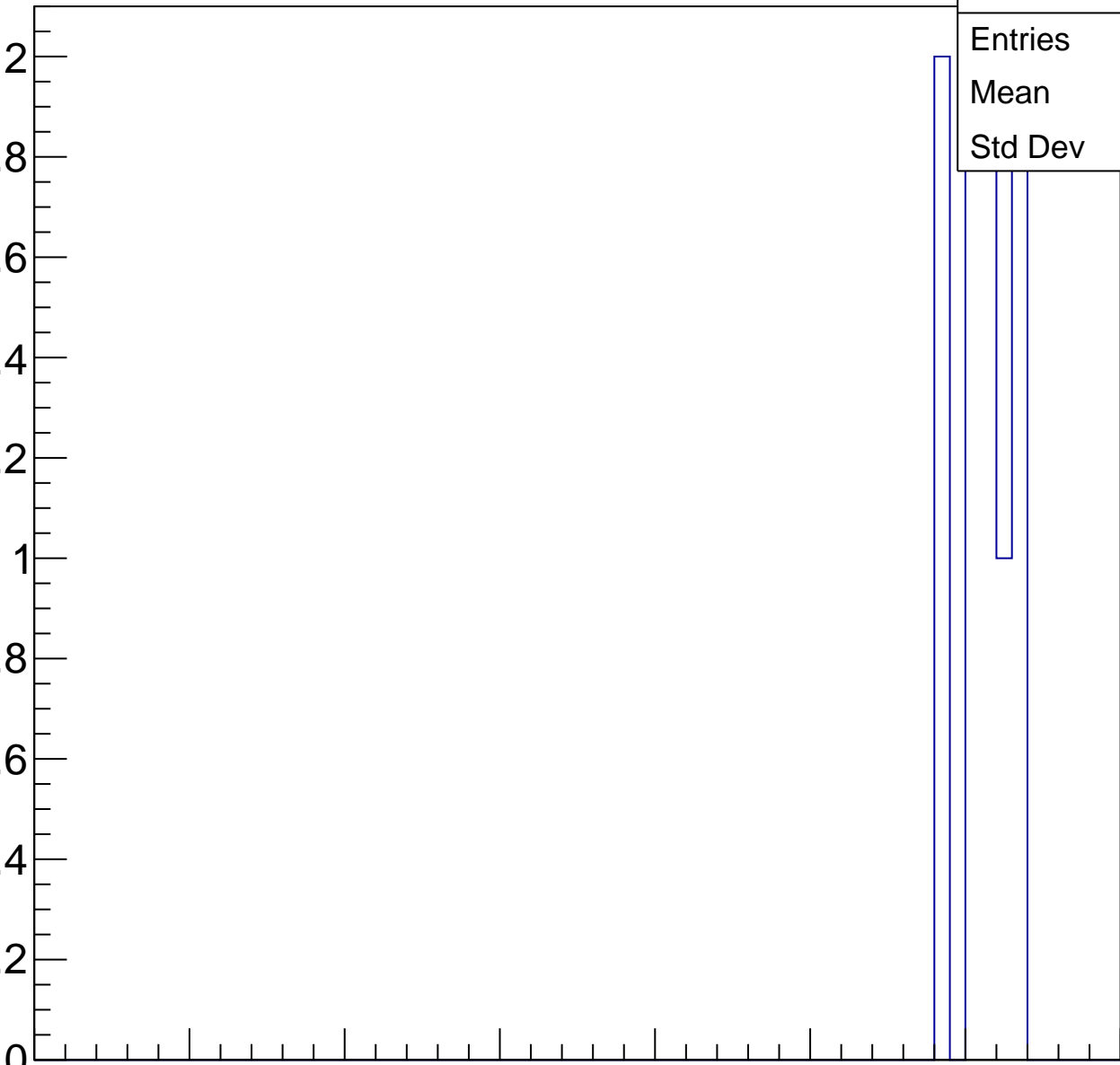
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	60.67
Std Dev	1.764

0 10 20 30 40 50 60 70

ampl



B1L102S, U17-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

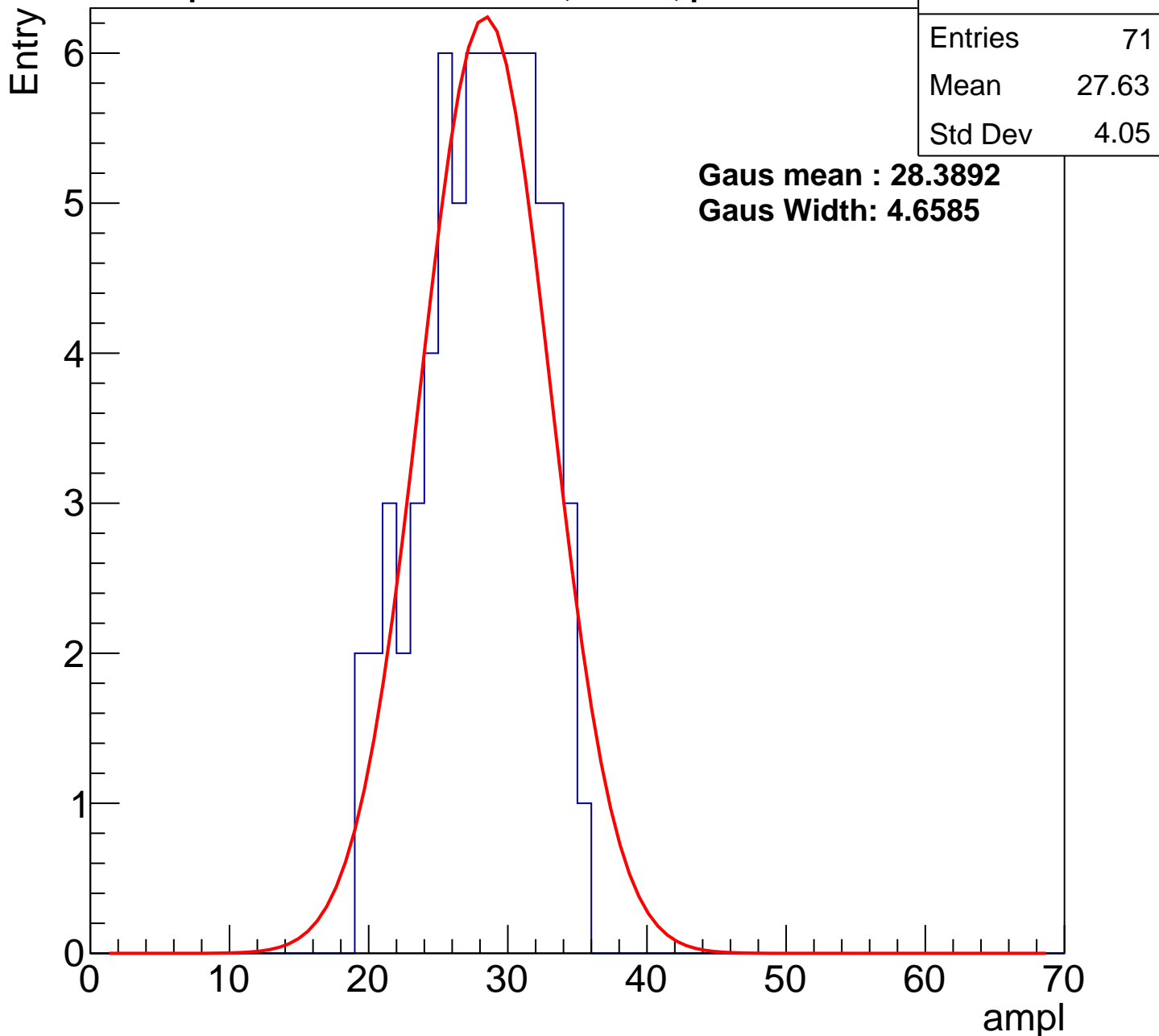
Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L102S, U17-ch28, adc0

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U17-ch28, adc1

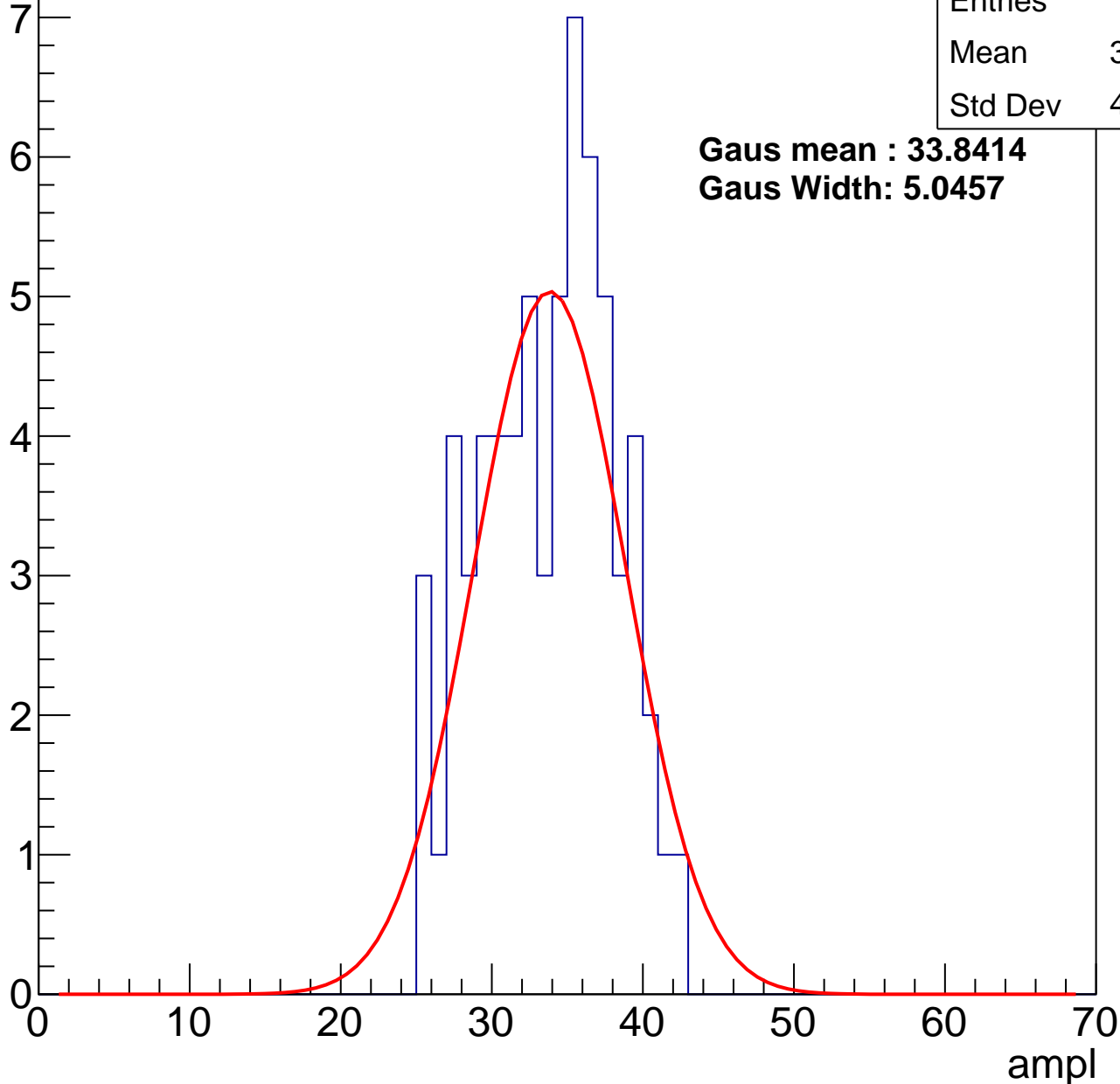
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	33.25
Std Dev	4.308

Gaus mean : 33.8414

Gaus Width: 5.0457



B1L102S, U17-ch28, adc2

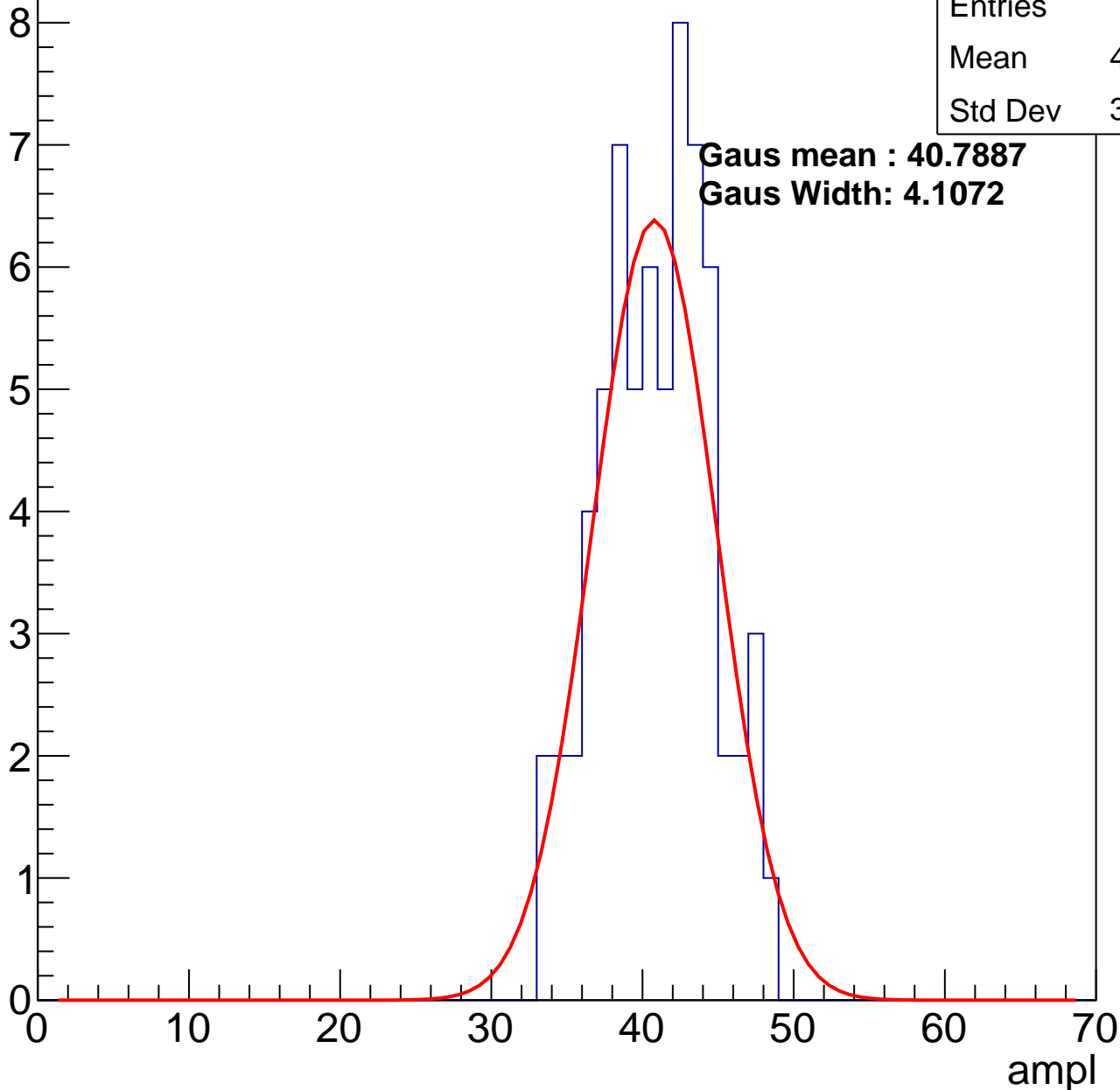
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	40.46
Std Dev	3.617

Gaus mean : 40.7887

Gaus Width: 4.1072

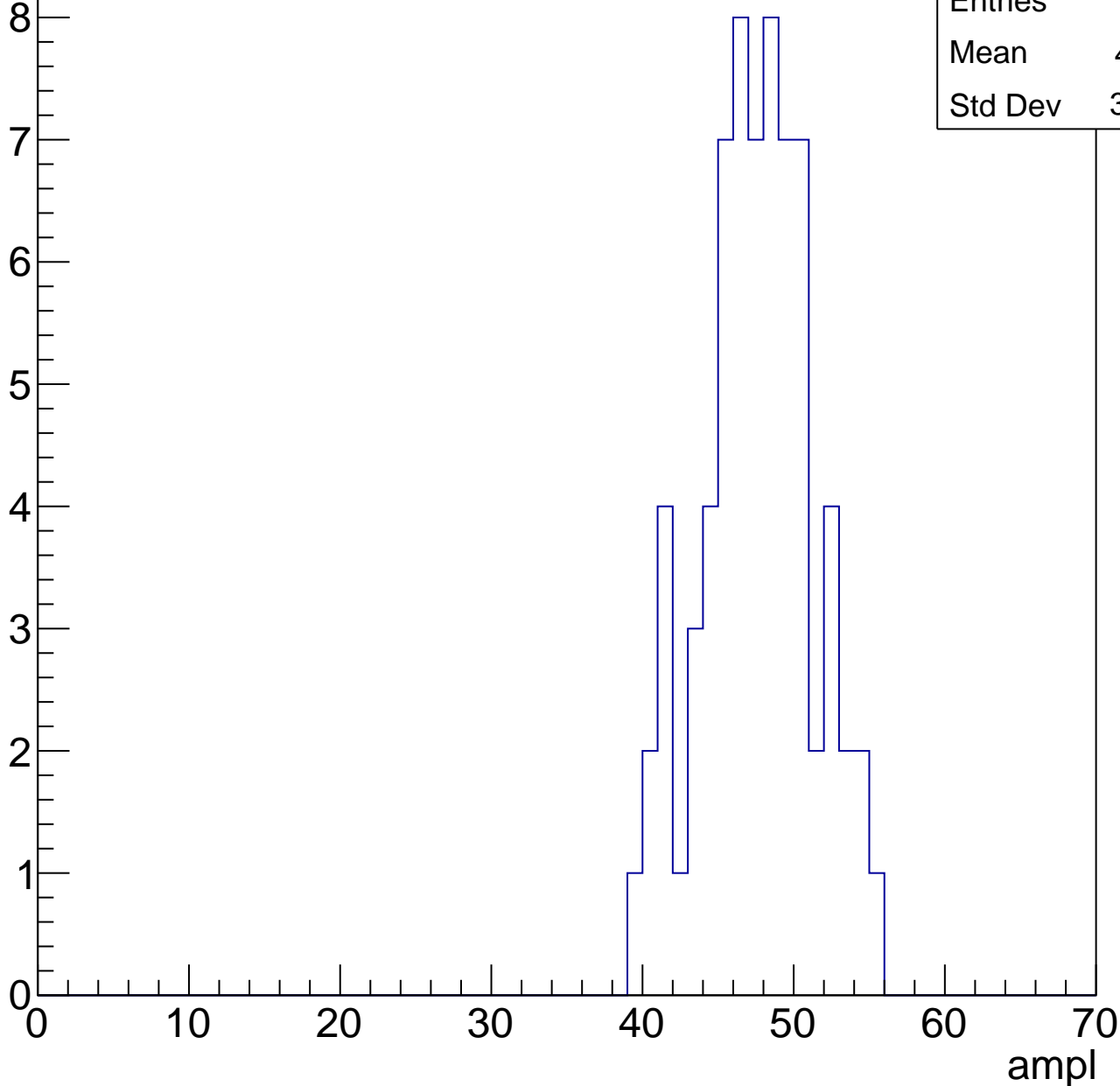


B1L102S, U17-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.11
Std Dev	3.616

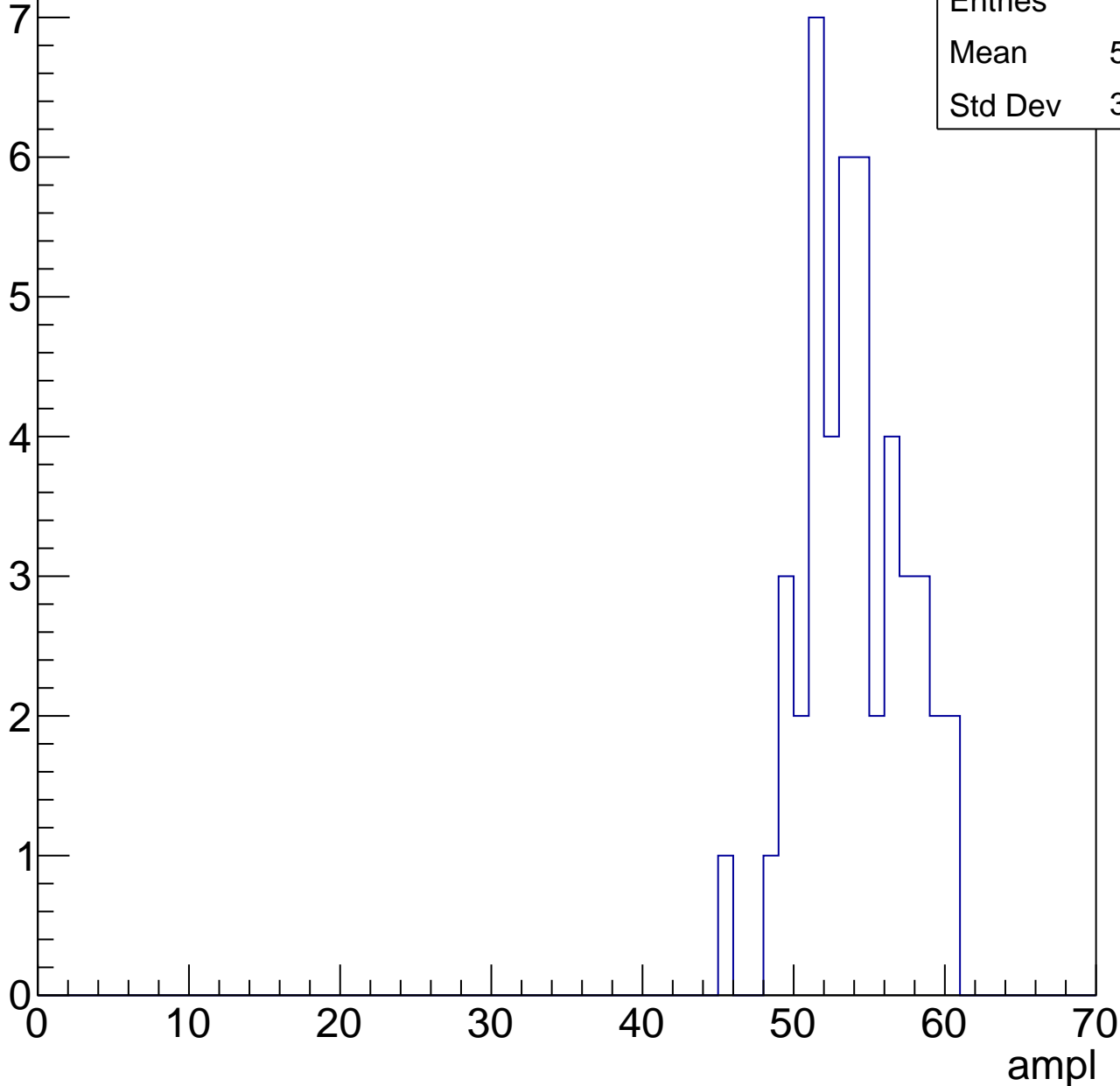


B1L102S, U17-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	53.57
Std Dev	3.347

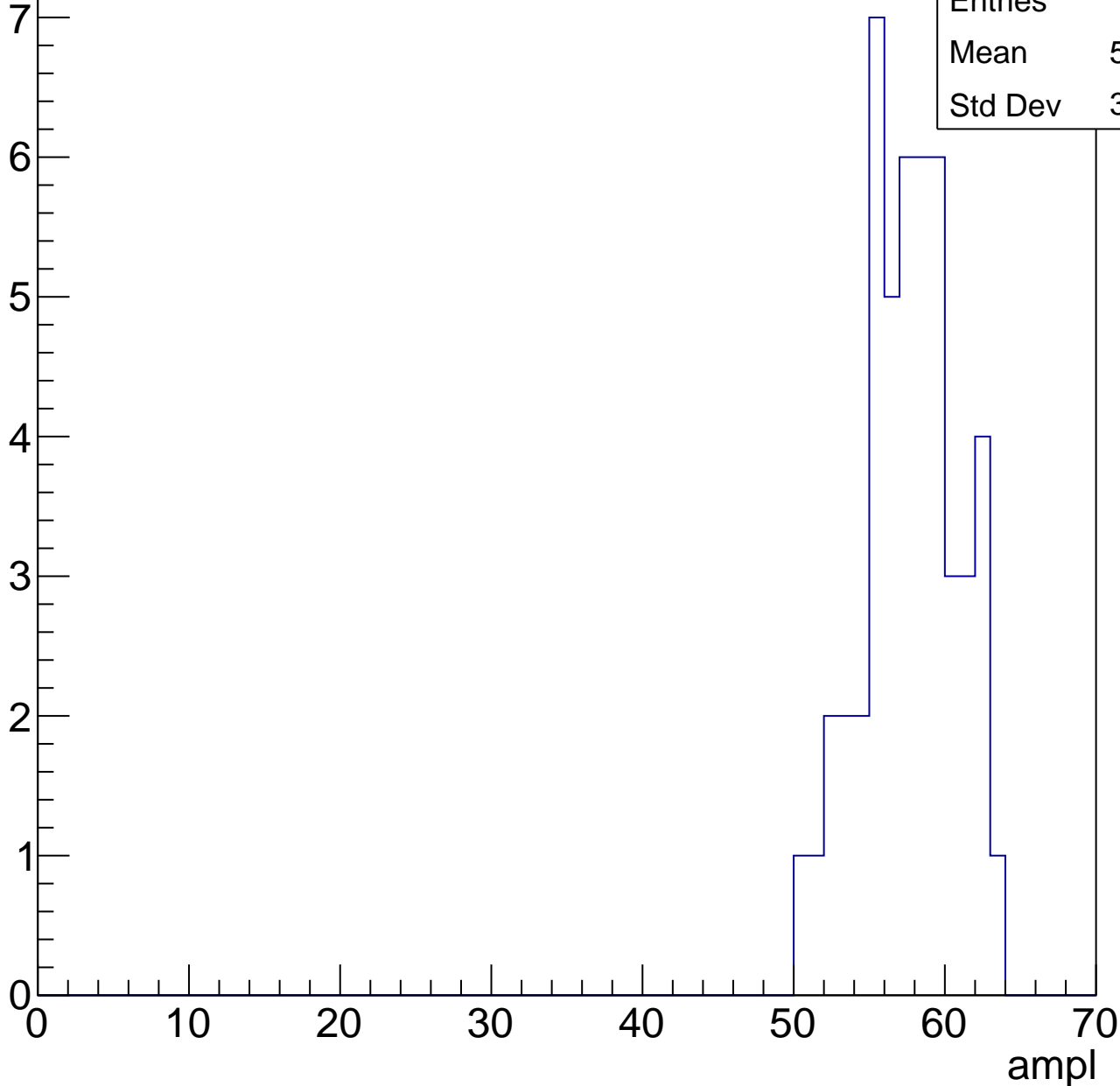


B1L102S, U17-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.18
Std Dev	3.055

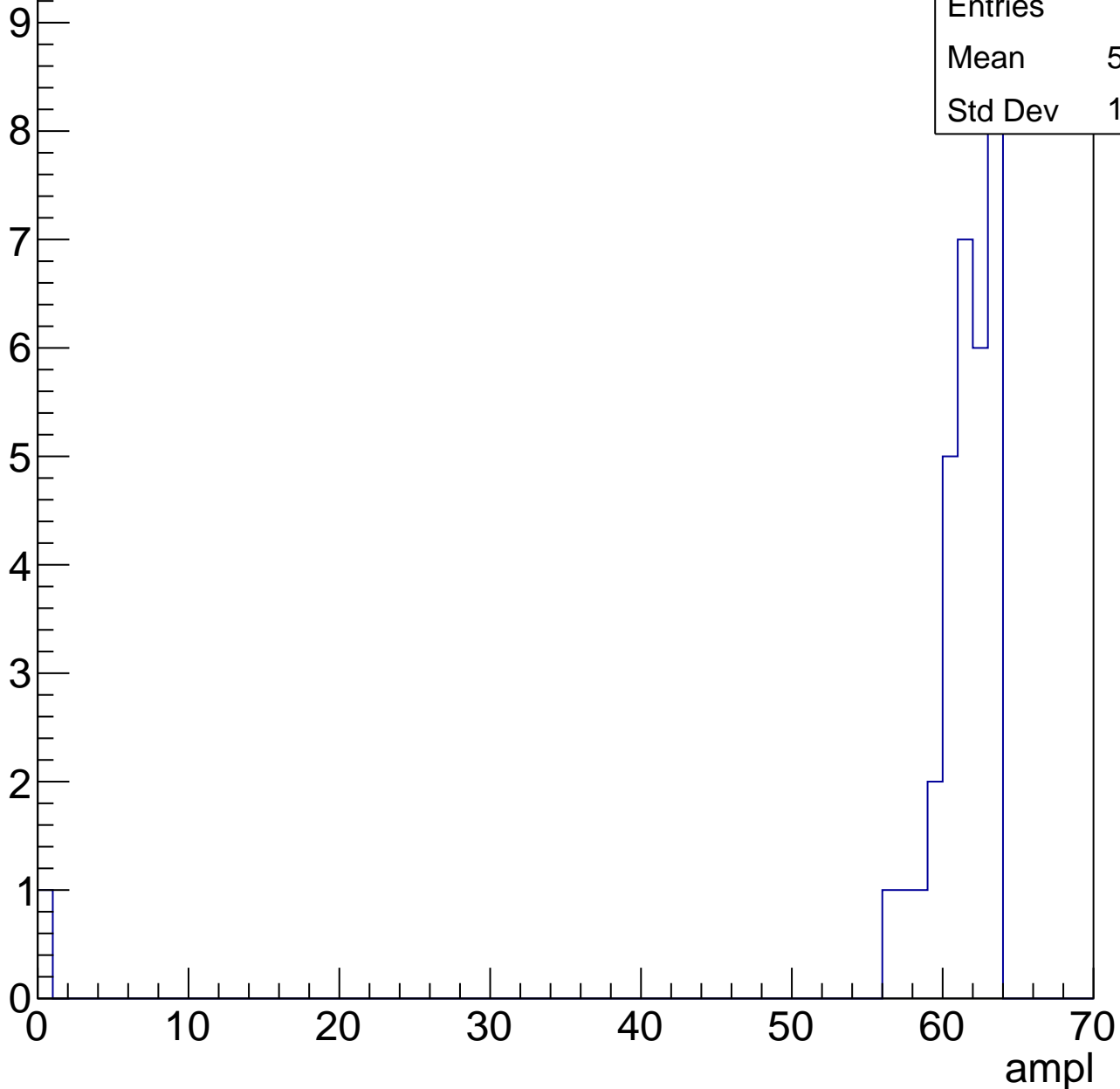


B1L102S, U17-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	59.24
Std Dev	10.62



B1L102S, U17-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U17-ch29, adc0

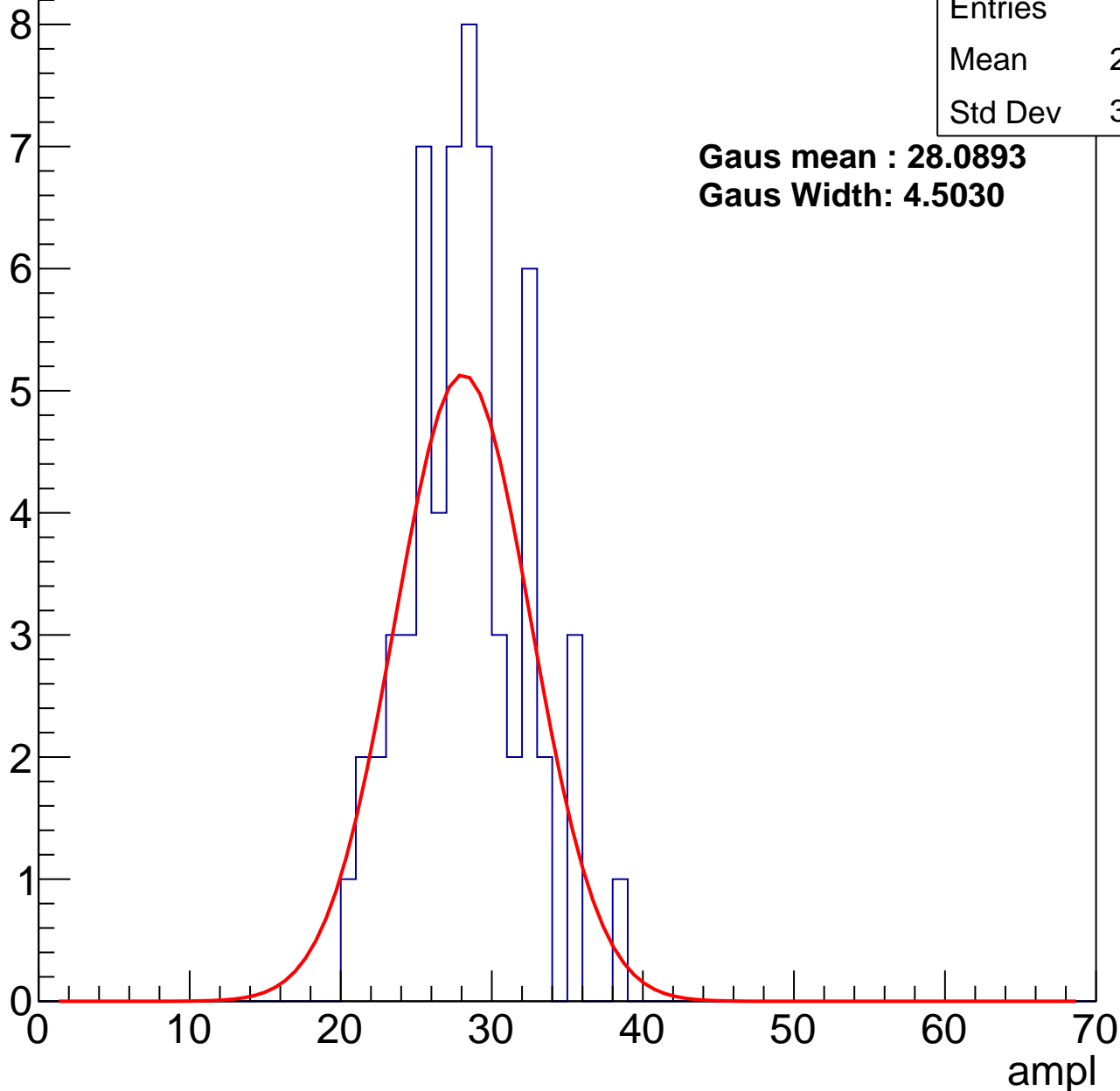
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.79
Std Dev	3.777

Gaus mean : 28.0893

Gaus Width: 4.5030



B1L102S, U17-ch29, adc1

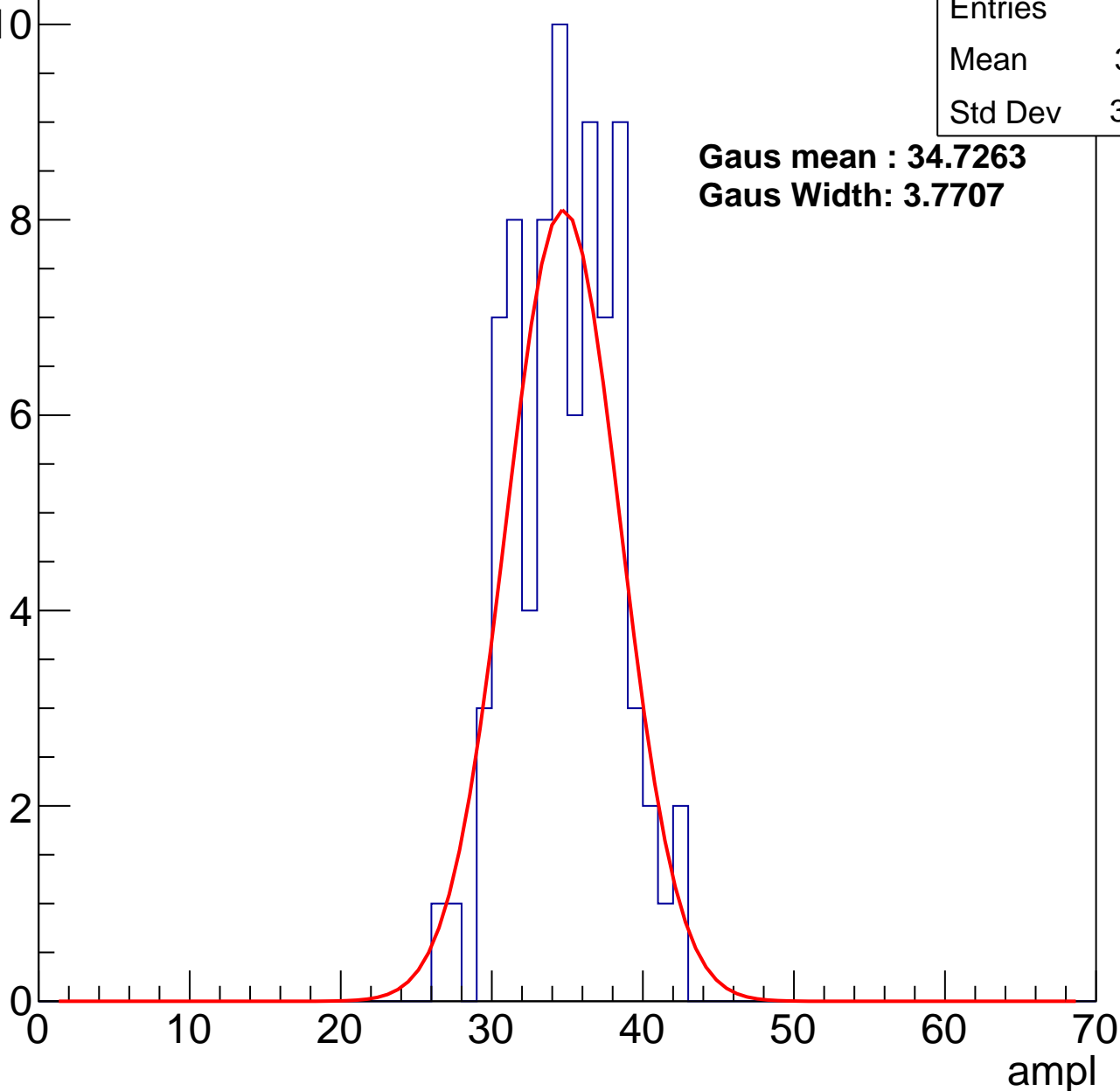
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	34.41
Std Dev	3.435

Gaus mean : 34.7263

Gaus Width: 3.7707

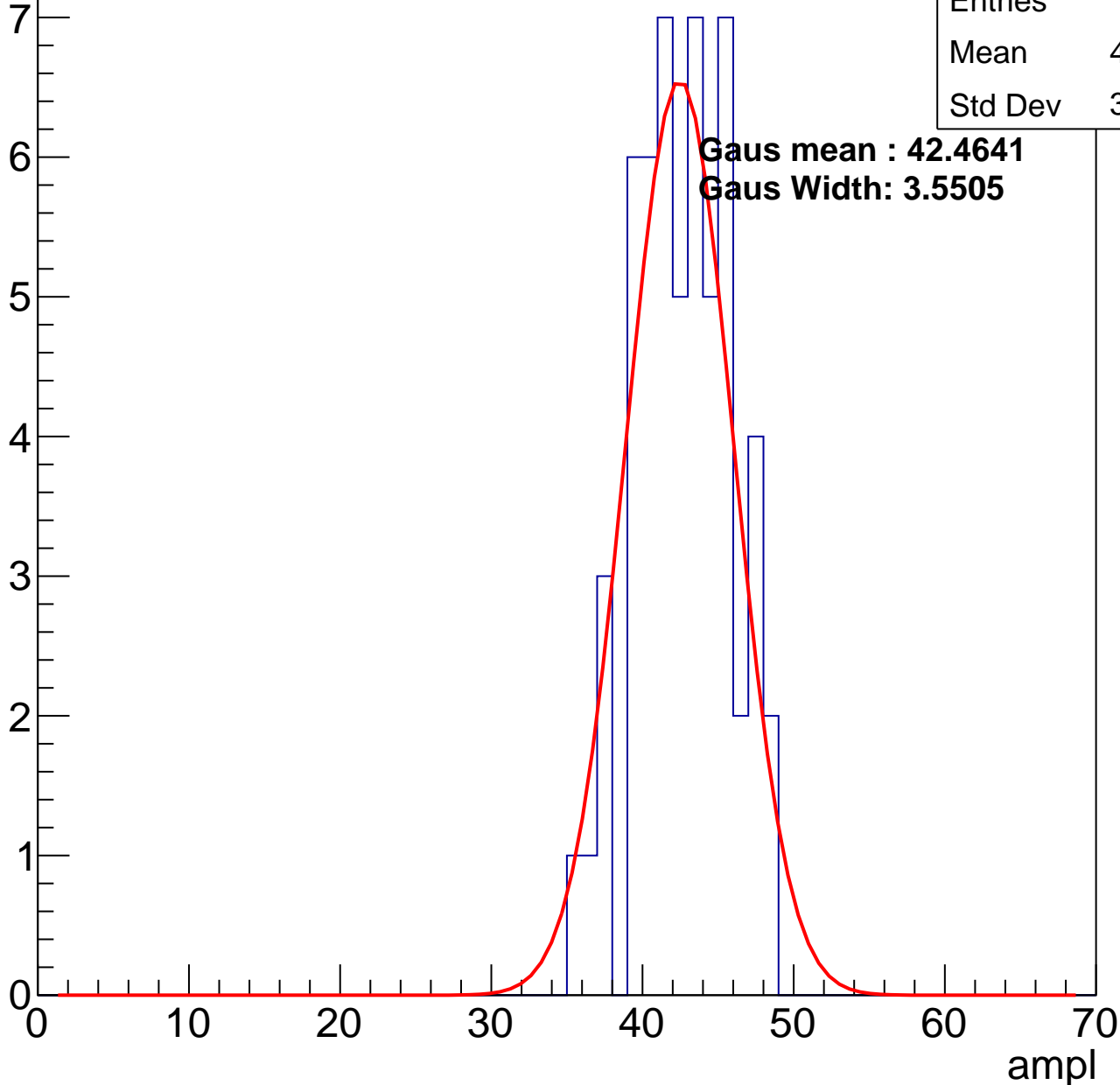


B1L102S, U17-ch29, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

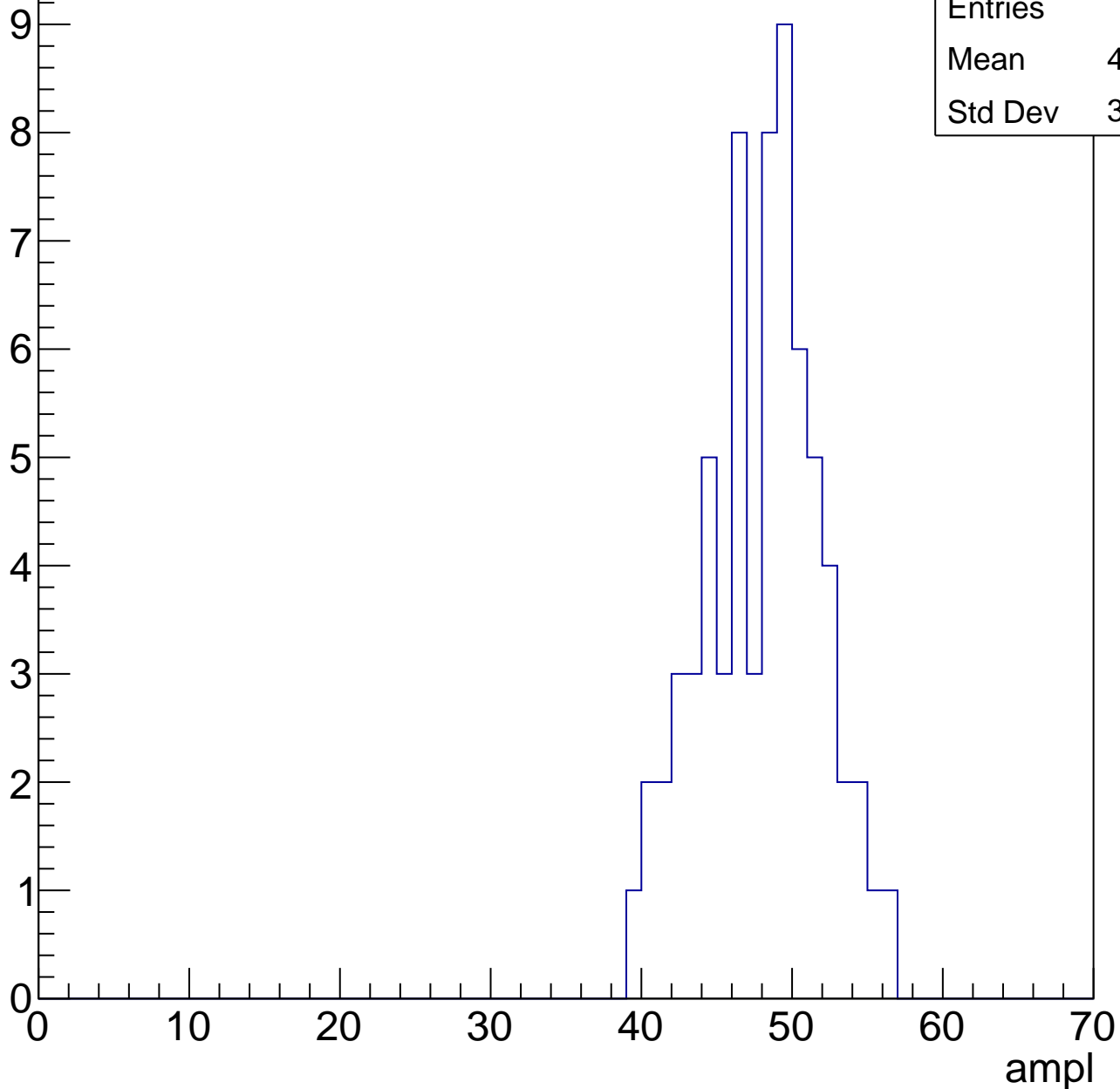
Entries	56
Mean	42.23
Std Dev	3.094



B1L102S, U17-ch29, adc3

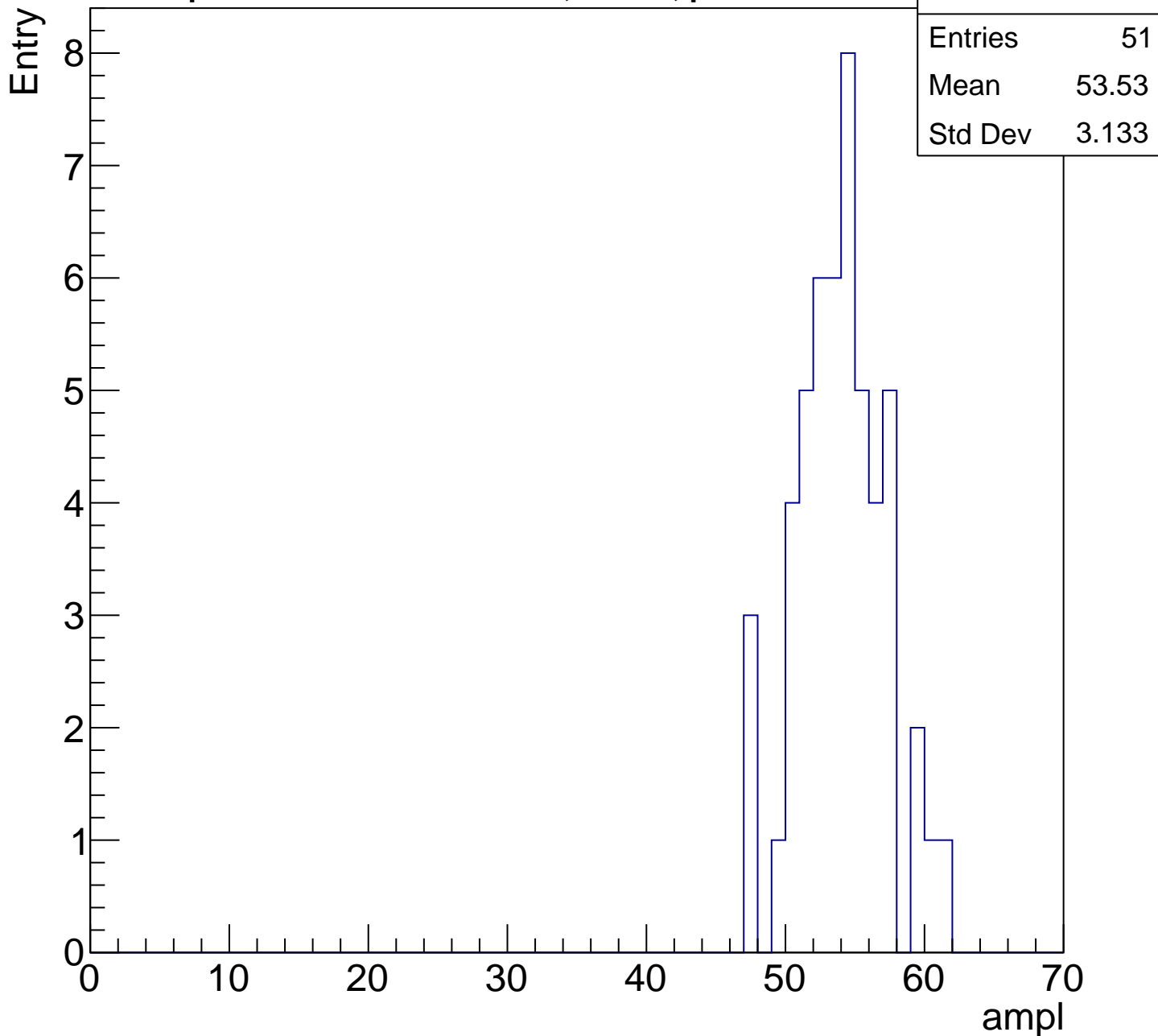
calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

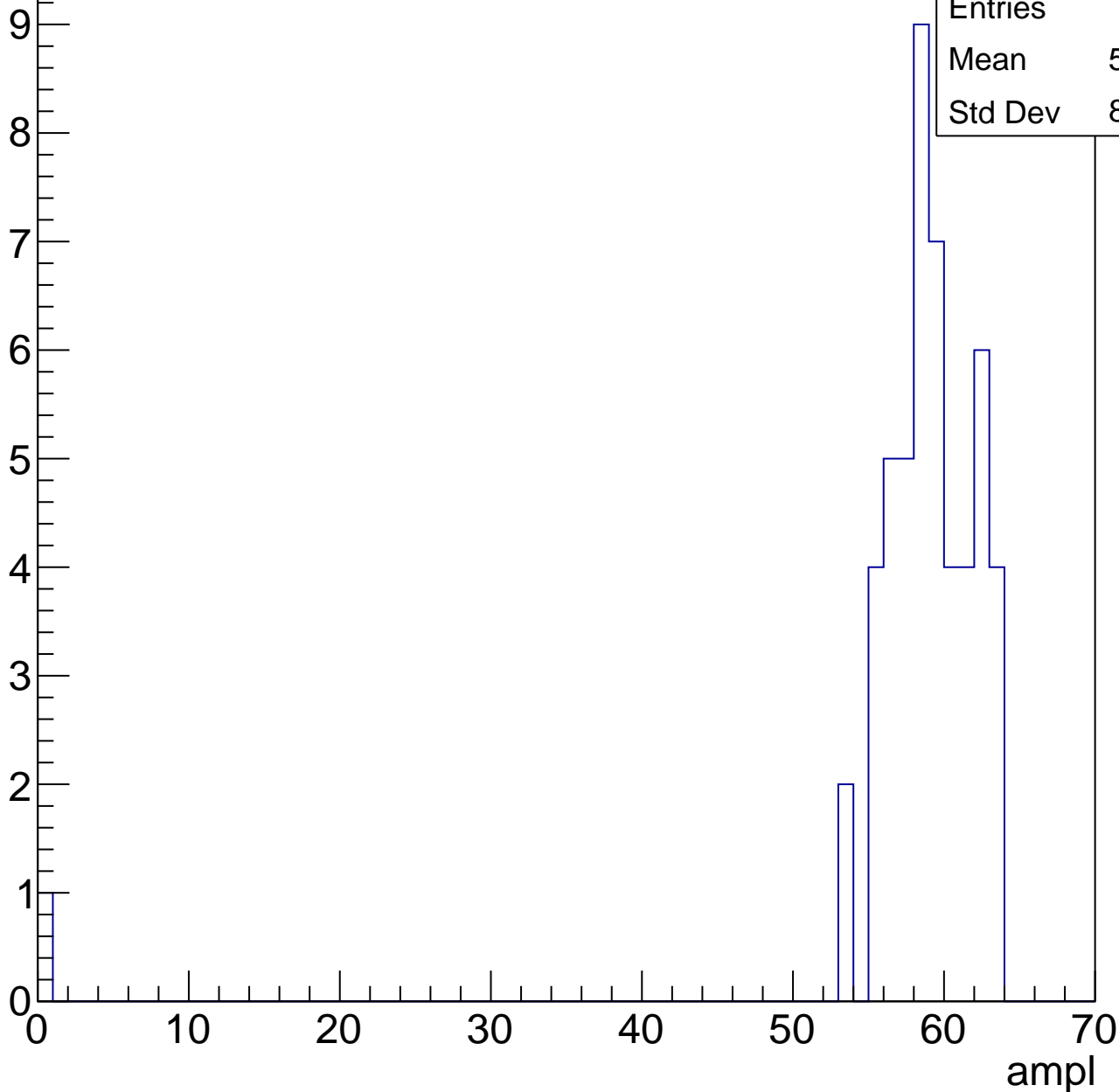


B1L102S, U17-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.53
Std Dev	8.539

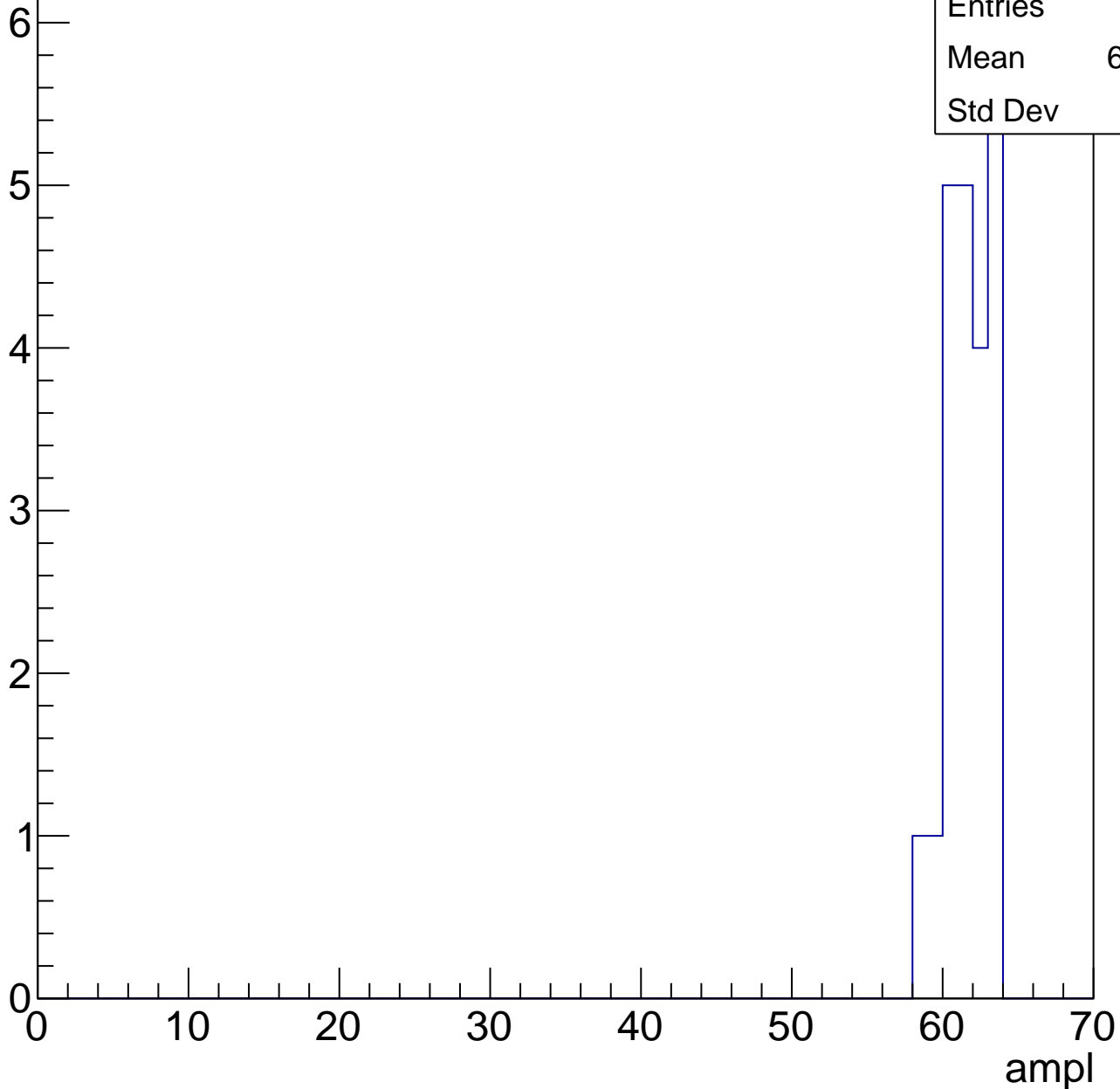


B1L102S, U17-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	61.27
Std Dev	1.42



B1L102S, U17-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch30, adc0

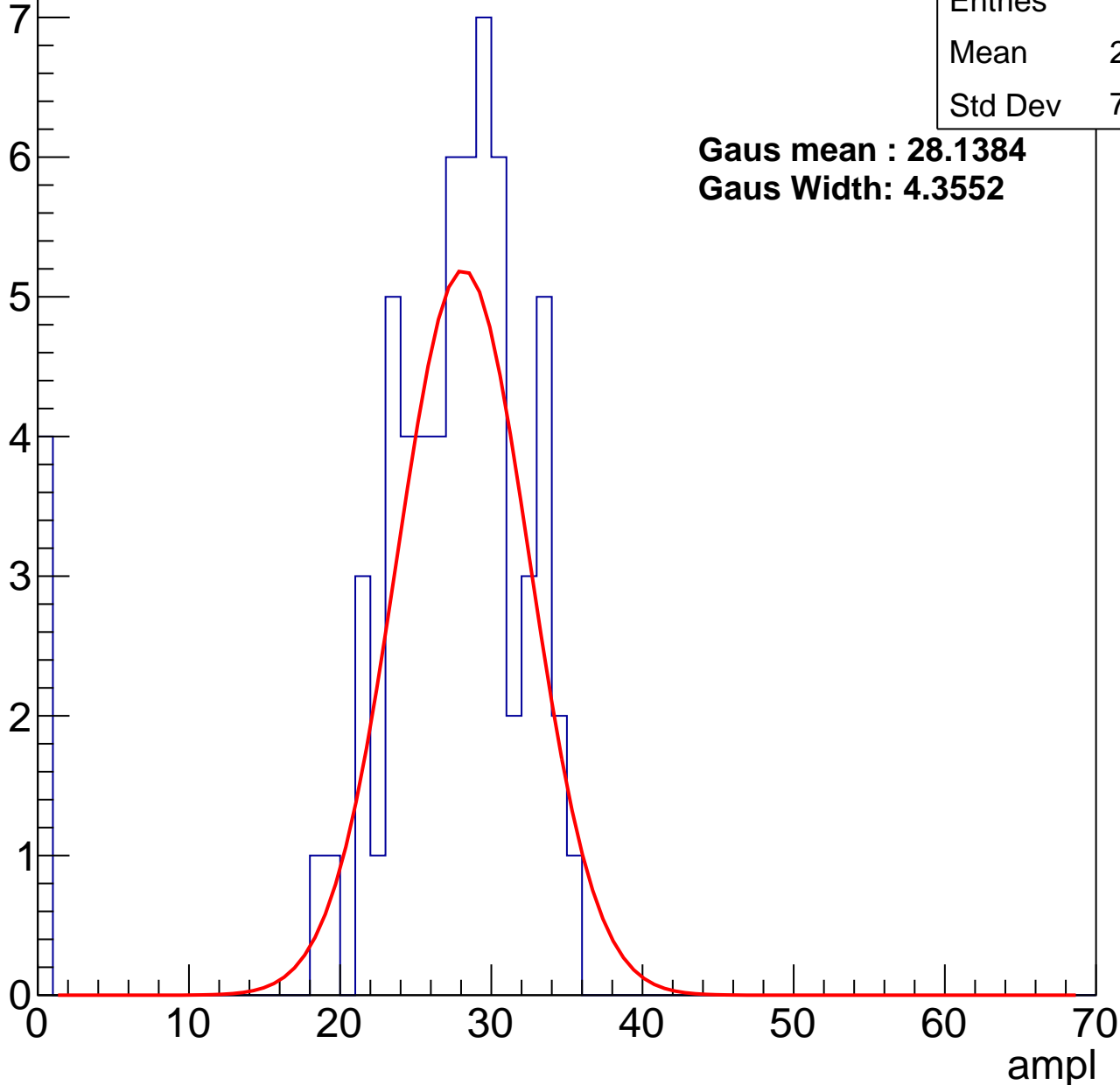
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	25.78
Std Dev	7.617

Gaus mean : 28.1384

Gaus Width: 4.3552



B1L102S, U17-ch30, adc1

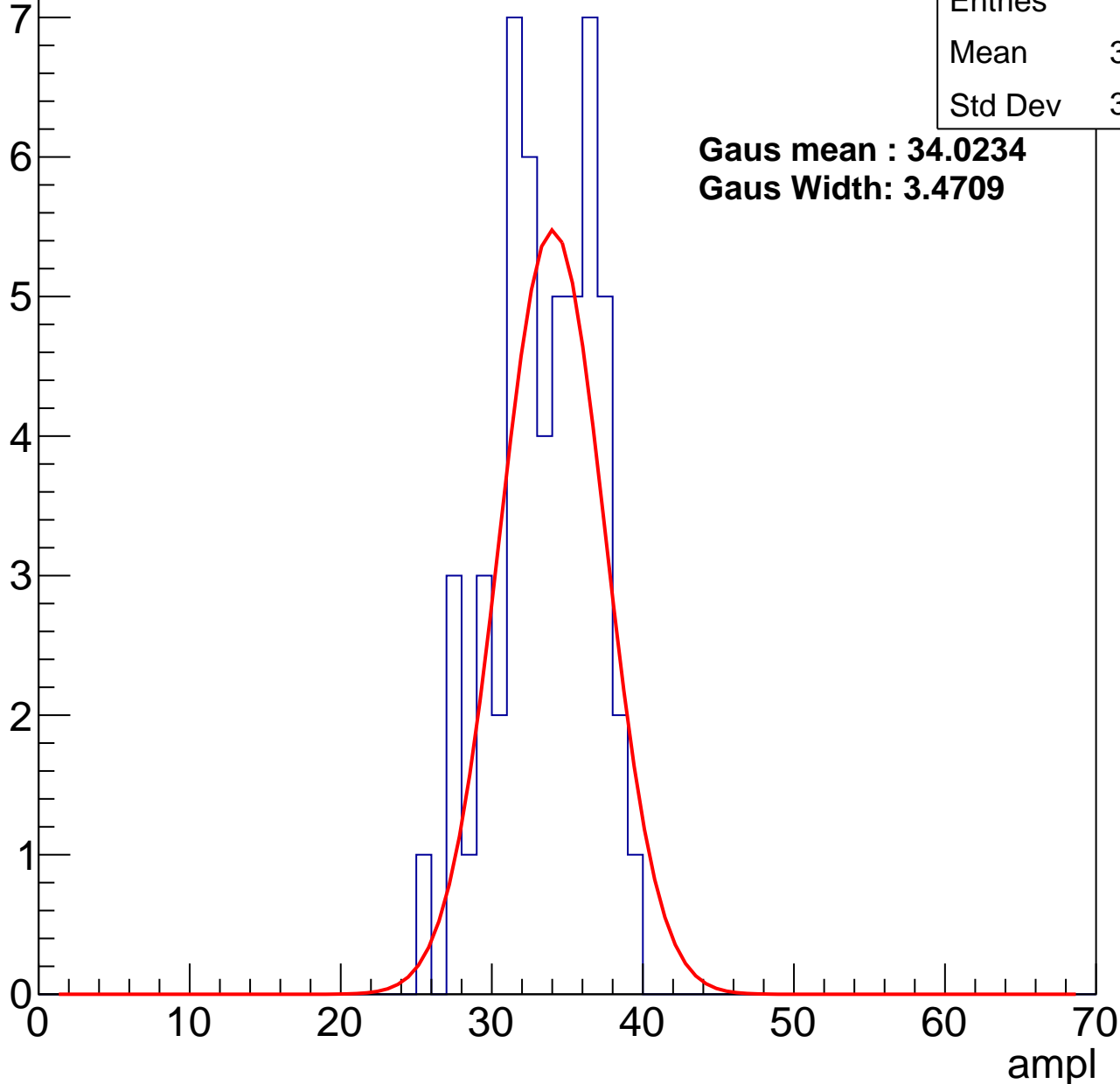
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	33.06
Std Dev	3.255

Gaus mean : 34.0234

Gaus Width: 3.4709



B1L102S, U17-ch30, adc2

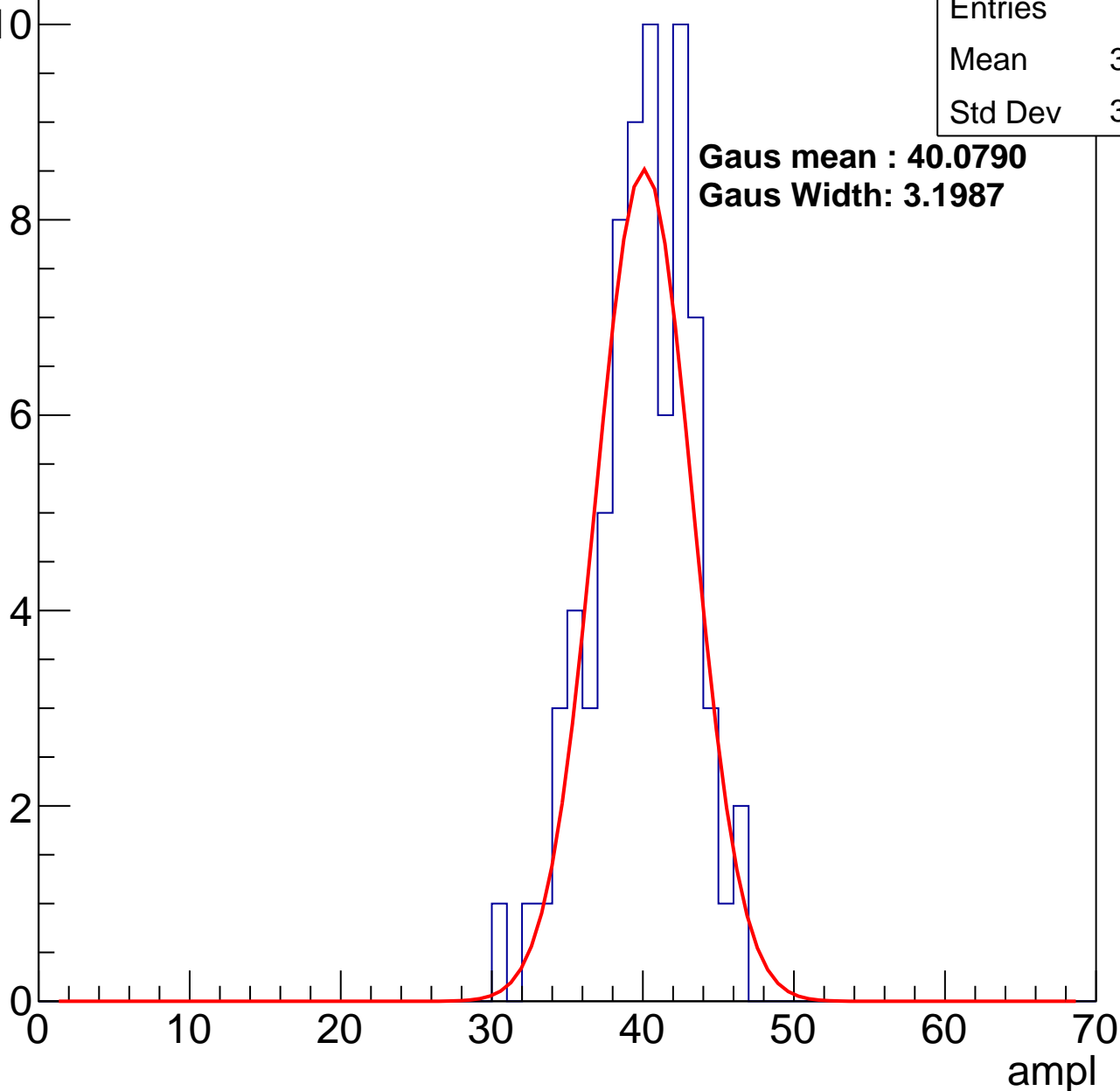
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	39.47
Std Dev	3.264

Gaus mean : 40.0790

Gaus Width: 3.1987



B1L102S, U17-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	47.37
Std Dev	3.374

Entry

10

8

6

4

2

0

0

10

20

30

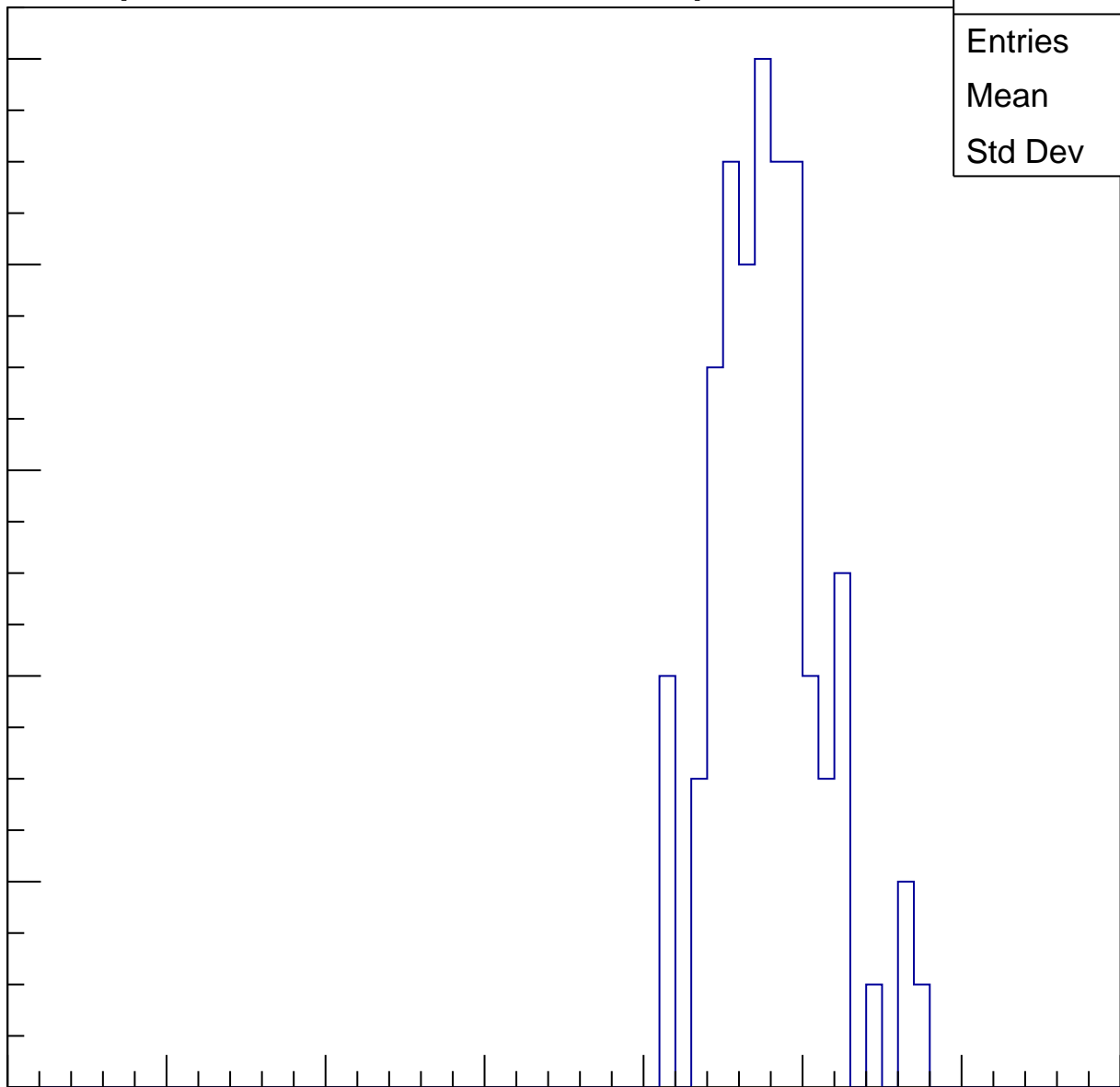
40

50

60

70

ampl

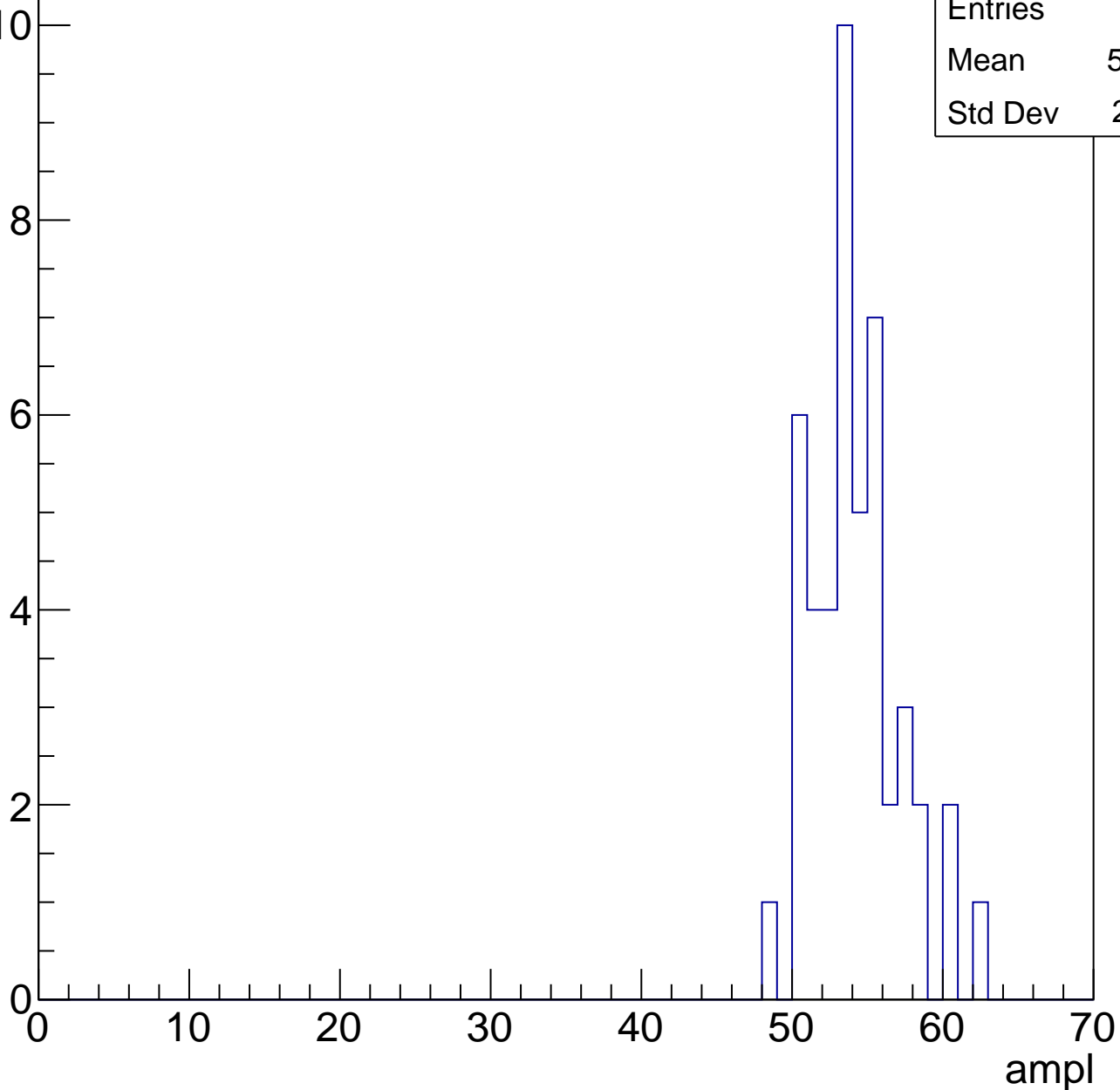


B1L102S, U17-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	53.74
Std Dev	2.921

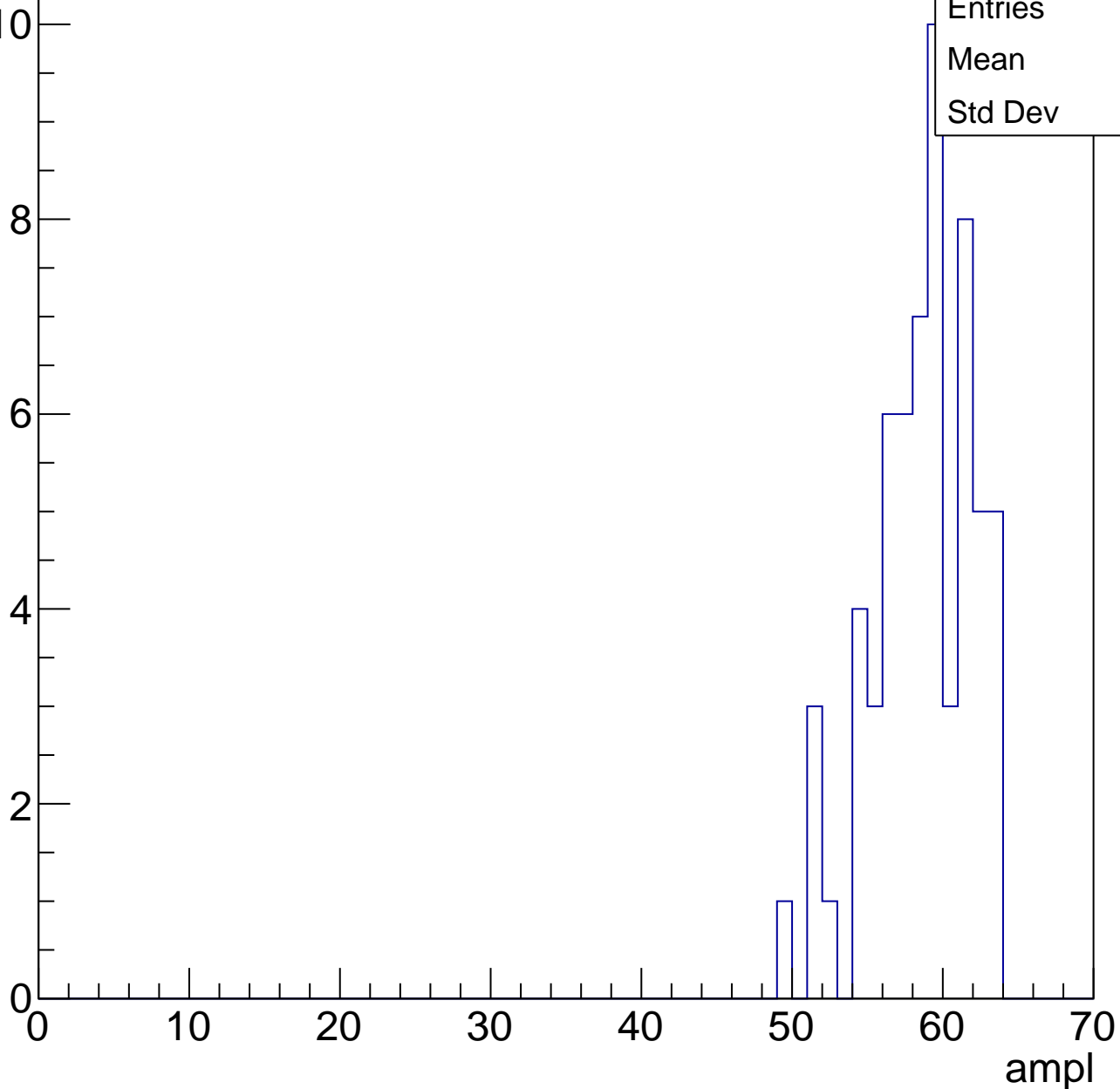


B1L102S, U17-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	58.1
Std Dev	3.32

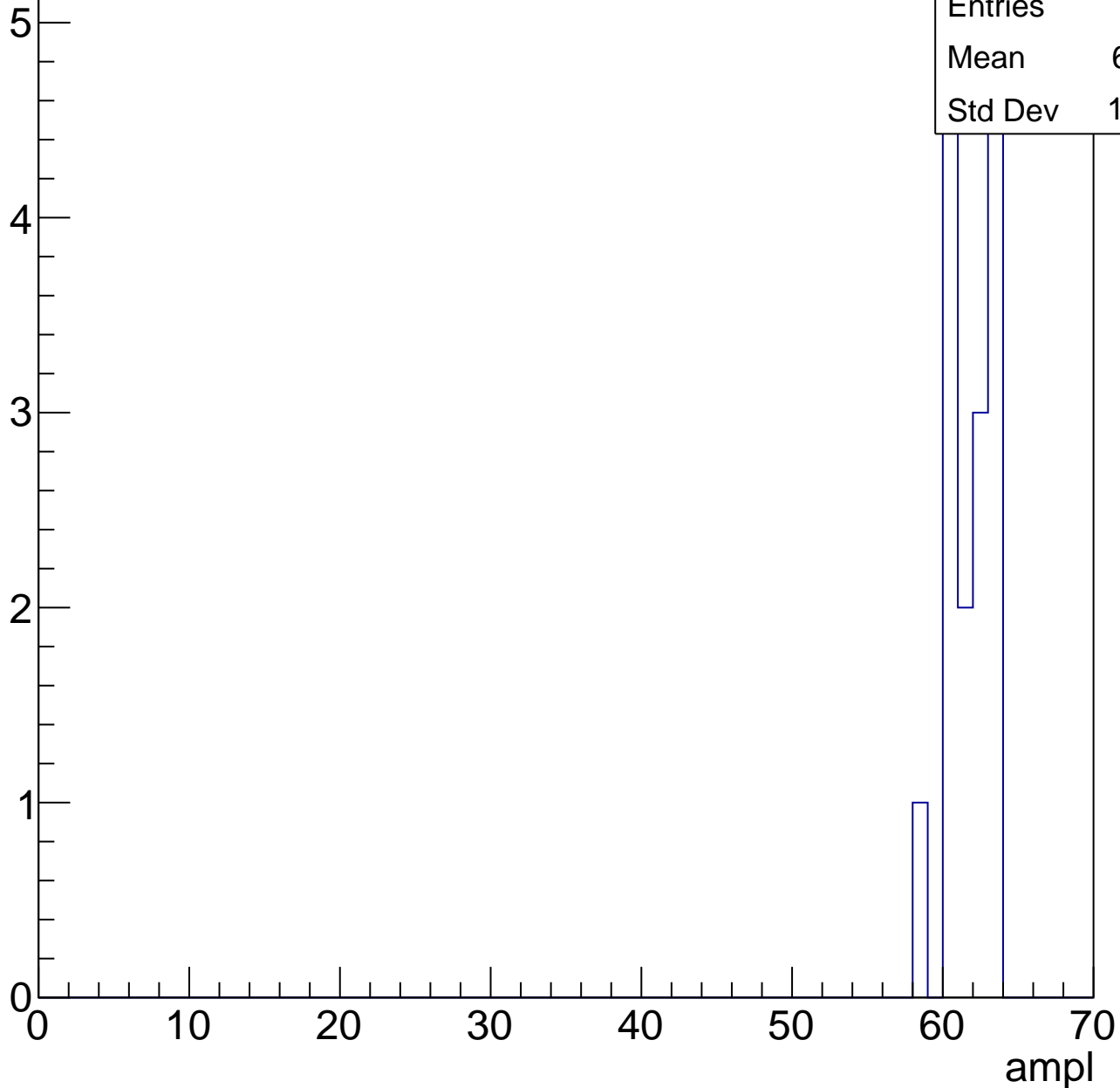


B1L102S, U17-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

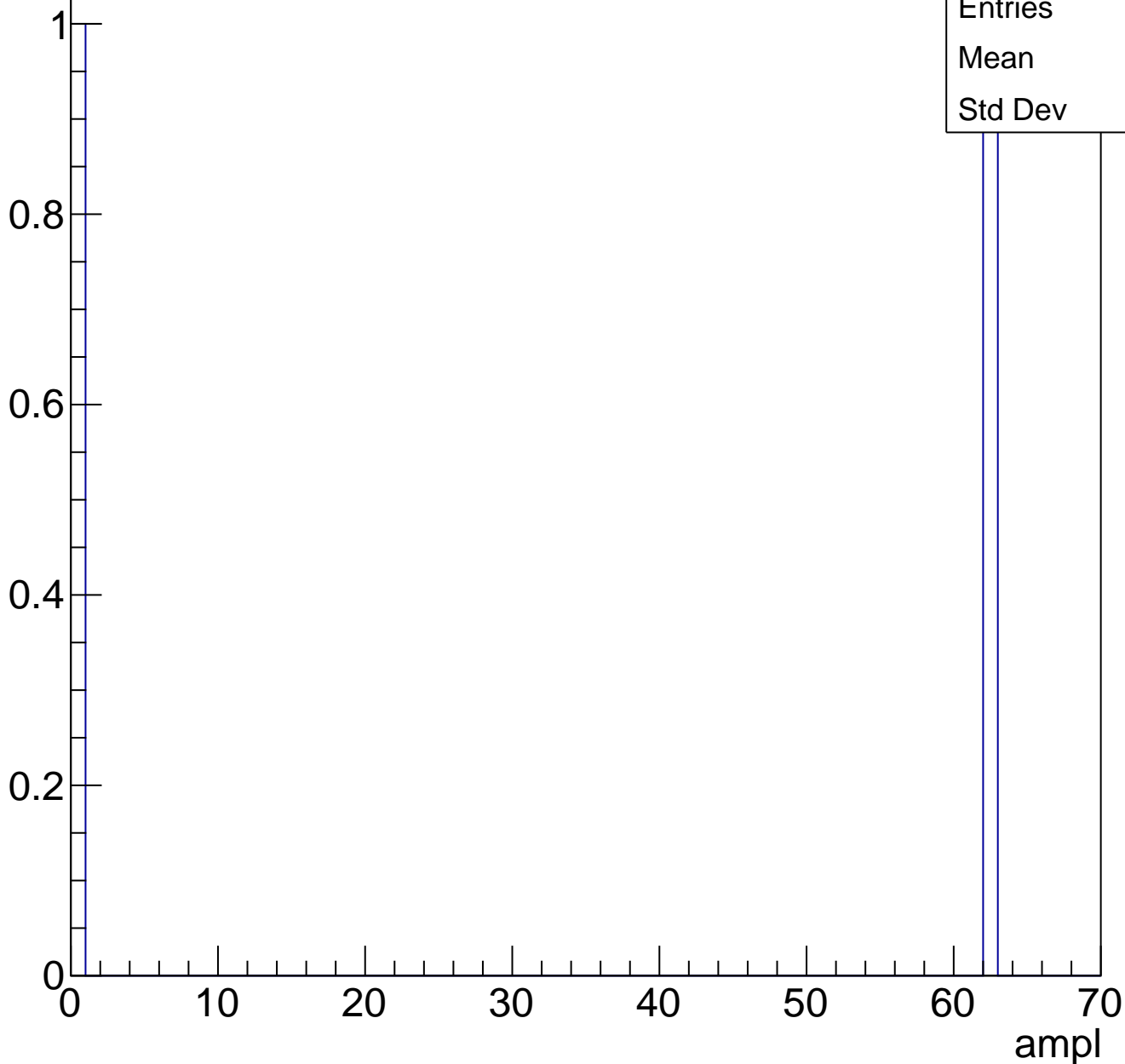
Entries	16
Mean	61.31
Std Dev	1.488



B1L102S, U17-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch31, adc0

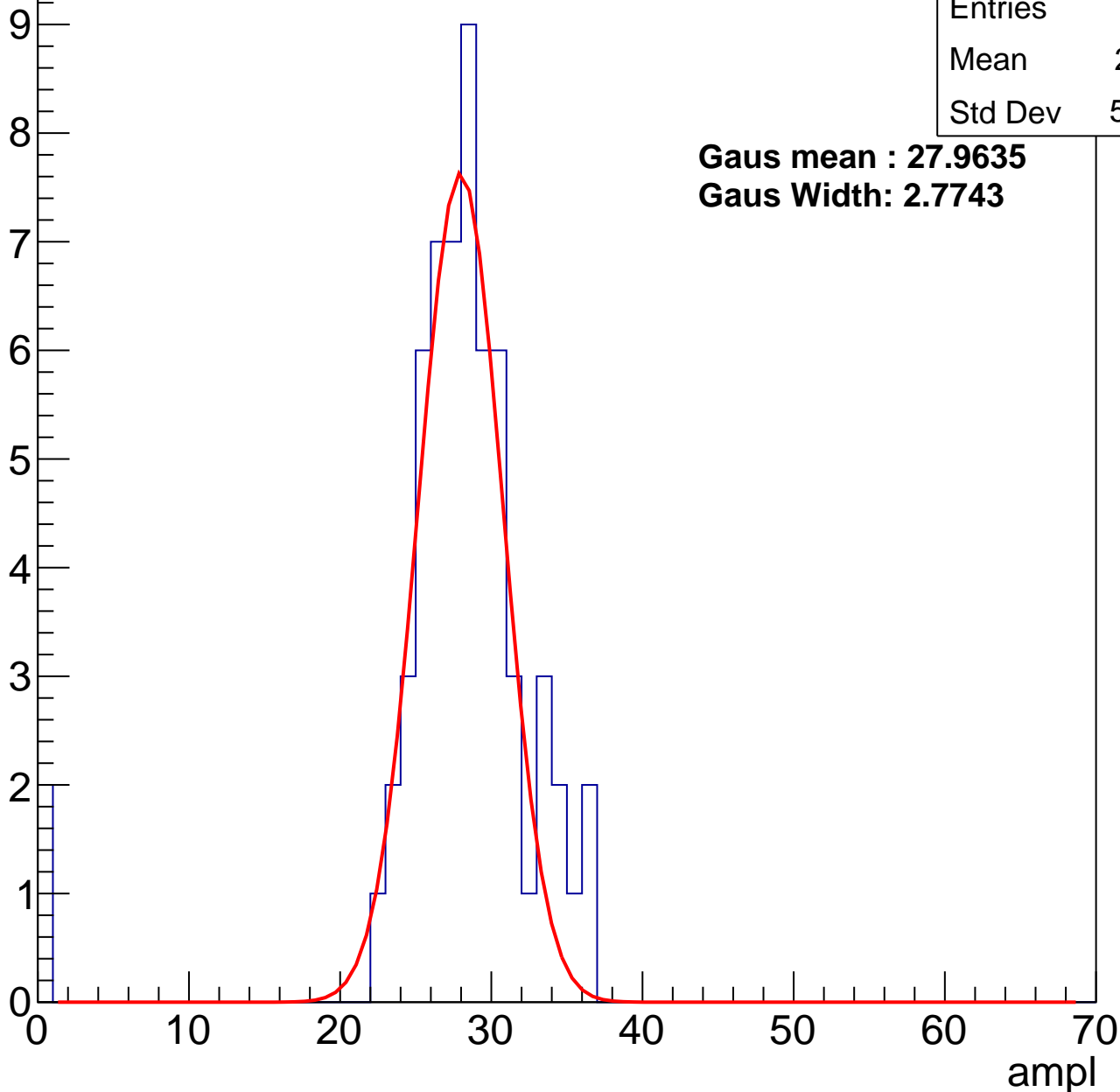
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.31
Std Dev	5.949

Gaus mean : 27.9635

Gaus Width: 2.7743



B1L102S, U17-ch31, adc1

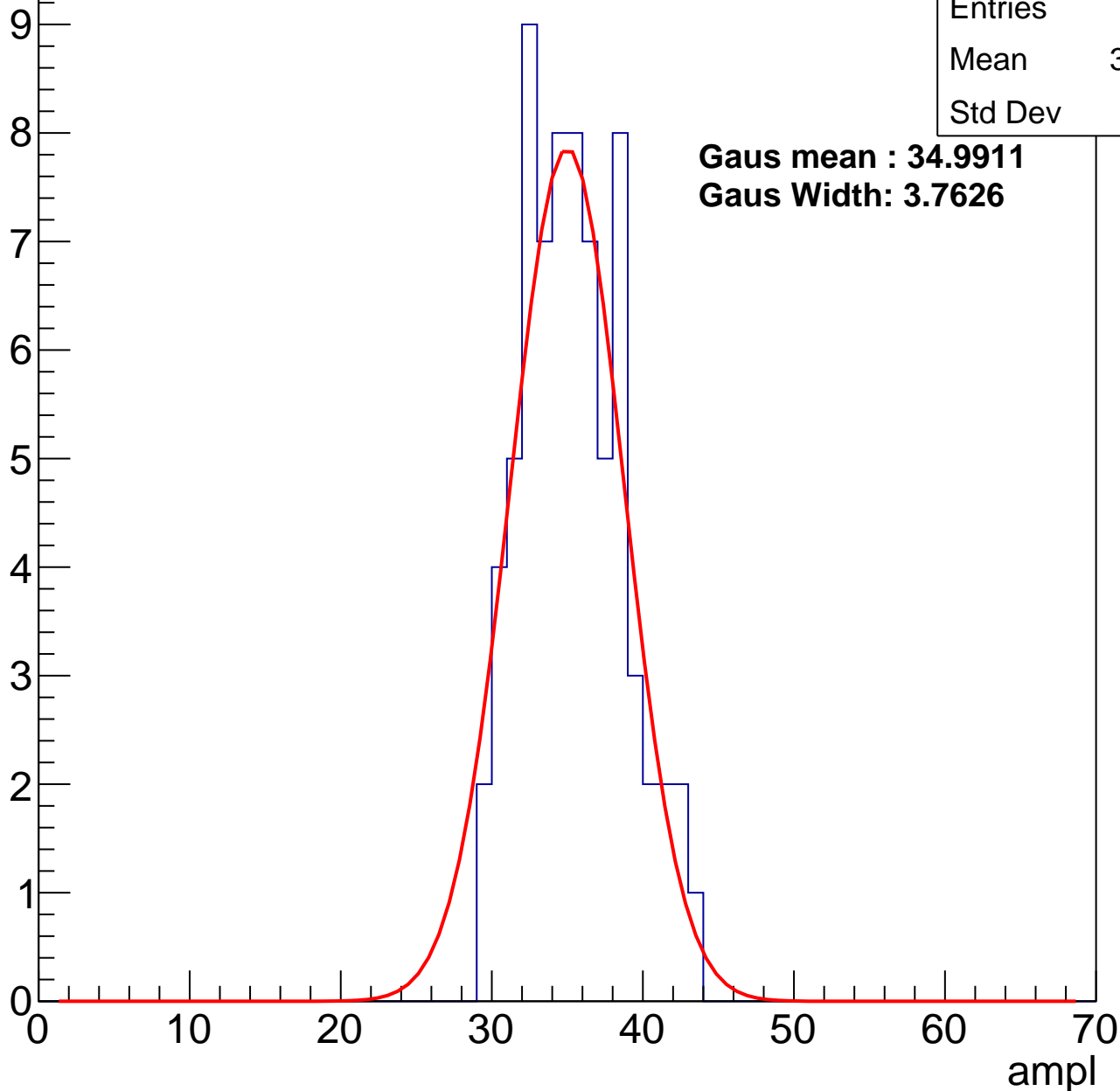
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.95
Std Dev	3.31

Gaus mean : 34.9911

Gaus Width: 3.7626



B1L102S, U17-ch31, adc2

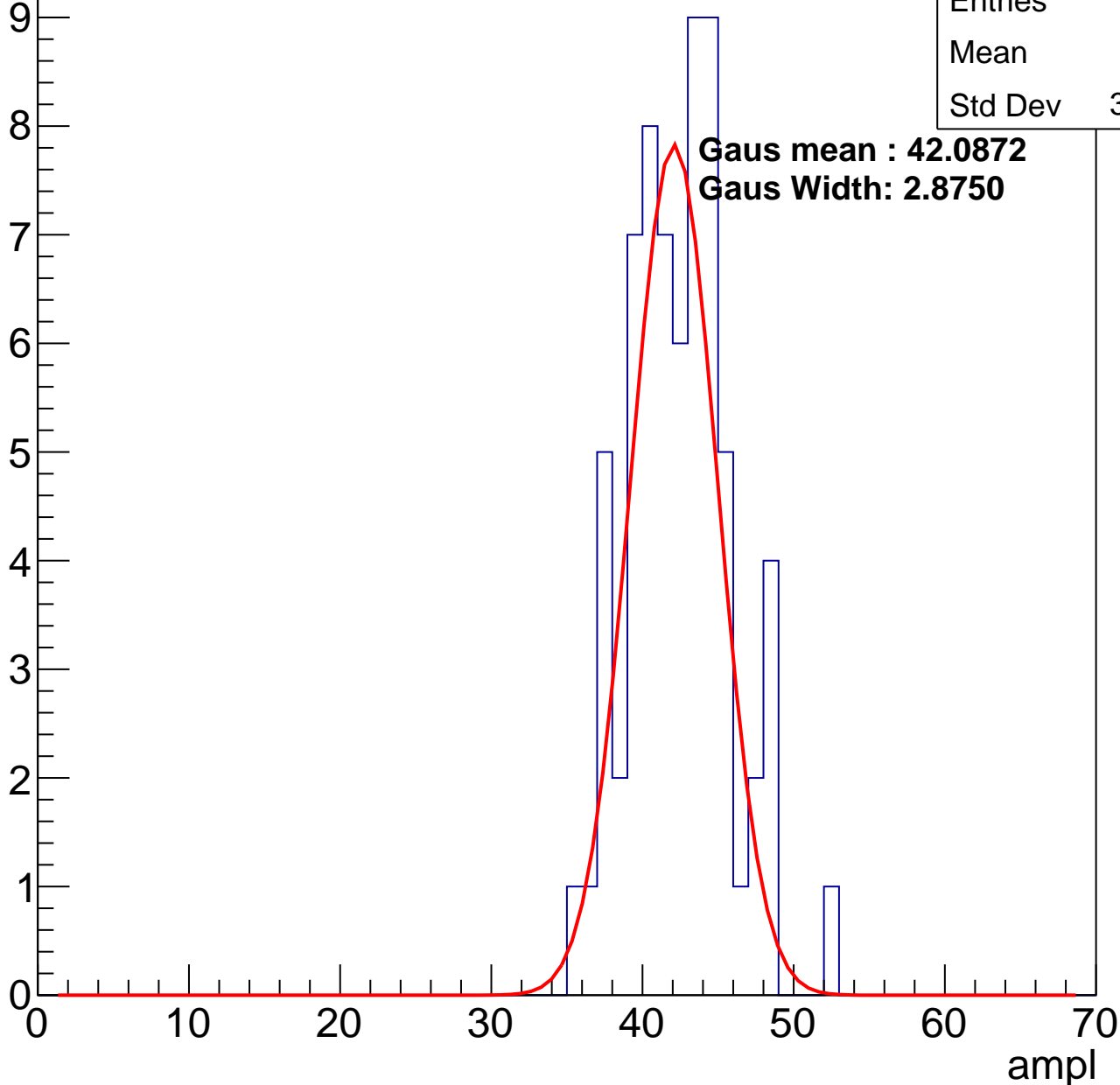
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42
Std Dev	3.308

Gaus mean : 42.0872

Gaus Width: 2.8750



B1L102S, U17-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

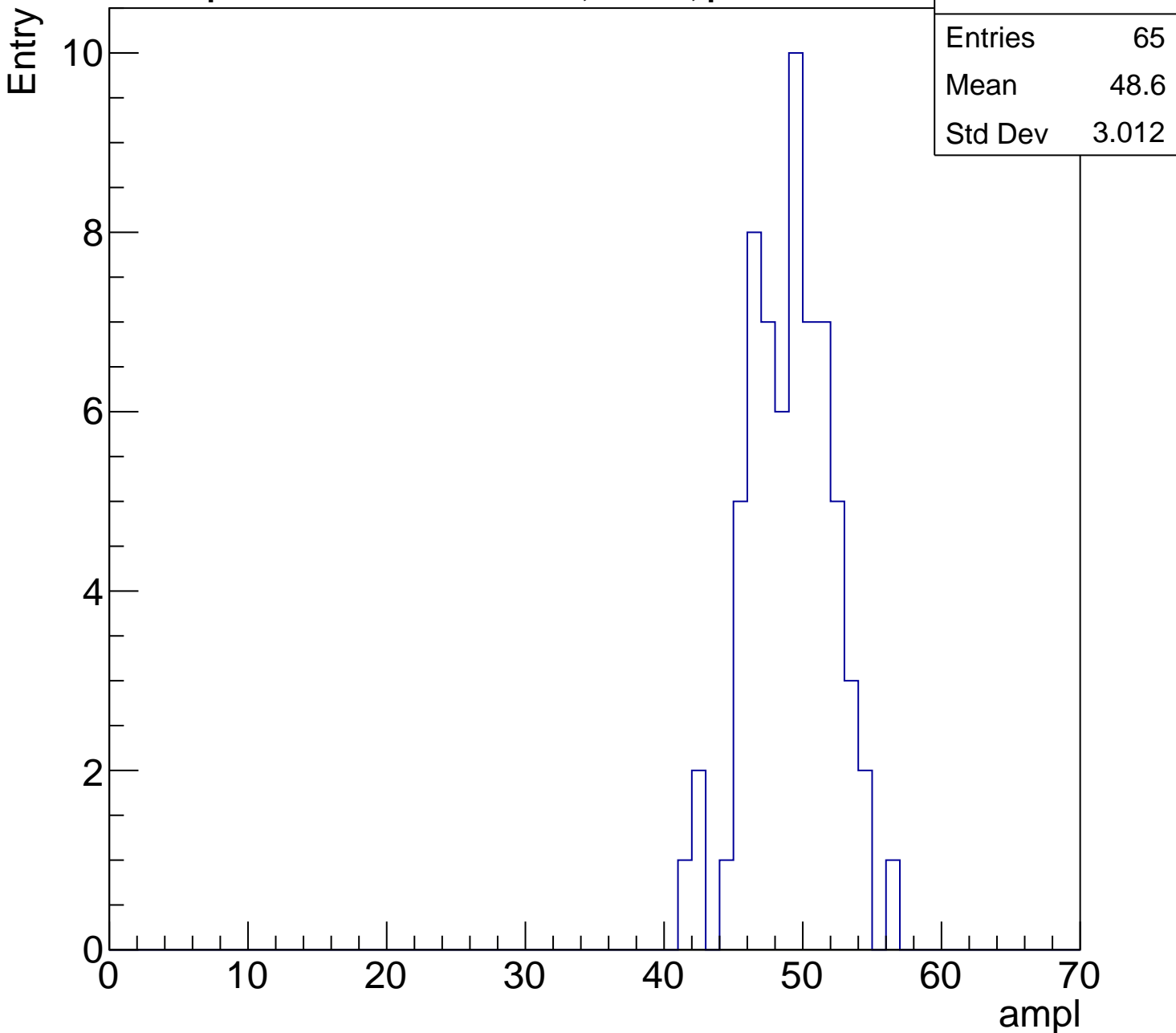
Entries	65
Mean	48.6
Std Dev	3.012

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U17-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	55.05
Std Dev	3.202

Entry

10

8

6

4

2

0

0

10

20

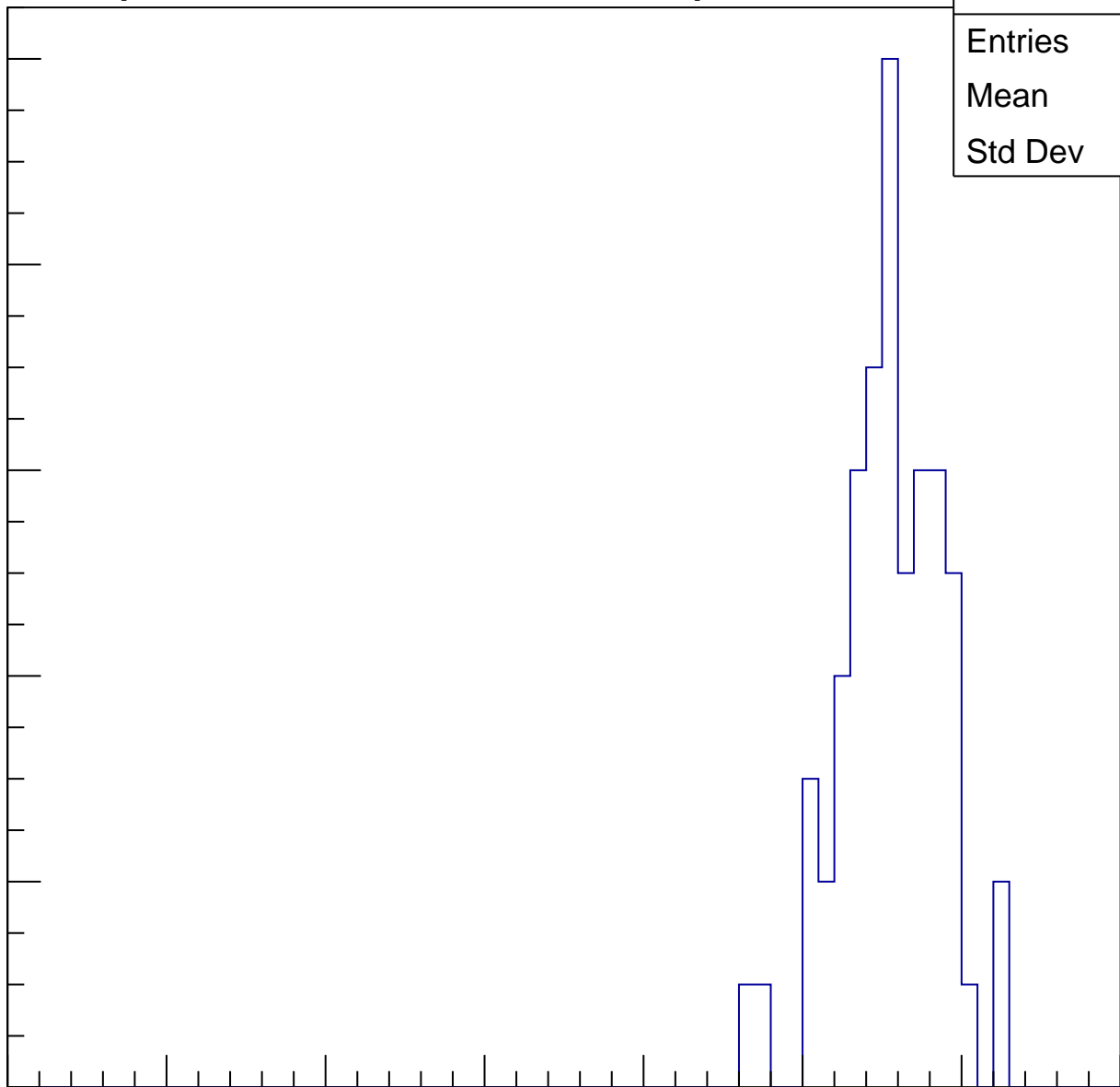
30

40

50

60

ampl

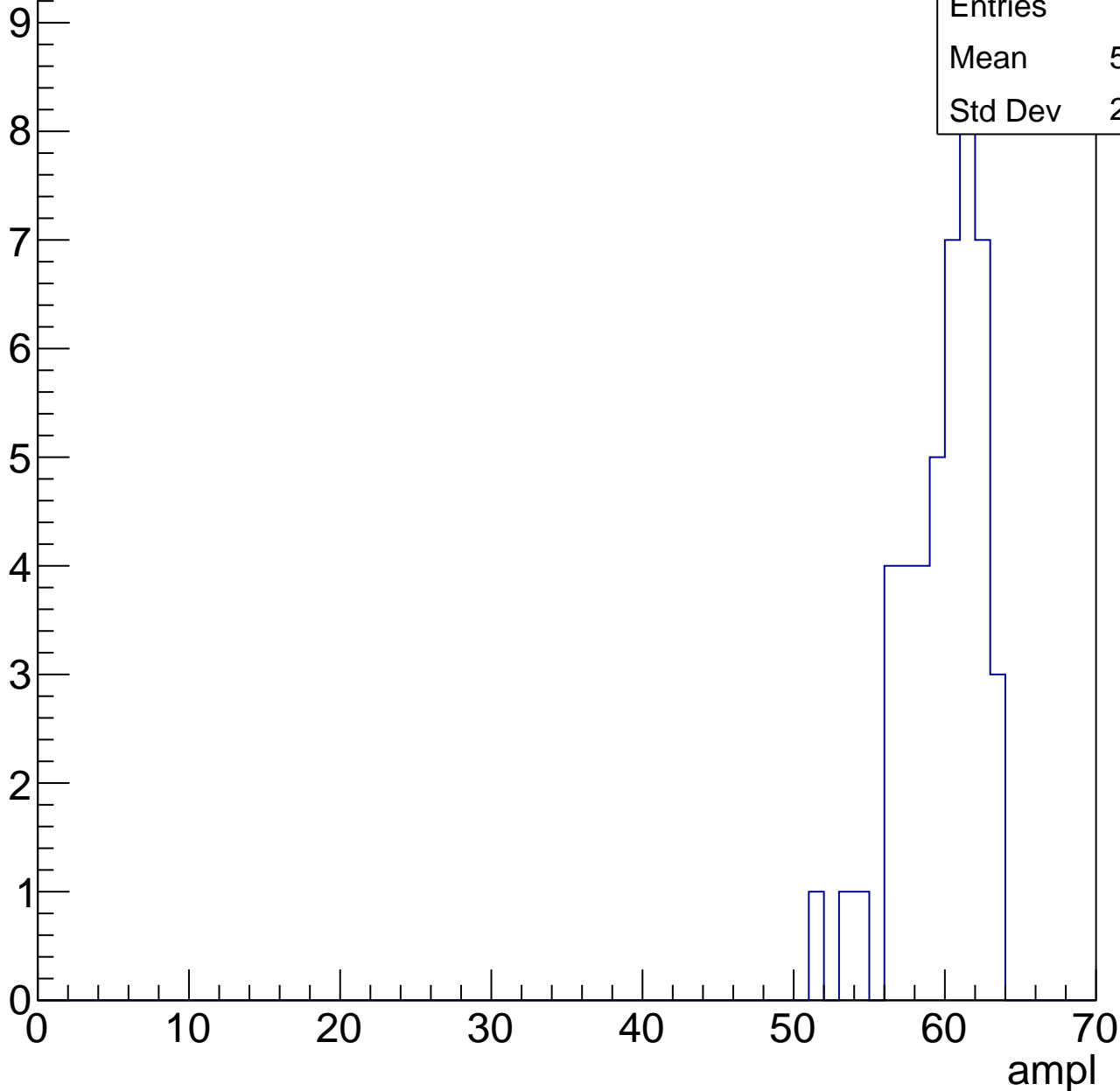


B1L102S, U17-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.33
Std Dev	2.679

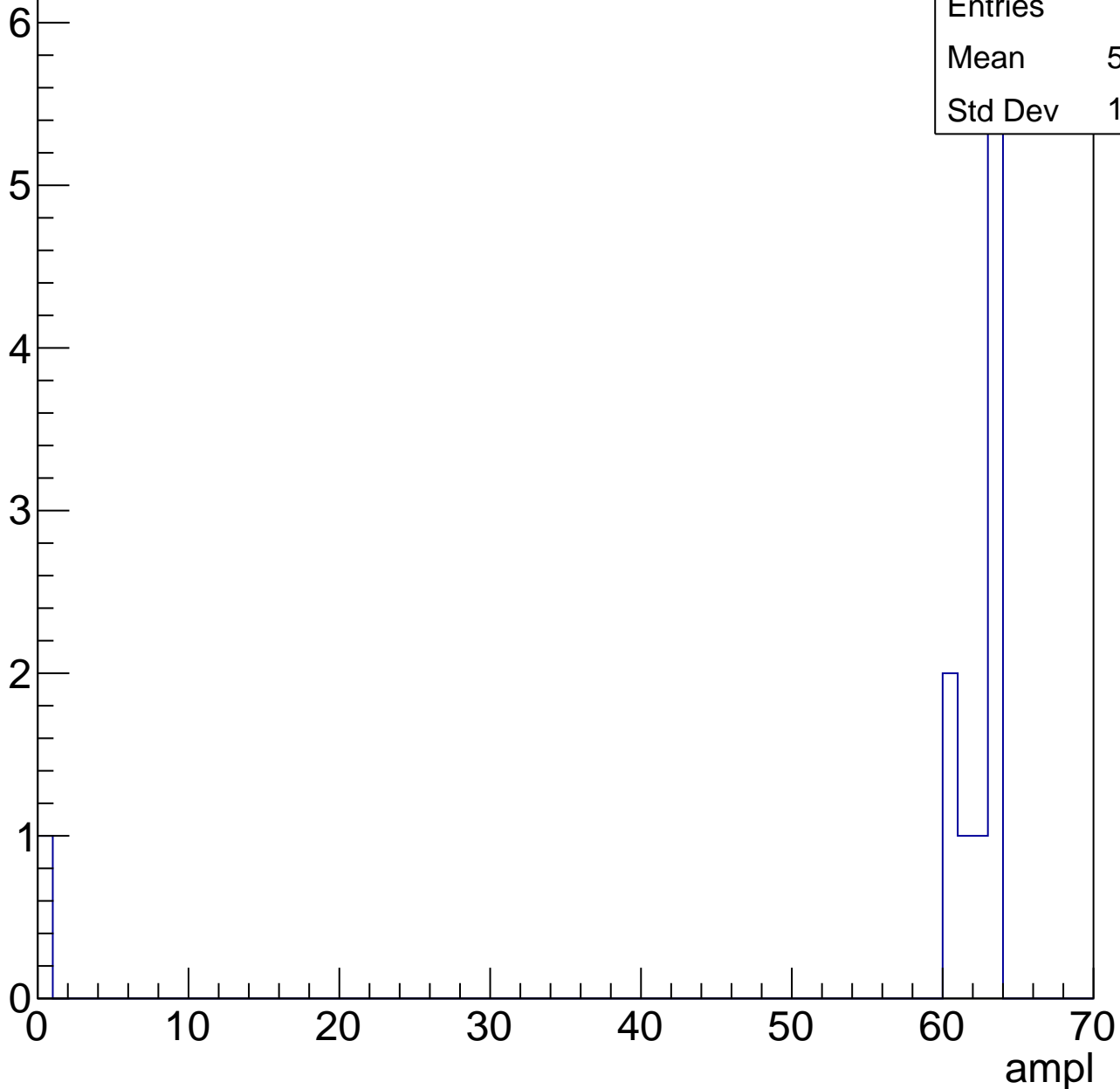


B1L102S, U17-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	56.45
Std Dev	17.89



B1L102S, U17-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch32, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	30.03
Std Dev	4.947

Gaus mean : 30.8353

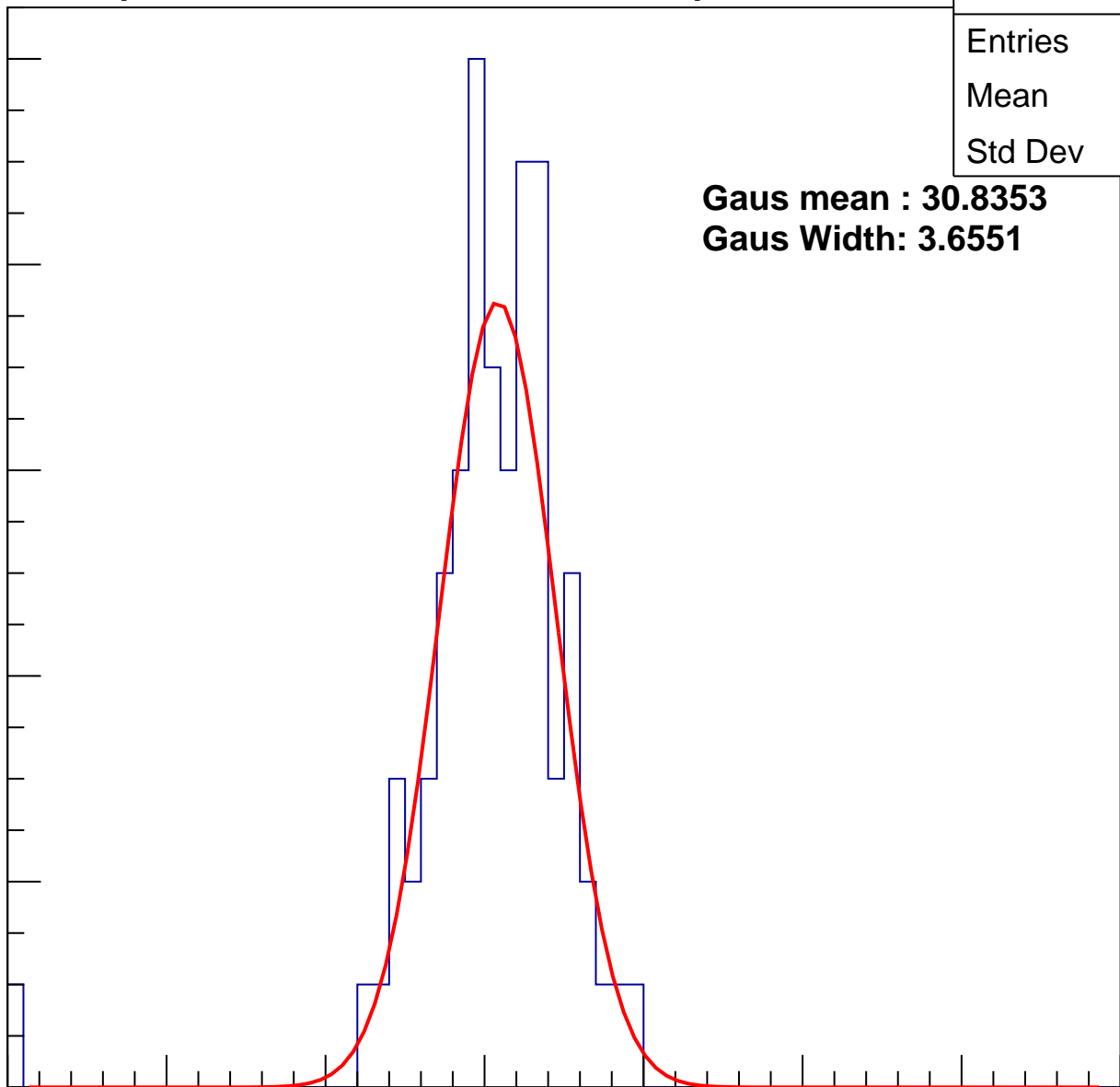
Gaus Width: 3.6551

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



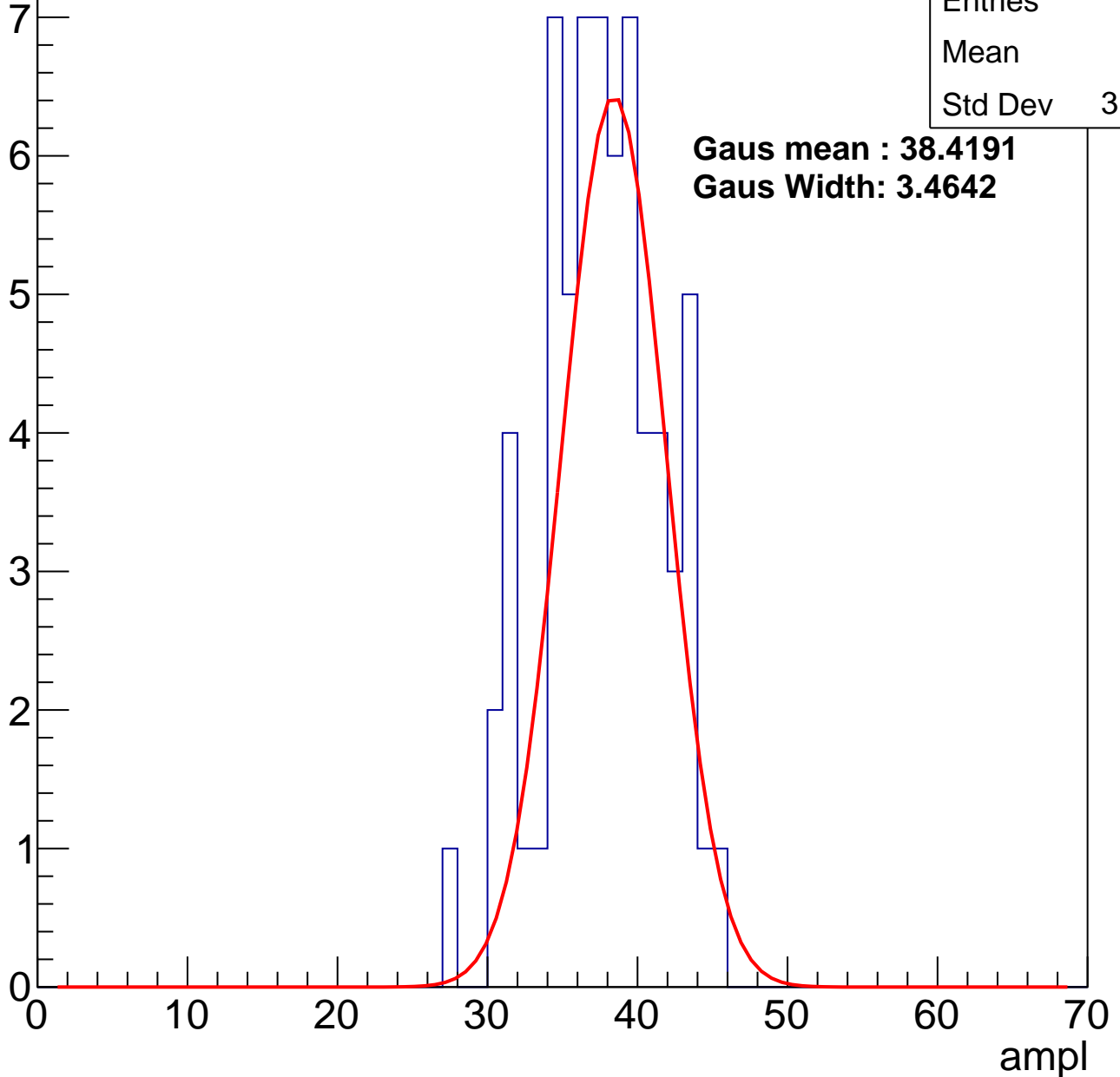
B1L102S, U17-ch32, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	37.2
Std Dev	3.838

Gaus mean : 38.4191
Gaus Width: 3.4642



B1L102S, U17-ch32, adc2

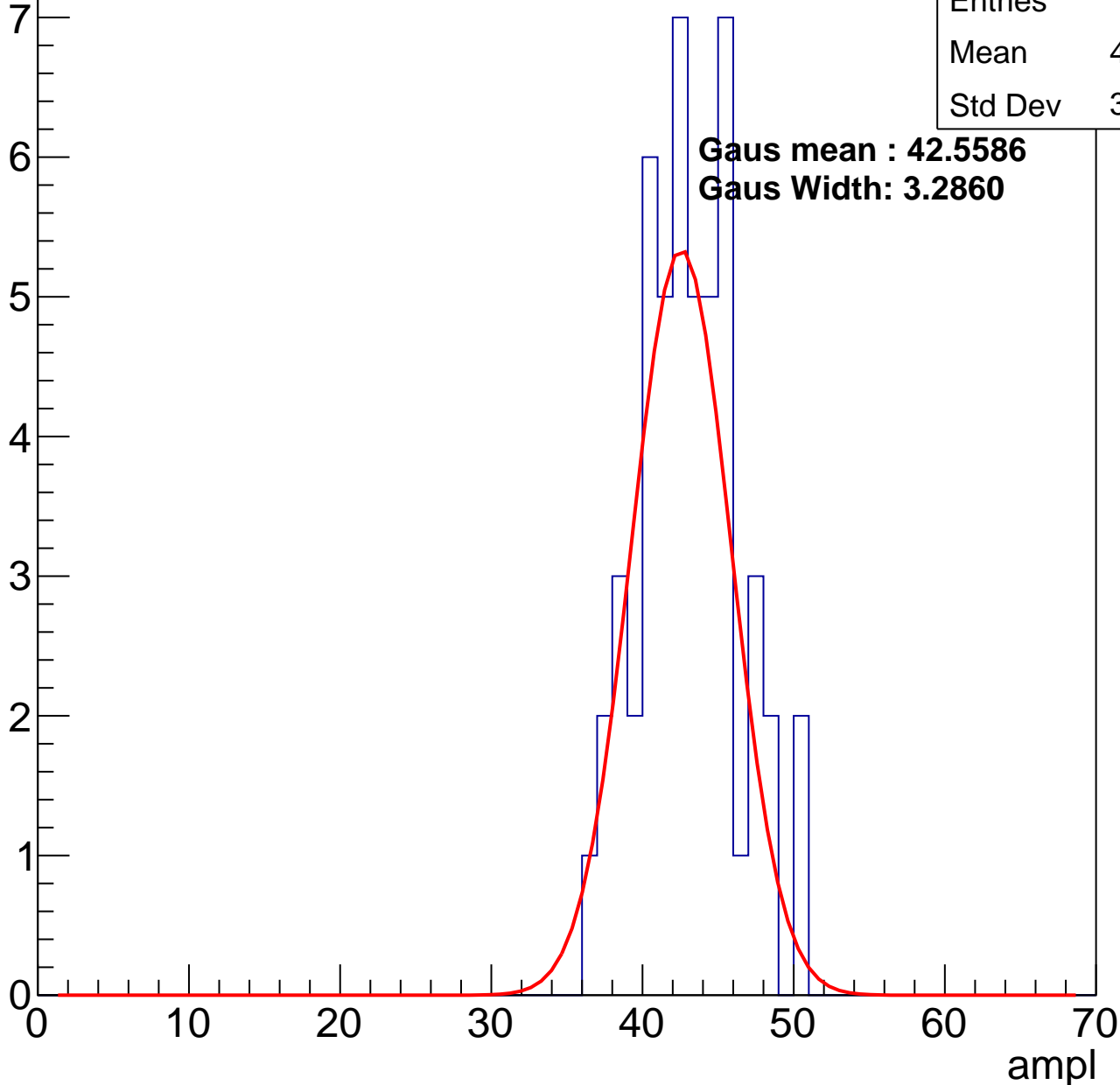
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.63
Std Dev	3.254

Gaus mean : 42.5586

Gaus Width: 3.2860

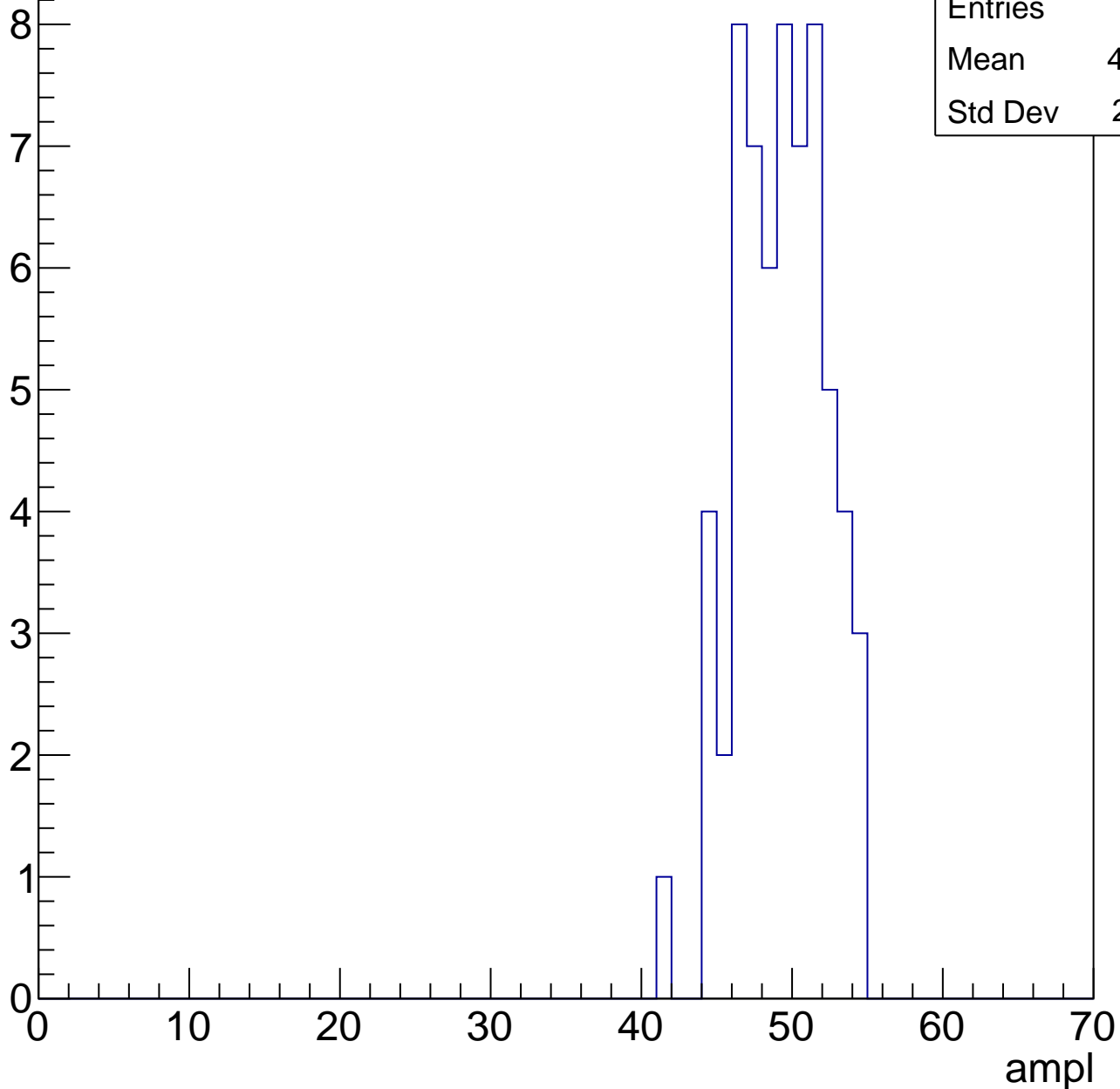


B1L102S, U17-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.83
Std Dev	2.881

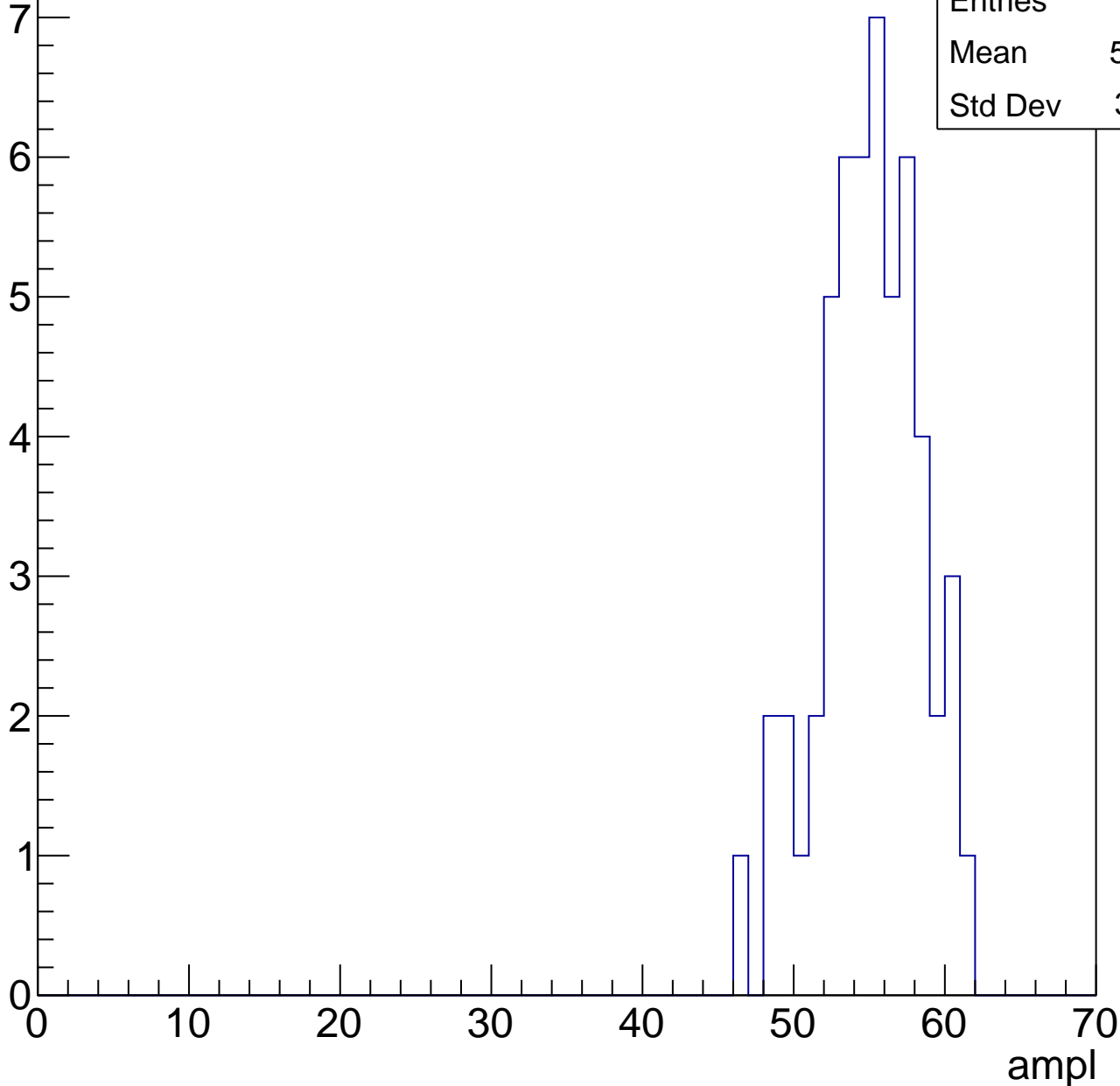


B1L102S, U17-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

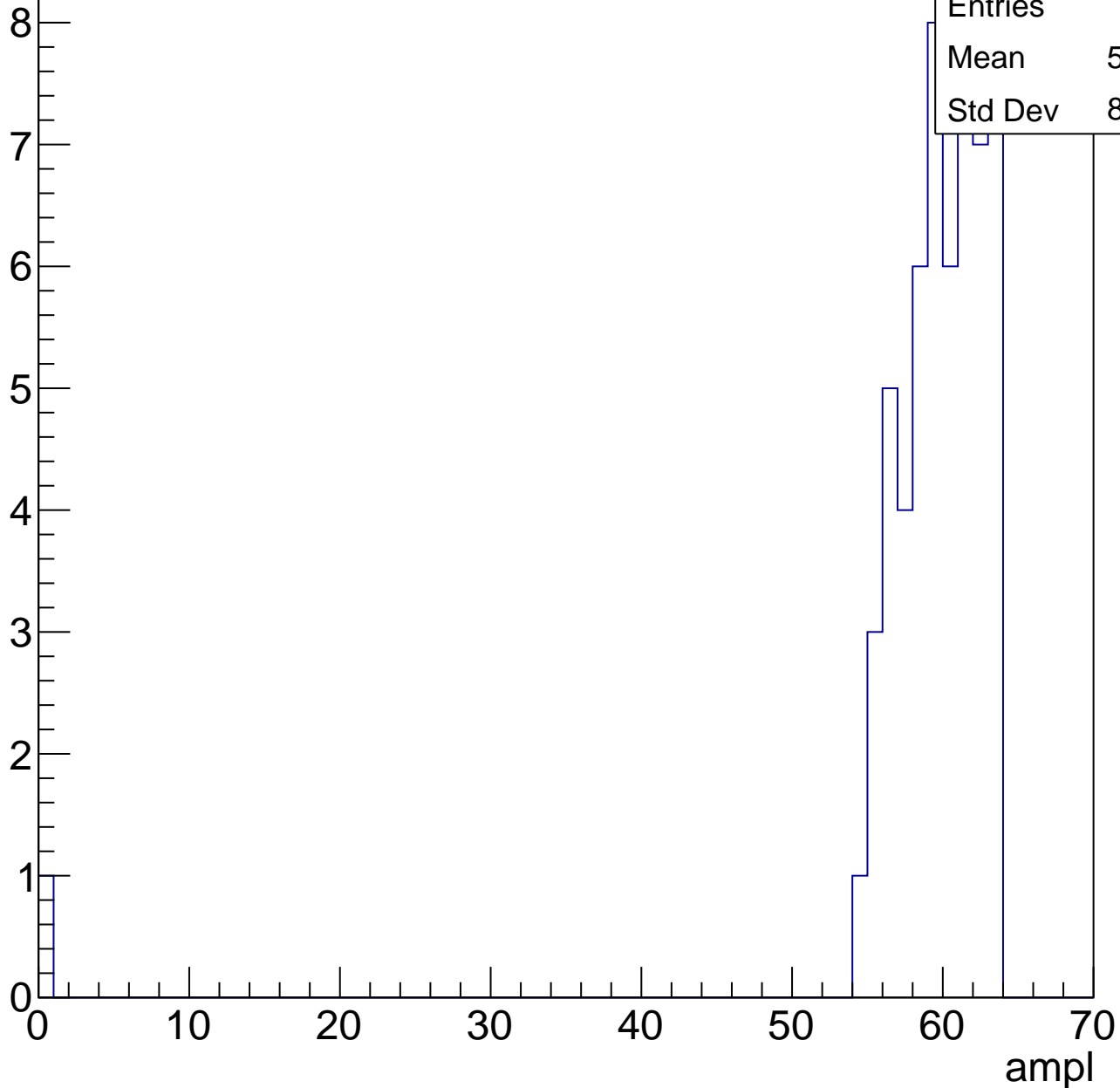
Entries	53
Mean	54.57
Std Dev	3.311



B1L102S, U17-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch33, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	26.67
Std Dev	5.365

Gaus mean : 27.8443

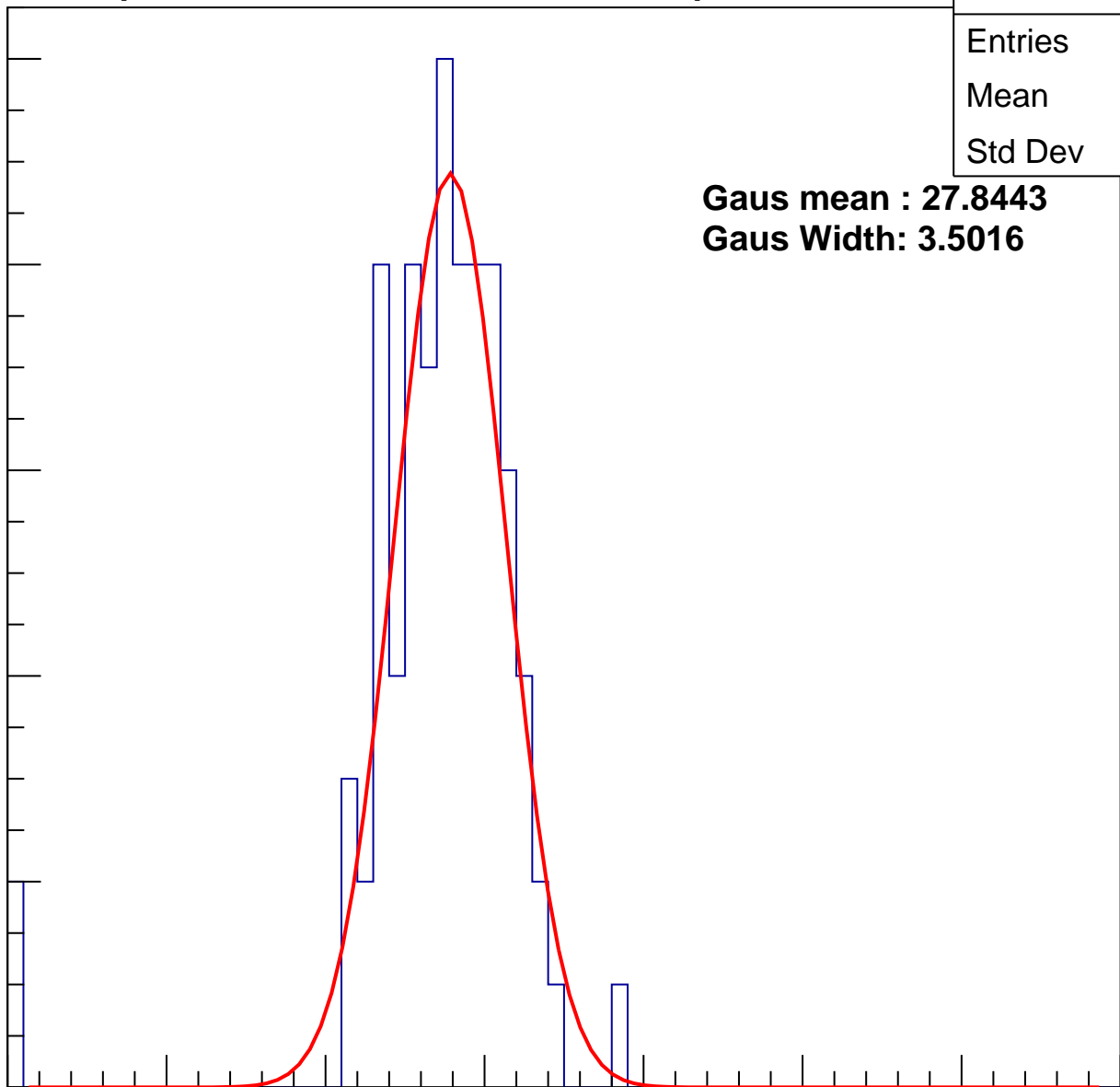
Gaus Width: 3.5016

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch33, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	35.03
Std Dev	3.073

Gaus mean : 35.2869

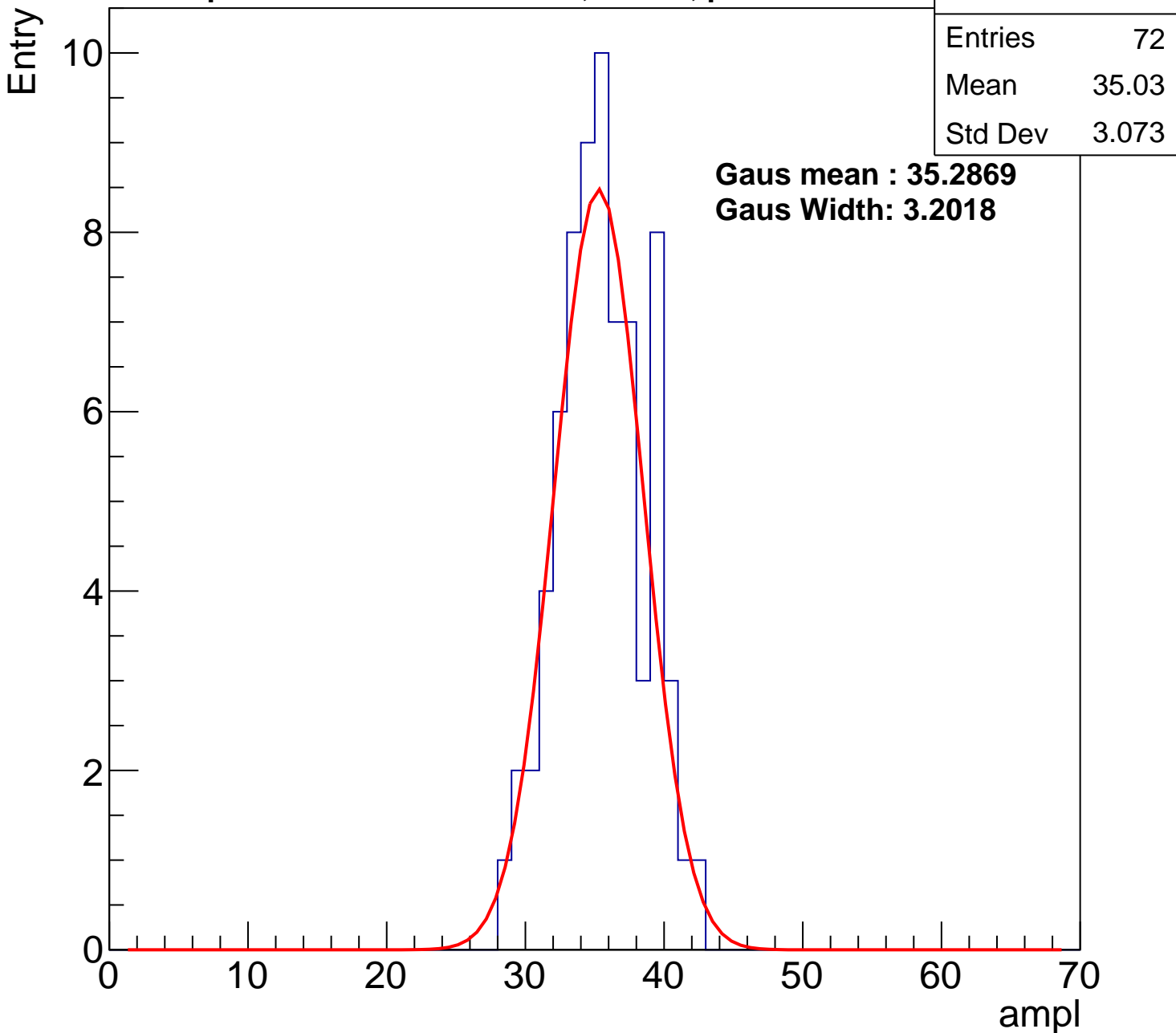
Gaus Width: 3.2018

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch33, adc2

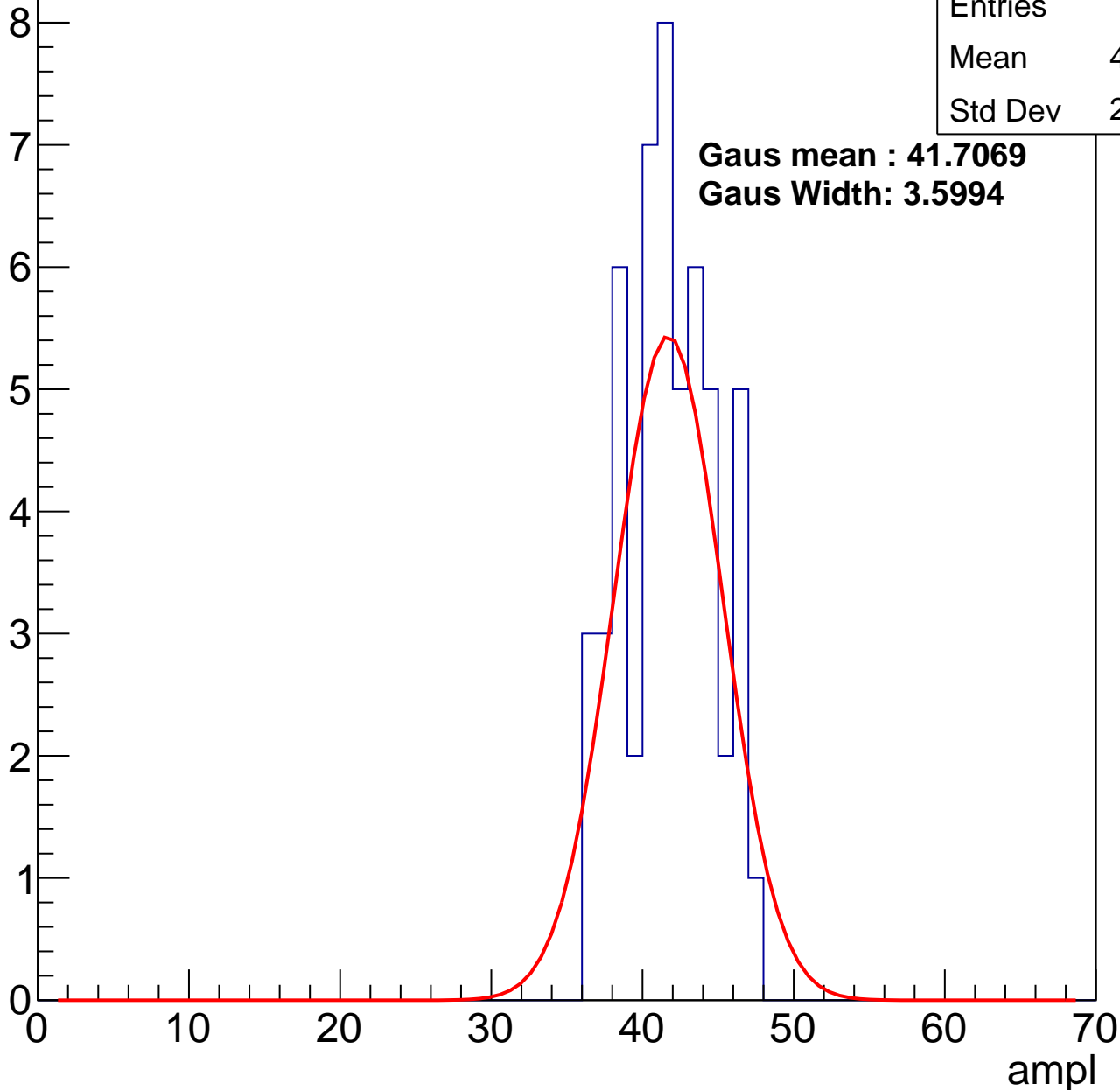
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.28
Std Dev	2.929

Gaus mean : 41.7069

Gaus Width: 3.5994

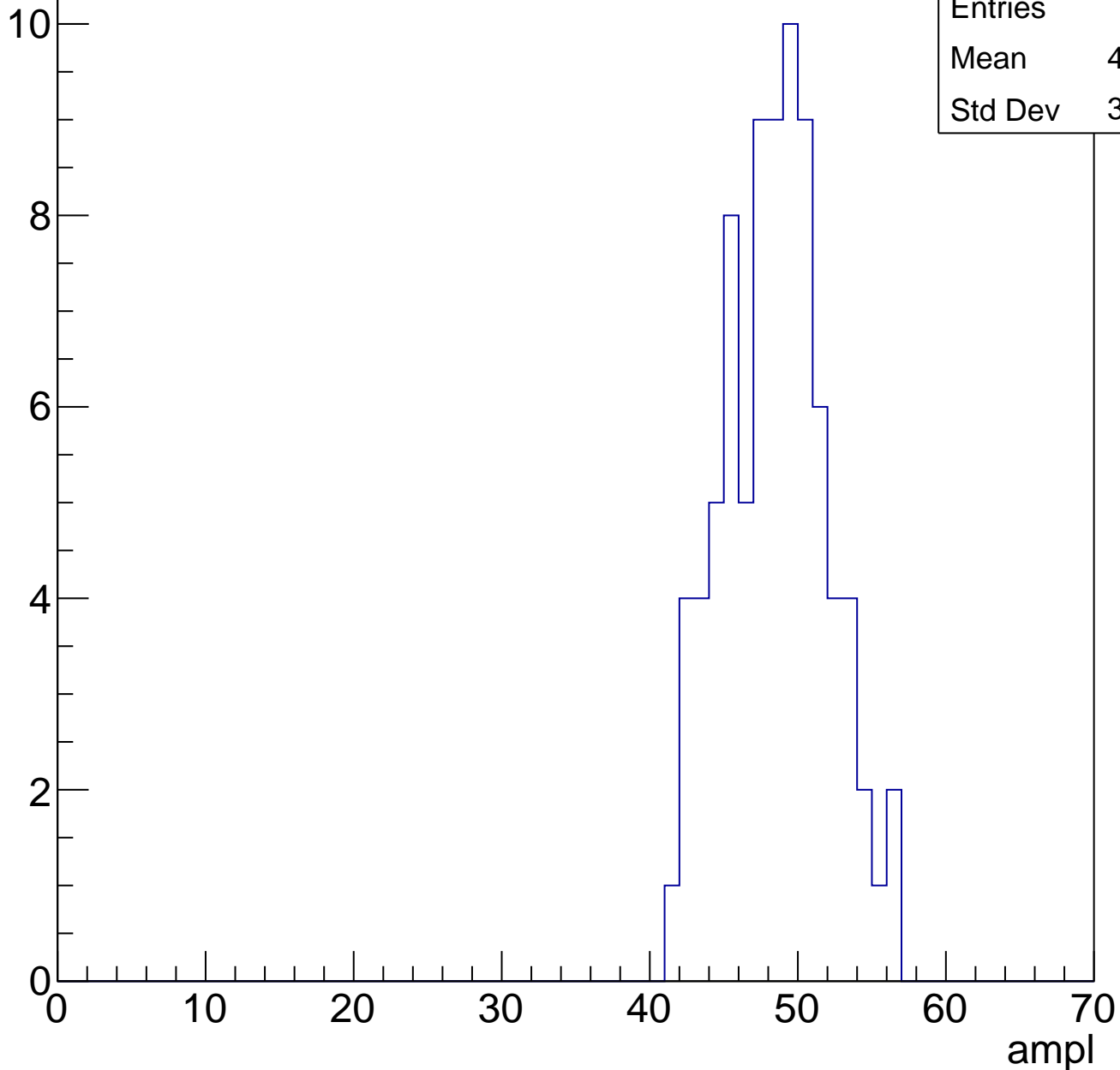


B1L102S, U17-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	48.04
Std Dev	3.448

Entry

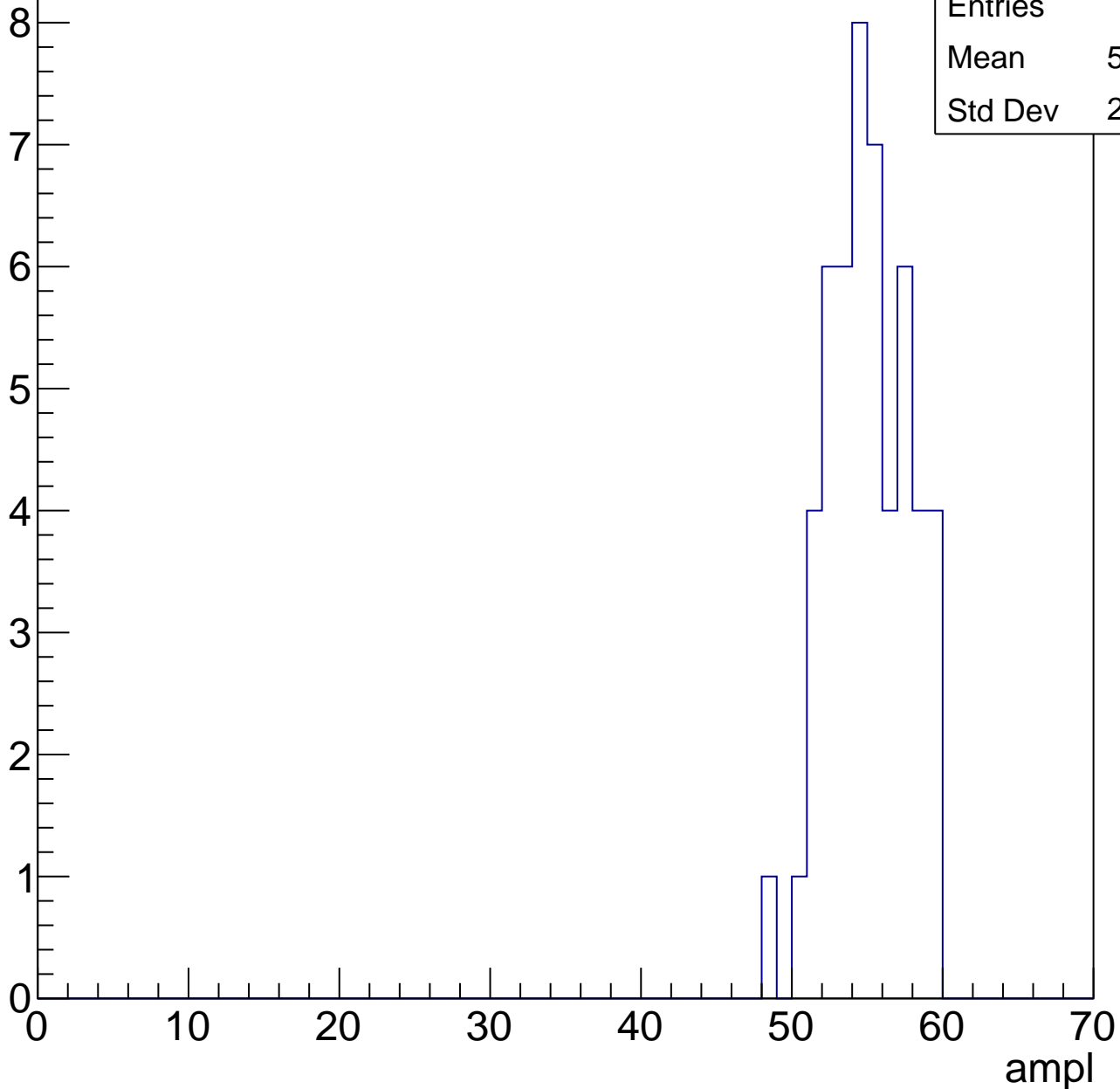


B1L102S, U17-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.57
Std Dev	2.592

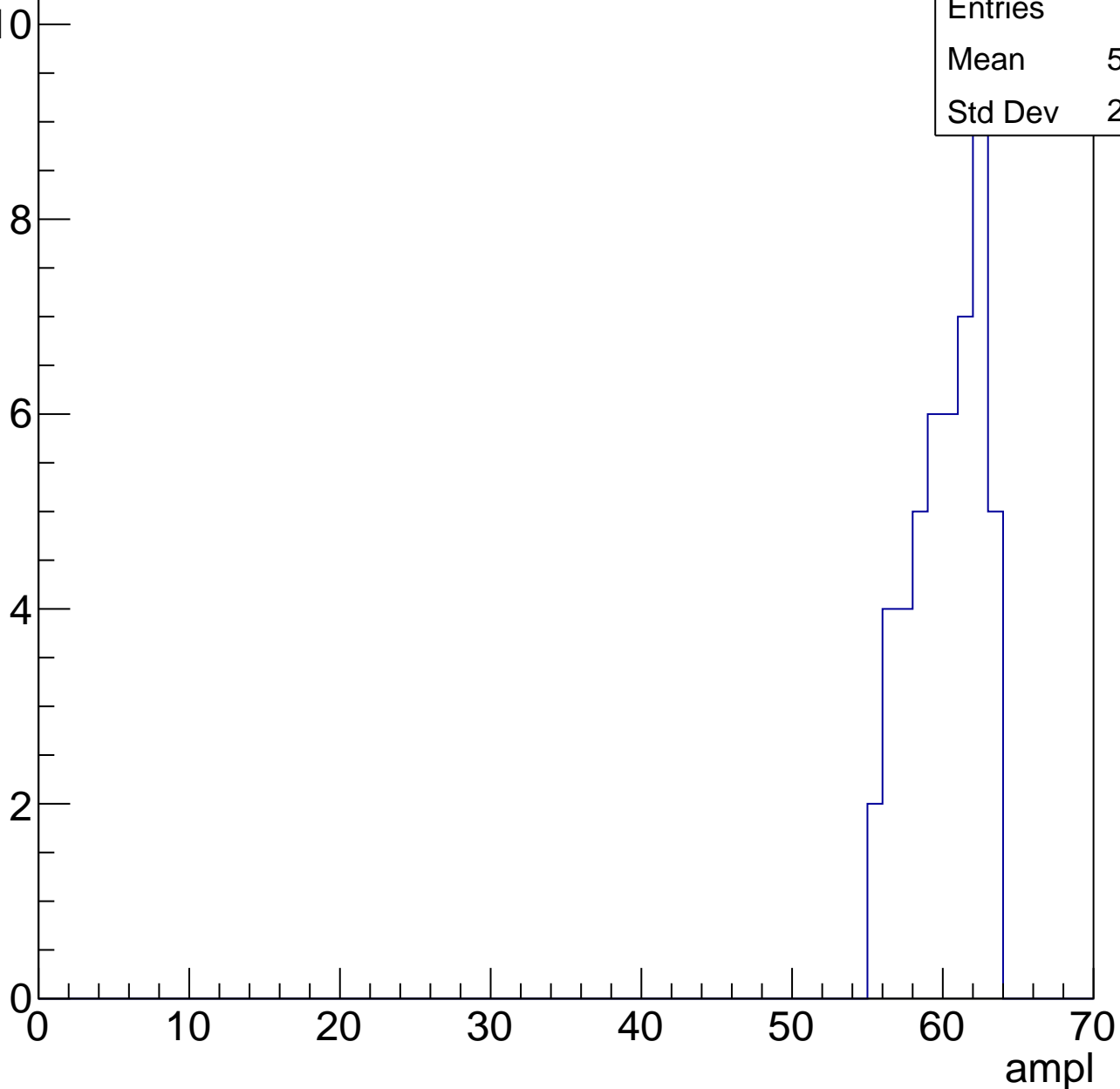


B1L102S, U17-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59.76
Std Dev	2.326

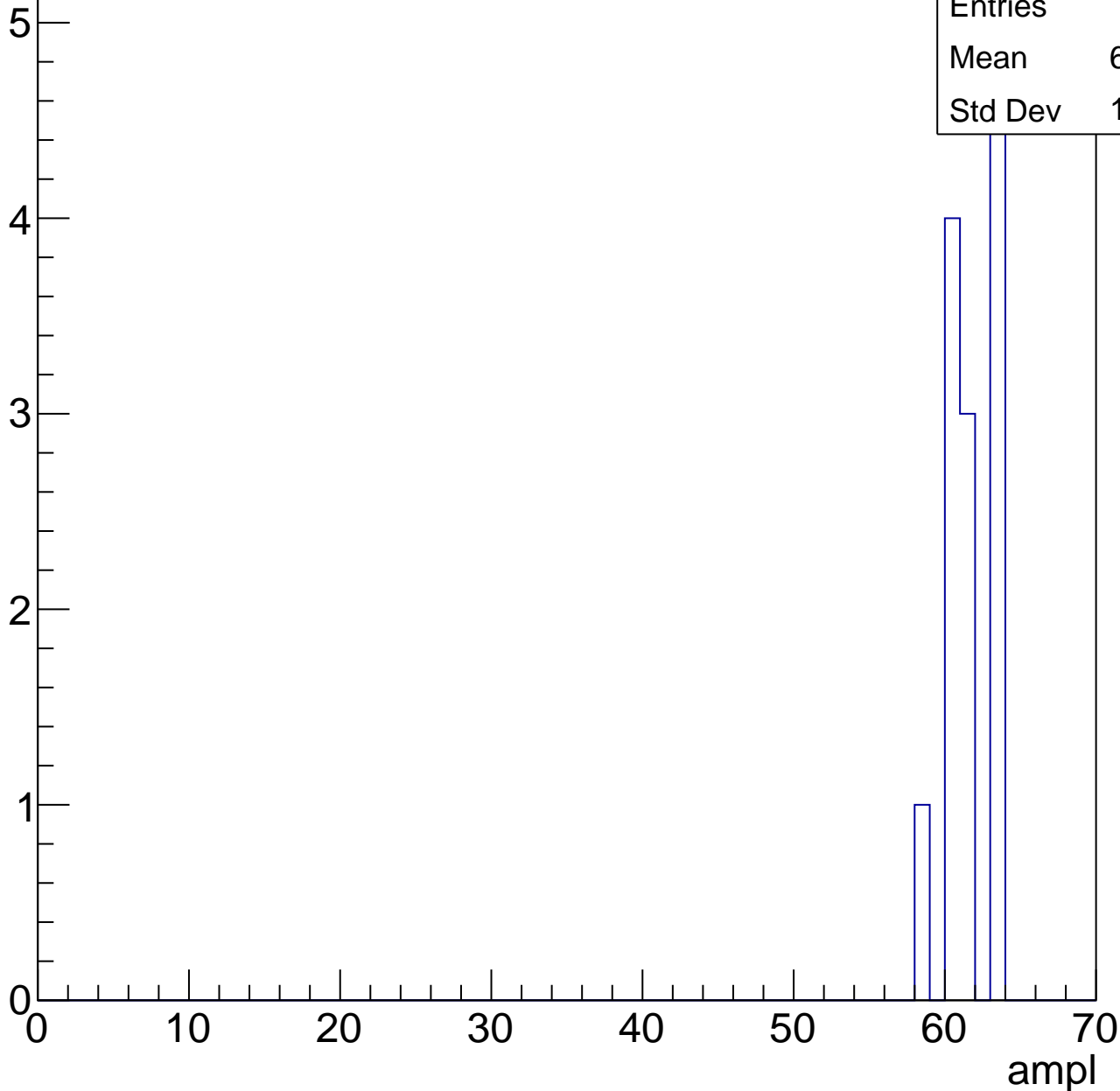


B1L102S, U17-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.23
Std Dev	1.576



B1L102S, U17-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch34, adc0

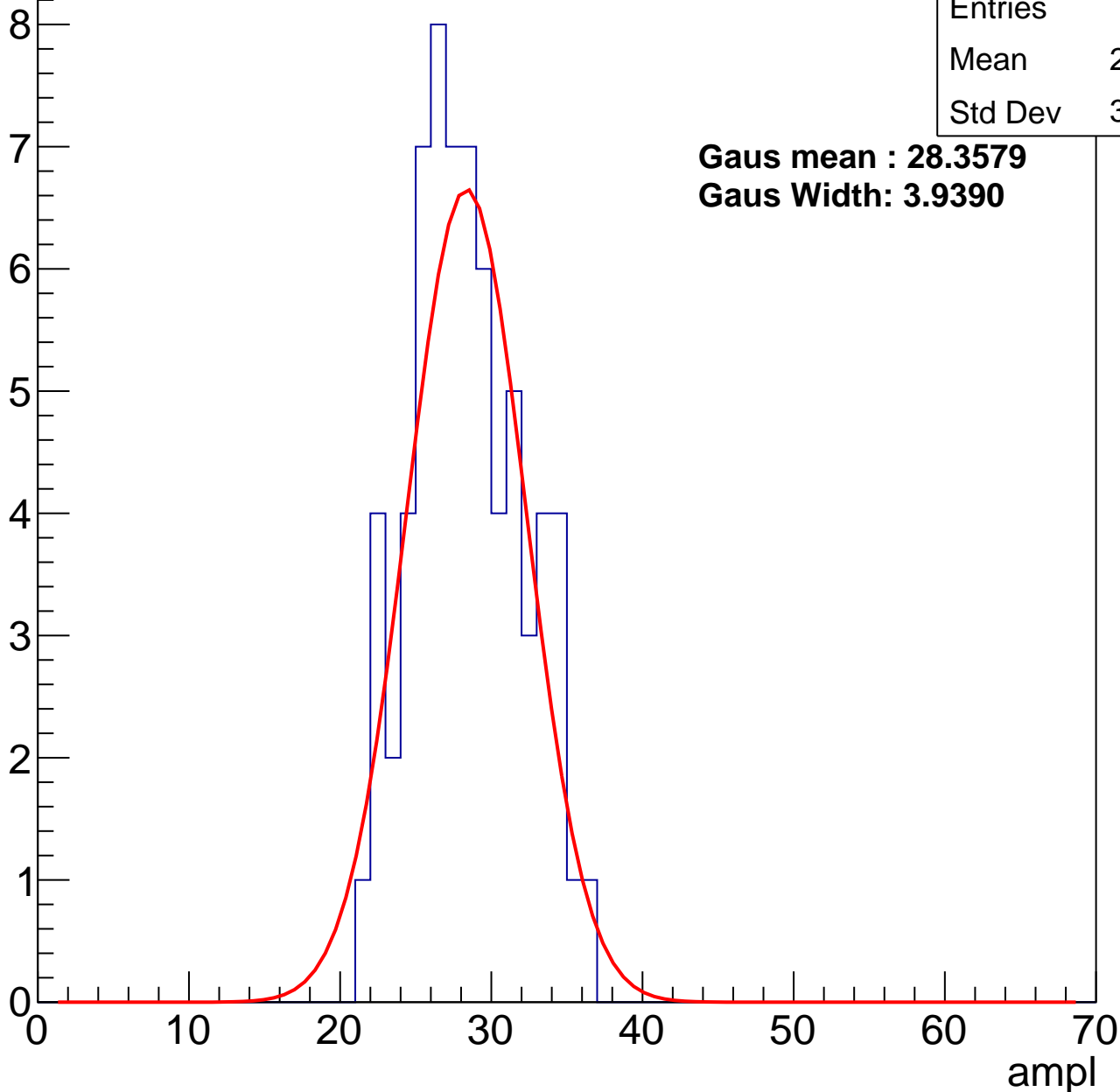
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.99
Std Dev	3.599

Gaus mean : 28.3579

Gaus Width: 3.9390



B1L102S, U17-ch34, adc1

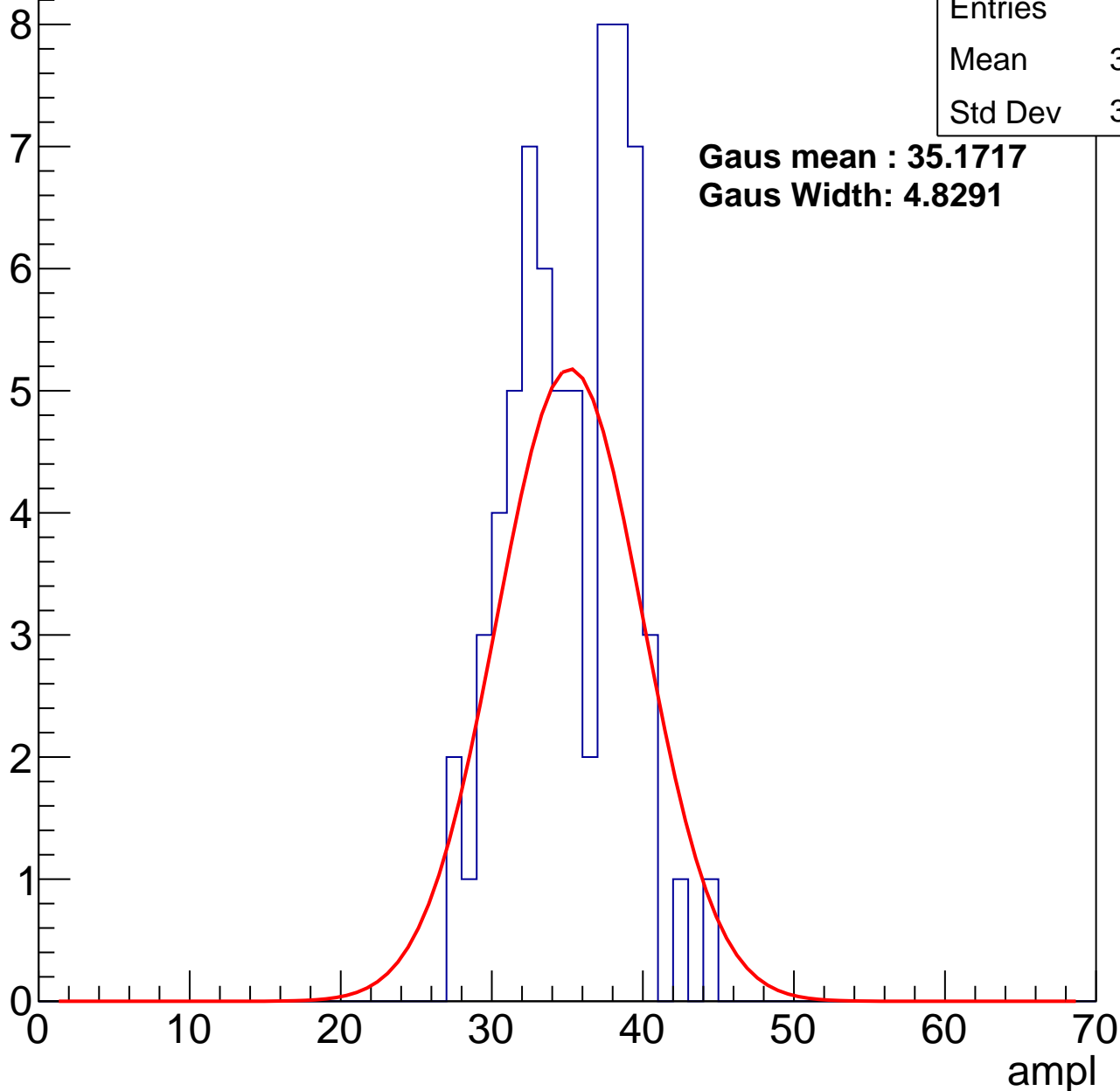
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.74
Std Dev	3.787

Gaus mean : 35.1717

Gaus Width: 4.8291



B1L102S, U17-ch34, adc2

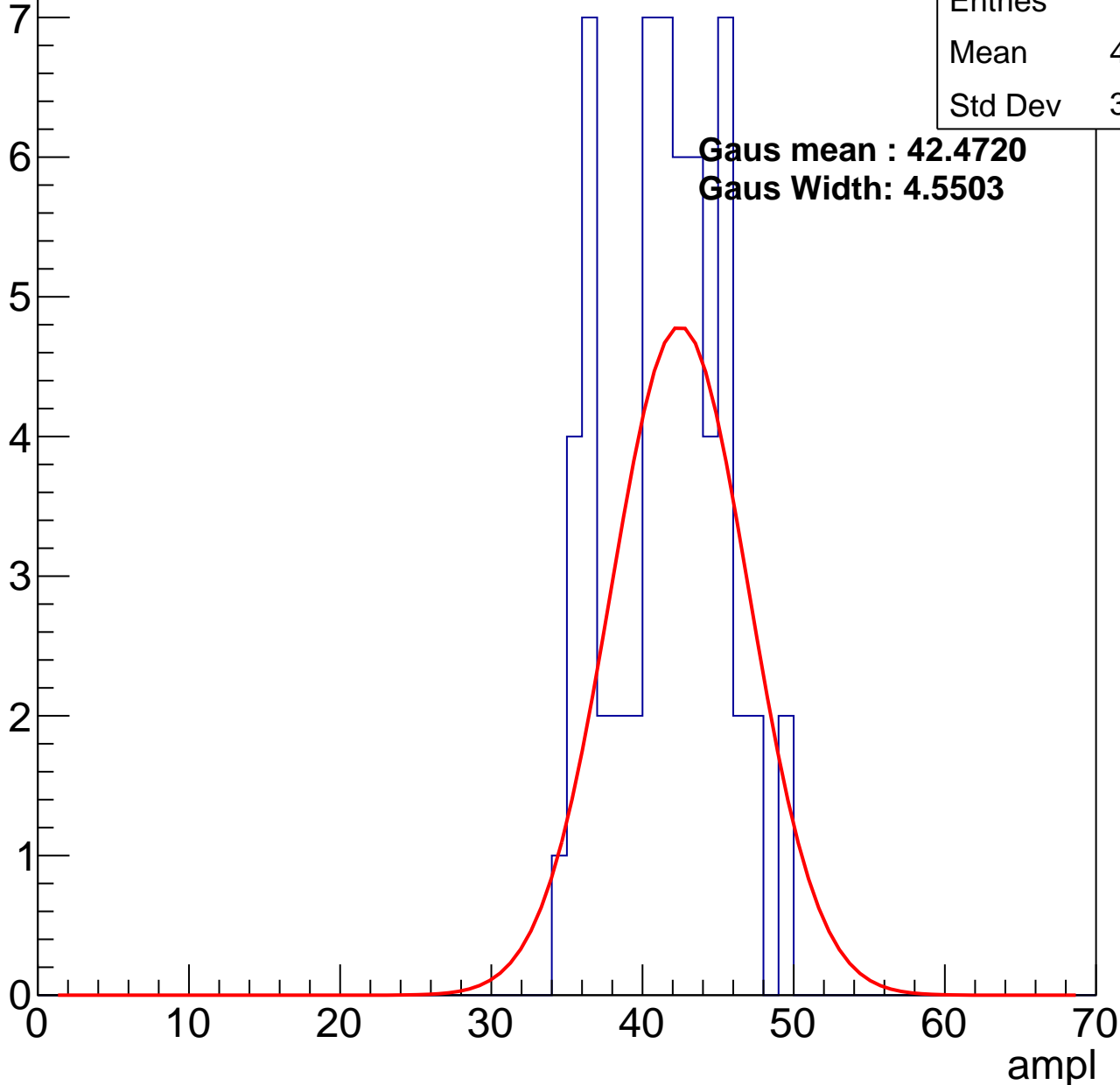
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.08
Std Dev	3.756

Gaus mean : 42.4720

Gaus Width: 4.5503

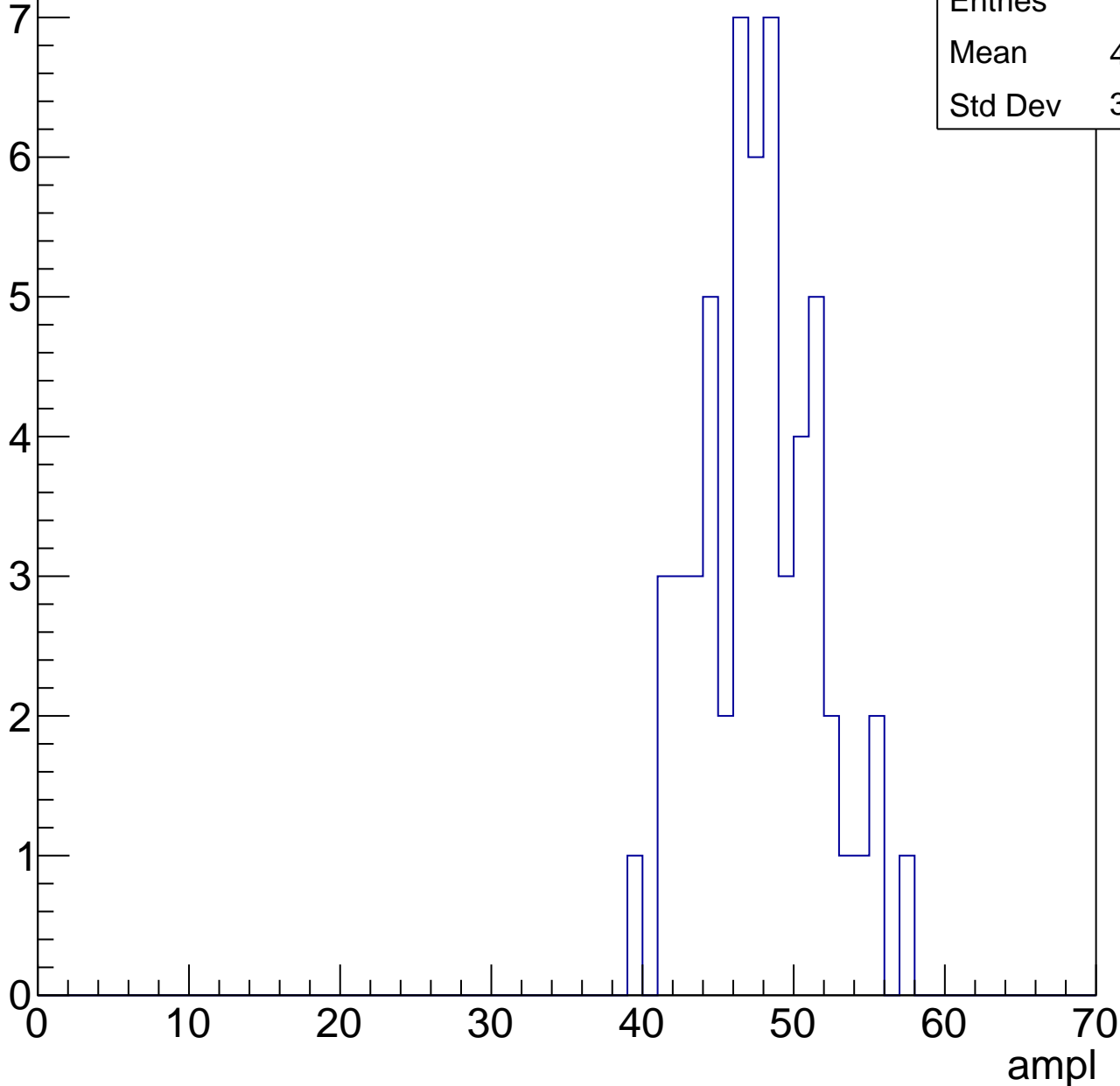


B1L102S, U17-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.27
Std Dev	3.894

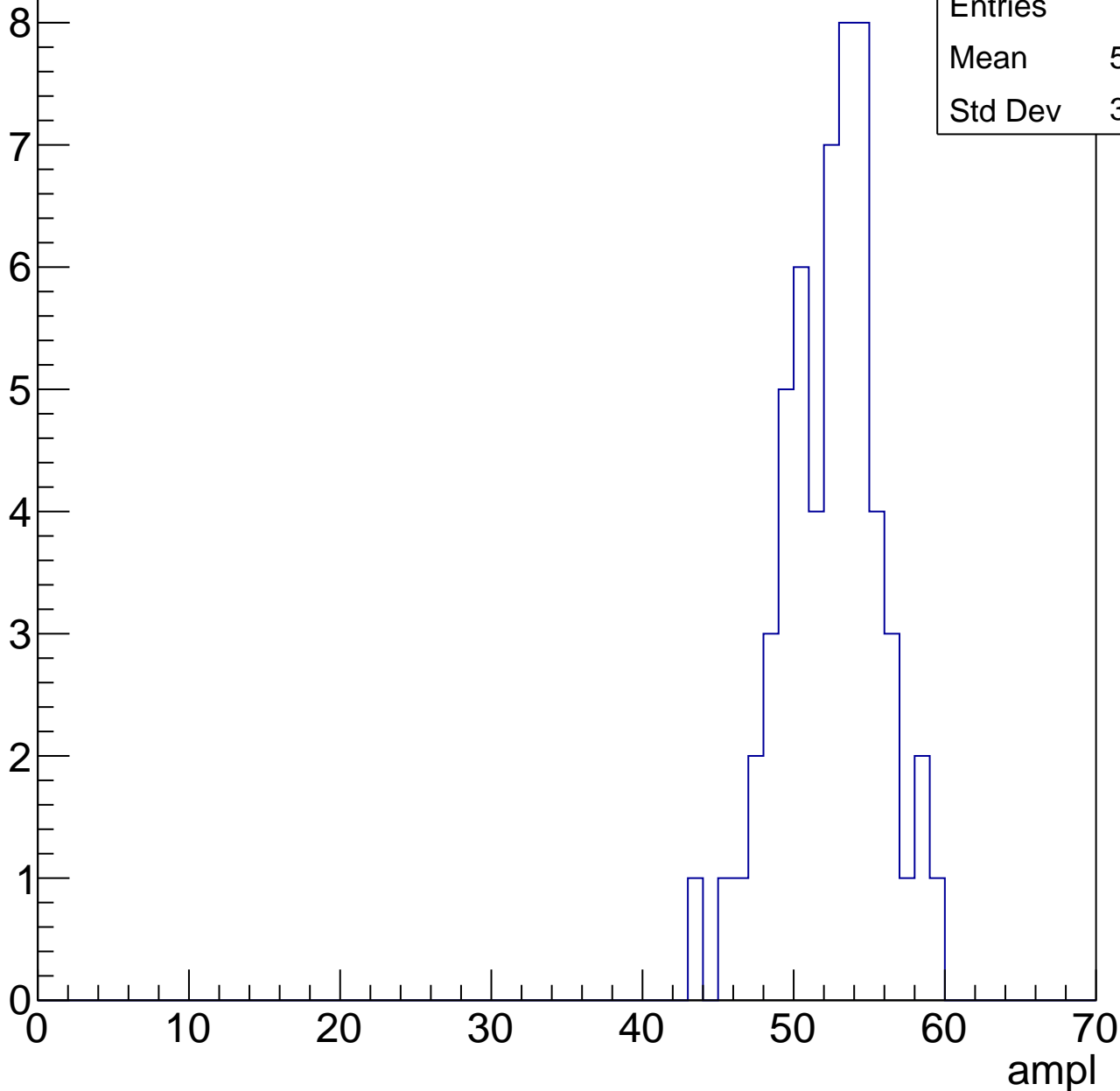


B1L102S, U17-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

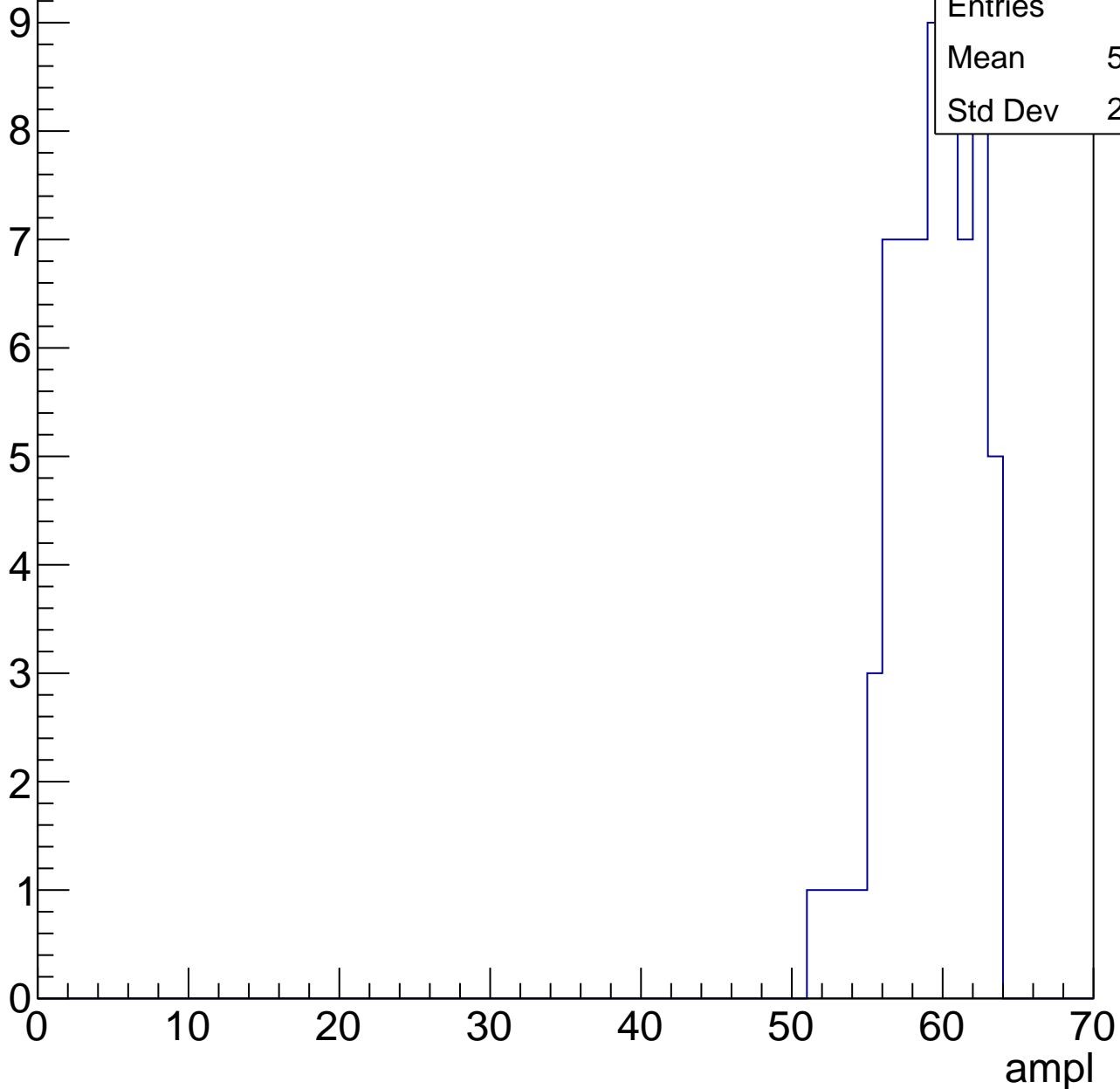
Entries	57
Mean	51.95
Std Dev	3.263



B1L102S, U17-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

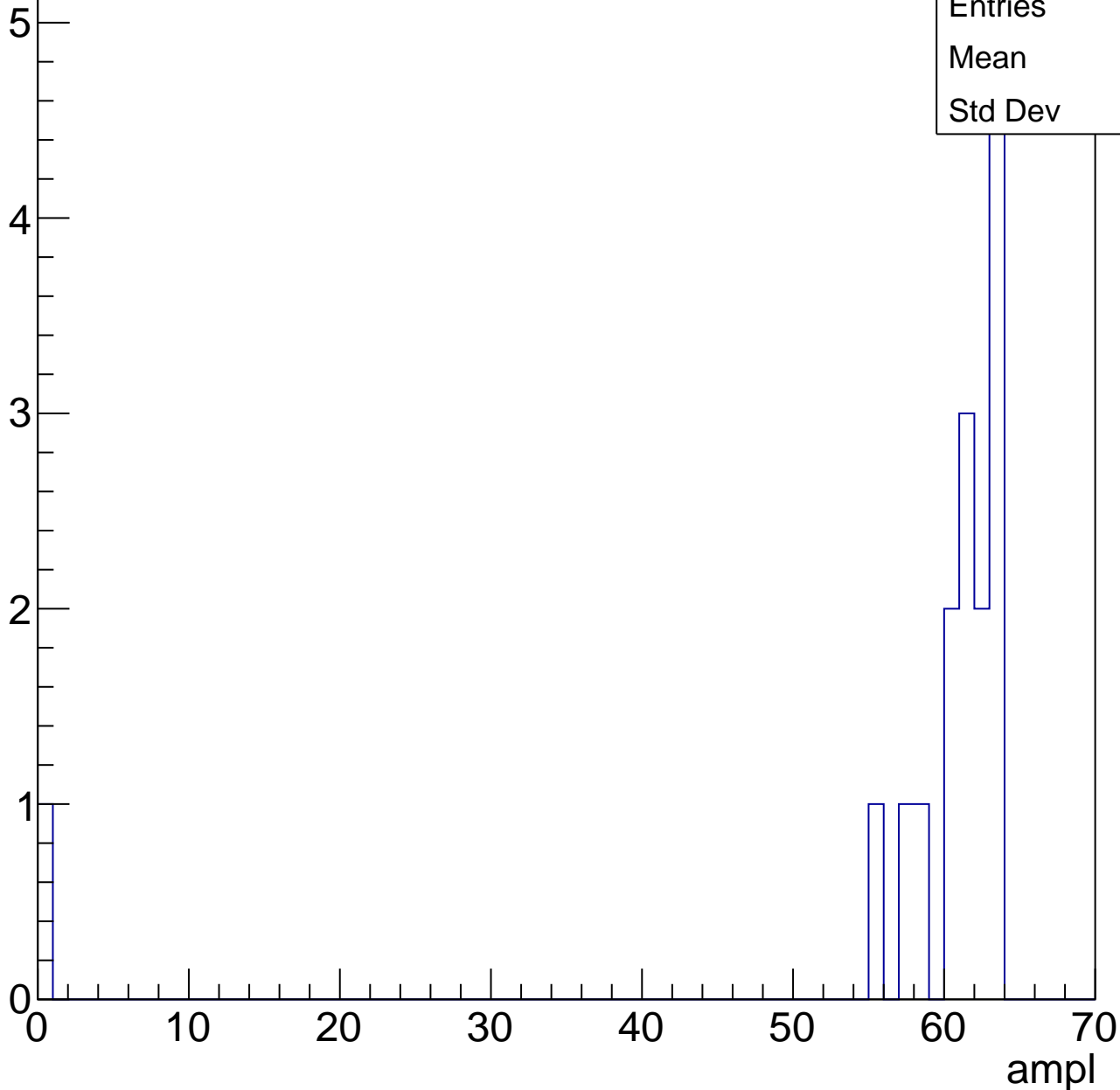


B1L102S, U17-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

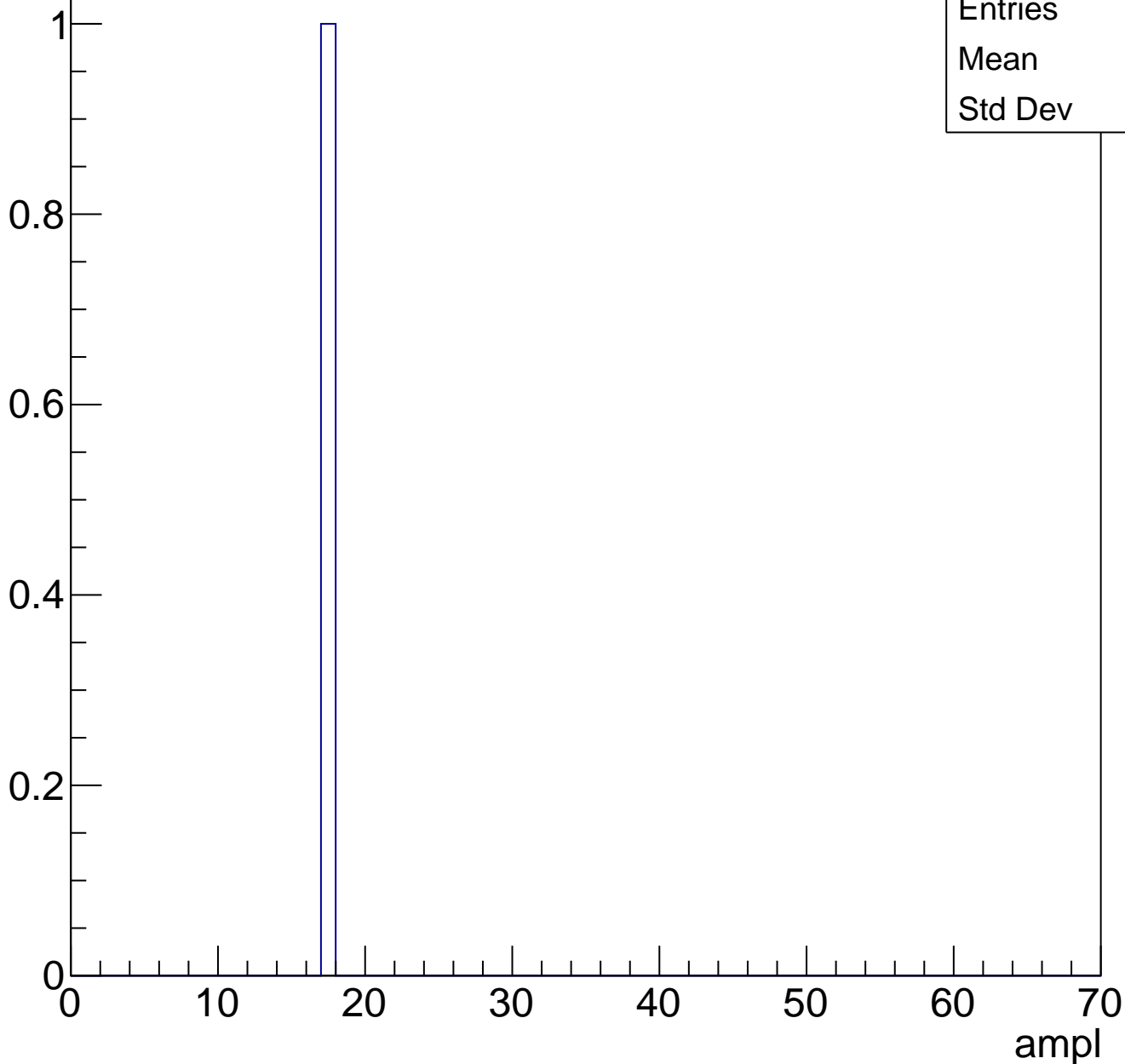
Entries	16
Mean	57
Std Dev	14.9



B1L102S, U17-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch35, adc0

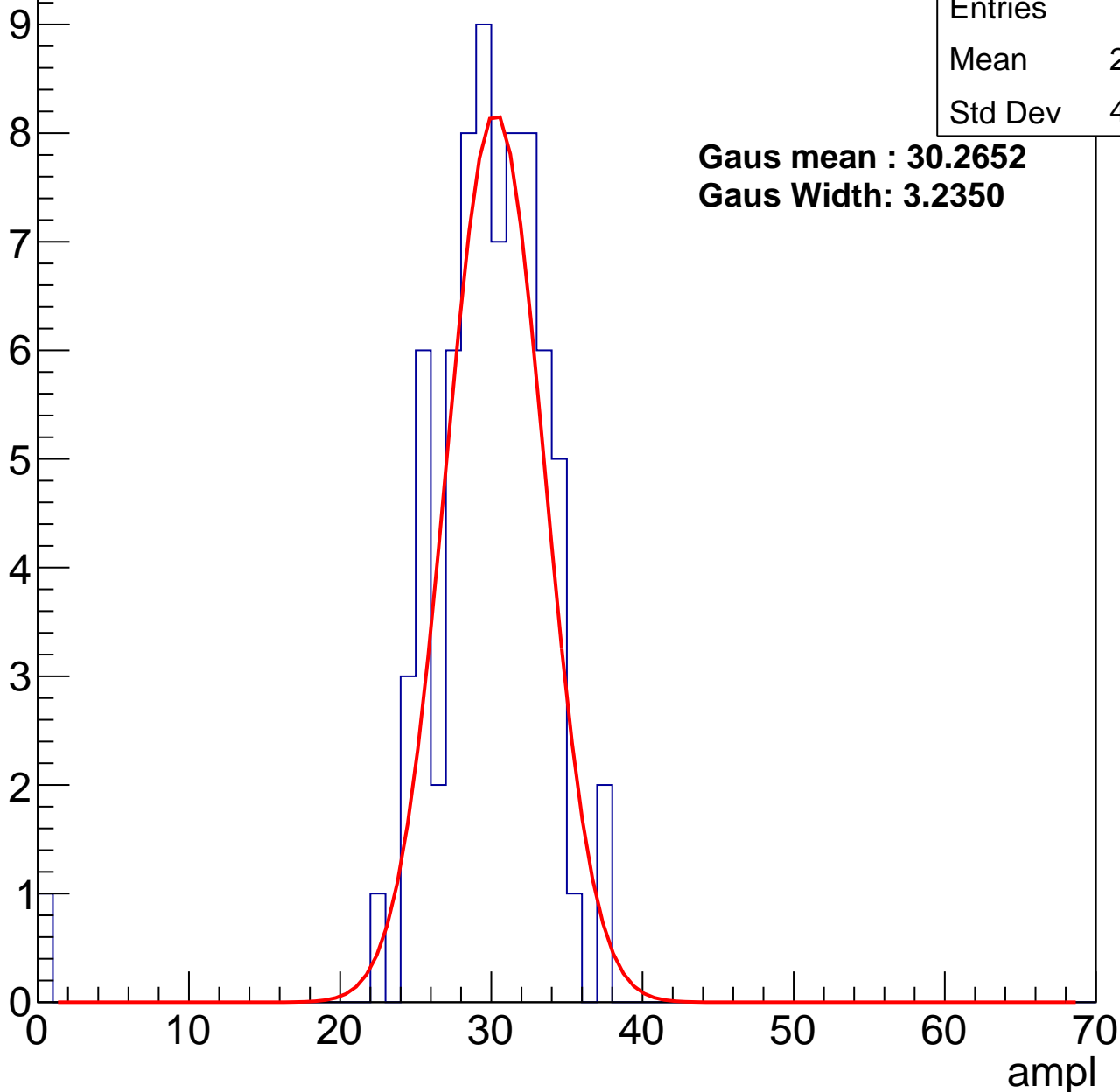
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.23
Std Dev	4.686

Gaus mean : 30.2652

Gaus Width: 3.2350



B1L102S, U17-ch35, adc1

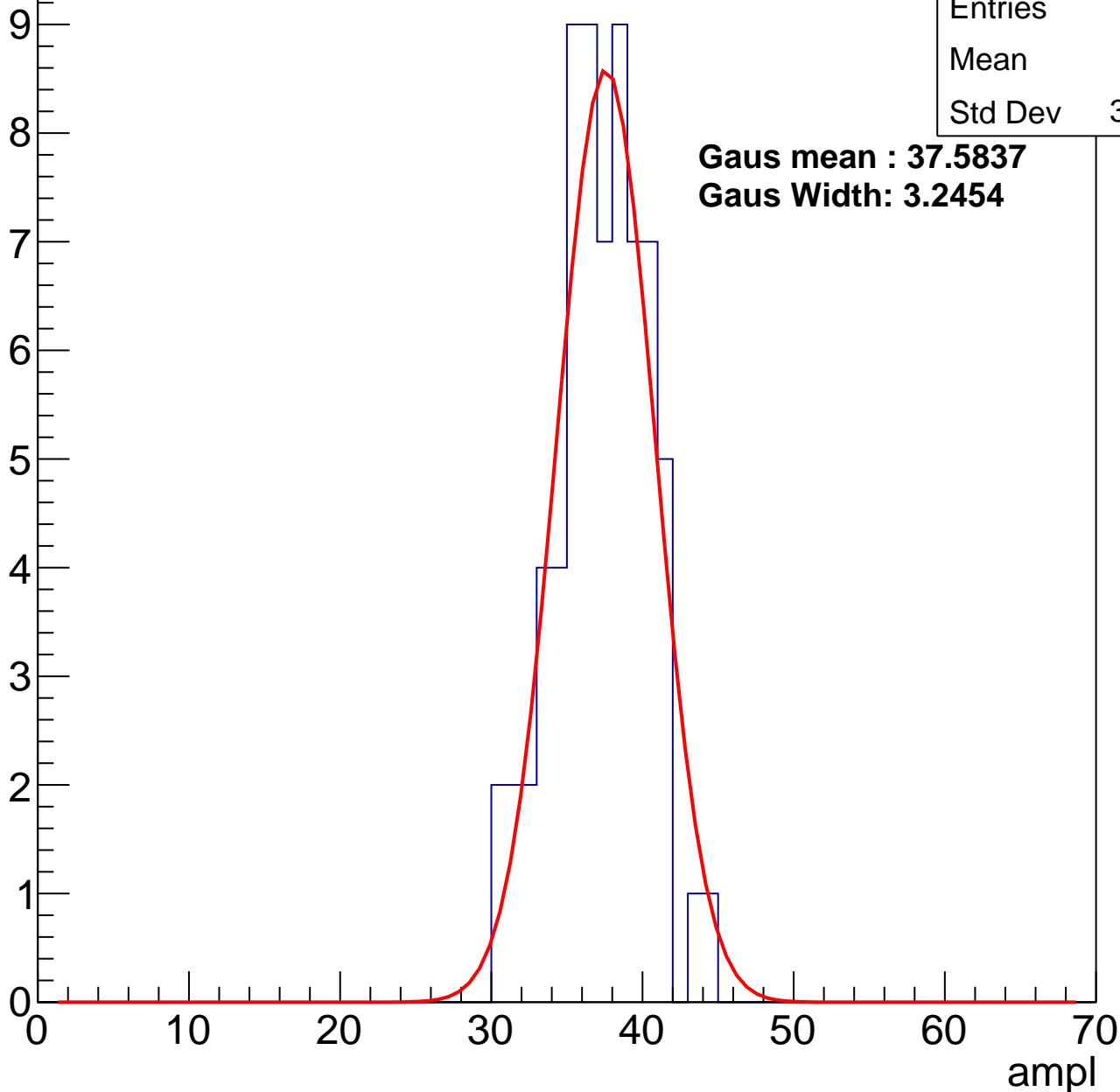
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.8
Std Dev	3.015

Gaus mean : 37.5837

Gaus Width: 3.2454



B1L102S, U17-ch35, adc2

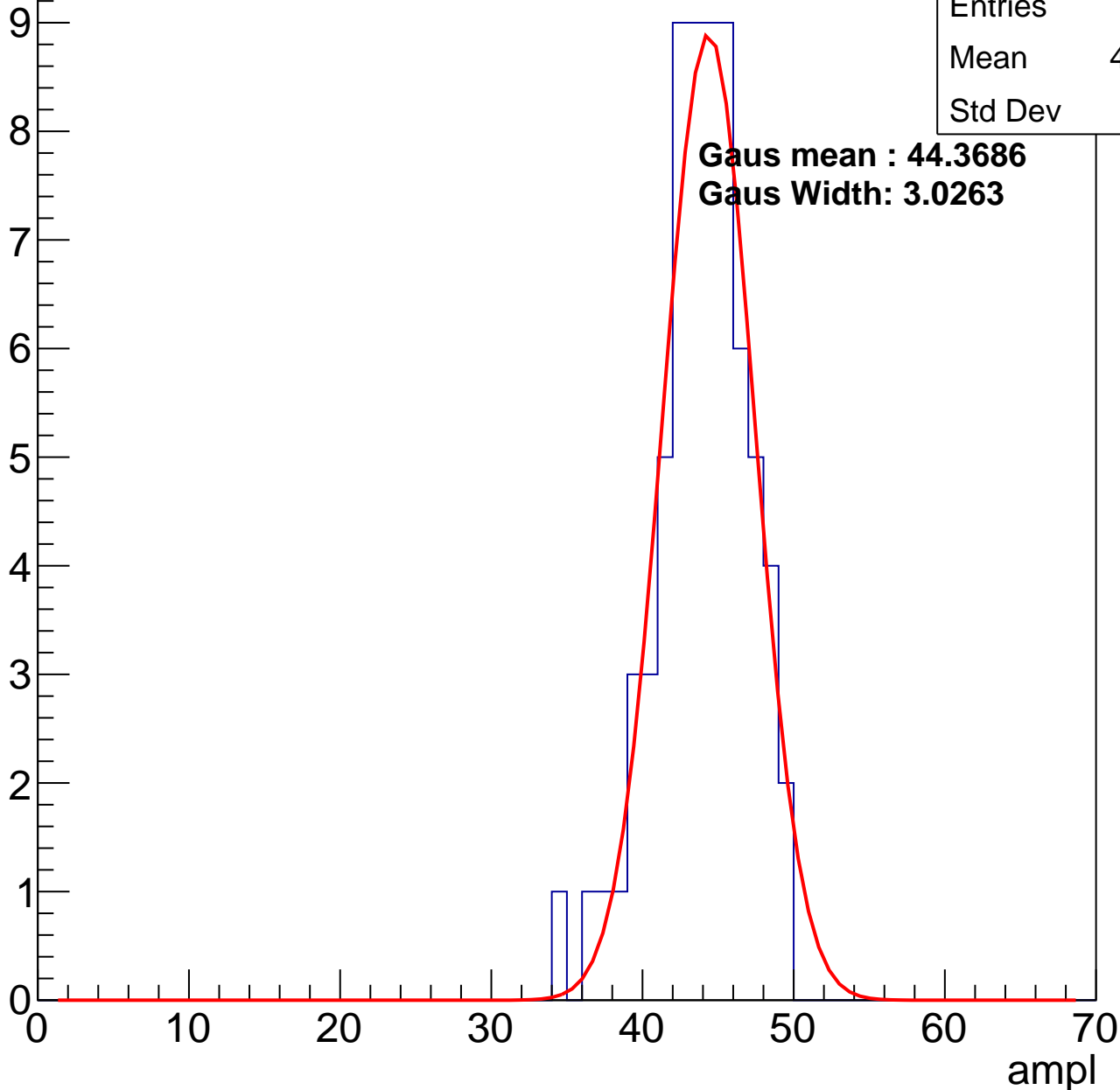
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.44
Std Dev	3.05

Gaus mean : 44.3686

Gaus Width: 3.0263

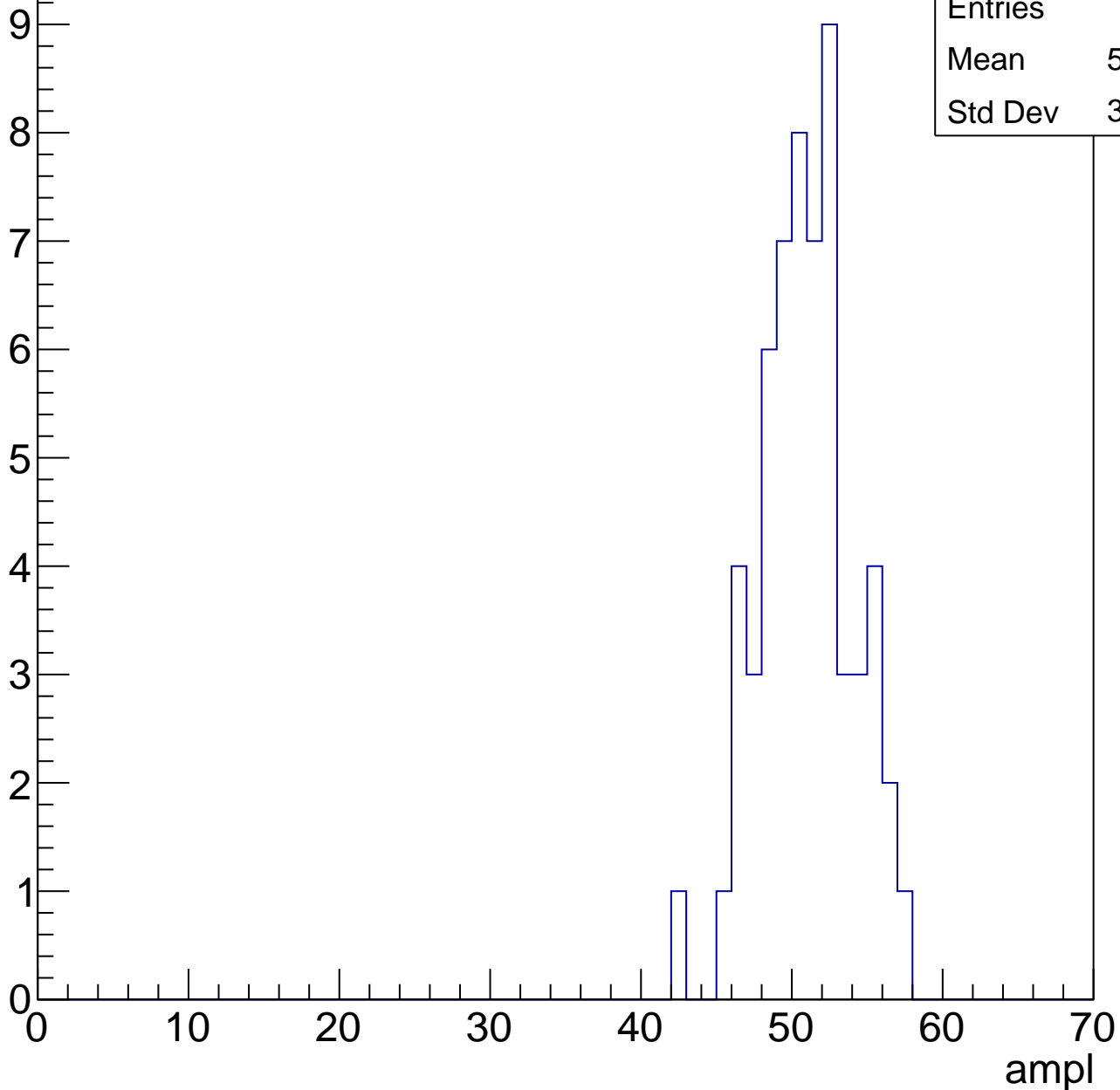


B1L102S, U17-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.47
Std Dev	3.016

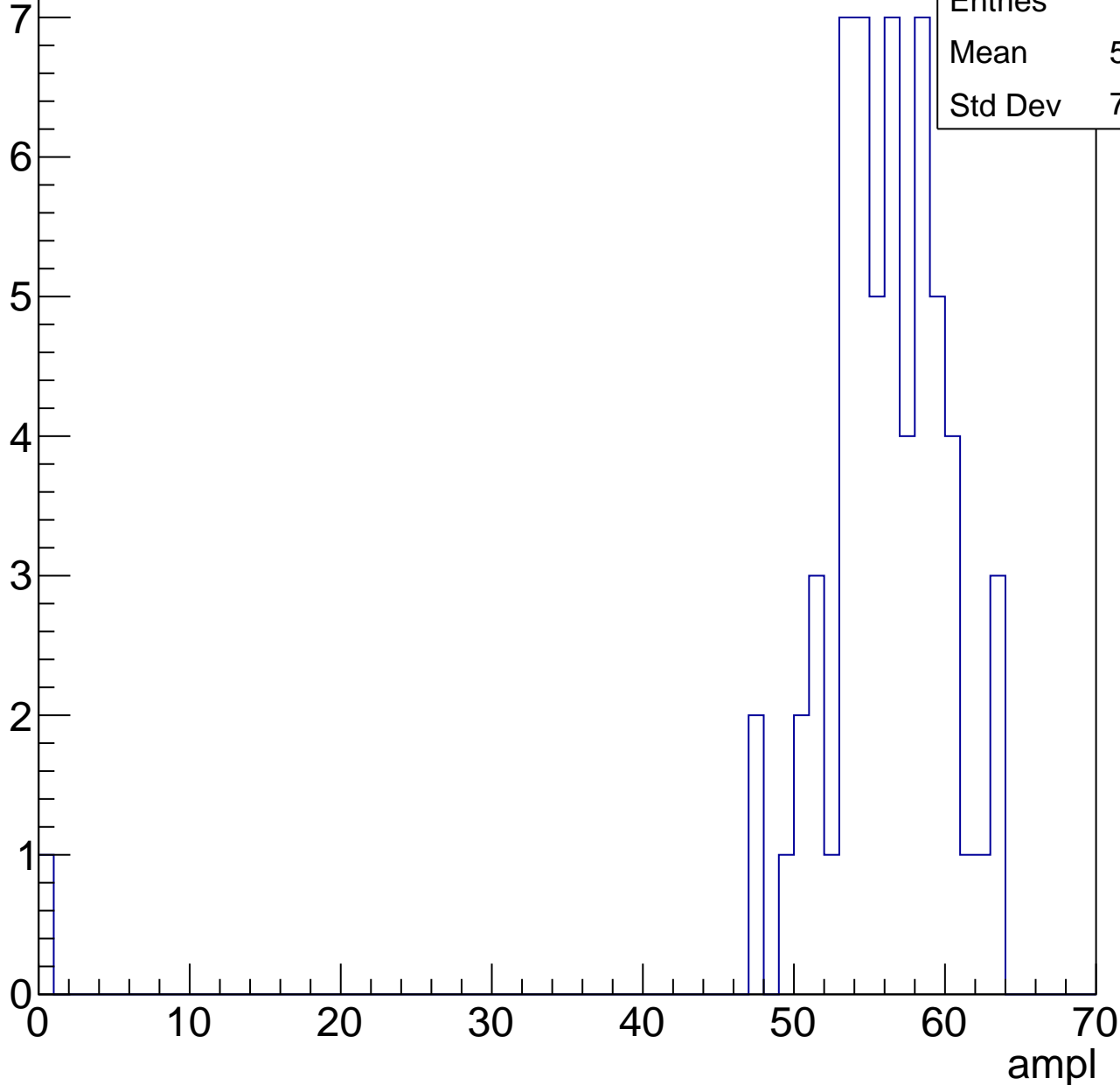


B1L102S, U17-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

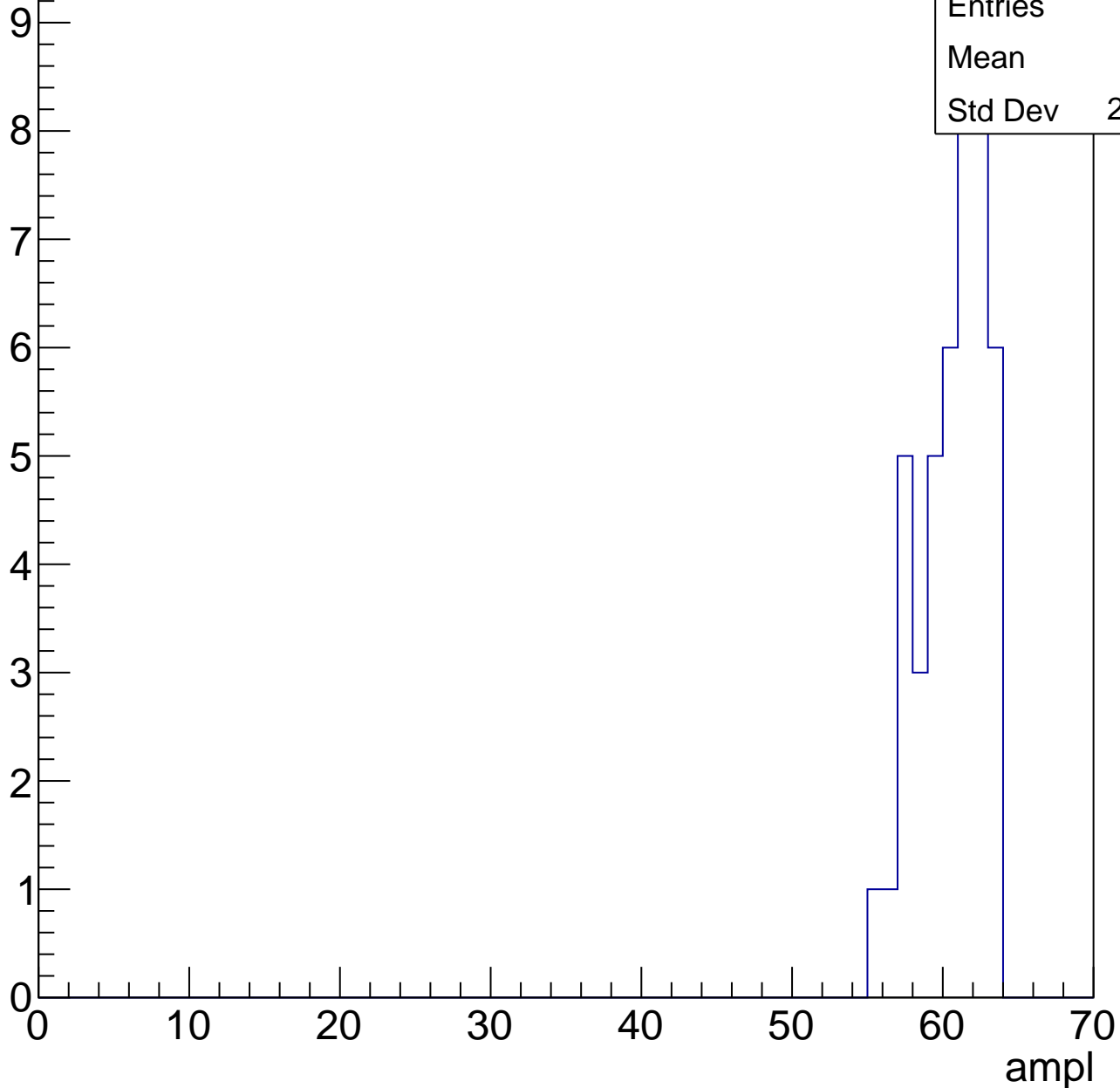
Entries	61
Mean	54.84
Std Dev	7.968



B1L102S, U17-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	44
Mean	60.2
Std Dev	2.128

B1L102S, U17-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch36, adc0

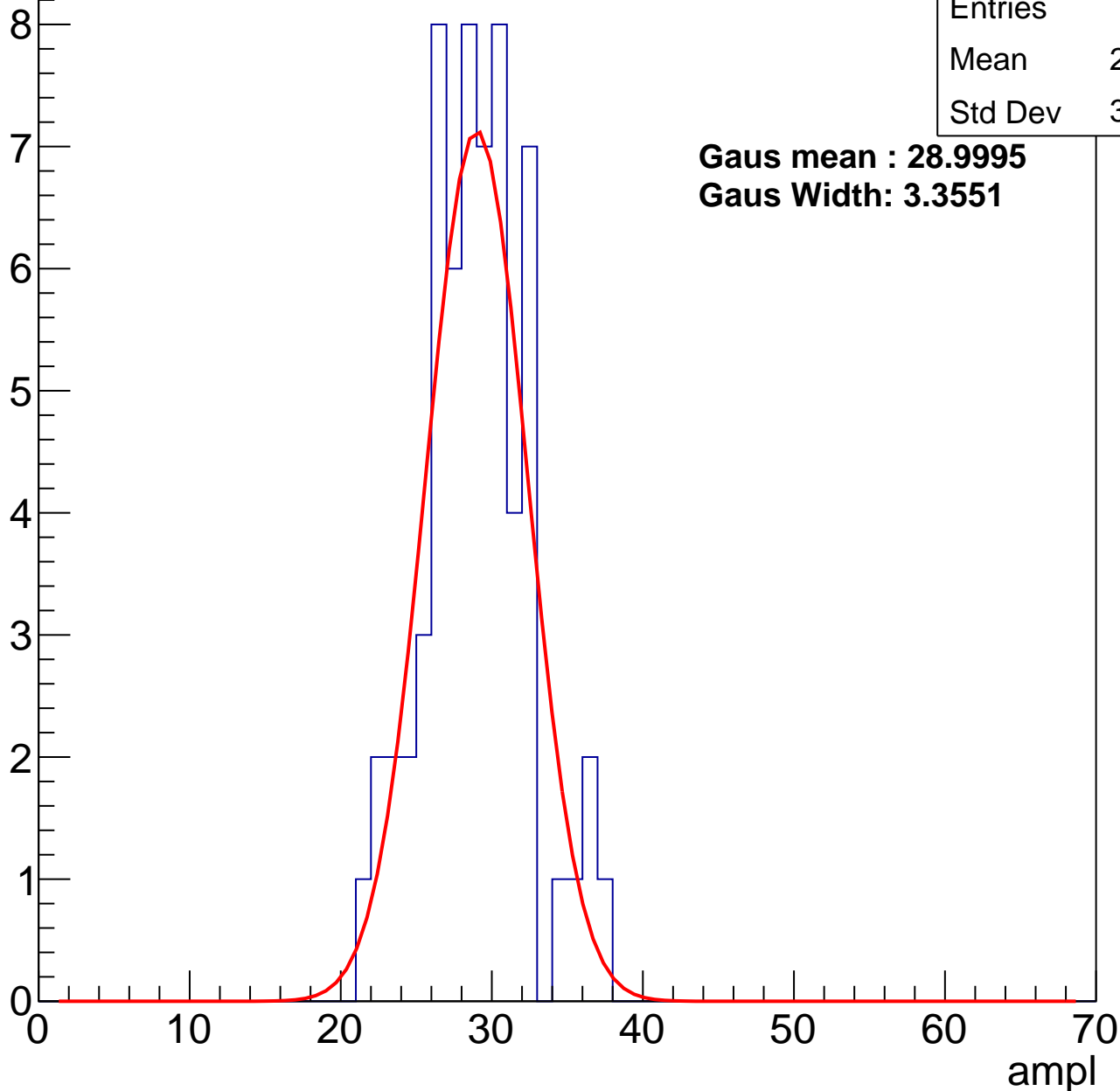
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.52
Std Dev	3.408

Gaus mean : 28.9995

Gaus Width: 3.3551



B1L102S, U17-ch36, adc1

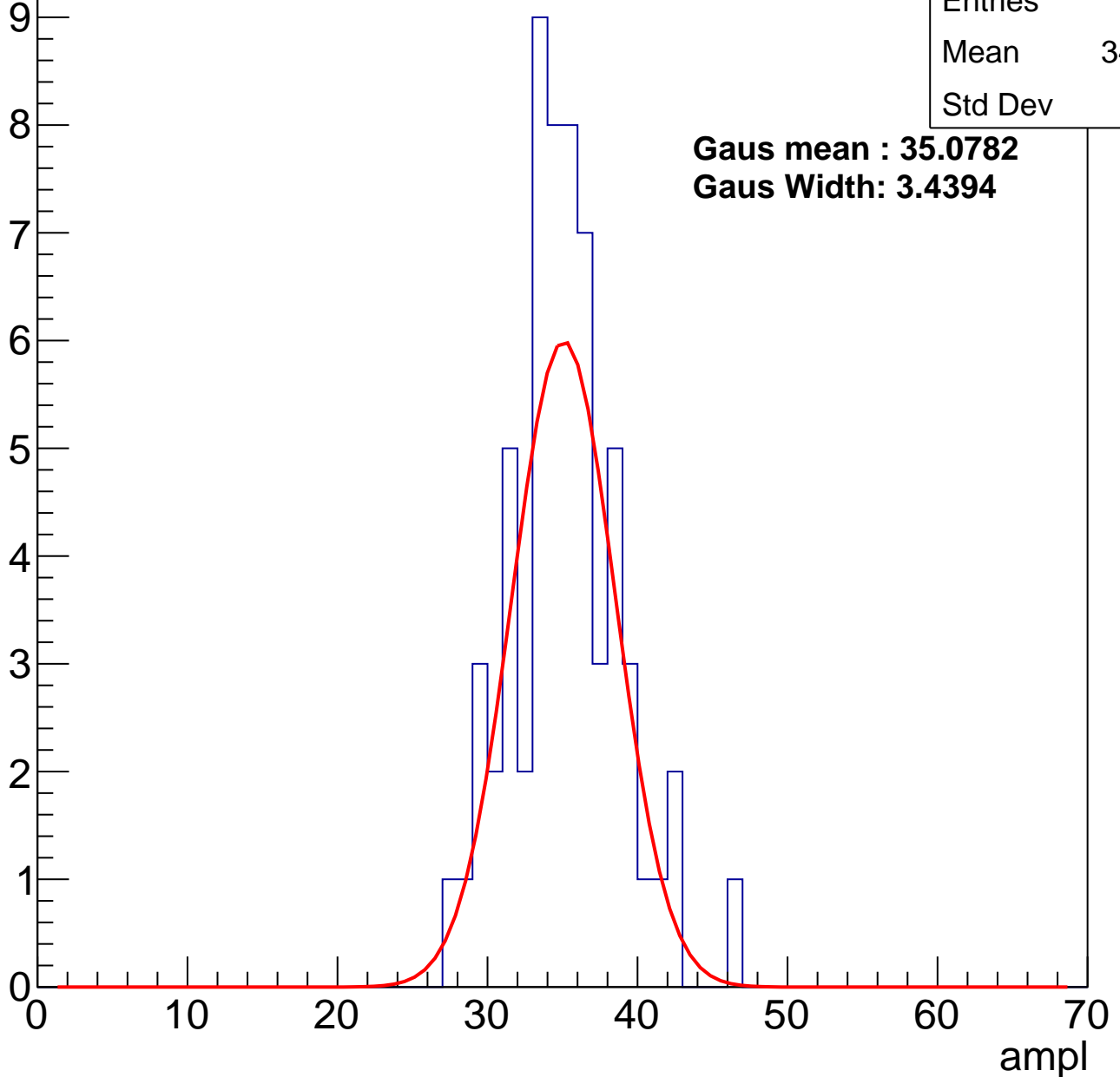
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.69
Std Dev	3.59

Gaus mean : 35.0782

Gaus Width: 3.4394



B1L102S, U17-ch36, adc2

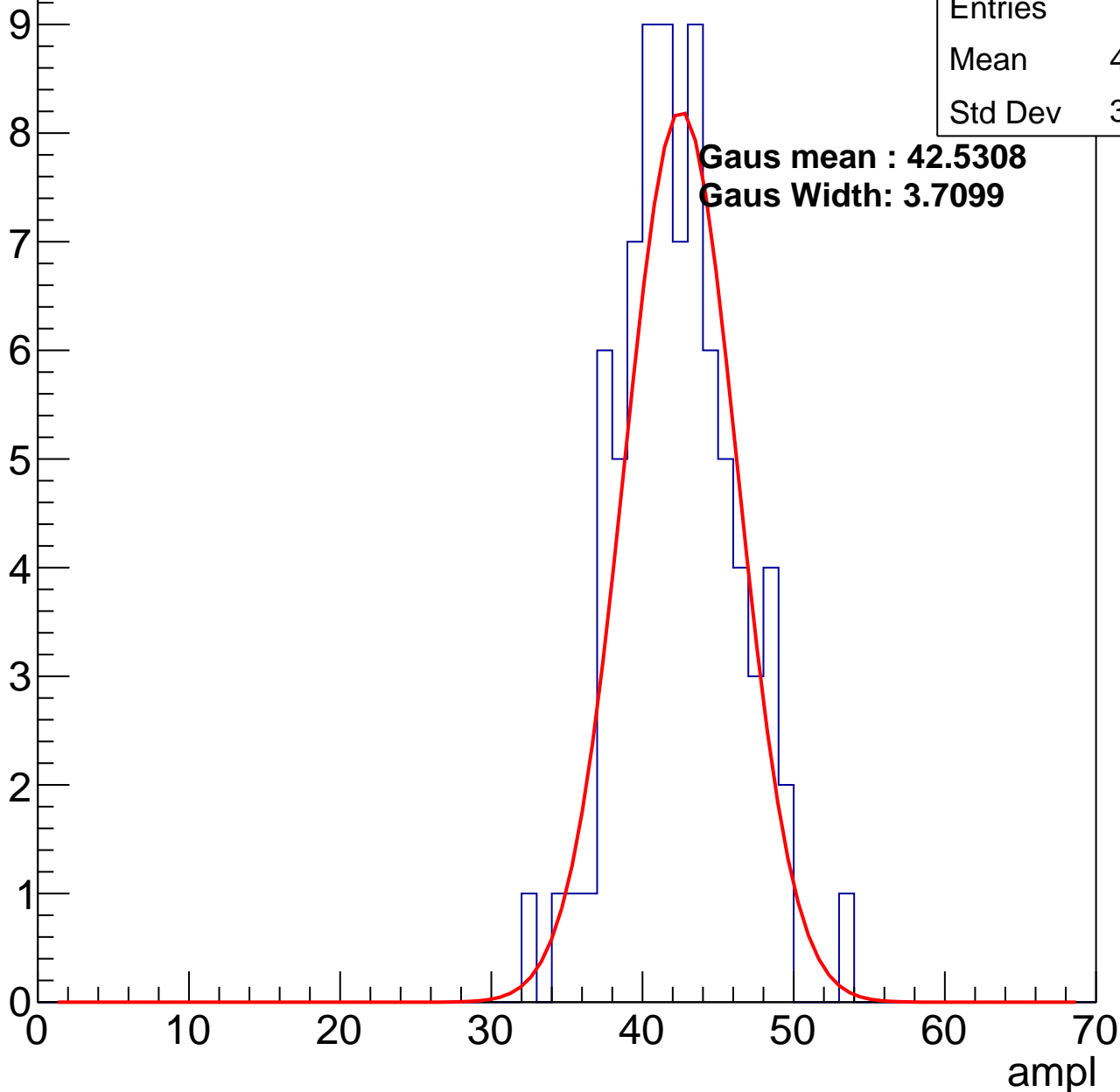
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	41.84
Std Dev	3.792

Gaus mean : 42.5308

Gaus Width: 3.7099

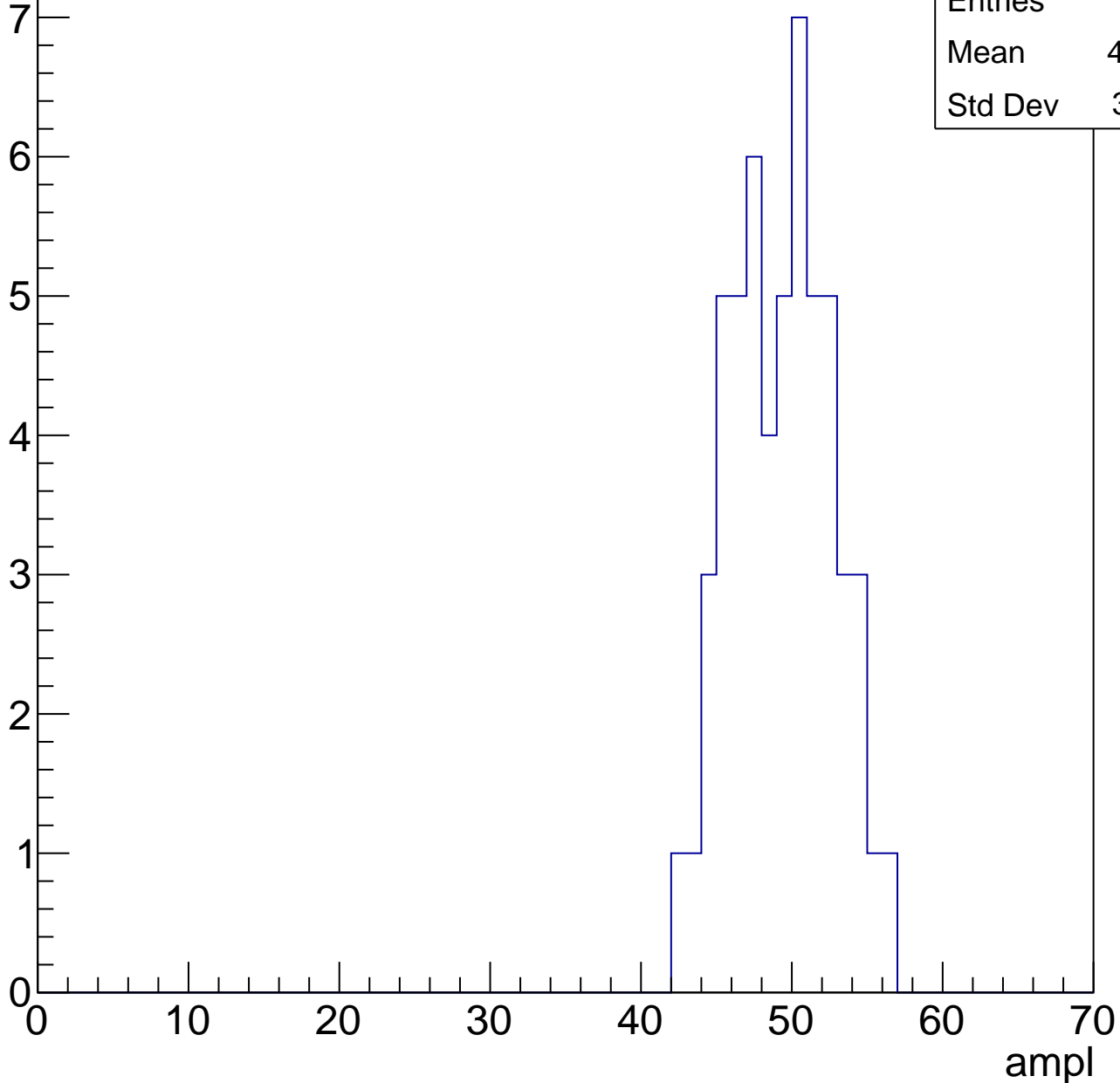


B1L102S, U17-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.87
Std Dev	3.281

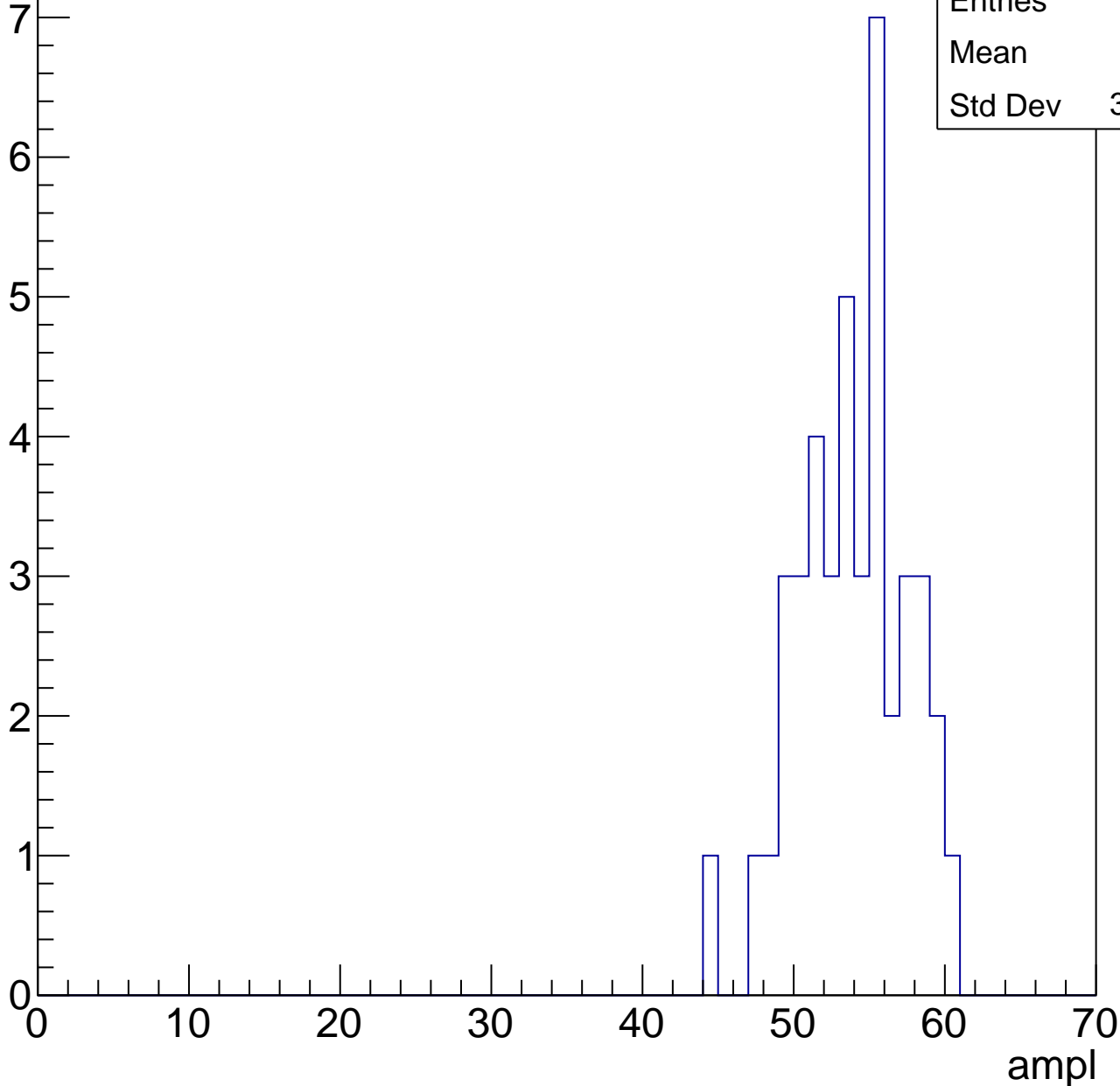


B1L102S, U17-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	53.4
Std Dev	3.526

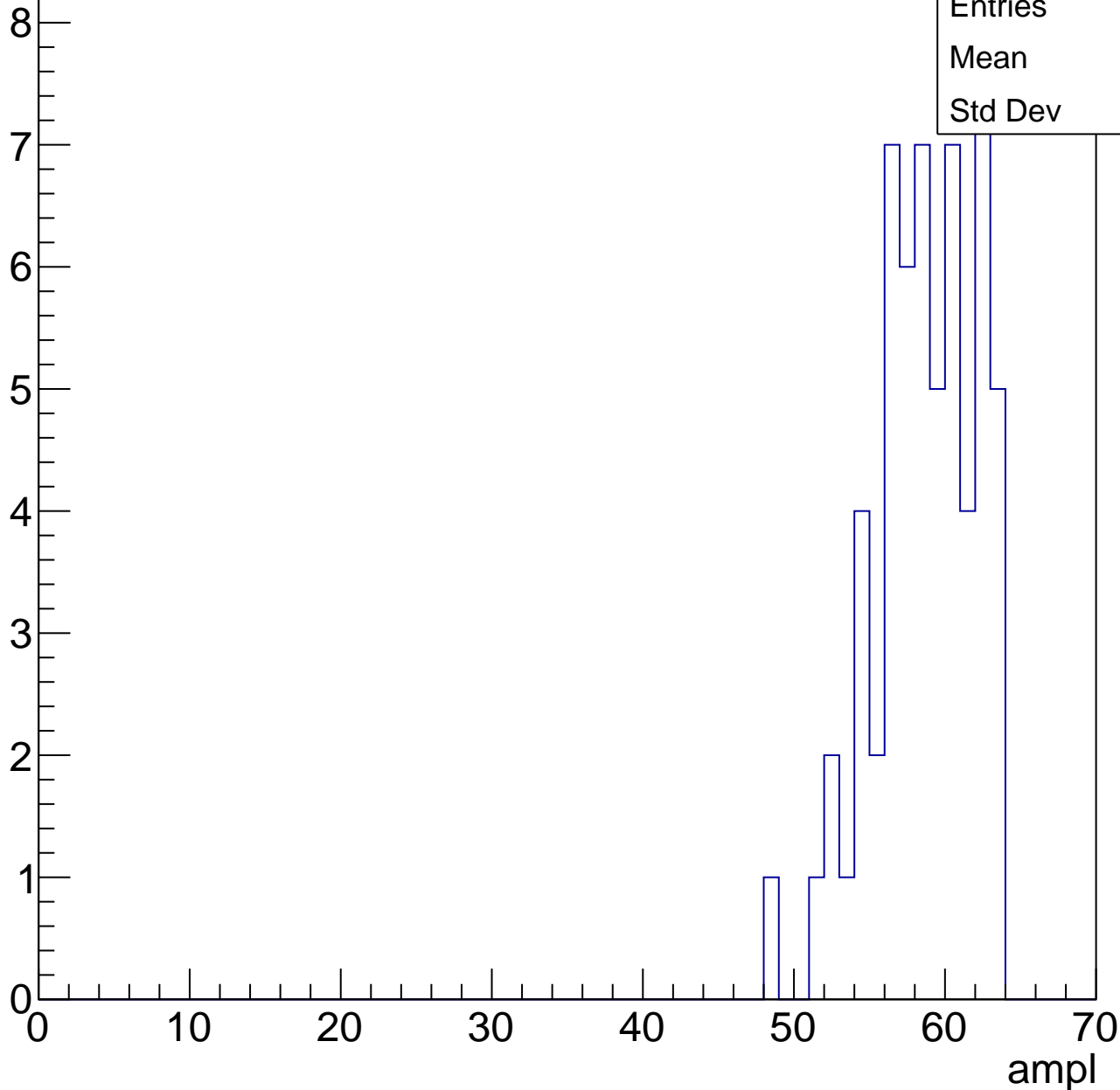


B1L102S, U17-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.2
Std Dev	3.37

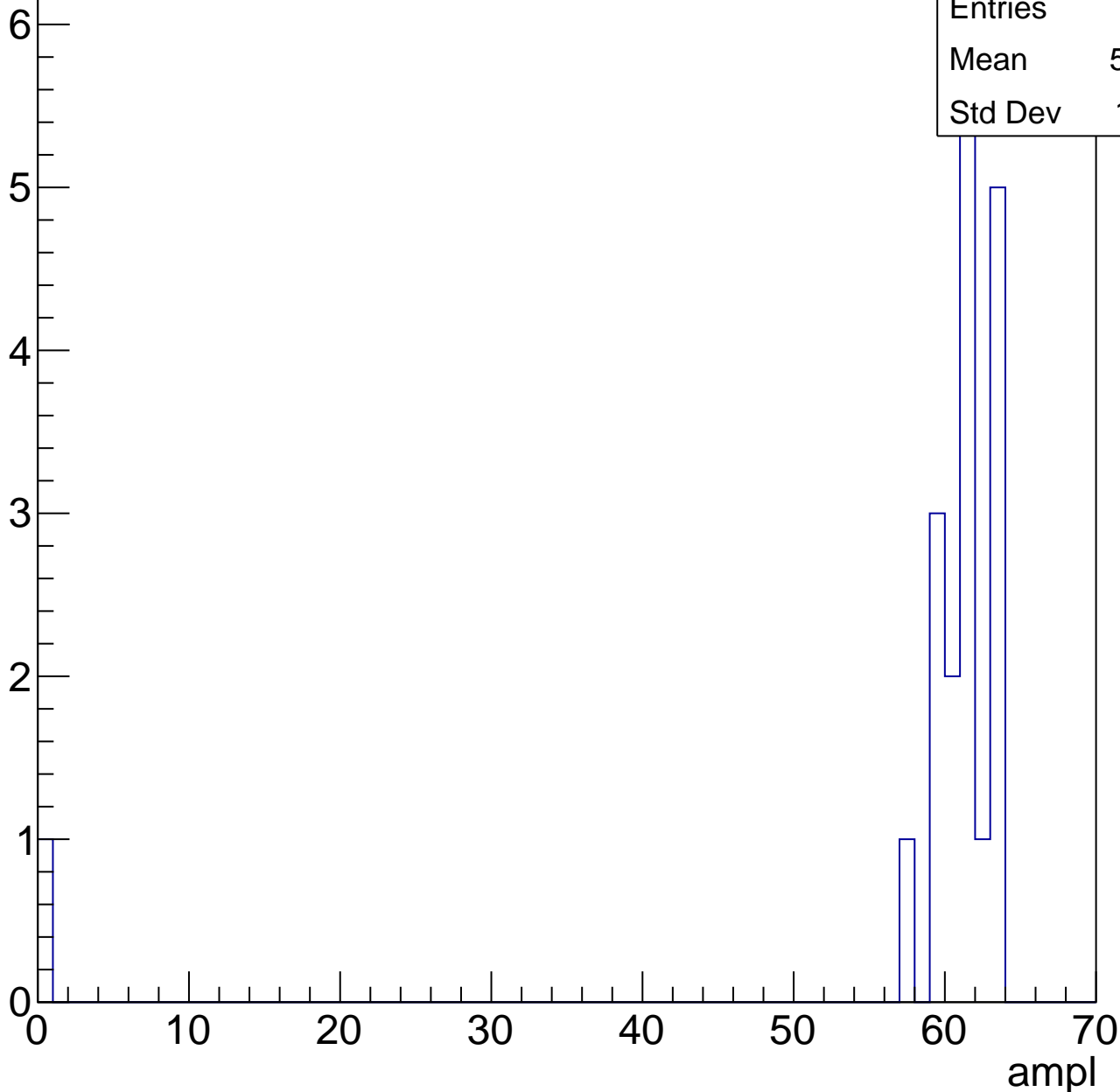


B1L102S, U17-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

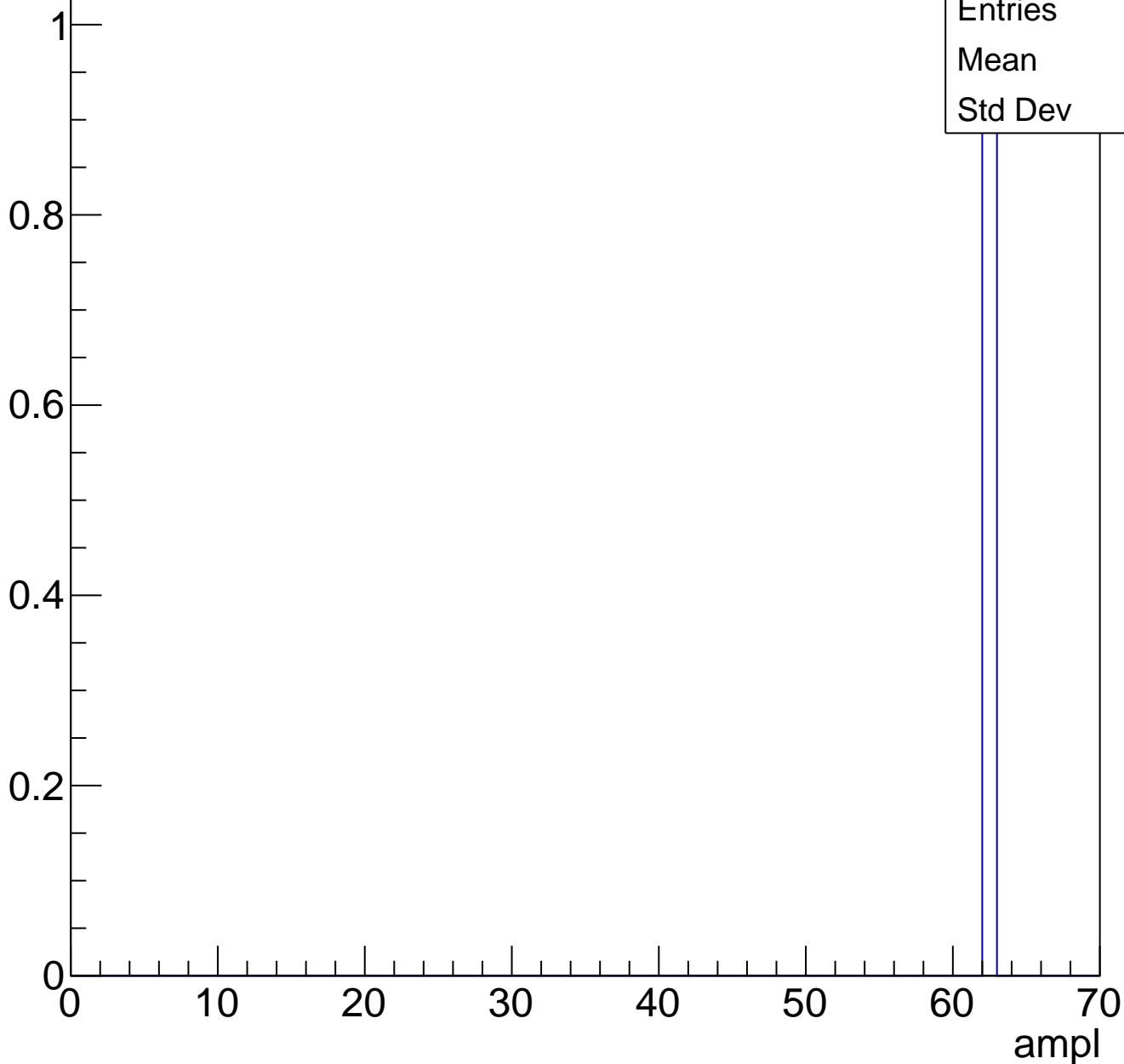
Entries	19
Mean	57.74
Std Dev	13.71



B1L102S, U17-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch37, adc0

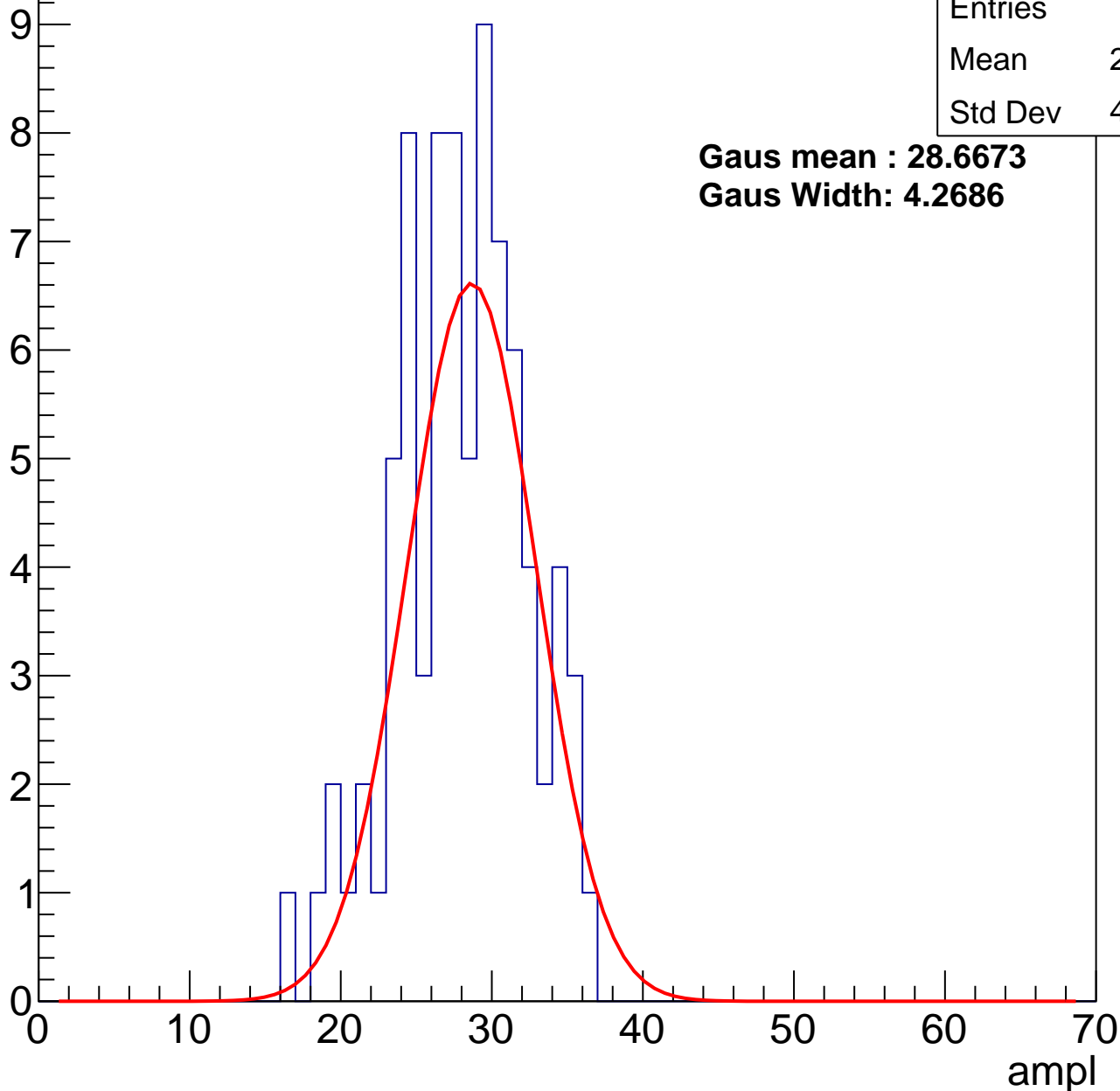
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.53
Std Dev	4.255

Gaus mean : 28.6673

Gaus Width: 4.2686



B1L102S, U17-ch37, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

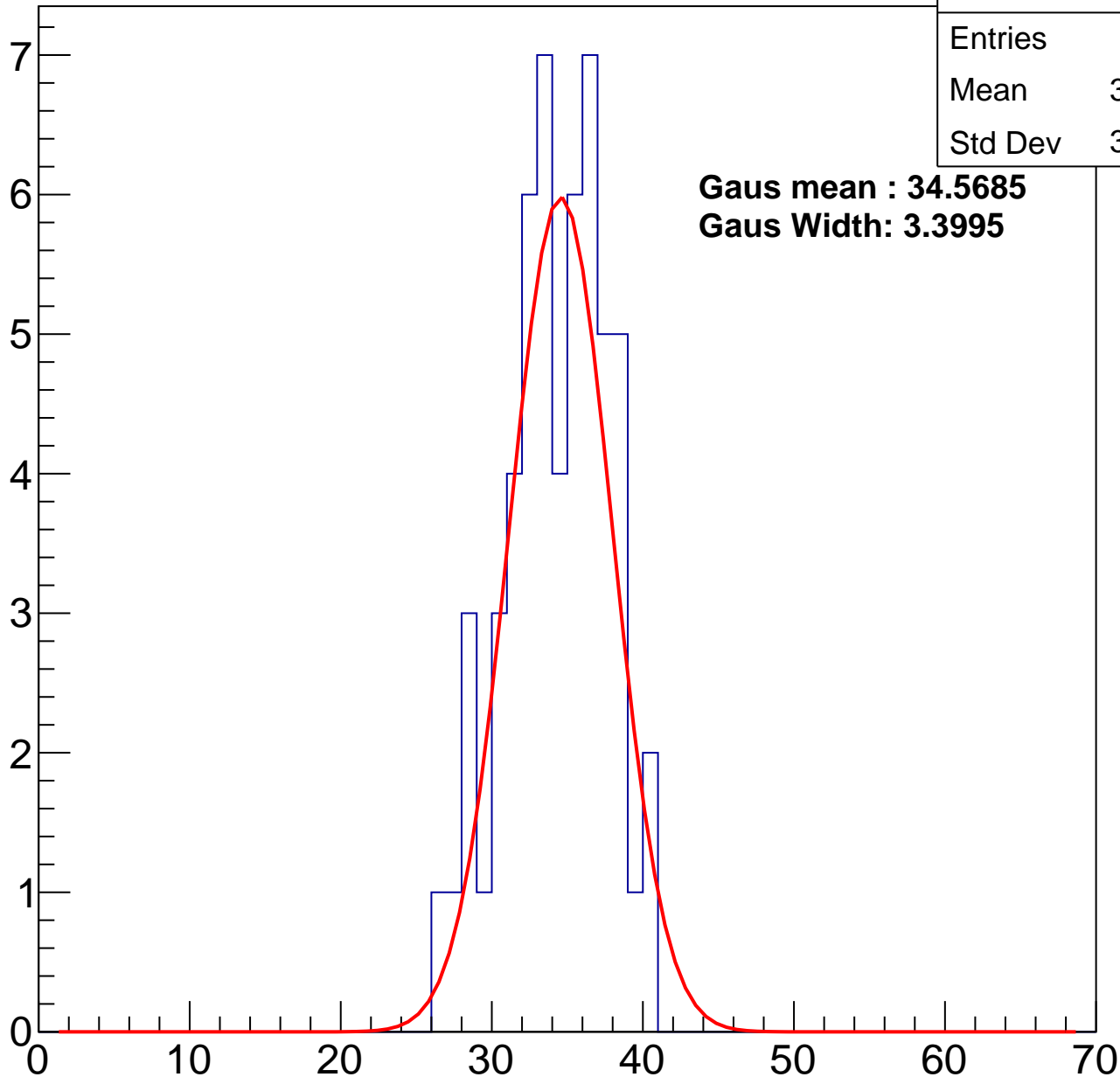
7
6
5
4
3
2
1
0

Entries	56
Mean	33.84
Std Dev	3.315

Gaus mean : 34.5685

Gaus Width: 3.3995

ampl



B1L102S, U17-ch37, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	40.52
Std Dev	3.115

Gaus mean : 41.1217

Gaus Width: 3.0332

10

8

6

4

2

0

0

10

20

30

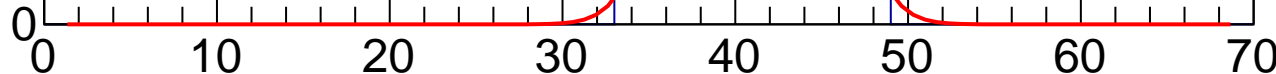
40

50

60

70

ampl

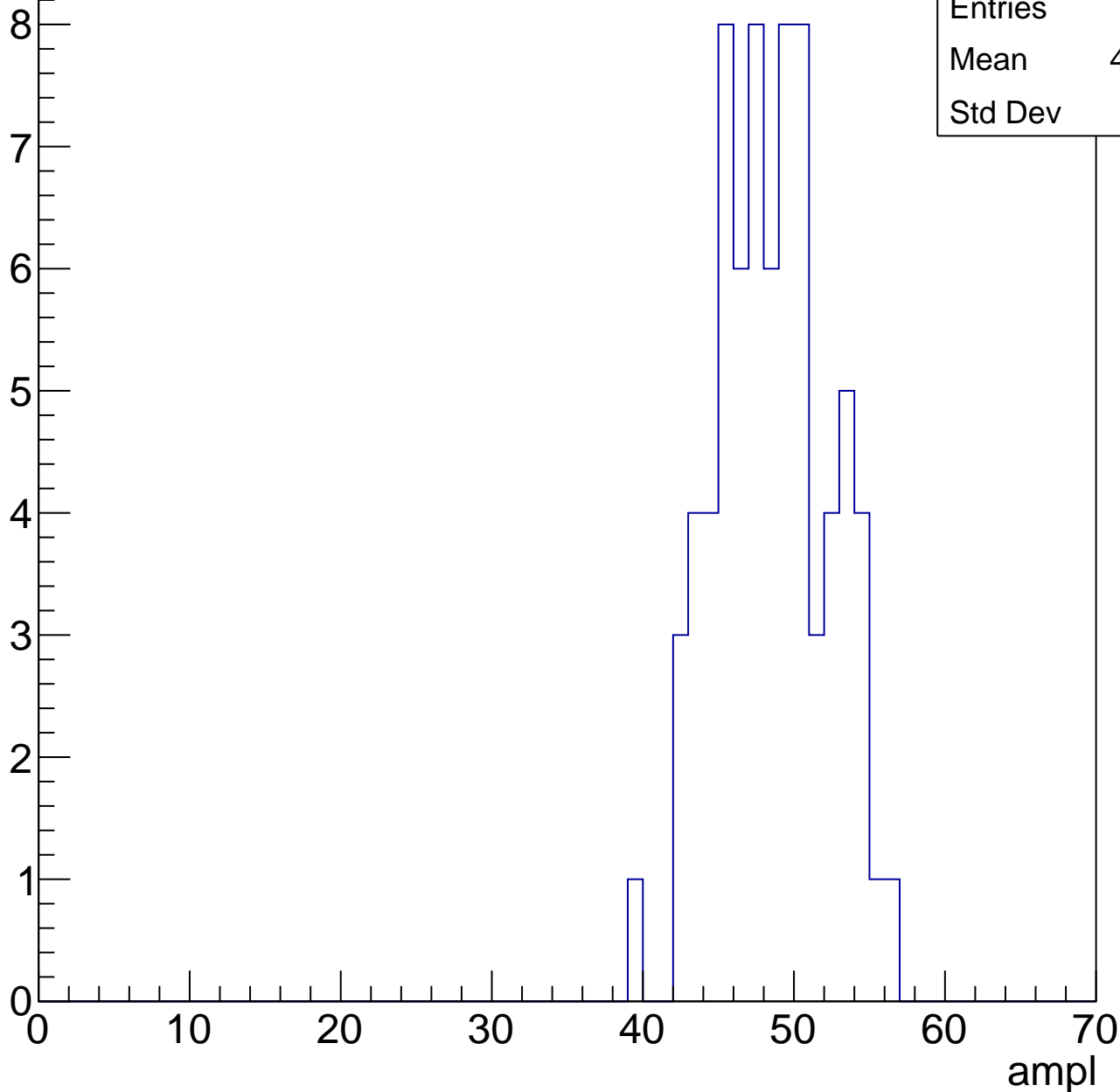


B1L102S, U17-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

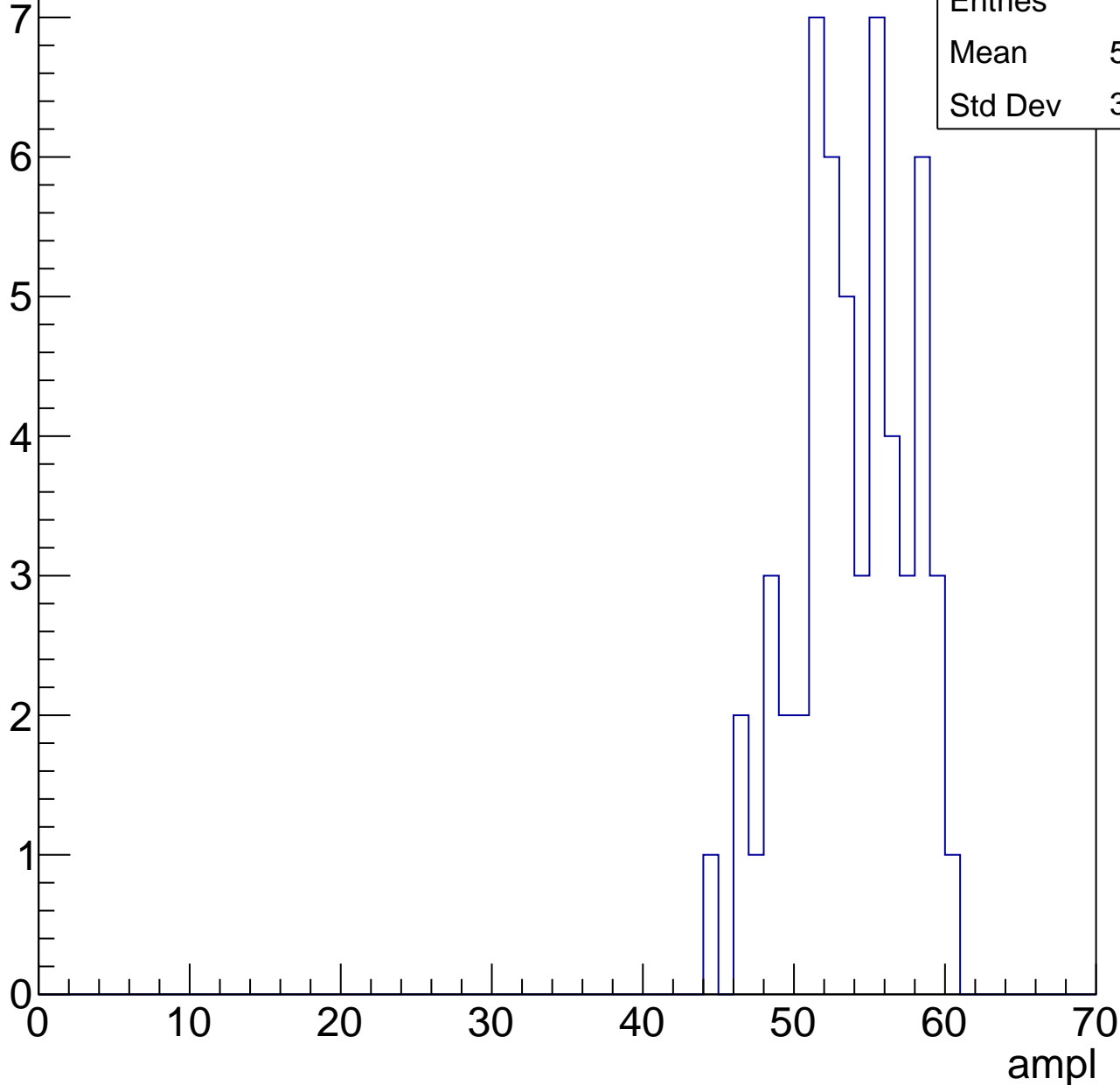
Entries	74
Mean	48.08
Std Dev	3.62



B1L102S, U17-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

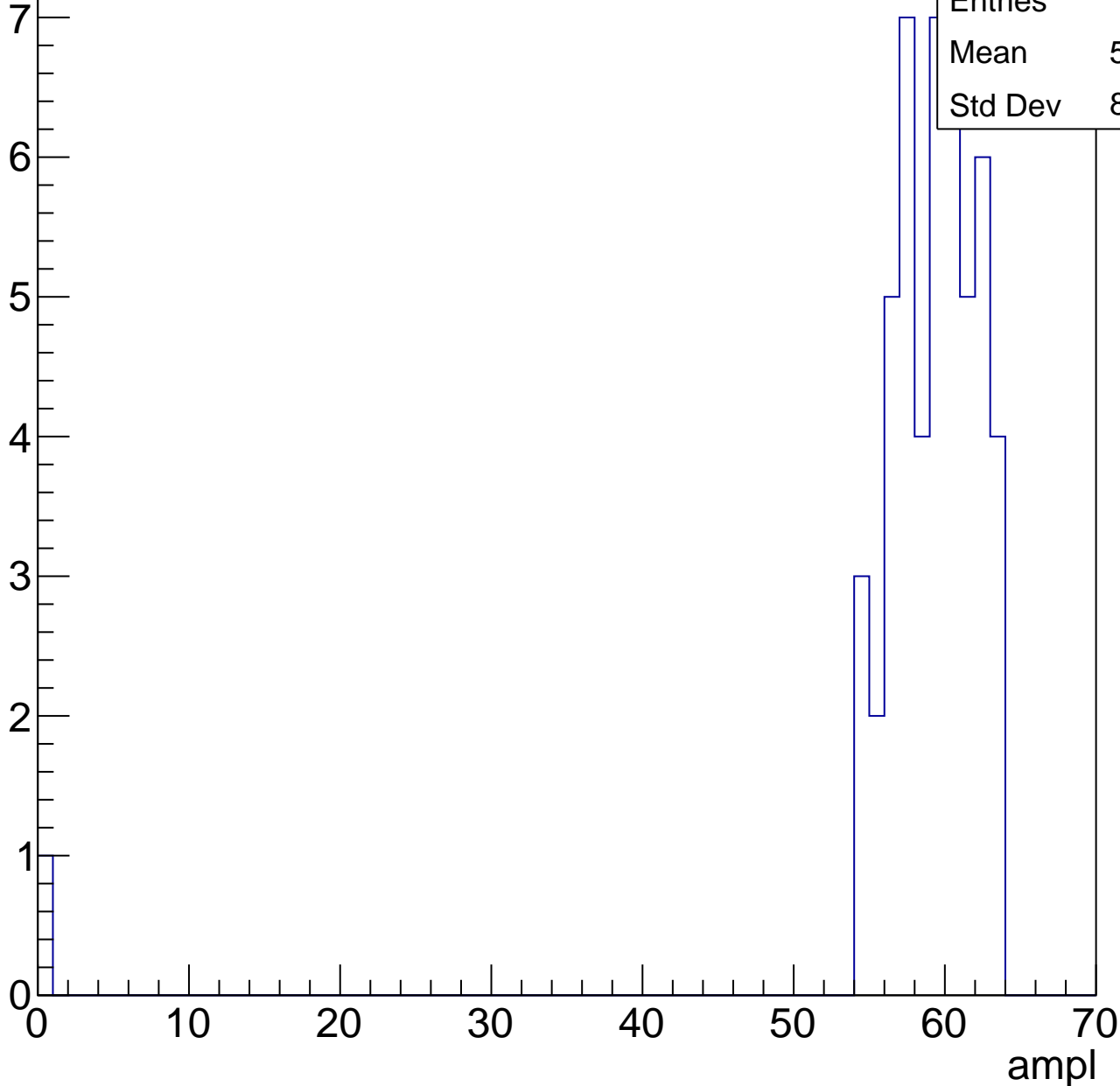


B1L102S, U17-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

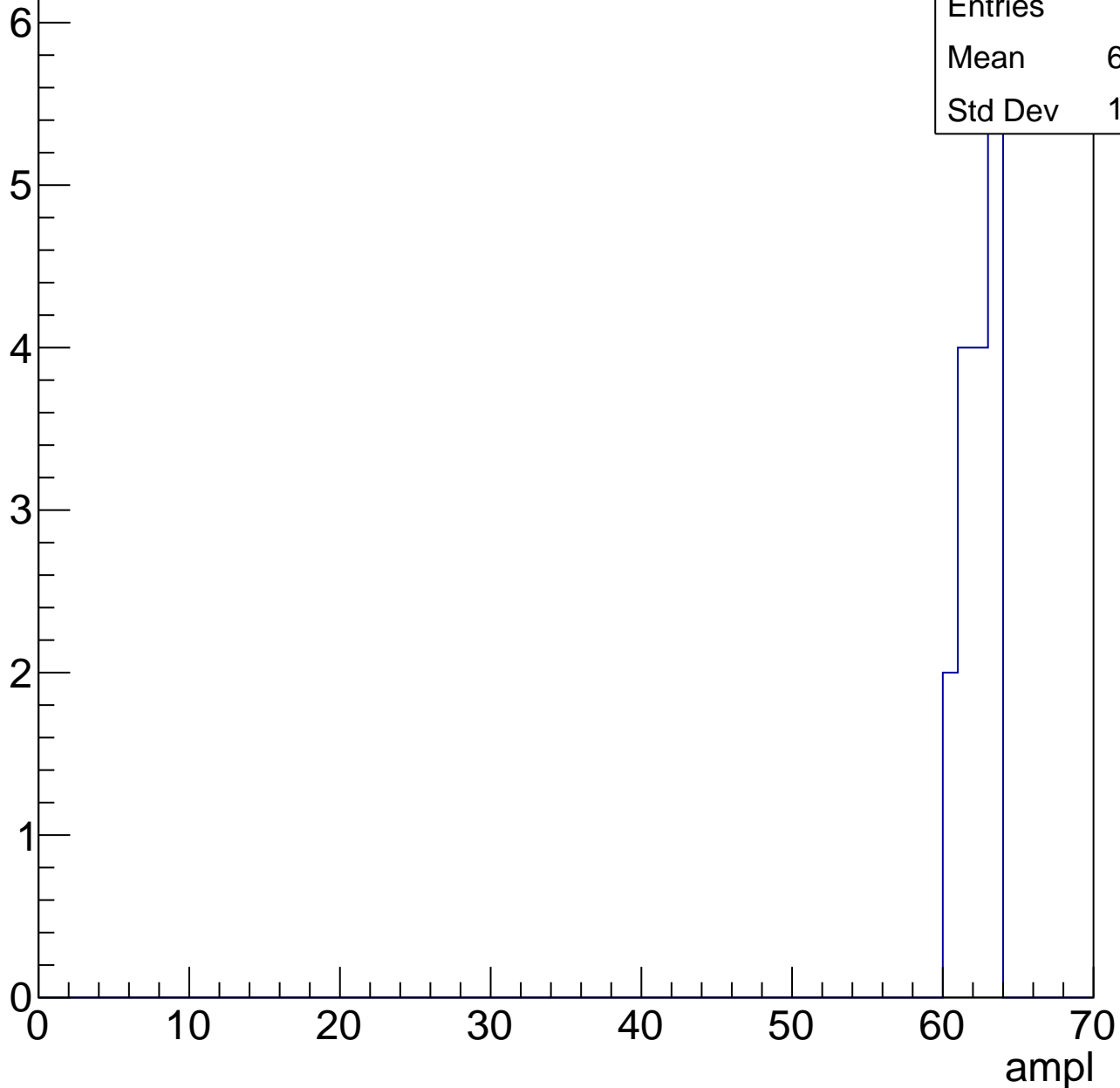
Entries	51
Mean	57.75
Std Dev	8.552



B1L102S, U17-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch38, adc0

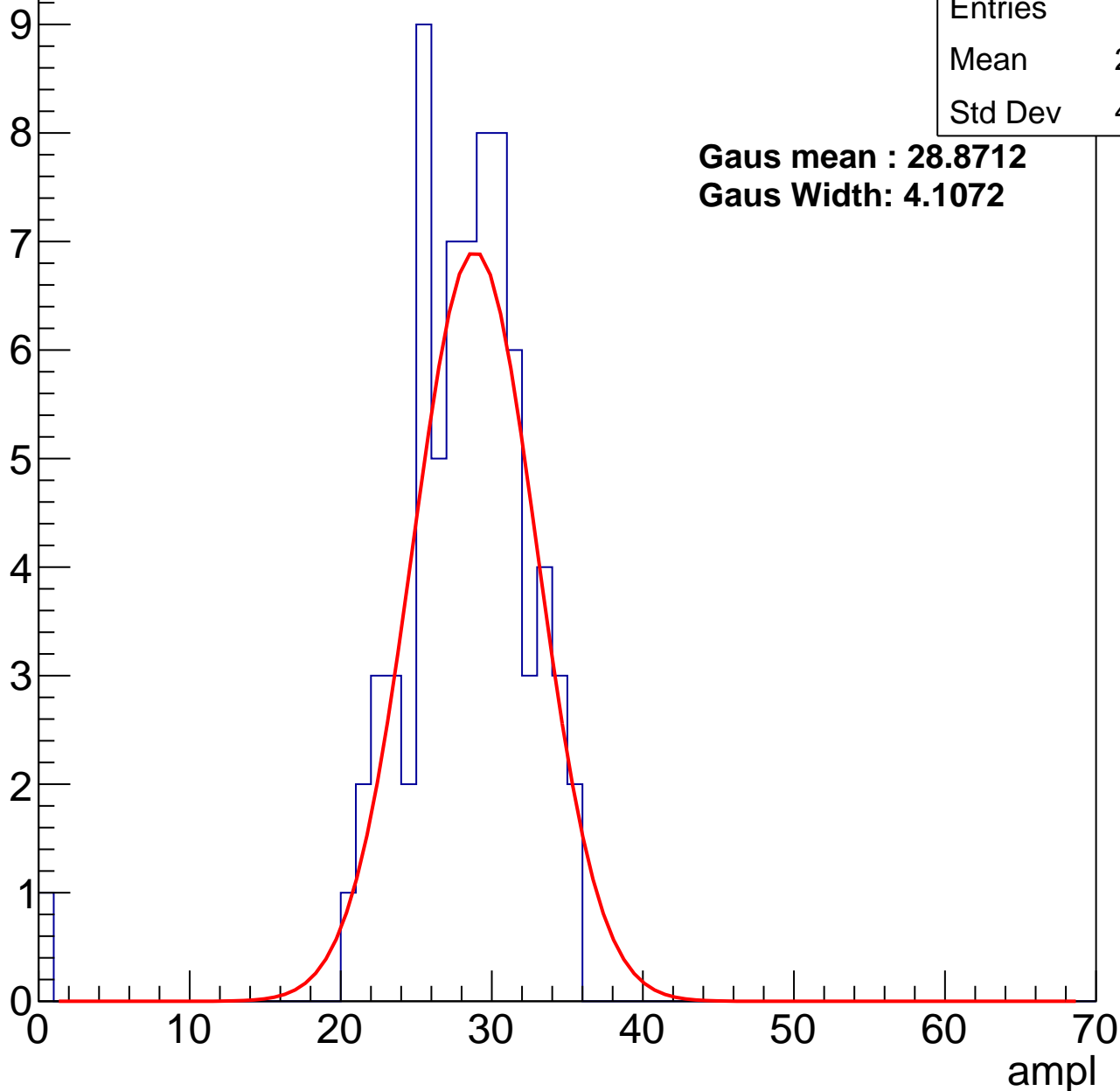
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.61
Std Dev	4.801

Gaus mean : 28.8712

Gaus Width: 4.1072



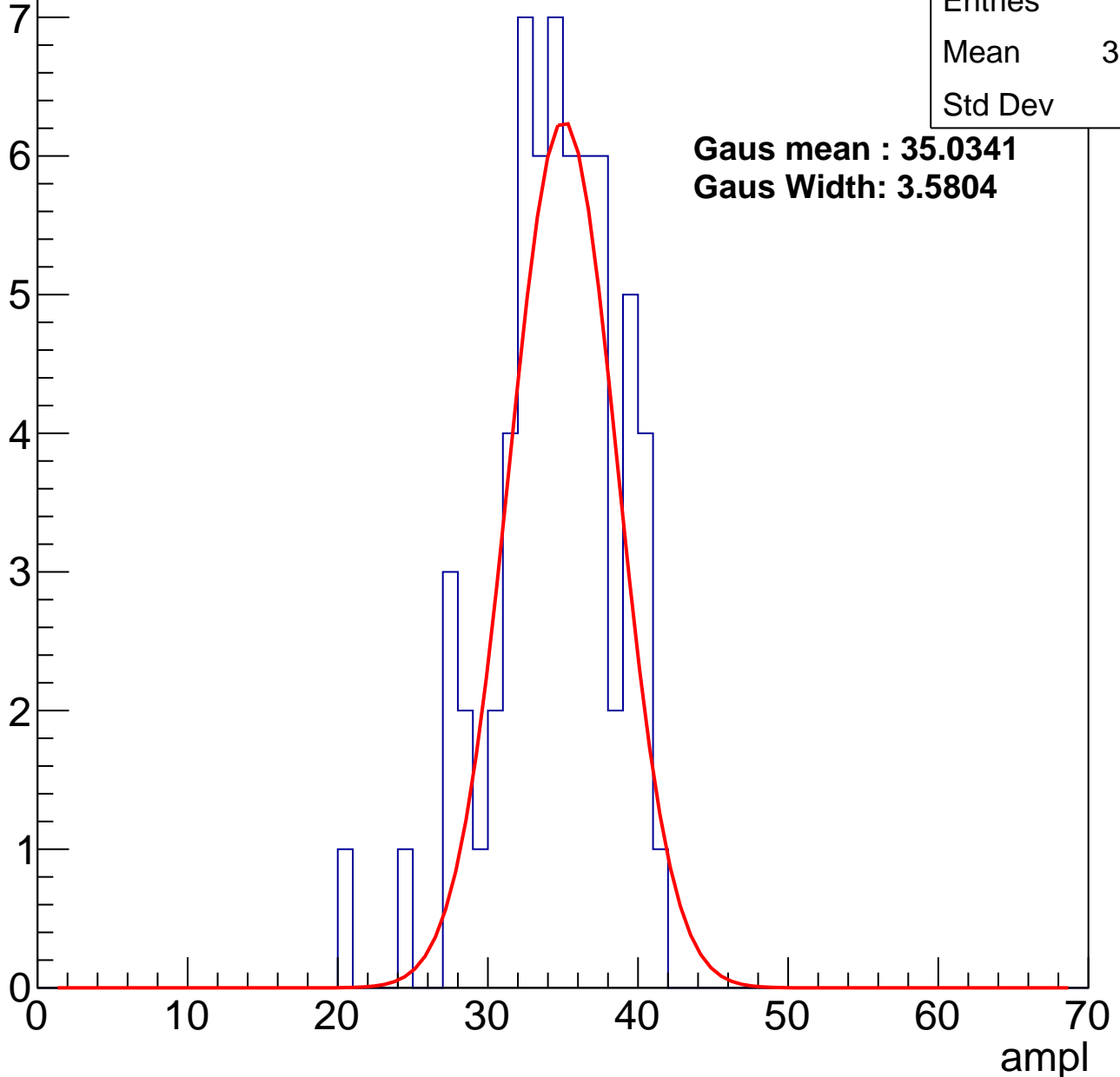
B1L102S, U17-ch38, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	33.97
Std Dev	4.1

Gaus mean : 35.0341
Gaus Width: 3.5804



B1L102S, U17-ch38, adc2

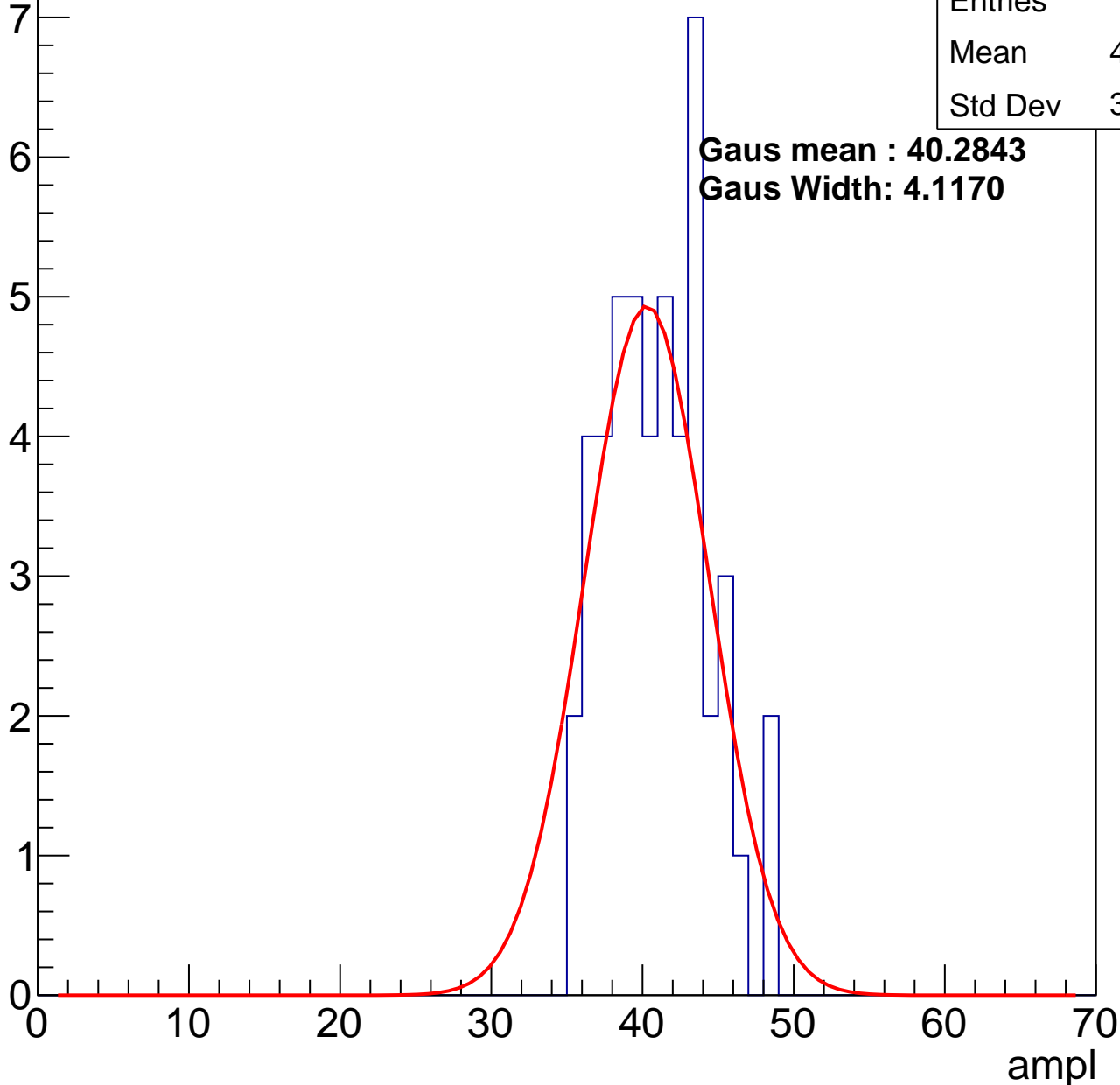
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	40.54
Std Dev	3.278

Gaus mean : 40.2843

Gaus Width: 4.1170

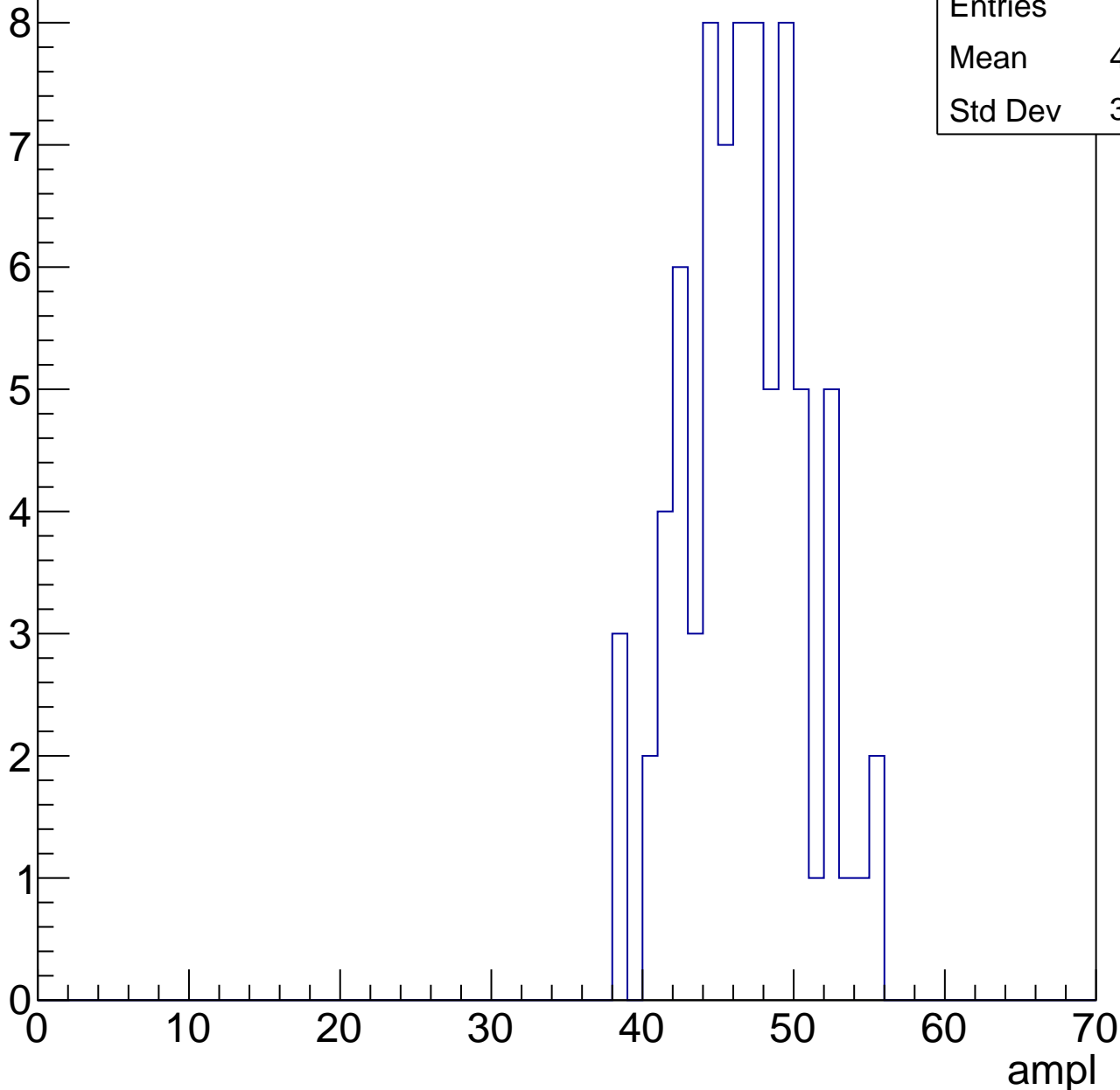


B1L102S, U17-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	46.23
Std Dev	3.918

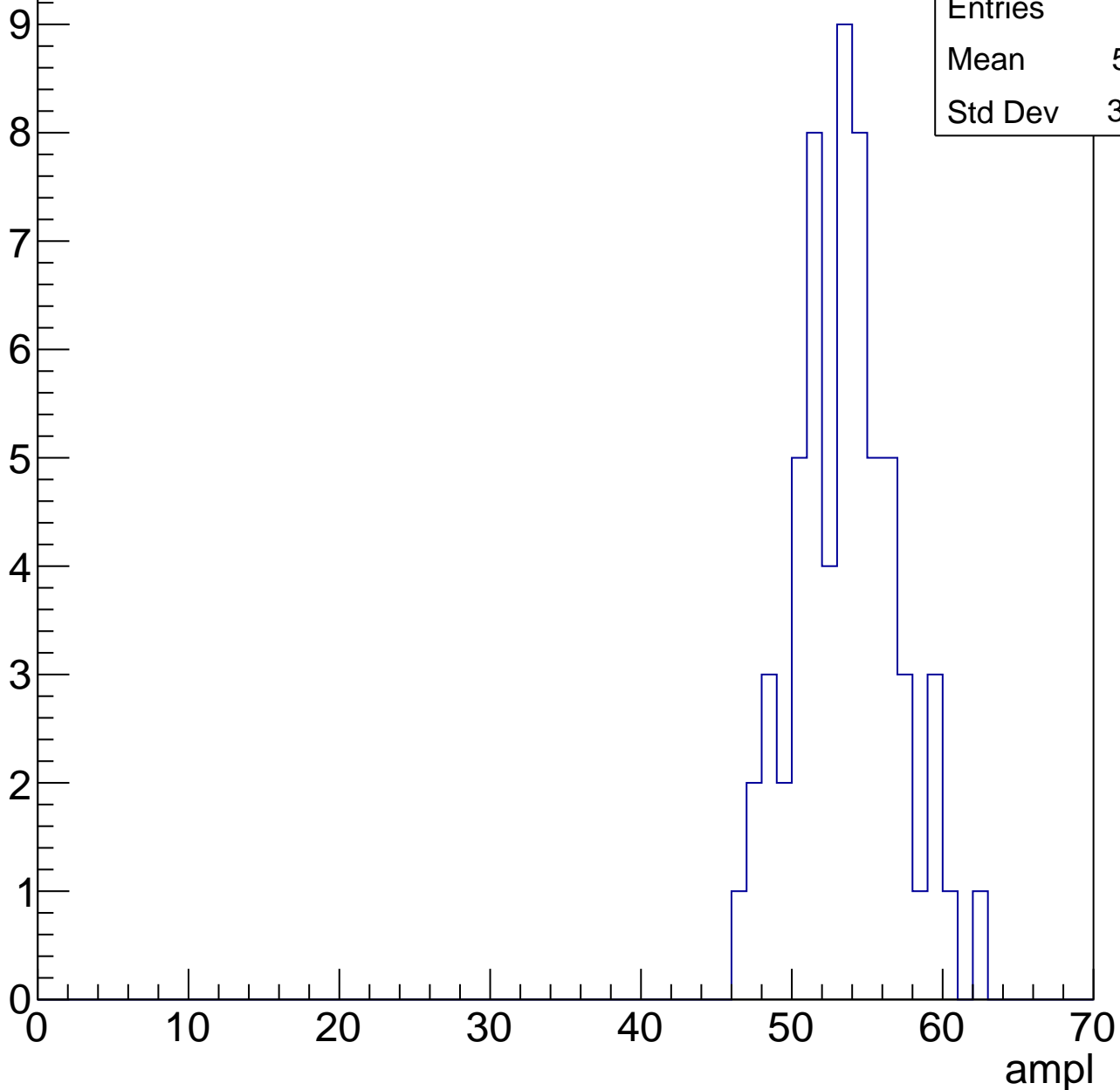


B1L102S, U17-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

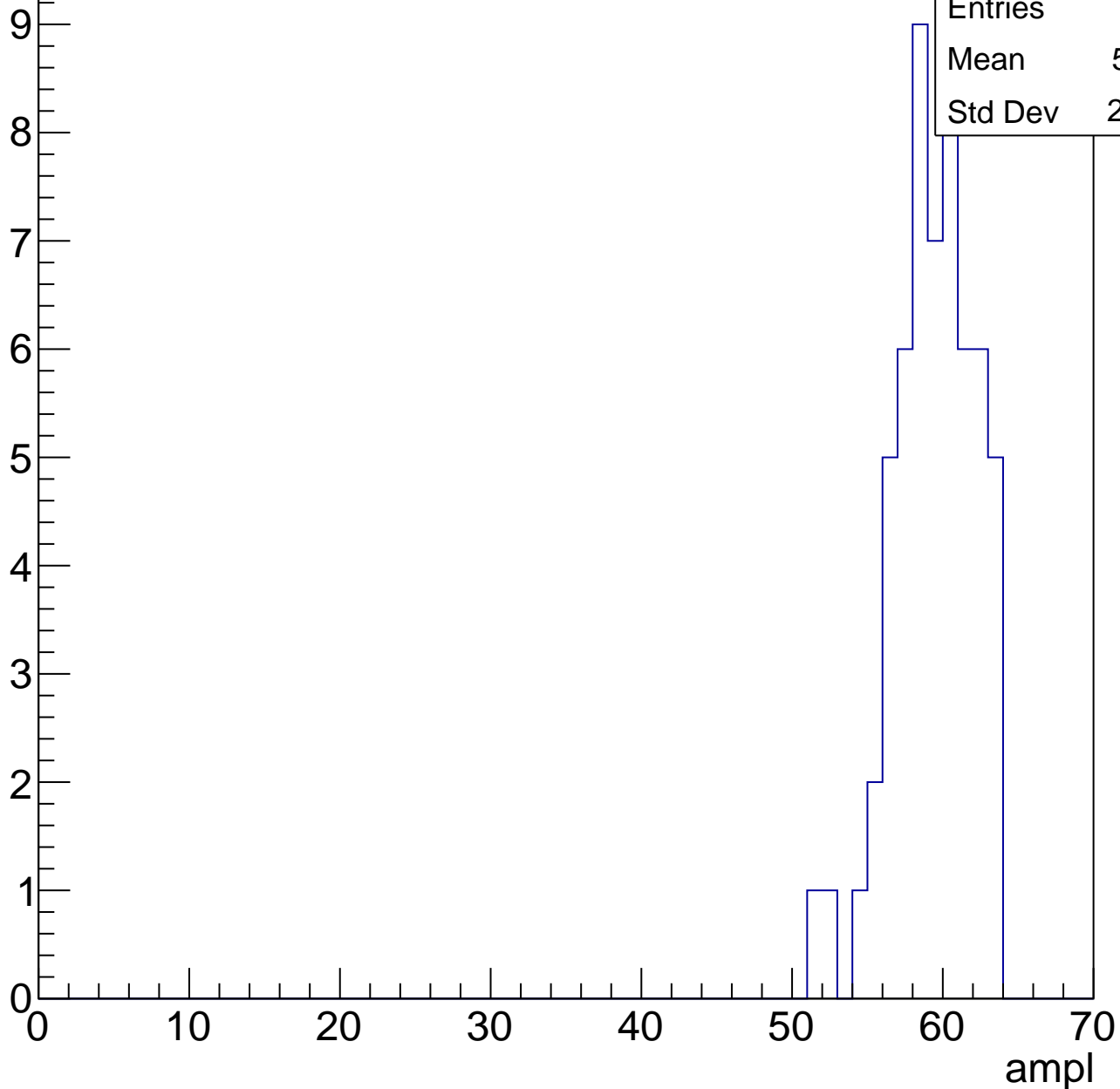
Entries	61
Mean	53.11
Std Dev	3.369



B1L102S, U17-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



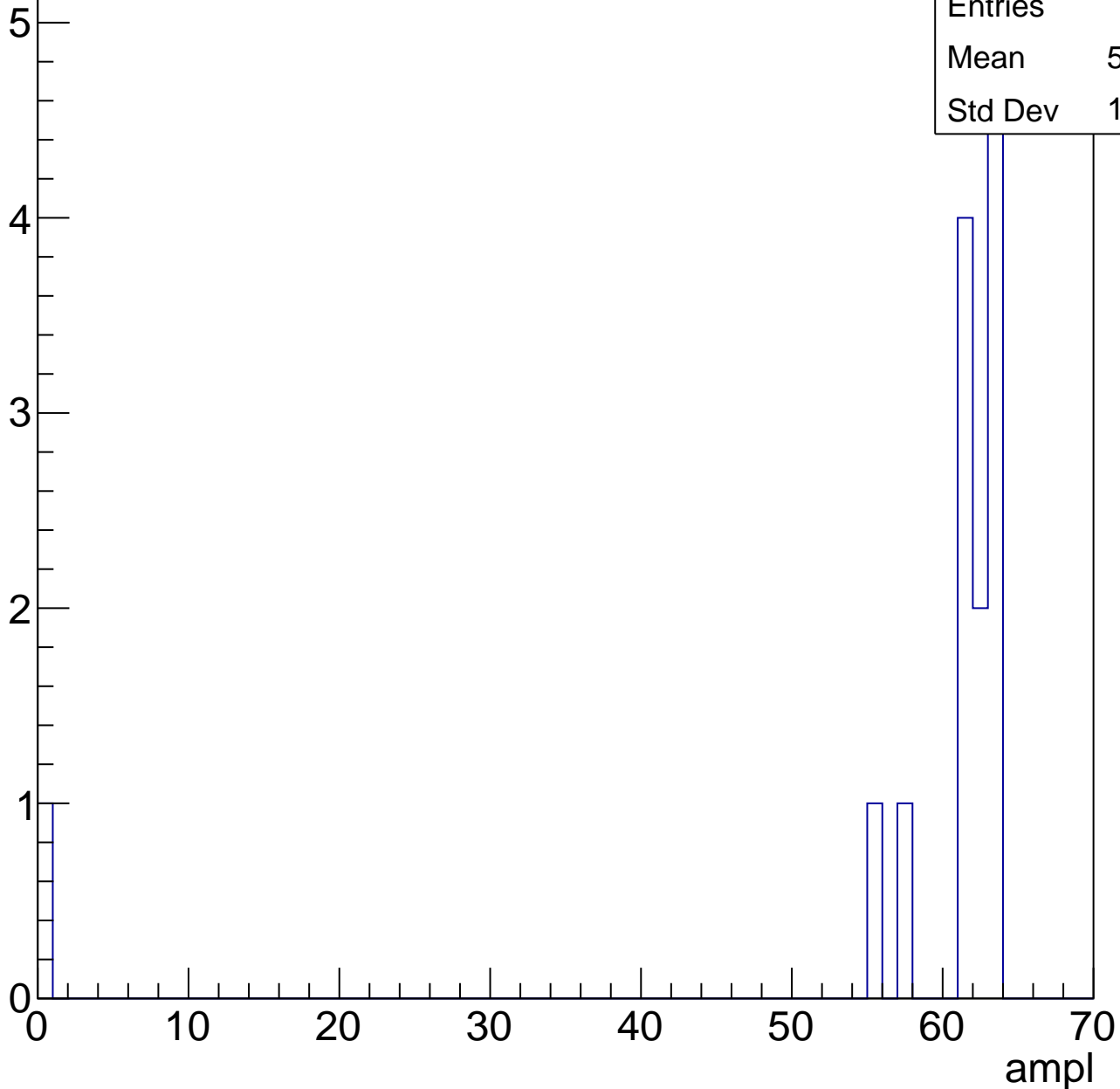
Entries	58
Mean	58.91
Std Dev	2.674

B1L102S, U17-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	56.79
Std Dev	15.92



B1L102S, U17-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch39, adc0

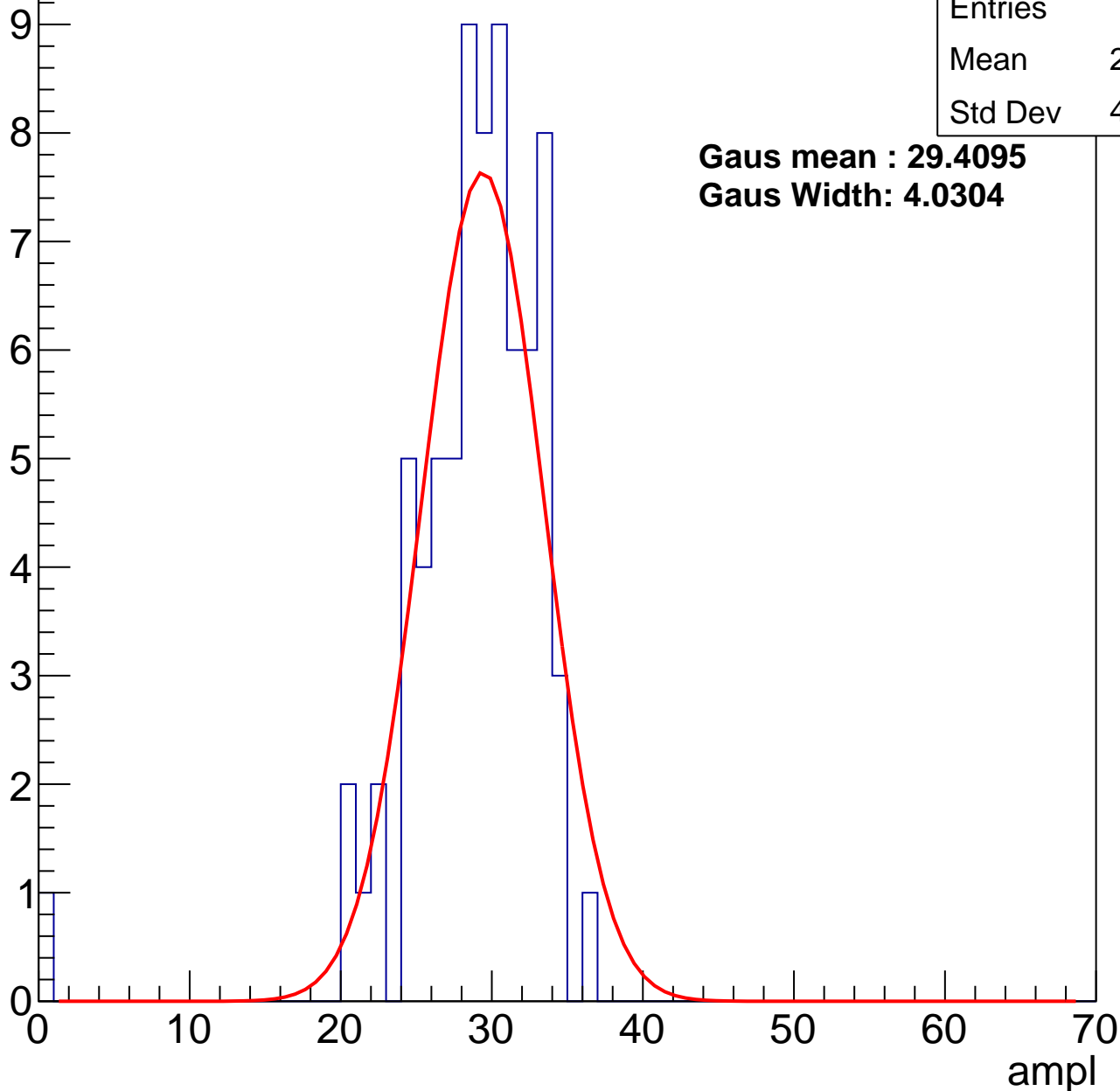
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.32
Std Dev	4.806

Gaus mean : 29.4095

Gaus Width: 4.0304



B1L102S, U17-ch39, adc1

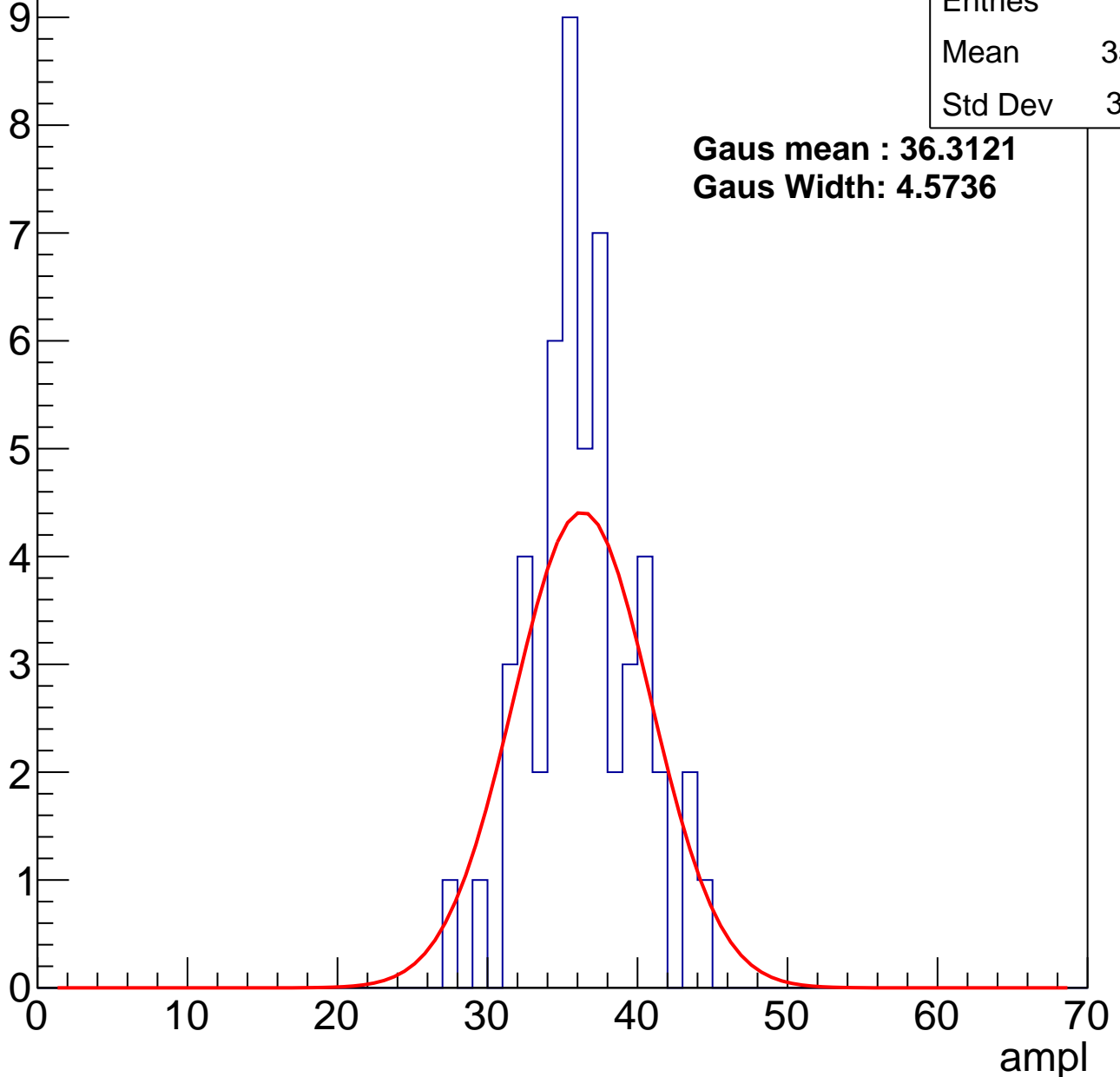
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	35.88
Std Dev	3.501

Gaus mean : 36.3121

Gaus Width: 4.5736



B1L102S, U17-ch39, adc2

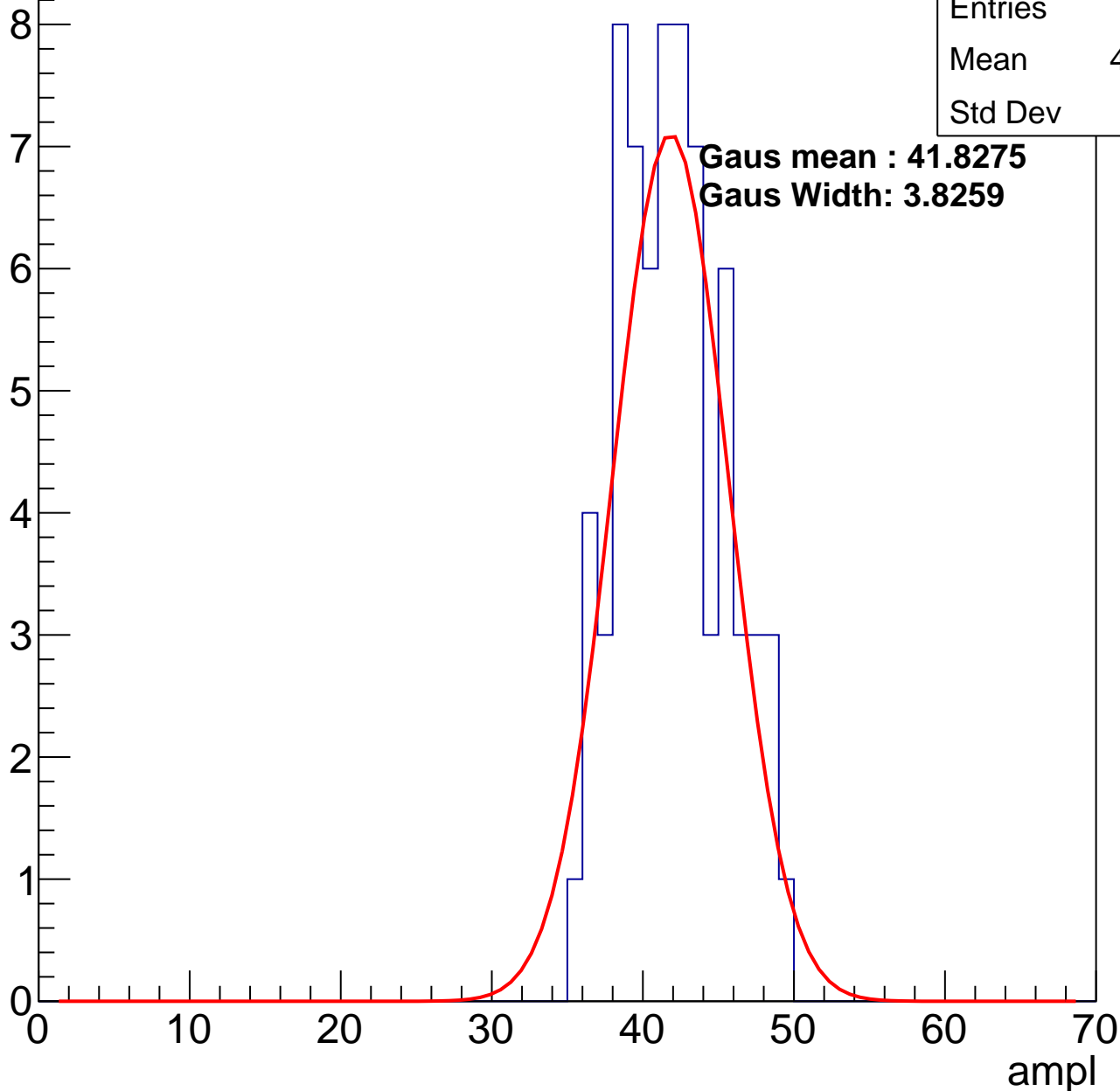
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.49
Std Dev	3.41

Gaus mean : 41.8275

Gaus Width: 3.8259

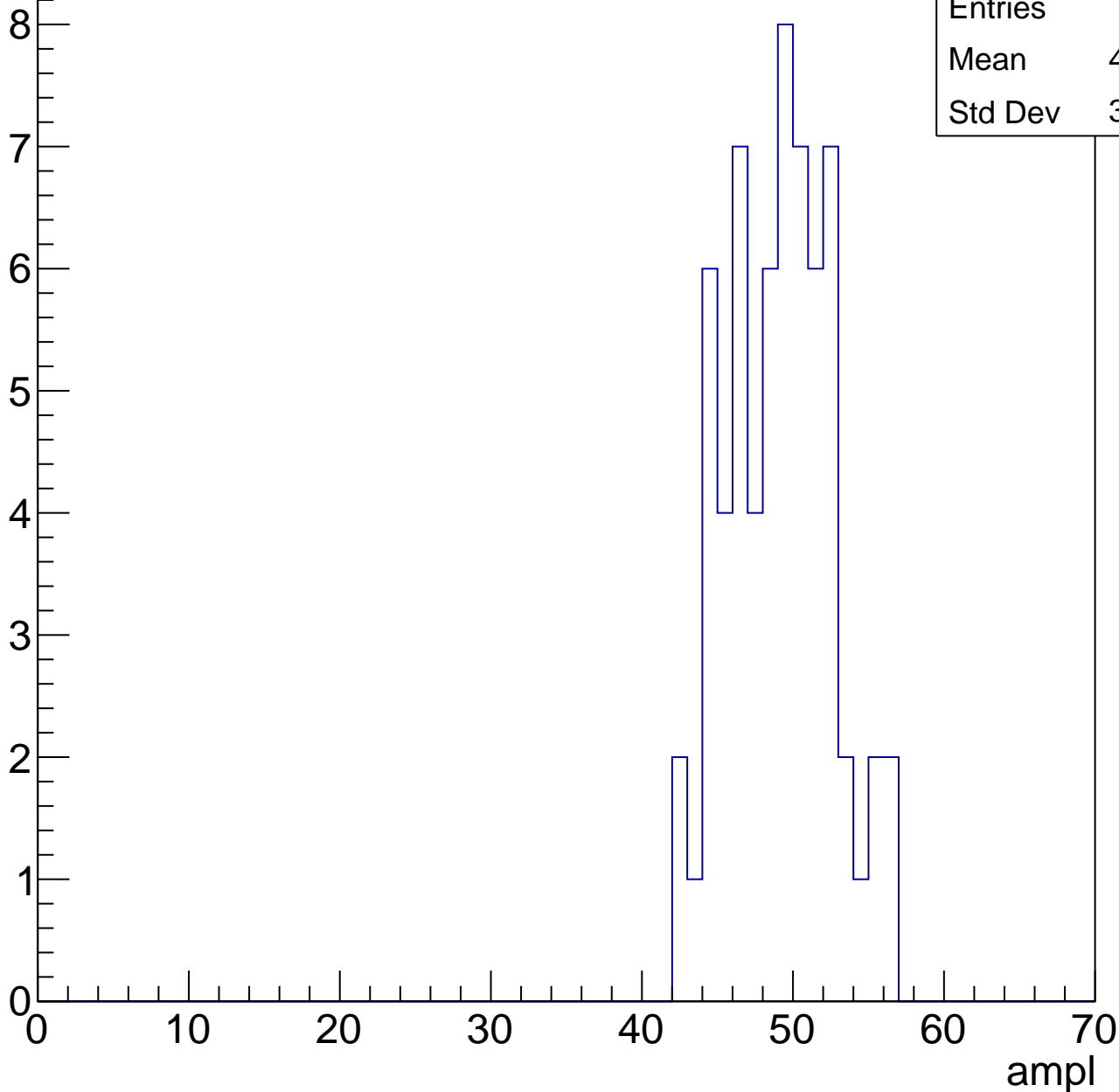


B1L102S, U17-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.66
Std Dev	3.389

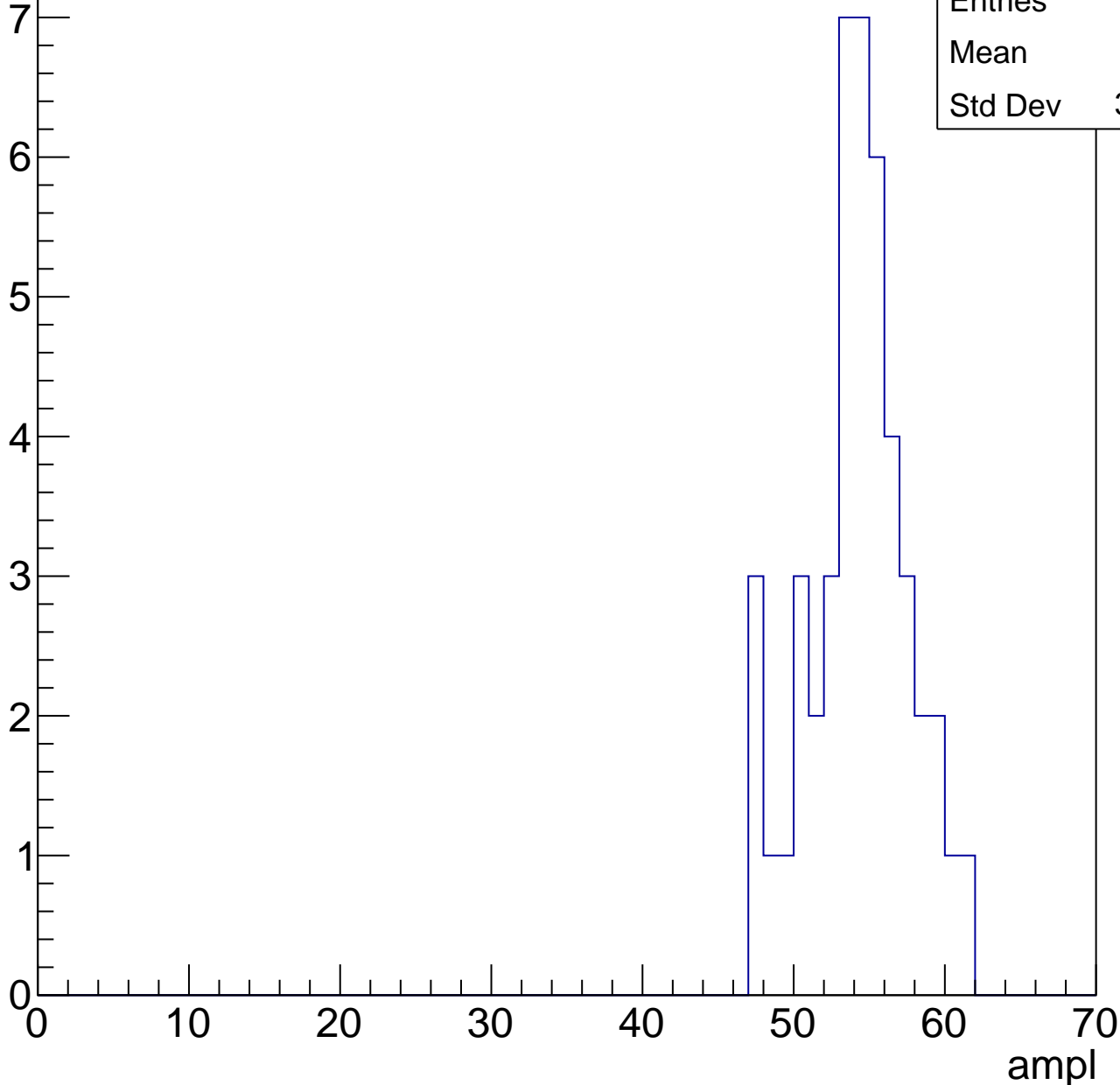


B1L102S, U17-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	53.8
Std Dev	3.321

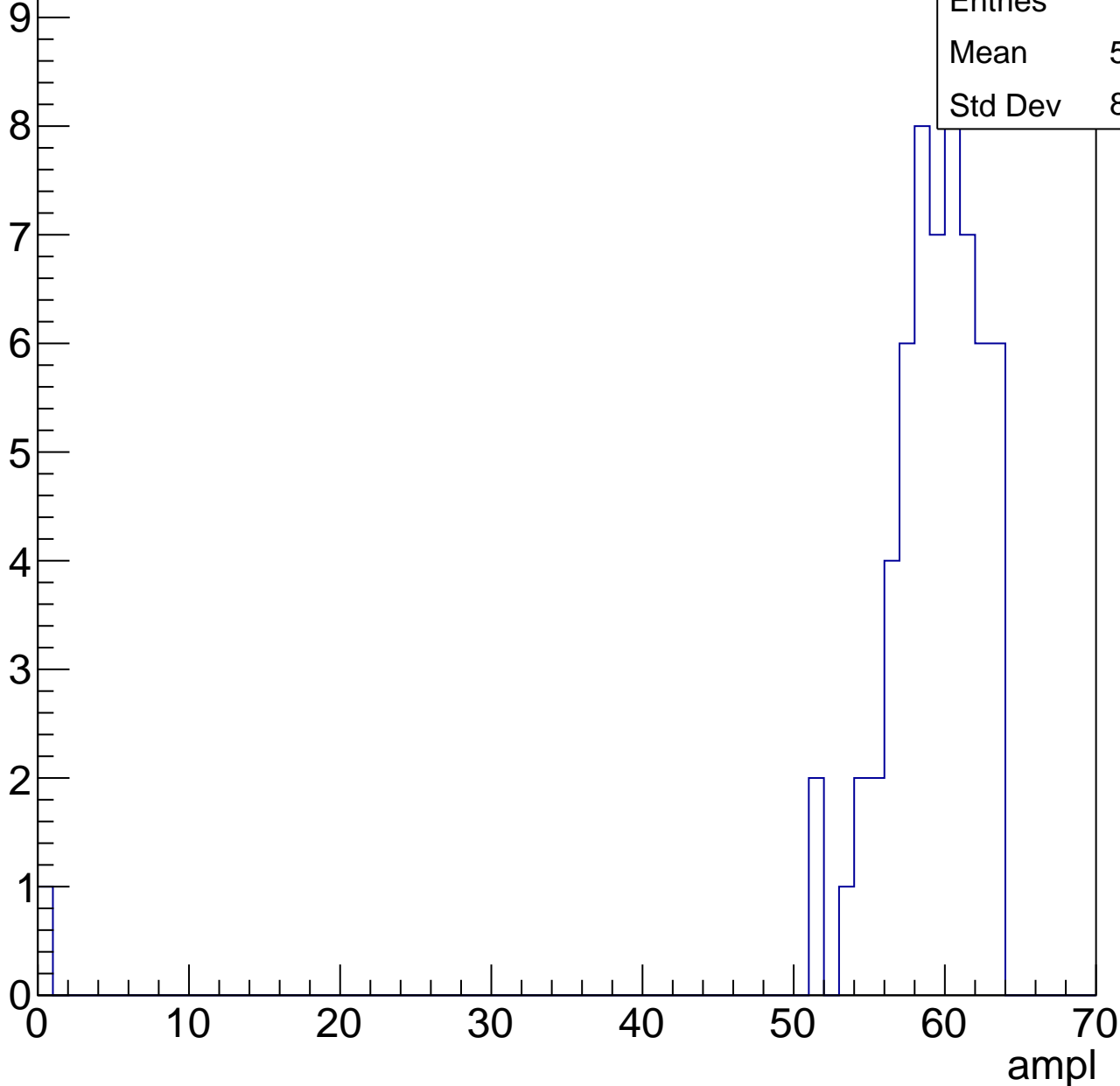


B1L102S, U17-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

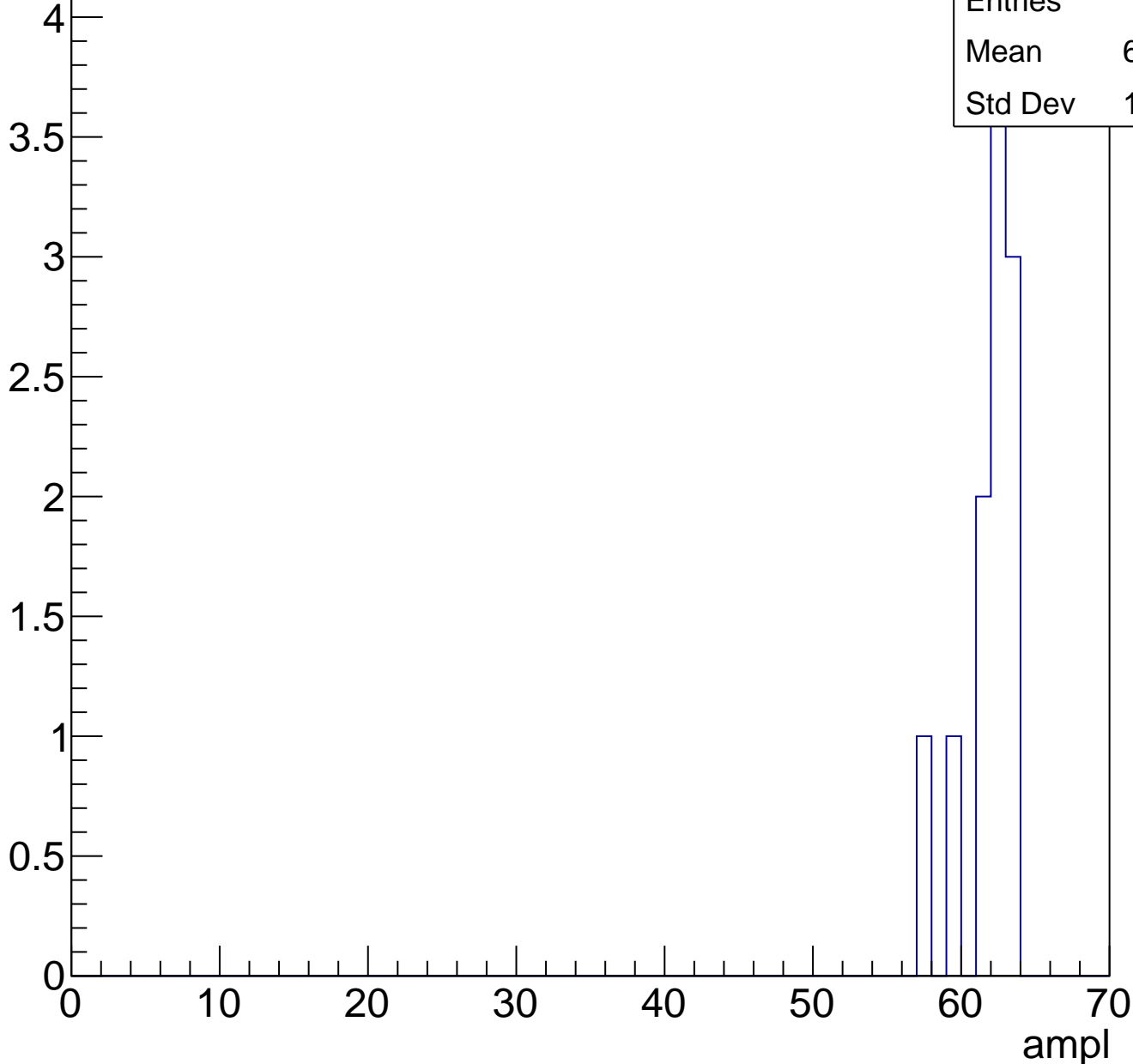
Entries	61
Mean	57.92
Std Dev	8.007



B1L102S, U17-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch40, adc0

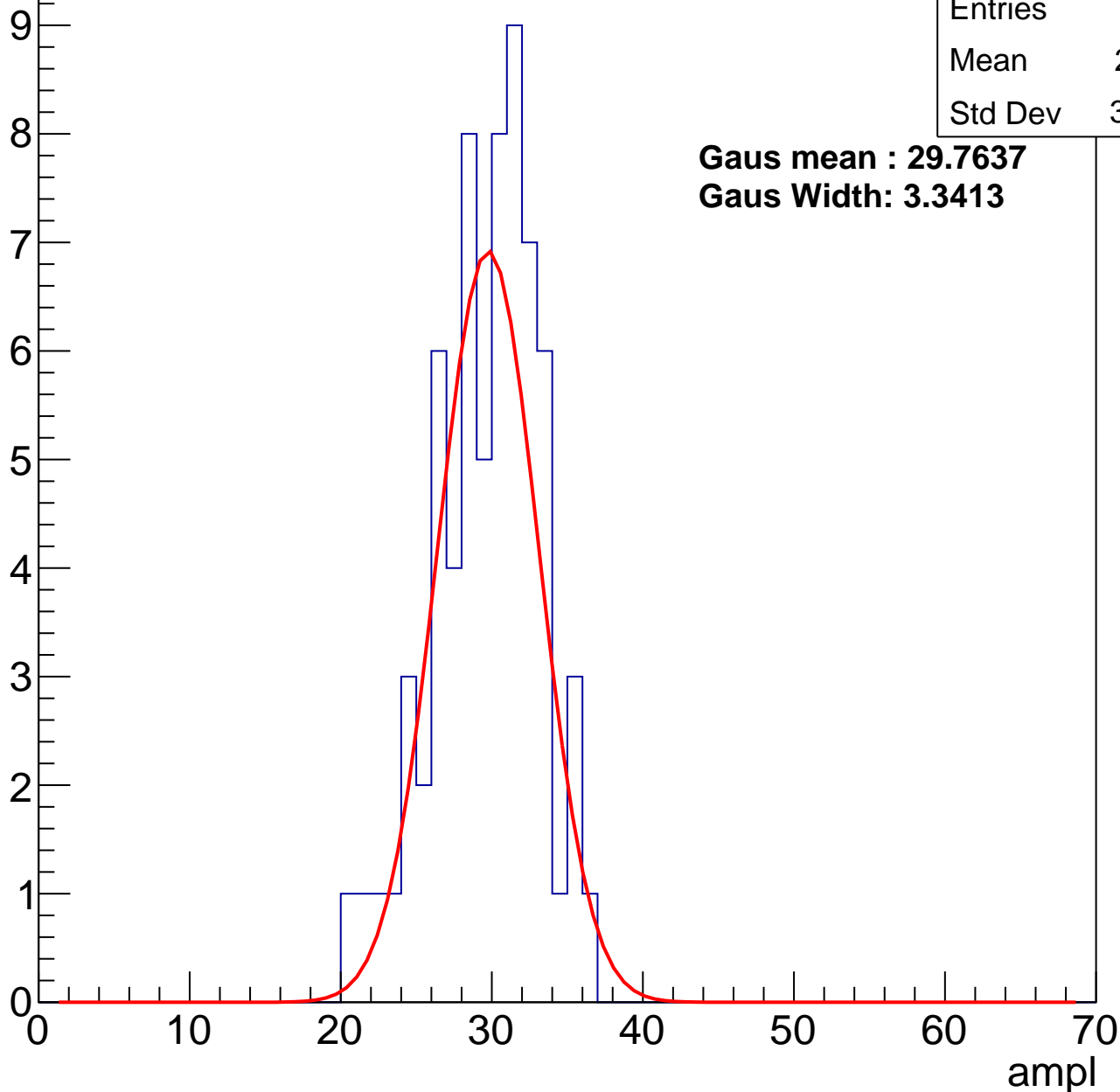
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.21
Std Dev	3.449

Gaus mean : 29.7637

Gaus Width: 3.3413



B1L102S, U17-ch40, adc1

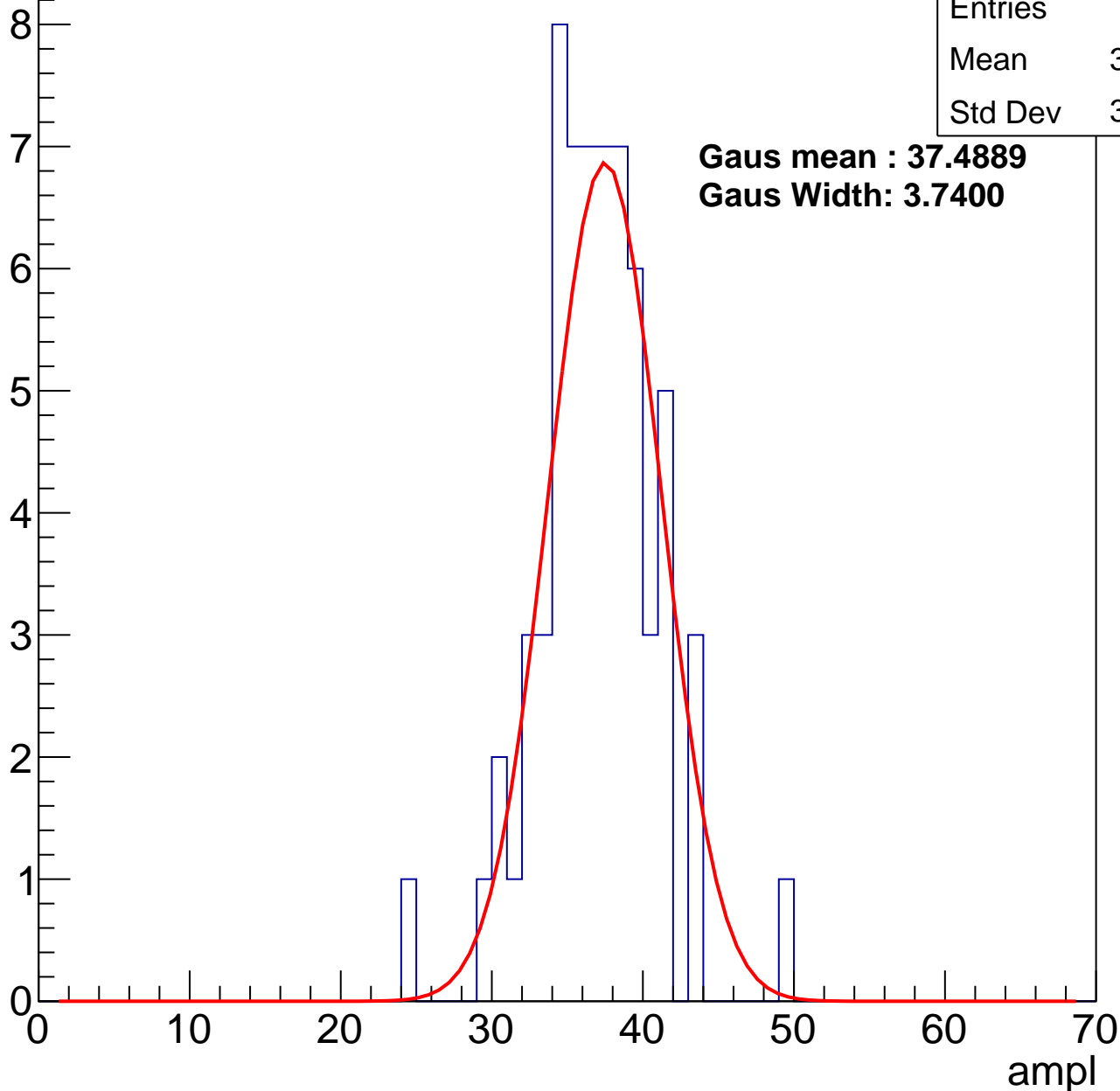
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.46
Std Dev	3.859

Gaus mean : 37.4889

Gaus Width: 3.7400

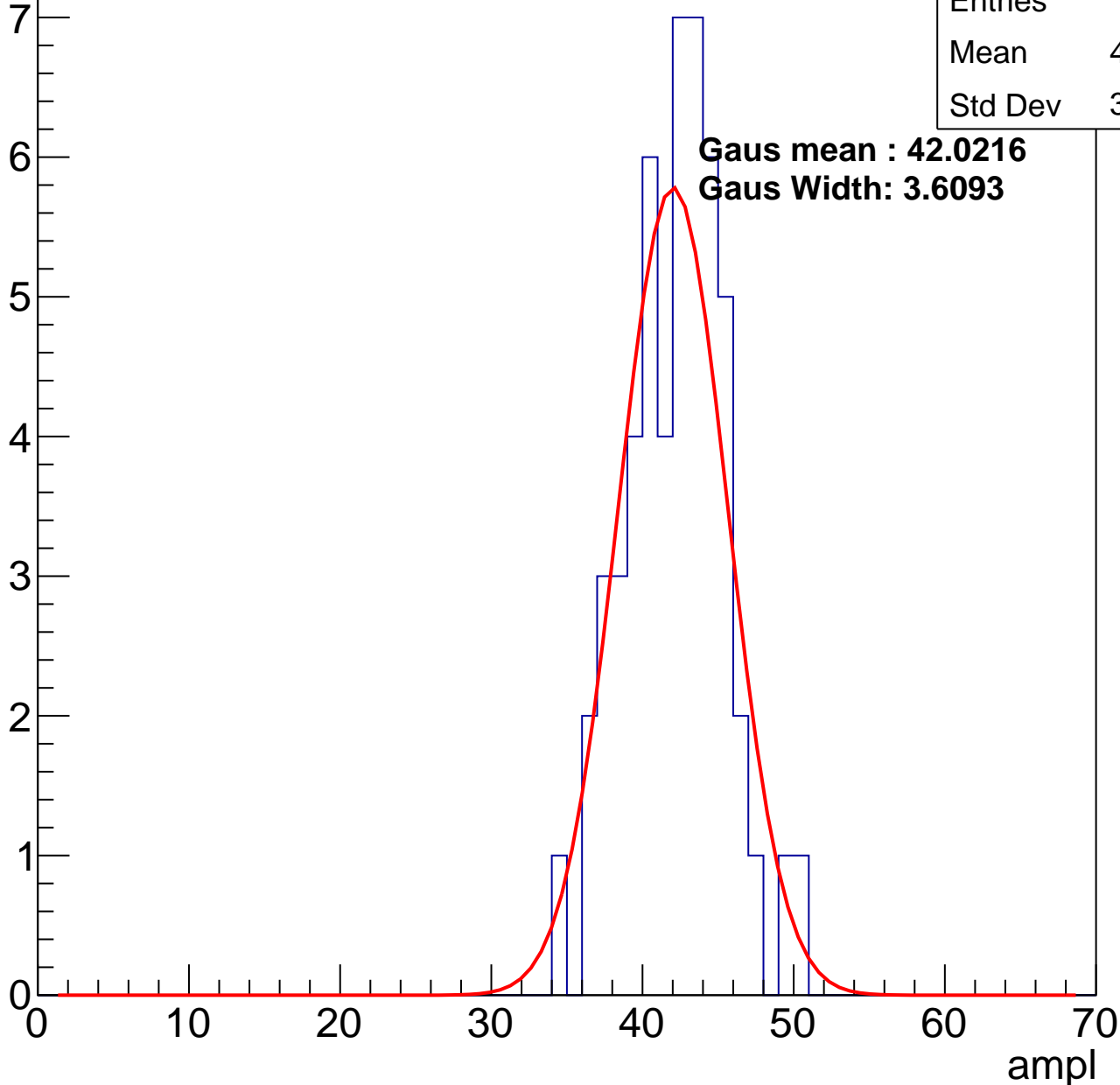


B1L102S, U17-ch40, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.75
Std Dev	3.267

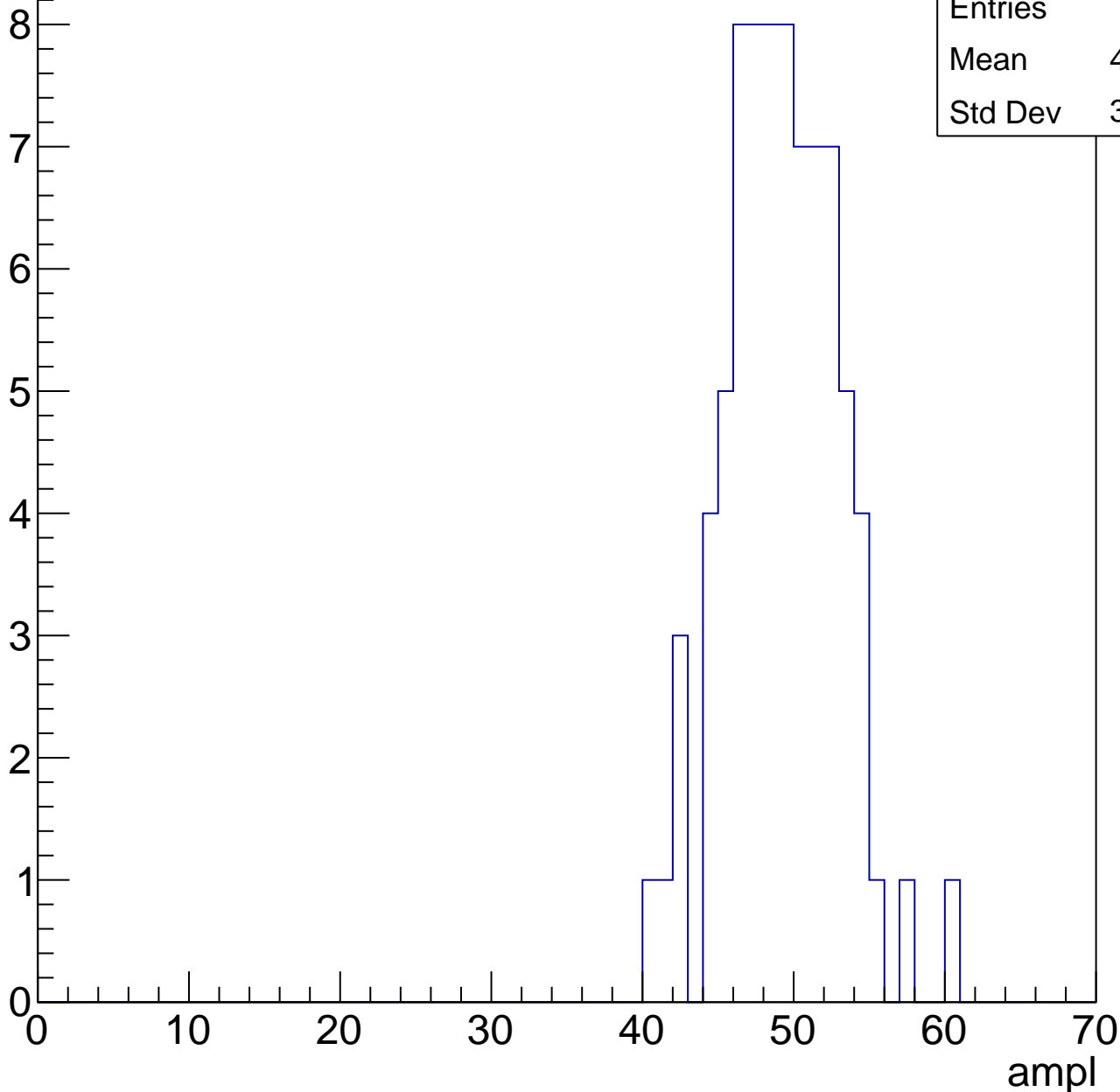


B1L102S, U17-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	48.76
Std Dev	3.695

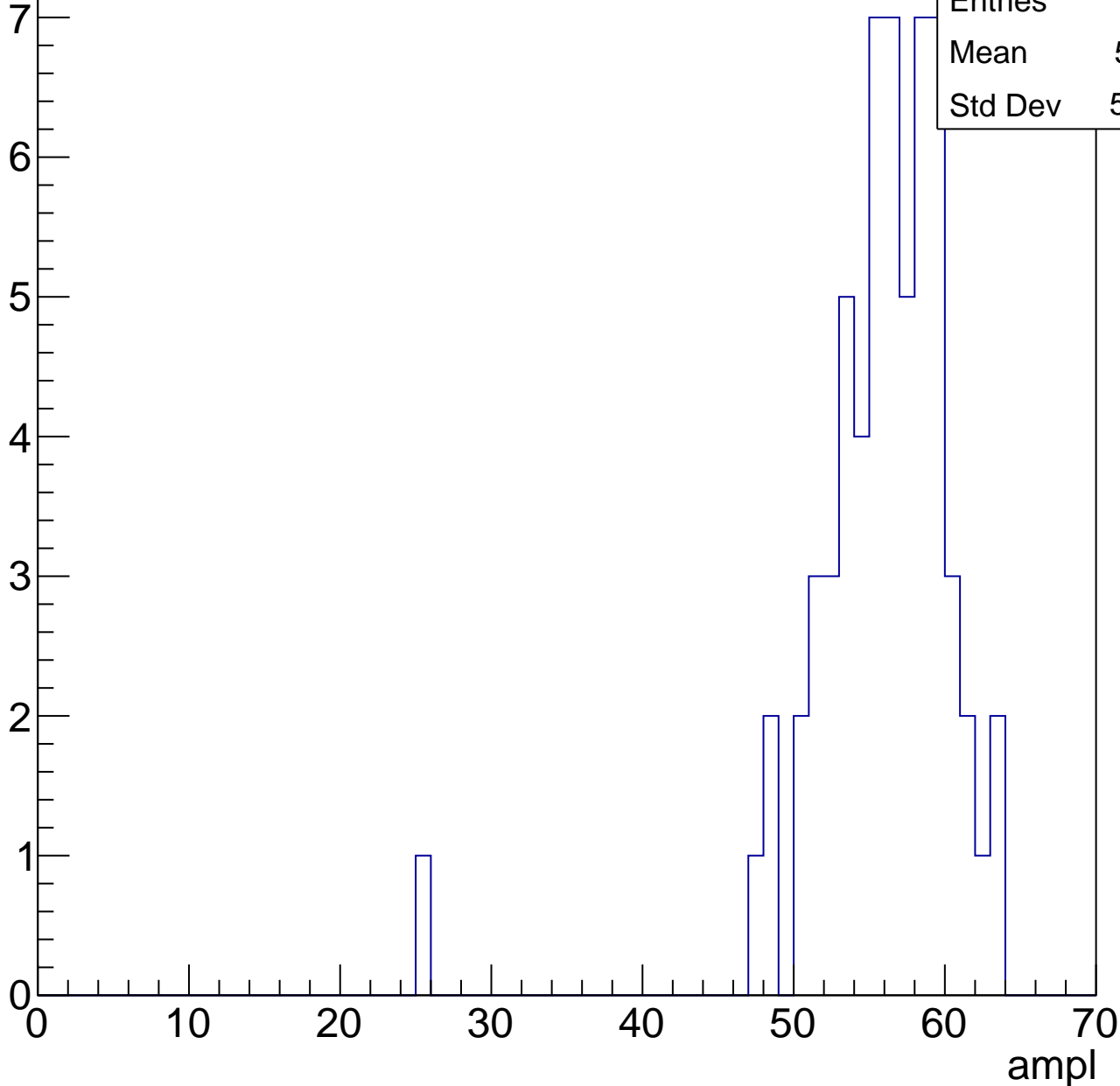


B1L102S, U17-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.31
Std Dev	5.284

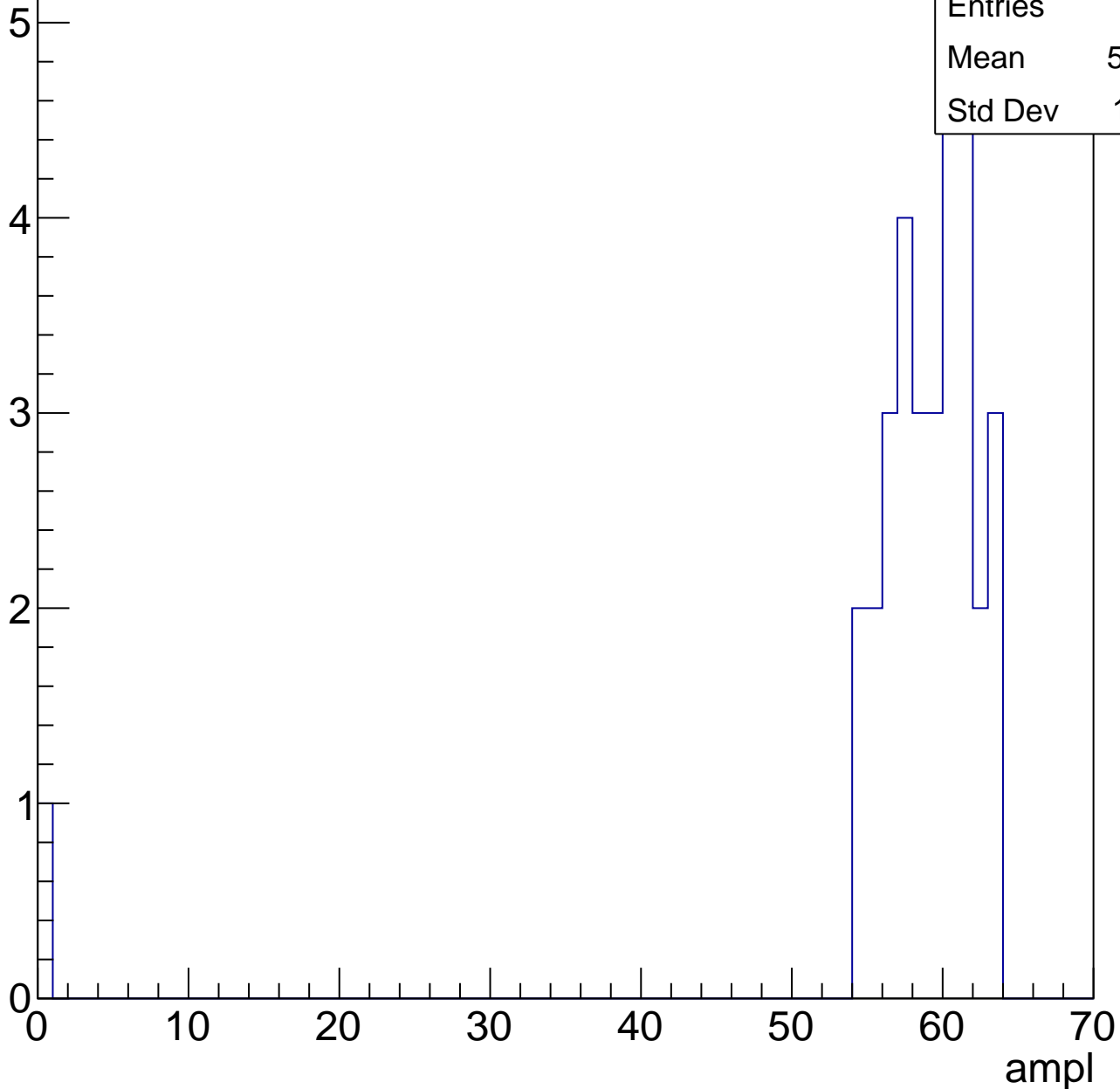


B1L102S, U17-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	57.06
Std Dev	10.41

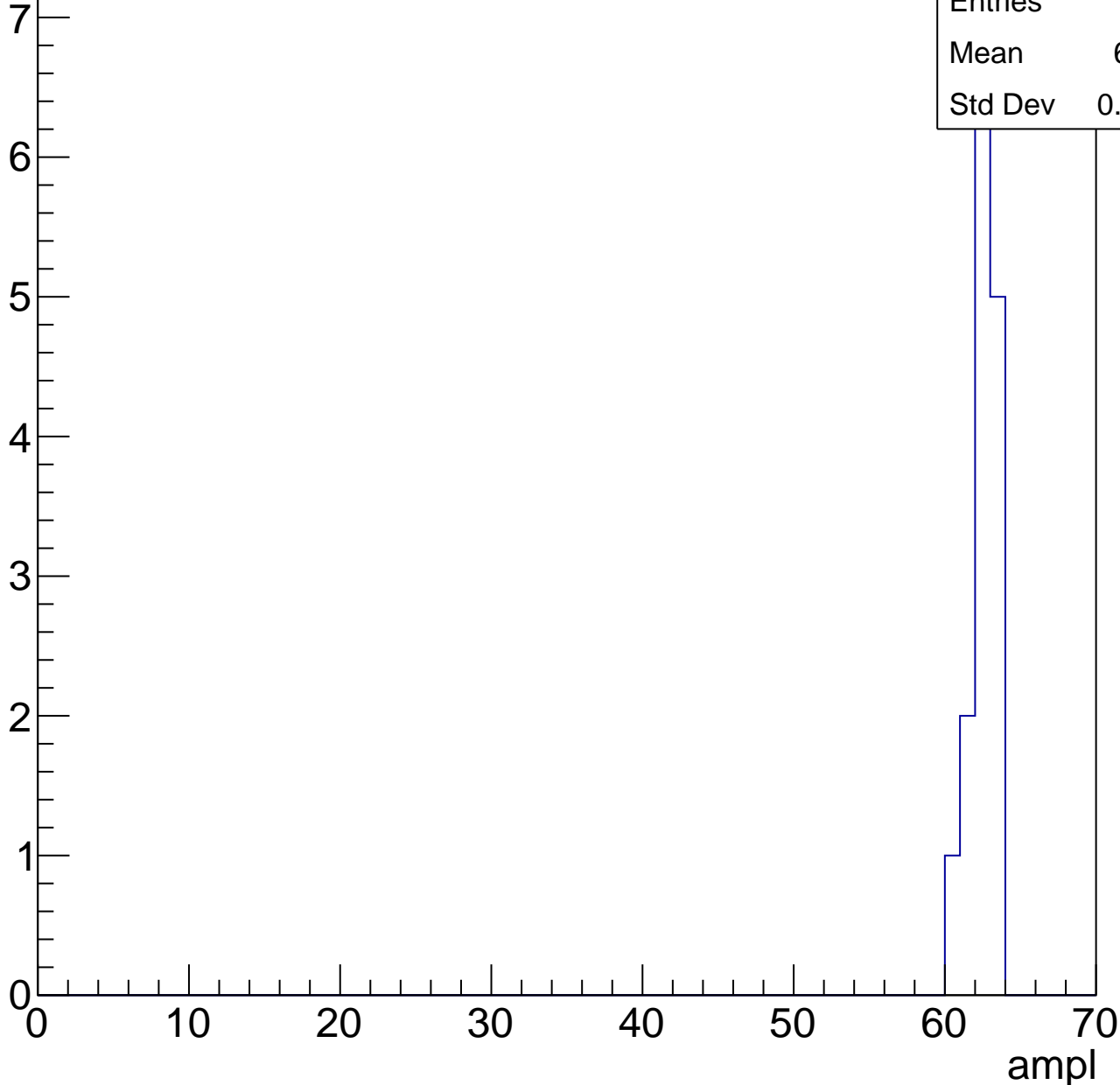


B1L102S, U17-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

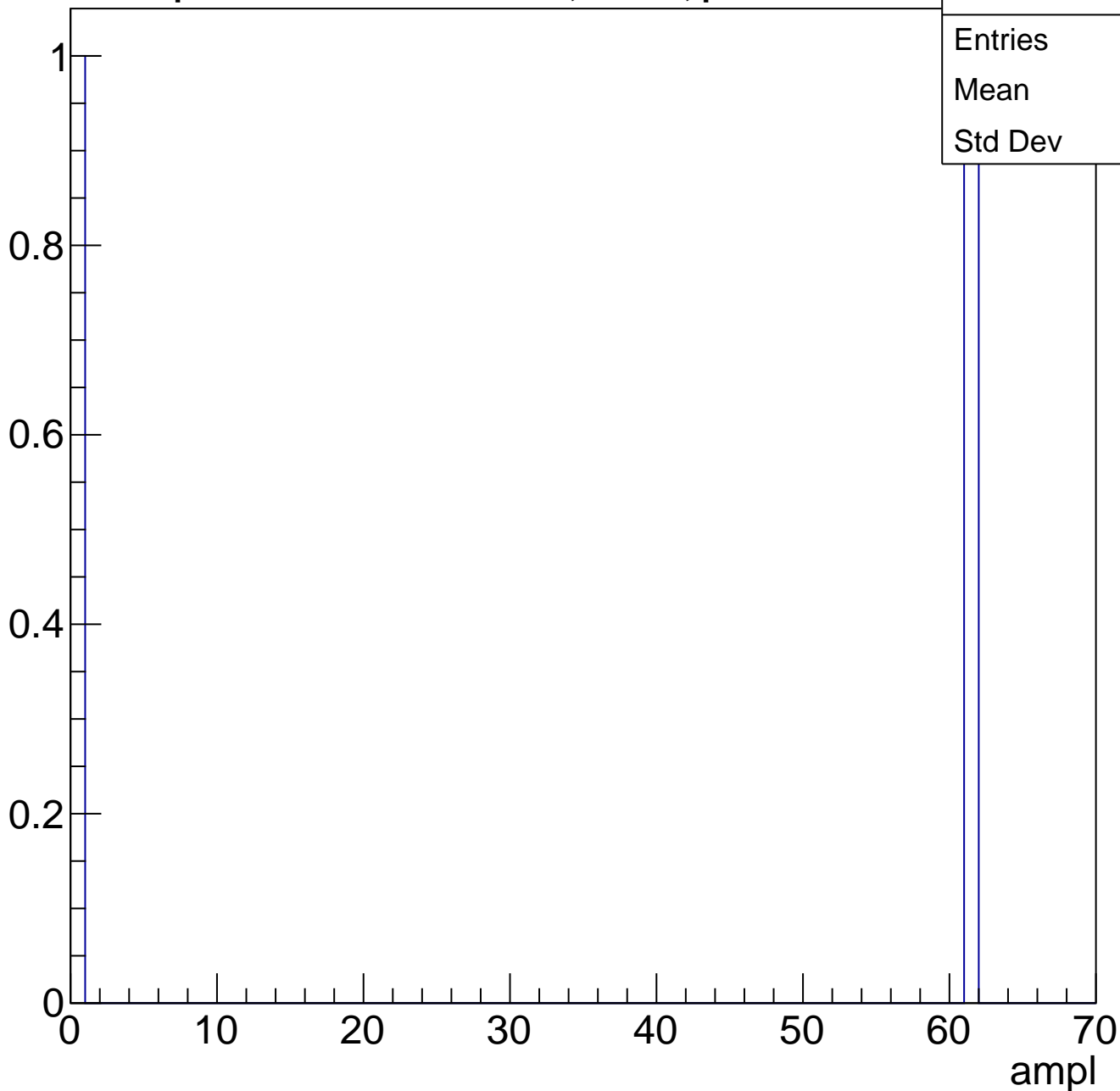
Entries	15
Mean	62.07
Std Dev	0.8537



B1L102S, U17-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch41, adc0

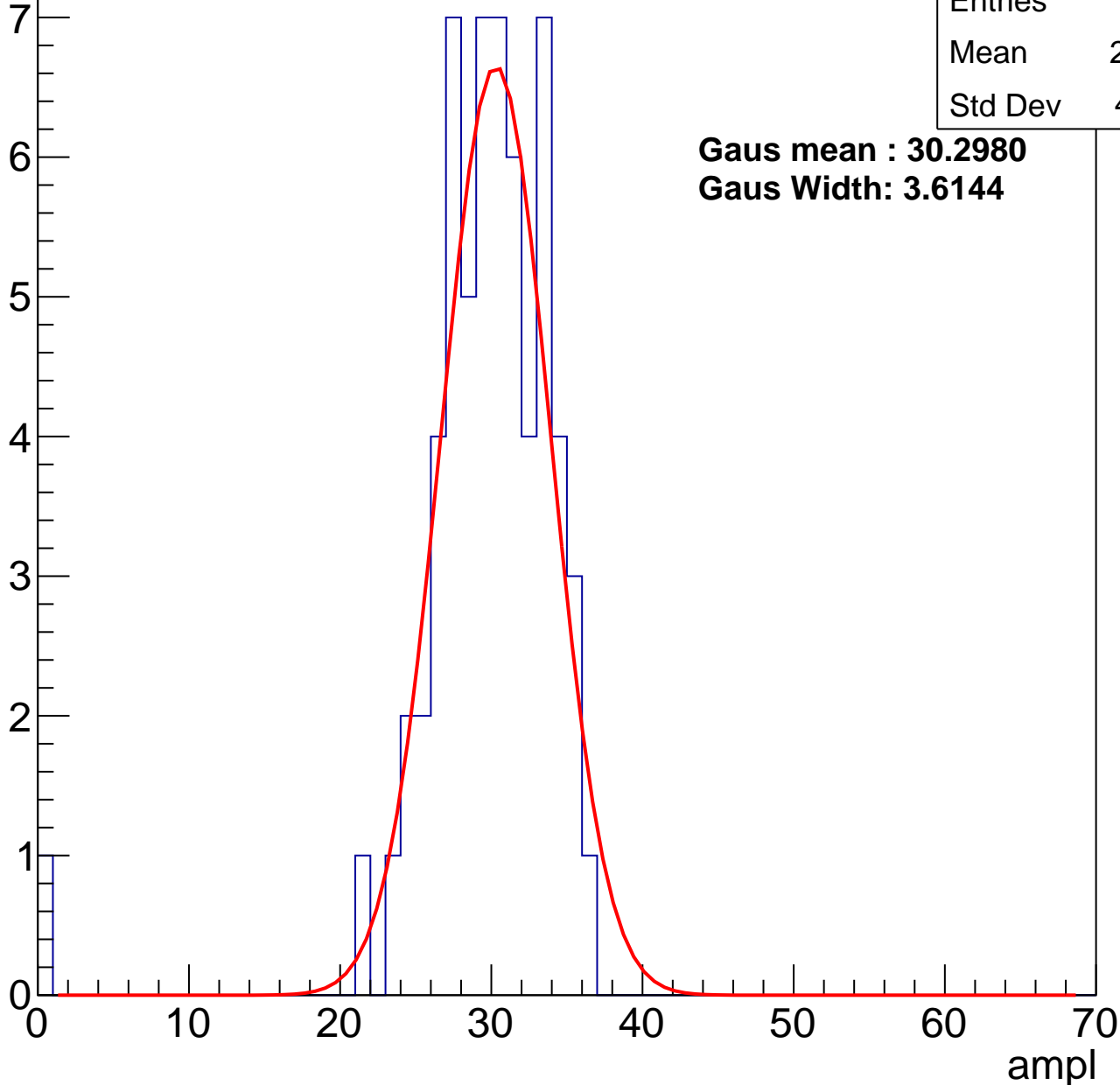
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.19
Std Dev	4.961

Gaus mean : 30.2980

Gaus Width: 3.6144



B1L102S, U17-ch41, adc1

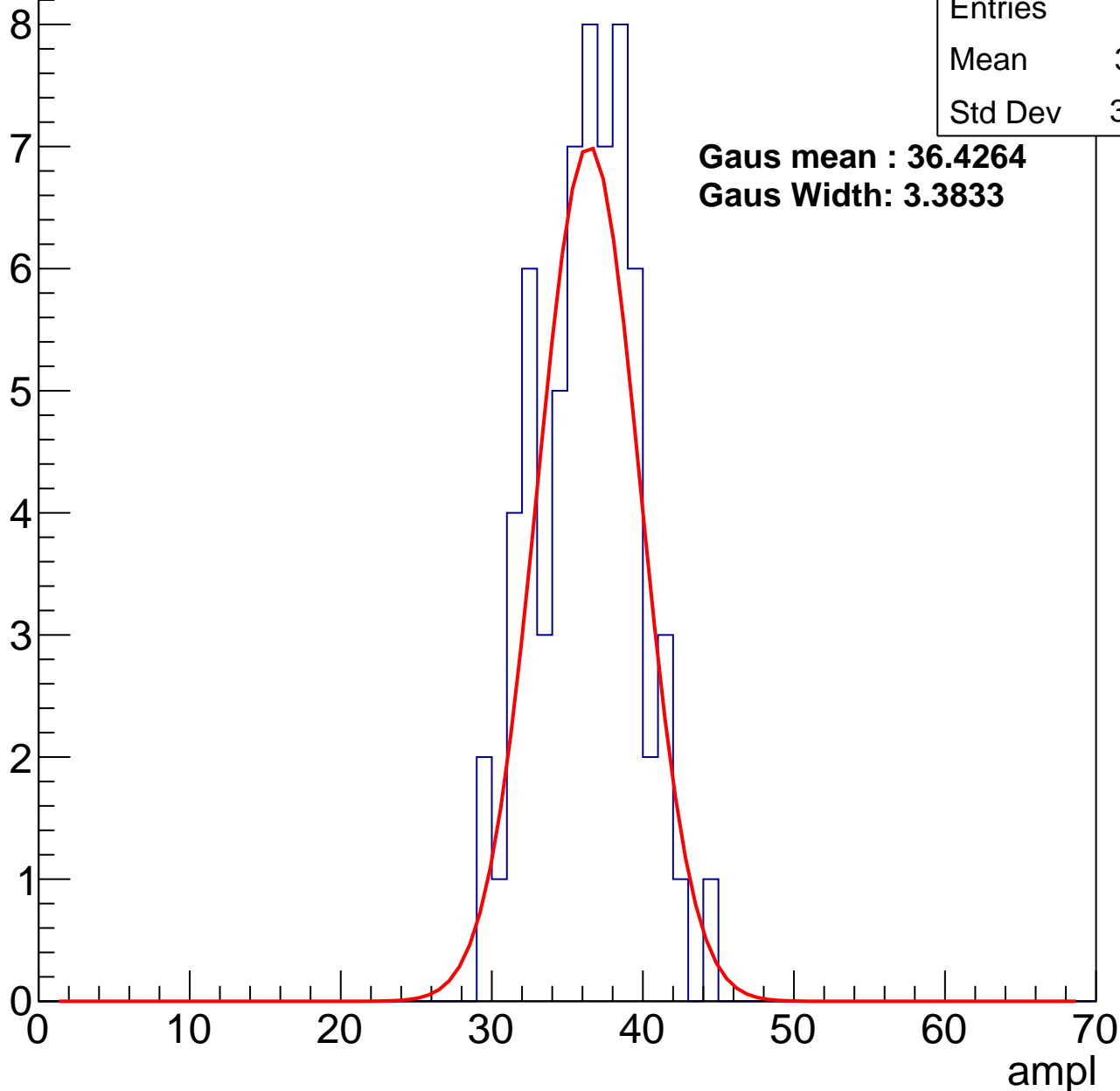
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.81
Std Dev	3.264

Gaus mean : 36.4264

Gaus Width: 3.3833



B1L102S, U17-ch41, adc2

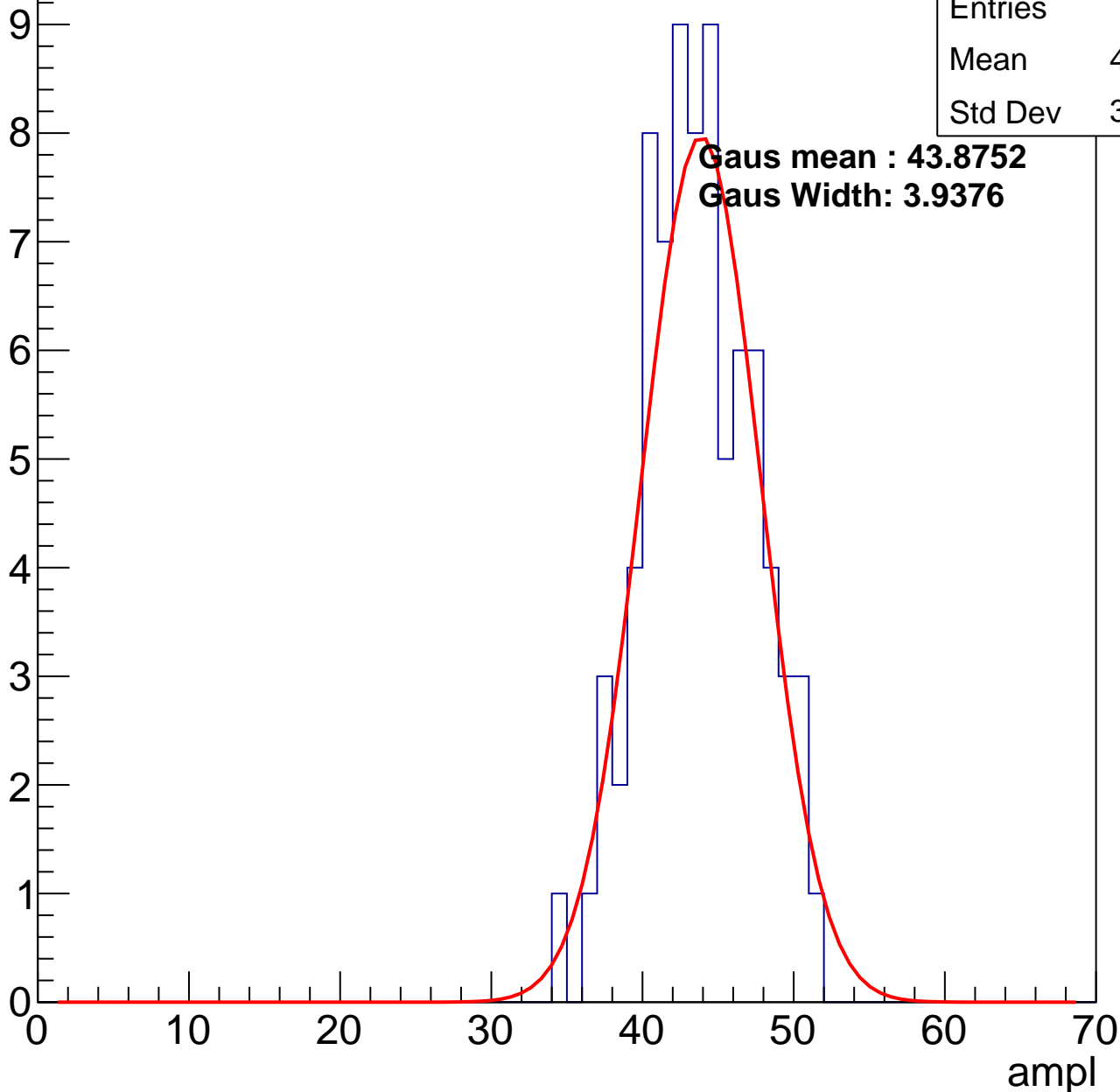
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	43.26
Std Dev	3.629

Gaus mean : 43.8752

Gaus Width: 3.9376

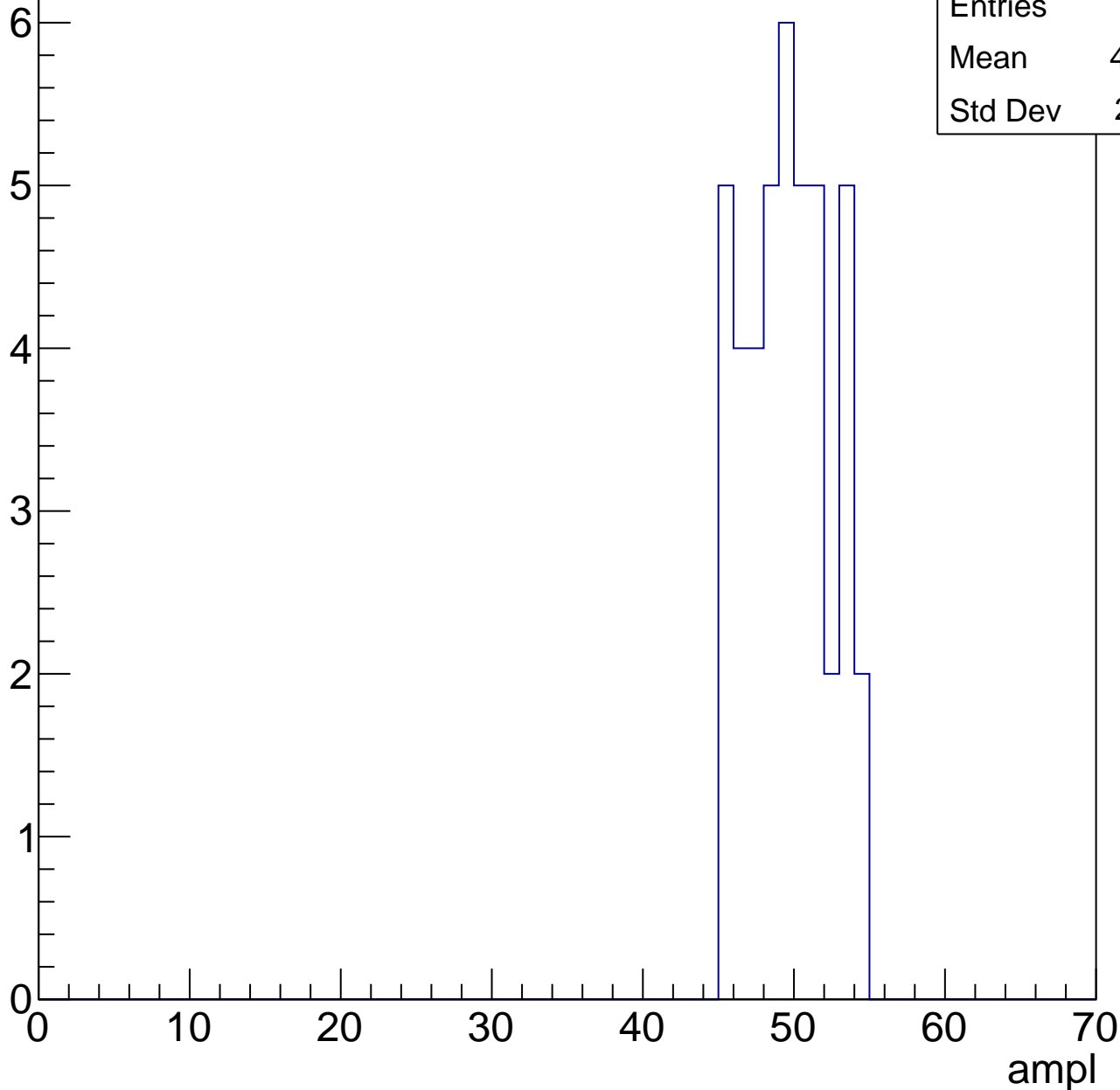


B1L102S, U17-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	49.14
Std Dev	2.681

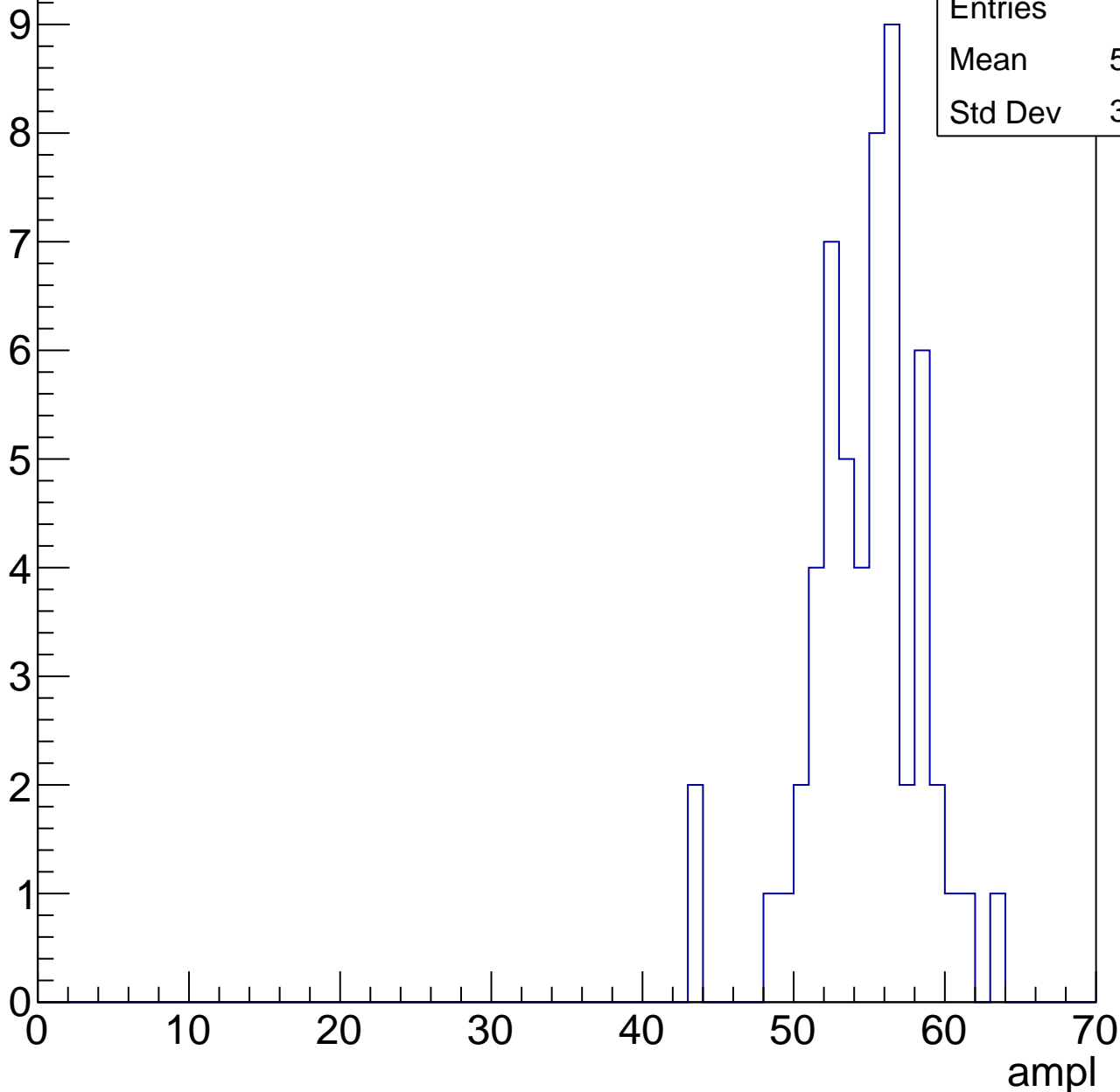


B1L102S, U17-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

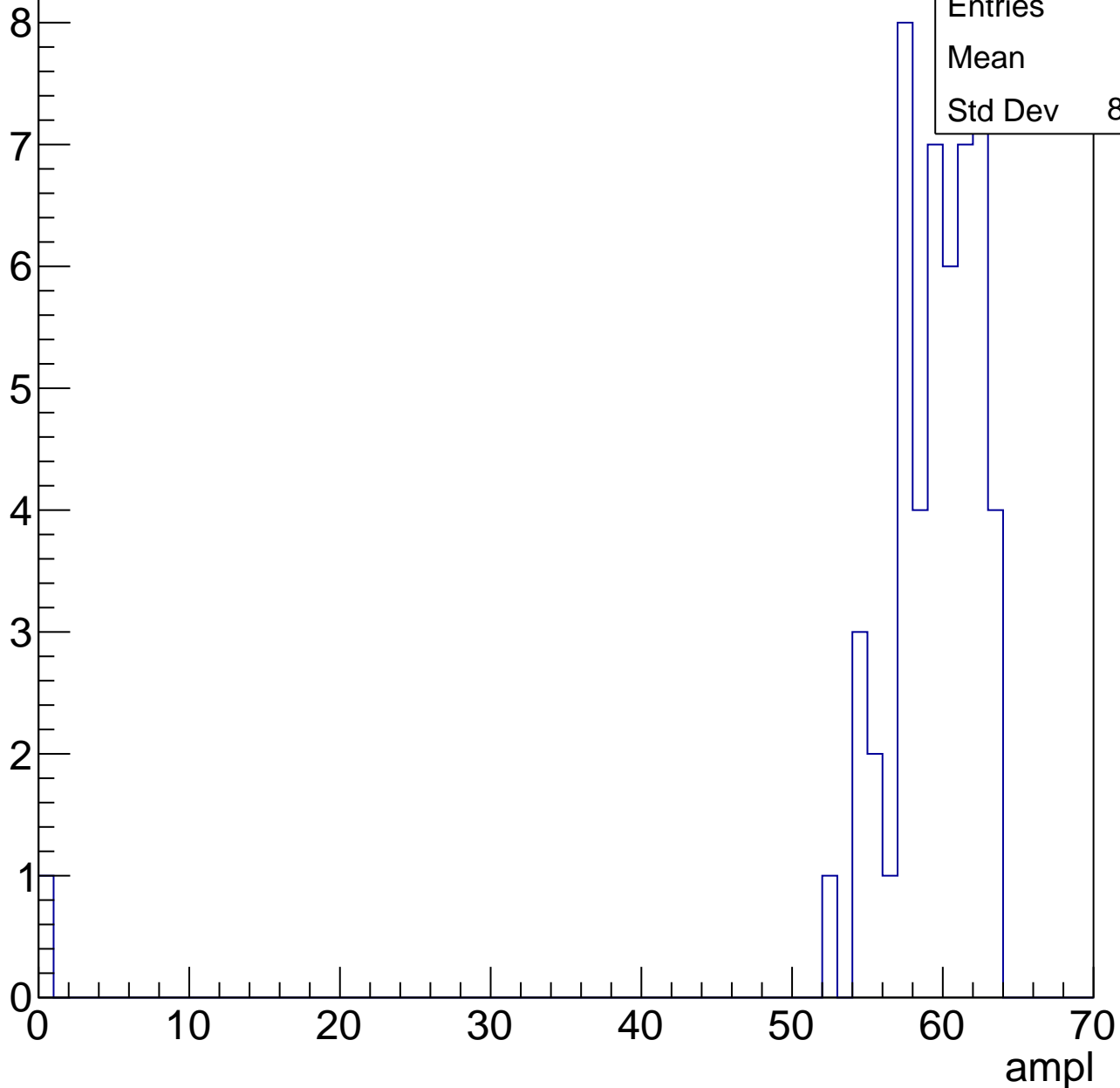
Entries	56
Mean	54.29
Std Dev	3.716



B1L102S, U17-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

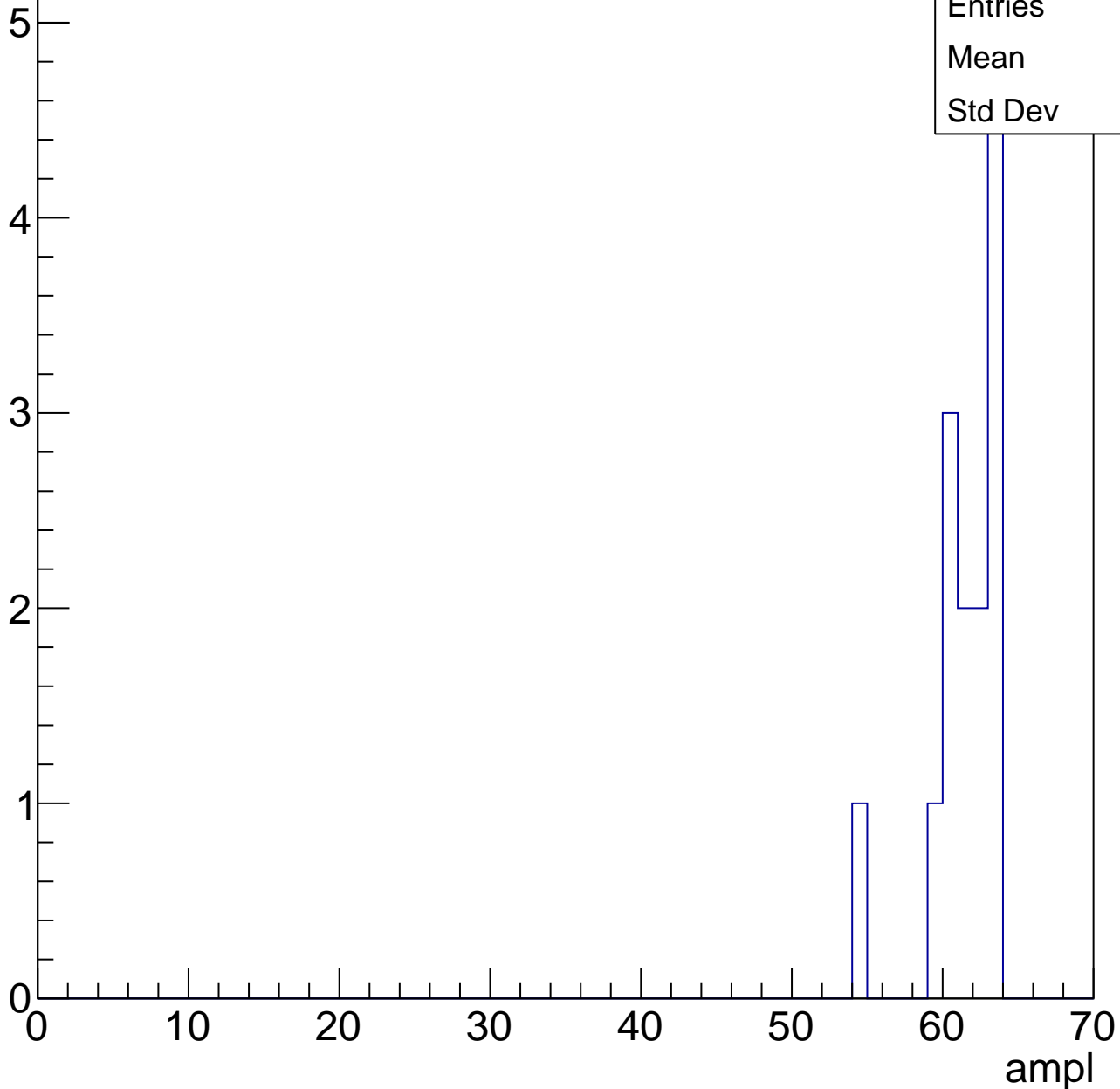


B1L102S, U17-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61
Std Dev	2.36



B1L102S, U17-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch42, adc0

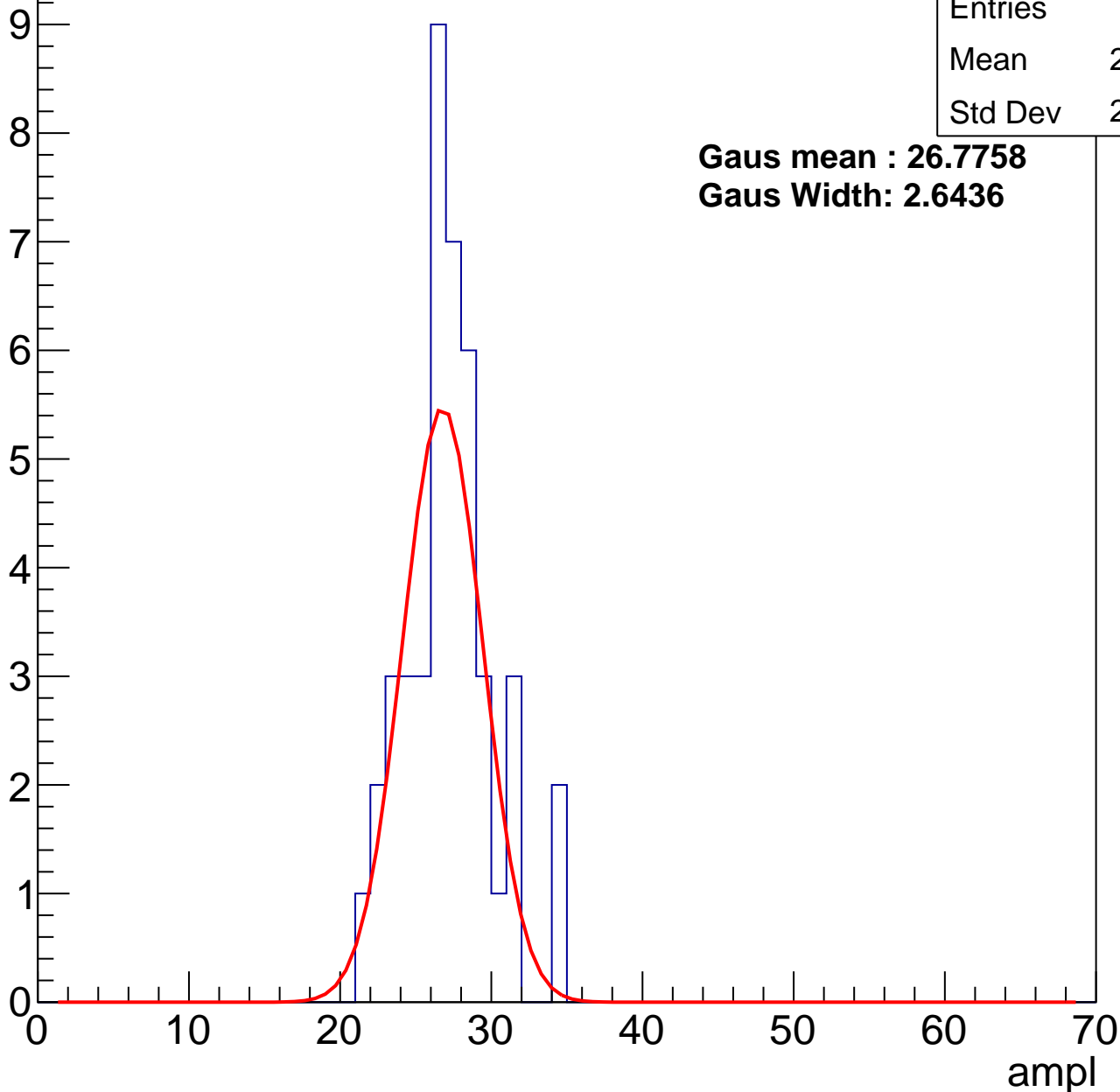
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	26.74
Std Dev	2.862

Gaus mean : 26.7758

Gaus Width: 2.6436



B1L102S, U17-ch42, adc1

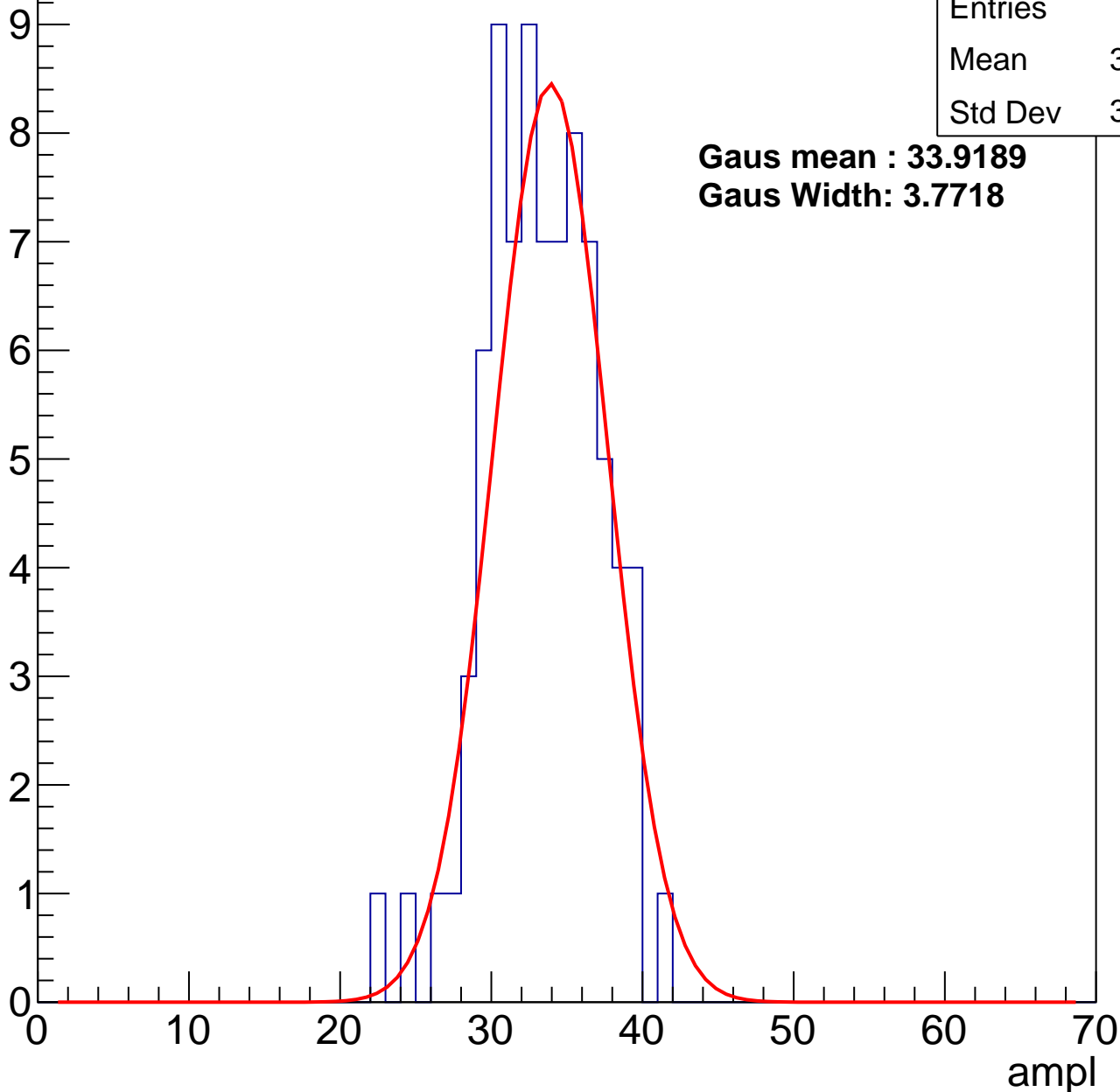
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	32.93
Std Dev	3.627

Gaus mean : 33.9189

Gaus Width: 3.7718



B1L102S, U17-ch42, adc2

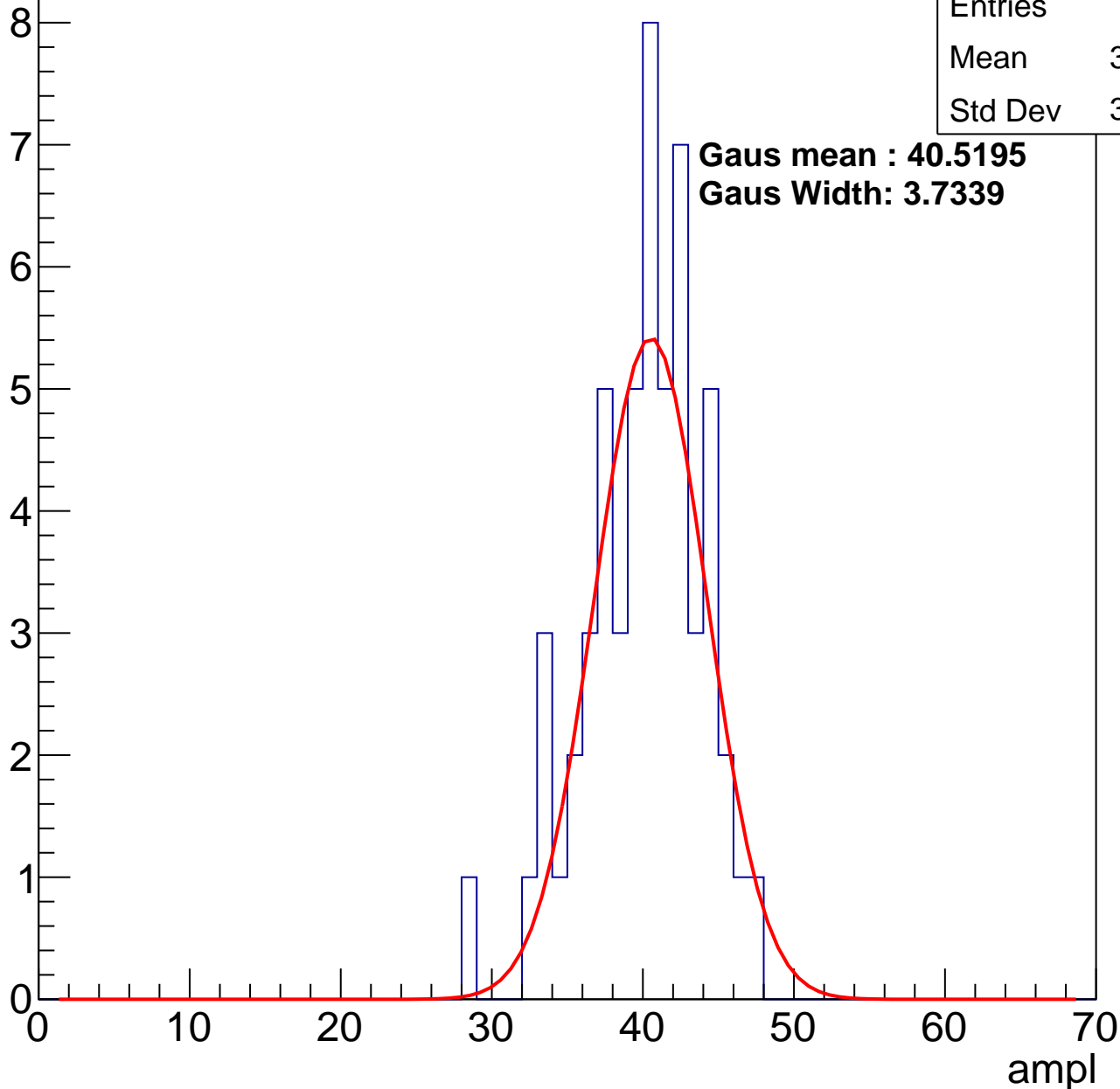
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	39.57
Std Dev	3.817

Gaus mean : 40.5195

Gaus Width: 3.7339

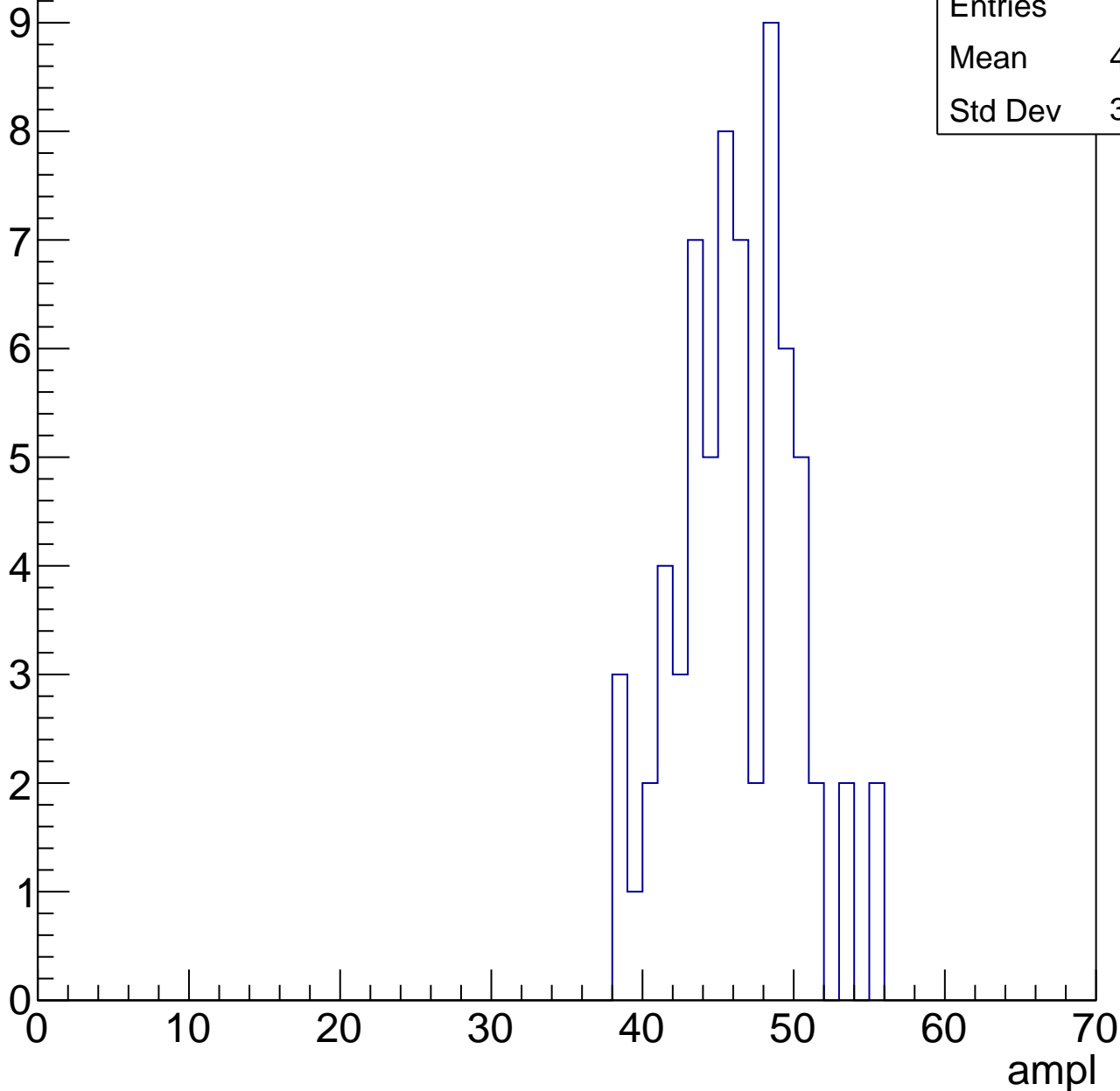


B1L102S, U17-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	45.79
Std Dev	3.894

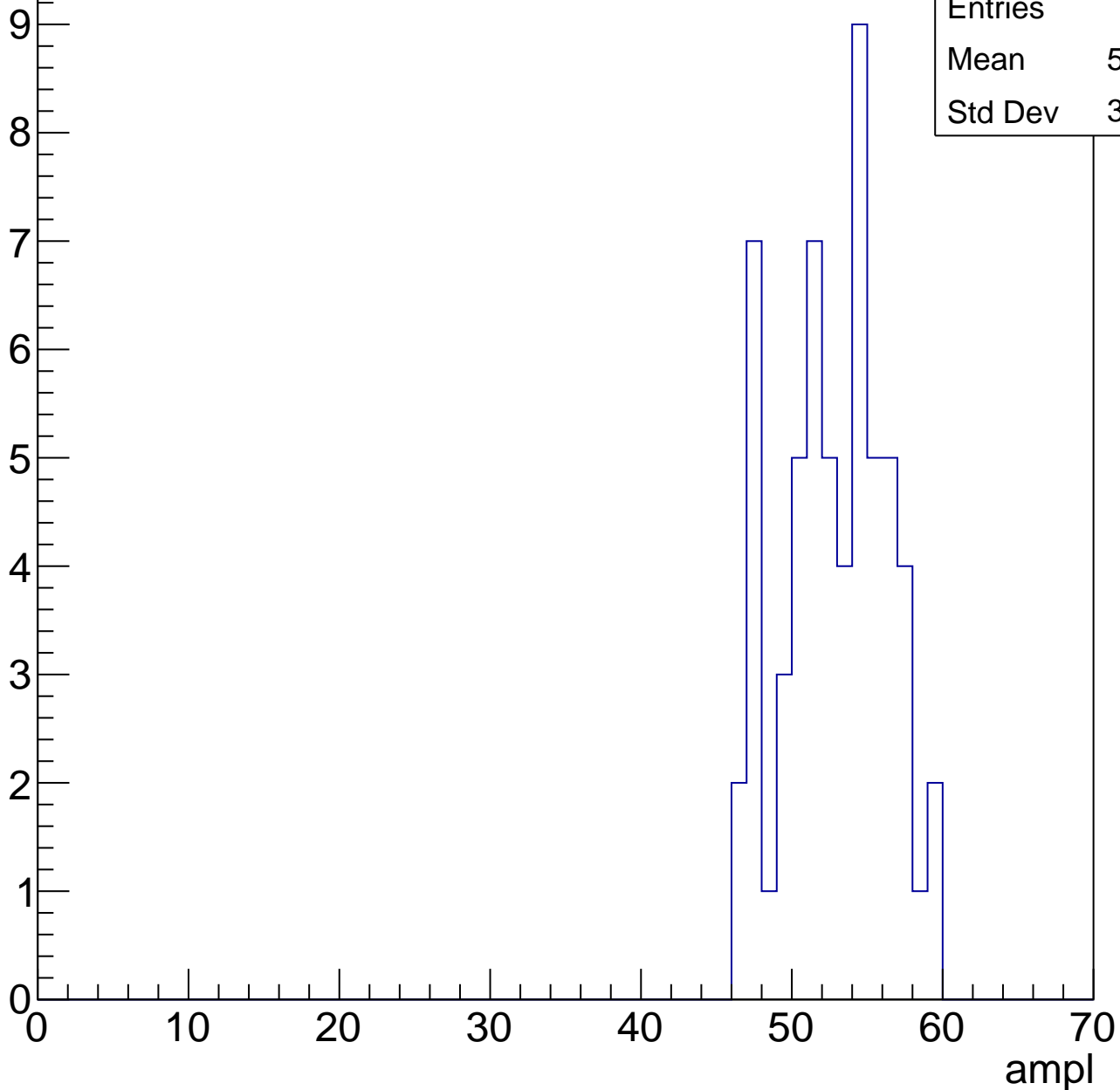


B1L102S, U17-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	52.33
Std Dev	3.438

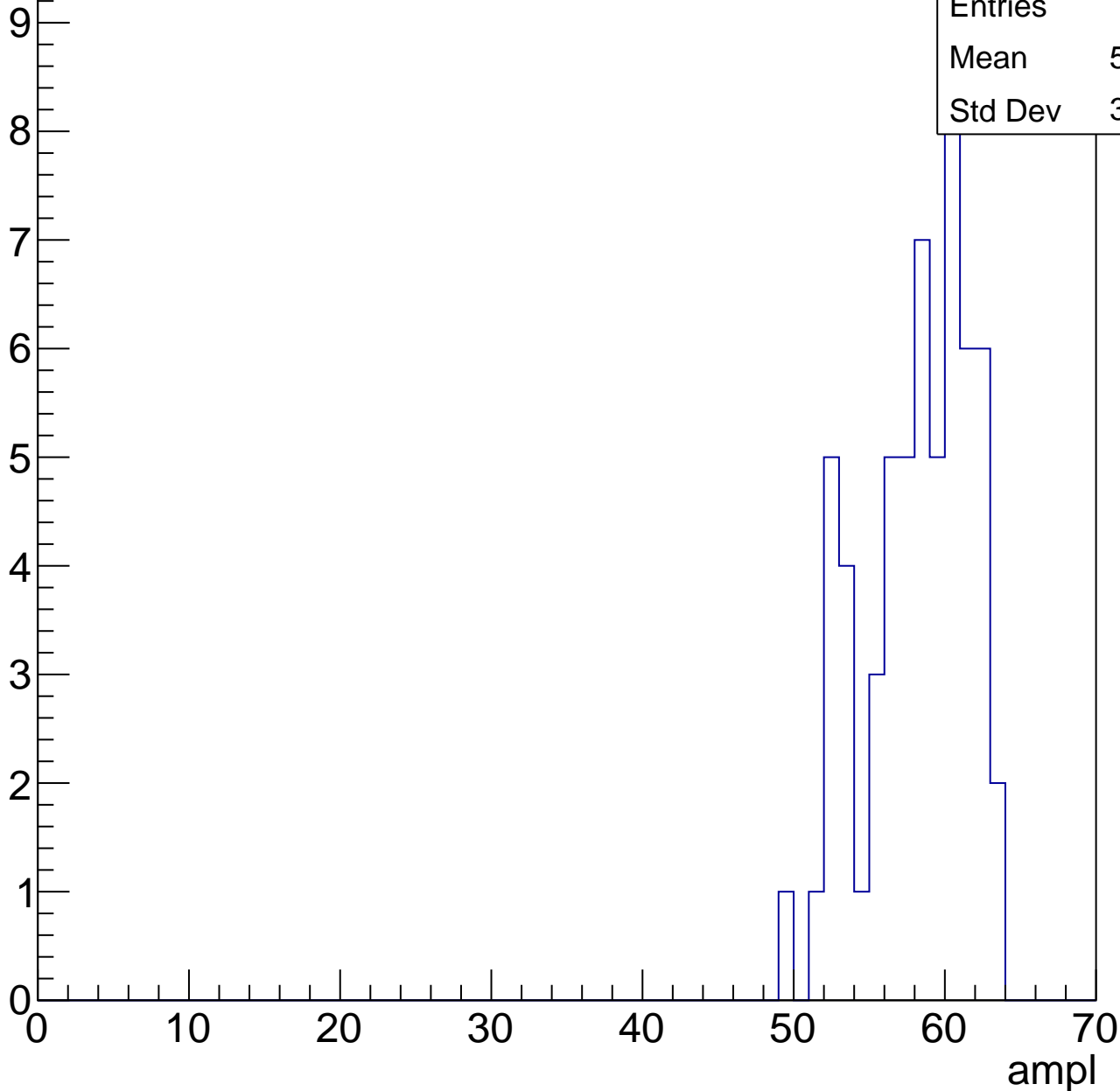


B1L102S, U17-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	57.68
Std Dev	3.447



B1L102S, U17-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

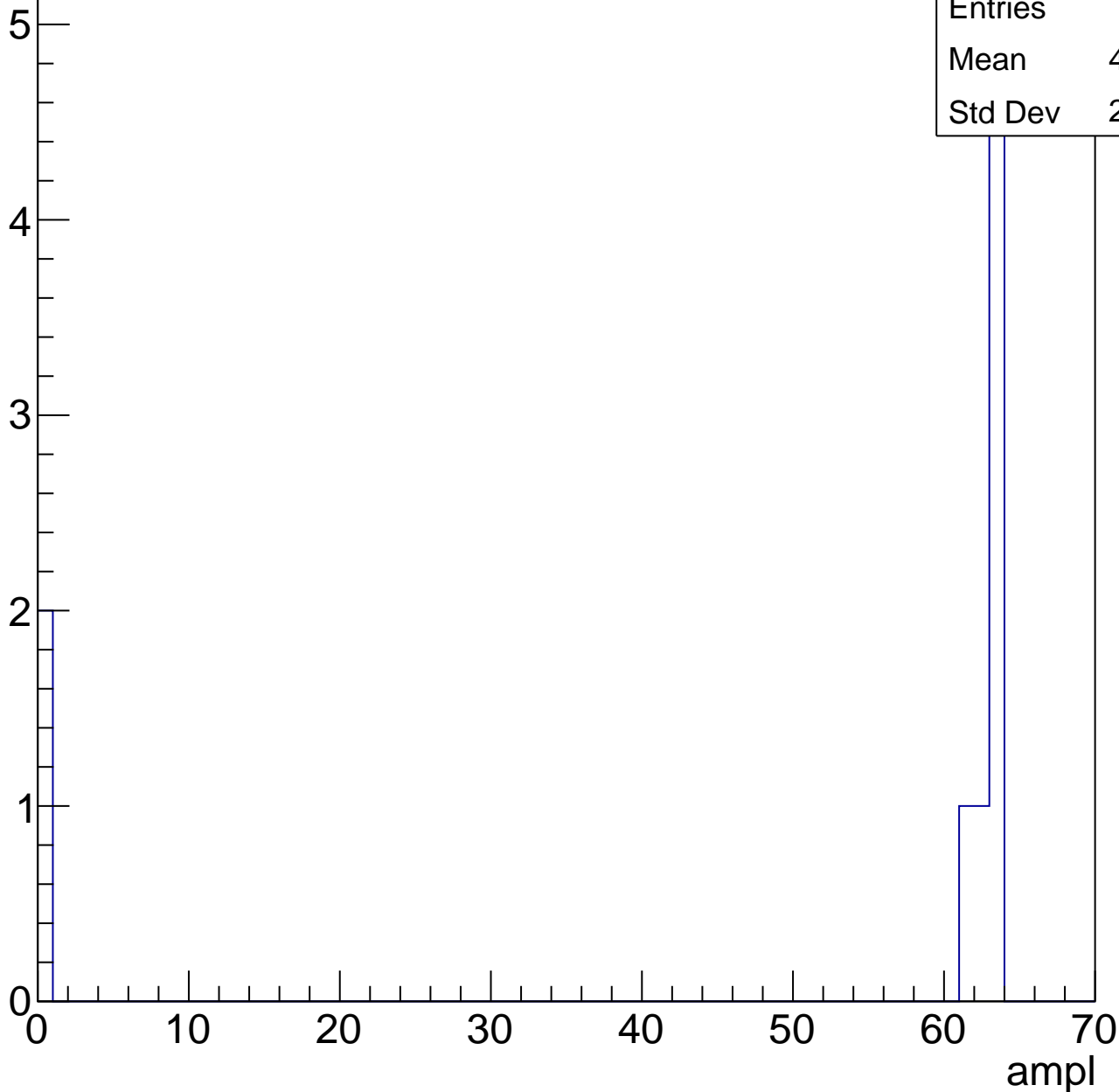
Entries	16
Mean	60.5
Std Dev	1.904

B1L102S, U17-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	48.67
Std Dev	26.02



B1L102S, U17-ch43, adc0

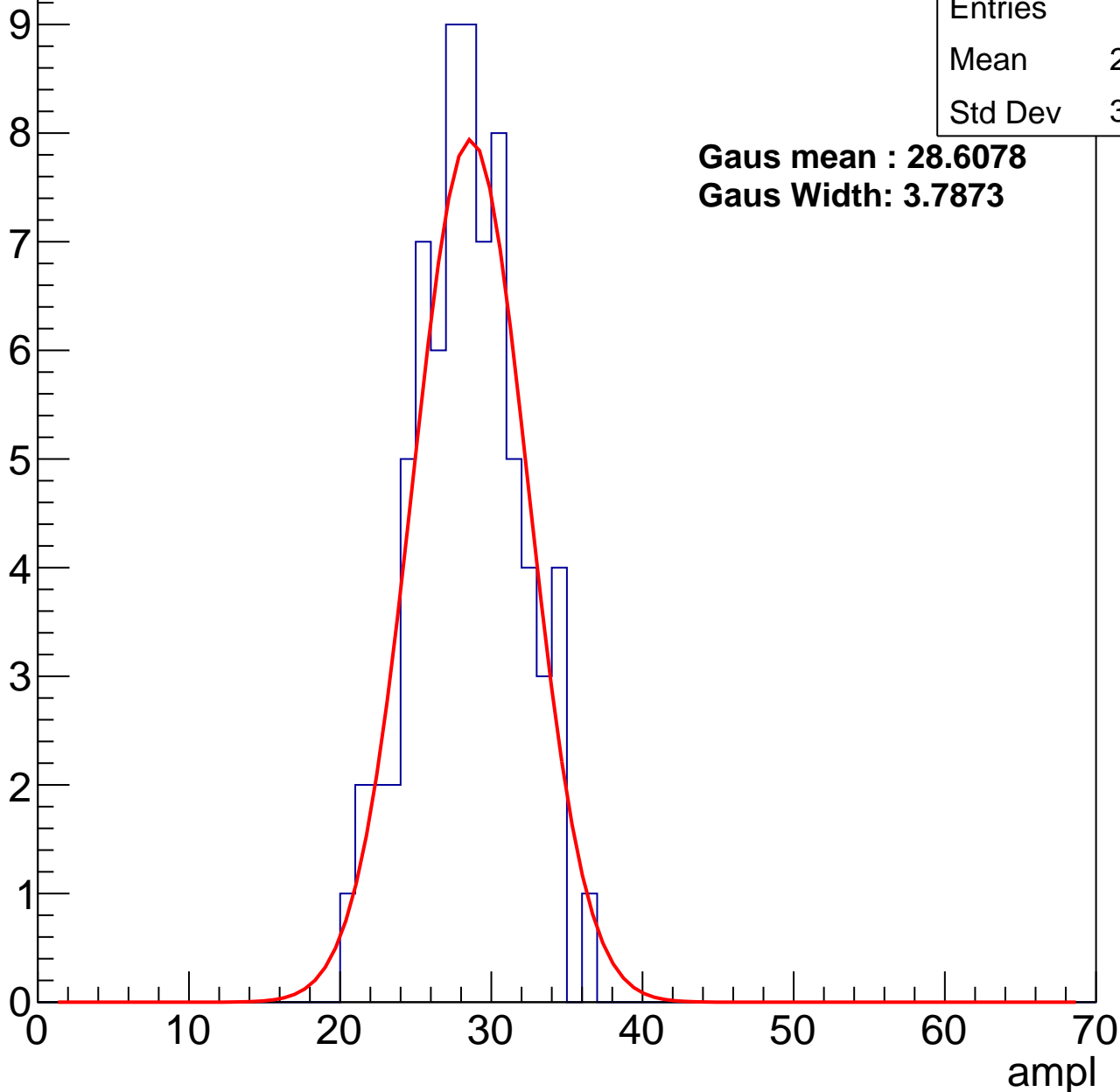
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.93
Std Dev	3.446

Gaus mean : 28.6078

Gaus Width: 3.7873



B1L102S, U17-ch43, adc1

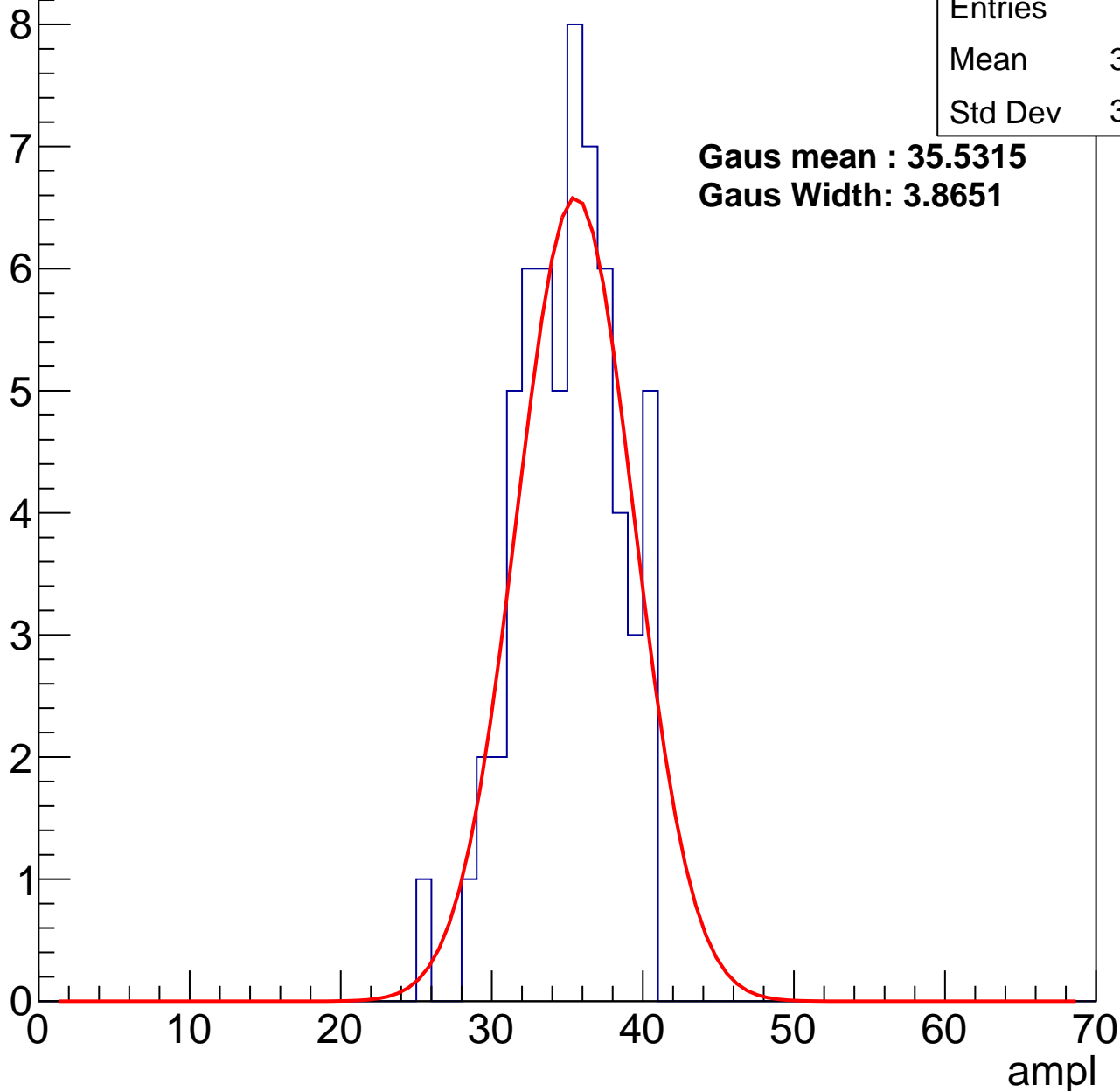
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.57
Std Dev	3.297

Gaus mean : 35.5315

Gaus Width: 3.8651



B1L102S, U17-ch43, adc2

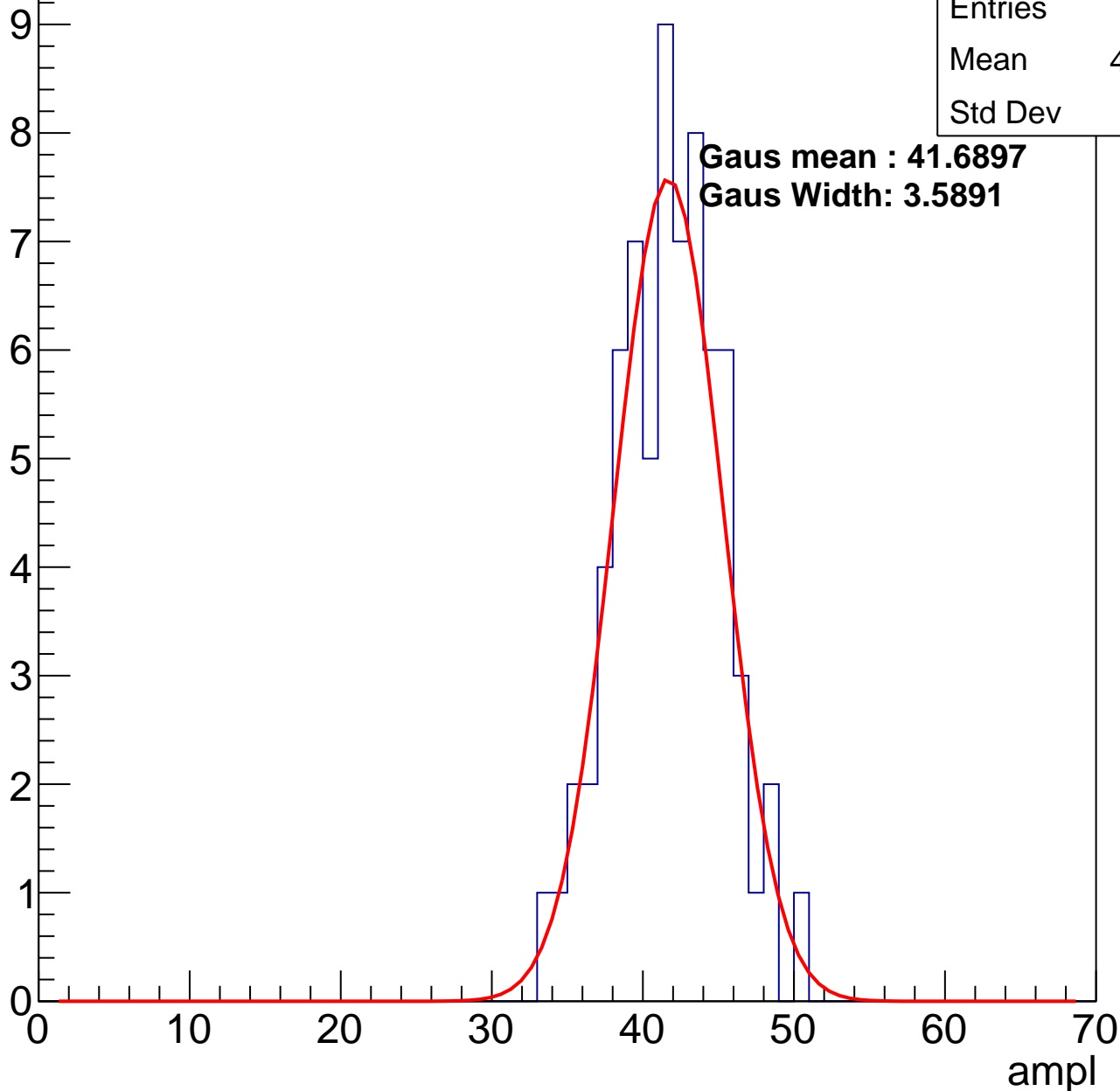
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.27
Std Dev	3.48

Gaus mean : 41.6897

Gaus Width: 3.5891

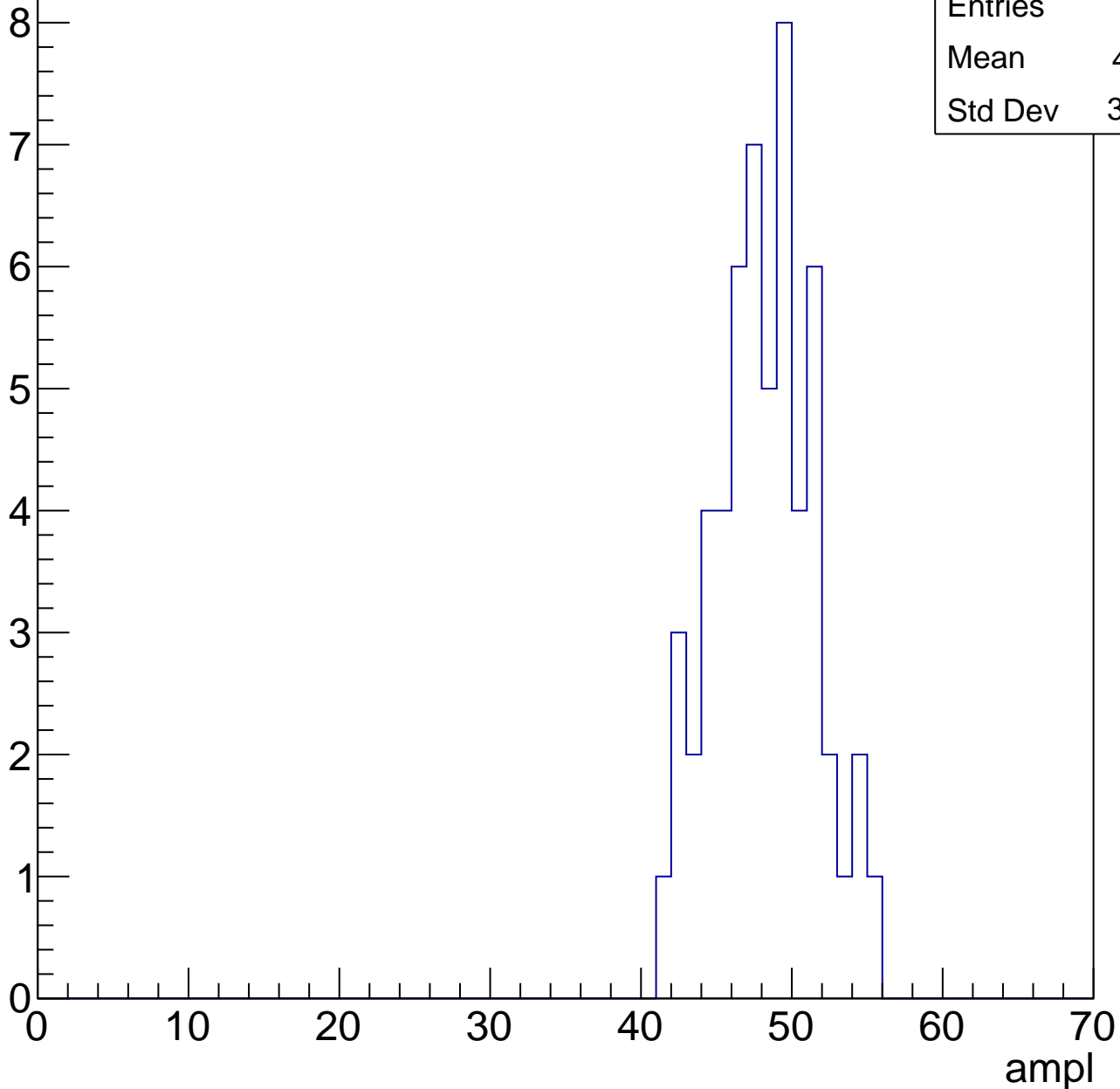


B1L102S, U17-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.71
Std Dev	3.244

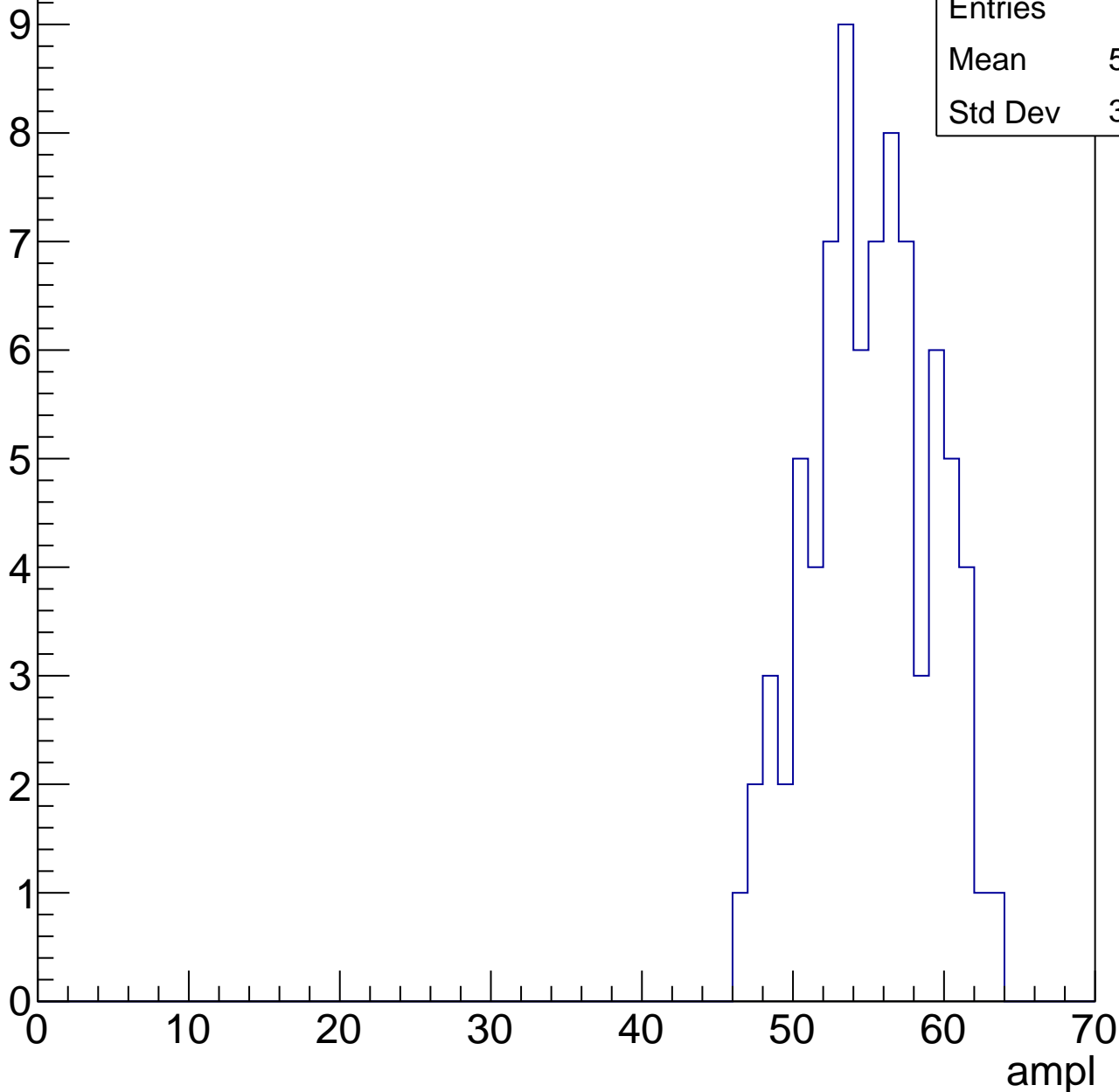


B1L102S, U17-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	54.69
Std Dev	3.937

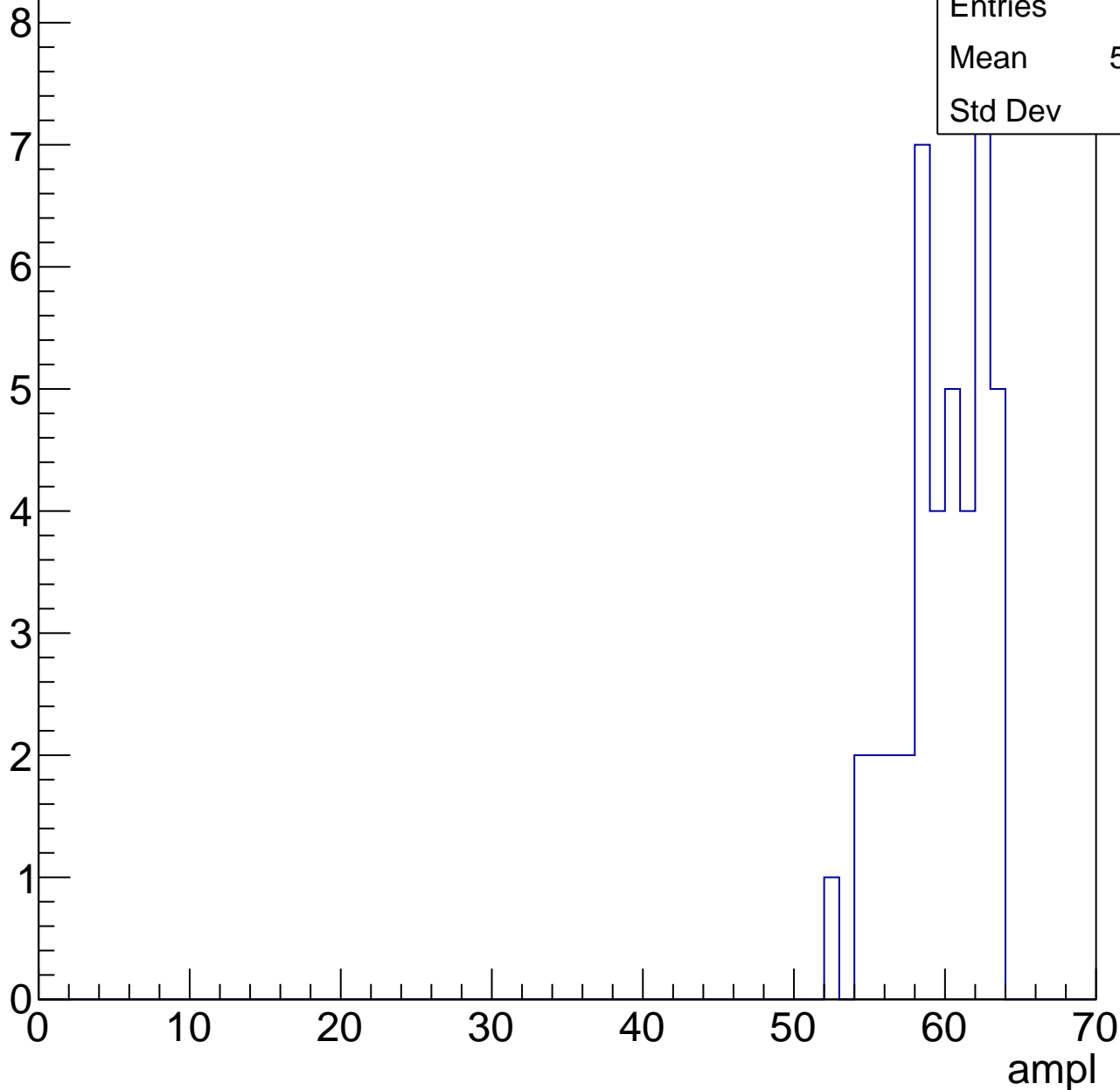


B1L102S, U17-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

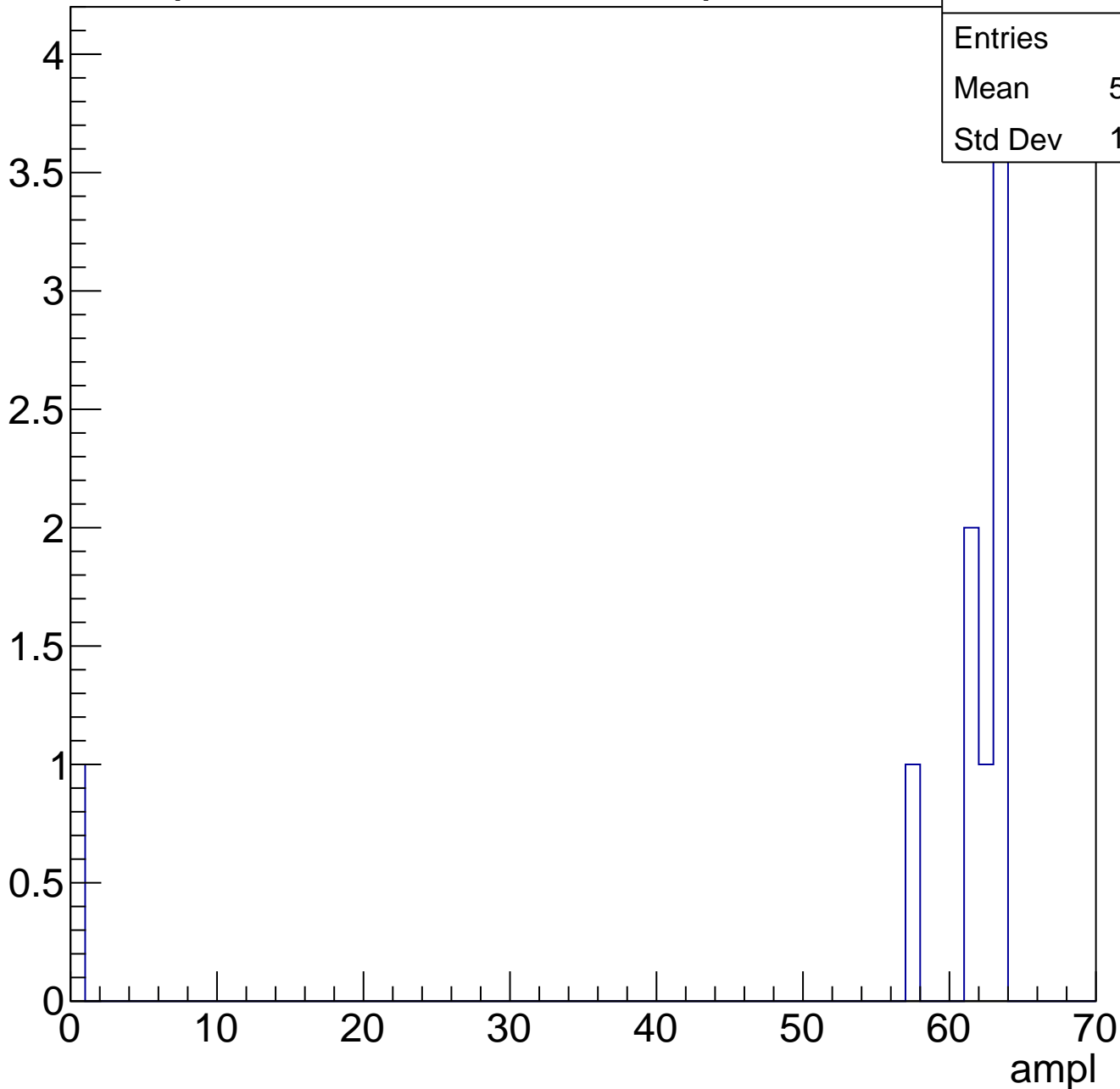
Entries	42
Mean	59.36
Std Dev	2.81



B1L102S, U17-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U17-ch44, adc0

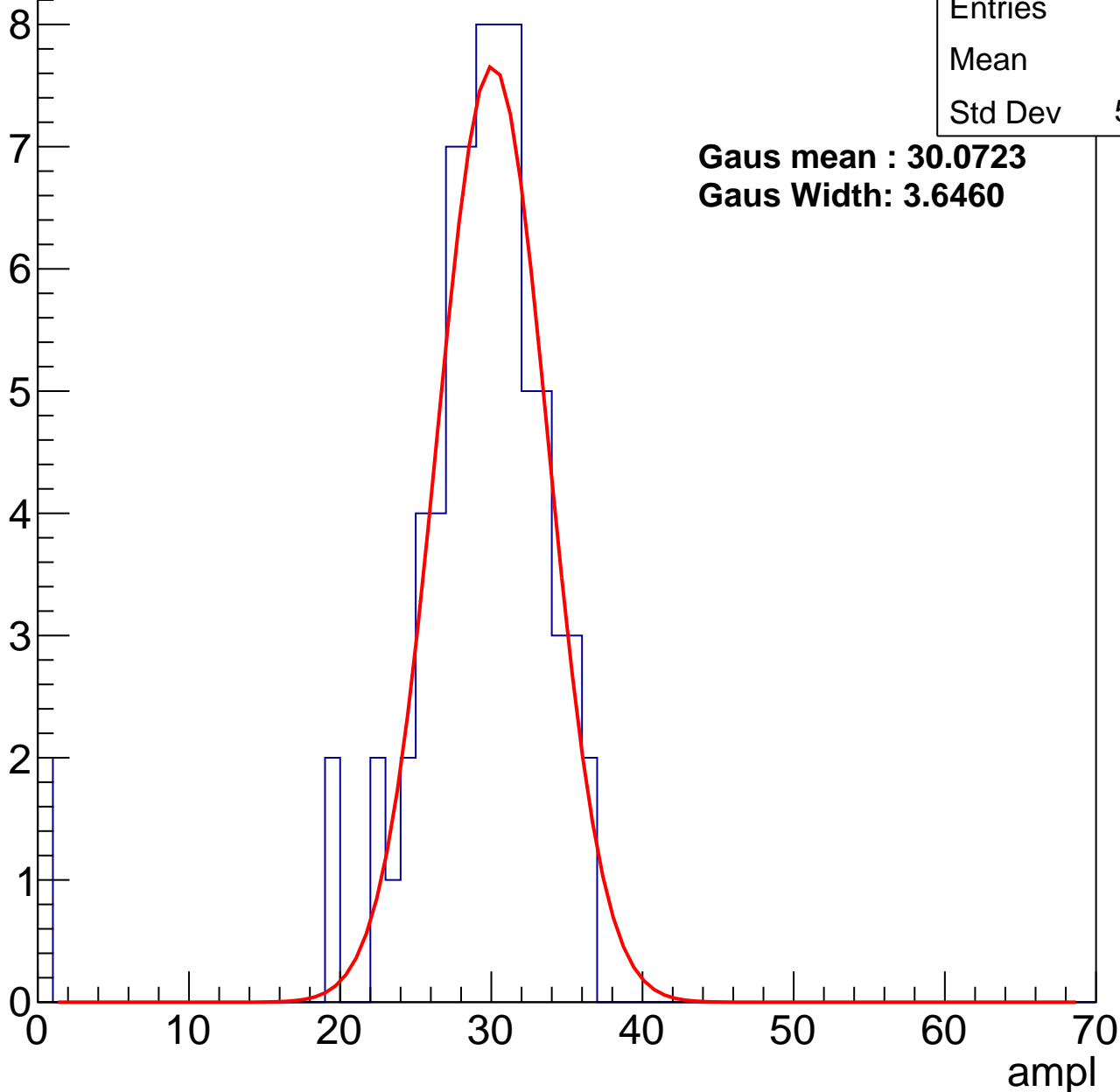
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.3
Std Dev	5.981

Gaus mean : 30.0723

Gaus Width: 3.6460



B1L102S, U17-ch44, adc1

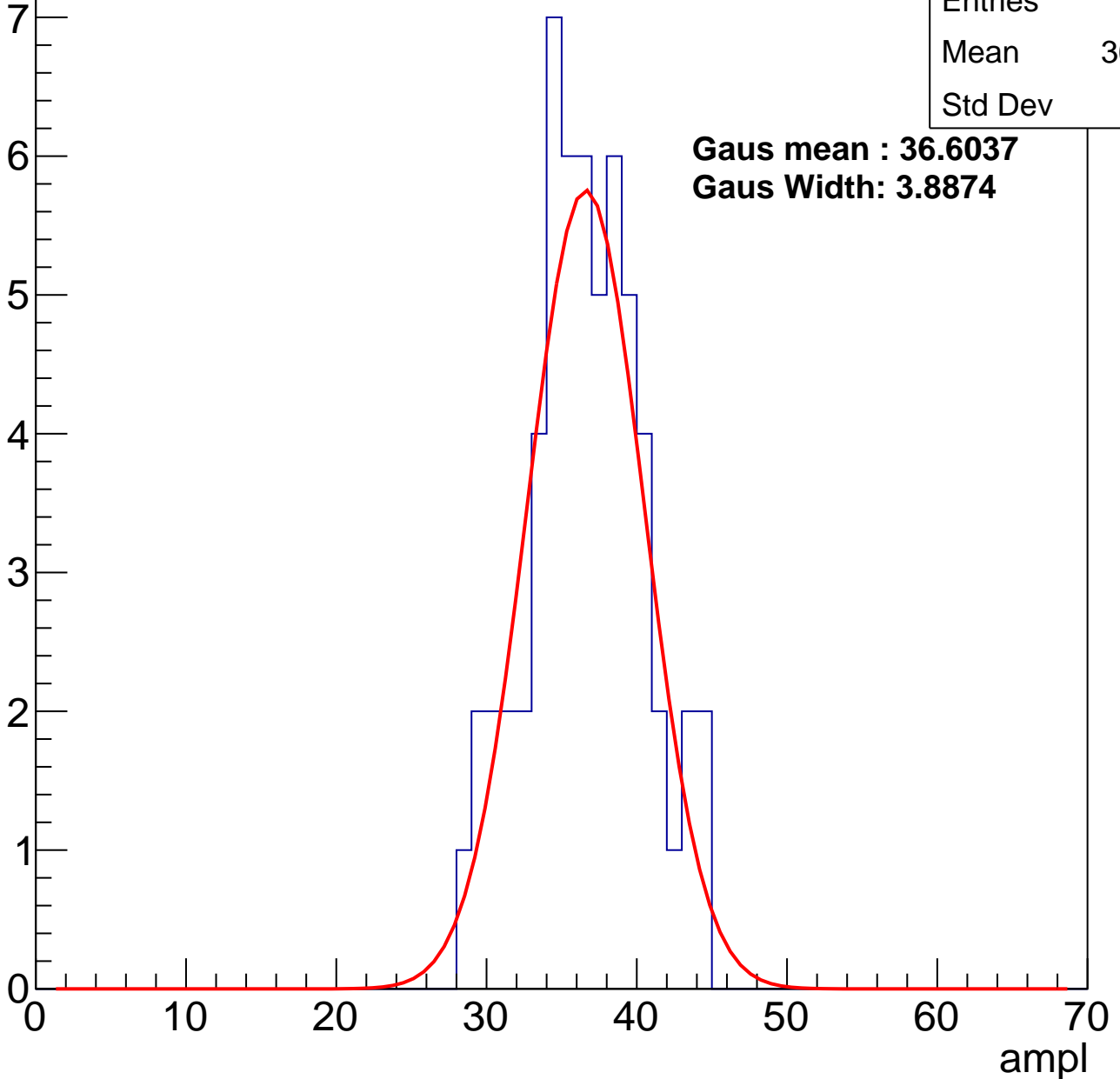
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.17
Std Dev	3.76

Gaus mean : 36.6037

Gaus Width: 3.8874



B1L102S, U17-ch44, adc2

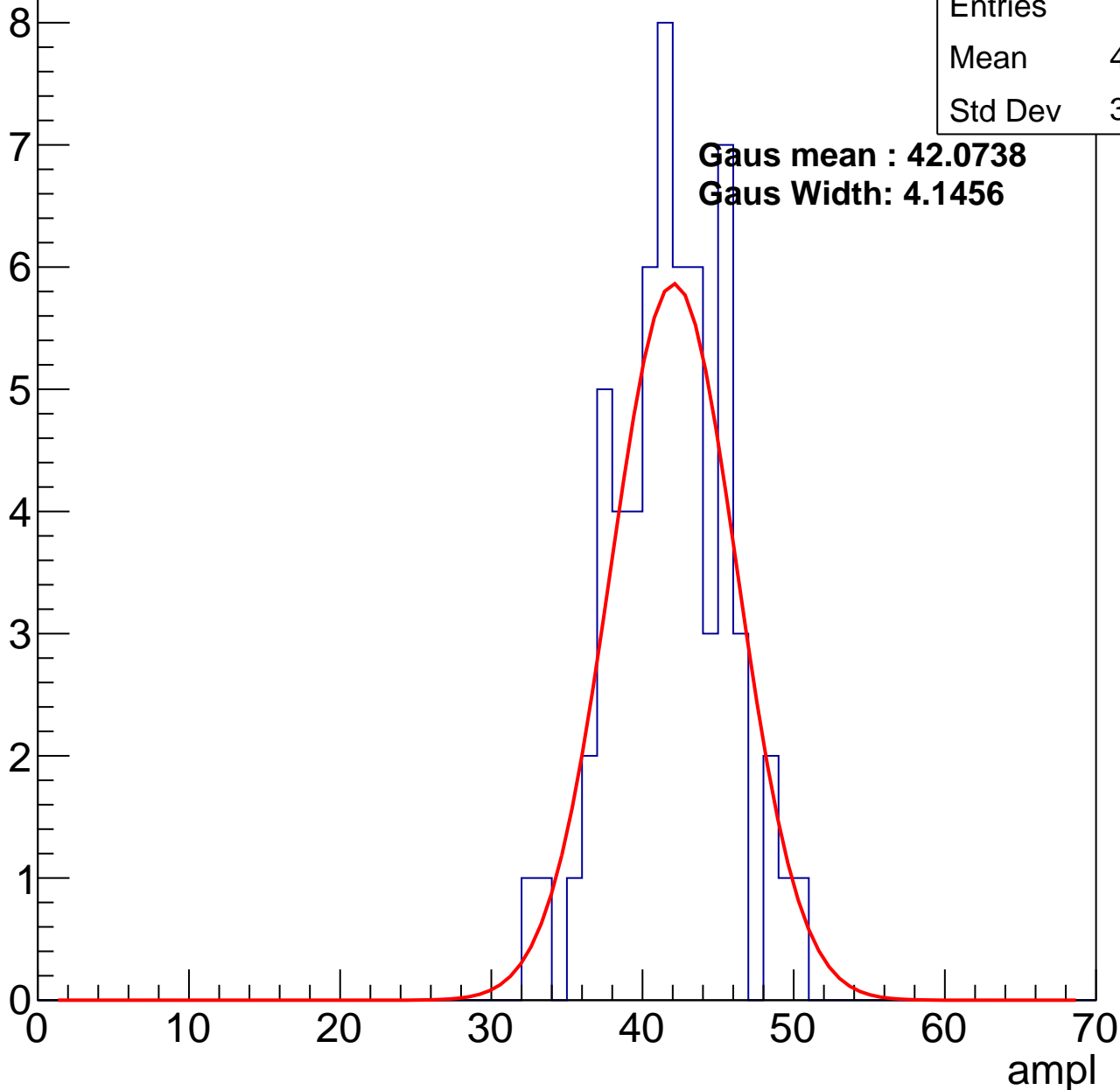
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.36
Std Dev	3.729

Gaus mean : 42.0738

Gaus Width: 4.1456



B1L102S, U17-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	47.61
Std Dev	3.133

Entry

10

8

6

4

2

0

0

10

20

30

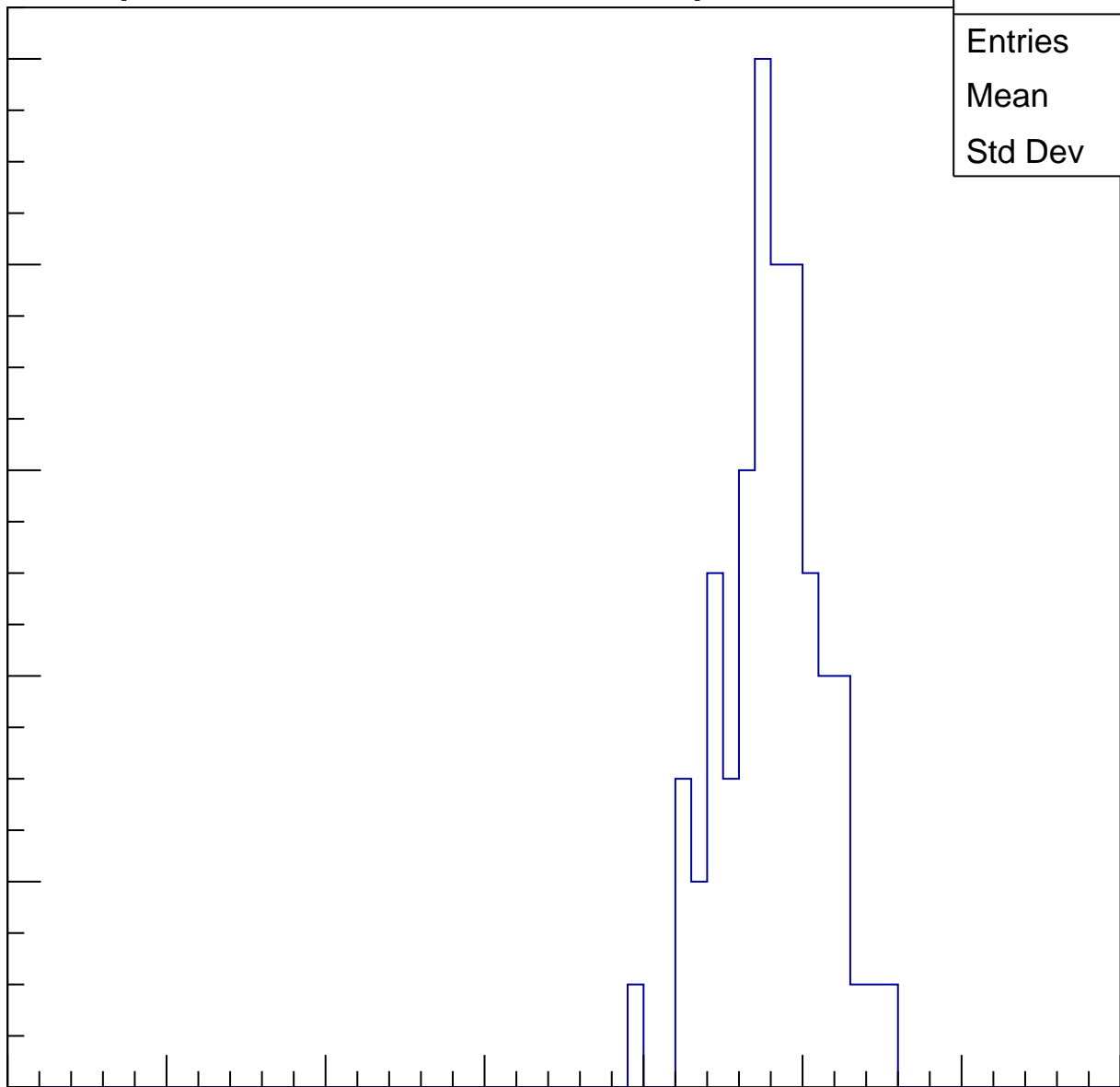
40

50

60

70

ampl

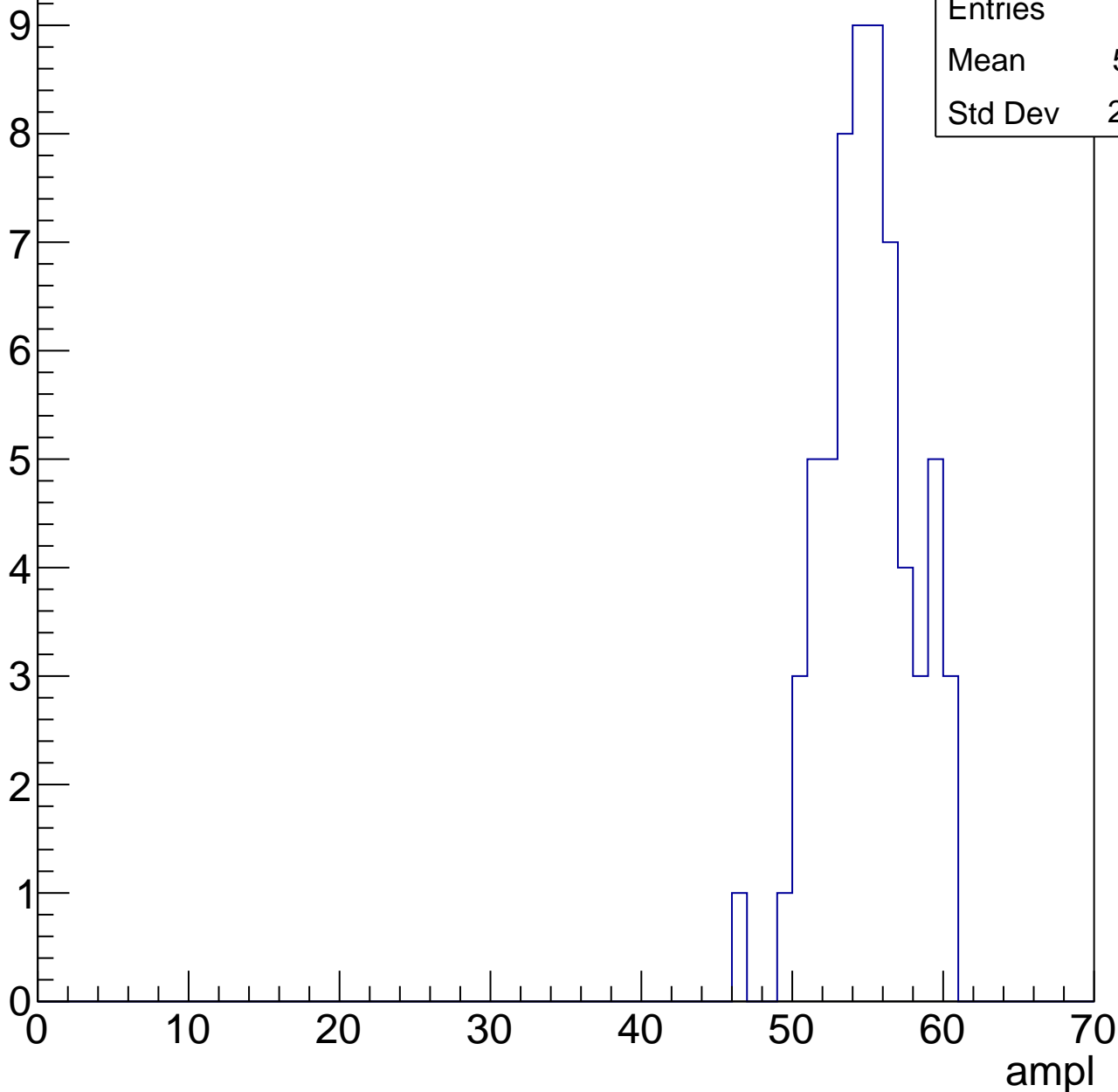


B1L102S, U17-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.51
Std Dev	2.949

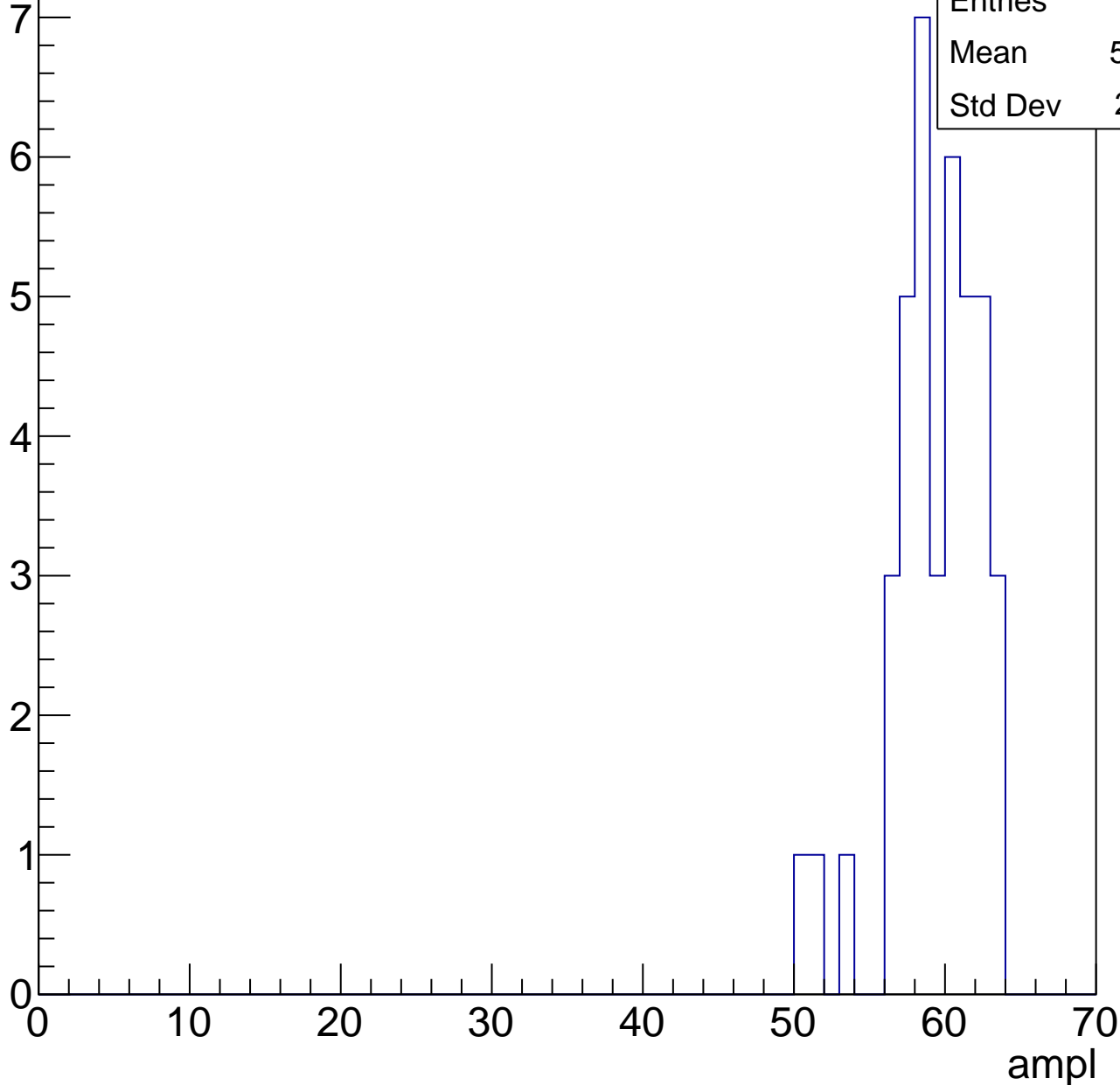


B1L102S, U17-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.85
Std Dev	2.971

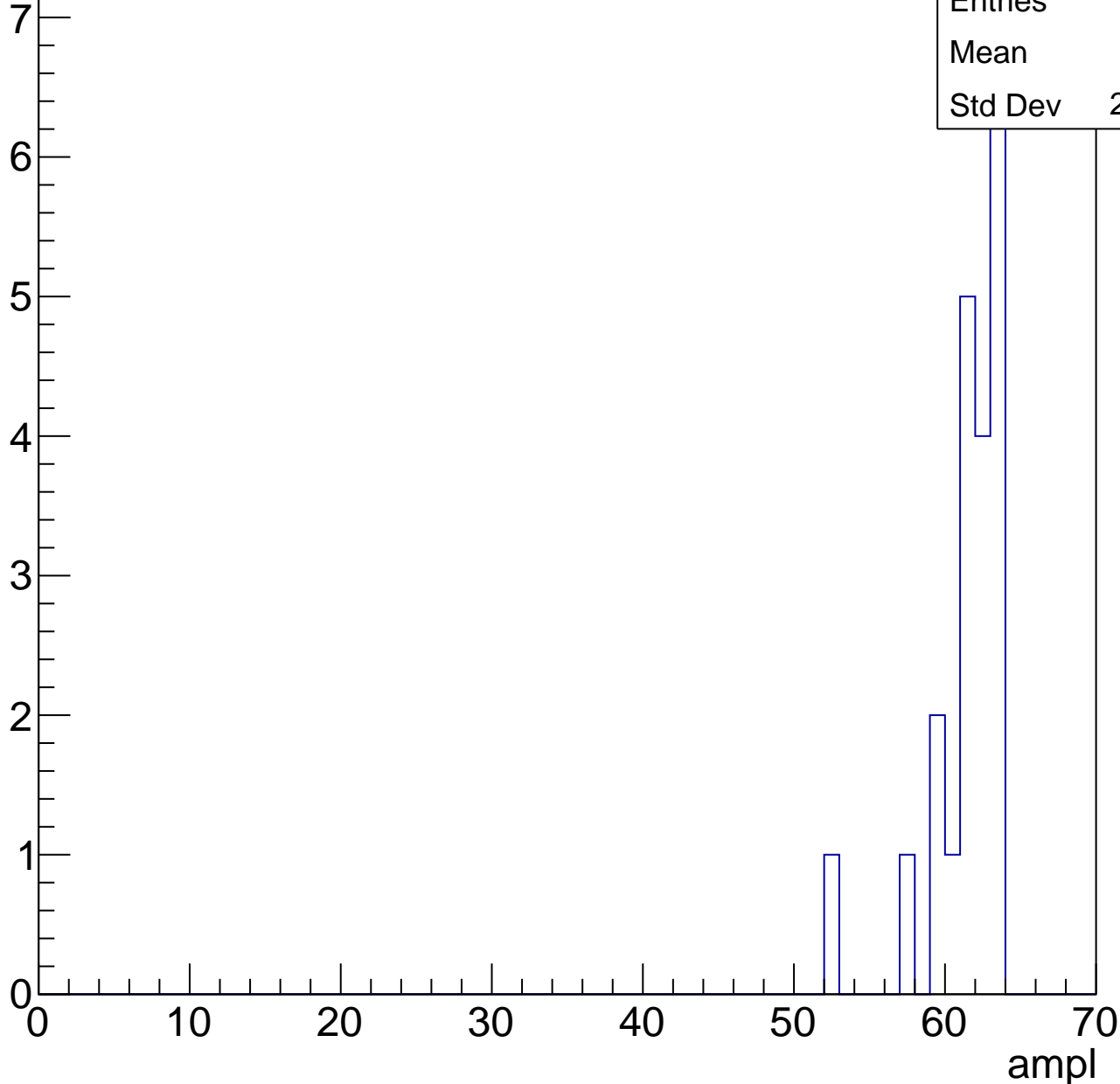


B1L102S, U17-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

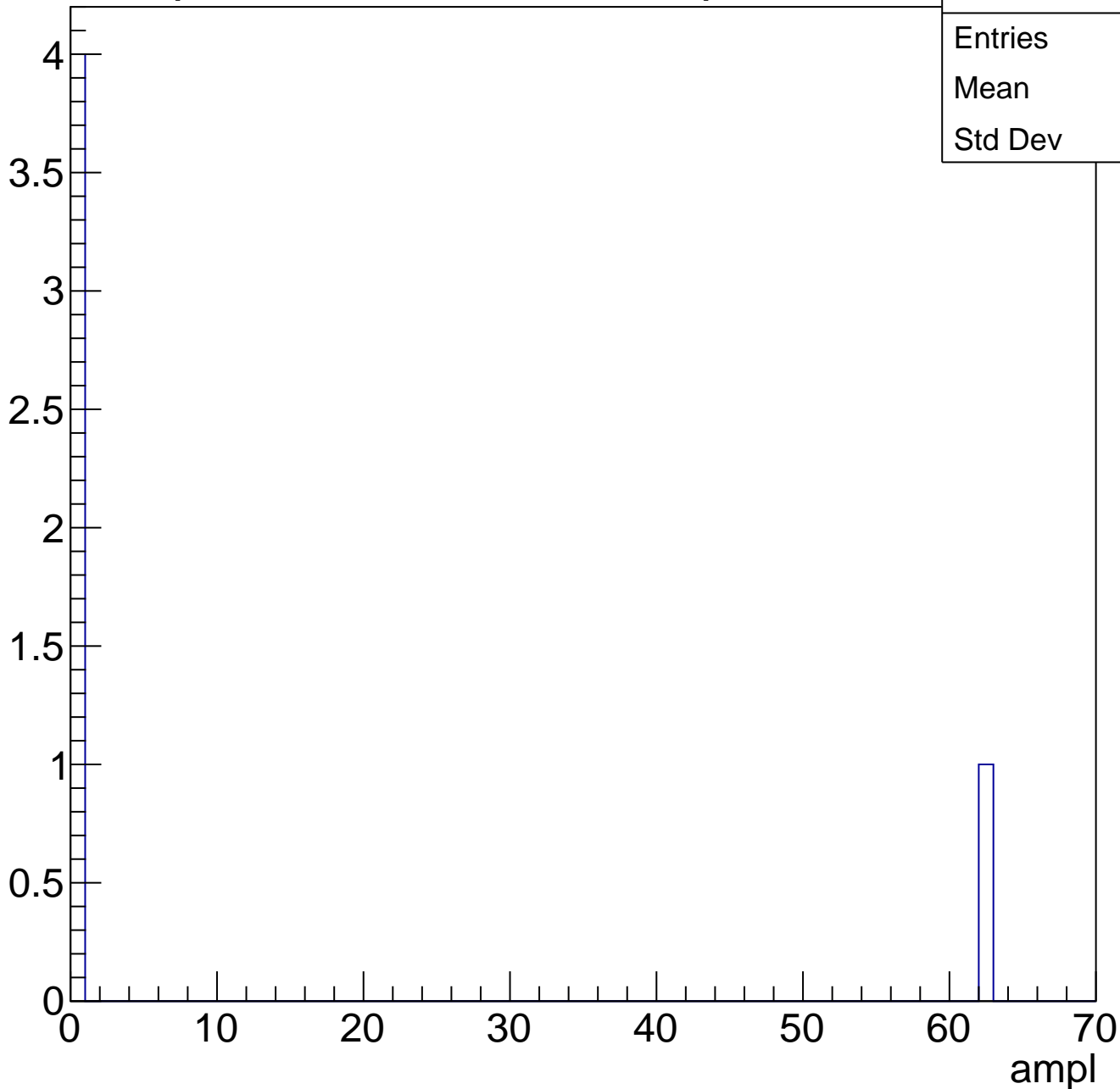
Entries	21
Mean	61
Std Dev	2.563



B1L102S, U17-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch45, adc0

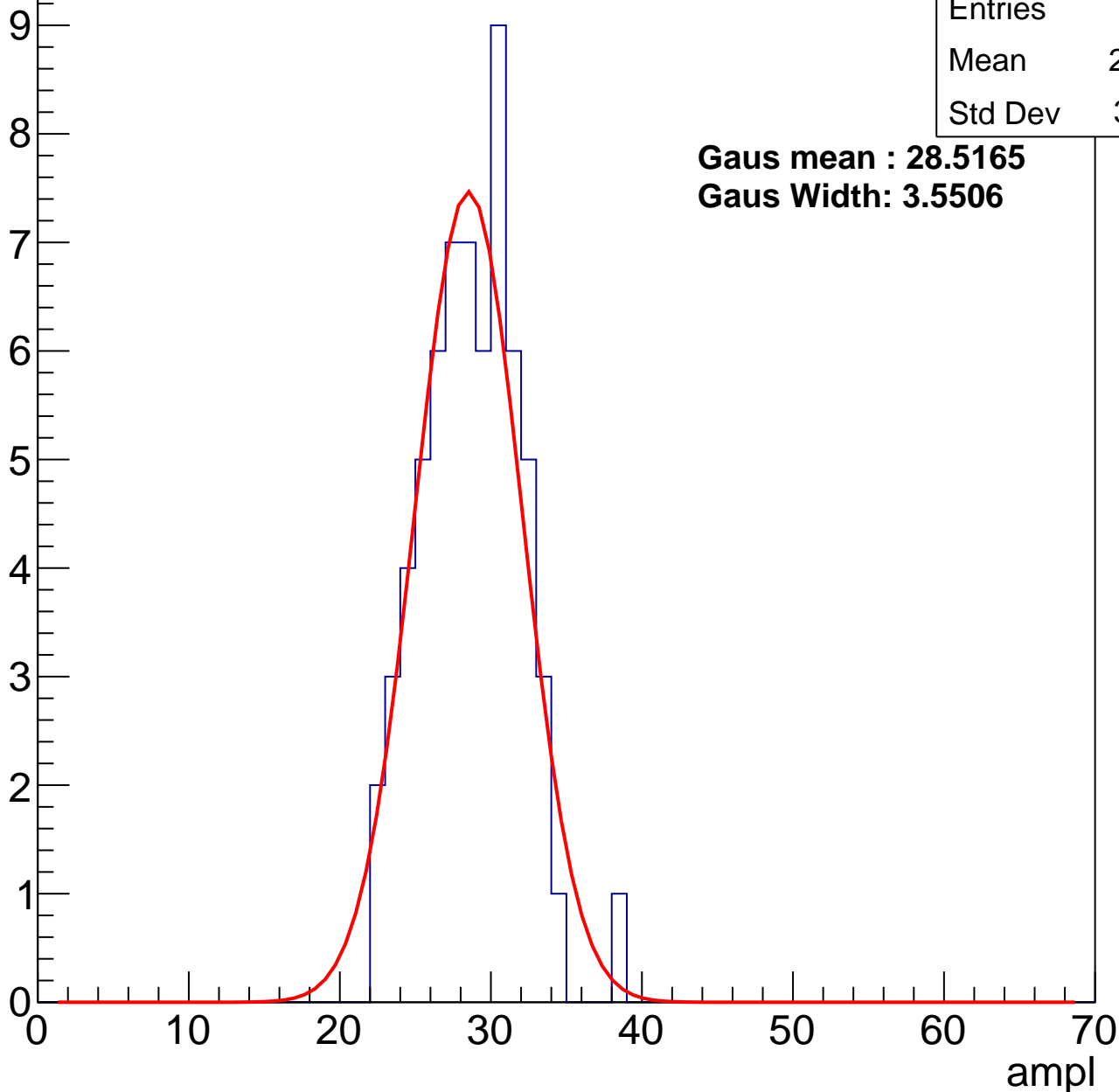
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.25
Std Dev	3.211

Gaus mean : 28.5165

Gaus Width: 3.5506



B1L102S, U17-ch45, adc1

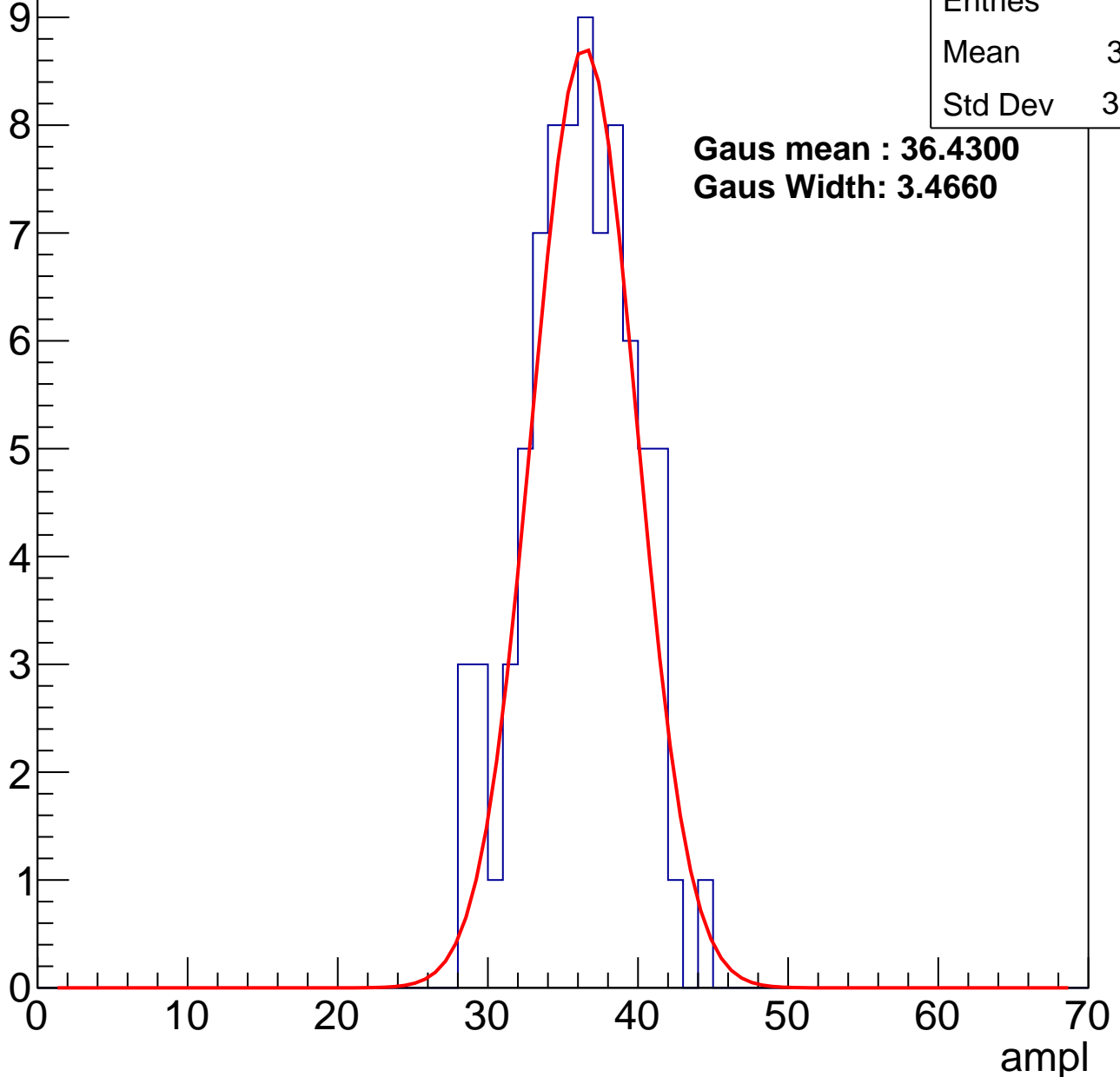
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.61
Std Dev	3.569

Gaus mean : 36.4300

Gaus Width: 3.4660



B1L102S, U17-ch45, adc2

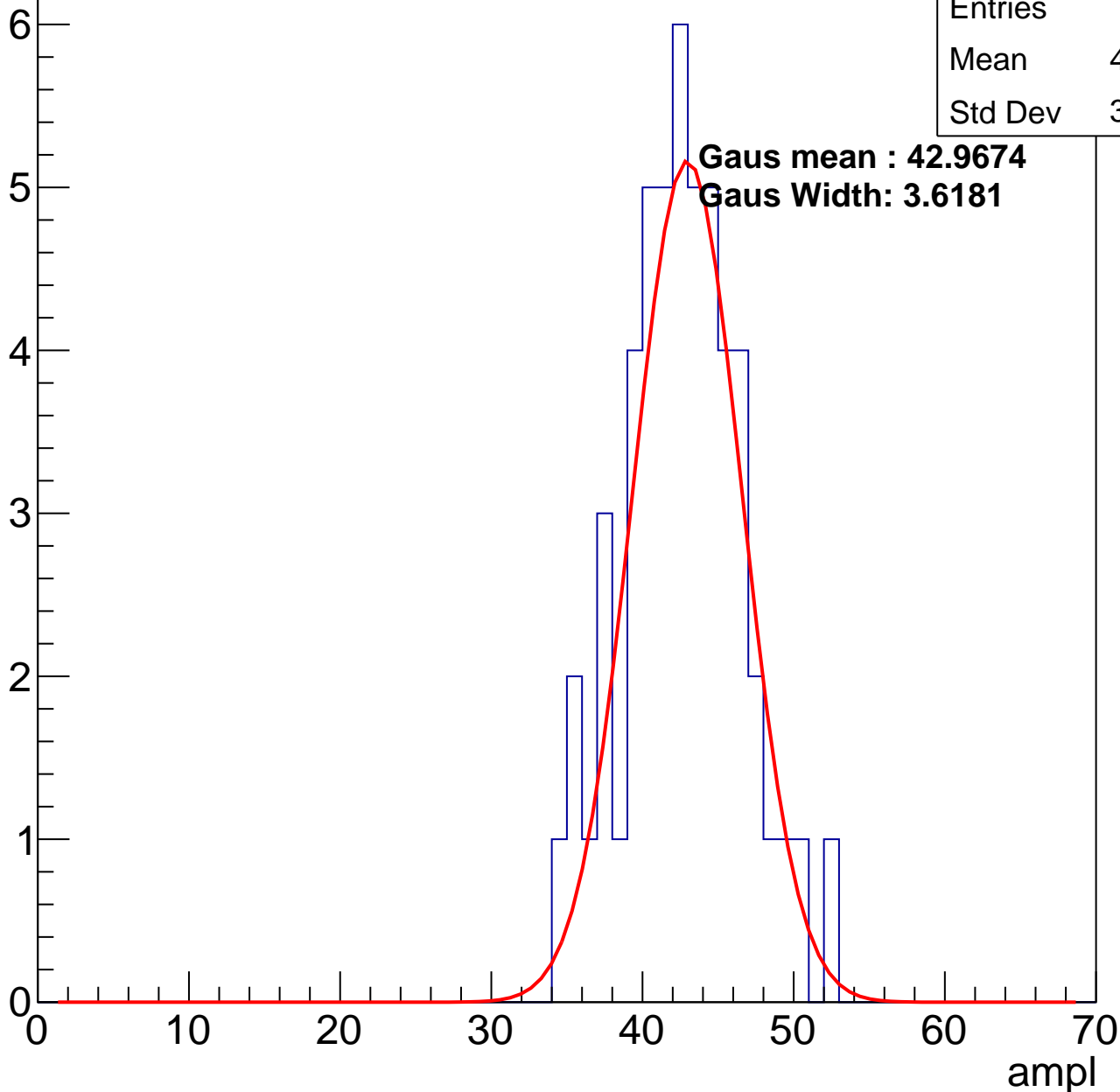
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.19
Std Dev	3.853

Gaus mean : 42.9674

Gaus Width: 3.6181

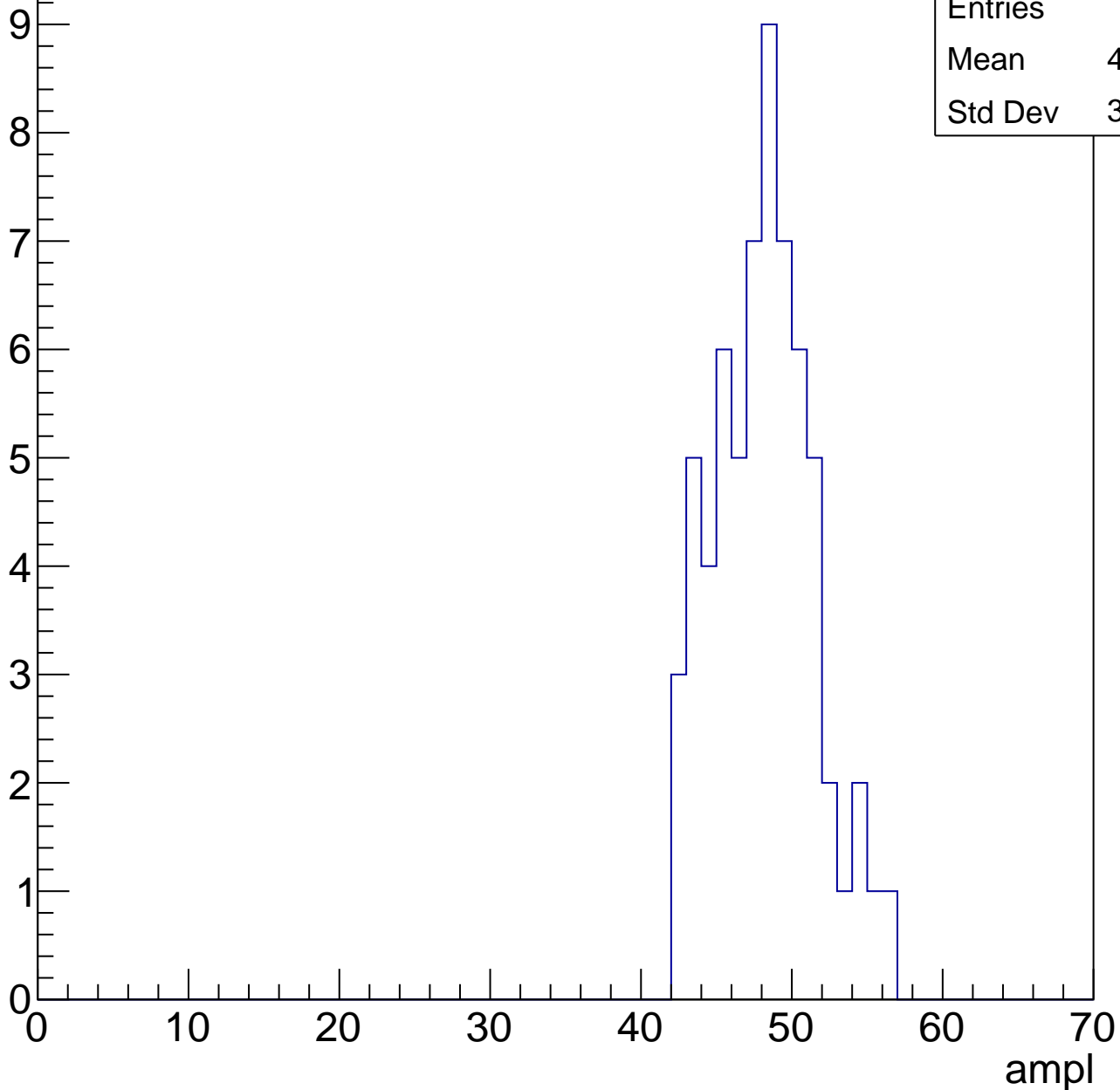


B1L102S, U17-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.69
Std Dev	3.283

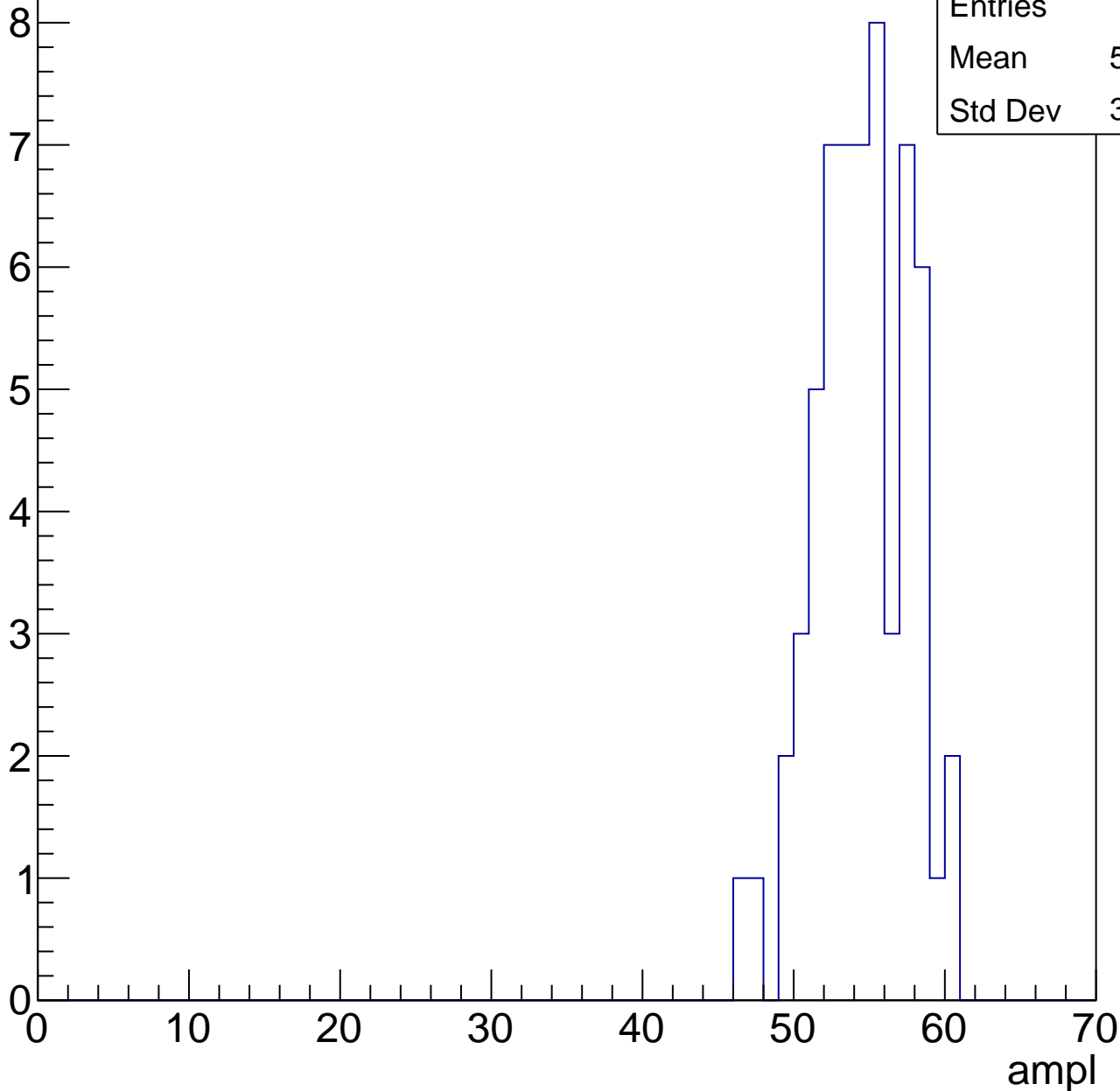


B1L102S, U17-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

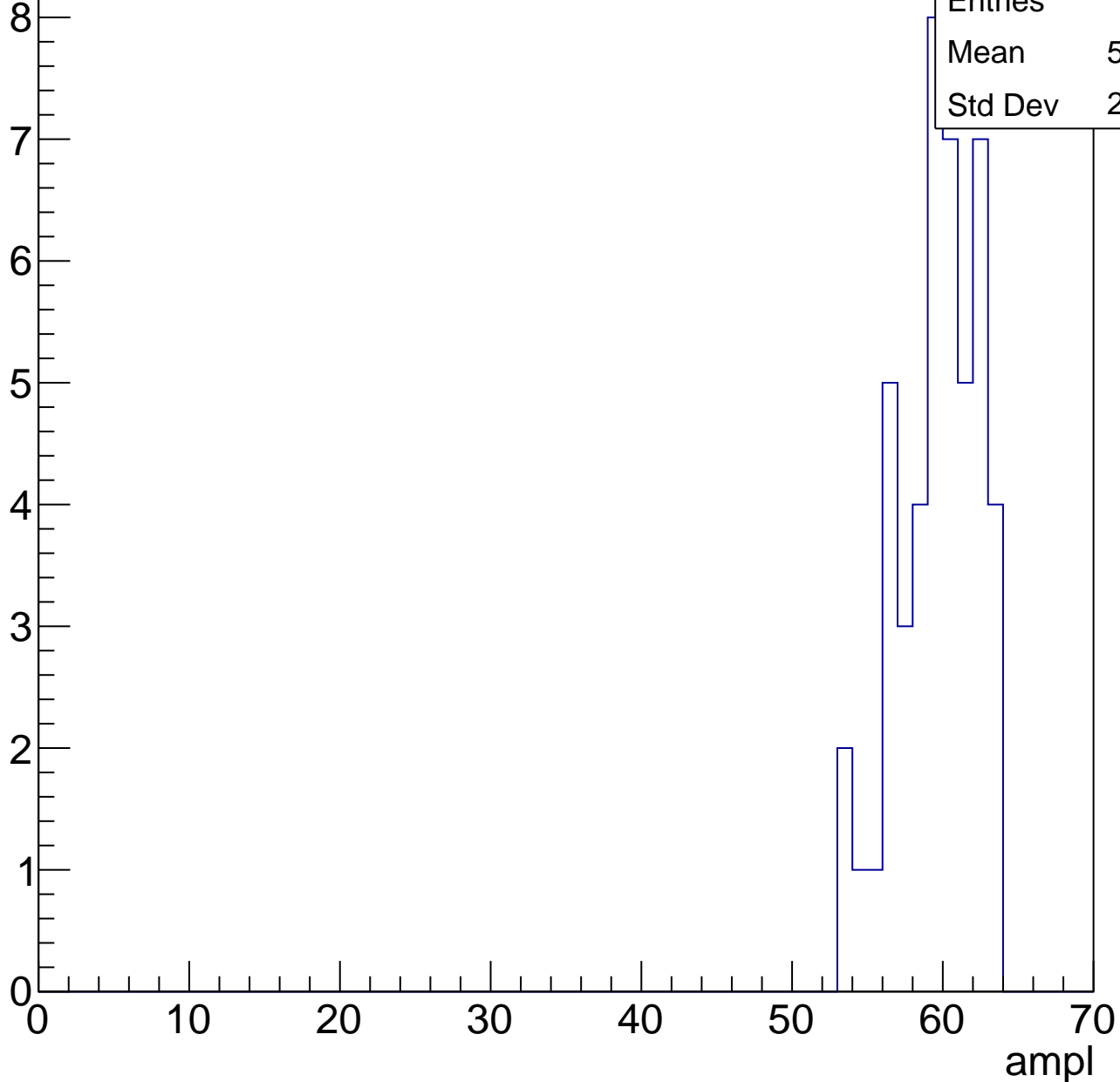
Entries	60
Mean	54.05
Std Dev	3.074



B1L102S, U17-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

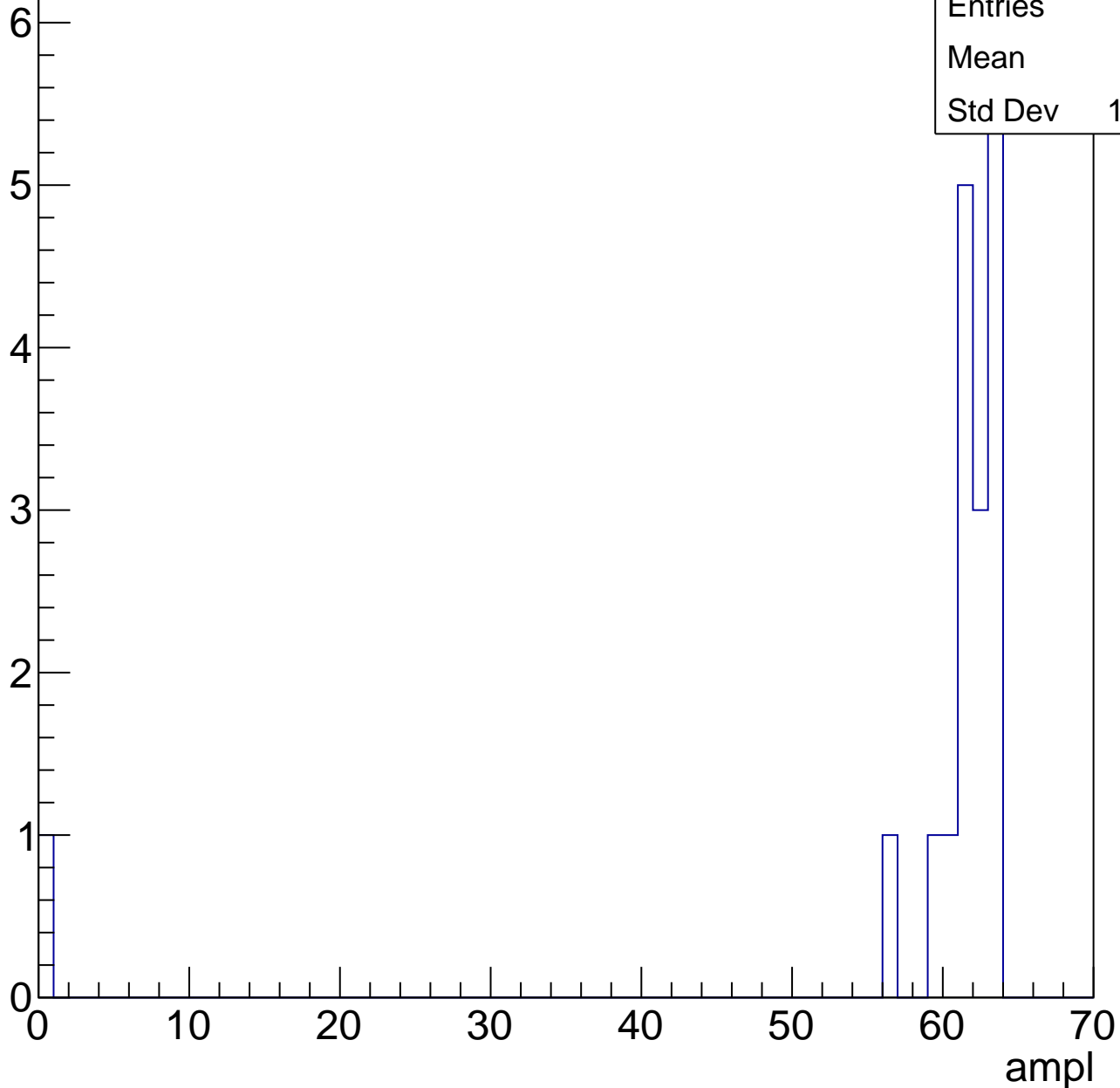


B1L102S, U17-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	58
Std Dev	14.17



B1L102S, U17-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch46, adc0

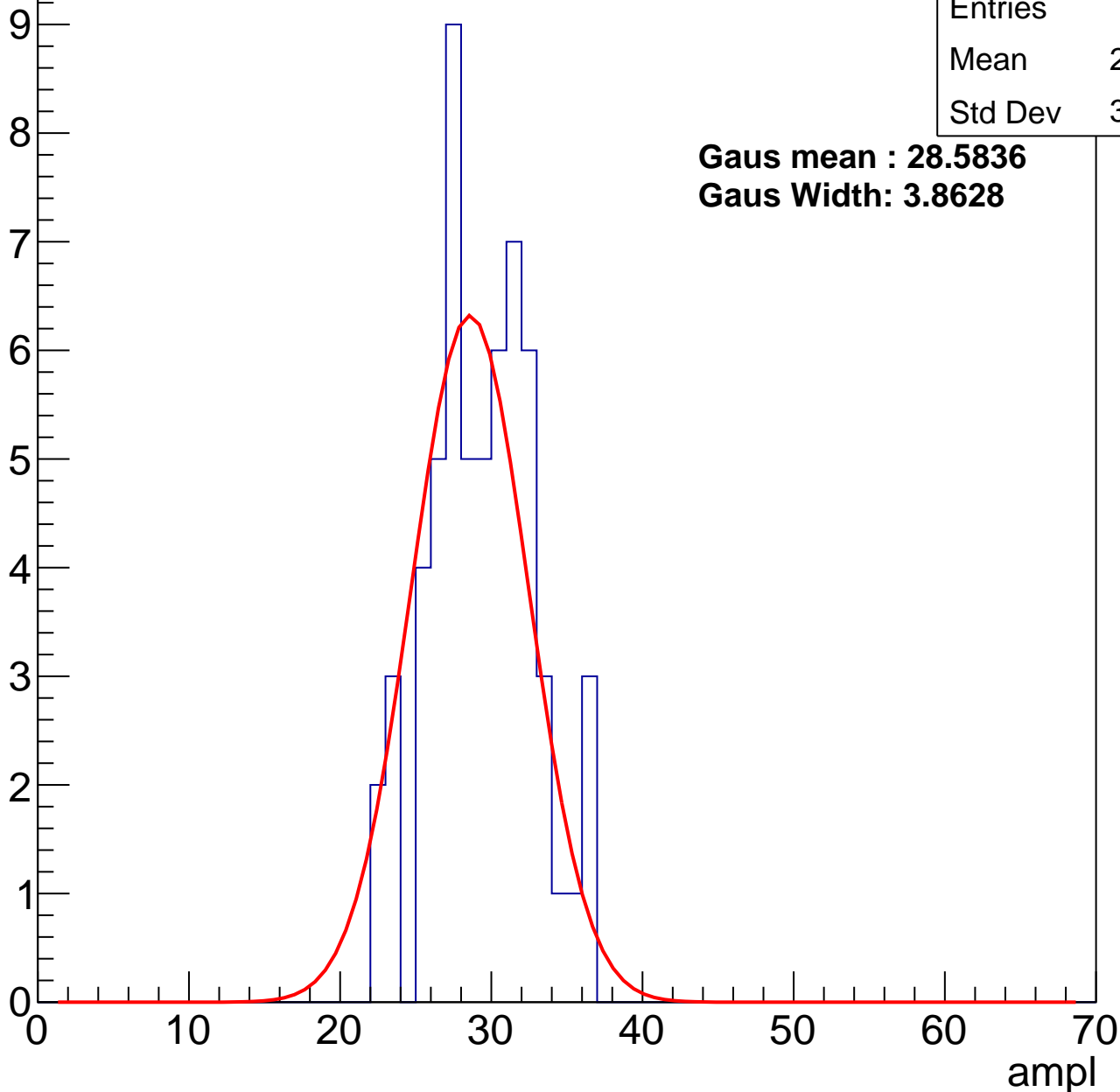
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.93
Std Dev	3.415

Gaus mean : 28.5836

Gaus Width: 3.8628



B1L102S, U17-ch46, adc1

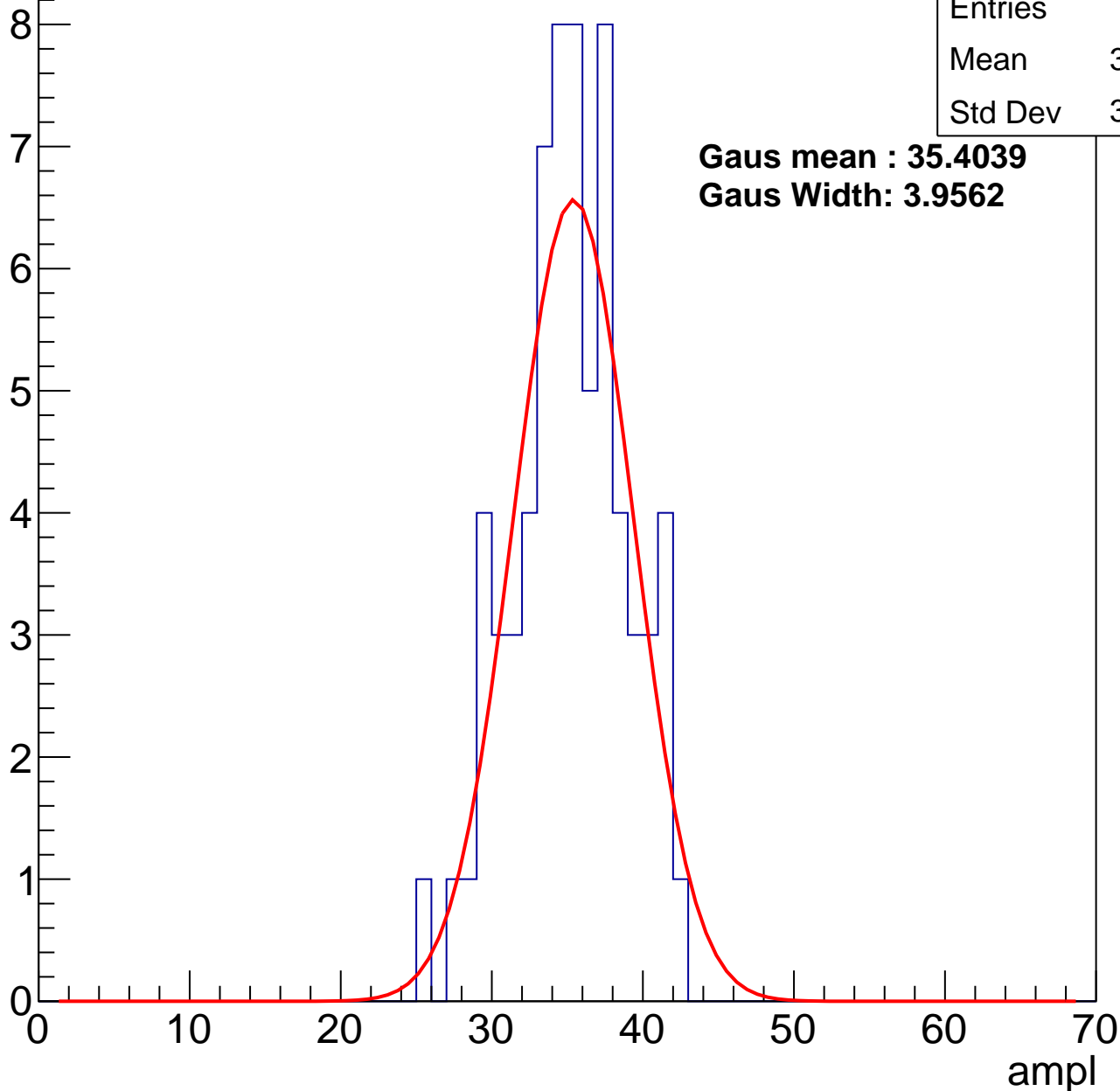
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.72
Std Dev	3.709

Gaus mean : 35.4039

Gaus Width: 3.9562

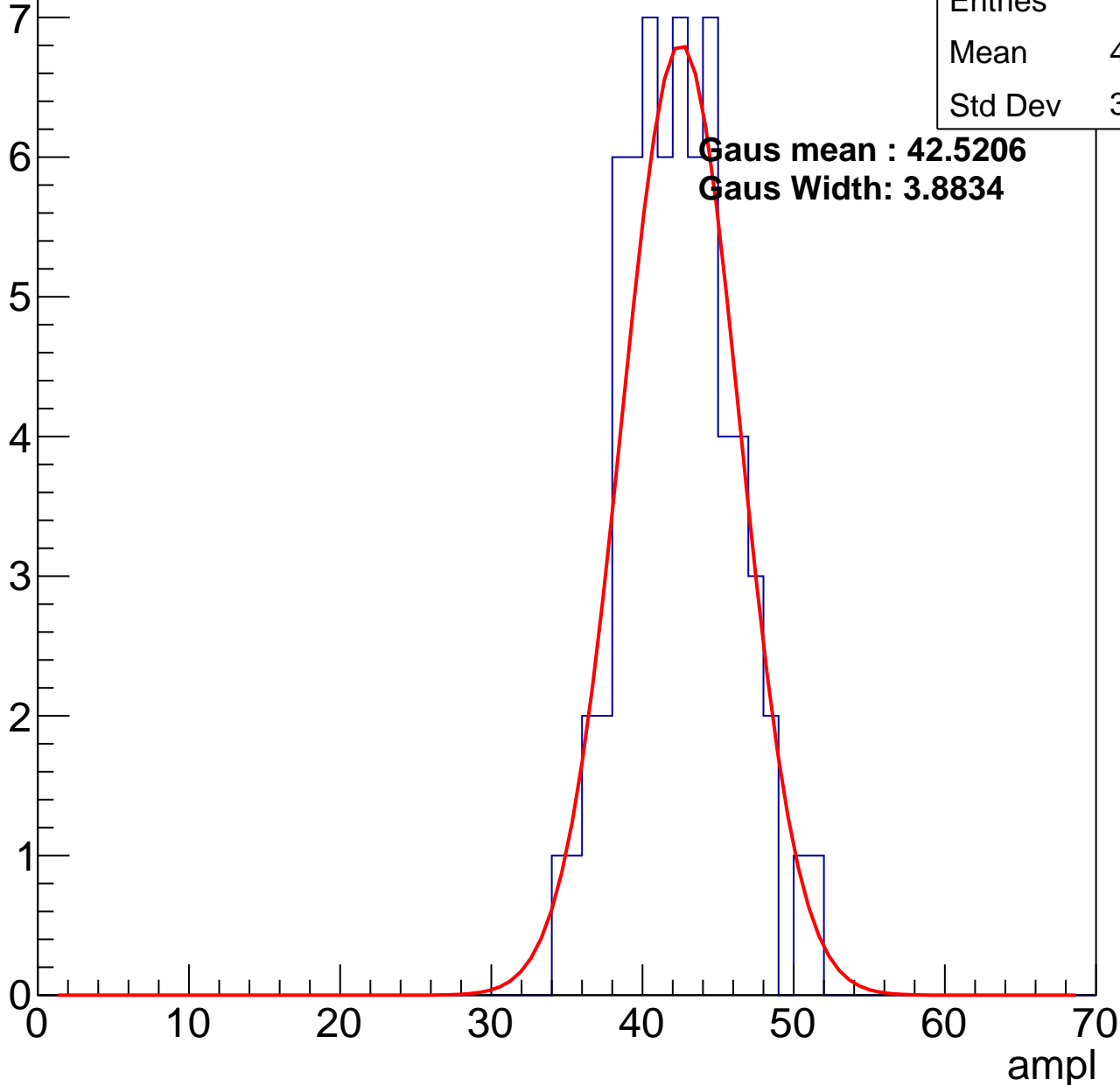


B1L102S, U17-ch46, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.89
Std Dev	3.577

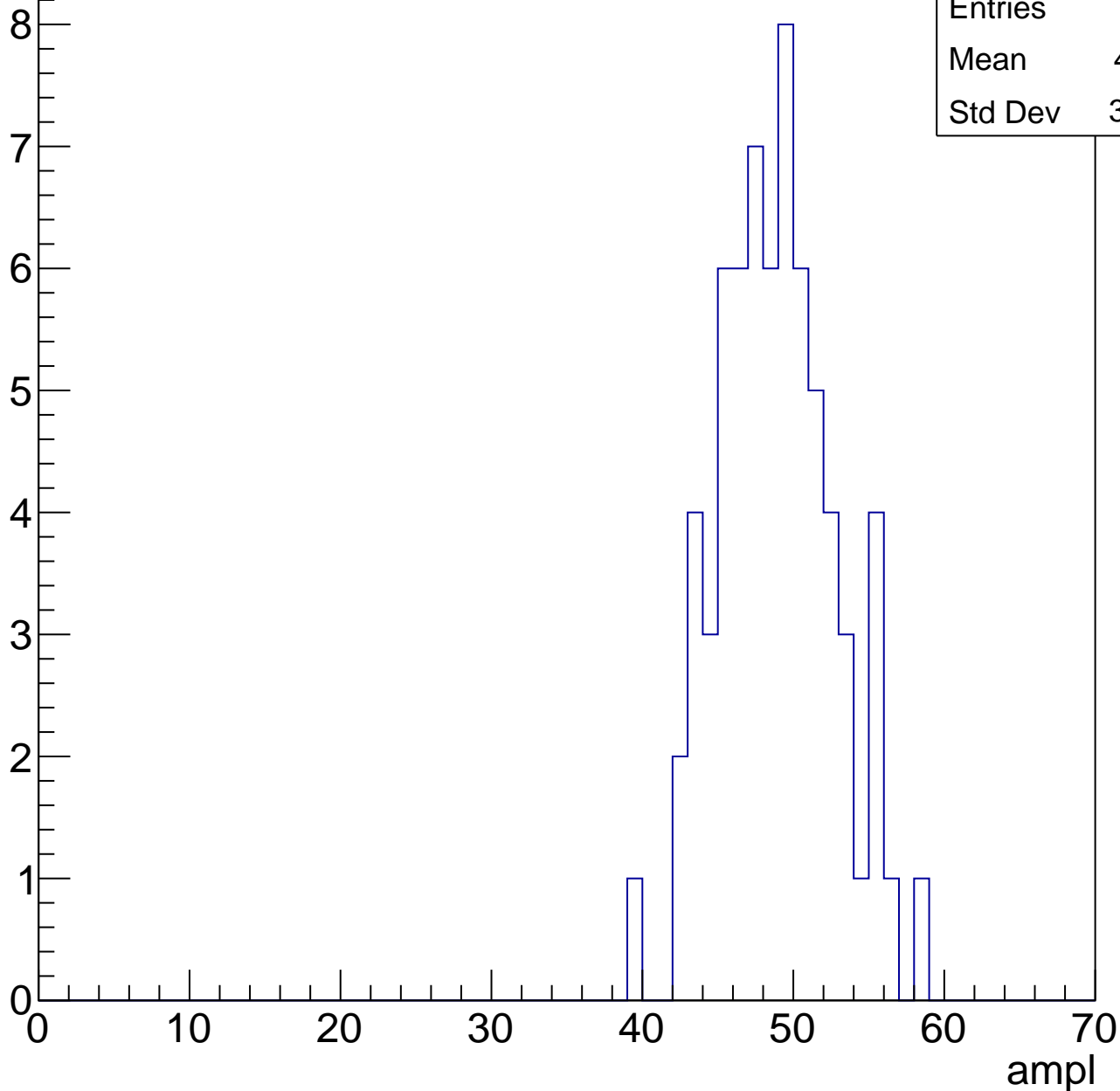


B1L102S, U17-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.41
Std Dev	3.809

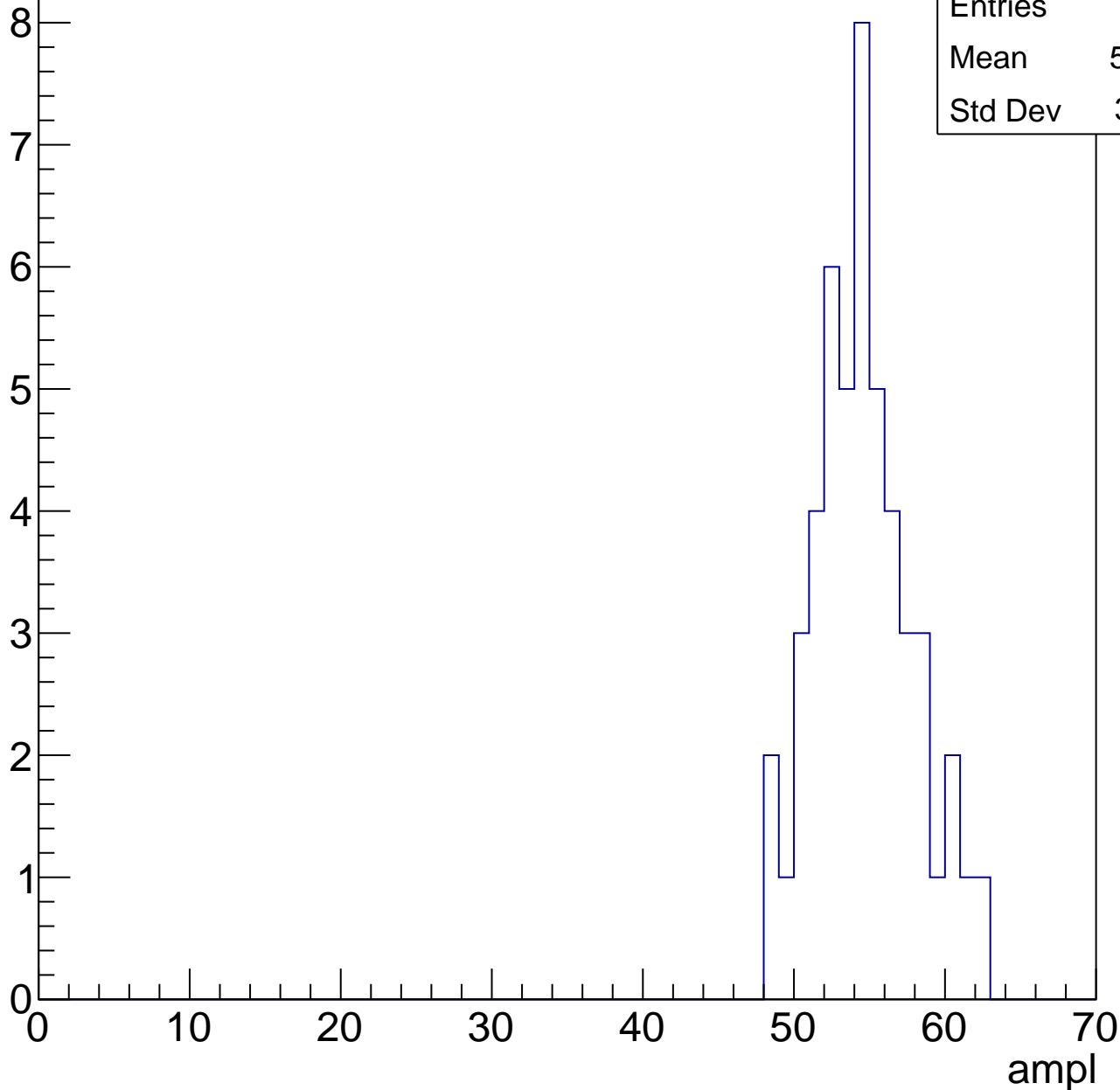


B1L102S, U17-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.16
Std Dev	3.241

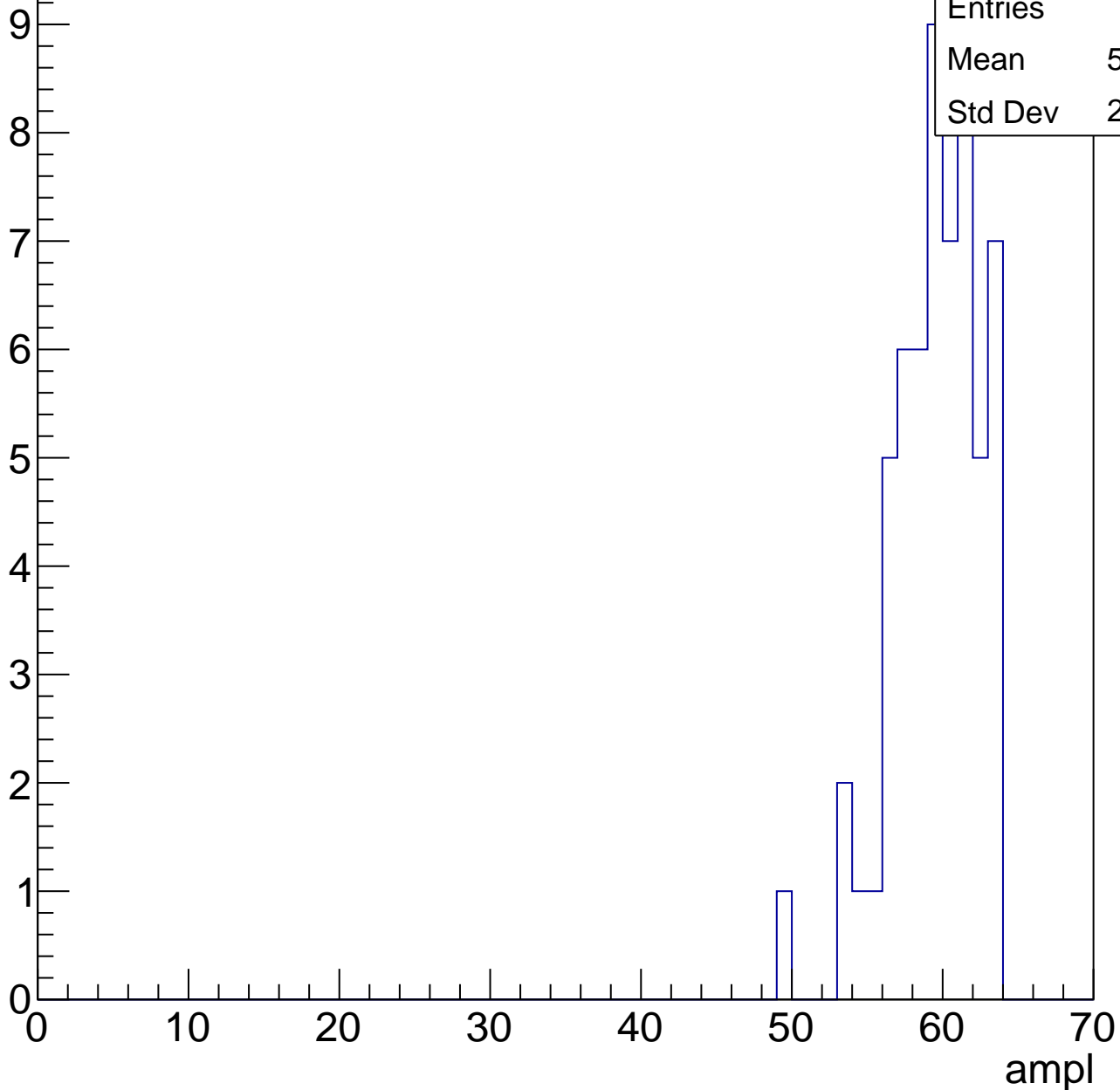


B1L102S, U17-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

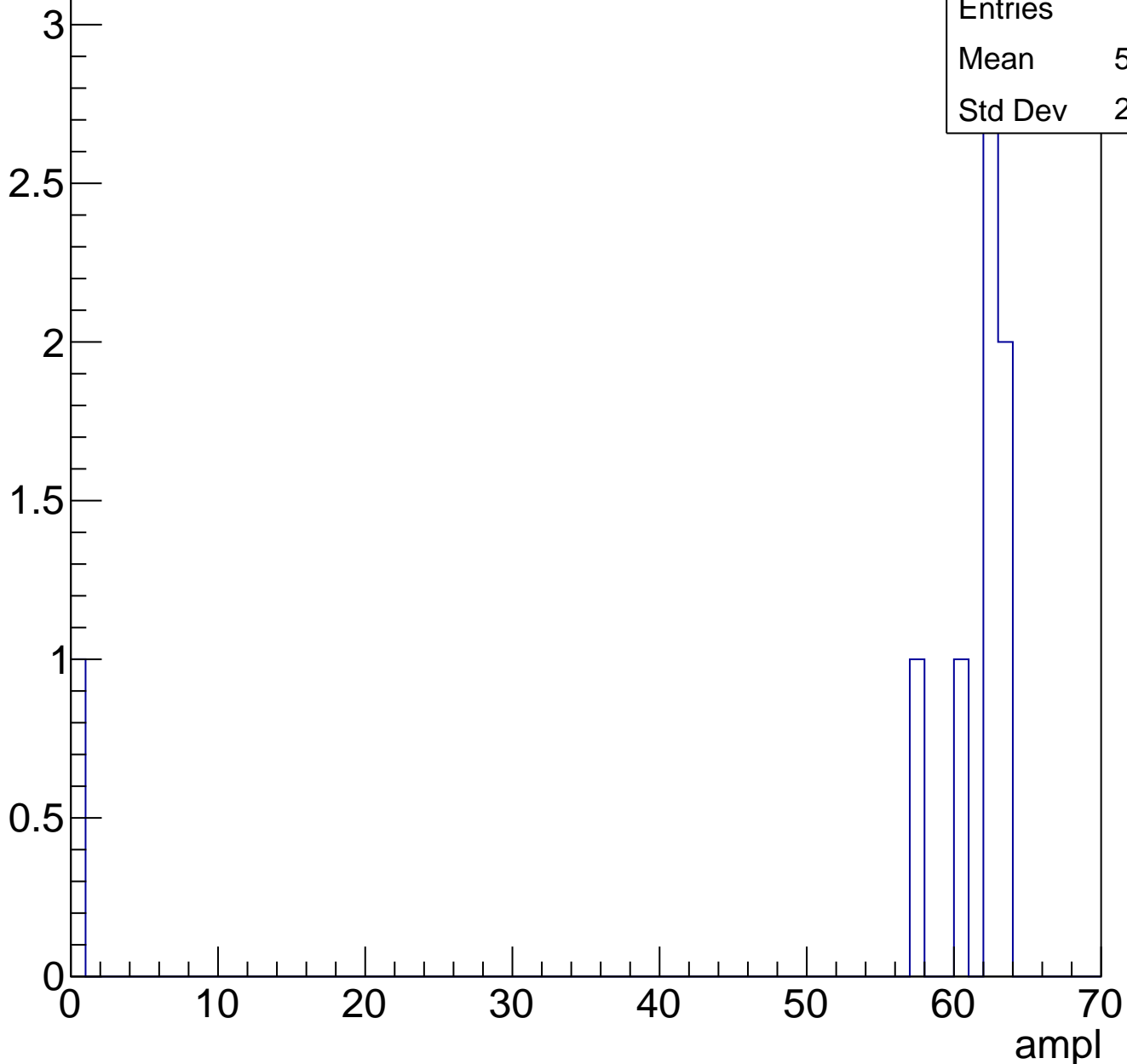
Entries	58
Mean	59.03
Std Dev	2.889



B1L102S, U17-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

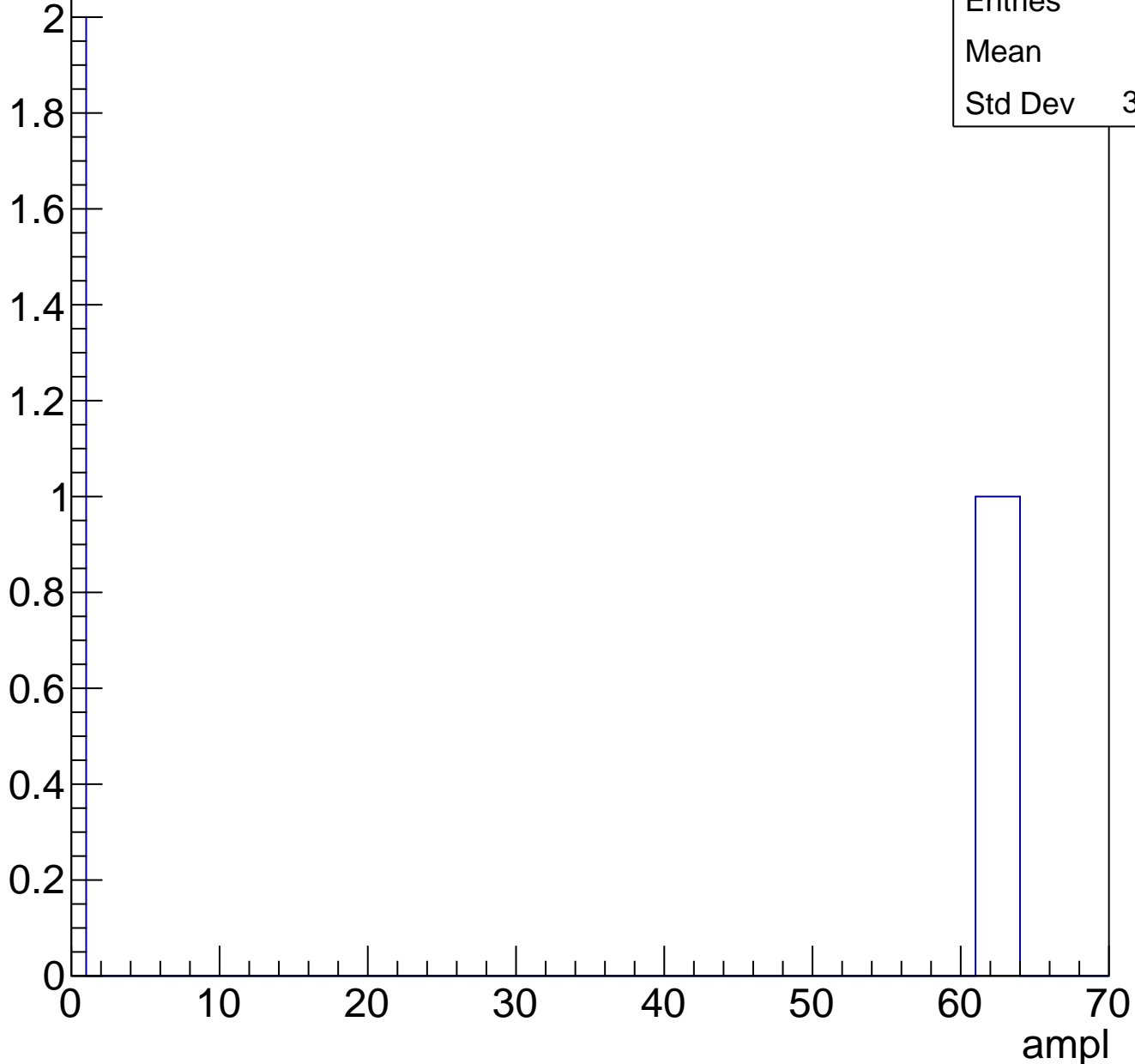
Entry



B1L102S, U17-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	37.2
Std Dev	30.38

B1L102S, U17-ch47, adc0

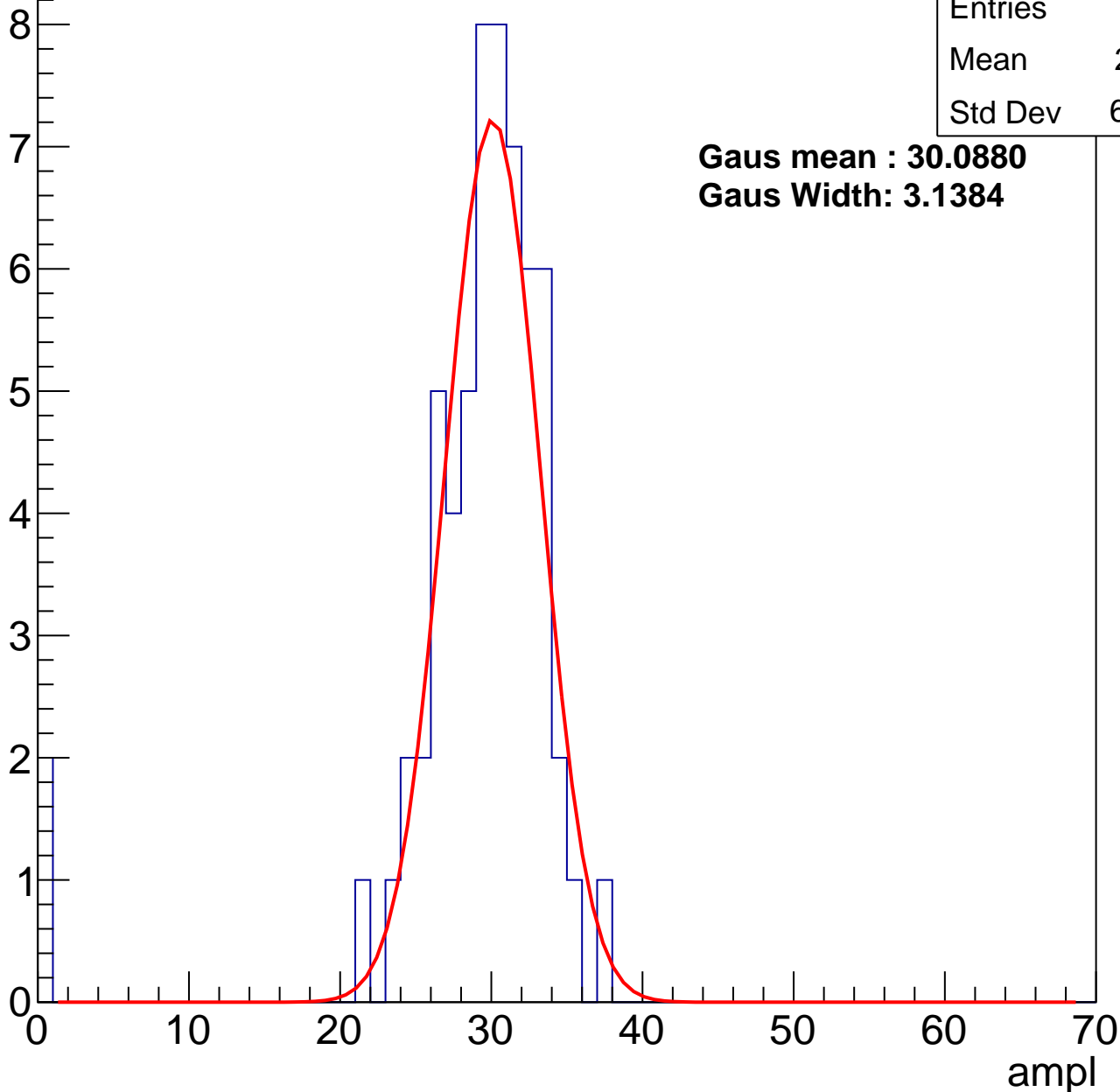
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.51
Std Dev	6.075

Gaus mean : 30.0880

Gaus Width: 3.1384



B1L102S, U17-ch47, adc1

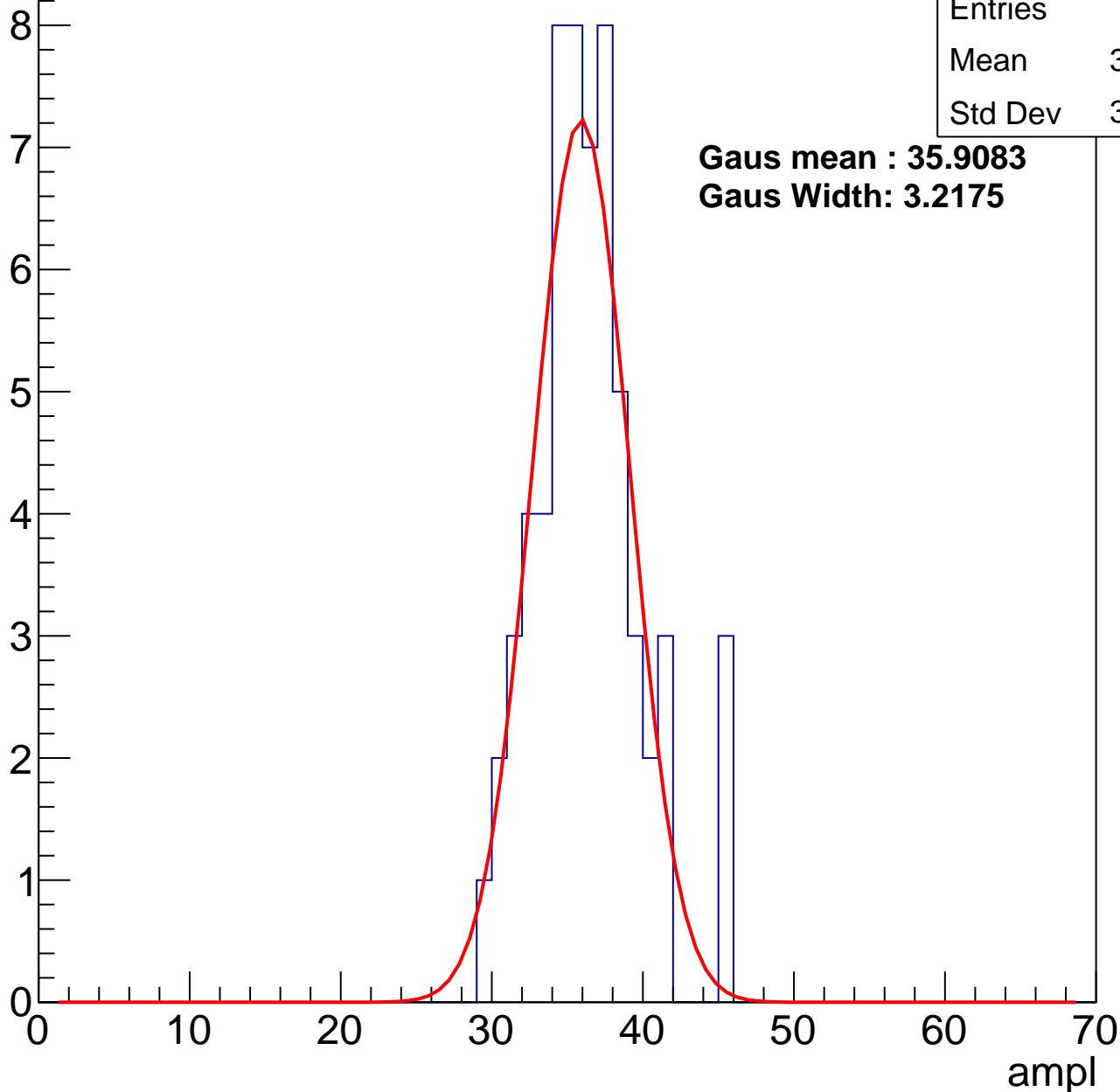
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.85
Std Dev	3.482

Gaus mean : 35.9083

Gaus Width: 3.2175



B1L102S, U17-ch47, adc2

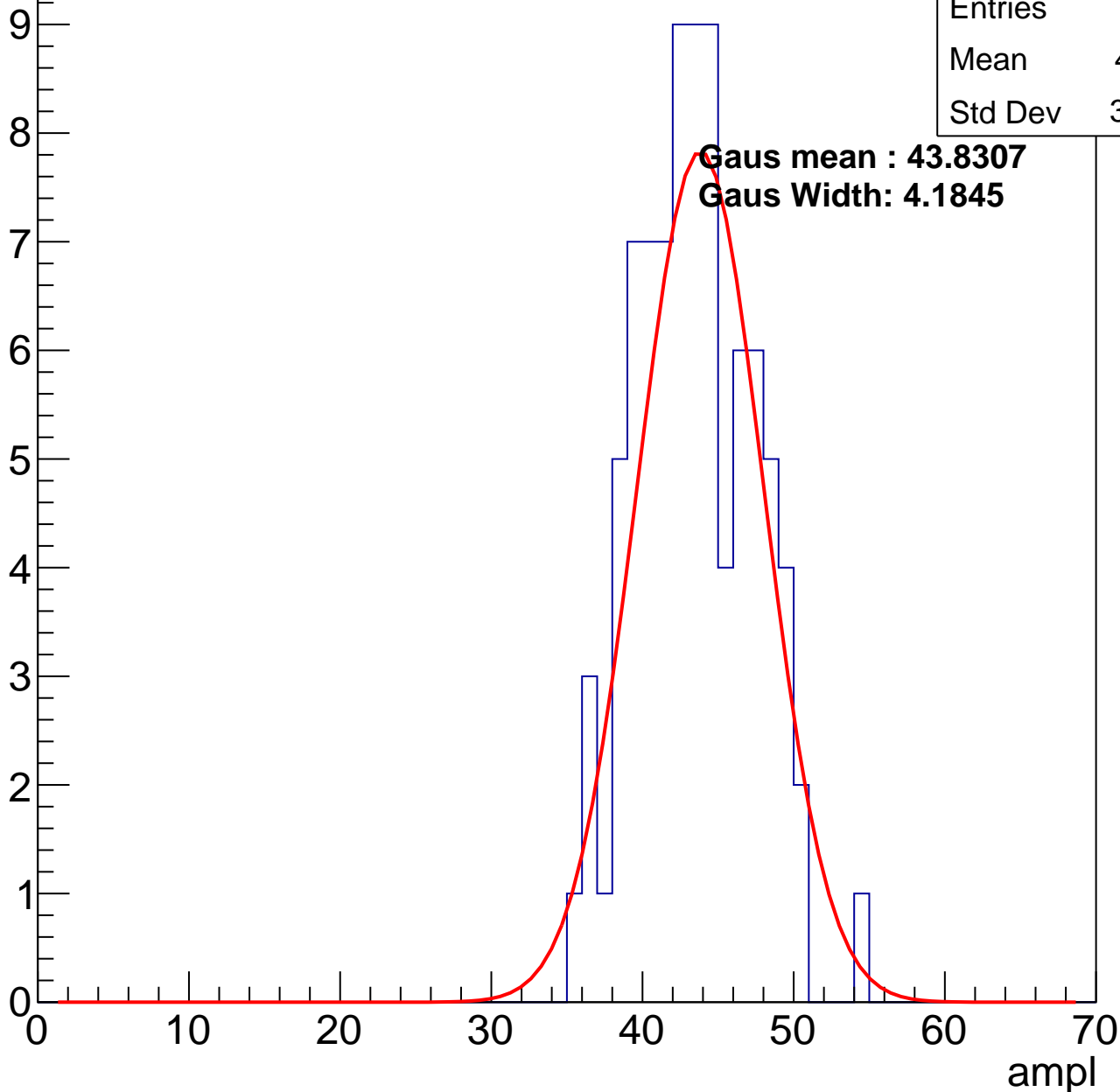
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	43.01
Std Dev	3.808

Gaus mean : 43.8307

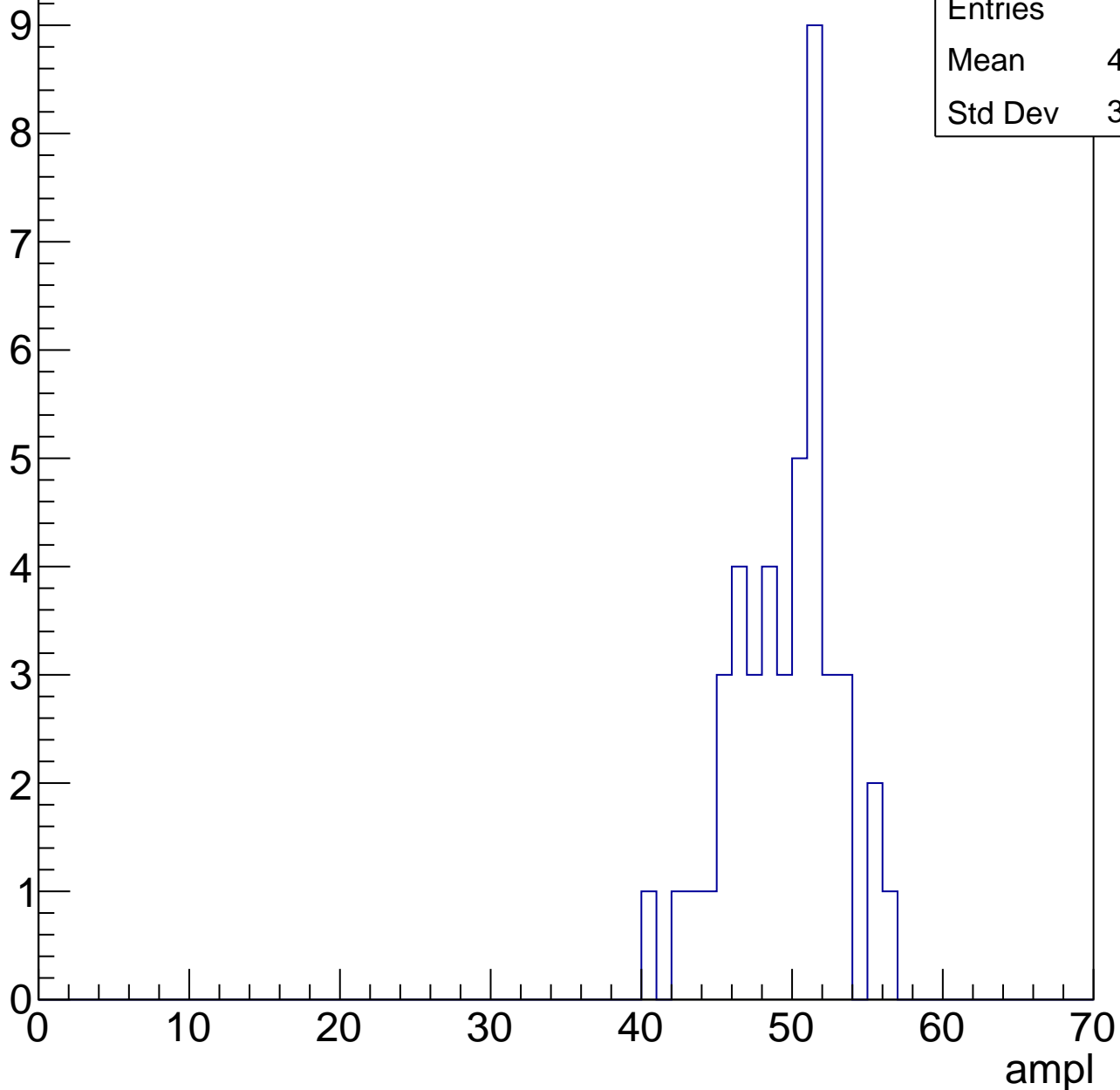
Gaus Width: 4.1845



B1L102S, U17-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



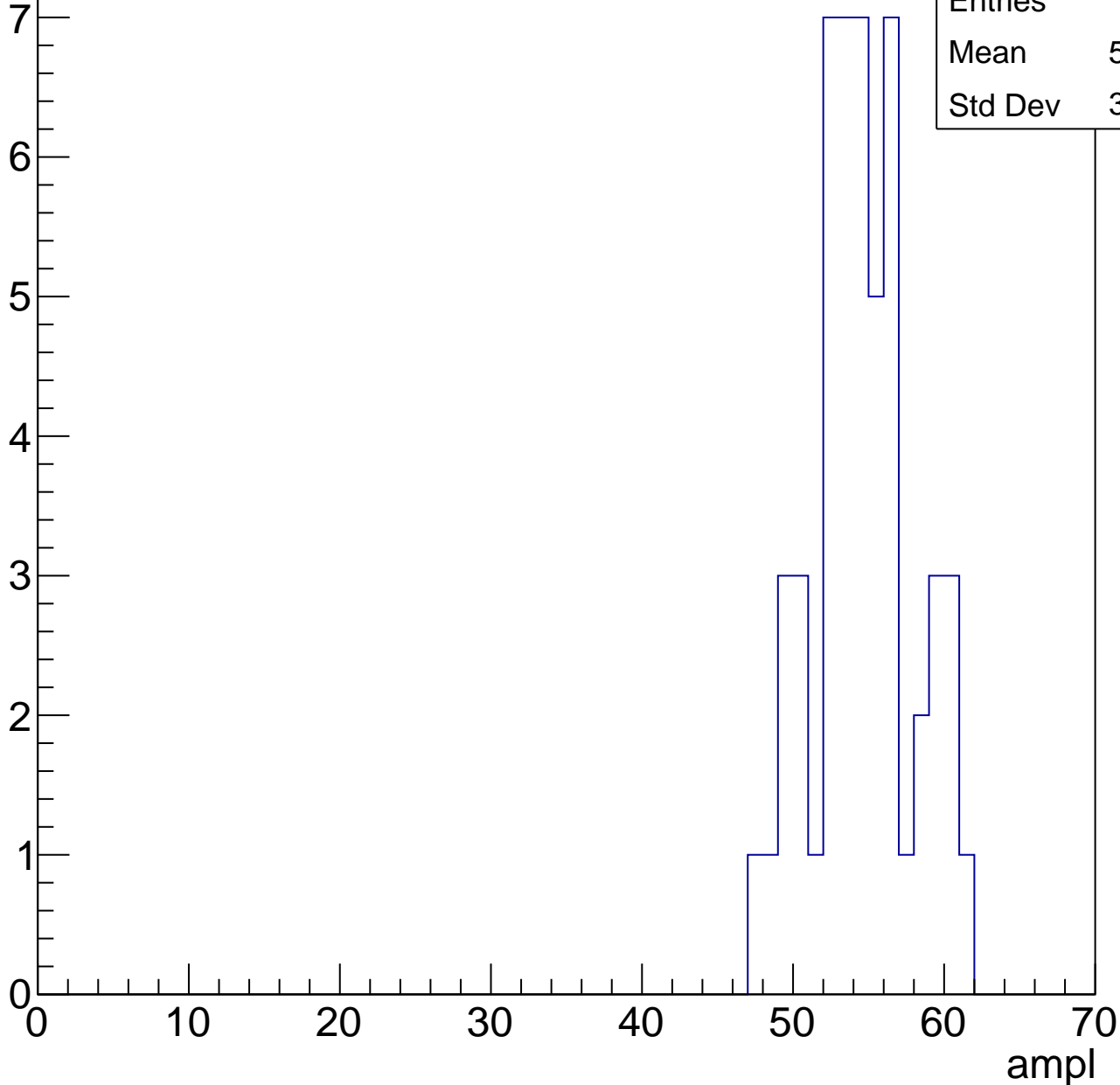
Entries	44
Mean	49.05
Std Dev	3.464

B1L102S, U17-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.12
Std Dev	3.274



B1L102S, U17-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

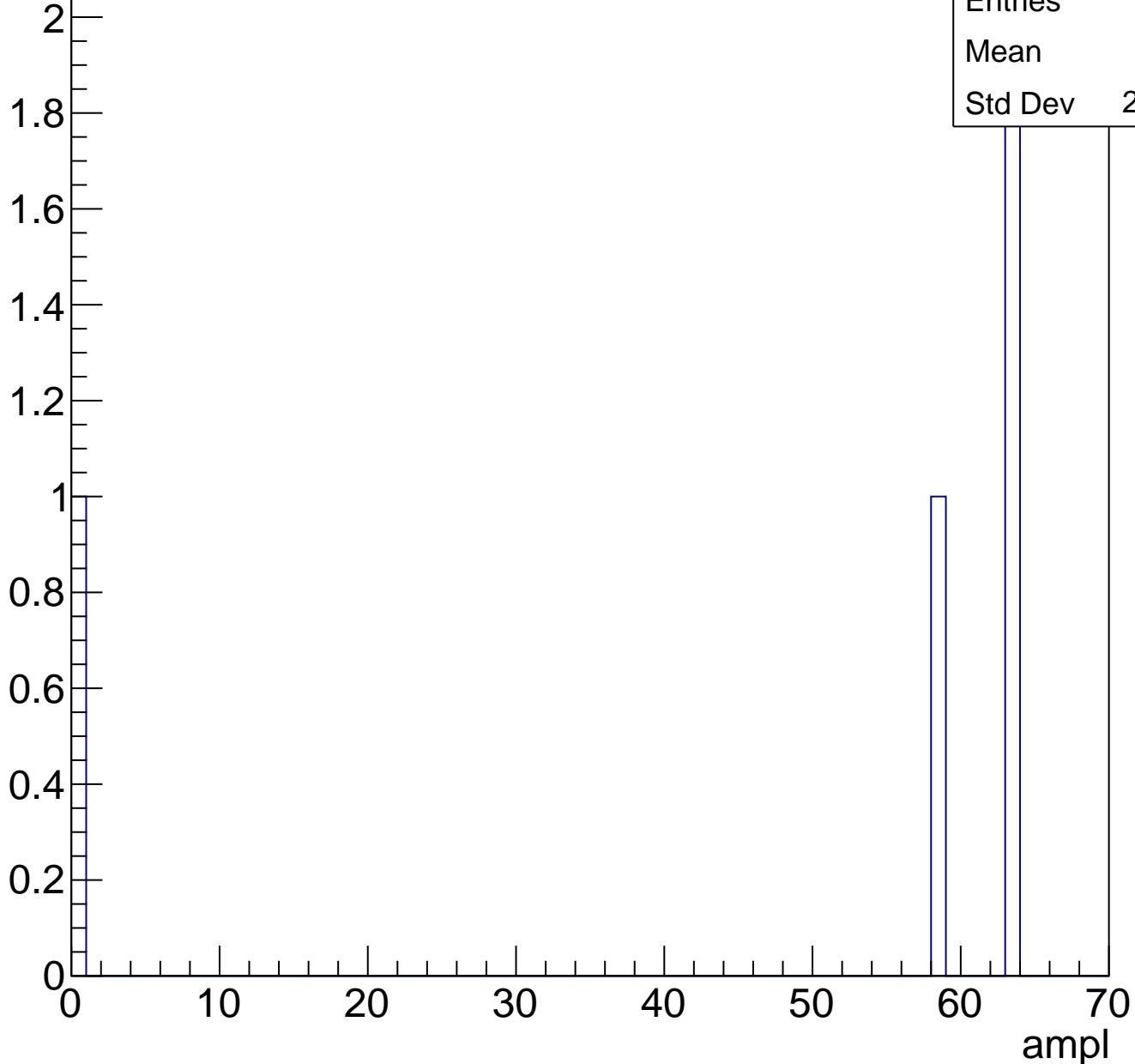
ampl

Entries	63
Mean	59.48
Std Dev	2.487

B1L102S, U17-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch48, adc0

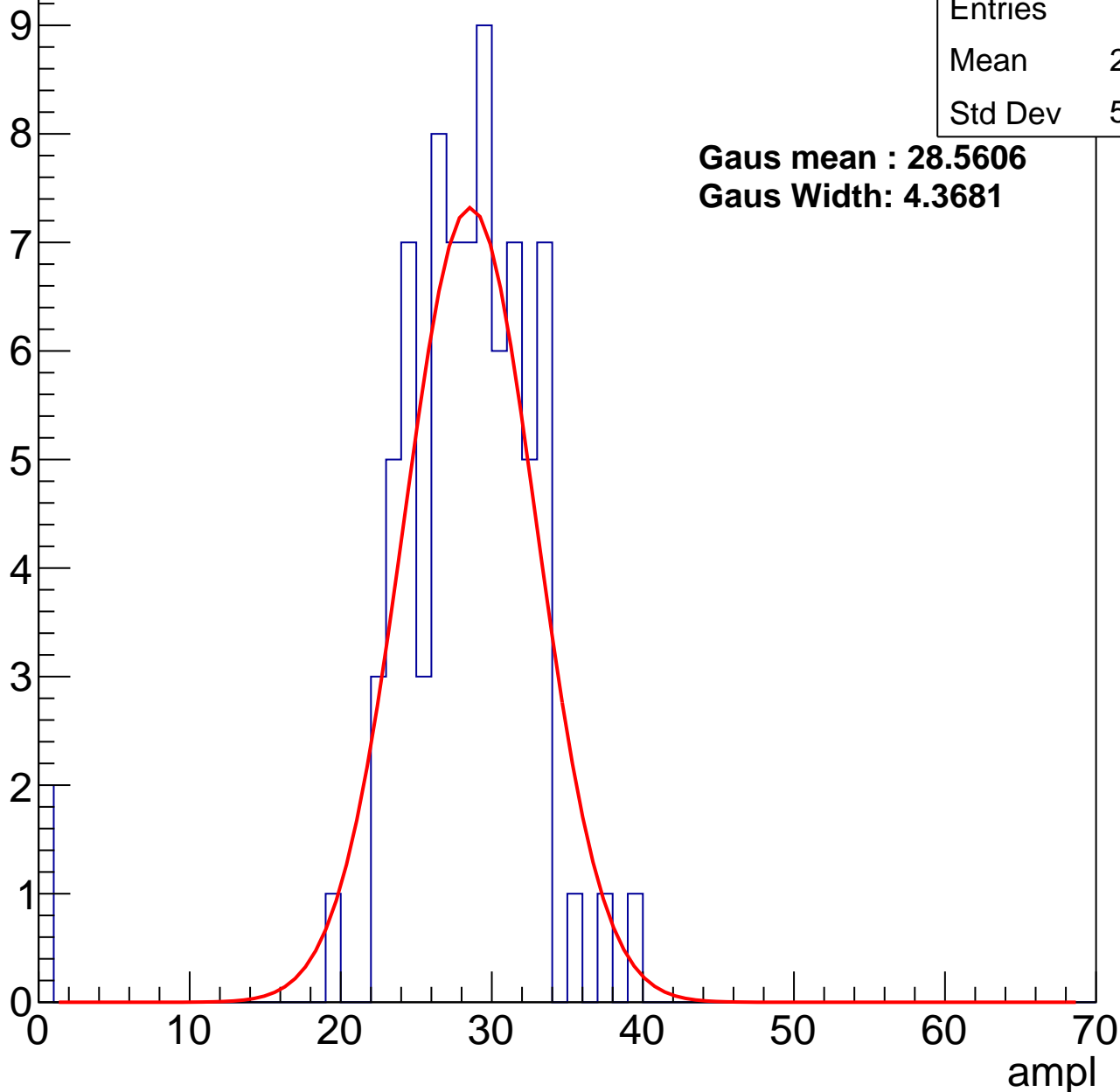
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.45
Std Dev	5.742

Gaus mean : 28.5606

Gaus Width: 4.3681



B1L102S, U17-ch48, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	55
Mean	34.55
Std Dev	3.218

Gaus mean : 35.0684

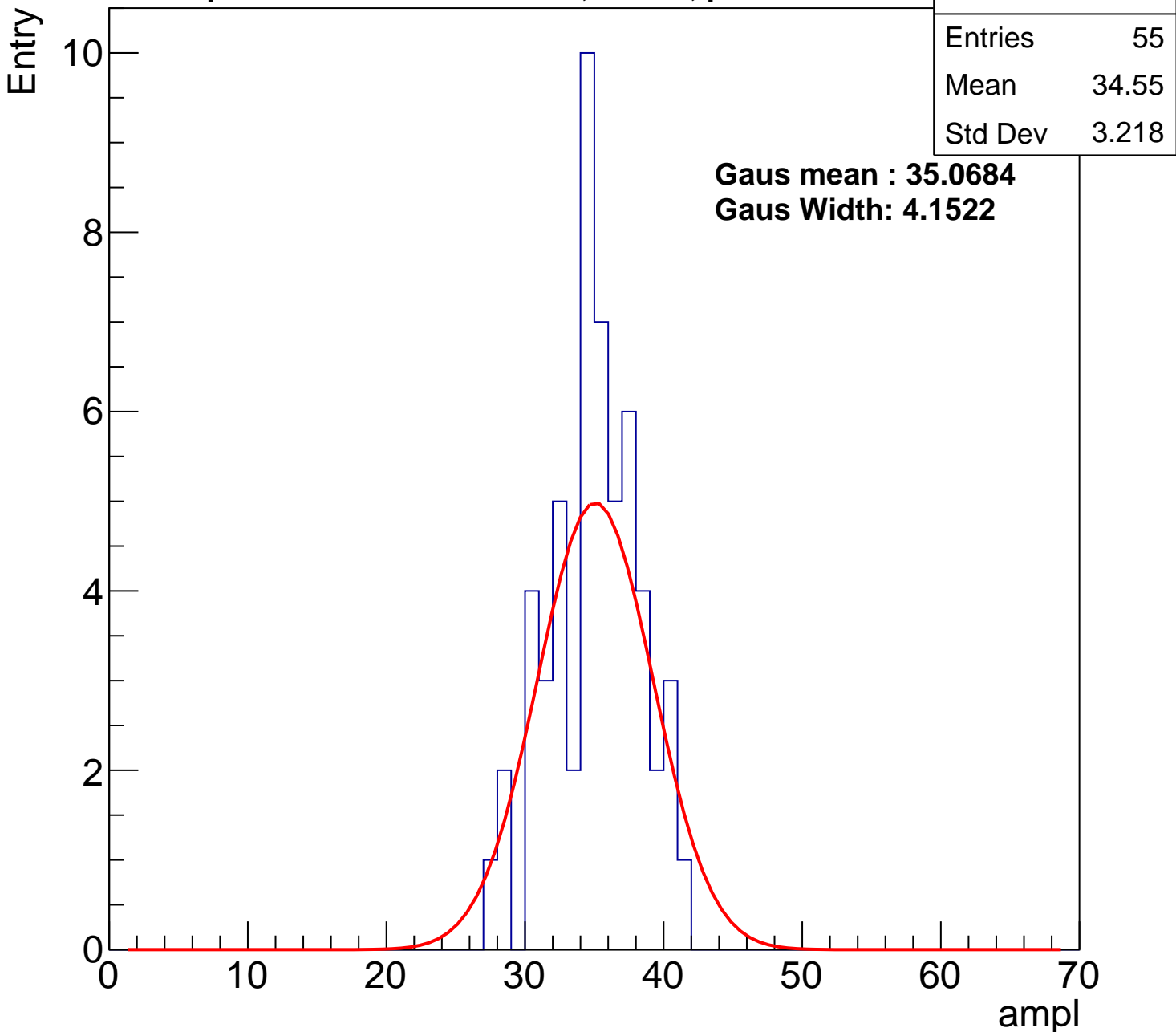
Gaus Width: 4.1522

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch48, adc2

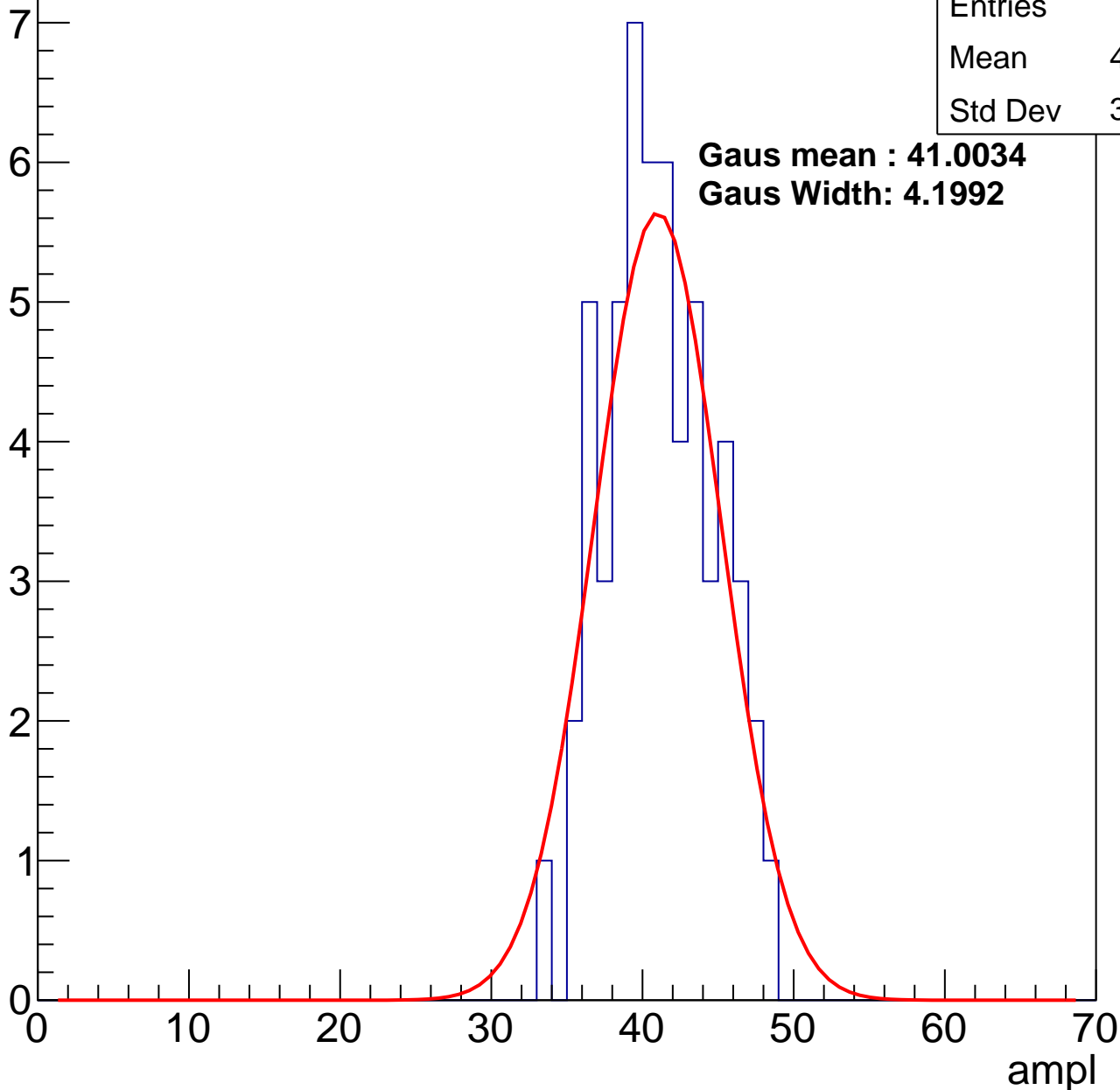
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	40.67
Std Dev	3.486

Gaus mean : 41.0034

Gaus Width: 4.1992

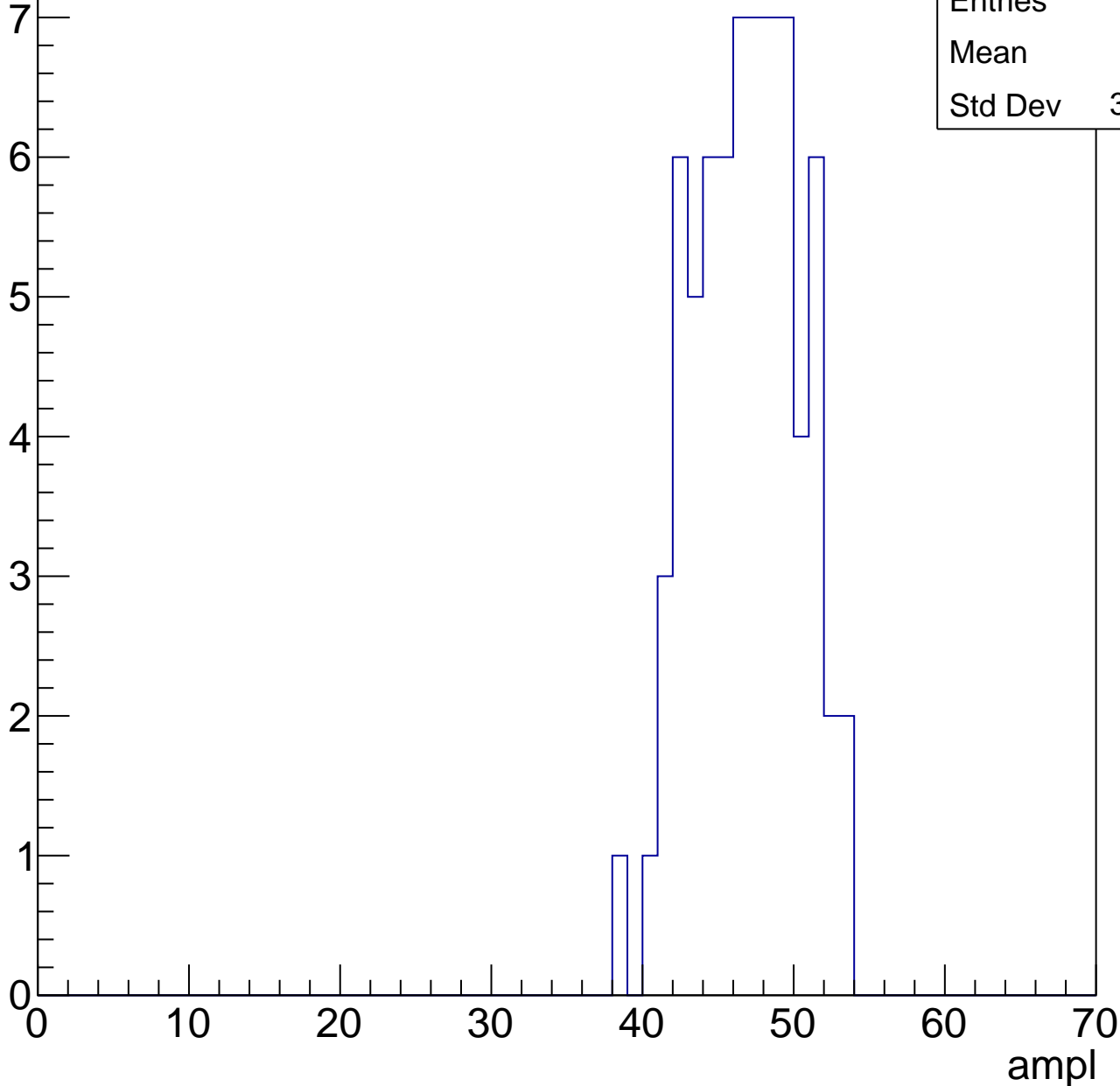


B1L102S, U17-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	46.4
Std Dev	3.416

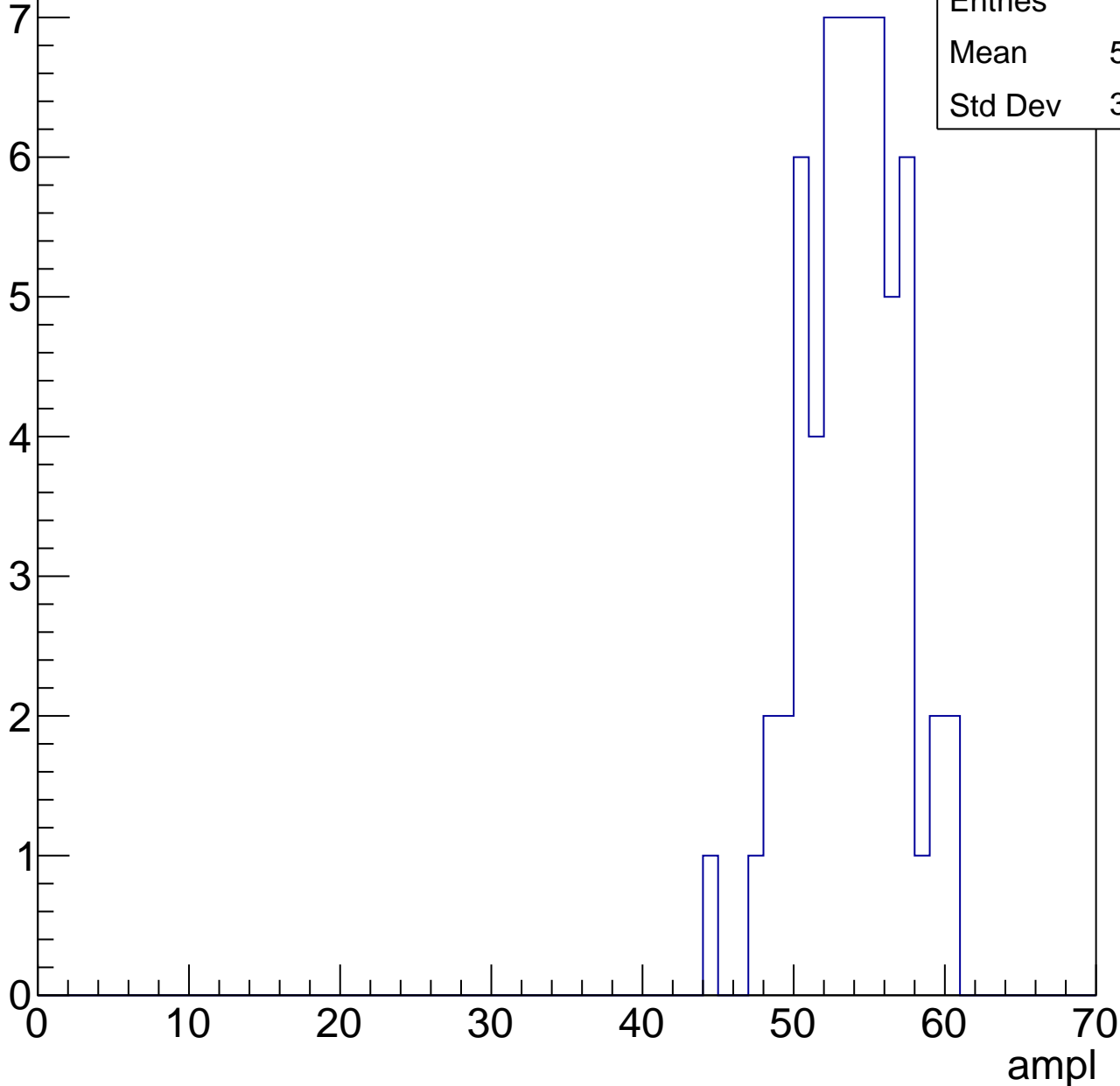


B1L102S, U17-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.42
Std Dev	3.257

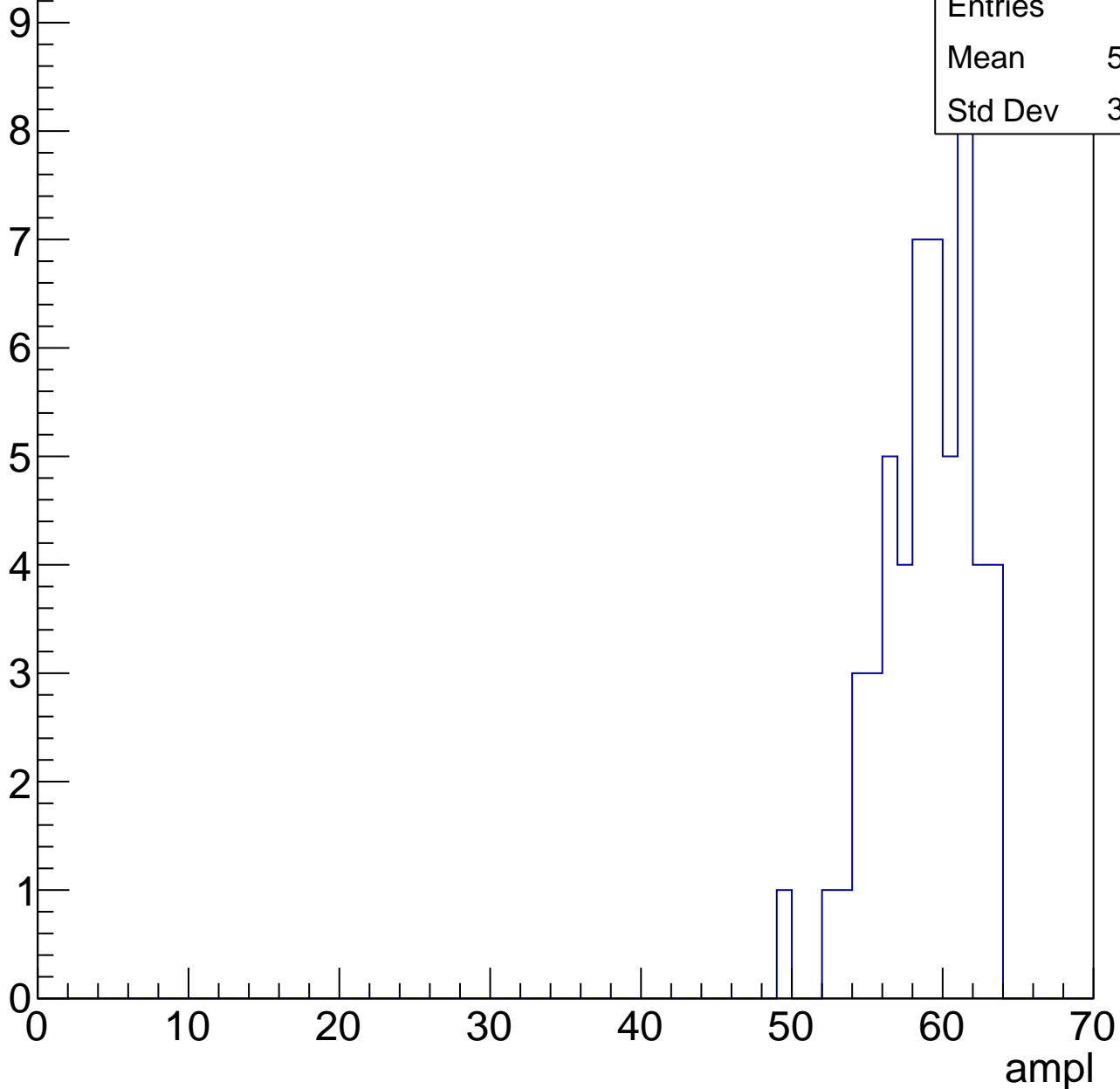


B1L102S, U17-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.46
Std Dev	3.047

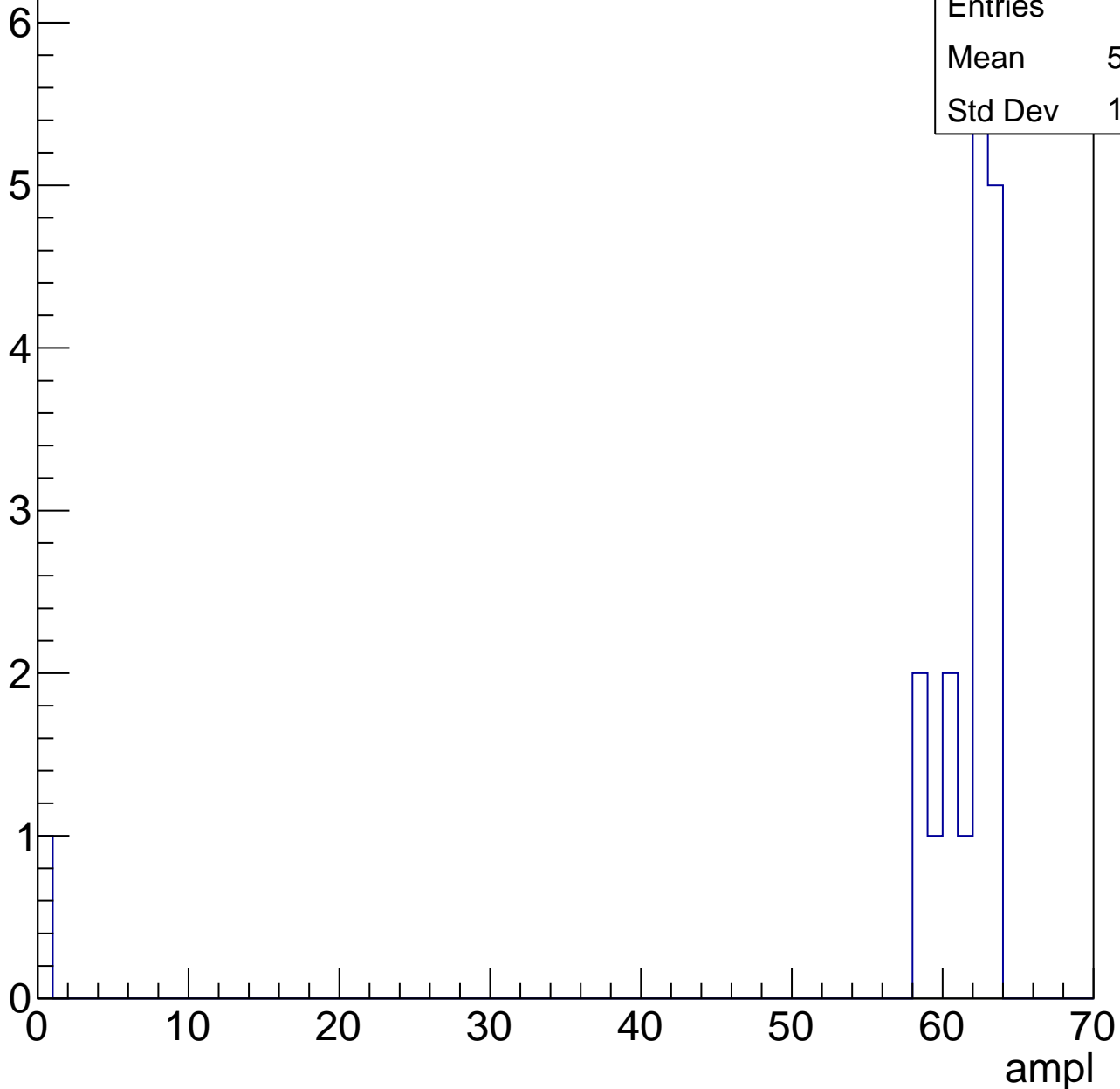


B1L102S, U17-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

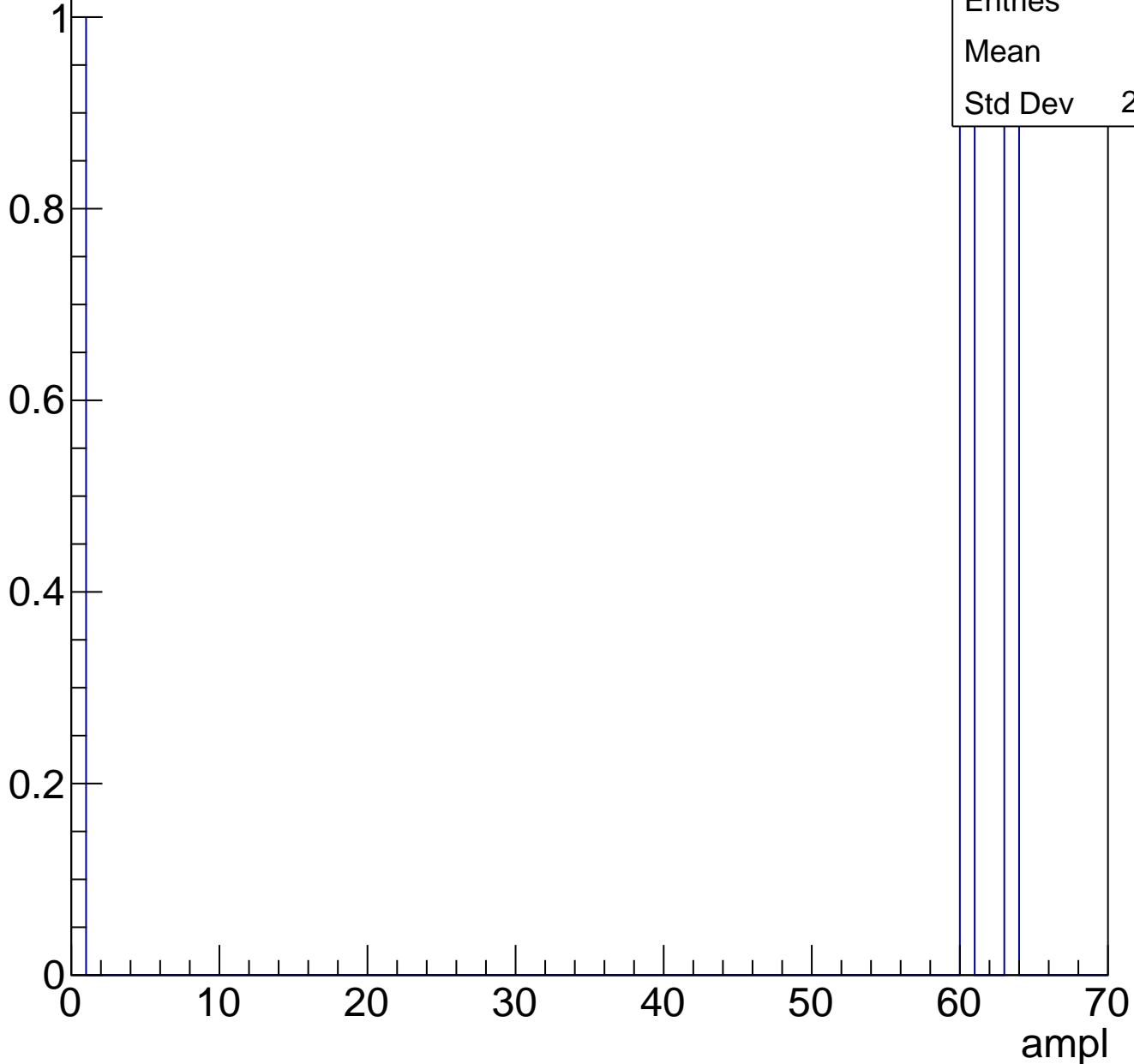
Entries	18
Mean	57.94
Std Dev	14.15



B1L102S, U17-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch49, adc0

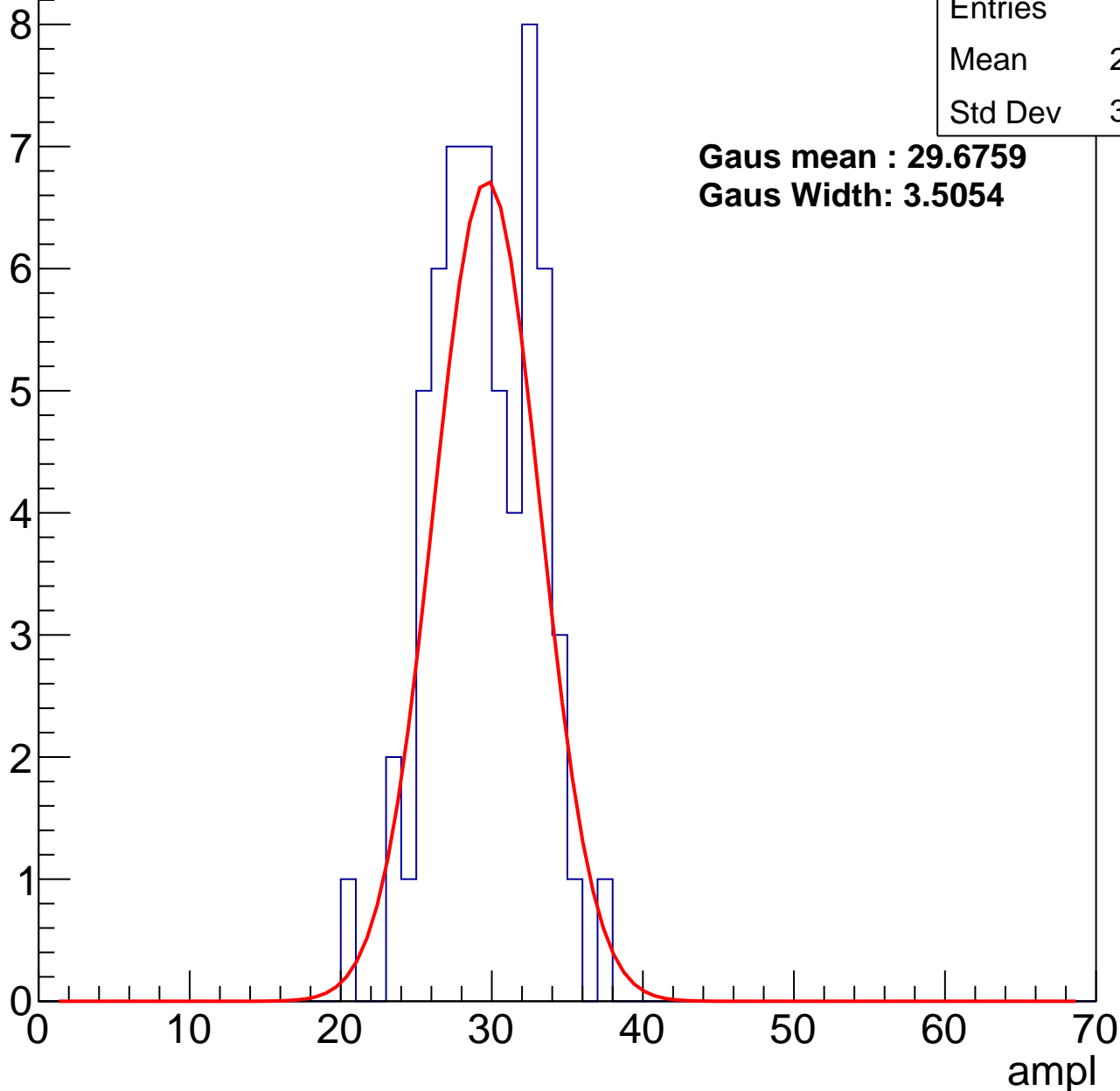
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.08
Std Dev	3.332

Gaus mean : 29.6759

Gaus Width: 3.5054



B1L102S, U17-ch49, adc1

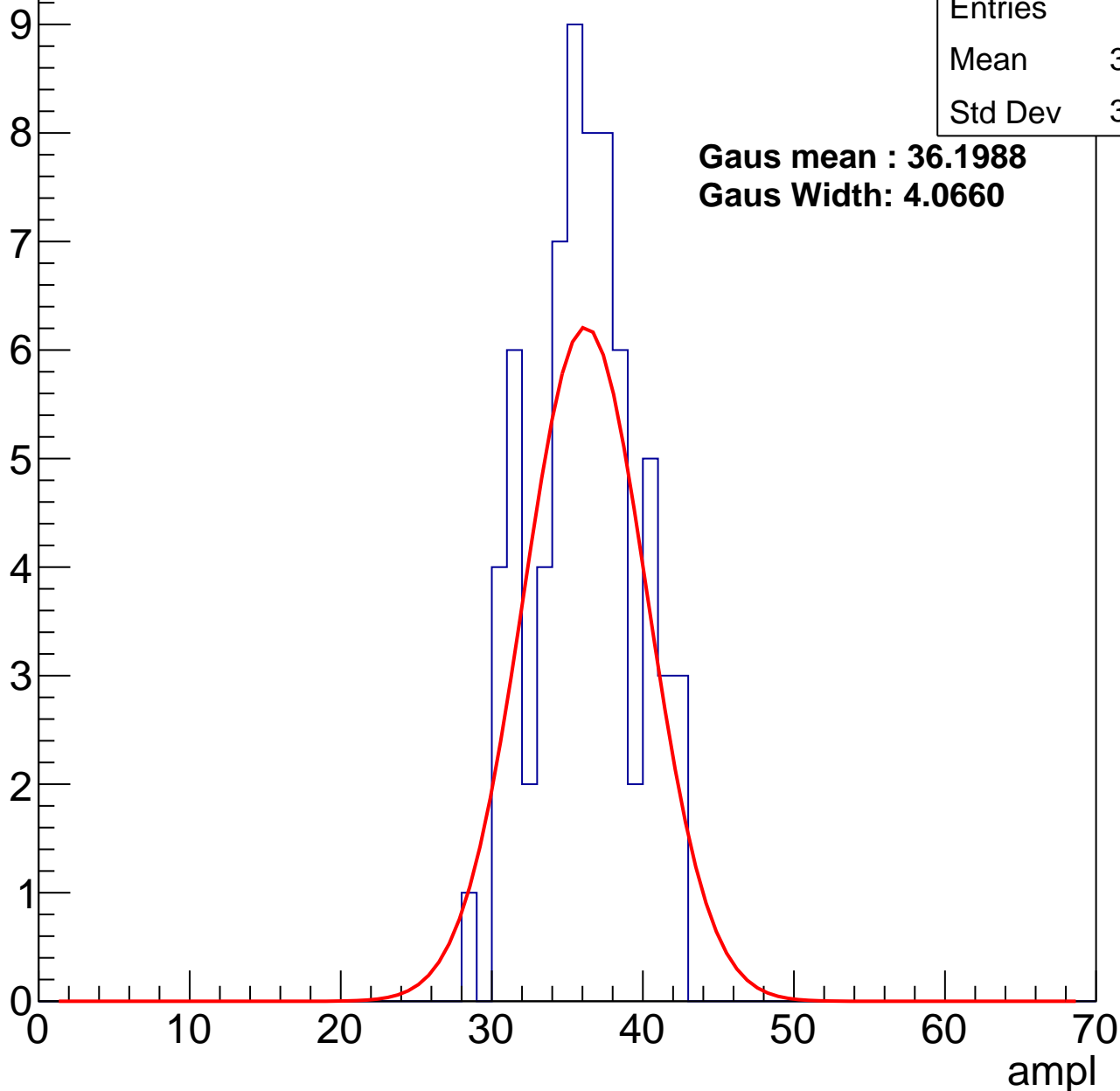
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.62
Std Dev	3.356

Gaus mean : 36.1988

Gaus Width: 4.0660



B1L102S, U17-ch49, adc2

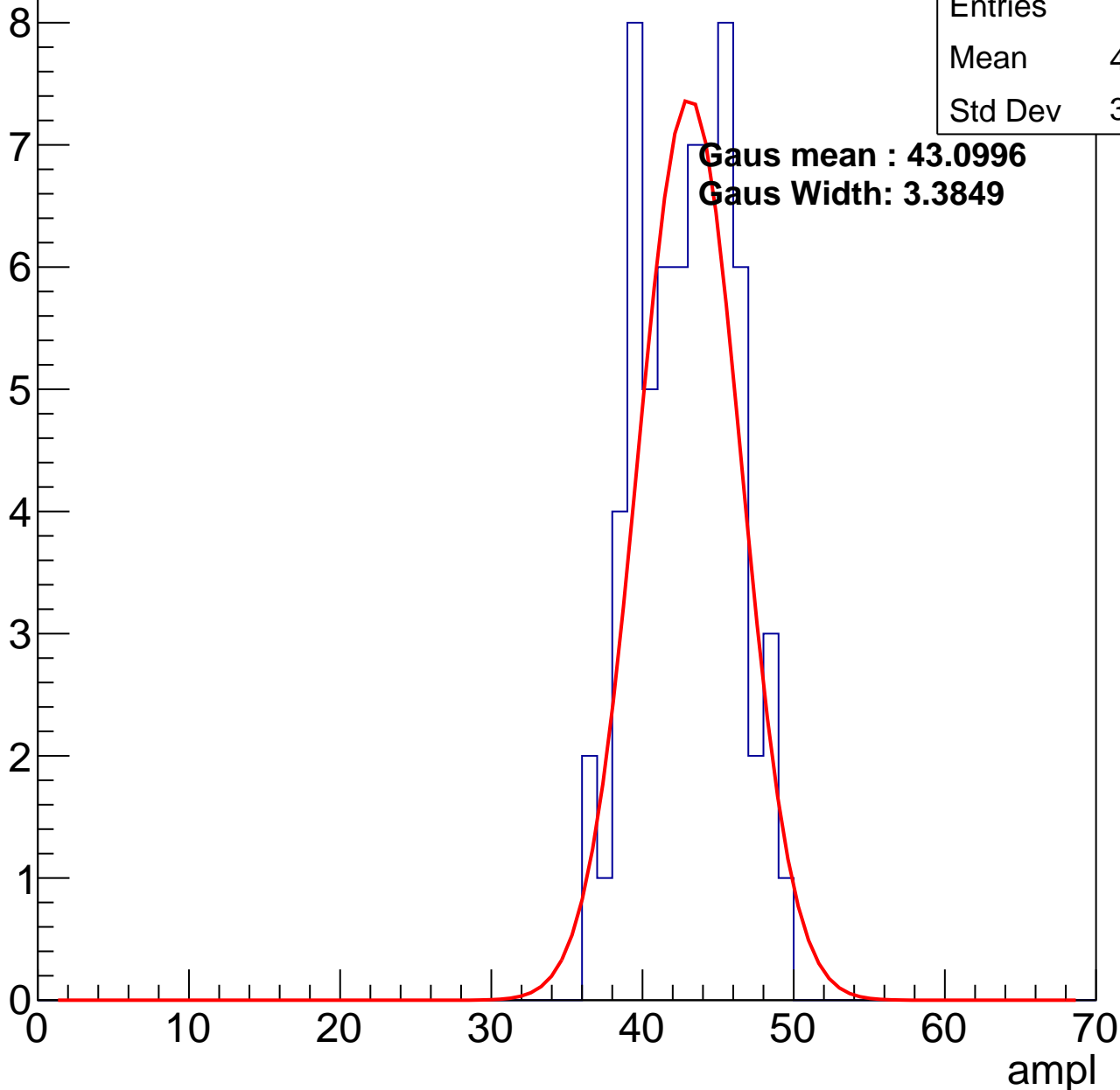
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.47
Std Dev	3.149

Gaus mean : 43.0996

Gaus Width: 3.3849

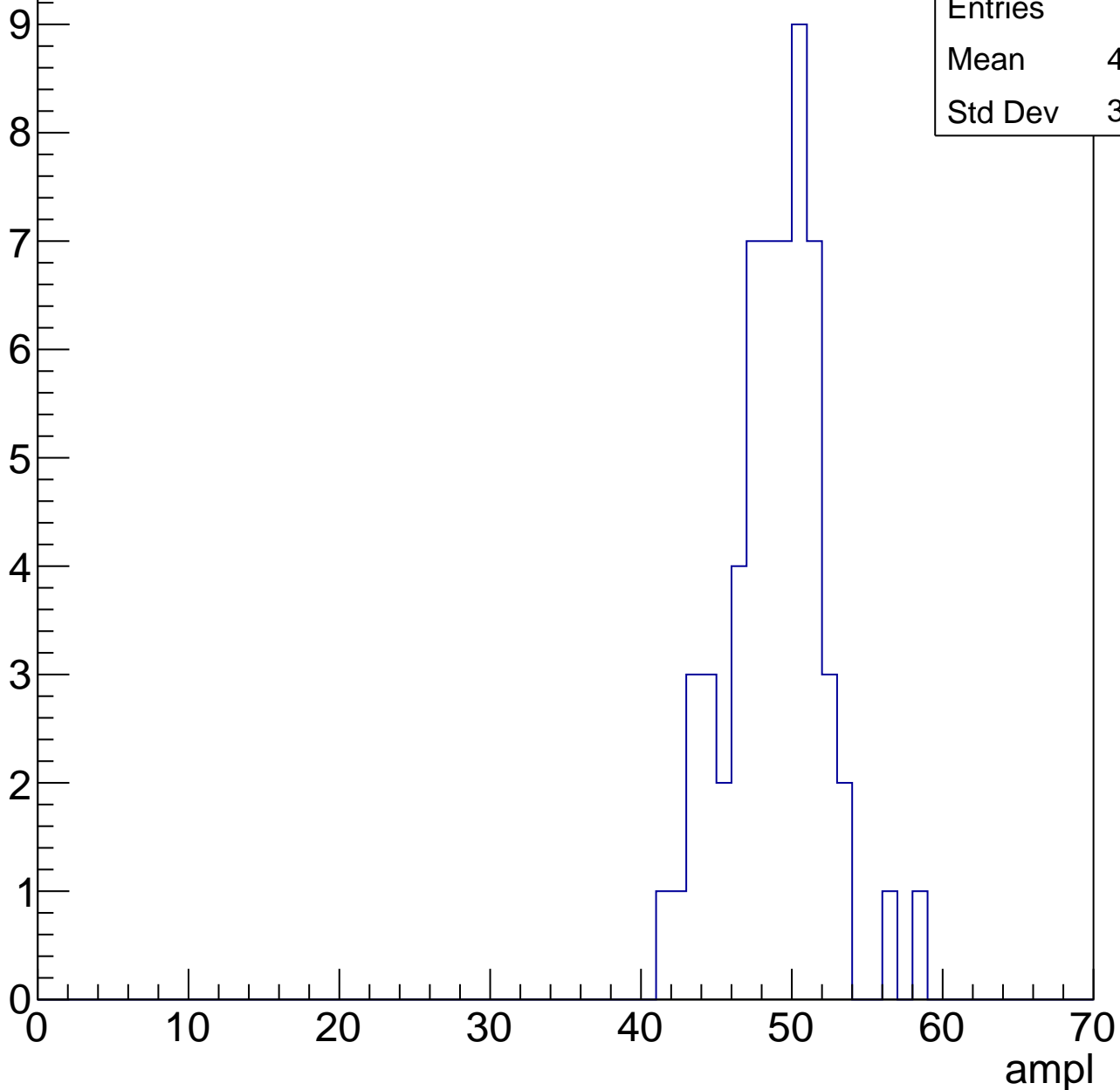


B1L102S, U17-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.43
Std Dev	3.233



B1L102S, U17-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

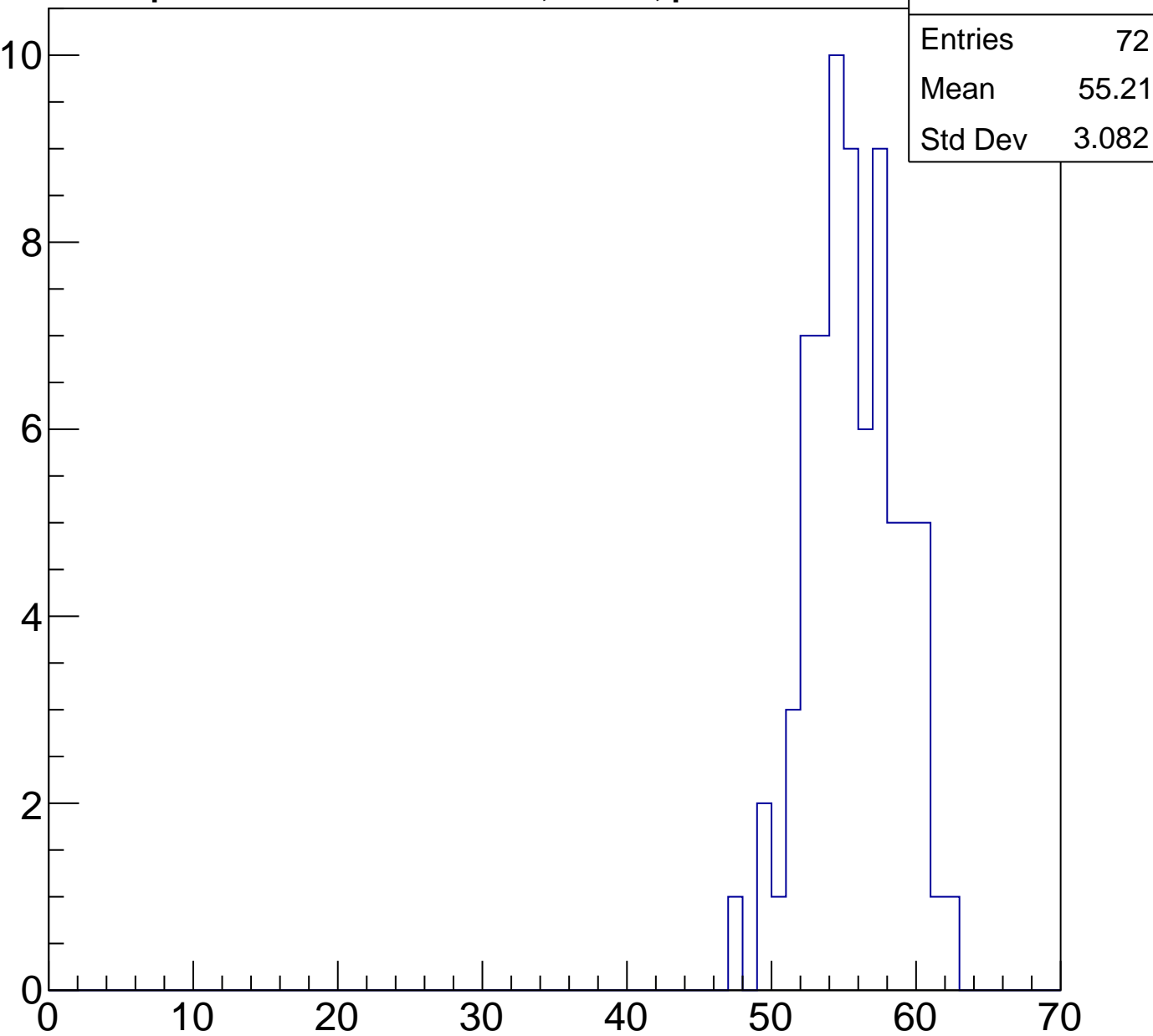
Entries	72
Mean	55.21
Std Dev	3.082

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

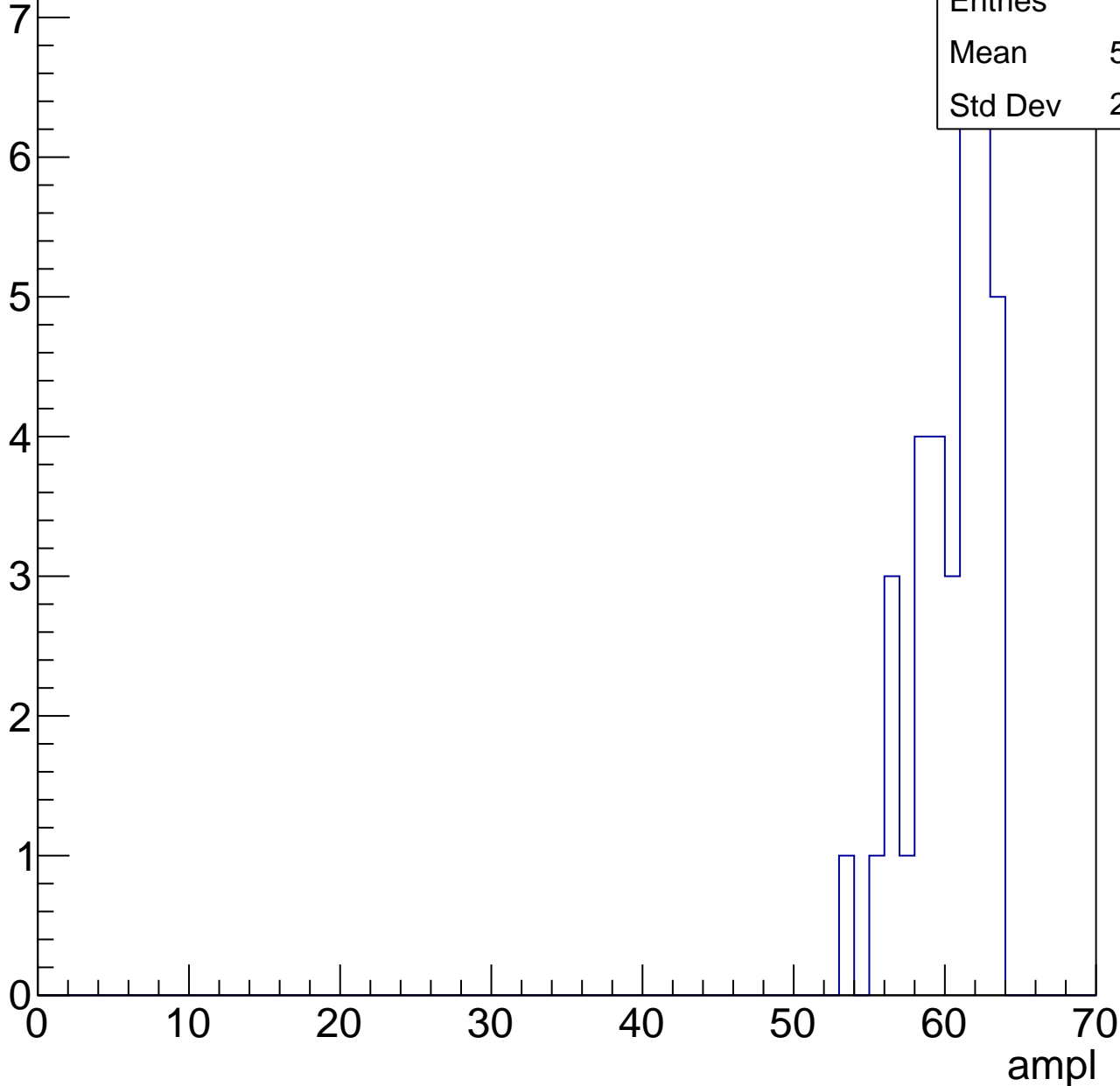


B1L102S, U17-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

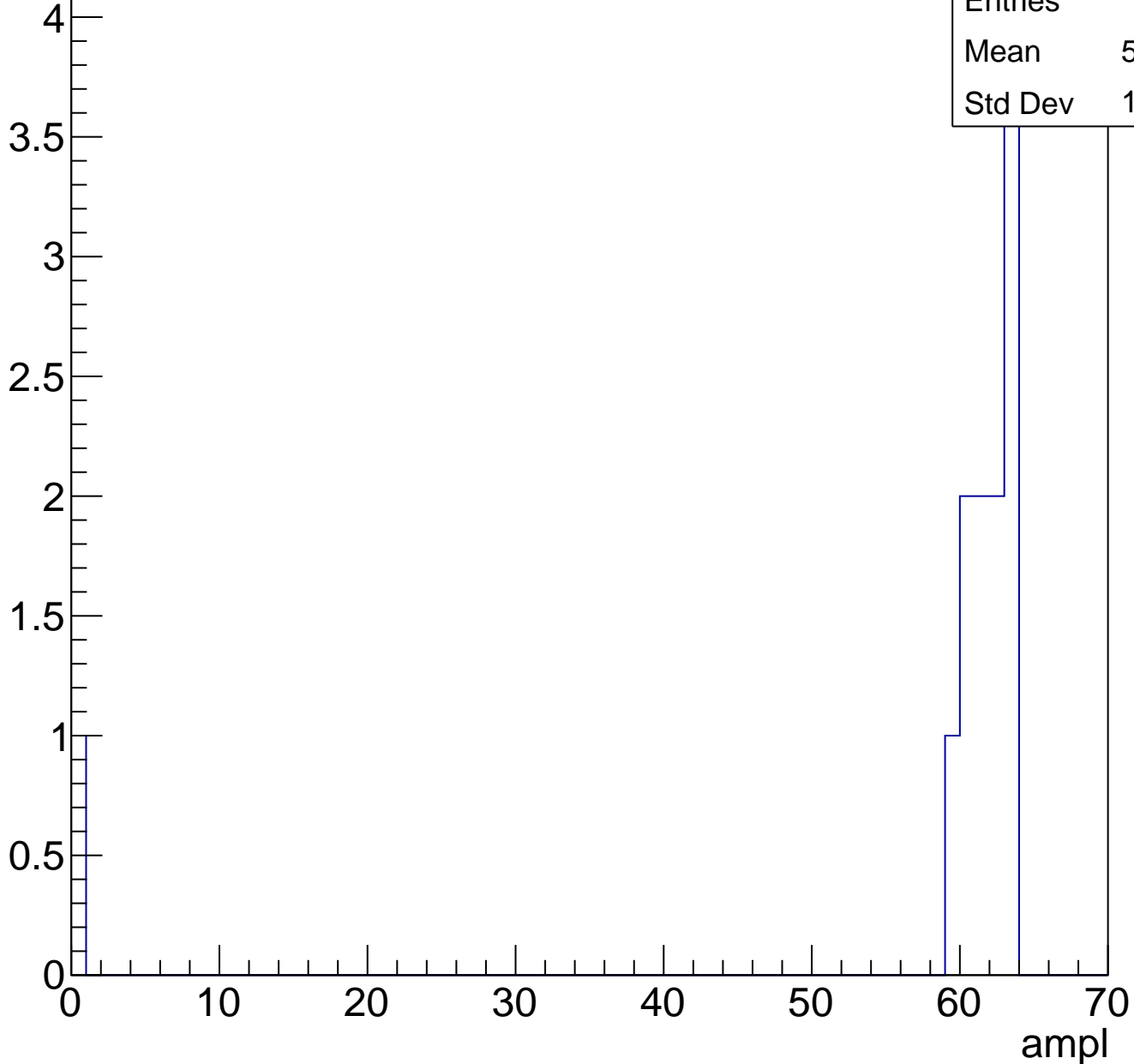
Entries	36
Mean	59.92
Std Dev	2.532



B1L102S, U17-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

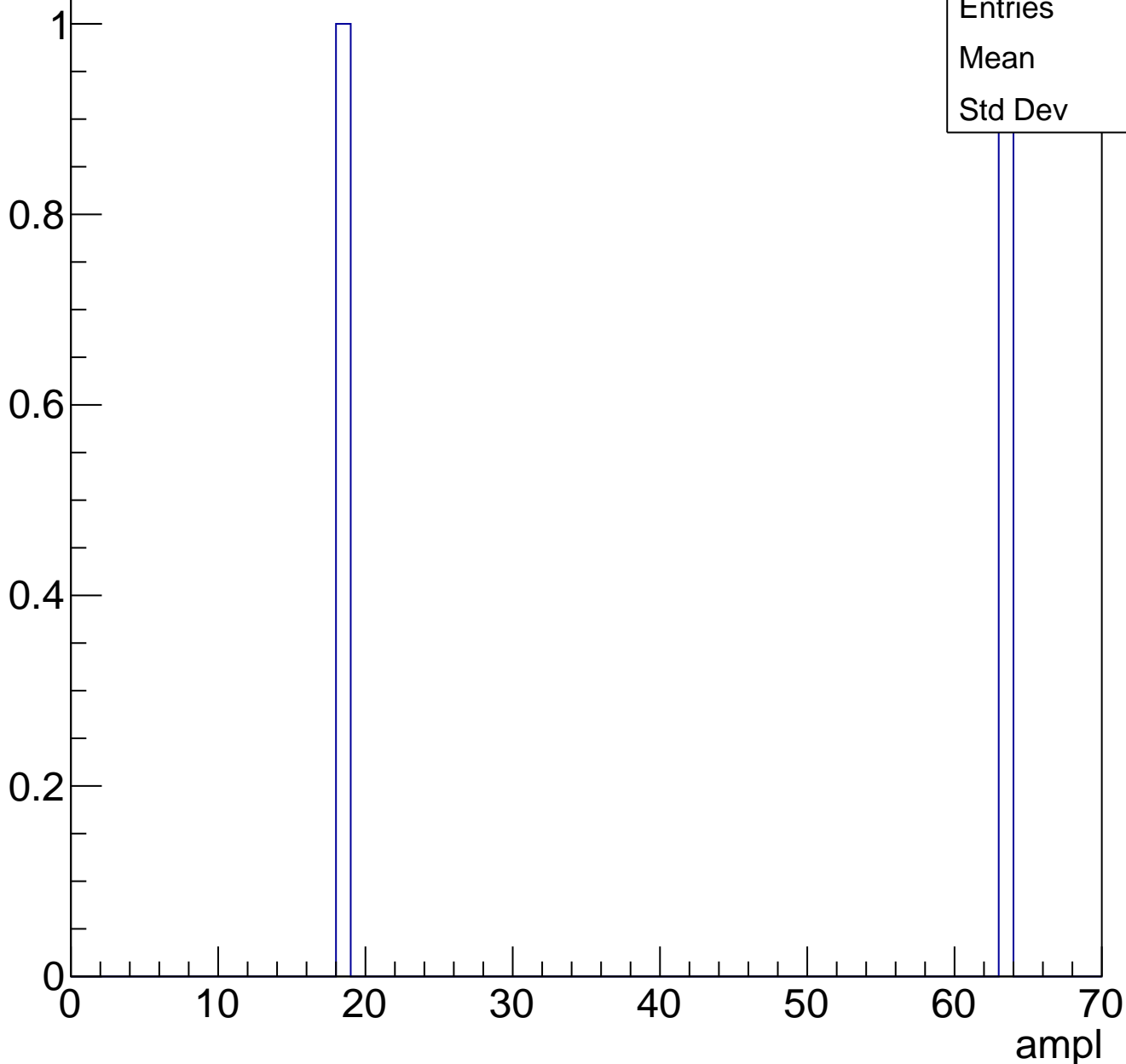
Entry



B1L102S, U17-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch50, adc0

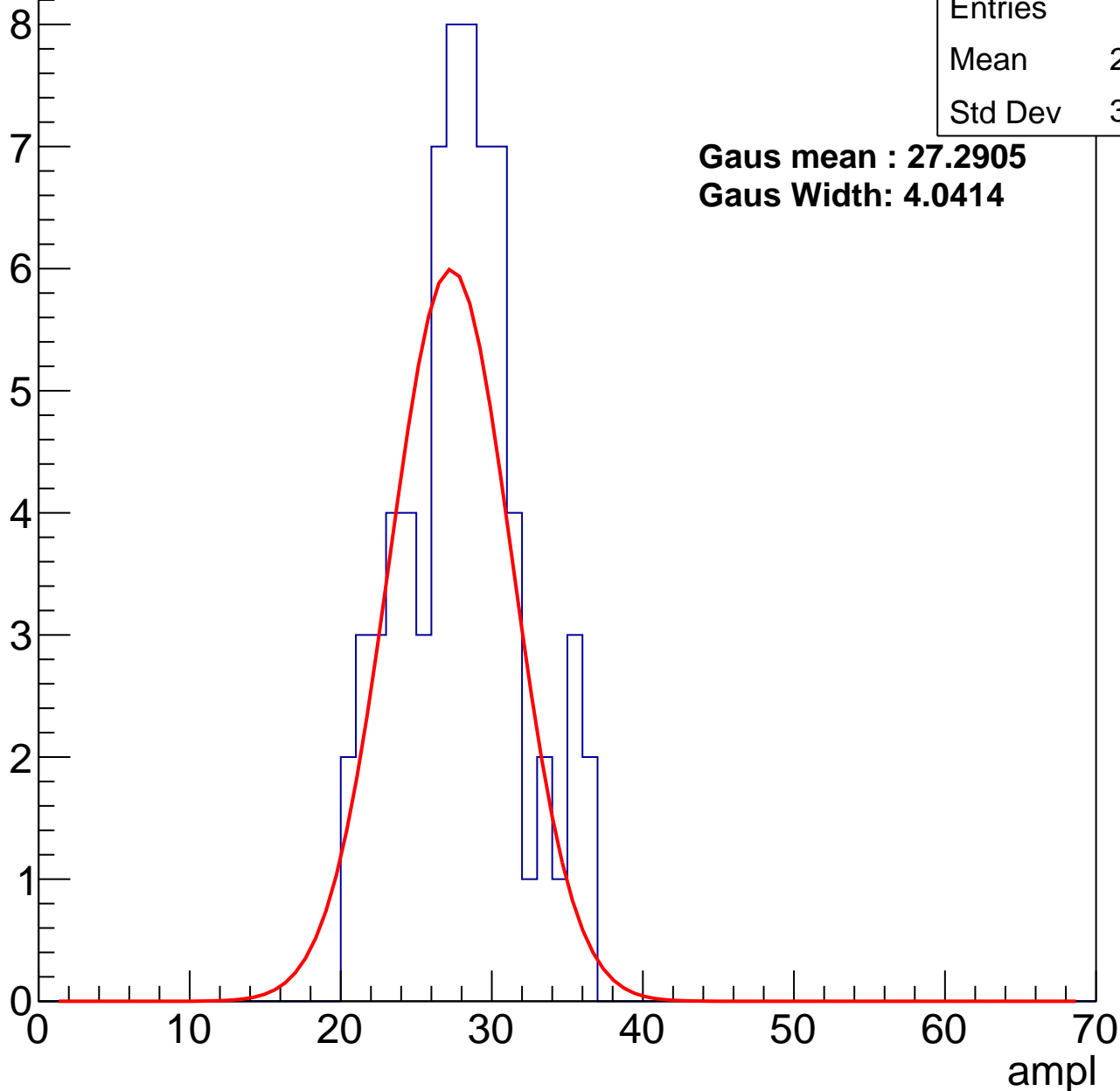
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.54
Std Dev	3.888

Gaus mean : 27.2905

Gaus Width: 4.0414



B1L102S, U17-ch50, adc1

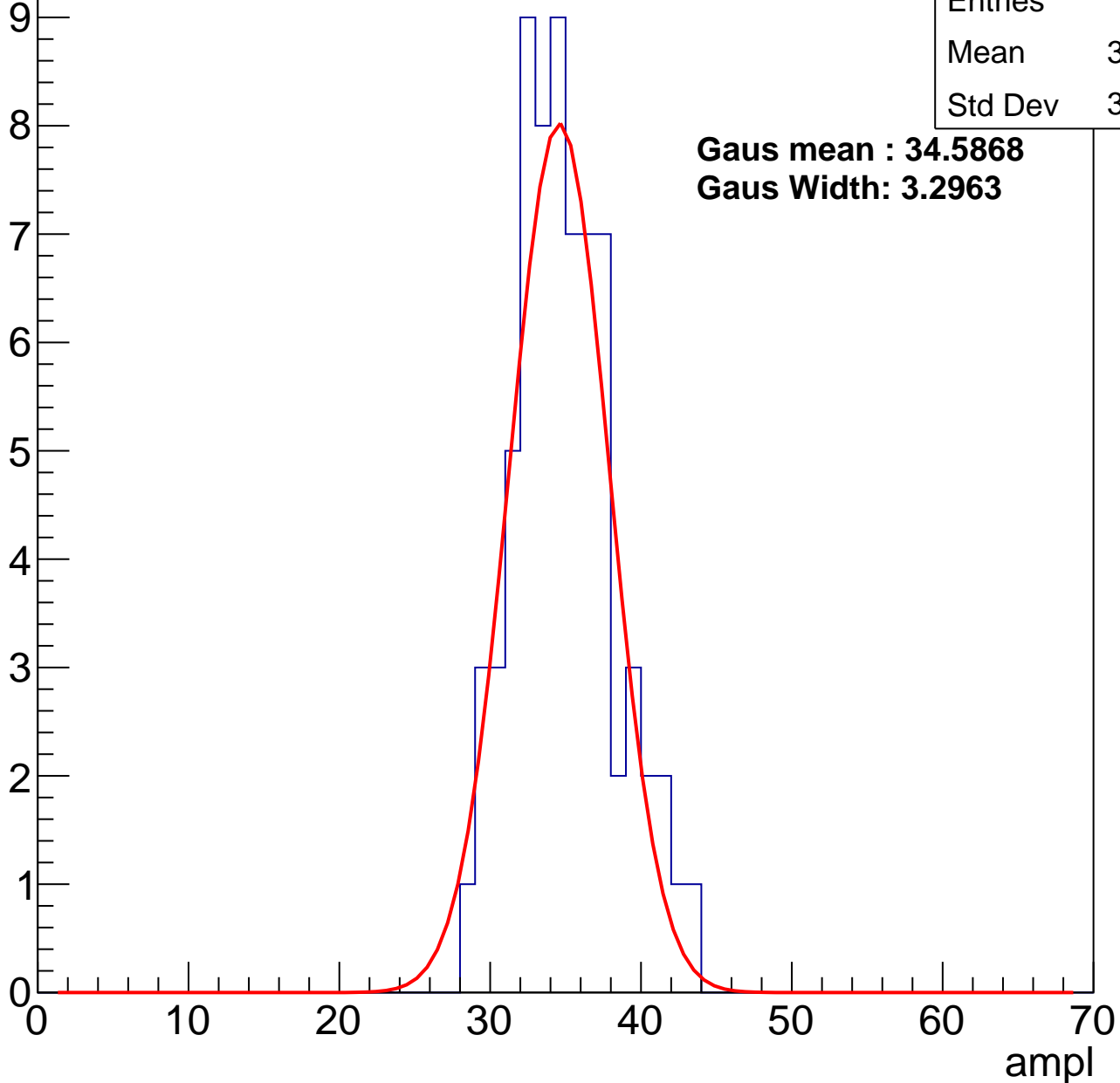
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.49
Std Dev	3.277

Gaus mean : 34.5868

Gaus Width: 3.2963



B1L102S, U17-ch50, adc2

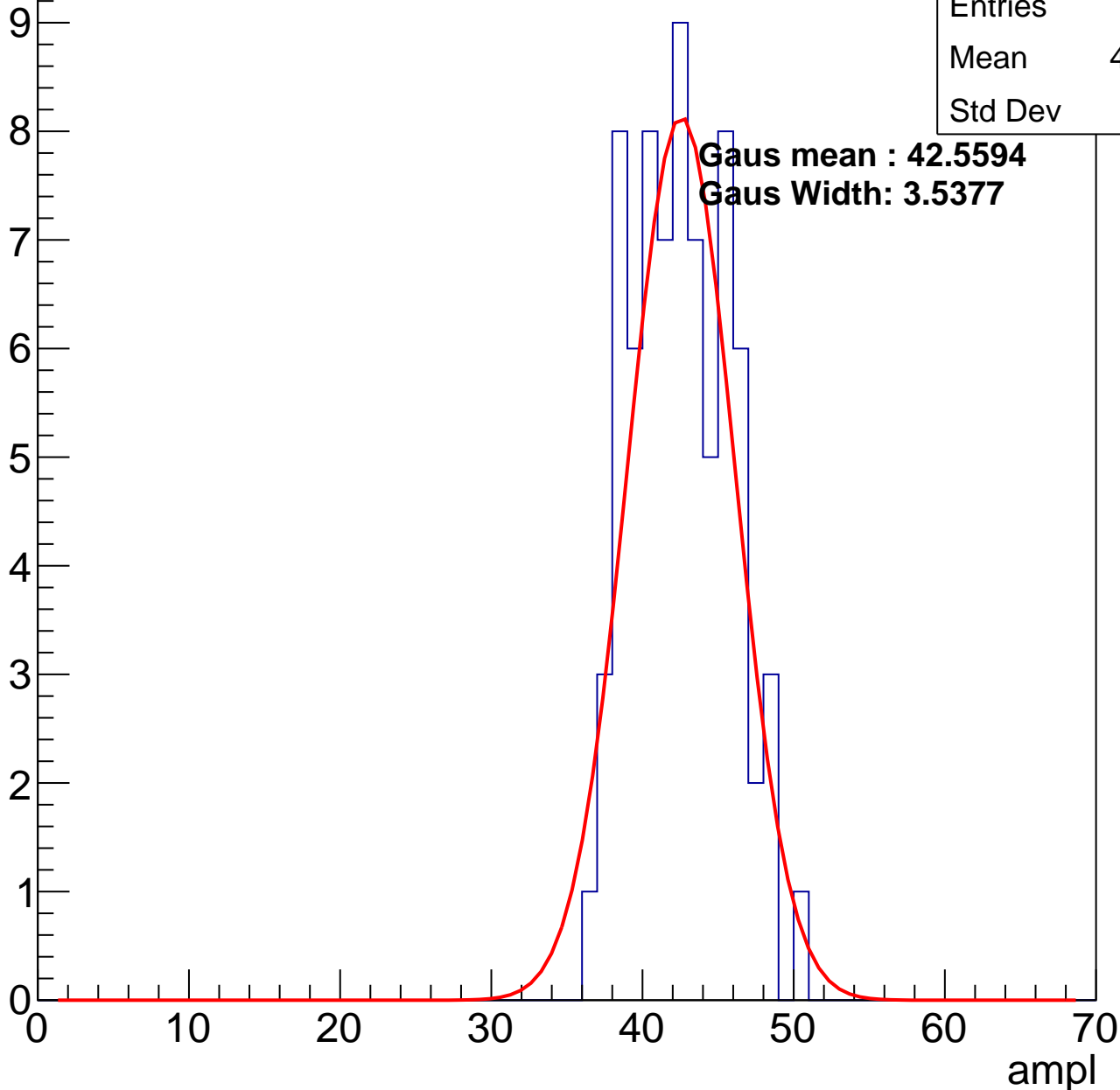
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.09
Std Dev	3.18

Gaus mean : 42.5594

Gaus Width: 3.5377

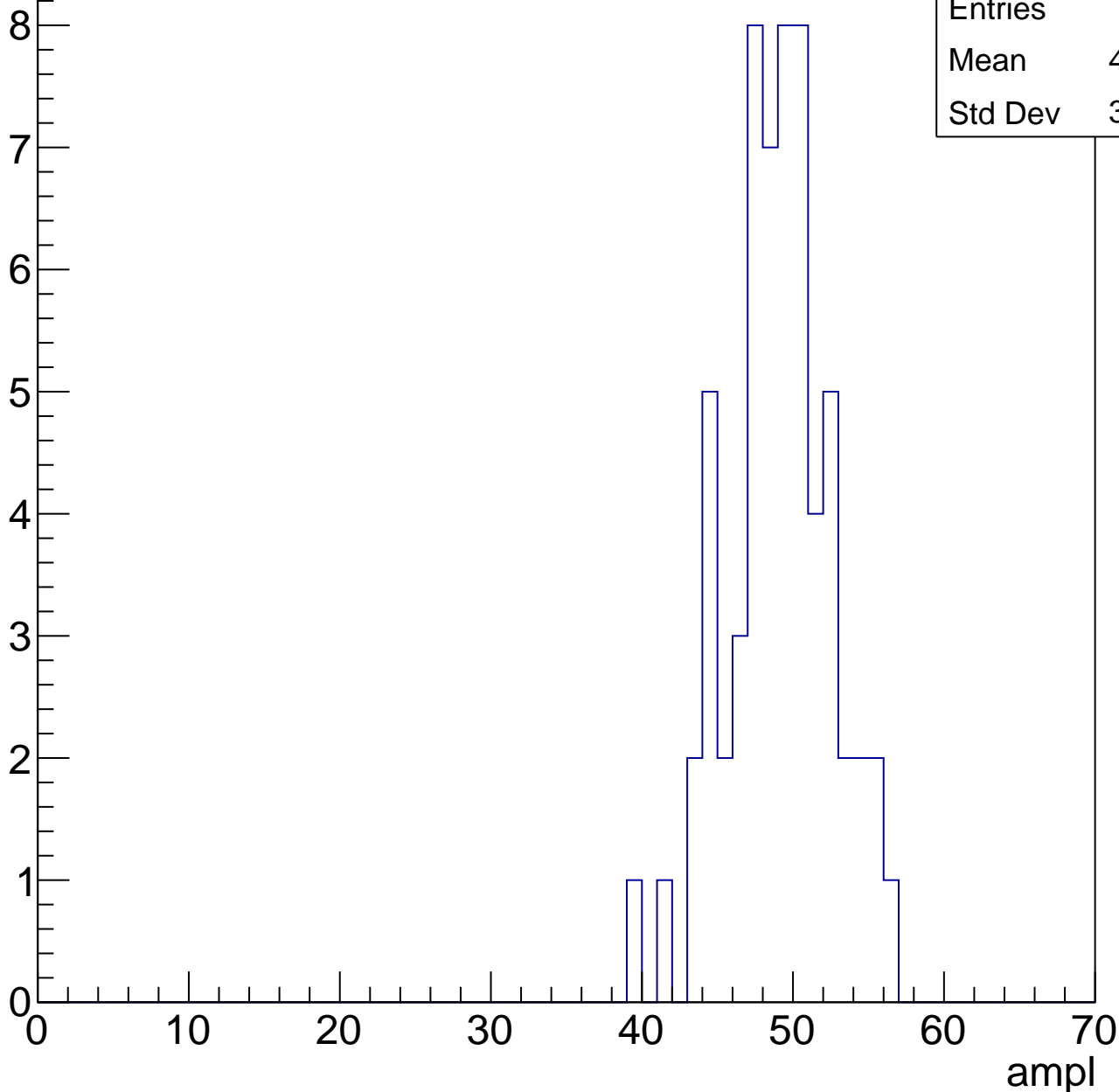


B1L102S, U17-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.56
Std Dev	3.433

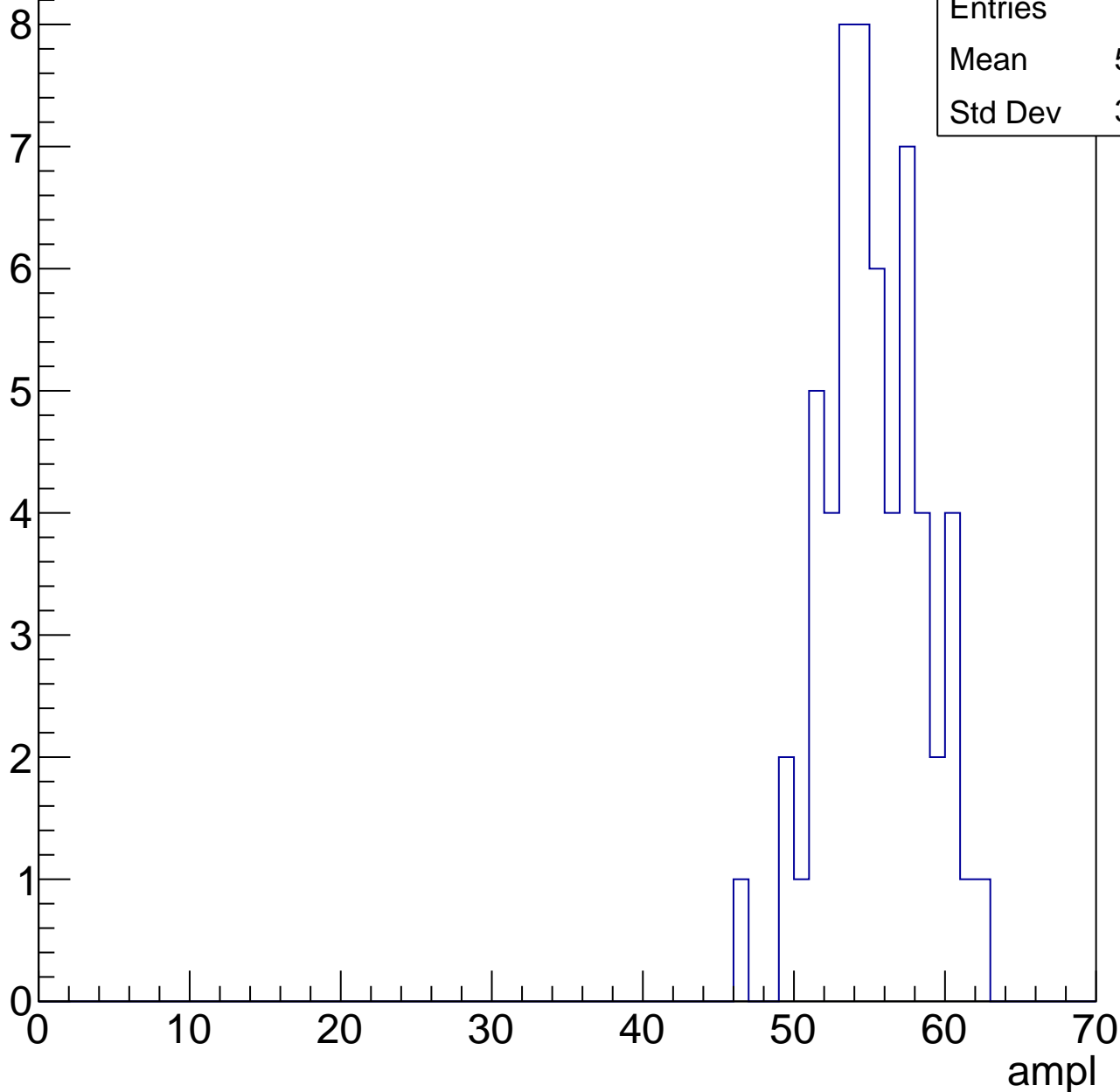


B1L102S, U17-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.81
Std Dev	3.261

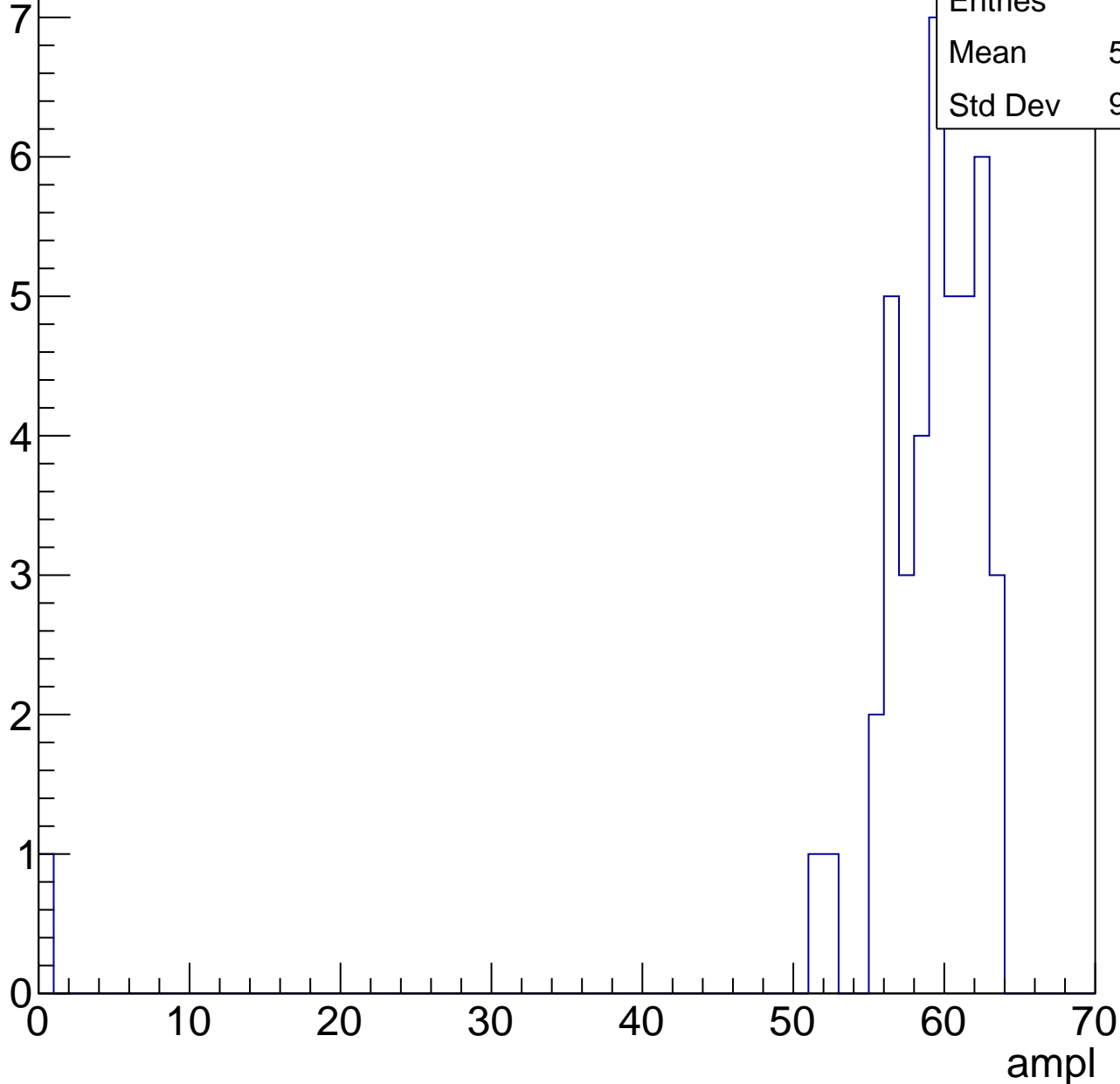


B1L102S, U17-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	57.56
Std Dev	9.307

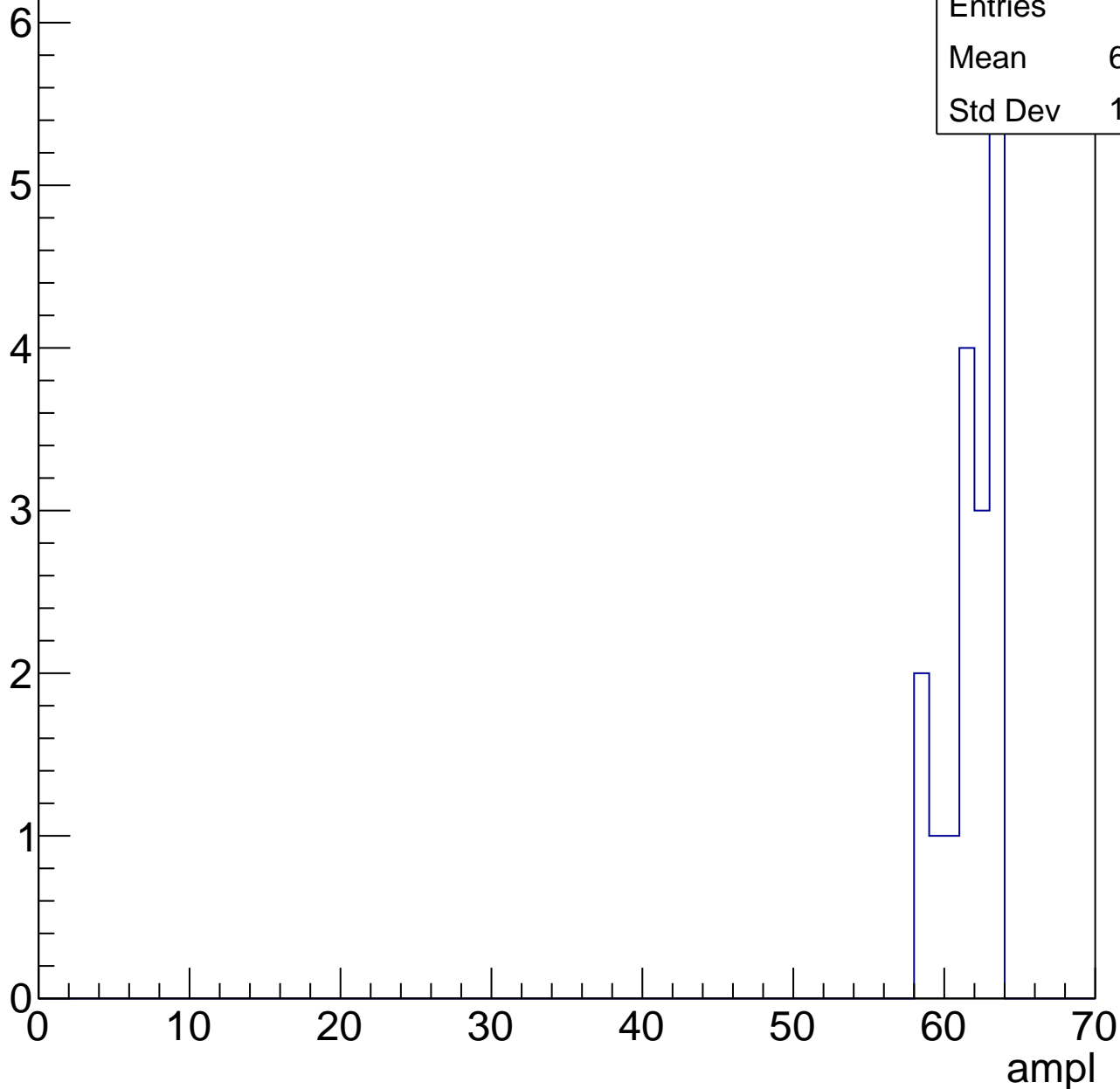


B1L102S, U17-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

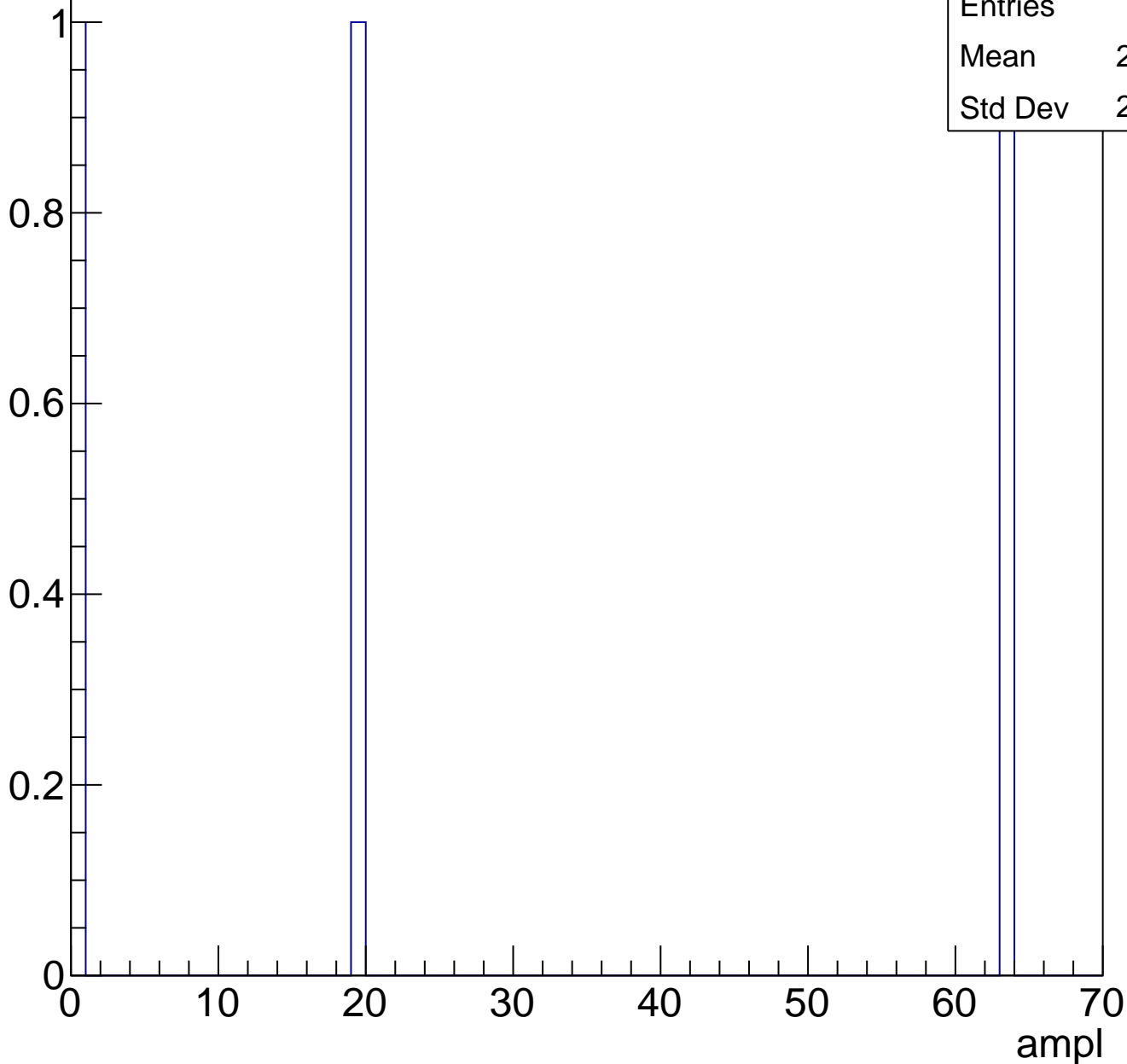
Entries	17
Mean	61.35
Std Dev	1.678



B1L102S, U17-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B1L102S, U17-ch51, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	28.24
Std Dev	6.504

Gaus mean : 29.6729

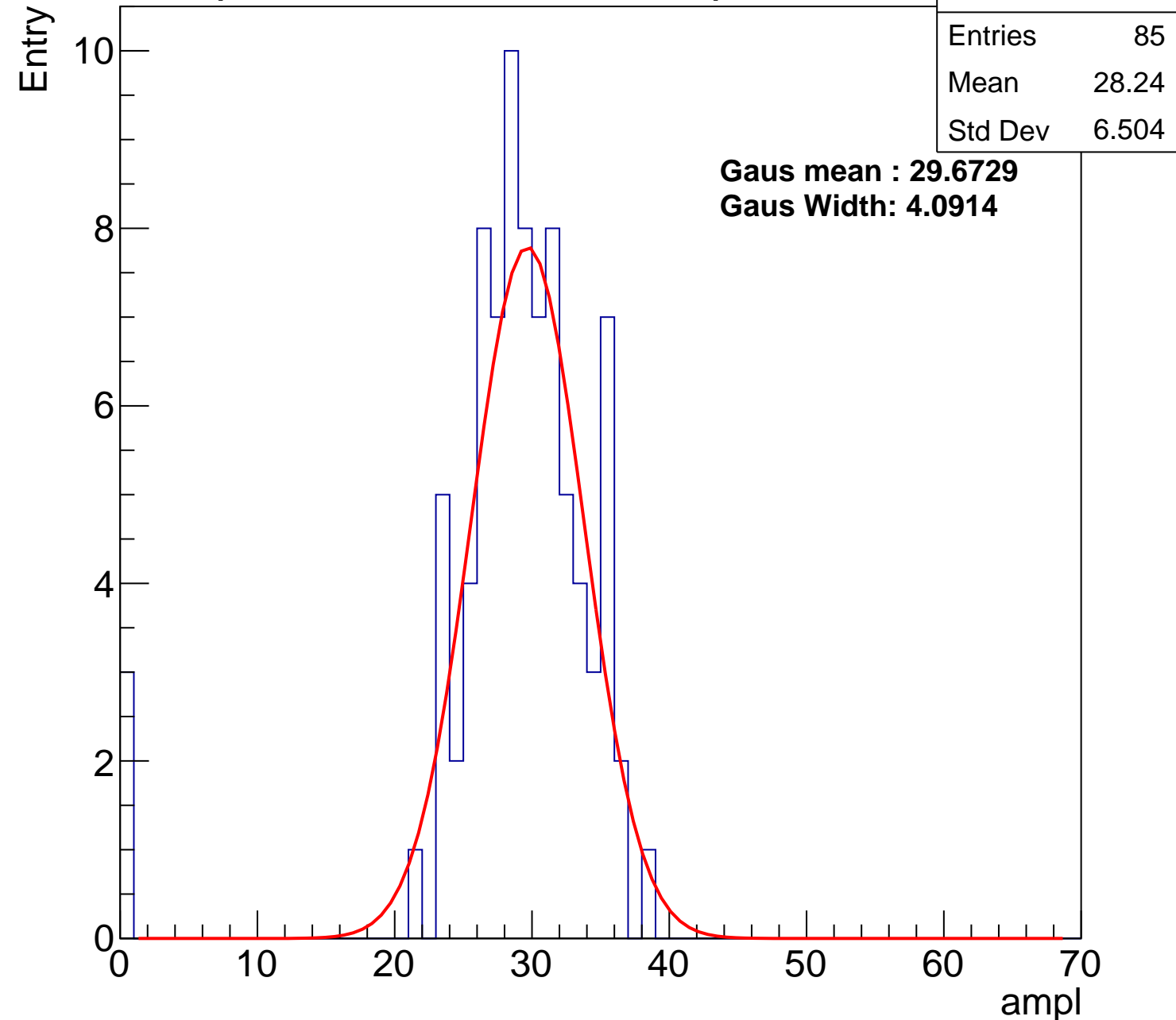
Gaus Width: 4.0914

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch51, adc1

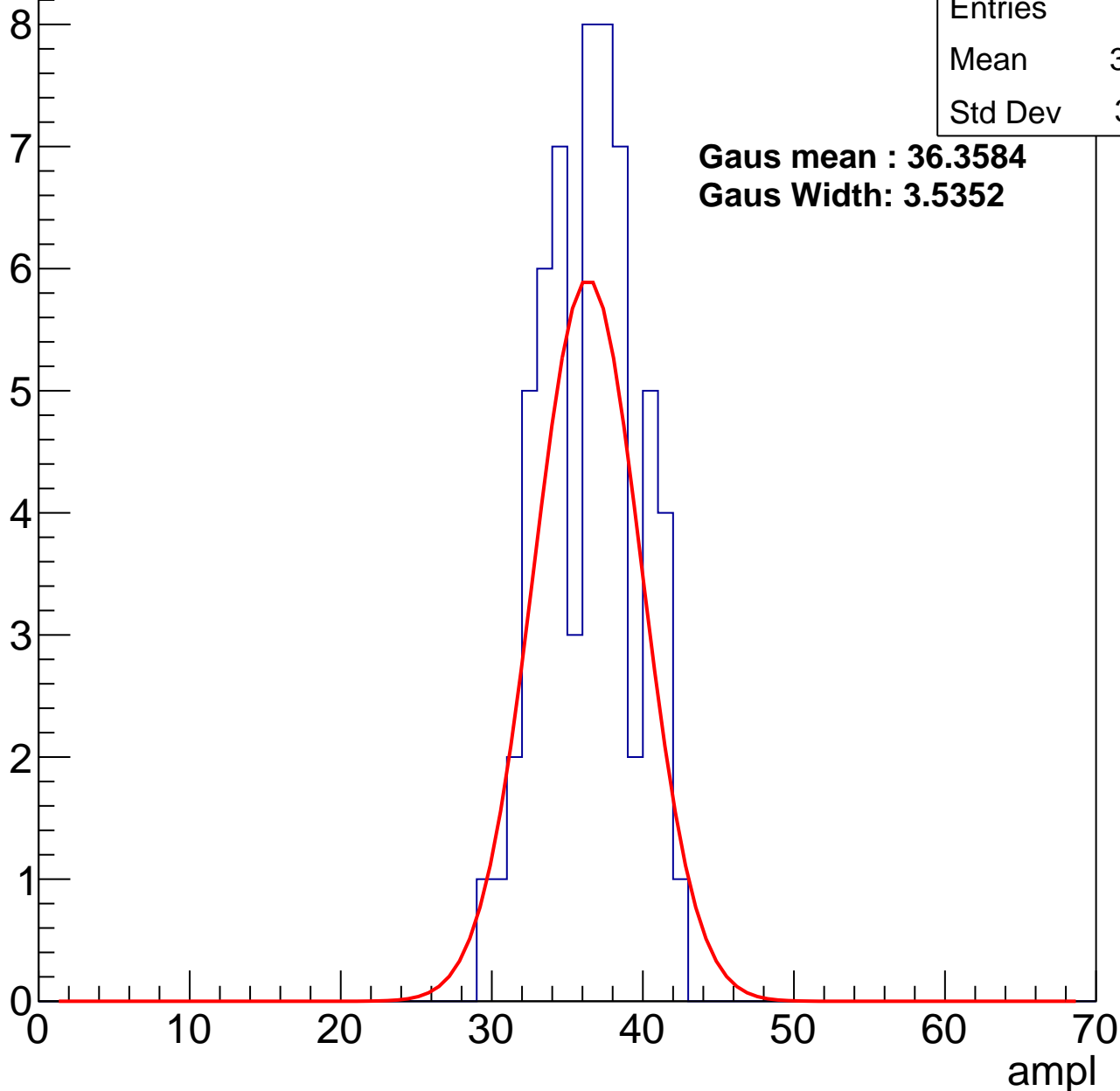
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.93
Std Dev	3.081

Gaus mean : 36.3584

Gaus Width: 3.5352



B1L102S, U17-ch51, adc2

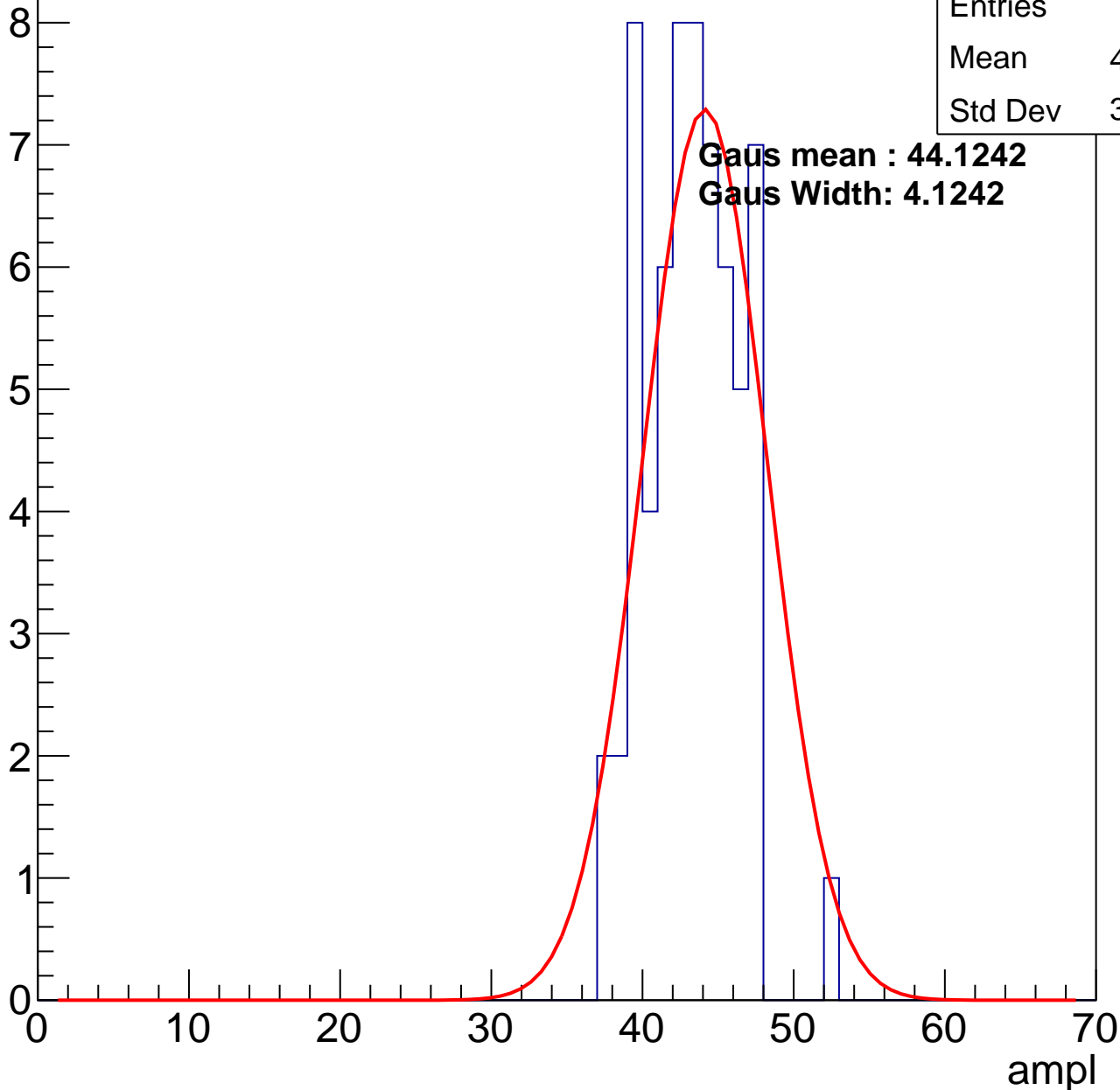
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.77
Std Dev	3.019

Gaus mean : 44.1242

Gaus Width: 4.1242



B1L102S, U17-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	61
Mean	48.74
Std Dev	3.594

Entry

10

8

6

4

2

0

0

10

20

30

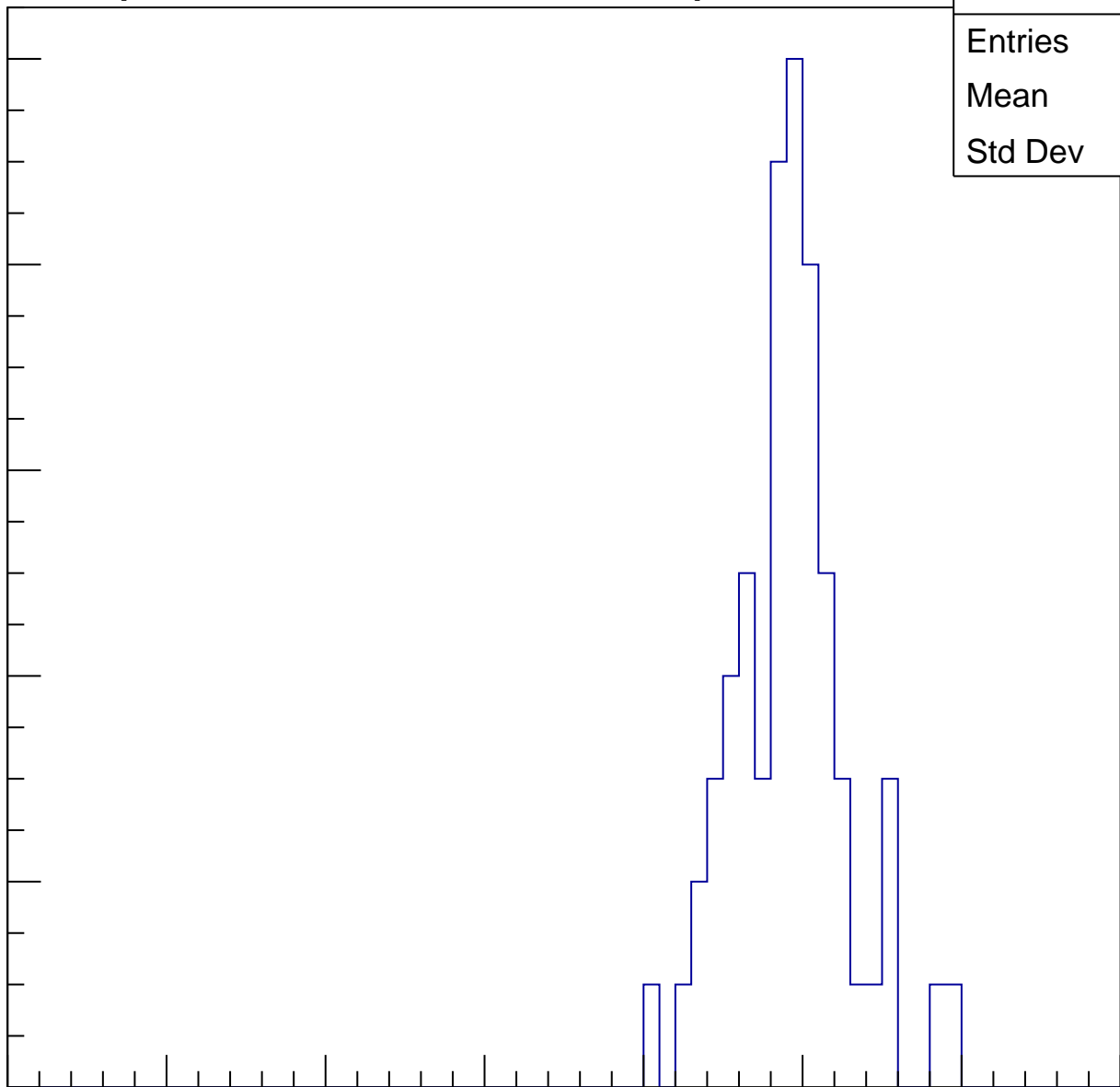
40

50

60

70

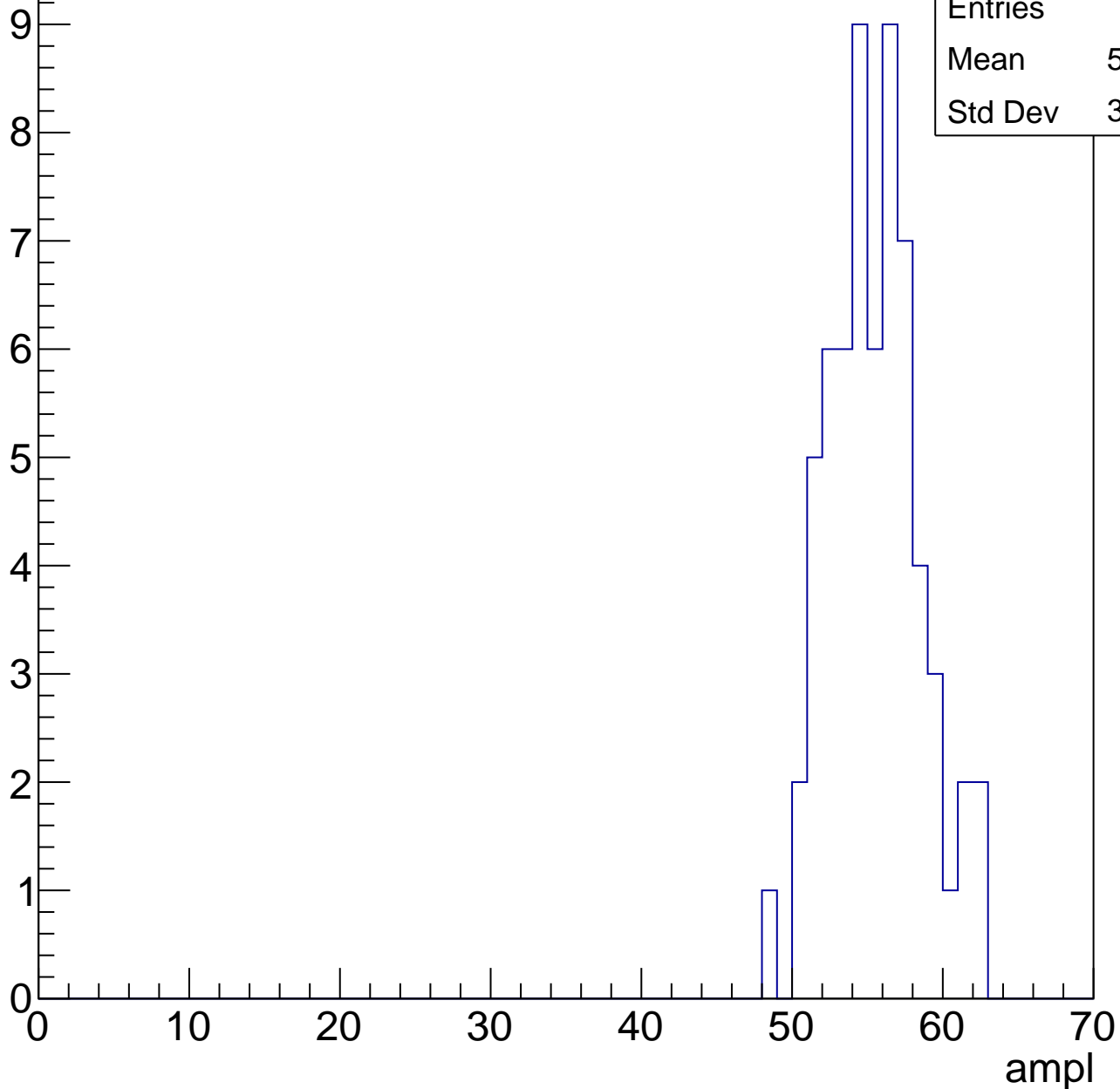
ampl



B1L102S, U17-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

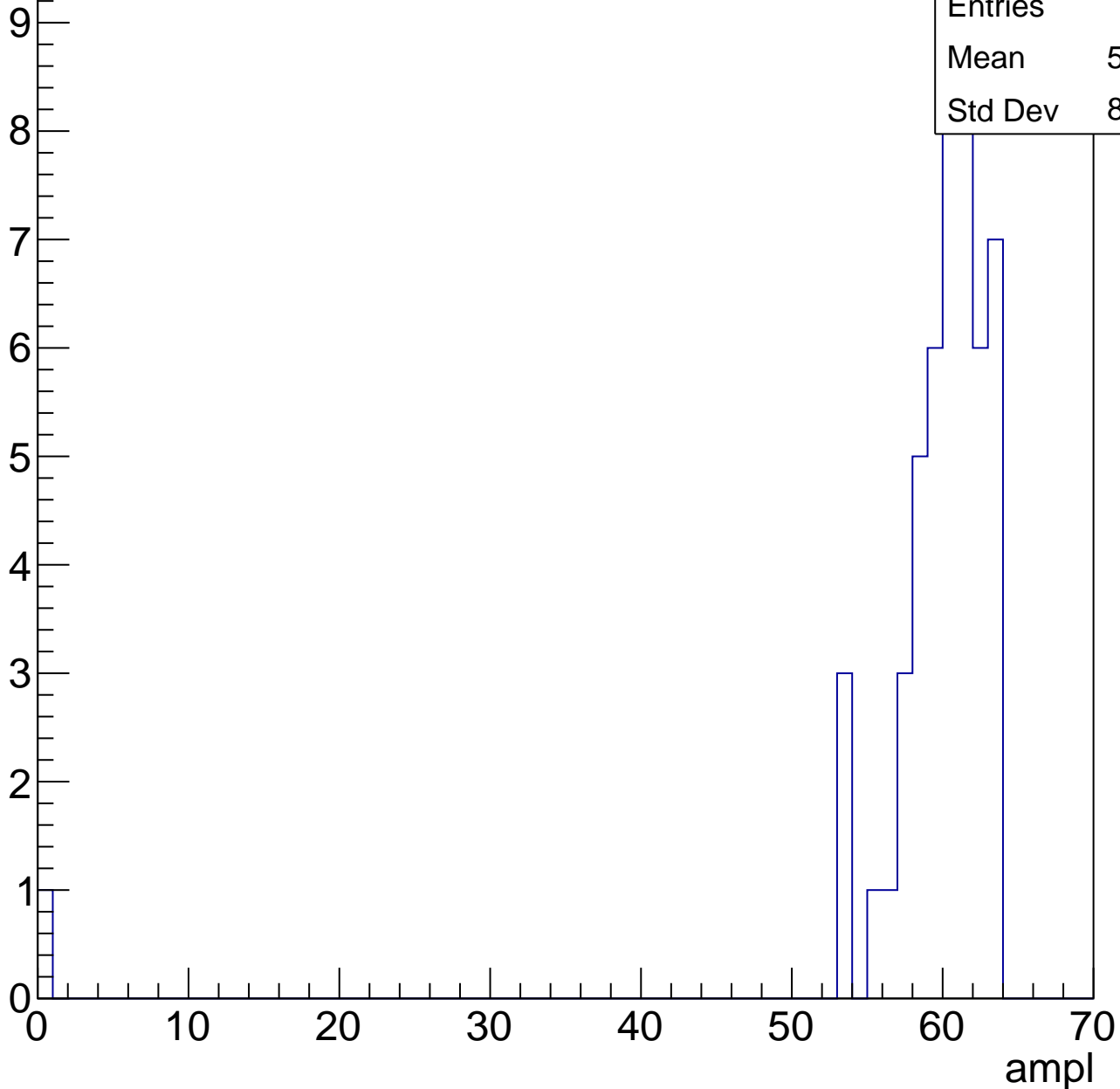


B1L102S, U17-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.52
Std Dev	8.748



B1L102S, U17-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

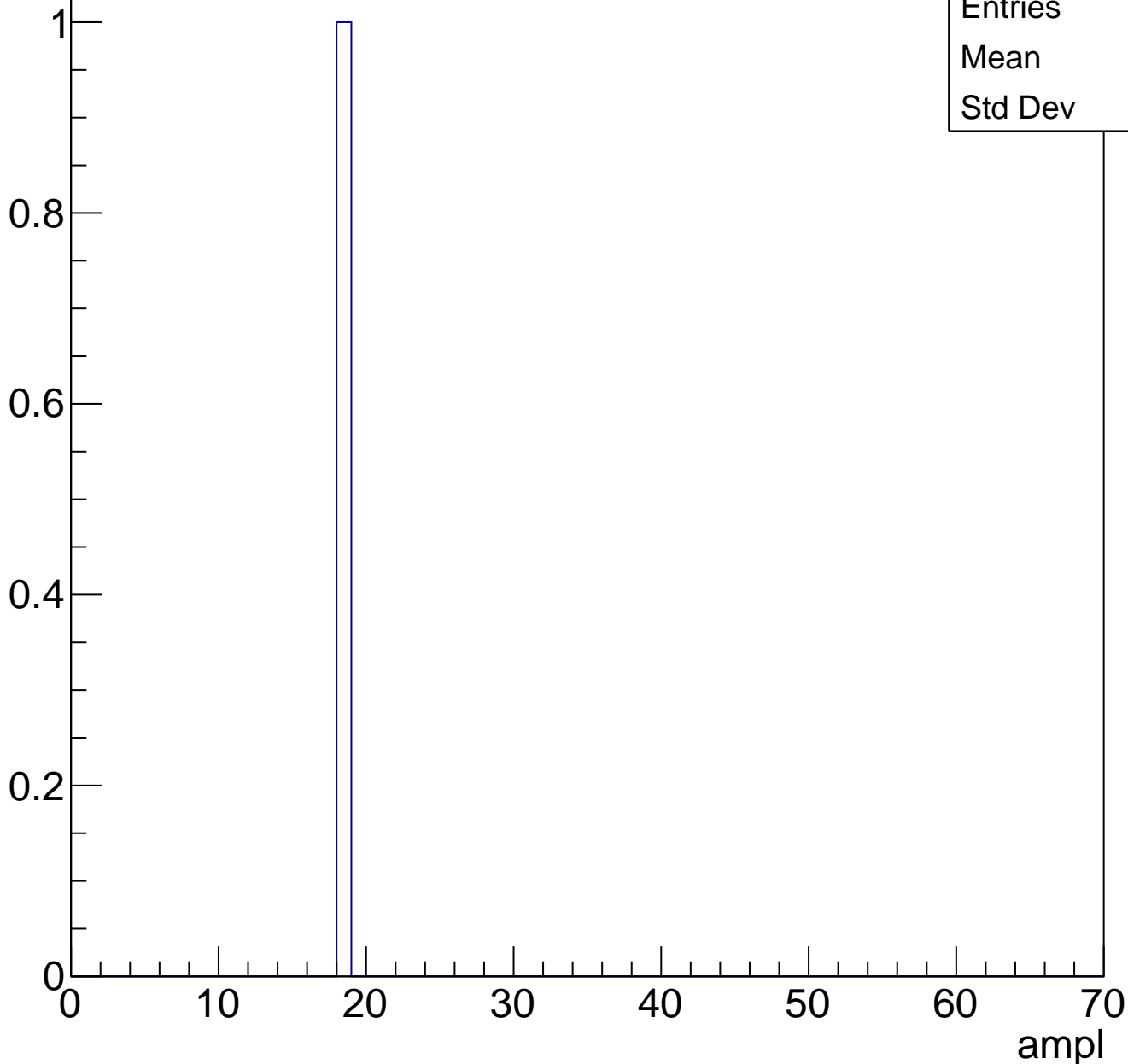
Entry



B1L102S, U17-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch52, adc0

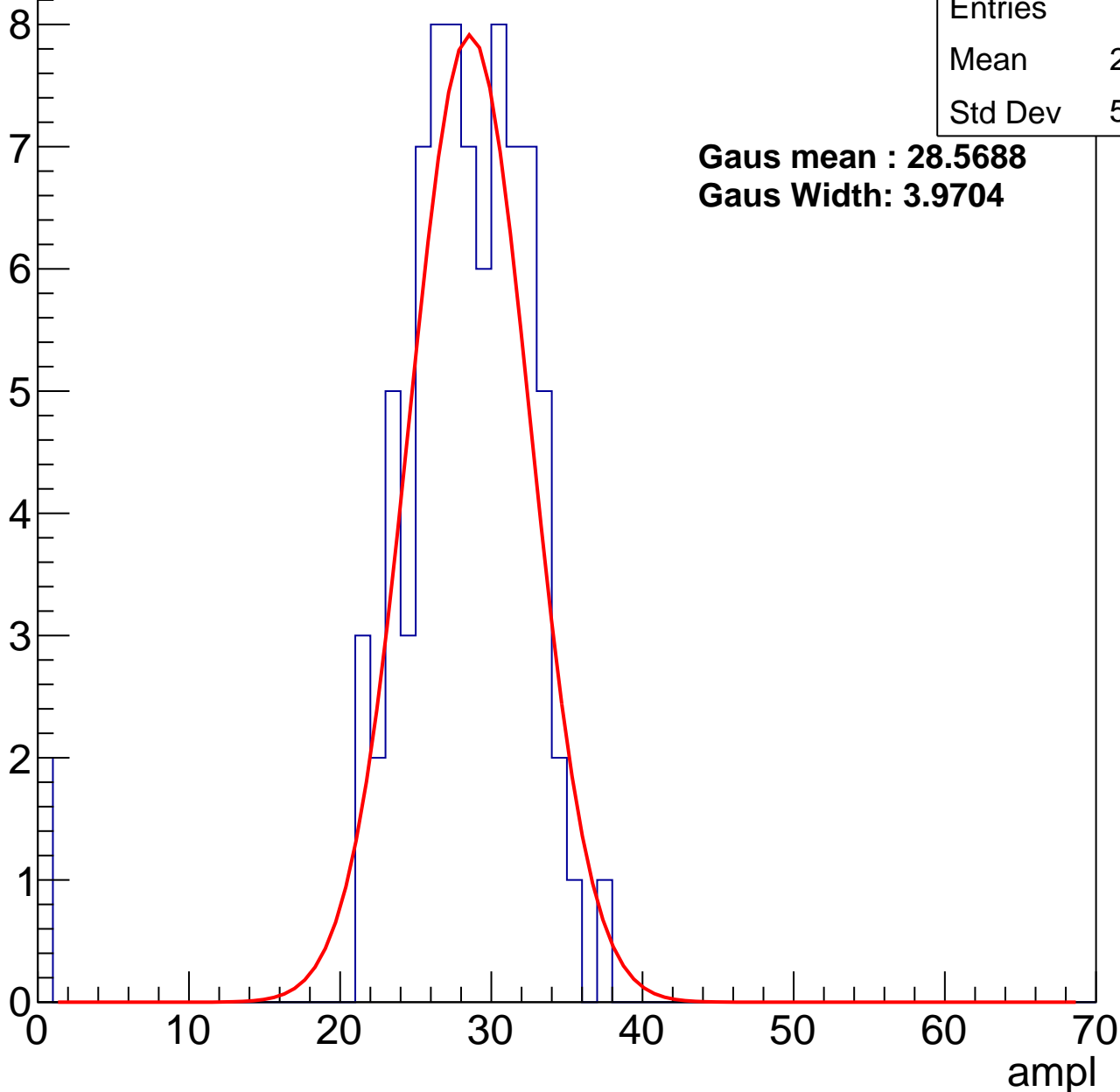
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	27.43
Std Dev	5.605

Gaus mean : 28.5688

Gaus Width: 3.9704



B1L102S, U17-ch52, adc1

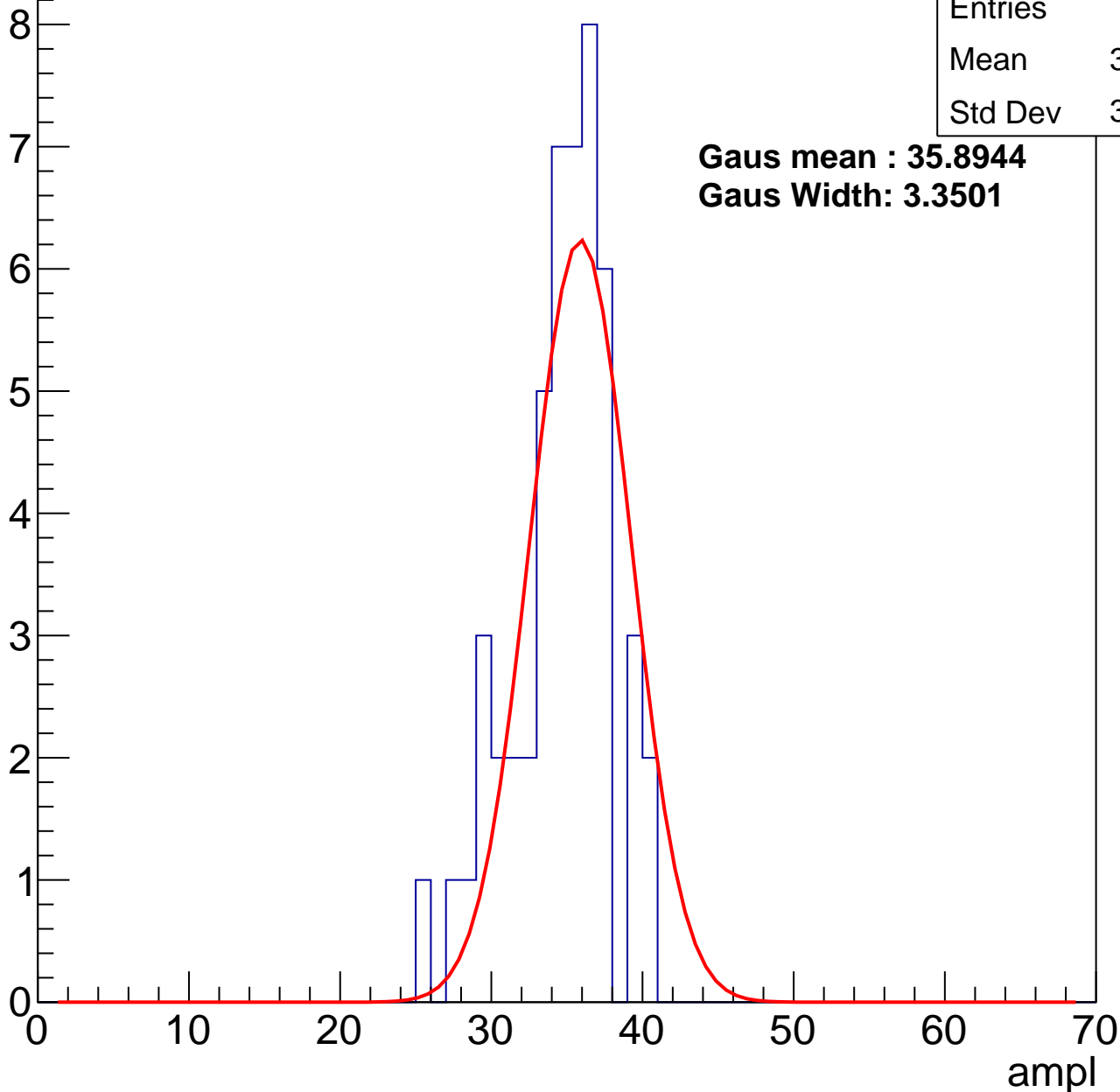
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	34.16
Std Dev	3.295

Gaus mean : 35.8944

Gaus Width: 3.3501



B1L102S, U17-ch52, adc2

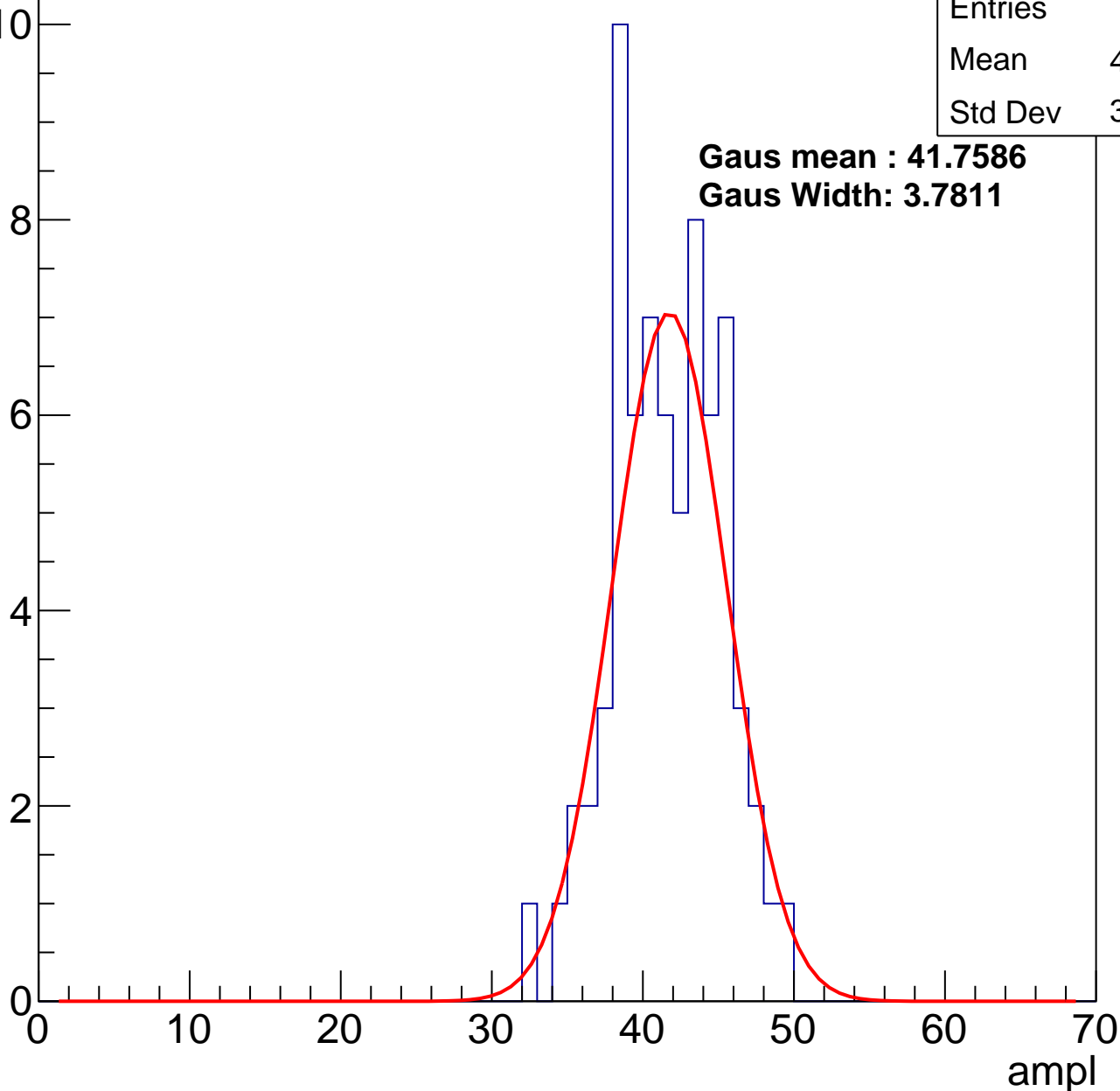
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.14
Std Dev	3.526

Gaus mean : 41.7586

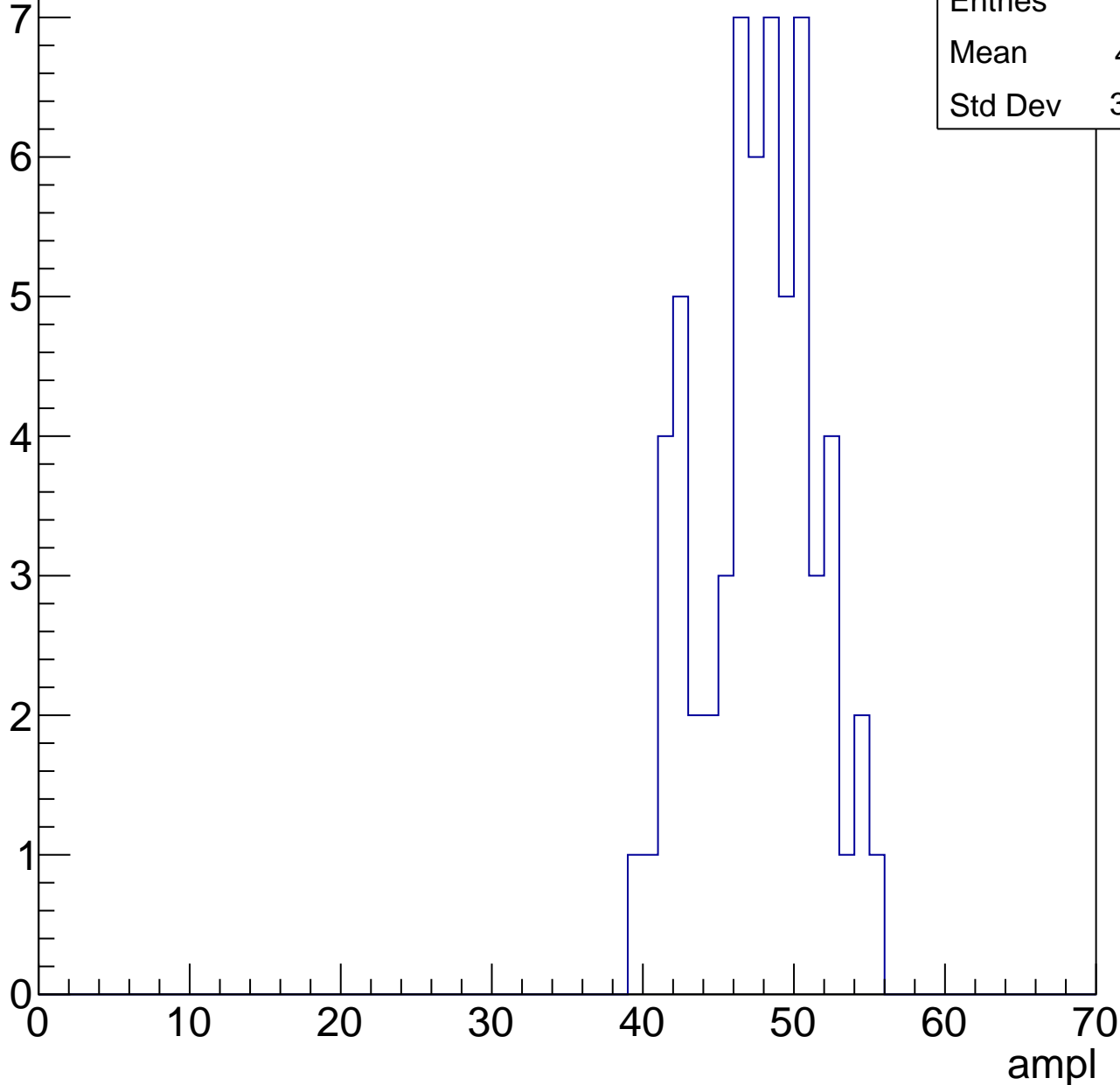
Gaus Width: 3.7811



B1L102S, U17-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



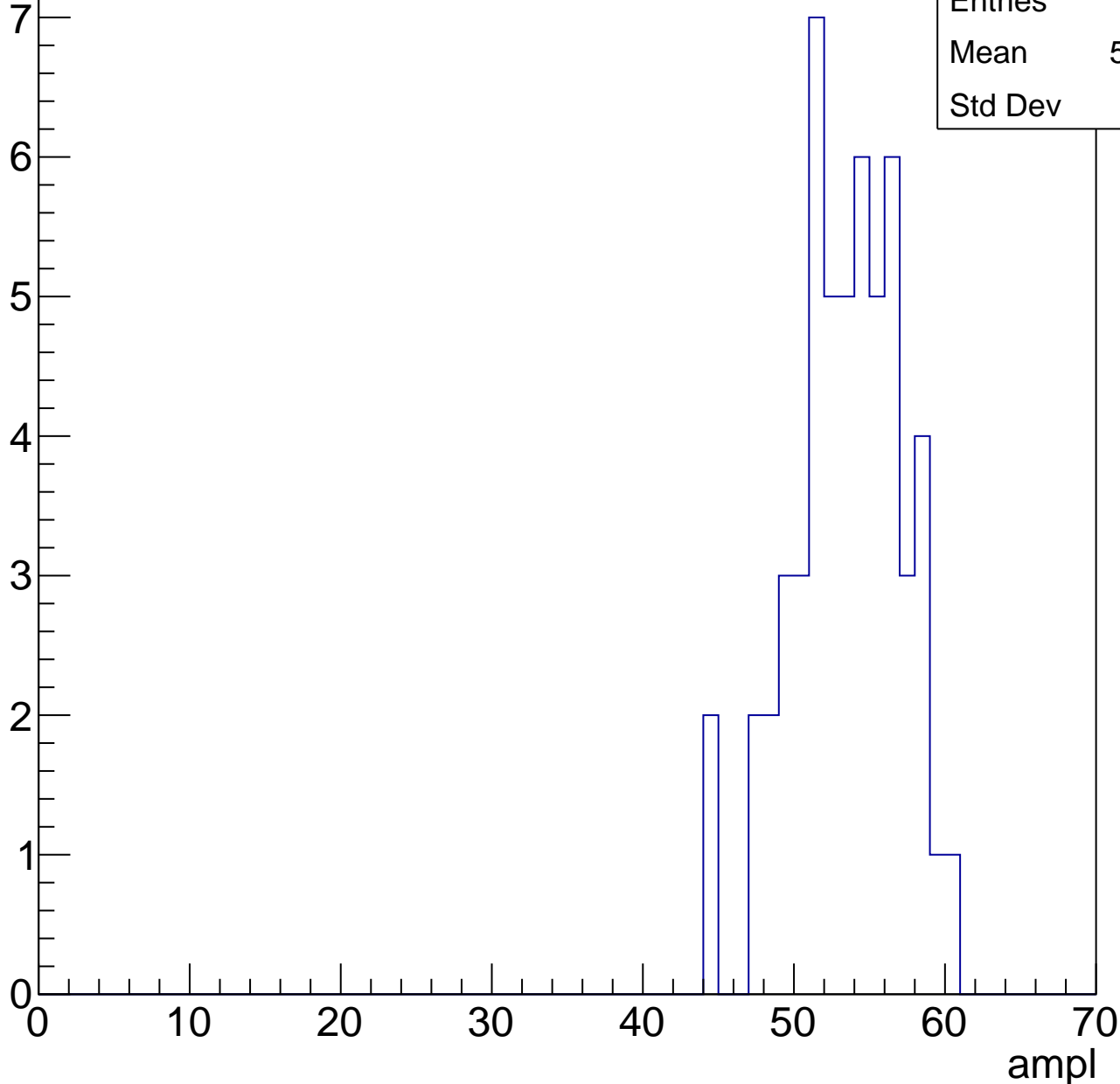
Entries	61
Mean	47.11
Std Dev	3.812

B1L102S, U17-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	52.98
Std Dev	3.58

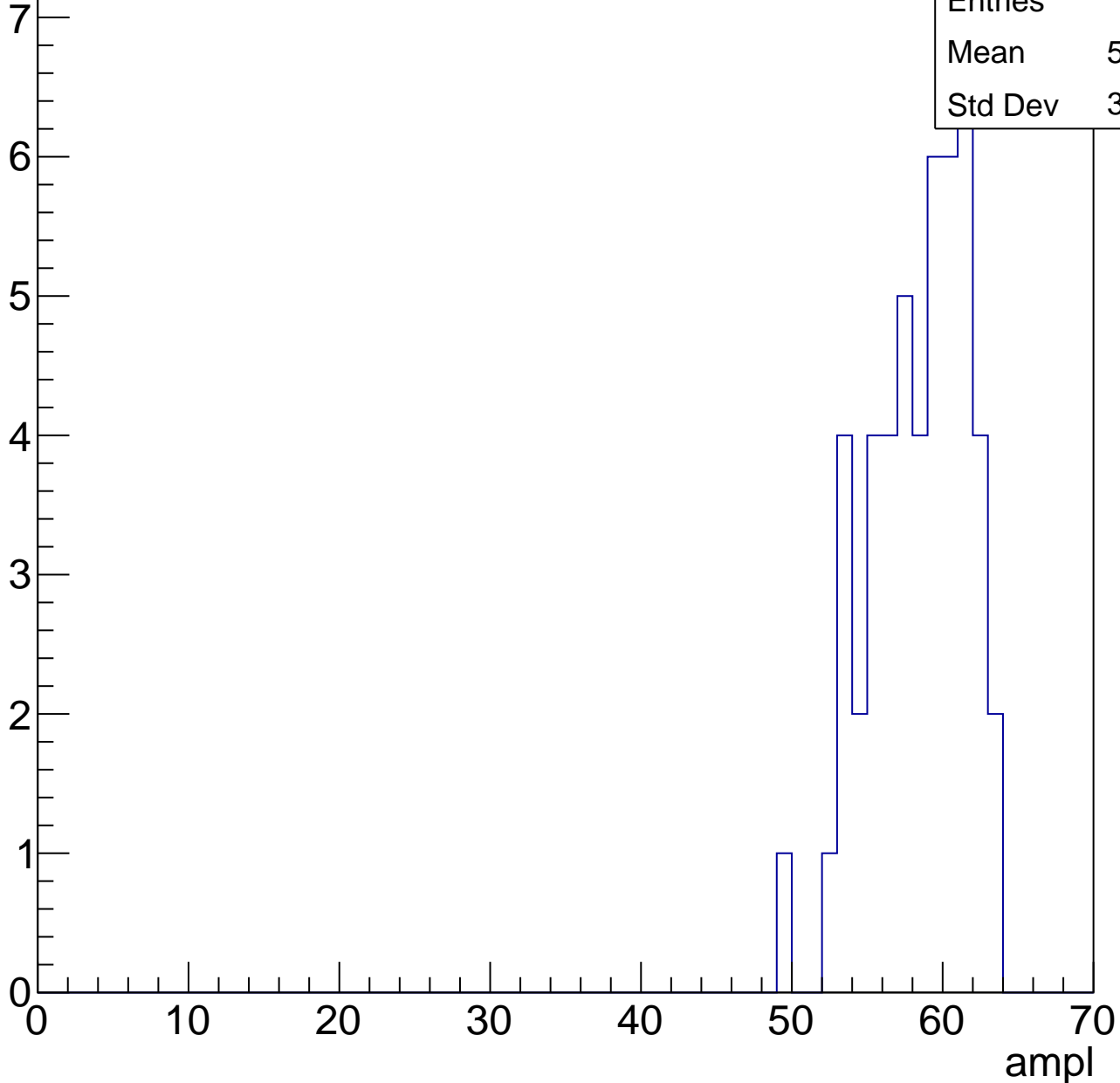


B1L102S, U17-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.94
Std Dev	3.203

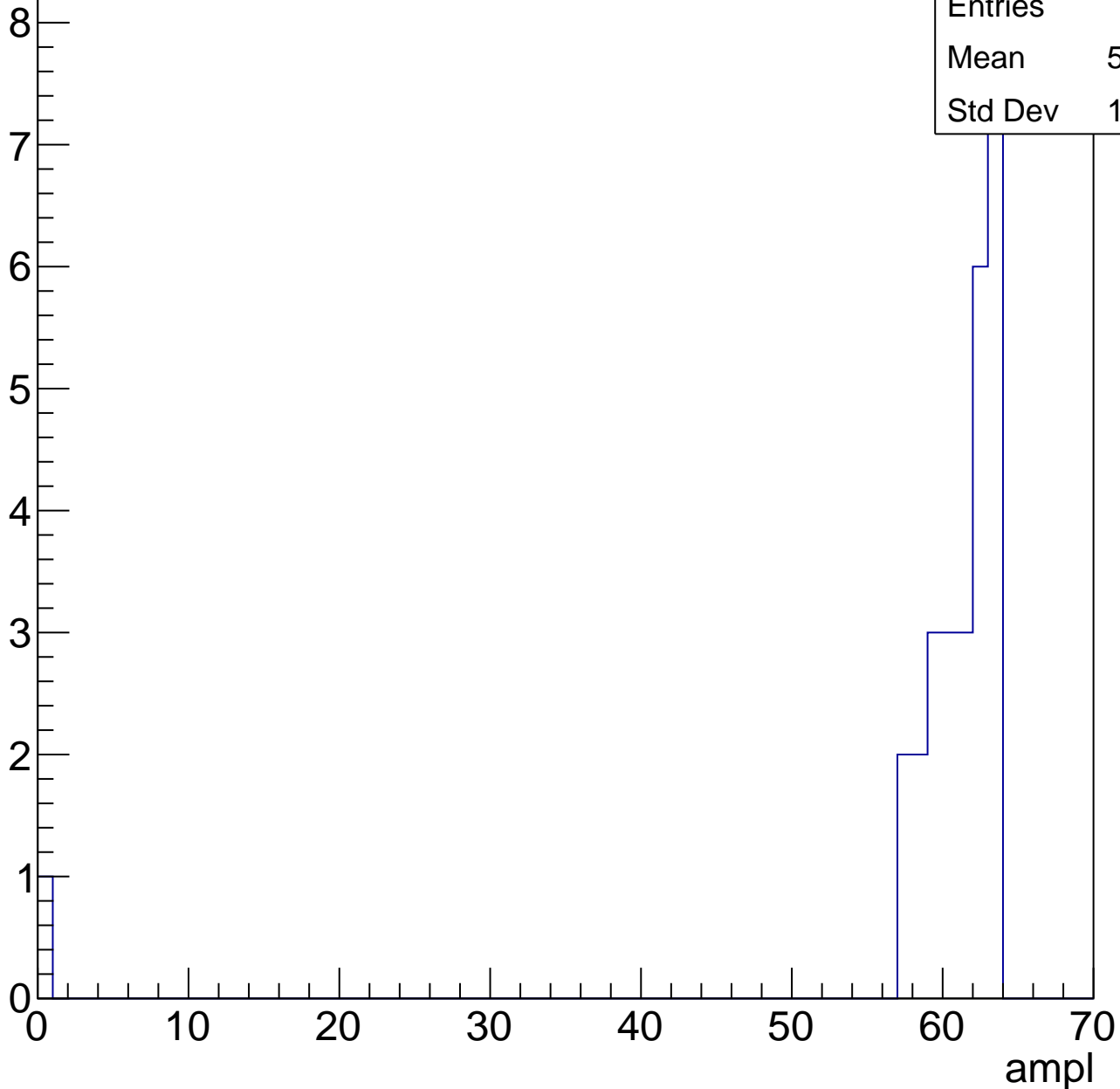


B1L102S, U17-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	58.79
Std Dev	11.47



B1L102S, U17-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch53, adc0

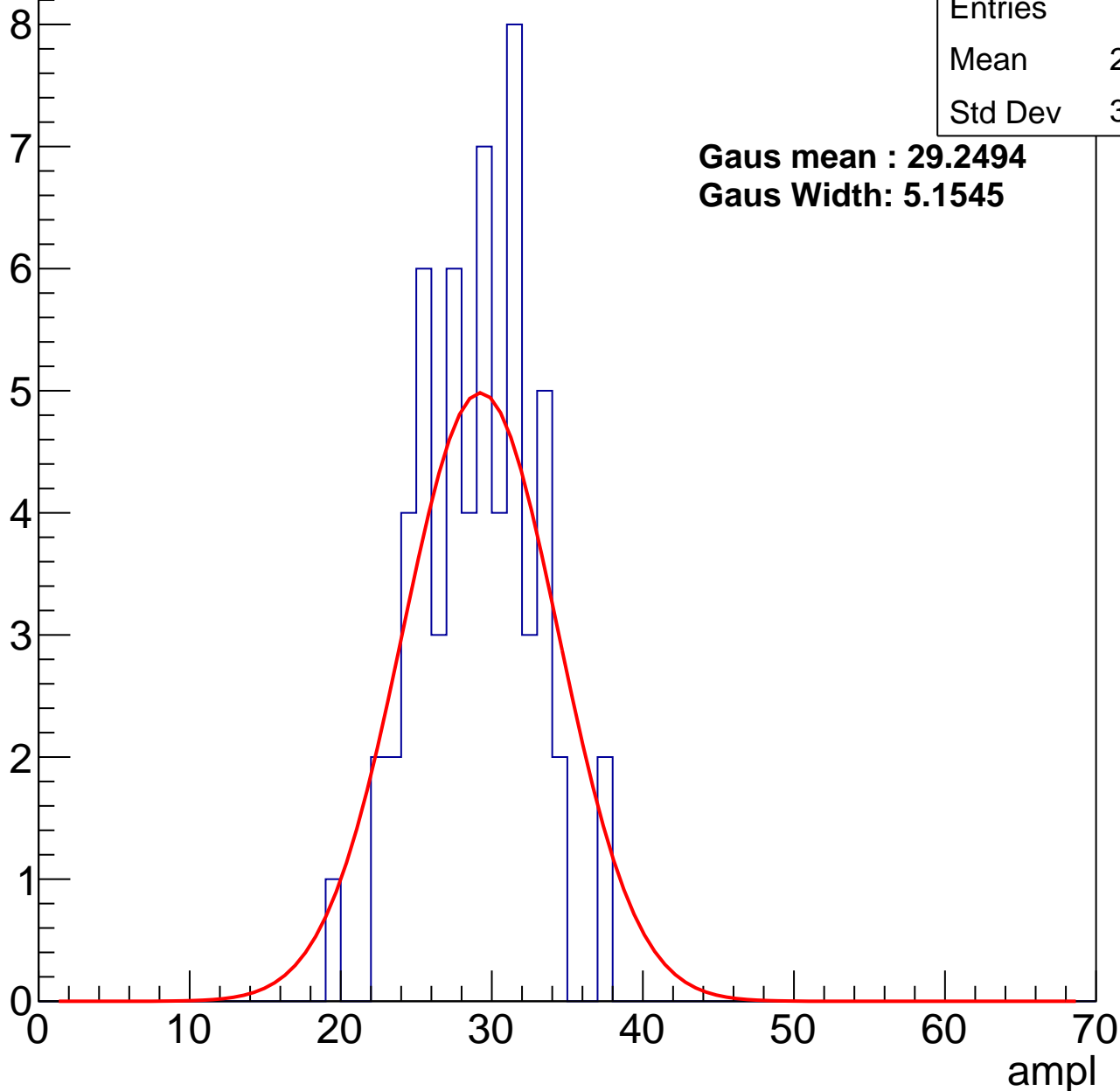
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.49
Std Dev	3.743

Gaus mean : 29.2494

Gaus Width: 5.1545



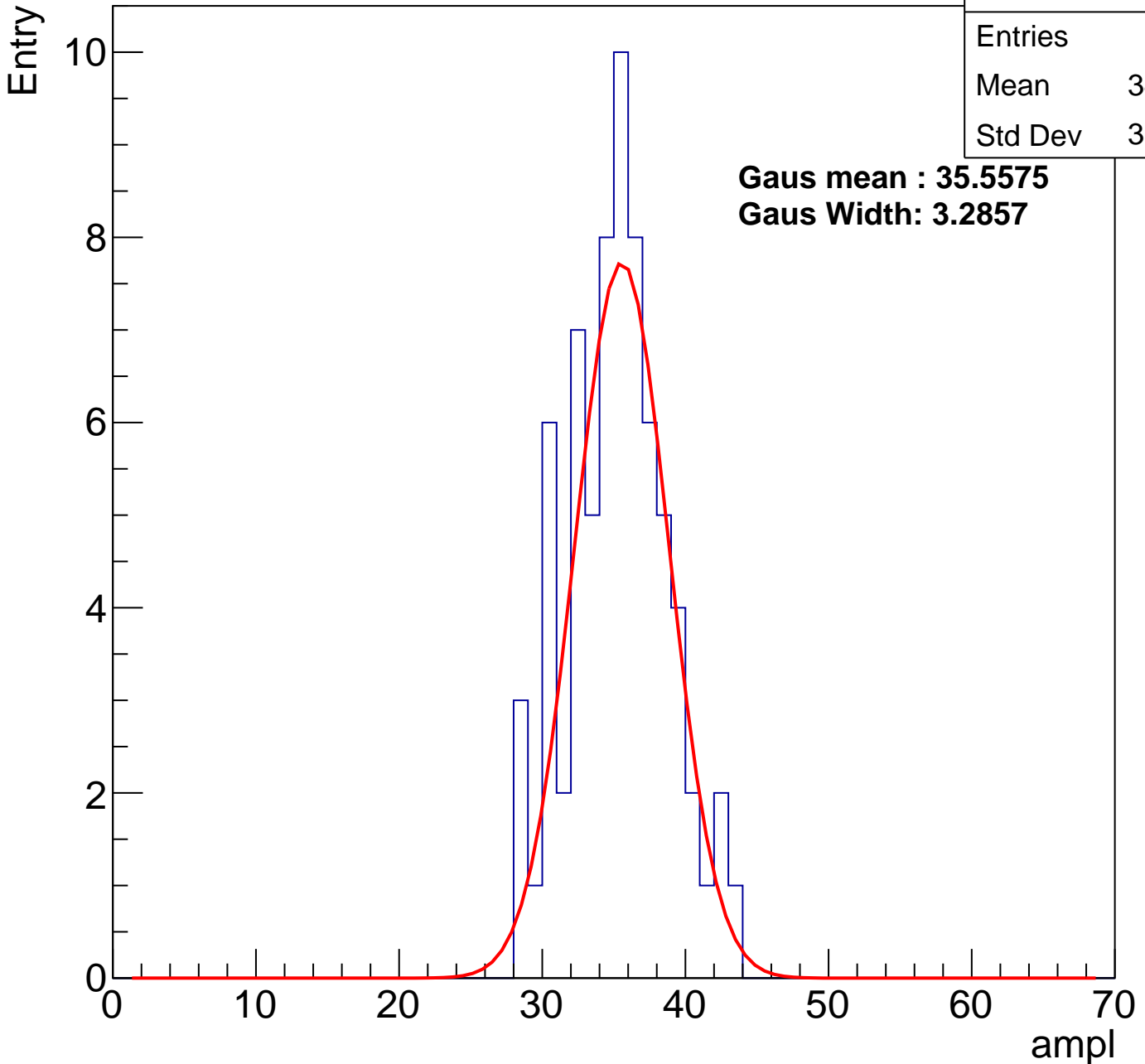
B1L102S, U17-ch53, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	34.79
Std Dev	3.443

Gaus mean : 35.5575

Gaus Width: 3.2857



B1L102S, U17-ch53, adc2

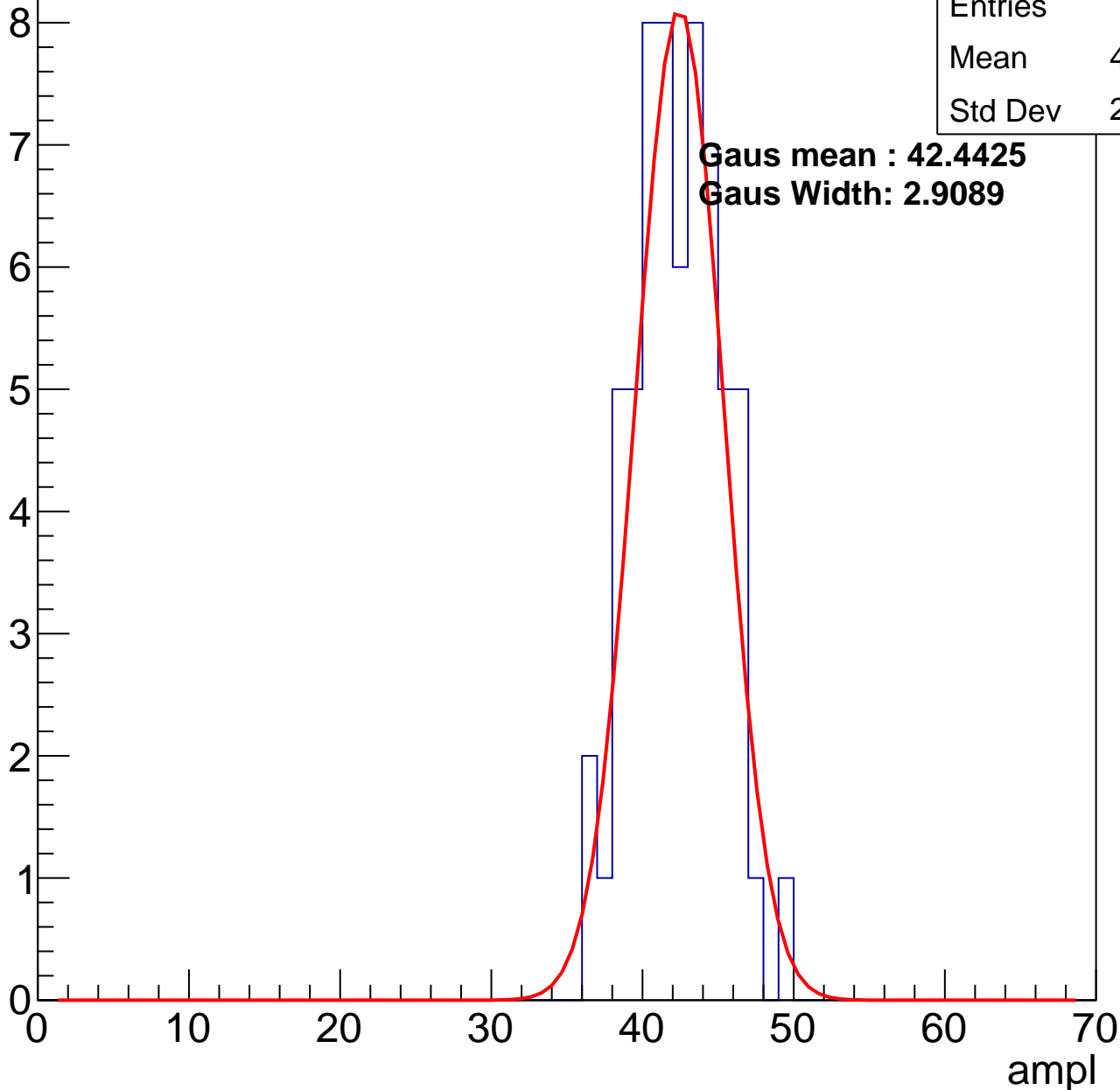
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.89
Std Dev	2.829

Gaus mean : 42.4425

Gaus Width: 2.9089

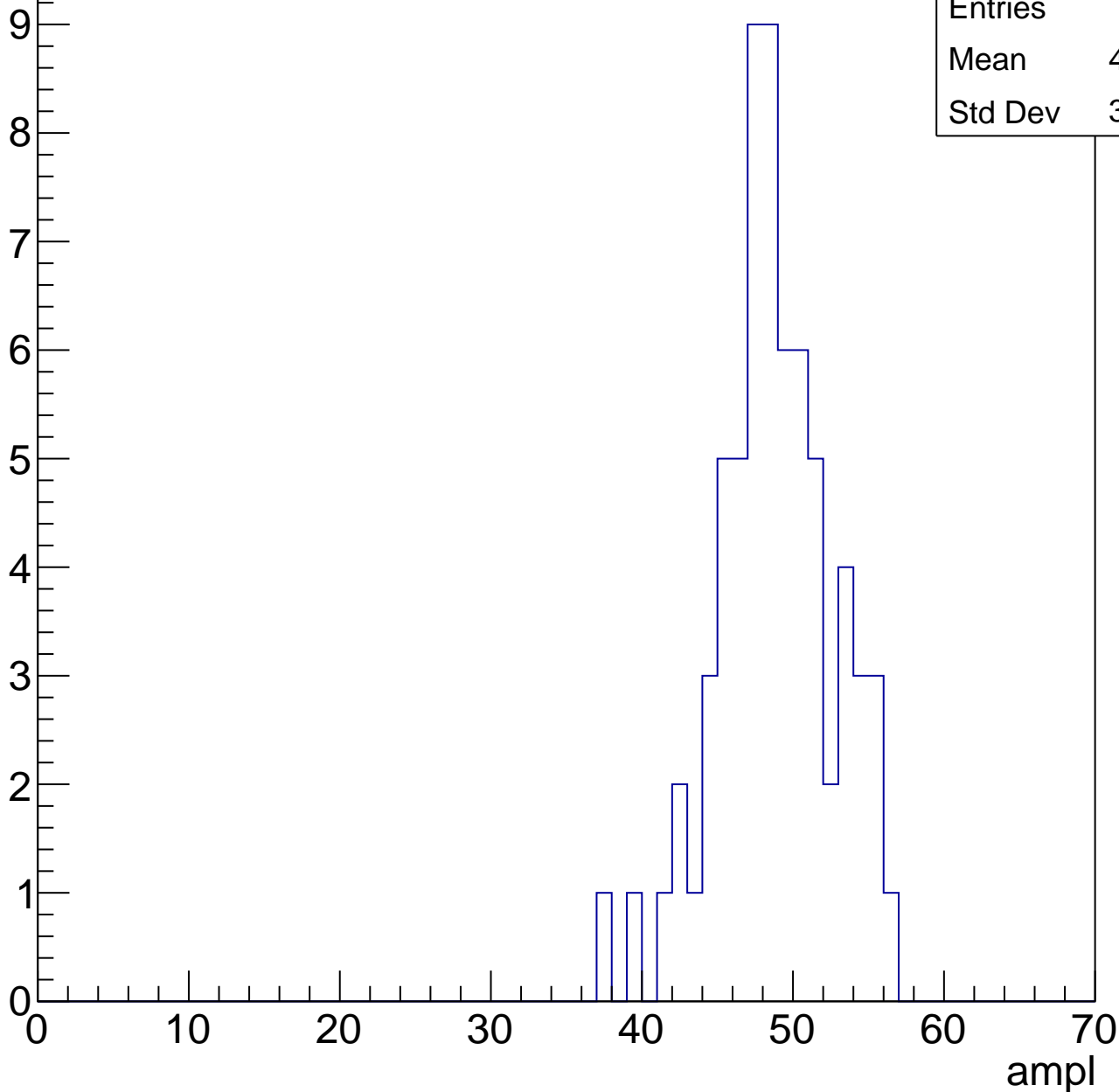


B1L102S, U17-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.27
Std Dev	3.846

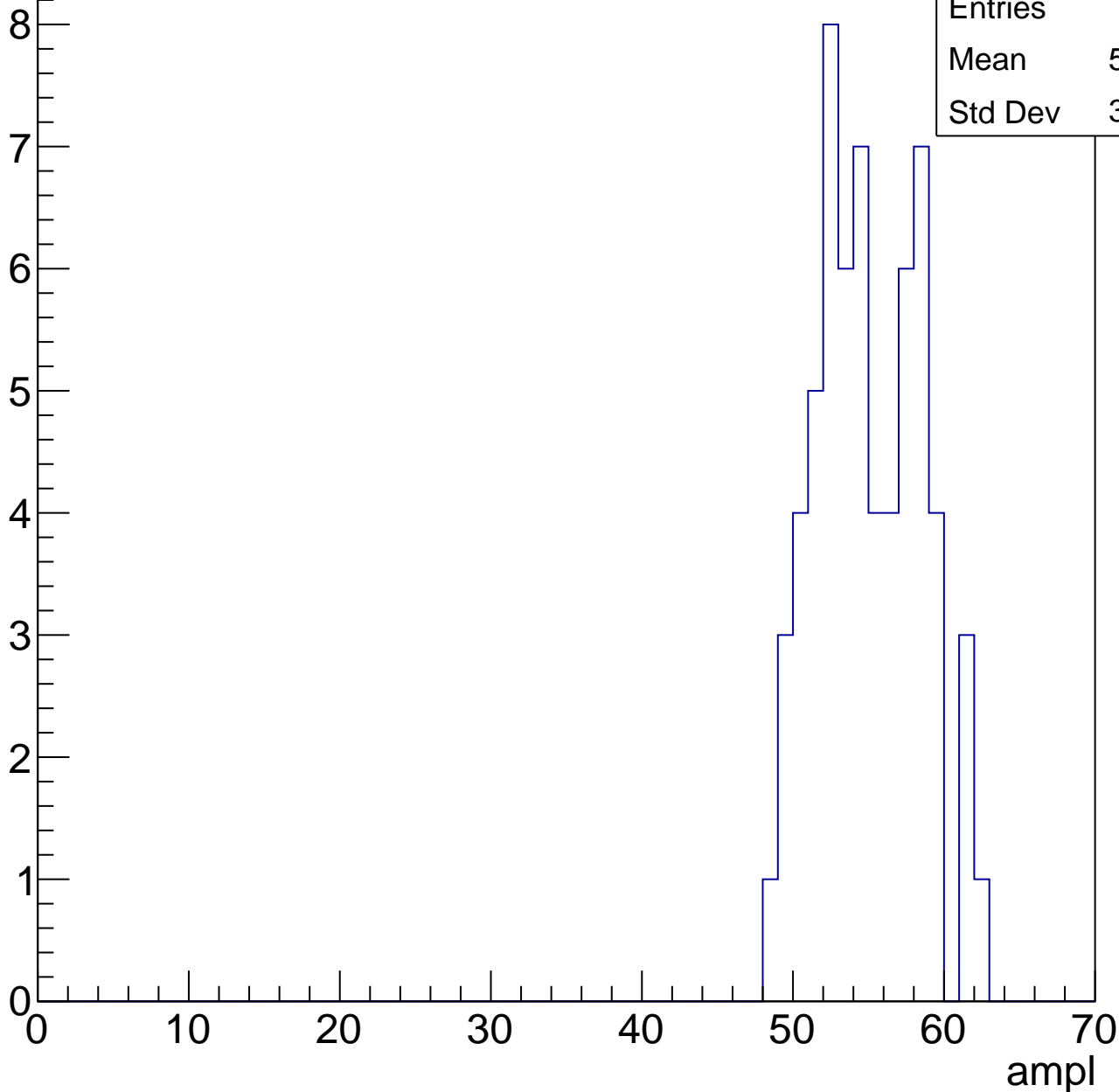


B1L102S, U17-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.52
Std Dev	3.422



B1L102S, U17-ch53, adc5

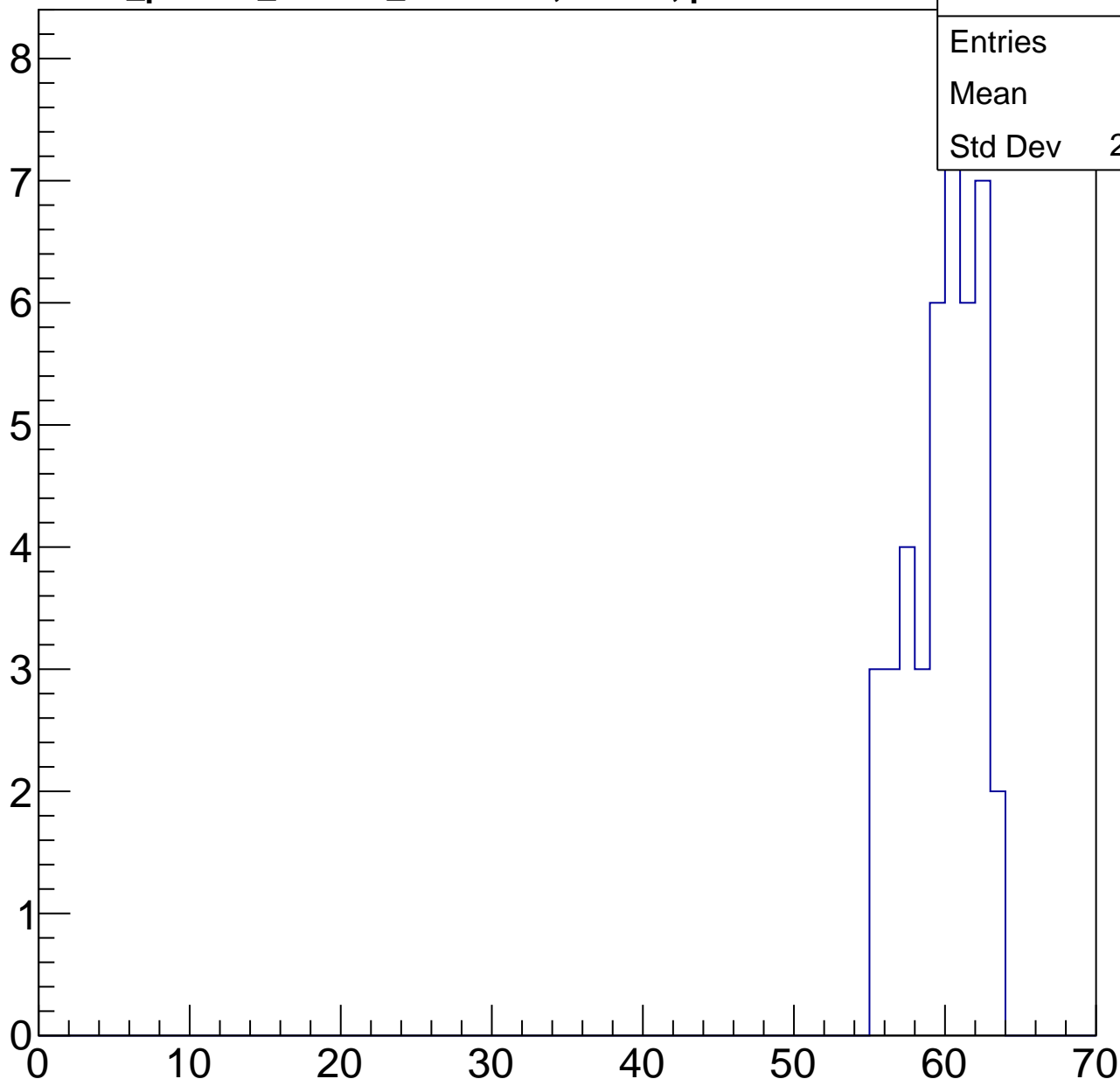
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.4
Std Dev	2.258

ampl

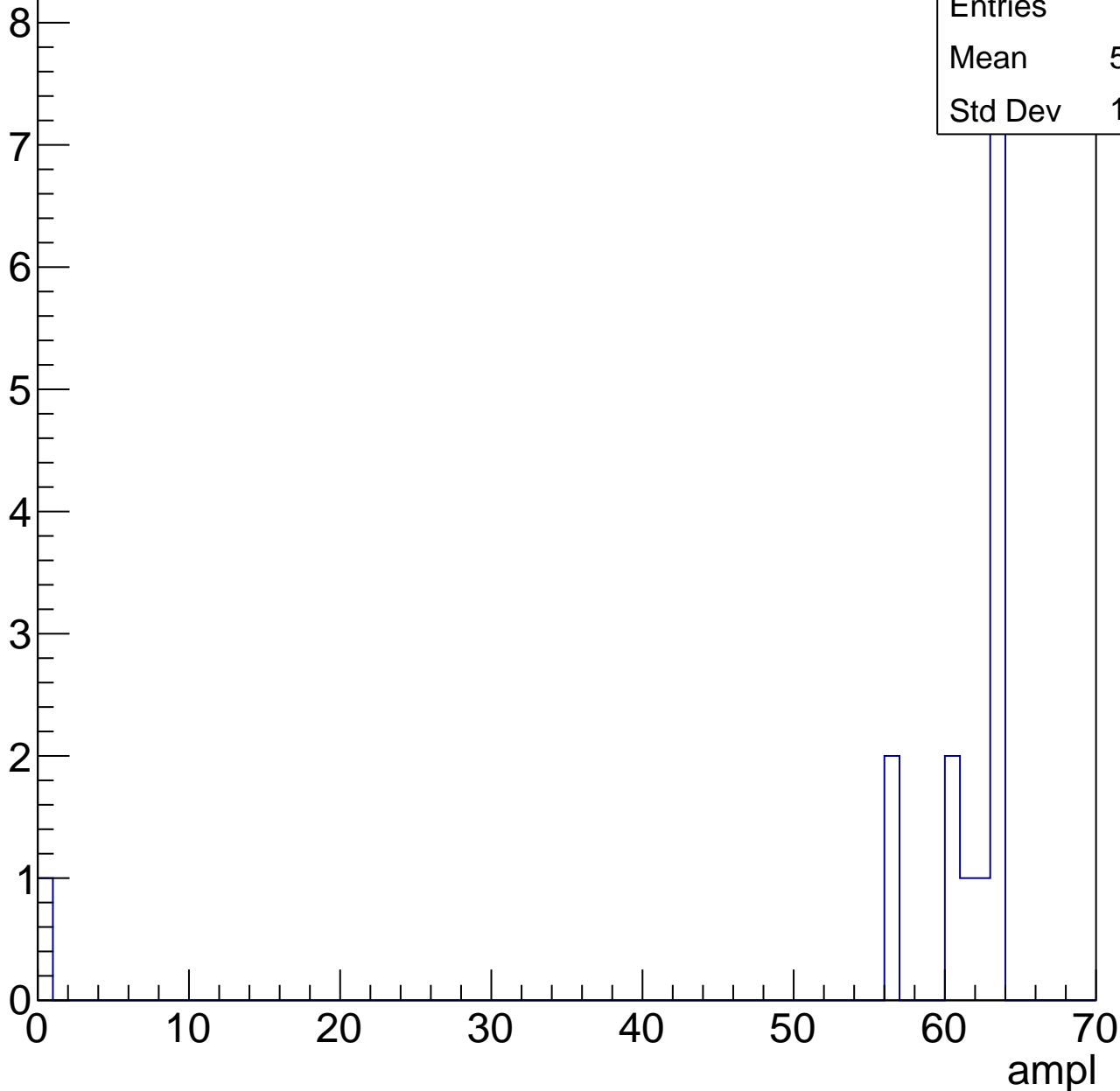


B1L102S, U17-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.27
Std Dev	15.49



B1L102S, U17-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch54, adc0

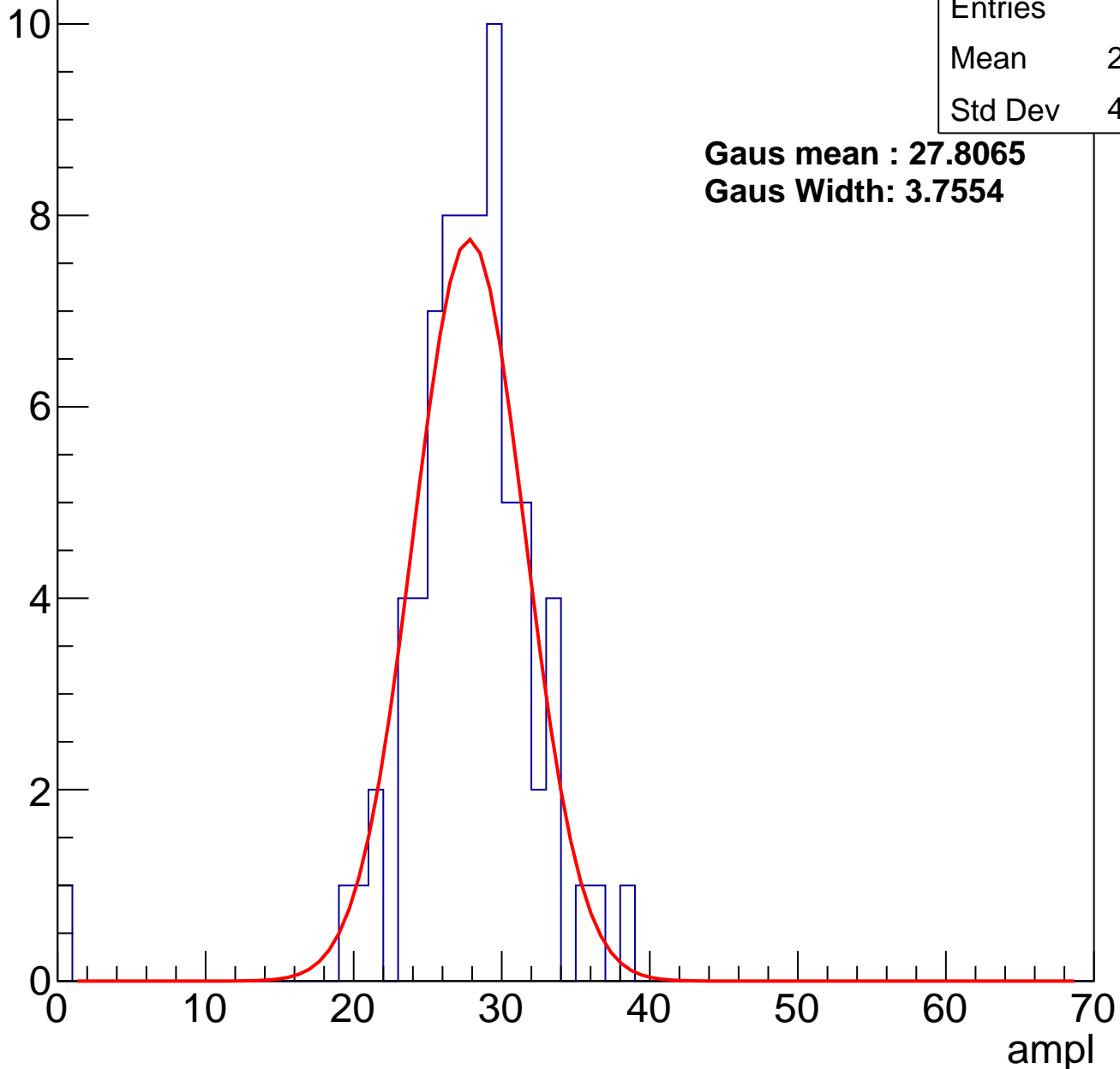
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	27.29
Std Dev	4.793

Gaus mean : 27.8065

Gaus Width: 3.7554

Entry



B1L102S, U17-ch54, adc1

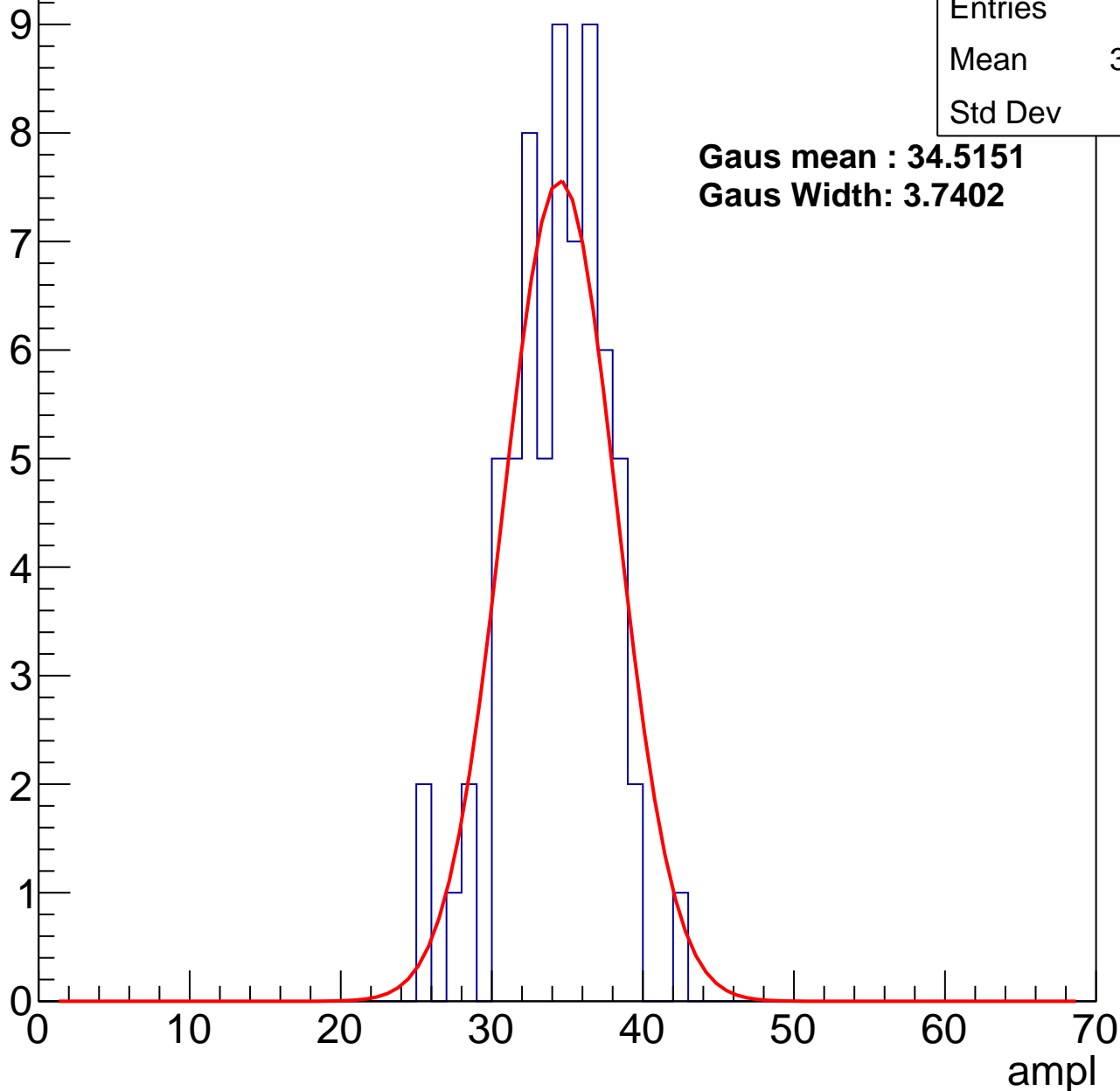
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	33.82
Std Dev	3.31

Gaus mean : 34.5151

Gaus Width: 3.7402



B1L102S, U17-ch54, adc2

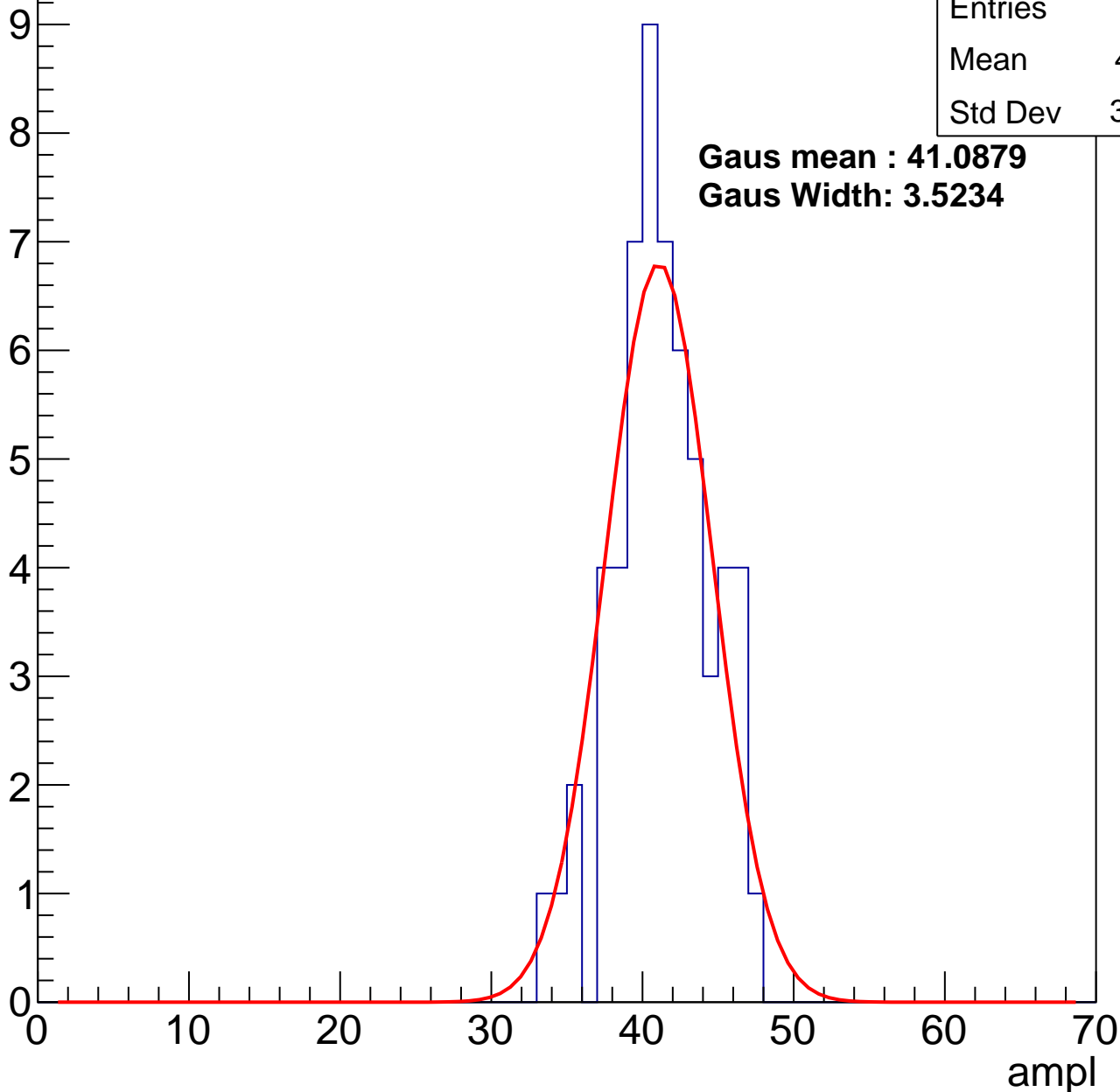
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	40.81
Std Dev	3.143

Gaus mean : 41.0879

Gaus Width: 3.5234

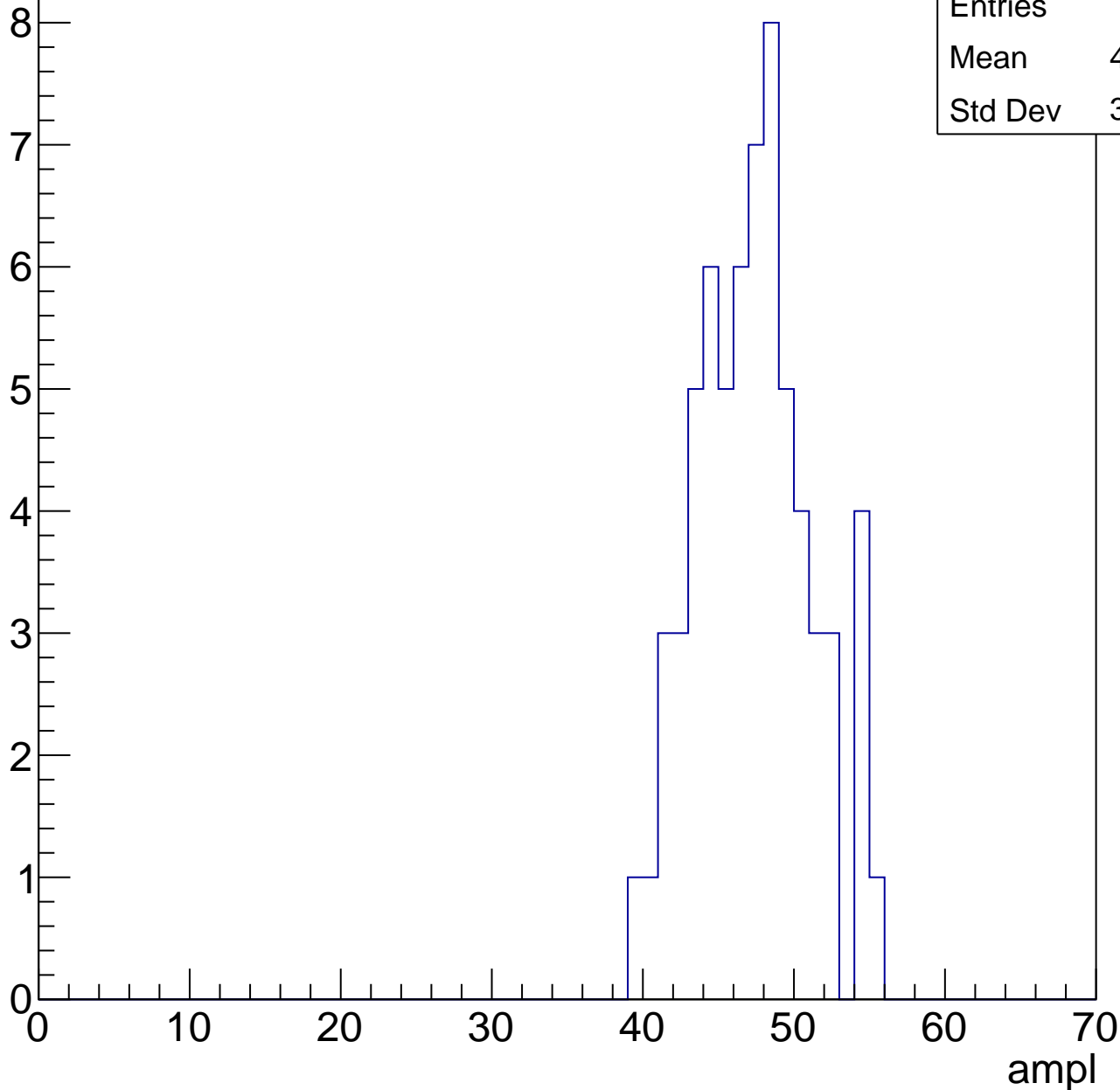


B1L102S, U17-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	46.86
Std Dev	3.725

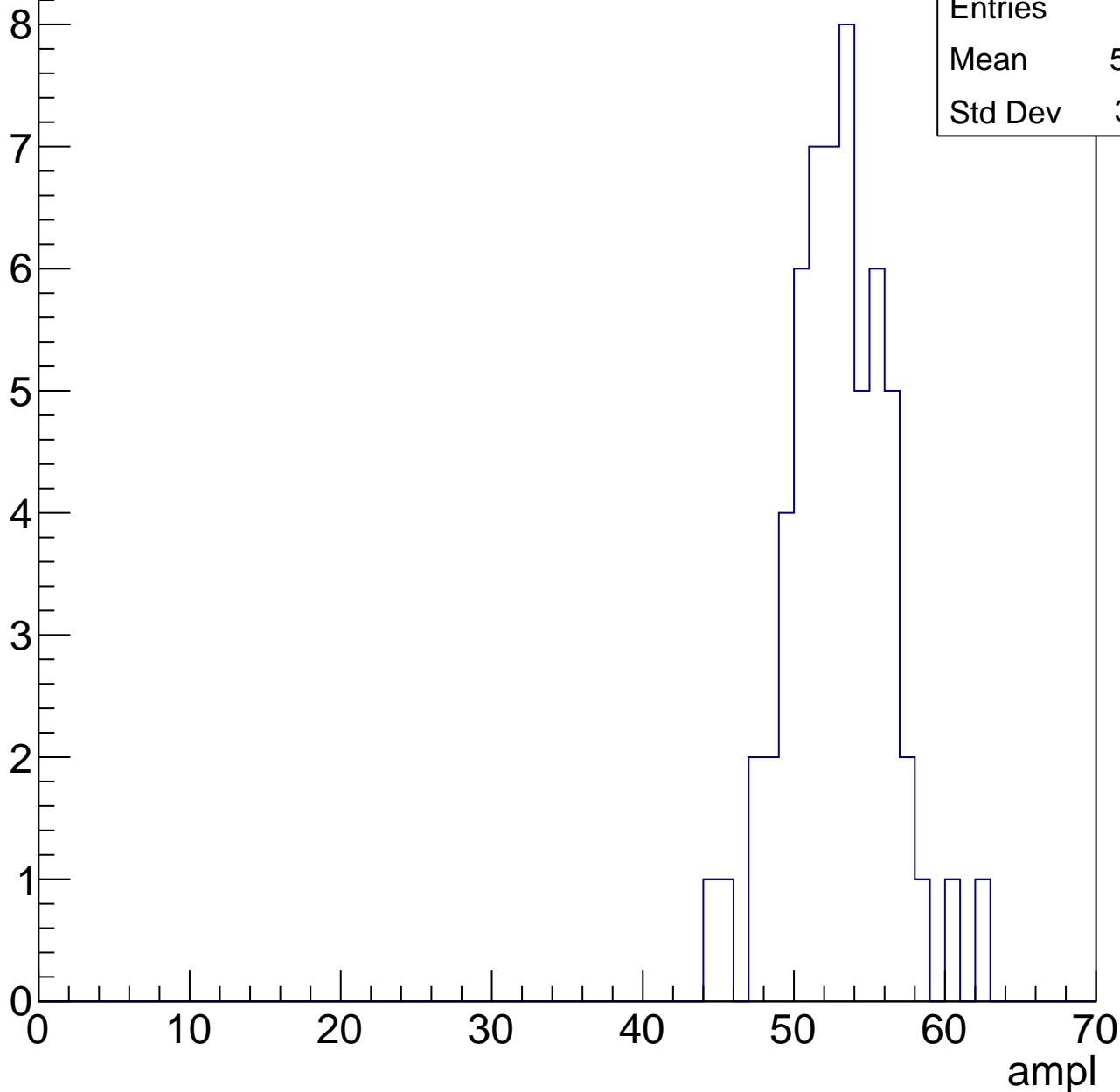


B1L102S, U17-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	52.44
Std Dev	3.351

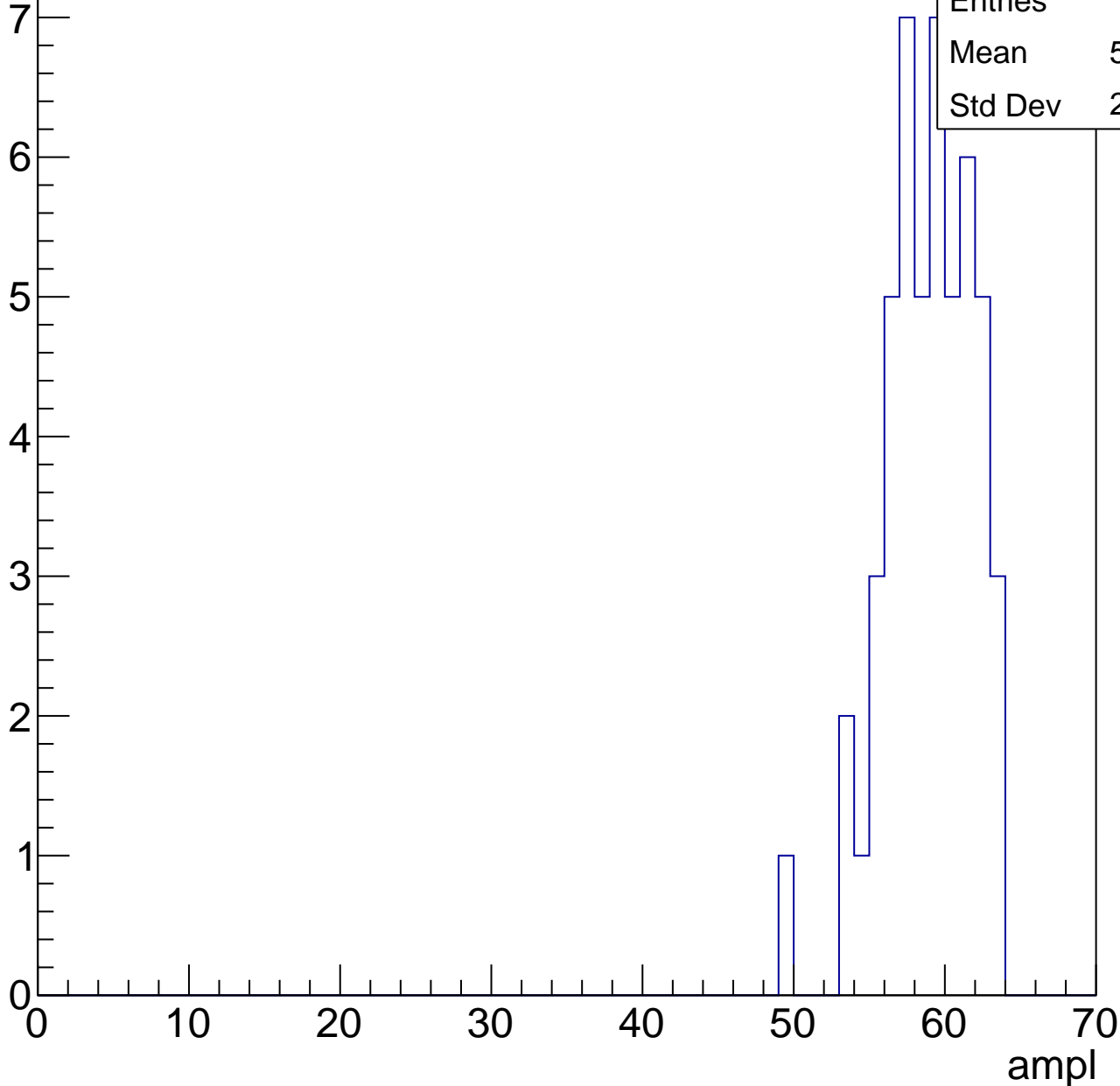


B1L102S, U17-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.42
Std Dev	2.926

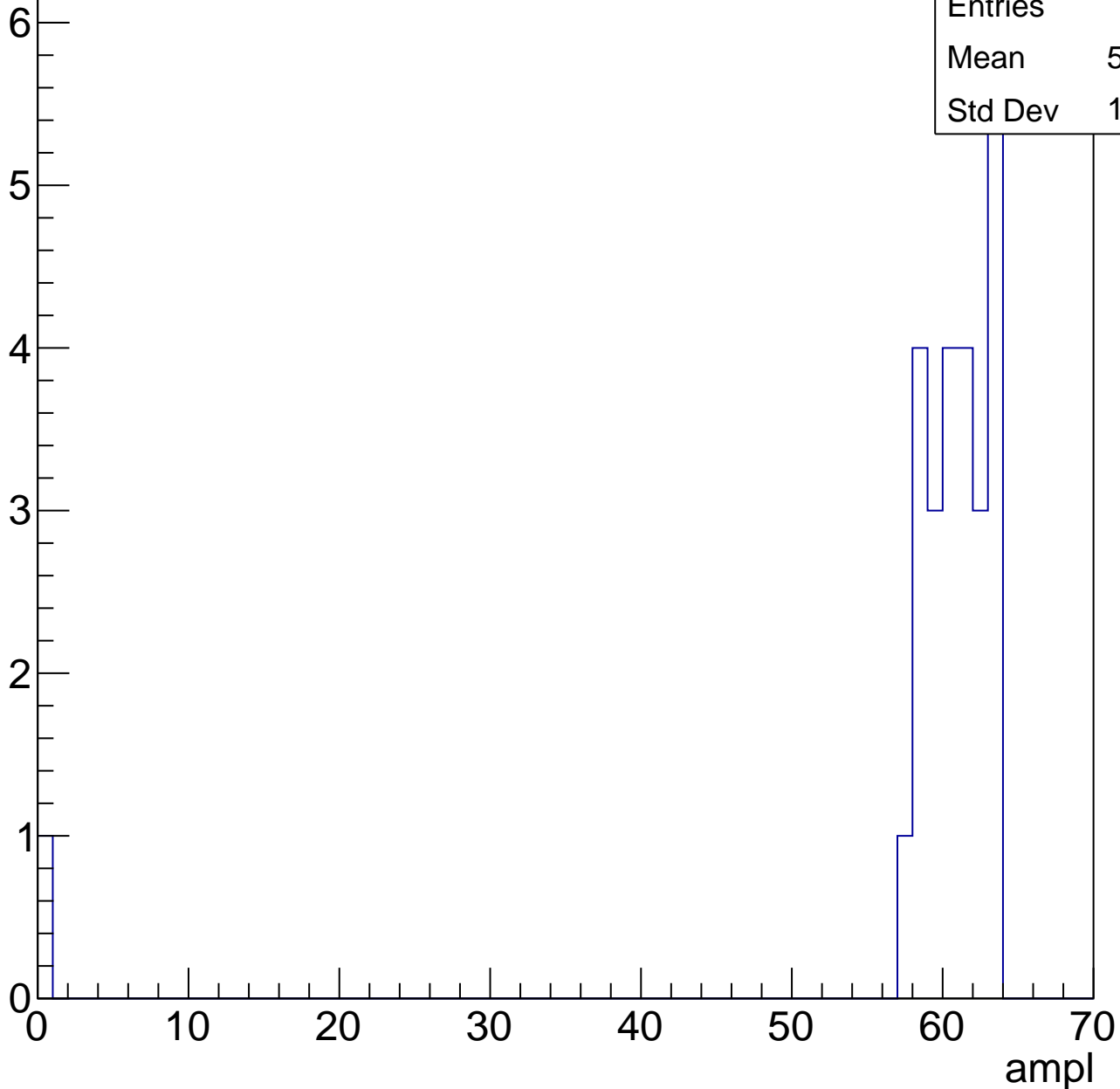


B1L102S, U17-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

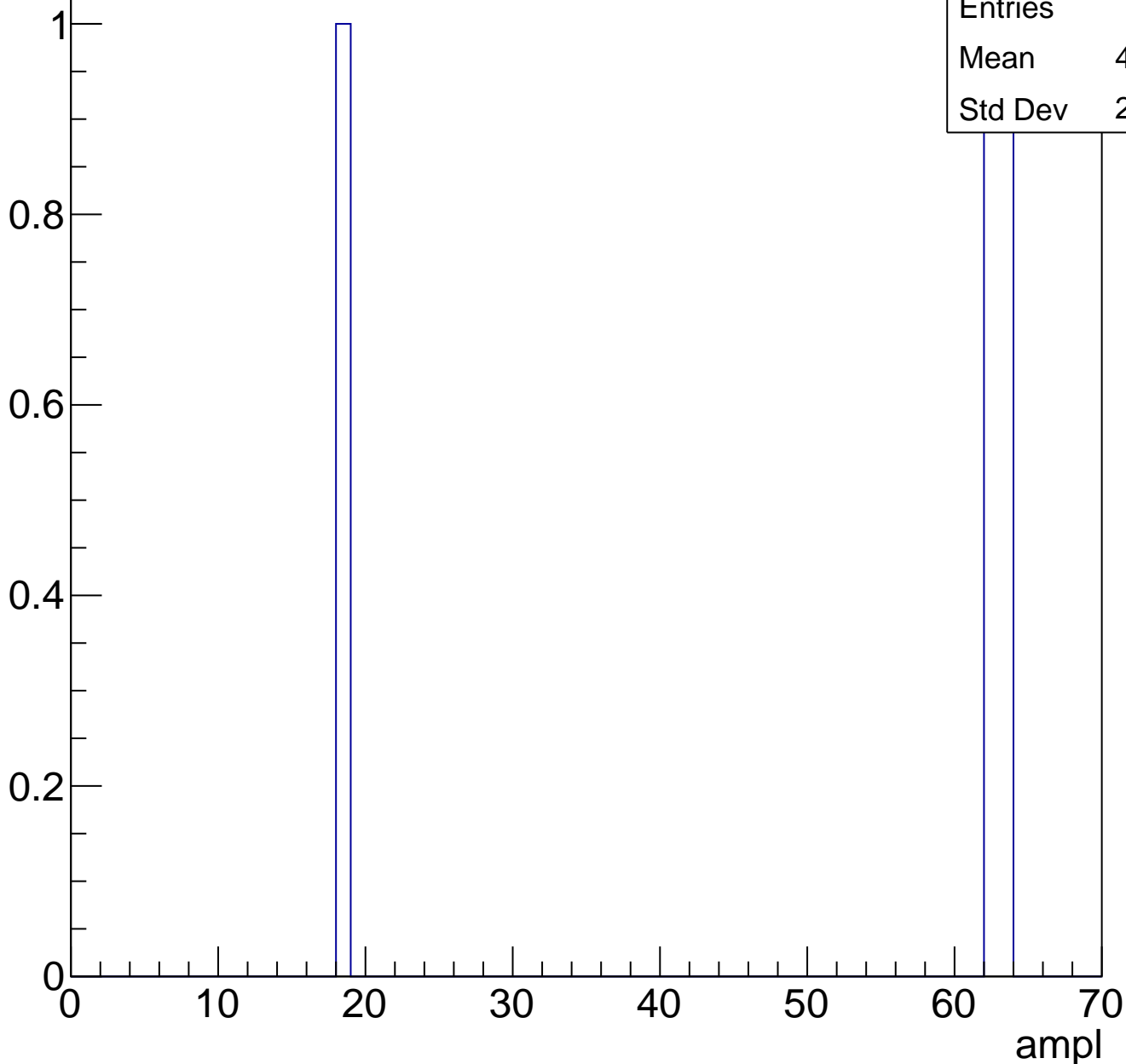
Entries	26
Mean	58.23
Std Dev	11.79



B1L102S, U17-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	47.67
Std Dev	20.98

B1L102S, U17-ch55, adc0

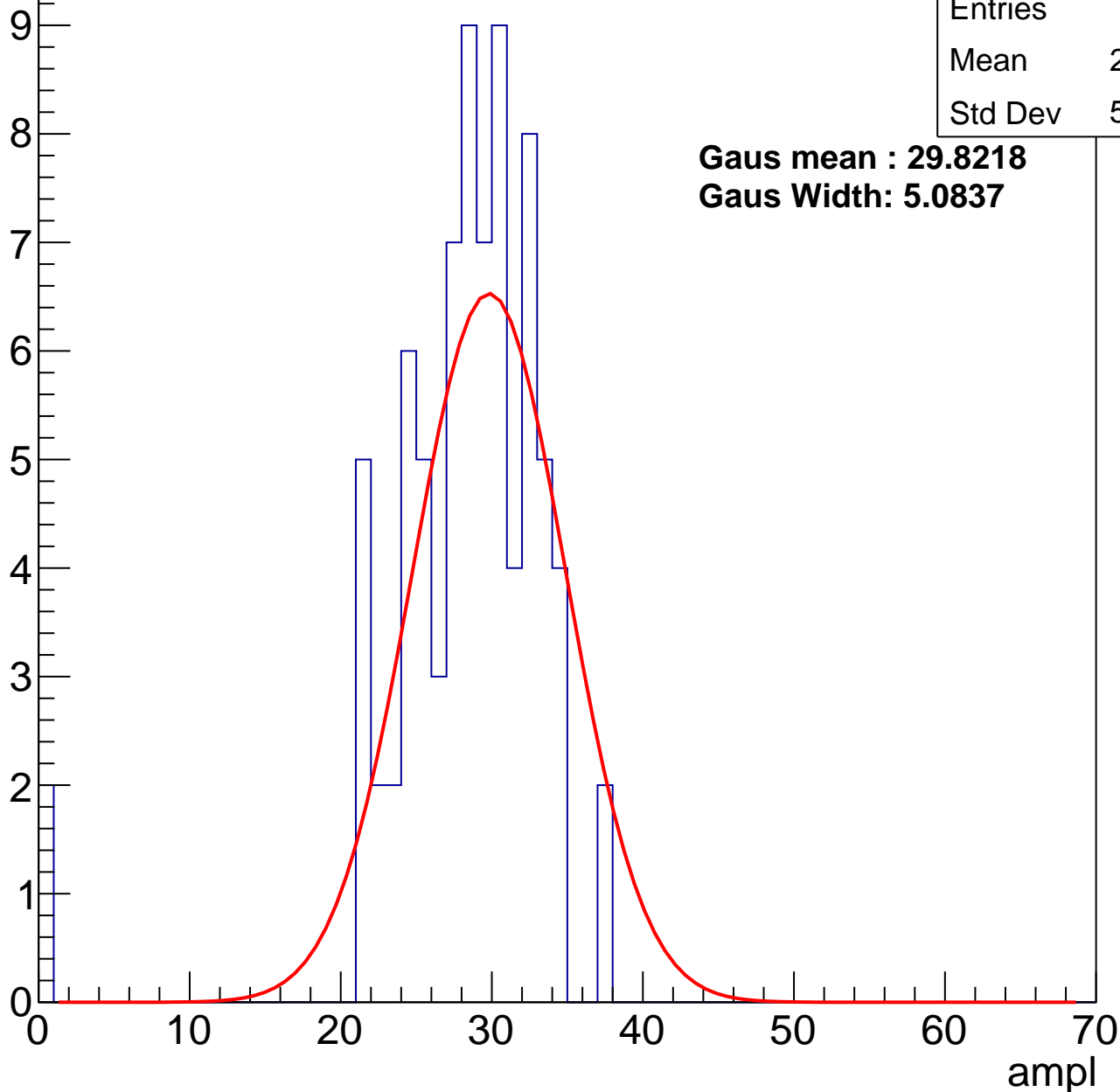
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.64
Std Dev	5.834

Gaus mean : 29.8218

Gaus Width: 5.0837



B1L102S, U17-ch55, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	36.25
Std Dev	3.587

Gaus mean : 36.7803

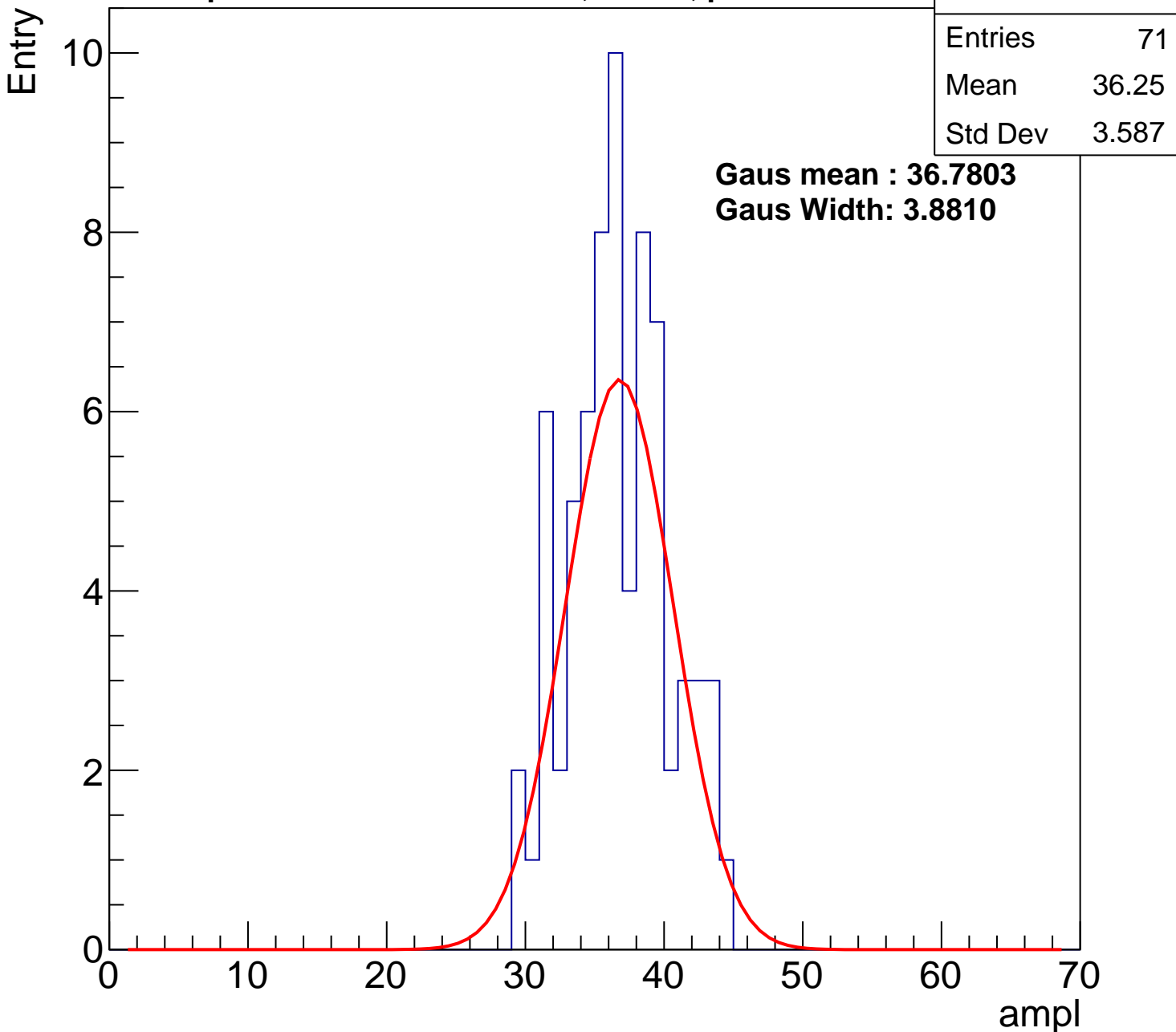
Gaus Width: 3.8810

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch55, adc2

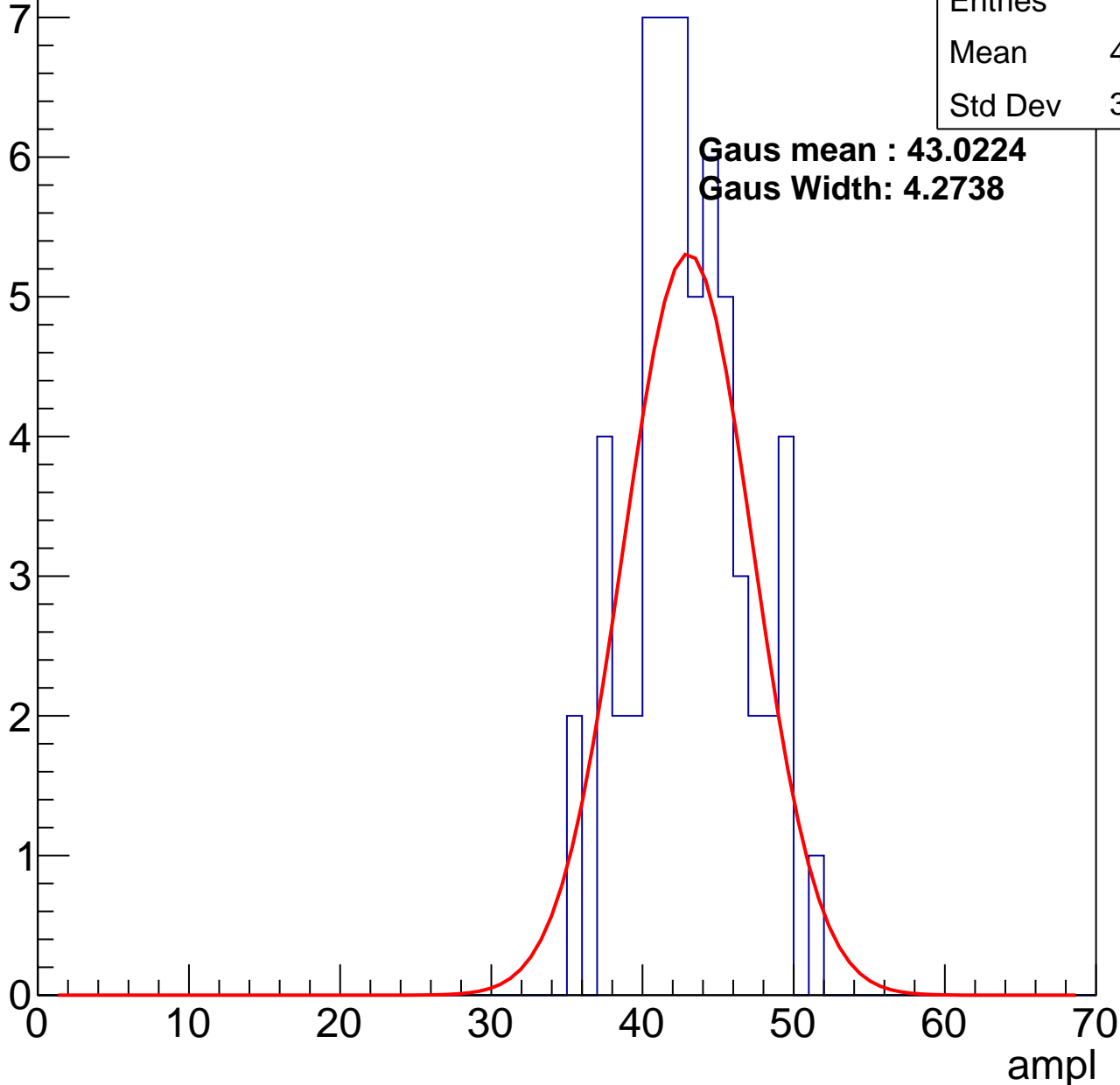
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.58
Std Dev	3.646

Gaus mean : 43.0224

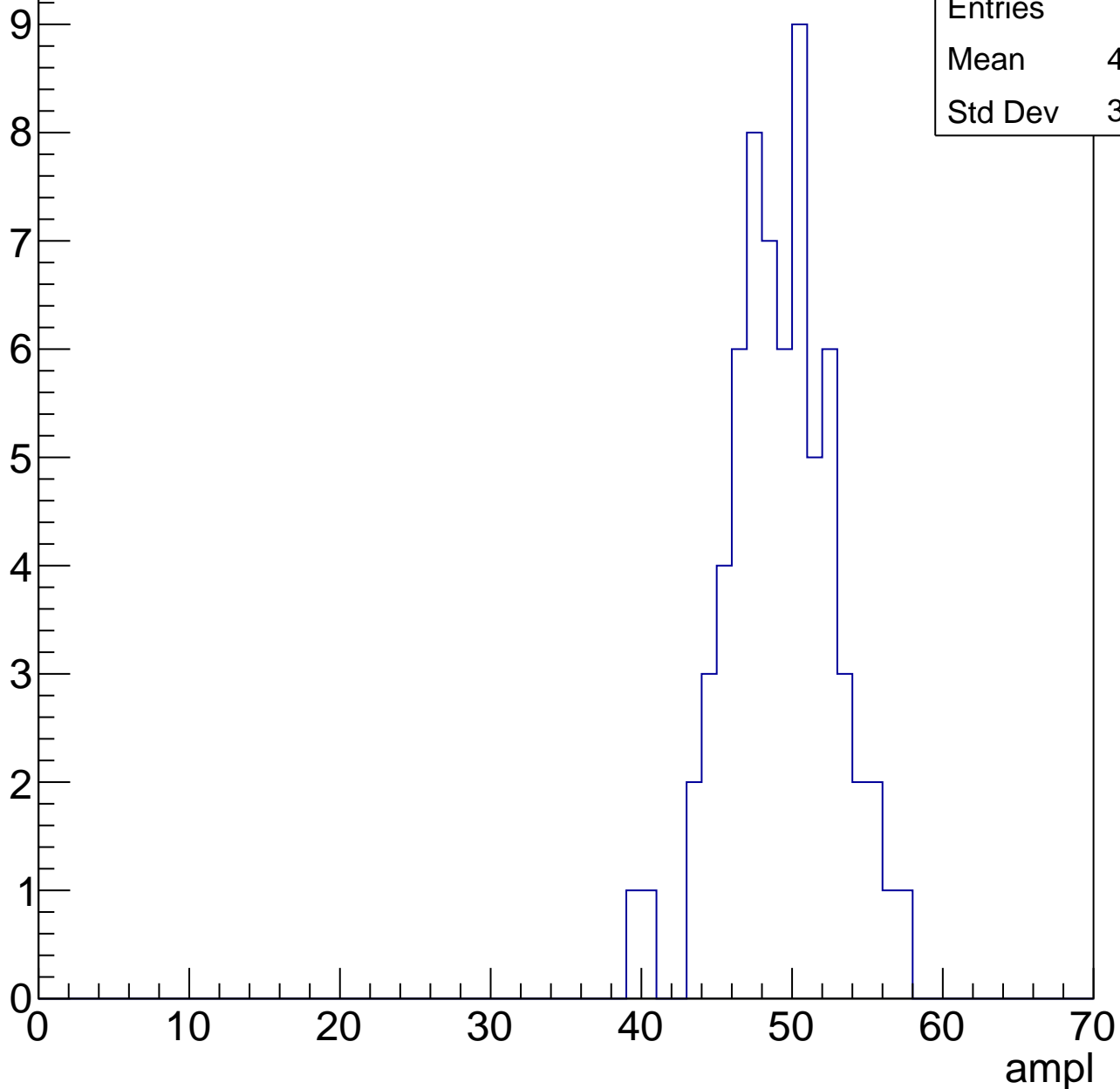
Gaus Width: 4.2738



B1L102S, U17-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

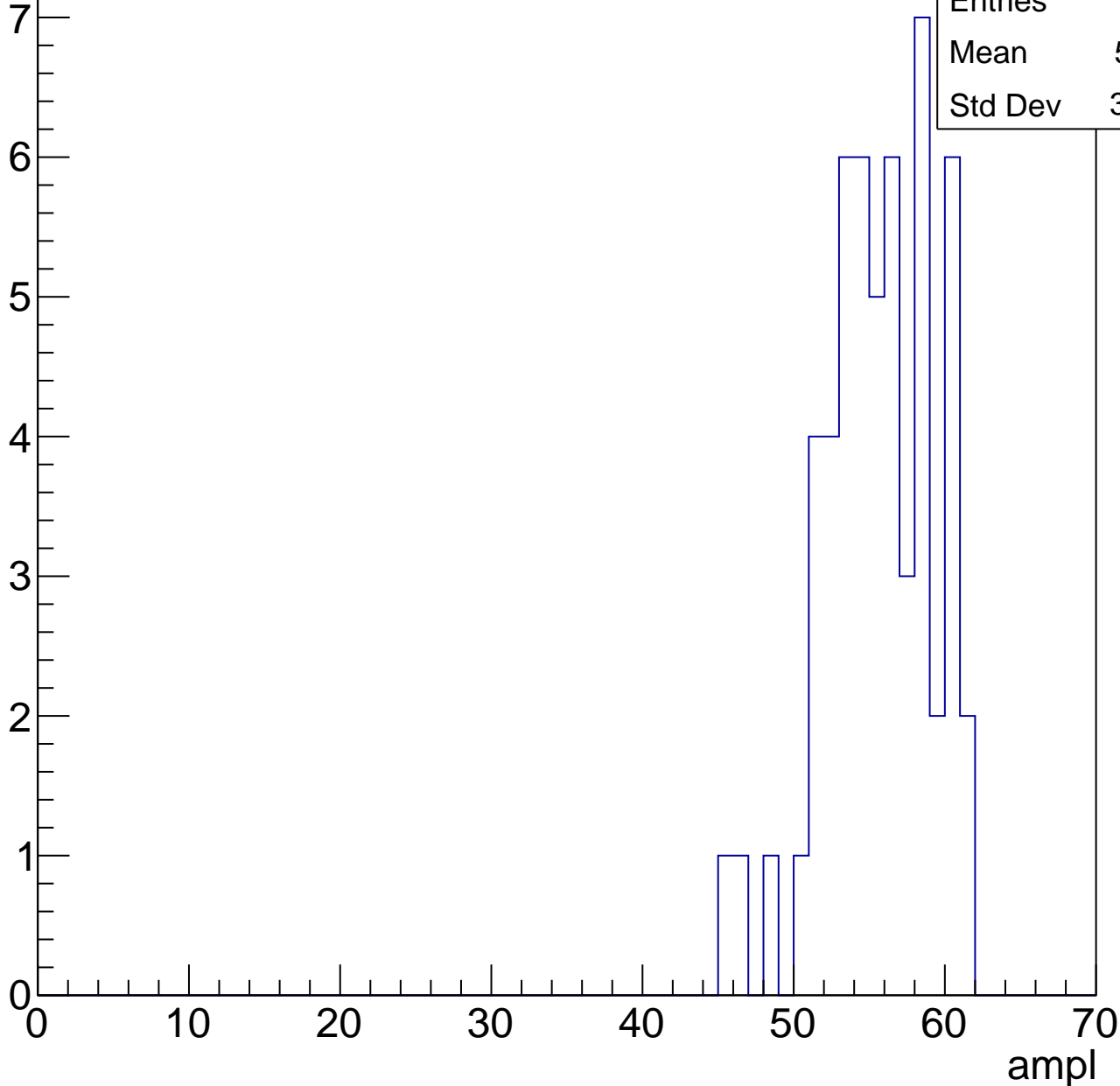


B1L102S, U17-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.11
Std Dev	3.616



B1L102S, U17-ch55, adc5

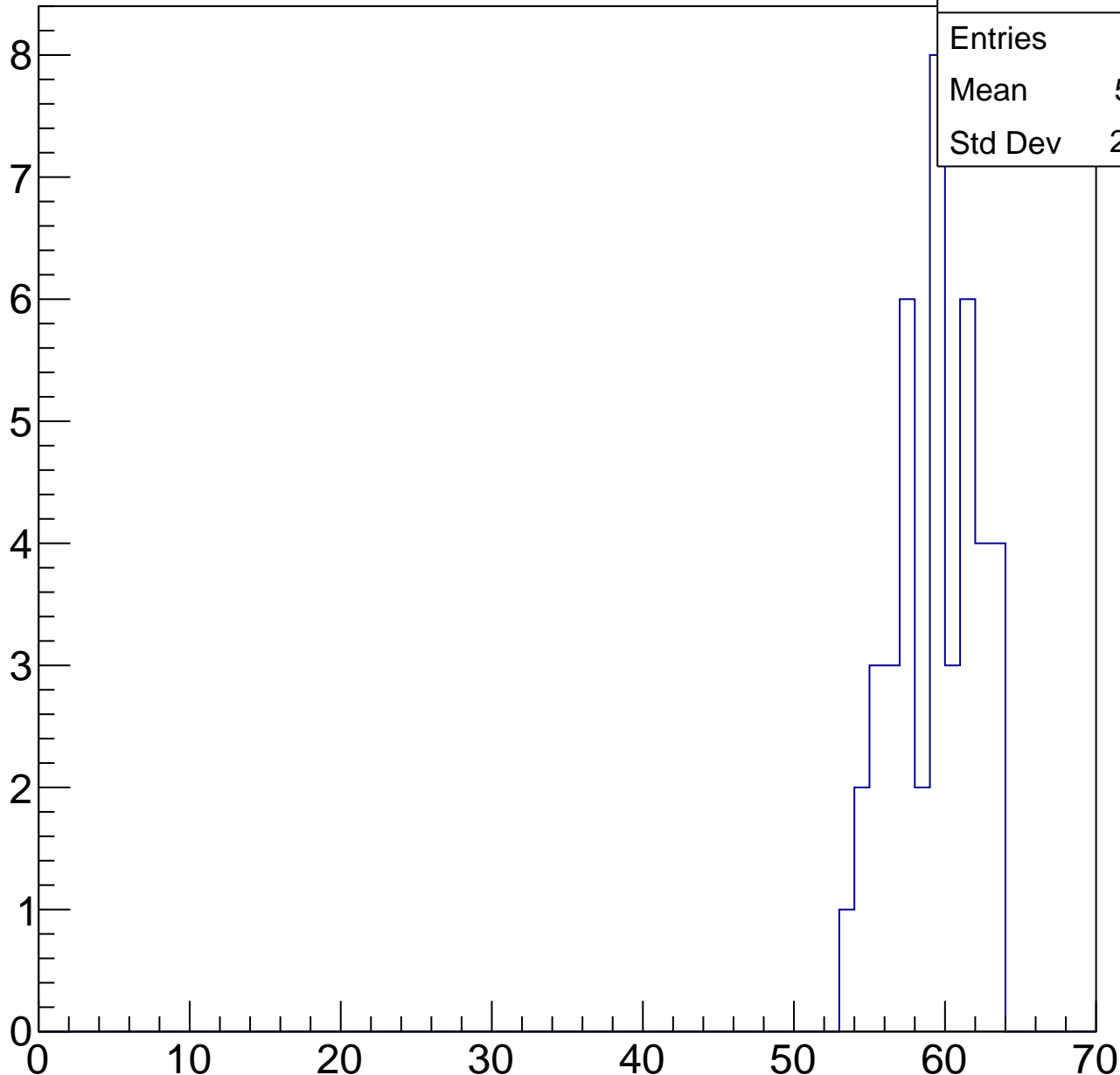
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.81
Std Dev	2.728

ampl

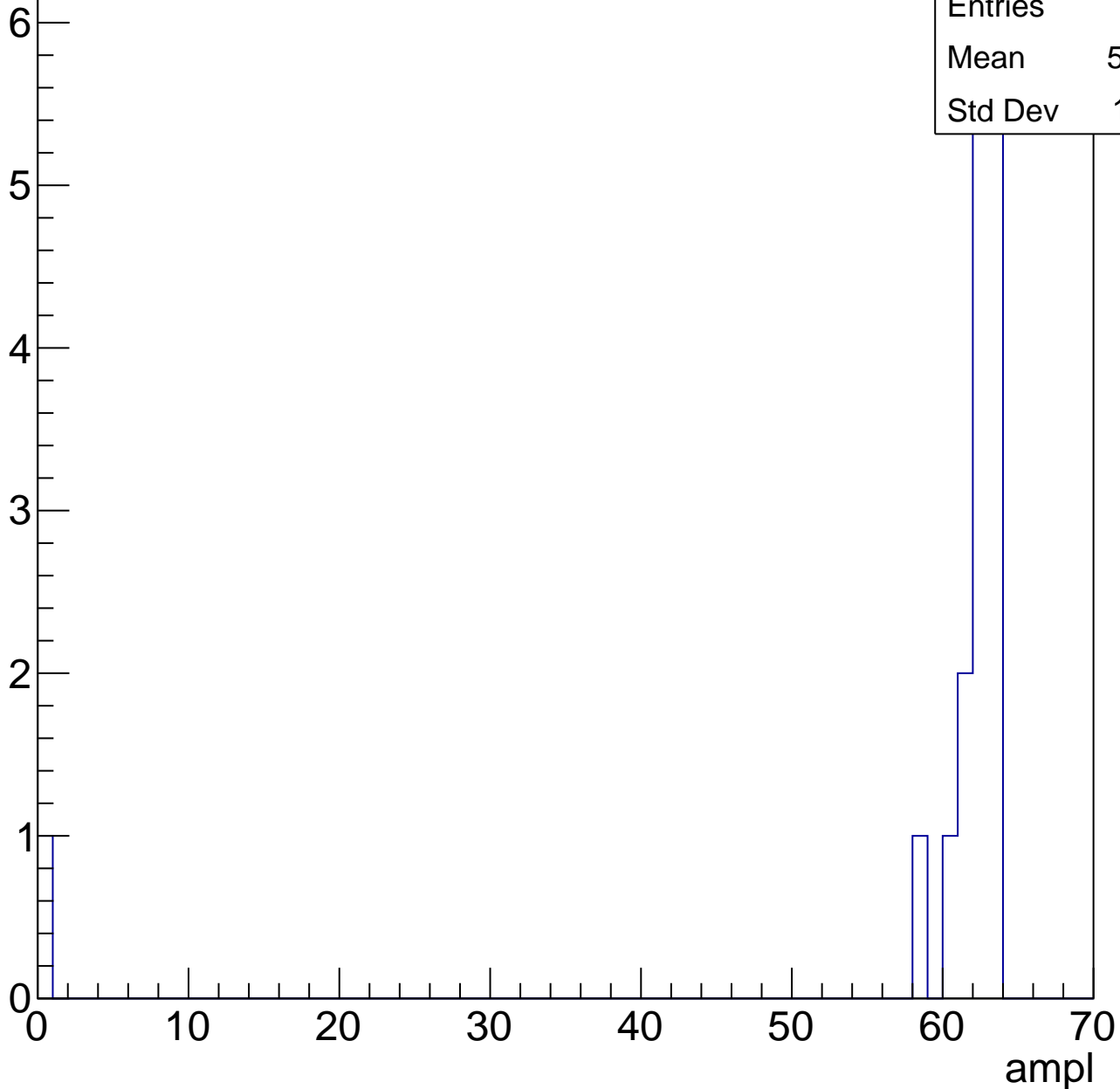


B1L102S, U17-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	58.24
Std Dev	14.61



B1L102S, U17-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch56, adc0

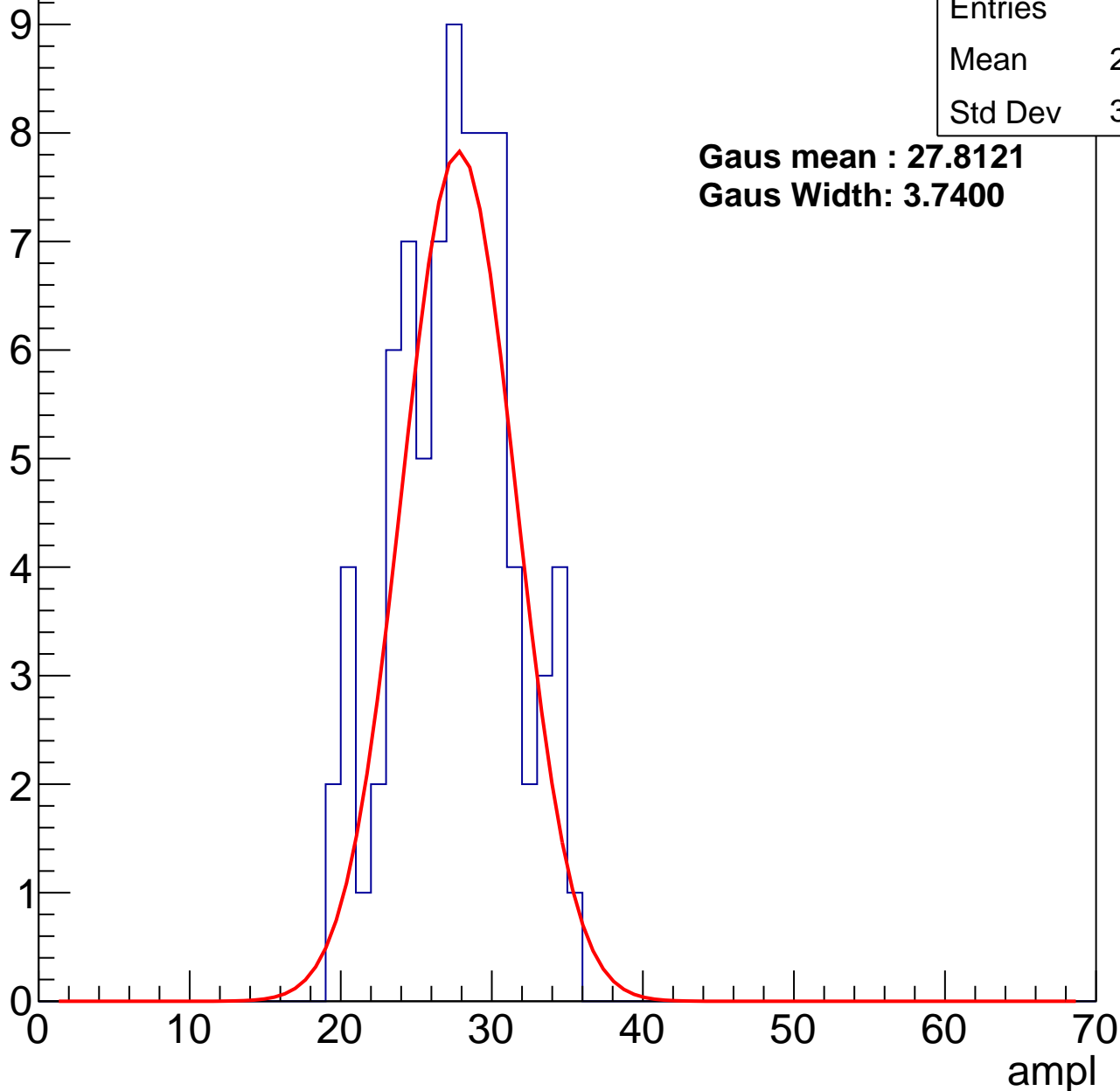
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.07
Std Dev	3.832

Gaus mean : 27.8121

Gaus Width: 3.7400



B1L102S, U17-ch56, adc1

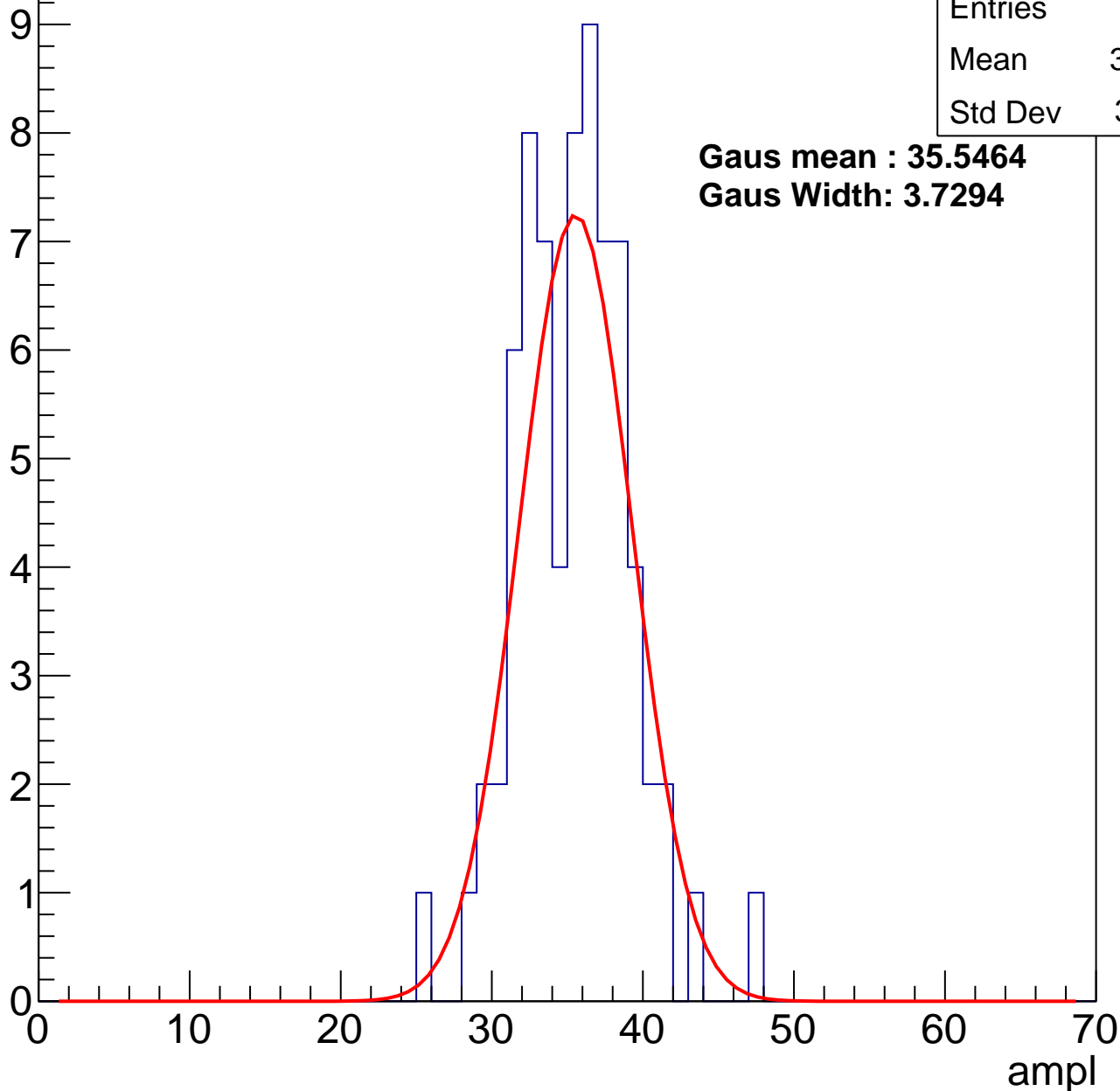
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.96
Std Dev	3.661

Gaus mean : 35.5464

Gaus Width: 3.7294



B1L102S, U17-ch56, adc2

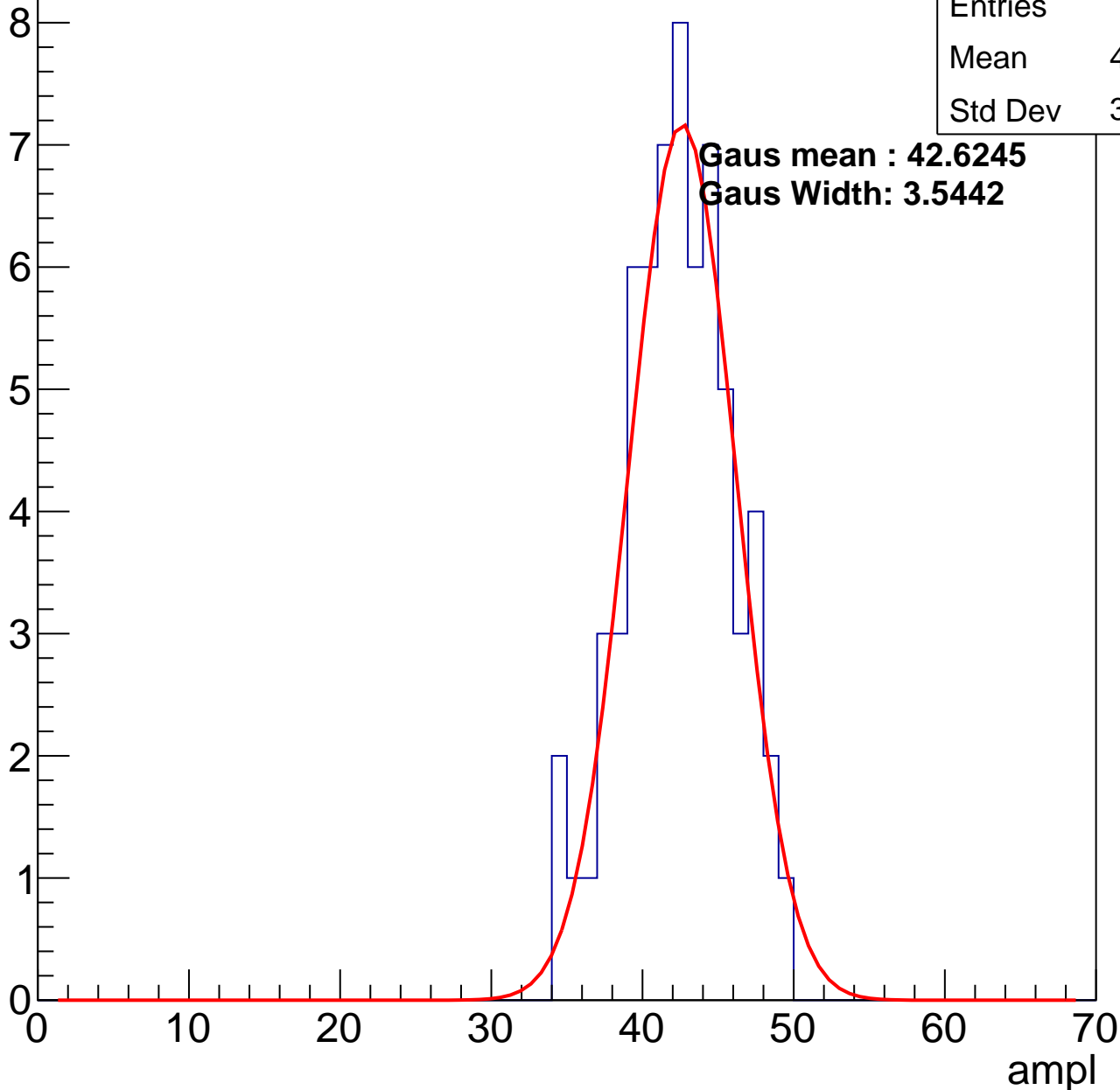
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.89
Std Dev	3.438

Gaus mean : 42.6245

Gaus Width: 3.5442

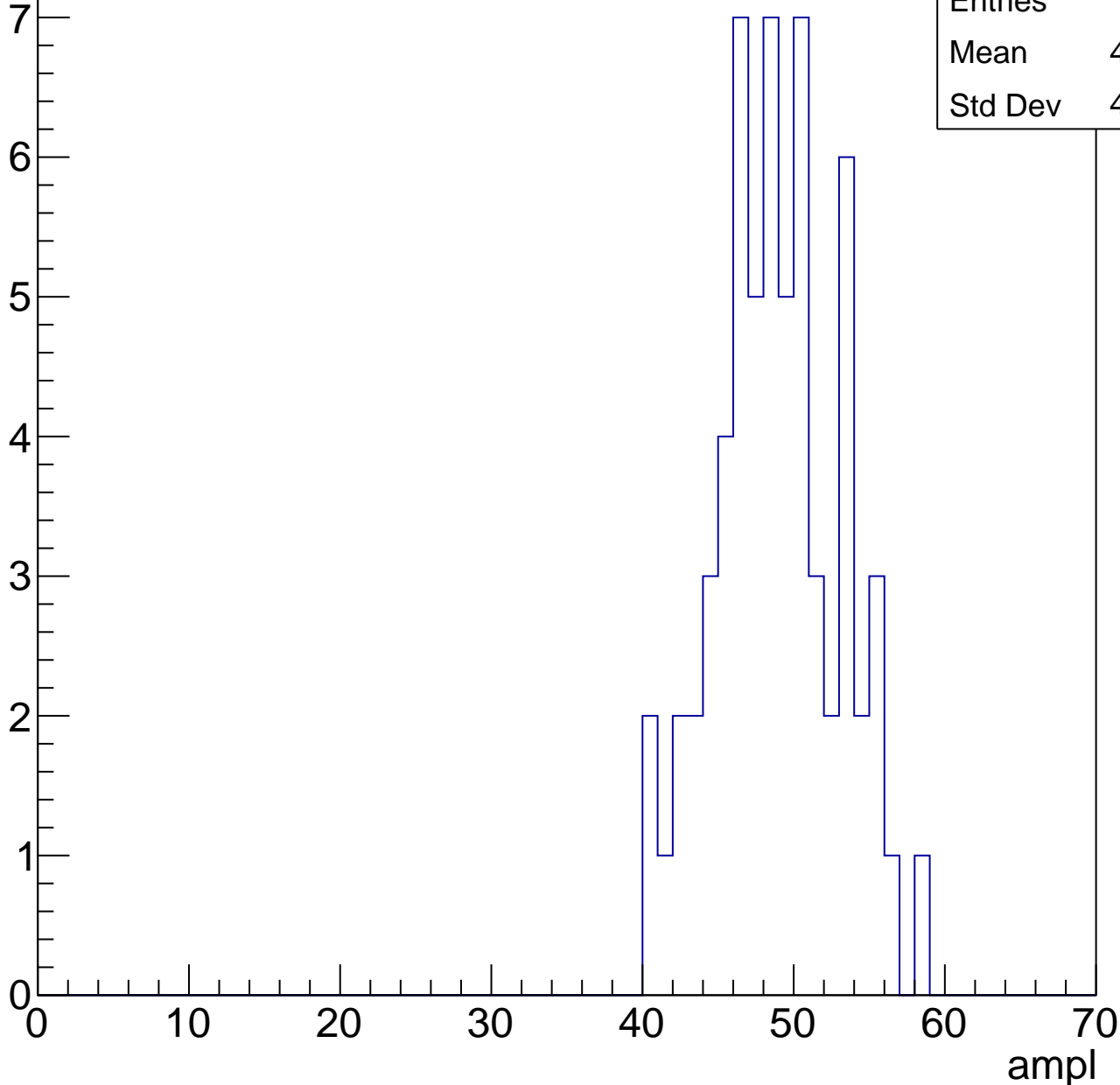


B1L102S, U17-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.46
Std Dev	4.039

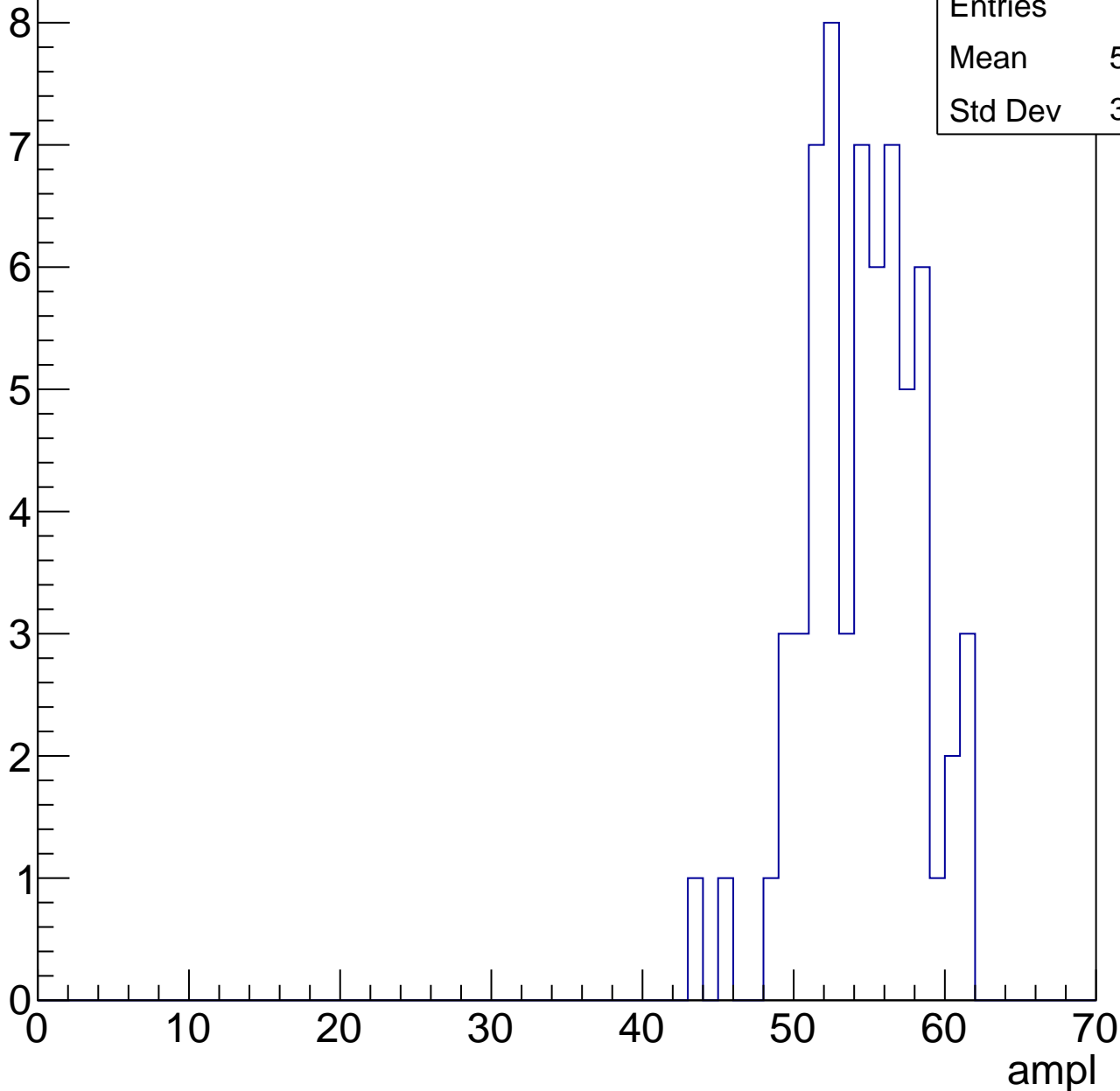


B1L102S, U17-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

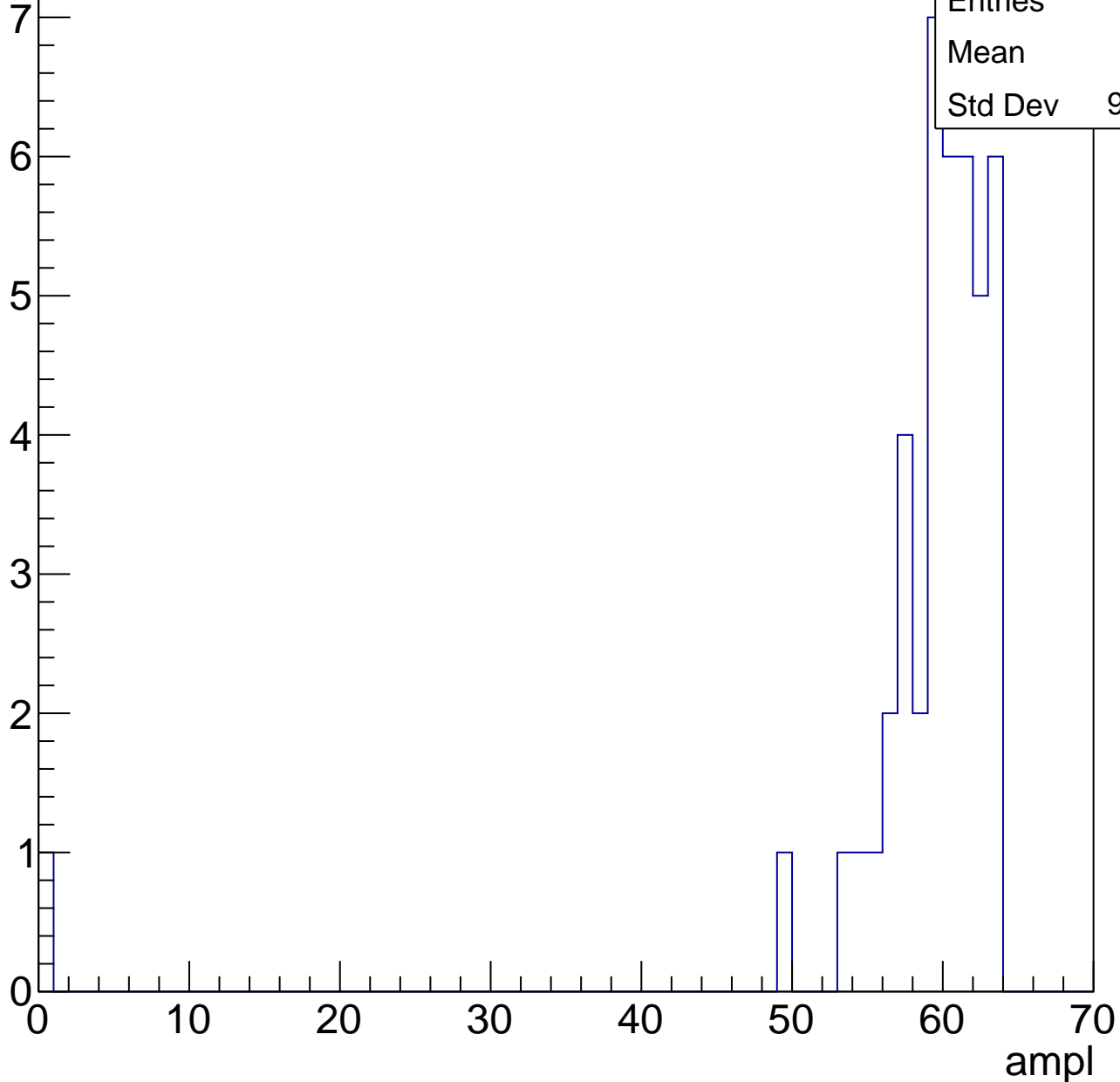
Entries	64
Mean	54.06
Std Dev	3.703



B1L102S, U17-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

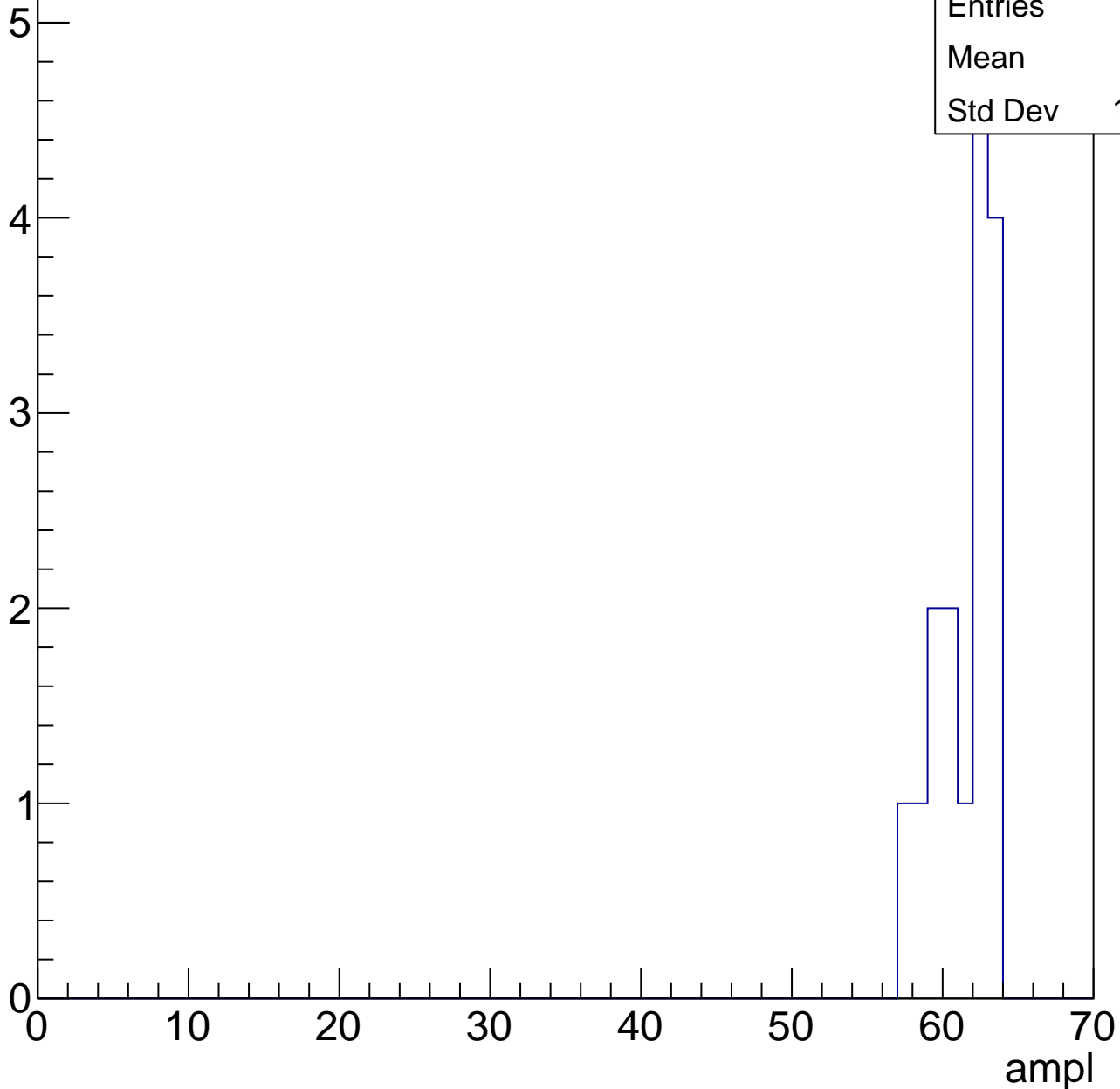


B1L102S, U17-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61
Std Dev	1.871



B1L102S, U17-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch57, adc0

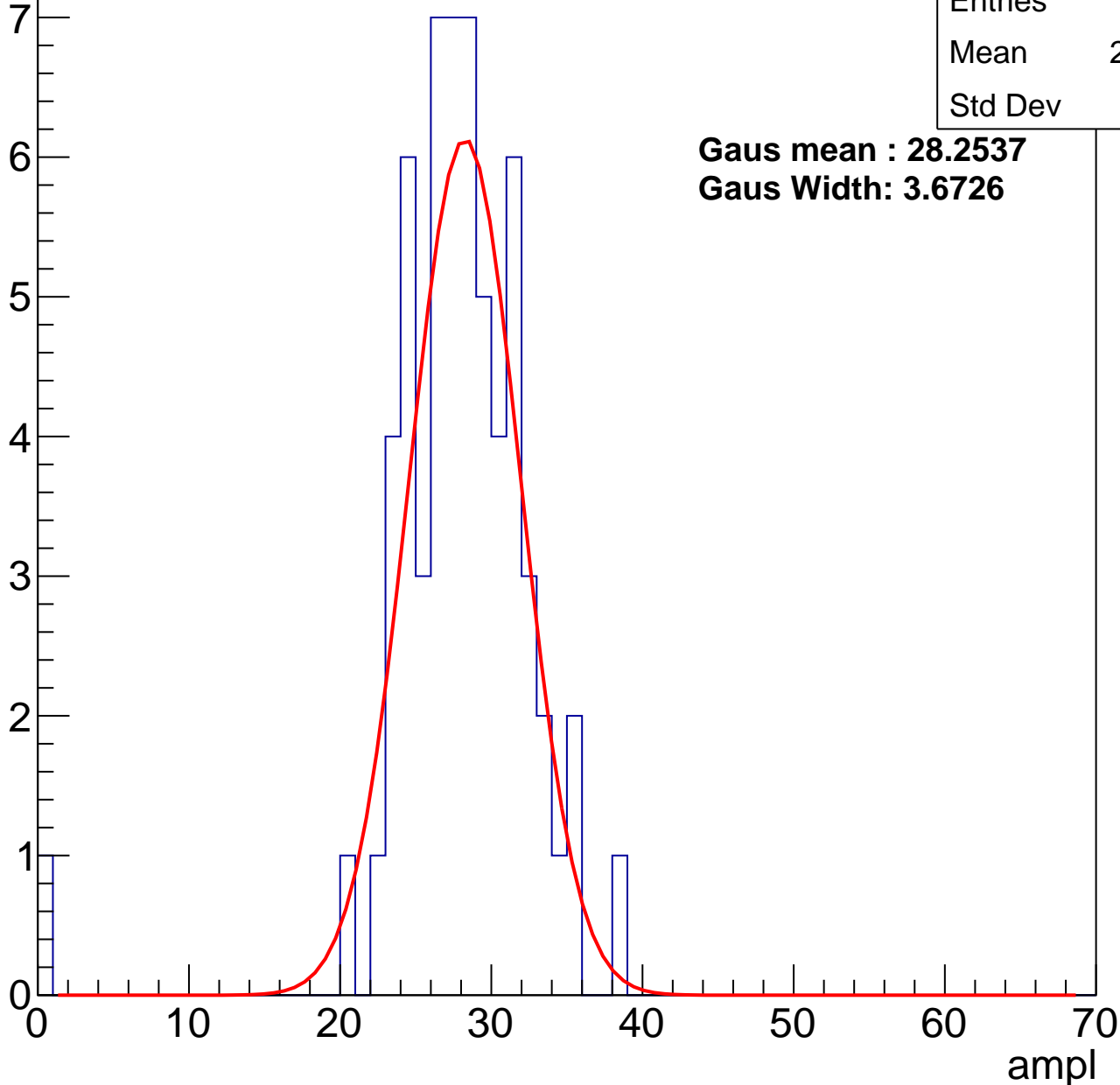
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.46
Std Dev	5.01

Gaus mean : 28.2537

Gaus Width: 3.6726



B1L102S, U17-ch57, adc1

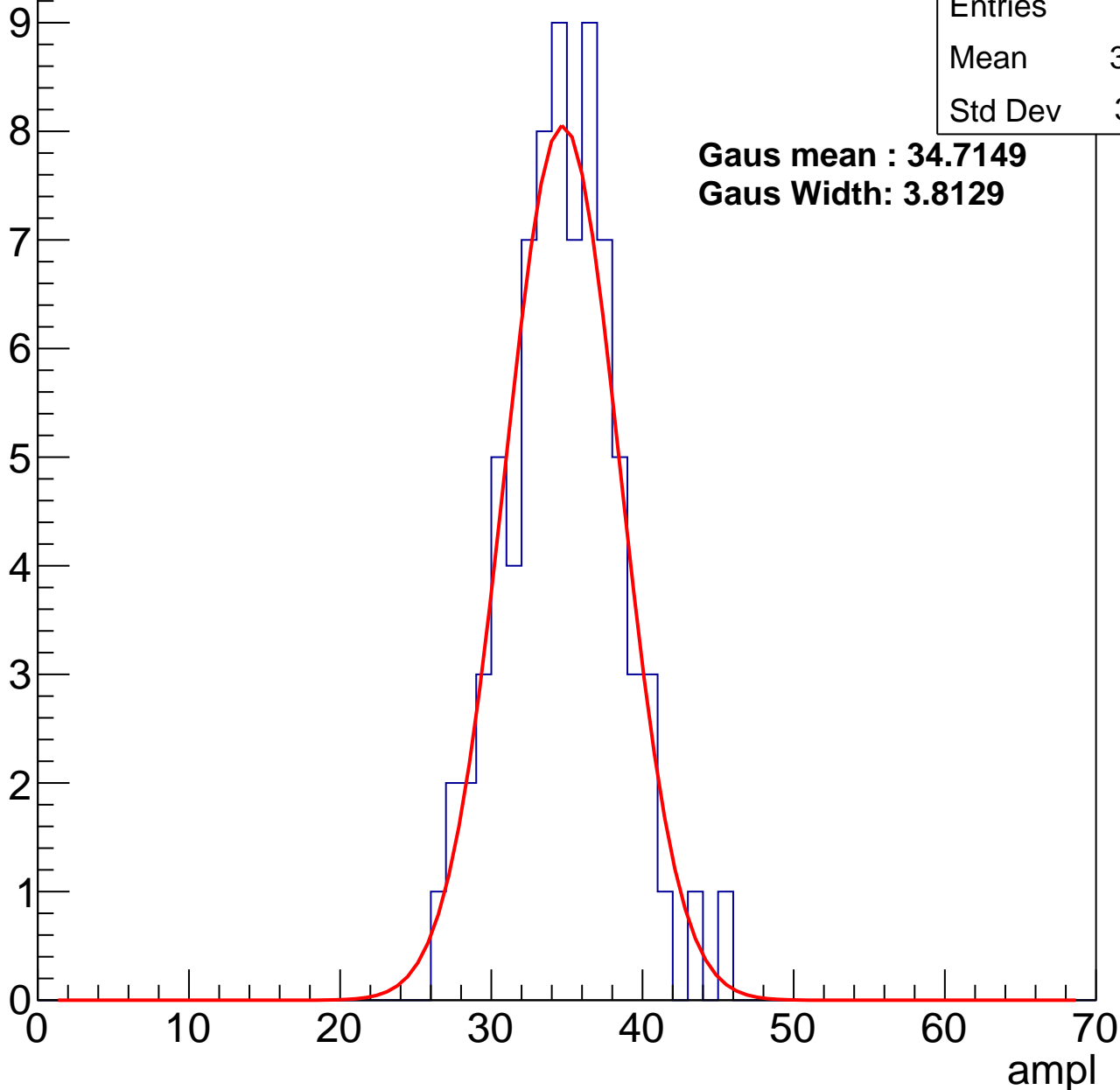
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	34.29
Std Dev	3.711

Gaus mean : 34.7149

Gaus Width: 3.8129



B1L102S, U17-ch57, adc2

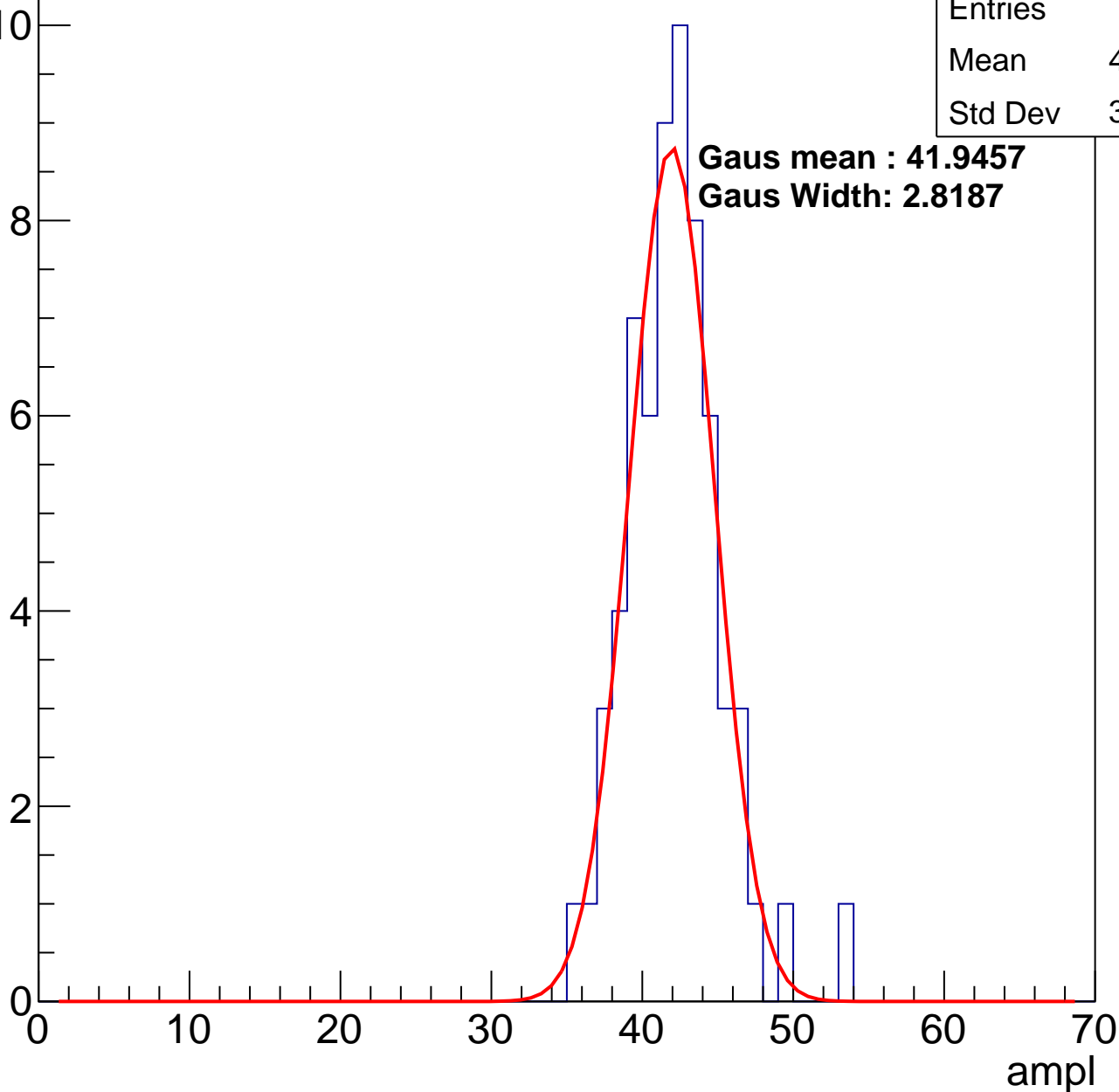
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.66
Std Dev	3.098

Gaus mean : 41.9457

Gaus Width: 2.8187

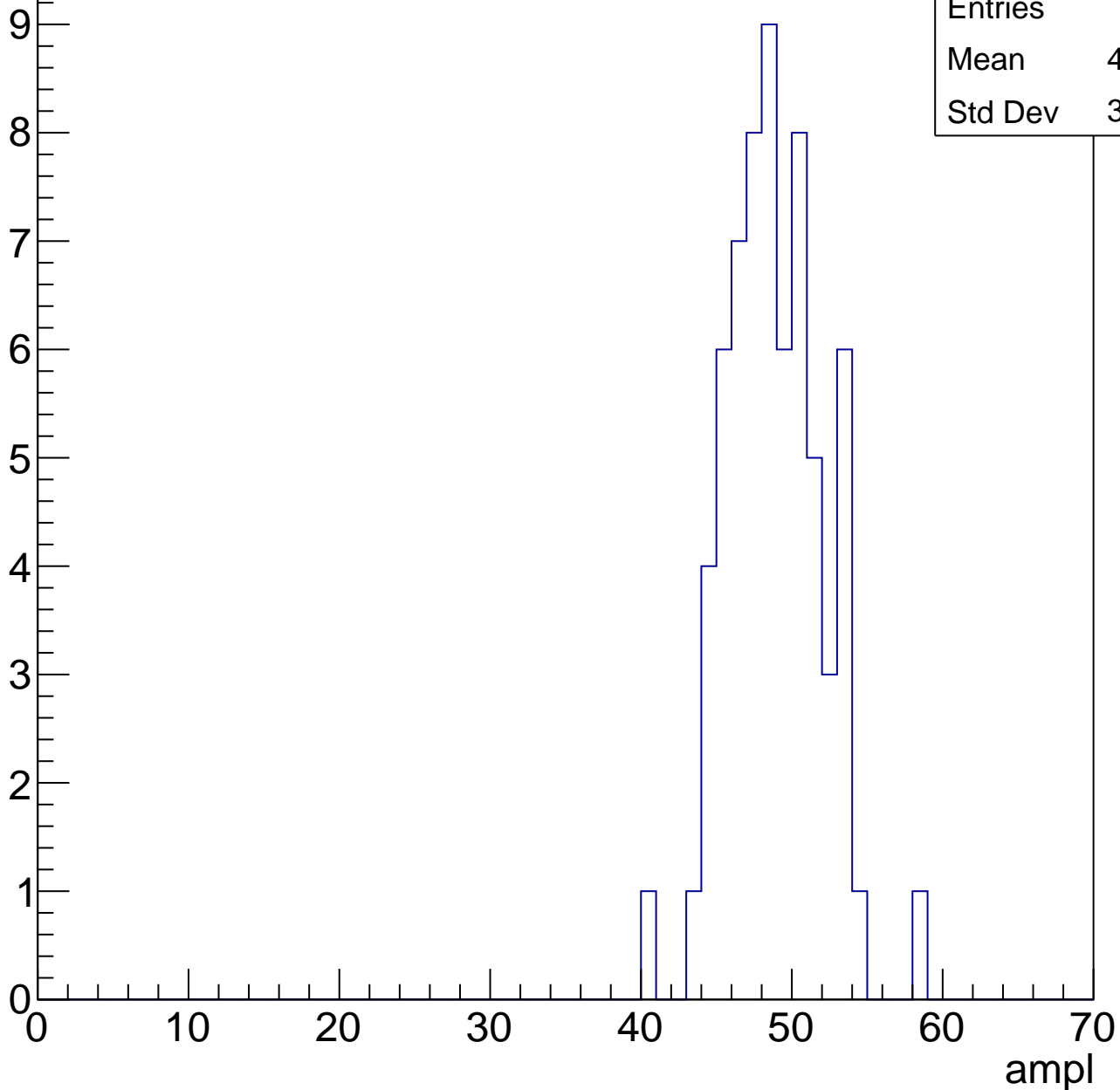


B1L102S, U17-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

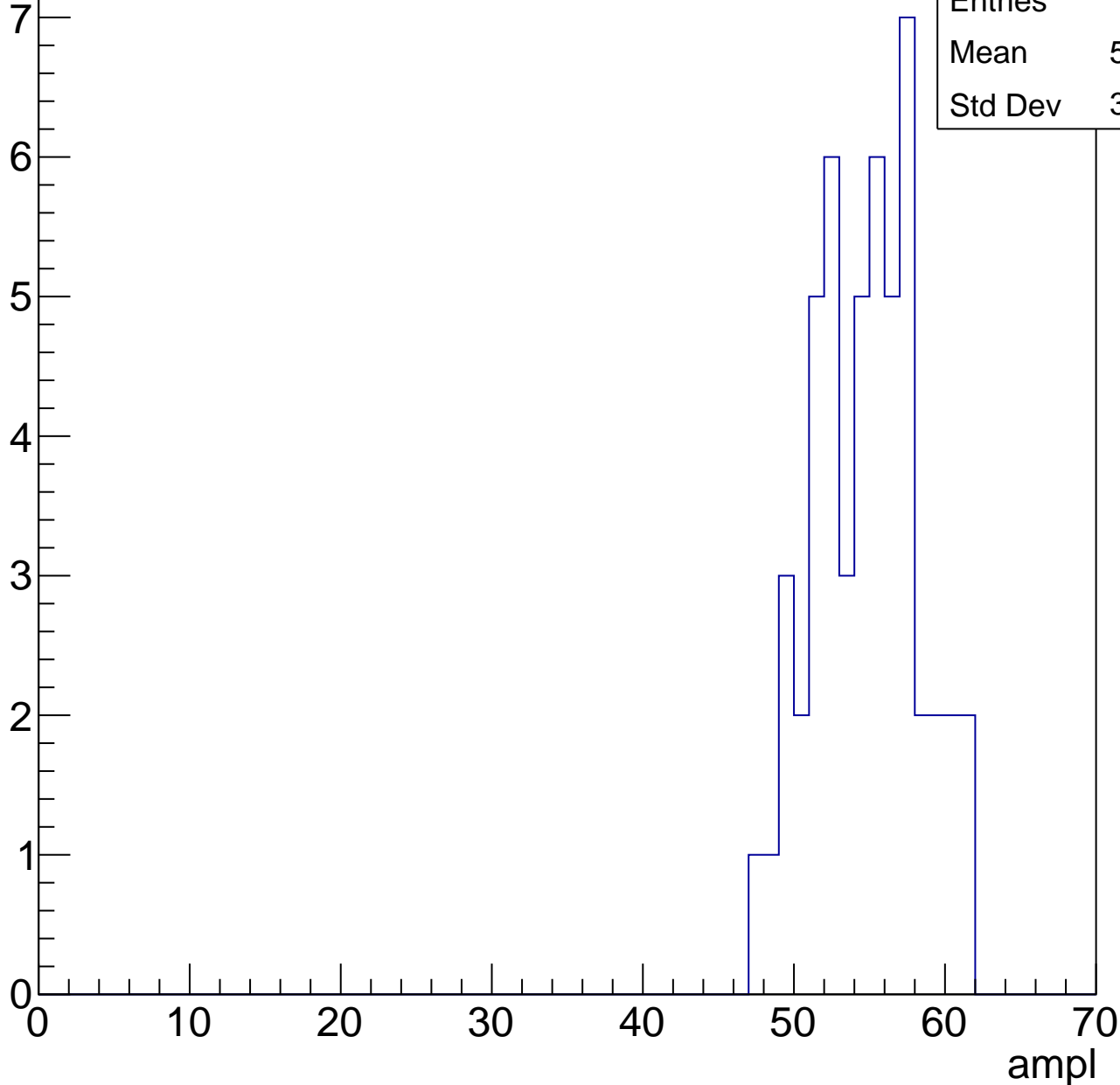
Entries	66
Mean	48.39
Std Dev	3.138



B1L102S, U17-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



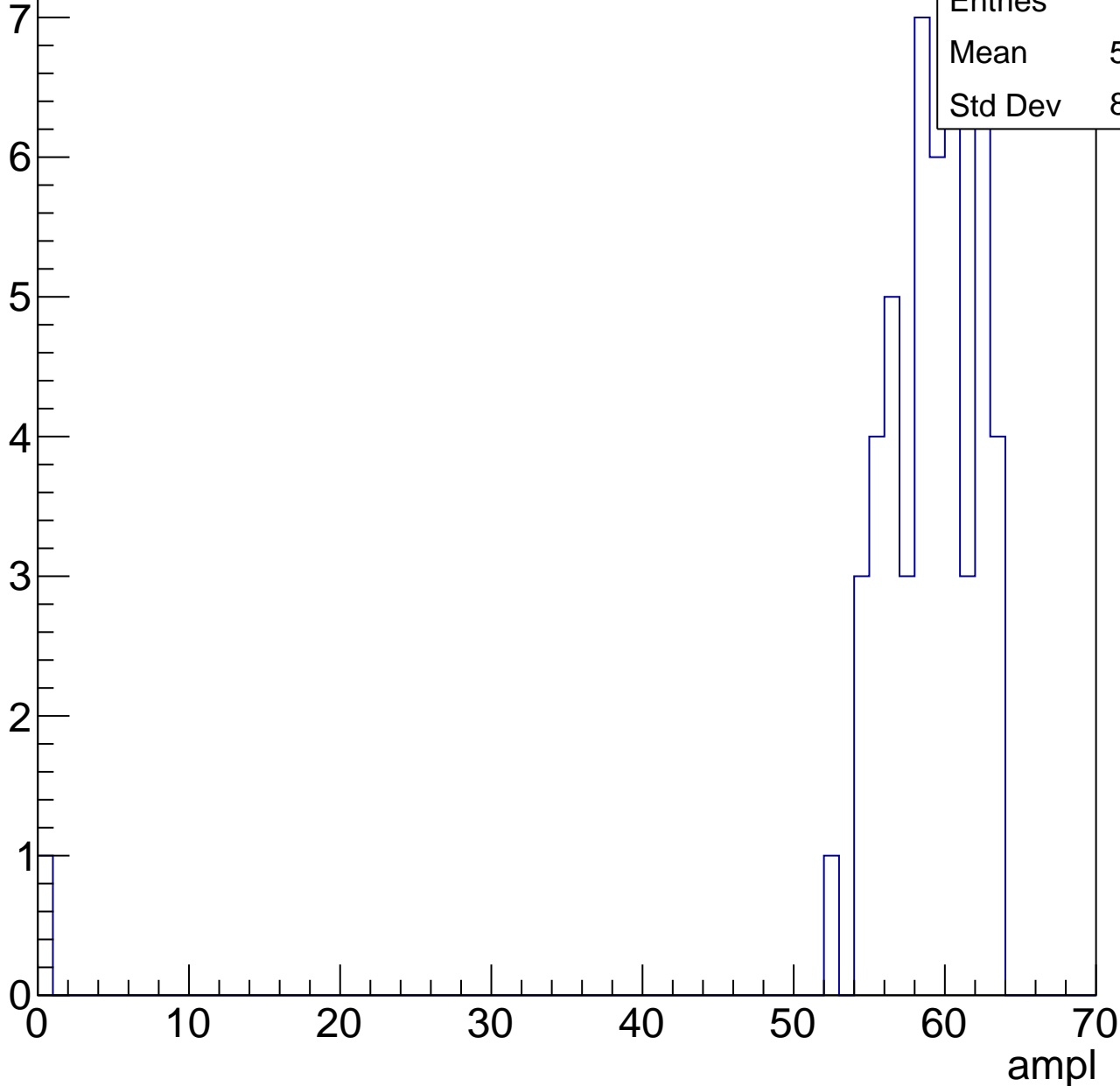
Entries	52
Mean	54.29
Std Dev	3.399

B1L102S, U17-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.53
Std Dev	8.596

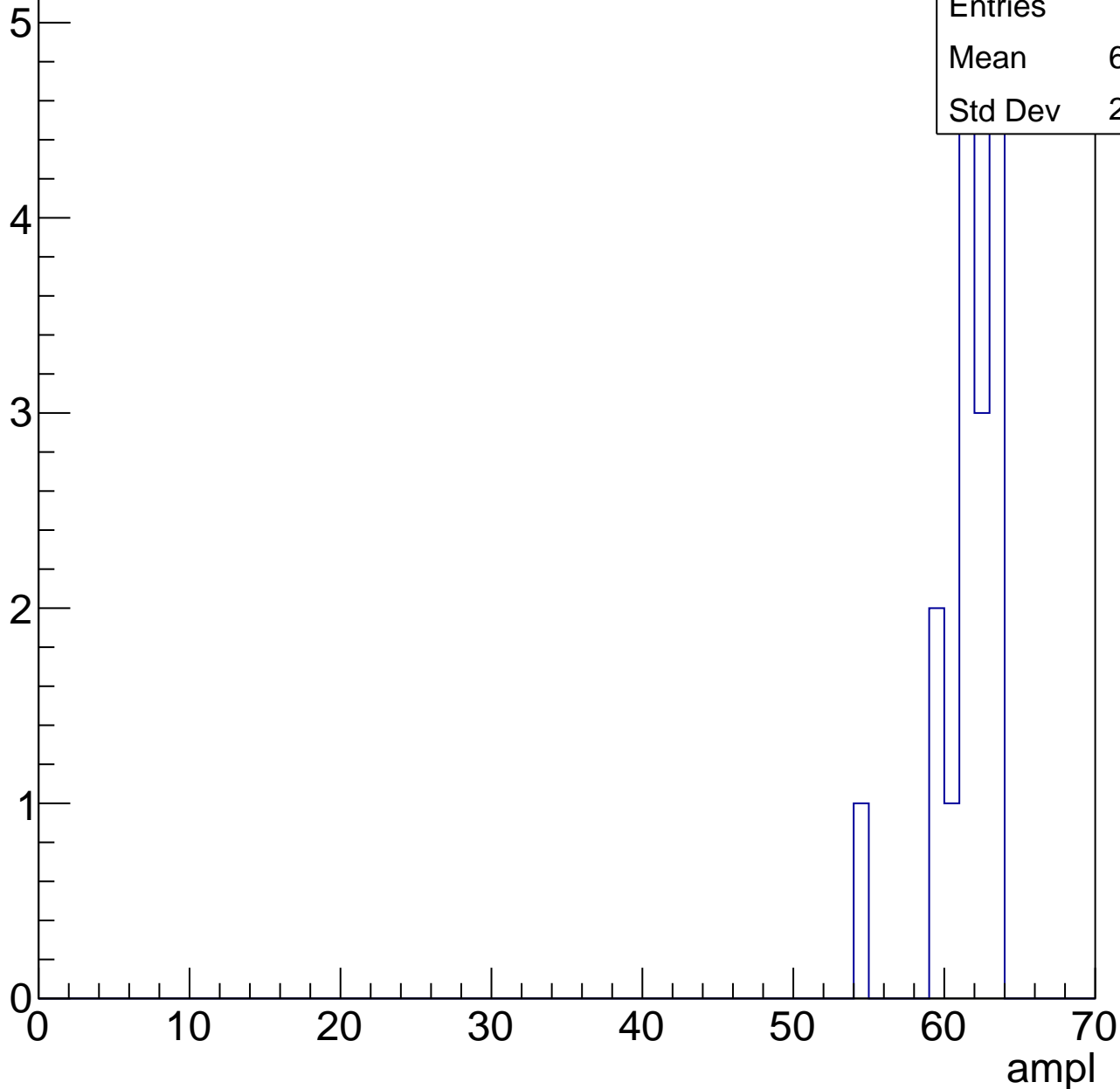


B1L102S, U17-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.06
Std Dev	2.182



B1L102S, U17-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch58, adc0

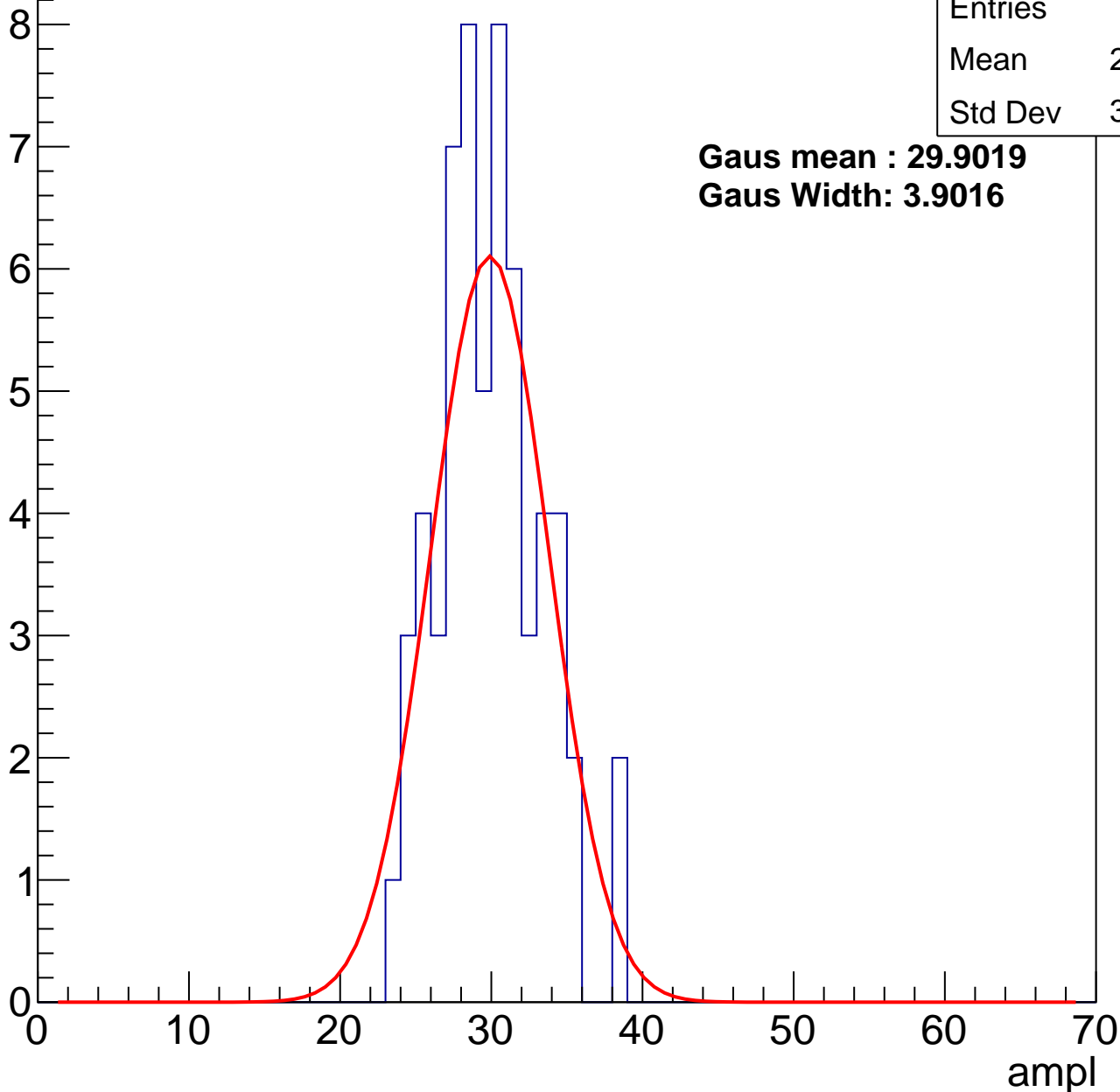
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.45
Std Dev	3.374

Gaus mean : 29.9019

Gaus Width: 3.9016



B1L102S, U17-ch58, adc1

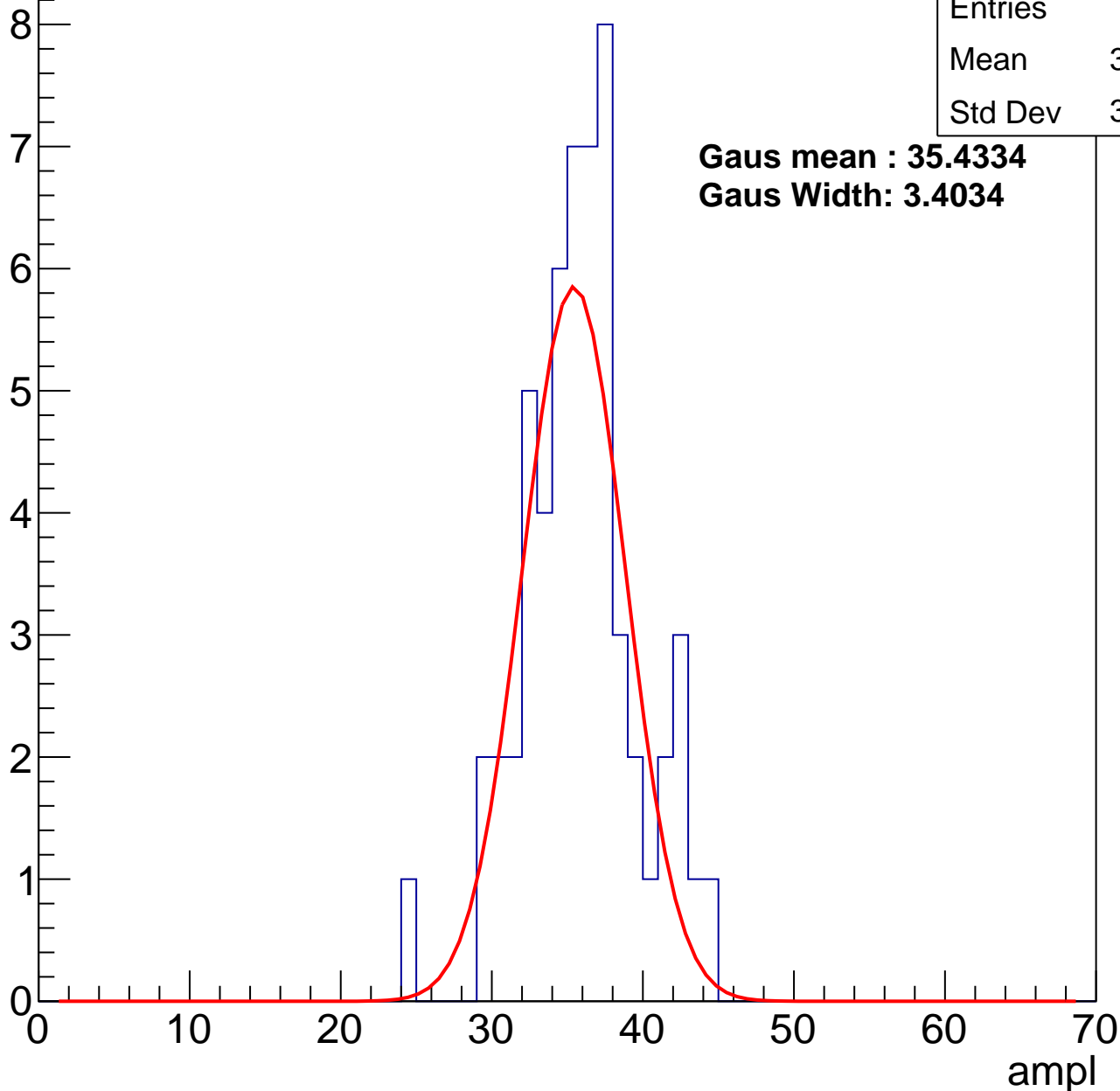
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.44
Std Dev	3.788

Gaus mean : 35.4334

Gaus Width: 3.4034



B1L102S, U17-ch58, adc2

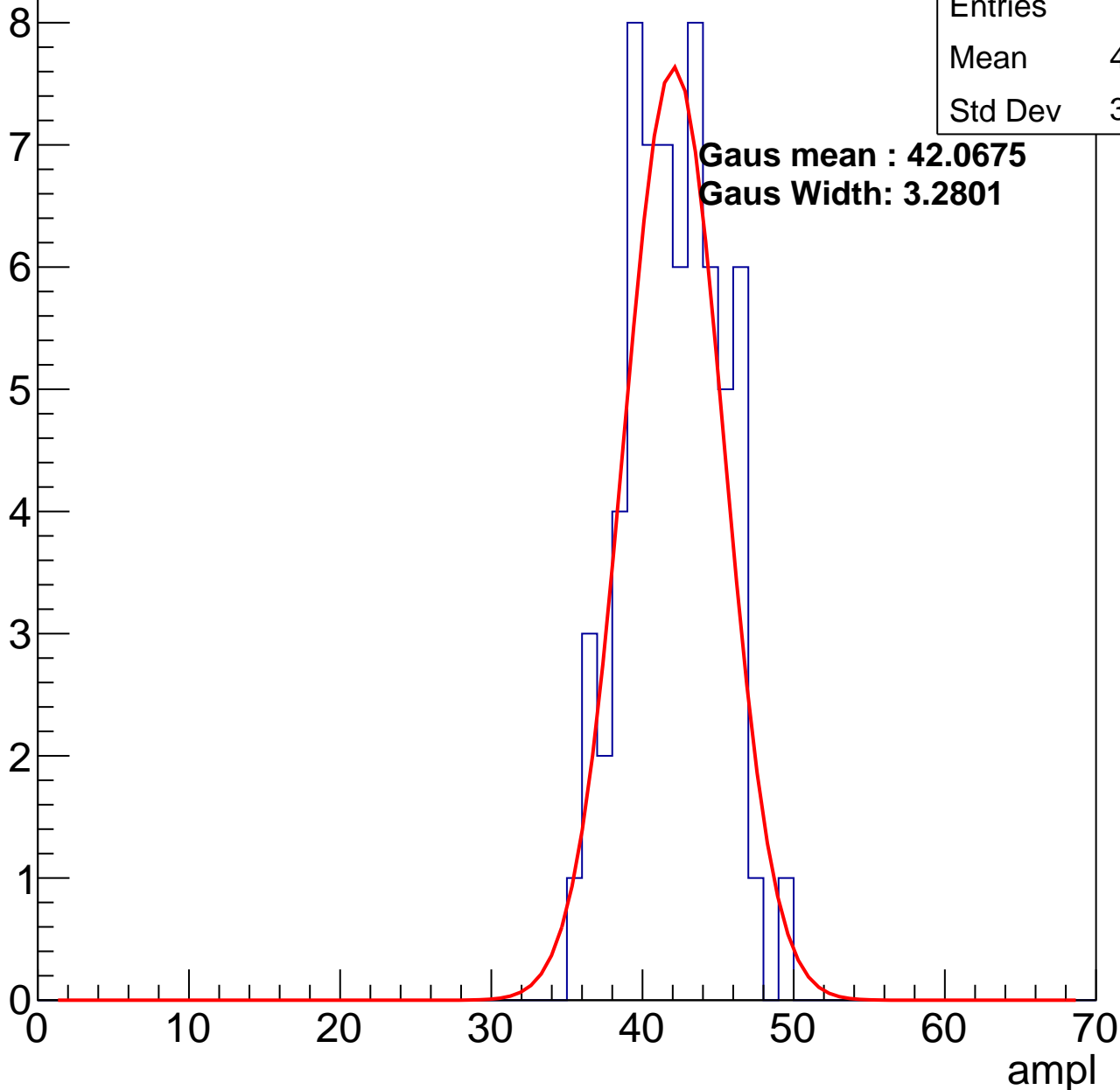
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.62
Std Dev	3.077

Gaus mean : 42.0675

Gaus Width: 3.2801

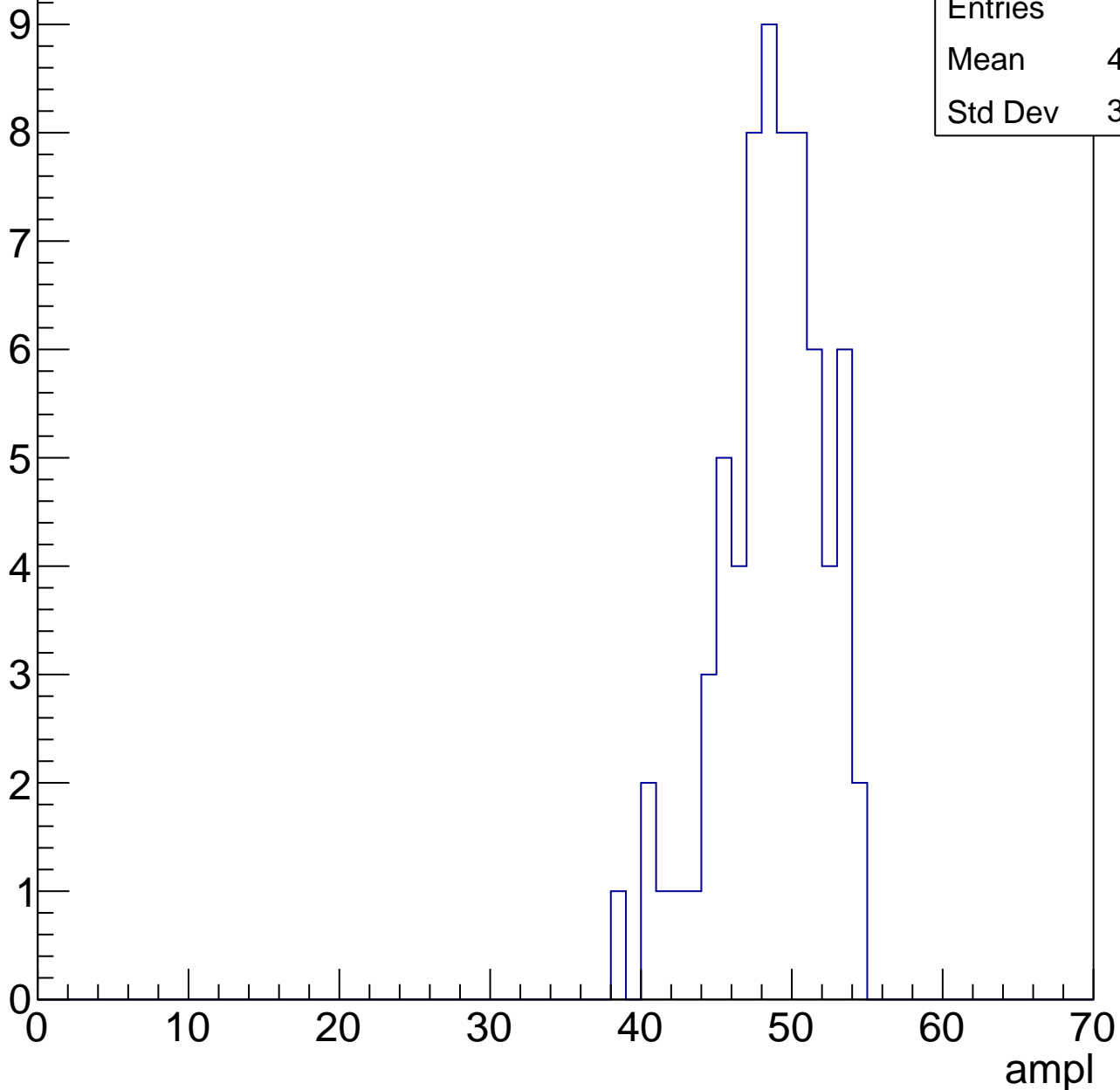


B1L102S, U17-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.19
Std Dev	3.474

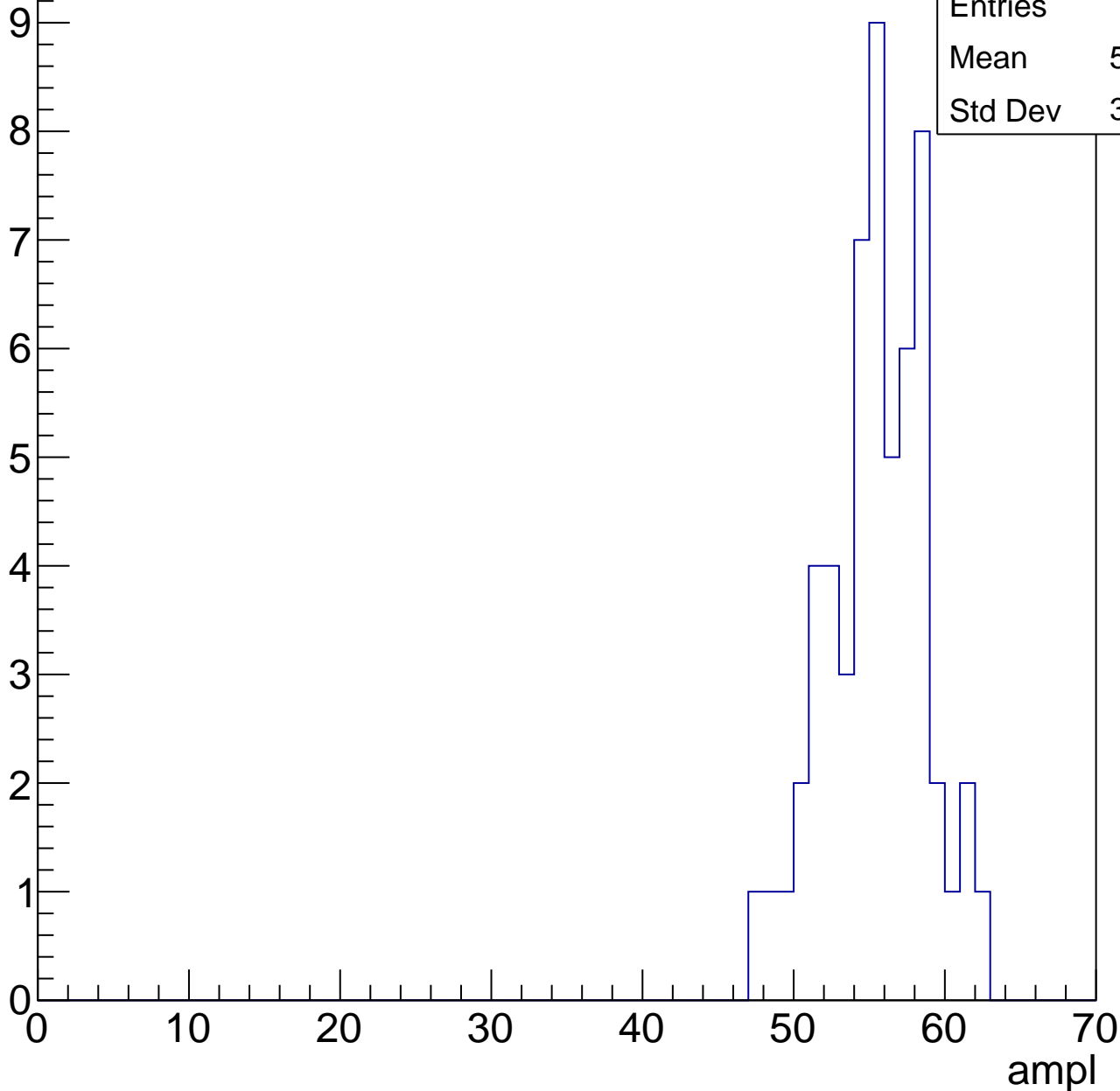


B1L102S, U17-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

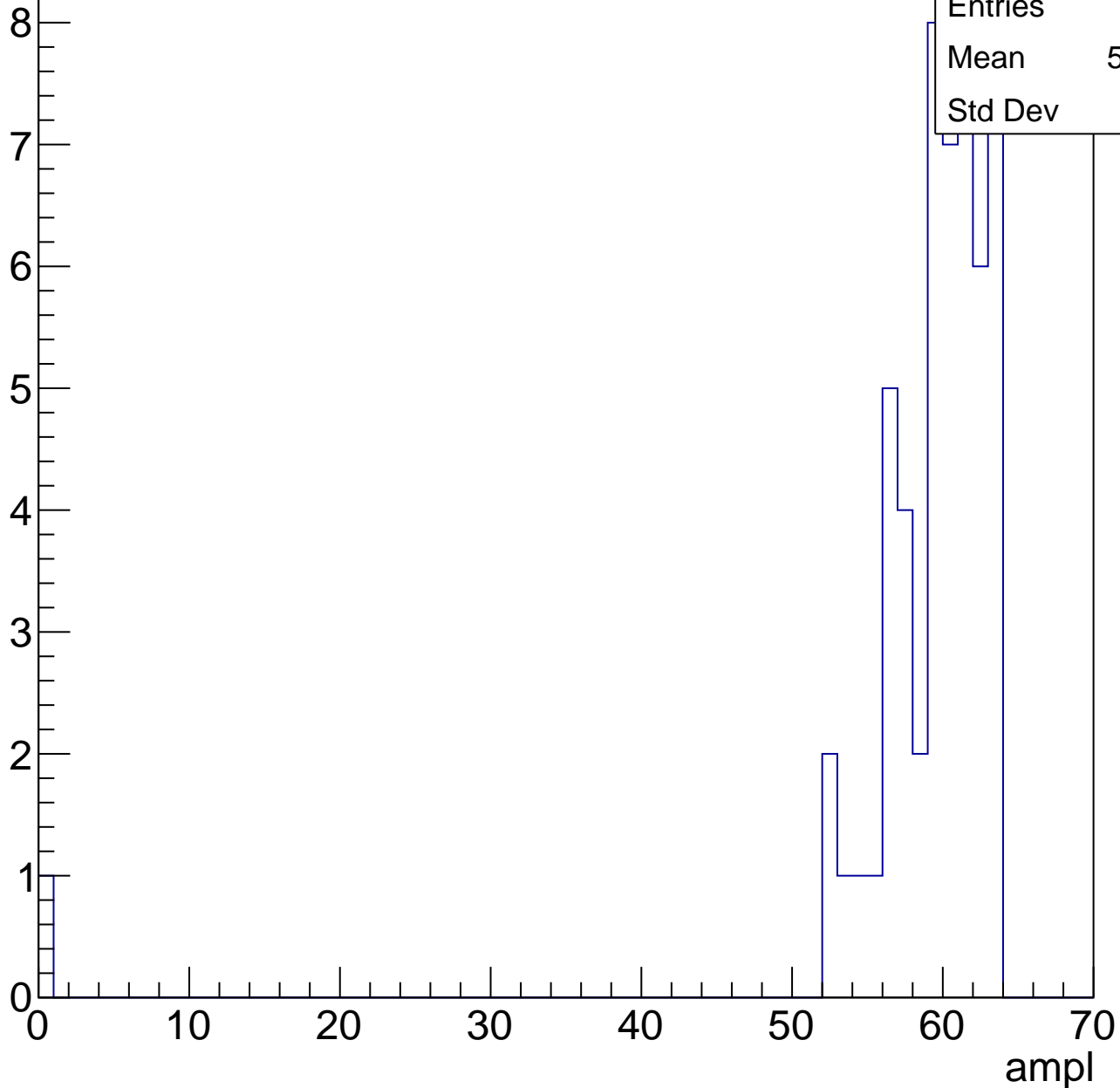
Entries	57
Mean	55.02
Std Dev	3.236



B1L102S, U17-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

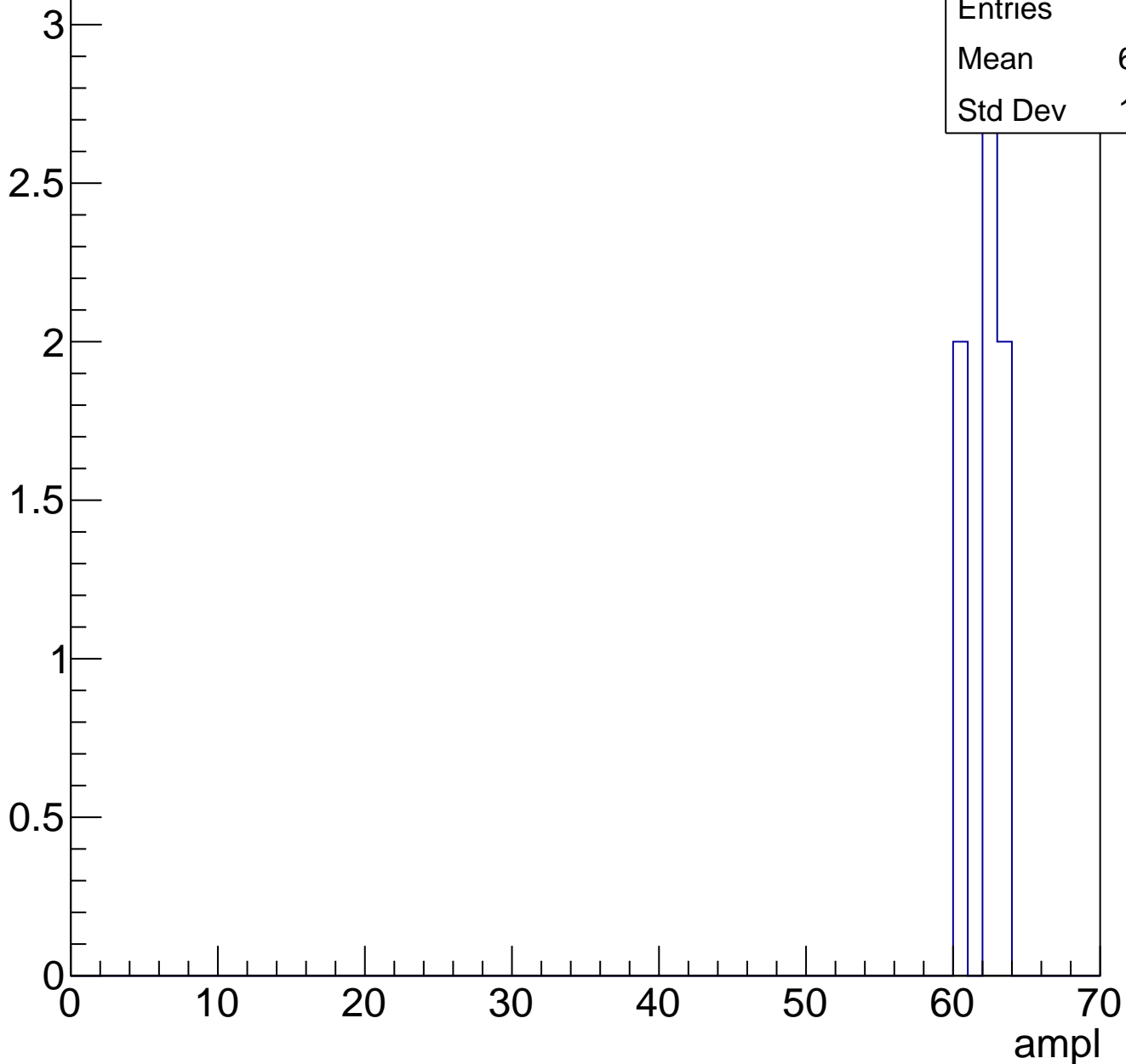
Entry



B1L102S, U17-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch59, adc0

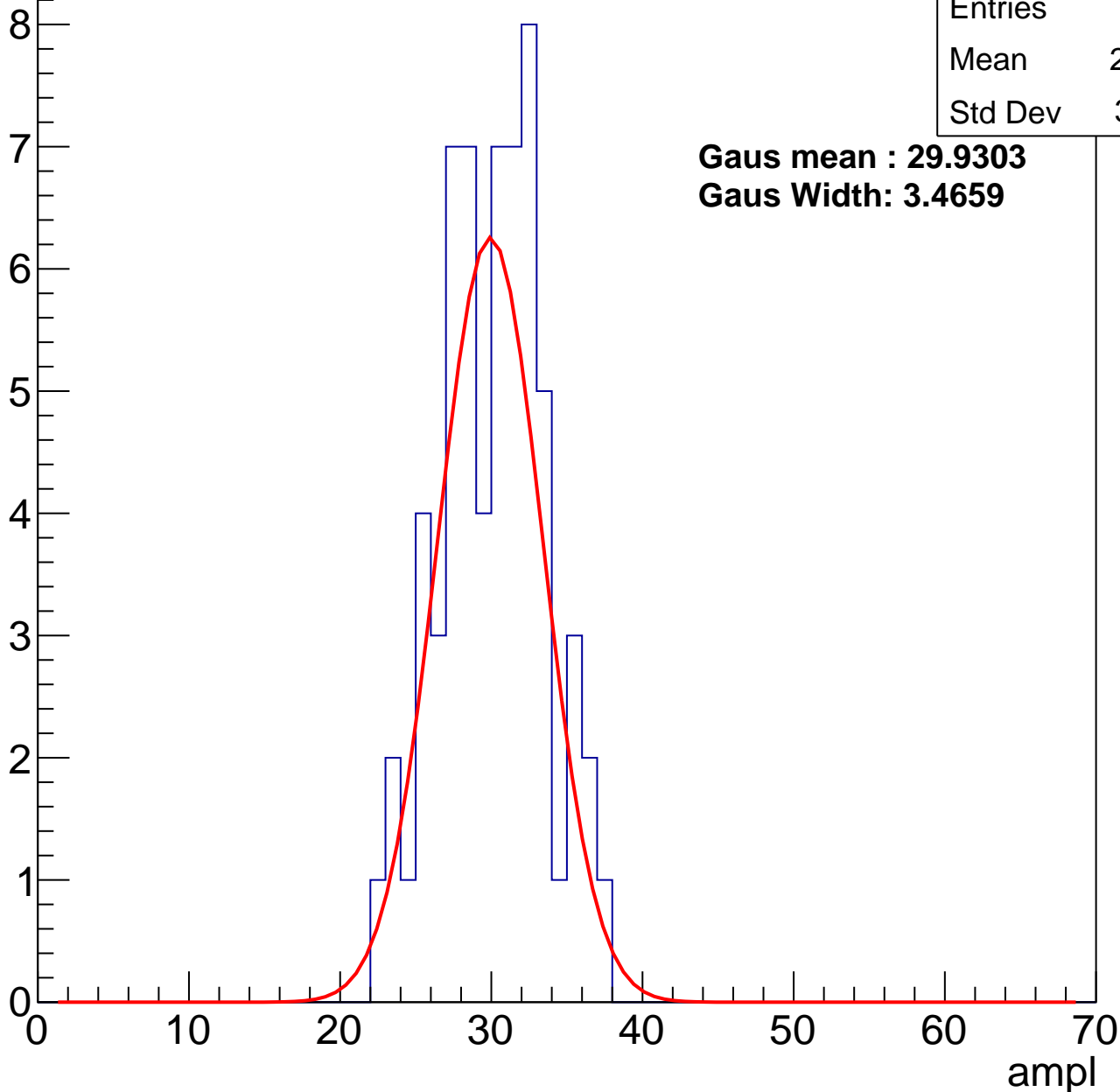
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.63
Std Dev	3.401

Gaus mean : 29.9303

Gaus Width: 3.4659



B1L102S, U17-ch59, adc1

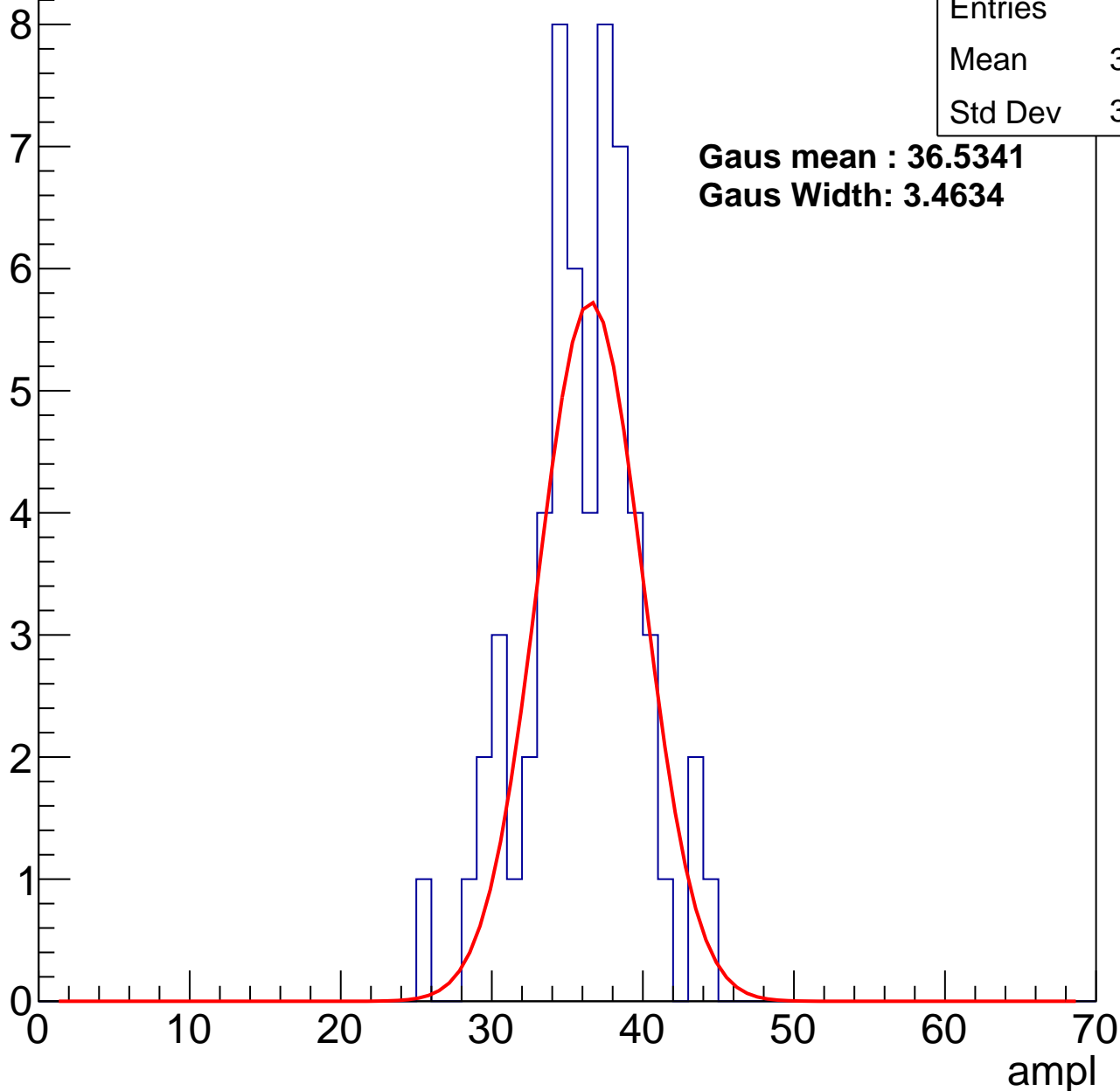
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.57
Std Dev	3.756

Gaus mean : 36.5341

Gaus Width: 3.4634



B1L102S, U17-ch59, adc2

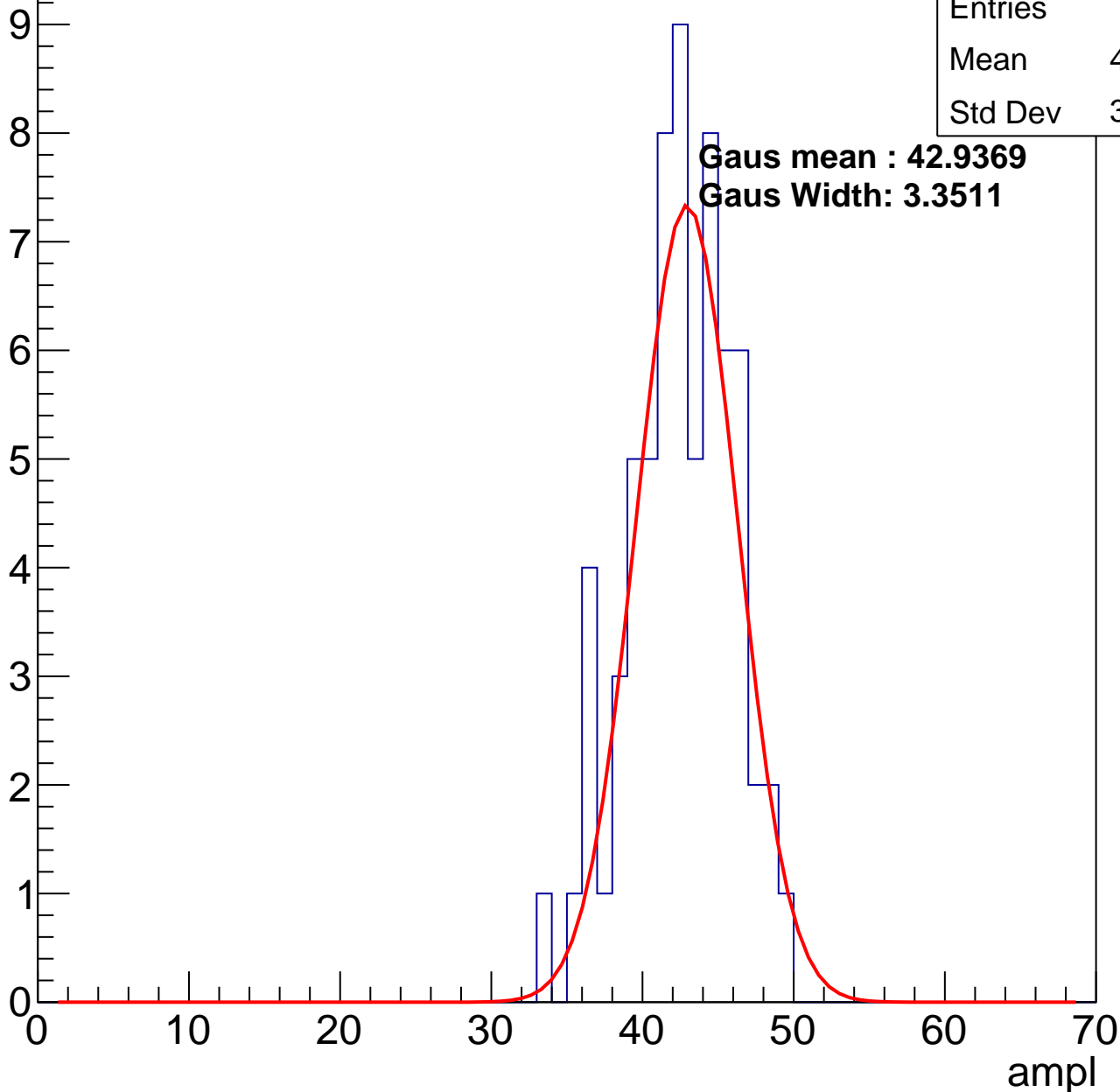
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.03
Std Dev	3.407

Gaus mean : 42.9369

Gaus Width: 3.3511

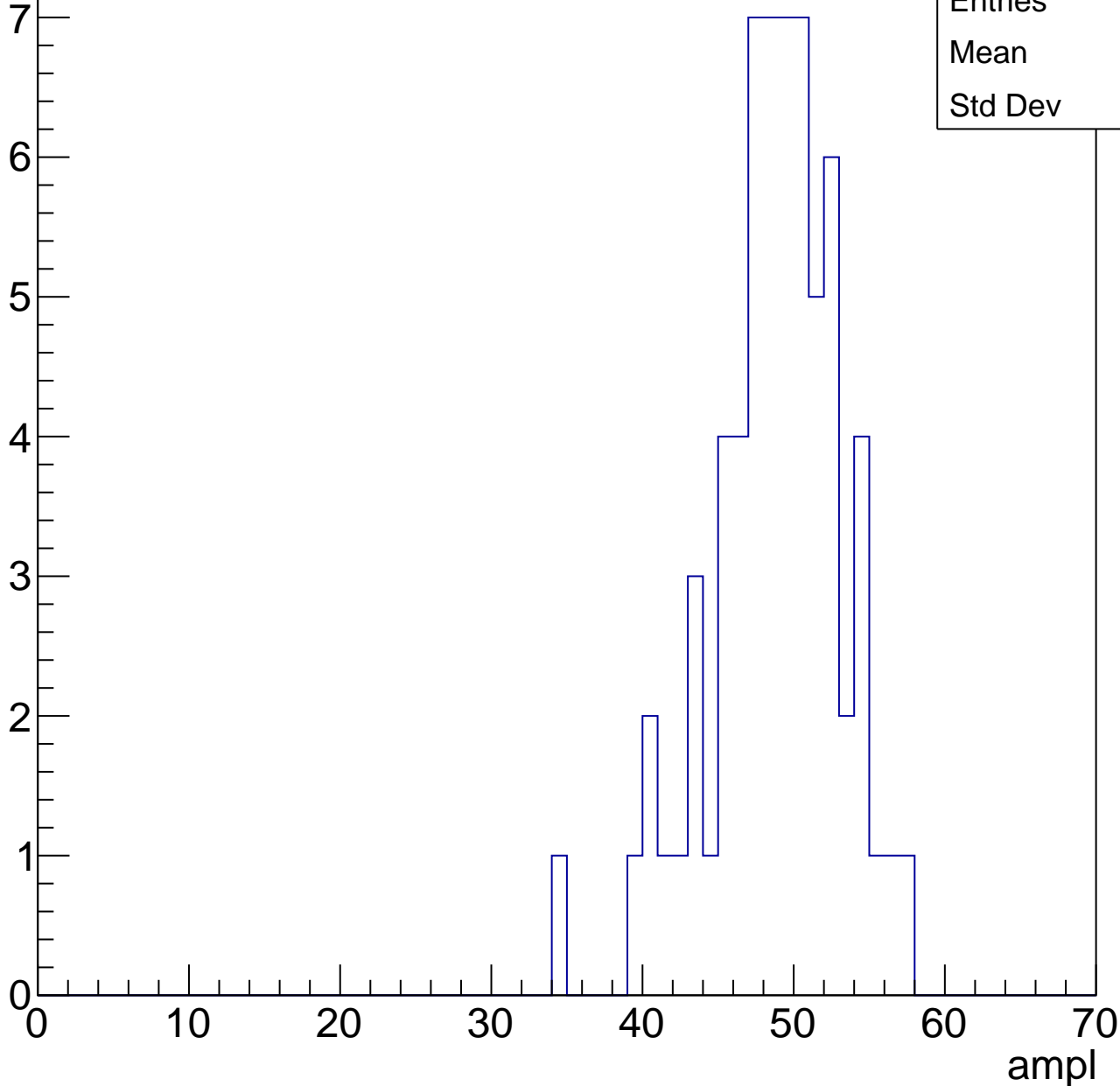


B1L102S, U17-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.3
Std Dev	4.26

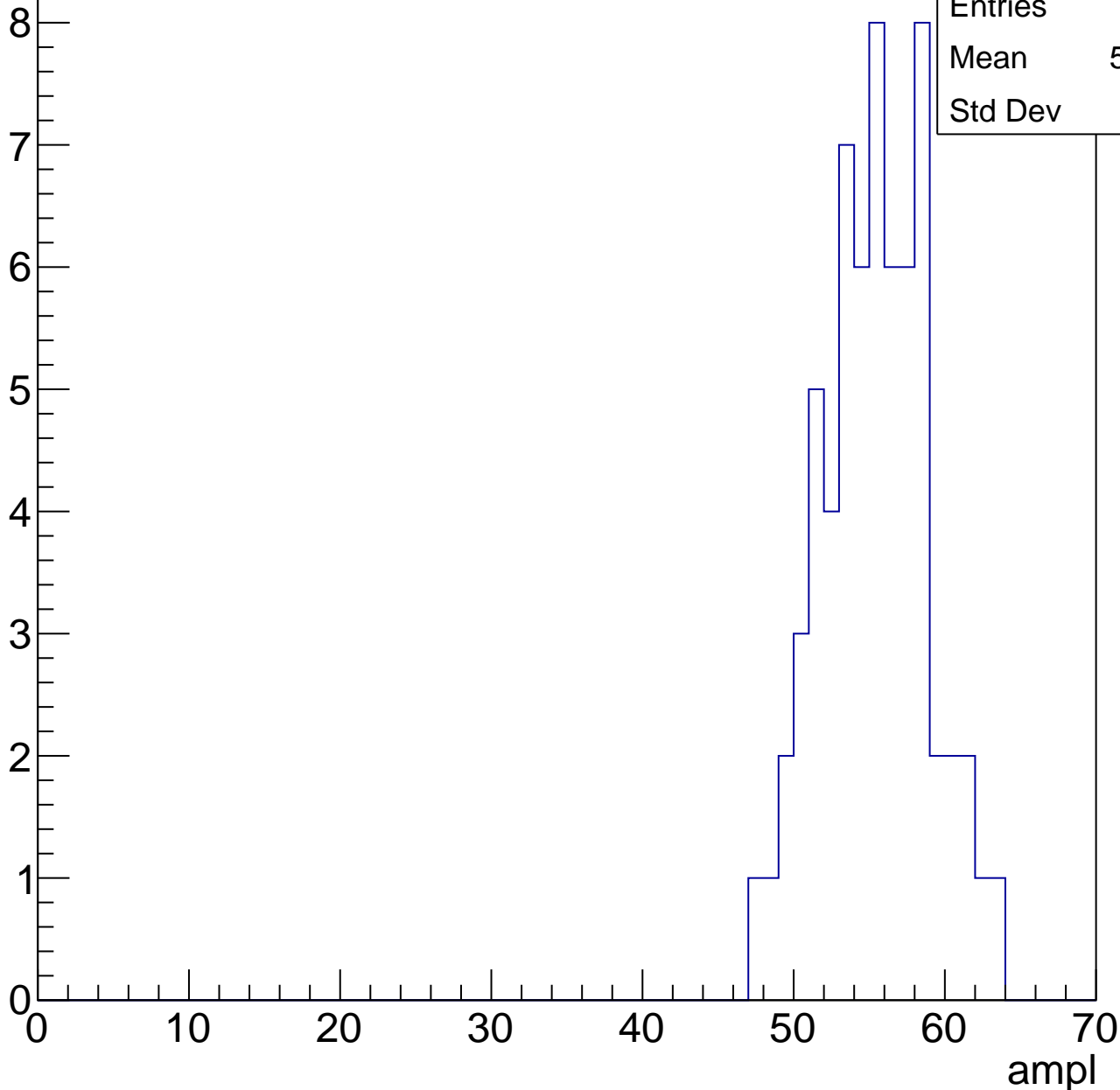


B1L102S, U17-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.89
Std Dev	3.46



B1L102S, U17-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	59.85
Std Dev	2.475

ampl

0

10

20

30

40

50

60

70

B1L102S, U17-ch59, adc6

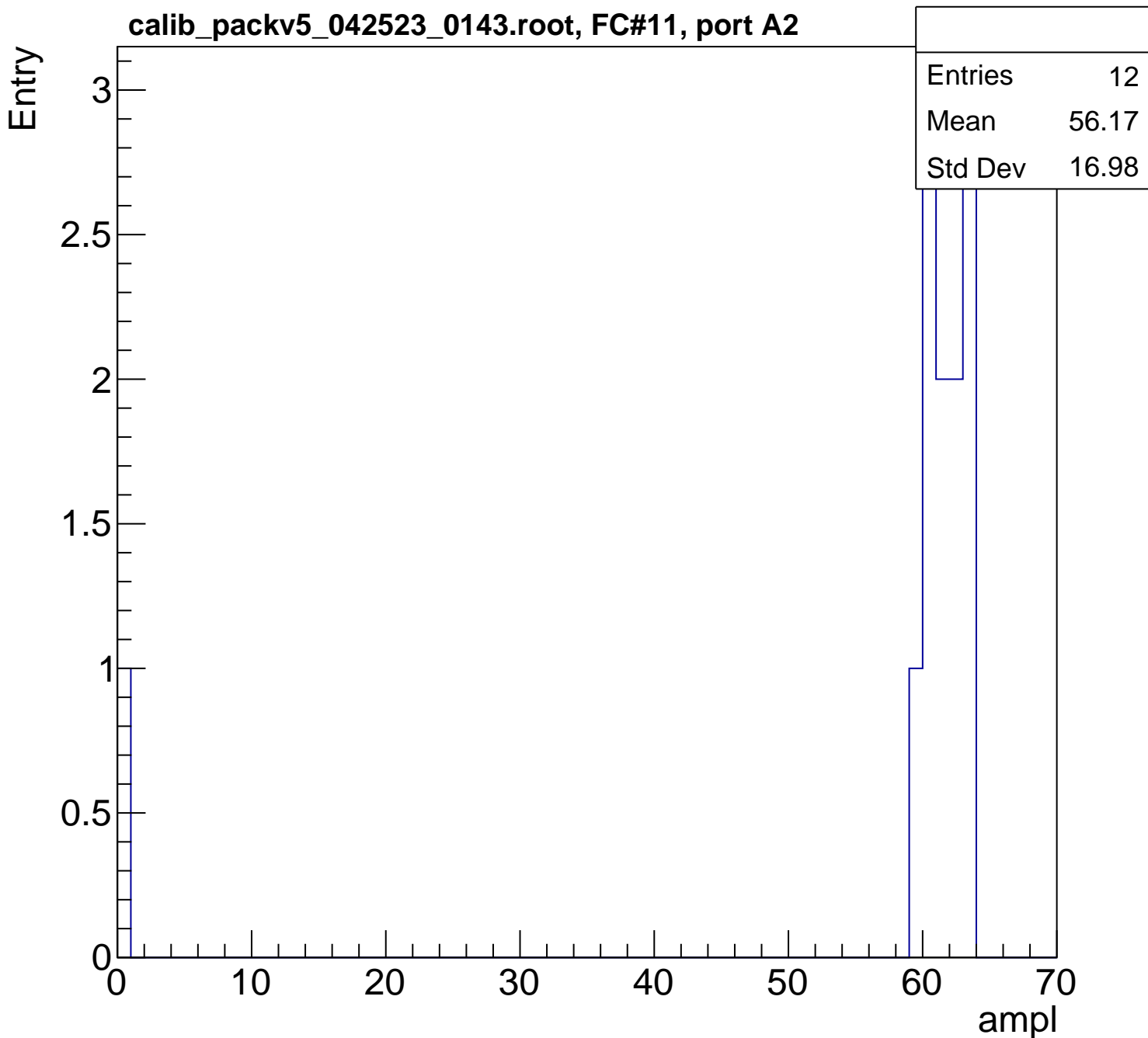
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	12
Mean	56.17
Std Dev	16.98

ampl



B1L102S, U17-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L102S, U17-ch60, adc0

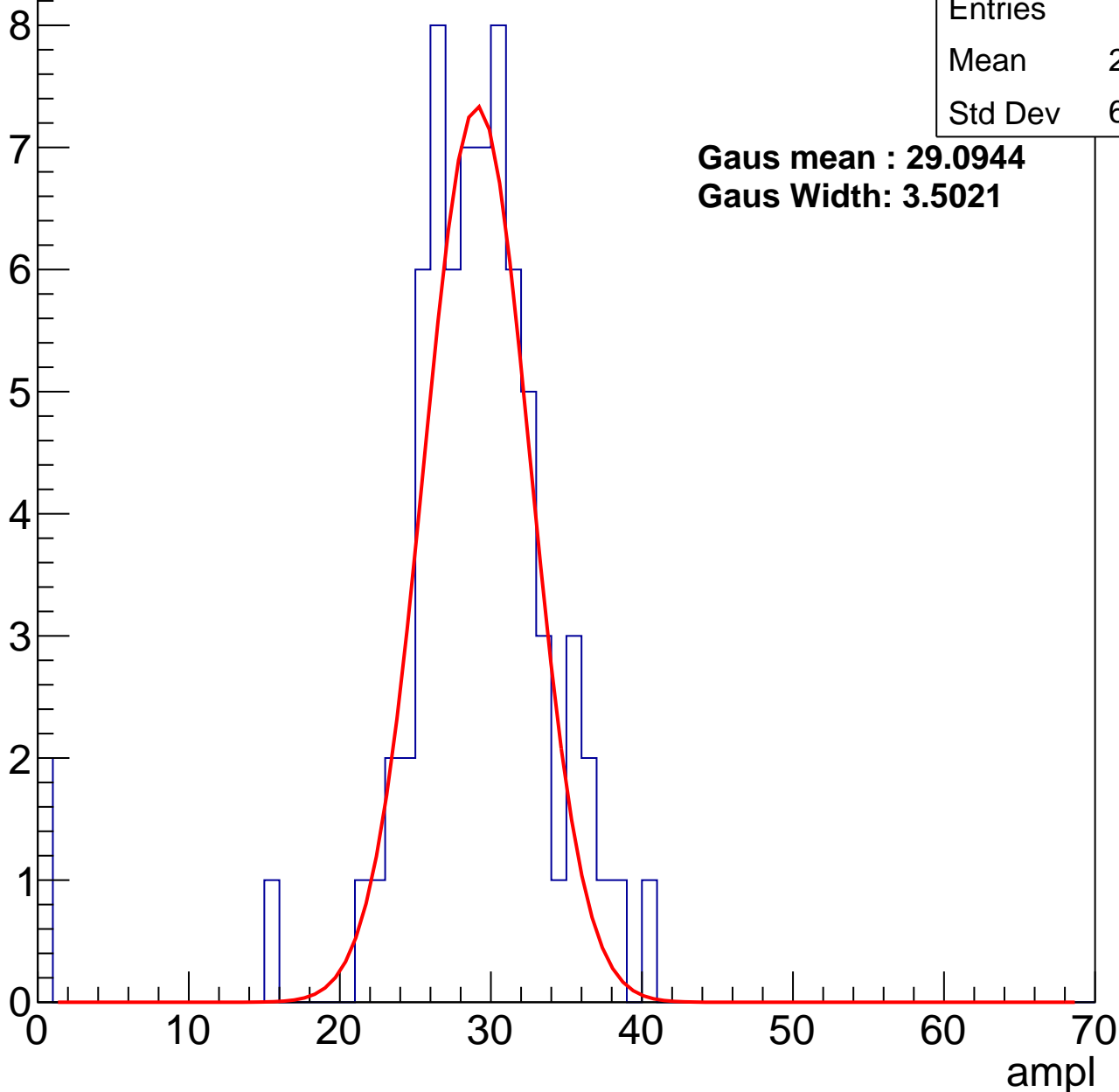
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.14
Std Dev	6.237

Gaus mean : 29.0944

Gaus Width: 3.5021



B1L102S, U17-ch60, adc1

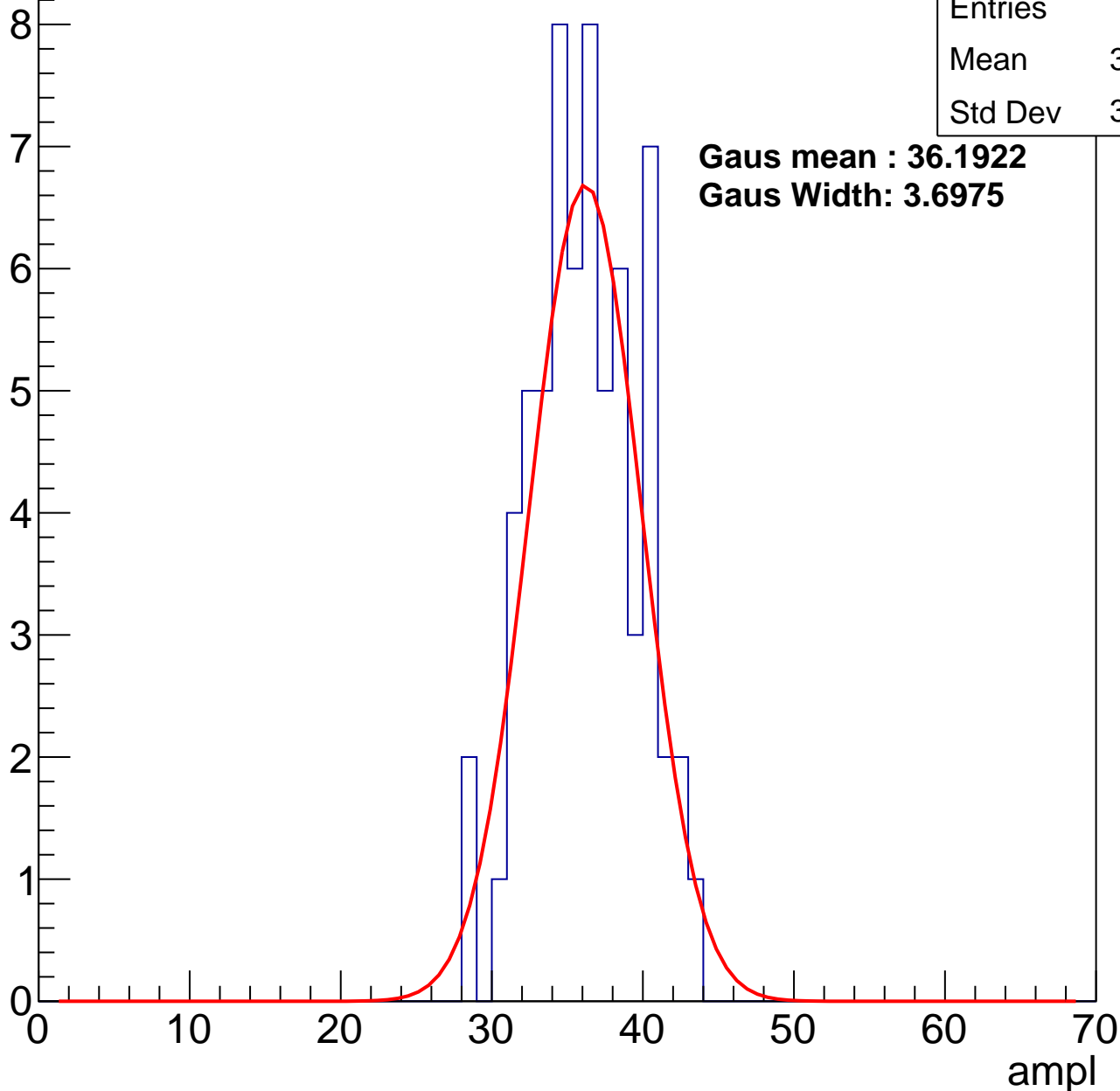
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.75
Std Dev	3.424

Gaus mean : 36.1922

Gaus Width: 3.6975



B1L102S, U17-ch60, adc2

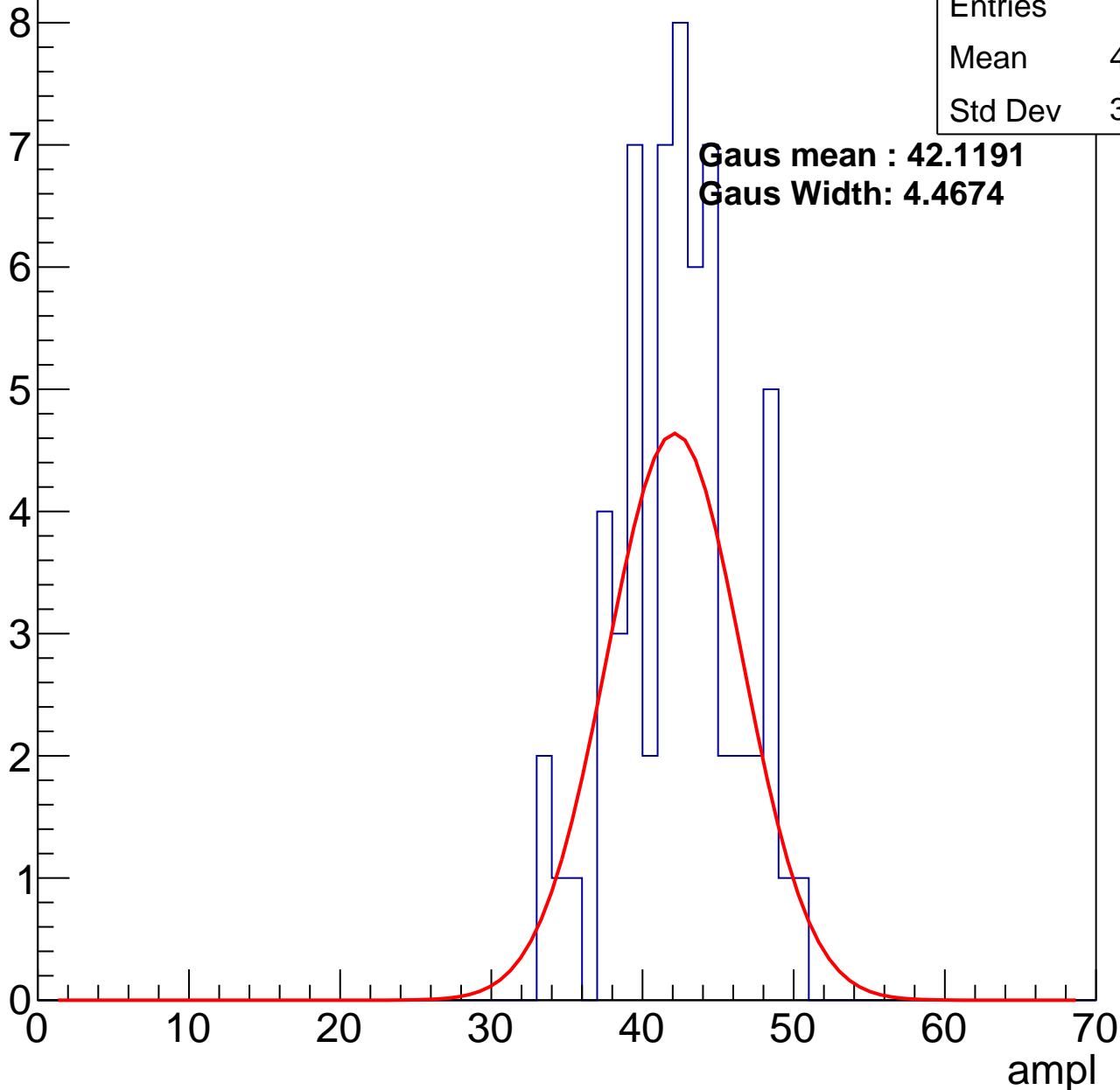
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.87
Std Dev	3.898

Gaus mean : 42.1191

Gaus Width: 4.4674

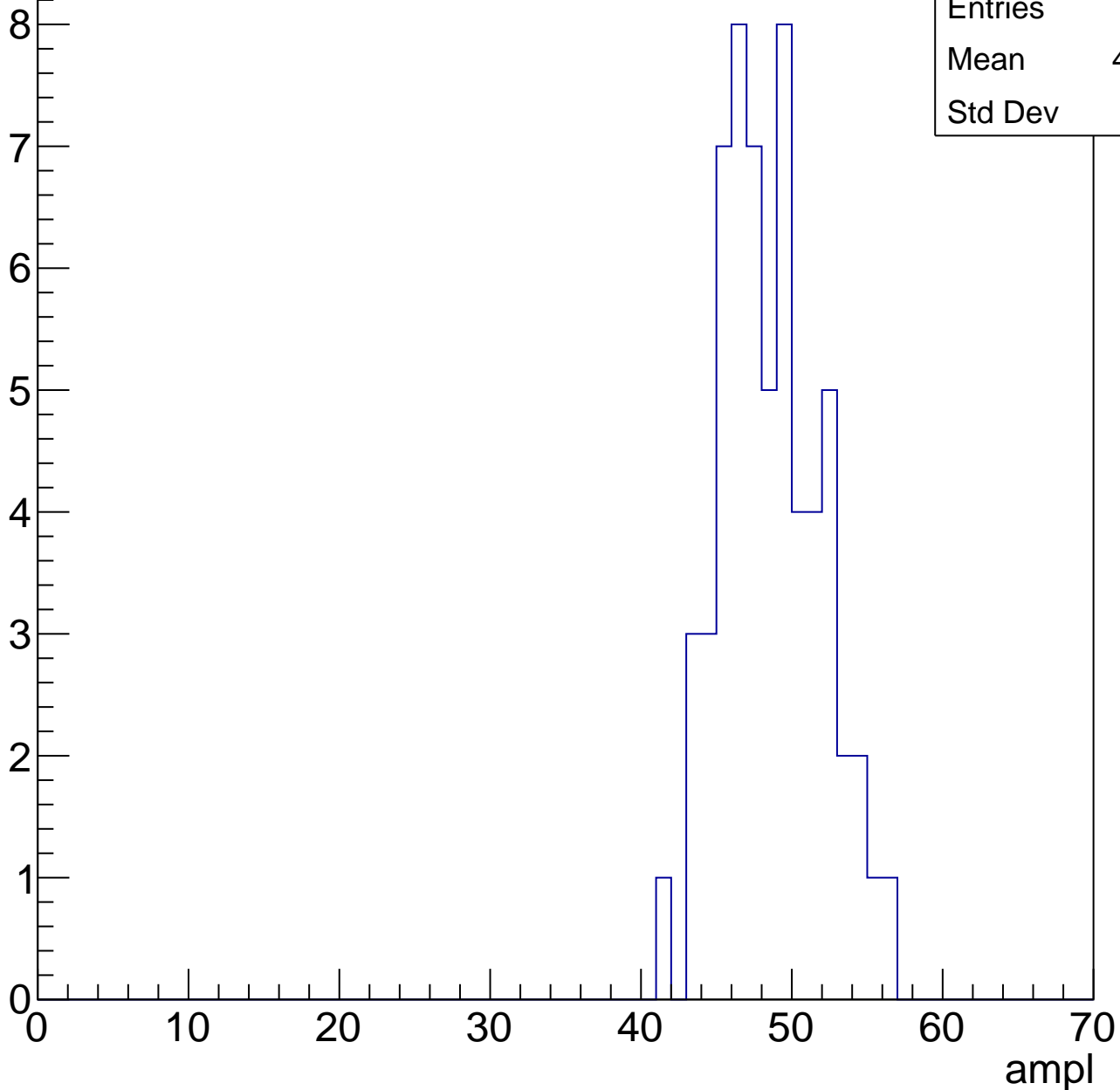


B1L102S, U17-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.11
Std Dev	3.26

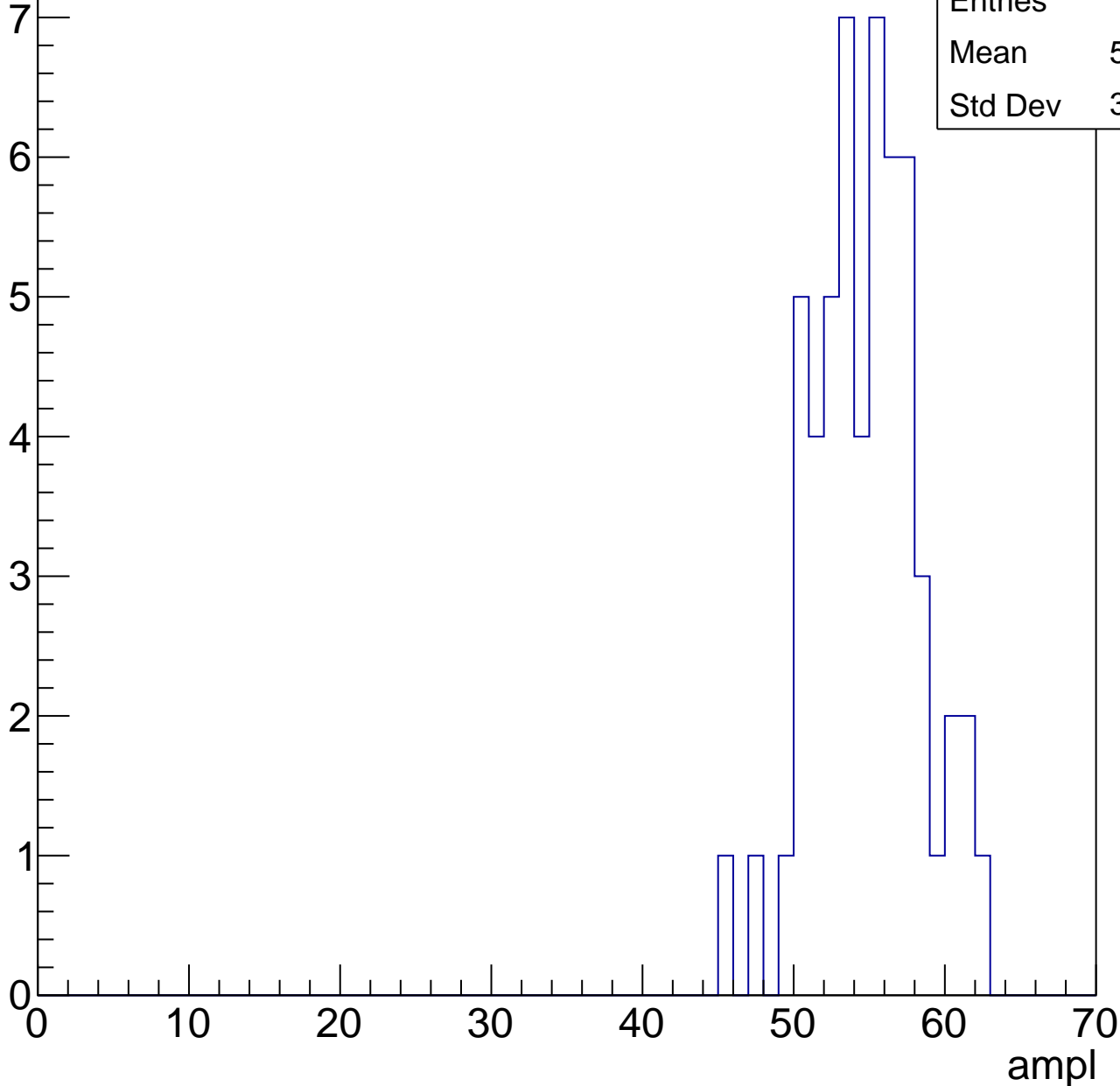


B1L102S, U17-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.32
Std Dev	3.495



B1L102S, U17-ch60, adc5

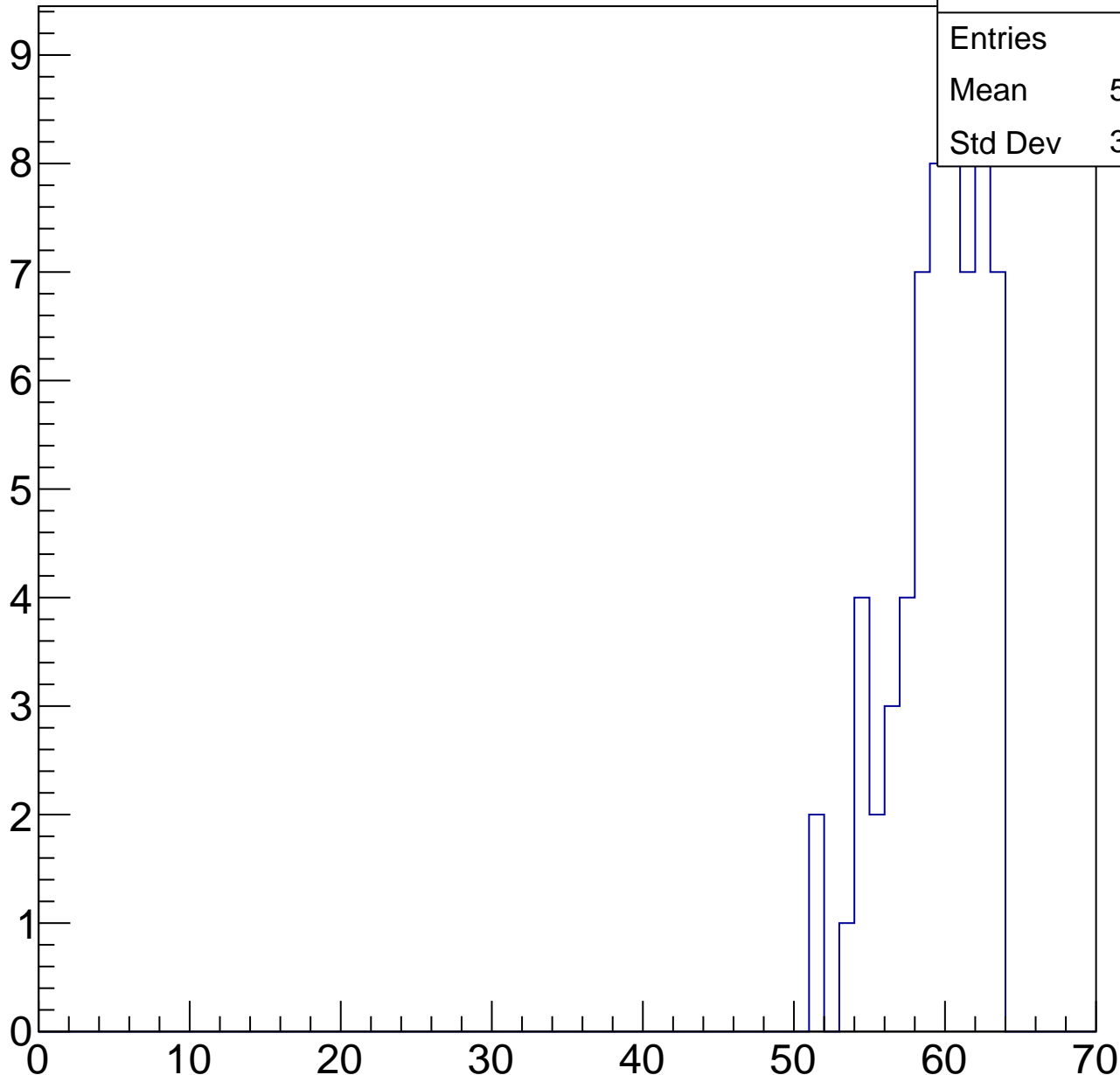
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	59.05
Std Dev	3.045

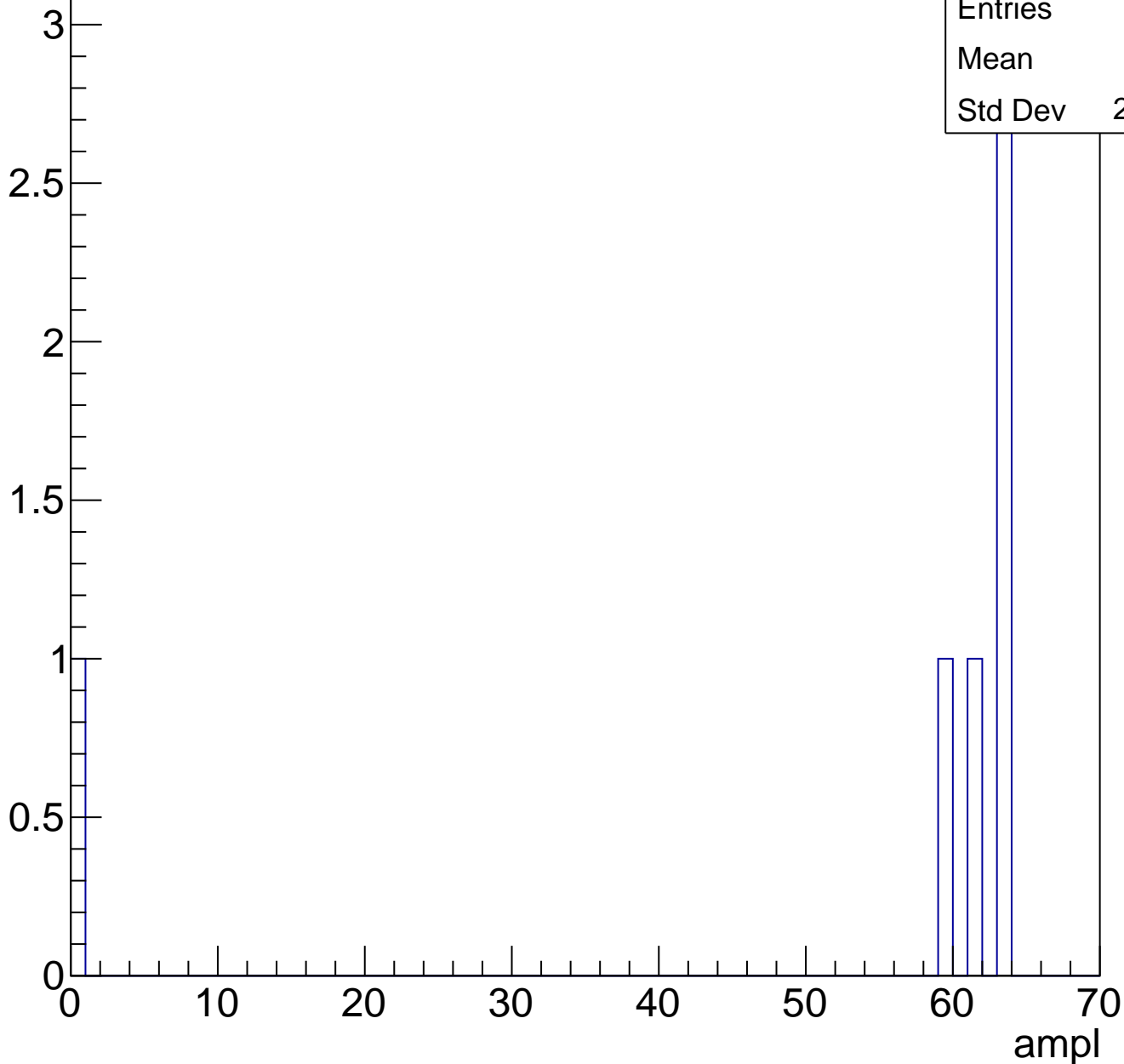
ampl



B1L102S, U17-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch61, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	26.92
Std Dev	5.761

Gaus mean : 28.4949

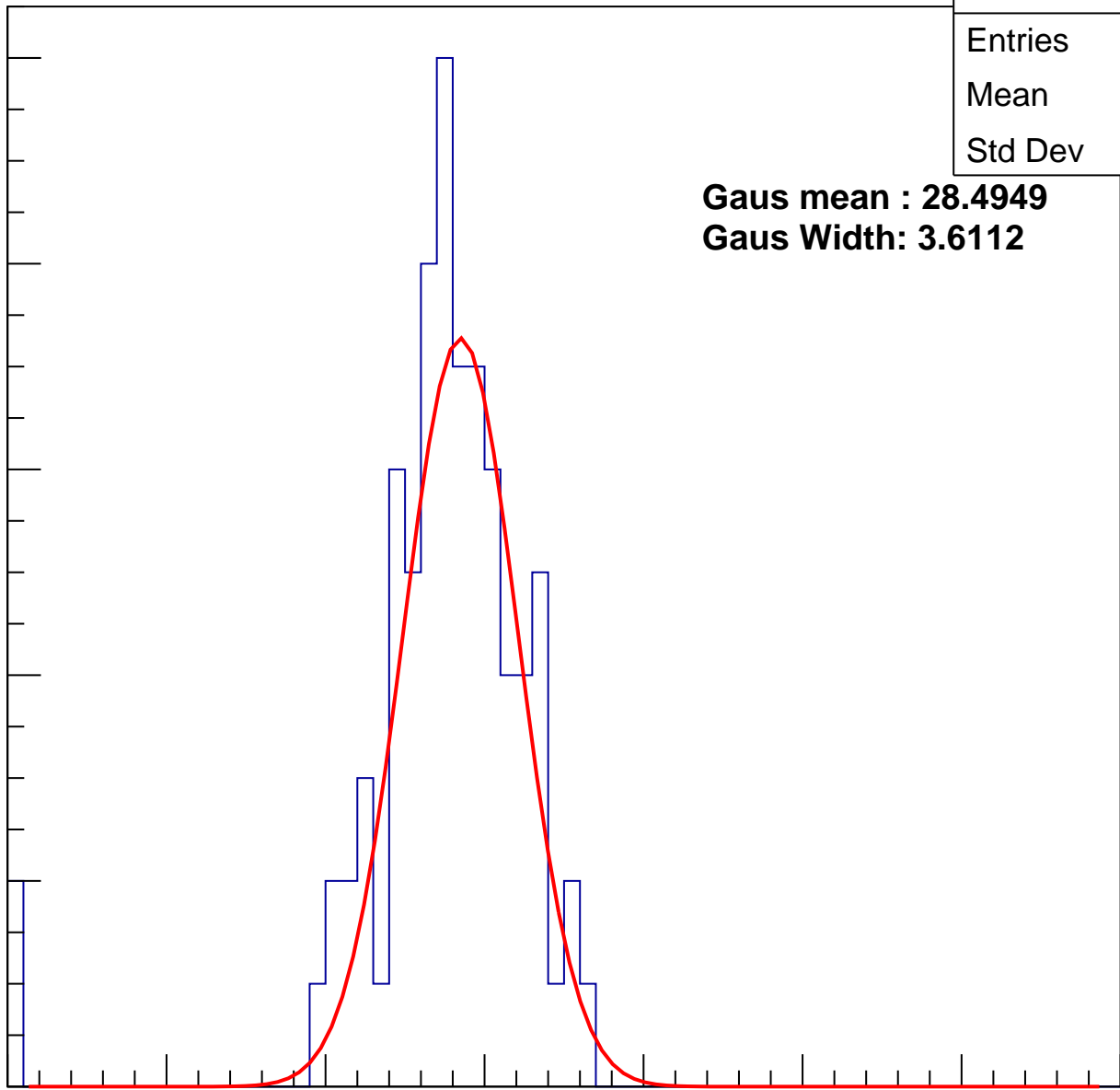
Gaus Width: 3.6112

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch61, adc1

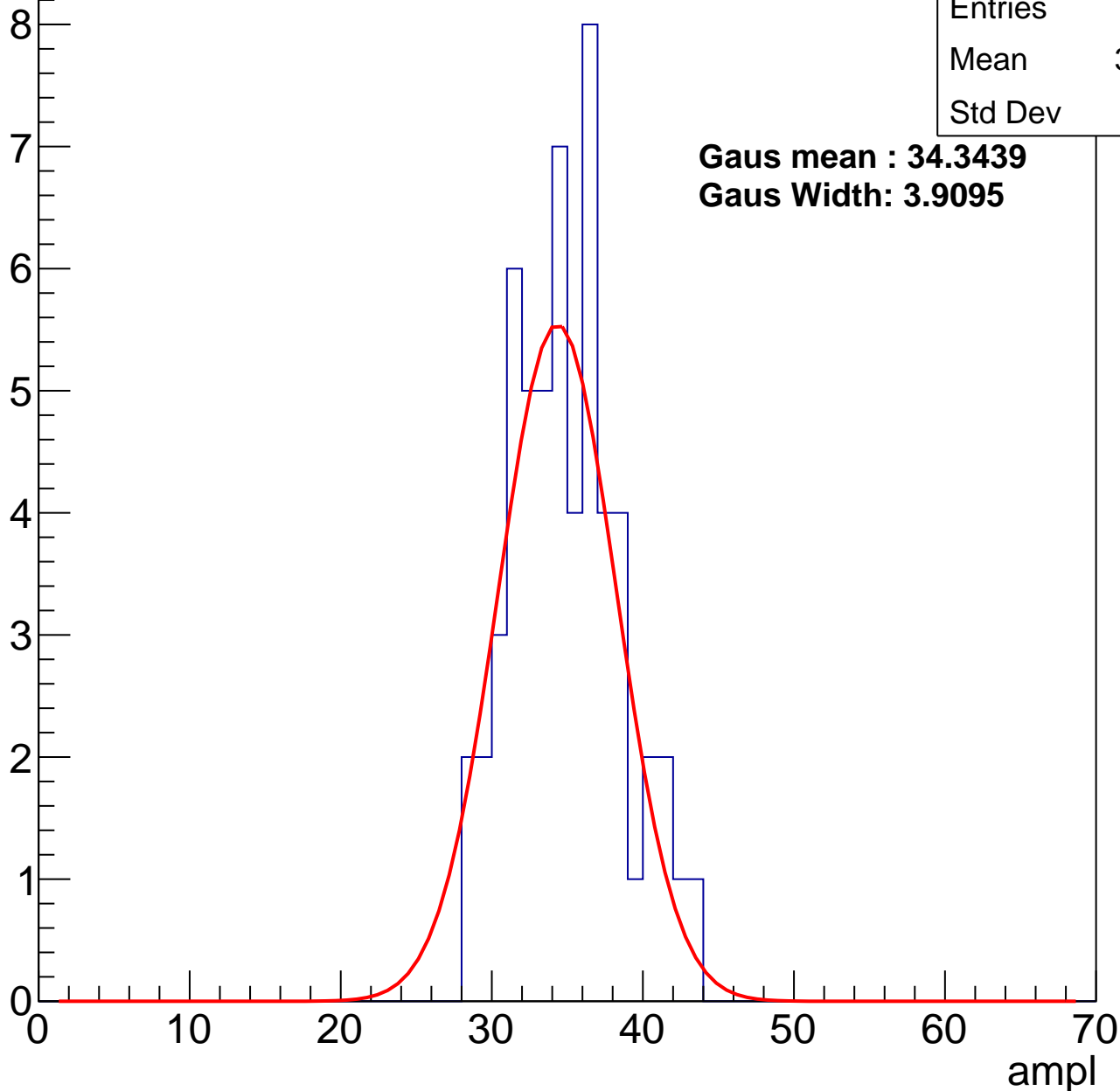
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.51
Std Dev	3.53

Gaus mean : 34.3439

Gaus Width: 3.9095



B1L102S, U17-ch61, adc2

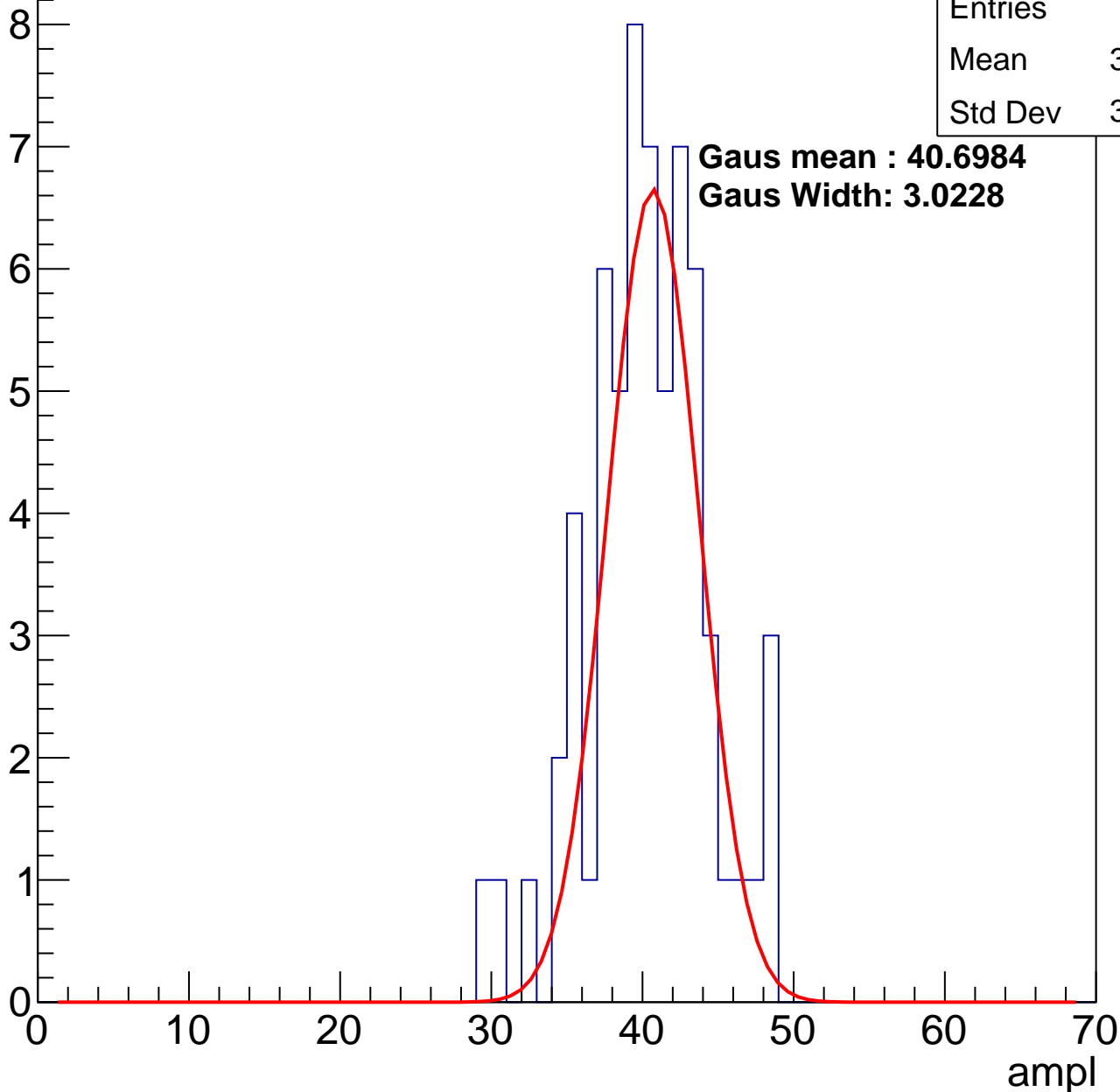
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	39.84
Std Dev	3.965

Gaus mean : 40.6984

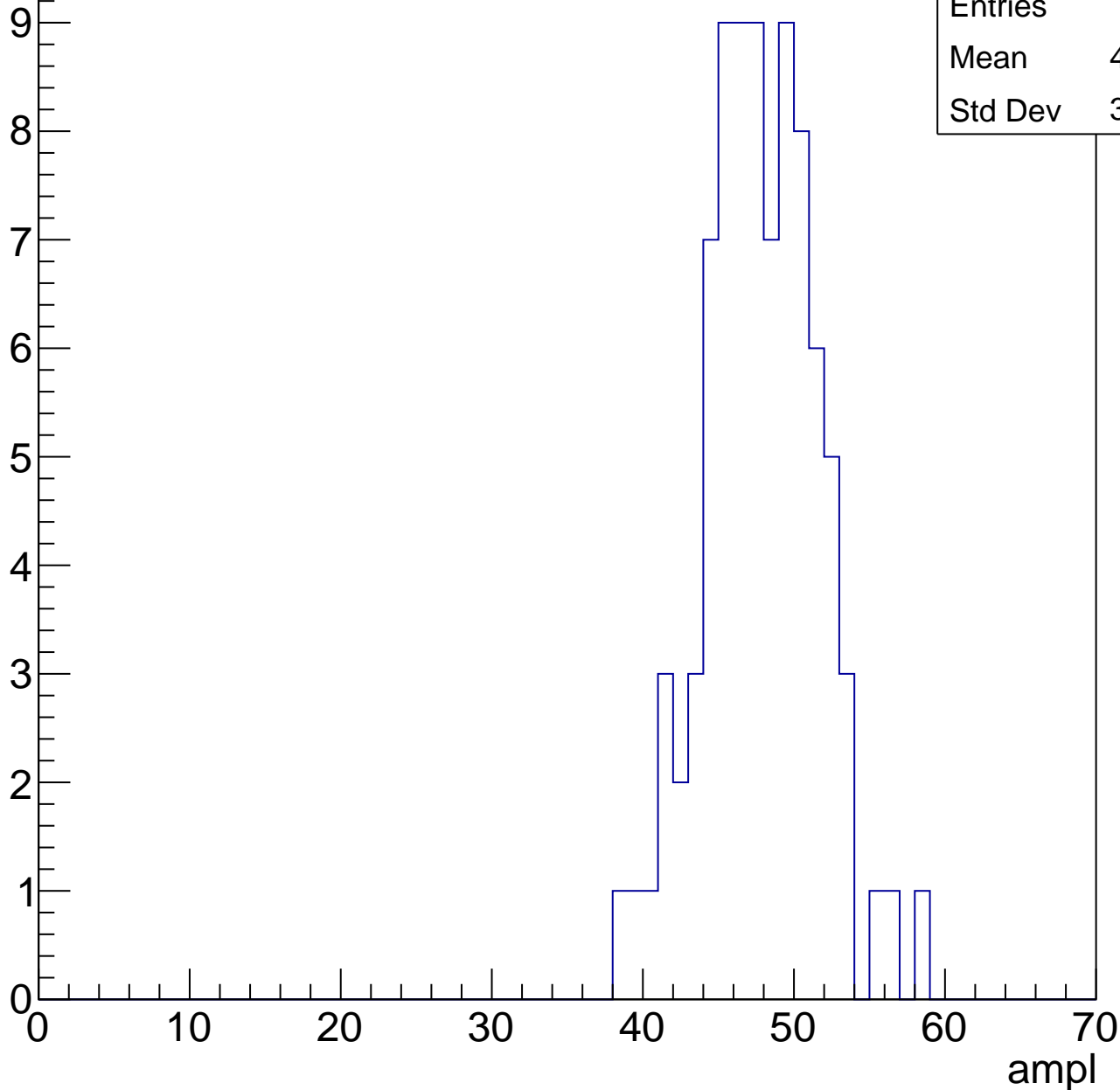
Gaus Width: 3.0228



B1L102S, U17-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



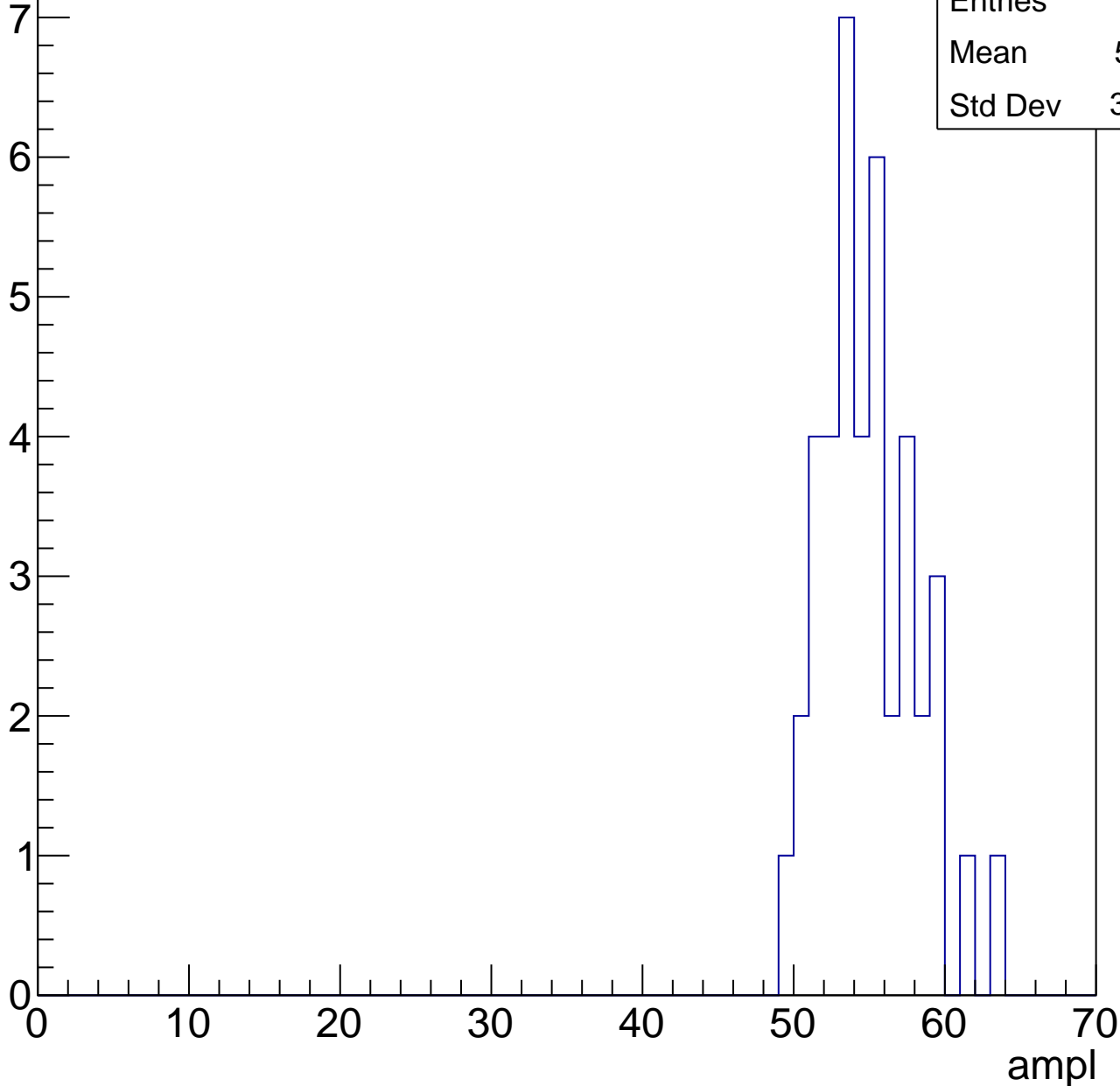
Entries	86
Mean	47.37
Std Dev	3.745

B1L102S, U17-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	54.51
Std Dev	3.093

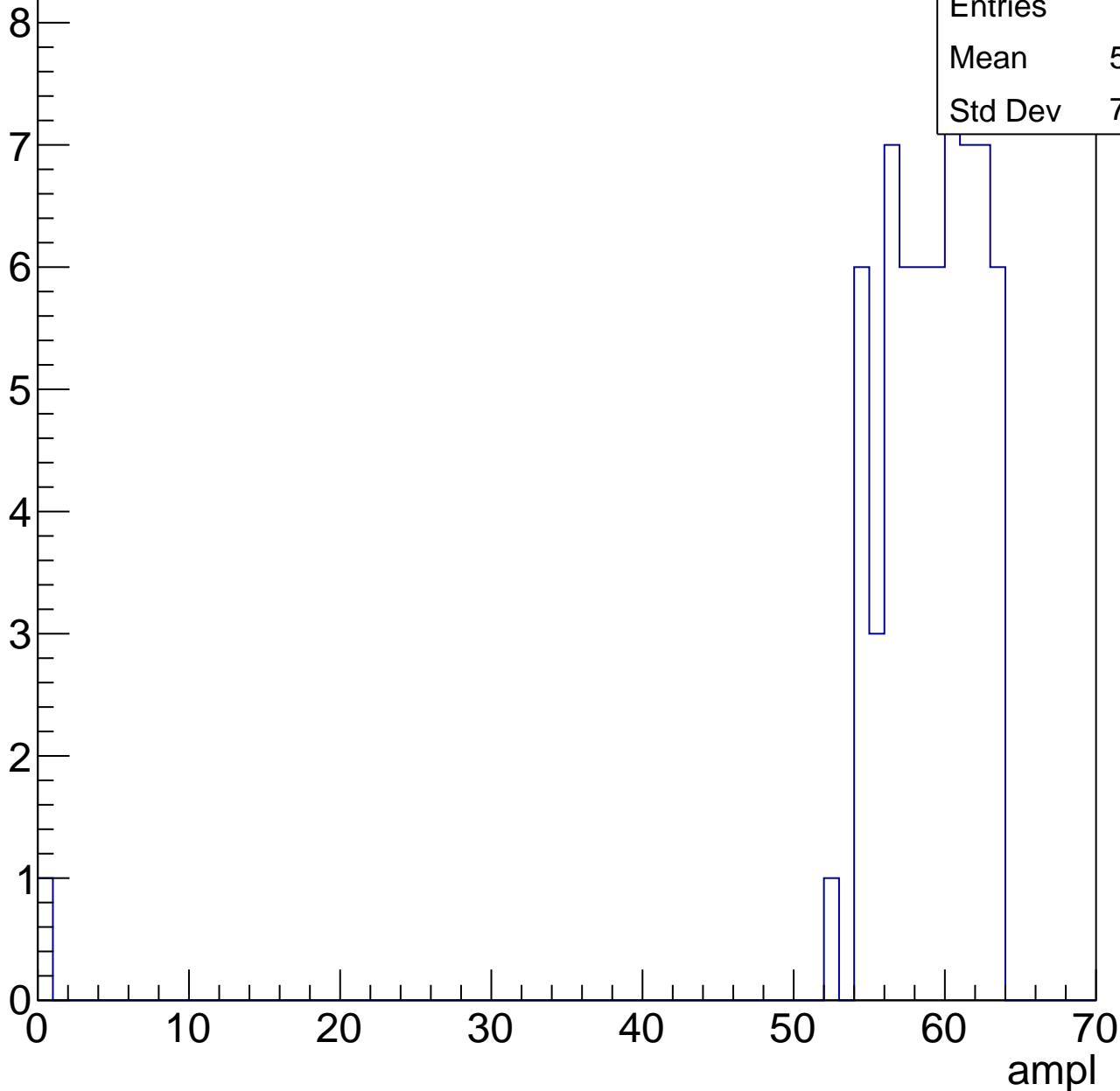


B1L102S, U17-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

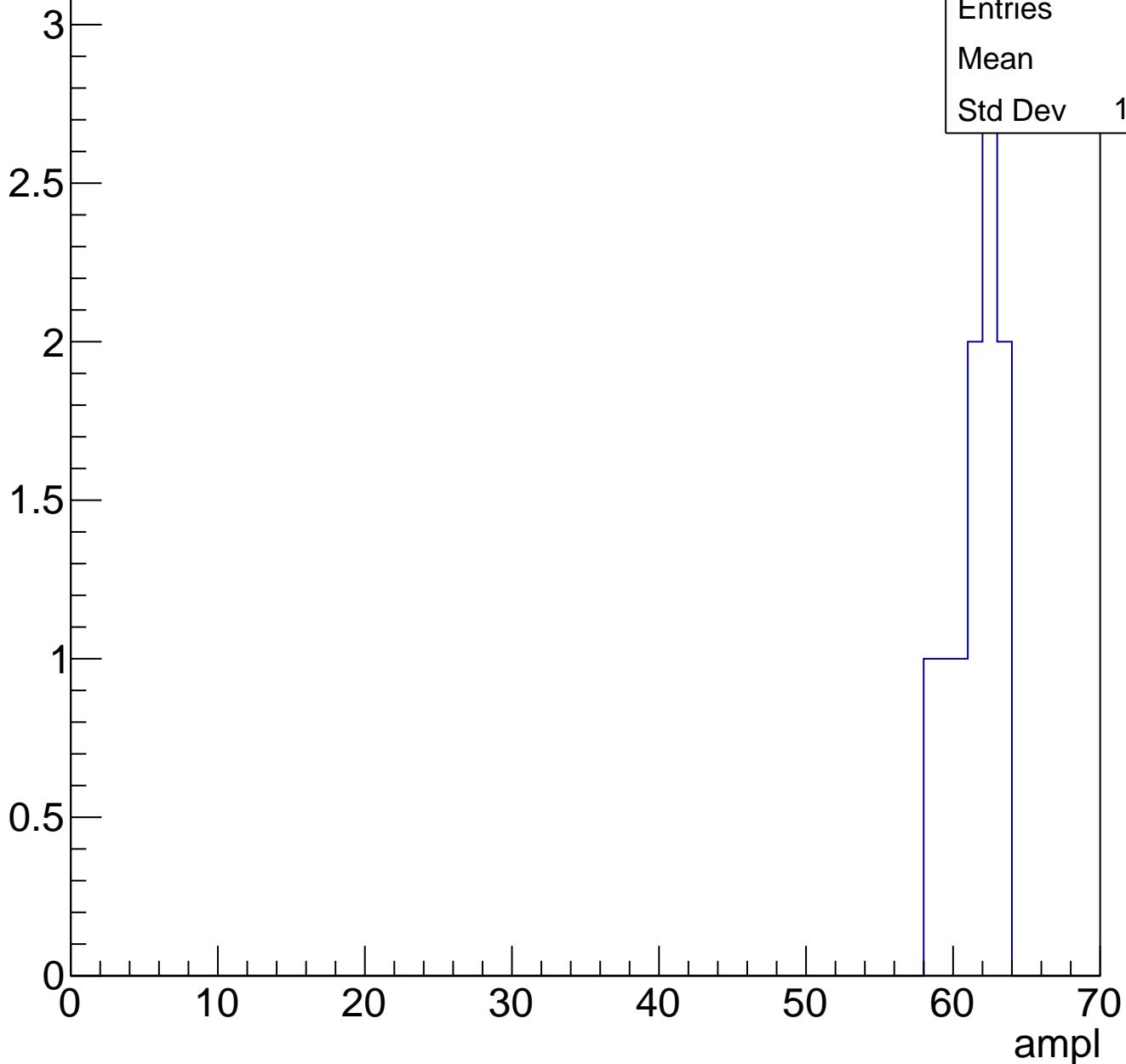
Entries	64
Mean	57.75
Std Dev	7.822



B1L102S, U17-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

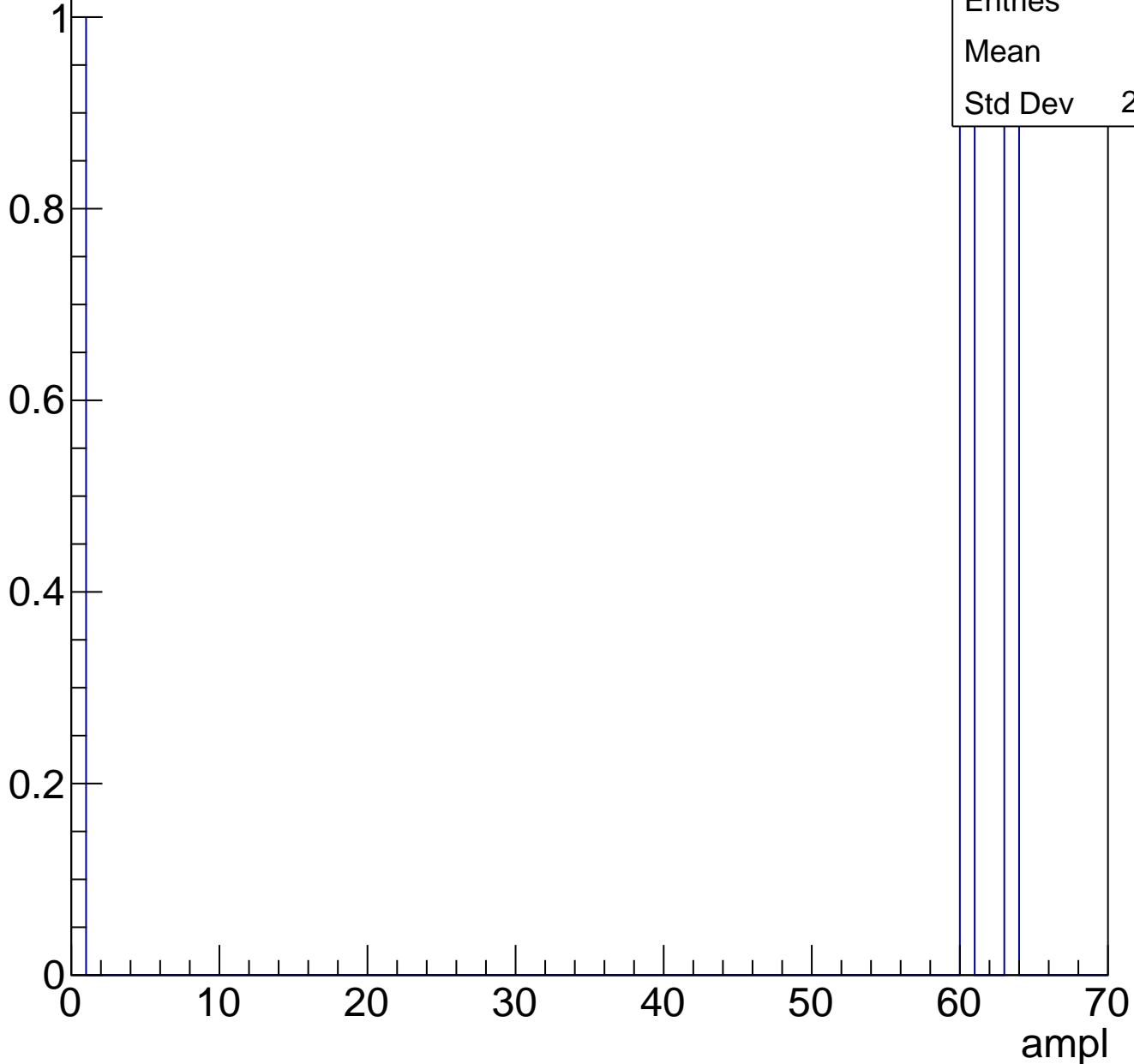
Entry



B1L102S, U17-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch62, adc0

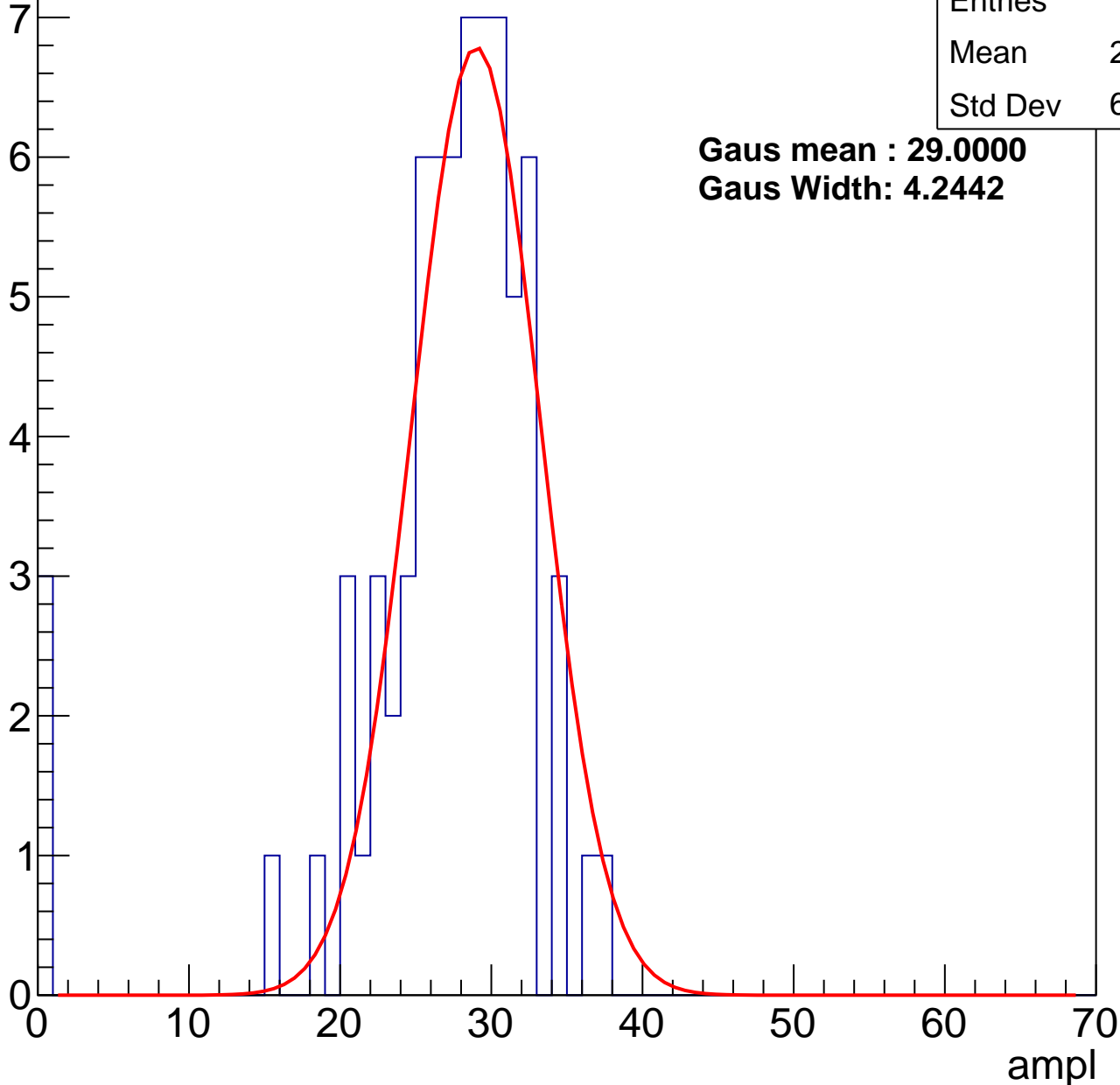
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	26.35
Std Dev	6.856

Gaus mean : 29.0000

Gaus Width: 4.2442



B1L102S, U17-ch62, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	34.92
Std Dev	3.313

Gaus mean : 35.3031

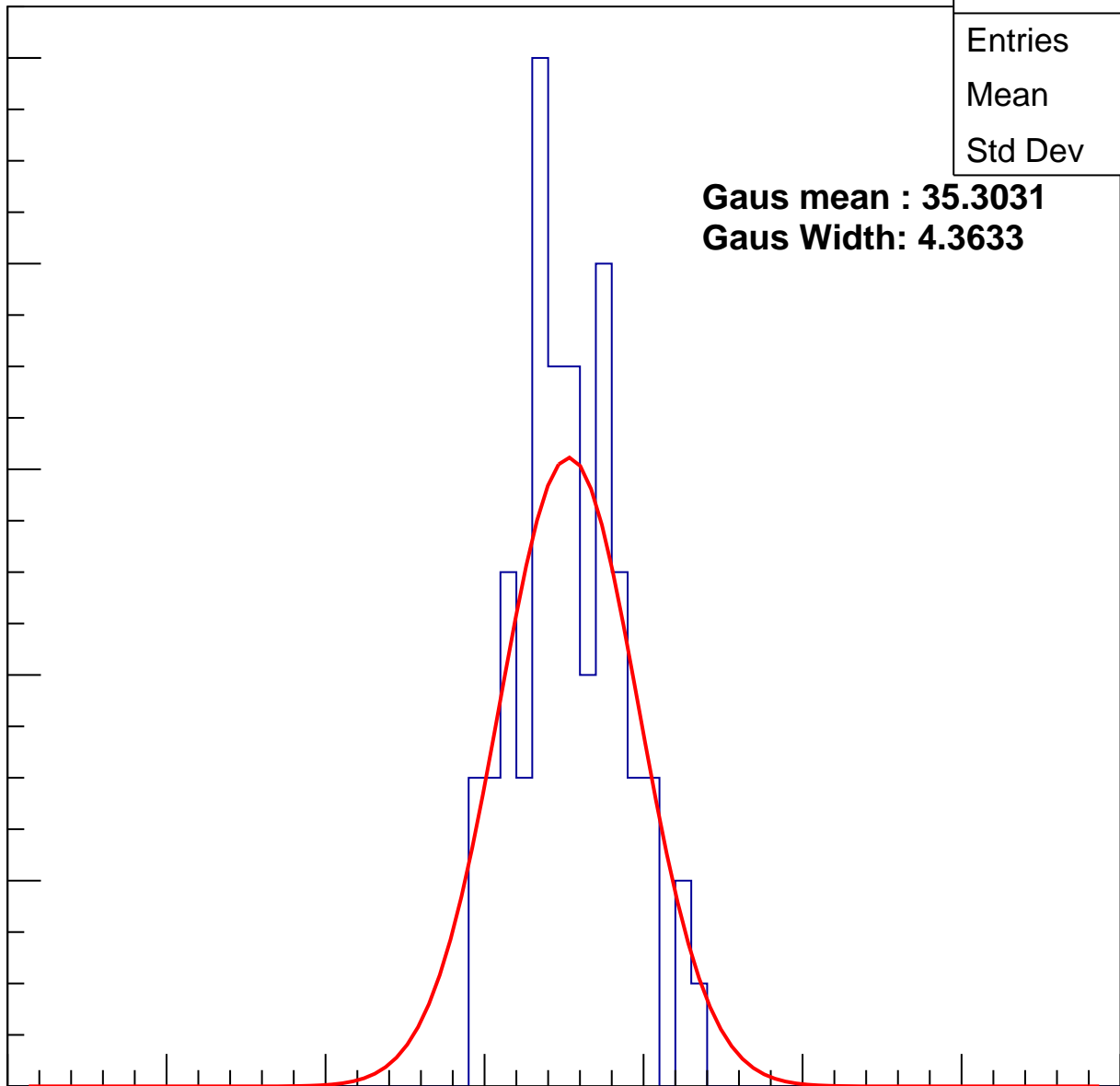
Gaus Width: 4.3633

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch62, adc2

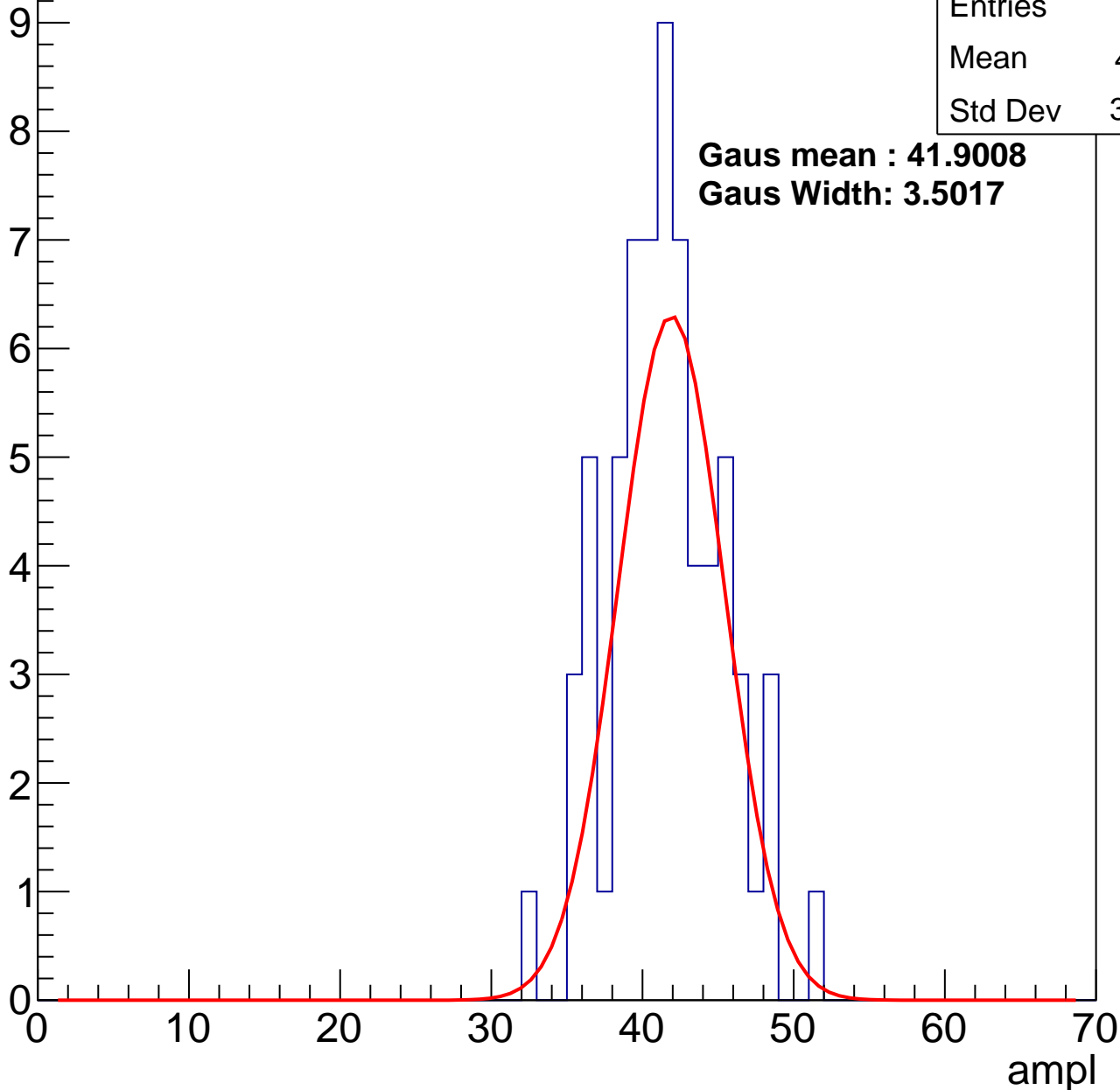
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.11
Std Dev	3.706

Gaus mean : 41.9008

Gaus Width: 3.5017

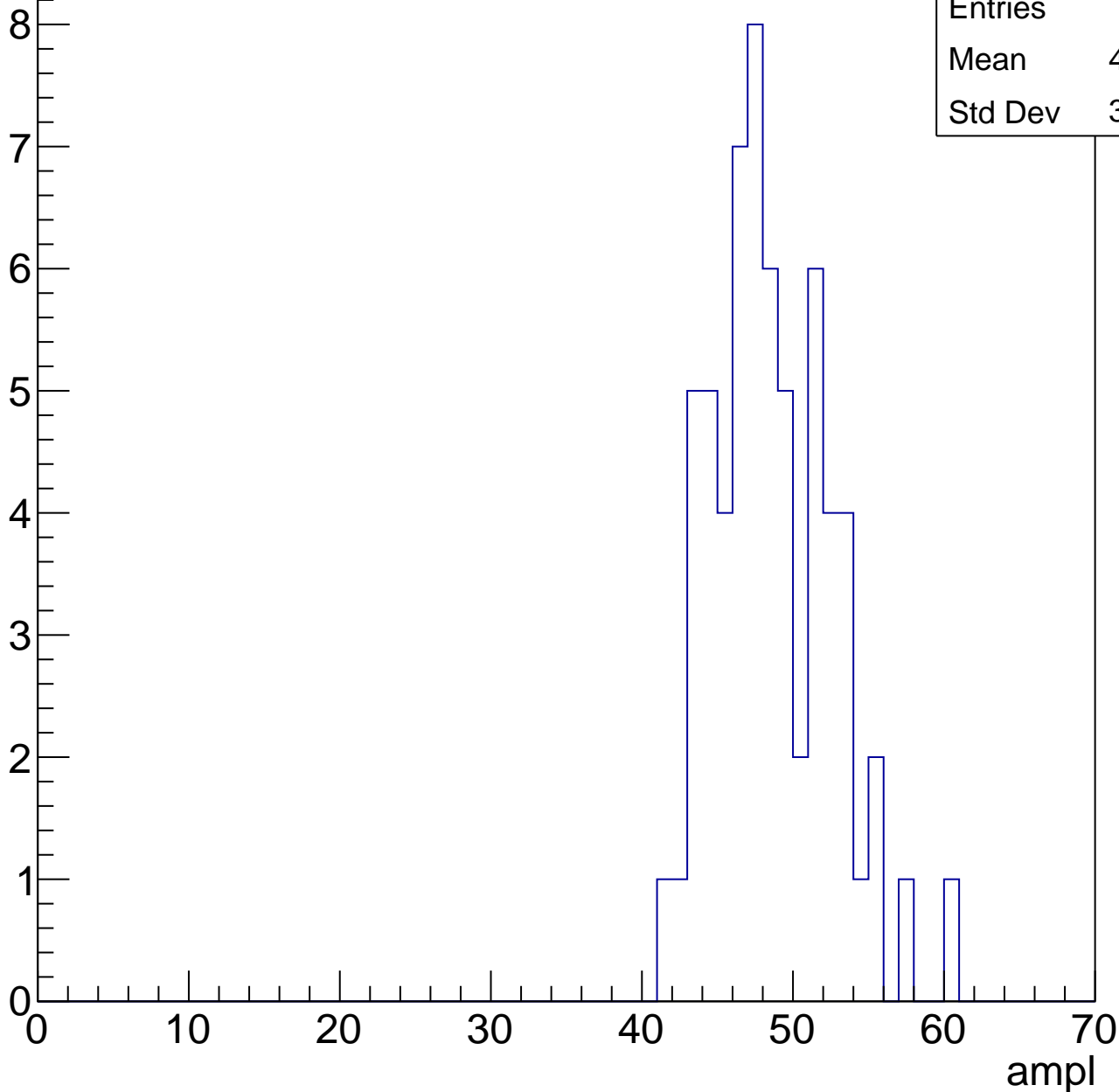


B1L102S, U17-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.19
Std Dev	3.883

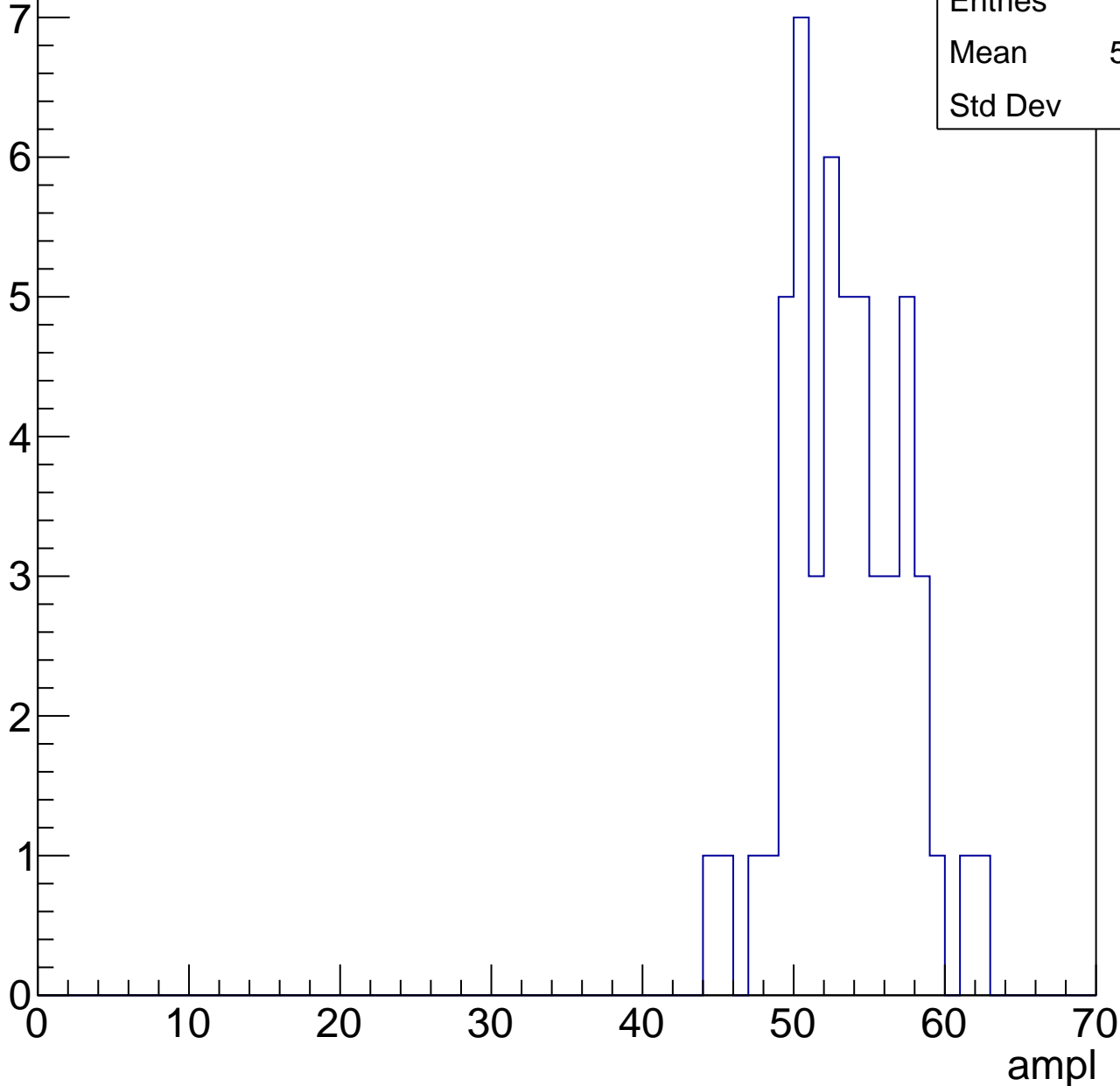


B1L102S, U17-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	52.94
Std Dev	3.79

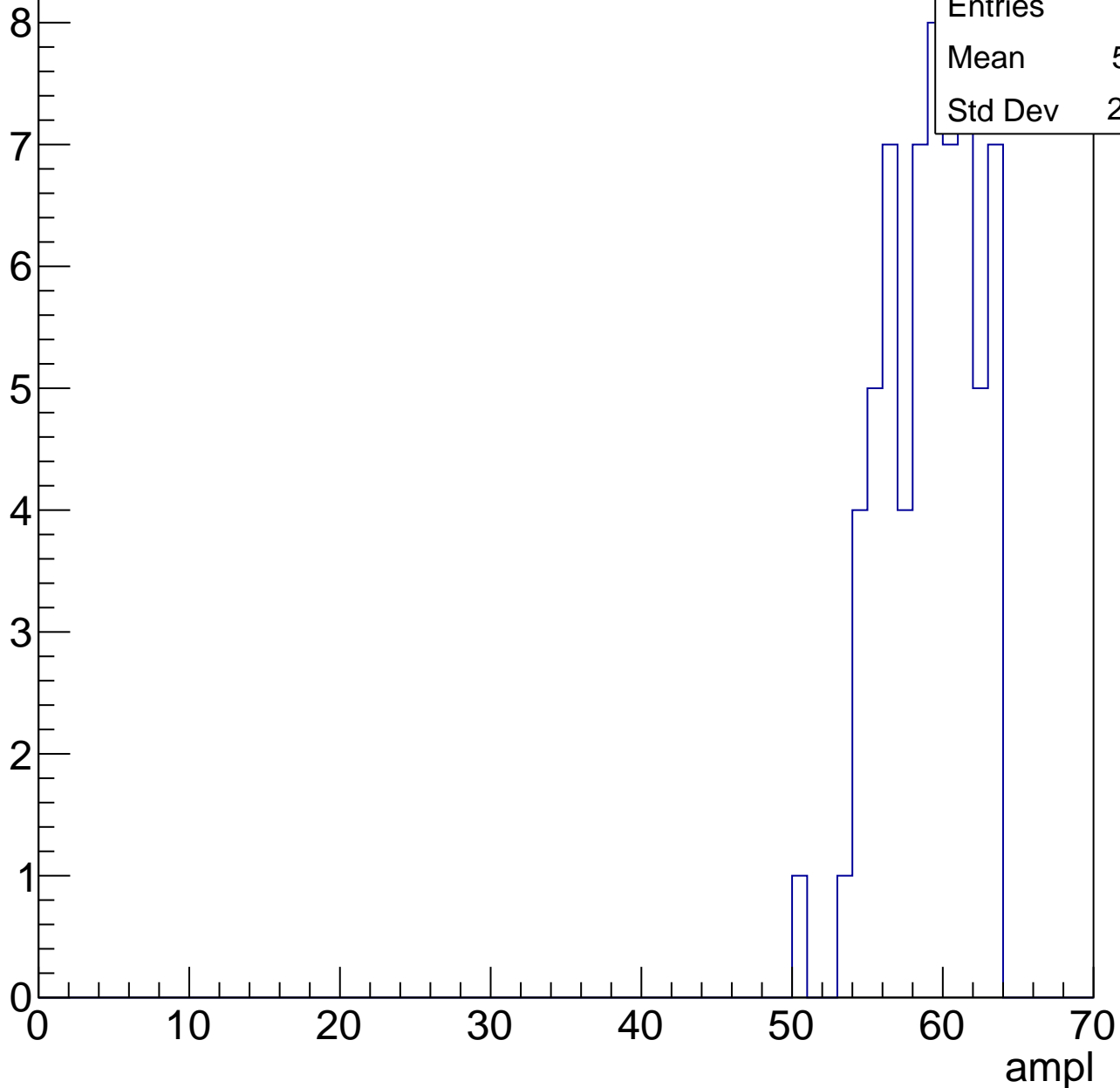


B1L102S, U17-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

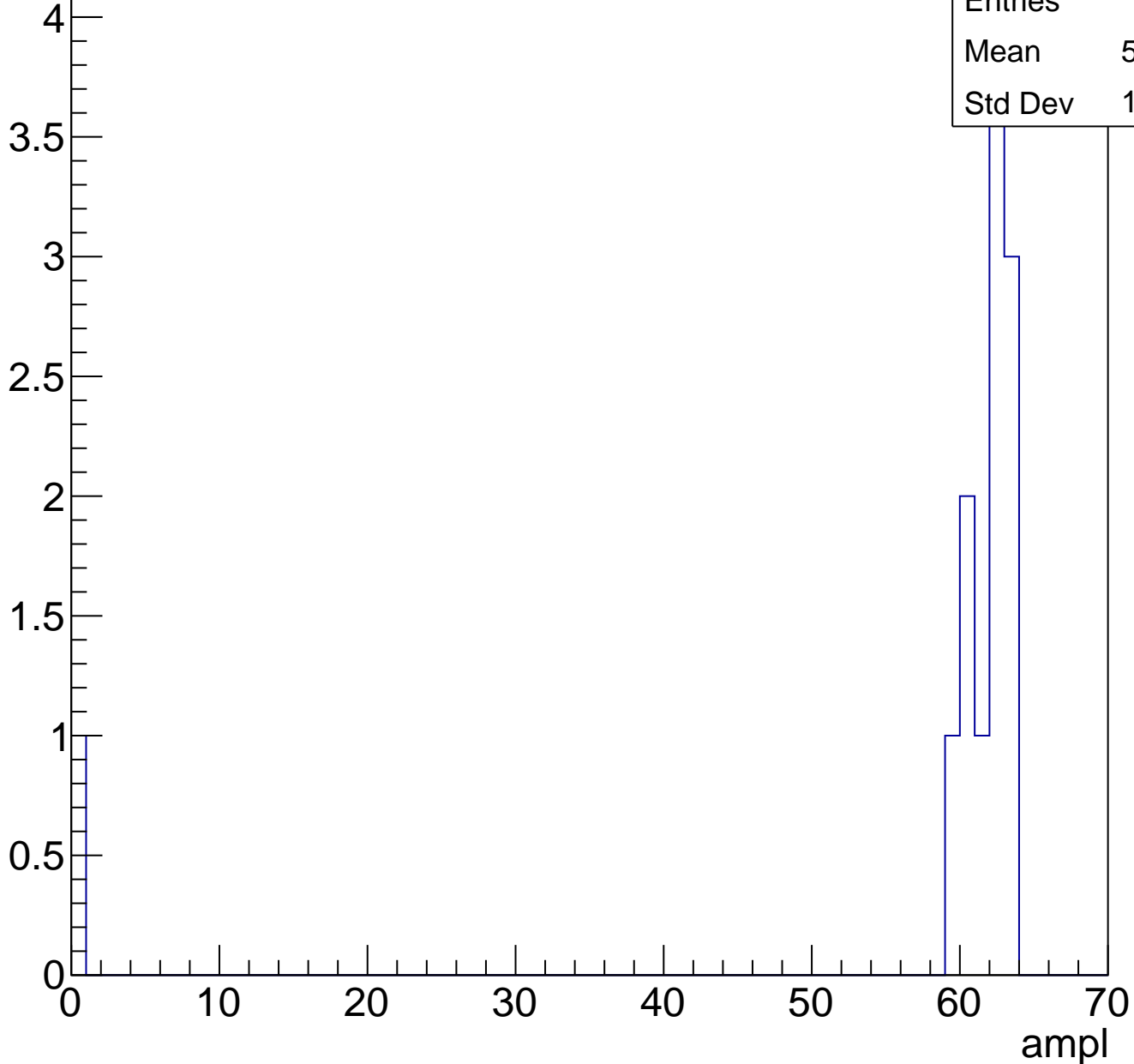
Entries	64
Mean	58.61
Std Dev	2.982



B1L102S, U17-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U17-ch63, adc0

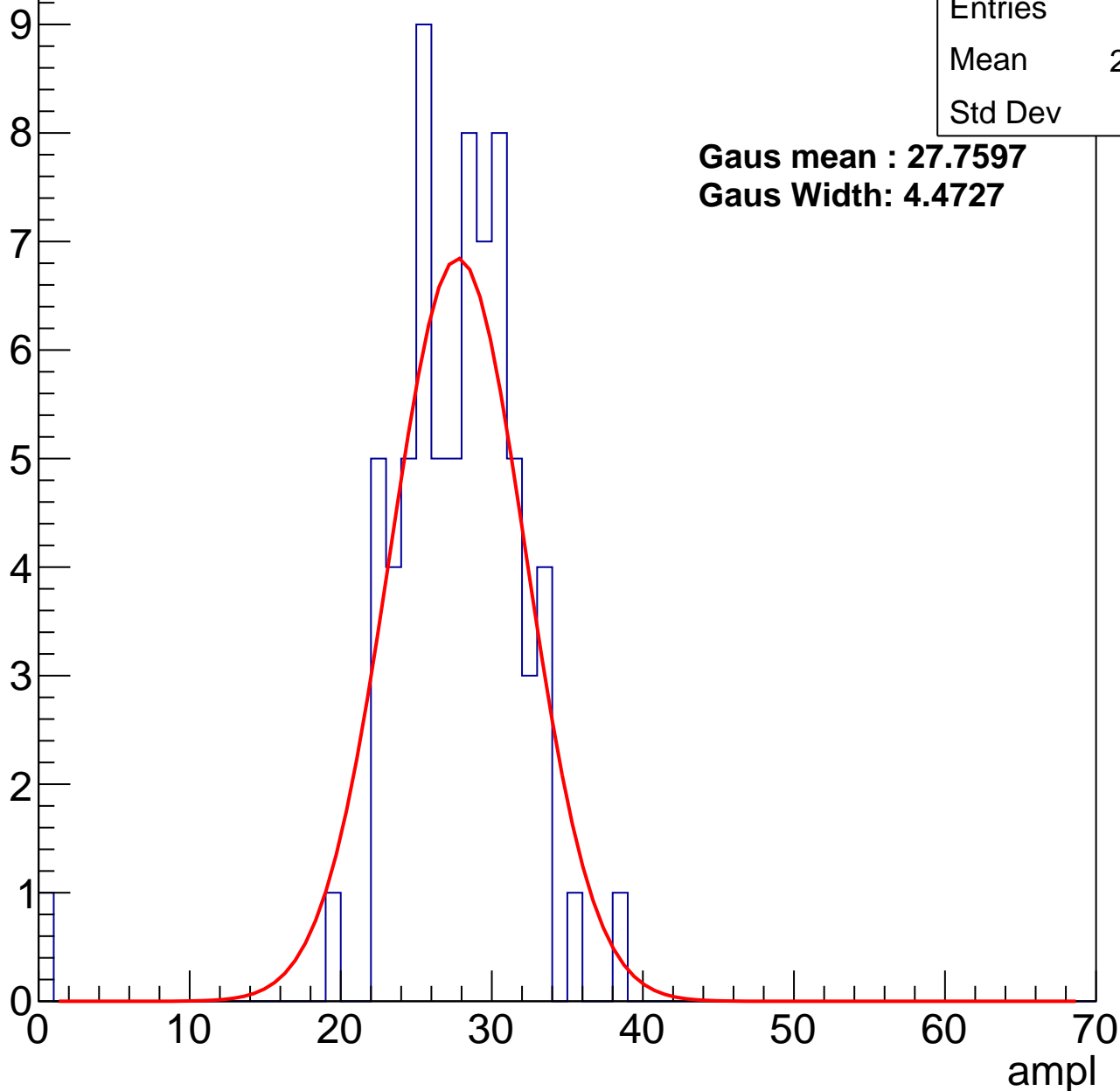
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.14
Std Dev	4.8

Gaus mean : 27.7597

Gaus Width: 4.4727



B1L102S, U17-ch63, adc1

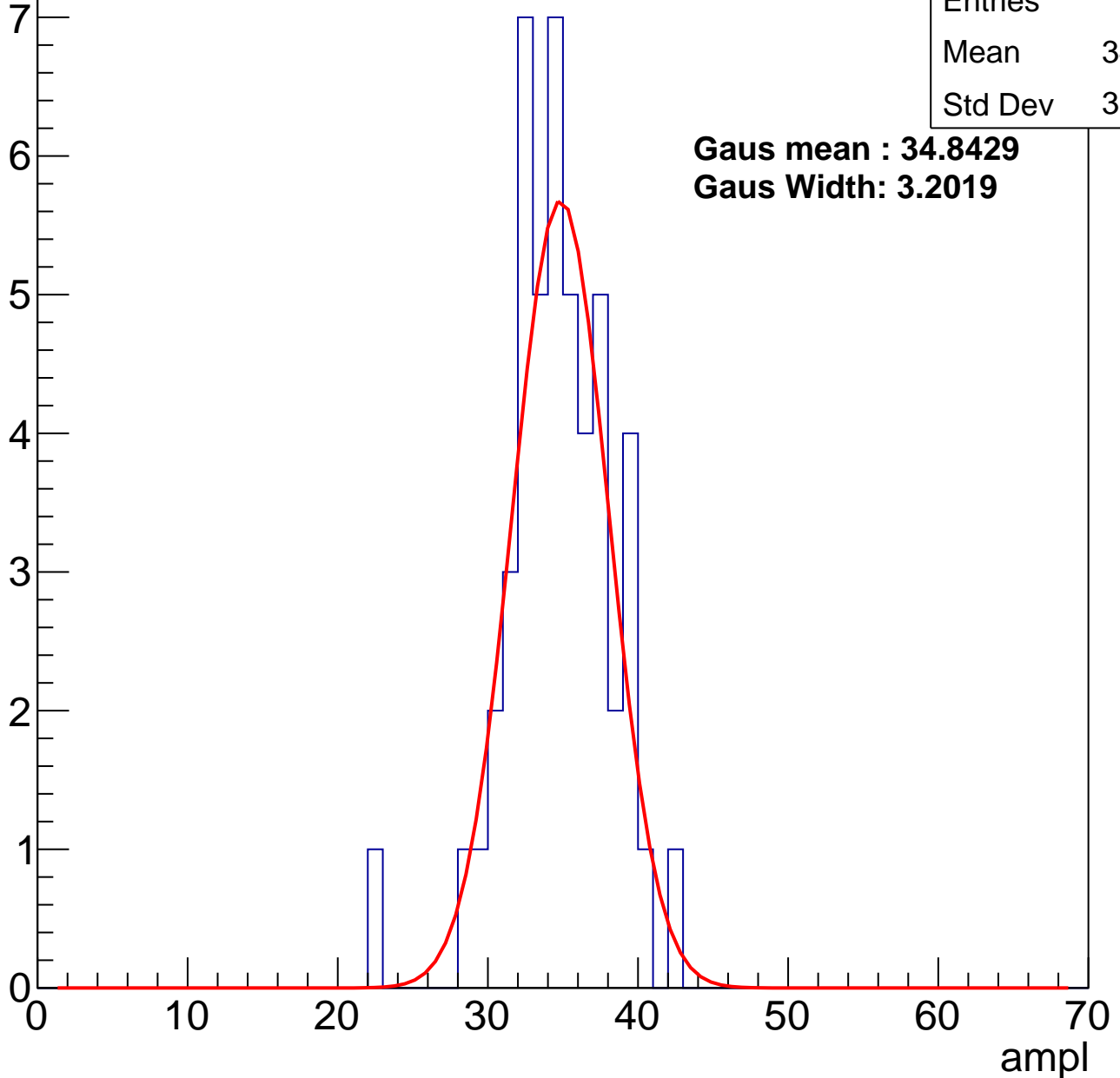
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	34.22
Std Dev	3.477

Gaus mean : 34.8429

Gaus Width: 3.2019



B1L102S, U17-ch63, adc2

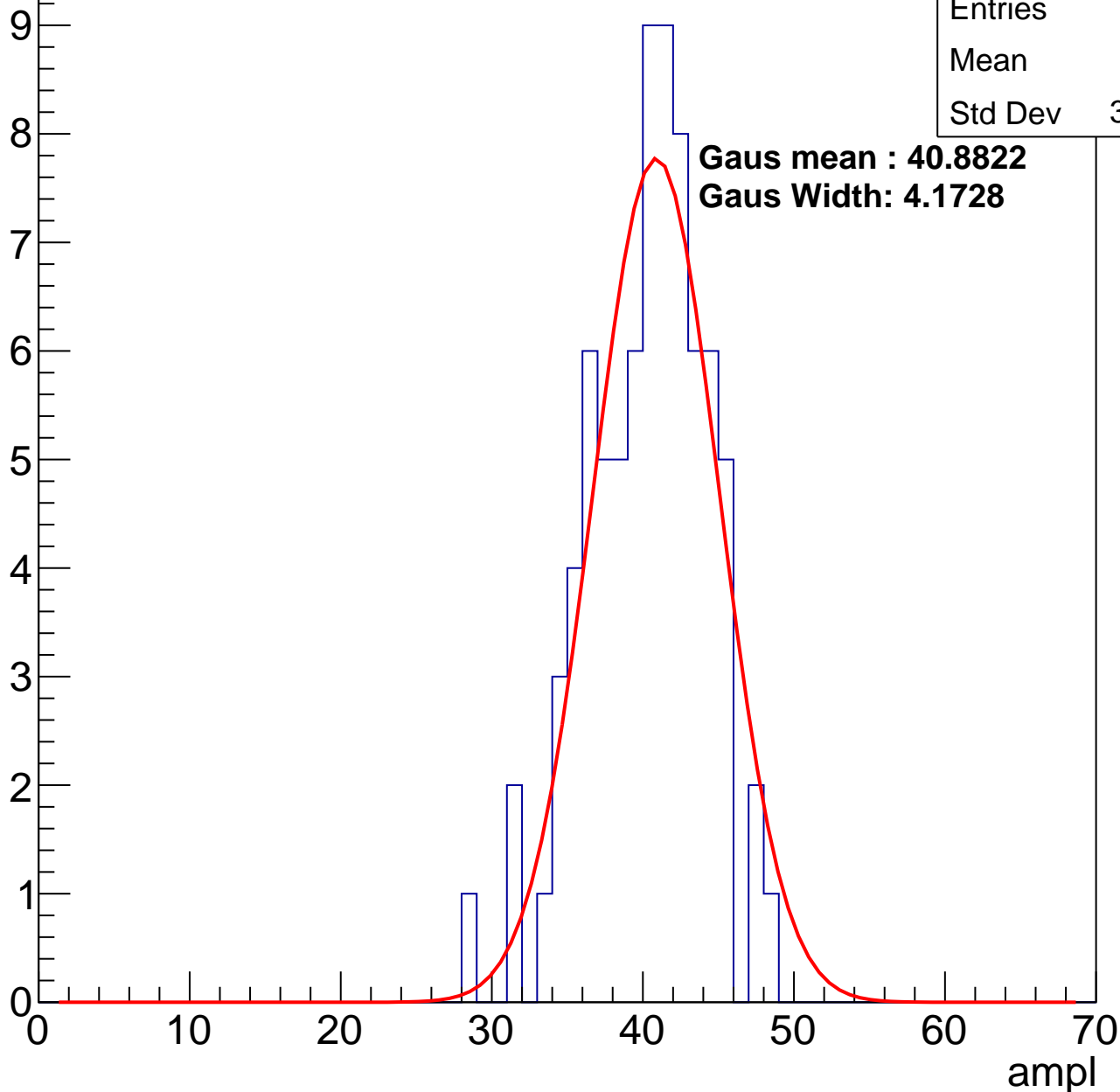
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	39.8
Std Dev	3.905

Gaus mean : 40.8822

Gaus Width: 4.1728



B1L102S, U17-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

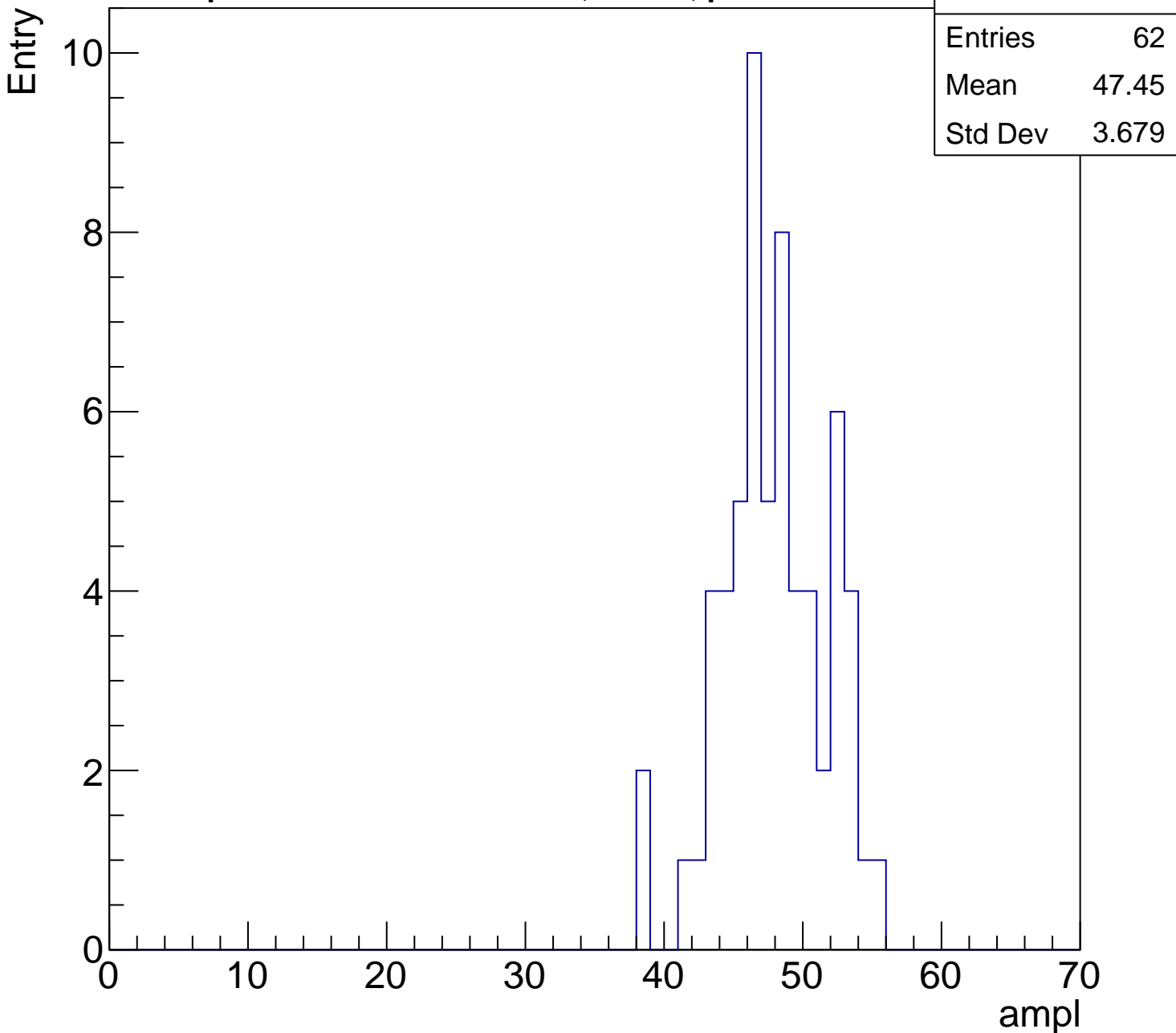
Entries	62
Mean	47.45
Std Dev	3.679

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

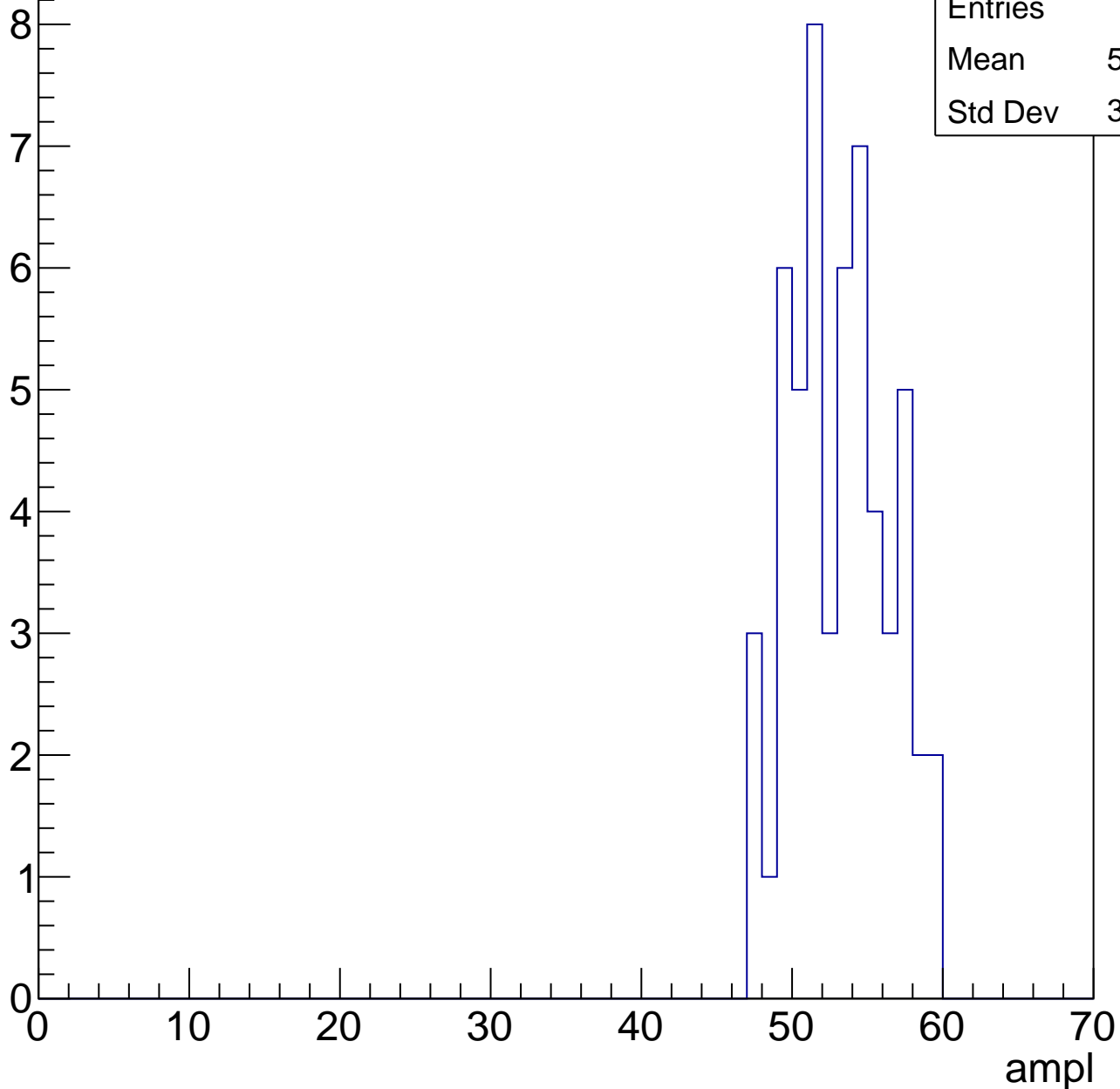


B1L102S, U17-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	52.73
Std Dev	3.182

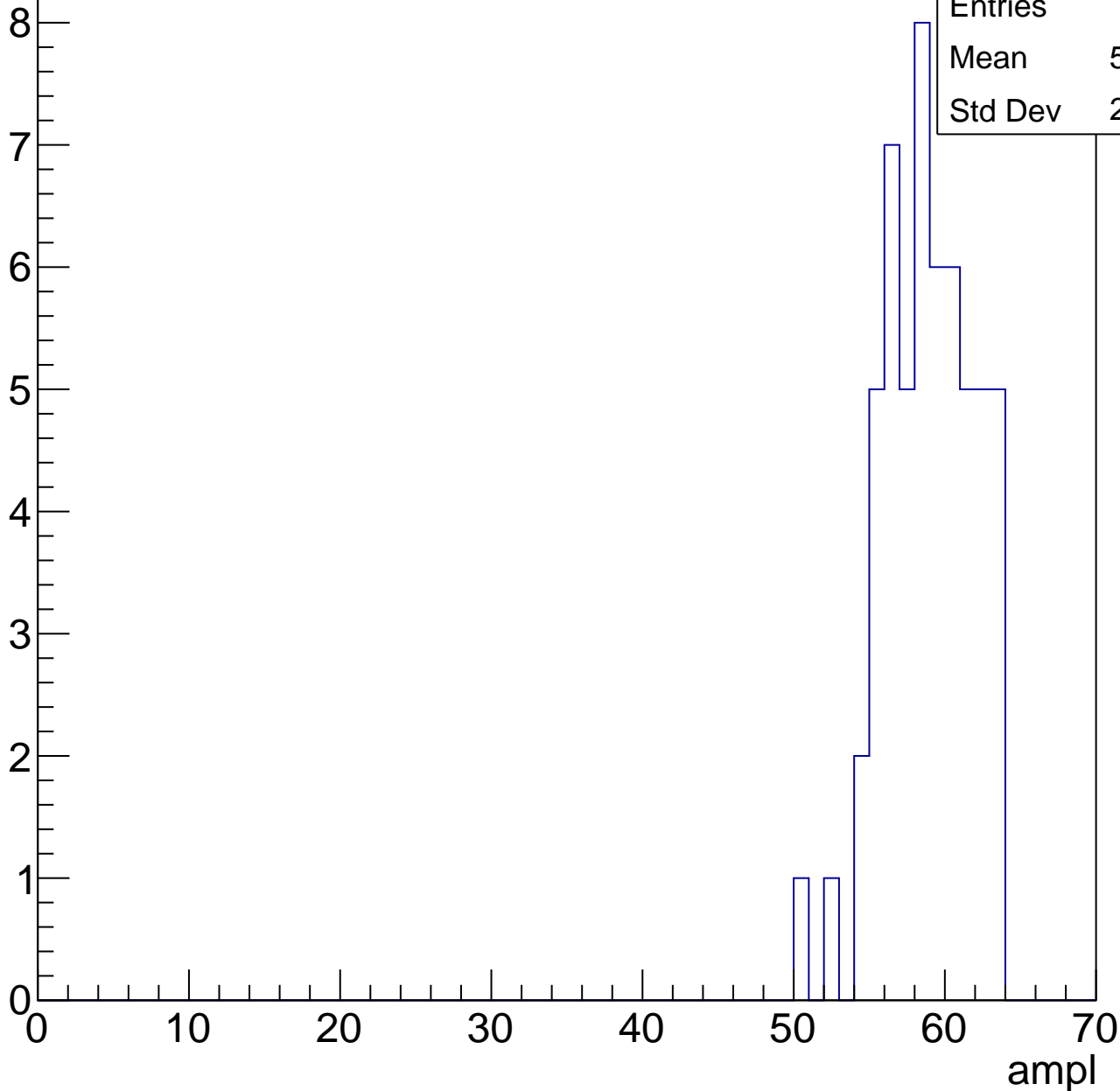


B1L102S, U17-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.39
Std Dev	2.932

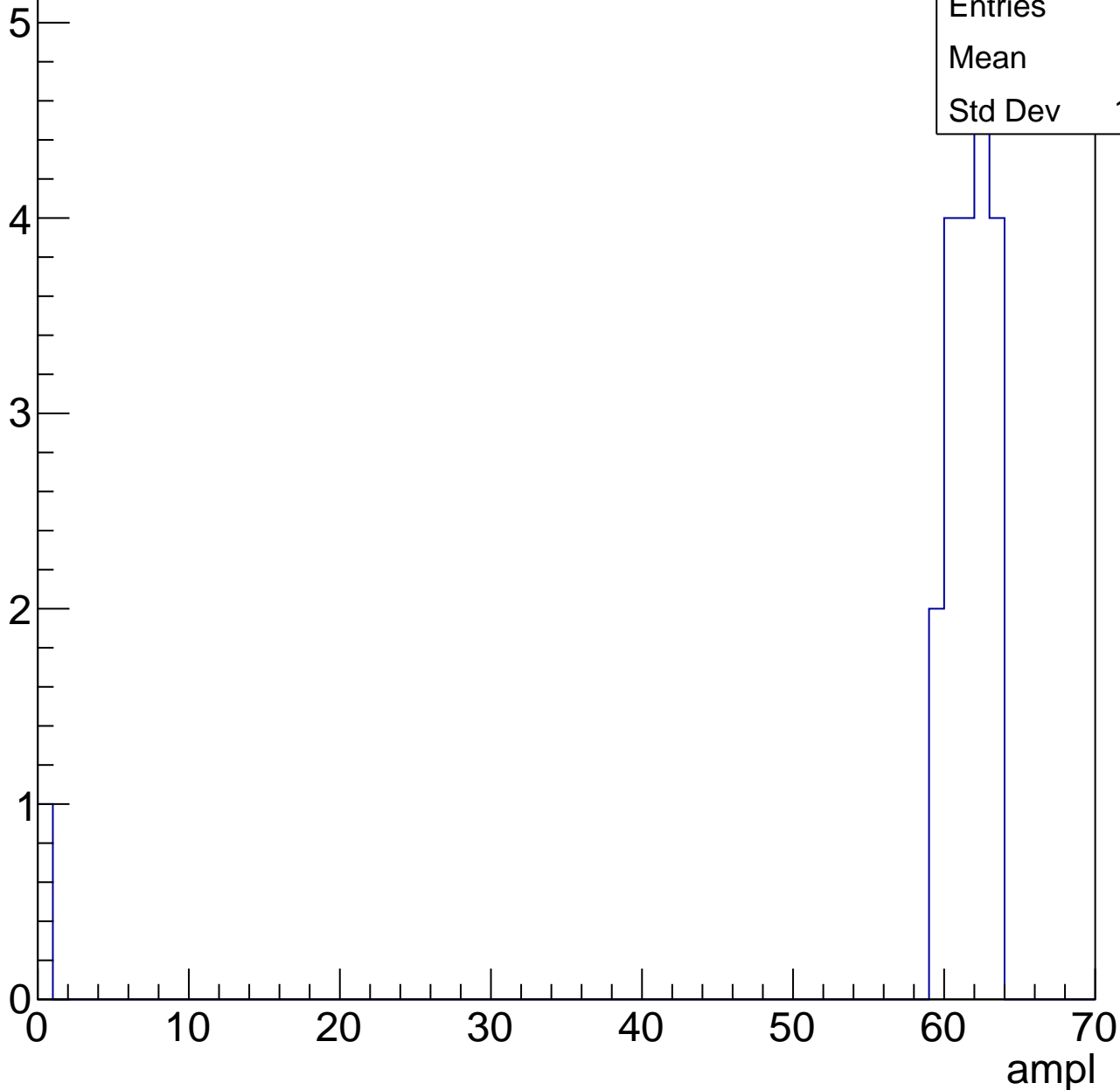


B1L102S, U17-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	58.2
Std Dev	13.41



B1L102S, U17-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch64, adc0

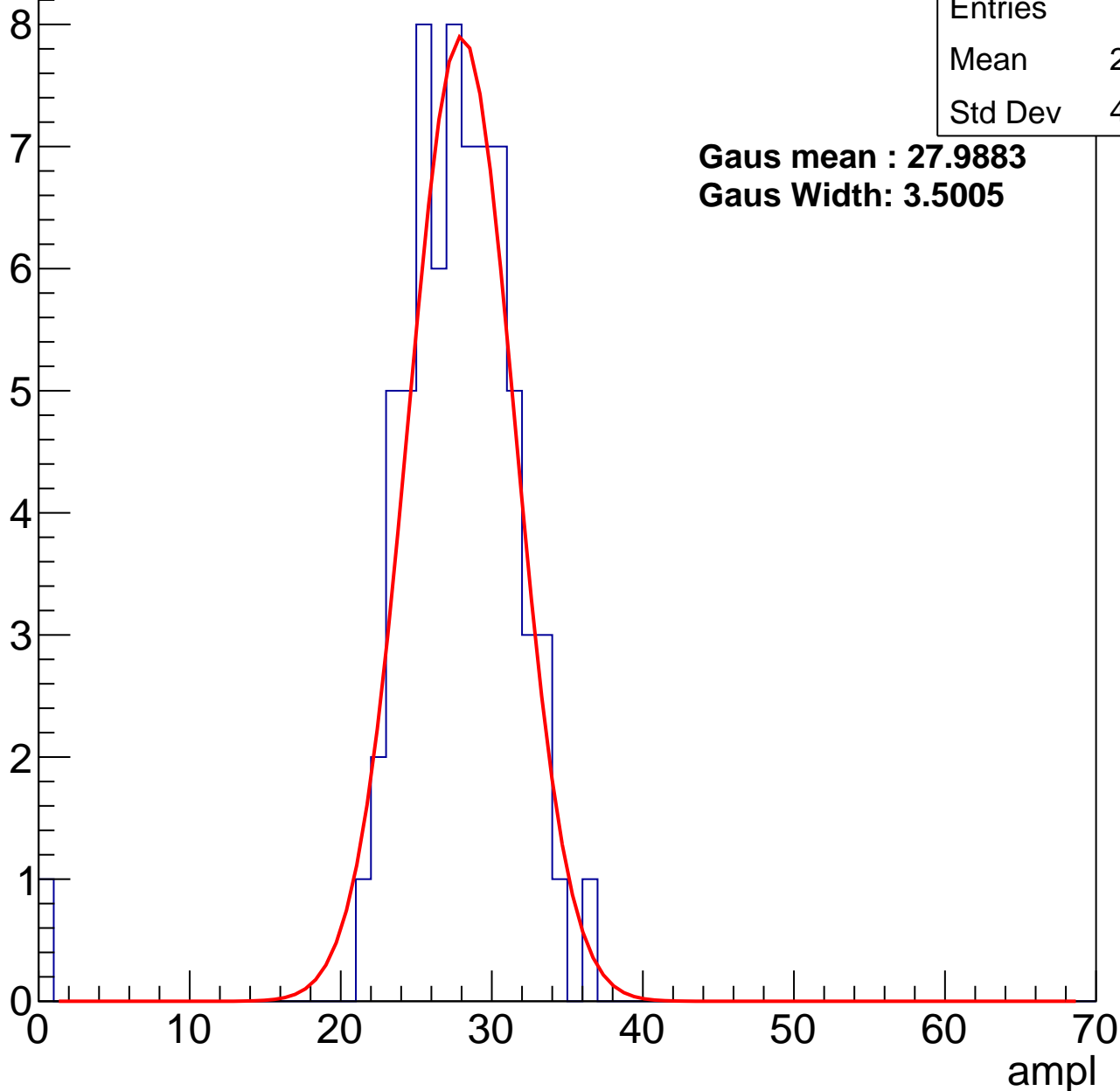
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.16
Std Dev	4.578

Gaus mean : 27.9883

Gaus Width: 3.5005



B1L102S, U17-ch64, adc1

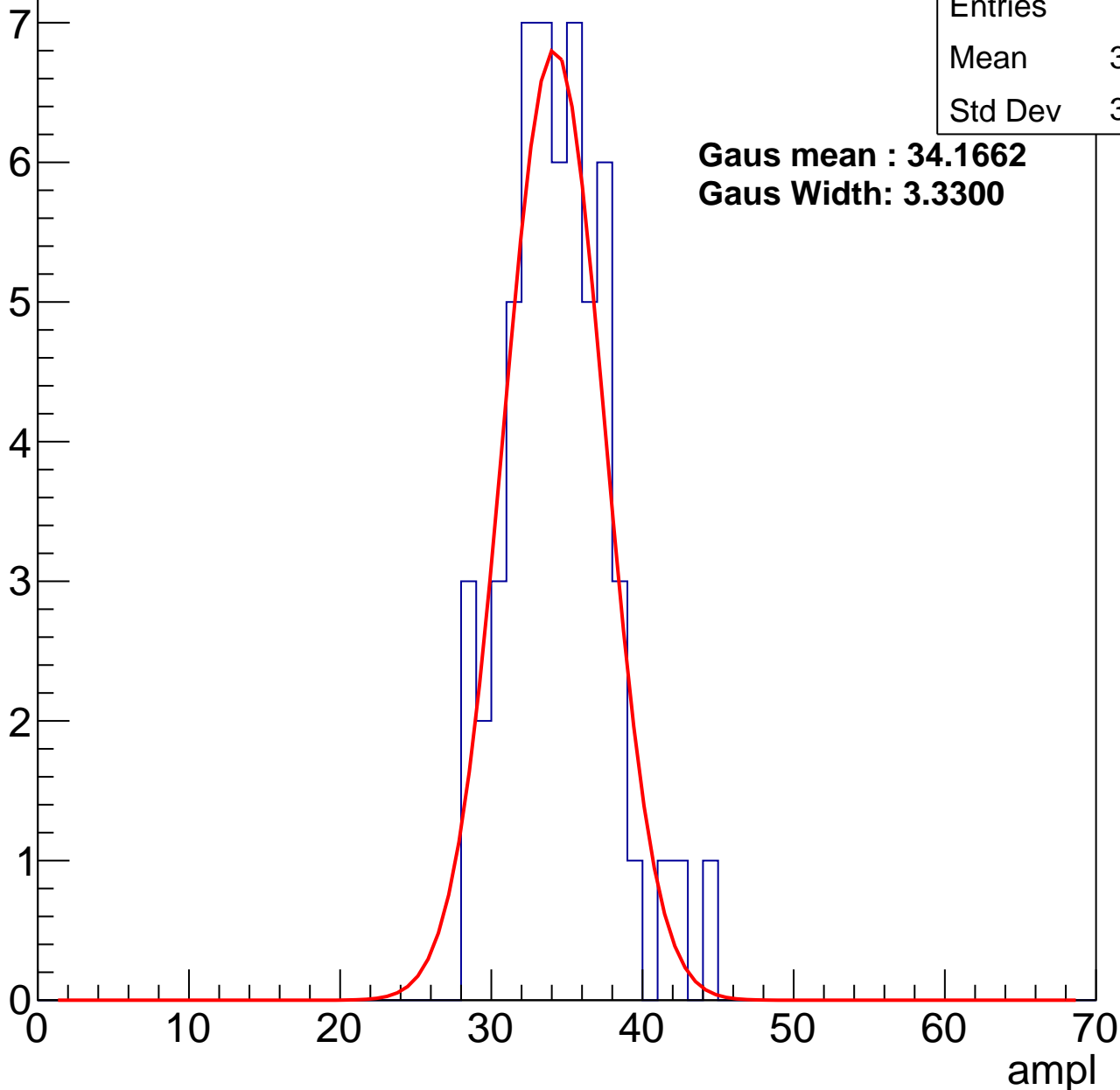
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	34.02
Std Dev	3.355

Gaus mean : 34.1662

Gaus Width: 3.3300



B1L102S, U17-ch64, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	40.75
Std Dev	3.545

Gaus mean : 40.4610

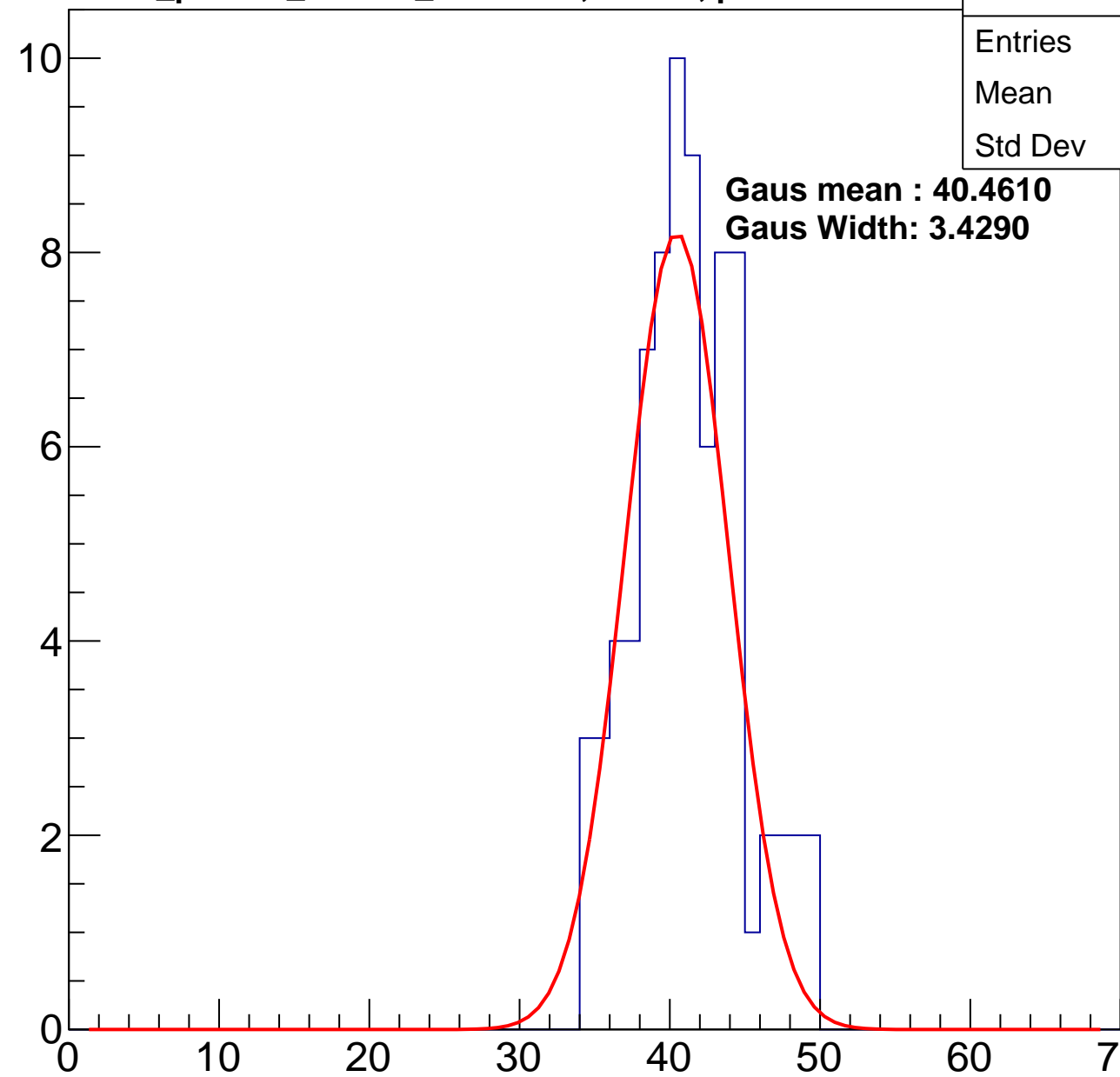
Gaus Width: 3.4290

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

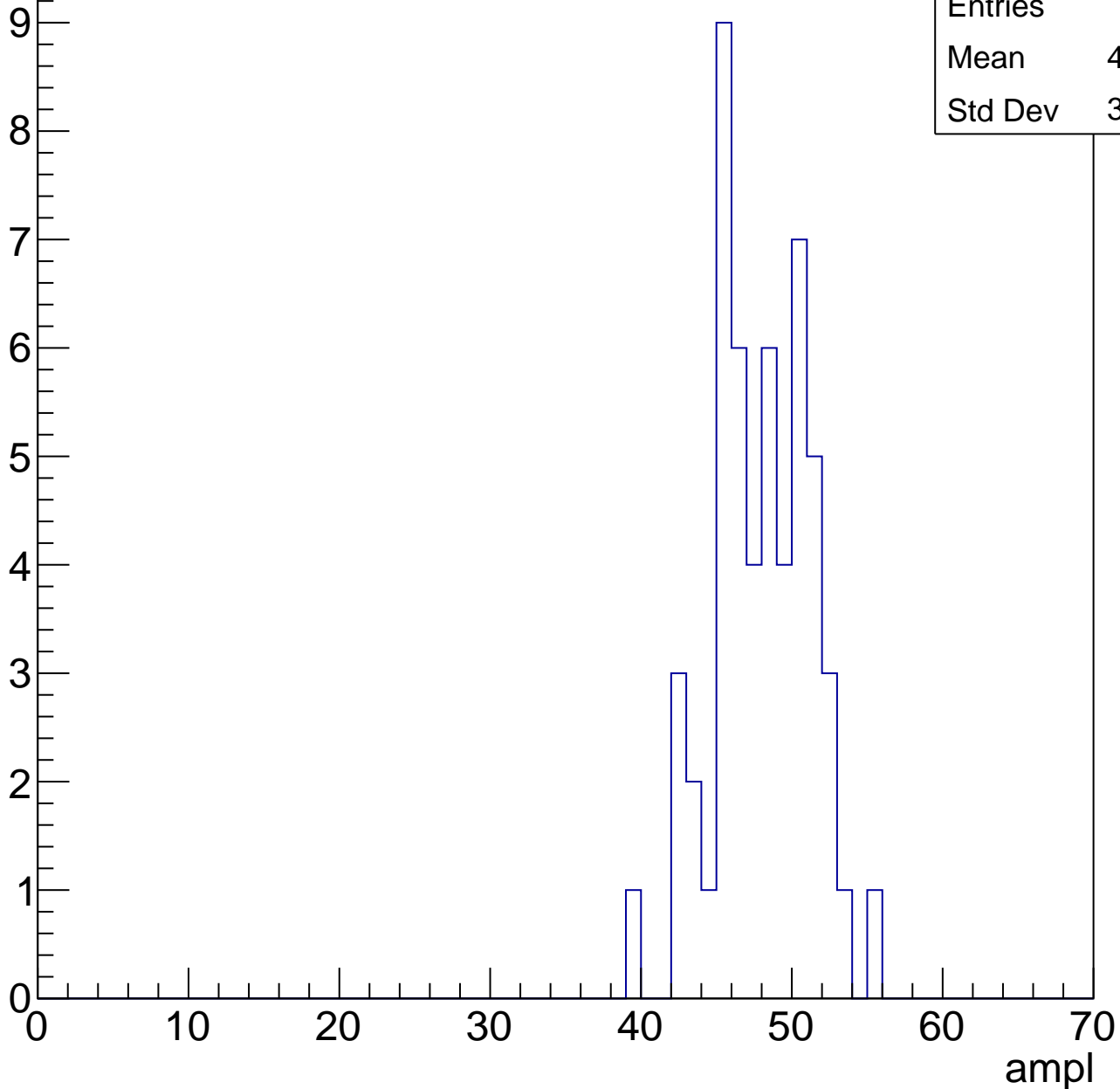


B1L102S, U17-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	47.49
Std Dev	3.219

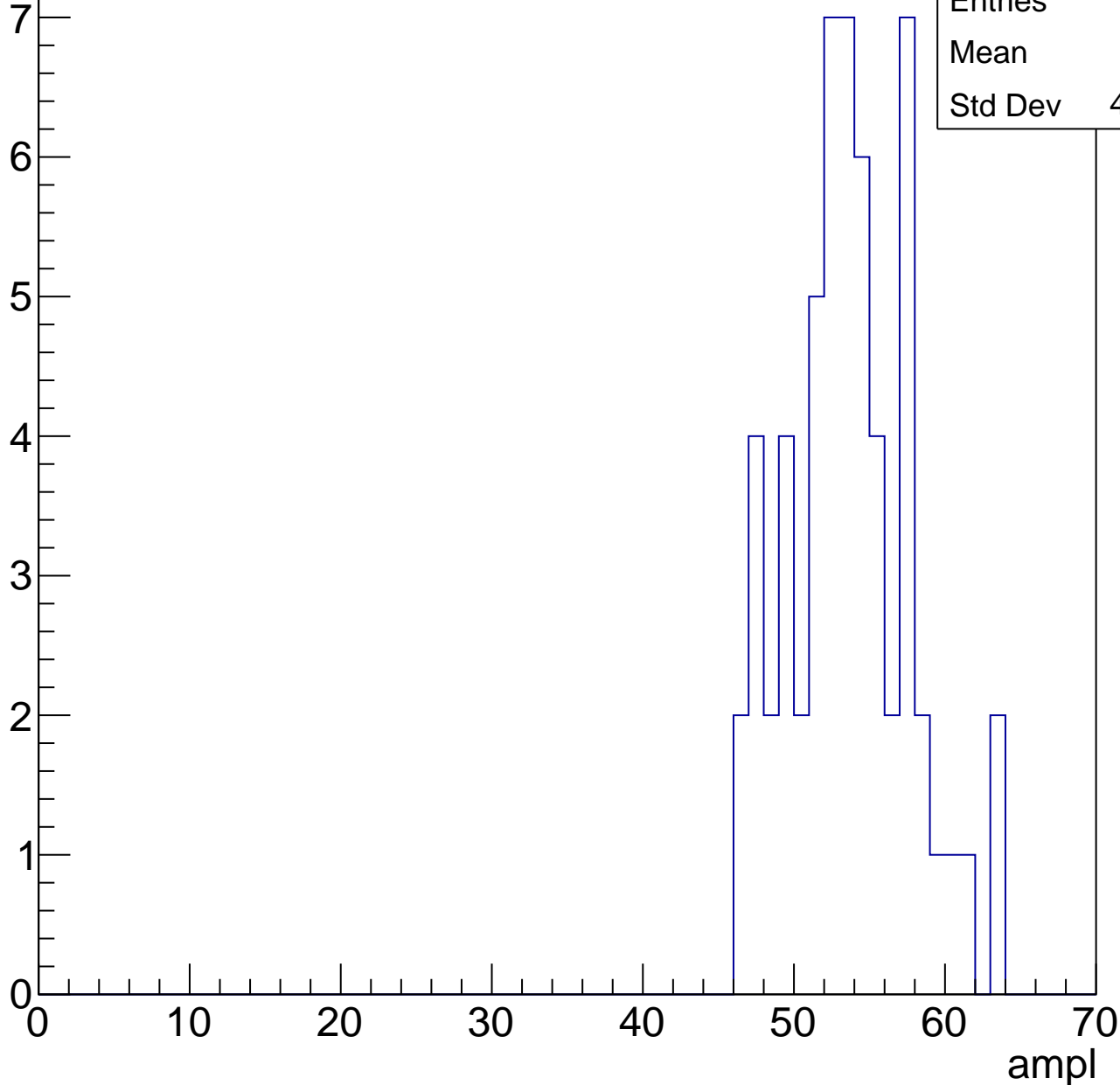


B1L102S, U17-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.2
Std Dev	4.012

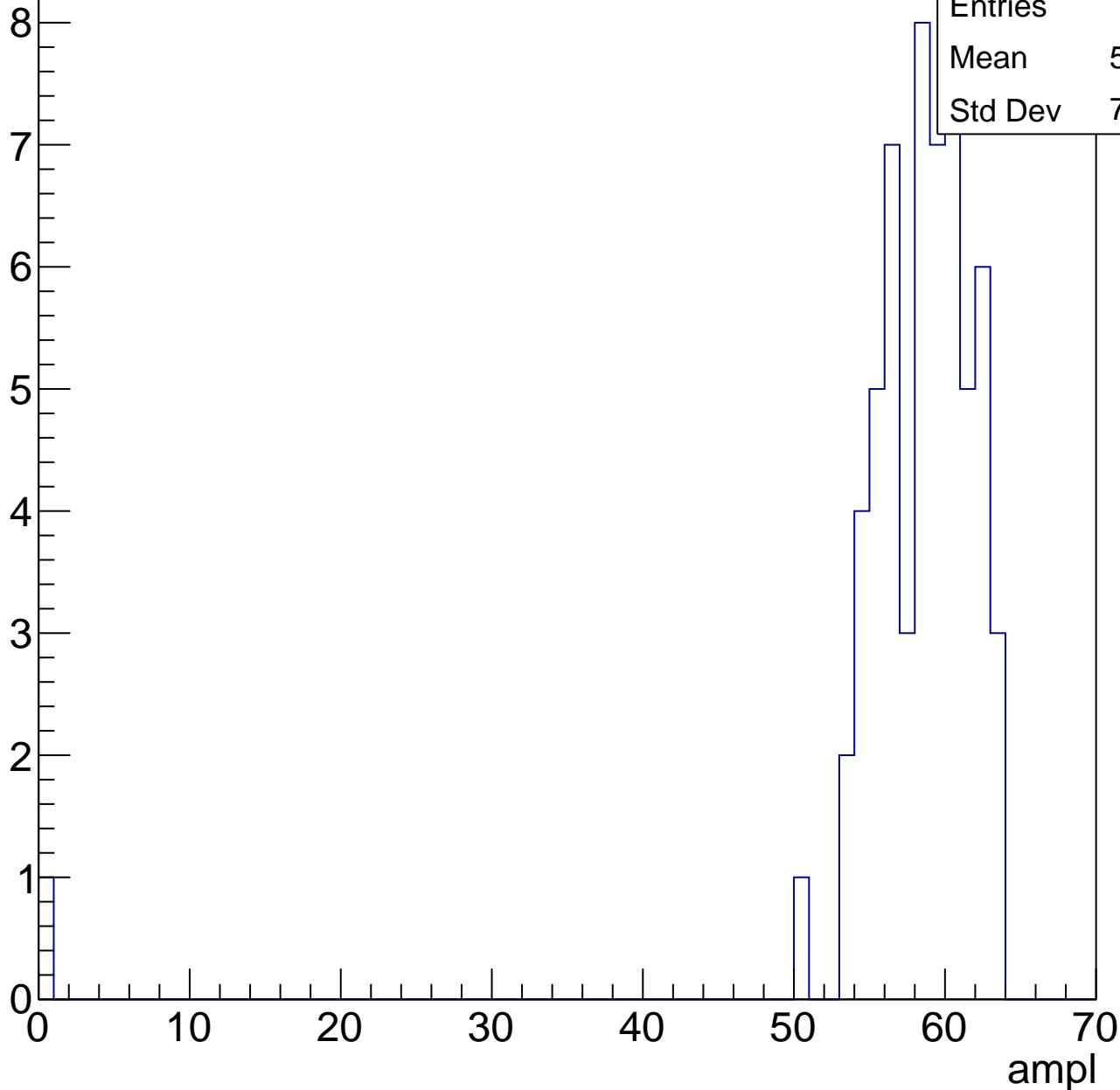


B1L102S, U17-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	57.22
Std Dev	7.996

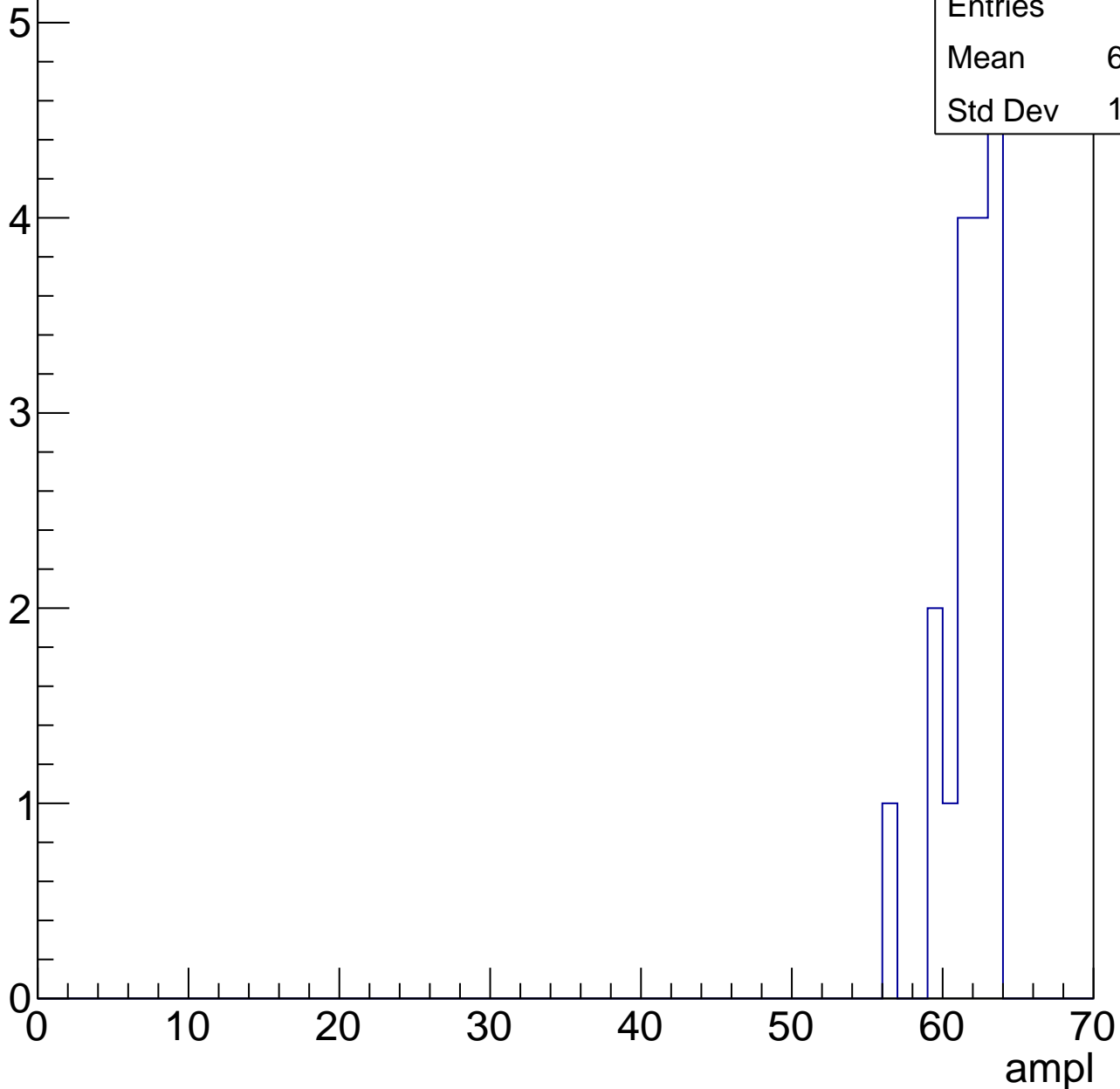


B1L102S, U17-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.24
Std Dev	1.832



B1L102S, U17-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch65, adc0

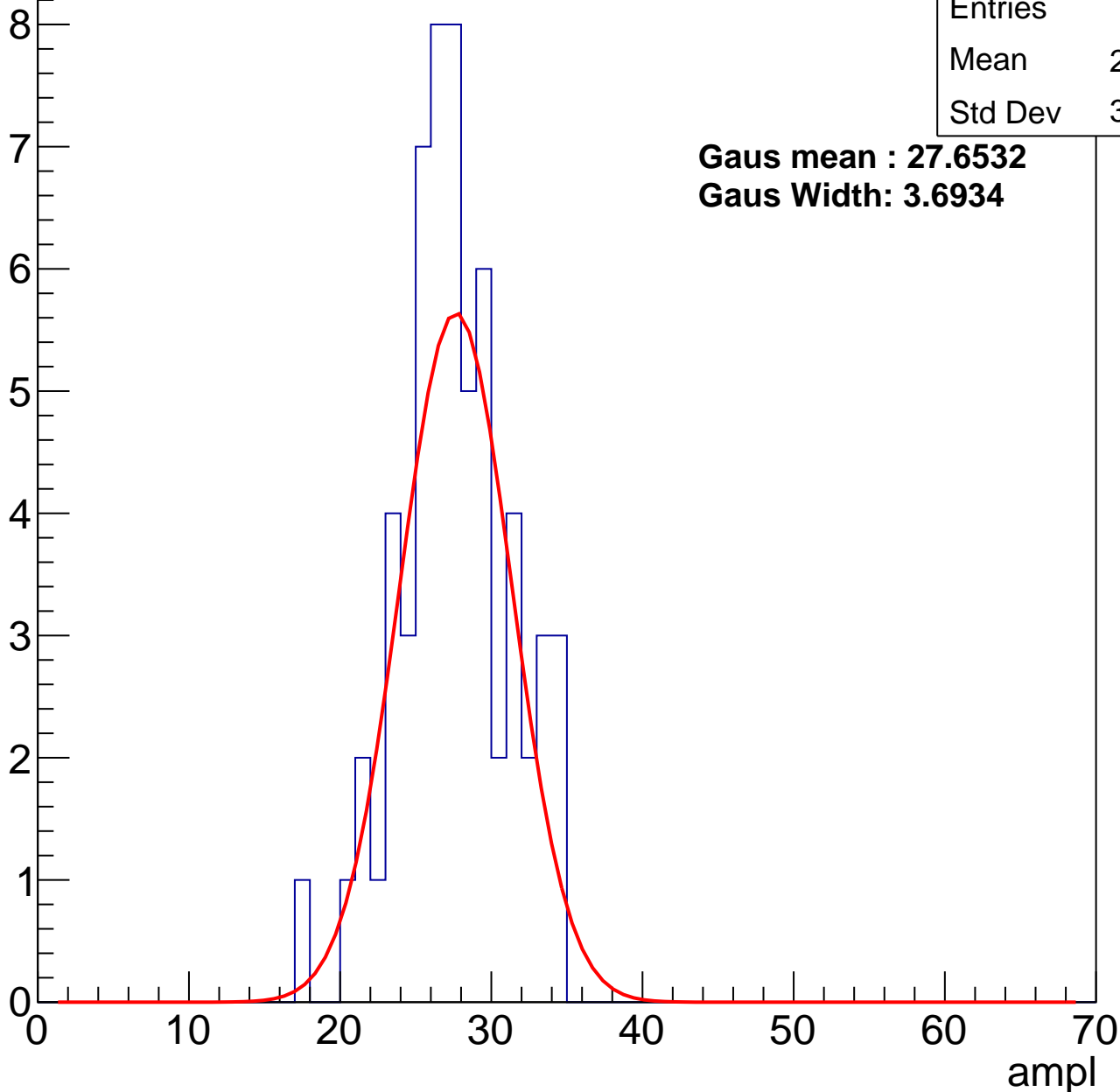
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	27.12
Std Dev	3.624

Gaus mean : 27.6532

Gaus Width: 3.6934



B1L102S, U17-ch65, adc1

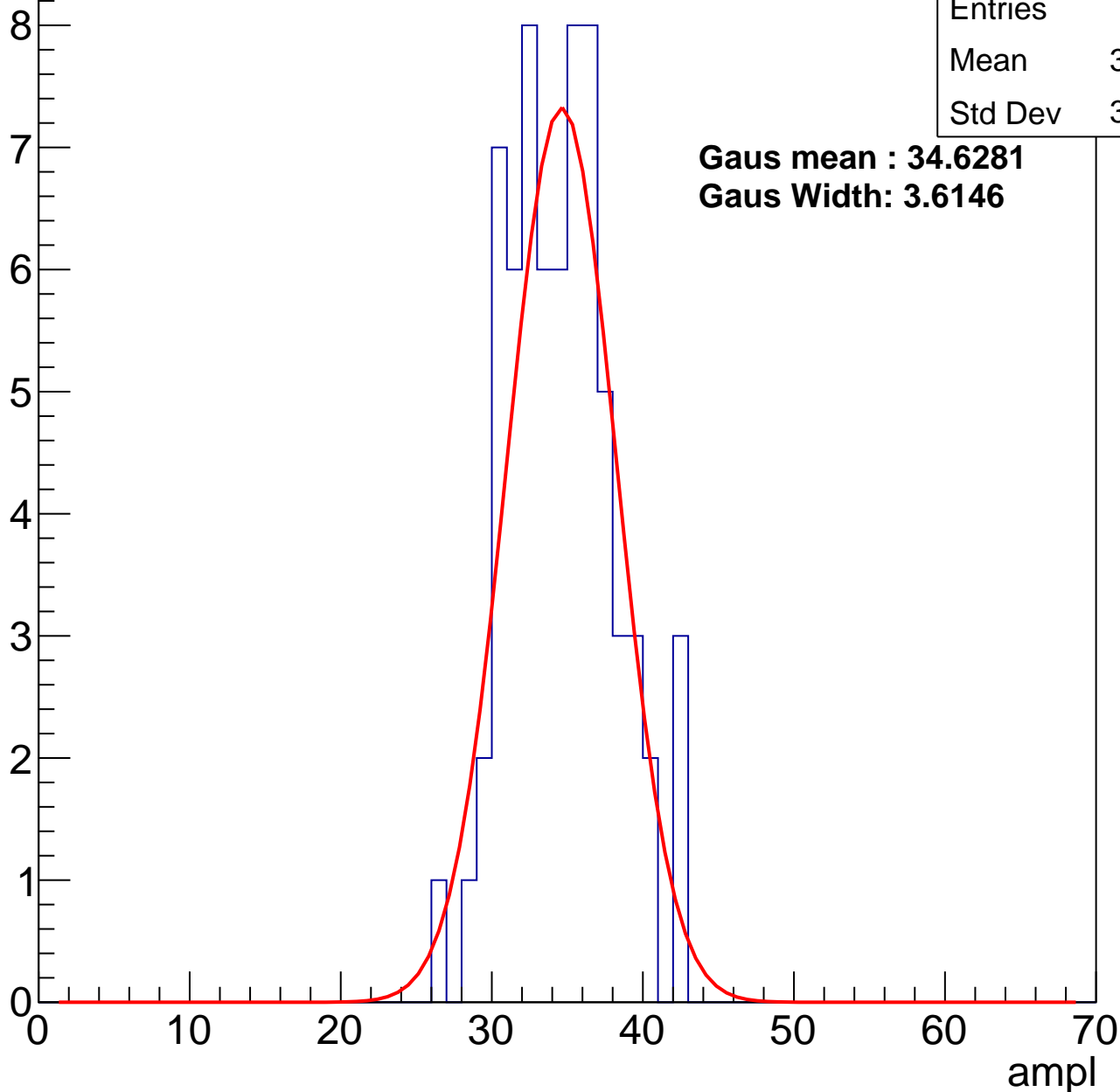
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.14
Std Dev	3.457

Gaus mean : 34.6281

Gaus Width: 3.6146



B1L102S, U17-ch65, adc2

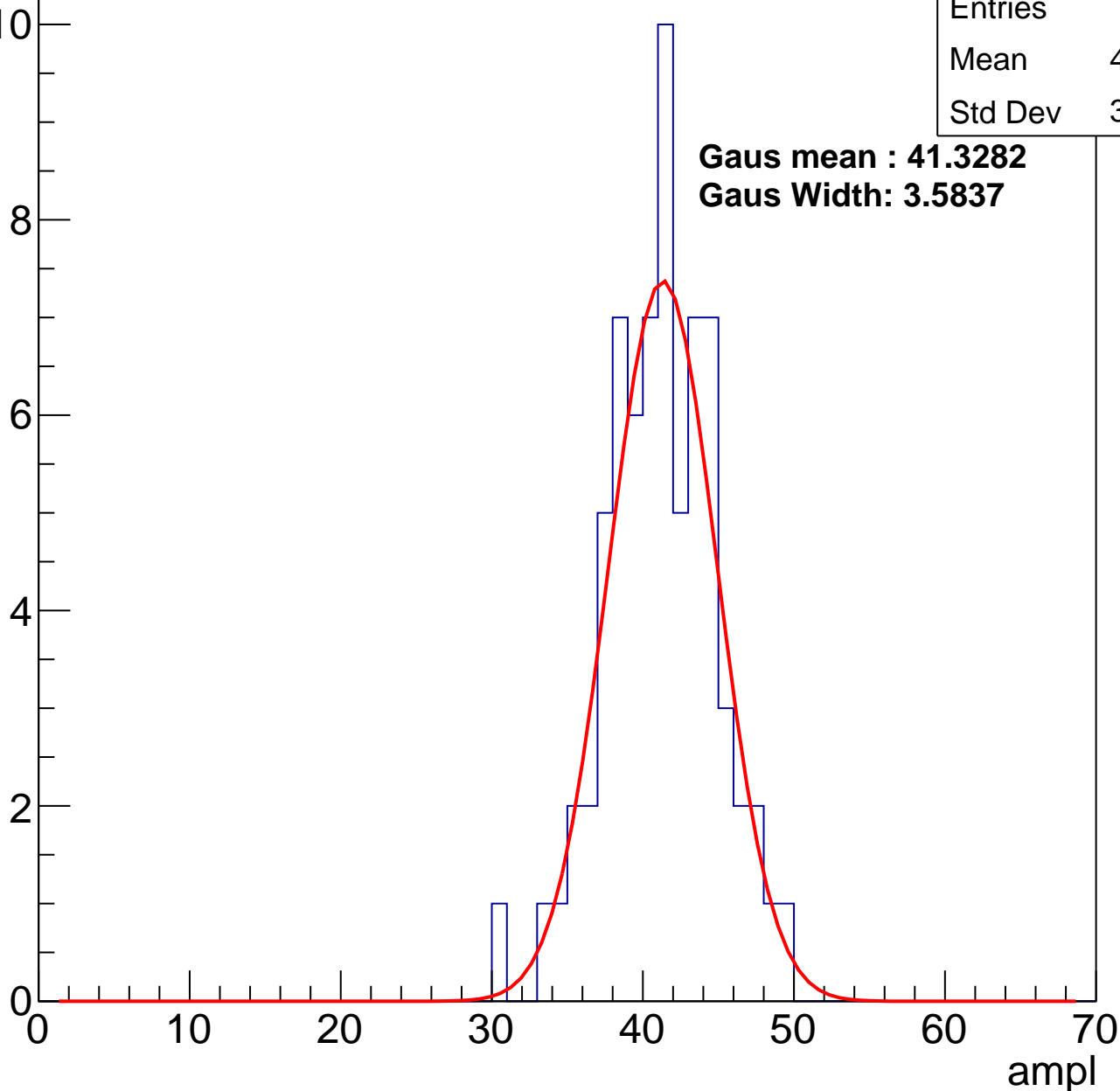
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40.73
Std Dev	3.593

Gaus mean : 41.3282

Gaus Width: 3.5837

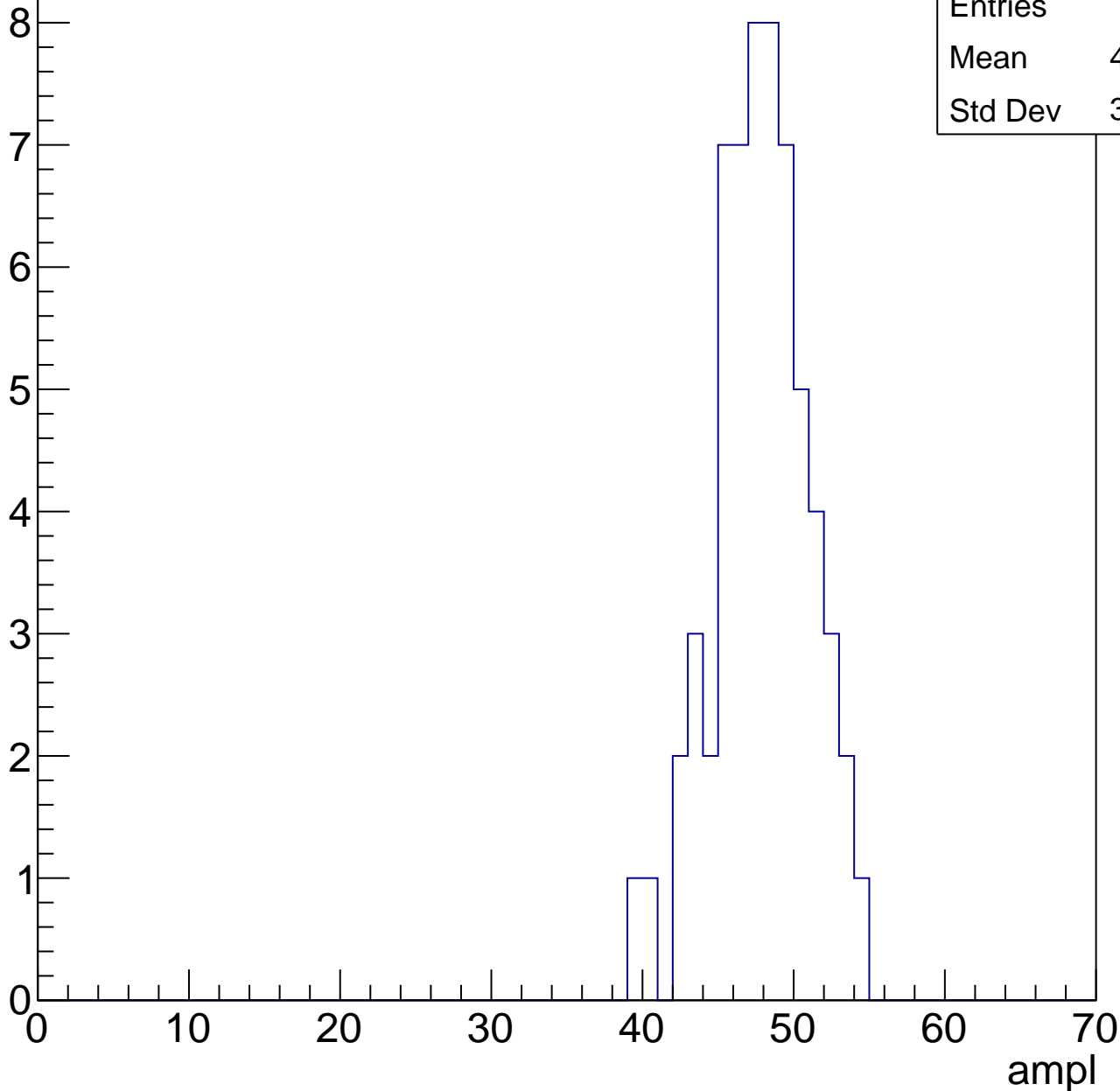


B1L102S, U17-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

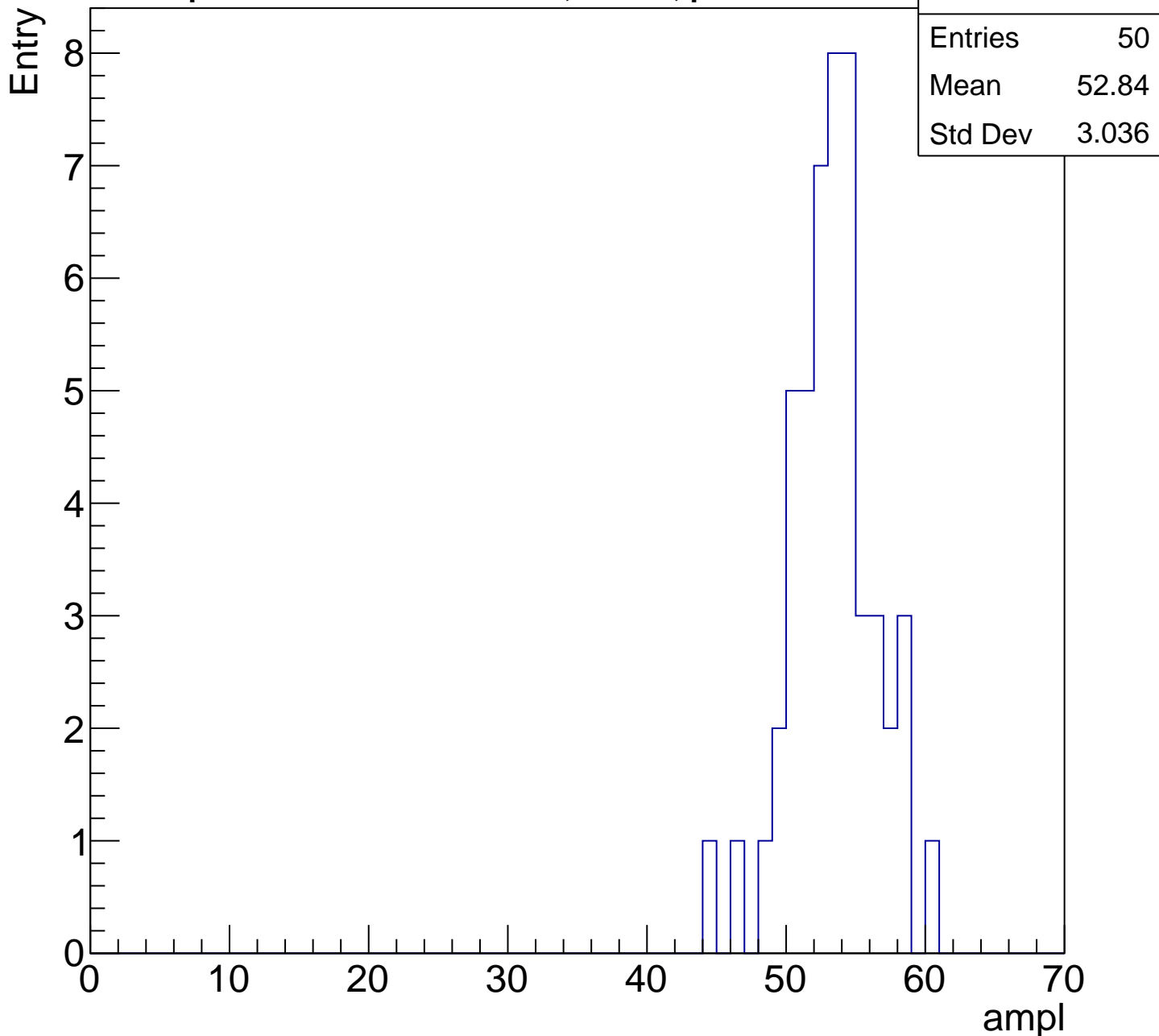
Entry

Entries	61
Mean	47.38
Std Dev	3.127



B1L102S, U17-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

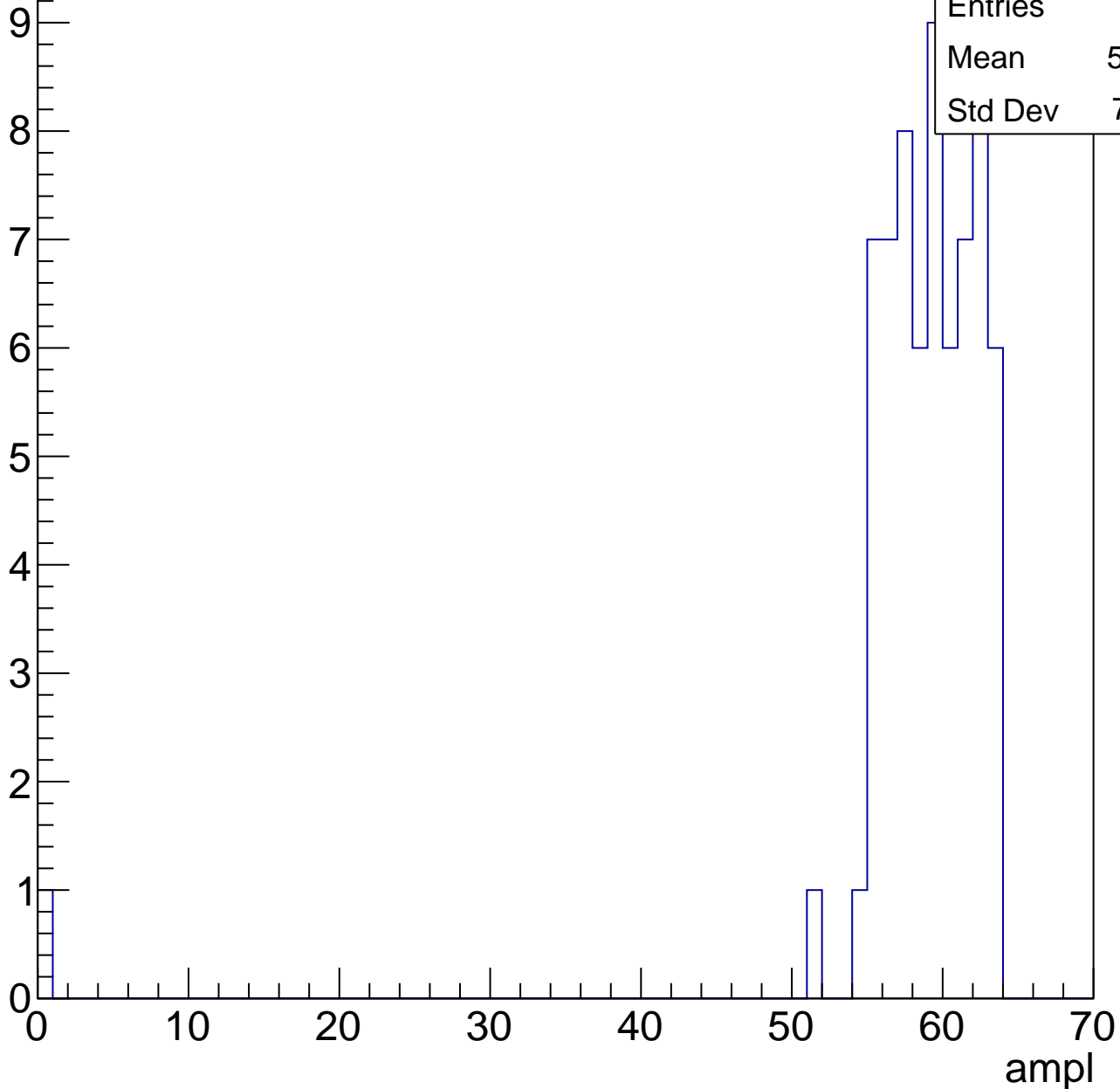


B1L102S, U17-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

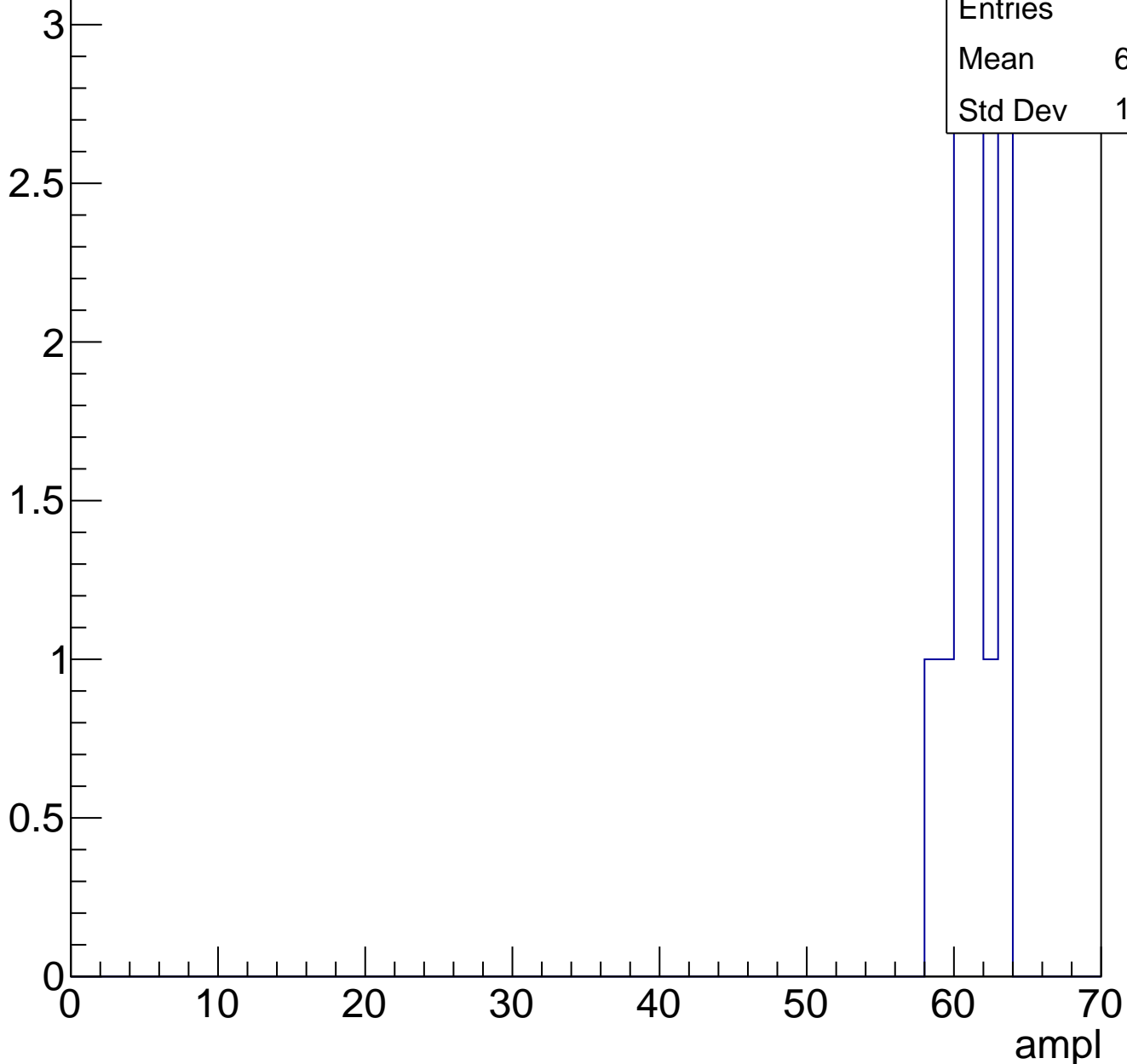
Entries	67
Mean	57.88
Std Dev	7.631



B1L102S, U17-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

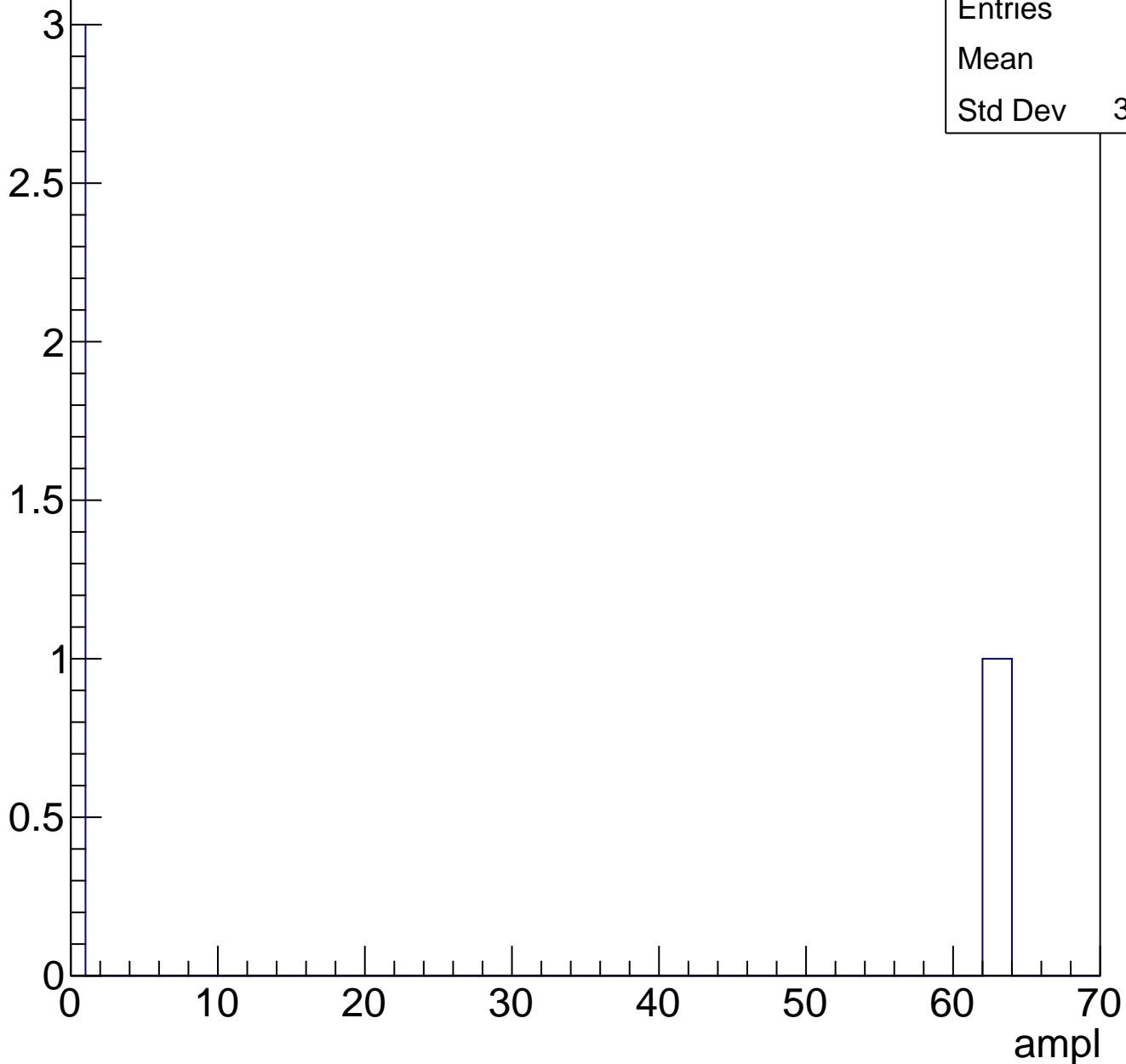
Entry



B1L102S, U17-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch66, adc0

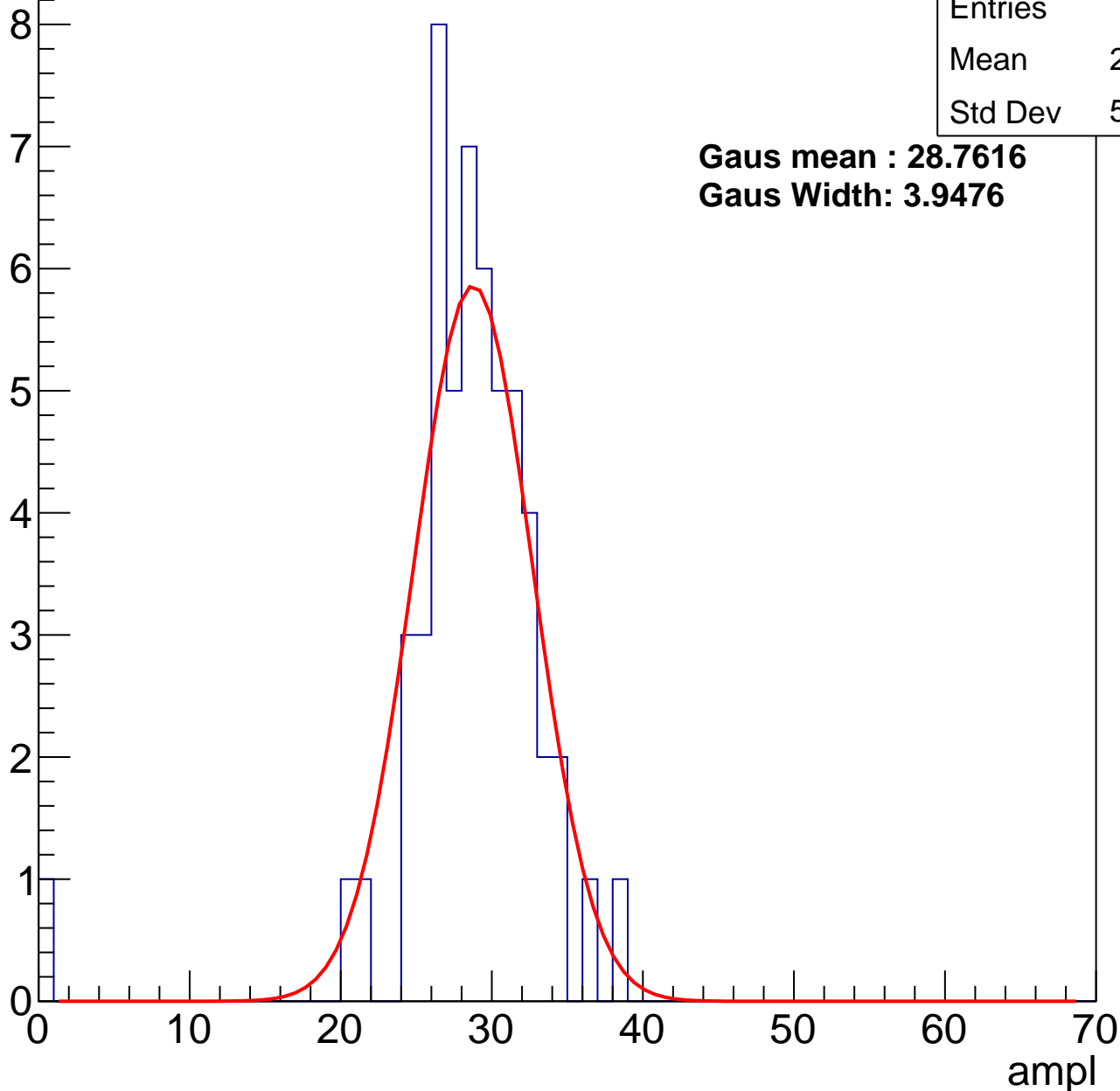
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.04
Std Dev	5.099

Gaus mean : 28.7616

Gaus Width: 3.9476



B1L102S, U17-ch66, adc1

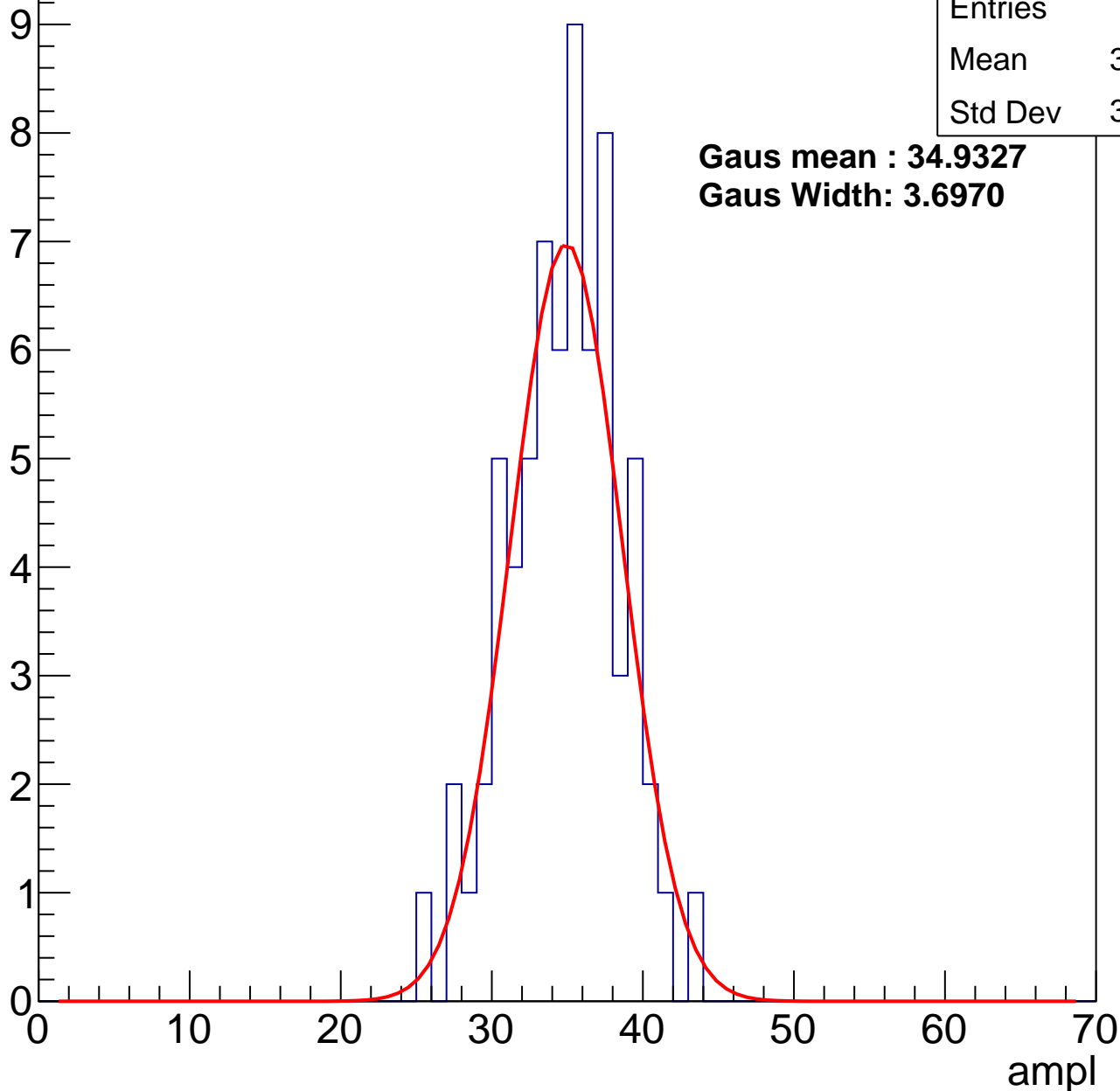
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.32
Std Dev	3.599

Gaus mean : 34.9327

Gaus Width: 3.6970



B1L102S, U17-ch66, adc2

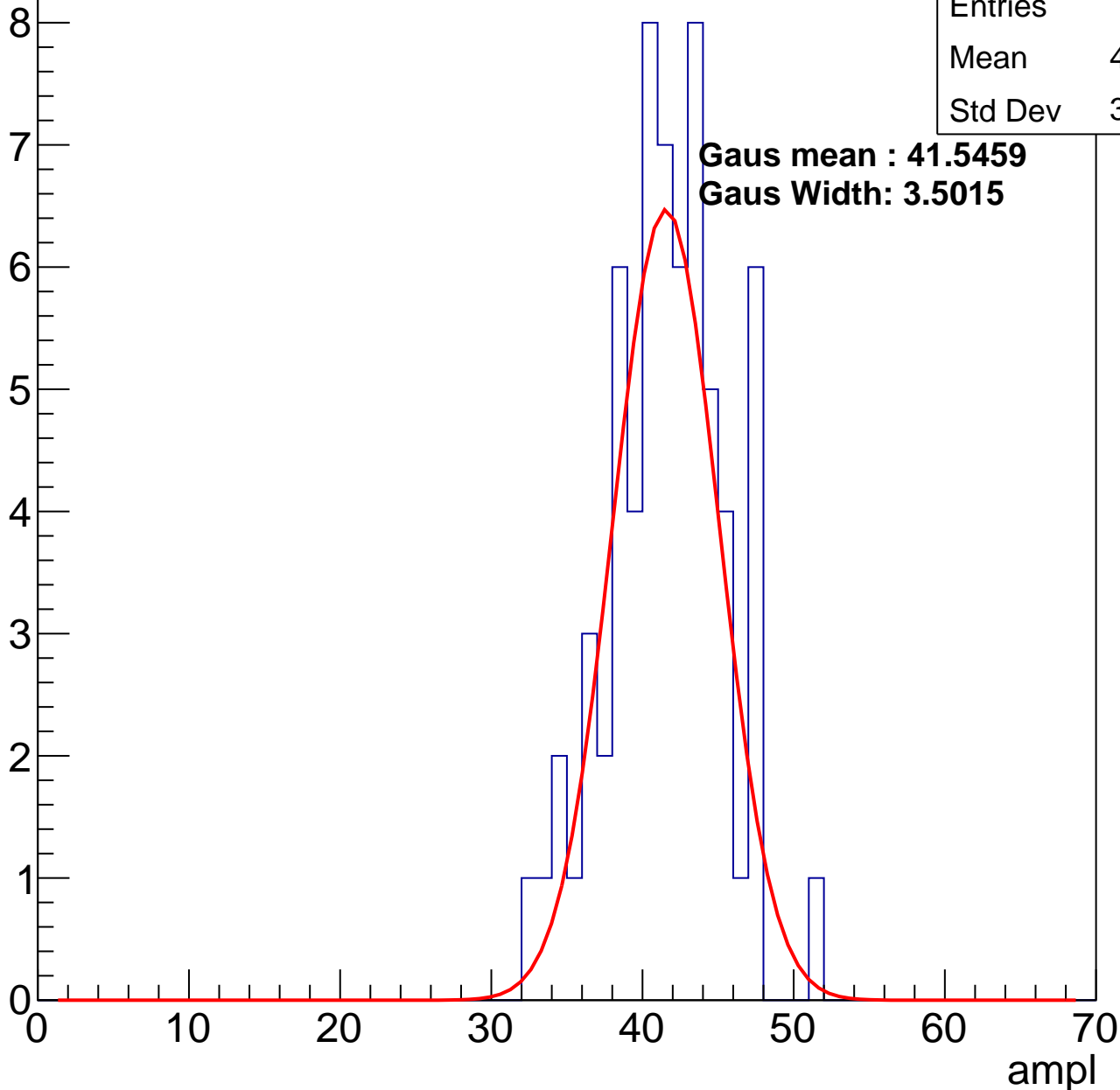
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.15
Std Dev	3.795

Gaus mean : 41.5459

Gaus Width: 3.5015

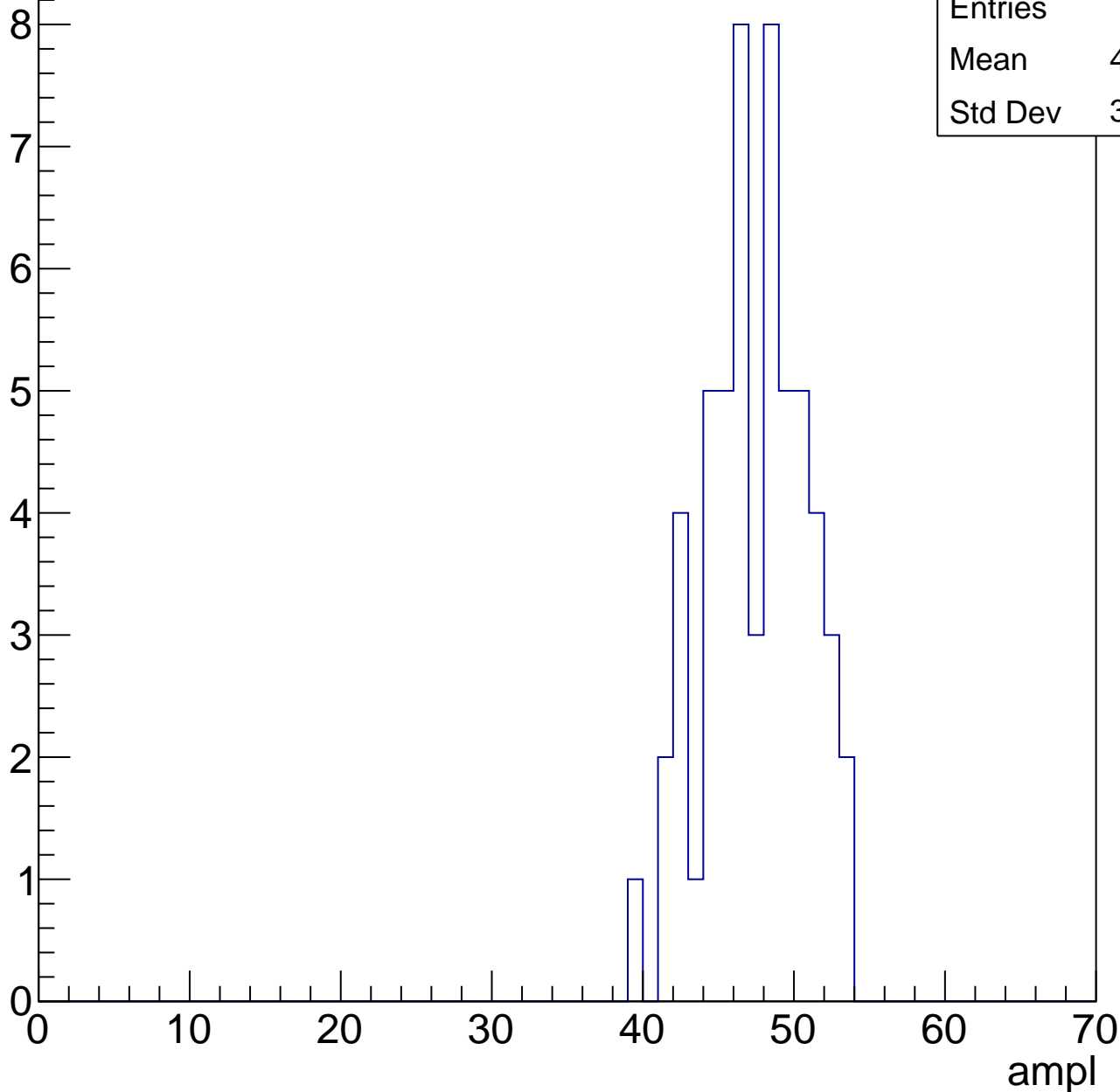


B1L102S, U17-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	46.98
Std Dev	3.298

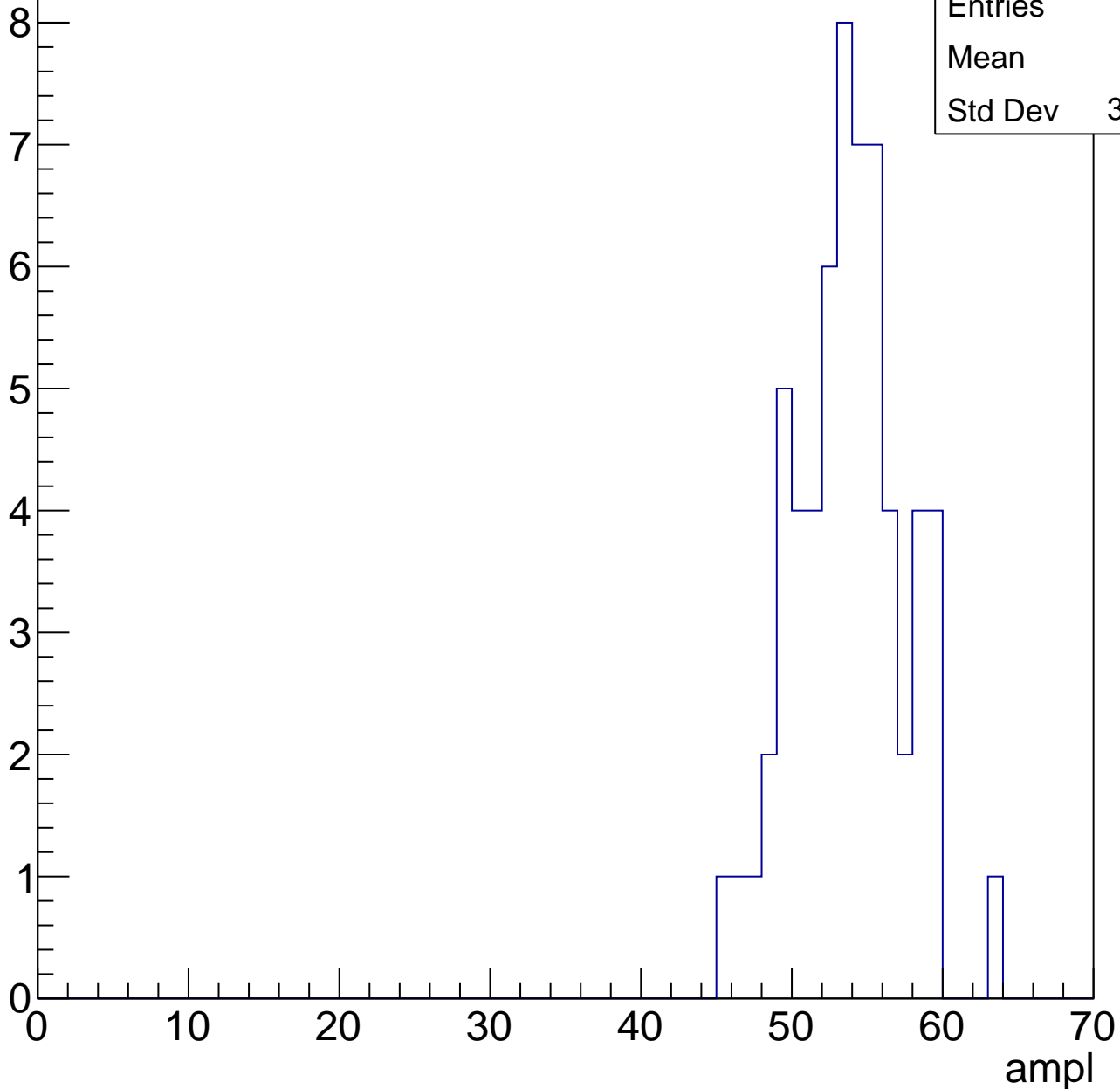


B1L102S, U17-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

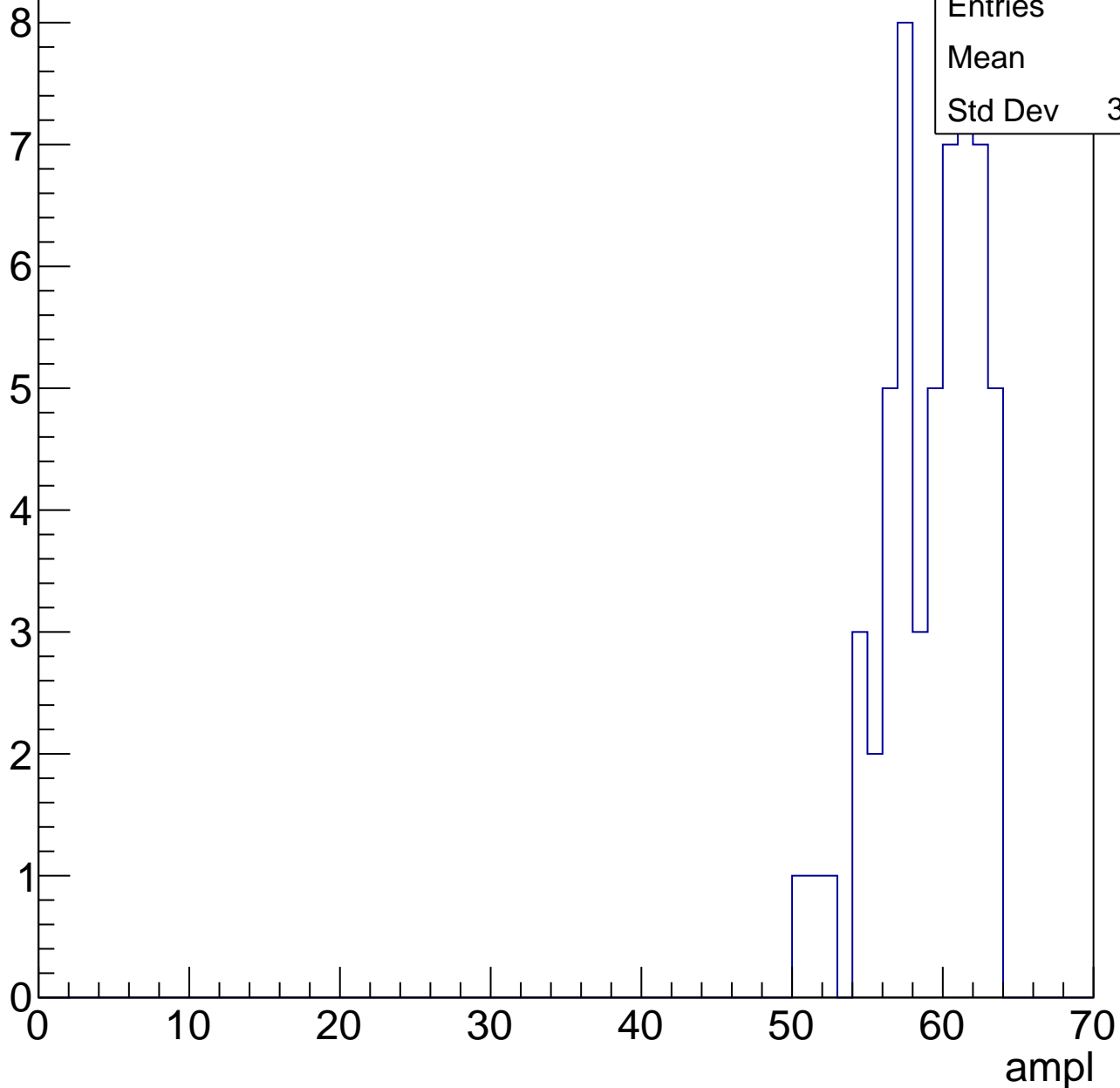
Entries	61
Mean	53.3
Std Dev	3.573



B1L102S, U17-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

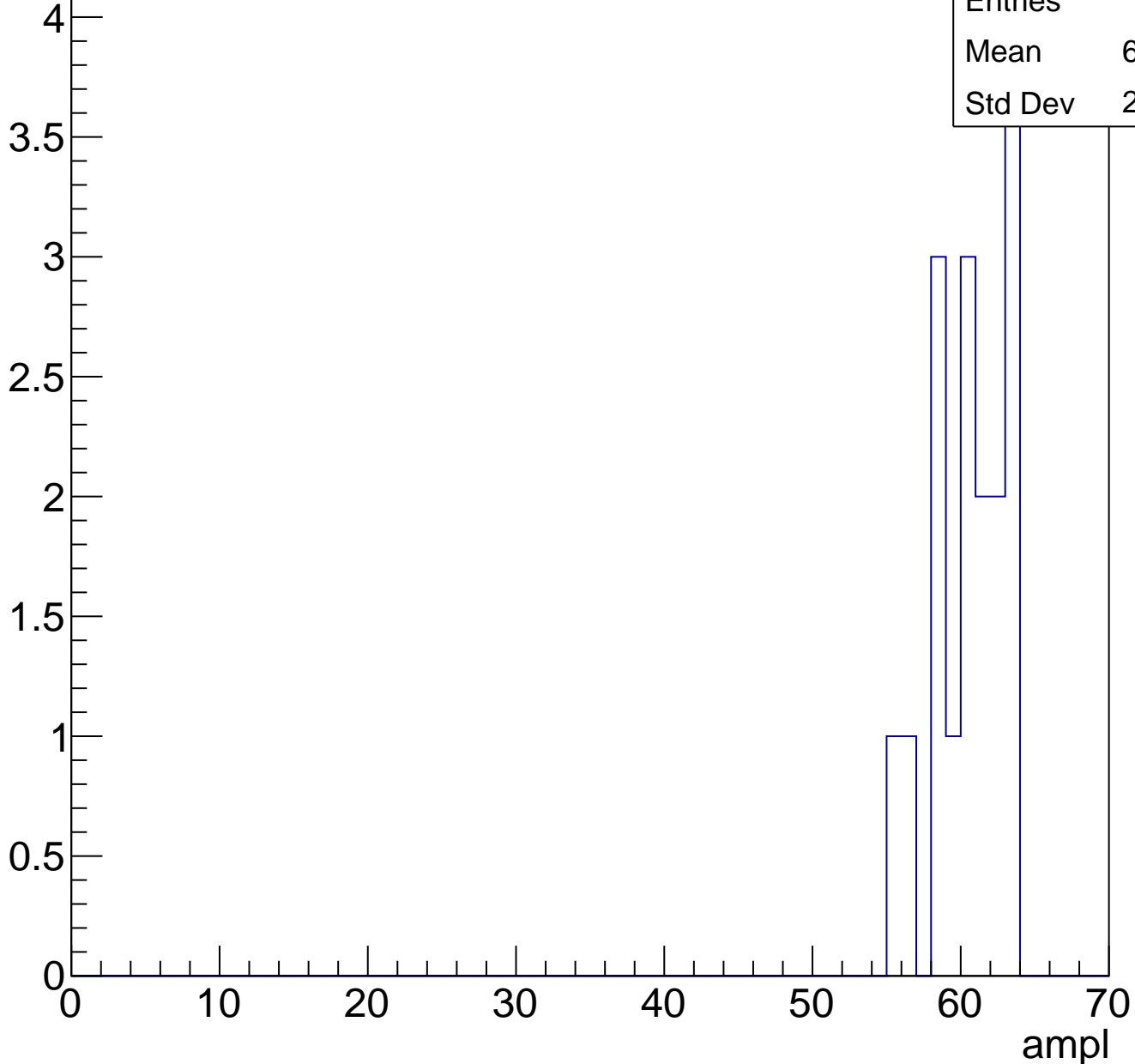
Entry



B1L102S, U17-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L102S, U17-ch67, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	28.49
Std Dev	4.793

Gaus mean : 29.5515

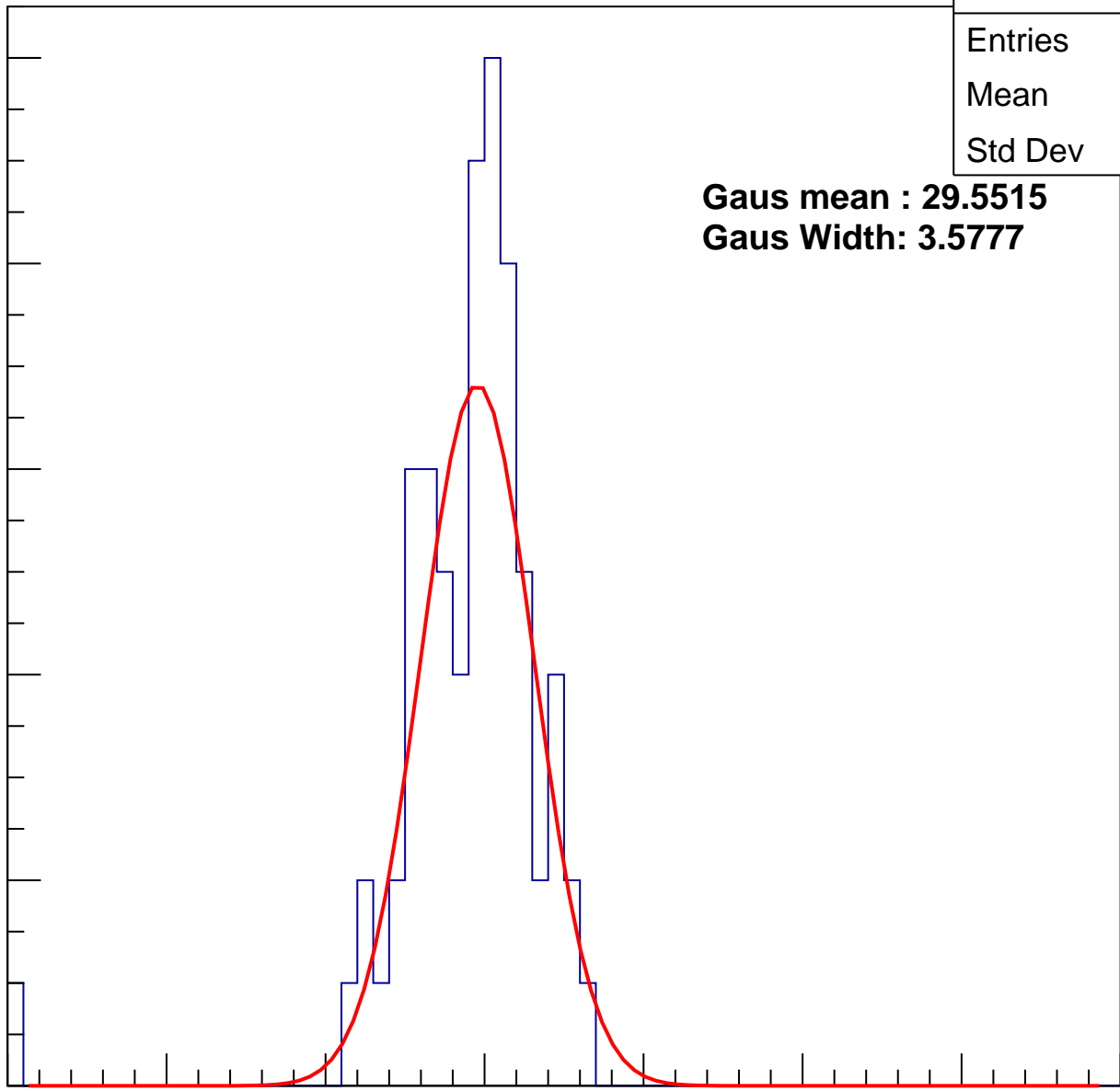
Gaus Width: 3.5777

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch67, adc1

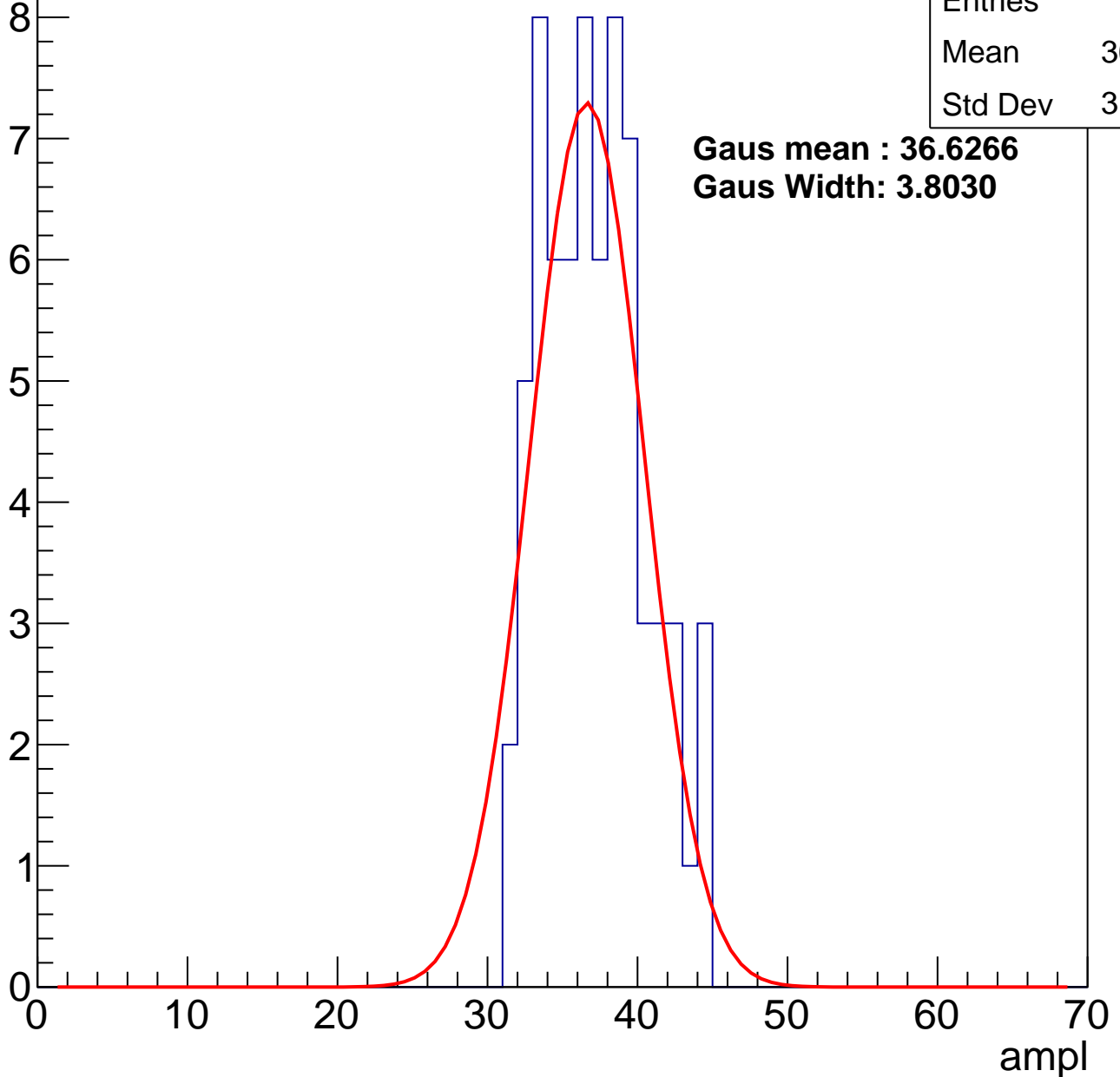
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.68
Std Dev	3.347

Gaus mean : 36.6266

Gaus Width: 3.8030



B1L102S, U17-ch67, adc2

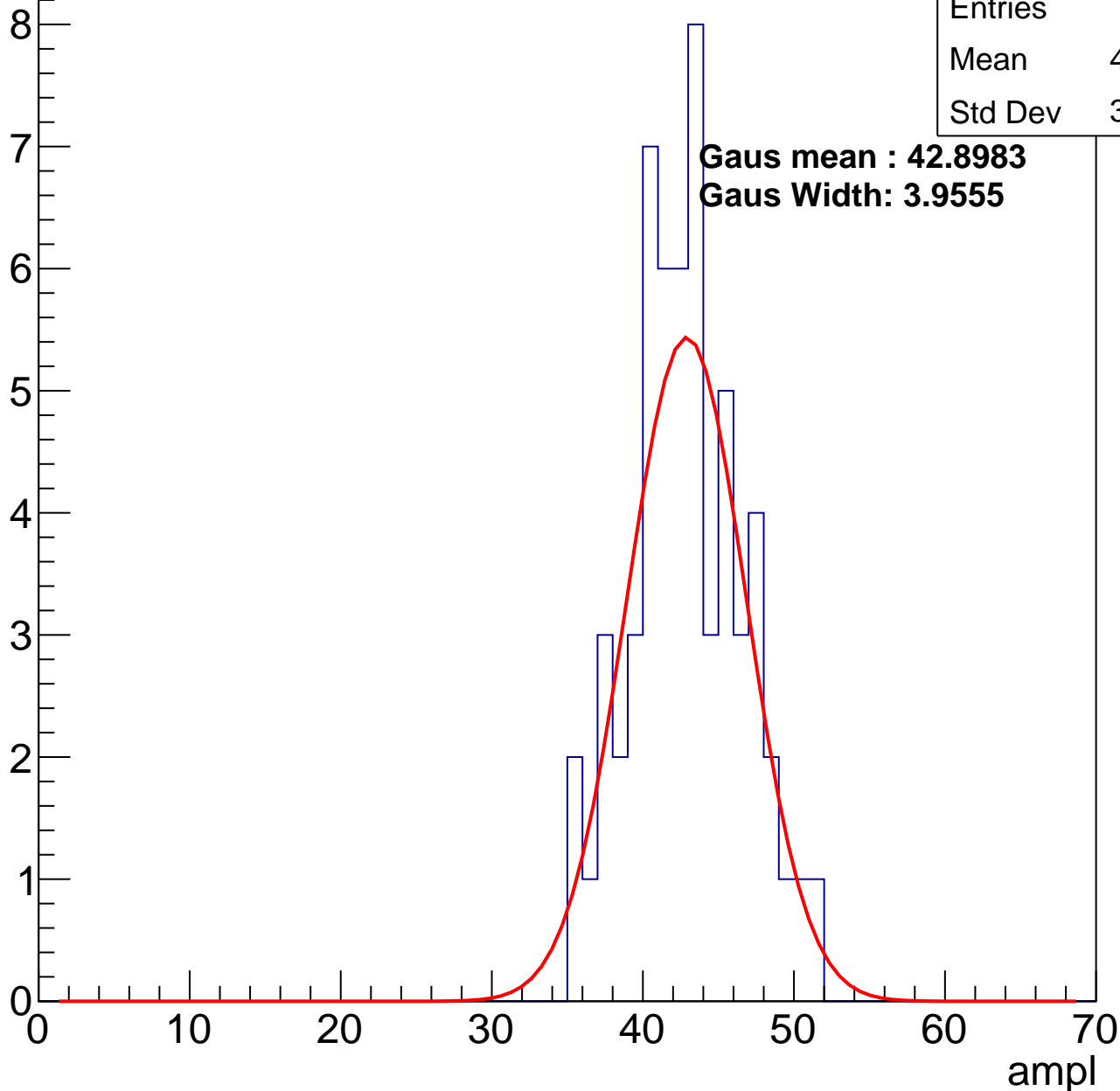
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.43
Std Dev	3.644

Gaus mean : 42.8983

Gaus Width: 3.9555

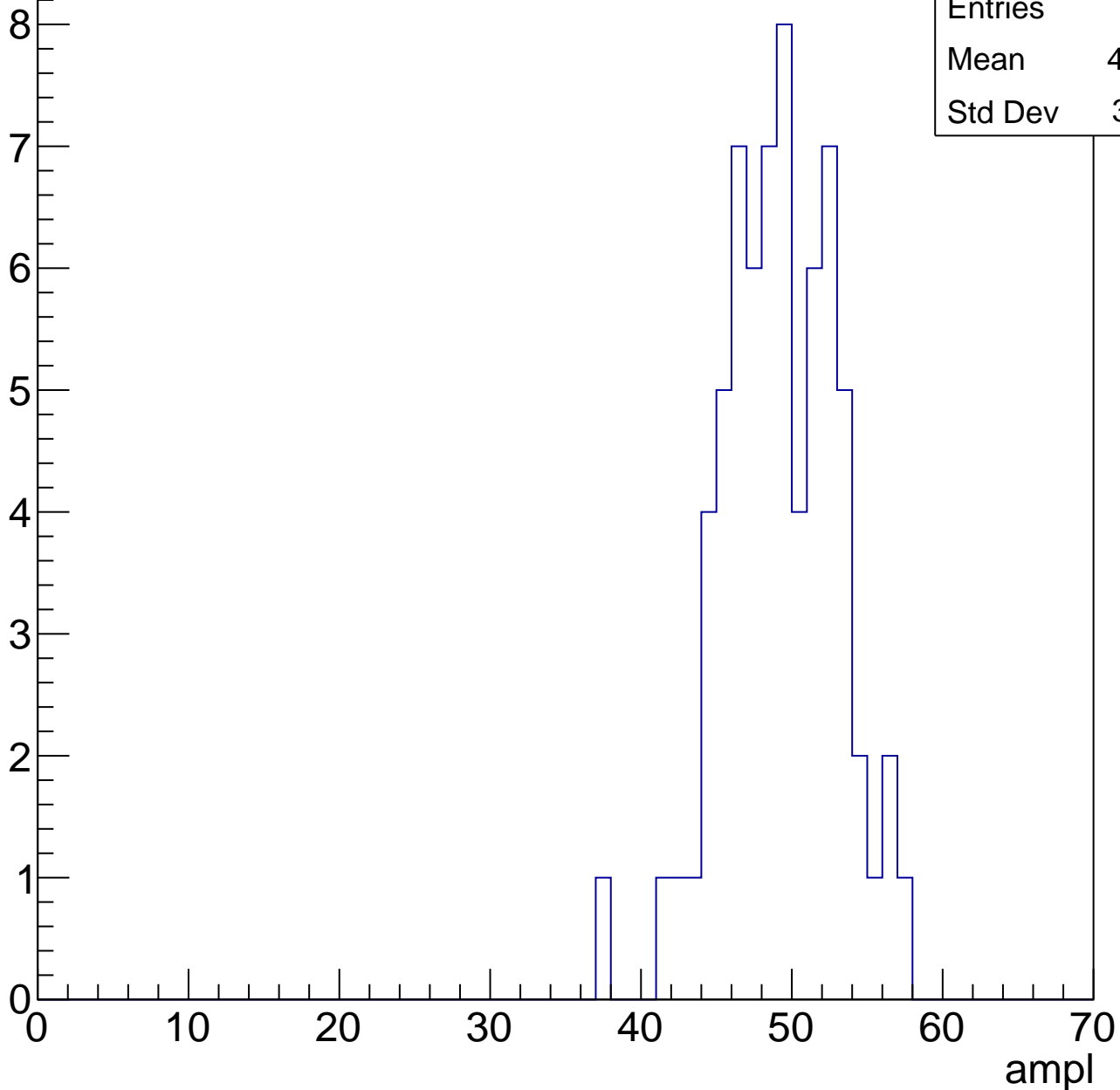


B1L102S, U17-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.74
Std Dev	3.771

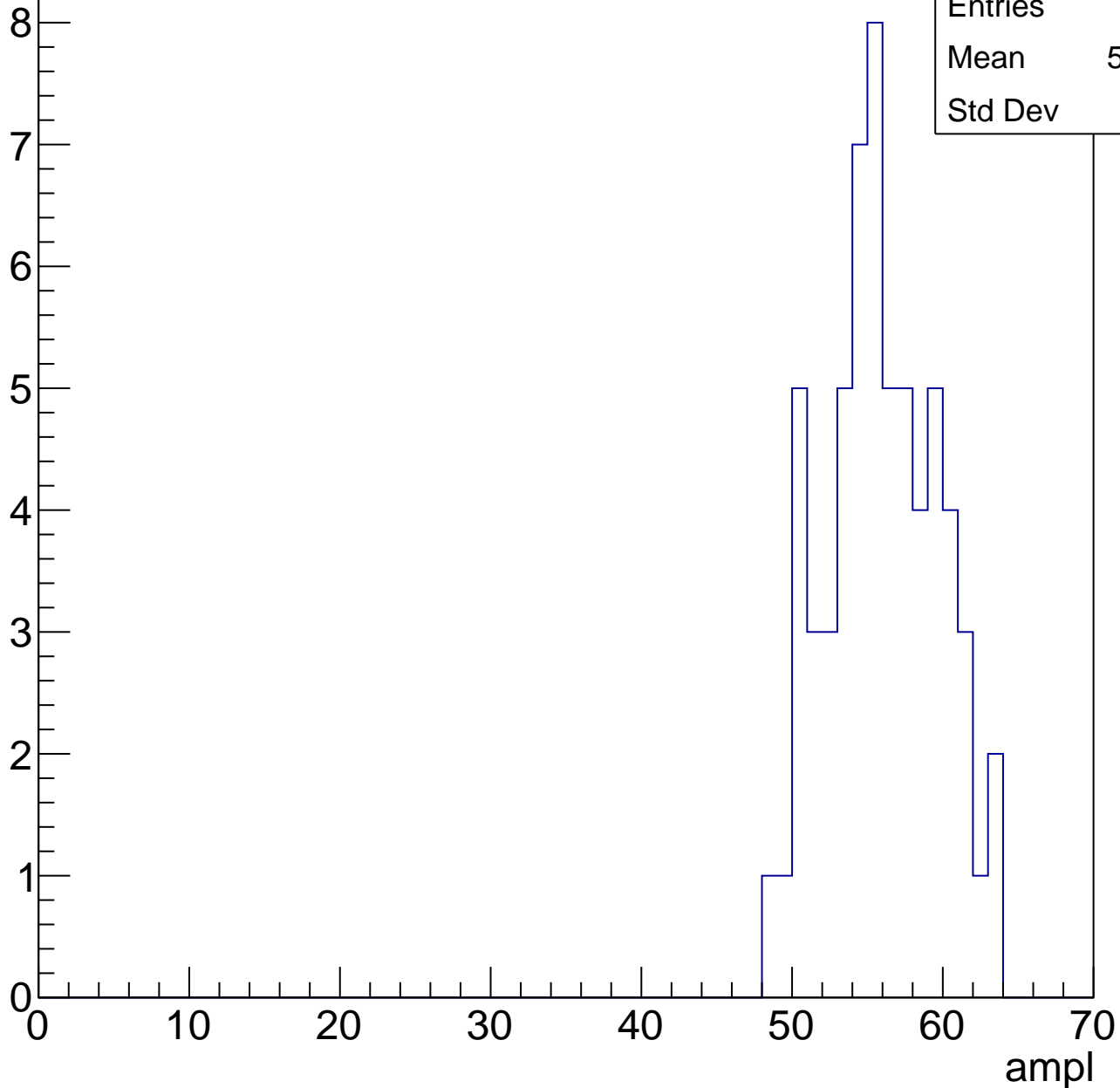


B1L102S, U17-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.52
Std Dev	3.64

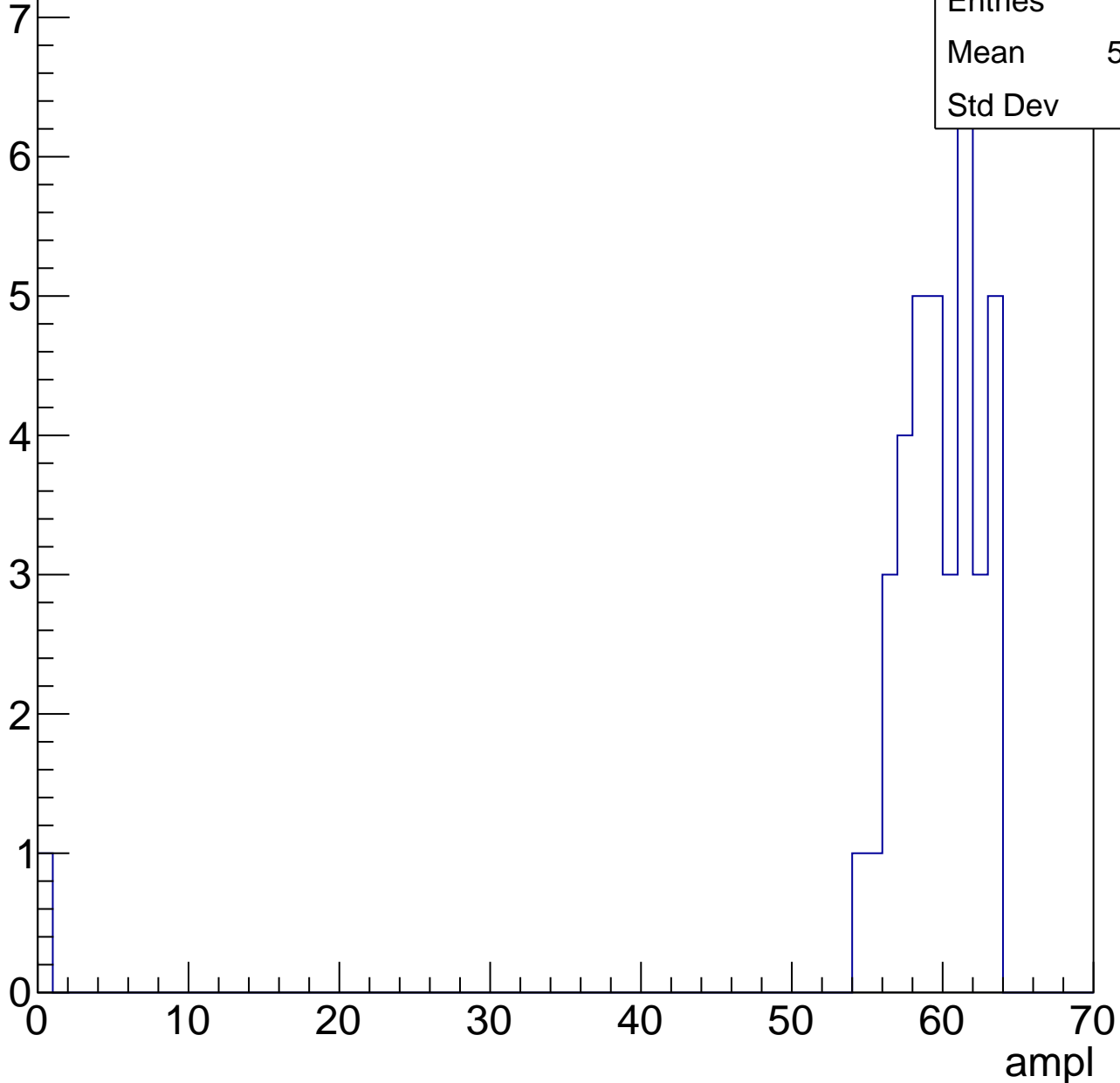


B1L102S, U17-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	57.84
Std Dev	9.81

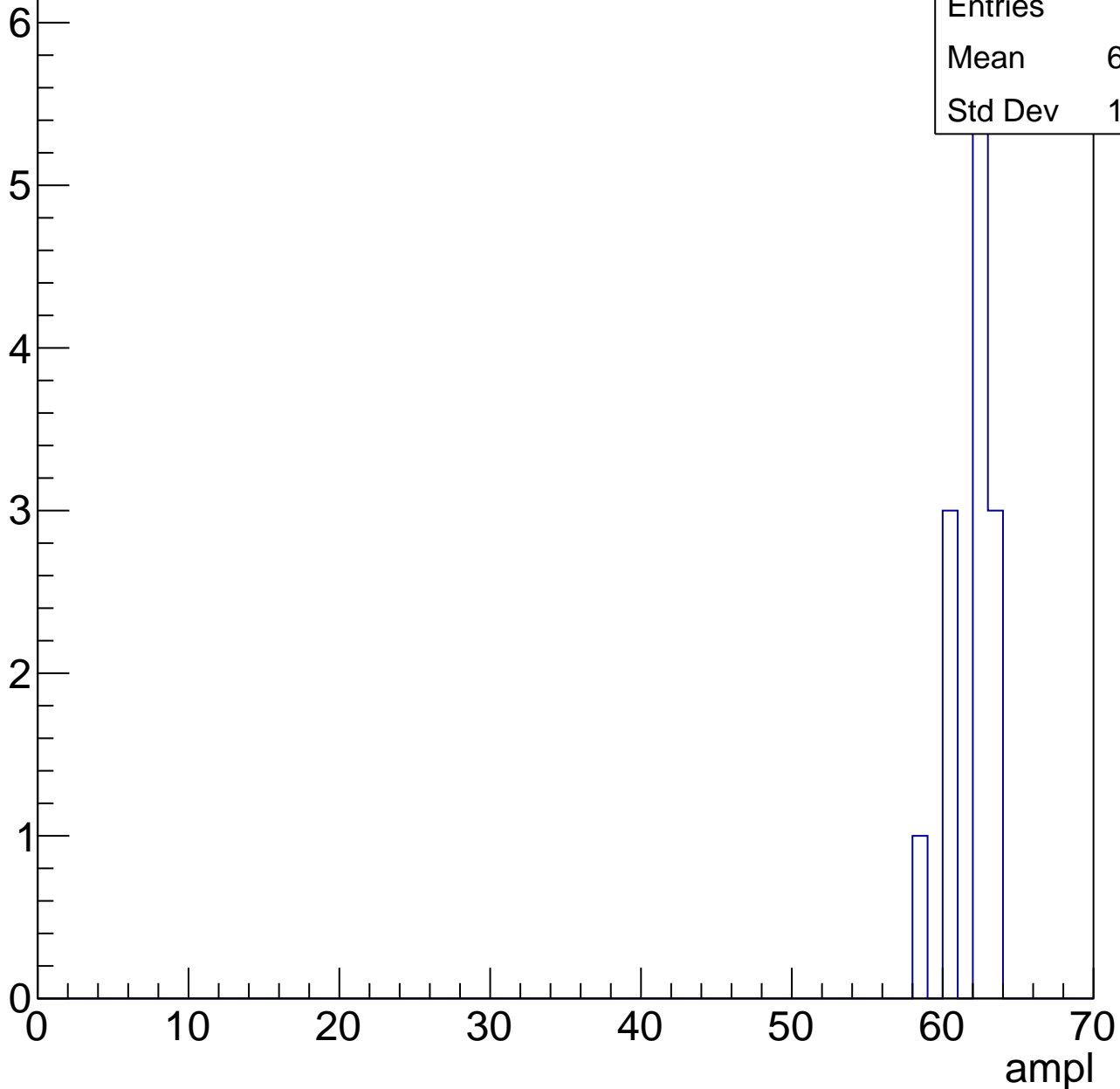


B1L102S, U17-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.46
Std Dev	1.447



B1L102S, U17-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch68, adc0

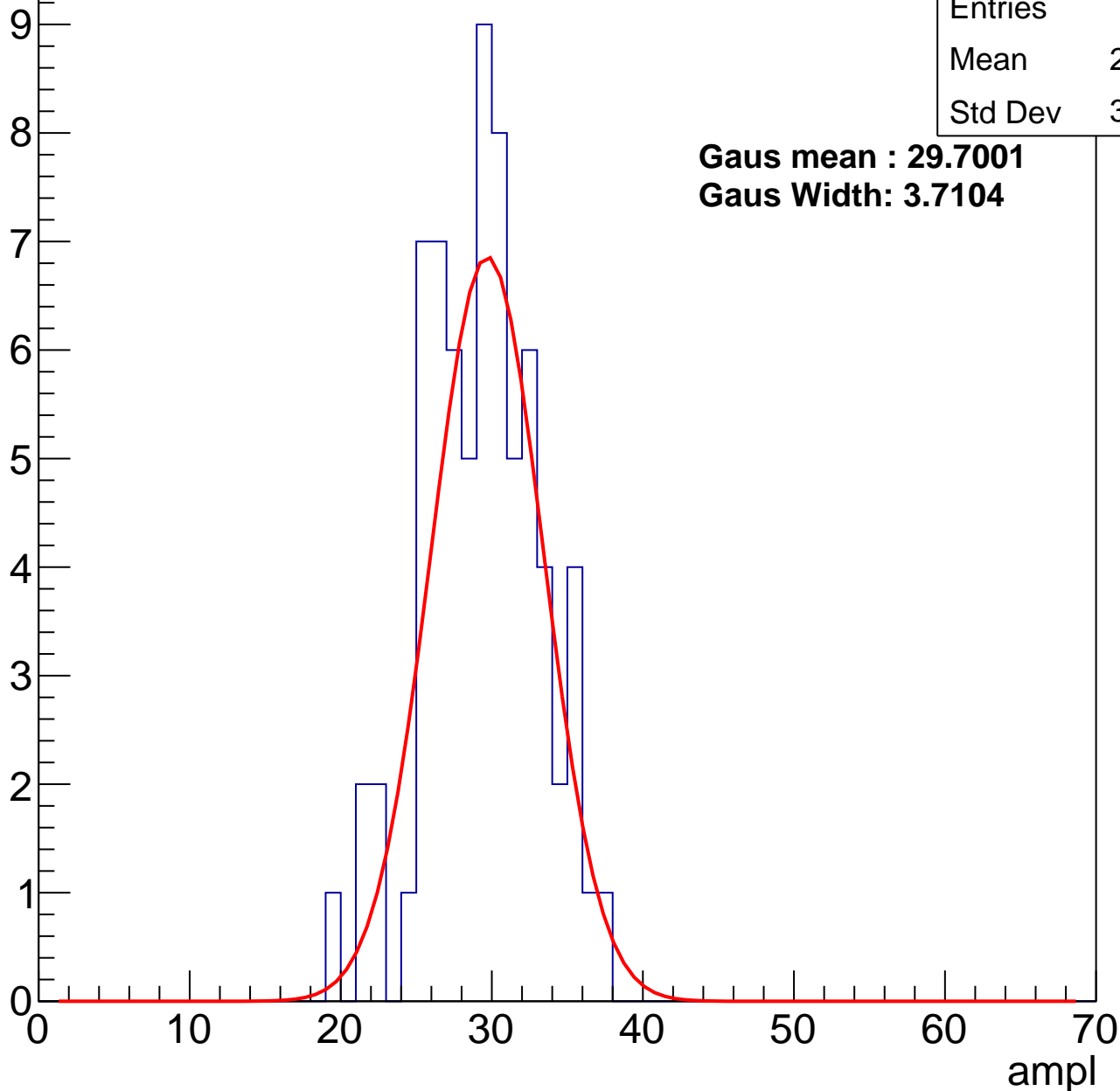
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.86
Std Dev	3.765

Gaus mean : 29.7001

Gaus Width: 3.7104



B1L102S, U17-ch68, adc1

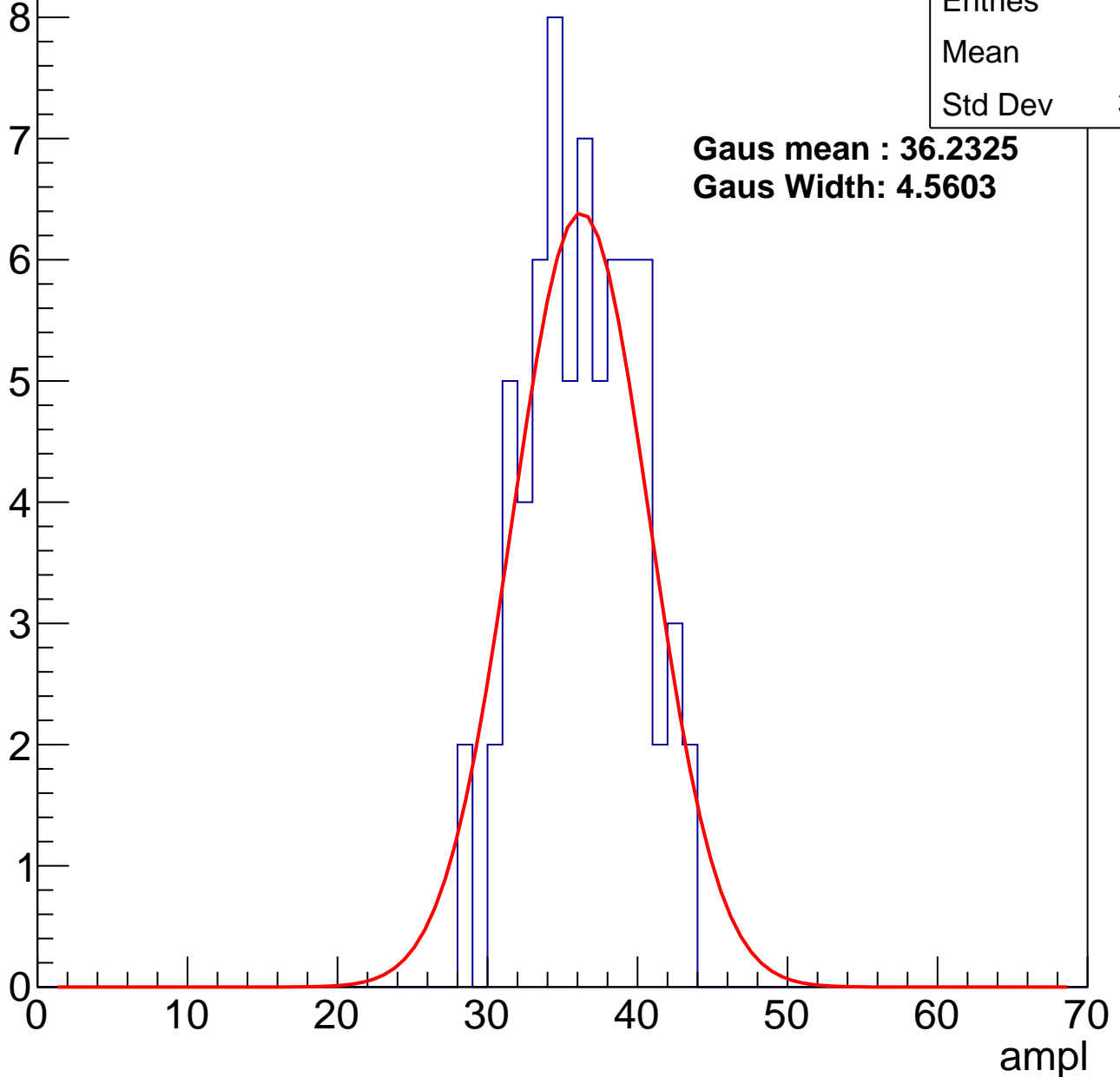
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.9
Std Dev	3.64

Gaus mean : 36.2325

Gaus Width: 4.5603



B1L102S, U17-ch68, adc2

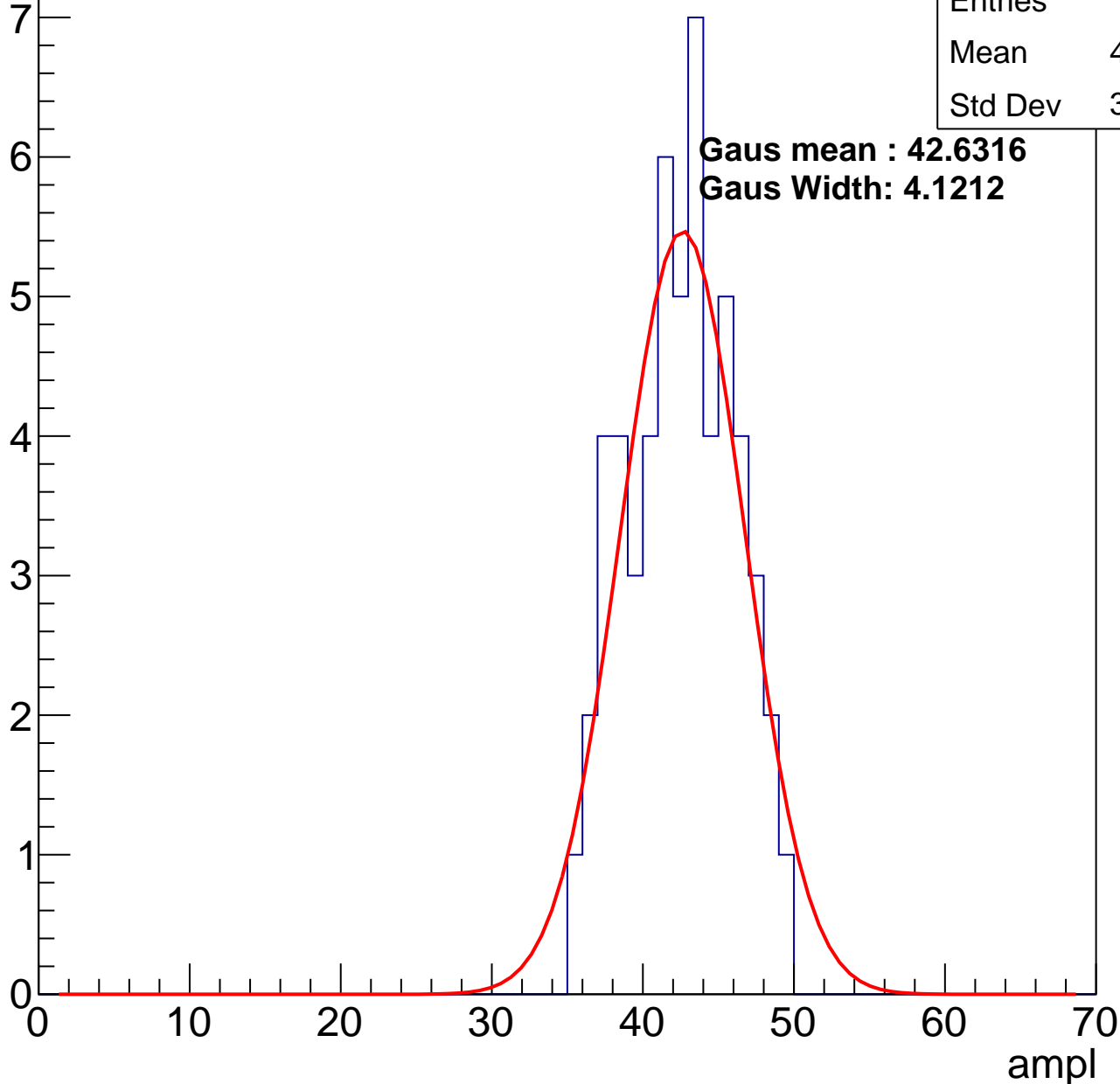
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.04
Std Dev	3.469

Gaus mean : 42.6316

Gaus Width: 4.1212

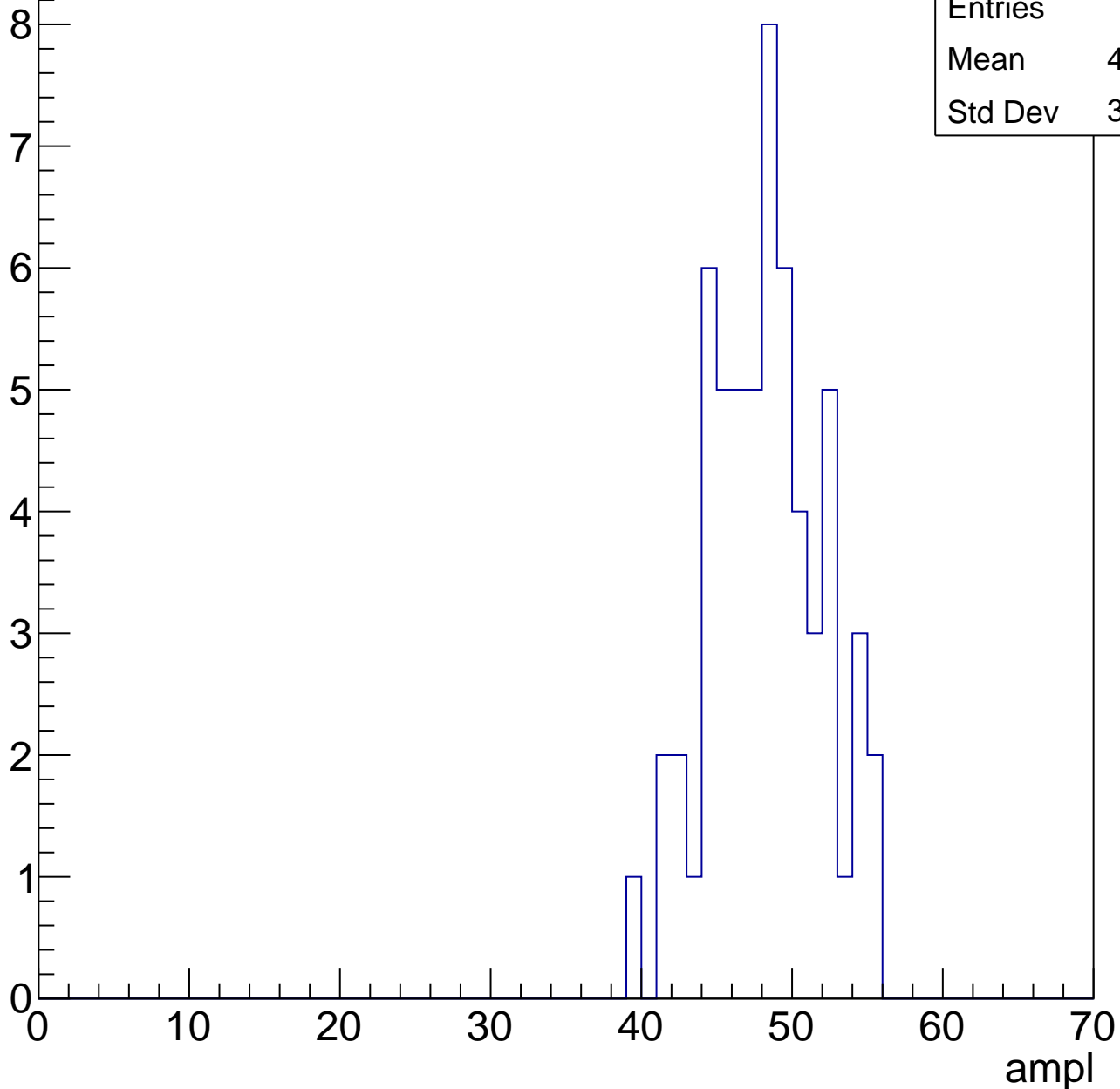


B1L102S, U17-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.76
Std Dev	3.679

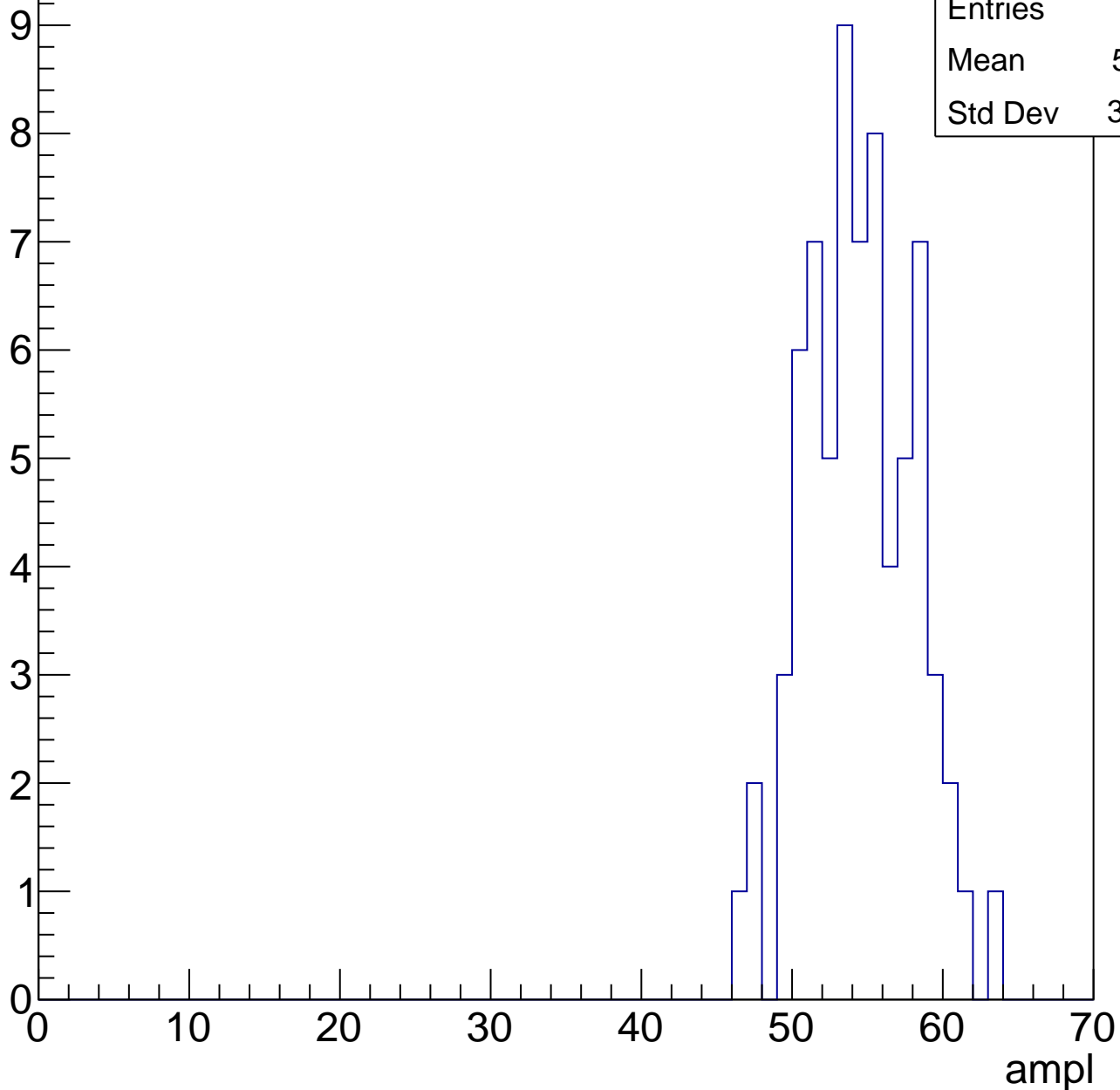


B1L102S, U17-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.01
Std Dev	3.527

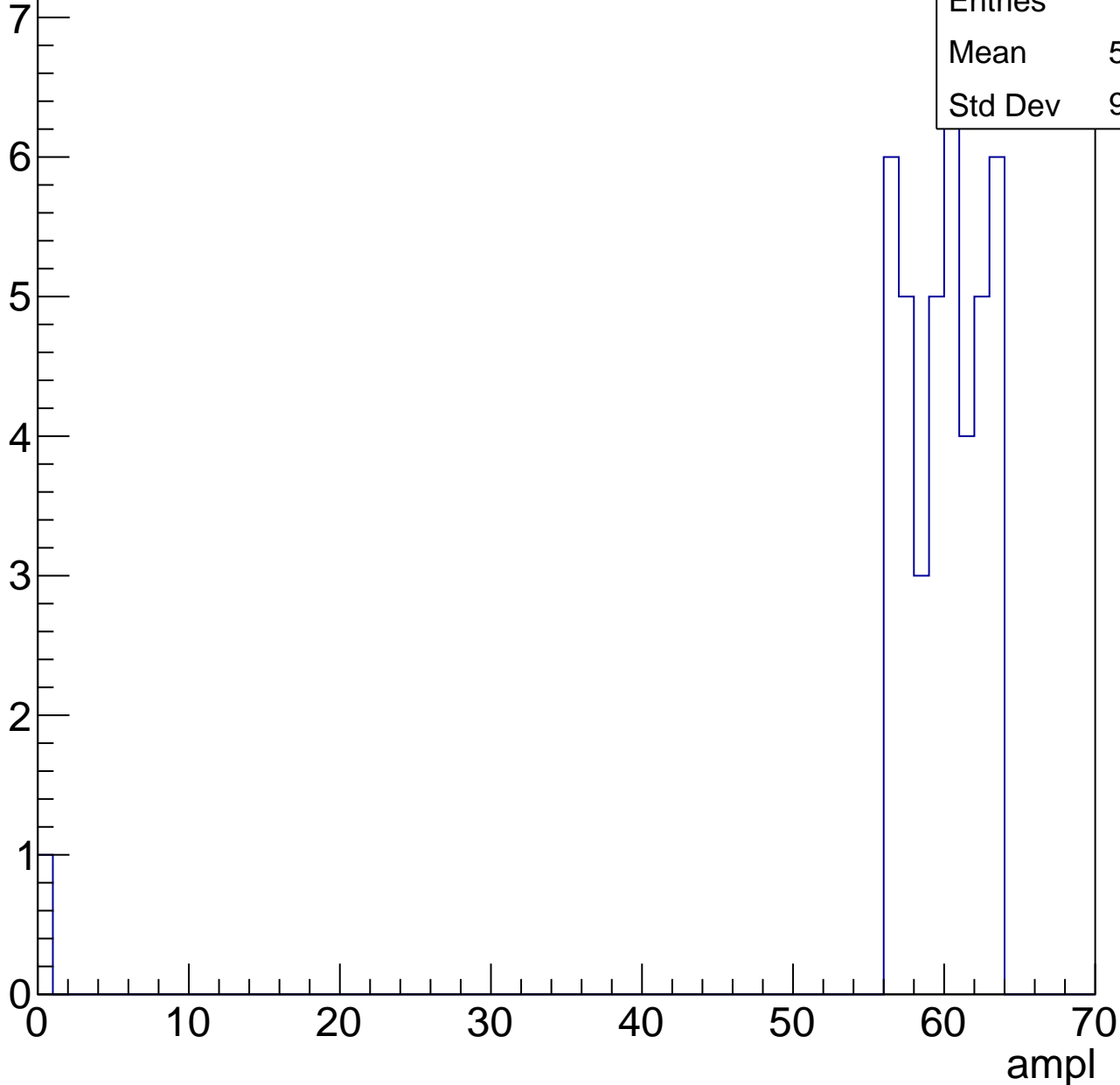


B1L102S, U17-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

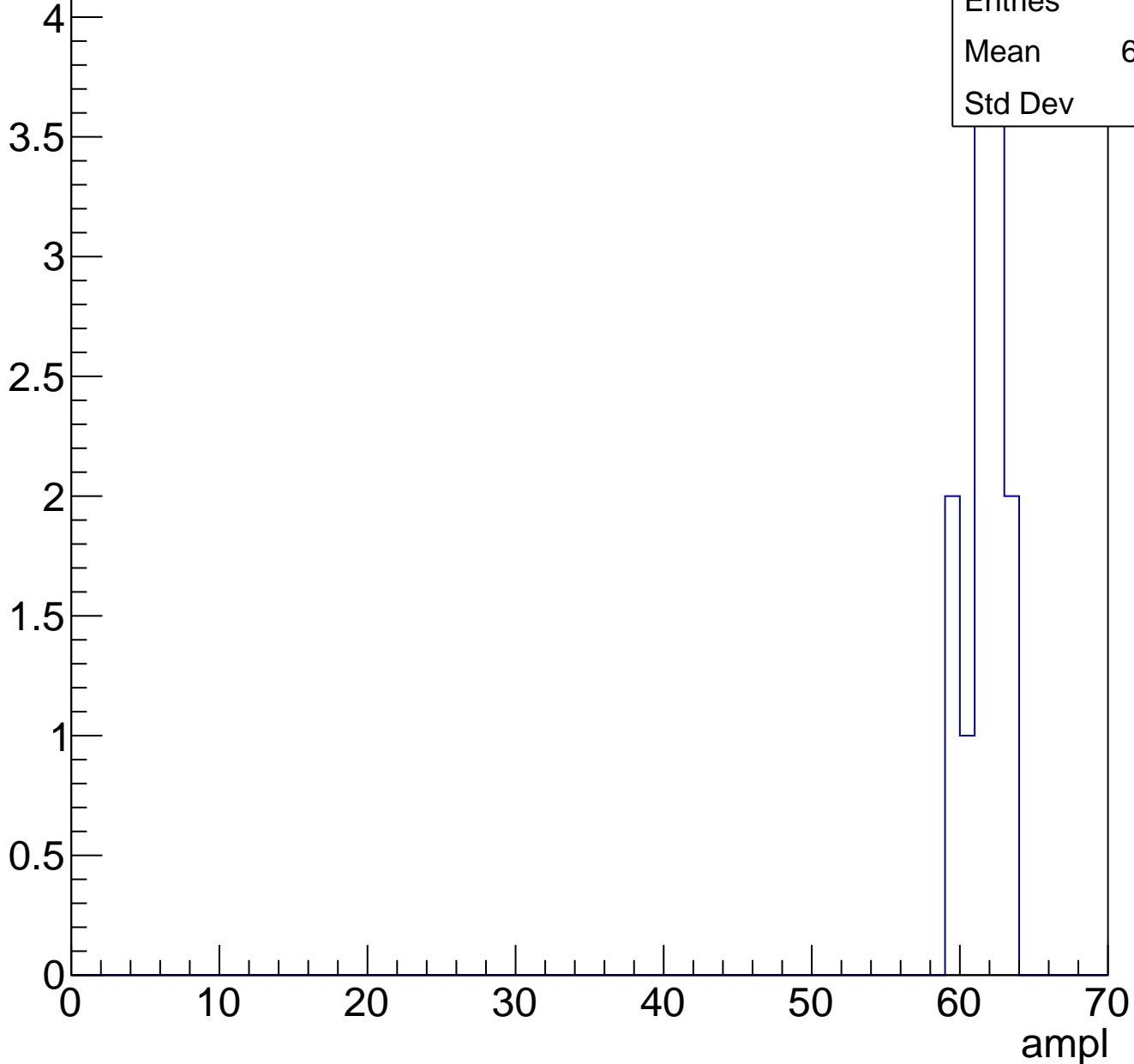
Entries	42
Mean	58.14
Std Dev	9.375



B1L102S, U17-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

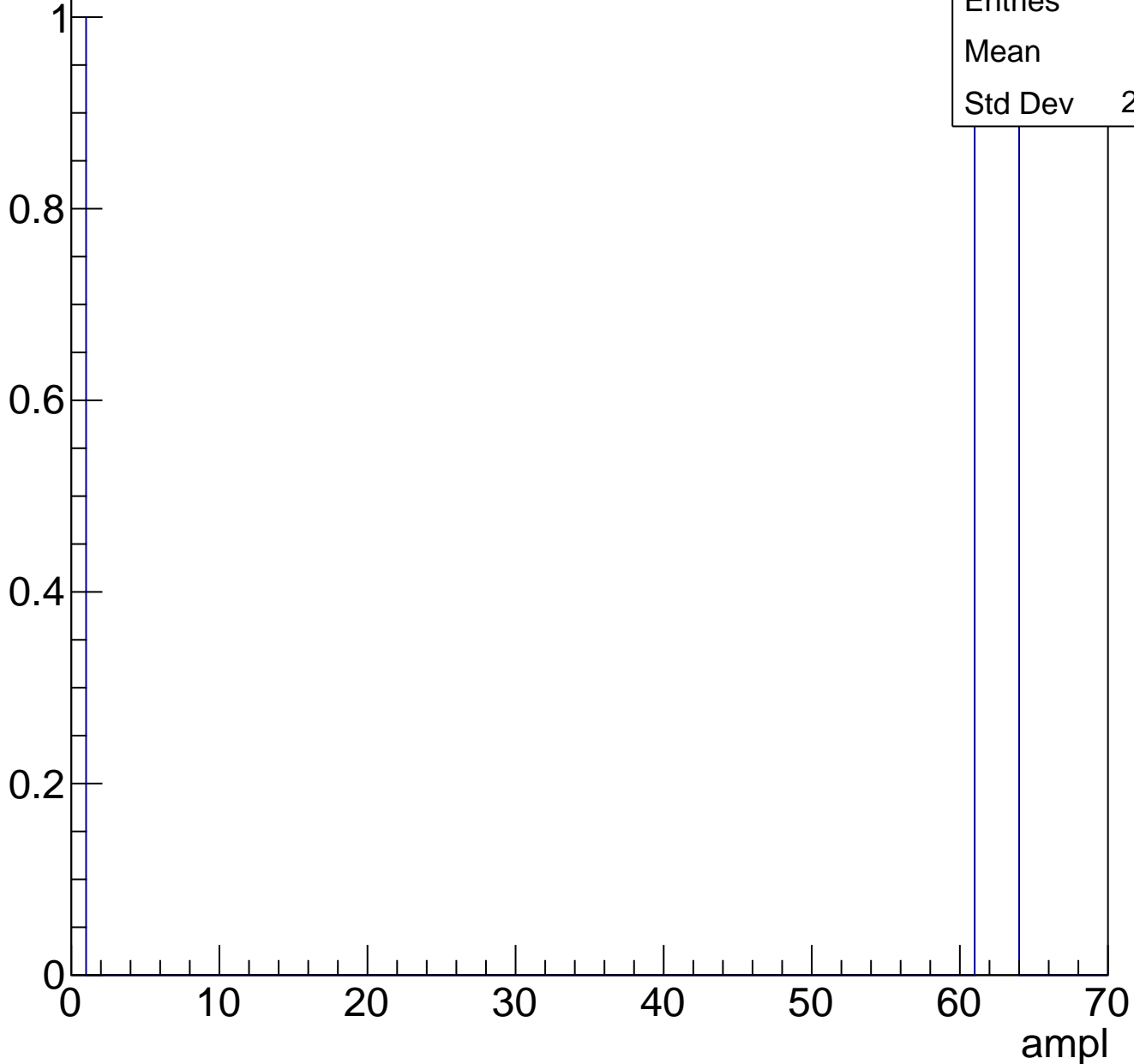
Entry



B1L102S, U17-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch69, adc0

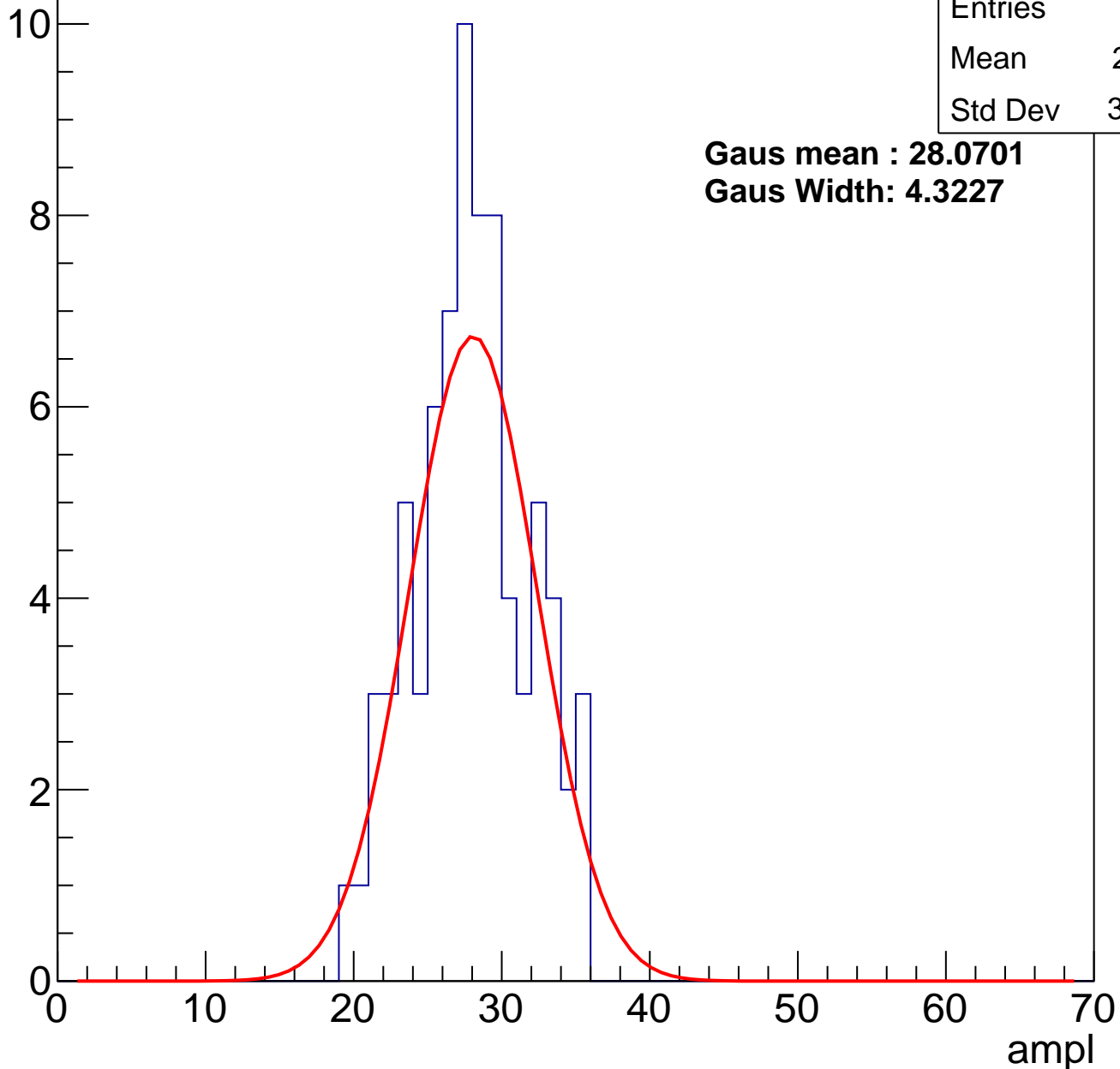
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	27.51
Std Dev	3.799

Gaus mean : 28.0701

Gaus Width: 4.3227

Entry



B1L102S, U17-ch69, adc1

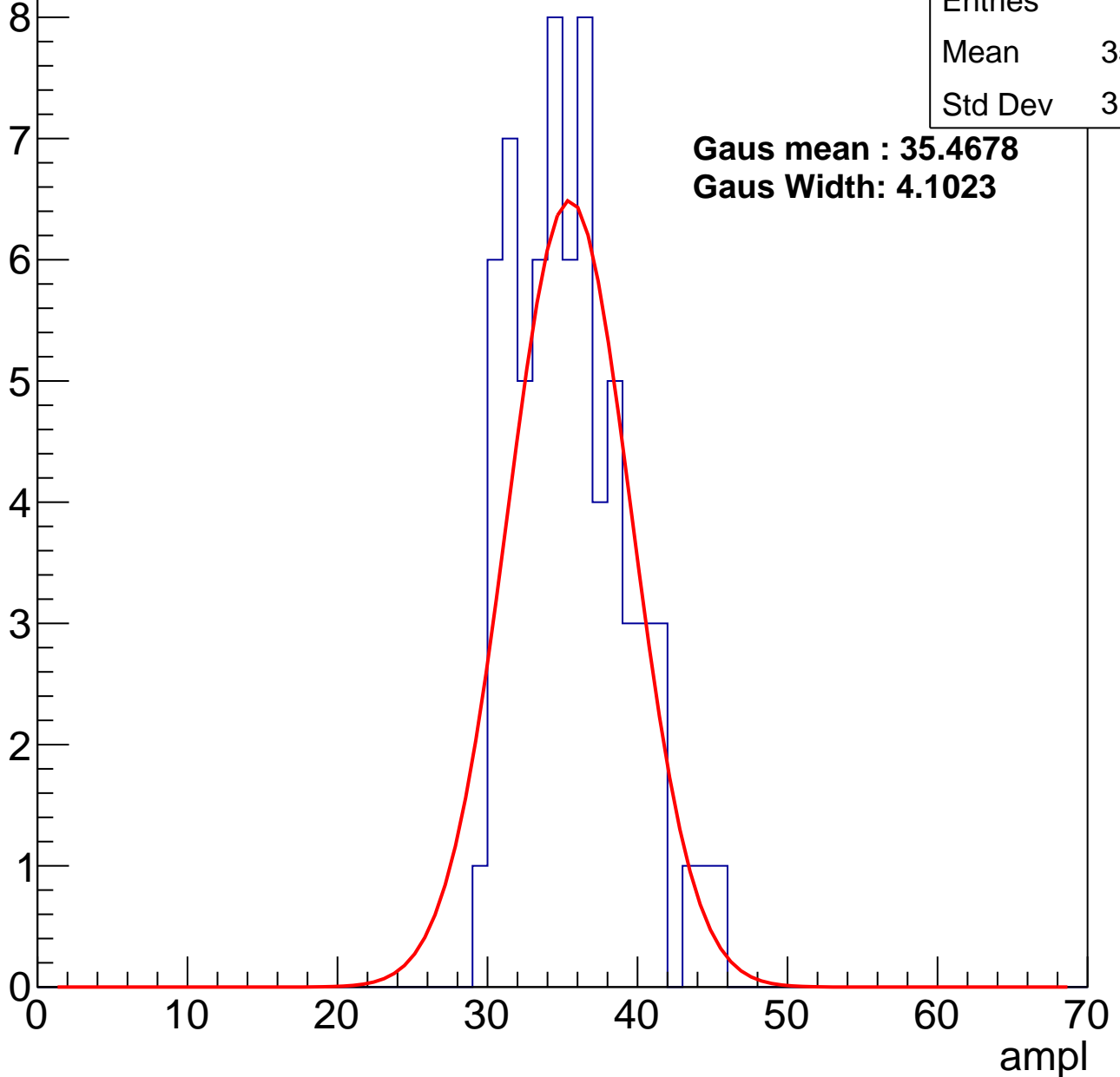
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.06
Std Dev	3.678

Gaus mean : 35.4678

Gaus Width: 4.1023



B1L102S, U17-ch69, adc2

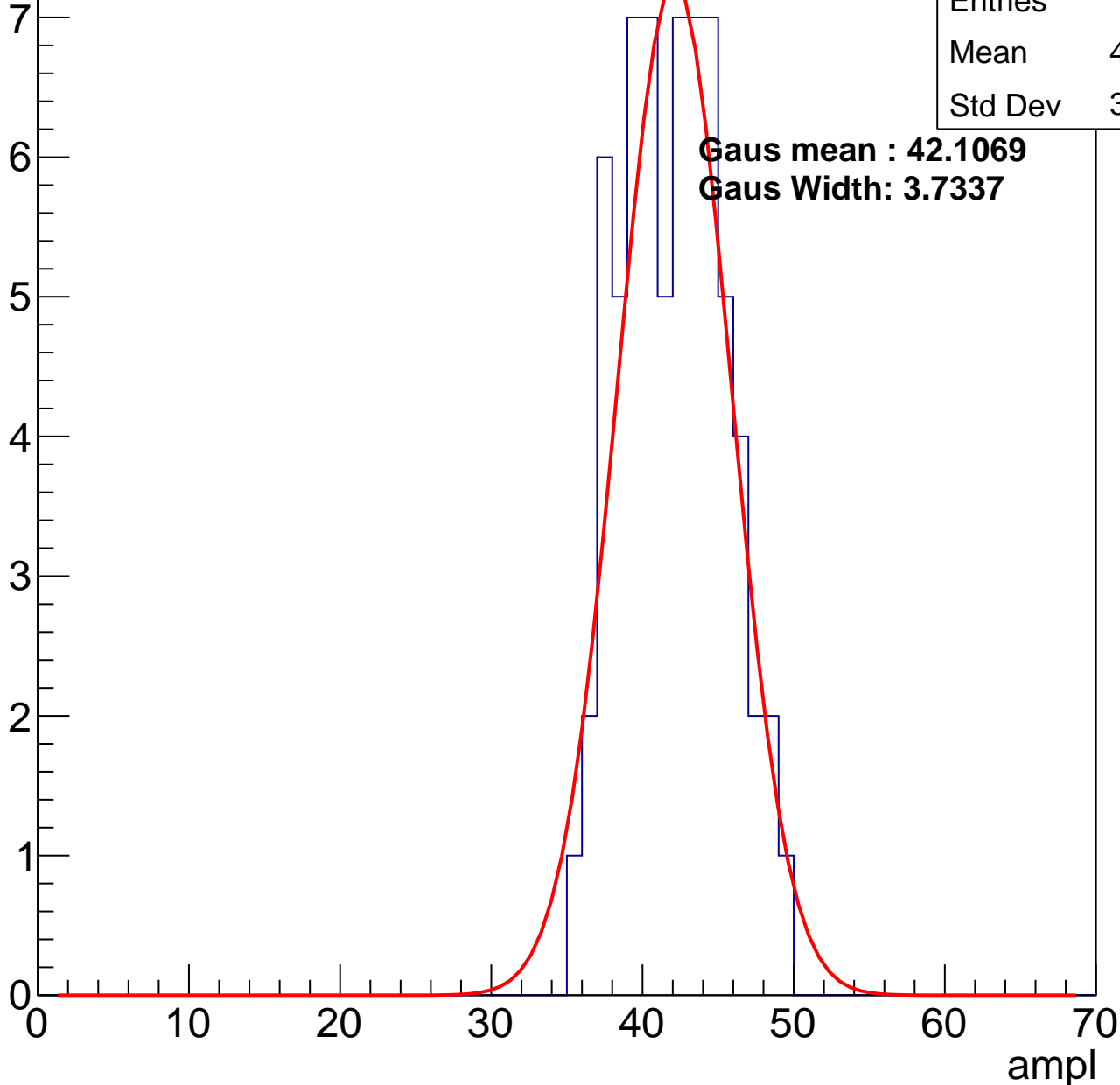
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.59
Std Dev	3.322

Gaus mean : 42.1069

Gaus Width: 3.7337

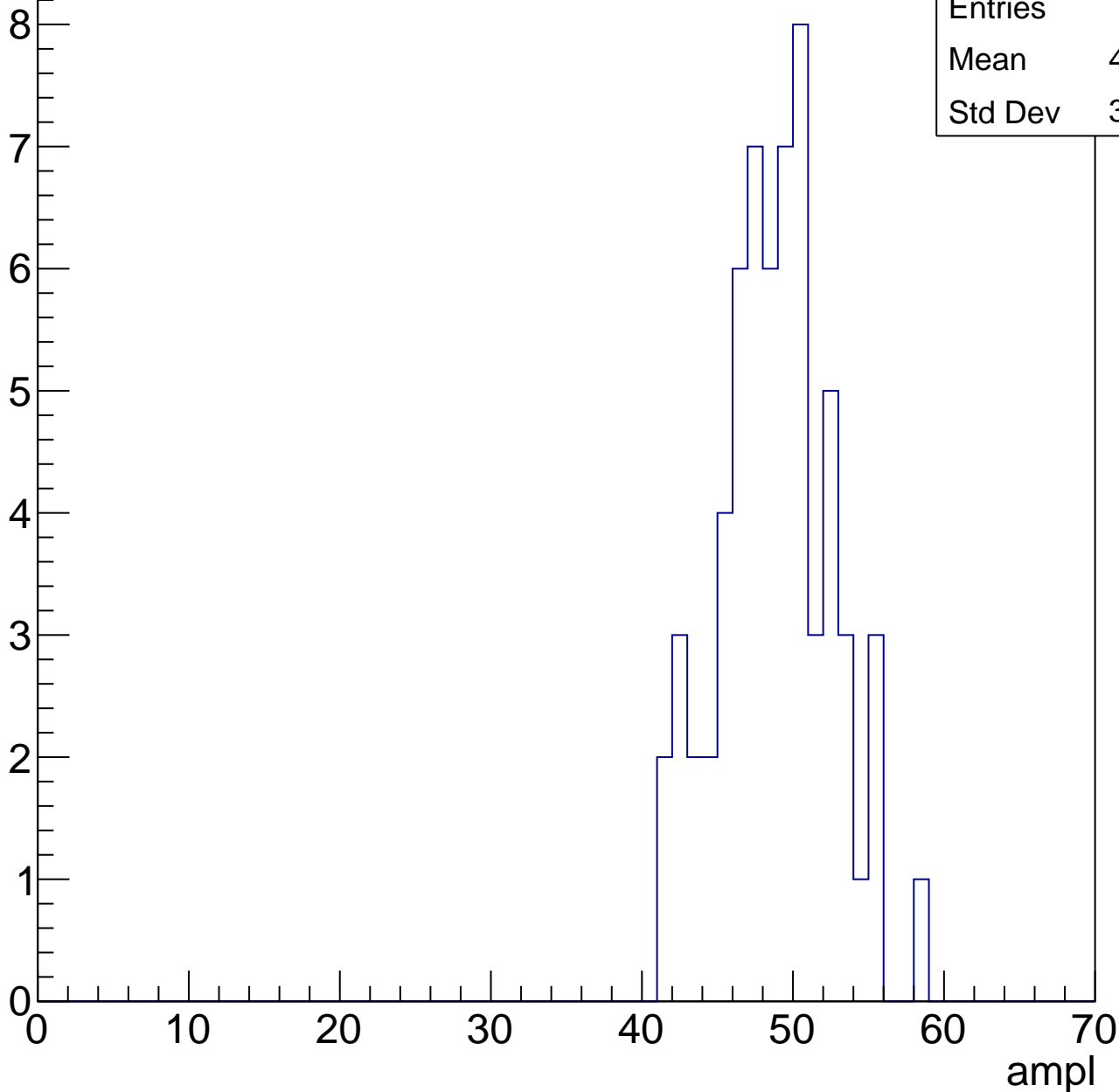


B1L102S, U17-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.37
Std Dev	3.674

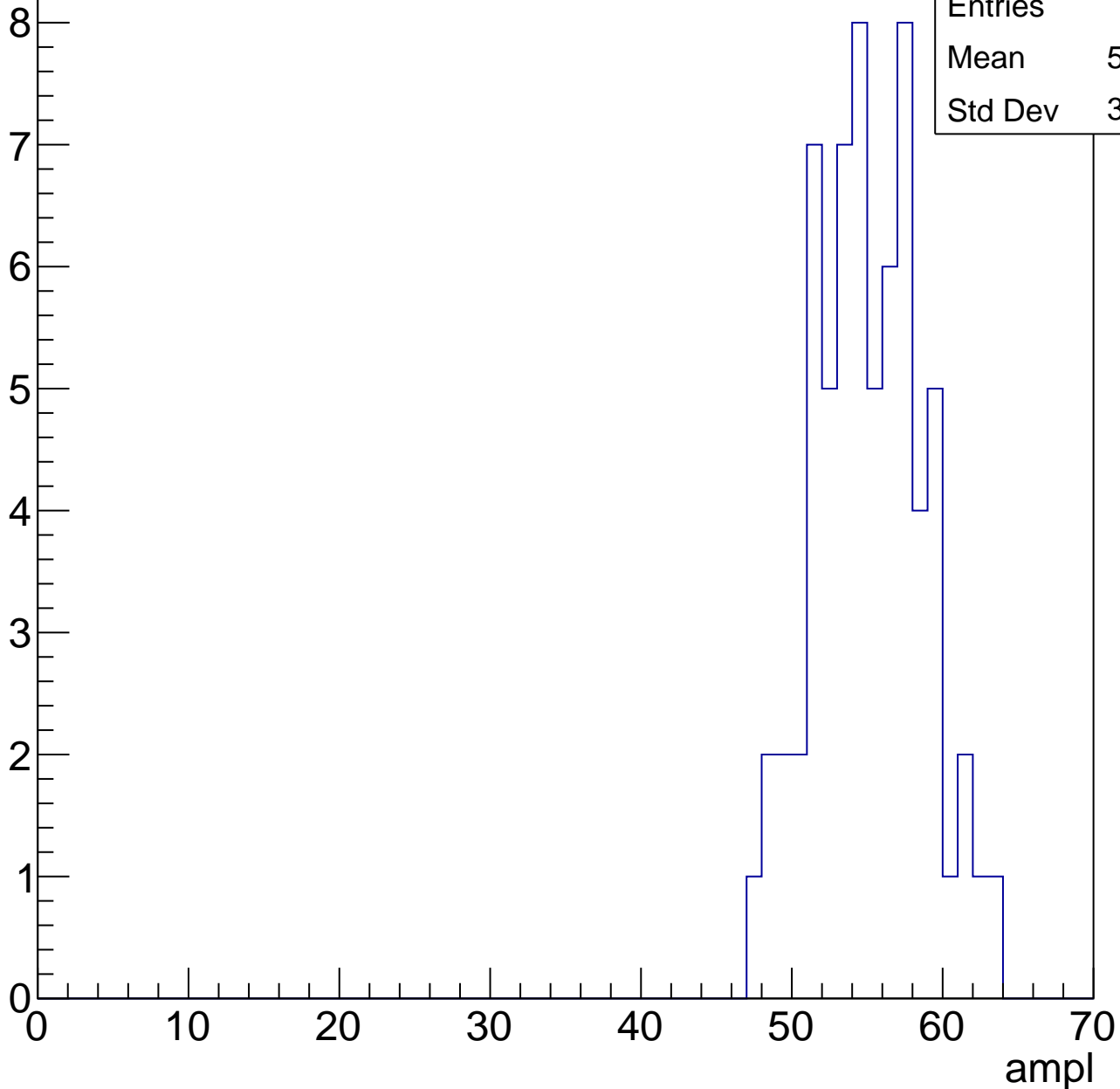


B1L102S, U17-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	54.66
Std Dev	3.526

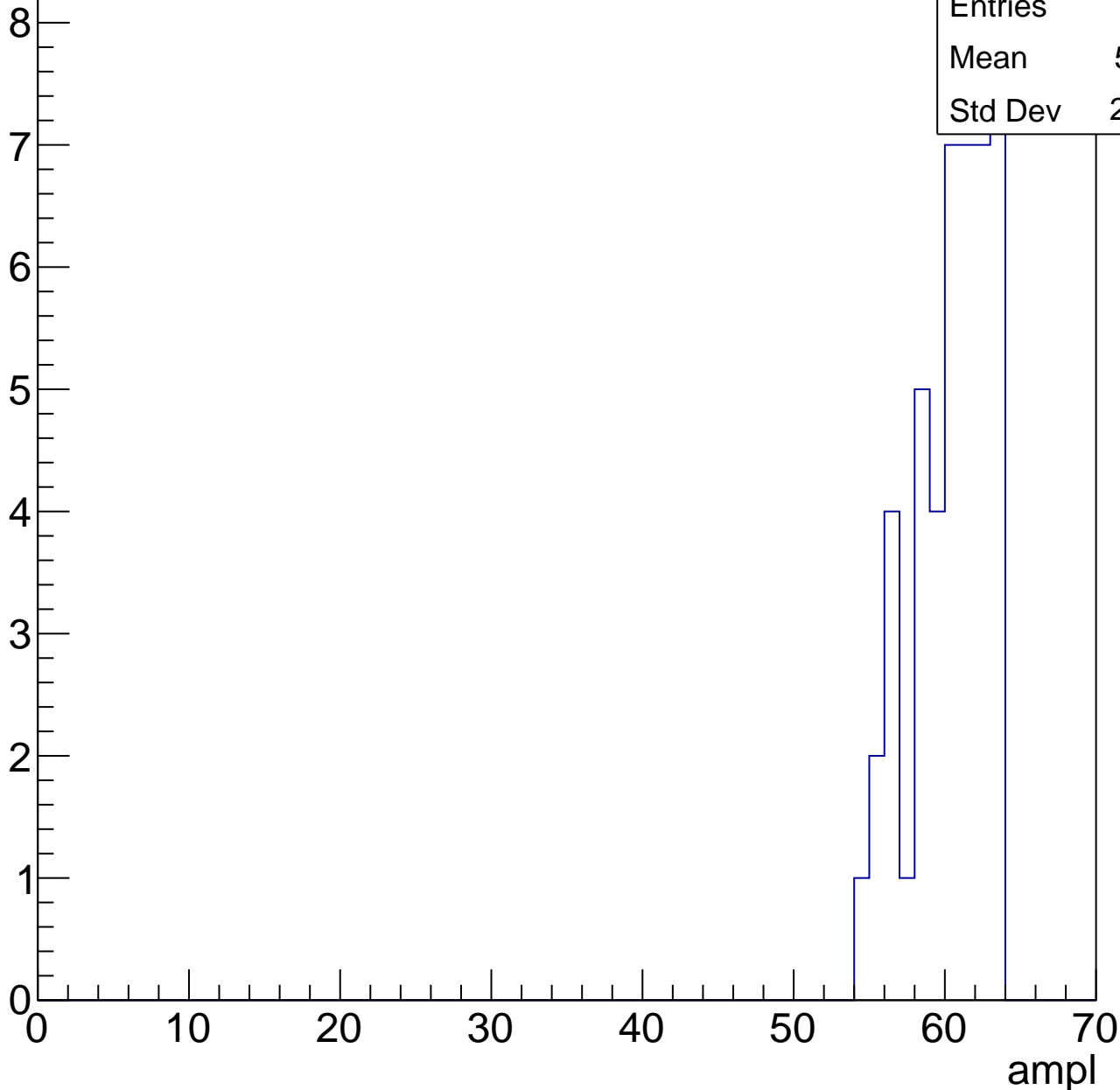


B1L102S, U17-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.91
Std Dev	2.509



B1L102S, U17-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

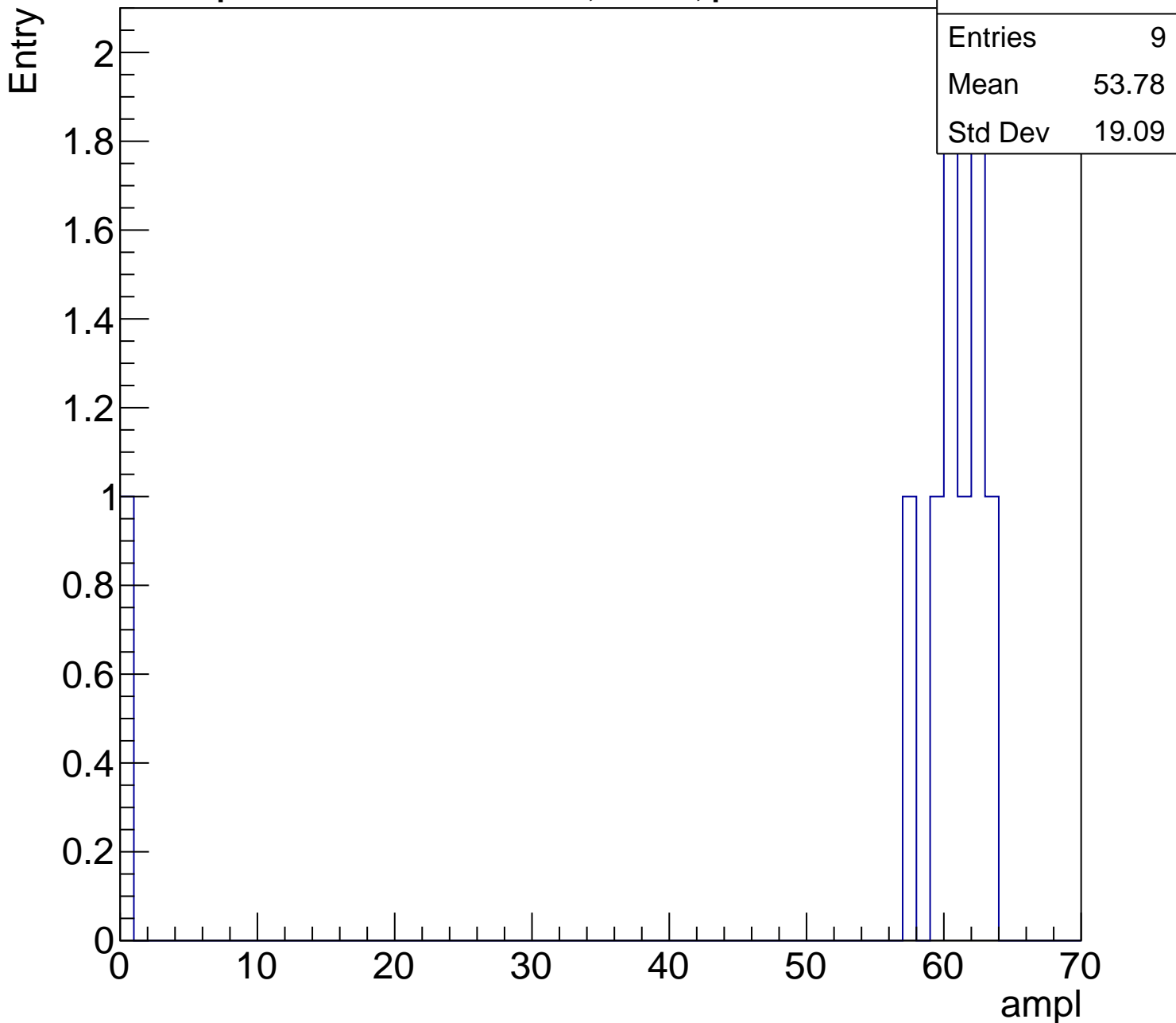
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	53.78
Std Dev	19.09

0 10 20 30 40 50 60 70

ampl



B1L102S, U17-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch70, adc0

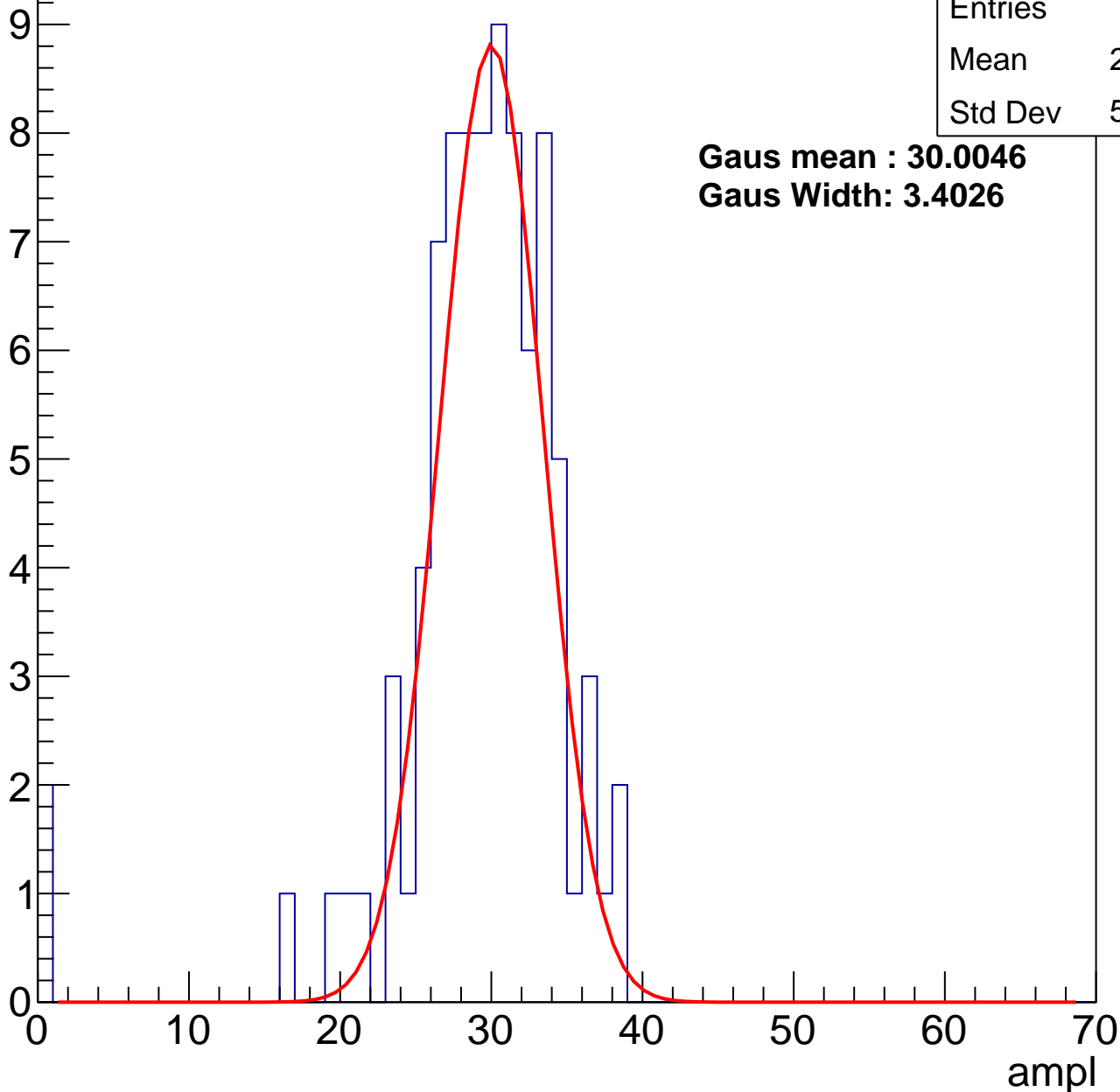
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	28.67
Std Dev	5.977

Gaus mean : 30.0046

Gaus Width: 3.4026



B1L102S, U17-ch70, adc1

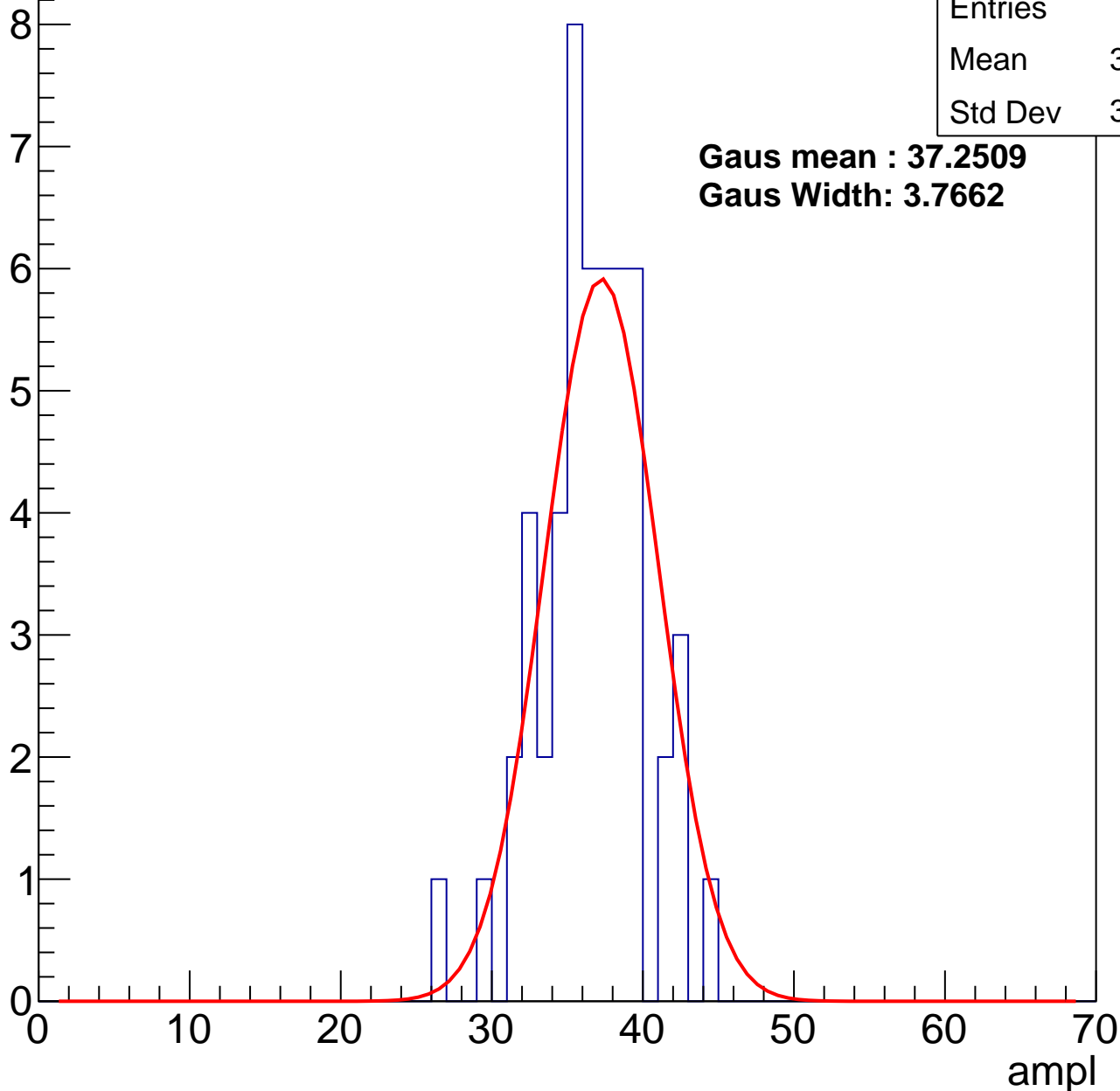
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	36.13
Std Dev	3.425

Gaus mean : 37.2509

Gaus Width: 3.7662



B1L102S, U17-ch70, adc2

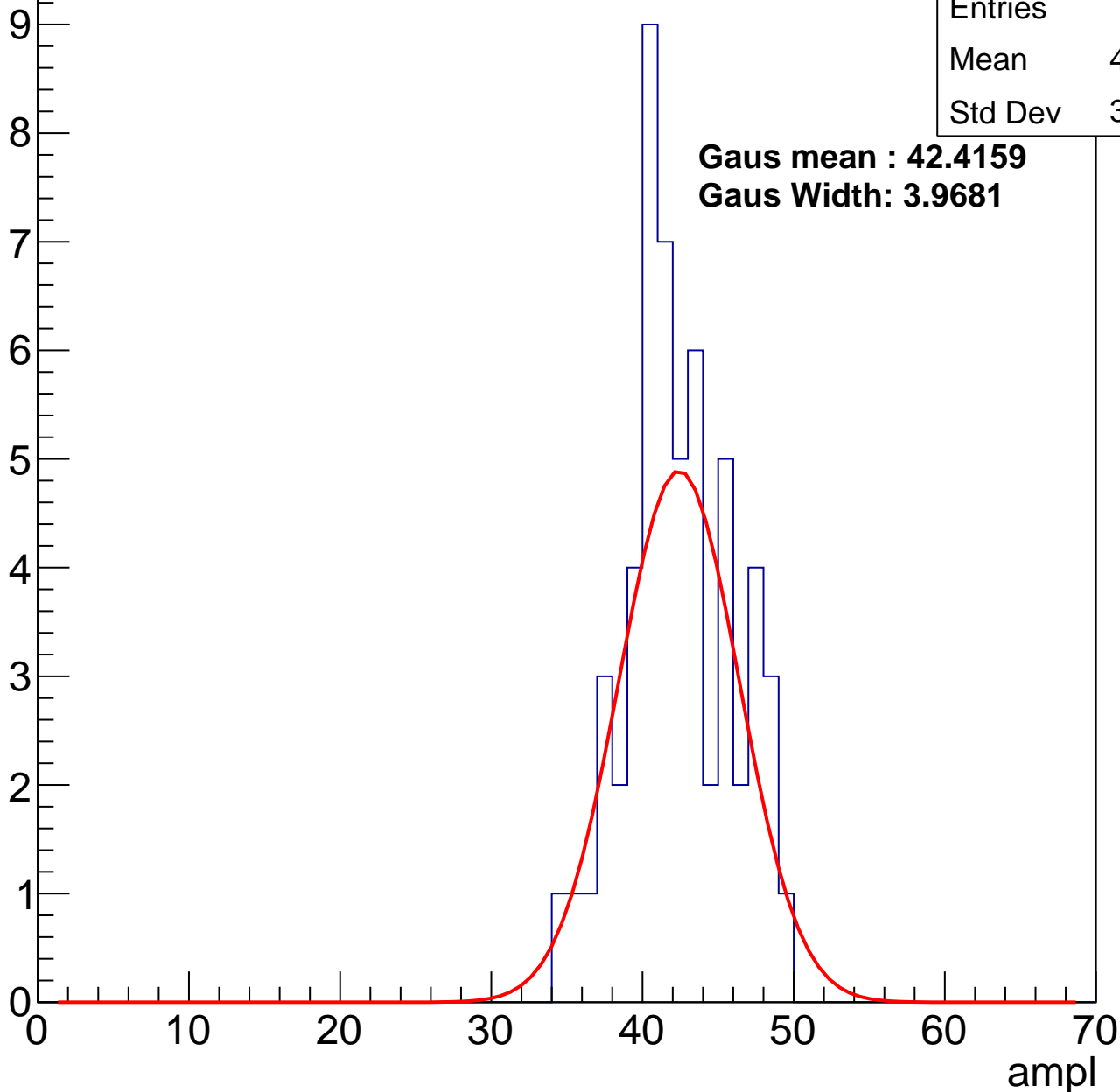
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.95
Std Dev	3.492

Gaus mean : 42.4159

Gaus Width: 3.9681

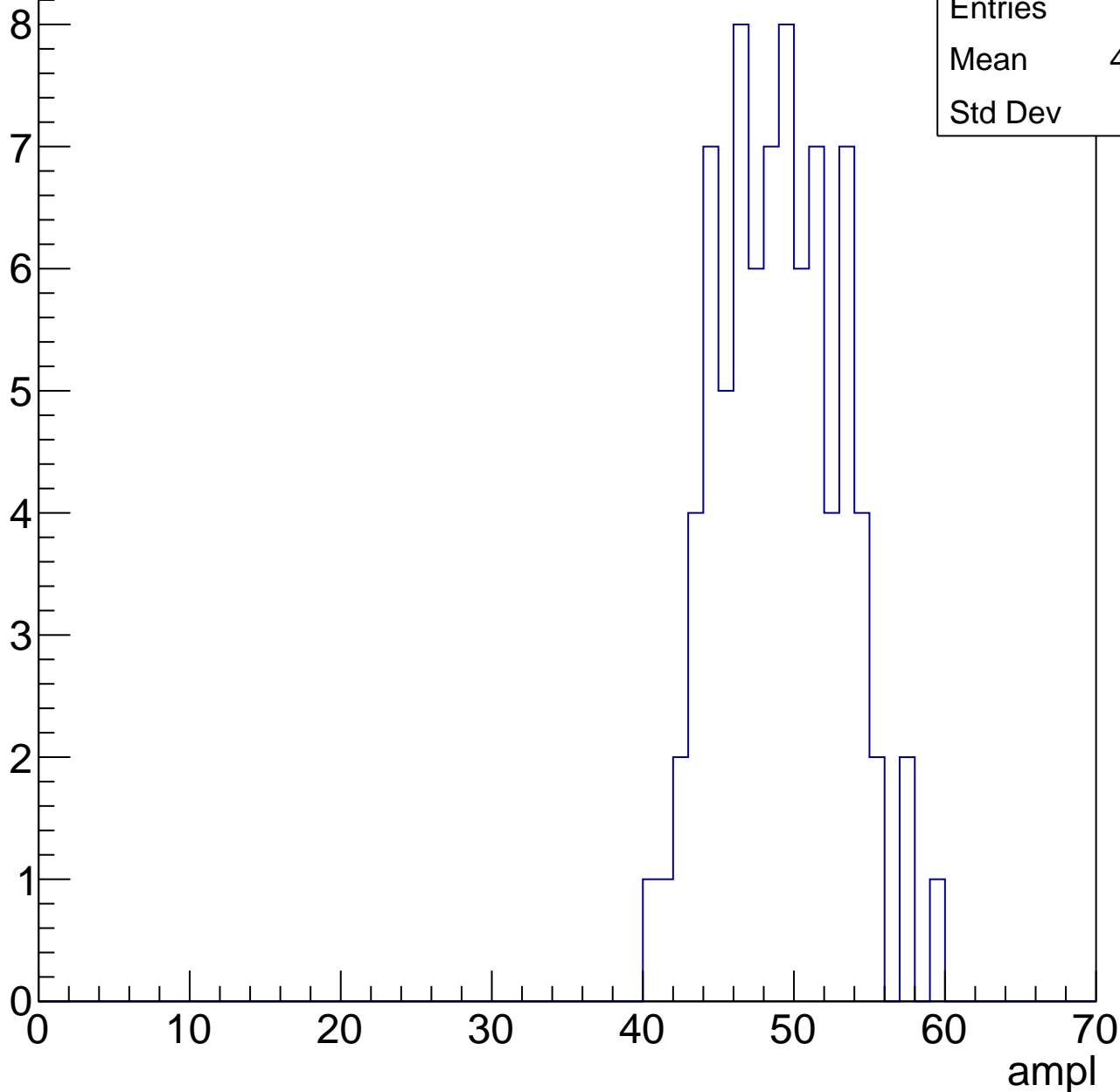


B1L102S, U17-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	48.57
Std Dev	4

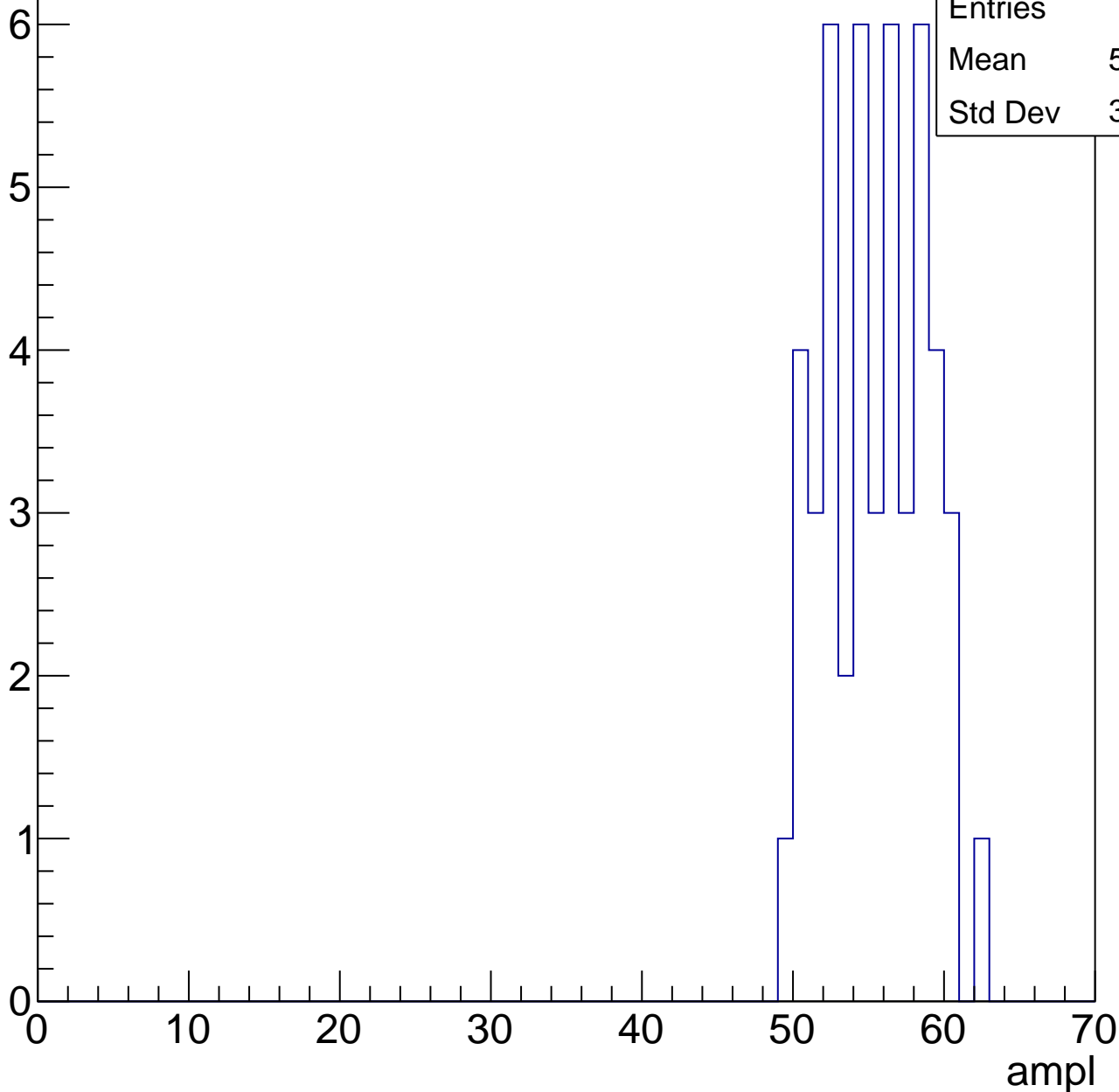


B1L102S, U17-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.04
Std Dev	3.266

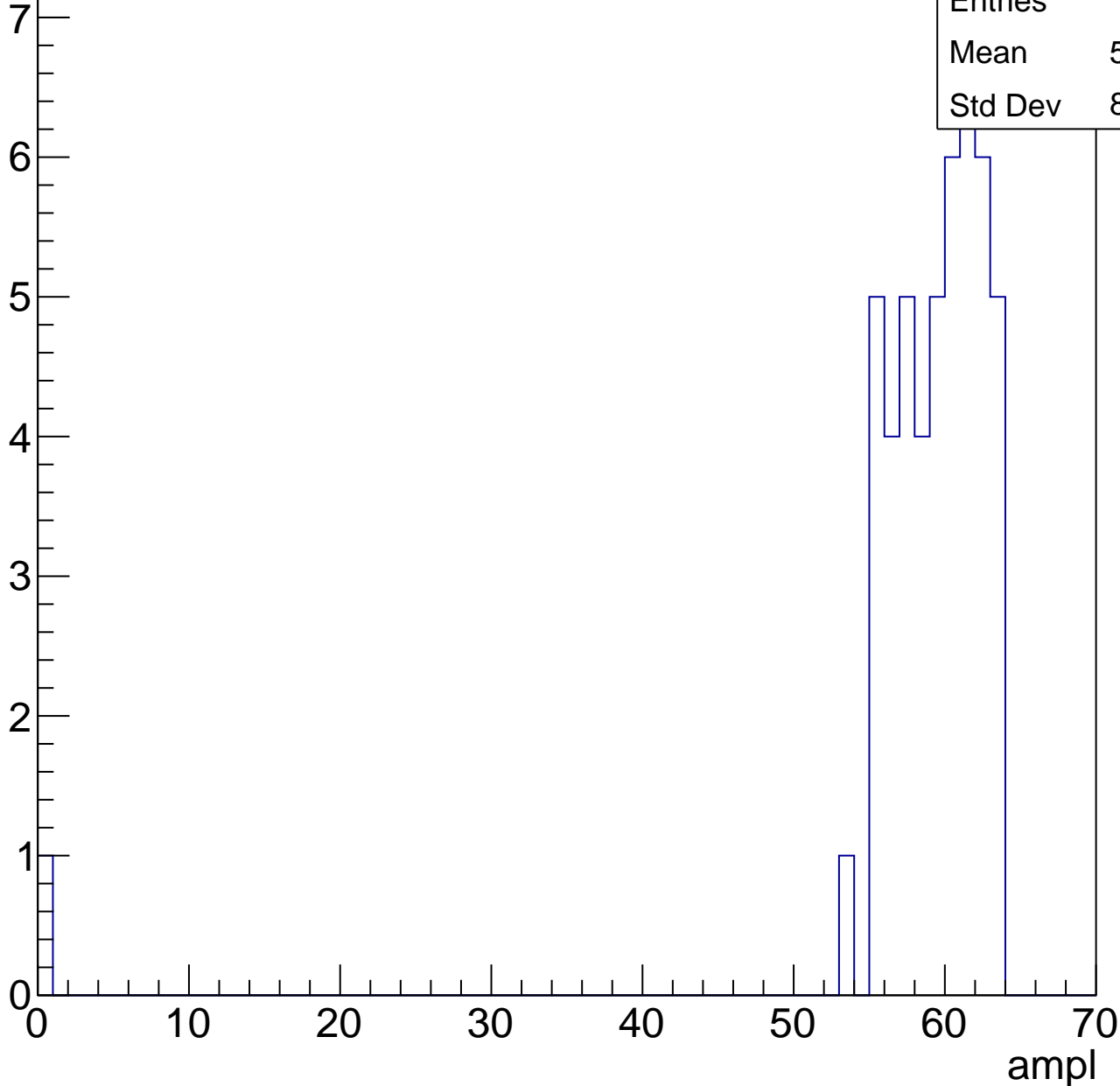


B1L102S, U17-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.92
Std Dev	8.769

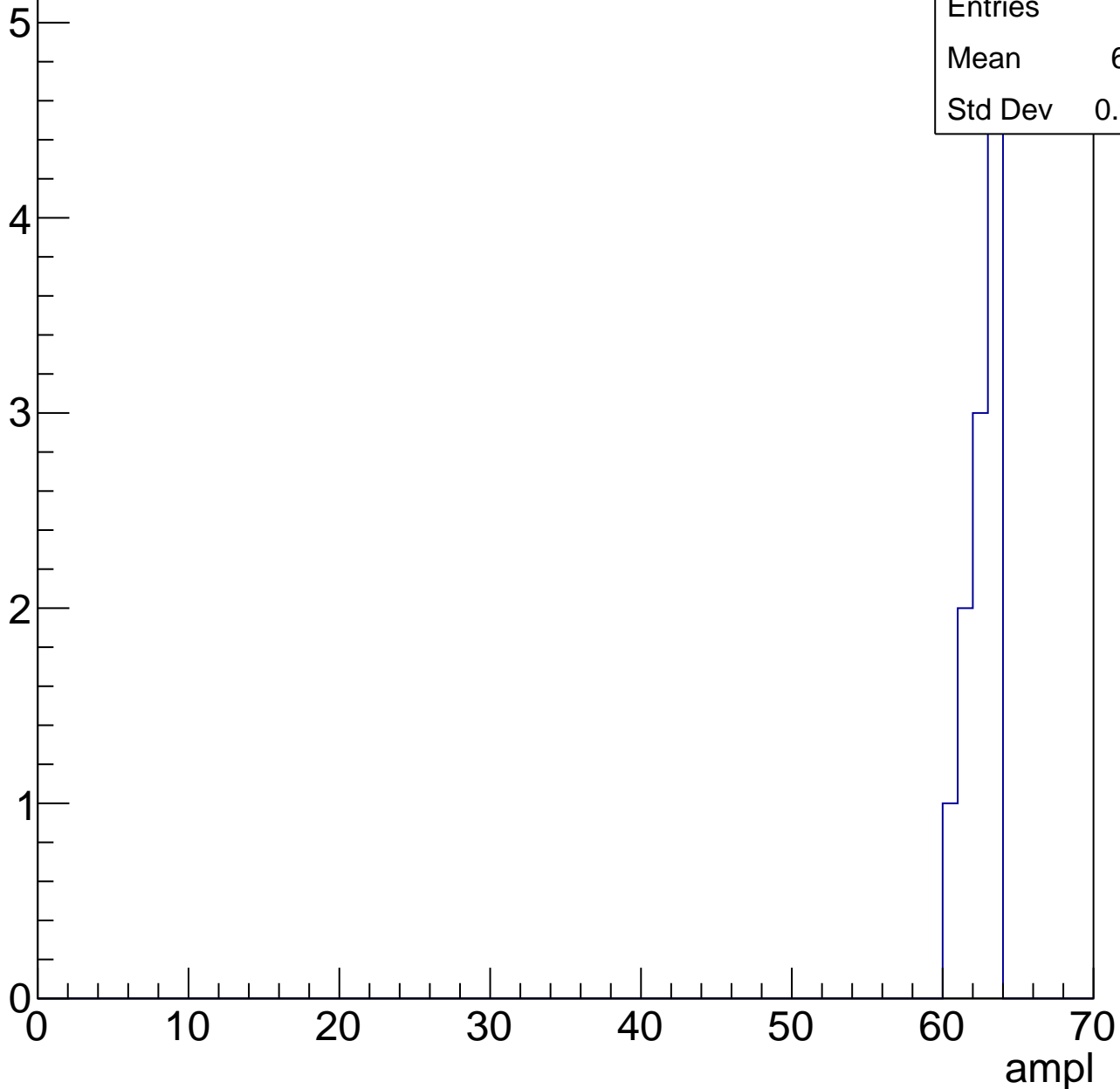


B1L102S, U17-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

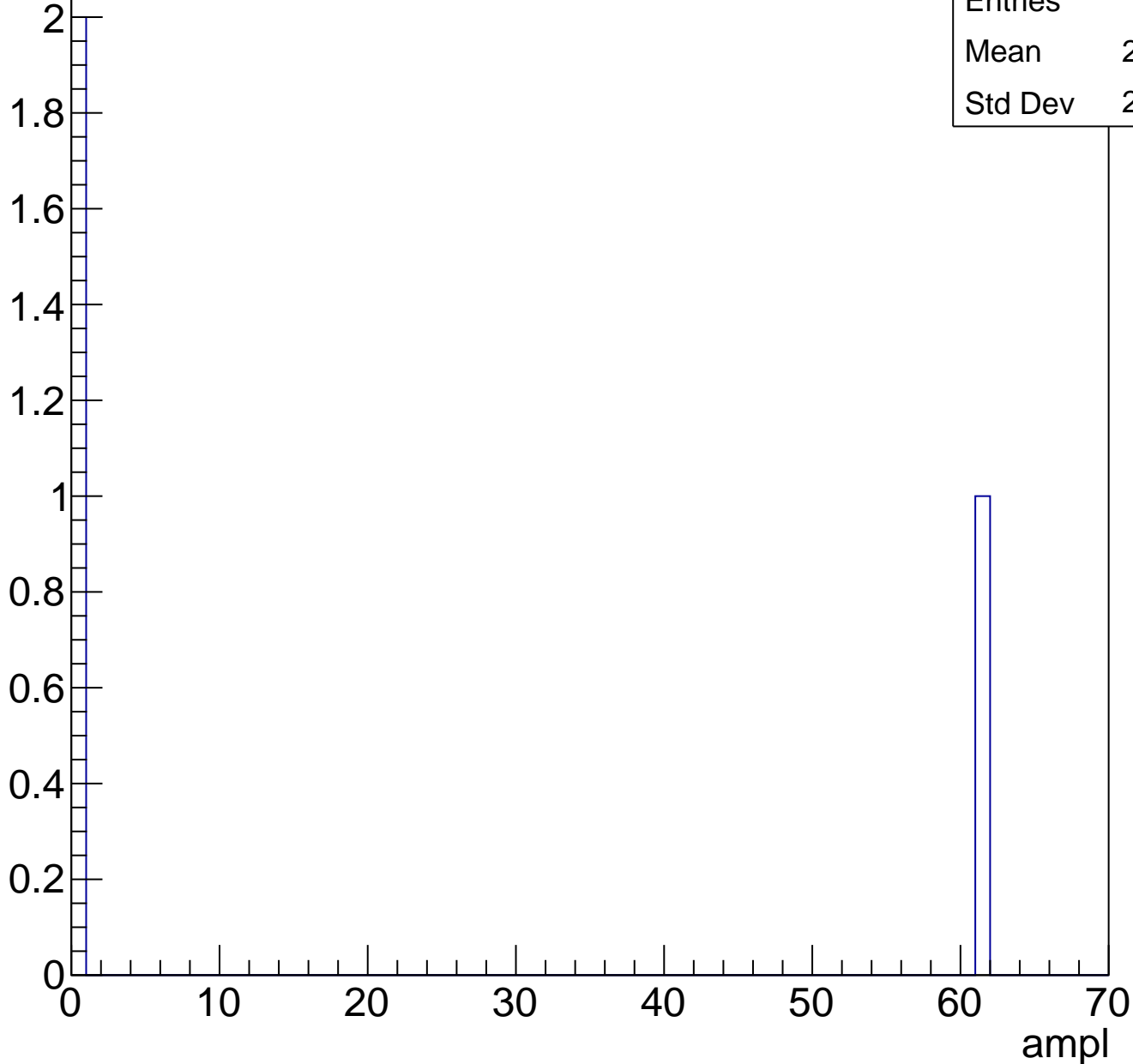
Entries	11
Mean	62.09
Std Dev	0.9959



B1L102S, U17-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch71, adc0

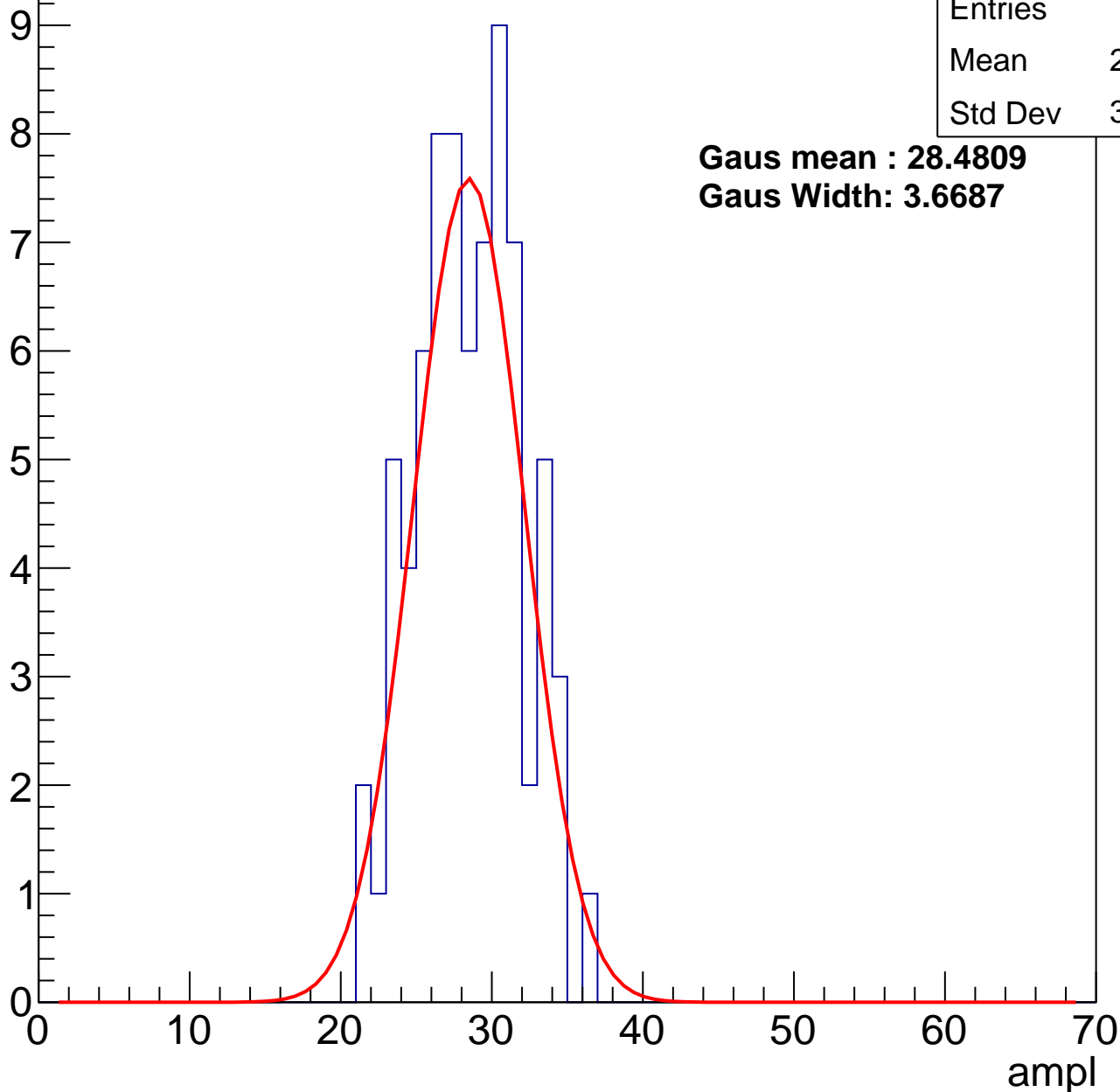
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.03
Std Dev	3.393

Gaus mean : 28.4809

Gaus Width: 3.6687



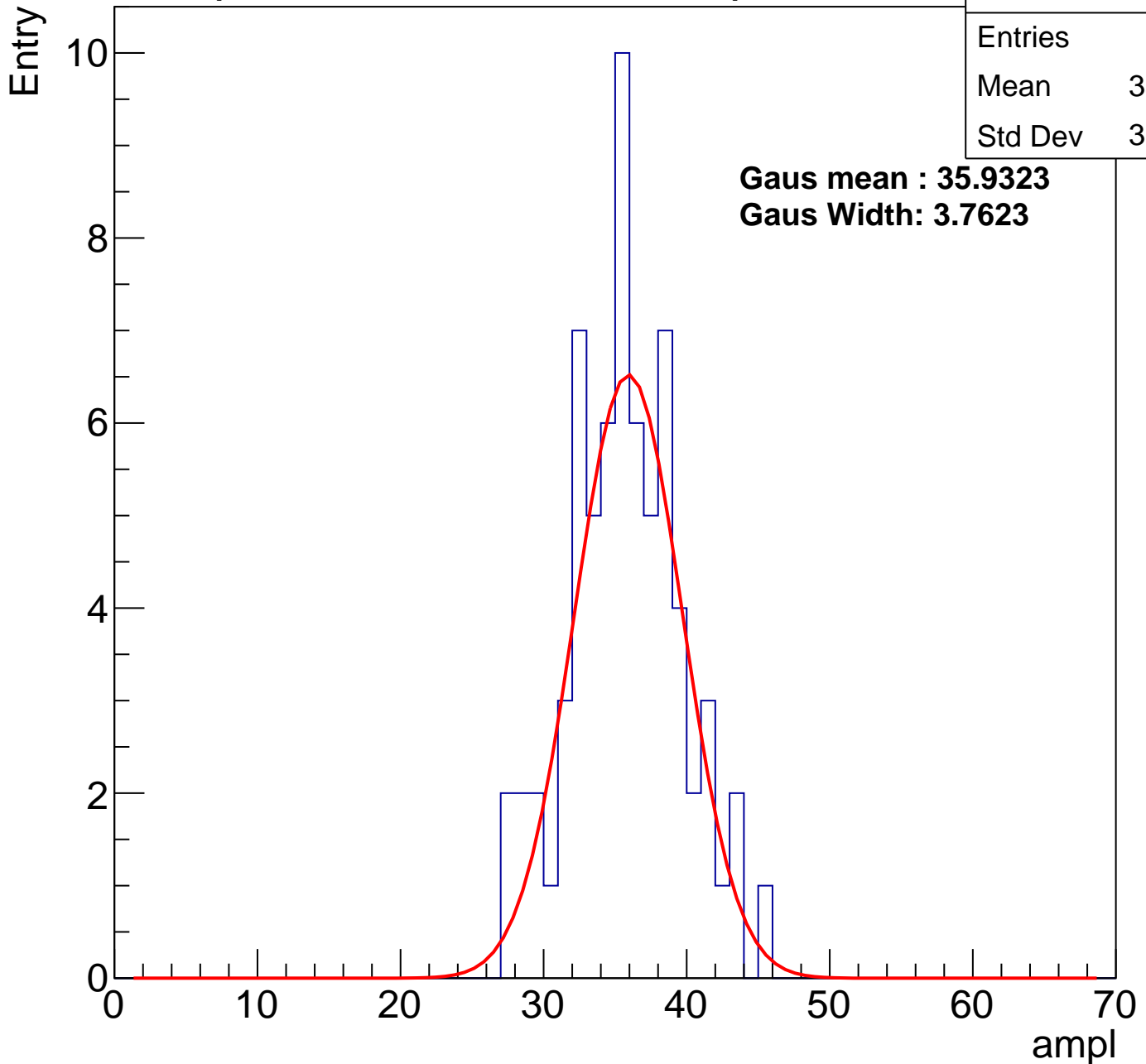
B1L102S, U17-ch71, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	35.26
Std Dev	3.888

Gaus mean : 35.9323

Gaus Width: 3.7623



B1L102S, U17-ch71, adc2

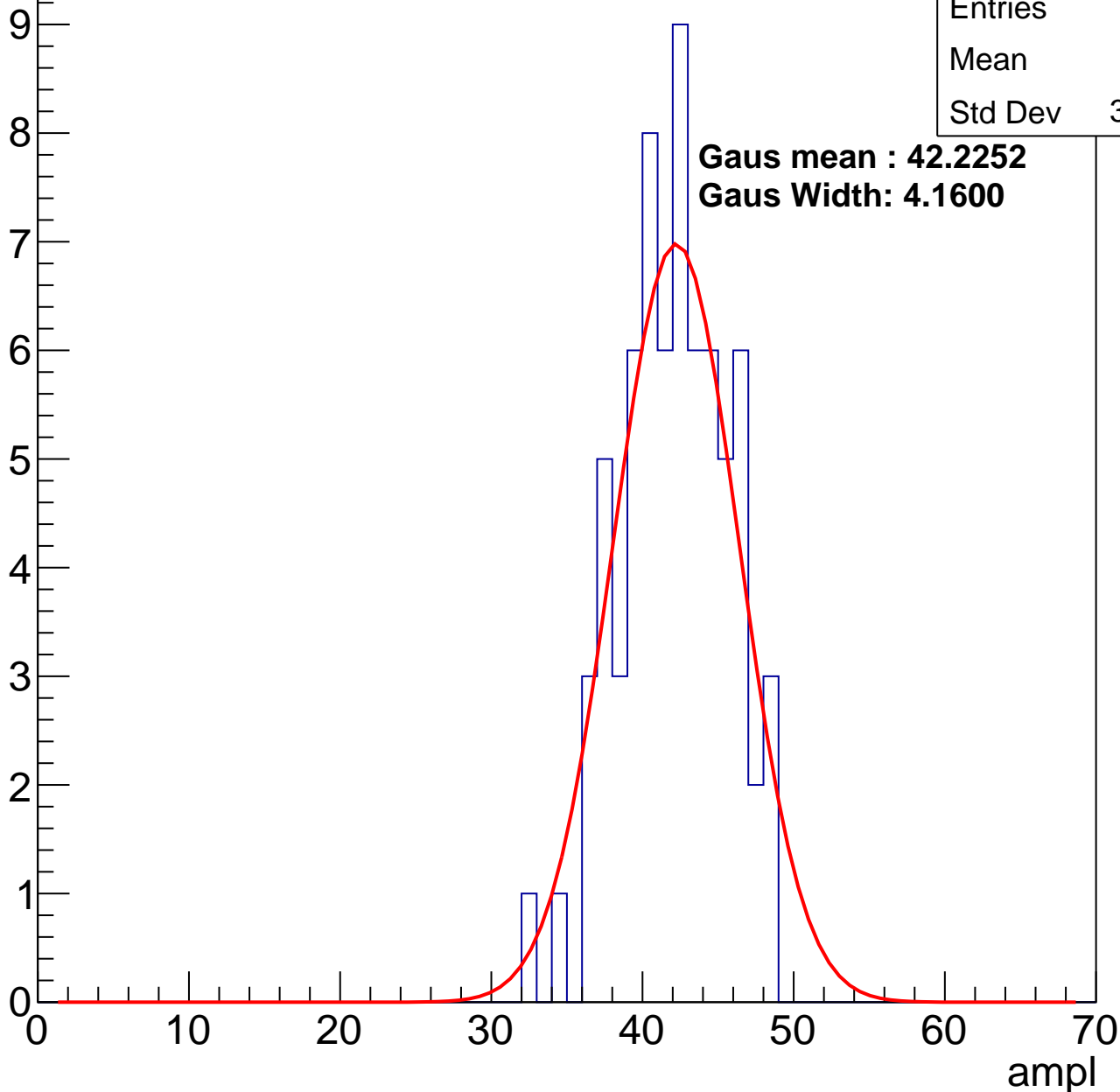
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.6
Std Dev	3.494

Gaus mean : 42.2252

Gaus Width: 4.1600

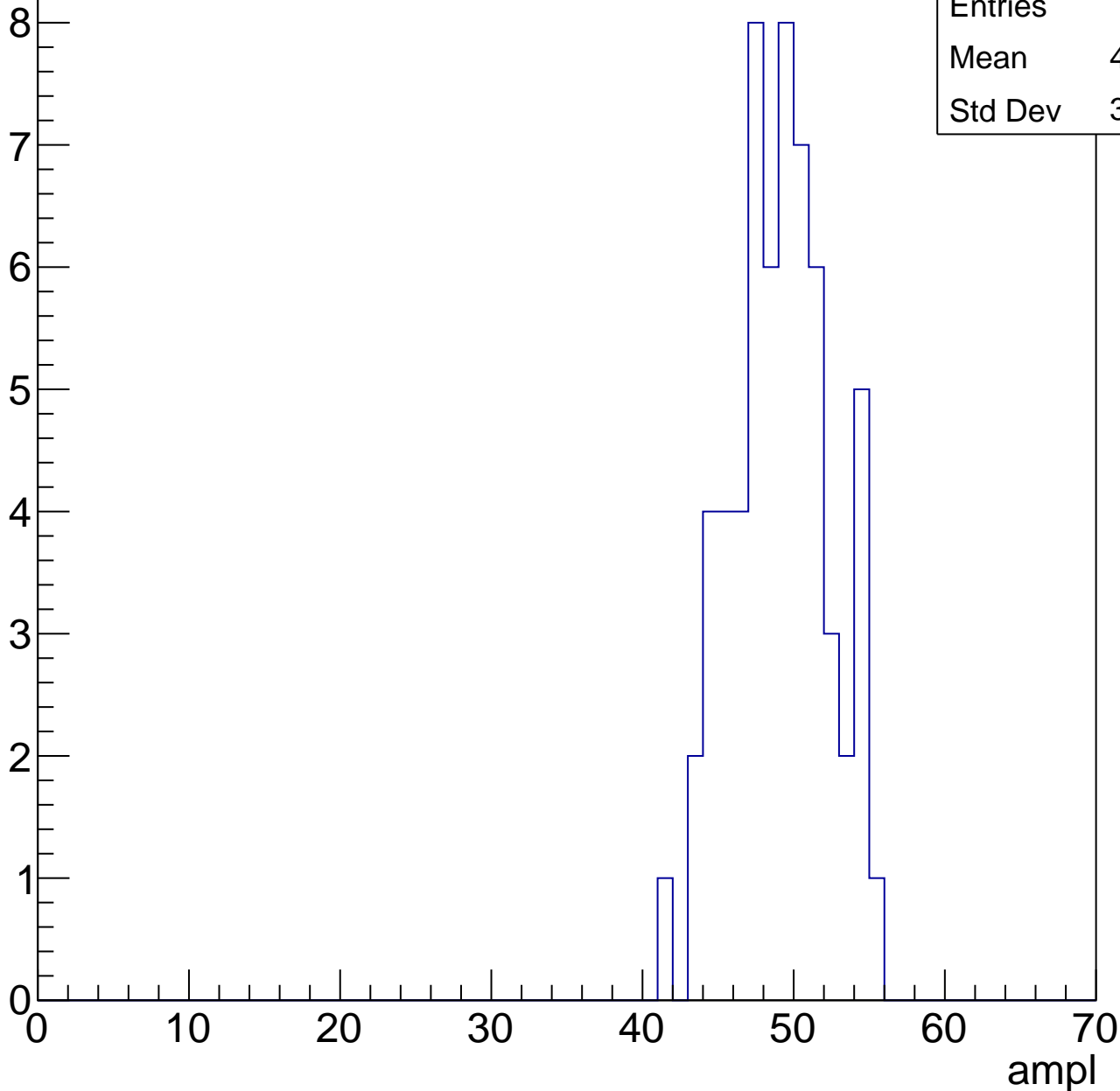


B1L102S, U17-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.62
Std Dev	3.179

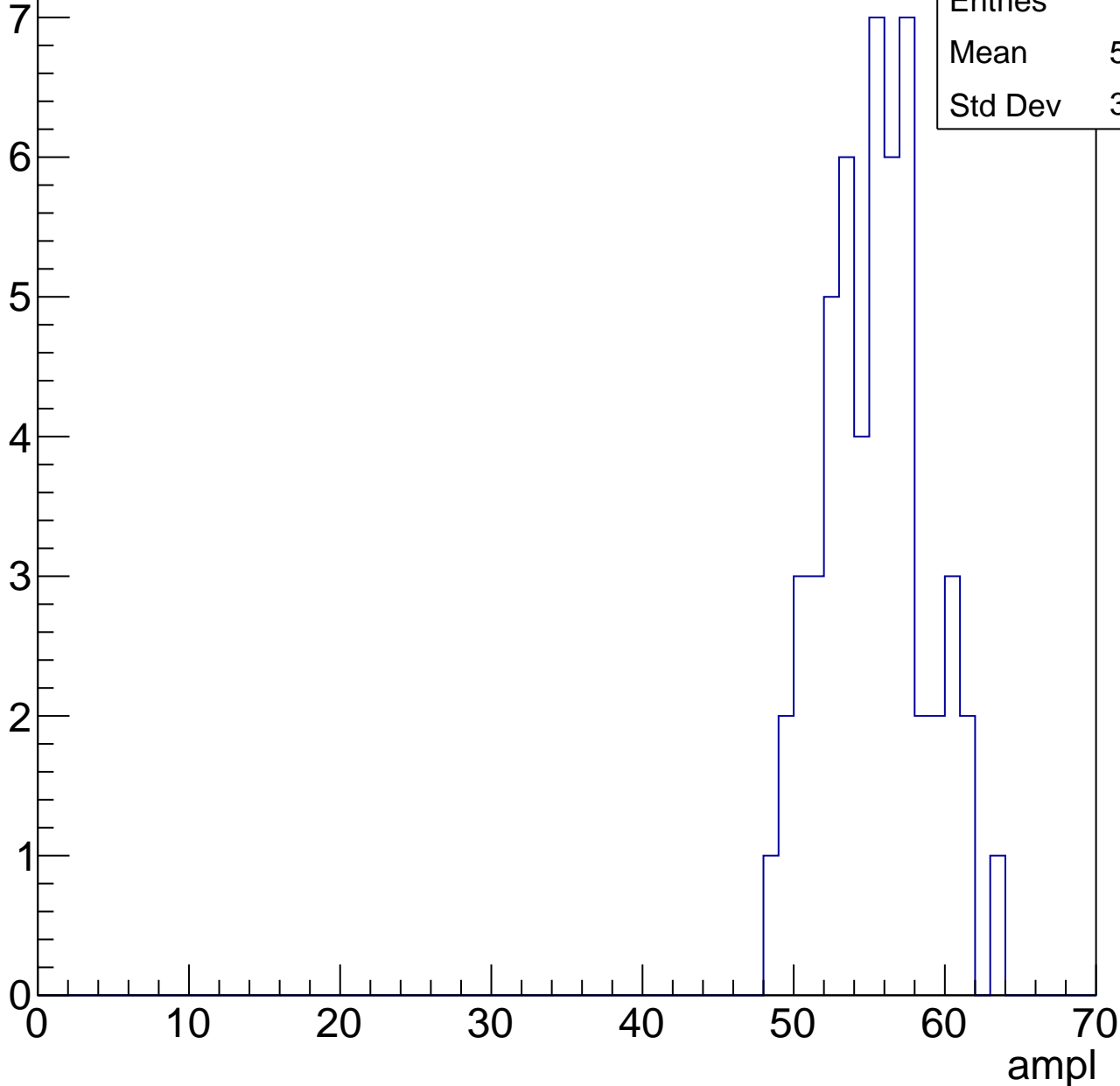


B1L102S, U17-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.85
Std Dev	3.363

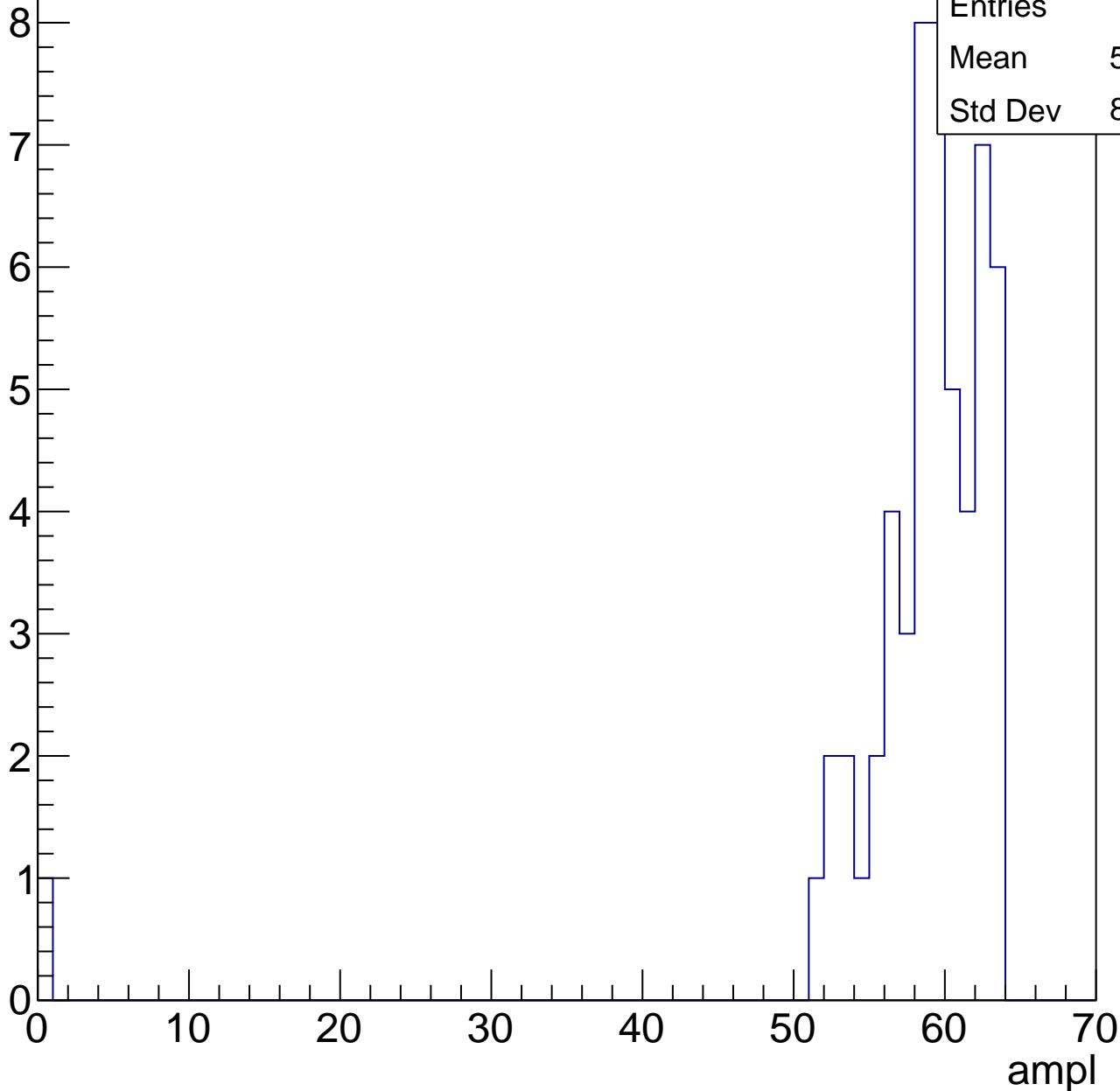


B1L102S, U17-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

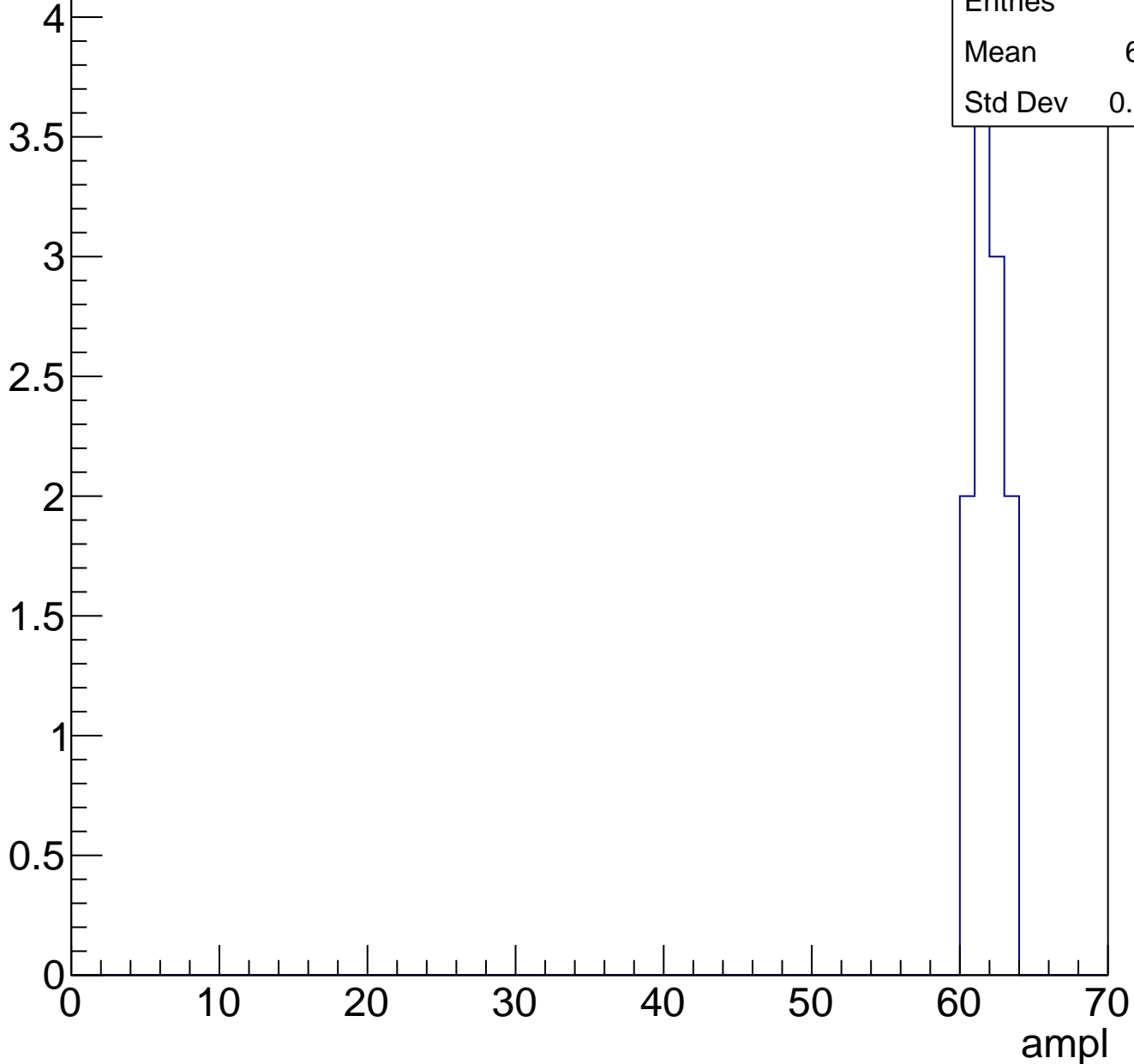
Entries	54
Mean	57.63
Std Dev	8.506



B1L102S, U17-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch72, adc0

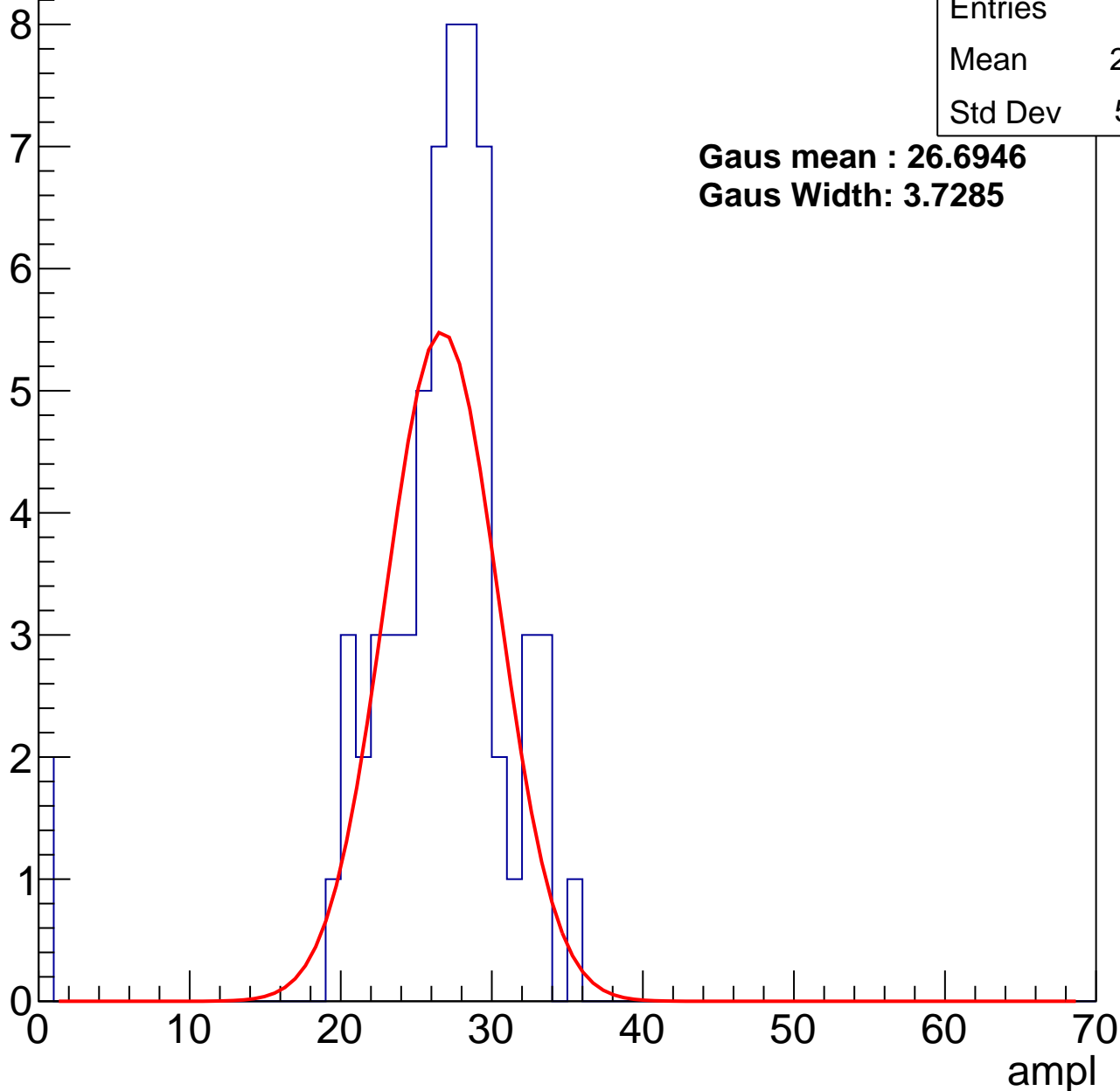
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	25.79
Std Dev	5.881

Gaus mean : 26.6946

Gaus Width: 3.7285



B1L102S, U17-ch72, adc1

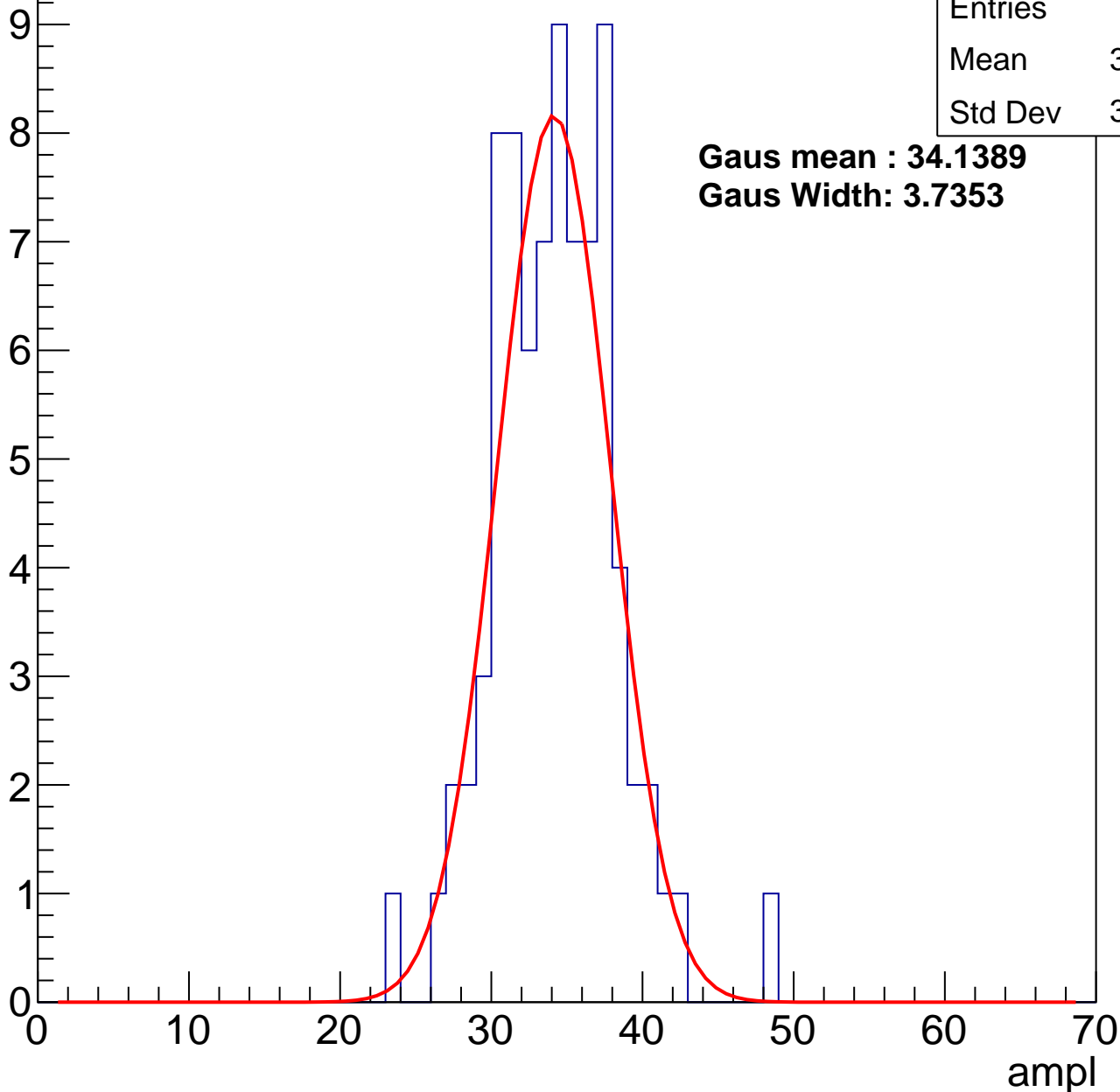
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	33.75
Std Dev	3.955

Gaus mean : 34.1389

Gaus Width: 3.7353



B1L102S, U17-ch72, adc2

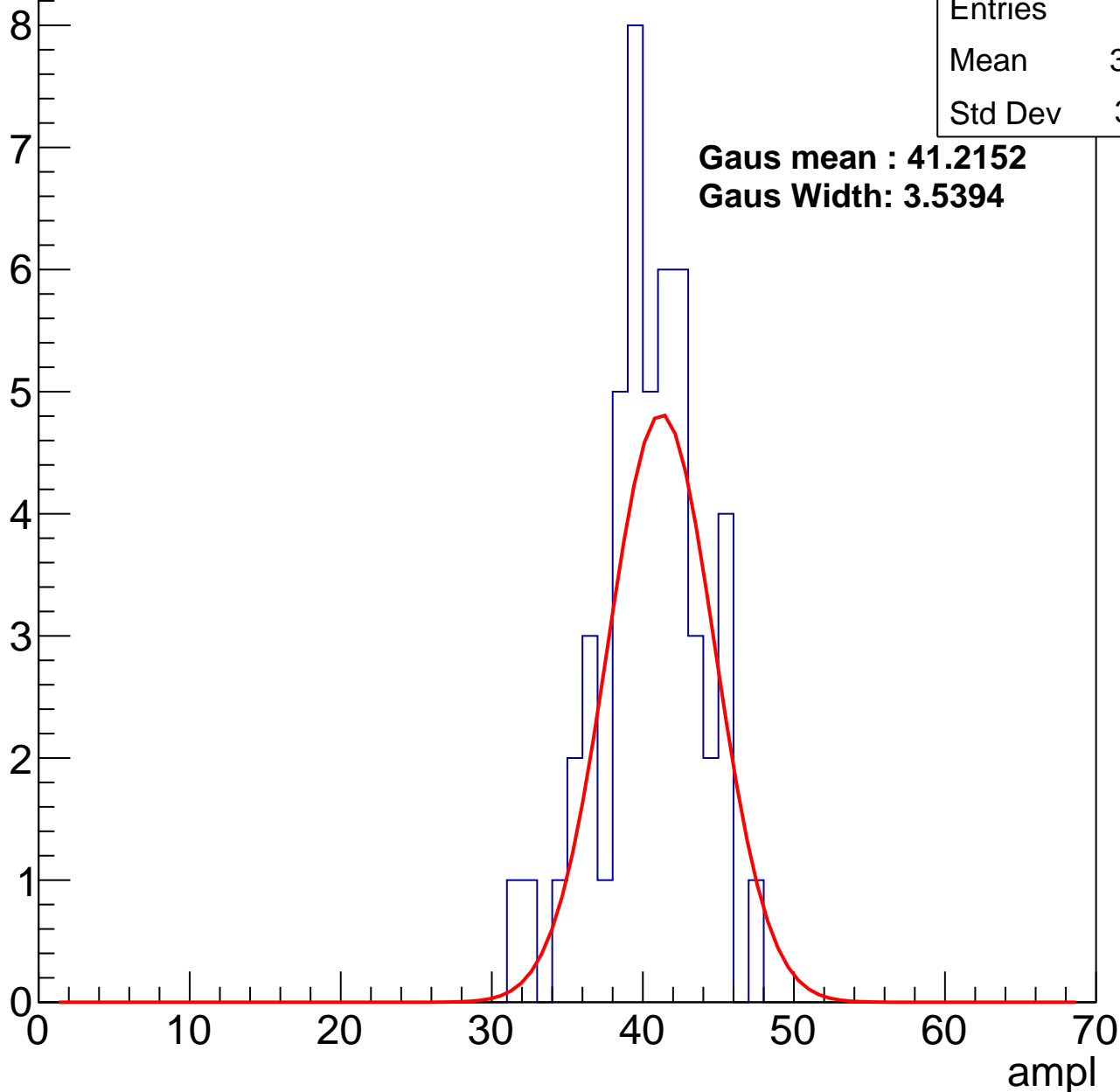
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	39.92
Std Dev	3.361

Gaus mean : 41.2152

Gaus Width: 3.5394

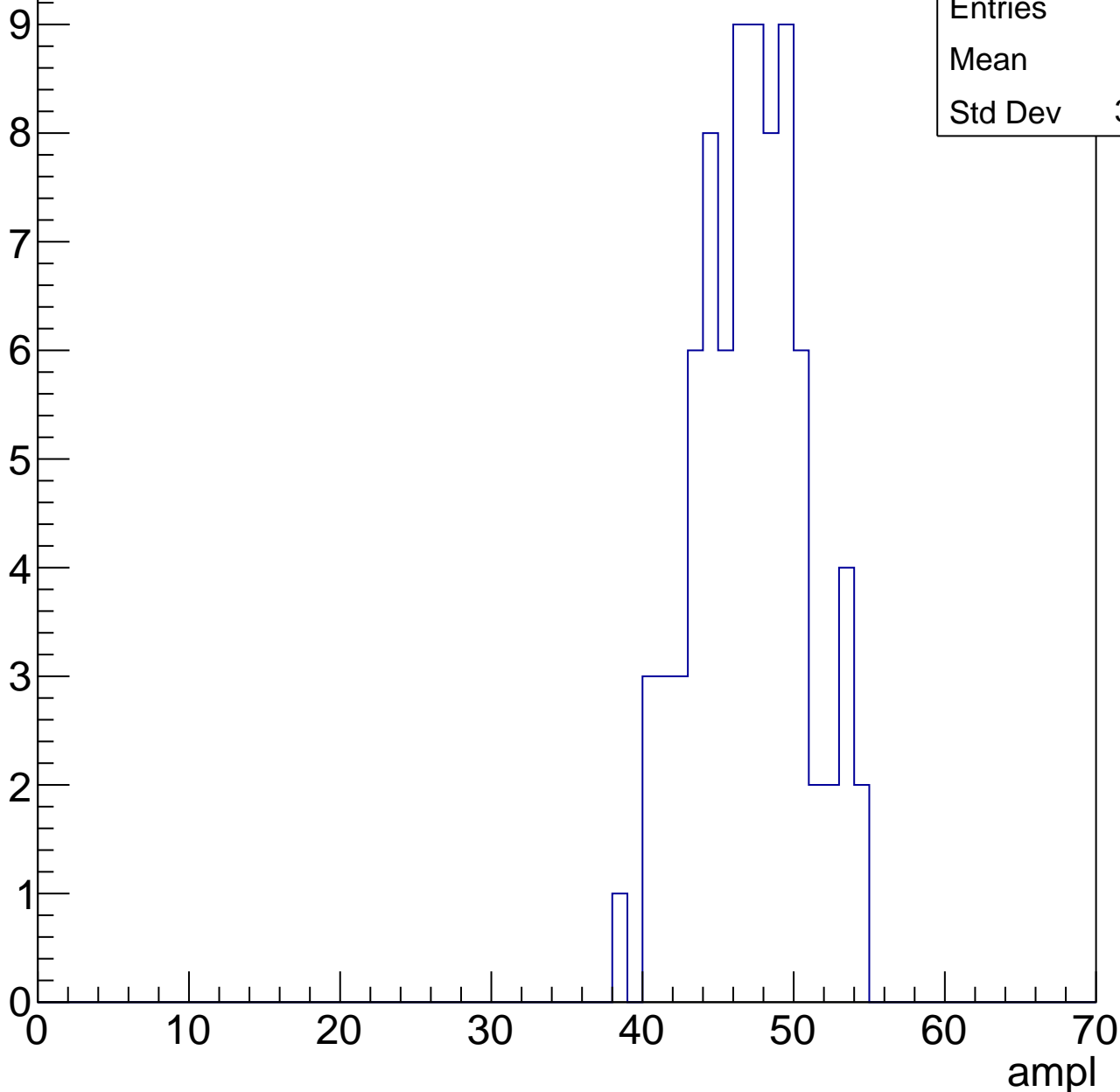


B1L102S, U17-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	46.6
Std Dev	3.551

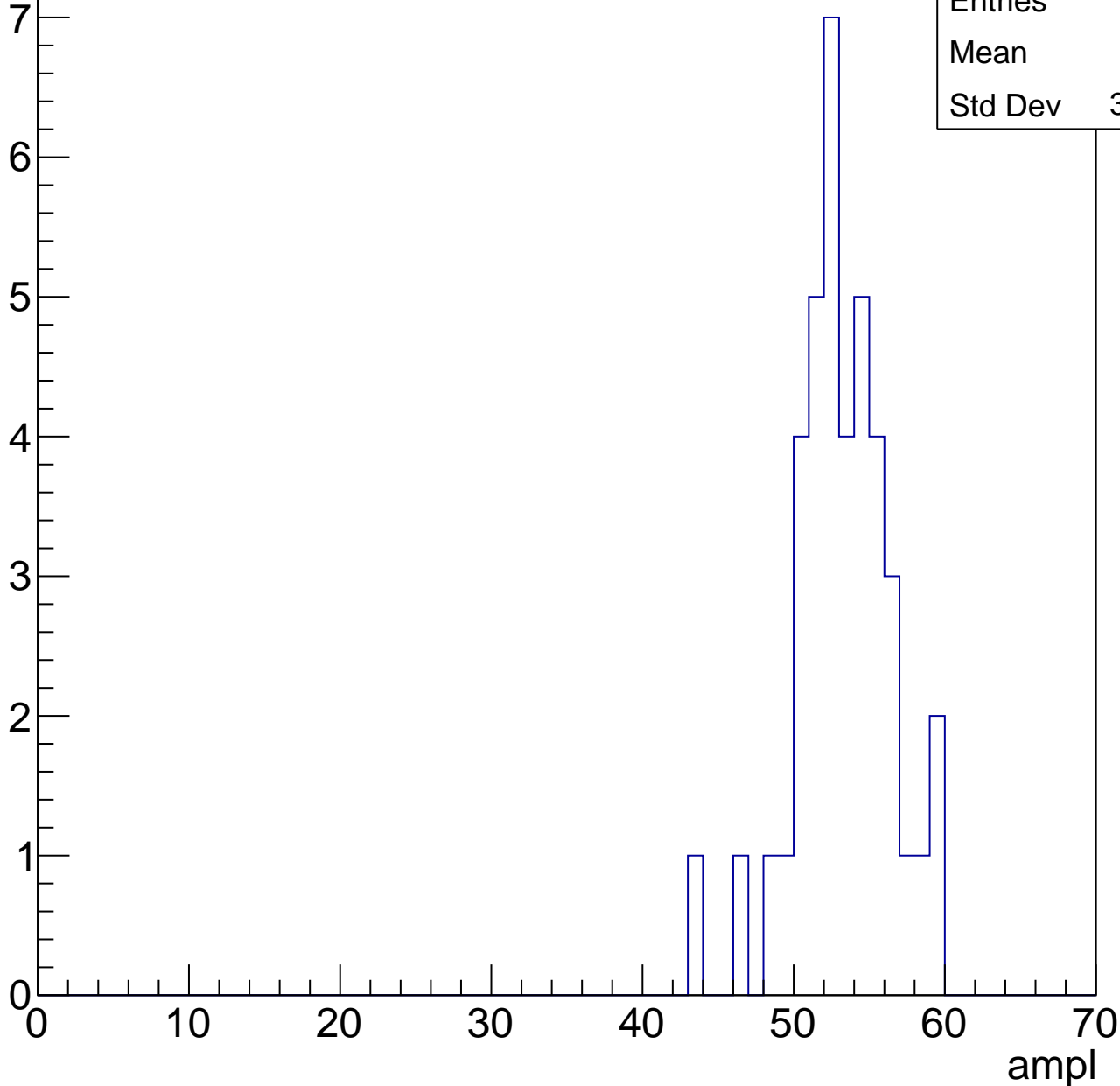


B1L102S, U17-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	52.7
Std Dev	3.203

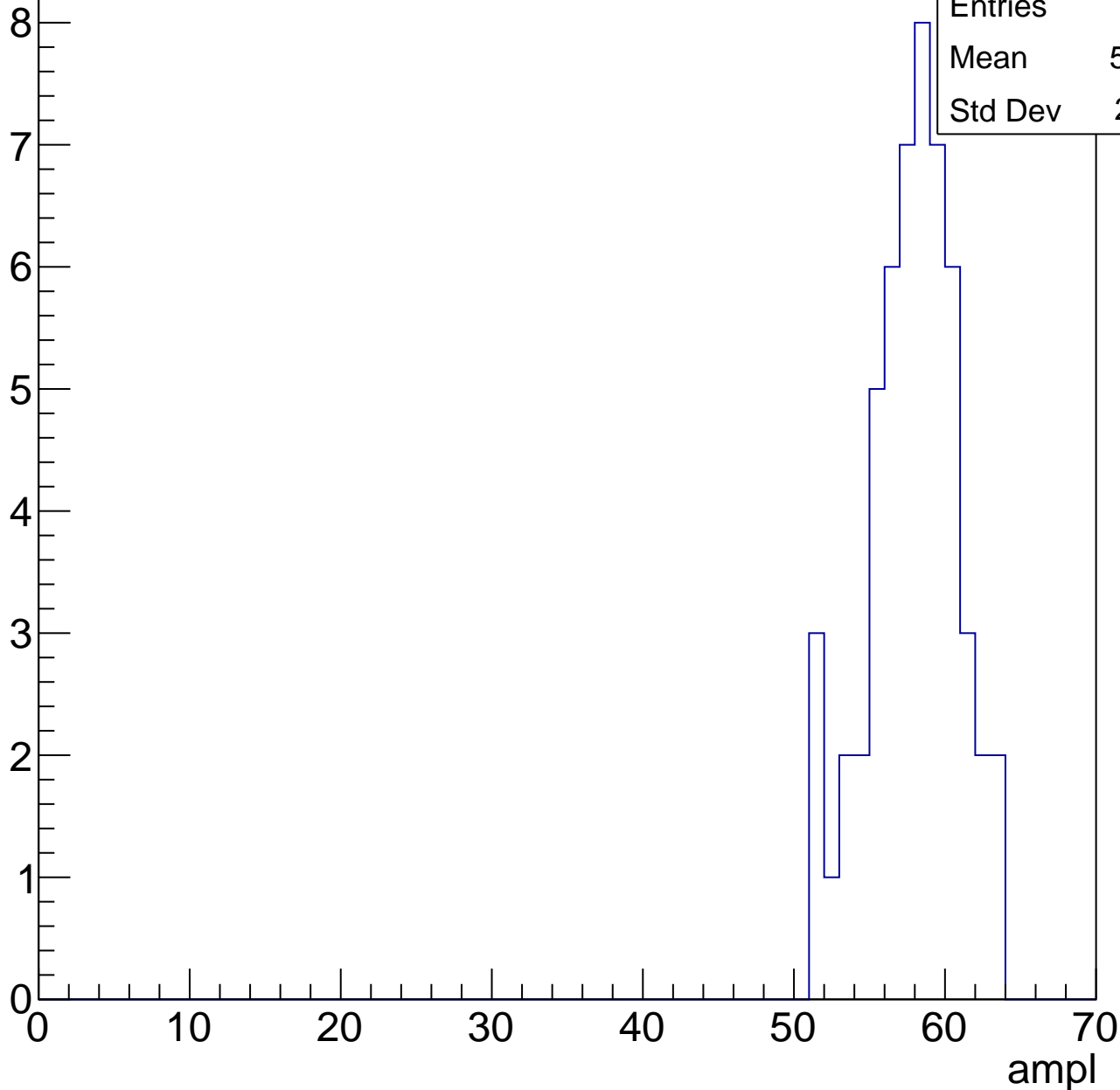


B1L102S, U17-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.39
Std Dev	2.921

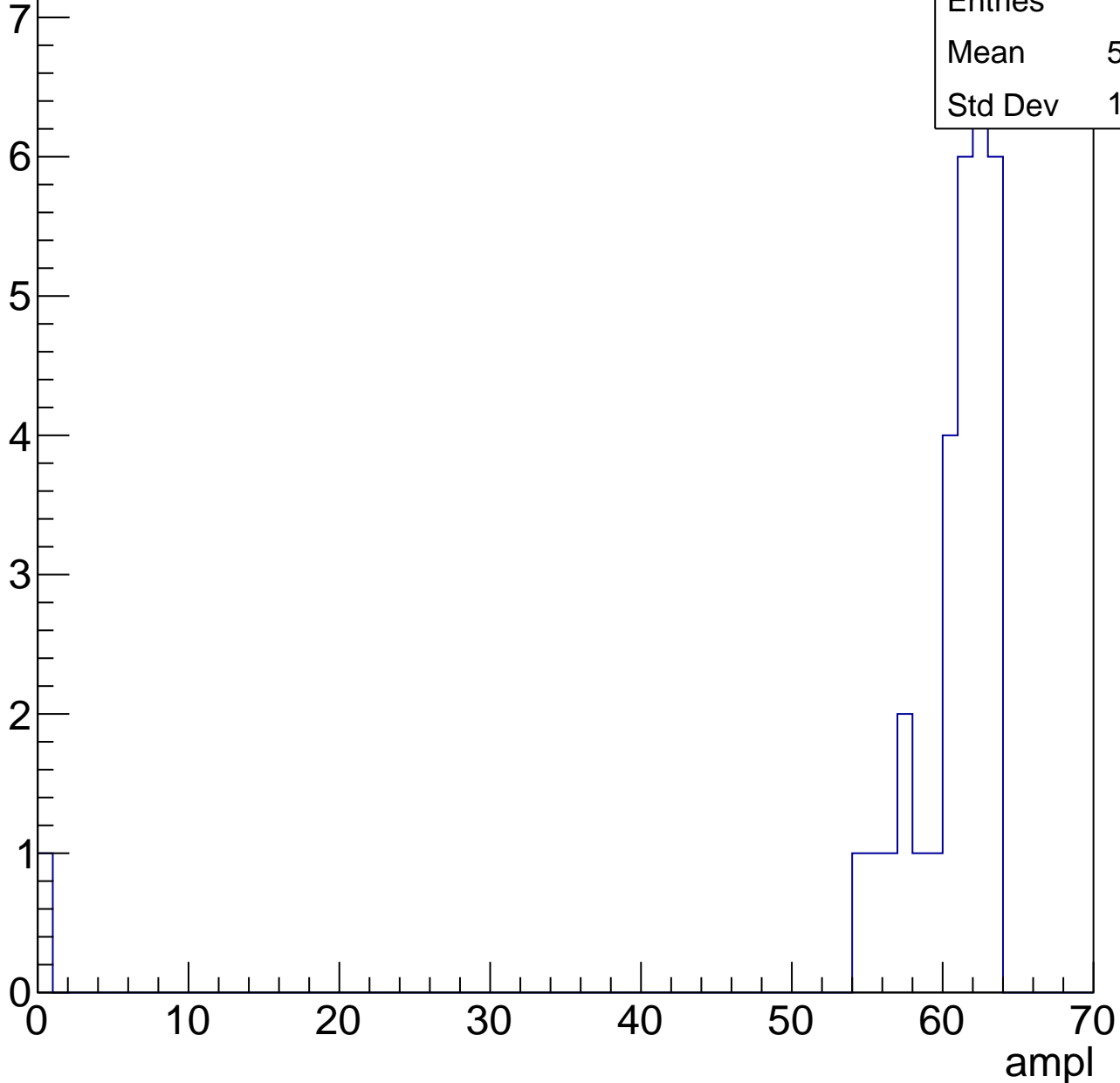


B1L102S, U17-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

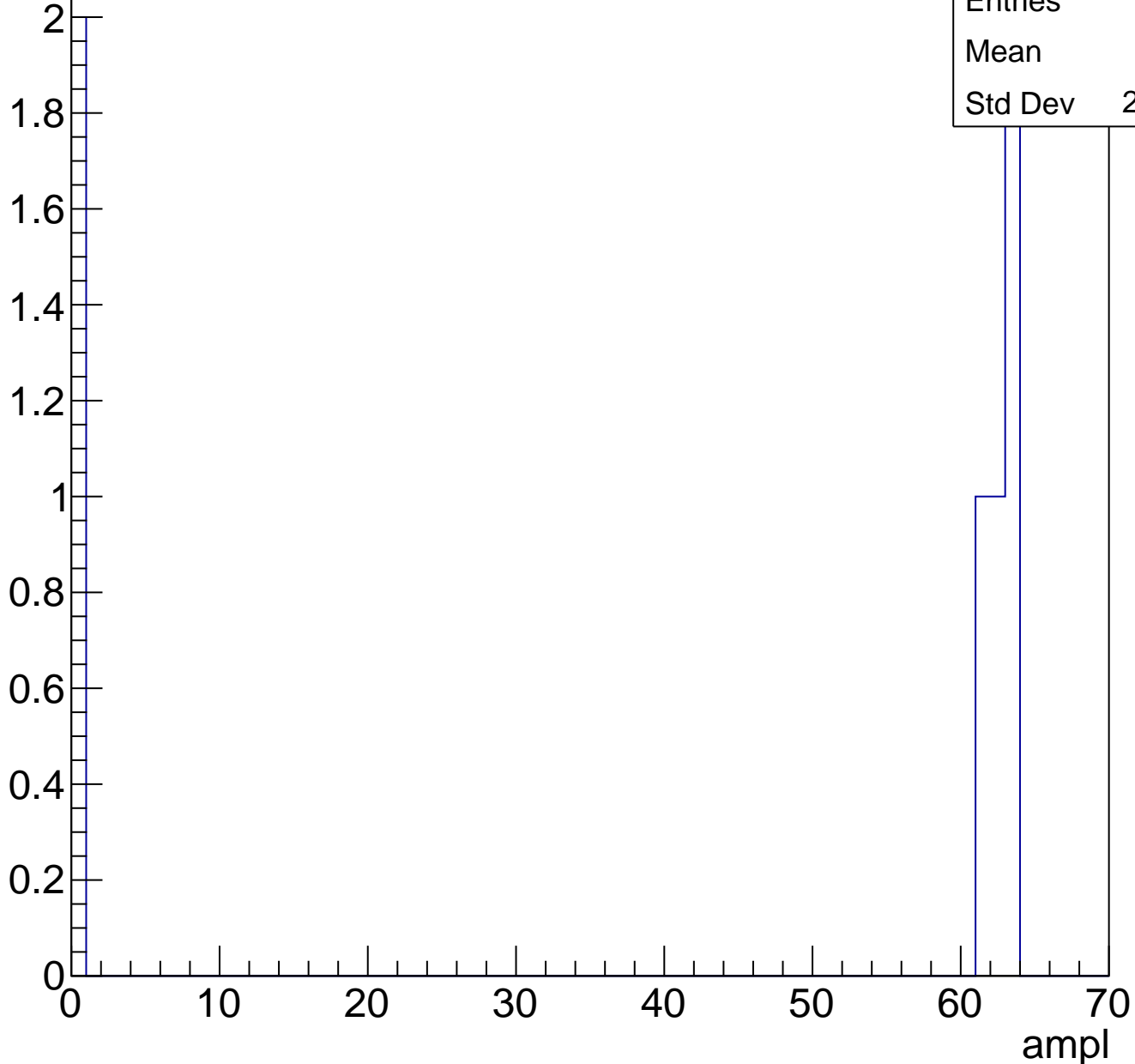
Entries	31
Mean	58.52
Std Dev	10.95



B1L102S, U17-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	6
Mean	41.5
Std Dev	29.35

B1L102S, U17-ch73, adc0

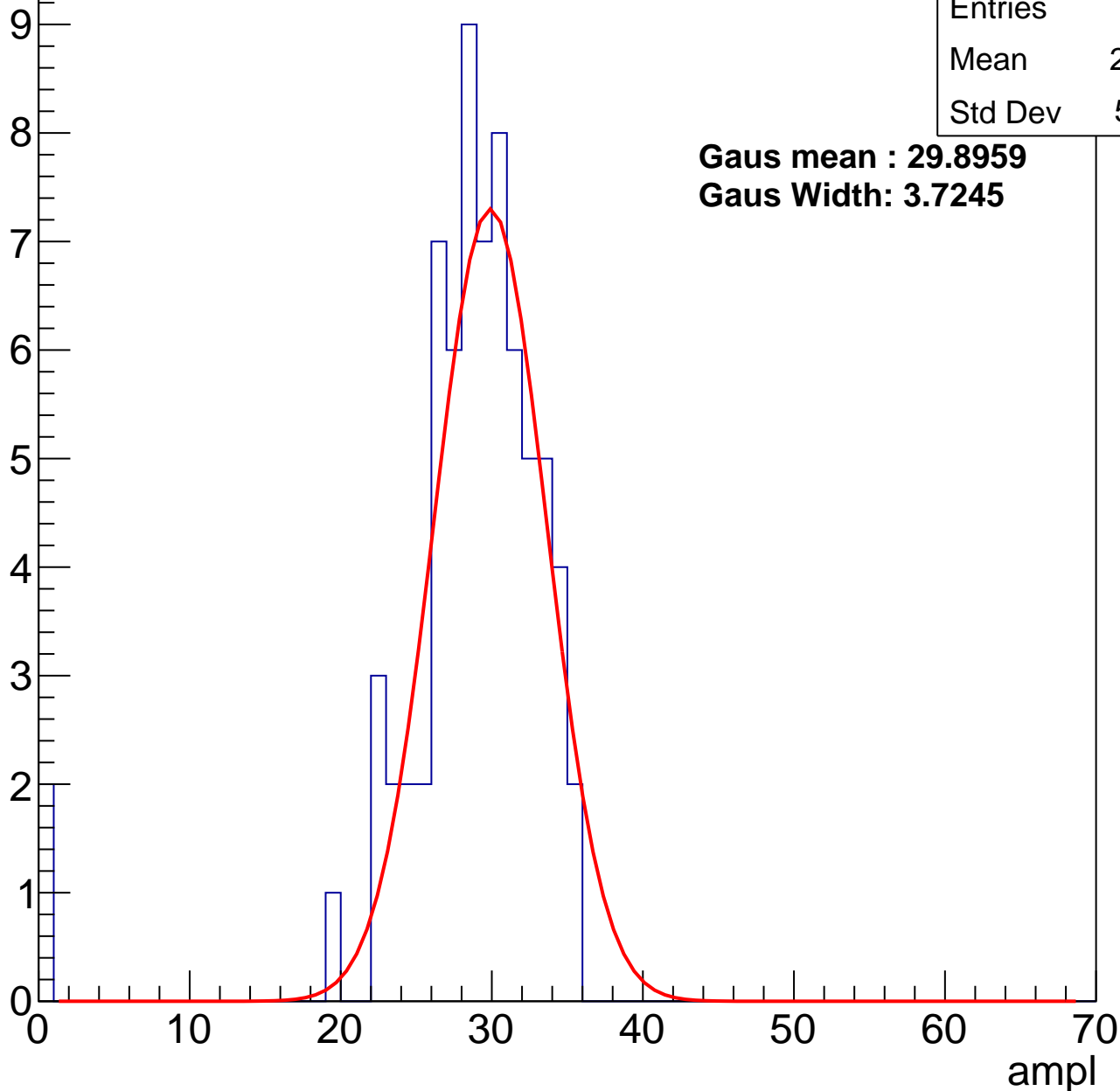
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.96
Std Dev	5.851

Gaus mean : 29.8959

Gaus Width: 3.7245



B1L102S, U17-ch73, adc1

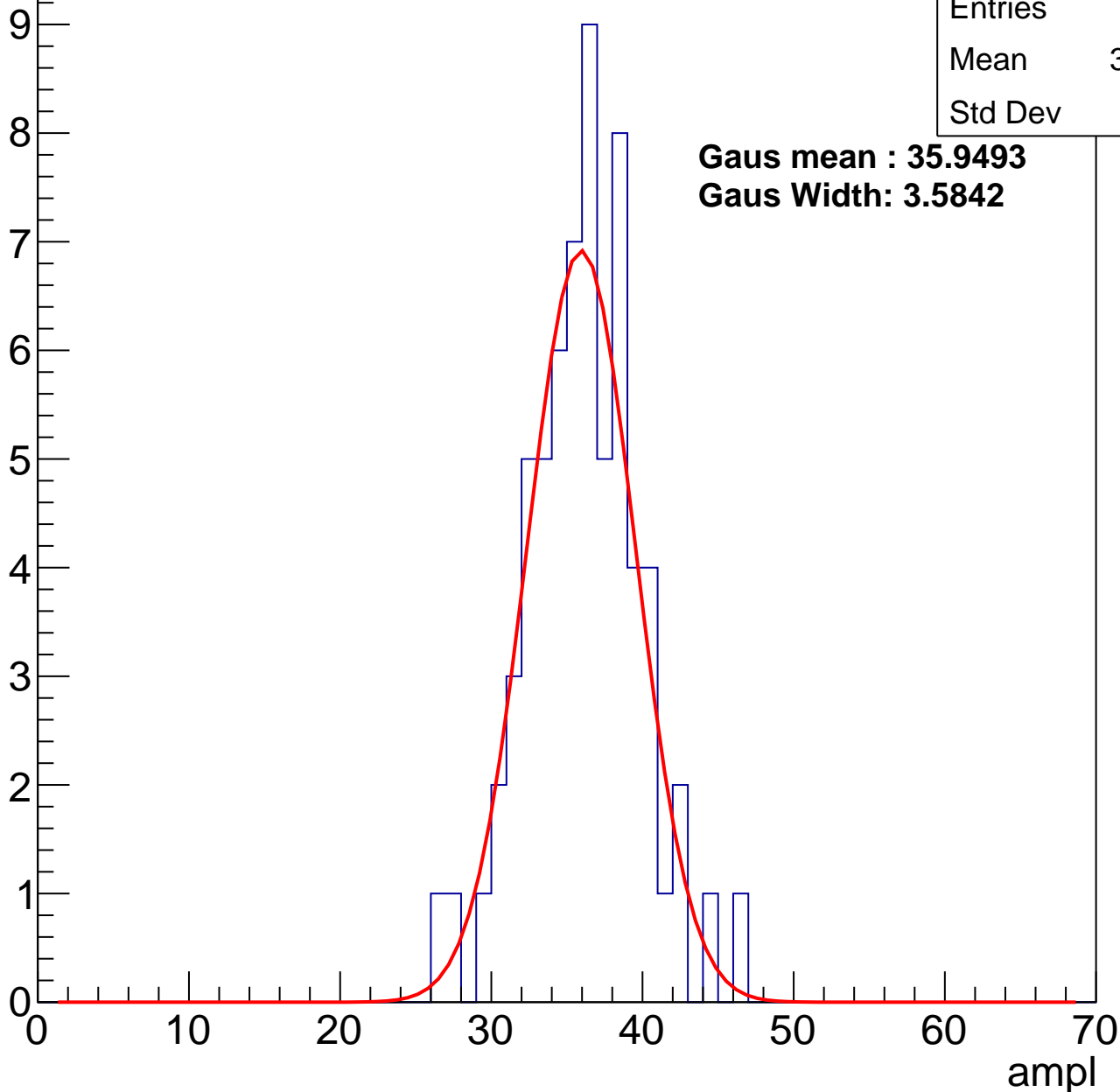
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.65
Std Dev	3.74

Gaus mean : 35.9493

Gaus Width: 3.5842



B1L102S, U17-ch73, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

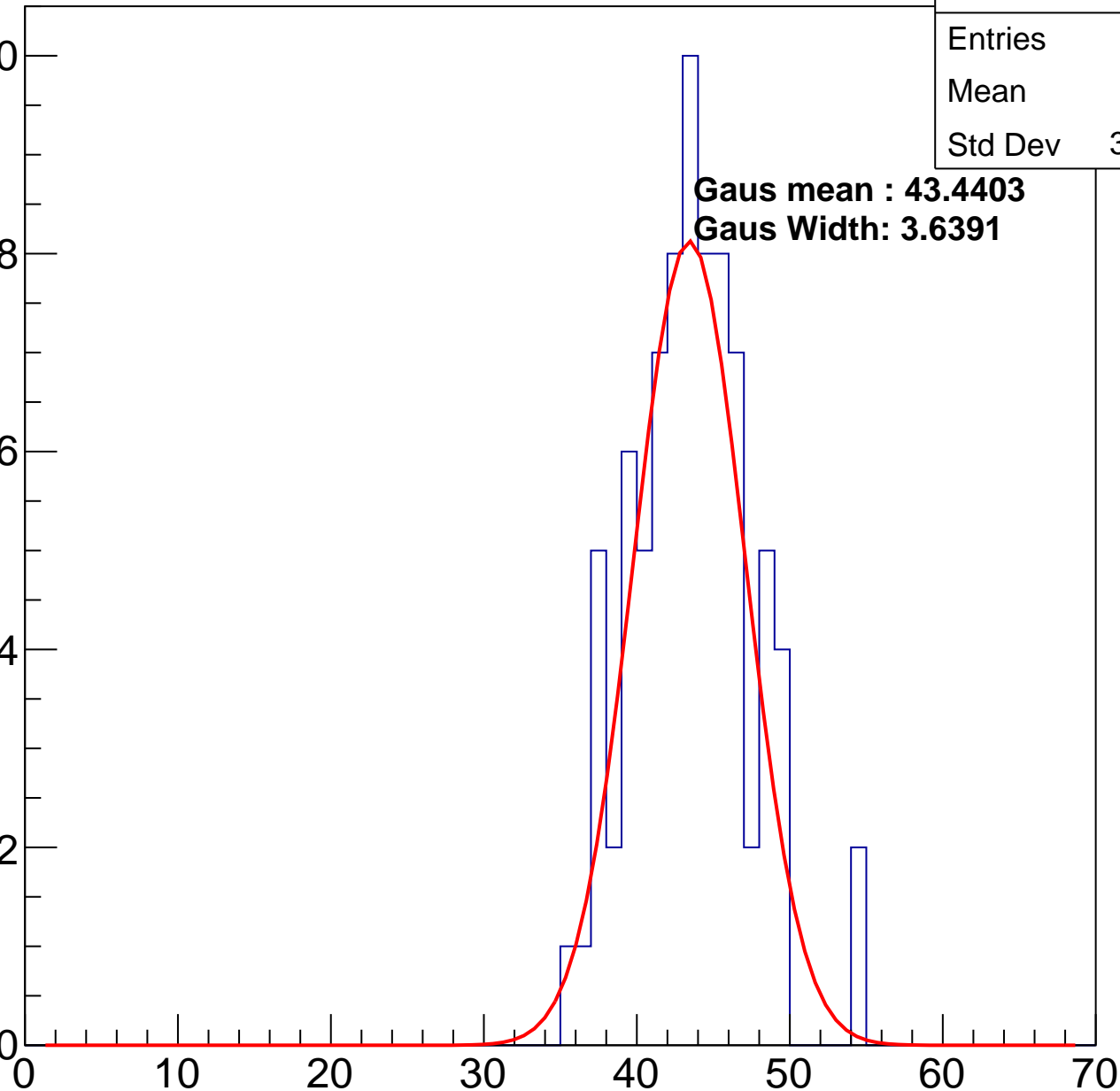
Entries	81
Mean	43.1
Std Dev	3.793

Gaus mean : 43.4403

Gaus Width: 3.6391

10
8
6
4
2
0

ampl

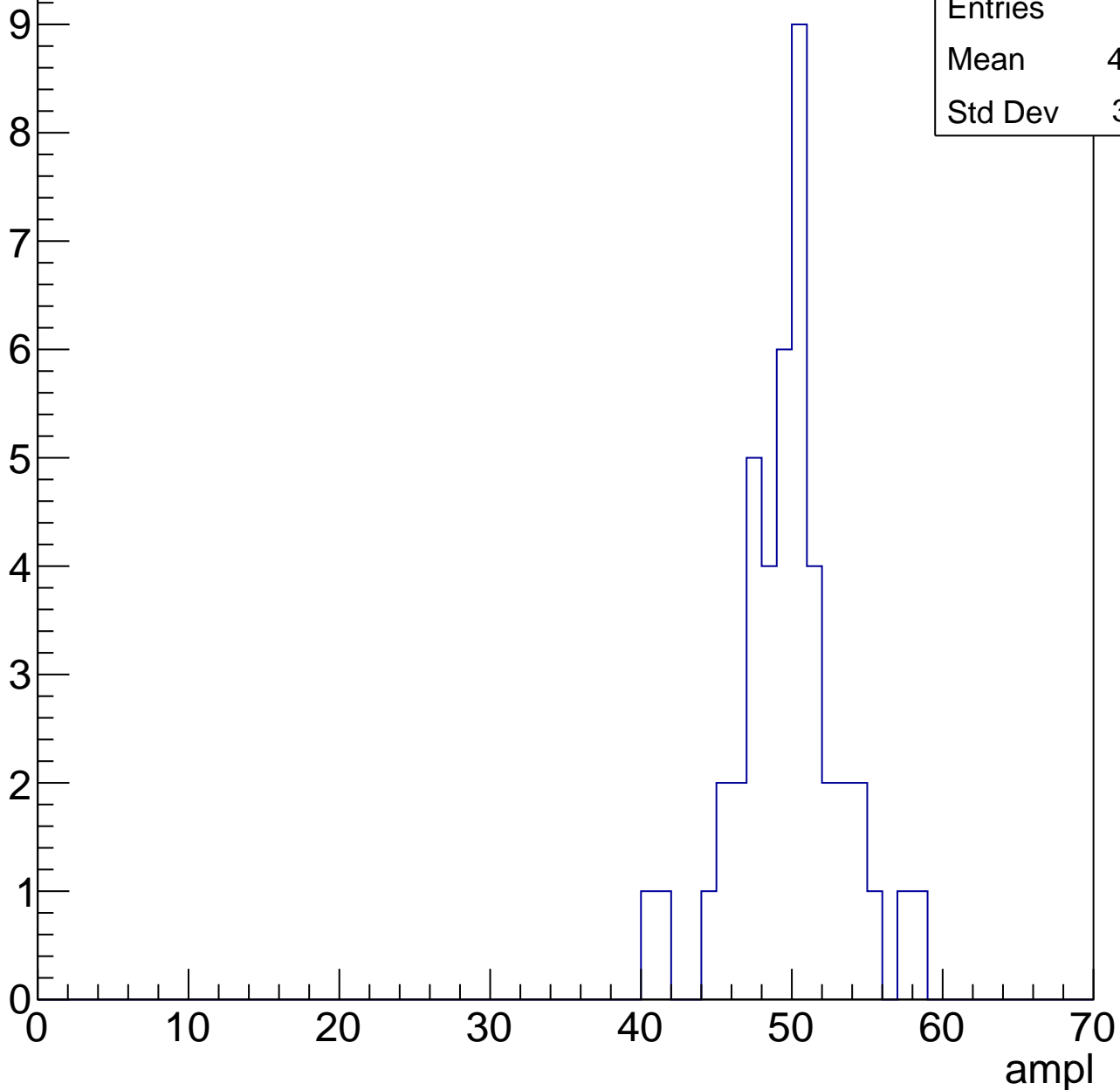


B1L102S, U17-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	49.32
Std Dev	3.521

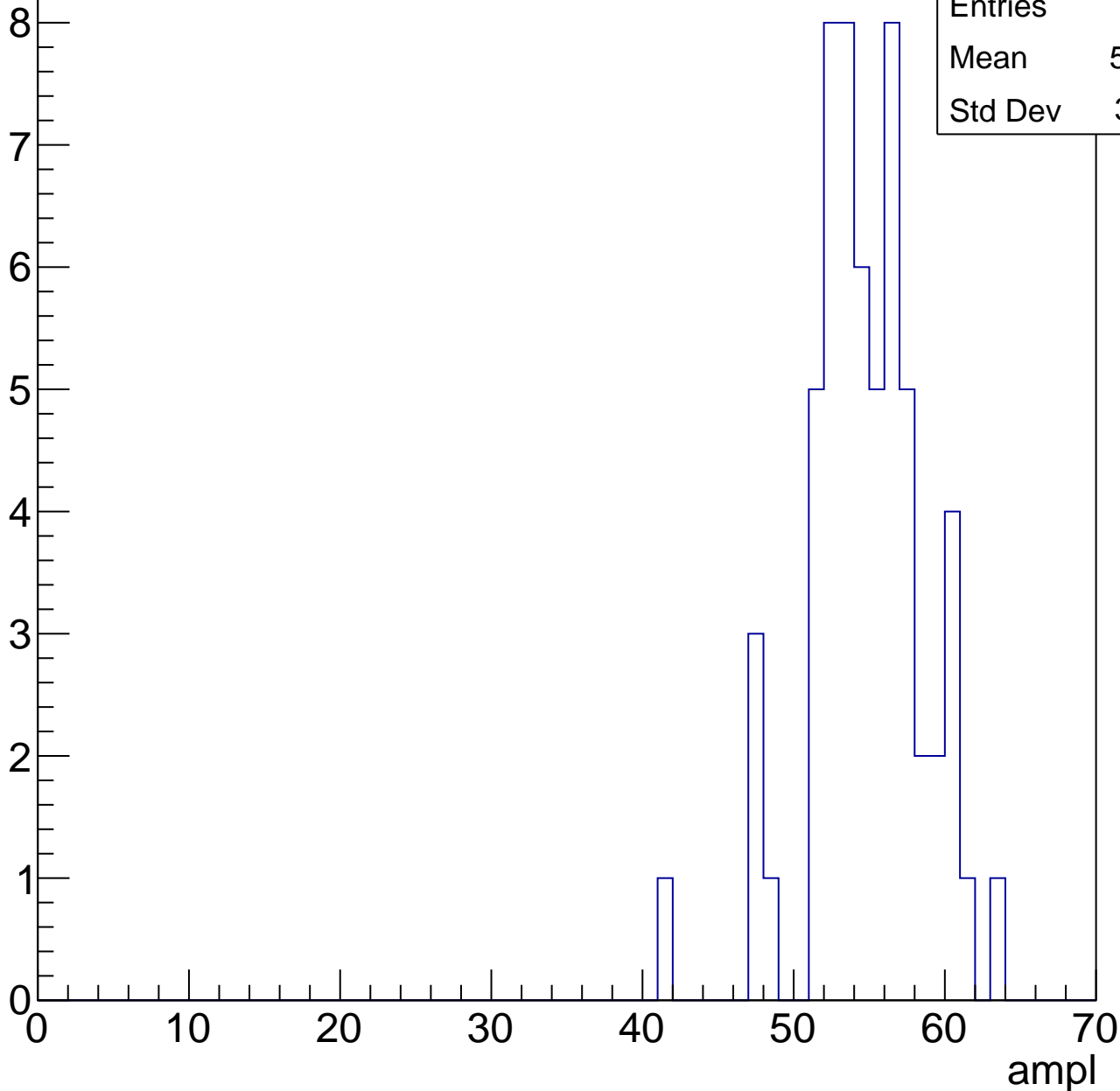


B1L102S, U17-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.25
Std Dev	3.811

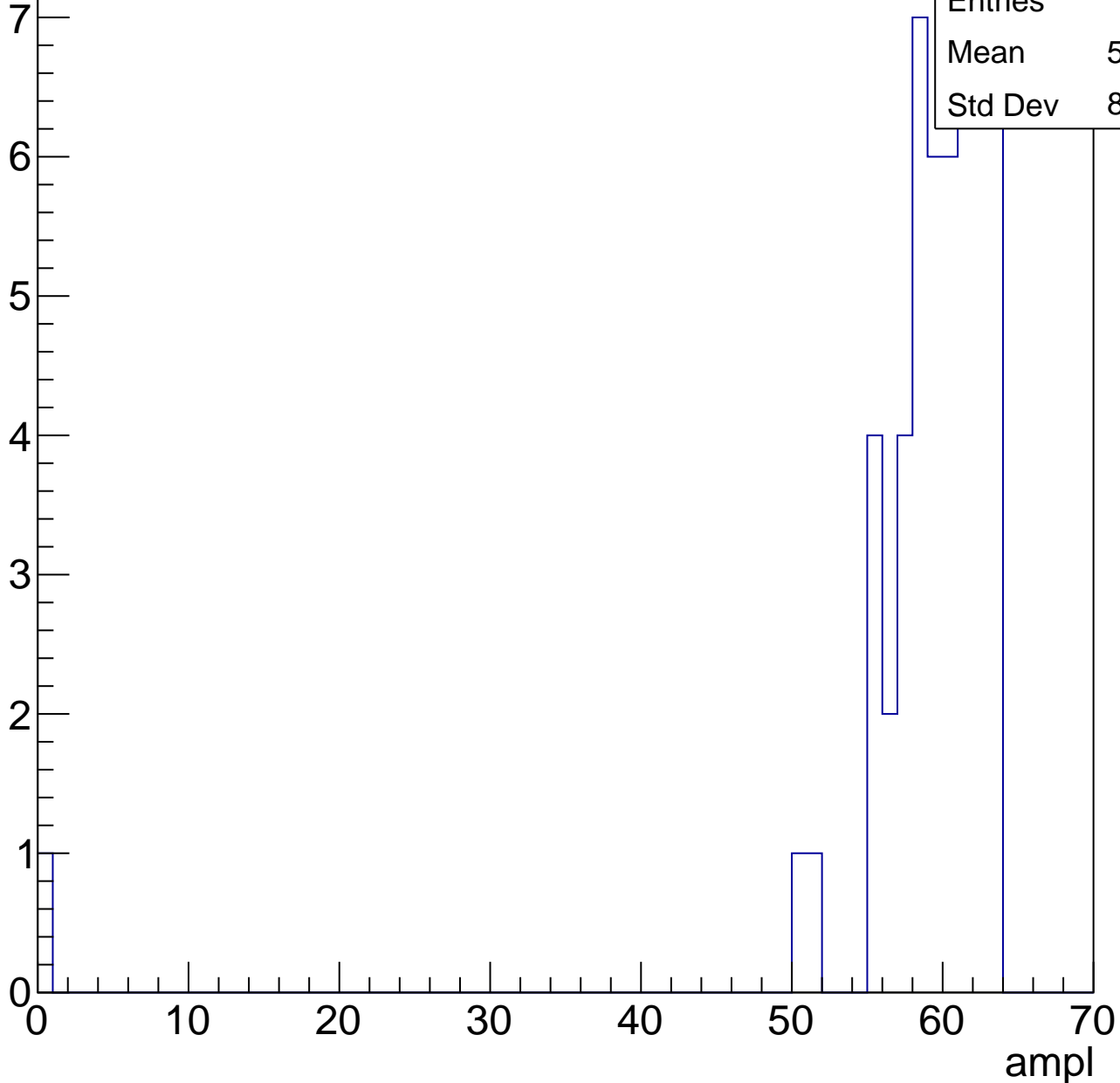


B1L102S, U17-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

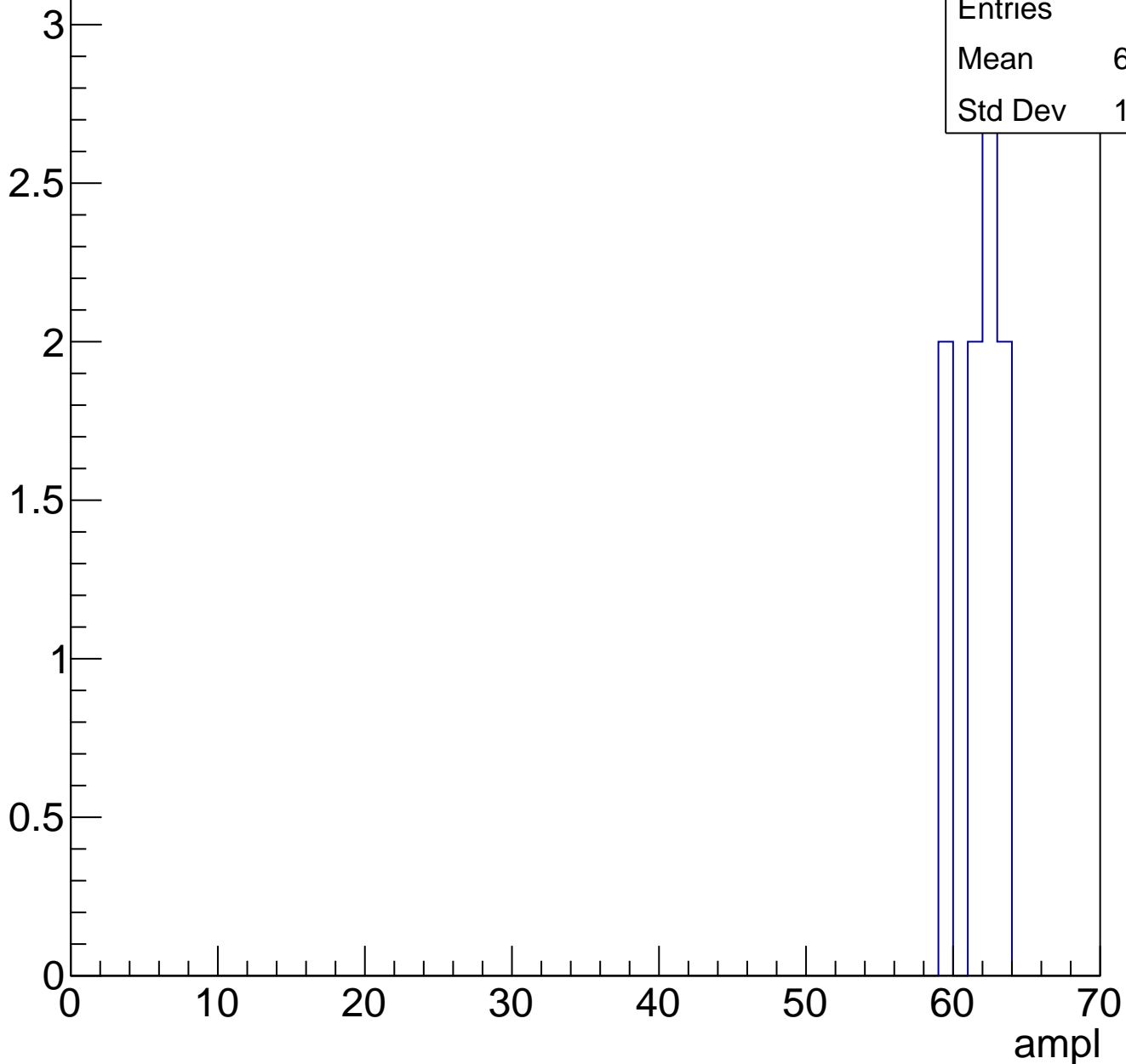
Entries	53
Mean	58.17
Std Dev	8.582



B1L102S, U17-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch74, adc0

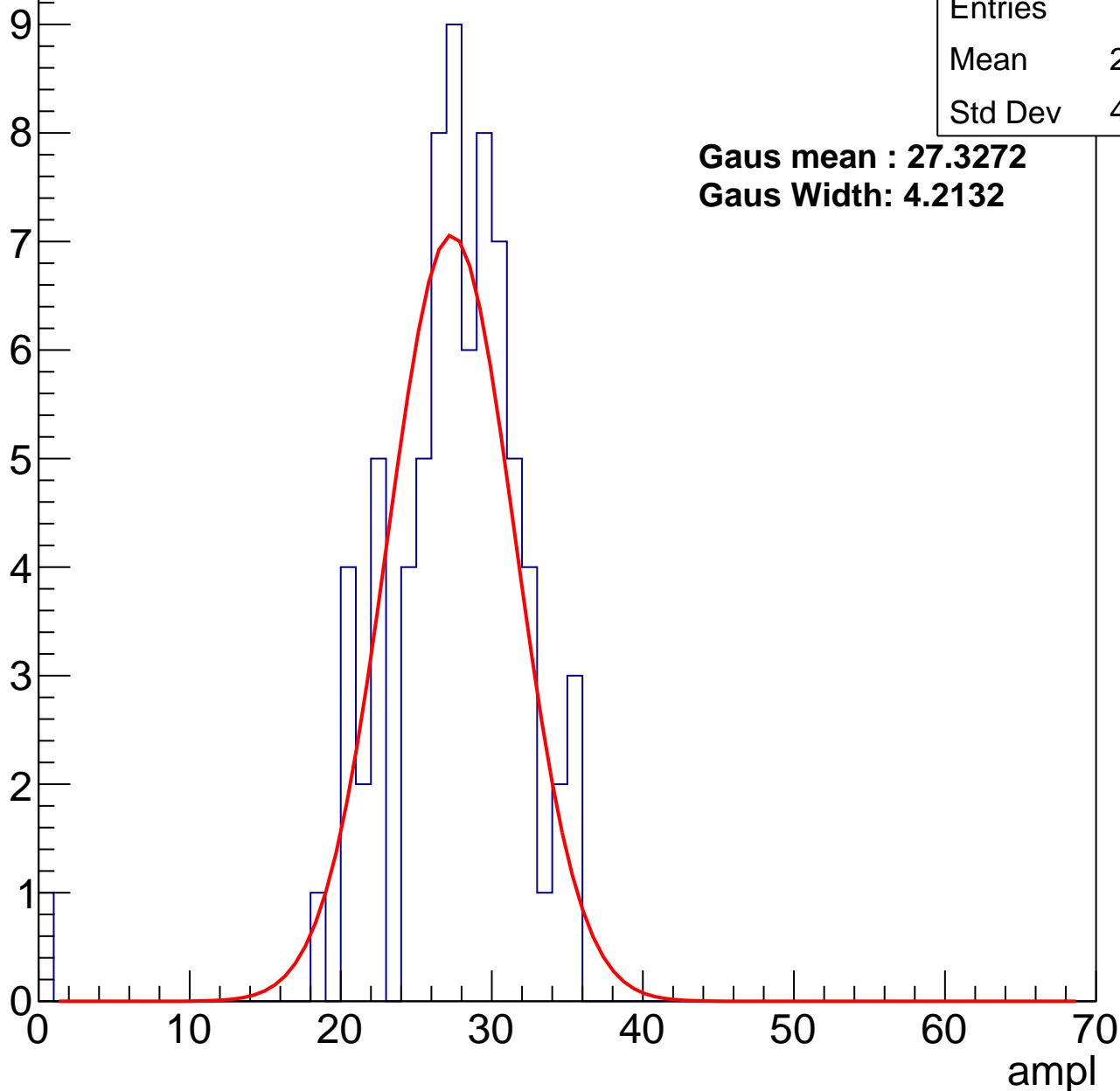
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	26.95
Std Dev	4.998

Gaus mean : 27.3272

Gaus Width: 4.2132



B1L102S, U17-ch74, adc1

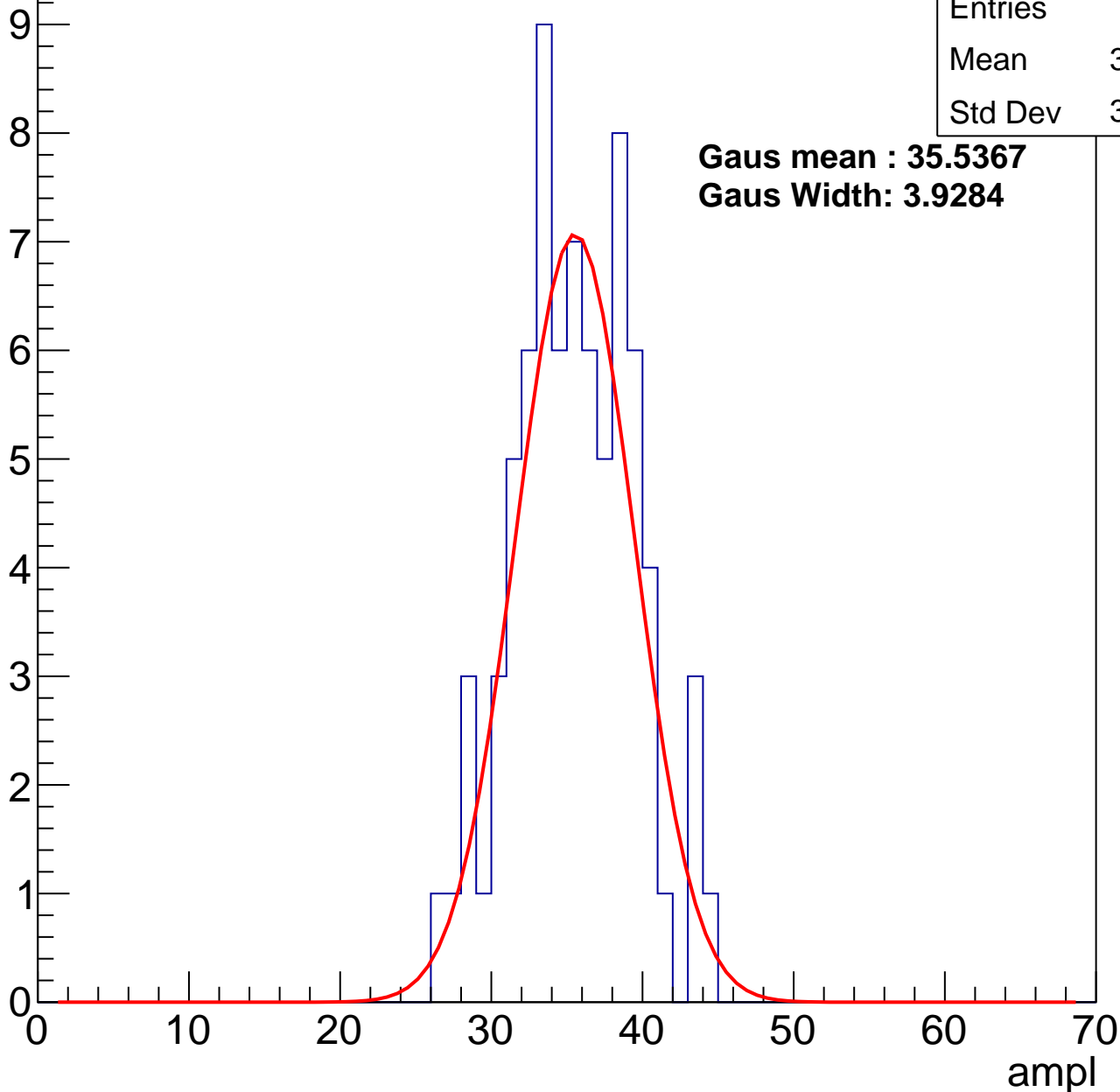
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.03
Std Dev	3.944

Gaus mean : 35.5367

Gaus Width: 3.9284



B1L102S, U17-ch74, adc2

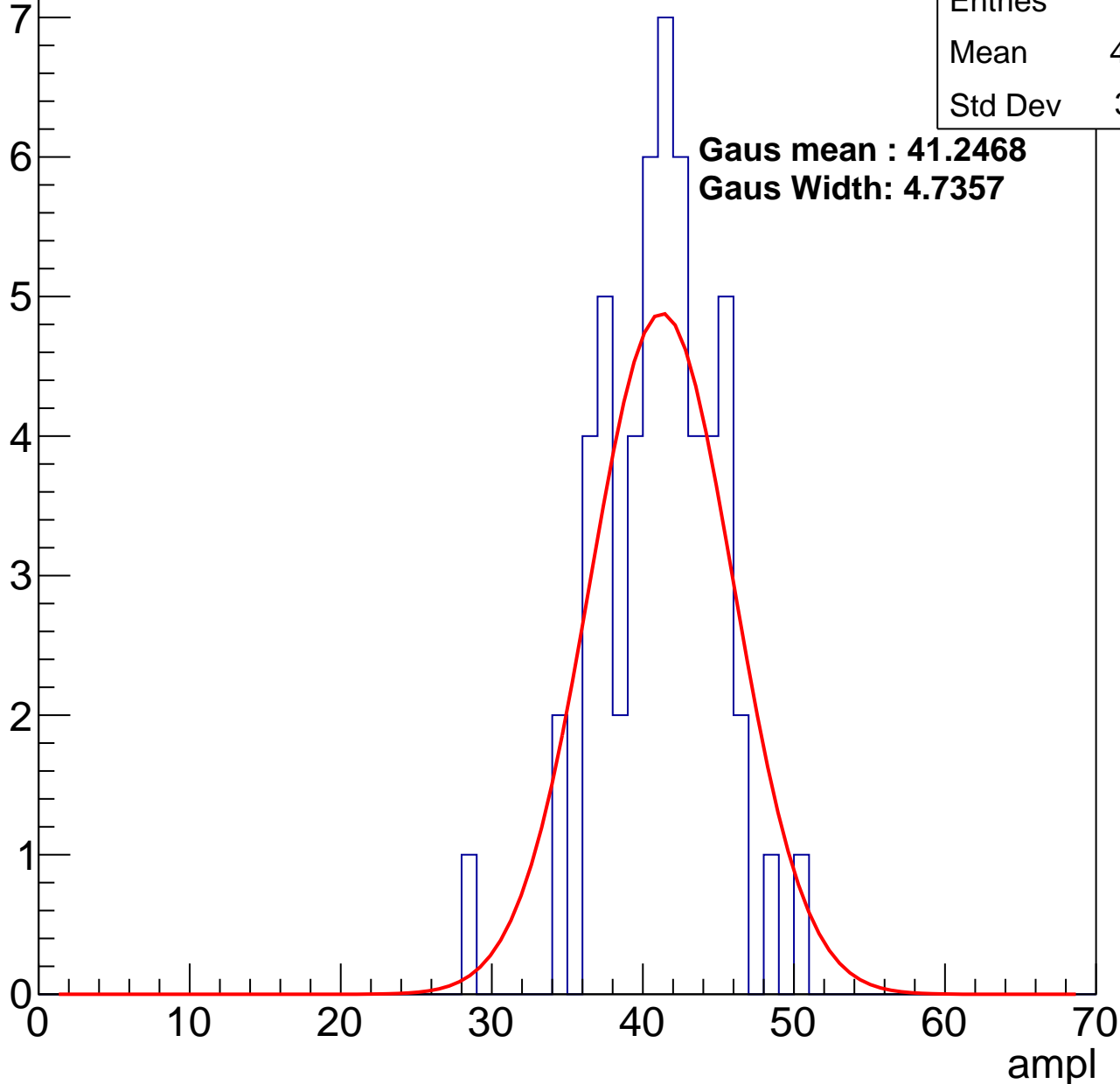
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	40.72
Std Dev	3.851

Gaus mean : 41.2468

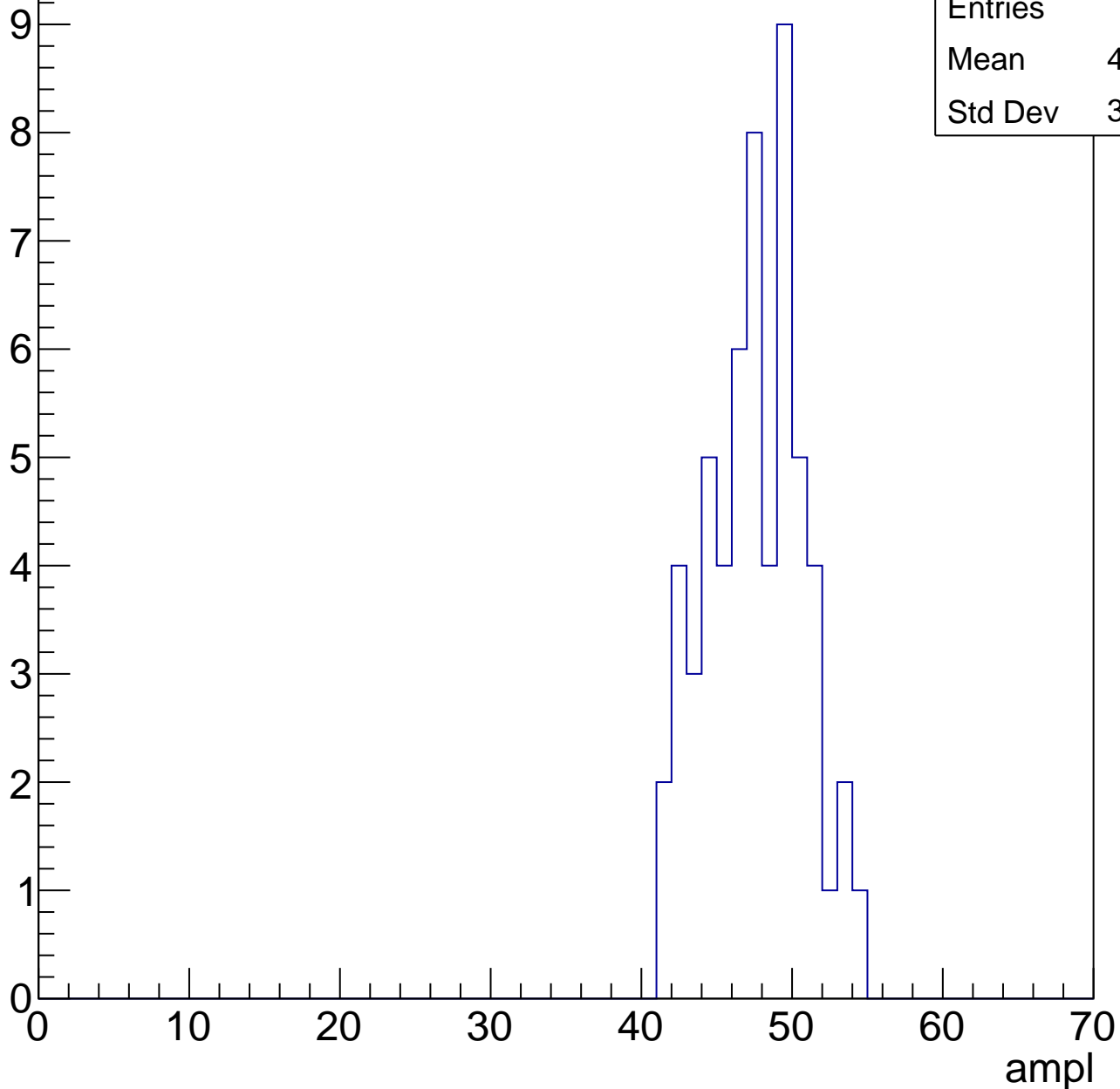
Gaus Width: 4.7357



B1L102S, U17-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

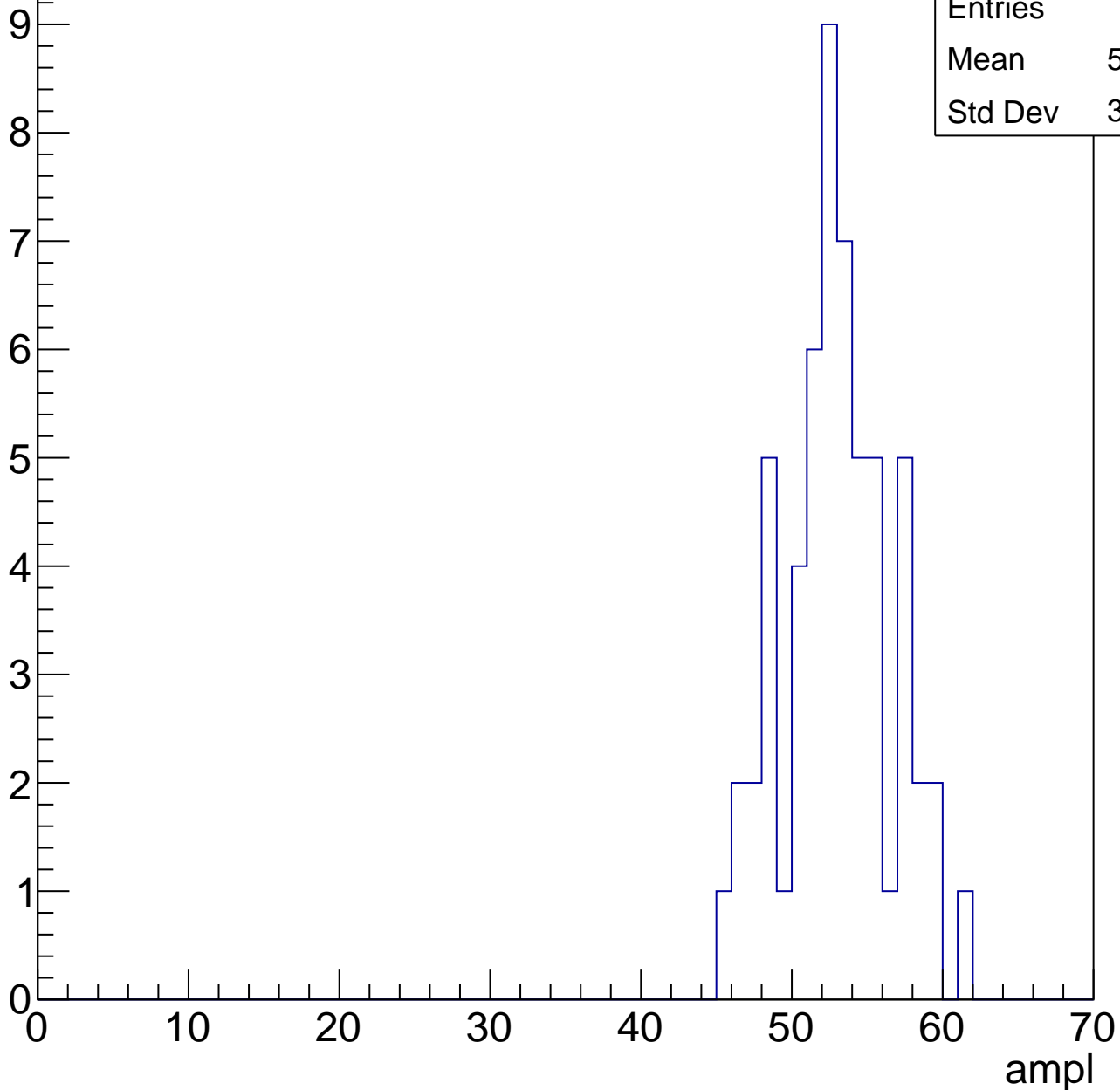


B1L102S, U17-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	52.52
Std Dev	3.549

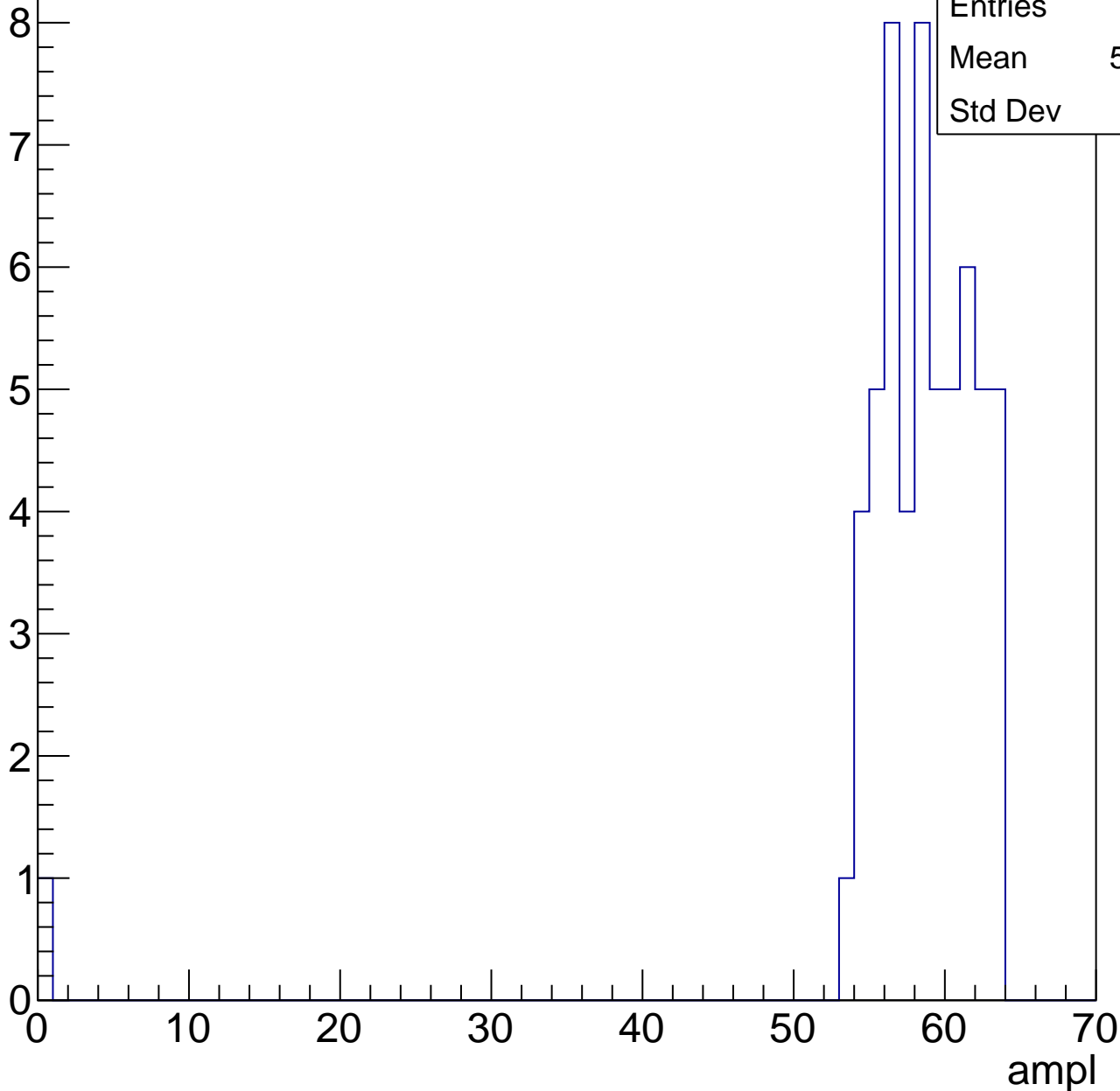


B1L102S, U17-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.37
Std Dev	8.16

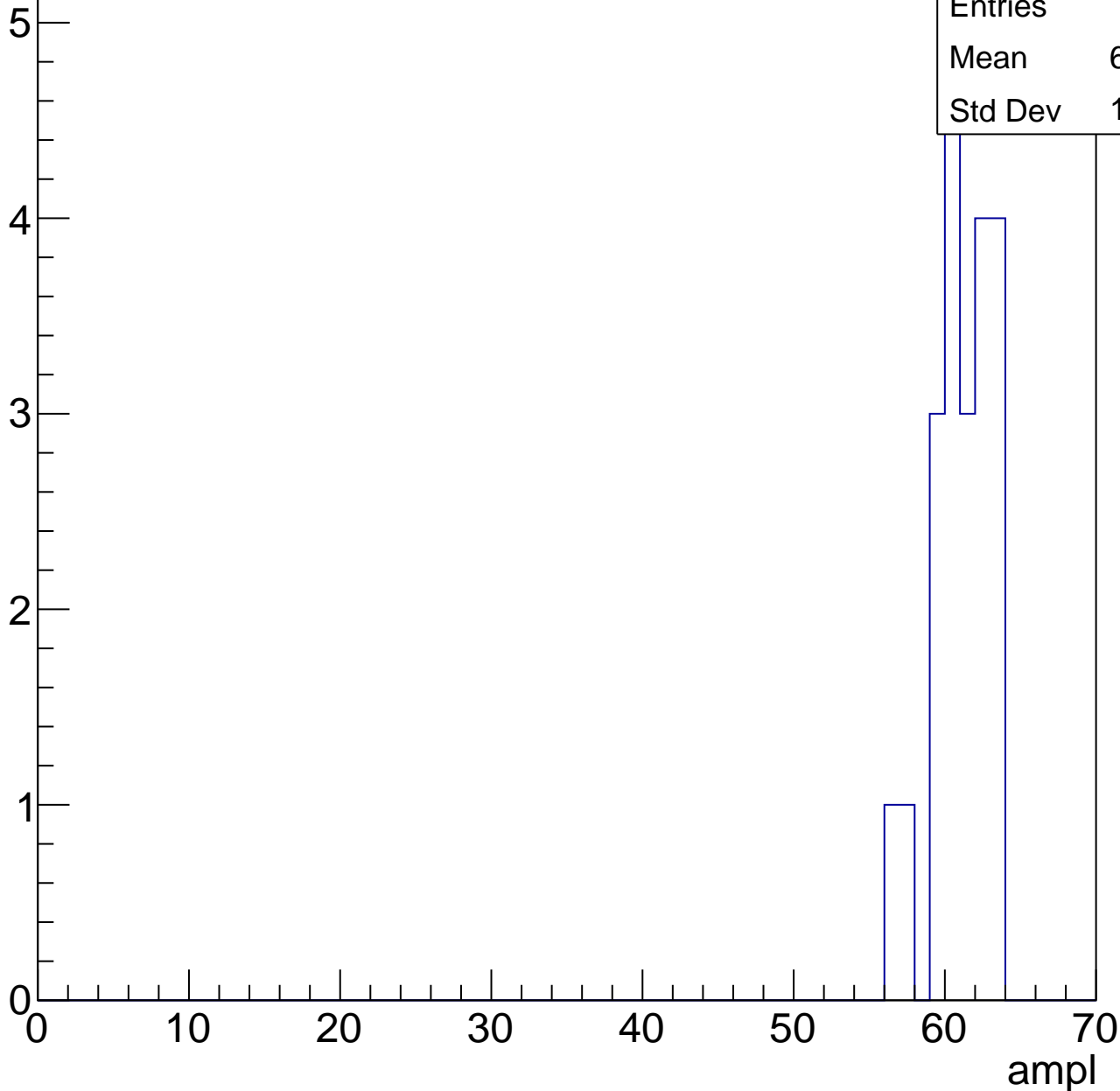


B1L102S, U17-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	60.62
Std Dev	1.889



B1L102S, U17-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L102S, U17-ch75, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	28.28
Std Dev	3.801

Gaus mean : 28.4058

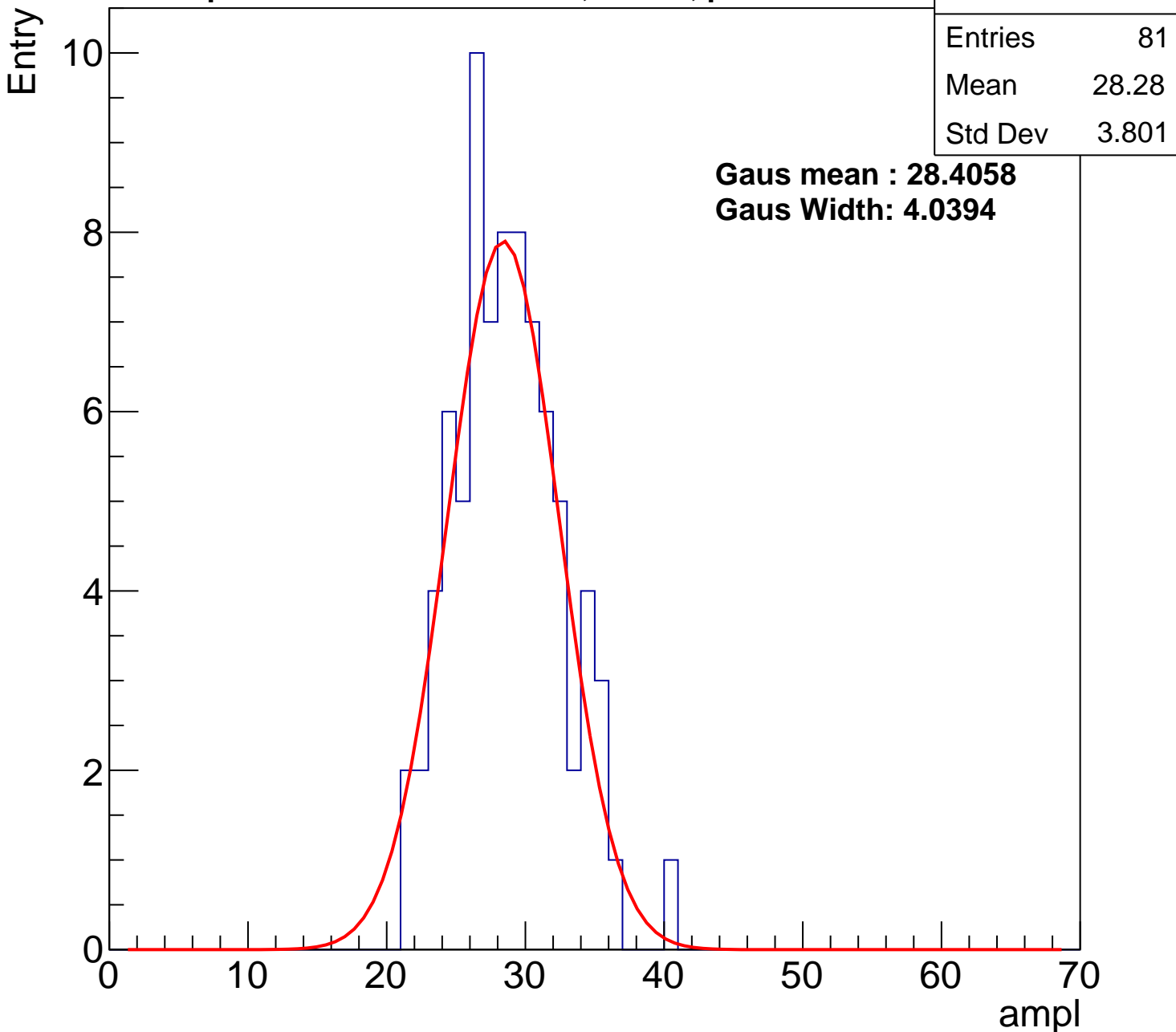
Gaus Width: 4.0394

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch75, adc1

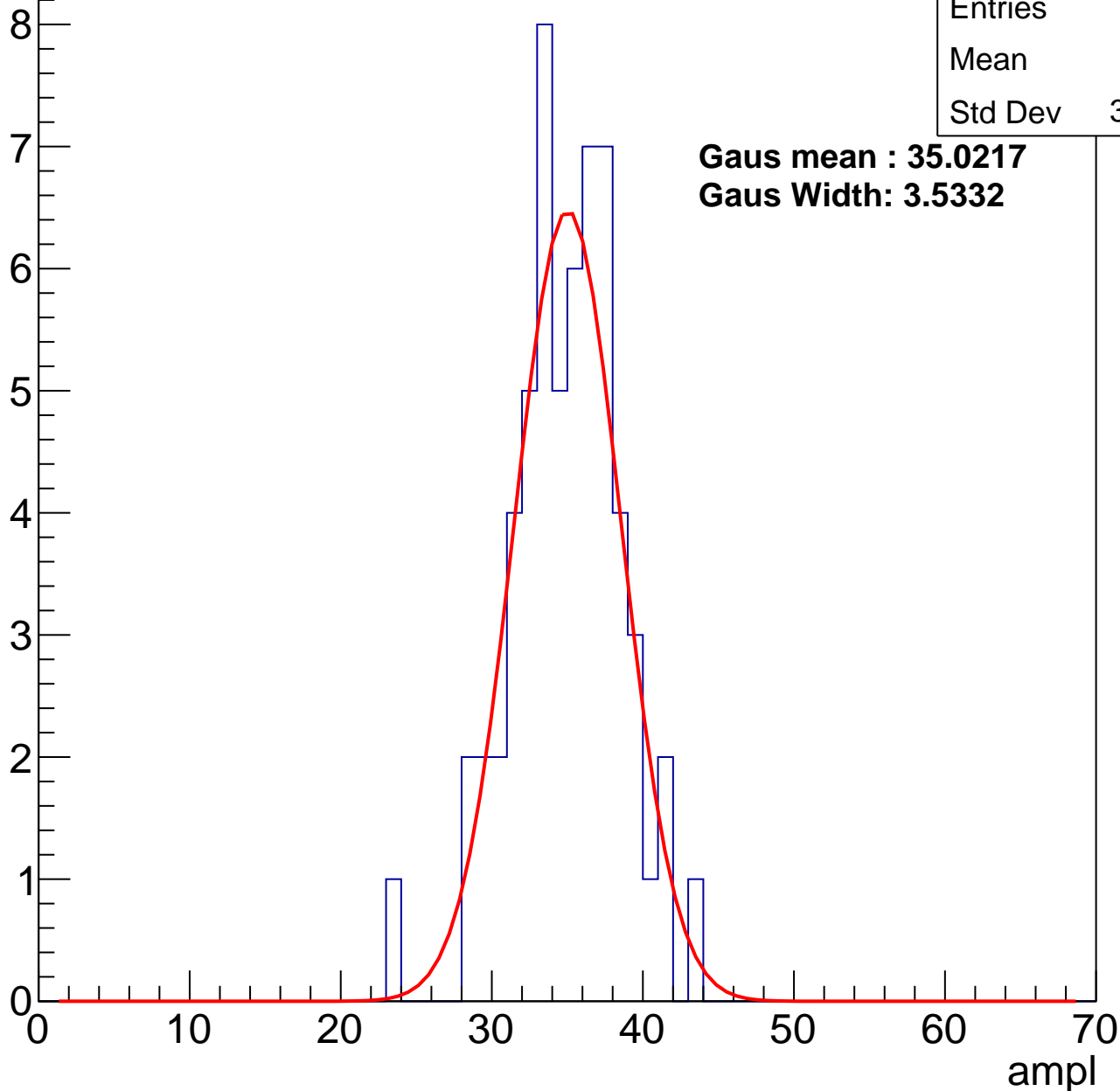
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.5
Std Dev	3.594

Gaus mean : 35.0217

Gaus Width: 3.5332



B1L102S, U17-ch75, adc2

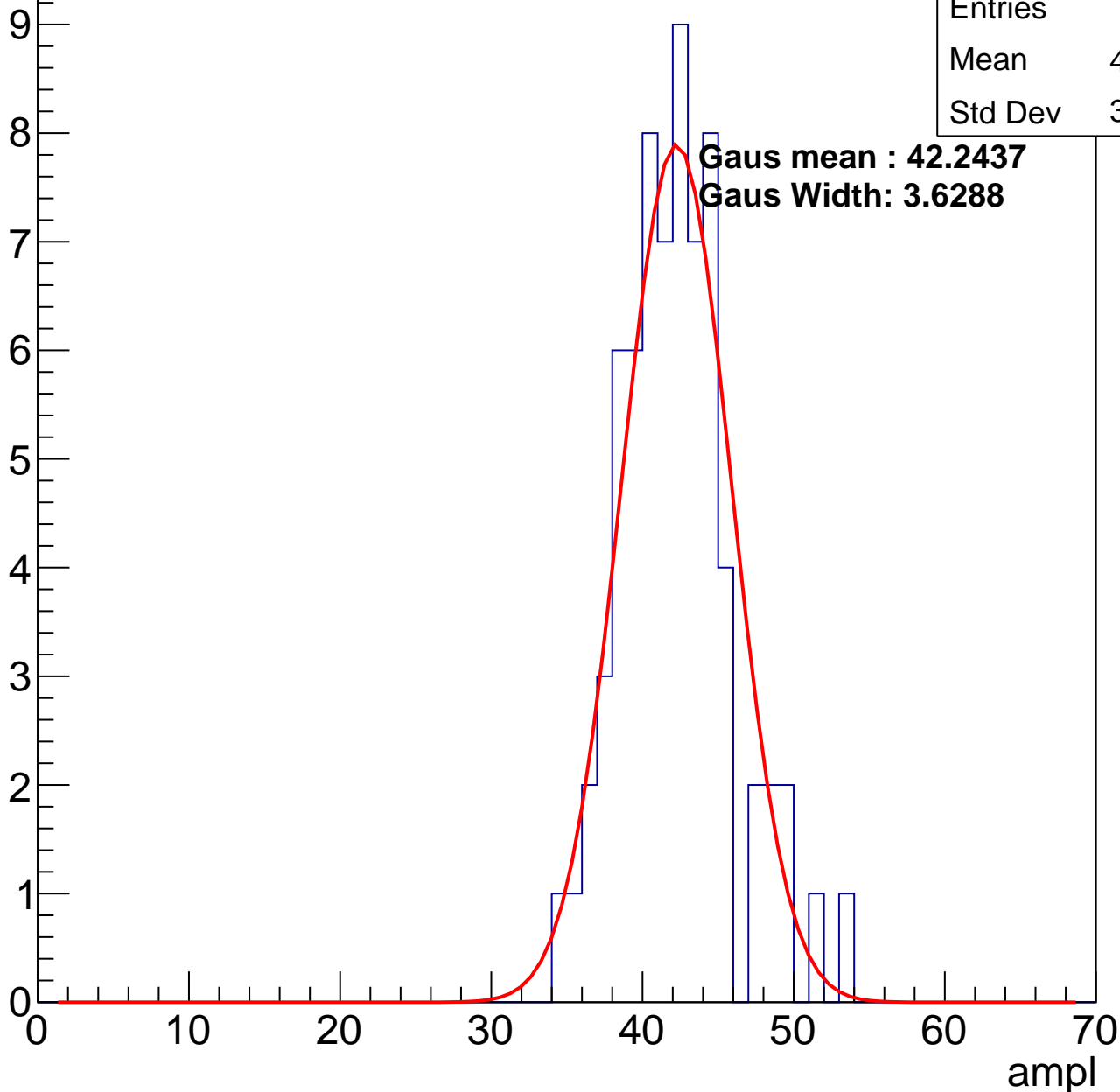
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.77
Std Dev	3.669

Gaus mean : 42.2437

Gaus Width: 3.6288



B1L102S, U17-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	48.21
Std Dev	3.52

Entry

10

8

6

4

2

0

0

10

20

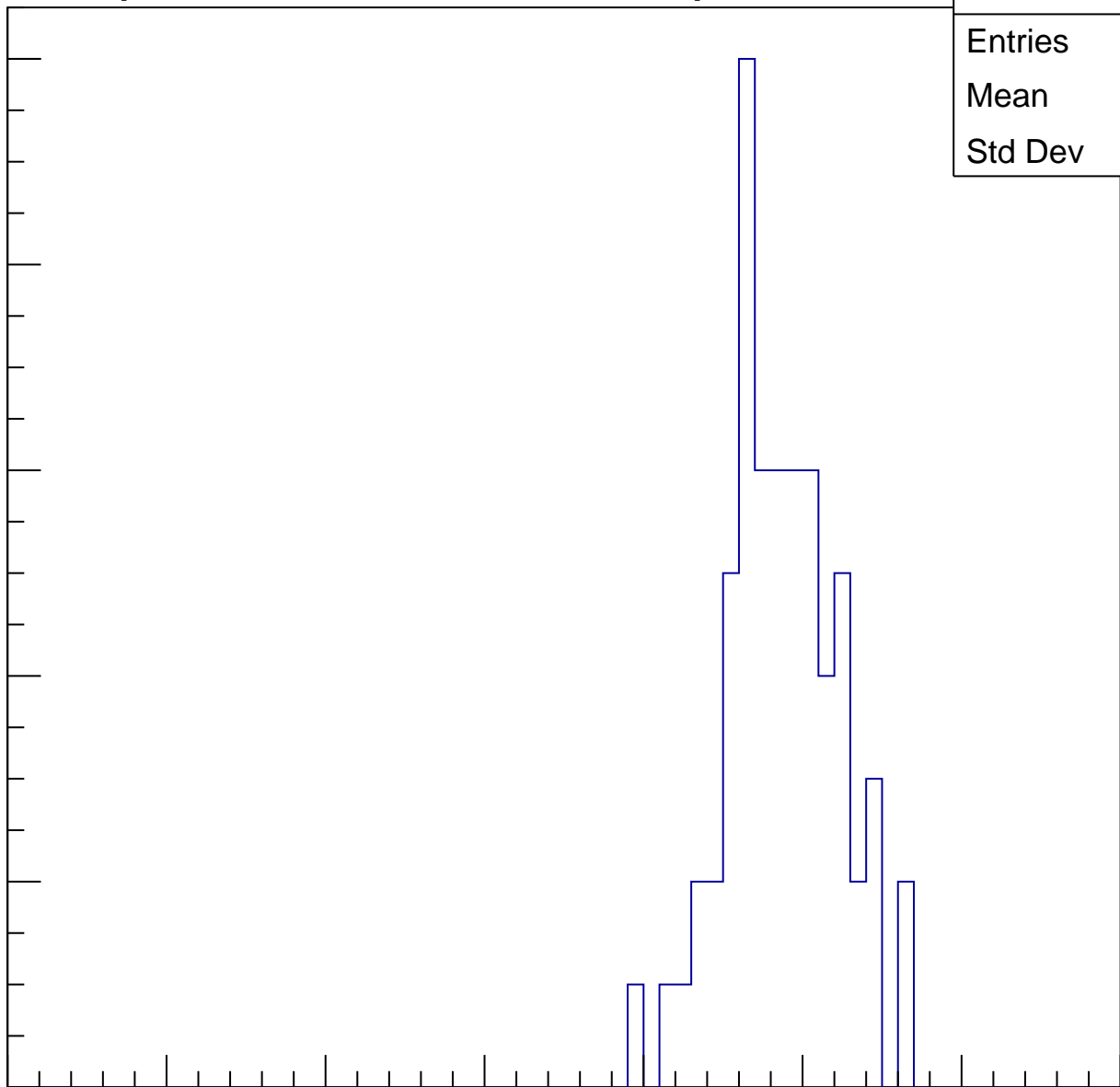
30

40

50

60

ampl

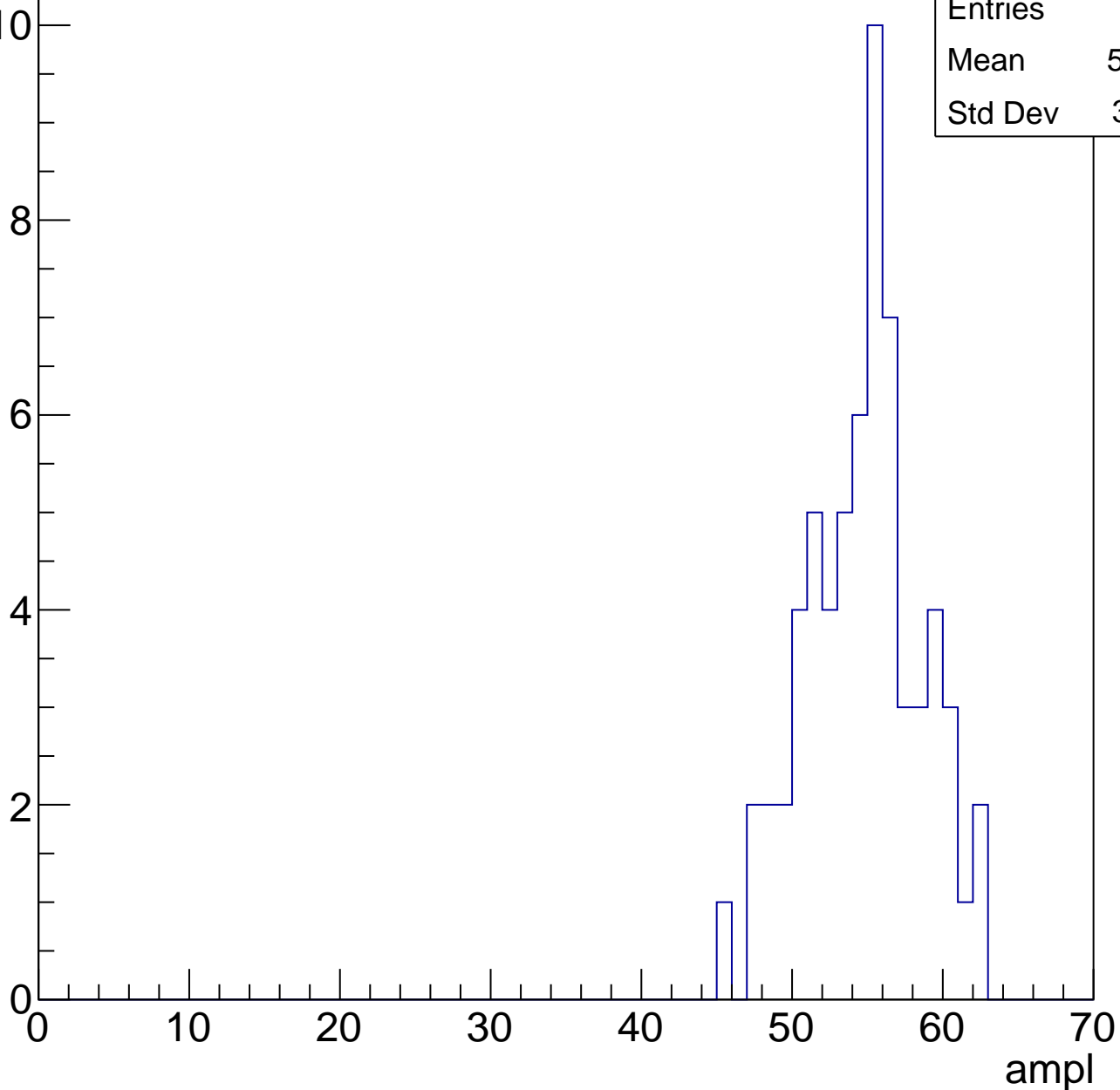


B1L102S, U17-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.27
Std Dev	3.801

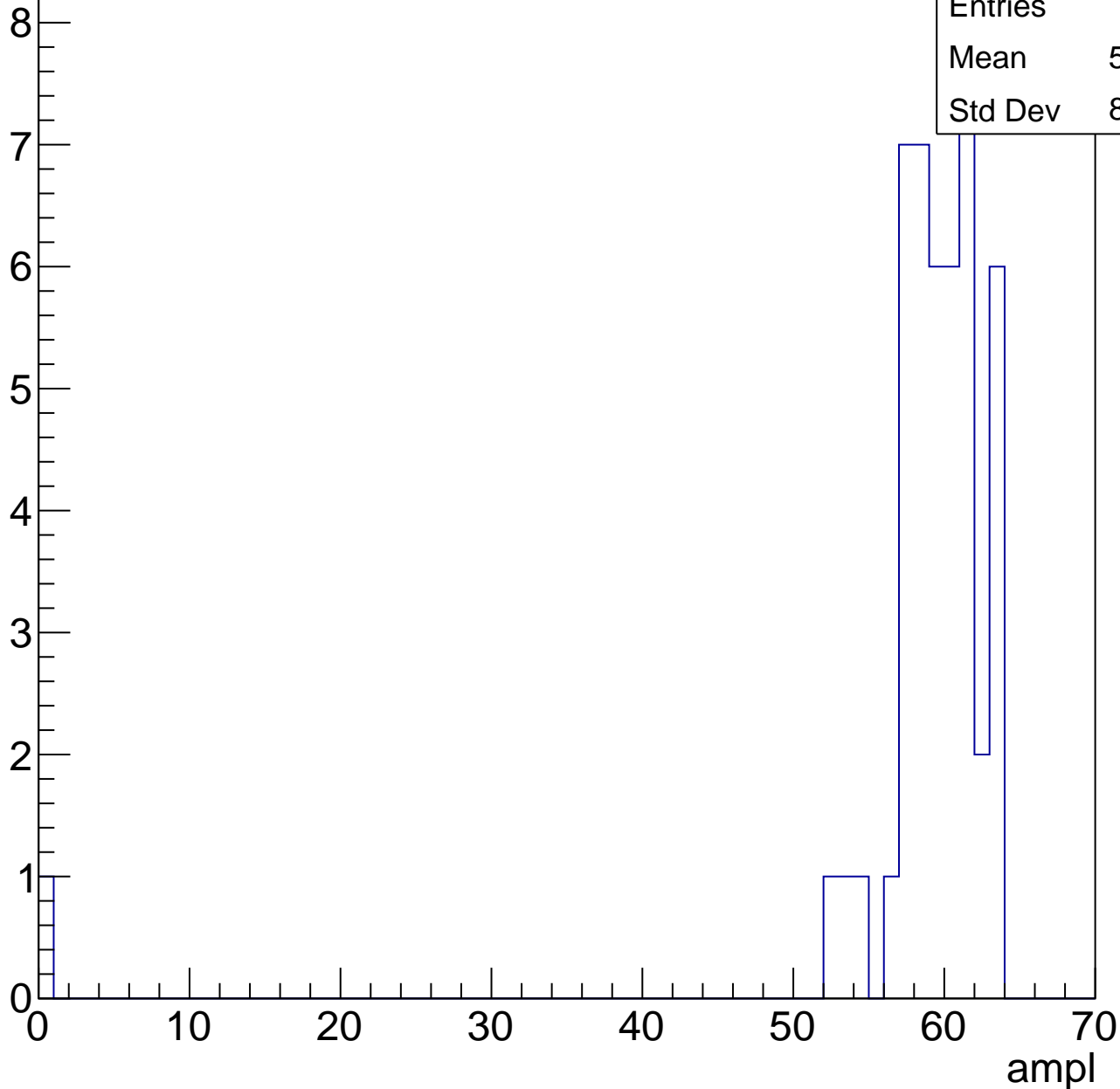


B1L102S, U17-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.96
Std Dev	8.916

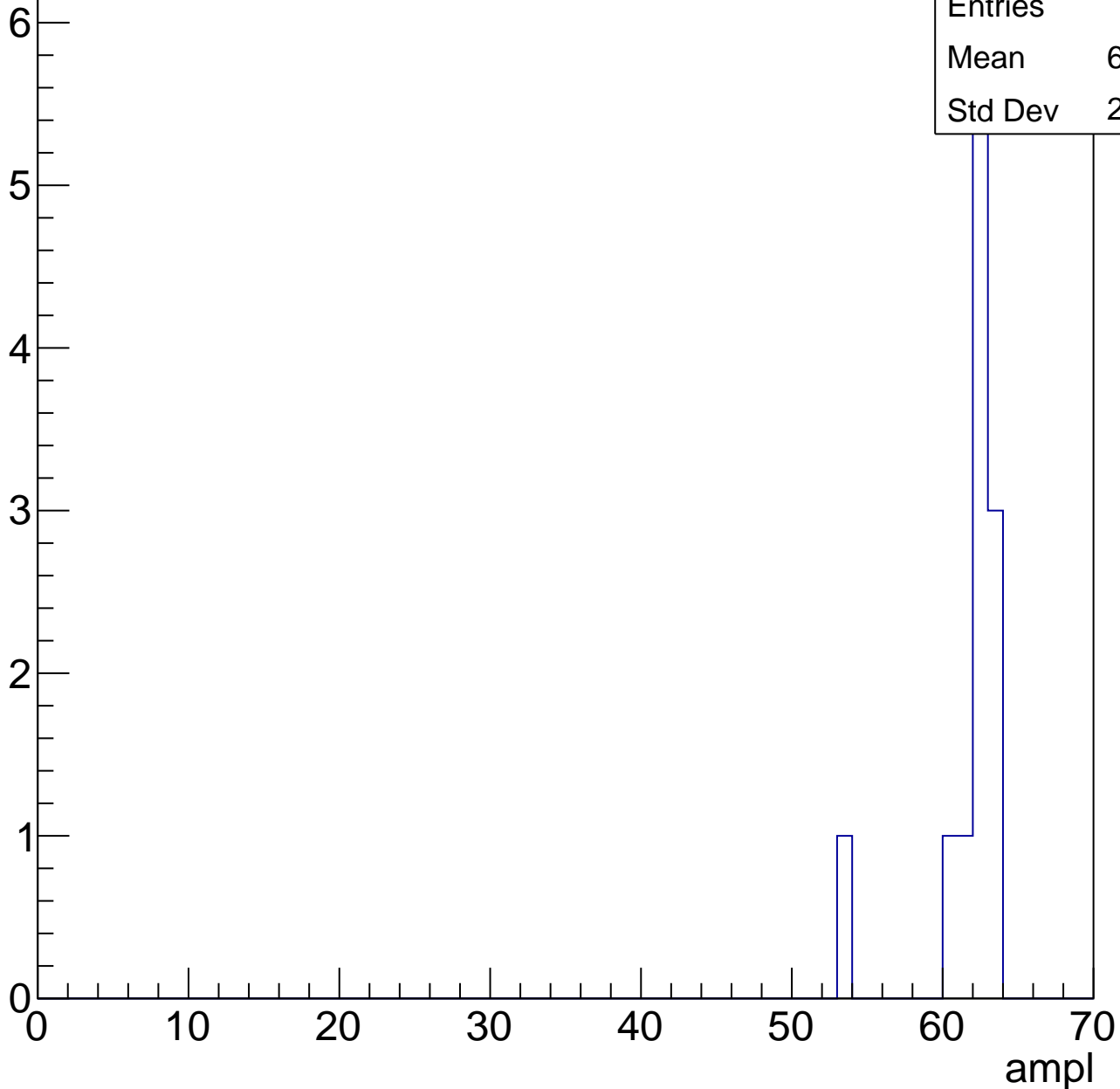


B1L102S, U17-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.25
Std Dev	2.618



B1L102S, U17-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch76, adc0

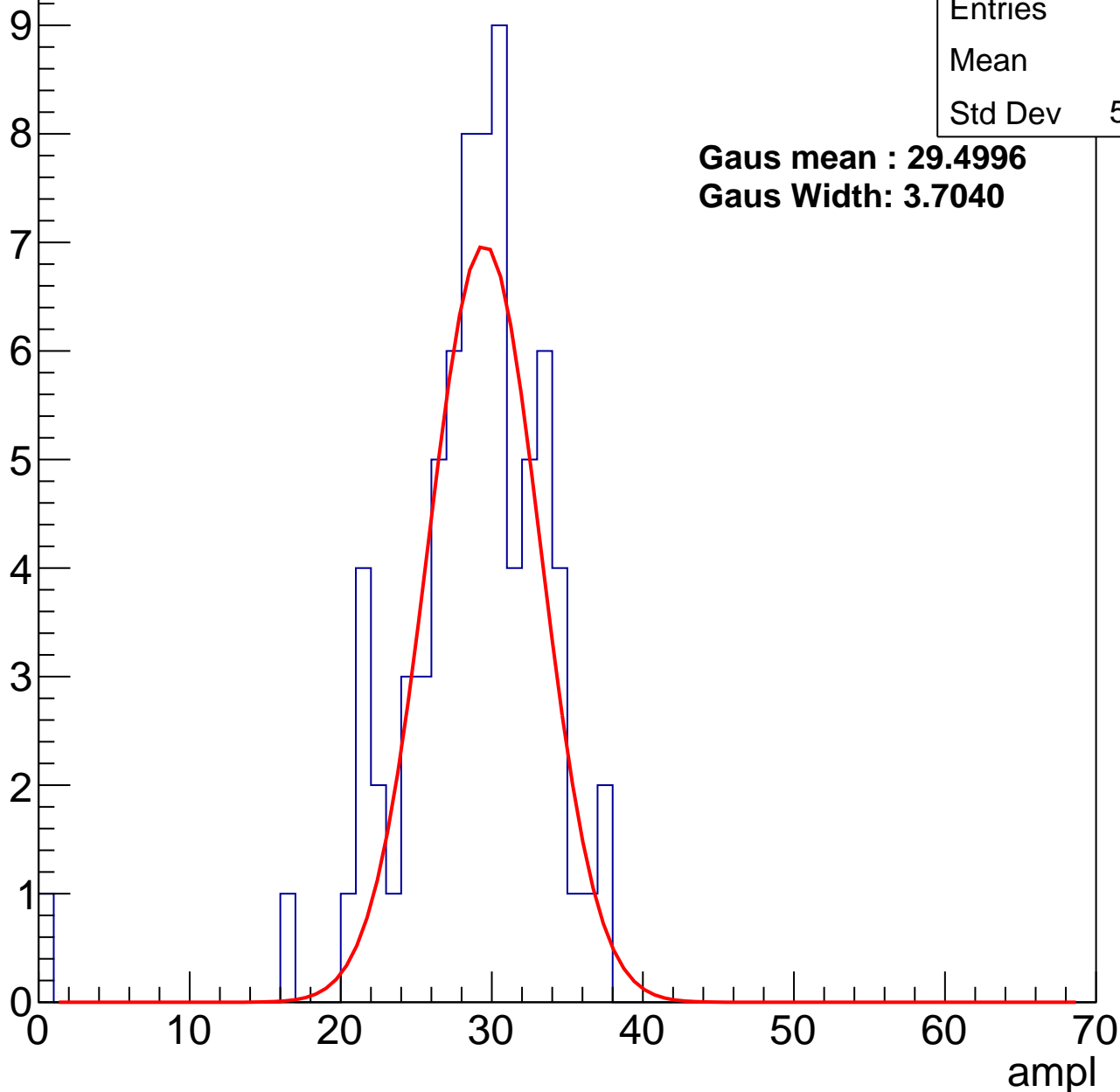
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.2
Std Dev	5.307

Gaus mean : 29.4996

Gaus Width: 3.7040



B1L102S, U17-ch76, adc1

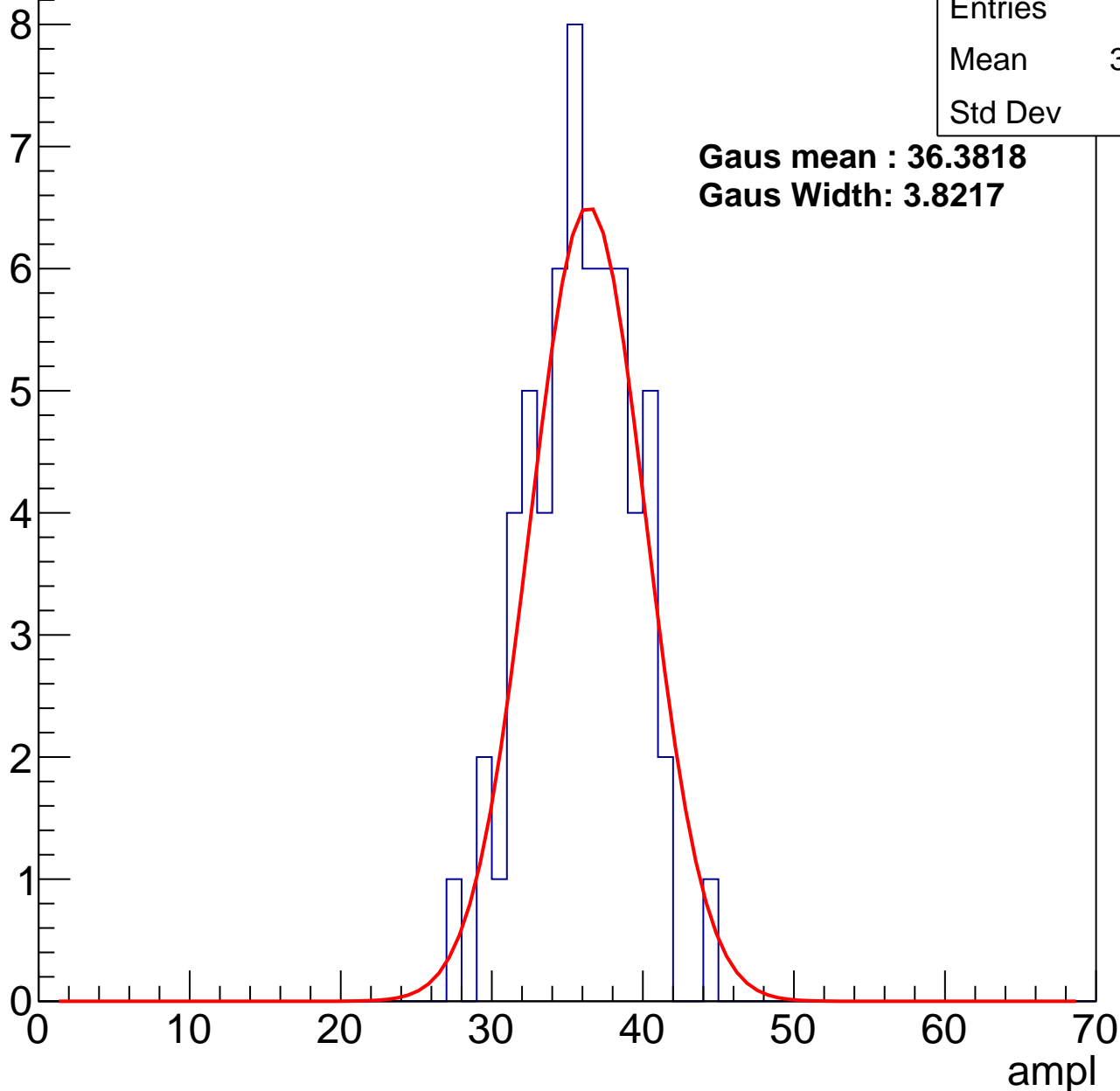
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.46
Std Dev	3.4

Gaus mean : 36.3818

Gaus Width: 3.8217



B1L102S, U17-ch76, adc2

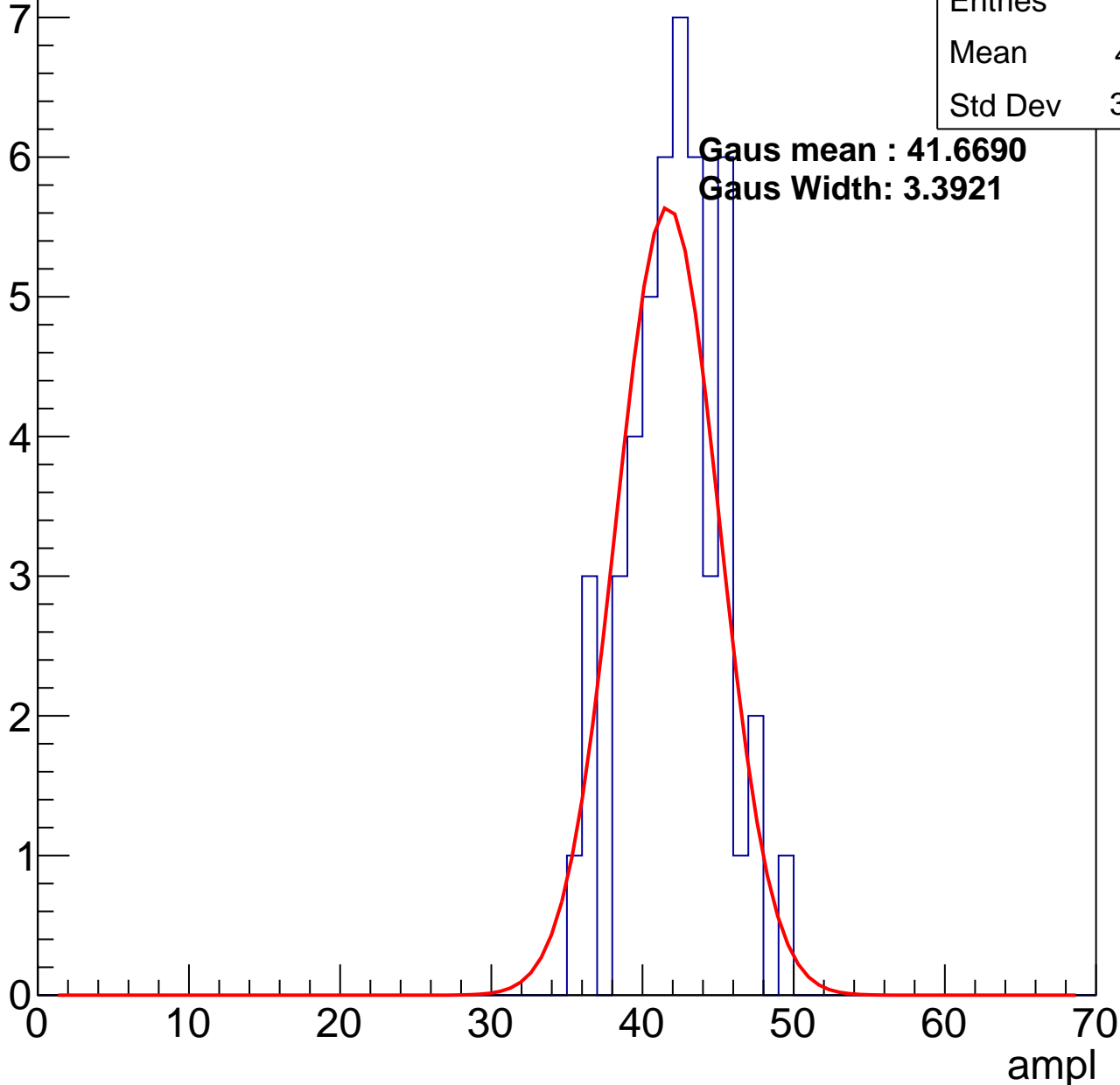
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	41.71
Std Dev	3.062

Gaus mean : 41.6690

Gaus Width: 3.3921

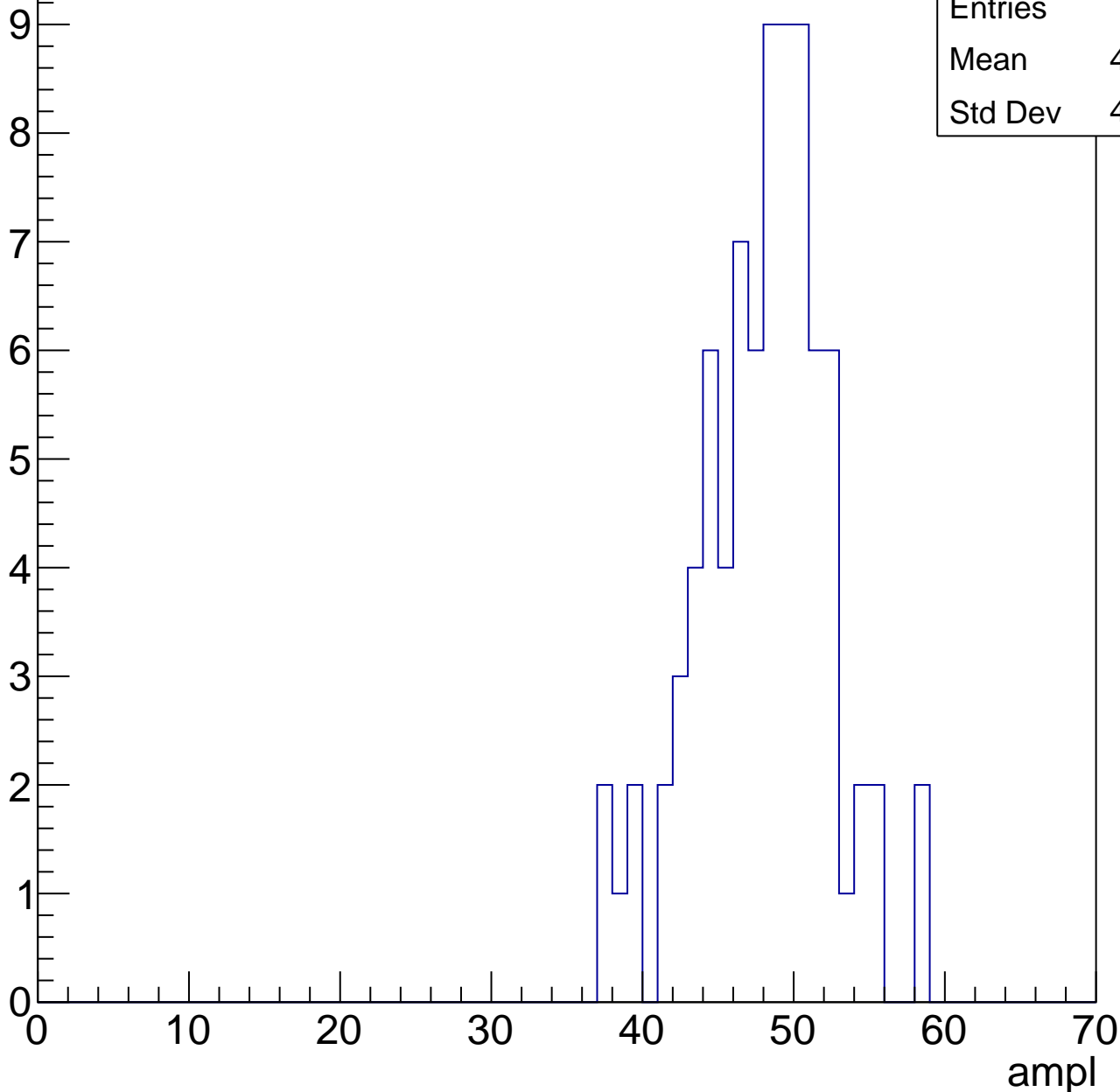


B1L102S, U17-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	47.54
Std Dev	4.319

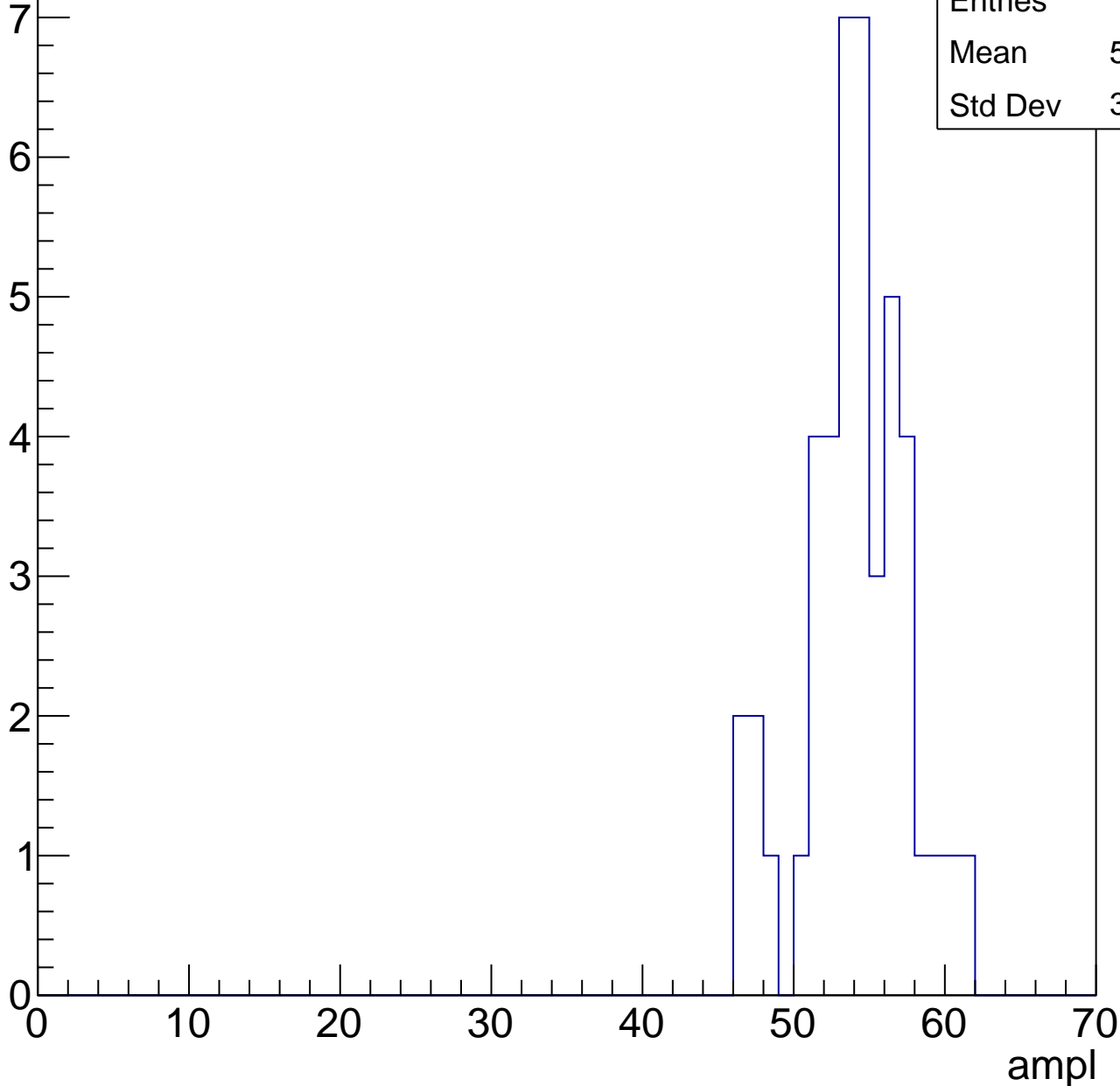


B1L102S, U17-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

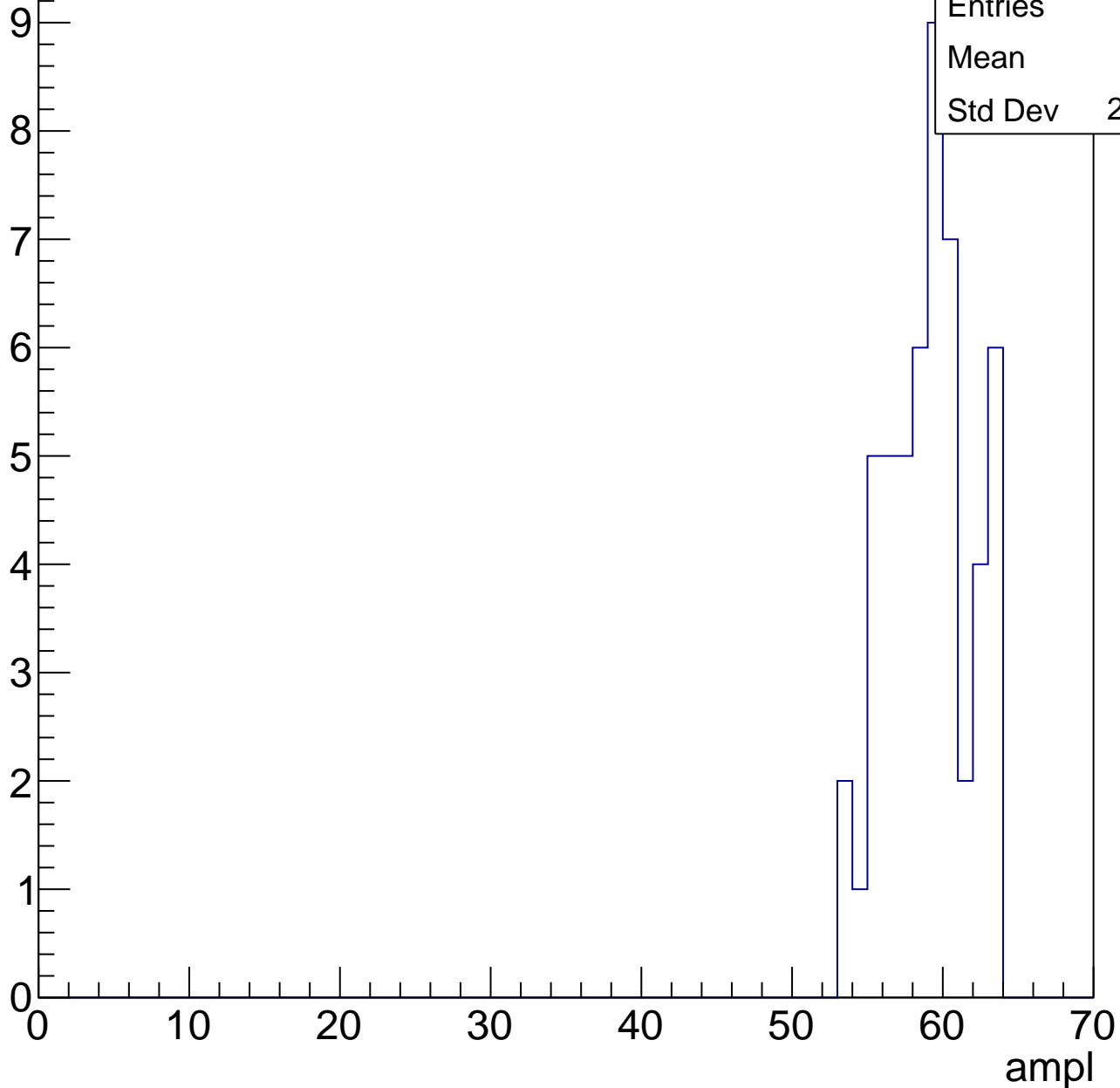
Entries	44
Mean	53.55
Std Dev	3.414



B1L102S, U17-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

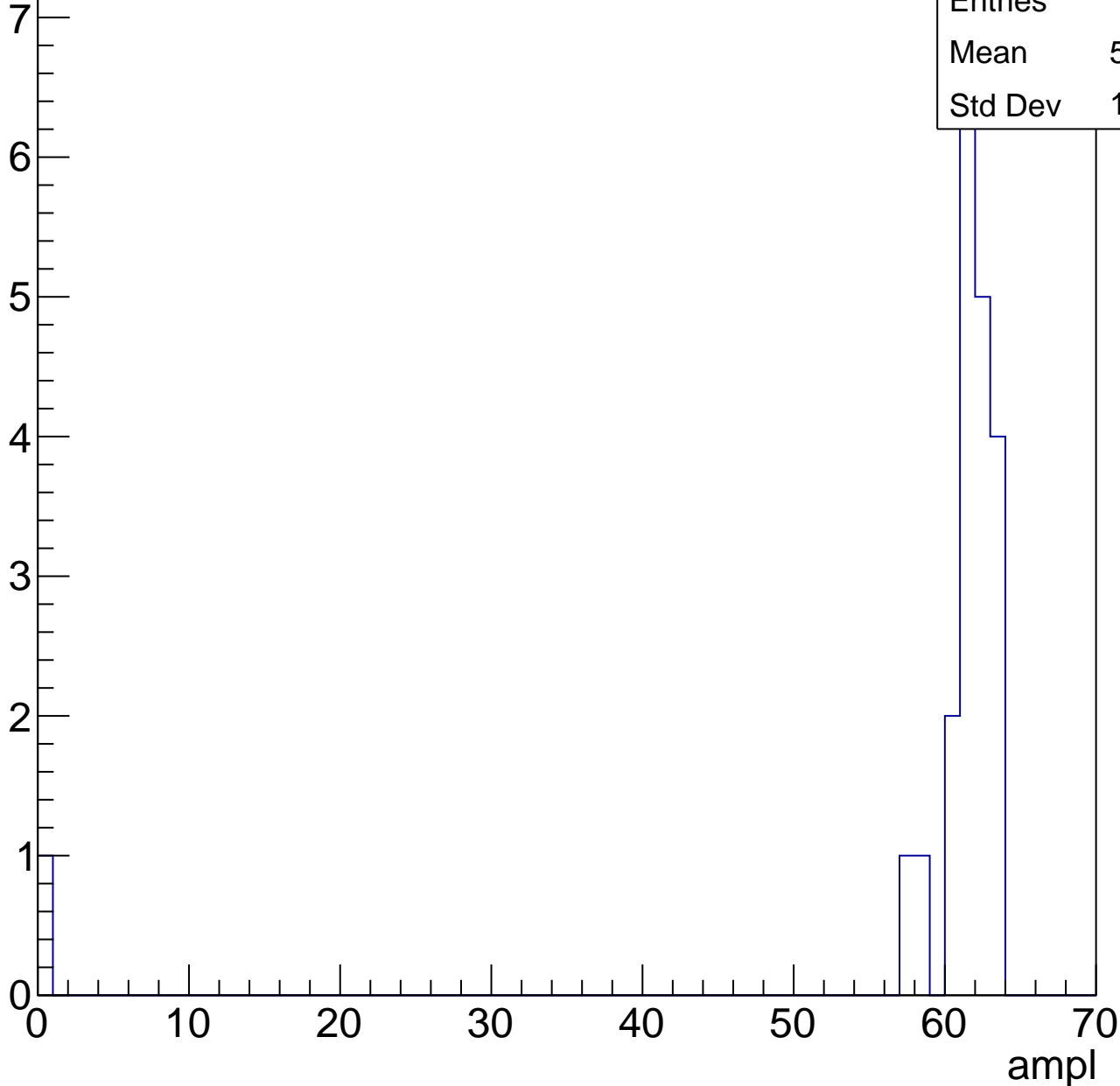


B1L102S, U17-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

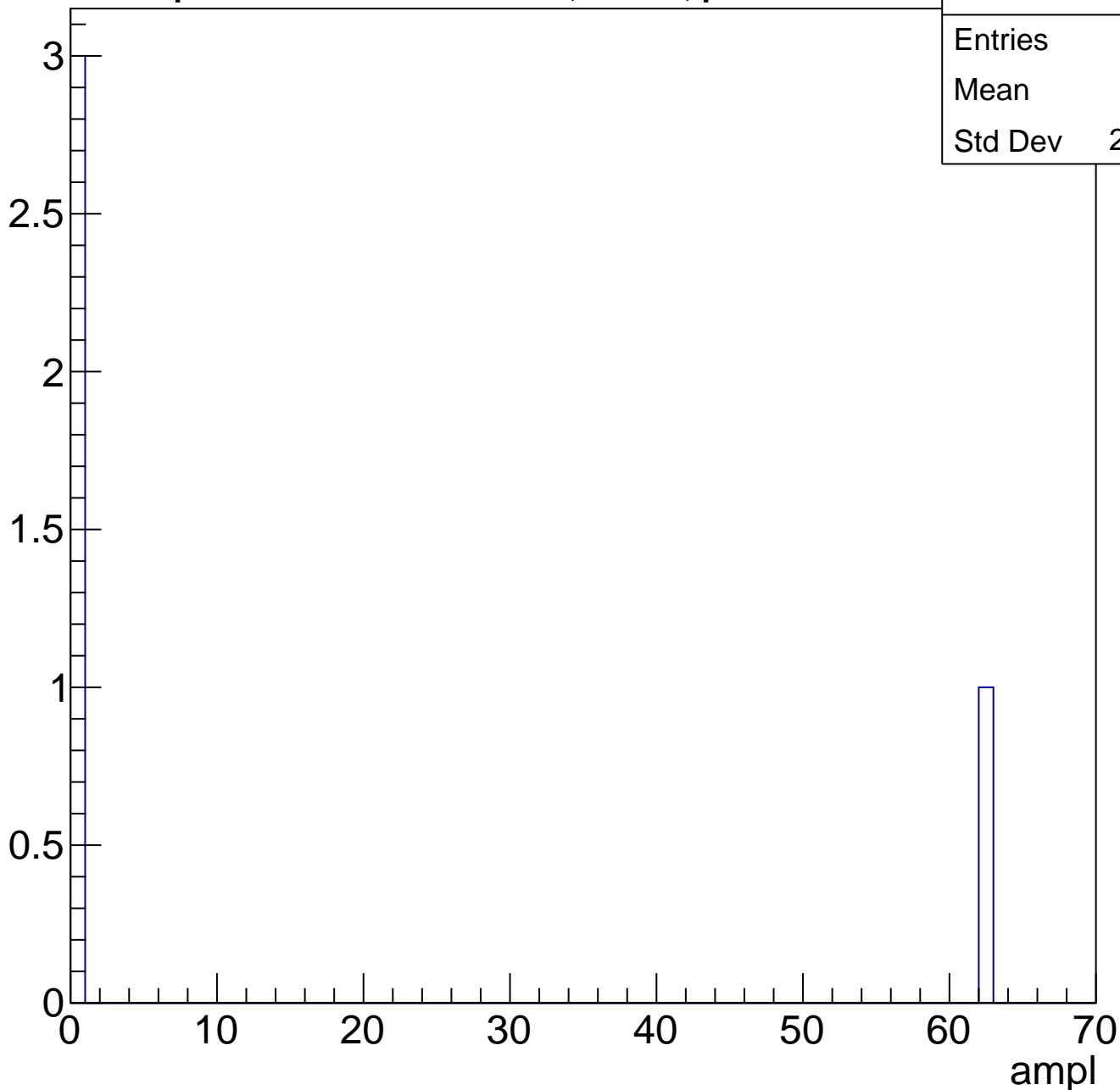
Entries	21
Mean	58.29
Std Dev	13.12



B1L102S, U17-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch77, adc0

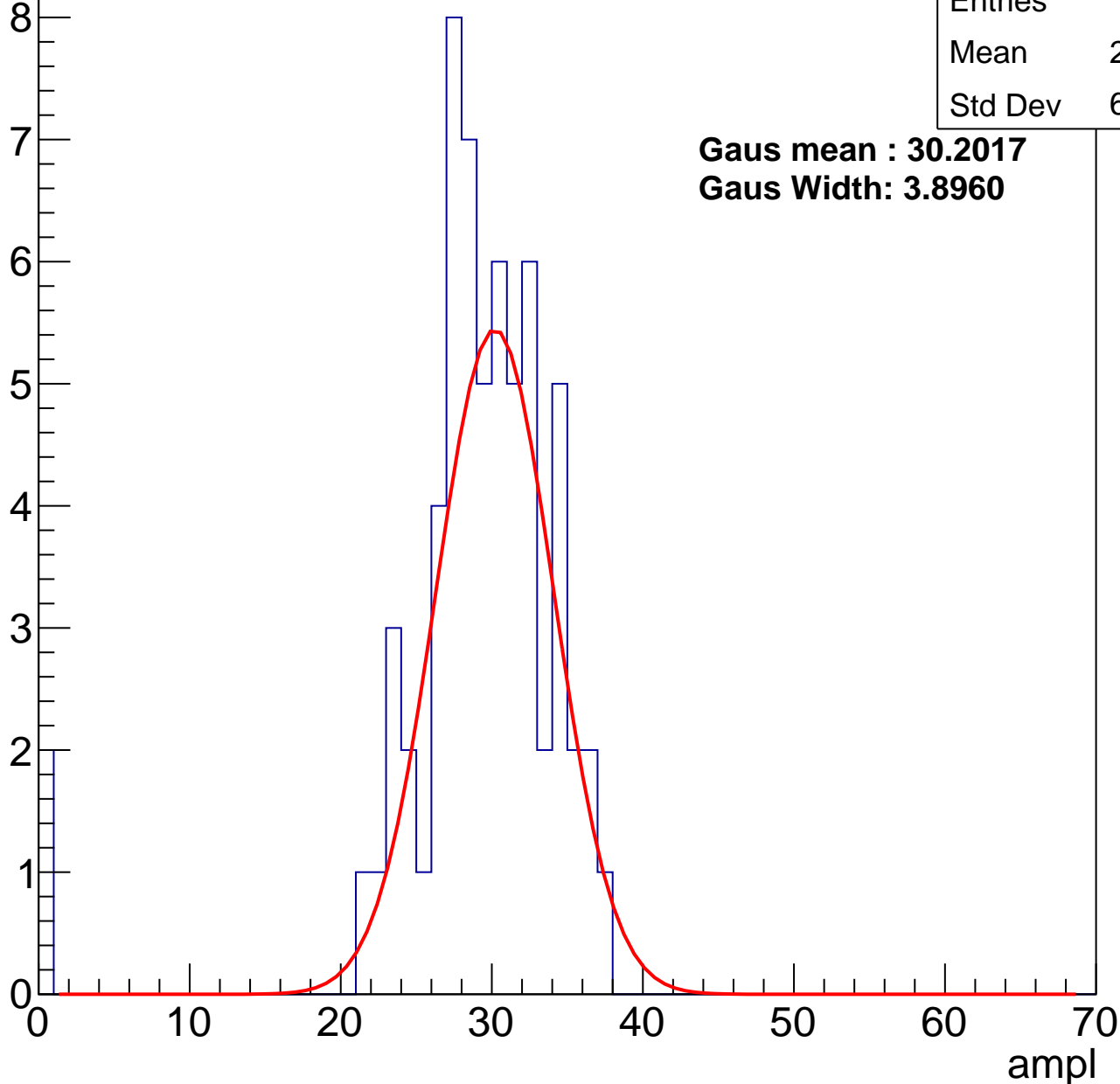
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.38
Std Dev	6.288

Gaus mean : 30.2017

Gaus Width: 3.8960



B1L102S, U17-ch77, adc1

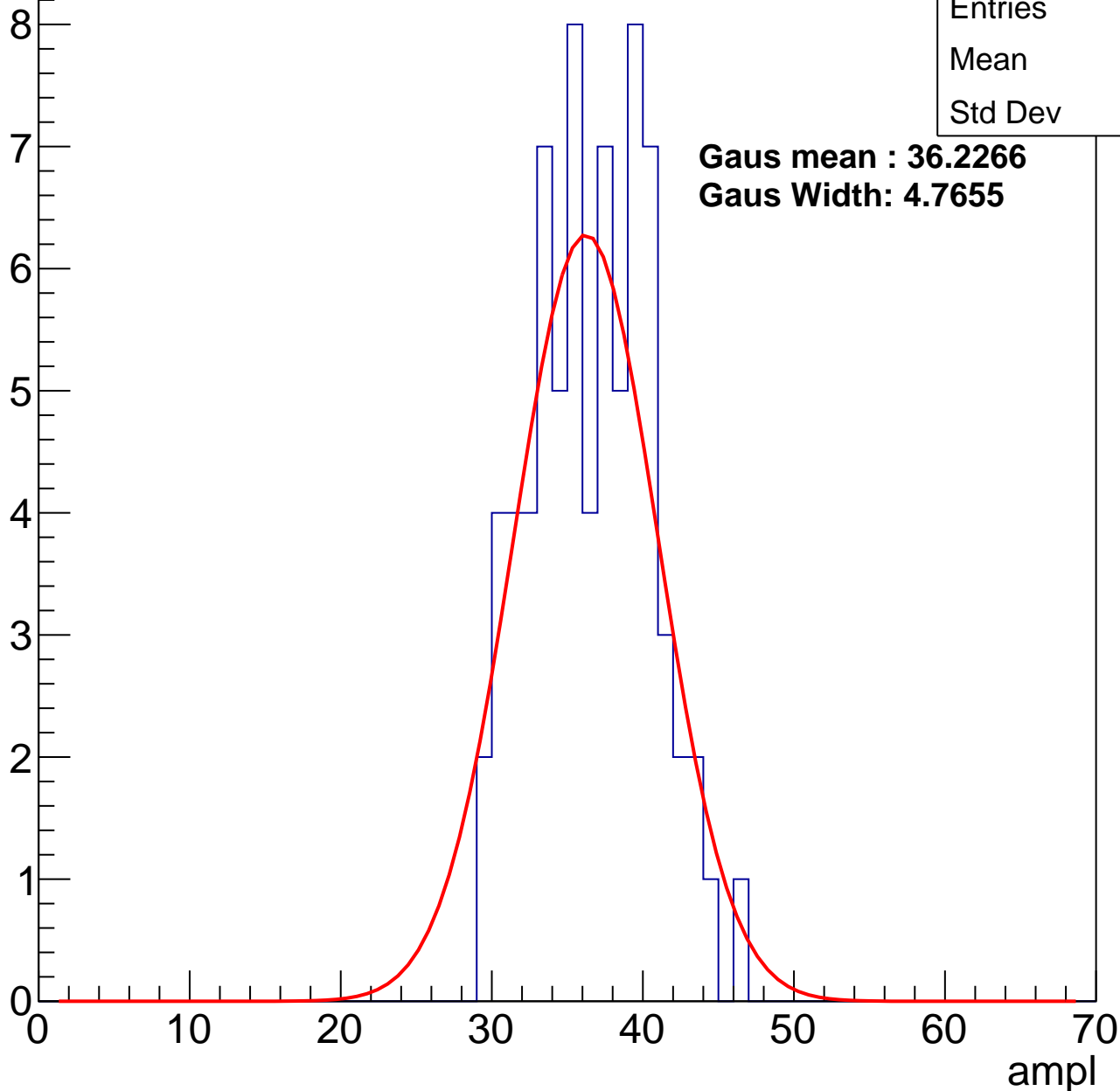
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.2
Std Dev	3.88

Gaus mean : 36.2266

Gaus Width: 4.7655



B1L102S, U17-ch77, adc2

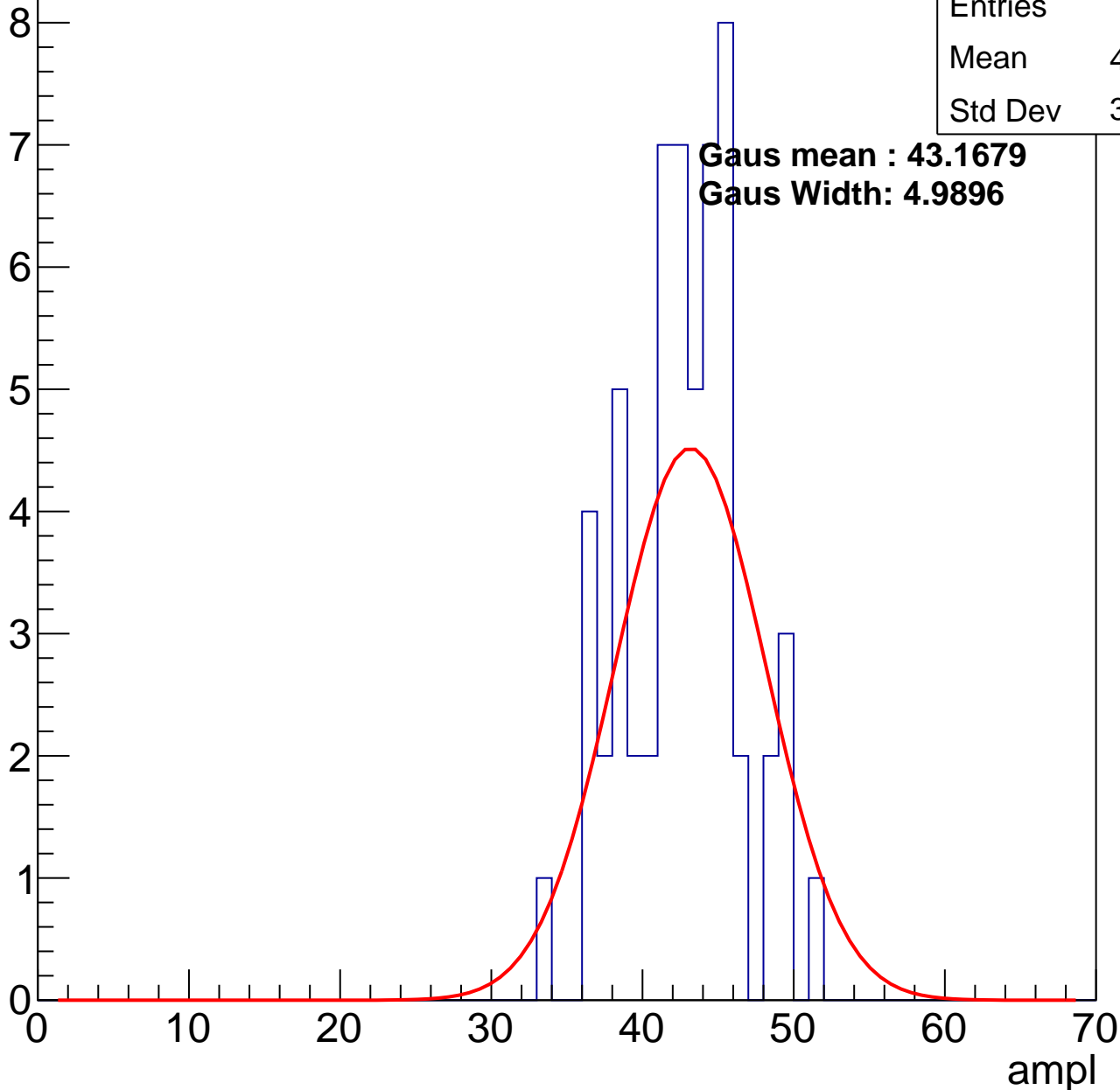
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.22
Std Dev	3.765

Gaus mean : 43.1679

Gaus Width: 4.9896



B1L102S, U17-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

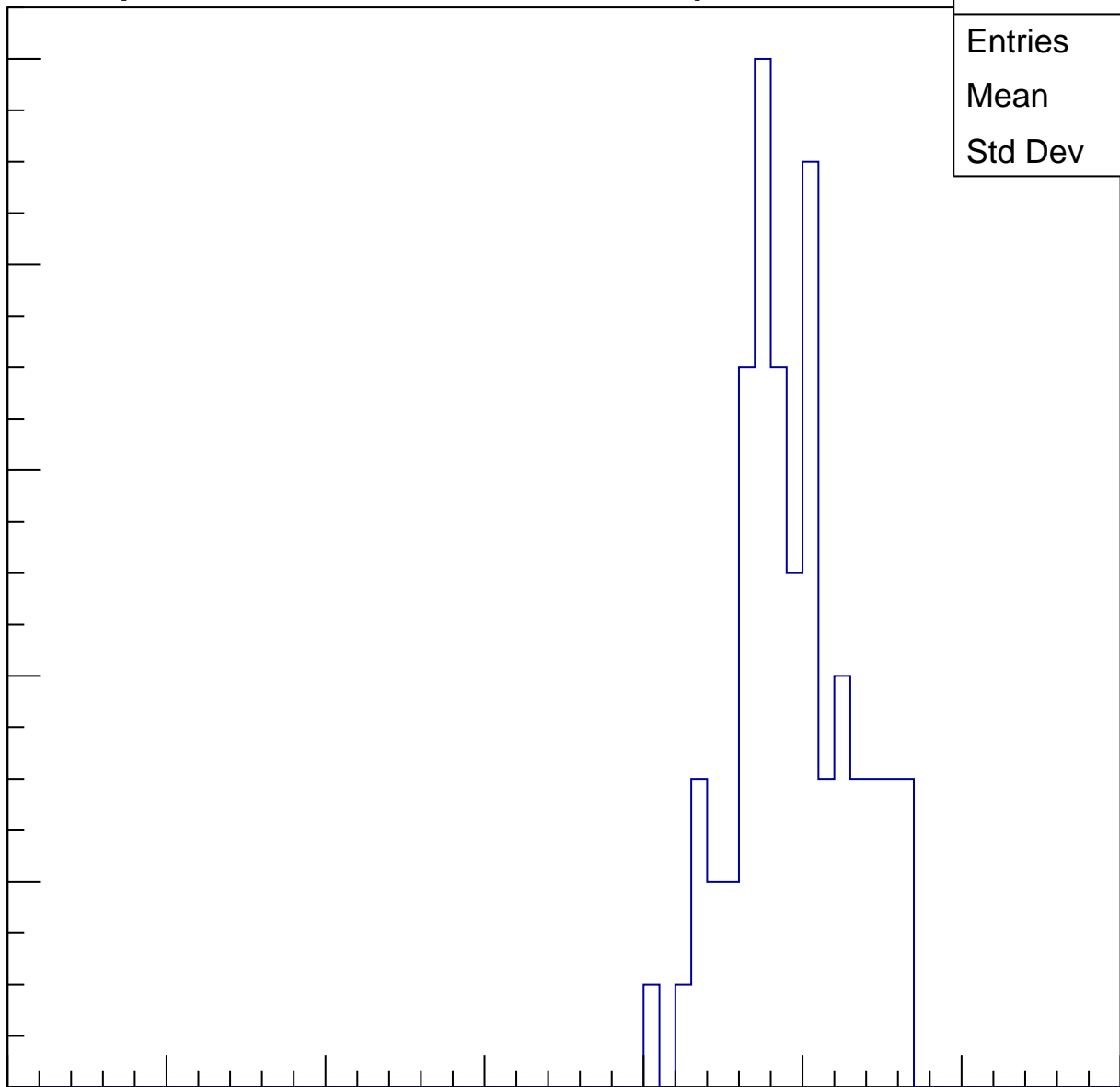
Entries	66
Mean	48.89
Std Dev	3.619

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

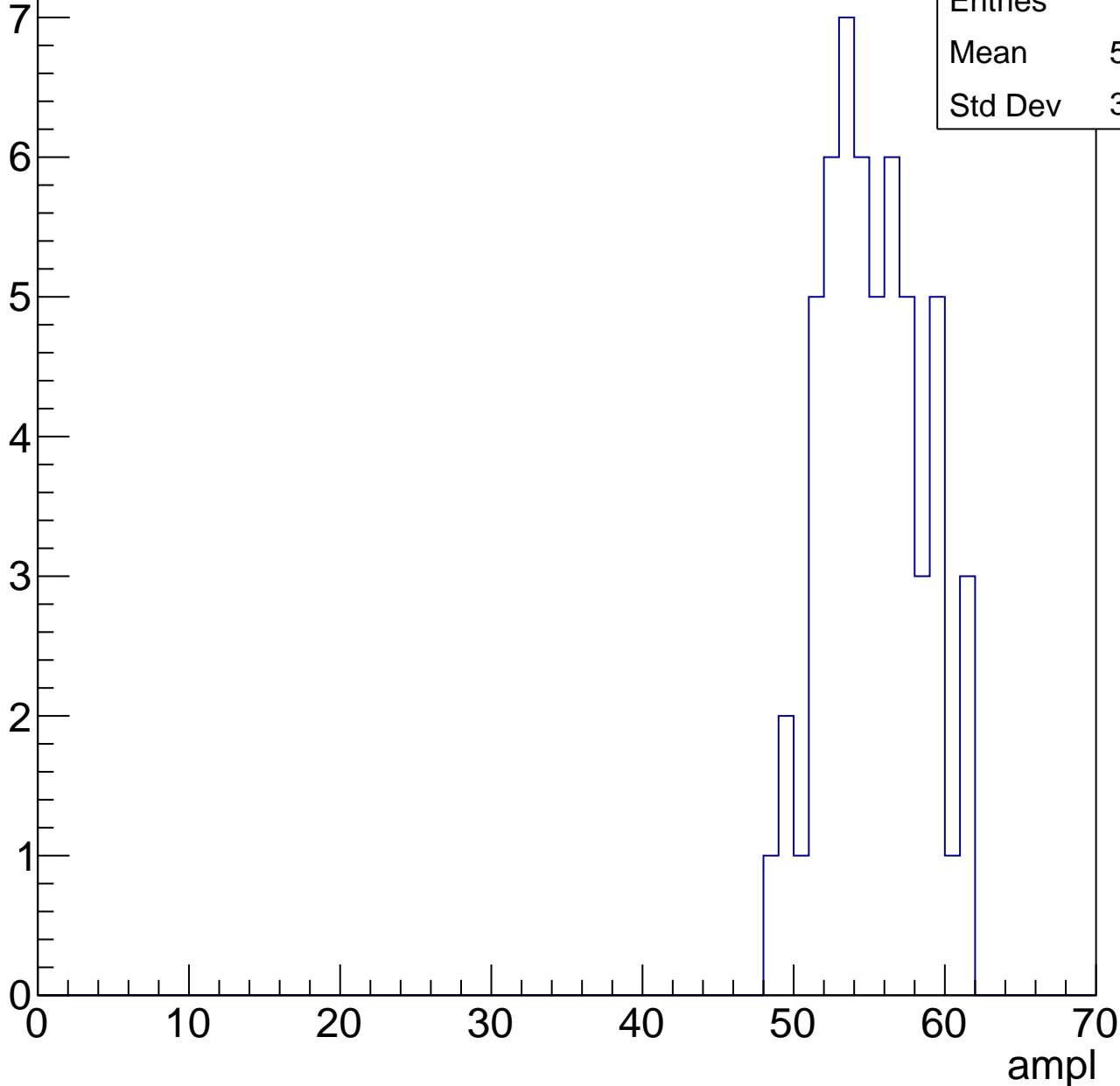


B1L102S, U17-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.75
Std Dev	3.209

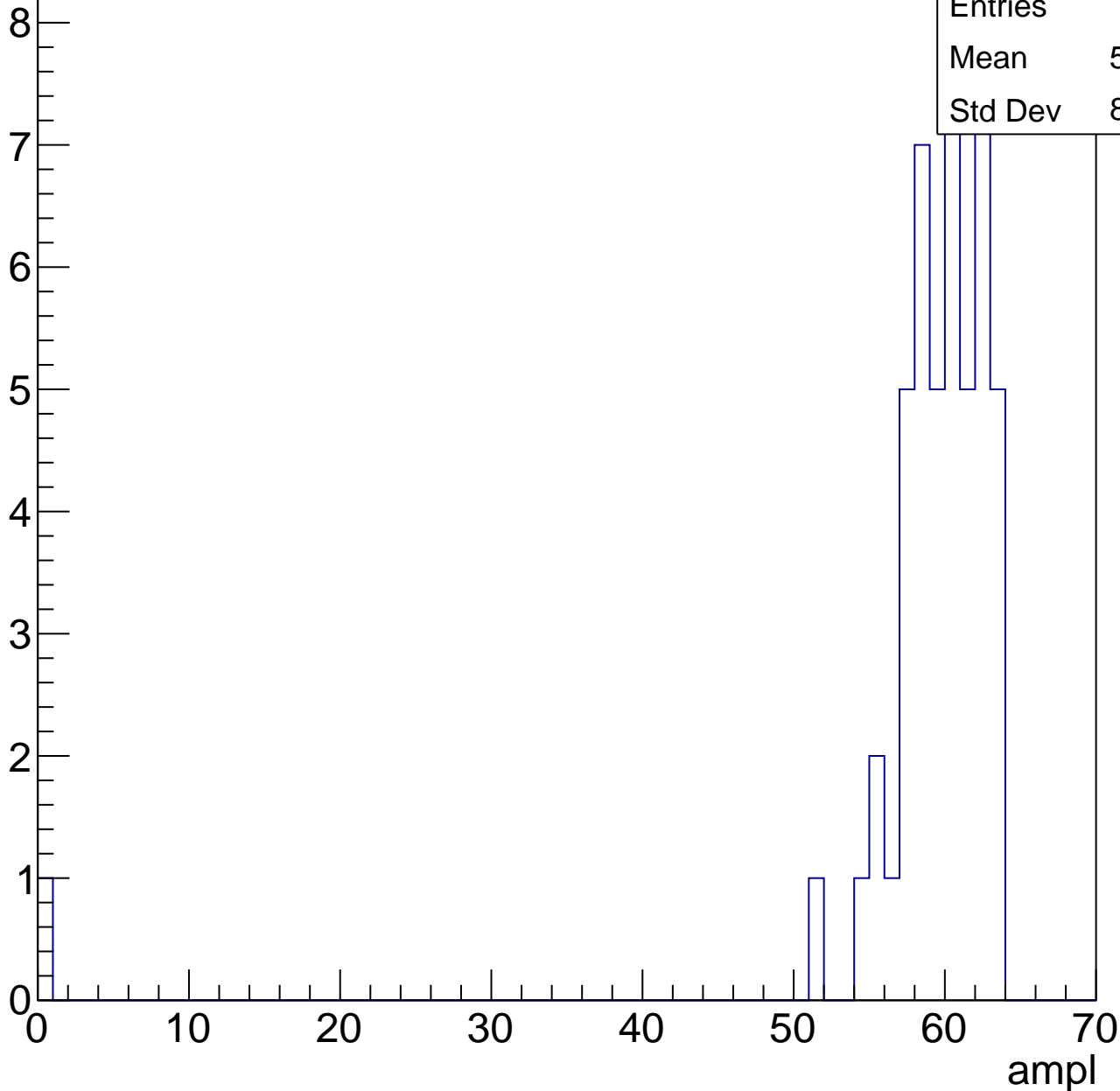


B1L102S, U17-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.22
Std Dev	8.793

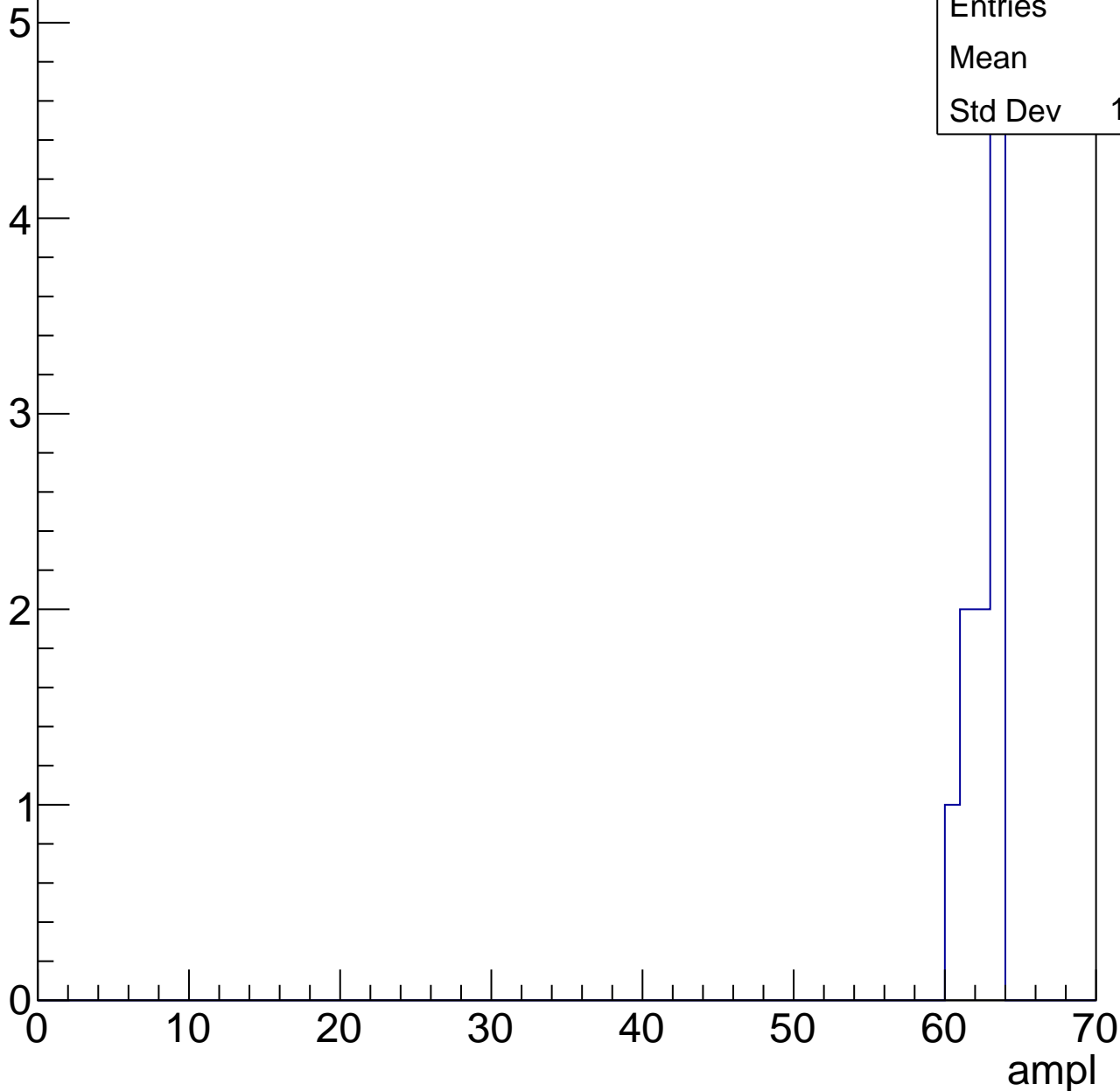


B1L102S, U17-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

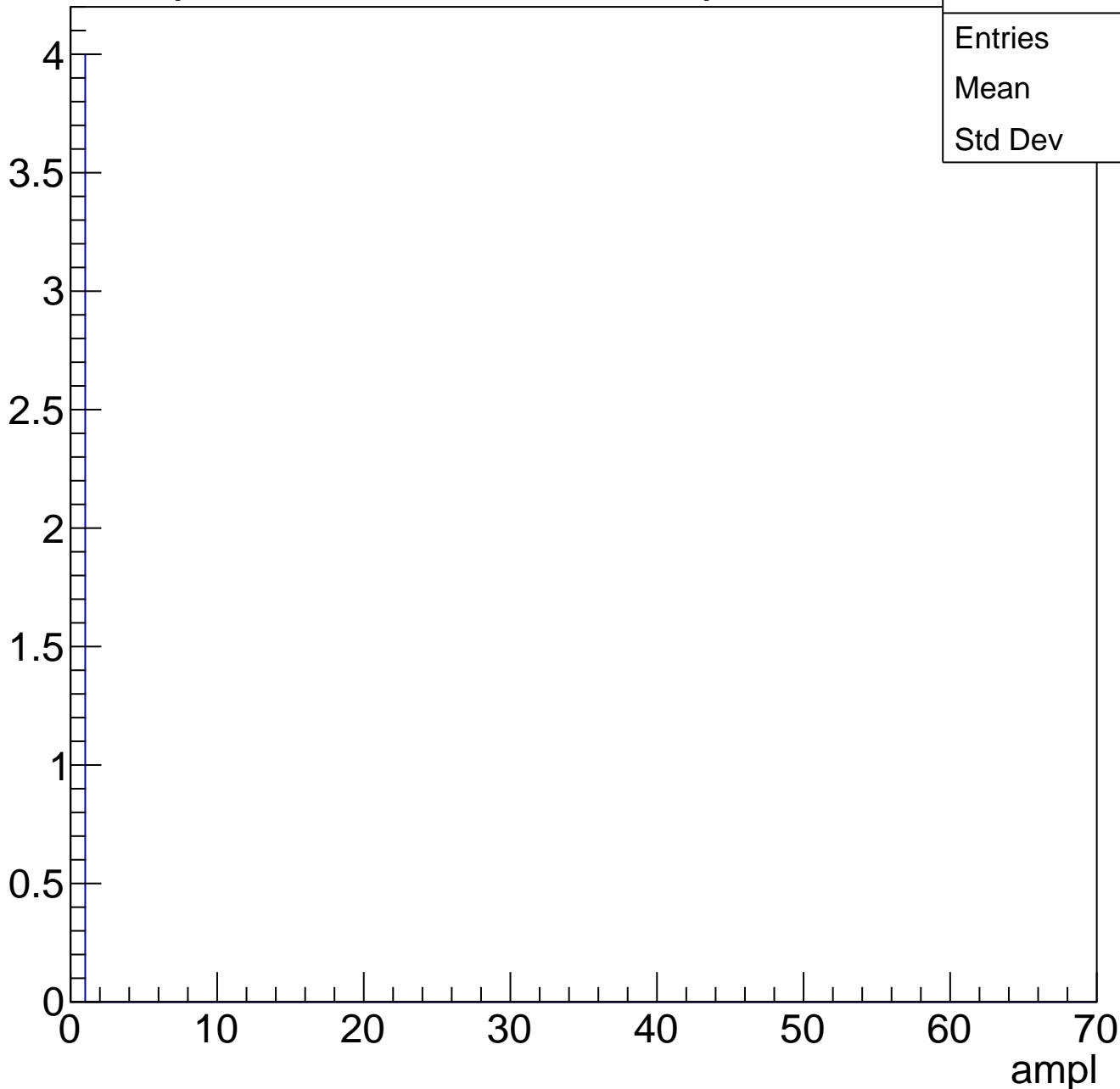
Entries	10
Mean	62.1
Std Dev	1.044



B1L102S, U17-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L102S, U17-ch78, adc0

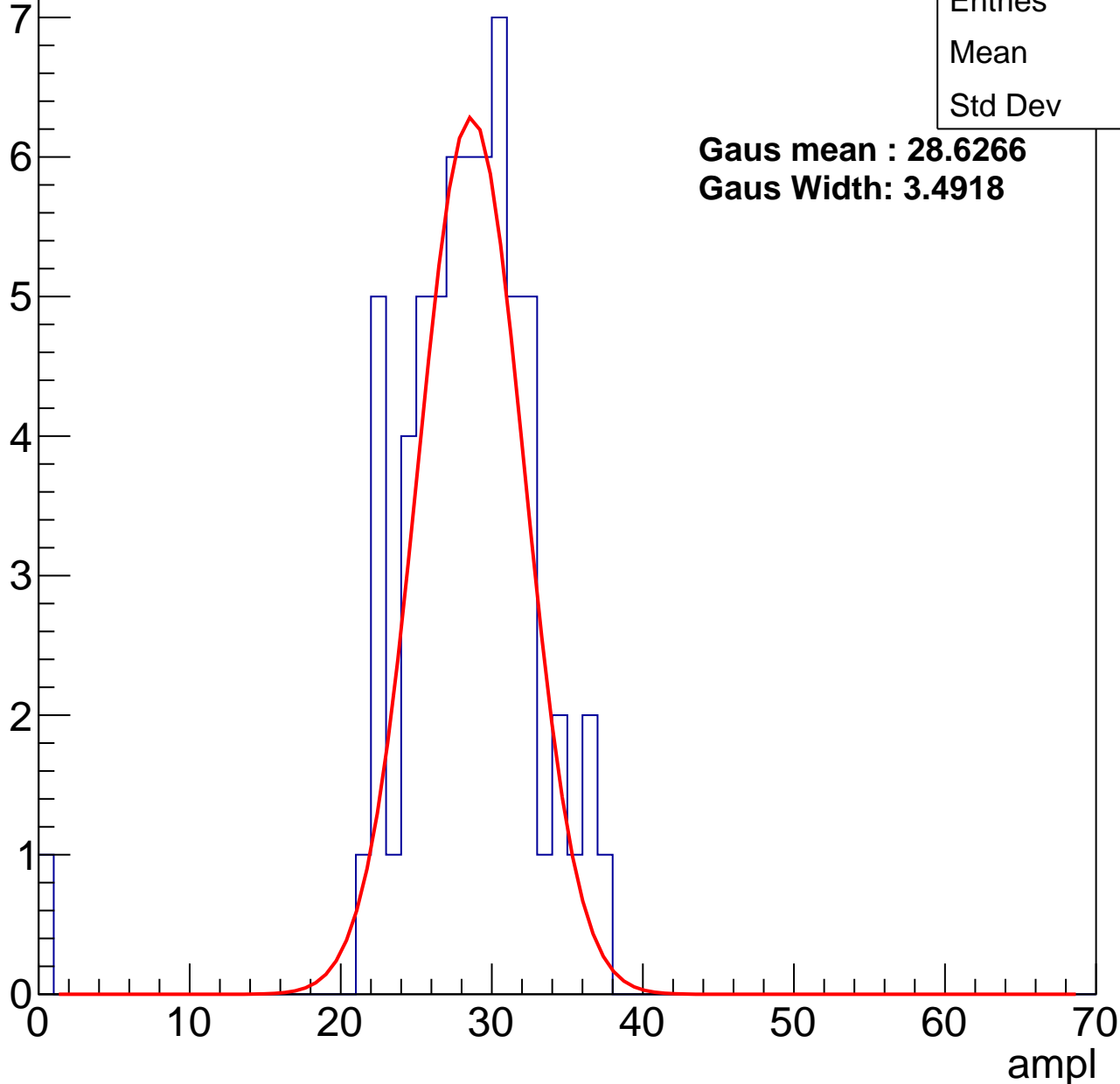
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.8
Std Dev	5.13

Gaus mean : 28.6266

Gaus Width: 3.4918



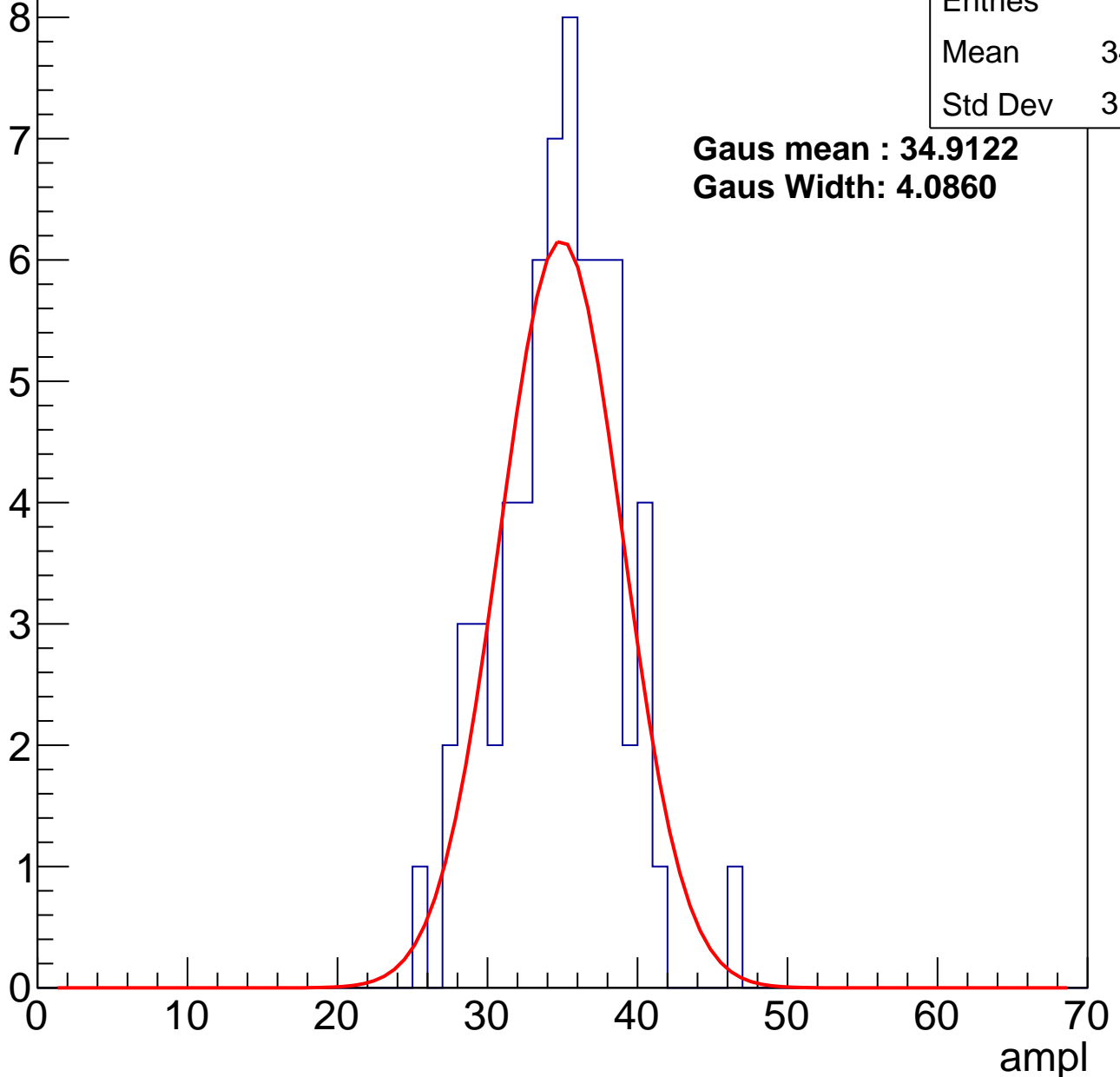
B1L102S, U17-ch78, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.38
Std Dev	3.915

Gaus mean : 34.9122
Gaus Width: 4.0860



B1L102S, U17-ch78, adc2

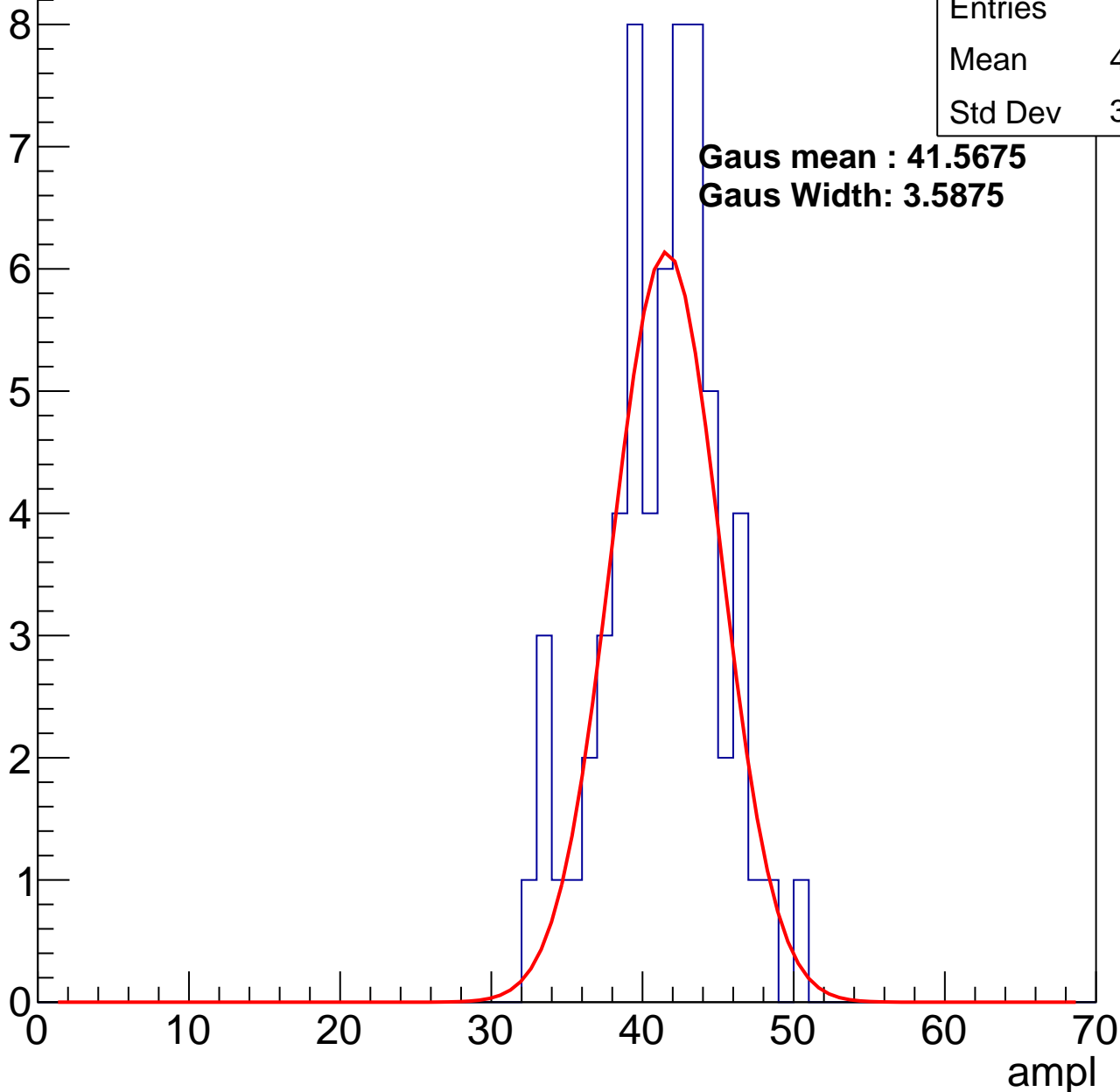
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	40.83
Std Dev	3.799

Gaus mean : 41.5675

Gaus Width: 3.5875

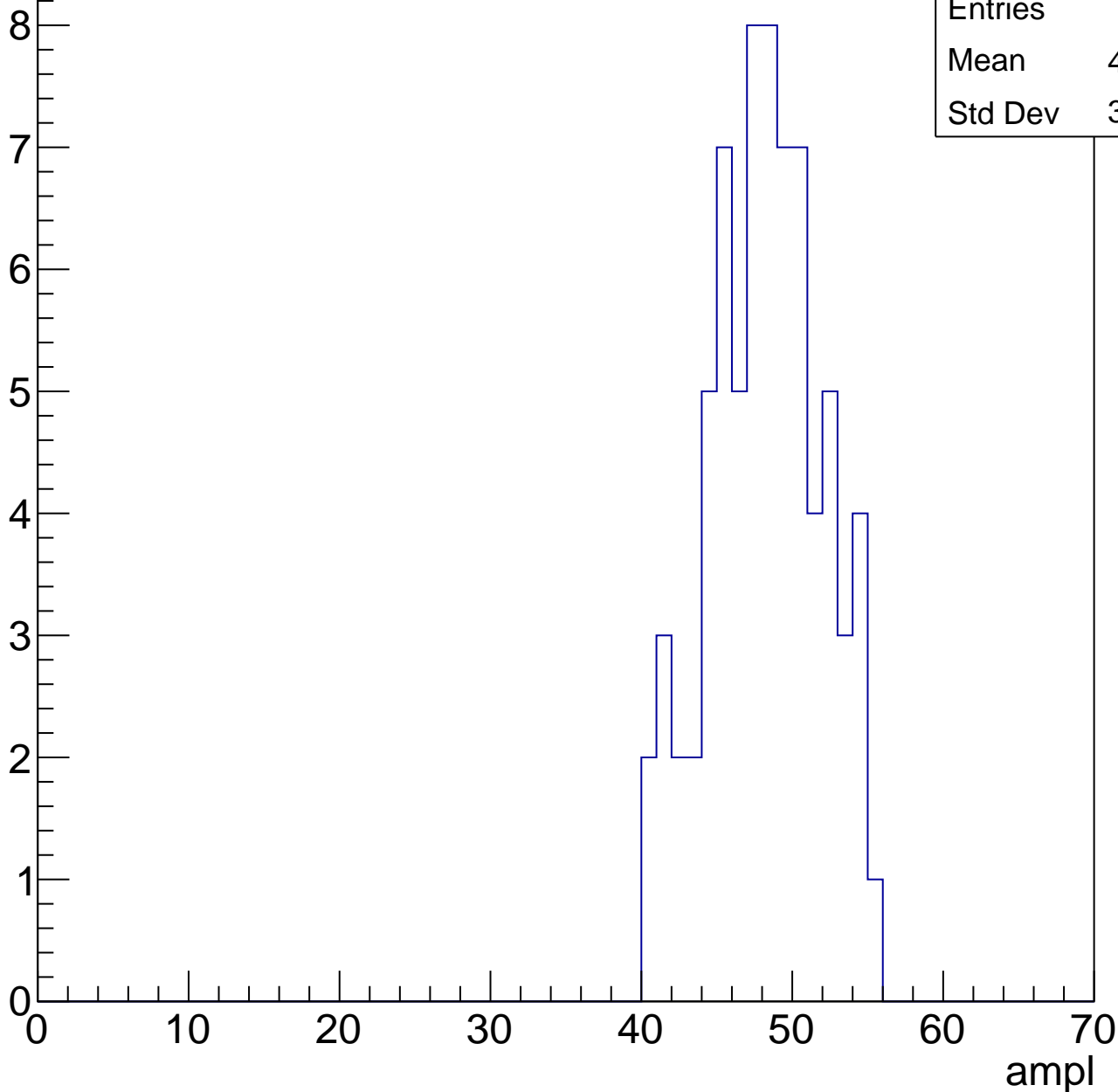


B1L102S, U17-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	47.74
Std Dev	3.668

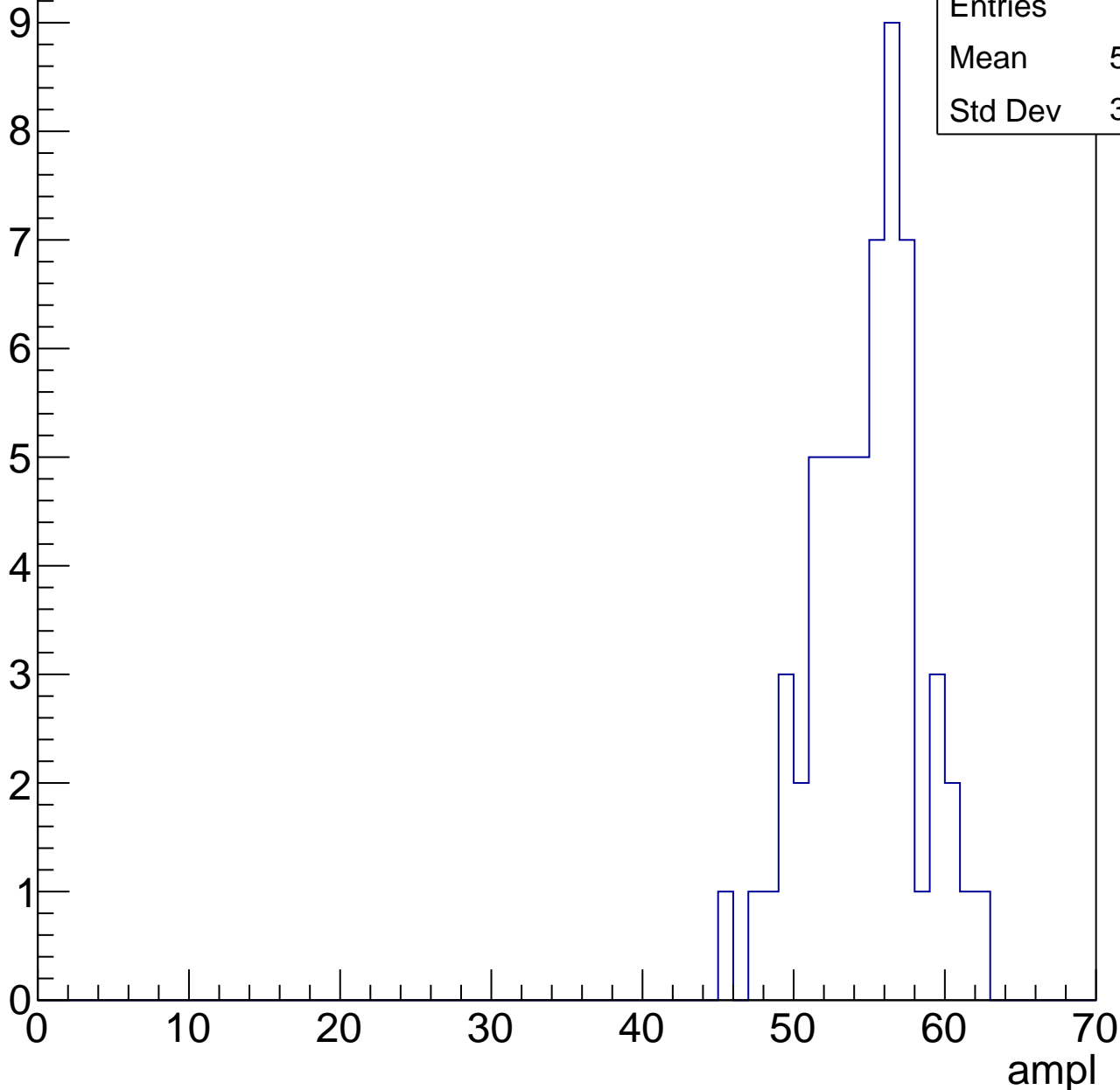


B1L102S, U17-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.29
Std Dev	3.494

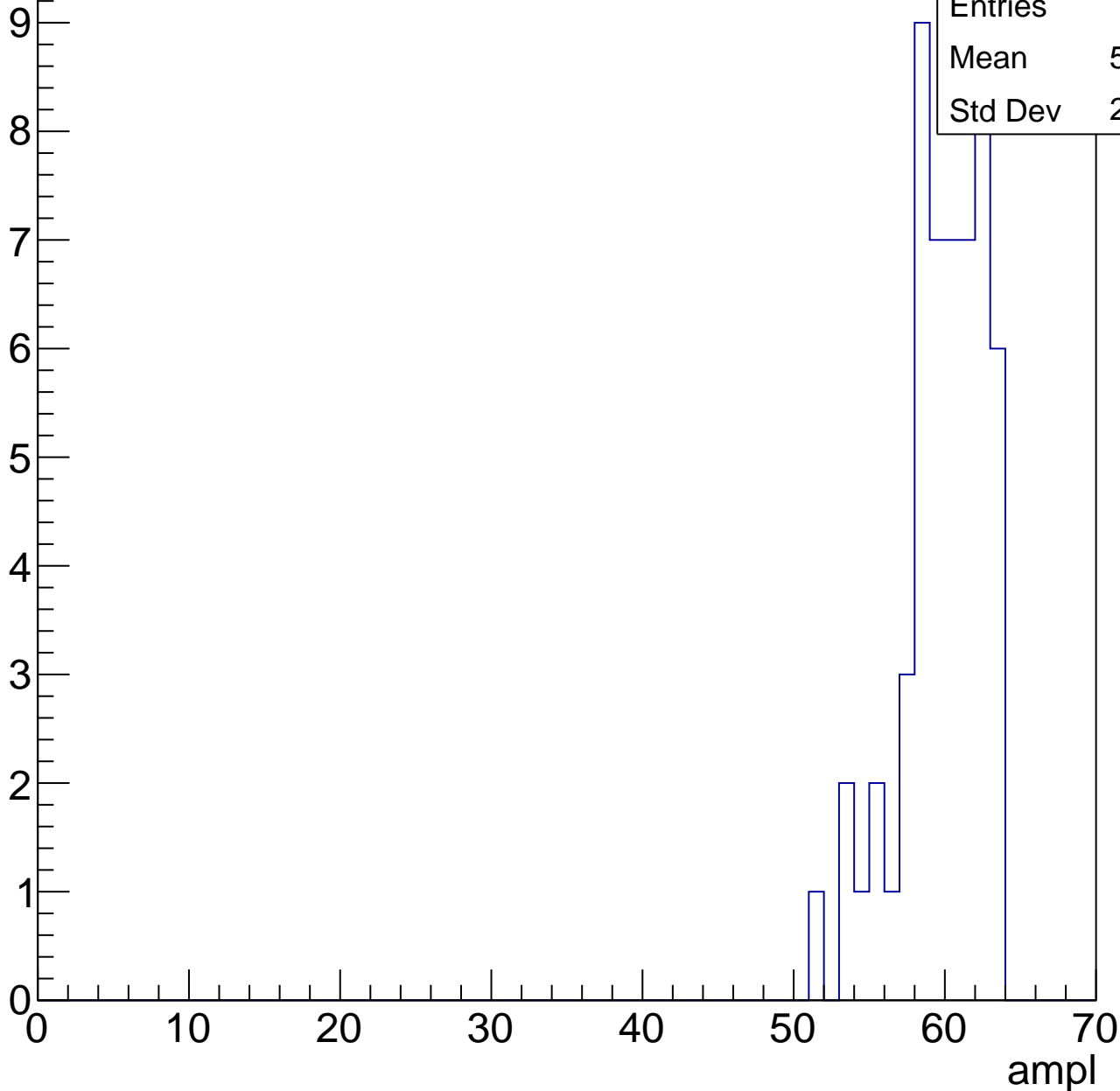


B1L102S, U17-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	59.38
Std Dev	2.786



B1L102S, U17-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

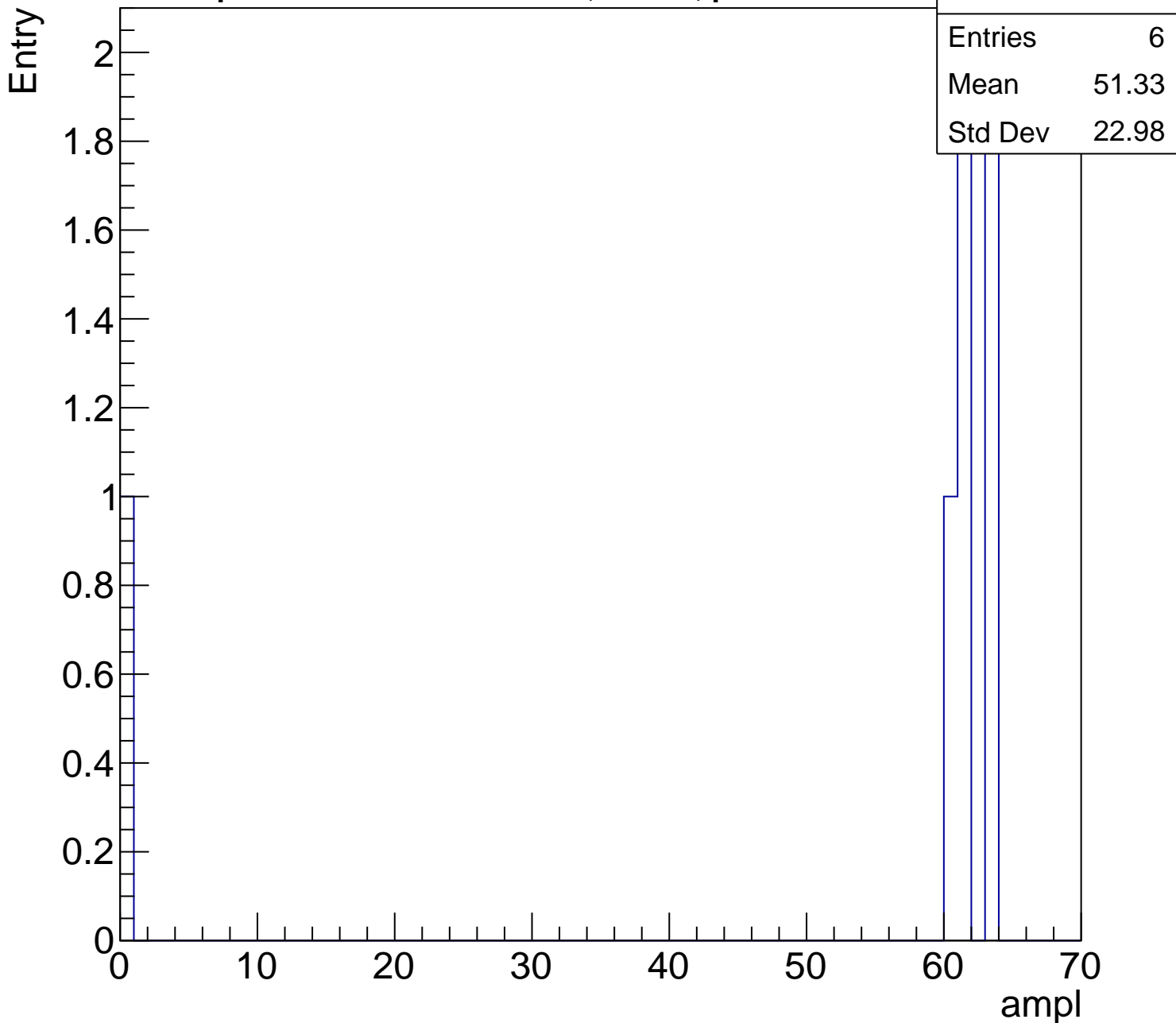
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.33
Std Dev	22.98

0 10 20 30 40 50 60 70

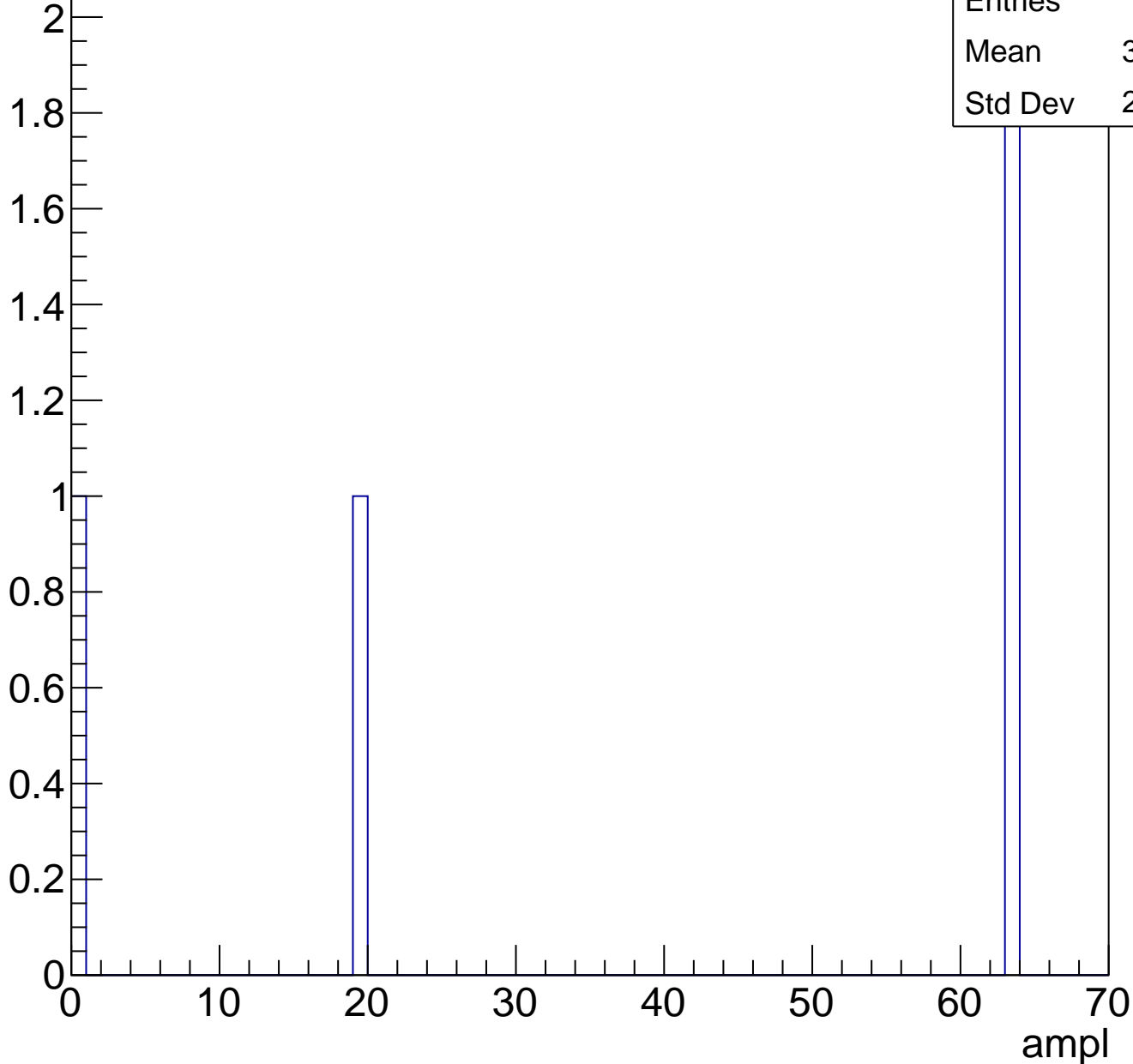
ampl



B1L102S, U17-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch79, adc0

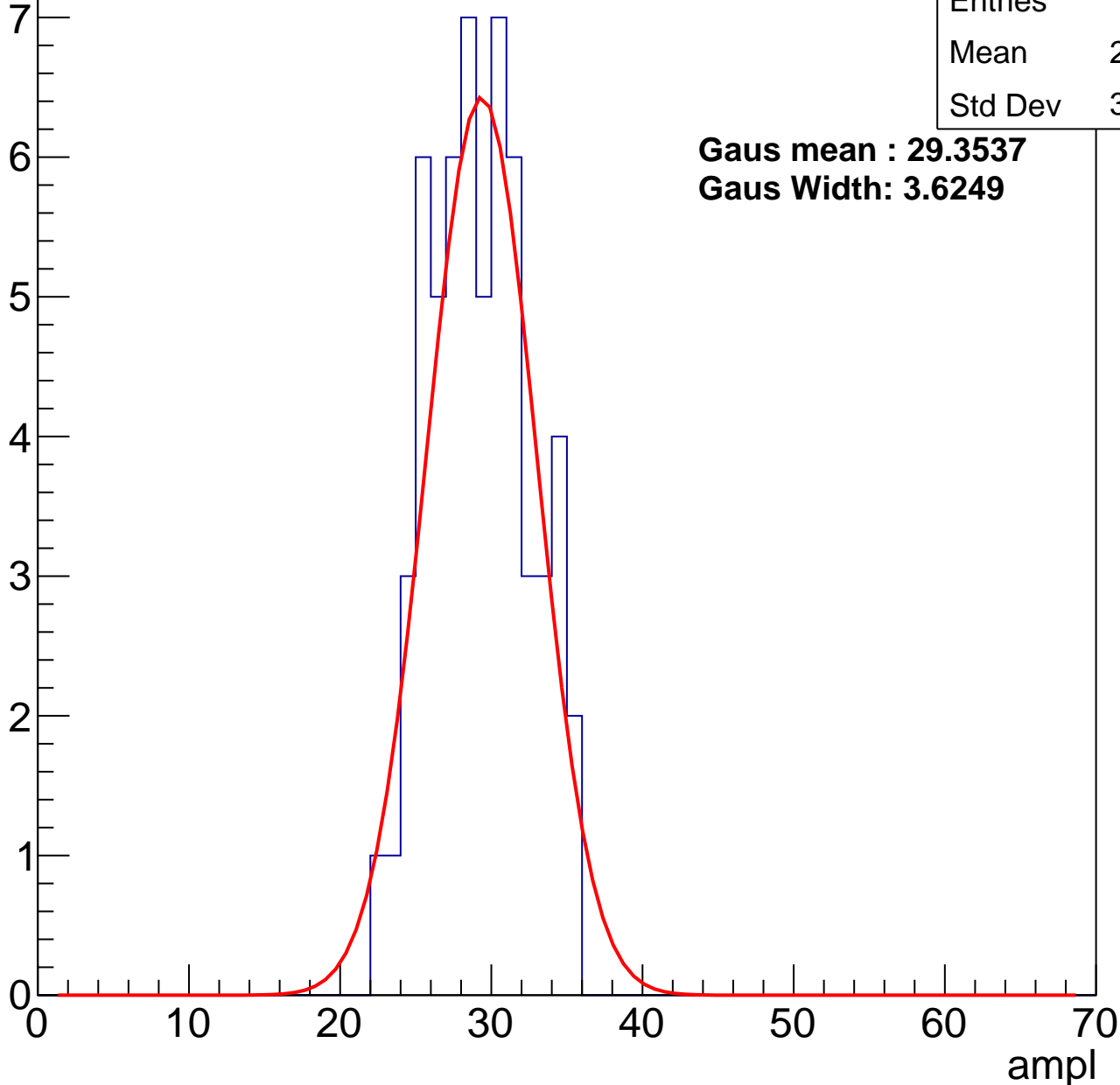
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.76
Std Dev	3.207

Gaus mean : 29.3537

Gaus Width: 3.6249



B1L102S, U17-ch79, adc1

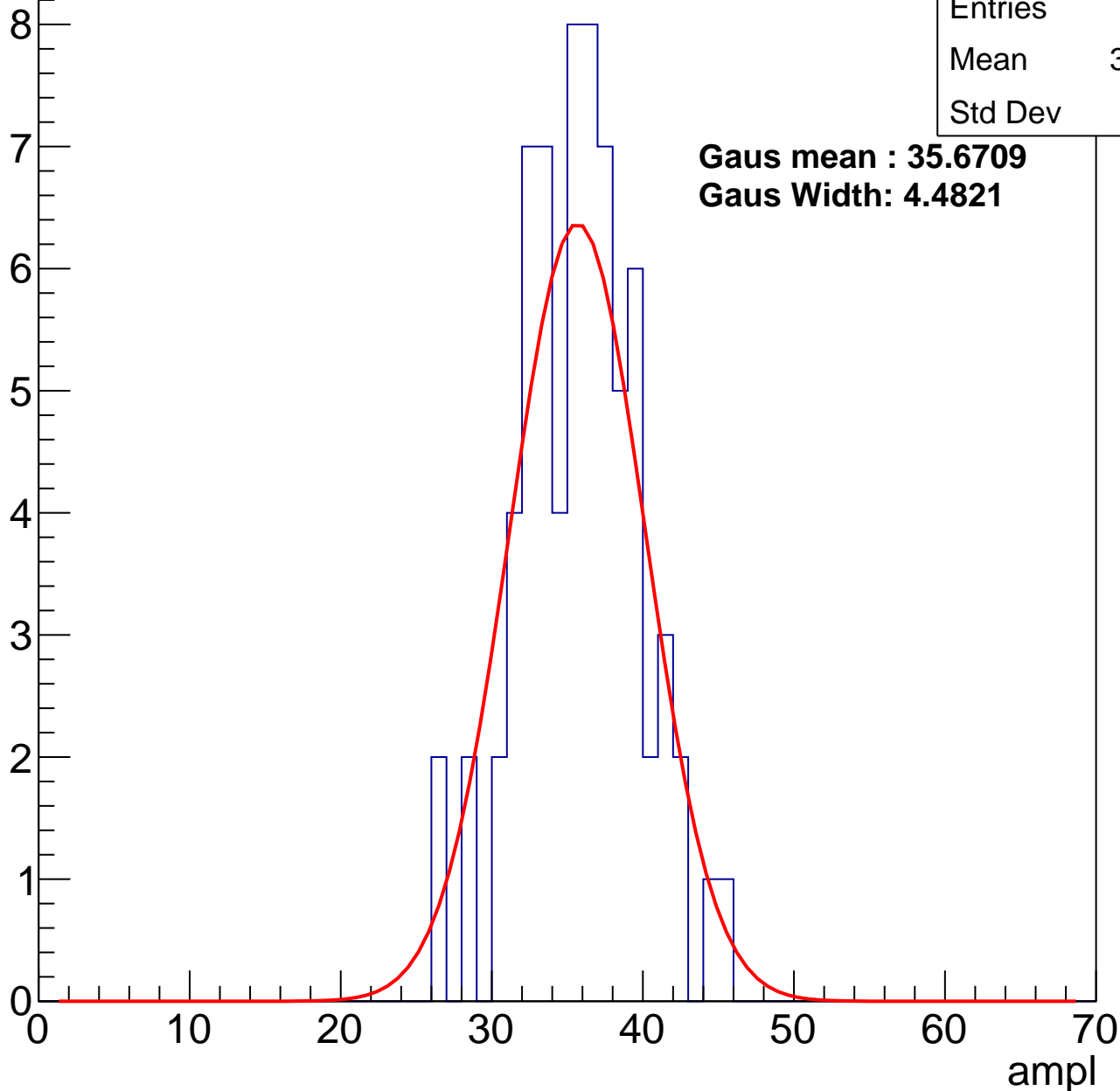
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.35
Std Dev	3.89

Gaus mean : 35.6709

Gaus Width: 4.4821



B1L102S, U17-ch79, adc2

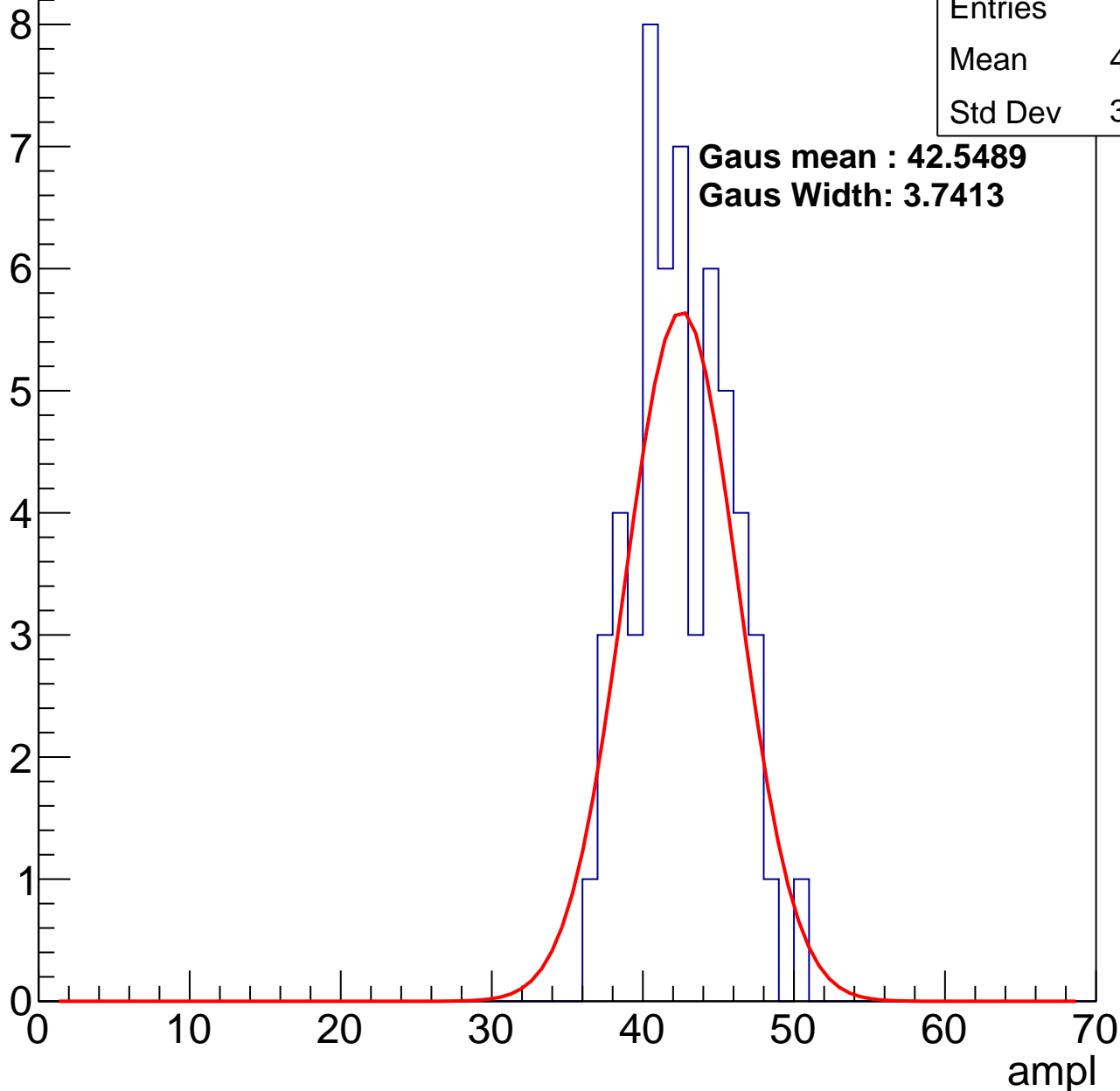
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.13
Std Dev	3.163

Gaus mean : 42.5489

Gaus Width: 3.7413

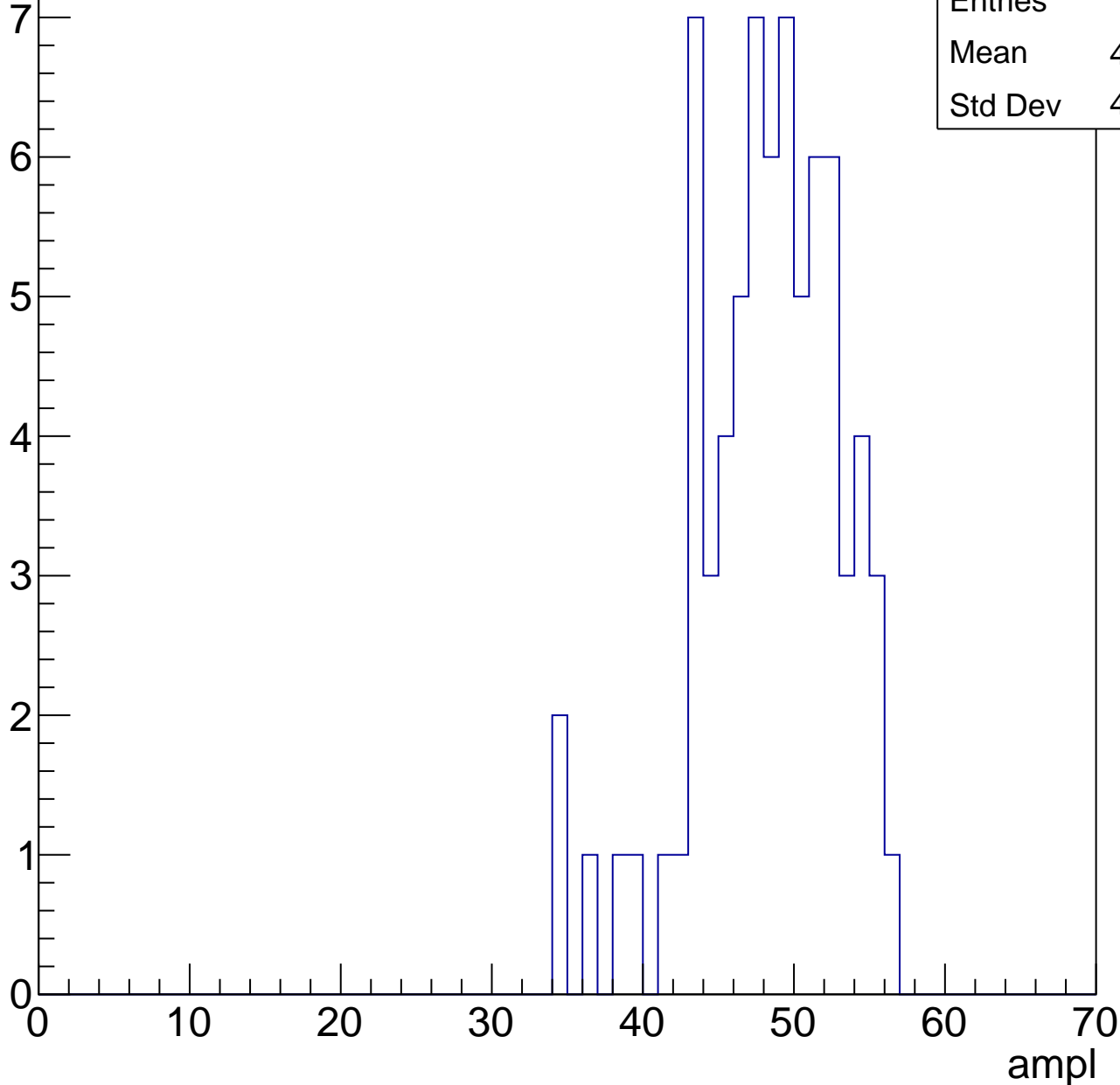


B1L102S, U17-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	47.72
Std Dev	4.778

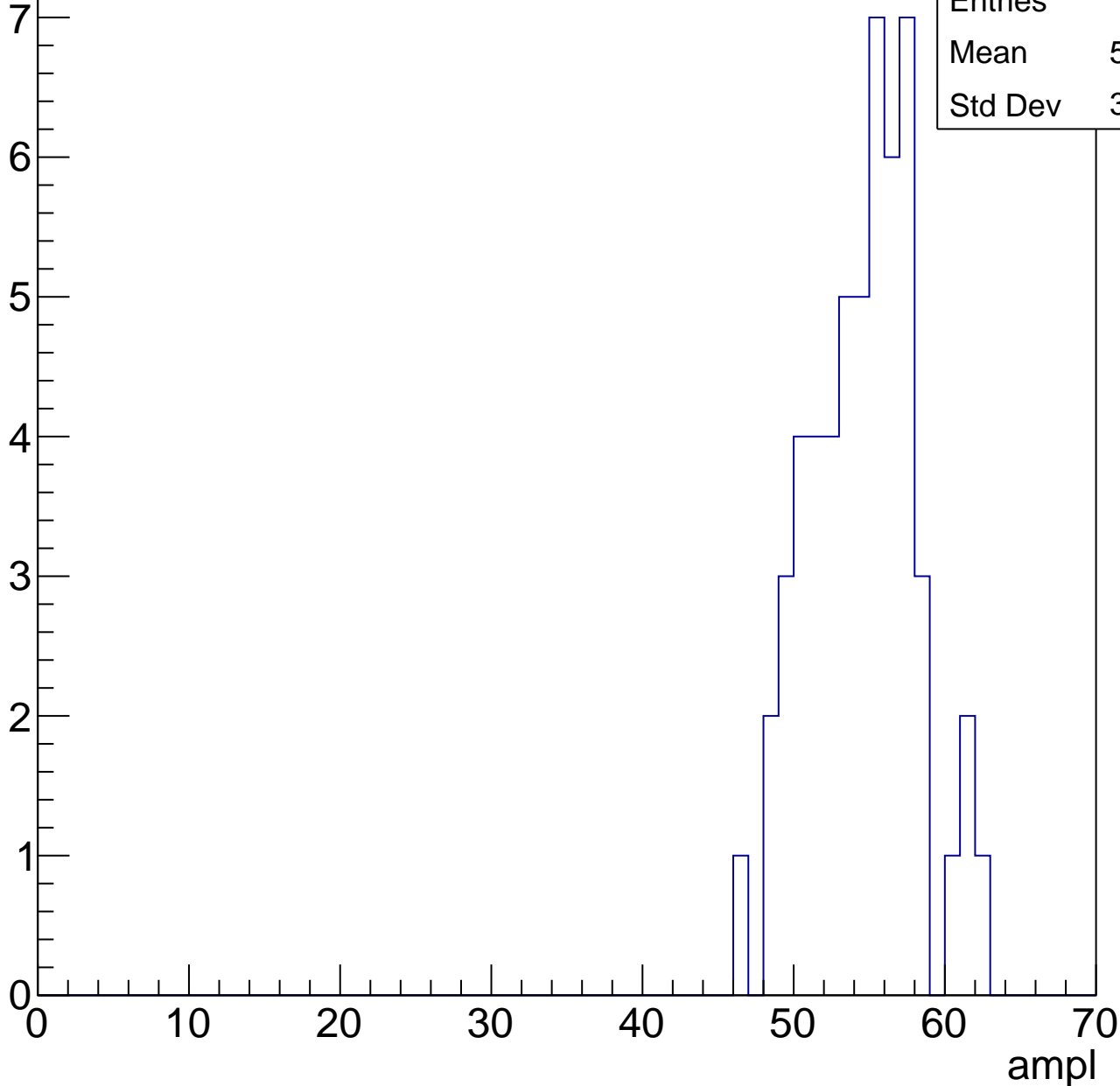


B1L102S, U17-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.07
Std Dev	3.495

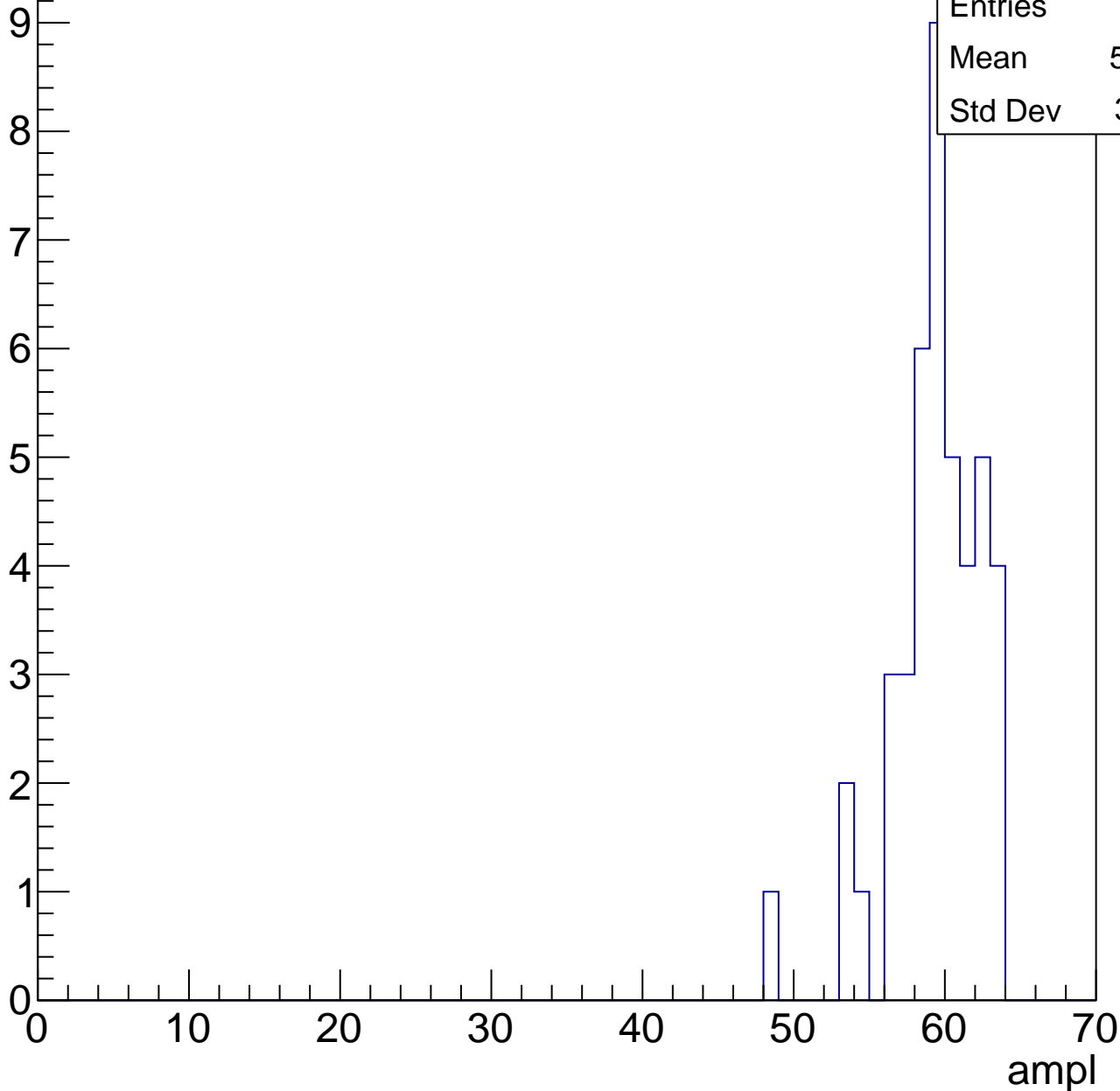


B1L102S, U17-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

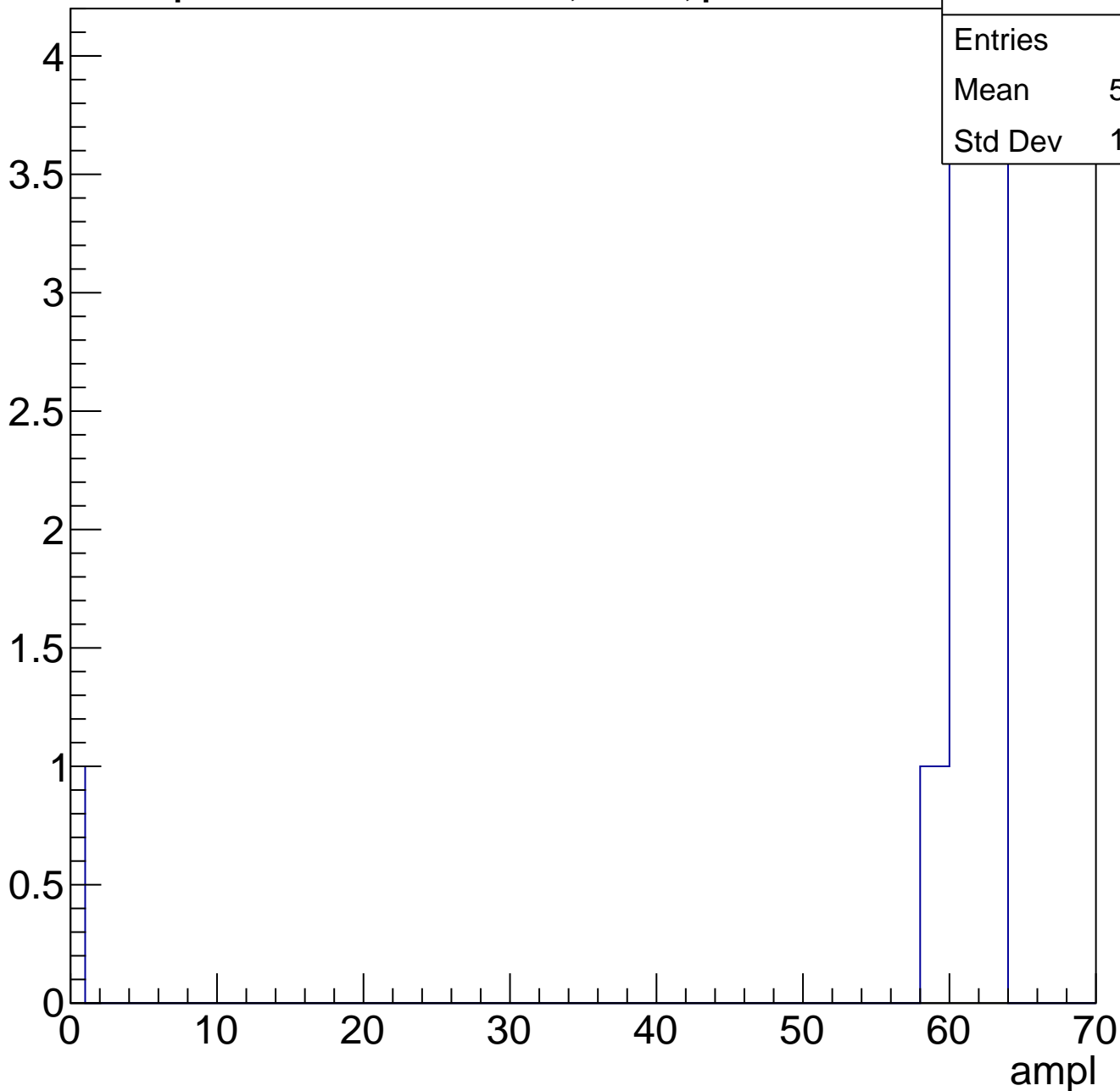
Entries	43
Mean	58.88
Std Dev	3.021



B1L102S, U17-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	63
Std Dev	0

B1L102S, U17-ch80, adc0

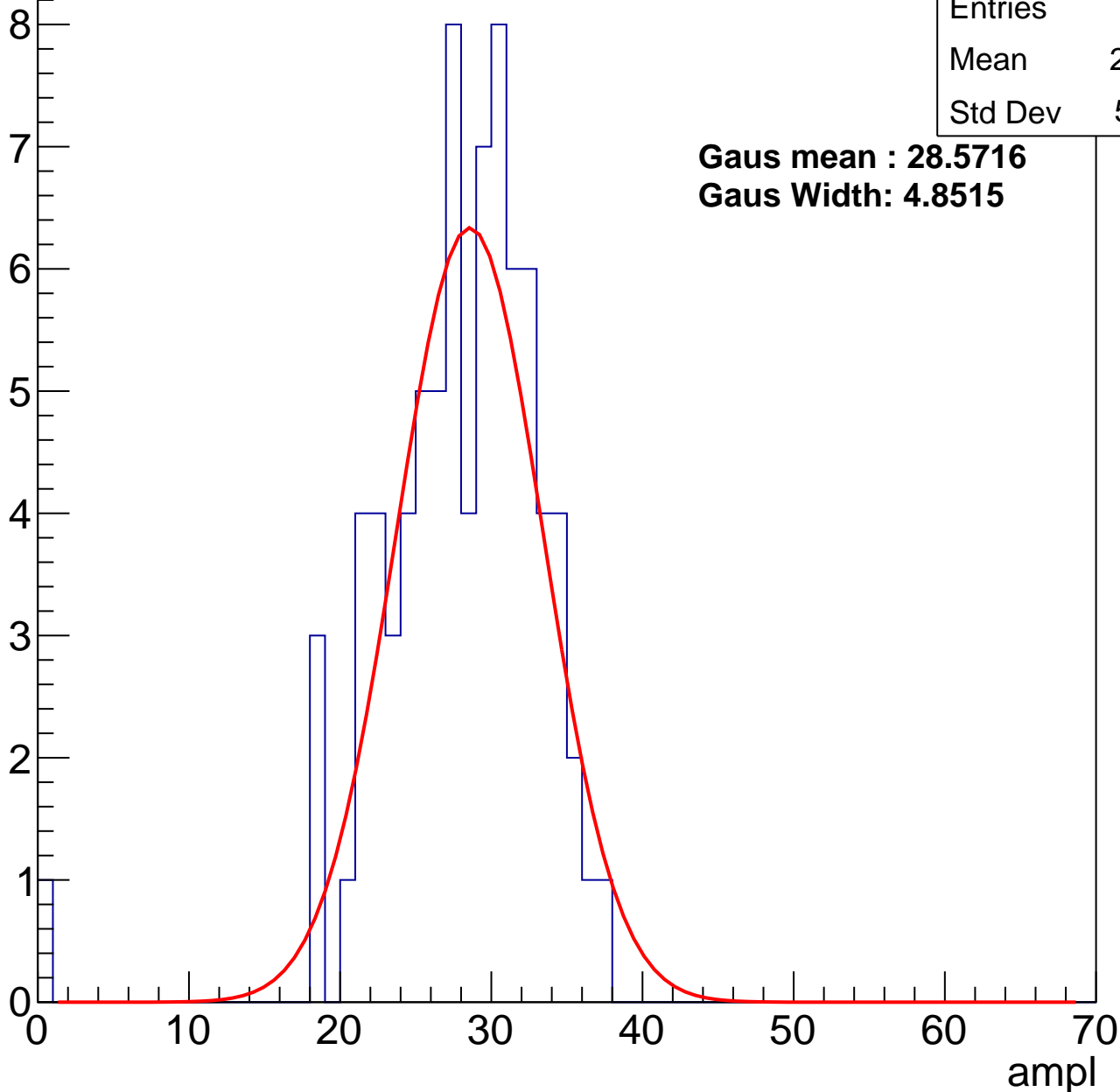
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.48
Std Dev	5.391

Gaus mean : 28.5716

Gaus Width: 4.8515



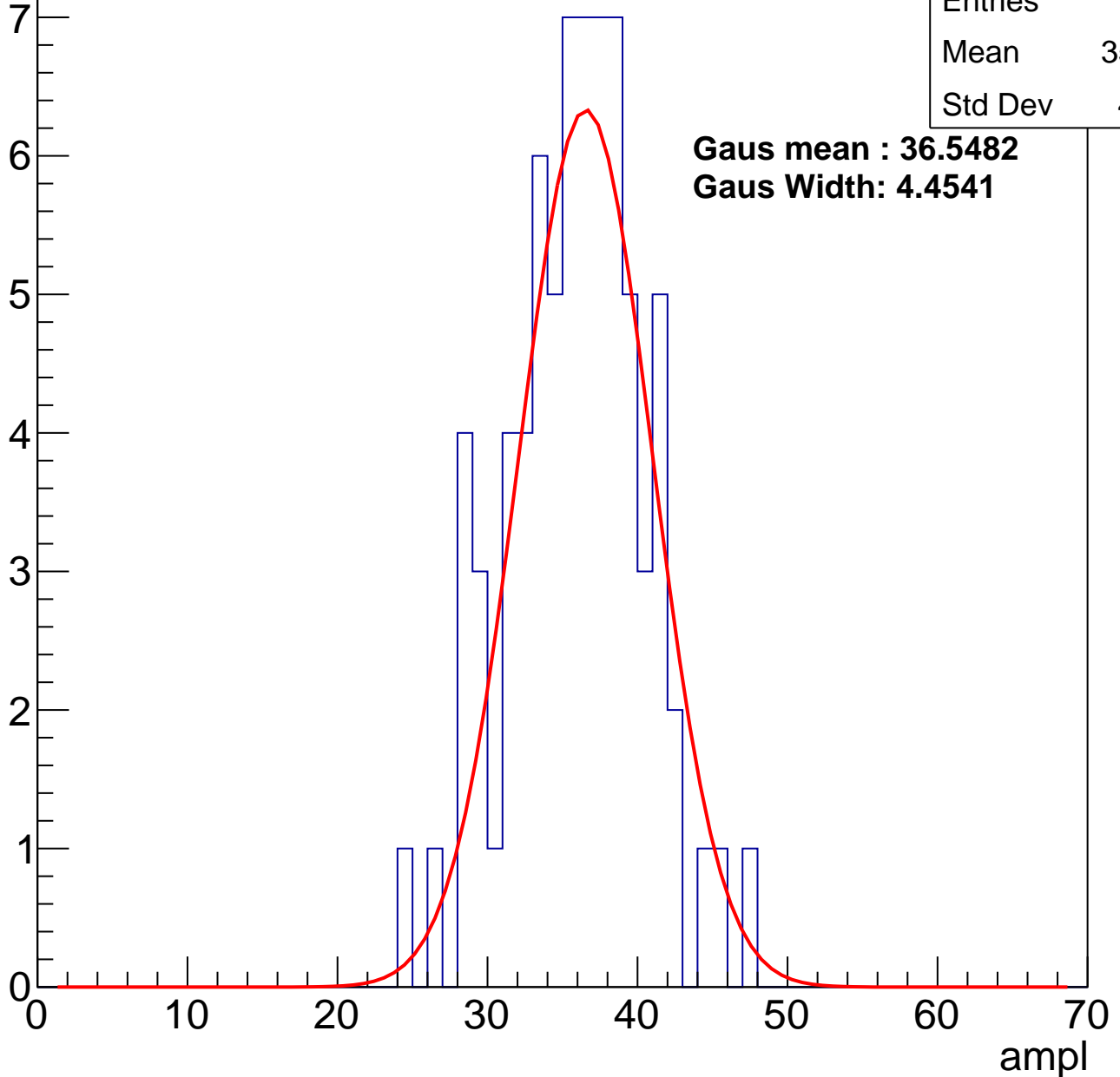
B1L102S, U17-ch80, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.48
Std Dev	4.47

Gaus mean : 36.5482
Gaus Width: 4.4541



B1L102S, U17-ch80, adc2

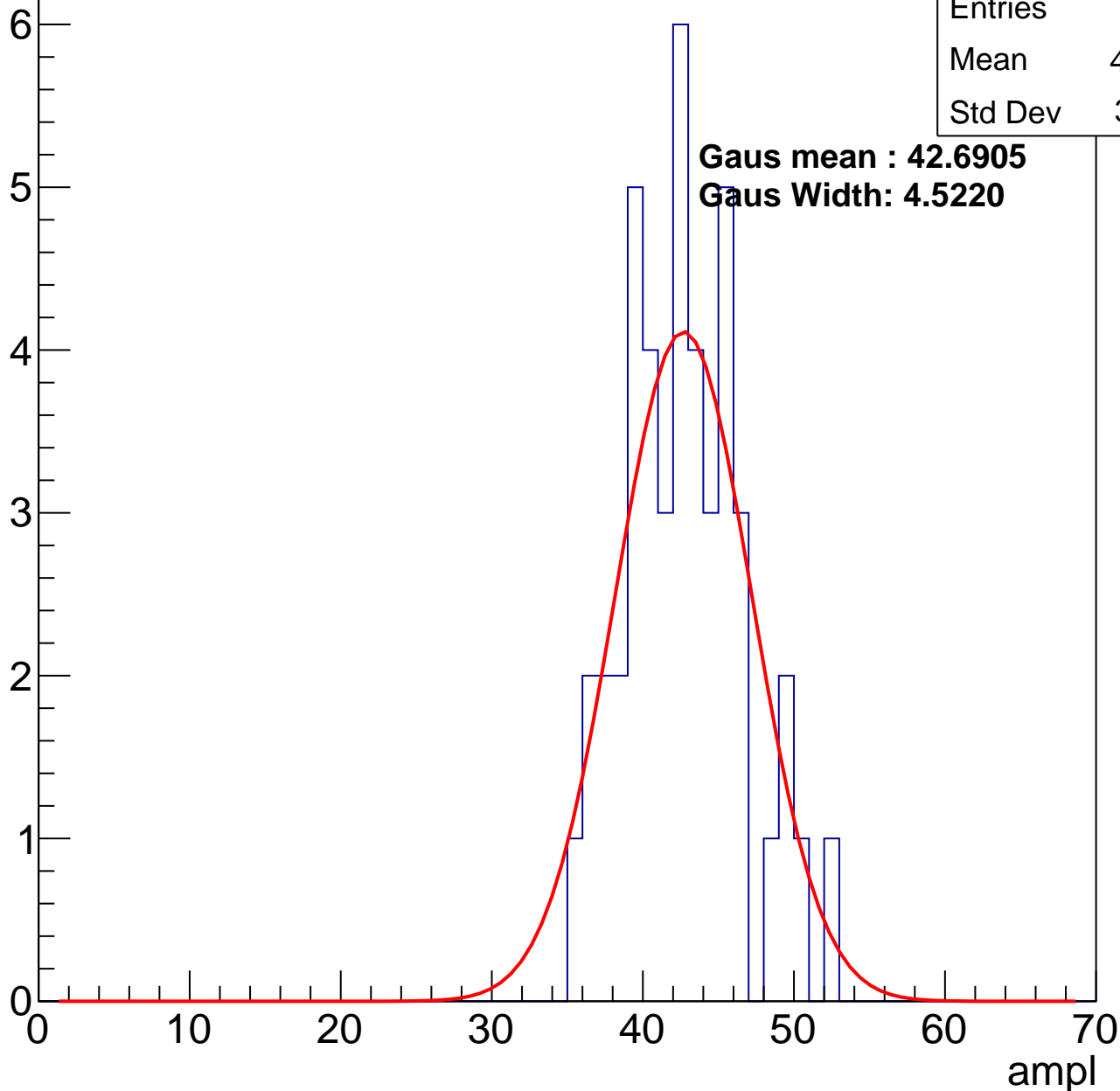
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	42.27
Std Dev	3.861

Gaus mean : 42.6905

Gaus Width: 4.5220

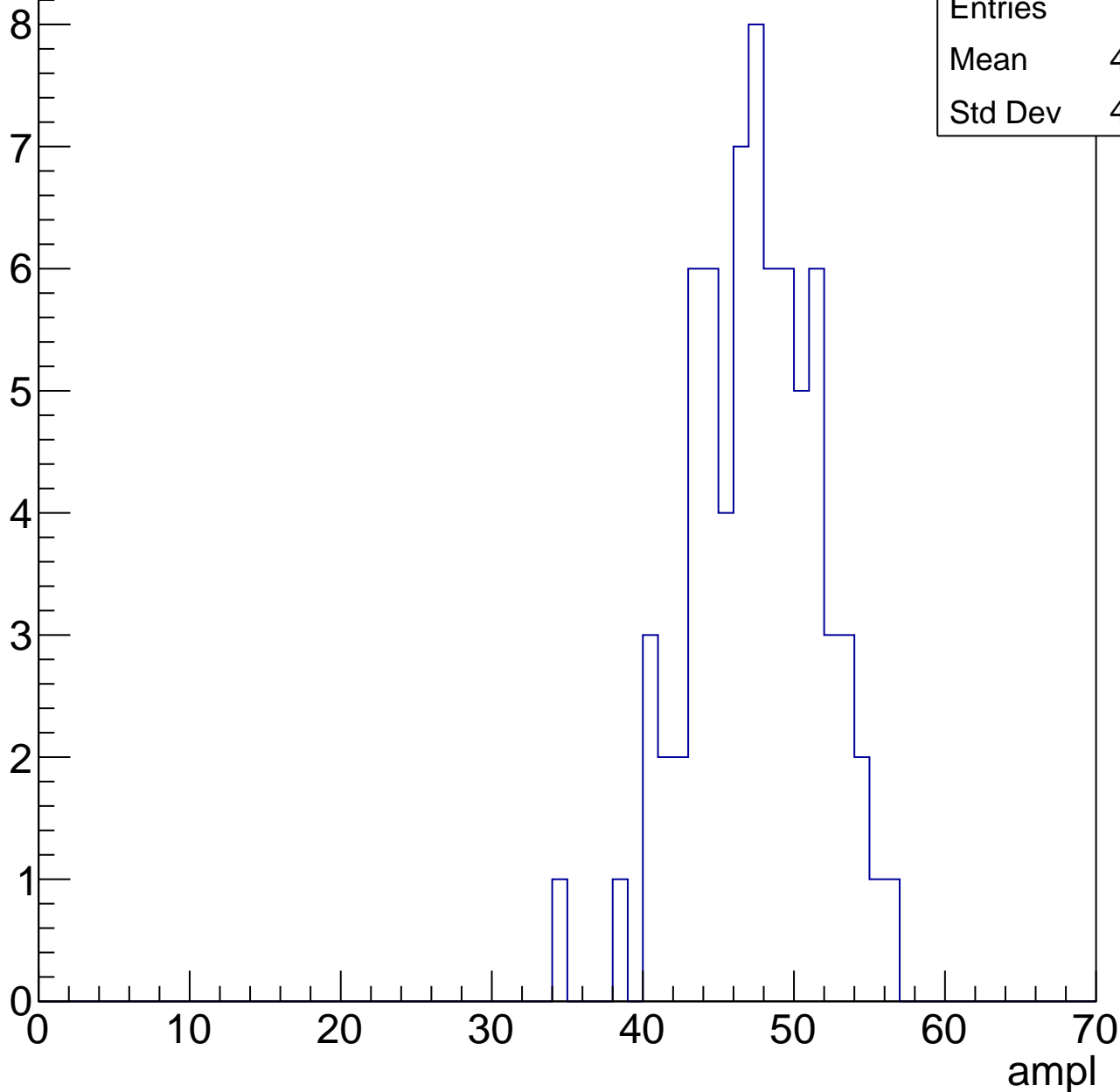


B1L102S, U17-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	46.99
Std Dev	4.202

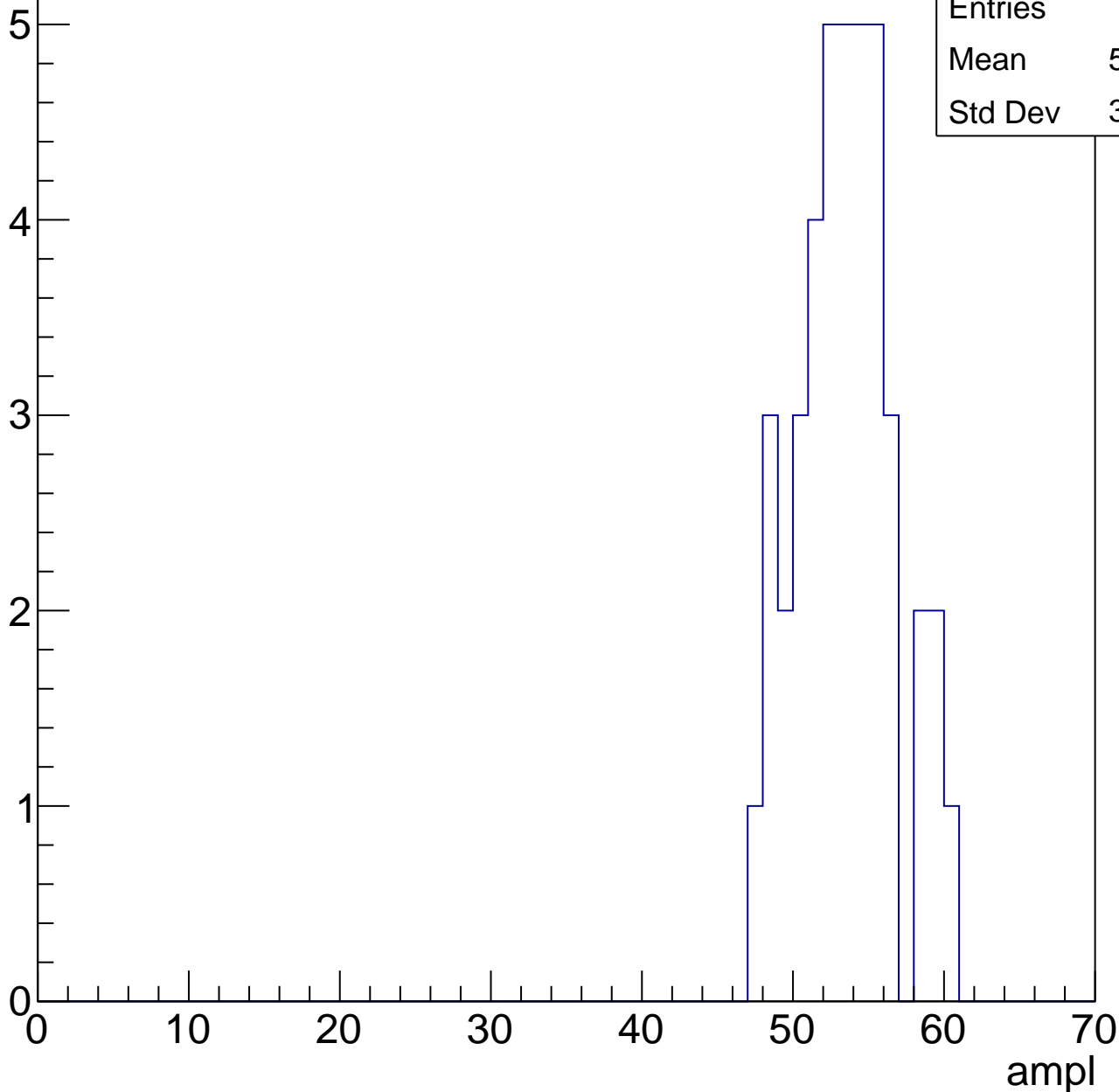


B1L102S, U17-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

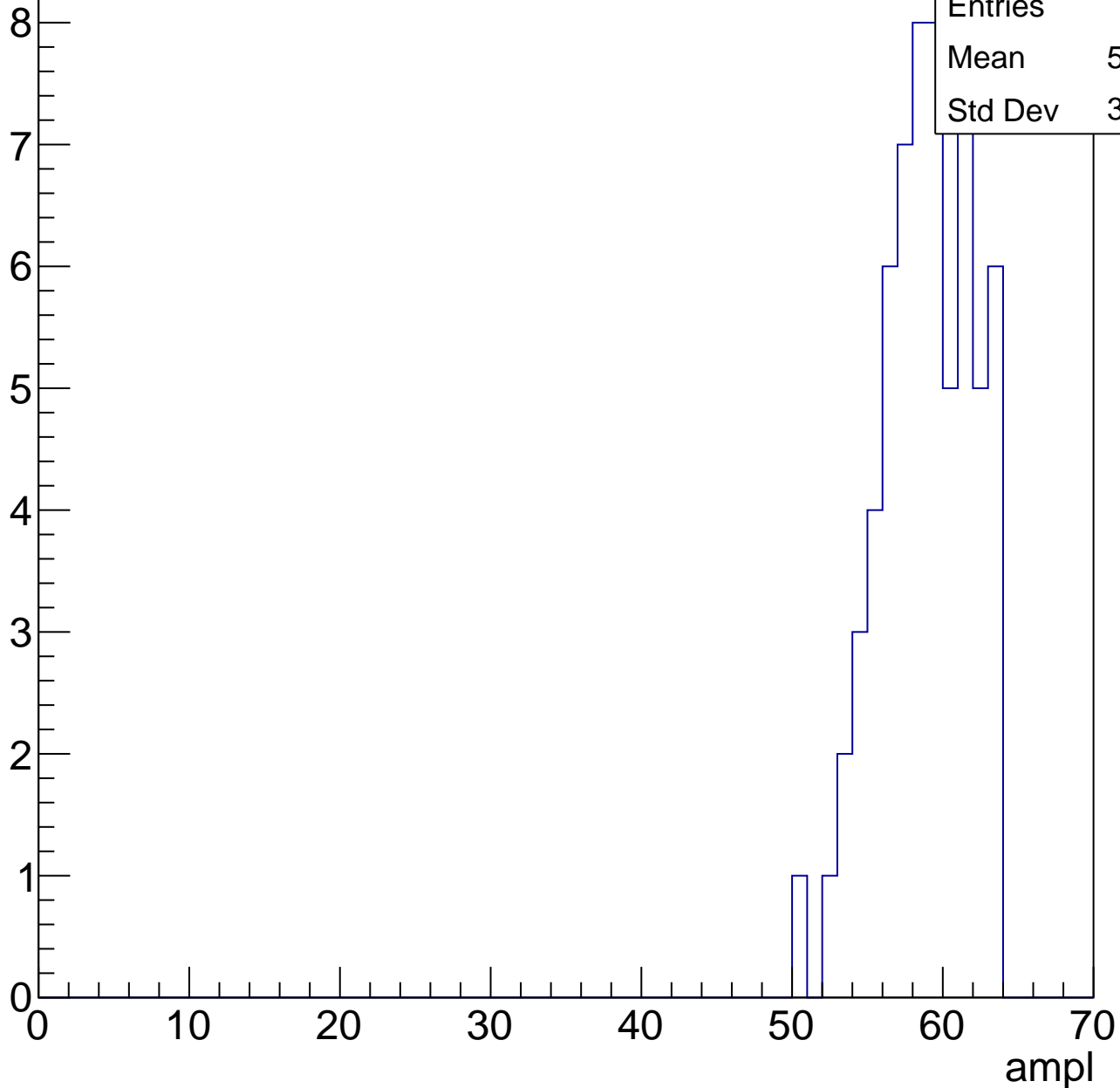
Entries	41
Mean	53.05
Std Dev	3.177



B1L102S, U17-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



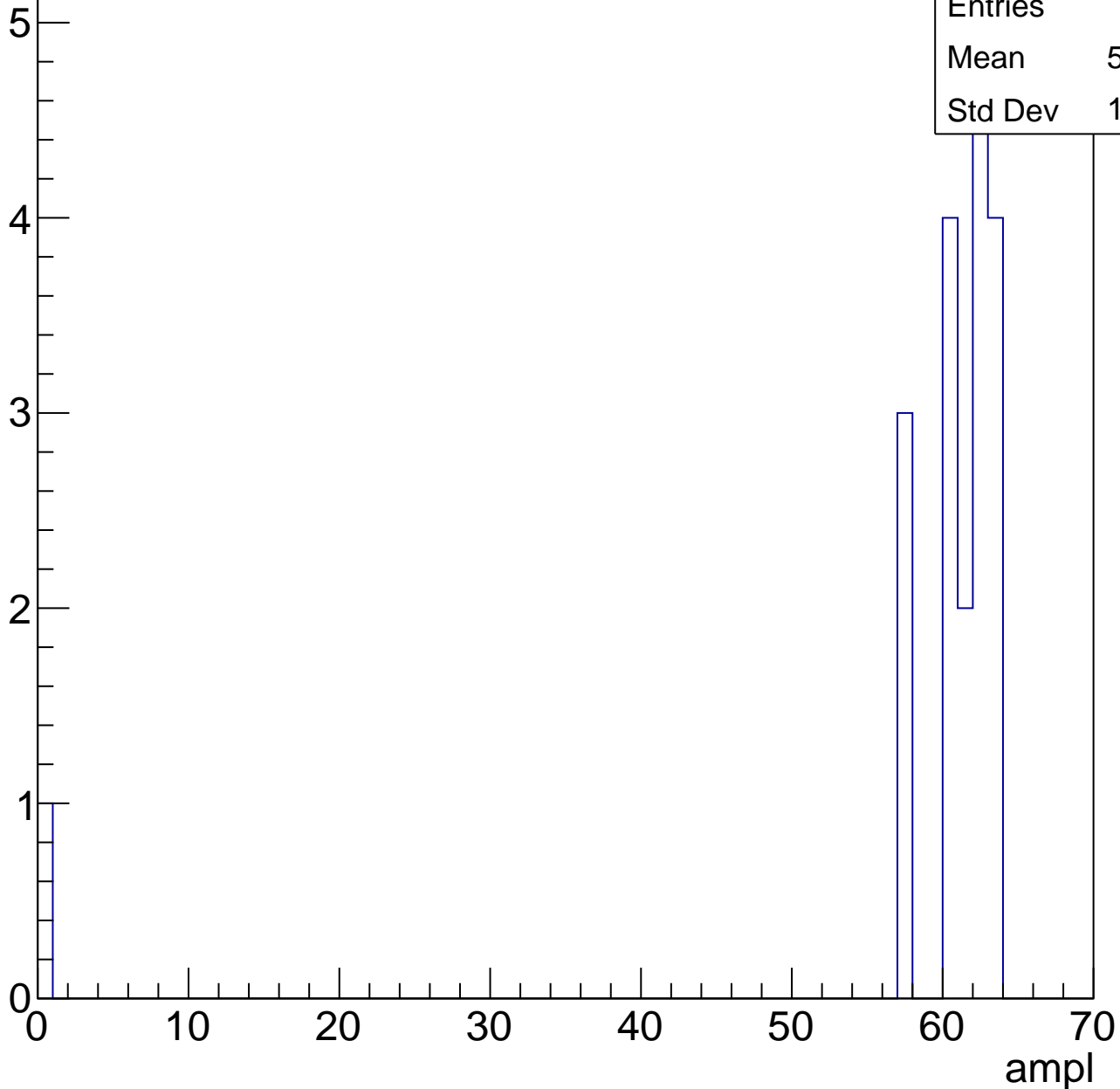
Entries	64
Mean	58.39
Std Dev	3.024

B1L102S, U17-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	57.63
Std Dev	13.72



B1L102S, U17-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch81, adc0

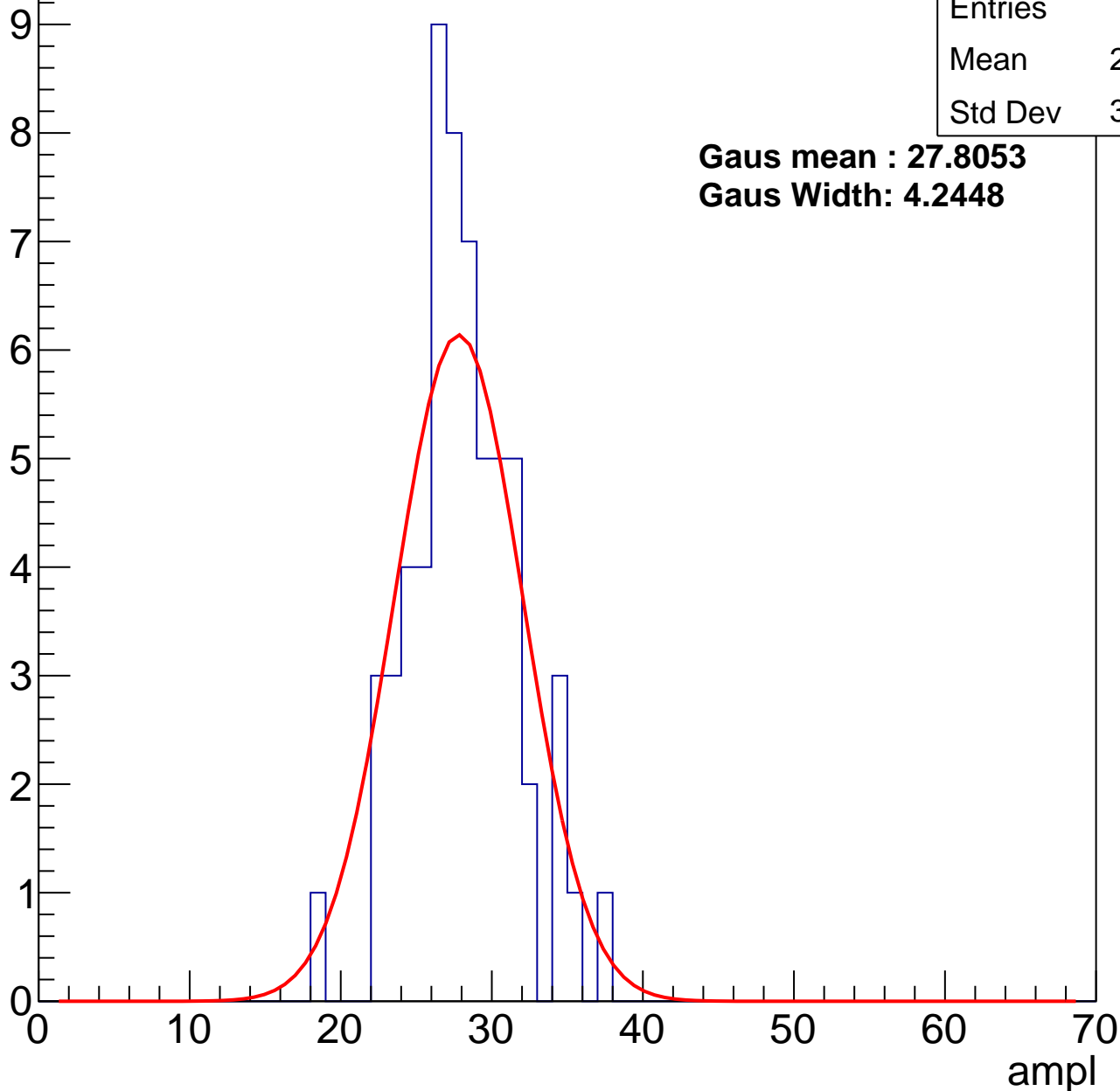
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.59
Std Dev	3.522

Gaus mean : 27.8053

Gaus Width: 4.2448



B1L102S, U17-ch81, adc1

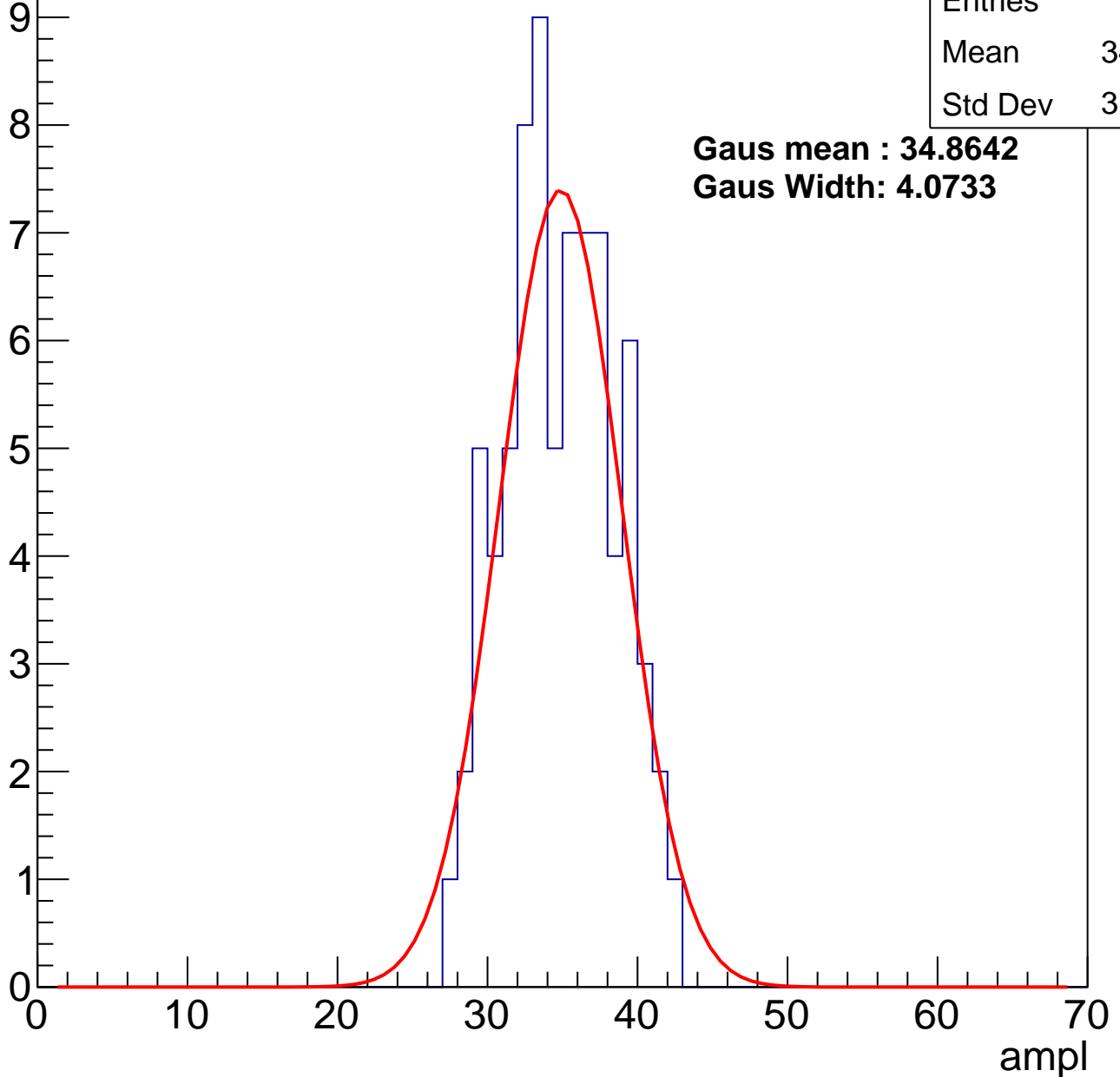
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.37
Std Dev	3.568

Gaus mean : 34.8642

Gaus Width: 4.0733



B1L102S, U17-ch81, adc2

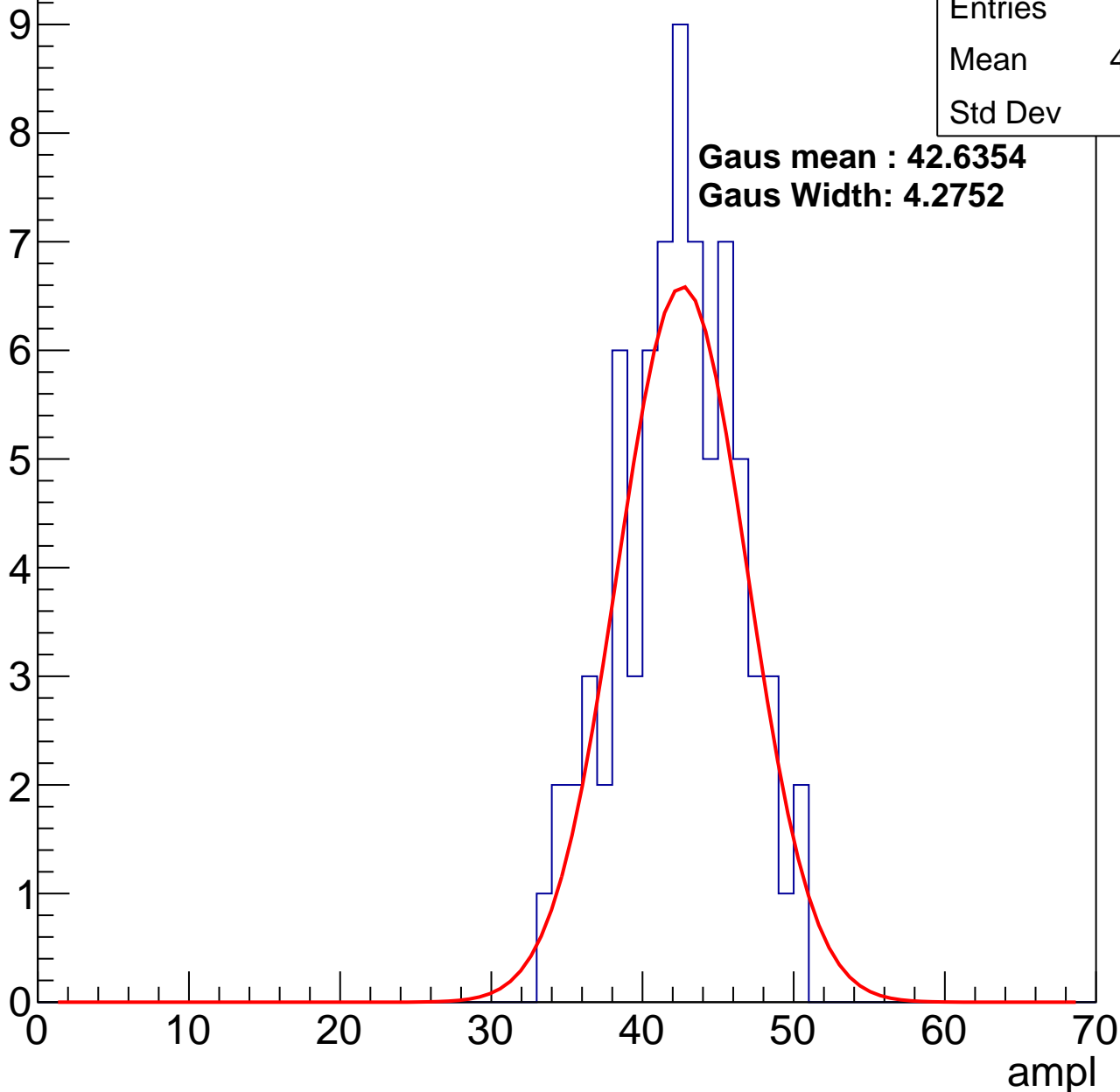
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.93
Std Dev	3.94

Gaus mean : 42.6354

Gaus Width: 4.2752

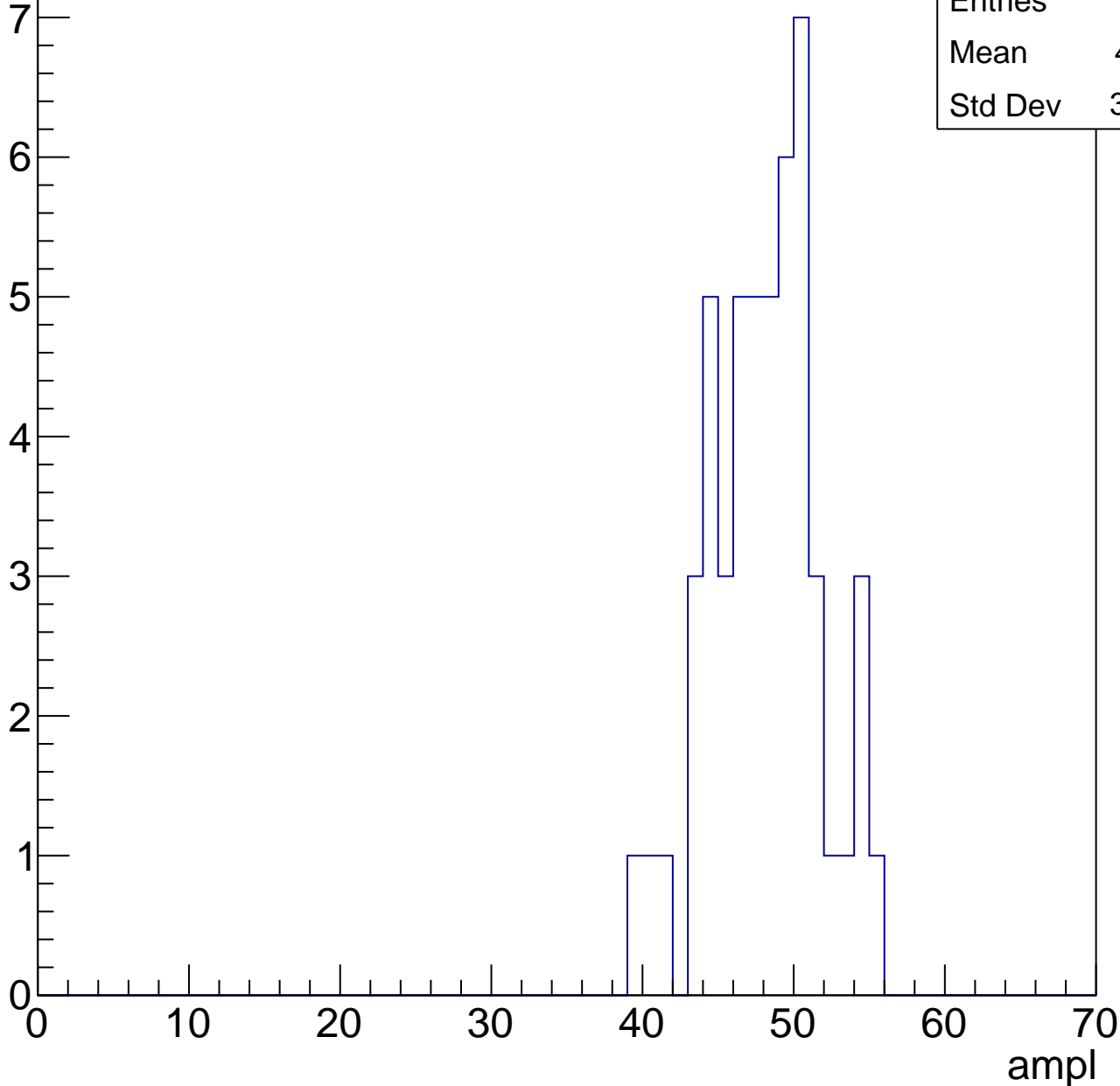


B1L102S, U17-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	47.61
Std Dev	3.576

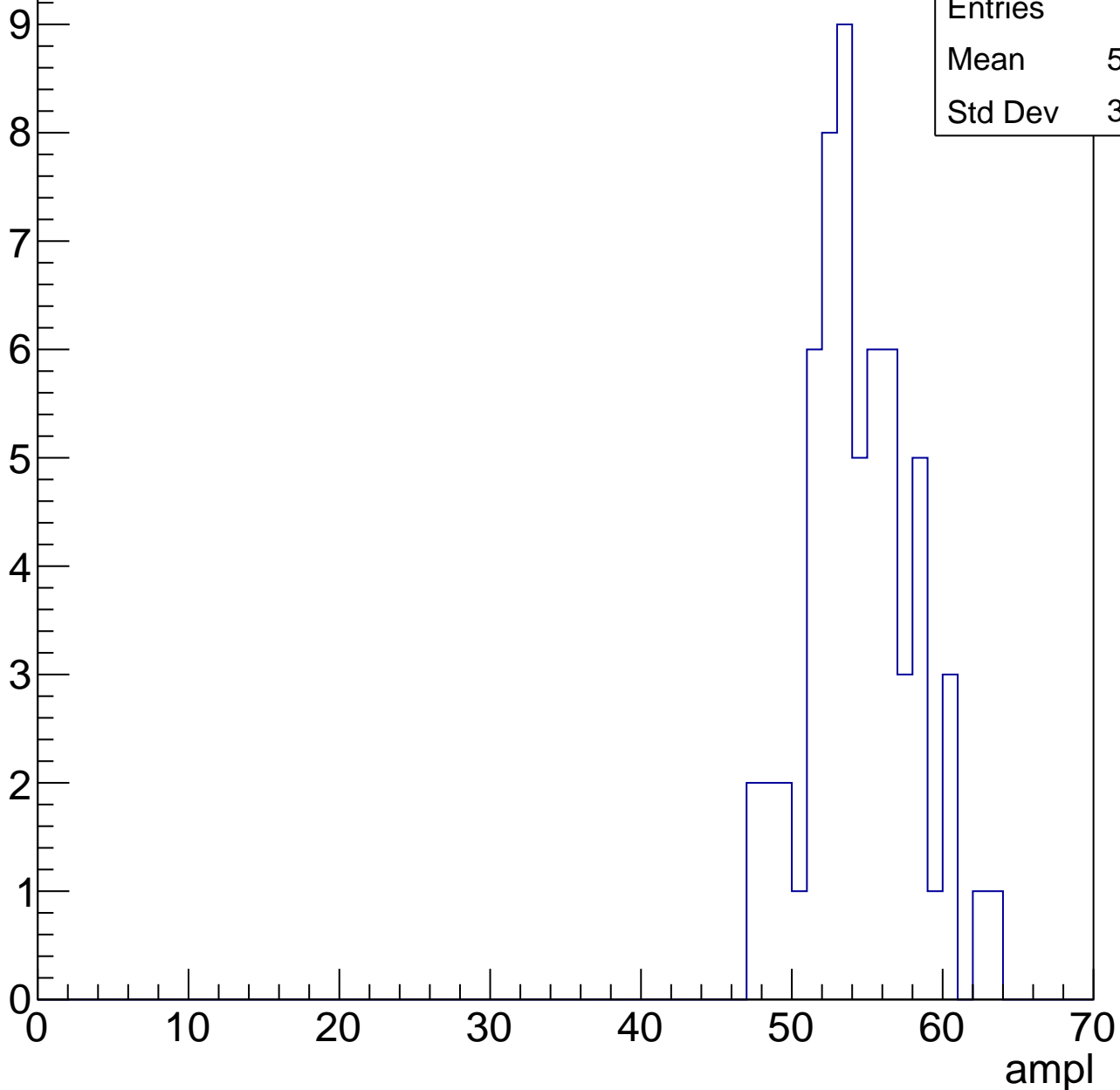


B1L102S, U17-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.07
Std Dev	3.496

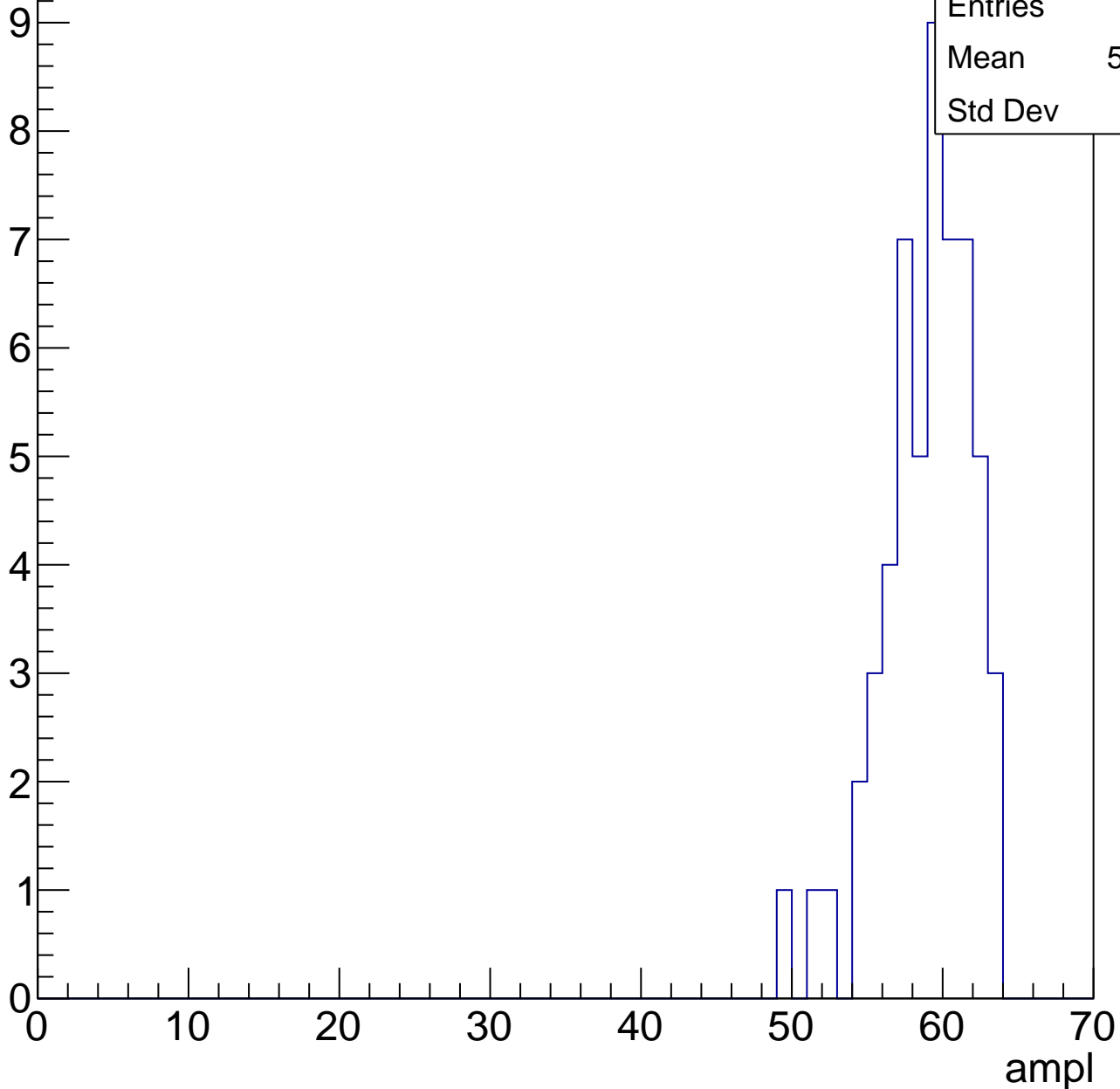


B1L102S, U17-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.45
Std Dev	2.99

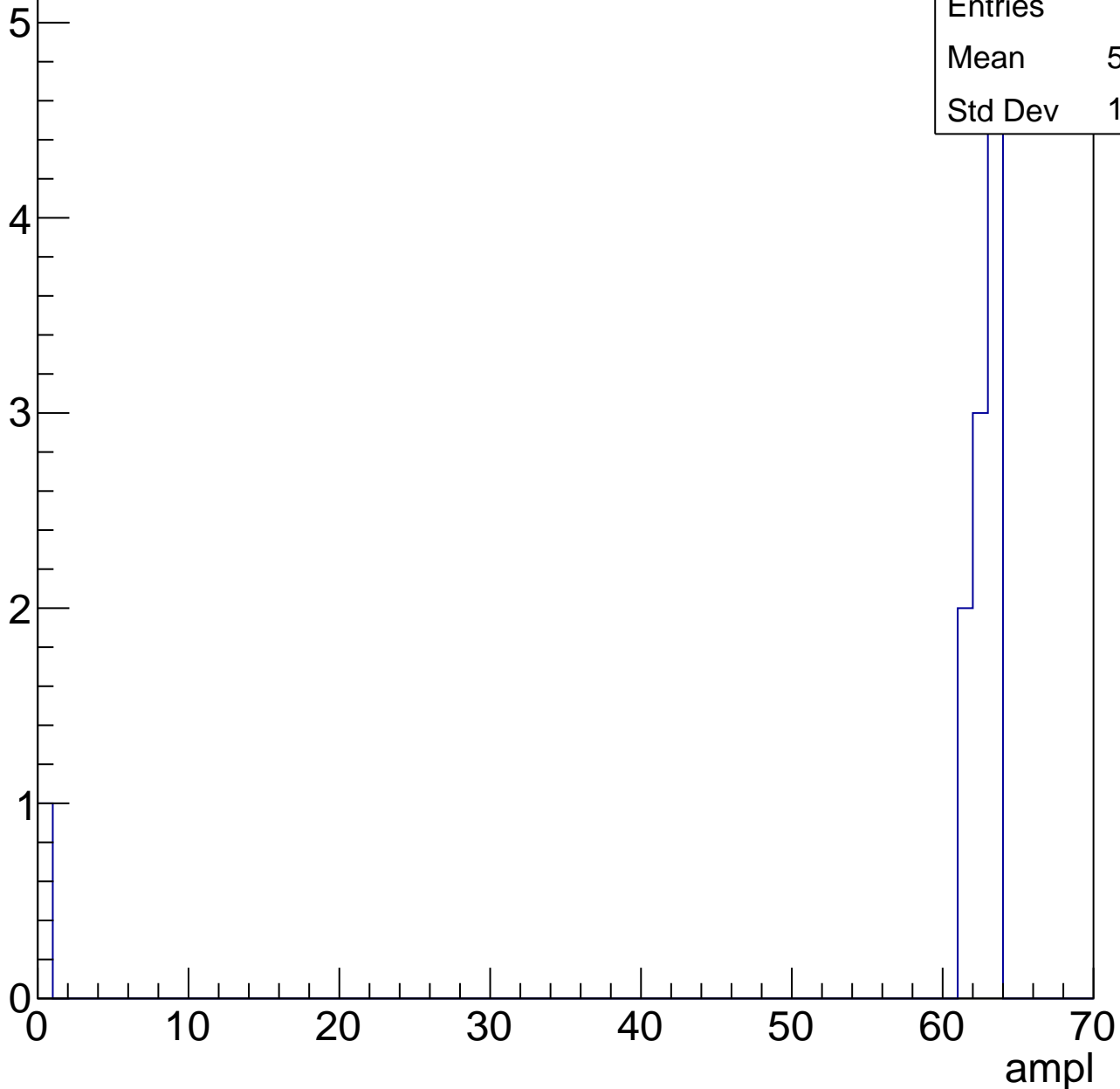


B1L102S, U17-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

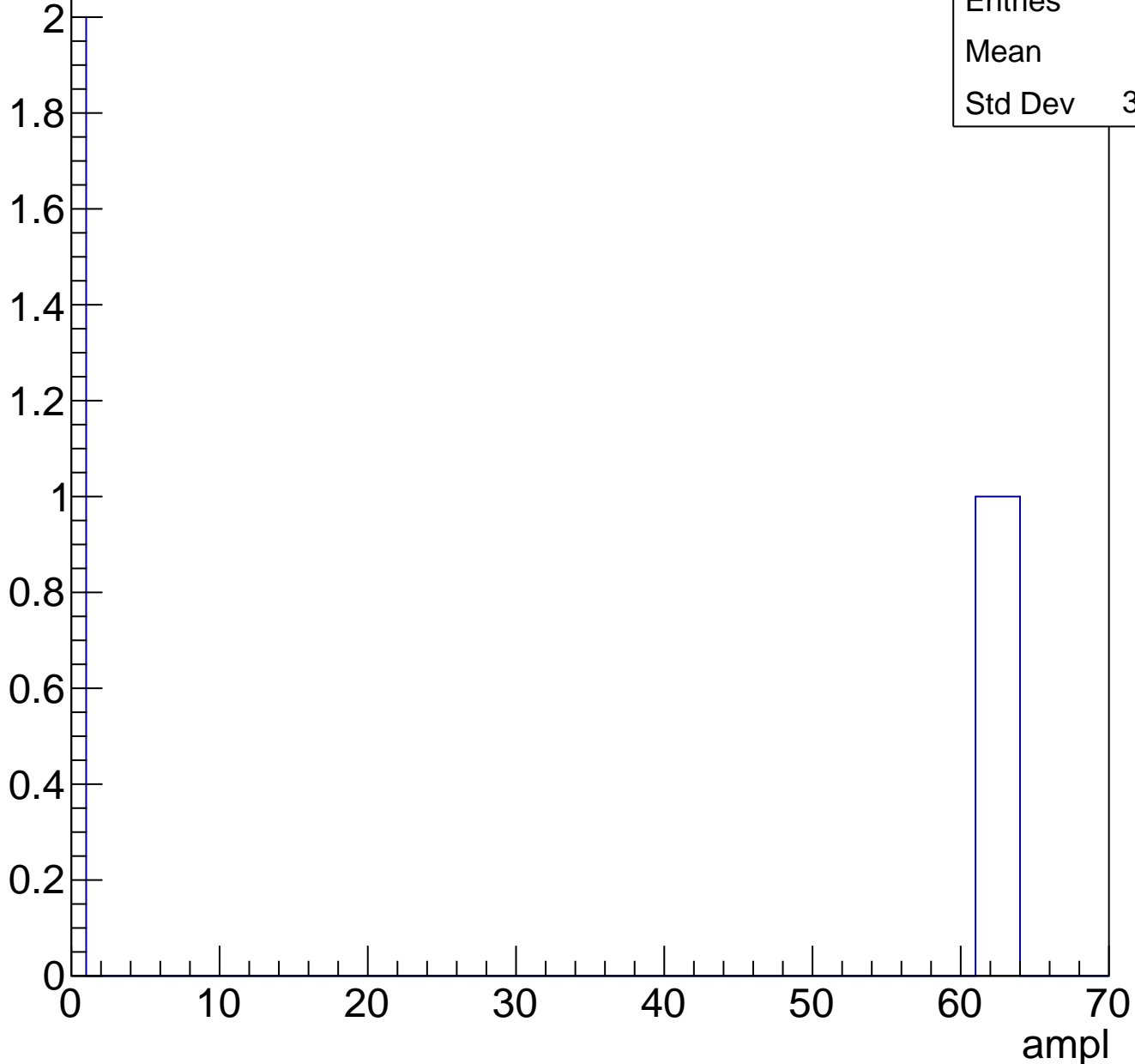
Entries	11
Mean	56.64
Std Dev	17.93



B1L102S, U17-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	37.2
Std Dev	30.38

B1L102S, U17-ch82, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	28.56
Std Dev	5.925

Gaus mean : 29.2580

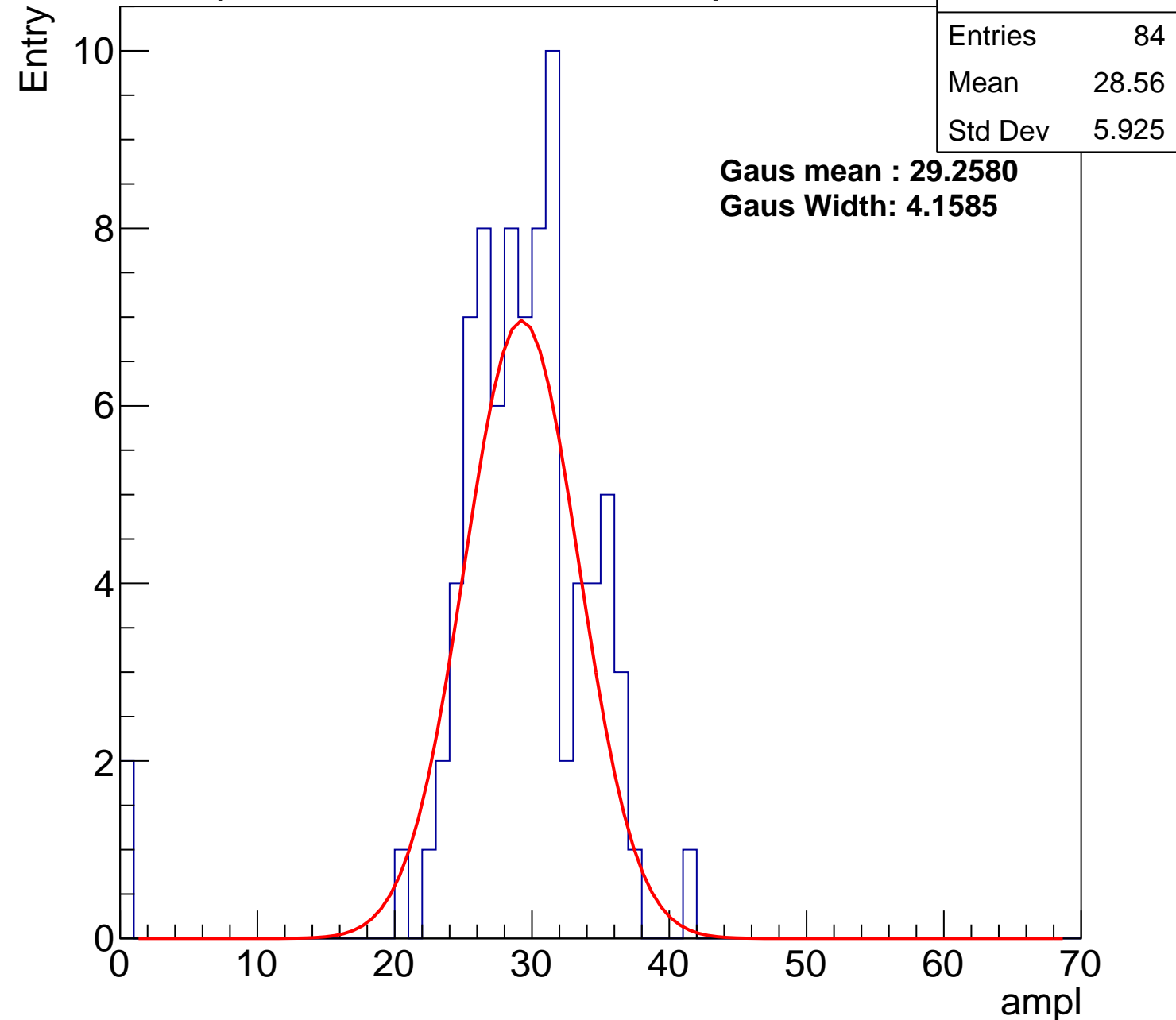
Gaus Width: 4.1585

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch82, adc1

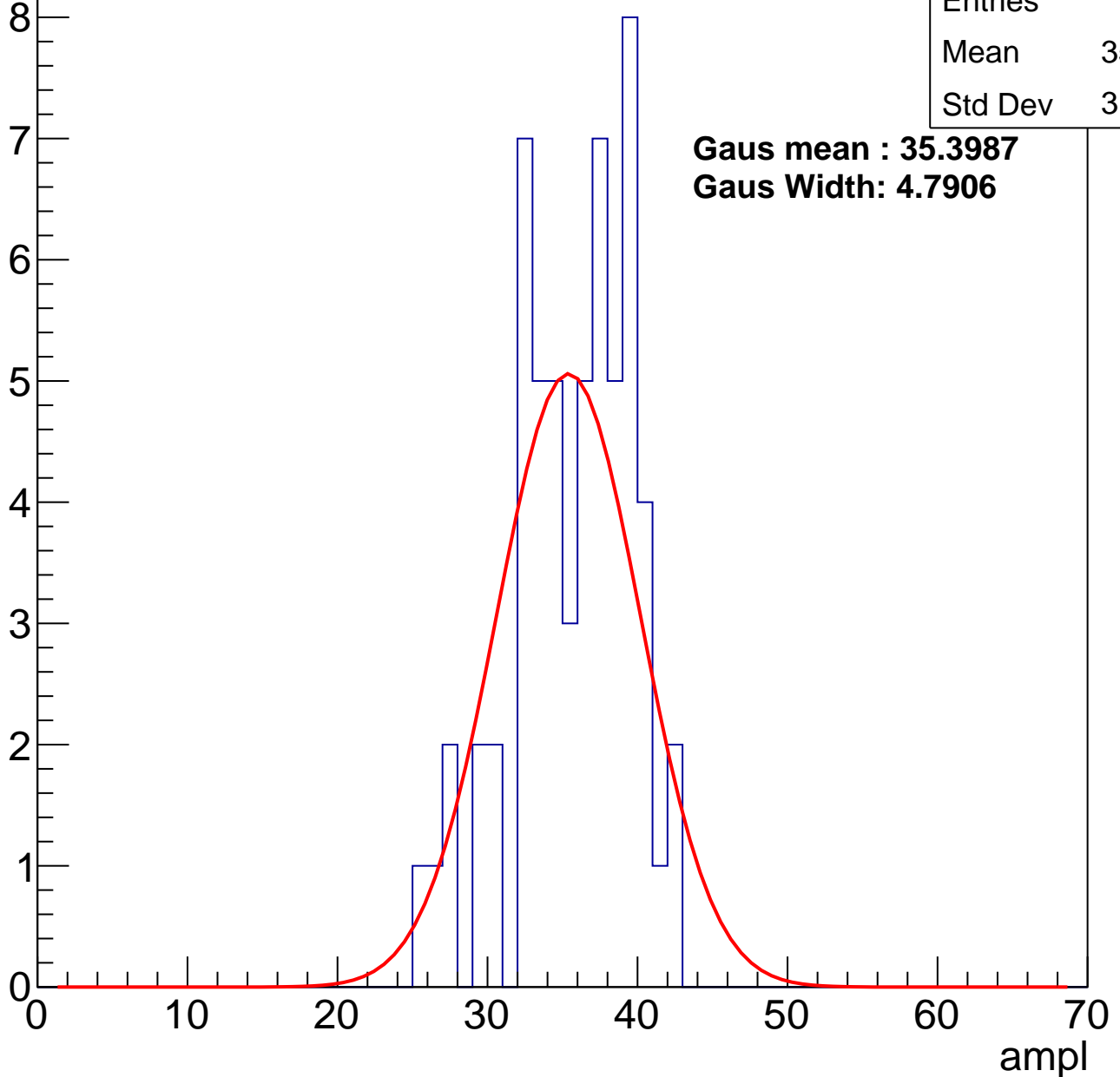
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.22
Std Dev	3.992

Gaus mean : 35.3987

Gaus Width: 4.7906



B1L102S, U17-ch82, adc2

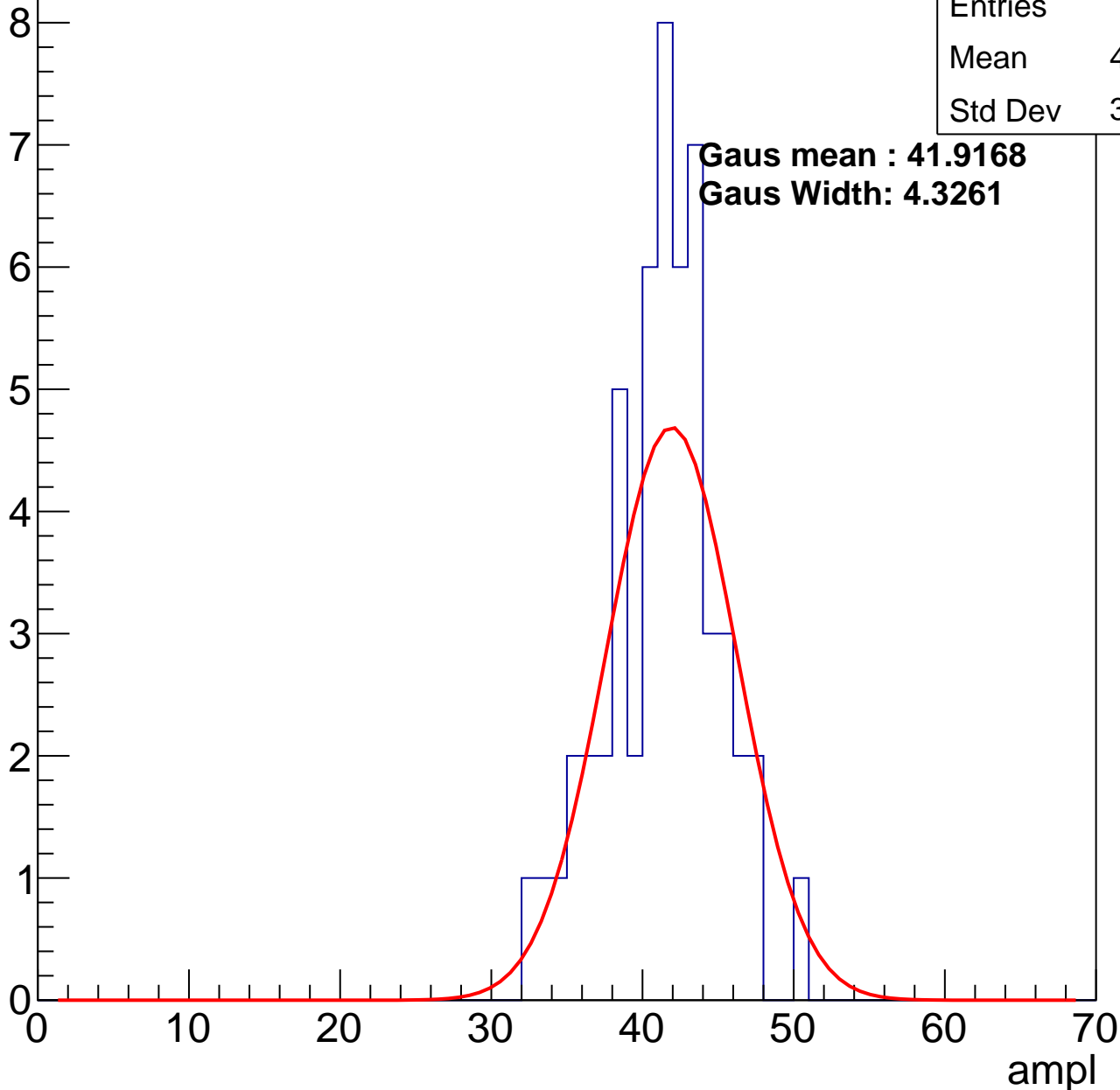
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	40.87
Std Dev	3.647

Gaus mean : 41.9168

Gaus Width: 4.3261

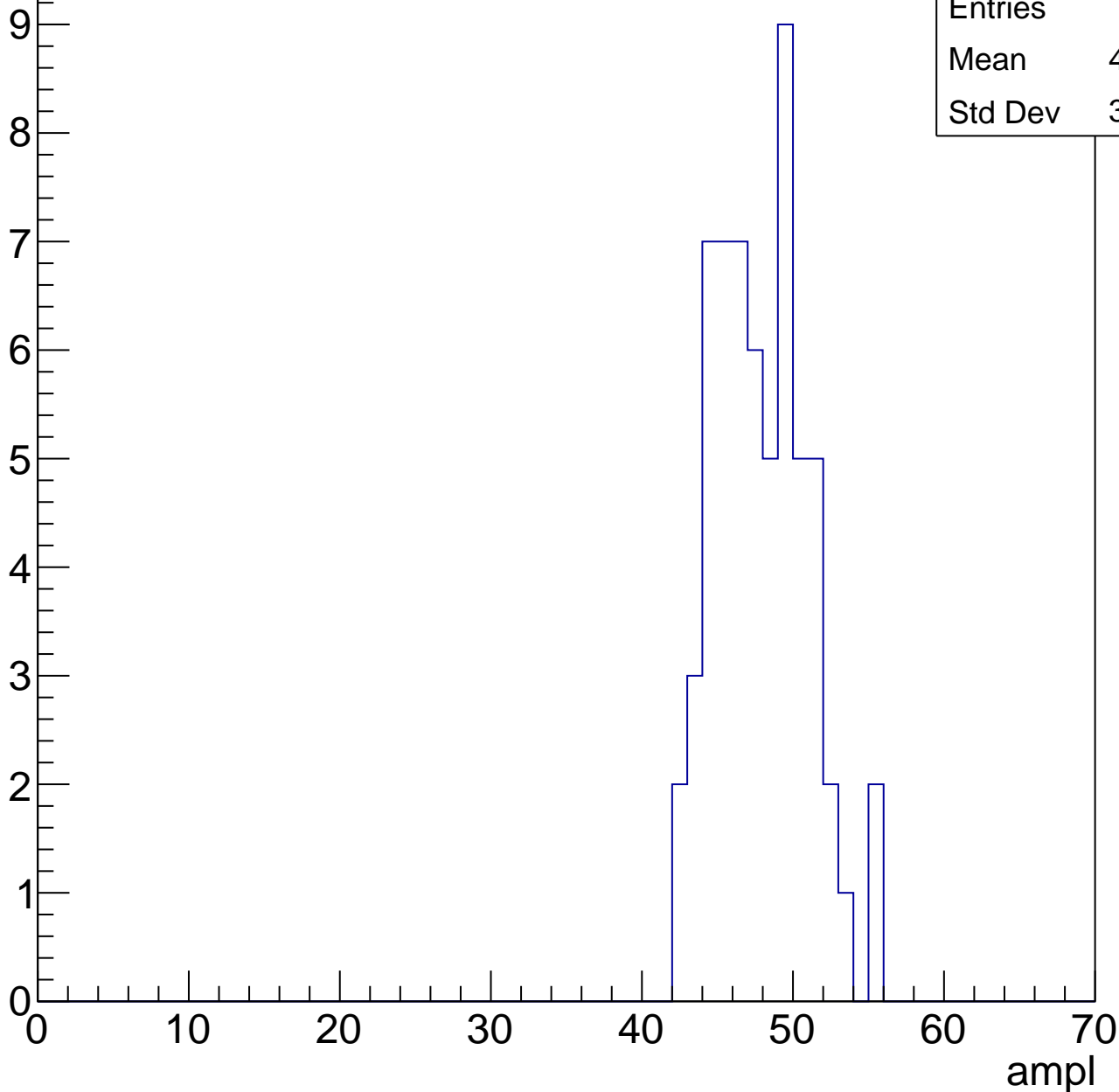


B1L102S, U17-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.43
Std Dev	3.043



B1L102S, U17-ch82, adc4

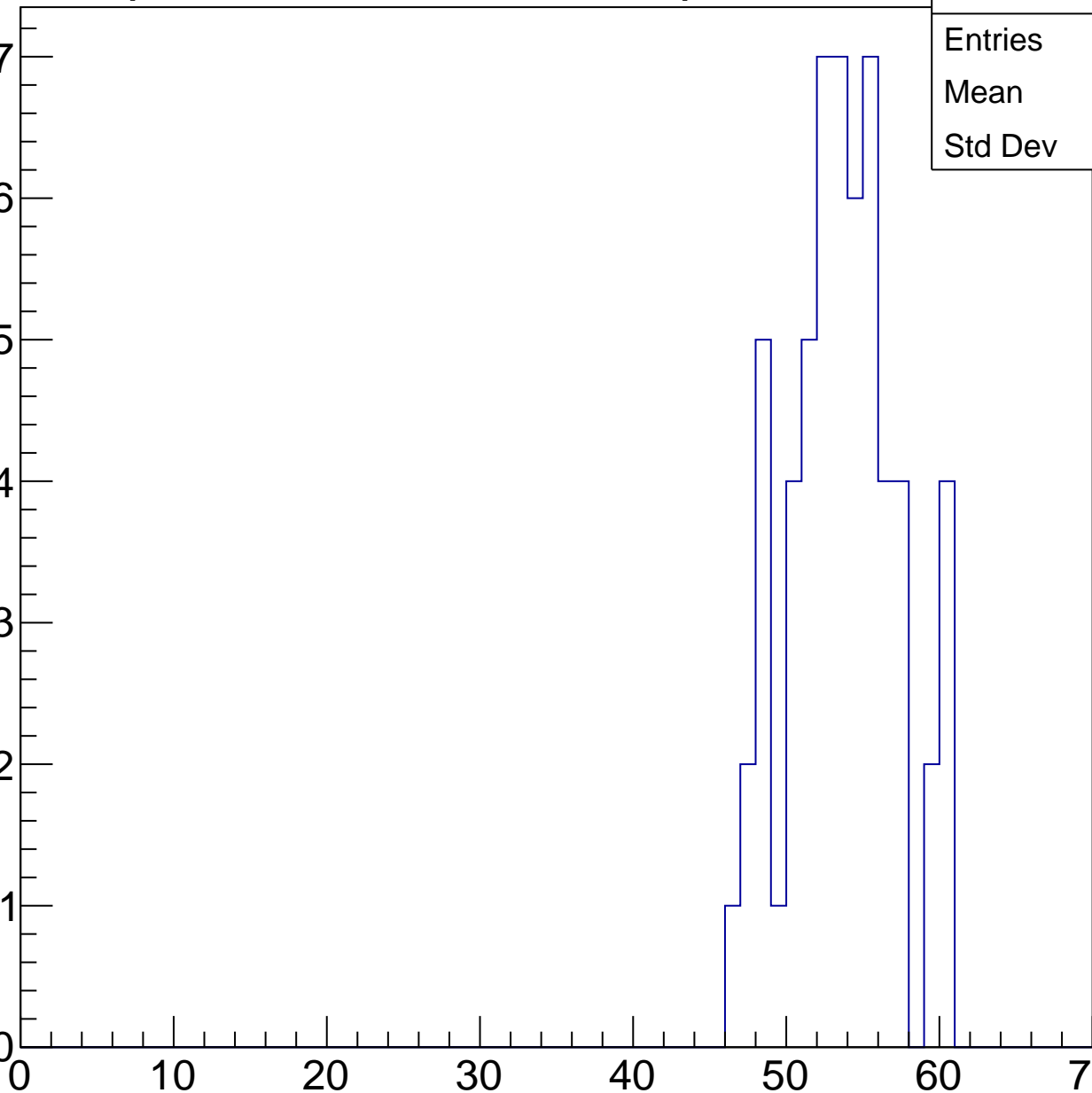
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	59
Mean	53.19
Std Dev	3.505

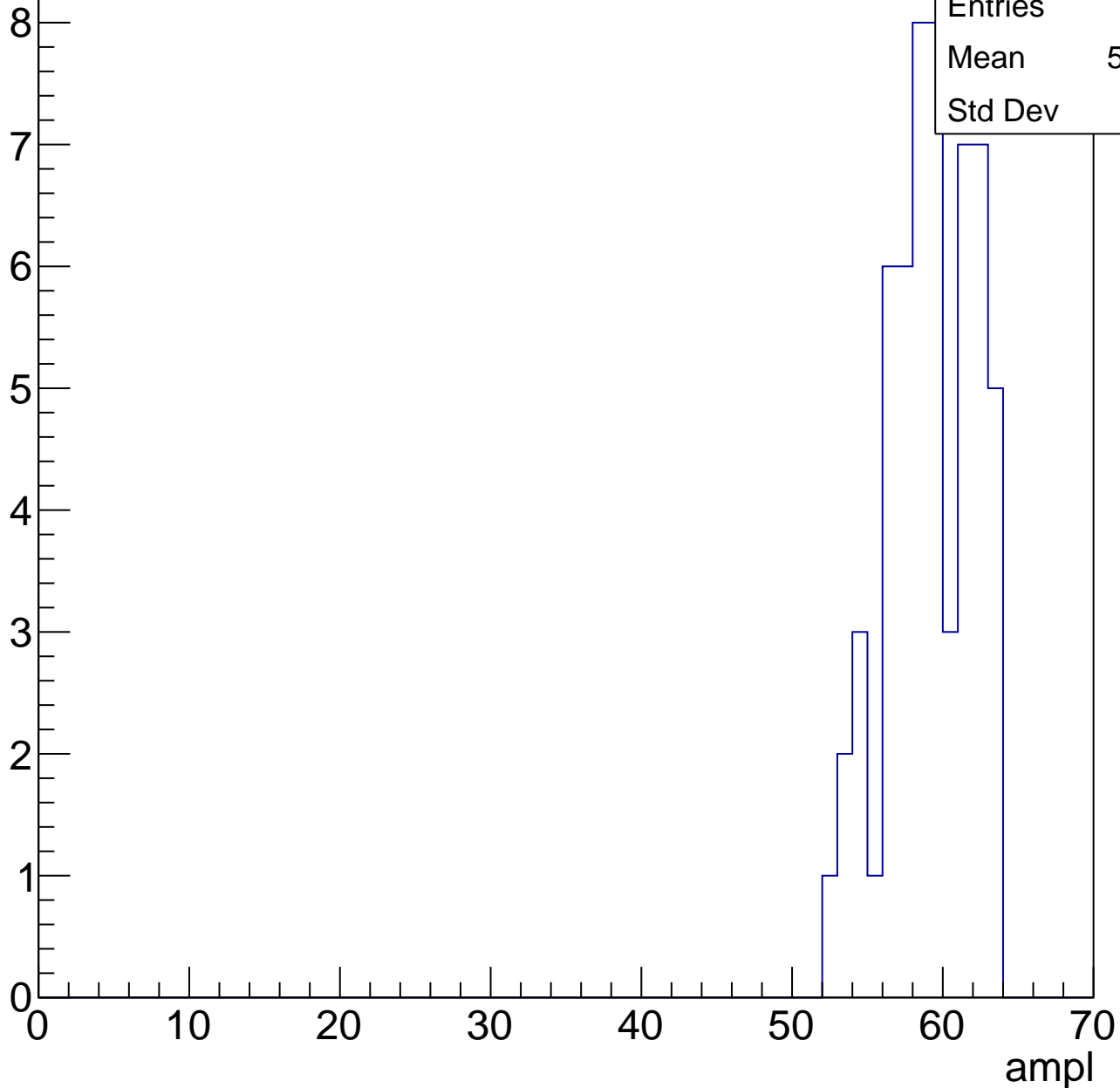
ampl



B1L102S, U17-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

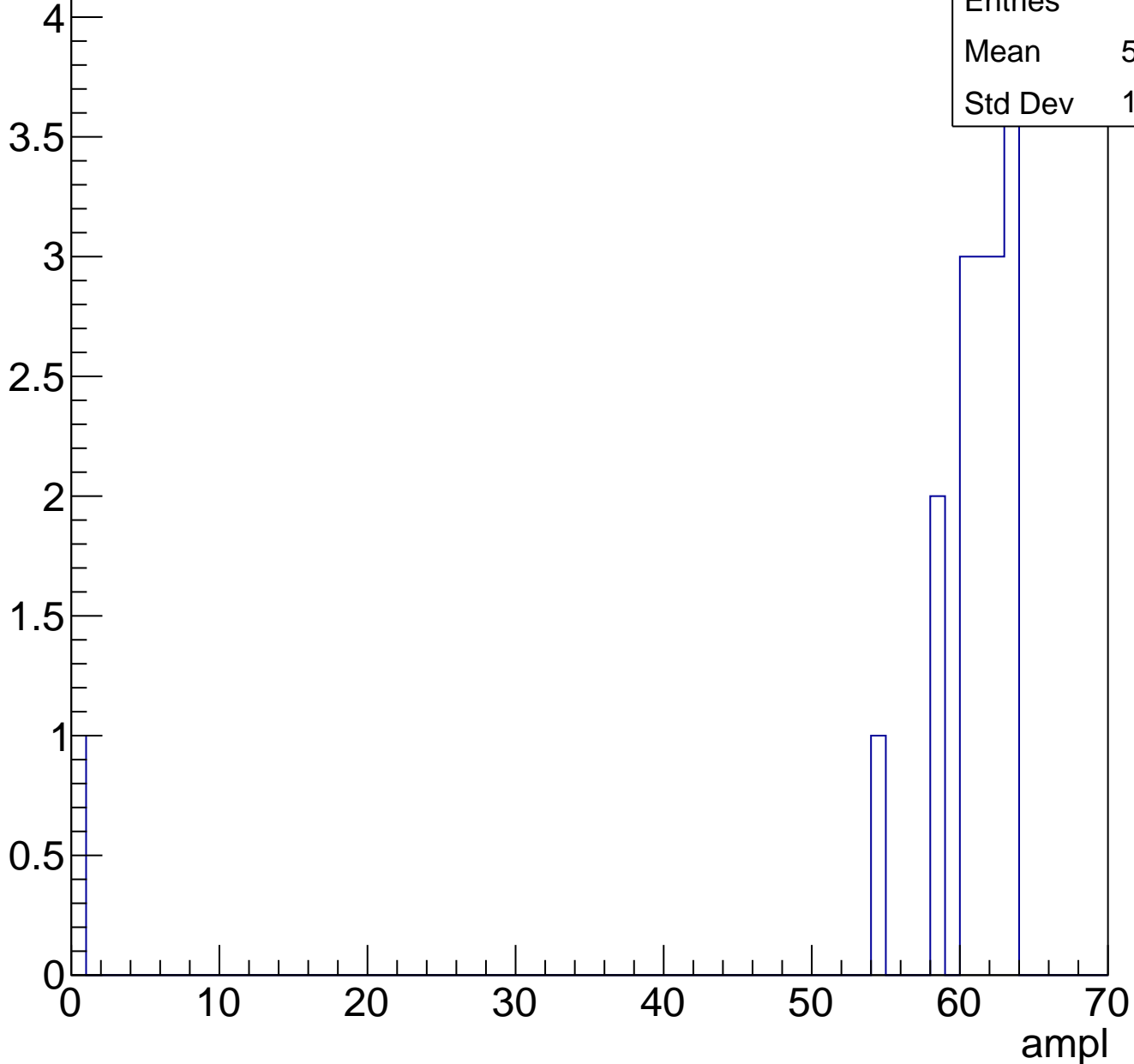
Entry



B1L102S, U17-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch83, adc0

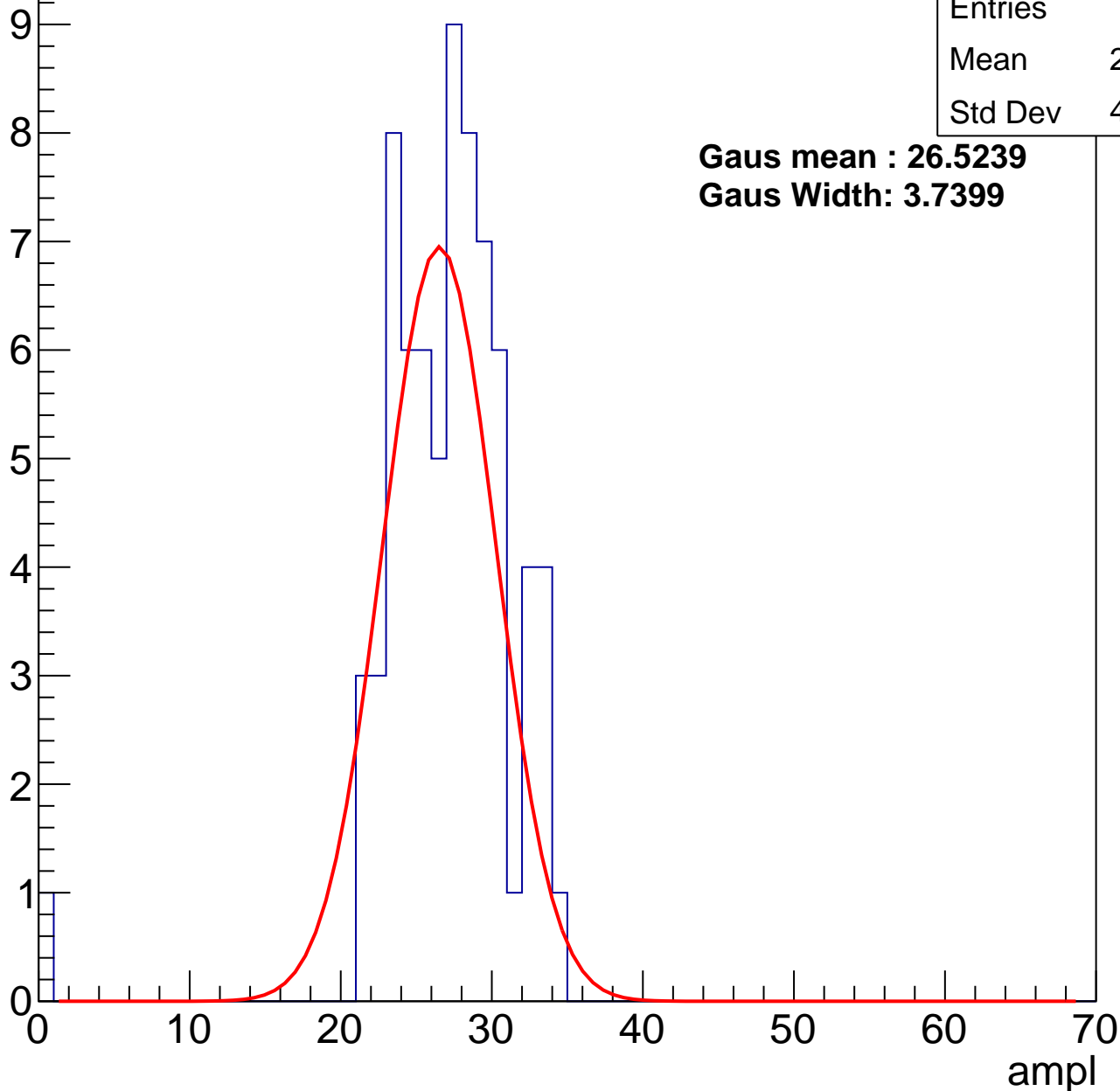
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	26.56
Std Dev	4.576

Gaus mean : 26.5239

Gaus Width: 3.7399



B1L102S, U17-ch83, adc1

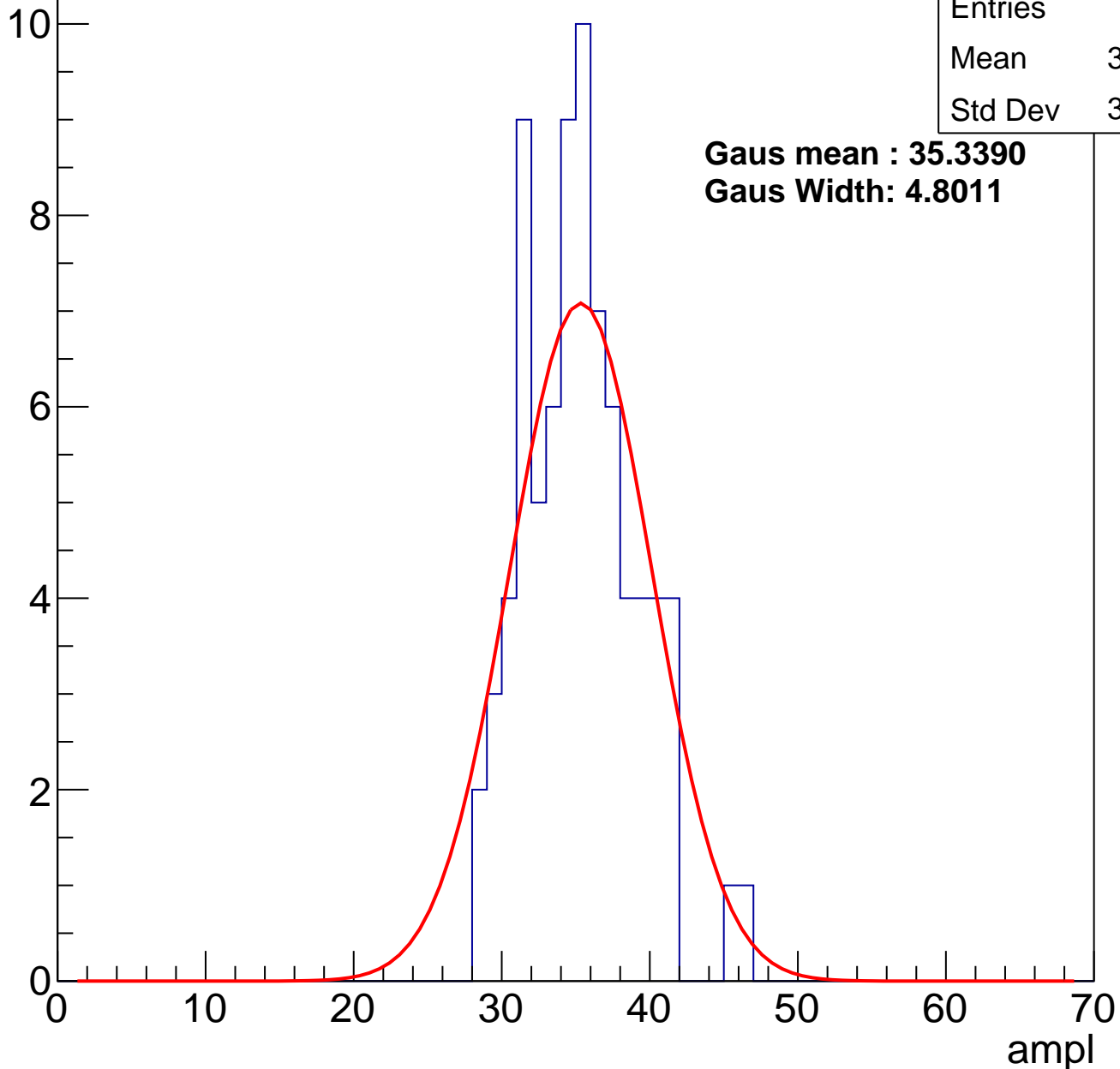
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	34.85
Std Dev	3.769

Gaus mean : 35.3390

Gaus Width: 4.8011

Entry



B1L102S, U17-ch83, adc2

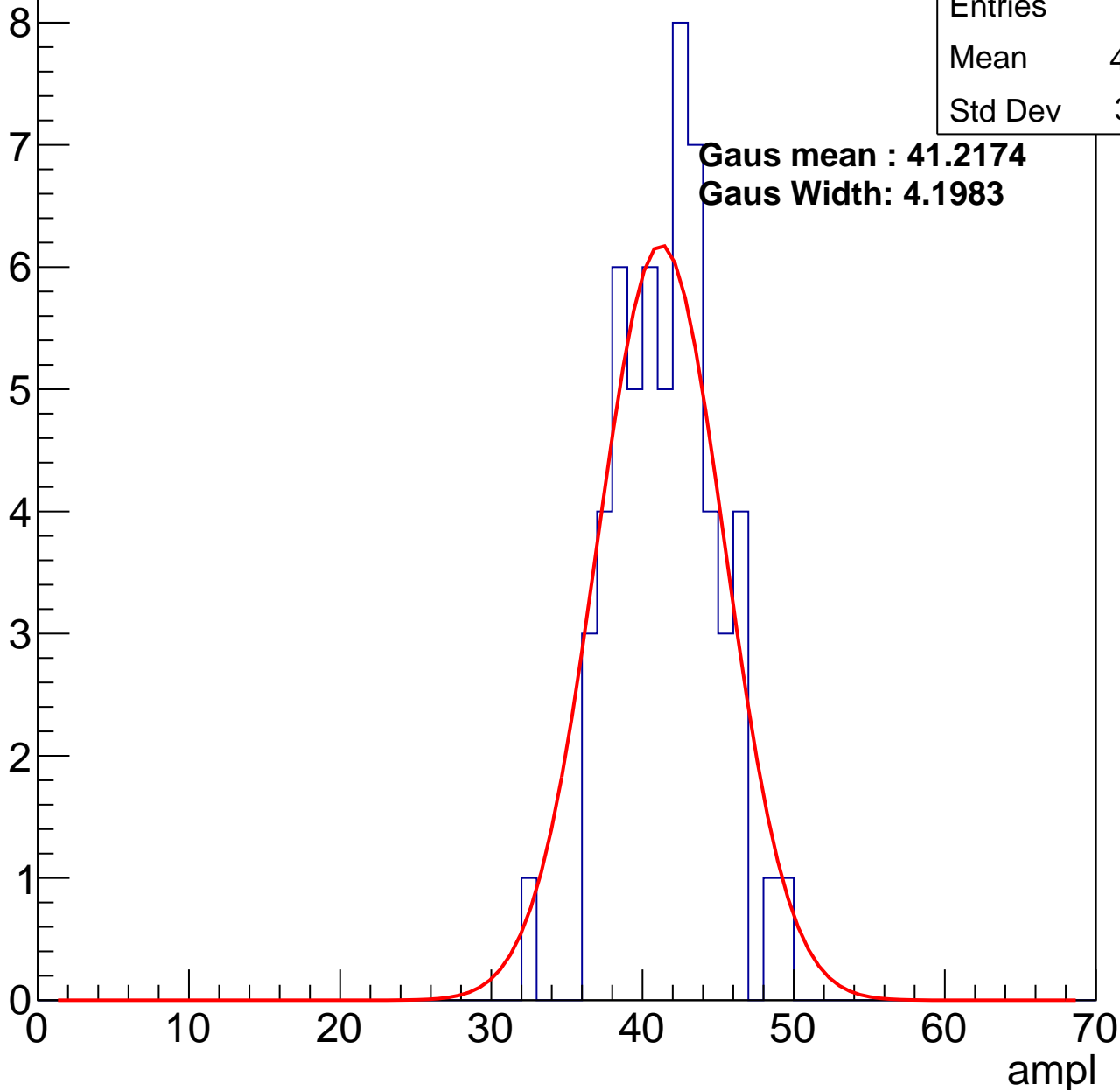
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	41.12
Std Dev	3.301

Gaus mean : 41.2174

Gaus Width: 4.1983

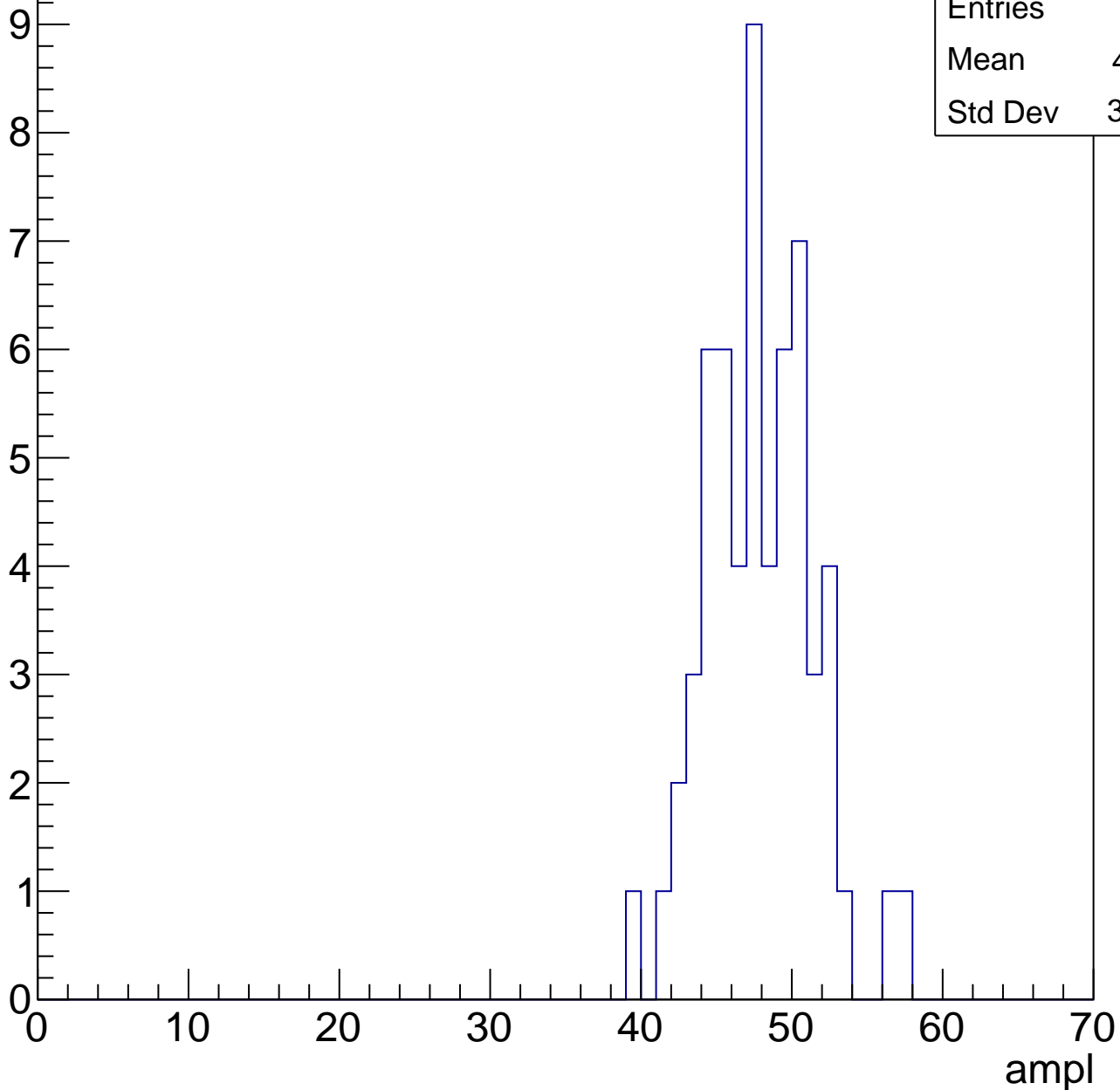


B1L102S, U17-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.41
Std Dev	3.508

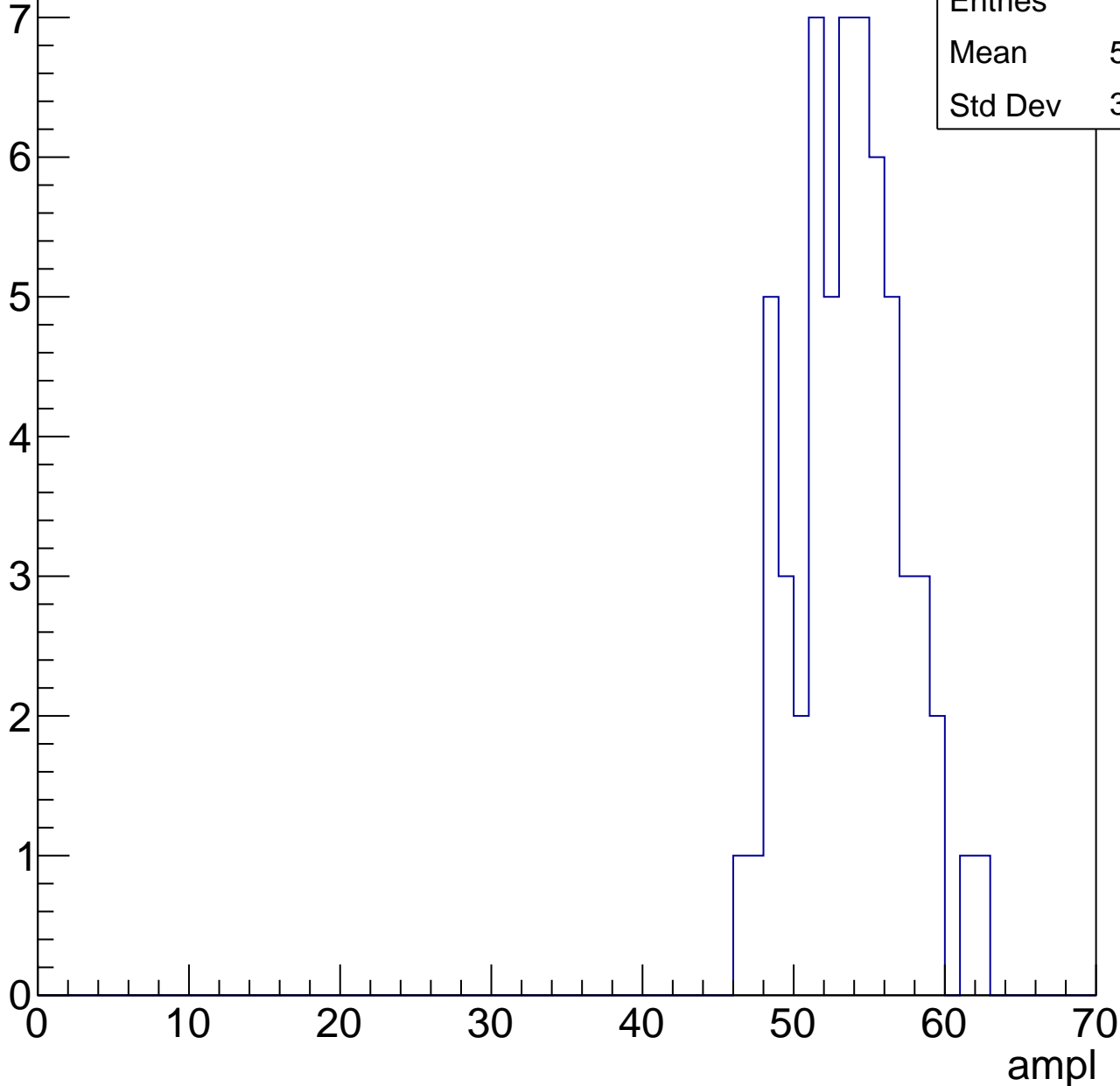


B1L102S, U17-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.25
Std Dev	3.496

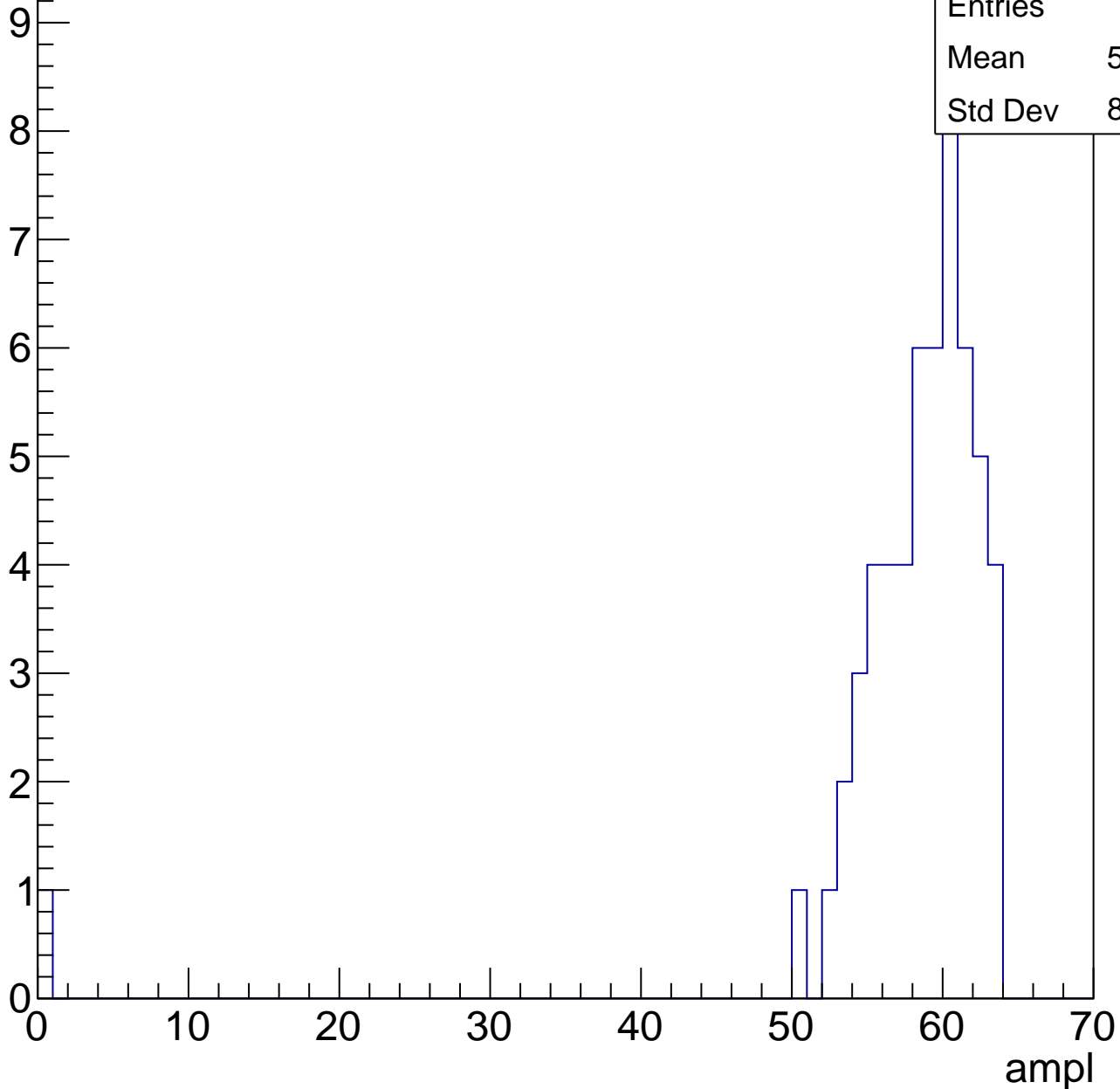


B1L102S, U17-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	57.36
Std Dev	8.314

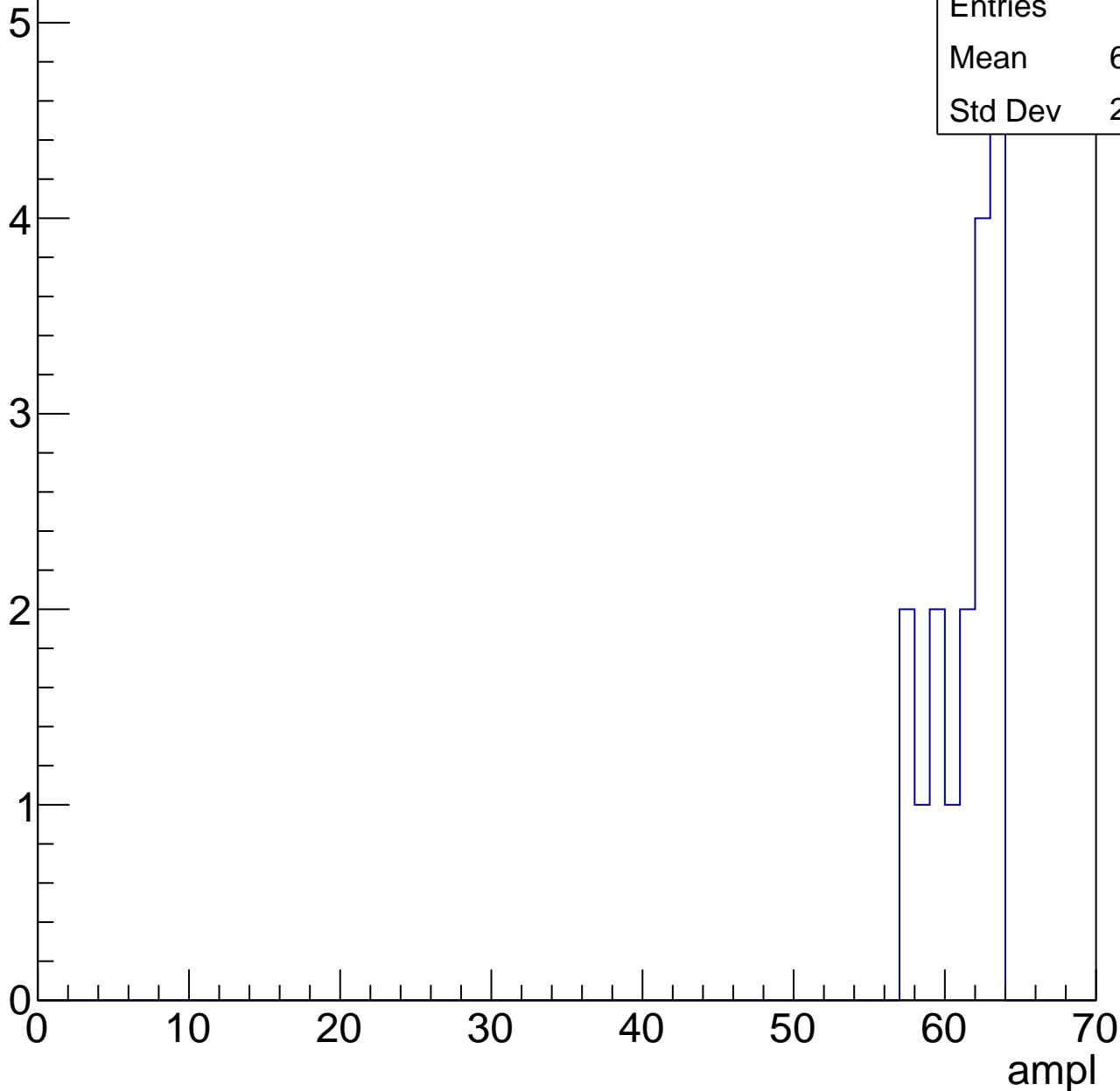


B1L102S, U17-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	60.88
Std Dev	2.083



B1L102S, U17-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch84, adc0

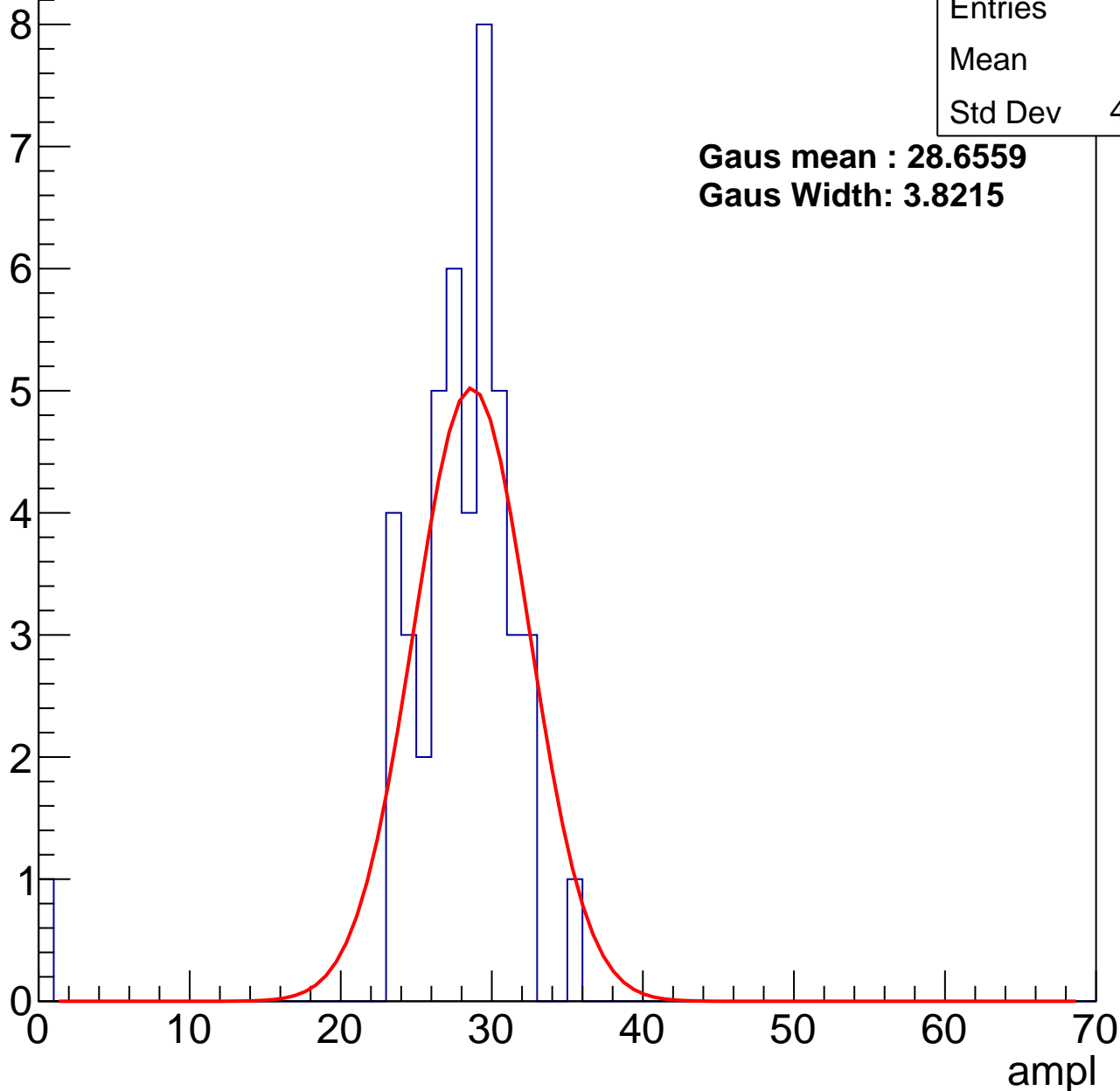
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	27.2
Std Dev	4.942

Gaus mean : 28.6559

Gaus Width: 3.8215



B1L102S, U17-ch84, adc1

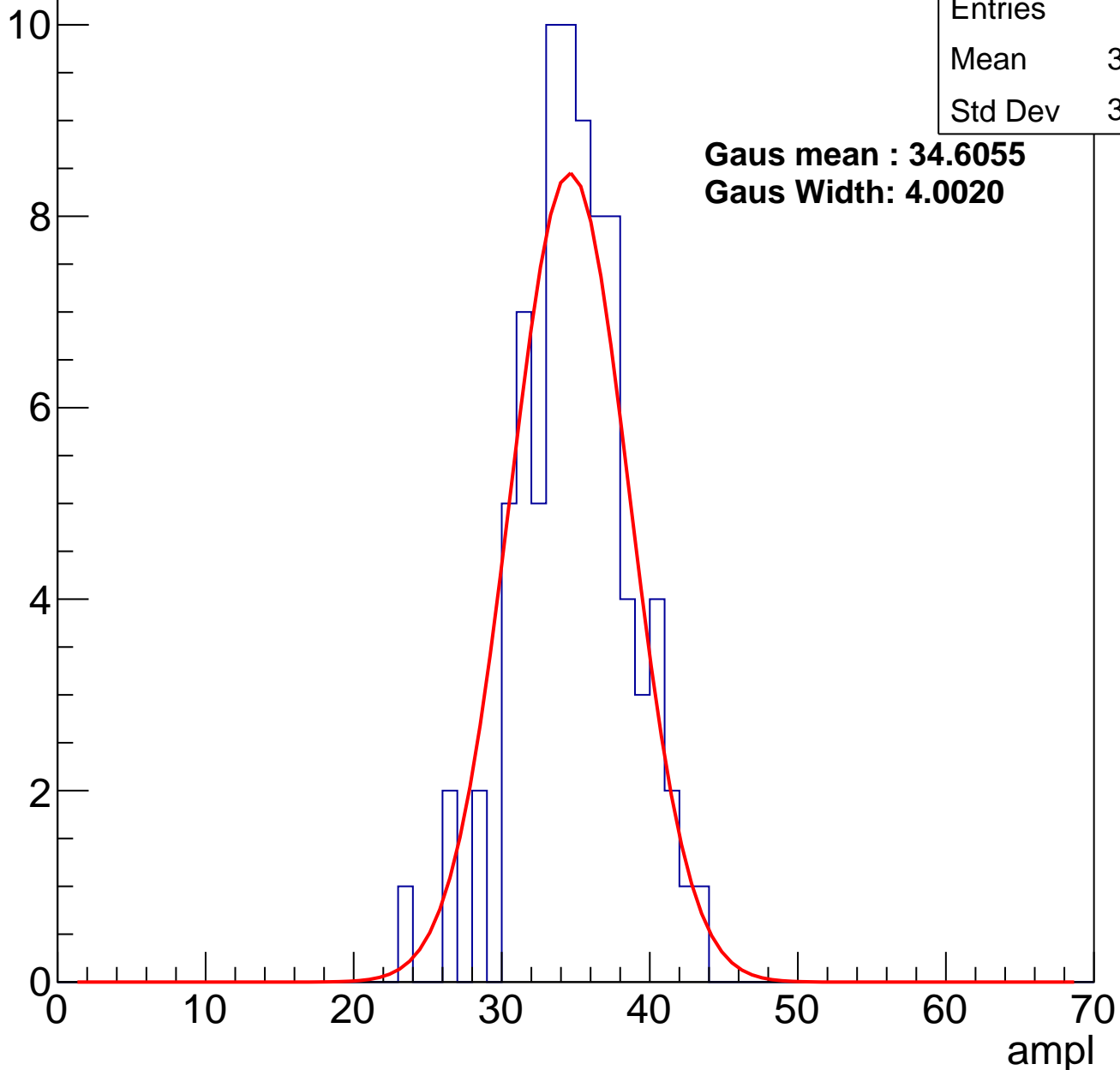
calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	34.43
Std Dev	3.693

Gaus mean : 34.6055

Gaus Width: 4.0020

Entry



B1L102S, U17-ch84, adc2

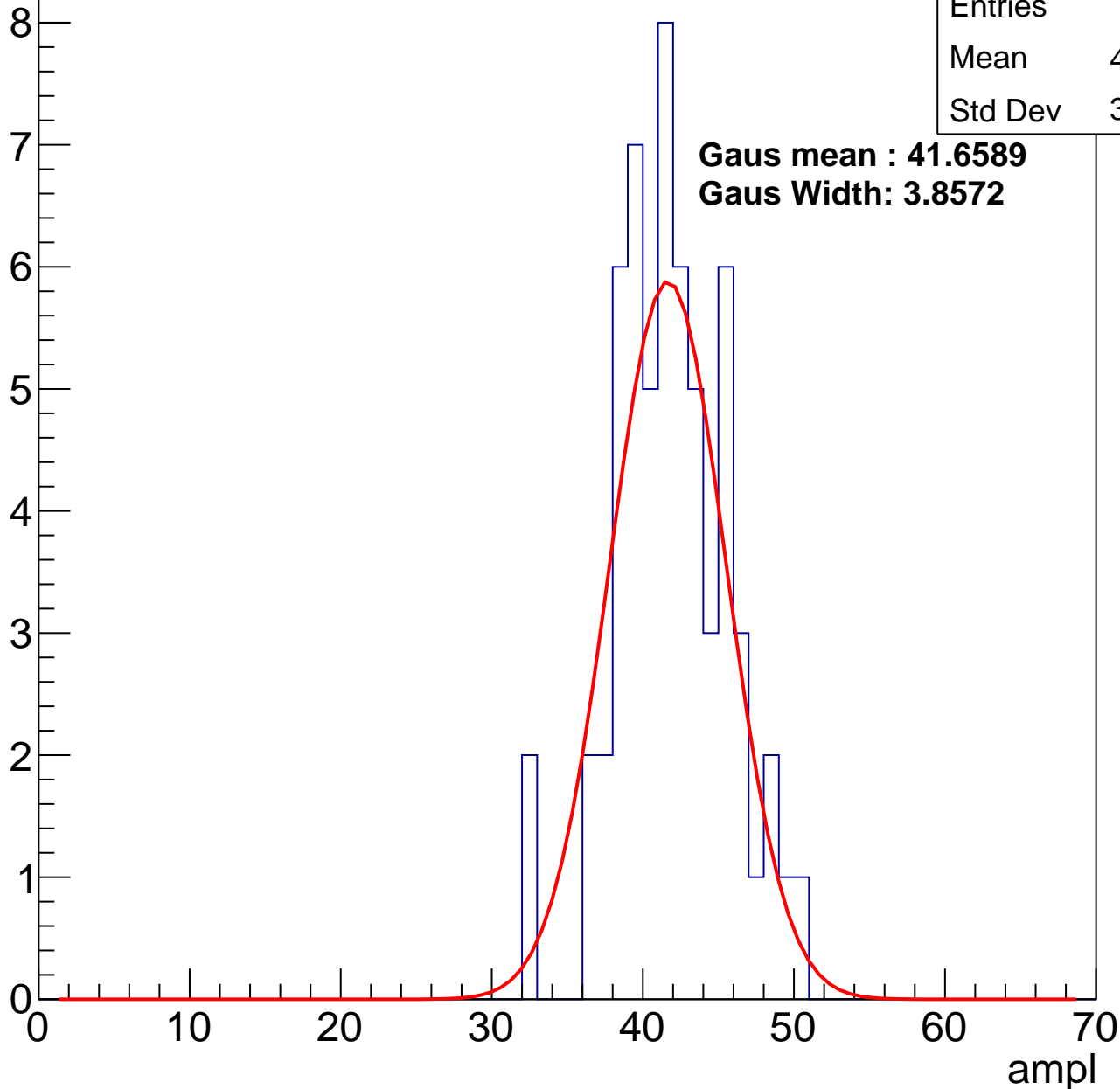
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.47
Std Dev	3.699

Gaus mean : 41.6589

Gaus Width: 3.8572

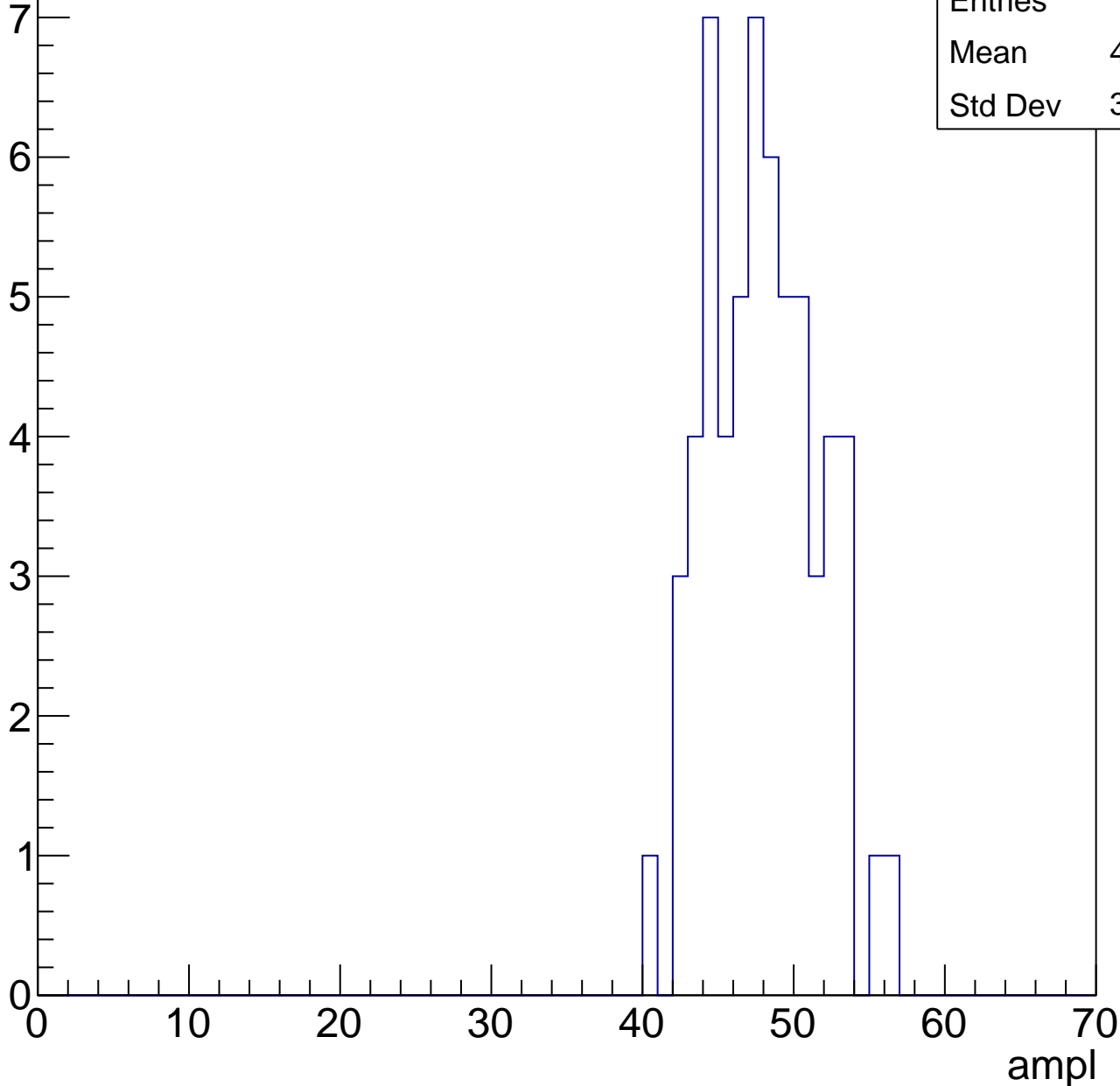


B1L102S, U17-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.53
Std Dev	3.566

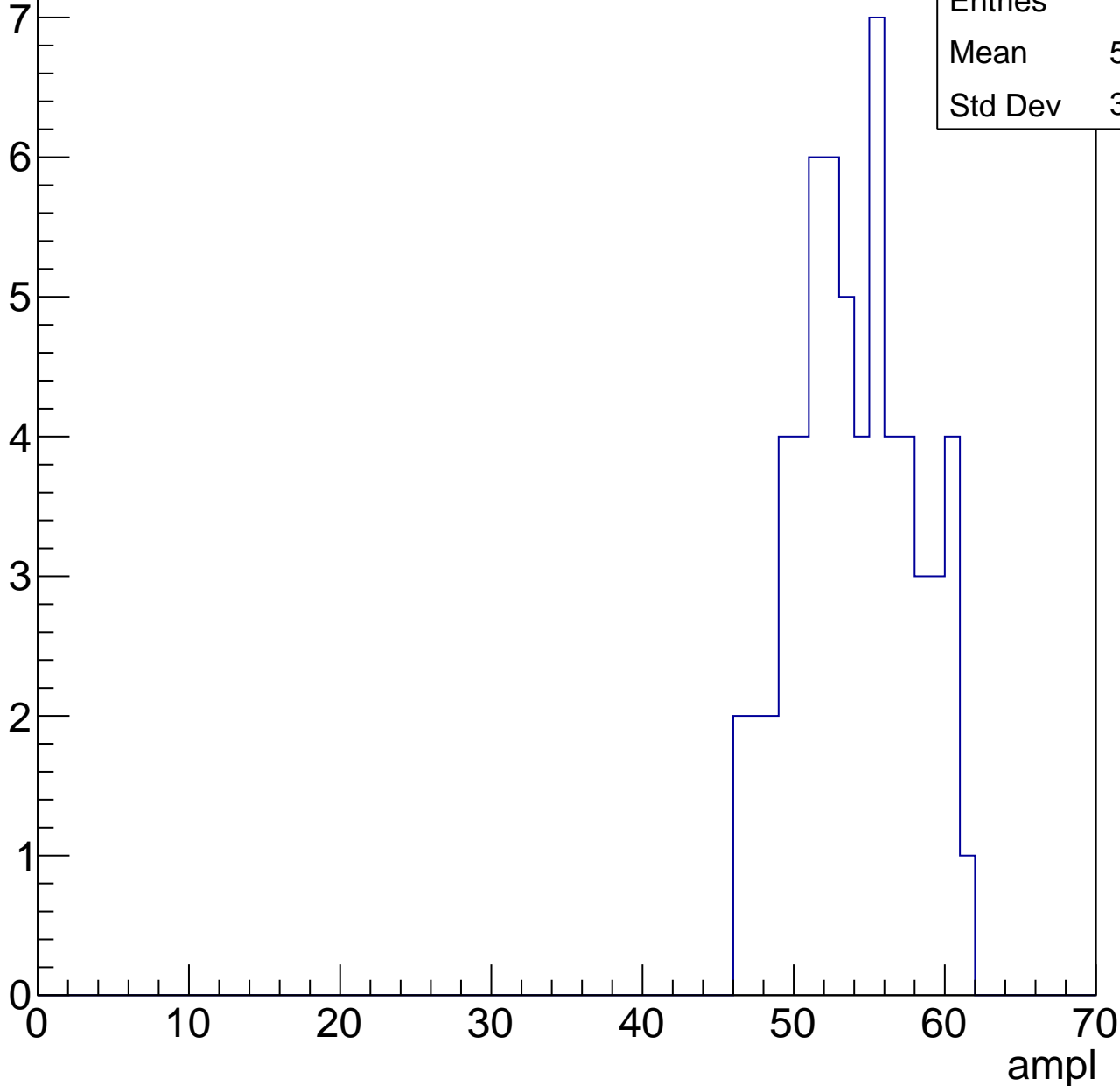


B1L102S, U17-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.54
Std Dev	3.856

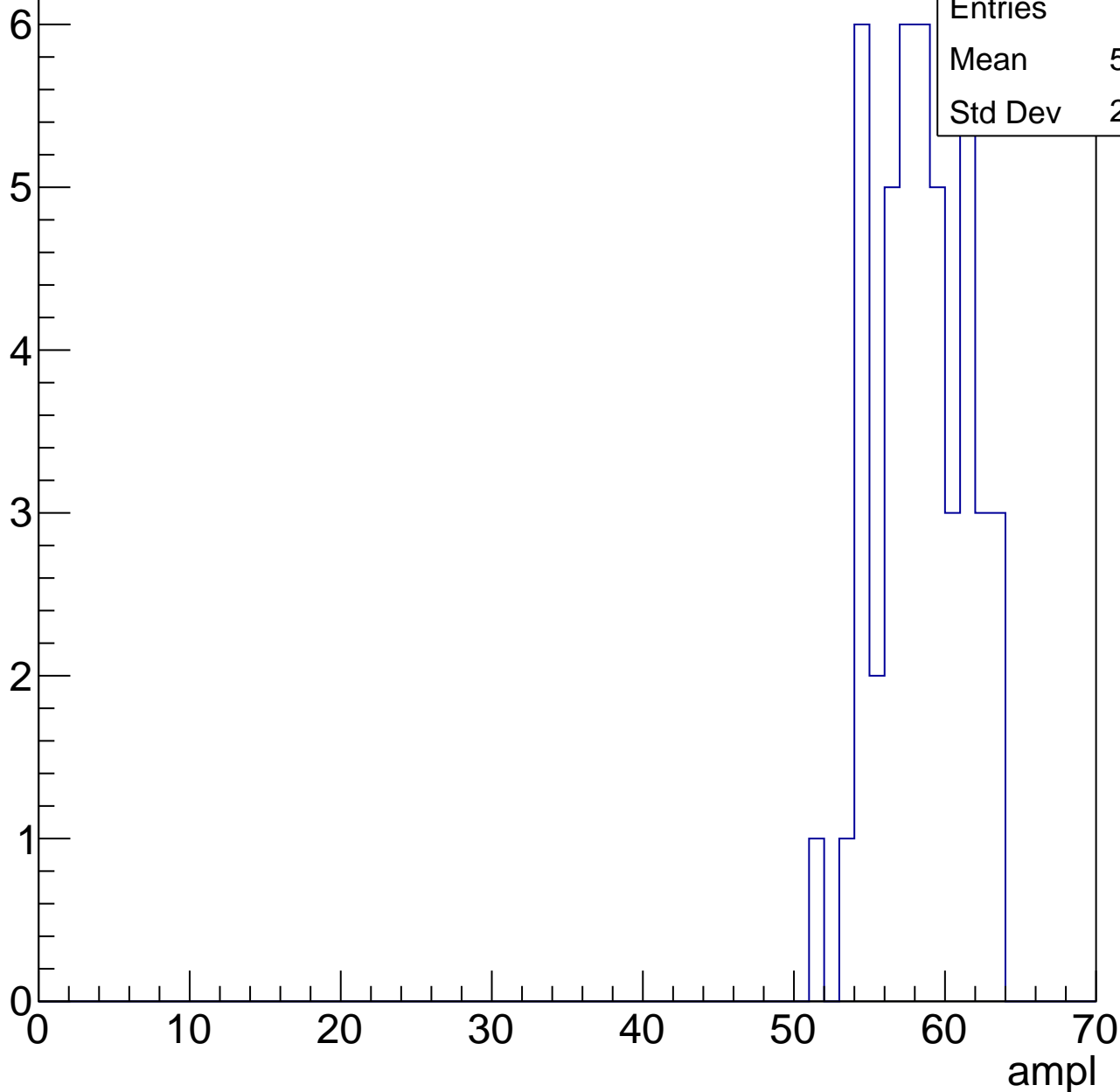


B1L102S, U17-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.96
Std Dev	2.946

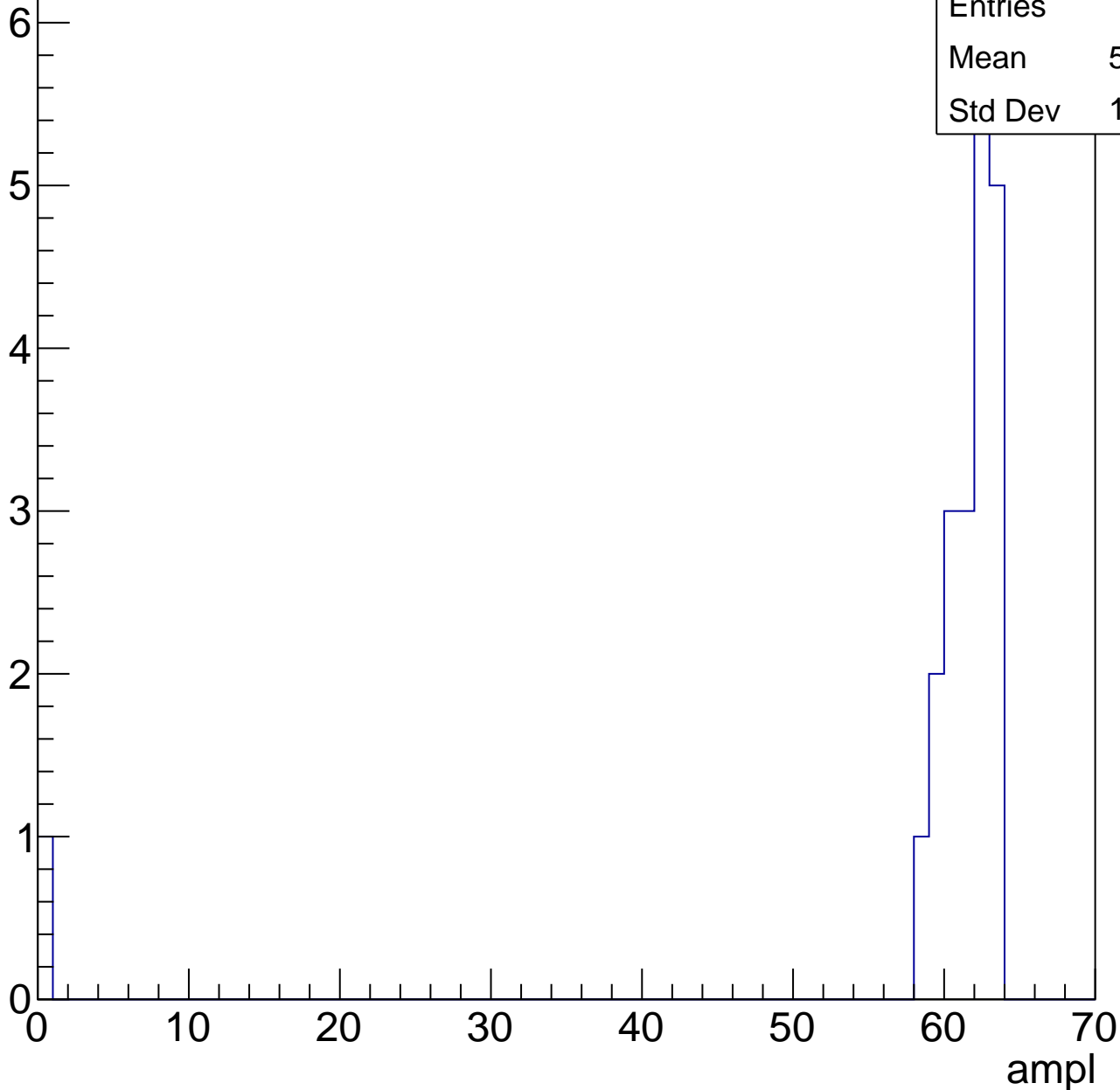


B1L102S, U17-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

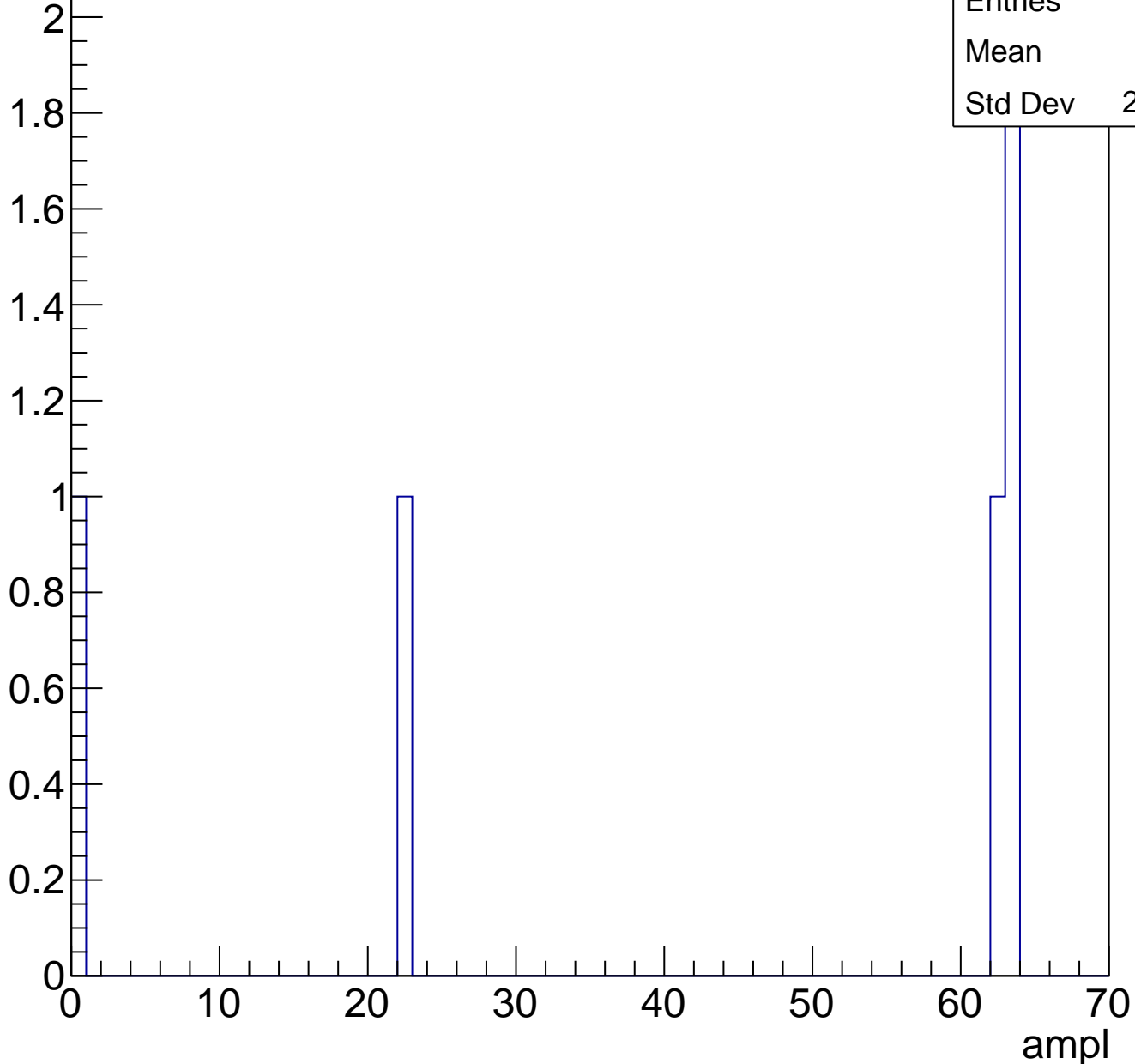
Entries	21
Mean	58.38
Std Dev	13.13



B1L102S, U17-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	42
Std Dev	26.25

B1L102S, U17-ch85, adc0

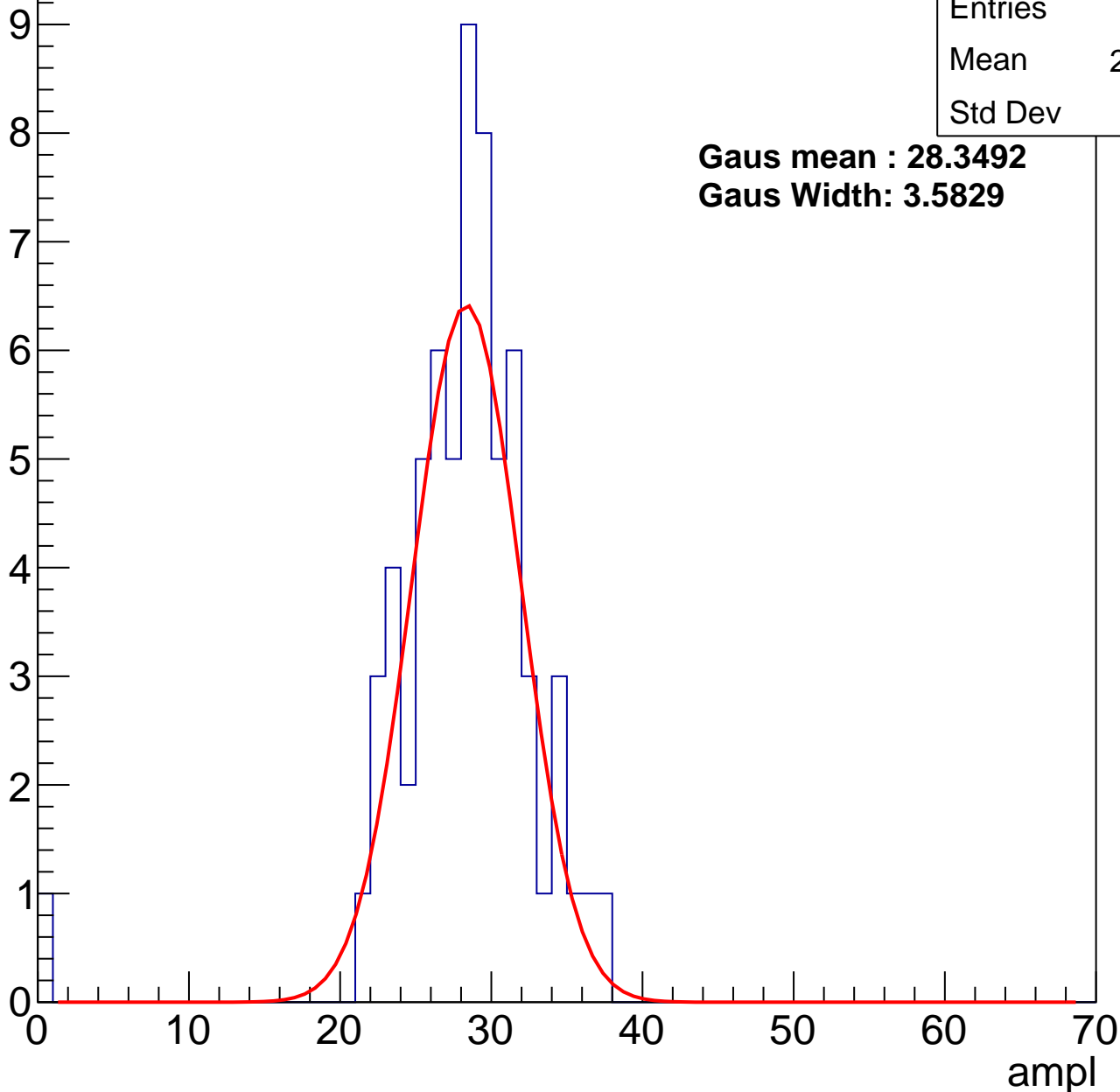
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.72
Std Dev	4.96

Gaus mean : 28.3492

Gaus Width: 3.5829



B1L102S, U17-ch85, adc1

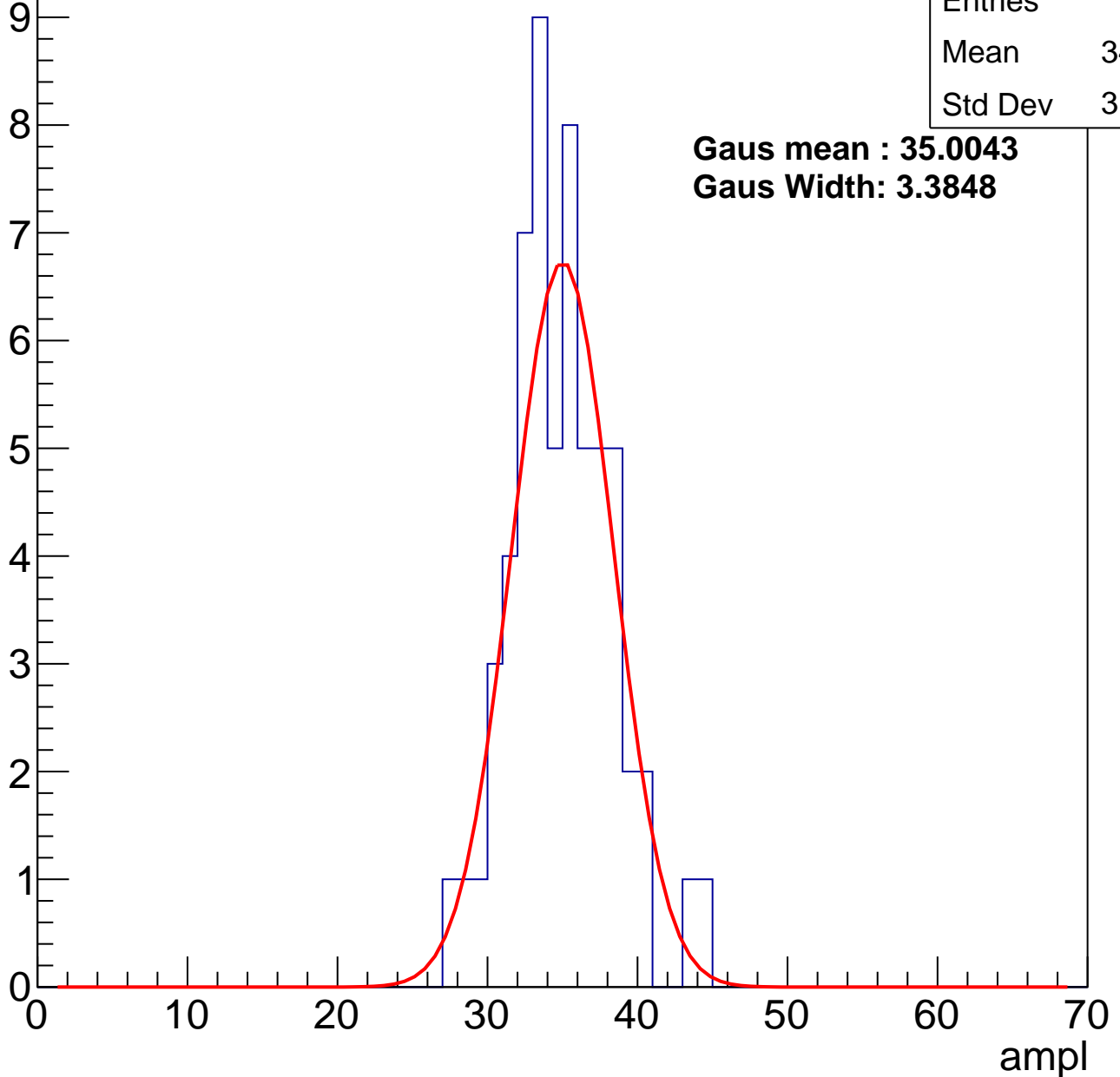
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.48
Std Dev	3.349

Gaus mean : 35.0043

Gaus Width: 3.3848



B1L102S, U17-ch85, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	40.78
Std Dev	3.185

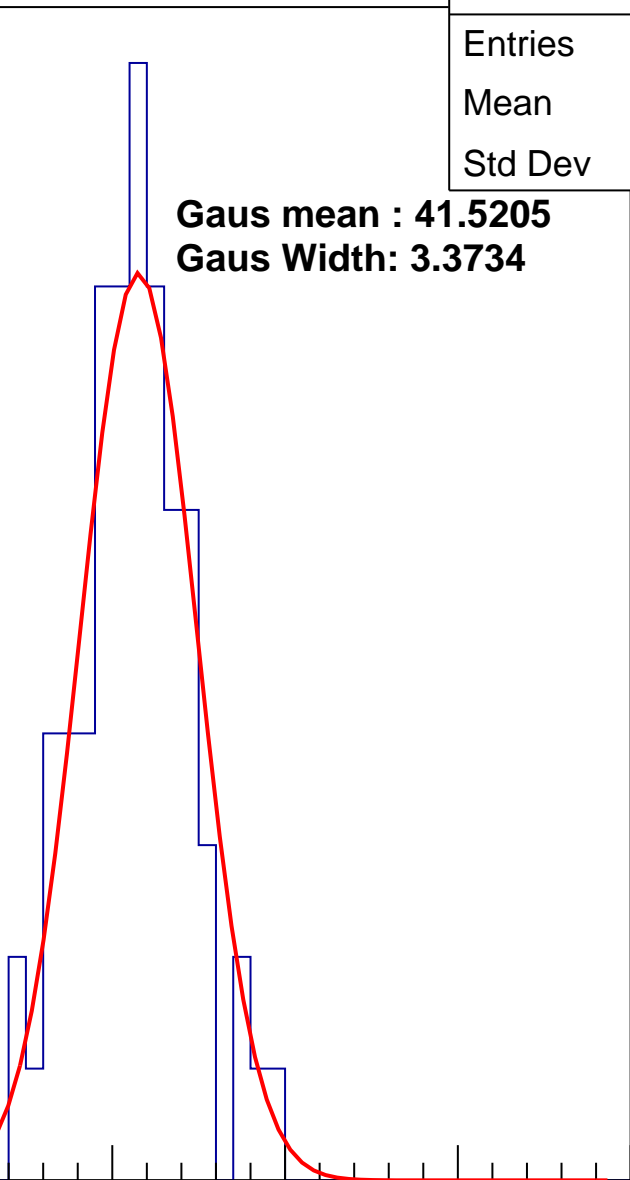
Gaus mean : 41.5205

Gaus Width: 3.3734

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

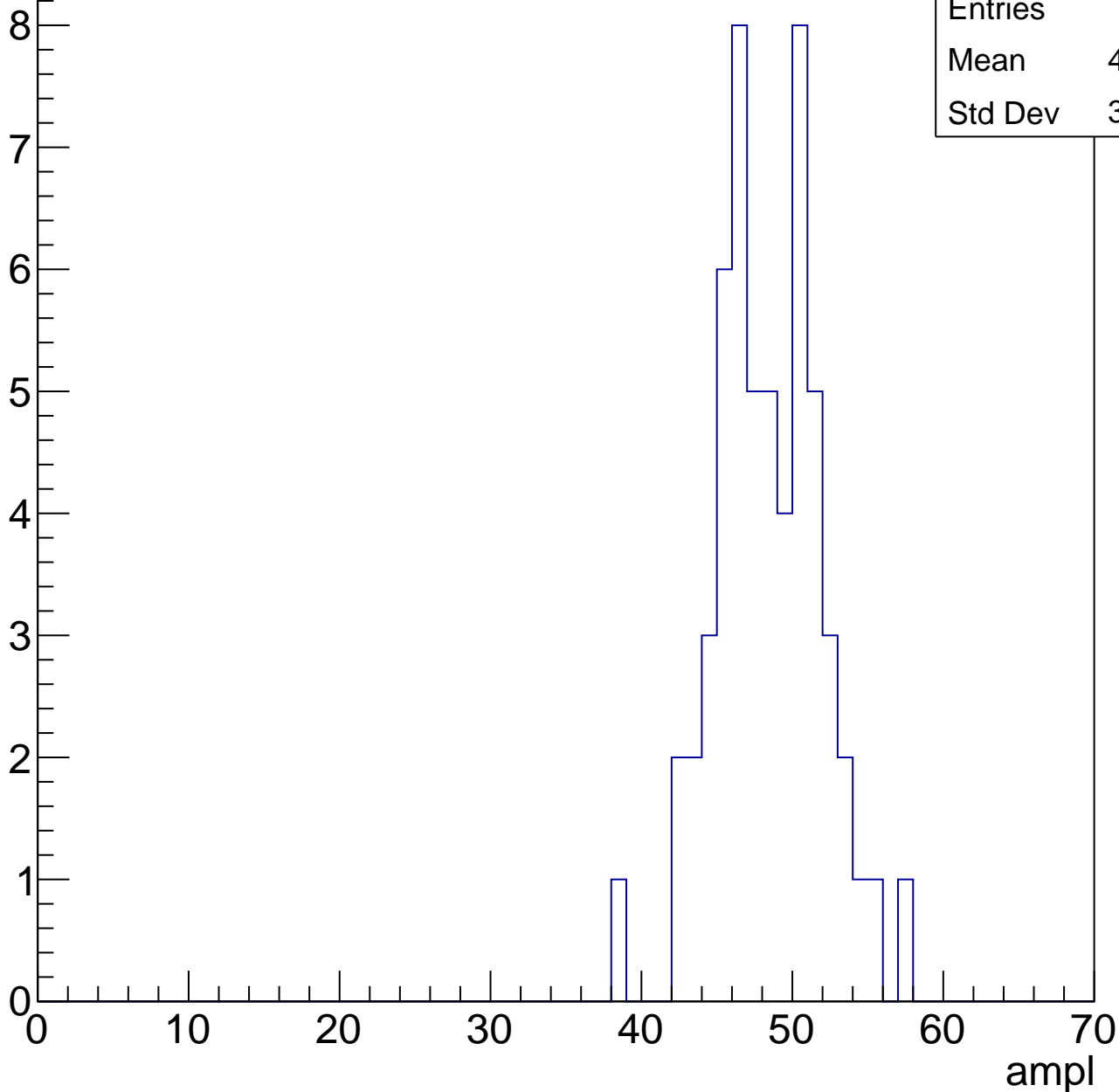


B1L102S, U17-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	47.93
Std Dev	3.519

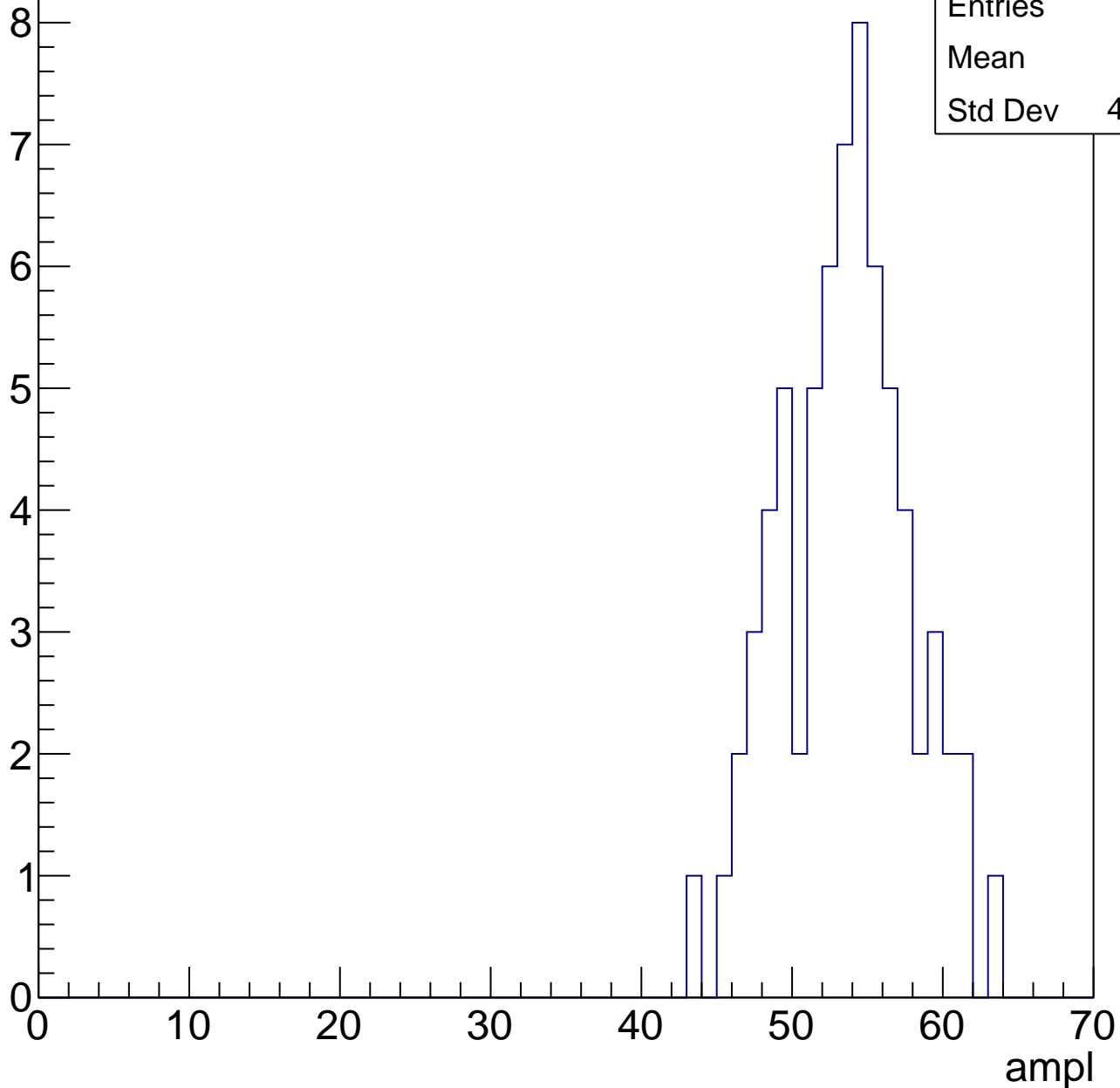


B1L102S, U17-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

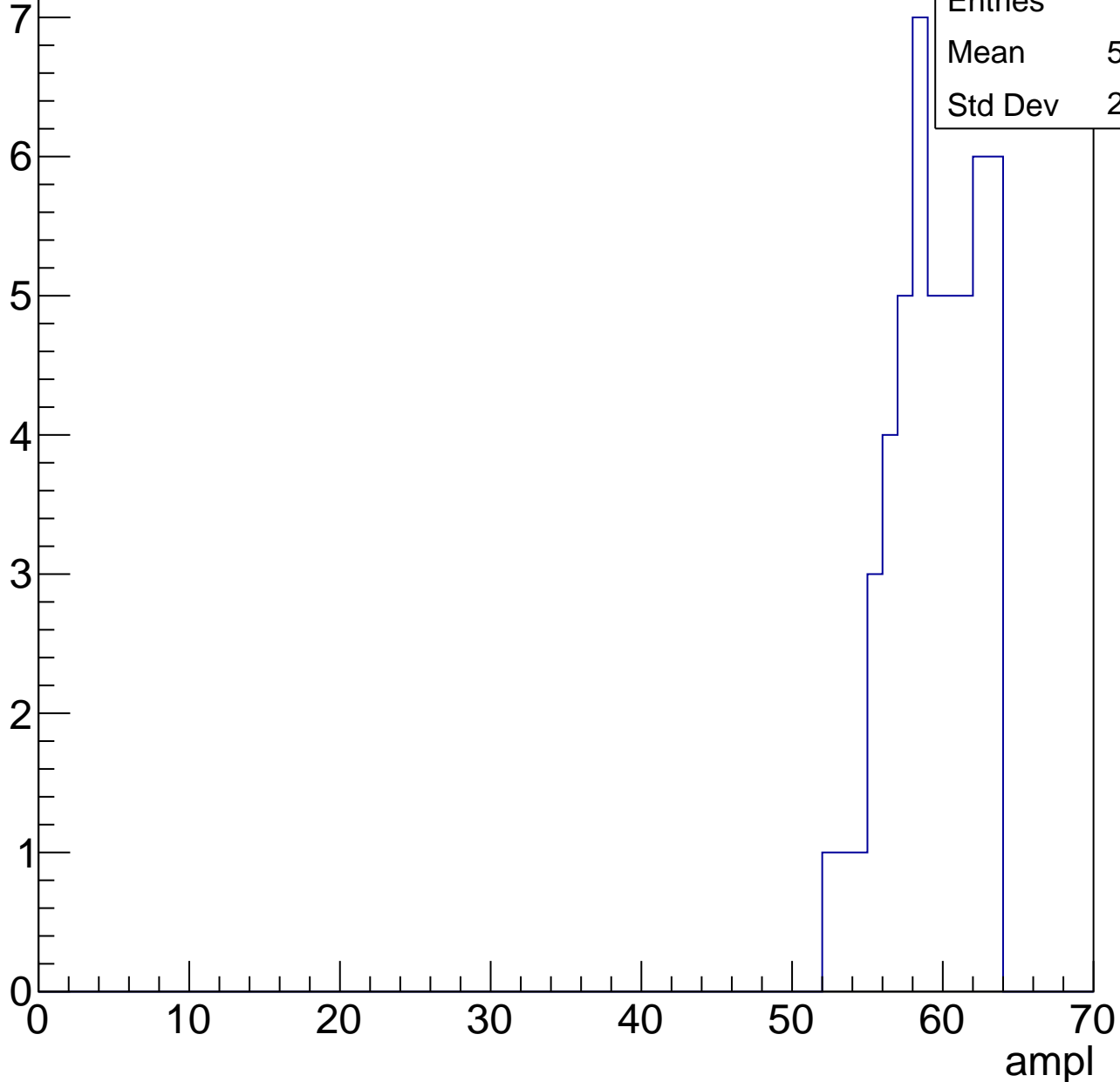
Entries	69
Mean	53.1
Std Dev	4.202



B1L102S, U17-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

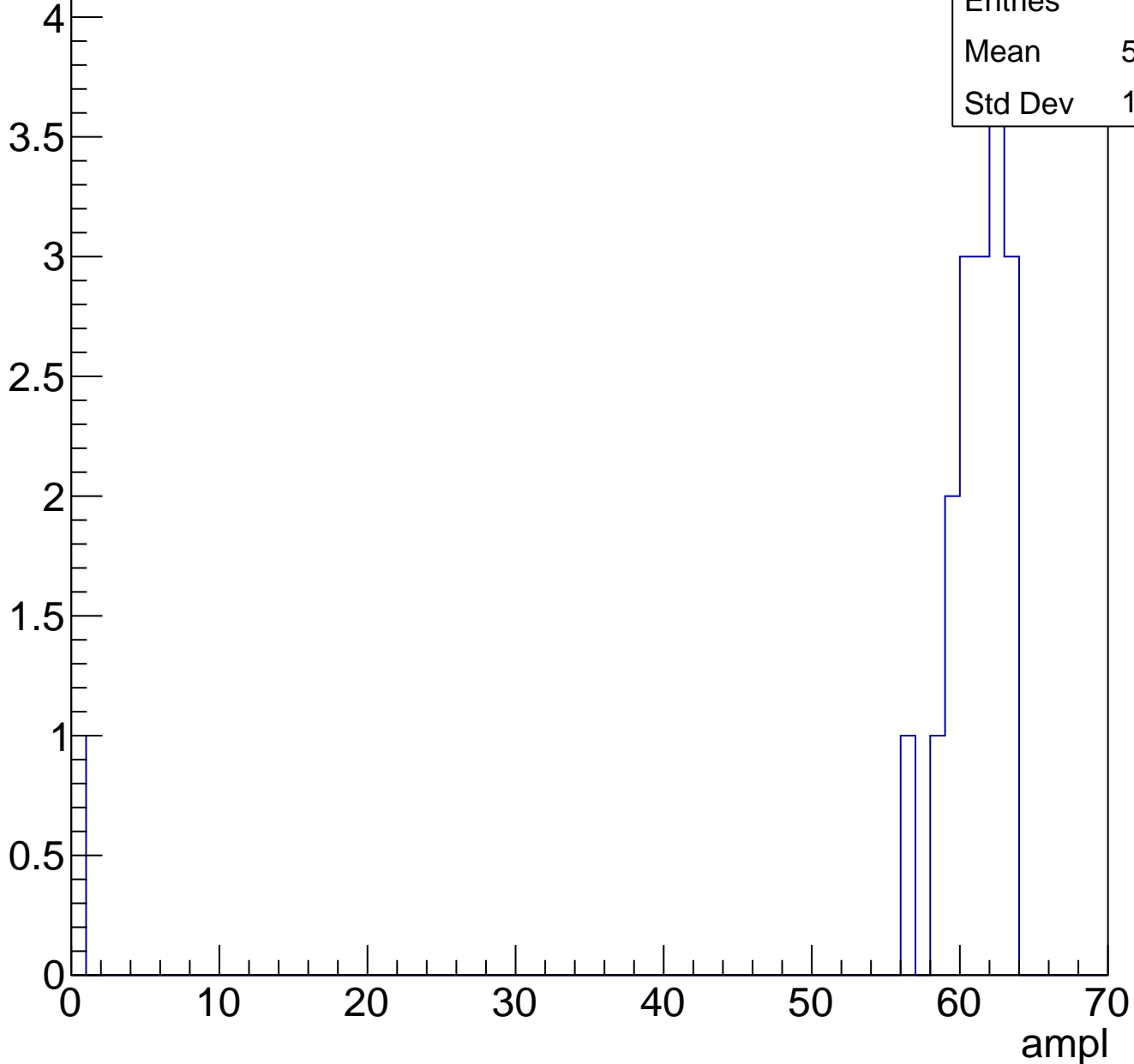


Entries	49
Mean	58.96
Std Dev	2.843

B1L102S, U17-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch86, adc0

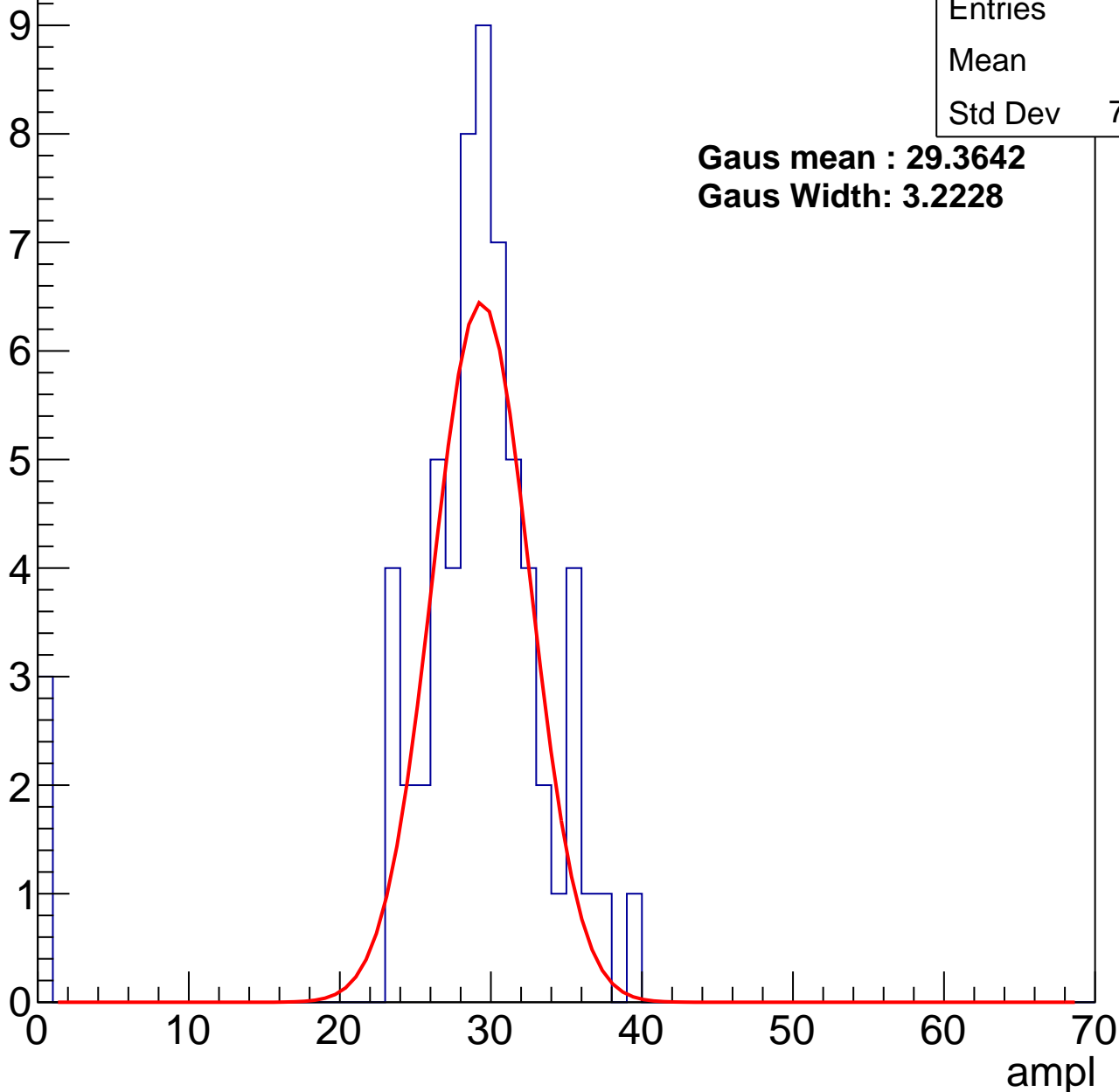
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.9
Std Dev	7.149

Gaus mean : 29.3642

Gaus Width: 3.2228



B1L102S, U17-ch86, adc1

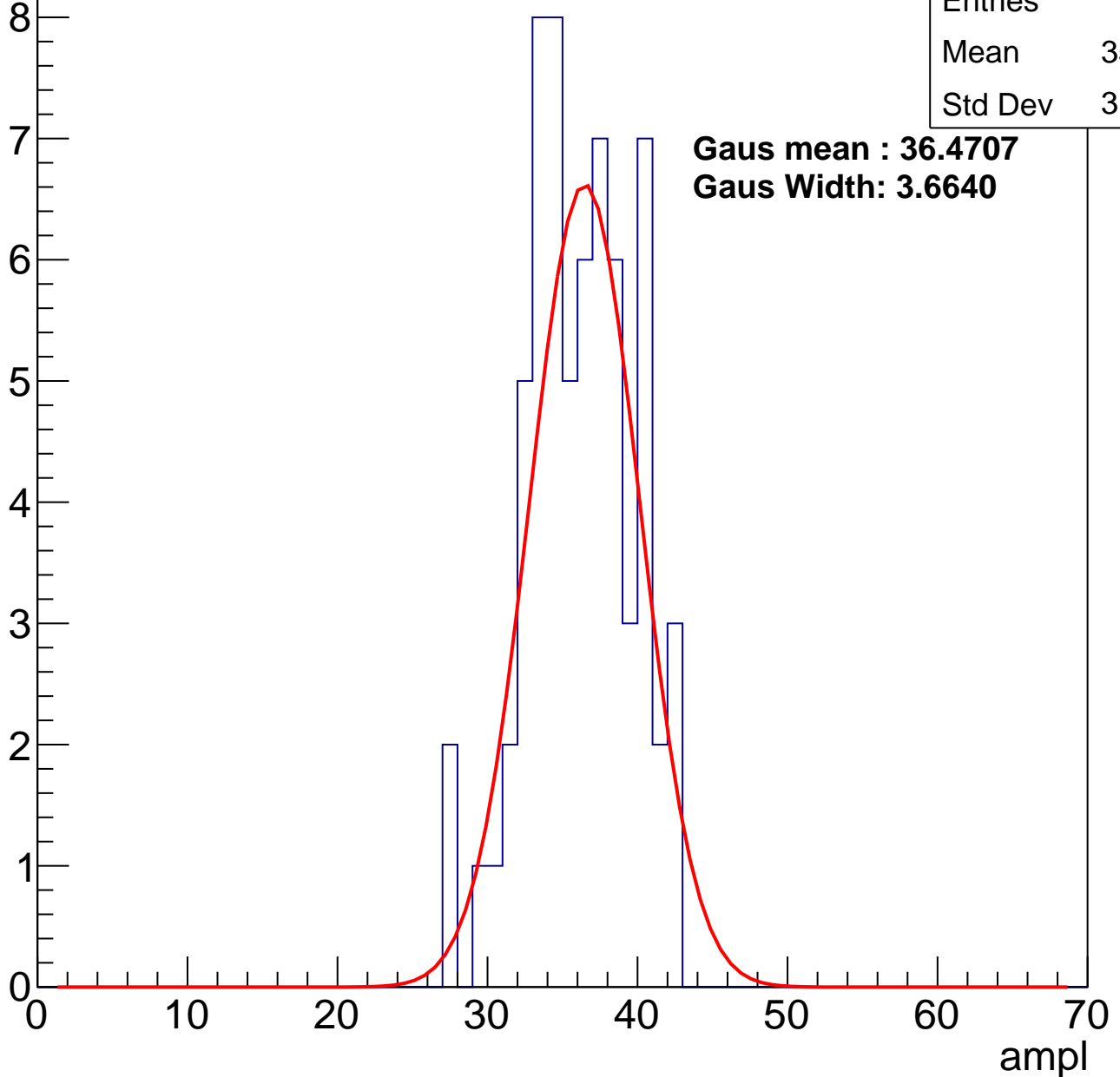
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.67
Std Dev	3.496

Gaus mean : 36.4707

Gaus Width: 3.6640



B1L102S, U17-ch86, adc2

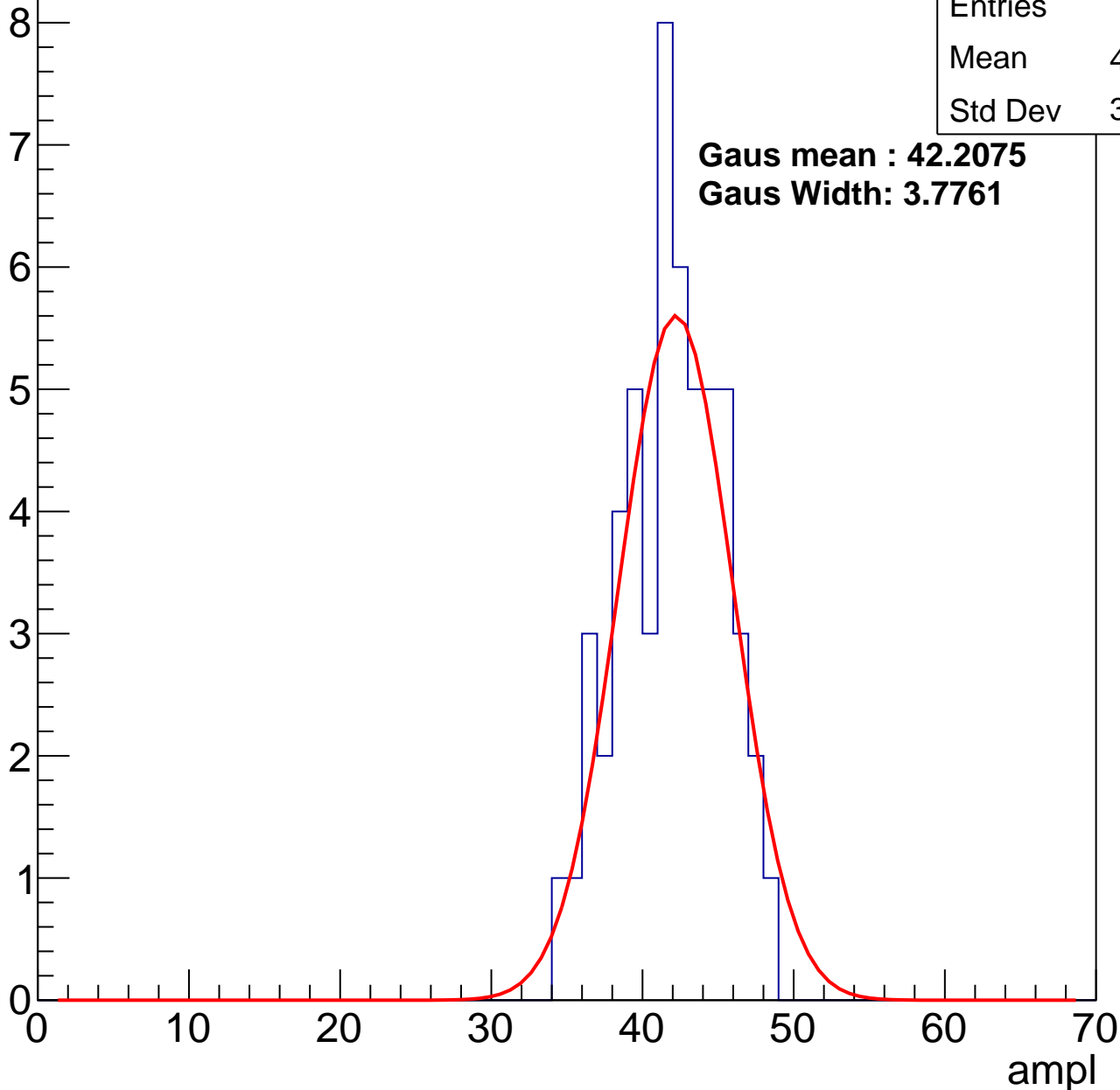
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.44
Std Dev	3.298

Gaus mean : 42.2075

Gaus Width: 3.7761

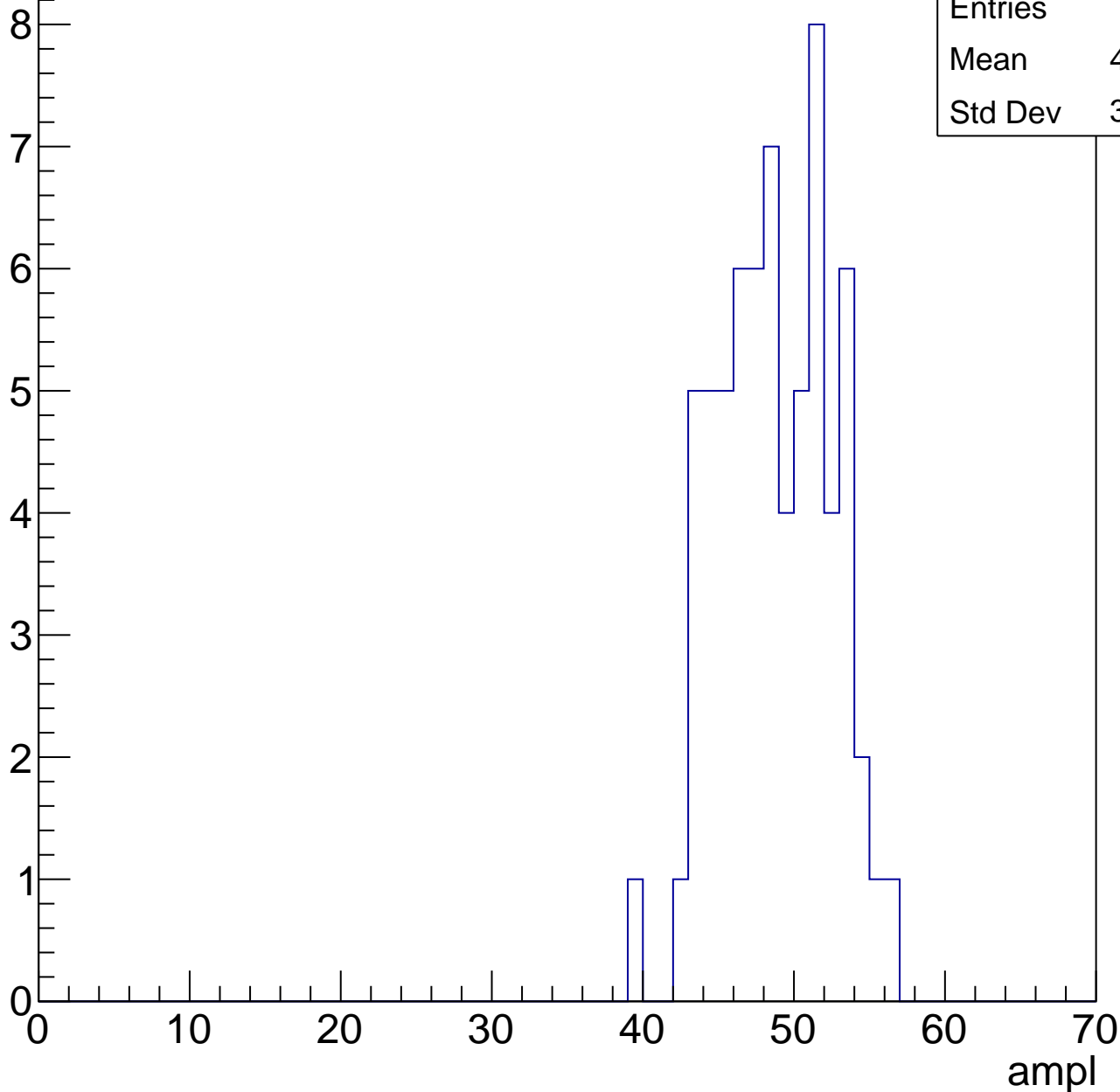


B1L102S, U17-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.27
Std Dev	3.639

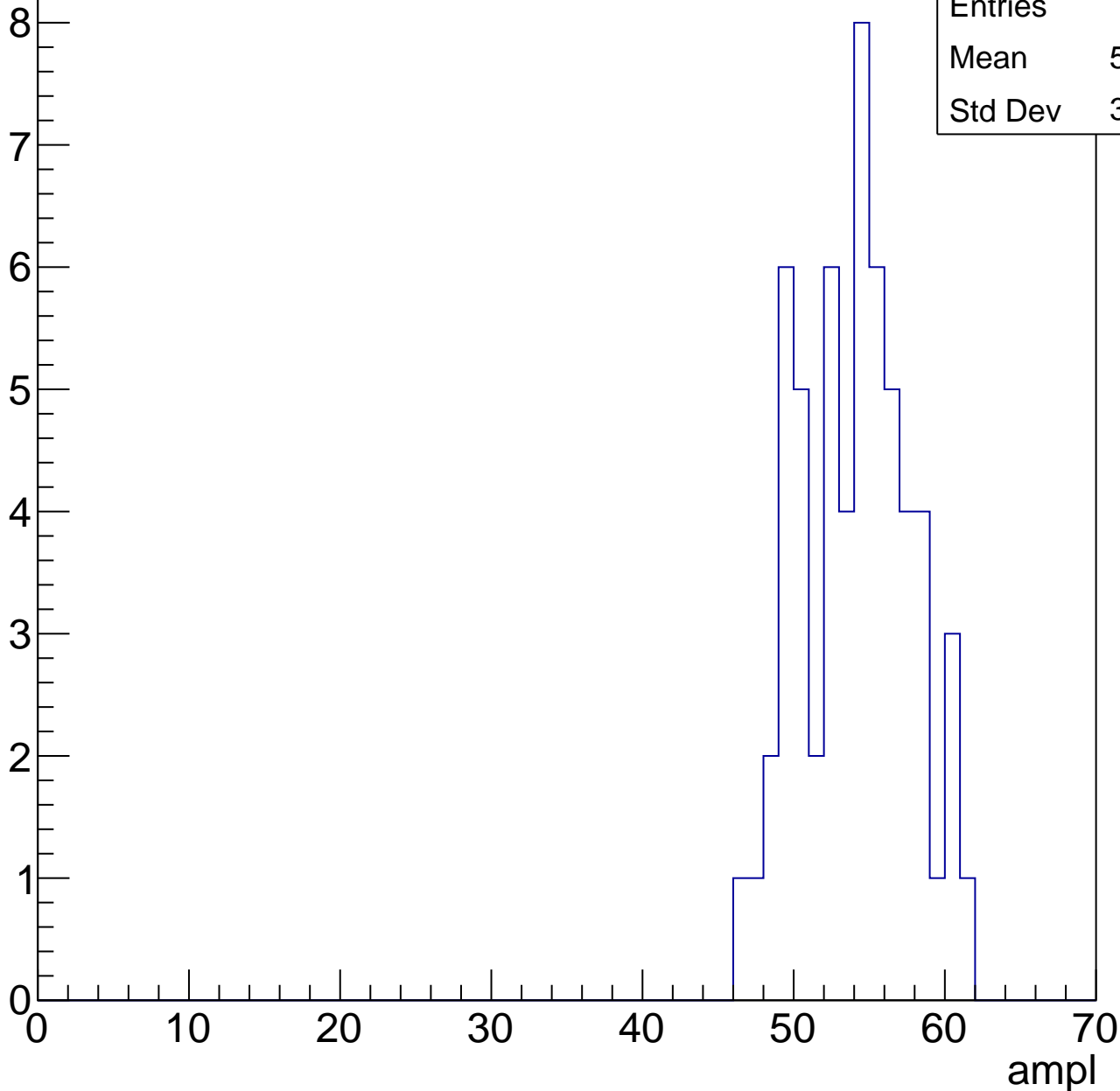


B1L102S, U17-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.58
Std Dev	3.585

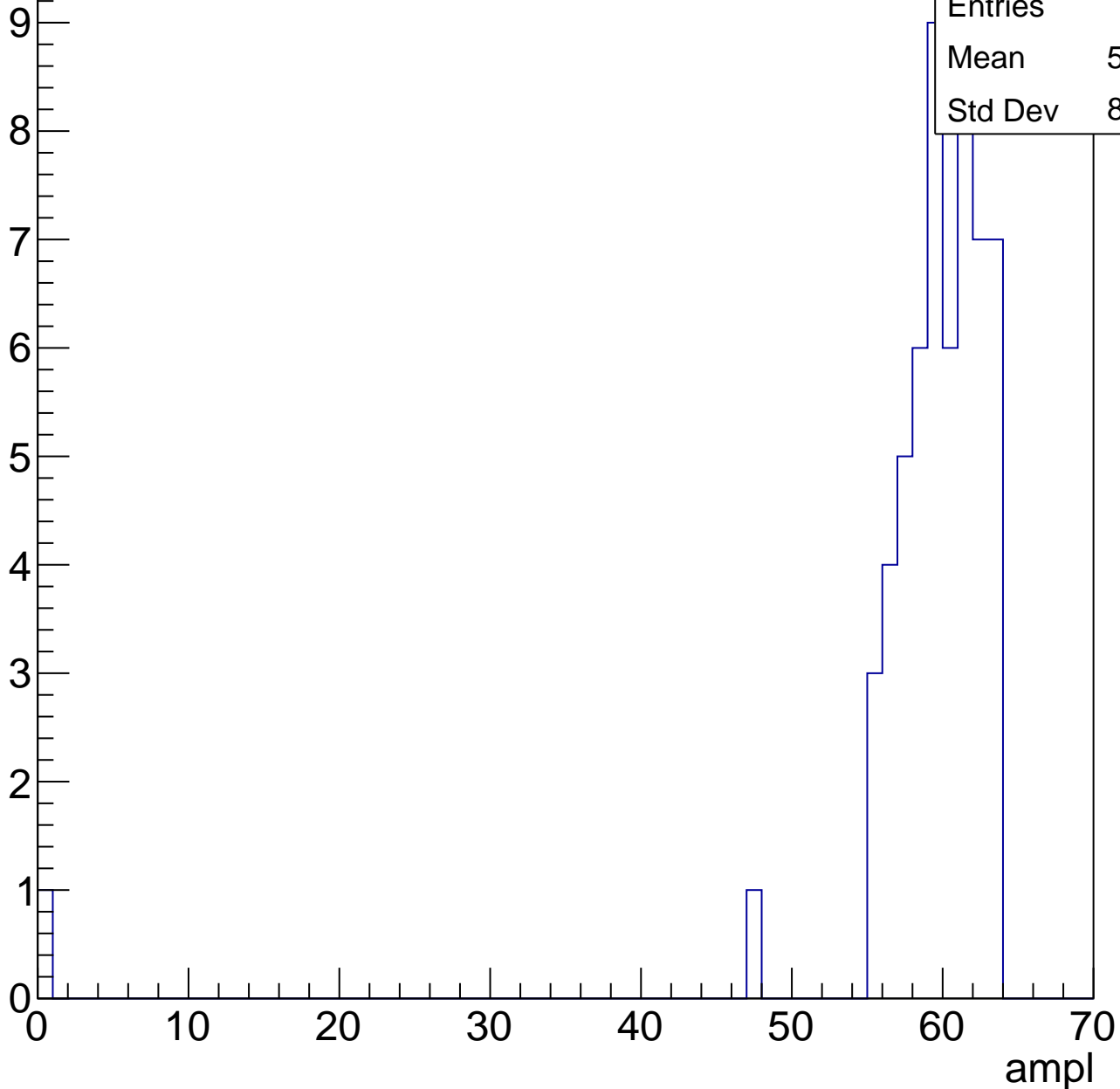


B1L102S, U17-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

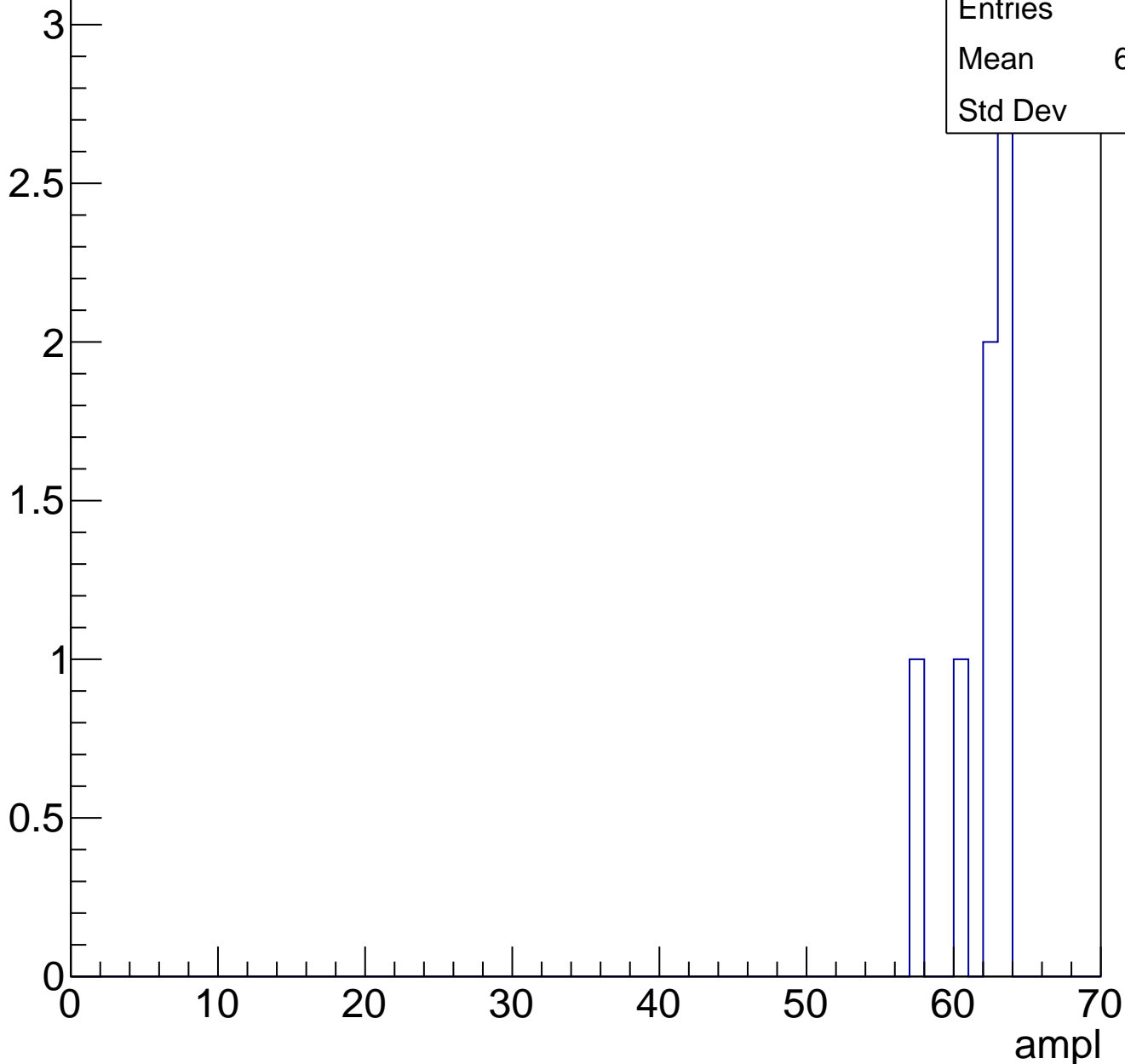
Entries	58
Mean	58.34
Std Dev	8.229



B1L102S, U17-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L102S, U17-ch87, adc0

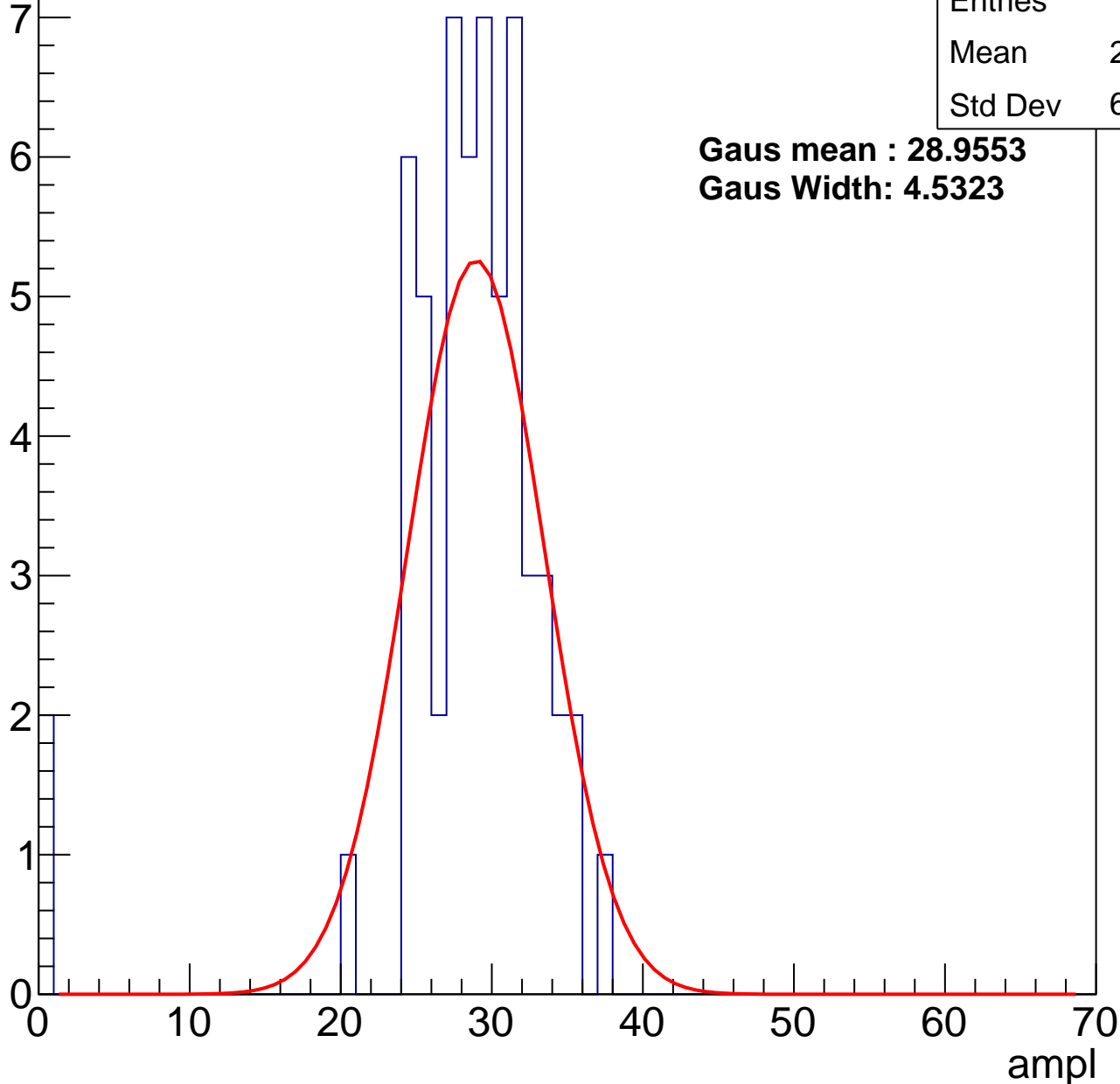
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.76
Std Dev	6.174

Gaus mean : 28.9553

Gaus Width: 4.5323



B1L102S, U17-ch87, adc1

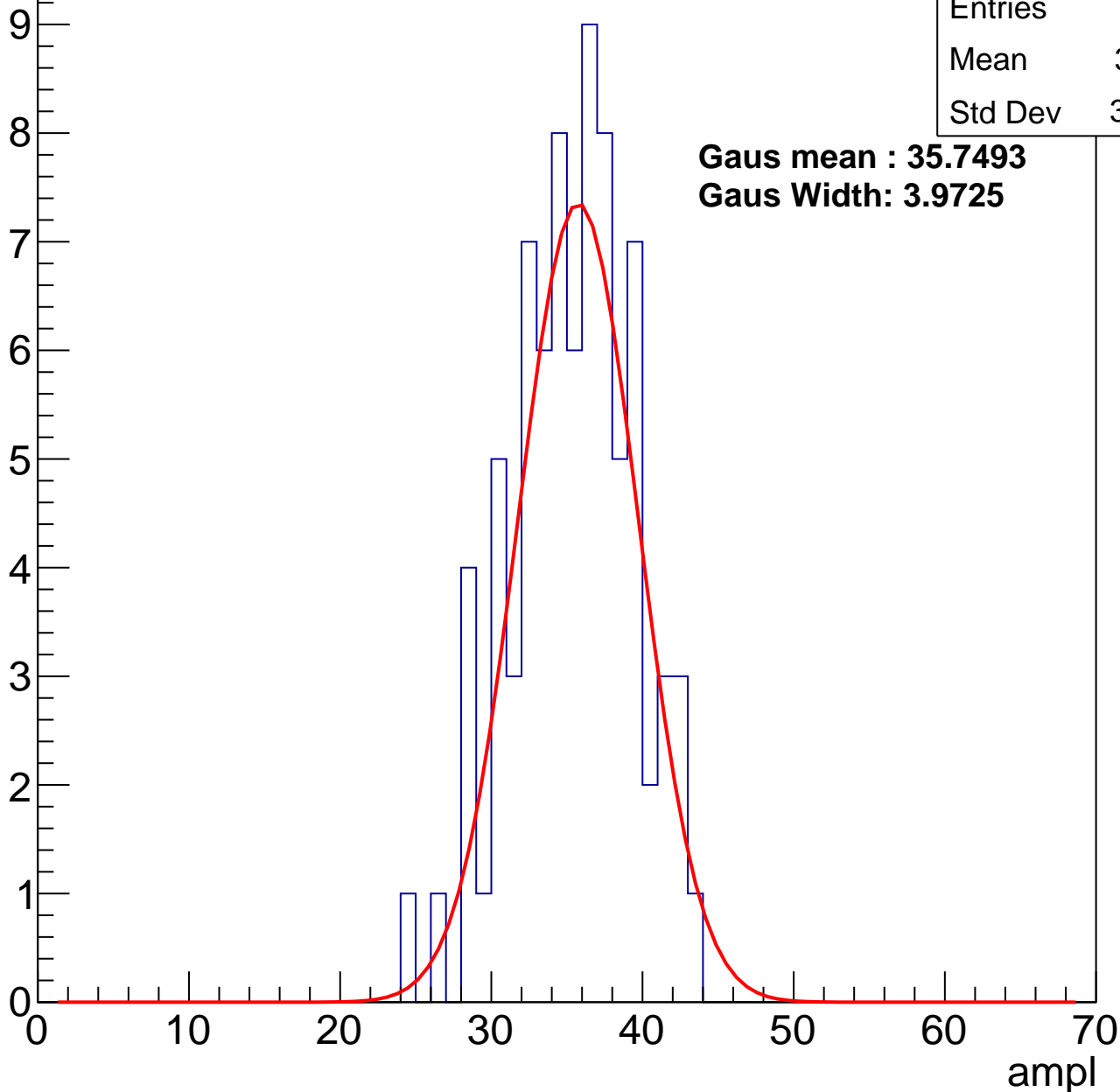
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.91
Std Dev	3.982

Gaus mean : 35.7493

Gaus Width: 3.9725



B1L102S, U17-ch87, adc2

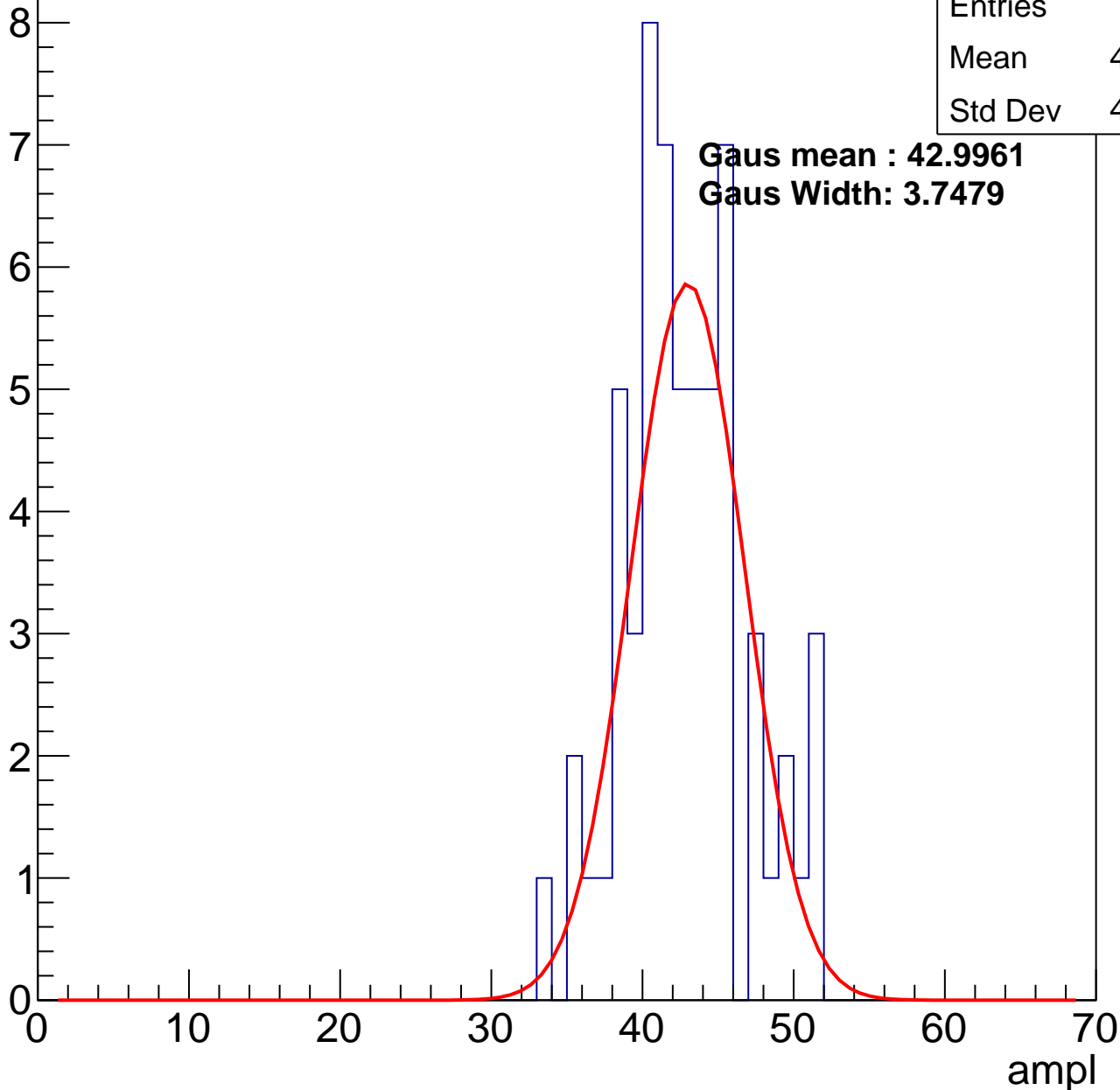
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.33
Std Dev	4.048

Gaus mean : 42.9961

Gaus Width: 3.7479

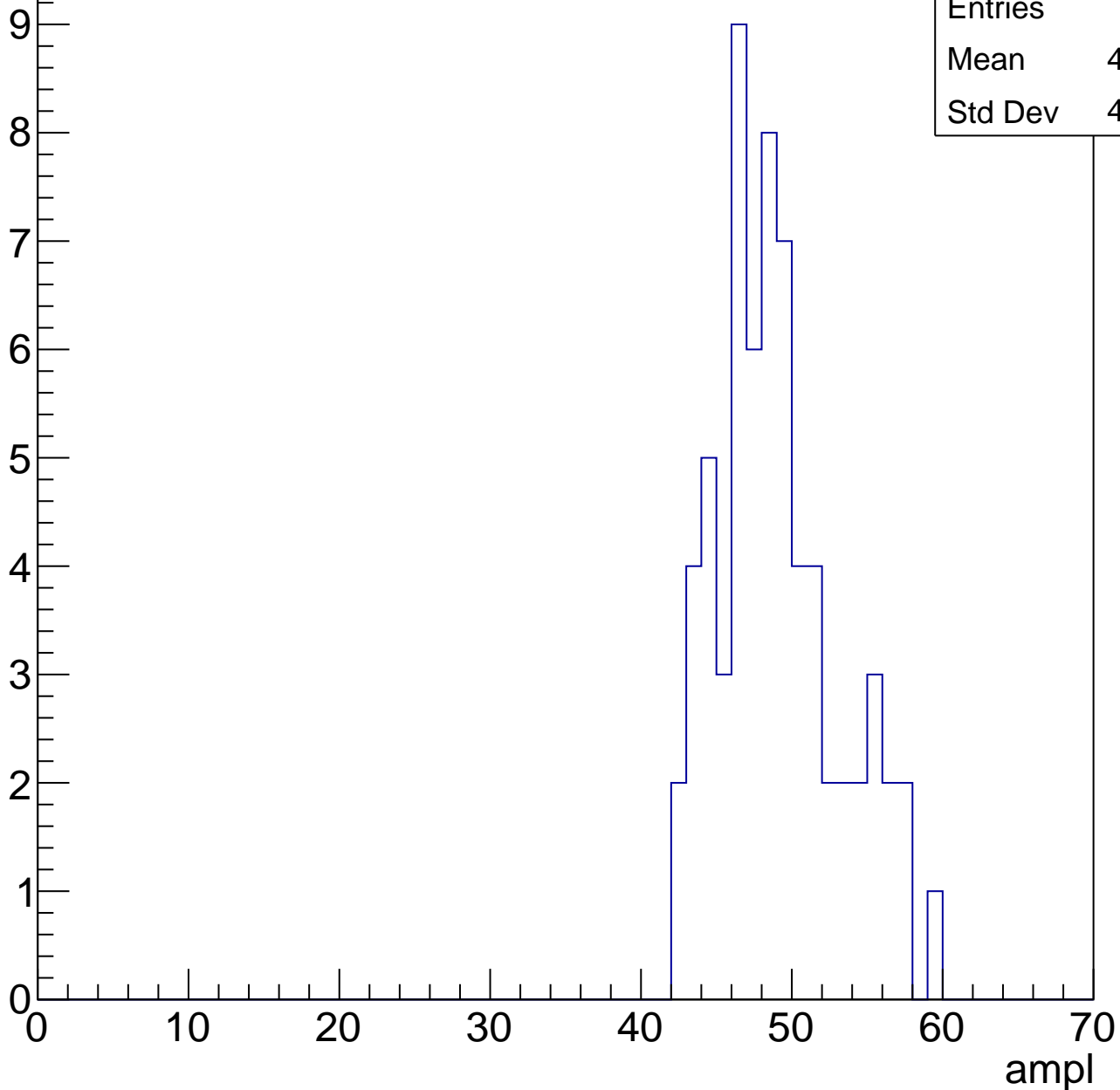


B1L102S, U17-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.58
Std Dev	4.027

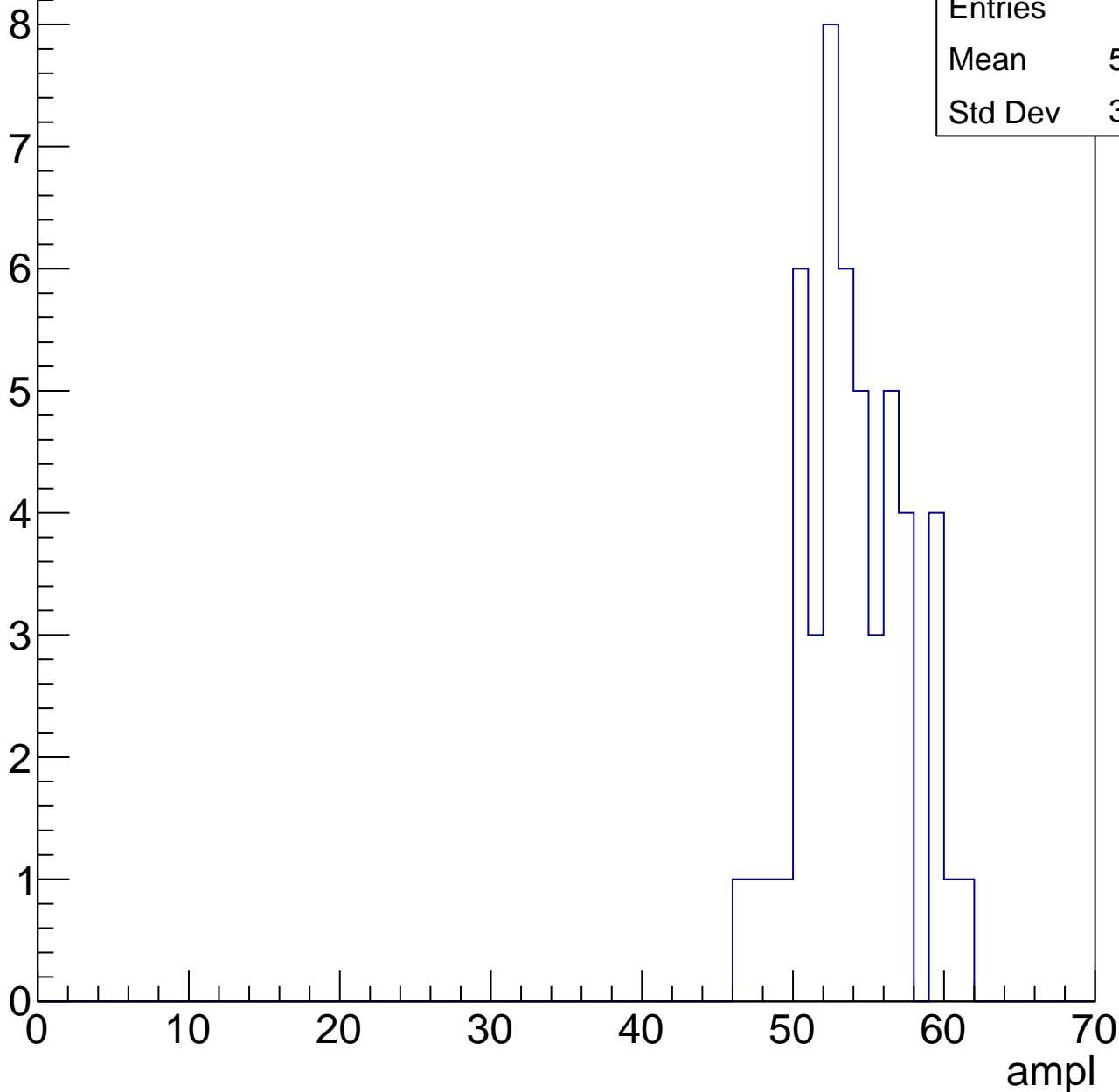


B1L102S, U17-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	53.54
Std Dev	3.372

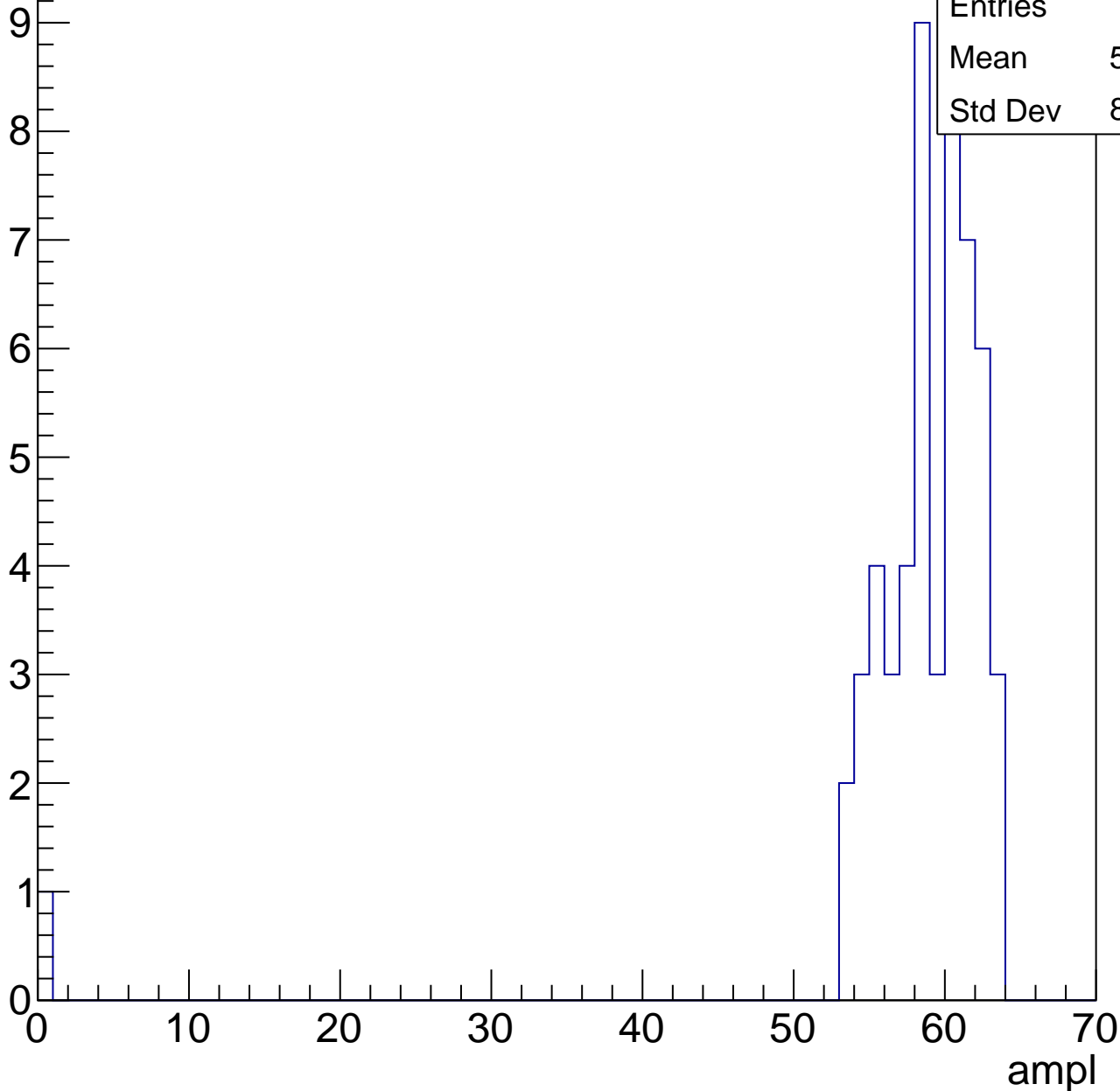


B1L102S, U17-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.57
Std Dev	8.437

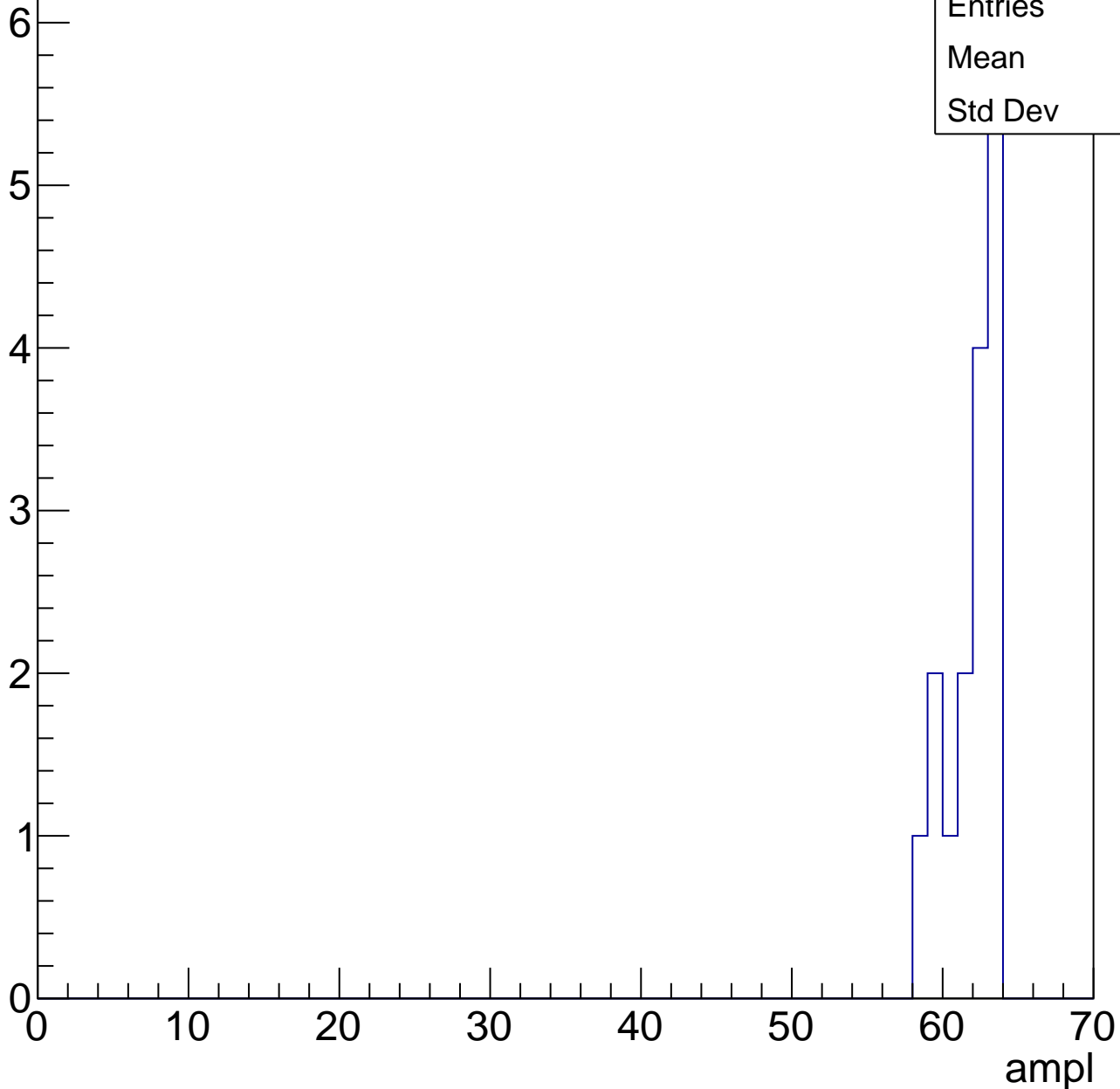


B1L102S, U17-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.5
Std Dev	1.62



B1L102S, U17-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch88, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	27.6
Std Dev	5.874

Gaus mean : 28.8296

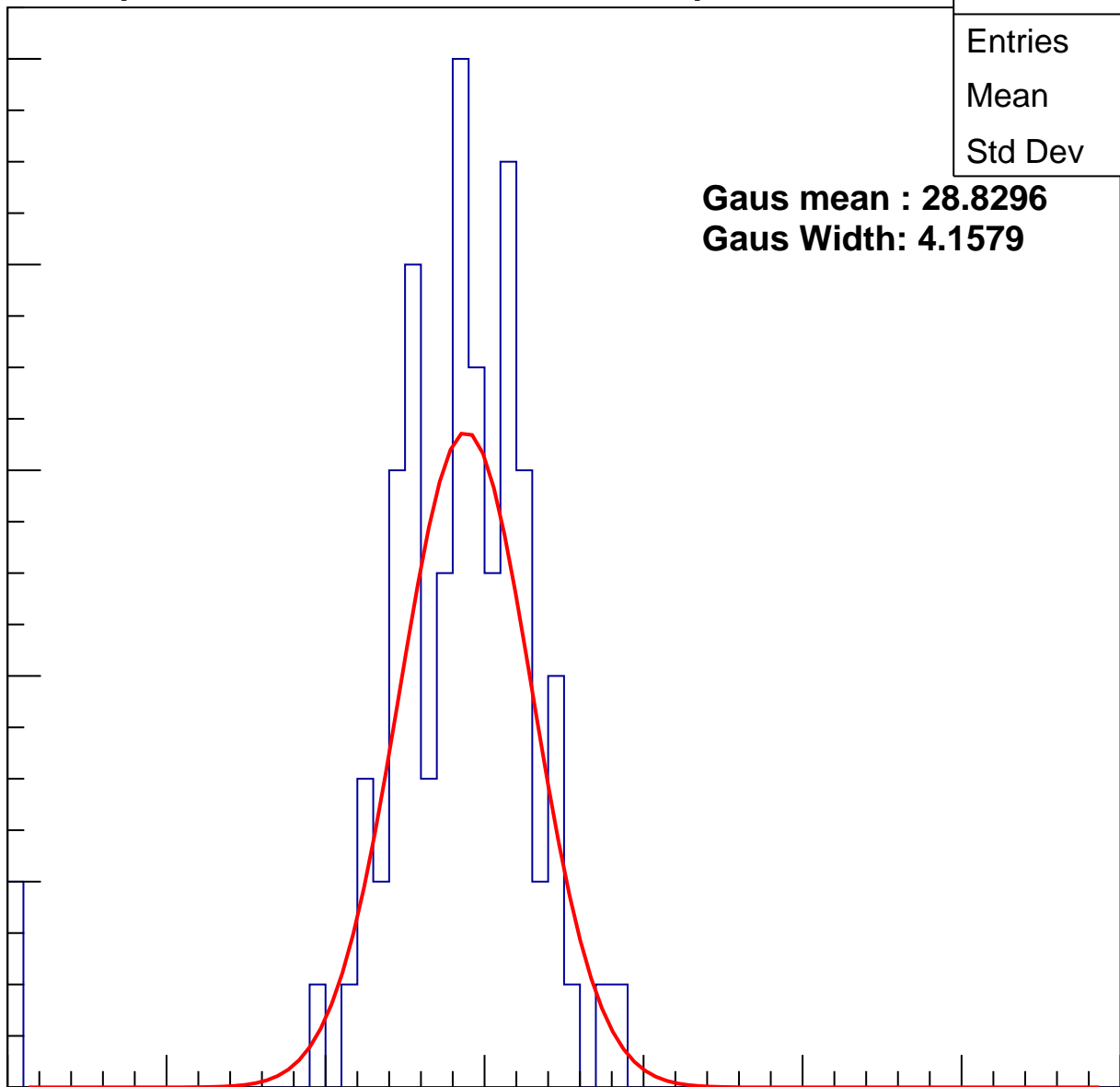
Gaus Width: 4.1579

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch88, adc1

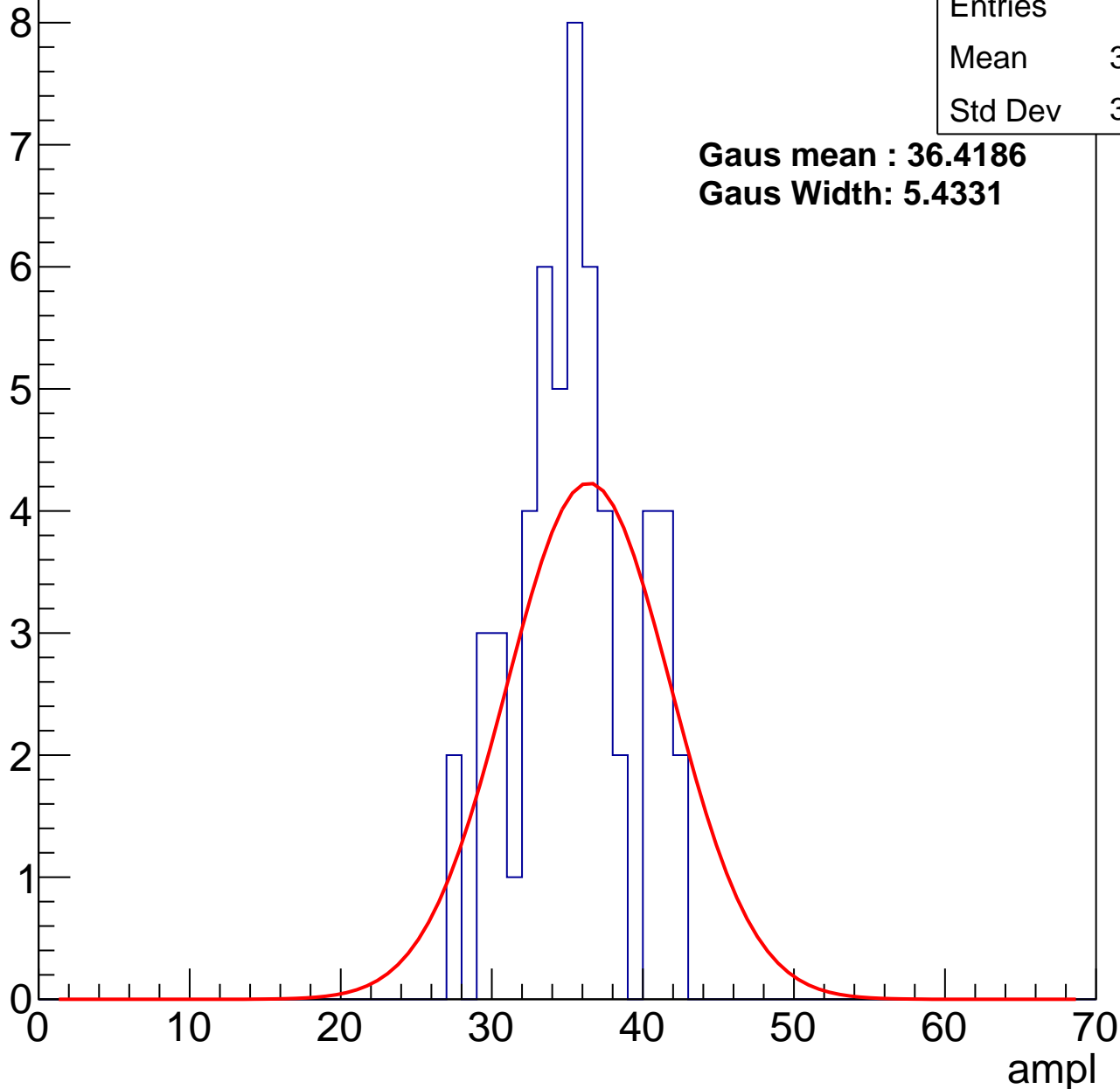
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	34.93
Std Dev	3.785

Gaus mean : 36.4186

Gaus Width: 5.4331



B1L102S, U17-ch88, adc2

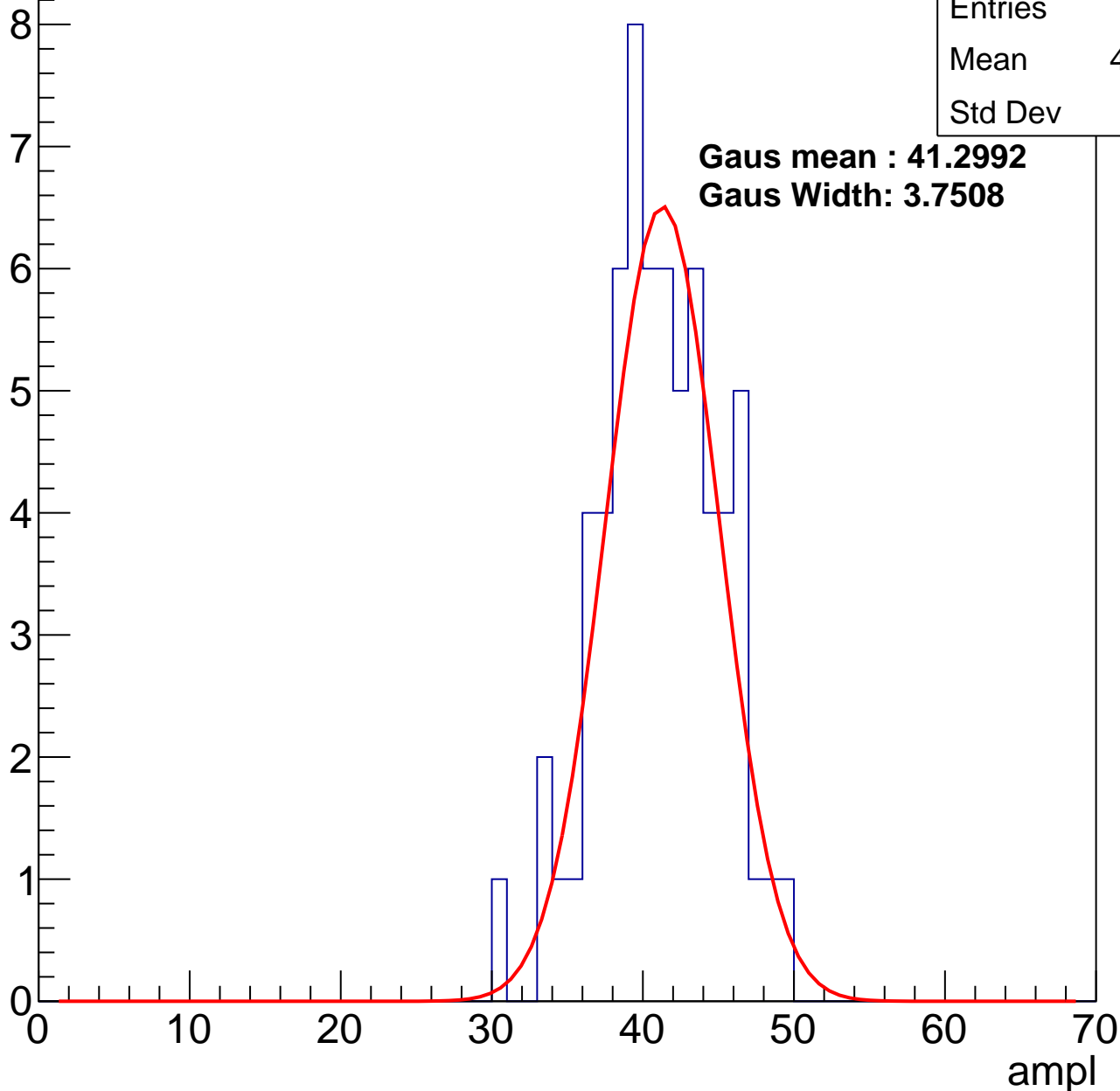
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	40.62
Std Dev	3.86

Gaus mean : 41.2992

Gaus Width: 3.7508

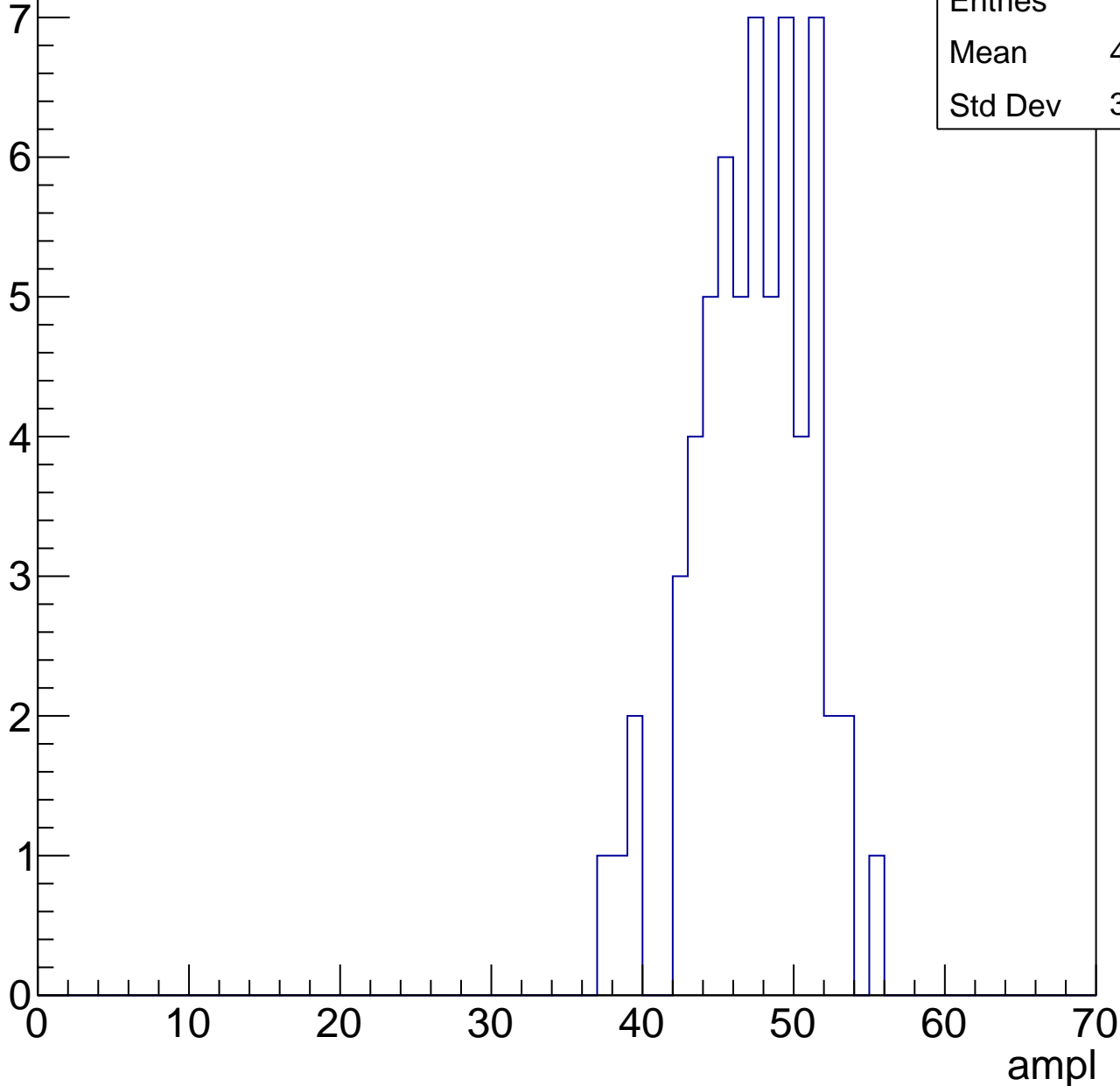


B1L102S, U17-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	46.85
Std Dev	3.784

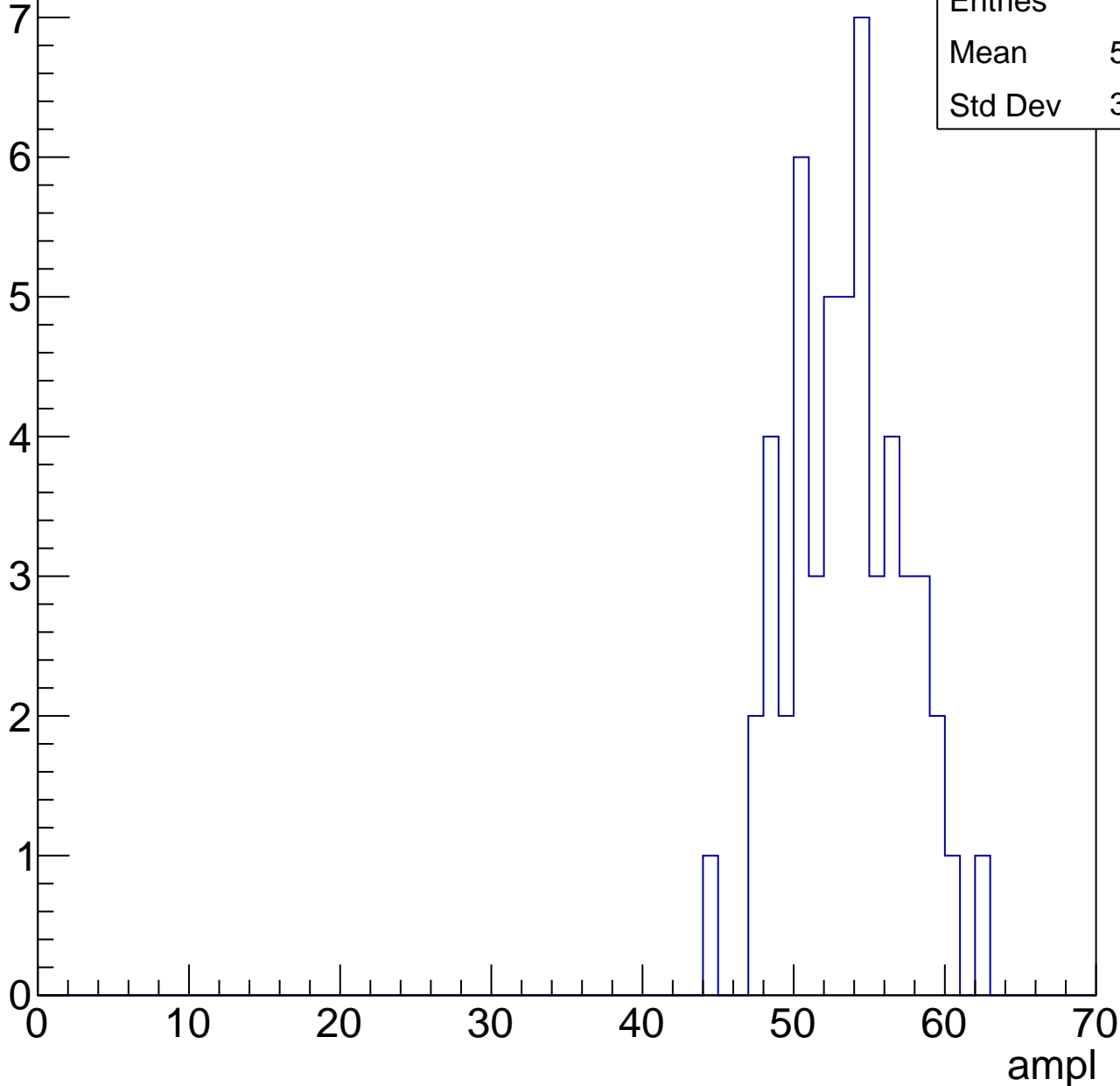


B1L102S, U17-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.04
Std Dev	3.762

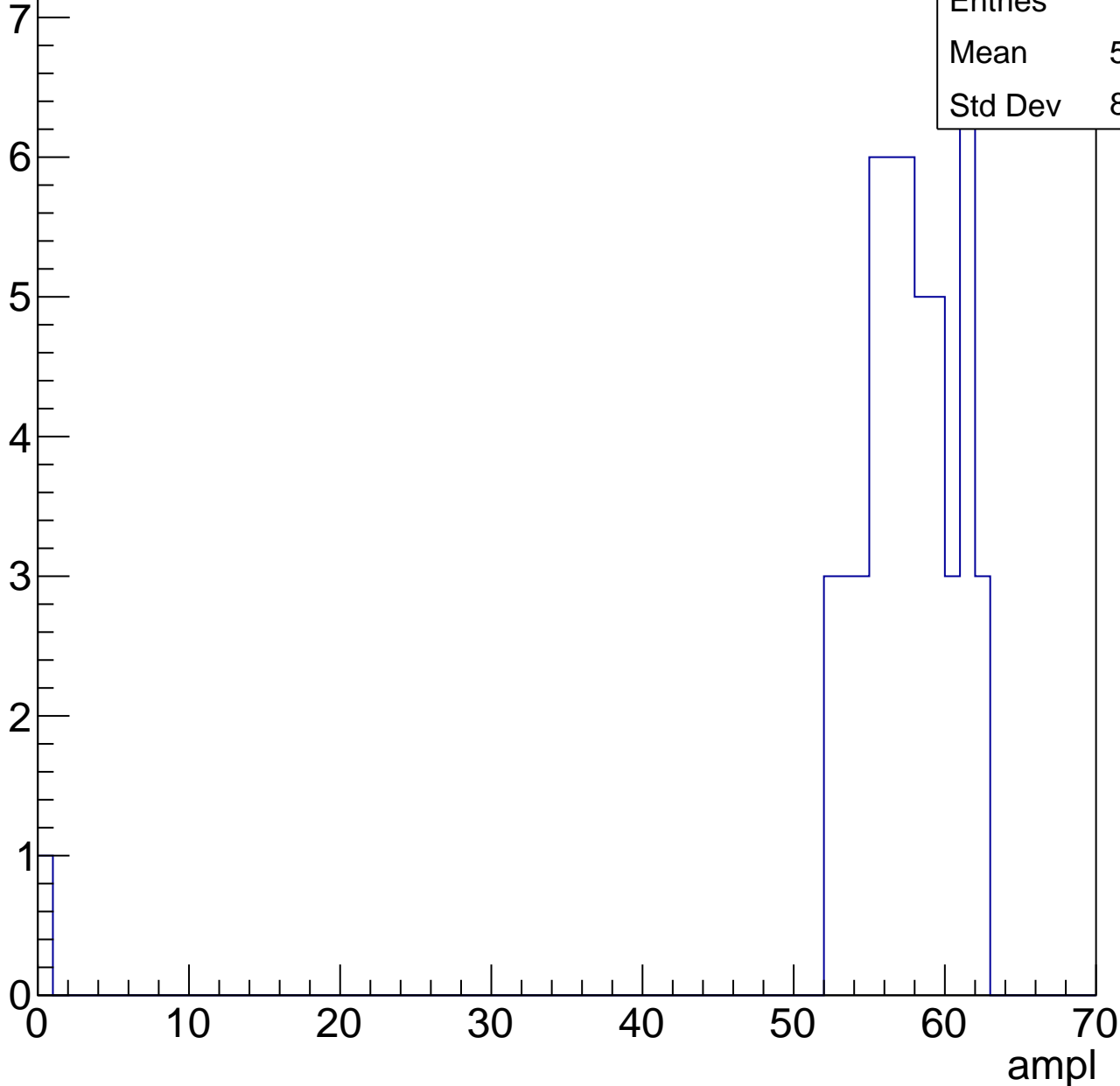


B1L102S, U17-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.14
Std Dev	8.437

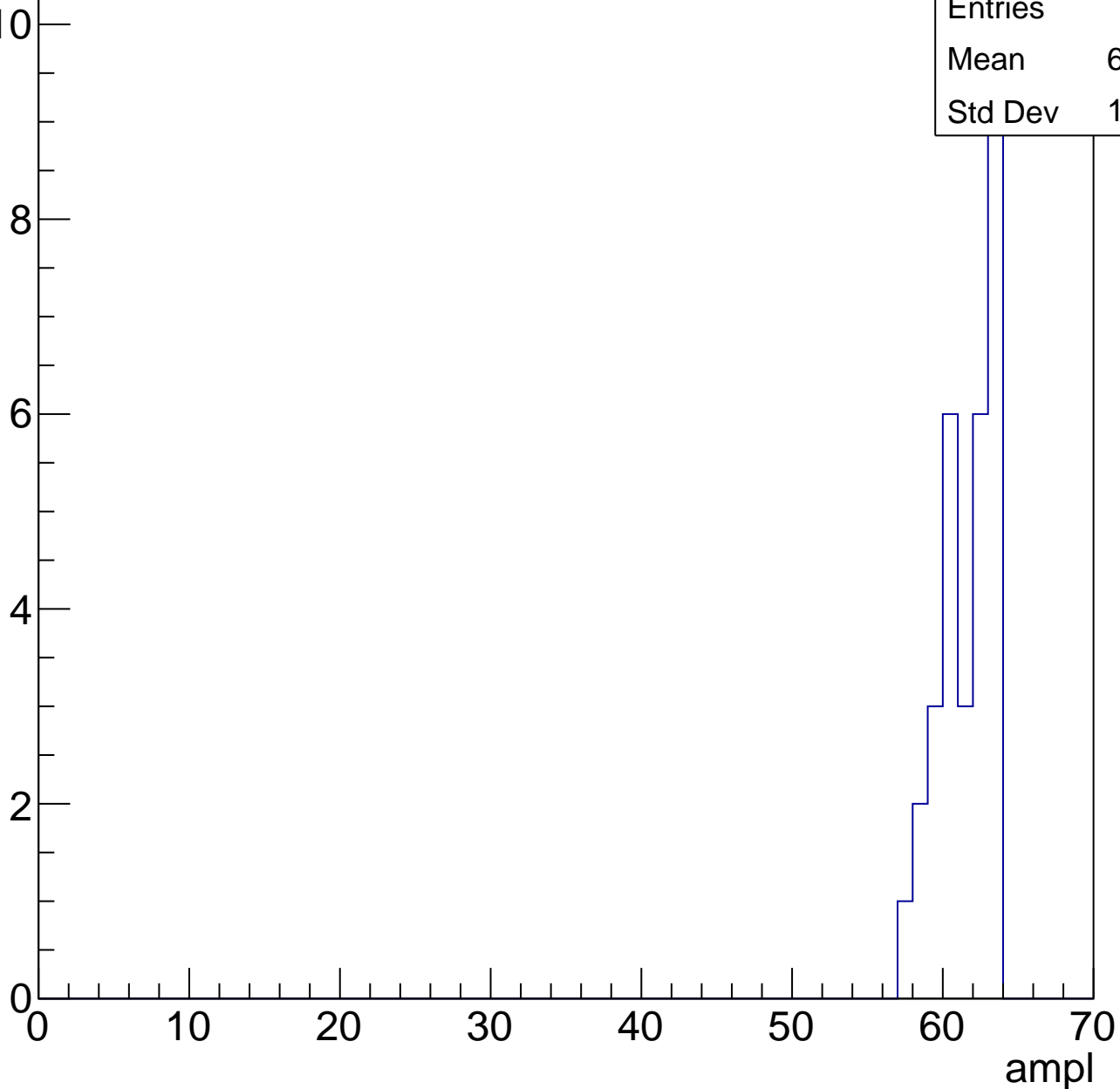


B1L102S, U17-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	61.13
Std Dev	1.773



B1L102S, U17-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U17-ch89, adc0

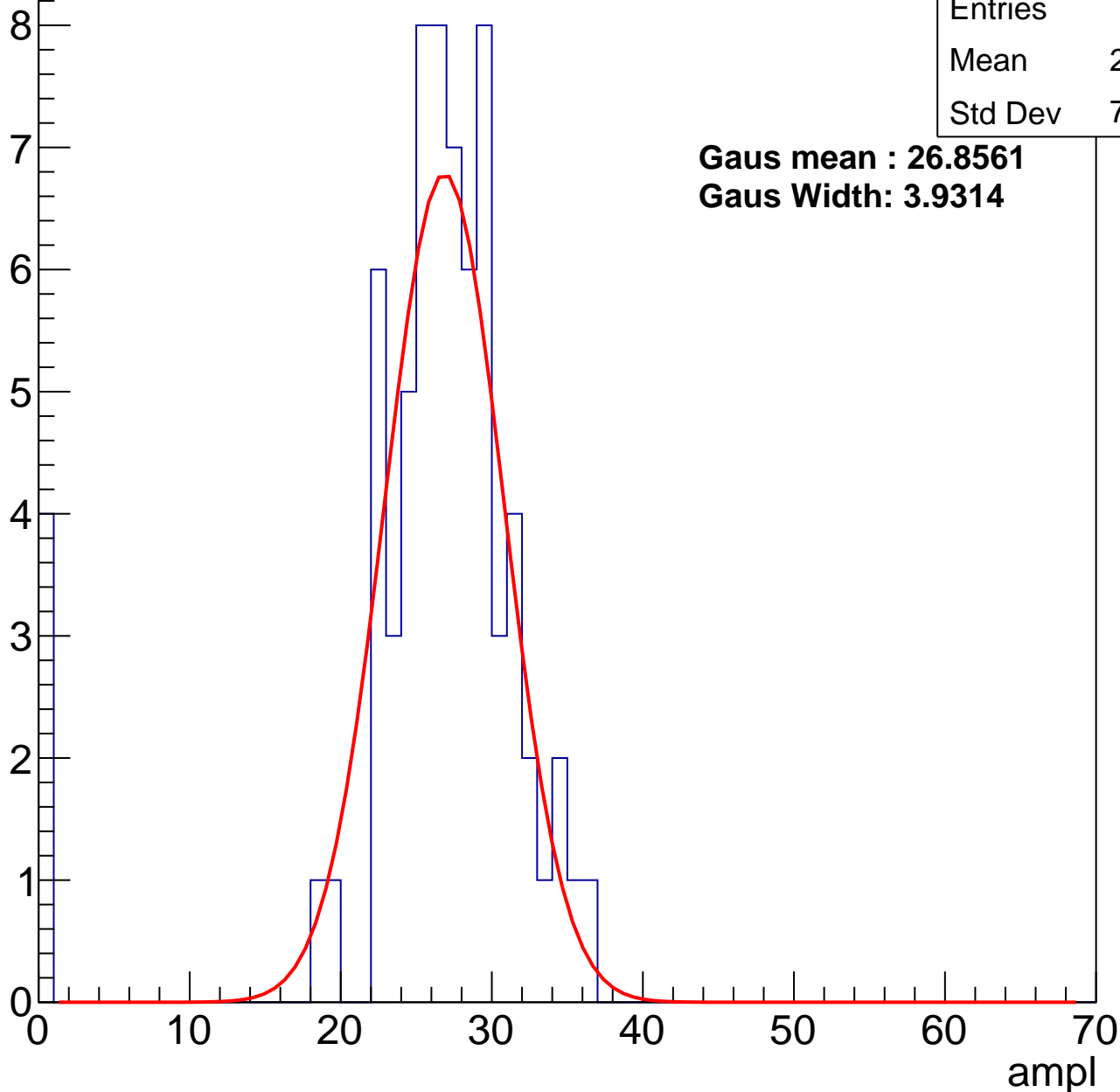
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	25.42
Std Dev	7.146

Gaus mean : 26.8561

Gaus Width: 3.9314



B1L102S, U17-ch89, adc1

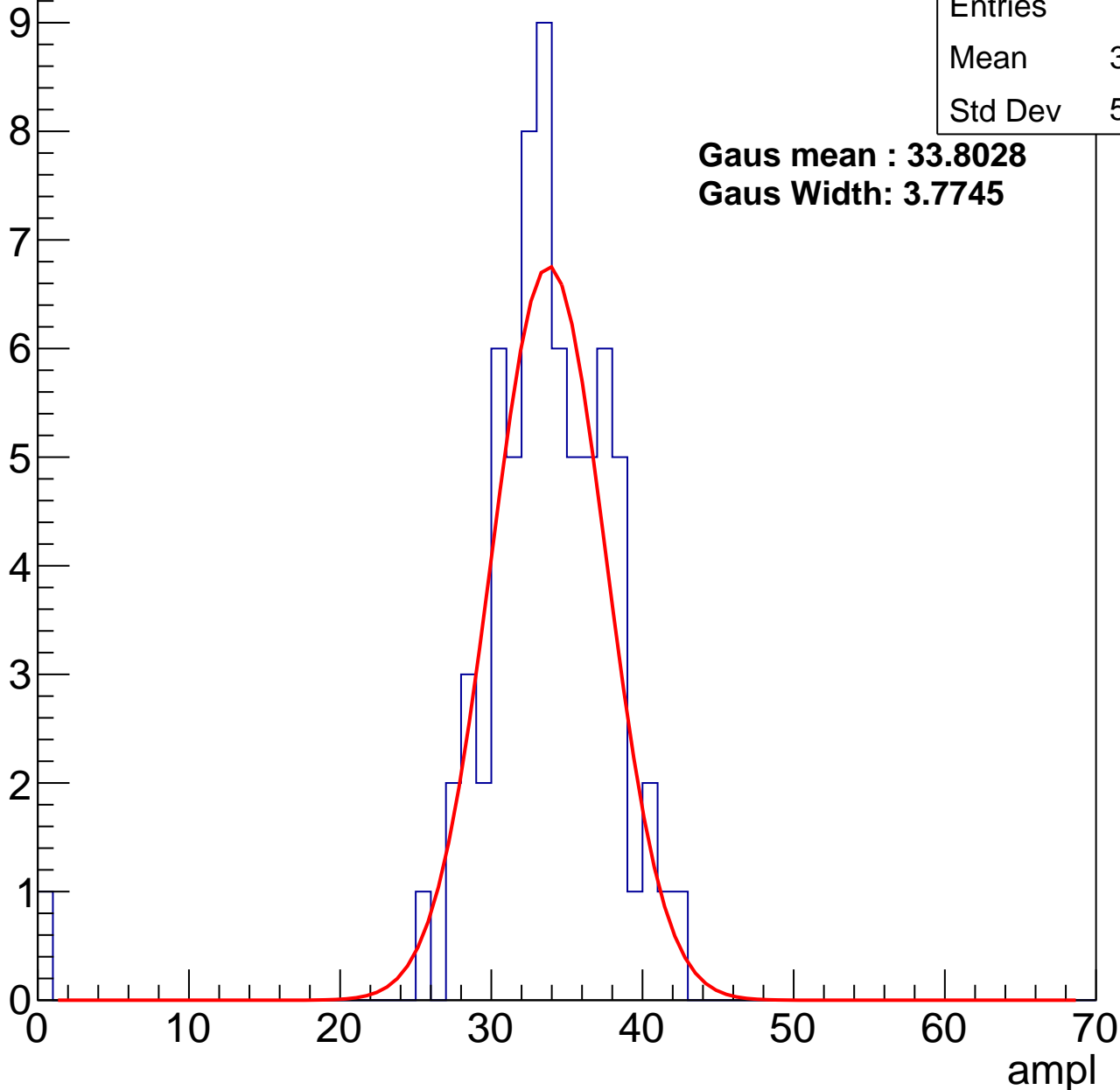
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	33.07
Std Dev	5.363

Gaus mean : 33.8028

Gaus Width: 3.7745



B1L102S, U17-ch89, adc2

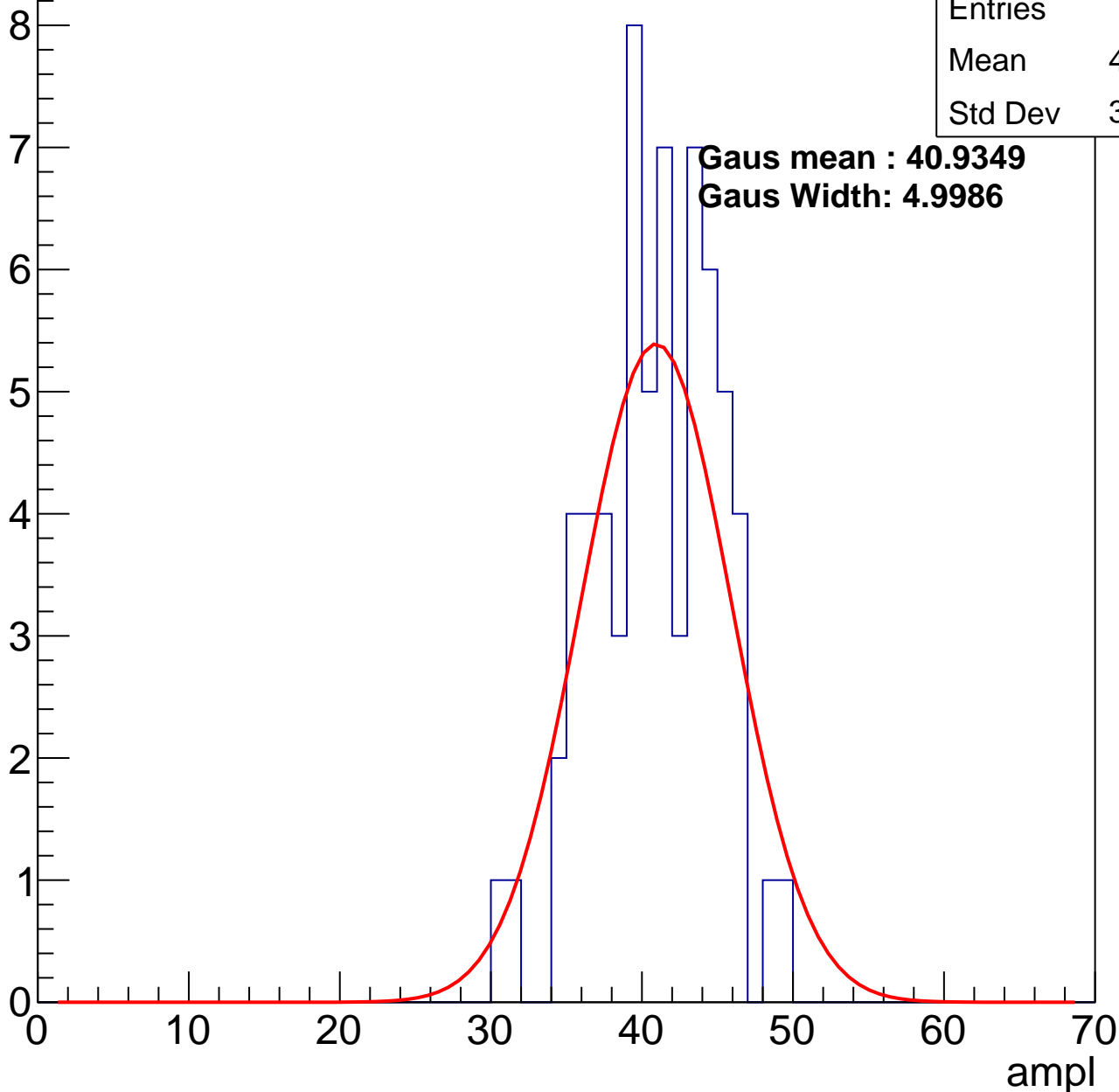
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	40.47
Std Dev	3.989

Gaus mean : 40.9349

Gaus Width: 4.9986

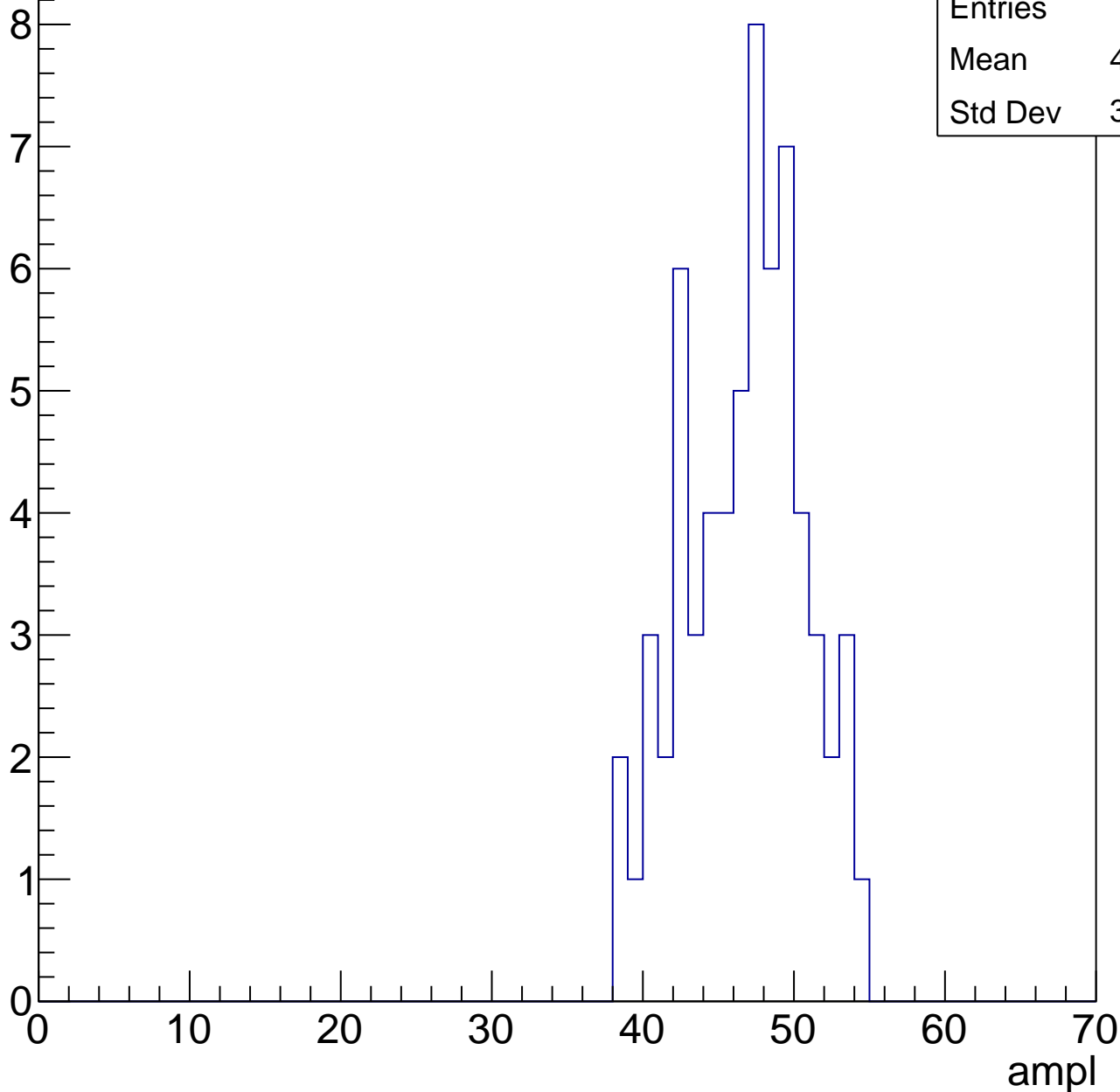


B1L102S, U17-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	46.27
Std Dev	3.934

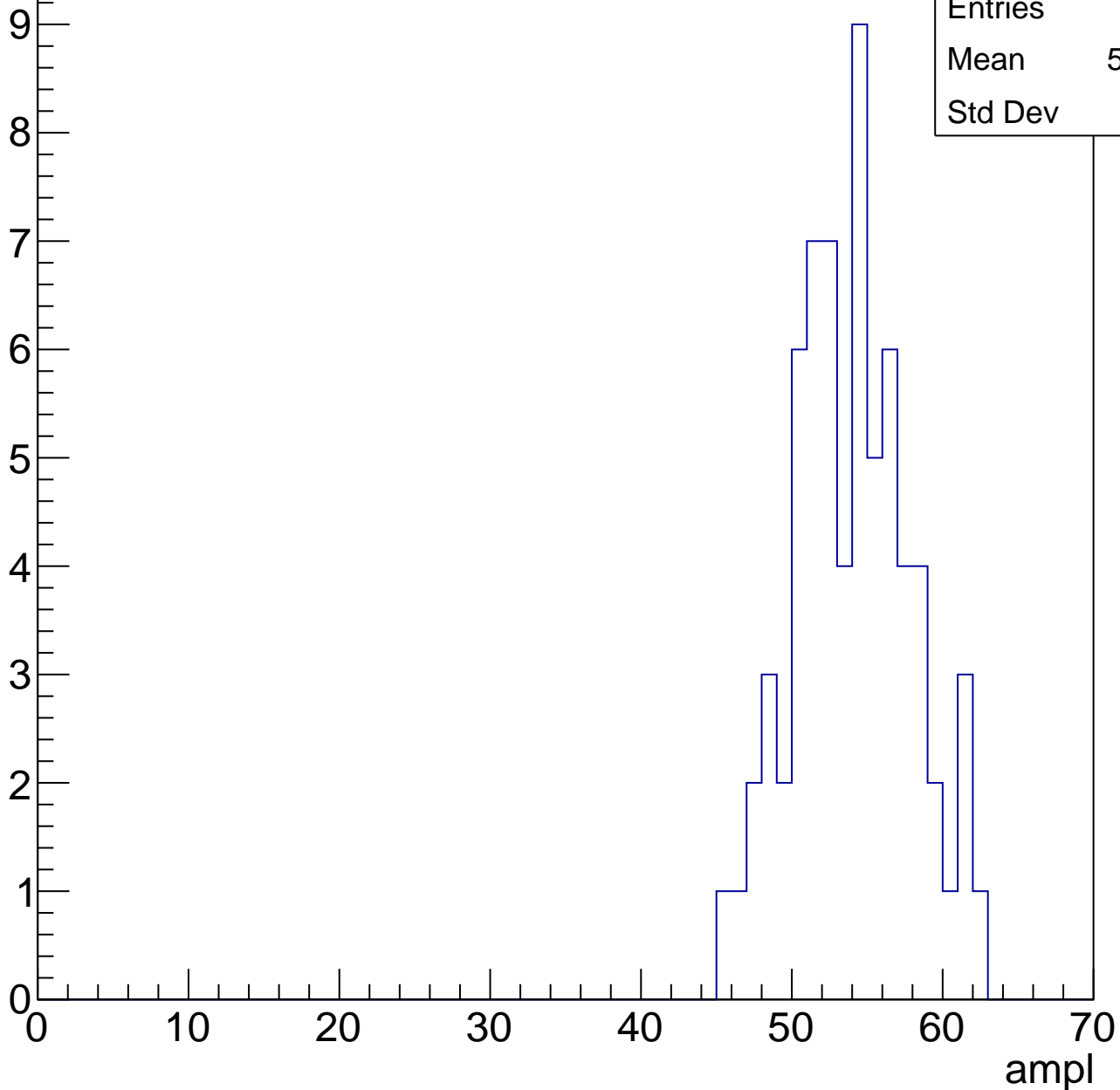


B1L102S, U17-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

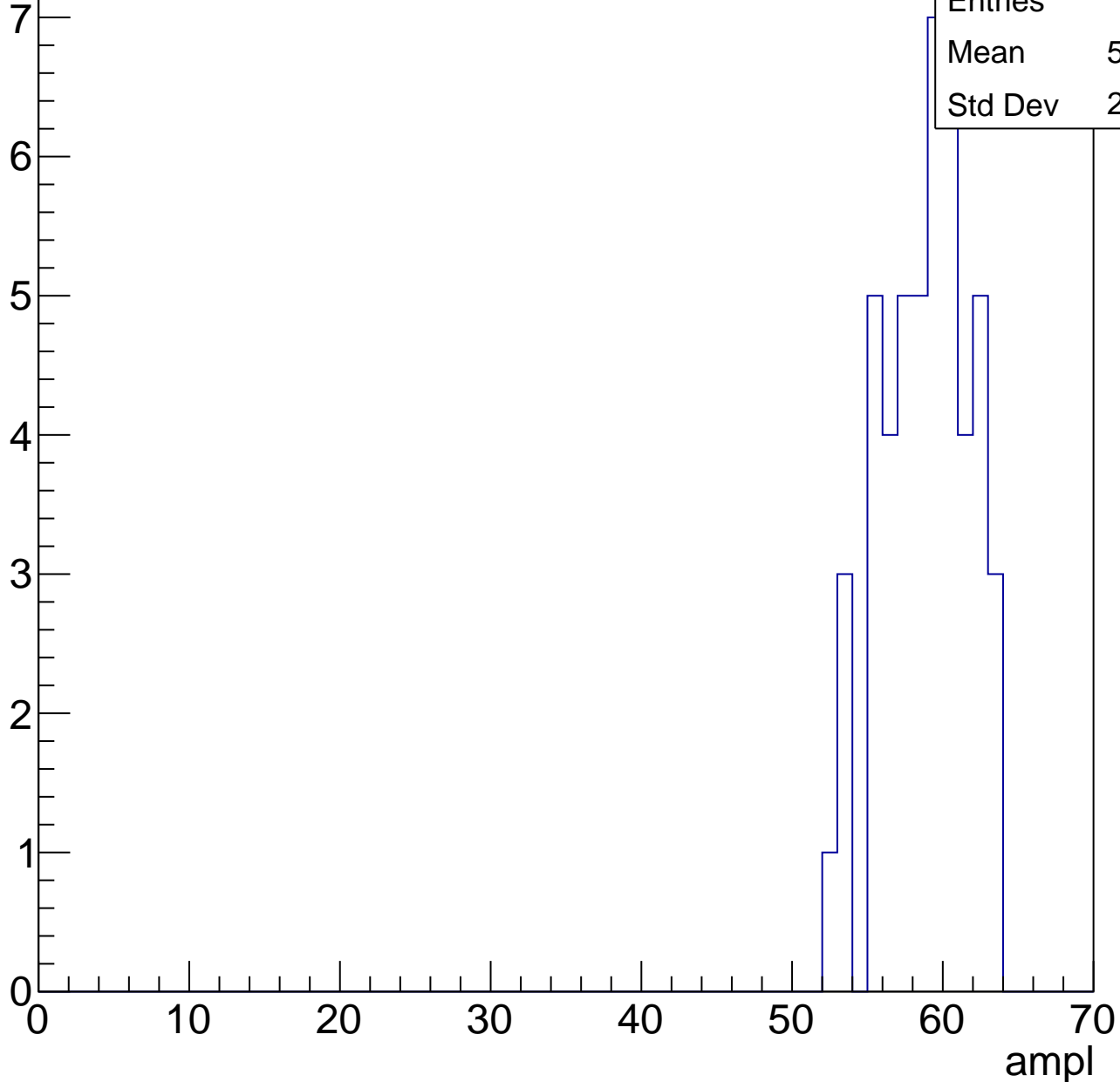
Entries	68
Mean	53.53
Std Dev	3.84



B1L102S, U17-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

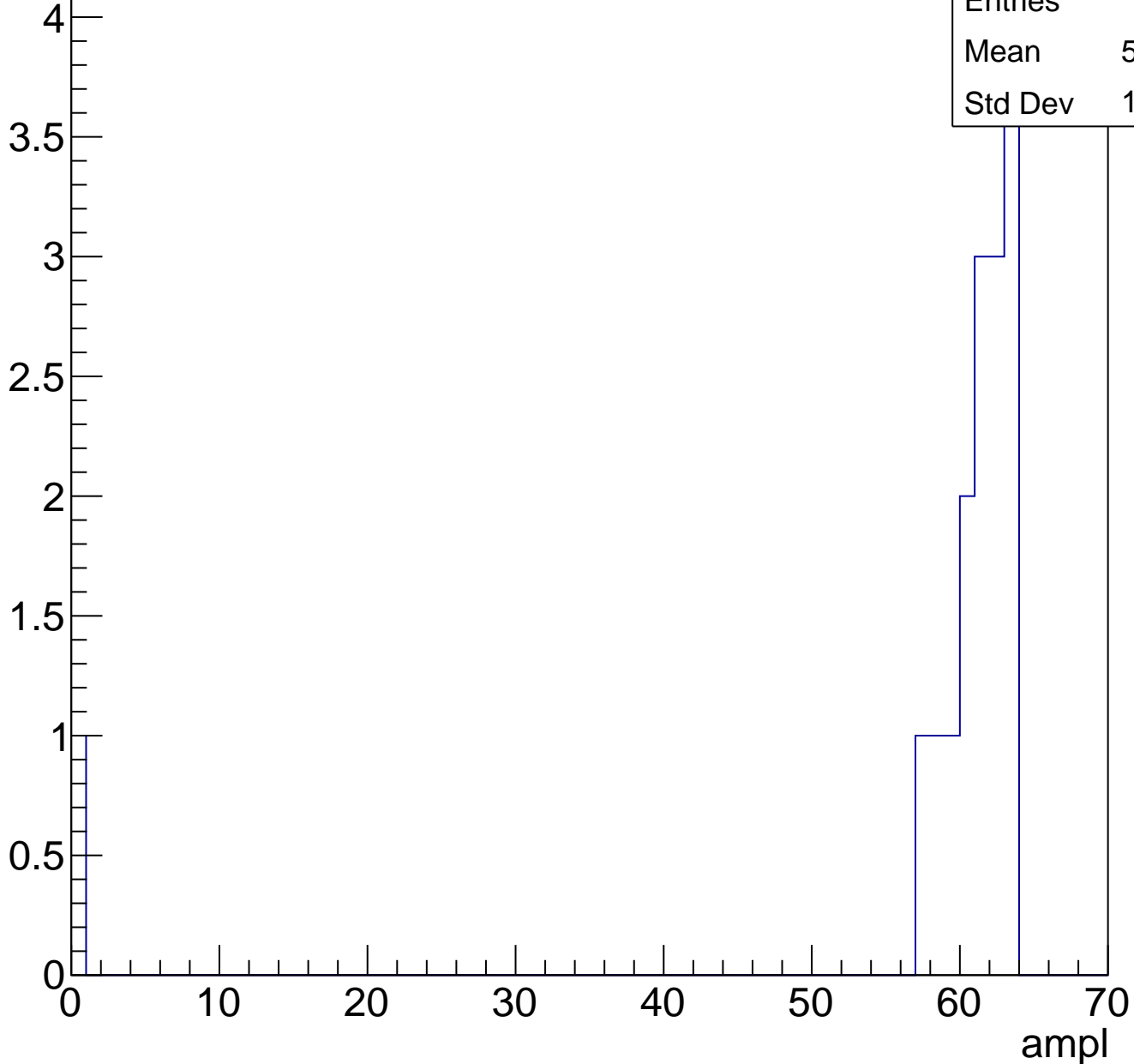
Entry



B1L102S, U17-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

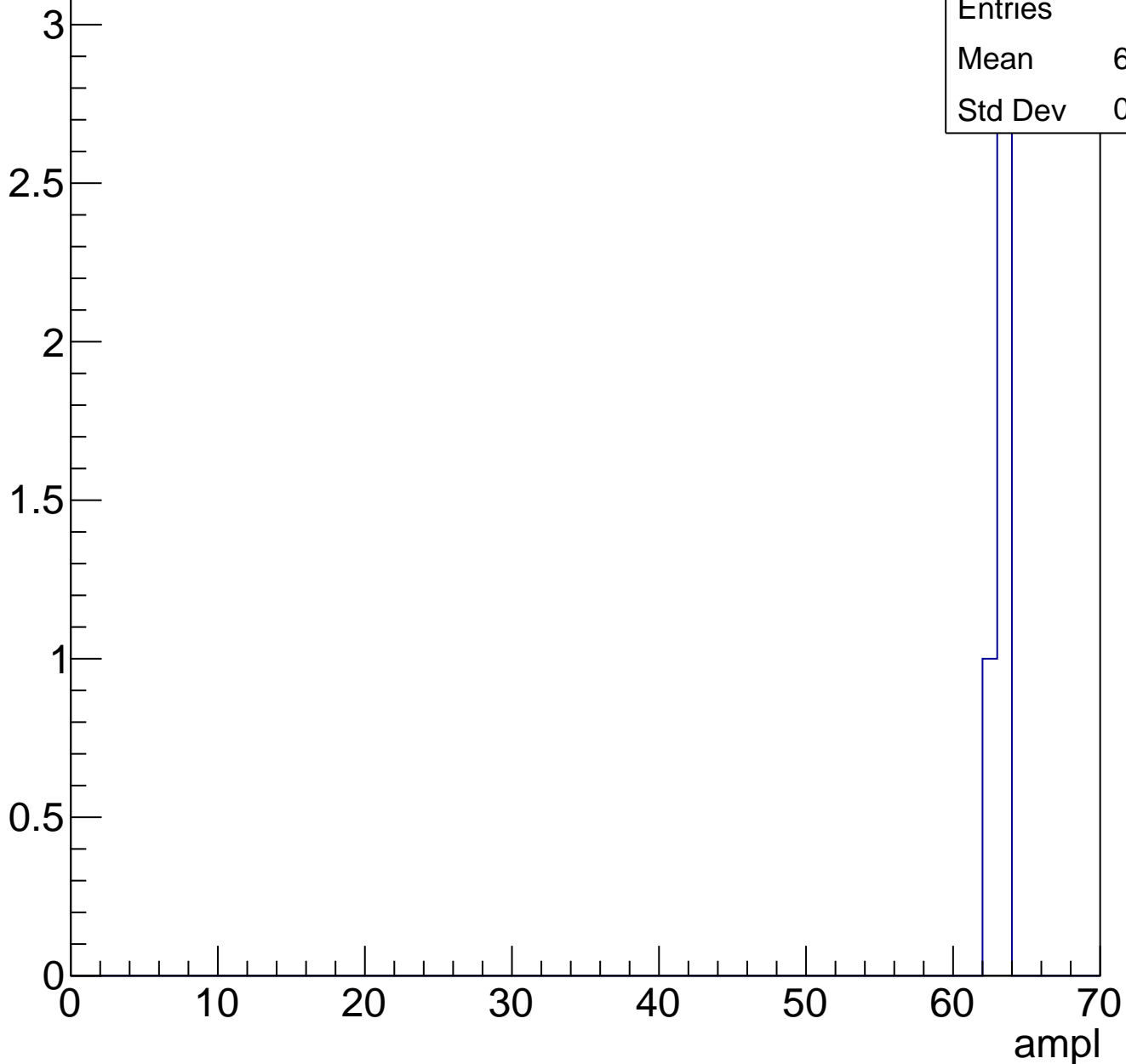
Entry



B1L102S, U17-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch90, adc0

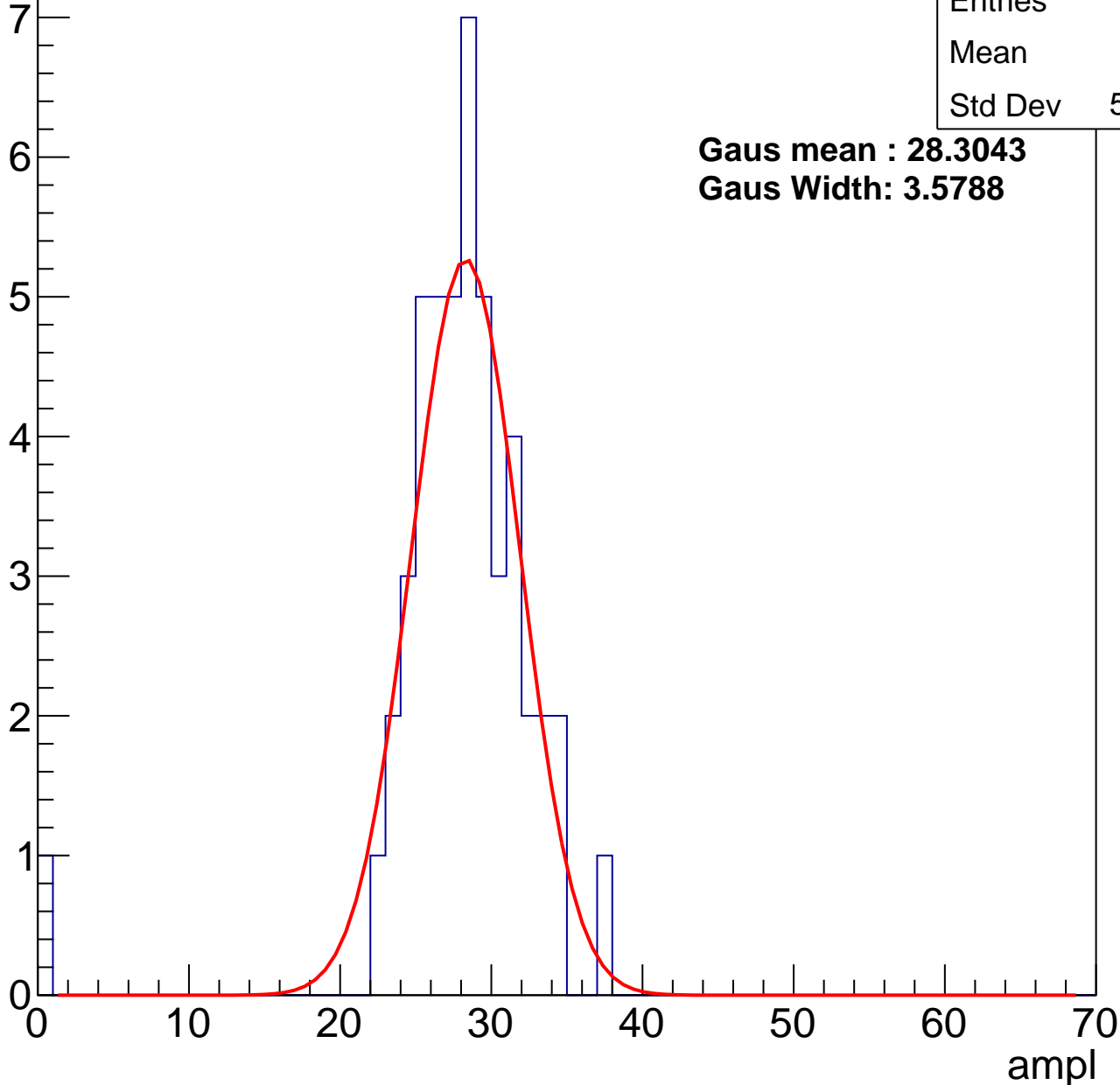
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	27.5
Std Dev	5.132

Gaus mean : 28.3043

Gaus Width: 3.5788



B1L102S, U17-ch90, adc1

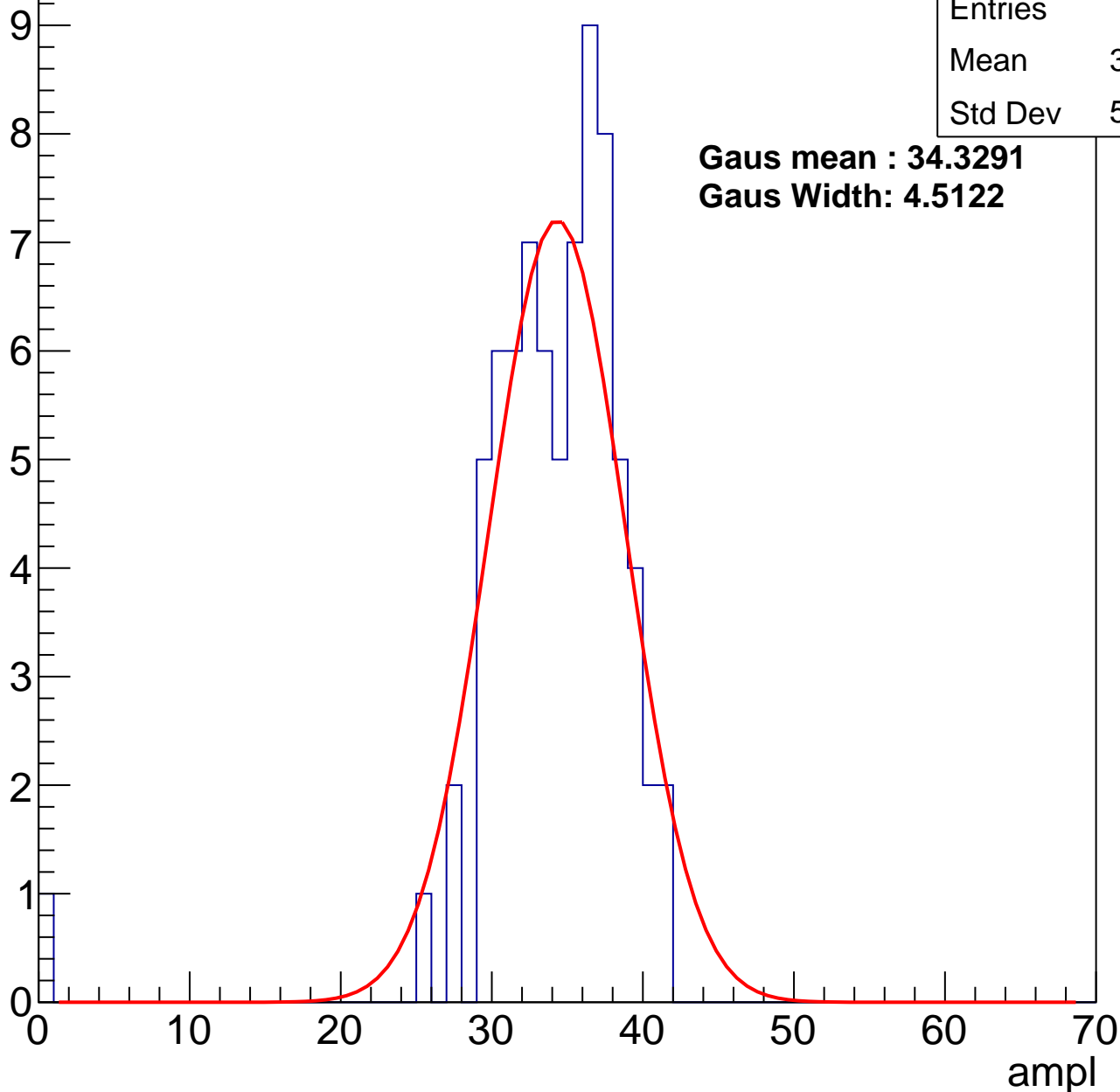
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	33.62
Std Dev	5.254

Gaus mean : 34.3291

Gaus Width: 4.5122



B1L102S, U17-ch90, adc2

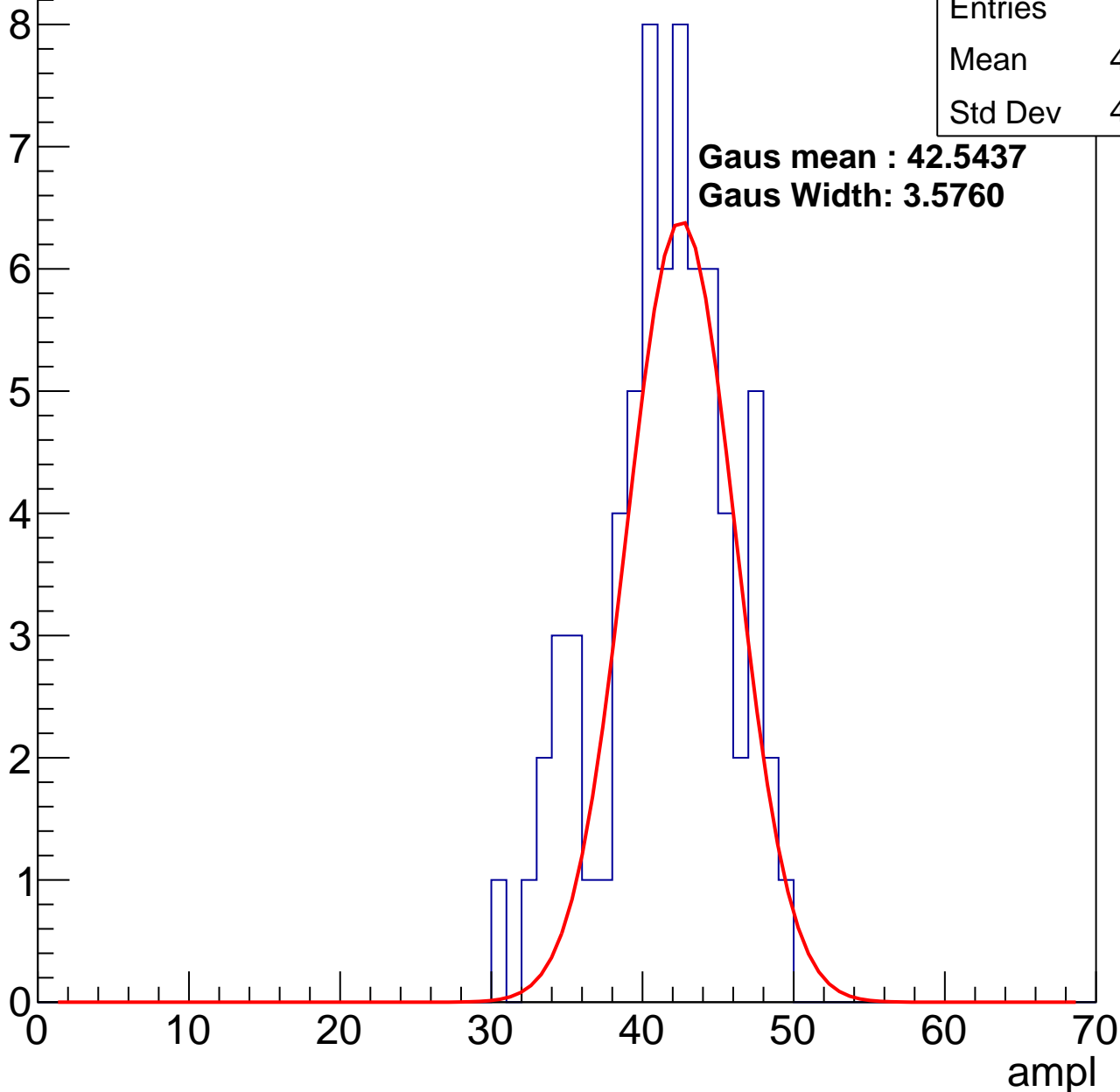
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.03
Std Dev	4.225

Gaus mean : 42.5437

Gaus Width: 3.5760

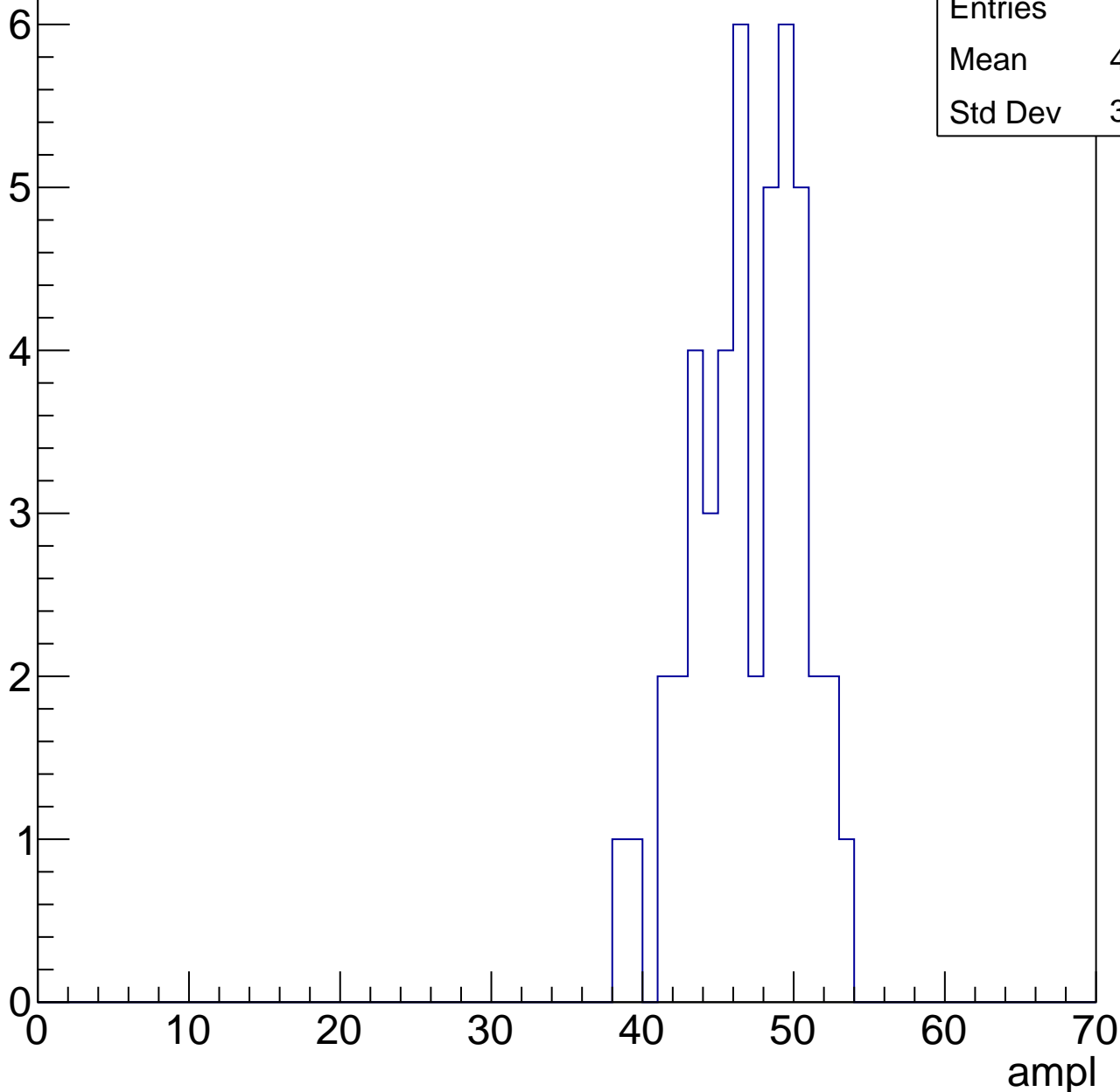


B1L102S, U17-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

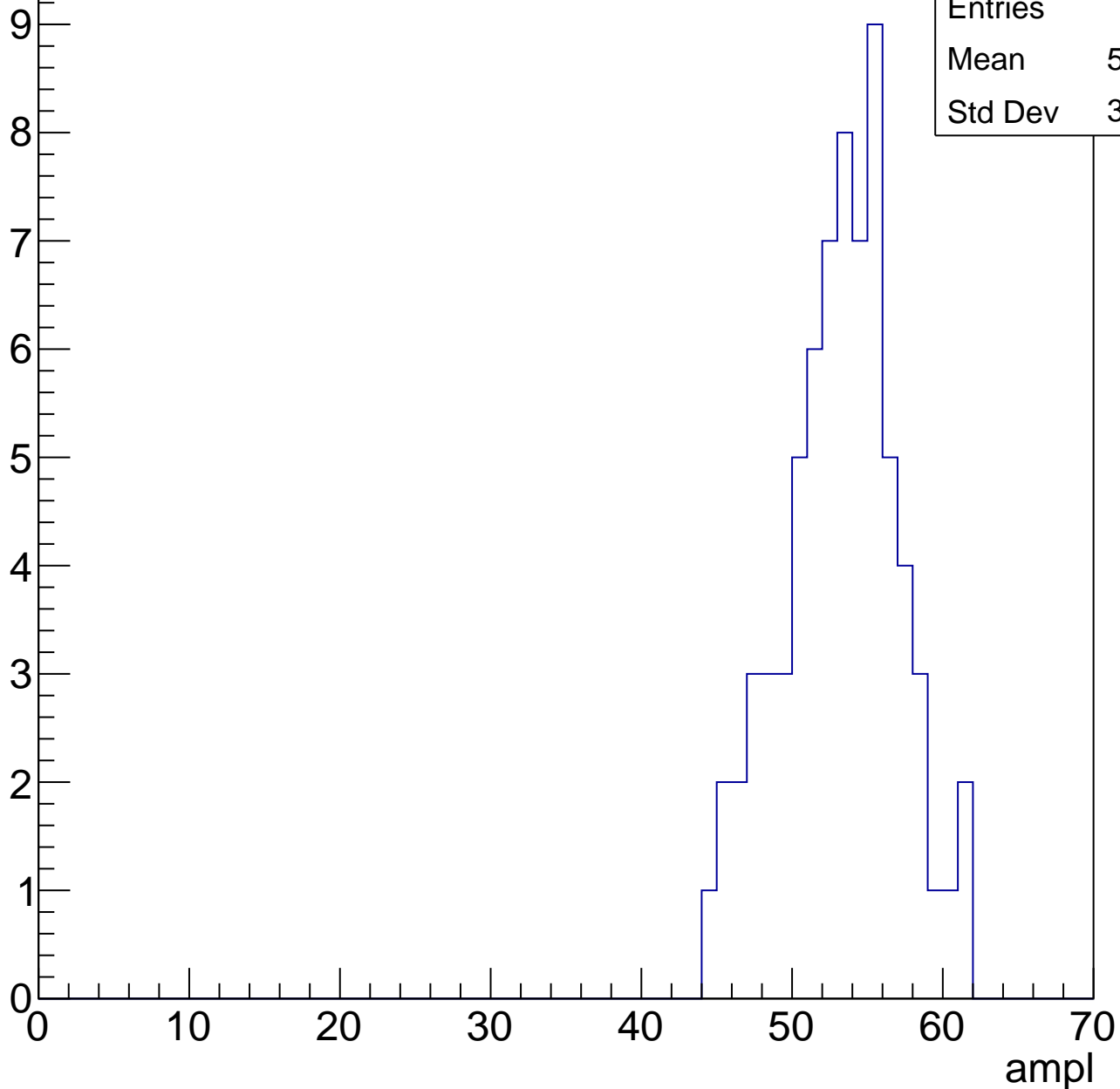
Entries	46
Mean	46.52
Std Dev	3.494



B1L102S, U17-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

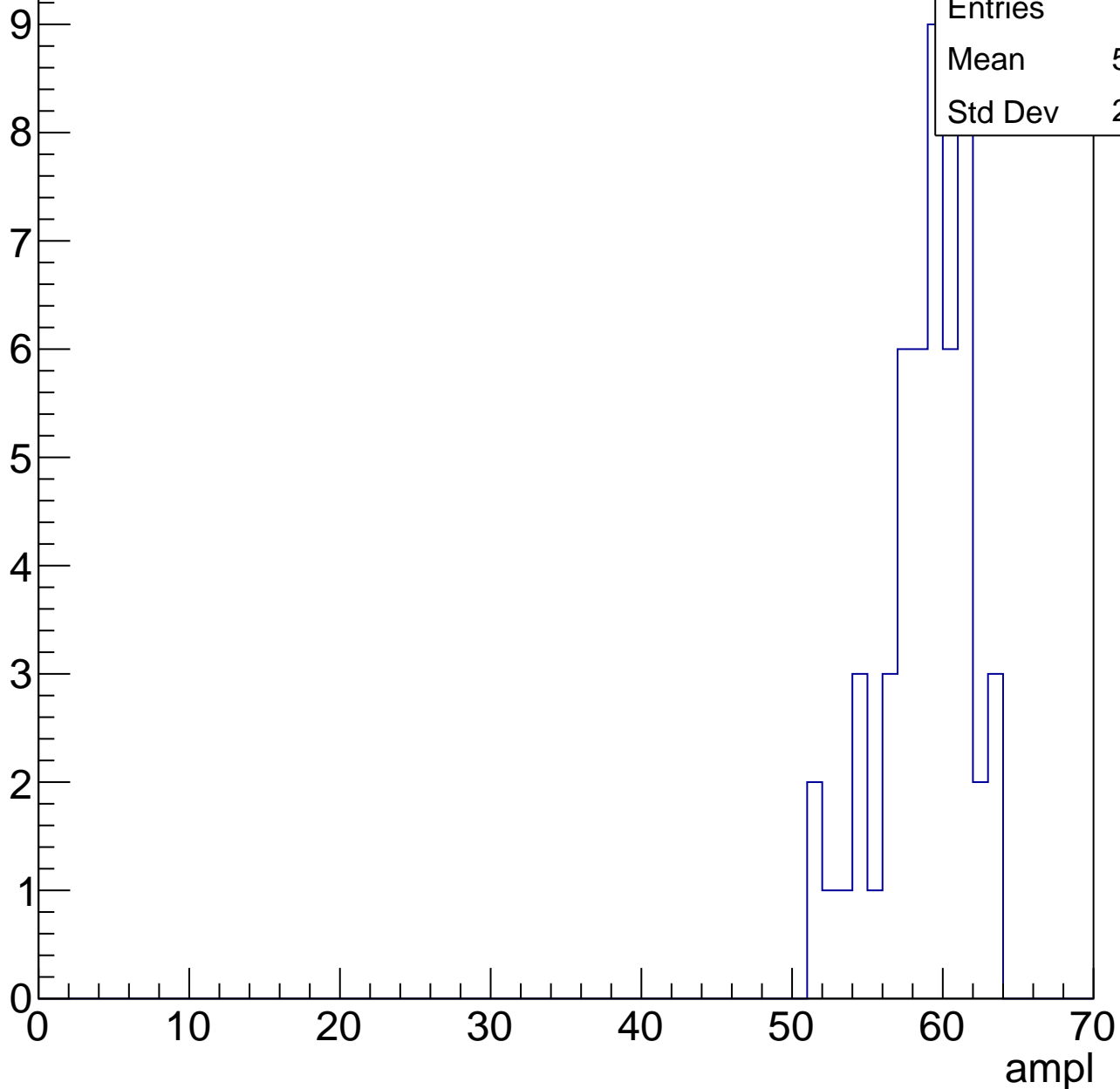
Entry



B1L102S, U17-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



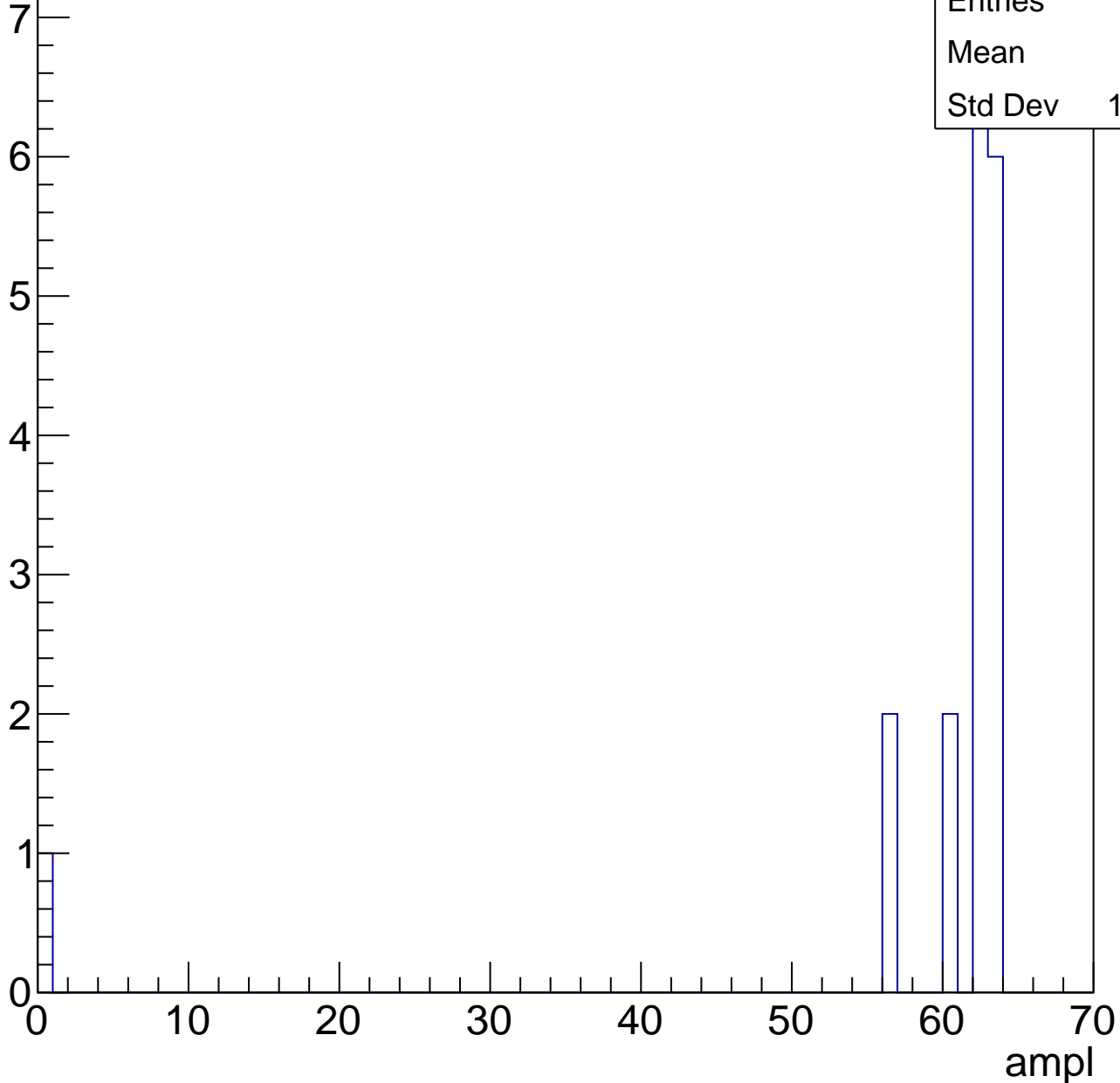
Entries	51
Mean	58.31
Std Dev	2.941

B1L102S, U17-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

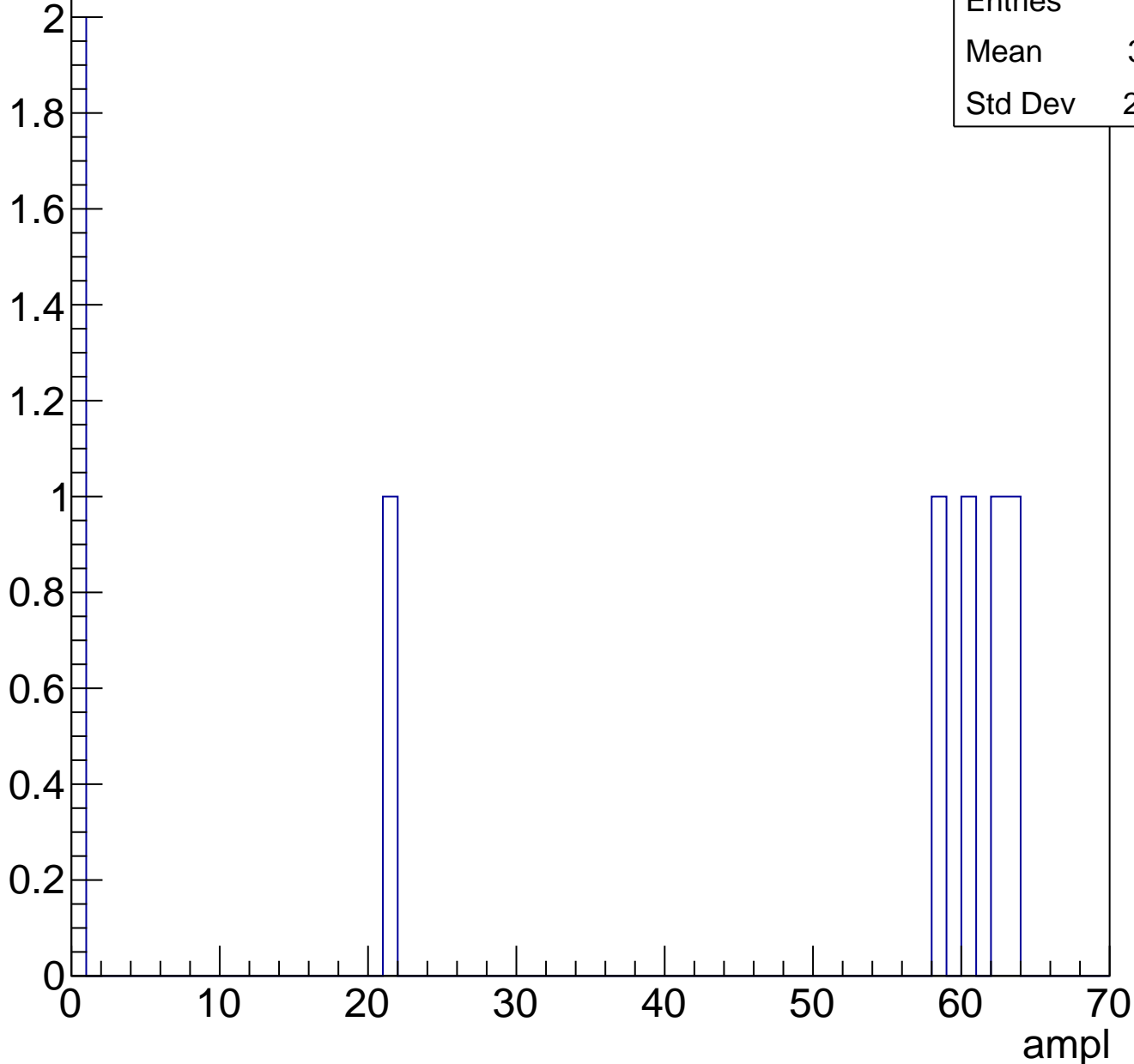
Entries	18
Mean	58
Std Dev	14.22



B1L102S, U17-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	7
Mean	37.71
Std Dev	27.42

B1L102S, U17-ch91, adc0

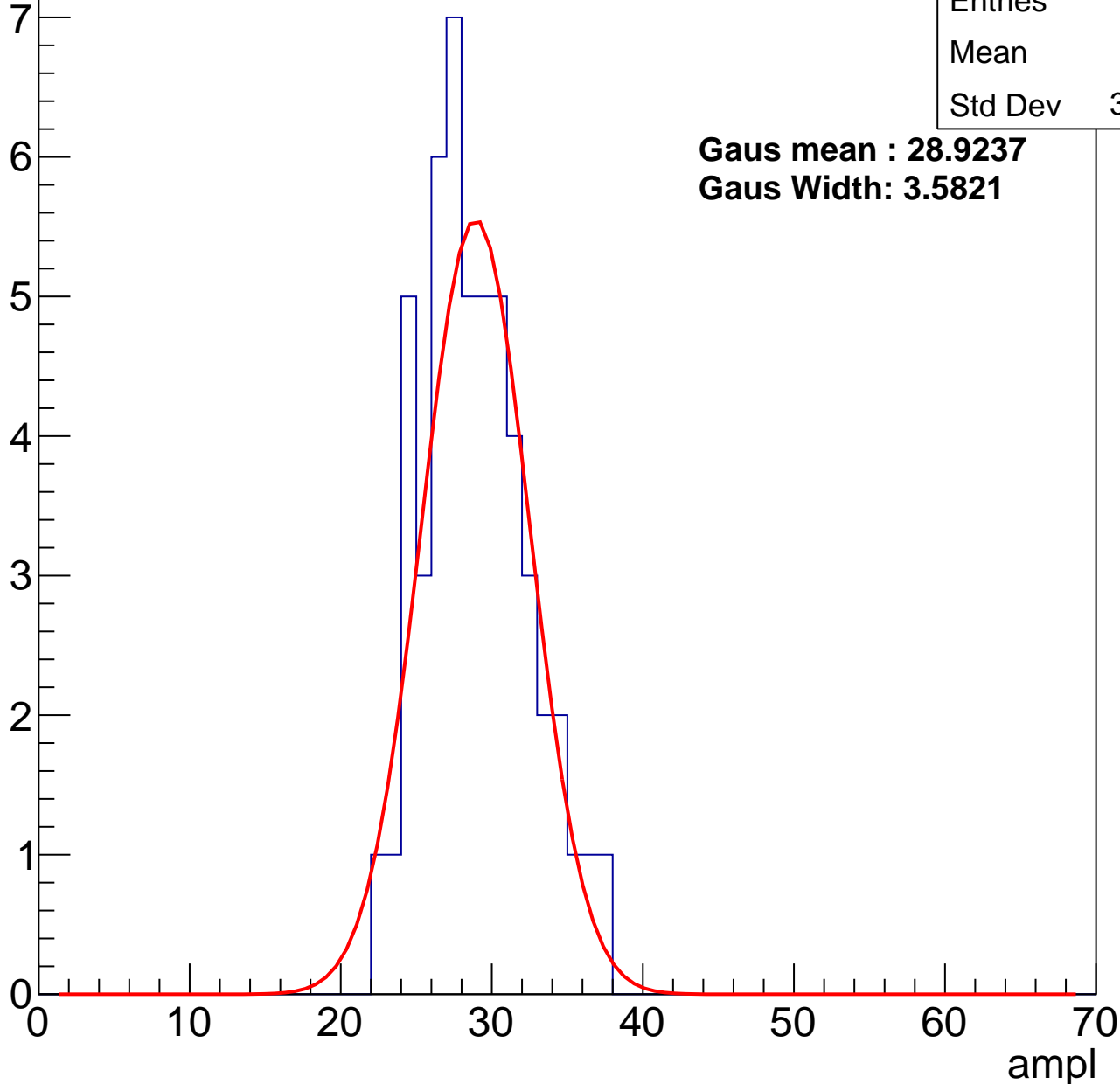
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	28.5
Std Dev	3.428

Gaus mean : 28.9237

Gaus Width: 3.5821



B1L102S, U17-ch91, adc1

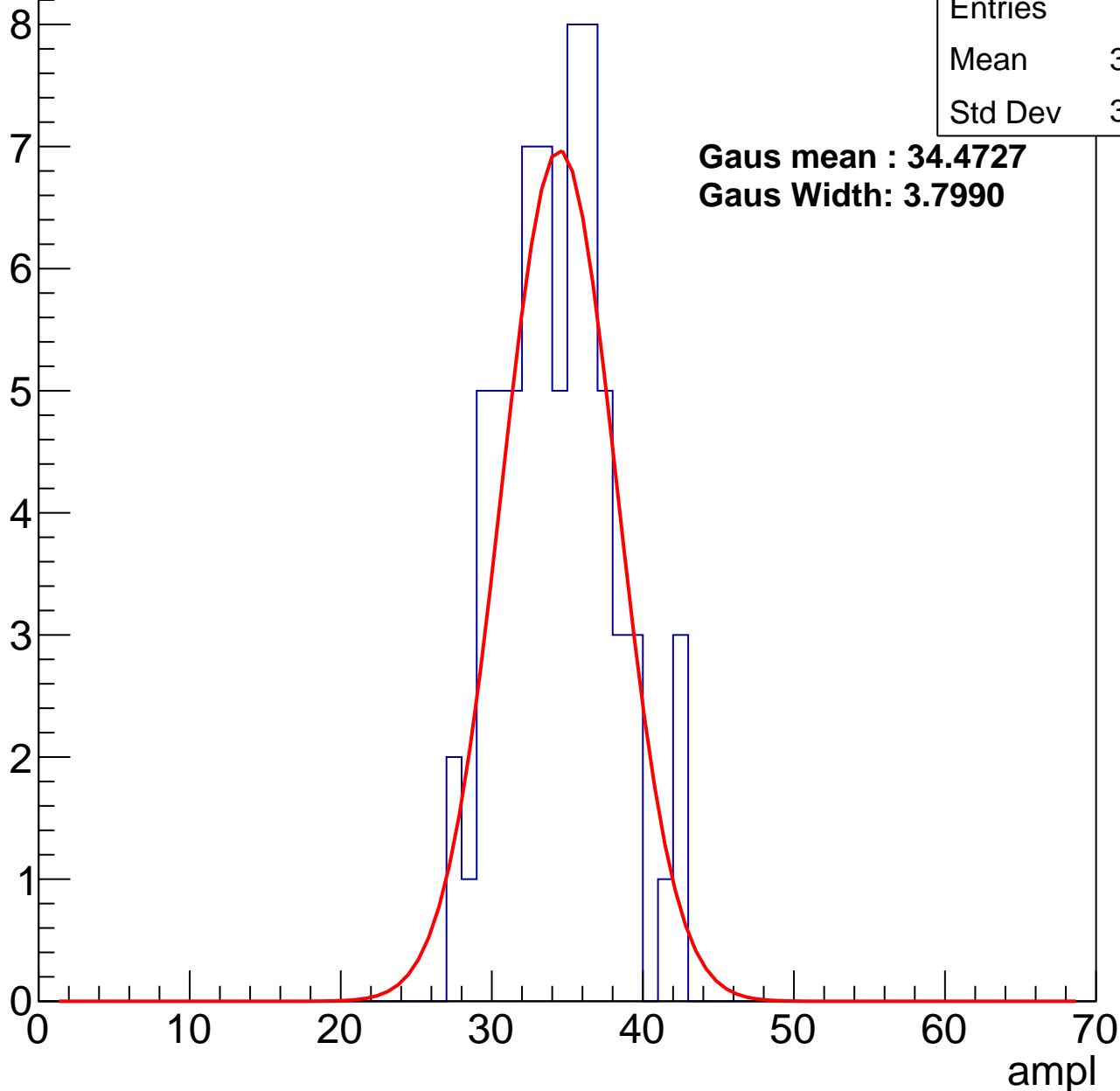
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	33.94
Std Dev	3.572

Gaus mean : 34.4727

Gaus Width: 3.7990



B1L102S, U17-ch91, adc2

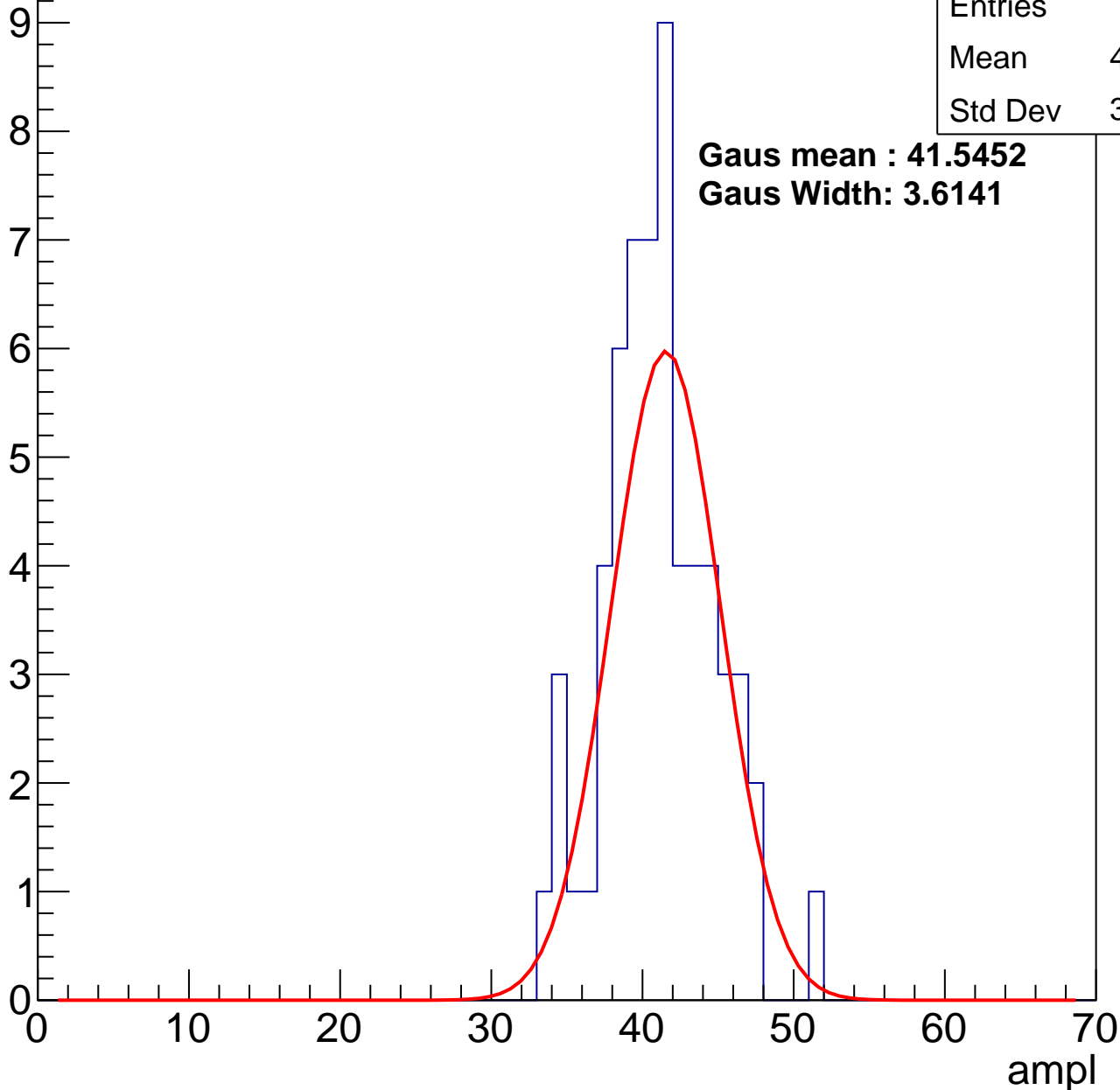
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	40.63
Std Dev	3.578

Gaus mean : 41.5452

Gaus Width: 3.6141

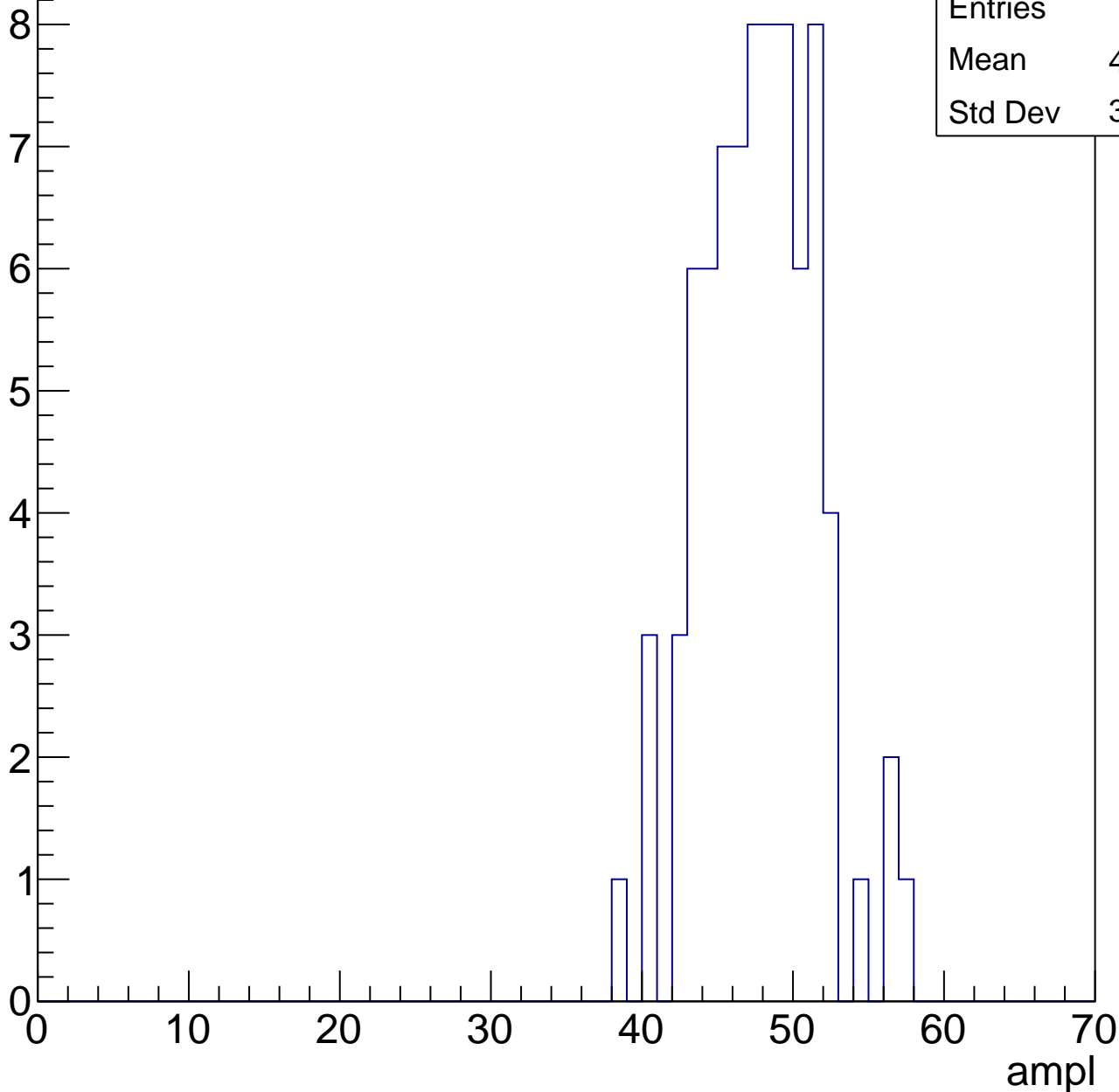


B1L102S, U17-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	47.27
Std Dev	3.758

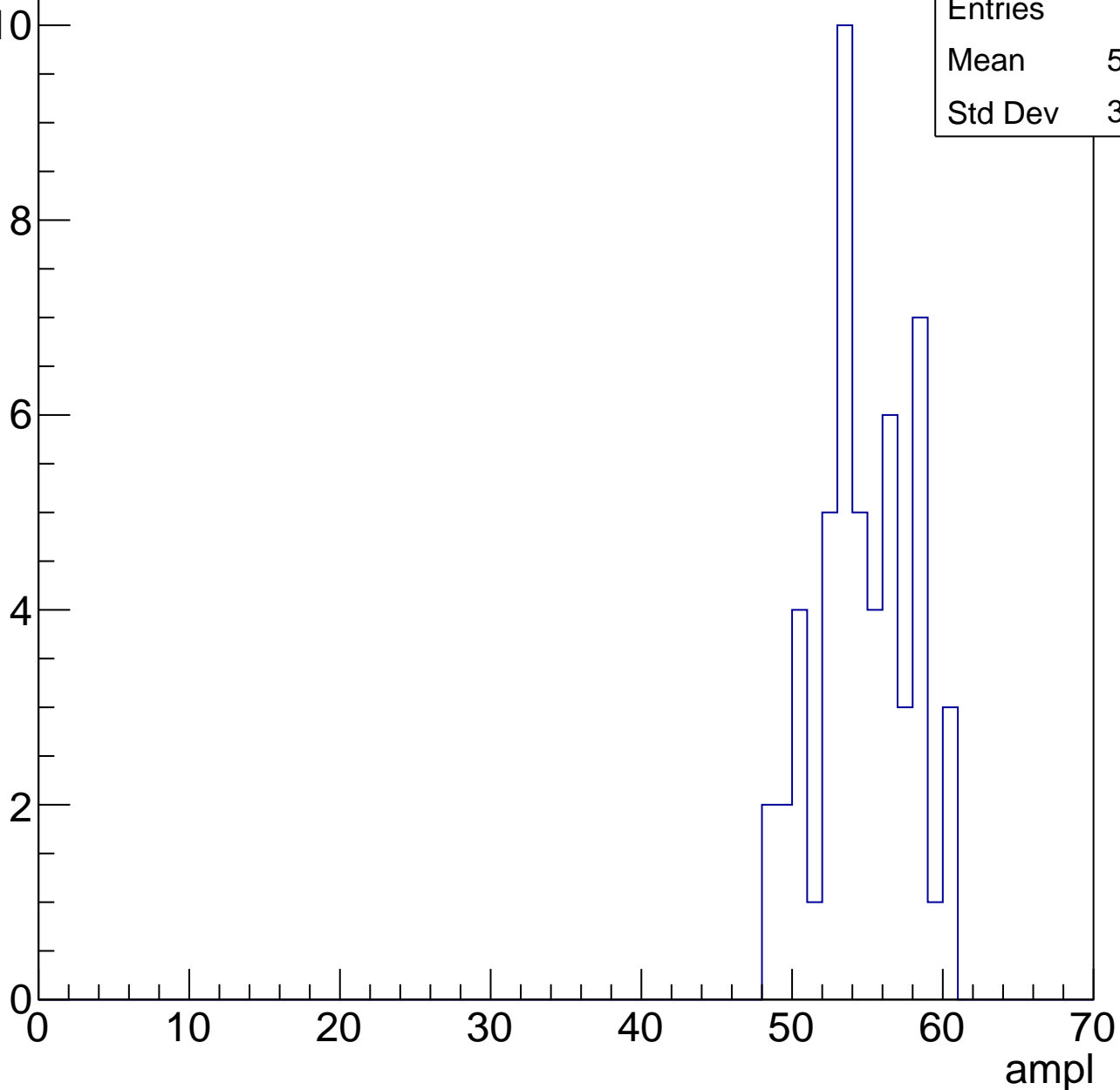


B1L102S, U17-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

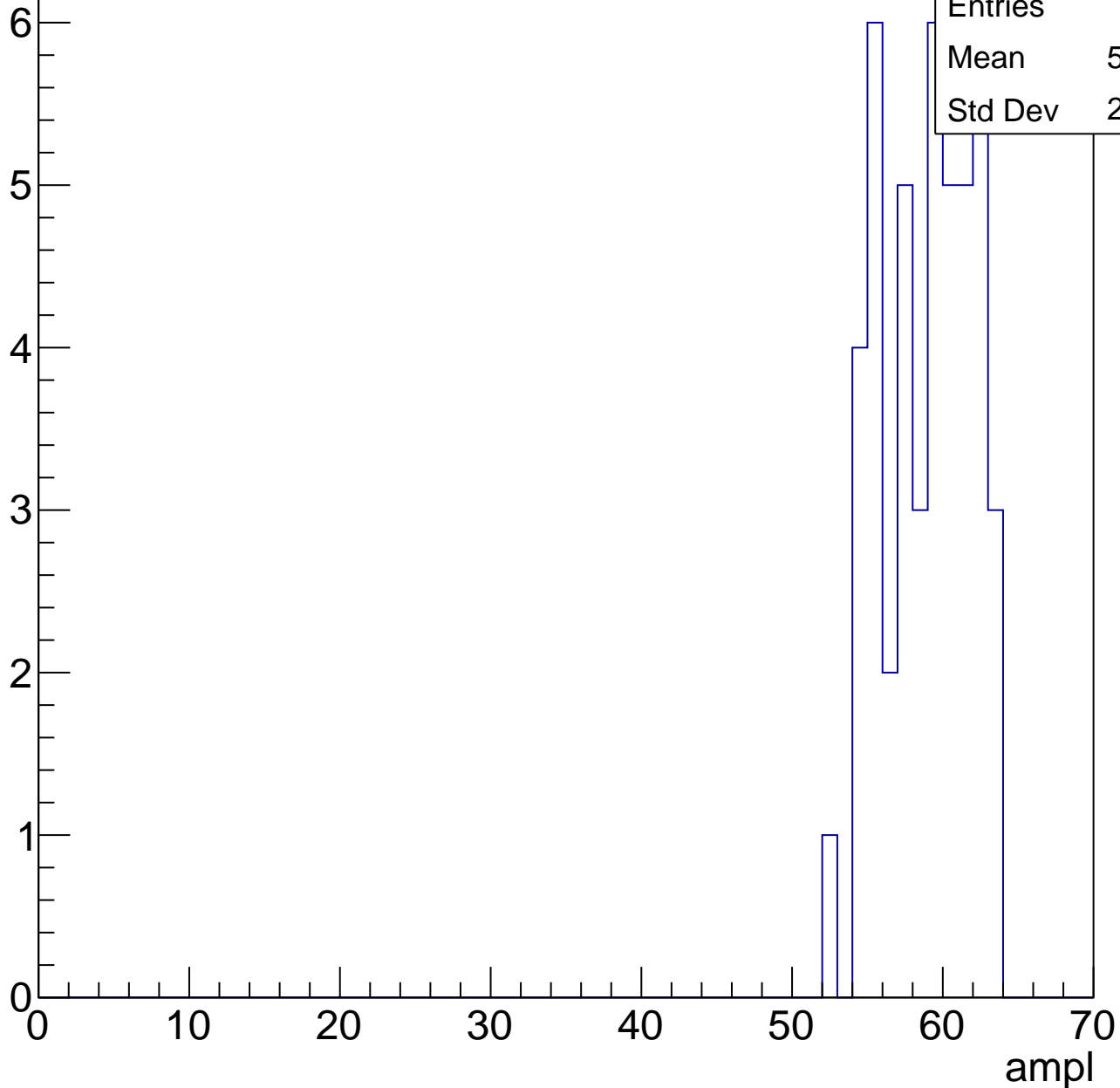
Entries	53
Mean	54.28
Std Dev	3.135



B1L102S, U17-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

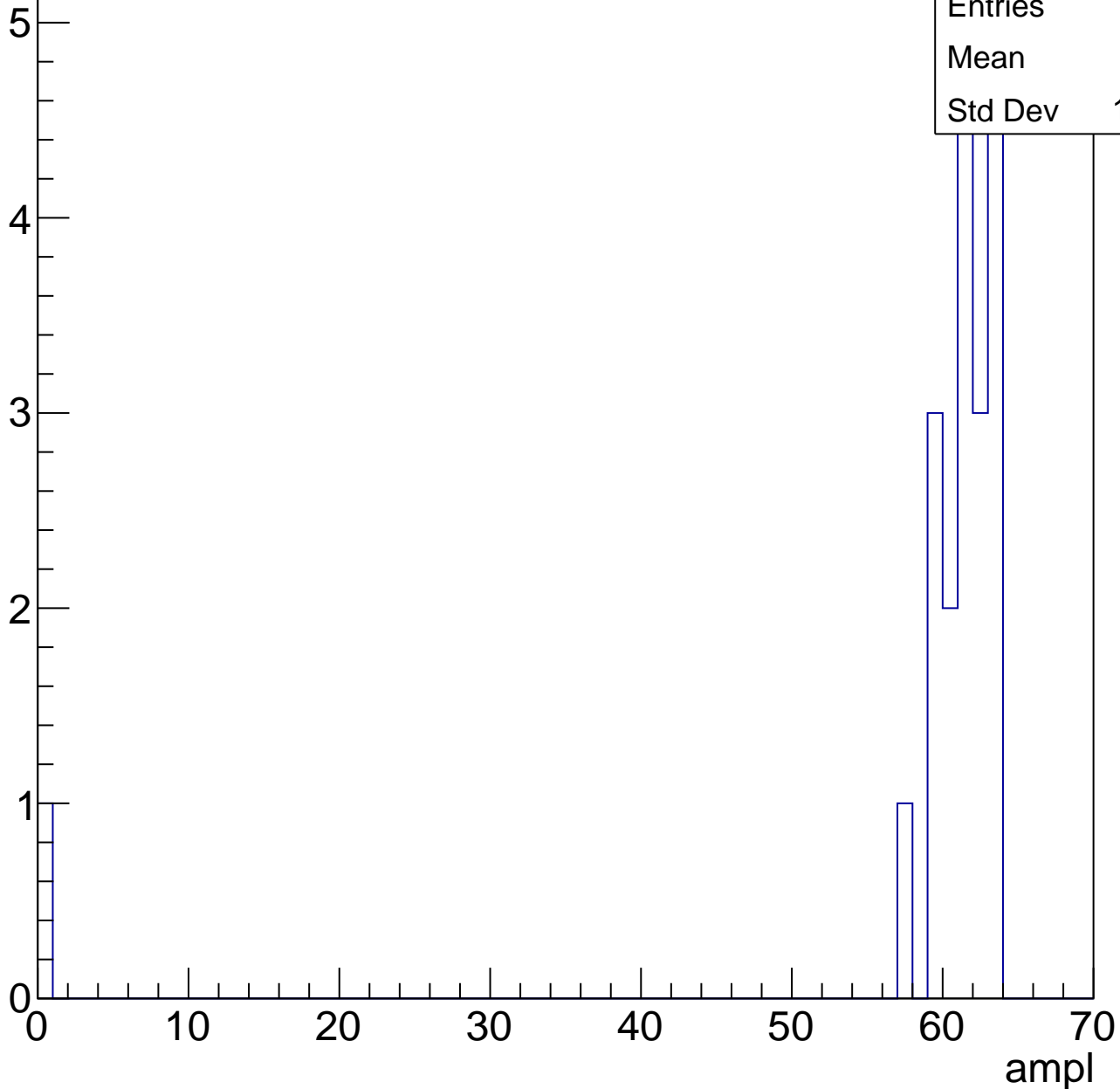


B1L102S, U17-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	58
Std Dev	13.41



B1L102S, U17-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch92, adc0

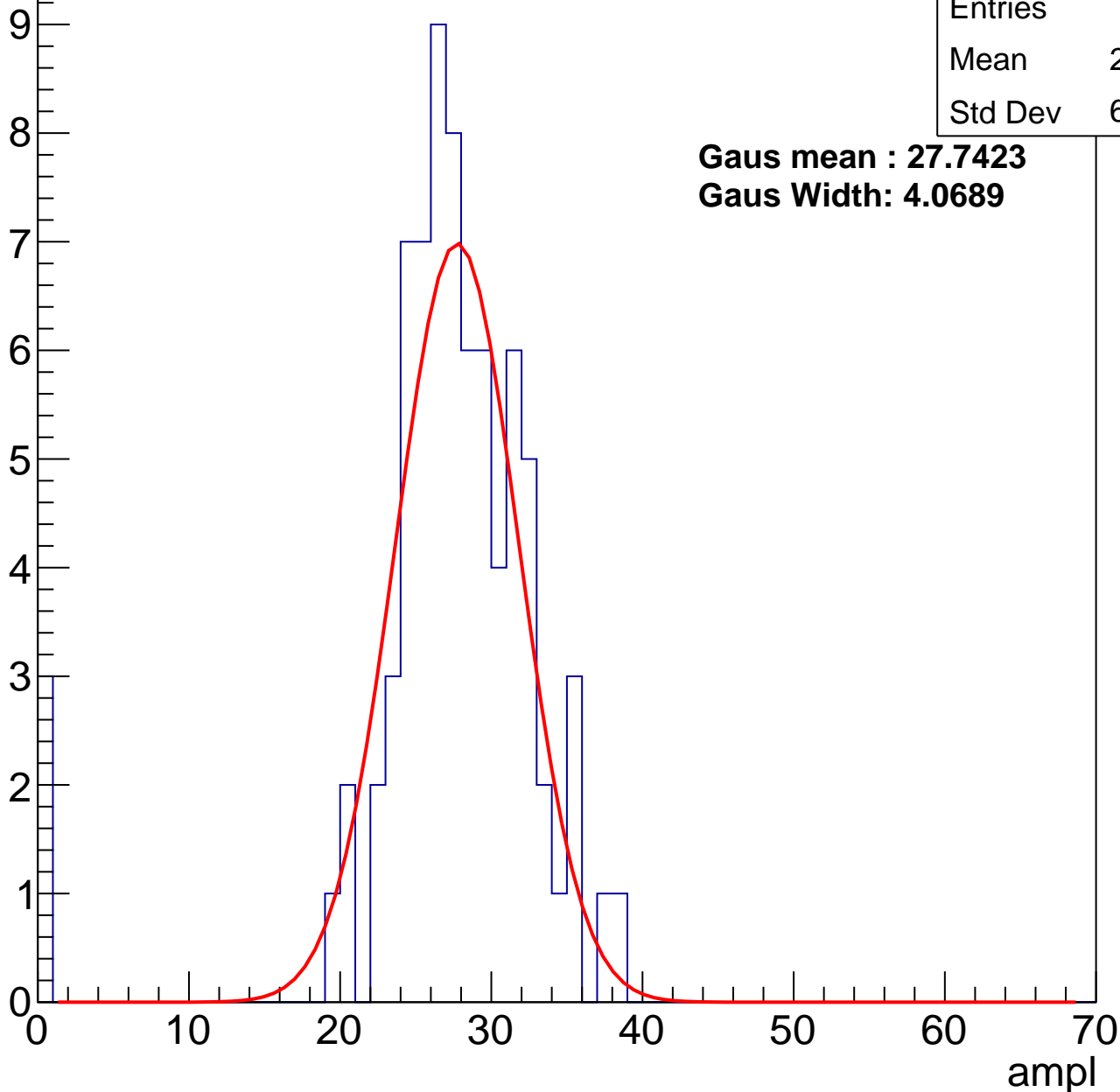
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	26.66
Std Dev	6.605

Gaus mean : 27.7423

Gaus Width: 4.0689



B1L102S, U17-ch92, adc1

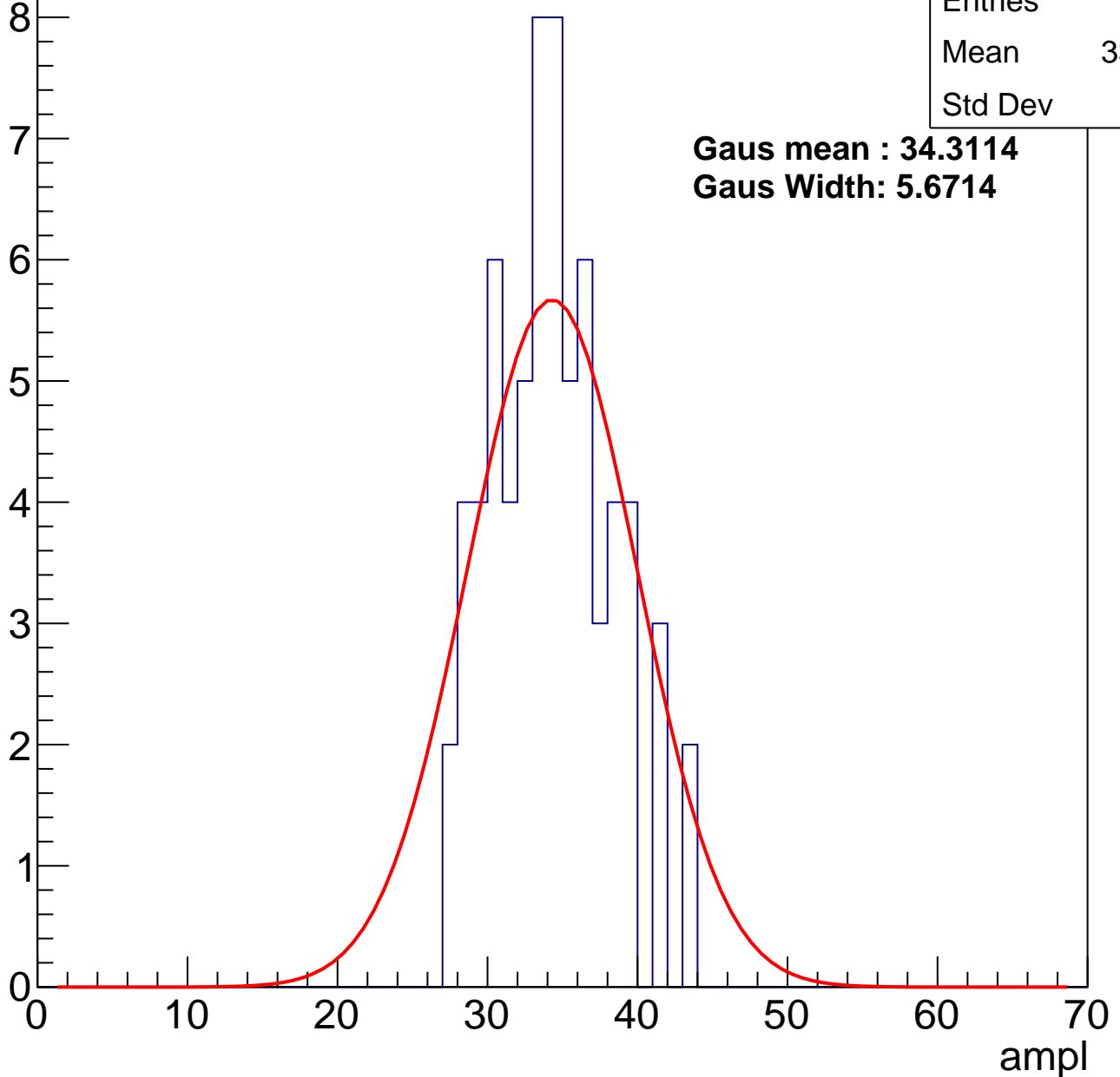
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	33.84
Std Dev	3.89

Gaus mean : 34.3114

Gaus Width: 5.6714



B1L102S, U17-ch92, adc2

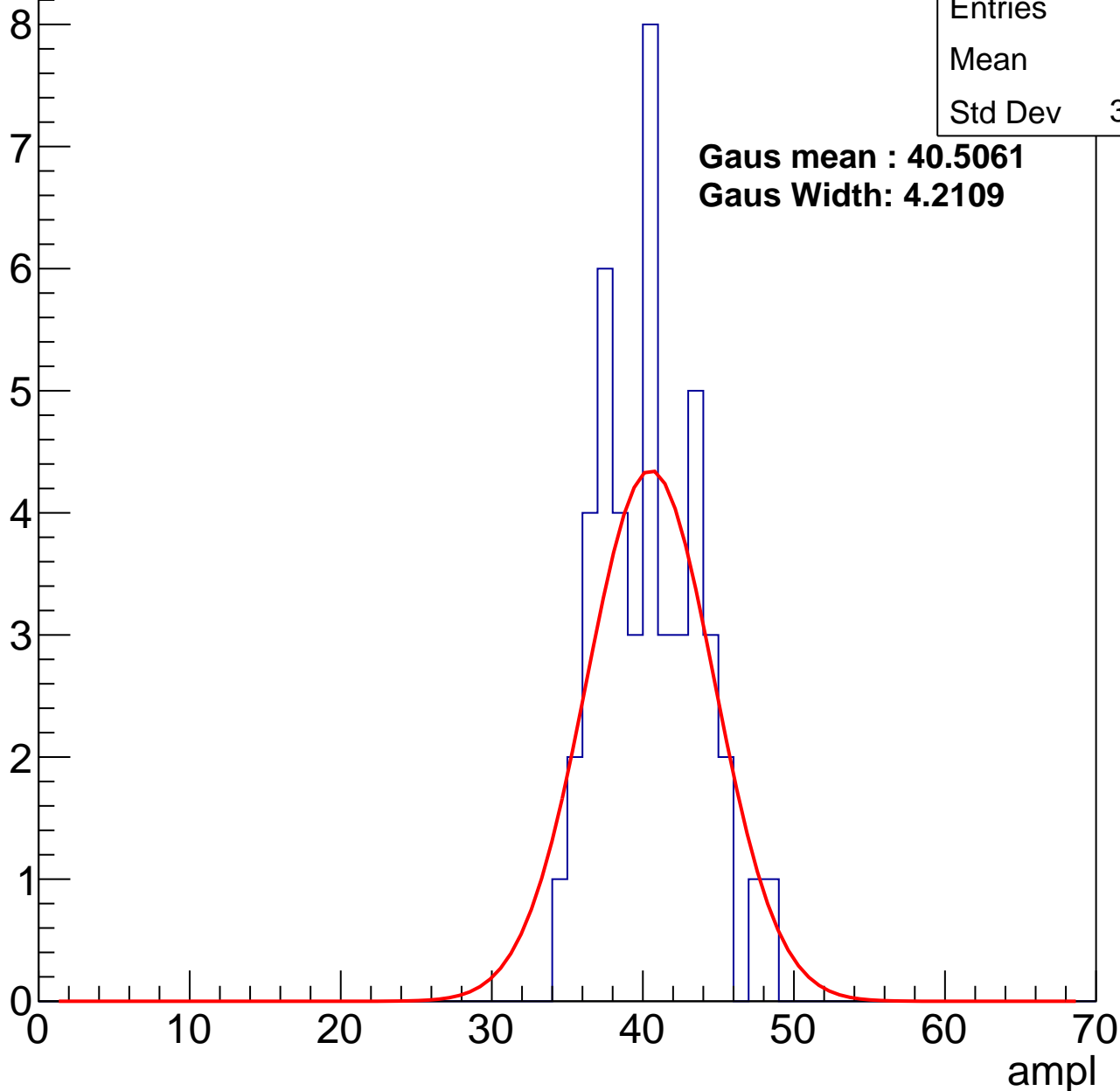
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	40
Std Dev	3.277

Gaus mean : 40.5061

Gaus Width: 4.2109



B1L102S, U17-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	45.72
Std Dev	3.844

Entry

10

8

6

4

2

0

0

10

20

30

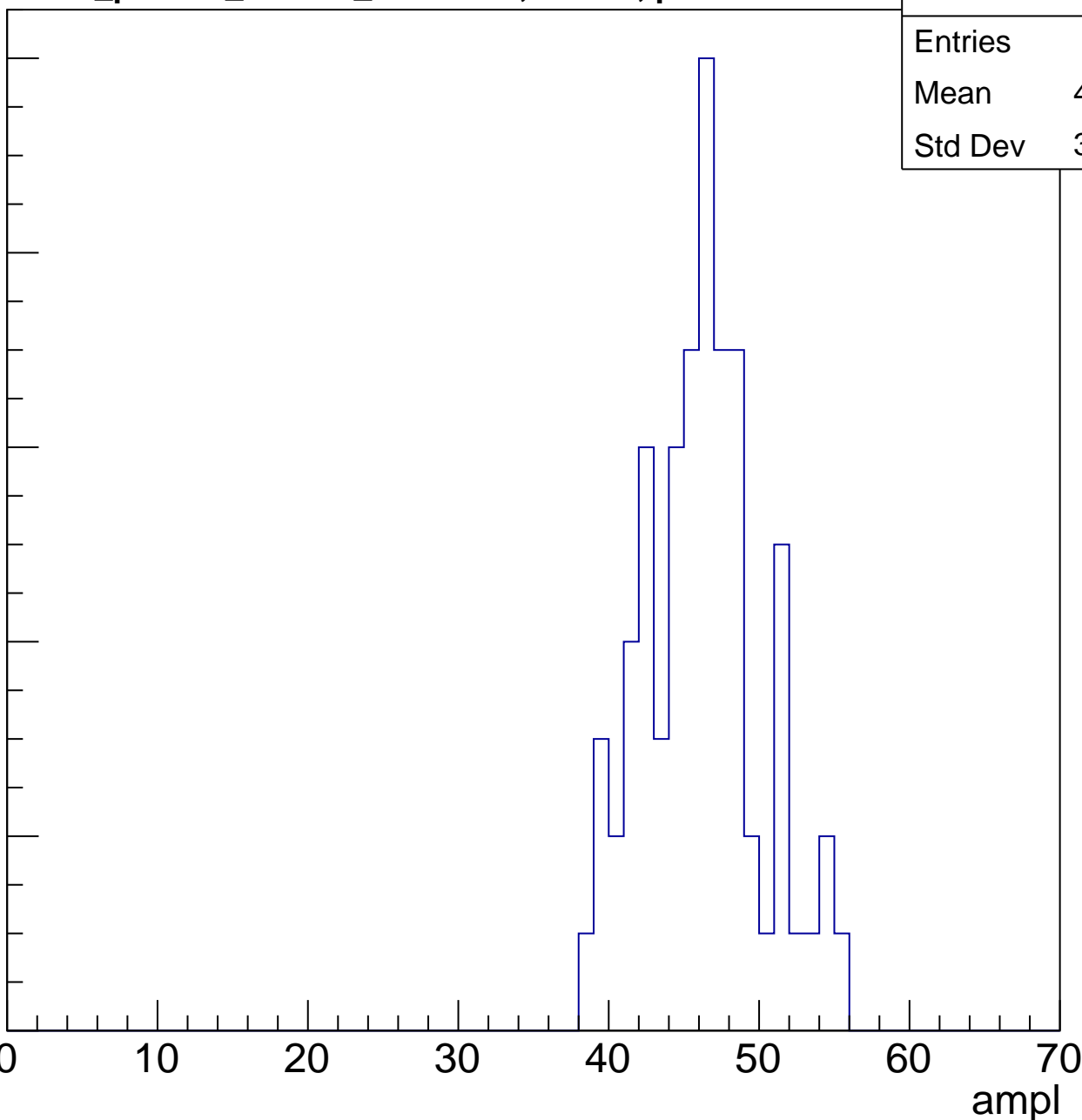
40

50

60

70

ampl

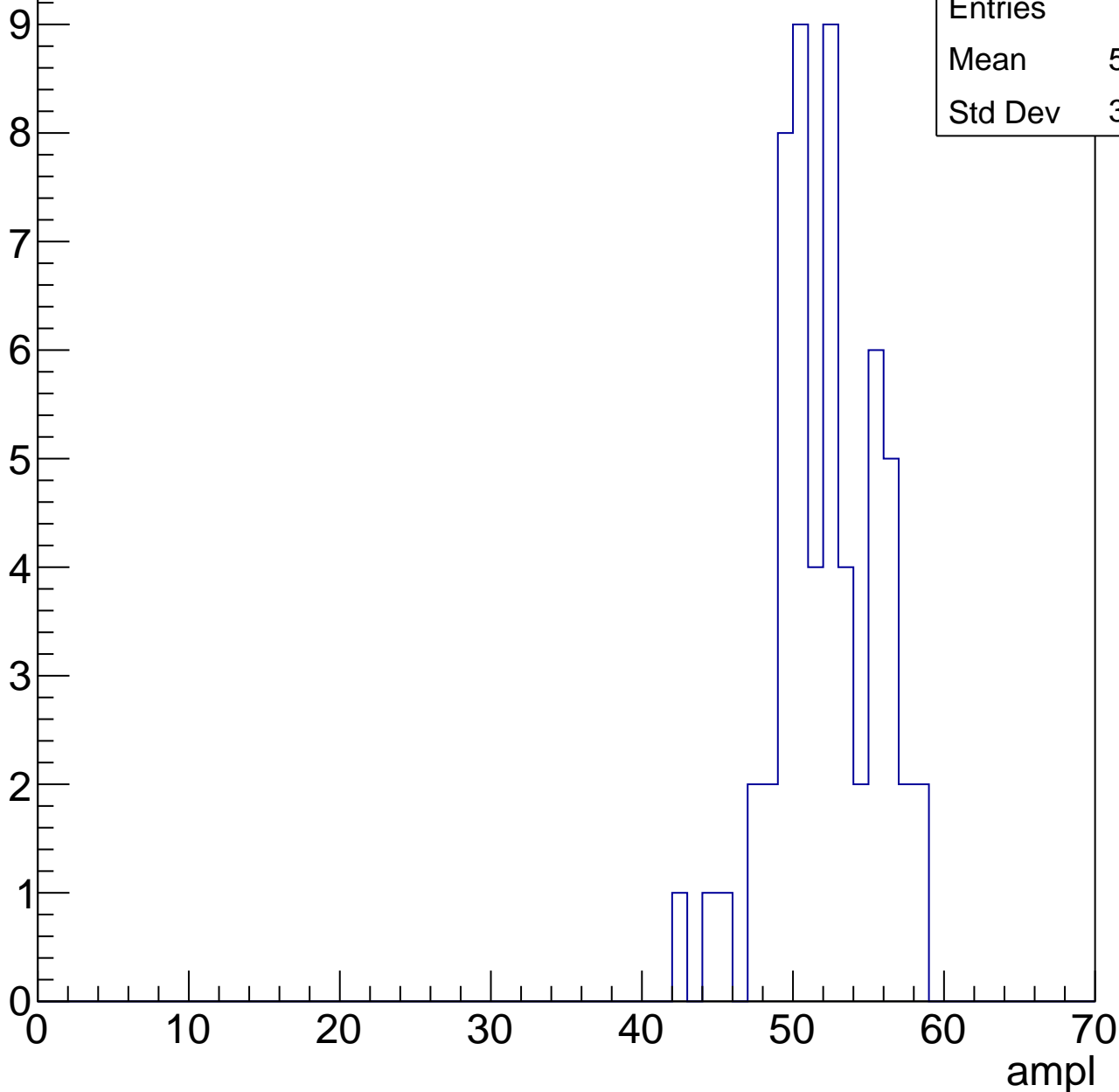


B1L102S, U17-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	51.64
Std Dev	3.392

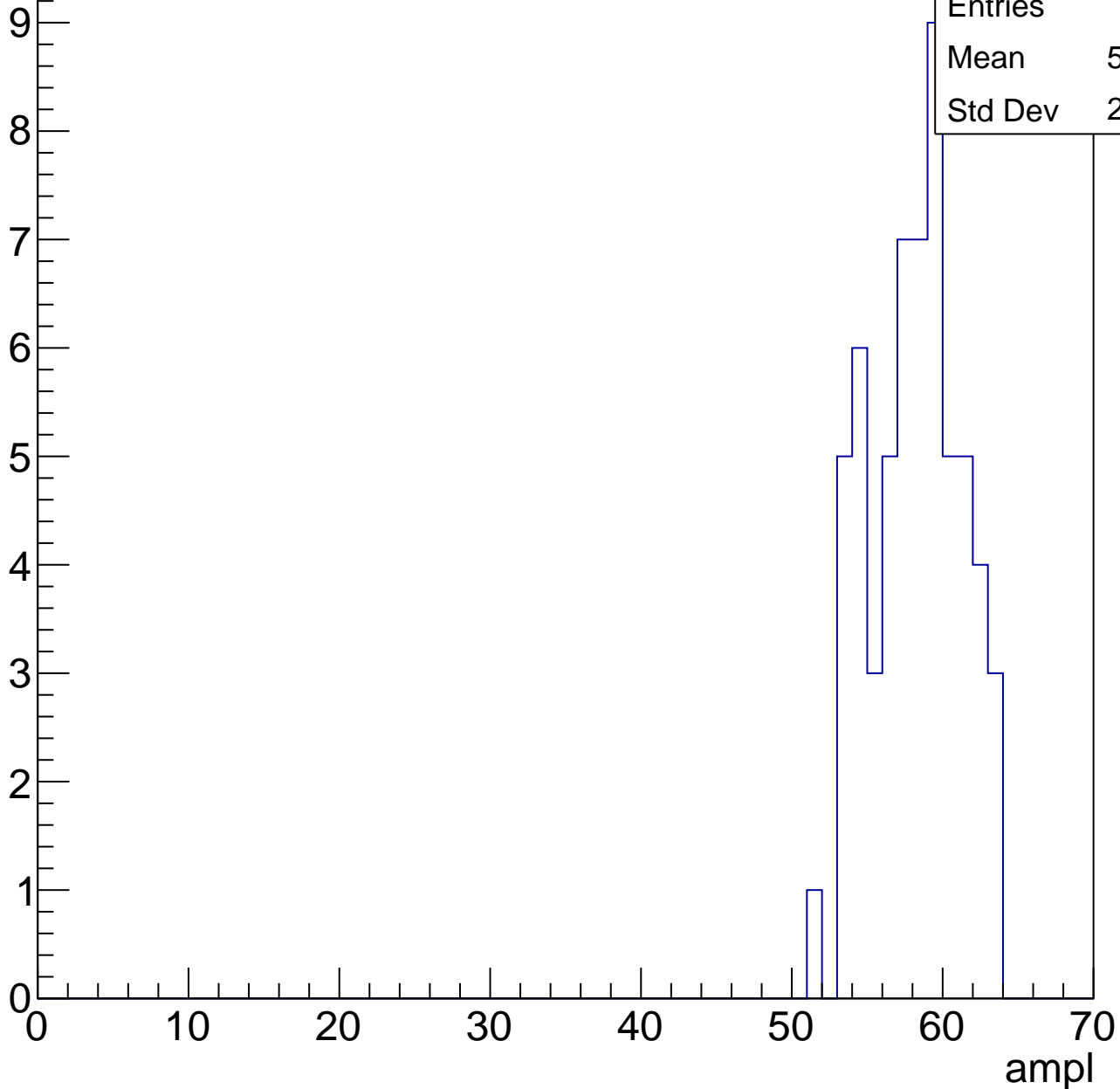


B1L102S, U17-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	57.72
Std Dev	2.978

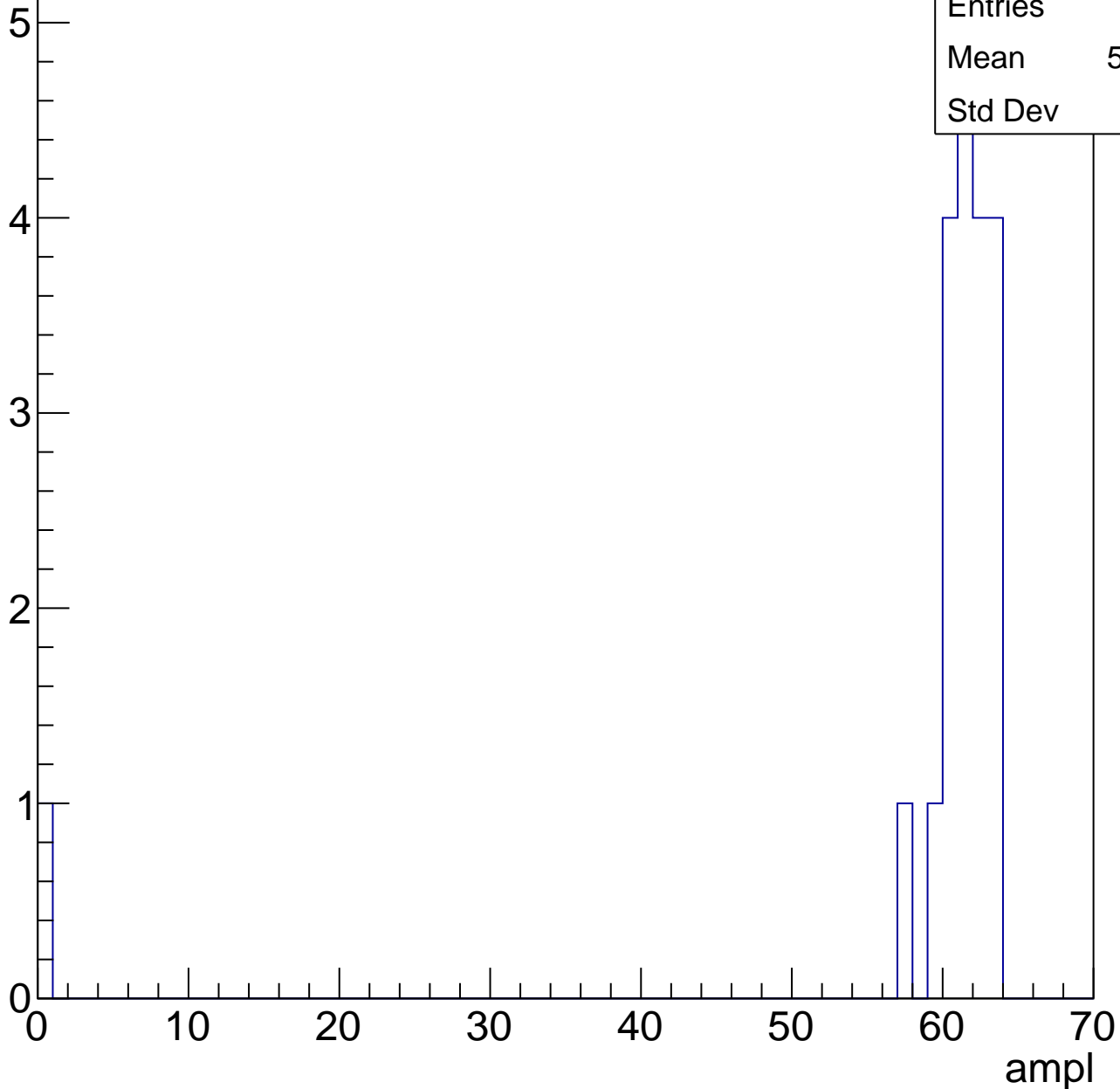


B1L102S, U17-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

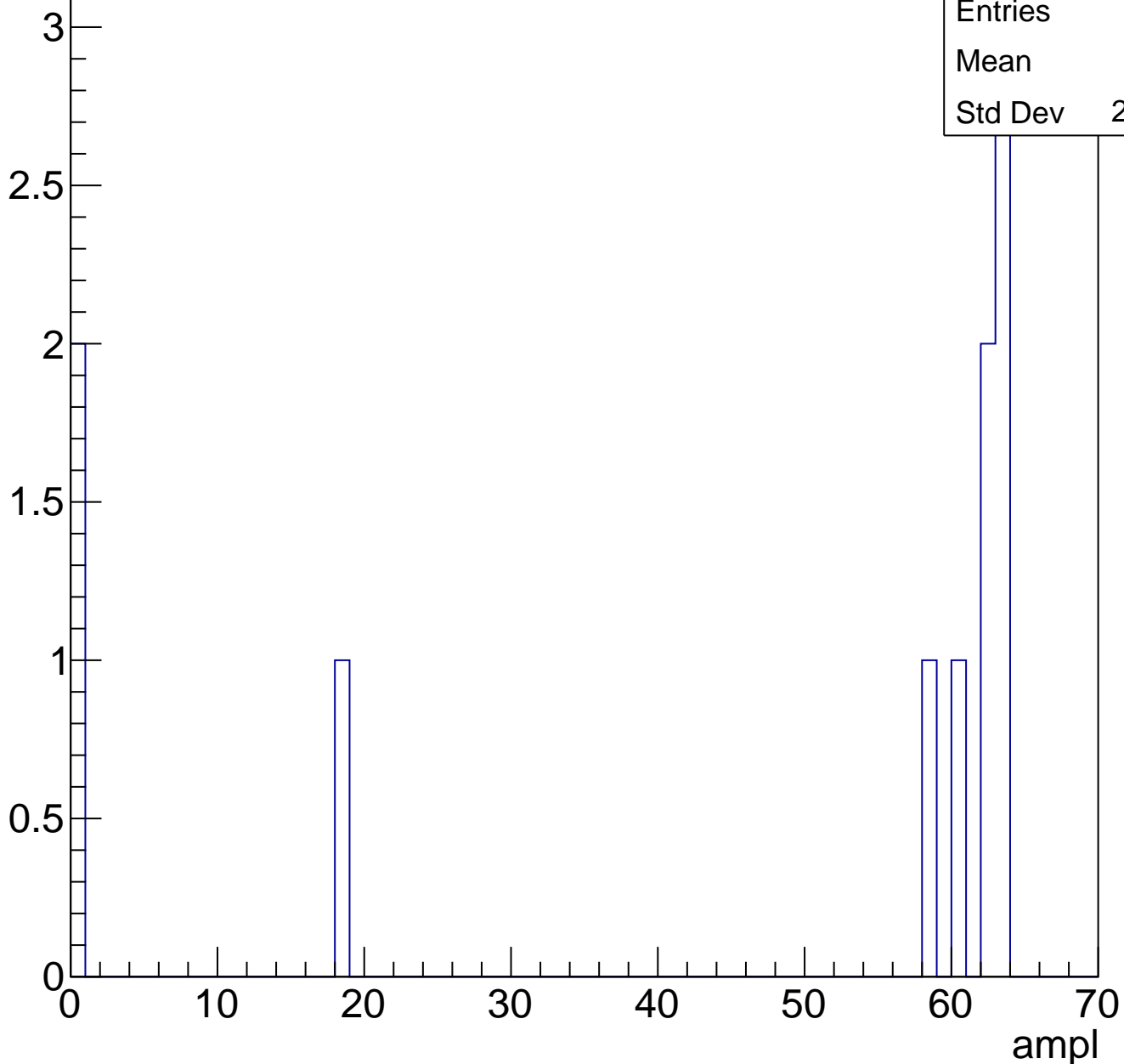
Entries	20
Mean	58.05
Std Dev	13.4



B1L102S, U17-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch93, adc0

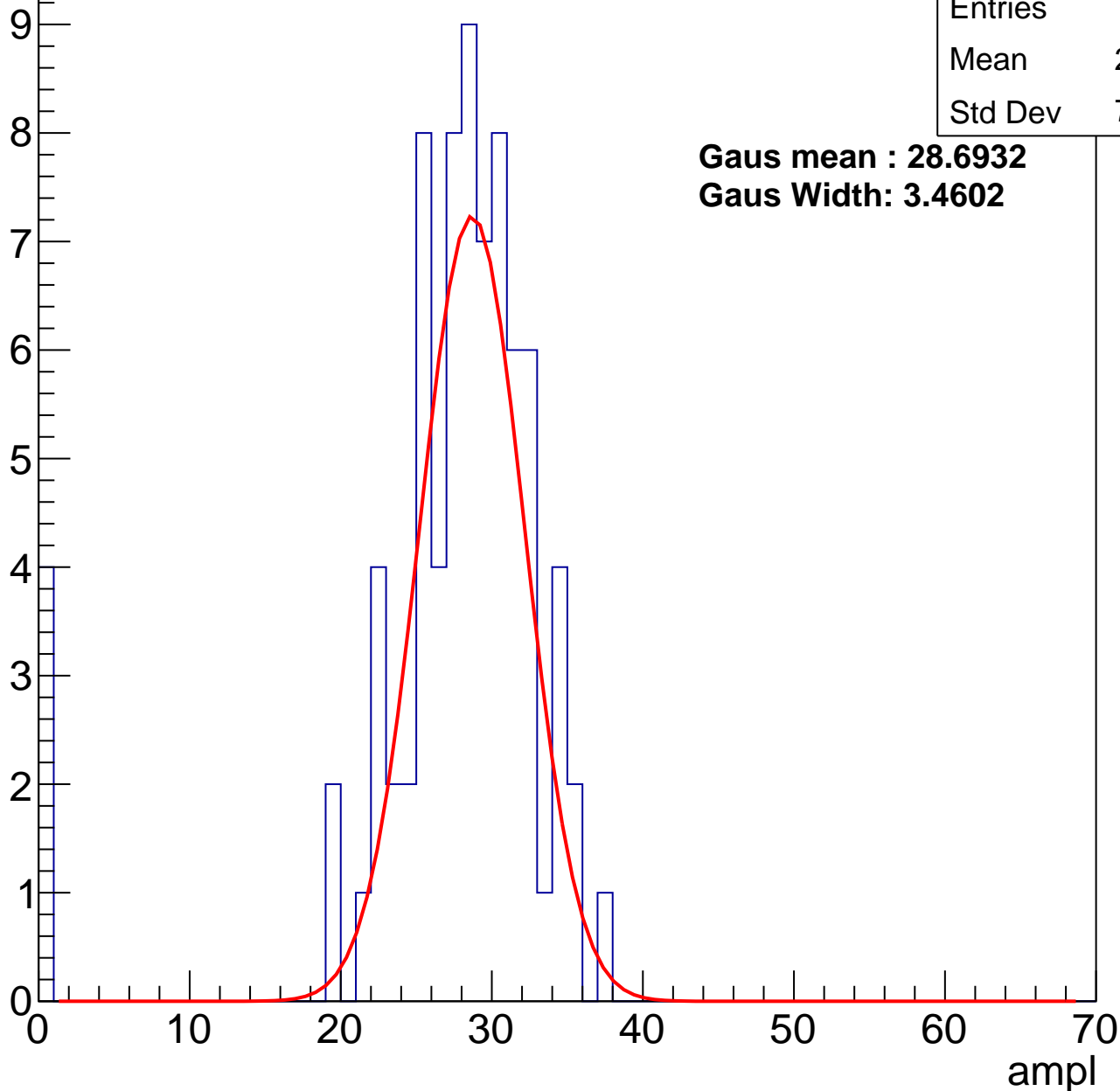
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	26.71
Std Dev	7.181

Gaus mean : 28.6932

Gaus Width: 3.4602



B1L102S, U17-ch93, adc1

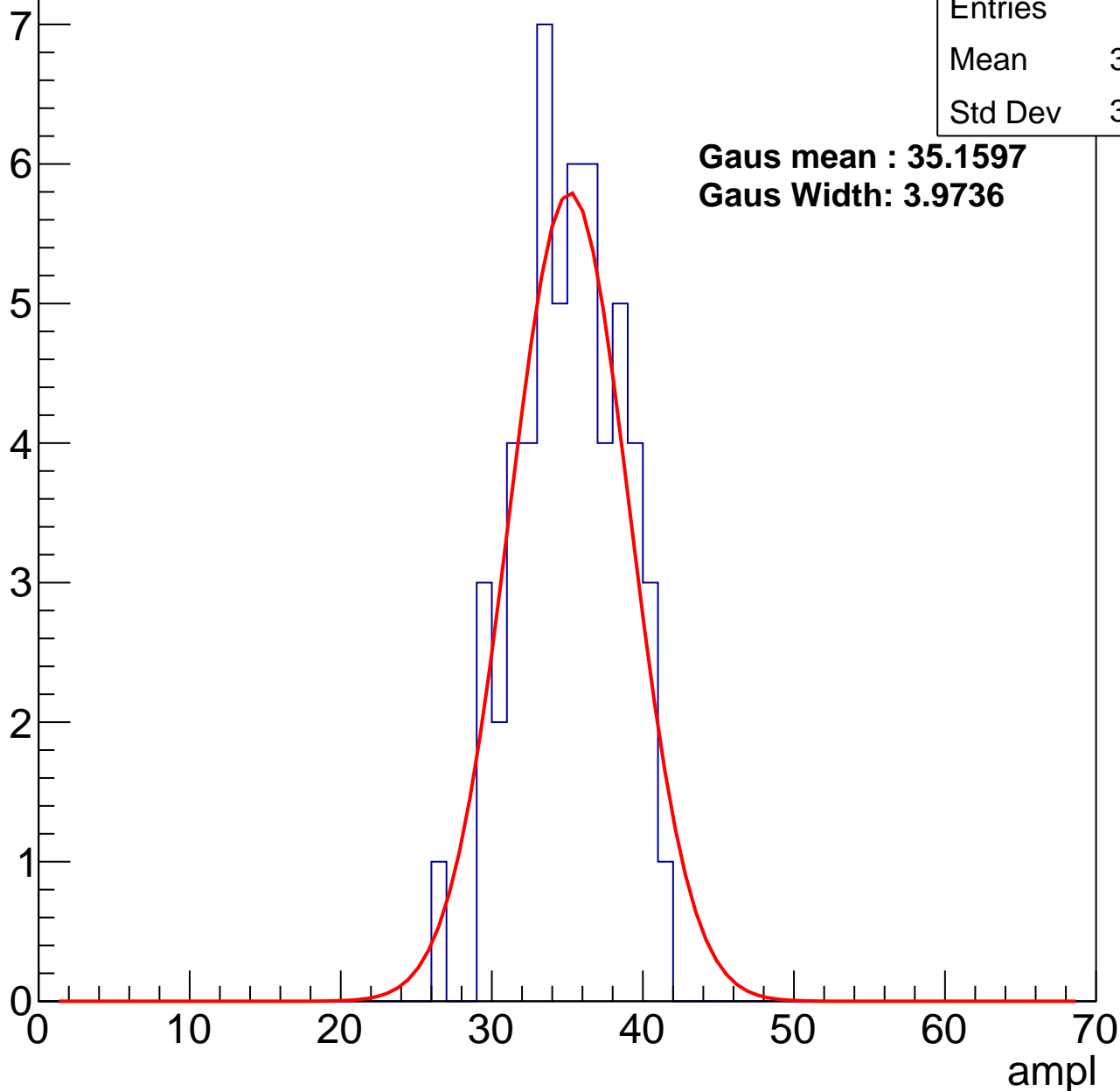
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	34.67
Std Dev	3.325

Gaus mean : 35.1597

Gaus Width: 3.9736



B1L102S, U17-ch93, adc2

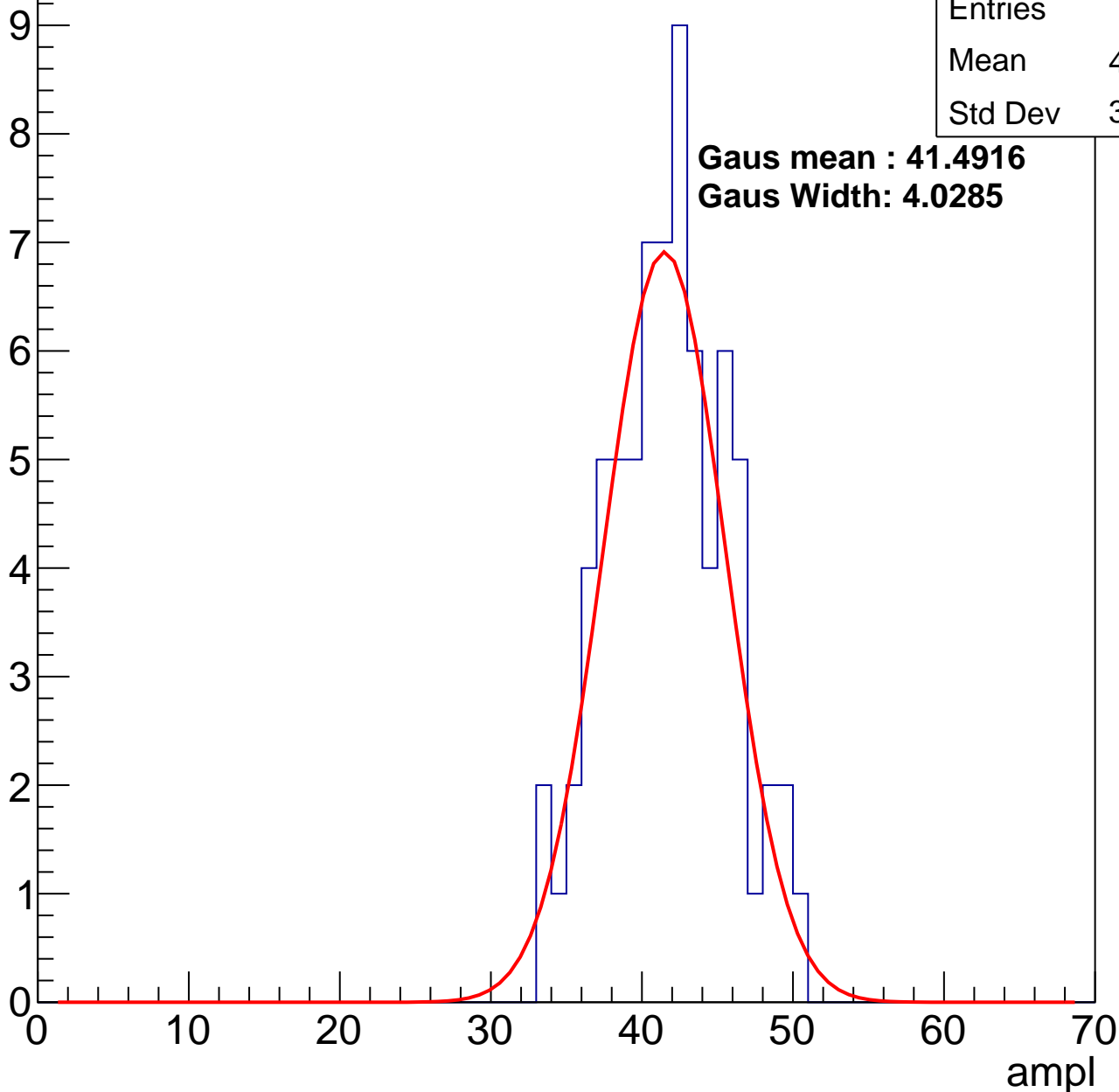
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.27
Std Dev	3.905

Gaus mean : 41.4916

Gaus Width: 4.0285

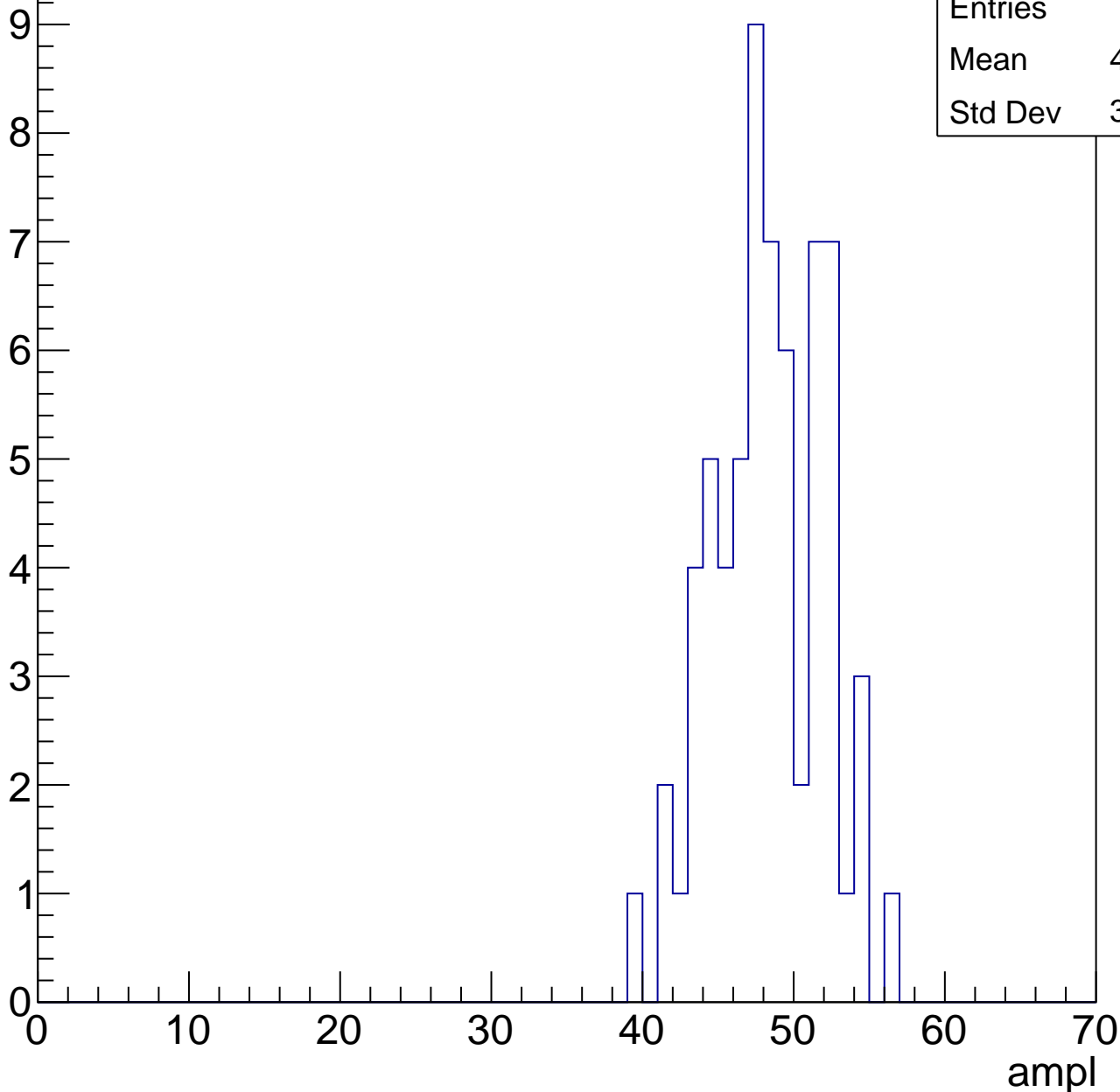


B1L102S, U17-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

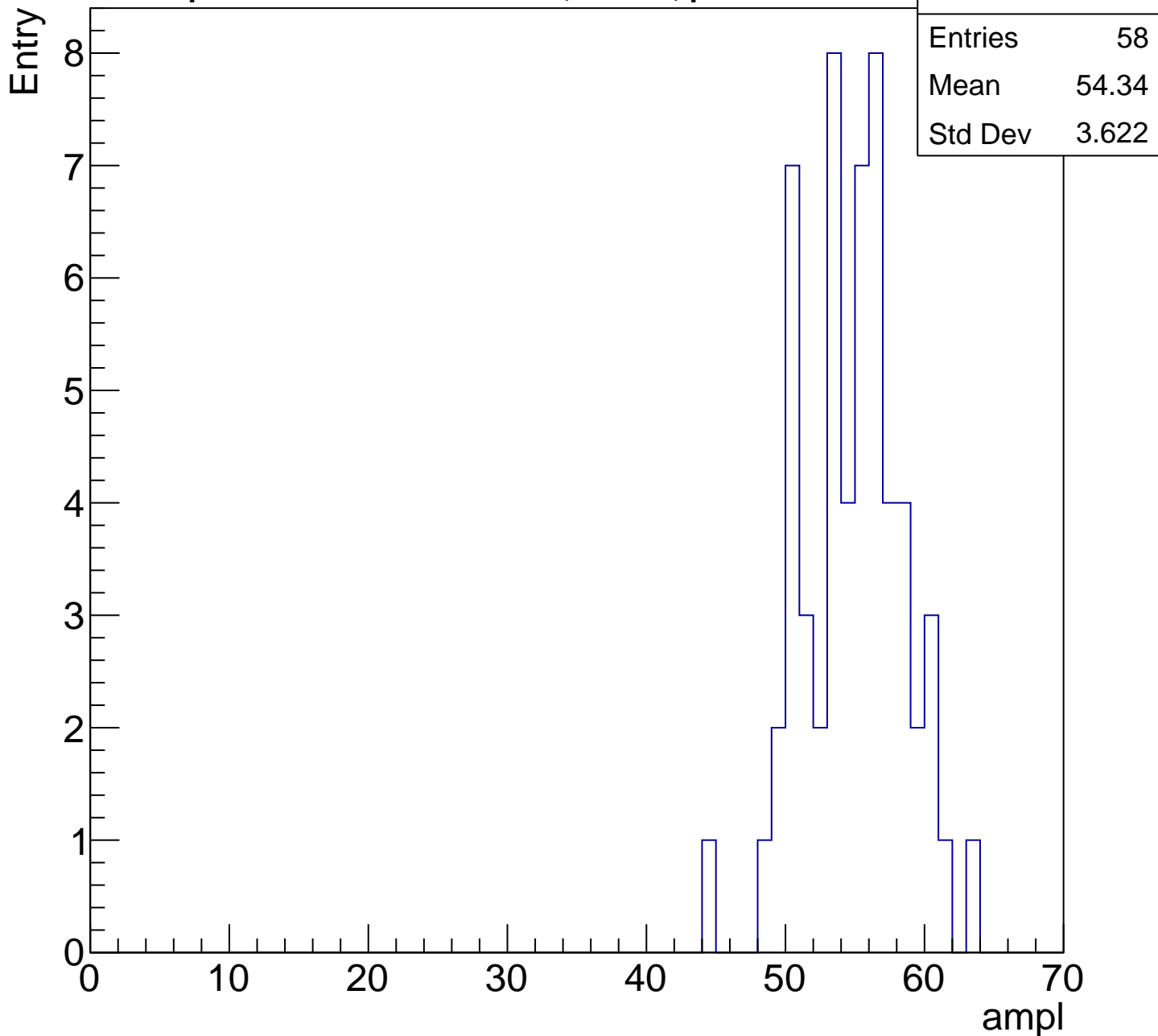
Entry

Entries	65
Mean	47.85
Std Dev	3.604



B1L102S, U17-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

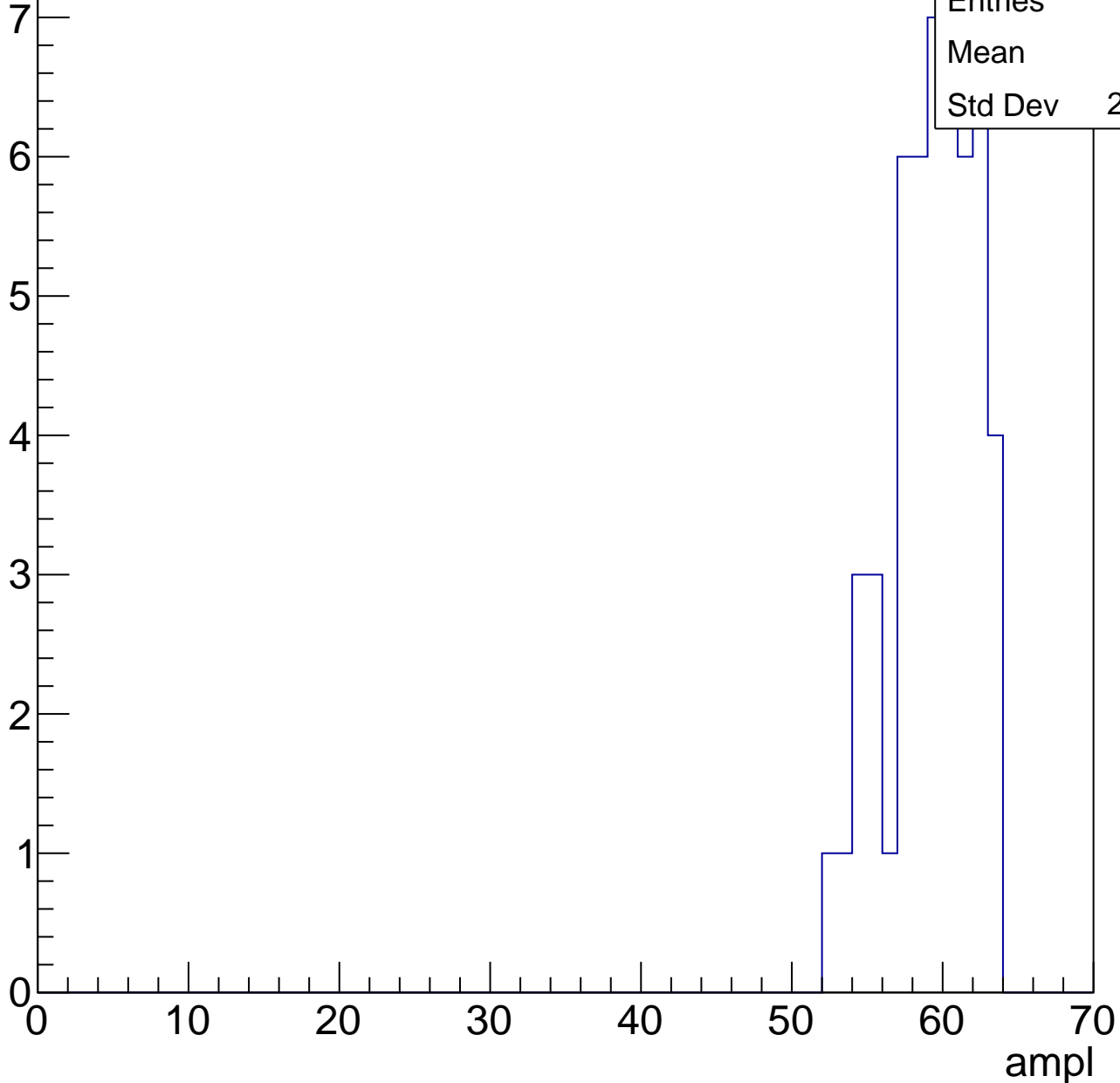


B1L102S, U17-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.9
Std Dev	2.789

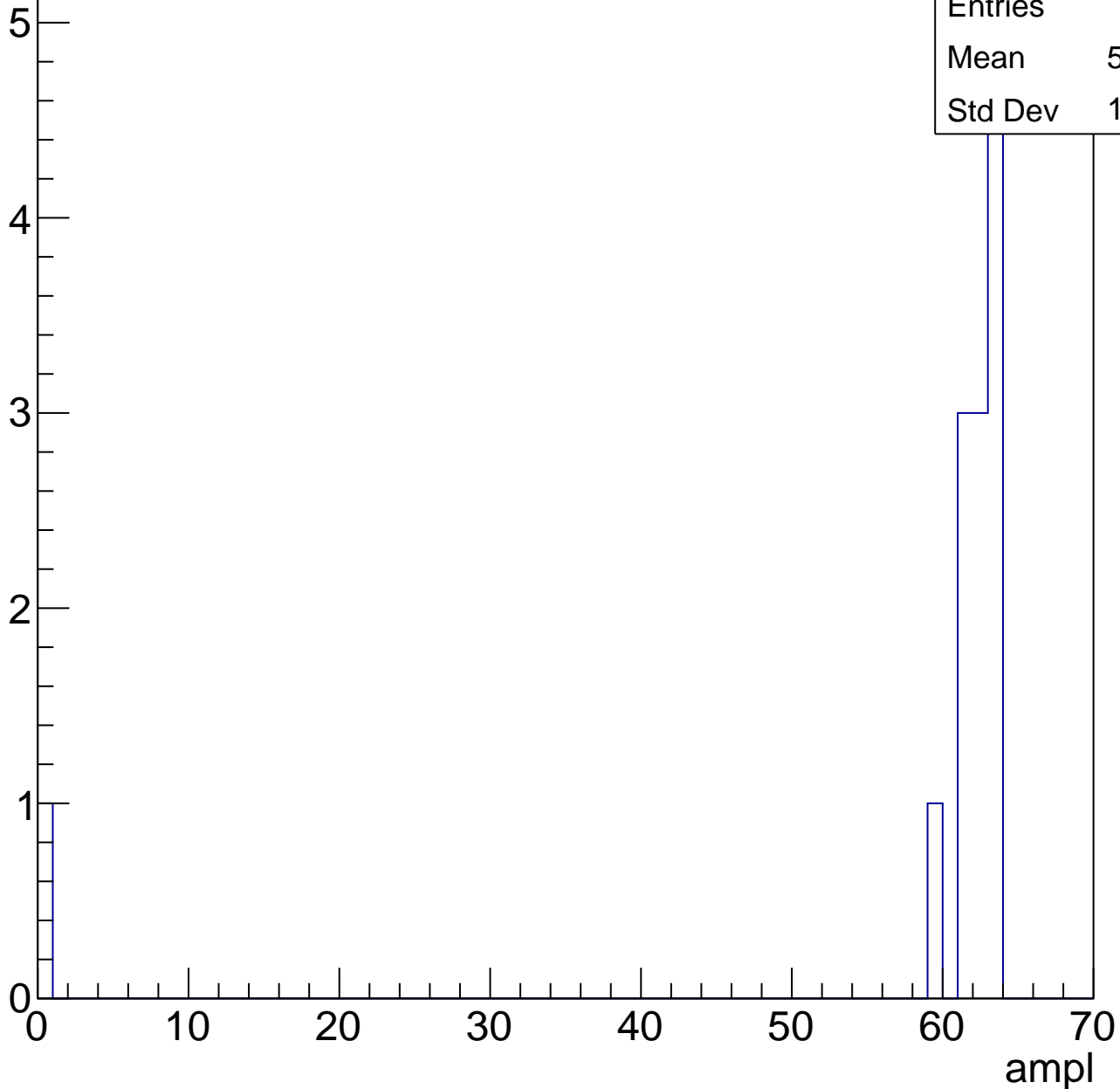


B1L102S, U17-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	57.15
Std Dev	16.54



B1L102S, U17-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch94, adc0

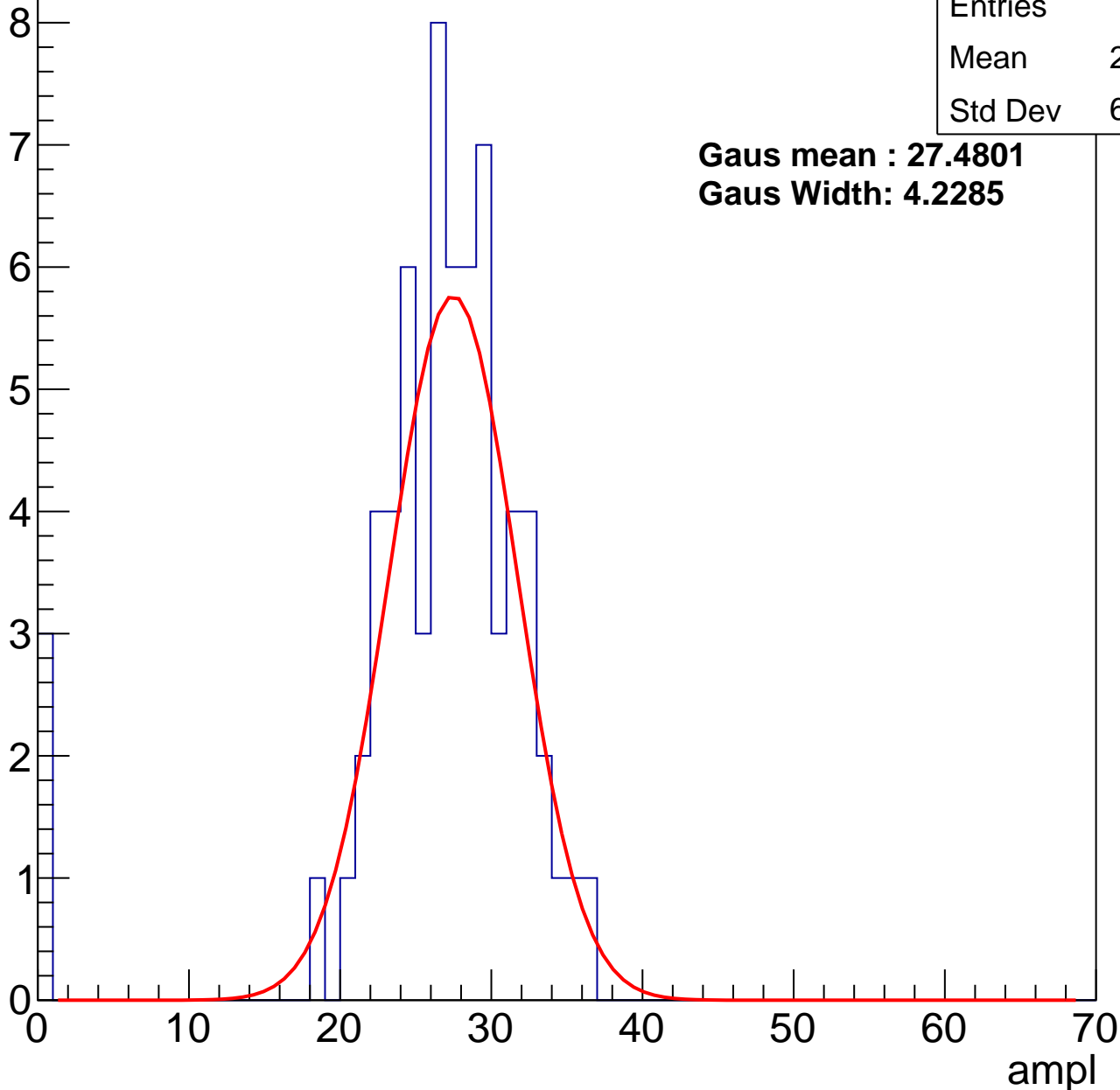
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	25.87
Std Dev	6.727

Gaus mean : 27.4801

Gaus Width: 4.2285



B1L102S, U17-ch94, adc1

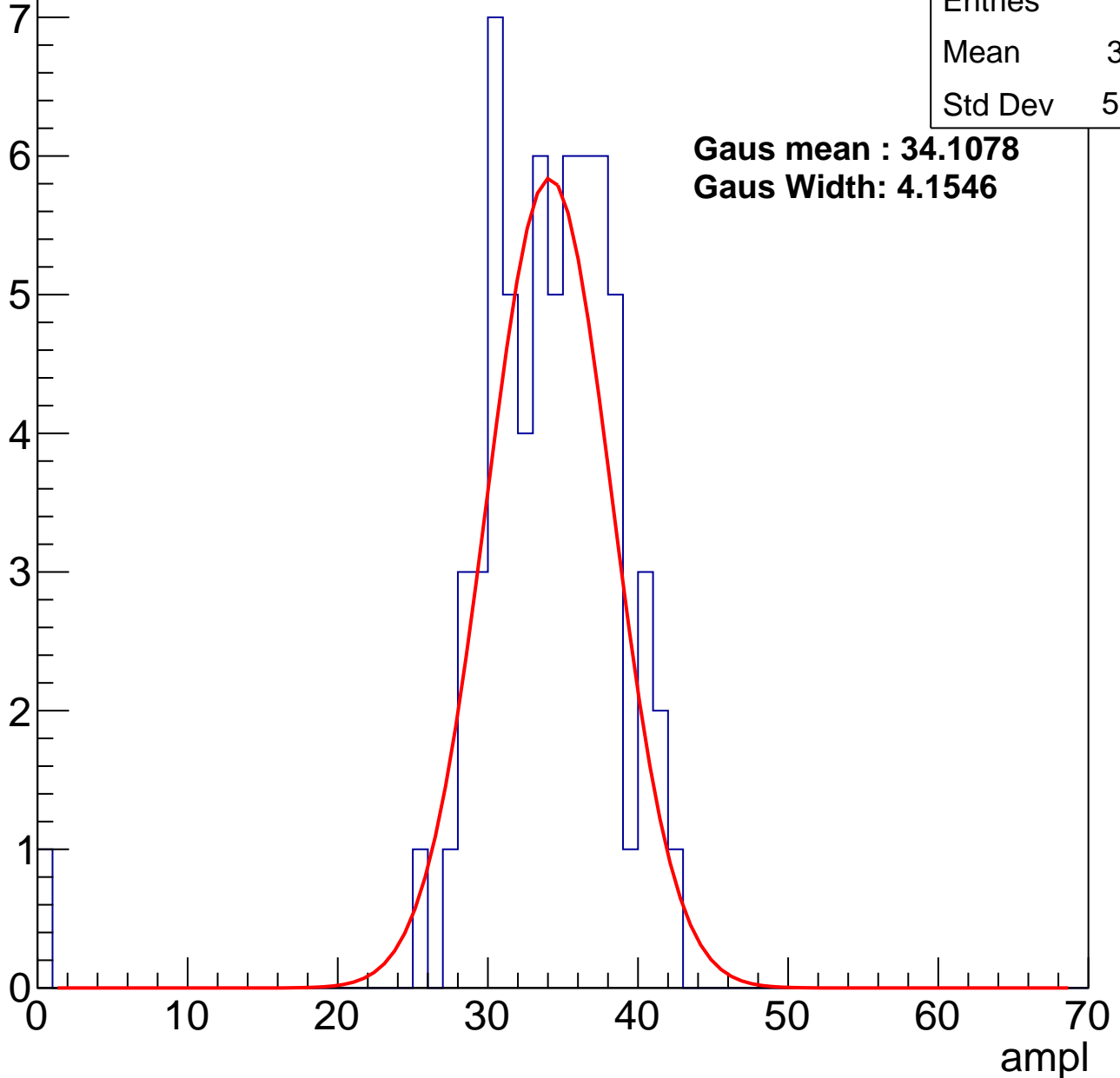
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	33.41
Std Dev	5.619

Gaus mean : 34.1078

Gaus Width: 4.1546



B1L102S, U17-ch94, adc2

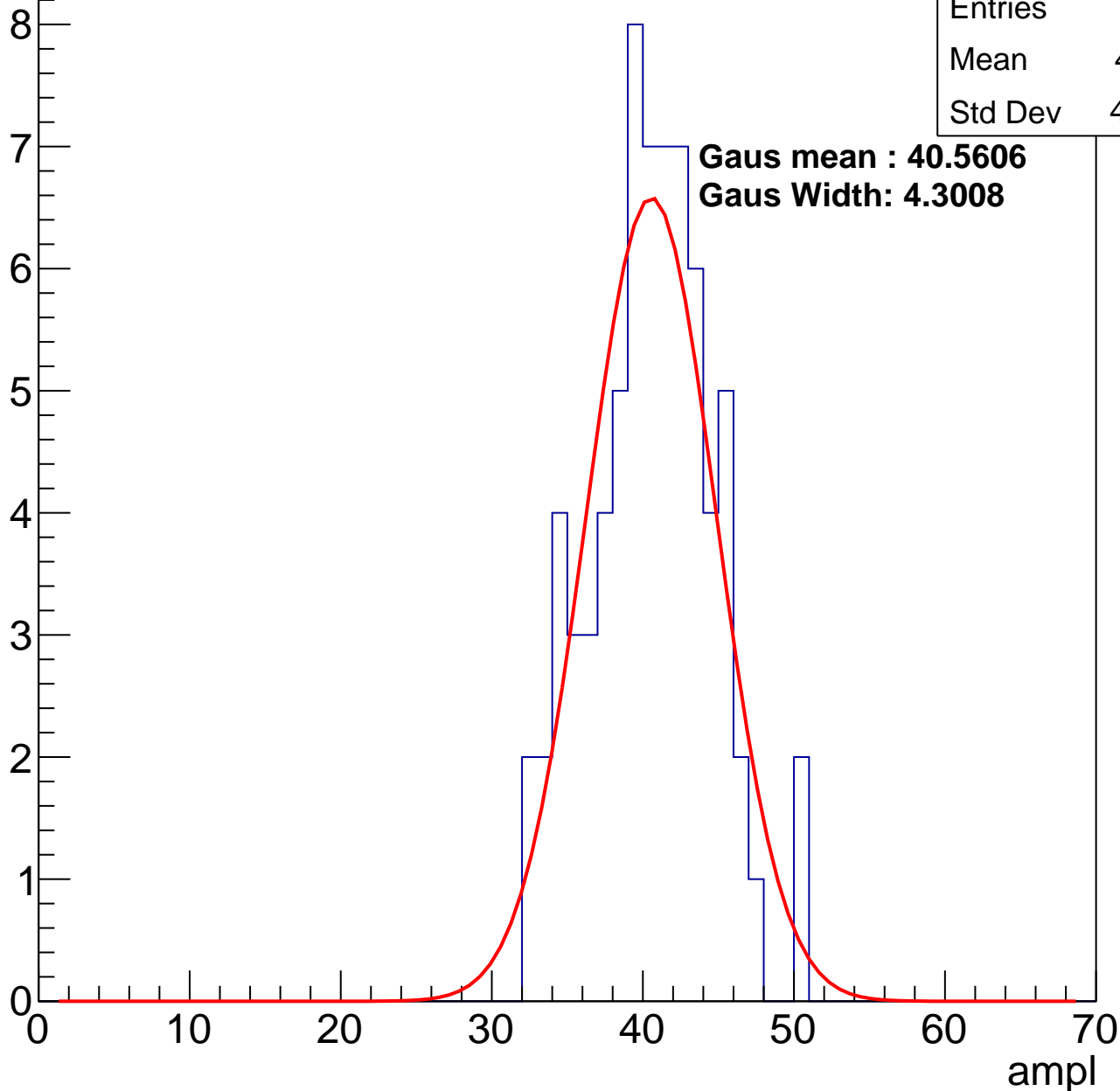
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	40.11
Std Dev	4.005

Gaus mean : 40.5606

Gaus Width: 4.3008

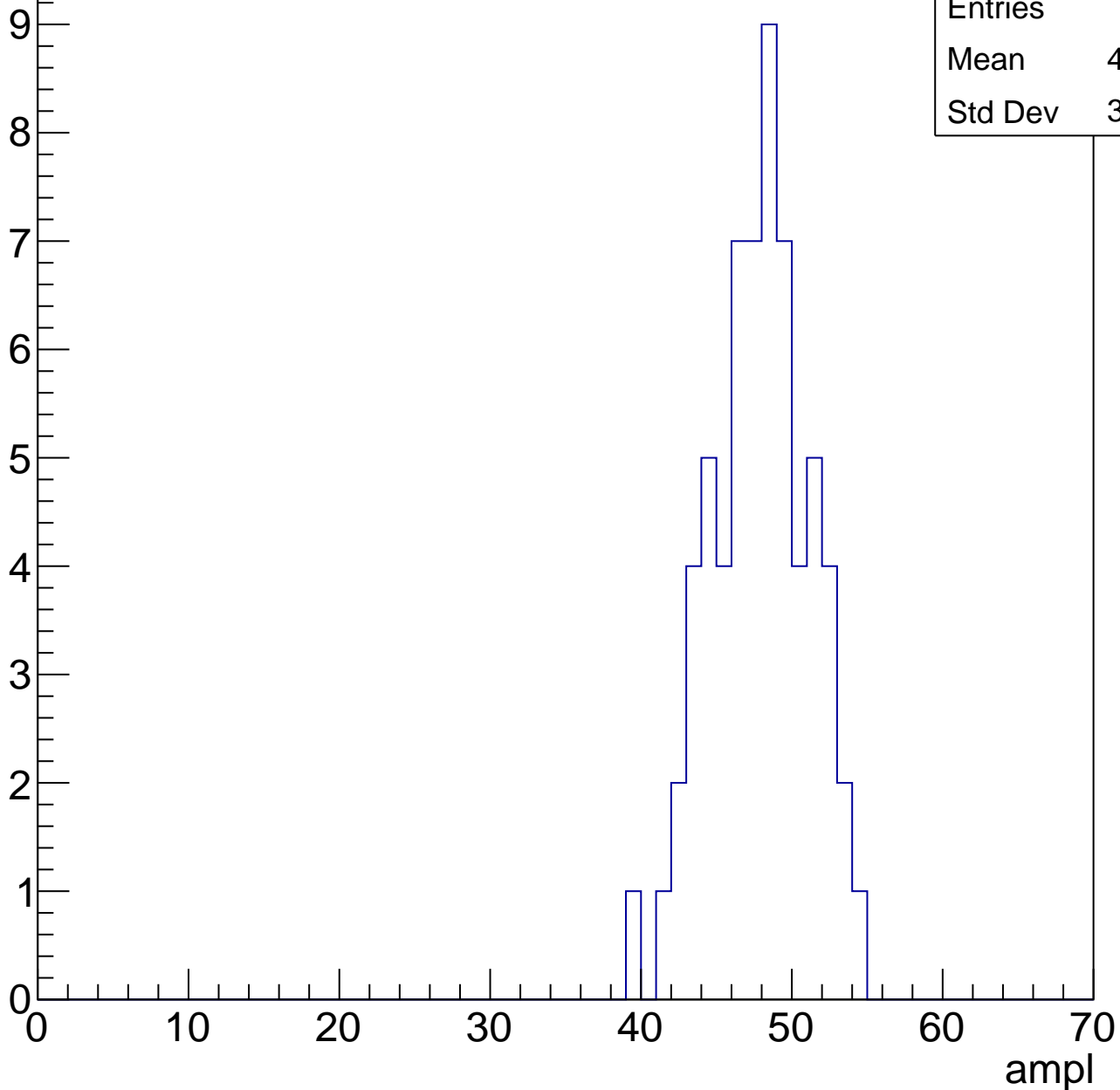


B1L102S, U17-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.38
Std Dev	3.204

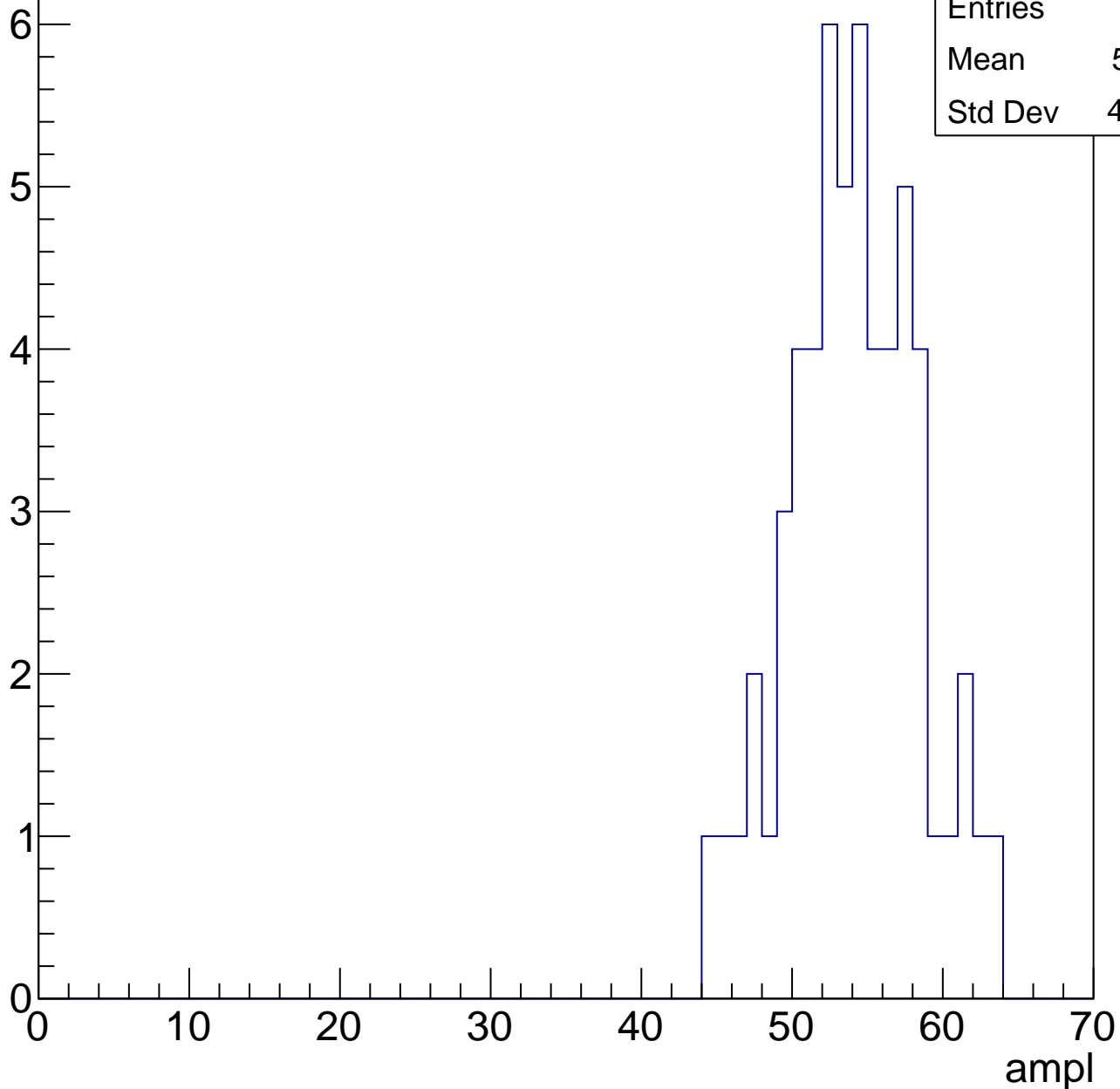


B1L102S, U17-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

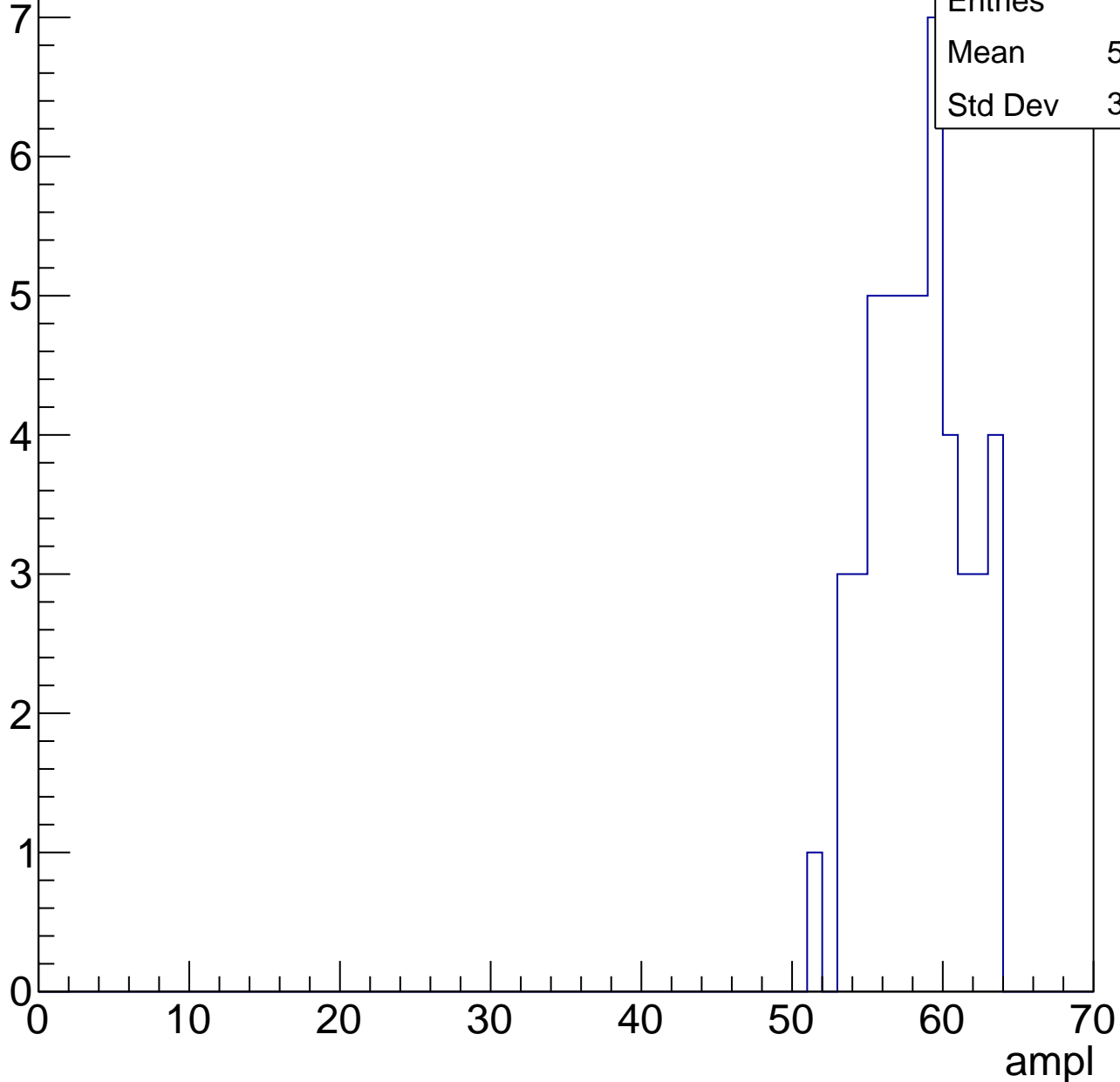
Entries	57
Mean	53.61
Std Dev	4.204



B1L102S, U17-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

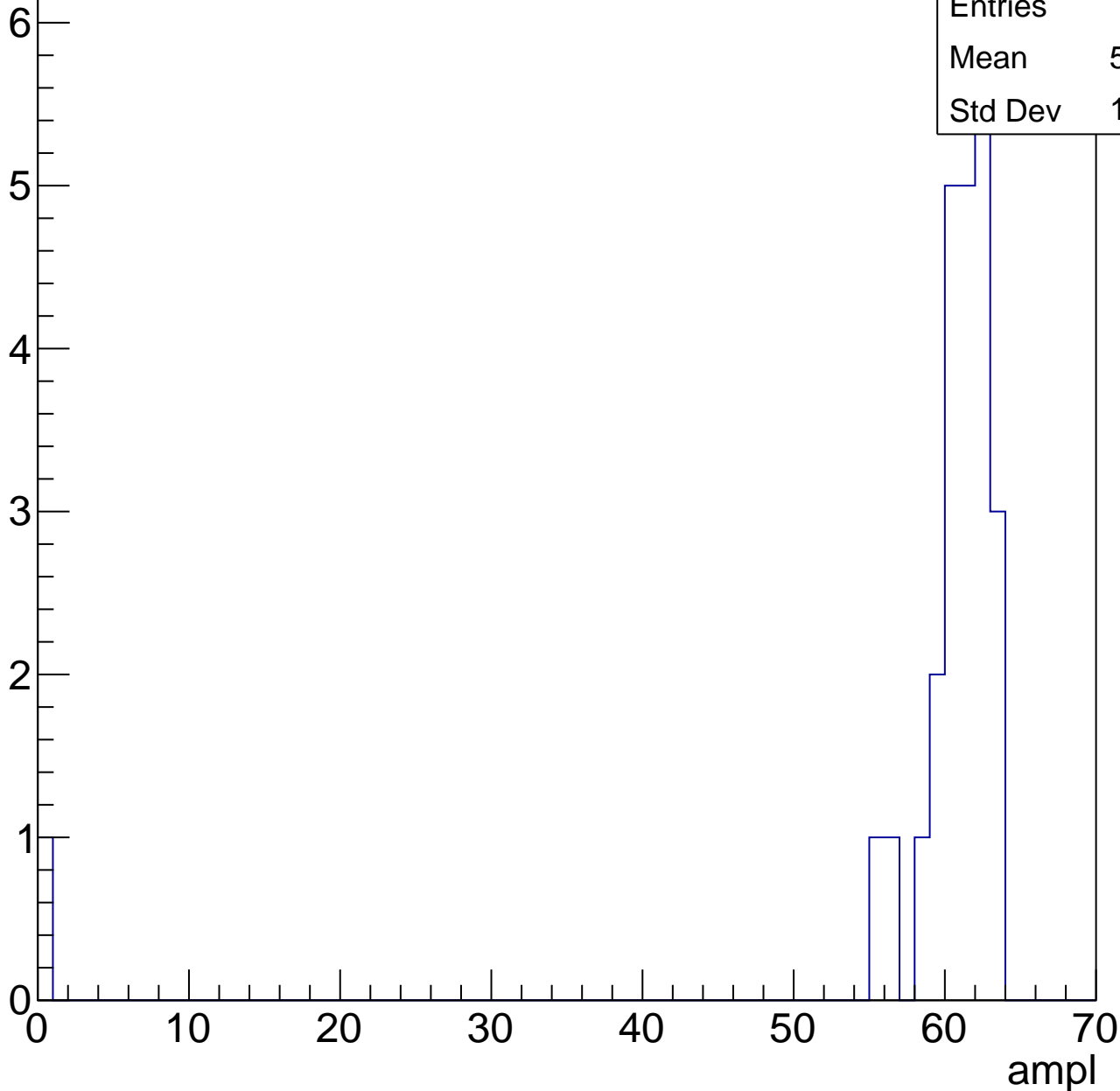


B1L102S, U17-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	58.12
Std Dev	12.02



B1L102S, U17-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch95, adc0

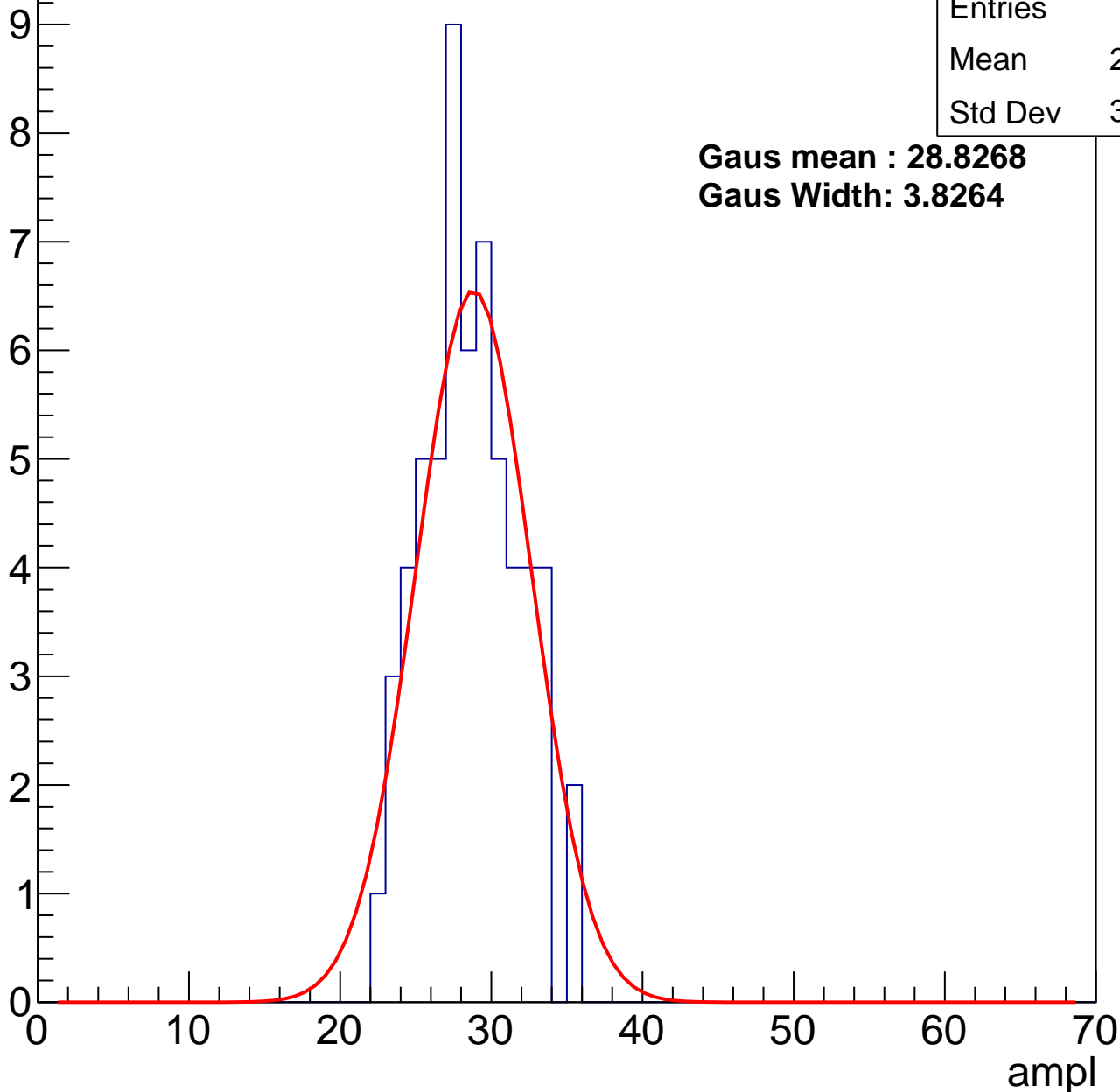
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.14
Std Dev	3.116

Gaus mean : 28.8268

Gaus Width: 3.8264



B1L102S, U17-ch95, adc1

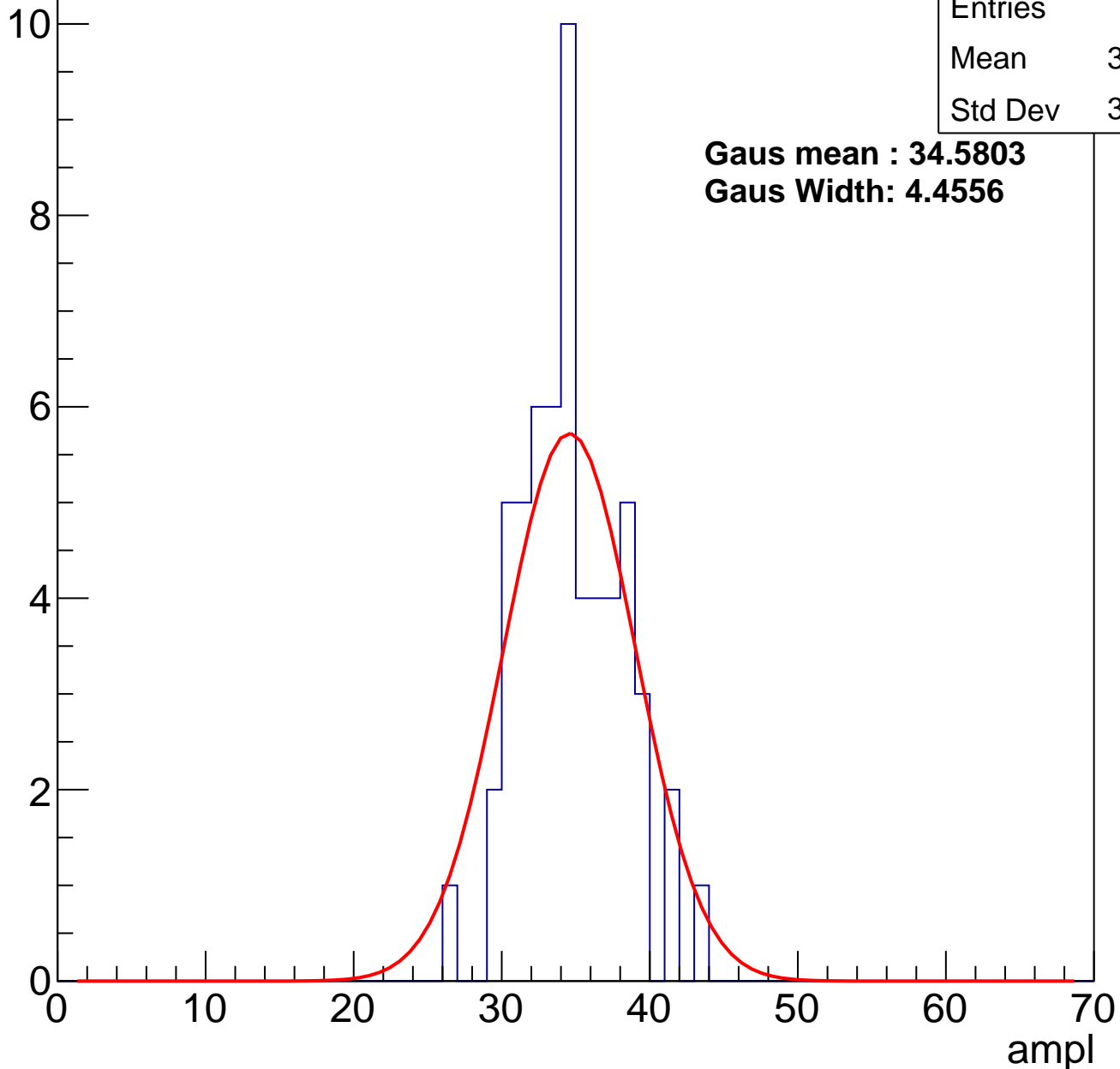
calib_packv5_042523_0143.root, FC#11, port A2

Entries	58
Mean	34.19
Std Dev	3.365

Gaus mean : 34.5803

Gaus Width: 4.4556

Entry



B1L102S, U17-ch95, adc2

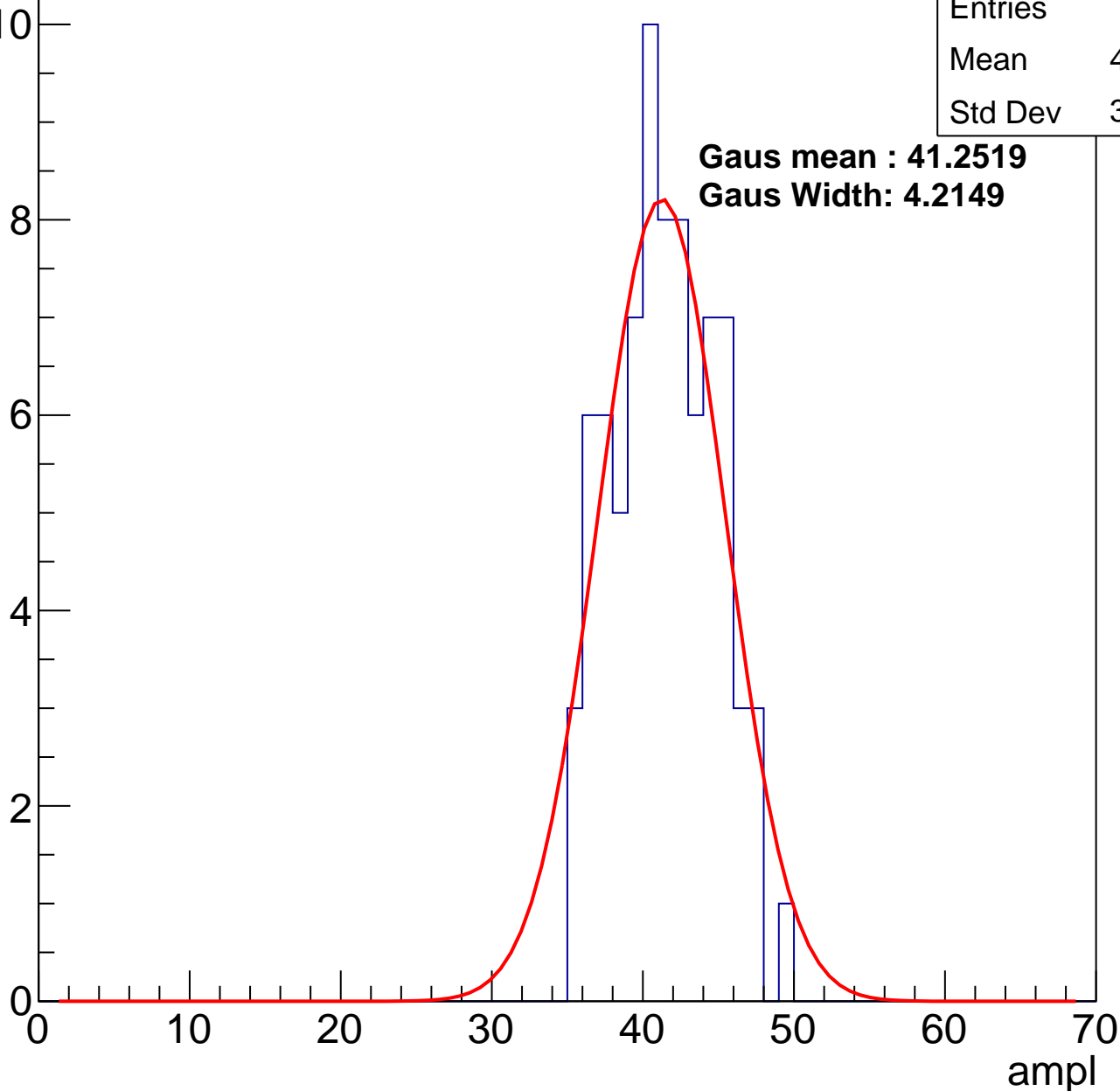
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	40.99
Std Dev	3.337

Gaus mean : 41.2519

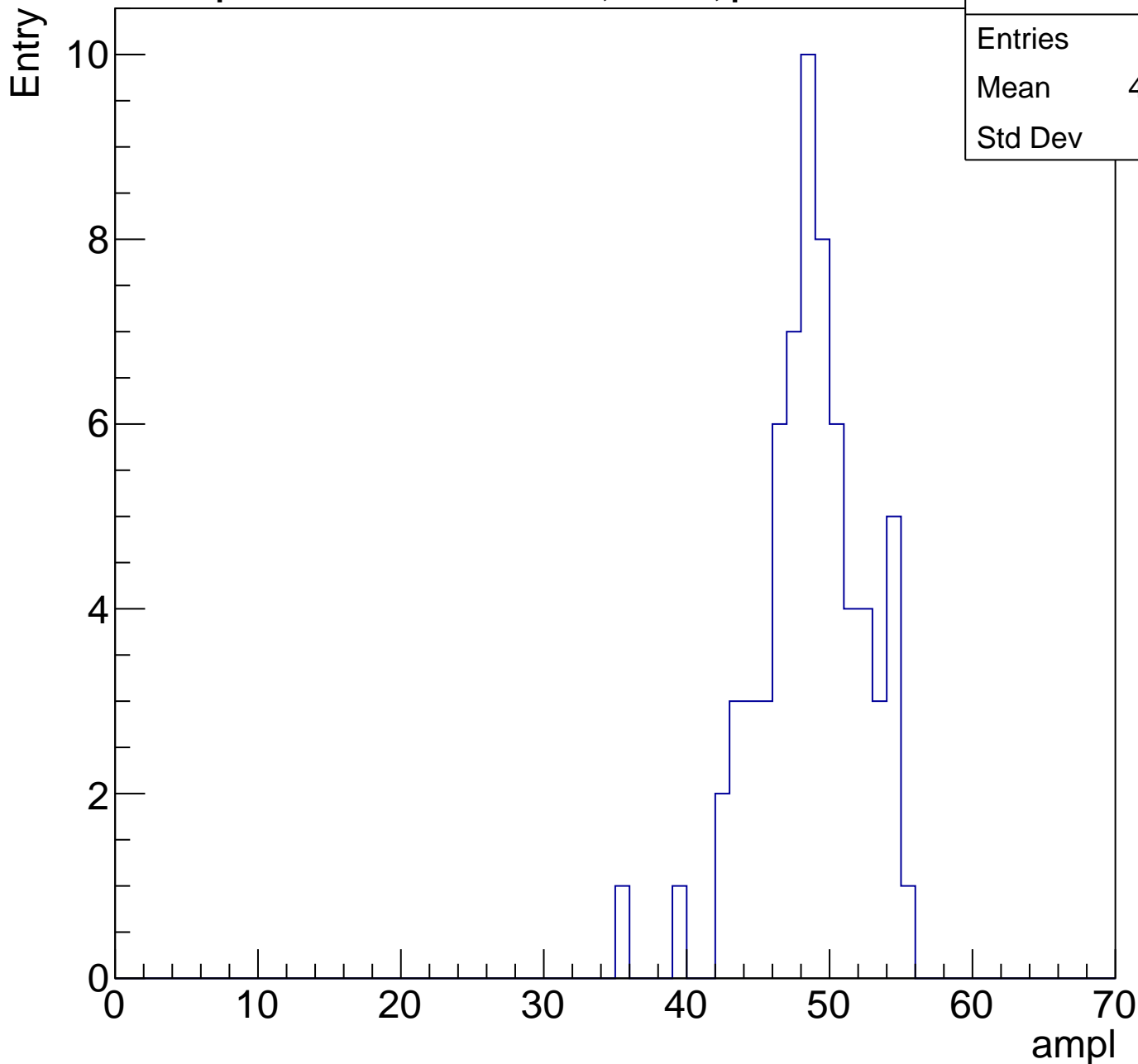
Gaus Width: 4.2149



B1L102S, U17-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	48.16
Std Dev	3.74

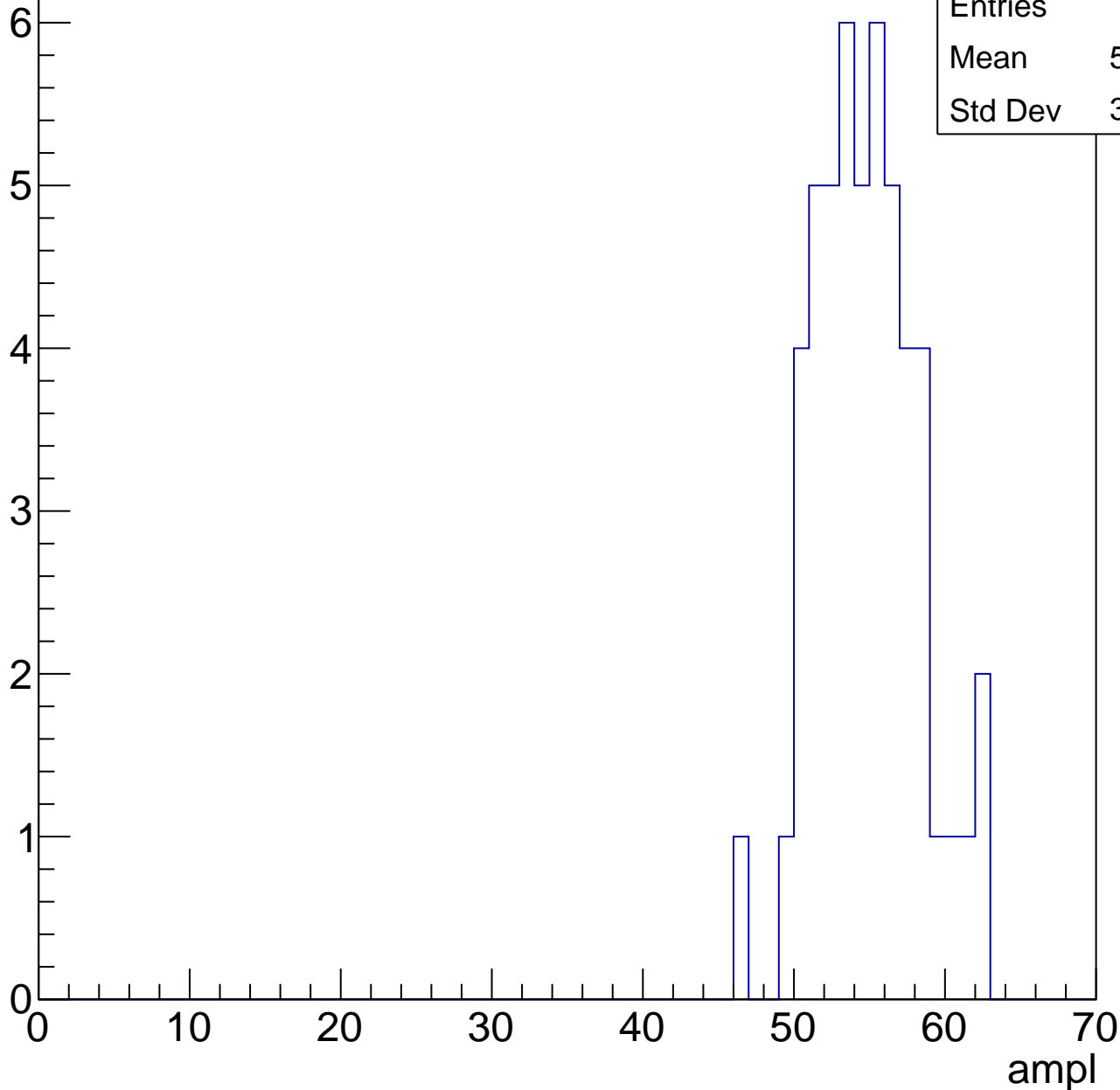


B1L102S, U17-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.35
Std Dev	3.377

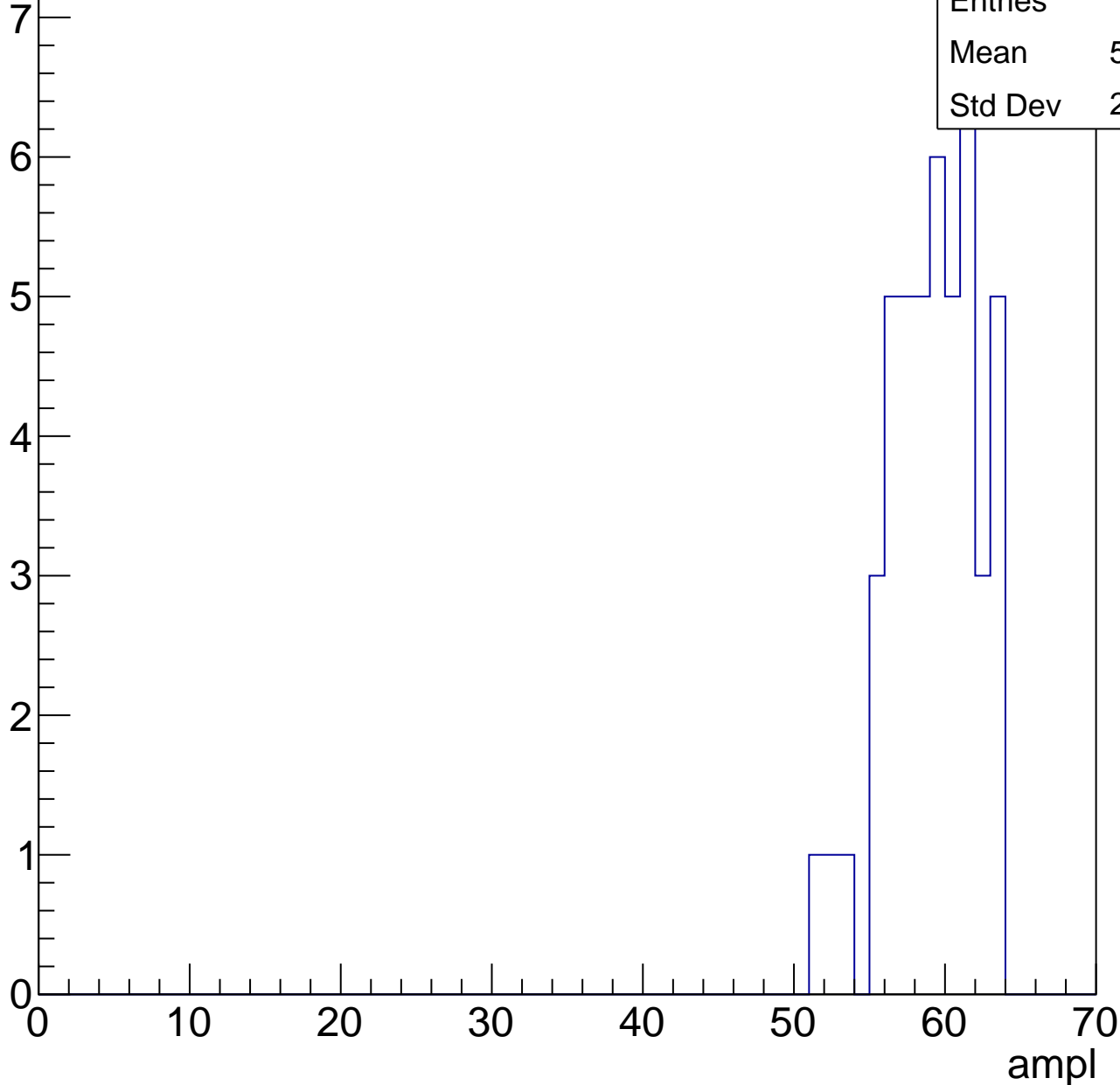


B1L102S, U17-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.68
Std Dev	2.925

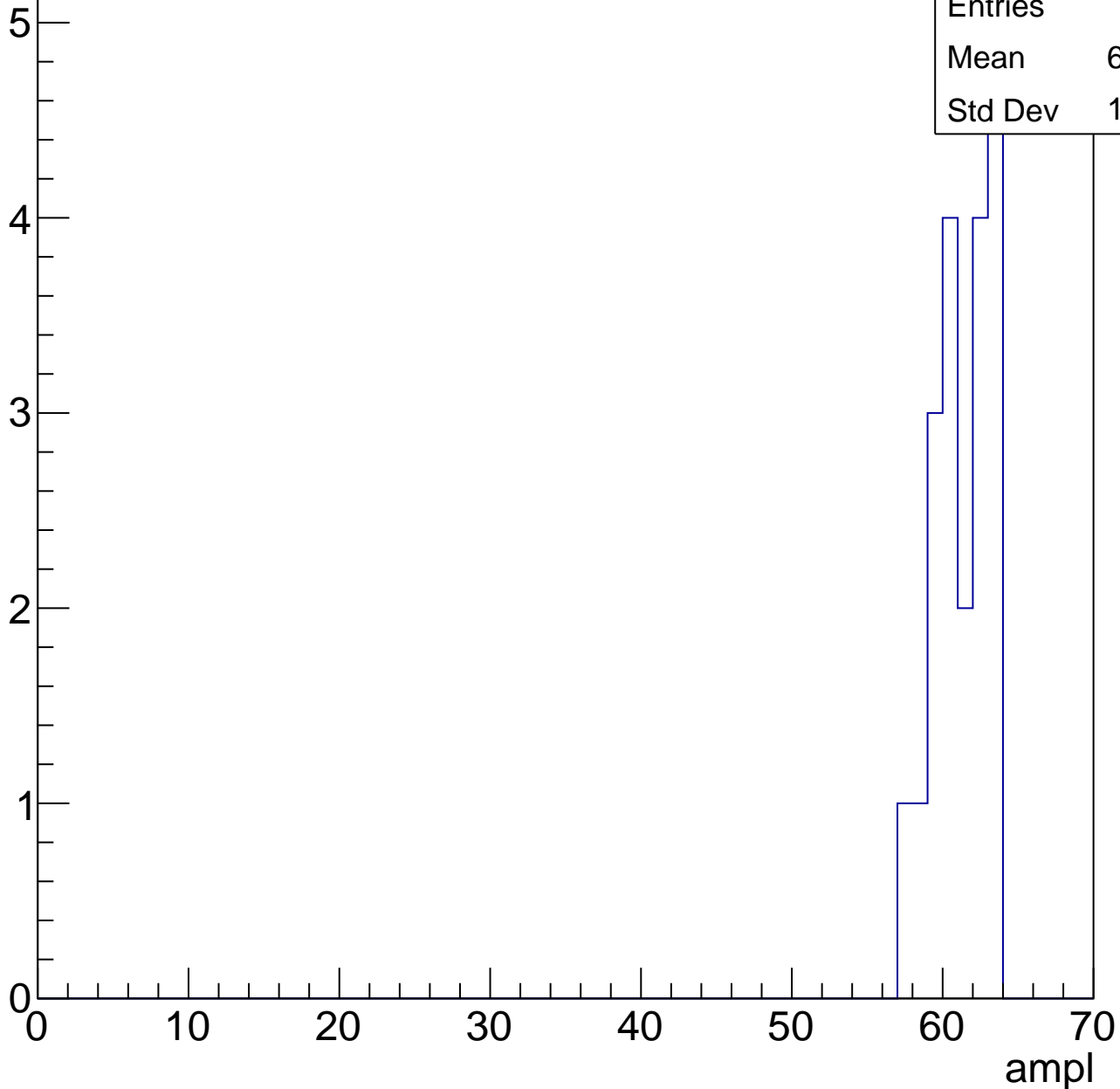


B1L102S, U17-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

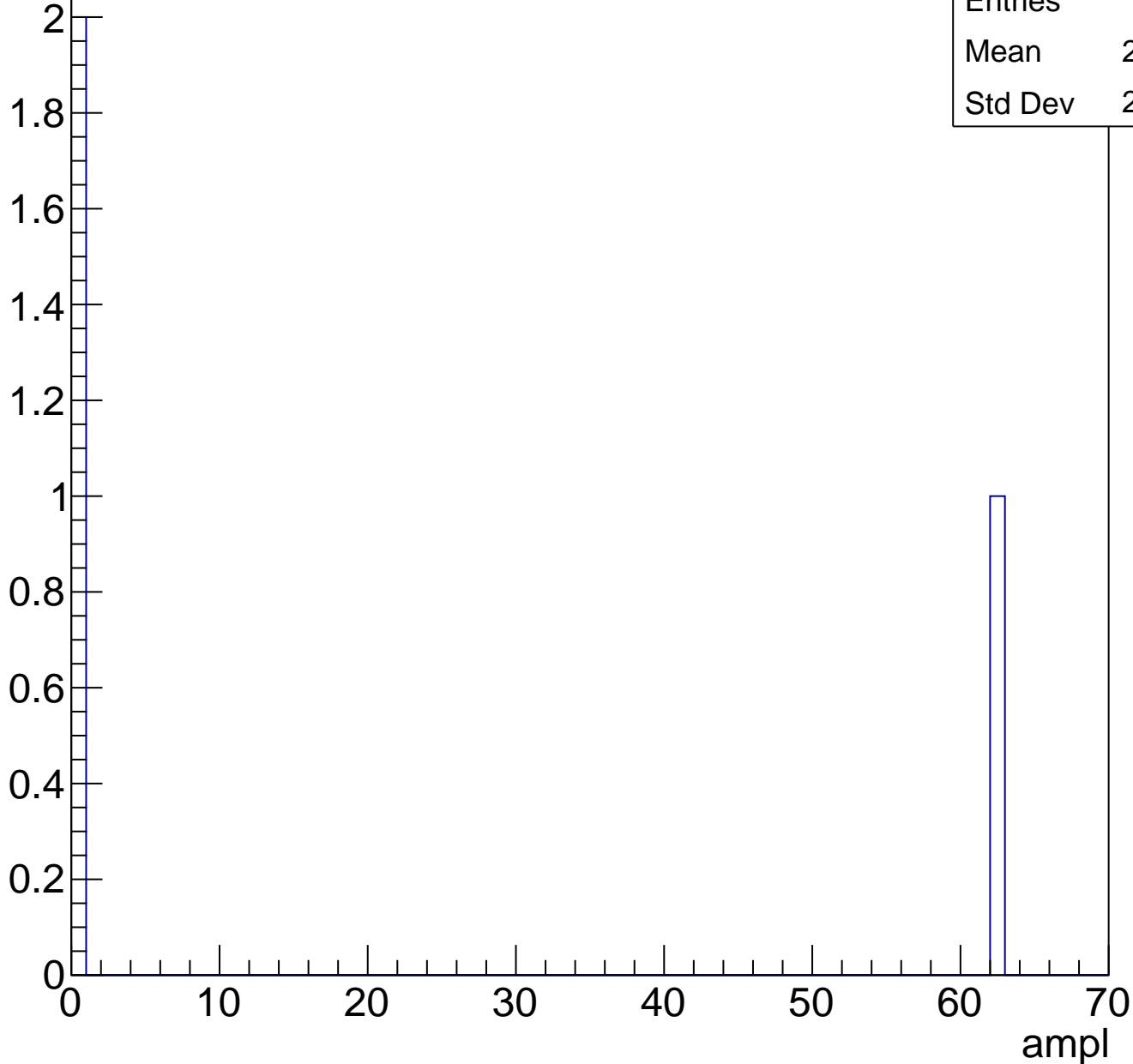
Entries	20
Mean	60.85
Std Dev	1.797



B1L102S, U17-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch96, adc0

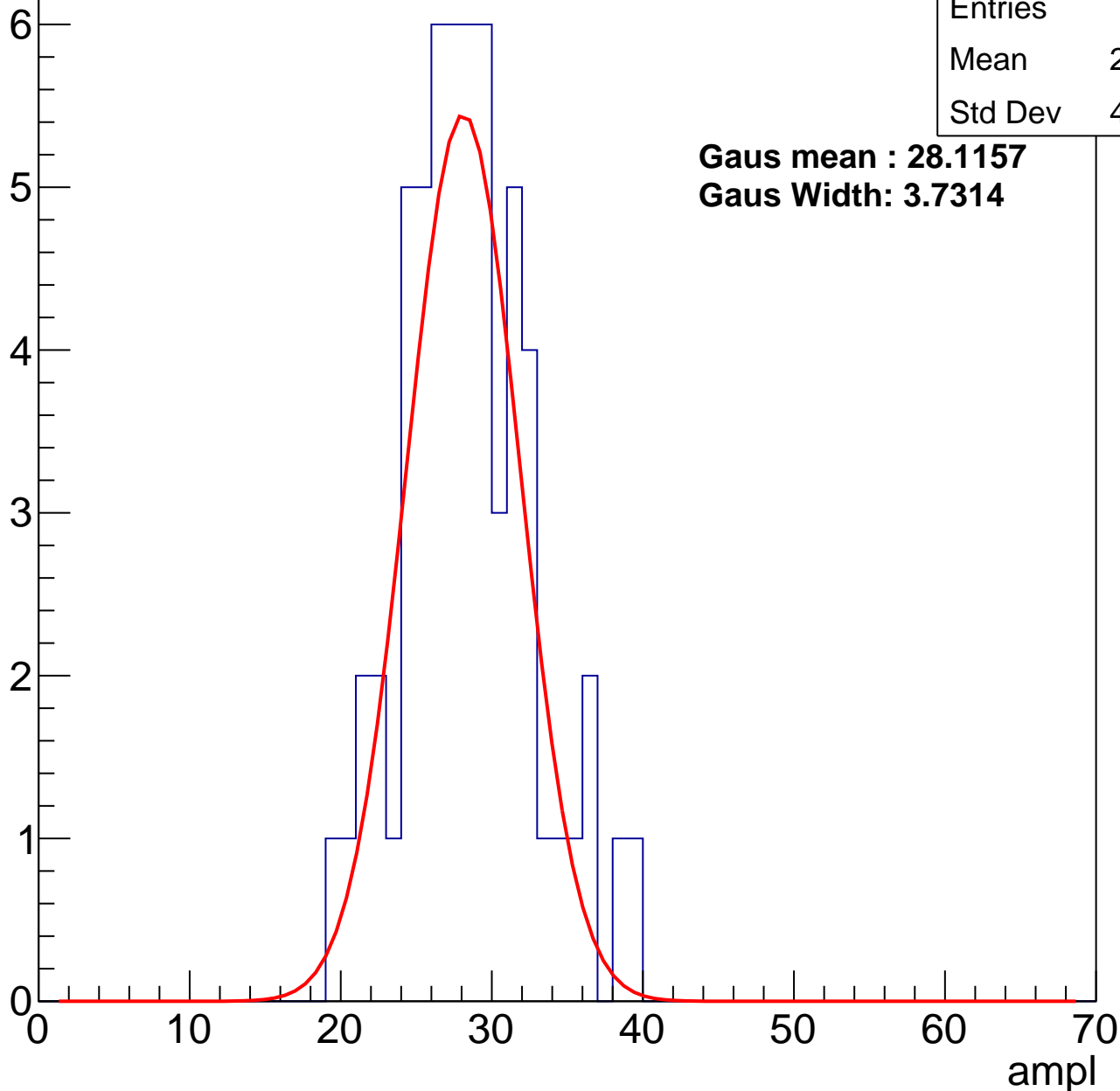
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	27.95
Std Dev	4.244

Gaus mean : 28.1157

Gaus Width: 3.7314



B1L102S, U17-ch96, adc1

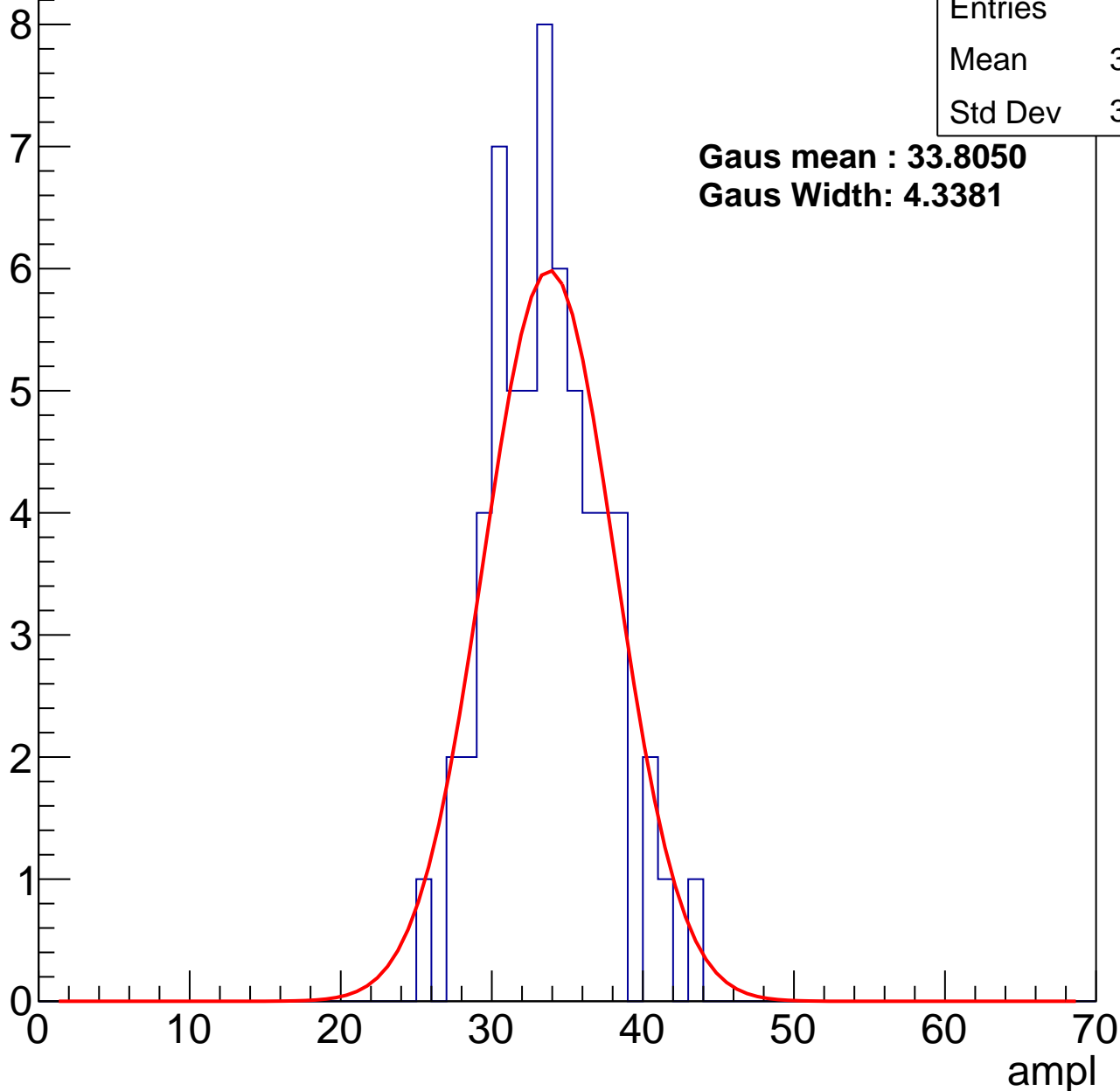
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	33.23
Std Dev	3.673

Gaus mean : 33.8050

Gaus Width: 4.3381



B1L102S, U17-ch96, adc2

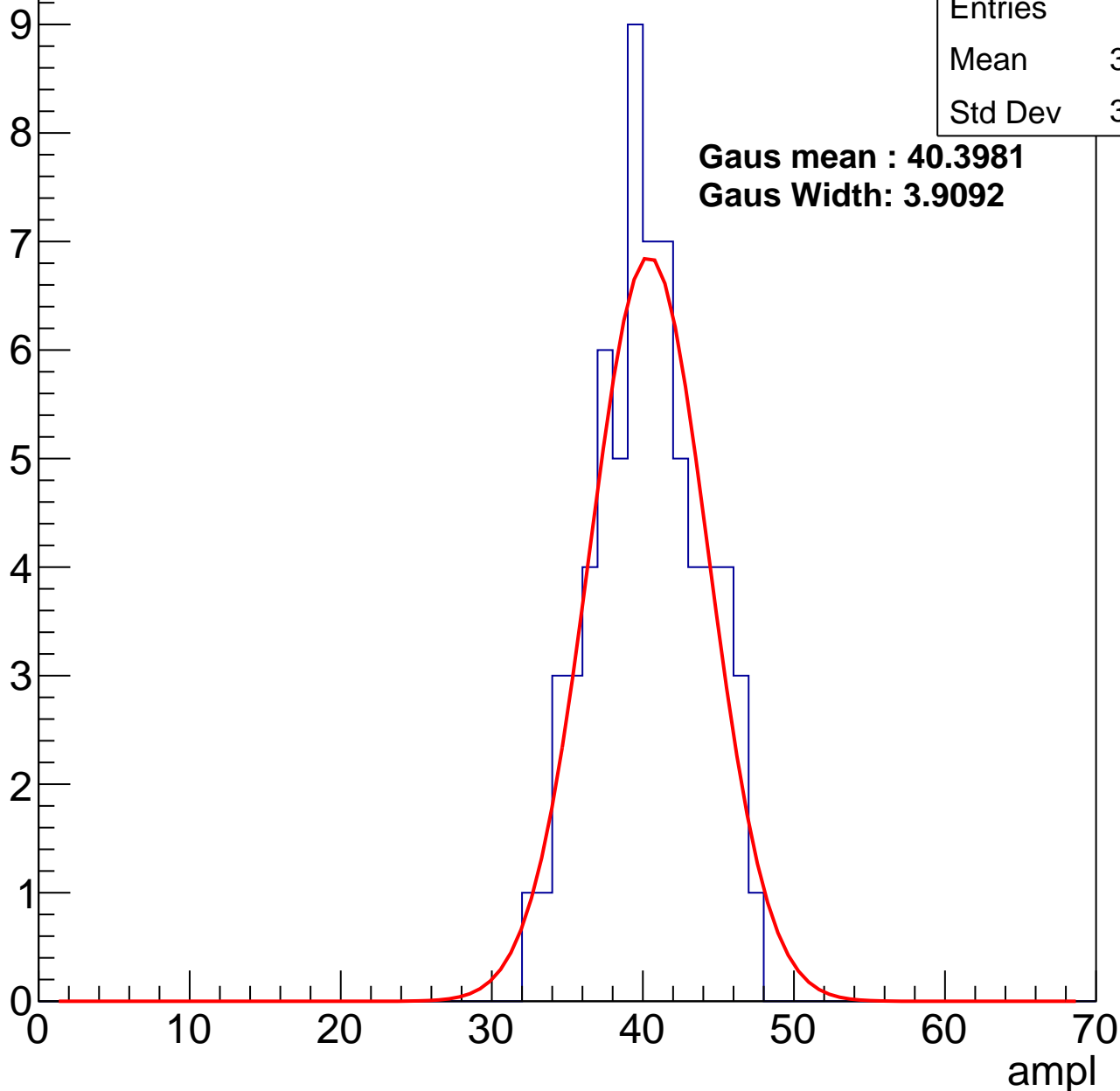
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	39.84
Std Dev	3.509

Gaus mean : 40.3981

Gaus Width: 3.9092

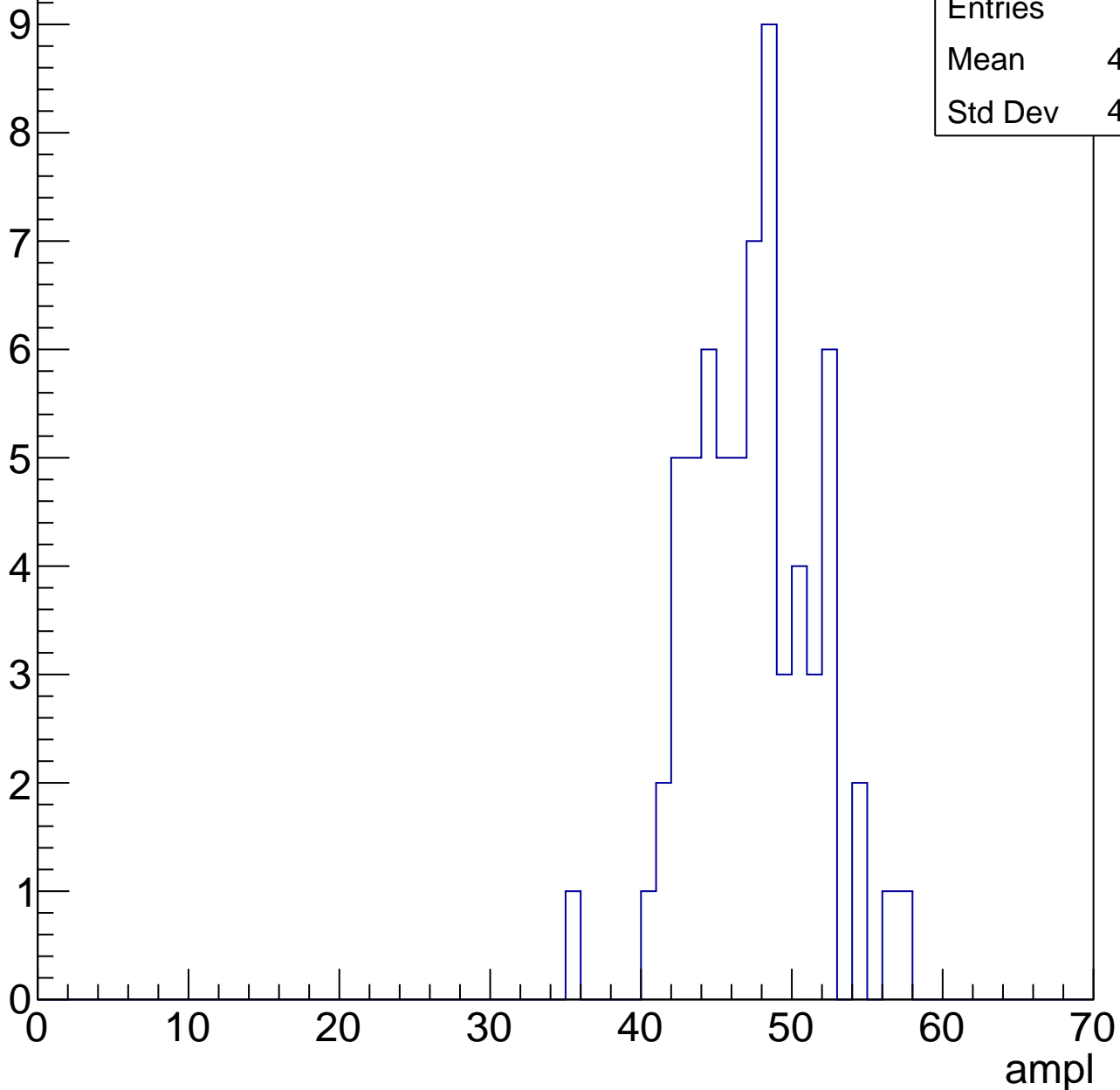


B1L102S, U17-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.89
Std Dev	4.049

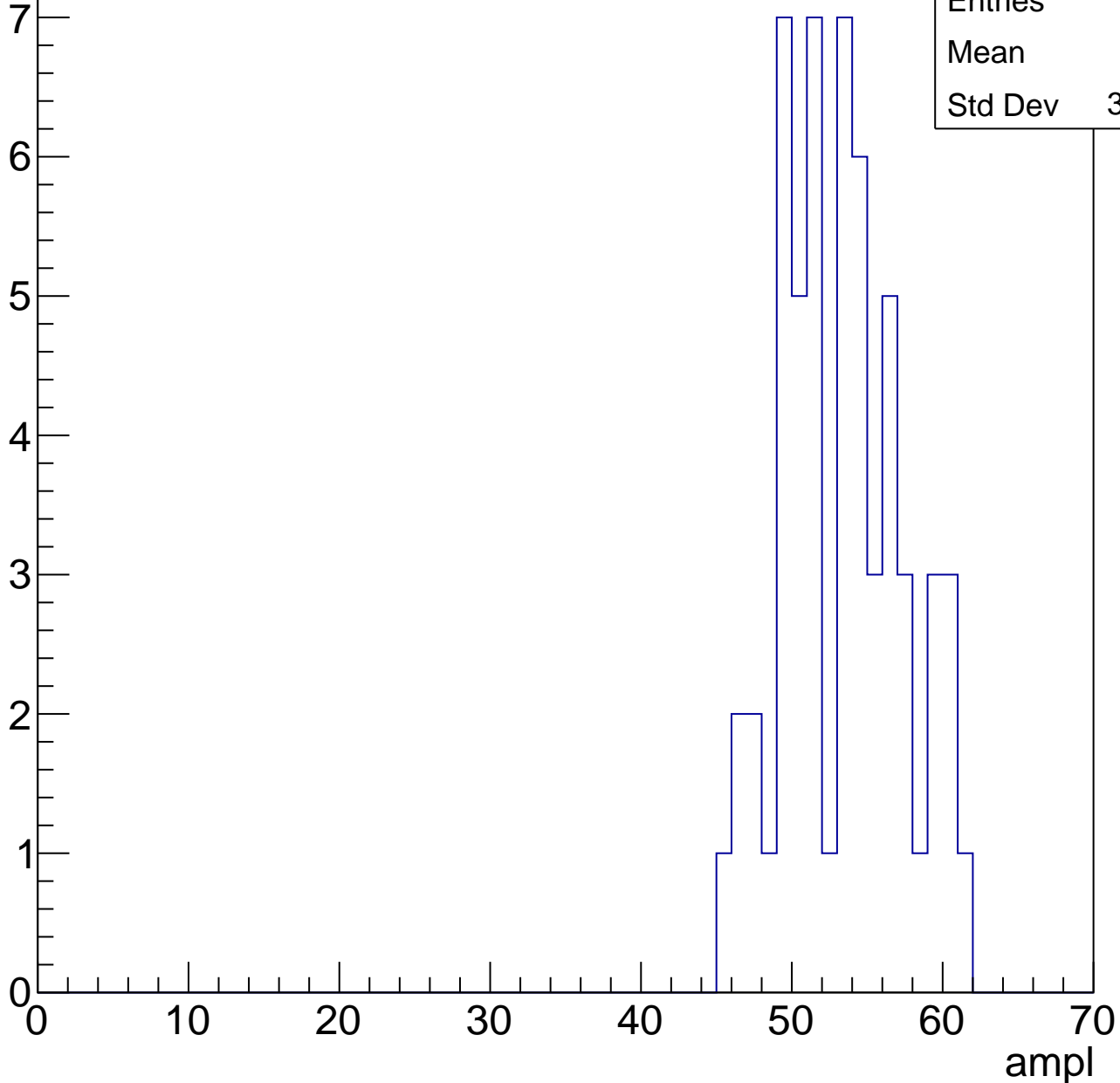


B1L102S, U17-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	52.9
Std Dev	3.938

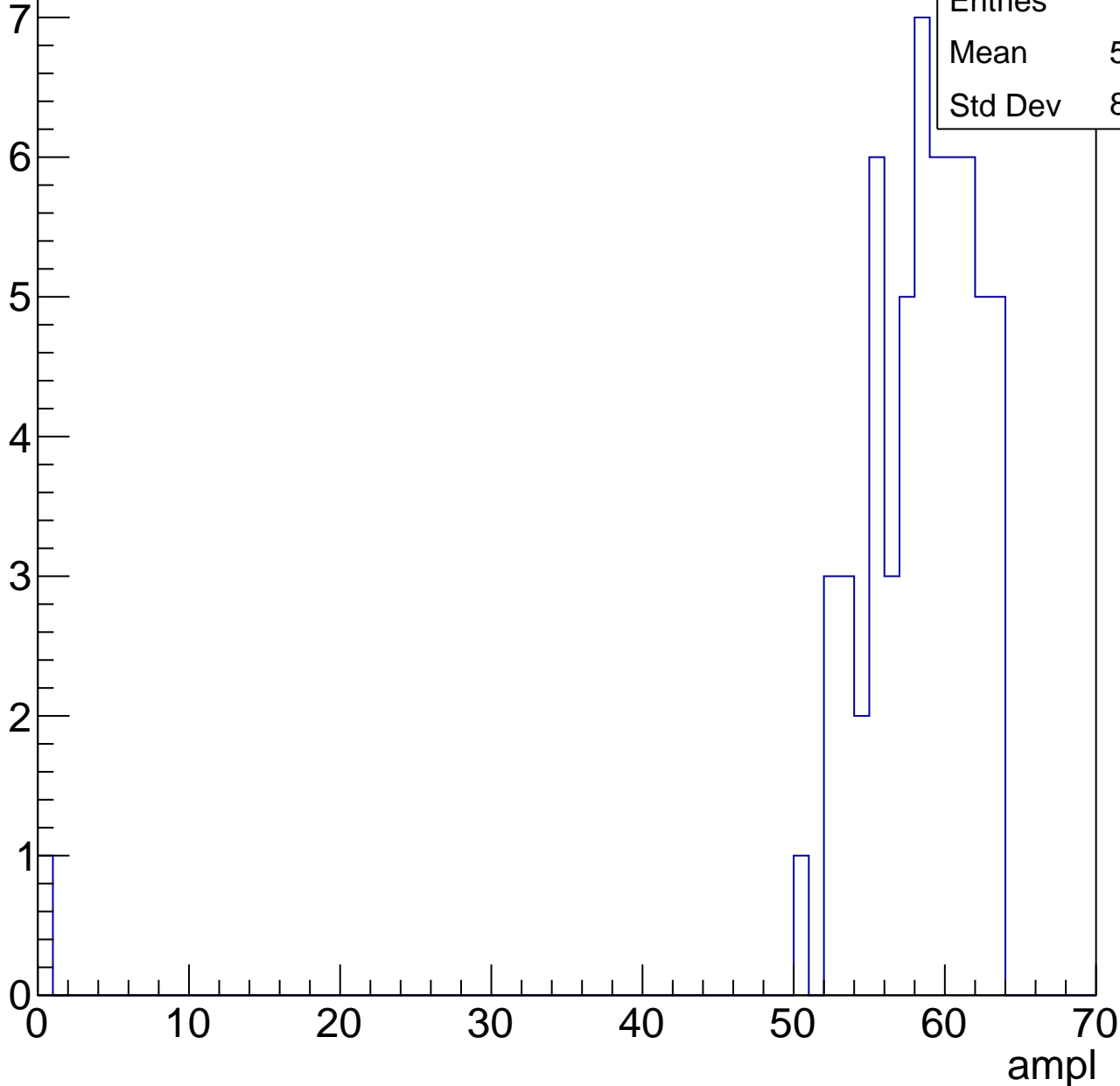


B1L102S, U17-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.07
Std Dev	8.184

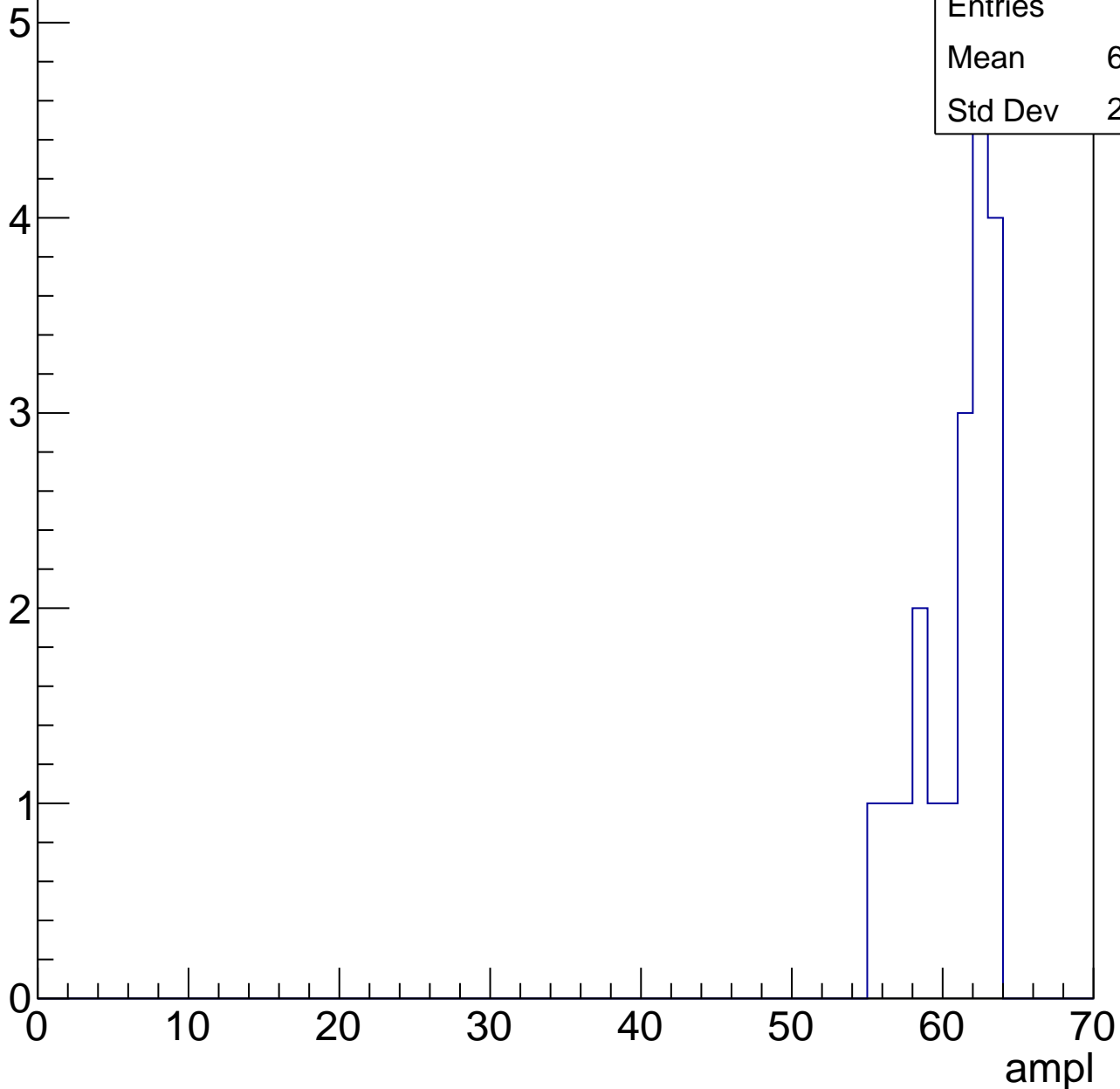


B1L102S, U17-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	60.42
Std Dev	2.456



B1L102S, U17-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch97, adc0

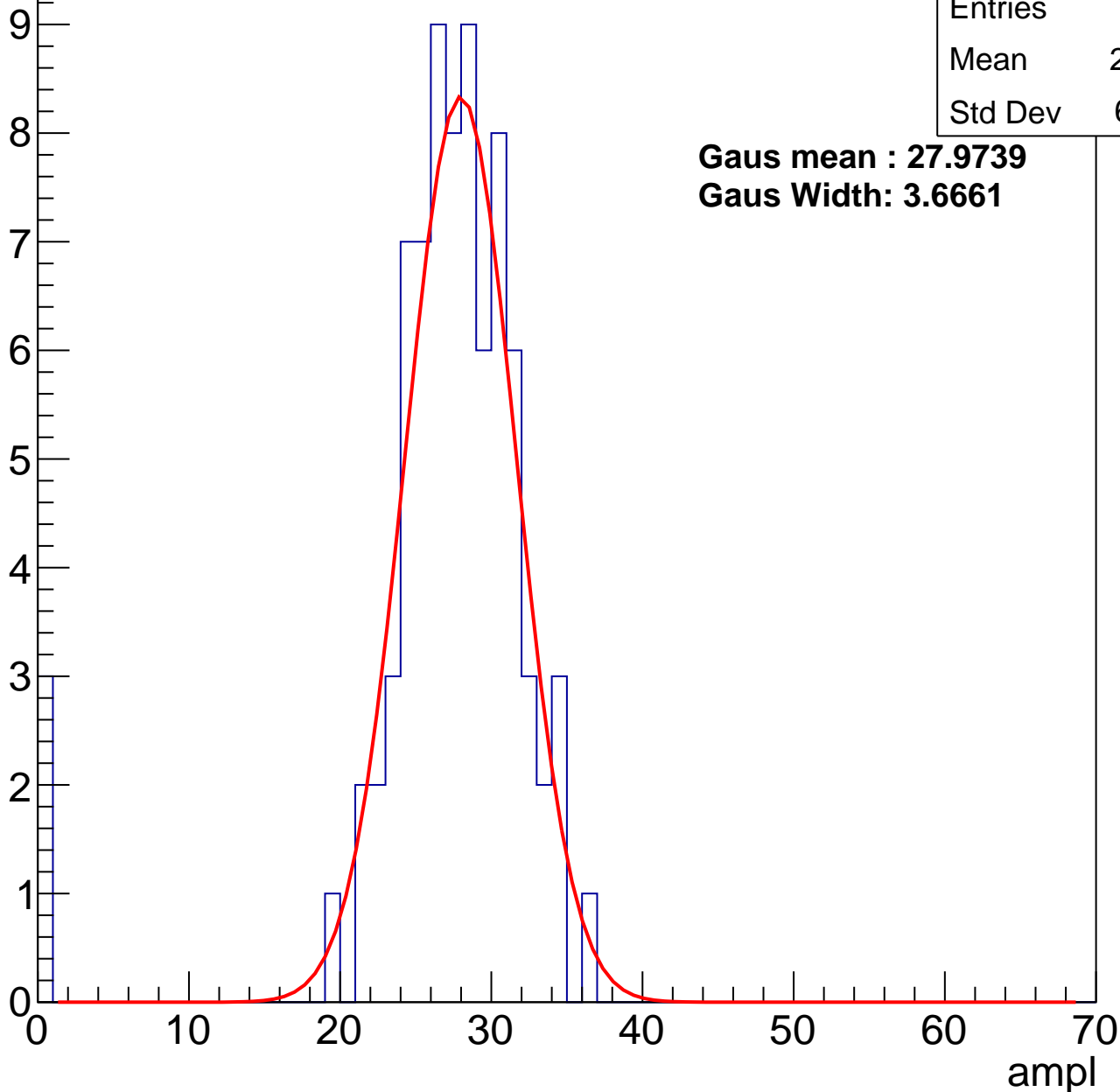
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	26.49
Std Dev	6.201

Gaus mean : 27.9739

Gaus Width: 3.6661



B1L102S, U17-ch97, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	34.46
Std Dev	3.484

Gaus mean : 35.8550

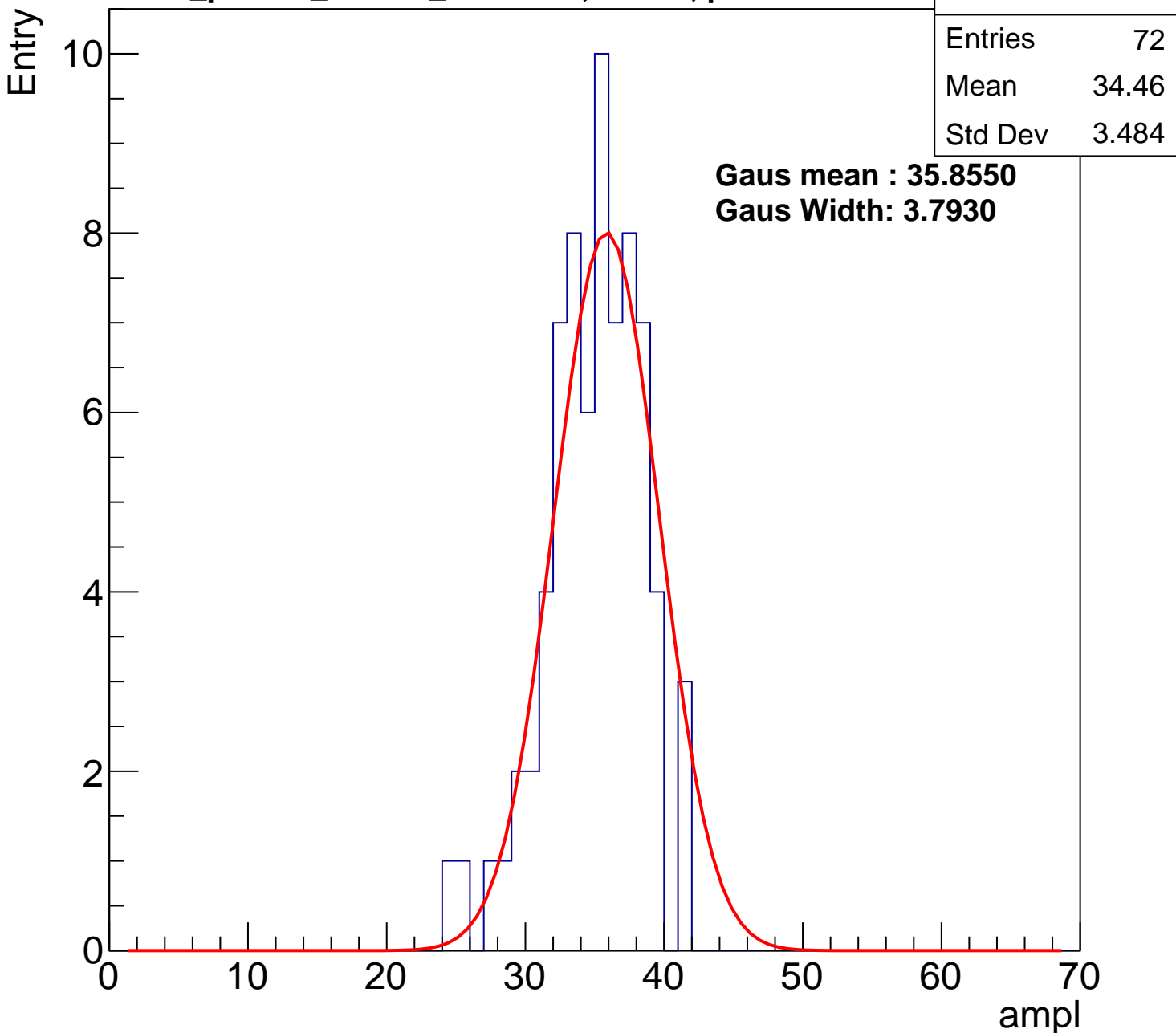
Gaus Width: 3.7930

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch97, adc2

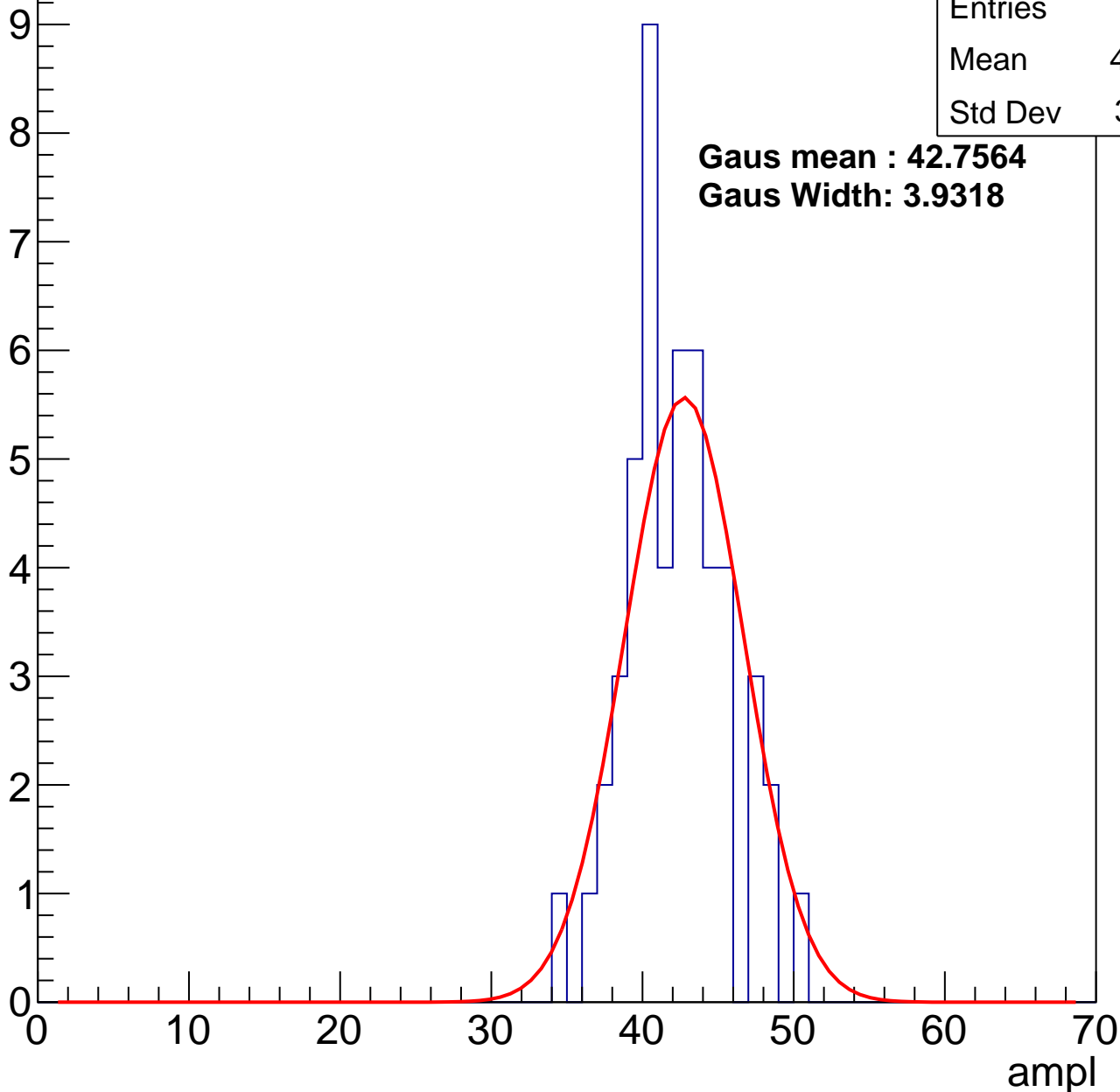
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.76
Std Dev	3.281

Gaus mean : 42.7564

Gaus Width: 3.9318

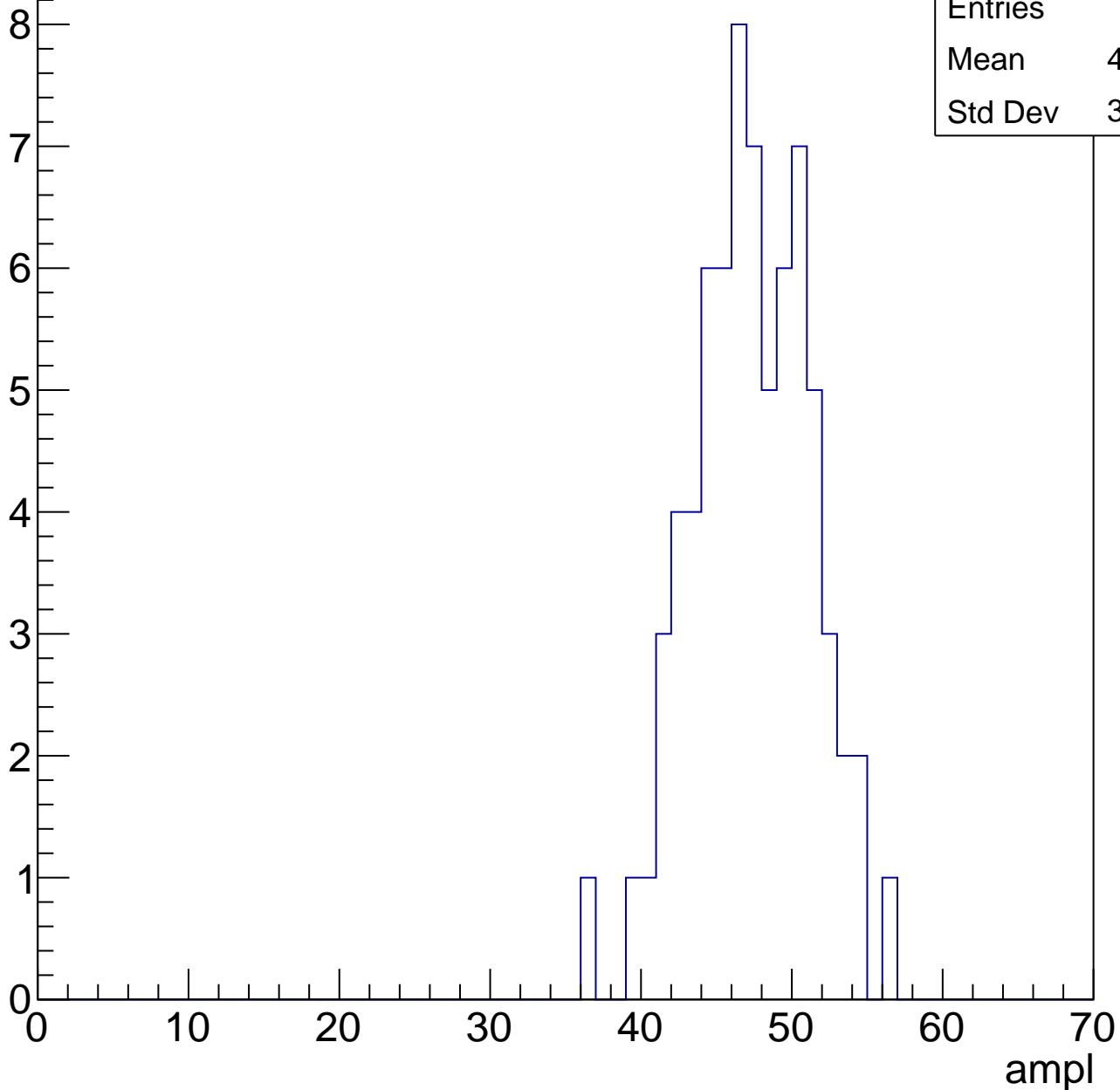


B1L102S, U17-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	46.86
Std Dev	3.888

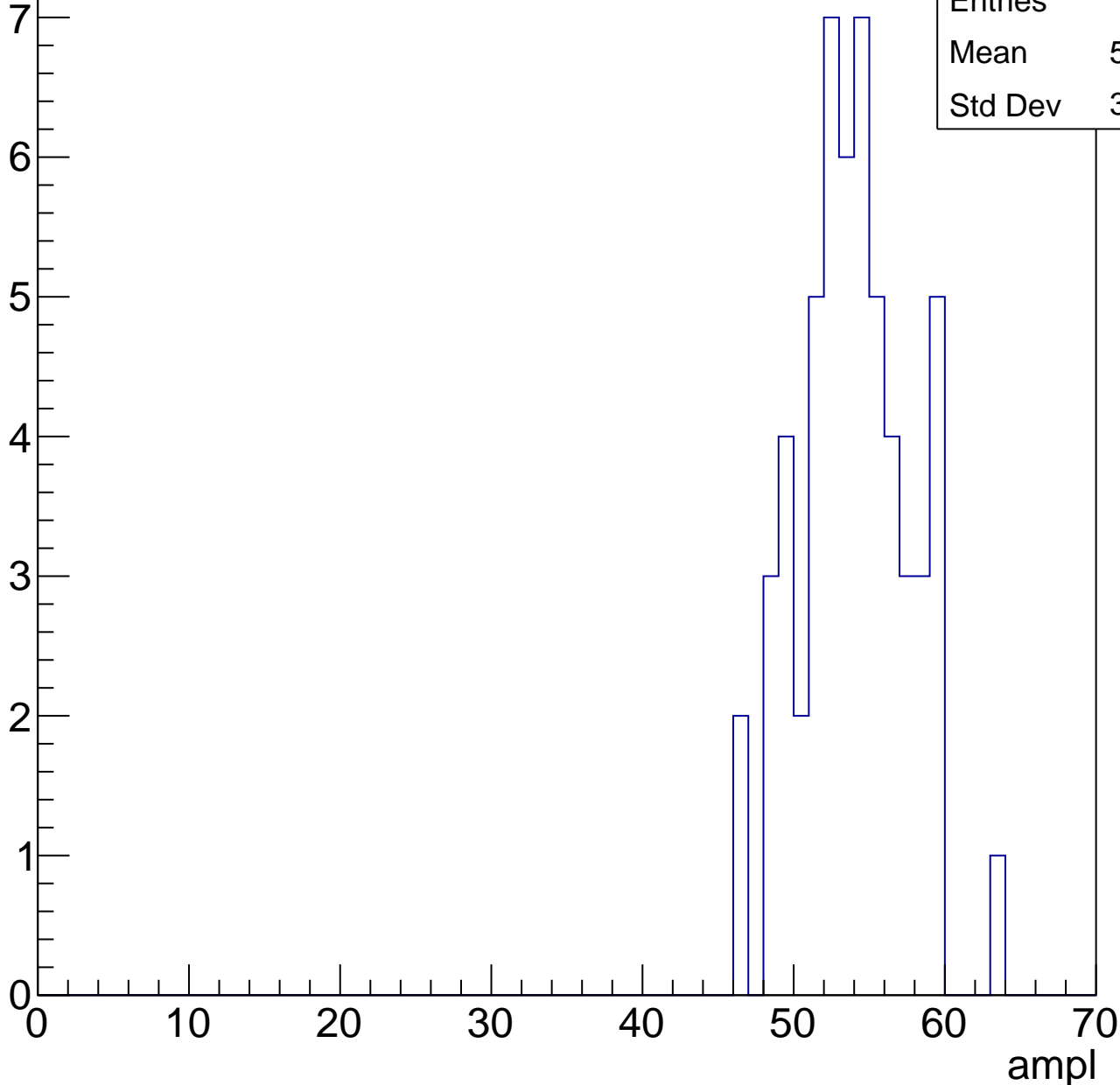


B1L102S, U17-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.49
Std Dev	3.589

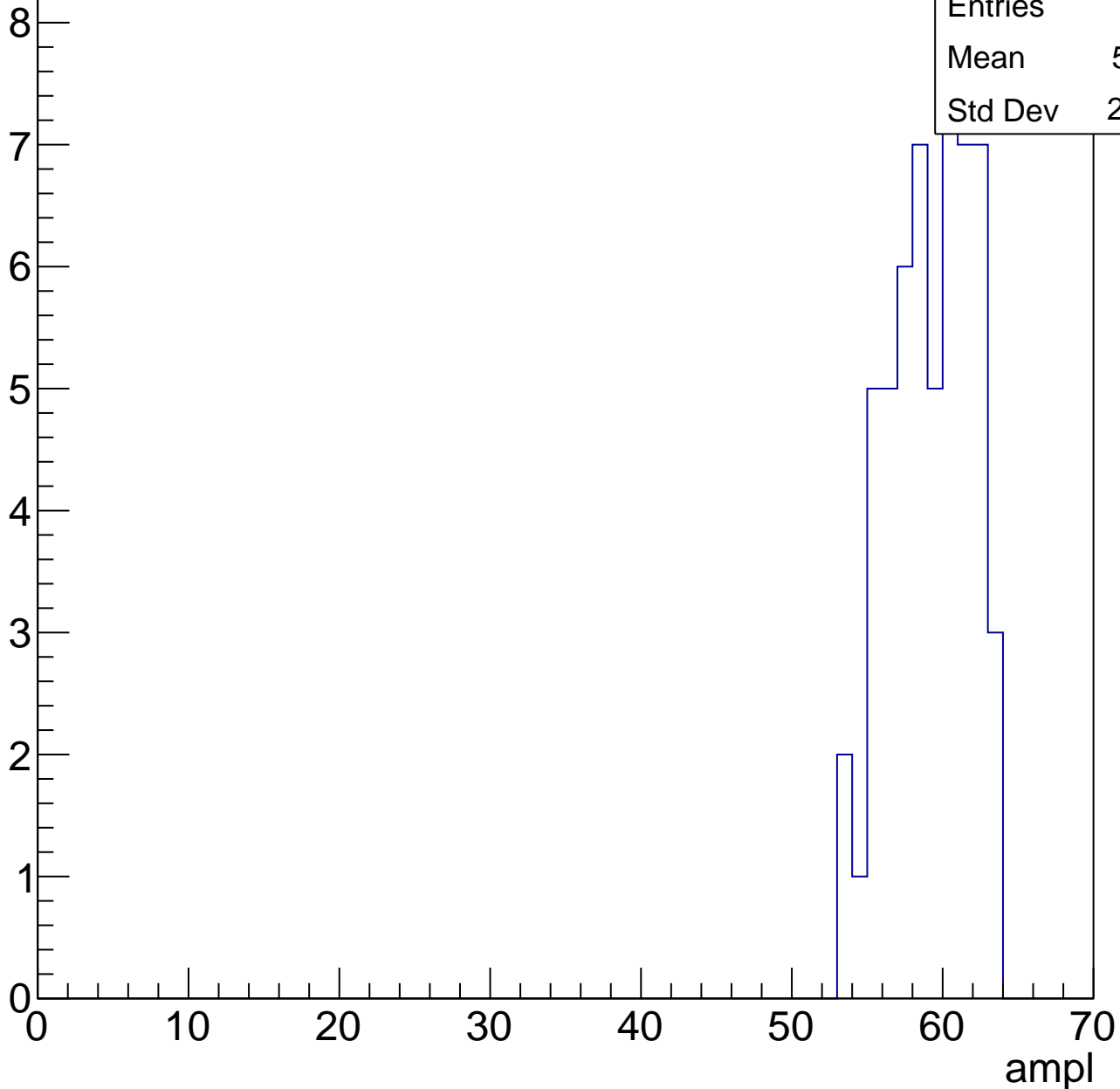


B1L102S, U17-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.71
Std Dev	2.657

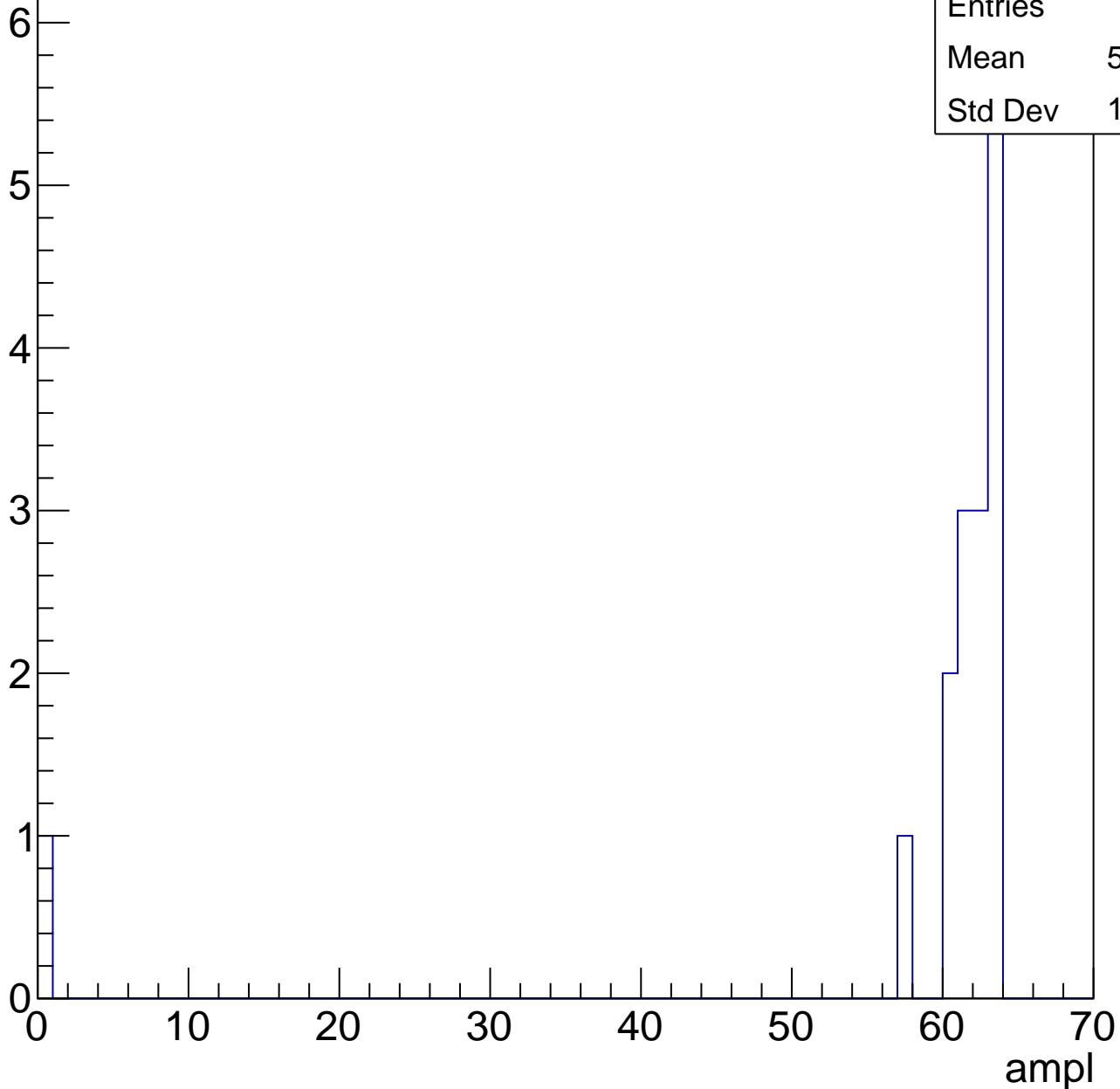


B1L102S, U17-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.75
Std Dev	14.99



B1L102S, U17-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch98, adc0

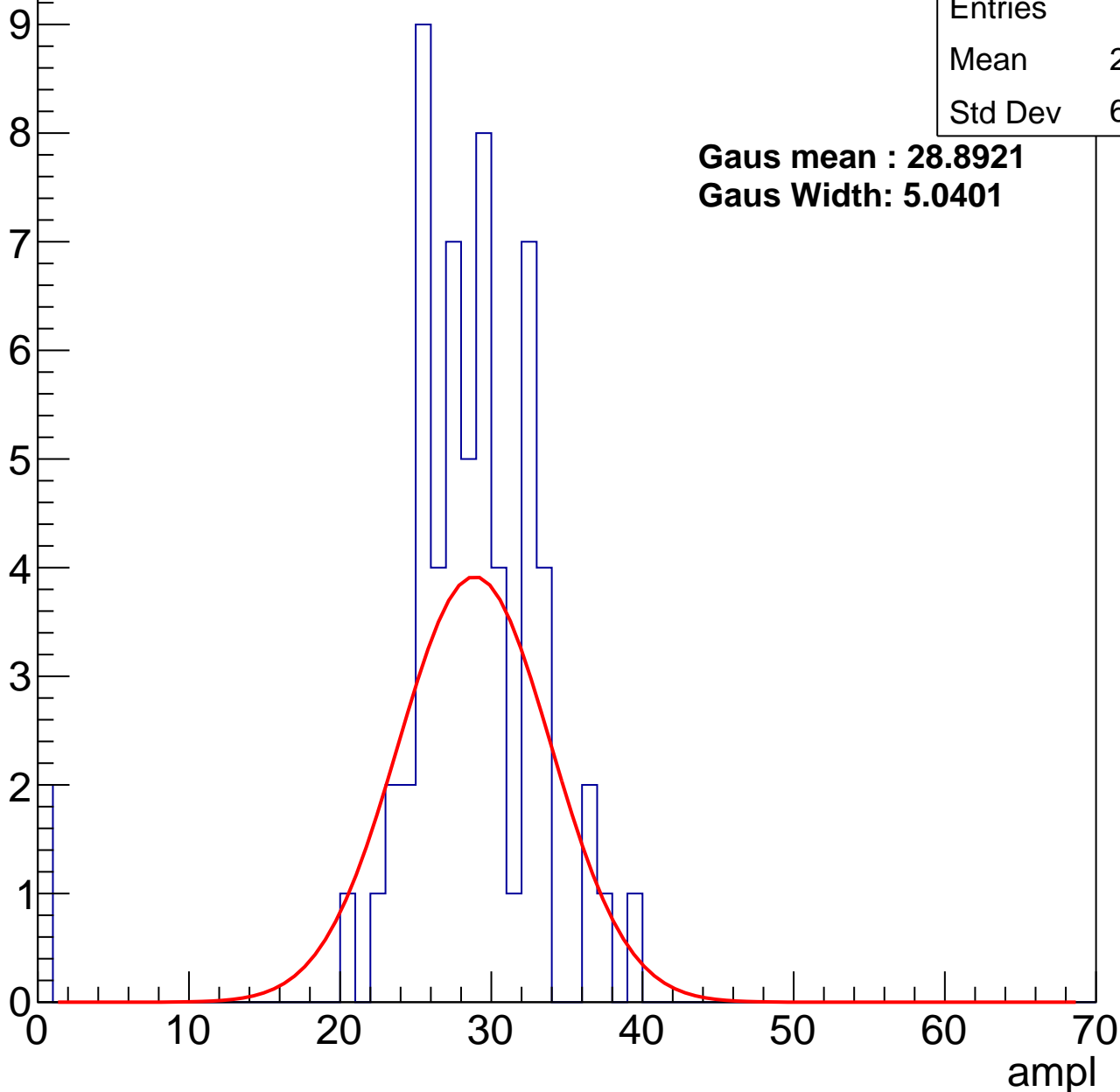
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.56
Std Dev	6.295

Gaus mean : 28.8921

Gaus Width: 5.0401



B1L102S, U17-ch98, adc1

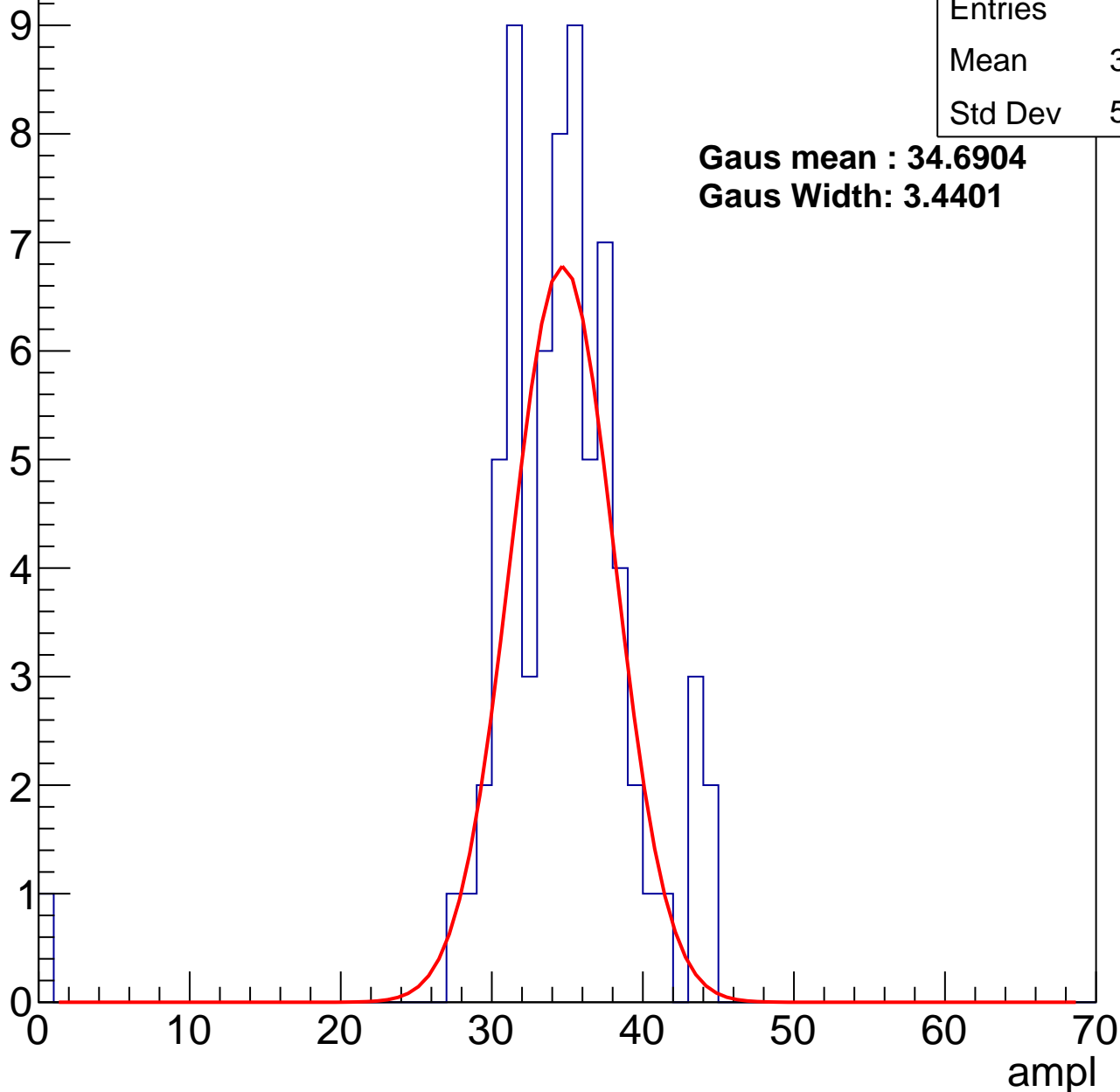
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.14
Std Dev	5.609

Gaus mean : 34.6904

Gaus Width: 3.4401



B1L102S, U17-ch98, adc2

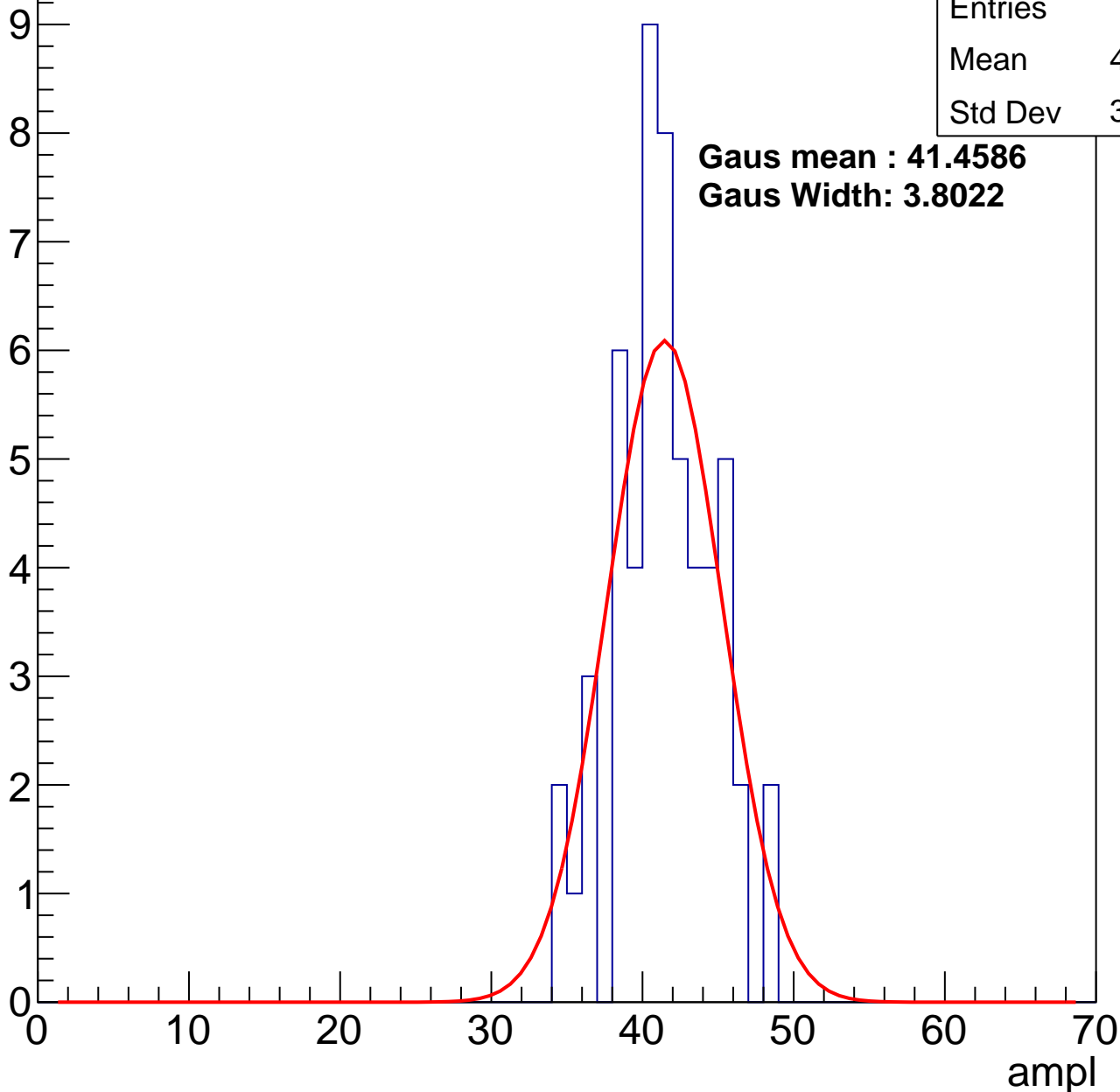
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	40.98
Std Dev	3.228

Gaus mean : 41.4586

Gaus Width: 3.8022

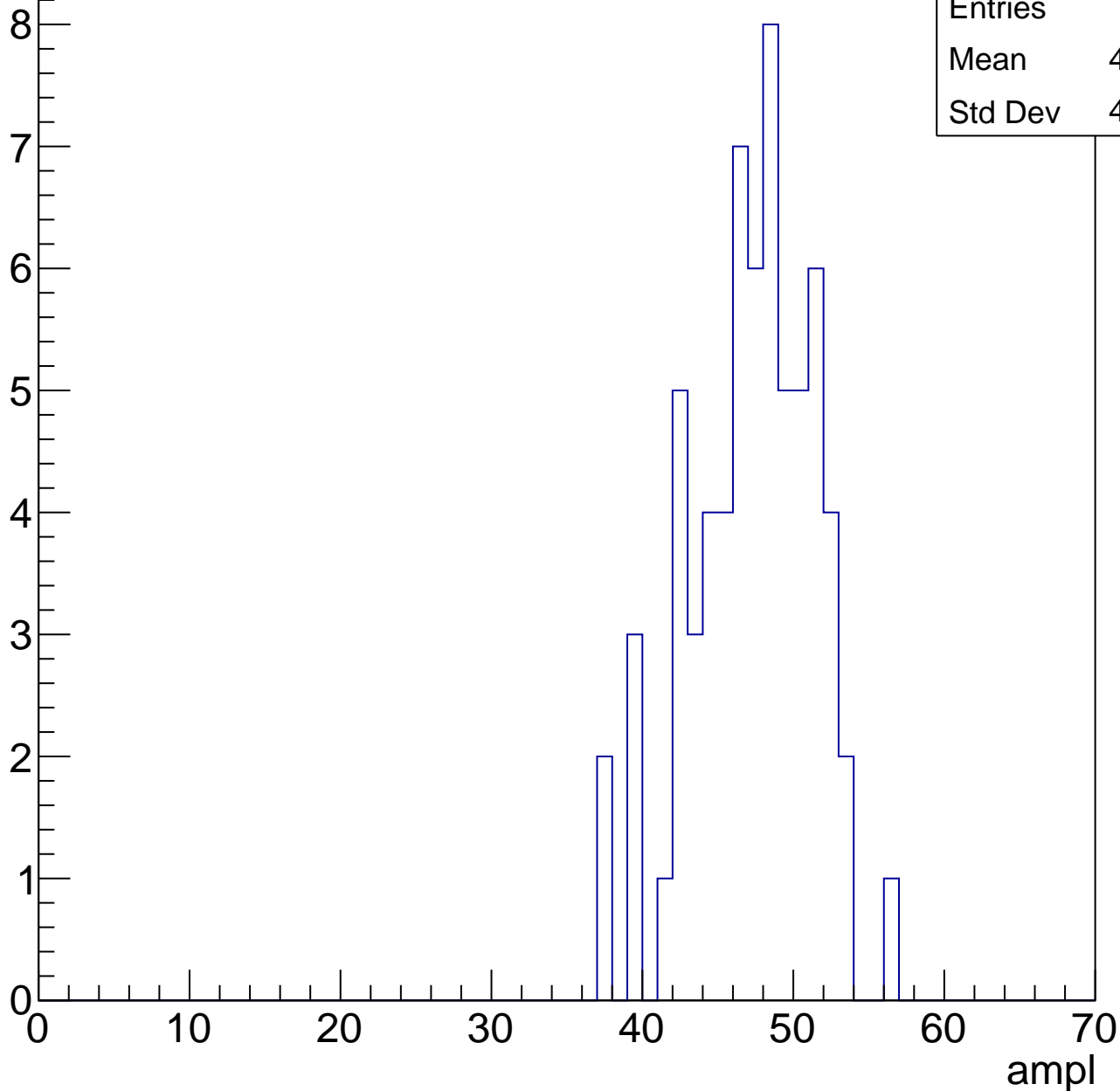


B1L102S, U17-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.76
Std Dev	4.042

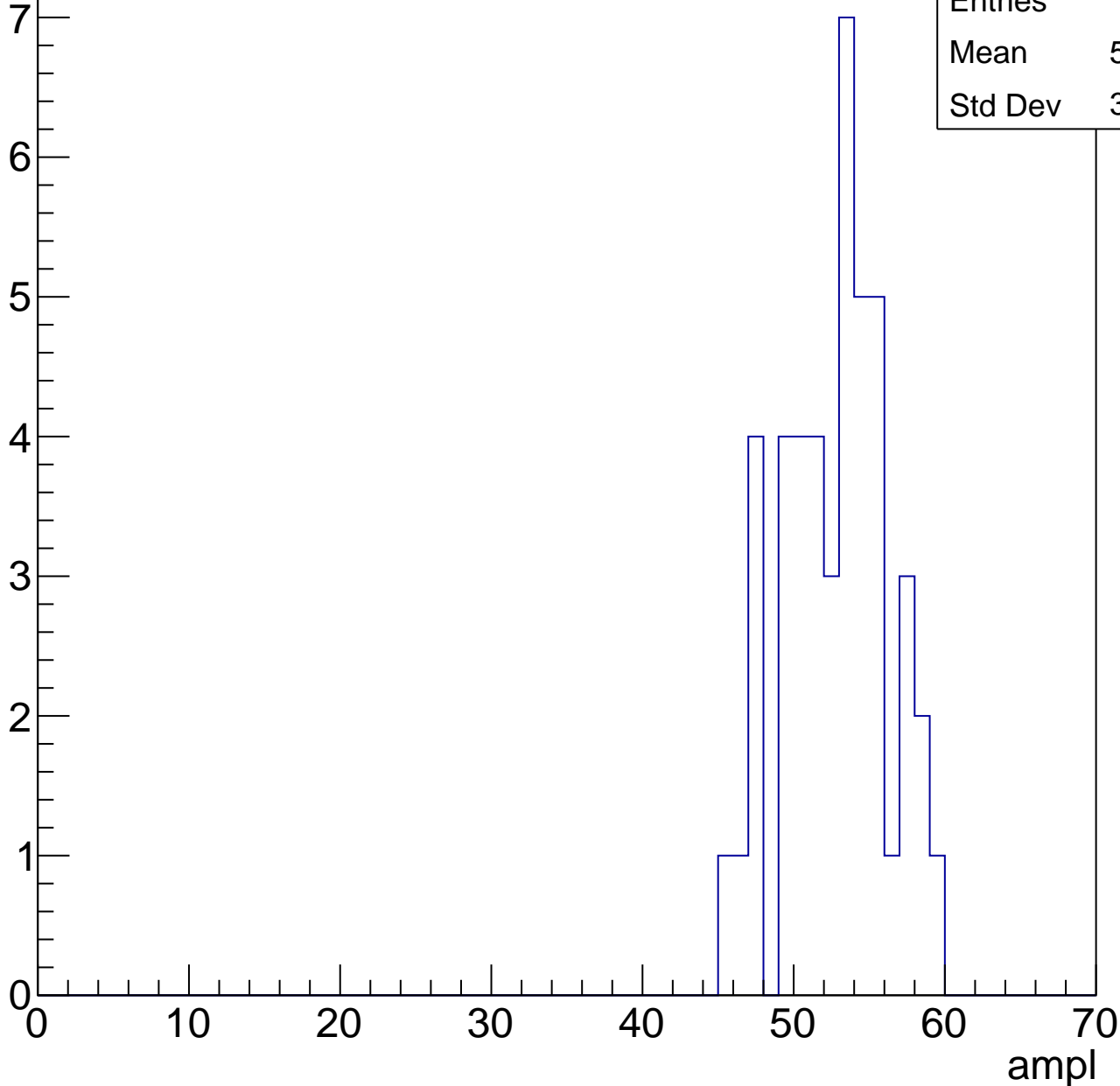


B1L102S, U17-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	52.29
Std Dev	3.403

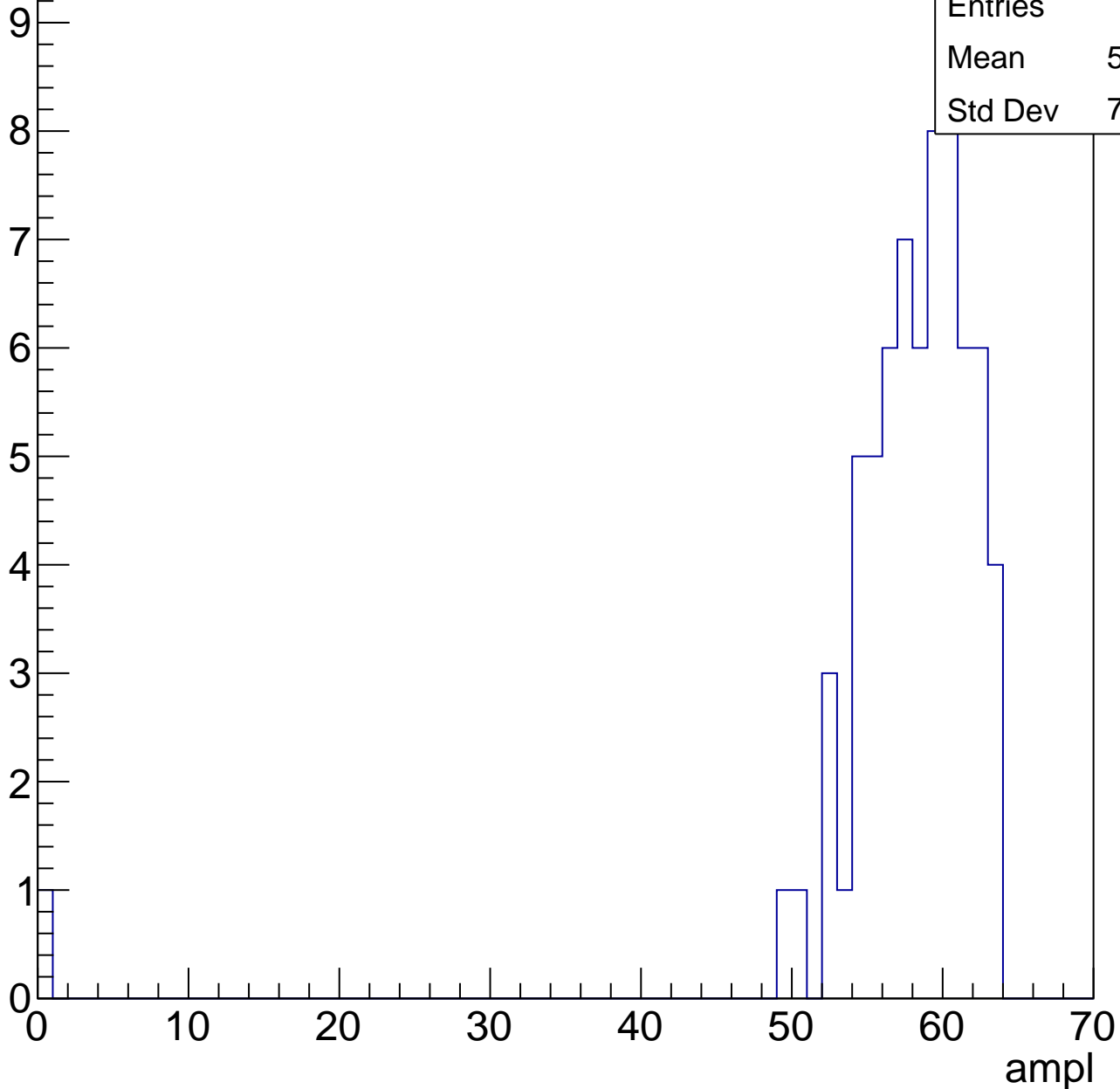


B1L102S, U17-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	57.07
Std Dev	7.645

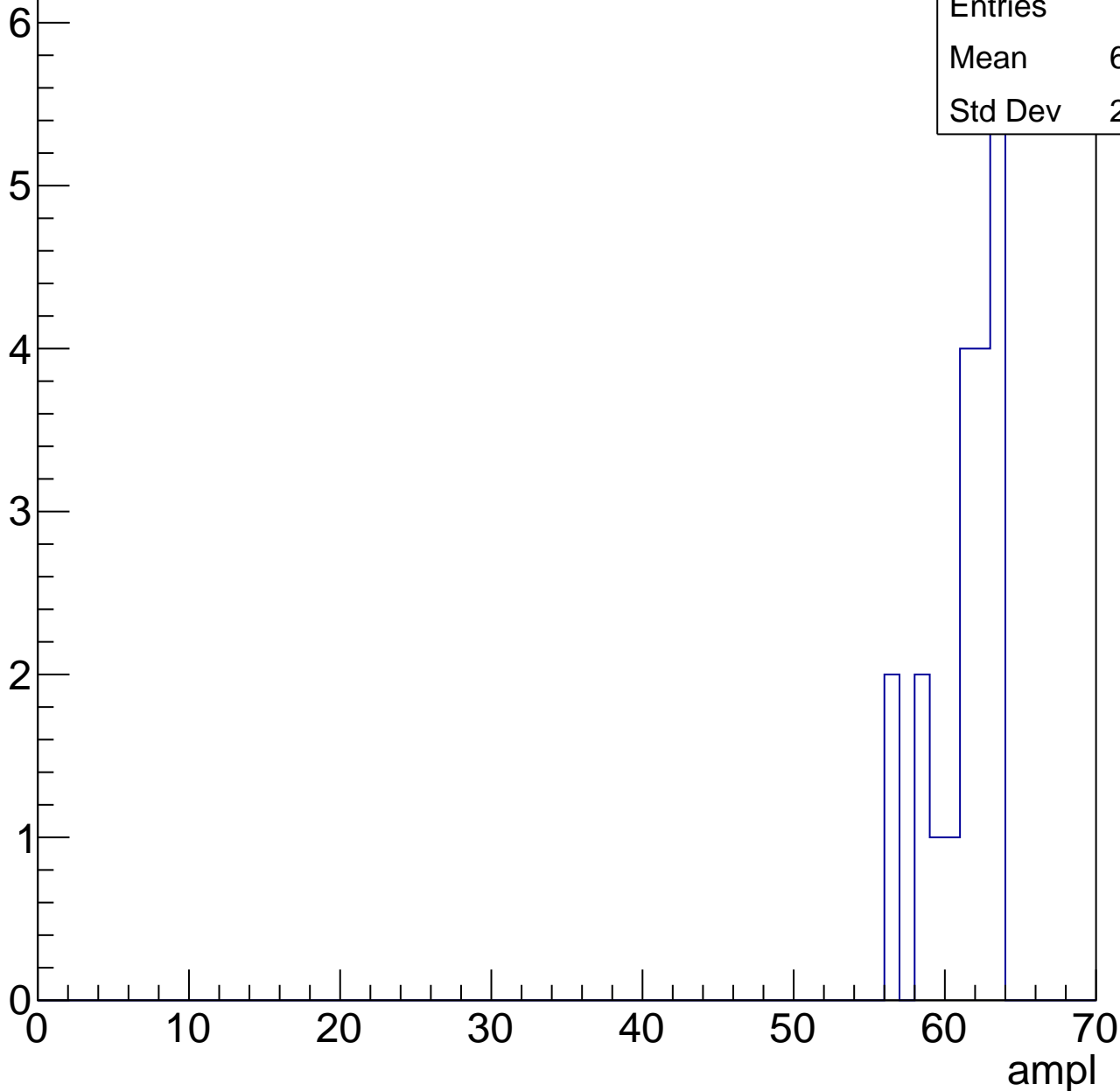


B1L102S, U17-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	60.85
Std Dev	2.242



B1L102S, U17-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U17-ch99, adc0

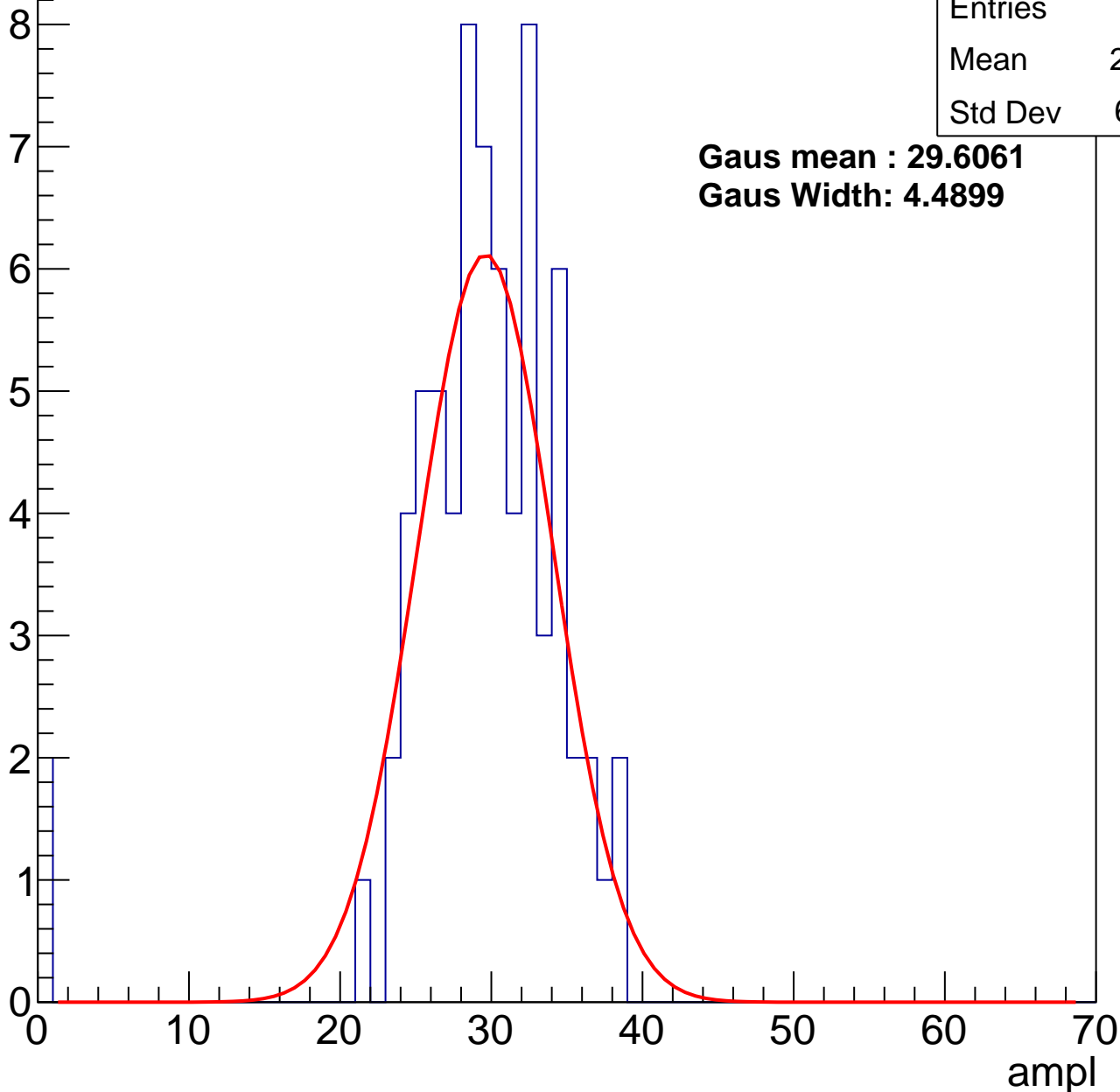
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.76
Std Dev	6.181

Gaus mean : 29.6061

Gaus Width: 4.4899



B1L102S, U17-ch99, adc1

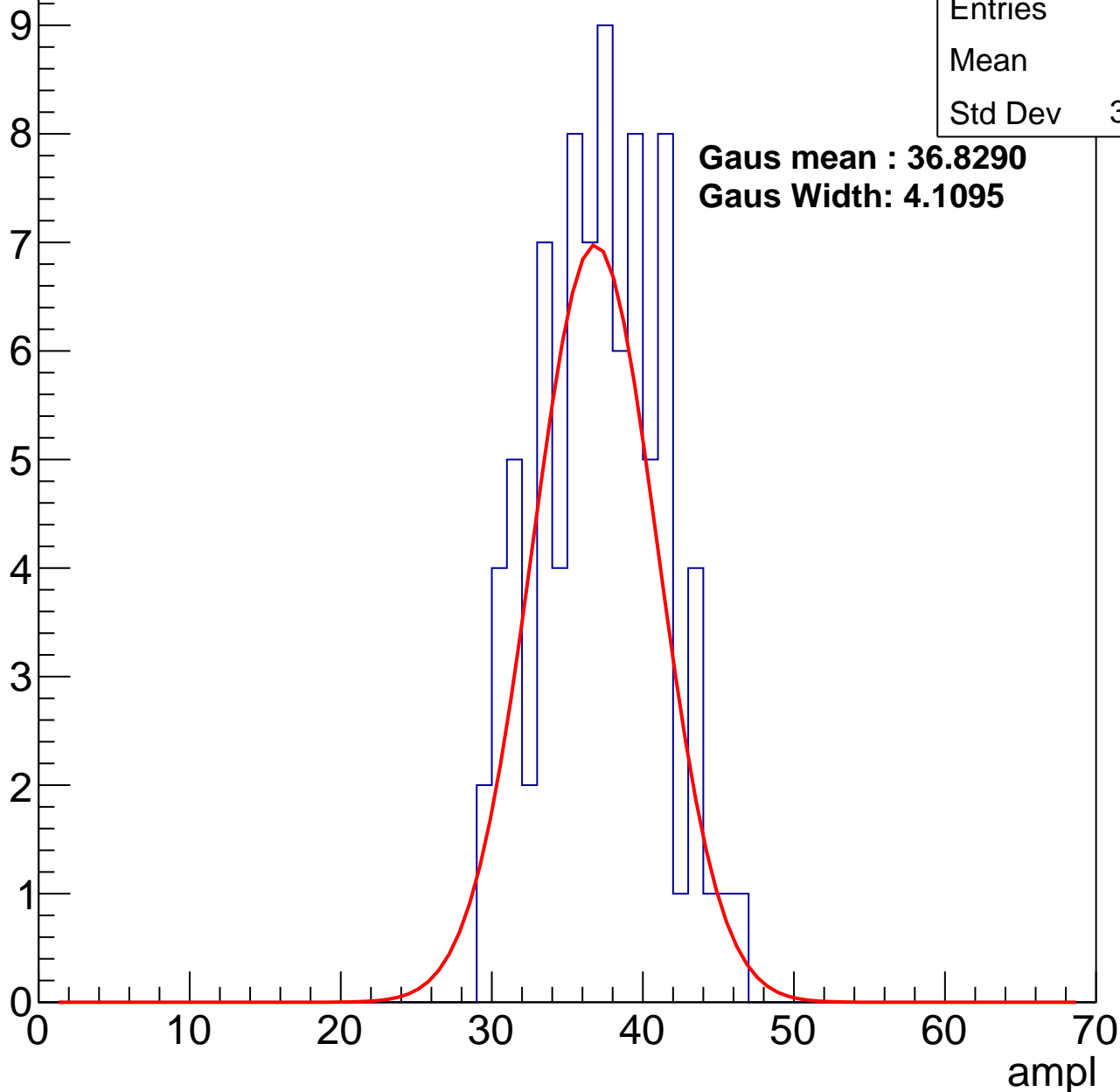
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	36.7
Std Dev	3.969

Gaus mean : 36.8290

Gaus Width: 4.1095



B1L102S, U17-ch99, adc2

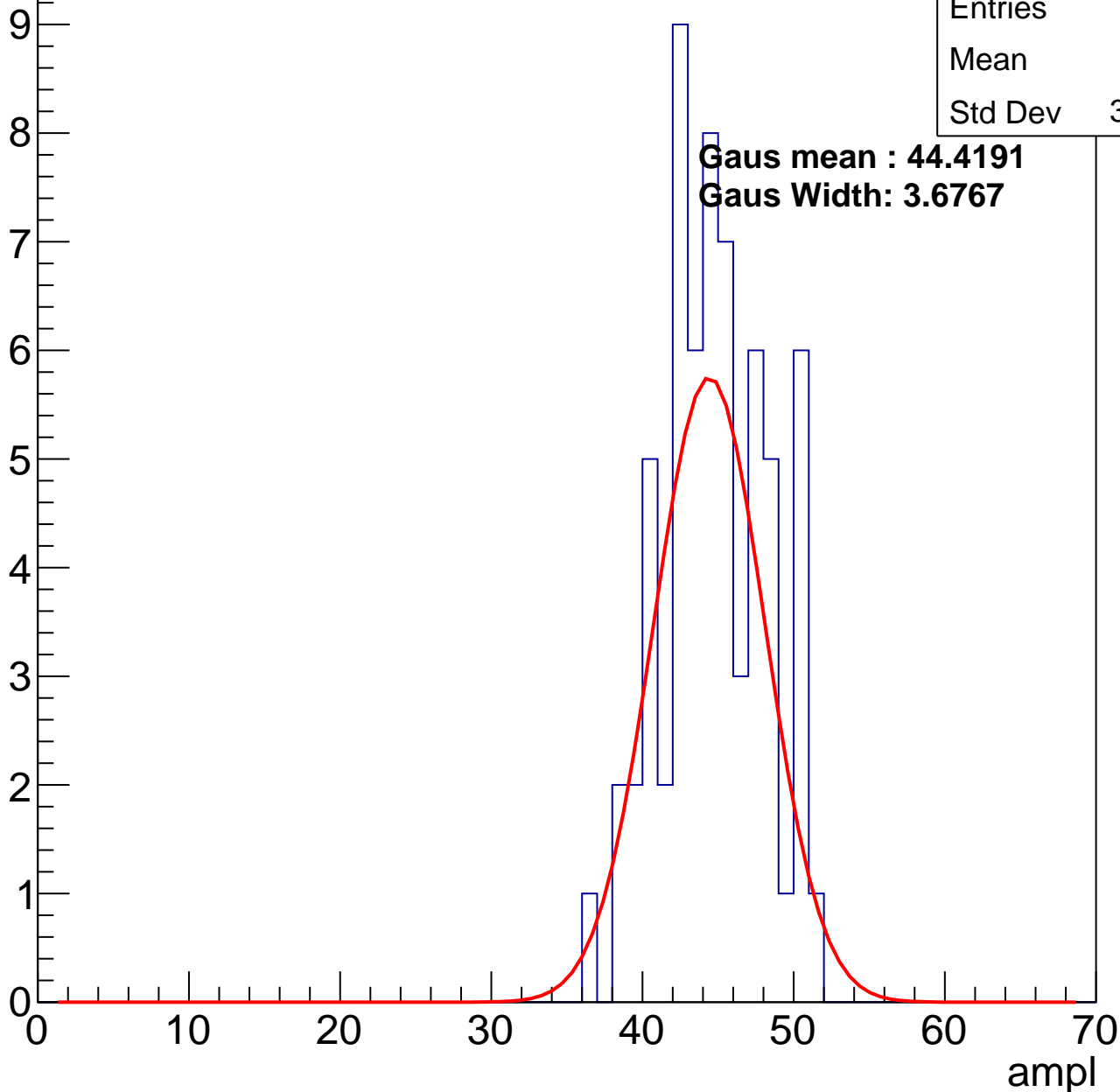
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	44.3
Std Dev	3.454

Gaus mean : 44.4191

Gaus Width: 3.6767

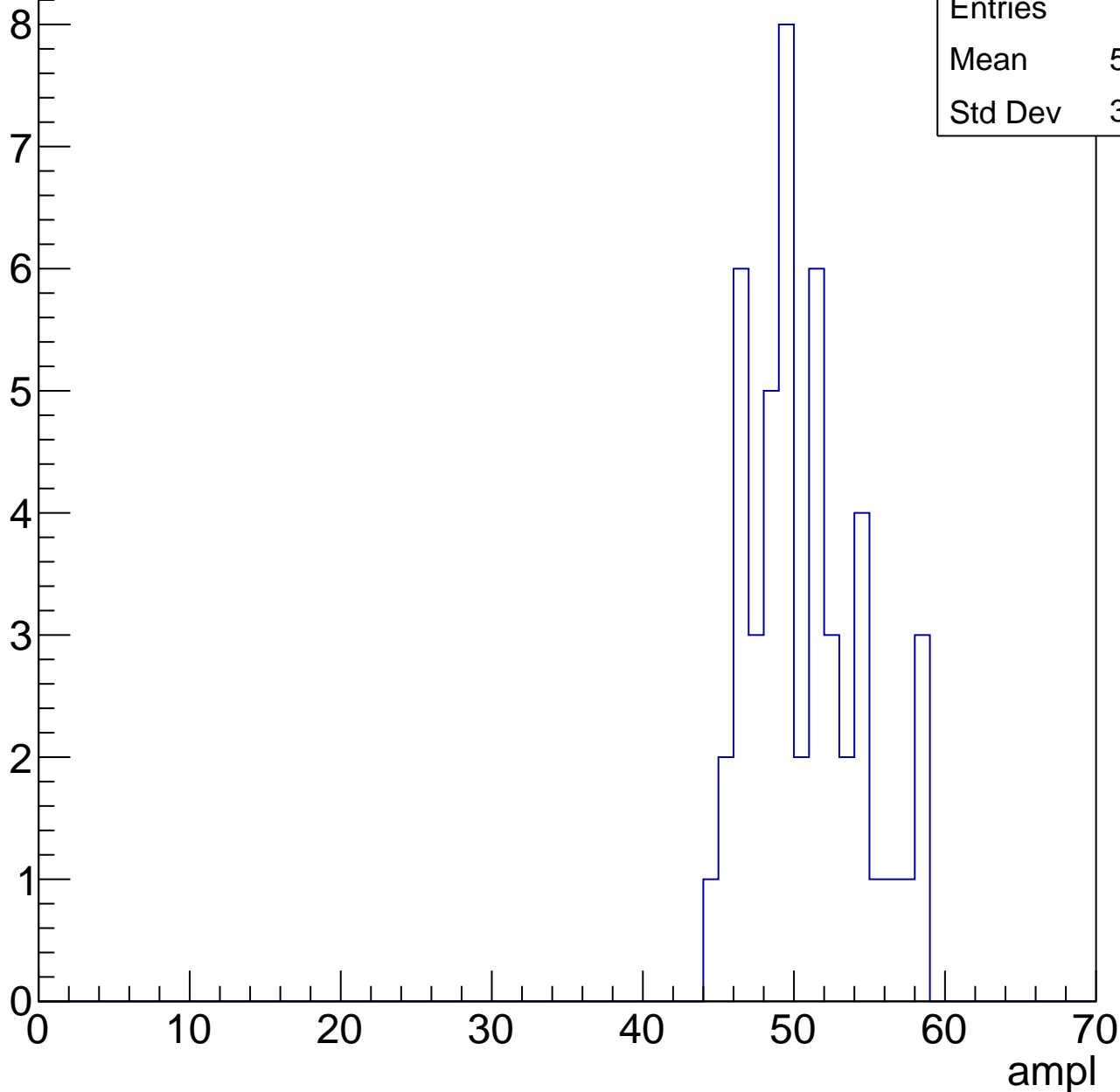


B1L102S, U17-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	50.19
Std Dev	3.644

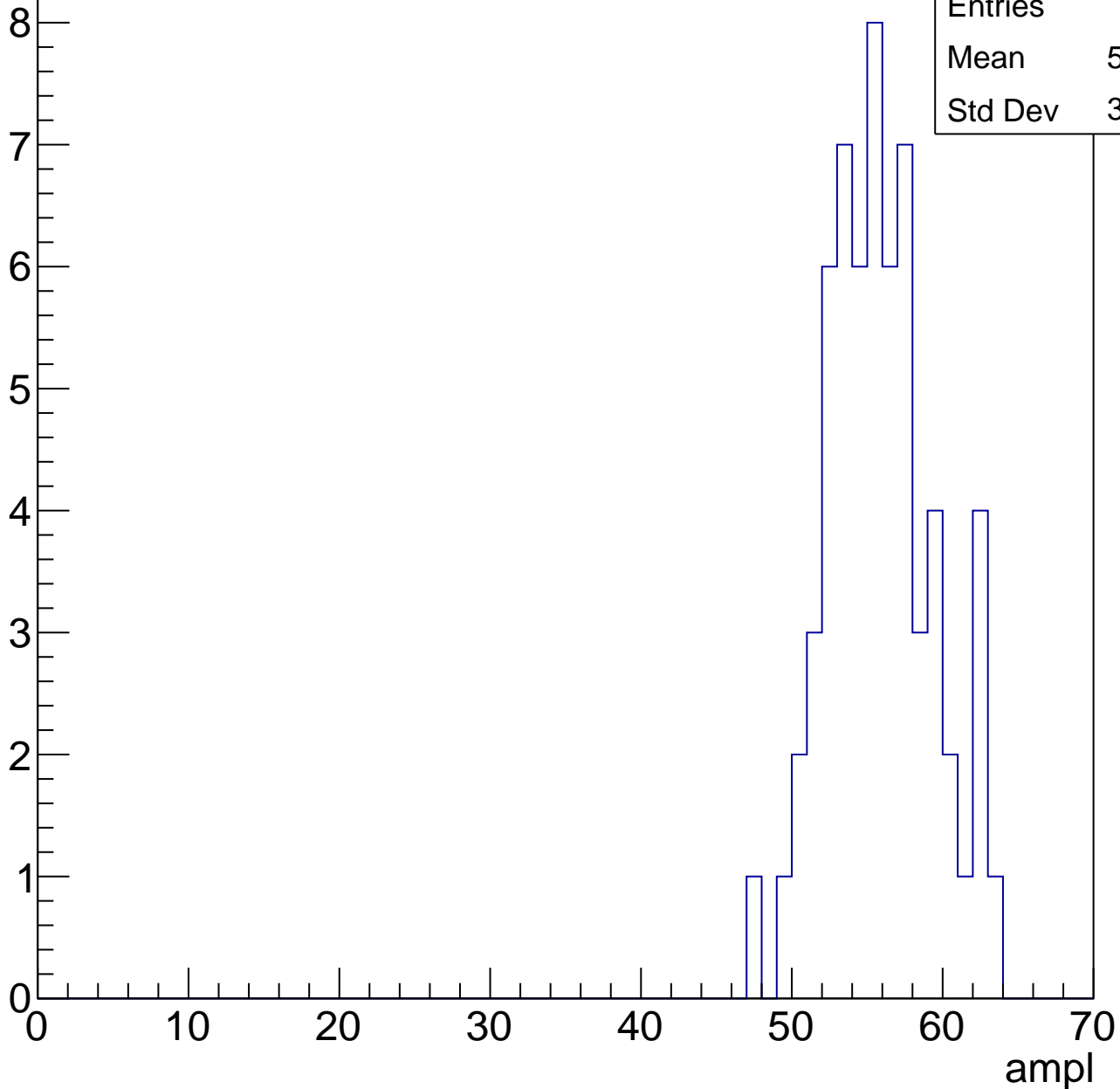


B1L102S, U17-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.37
Std Dev	3.474

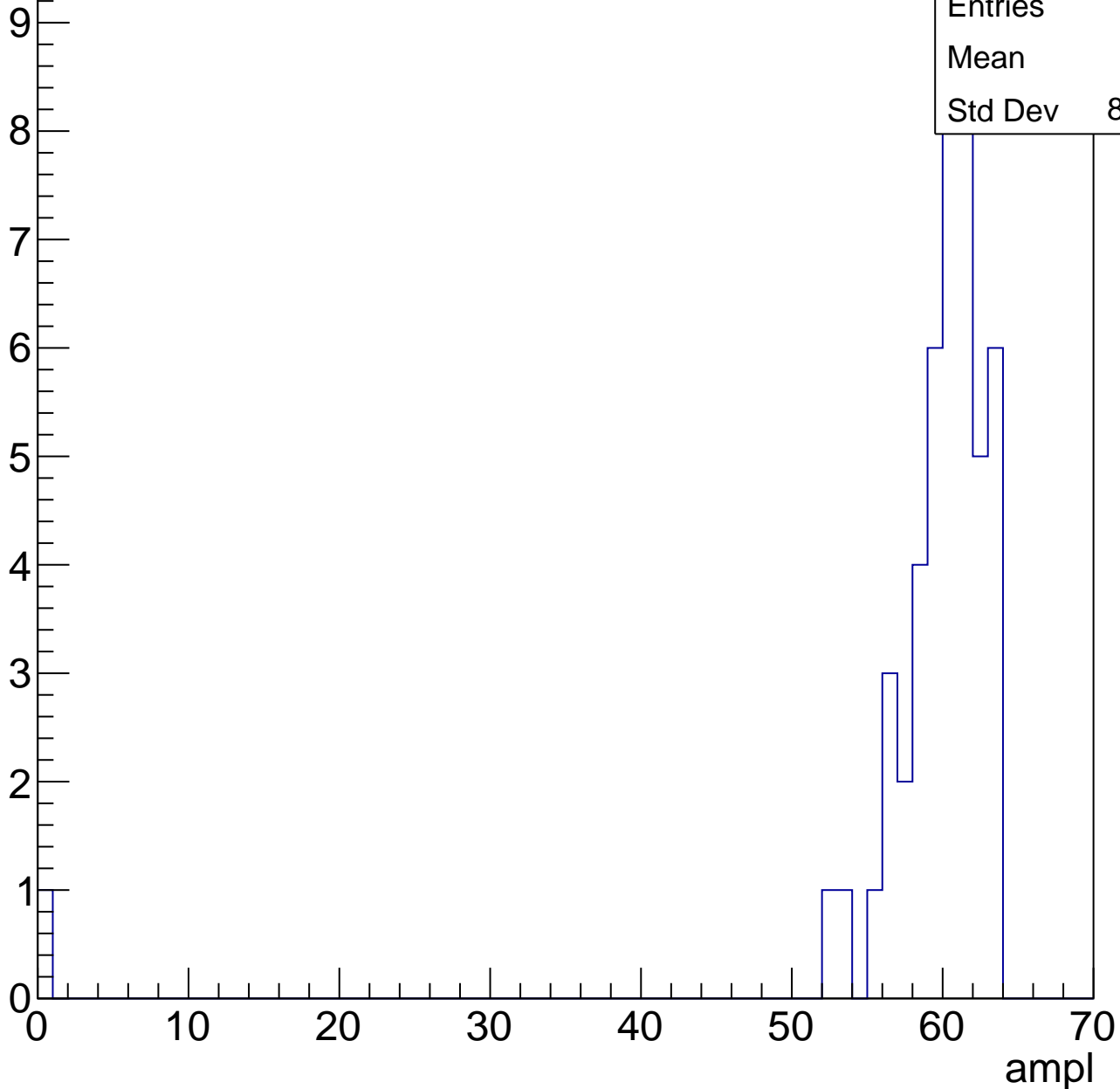


B1L102S, U17-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.4
Std Dev	8.979



B1L102S, U17-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch100, adc0

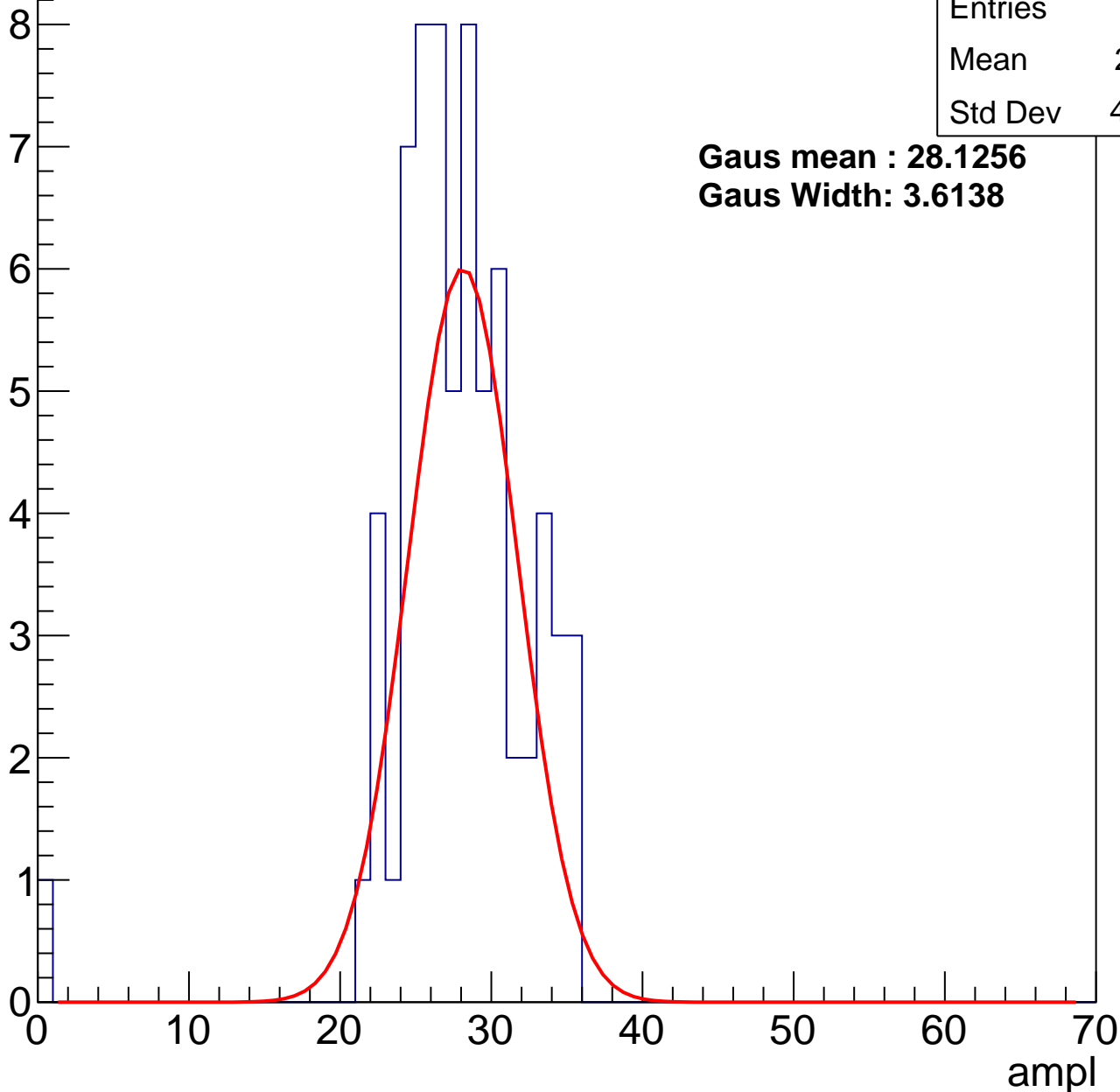
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.31
Std Dev	4.888

Gaus mean : 28.1256

Gaus Width: 3.6138



B1L102S, U17-ch100, adc1

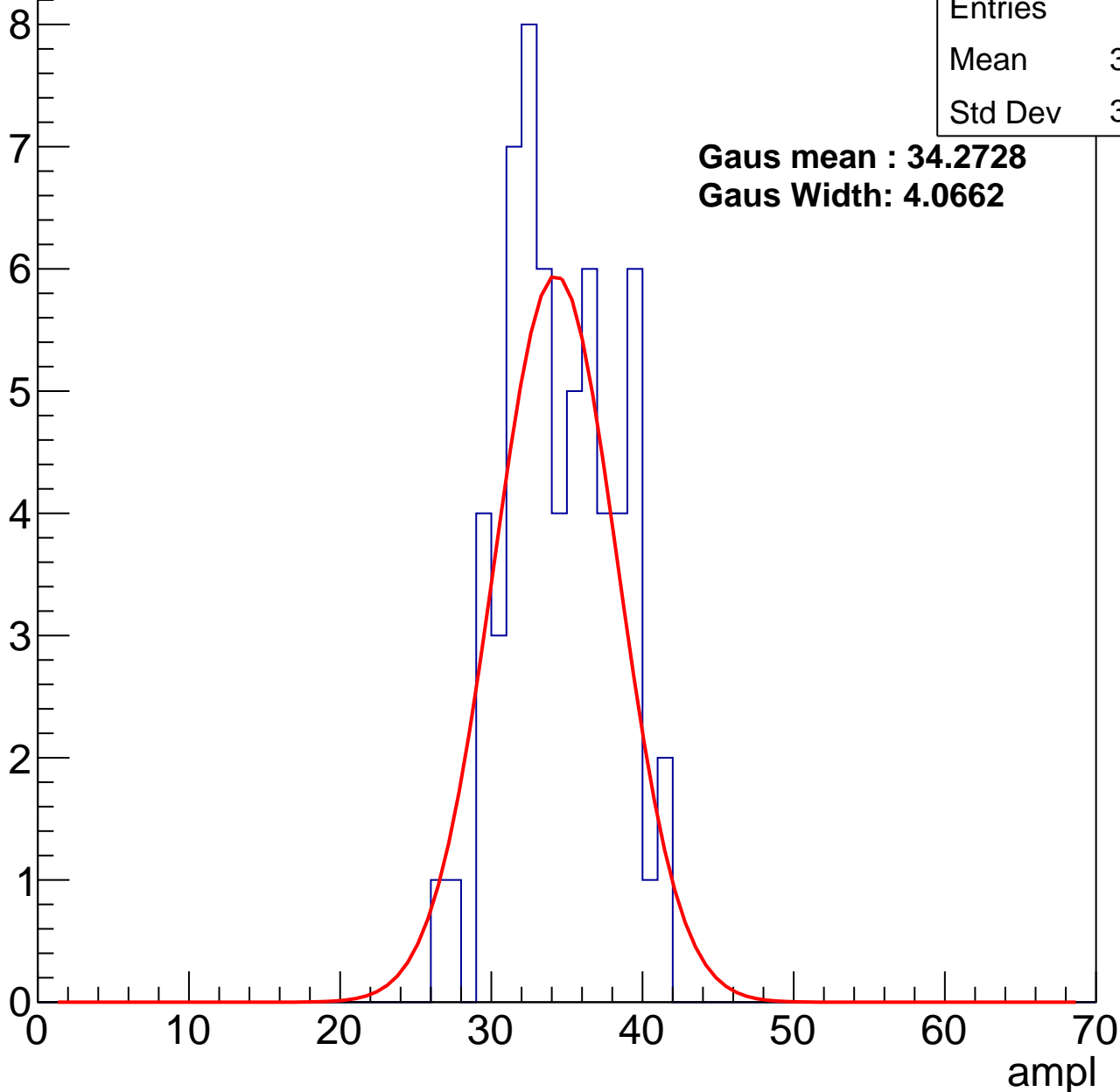
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.08
Std Dev	3.535

Gaus mean : 34.2728

Gaus Width: 4.0662



B1L102S, U17-ch100, adc2

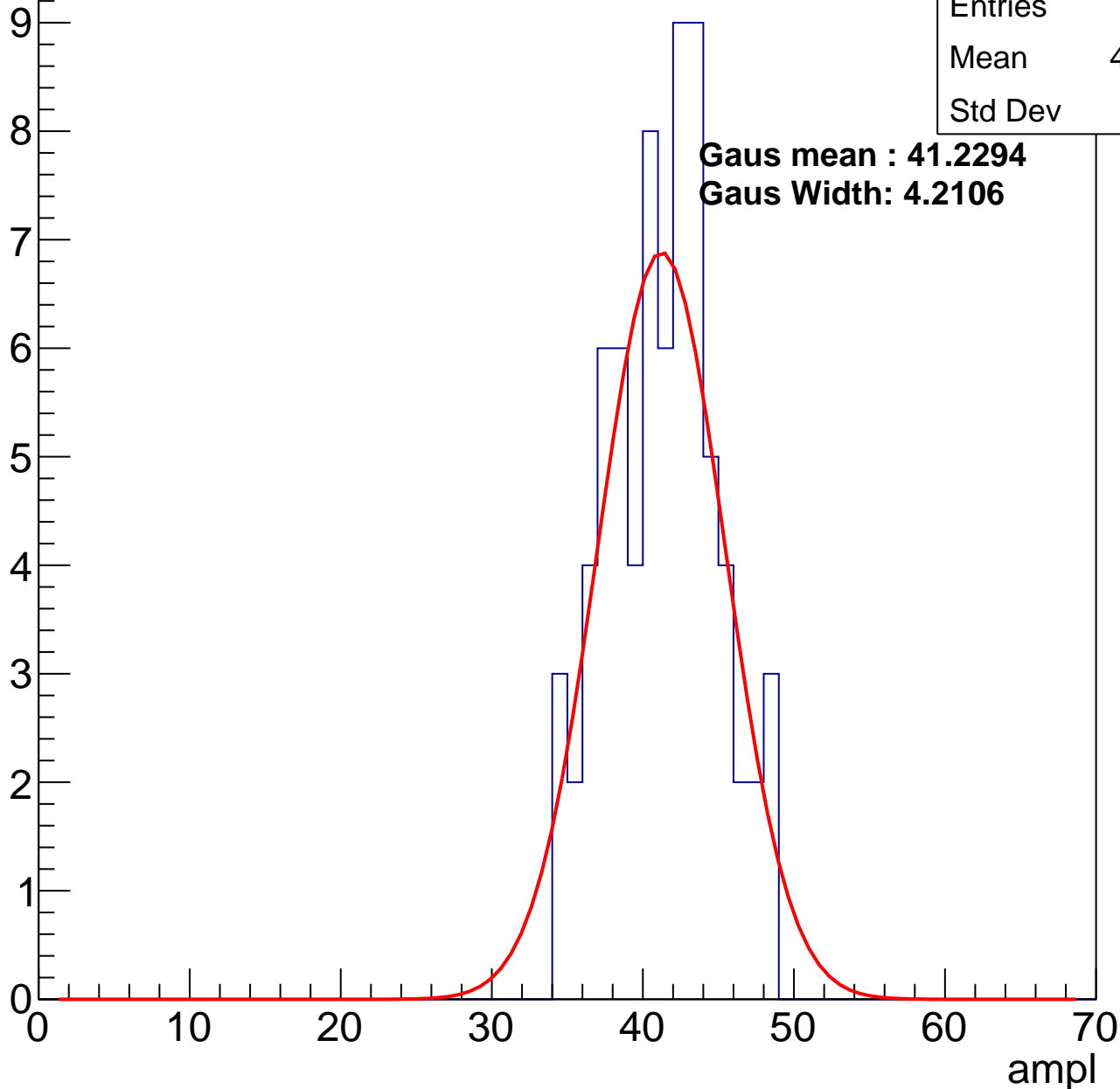
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	40.86
Std Dev	3.54

Gaus mean : 41.2294

Gaus Width: 4.2106

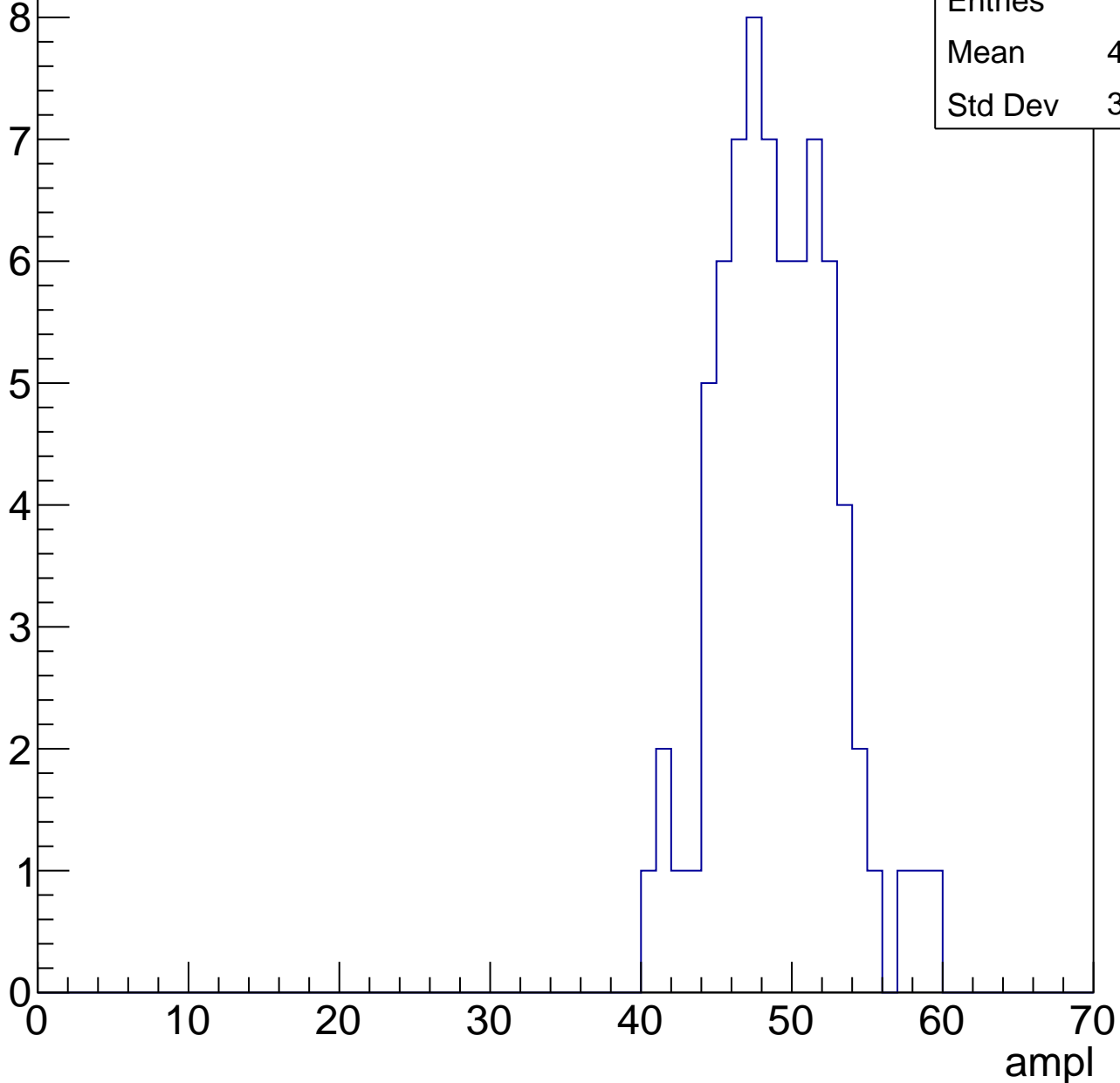


B1L102S, U17-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	48.53
Std Dev	3.854

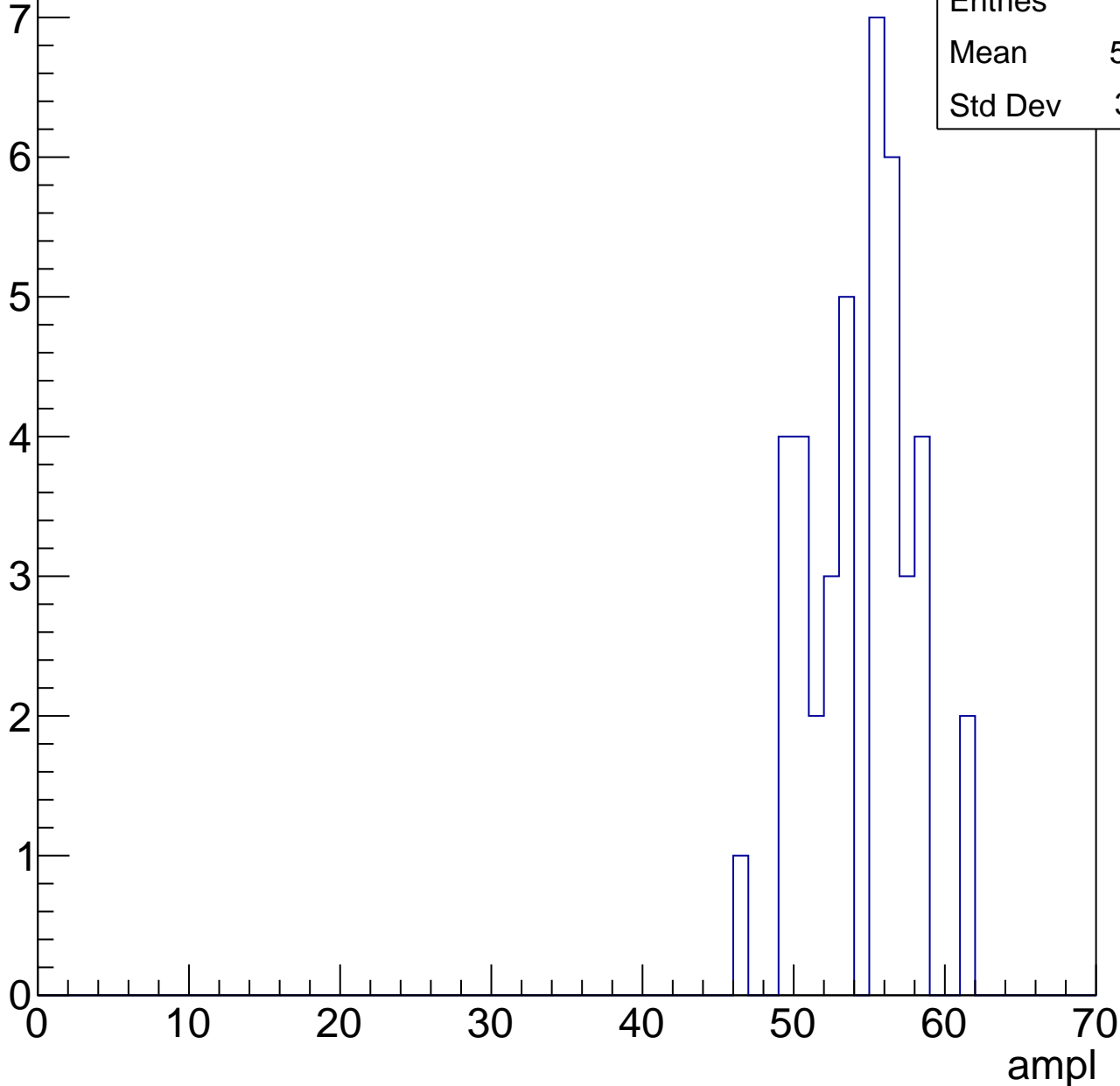


B1L102S, U17-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

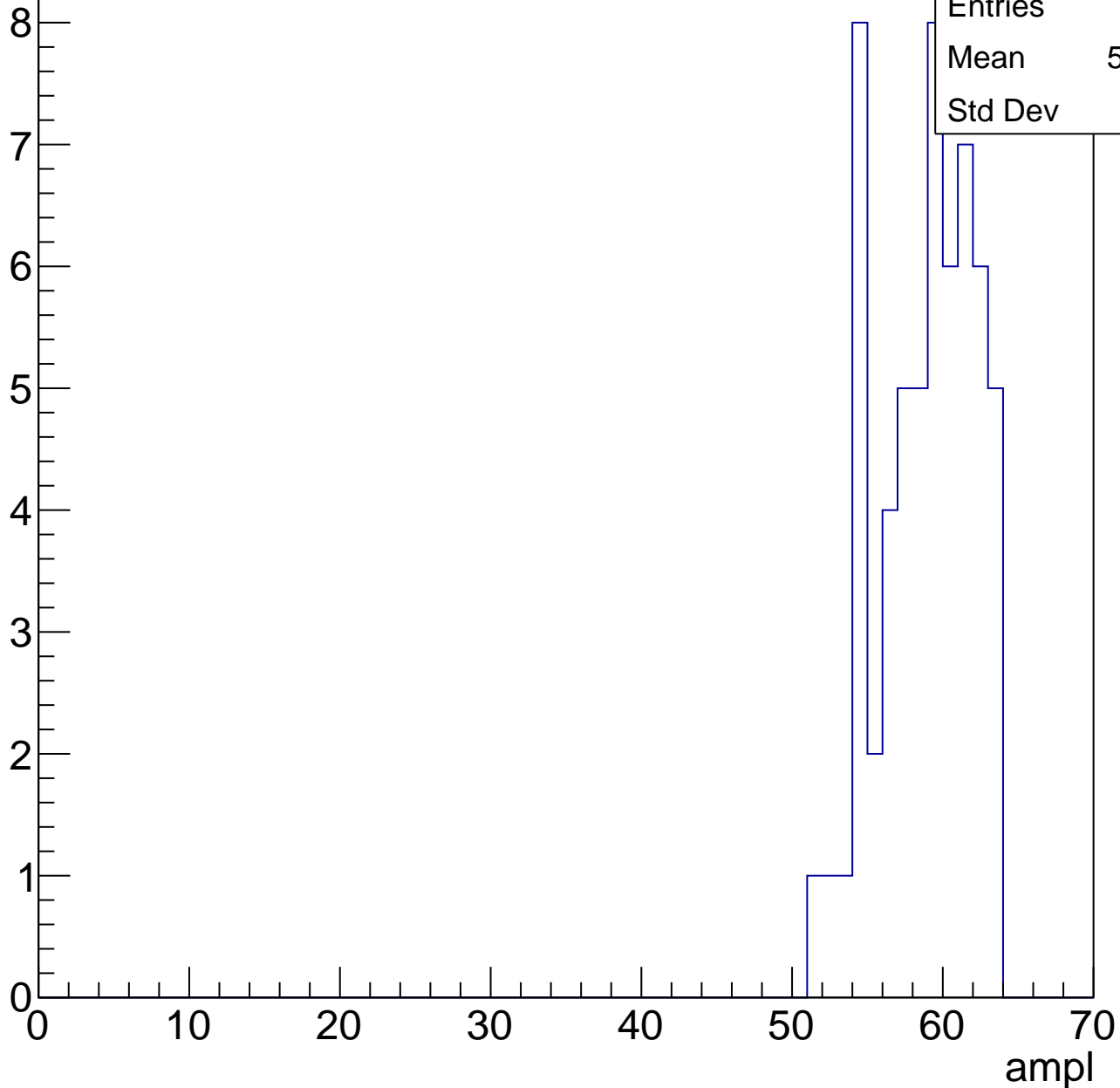
Entries	41
Mean	53.93
Std Dev	3.431



B1L102S, U17-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

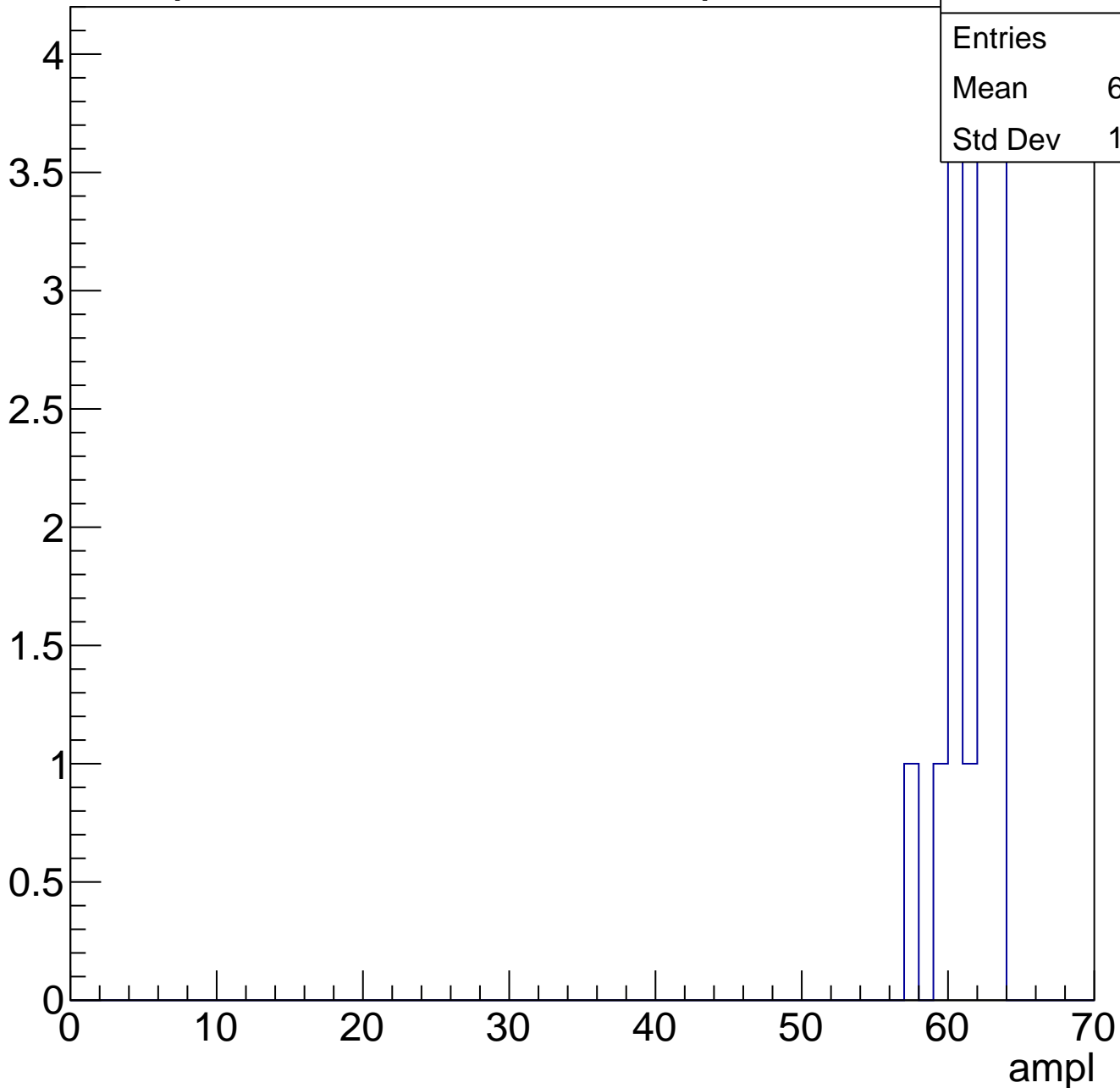


Entries	59
Mean	58.36
Std Dev	3.15

B1L102S, U17-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

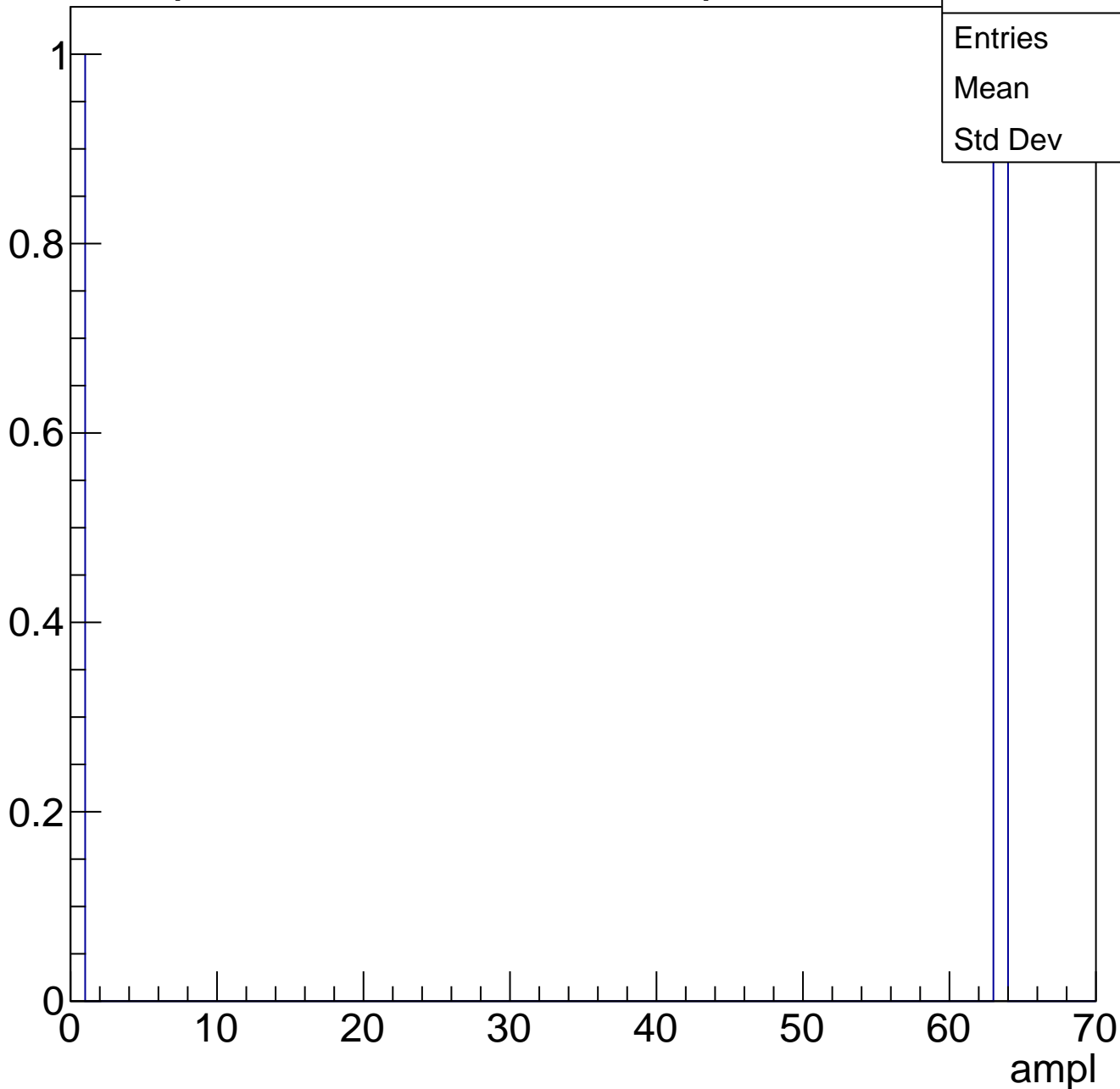
Entry



B1L102S, U17-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch101, adc0

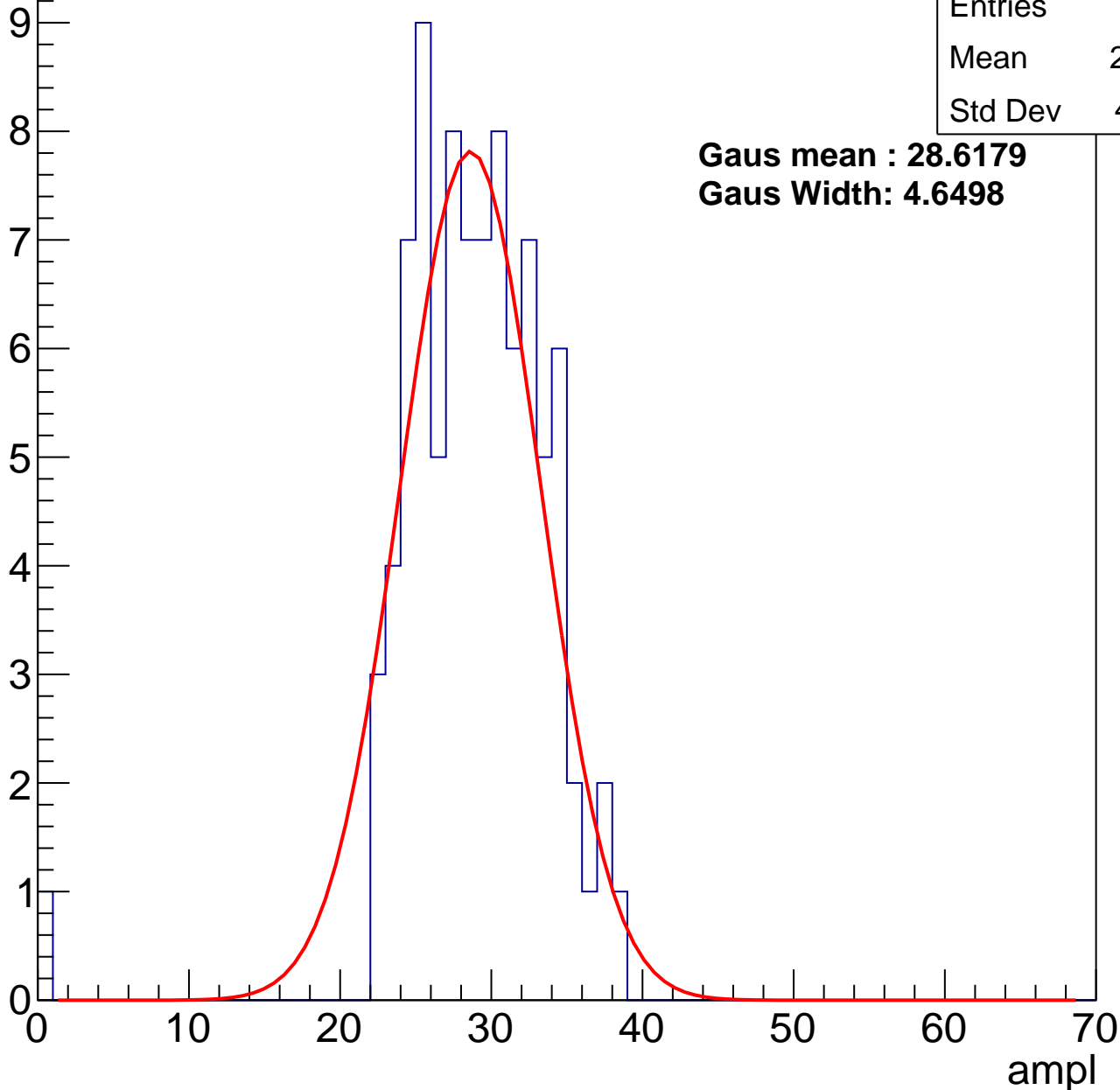
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	28.46
Std Dev	4.931

Gaus mean : 28.6179

Gaus Width: 4.6498



B1L102S, U17-ch101, adc1

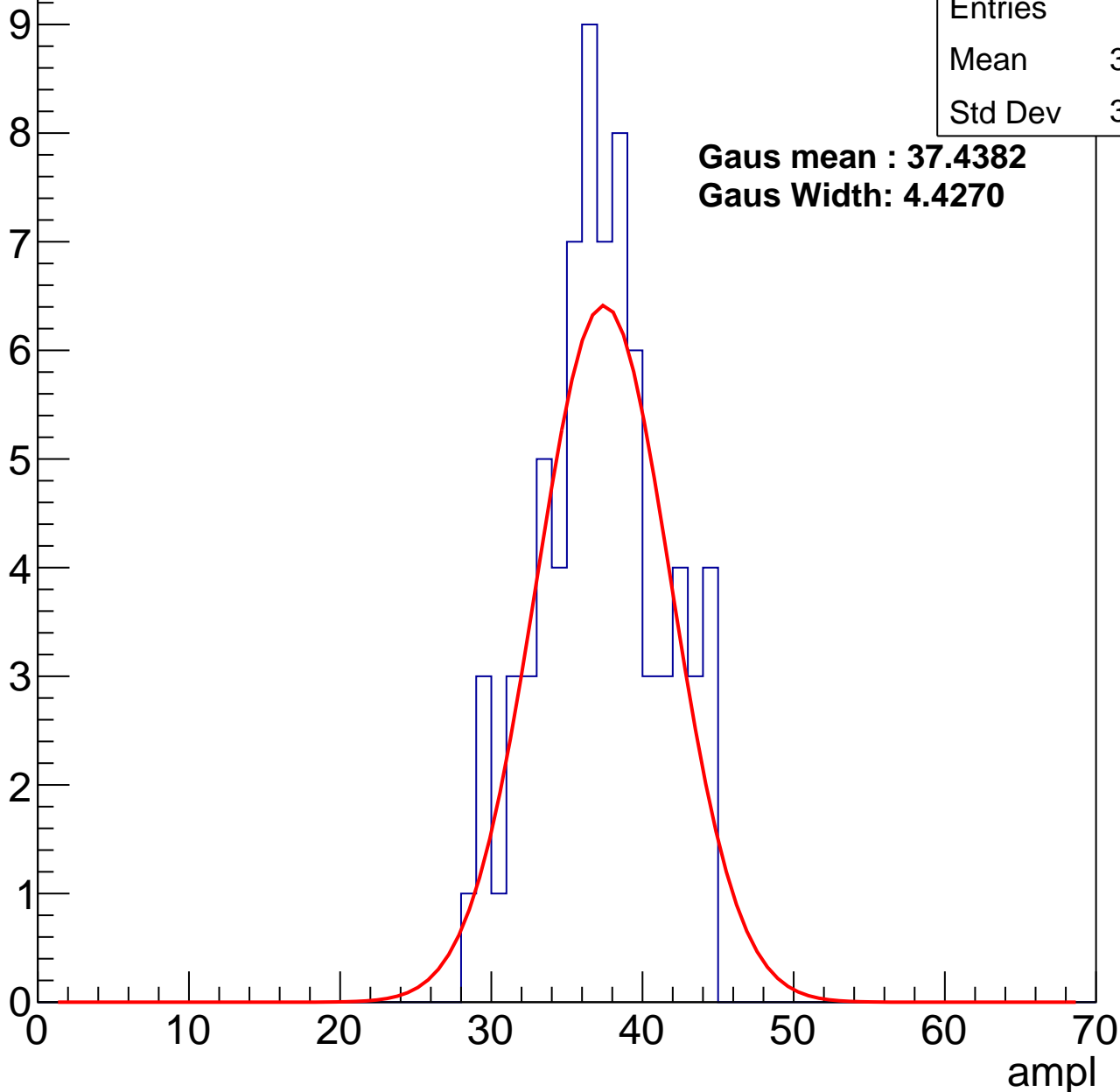
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.72
Std Dev	3.964

Gaus mean : 37.4382

Gaus Width: 4.4270



B1L102S, U17-ch101, adc2

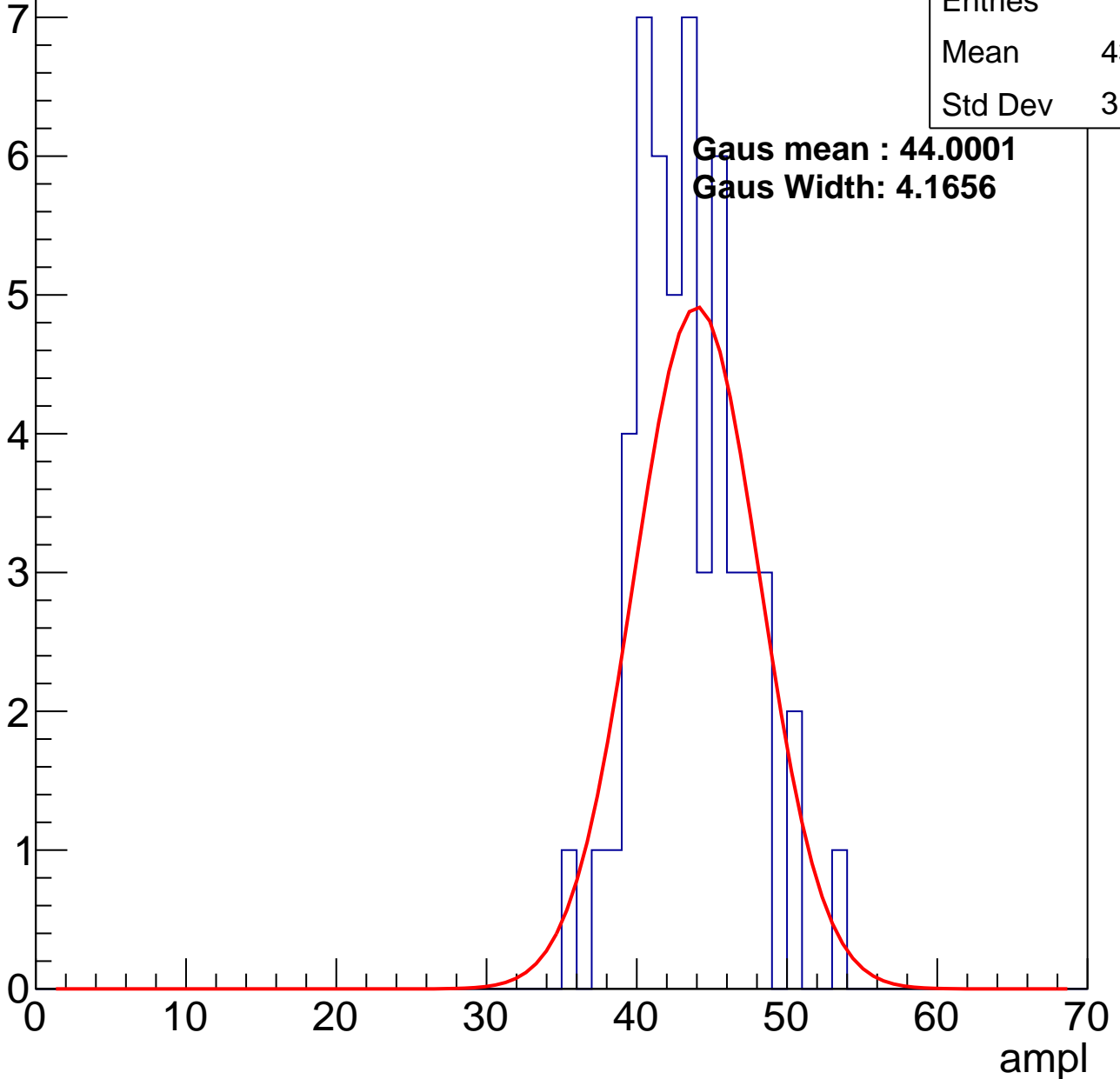
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	43.04
Std Dev	3.518

Gaus mean : 44.0001

Gaus Width: 4.1656

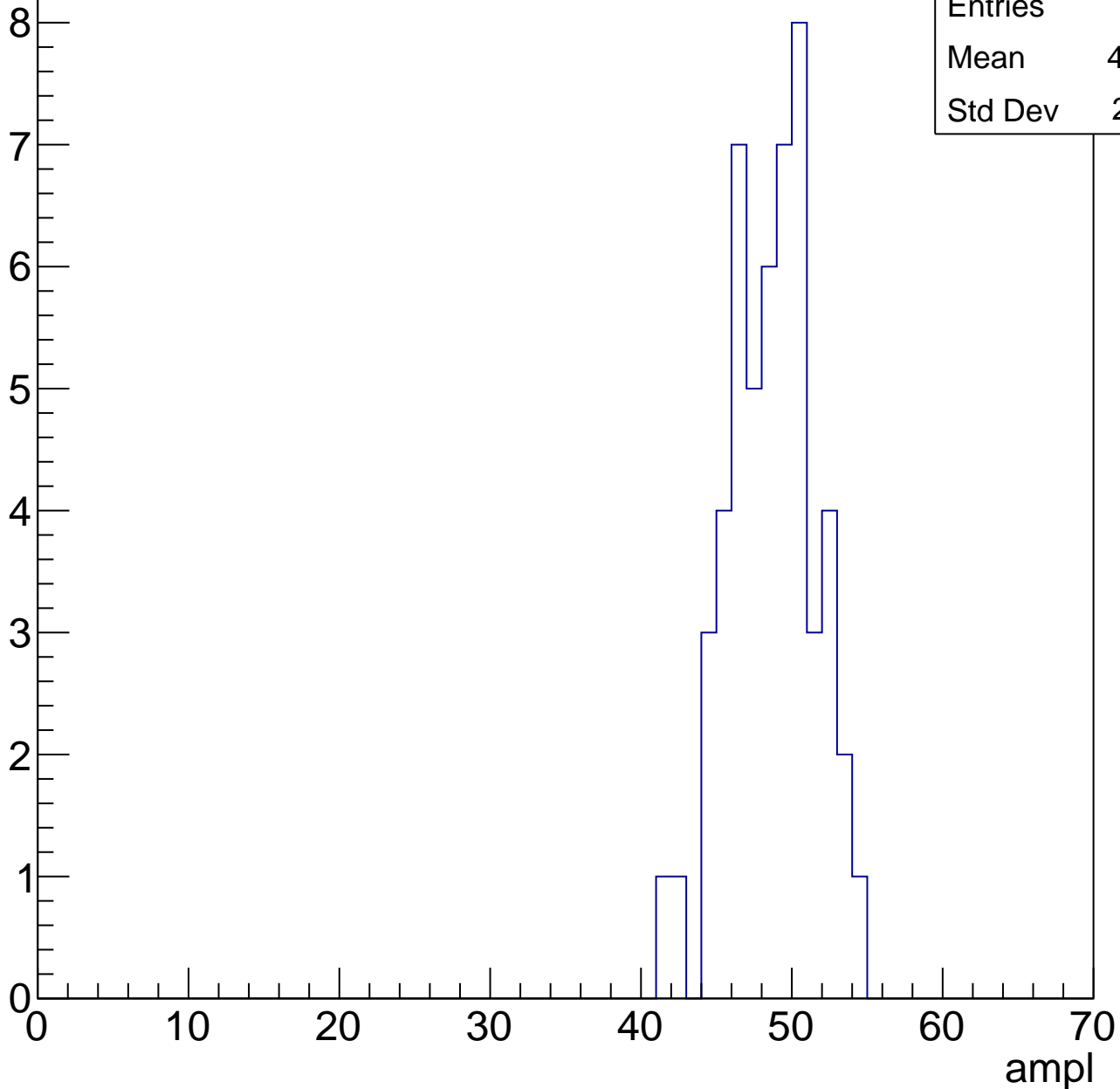


B1L102S, U17-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	48.15
Std Dev	2.831

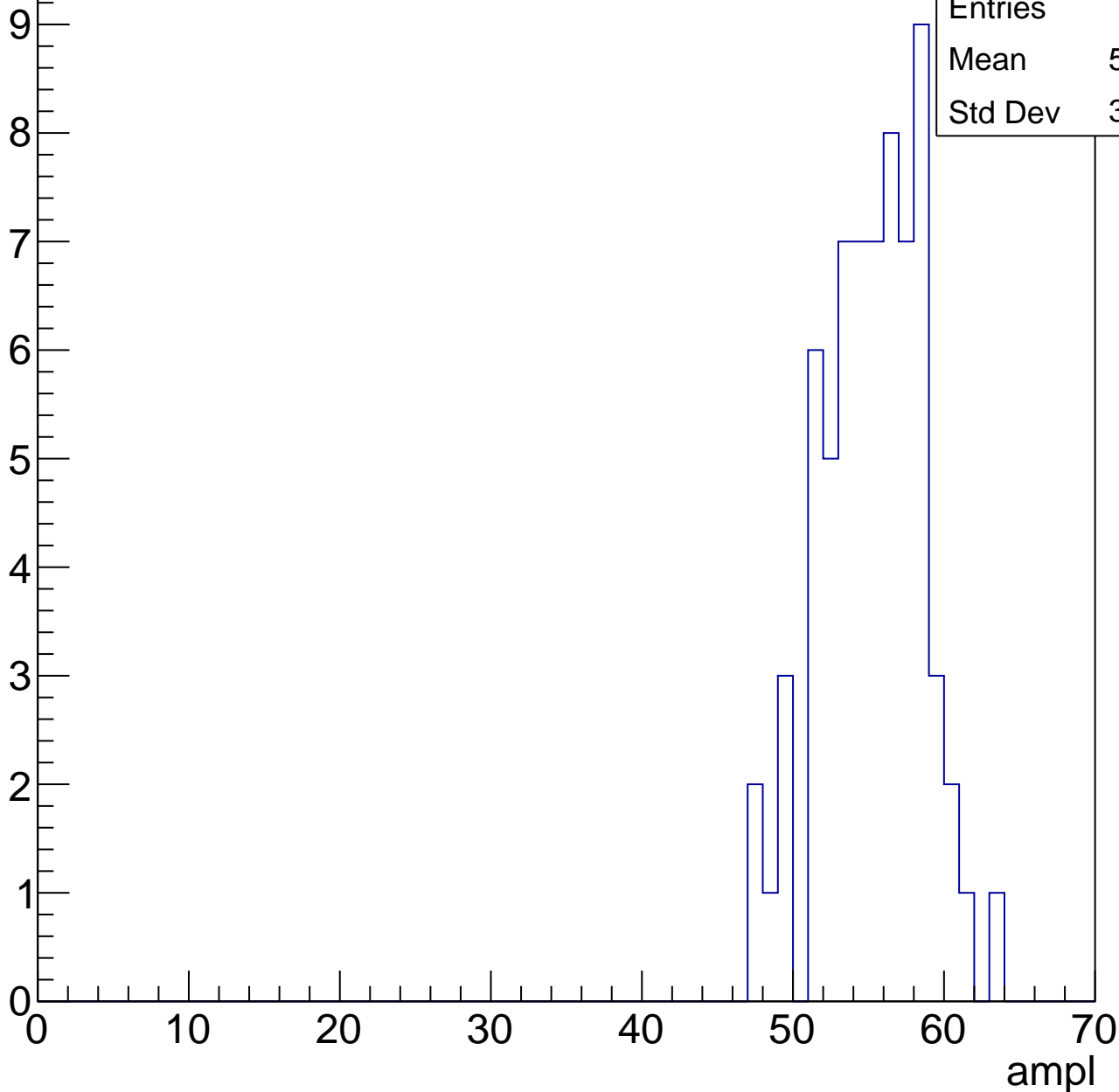


B1L102S, U17-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	54.77
Std Dev	3.346

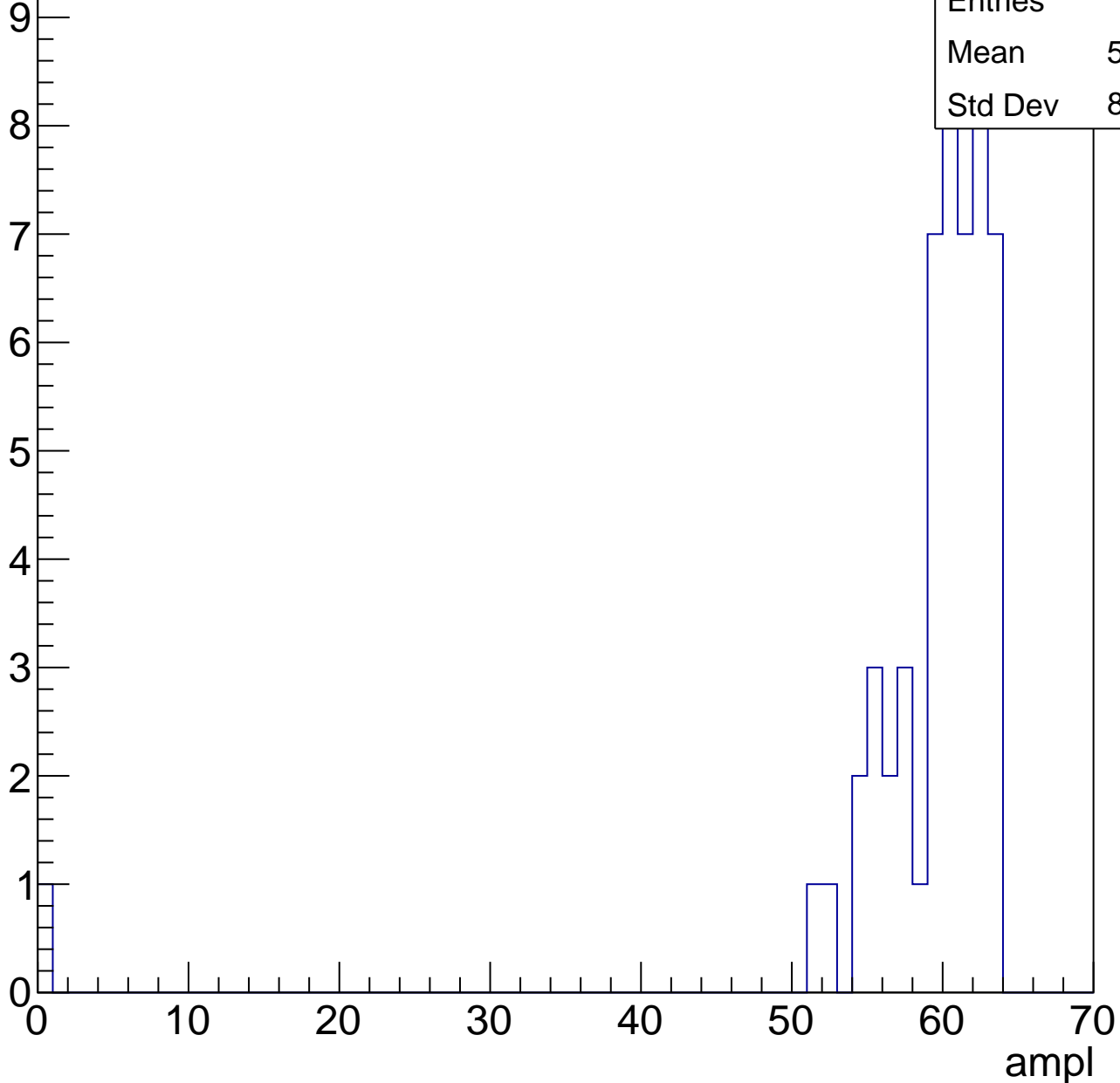


B1L102S, U17-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.38
Std Dev	8.696



B1L102S, U17-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

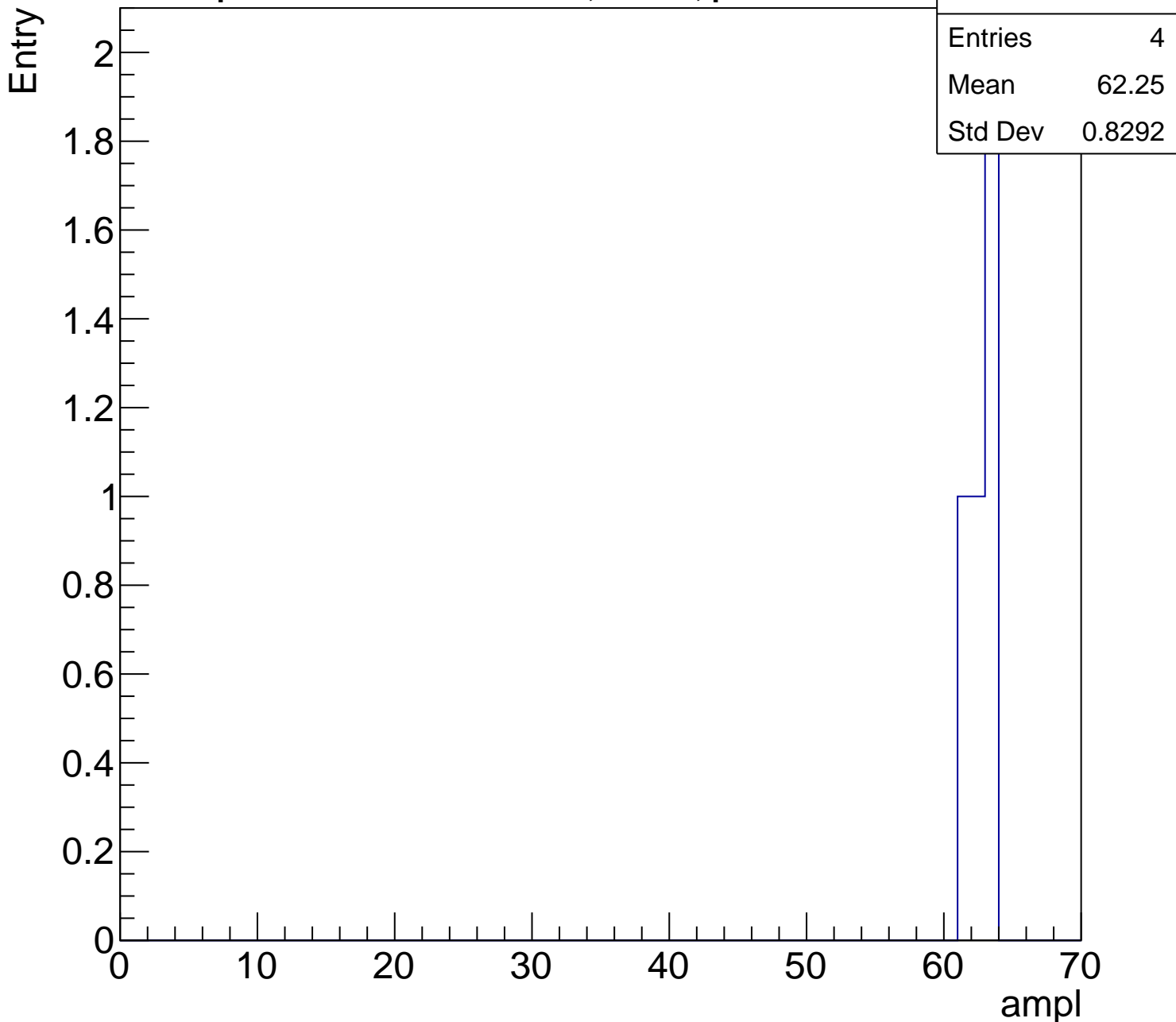
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



B1L102S, U17-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch102, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	27.81
Std Dev	3.798

Gaus mean : 28.6223

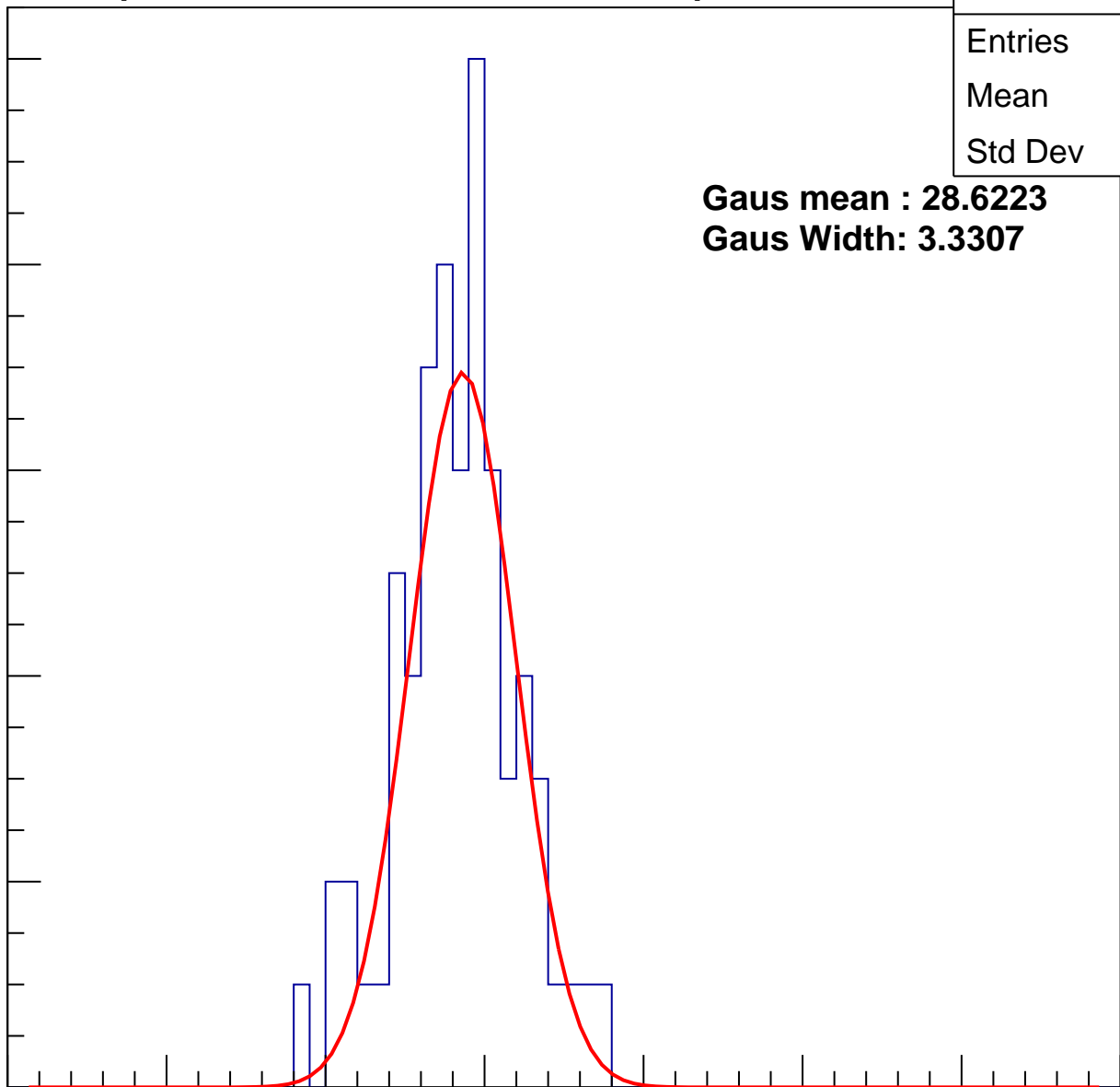
Gaus Width: 3.3307

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch102, adc1

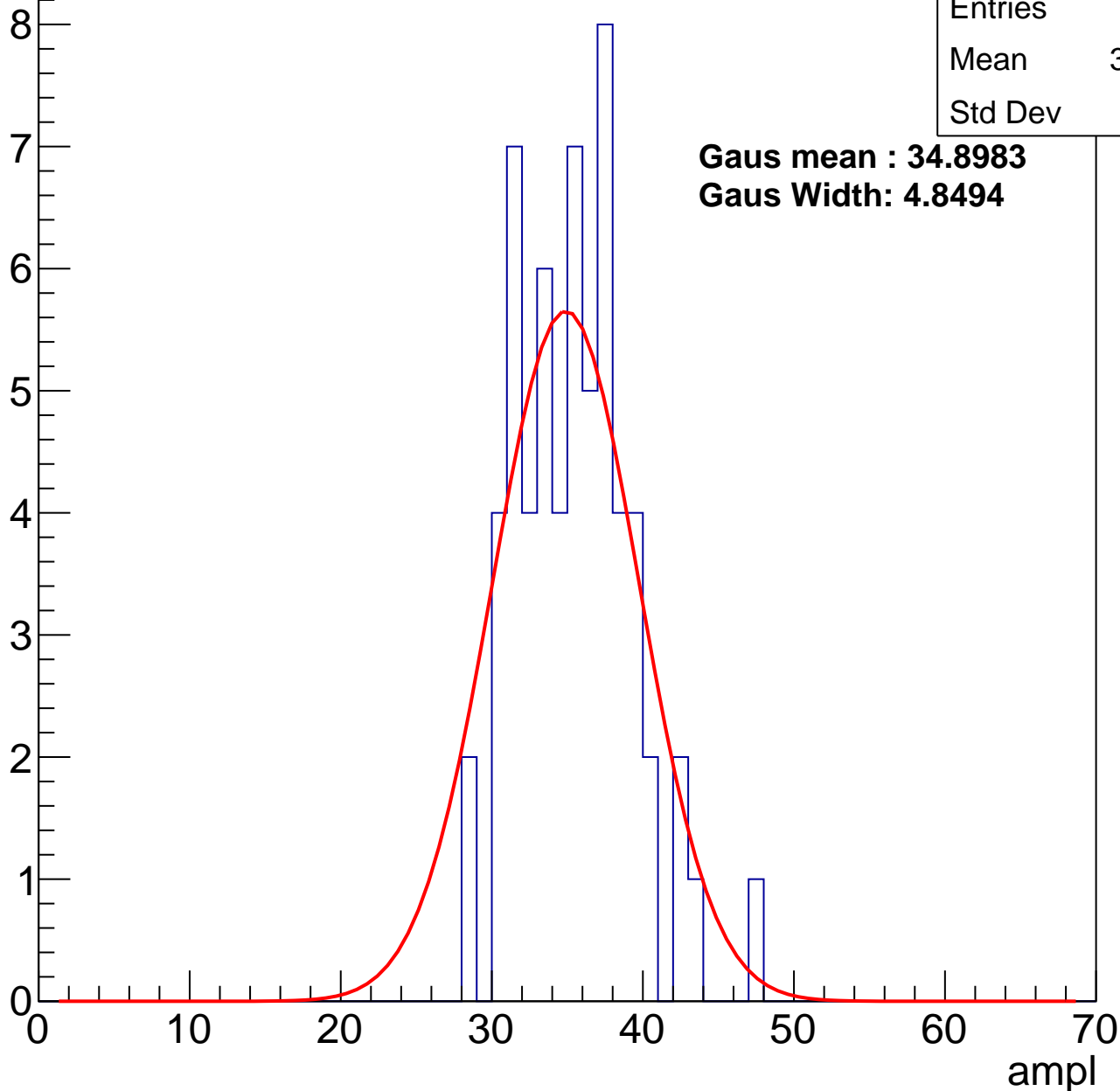
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.05
Std Dev	3.77

Gaus mean : 34.8983

Gaus Width: 4.8494



B1L102S, U17-ch102, adc2

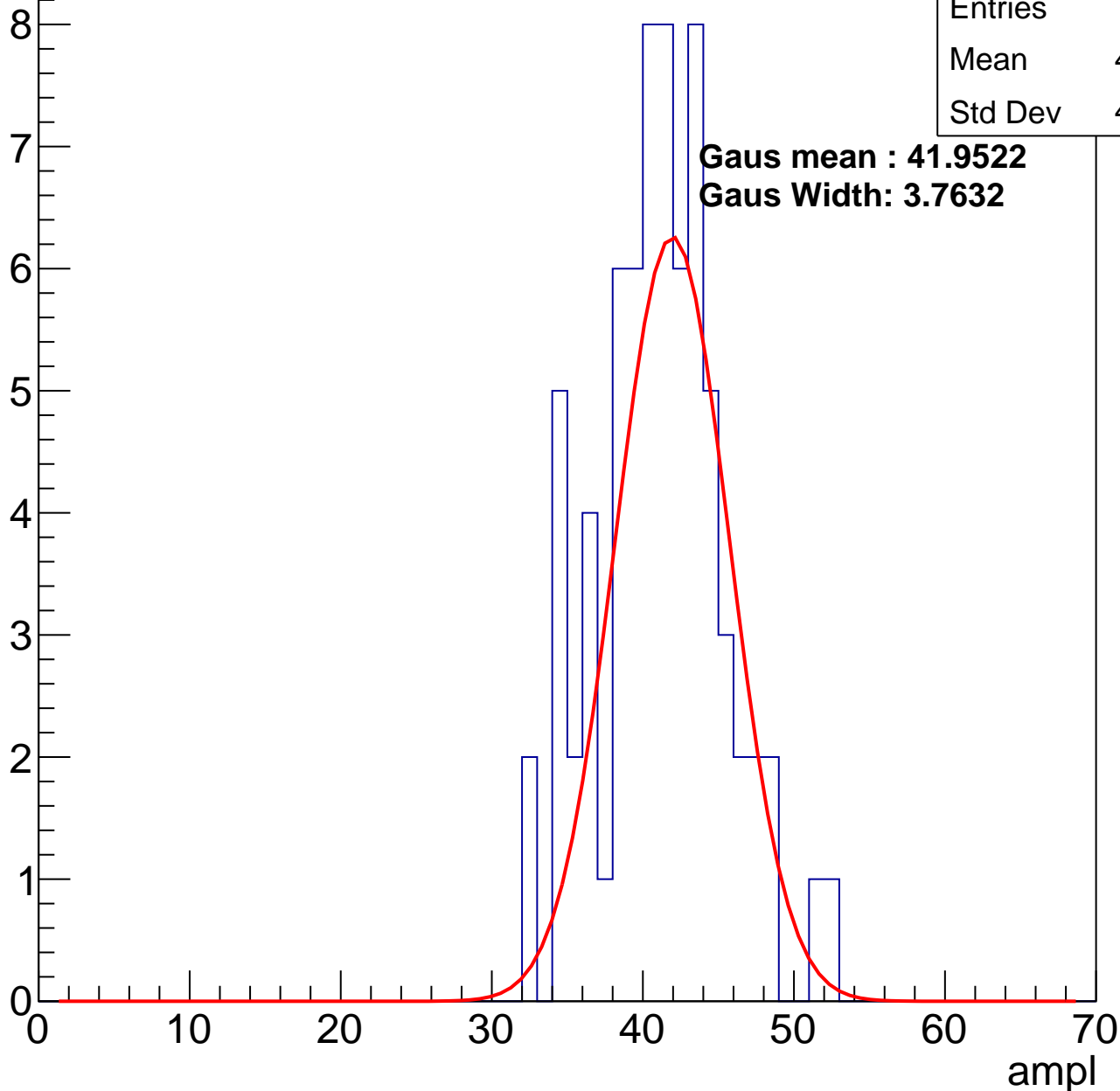
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	40.71
Std Dev	4.161

Gaus mean : 41.9522

Gaus Width: 3.7632

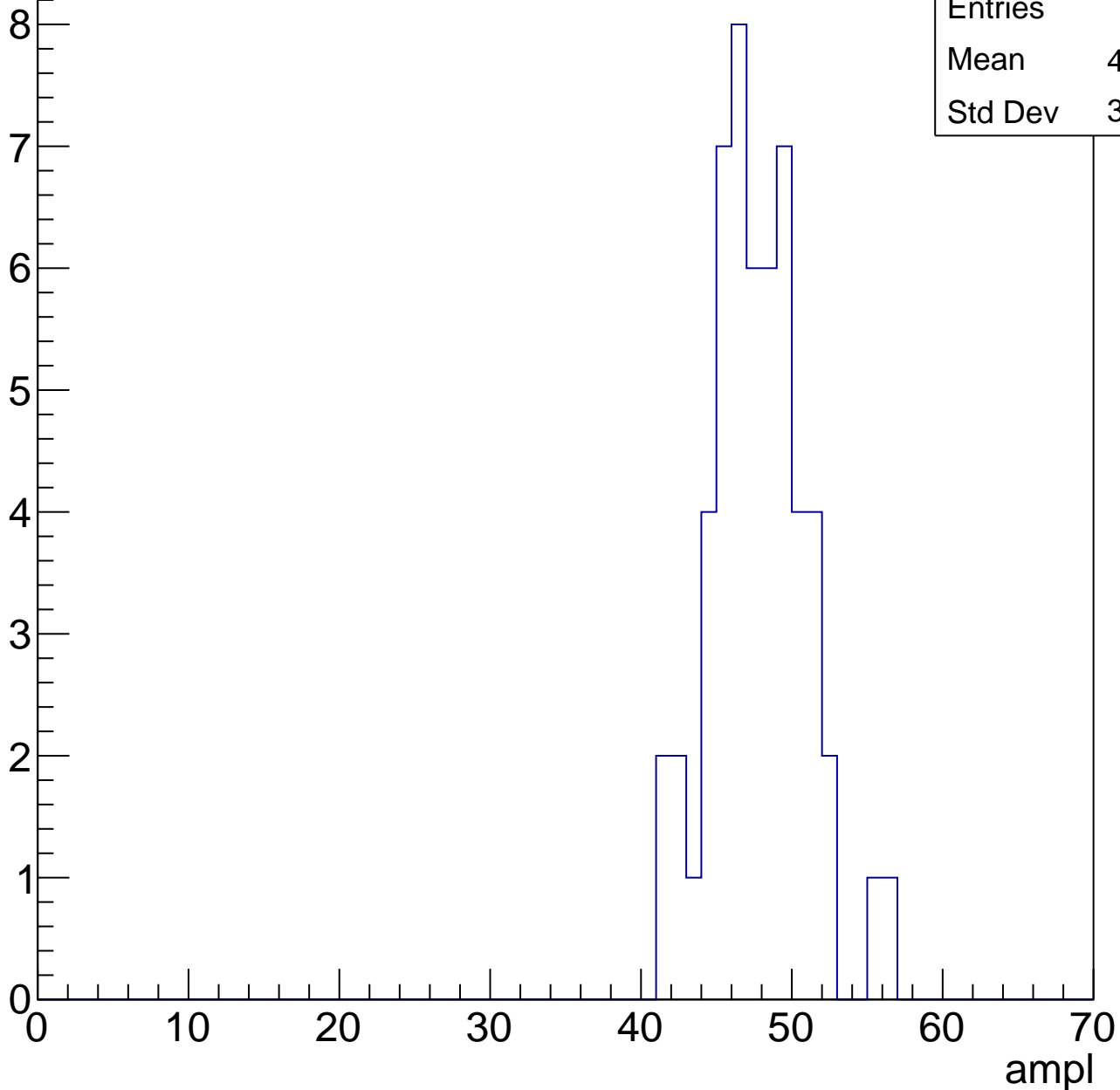


B1L102S, U17-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	47.27
Std Dev	3.119

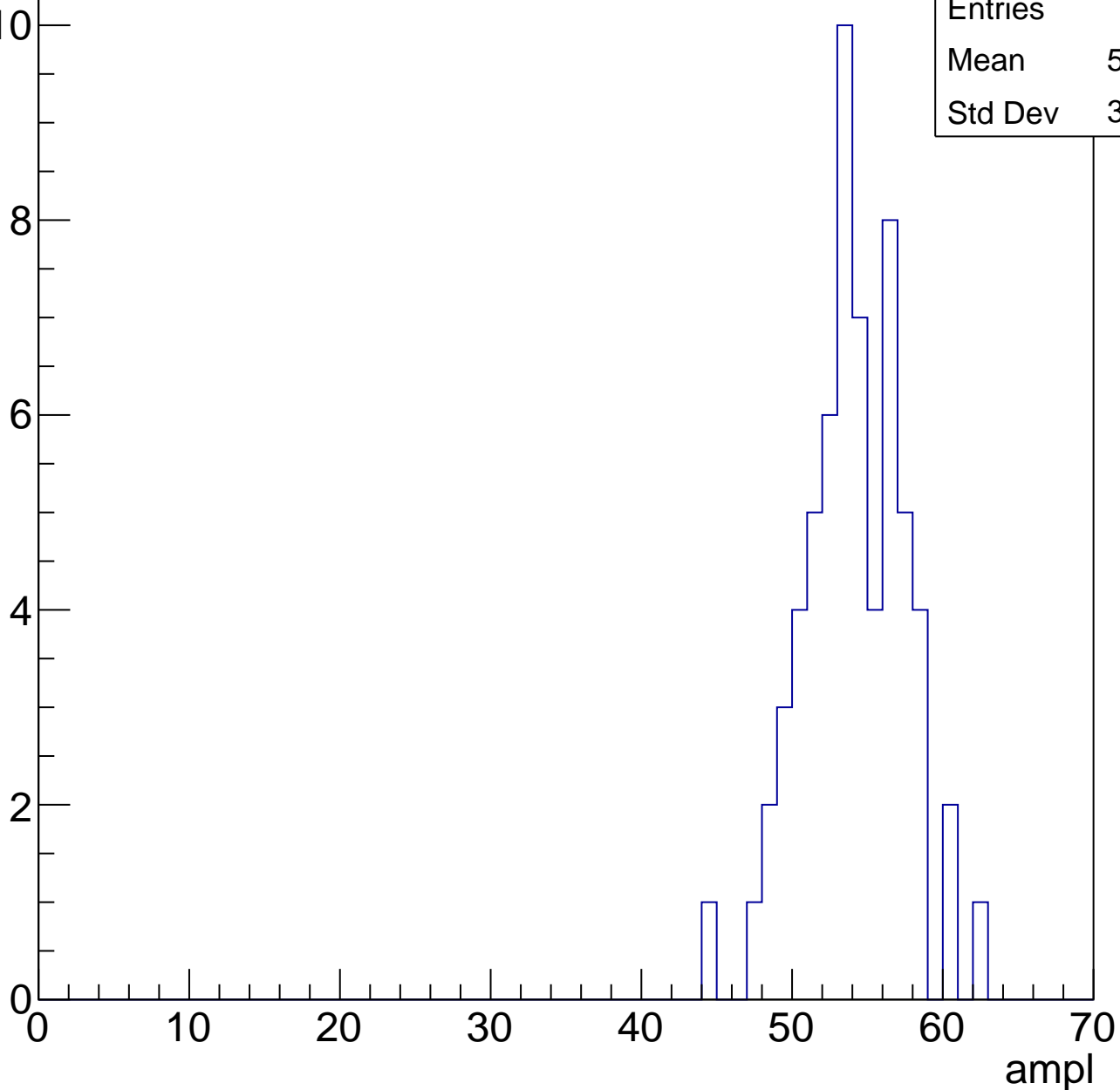


B1L102S, U17-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.59
Std Dev	3.355

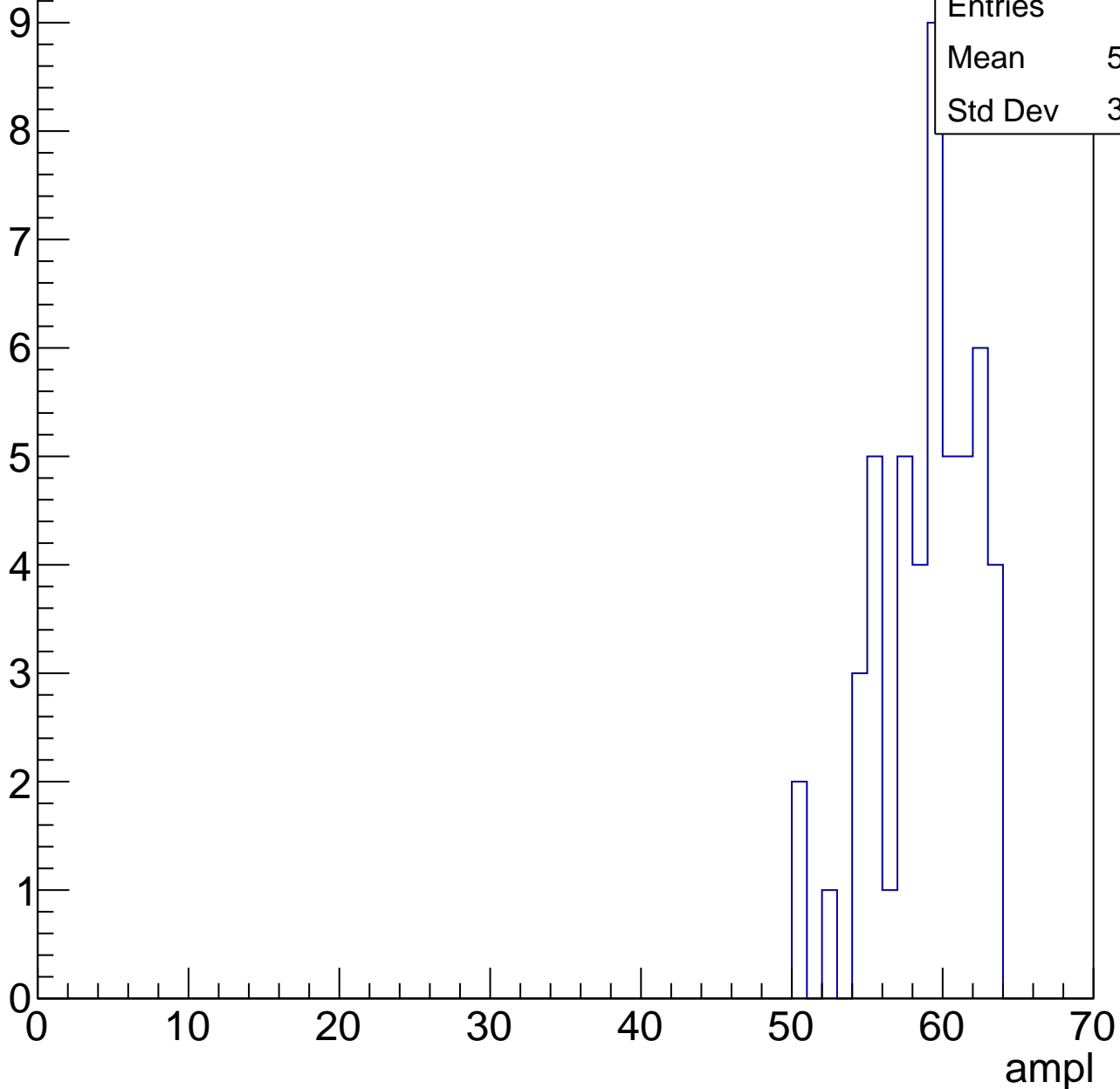


B1L102S, U17-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.44
Std Dev	3.244

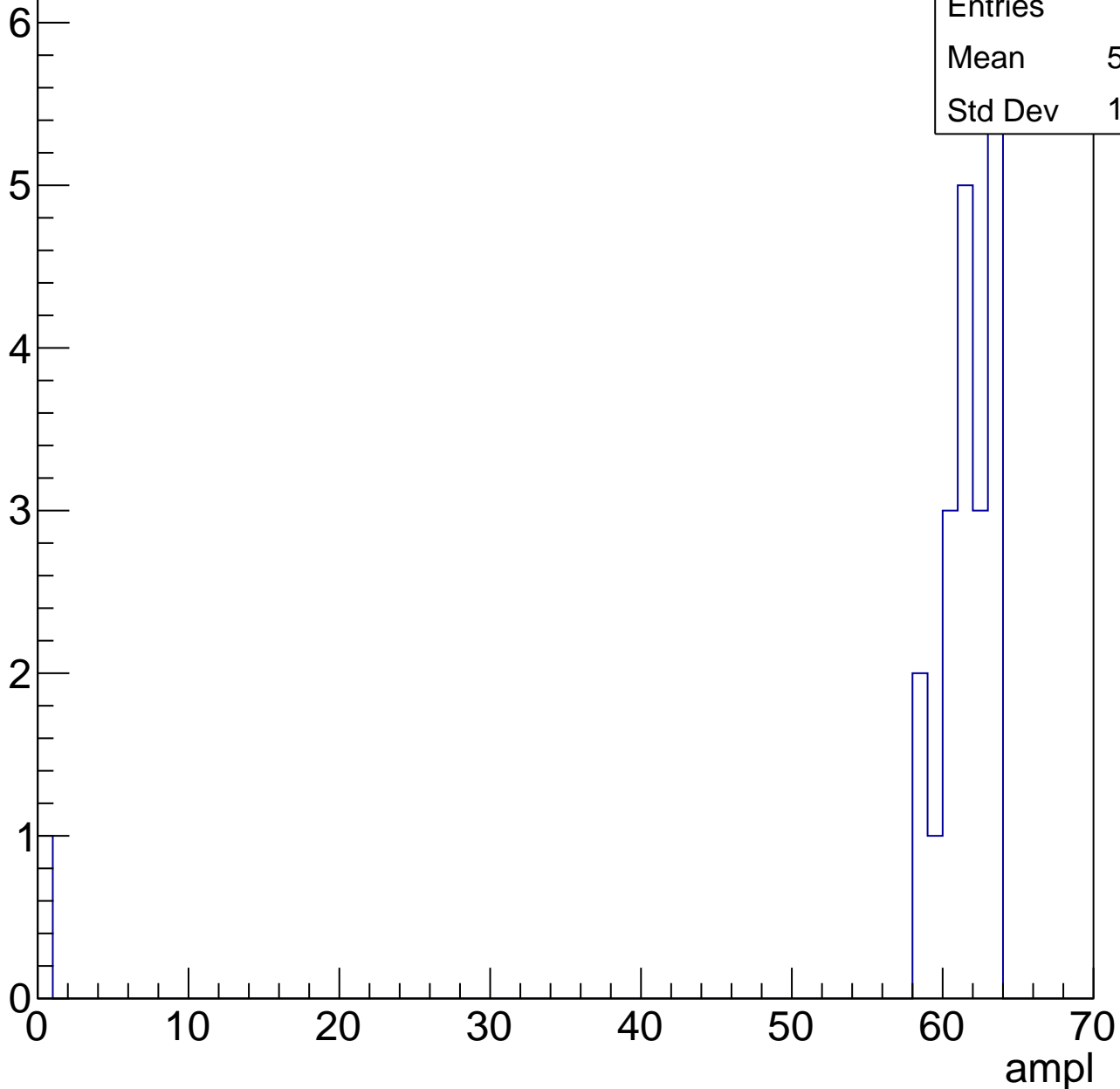


B1L102S, U17-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	58.29
Std Dev	13.13



B1L102S, U17-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch103, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	28.2
Std Dev	5.23

Gaus mean : 29.1296

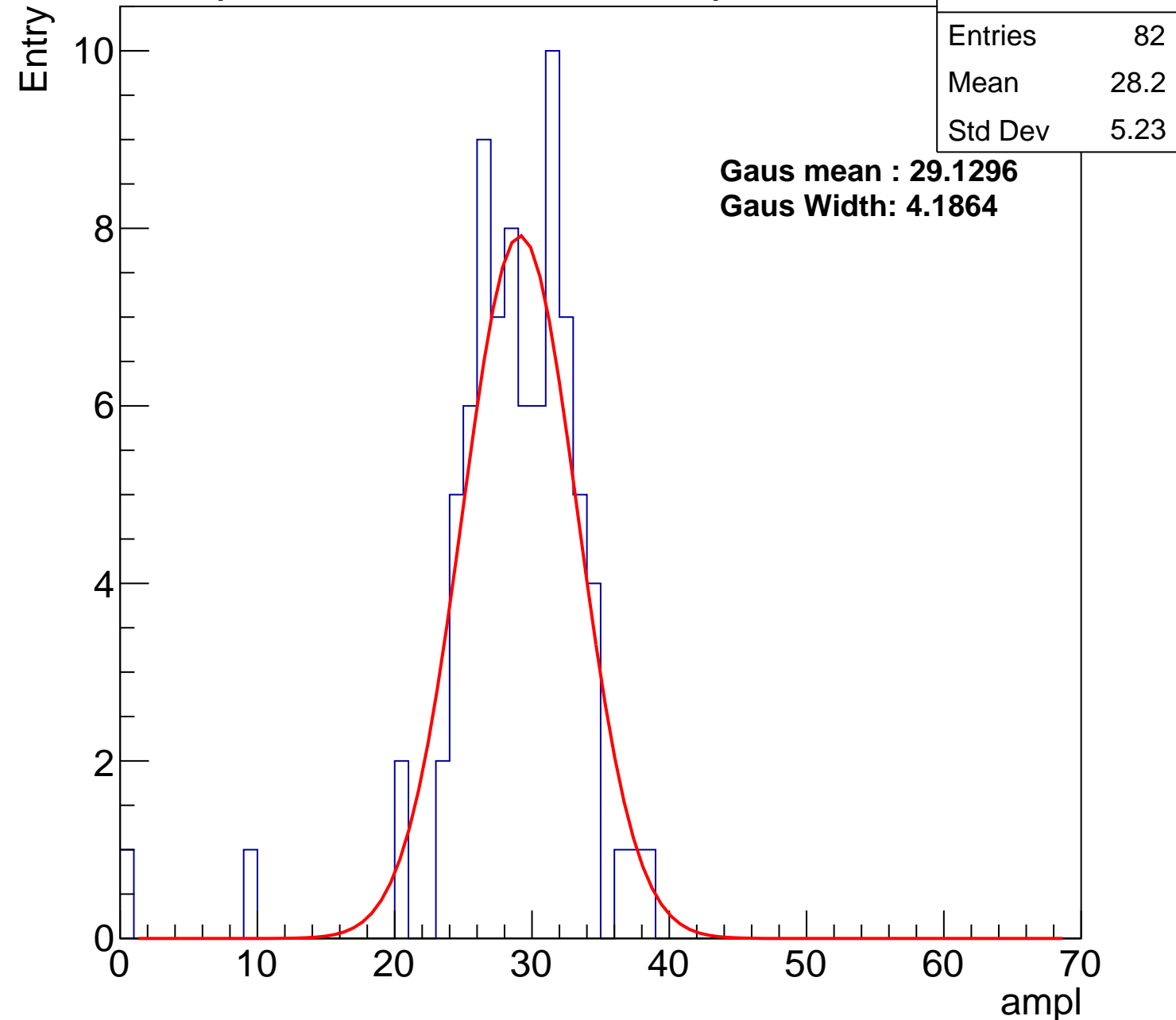
Gaus Width: 4.1864

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch103, adc1

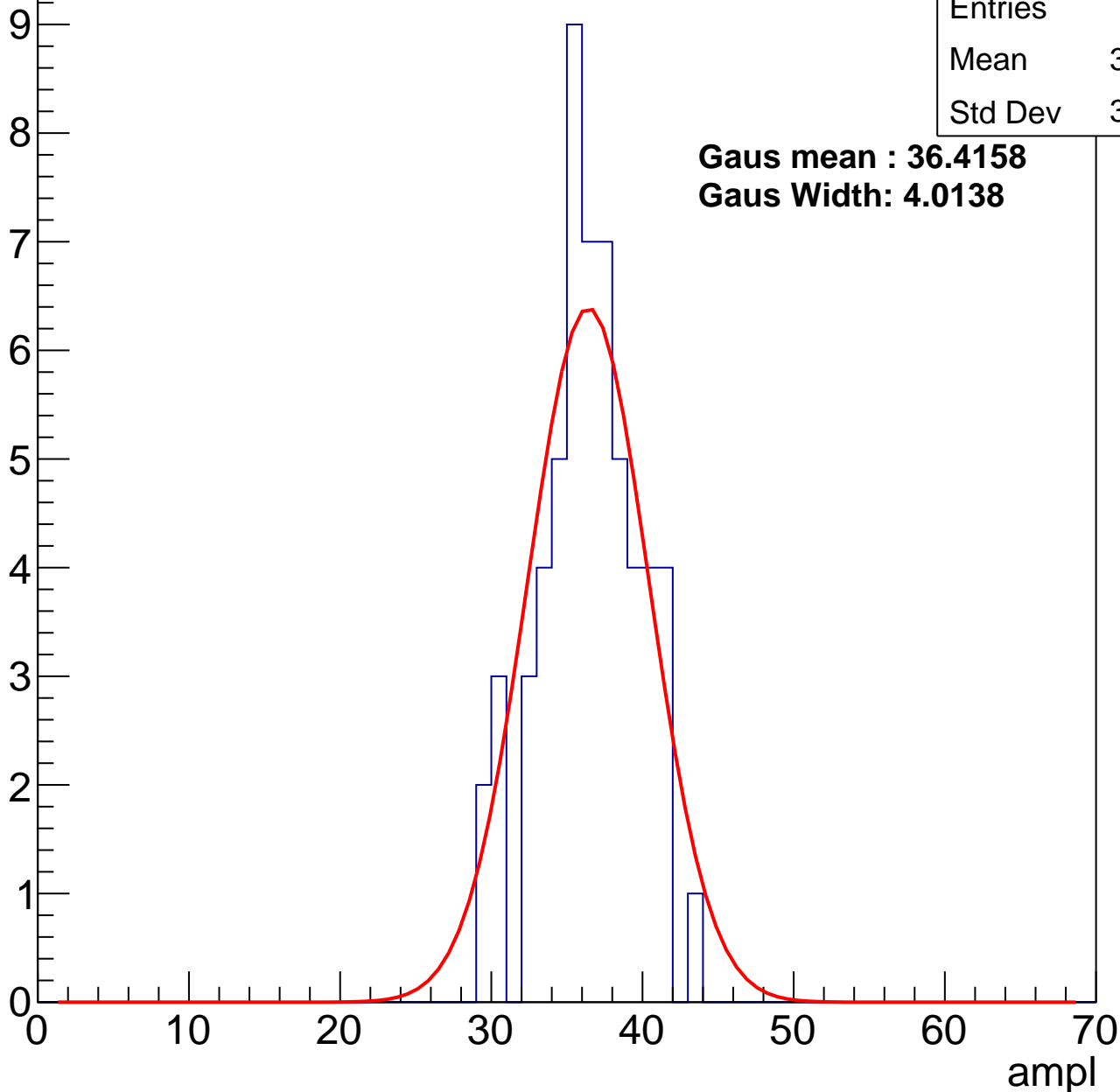
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.95
Std Dev	3.202

Gaus mean : 36.4158

Gaus Width: 4.0138



B1L102S, U17-ch103, adc2

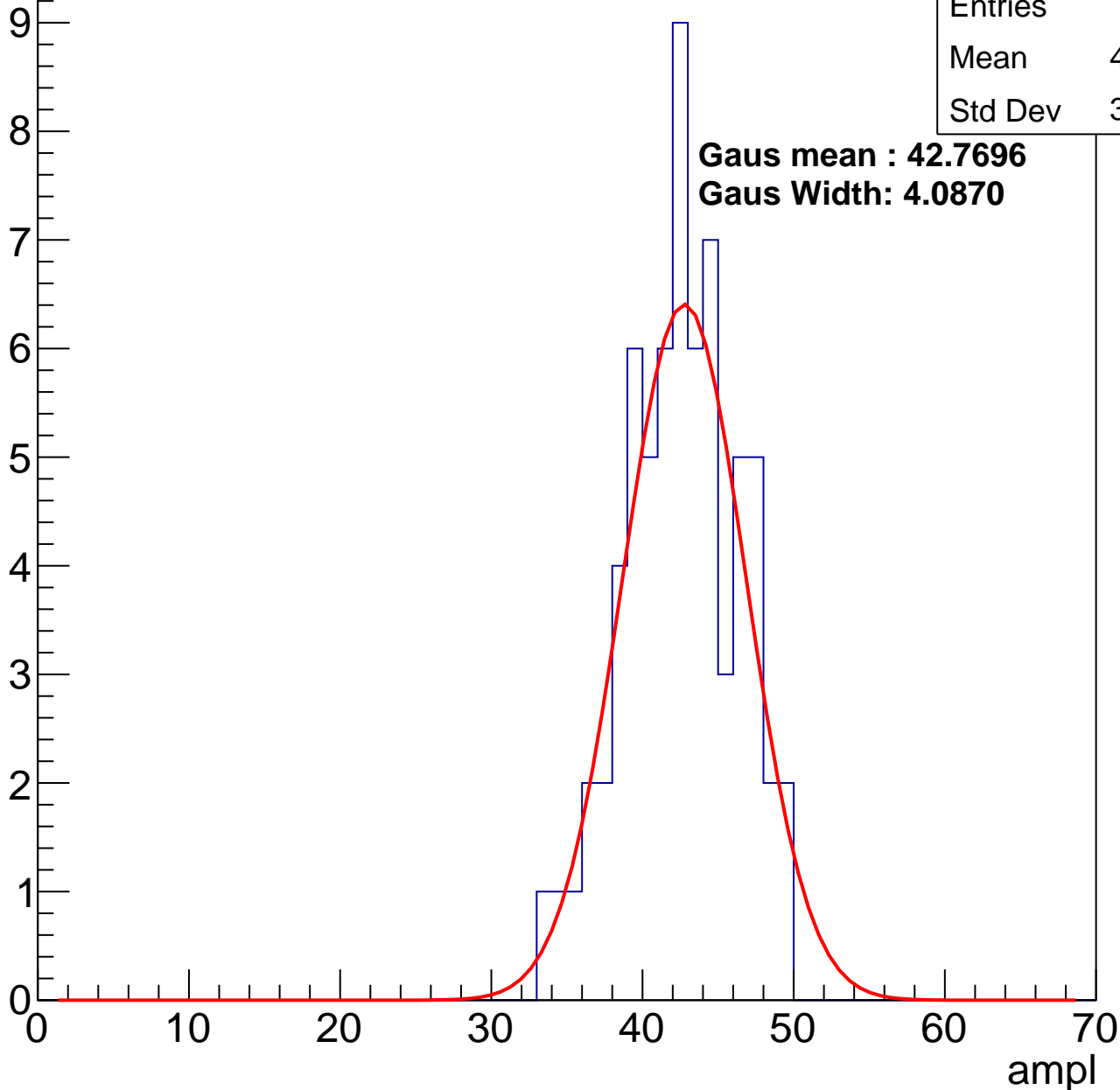
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.06
Std Dev	3.656

Gaus mean : 42.7696

Gaus Width: 4.0870

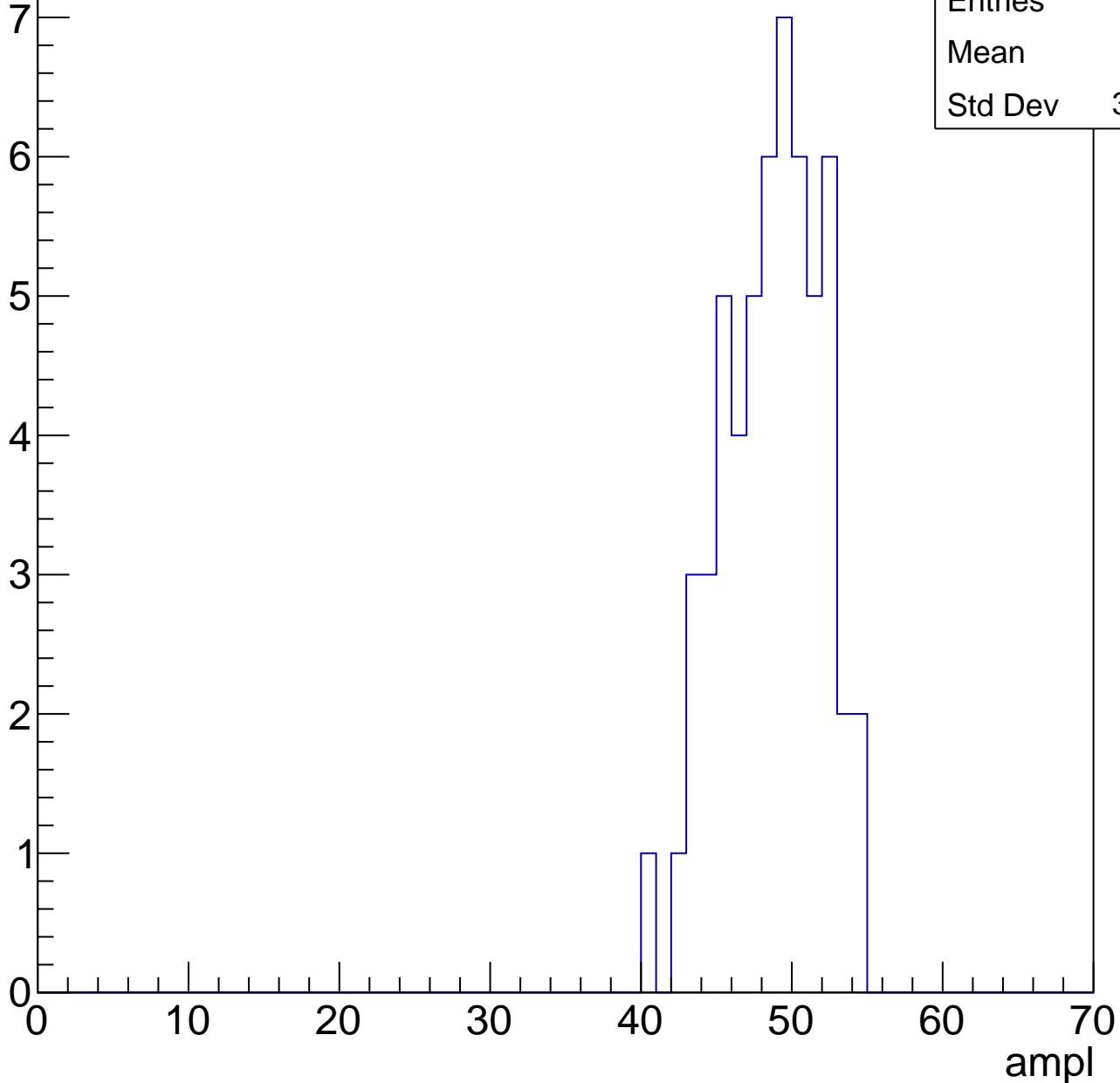


B1L102S, U17-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.2
Std Dev	3.221

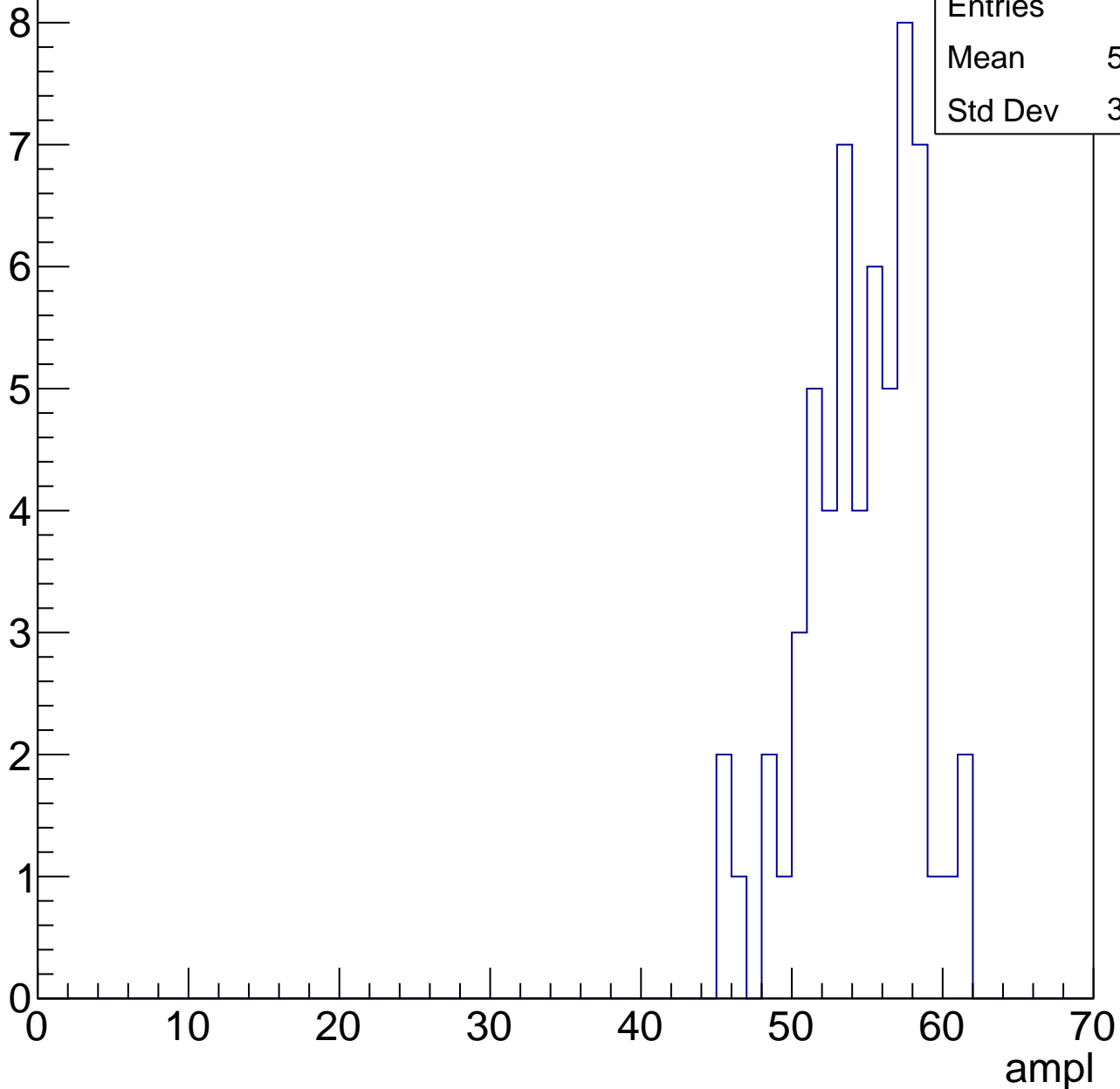


B1L102S, U17-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.14
Std Dev	3.698

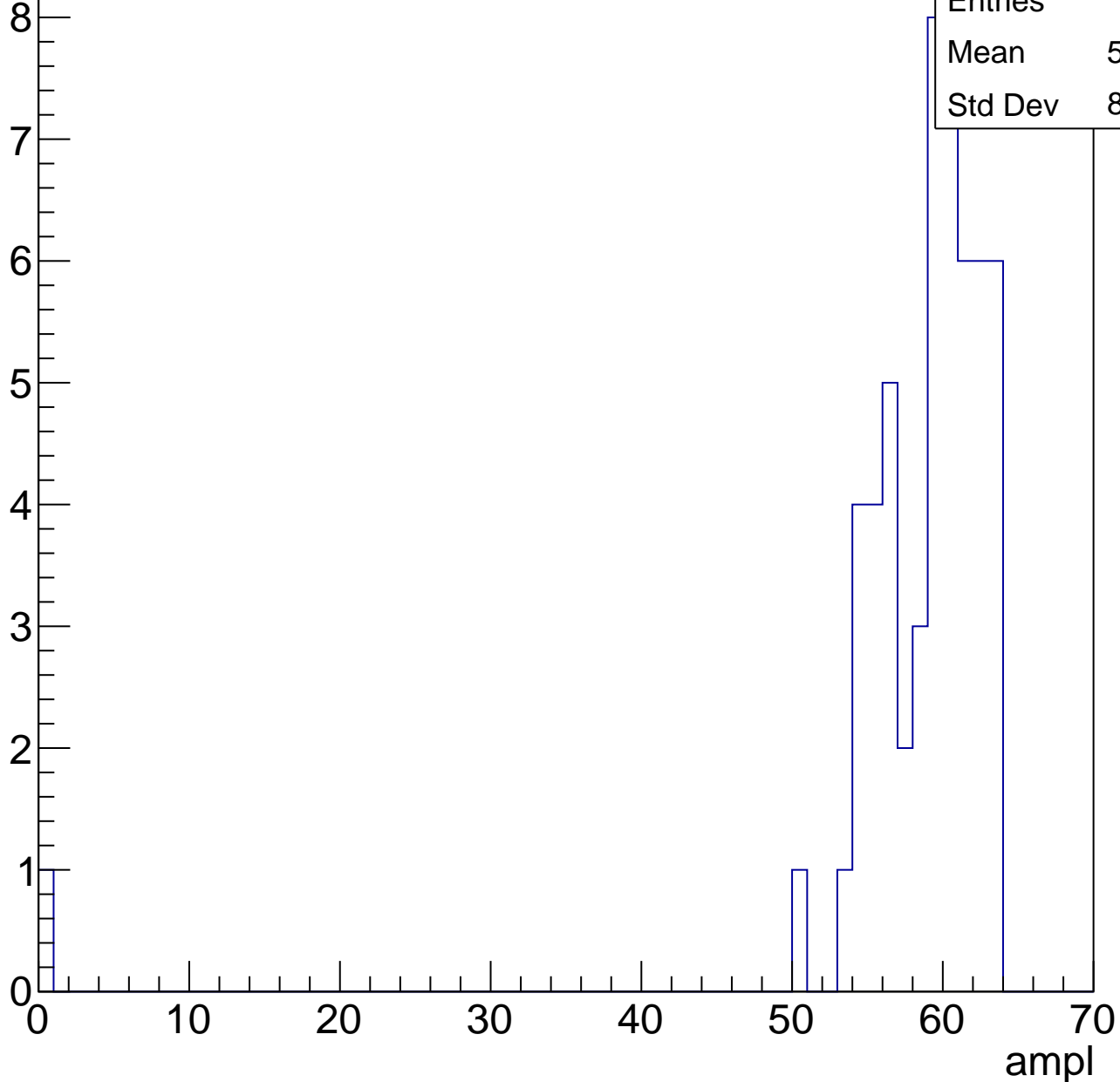


B1L102S, U17-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

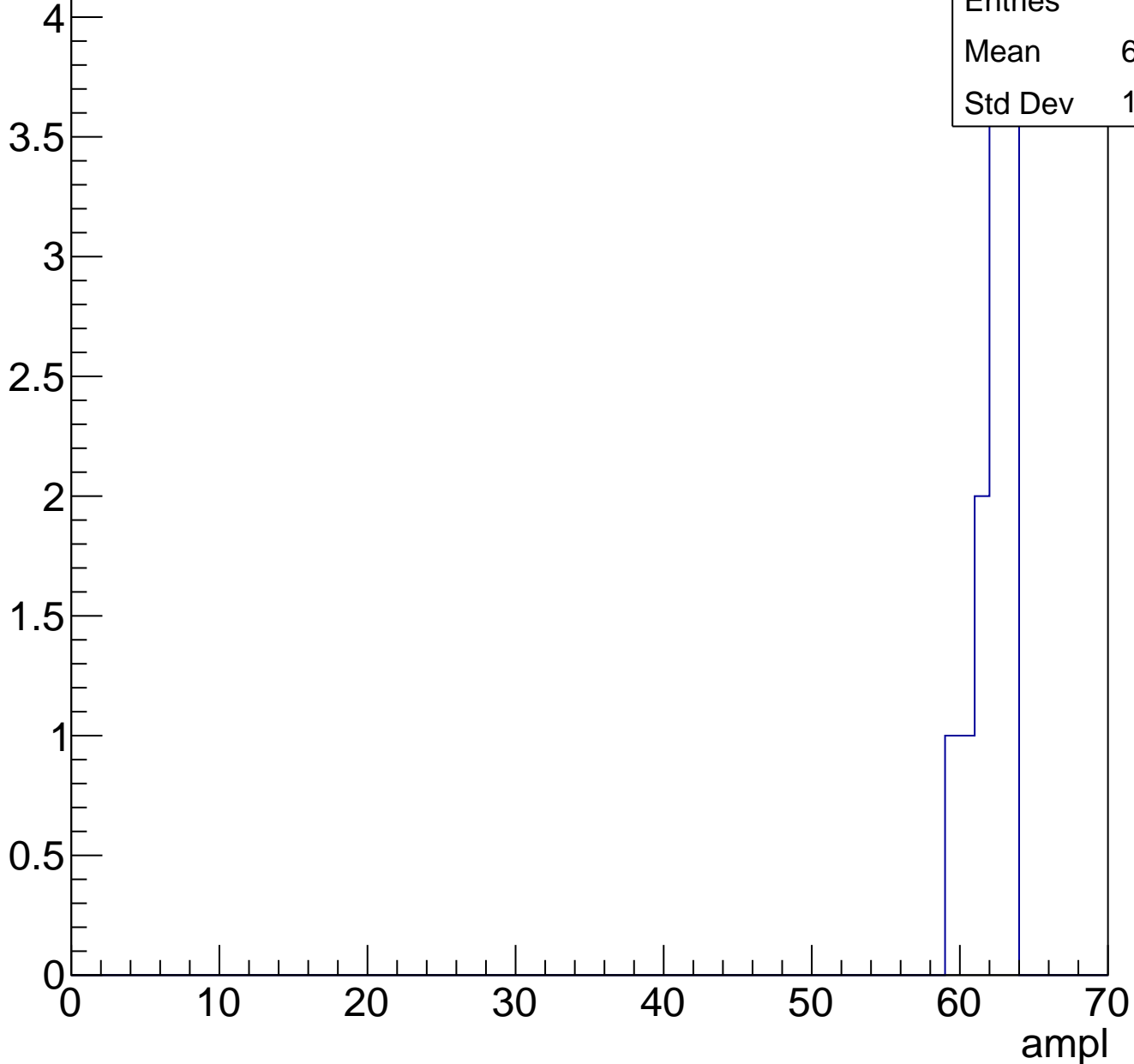
Entries	55
Mean	57.73
Std Dev	8.433



B1L102S, U17-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	12
Mean	61.75
Std Dev	1.233

B1L102S, U17-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L102S, U17-ch104, adc0

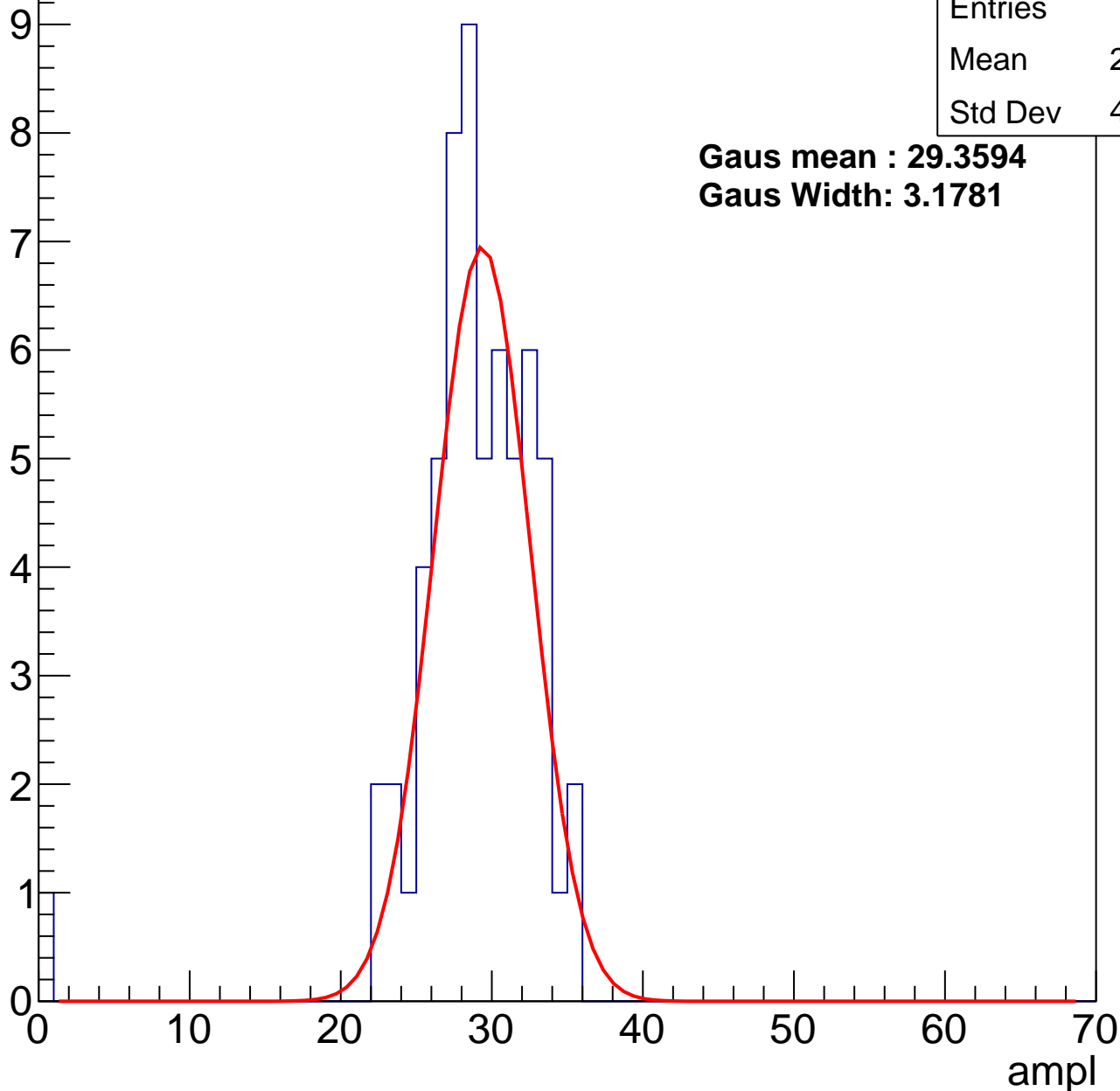
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.27
Std Dev	4.769

Gaus mean : 29.3594

Gaus Width: 3.1781



B1L102S, U17-ch104, adc1

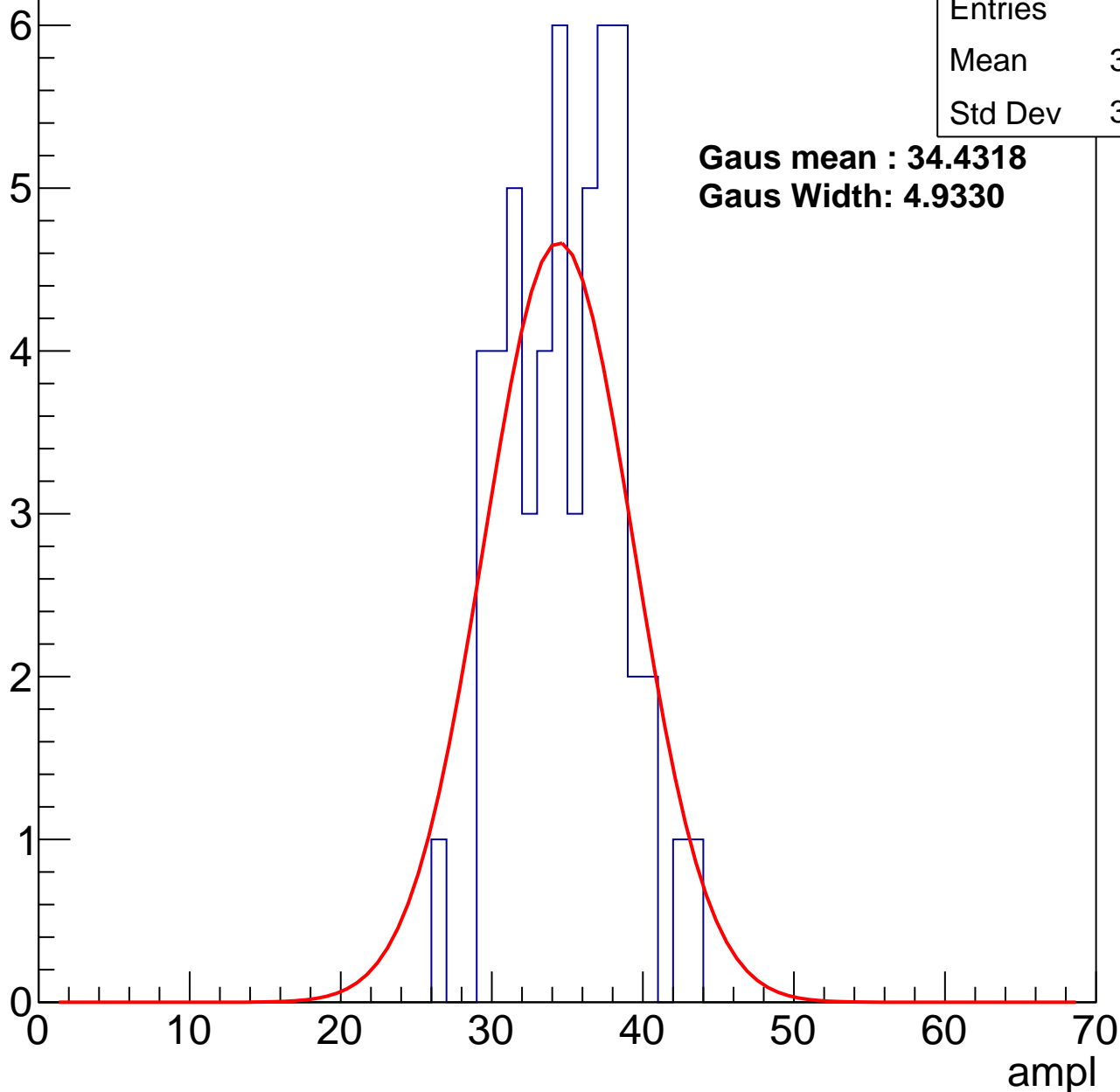
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.47
Std Dev	3.679

Gaus mean : 34.4318

Gaus Width: 4.9330



B1L102S, U17-ch104, adc2

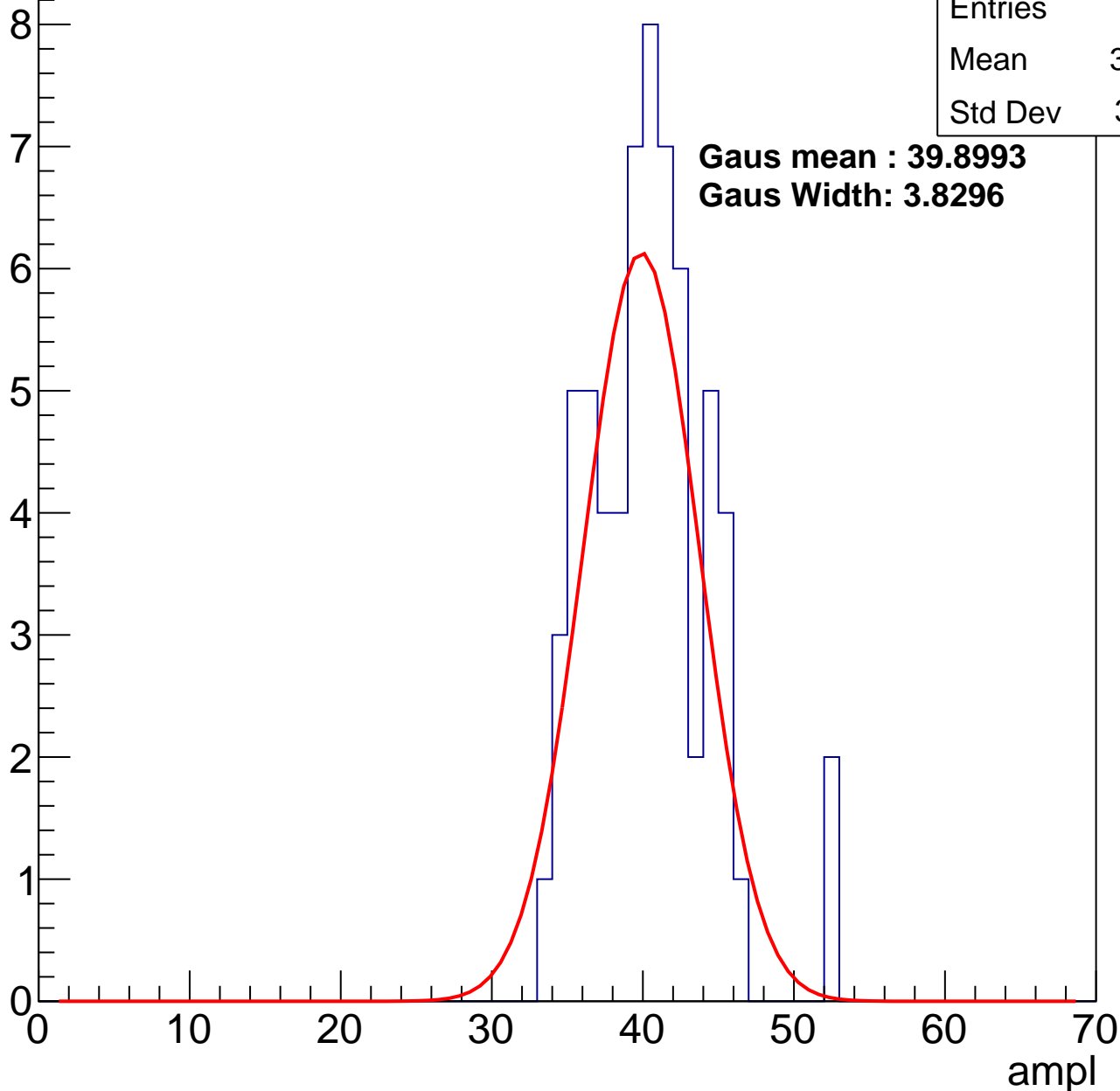
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	39.97
Std Dev	3.901

Gaus mean : 39.8993

Gaus Width: 3.8296

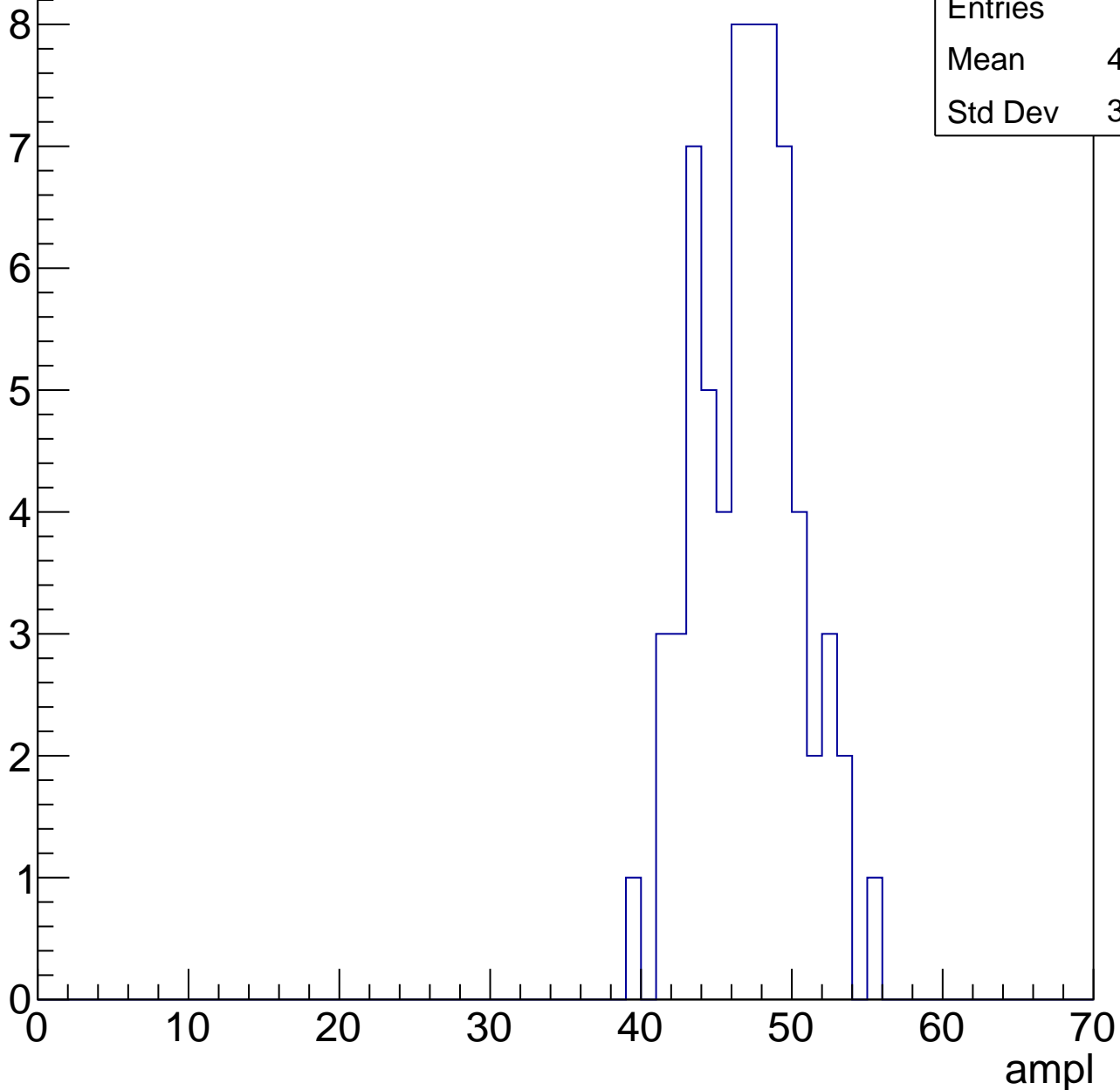


B1L102S, U17-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.65
Std Dev	3.337



B1L102S, U17-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

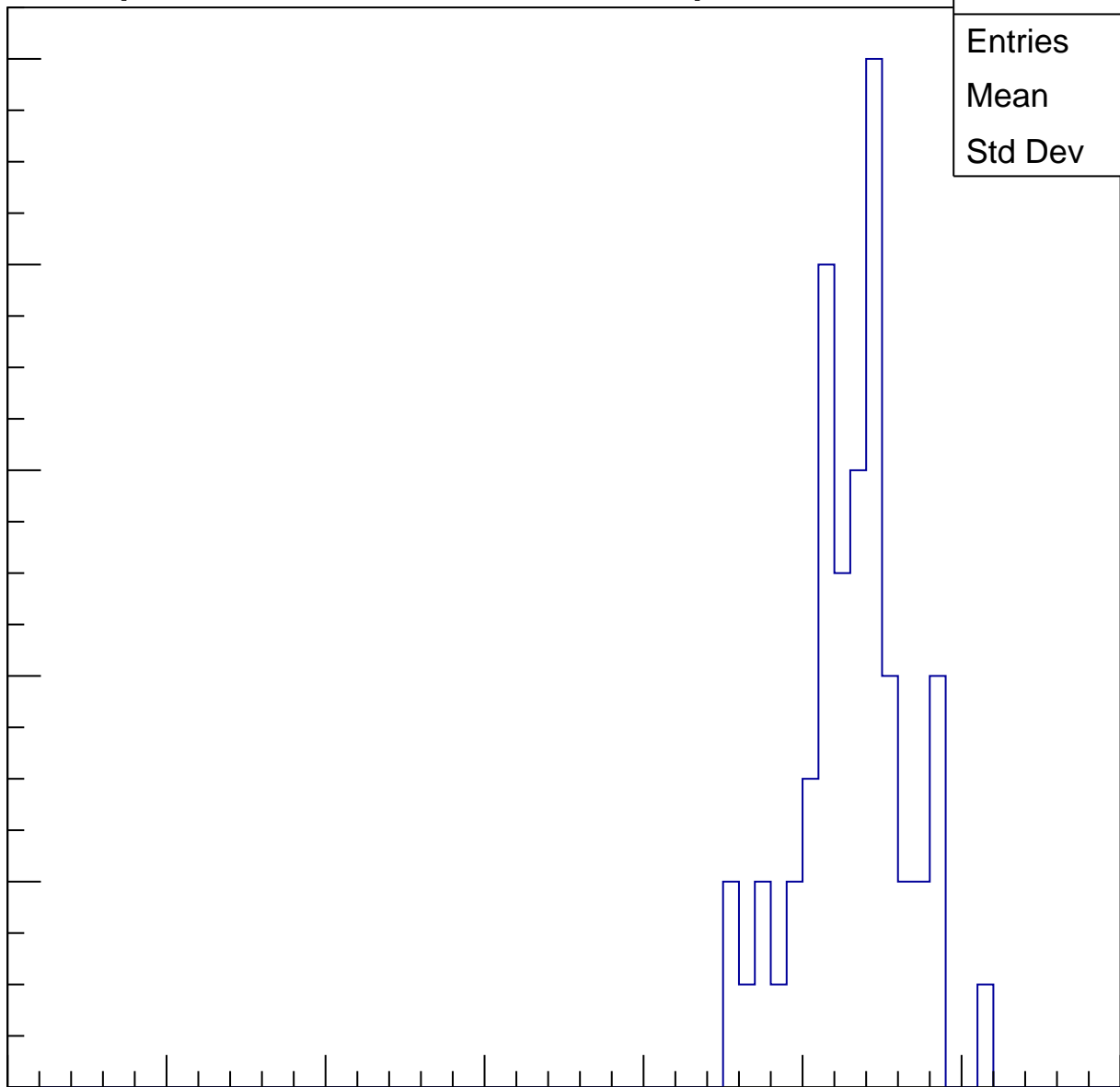
Entries	53
Mean	52.66
Std Dev	3.376

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

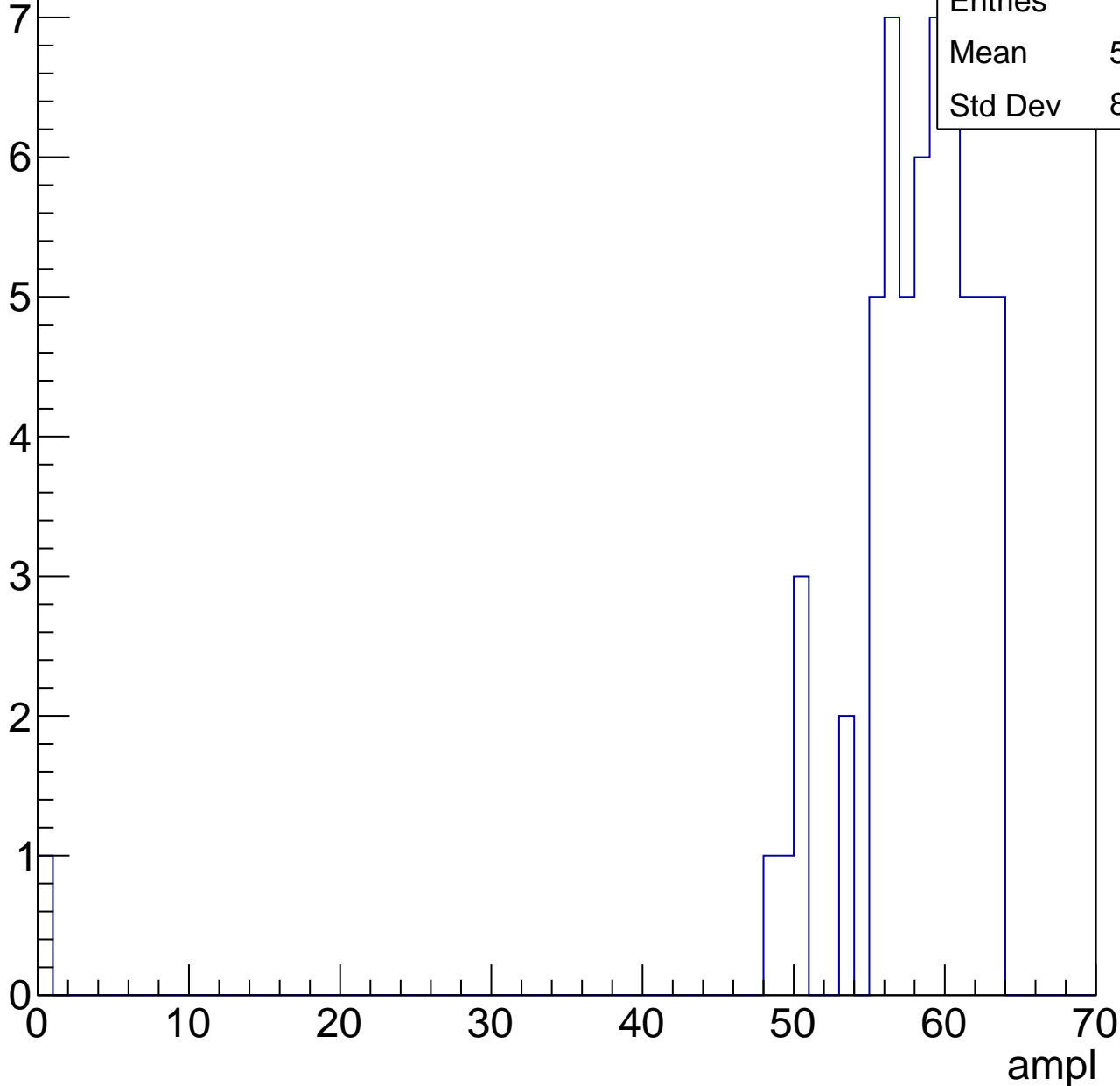


B1L102S, U17-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

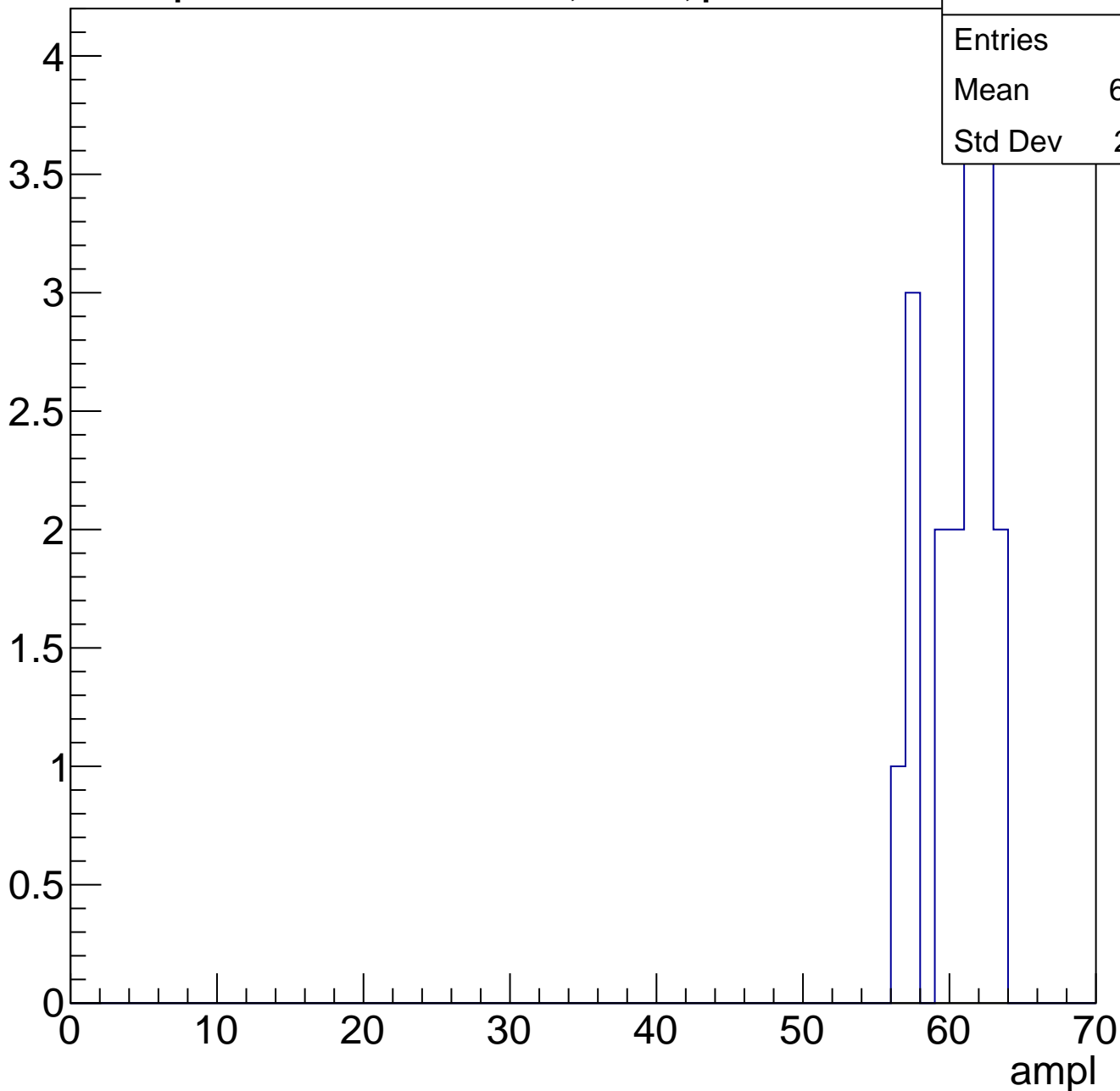
Entries	60
Mean	56.93
Std Dev	8.248



B1L102S, U17-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

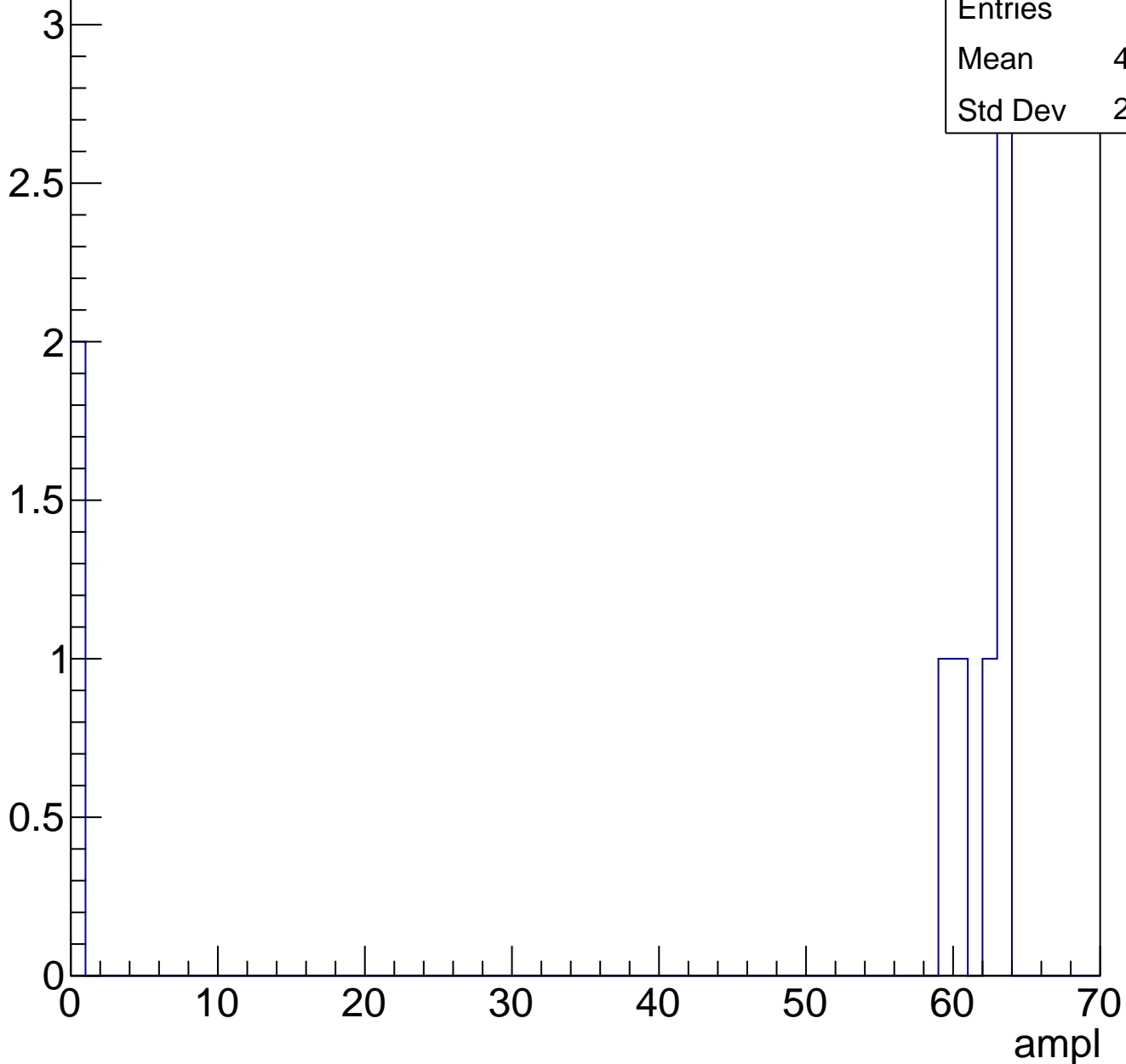
Entry



B1L102S, U17-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch105, adc0

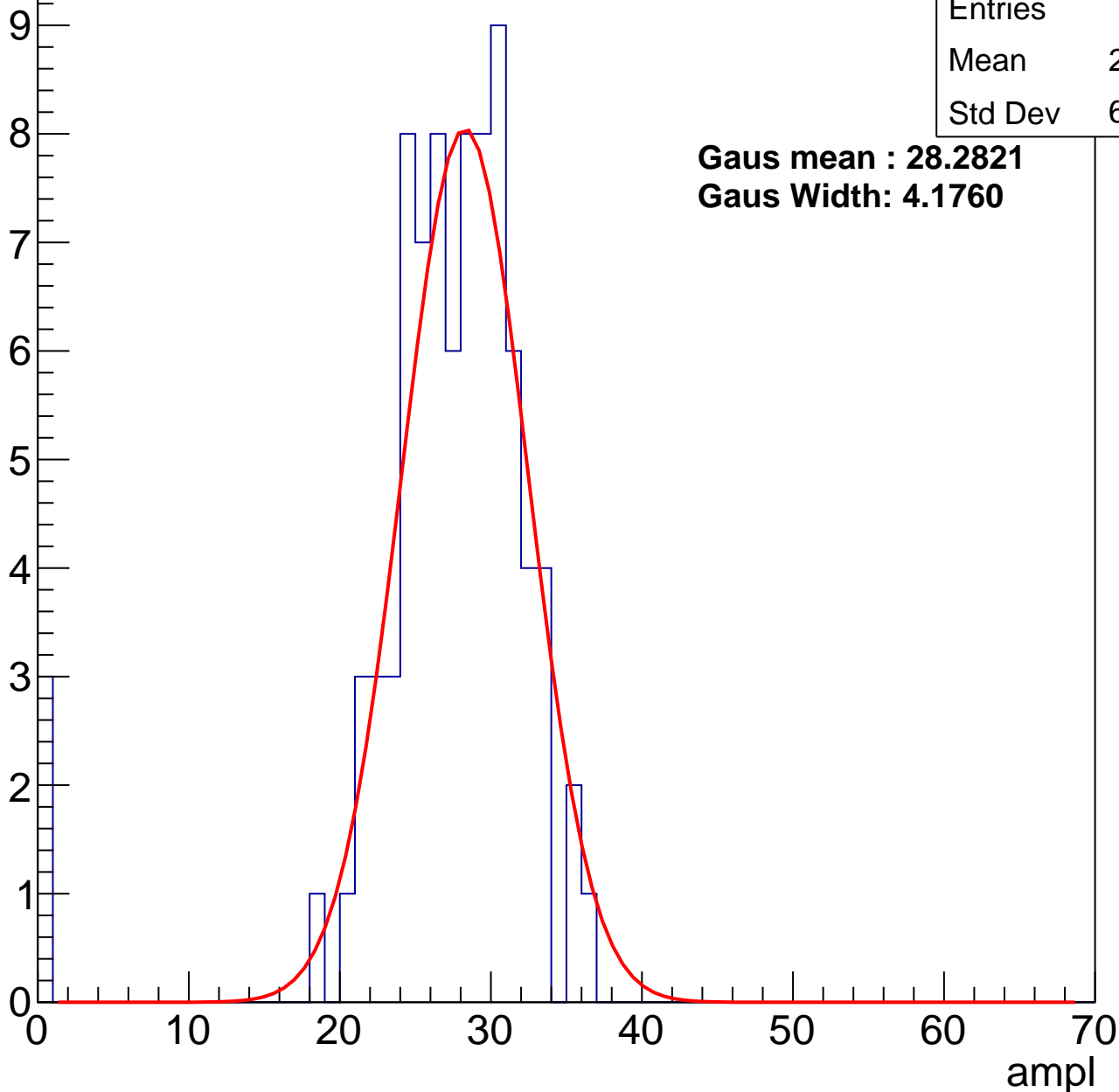
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	26.48
Std Dev	6.234

Gaus mean : 28.2821

Gaus Width: 4.1760



B1L102S, U17-ch105, adc1

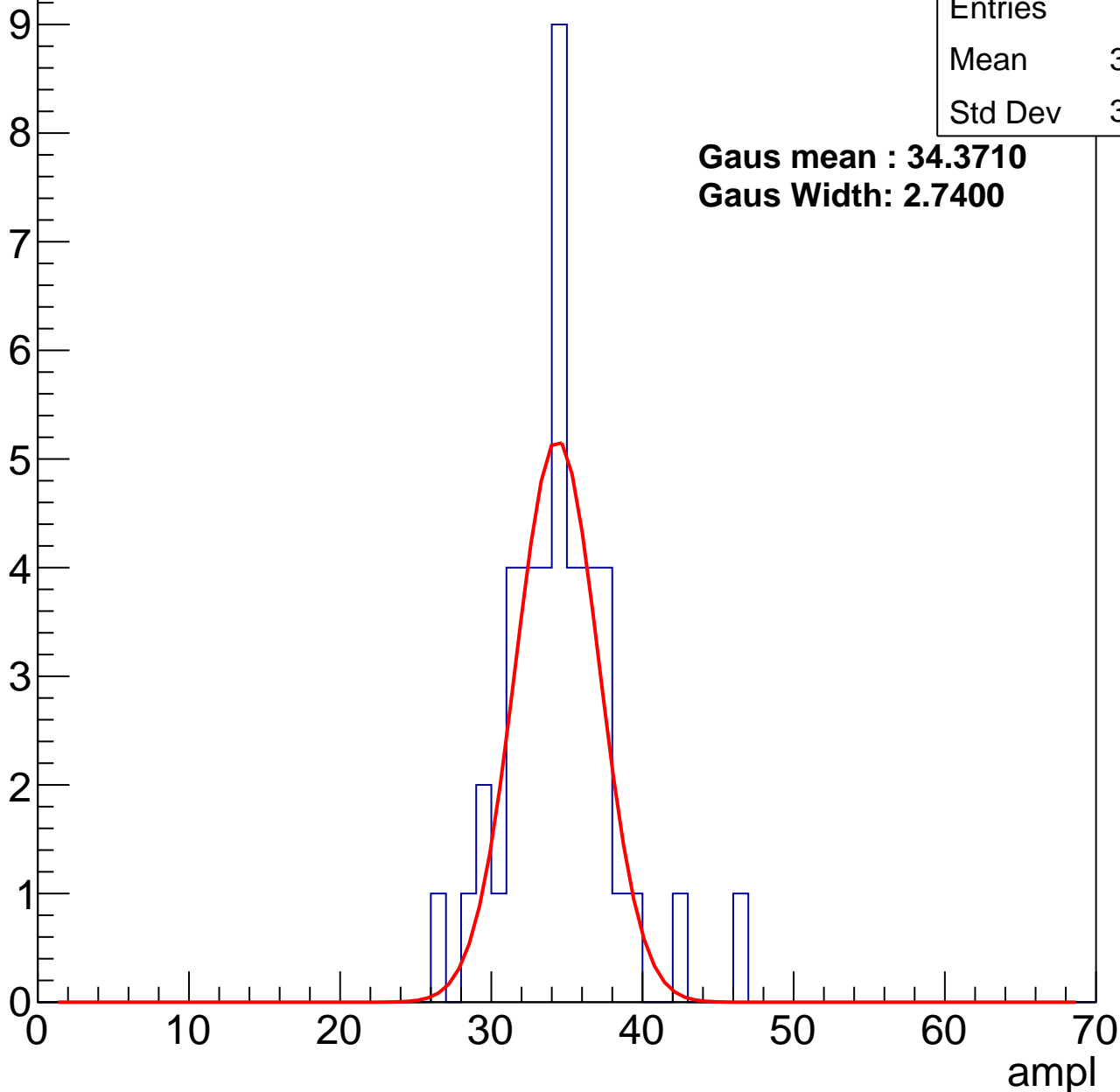
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	34.02
Std Dev	3.542

Gaus mean : 34.3710

Gaus Width: 2.7400



B1L102S, U17-ch105, adc2

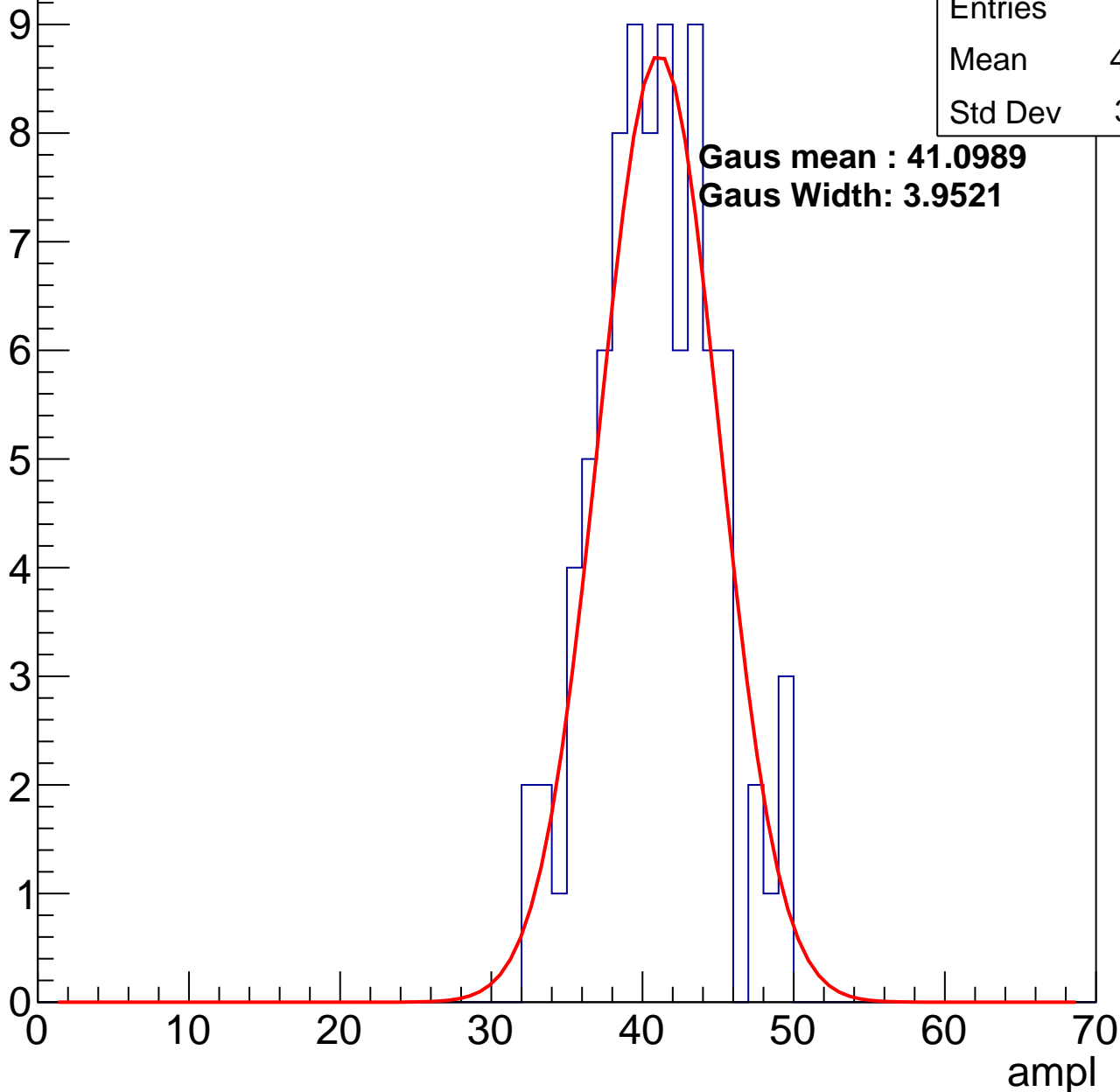
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	40.37
Std Dev	3.851

Gaus mean : 41.0989

Gaus Width: 3.9521

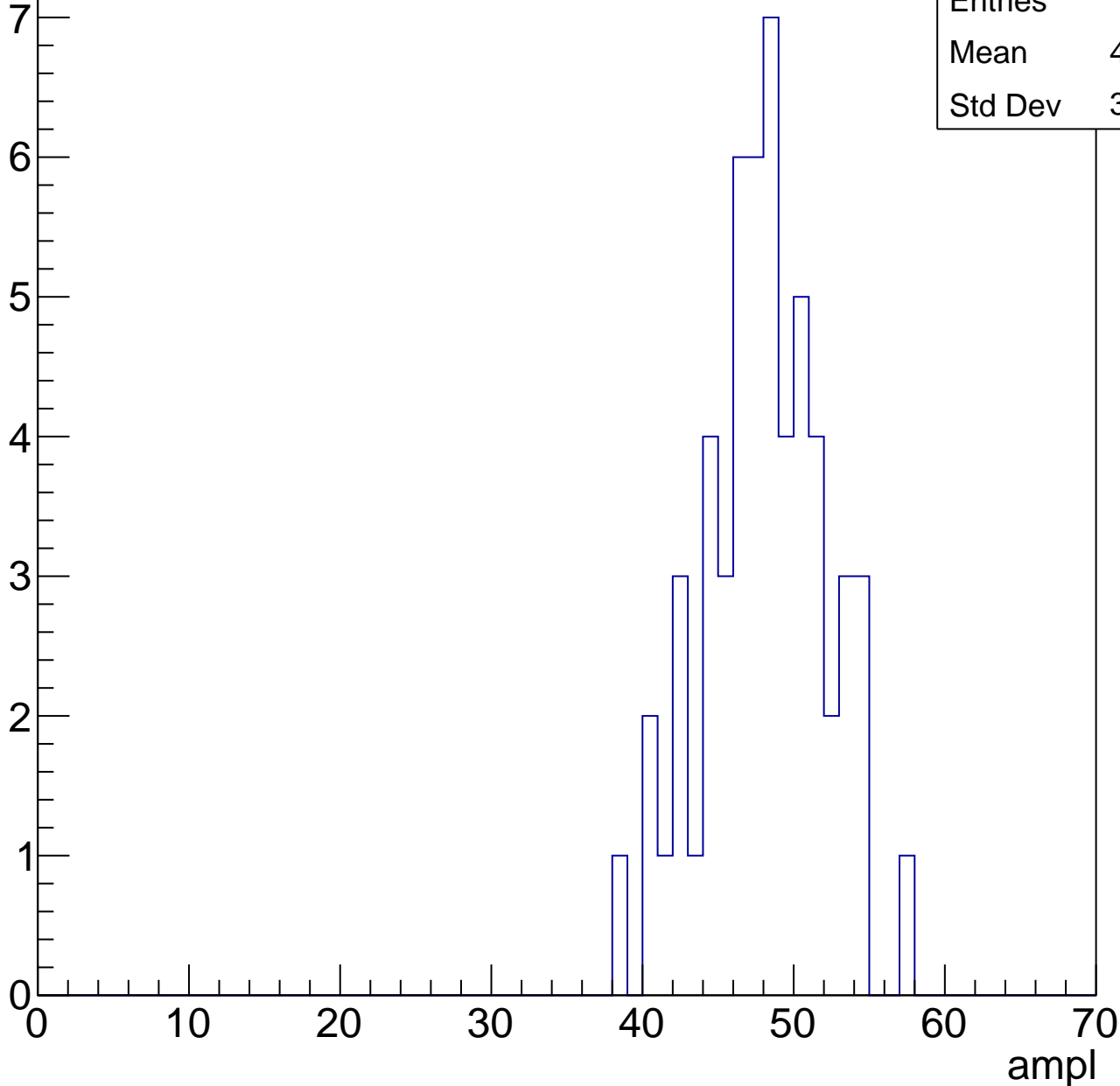


B1L102S, U17-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.59
Std Dev	3.959

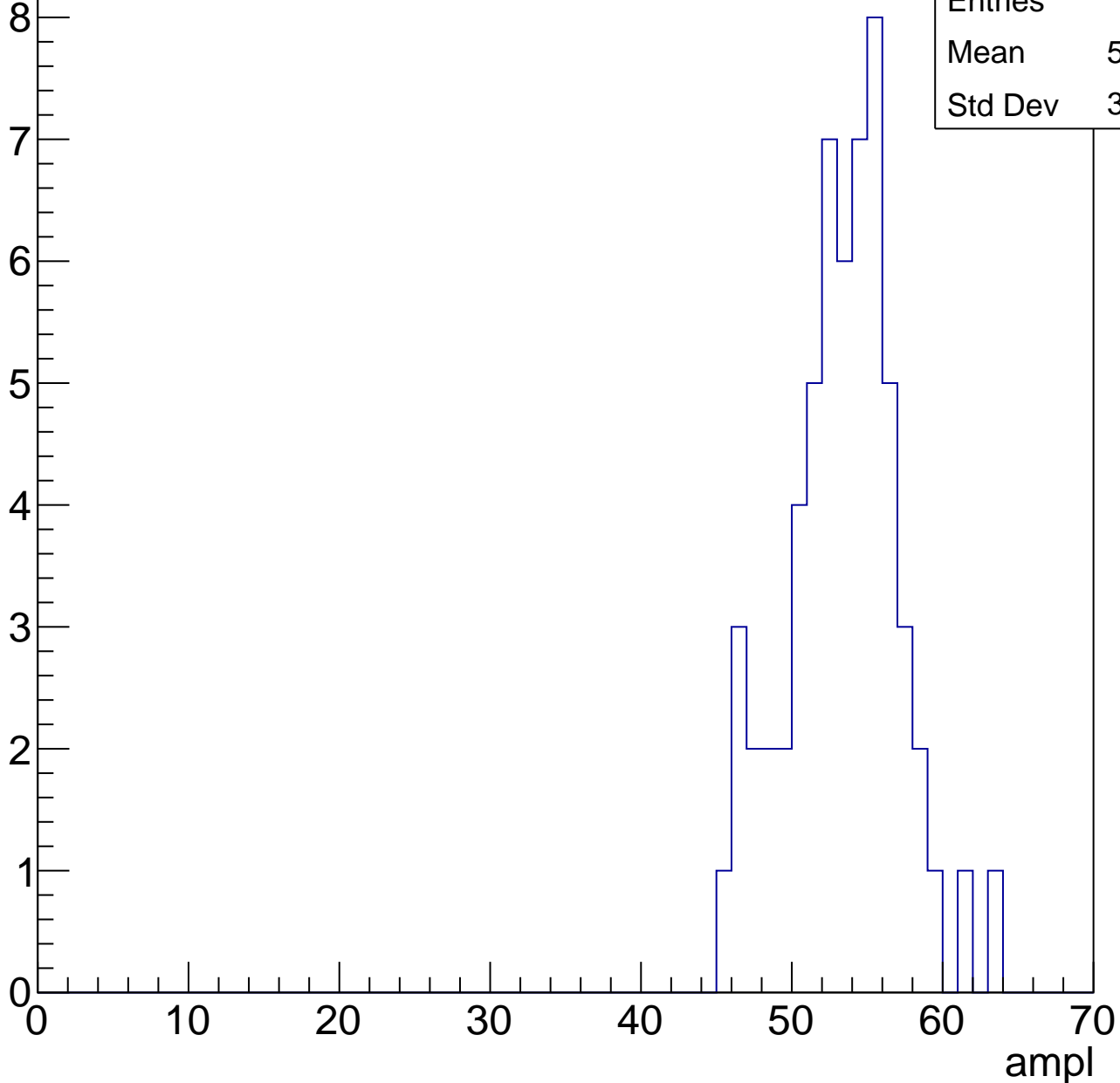


B1L102S, U17-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	52.93
Std Dev	3.674

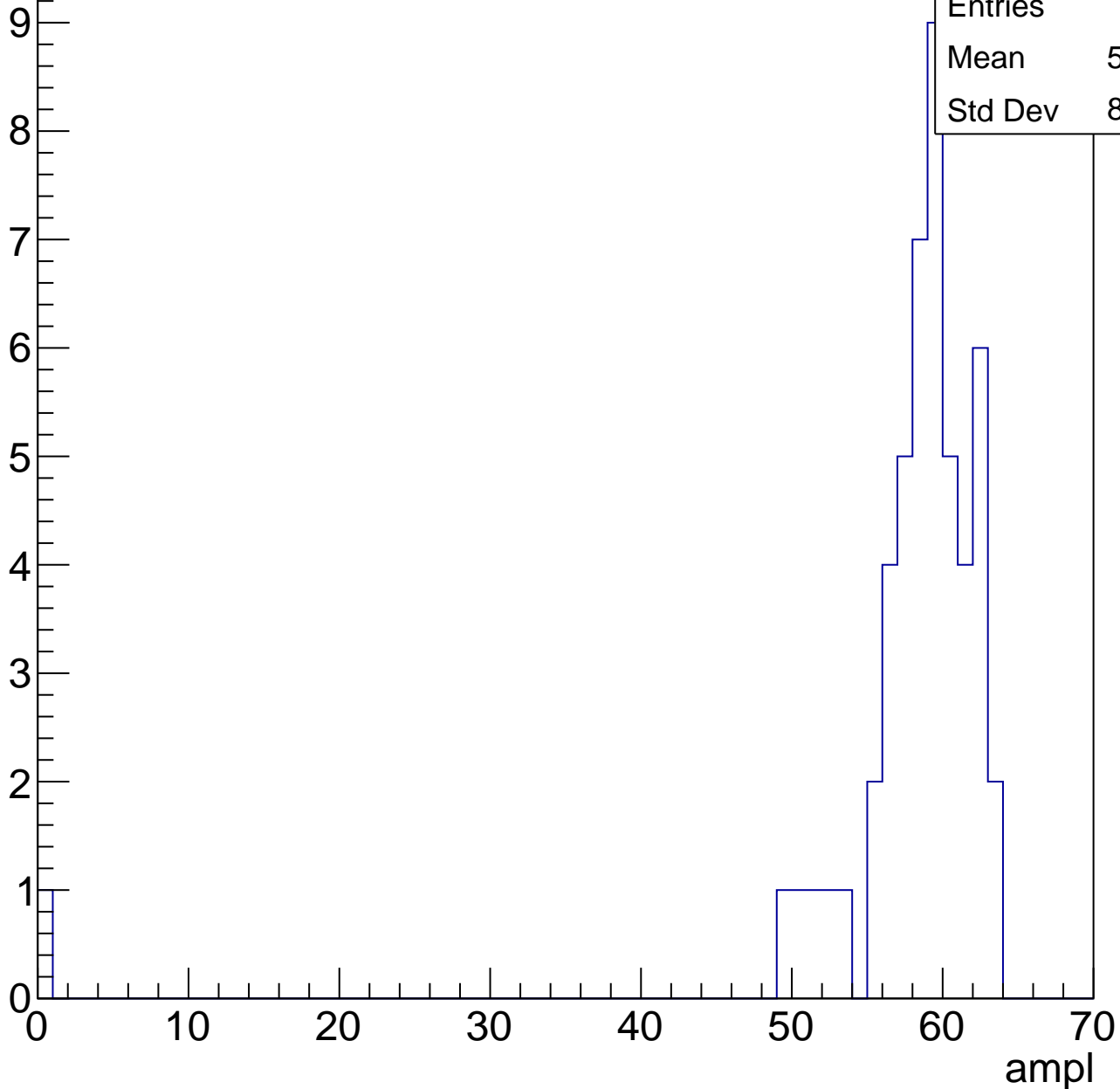


B1L102S, U17-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.06
Std Dev	8.746

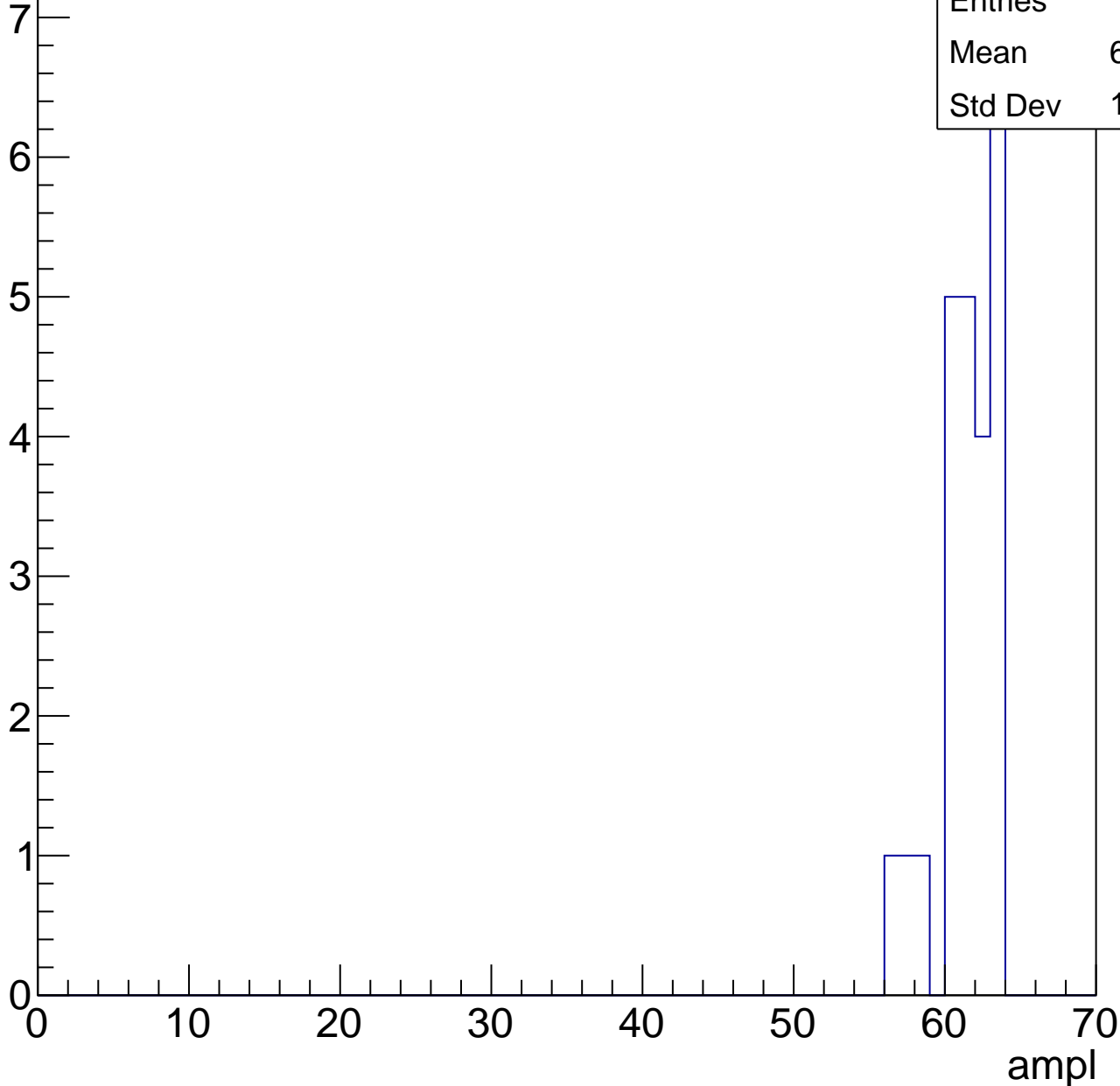


B1L102S, U17-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	24
Mean	61.04
Std Dev	1.903



B1L102S, U17-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U17-ch106, adc0

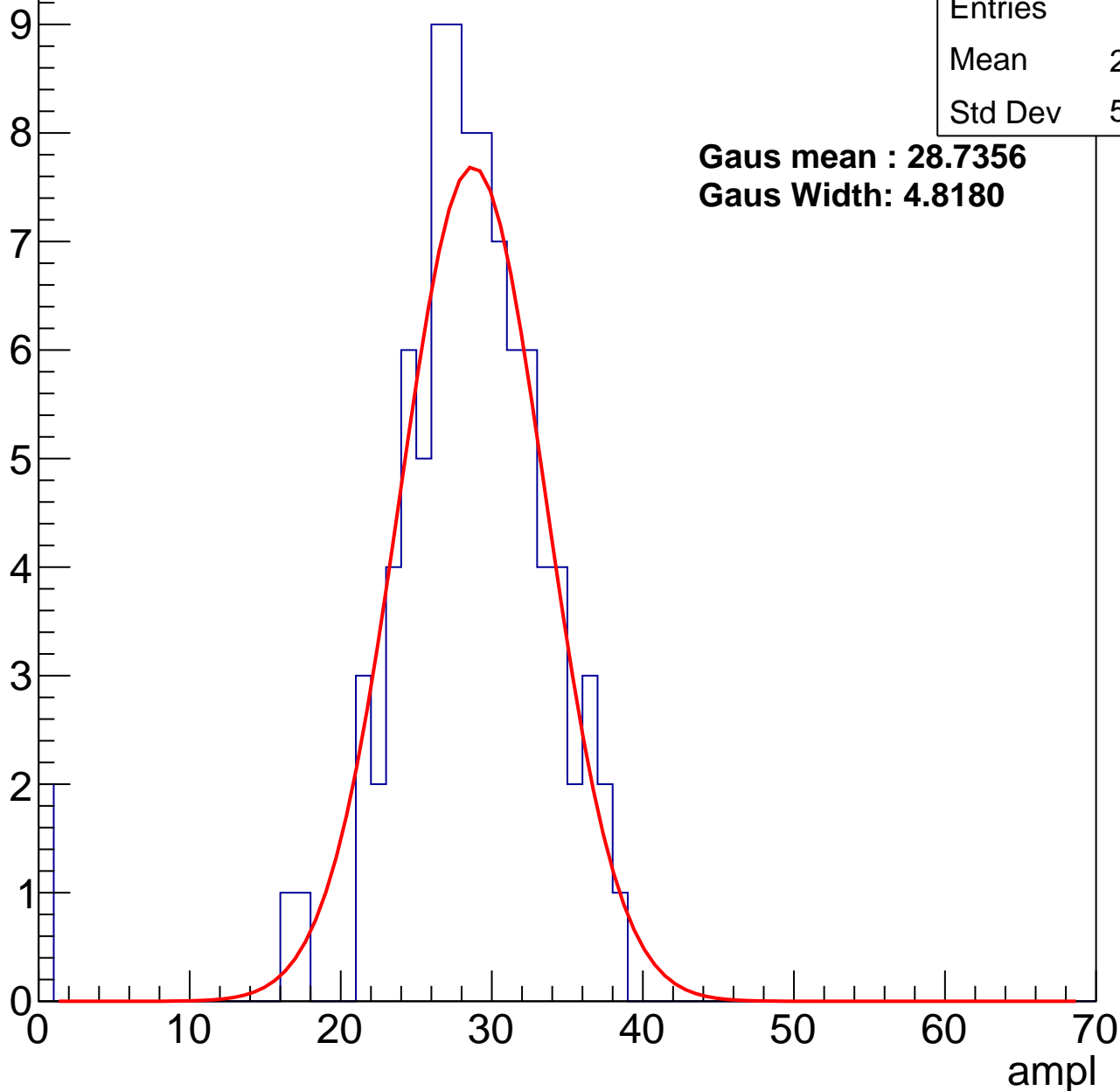
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	27.74
Std Dev	5.969

Gaus mean : 28.7356

Gaus Width: 4.8180



B1L102S, U17-ch106, adc1

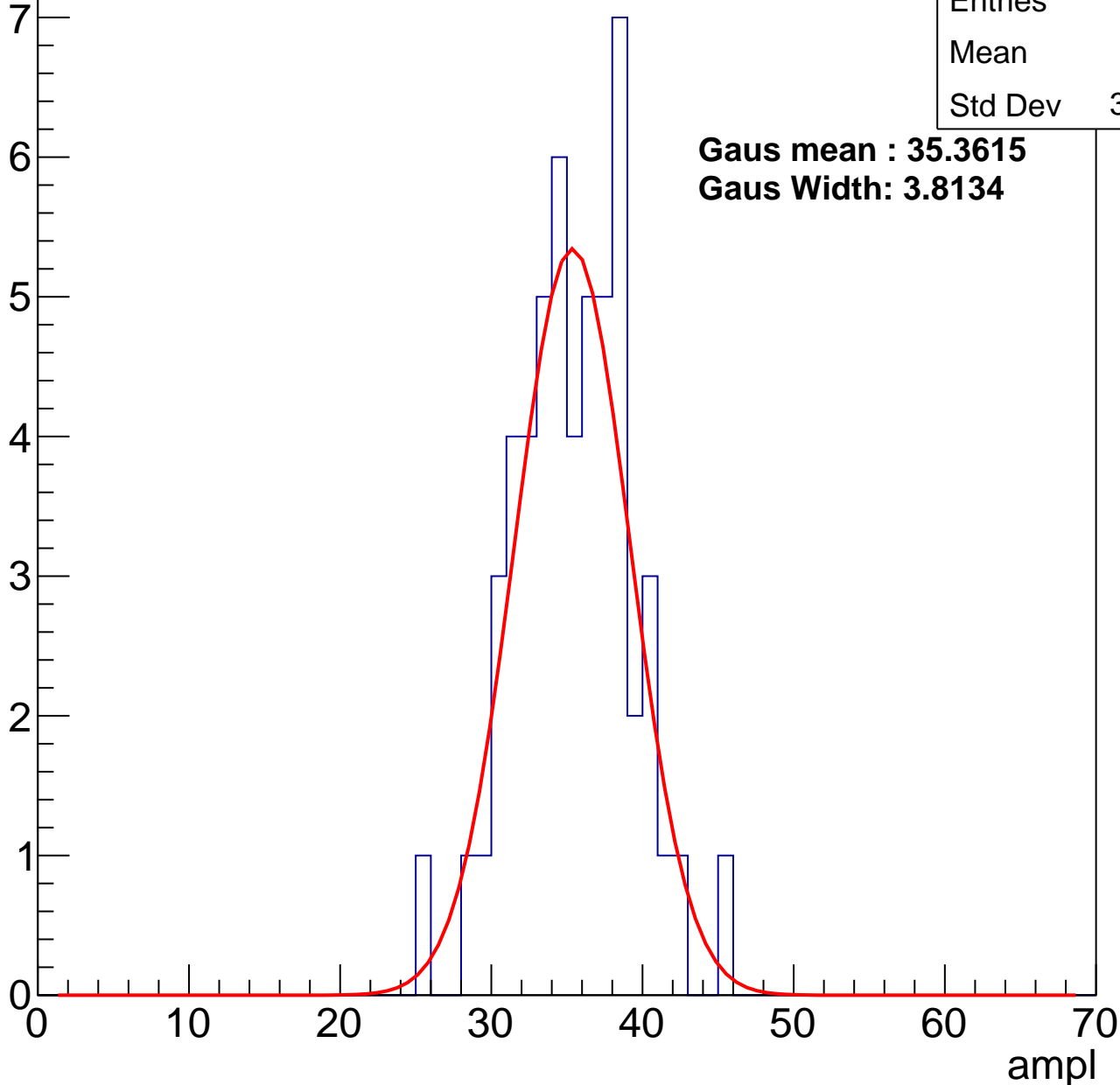
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35
Std Dev	3.766

Gaus mean : 35.3615

Gaus Width: 3.8134



B1L102S, U17-ch106, adc2

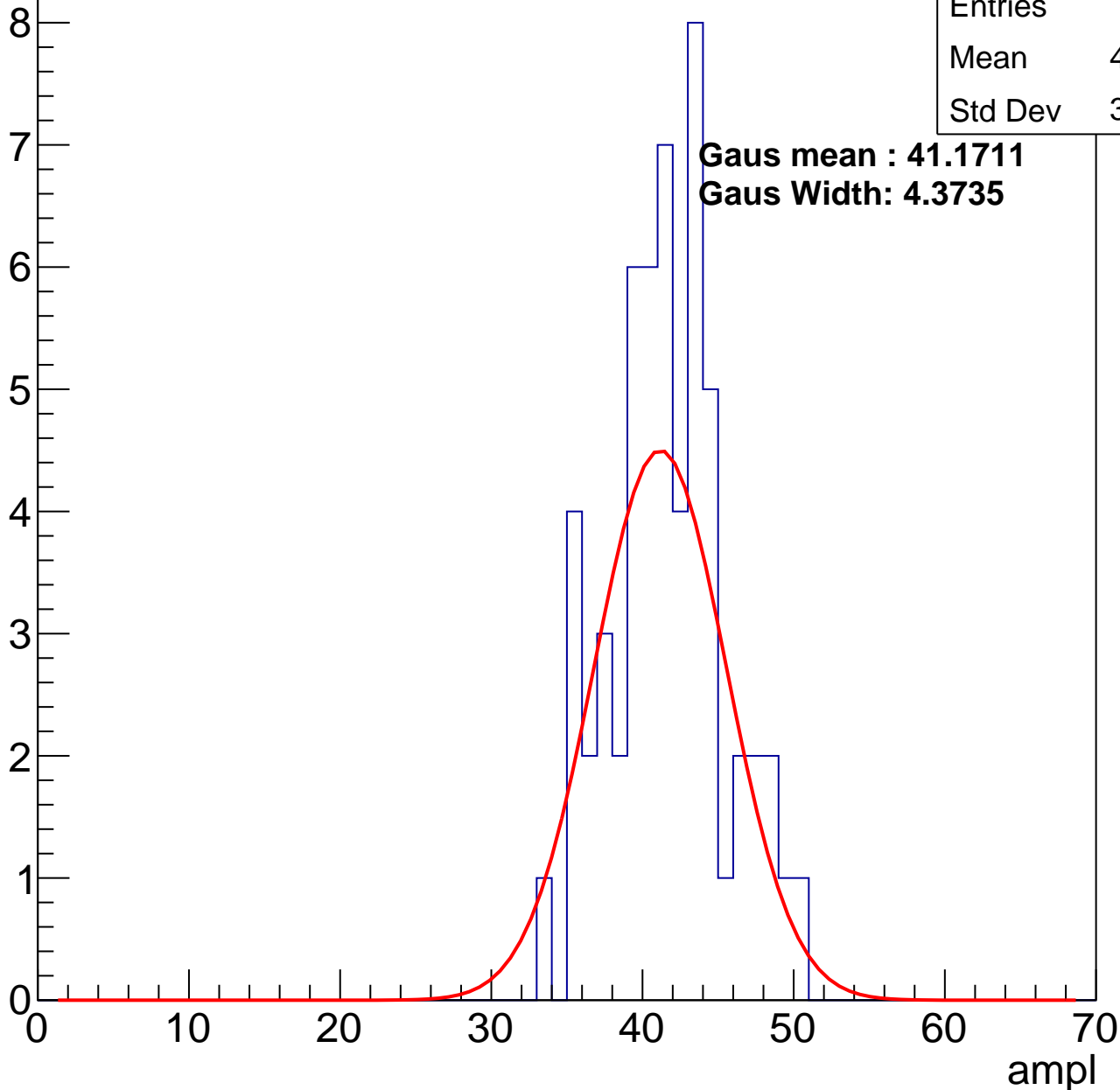
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.25
Std Dev	3.776

Gaus mean : 41.1711

Gaus Width: 4.3735

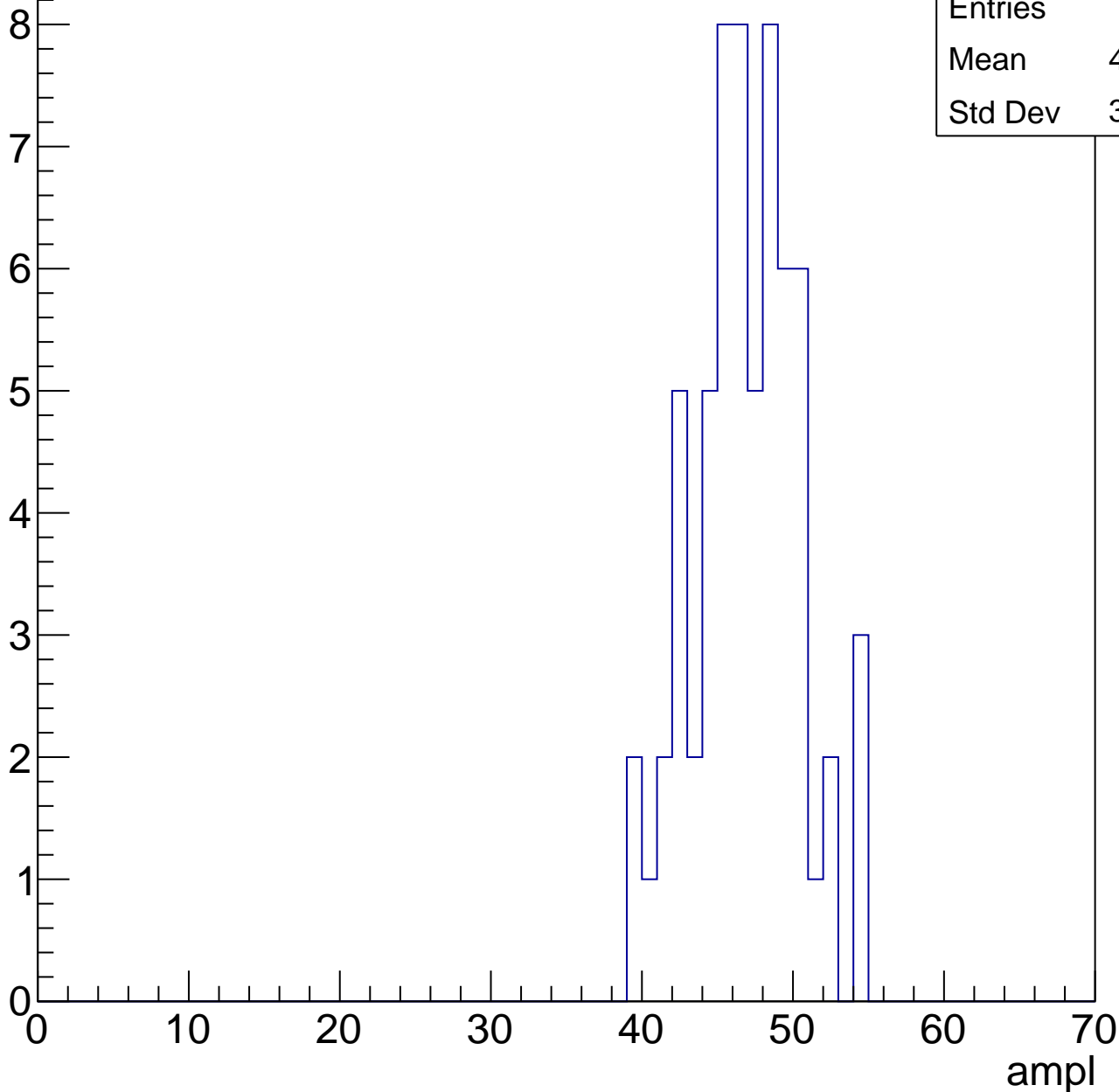


B1L102S, U17-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

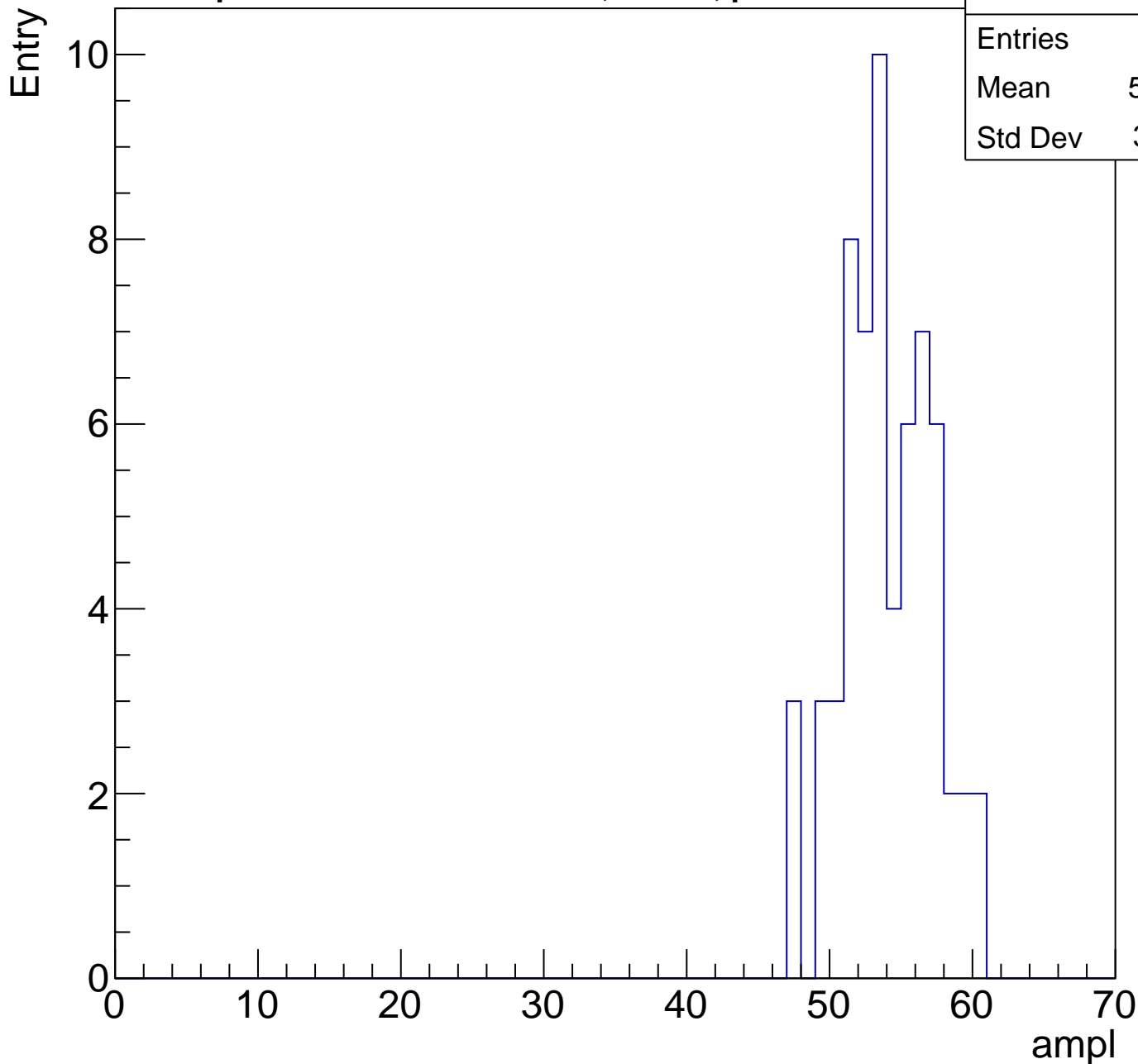
Entries	64
Mean	46.47
Std Dev	3.473



B1L102S, U17-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	53.56
Std Dev	3.111

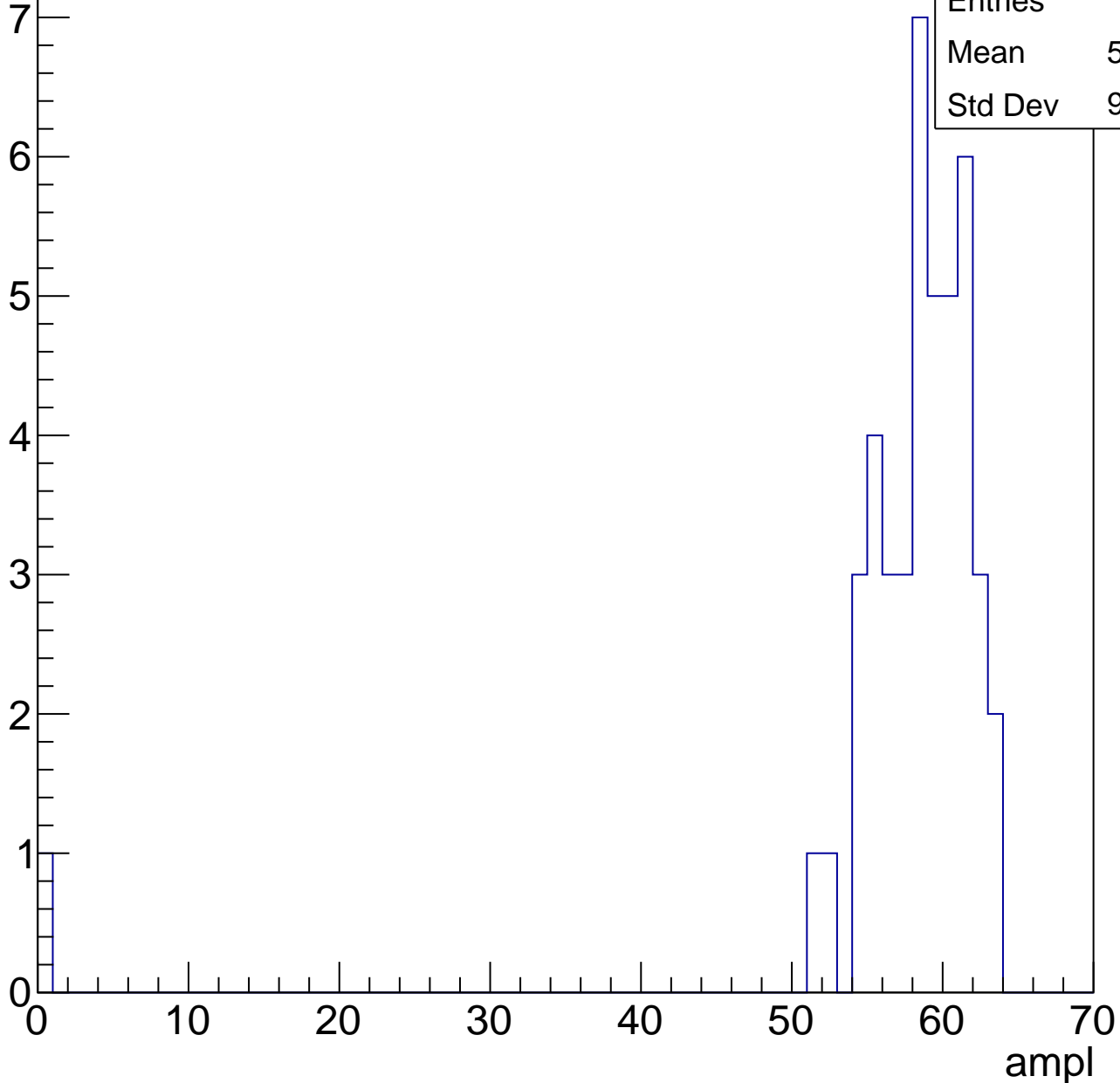


B1L102S, U17-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	56.89
Std Dev	9.133

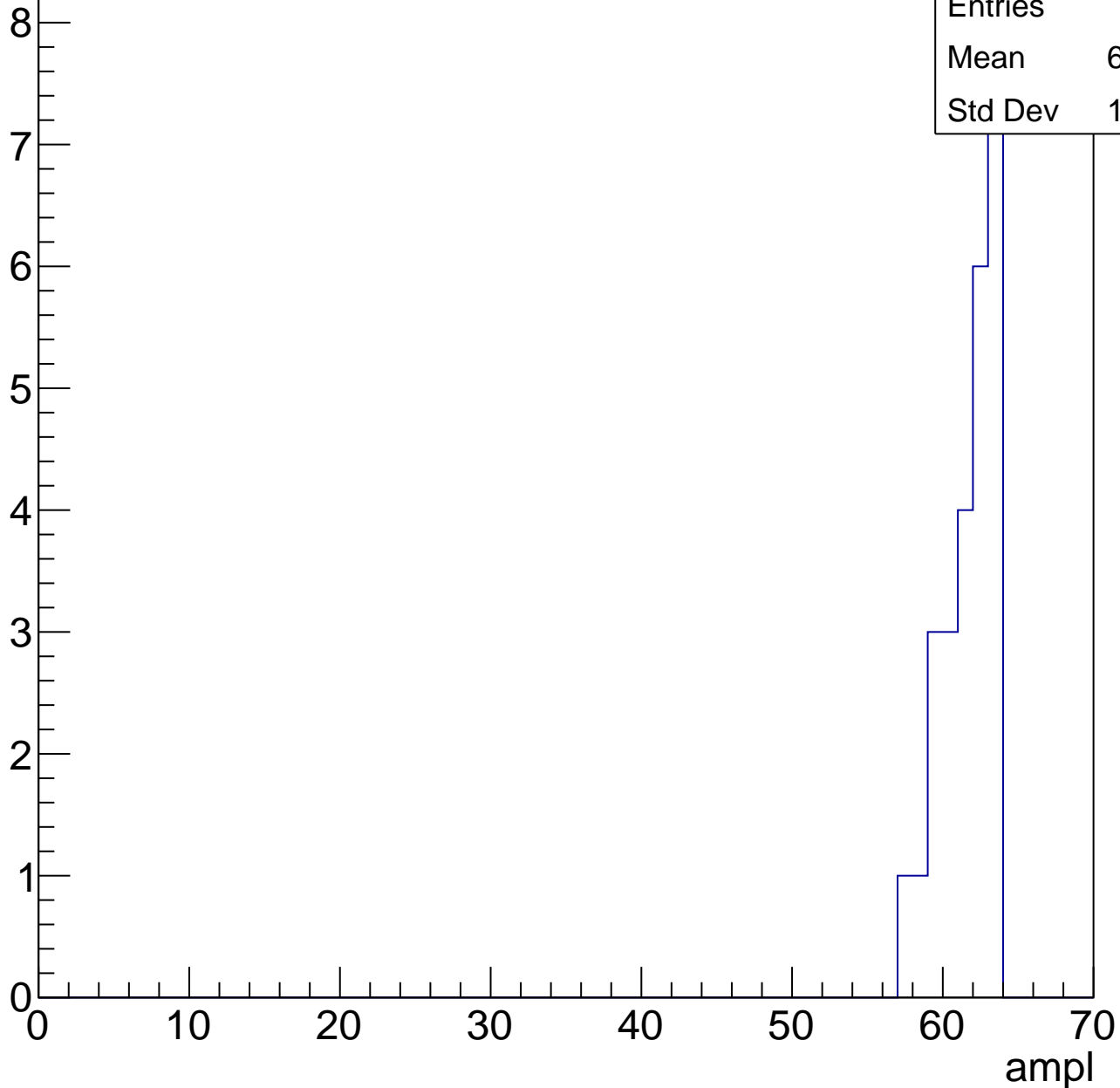


B1L102S, U17-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	61.23
Std Dev	1.717



B1L102S, U17-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch107, adc0

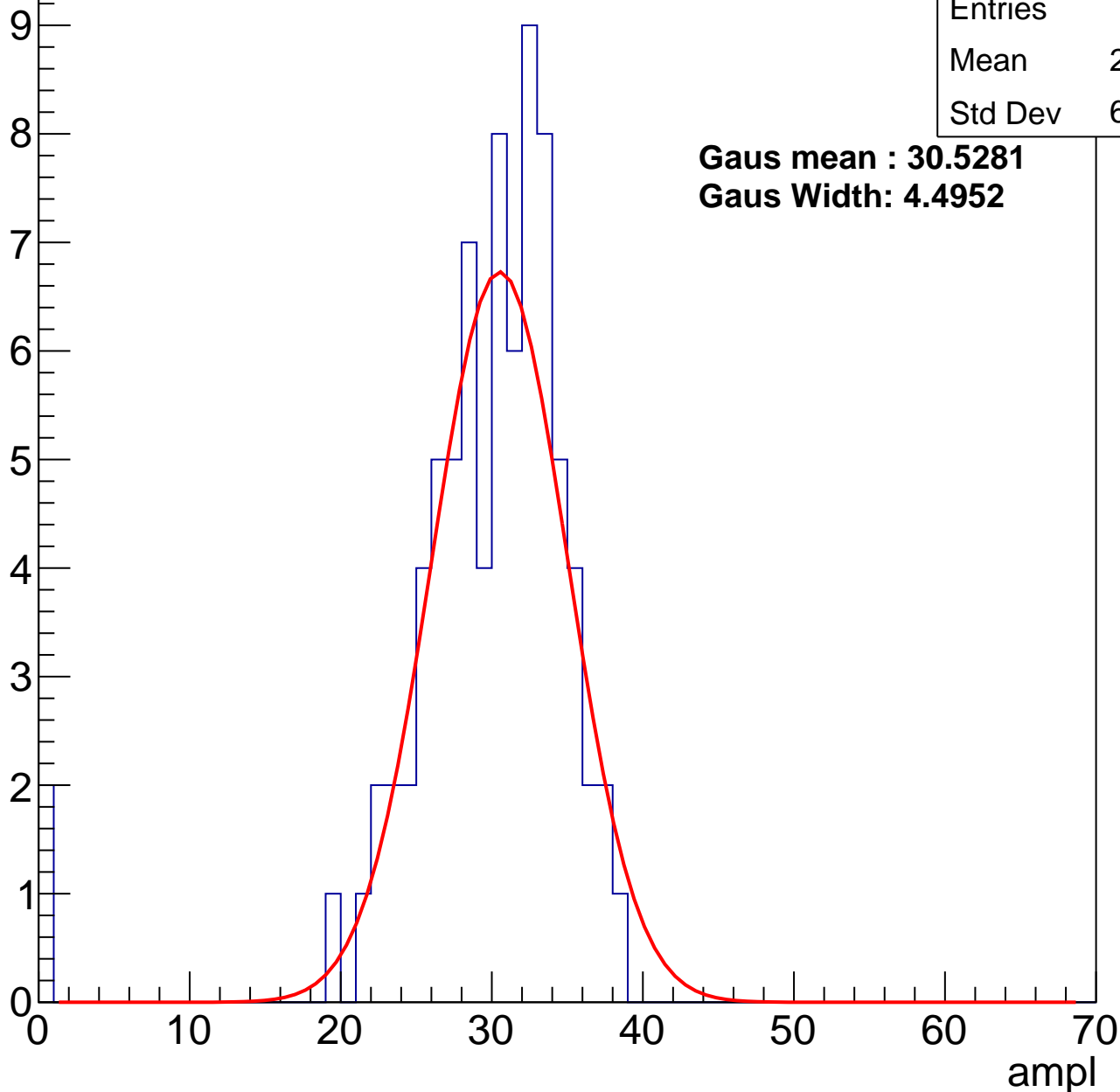
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.09
Std Dev	6.142

Gaus mean : 30.5281

Gaus Width: 4.4952



B1L102S, U17-ch107, adc1

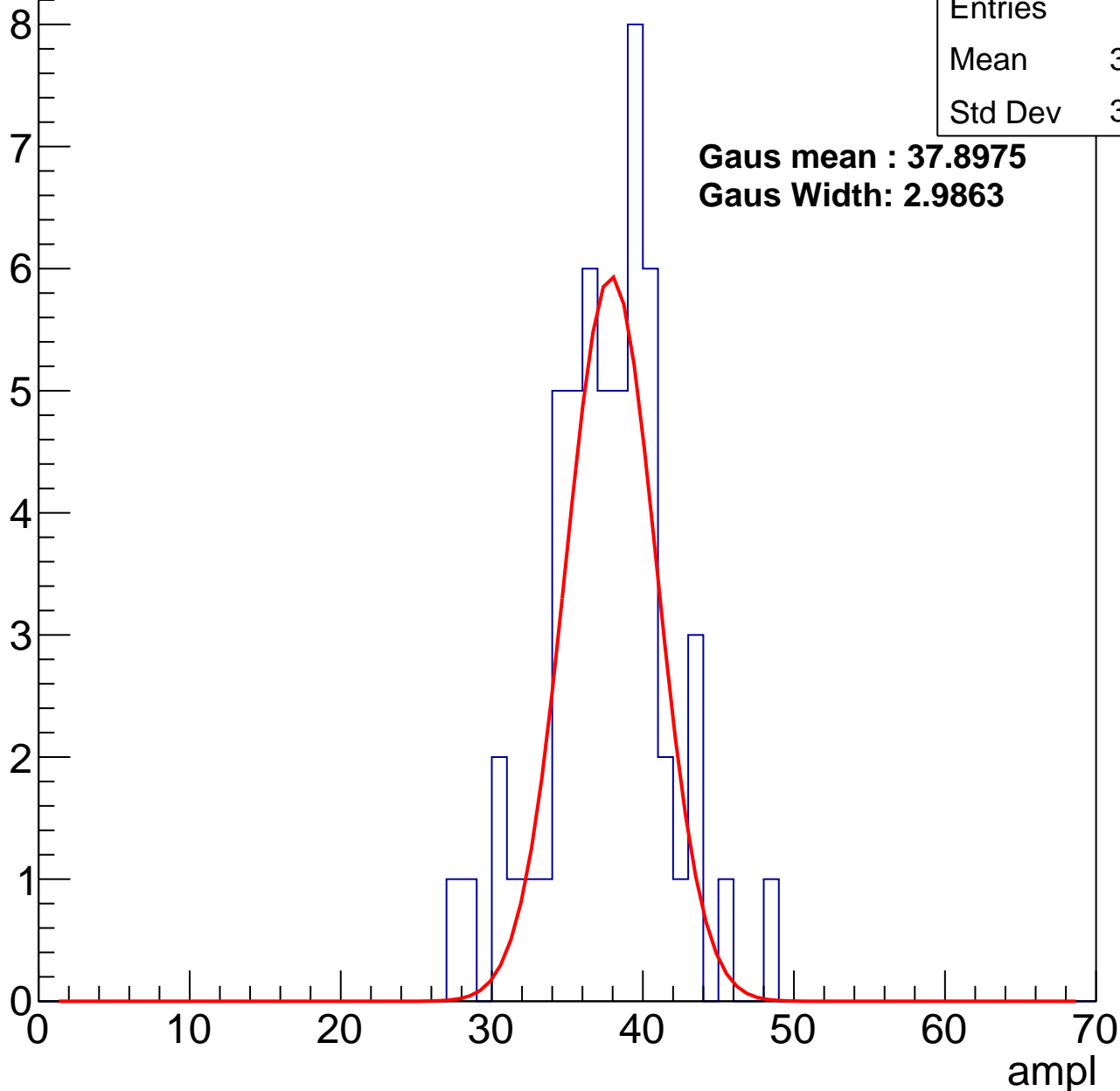
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	37.18
Std Dev	3.968

Gaus mean : 37.8975

Gaus Width: 2.9863



B1L102S, U17-ch107, adc2

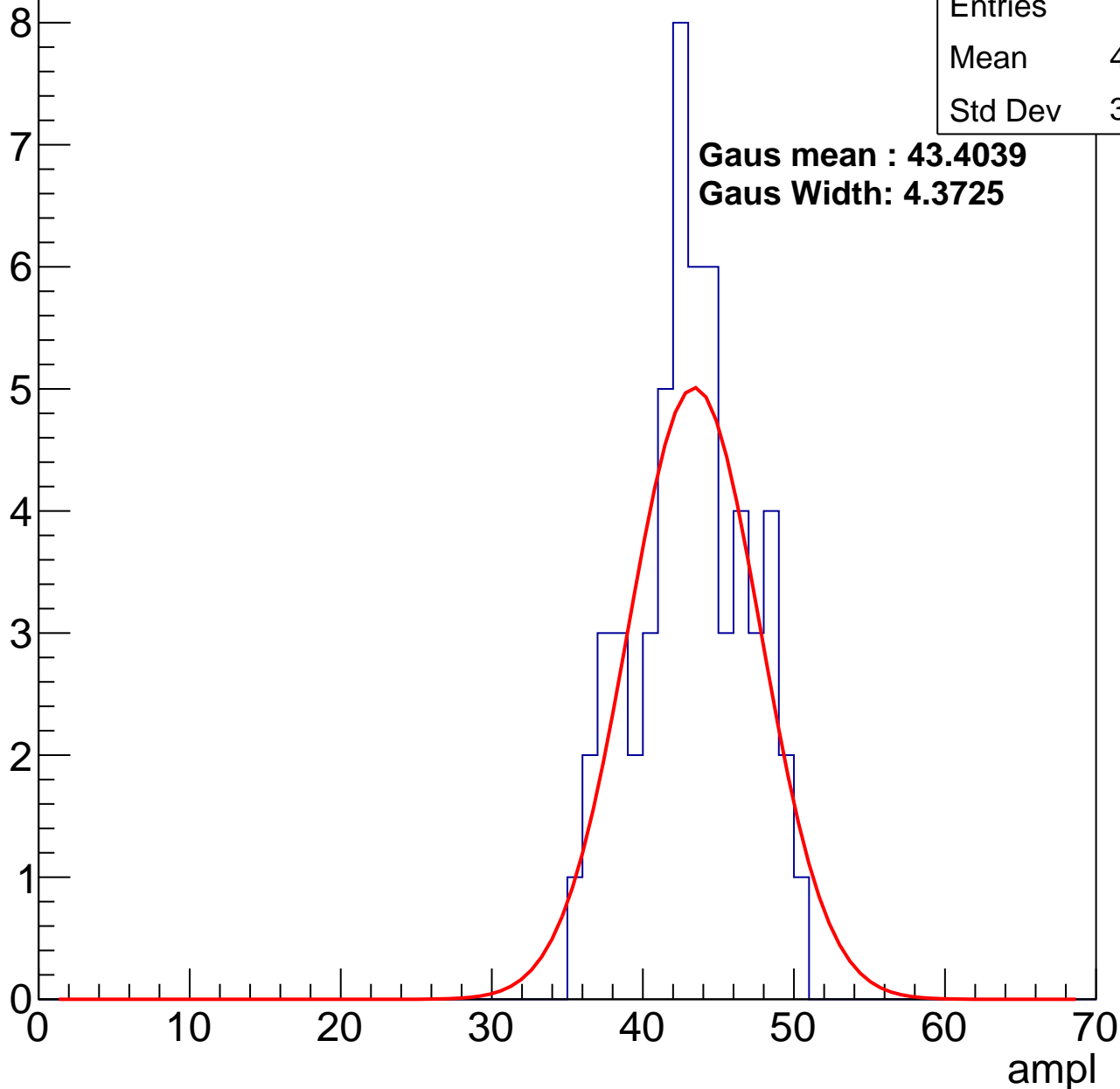
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.73
Std Dev	3.662

Gaus mean : 43.4039

Gaus Width: 4.3725

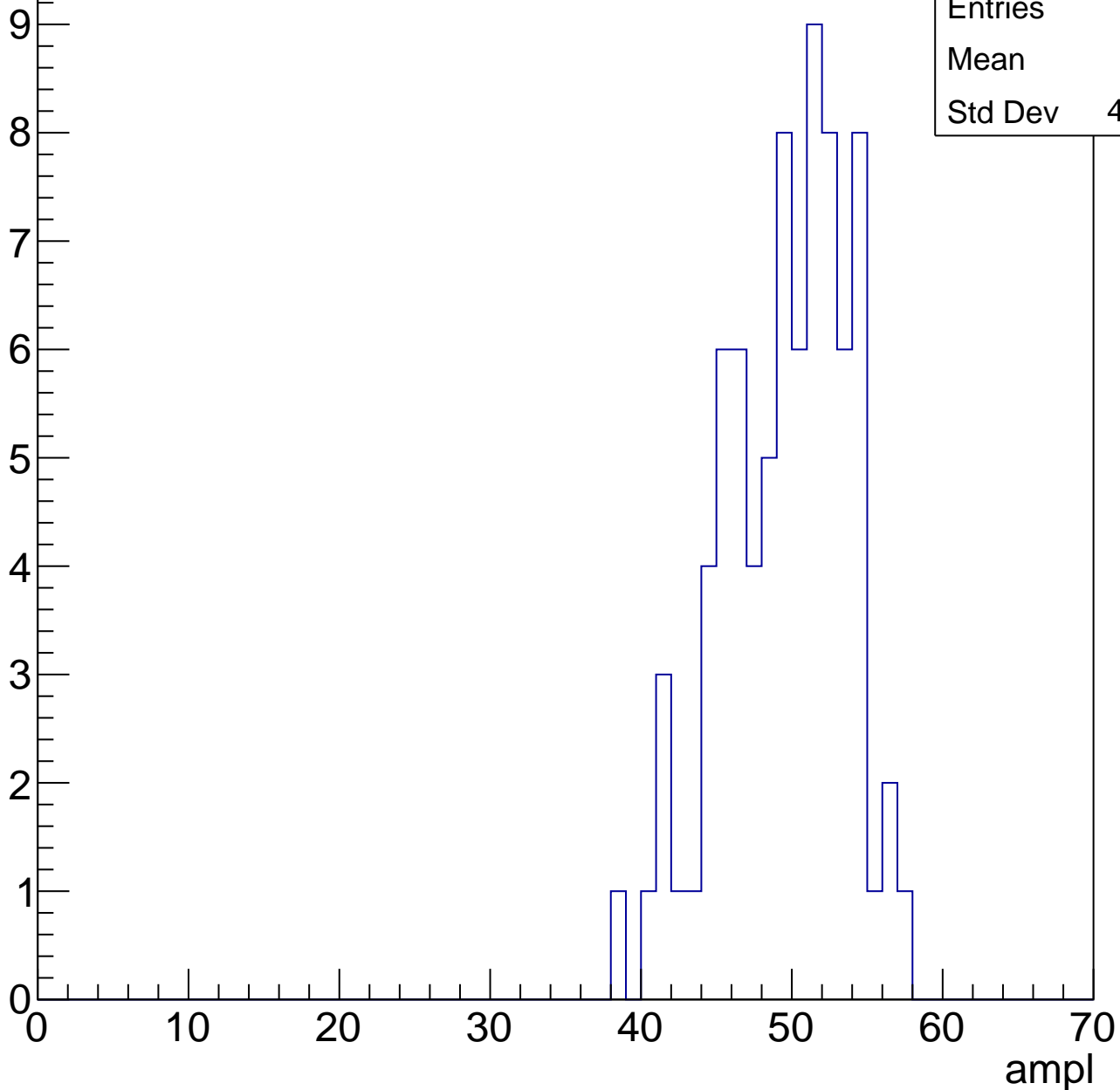


B1L102S, U17-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	49.1
Std Dev	4.093

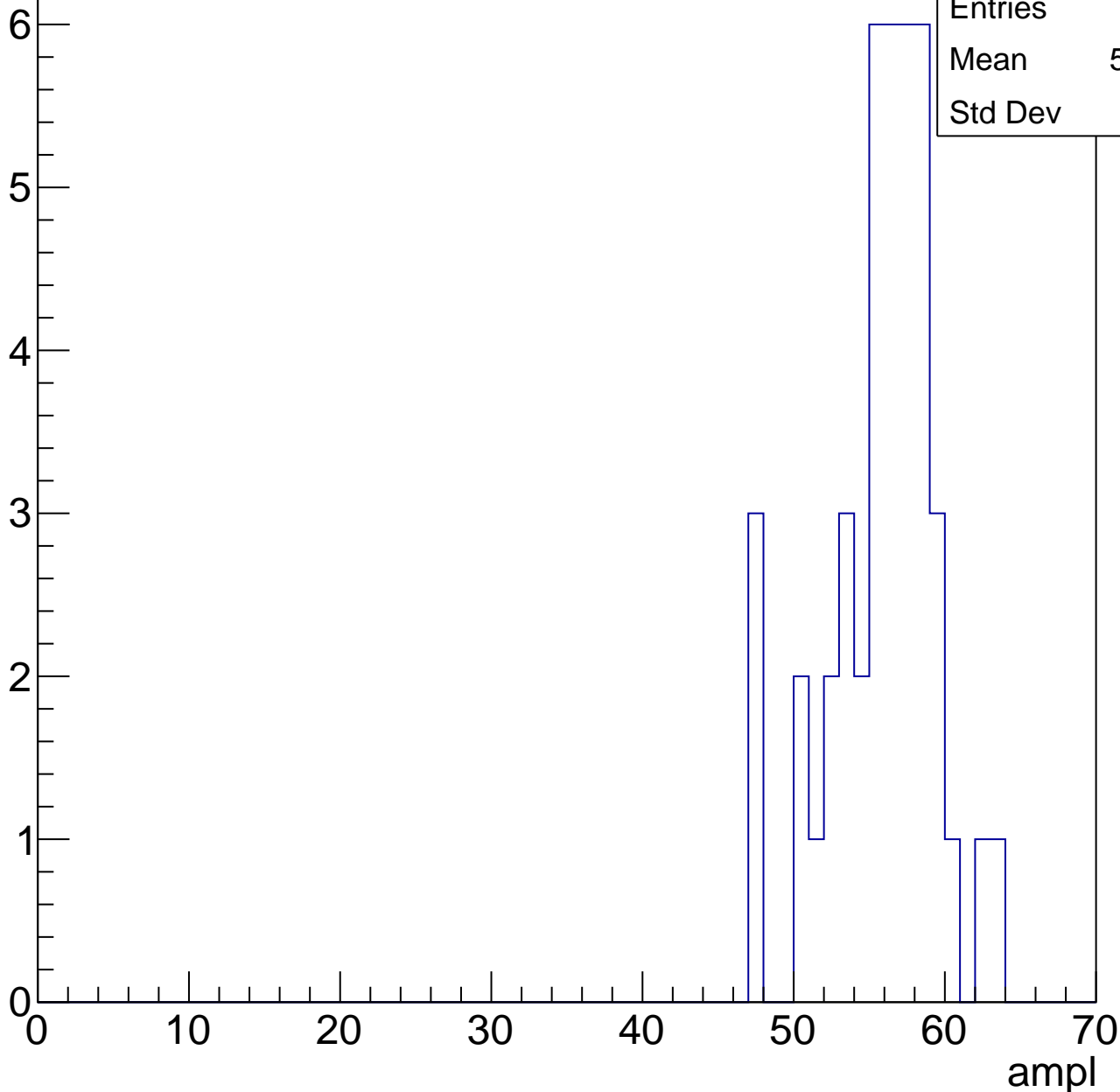


B1L102S, U17-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55.37
Std Dev	3.59

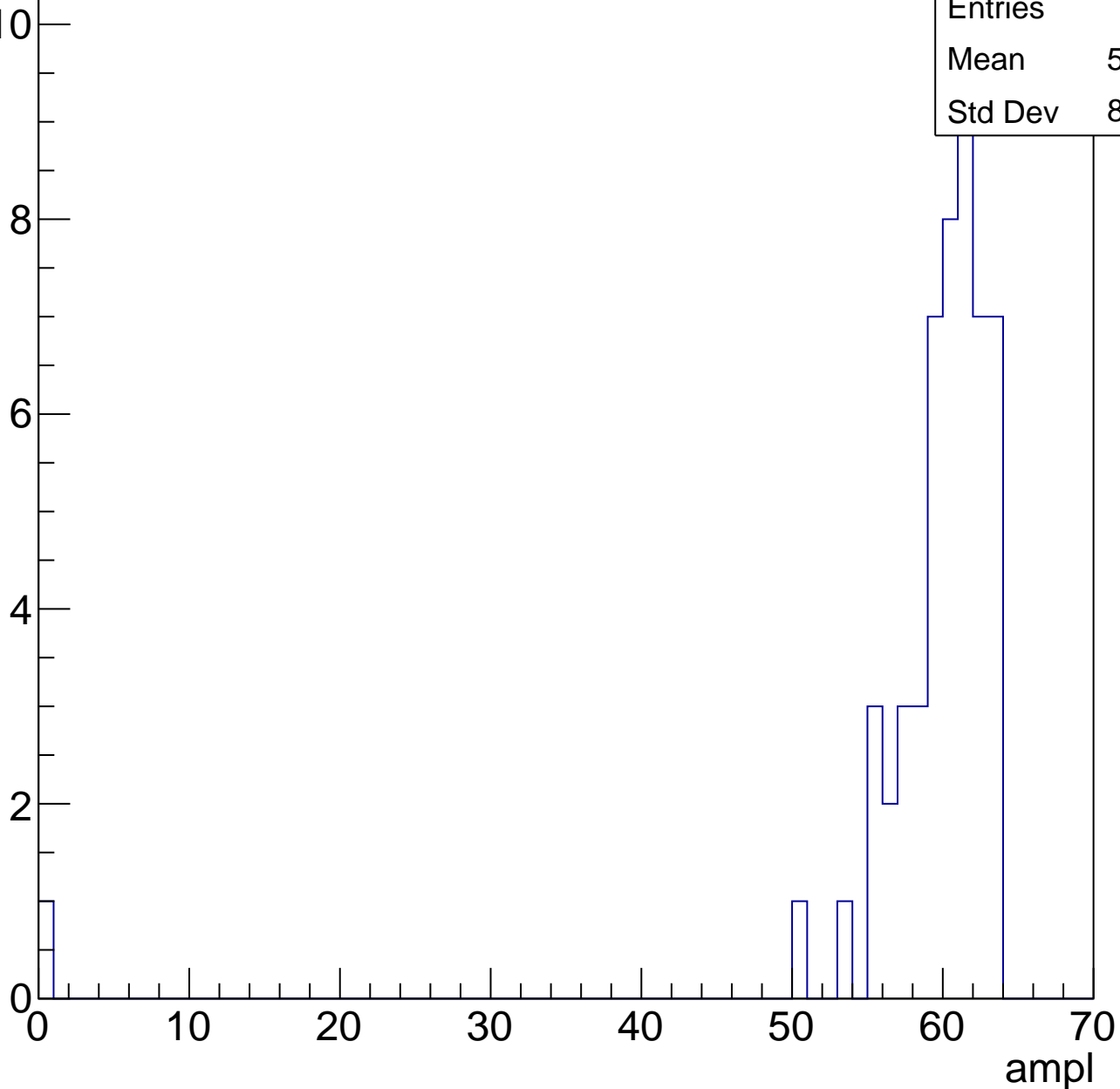


B1L102S, U17-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.55
Std Dev	8.568



B1L102S, U17-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

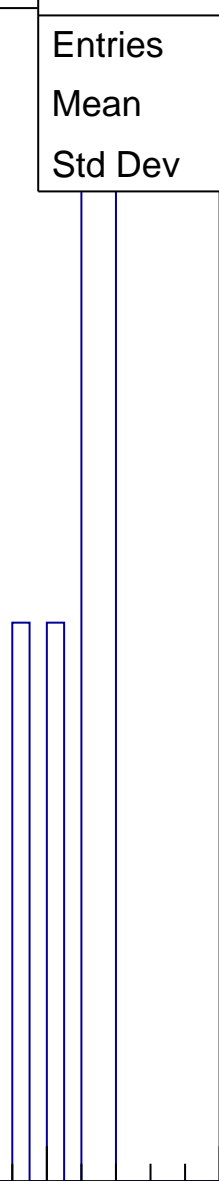
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.795

0 10 20 30 40 50 60 70

ampl



B1L102S, U17-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch108, adc0

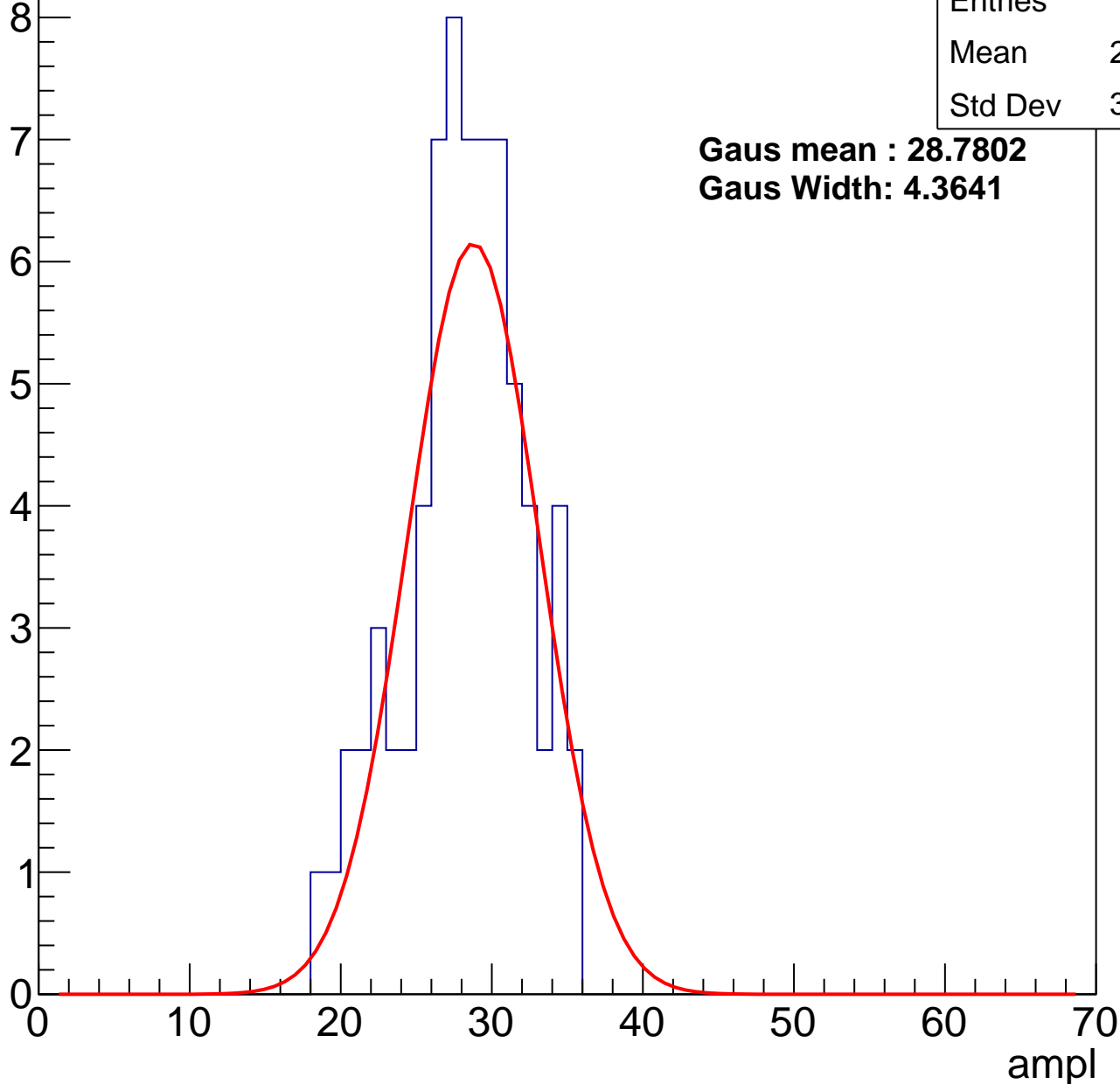
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.73
Std Dev	3.964

Gaus mean : 28.7802

Gaus Width: 4.3641



B1L102S, U17-ch108, adc1

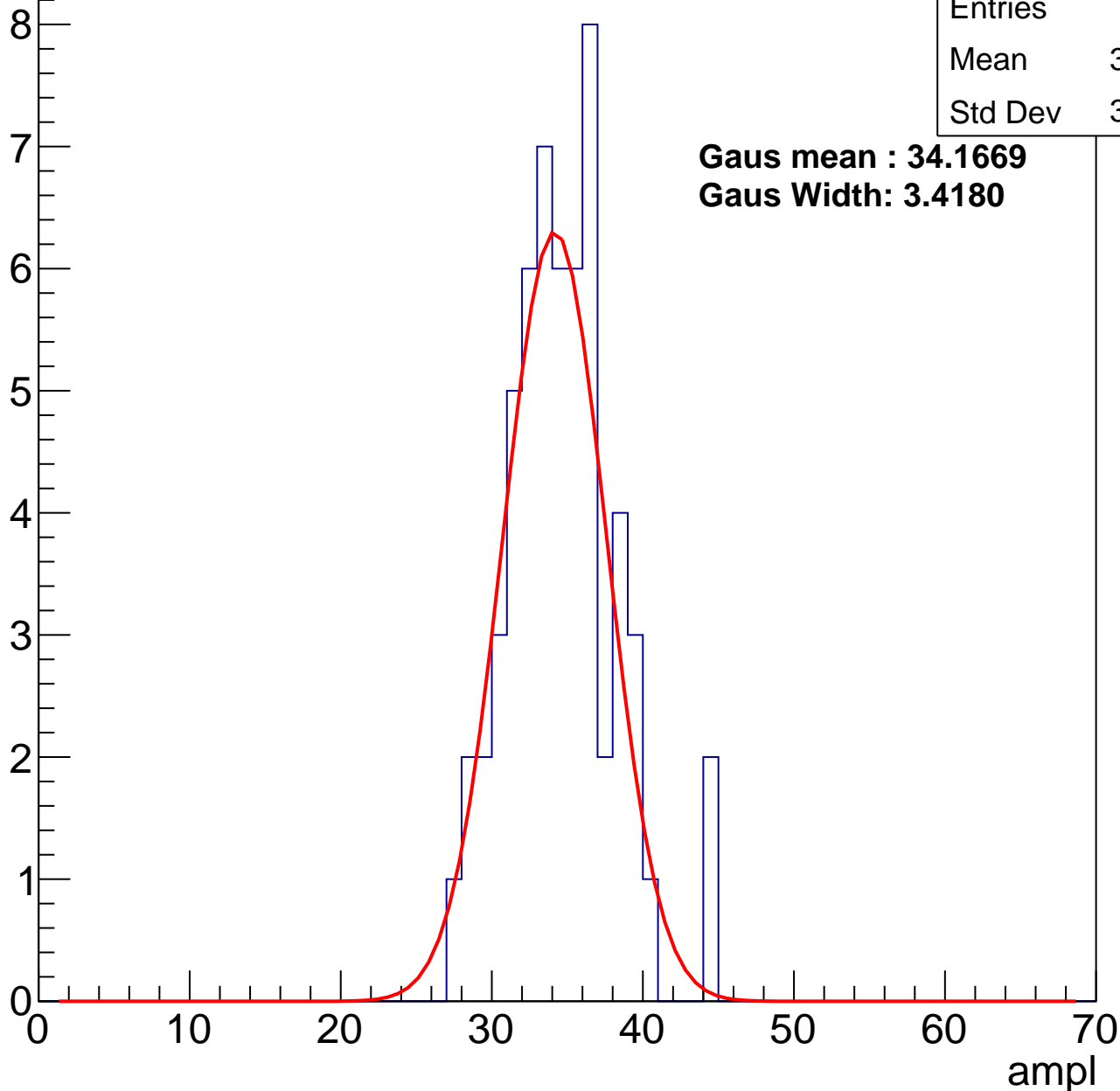
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	34.17
Std Dev	3.534

Gaus mean : 34.1669

Gaus Width: 3.4180



B1L102S, U17-ch108, adc2

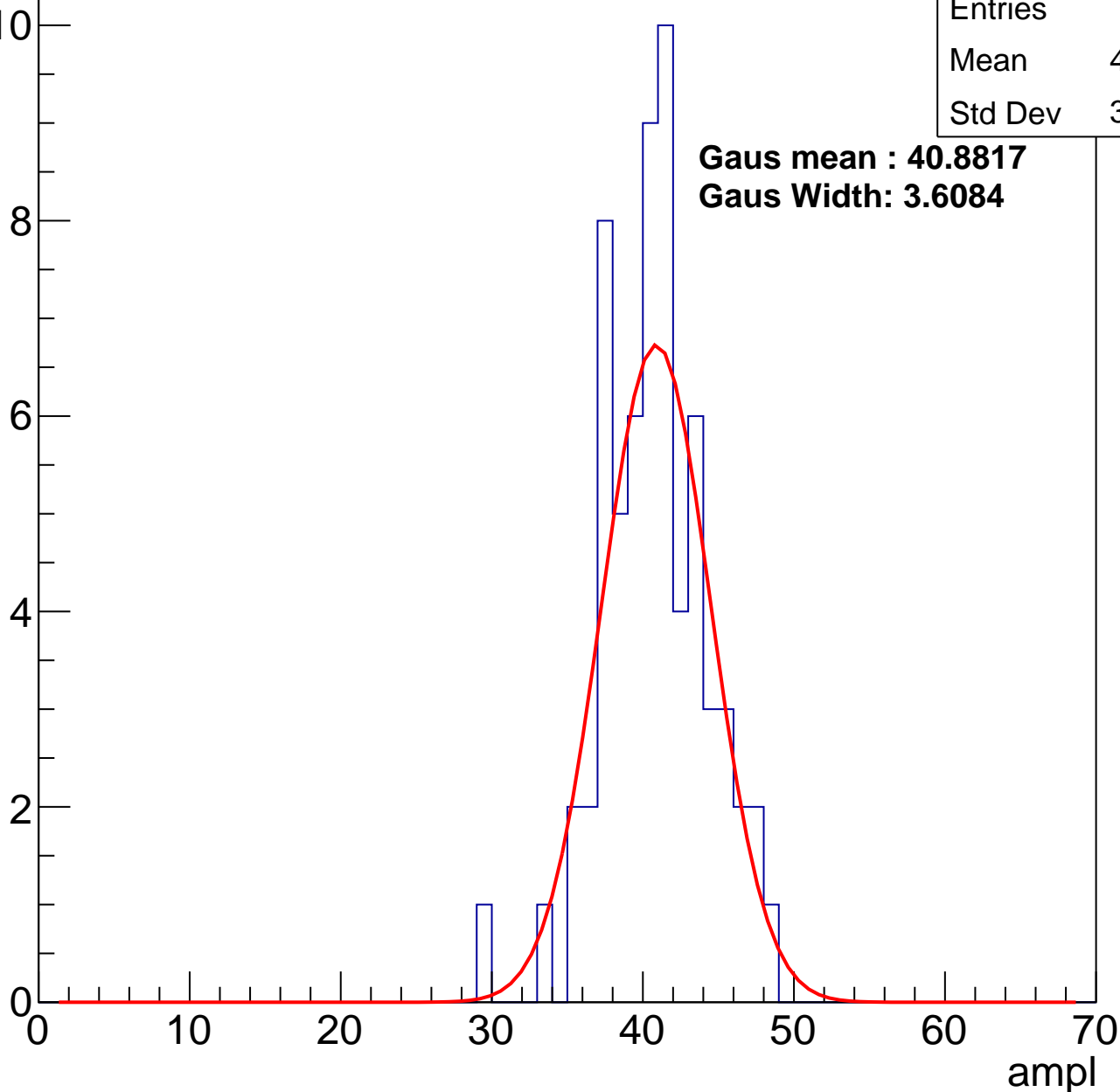
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	40.32
Std Dev	3.465

Gaus mean : 40.8817

Gaus Width: 3.6084

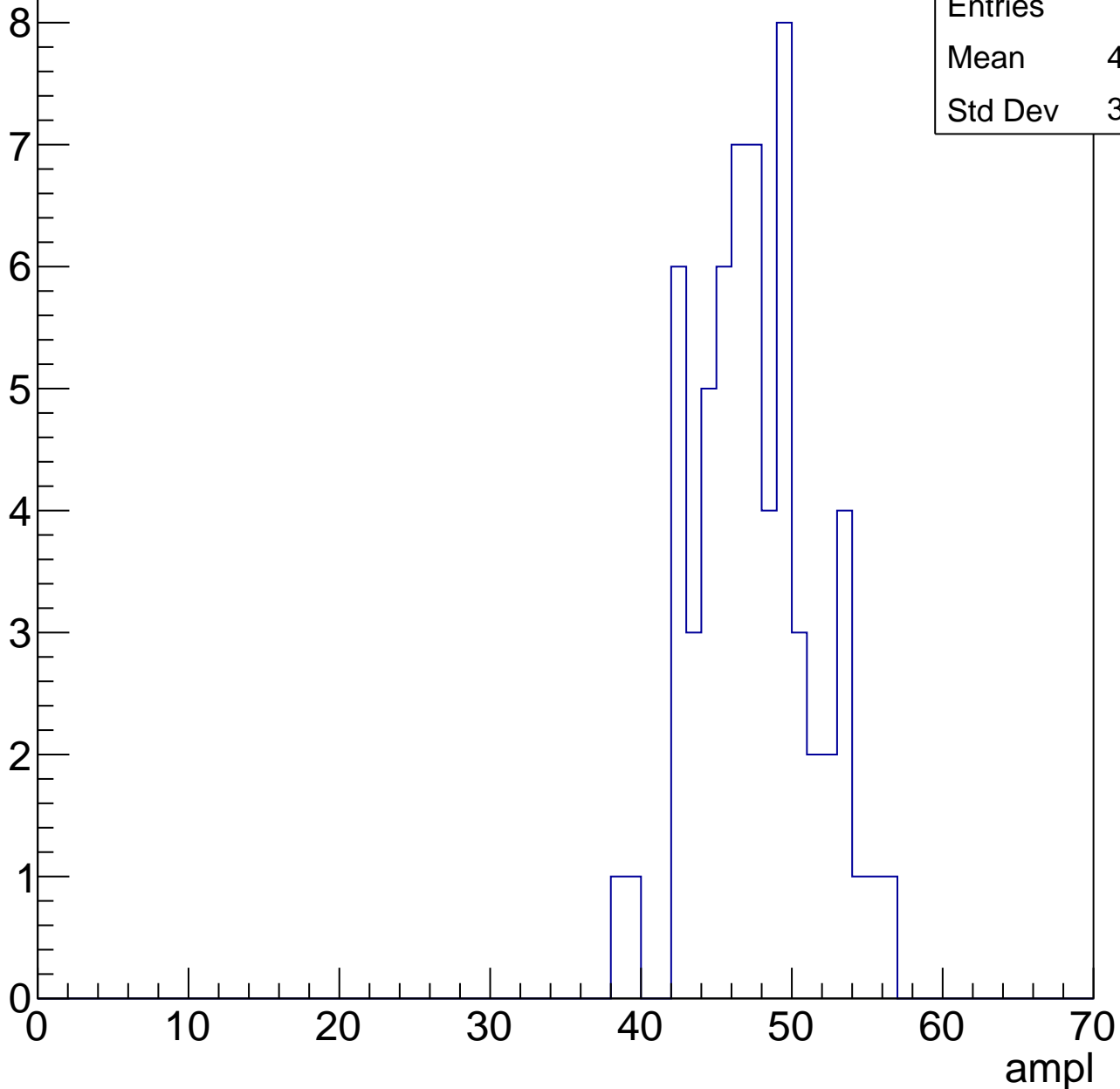


B1L102S, U17-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	47.03
Std Dev	3.814

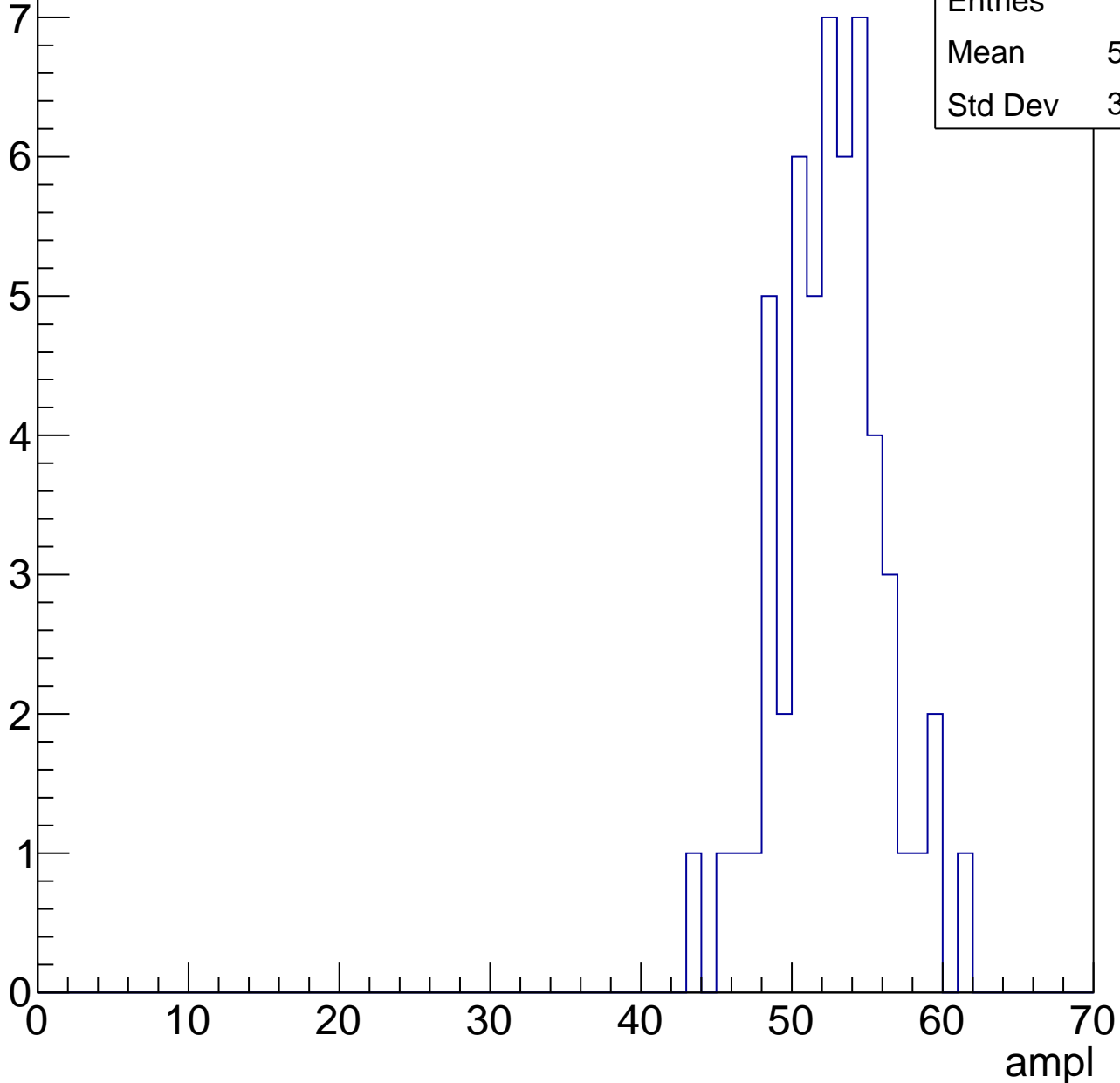


B1L102S, U17-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	52.15
Std Dev	3.535

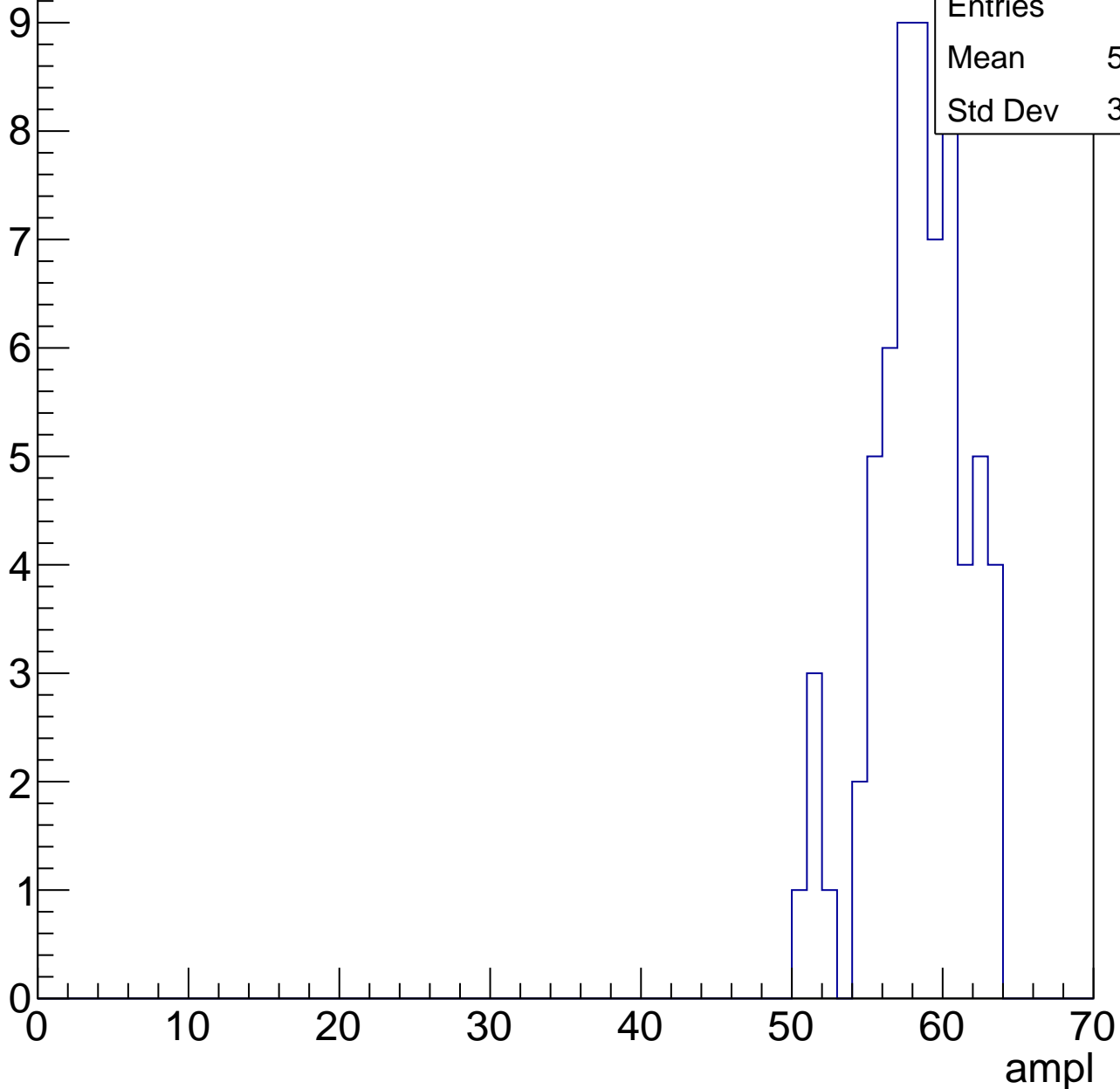


B1L102S, U17-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	57.94
Std Dev	3.092

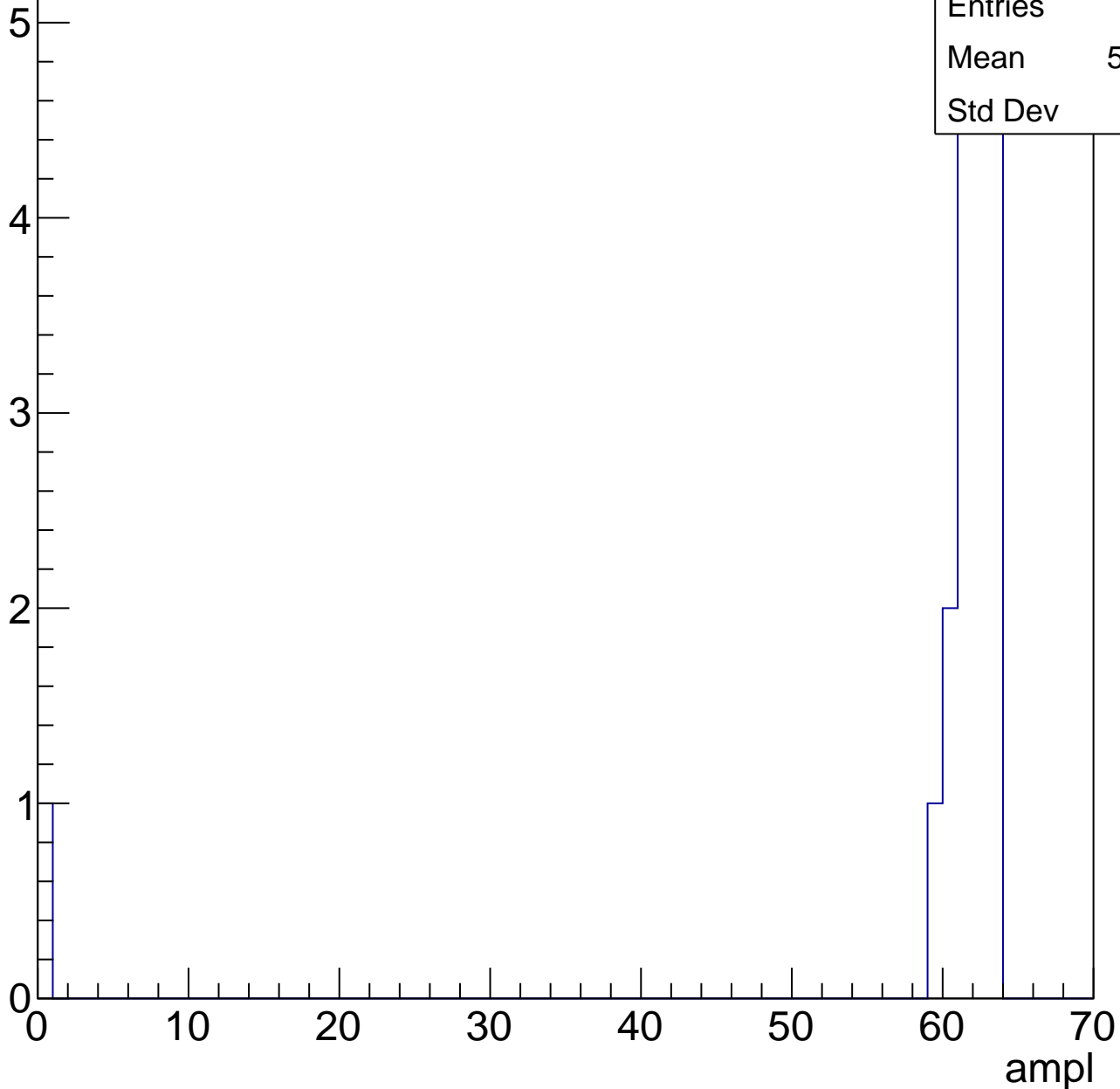


B1L102S, U17-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.37
Std Dev	13.8



B1L102S, U17-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch109, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	29.73
Std Dev	3.8

Gaus mean : 29.7937

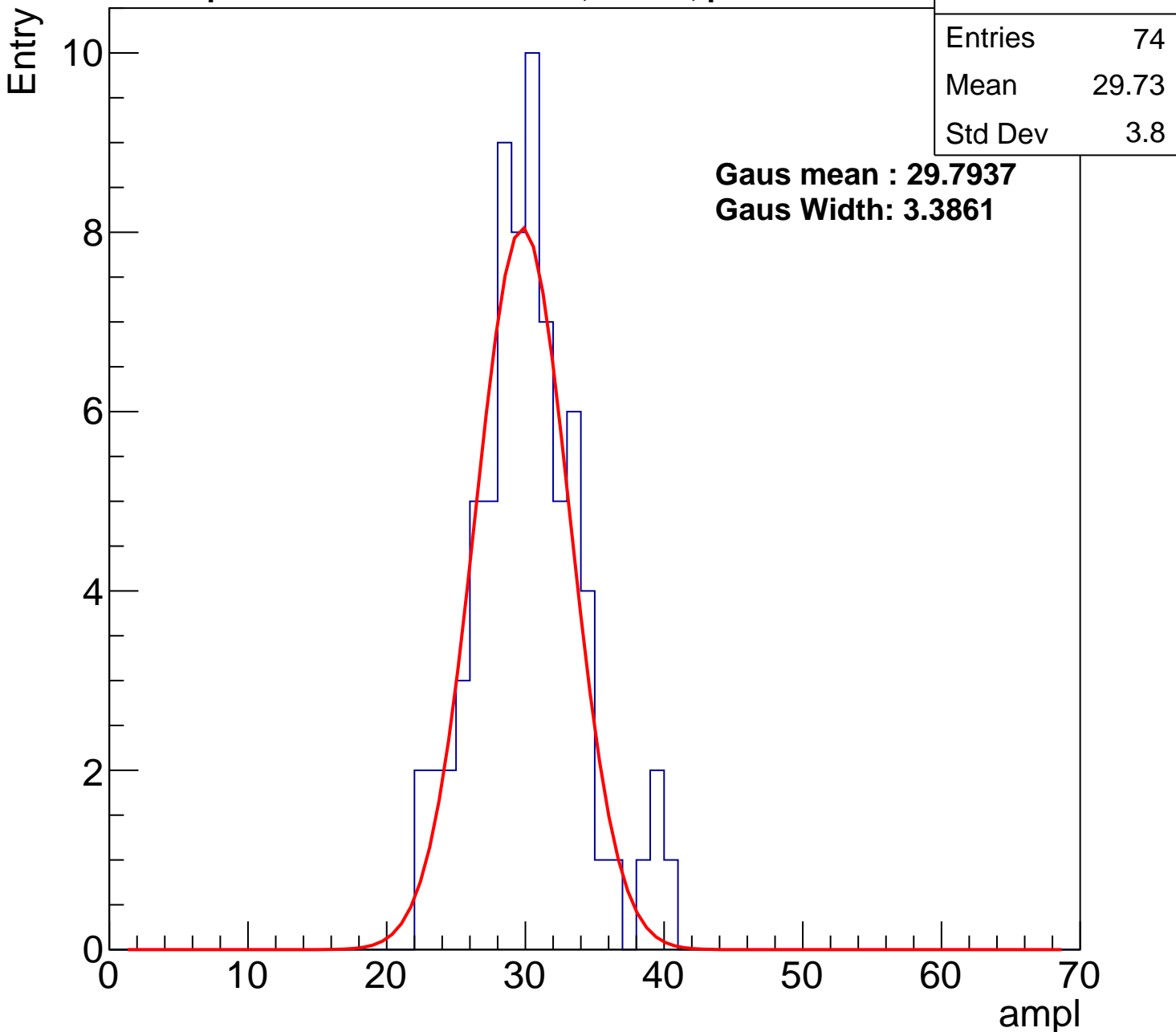
Gaus Width: 3.3861

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch109, adc1

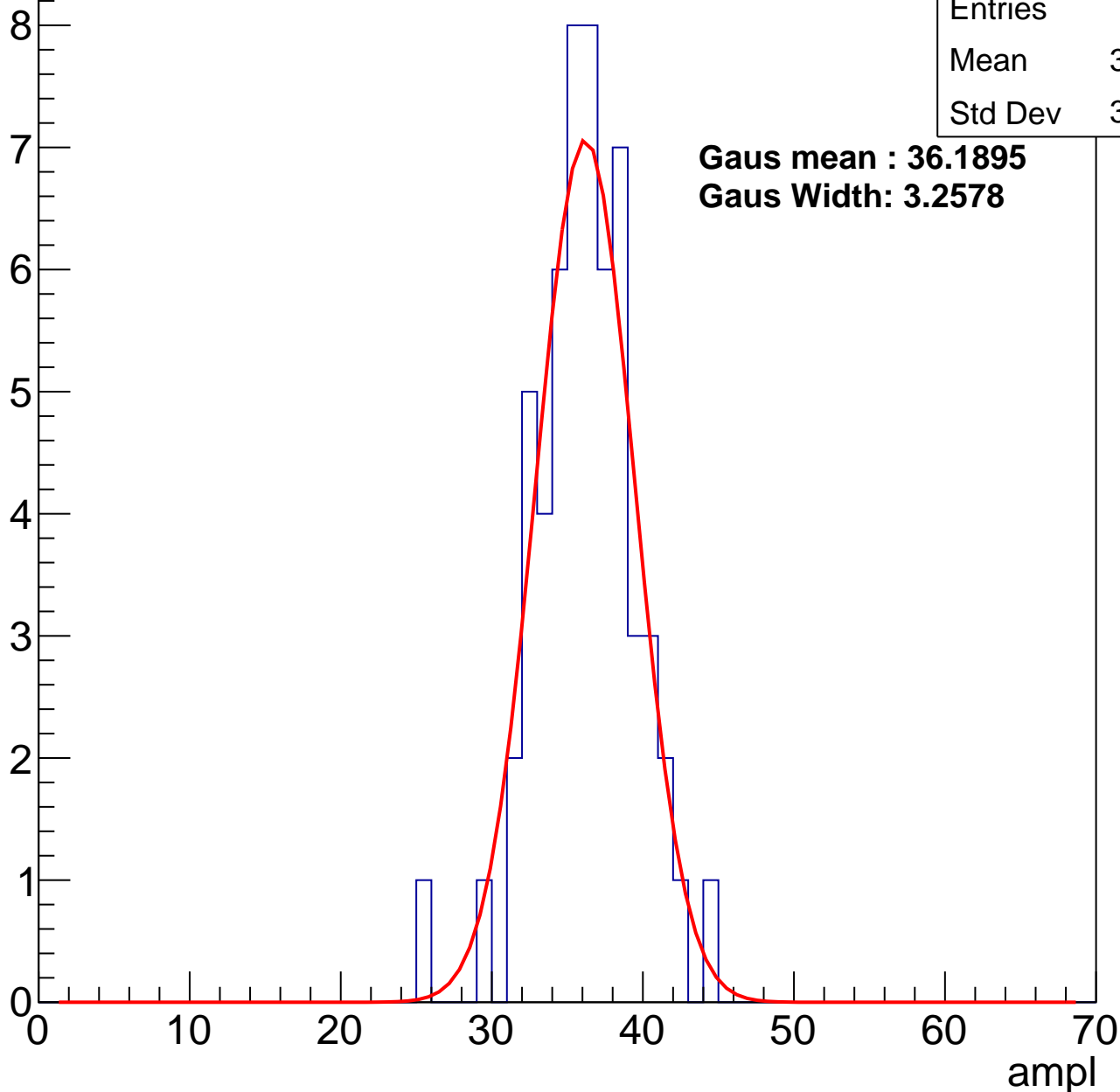
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.74
Std Dev	3.278

Gaus mean : 36.1895

Gaus Width: 3.2578



B1L102S, U17-ch109, adc2

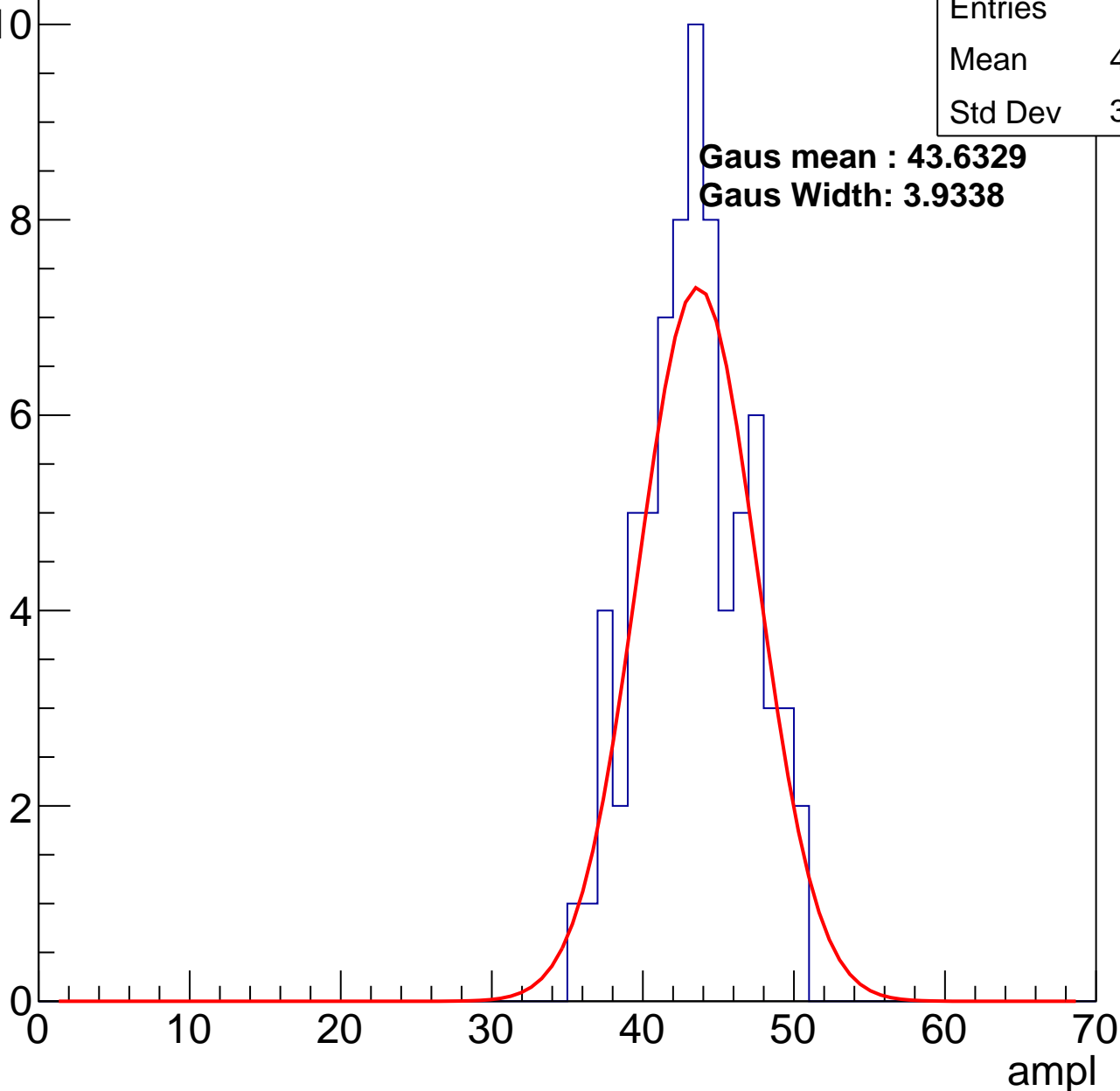
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.95
Std Dev	3.514

Gaus mean : 43.6329

Gaus Width: 3.9338

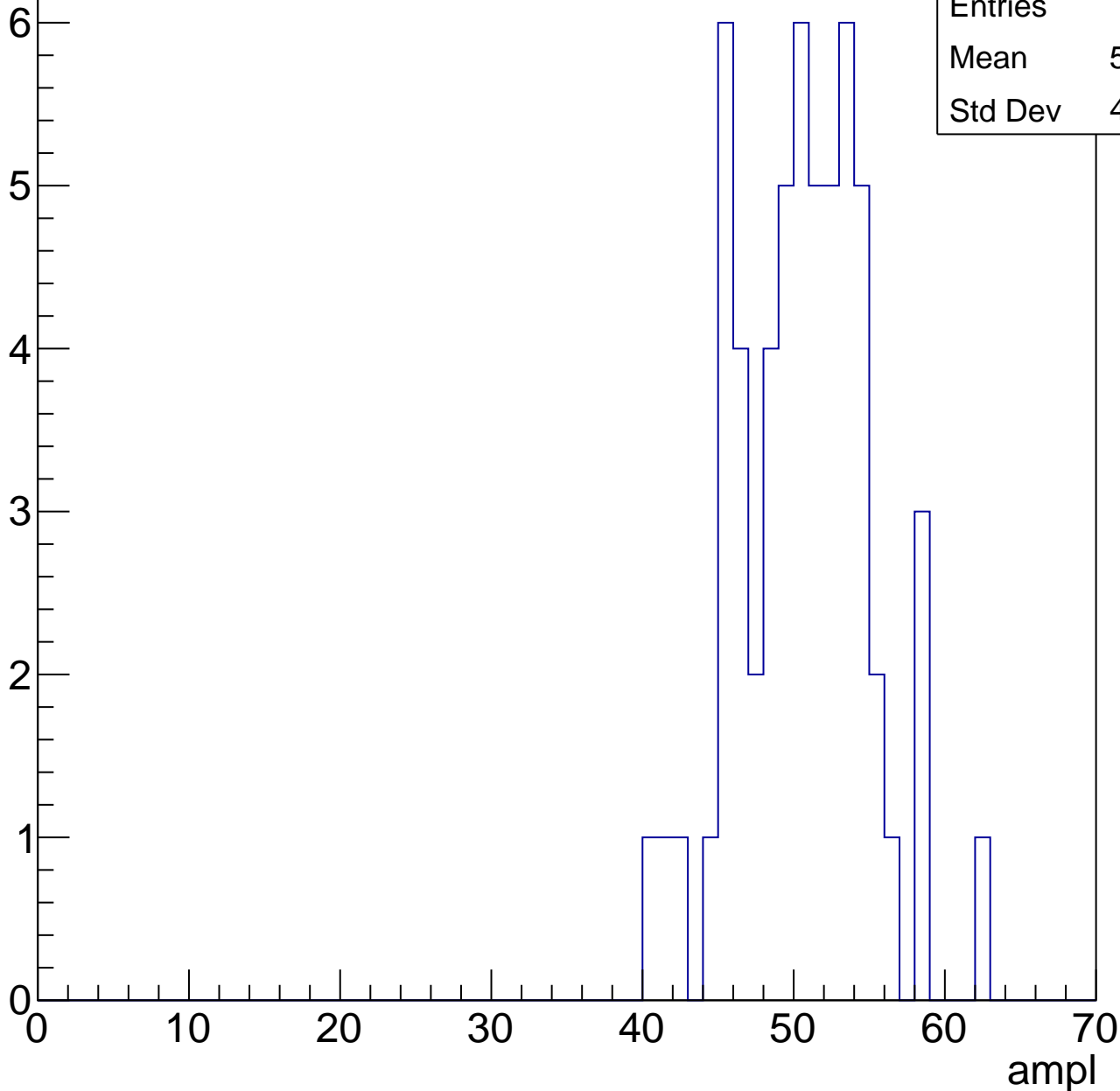


B1L102S, U17-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.12
Std Dev	4.346

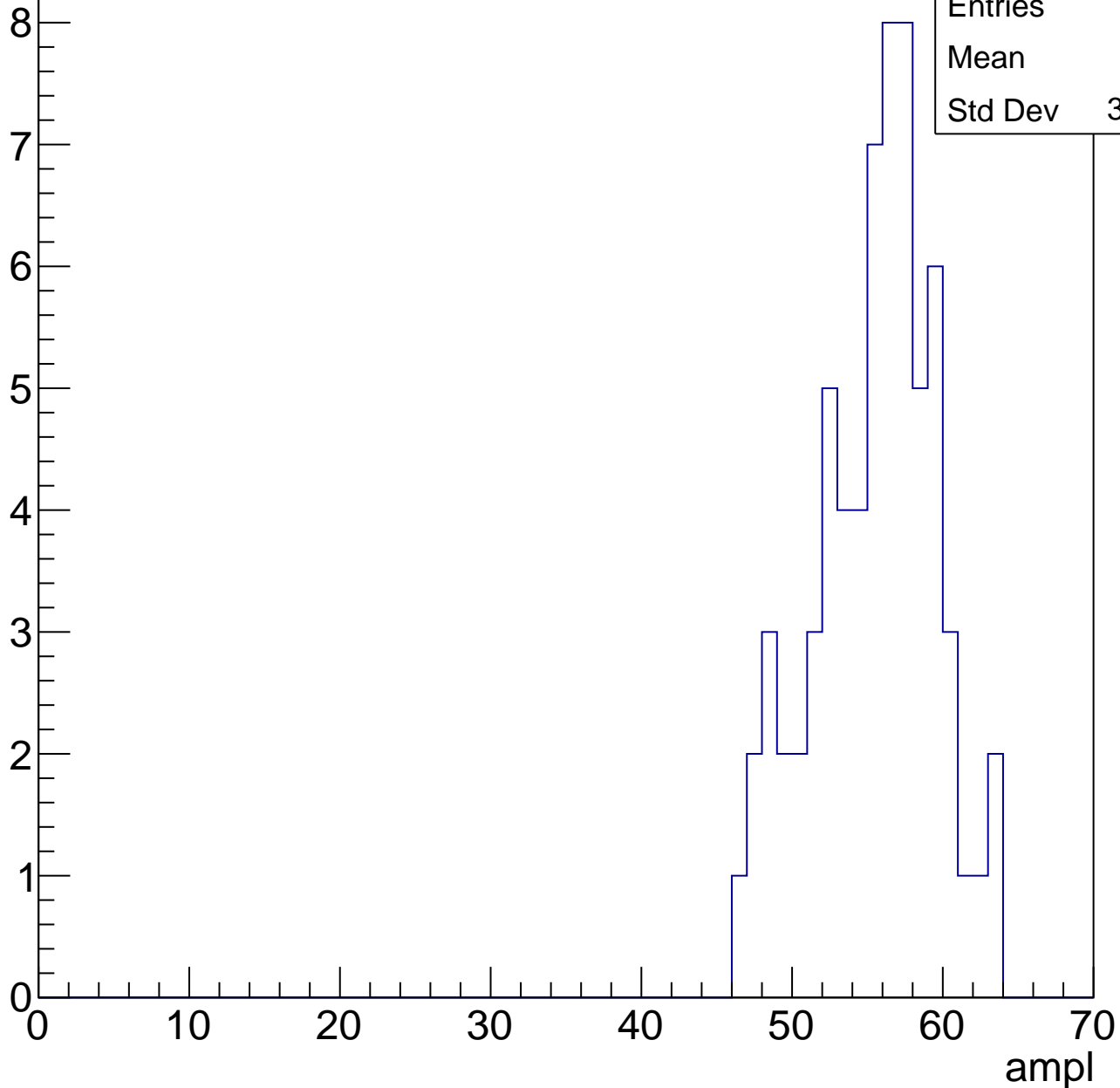


B1L102S, U17-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	55
Std Dev	3.978

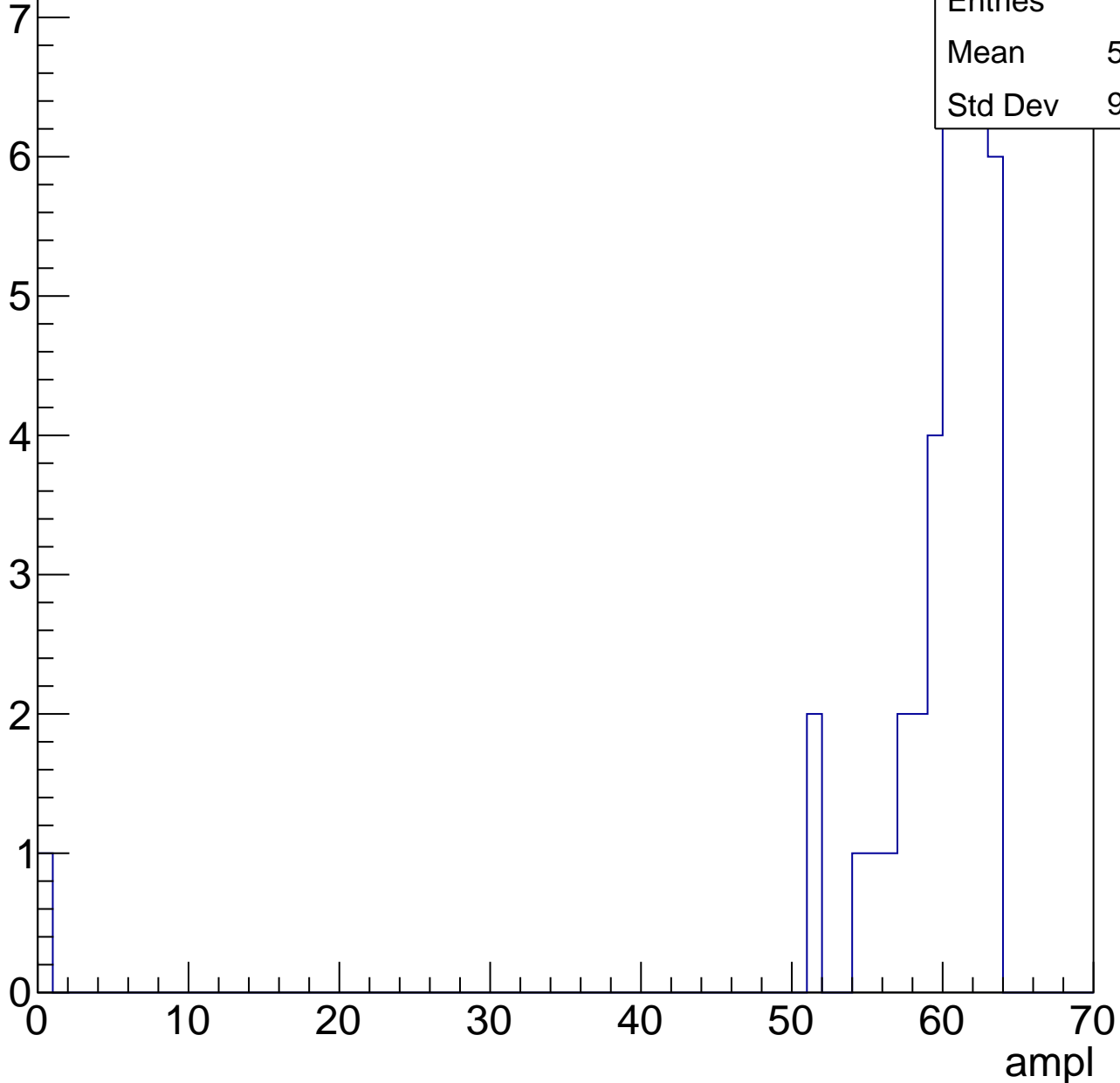


B1L102S, U17-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.34
Std Dev	9.684



B1L102S, U17-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B1L102S, U17-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U17-ch110, adc0

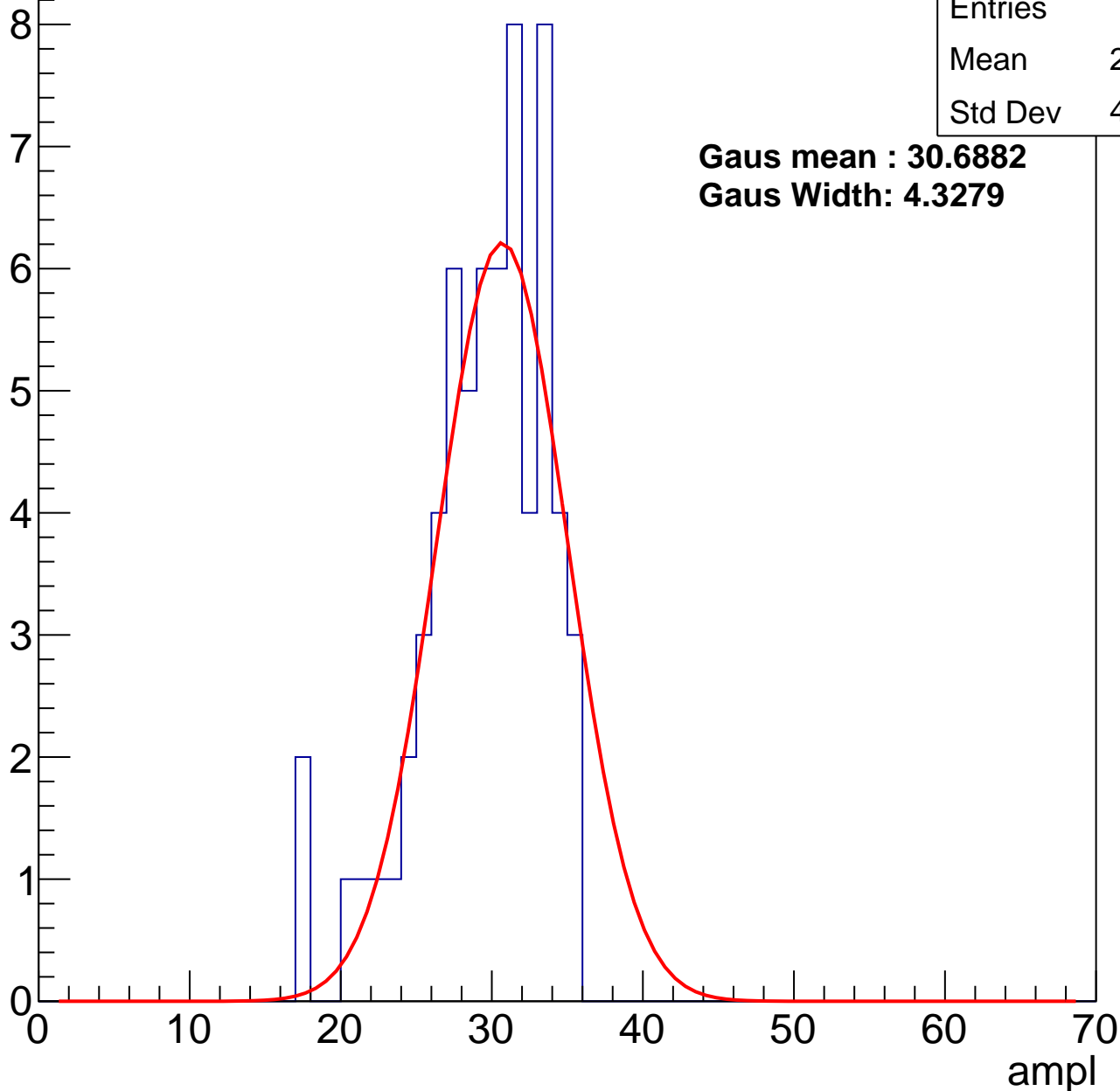
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.98
Std Dev	4.097

Gaus mean : 30.6882

Gaus Width: 4.3279



B1L102S, U17-ch110, adc1

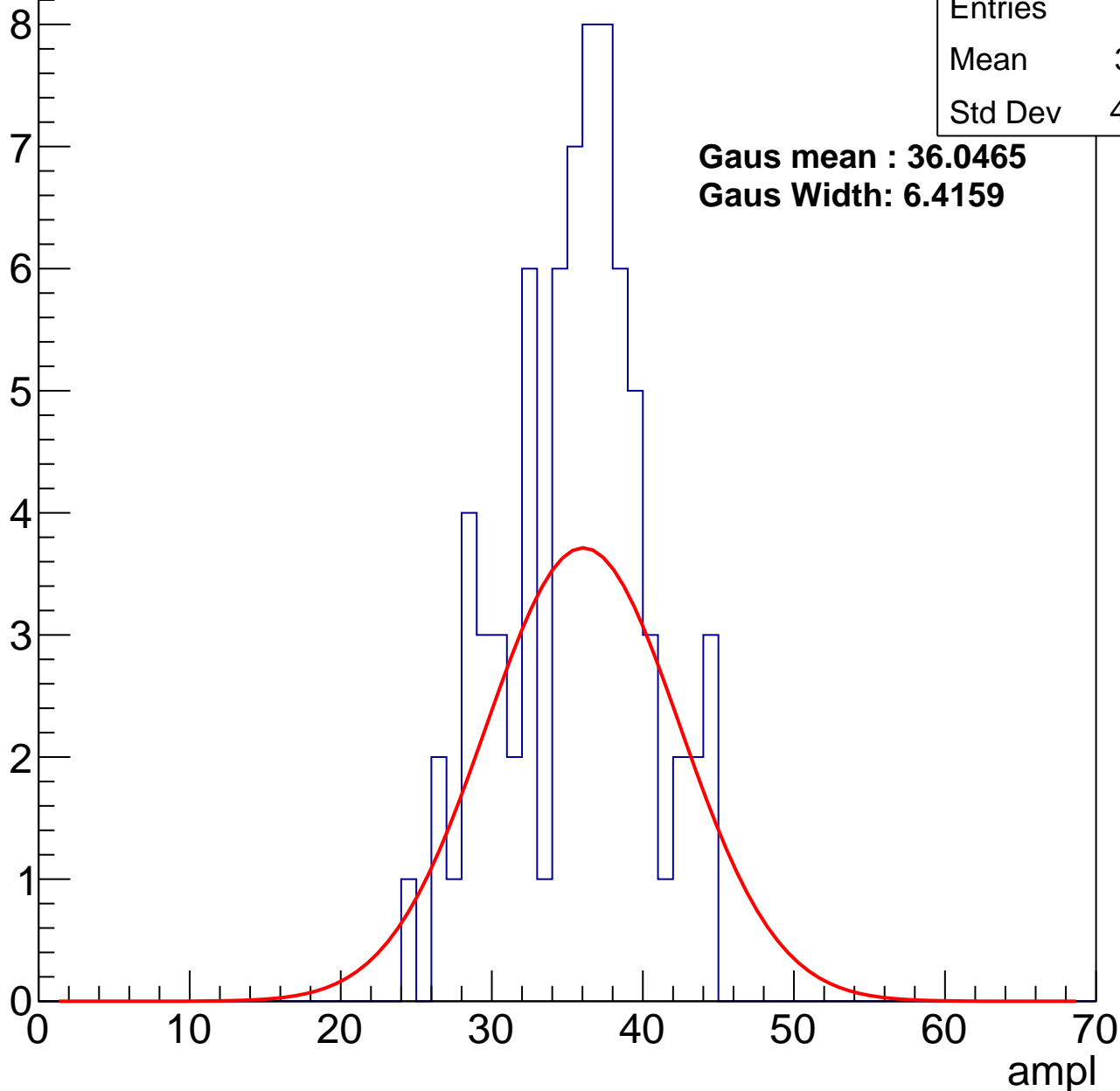
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.11
Std Dev	4.614

Gaus mean : 36.0465

Gaus Width: 6.4159



B1L102S, U17-ch110, adc2

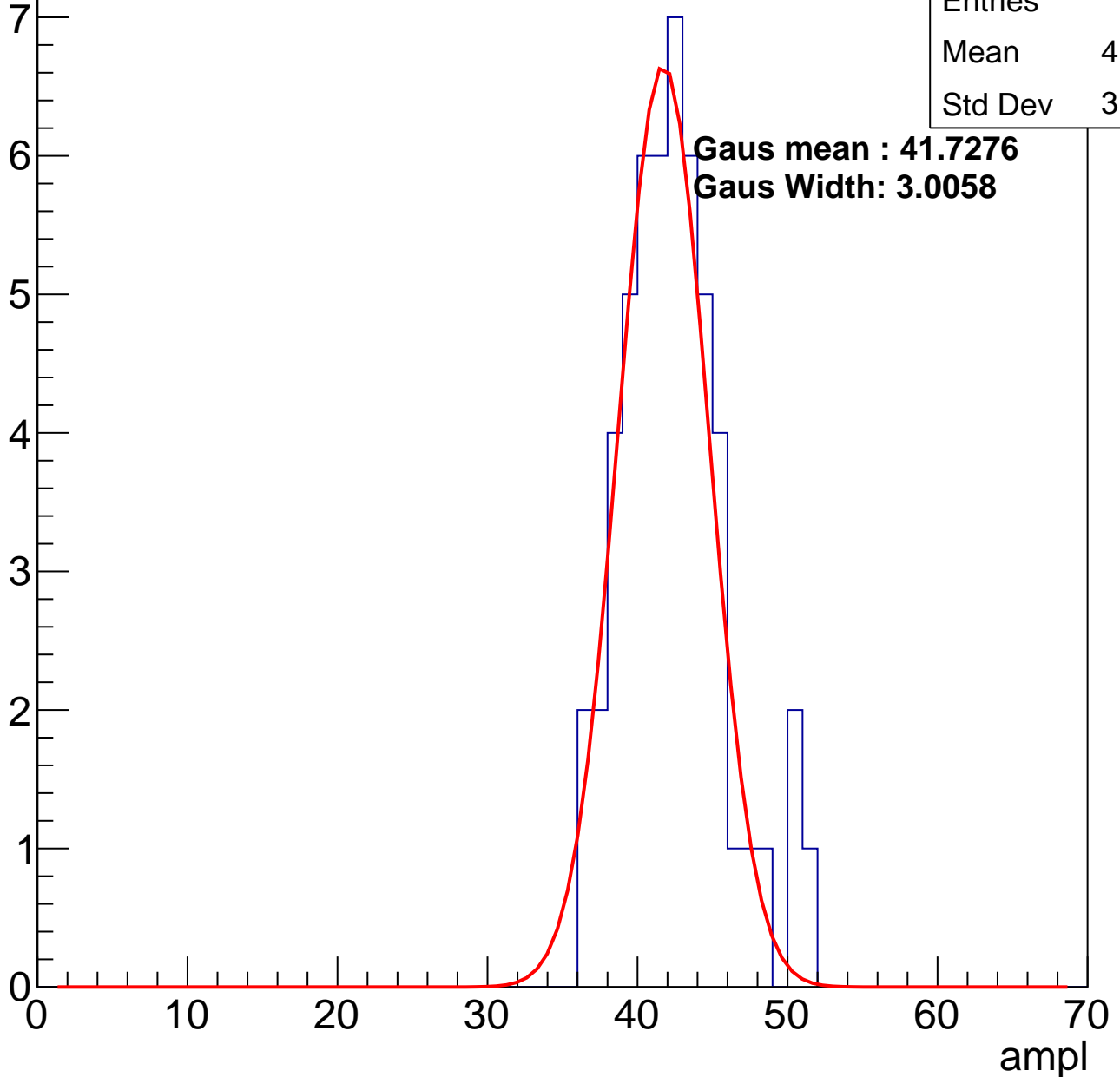
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.94
Std Dev	3.389

Gaus mean : 41.7276

Gaus Width: 3.0058

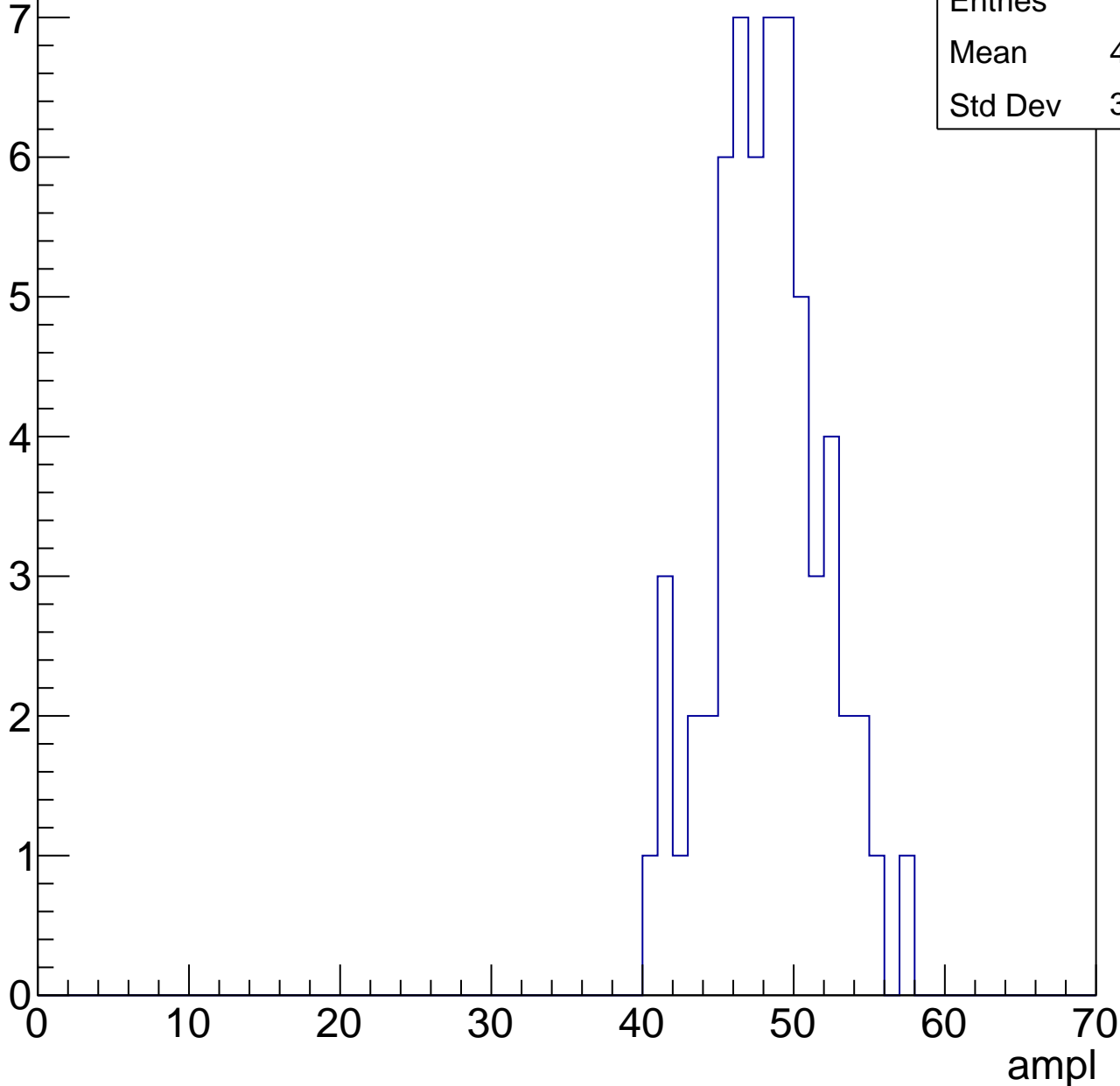


B1L102S, U17-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.82
Std Dev	3.617

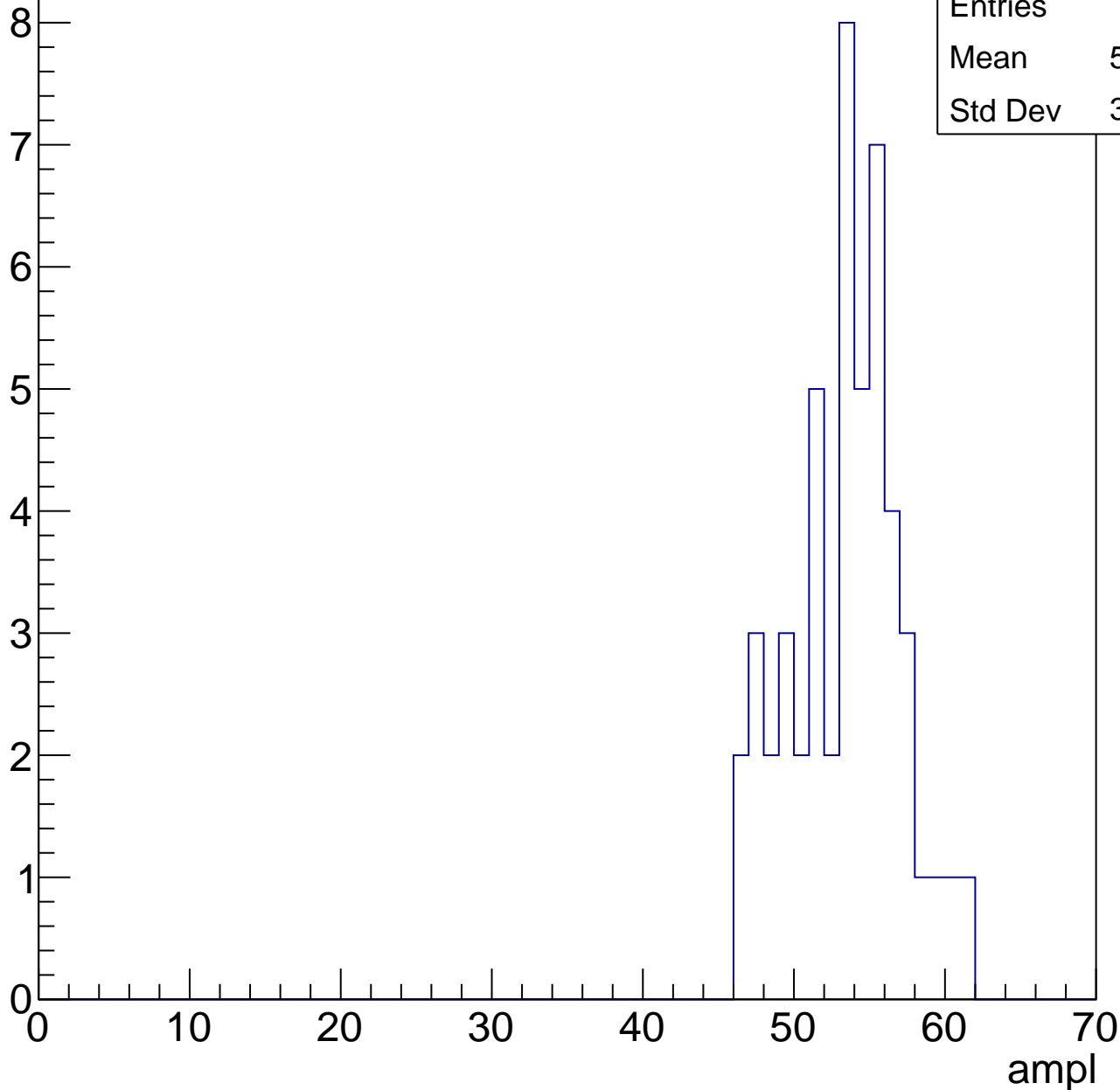


B1L102S, U17-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	52.94
Std Dev	3.563

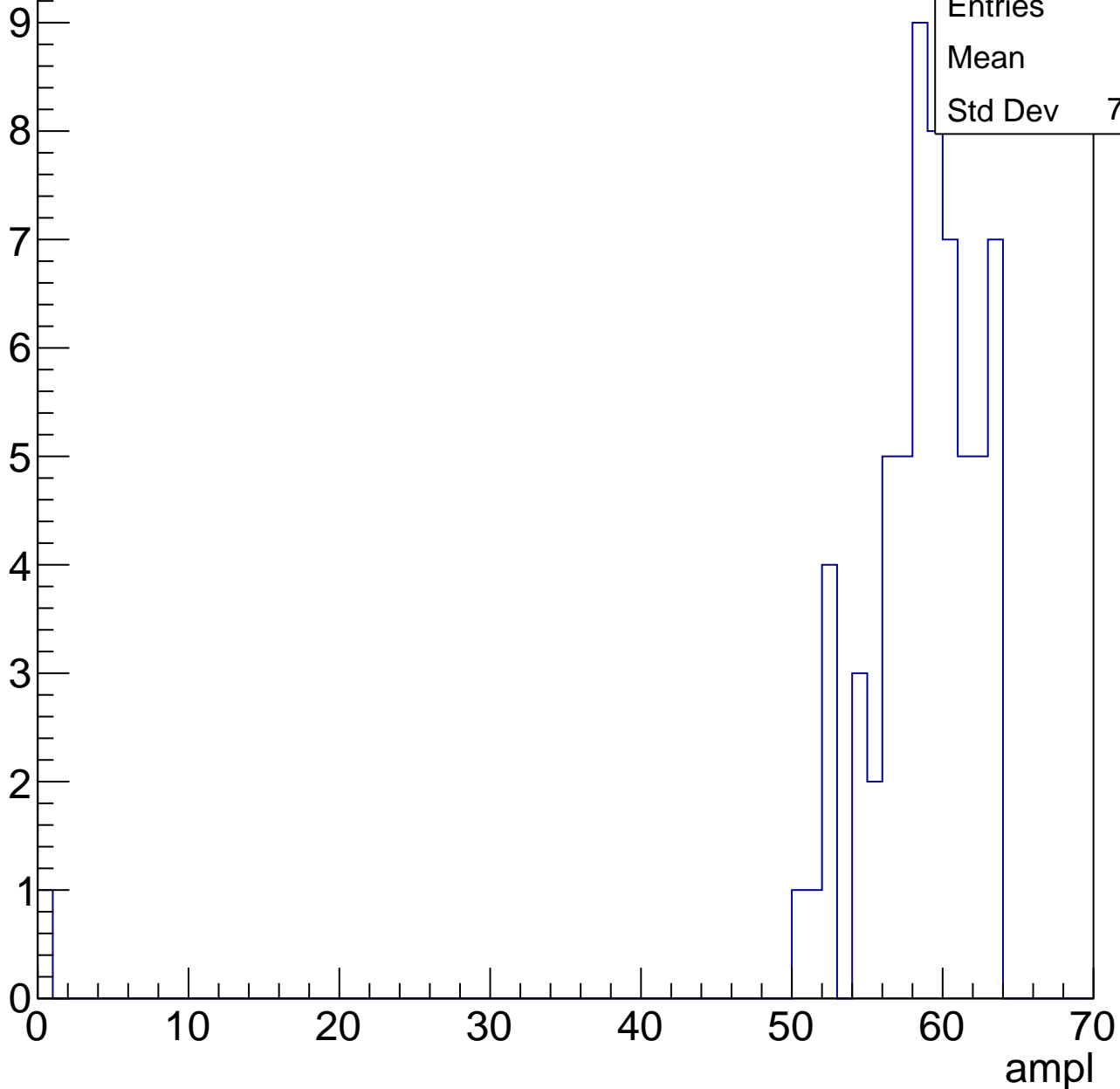


B1L102S, U17-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	57.4
Std Dev	7.993

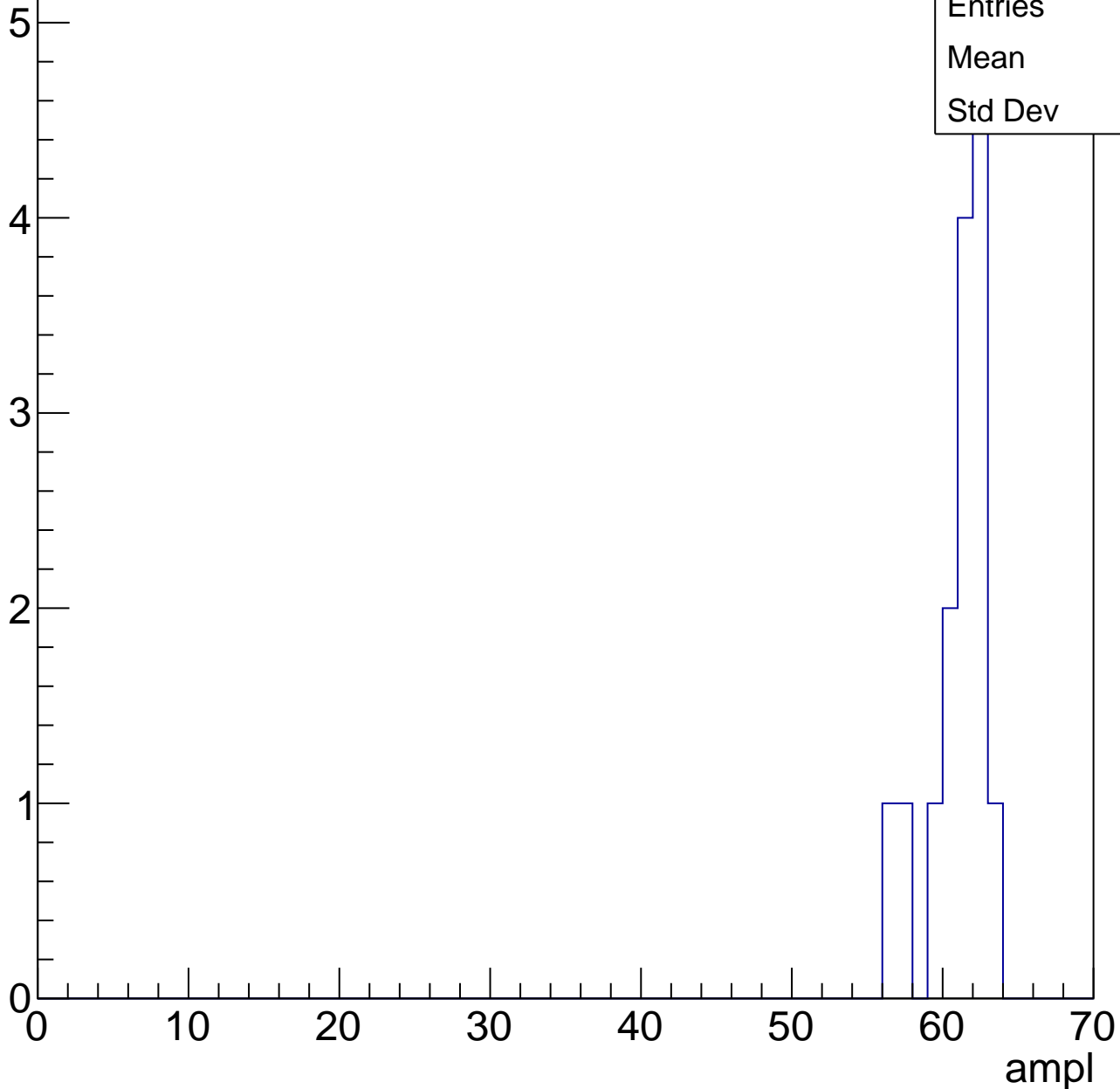


B1L102S, U17-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	60.6
Std Dev	1.89



B1L102S, U17-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	31.5
Std Dev	31.5

B1L102S, U17-ch111, adc0

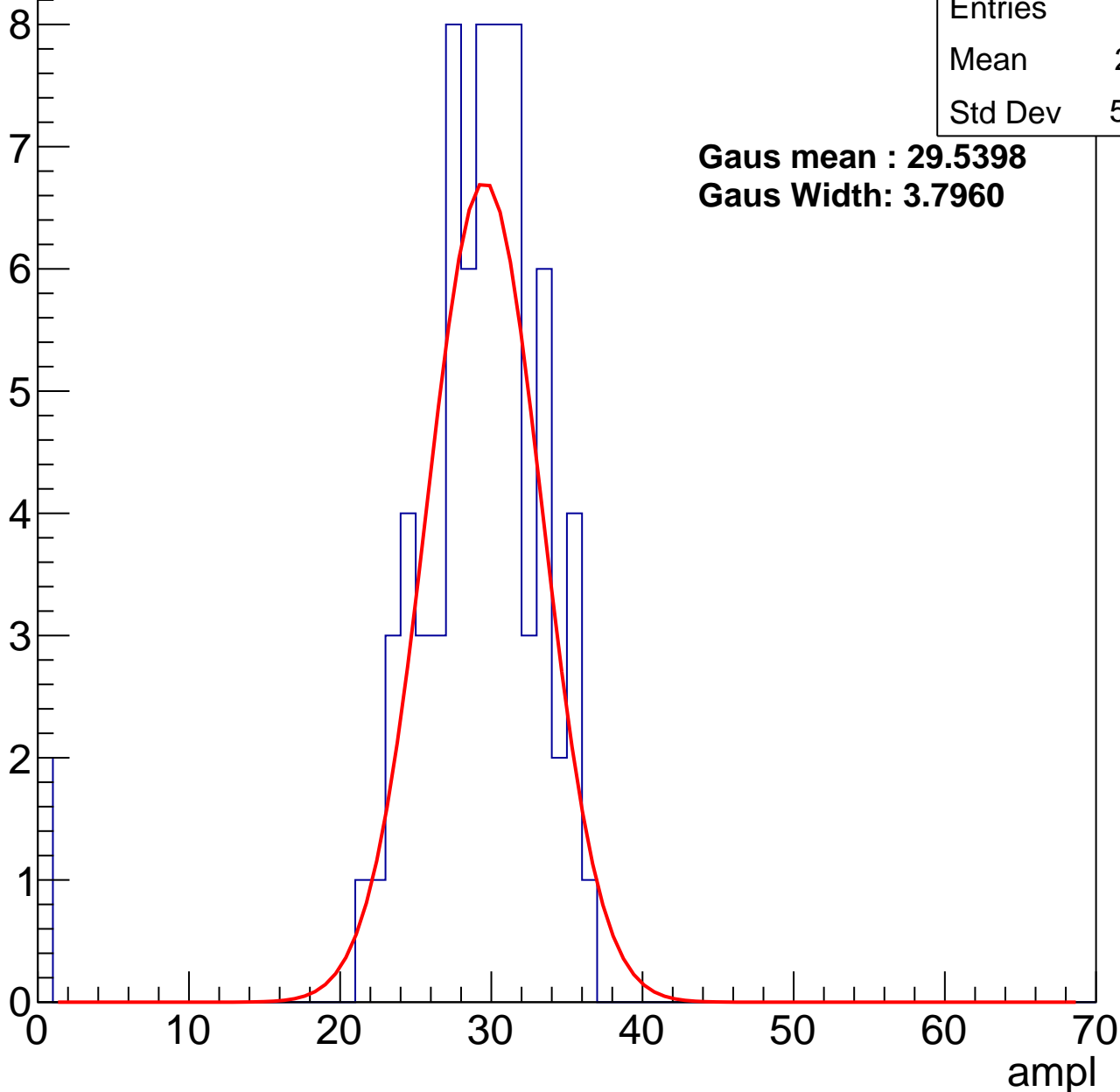
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.21
Std Dev	5.908

Gaus mean : 29.5398

Gaus Width: 3.7960



B1L102S, U17-ch111, adc1

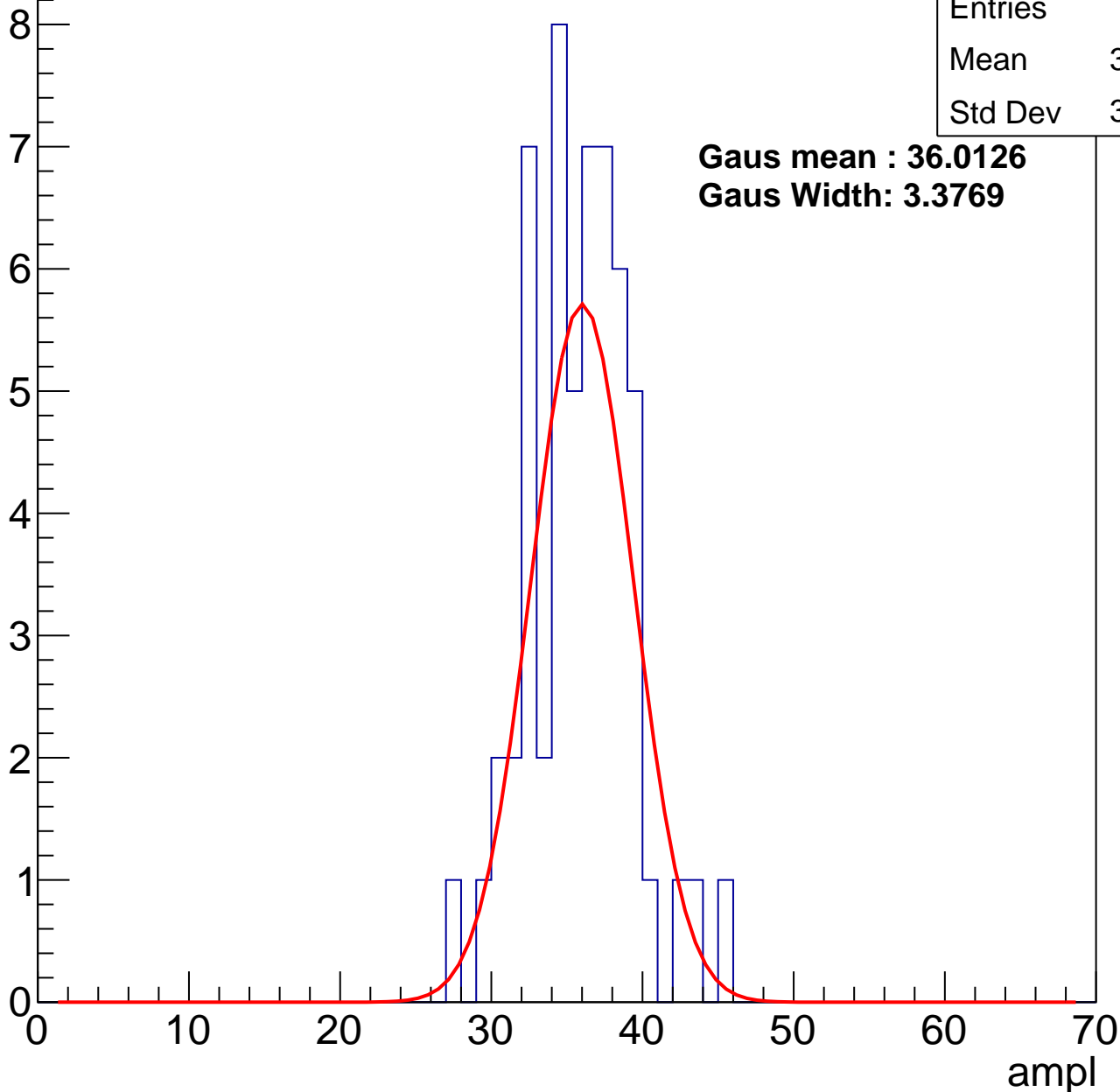
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.42
Std Dev	3.397

Gaus mean : 36.0126

Gaus Width: 3.3769



B1L102S, U17-ch111, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	41.78
Std Dev	3.81

Gaus mean : 41.6498

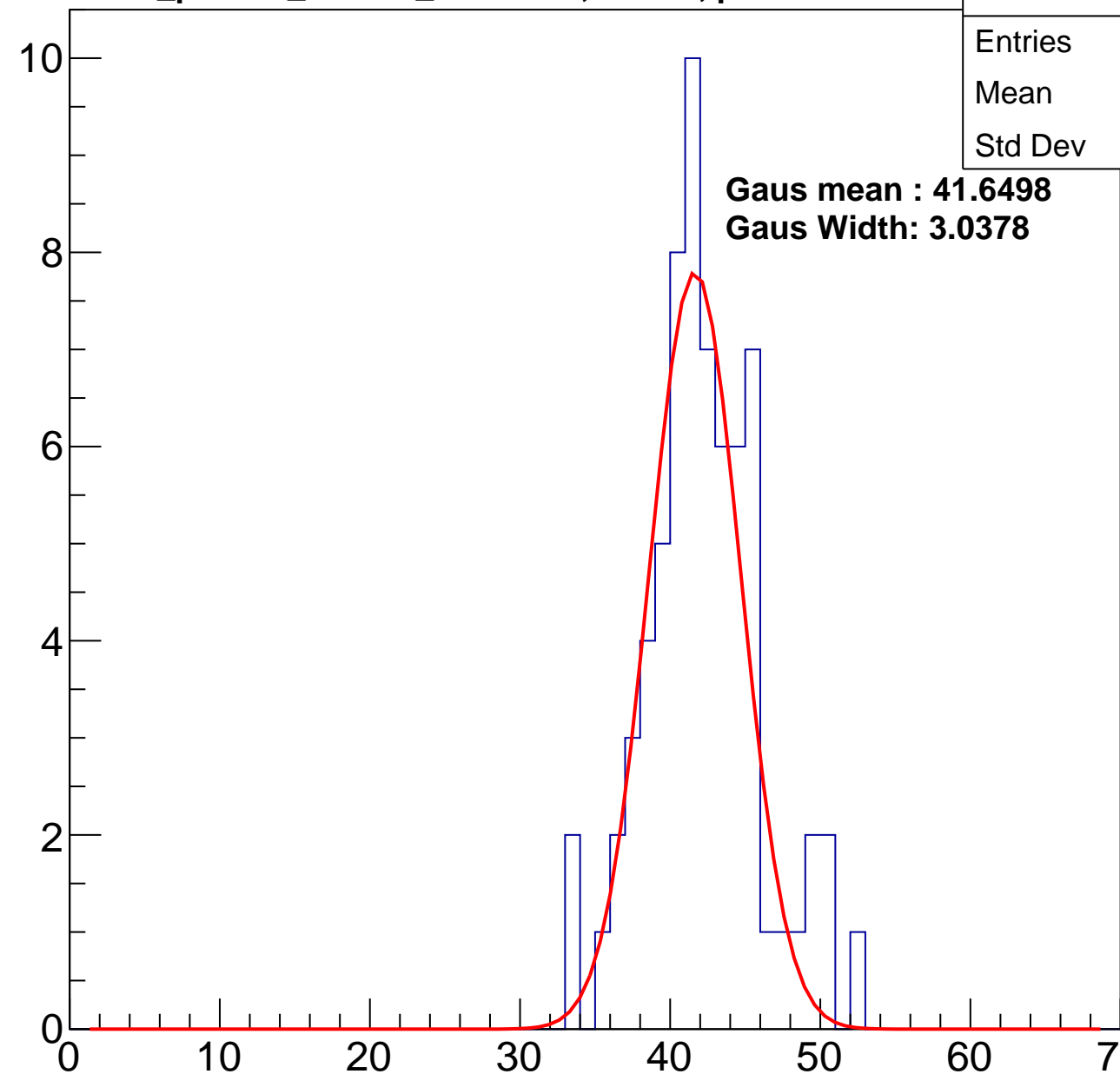
Gaus Width: 3.0378

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

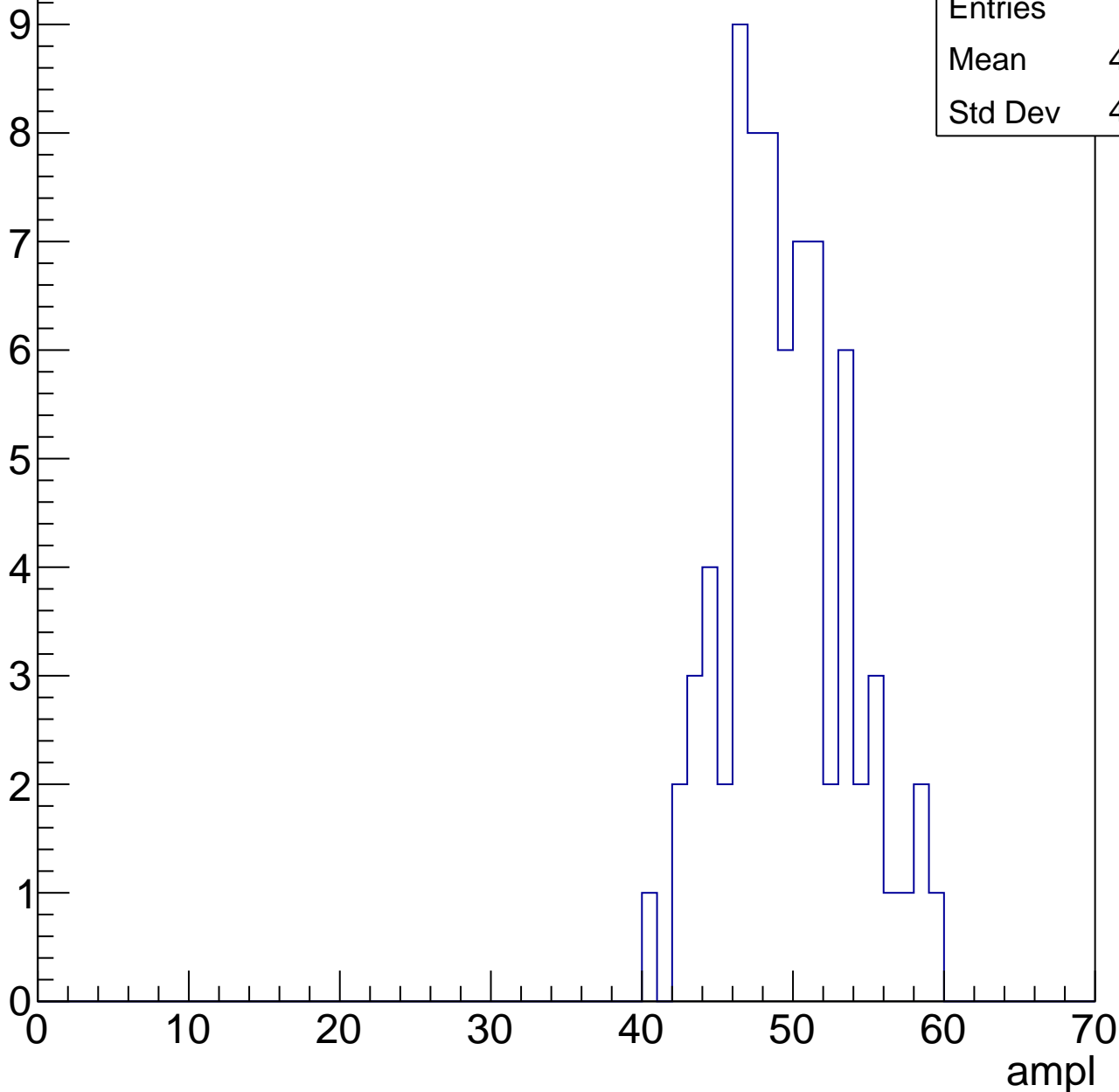


B1L102S, U17-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	49.03
Std Dev	4.036

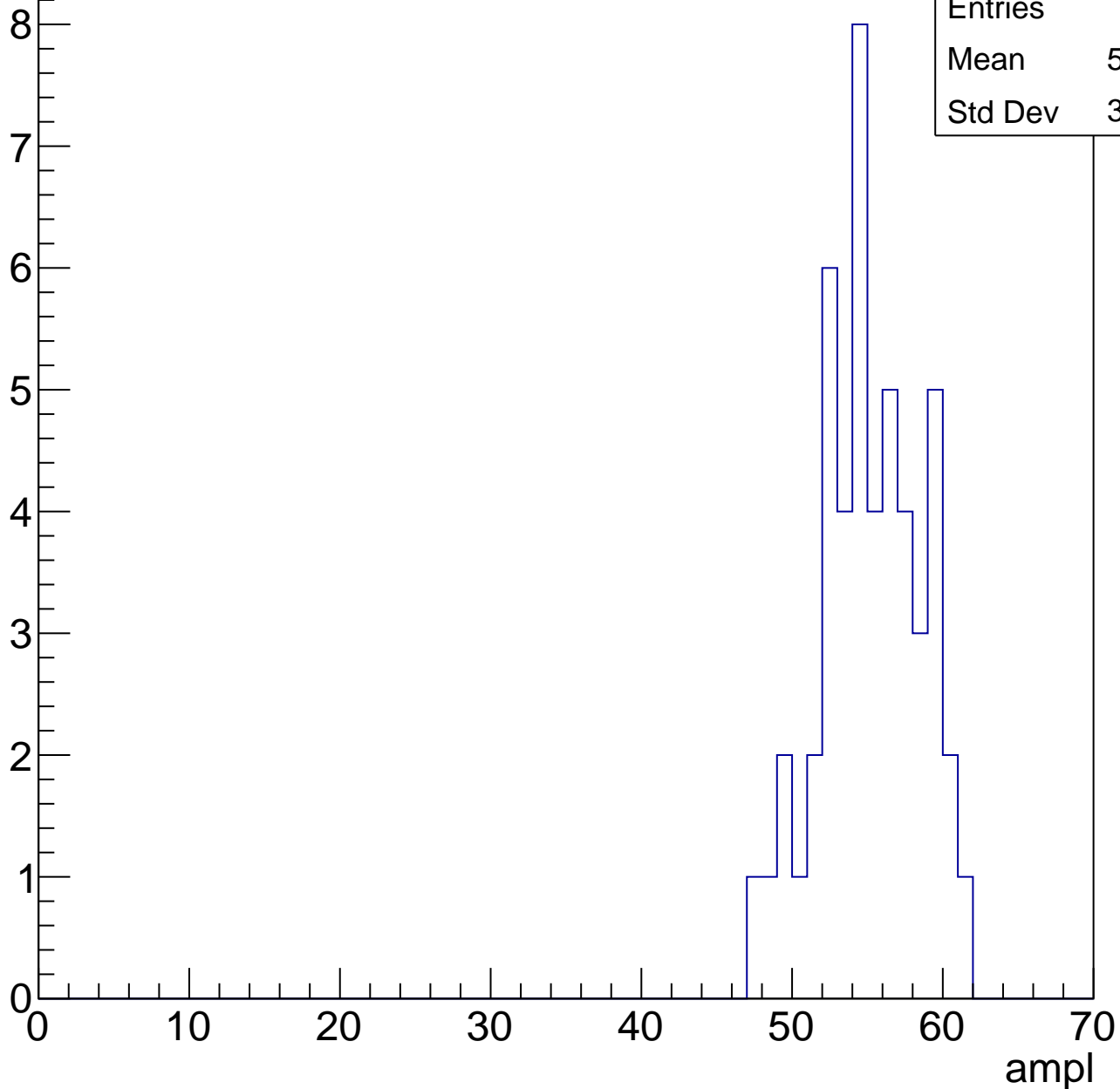


B1L102S, U17-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.67
Std Dev	3.285

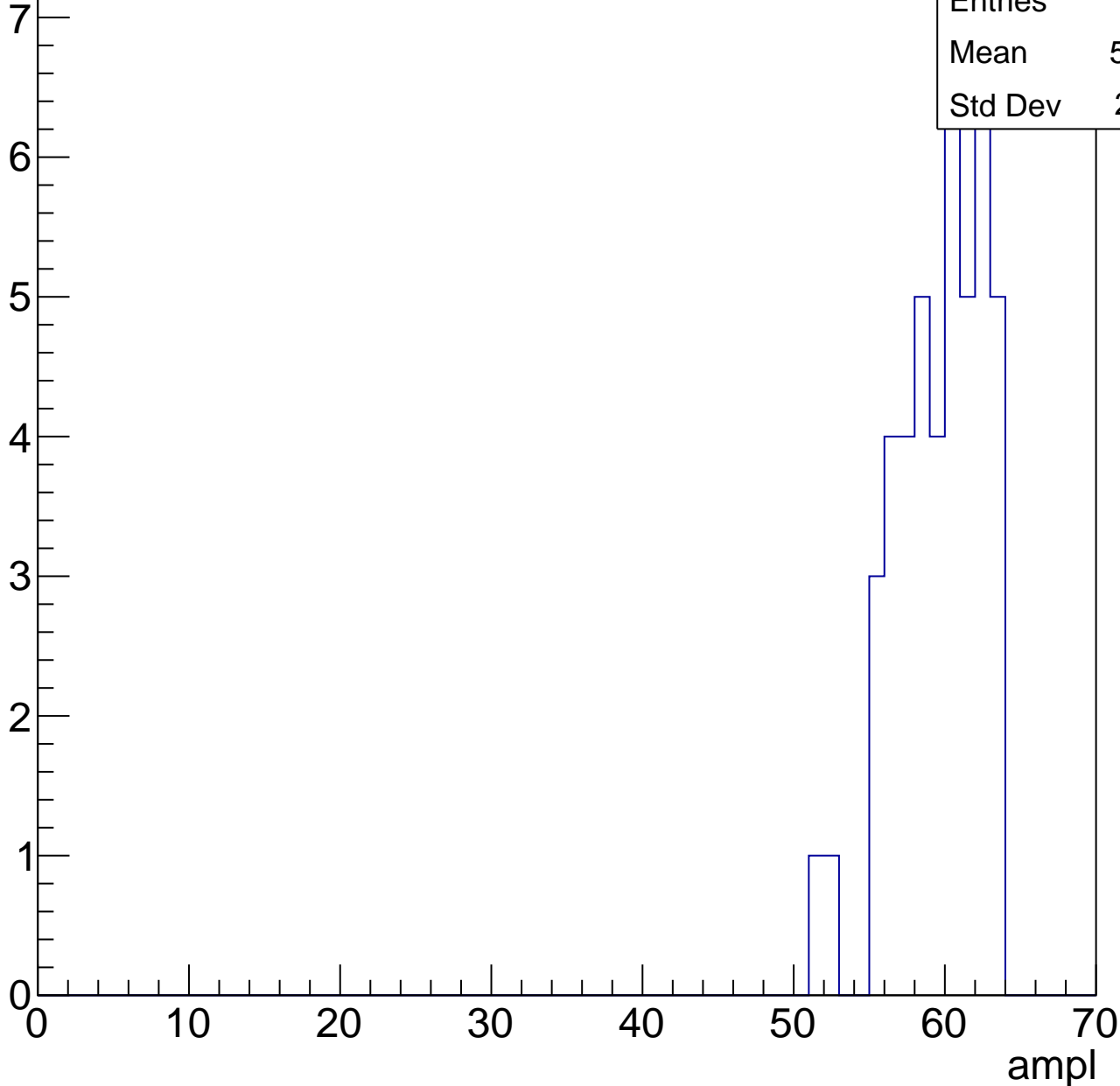


B1L102S, U17-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.13
Std Dev	2.901

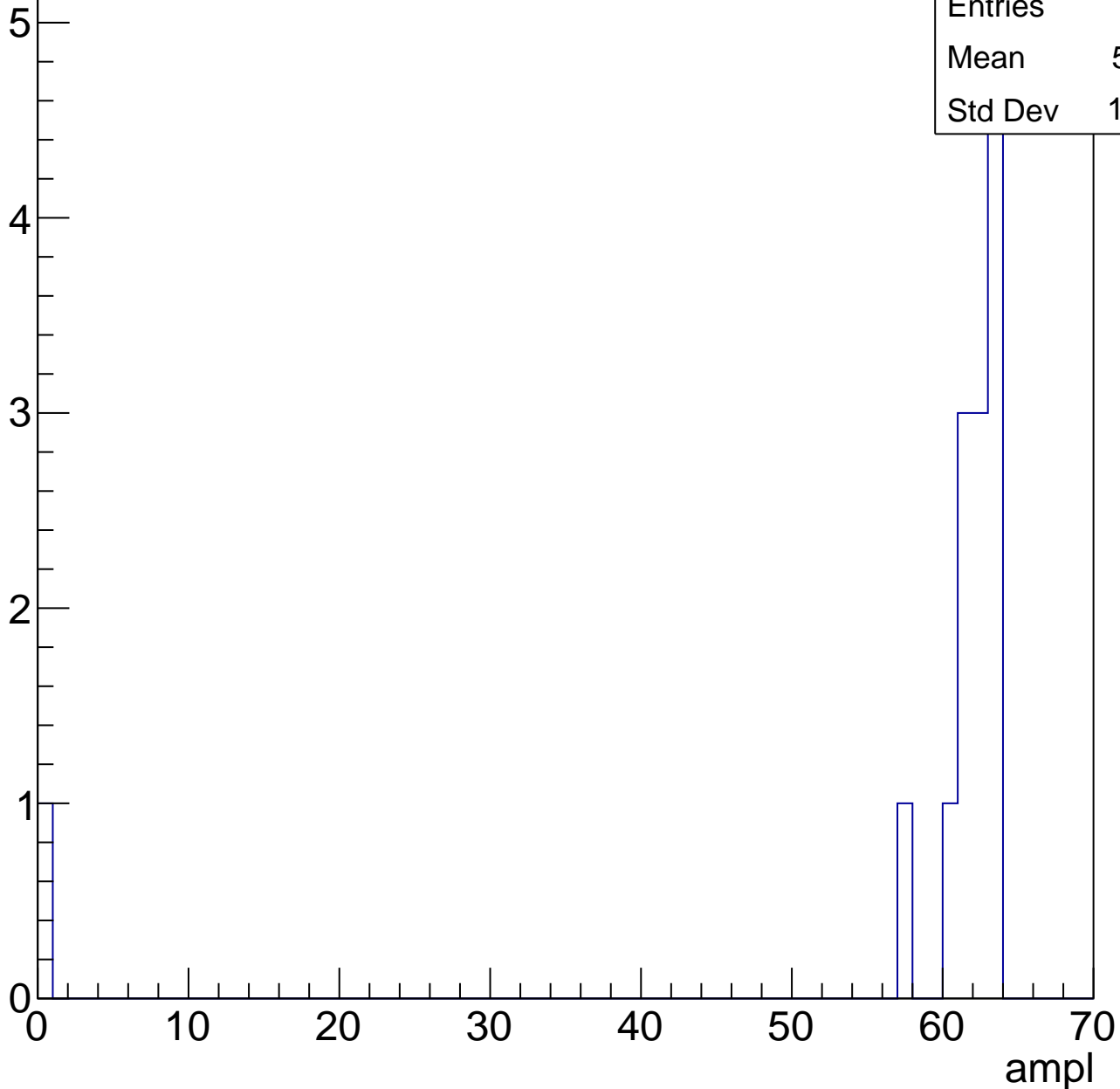


B1L102S, U17-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.21
Std Dev	15.95



B1L102S, U17-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L102S, U17-ch112, adc0

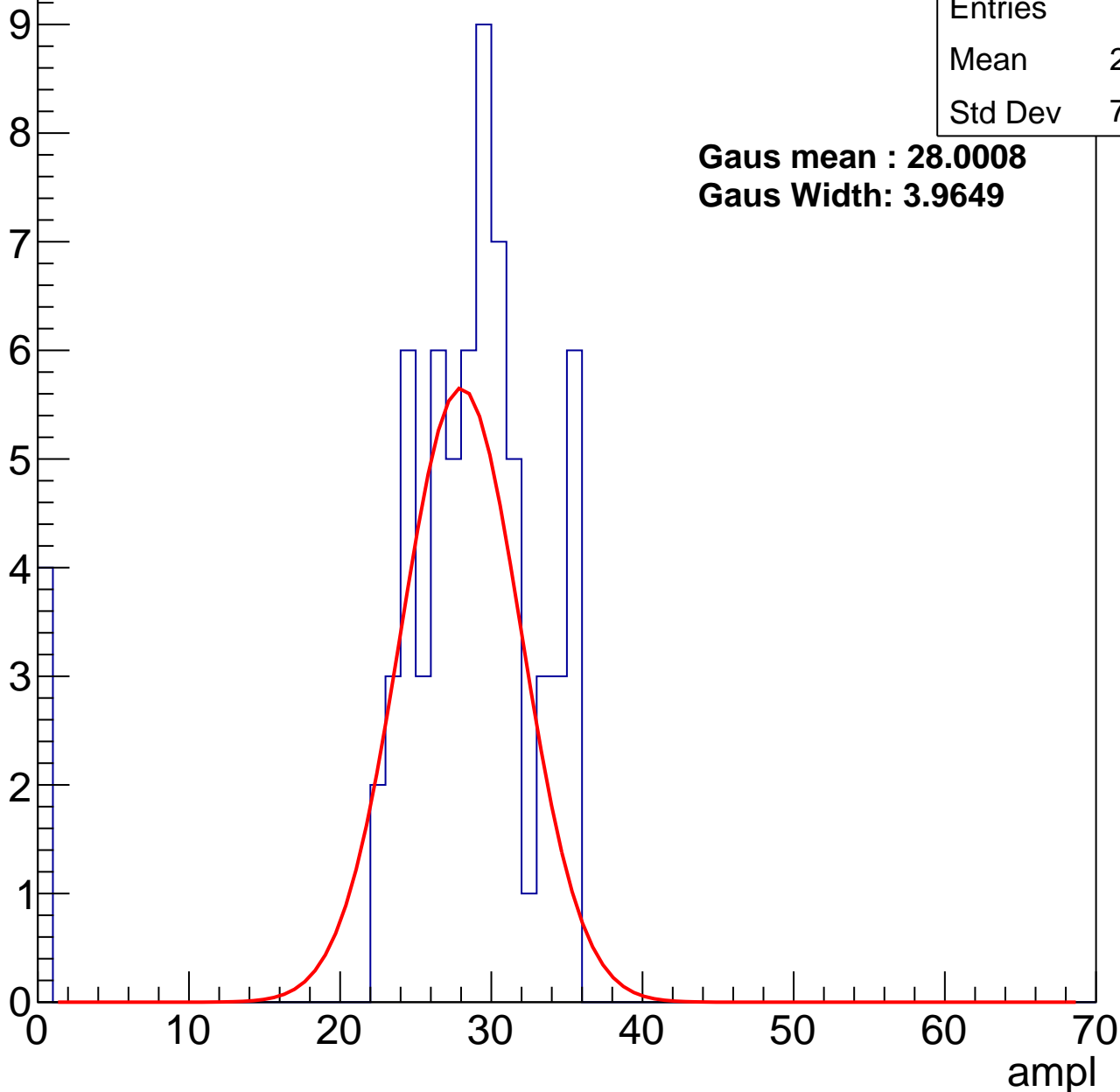
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	26.96
Std Dev	7.552

Gaus mean : 28.0008

Gaus Width: 3.9649



B1L102S, U17-ch112, adc1

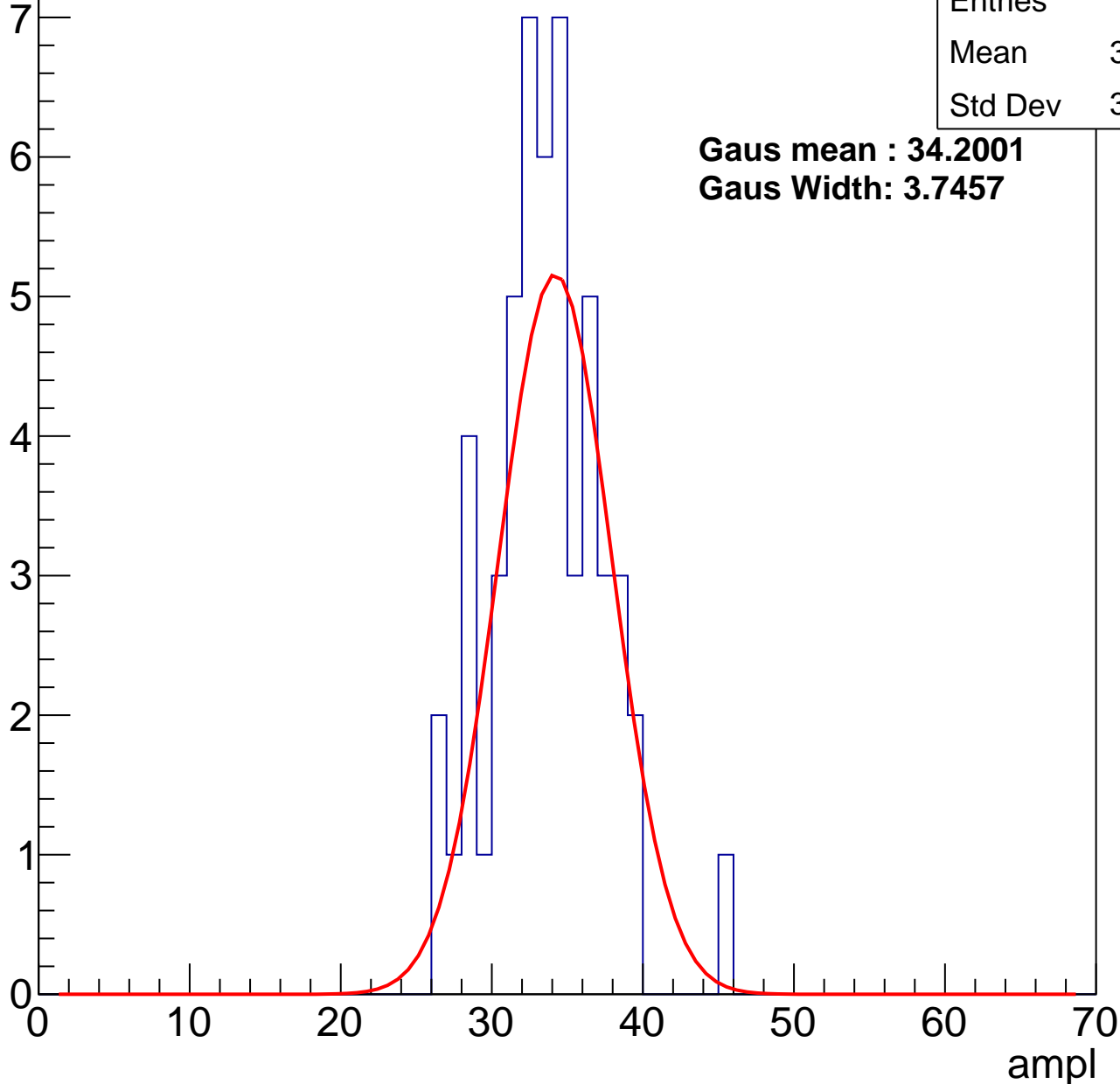
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	33.17
Std Dev	3.648

Gaus mean : 34.2001

Gaus Width: 3.7457



B1L102S, U17-ch112, adc2

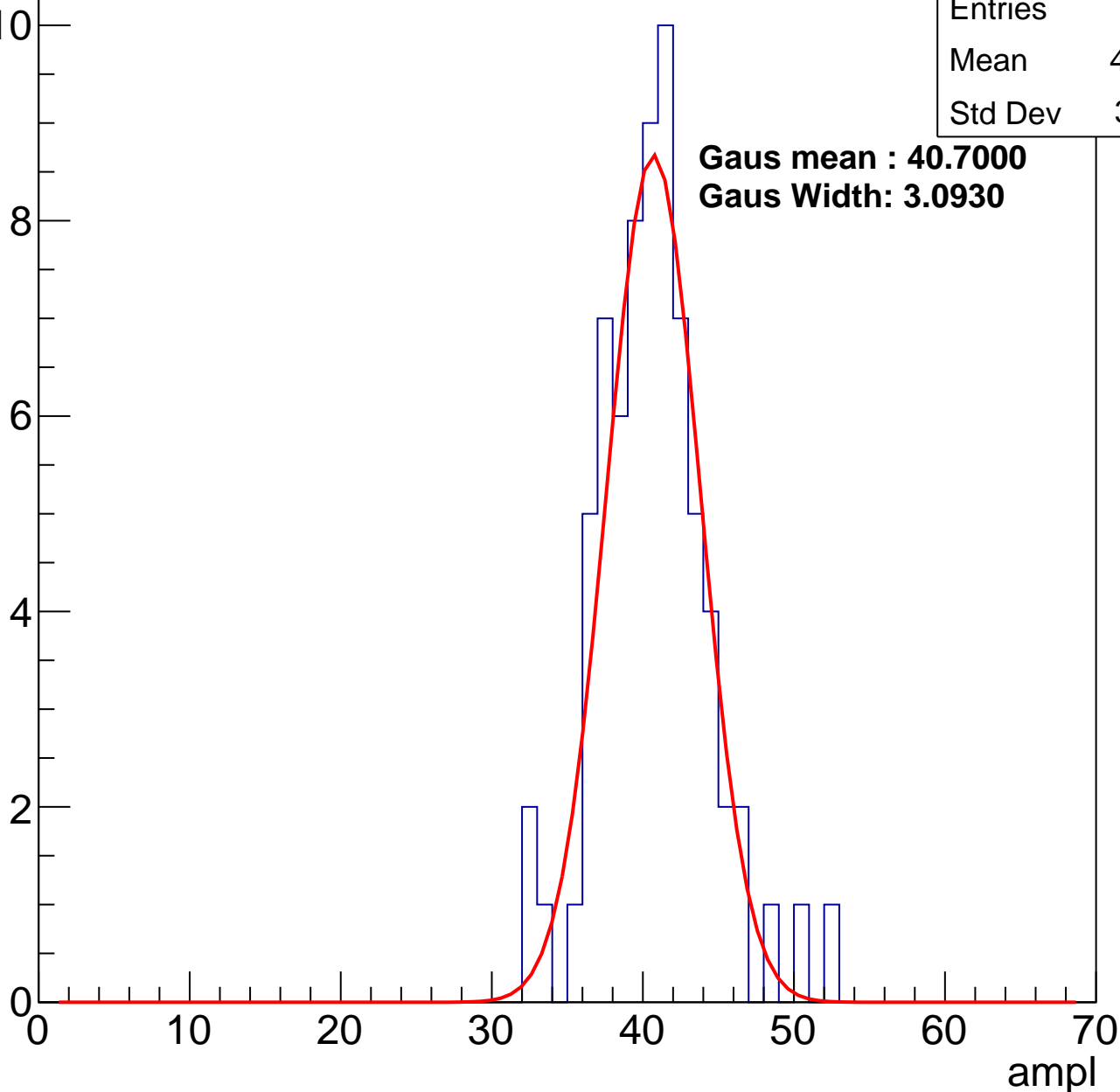
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	40.25
Std Dev	3.631

Gaus mean : 40.7000

Gaus Width: 3.0930

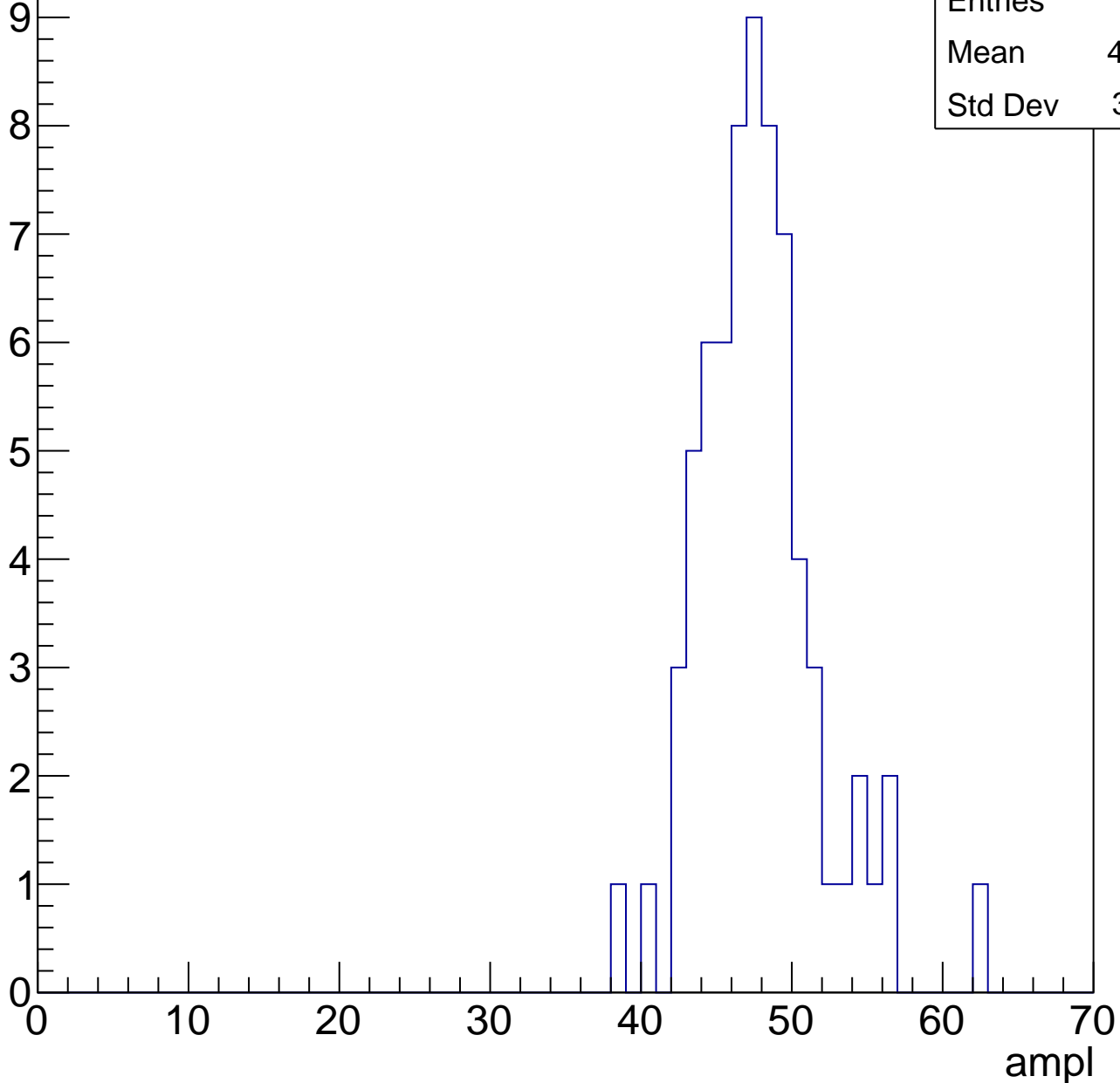


B1L102S, U17-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.33
Std Dev	3.981

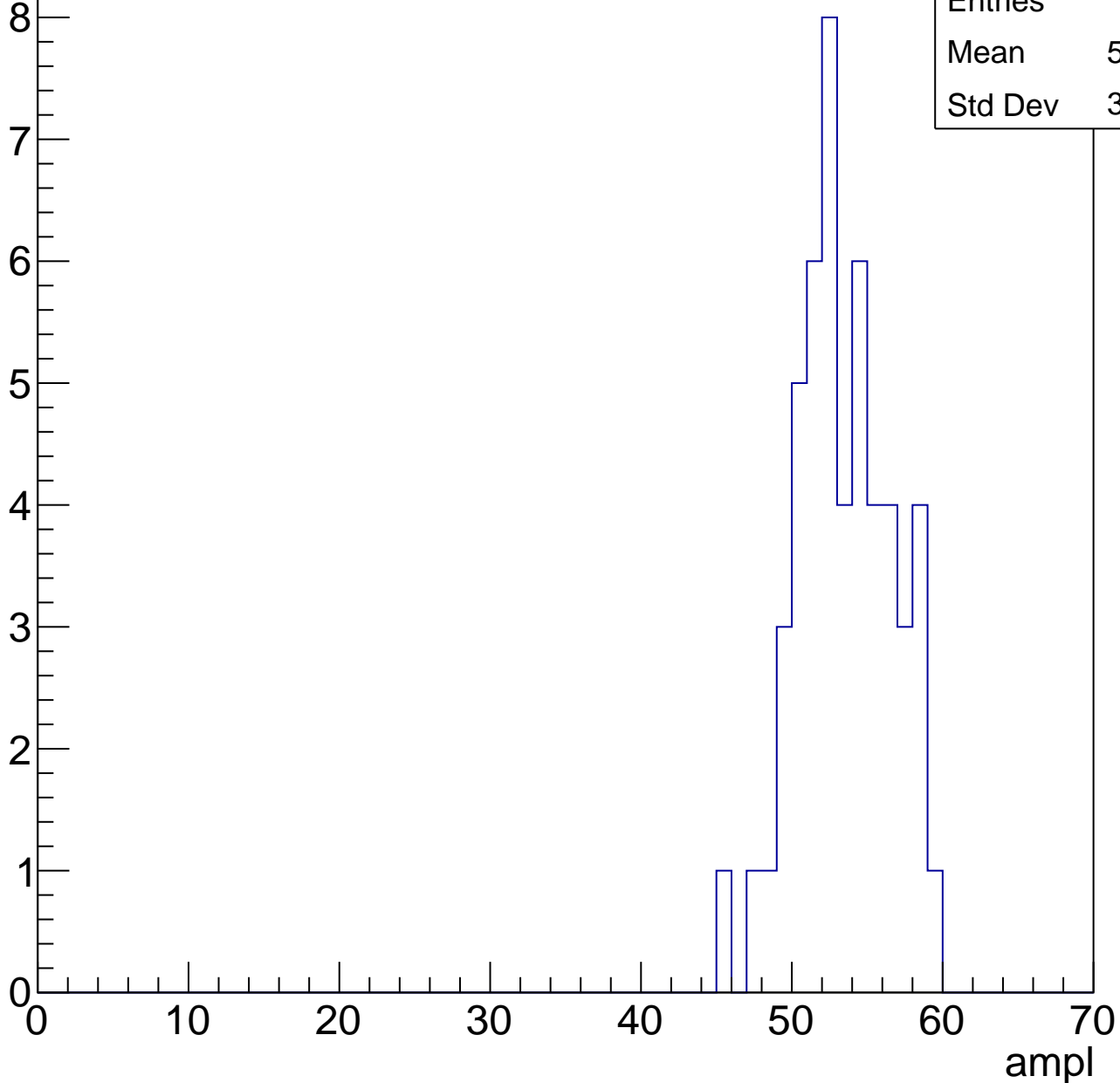


B1L102S, U17-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	52.96
Std Dev	3.106

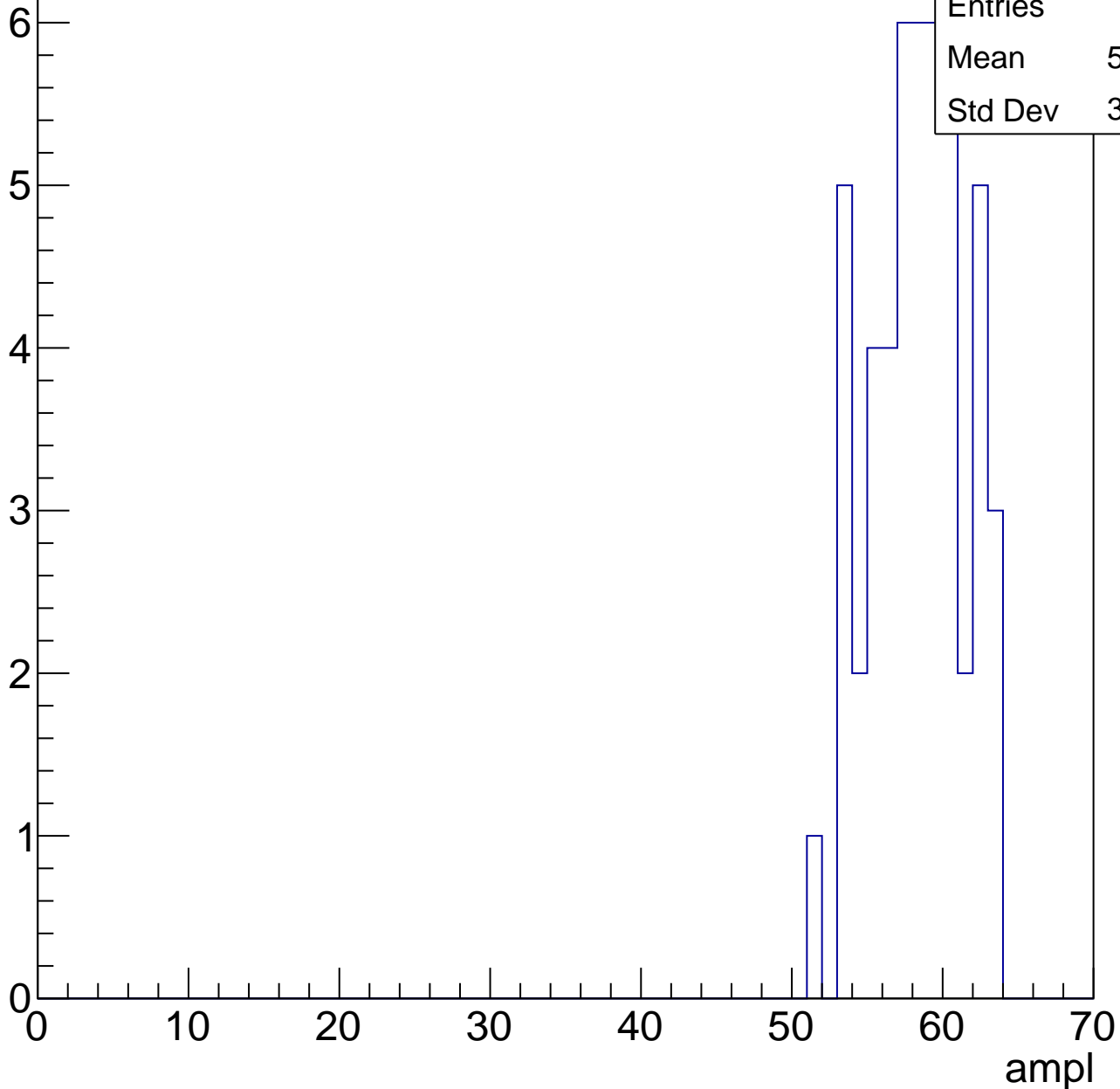


B1L102S, U17-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.86
Std Dev	3.053

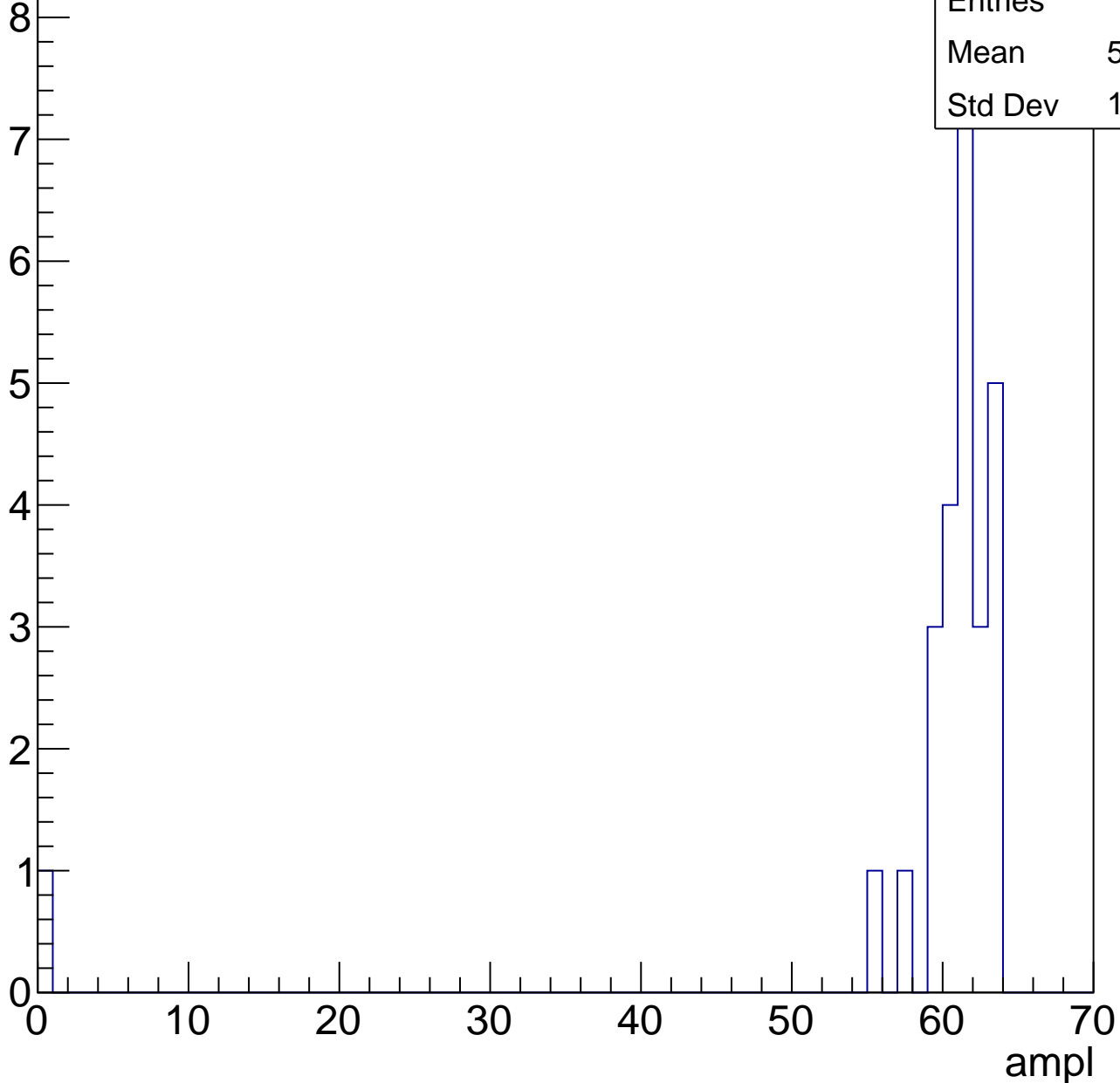


B1L102S, U17-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

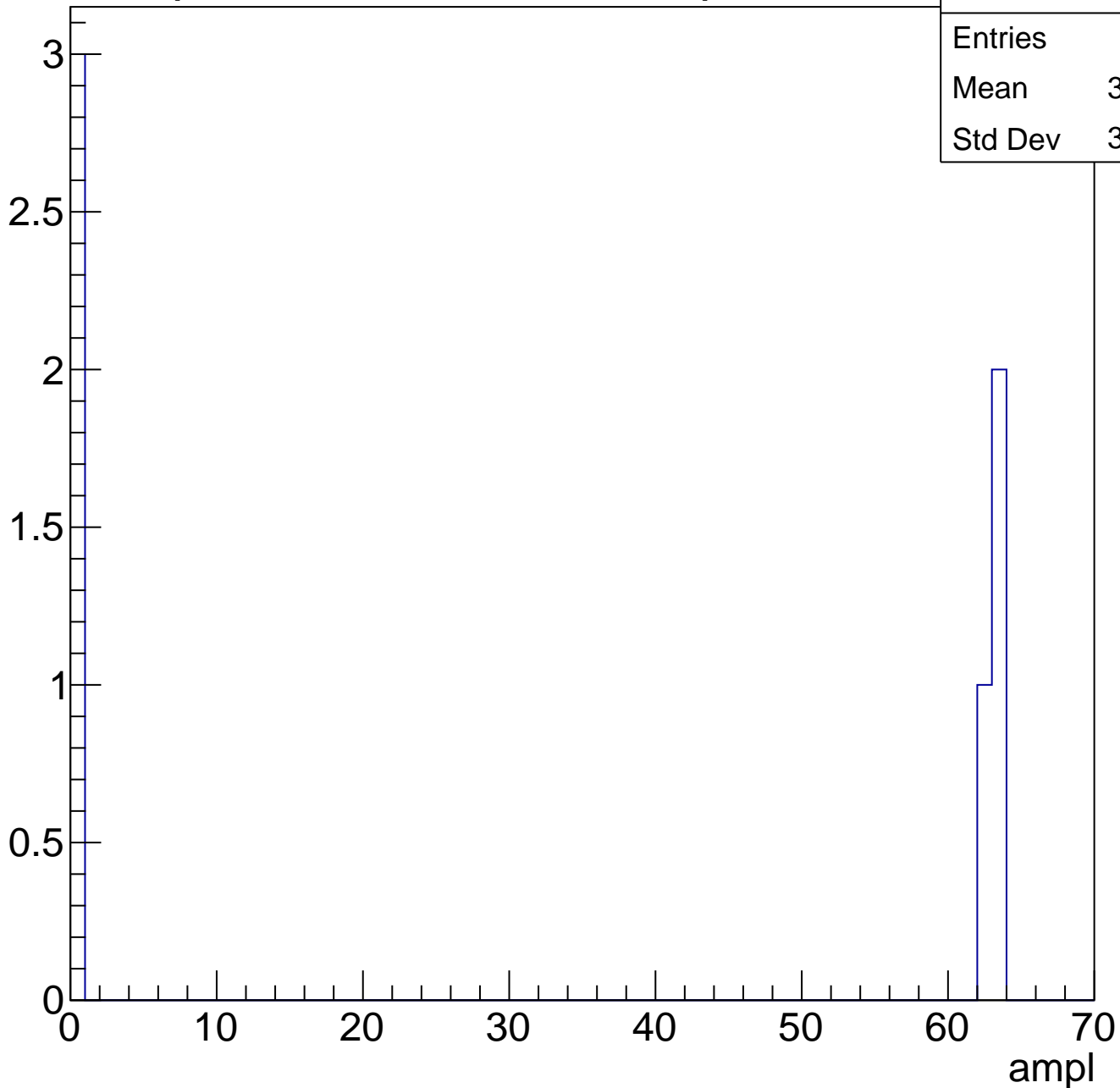
Entries	26
Mean	58.38
Std Dev	11.82



B1L102S, U17-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch113, adc0

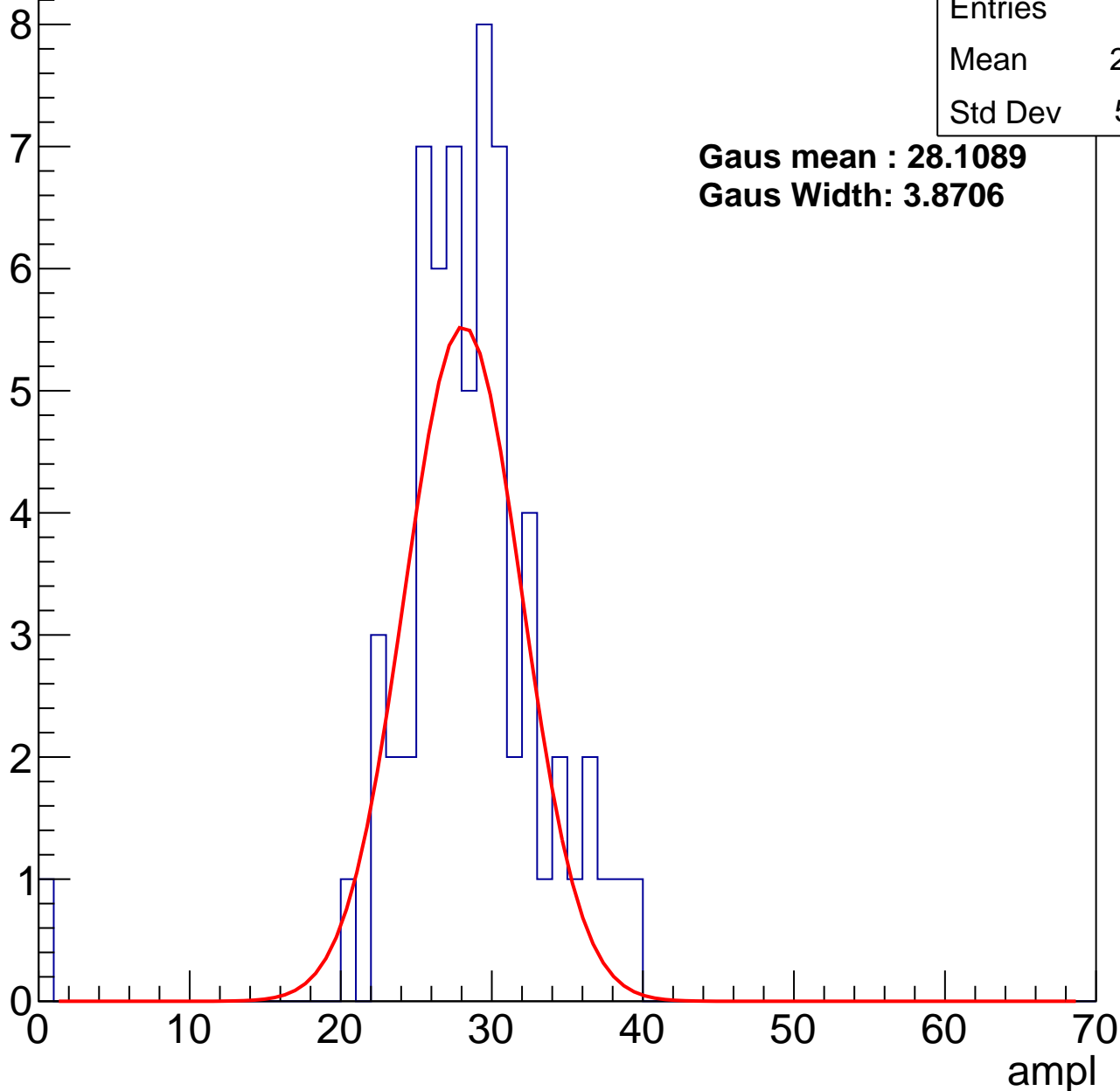
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.03
Std Dev	5.341

Gaus mean : 28.1089

Gaus Width: 3.8706



B1L102S, U17-ch113, adc1

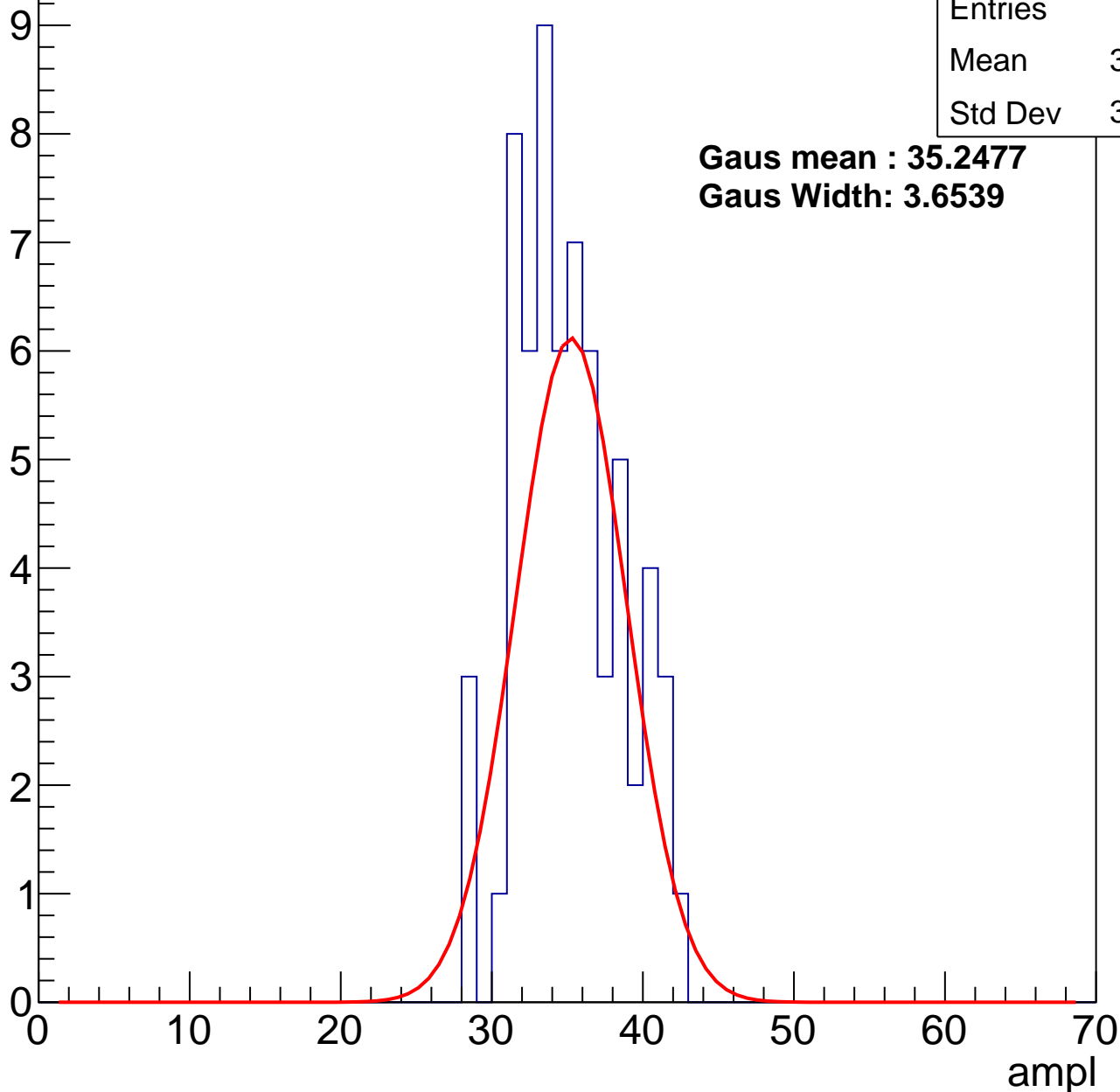
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	34.69
Std Dev	3.404

Gaus mean : 35.2477

Gaus Width: 3.6539



B1L102S, U17-ch113, adc2

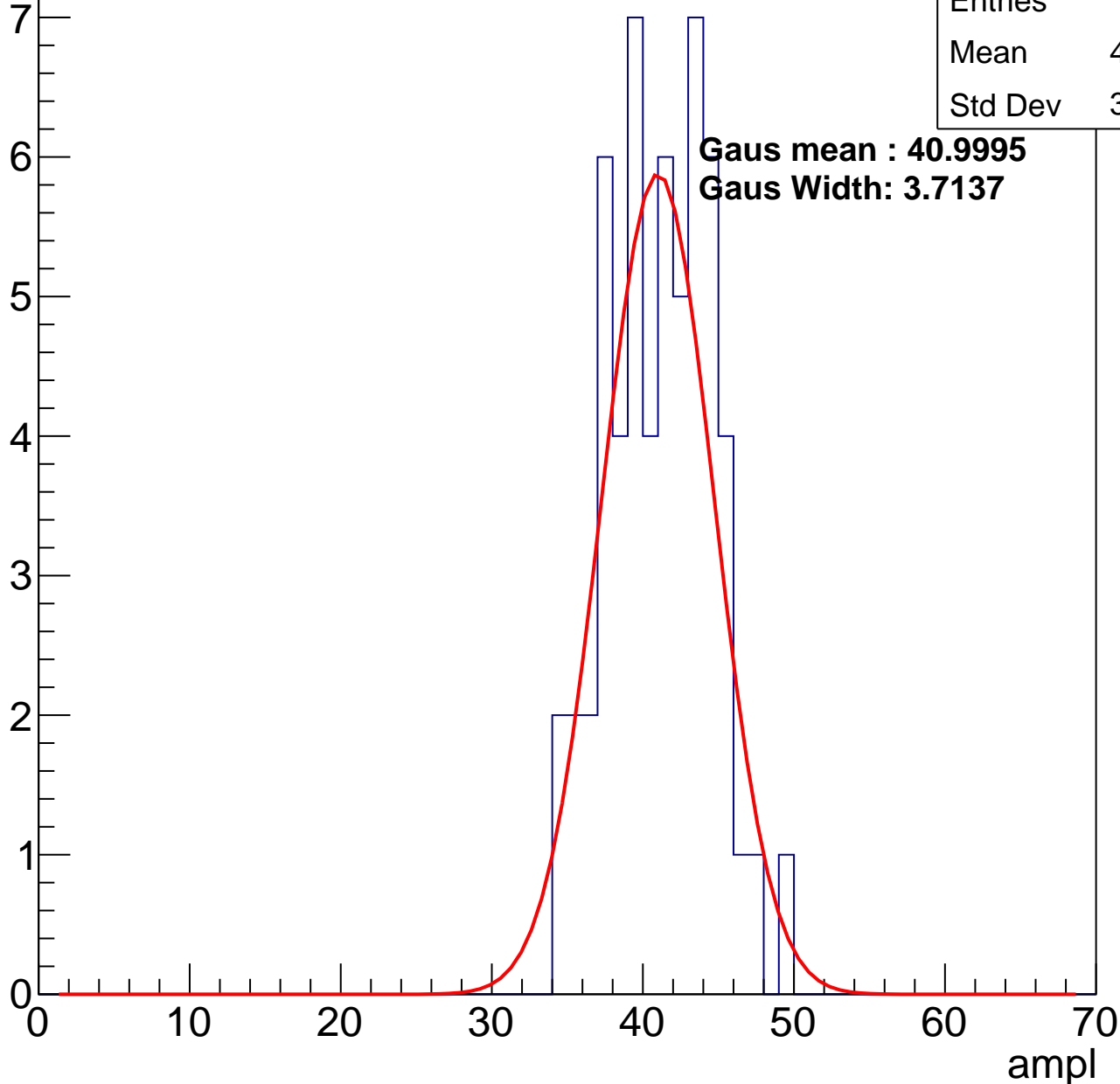
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	40.69
Std Dev	3.359

Gaus mean : 40.9995

Gaus Width: 3.7137

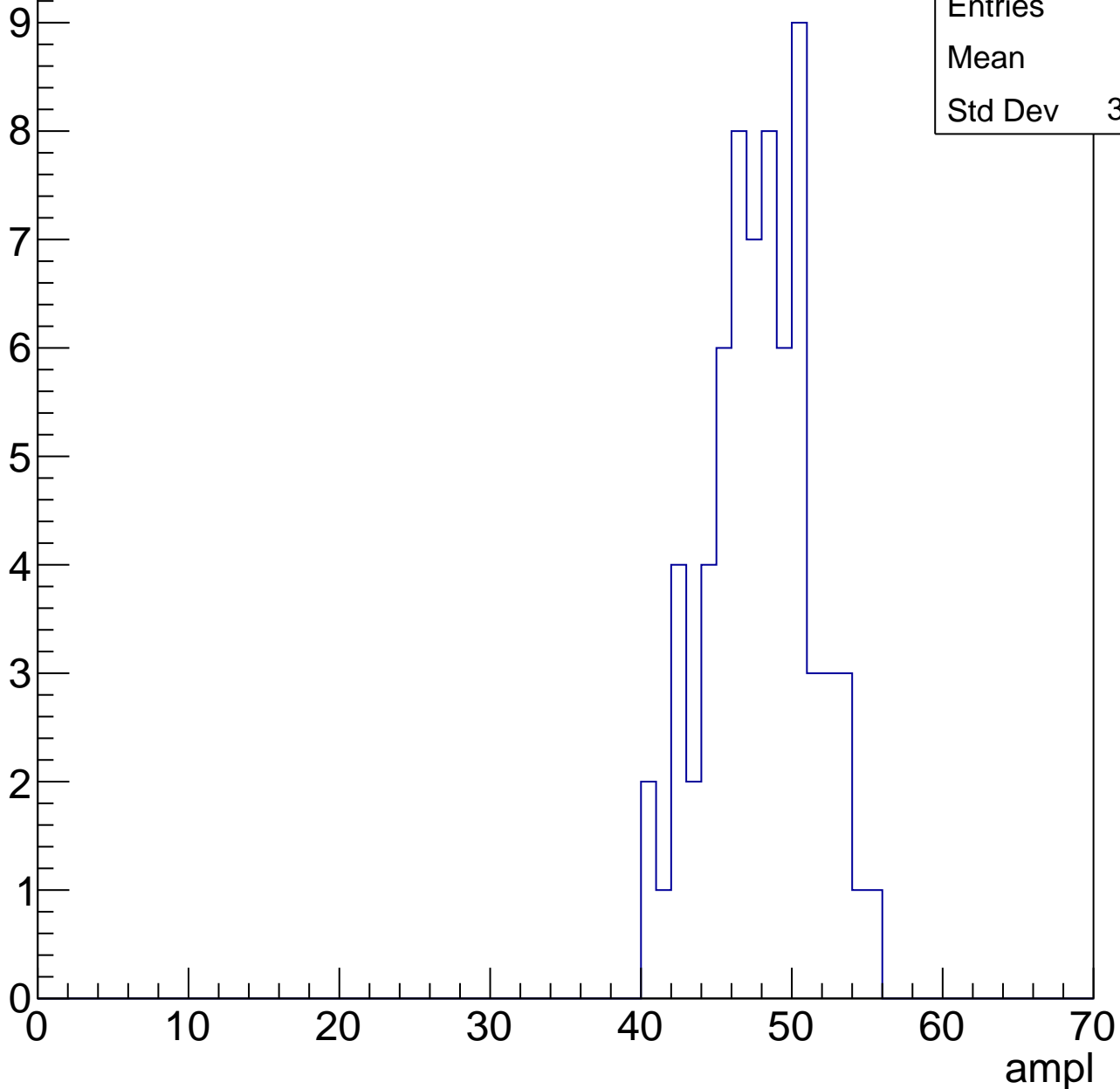


B1L102S, U17-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	47.4
Std Dev	3.387

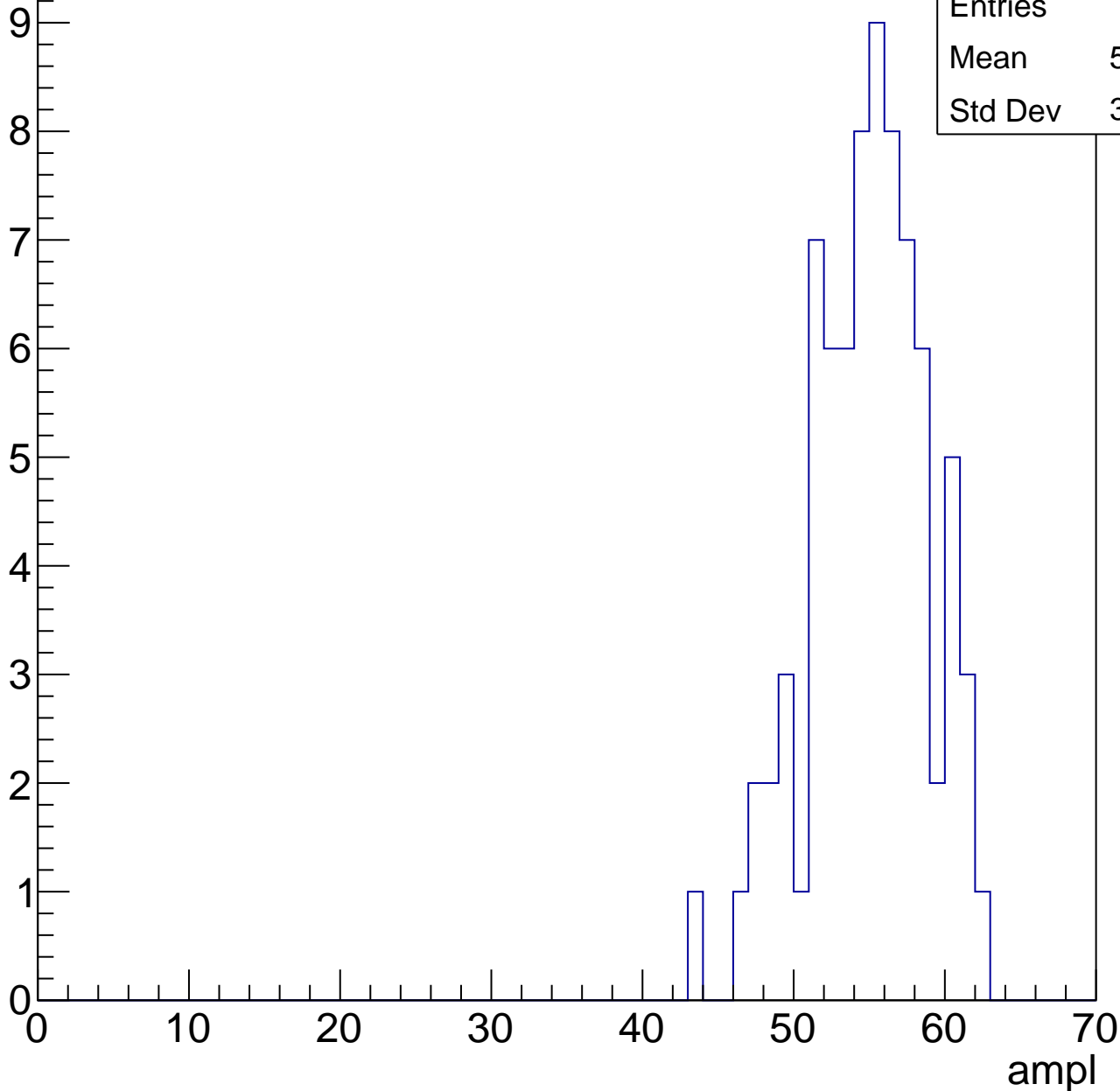


B1L102S, U17-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	54.46
Std Dev	3.862

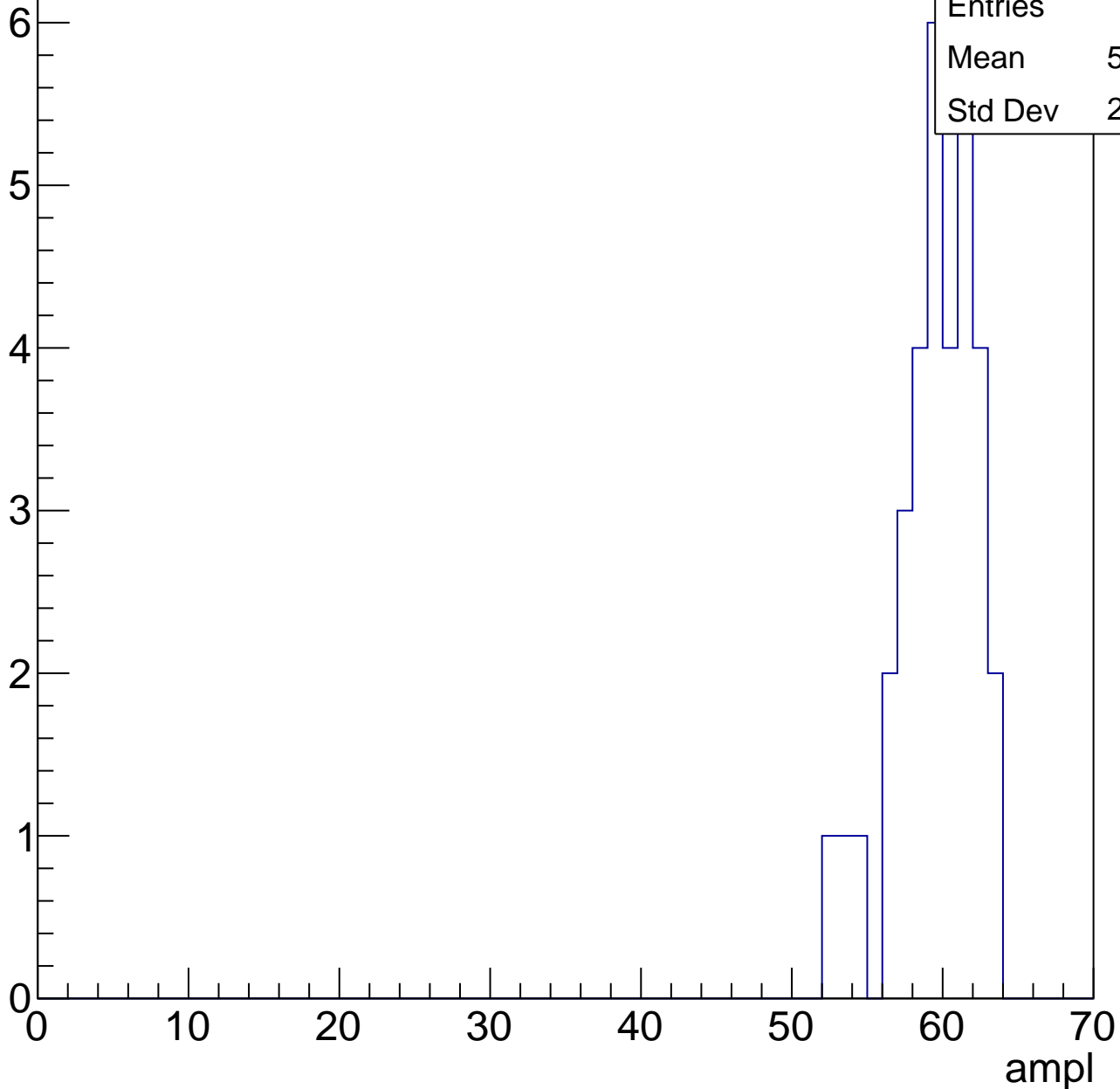


B1L102S, U17-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	59.06
Std Dev	2.656

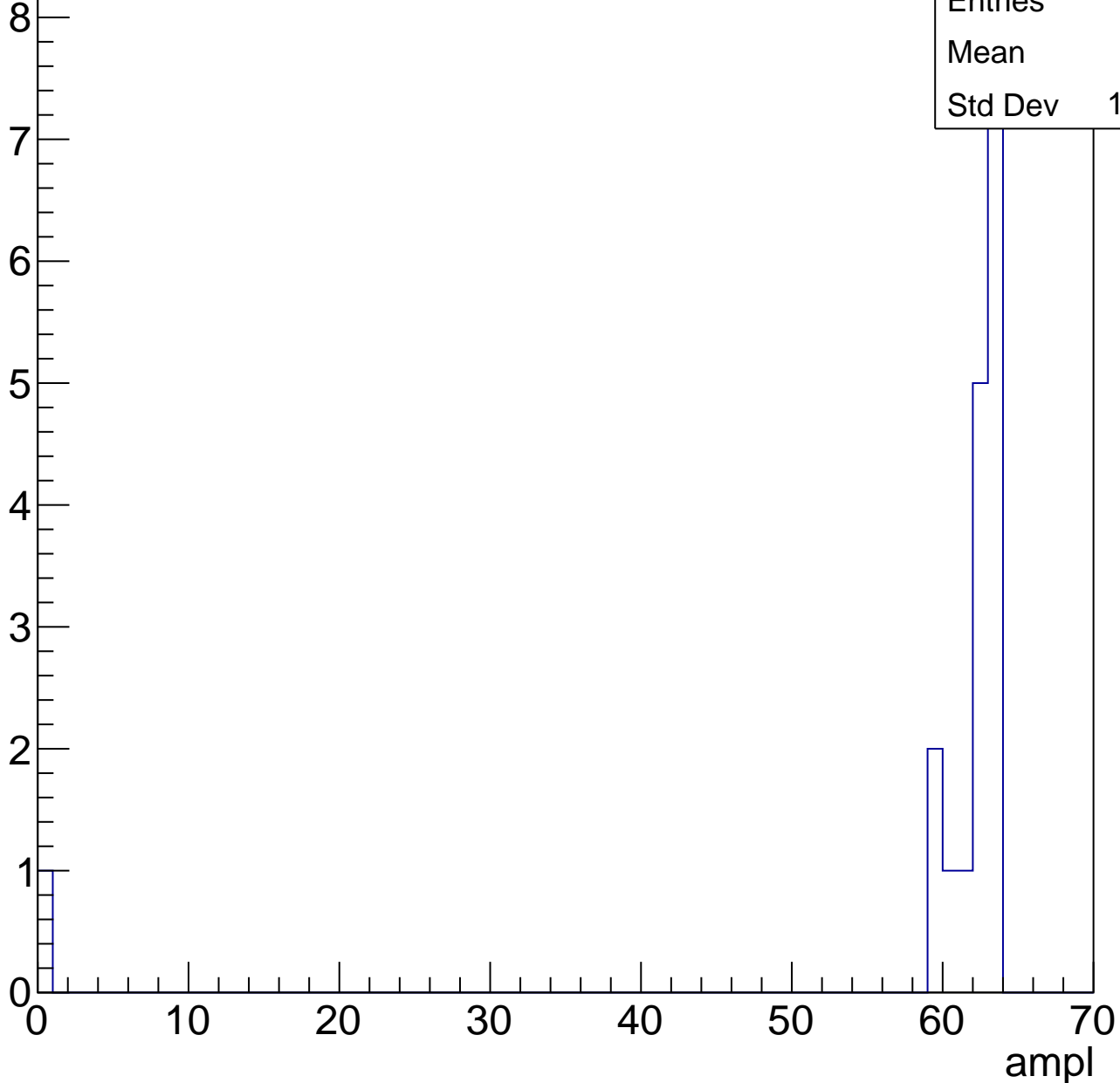


B1L102S, U17-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	58.5
Std Dev	14.25



B1L102S, U17-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U17-ch114, adc0

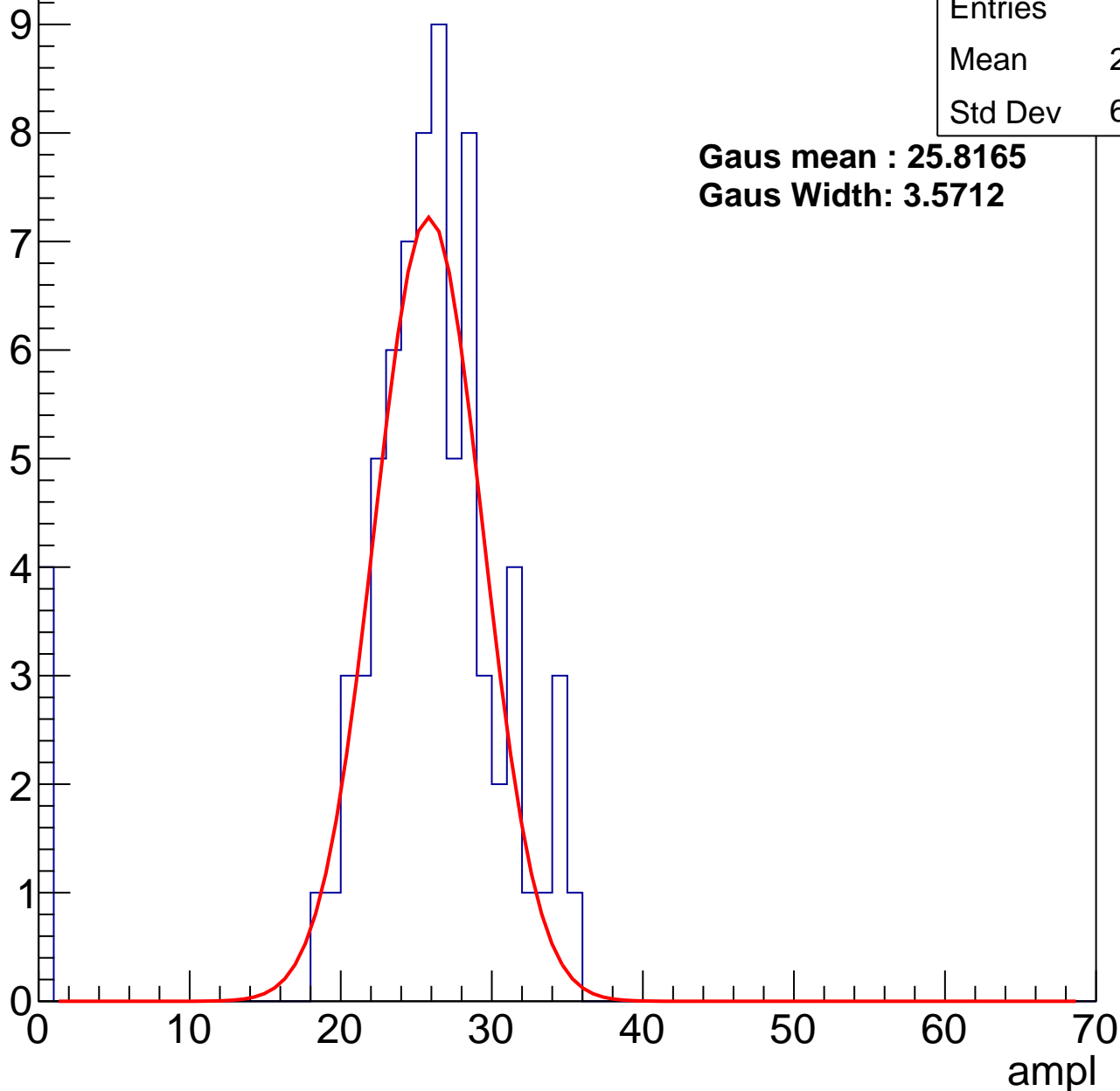
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	24.56
Std Dev	6.898

Gaus mean : 25.8165

Gaus Width: 3.5712



B1L102S, U17-ch114, adc1

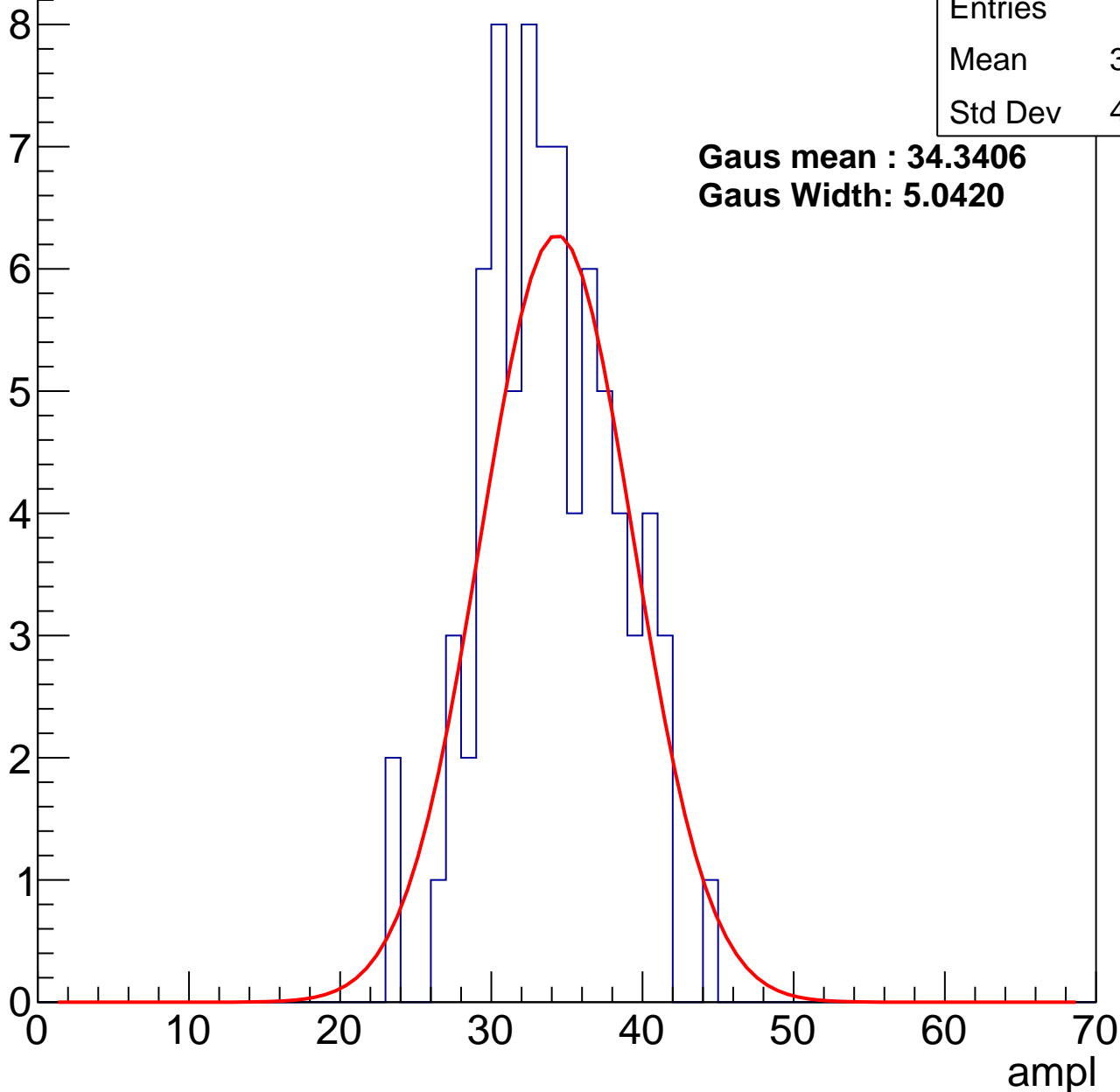
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	33.42
Std Dev	4.304

Gaus mean : 34.3406

Gaus Width: 5.0420



B1L102S, U17-ch114, adc2

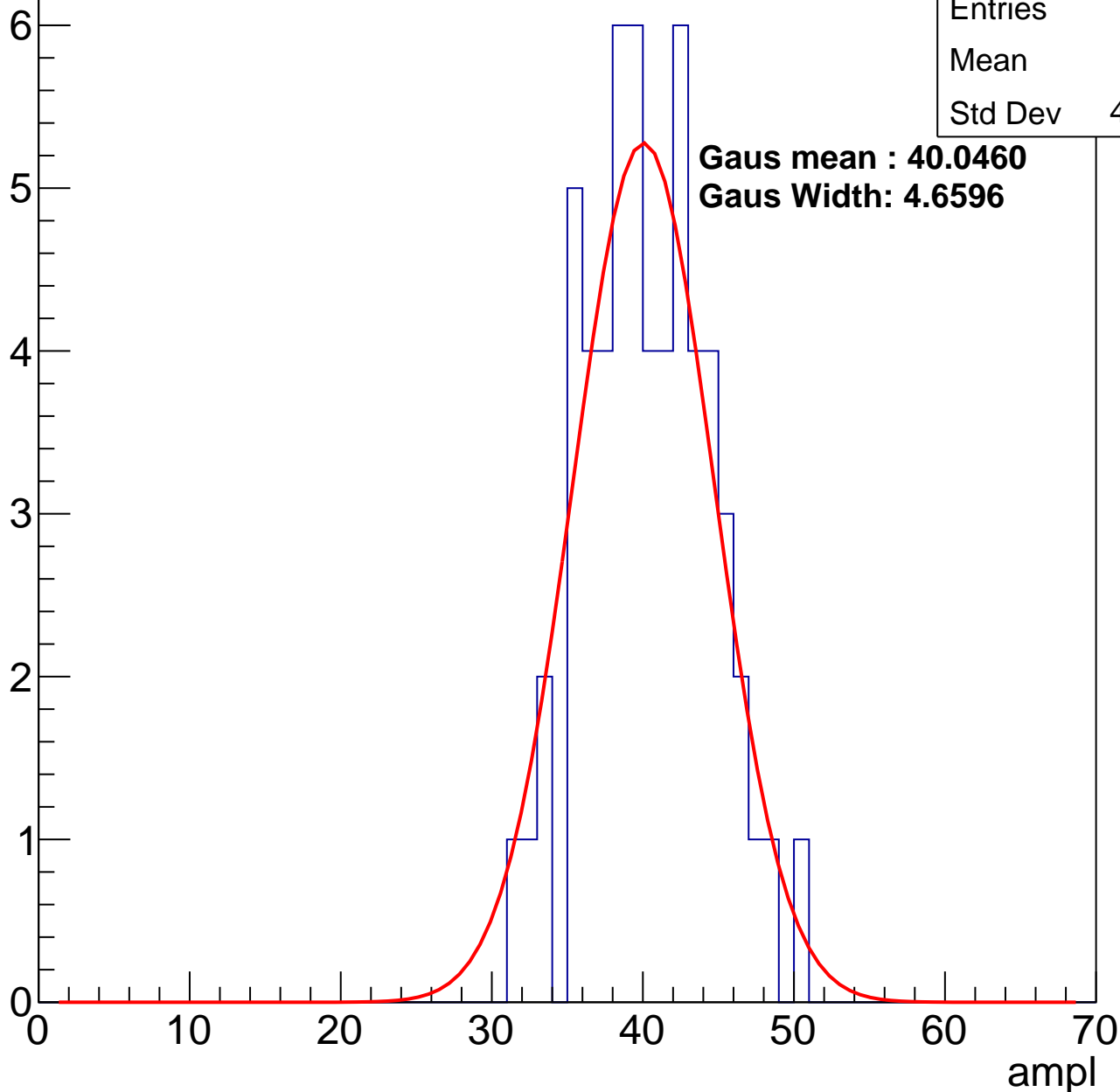
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	39.9
Std Dev	4.087

Gaus mean : 40.0460

Gaus Width: 4.6596

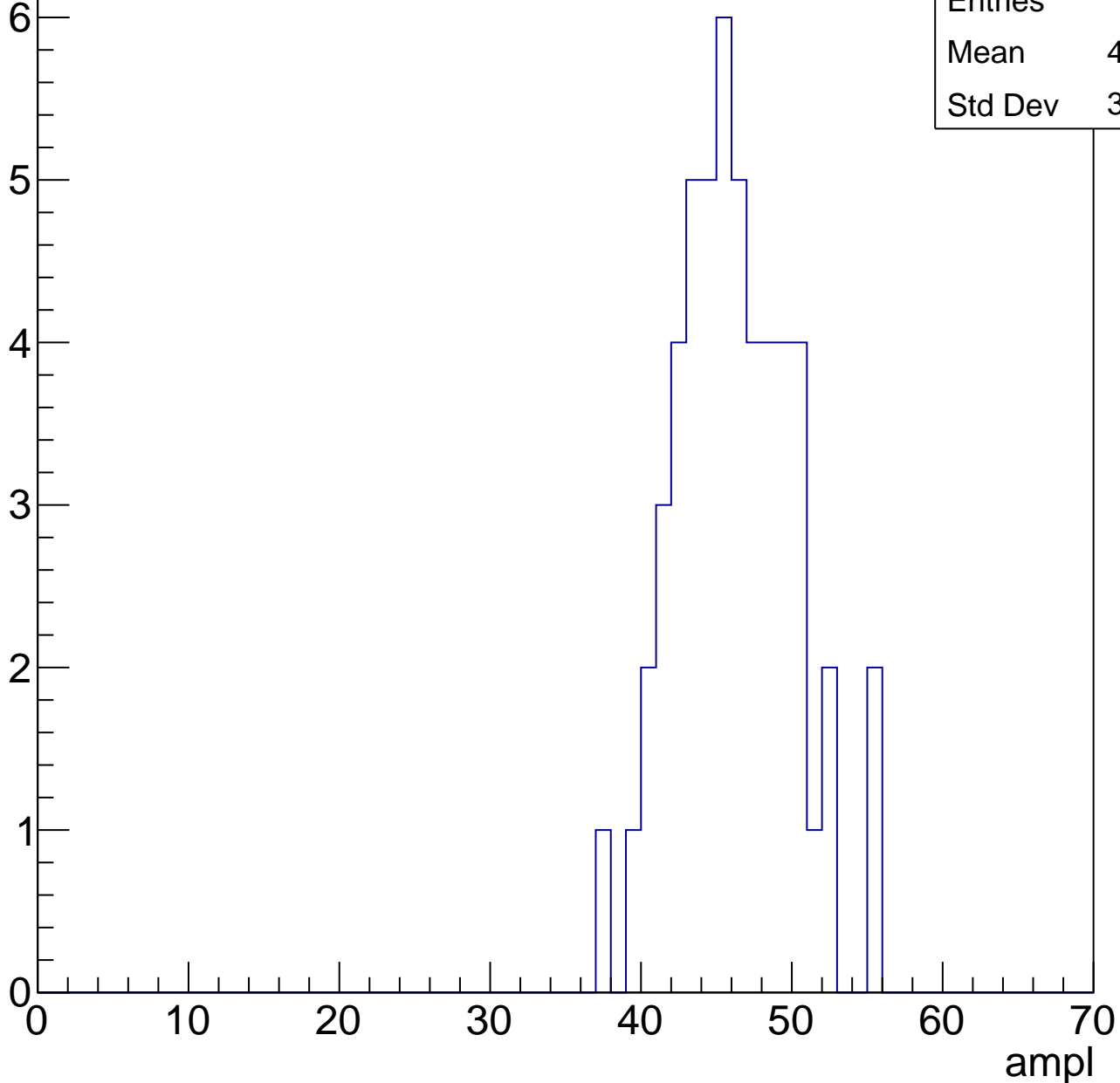


B1L102S, U17-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	45.72
Std Dev	3.853

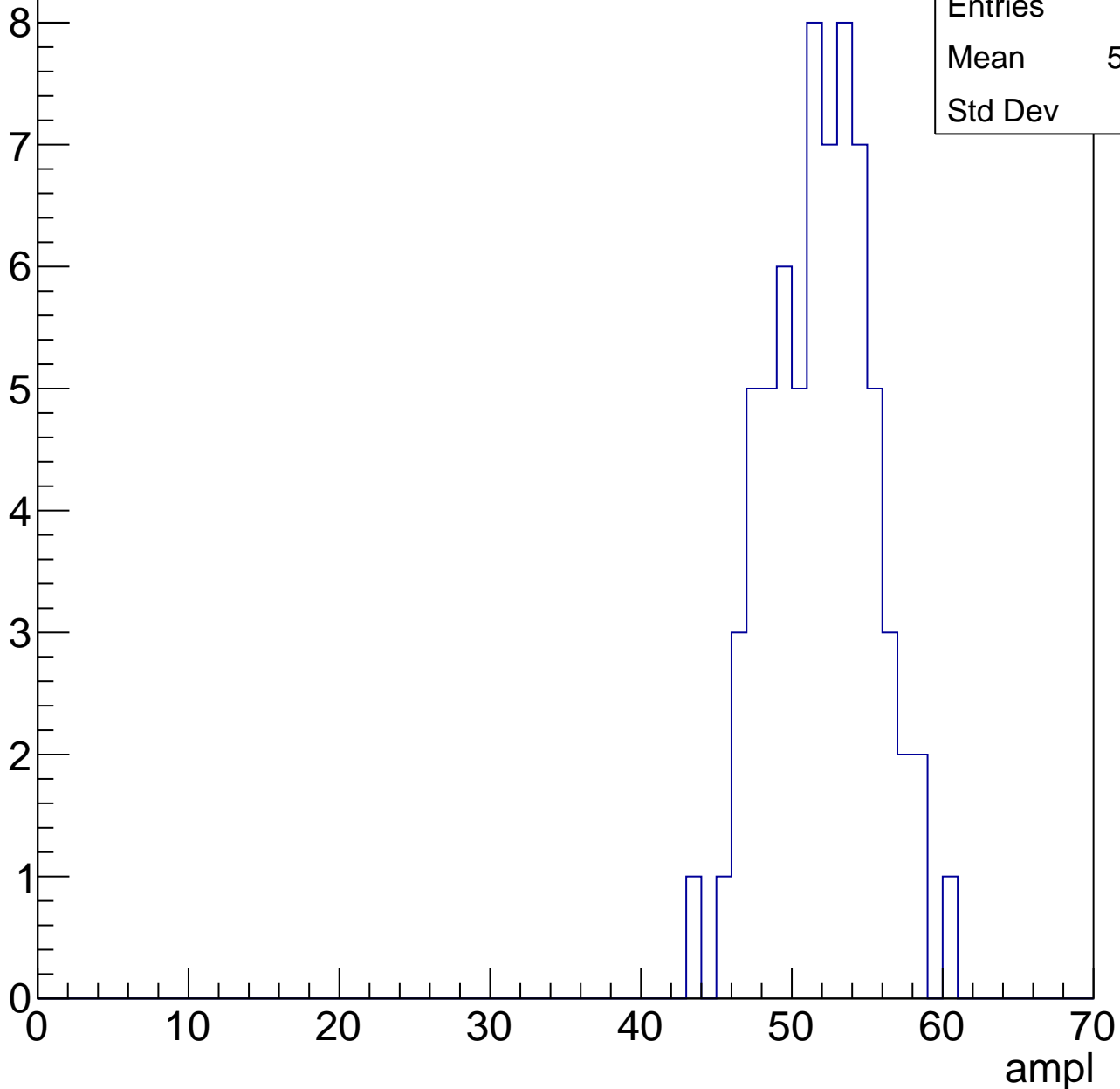


B1L102S, U17-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	51.48
Std Dev	3.45

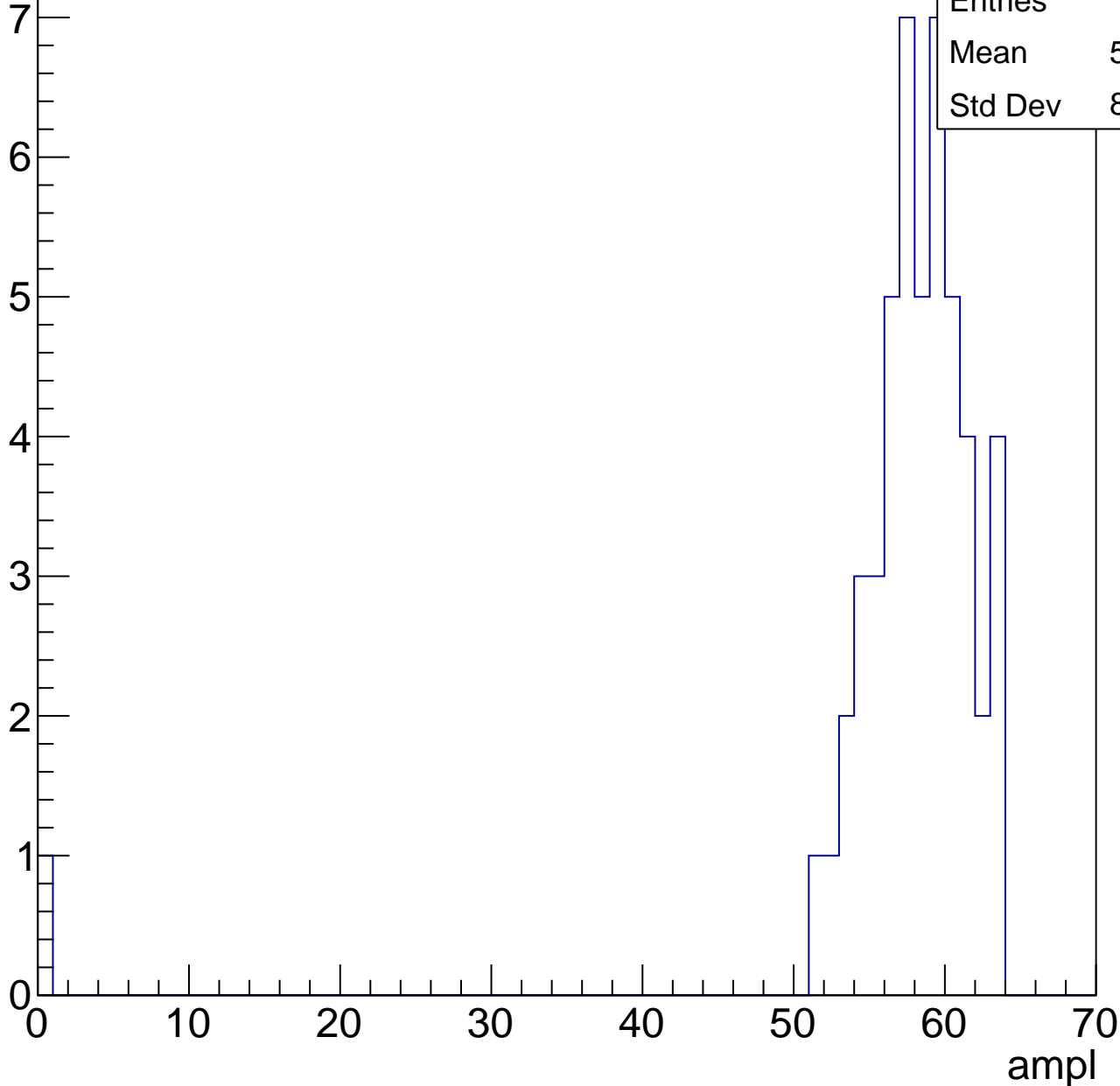


B1L102S, U17-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.76
Std Dev	8.625

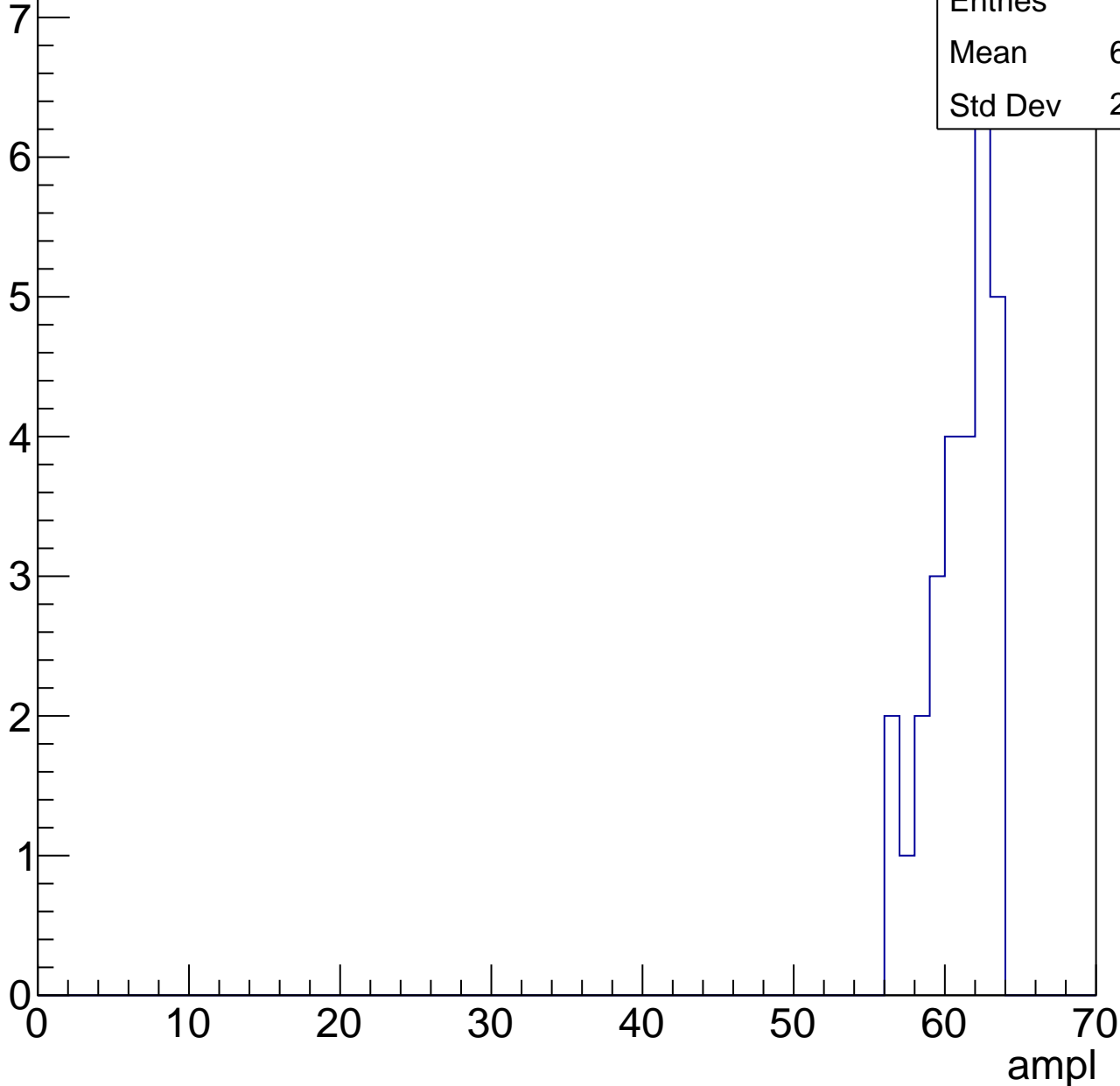


B1L102S, U17-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

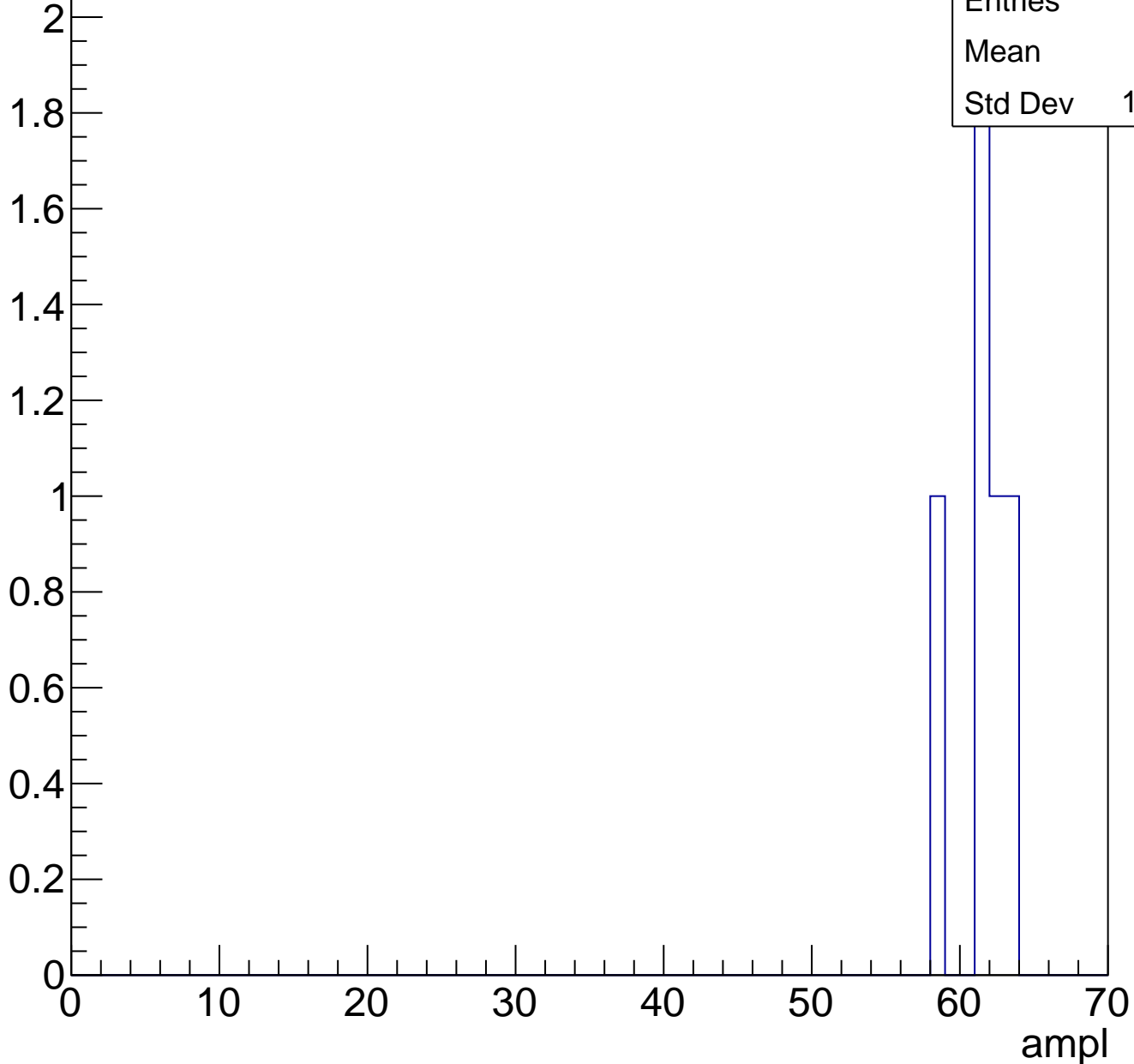
Entries	28
Mean	60.54
Std Dev	2.078



B1L102S, U17-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch115, adc0

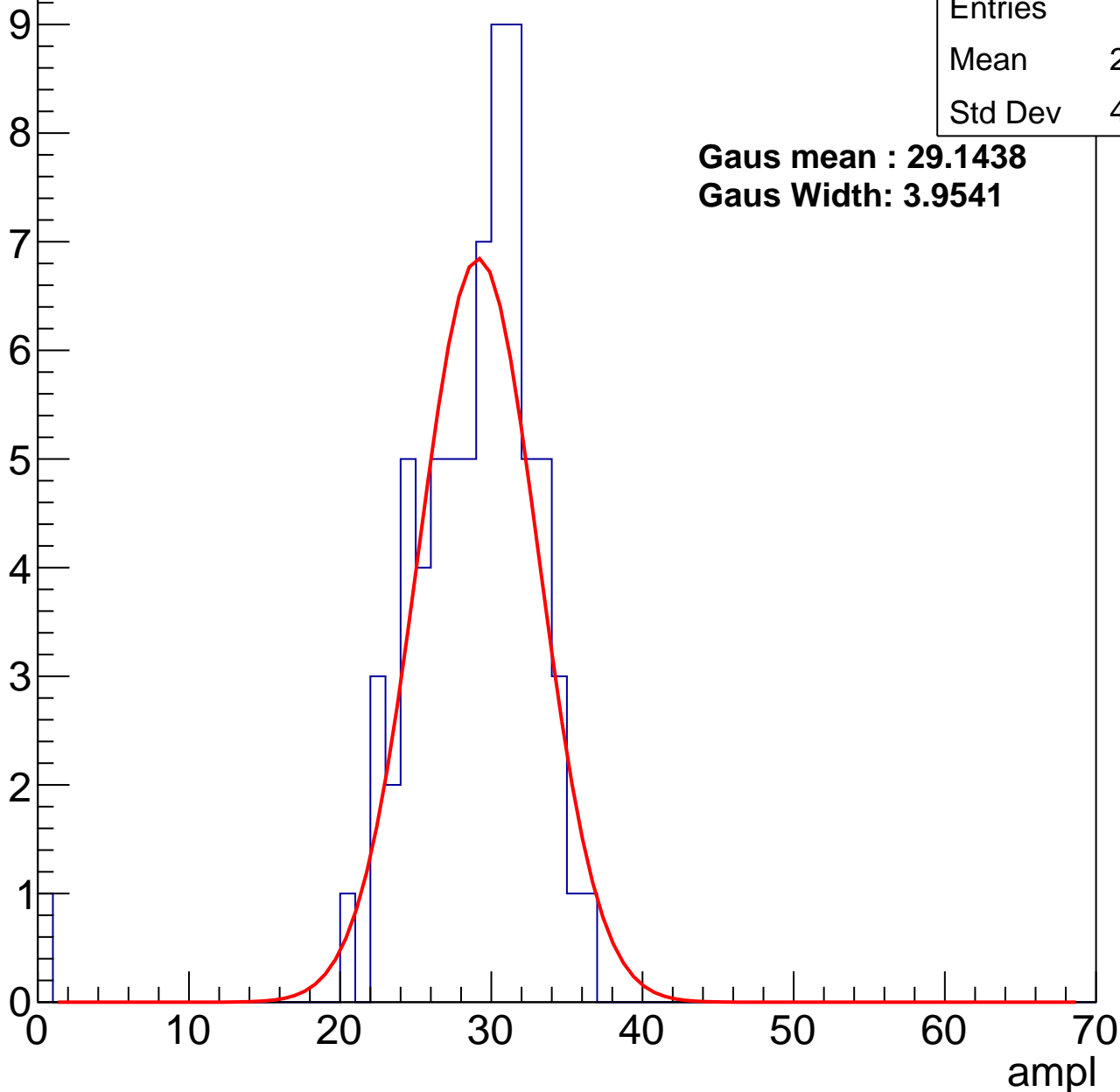
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.27
Std Dev	4.882

Gaus mean : 29.1438

Gaus Width: 3.9541



B1L102S, U17-ch115, adc1

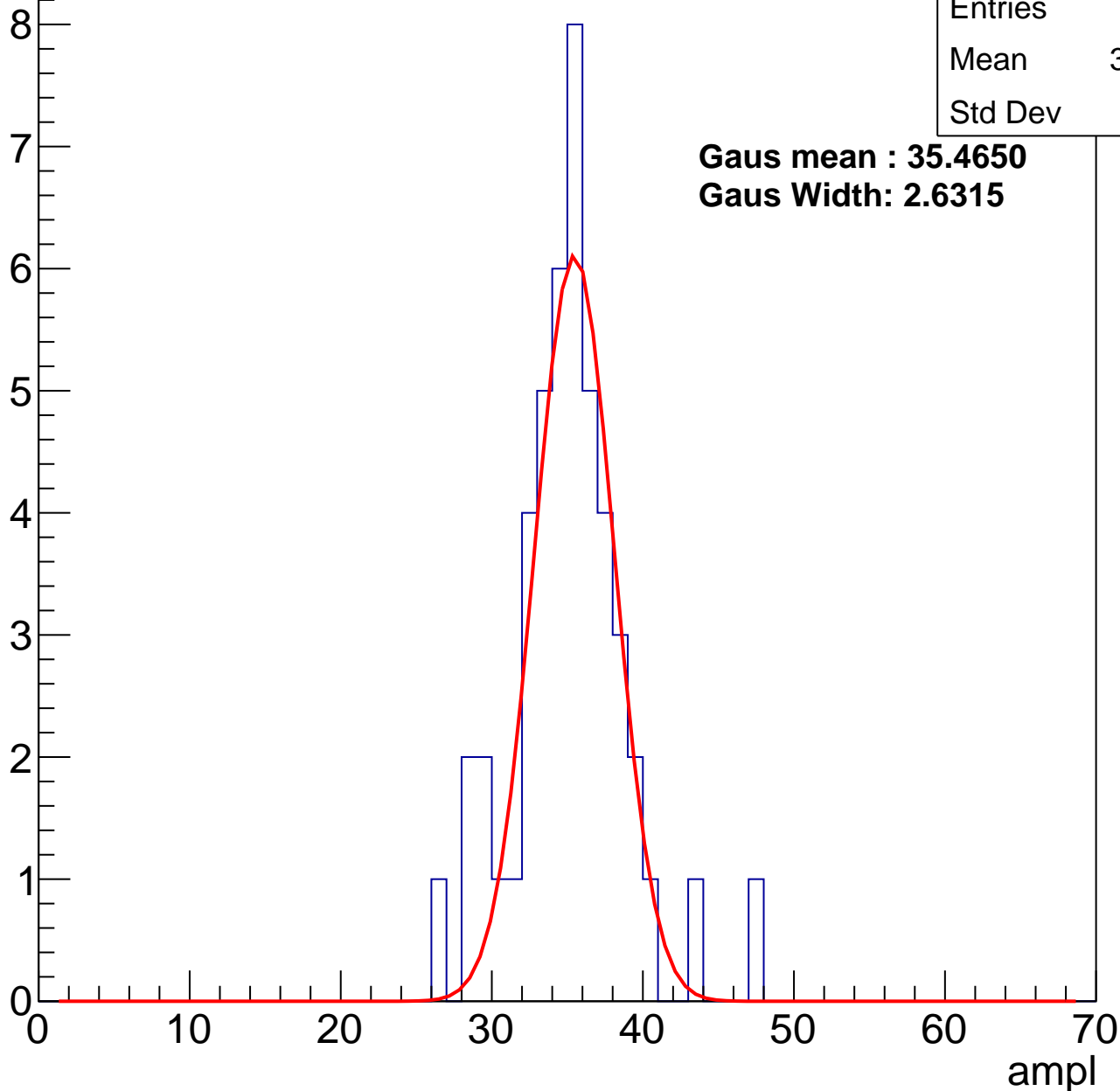
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	34.64
Std Dev	3.75

Gaus mean : 35.4650

Gaus Width: 2.6315



B1L102S, U17-ch115, adc2

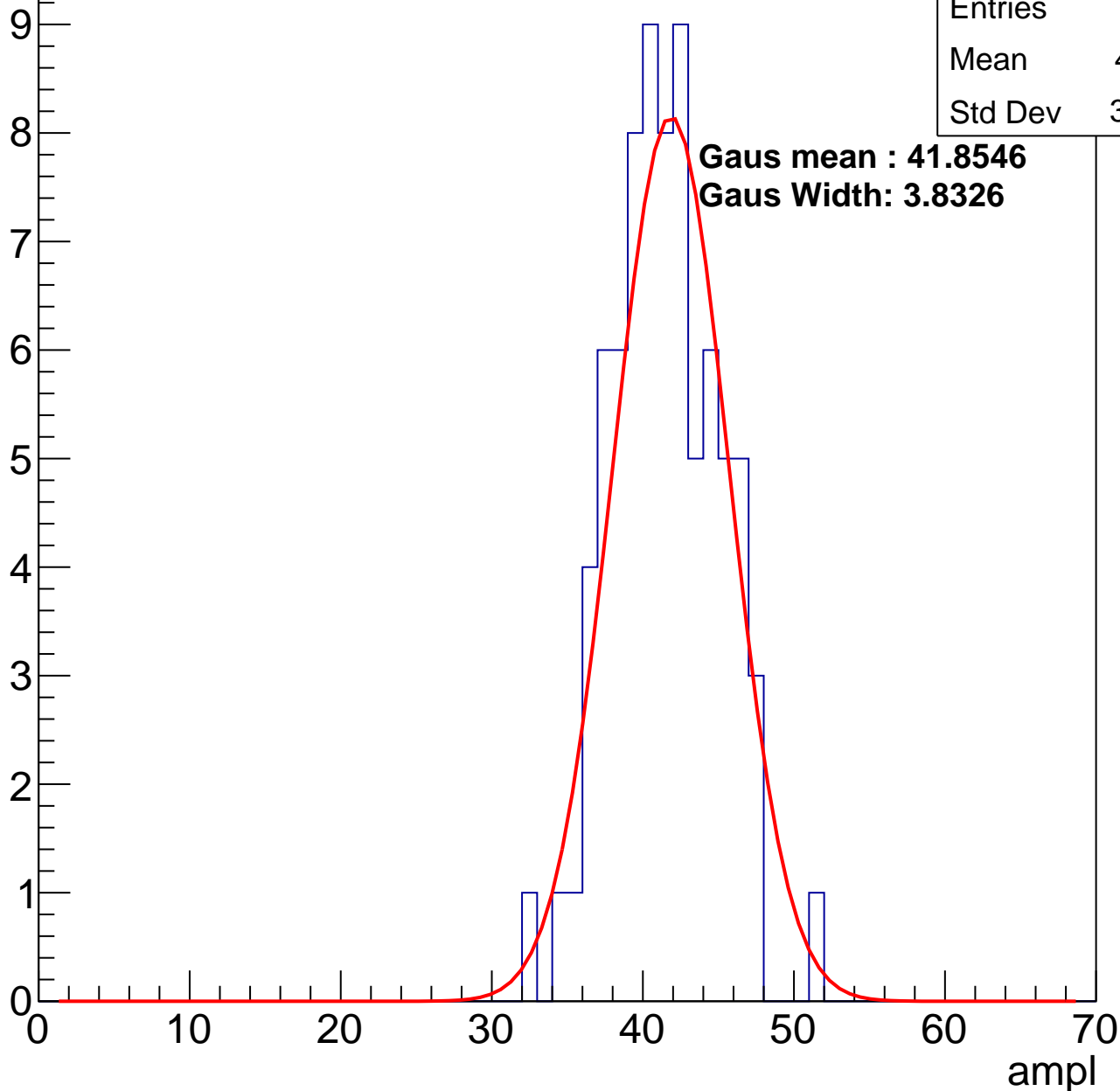
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	41.01
Std Dev	3.495

Gaus mean : 41.8546

Gaus Width: 3.8326

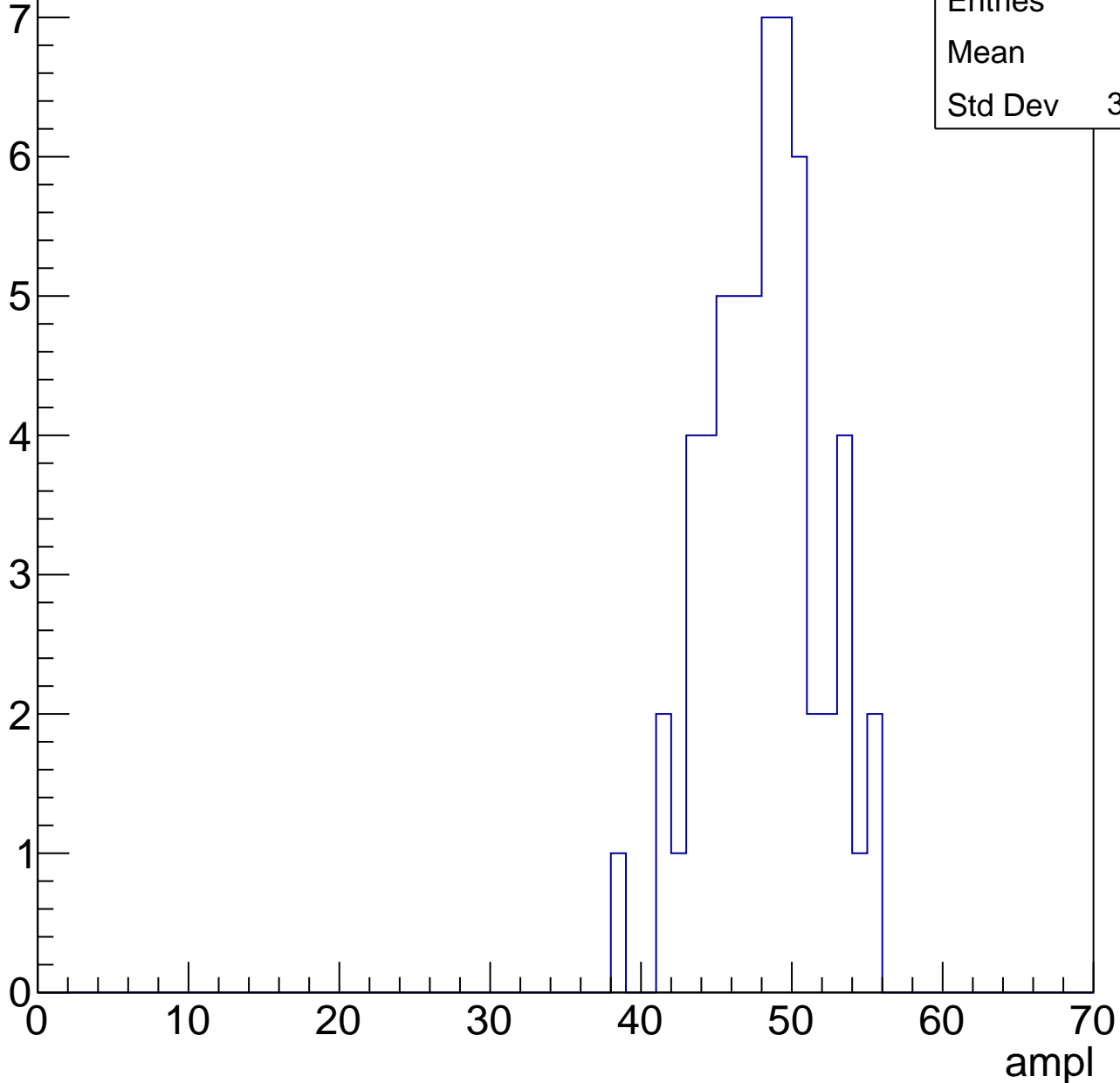


B1L102S, U17-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	47.6
Std Dev	3.653

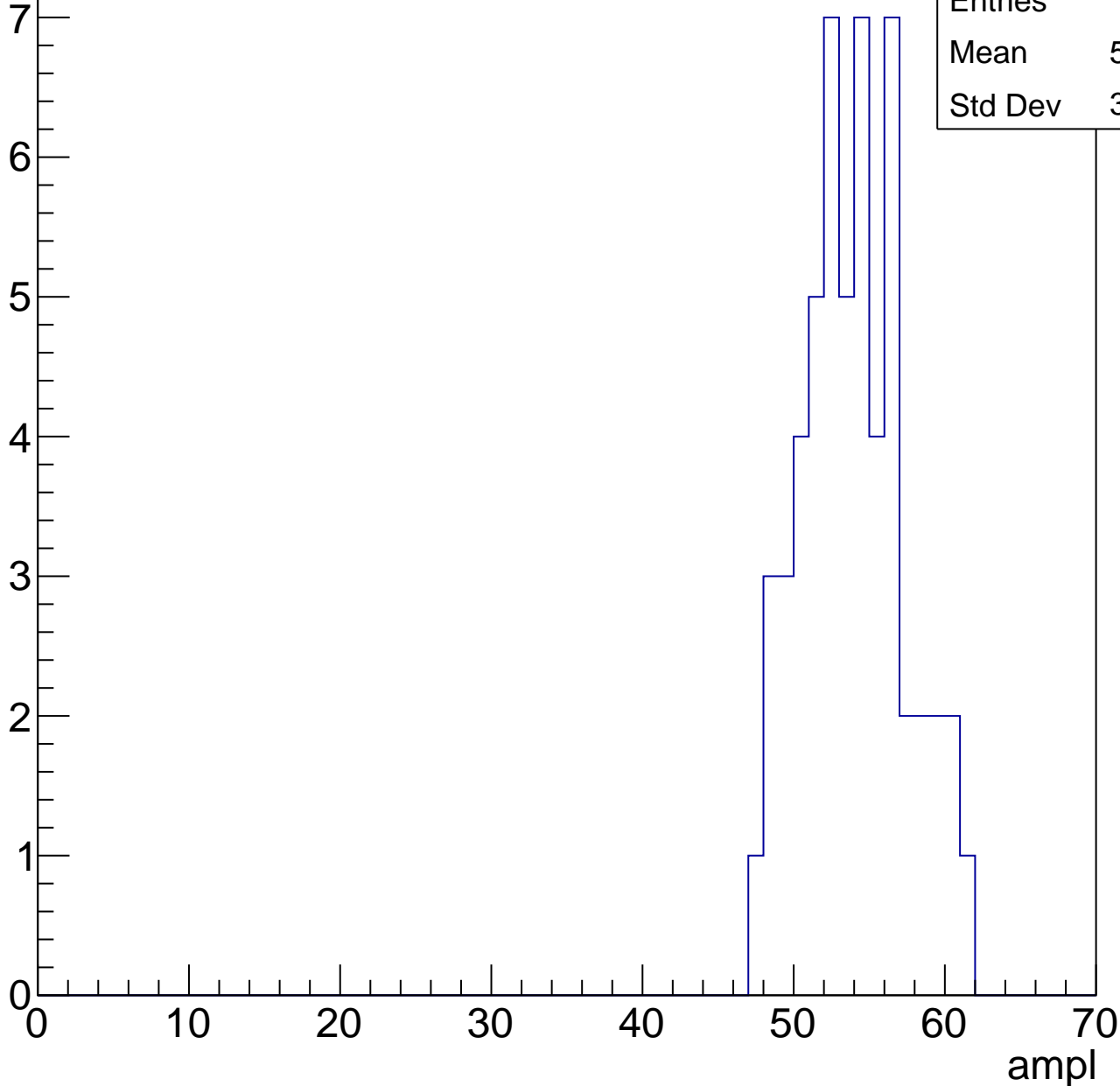


B1L102S, U17-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	53.47
Std Dev	3.335

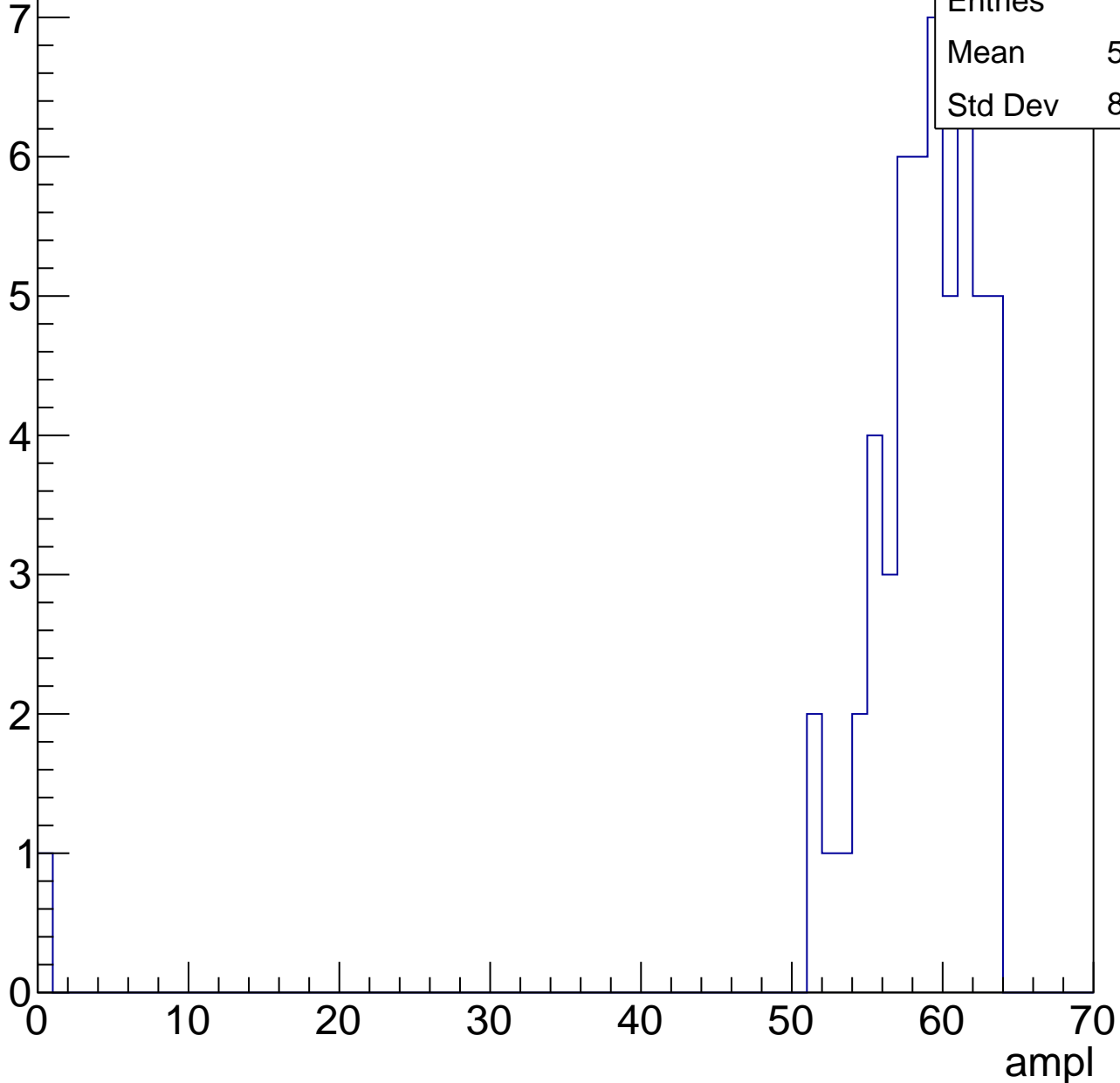


B1L102S, U17-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	57.42
Std Dev	8.405

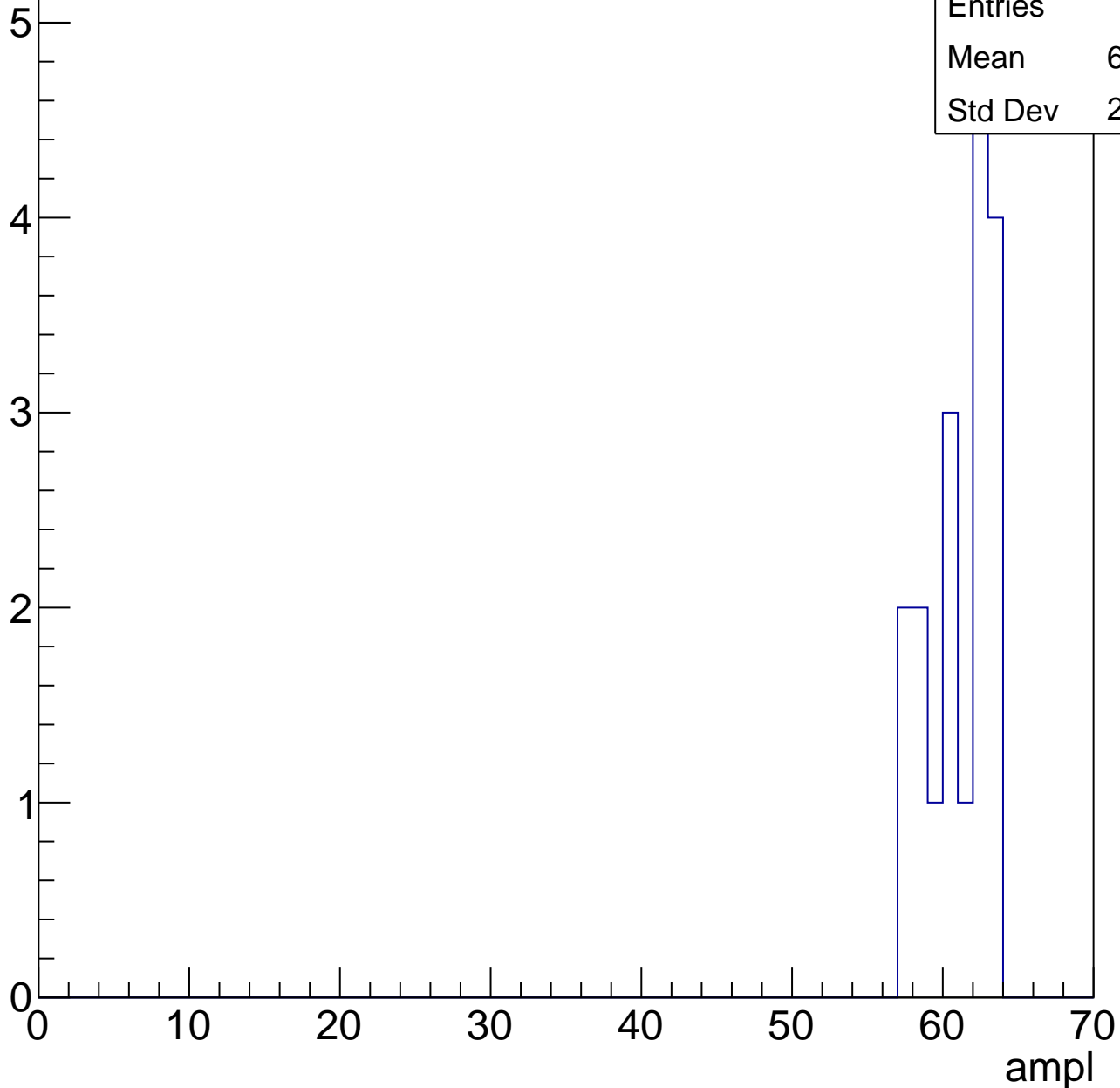


B1L102S, U17-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	60.67
Std Dev	2.055



B1L102S, U17-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch116, adc0

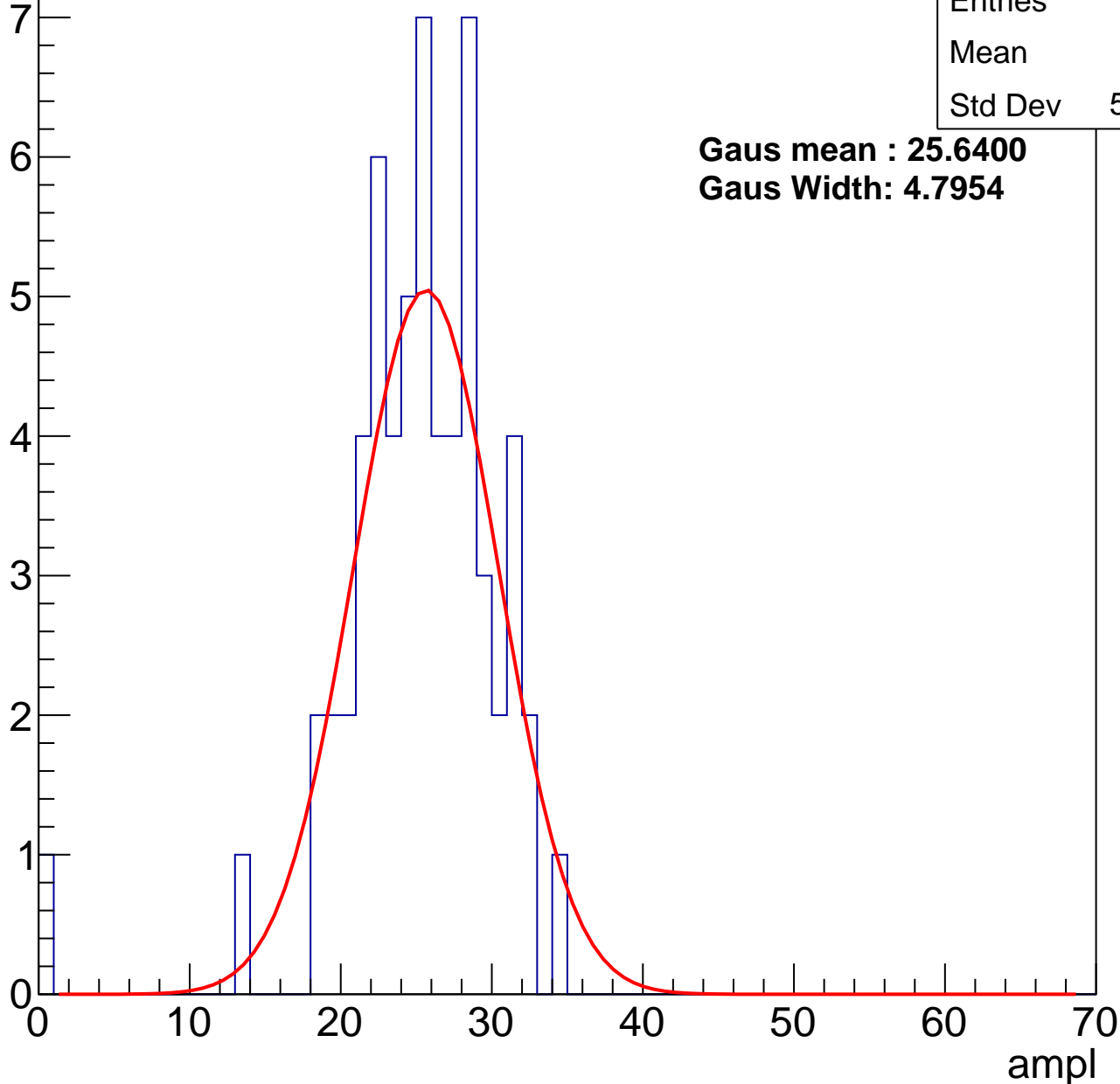
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	24.7
Std Dev	5.164

Gaus mean : 25.6400

Gaus Width: 4.7954



B1L102S, U17-ch116, adc1

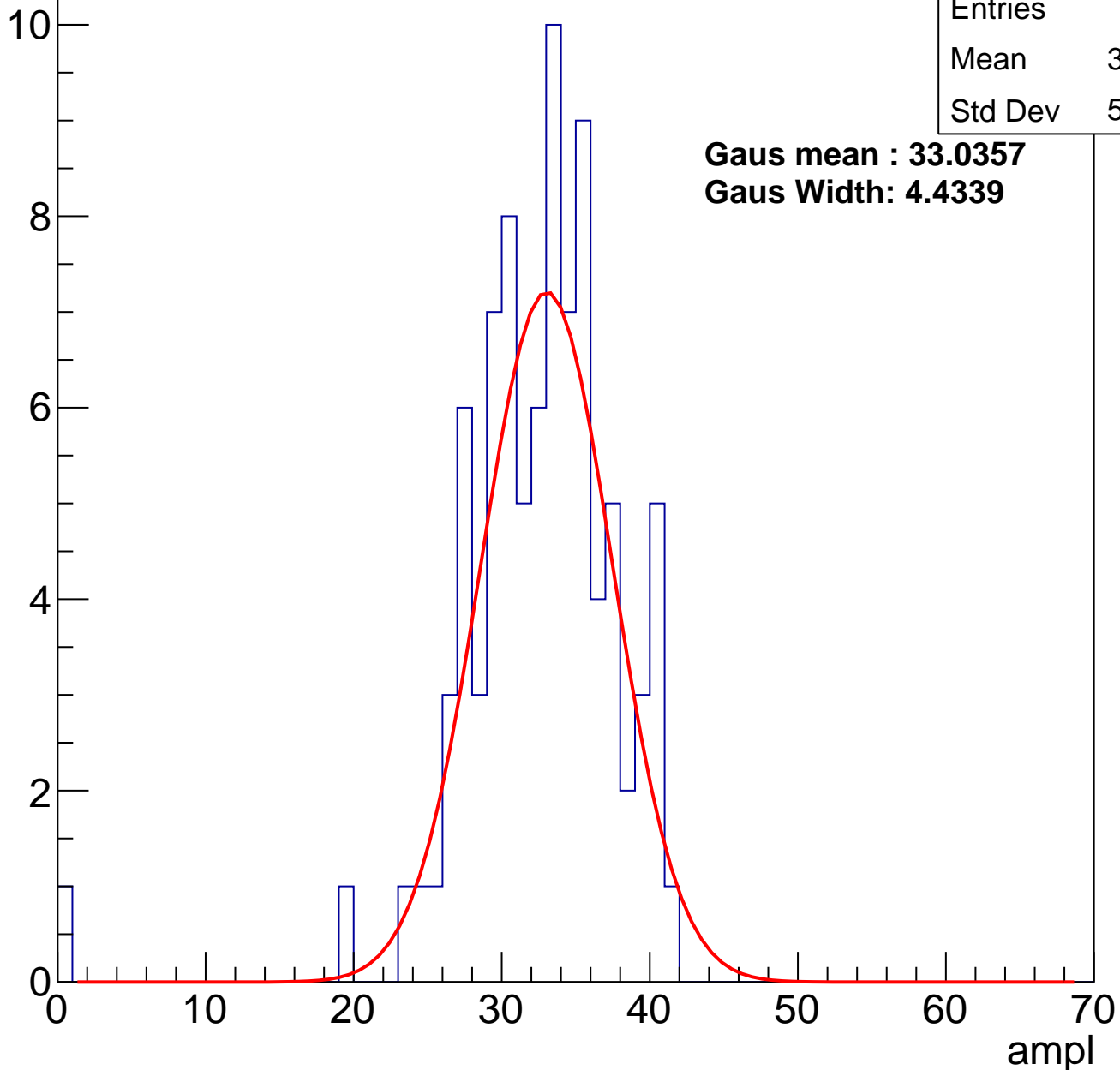
calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	32.03
Std Dev	5.529

Gaus mean : 33.0357

Gaus Width: 4.4339

Entry



B1L102S, U17-ch116, adc2

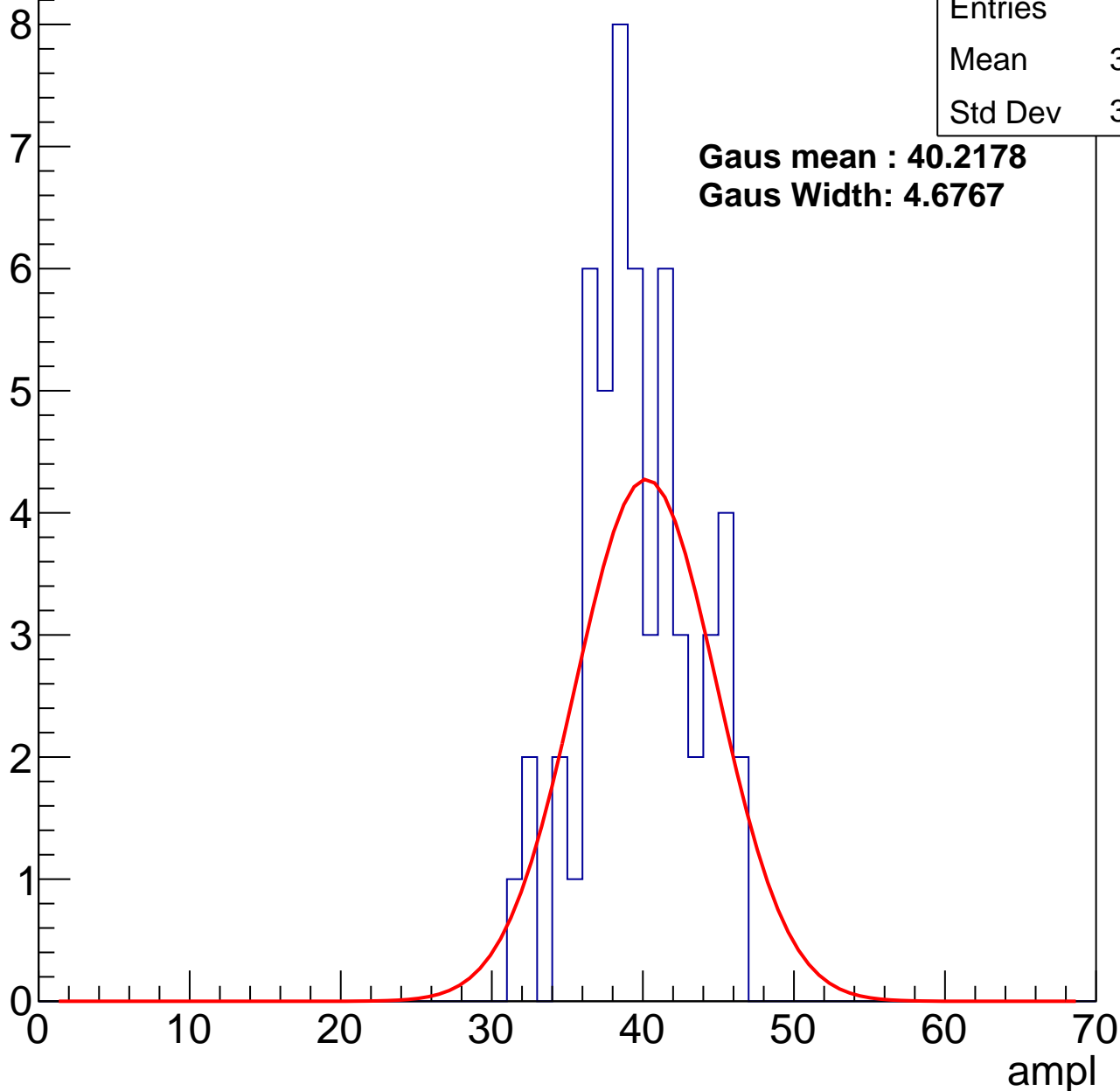
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	39.24
Std Dev	3.626

Gaus mean : 40.2178

Gaus Width: 4.6767

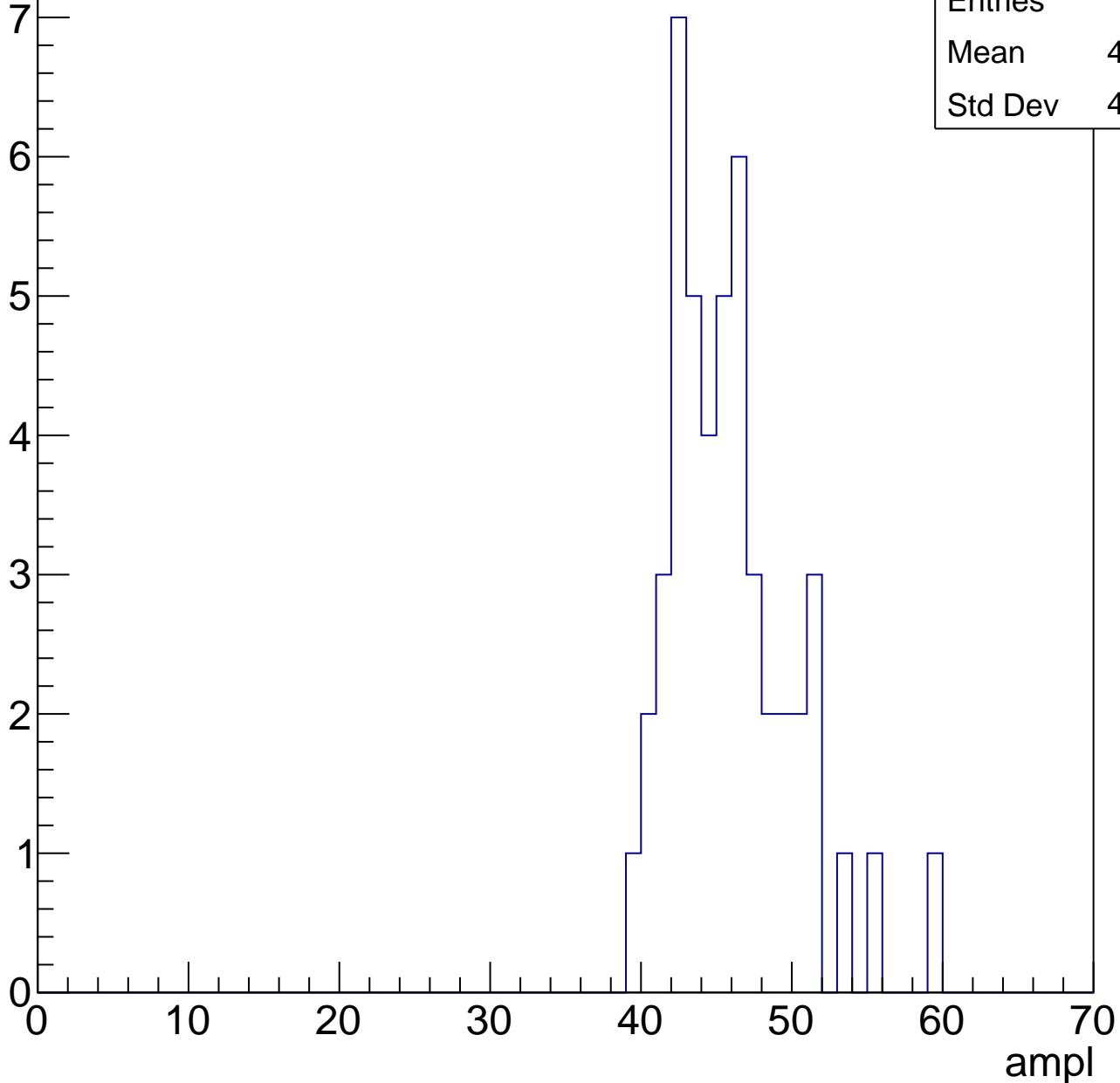


B1L102S, U17-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	45.48
Std Dev	4.072

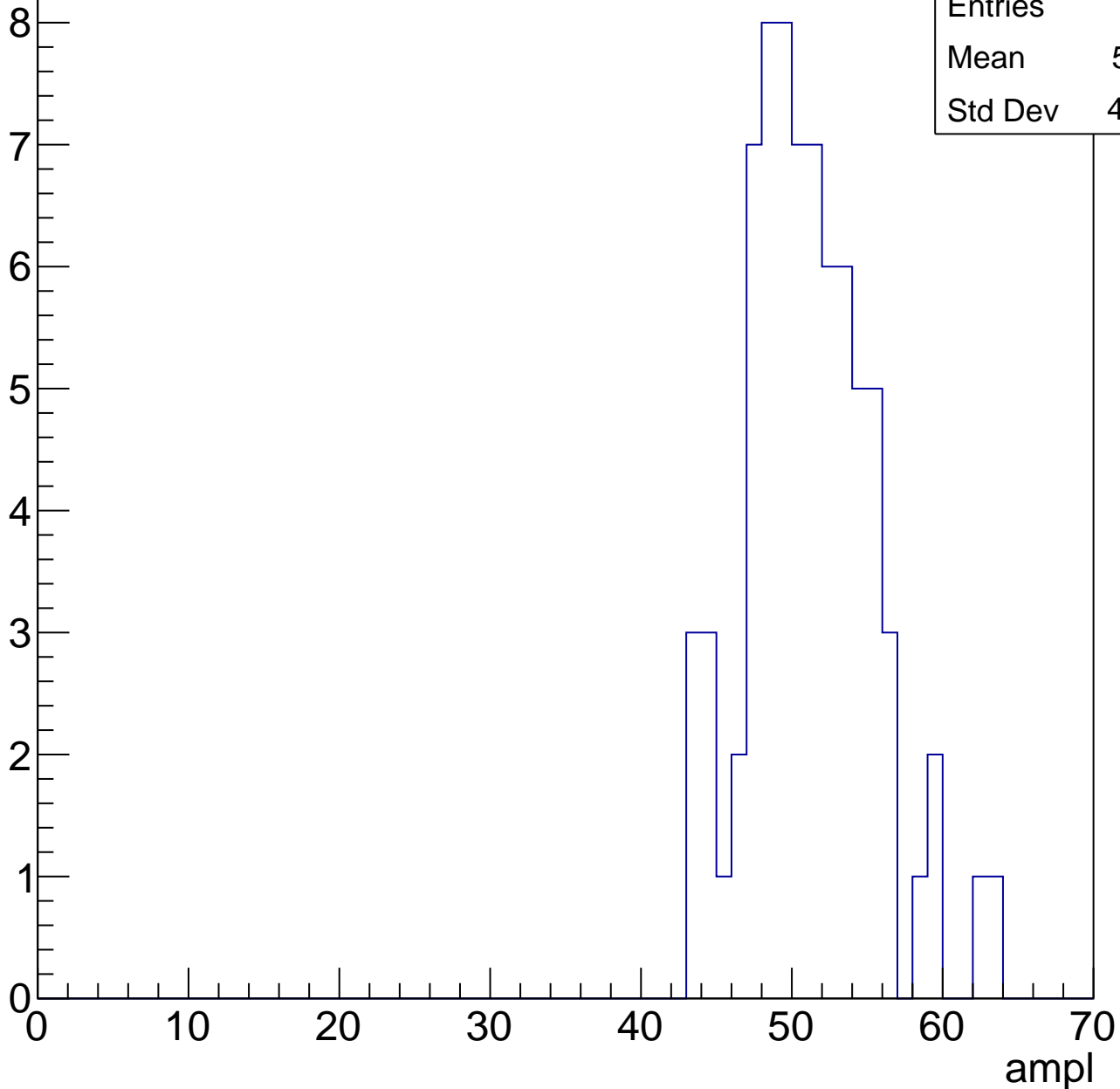


B1L102S, U17-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	50.71
Std Dev	4.157

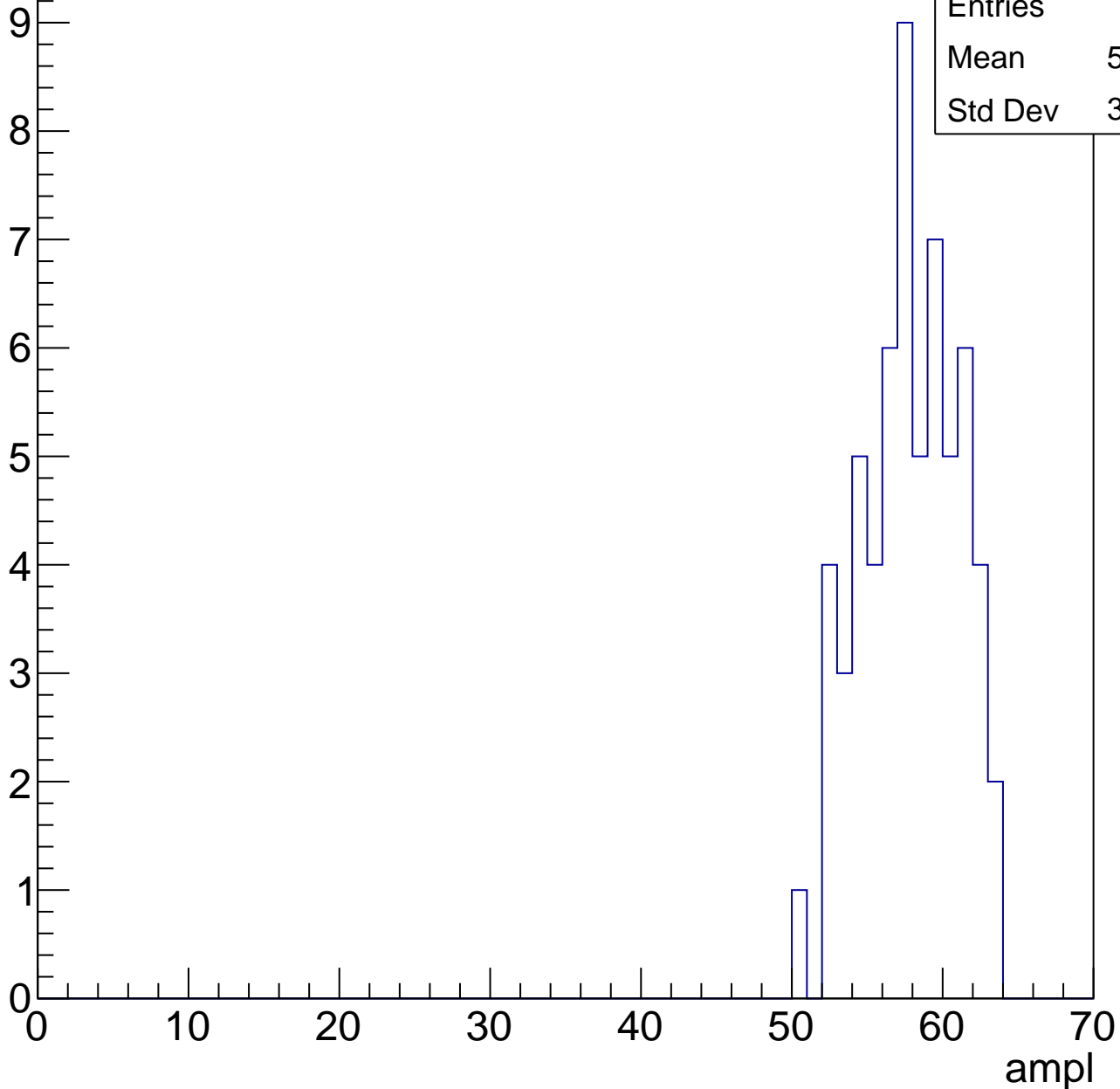


B1L102S, U17-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	57.36
Std Dev	3.142

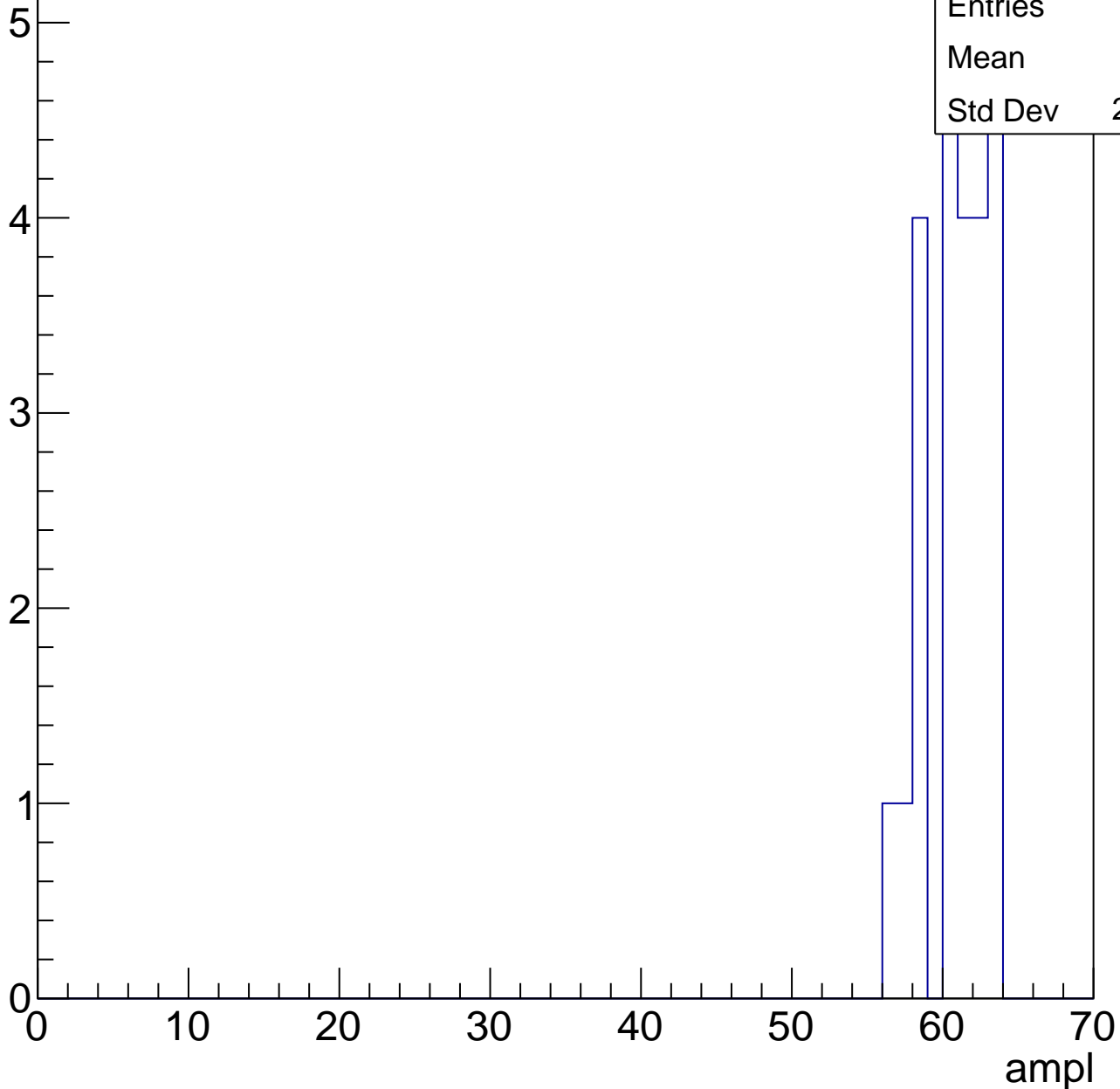


B1L102S, U17-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	24
Mean	60.5
Std Dev	2.041



B1L102S, U17-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch117, adc0

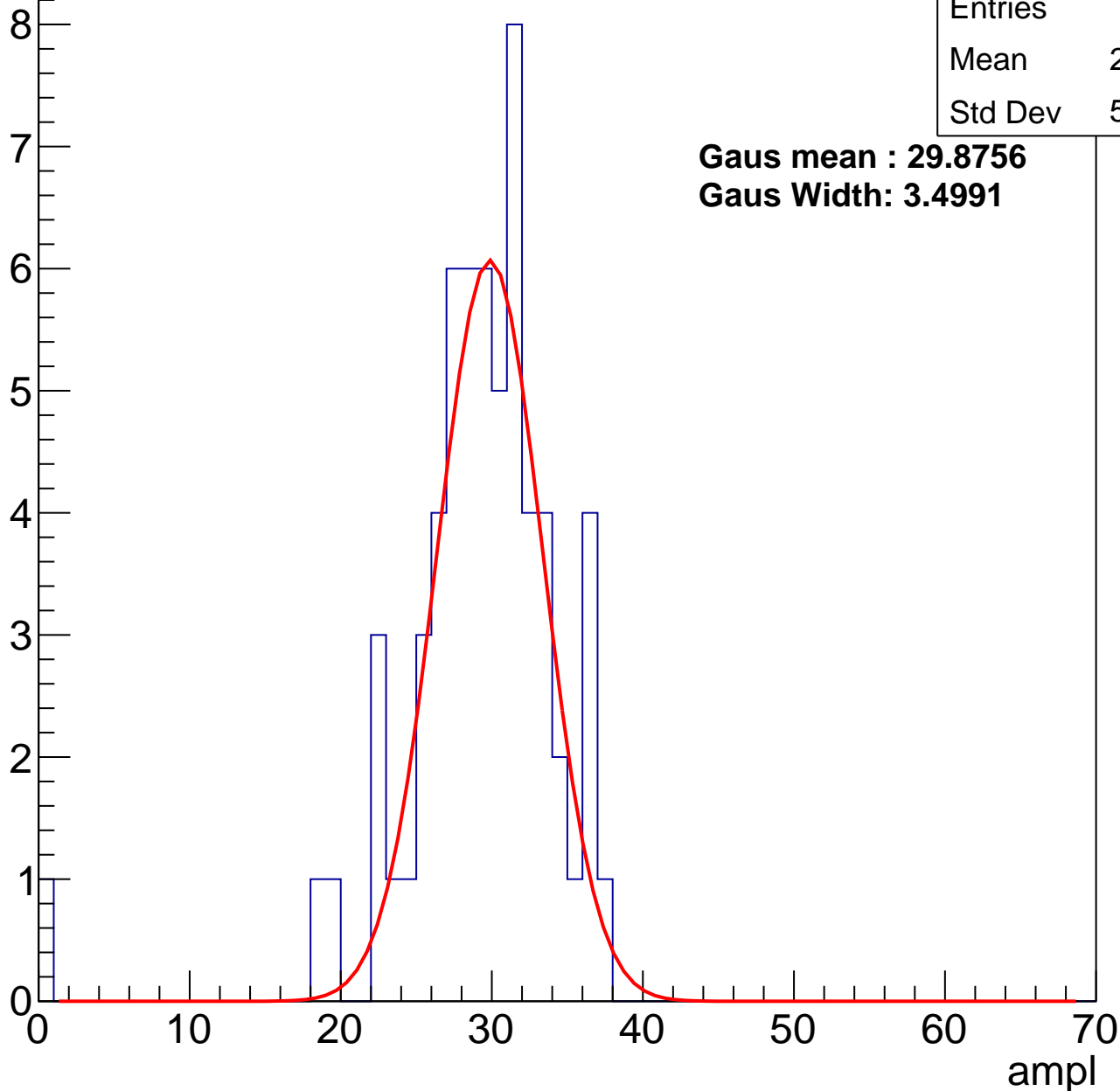
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.63
Std Dev	5.475

Gaus mean : 29.8756

Gaus Width: 3.4991



B1L102S, U17-ch117, adc1

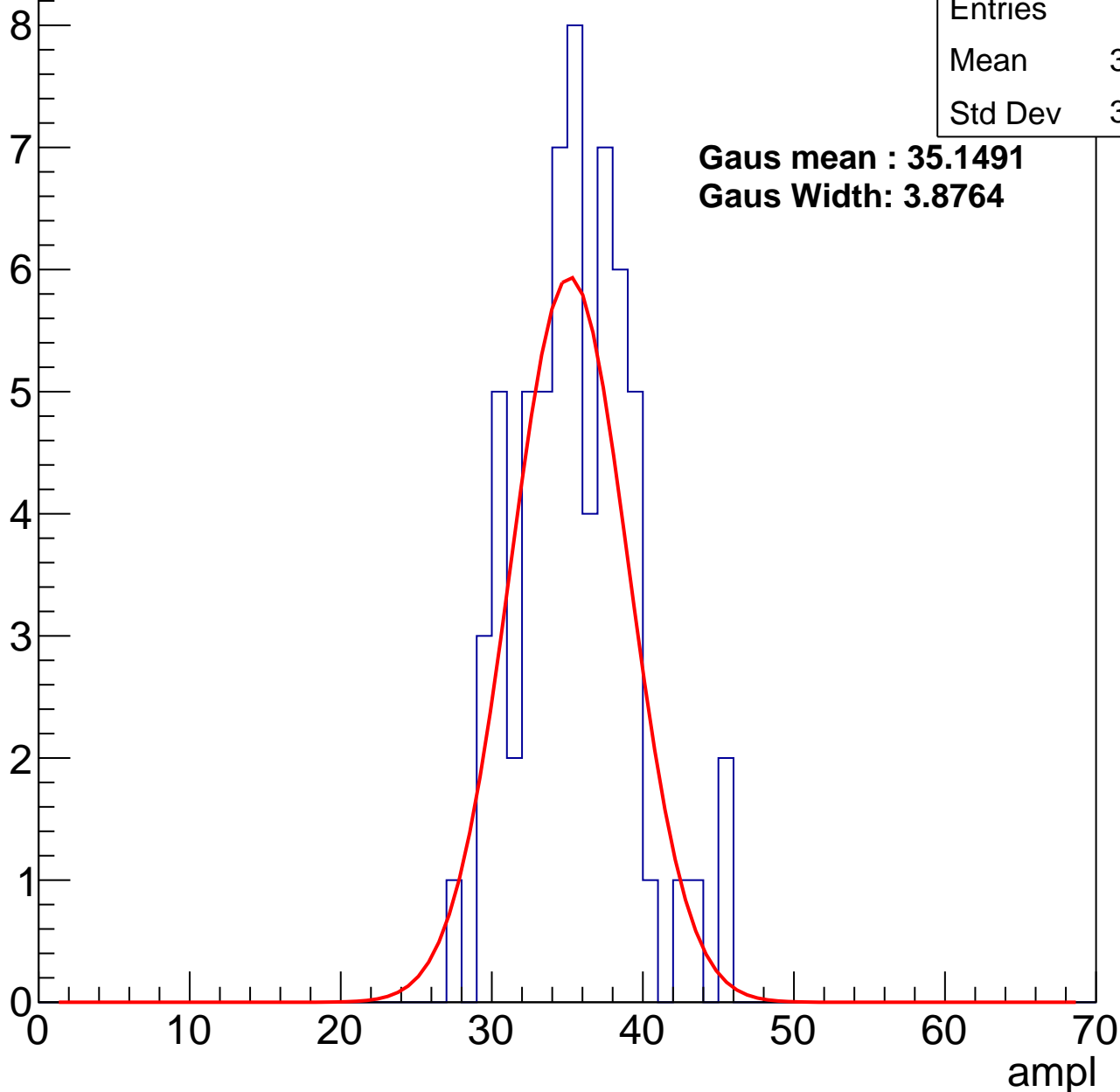
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.08
Std Dev	3.789

Gaus mean : 35.1491

Gaus Width: 3.8764



B1L102S, U17-ch117, adc2

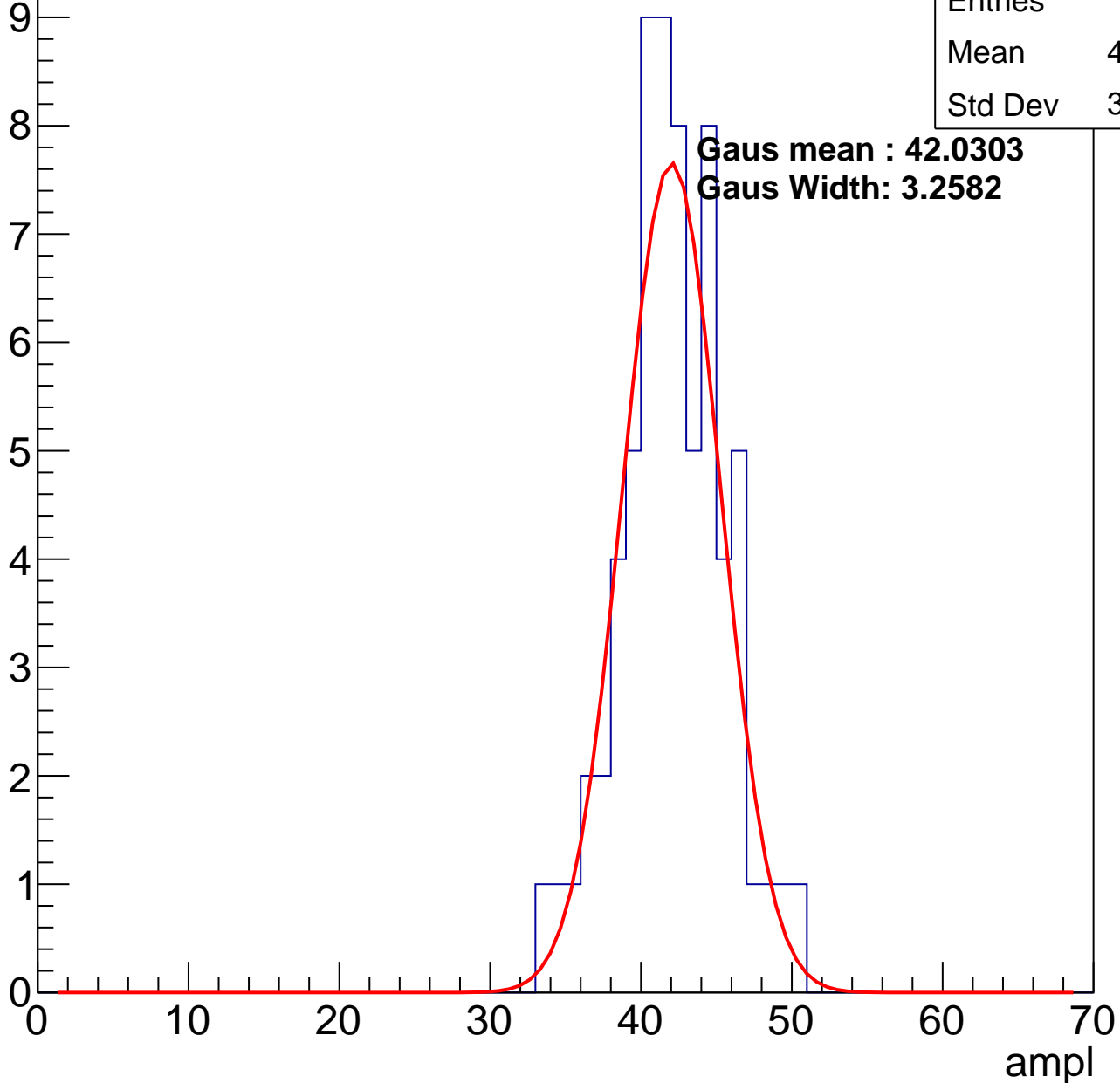
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.63
Std Dev	3.404

Gaus mean : 42.0303

Gaus Width: 3.2582

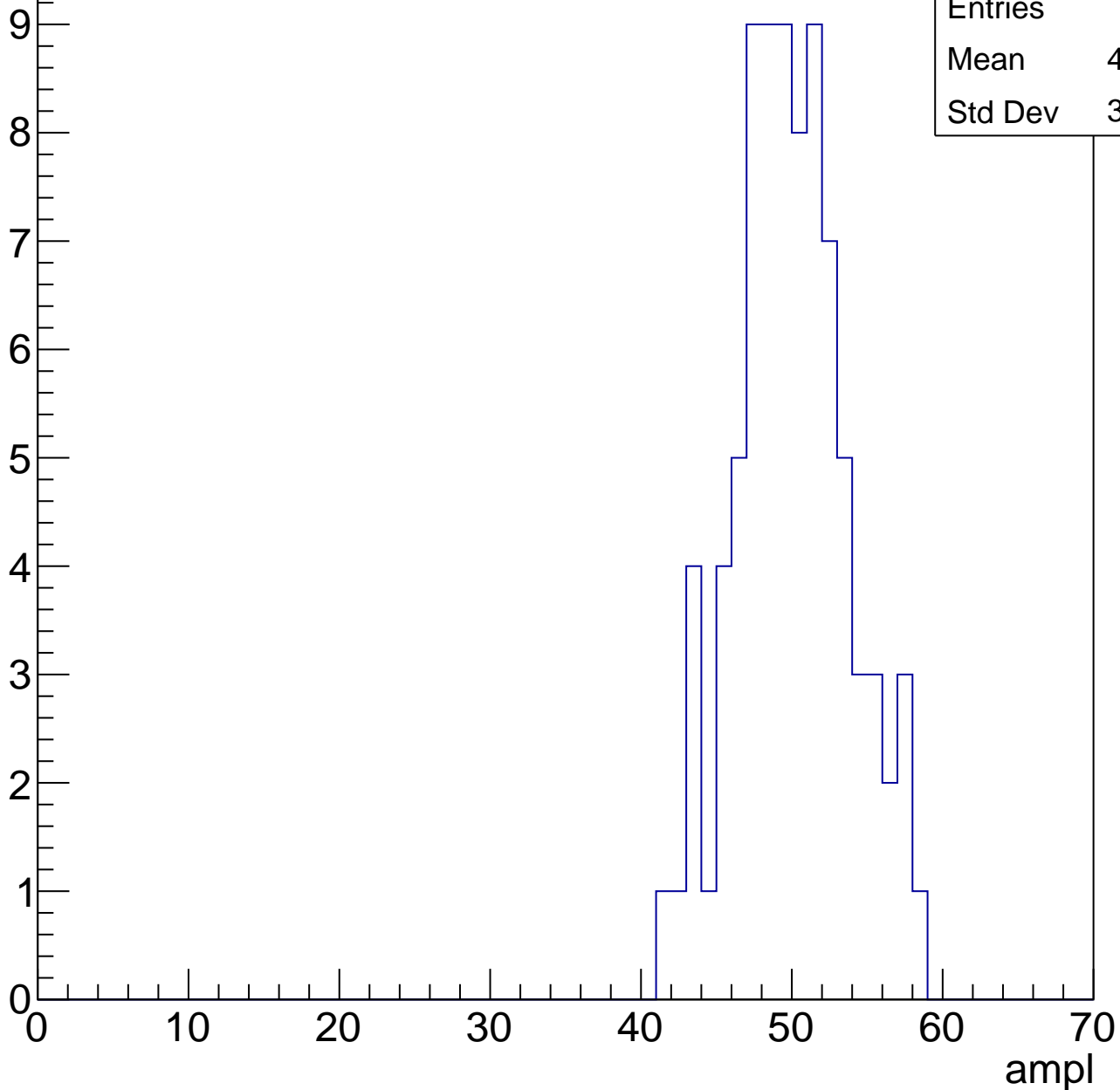


B1L102S, U17-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	49.54
Std Dev	3.705

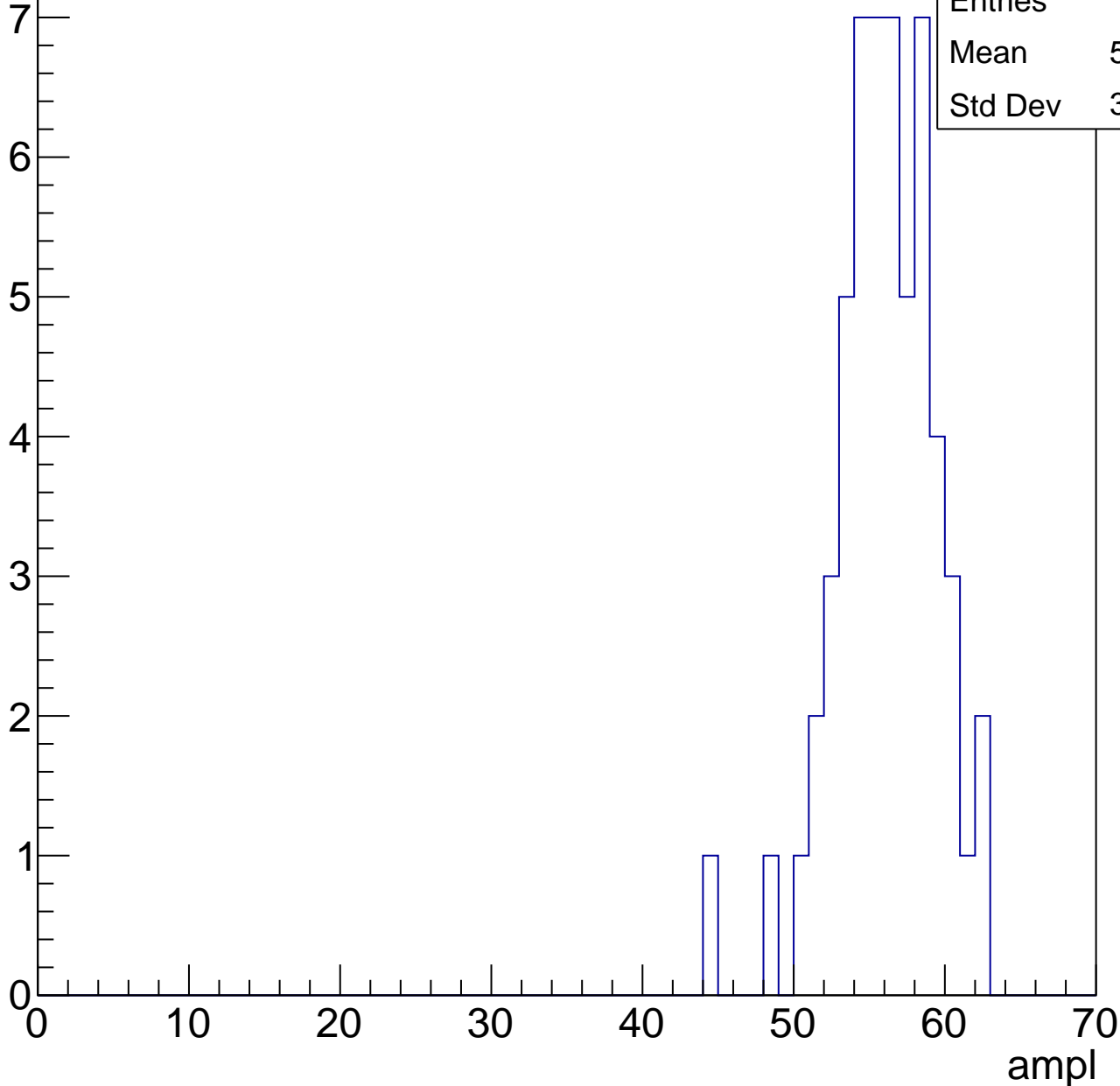


B1L102S, U17-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.57
Std Dev	3.343

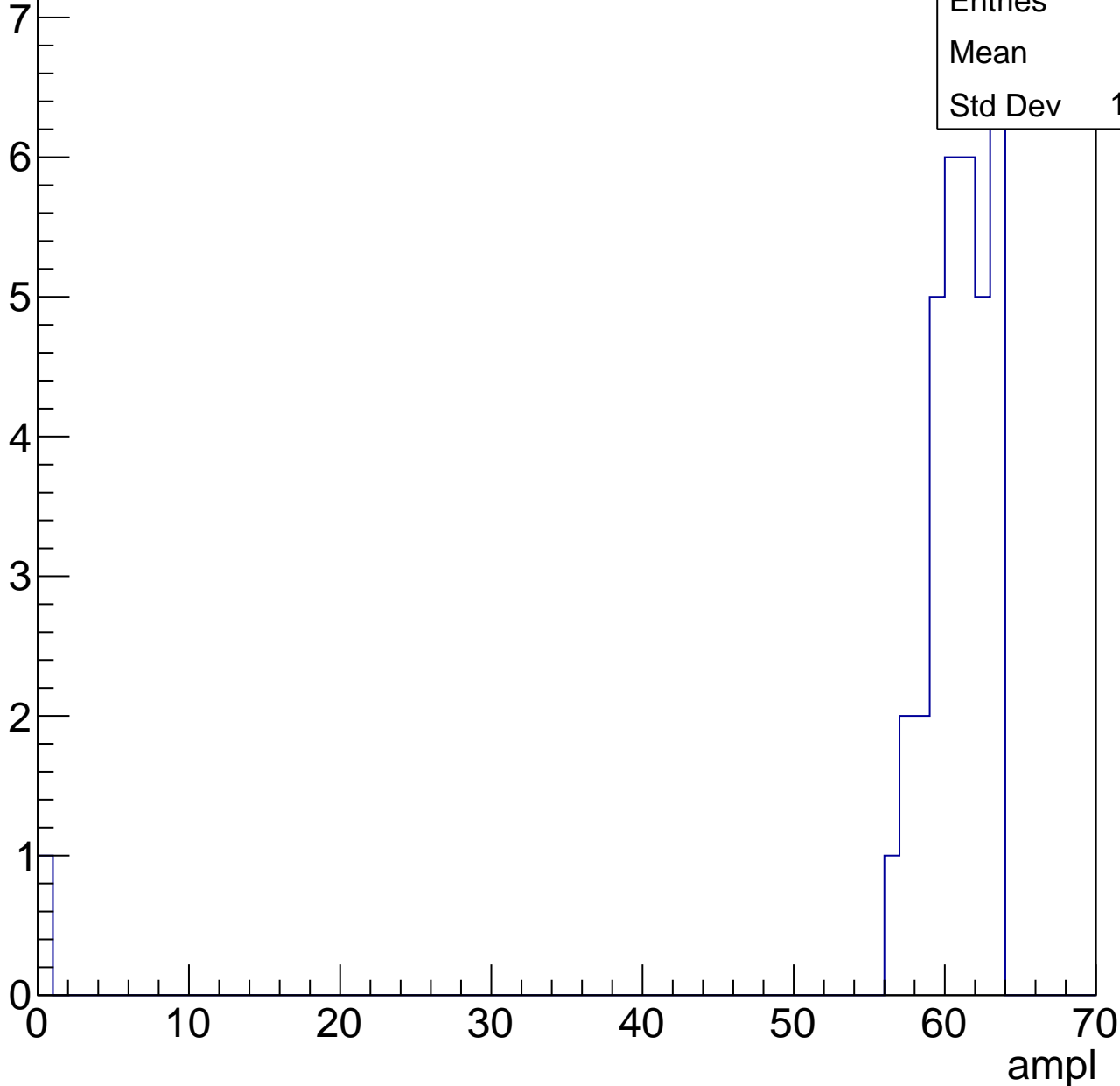


B1L102S, U17-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

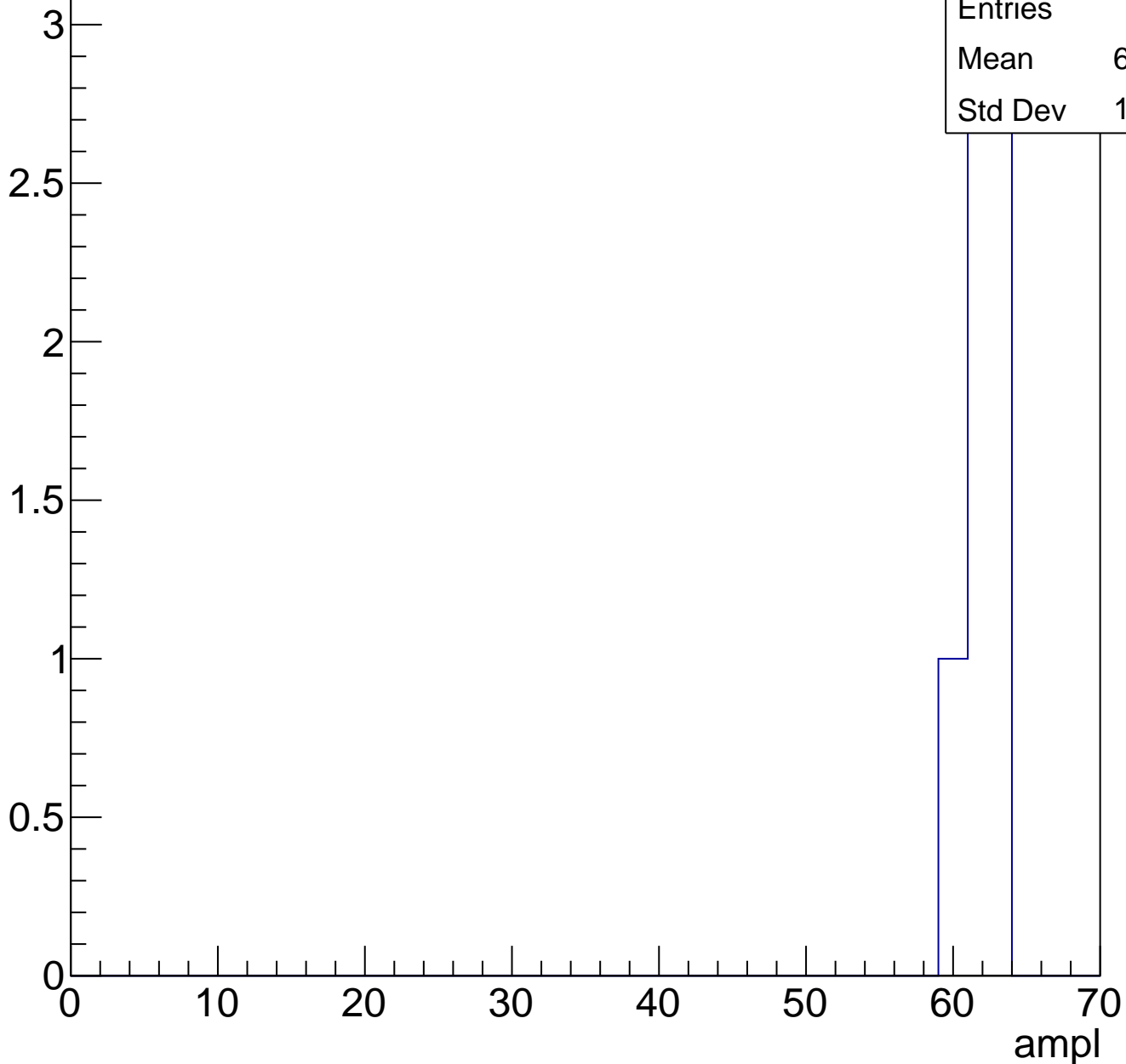
Entries	35
Mean	58.8
Std Dev	10.26



B1L102S, U17-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U17-ch118, adc0

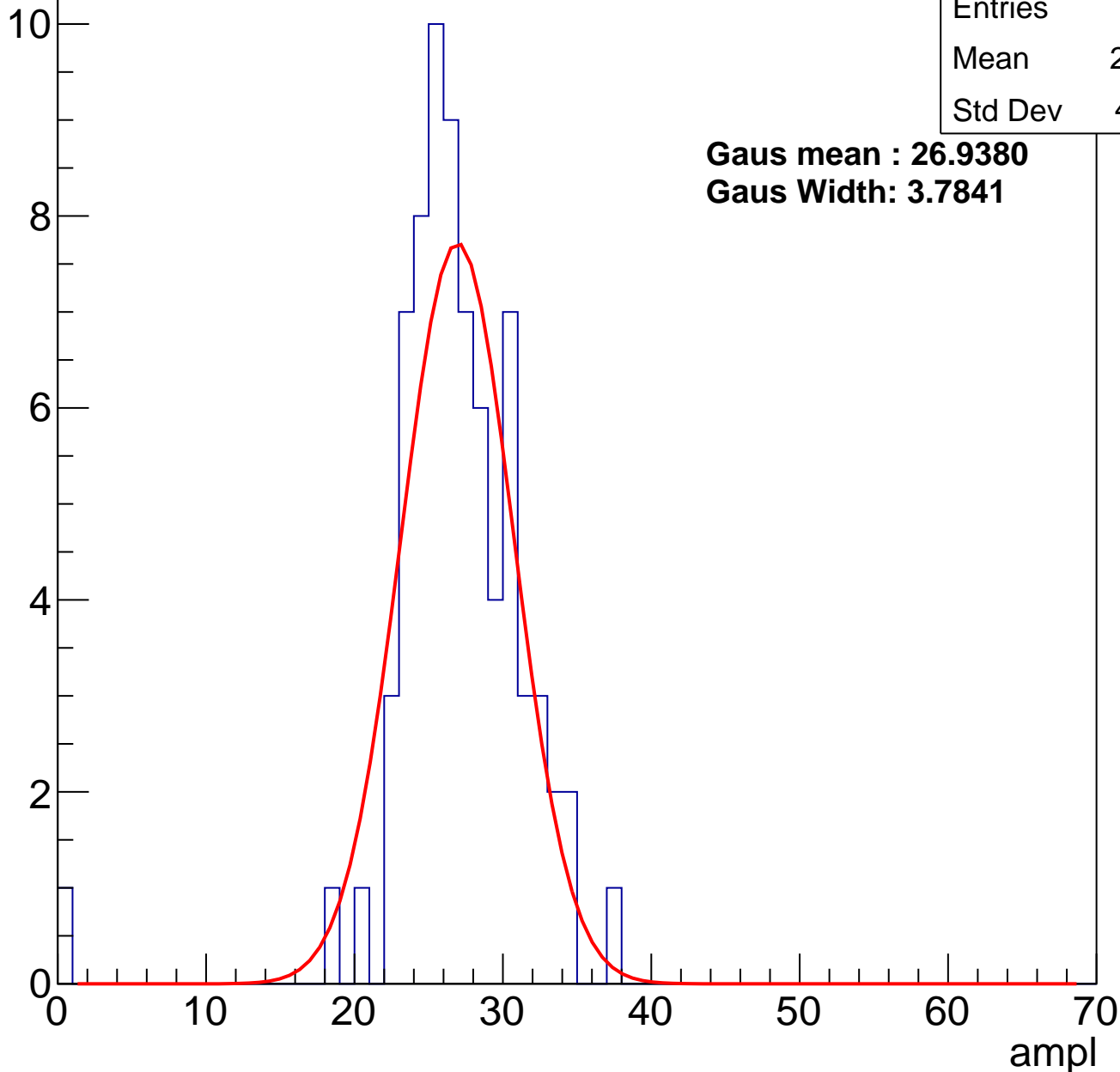
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	26.45
Std Dev	4.651

Gaus mean : 26.9380

Gaus Width: 3.7841

Entry



B1L102S, U17-ch118, adc1

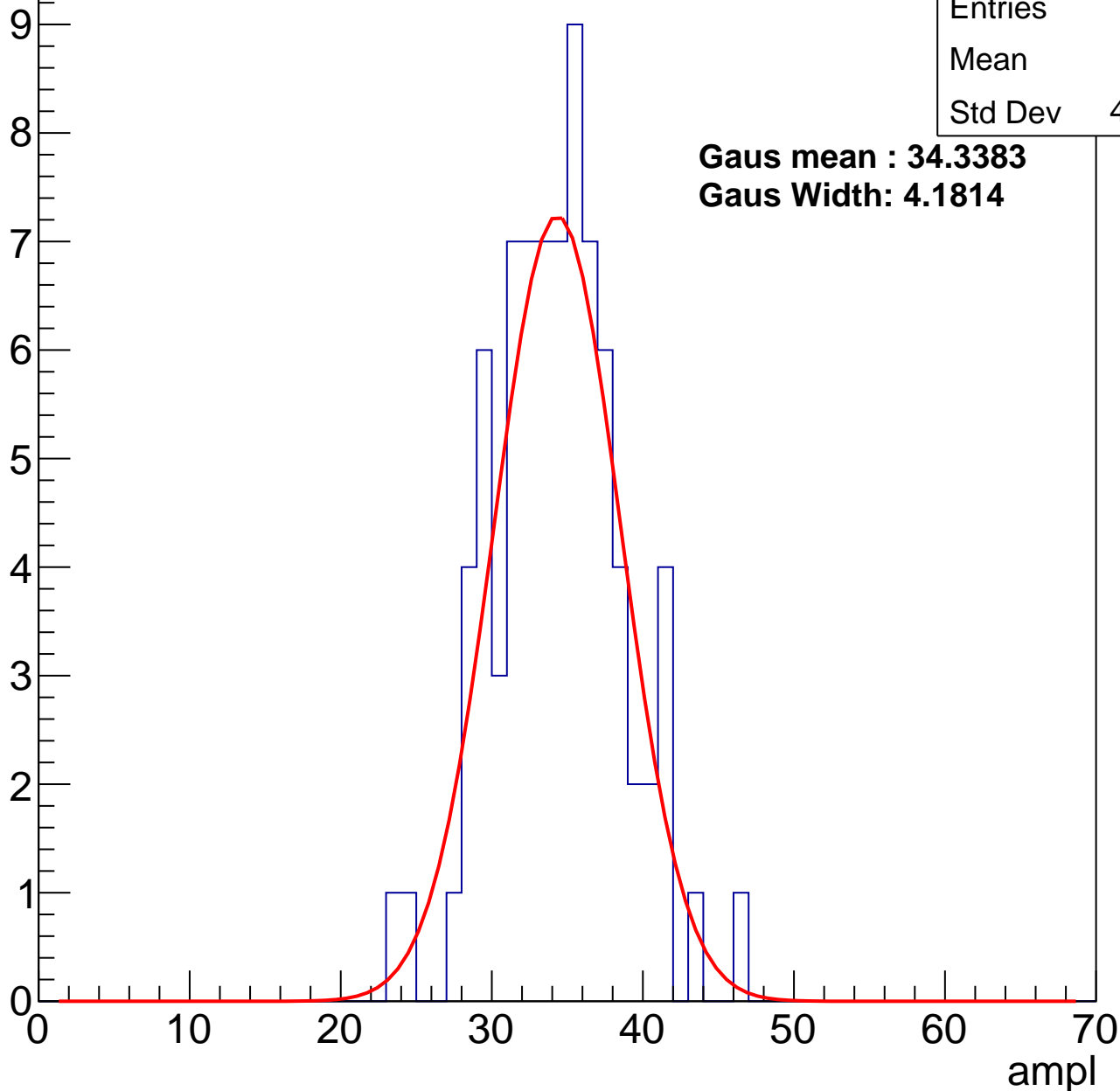
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	33.9
Std Dev	4.194

Gaus mean : 34.3383

Gaus Width: 4.1814



B1L102S, U17-ch118, adc2

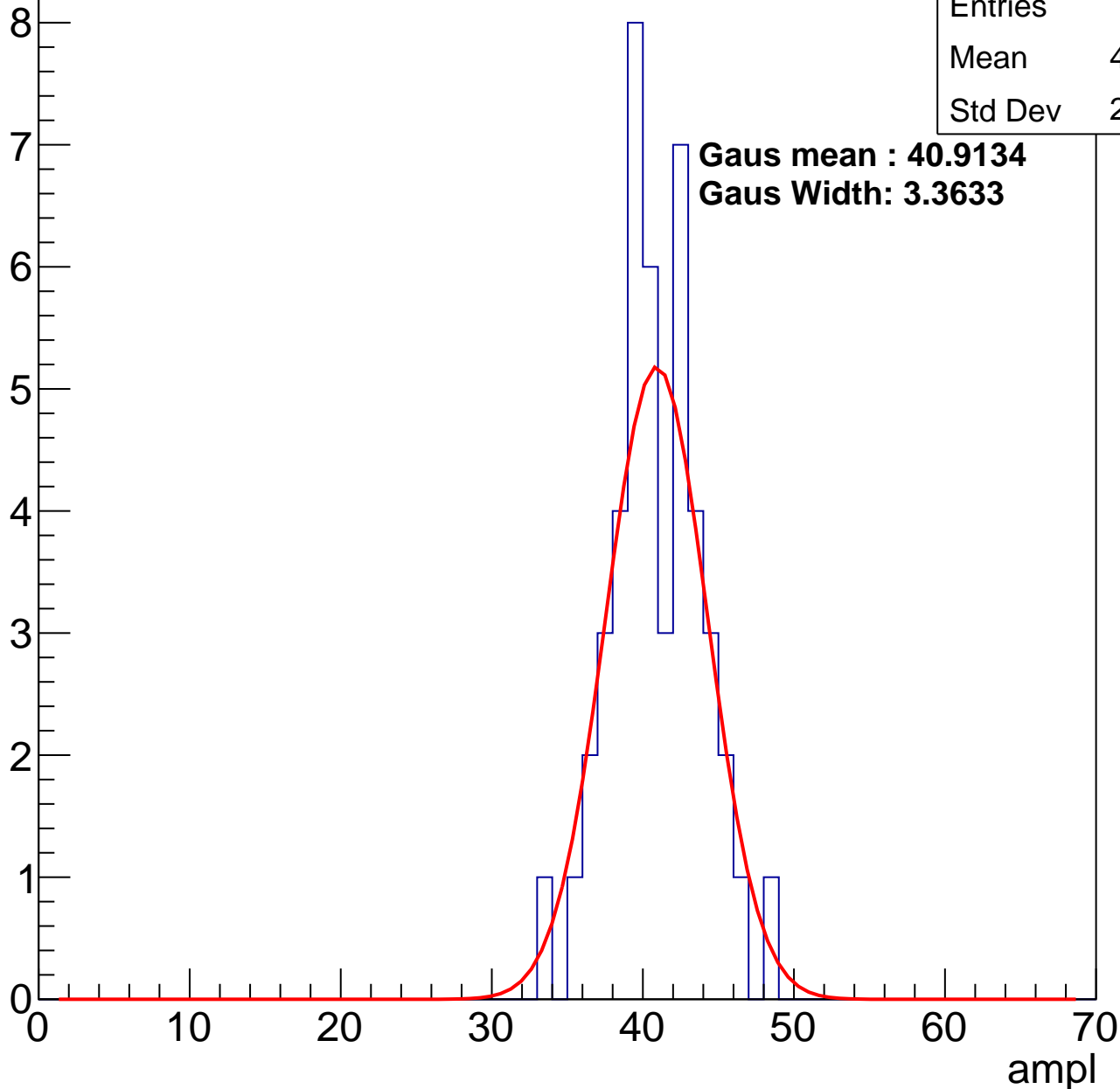
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	40.43
Std Dev	2.997

Gaus mean : 40.9134

Gaus Width: 3.3633

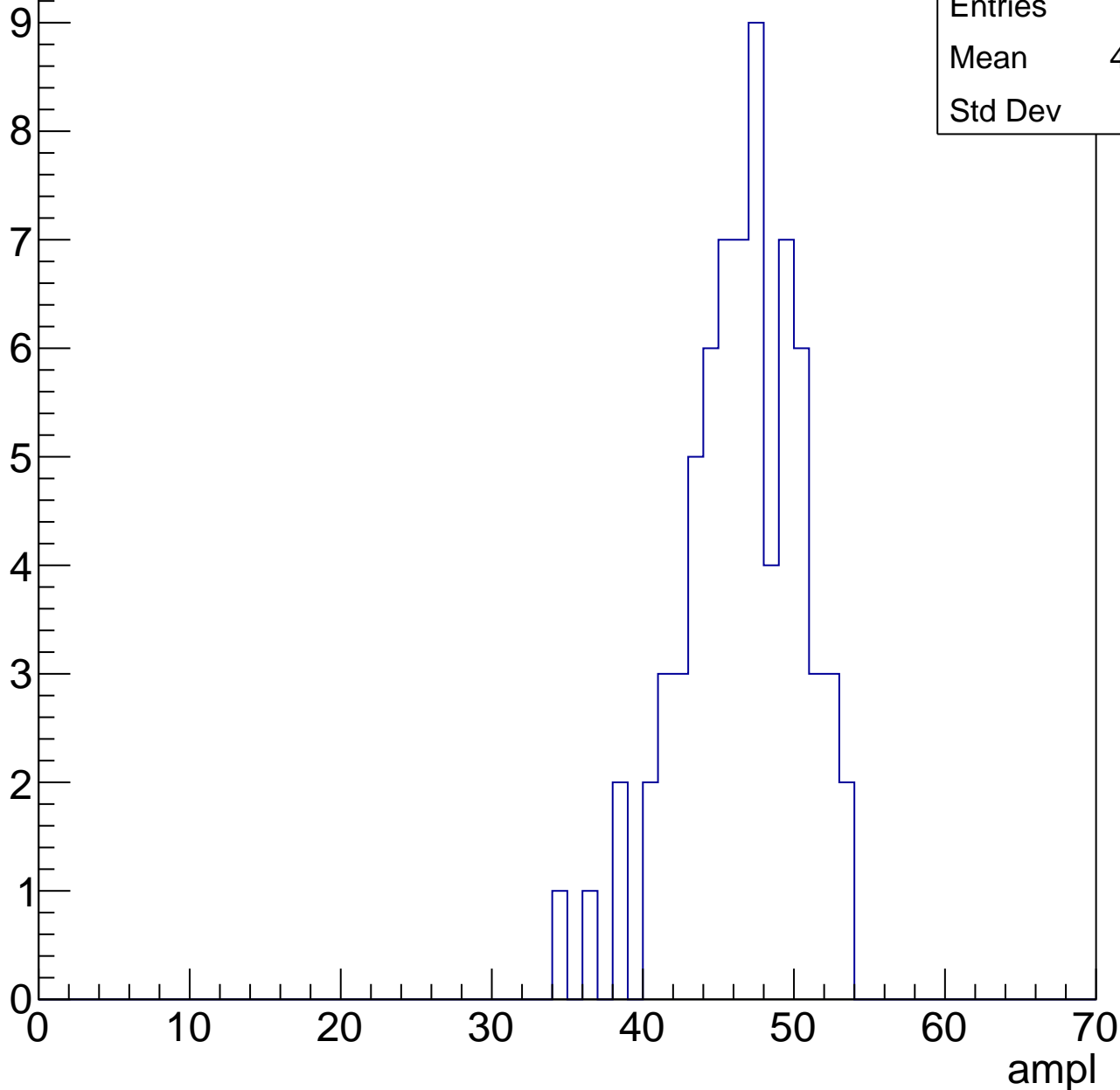


B1L102S, U17-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	45.97
Std Dev	3.95

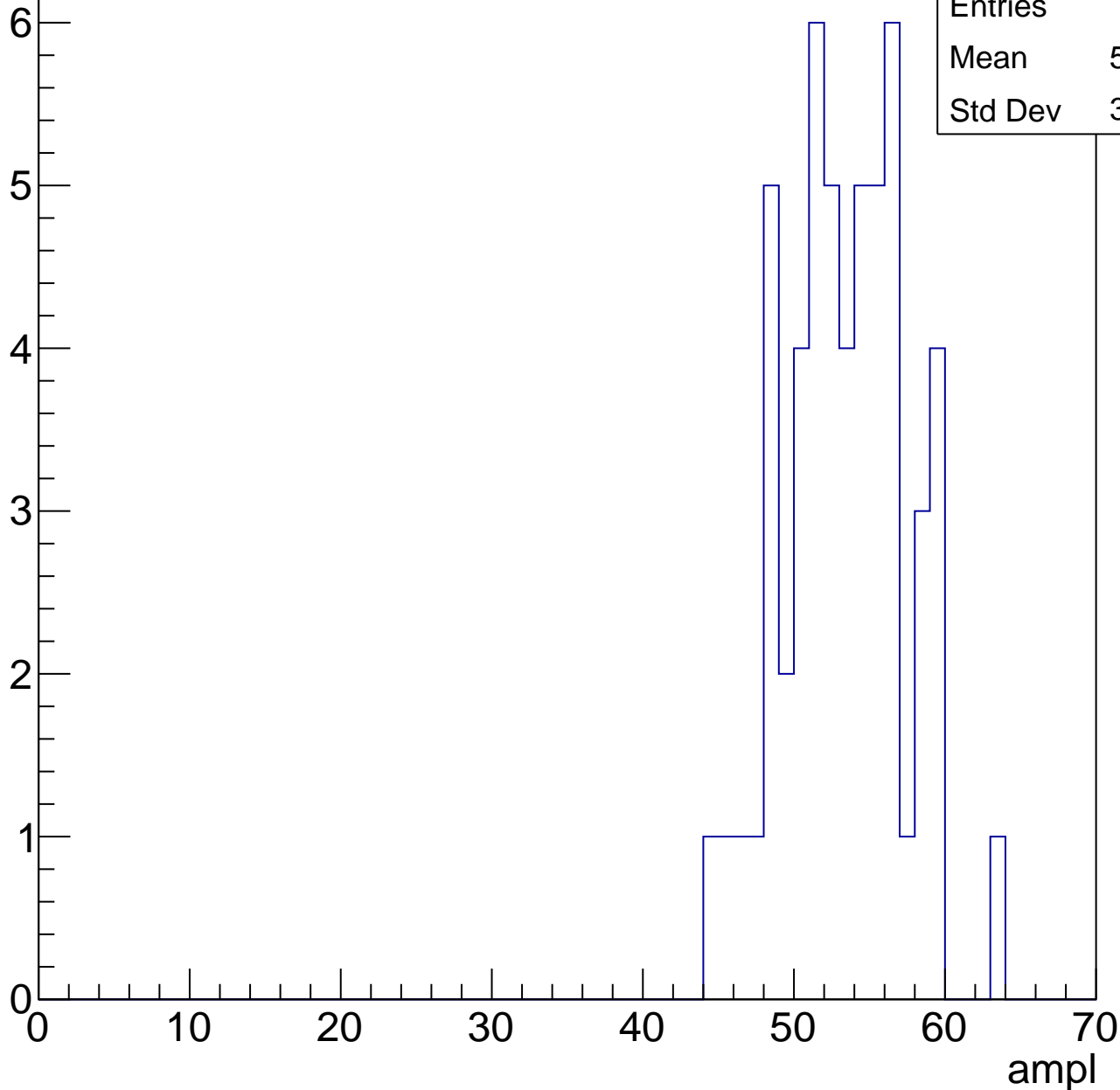


B1L102S, U17-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	52.89
Std Dev	3.967

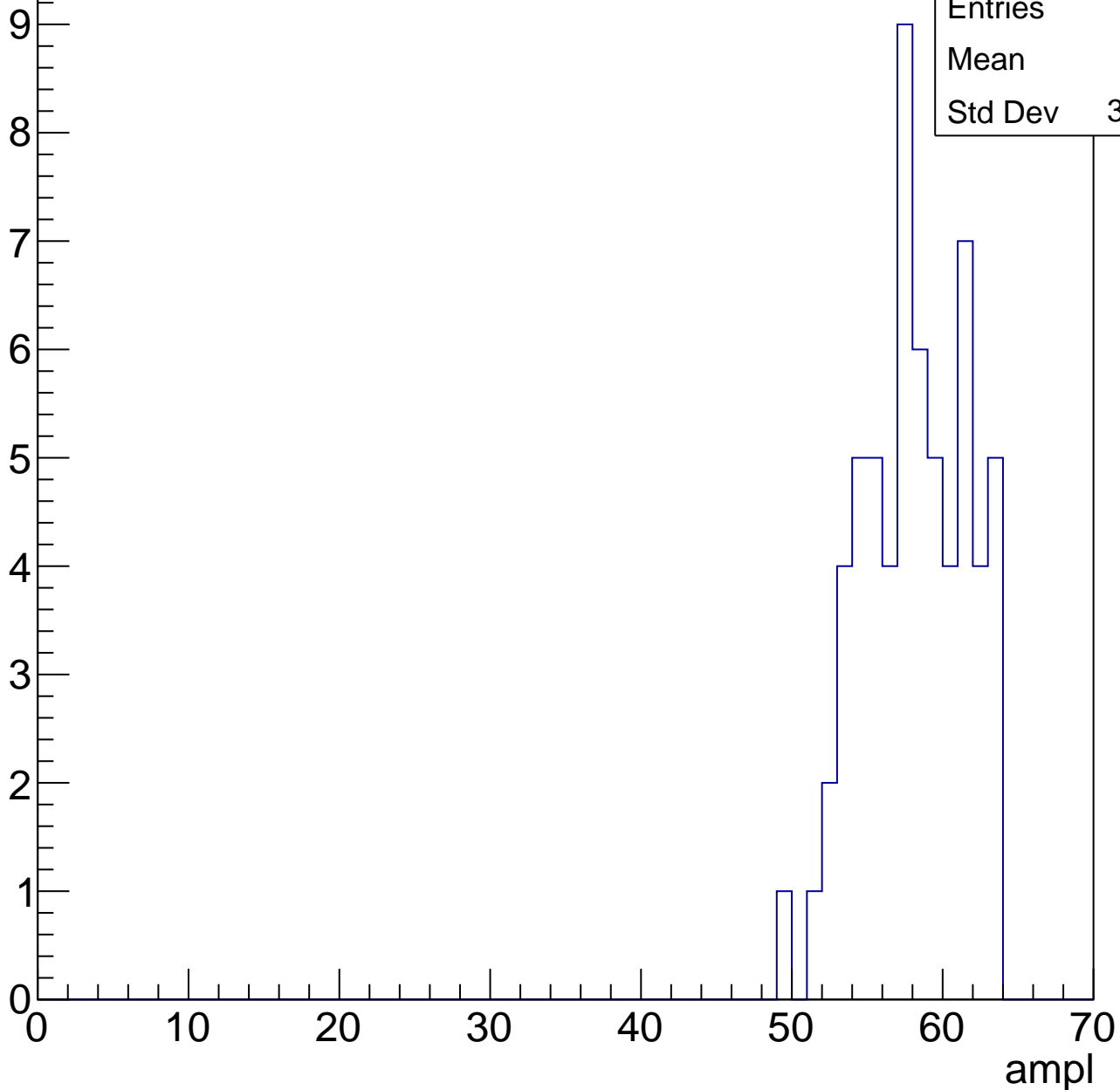


B1L102S, U17-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	57.6
Std Dev	3.396

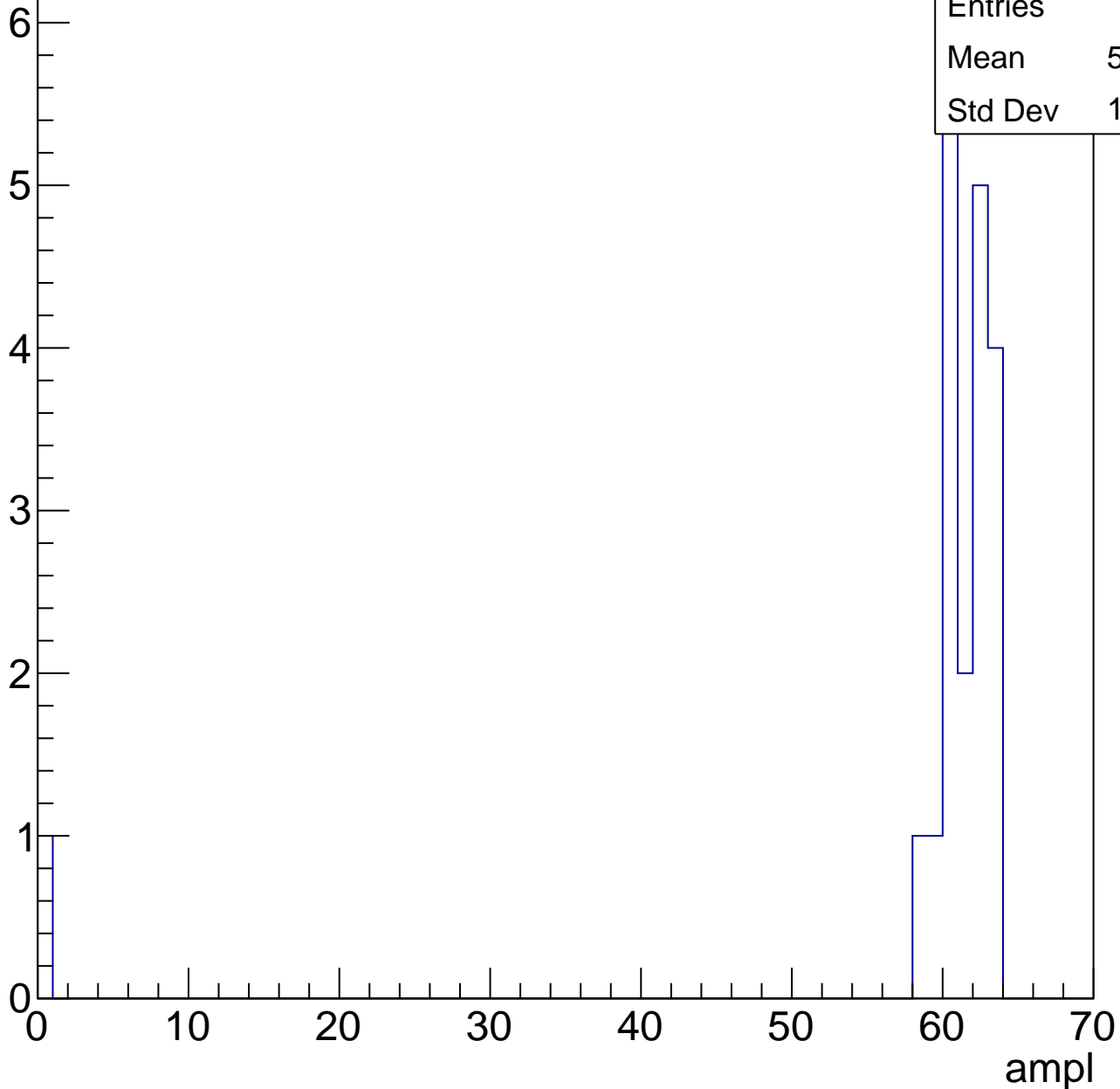


B1L102S, U17-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

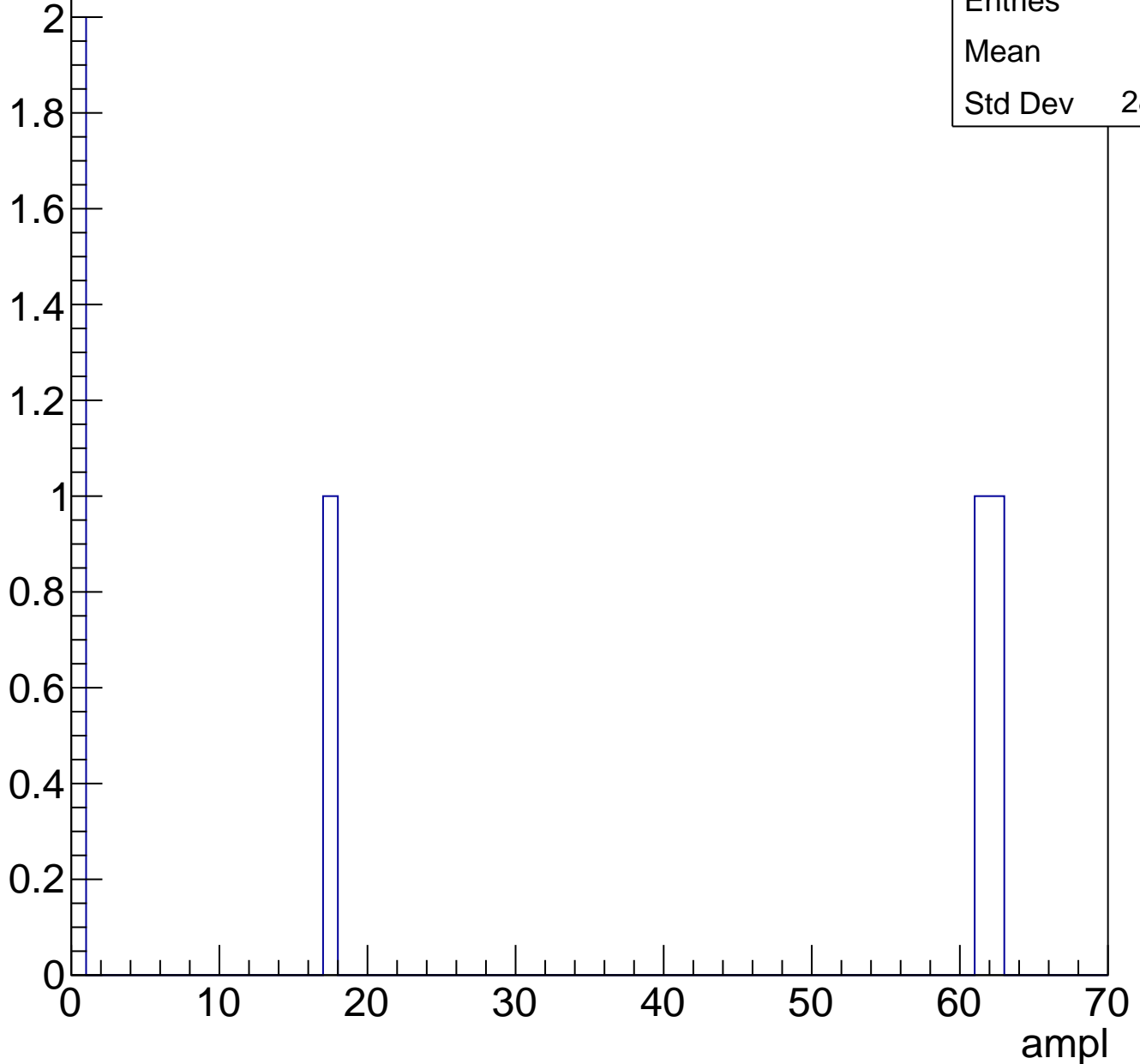
Entries	20
Mean	58.05
Std Dev	13.39



B1L102S, U17-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	28
Std Dev	28.05

B1L102S, U17-ch119, adc0

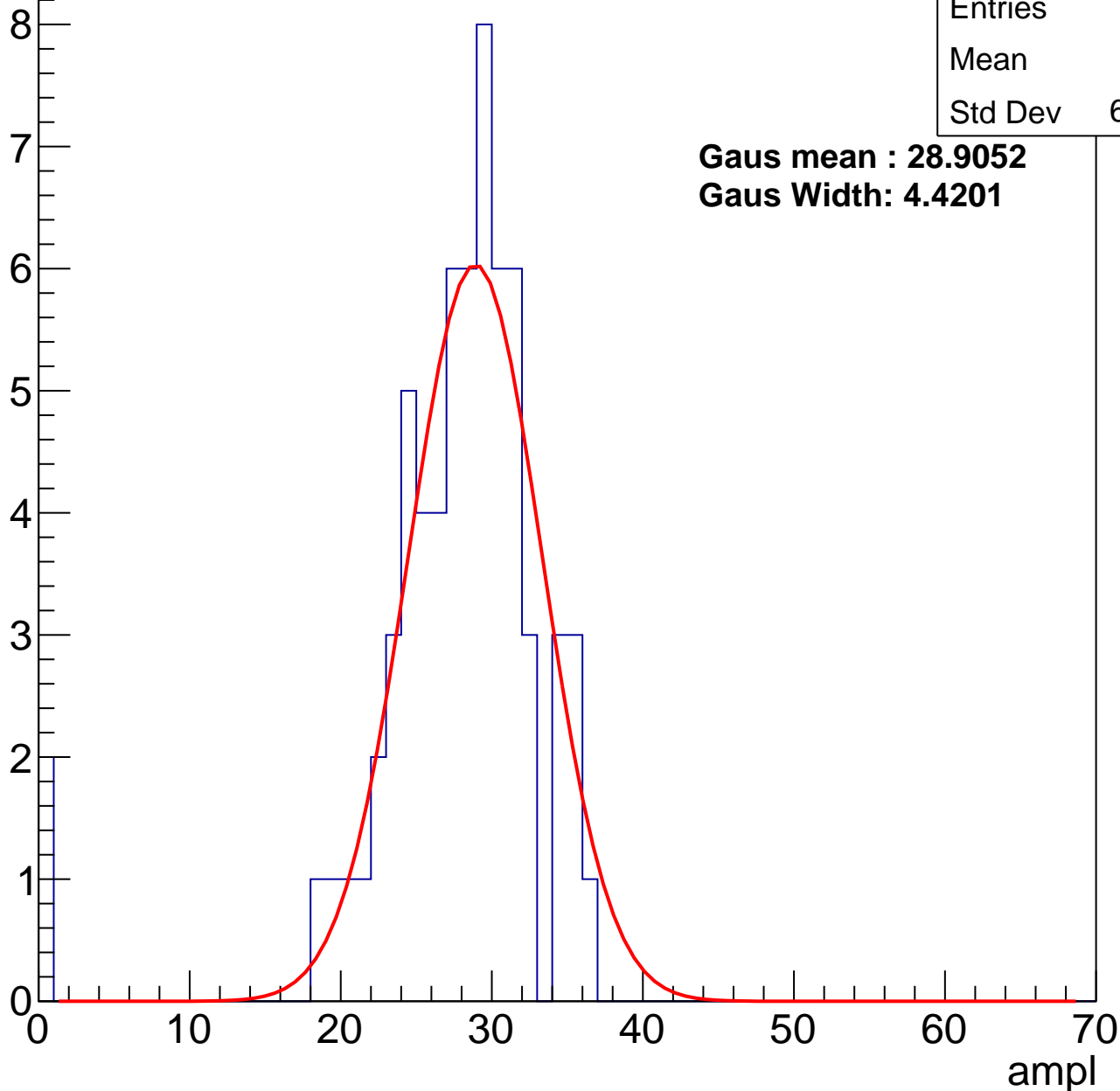
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27
Std Dev	6.196

Gaus mean : 28.9052

Gaus Width: 4.4201



B1L102S, U17-ch119, adc1

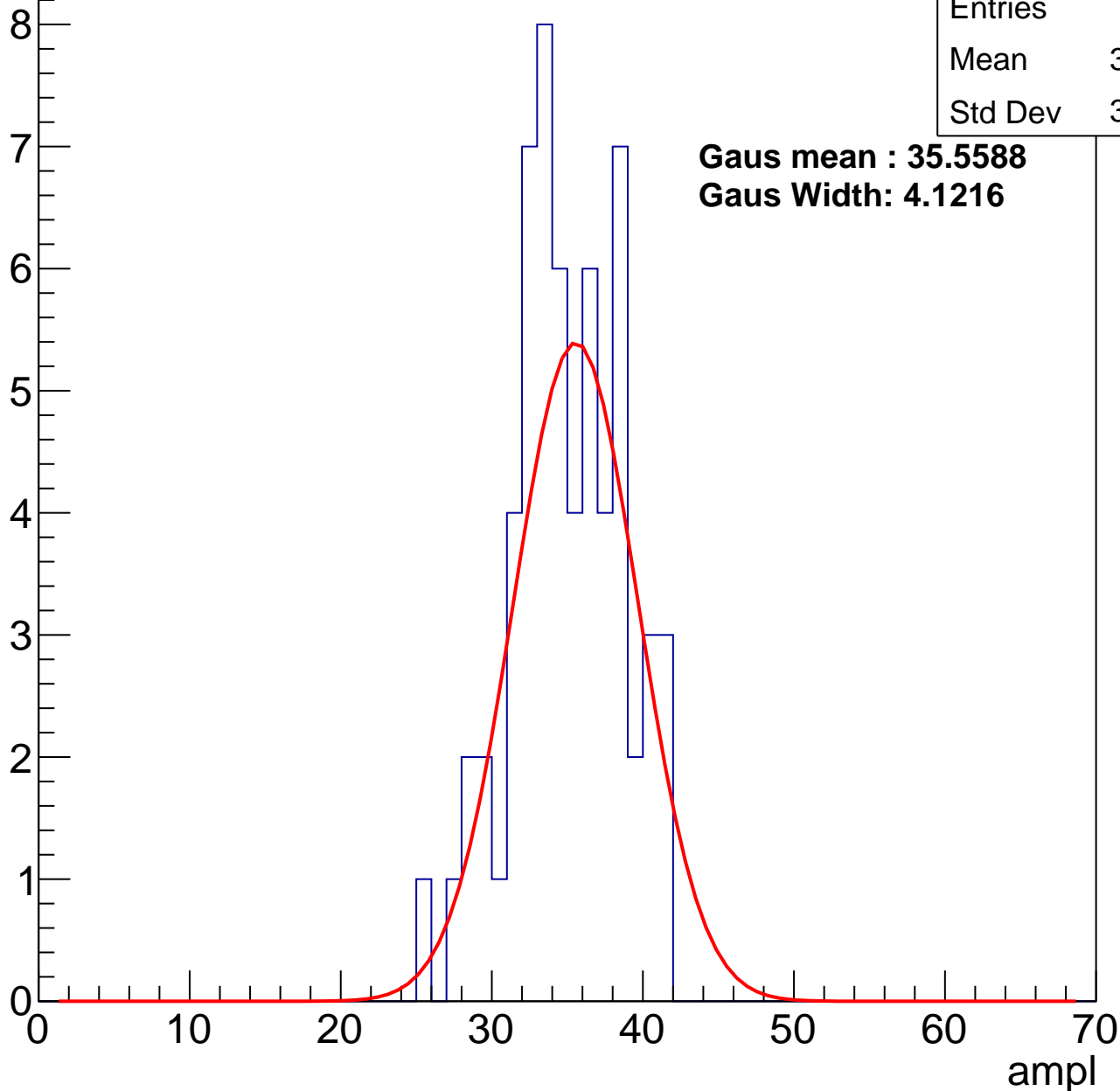
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.48
Std Dev	3.638

Gaus mean : 35.5588

Gaus Width: 4.1216



B1L102S, U17-ch119, adc2

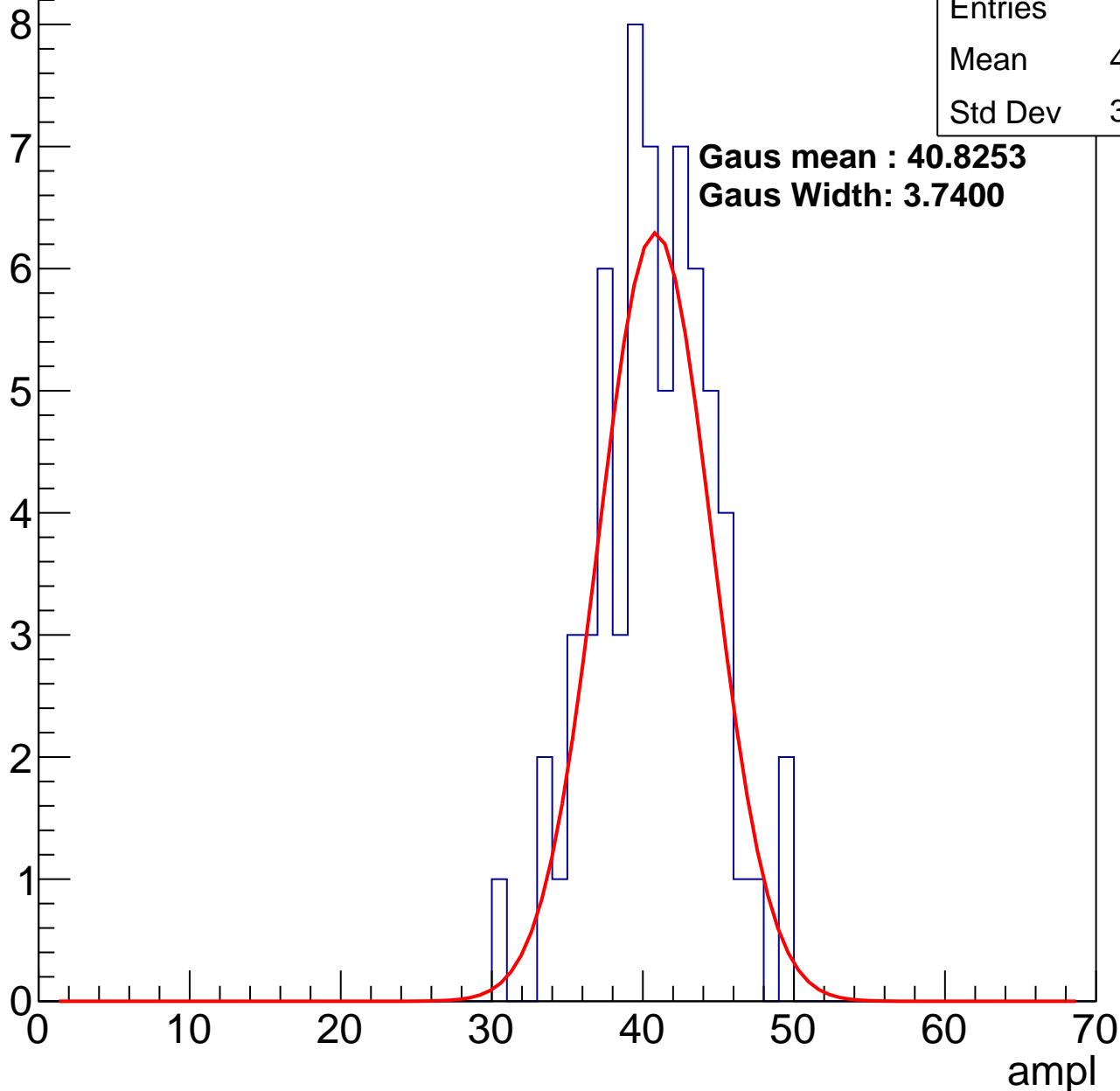
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	40.29
Std Dev	3.802

Gaus mean : 40.8253

Gaus Width: 3.7400

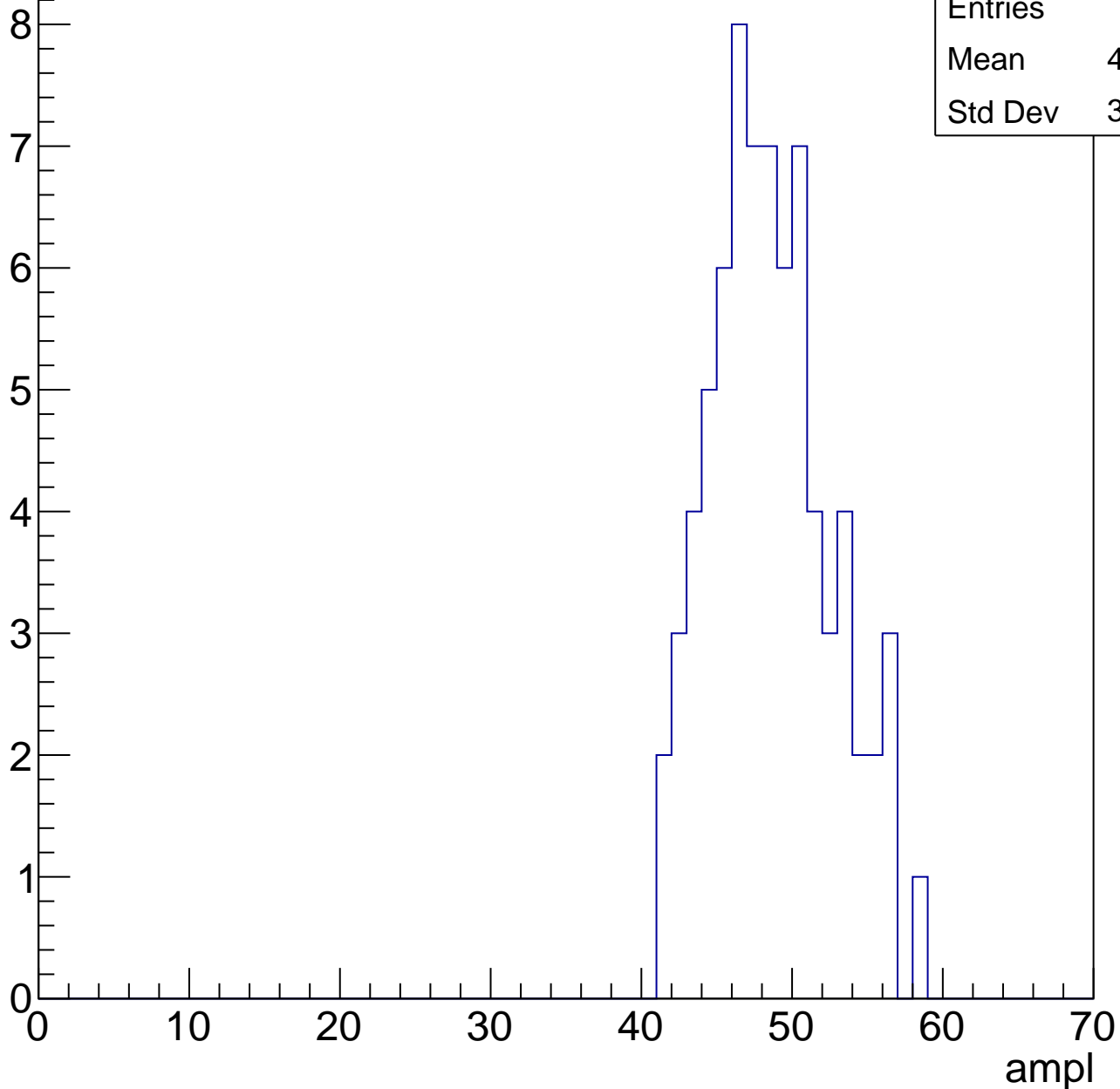


B1L102S, U17-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.15
Std Dev	3.955

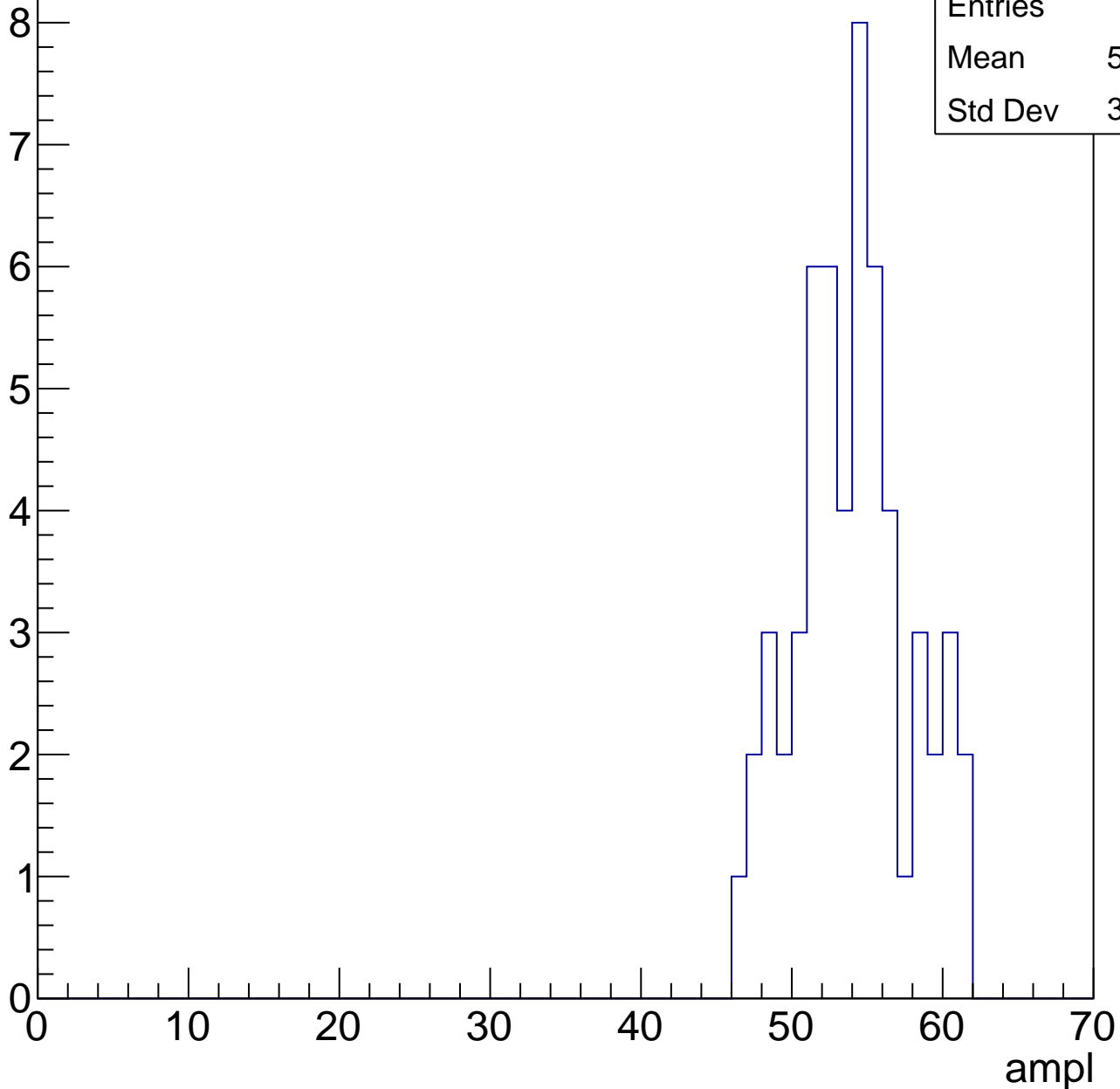


B1L102S, U17-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.55
Std Dev	3.717

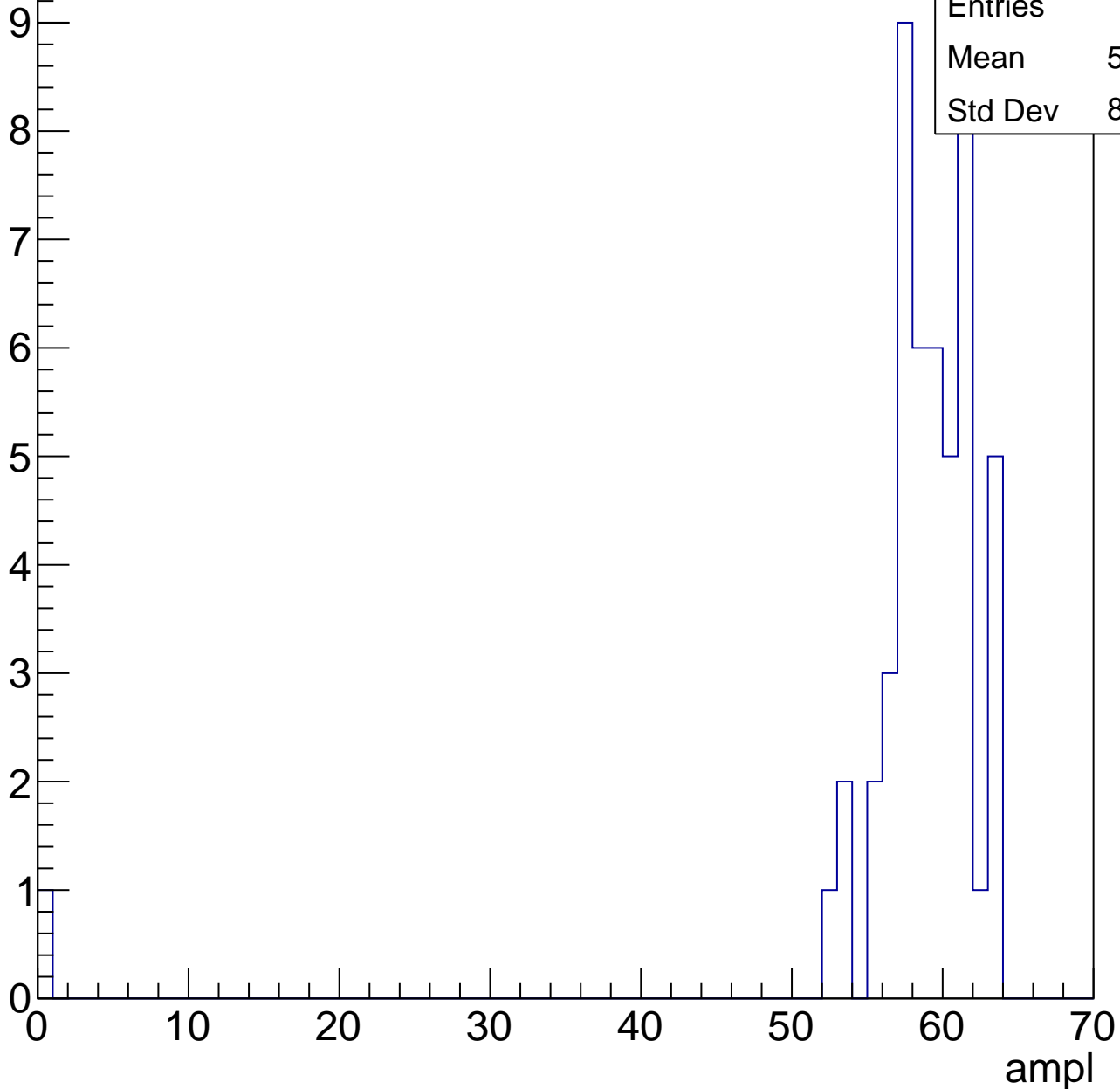


B1L102S, U17-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.47
Std Dev	8.706

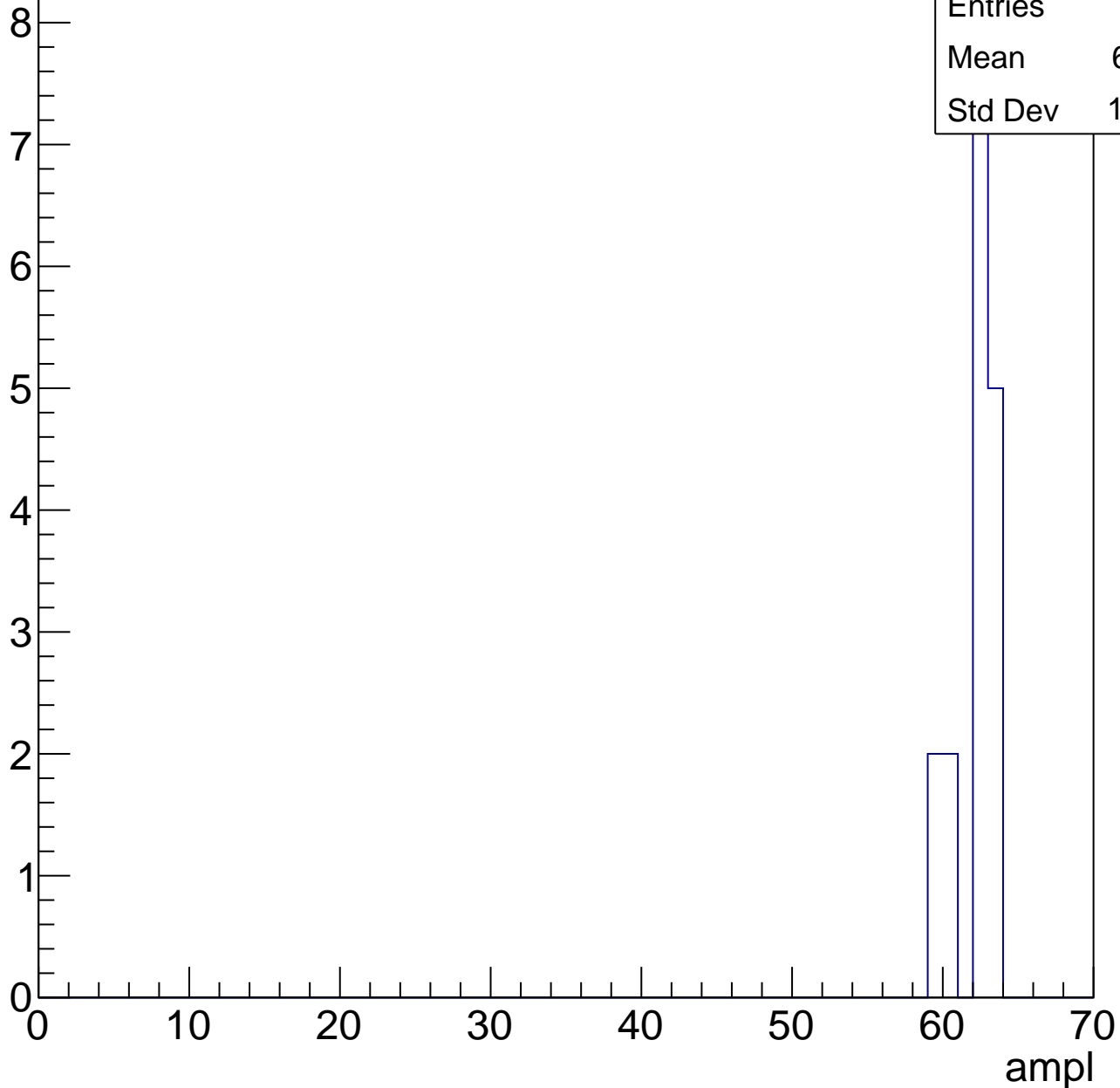


B1L102S, U17-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.71
Std Dev	1.318



B1L102S, U17-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch120, adc0

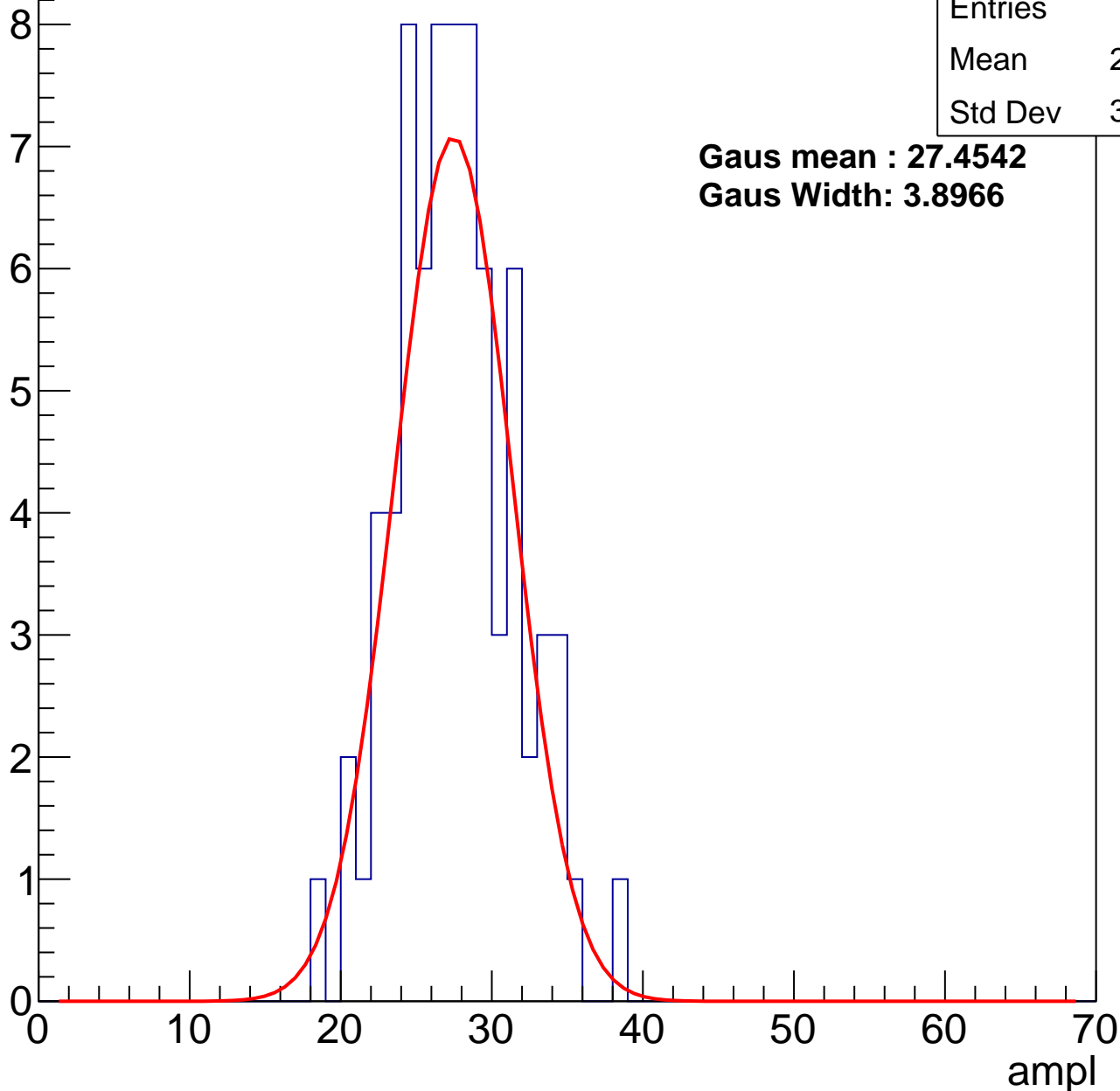
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.16
Std Dev	3.875

Gaus mean : 27.4542

Gaus Width: 3.8966



B1L102S, U17-ch120, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	34.42
Std Dev	3.612

Gaus mean : 34.3060

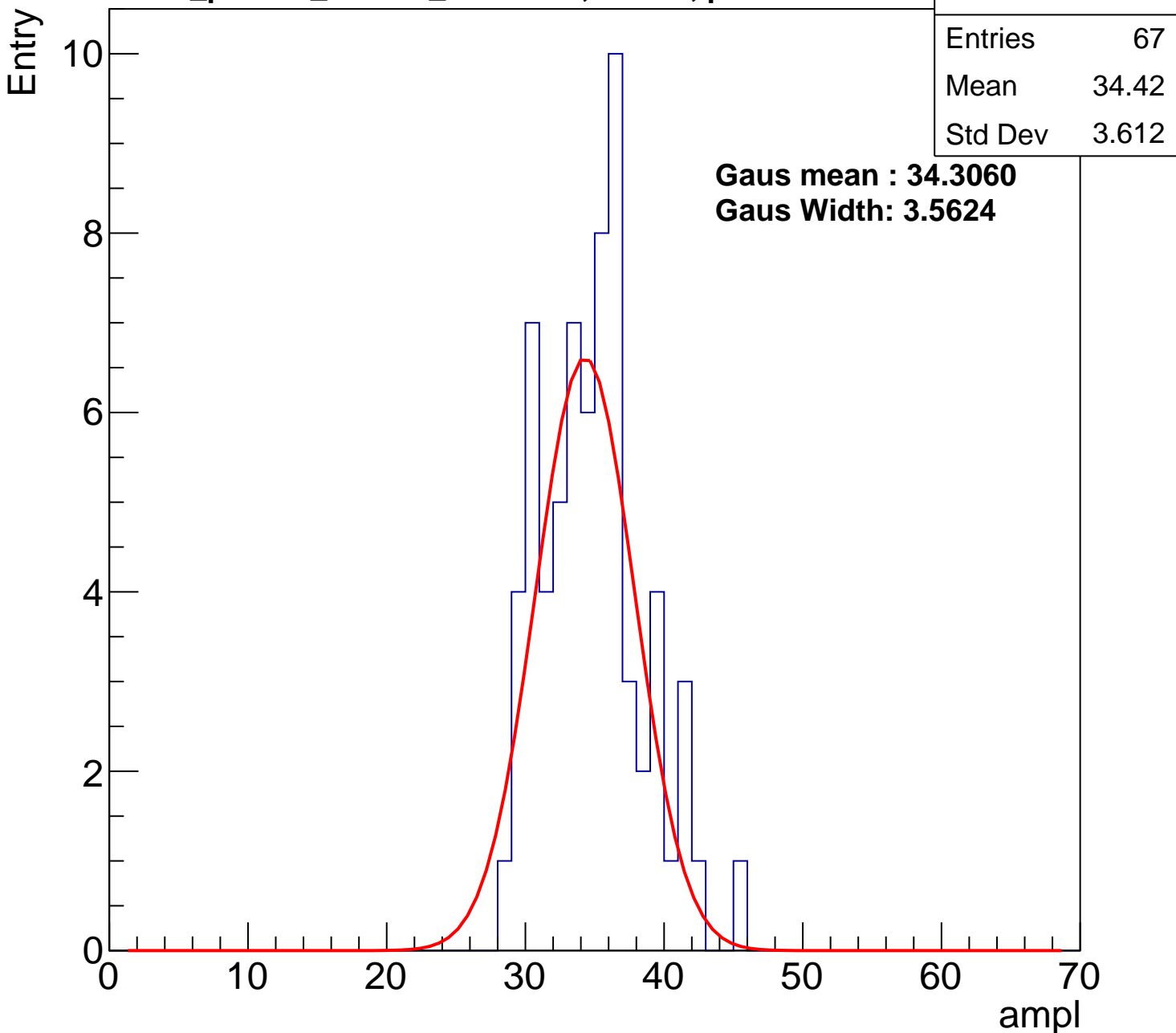
Gaus Width: 3.5624

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U17-ch120, adc2

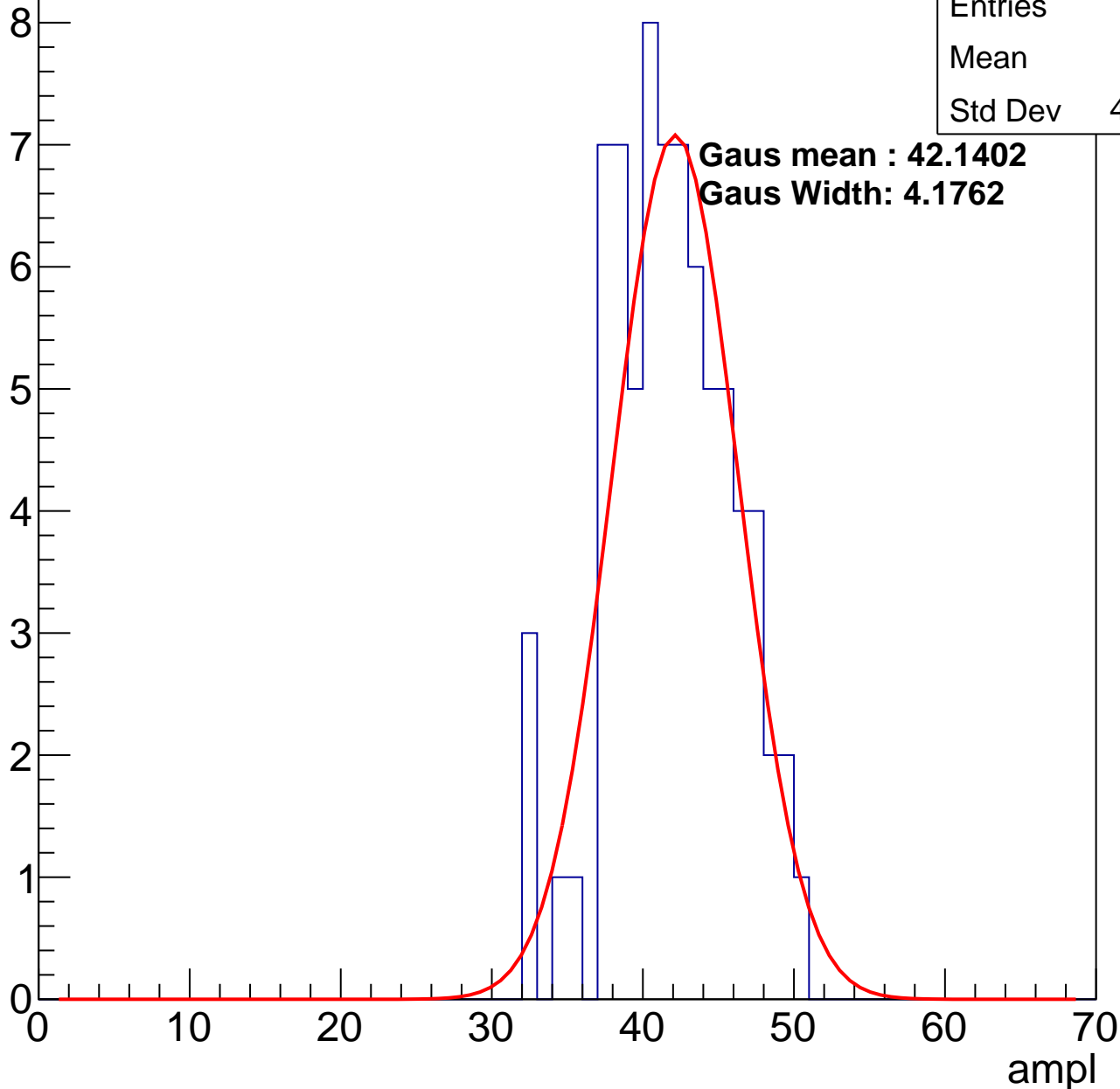
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.4
Std Dev	4.043

Gaus mean : 42.1402

Gaus Width: 4.1762

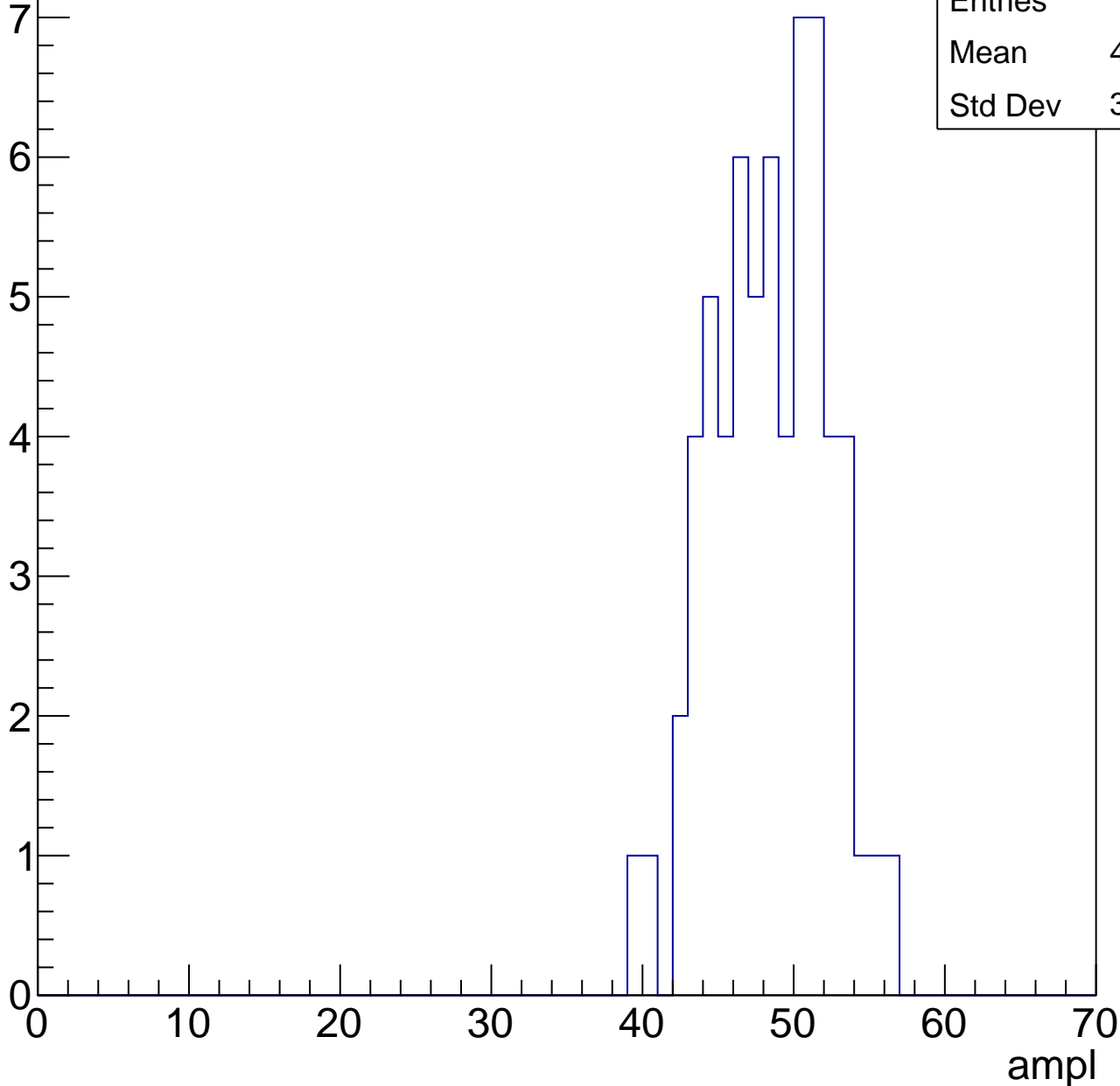


B1L102S, U17-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.97
Std Dev	3.716

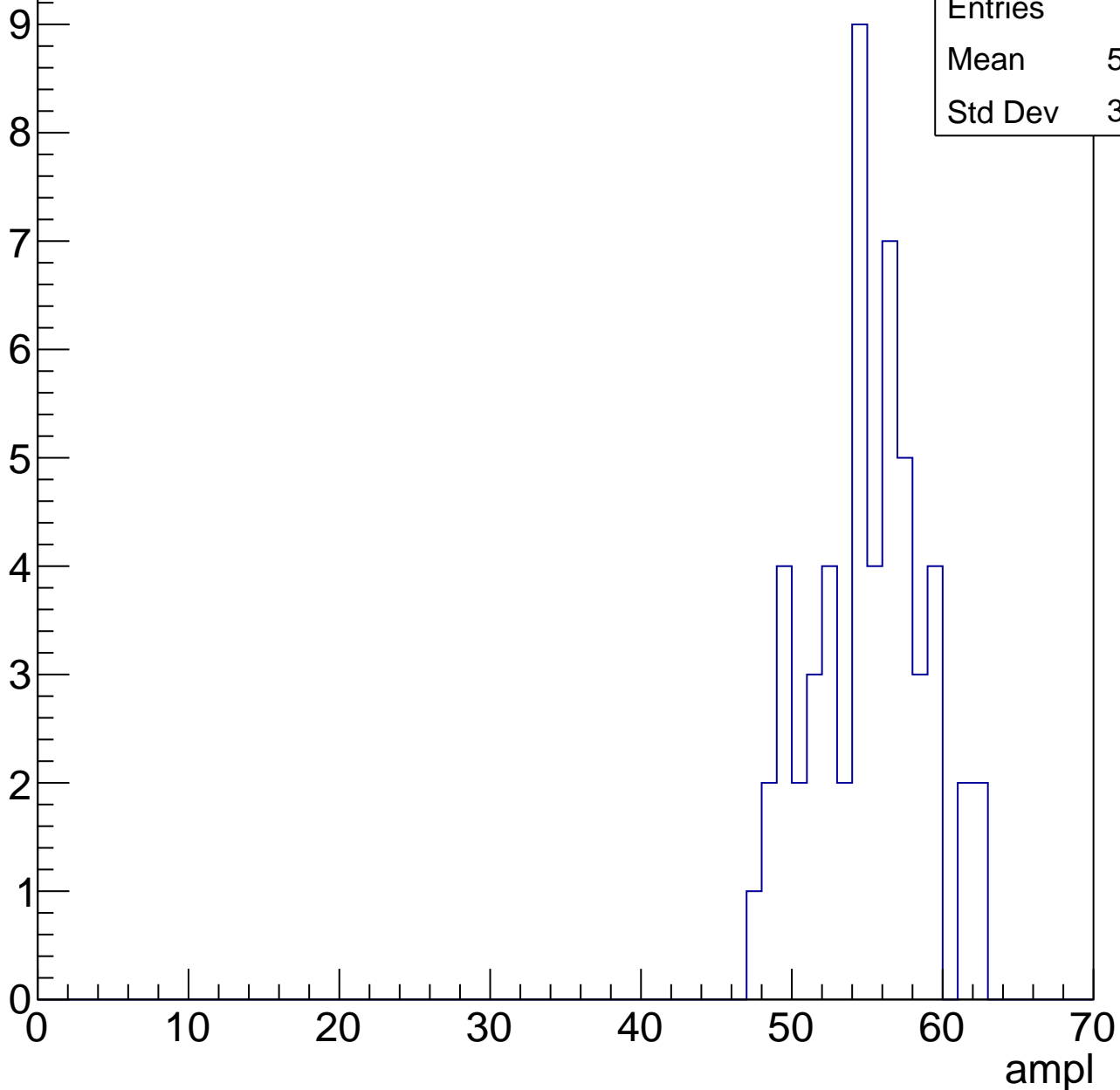


B1L102S, U17-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.54
Std Dev	3.685

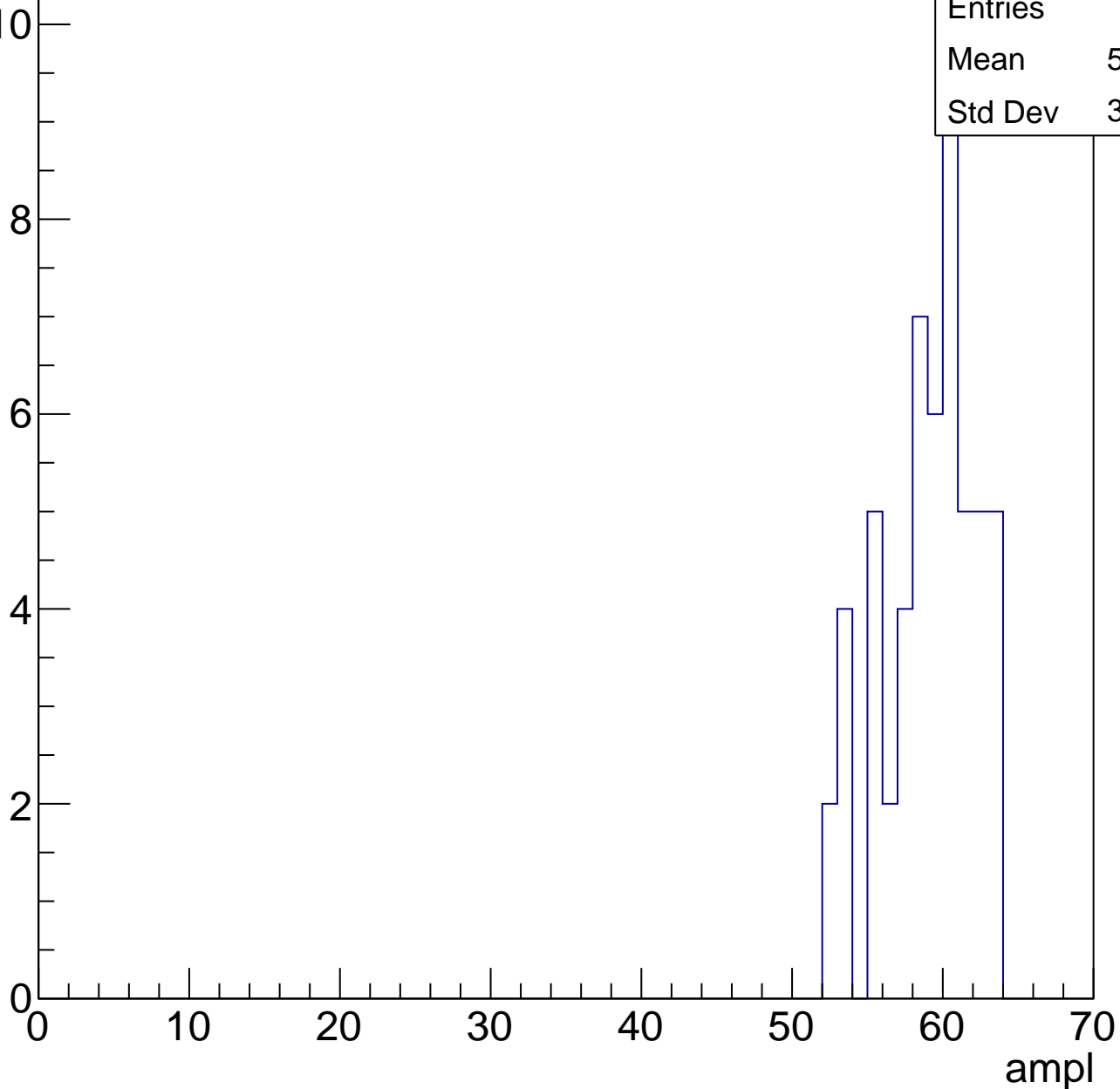


B1L102S, U17-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.56
Std Dev	3.038

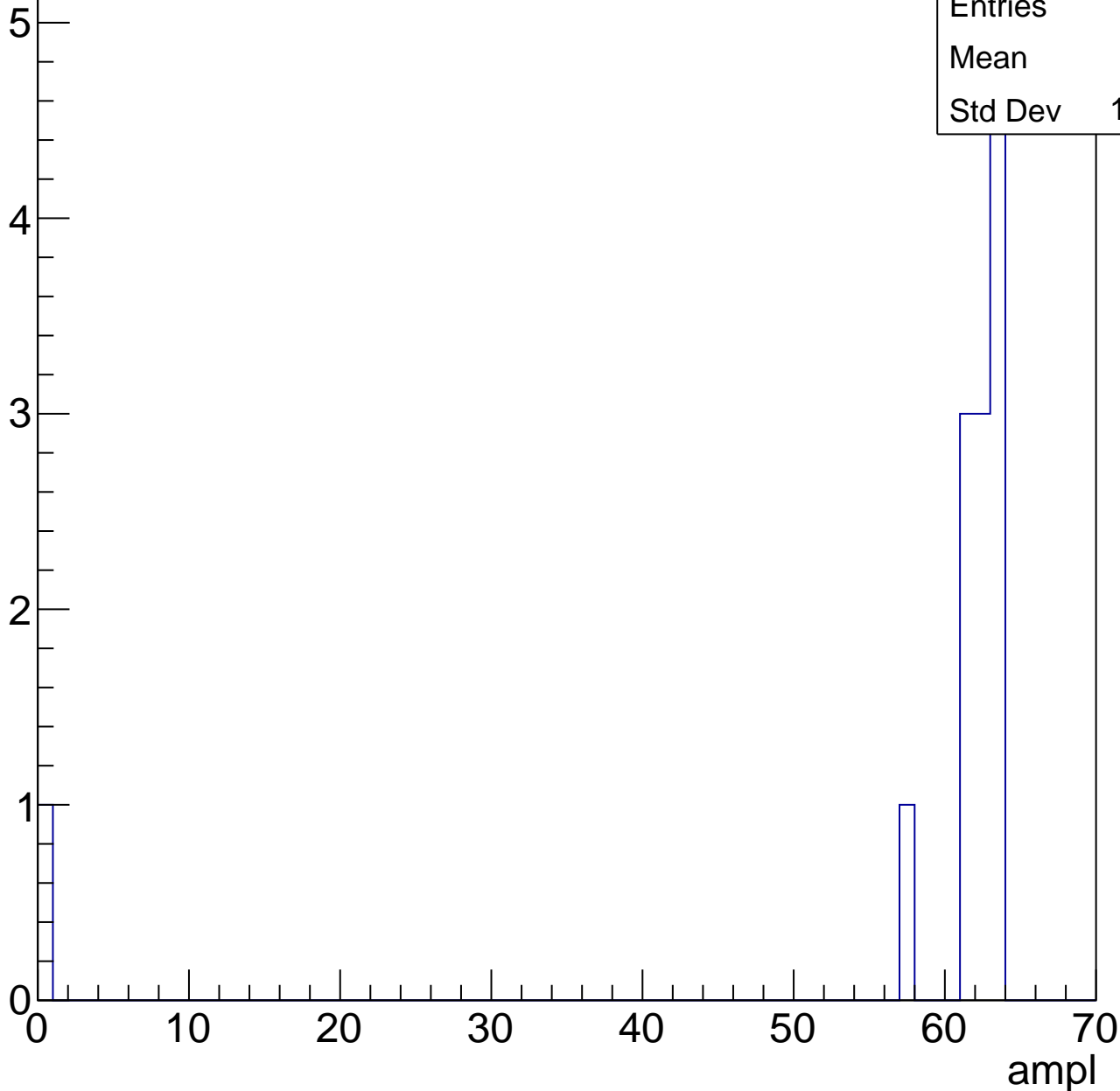


B1L102S, U17-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	57
Std Dev	16.53



B1L102S, U17-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch121, adc0

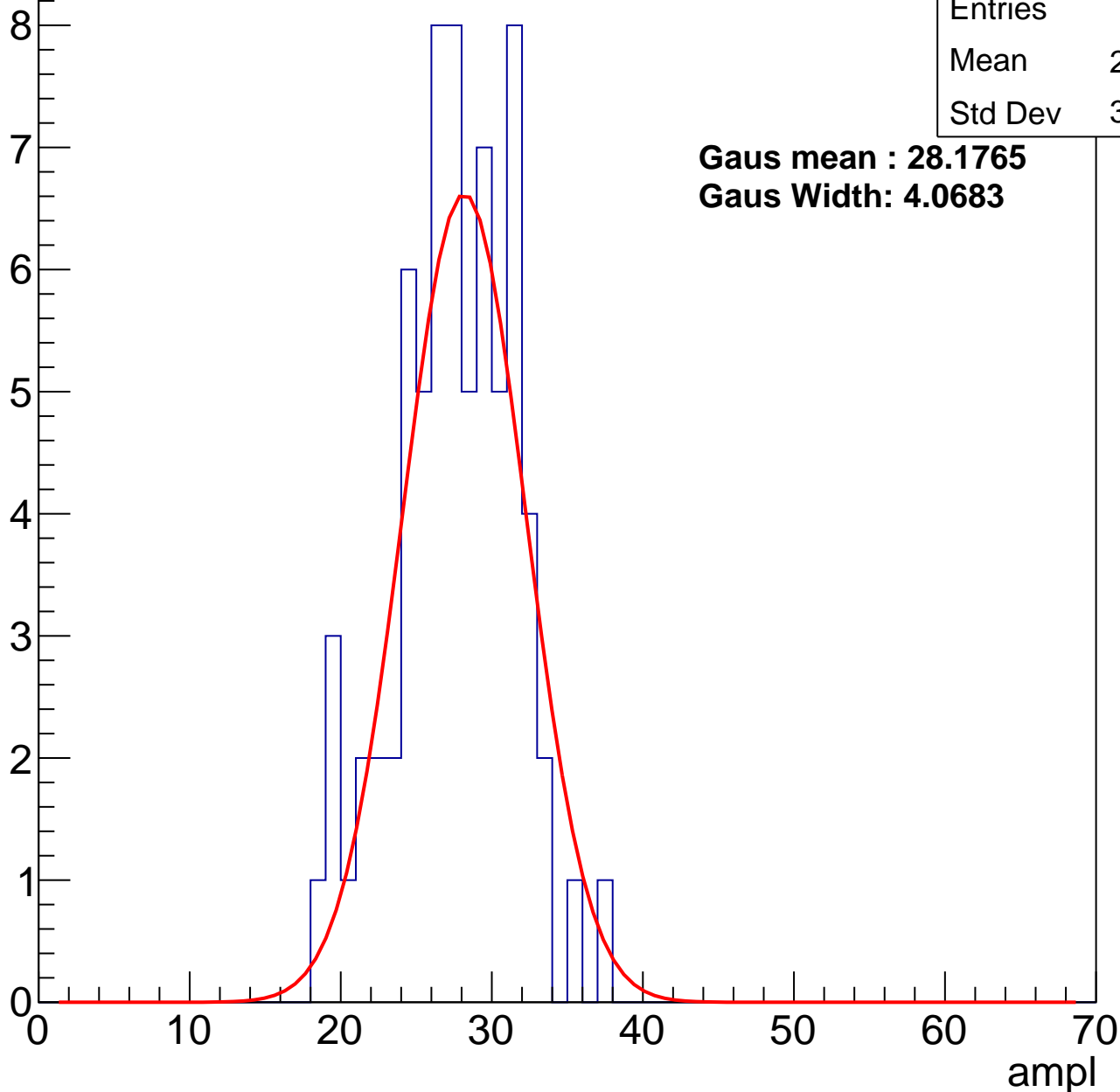
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.14
Std Dev	3.934

Gaus mean : 28.1765

Gaus Width: 4.0683



B1L102S, U17-ch121, adc1

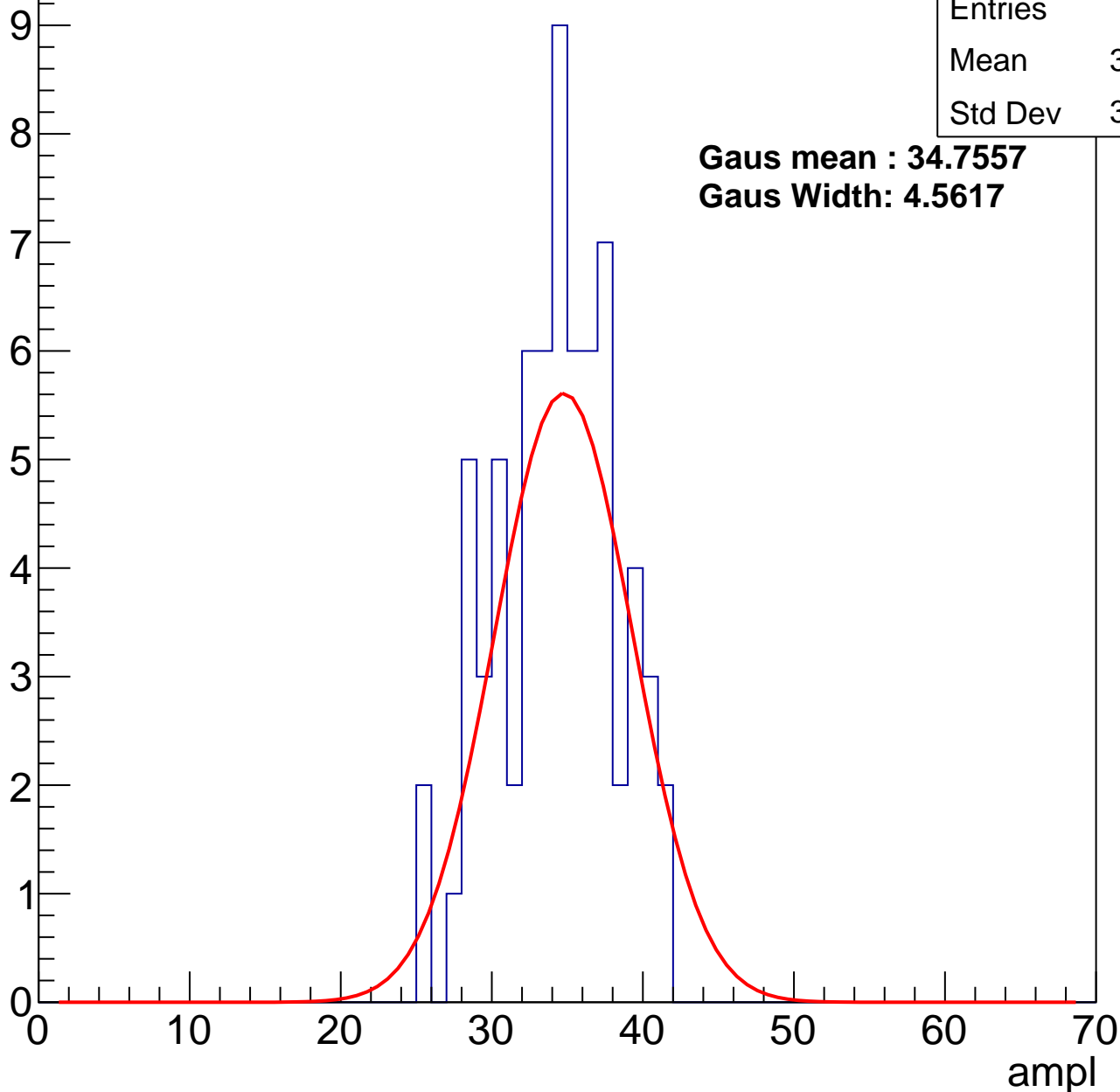
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	33.78
Std Dev	3.848

Gaus mean : 34.7557

Gaus Width: 4.5617



B1L102S, U17-ch121, adc2

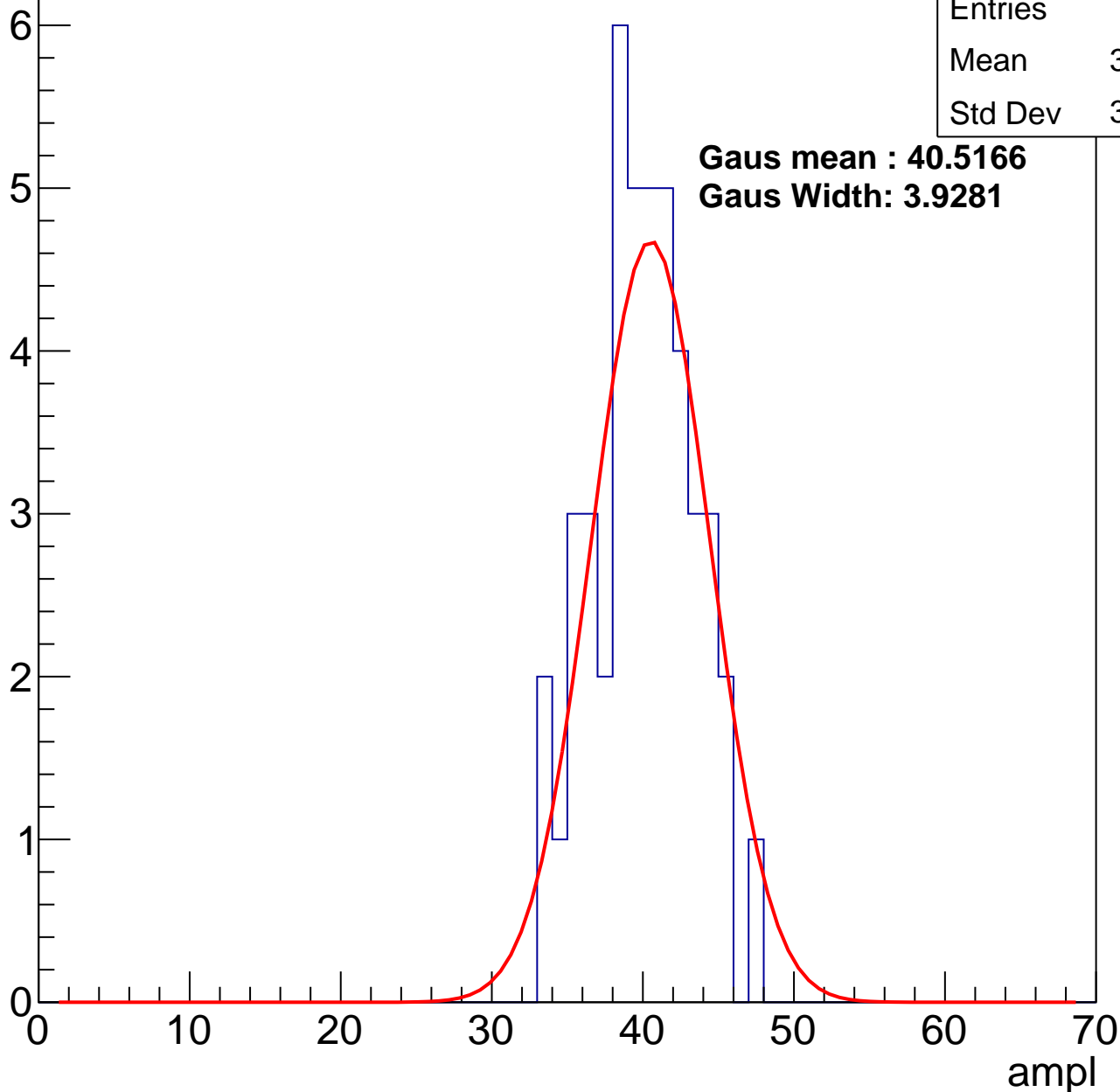
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	39.58
Std Dev	3.303

Gaus mean : 40.5166

Gaus Width: 3.9281

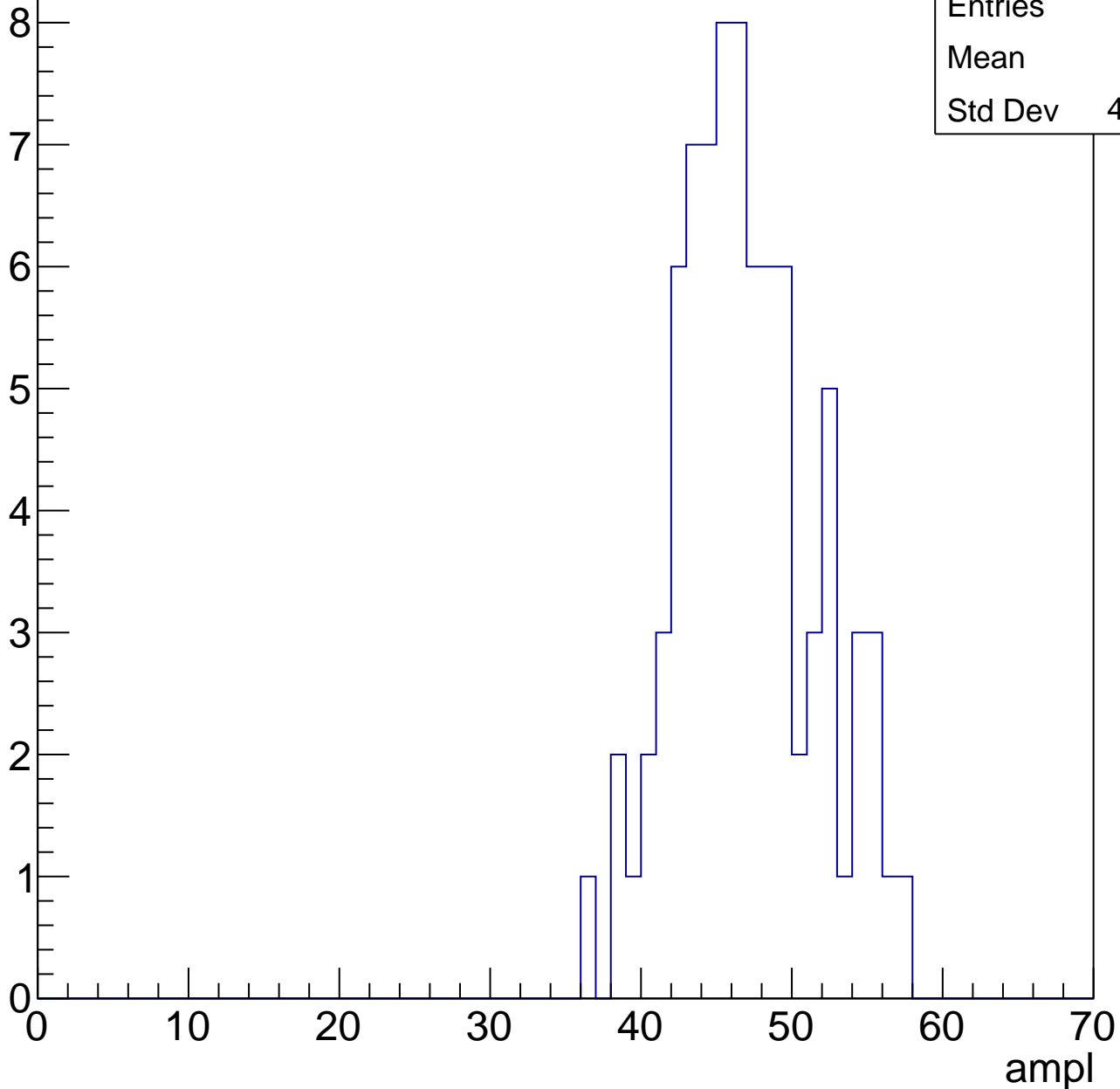


B1L102S, U17-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	46.5
Std Dev	4.519

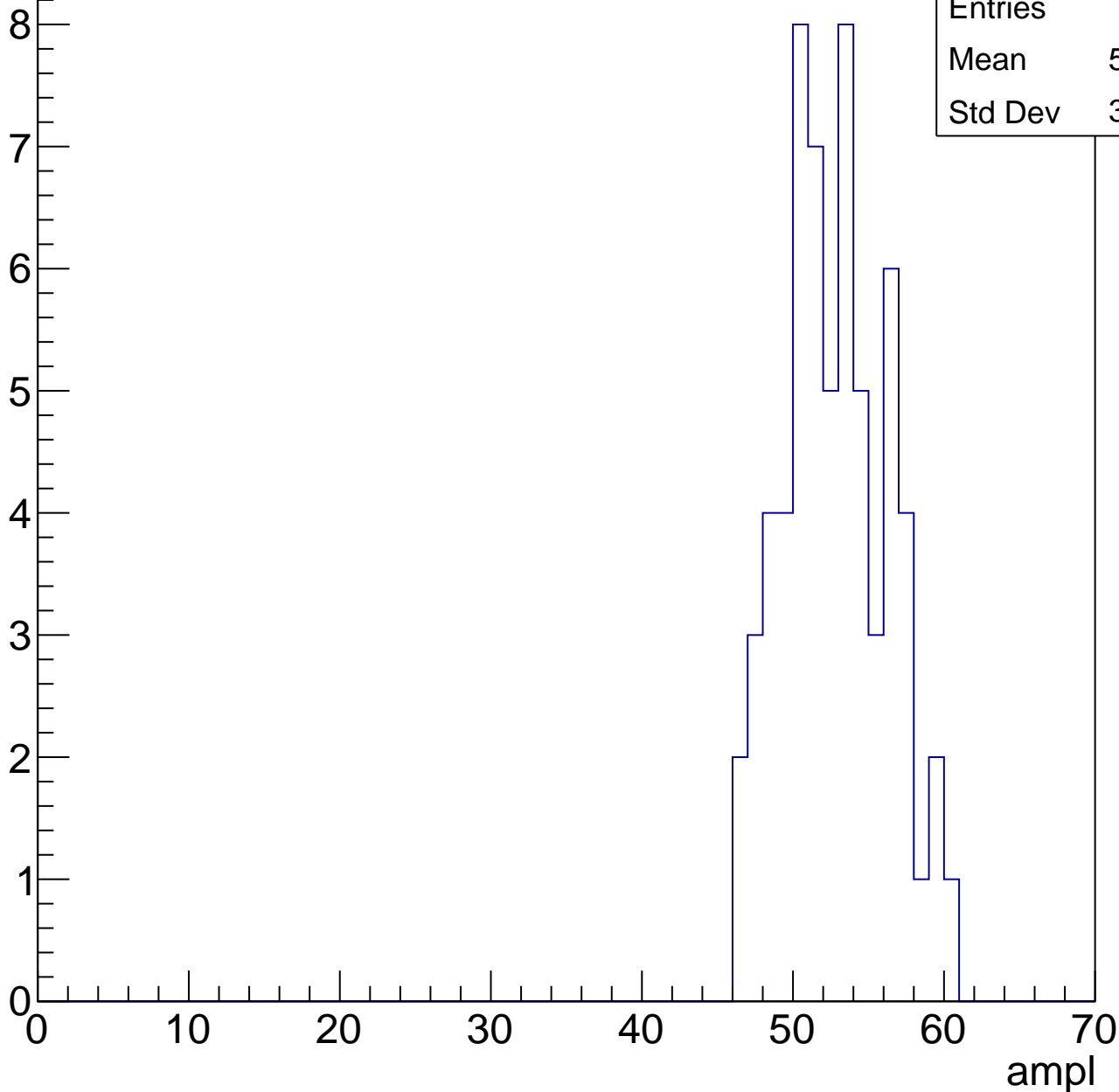


B1L102S, U17-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	52.33
Std Dev	3.399

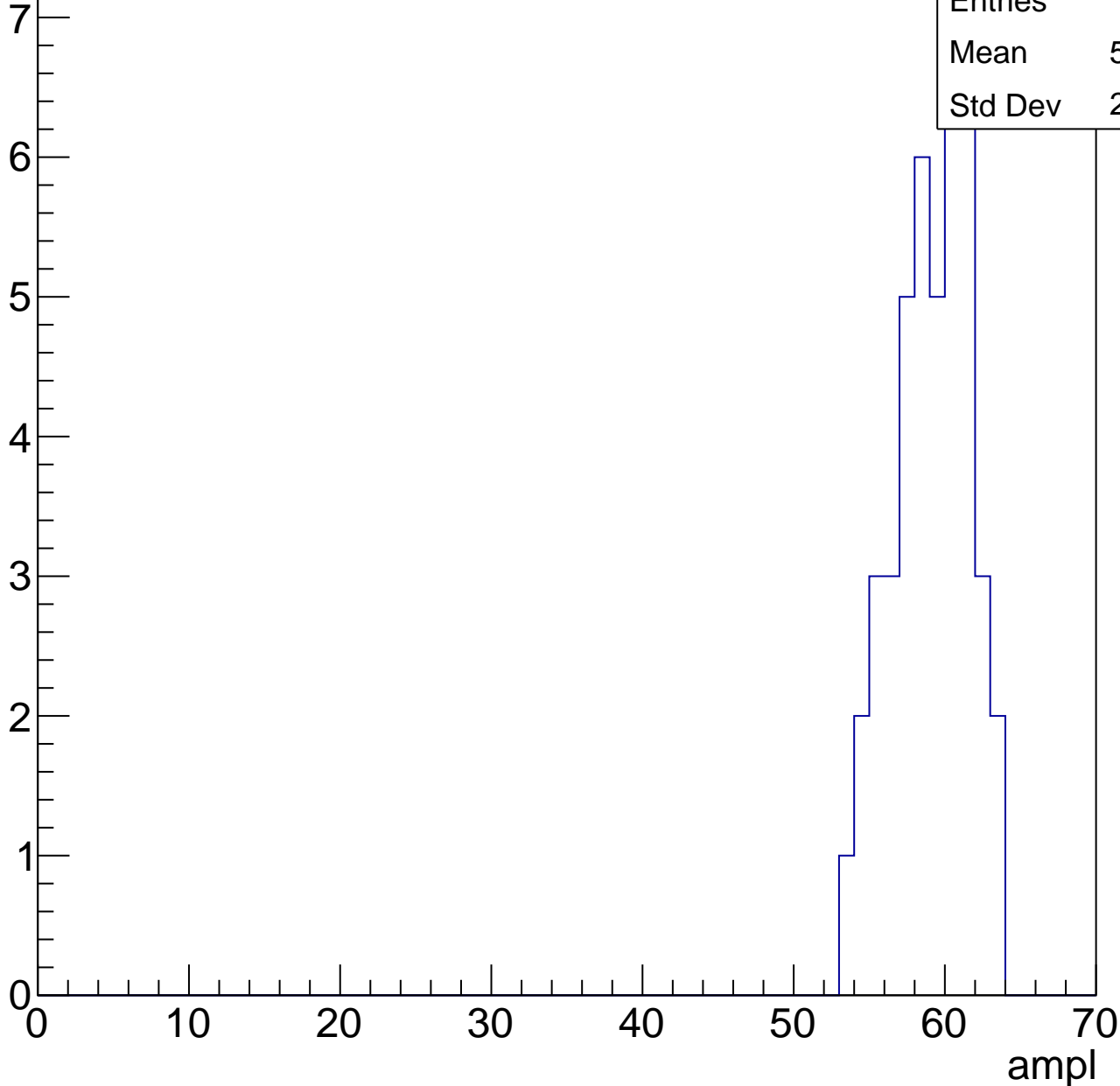


B1L102S, U17-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.66
Std Dev	2.504

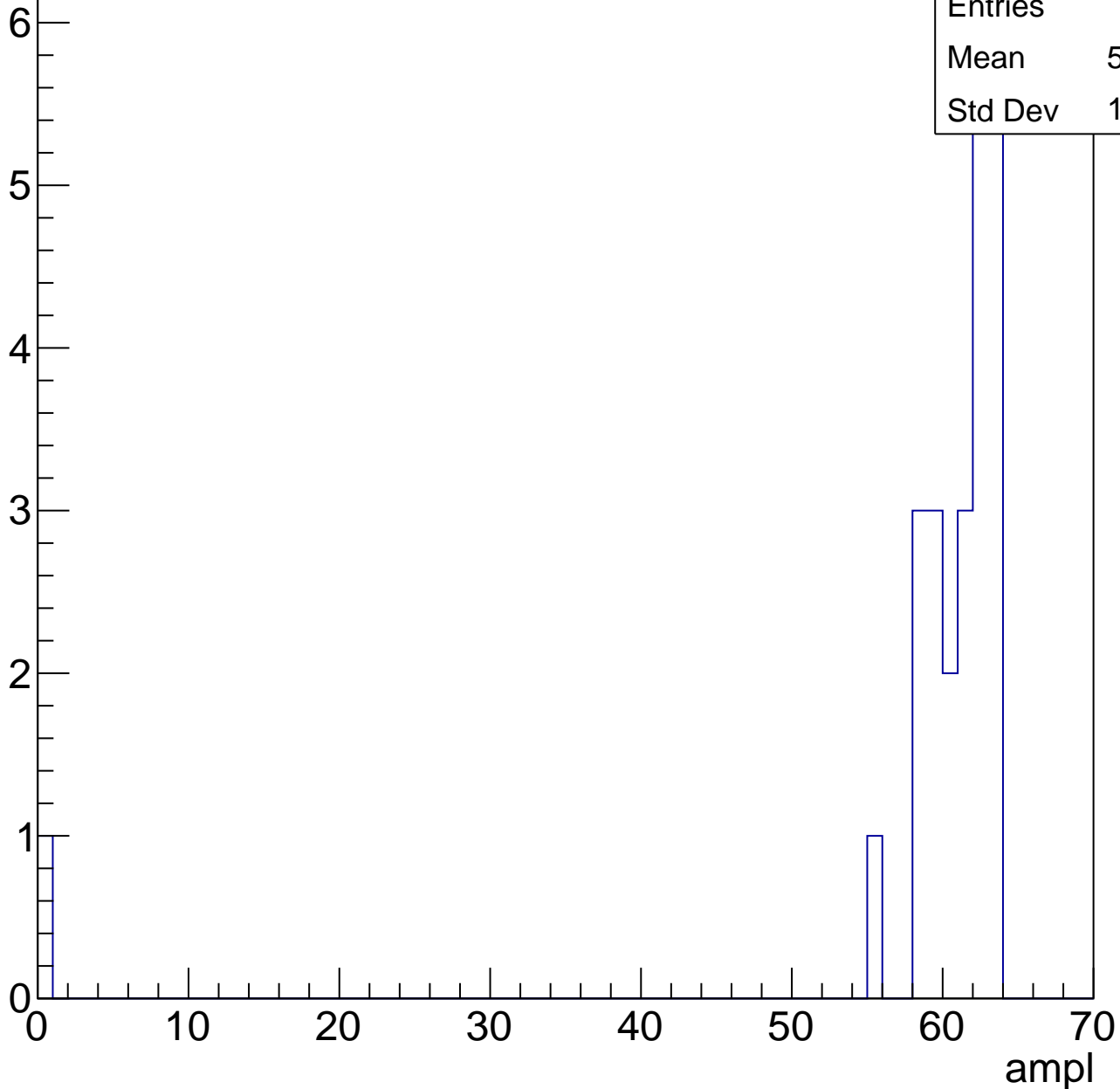


B1L102S, U17-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	58.36
Std Dev	12.09



B1L102S, U17-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	51.25
Std Dev	19.78

ampl

0 10 20 30 40 50 60 70

1

1

B1L102S, U17-ch122, adc0

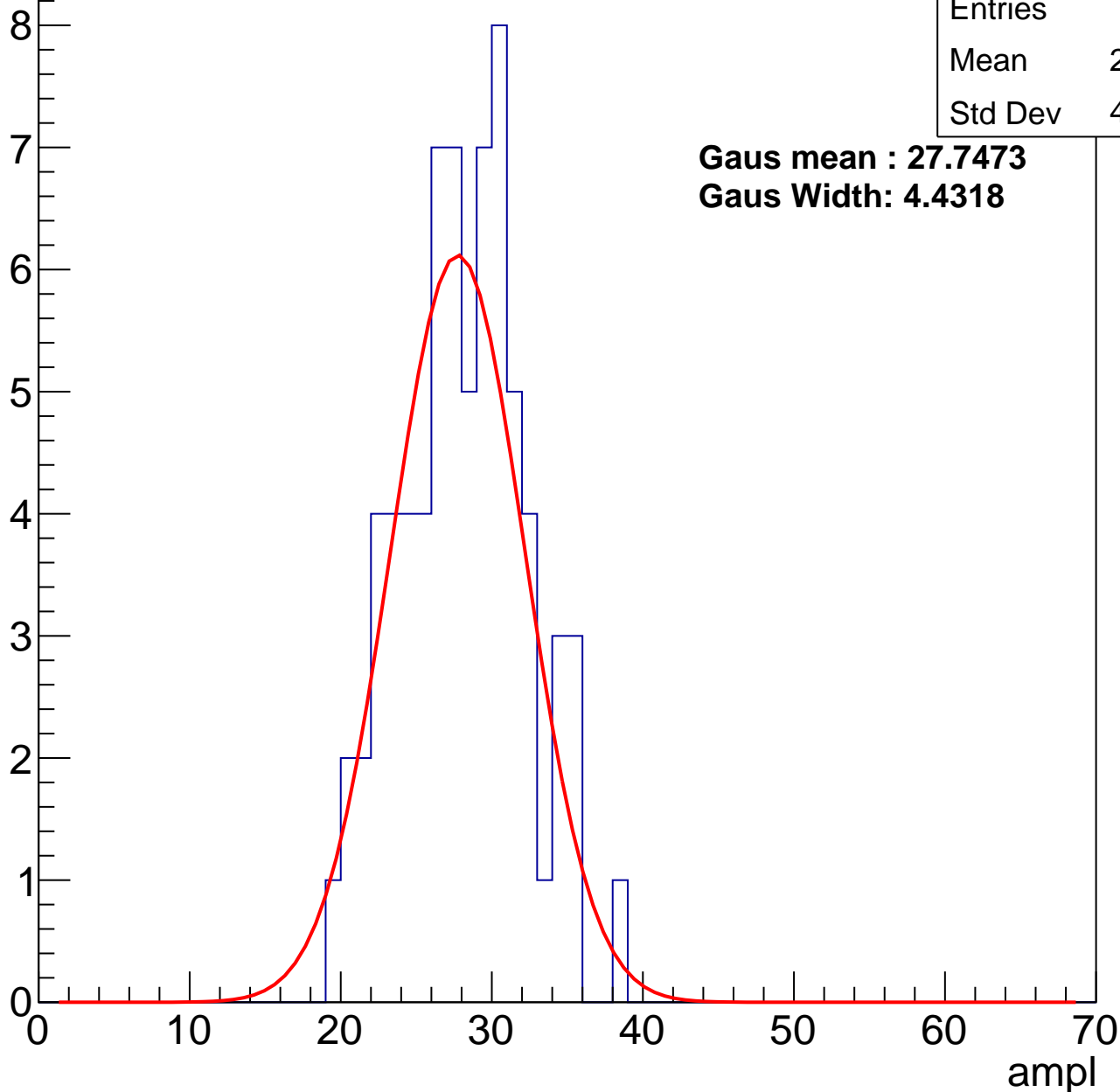
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.67
Std Dev	4.093

Gaus mean : 27.7473

Gaus Width: 4.4318



B1L102S, U17-ch122, adc1

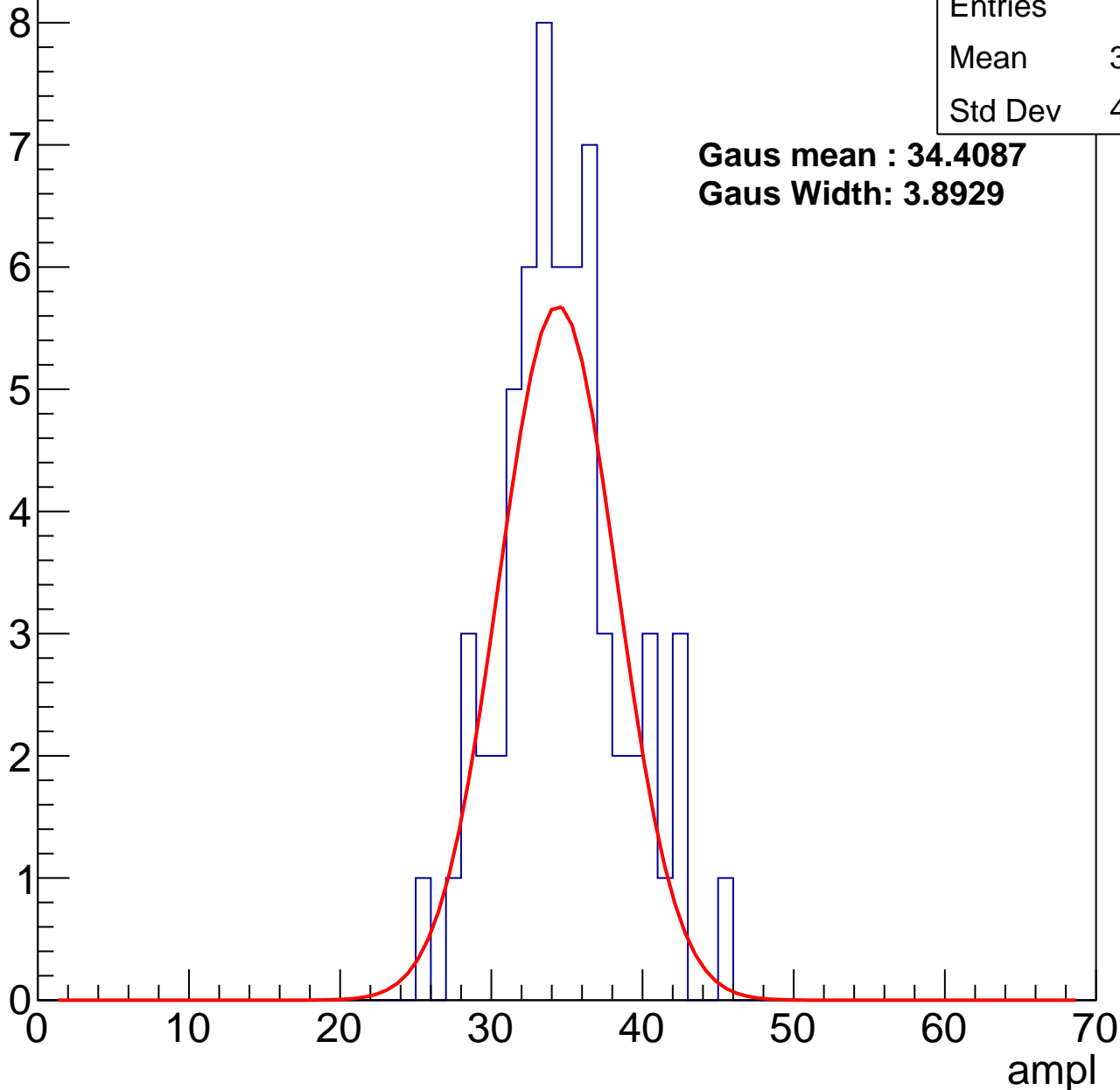
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.32
Std Dev	4.023

Gaus mean : 34.4087

Gaus Width: 3.8929



B1L102S, U17-ch122, adc2

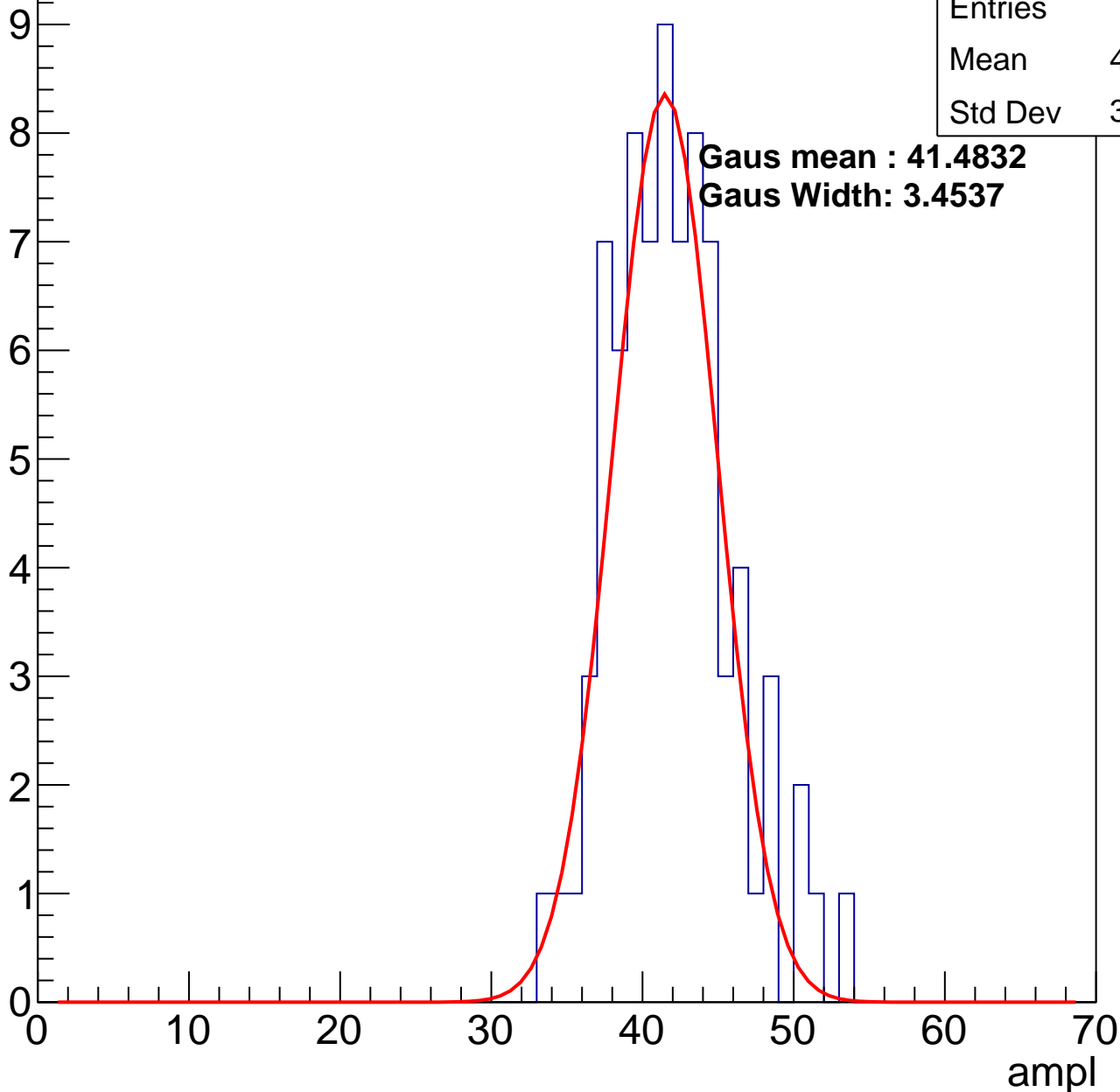
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.48
Std Dev	3.953

Gaus mean : 41.4832

Gaus Width: 3.4537

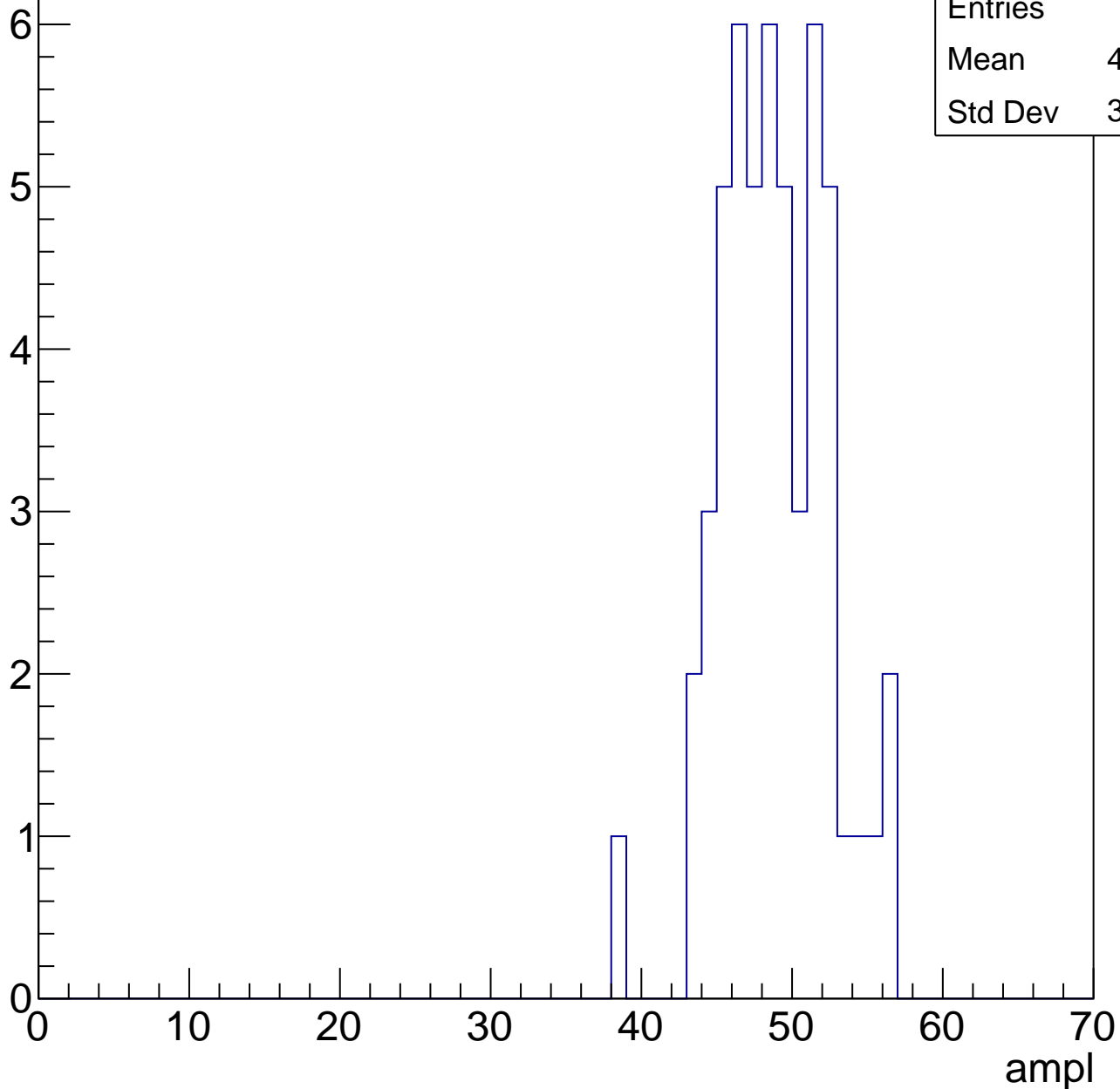


B1L102S, U17-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	48.37
Std Dev	3.547

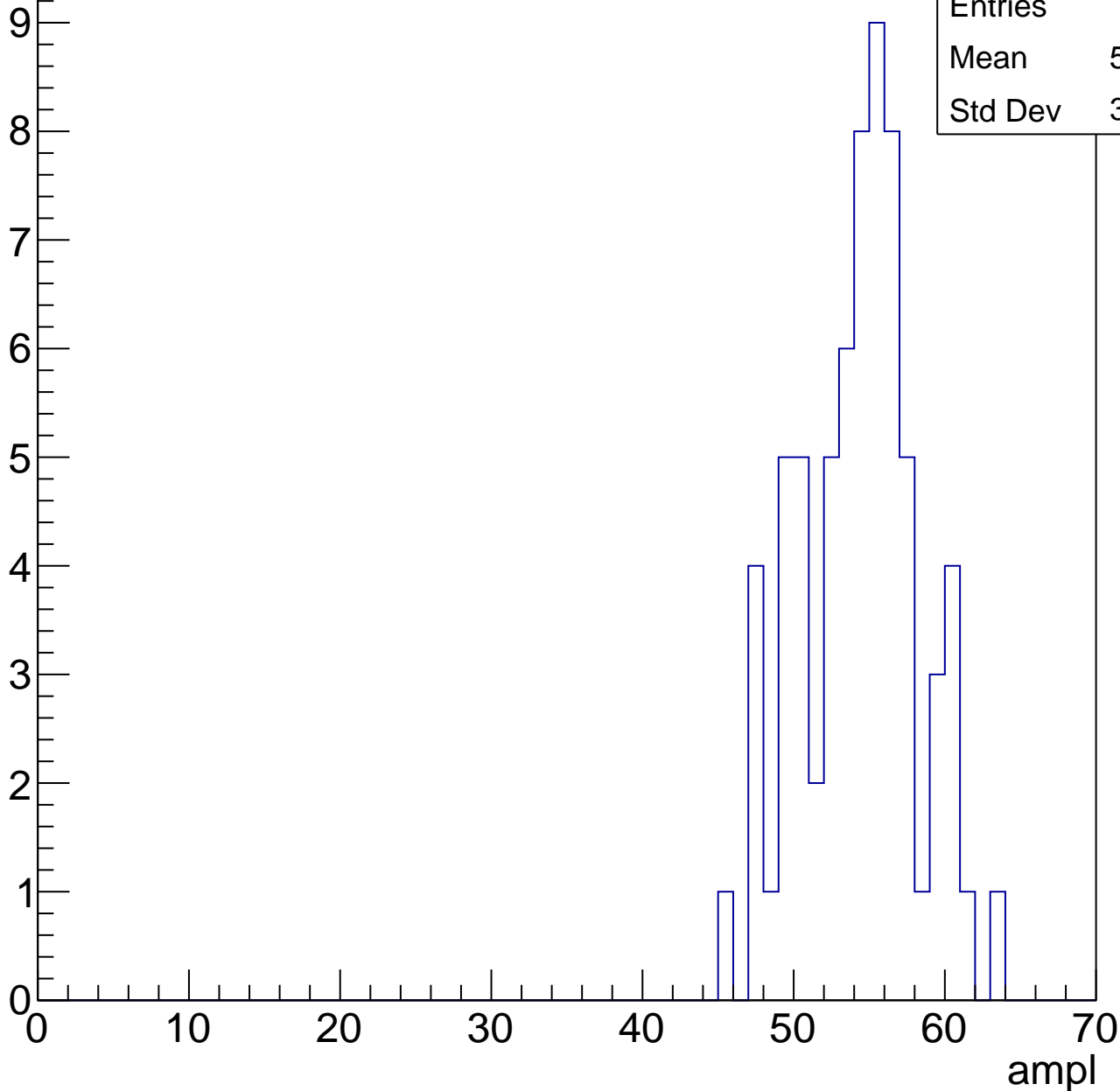


B1L102S, U17-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	53.84
Std Dev	3.828

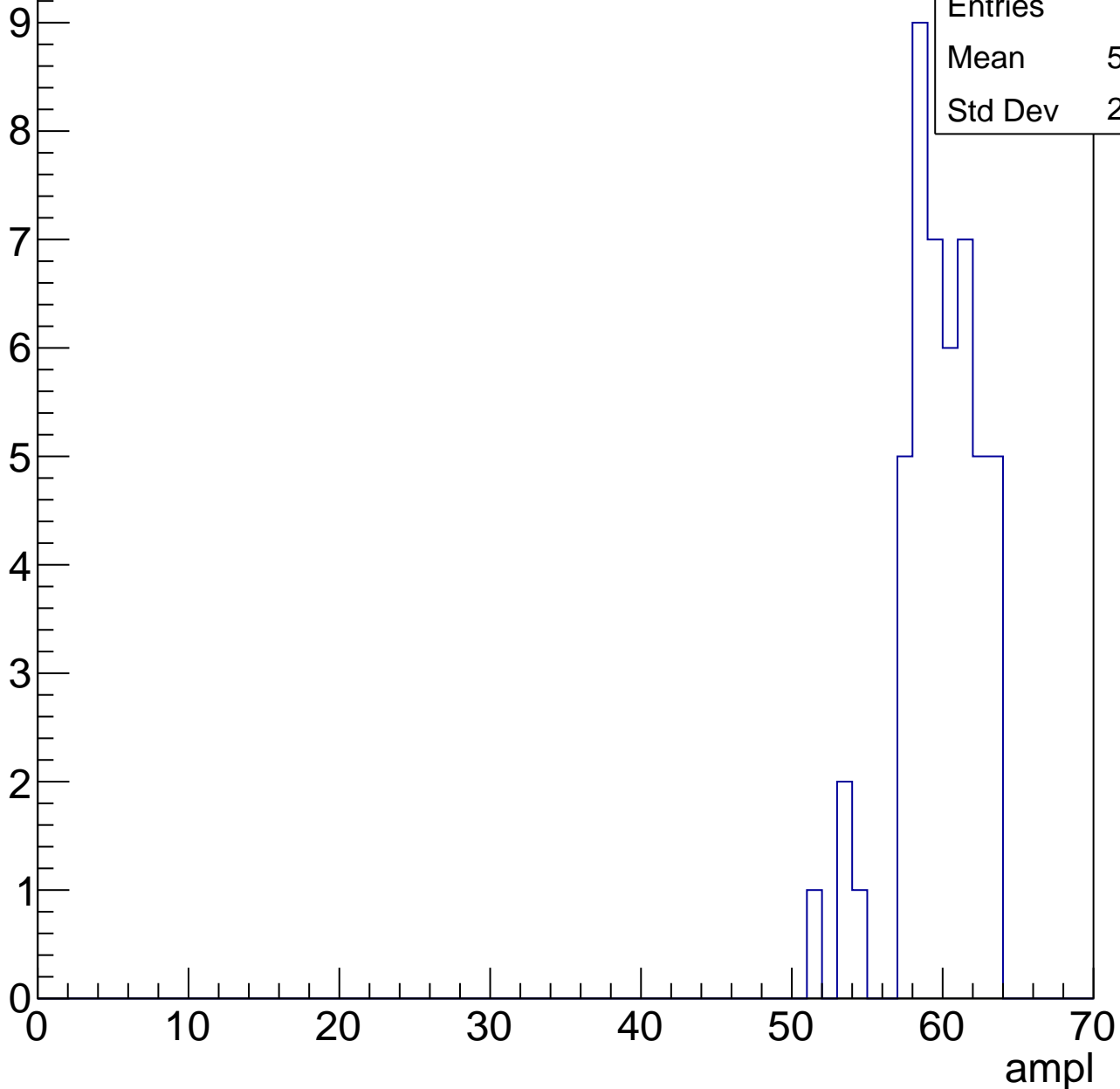


B1L102S, U17-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

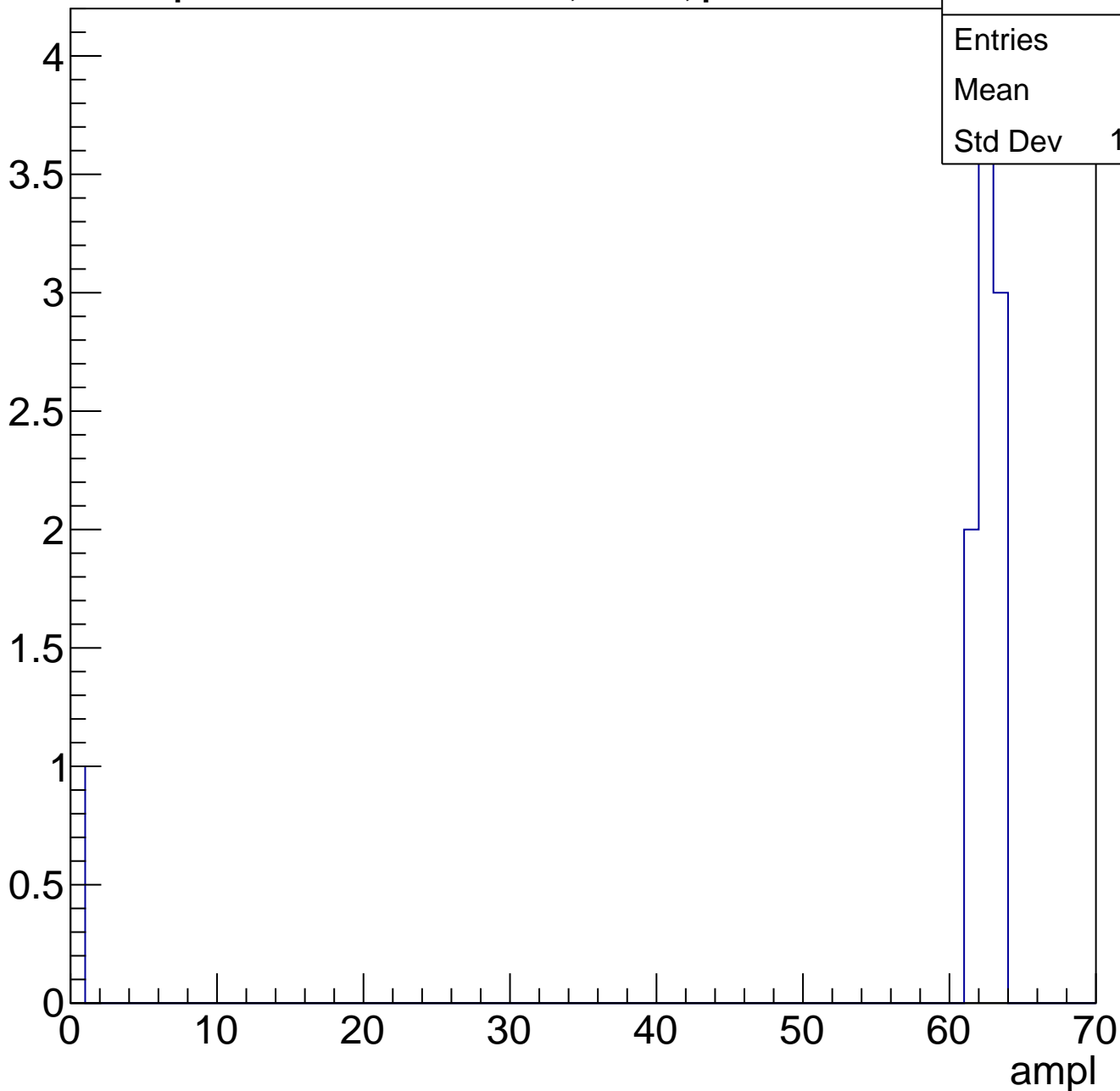
Entries	48
Mean	59.23
Std Dev	2.687



B1L102S, U17-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

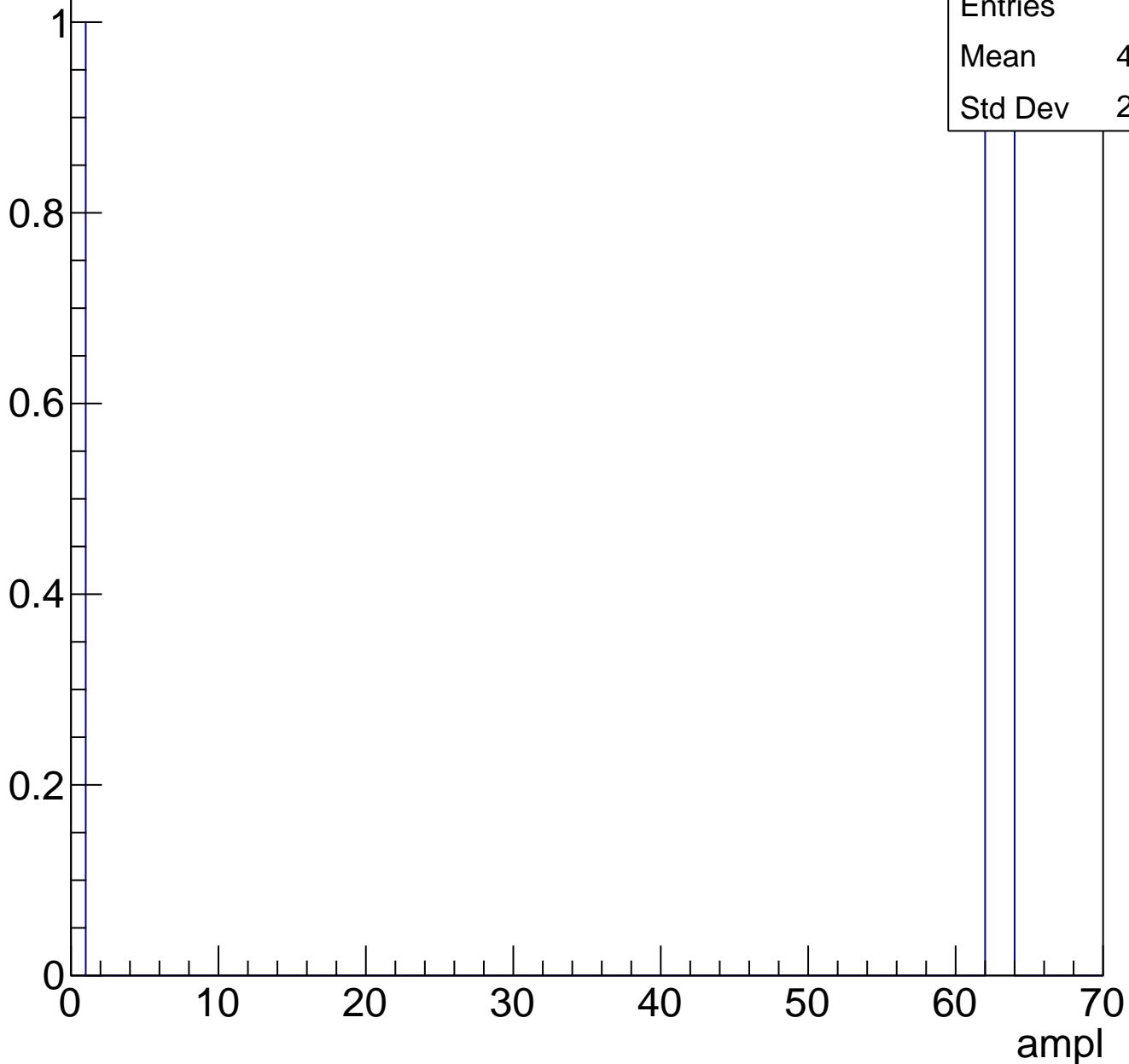
Entry



B1L102S, U17-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch123, adc0

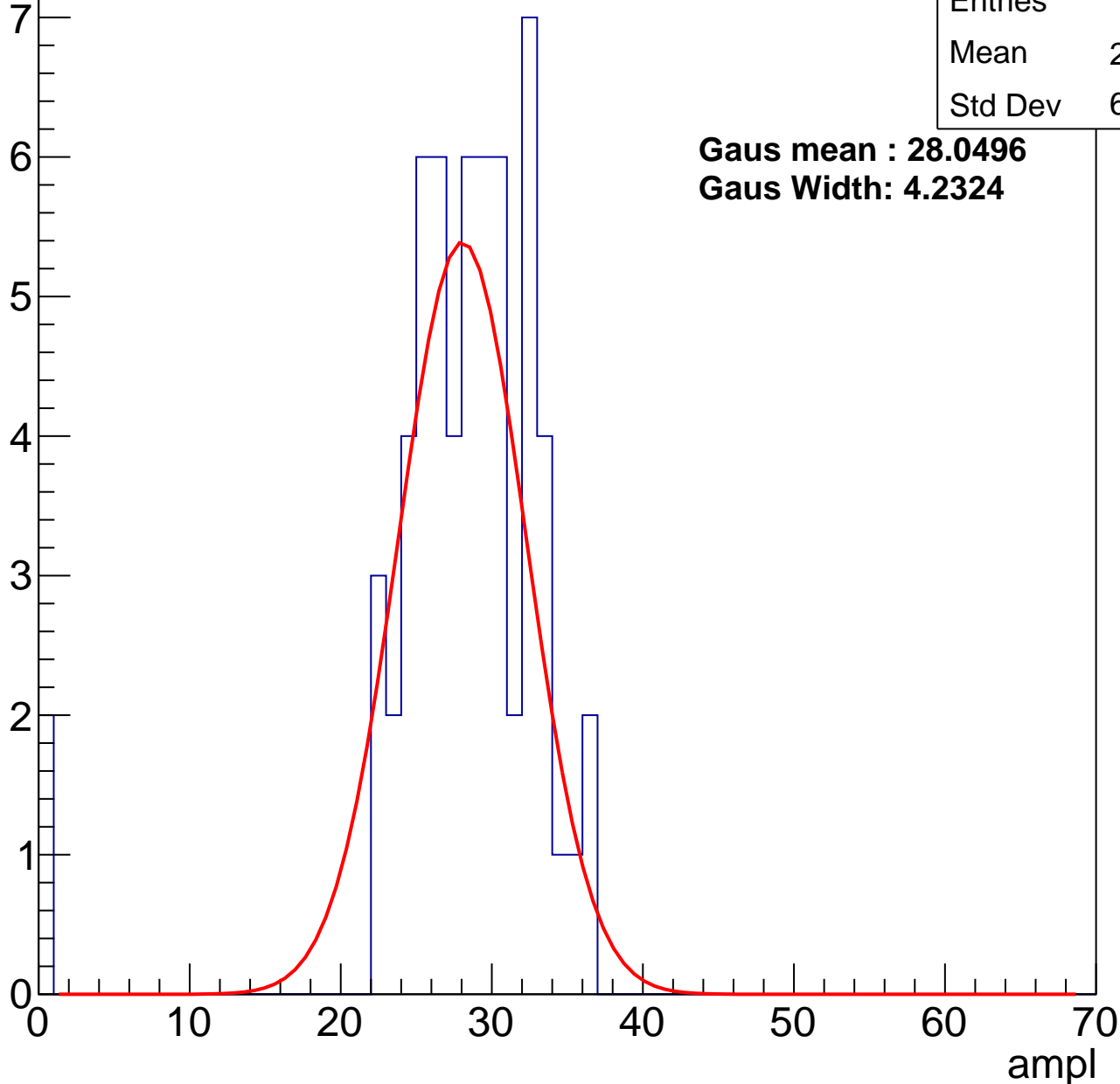
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.47
Std Dev	6.127

Gaus mean : 28.0496

Gaus Width: 4.2324



B1L102S, U17-ch123, adc1

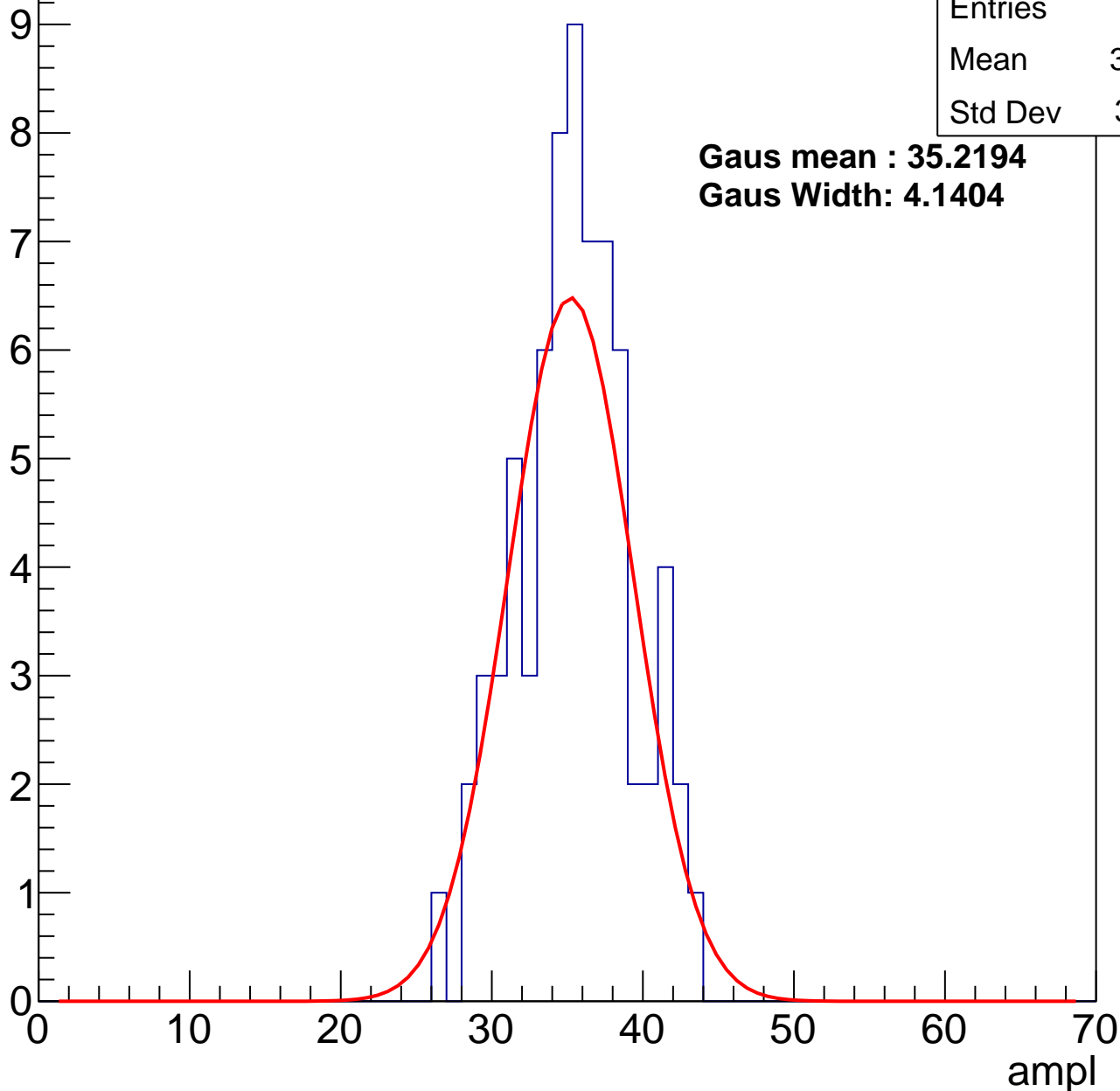
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.97
Std Dev	3.711

Gaus mean : 35.2194

Gaus Width: 4.1404



B1L102S, U17-ch123, adc2

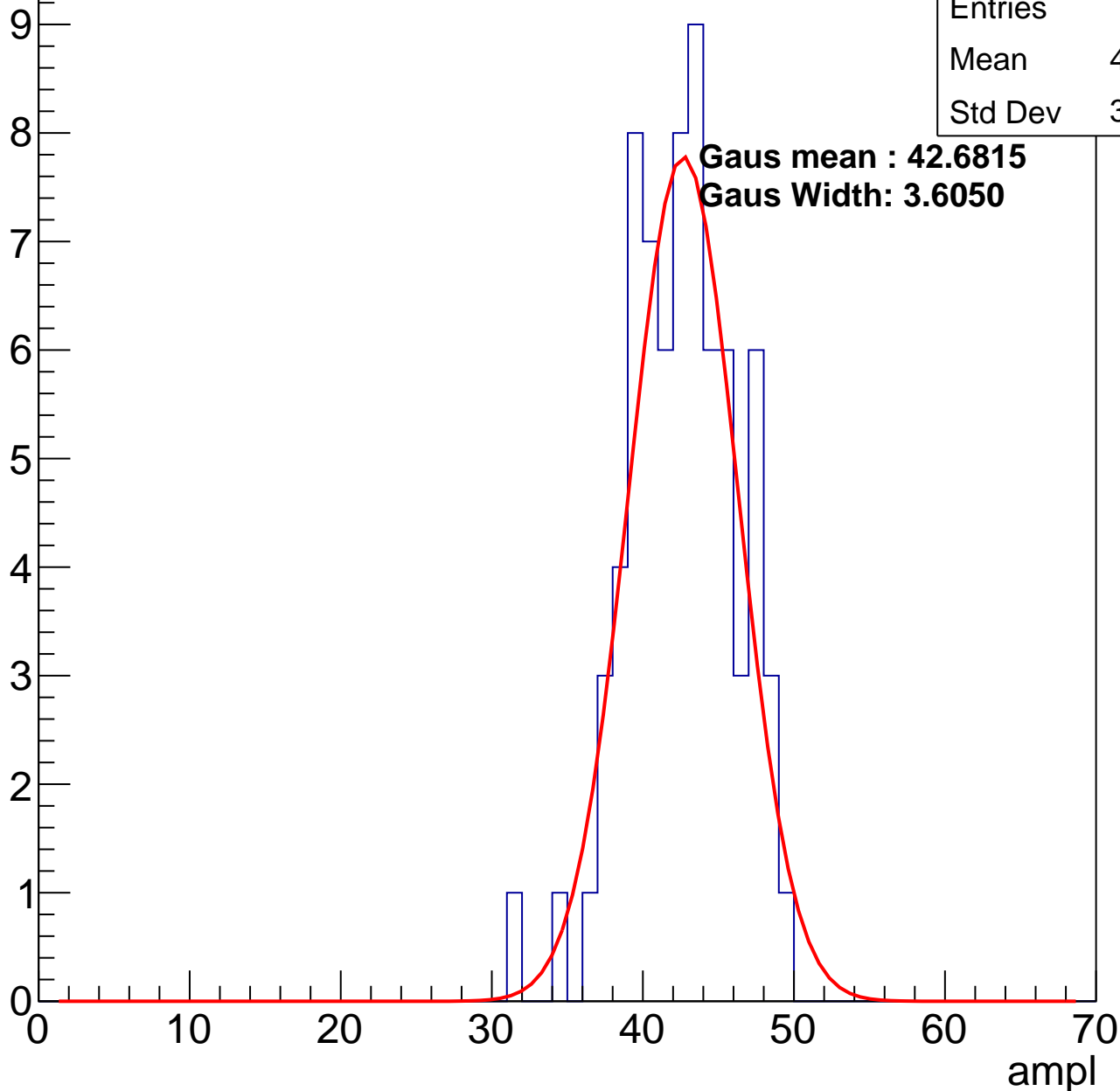
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.08
Std Dev	3.526

Gaus mean : 42.6815

Gaus Width: 3.6050

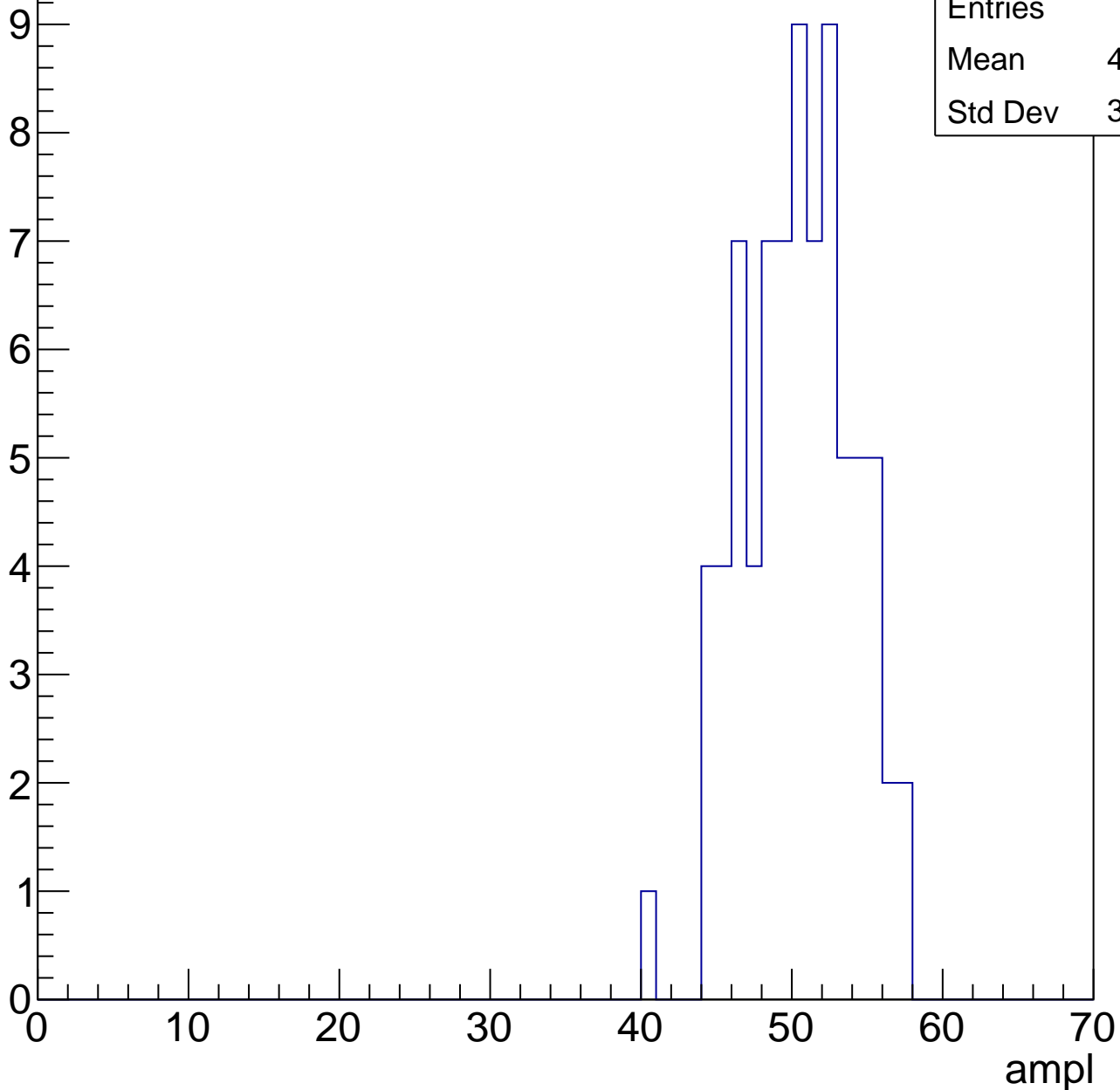


B1L102S, U17-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	49.95
Std Dev	3.562

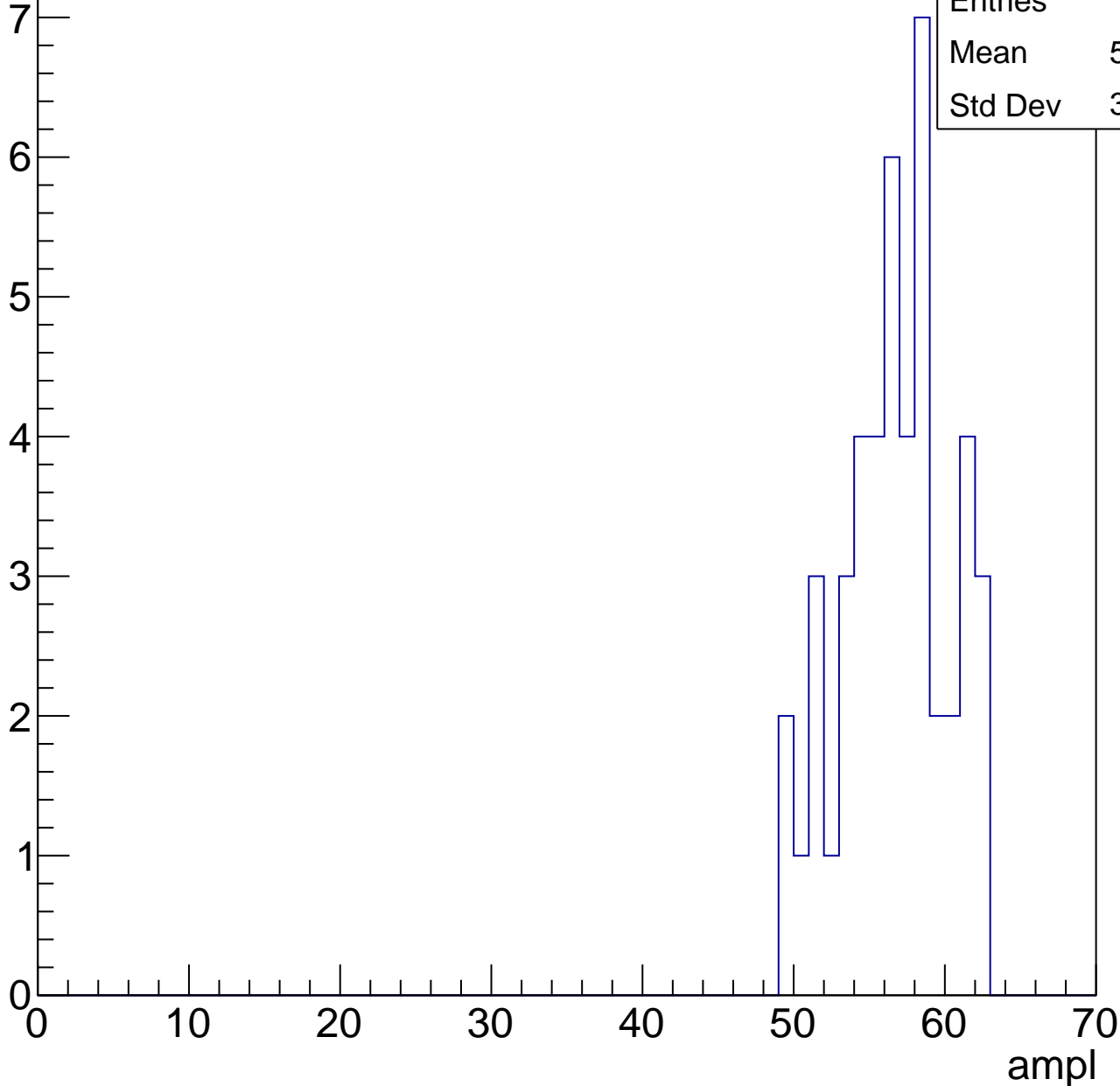


B1L102S, U17-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	56.22
Std Dev	3.489

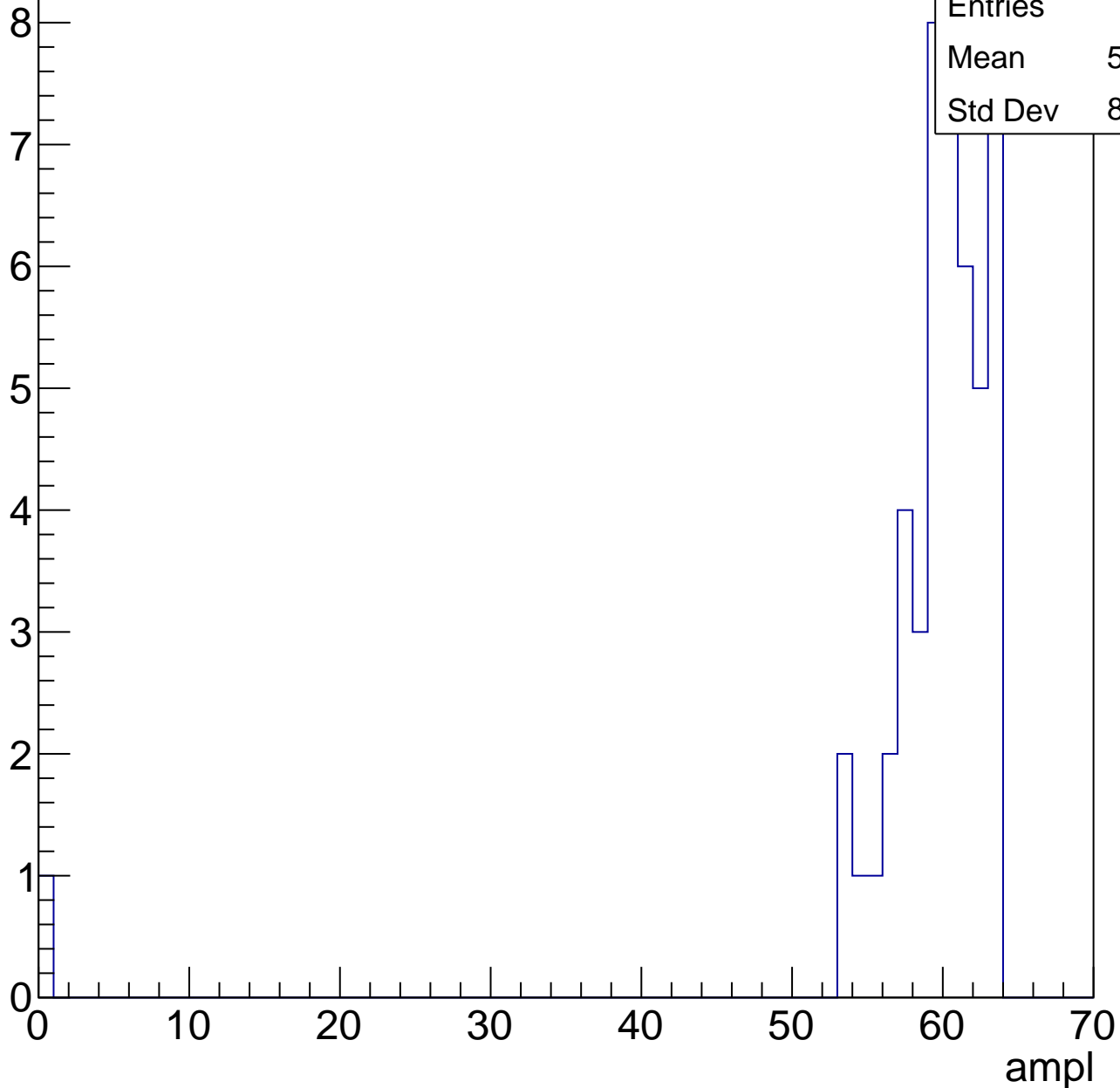


B1L102S, U17-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.39
Std Dev	8.827



B1L102S, U17-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U17-ch124, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.05
Std Dev	3.922

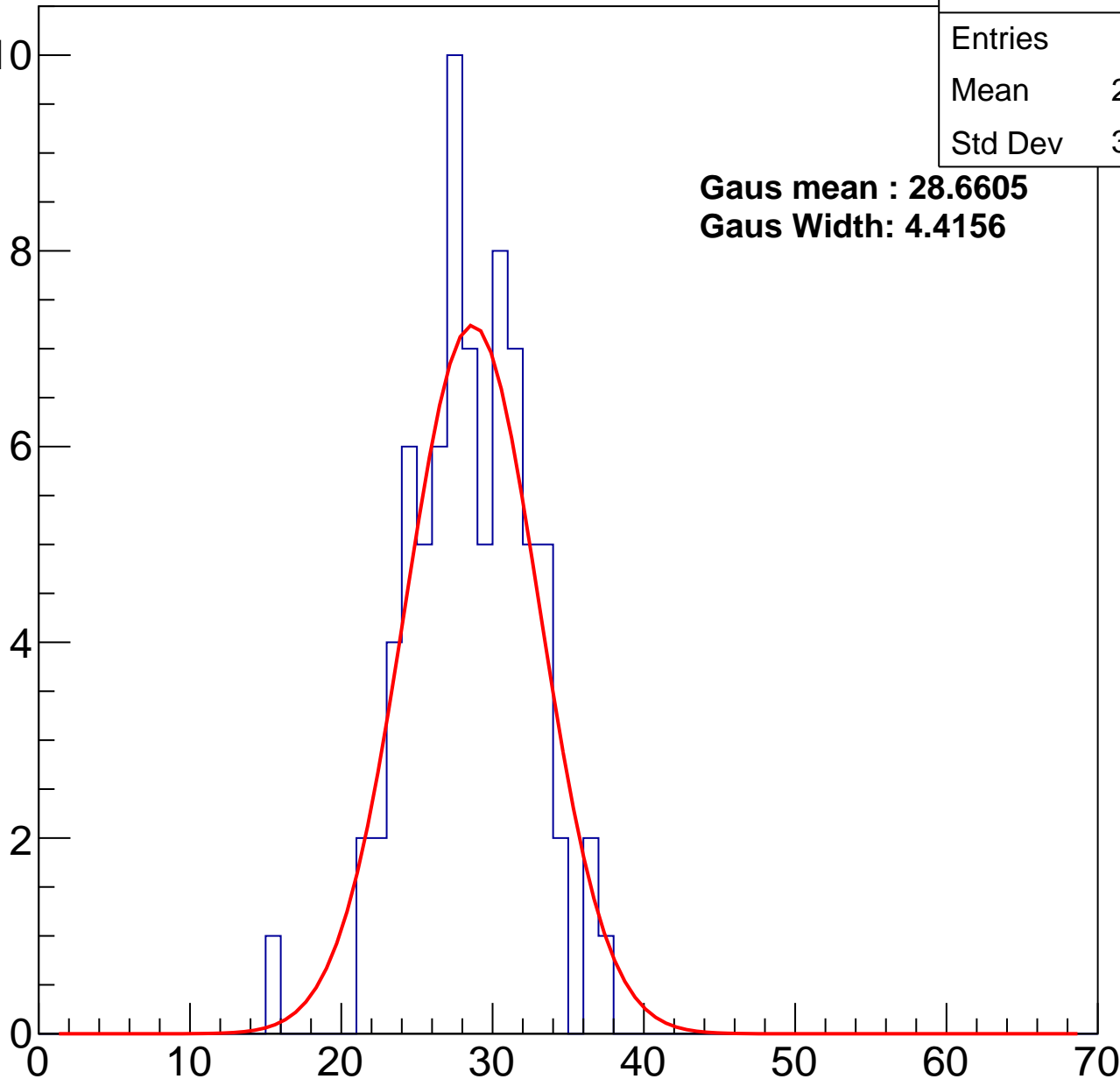
Gaus mean : 28.6605

Gaus Width: 4.4156

Entry

10
8
6
4
2
0

ampl



B1L102S, U17-ch124, adc1

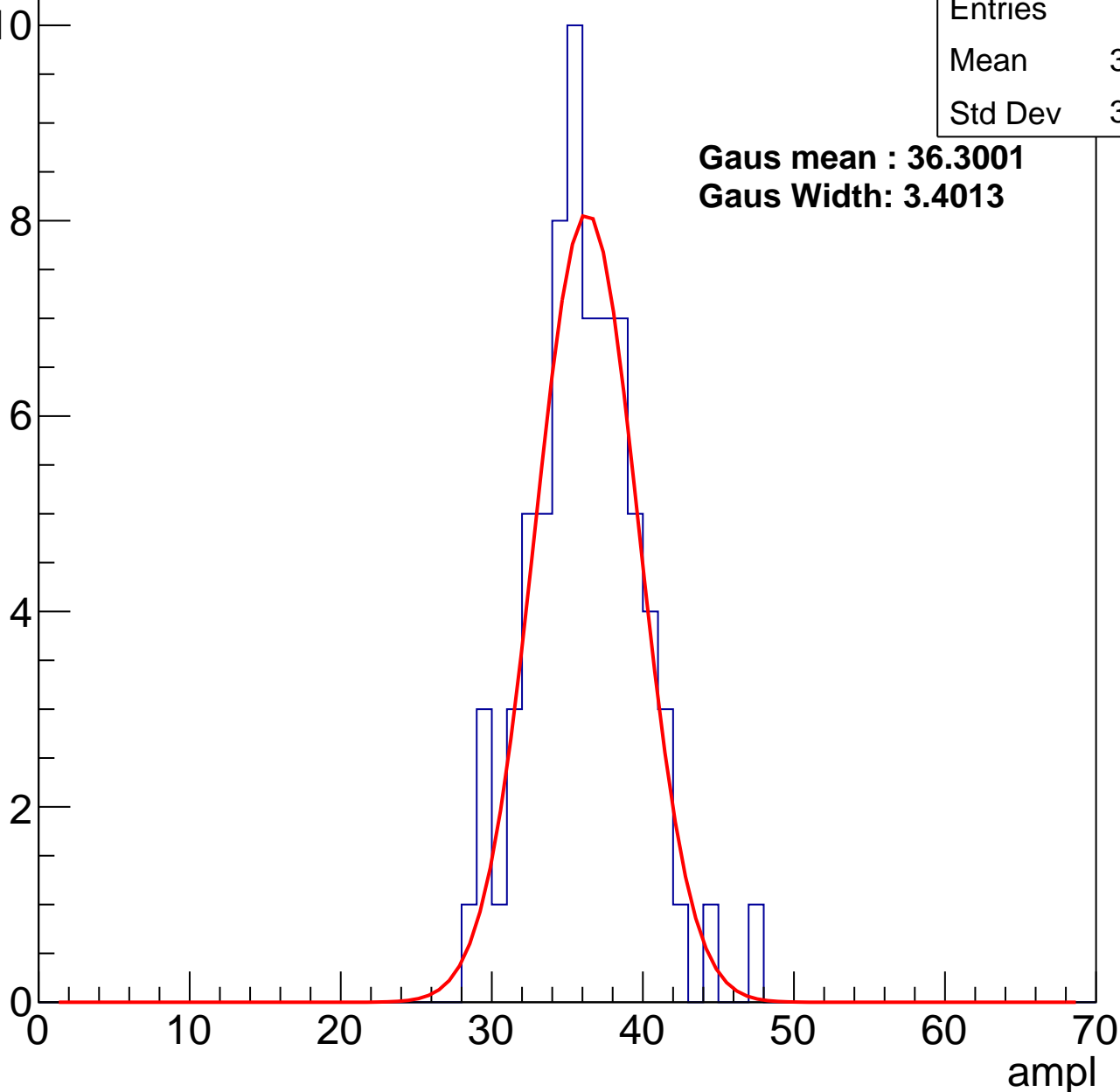
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.74
Std Dev	3.582

Gaus mean : 36.3001

Gaus Width: 3.4013



B1L102S, U17-ch124, adc2

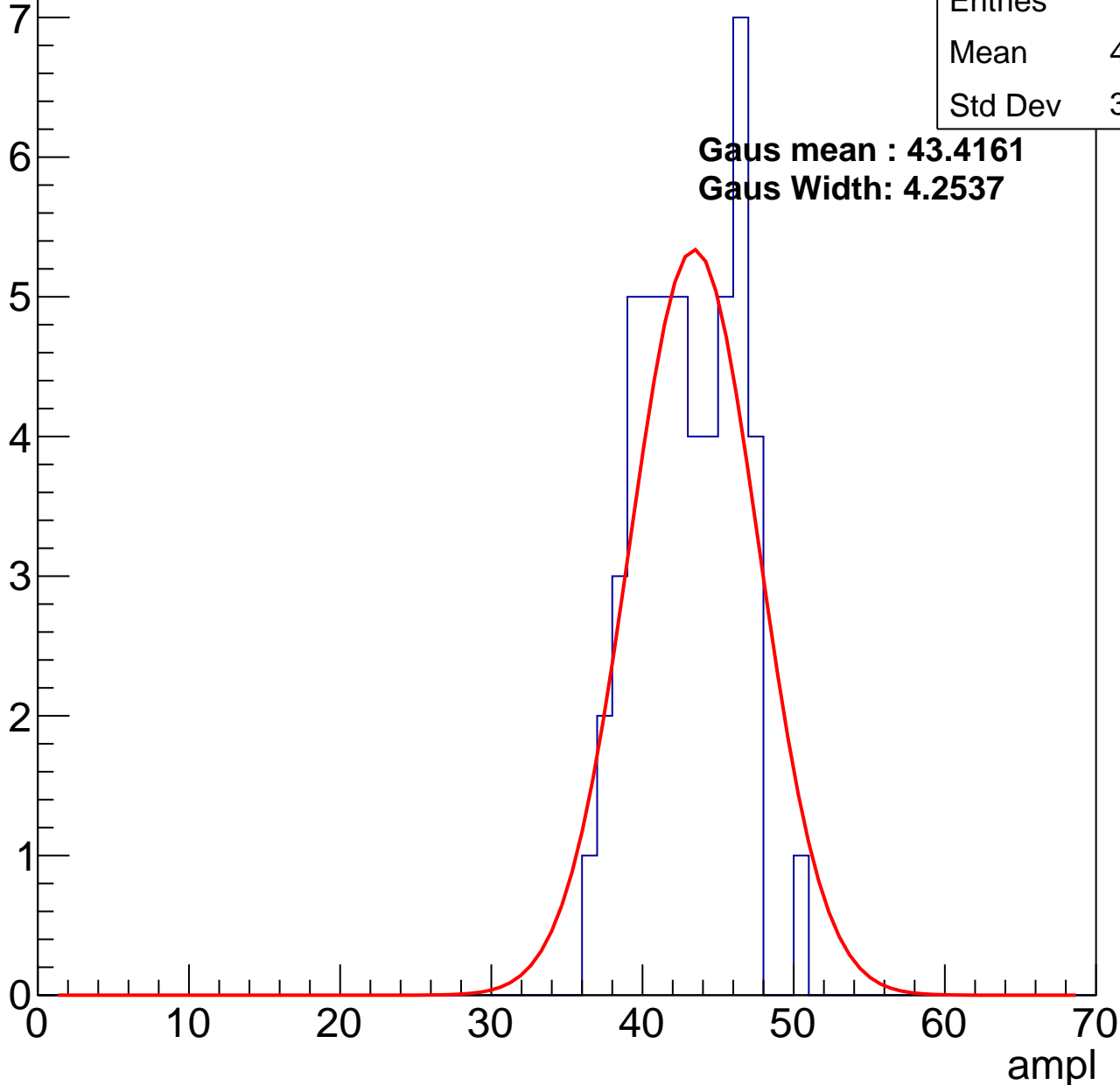
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.49
Std Dev	3.232

Gaus mean : 43.4161

Gaus Width: 4.2537

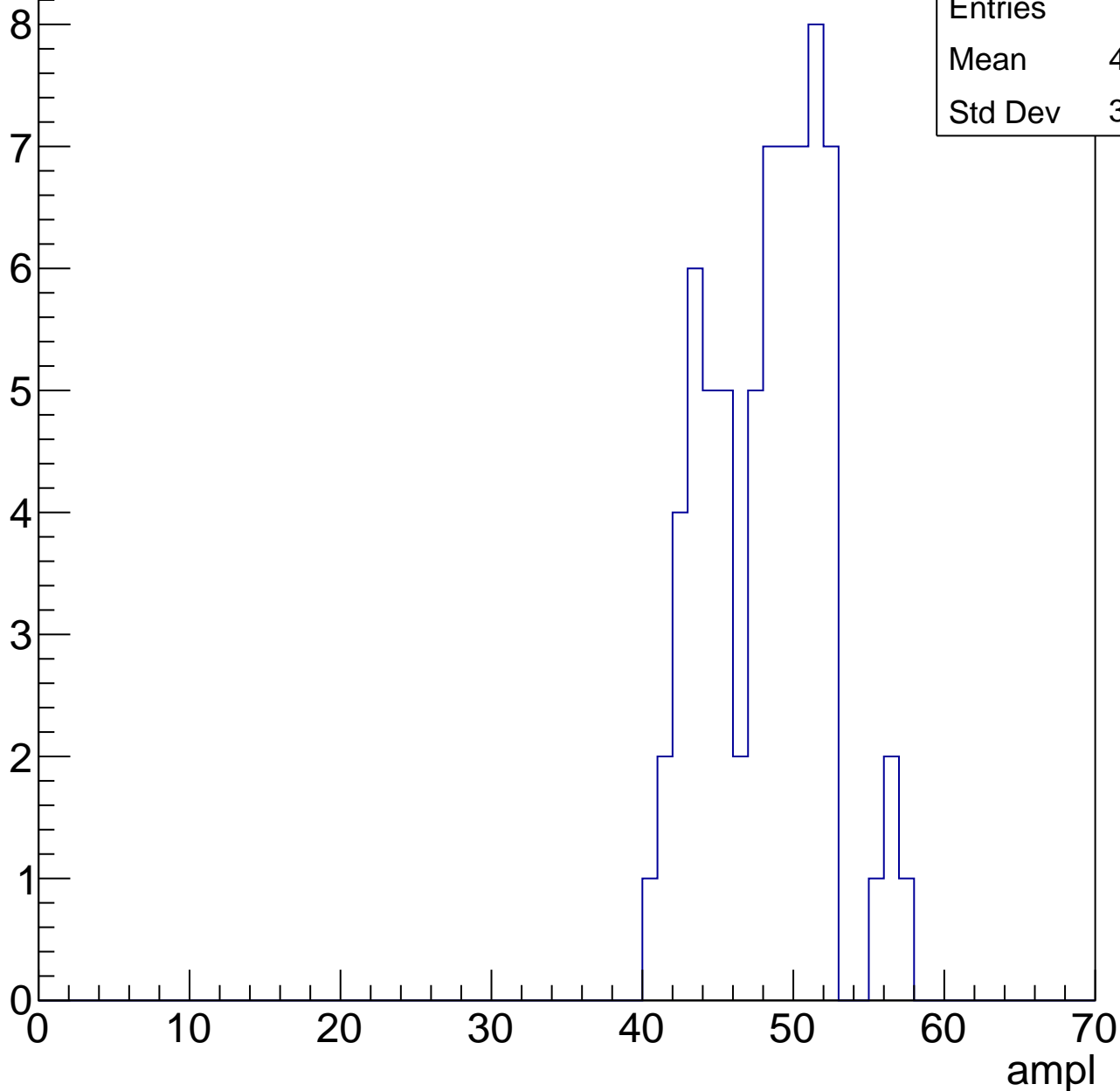


B1L102S, U17-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.79
Std Dev	3.906

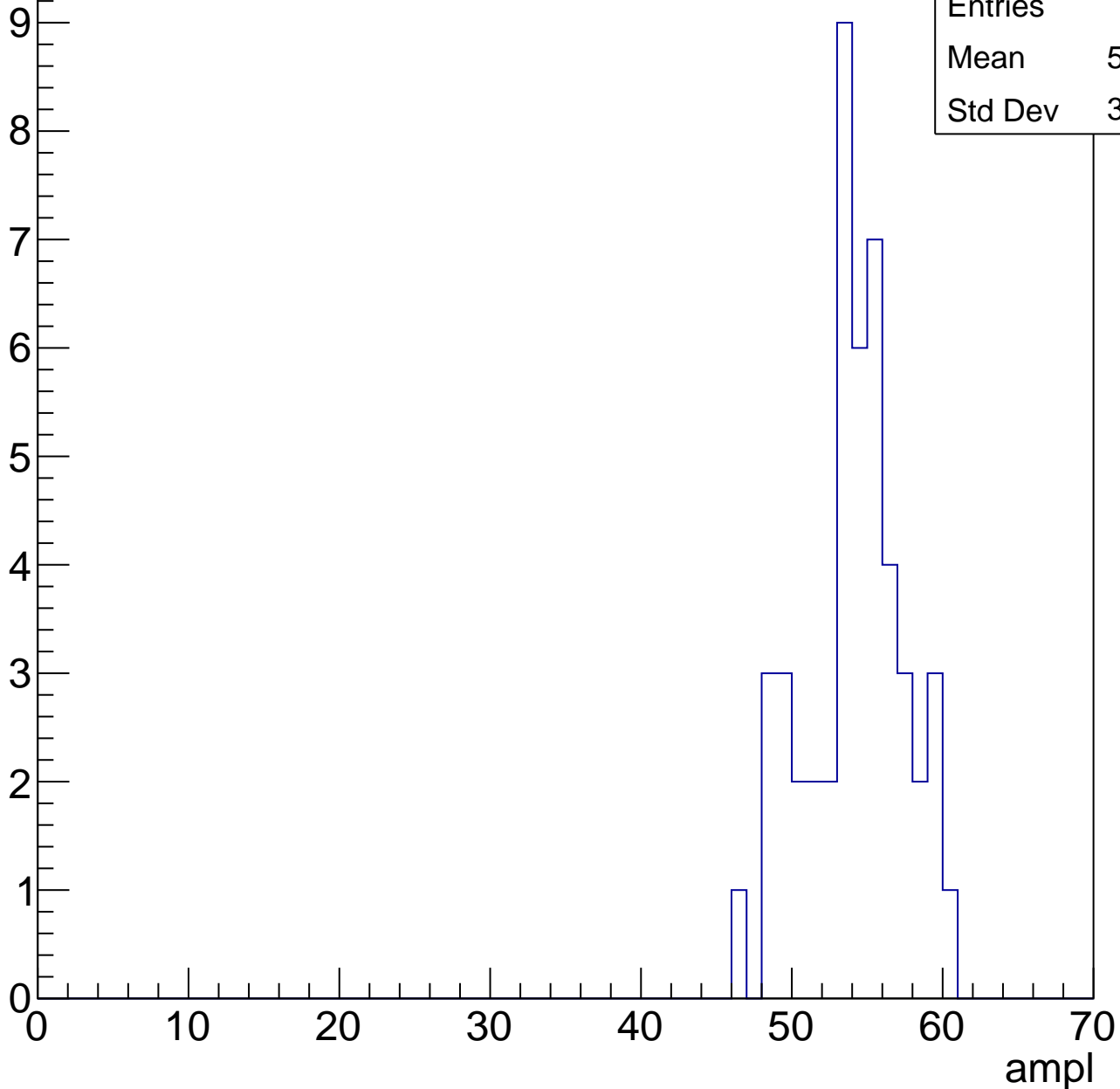


B1L102S, U17-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	53.69
Std Dev	3.235



B1L102S, U17-ch124, adc5

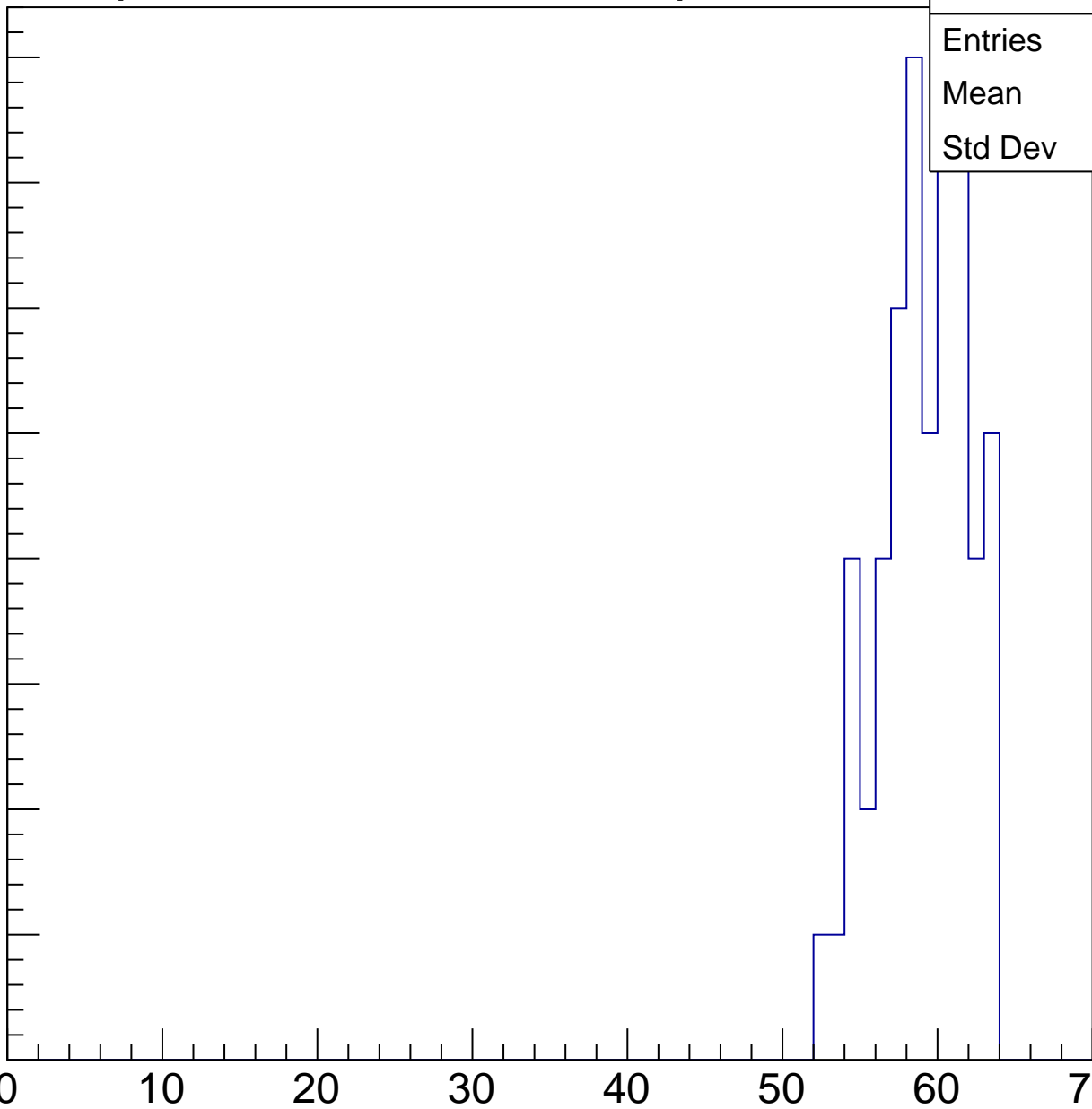
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.7
Std Dev	2.79

ampl

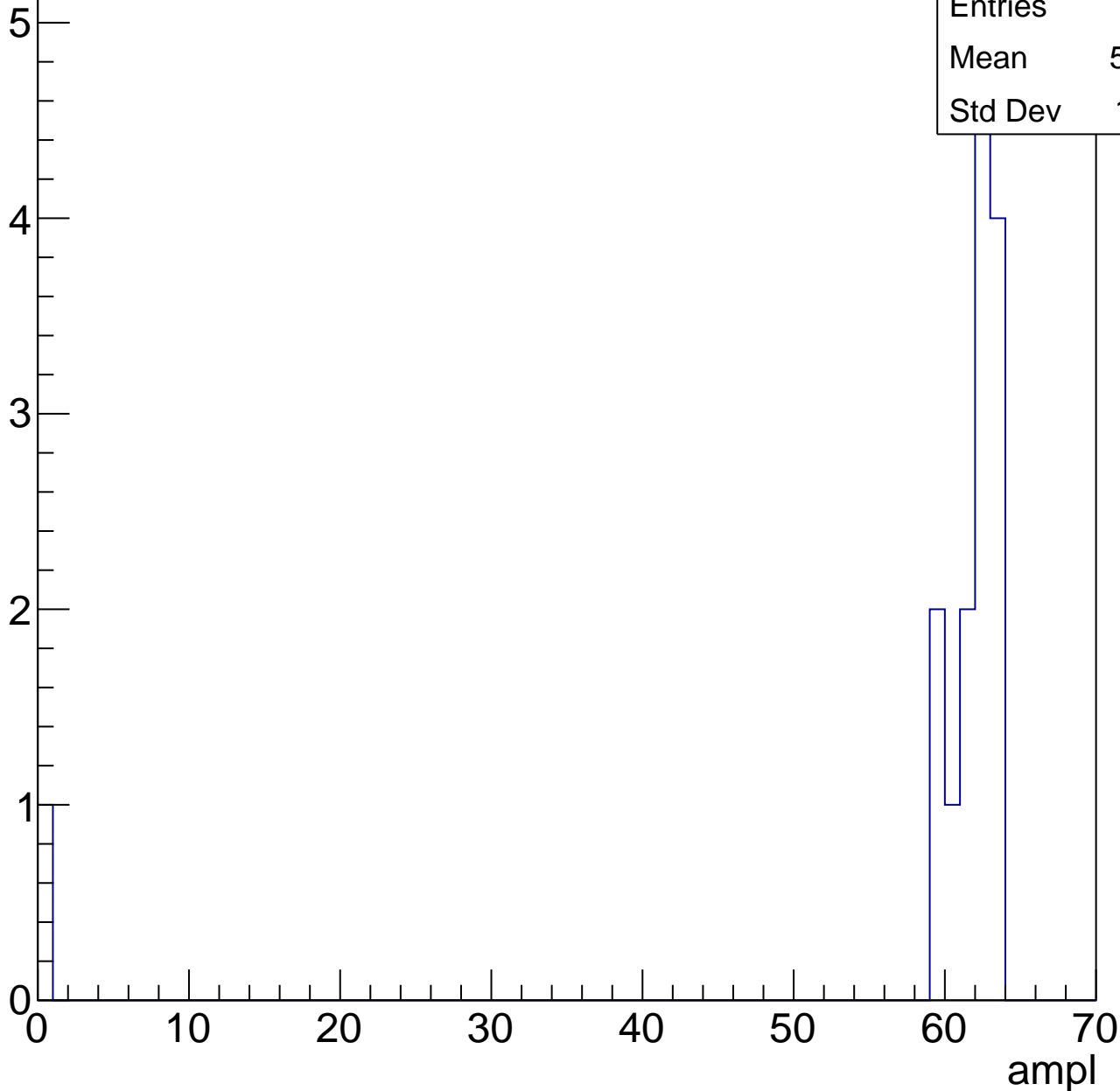


B1L102S, U17-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.47
Std Dev	15.41



B1L102S, U17-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch125, adc0

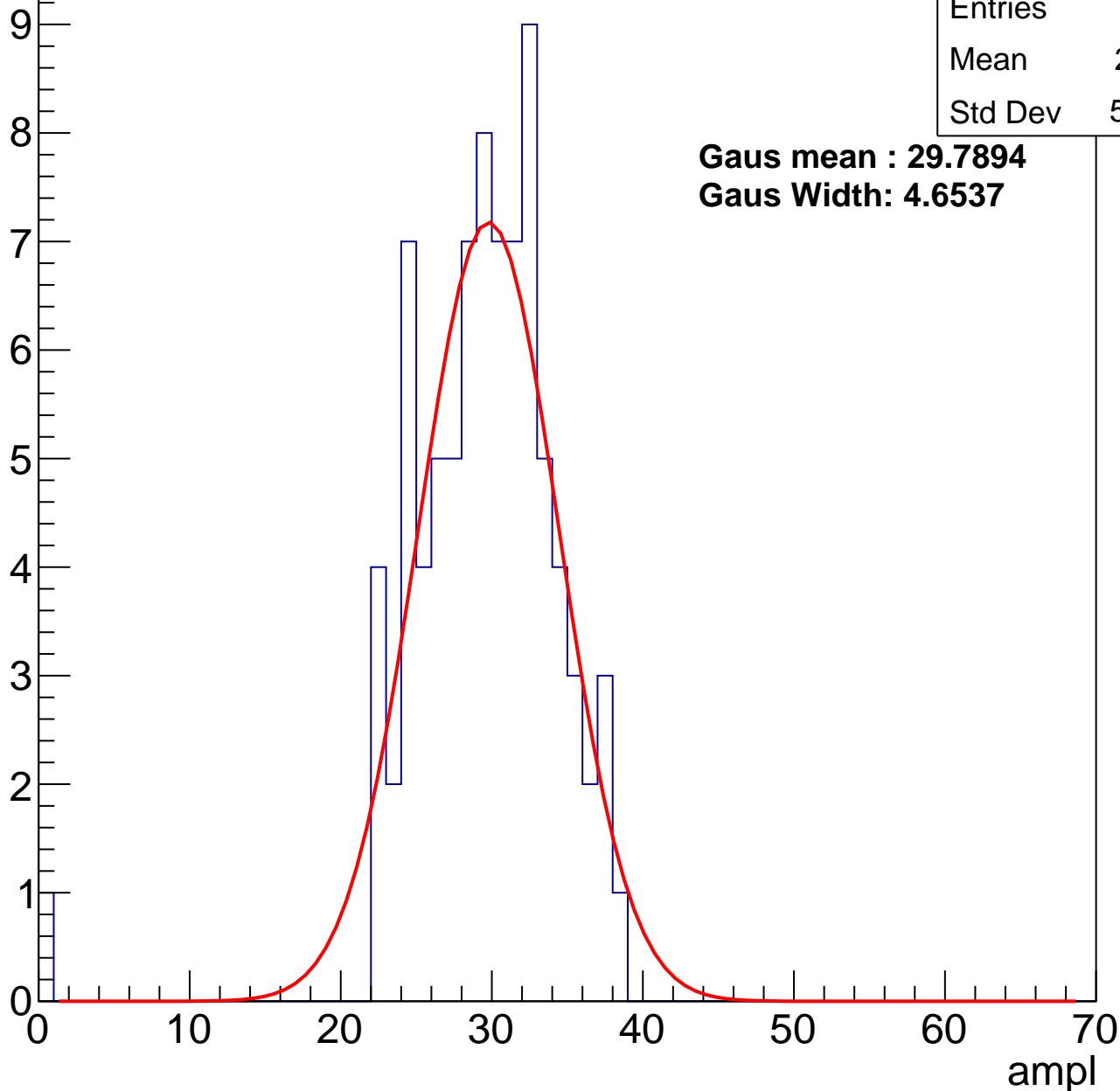
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	29.01
Std Dev	5.112

Gaus mean : 29.7894

Gaus Width: 4.6537



B1L102S, U17-ch125, adc1

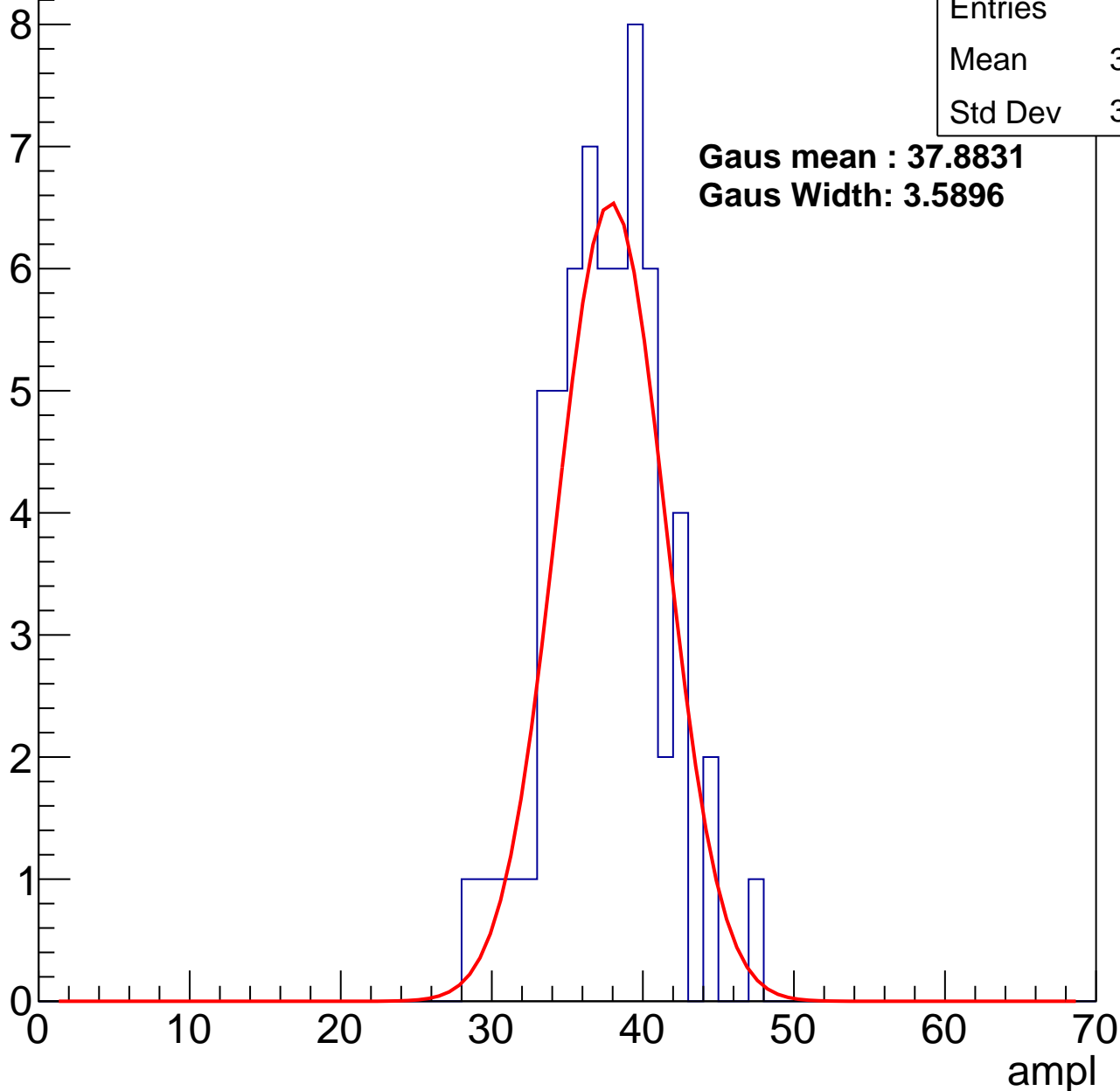
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.05
Std Dev	3.636

Gaus mean : 37.8831

Gaus Width: 3.5896



B1L102S, U17-ch125, adc2

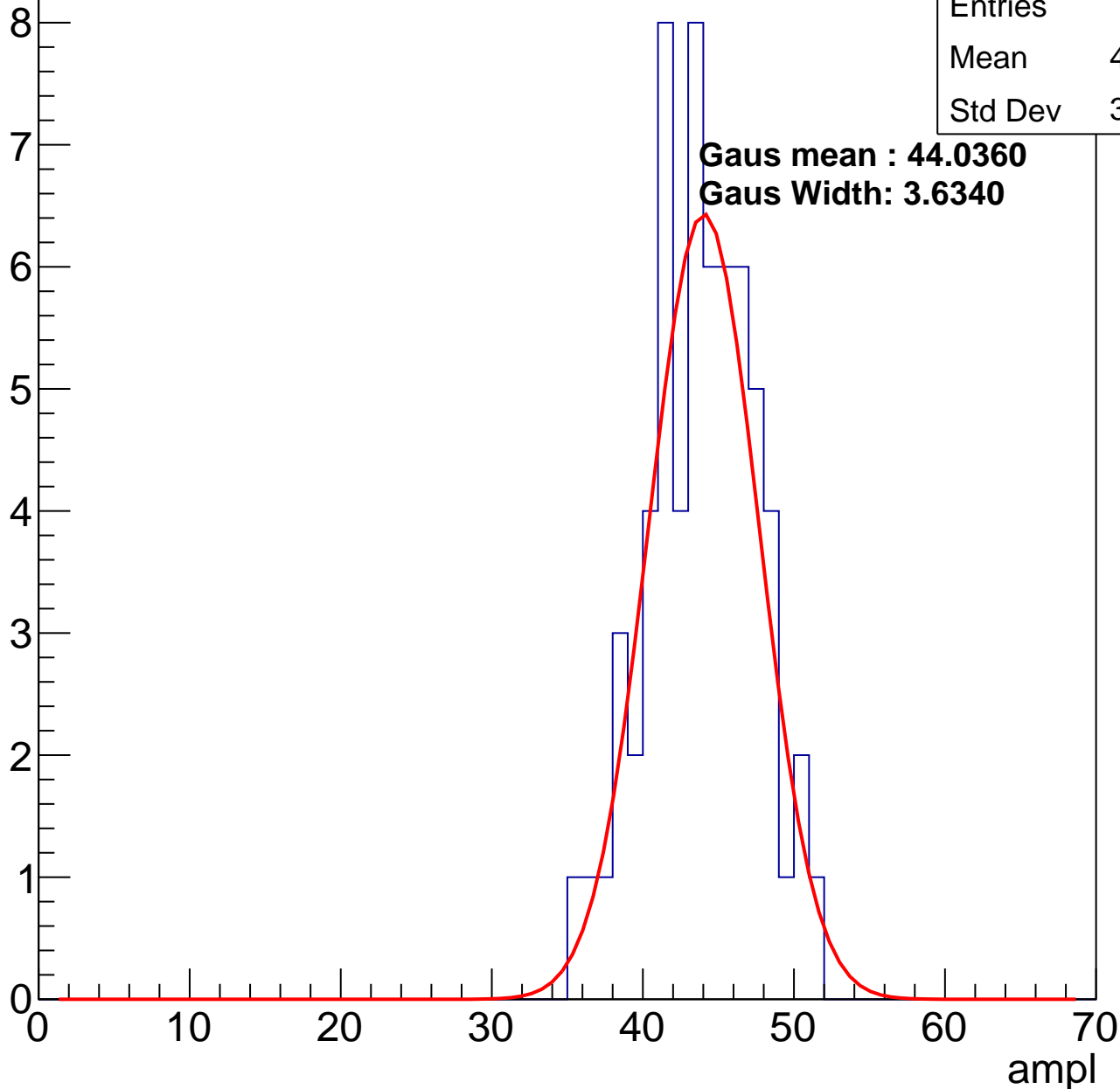
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.44
Std Dev	3.513

Gaus mean : 44.0360

Gaus Width: 3.6340

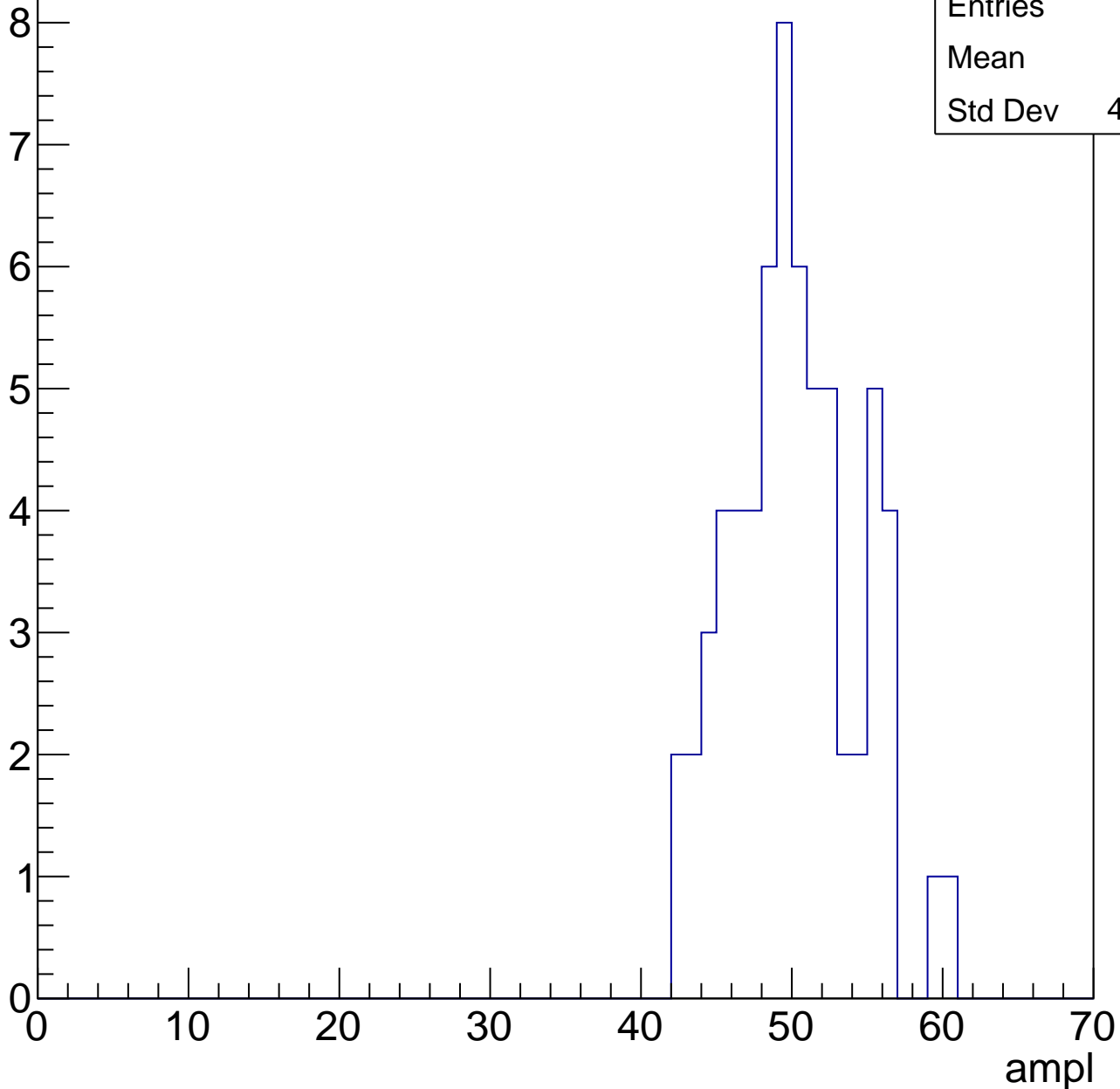


B1L102S, U17-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.7
Std Dev	4.118

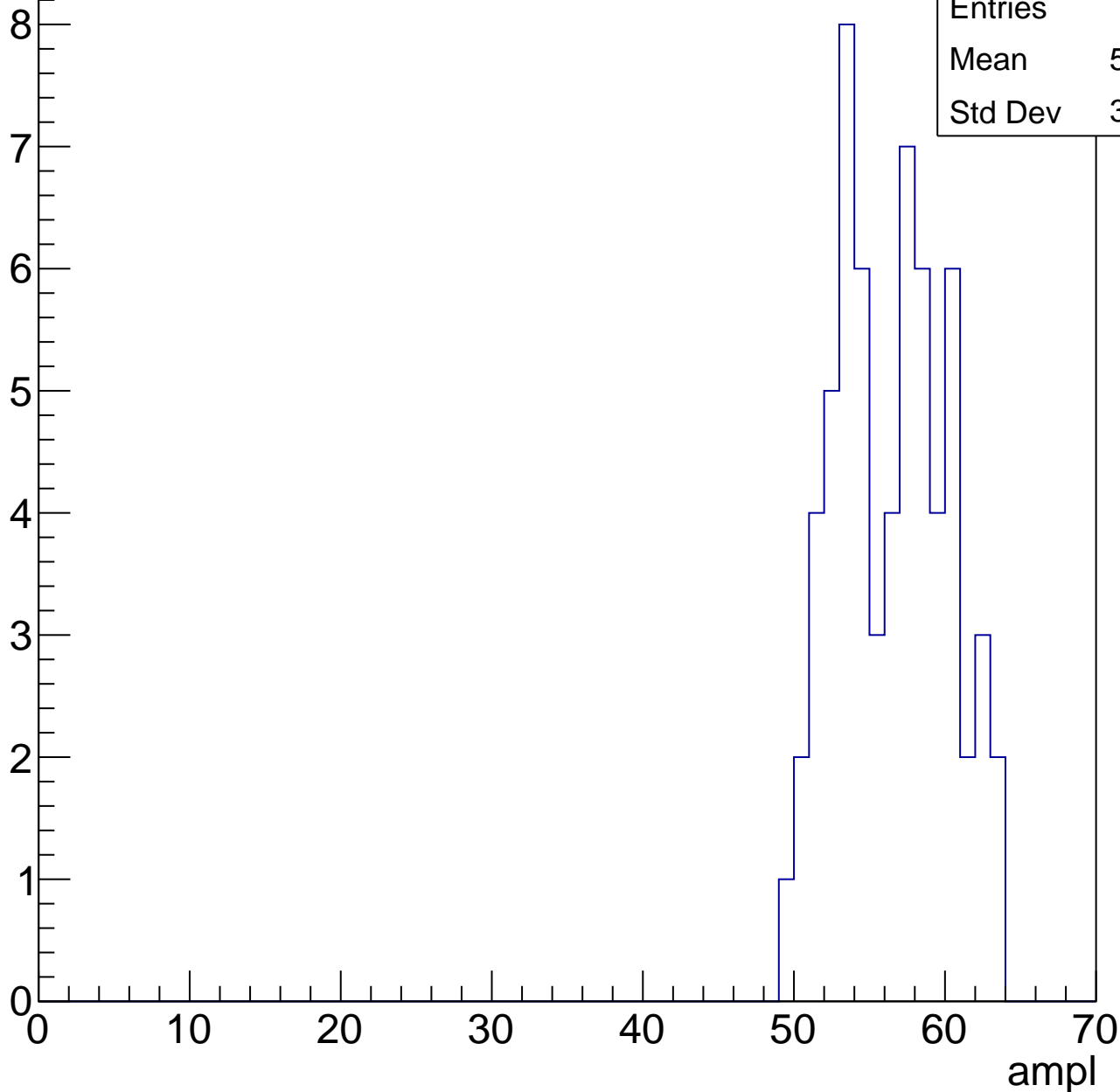


B1L102S, U17-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.98
Std Dev	3.606

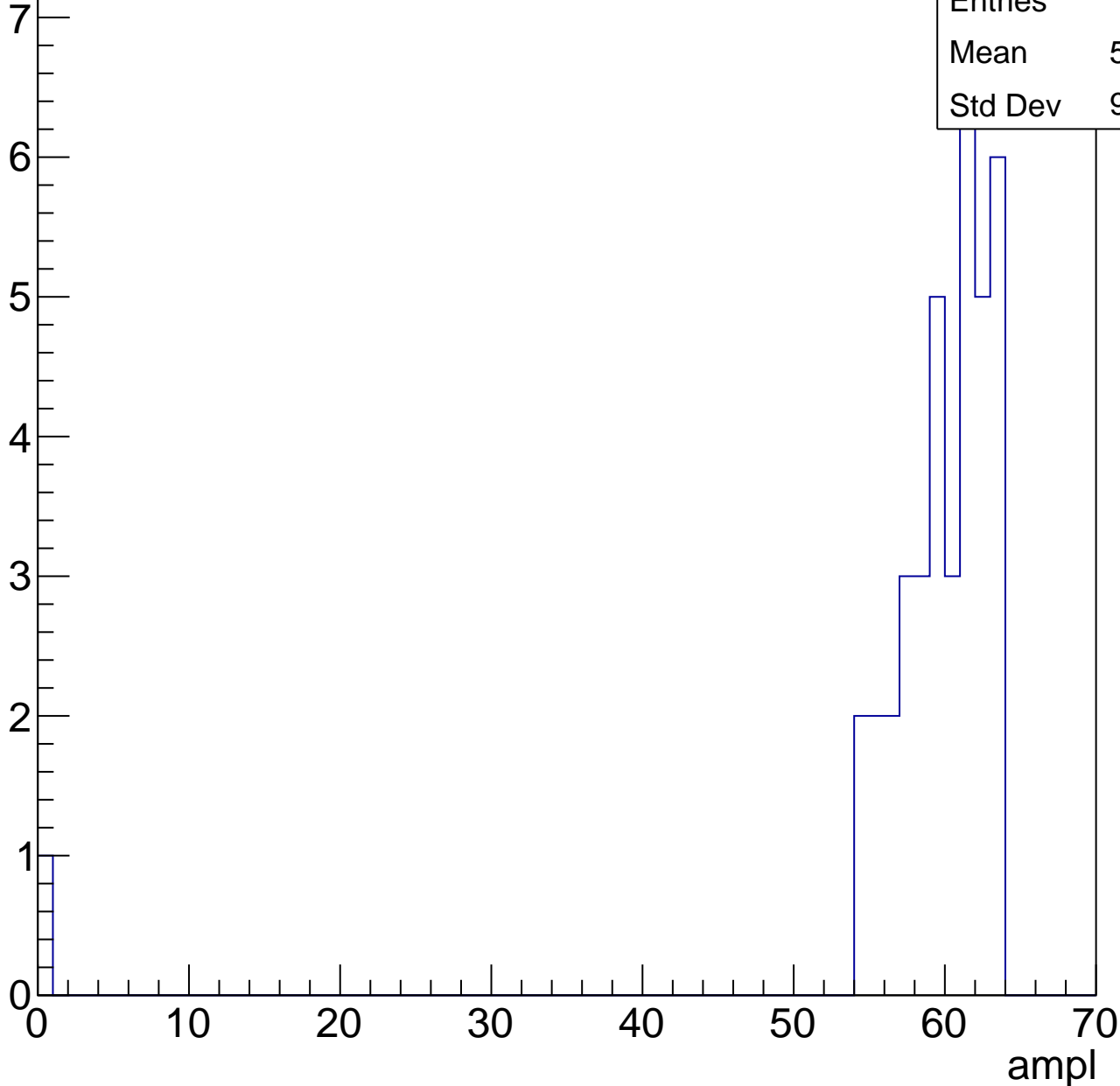


B1L102S, U17-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

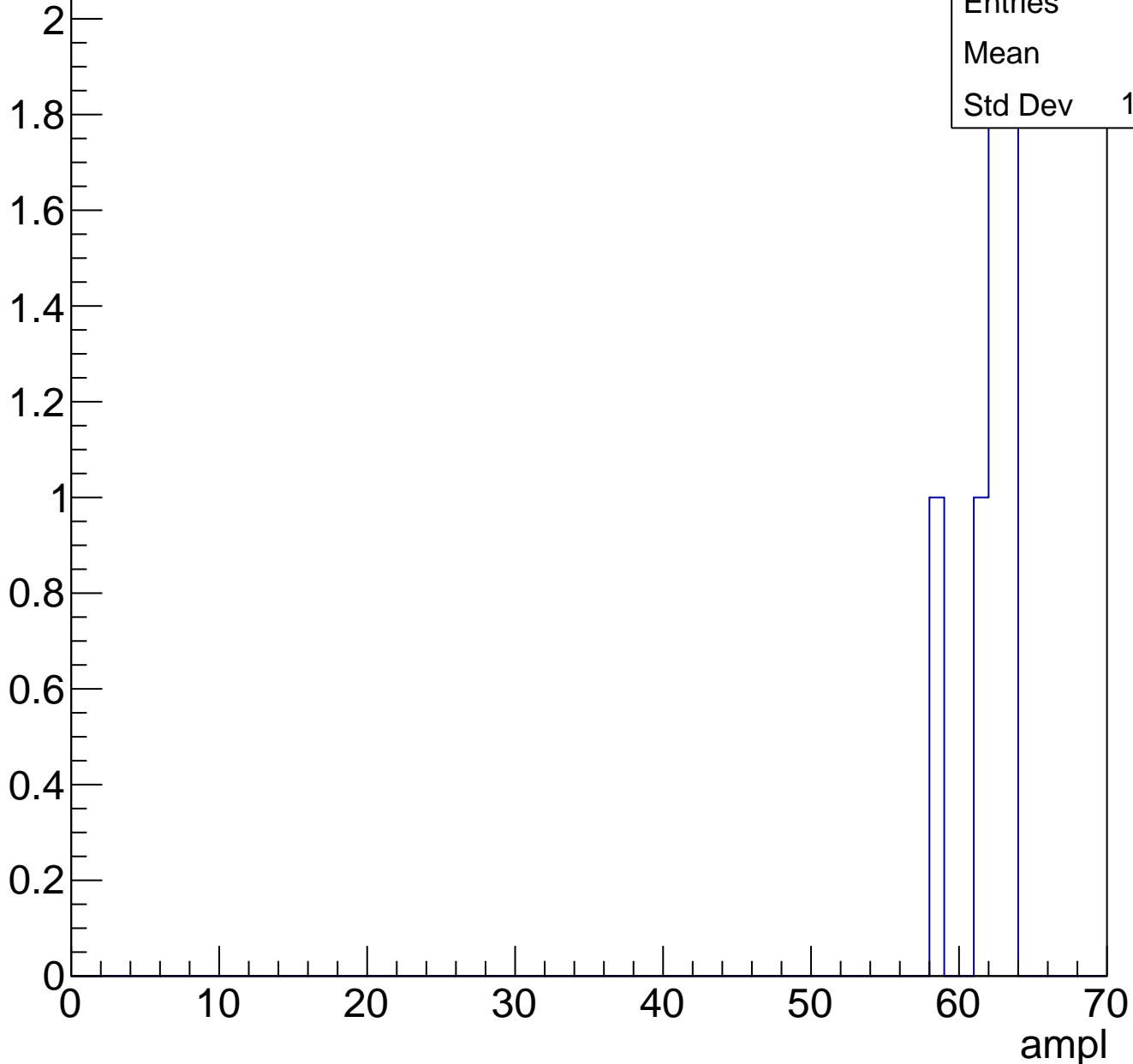
Entries	39
Mean	58.08
Std Dev	9.786



B1L102S, U17-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch126, adc0

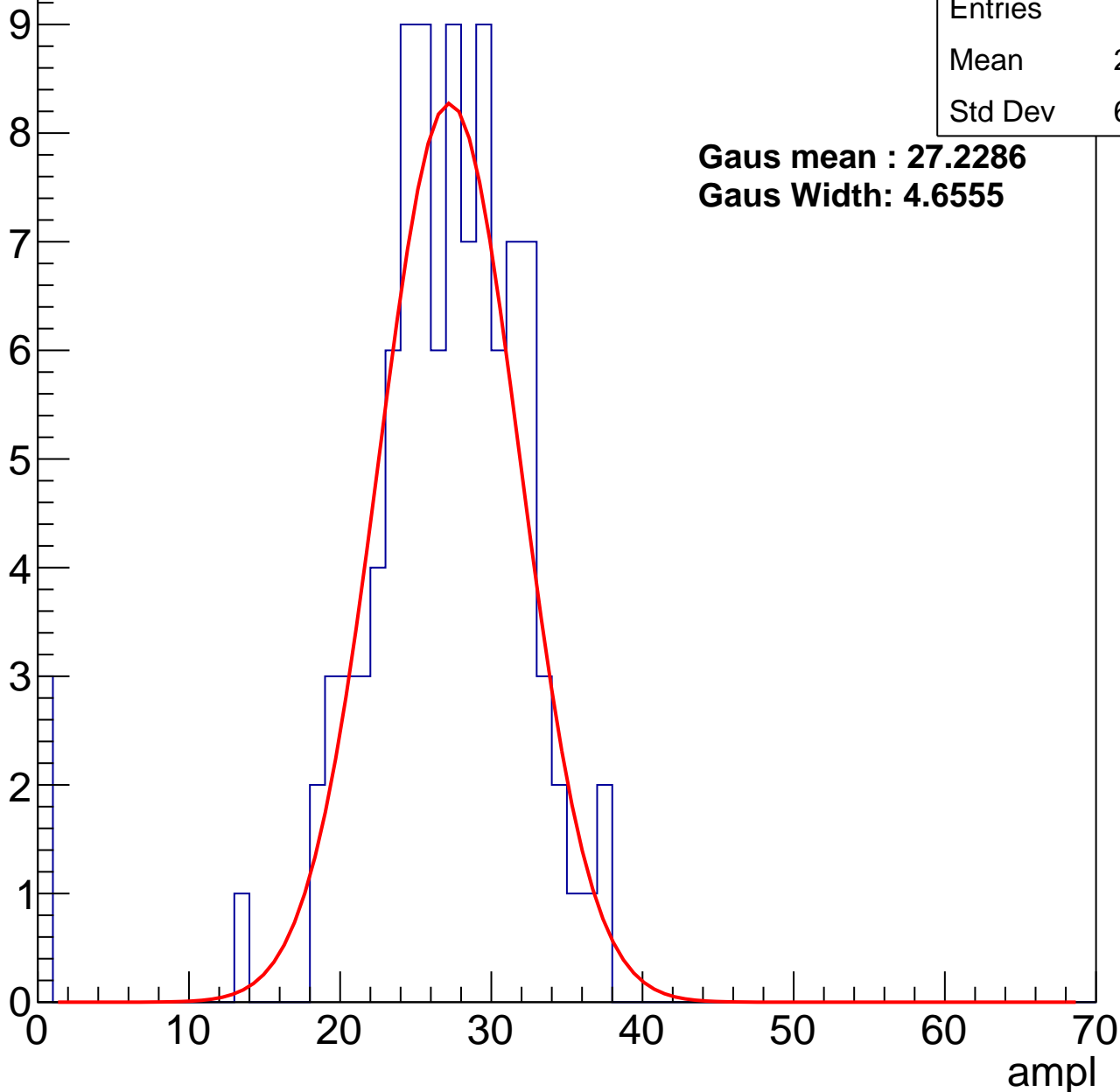
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	103
Mean	26.07
Std Dev	6.353

Gaus mean : 27.2286

Gaus Width: 4.6555



B1L102S, U17-ch126, adc1

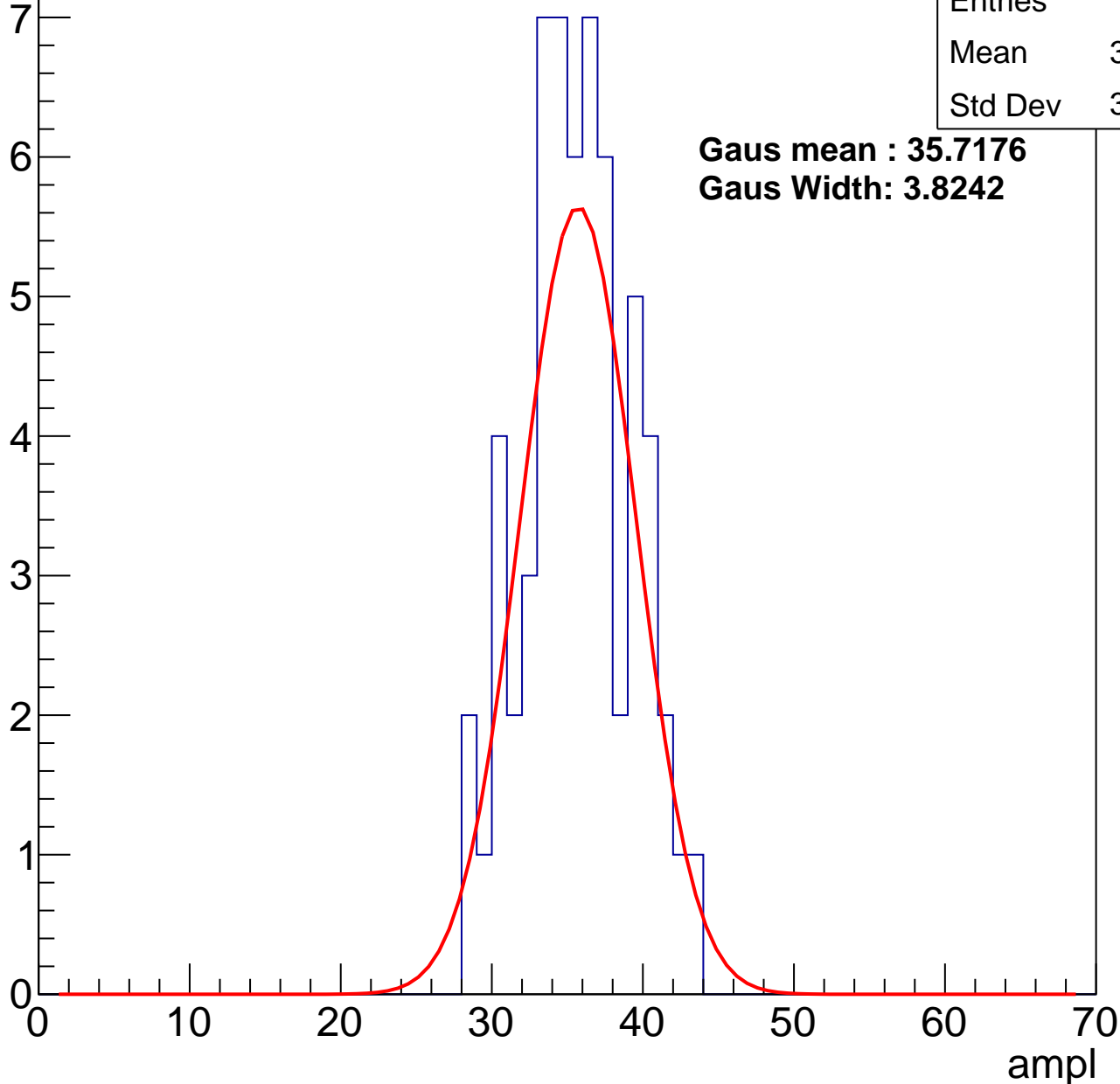
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.23
Std Dev	3.509

Gaus mean : 35.7176

Gaus Width: 3.8242



B1L102S, U17-ch126, adc2

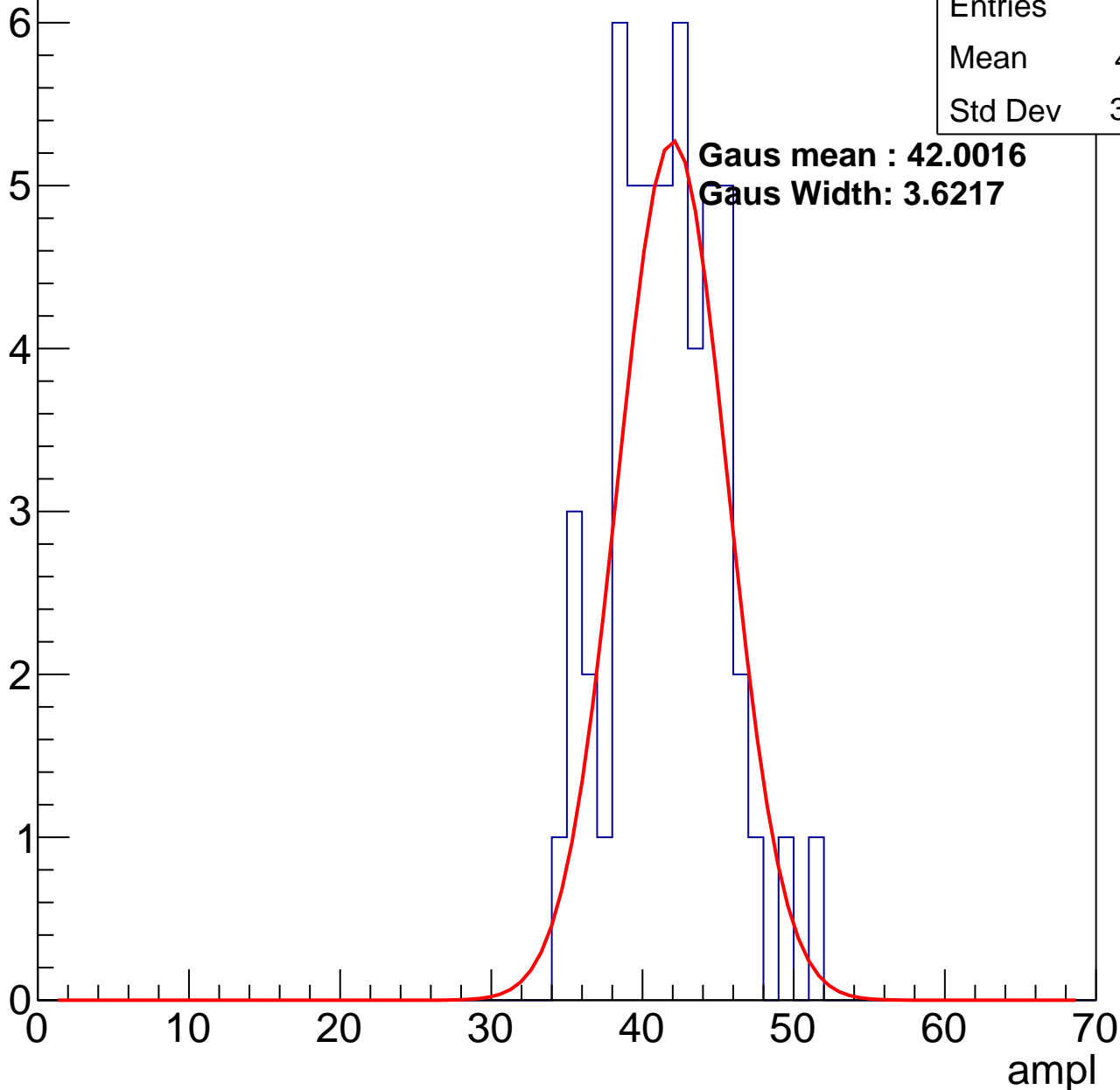
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.21
Std Dev	3.626

Gaus mean : 42.0016

Gaus Width: 3.6217

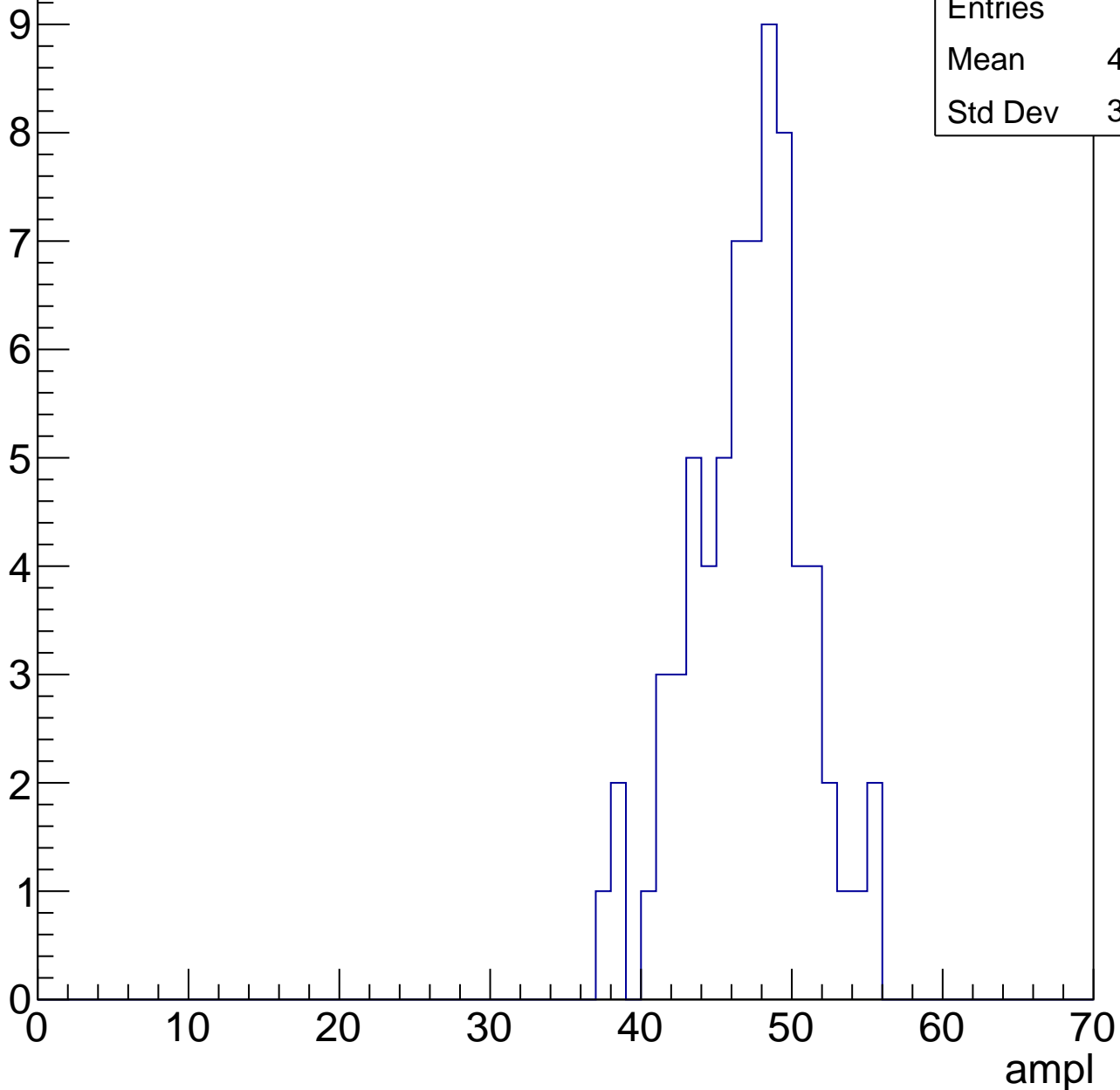


B1L102S, U17-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	46.64
Std Dev	3.867

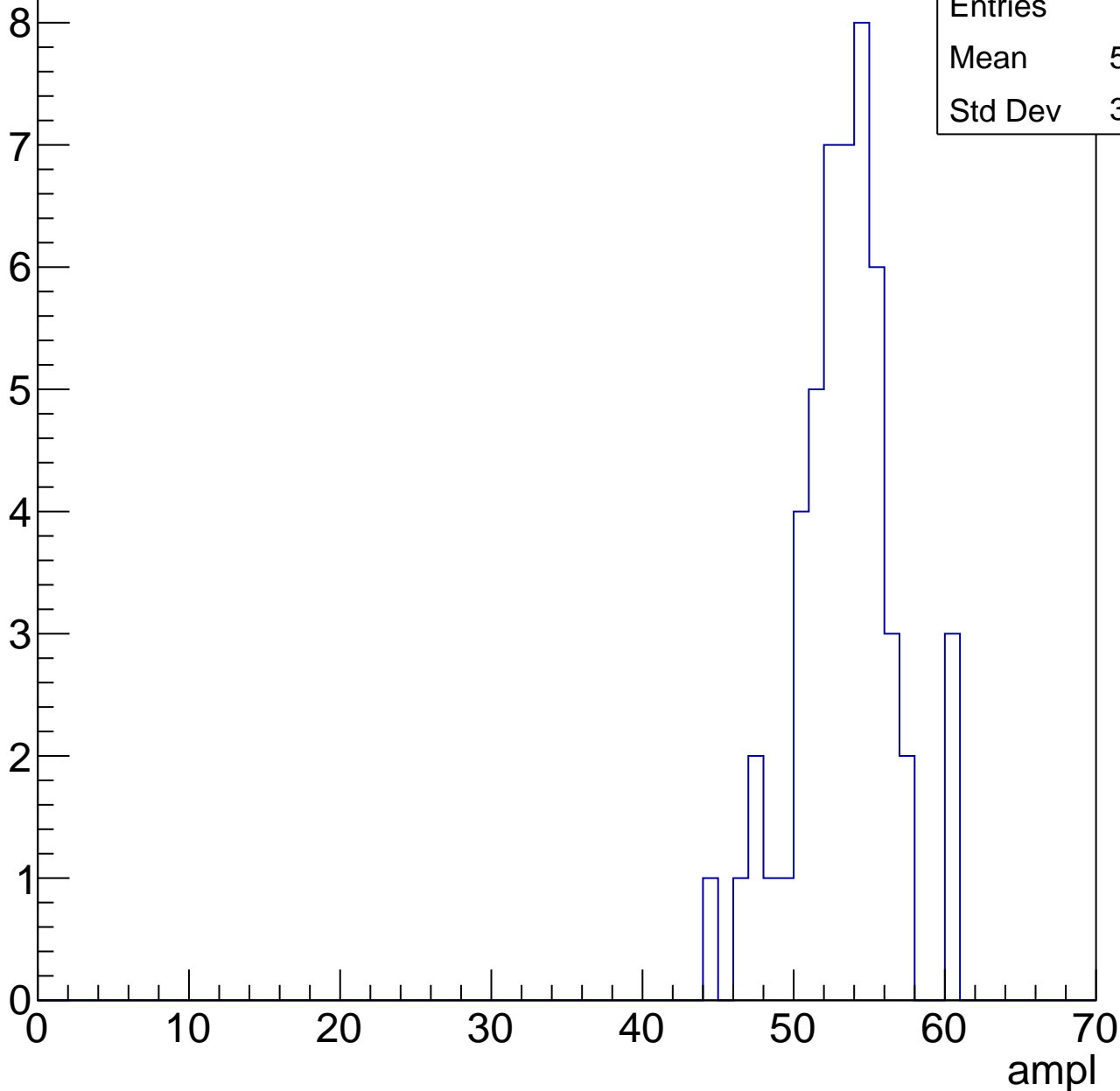


B1L102S, U17-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	52.84
Std Dev	3.262

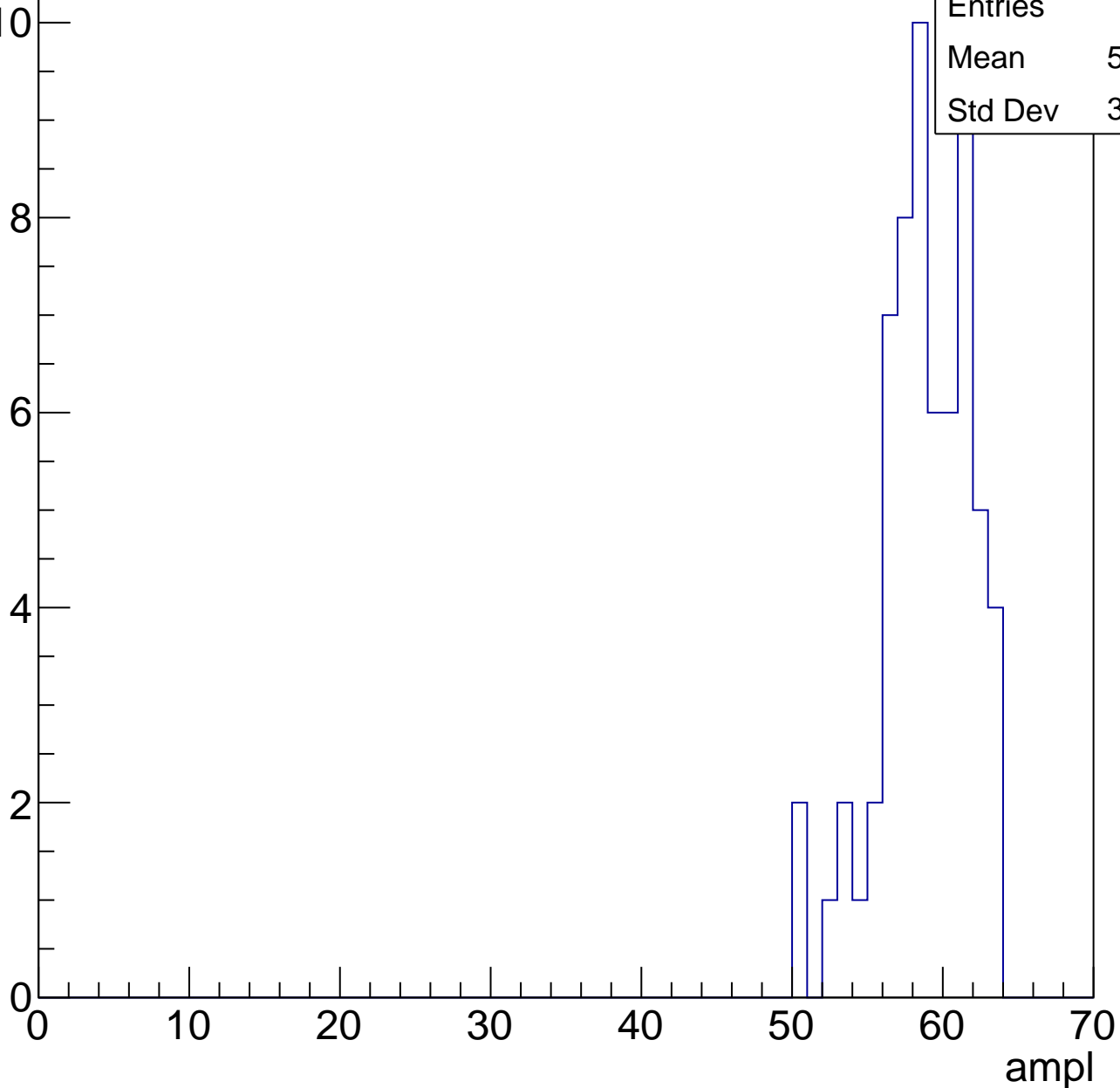


B1L102S, U17-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	58.33
Std Dev	3.003

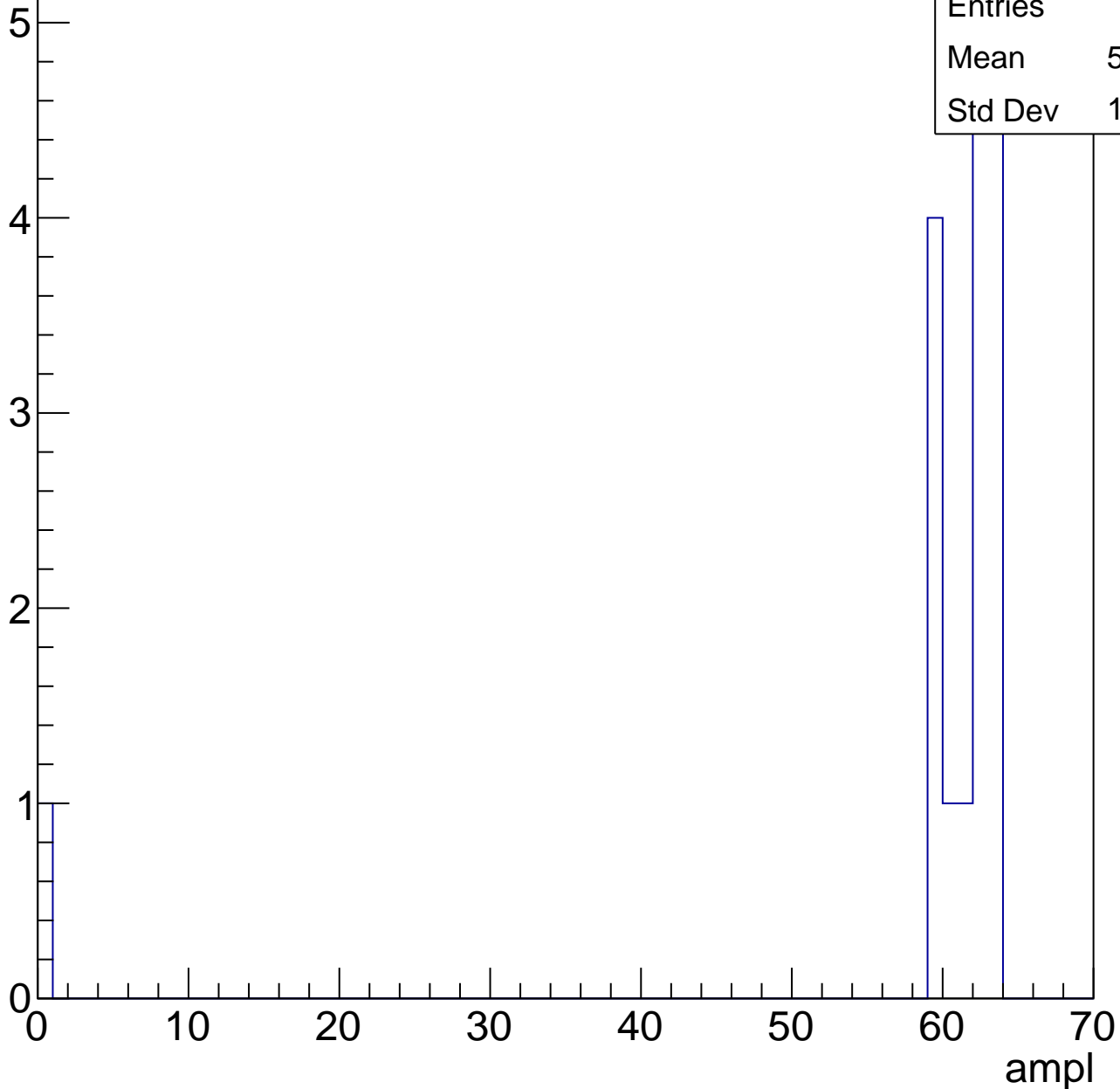


B1L102S, U17-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

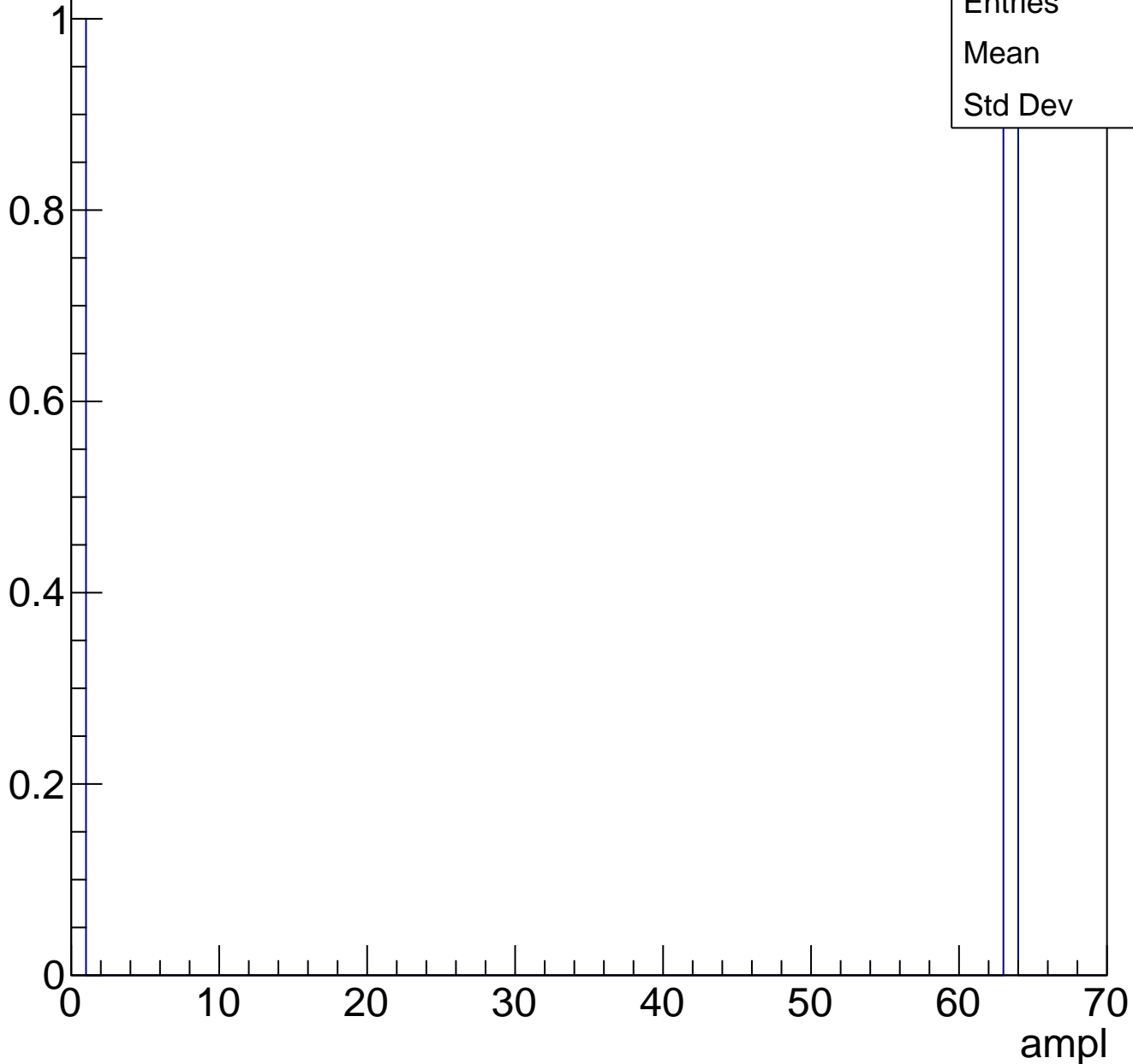
Entries	17
Mean	57.76
Std Dev	14.52



B1L102S, U17-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch127, adc0

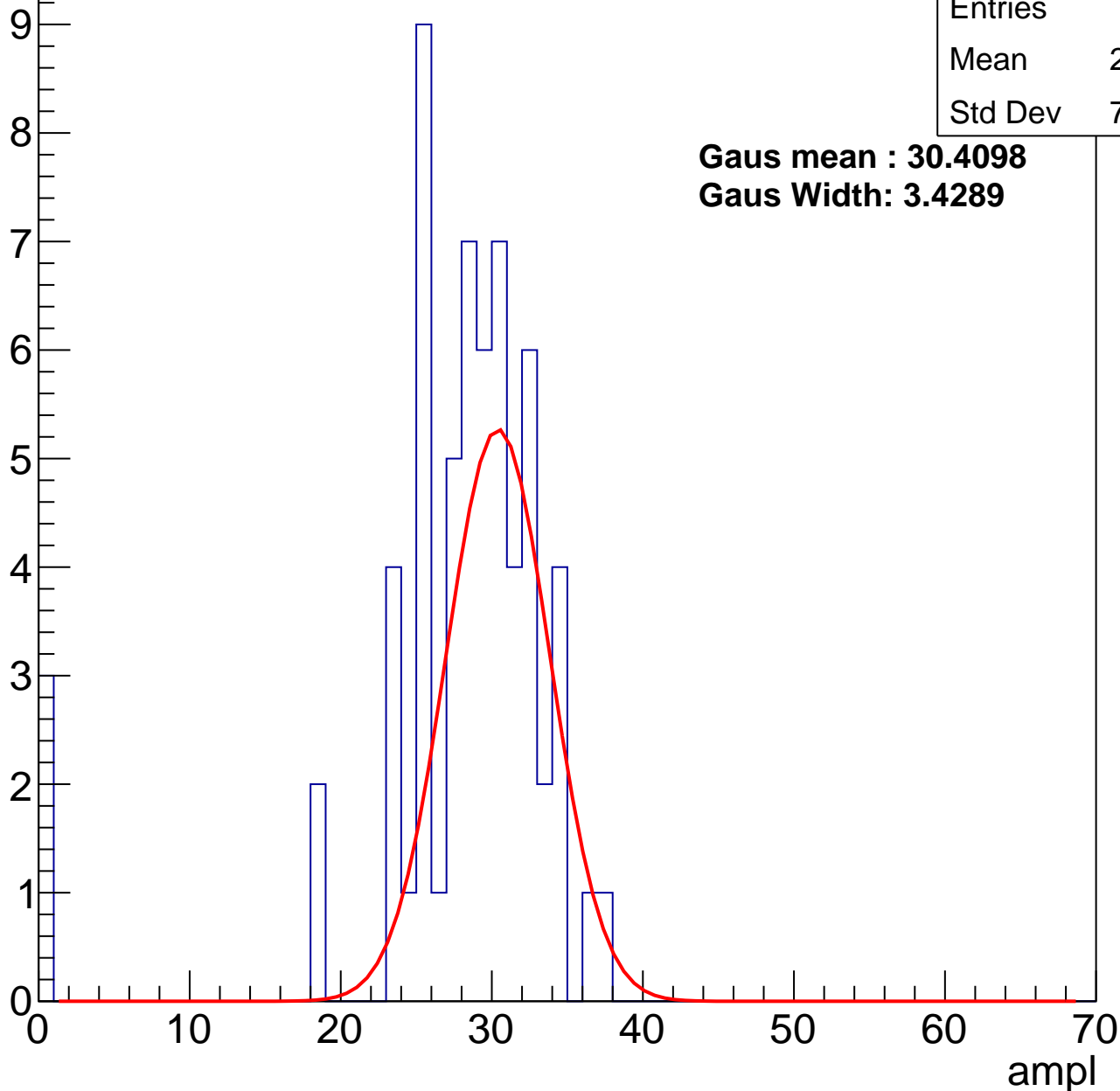
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.13
Std Dev	7.144

Gaus mean : 30.4098

Gaus Width: 3.4289



B1L102S, U17-ch127, adc1

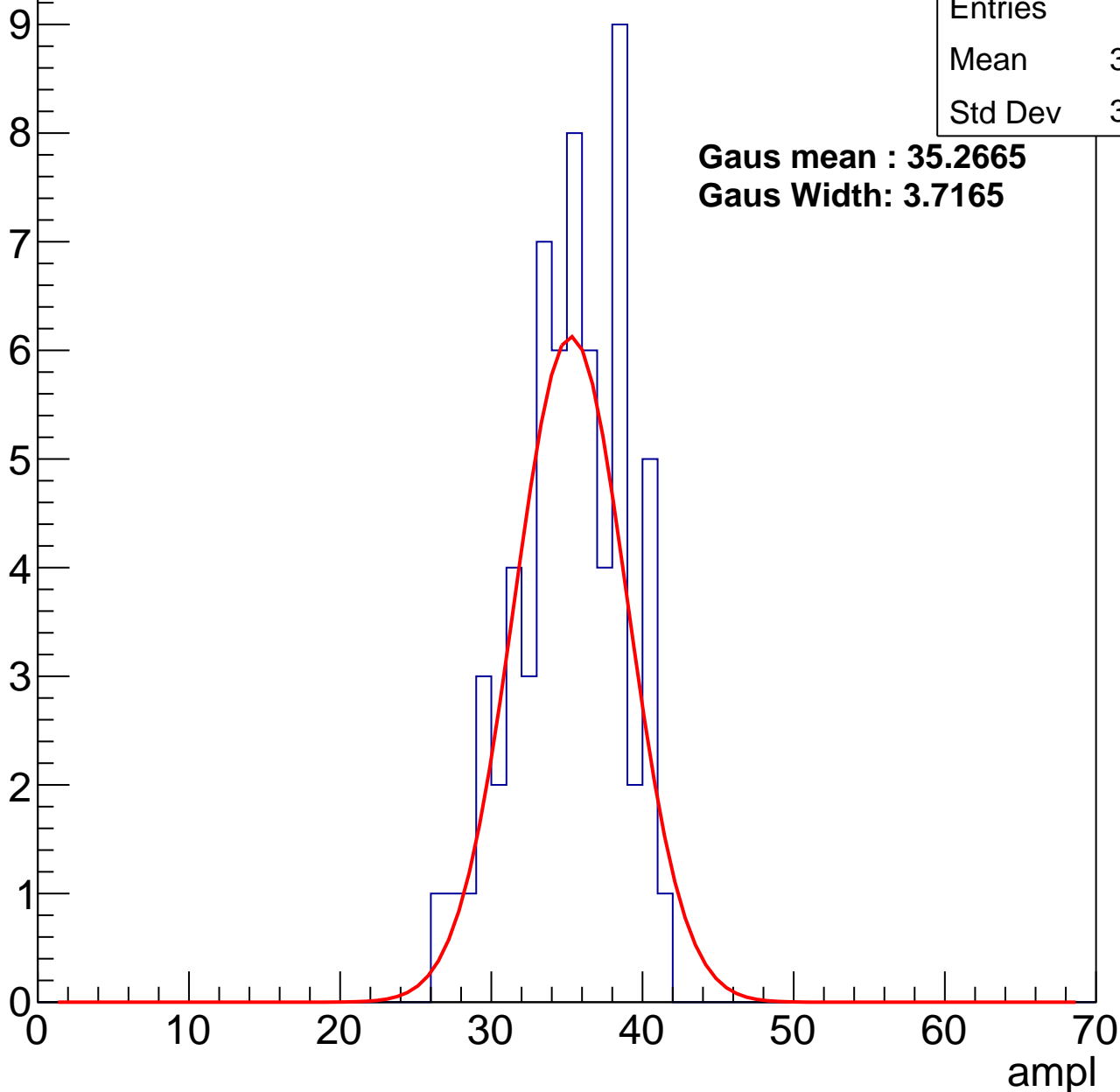
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.73
Std Dev	3.492

Gaus mean : 35.2665

Gaus Width: 3.7165



B1L102S, U17-ch127, adc2

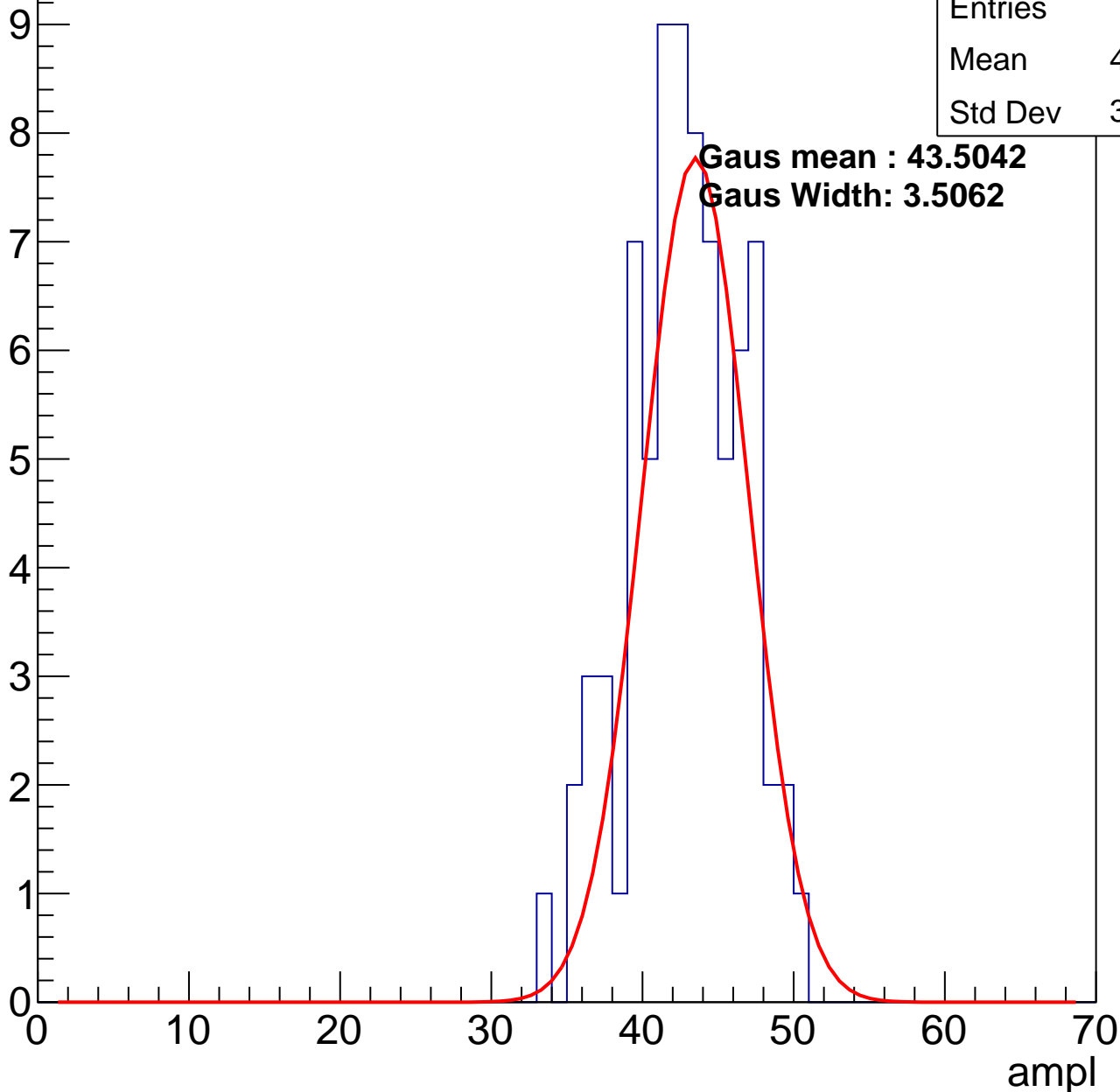
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.38
Std Dev	3.659

Gaus mean : 43.5042

Gaus Width: 3.5062

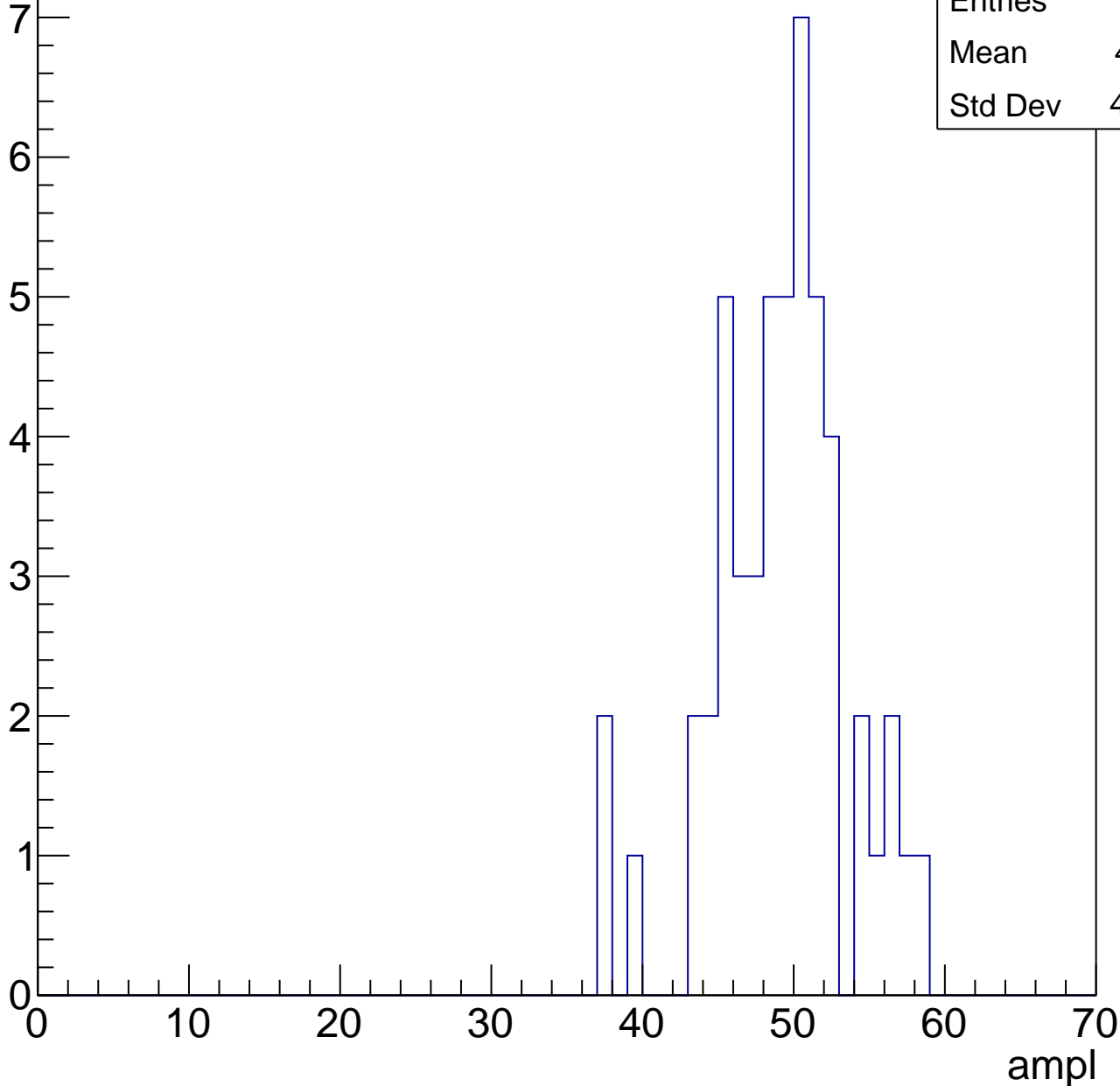


B1L102S, U17-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	48.61
Std Dev	4.468

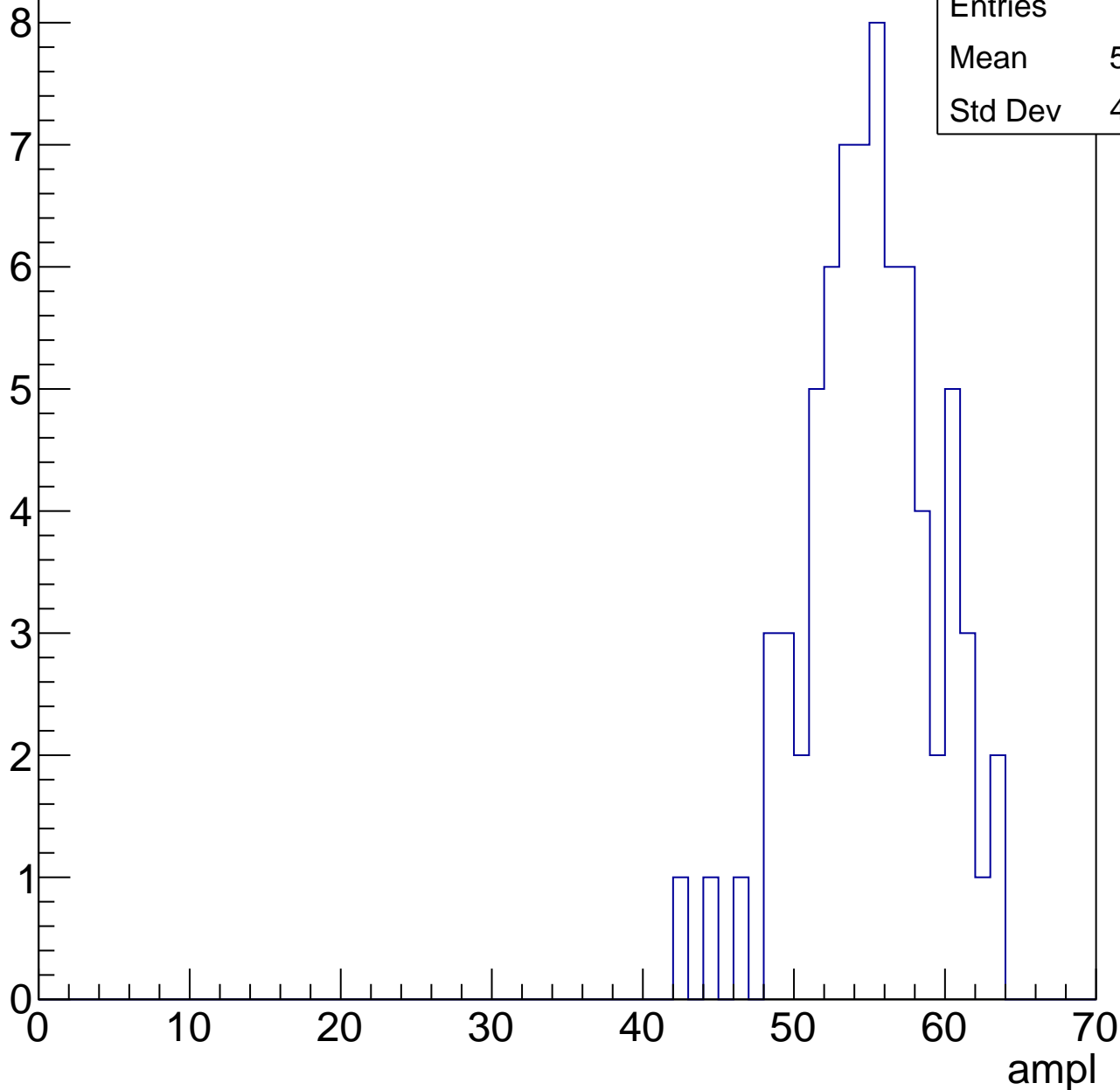


B1L102S, U17-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	54.49
Std Dev	4.275



B1L102S, U17-ch127, adc5

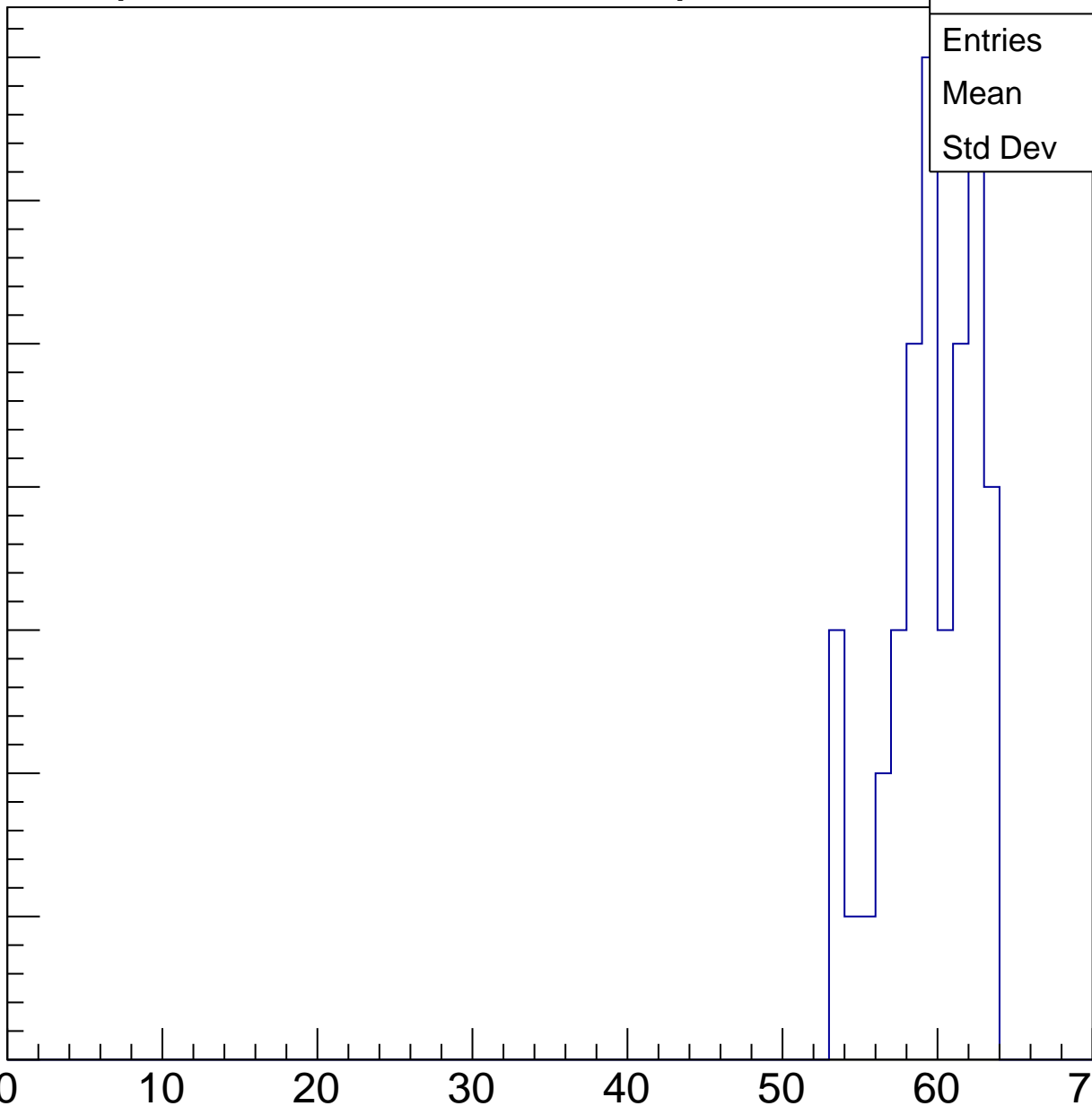
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	59.15
Std Dev	2.85

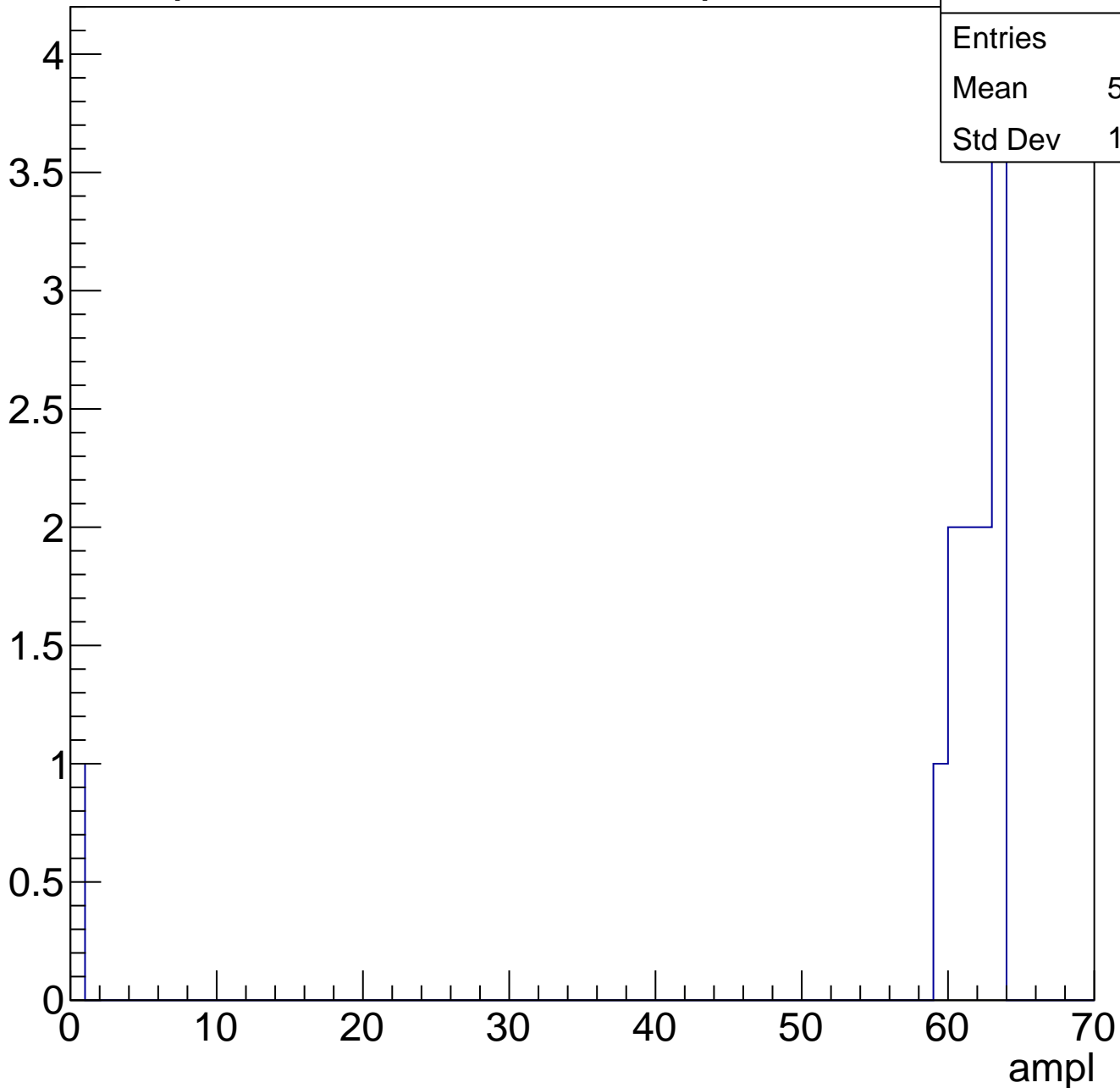
ampl



B1L102S, U17-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U17-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

