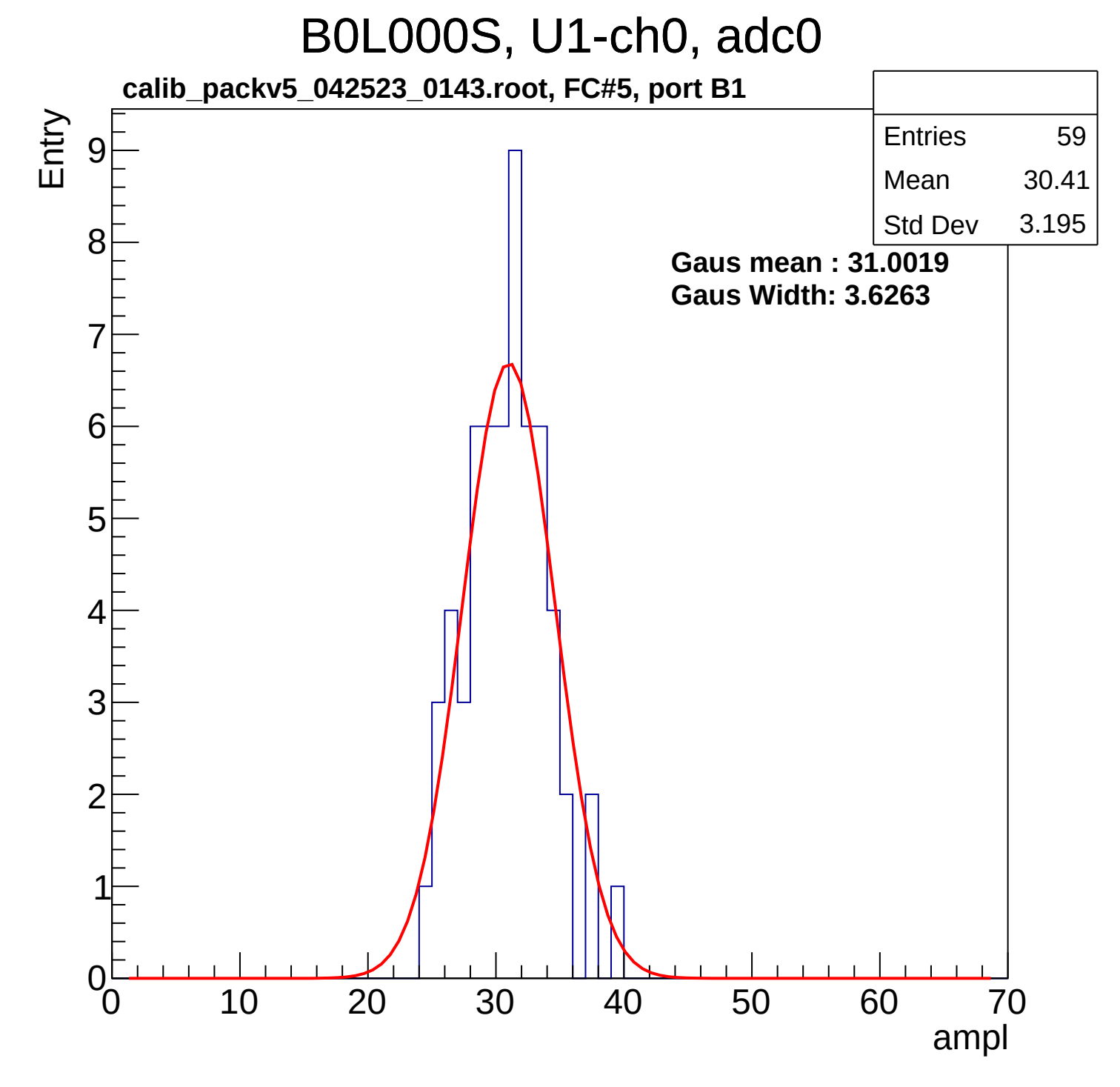
B0L000S, U1-ch0, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 30.41
Std Dev 3.195
Gaus mean : 31.0019
Gaus Width: 3.6263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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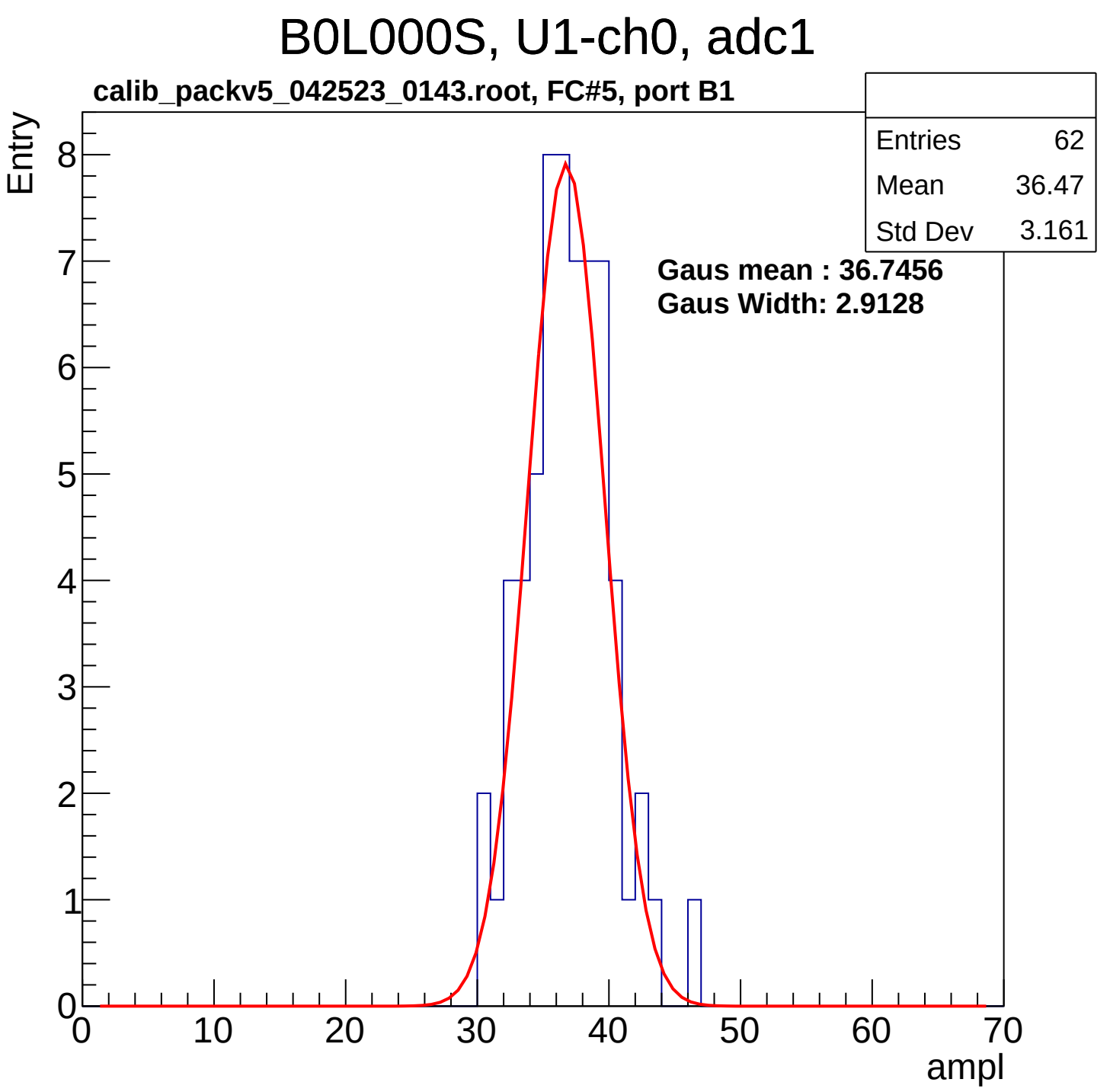
B0L000S, U1-ch0, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.47
Std Dev 3.161
Gaus mean : 36.7456
Gaus Width: 2.9128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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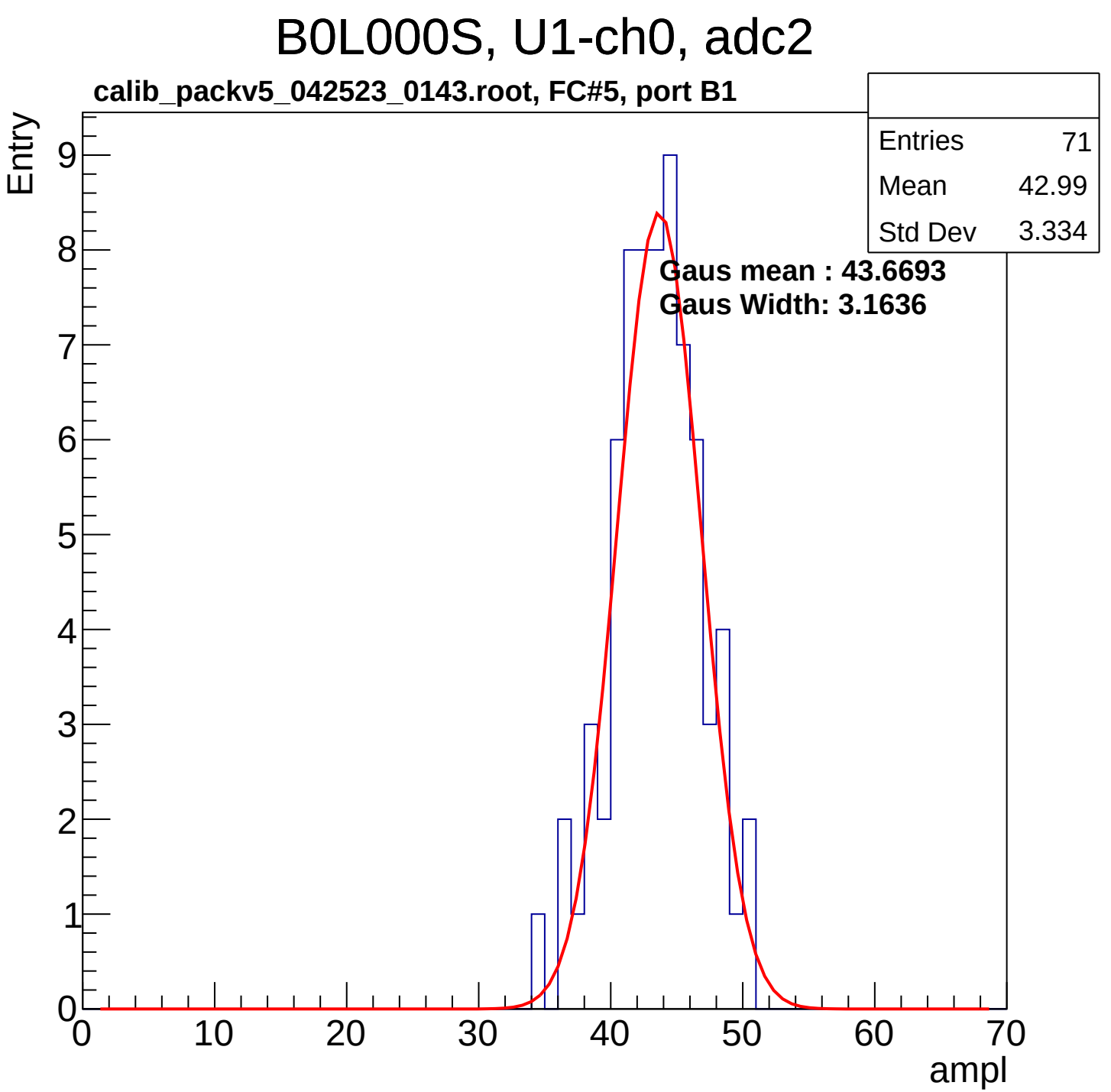
B0L000S, U1-ch0, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.99
Std Dev 3.334
Gaus mean : 43.6693
Gaus Width: 3.1636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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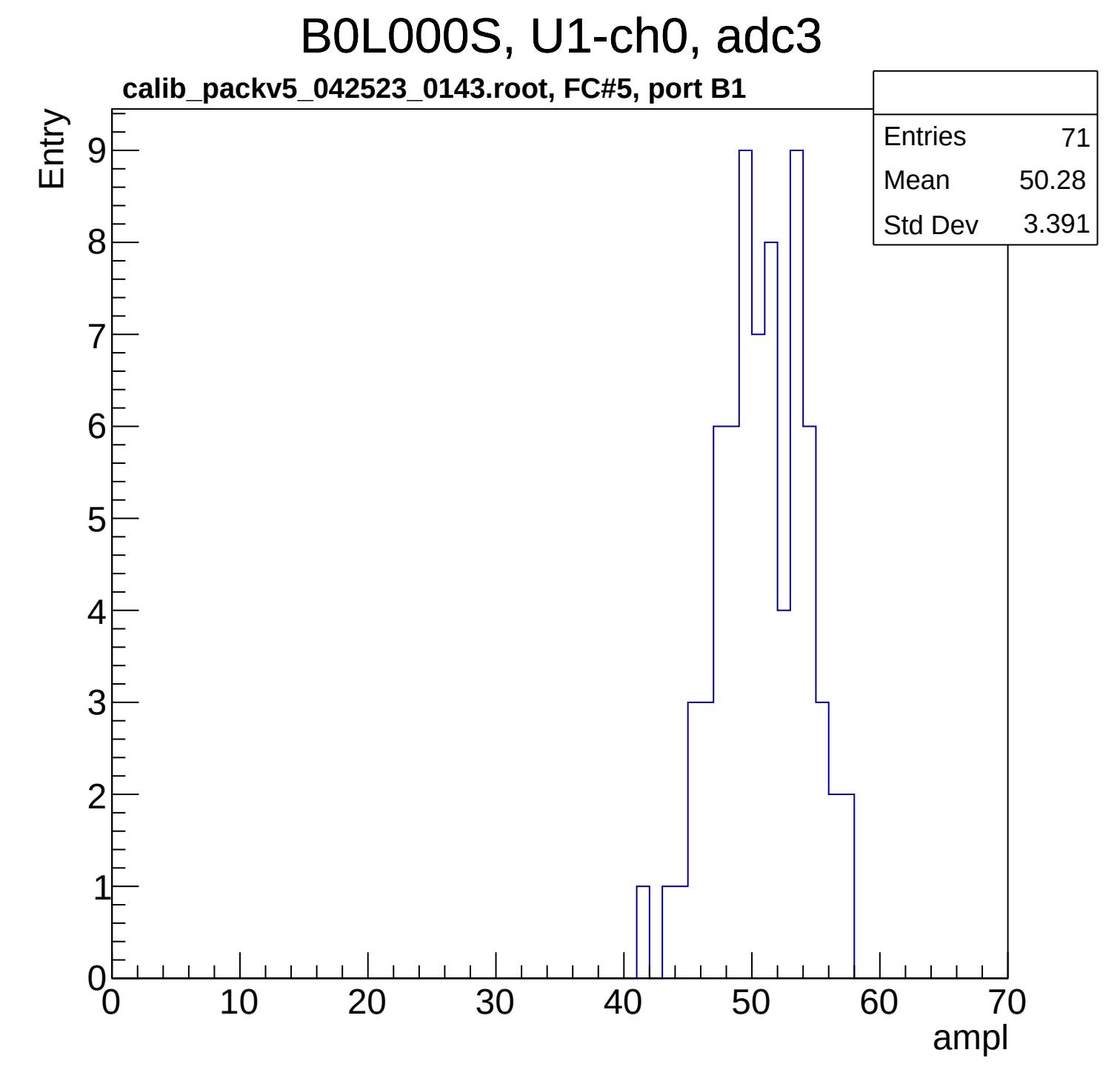
B0L000S, U1-ch0, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.28
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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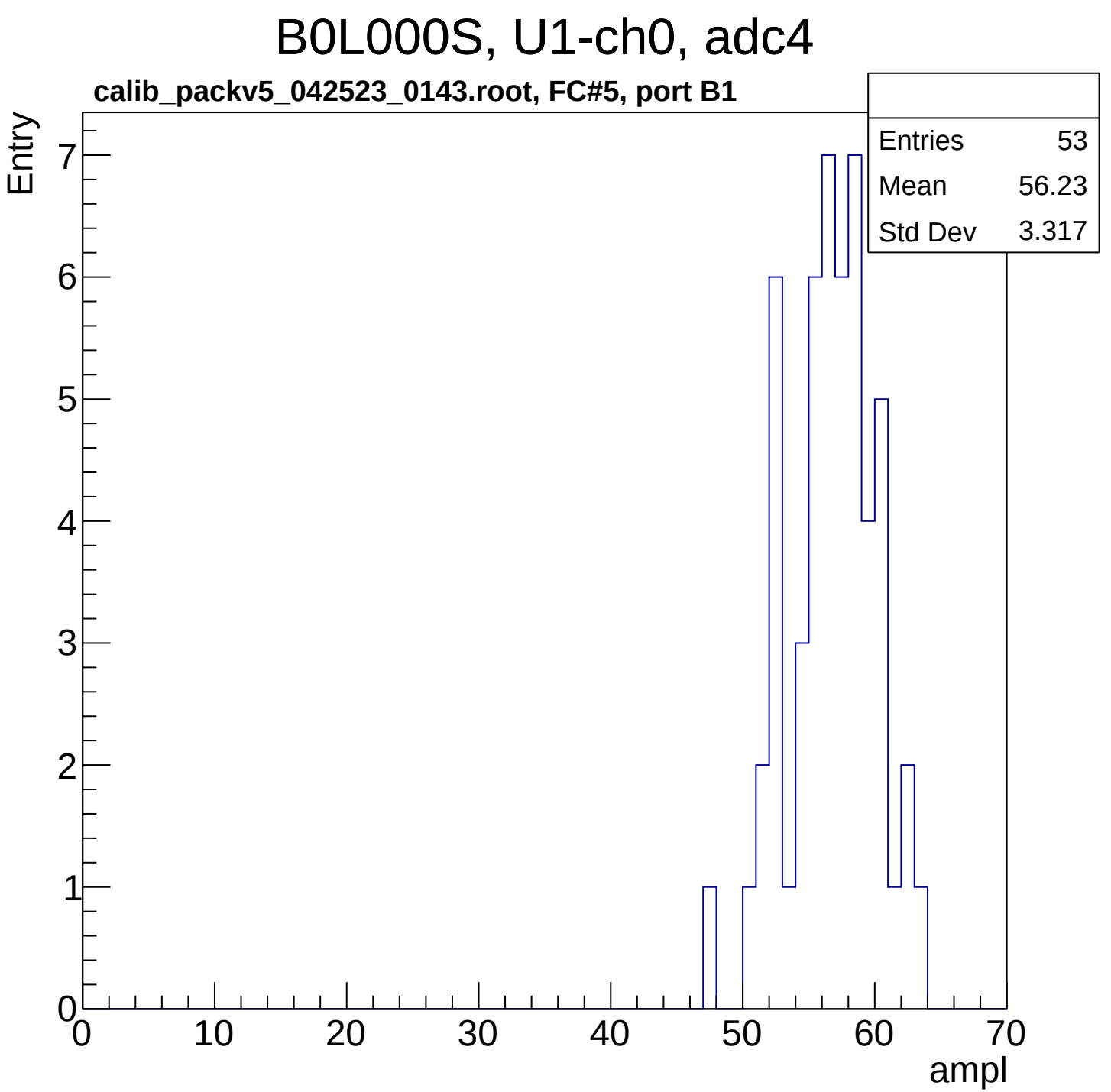
B0L000S, U1-ch0, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.23
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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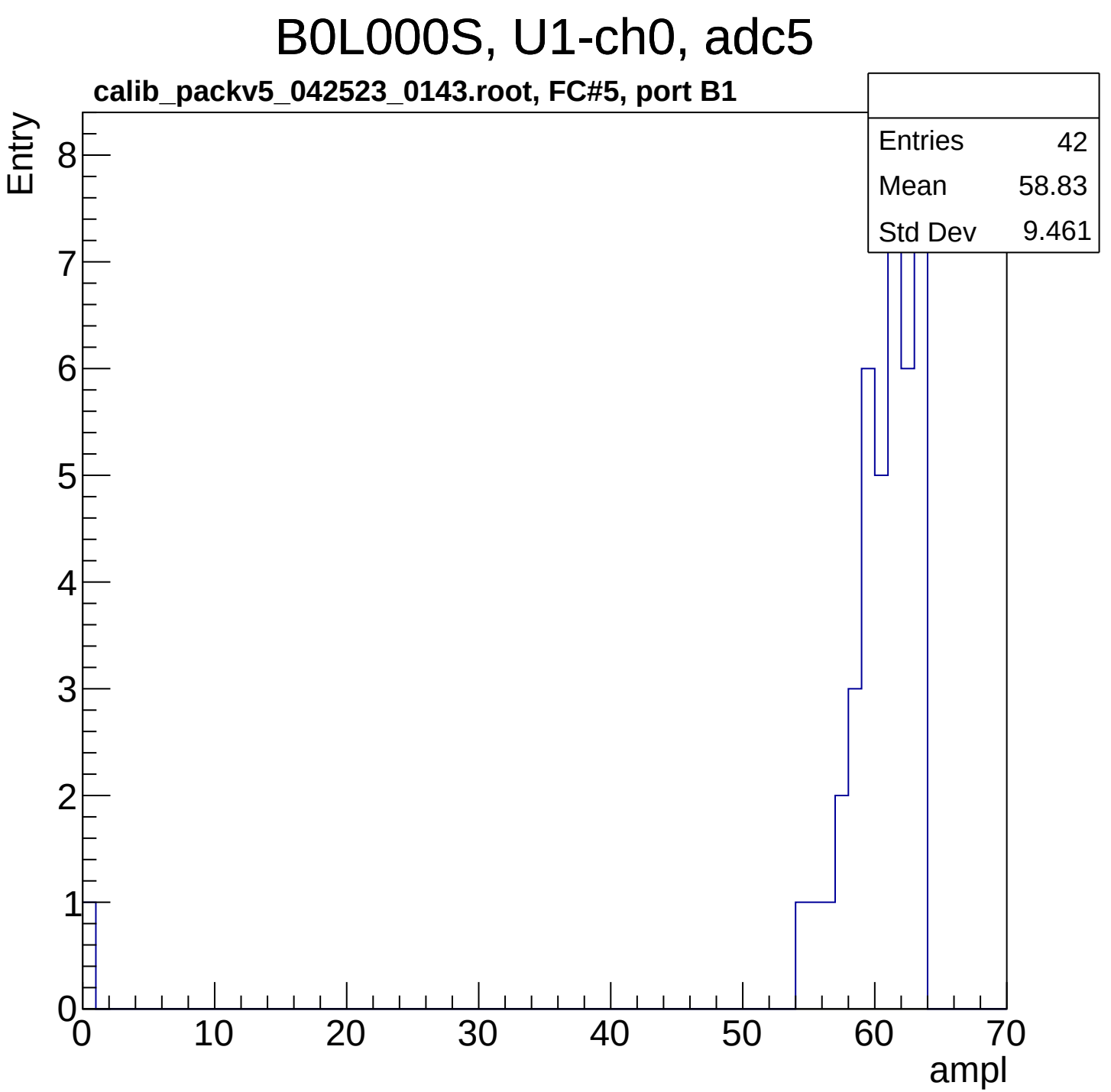
B0L000S, U1-ch0, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.83
Std Dev 9.461
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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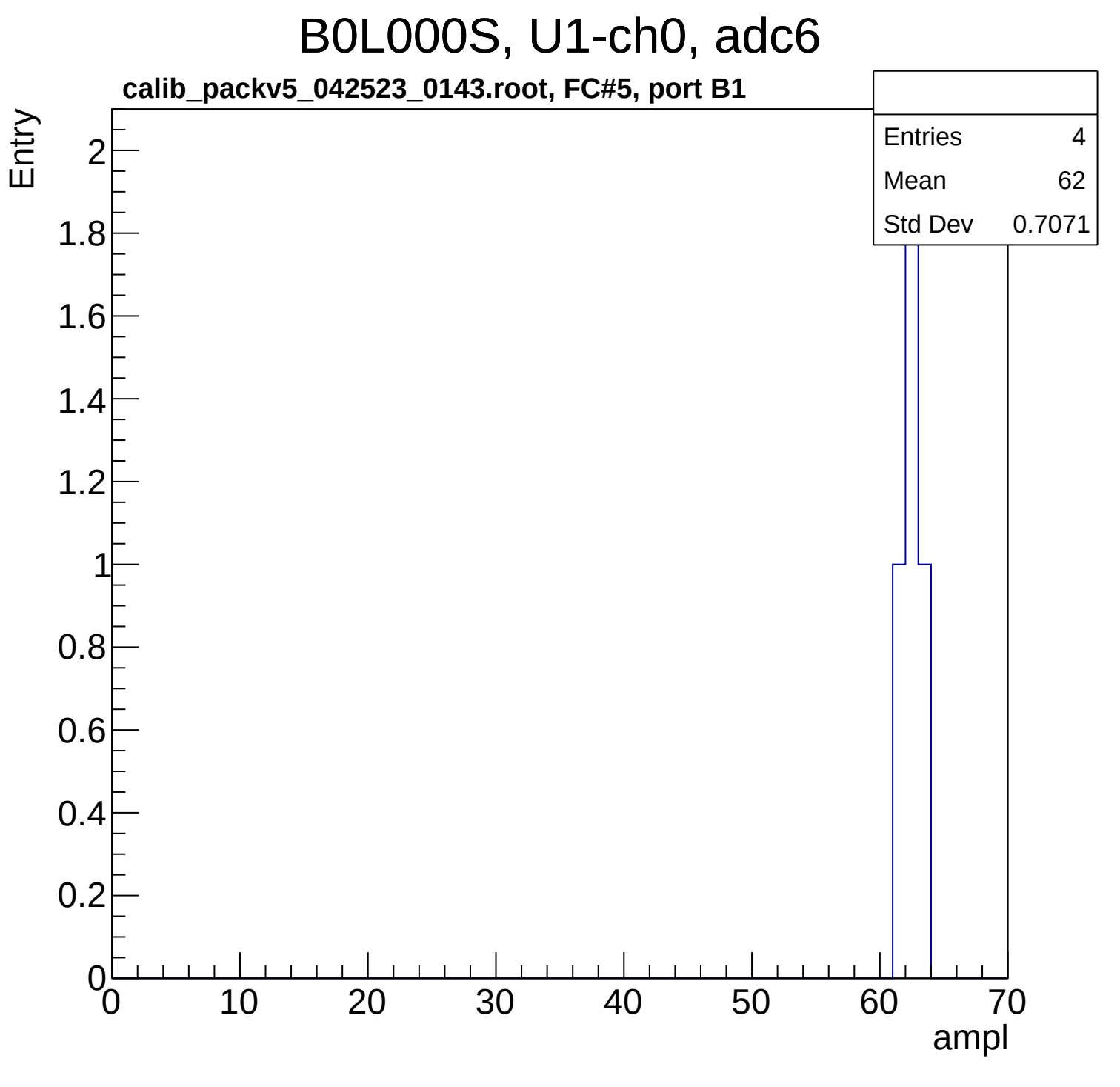
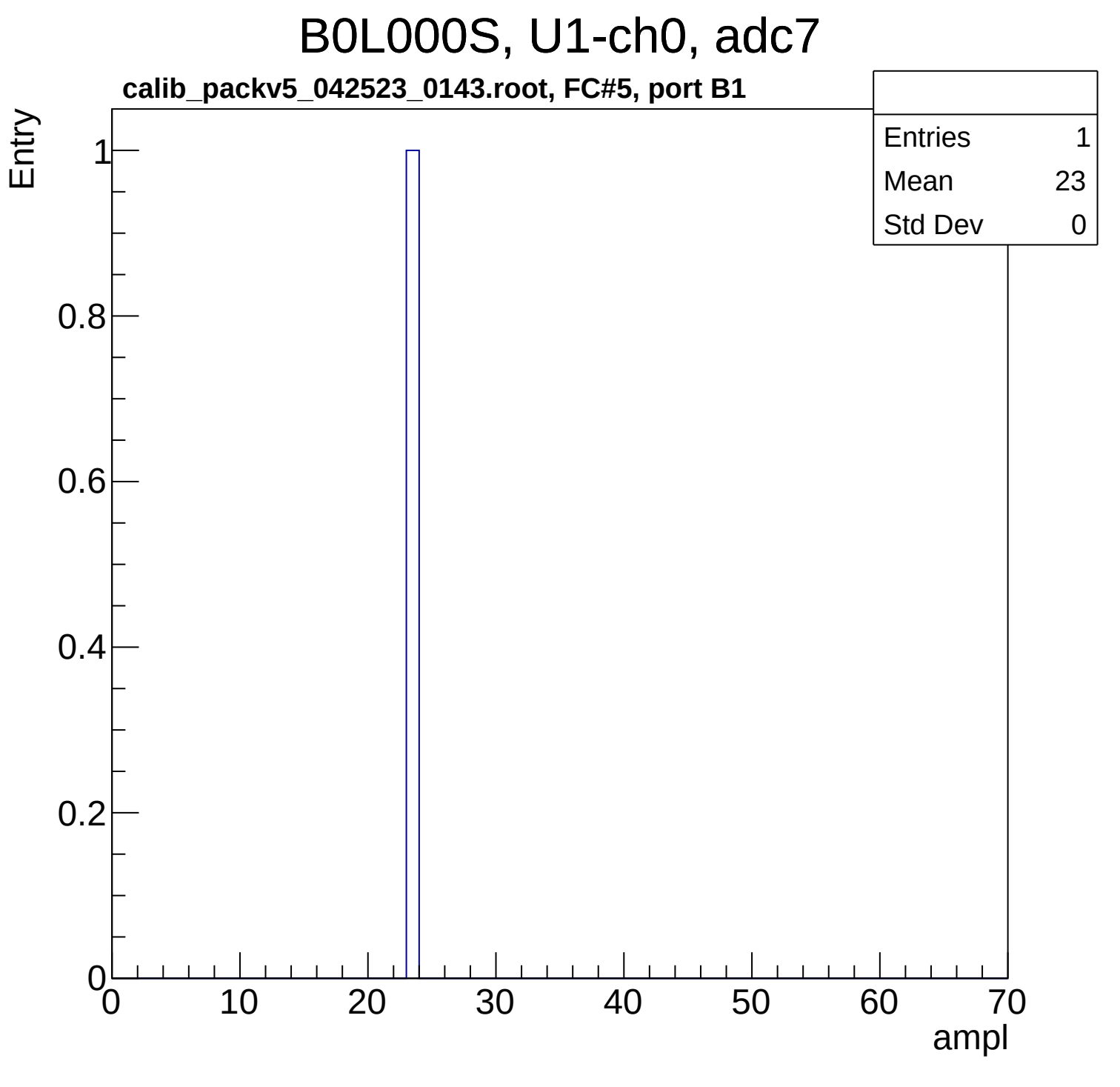

B0L000S, U1-ch0, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch0, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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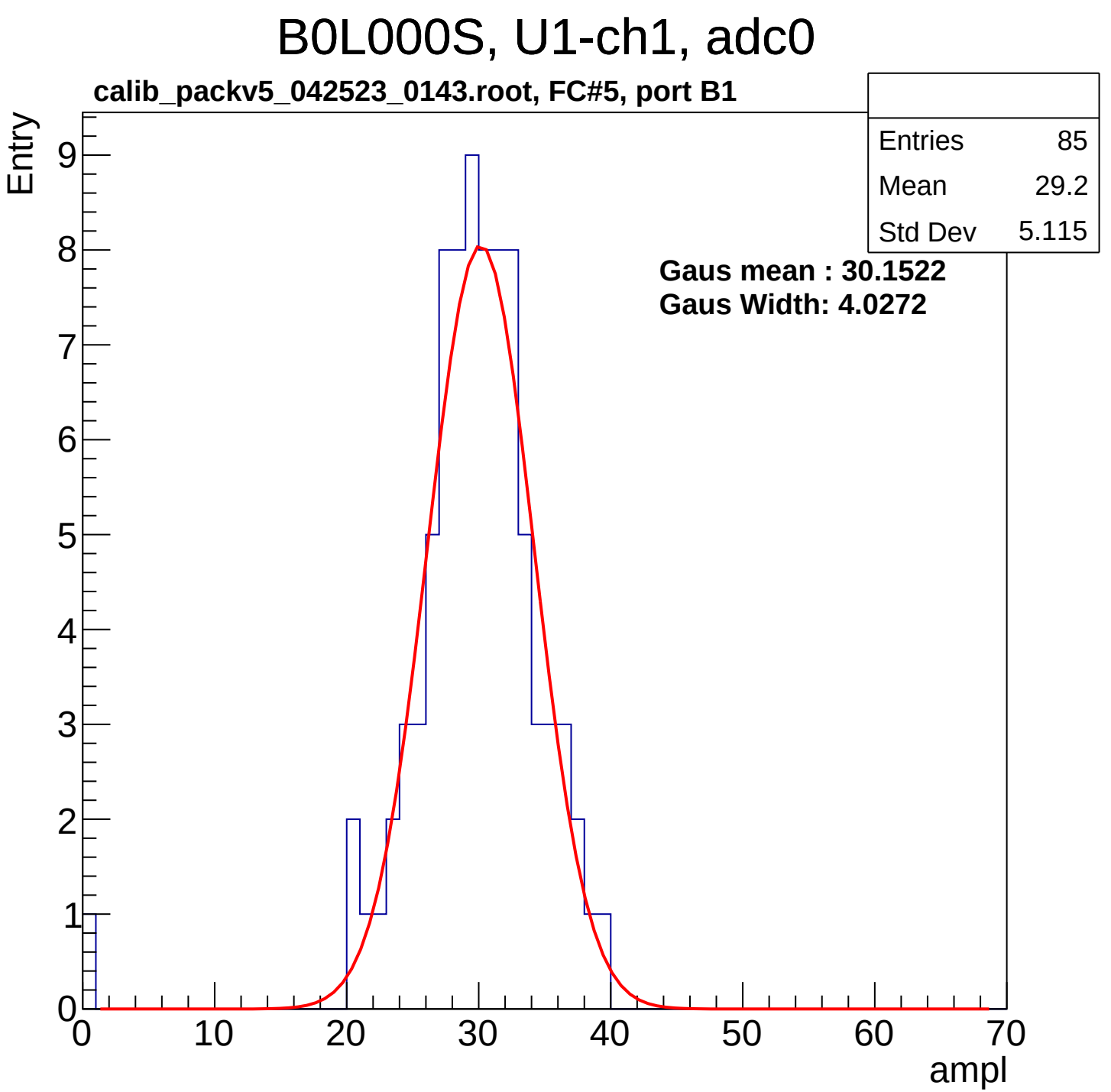
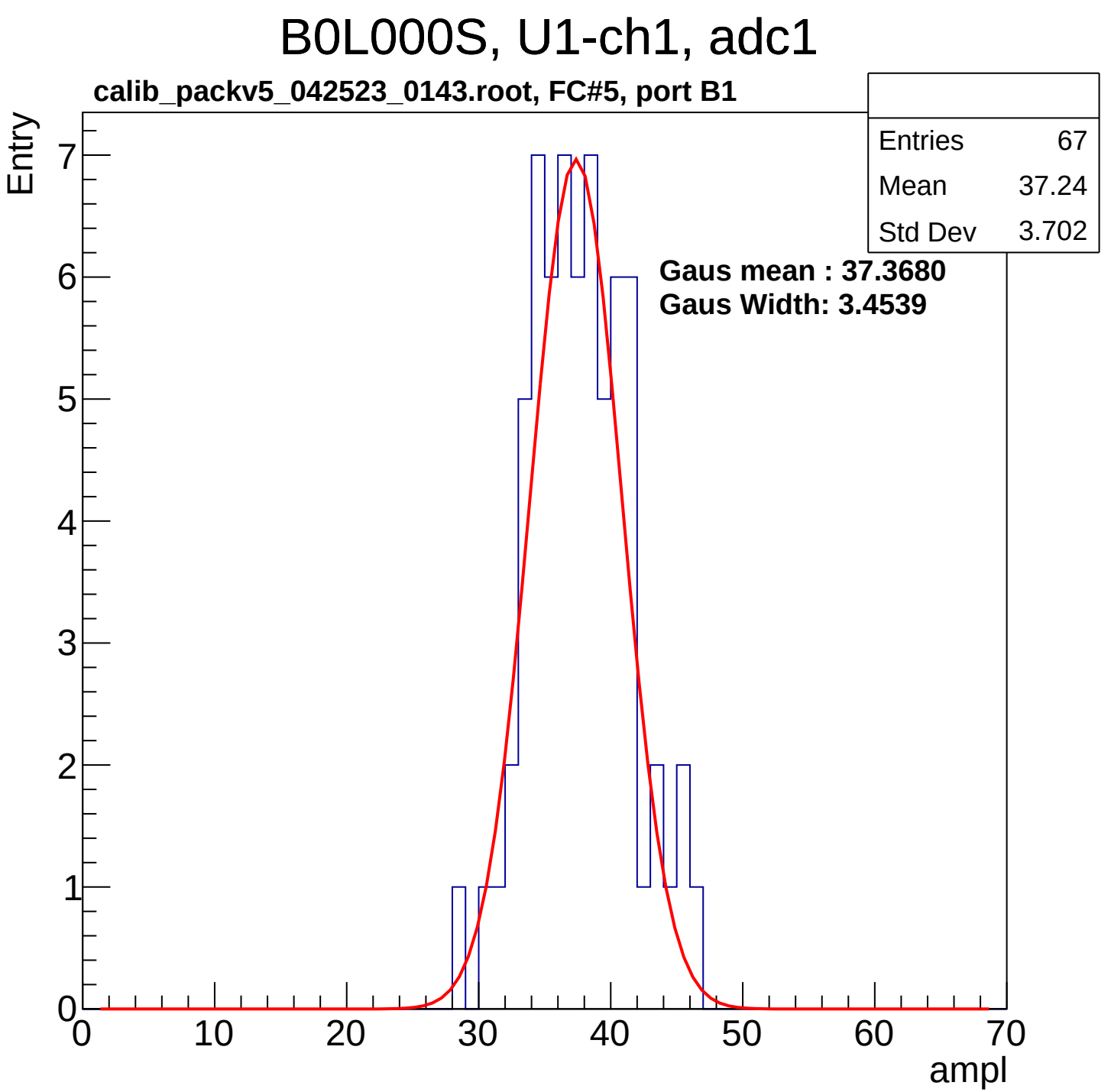
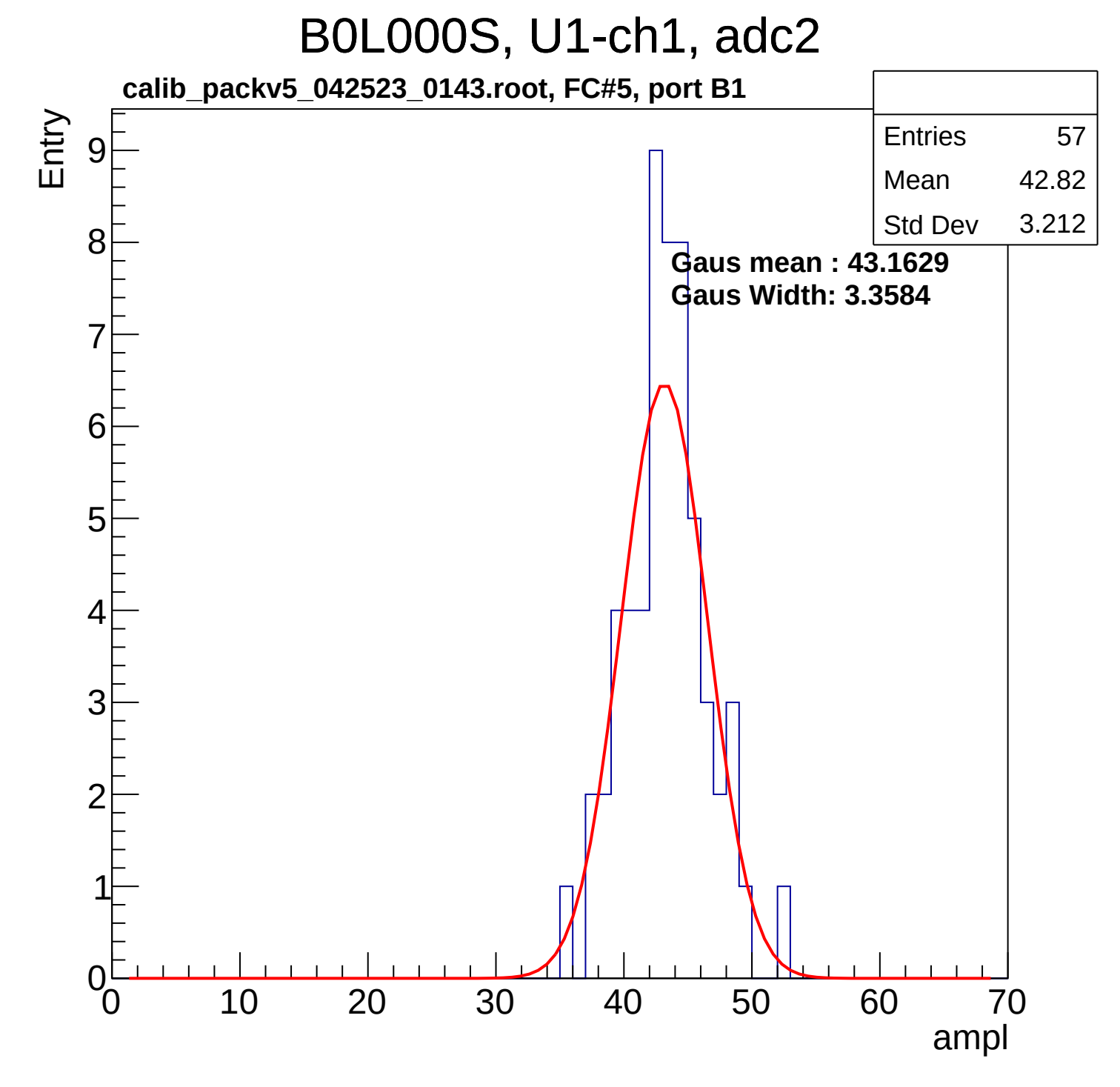
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch1, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 29.2
Std Dev 5.115
Gaus mean : 30.1522
Gaus Width: 4.0272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch1, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.24
Std Dev 3.702
Gaus mean : 37.3680
Gaus Width: 3.4539
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch1, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 42.82
Std Dev 3.212
Gaus mean : 43.1629
Gaus Width: 3.3584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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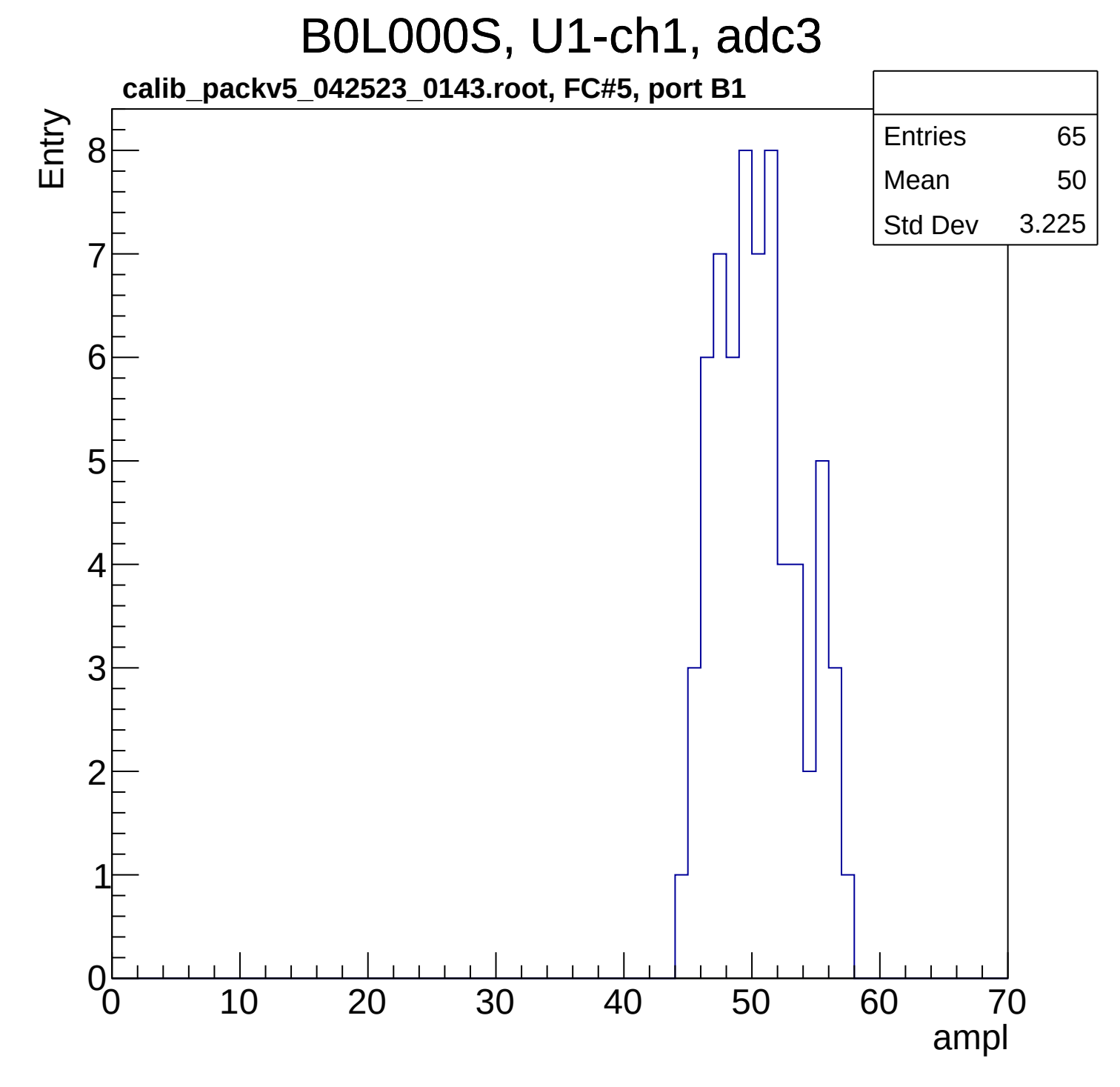
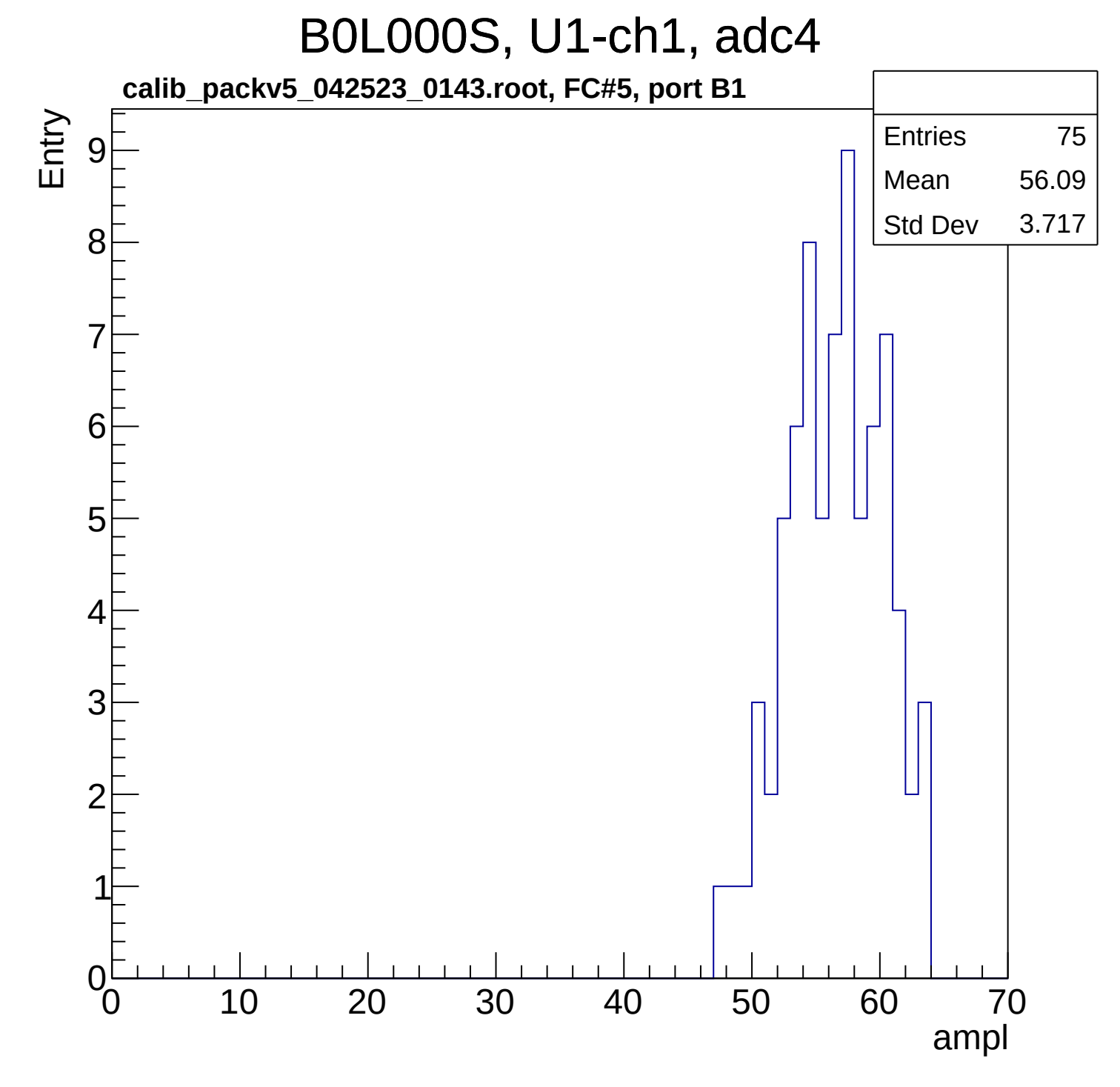

B0L000S, U1-ch1, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50
Std Dev 3.225
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch1, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 56.09
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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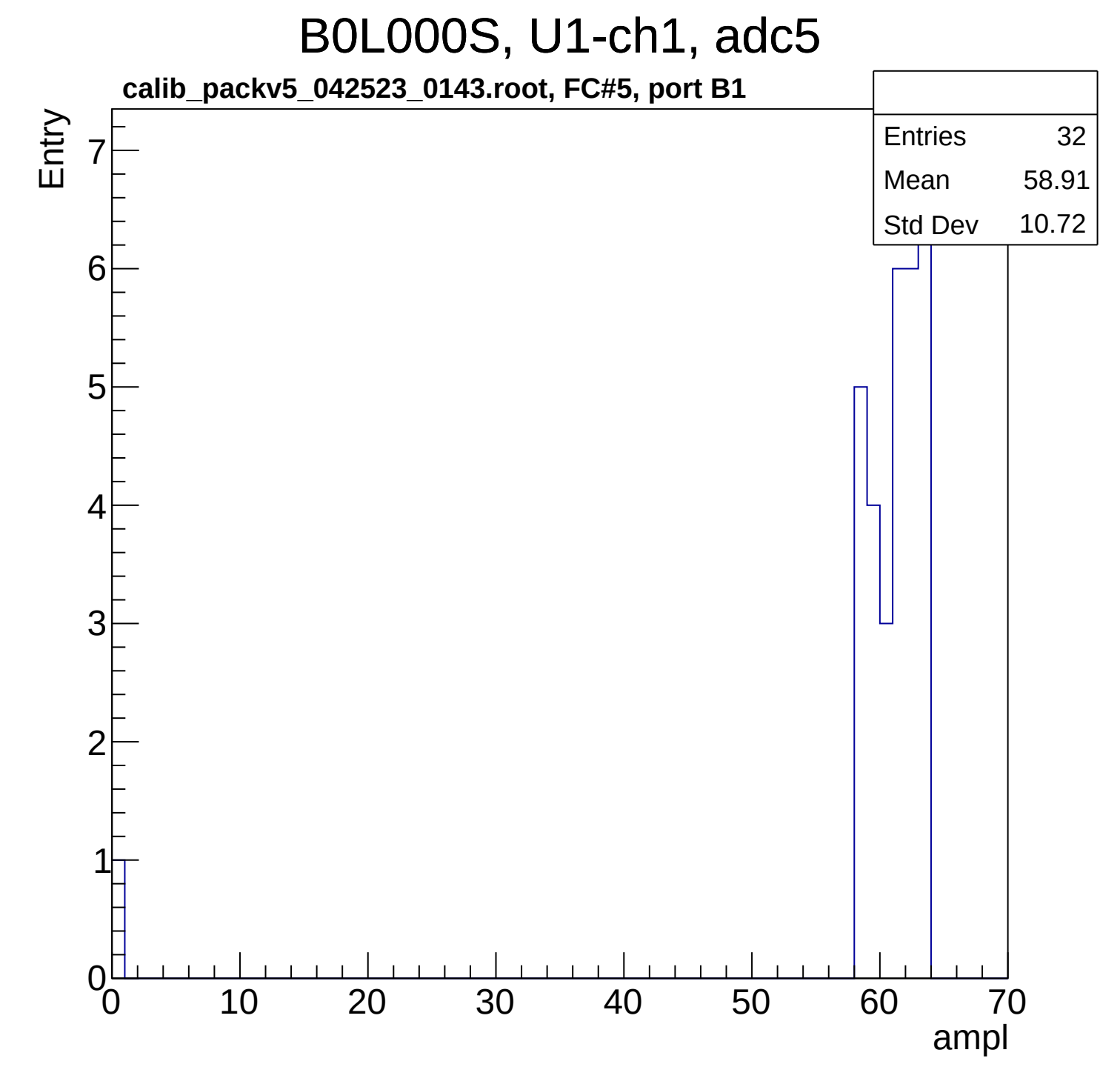
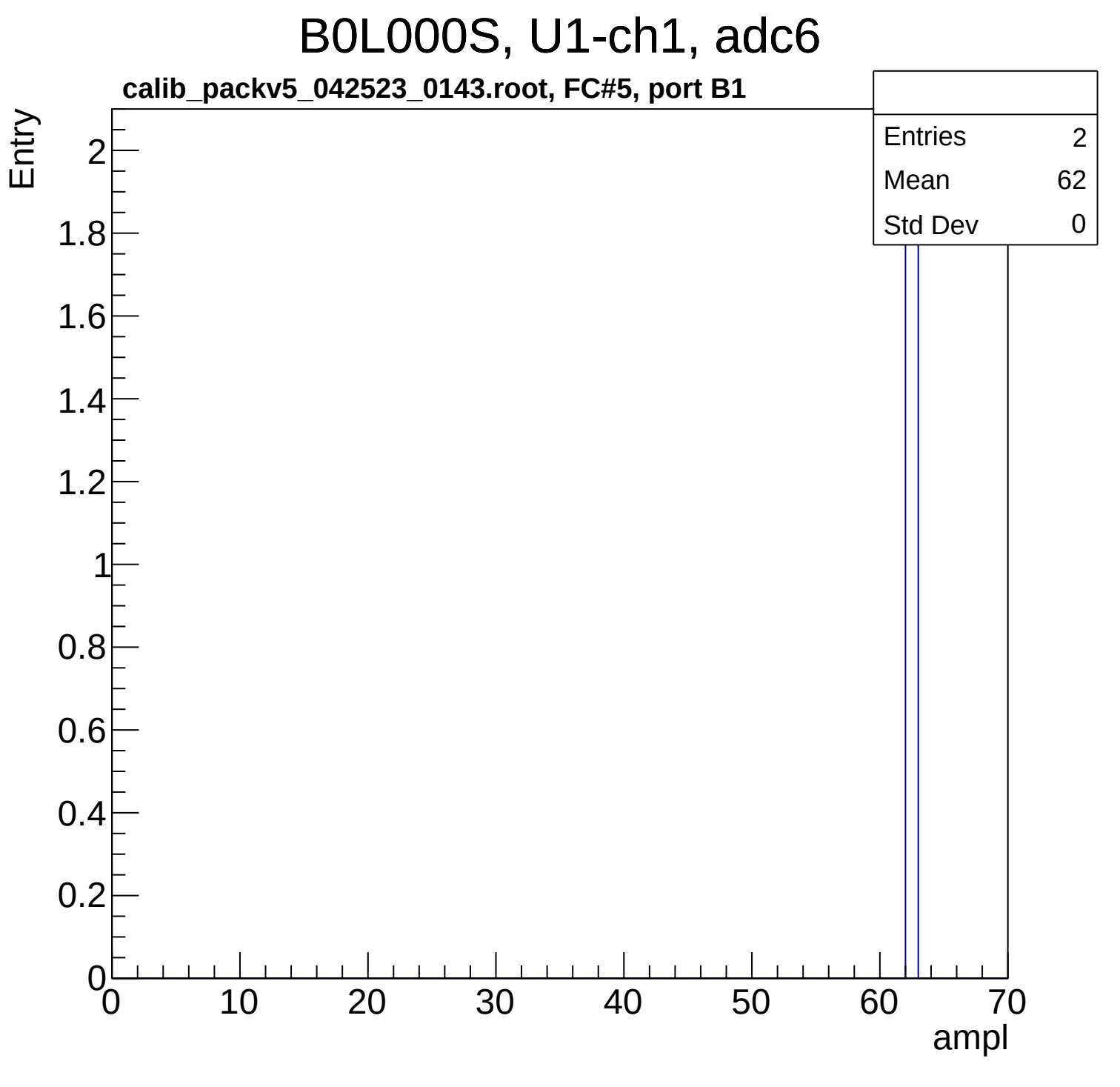
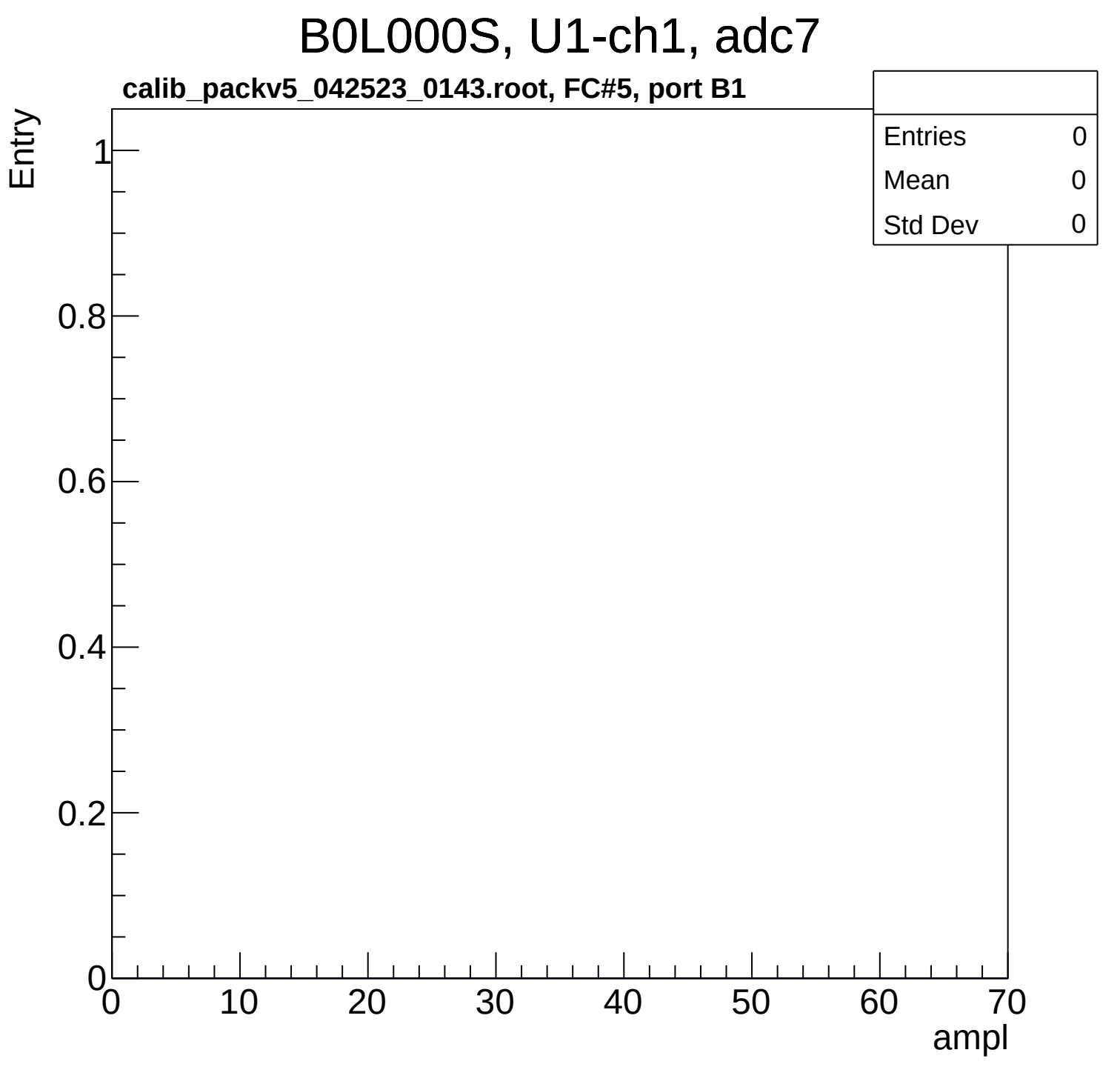
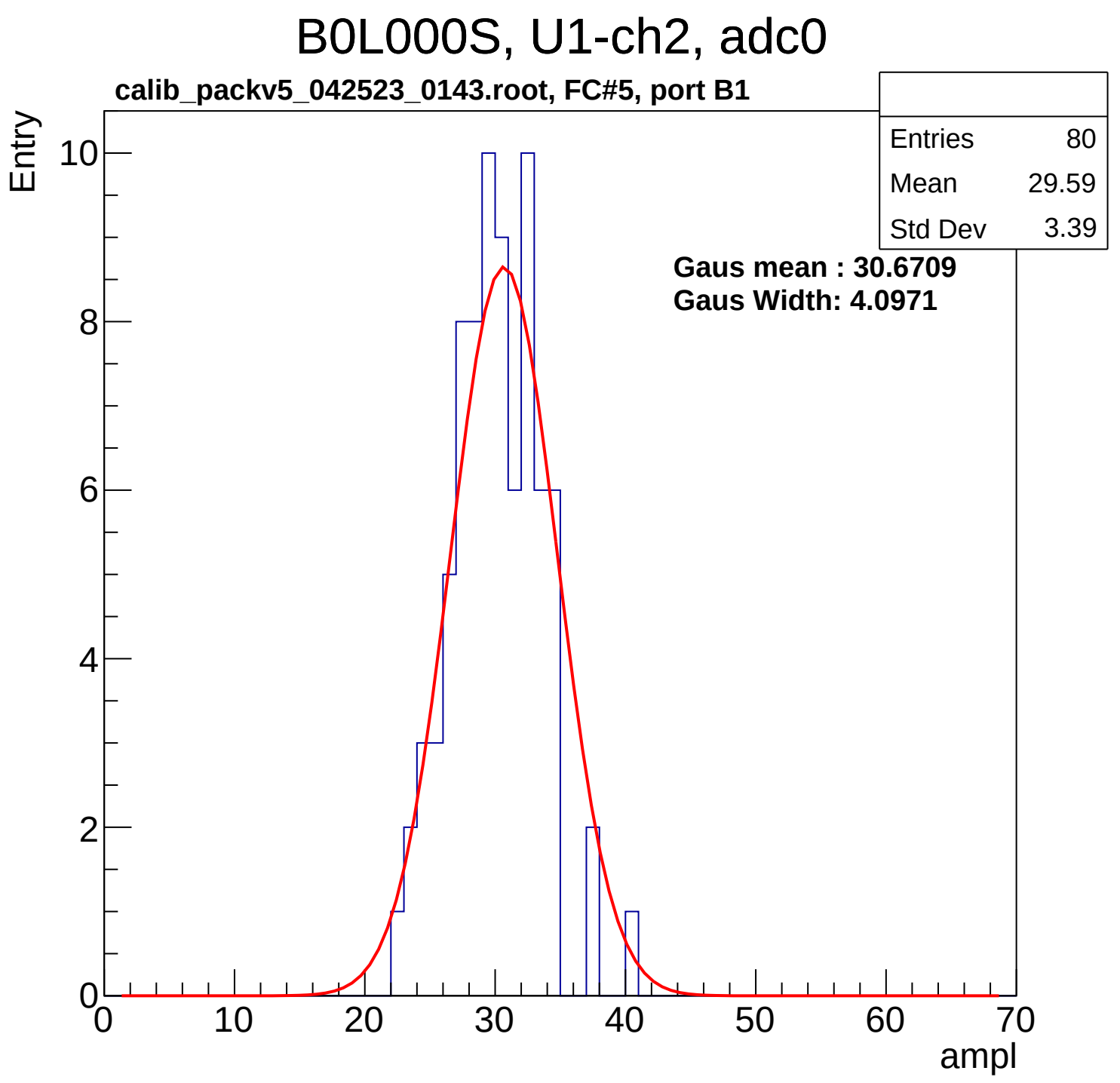
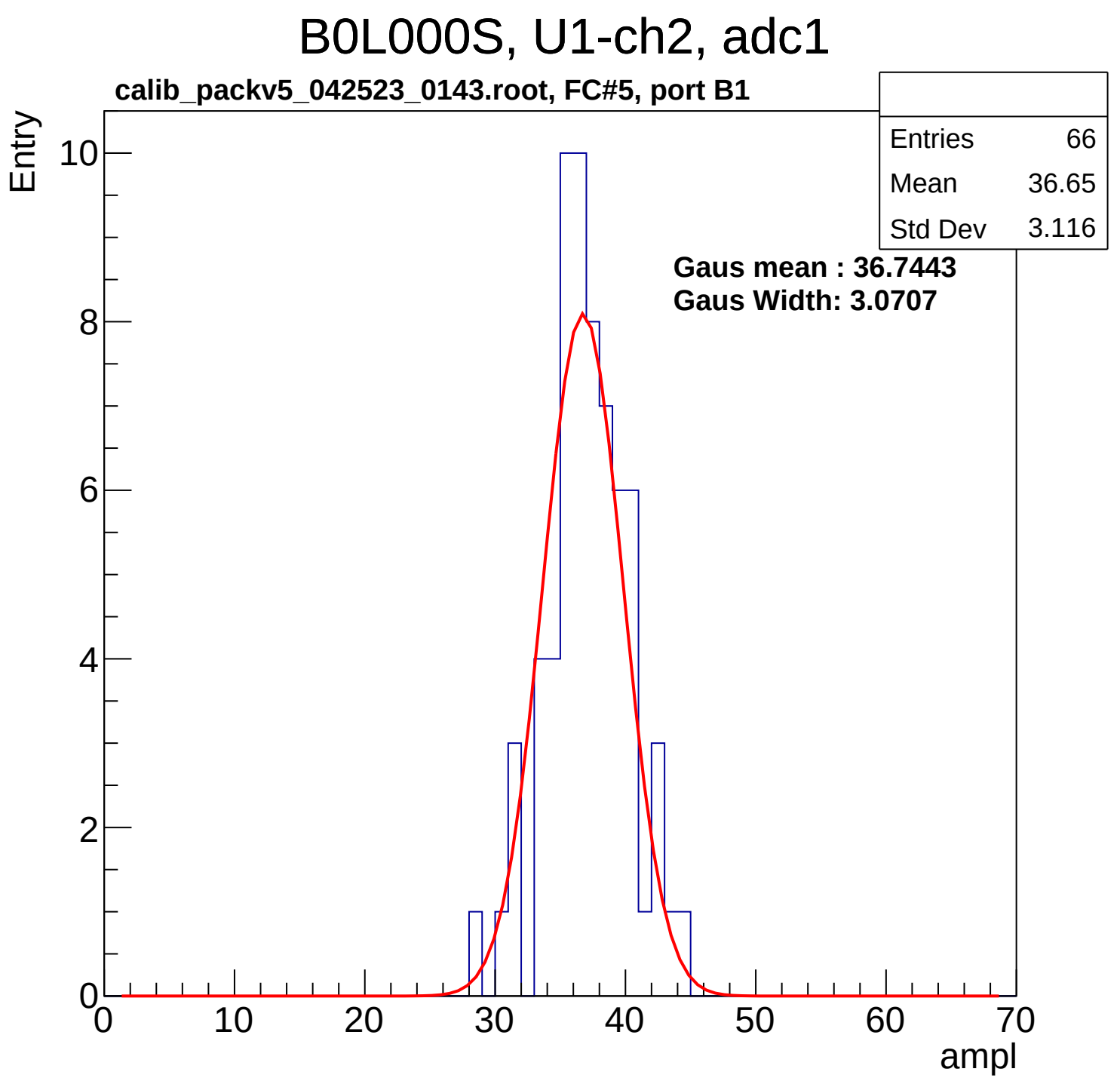
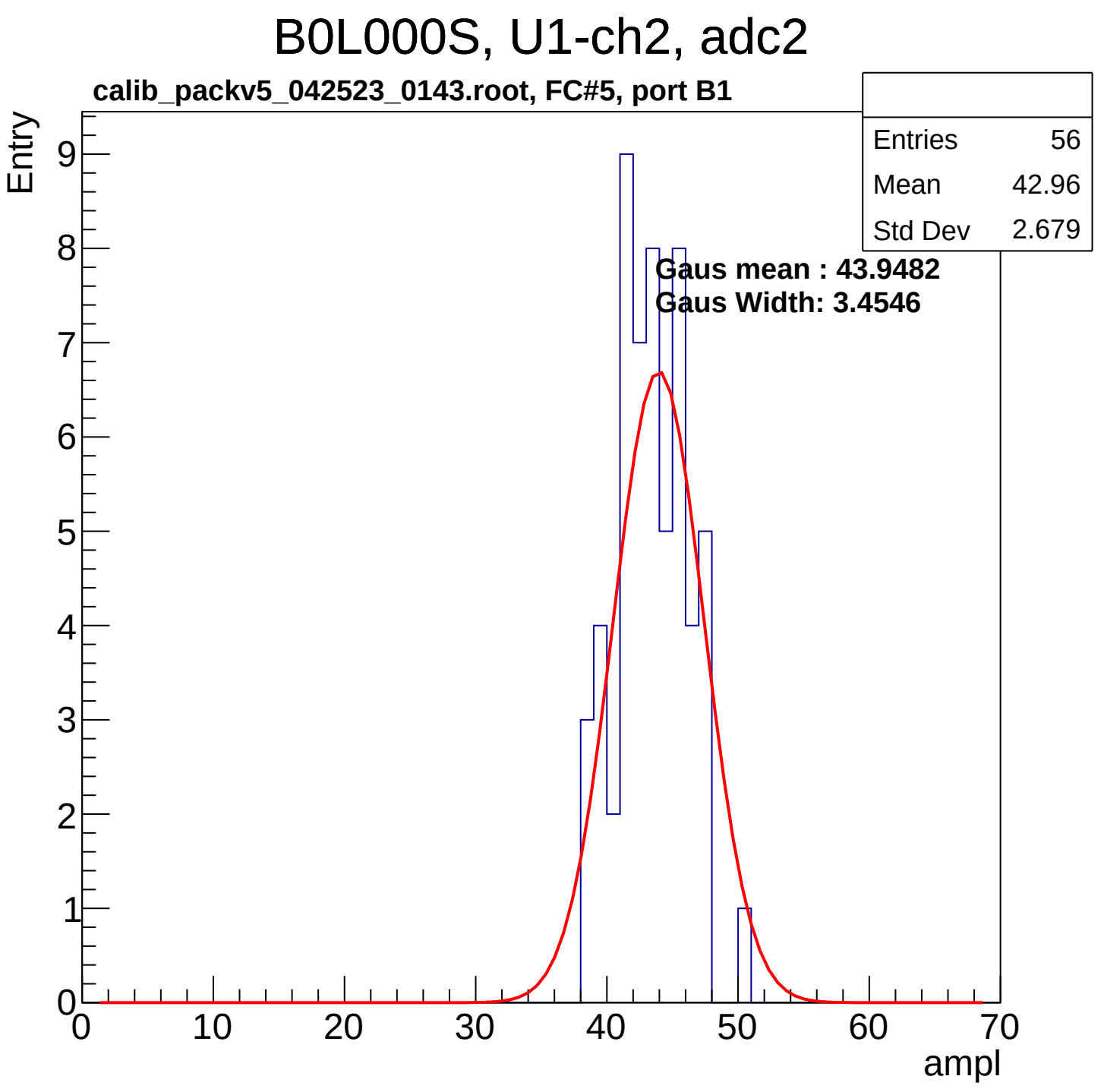

B0L000S, U1-ch1, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.91
Std Dev 10.72
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch1, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch1, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch2, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 29.59
Std Dev 3.39
Gaus mean : 30.6709
Gaus Width: 4.0971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch2, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.65
Std Dev 3.116
Gaus mean : 36.7443
Gaus Width: 3.0707
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch2, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.96
Std Dev 2.679
Gaus mean : 43.9482
Gaus Width: 3.4546
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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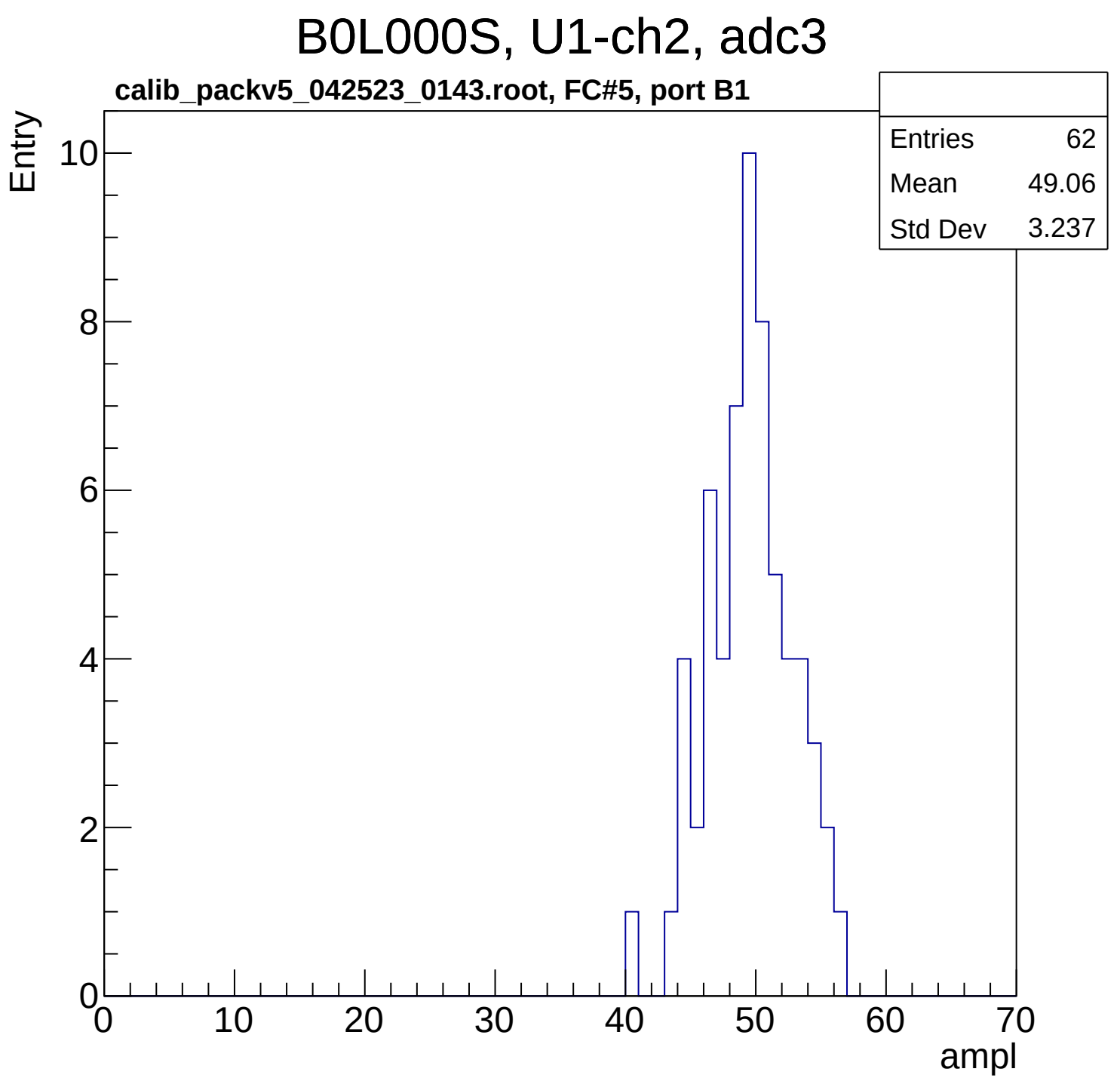
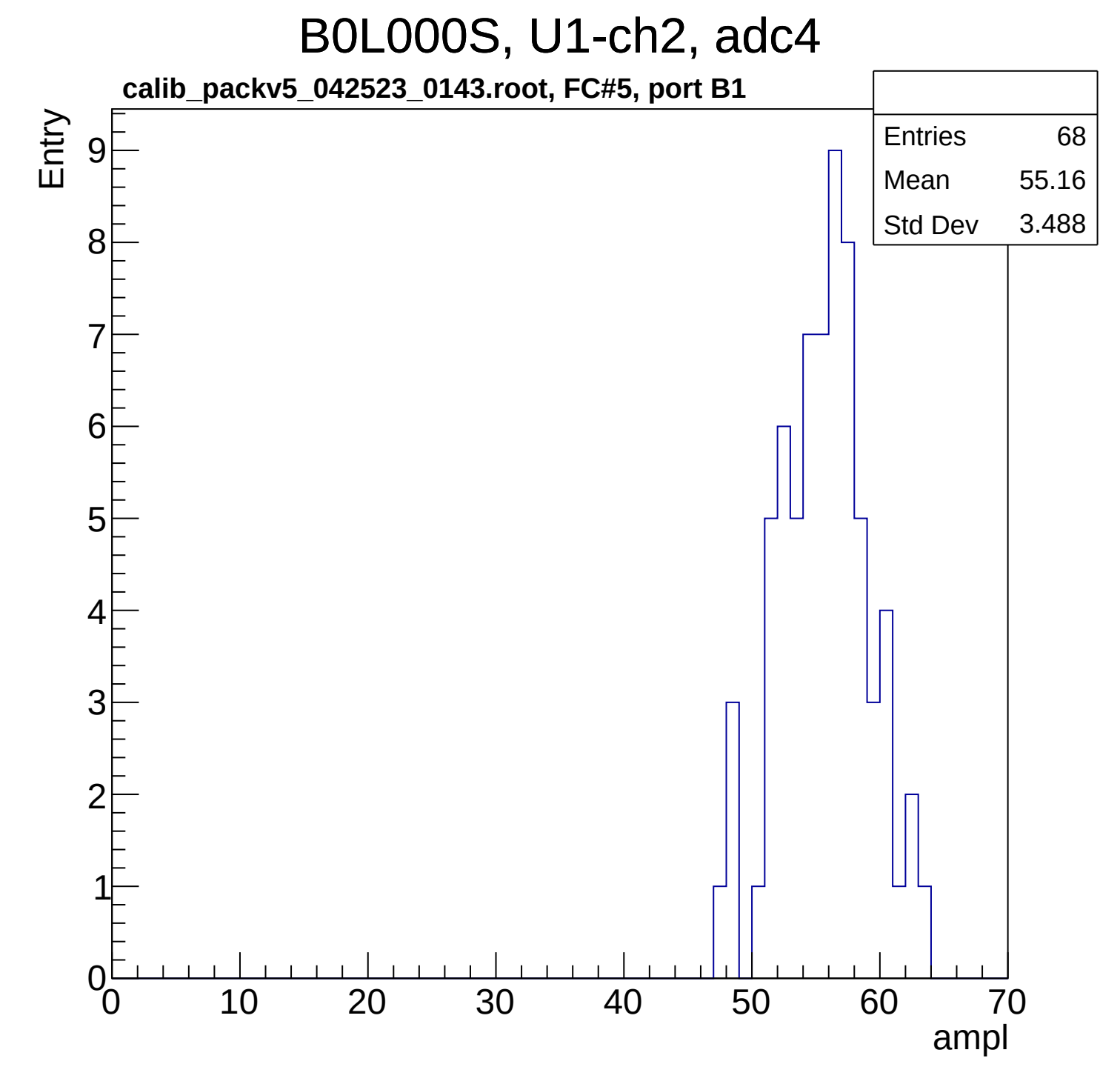

B0L000S, U1-ch2, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.06
Std Dev 3.237
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch2, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 55.16
Std Dev 3.488
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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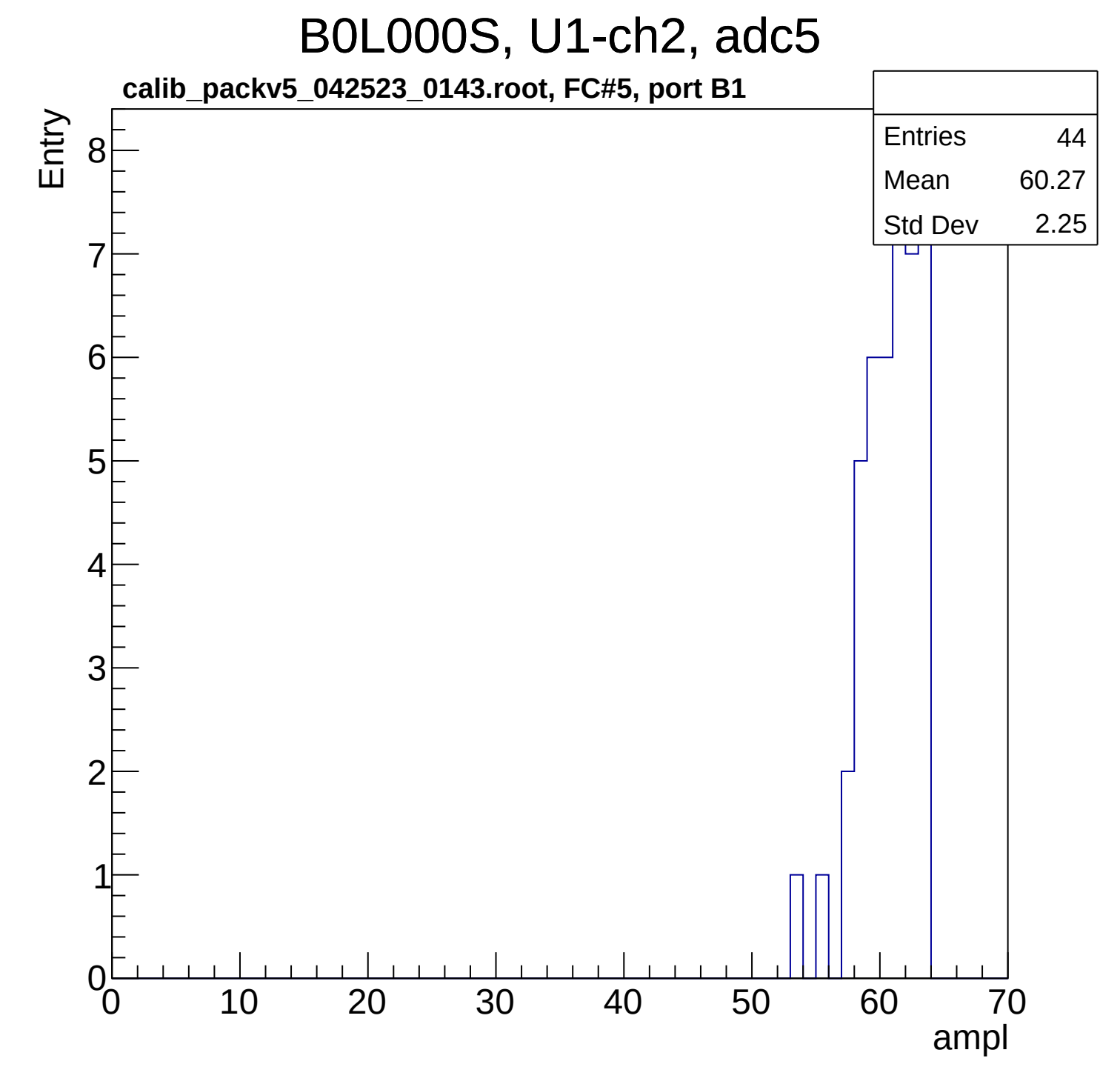
B0L000S, U1-ch2, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 60.27
Std Dev 2.25
Entry
ampl
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1
2
3
4
5
6
7
8
0
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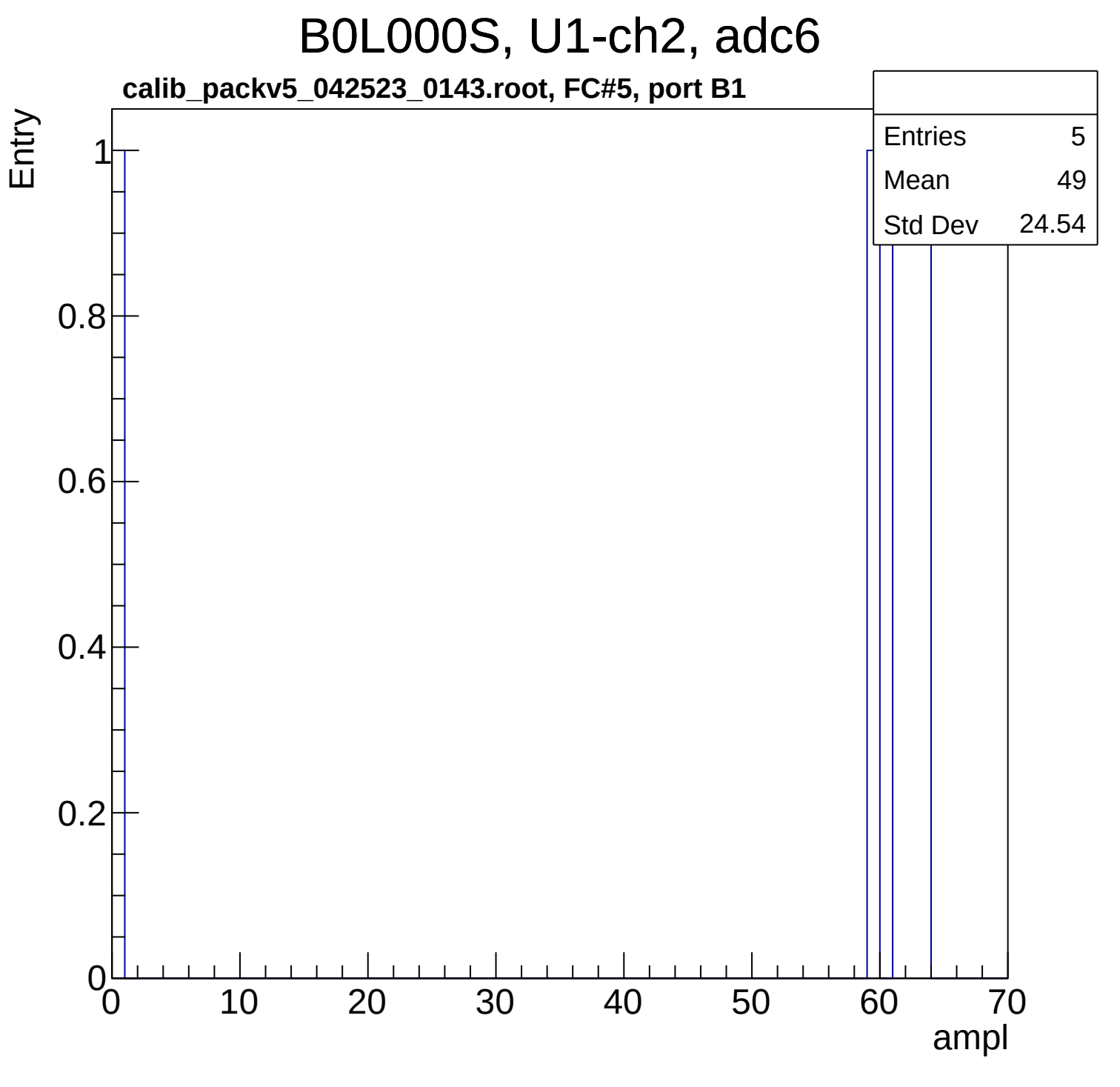
20
30
40
50
60
70


B0L000S, U1-ch2, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49
Std Dev 24.54
Entry
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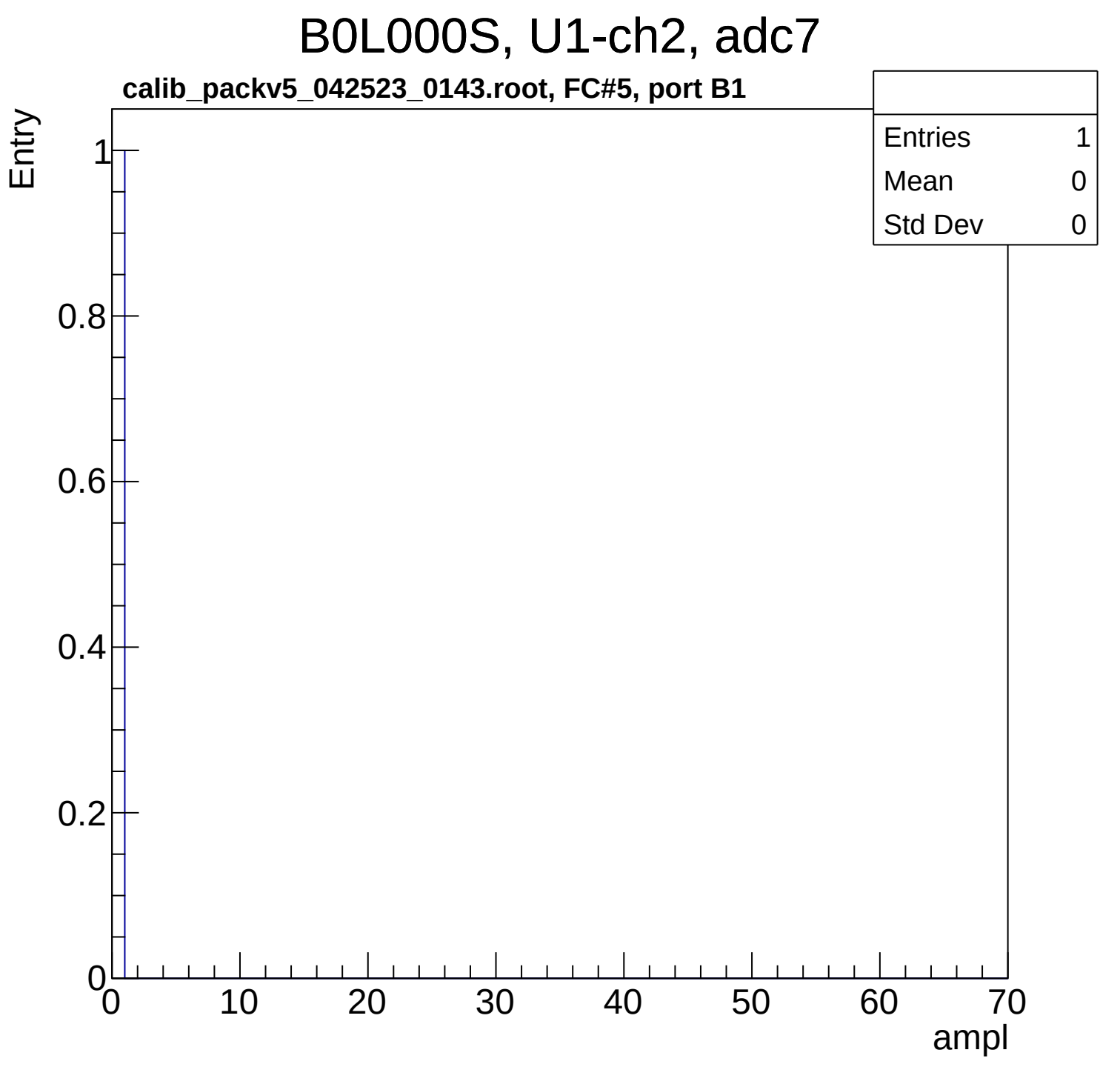
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch2, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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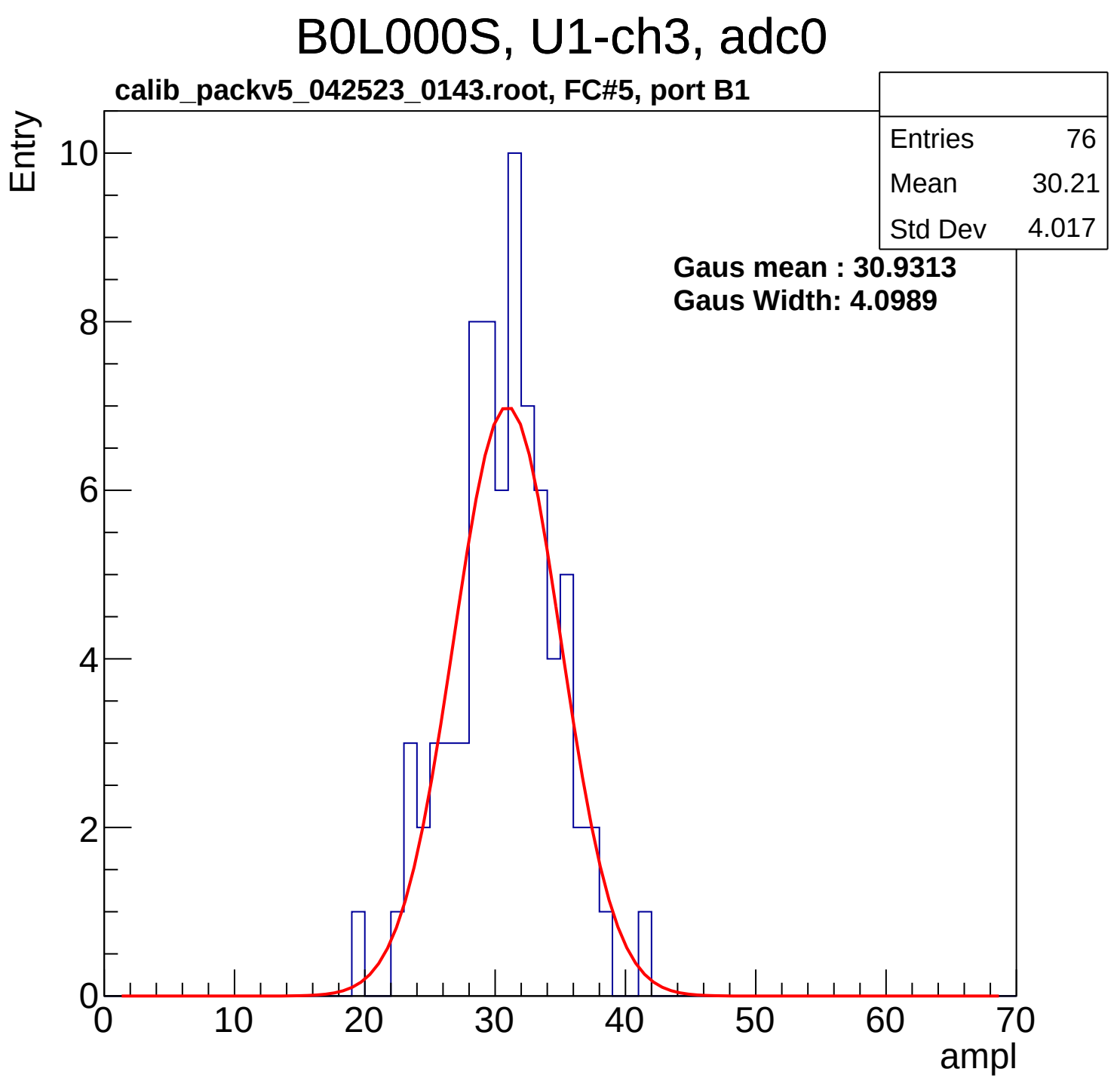
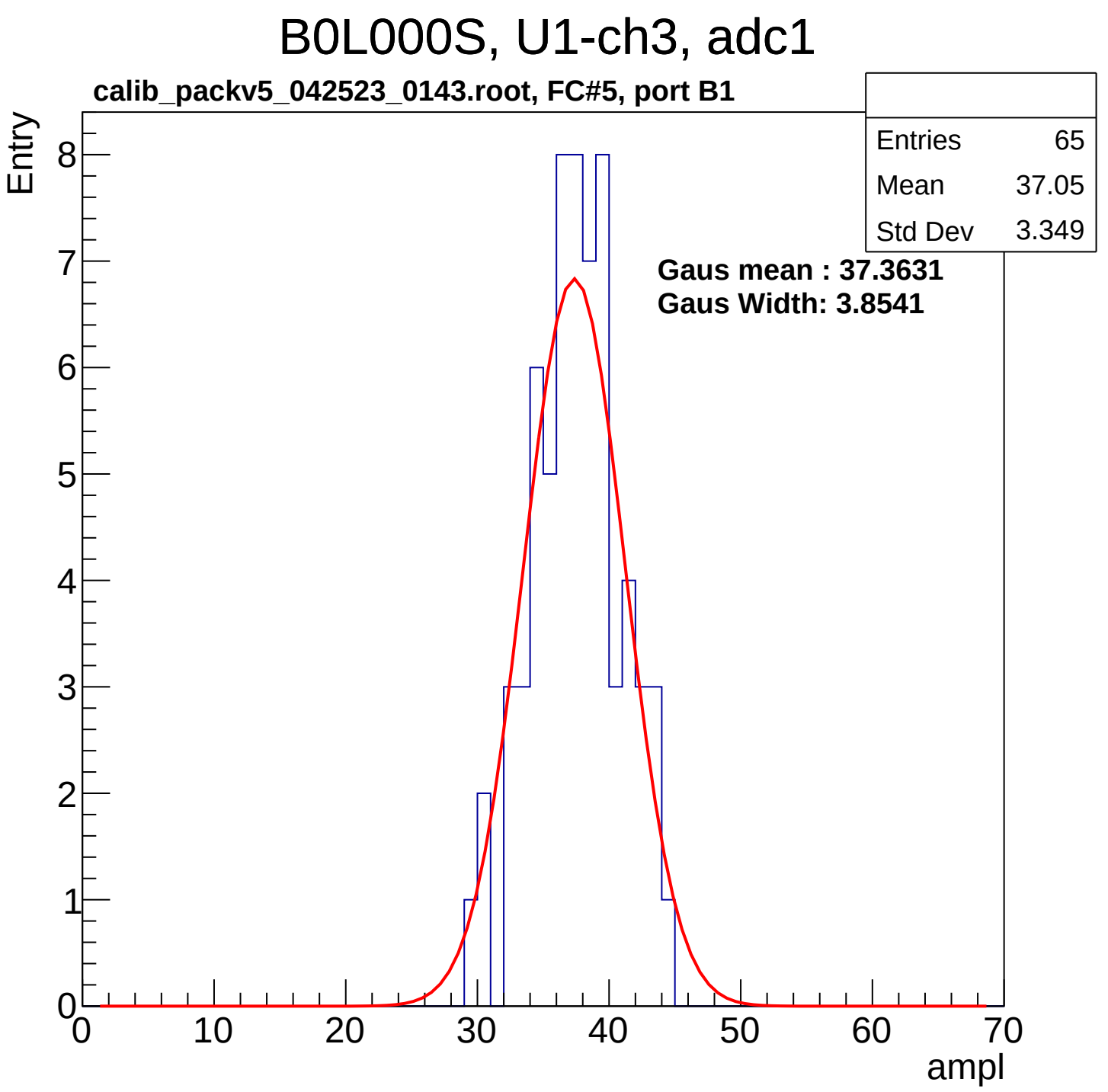
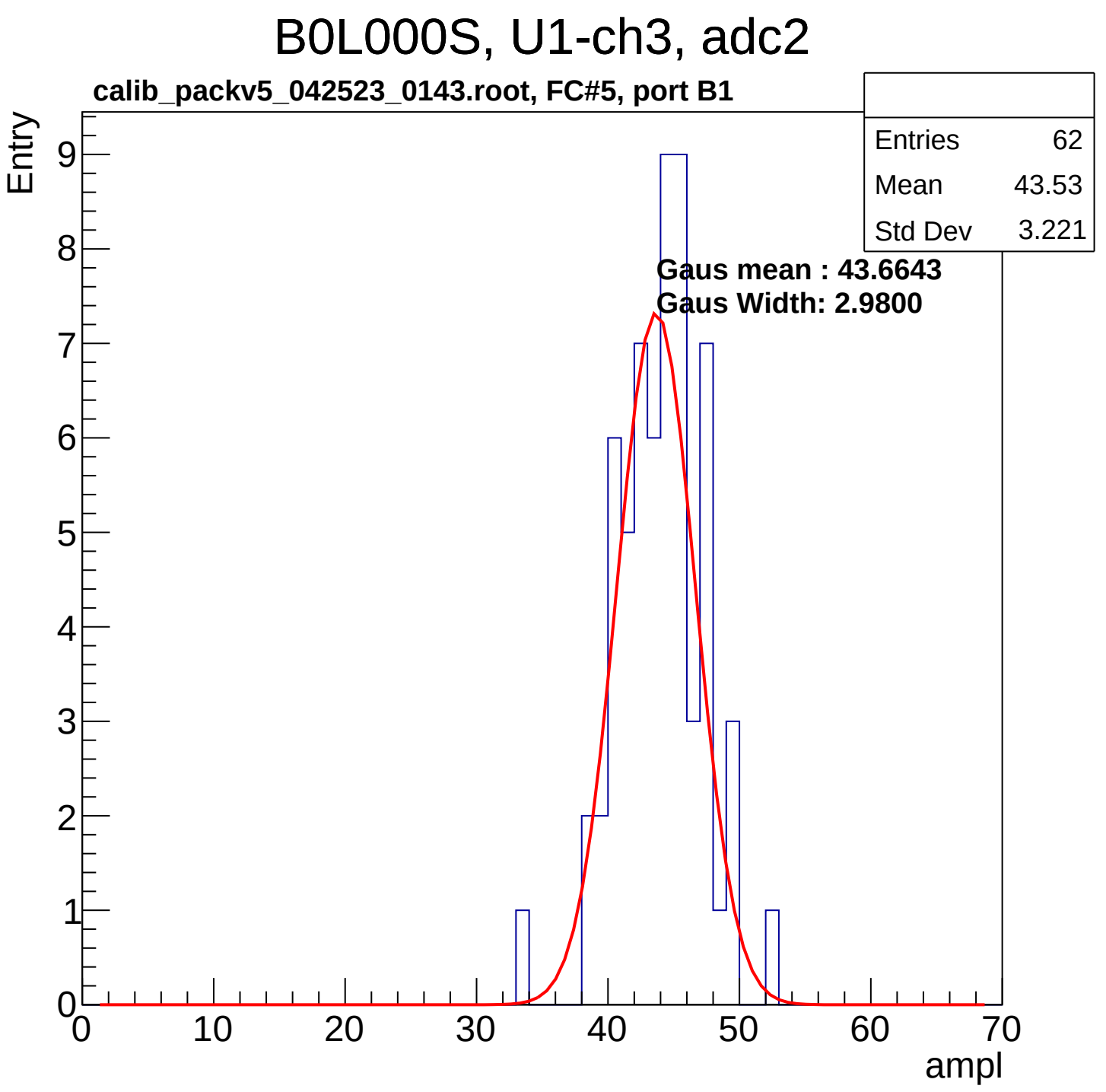
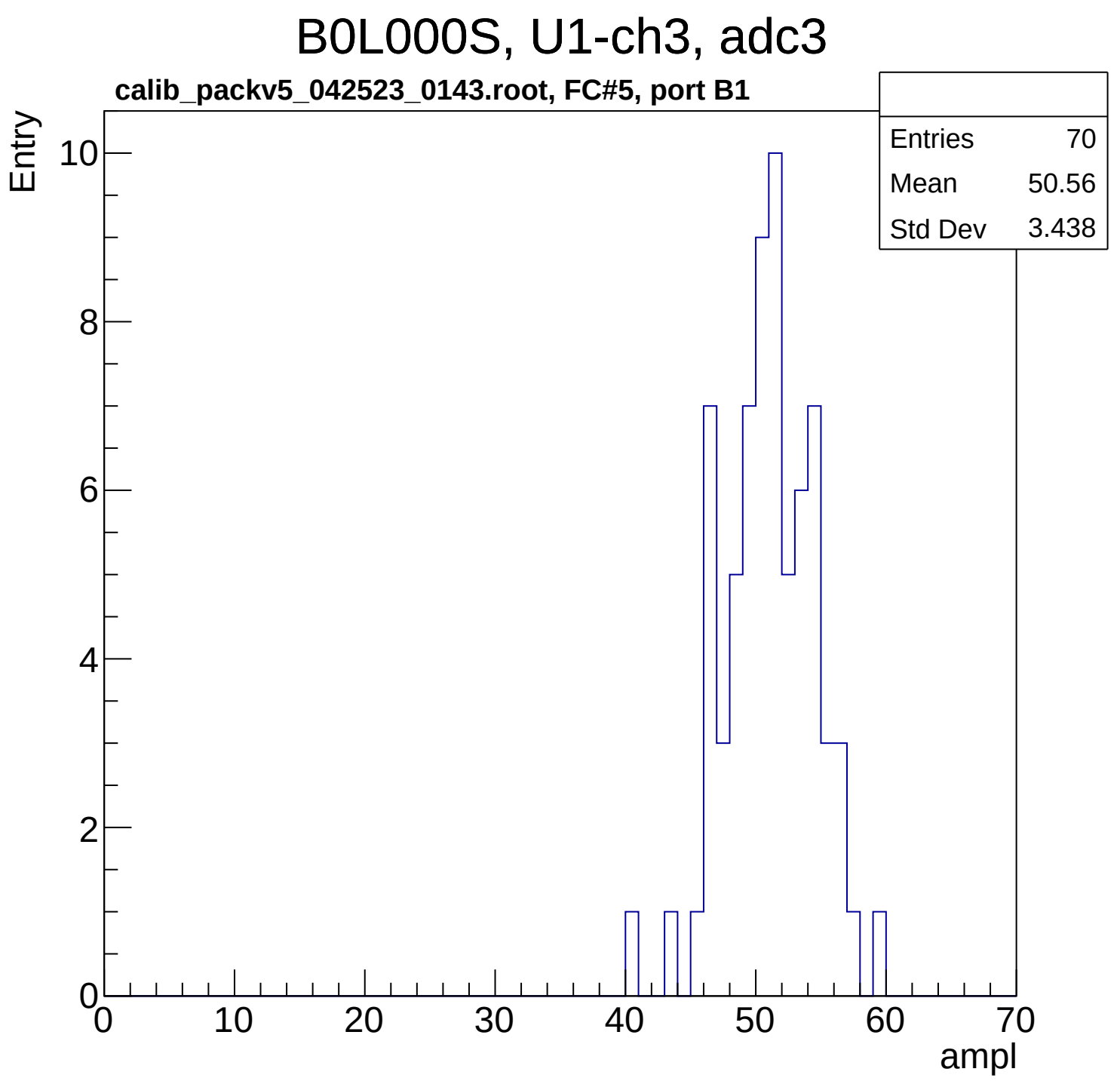
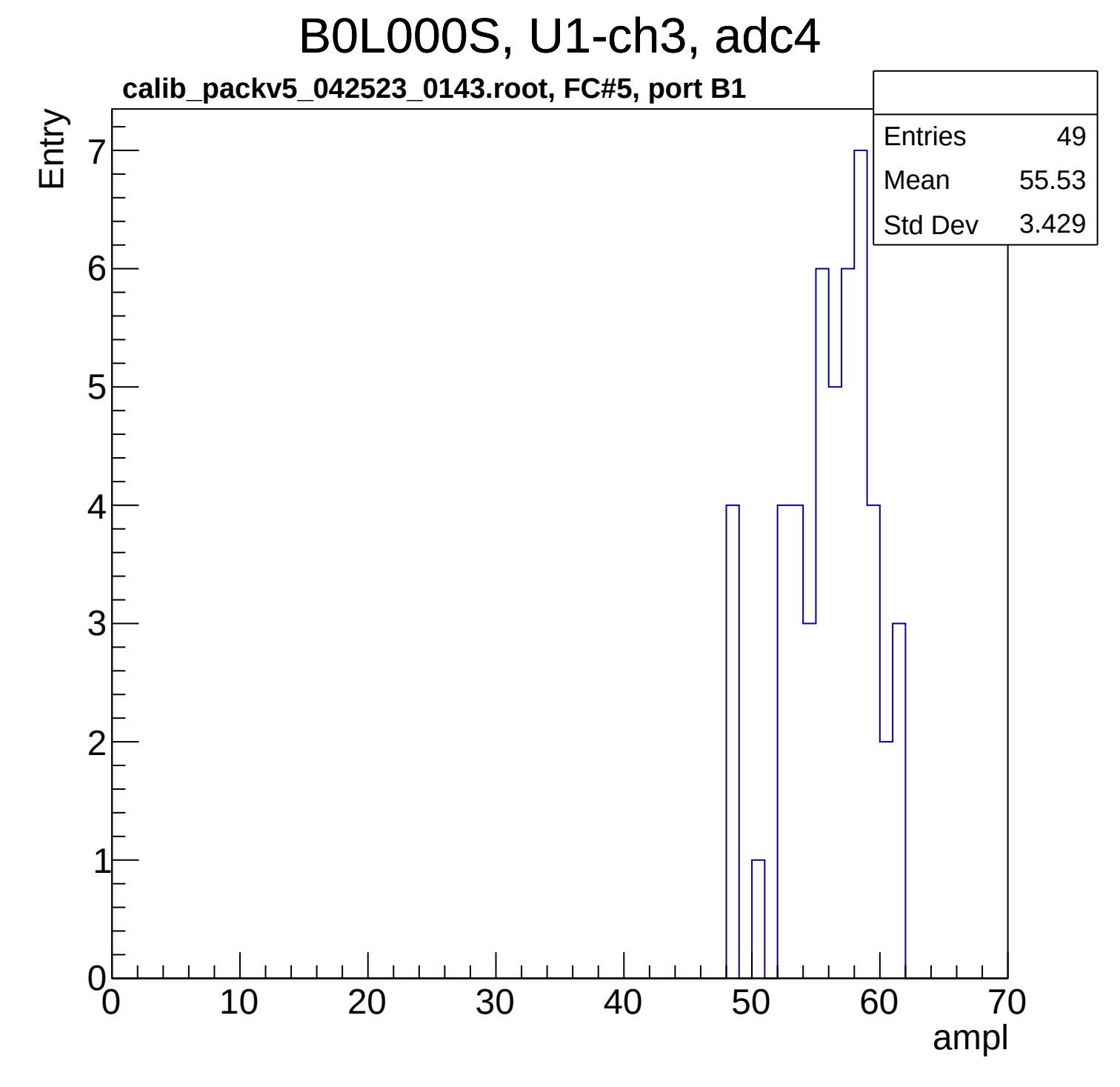
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch3, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 30.21
Std Dev 4.017
Gaus mean : 30.9313
Gaus Width: 4.0989
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch3, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 37.05
Std Dev 3.349
Gaus mean : 37.3631
Gaus Width: 3.8541
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch3, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.53
Std Dev 3.221
Gaus mean : 43.6643
Gaus Width: 2.9800
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch3, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 50.56
Std Dev 3.438
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch3, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.53
Std Dev 3.429
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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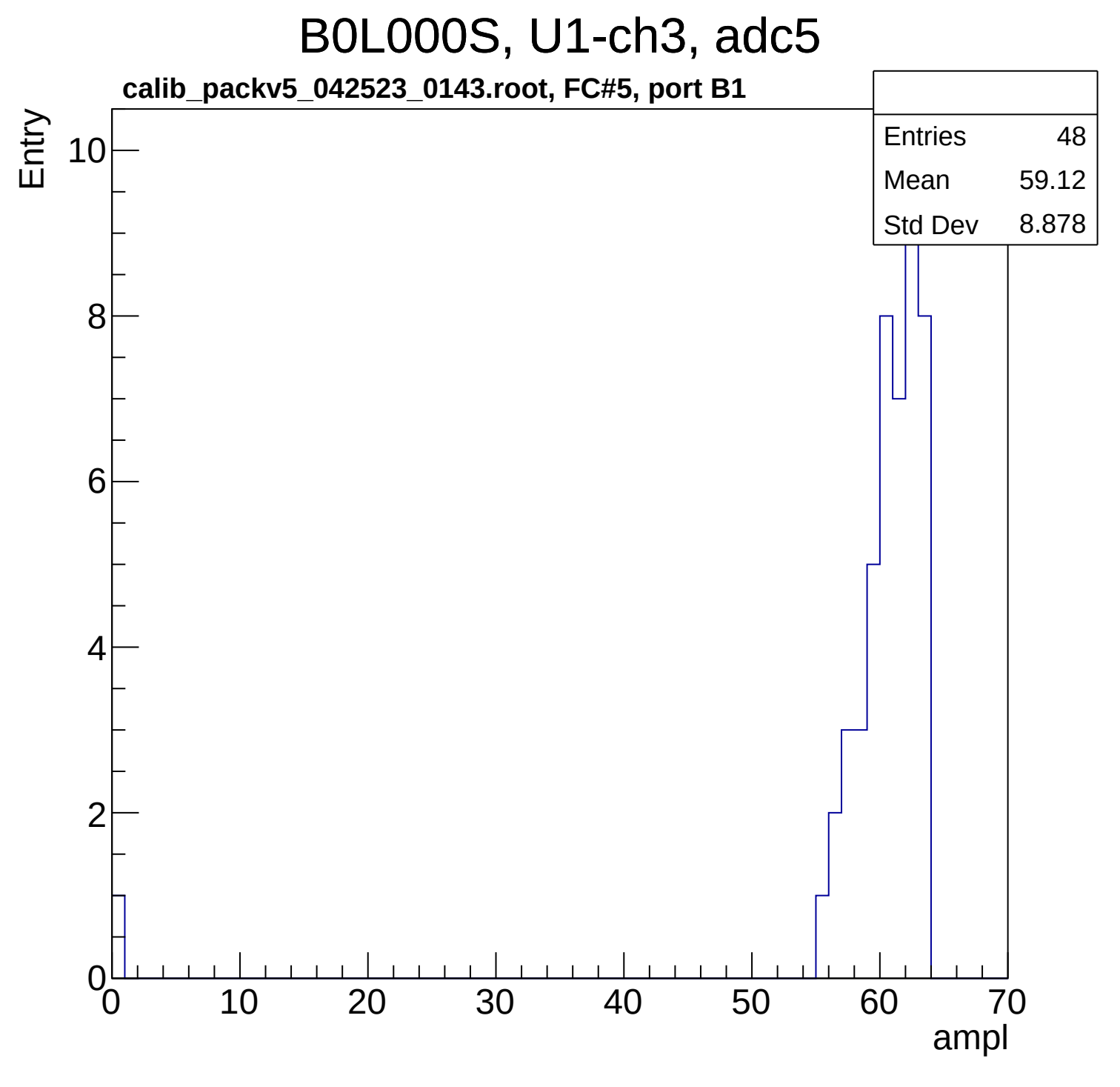
B0L000S, U1-ch3, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.12
Std Dev 8.878
Entry
ampl
0
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4
6
8
10
0
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20
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40
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60
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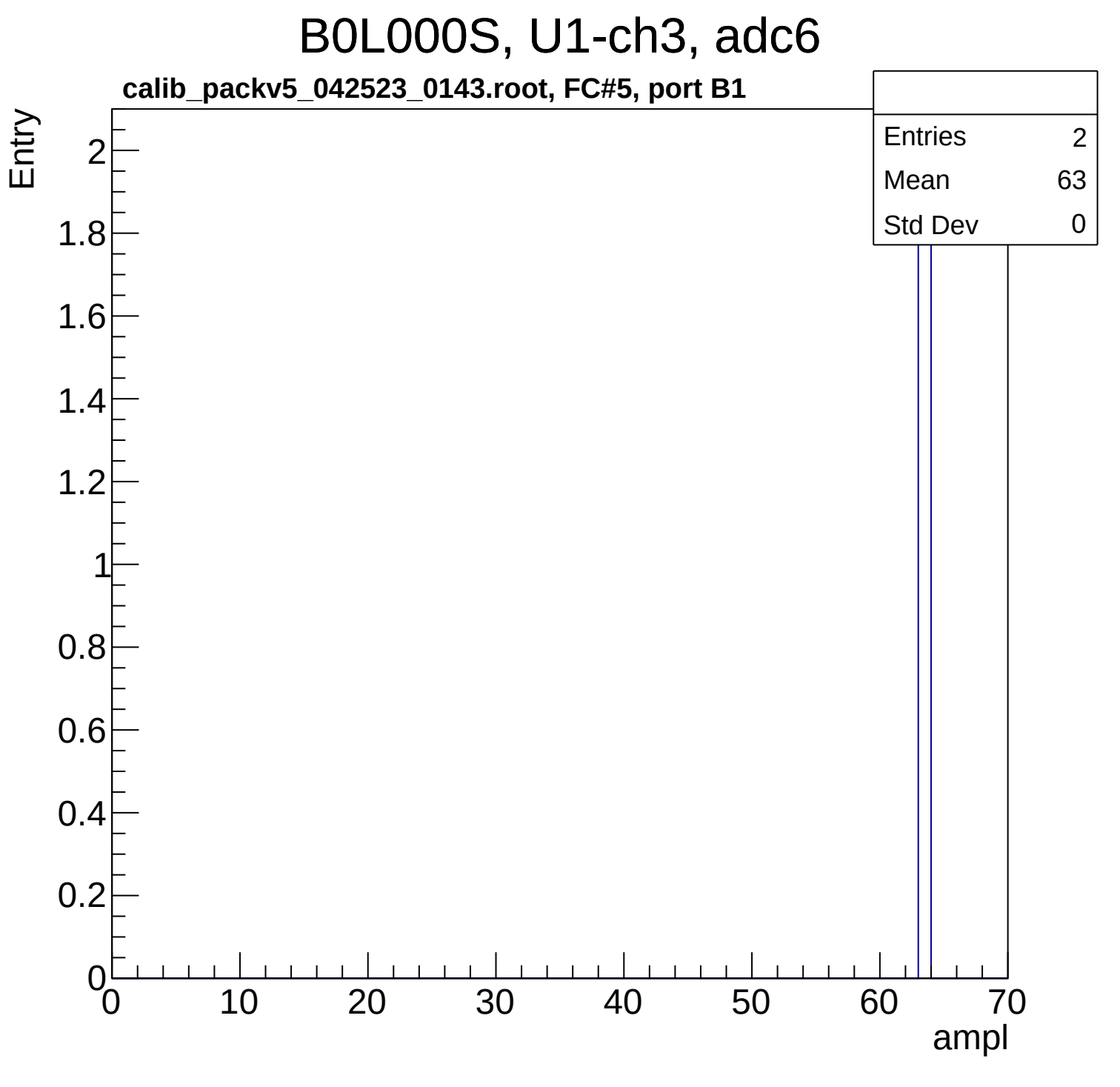
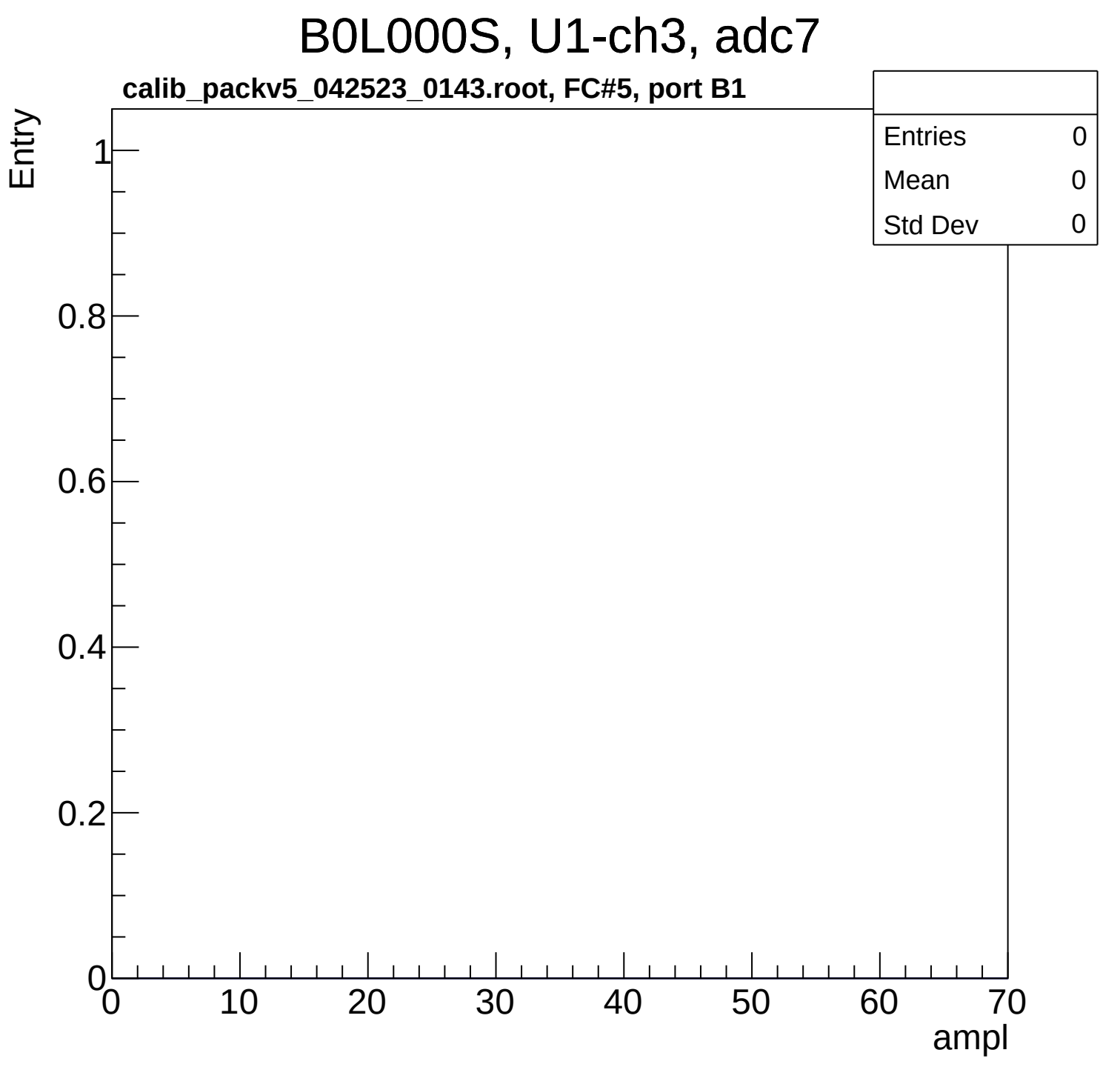

B0L000S, U1-ch3, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch3, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
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10
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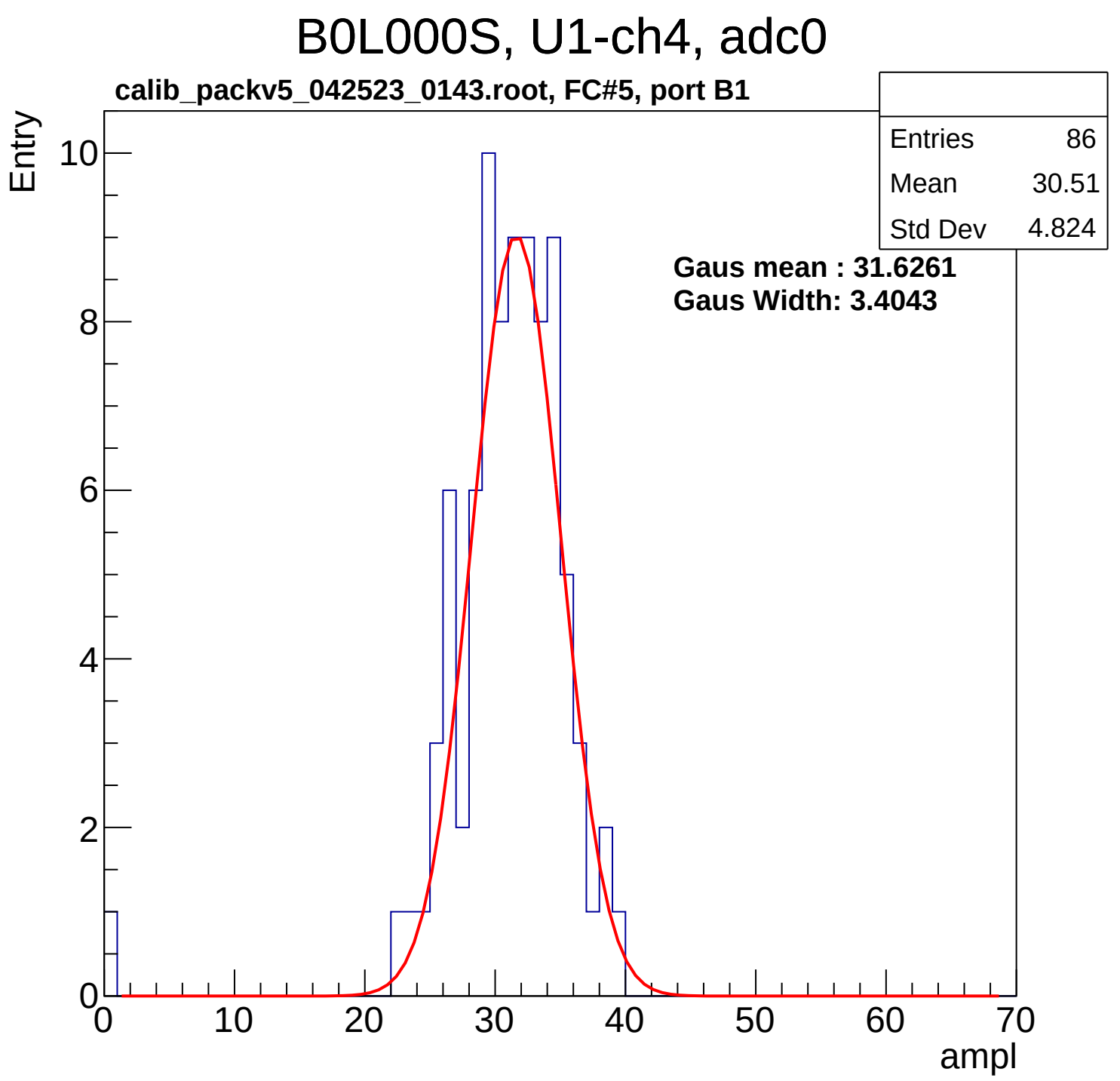
ampl


B0L000S, U1-ch4, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 30.51
Std Dev 4.824
Gaus mean : 31.6261
Gaus Width: 3.4043
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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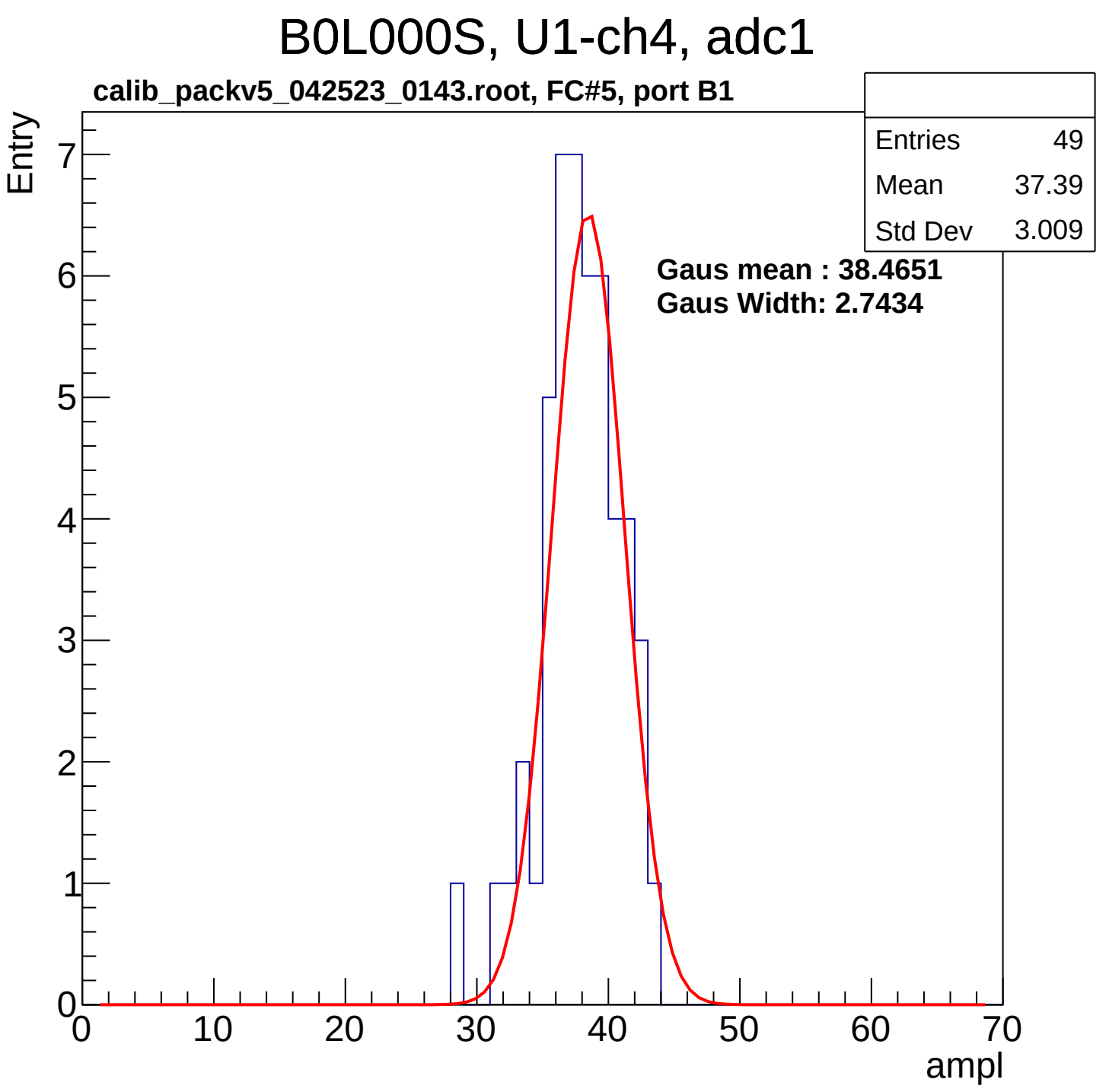
B0L000S, U1-ch4, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 37.39
Std Dev 3.009
Gaus mean : 38.4651
Gaus Width: 2.7434
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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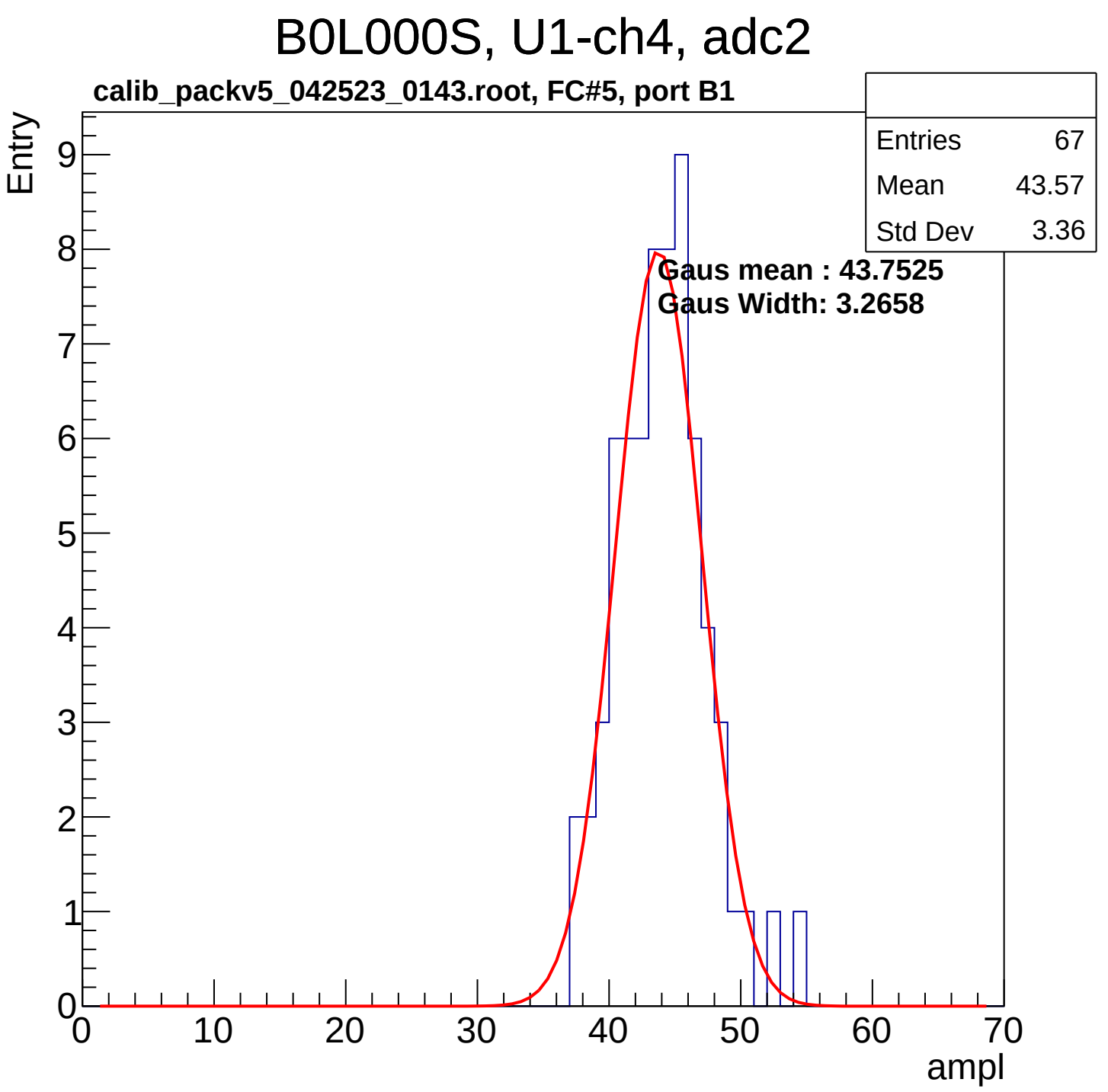
B0L000S, U1-ch4, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.57
Std Dev 3.36
Gaus mean : 43.7525
Gaus Width: 3.2658
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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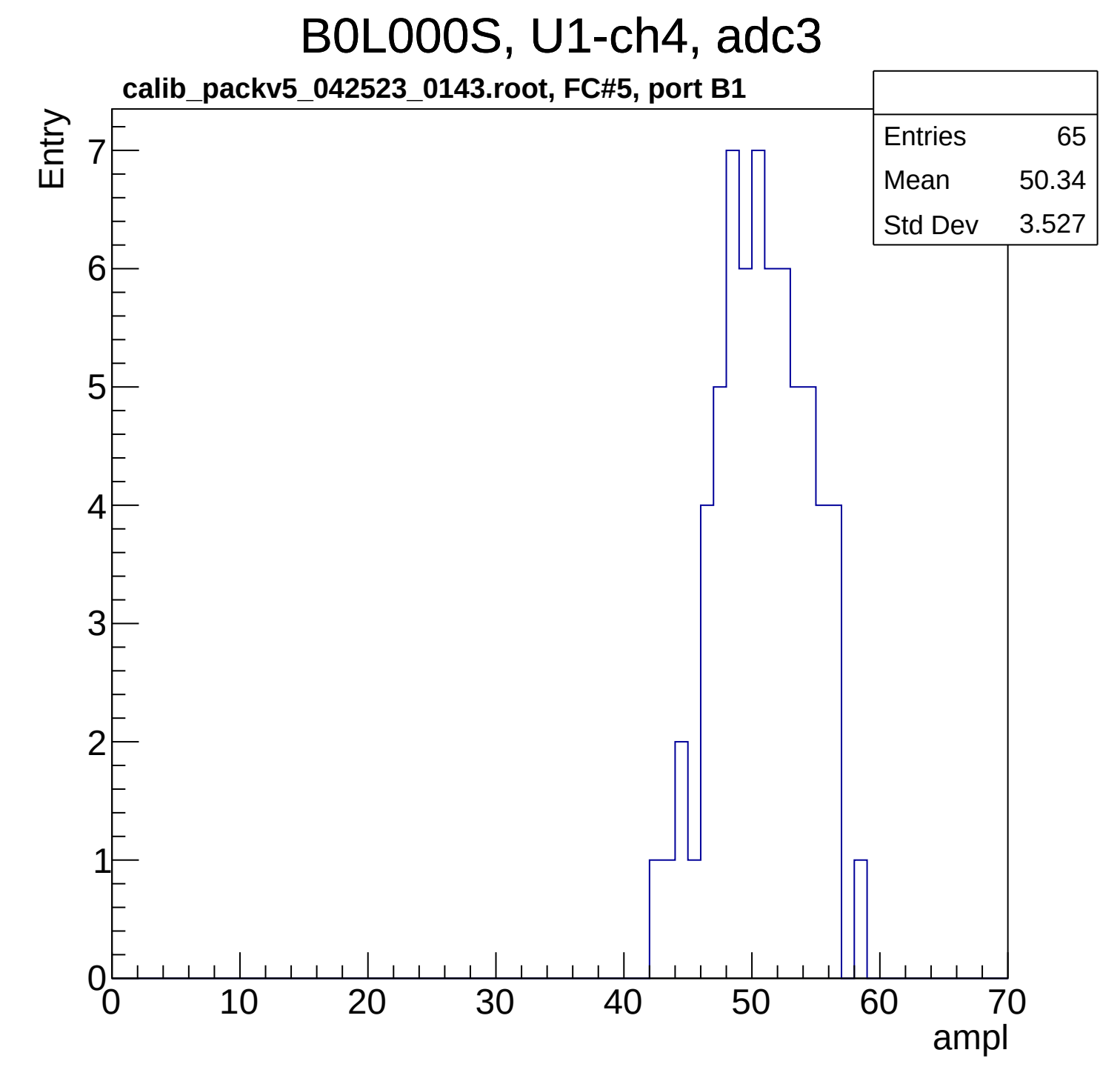
B0L000S, U1-ch4, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.34
Std Dev 3.527
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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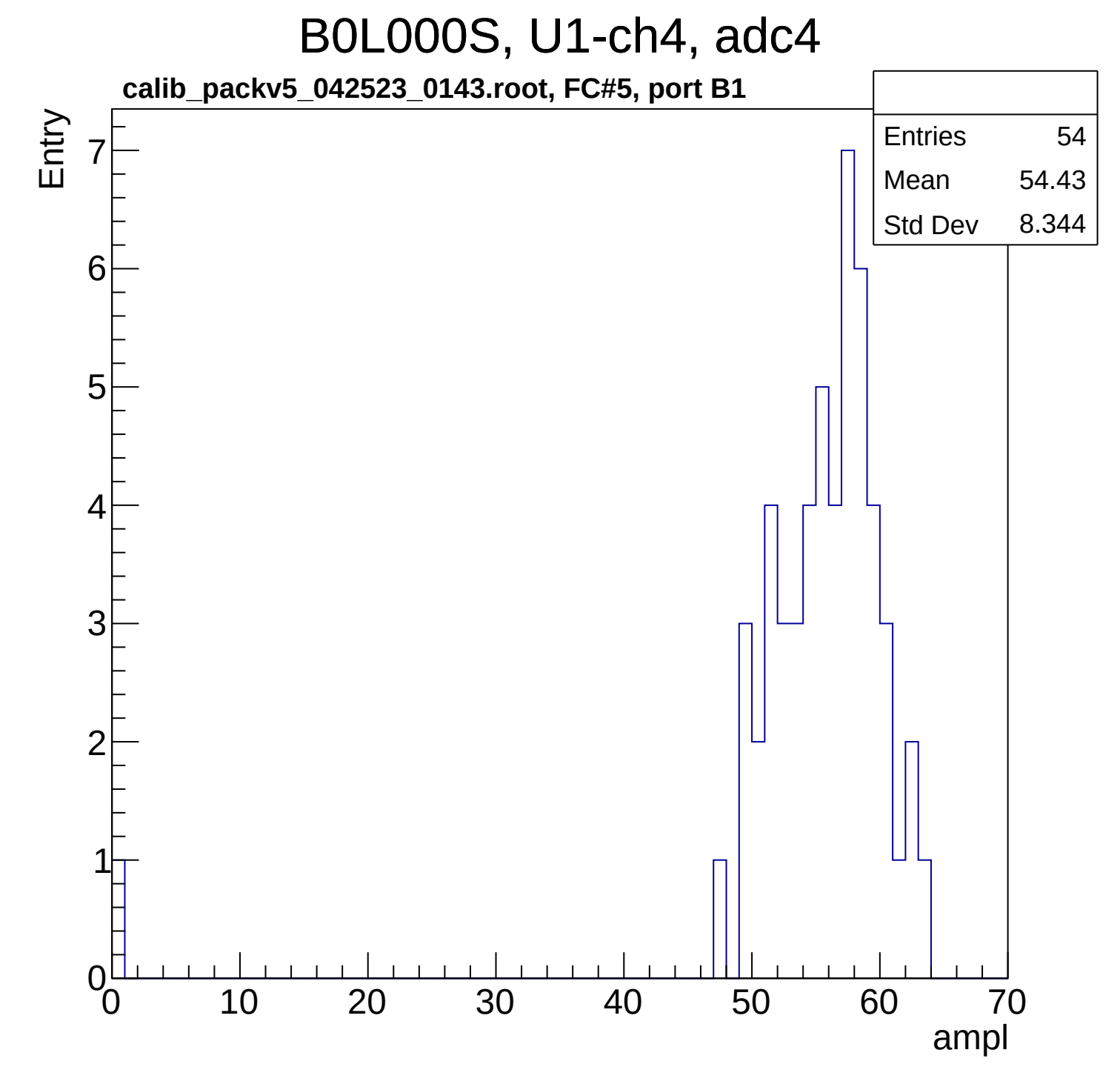
B0L000S, U1-ch4, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.43
Std Dev 8.344
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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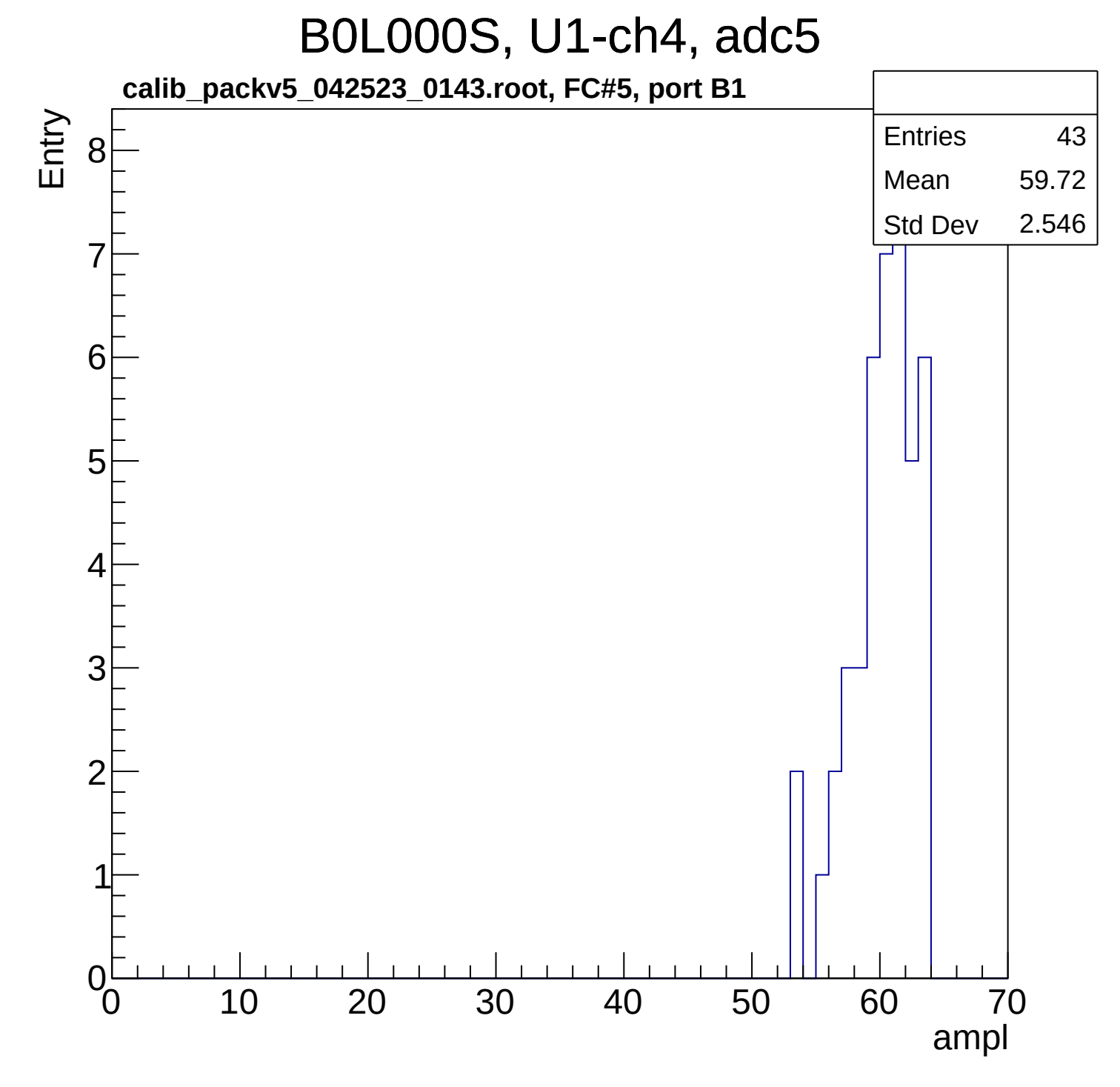
B0L000S, U1-ch4, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.72
Std Dev 2.546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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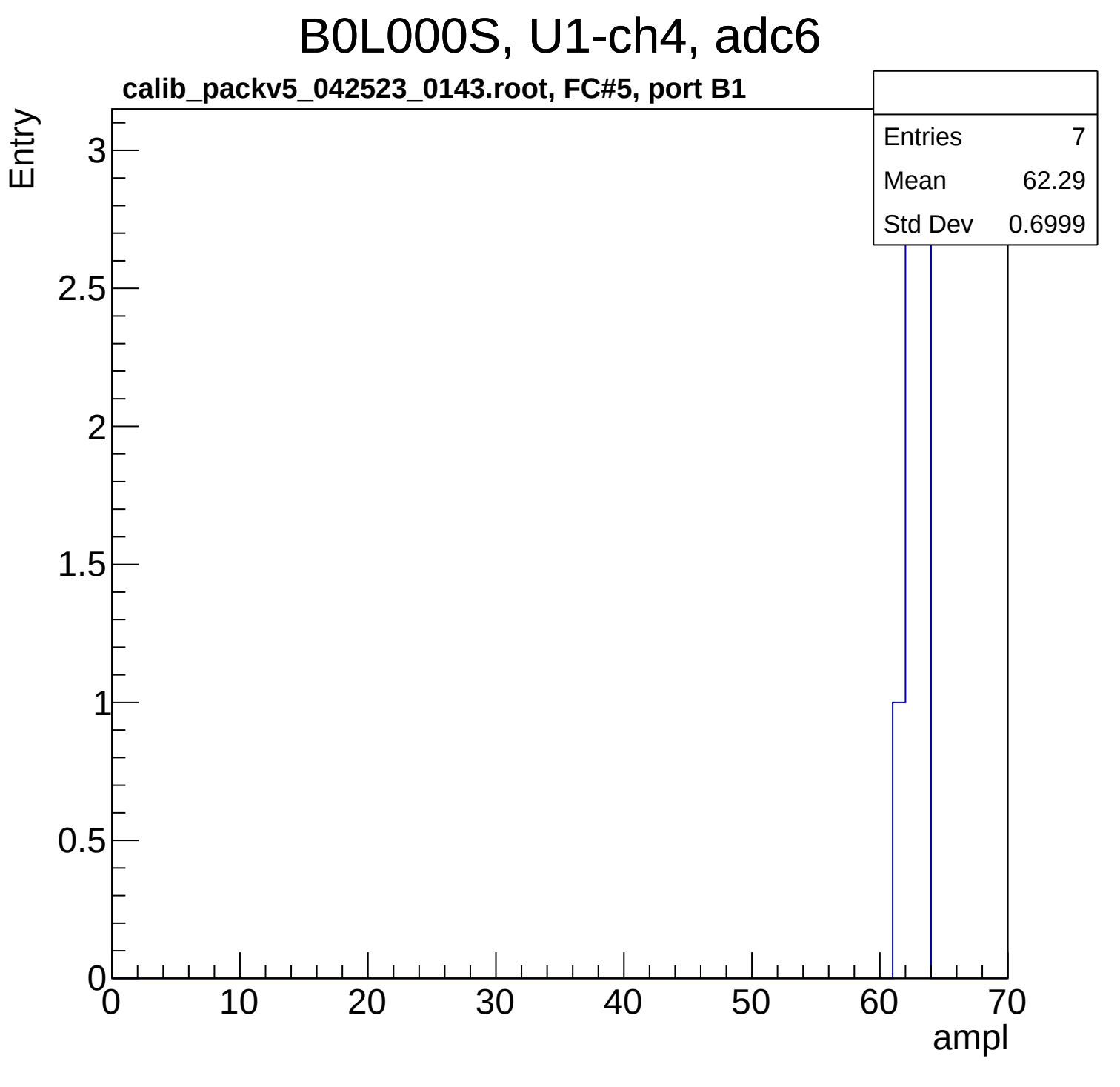
B0L000S, U1-ch4, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
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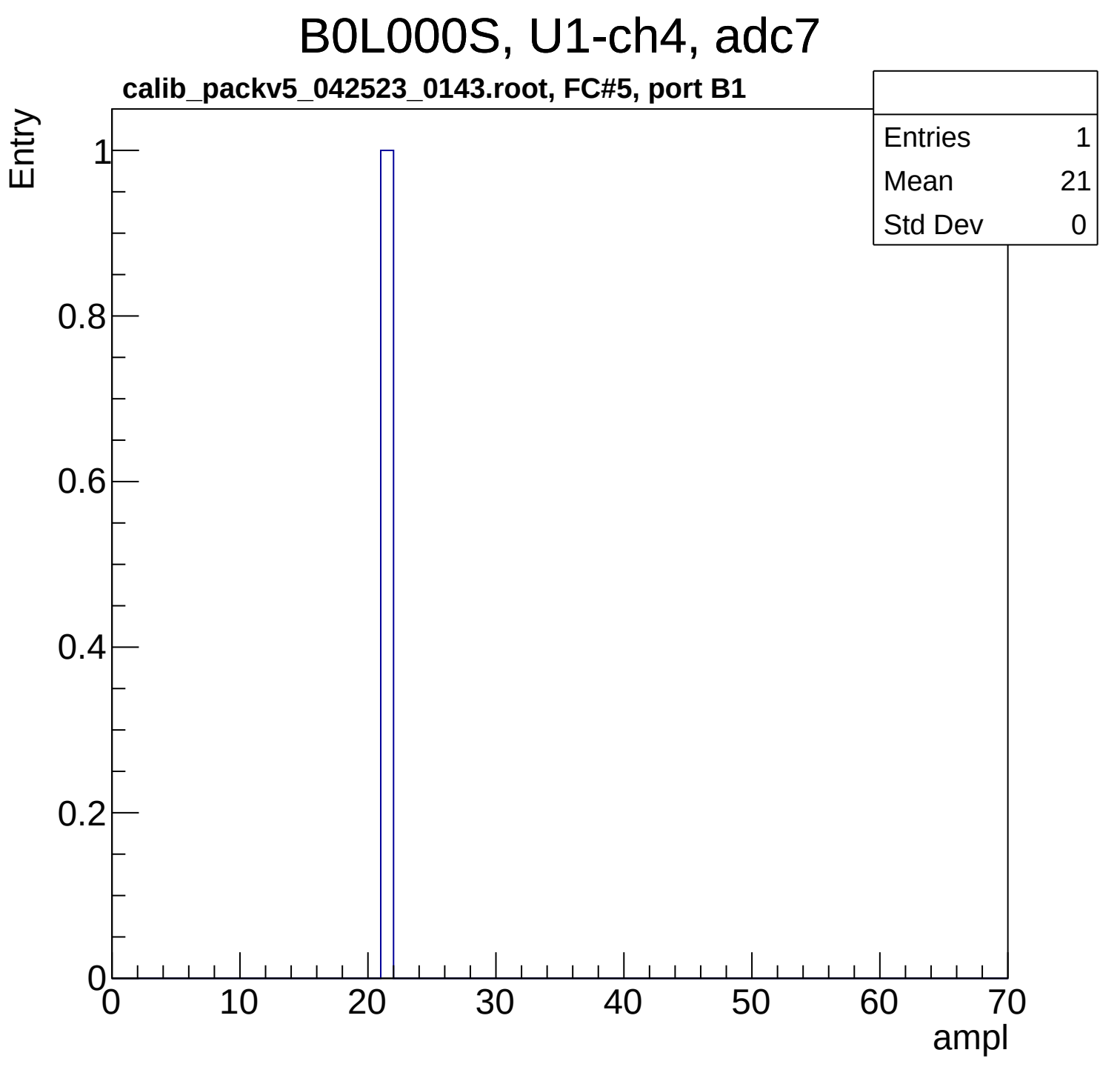
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch4, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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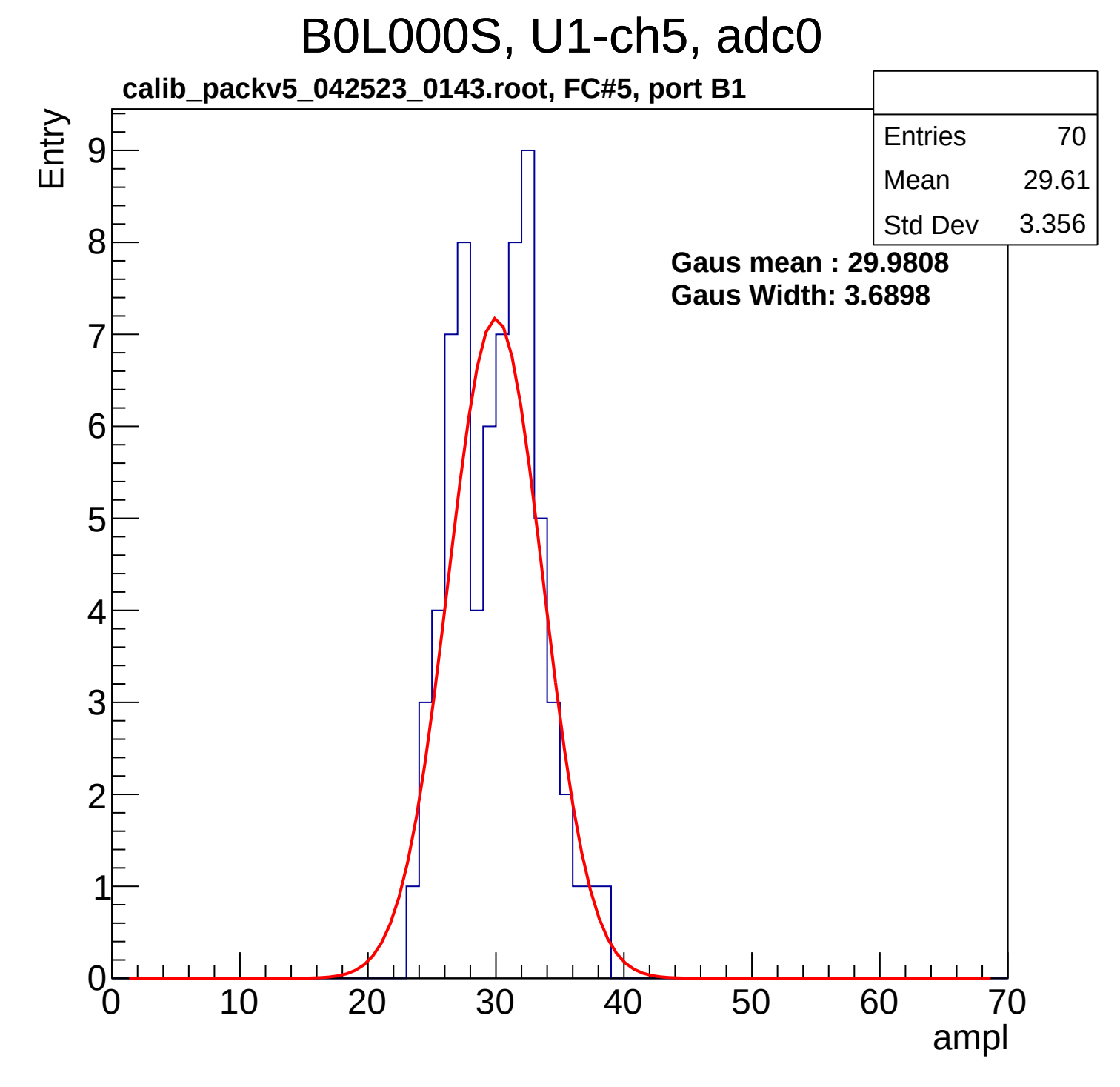
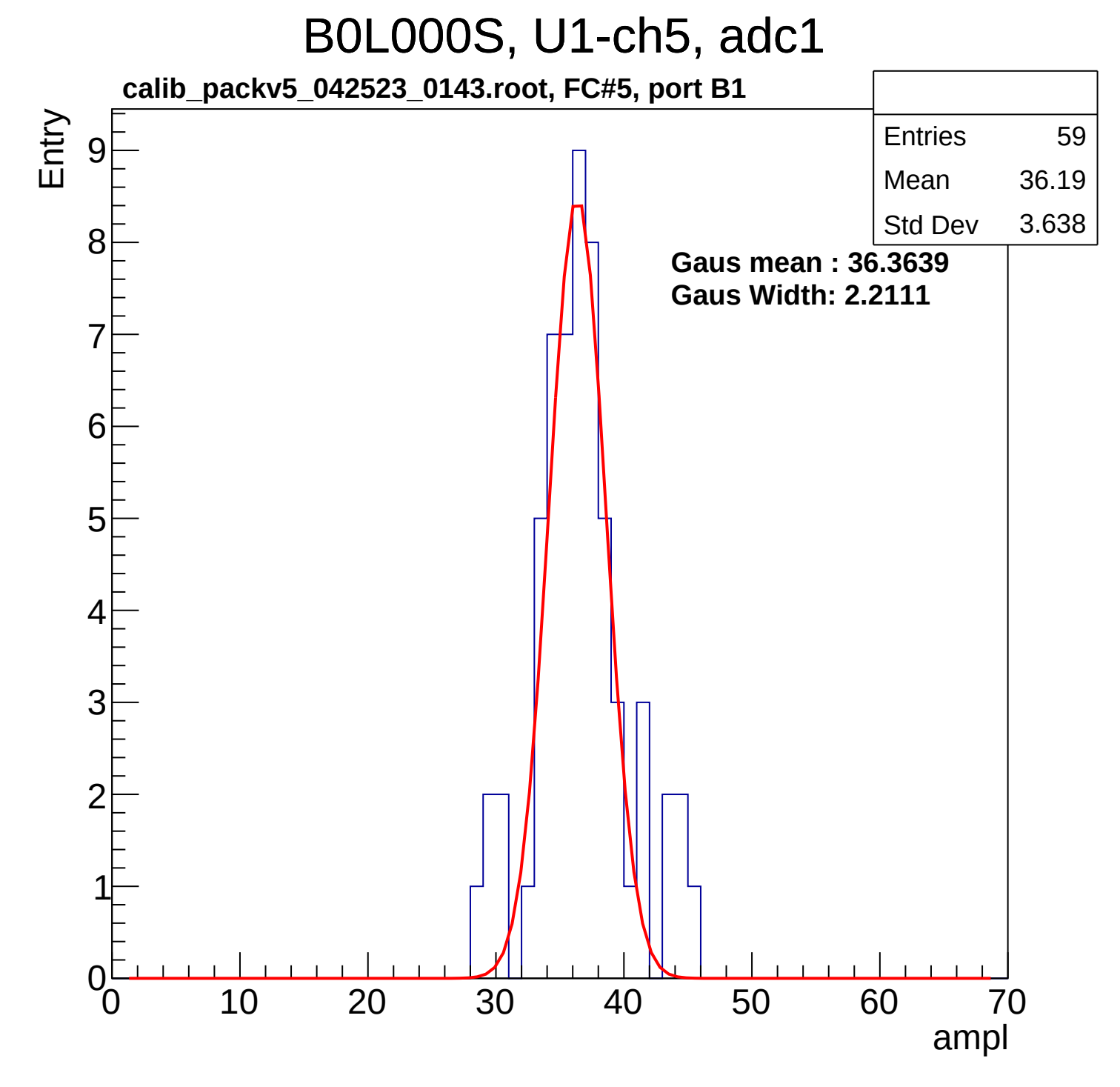
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch5, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.61
Std Dev 3.356
Gaus mean : 29.9808
Gaus Width: 3.6898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch5, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 36.19
Std Dev 3.638
Gaus mean : 36.3639
Gaus Width: 2.2111
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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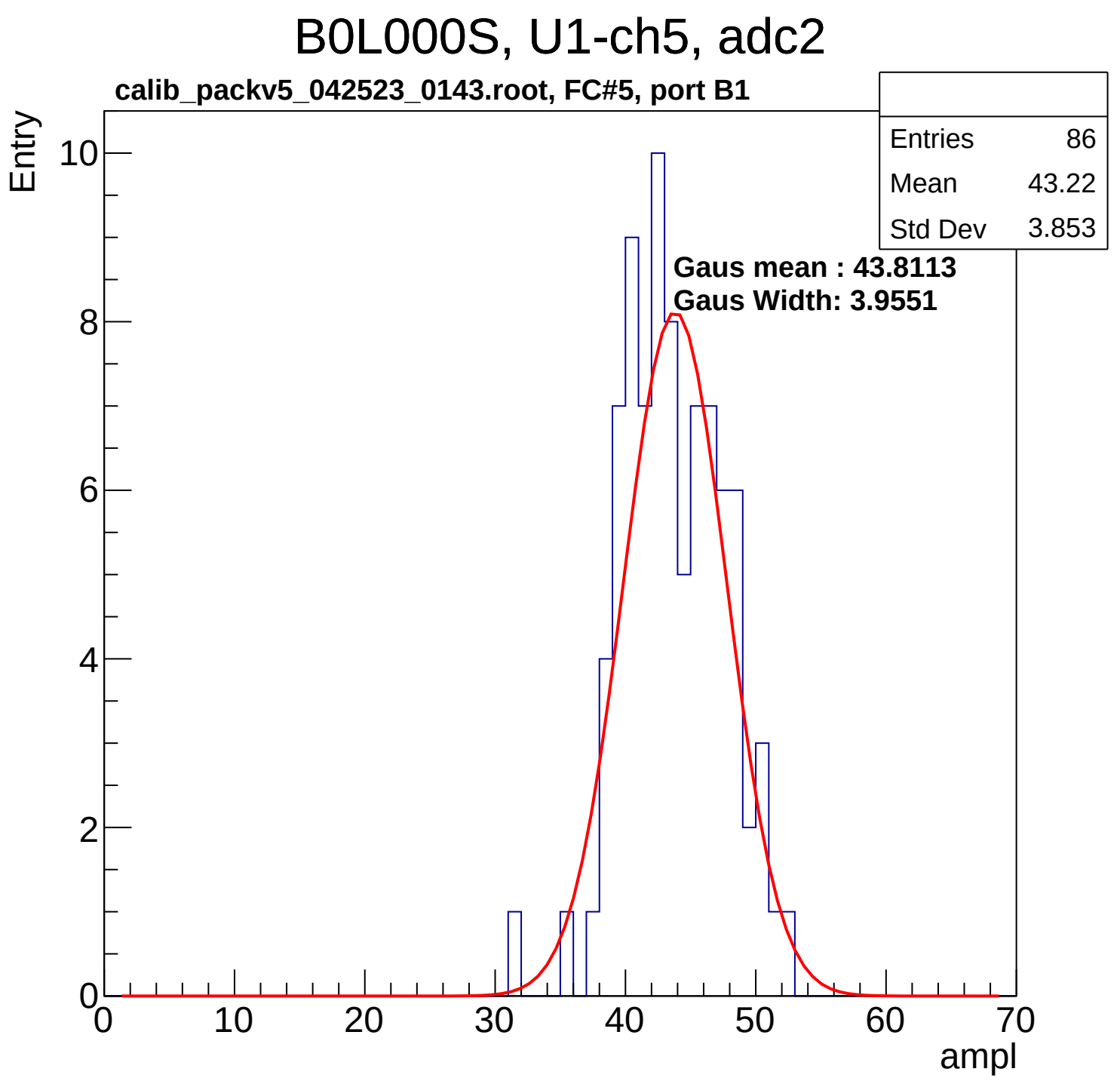
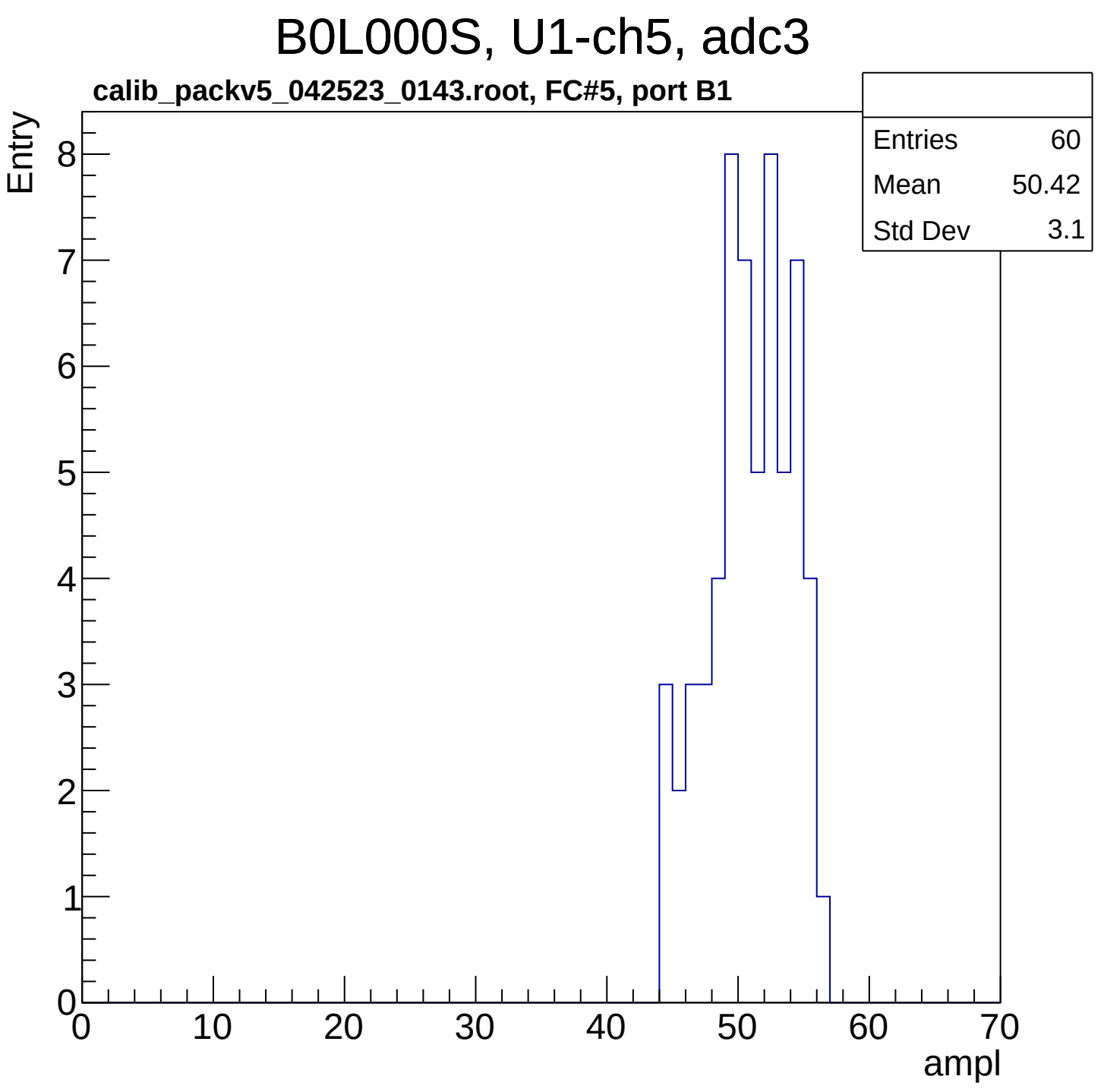
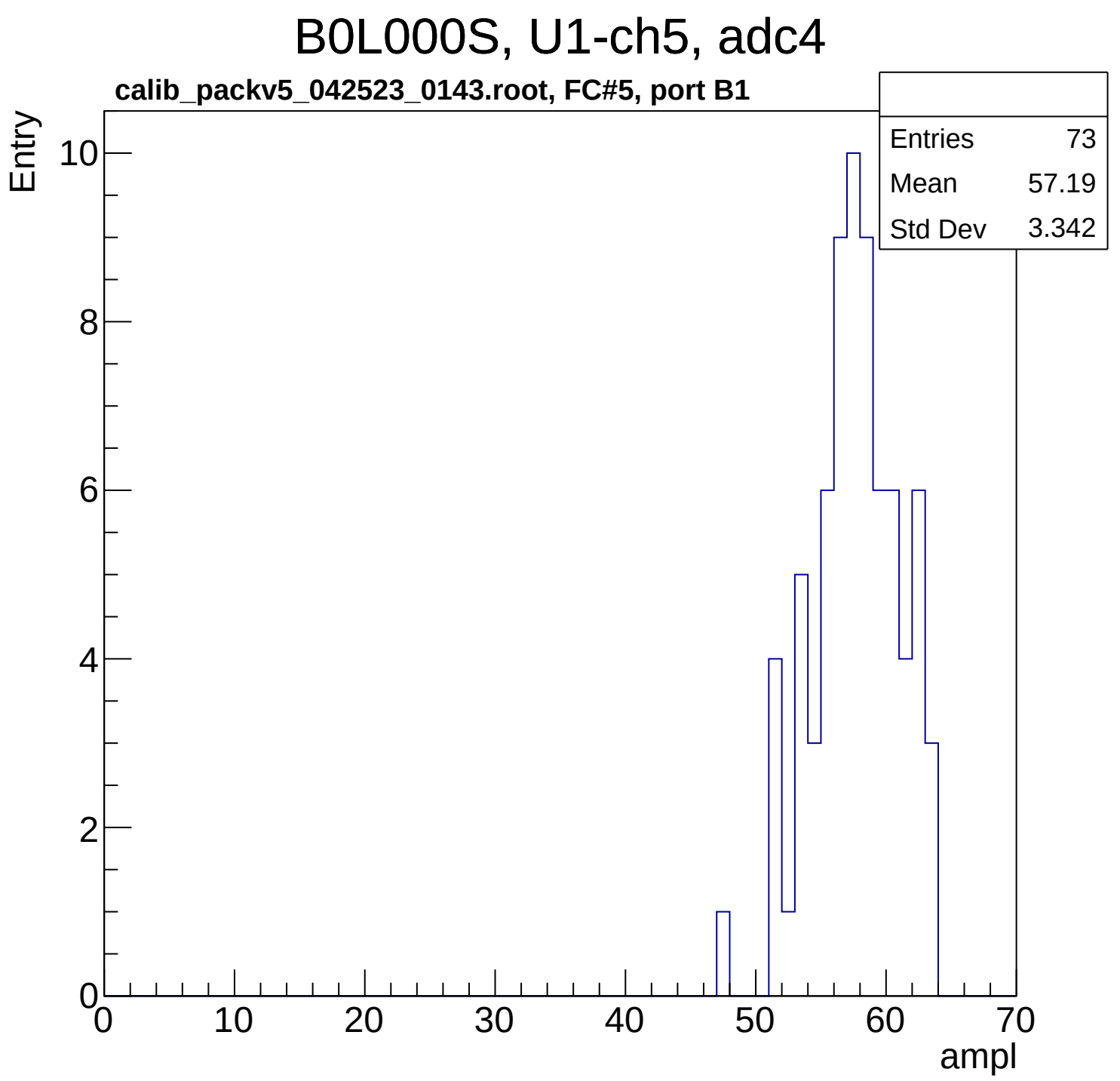
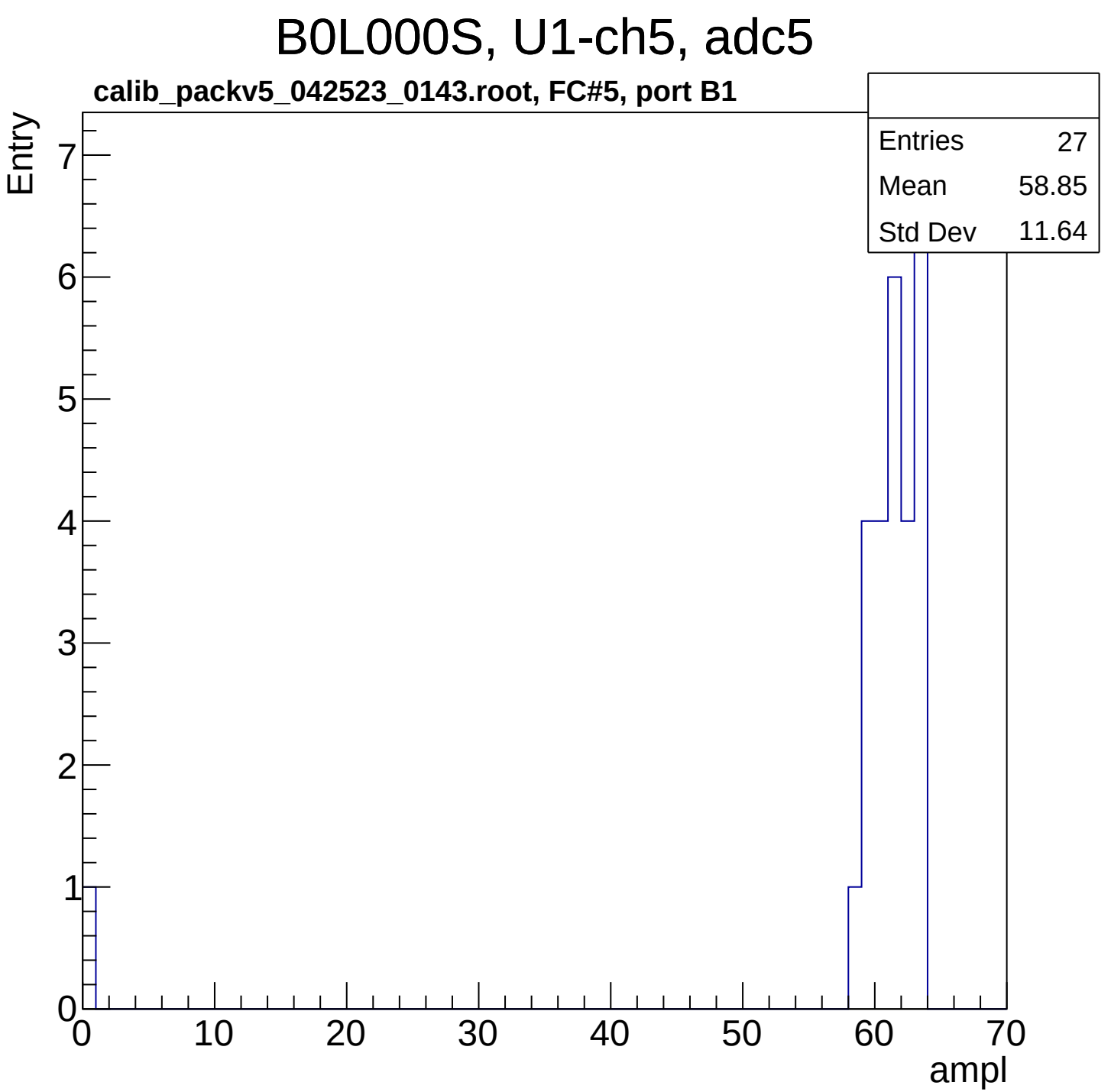
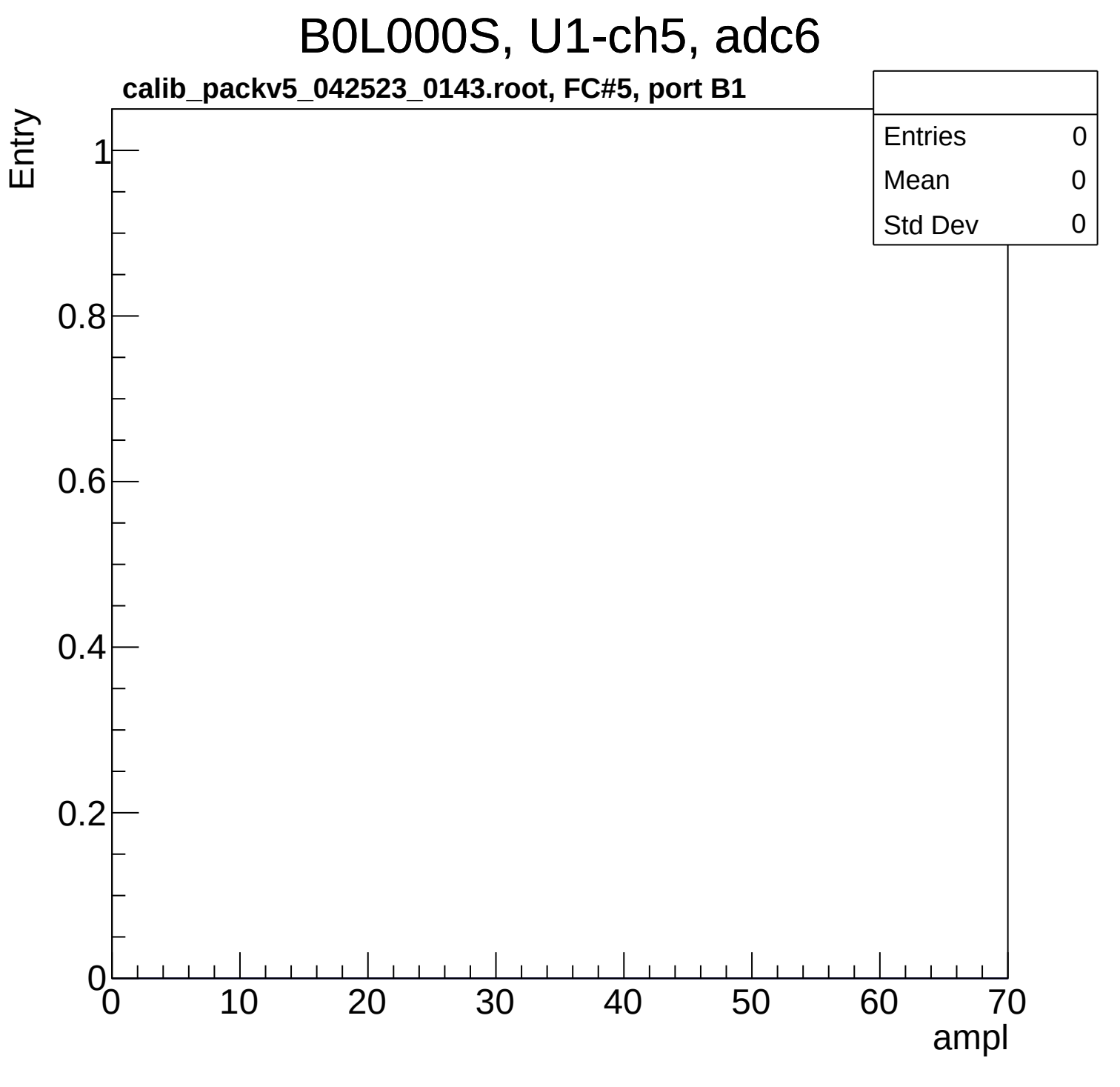

B0L000S, U1-ch5, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 43.22
Std Dev 3.853
Gaus mean : 43.8113
Gaus Width: 3.9551
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch5, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 50.42
Std Dev 3.1
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch5, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 57.19
Std Dev 3.342
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch5, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.85
Std Dev 11.64
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch5, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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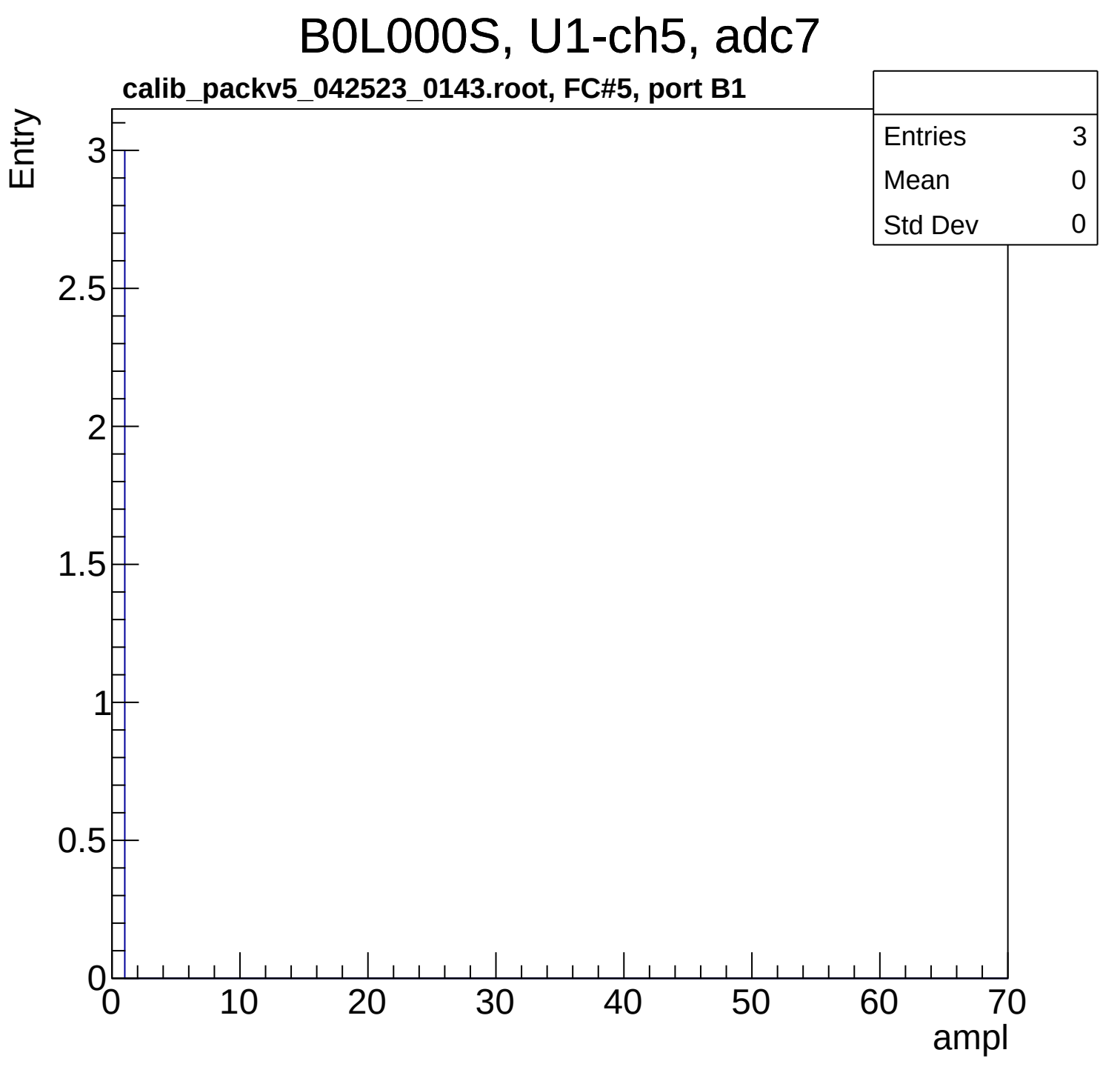
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch5, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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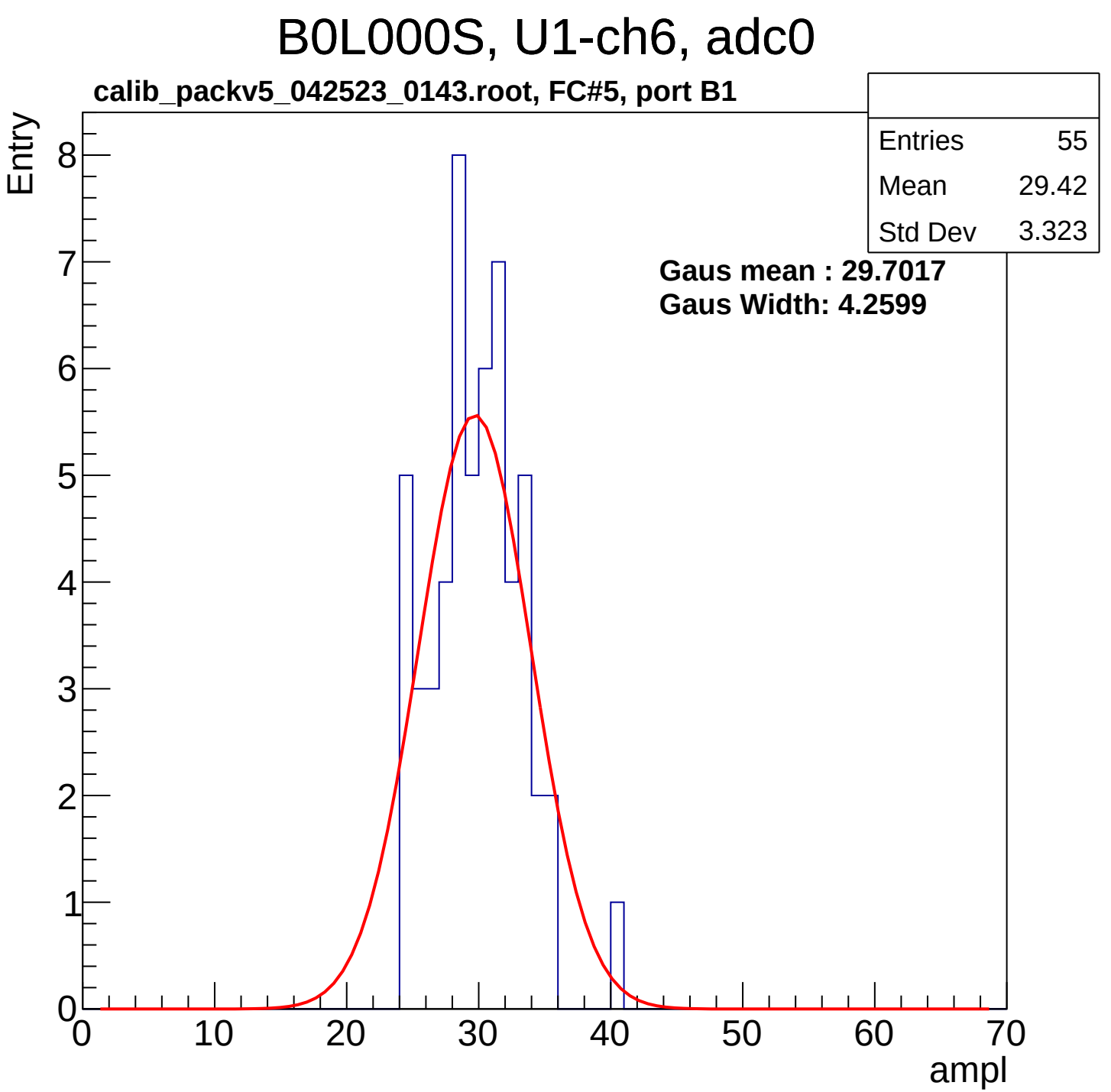
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch6, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.42
Std Dev 3.323
Gaus mean : 29.7017
Gaus Width: 4.2599
Entry
ampl
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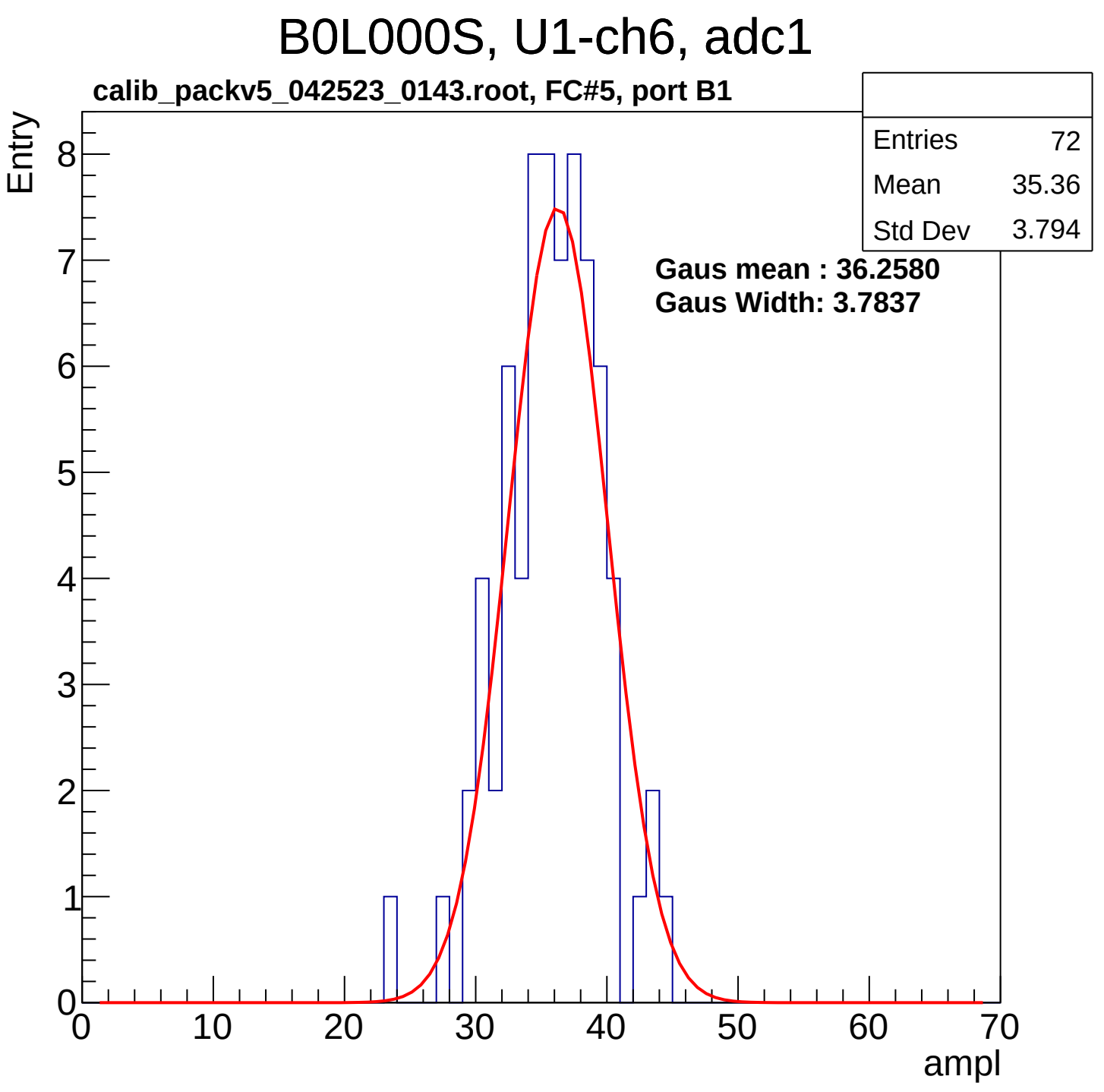
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch6, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.36
Std Dev 3.794
Gaus mean : 36.2580
Gaus Width: 3.7837
Entry
ampl
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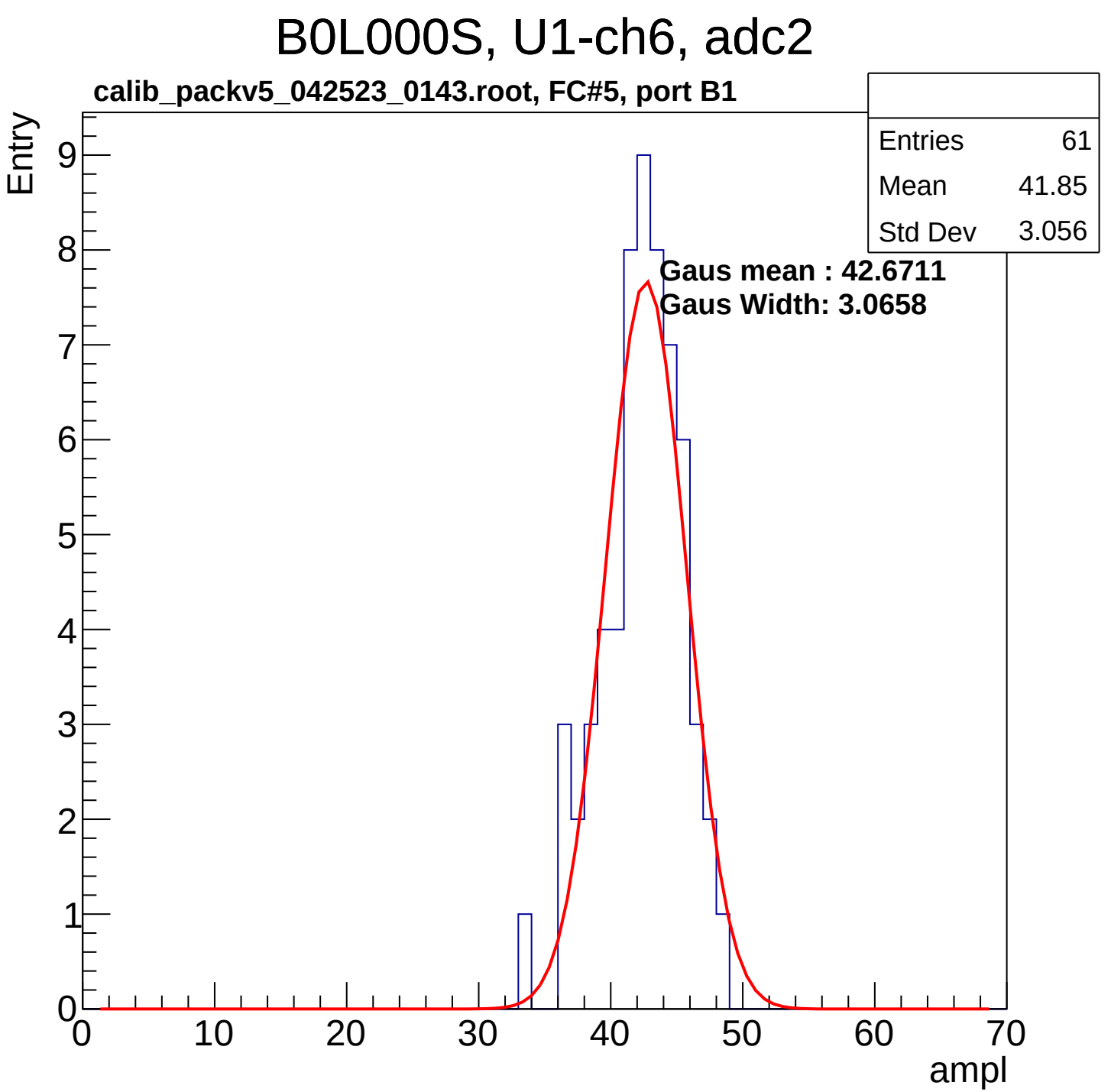
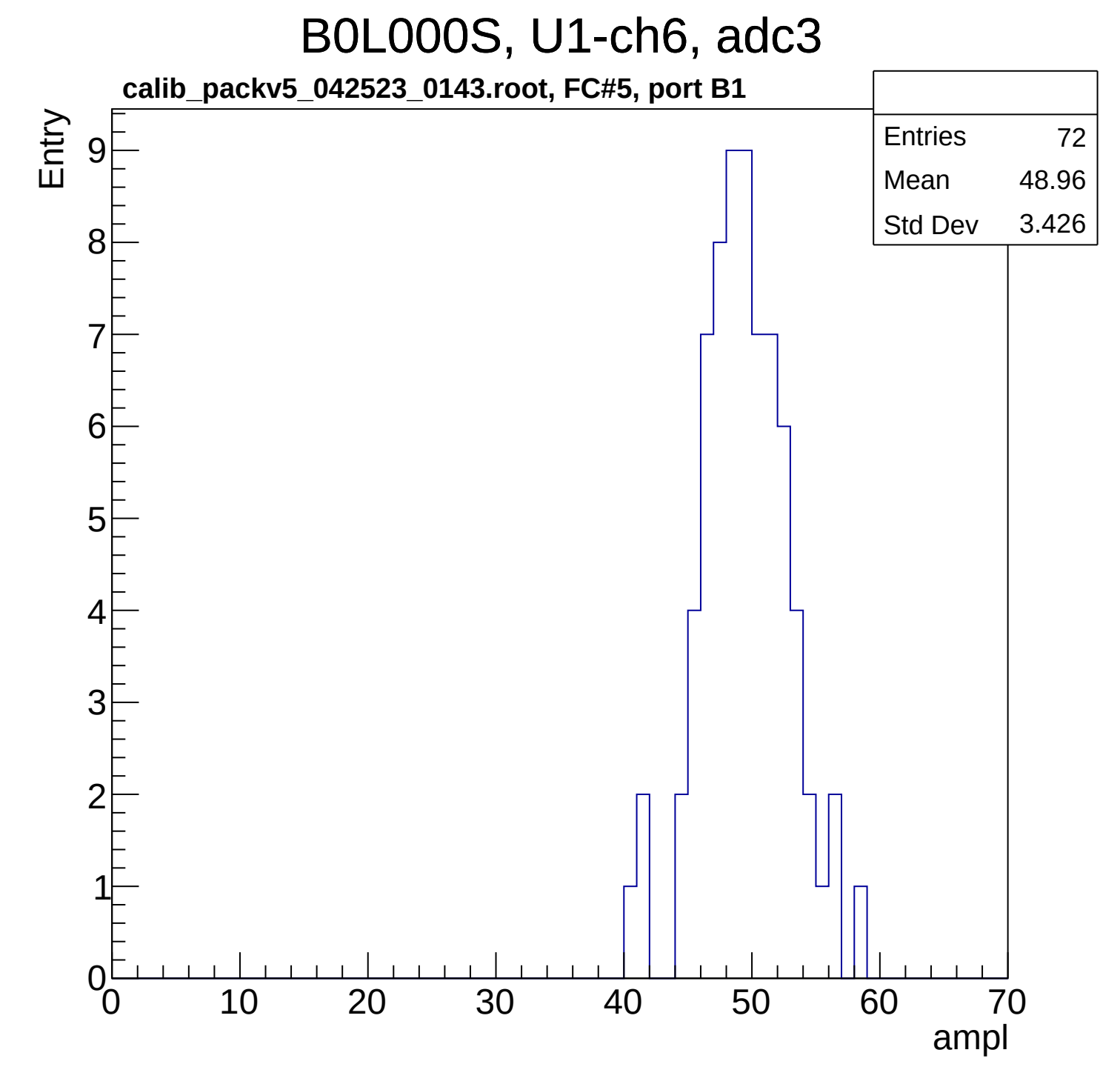
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch6, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 41.85
Std Dev 3.056
Gaus mean : 42.6711
Gaus Width: 3.0658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch6, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.96
Std Dev 3.426
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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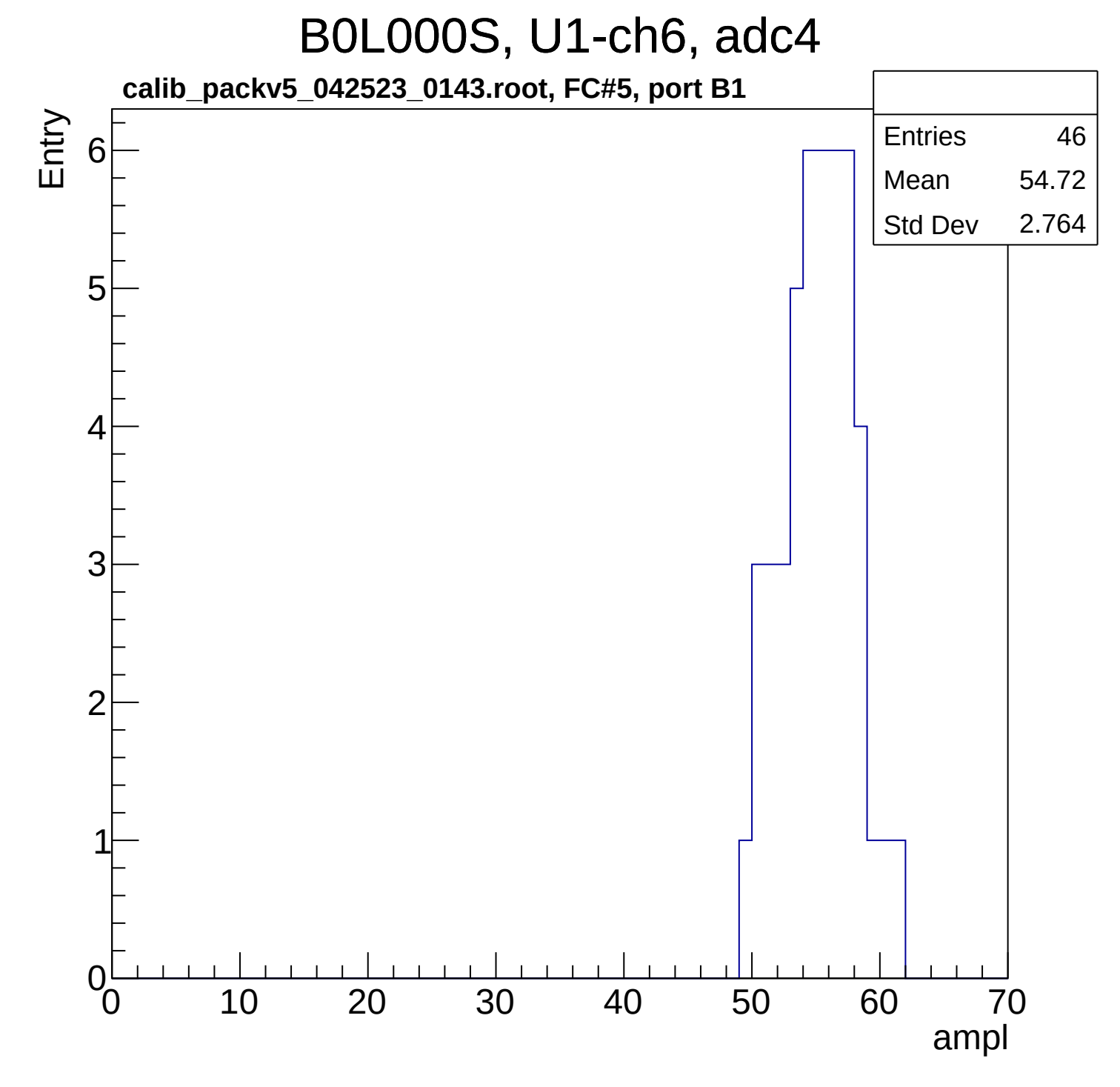
B0L000S, U1-ch6, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 54.72
Std Dev 2.764
Entry
ampl
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3
4
5
6
0
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20
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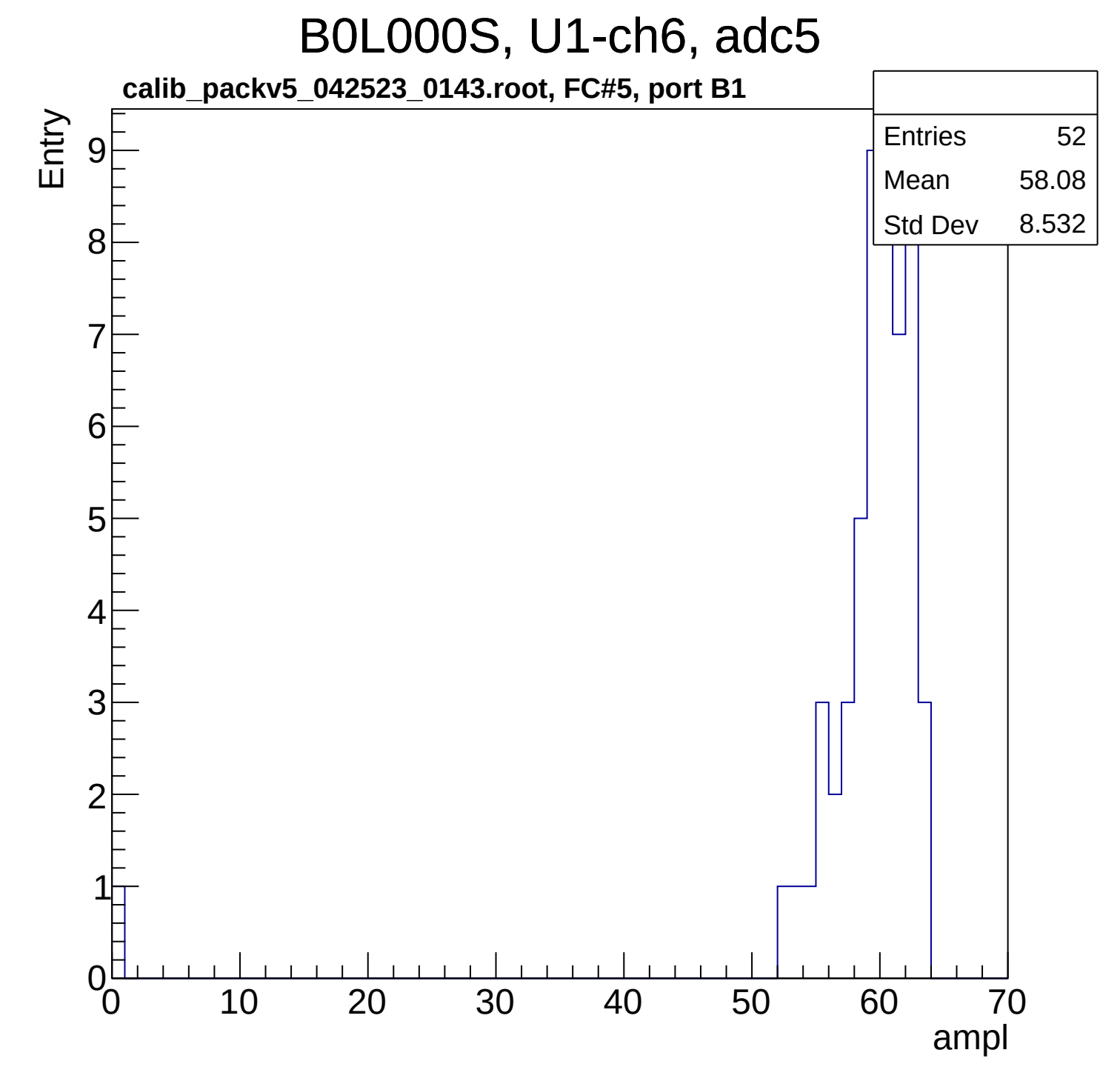
40
50
60
70


B0L000S, U1-ch6, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.08
Std Dev 8.532
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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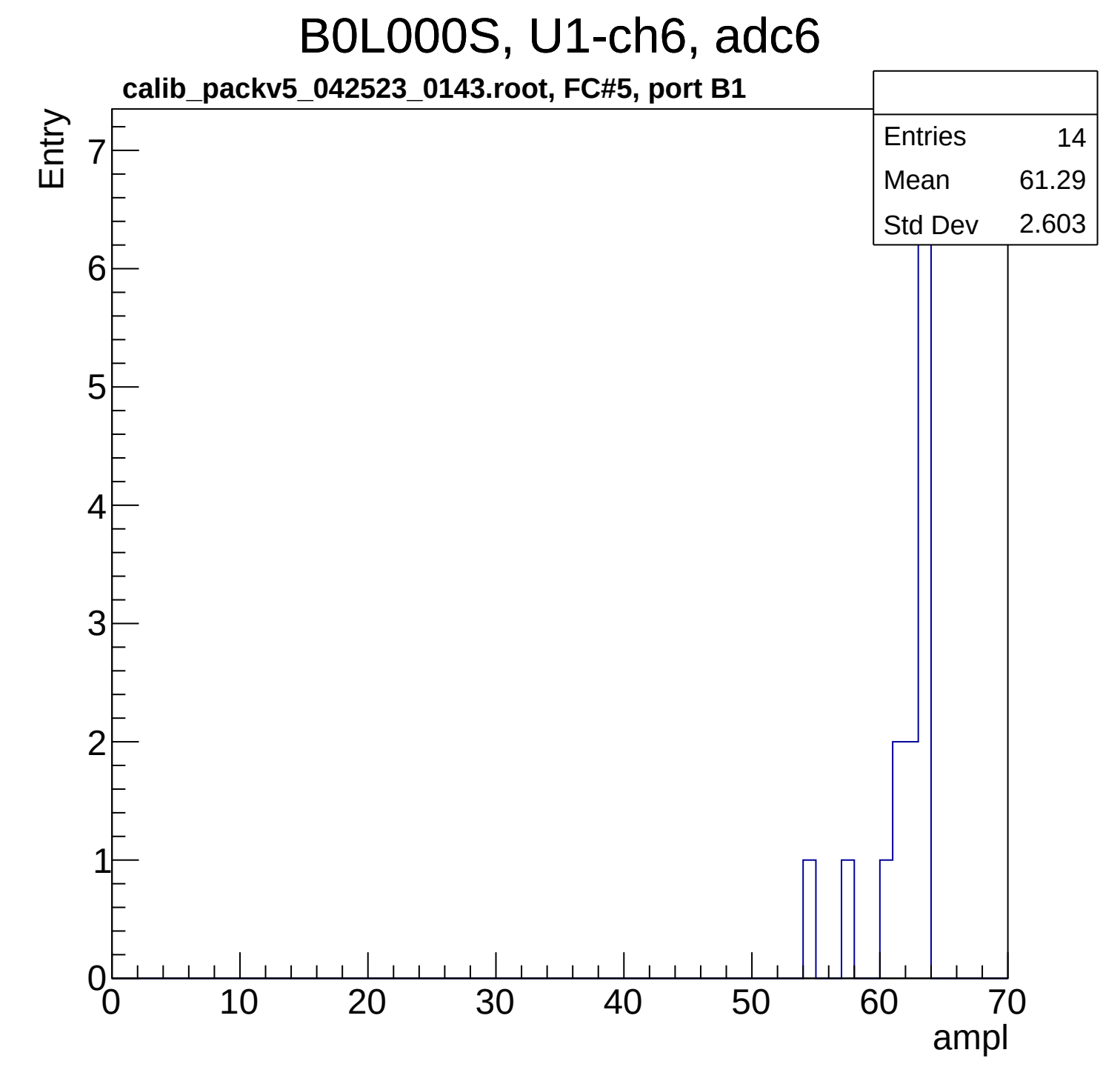
B0L000S, U1-ch6, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.29
Std Dev 2.603
Entry
ampl
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2
3
4
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6
7
0
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60
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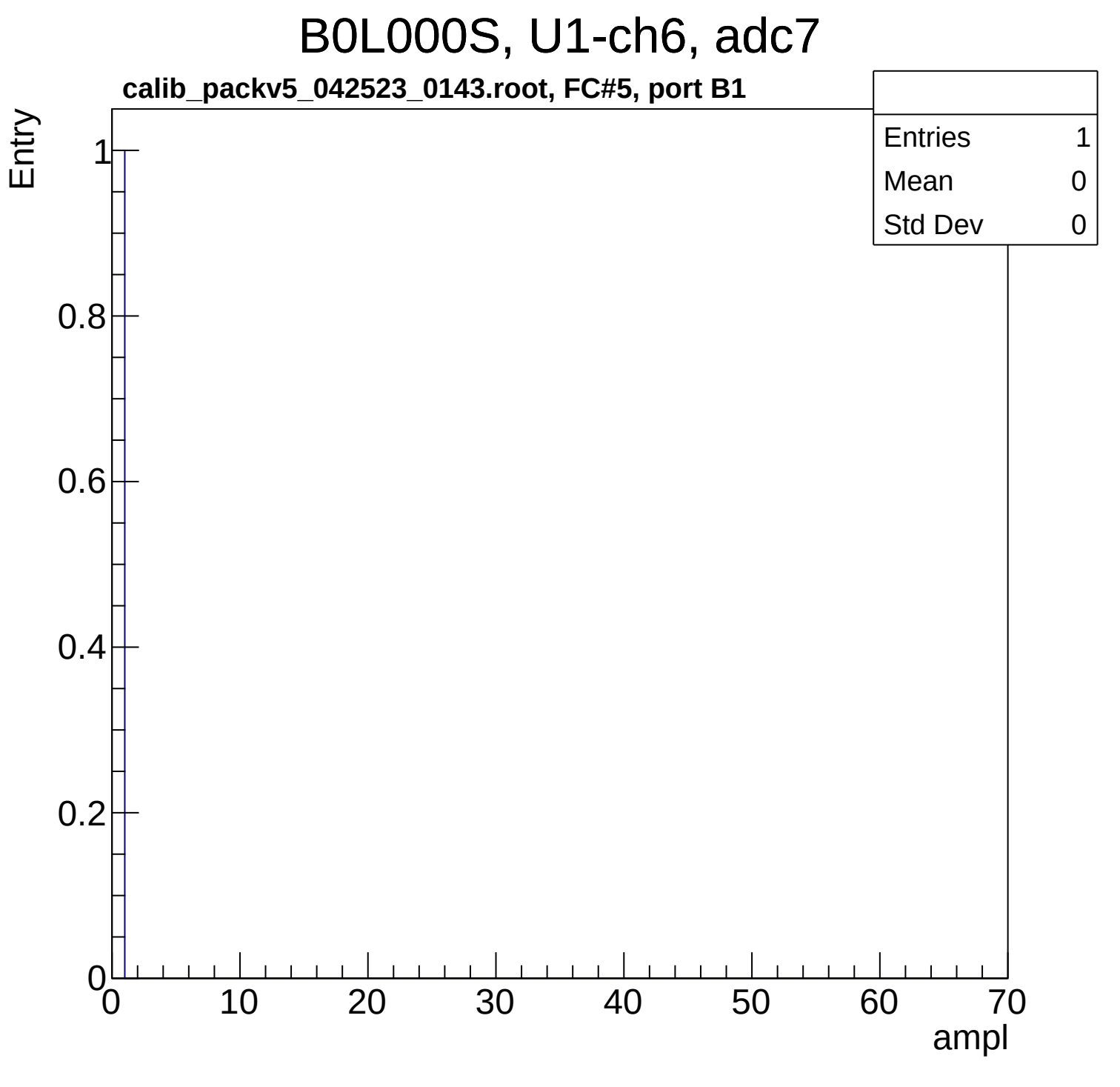
B0L000S, U1-ch6, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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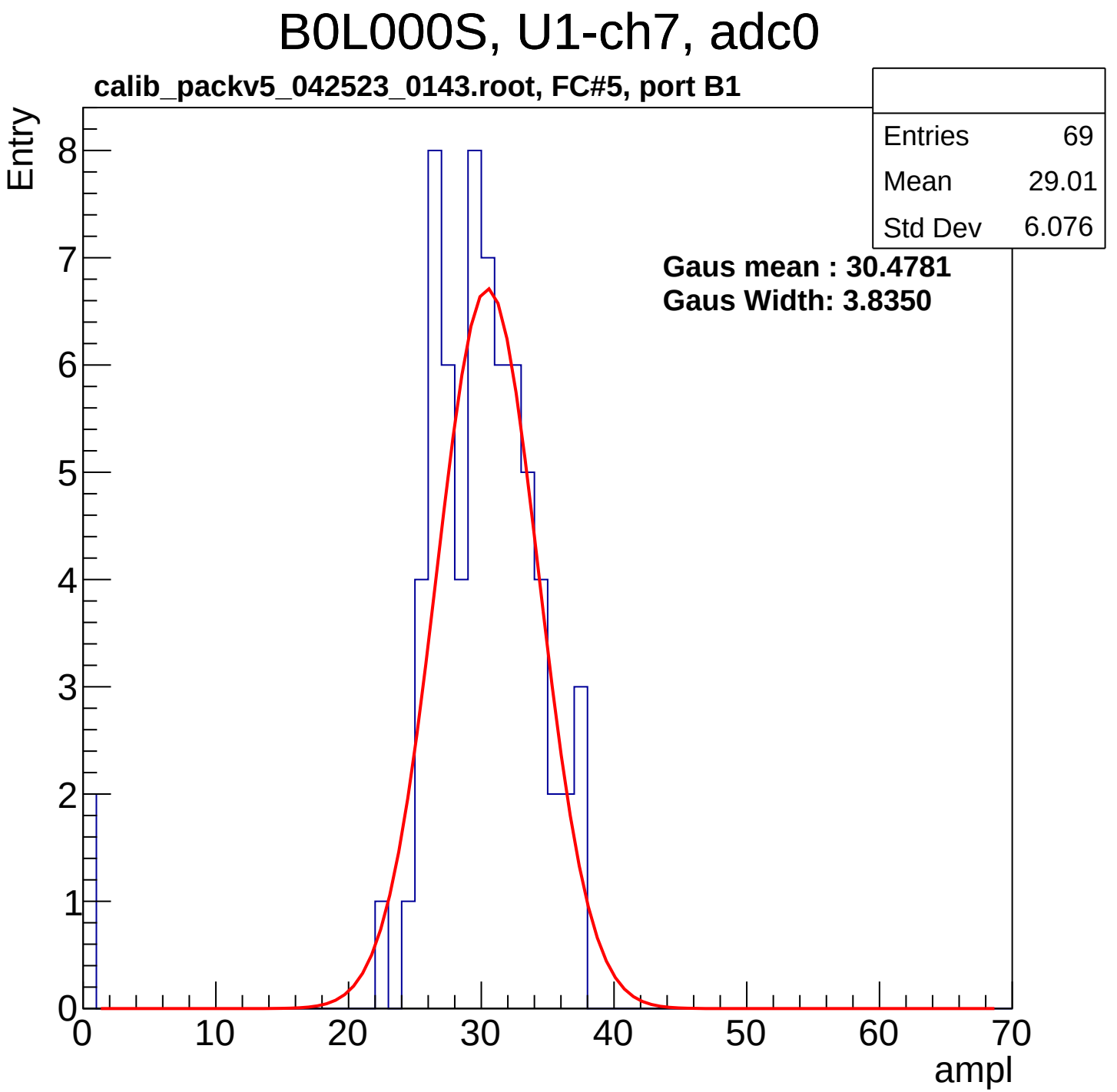
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch7, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.01
Std Dev 6.076
Gaus mean : 30.4781
Gaus Width: 3.8350
Entry
ampl
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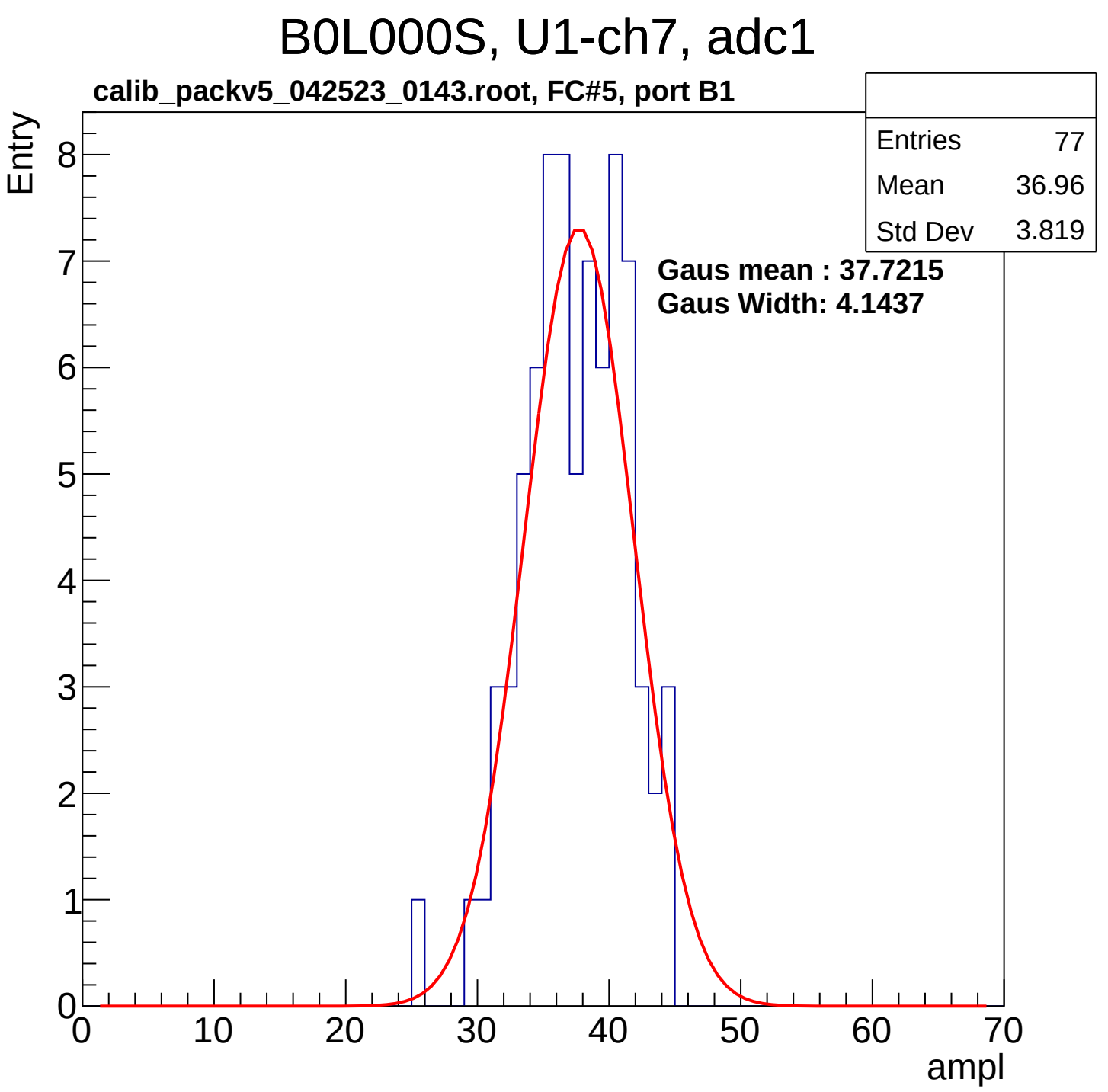
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch7, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.96
Std Dev 3.819
Gaus mean : 37.7215
Gaus Width: 4.1437
Entry
ampl
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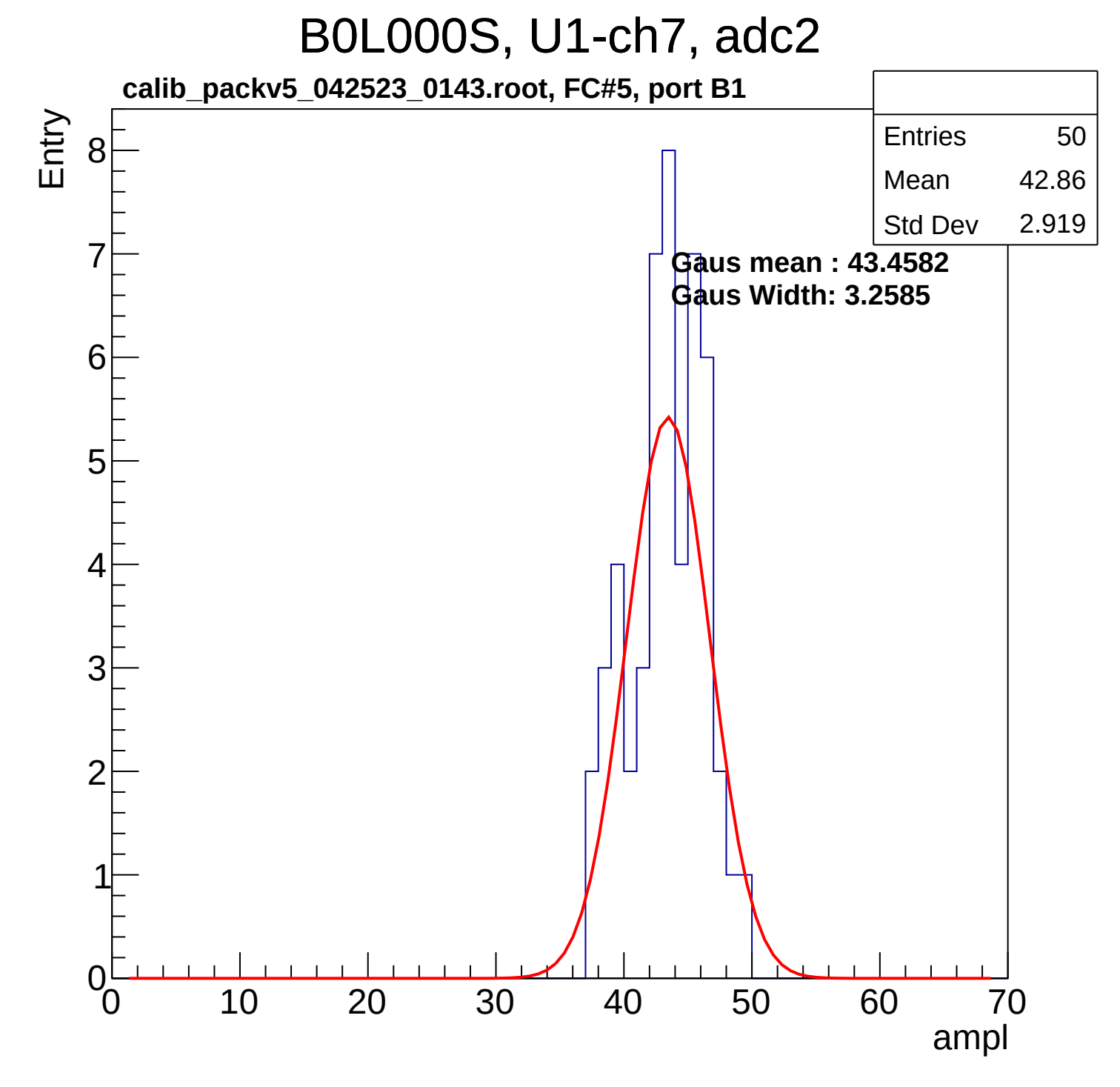
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch7, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.86
Std Dev 2.919
Gaus mean : 43.4582
Gaus Width: 3.2585
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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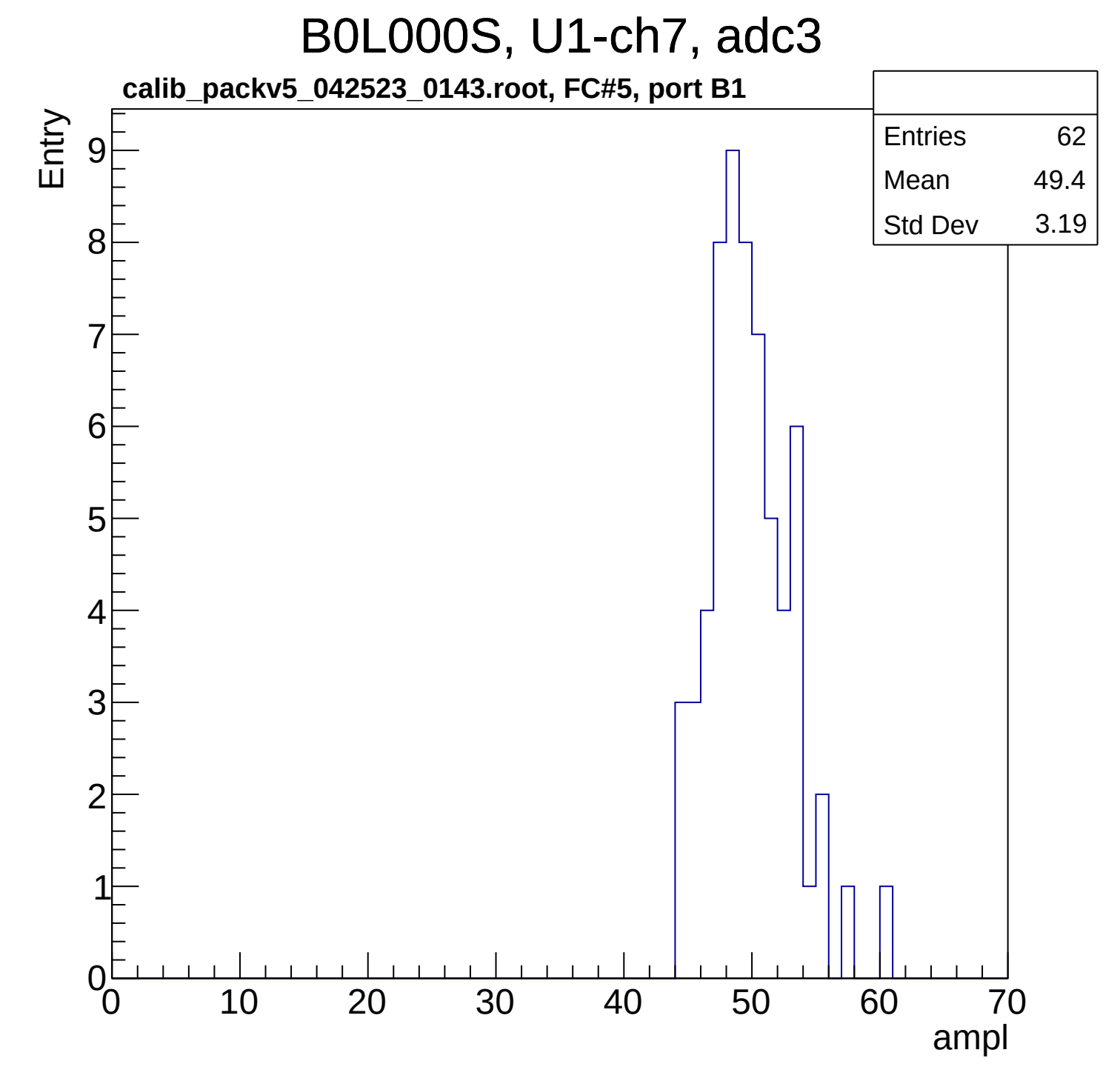
B0L000S, U1-ch7, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.4
Std Dev 3.19
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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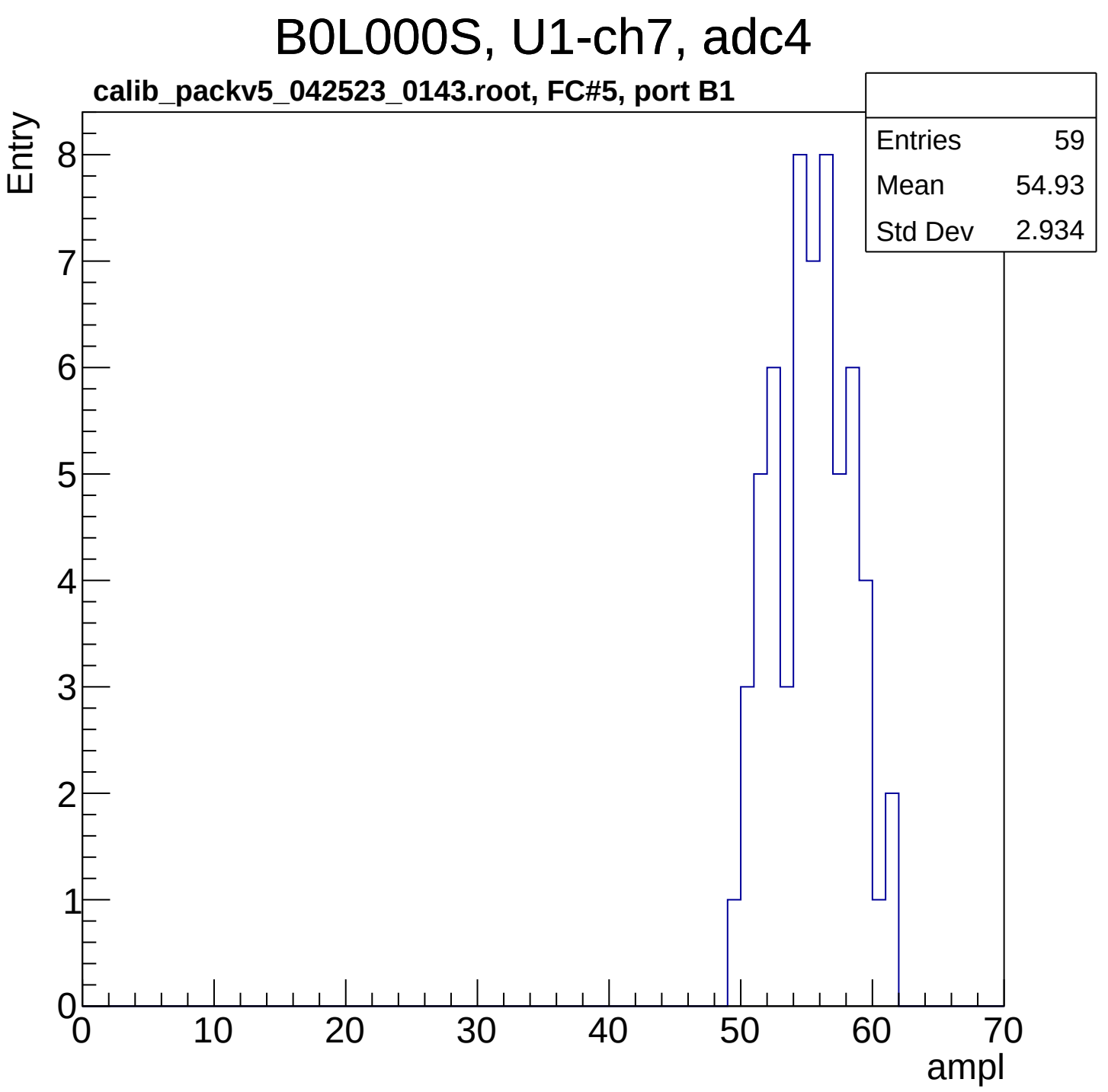
B0L000S, U1-ch7, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.93
Std Dev 2.934
Entry
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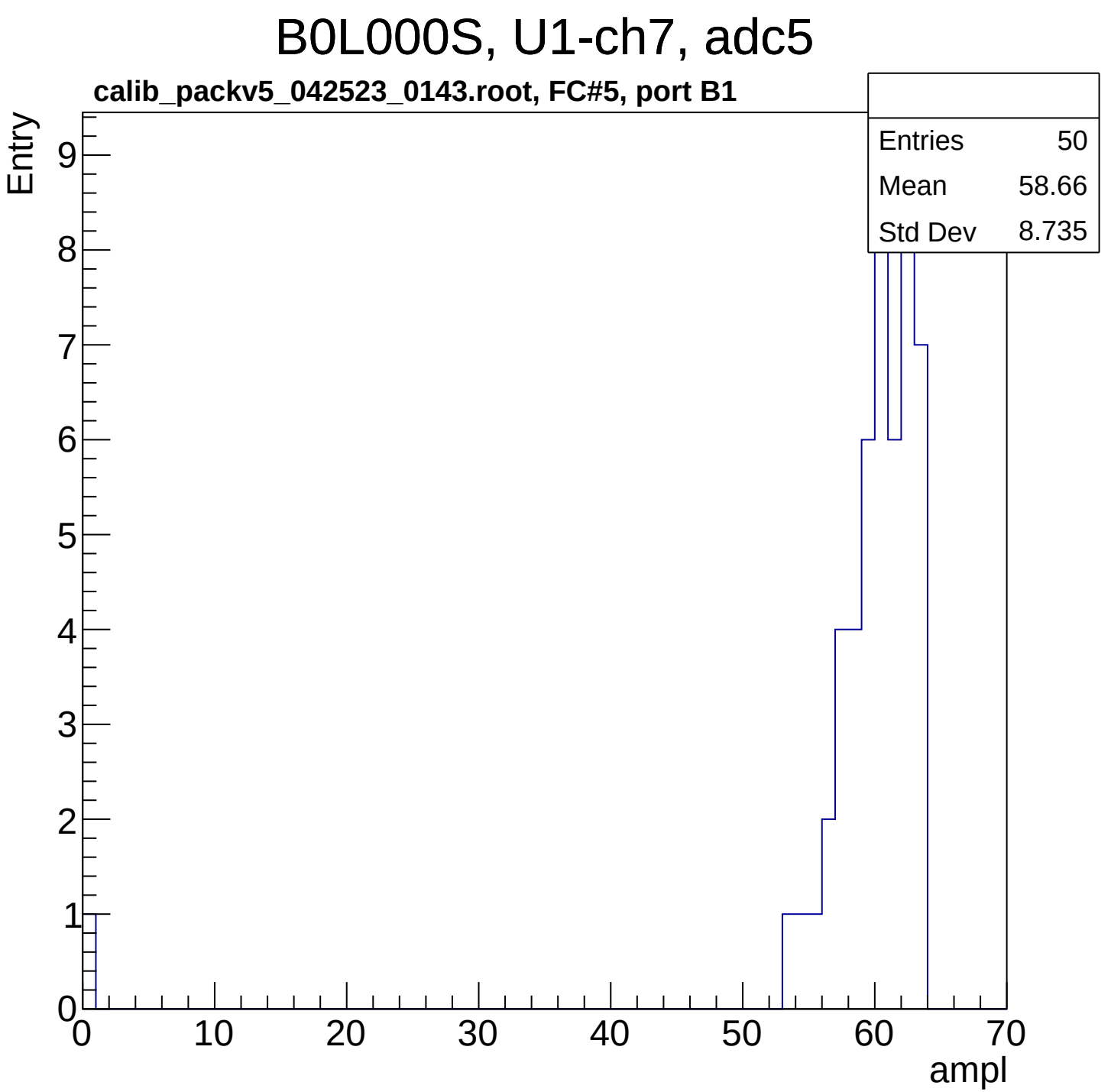
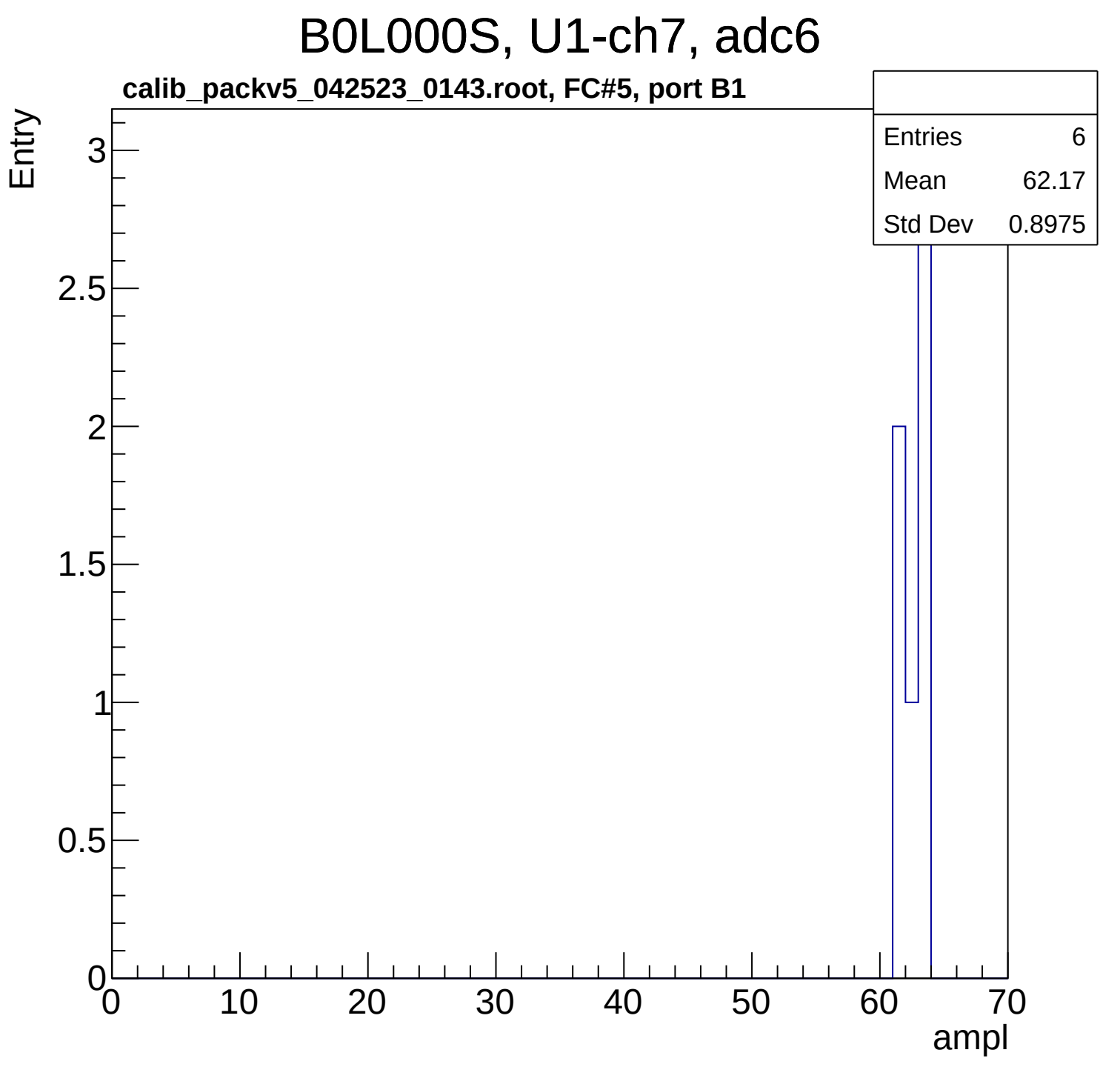
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch7, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.66
Std Dev 8.735
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch7, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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60
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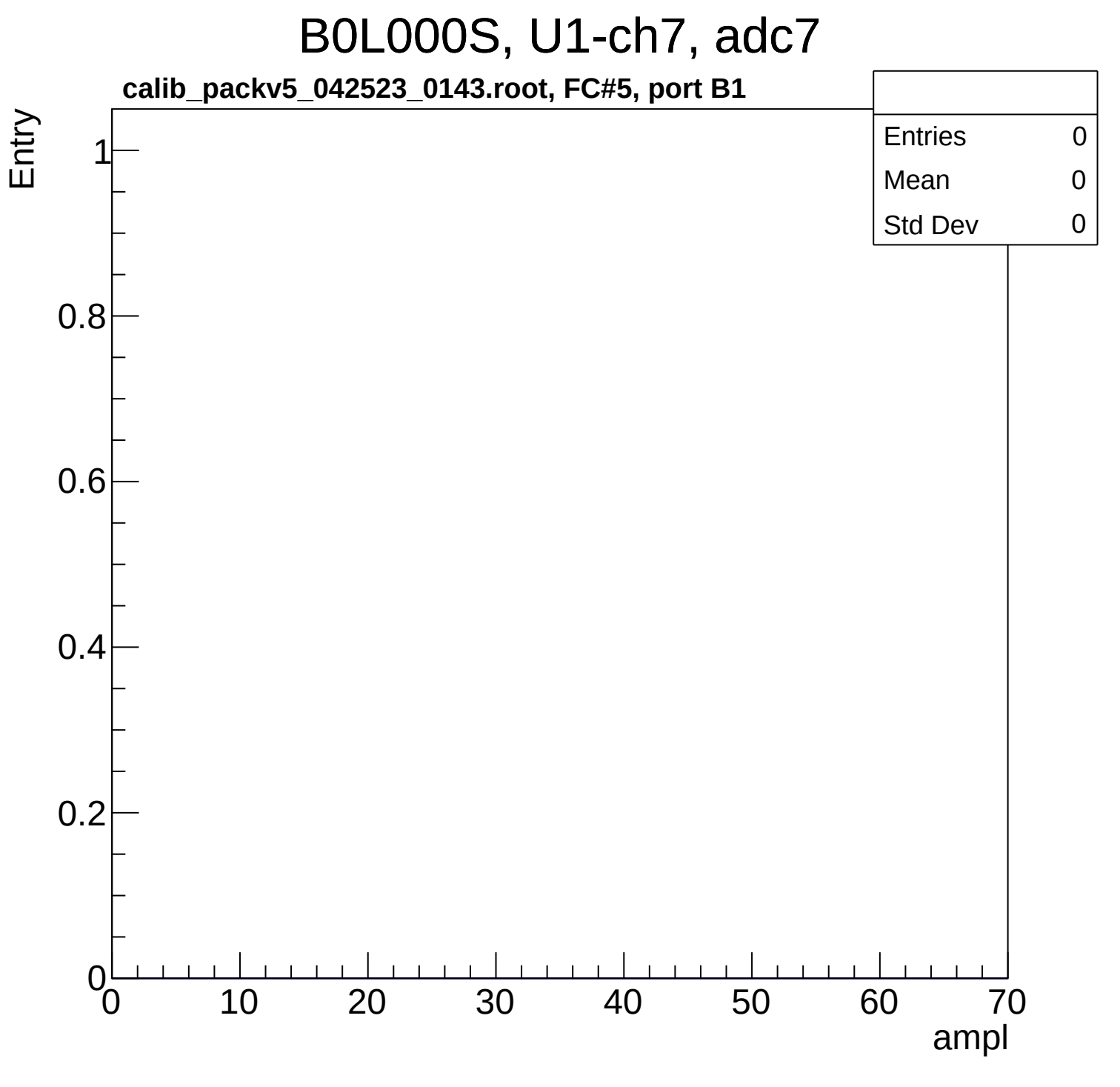
B0L000S, U1-ch7, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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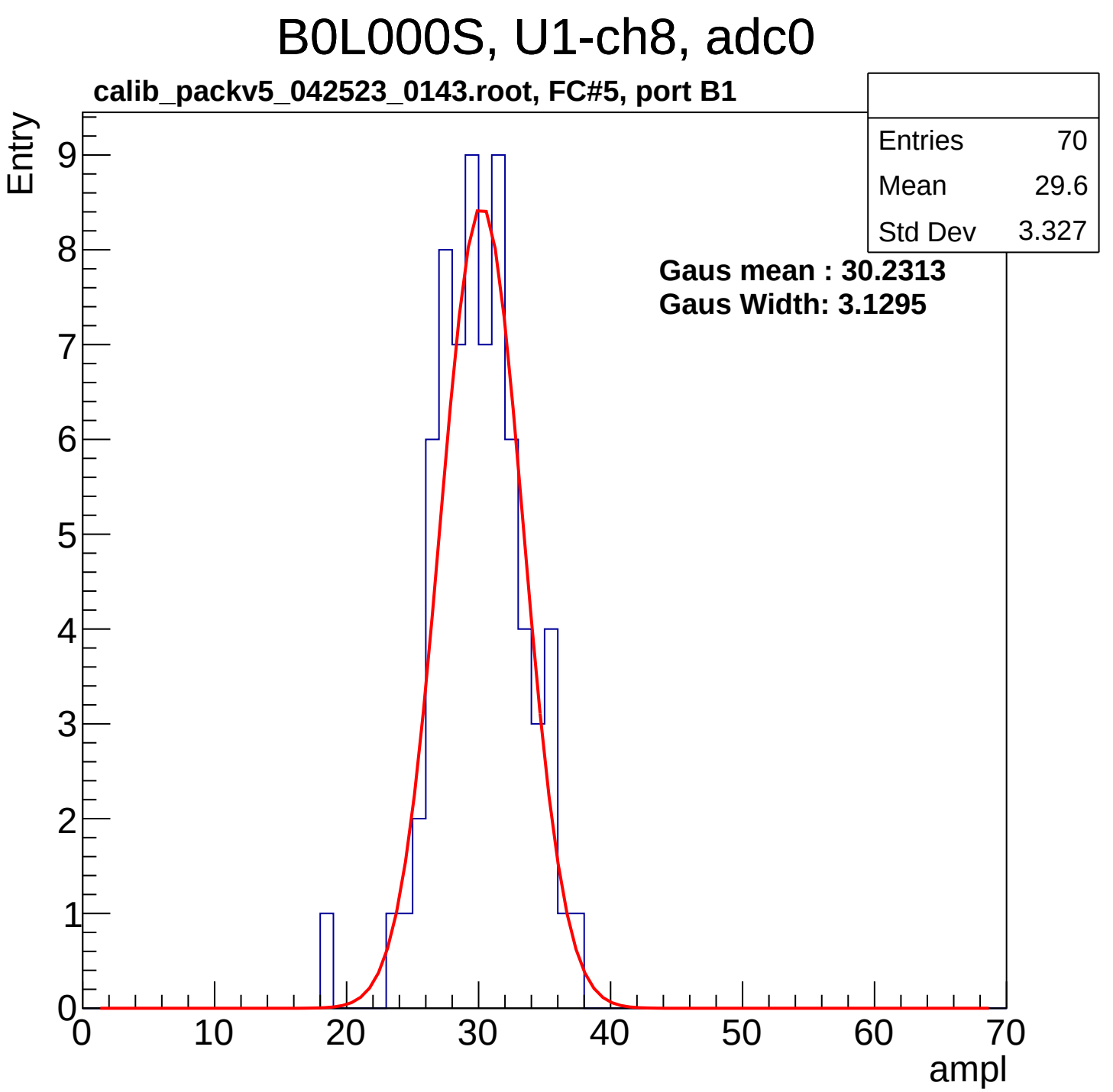
50
60
70
ampl


B0L000S, U1-ch8, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.6
Std Dev 3.327
Gaus mean : 30.2313
Gaus Width: 3.1295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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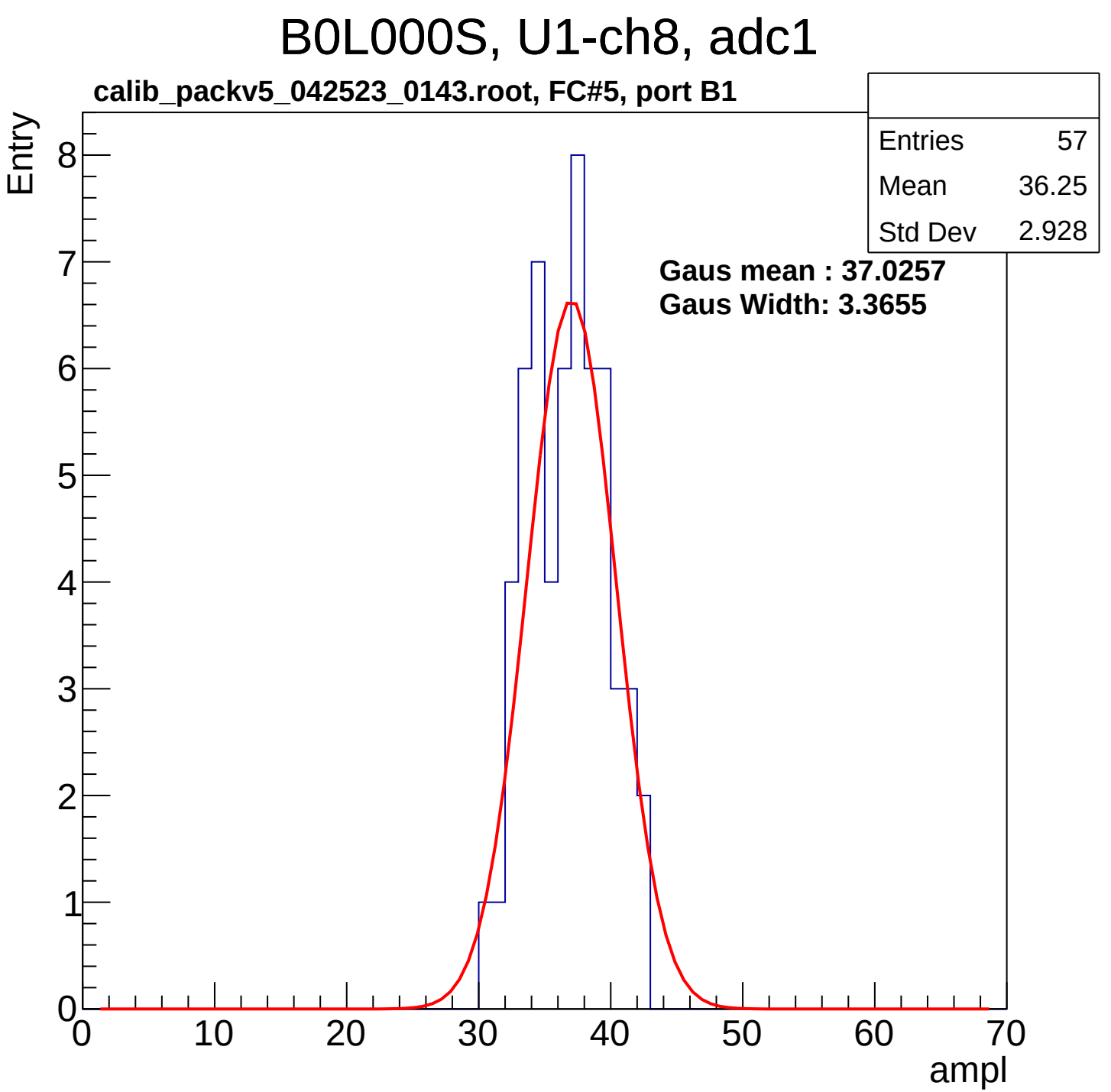
B0L000S, U1-ch8, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.25
Std Dev 2.928
Gaus mean : 37.0257
Gaus Width: 3.3655
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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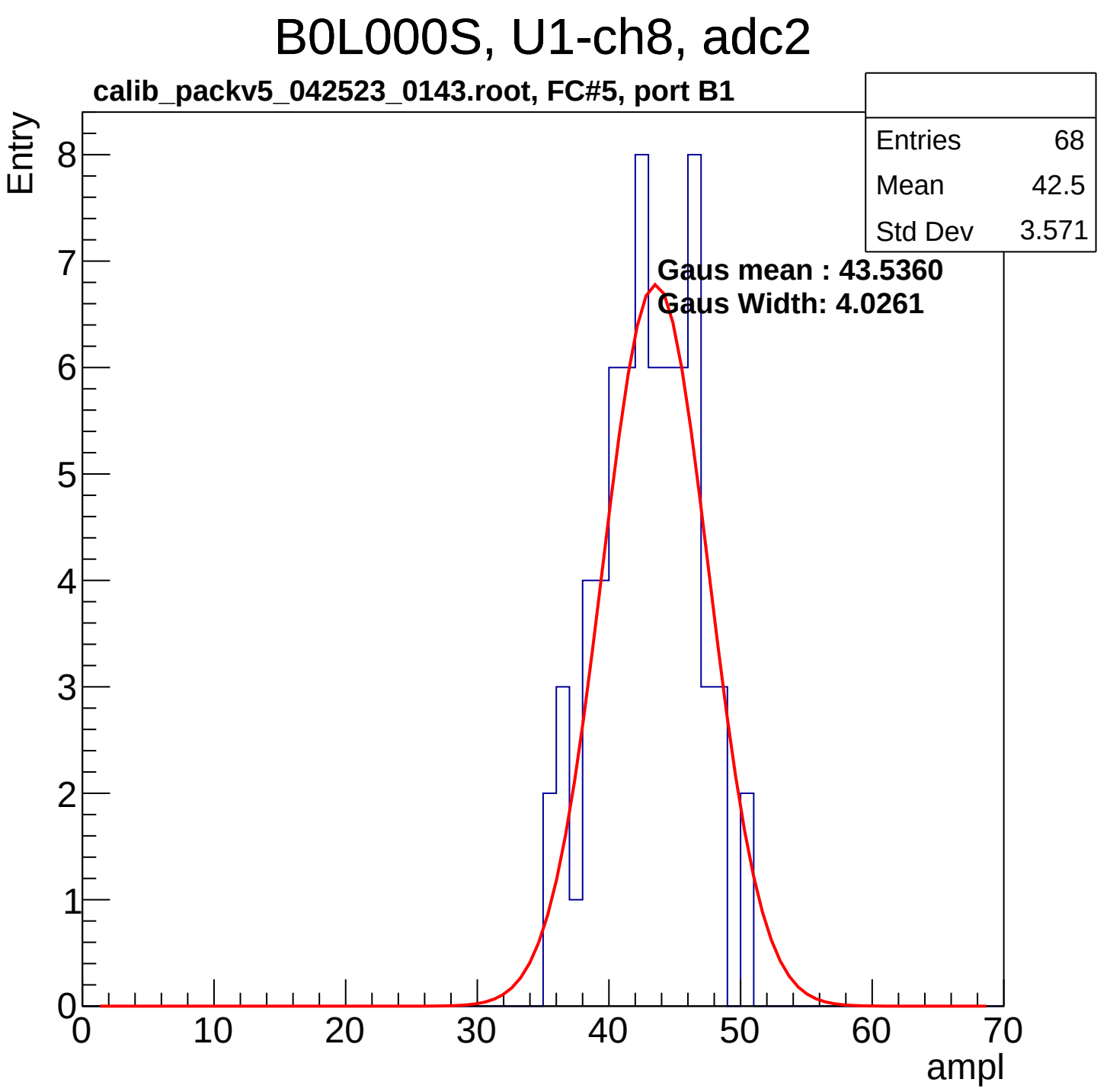
B0L000S, U1-ch8, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.5
Std Dev 3.571
Gaus mean : 43.5360
Gaus Width: 4.0261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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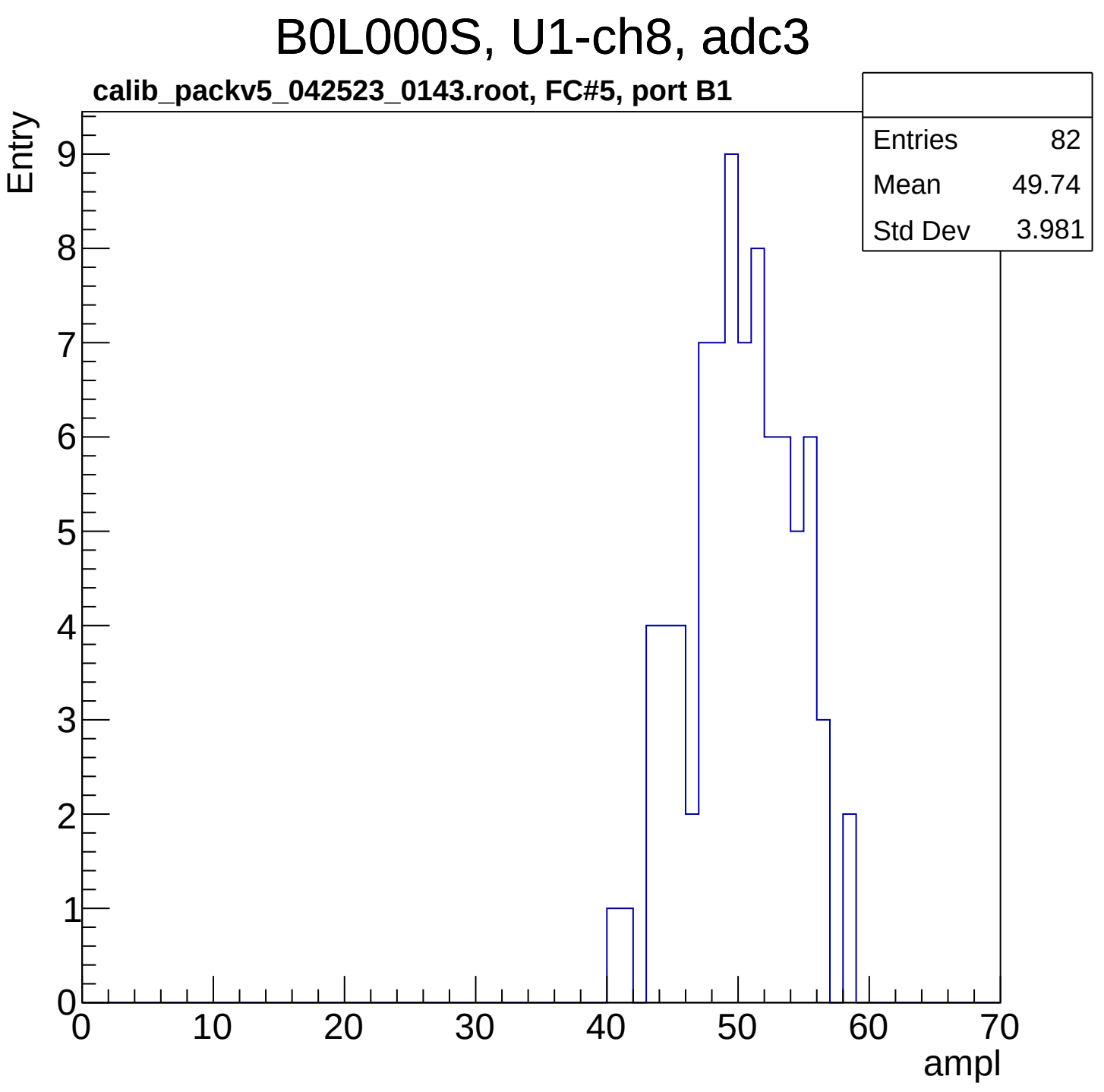
B0L000S, U1-ch8, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 49.74
Std Dev 3.981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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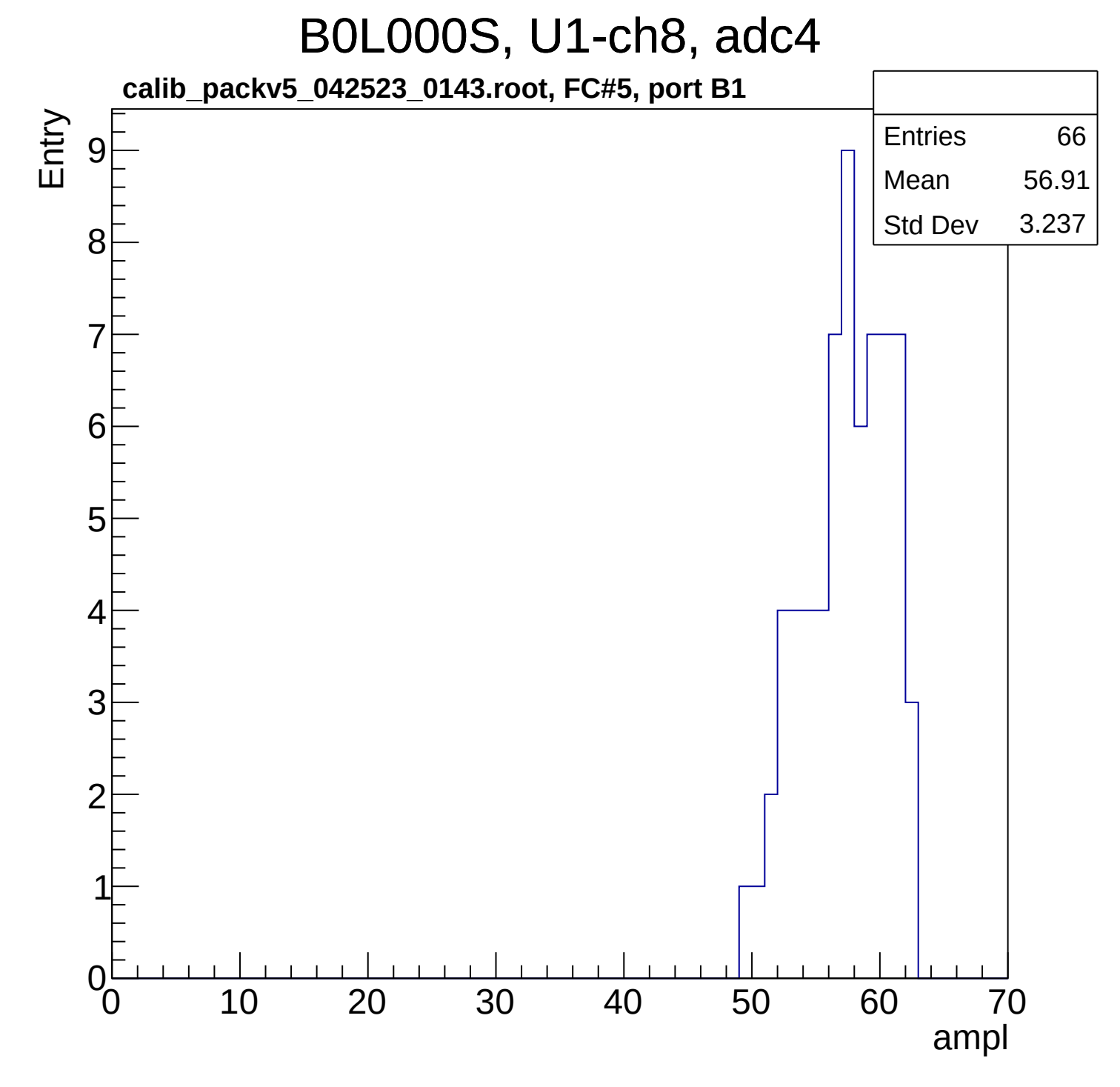
B0L000S, U1-ch8, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 56.91
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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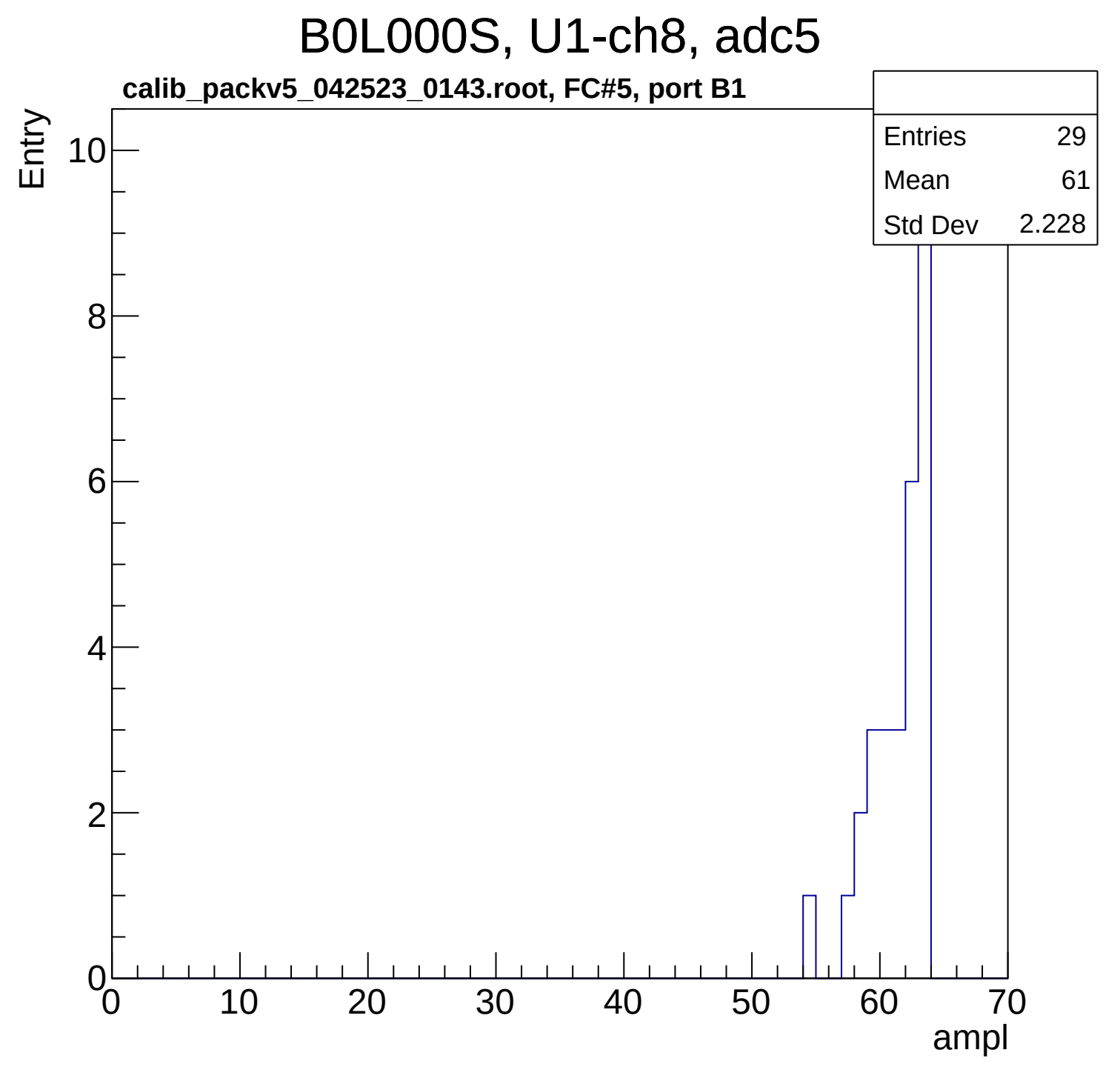
B0L000S, U1-ch8, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 61
Std Dev 2.228
Entry
ampl
0
2
4
6
8
10
0
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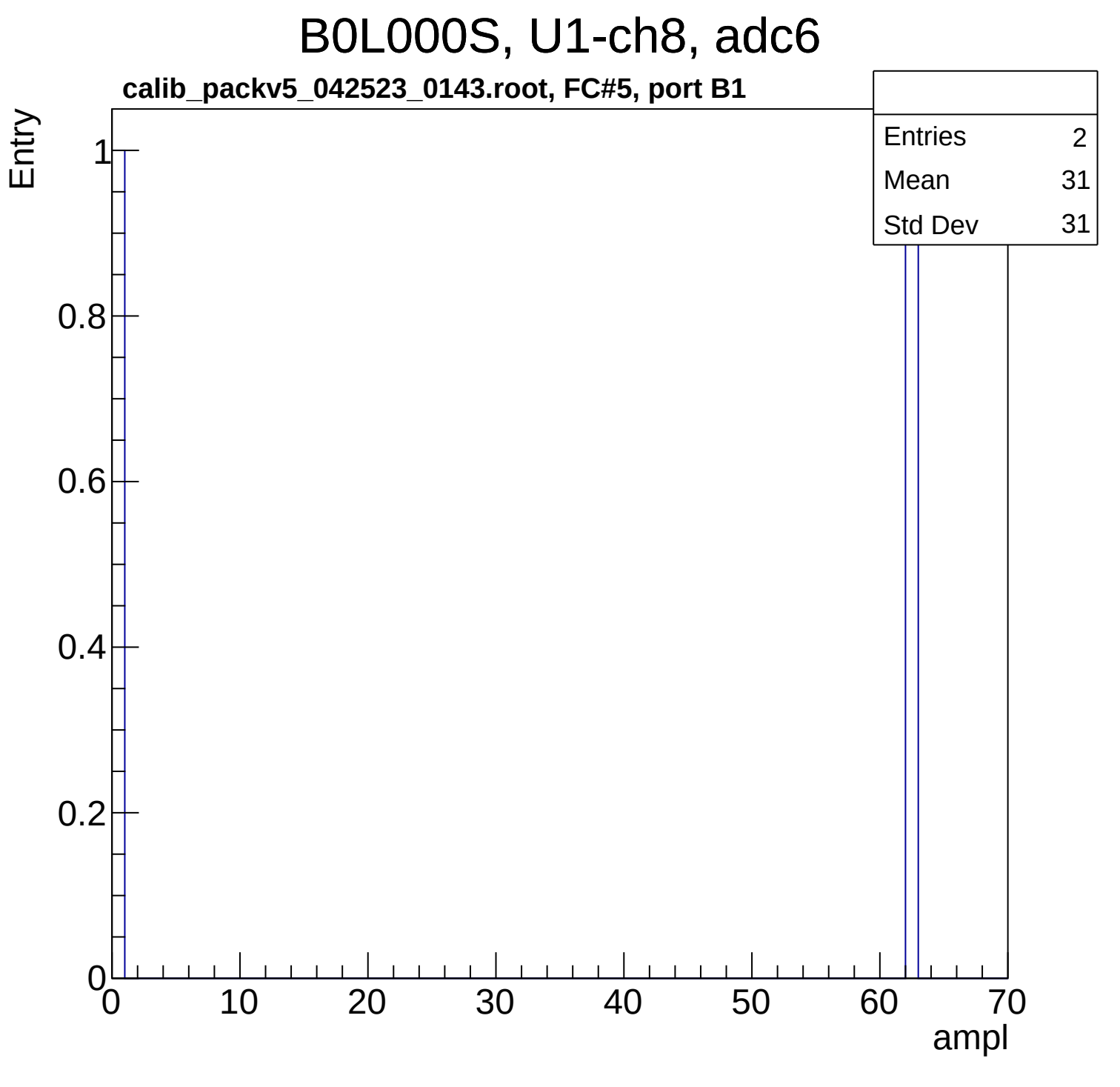
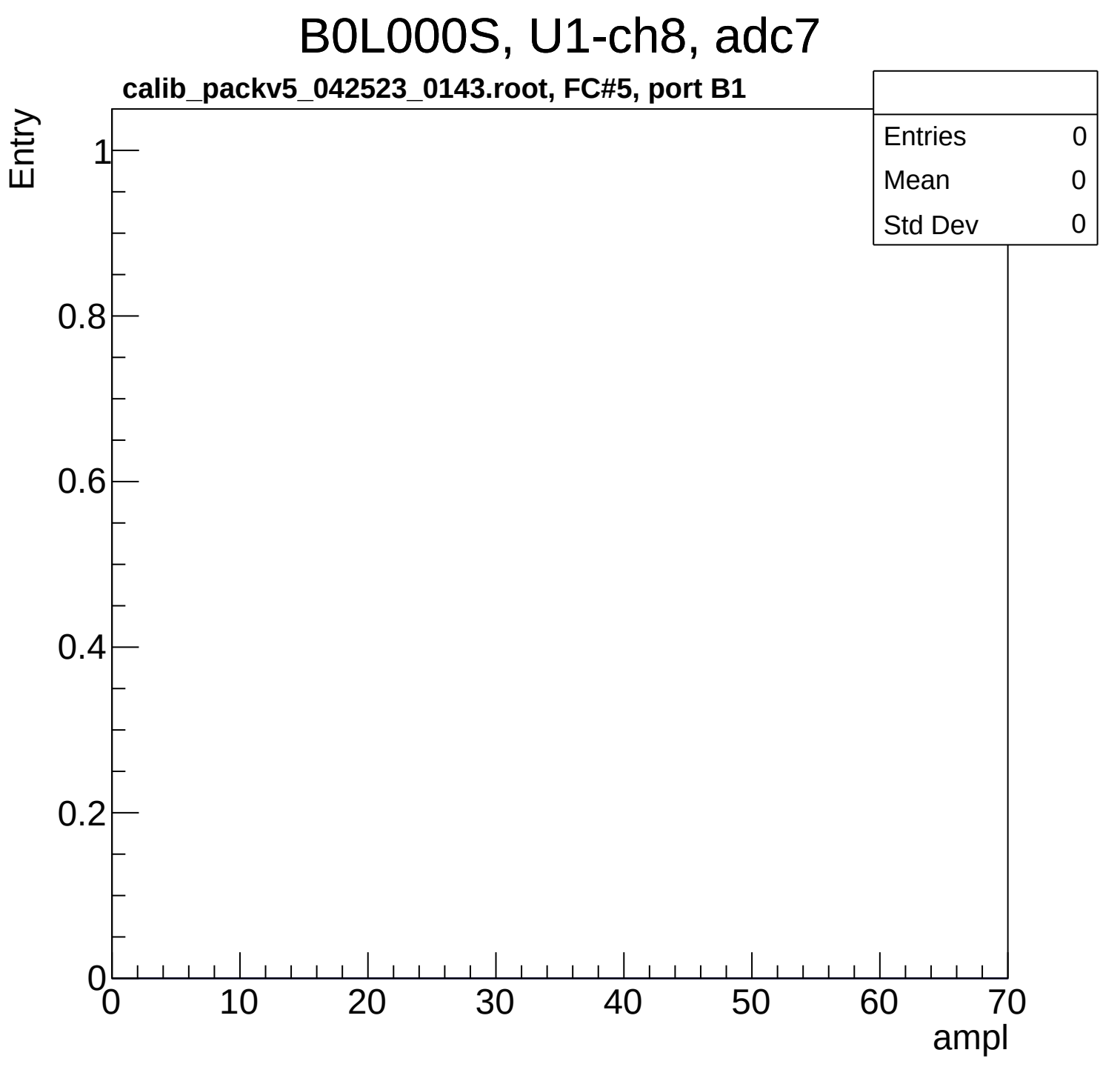
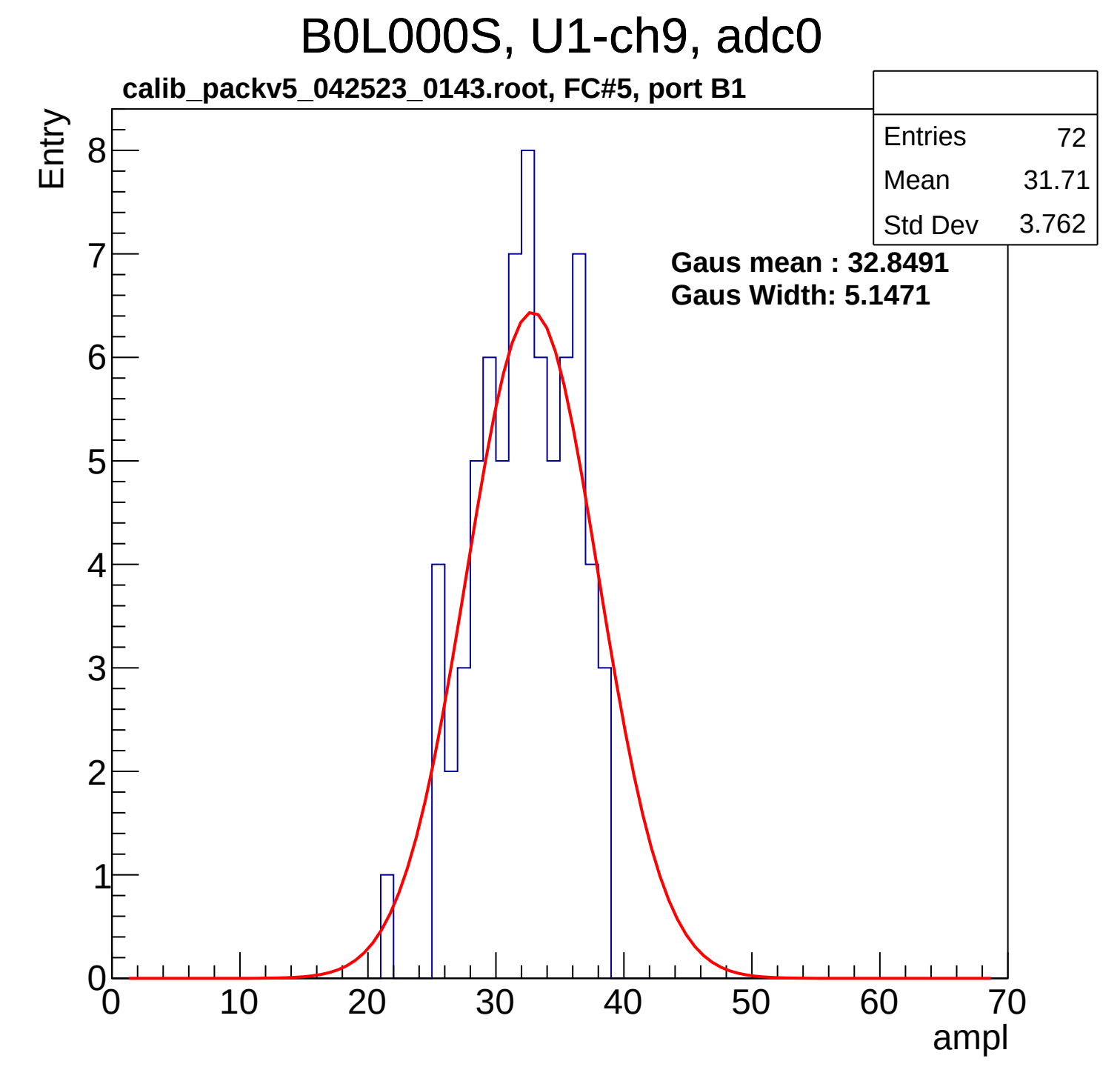
20
30
40
50
60
70


B0L000S, U1-ch8, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch8, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch9, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 31.71
Std Dev 3.762
Gaus mean : 32.8491
Gaus Width: 5.1471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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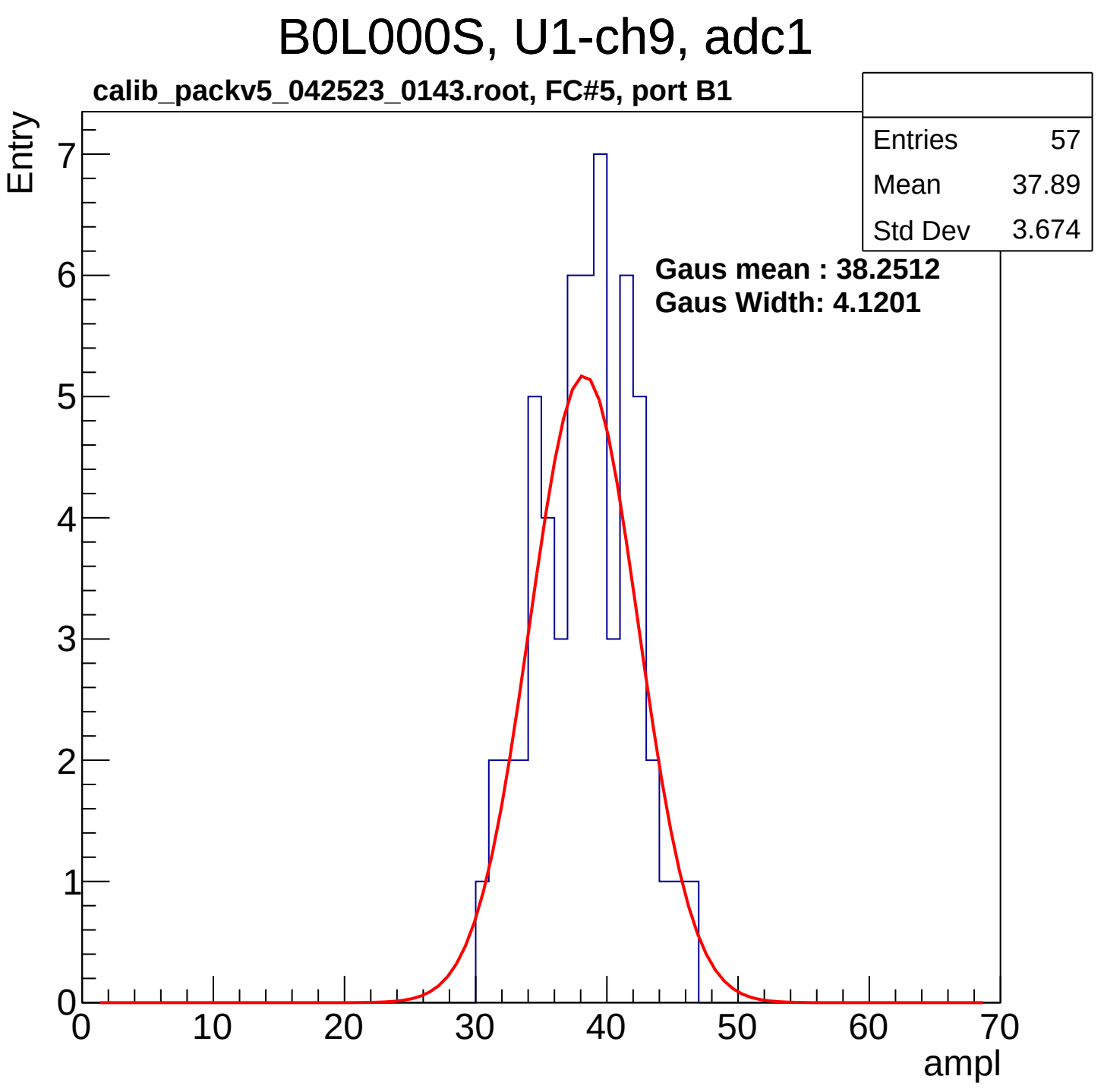
B0L000S, U1-ch9, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 37.89
Std Dev 3.674
Gaus mean : 38.2512
Gaus Width: 4.1201
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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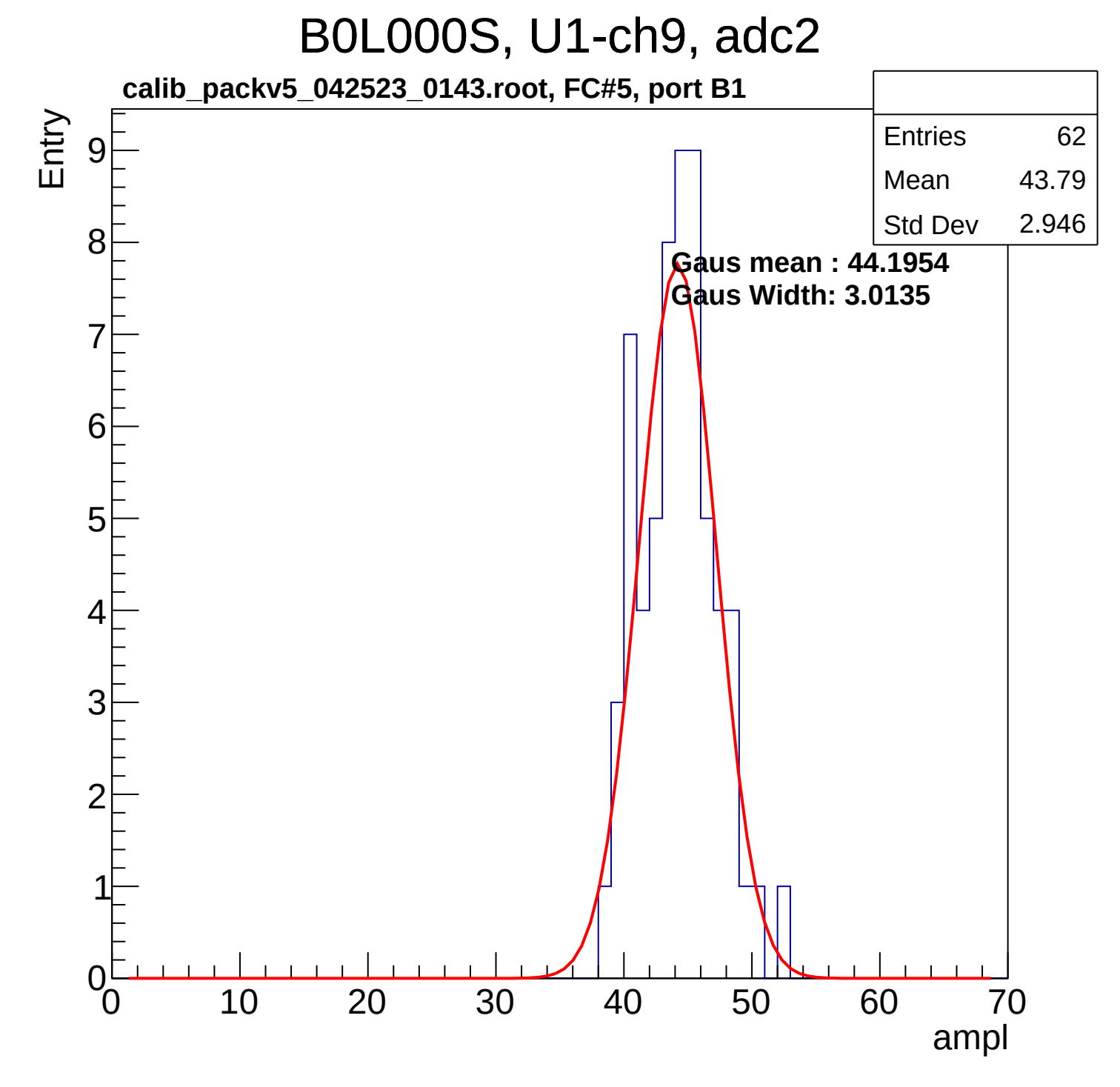
B0L000S, U1-ch9, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.79
Std Dev 2.946
Gaus mean : 44.1954
Gaus Width: 3.0135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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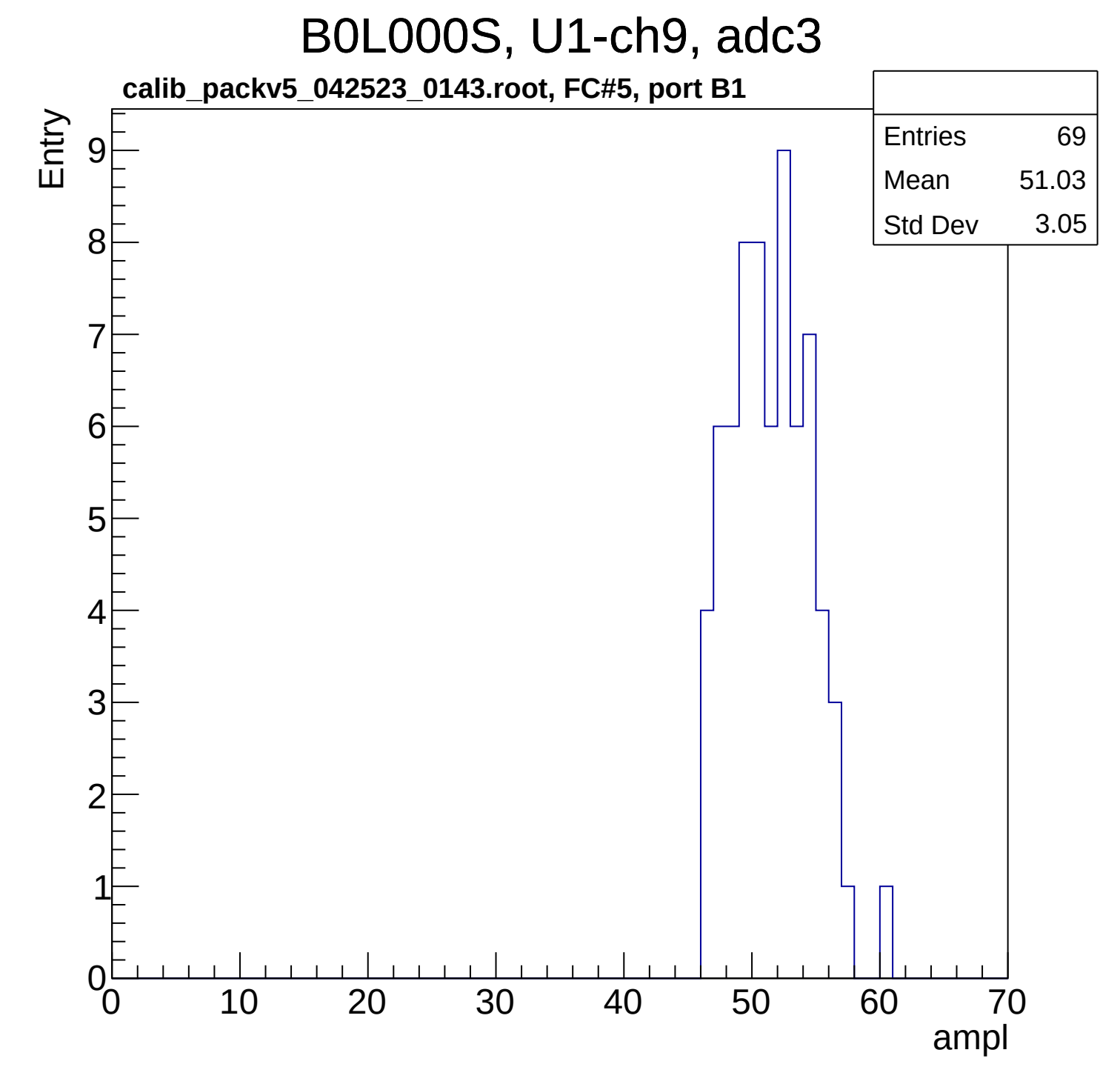
B0L000S, U1-ch9, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 51.03
Std Dev 3.05
Entry
ampl
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2
3
4
5
6
7
8
9
0
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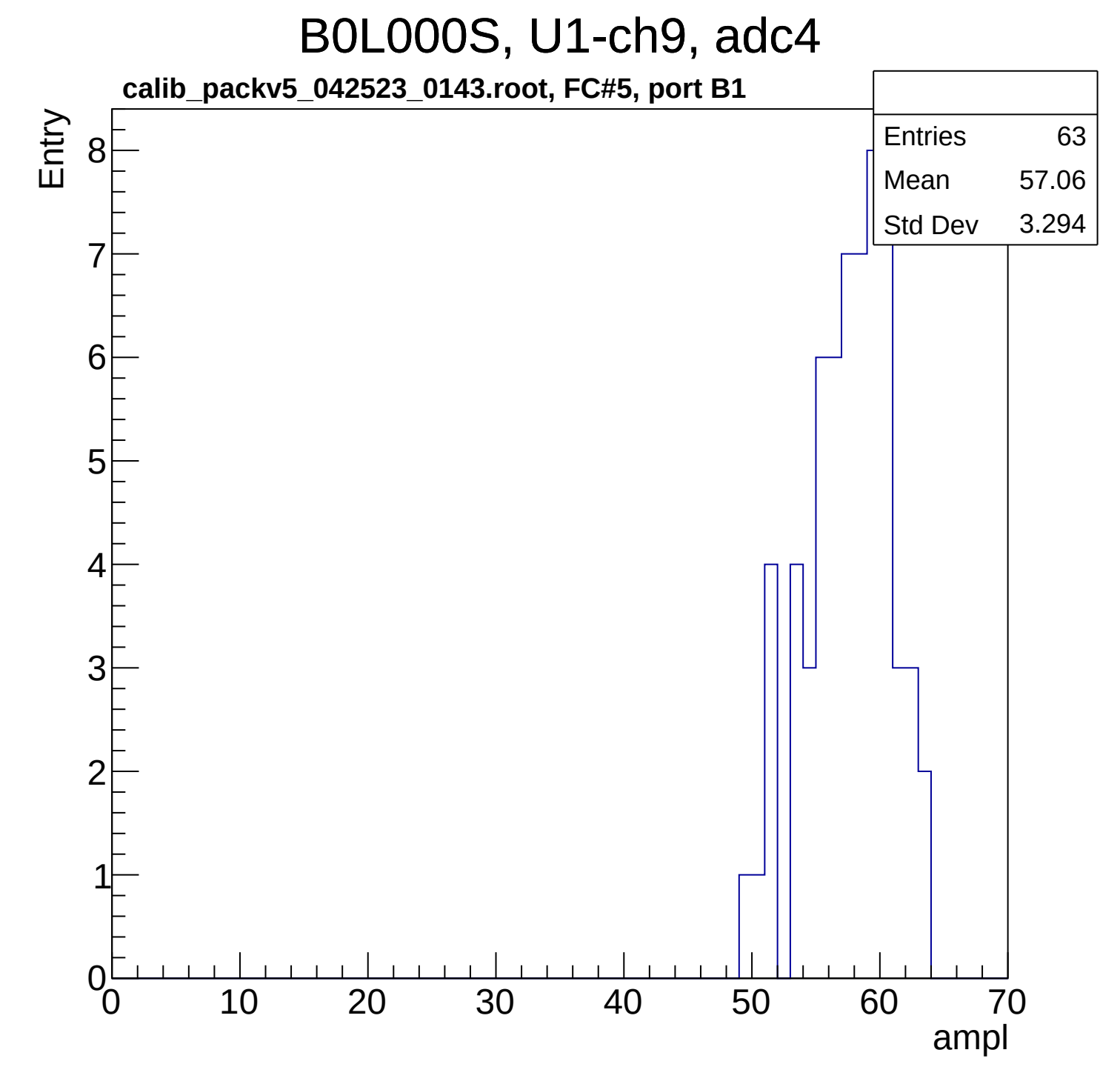
B0L000S, U1-ch9, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 57.06
Std Dev 3.294
Entry
ampl
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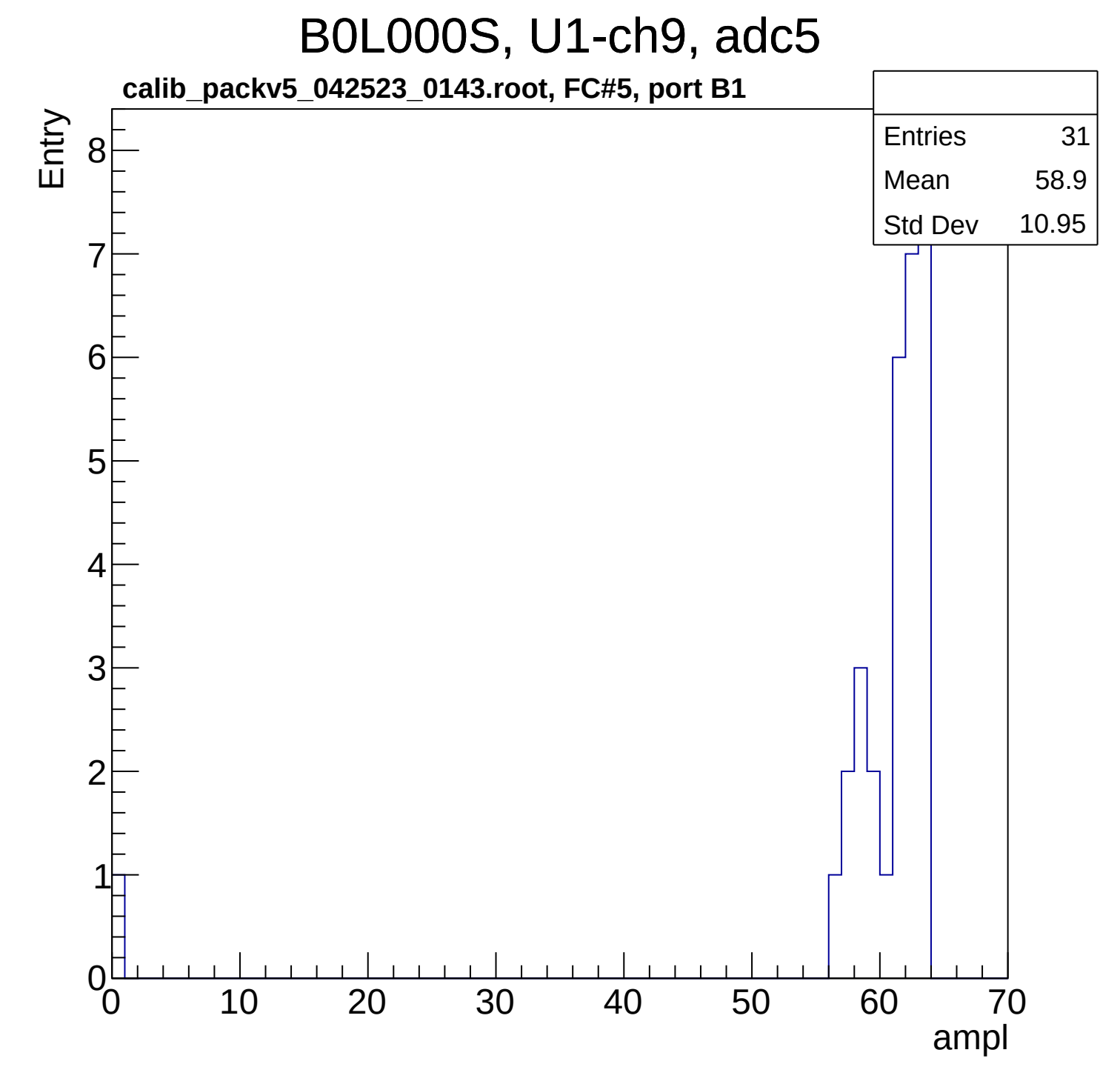
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch9, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 58.9
Std Dev 10.95
Entry
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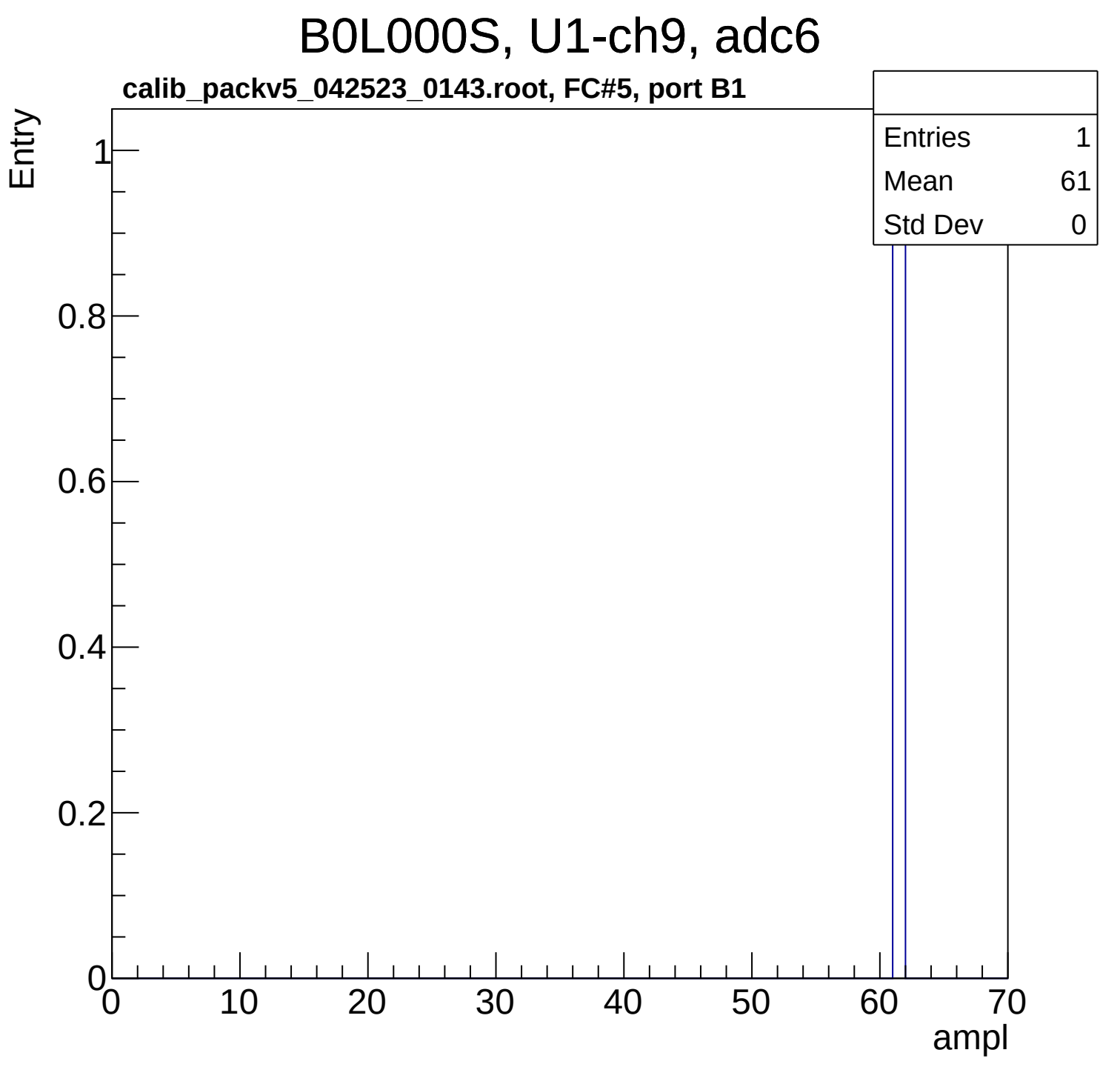
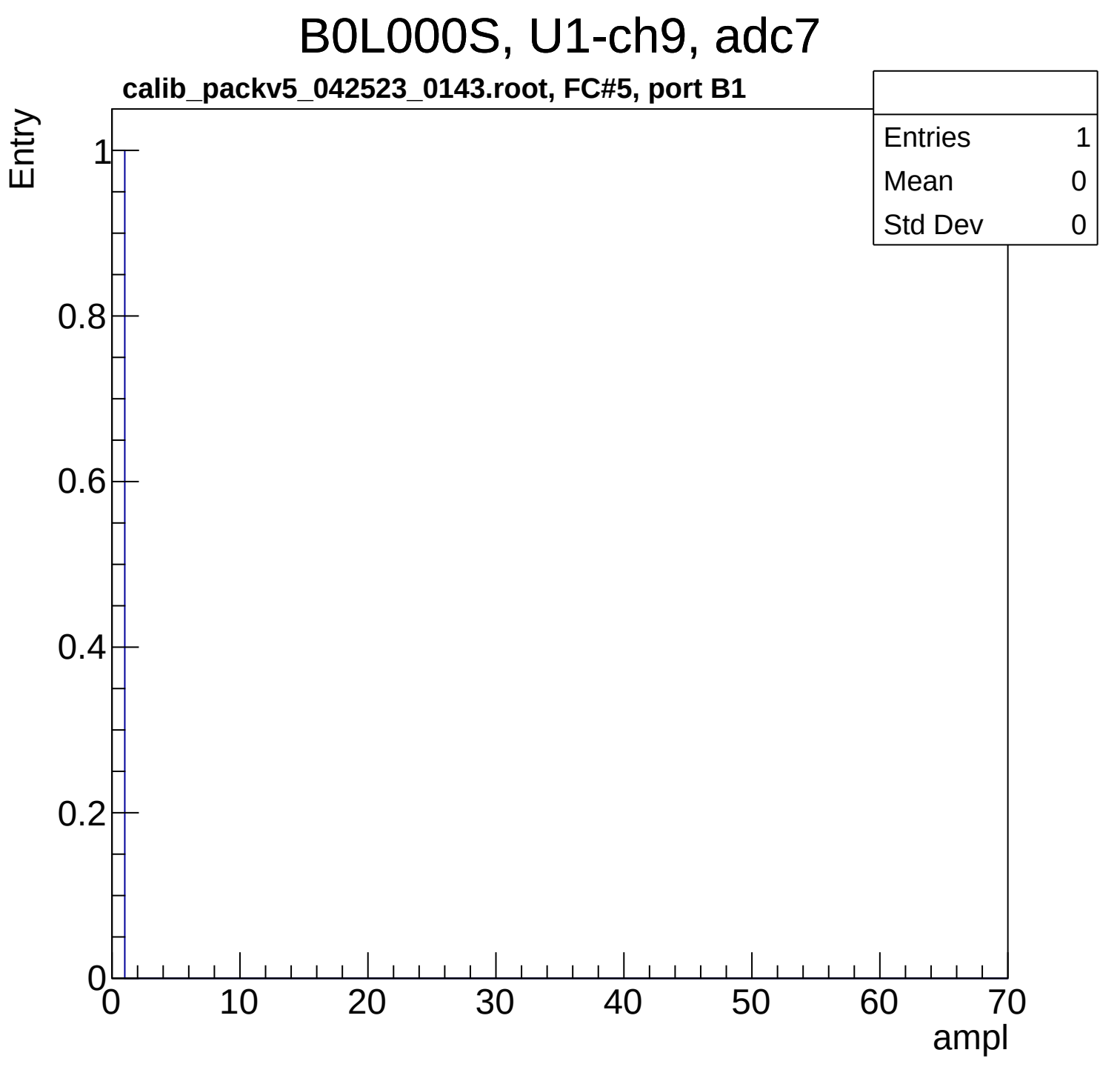
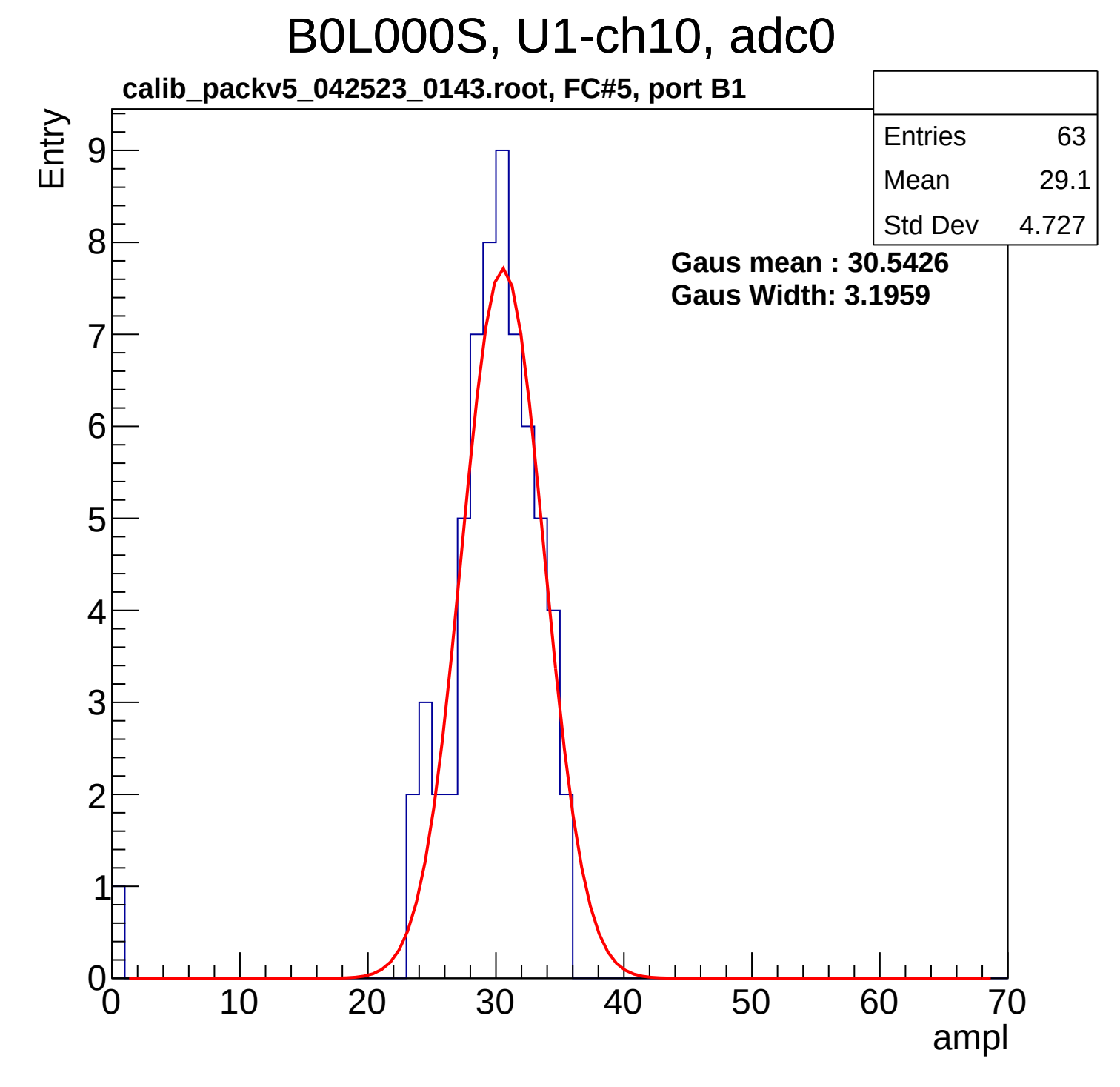
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch9, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch9, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch10, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.1
Std Dev 4.727
Gaus mean : 30.5426
Gaus Width: 3.1959
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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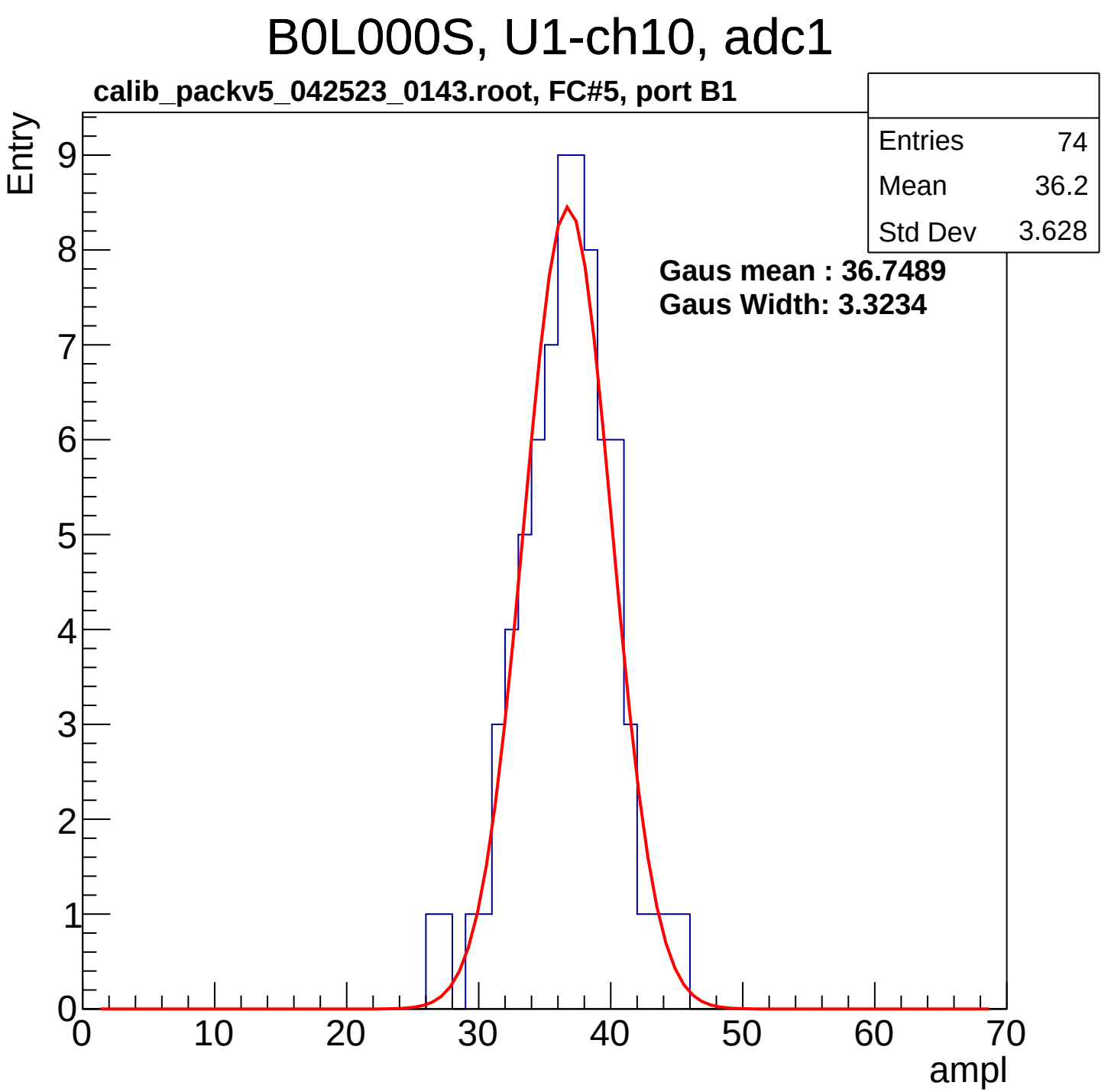
B0L000S, U1-ch10, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 36.2
Std Dev 3.628
Gaus mean : 36.7489
Gaus Width: 3.3234
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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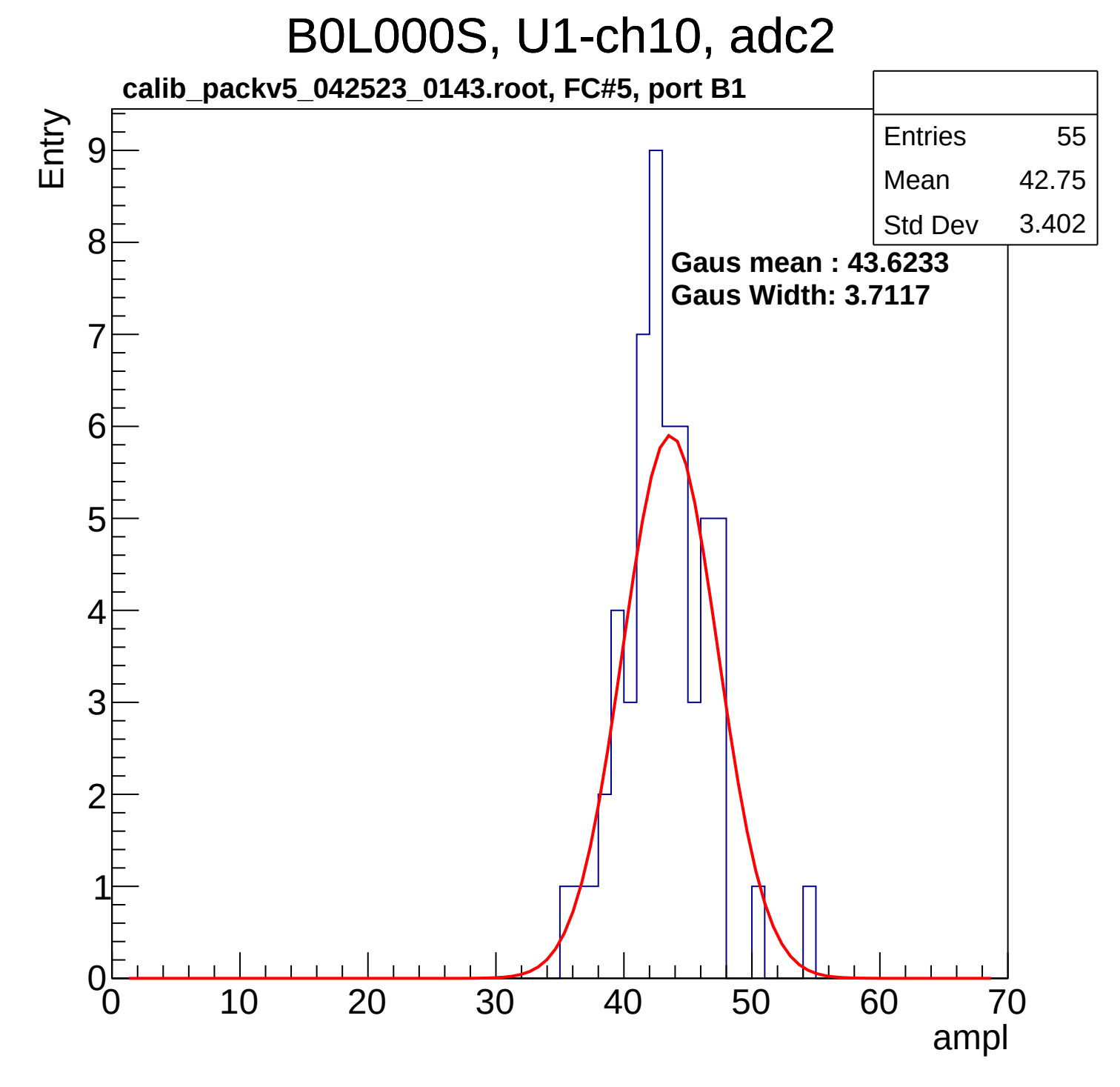
B0L000S, U1-ch10, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 42.75
Std Dev 3.402
Gaus mean : 43.6233
Gaus Width: 3.7117
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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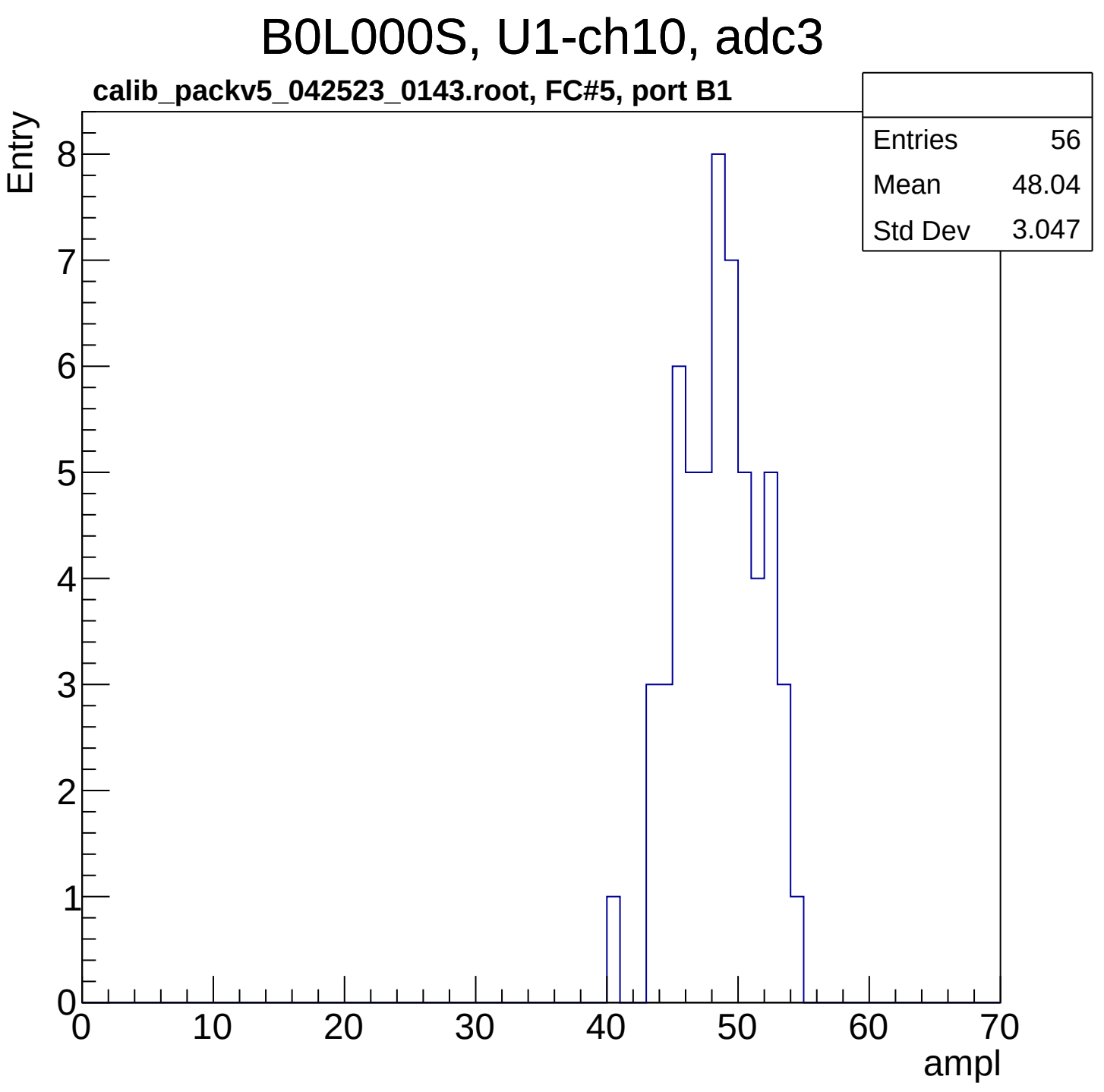
B0L000S, U1-ch10, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 48.04
Std Dev 3.047
Entry
ampl
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4
5
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0
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40
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60
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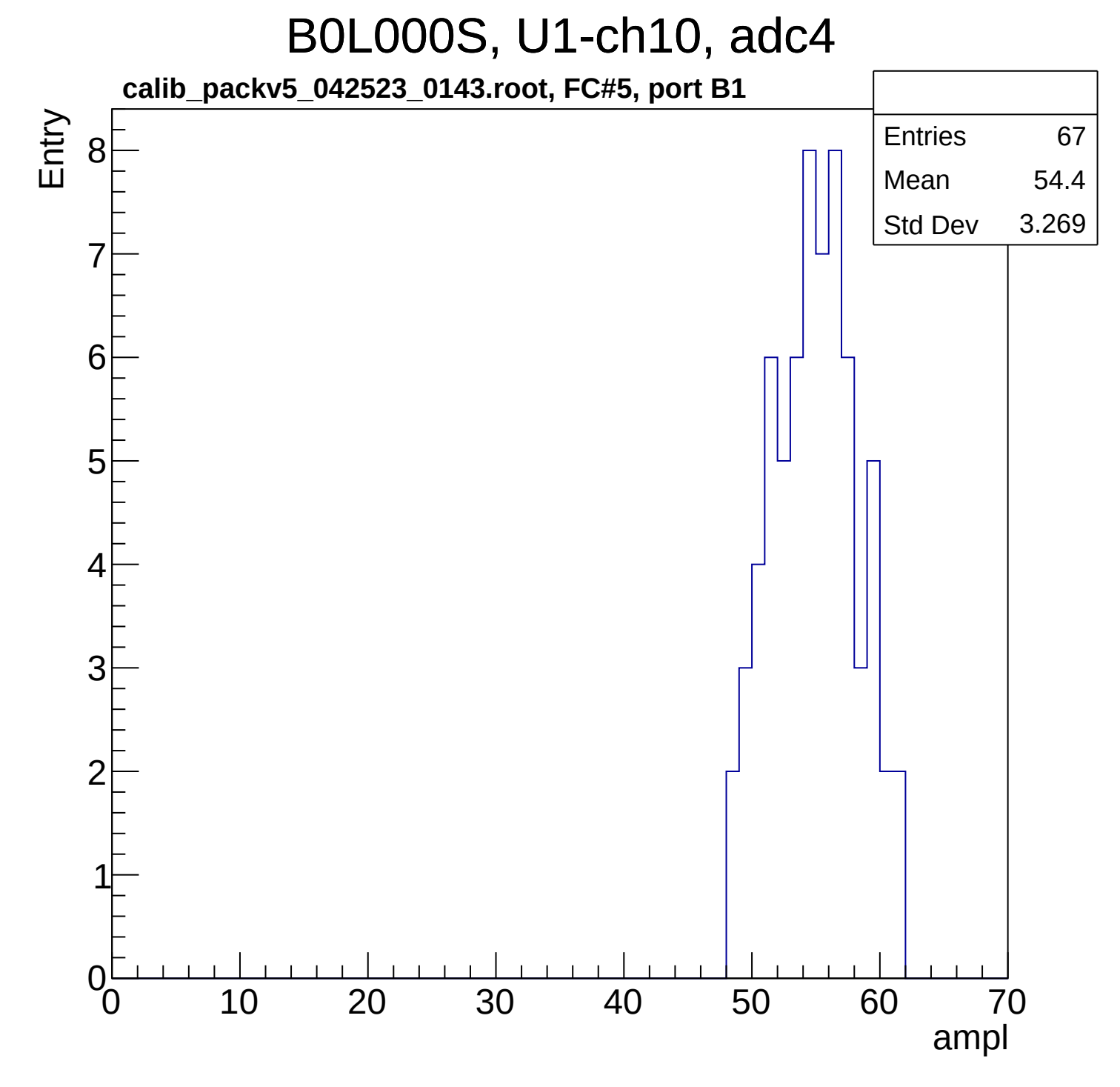
B0L000S, U1-ch10, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 54.4
Std Dev 3.269
Entry
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3
4
5
6
7
8
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10
20
30
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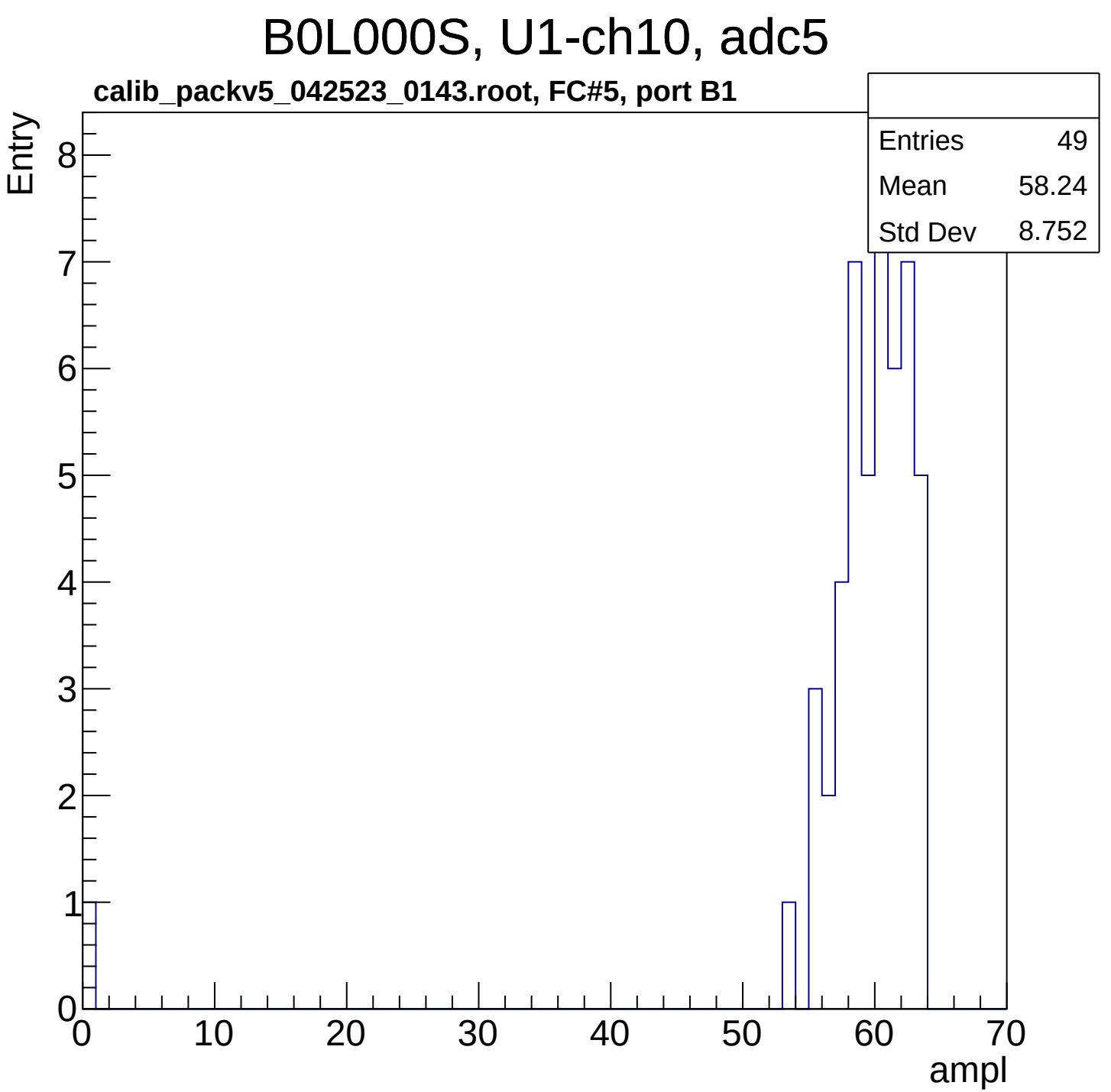
50
60
70
ampl


B0L000S, U1-ch10, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.24
Std Dev 8.752
Entry
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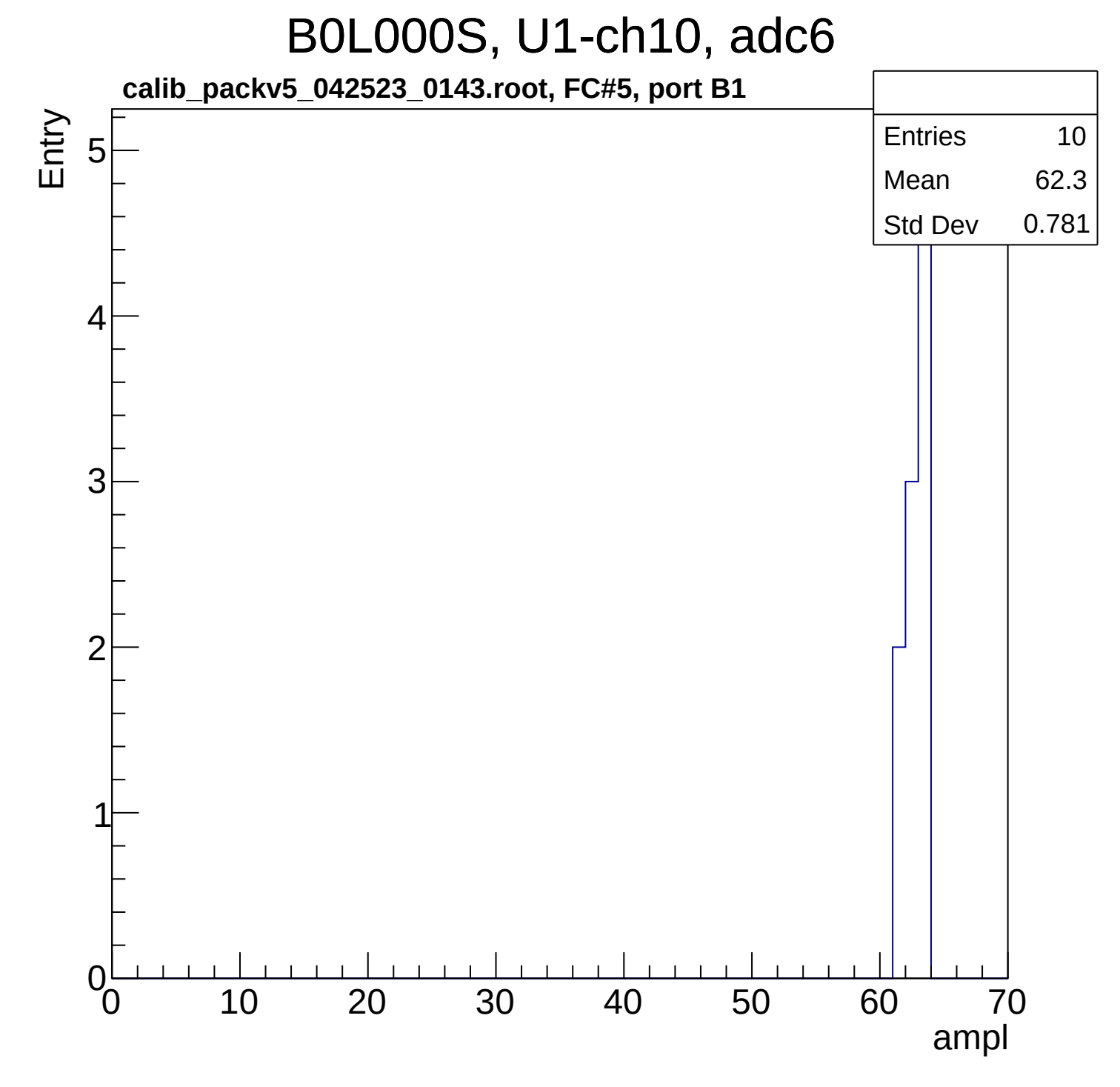
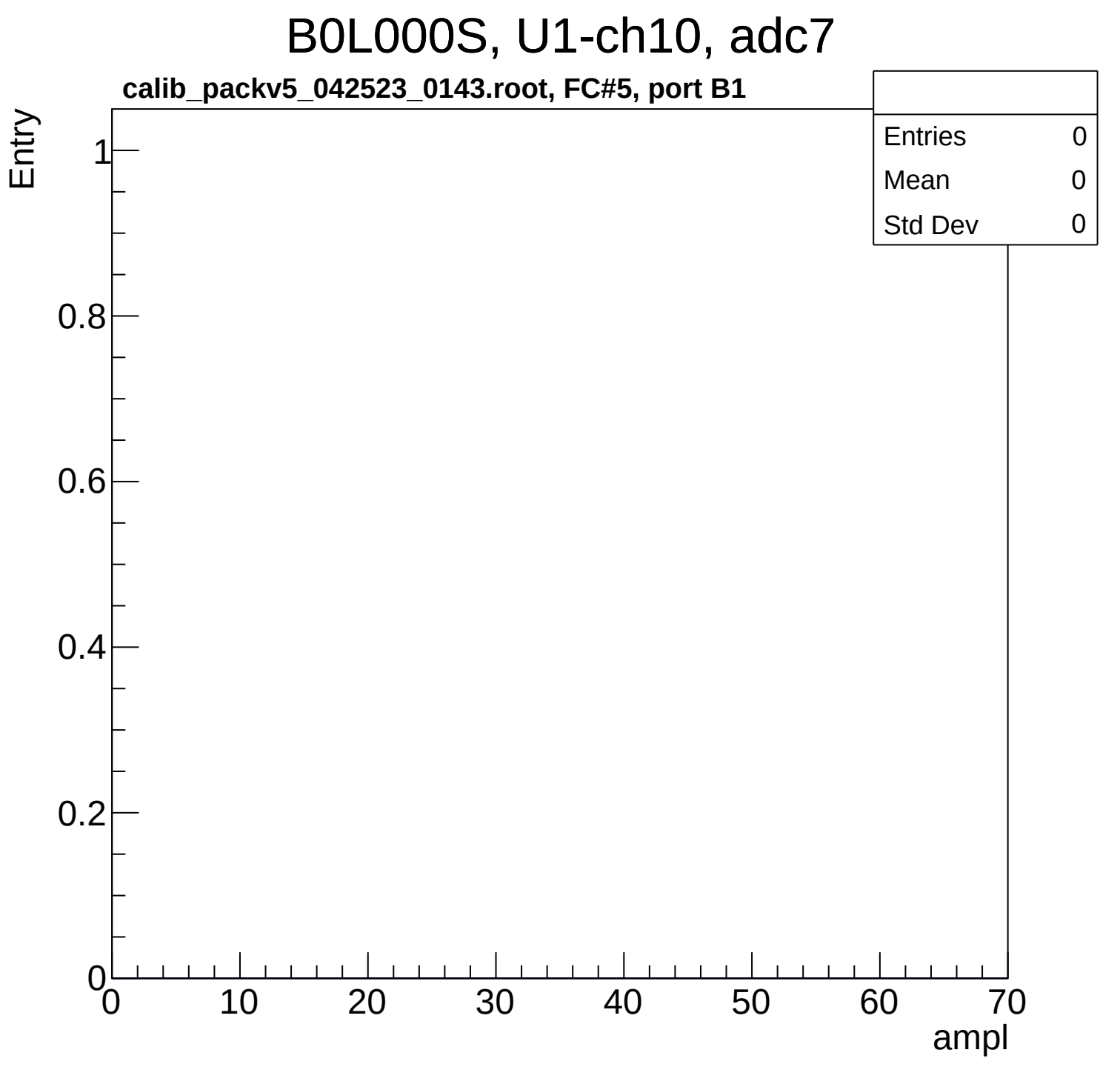
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch10, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 62.3
Std Dev 0.781
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch10, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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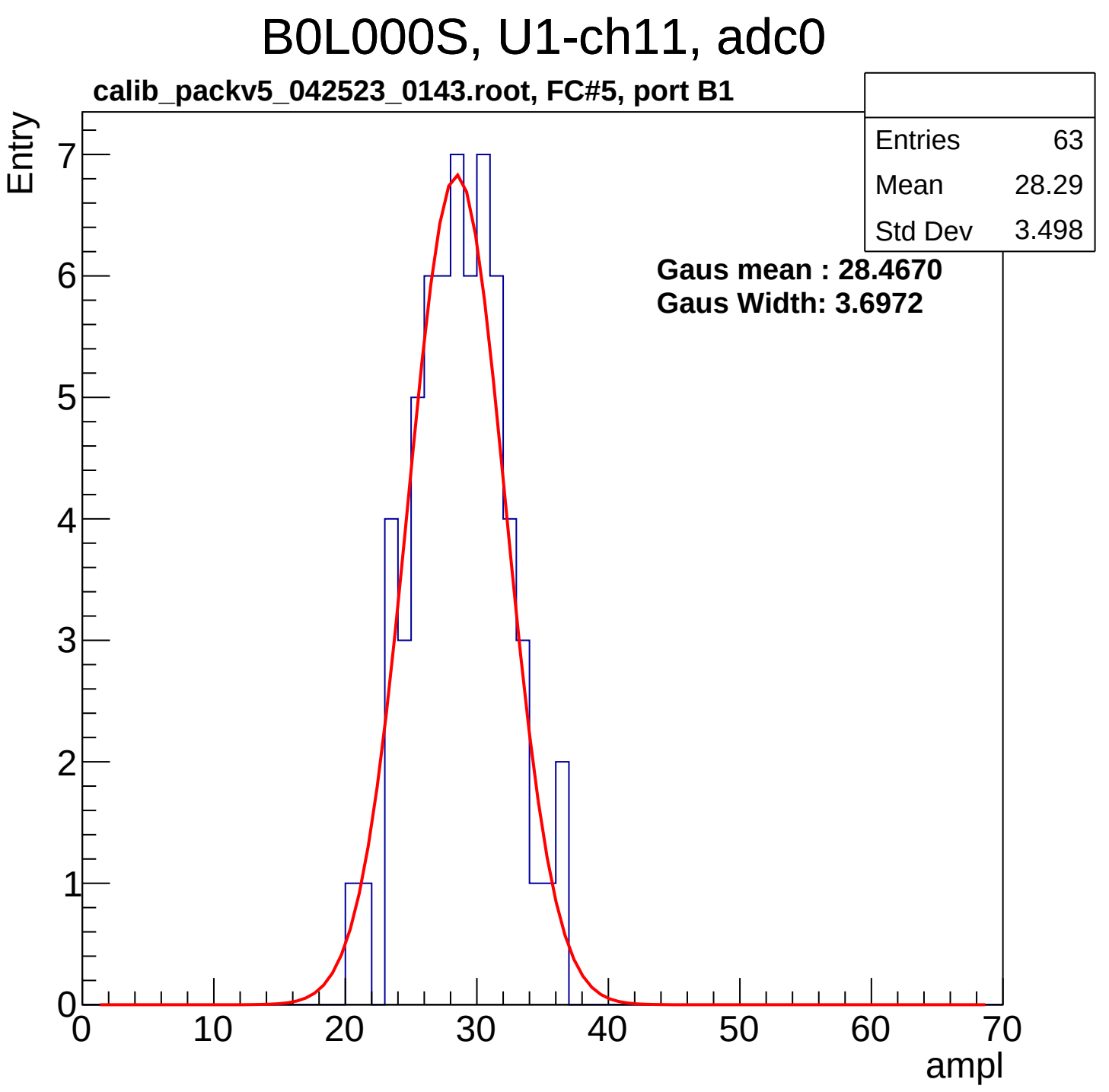
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch11, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 28.29
Std Dev 3.498
Gaus mean : 28.4670
Gaus Width: 3.6972
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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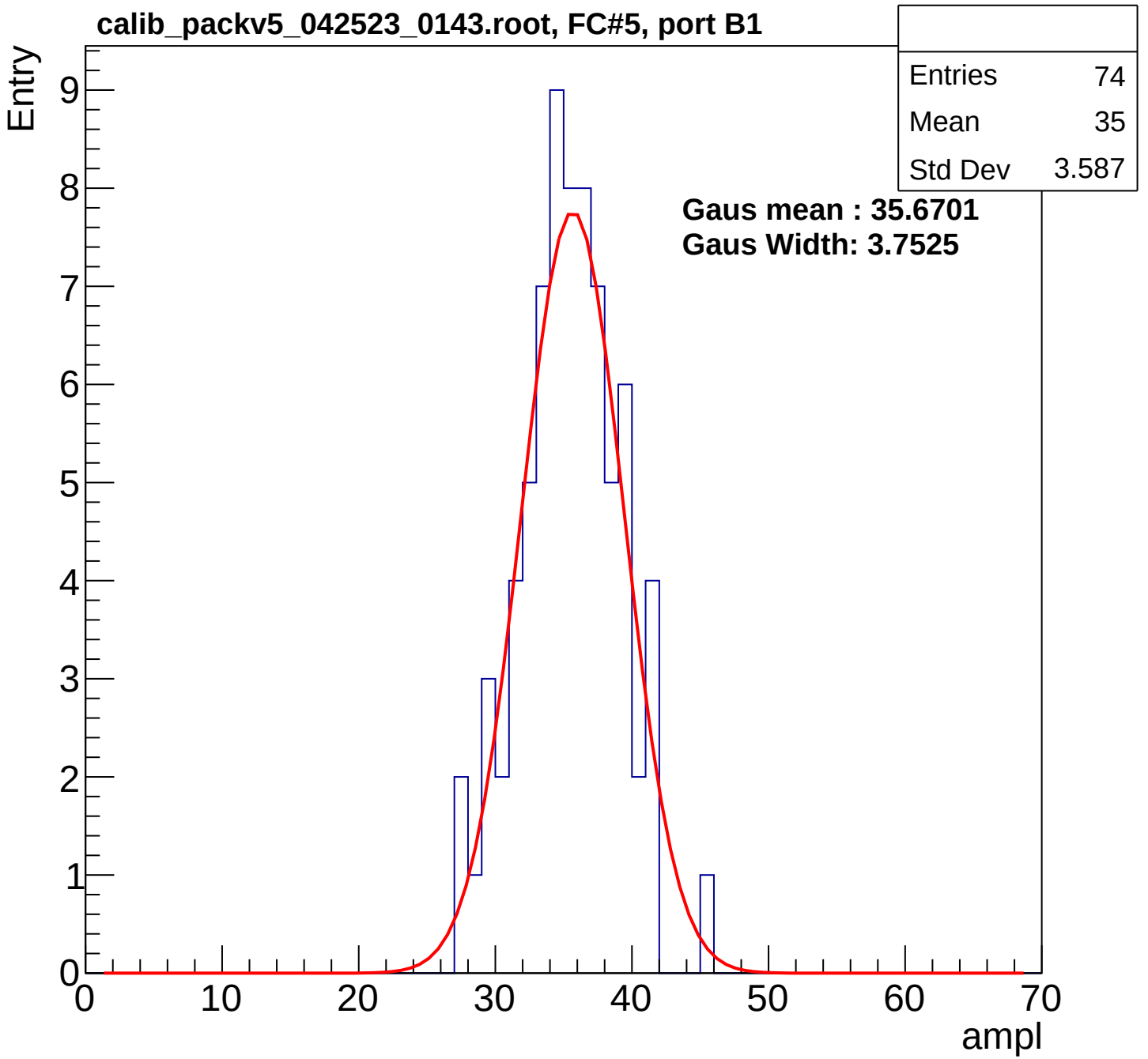
B0L000S, U1-ch11, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 35
Std Dev 3.587
Gaus mean : 35.6701
Gaus Width: 3.7525
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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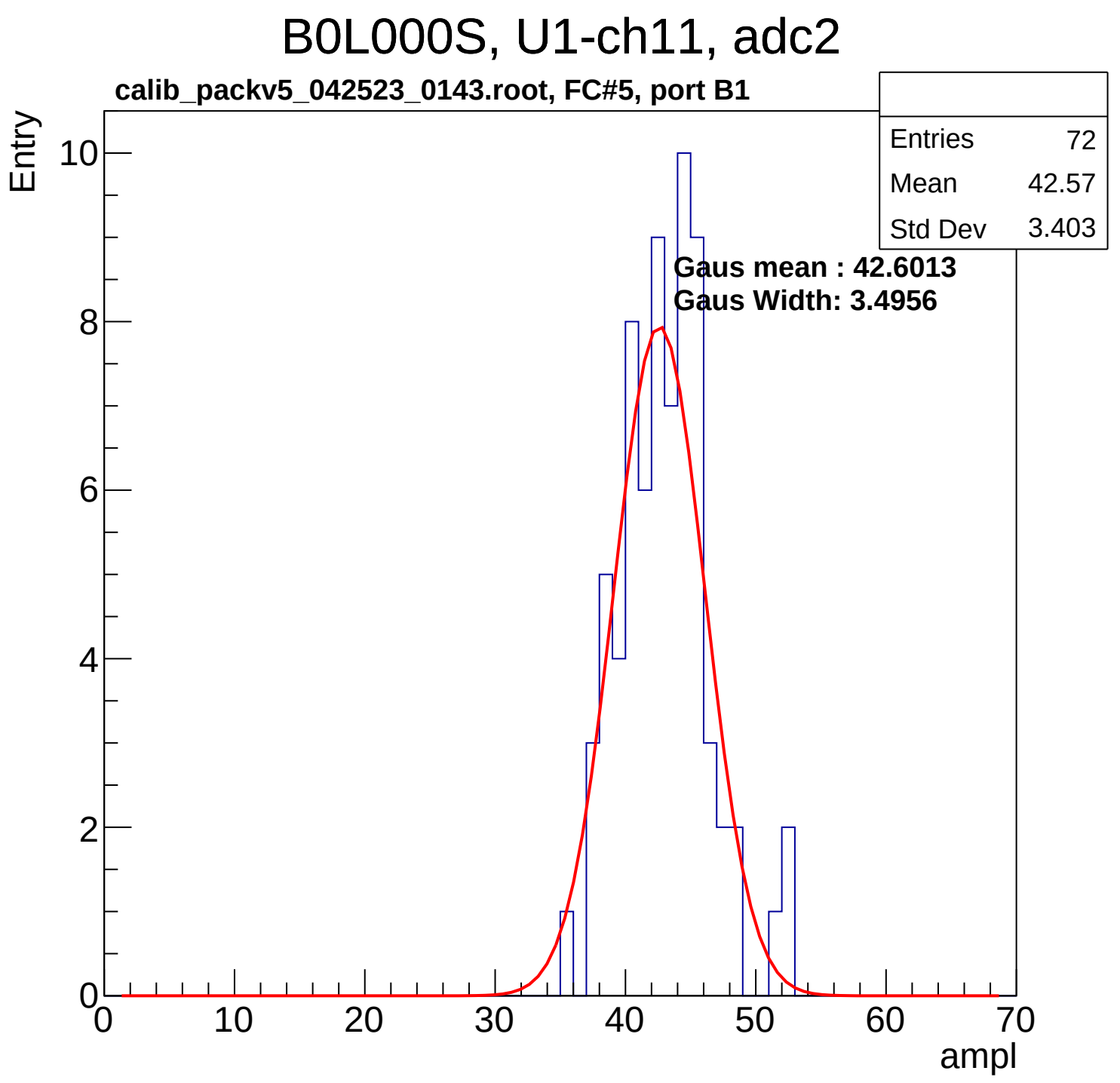
B0L000S, U1-ch11, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 42.57
Std Dev 3.403
Gaus mean : 42.6013
Gaus Width: 3.4956
Entry
ampl
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4
6
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10
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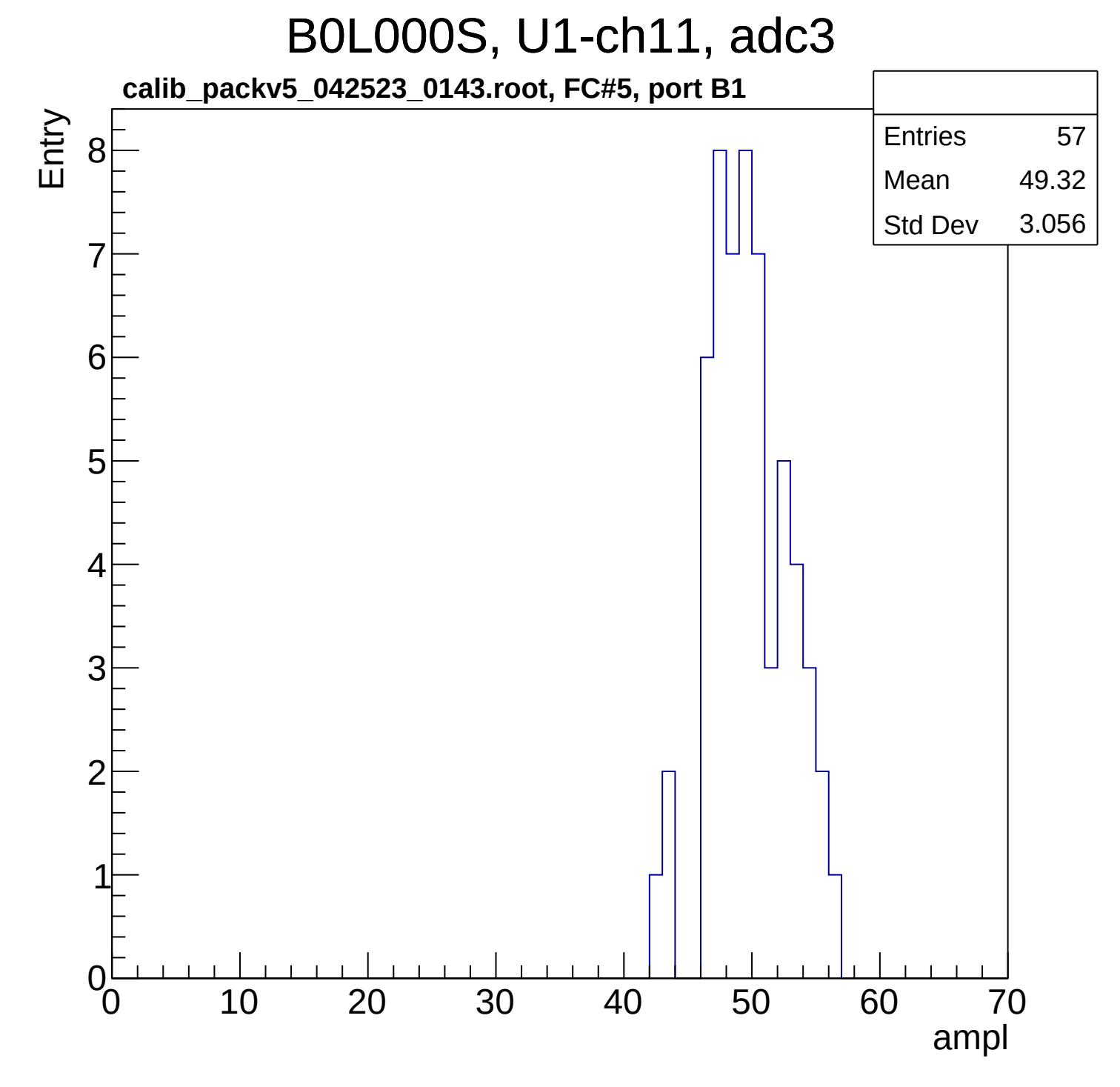
B0L000S, U1-ch11, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.32
Std Dev 3.056
Entry
ampl
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1
2
3
4
5
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7
8
0
10
20
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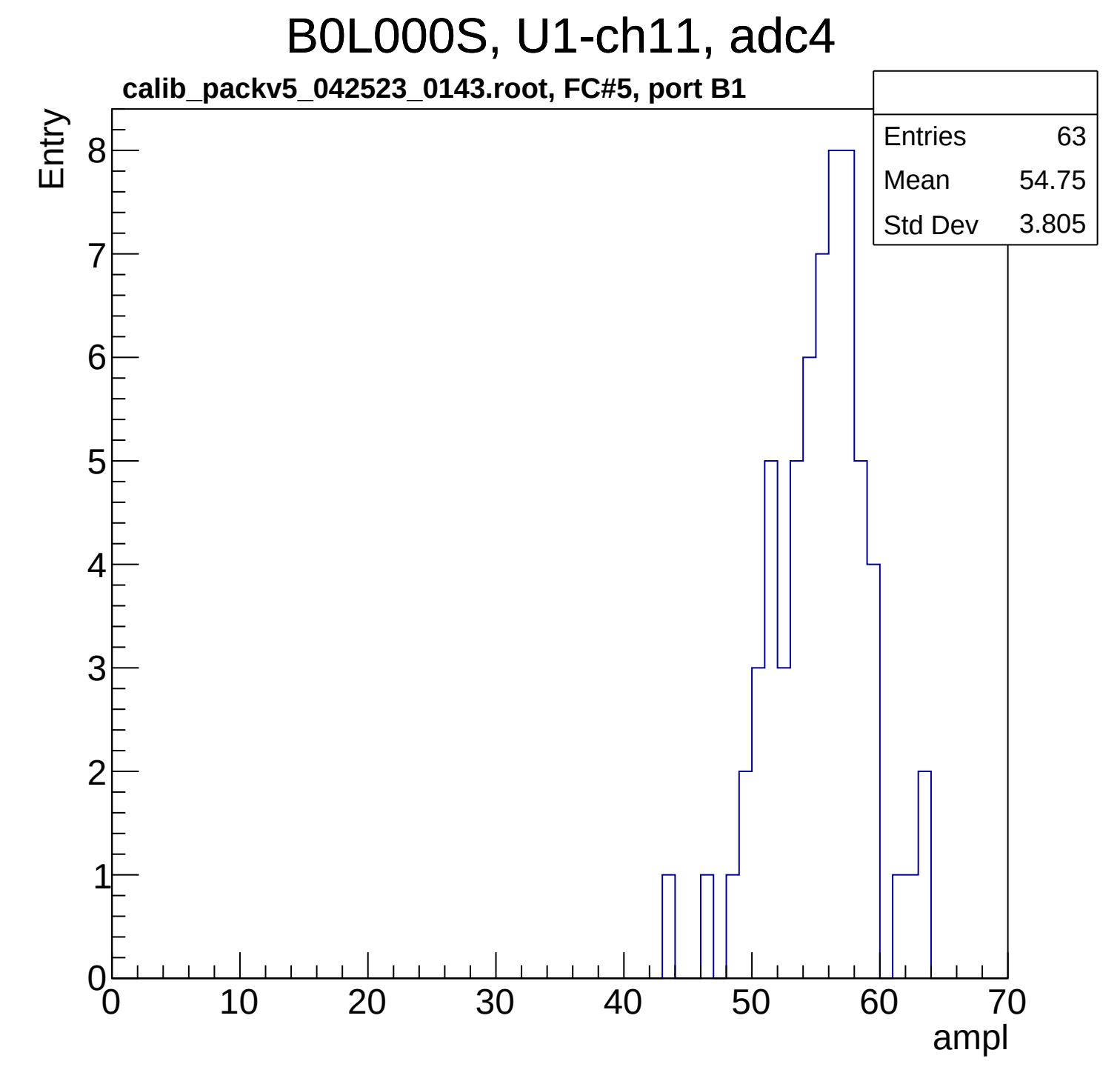
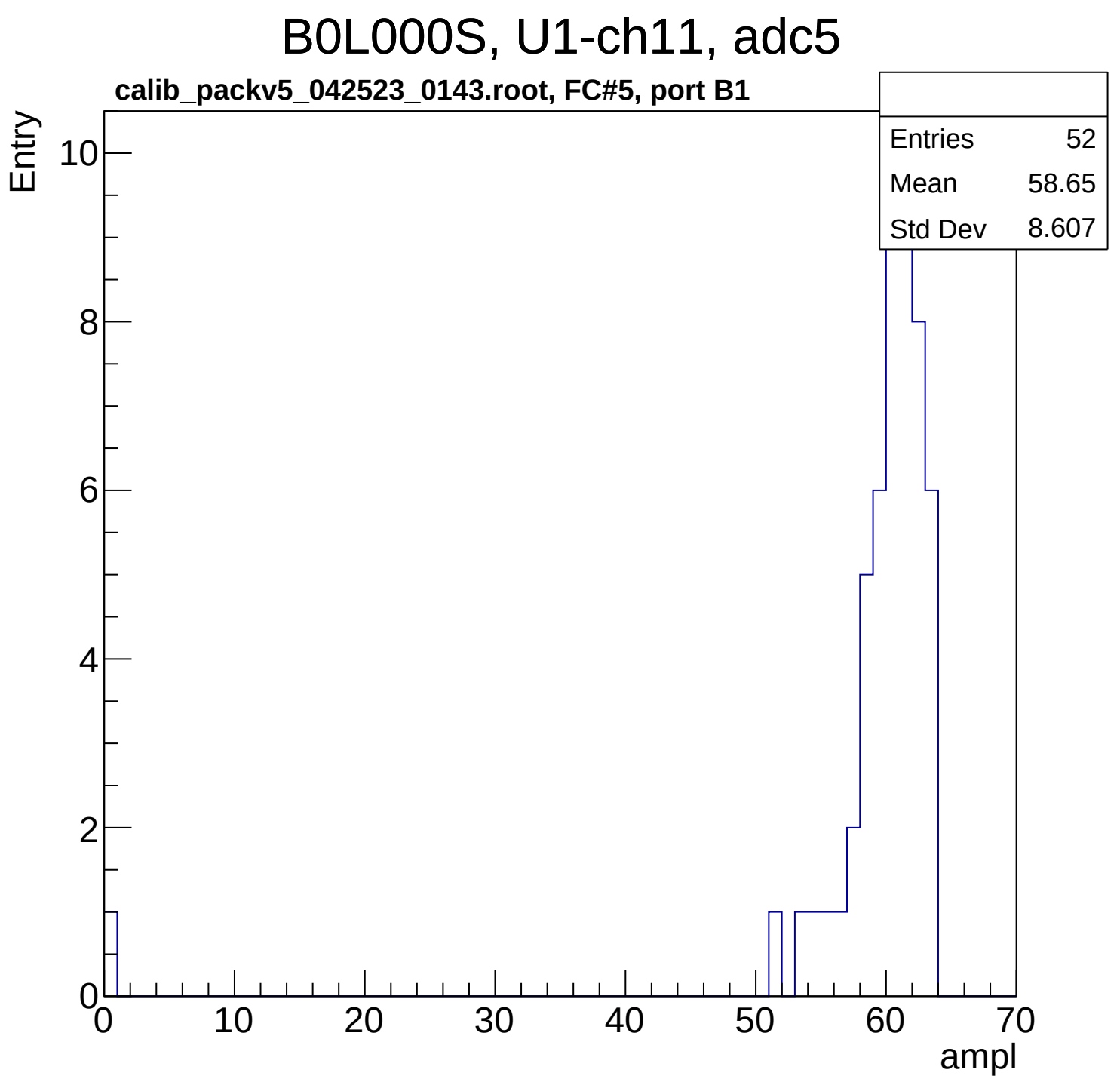

B0L000S, U1-ch11, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.75
Std Dev 3.805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch11, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.65
Std Dev 8.607
Entry
0
2
4
6
8
10
0
10
20
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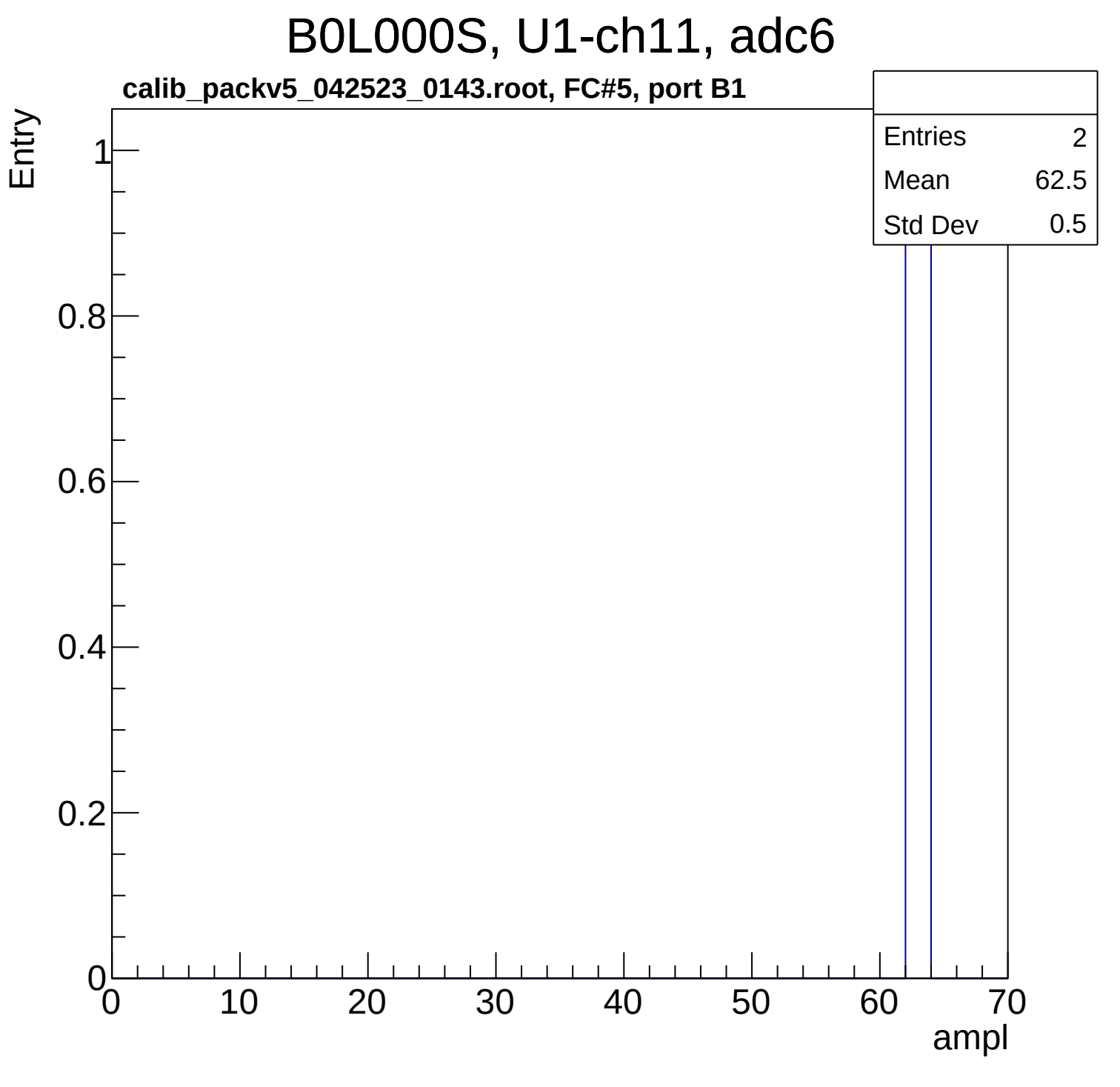
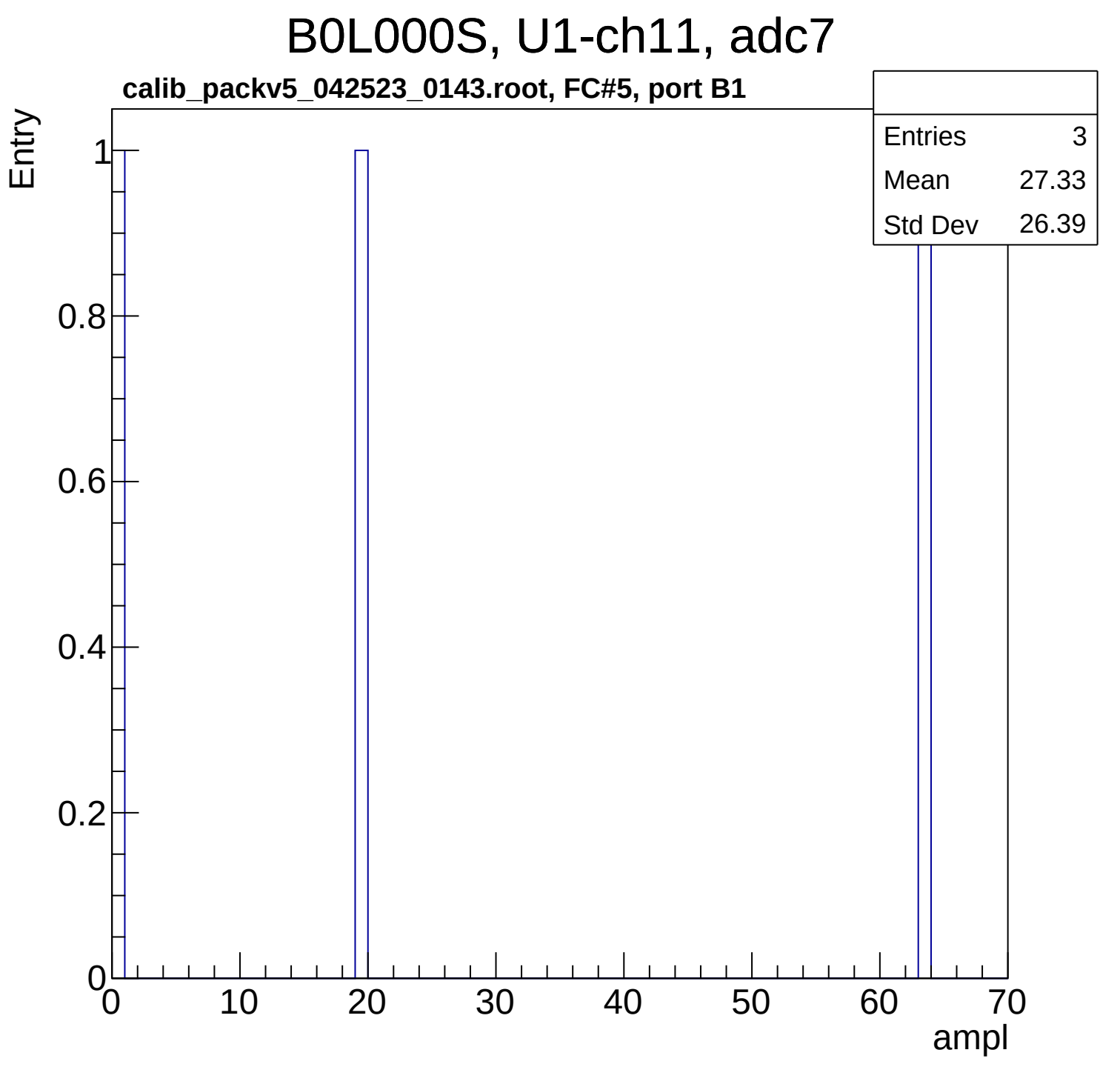
70
ampl


B0L000S, U1-ch11, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch11, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 27.33
Std Dev 26.39
Entry
ampl
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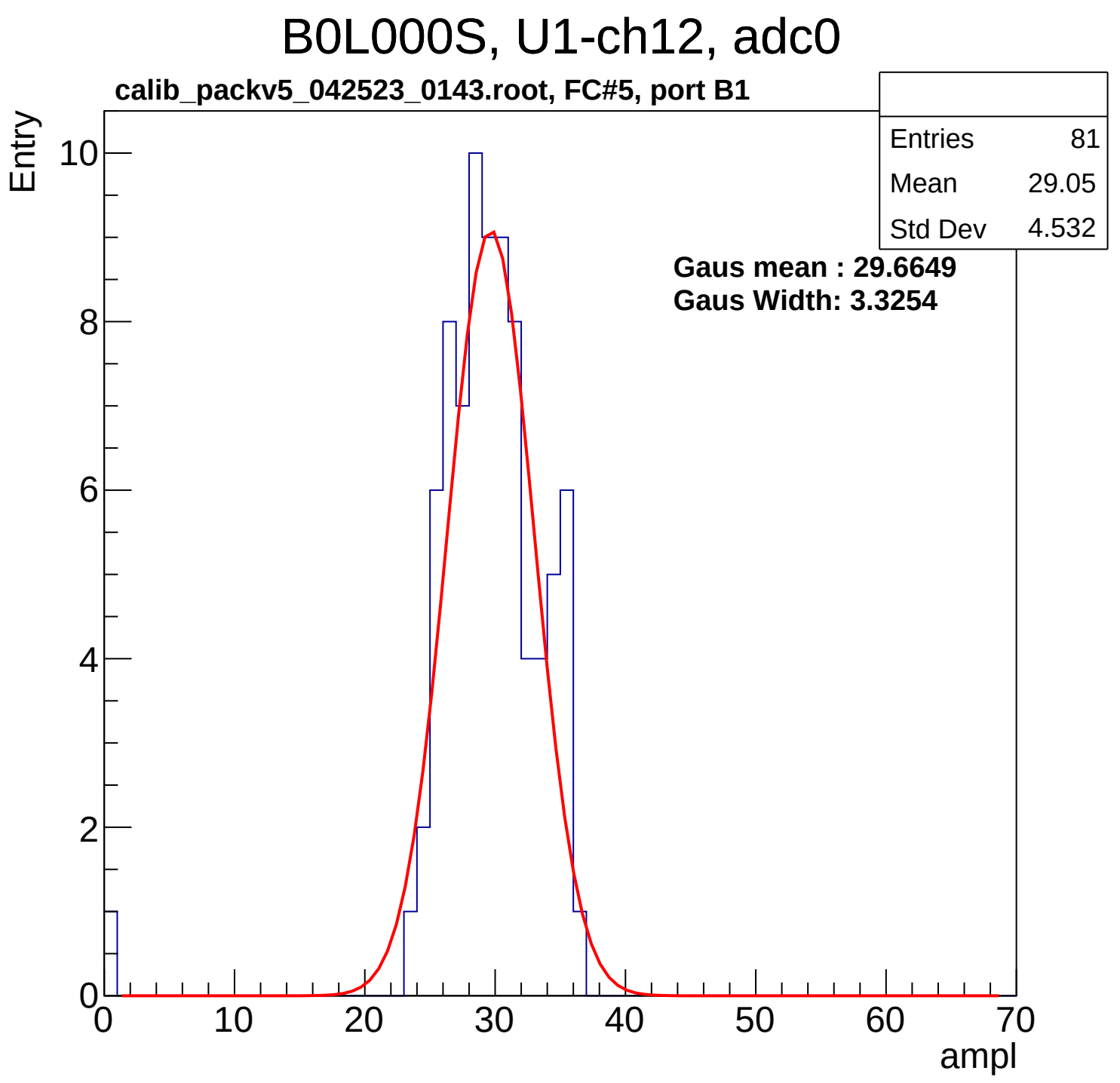
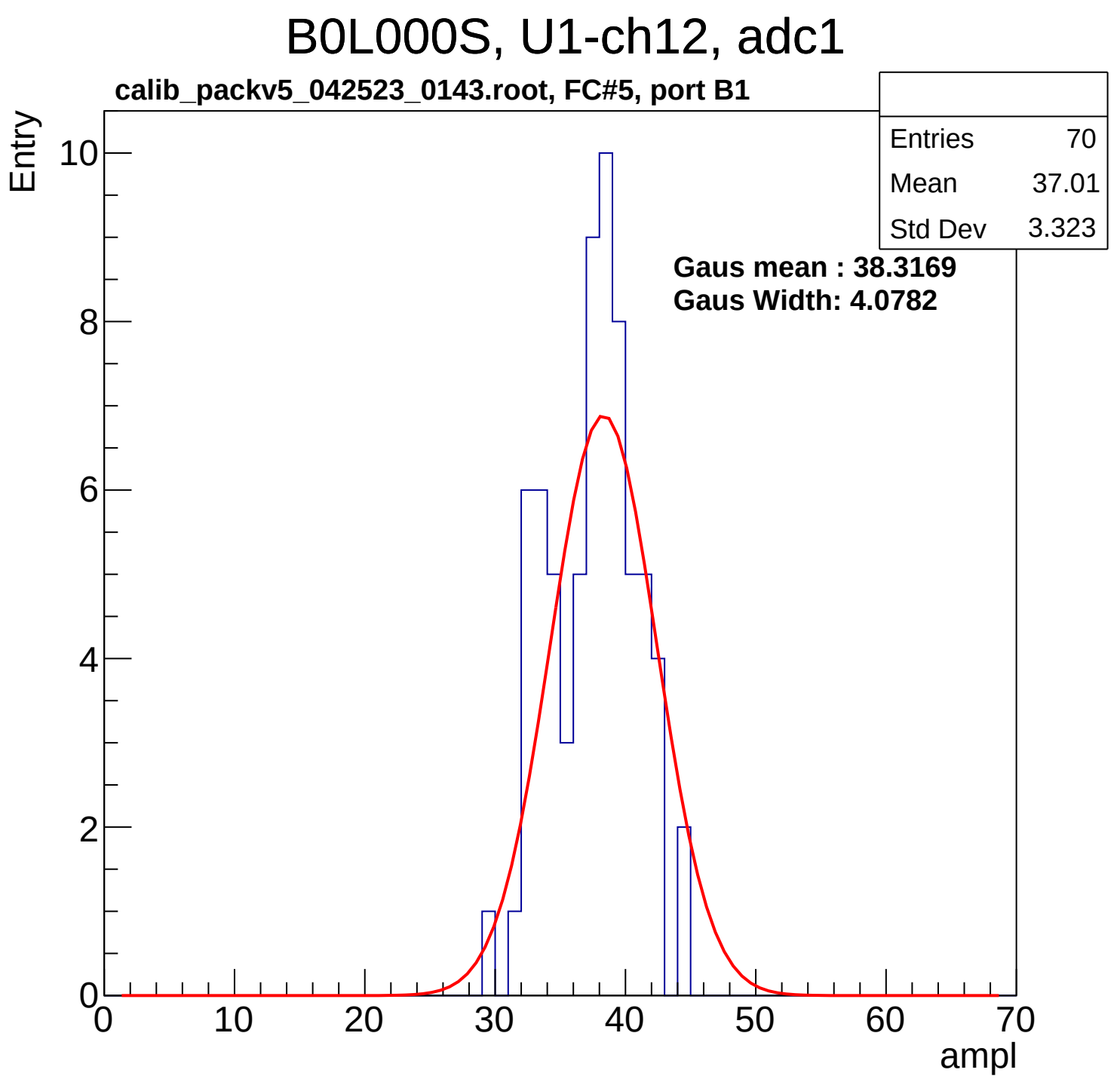
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U1-ch12, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 29.05
Std Dev 4.532
Gaus mean : 29.6649
Gaus Width: 3.3254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch12, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 37.01
Std Dev 3.323
Gaus mean : 38.3169
Gaus Width: 4.0782
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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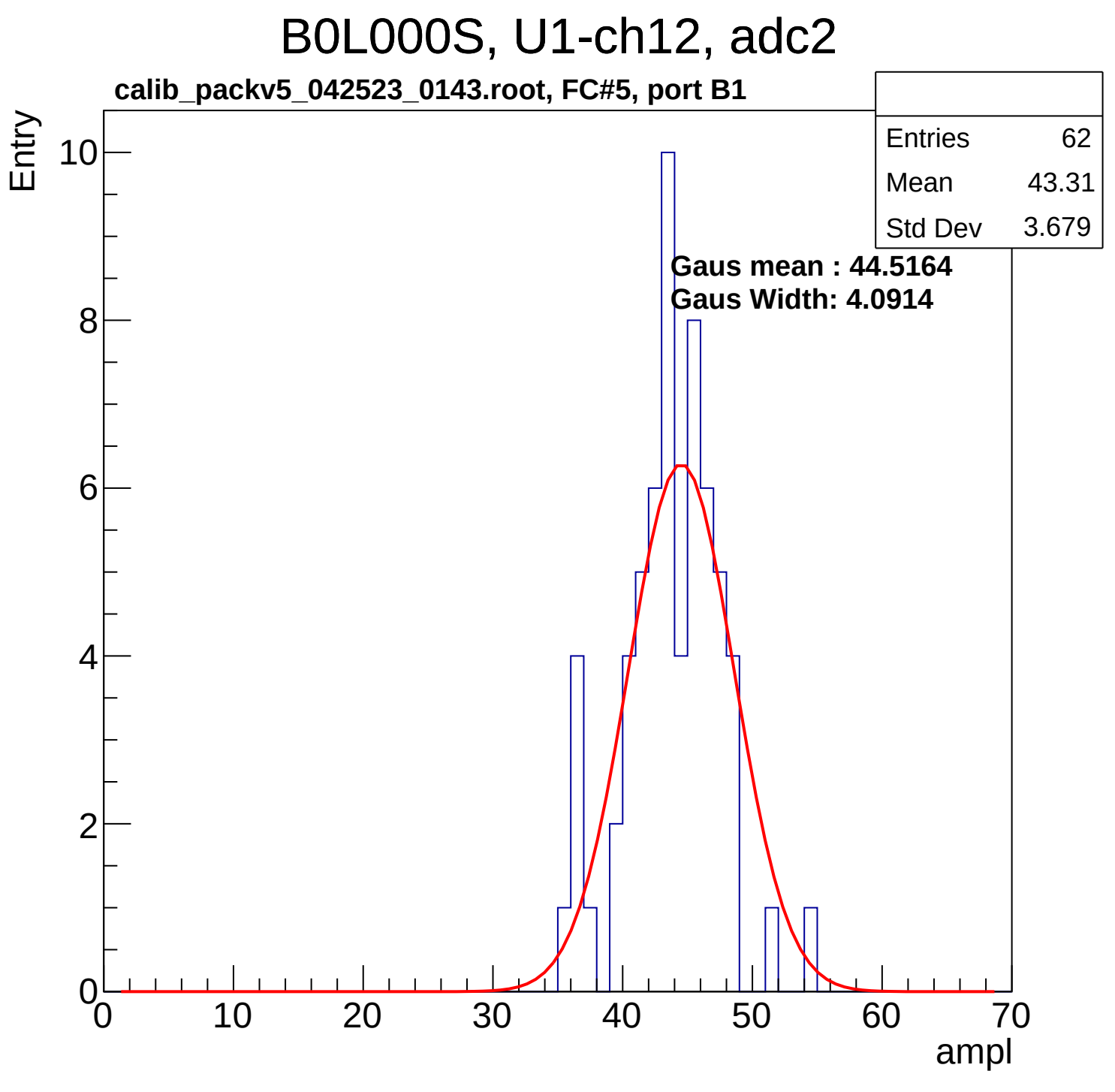
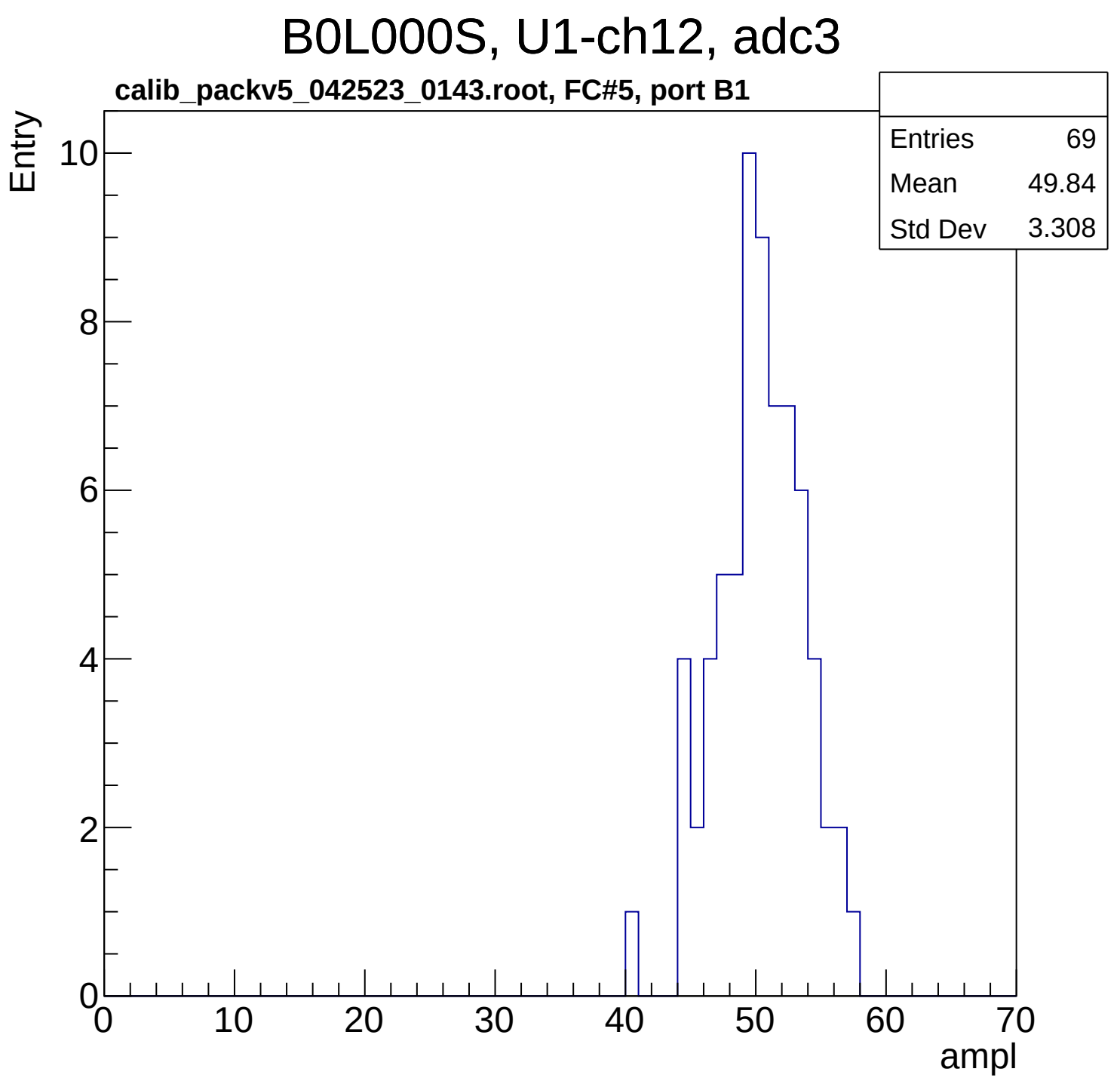
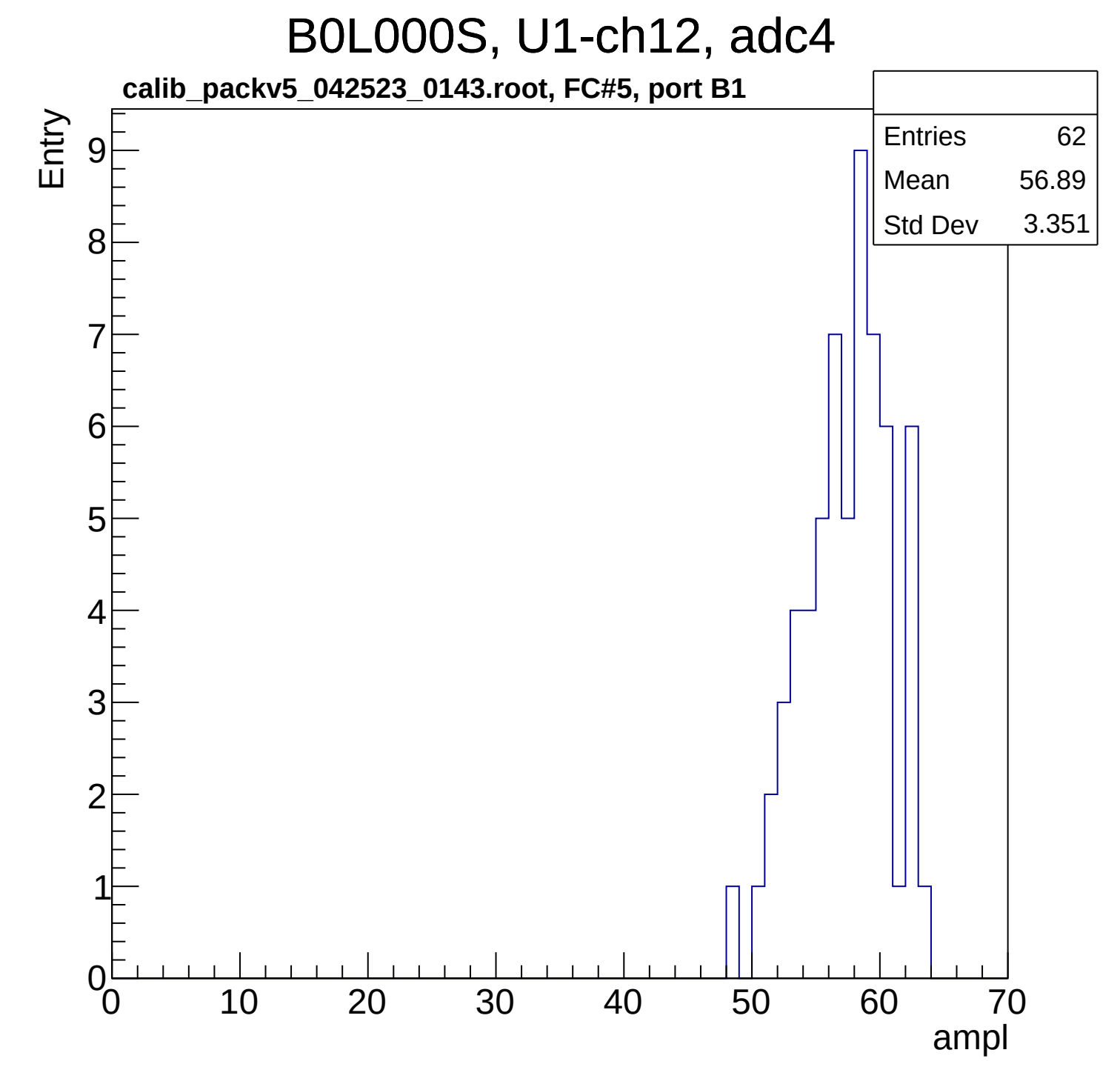

B0L000S, U1-ch12, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.31
Std Dev 3.679
Gaus mean : 44.5164
Gaus Width: 4.0914
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch12, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.84
Std Dev 3.308
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch12, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 56.89
Std Dev 3.351
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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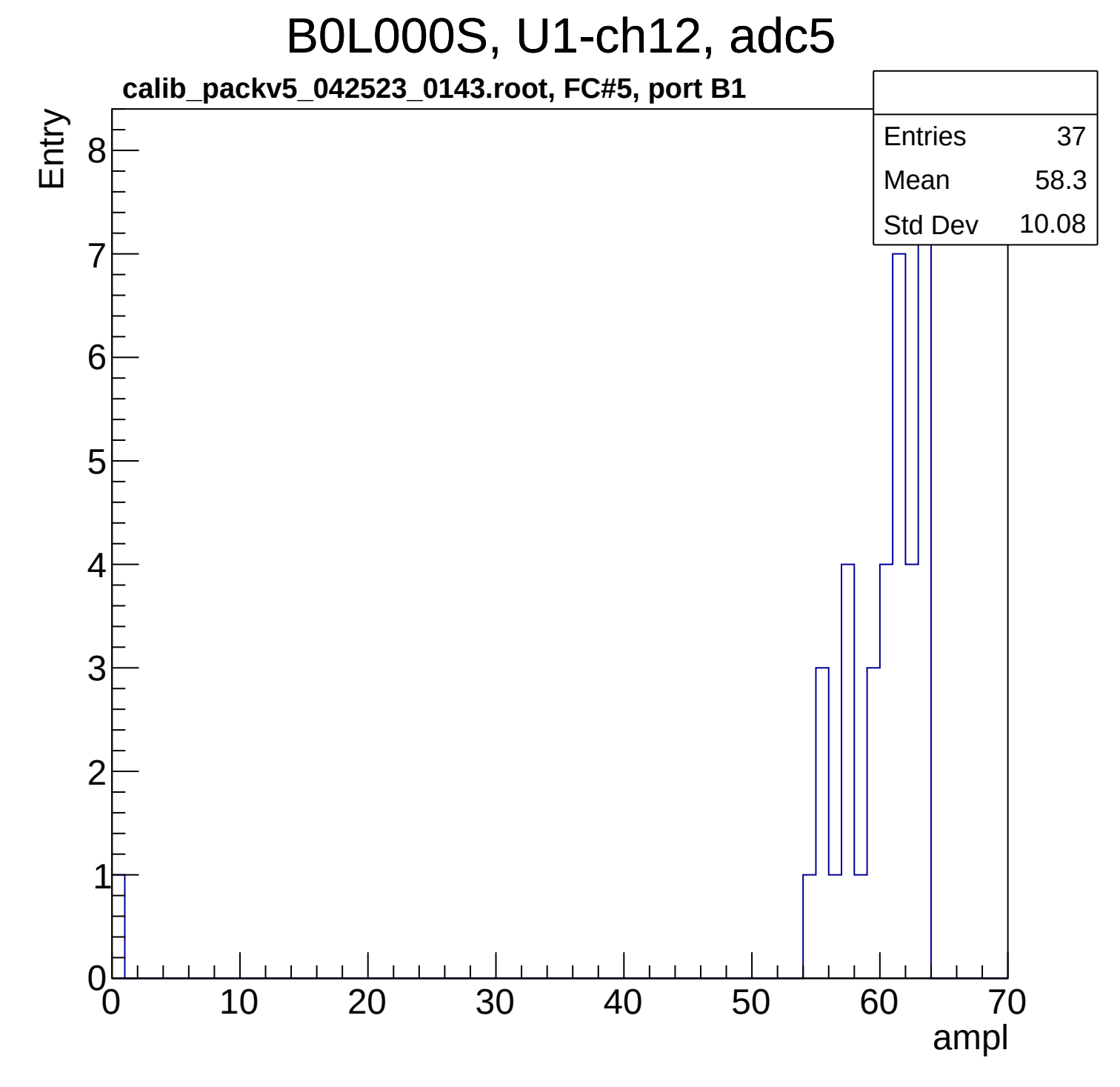
B0L000S, U1-ch12, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.3
Std Dev 10.08
Entry
ampl
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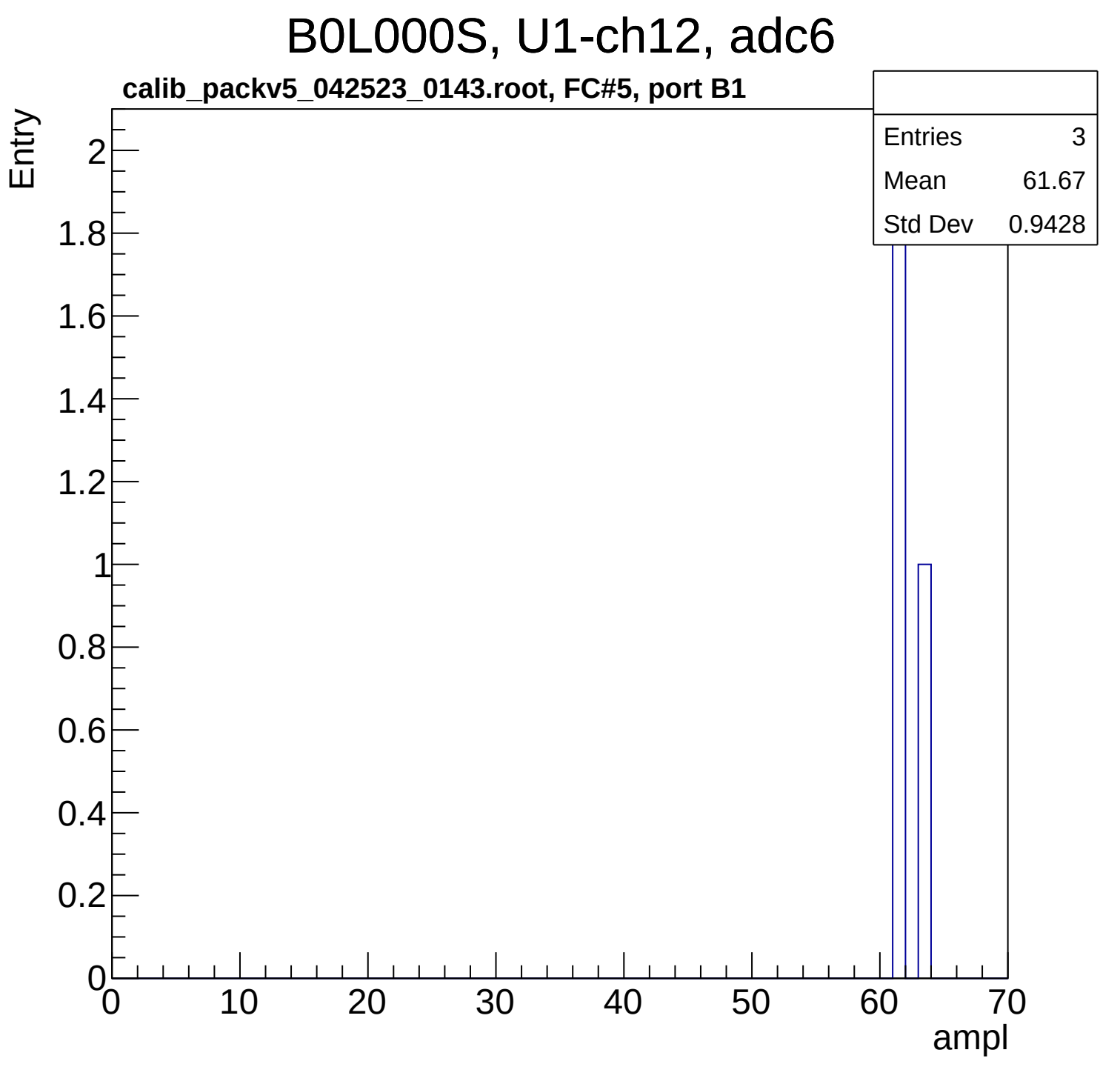
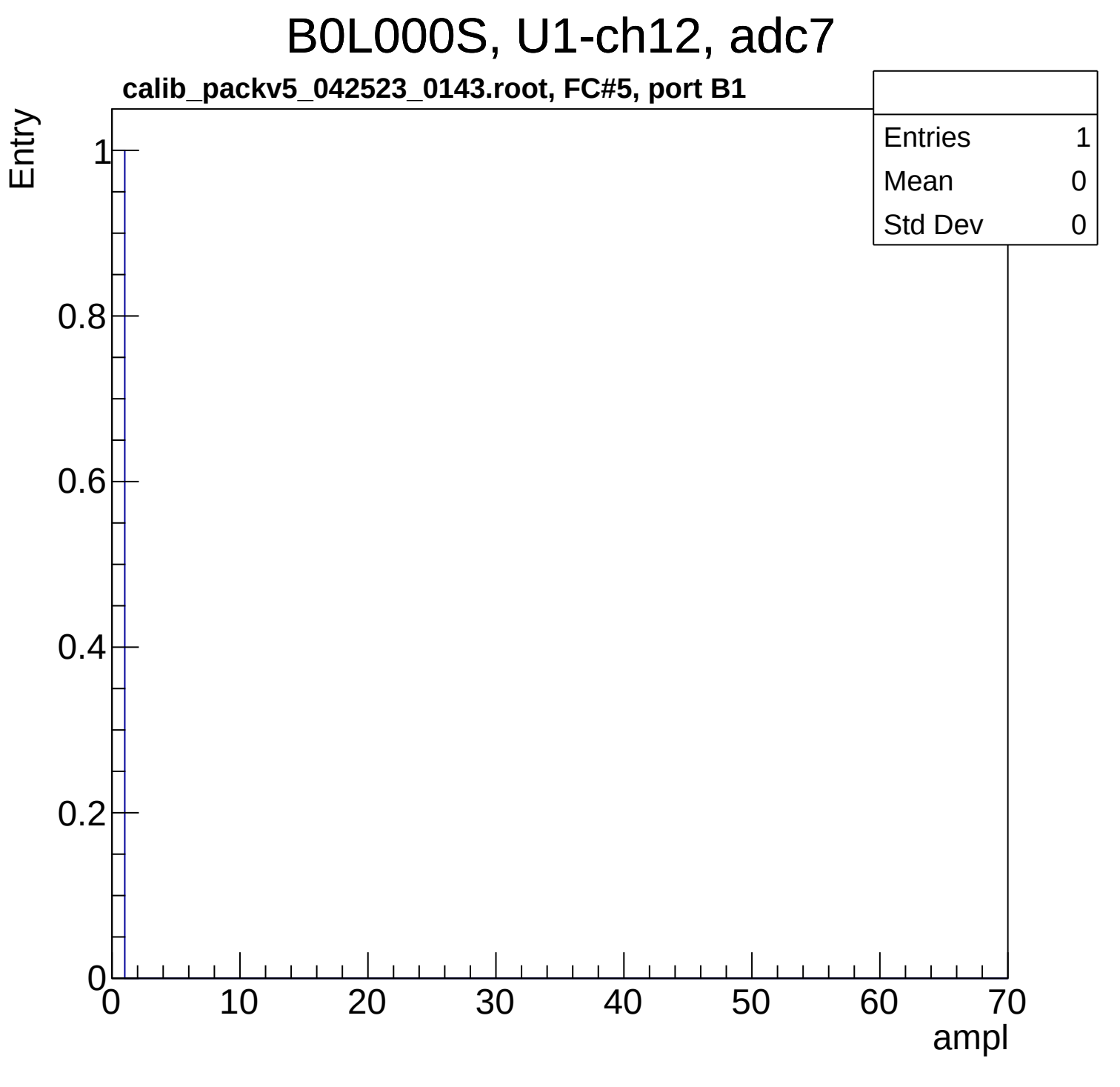
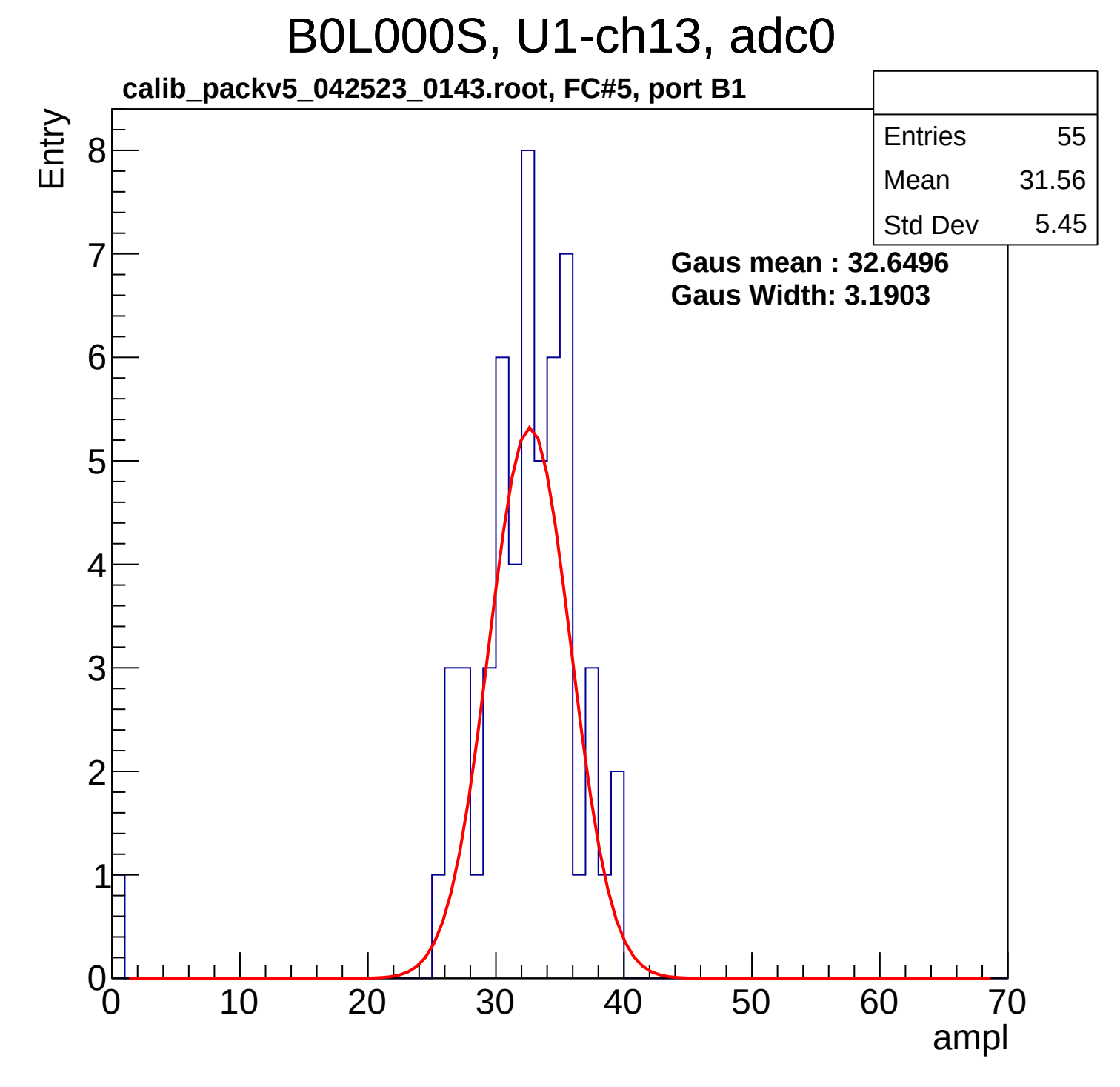
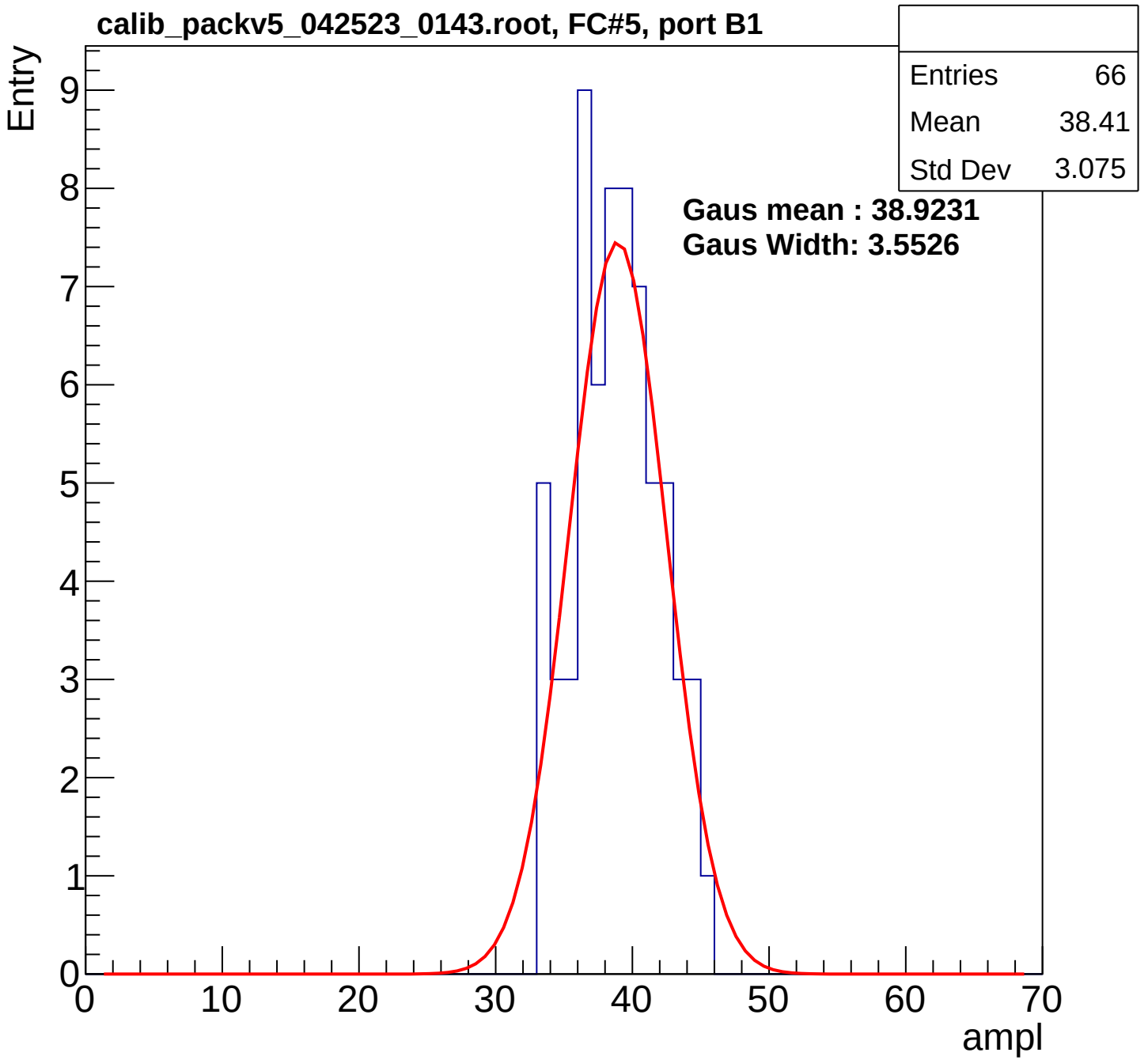
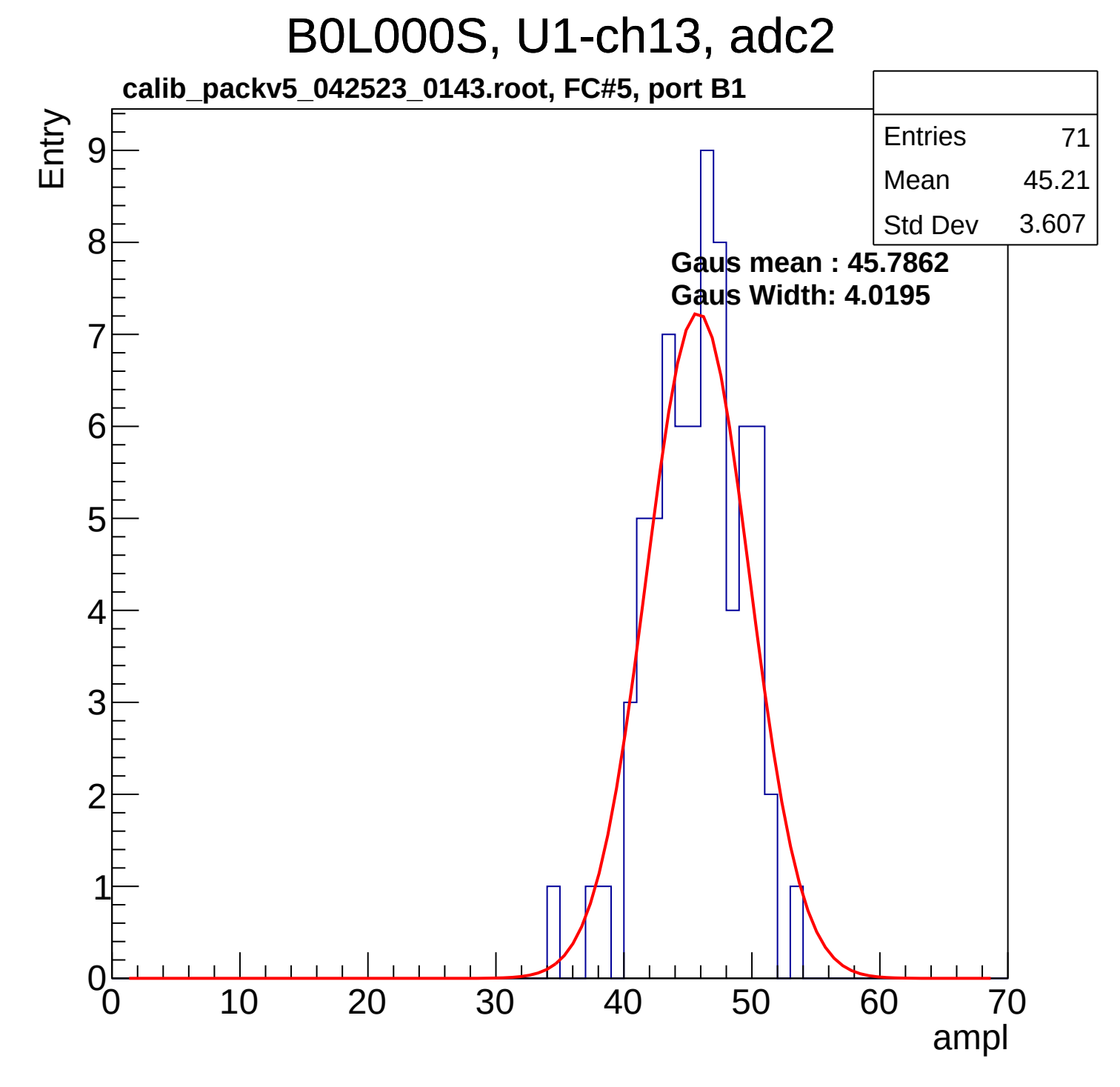
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch12, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch12, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch13, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 31.56
Std Dev 5.45
Gaus mean : 32.6496
Gaus Width: 3.1903
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch13, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 38.41
Std Dev 3.075
Gaus mean : 38.9231
Gaus Width: 3.5526
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U1-ch13, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 45.21
Std Dev 3.607
Gaus mean : 45.7862
Gaus Width: 4.0195
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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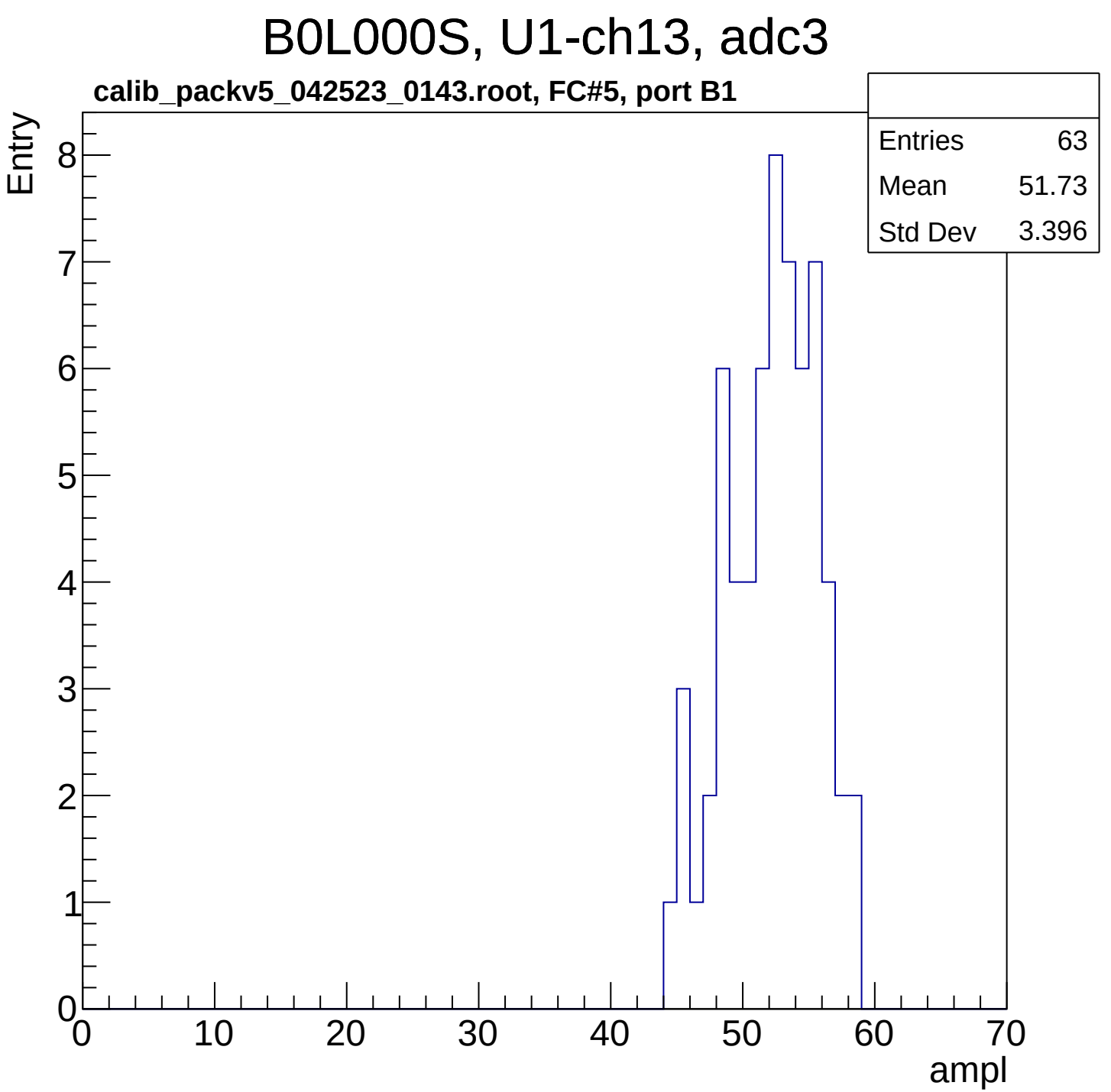
B0L000S, U1-ch13, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 51.73
Std Dev 3.396
Entry
0
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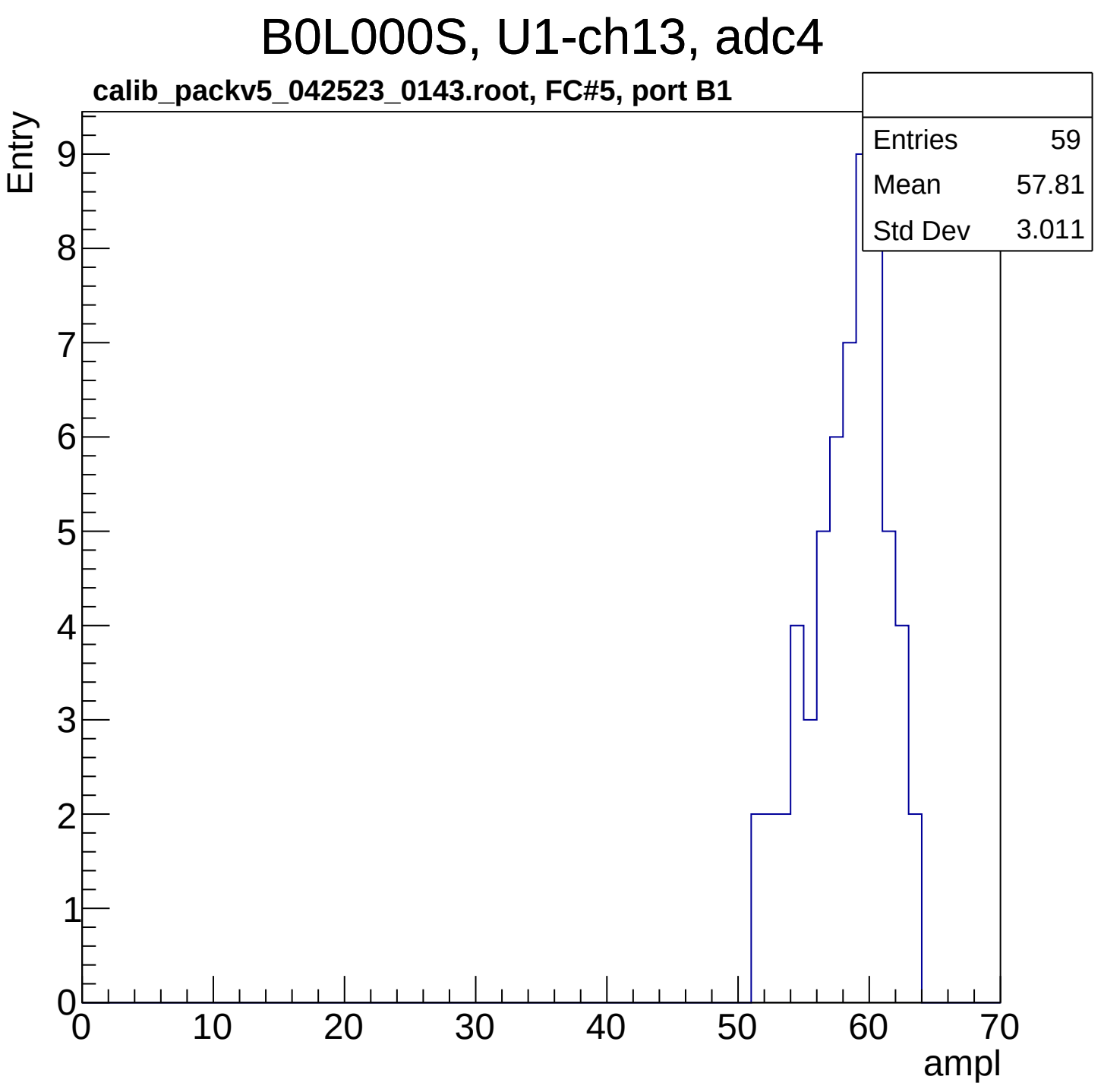
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch13, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.81
Std Dev 3.011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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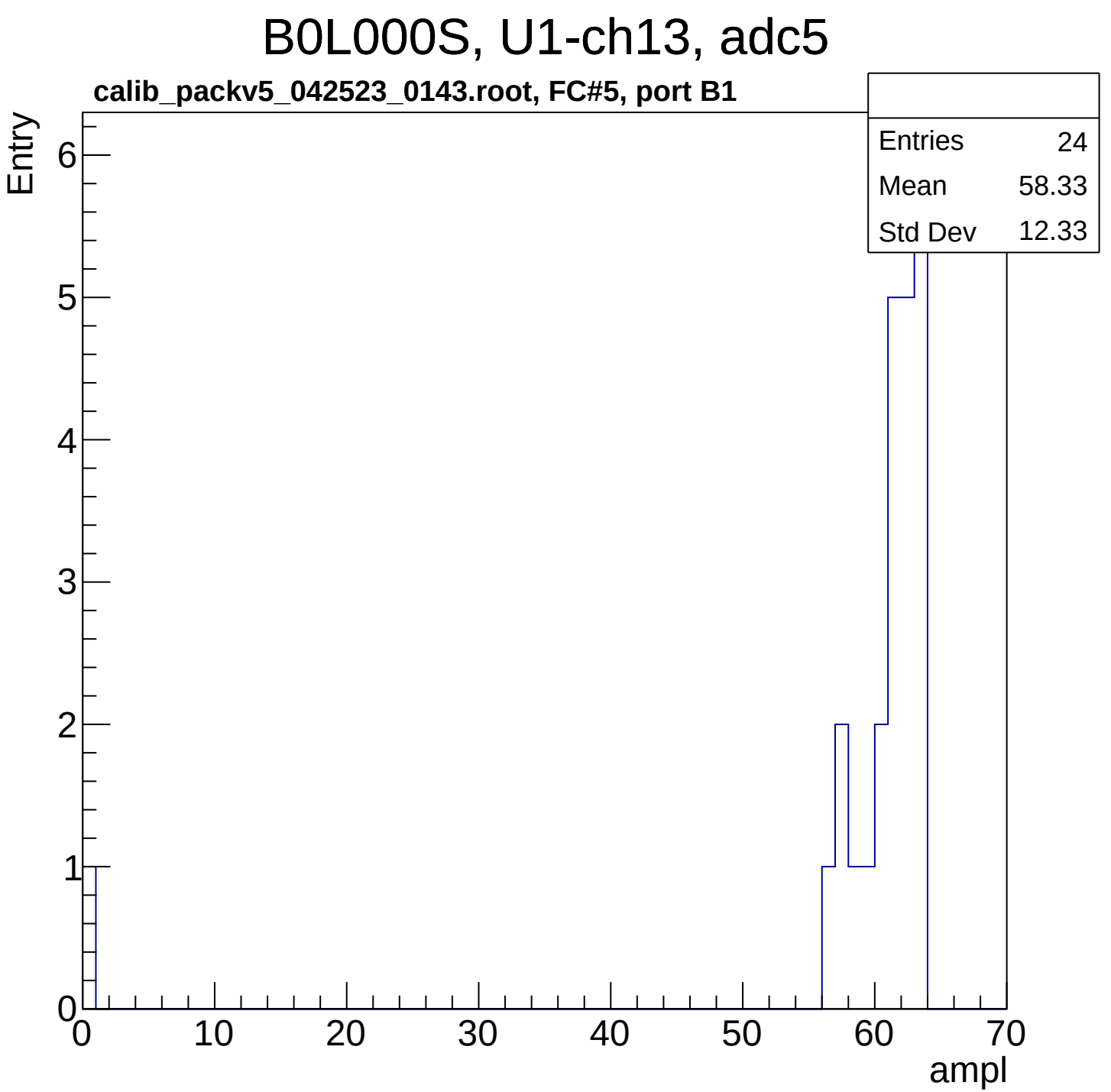
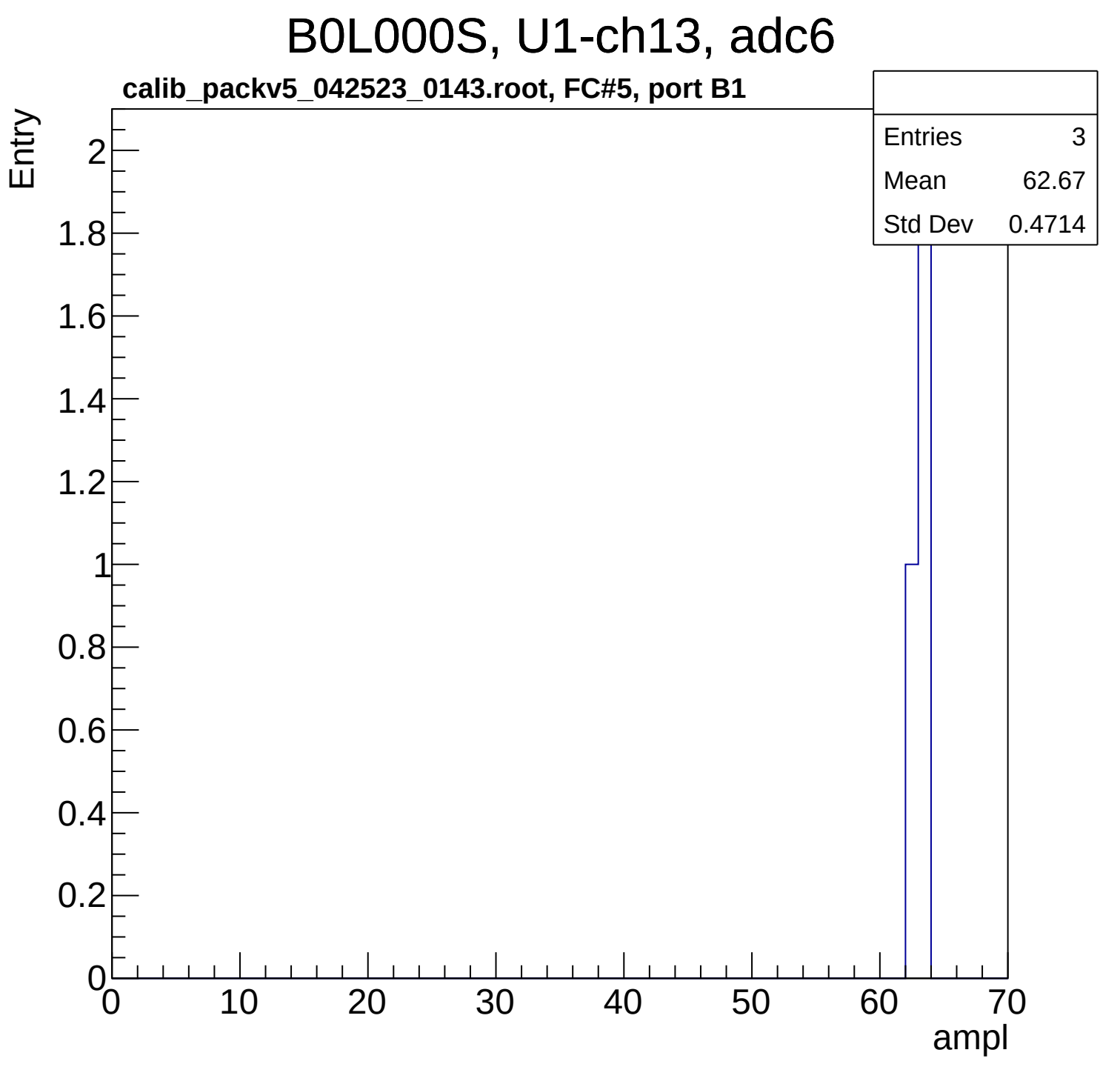
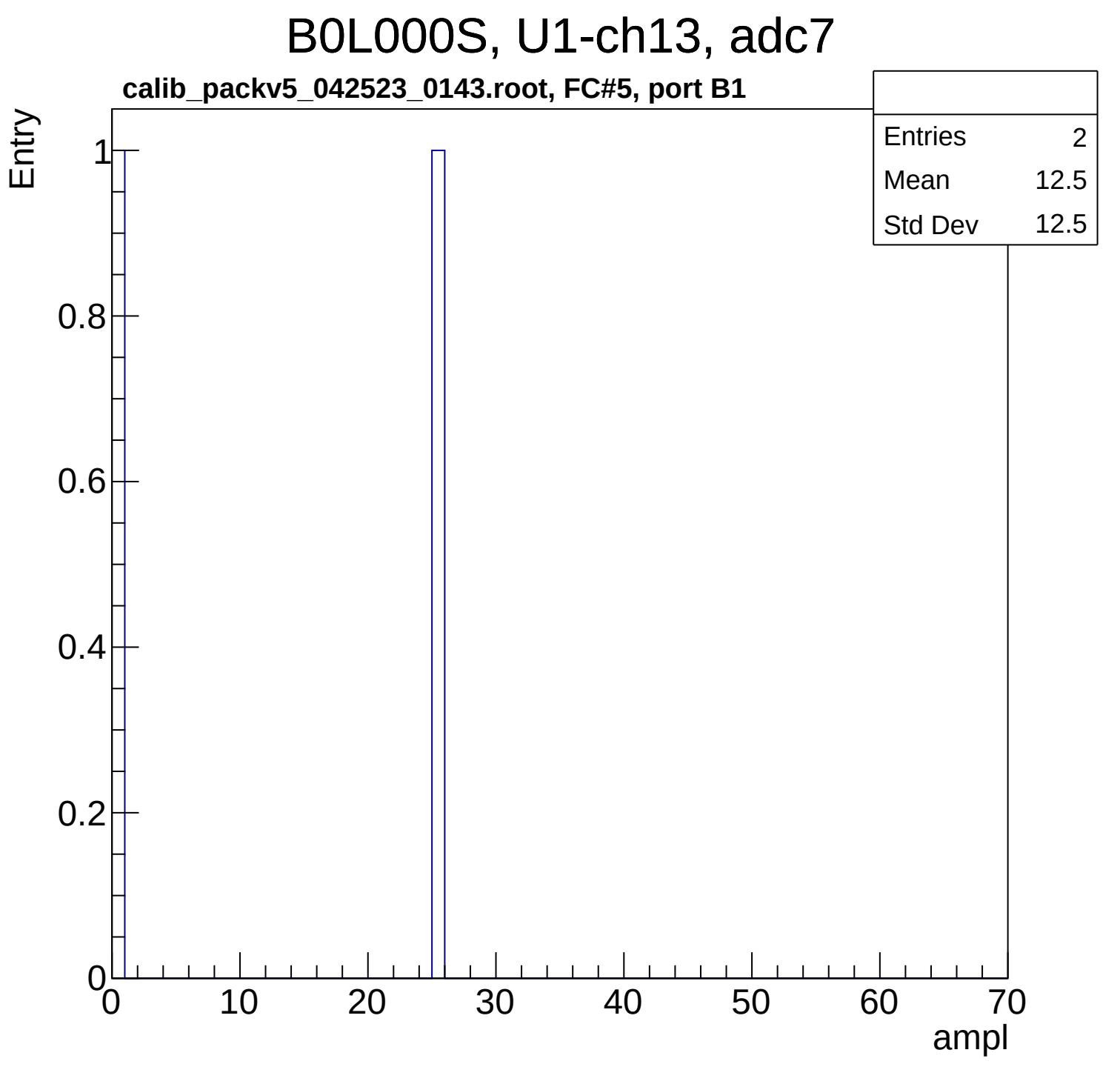

B0L000S, U1-ch13, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.33
Std Dev 12.33
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch13, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch13, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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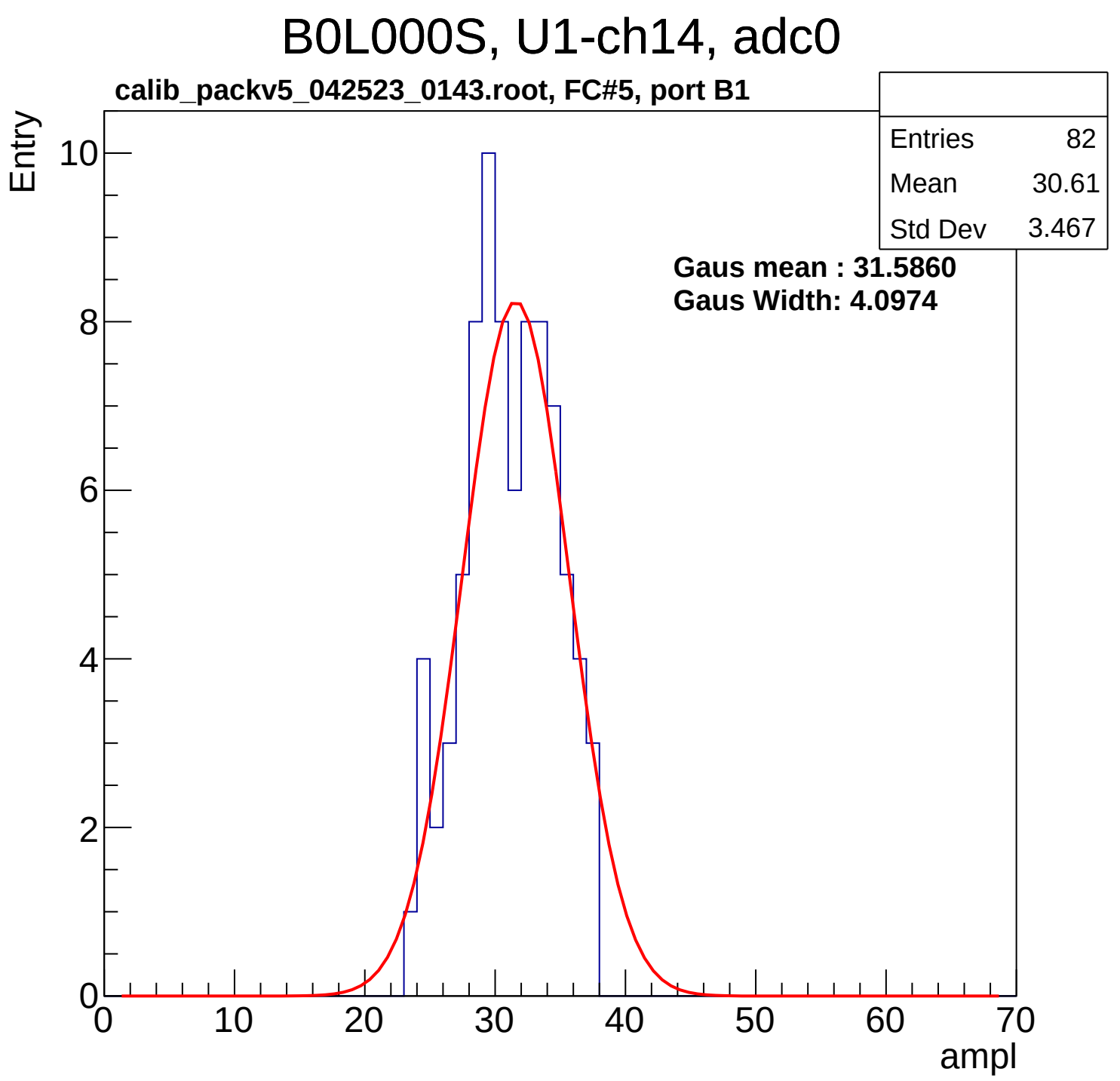
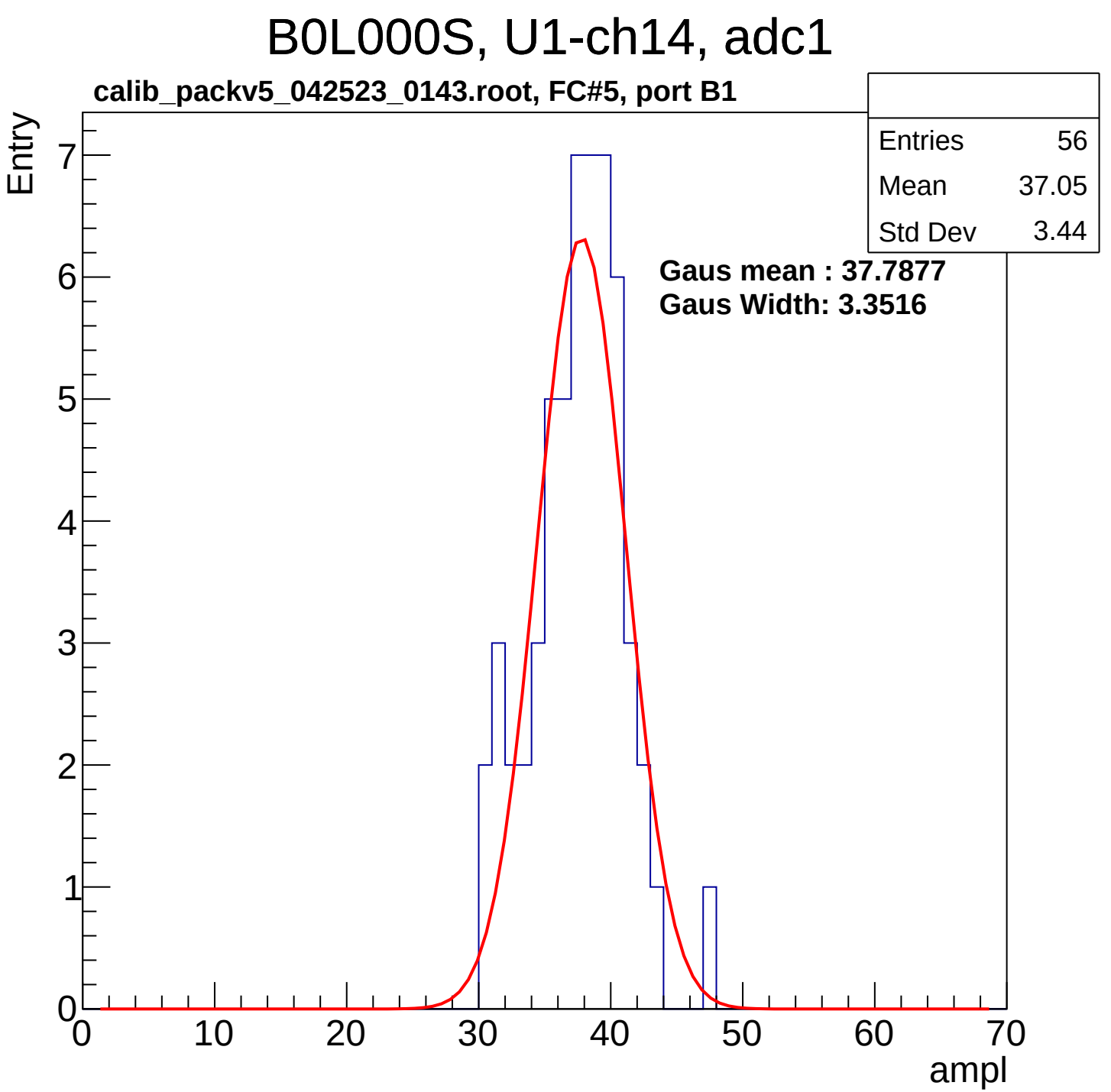
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch14, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 30.61
Std Dev 3.467
Gaus mean : 31.5860
Gaus Width: 4.0974
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch14, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.05
Std Dev 3.44
Gaus mean : 37.7877
Gaus Width: 3.3516
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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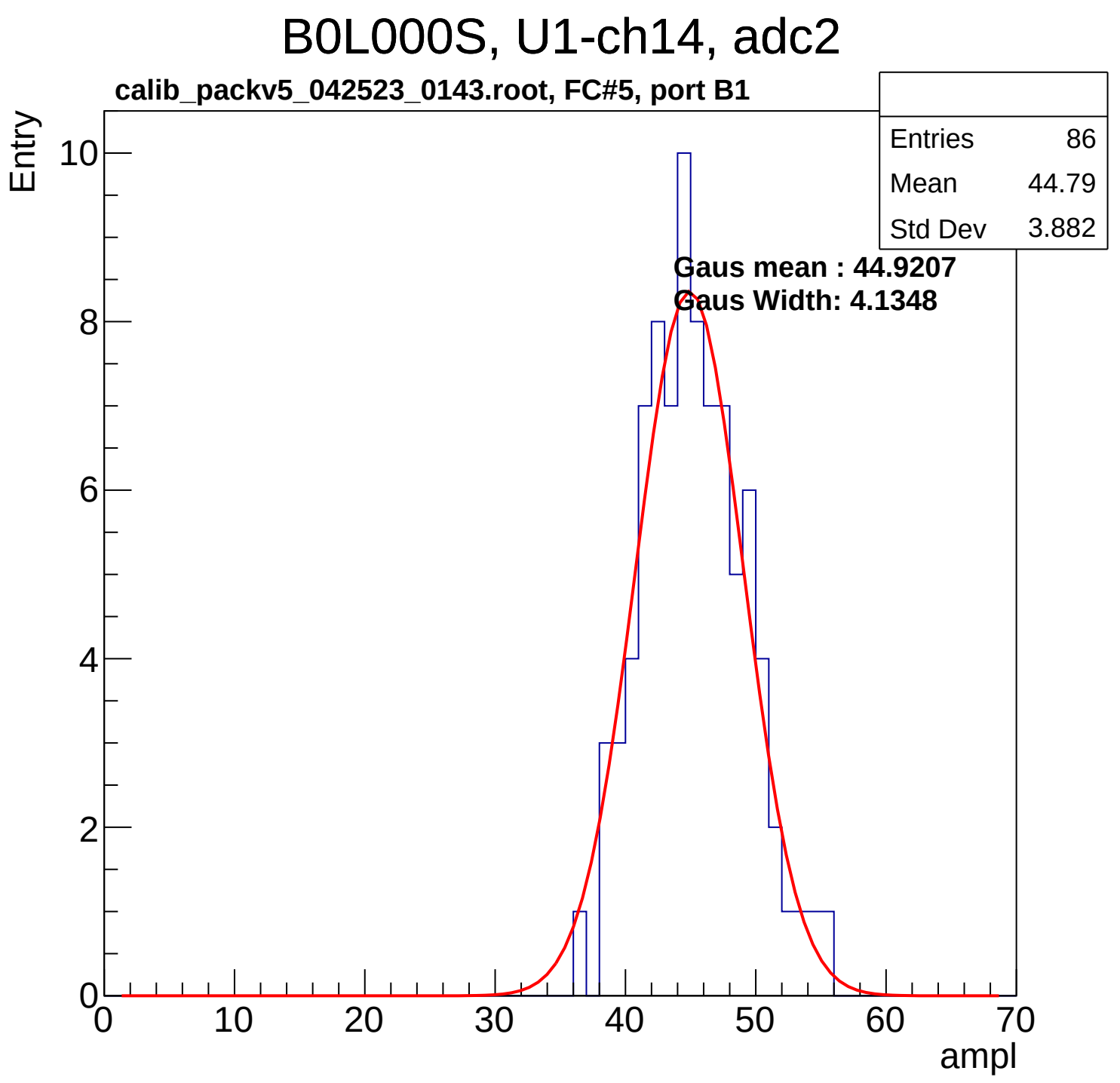
40
50
60
70


B0L000S, U1-ch14, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 44.79
Std Dev 3.882
Gaus mean : 44.9207
Gaus Width: 4.1348
Entry
ampl
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2
4
6
8
10
0
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20
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40
50
60
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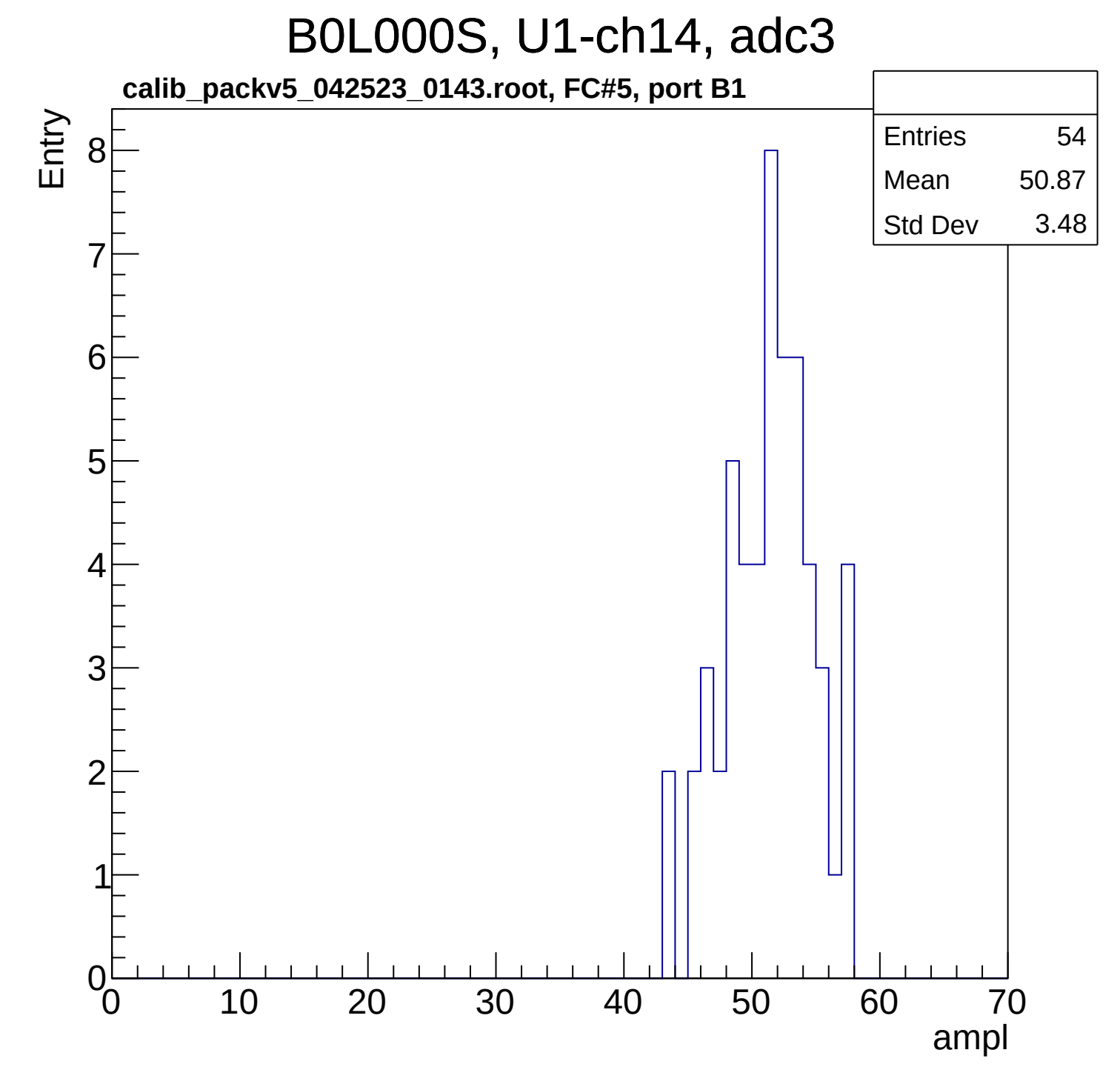
B0L000S, U1-ch14, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 50.87
Std Dev 3.48
Entry
ampl
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2
3
4
5
6
7
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0
10
20
30
40
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60
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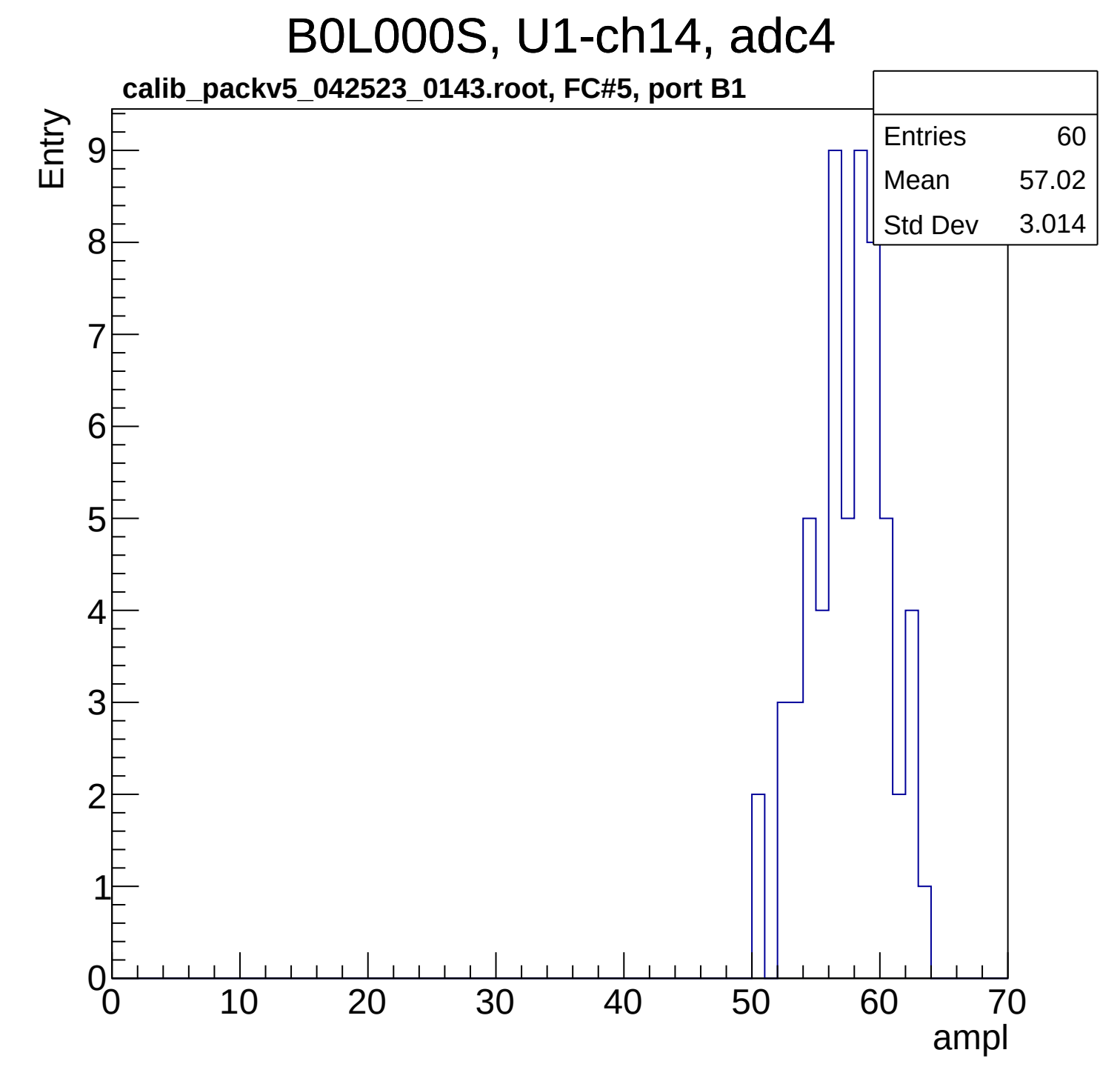
B0L000S, U1-ch14, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.02
Std Dev 3.014
Entry
ampl
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2
3
4
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6
7
8
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0
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40
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60
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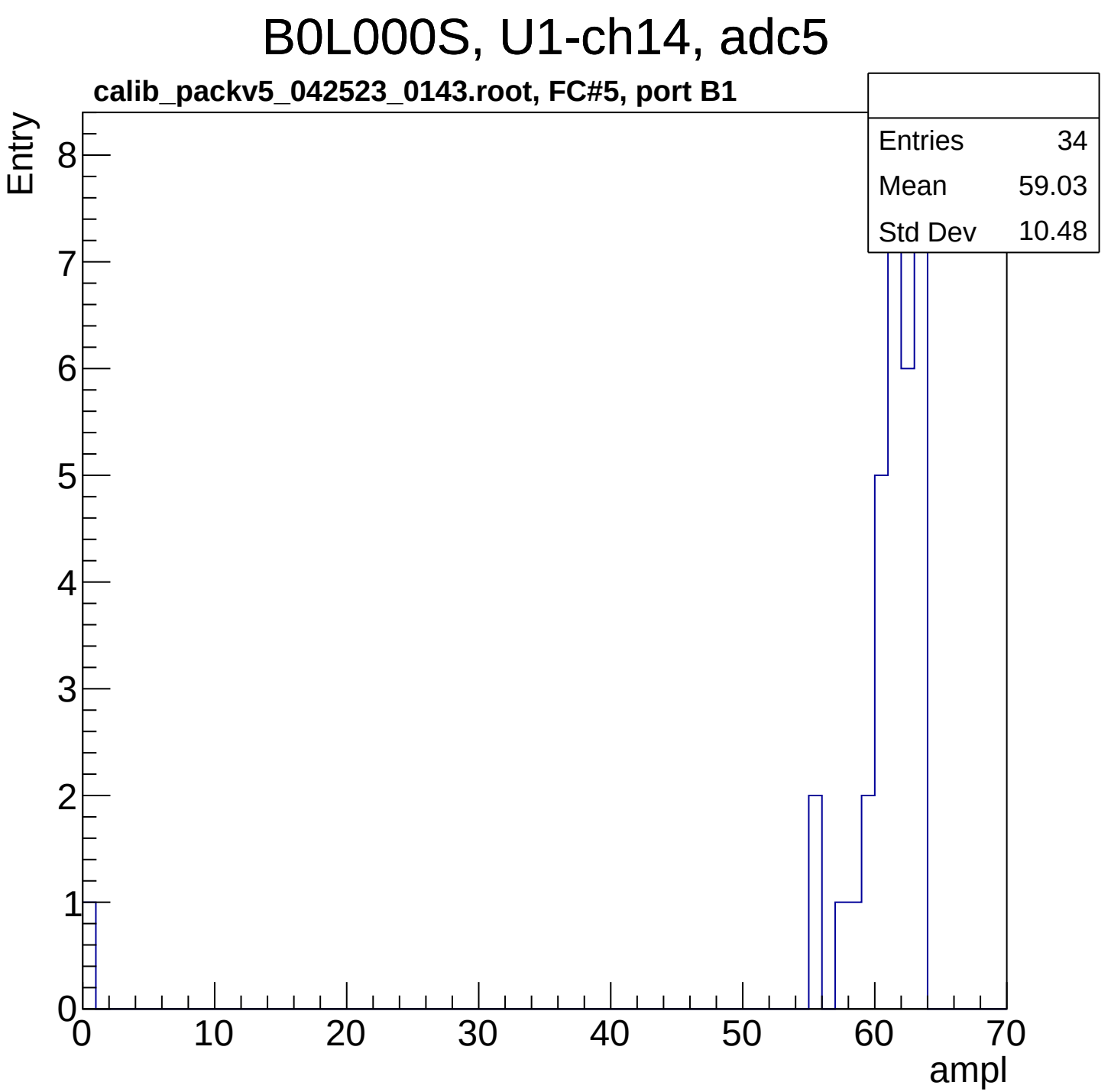
B0L000S, U1-ch14, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 59.03
Std Dev 10.48
Entry
ampl
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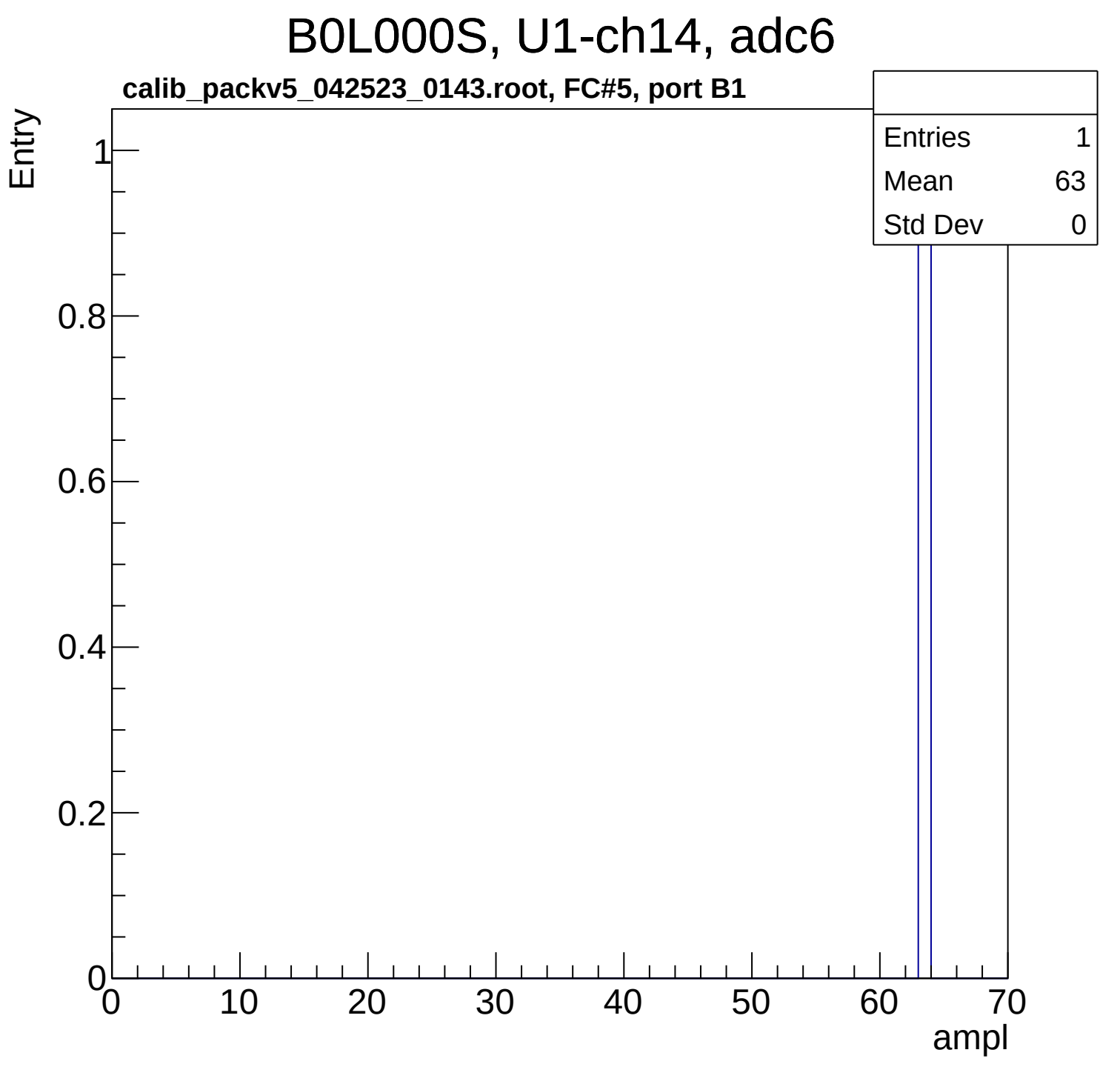
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch14, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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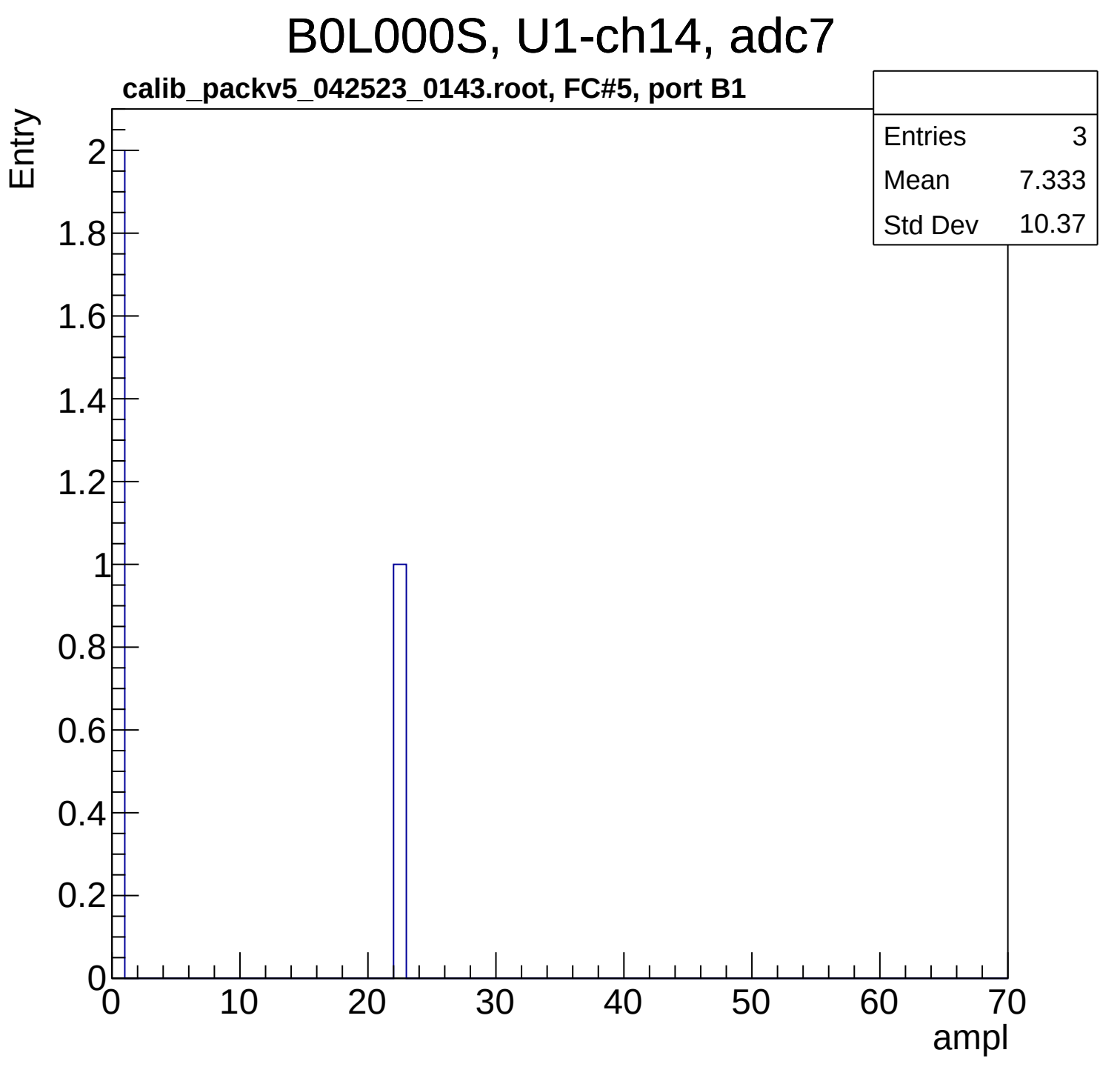
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch14, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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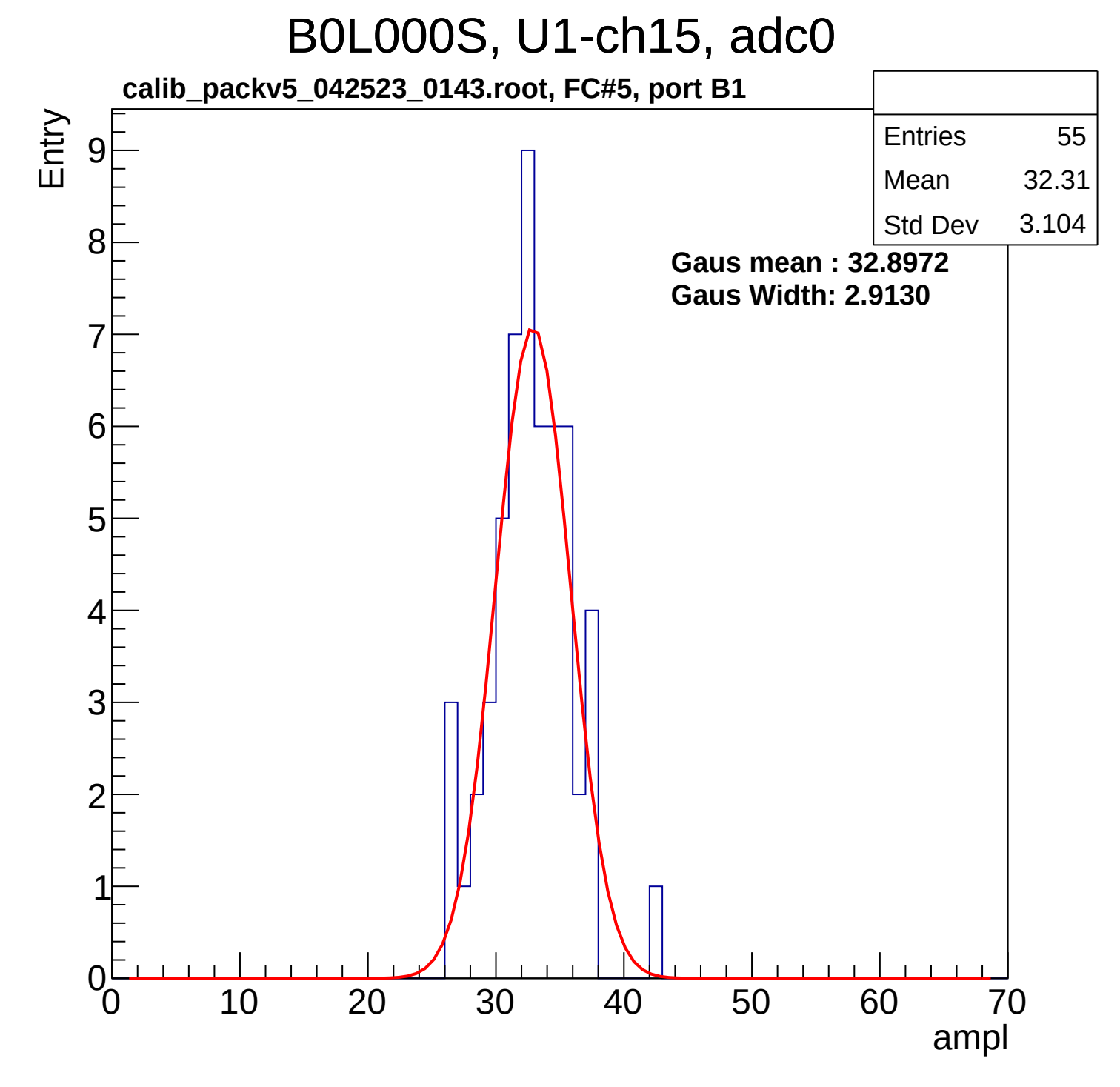
B0L000S, U1-ch15, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 32.31
Std Dev 3.104
Gaus mean : 32.8972
Gaus Width: 2.9130
Entry
ampl
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2
3
4
5
6
7
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0
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60
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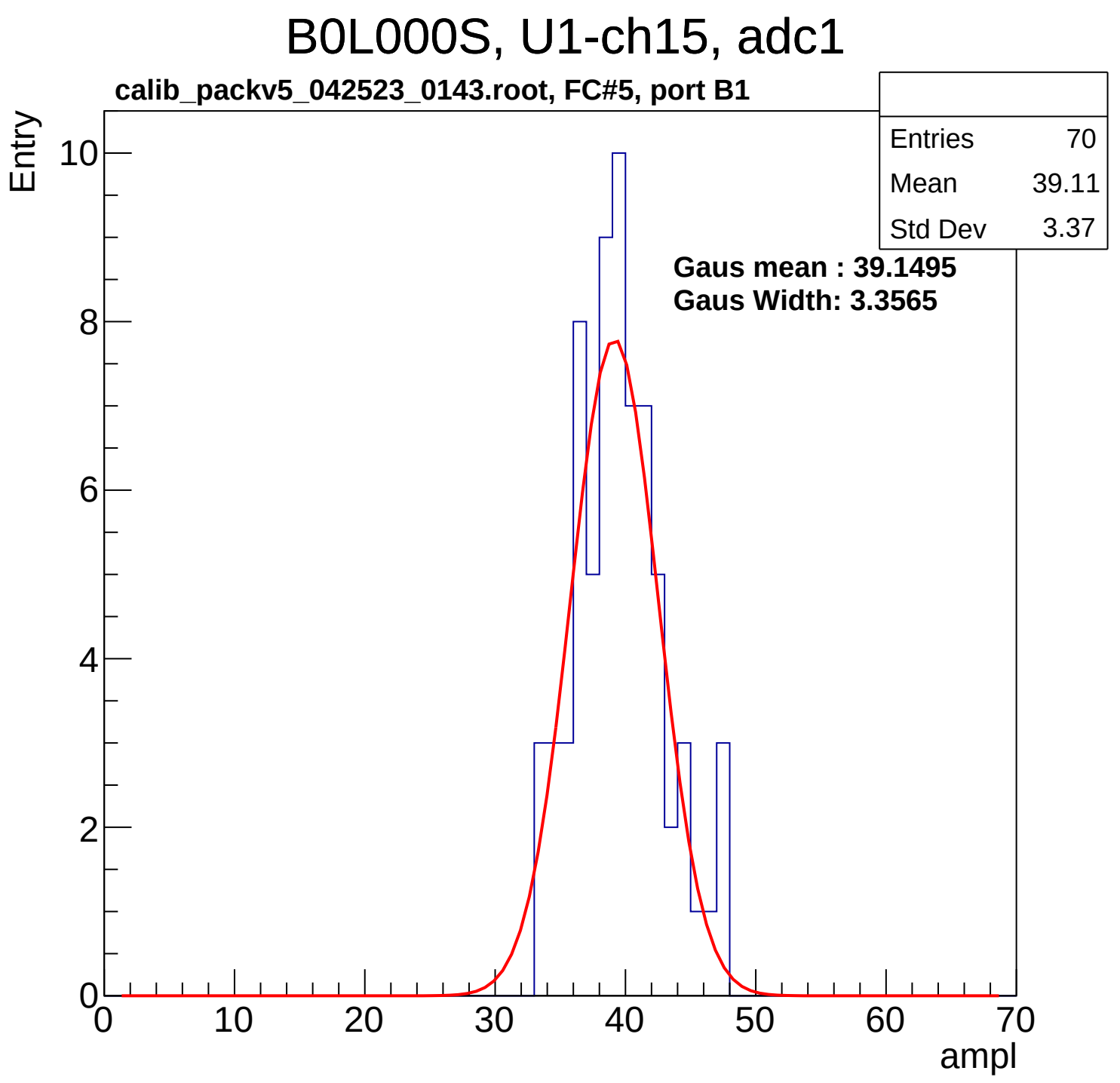
B0L000S, U1-ch15, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 39.11
Std Dev 3.37
Gaus mean : 39.1495
Gaus Width: 3.3565
Entry
ampl
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8
10
0
10
20
30
40
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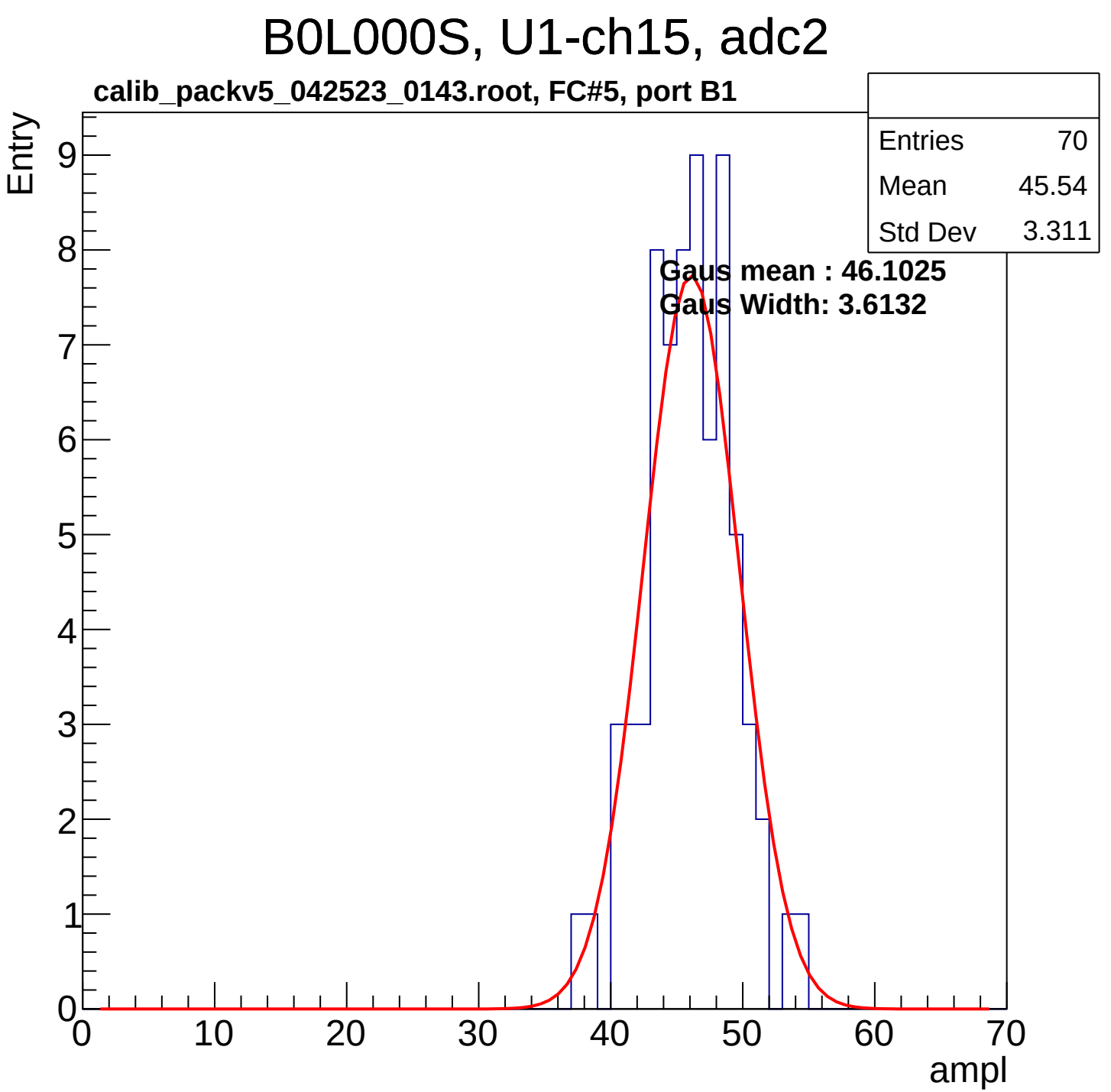
60
70


B0L000S, U1-ch15, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 45.54
Std Dev 3.311
Gaus mean : 46.1025
Gaus Width: 3.6132
Entry
ampl
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3
4
5
6
7
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0
10
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40
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60
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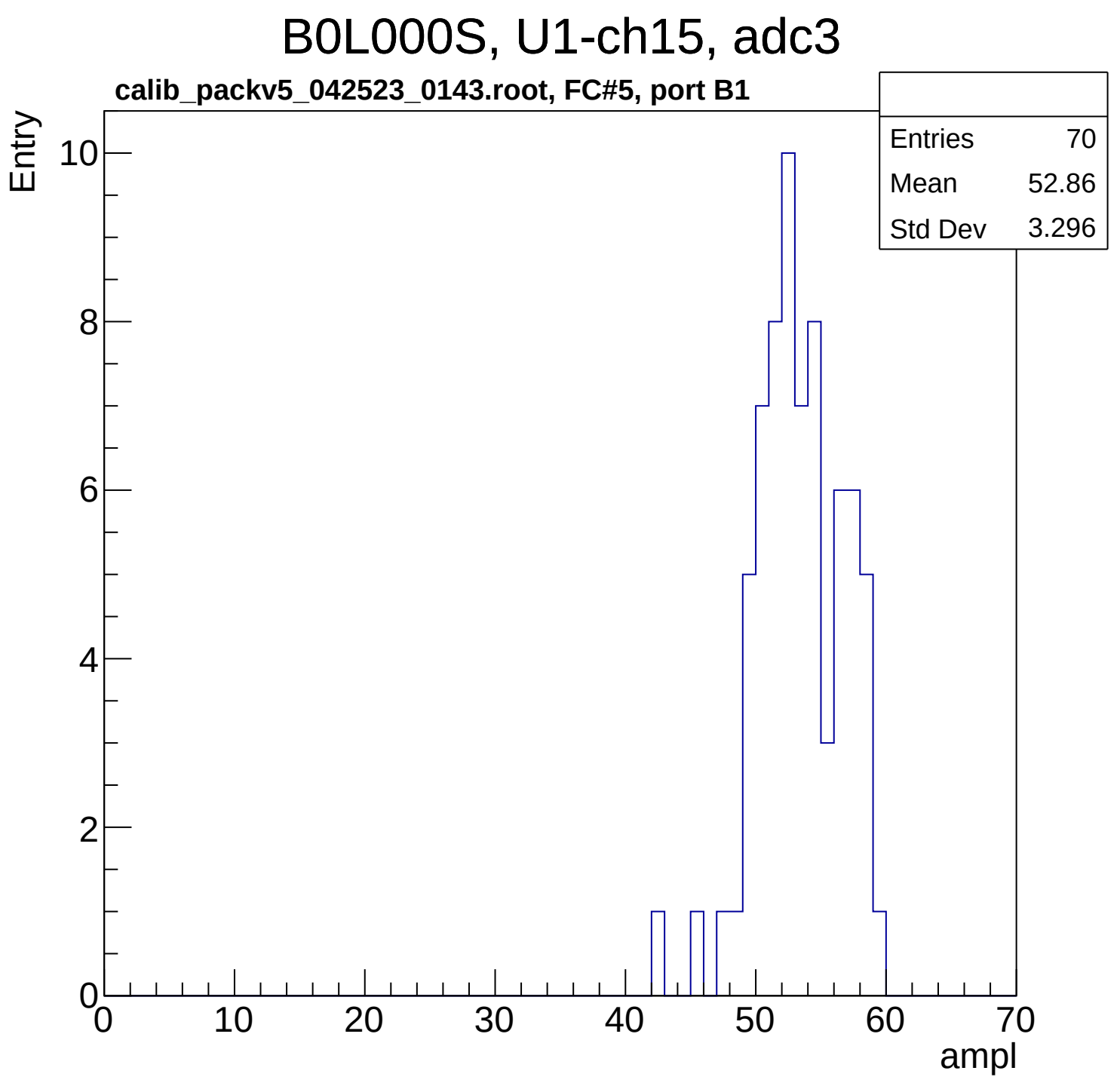
B0L000S, U1-ch15, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 52.86
Std Dev 3.296
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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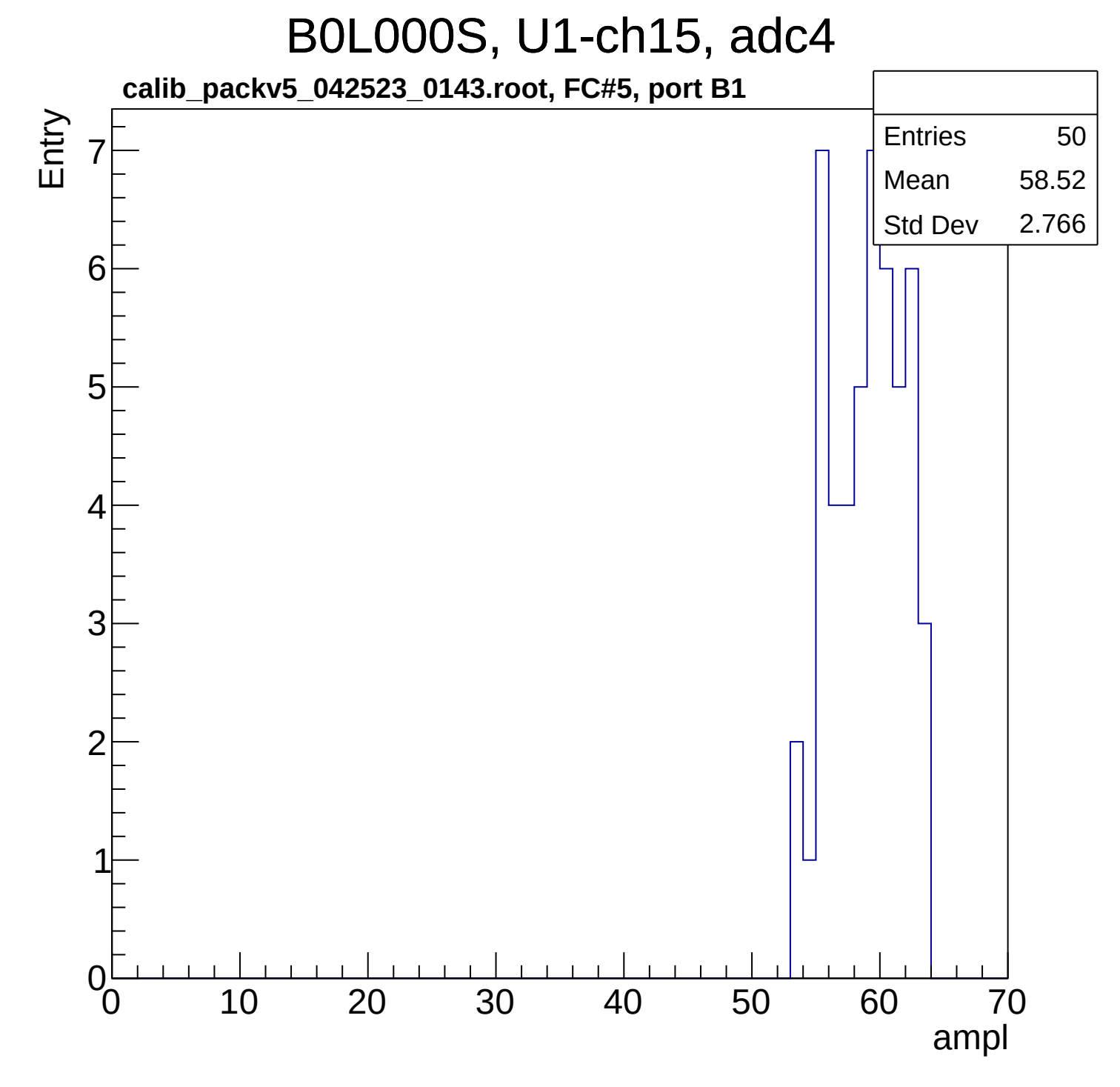
B0L000S, U1-ch15, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.52
Std Dev 2.766
Entry
ampl
0
1
2
3
4
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6
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0
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20
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40
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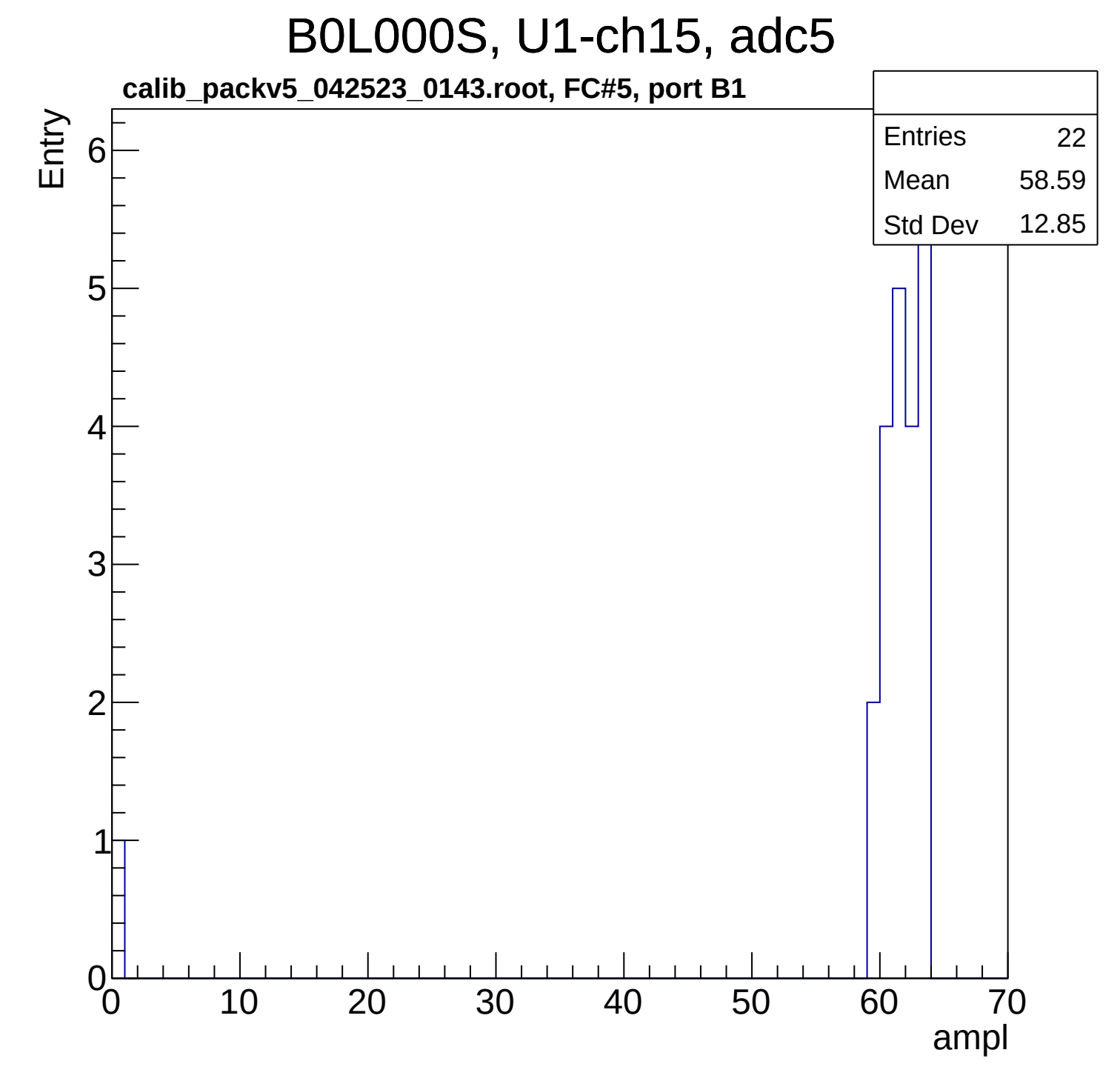
60
70


B0L000S, U1-ch15, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.59
Std Dev 12.85
Entry
0
1
2
3
4
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6
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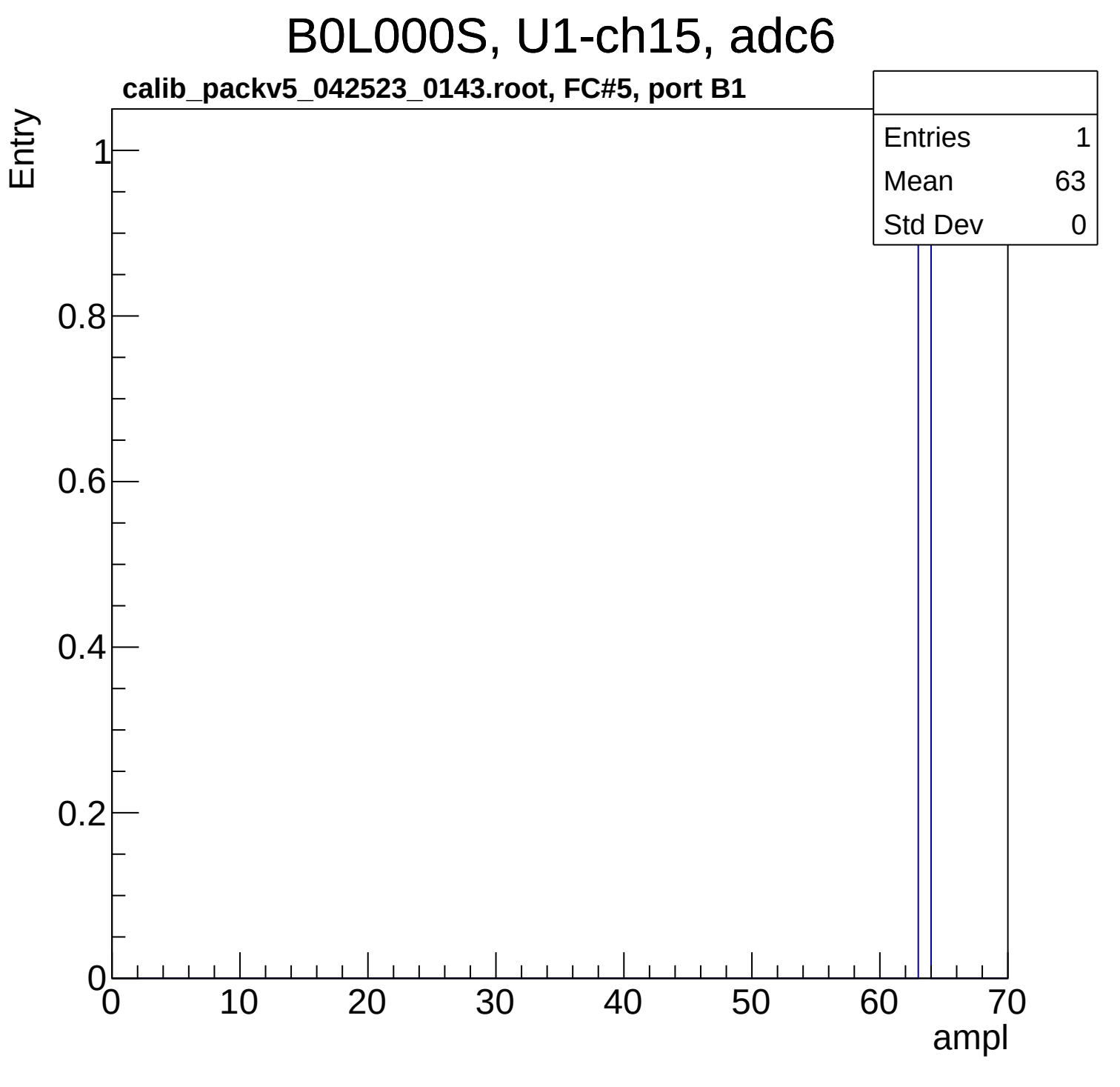
70
ampl


B0L000S, U1-ch15, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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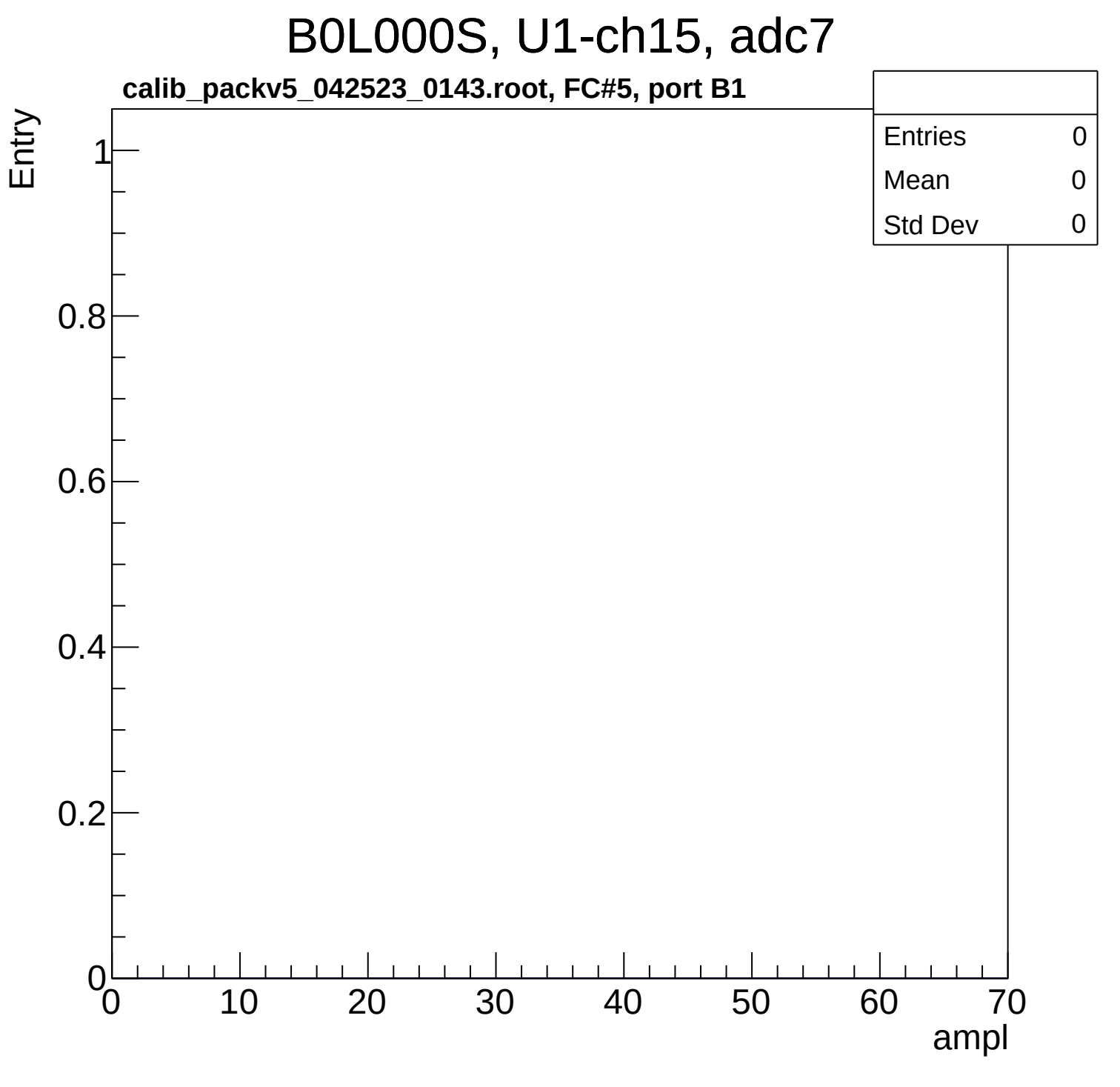
40
50
60
70
ampl


B0L000S, U1-ch15, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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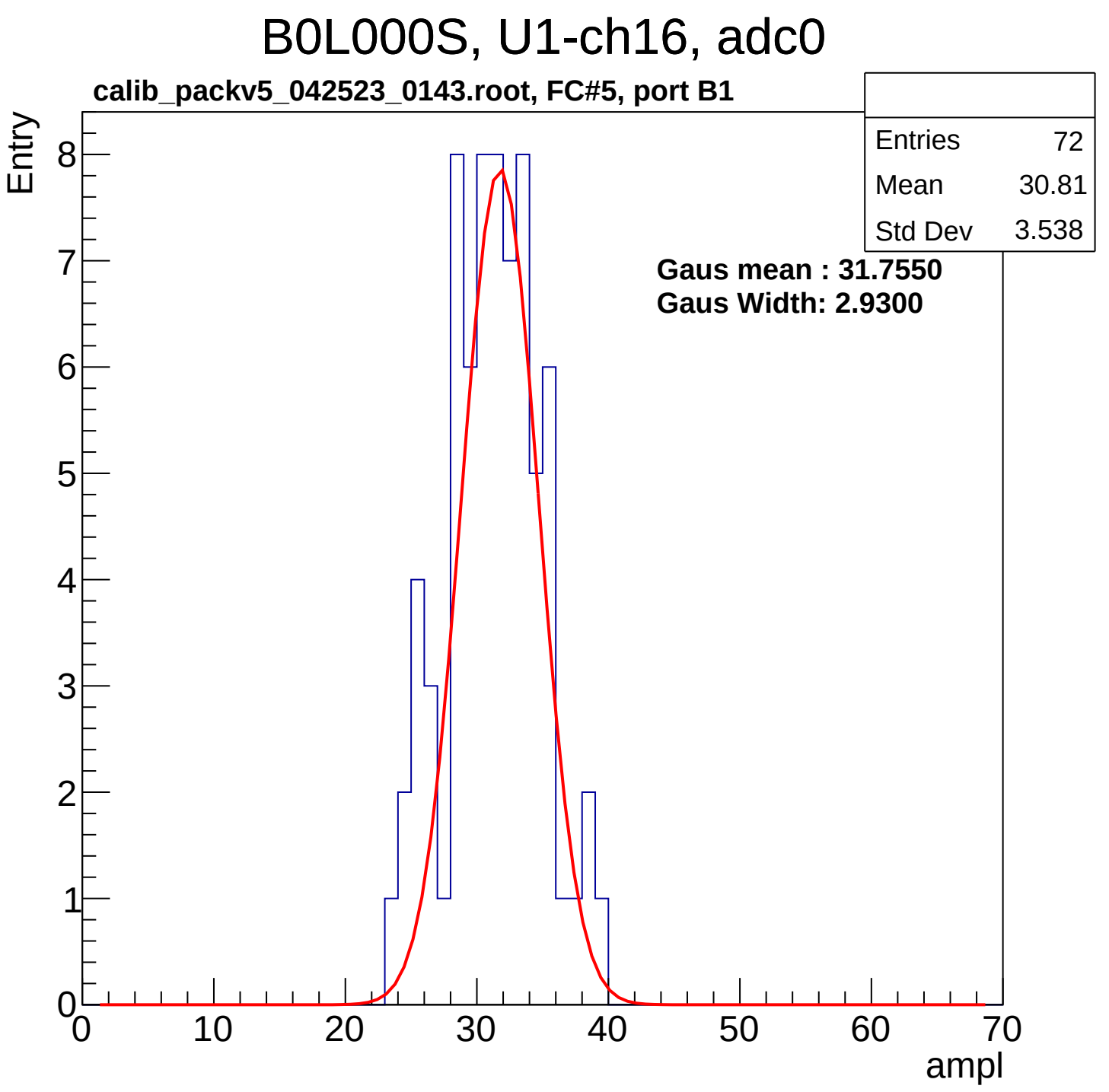
40
50
60
70
ampl


B0L000S, U1-ch16, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 30.81
Std Dev 3.538
Gaus mean : 31.7550
Gaus Width: 2.9300
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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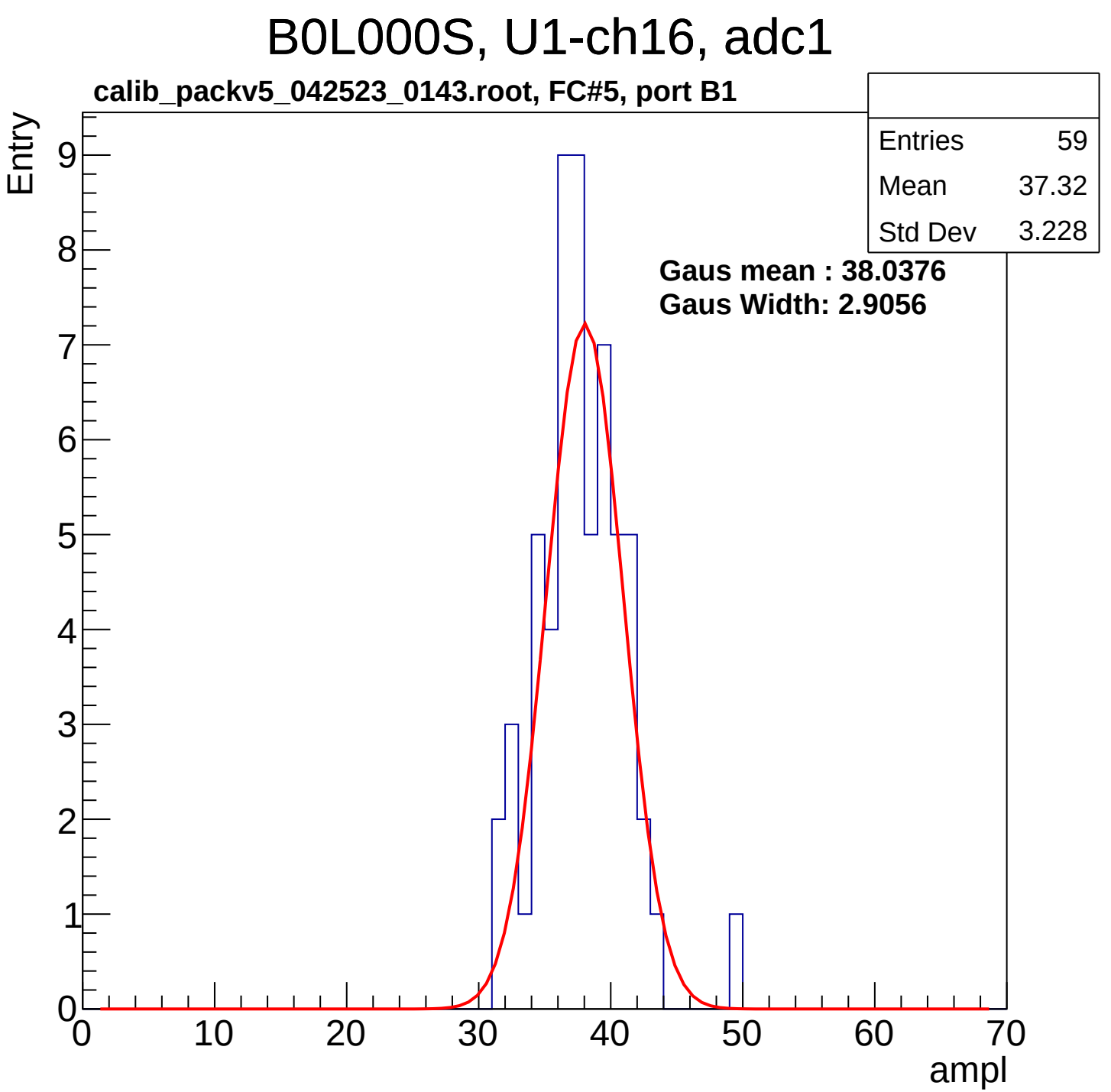
B0L000S, U1-ch16, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 37.32
Std Dev 3.228
Gaus mean : 38.0376
Gaus Width: 2.9056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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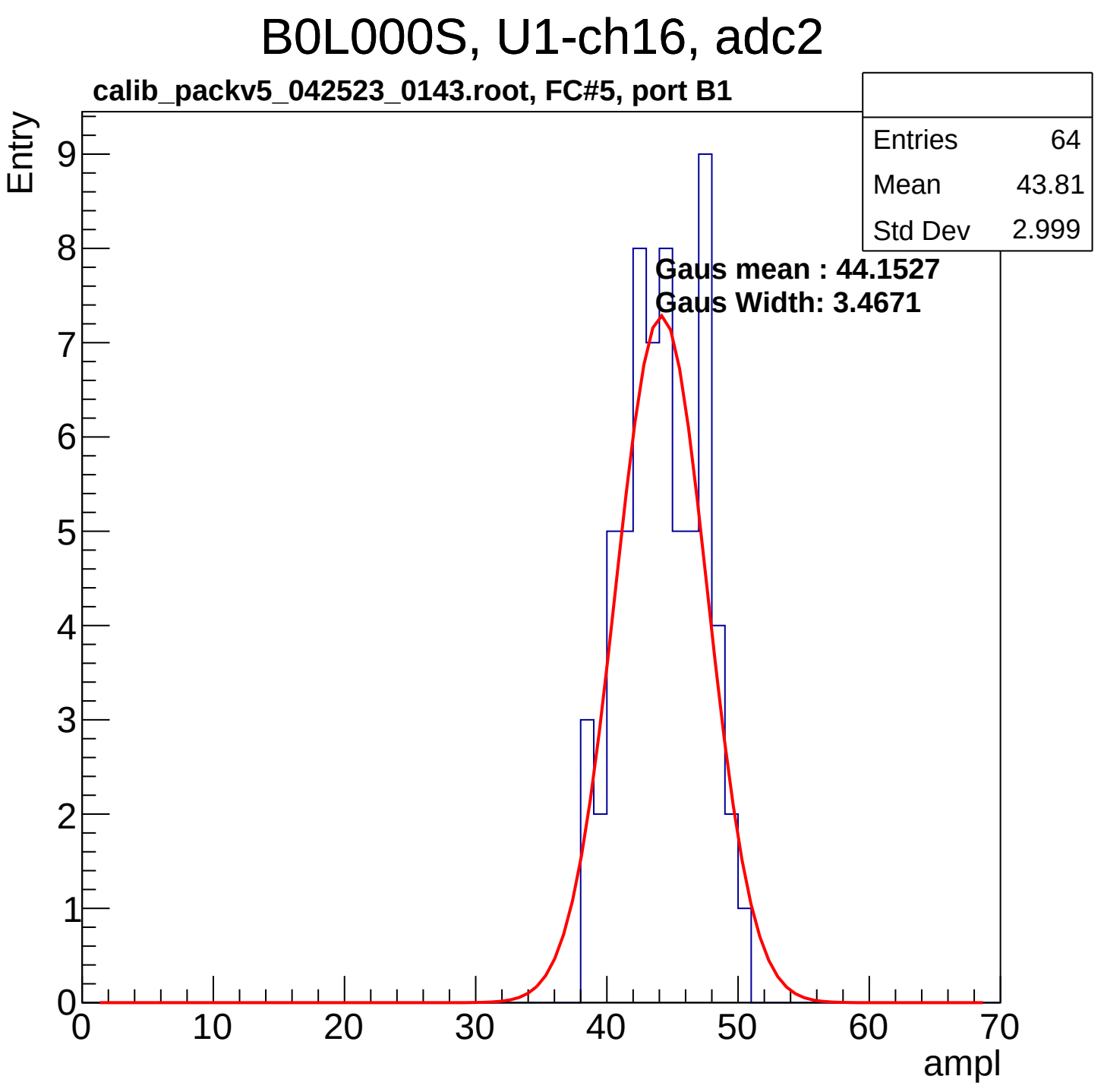
B0L000S, U1-ch16, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.81
Std Dev 2.999
Gaus mean : 44.1527
Gaus Width: 3.4671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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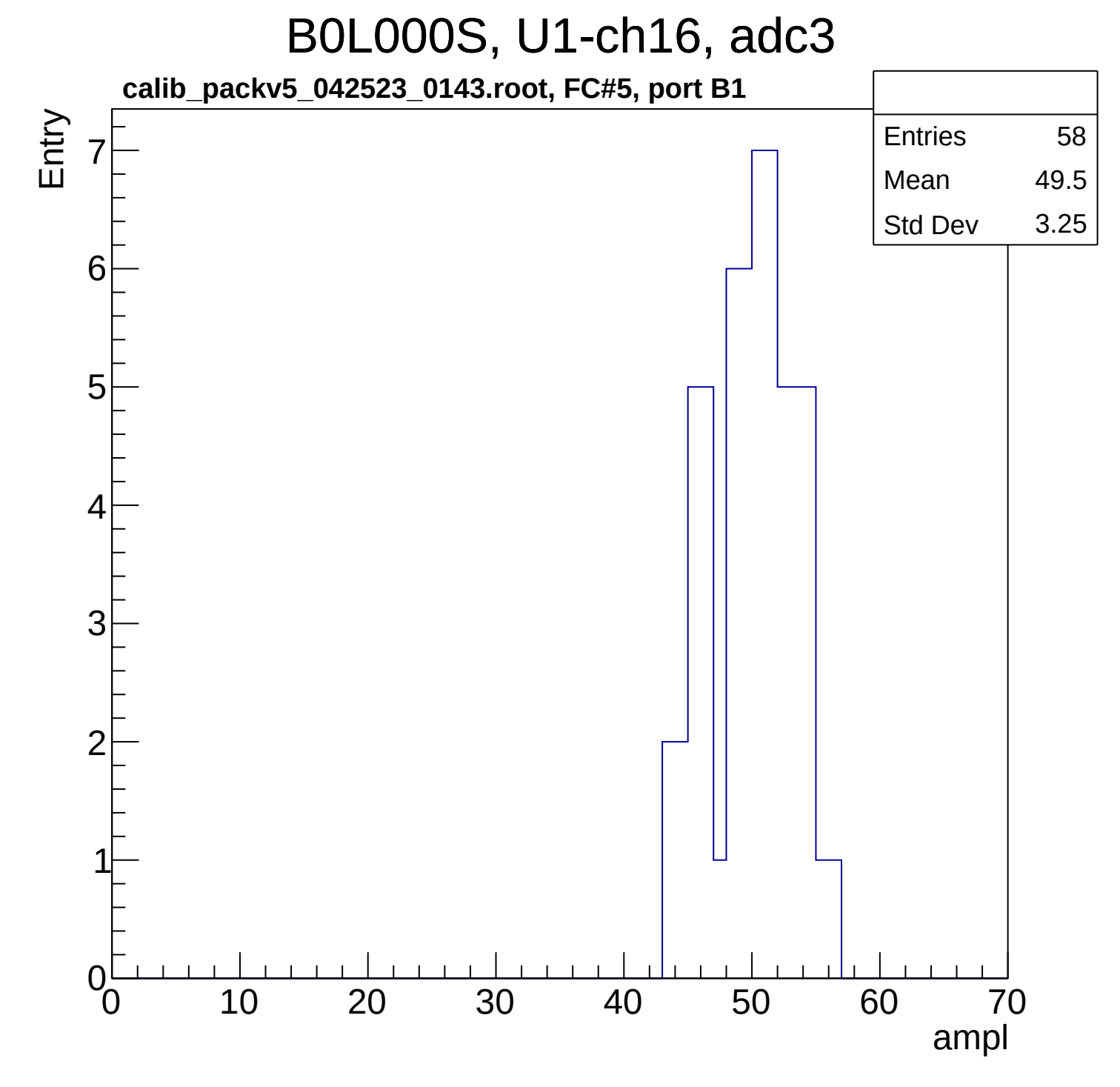
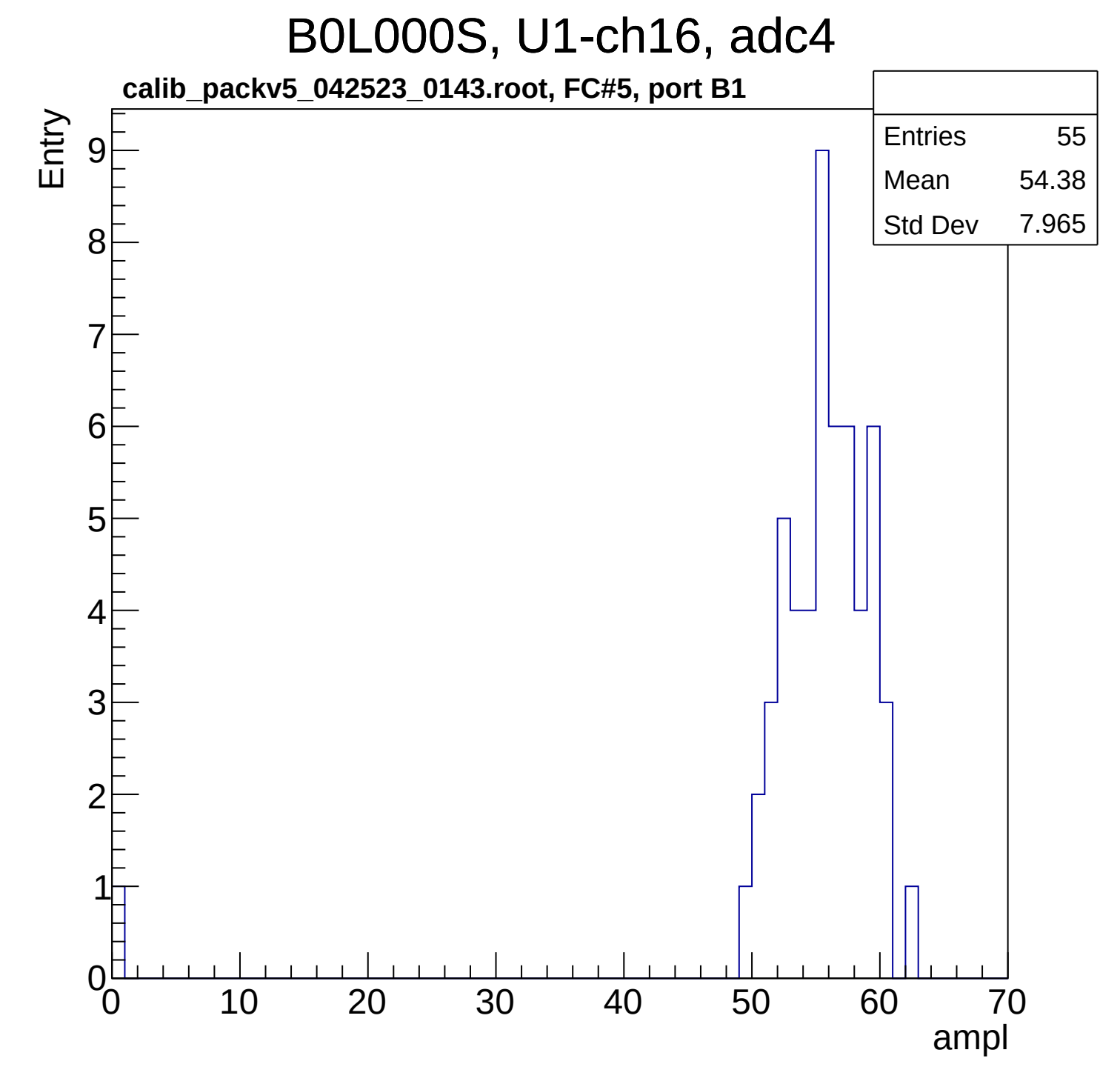
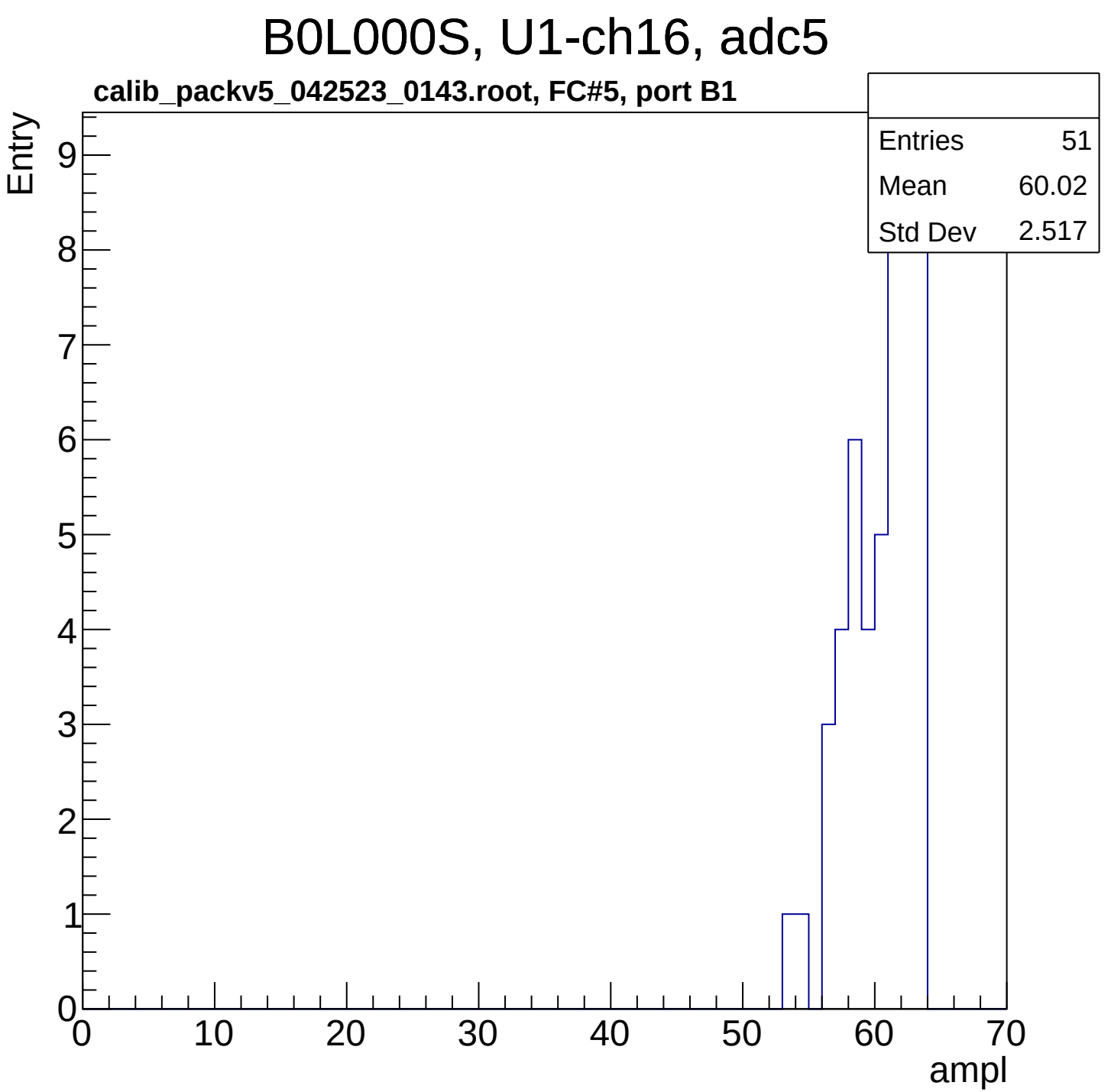
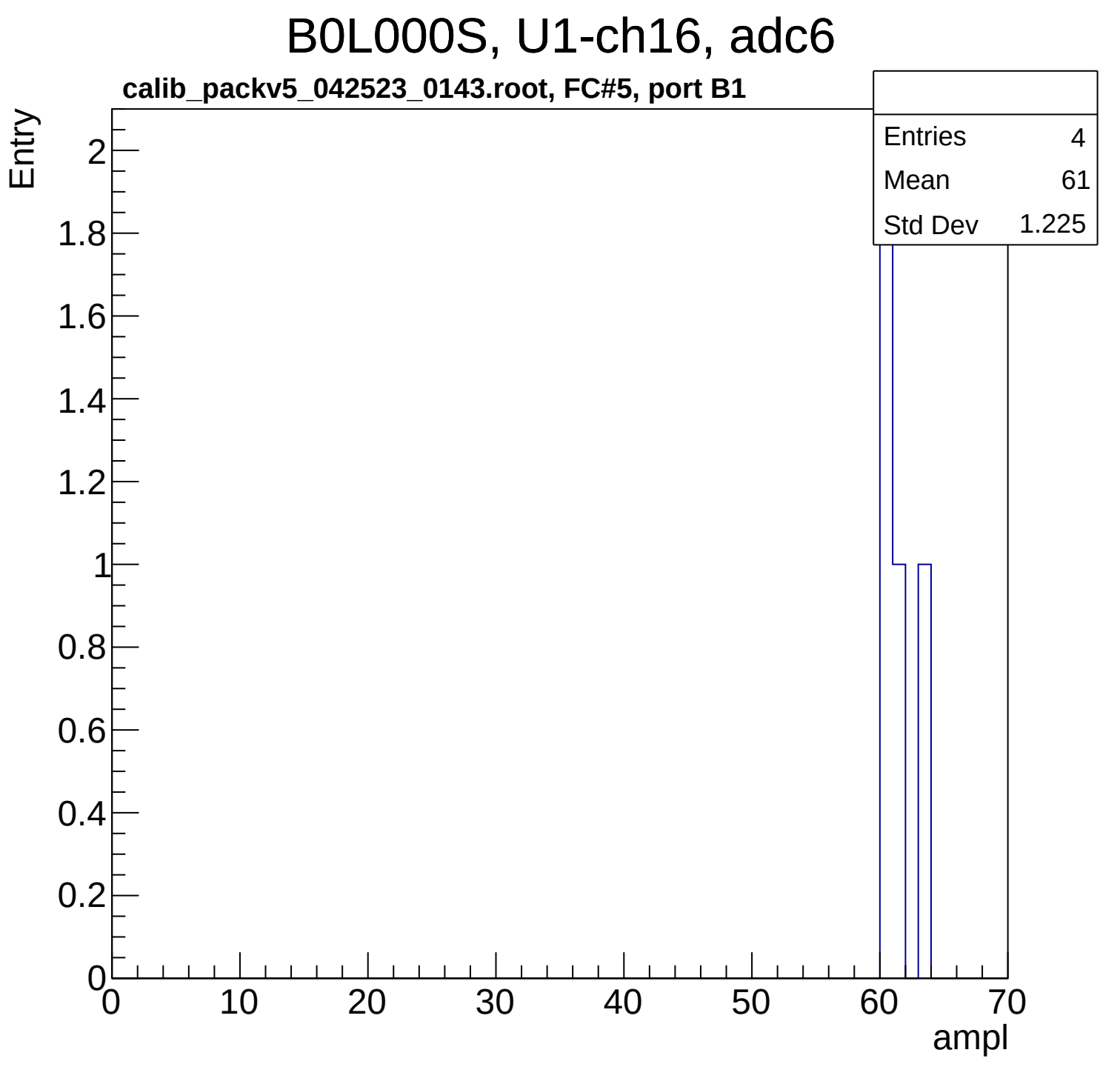
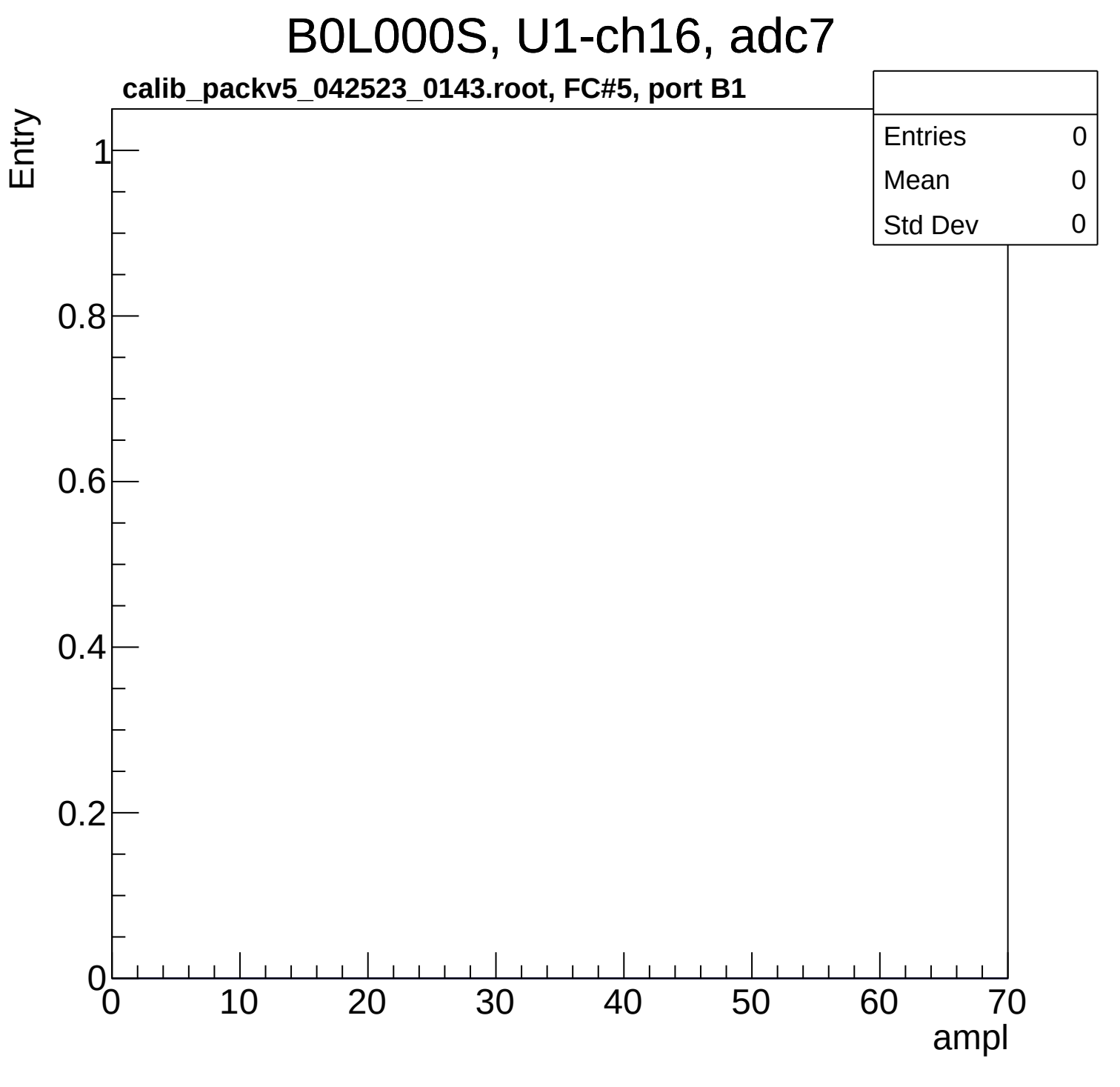

B0L000S, U1-ch16, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 49.5
Std Dev 3.25
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch16, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 54.38
Std Dev 7.965
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch16, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 60.02
Std Dev 2.517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch16, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch16, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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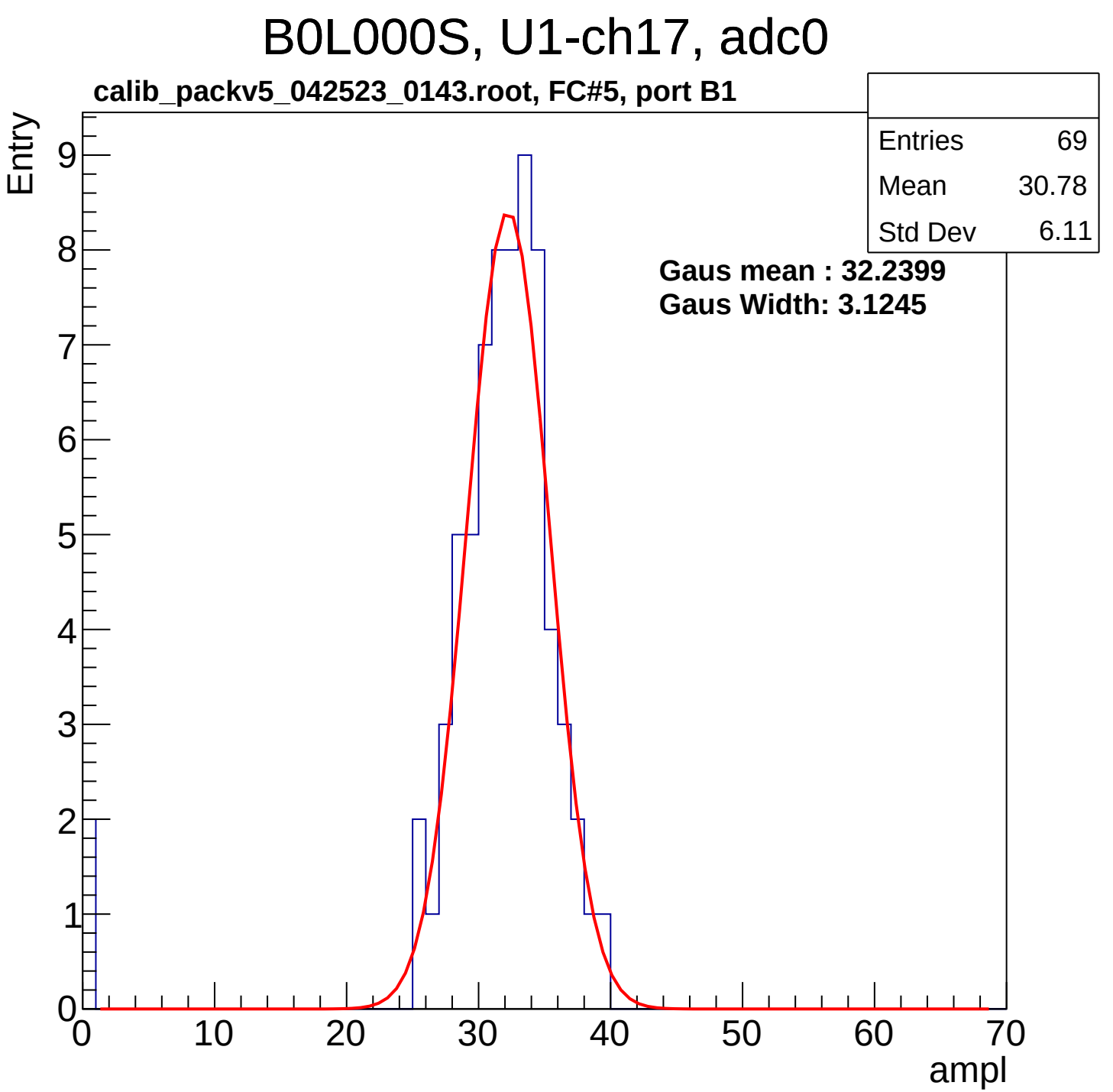
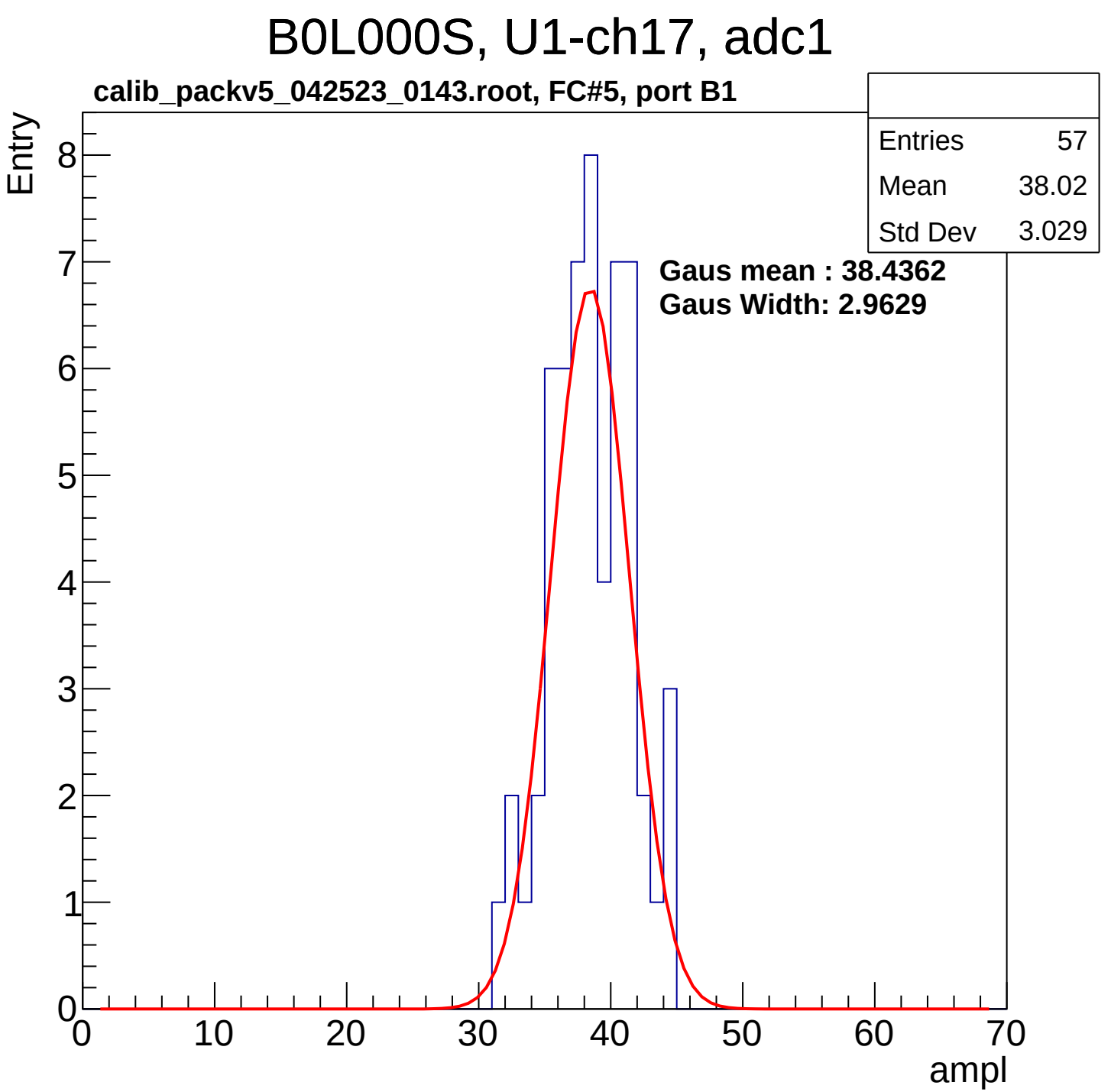
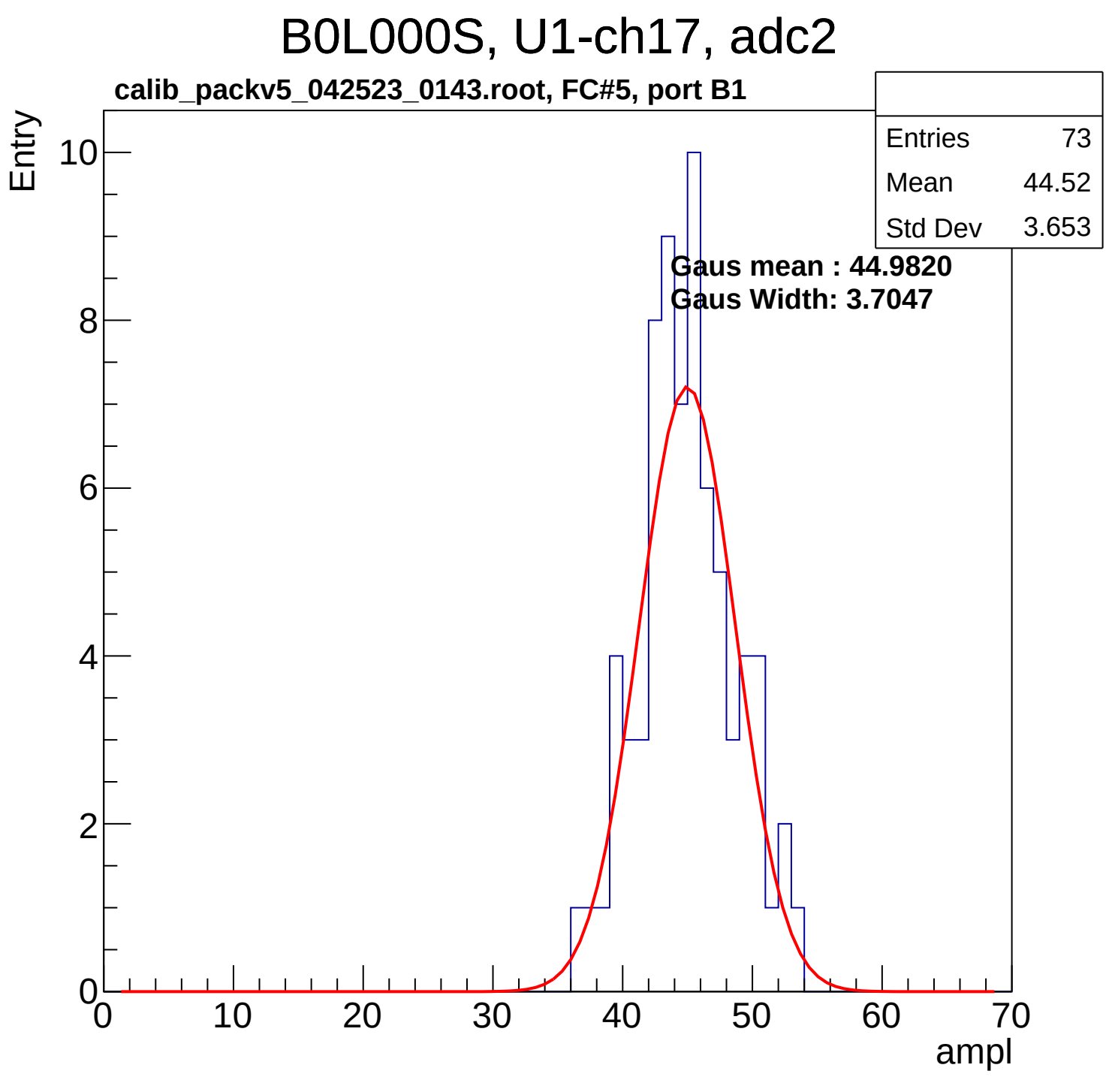
50
60
70
ampl


B0L000S, U1-ch17, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 30.78
Std Dev 6.11
Gaus mean : 32.2399
Gaus Width: 3.1245
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch17, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 38.02
Std Dev 3.029
Gaus mean : 38.4362
Gaus Width: 2.9629
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch17, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 44.52
Std Dev 3.653
Gaus mean : 44.9820
Gaus Width: 3.7047
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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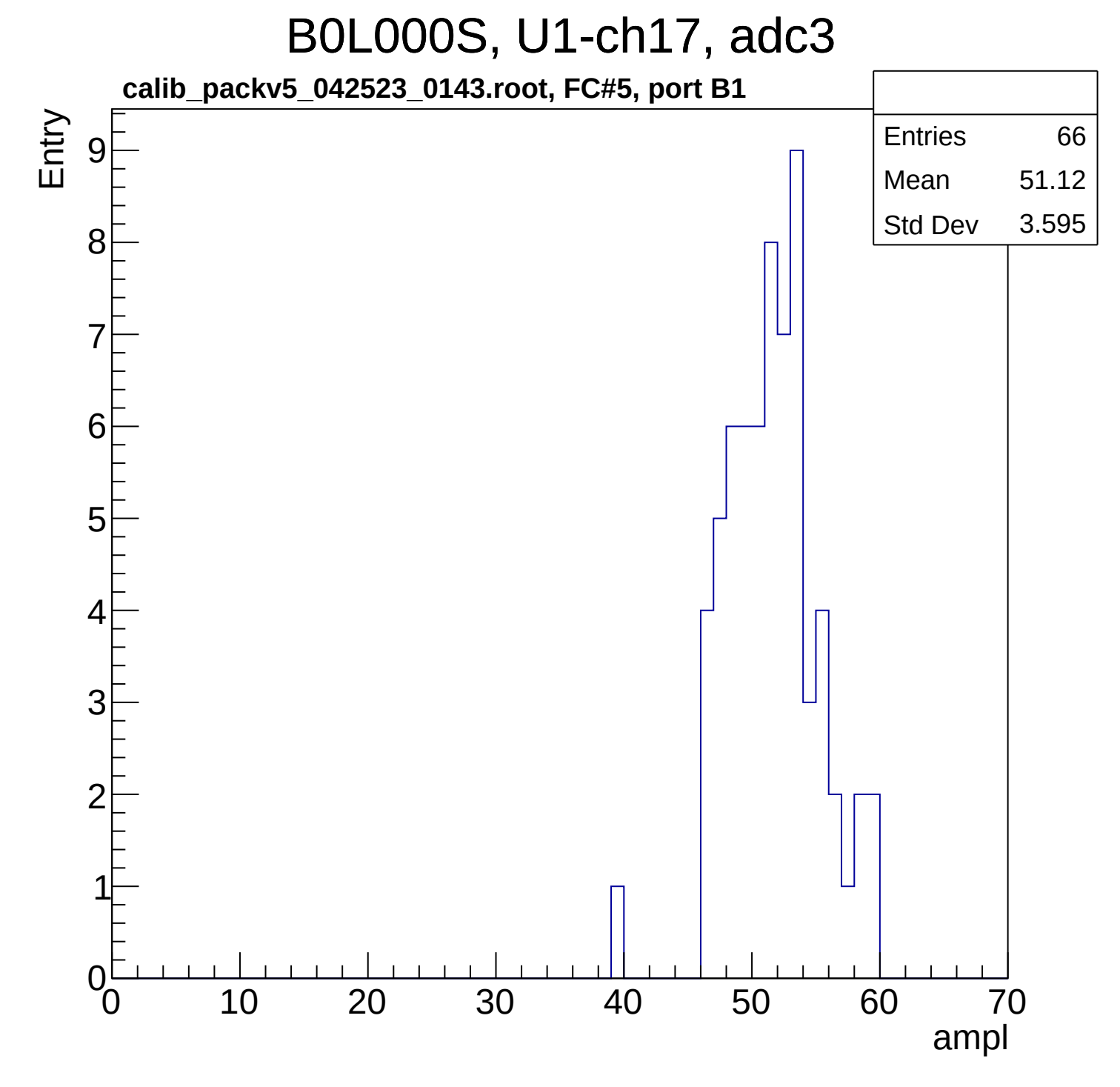
B0L000S, U1-ch17, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 51.12
Std Dev 3.595
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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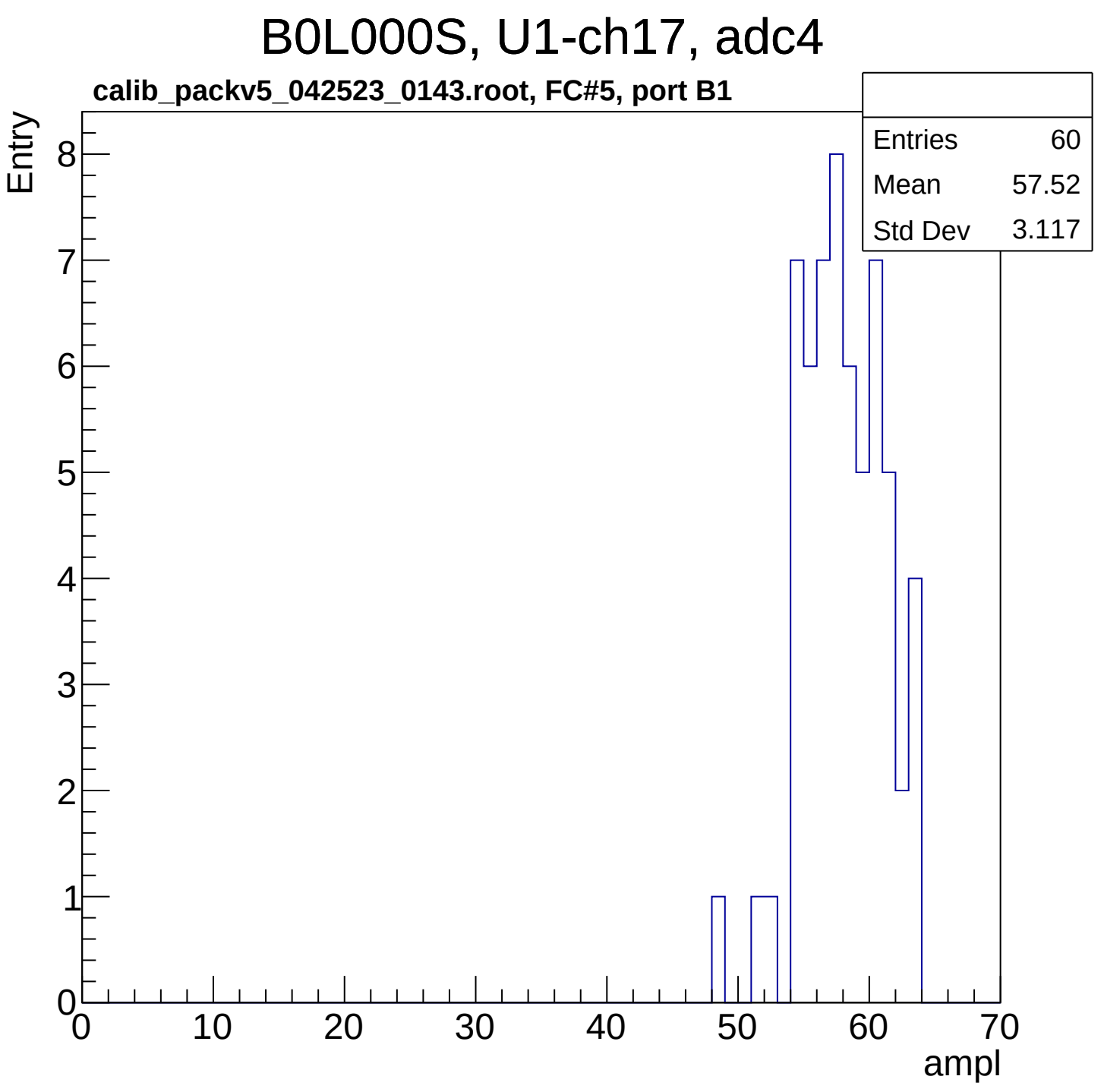
B0L000S, U1-ch17, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.52
Std Dev 3.117
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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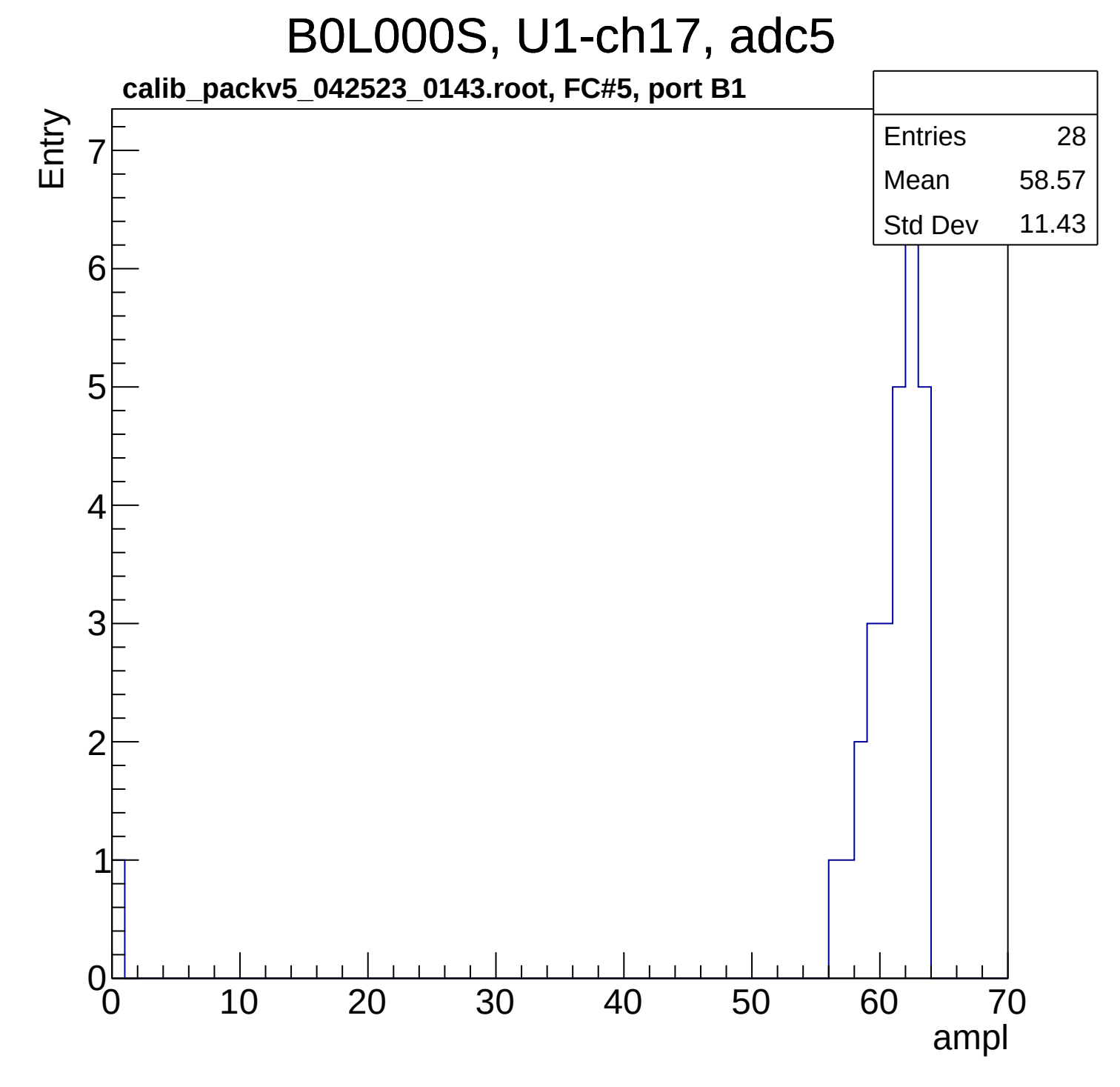
B0L000S, U1-ch17, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.57
Std Dev 11.43
Entry
ampl
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3
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0
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20
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40
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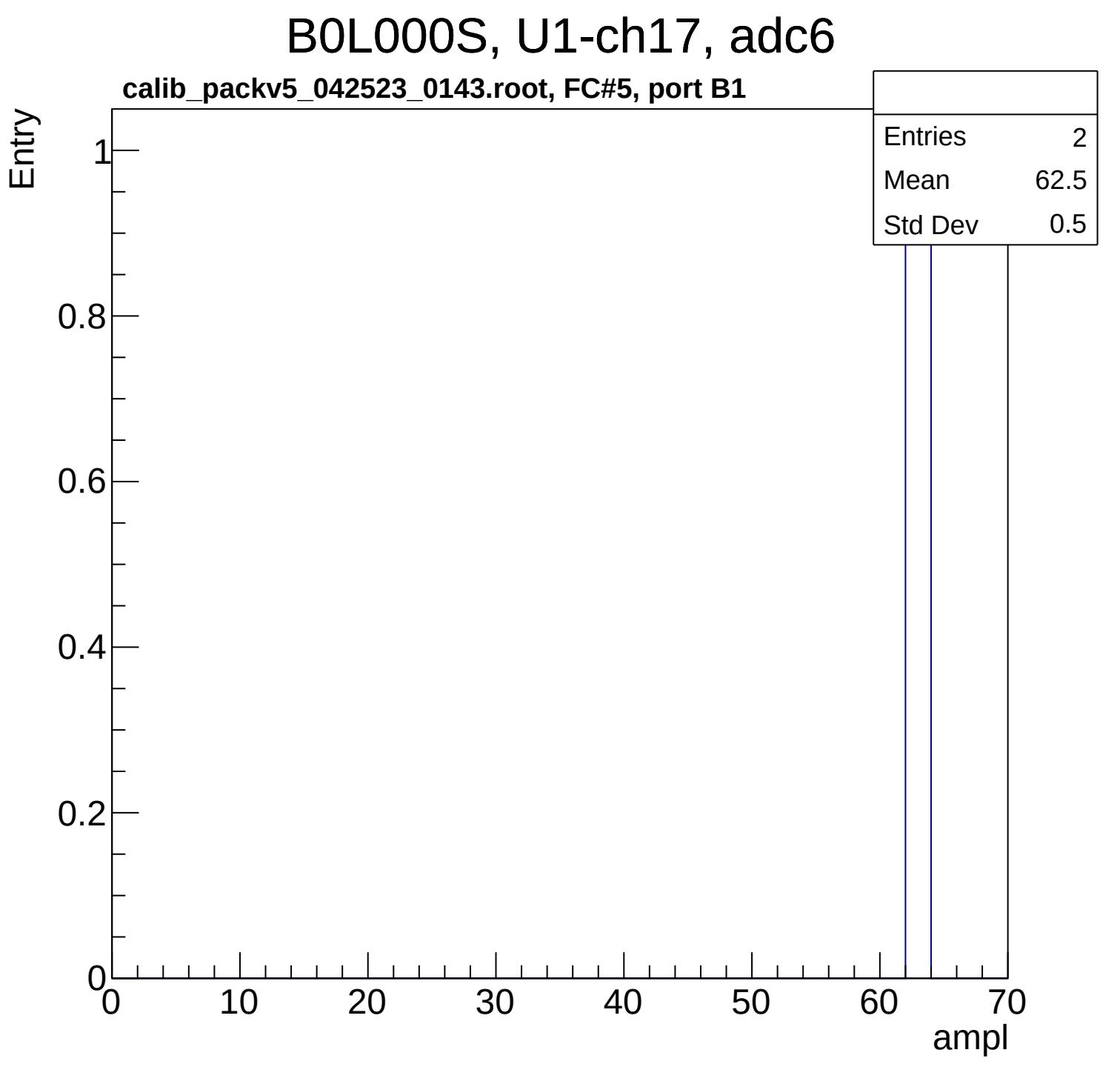
B0L000S, U1-ch17, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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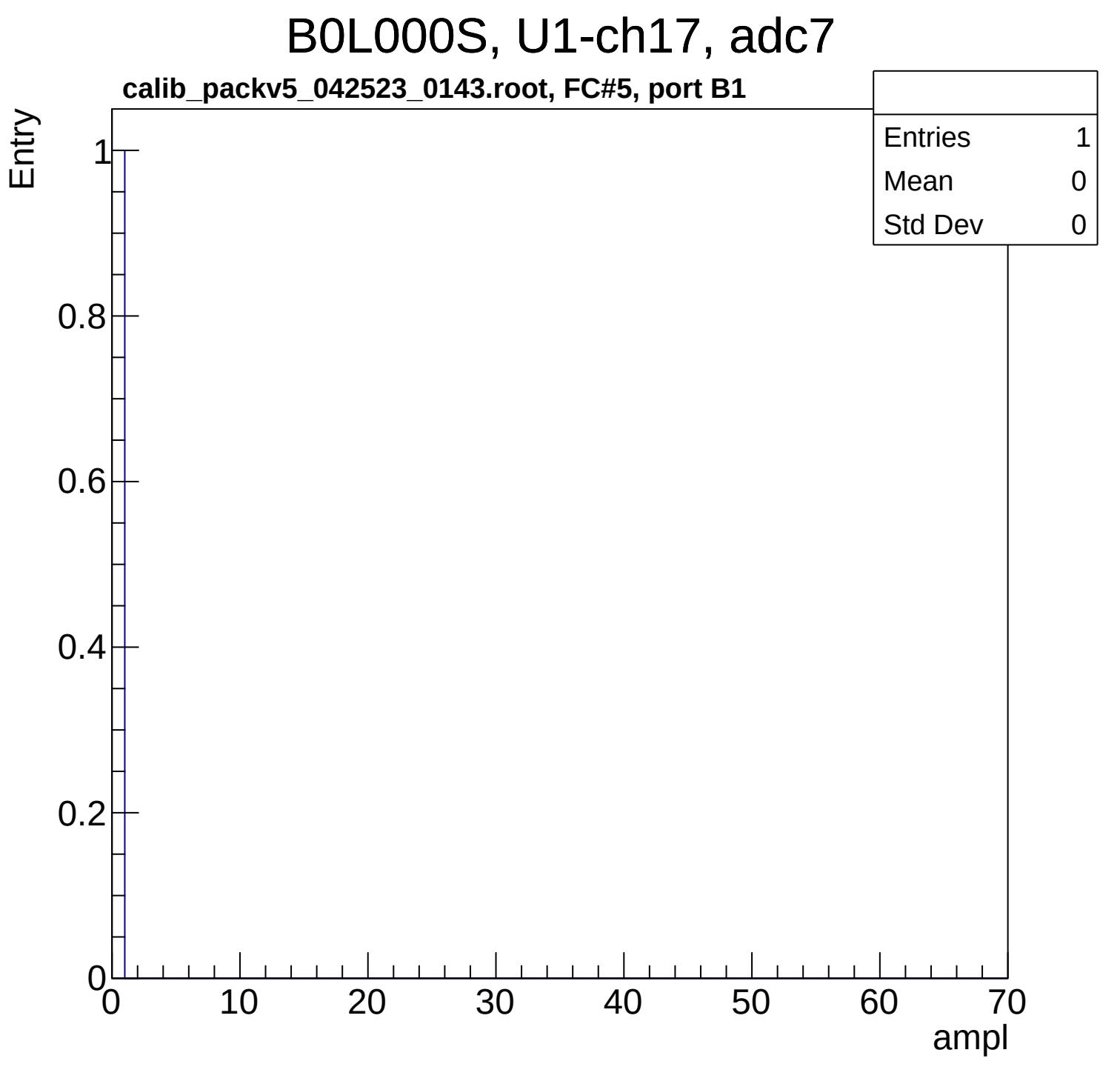
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch17, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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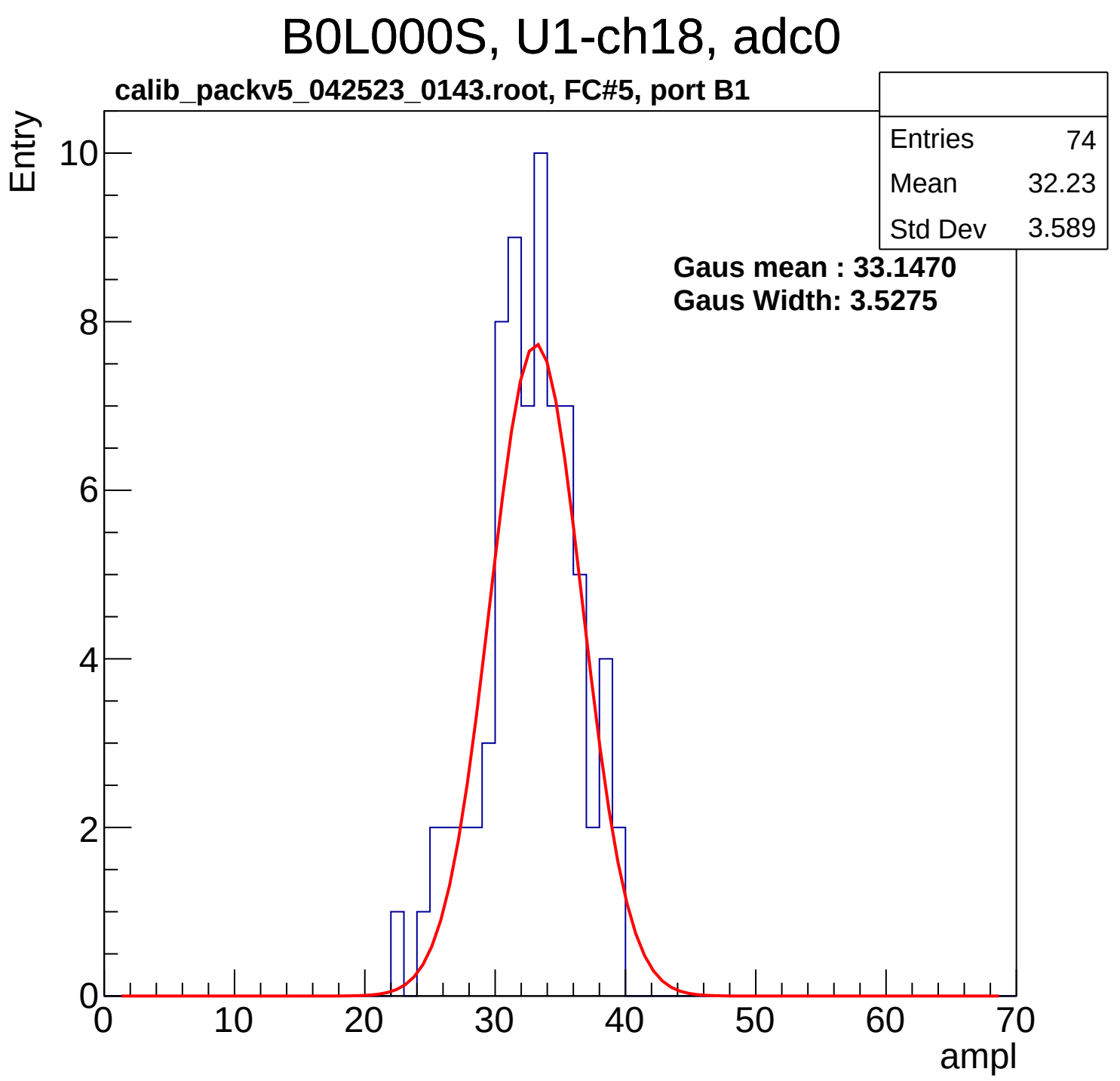
50
60
70
ampl


B0L000S, U1-ch18, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 32.23
Std Dev 3.589
Gaus mean : 33.1470
Gaus Width: 3.5275
Entry
ampl
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2
4
6
8
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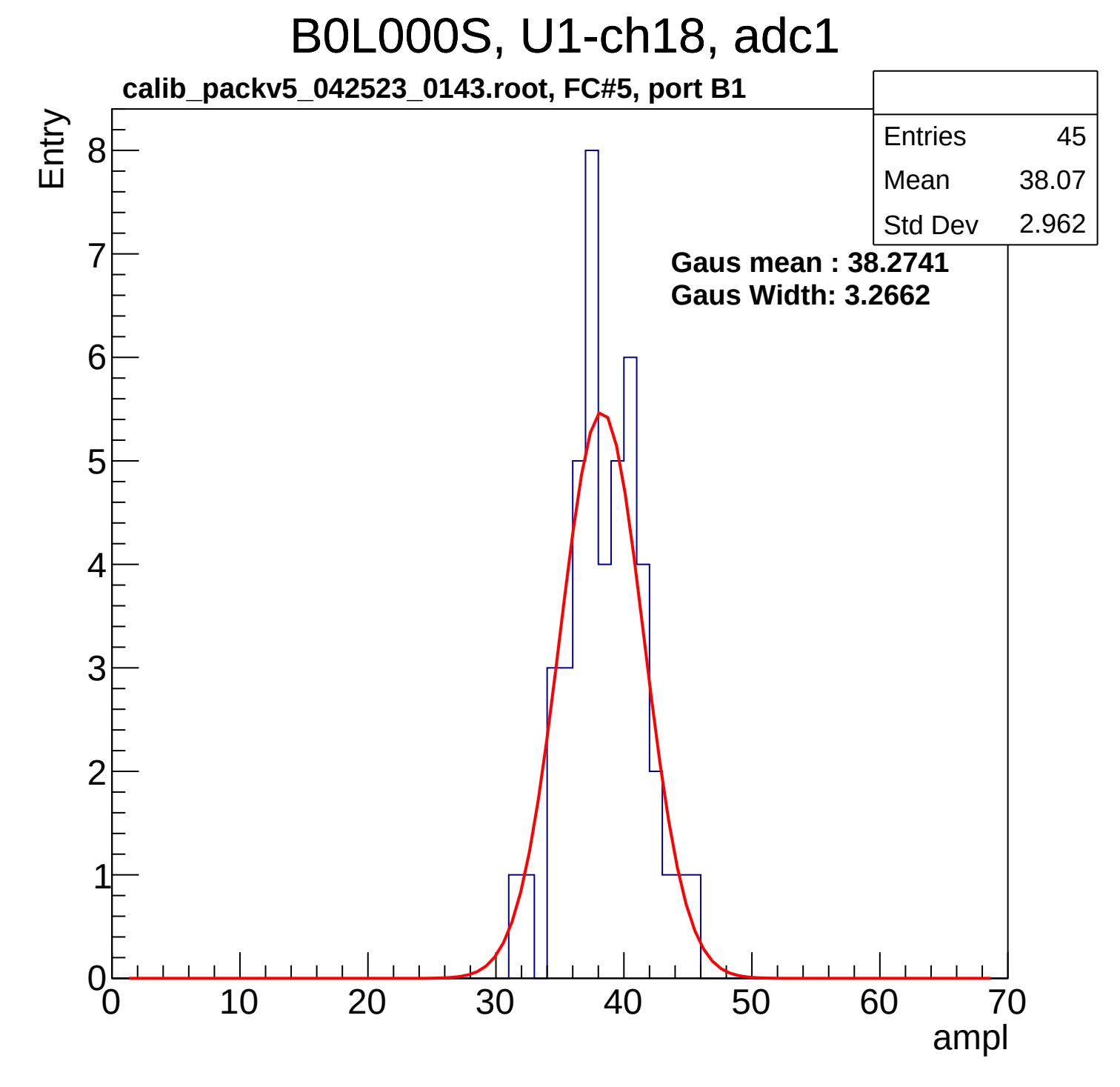
0
10
20
30
40
50
60
70


B0L000S, U1-ch18, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 38.07
Std Dev 2.962
Gaus mean : 38.2741
Gaus Width: 3.2662
Entry
ampl
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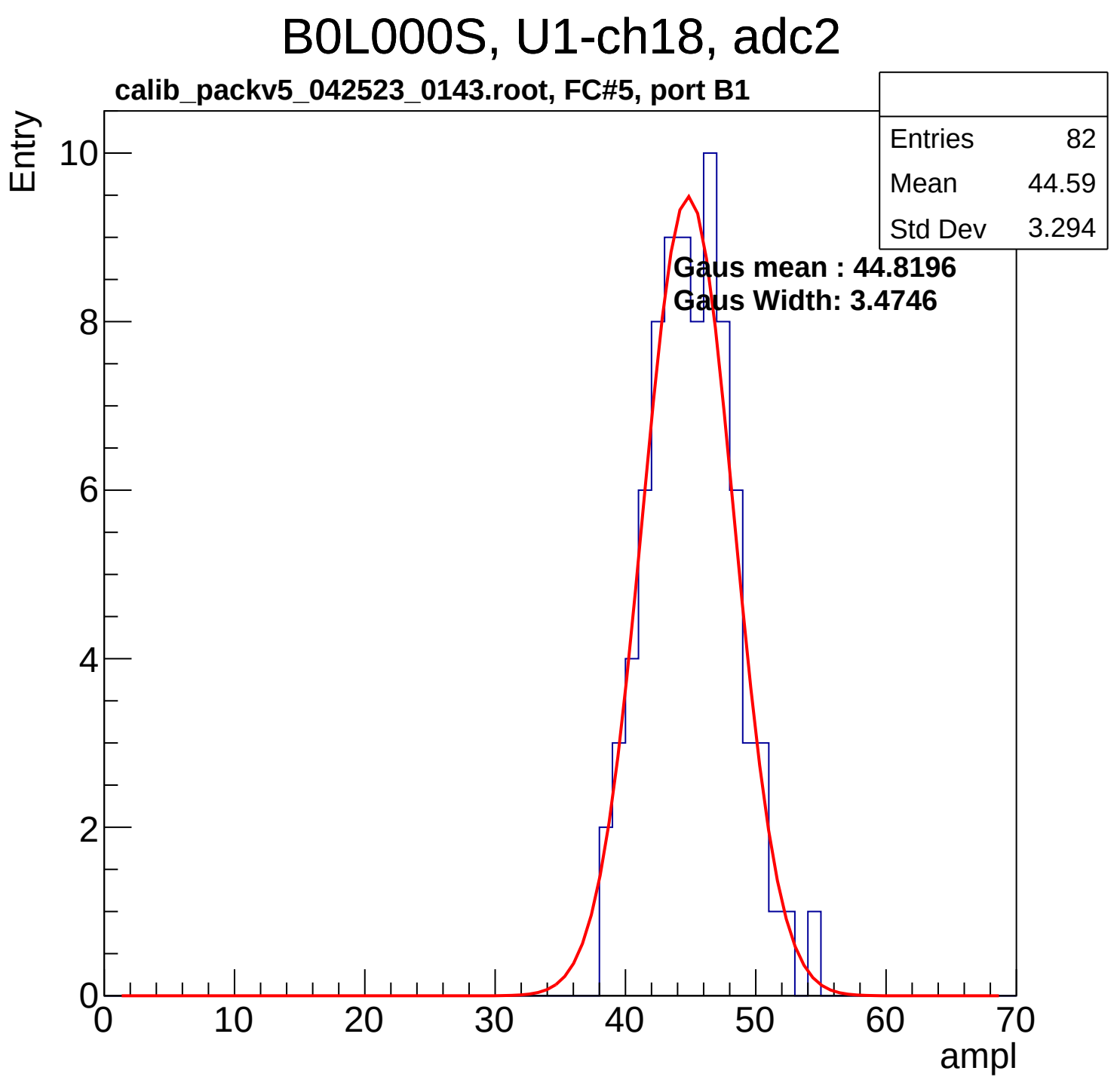
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch18, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 44.59
Std Dev 3.294
Gaus mean : 44.8196
Gaus Width: 3.4746
Entry
ampl
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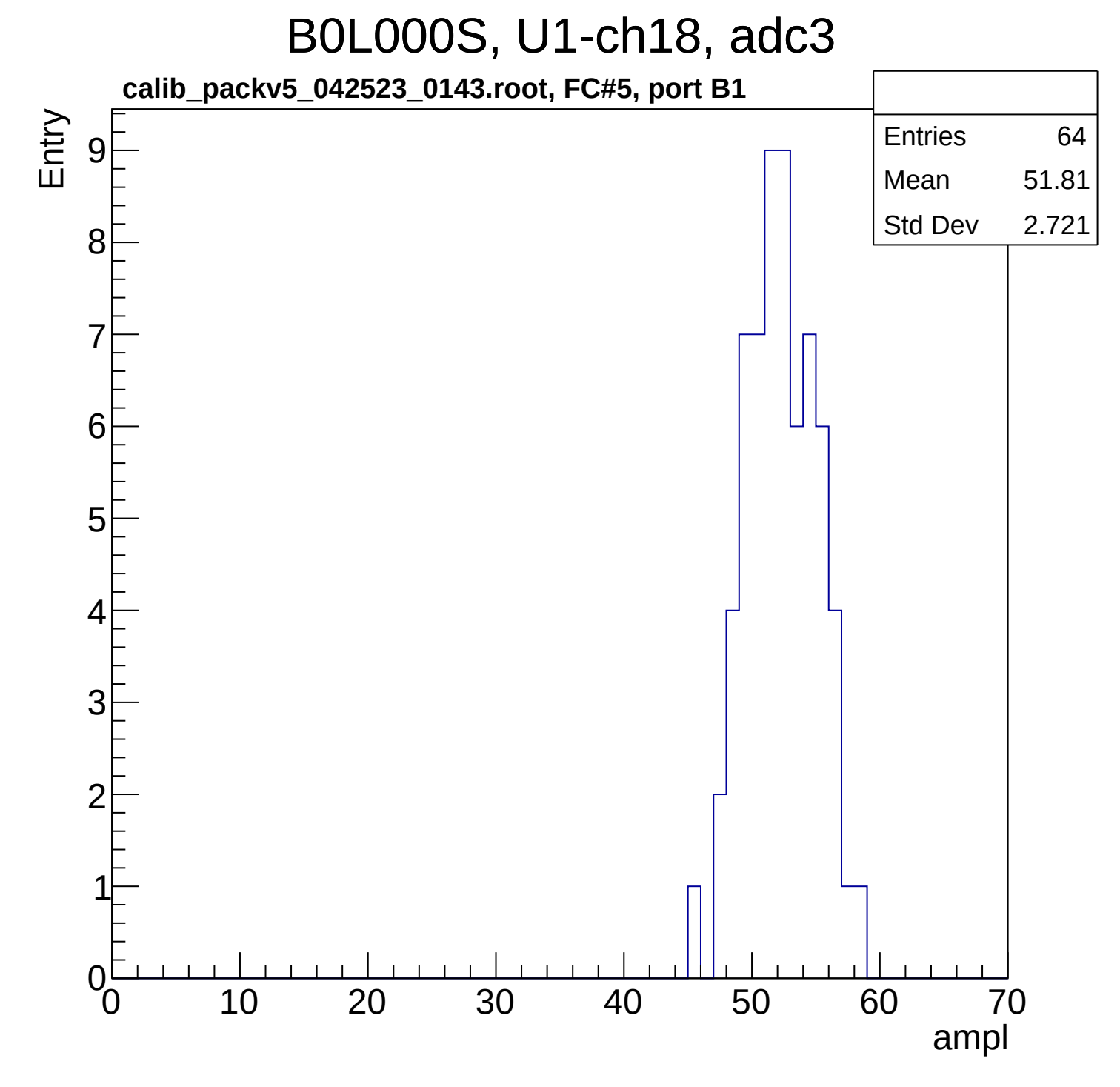
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch18, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 51.81
Std Dev 2.721
Entry
ampl
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3
4
5
6
7
8
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0
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60
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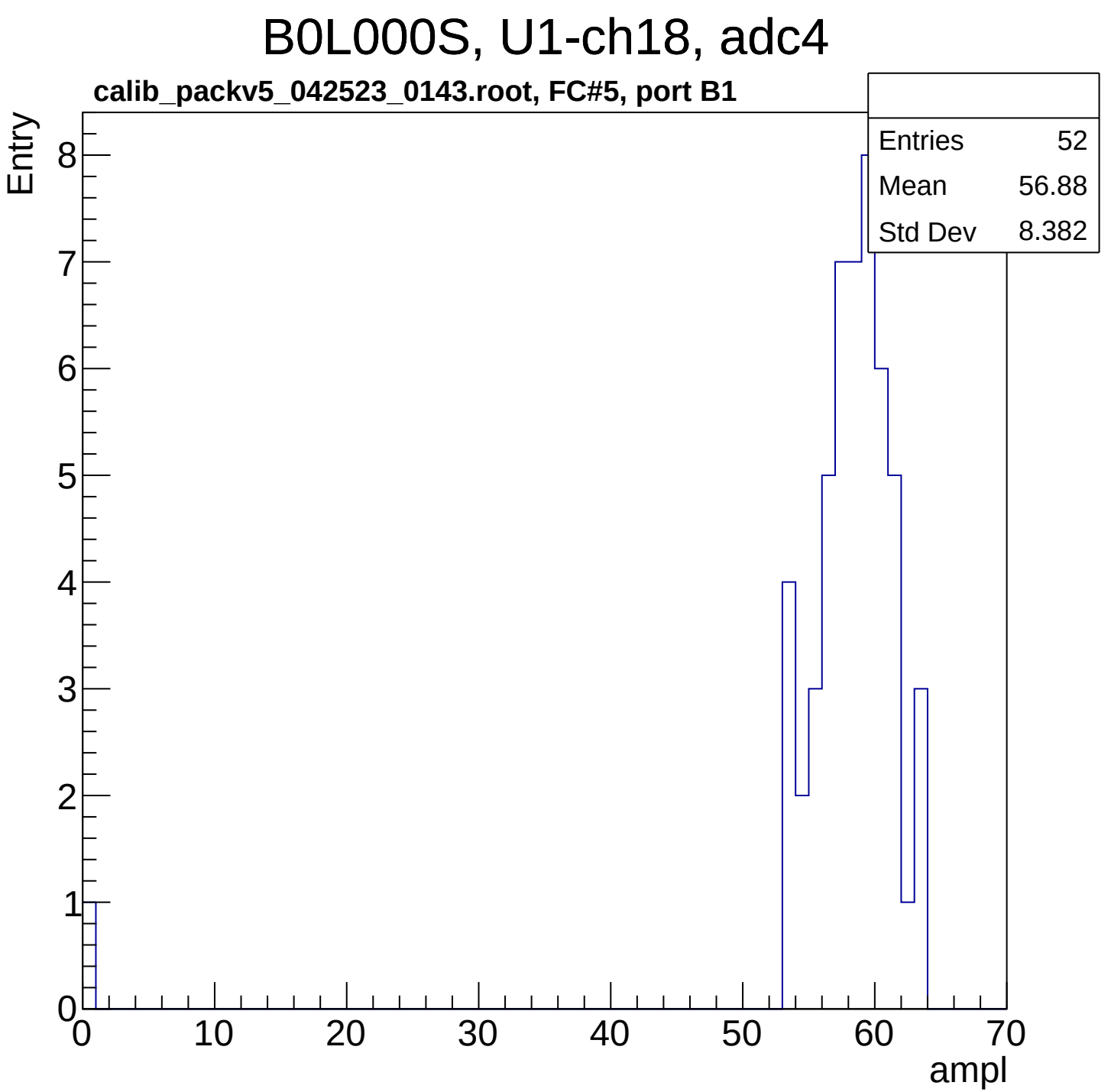
B0L000S, U1-ch18, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.88
Std Dev 8.382
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
50
60
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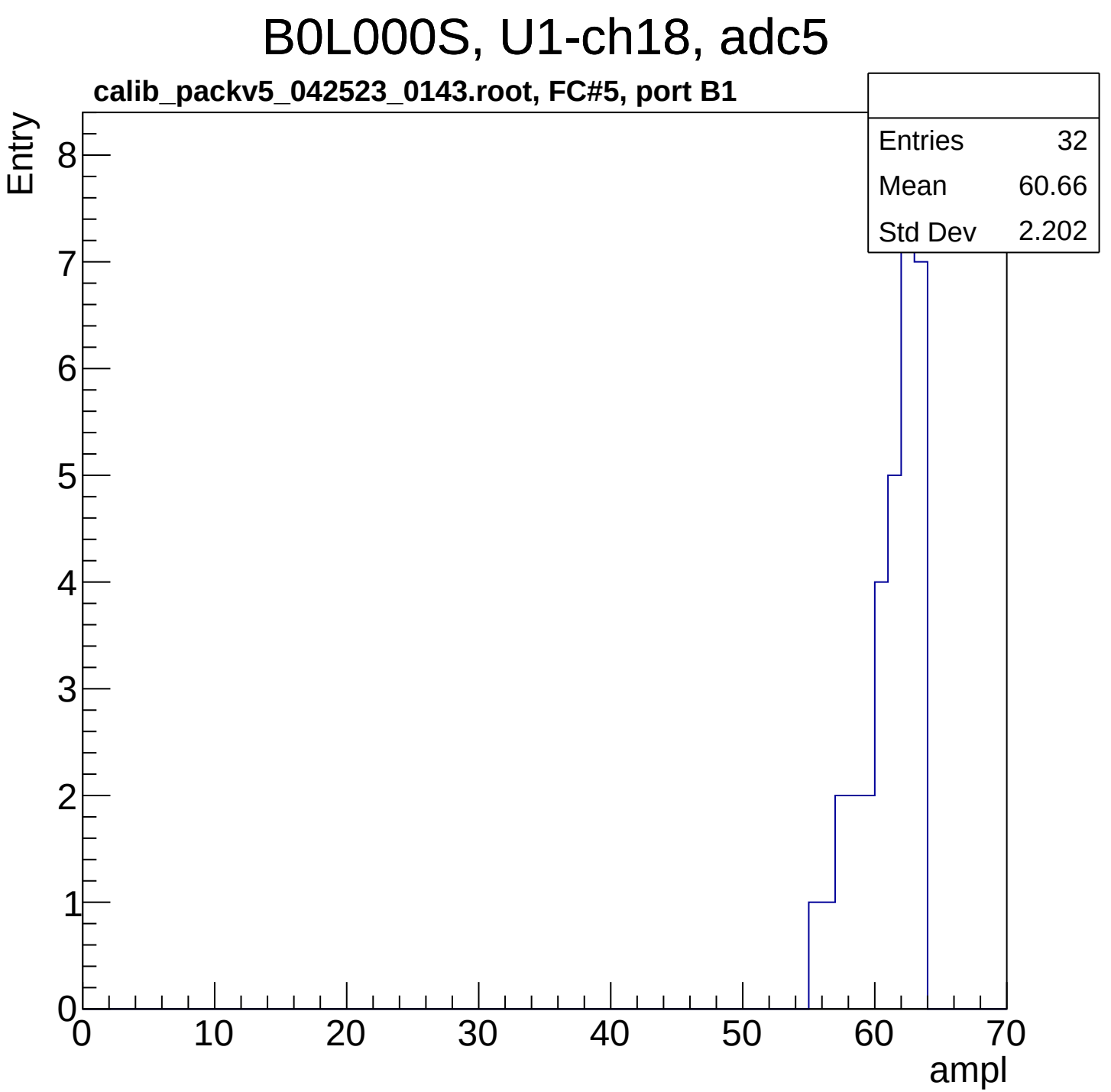
B0L000S, U1-ch18, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 60.66
Std Dev 2.202
Entry
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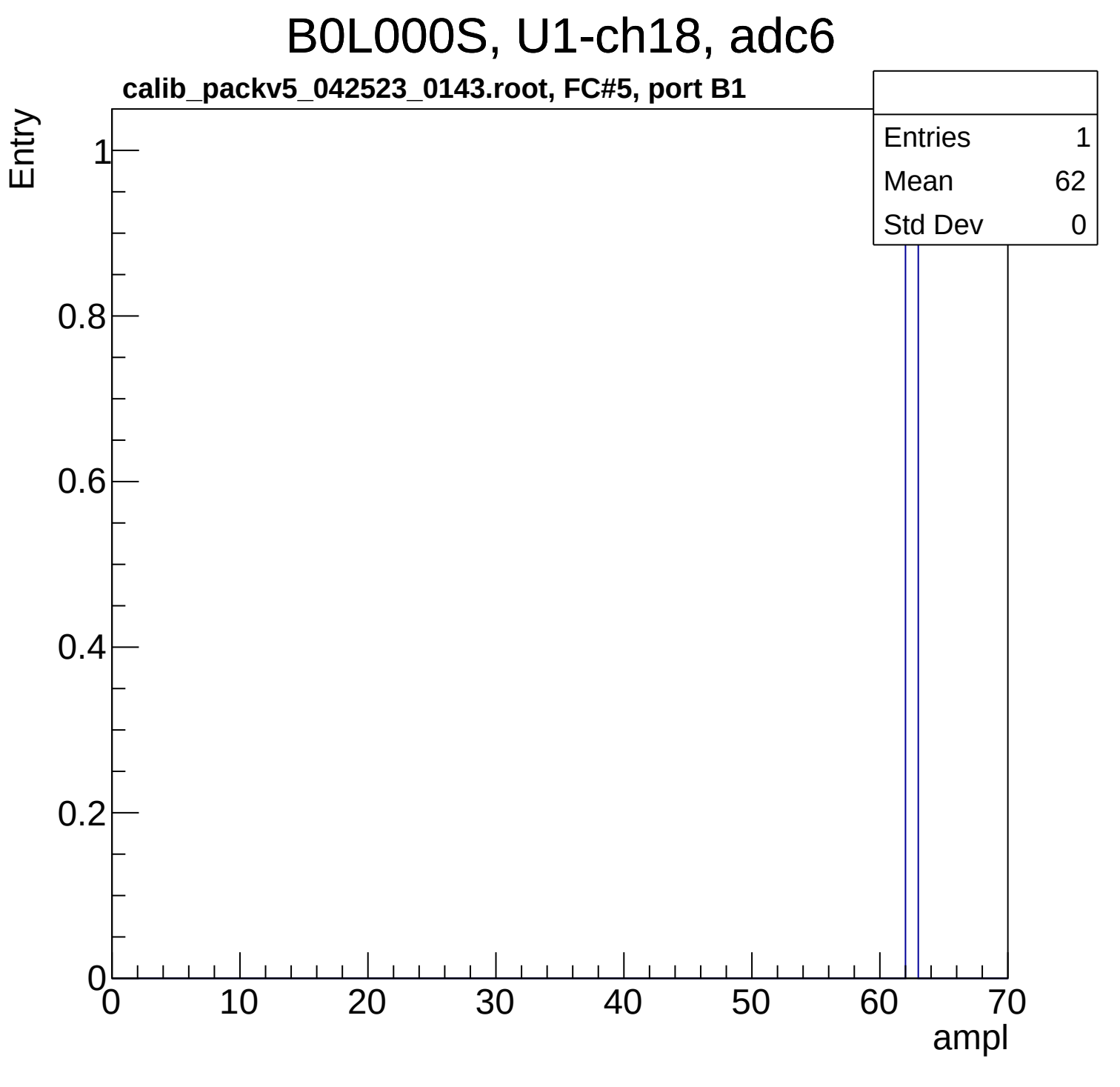
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch18, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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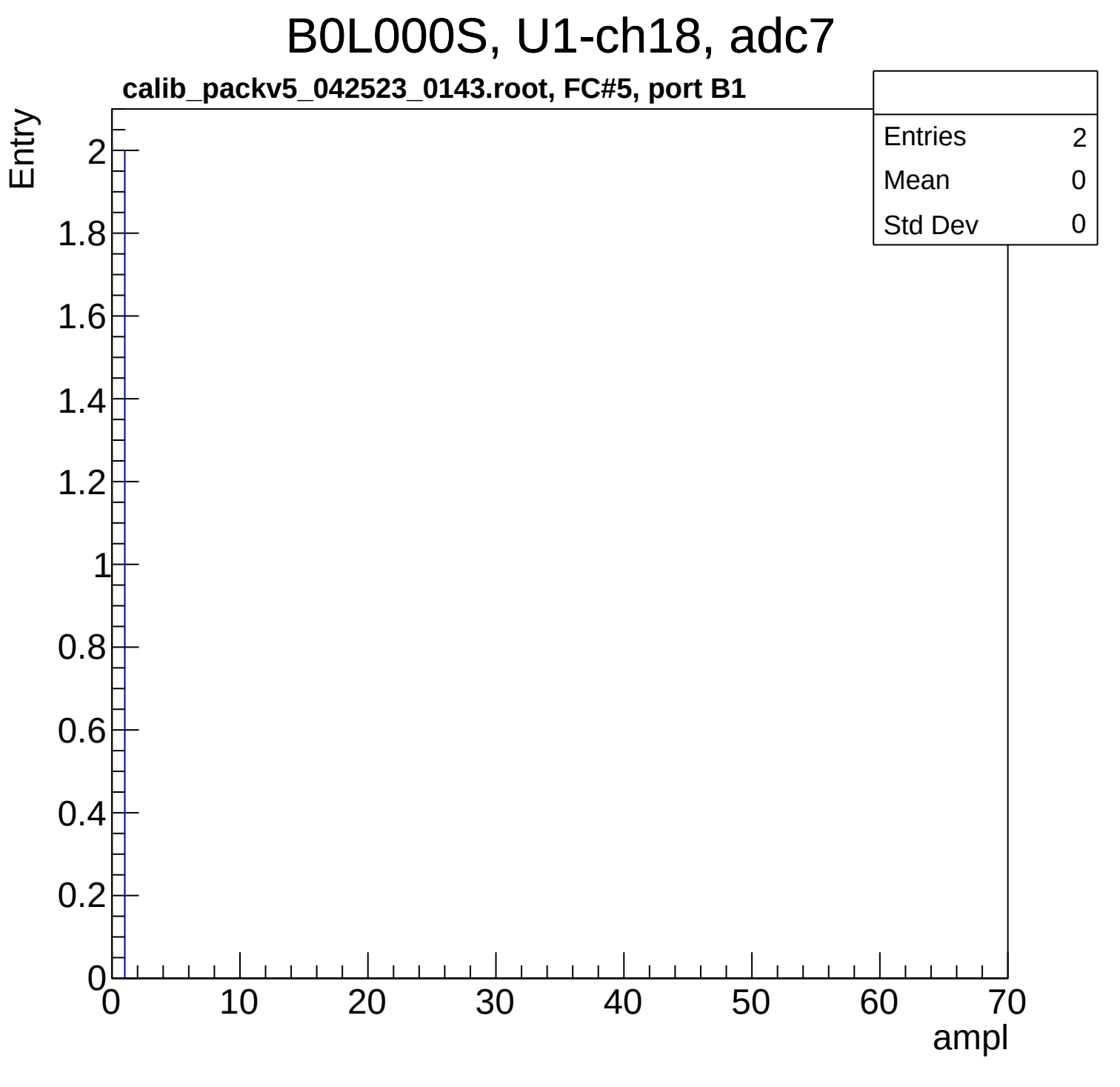
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch18, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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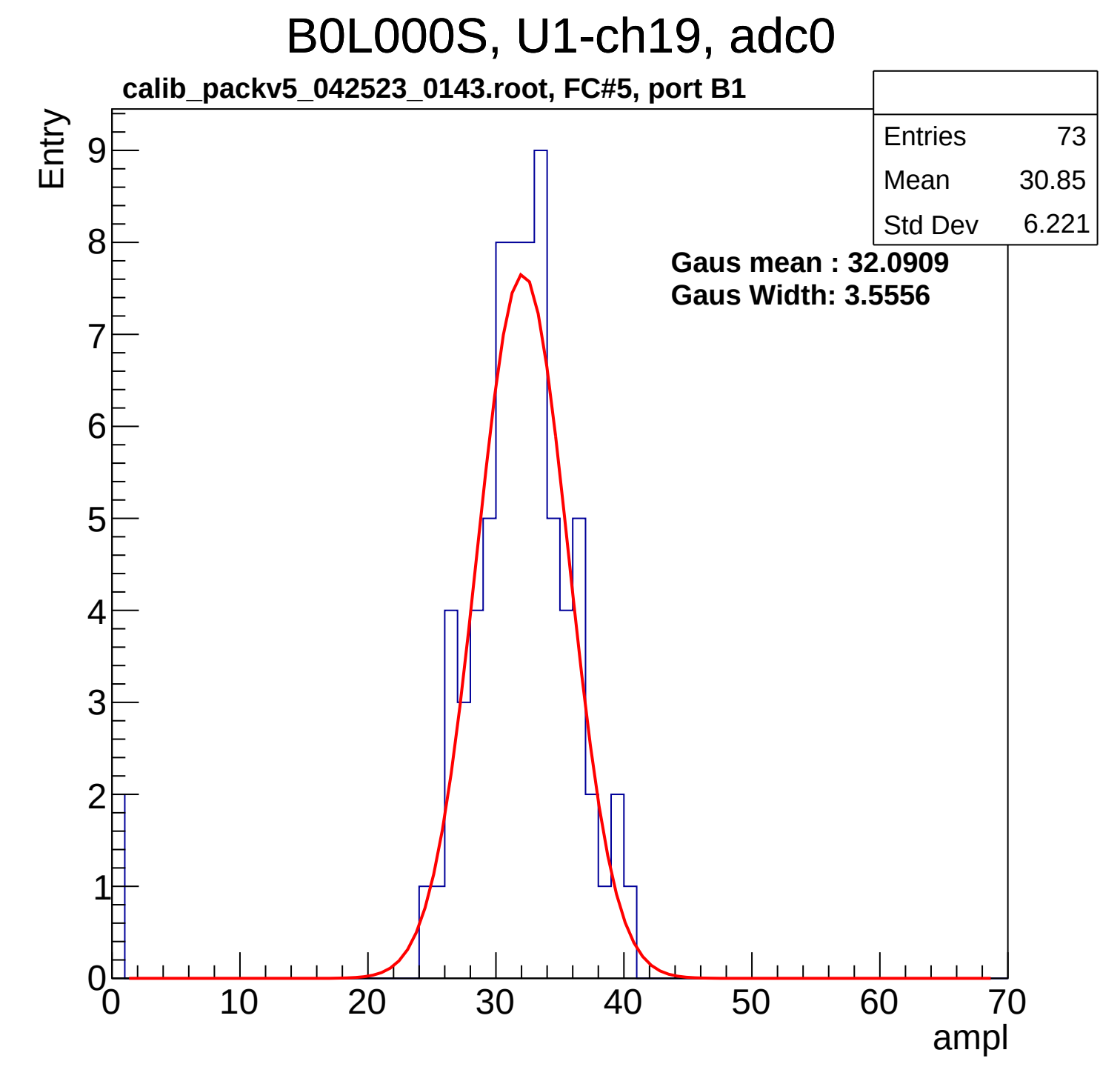
40
50
60
70


B0L000S, U1-ch19, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.85
Std Dev 6.221
Gaus mean : 32.0909
Gaus Width: 3.5556
Entry
ampl
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3
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5
6
7
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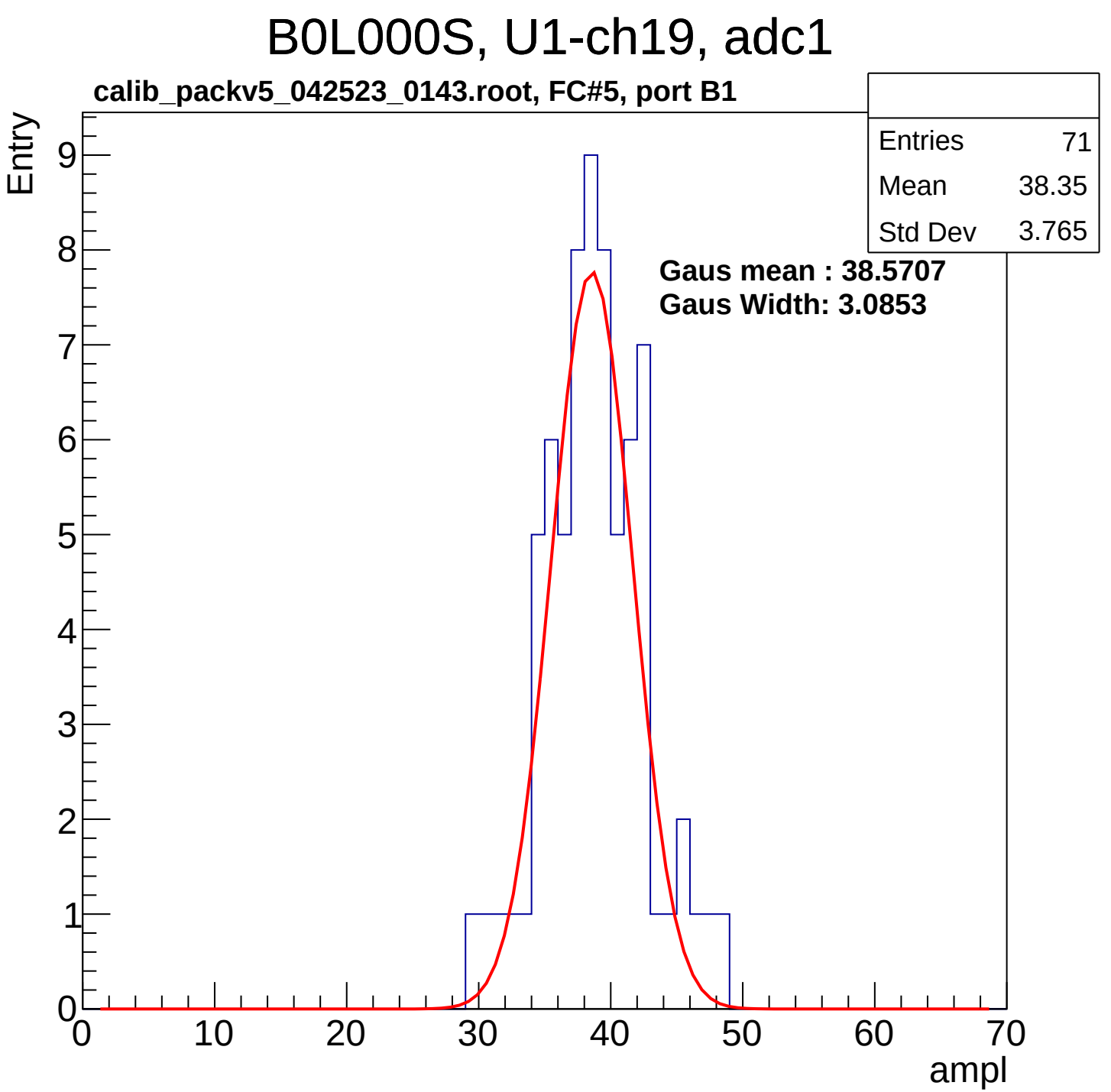
B0L000S, U1-ch19, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 38.35
Std Dev 3.765
Gaus mean : 38.5707
Gaus Width: 3.0853
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
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60
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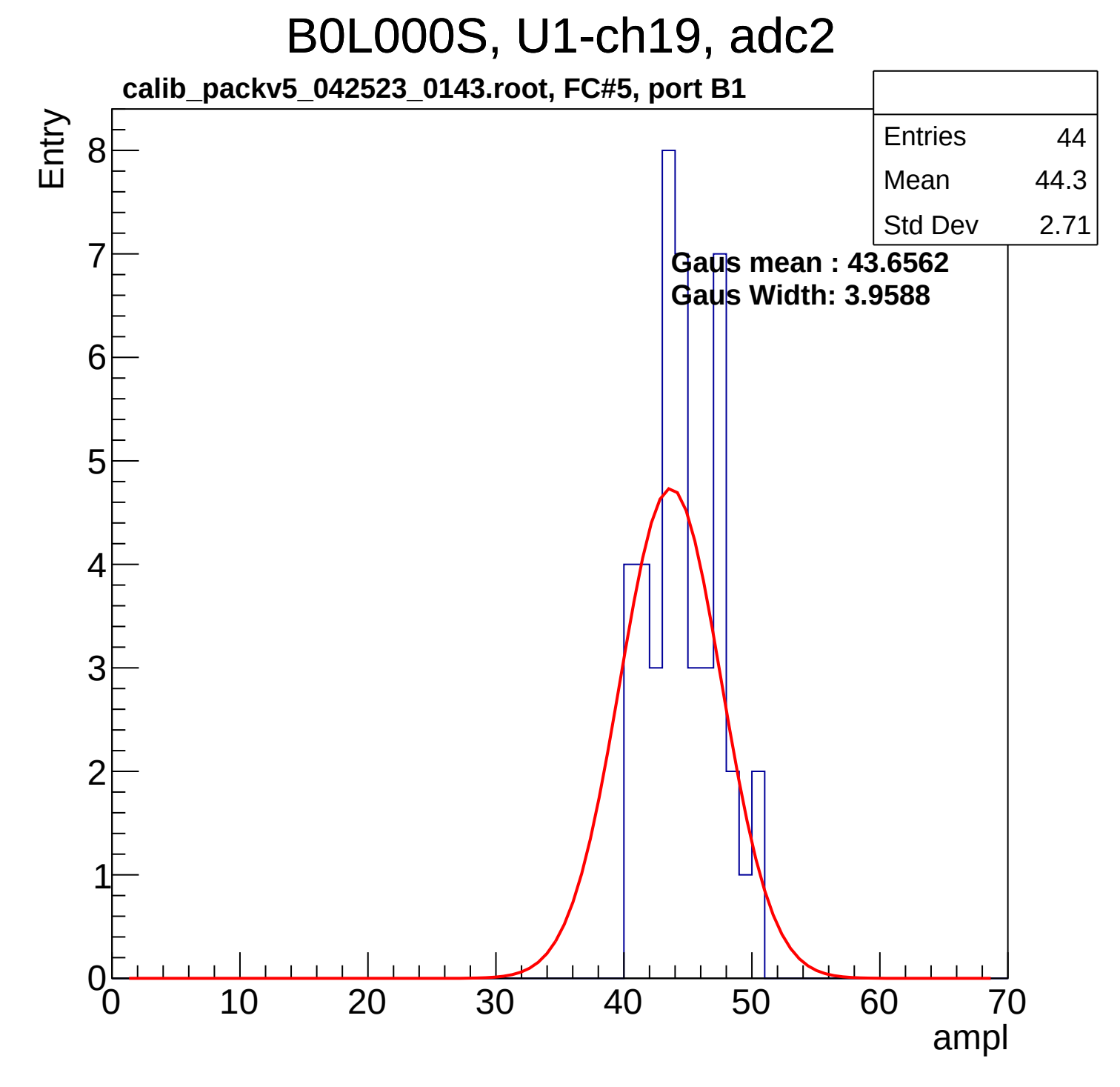
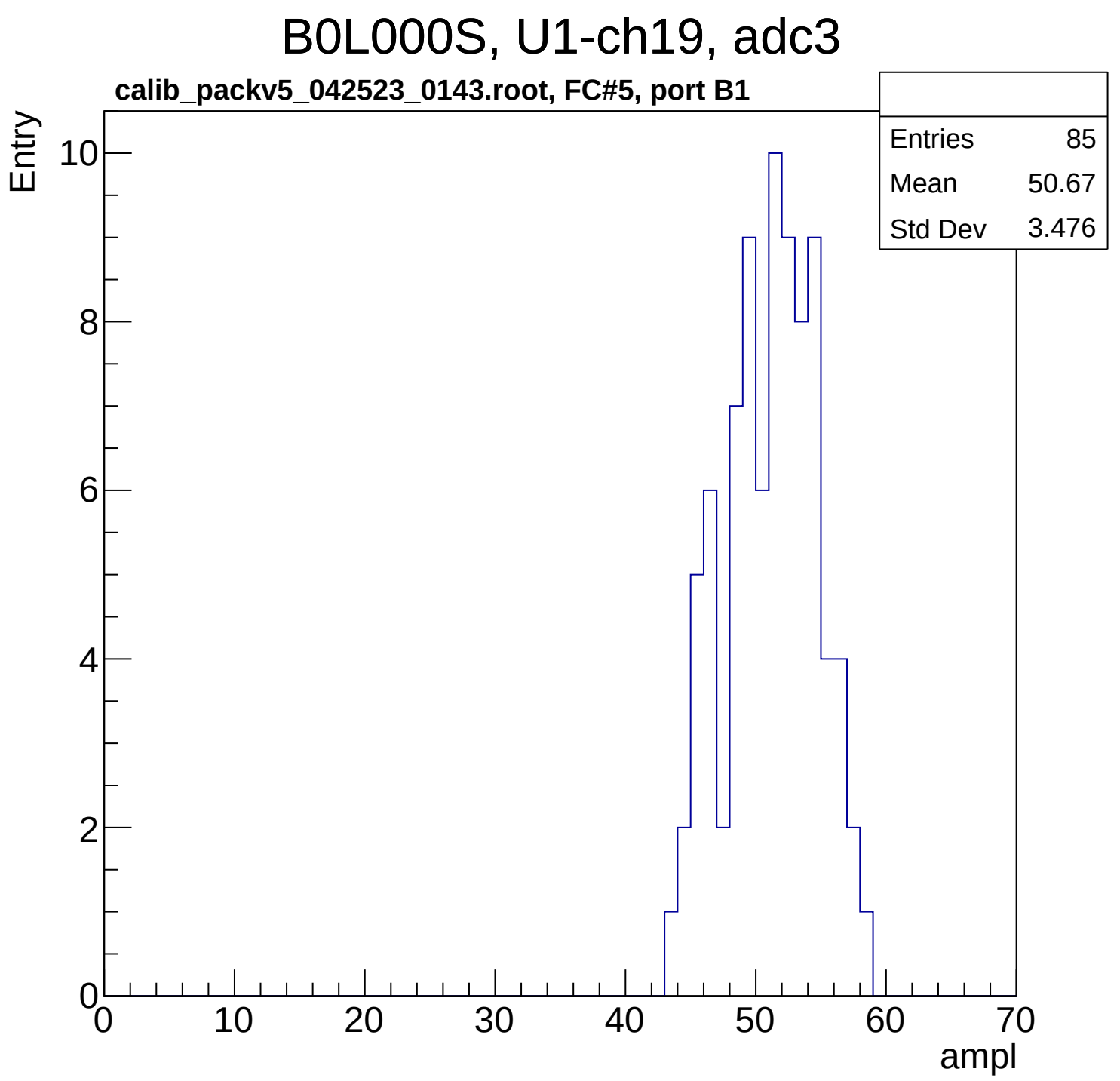
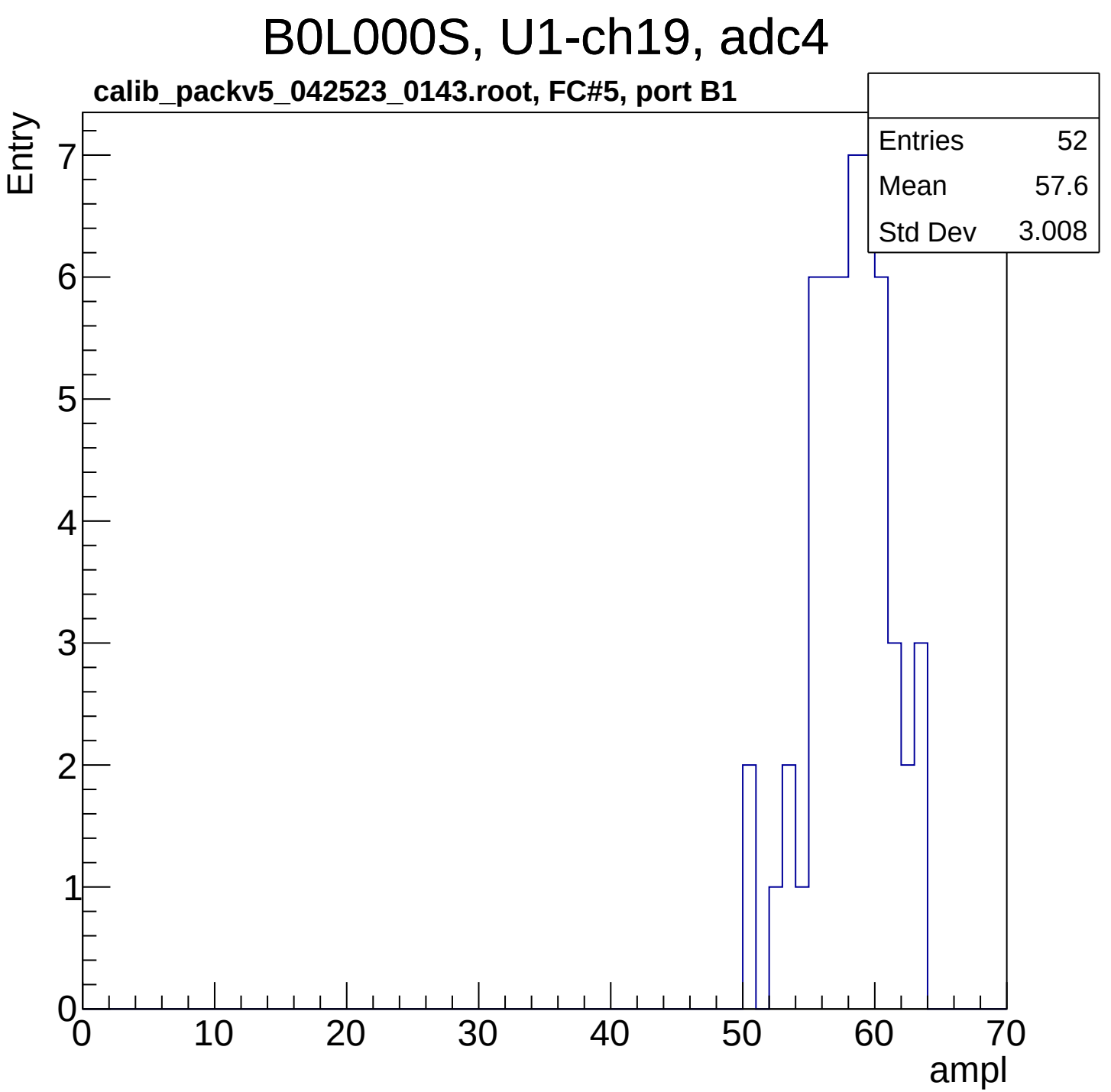
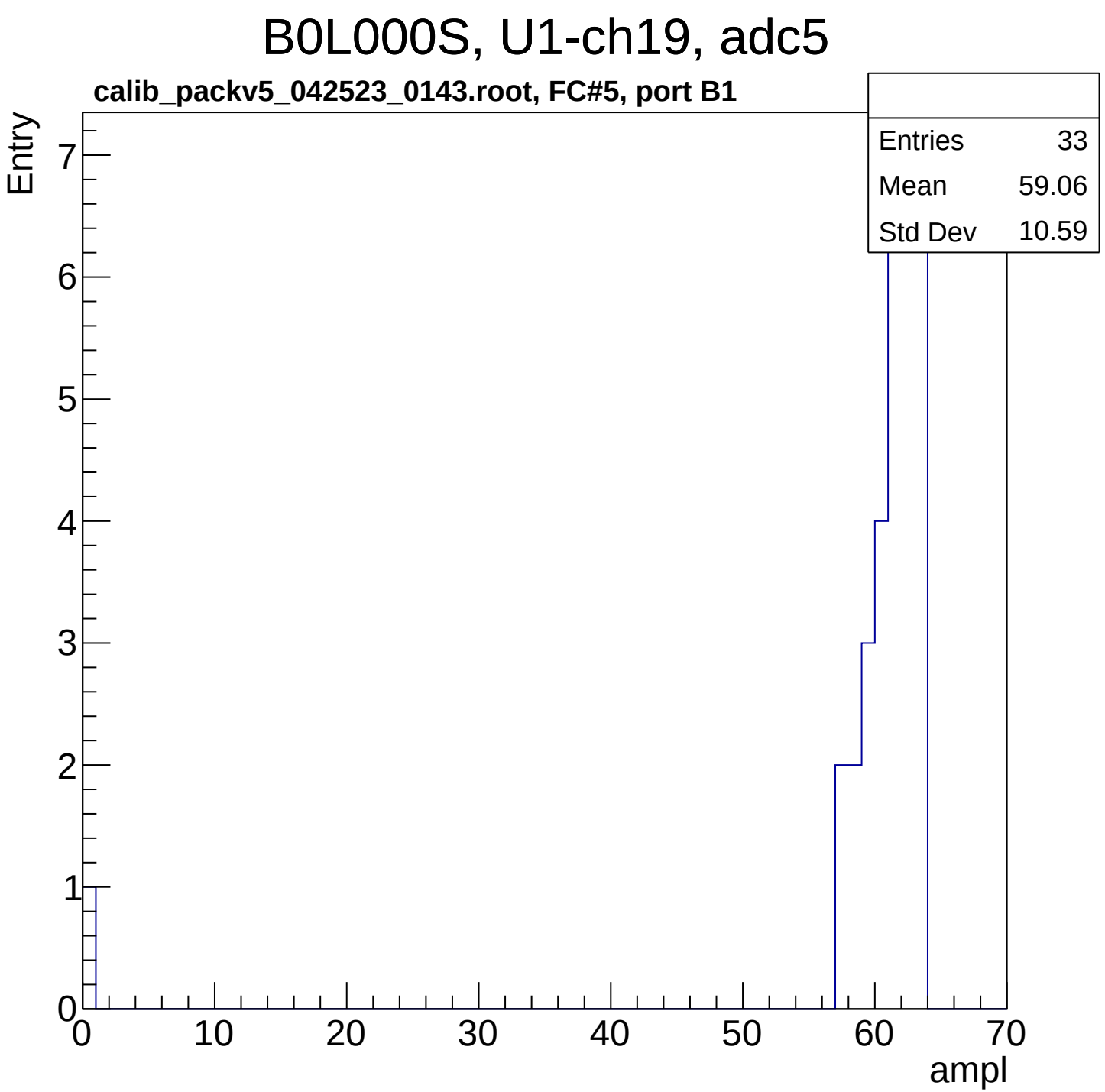
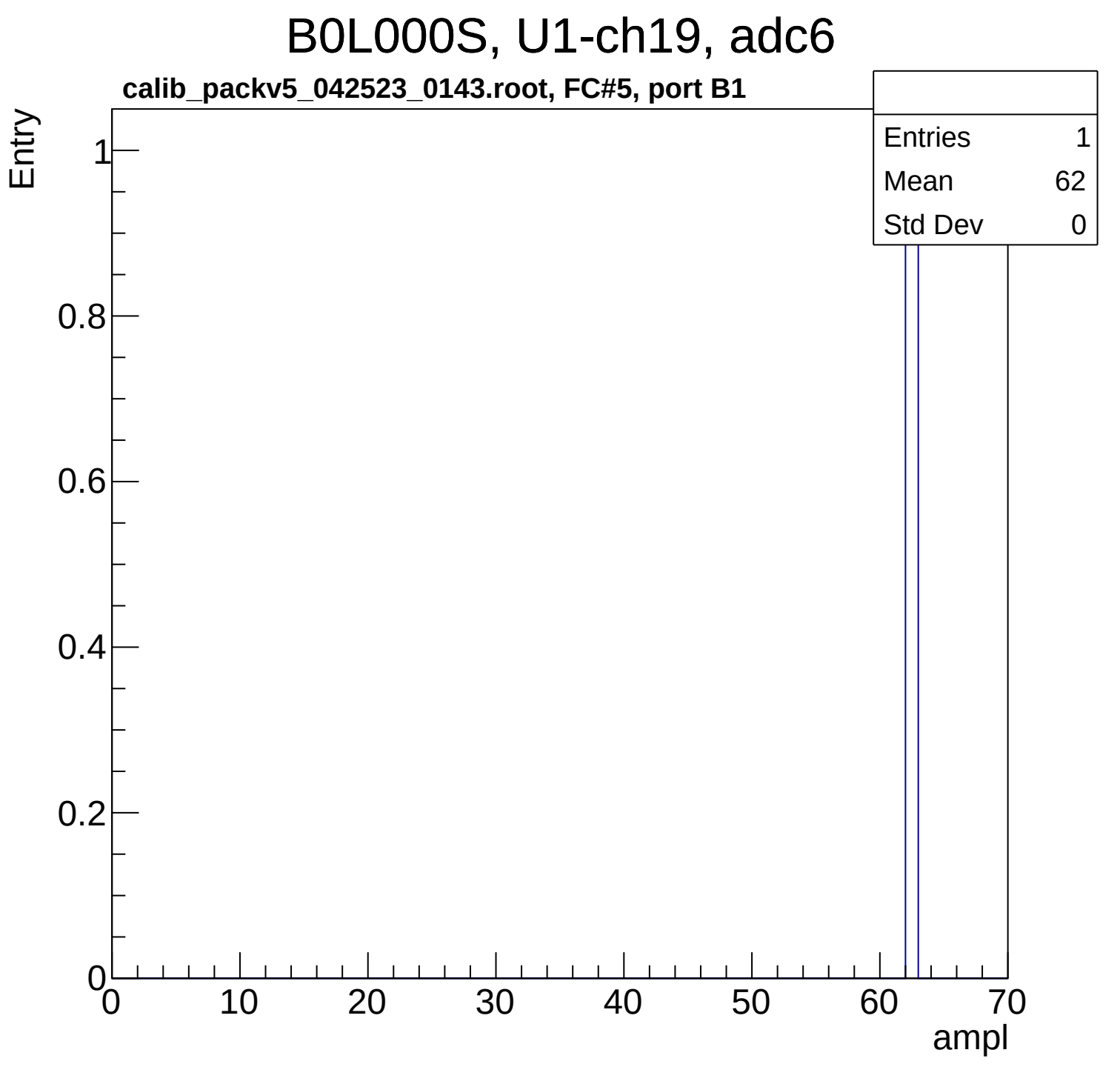

B0L000S, U1-ch19, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 44.3
Std Dev 2.71
Gaus mean : 43.6562
Gaus Width: 3.9588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch19, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 50.67
Std Dev 3.476
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch19, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.6
Std Dev 3.008
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch19, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 59.06
Std Dev 10.59
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch19, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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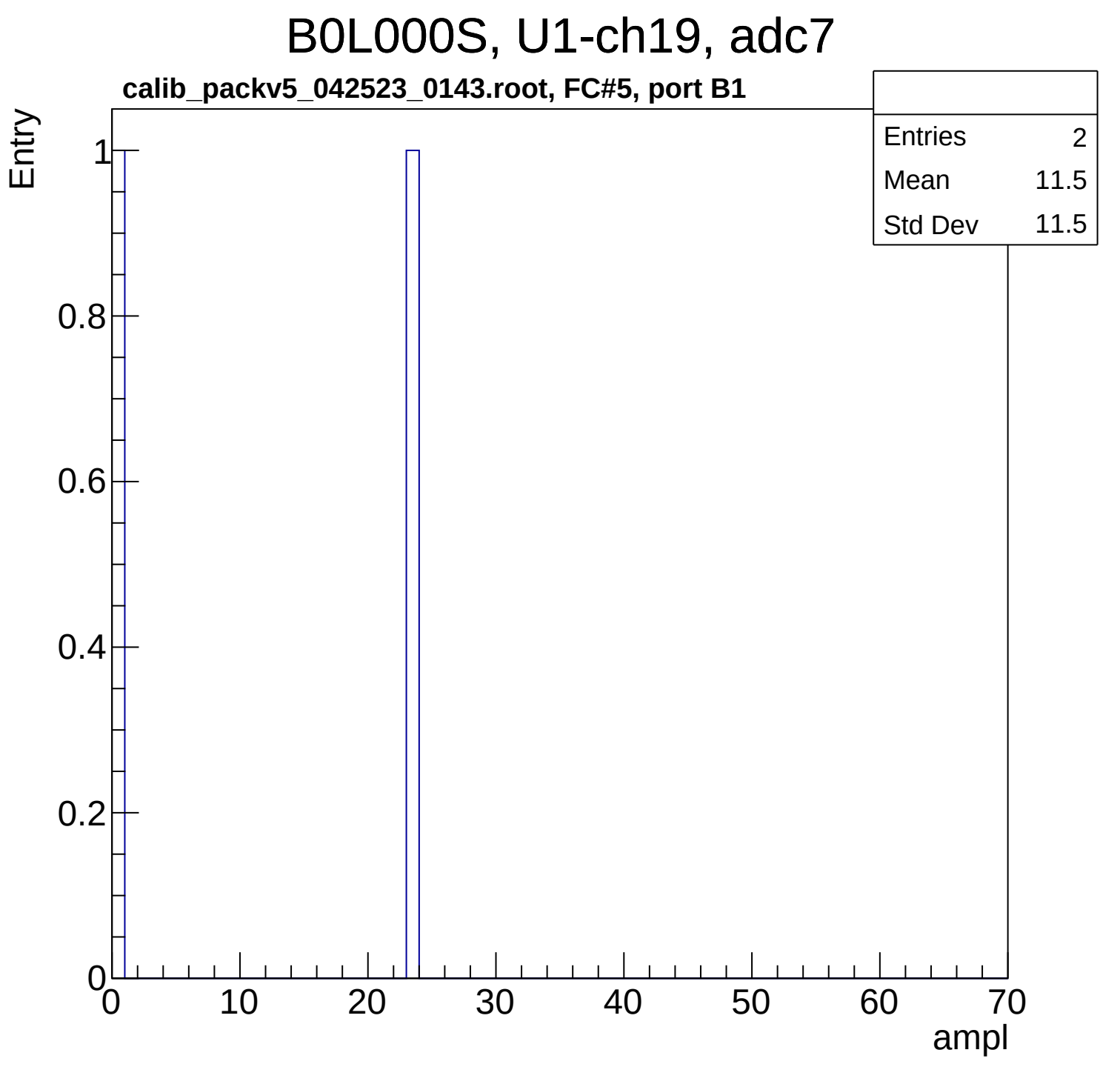
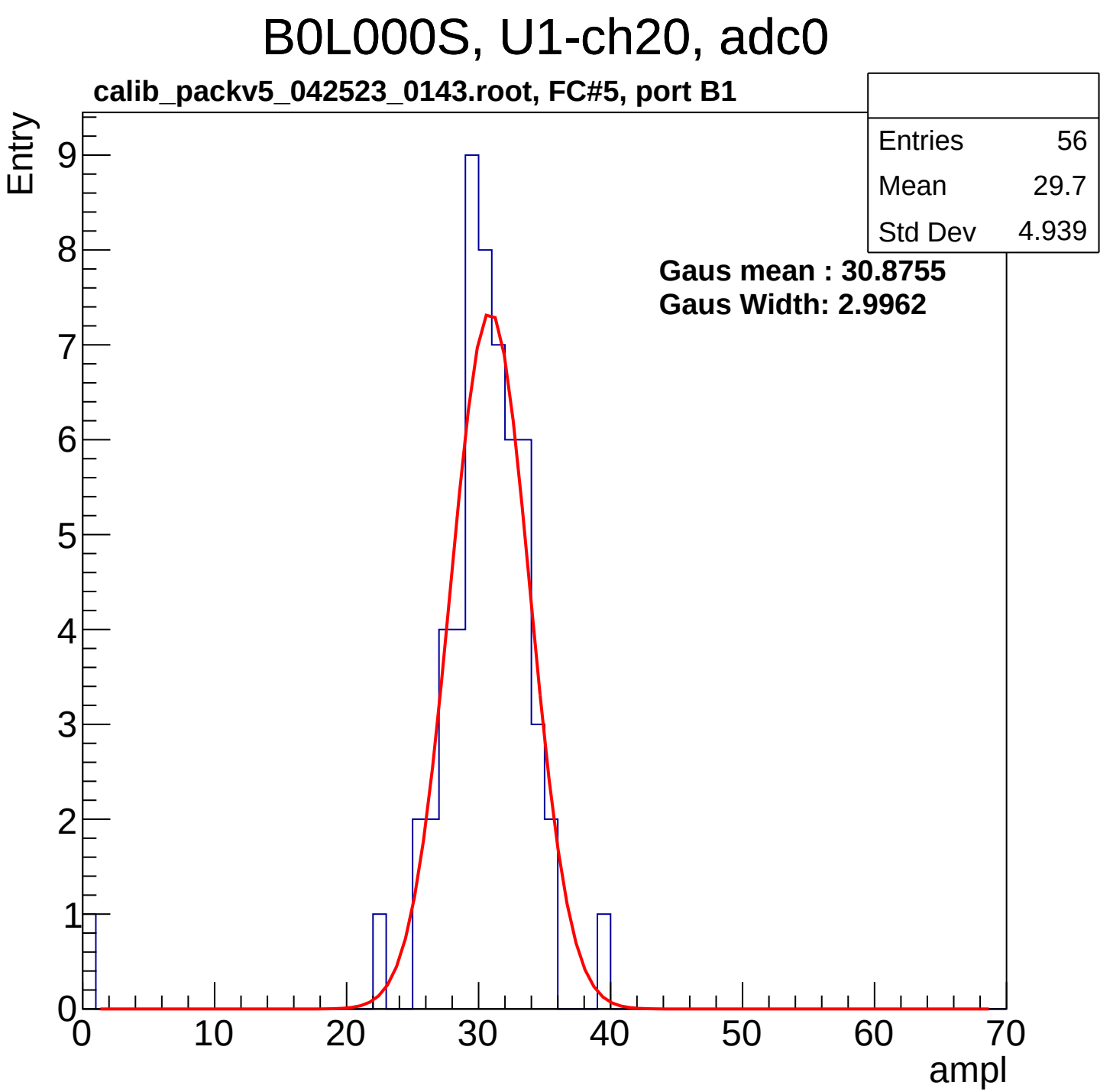
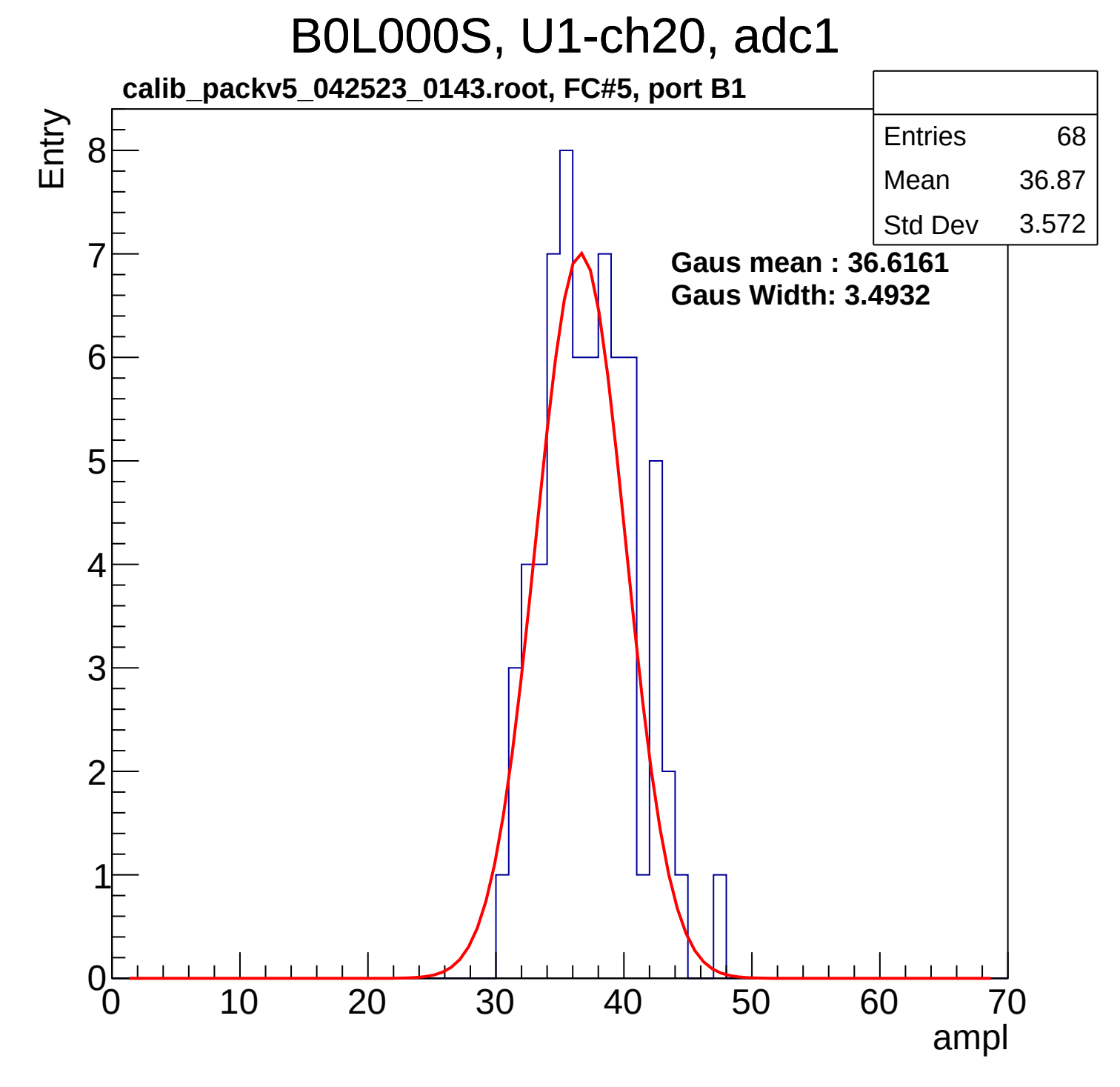
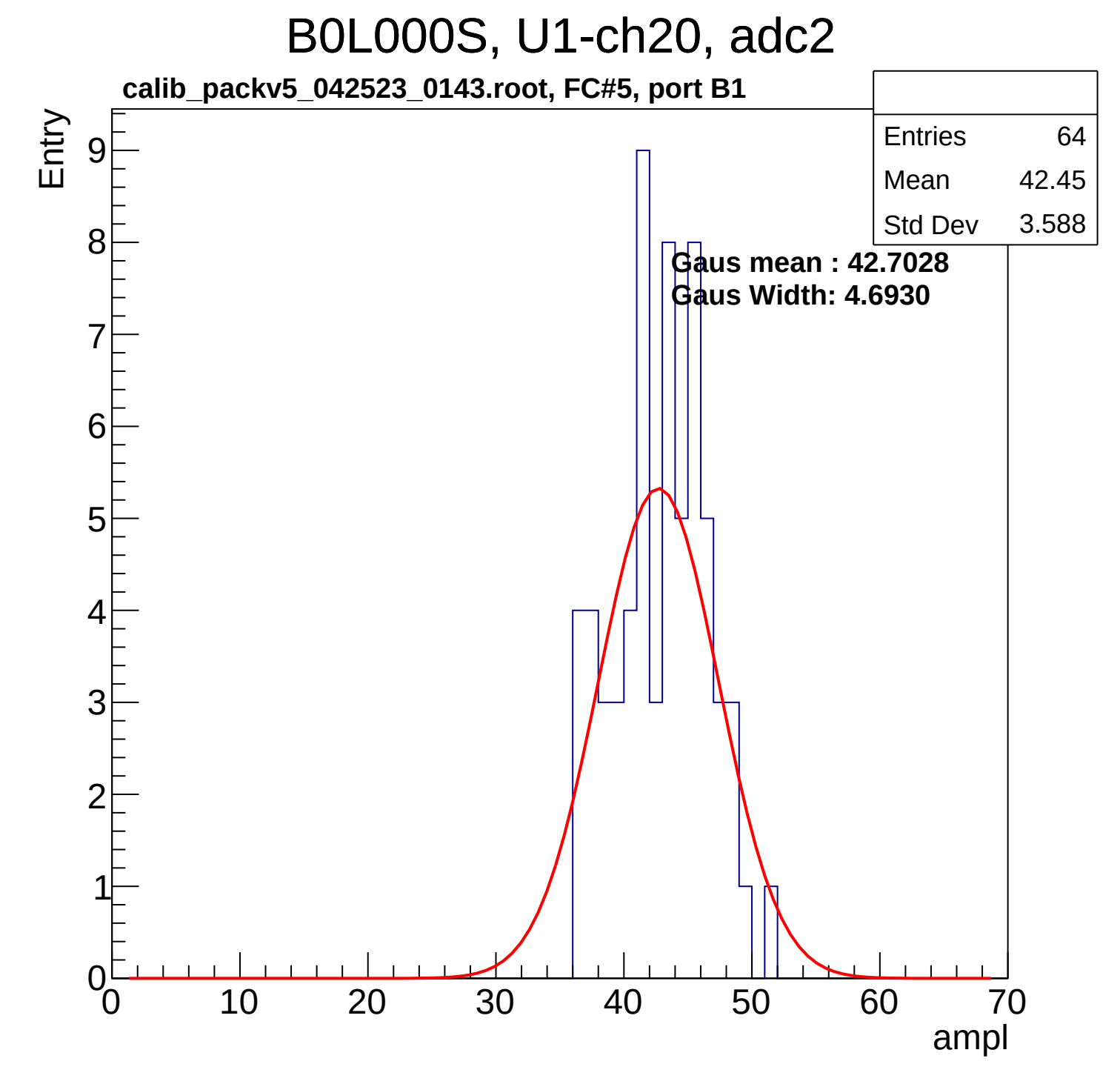
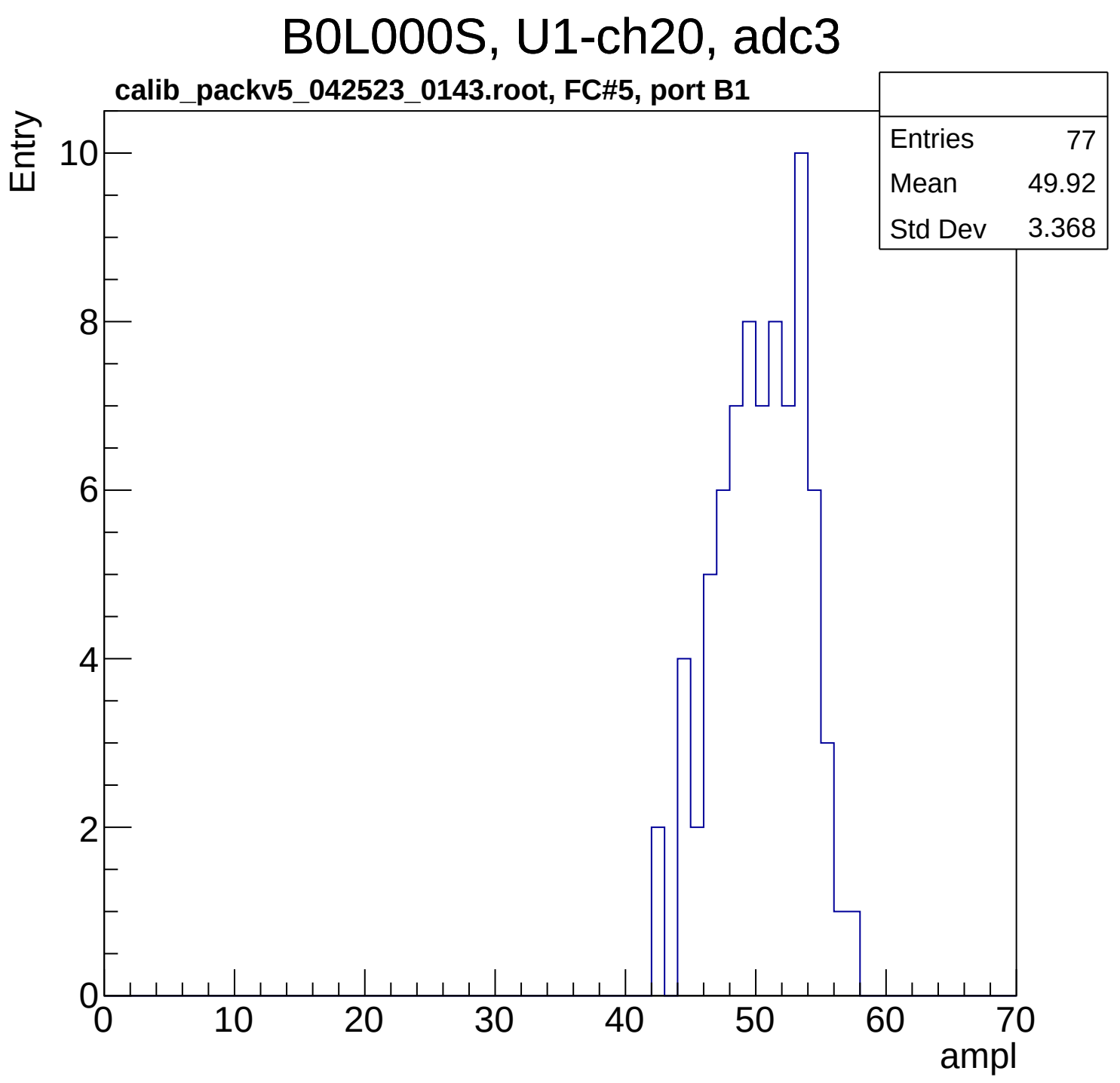
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

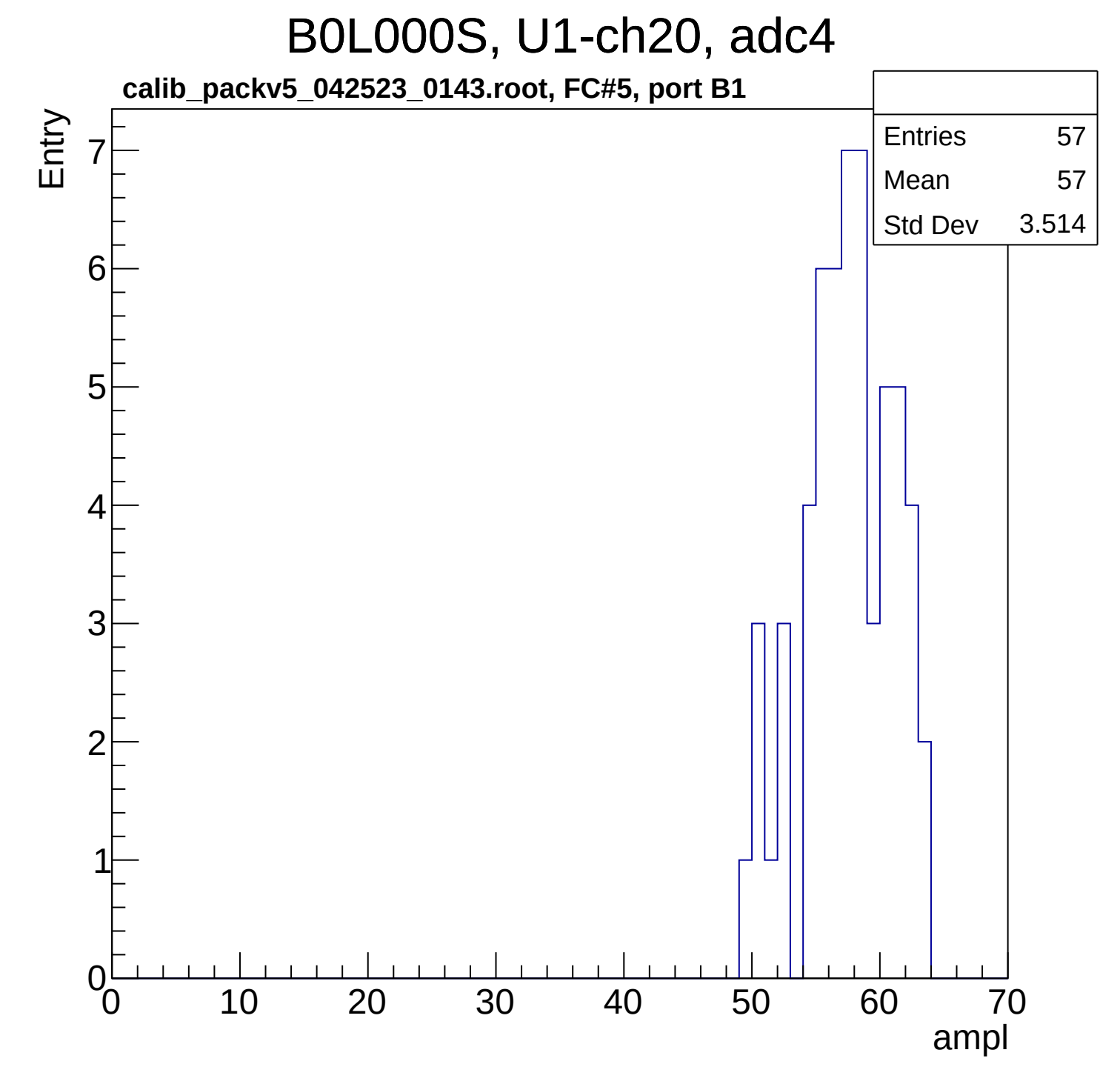

B0L000S, U1-ch19, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

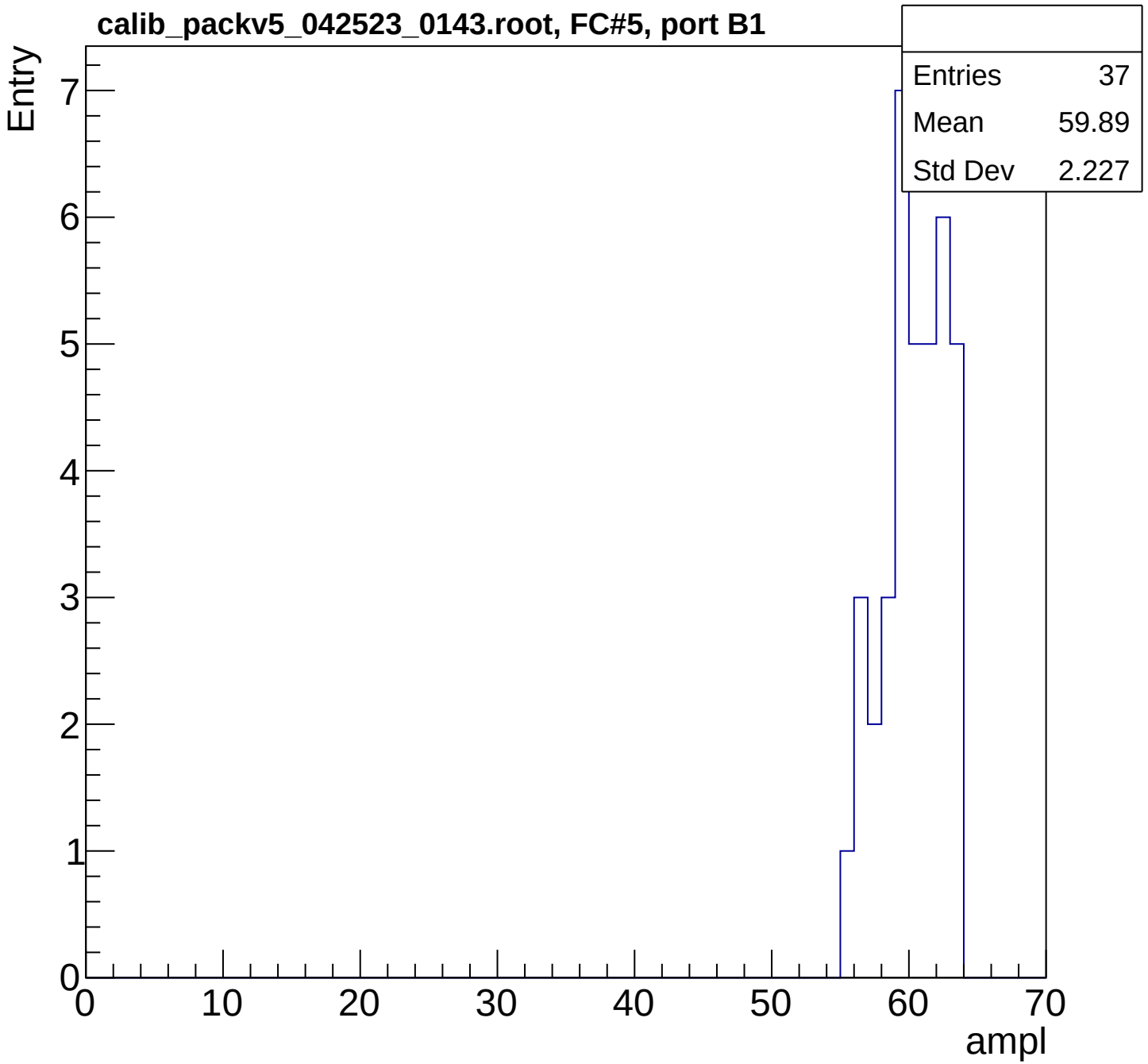

B0L000S, U1-ch20, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 29.7
Std Dev 4.939
Gaus mean : 30.8755
Gaus Width: 2.9962
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

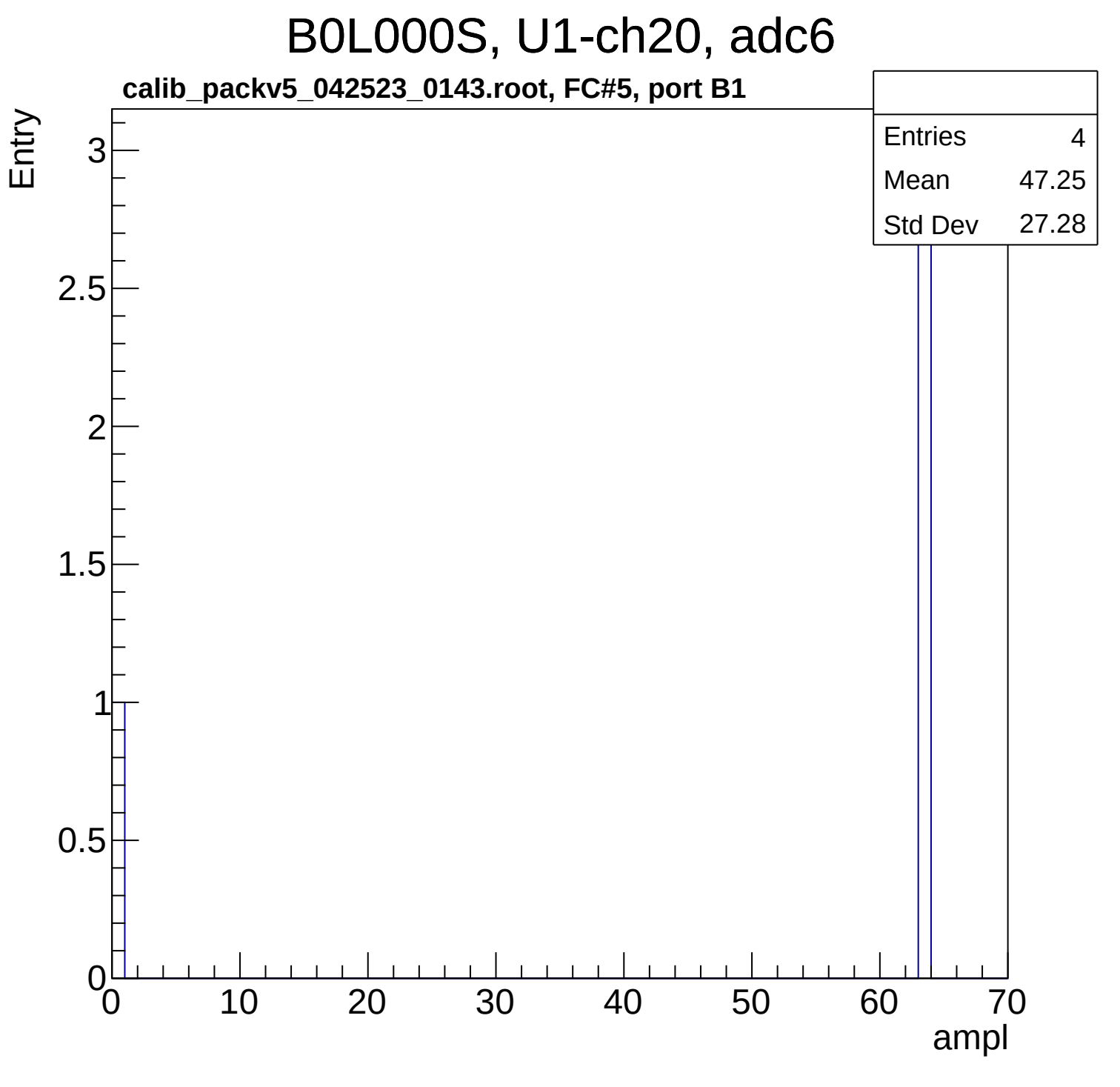

B0L000S, U1-ch20, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.87
Std Dev 3.572
Gaus mean : 36.6161
Gaus Width: 3.4932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch20, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.45
Std Dev 3.588
Gaus mean : 42.7028
Gaus Width: 4.6930
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch20, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 49.92
Std Dev 3.368
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch20, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57
Std Dev 3.514
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch20, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.89
Std Dev 2.227
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch20, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
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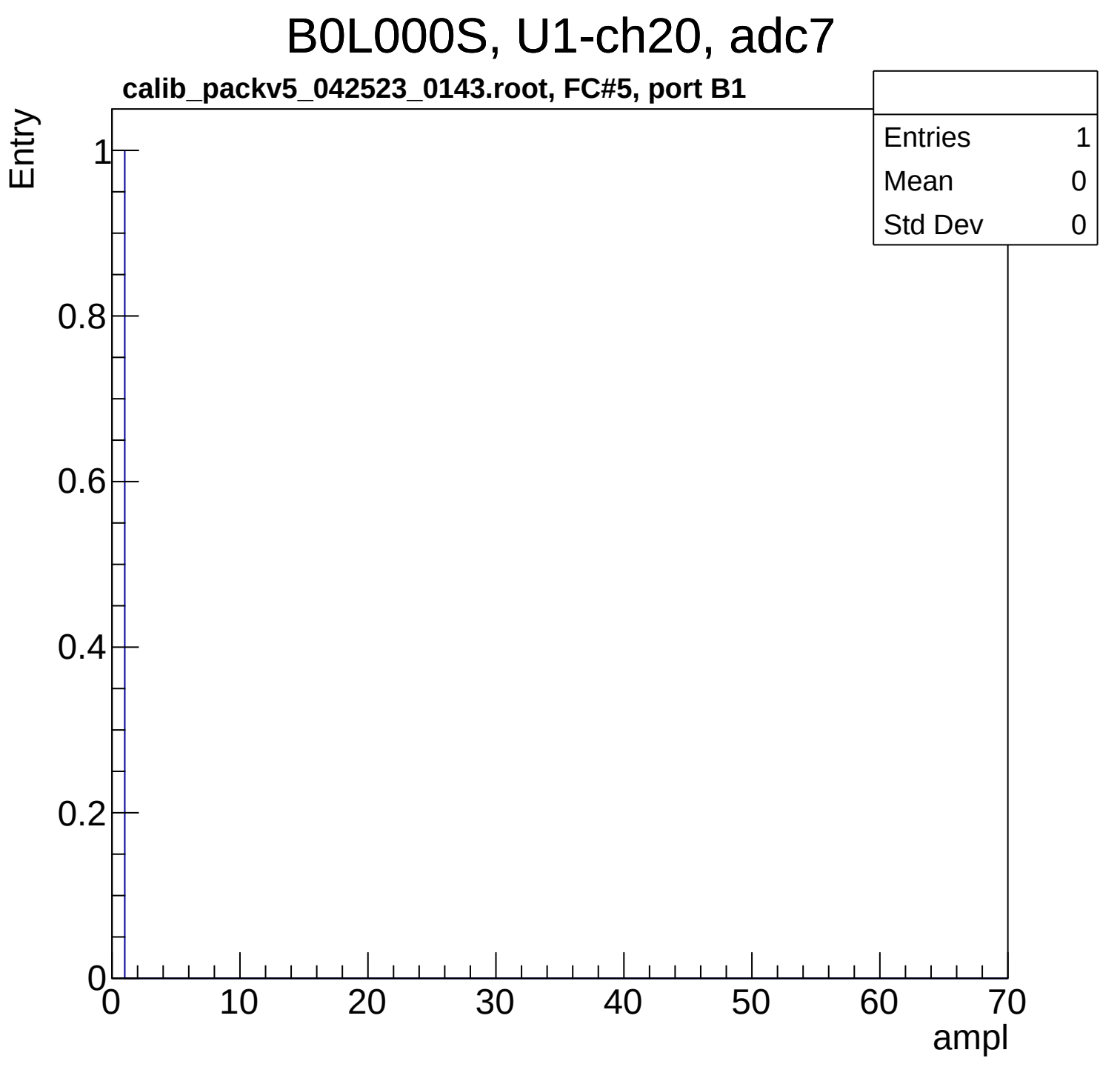
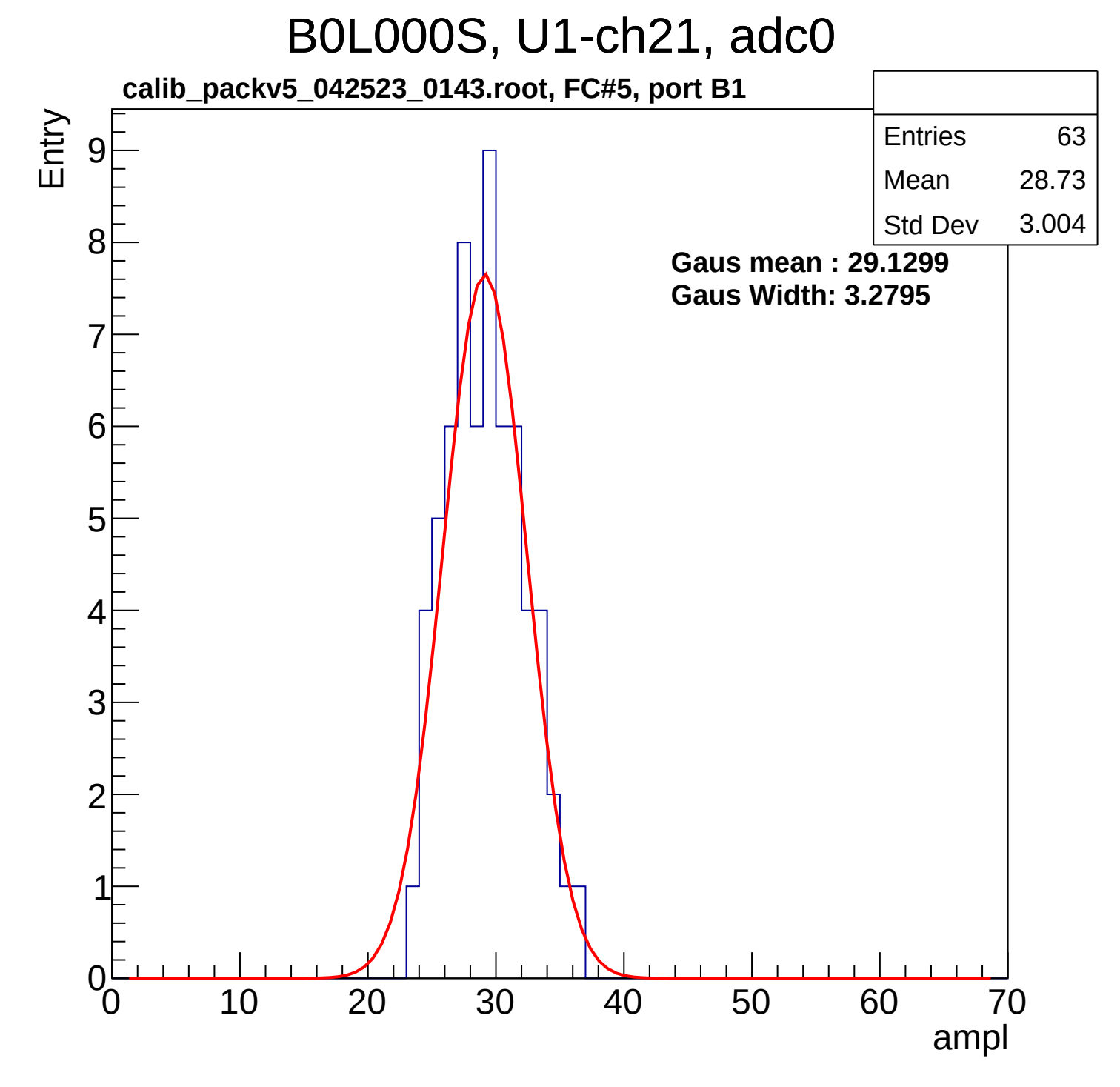
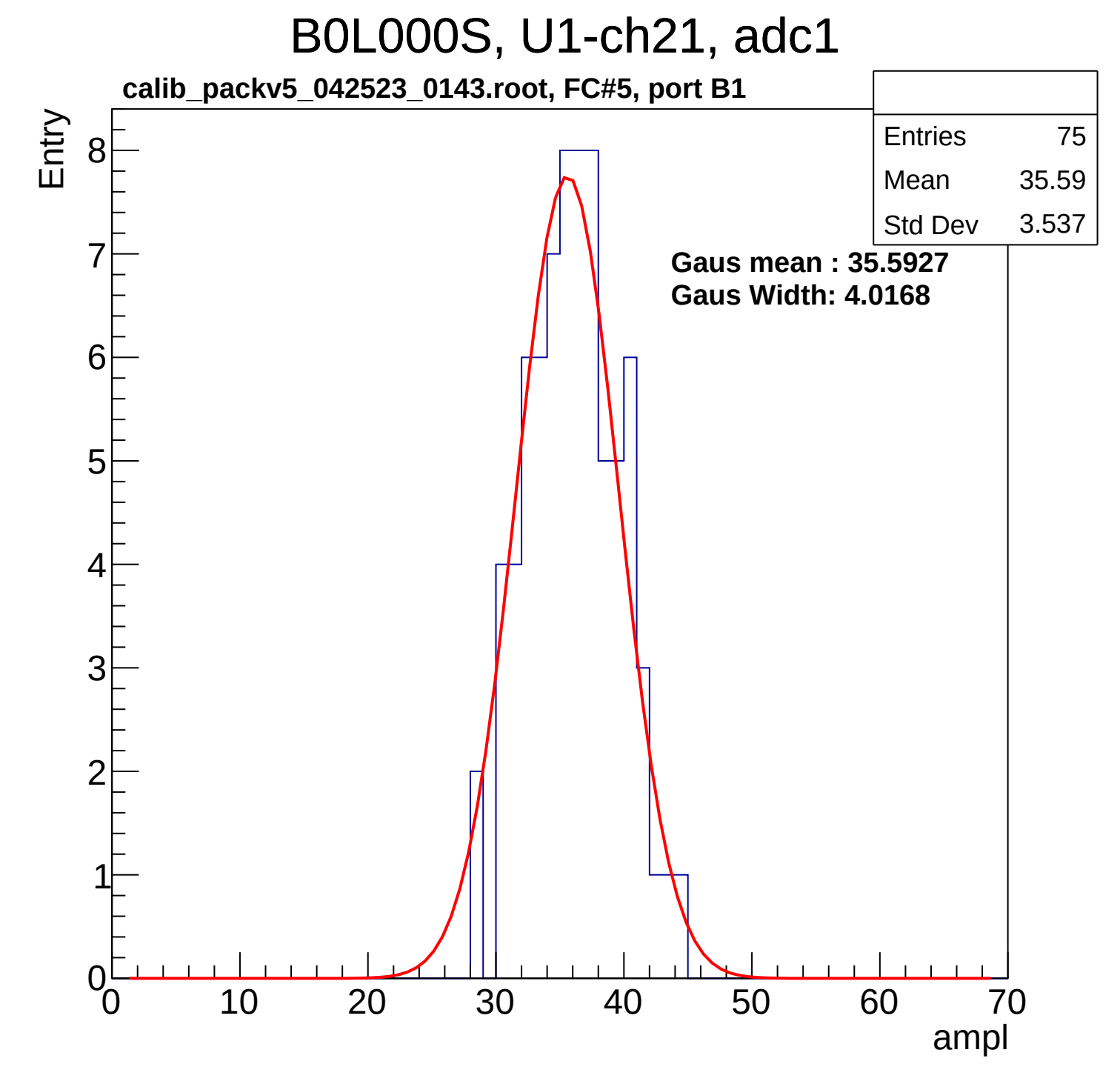
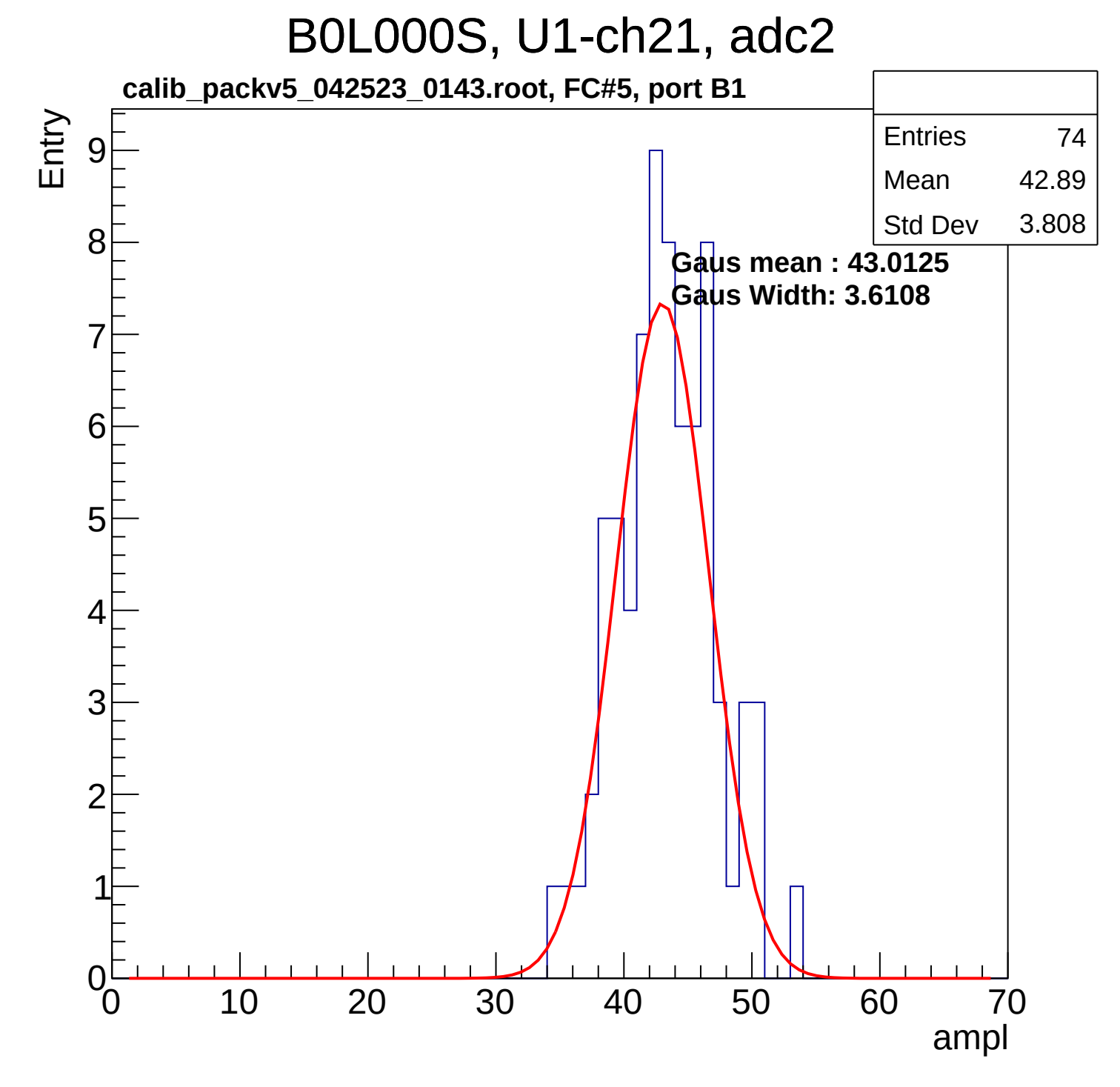
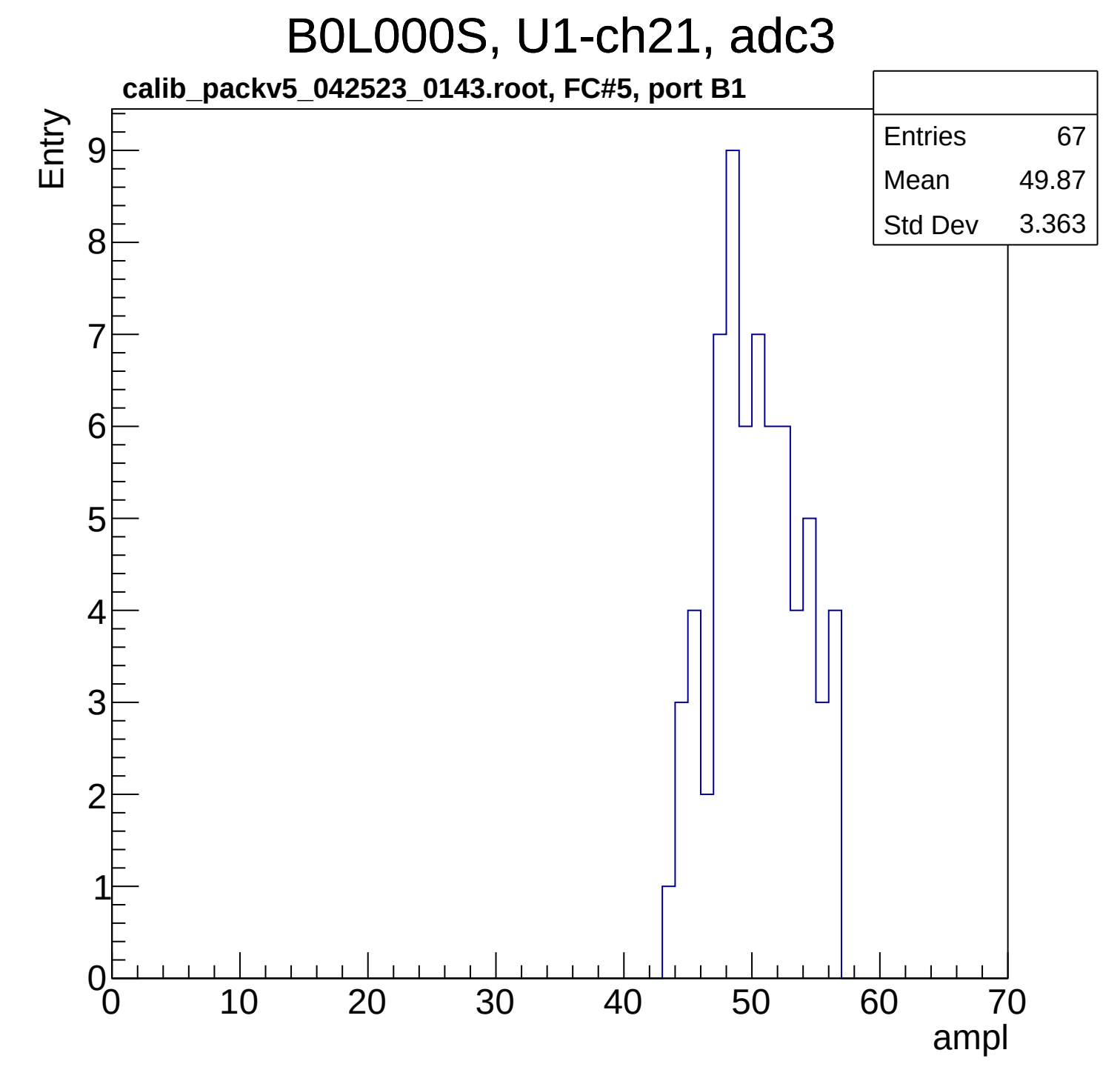
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch20, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch21, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 28.73
Std Dev 3.004
Gaus mean : 29.1299
Gaus Width: 3.2795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch21, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.59
Std Dev 3.537
Gaus mean : 35.5927
Gaus Width: 4.0168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch21, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 42.89
Std Dev 3.808
Gaus mean : 43.0125
Gaus Width: 3.6108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch21, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.87
Std Dev 3.363
Entry
ampl
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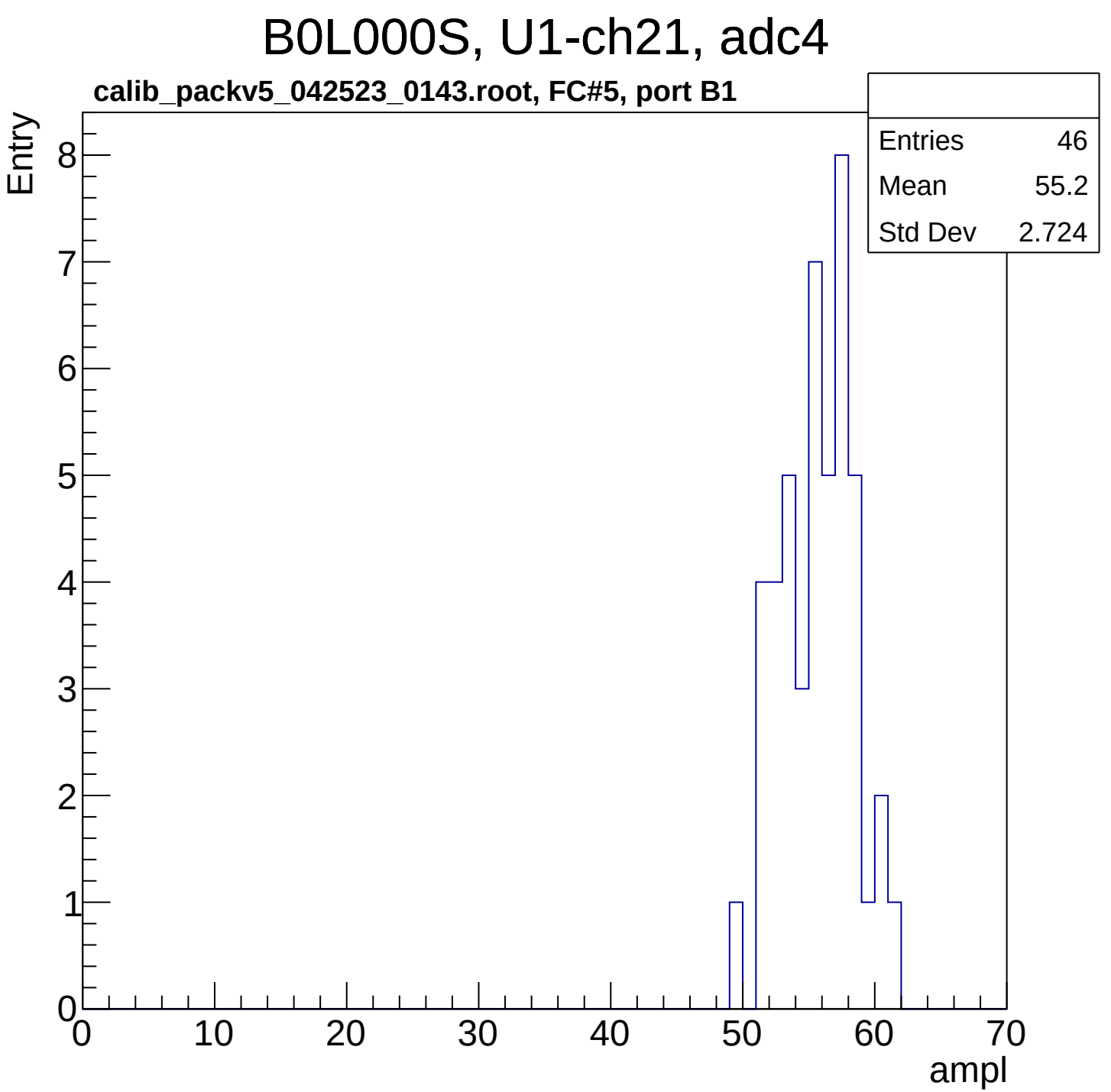
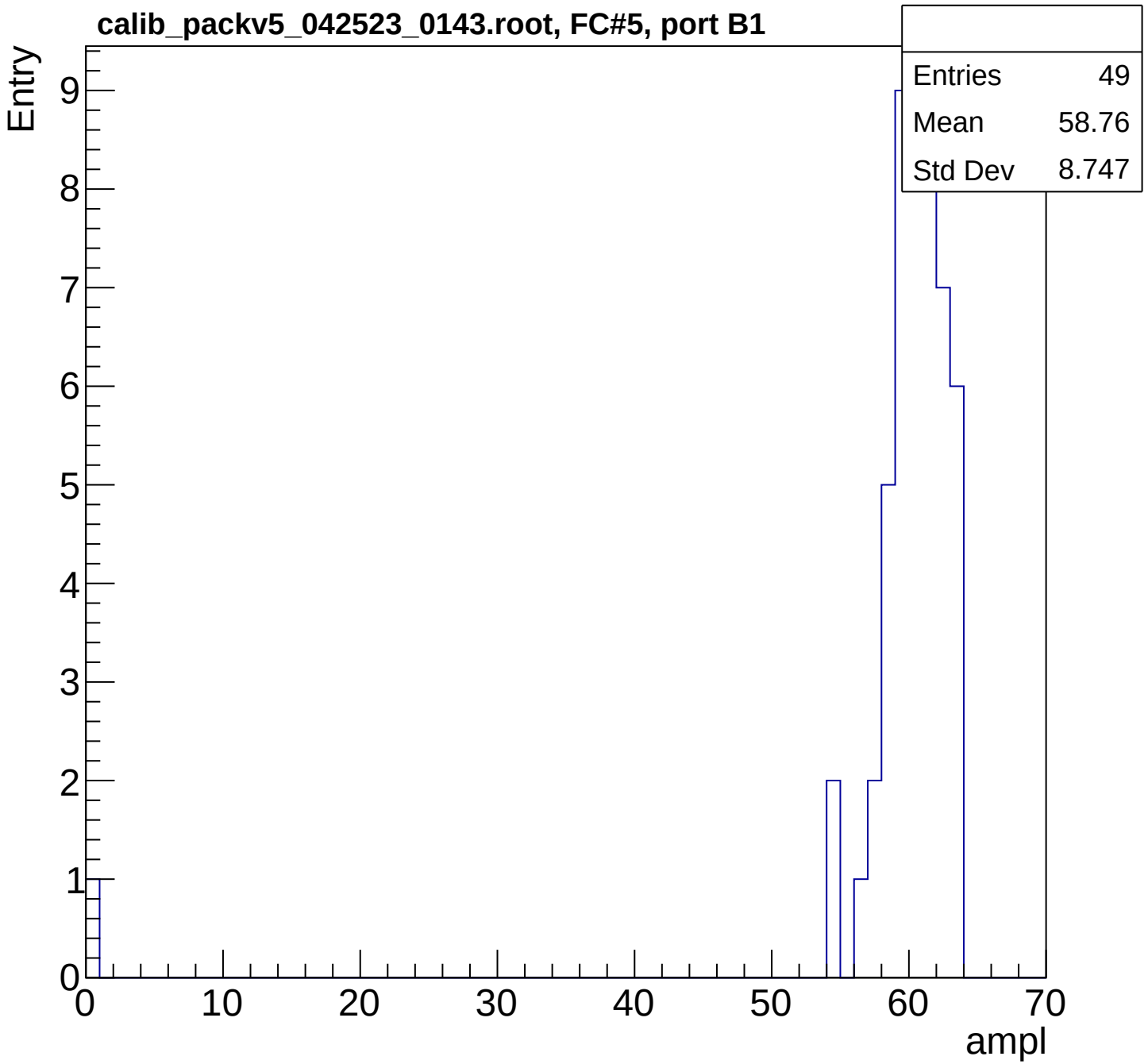
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch21, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 55.2
Std Dev 2.724
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch21, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.76
Std Dev 8.747
Entry
ampl
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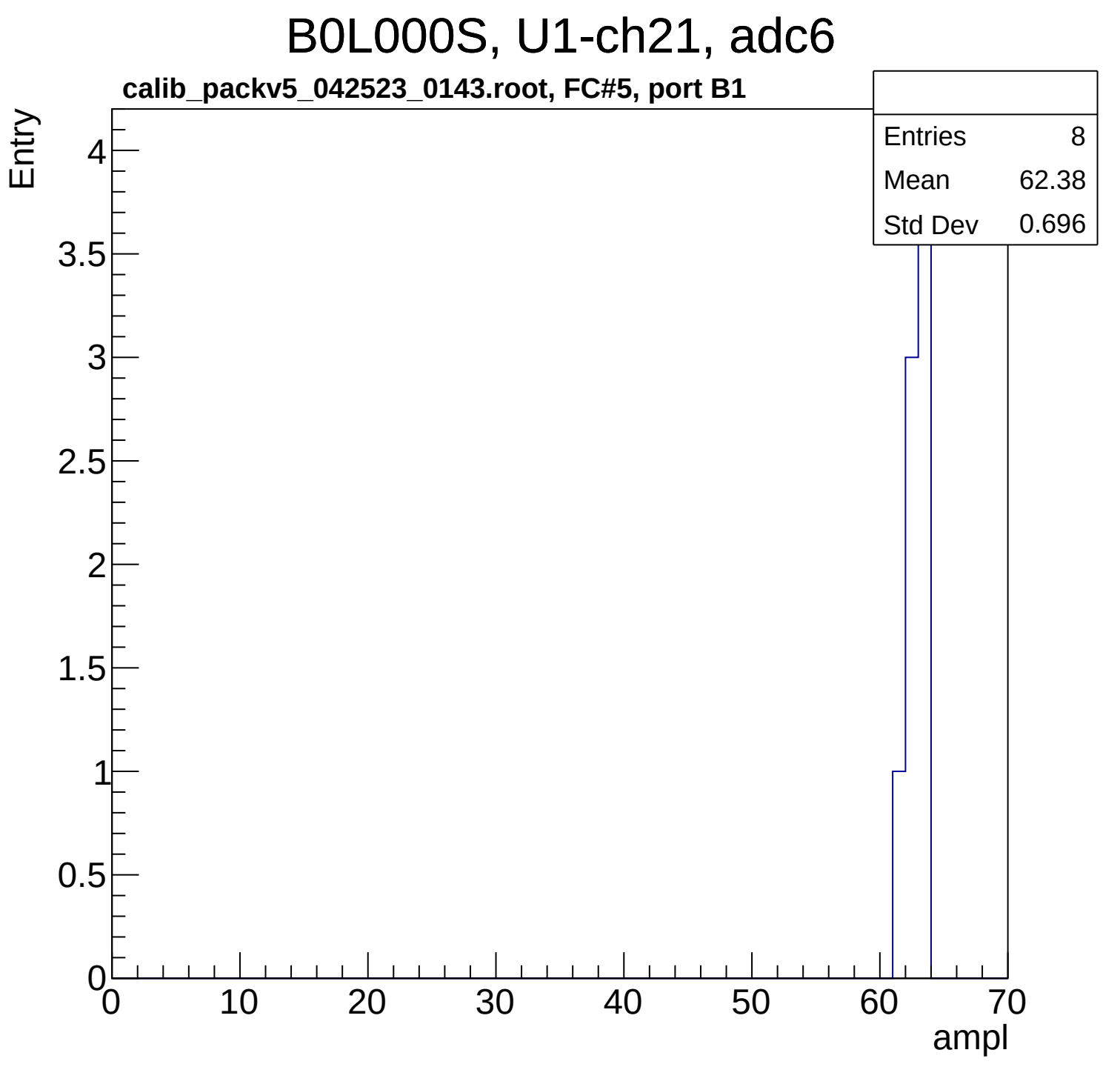
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch21, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.38
Std Dev 0.696
Entry
ampl
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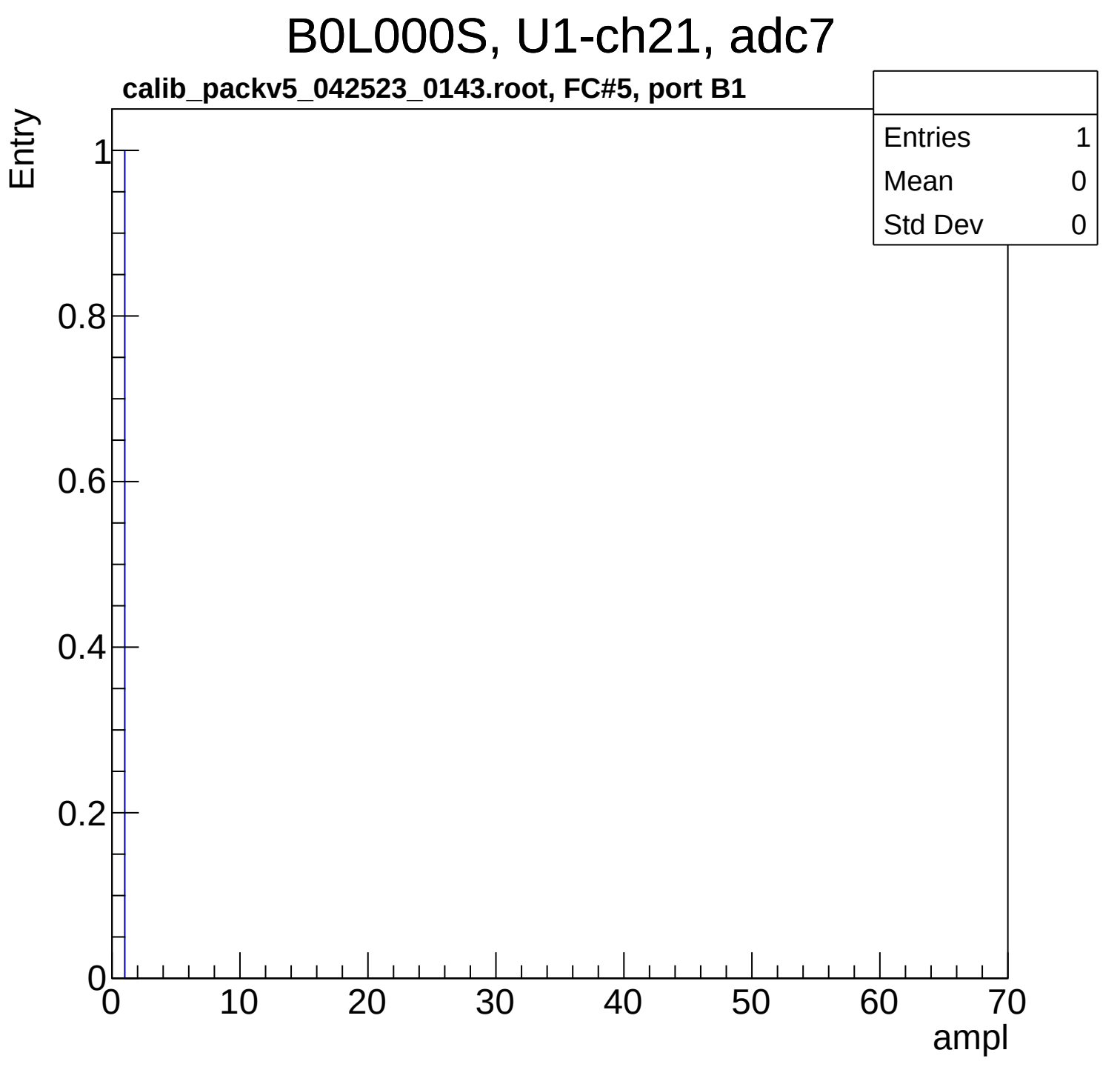
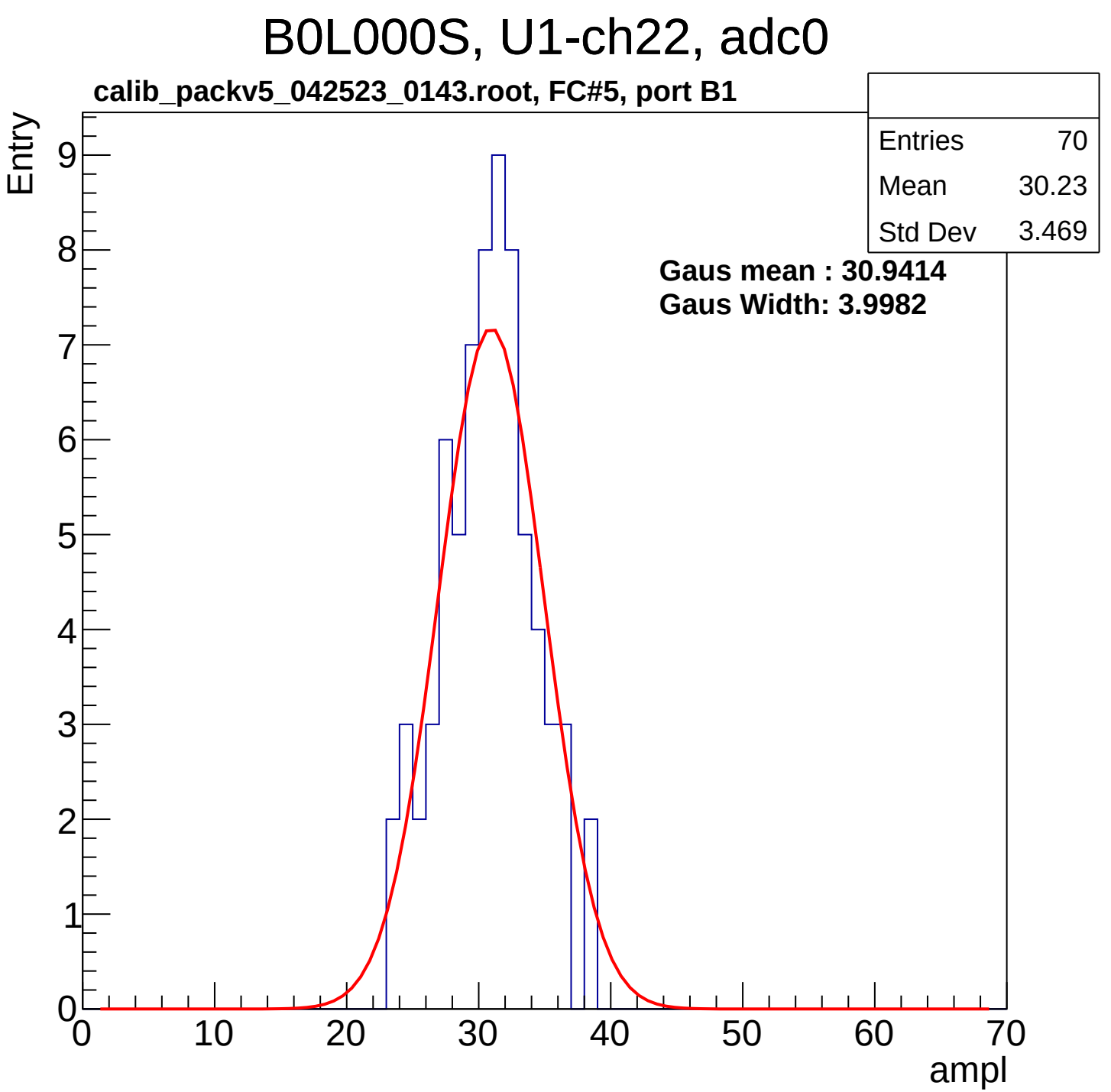
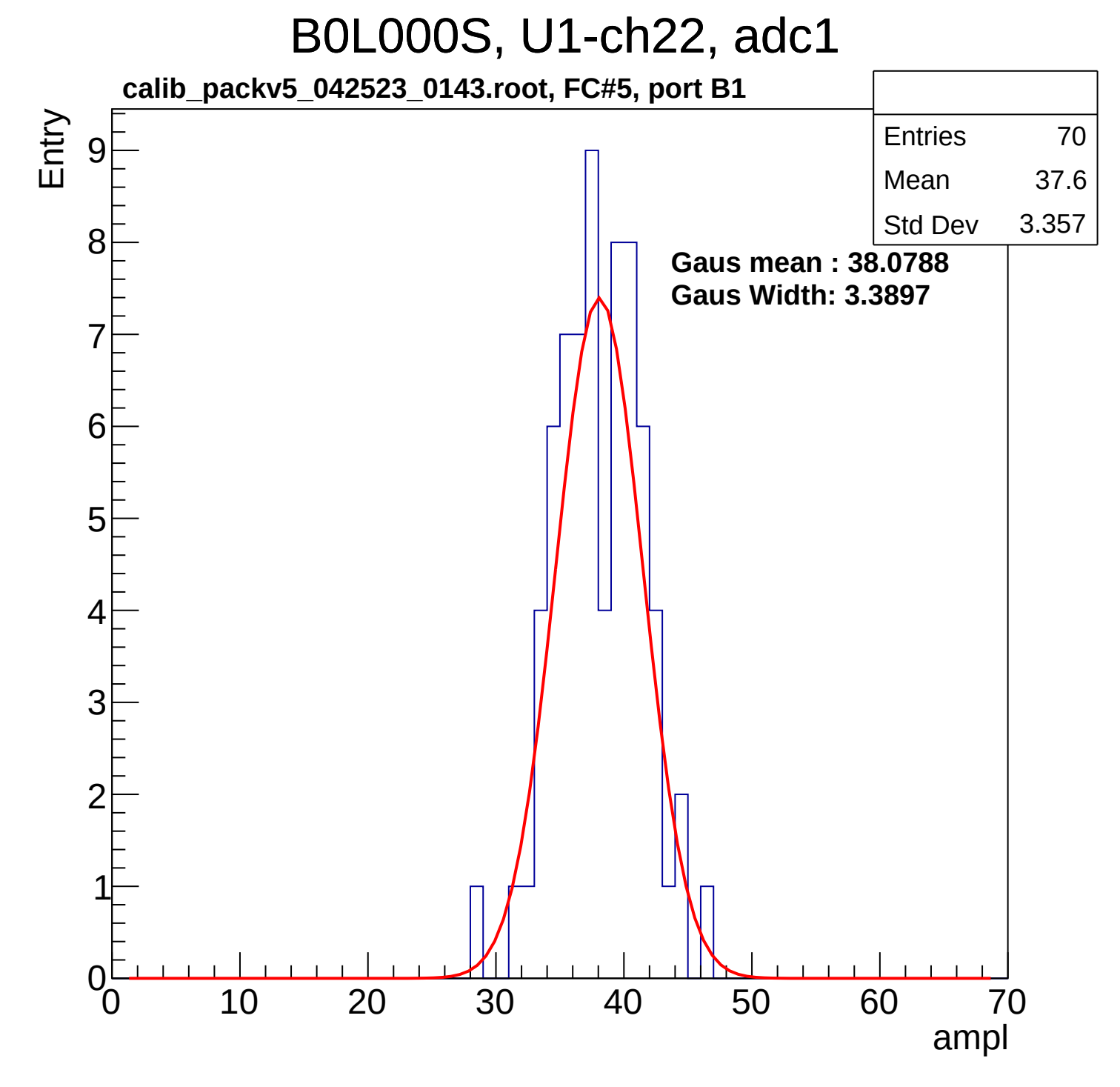
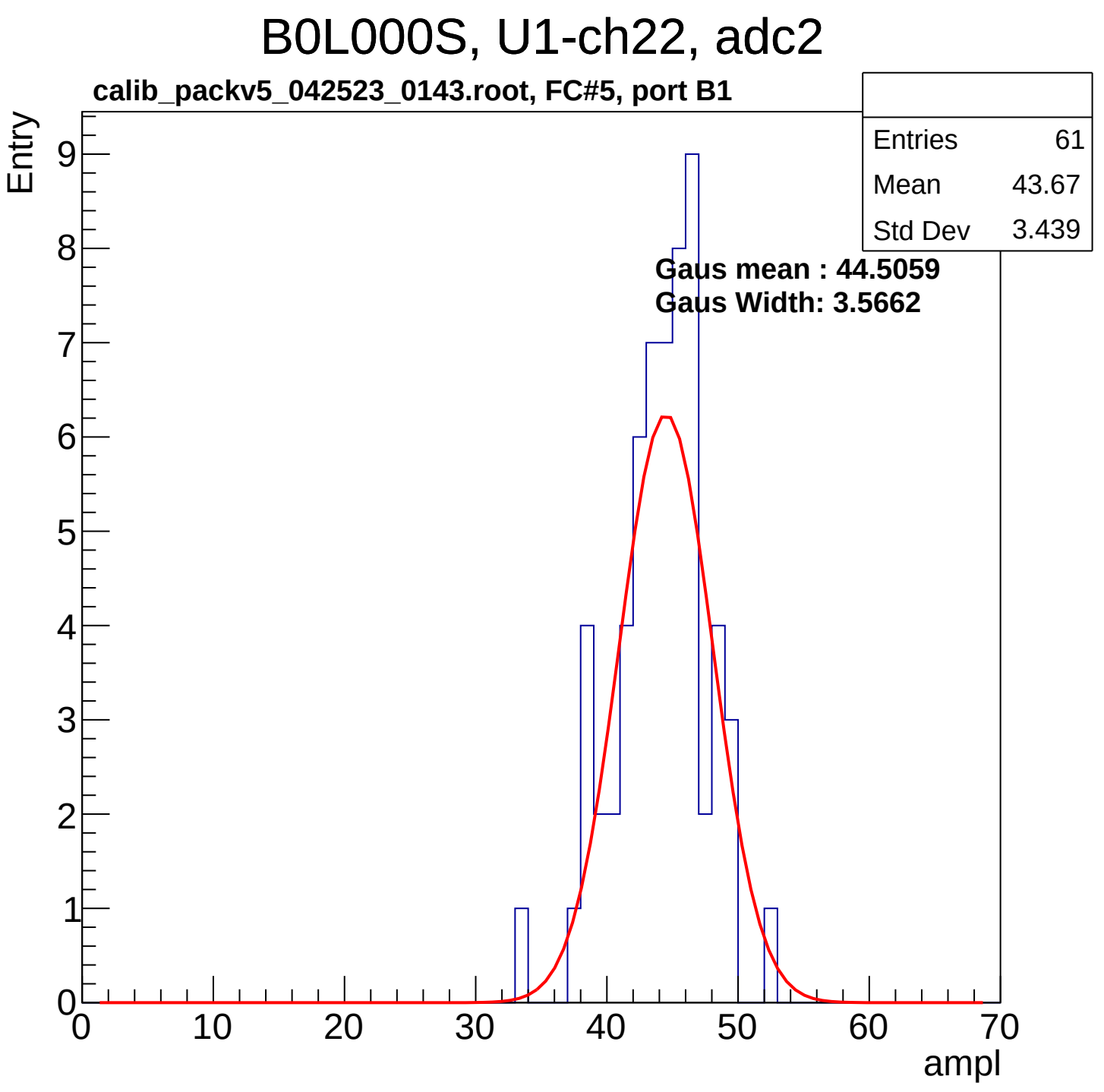
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U1-ch21, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch22, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 30.23
Std Dev 3.469
Gaus mean : 30.9414
Gaus Width: 3.9982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch22, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 37.6
Std Dev 3.357
Gaus mean : 38.0788
Gaus Width: 3.3897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch22, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 43.67
Std Dev 3.439
Gaus mean : 44.5059
Gaus Width: 3.5662
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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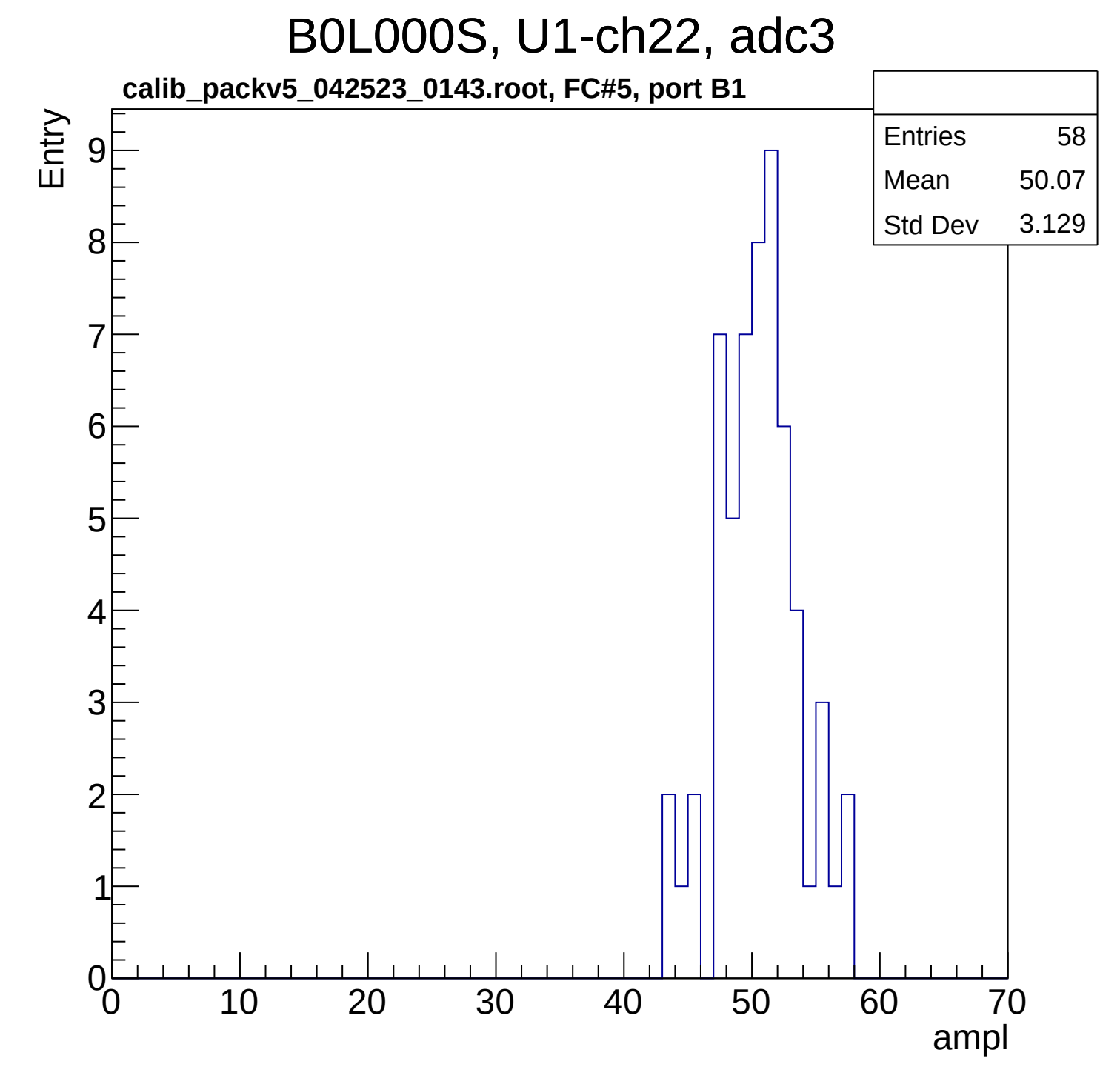
B0L000S, U1-ch22, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 50.07
Std Dev 3.129
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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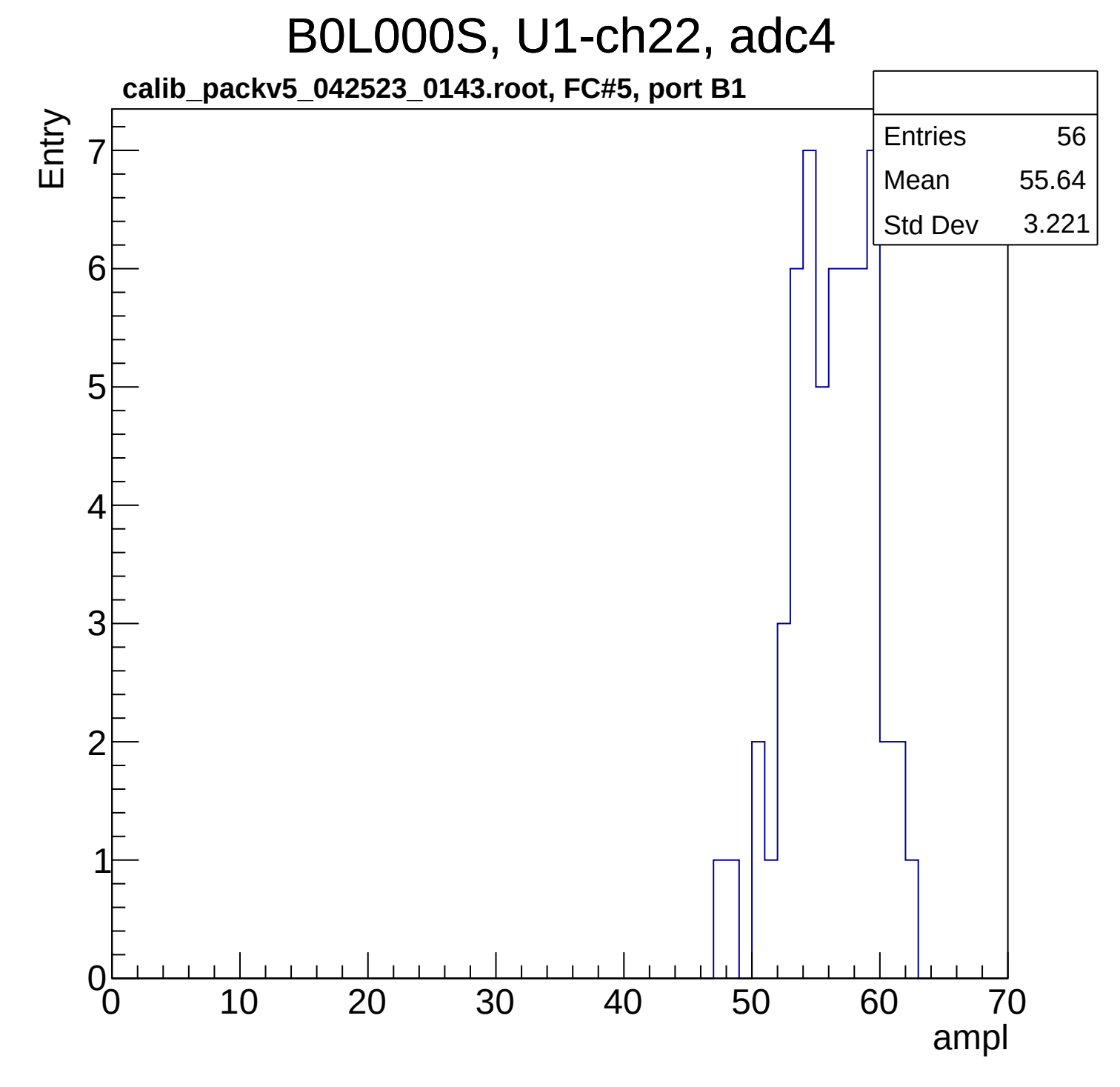
B0L000S, U1-ch22, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.64
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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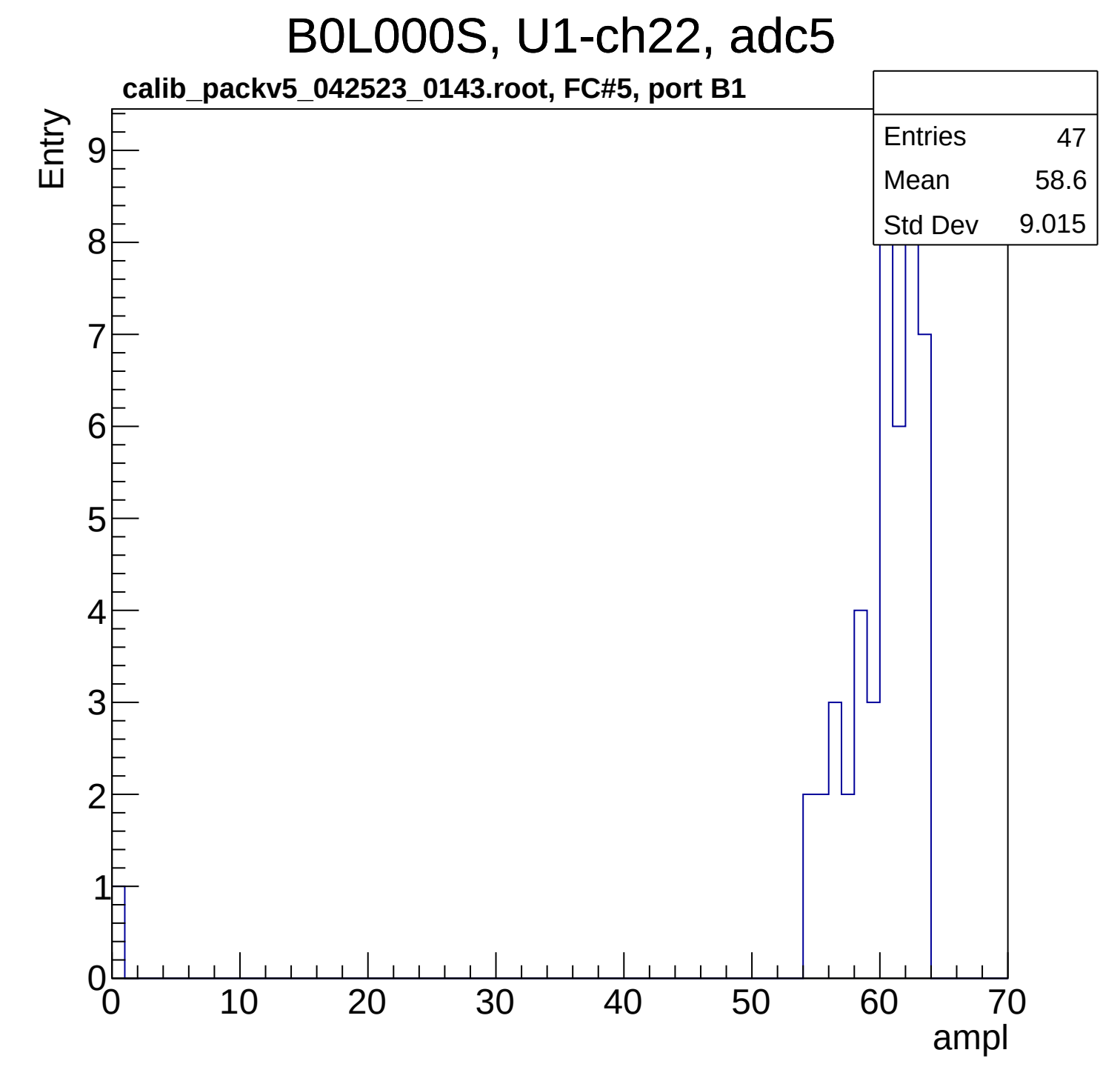
B0L000S, U1-ch22, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.6
Std Dev 9.015
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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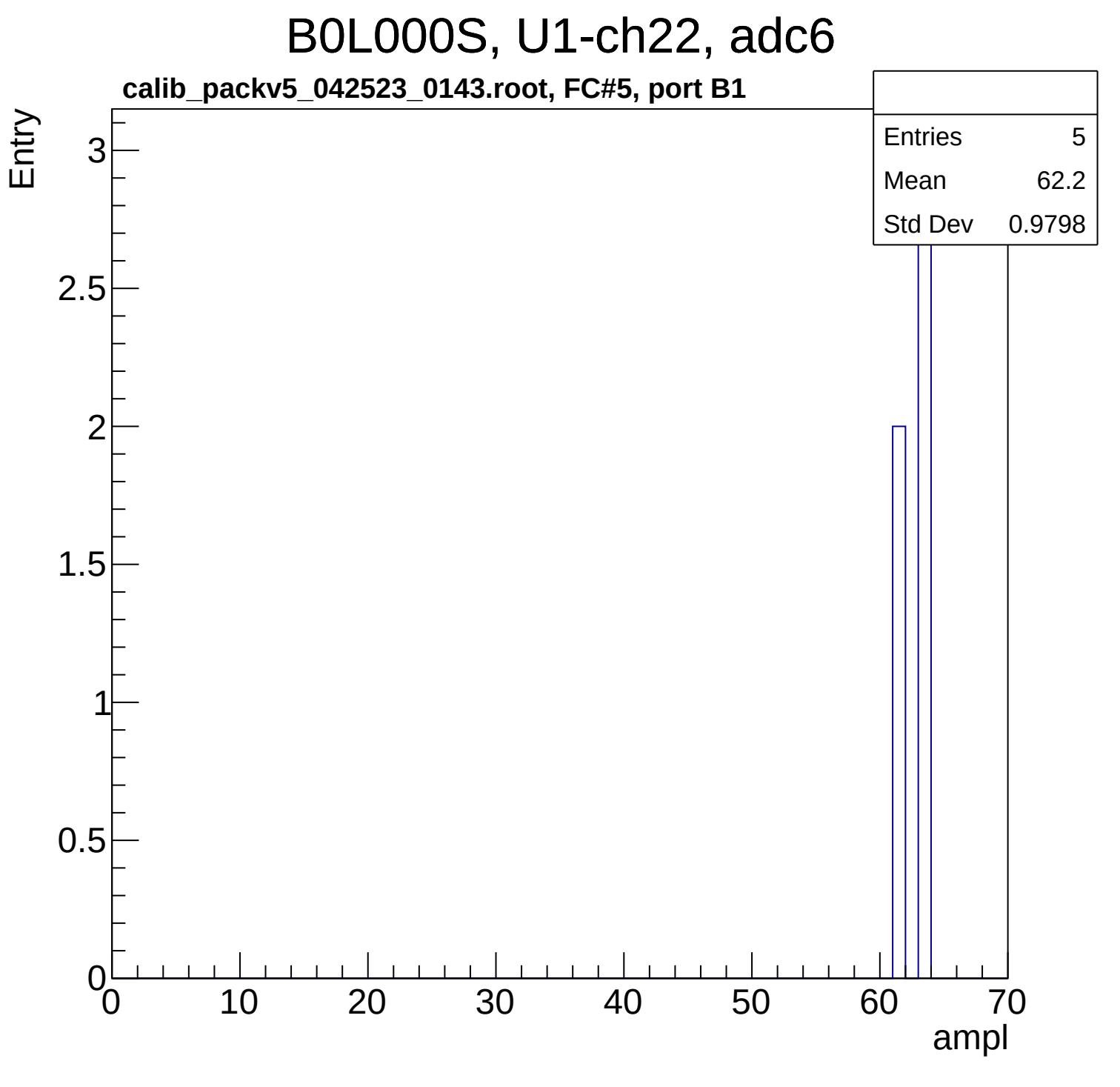
B0L000S, U1-ch22, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 0.9798
Entry
ampl
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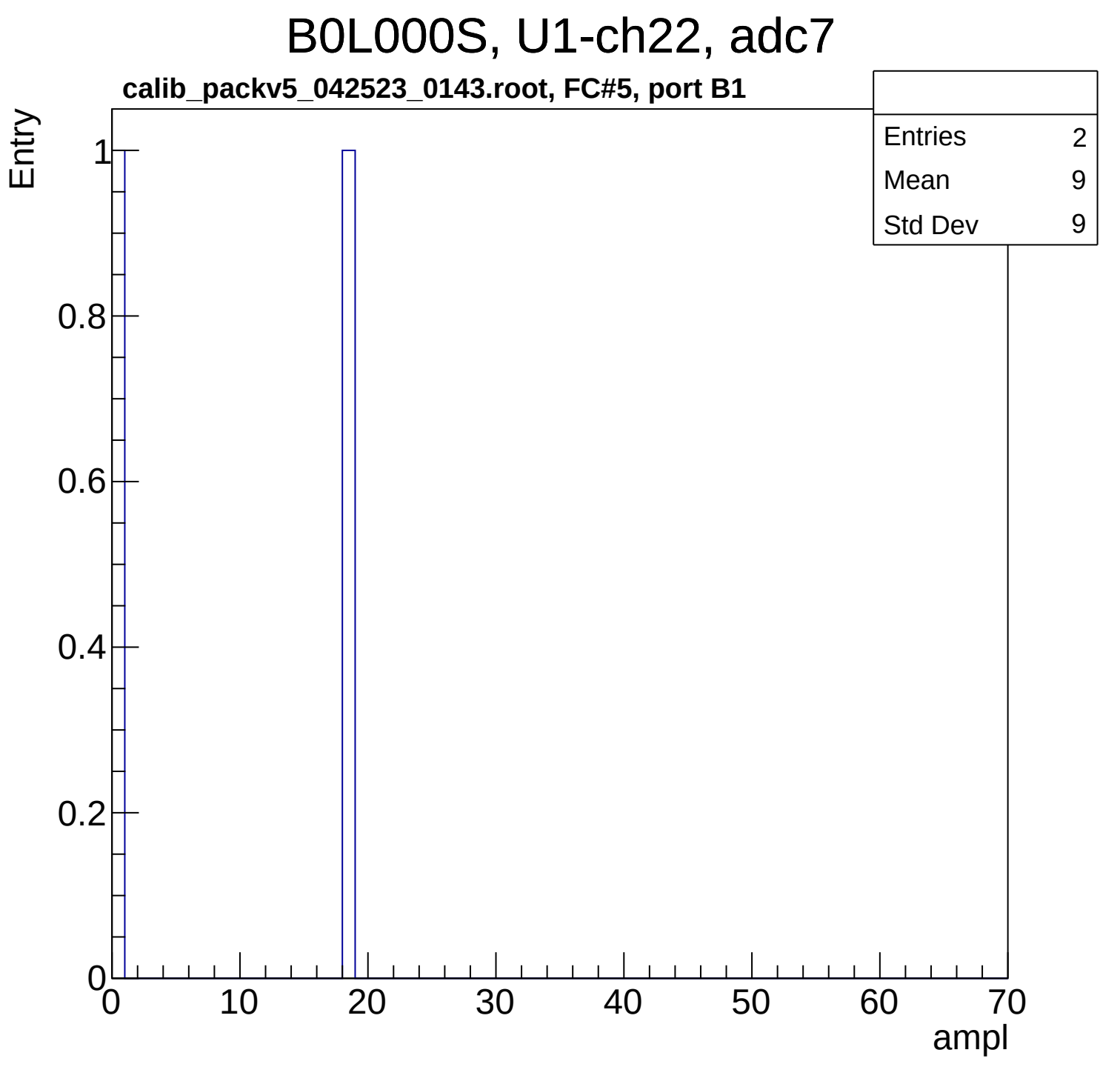
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch22, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9
Std Dev 9
Entry
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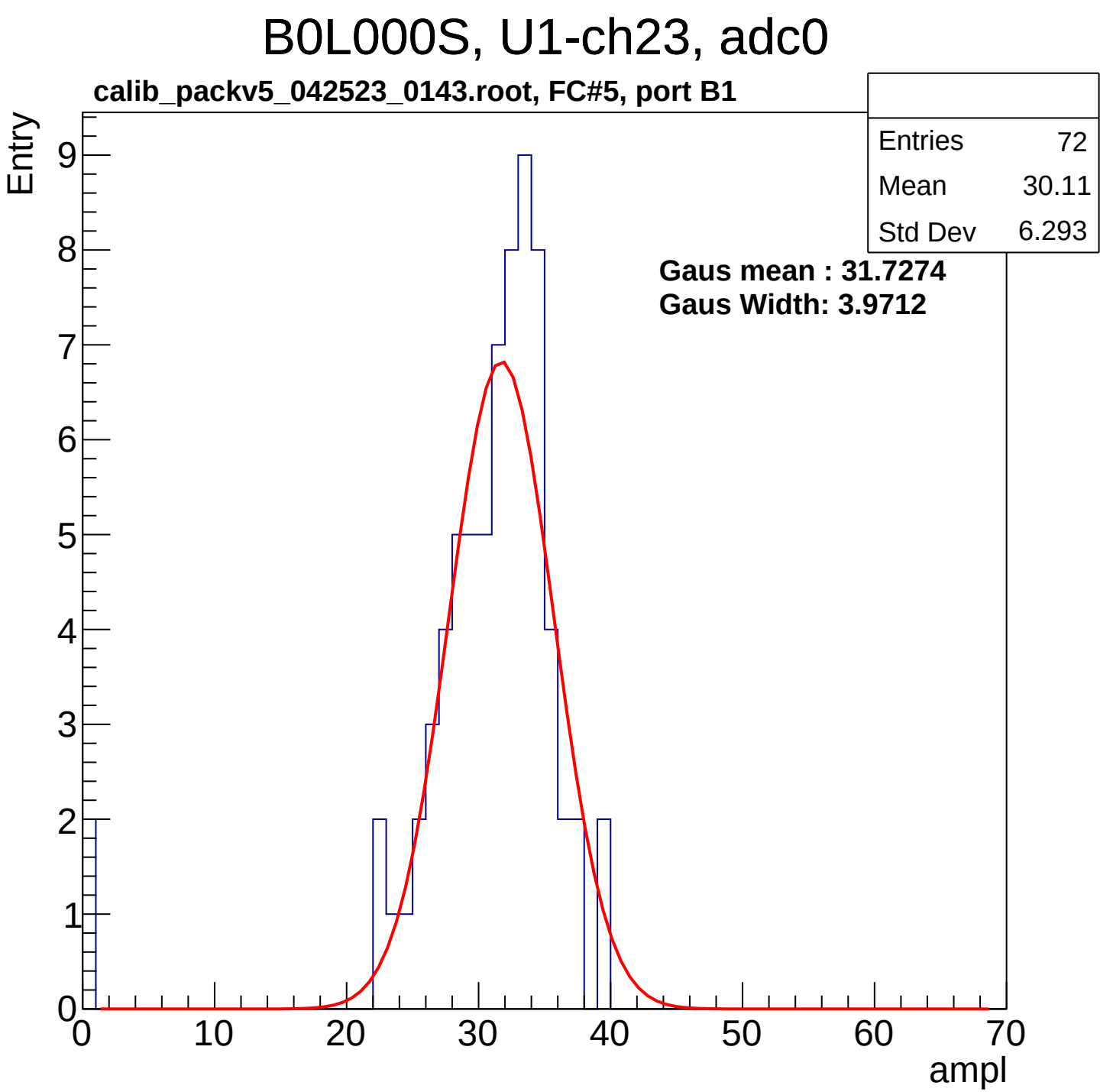
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch23, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 30.11
Std Dev 6.293
Gaus mean : 31.7274
Gaus Width: 3.9712
Entry
ampl
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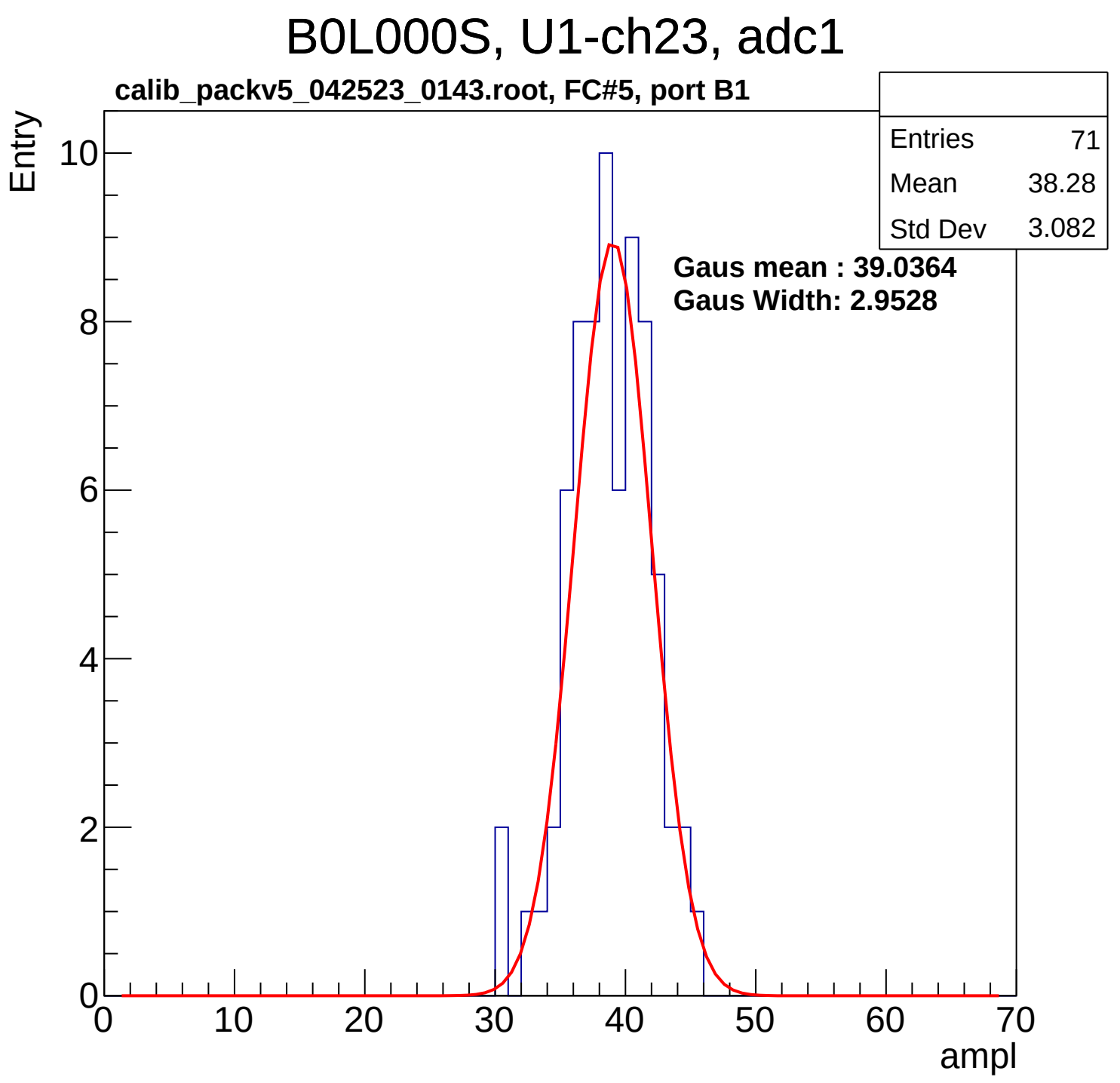
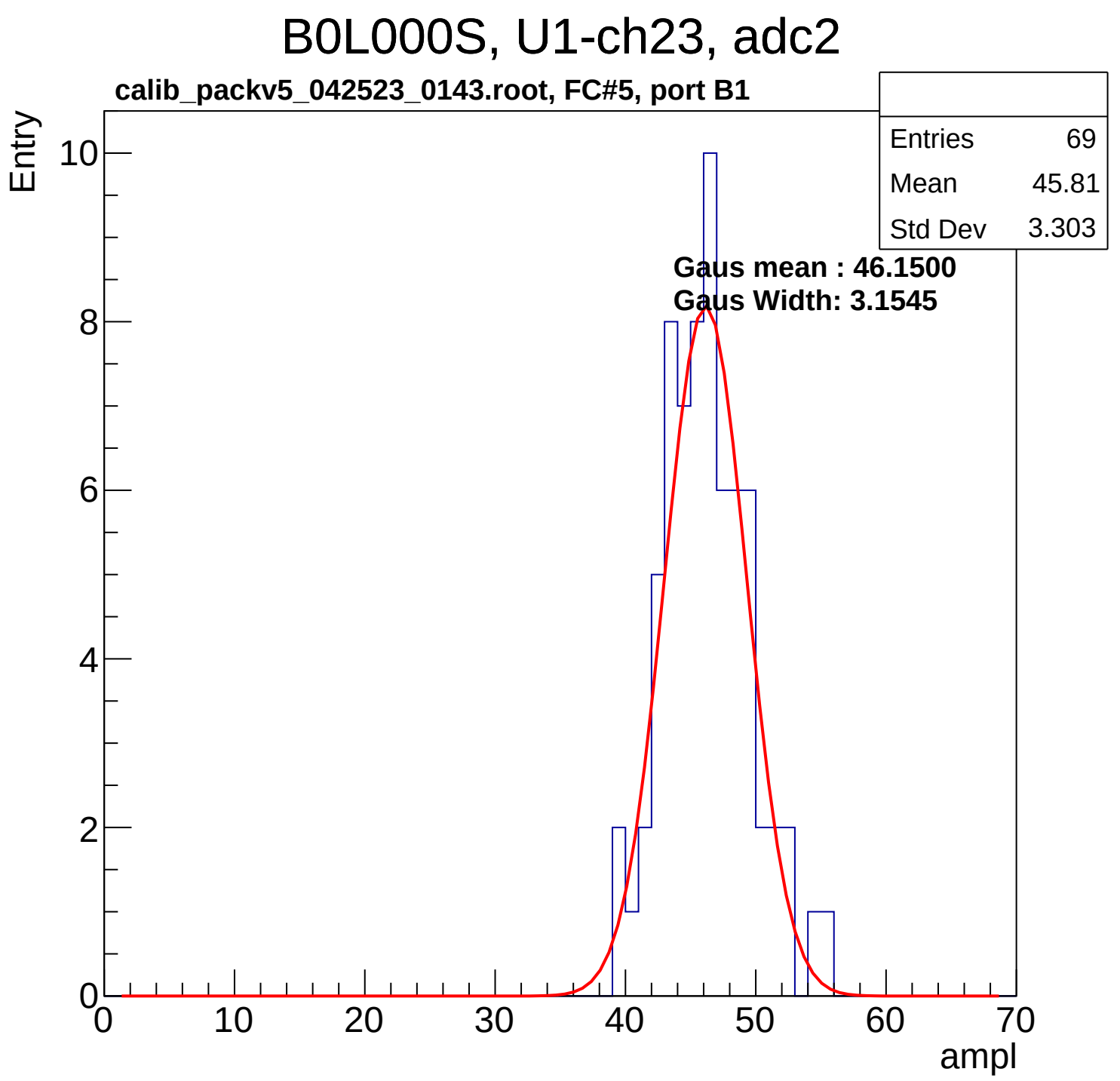
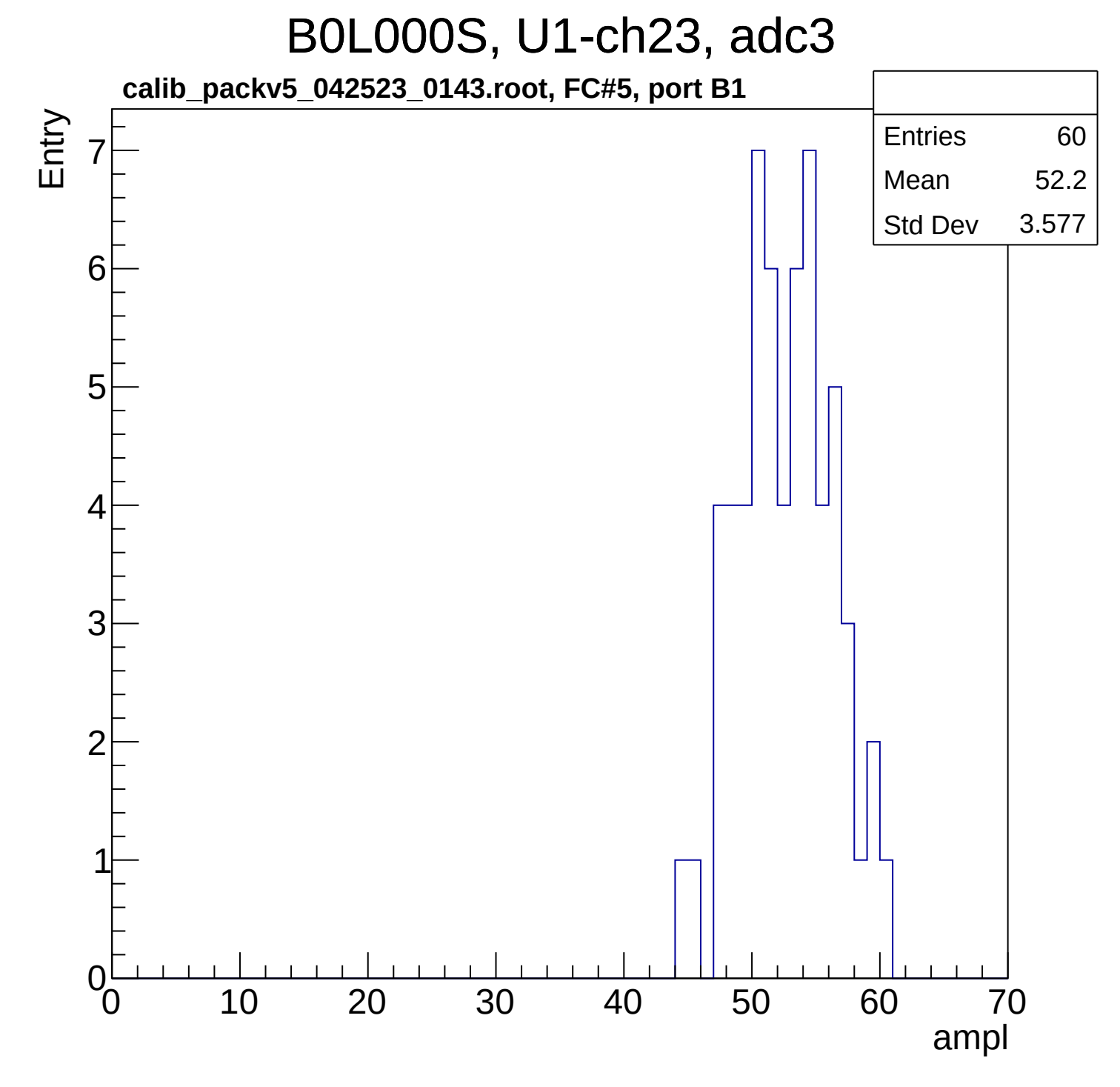
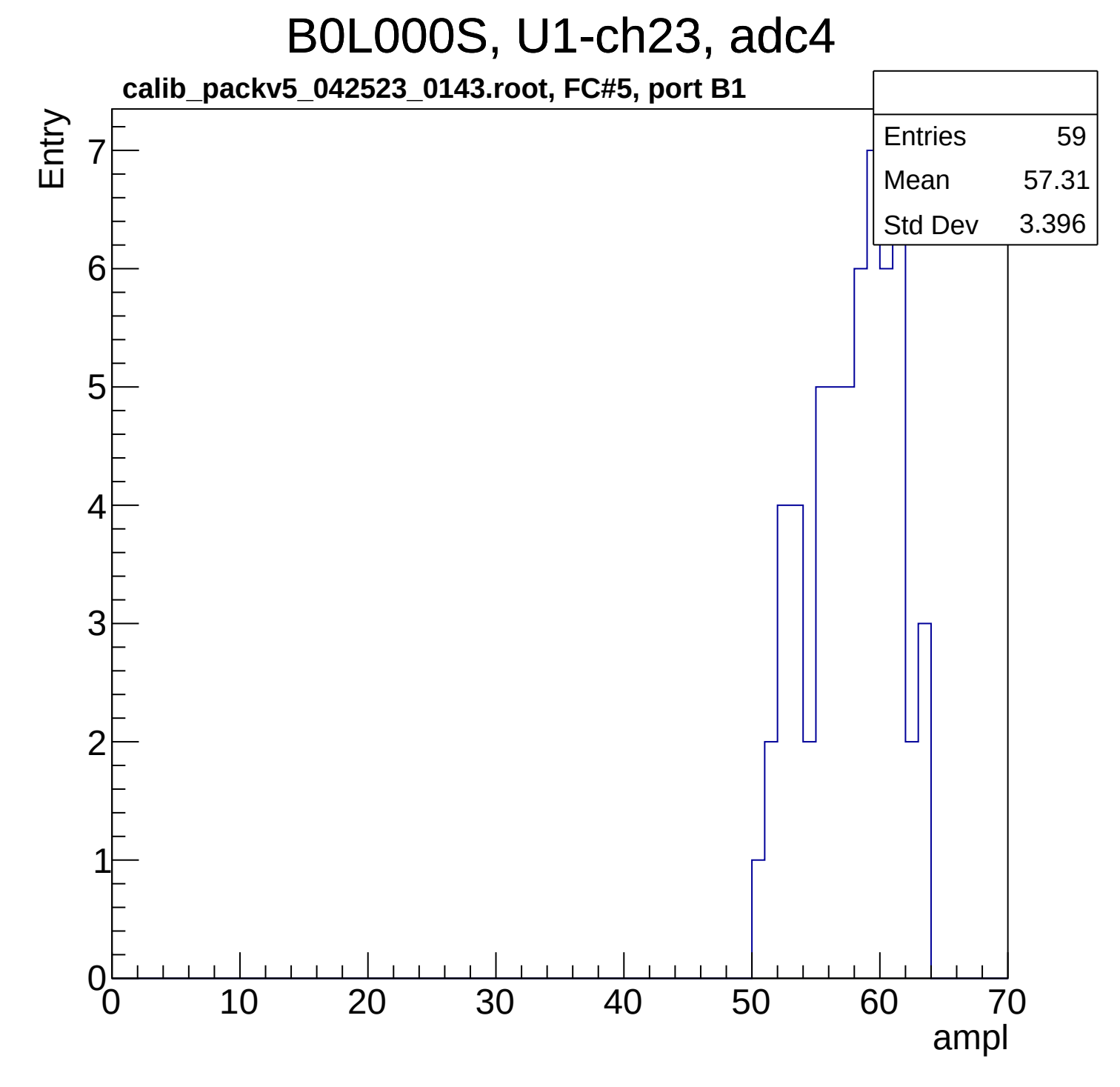
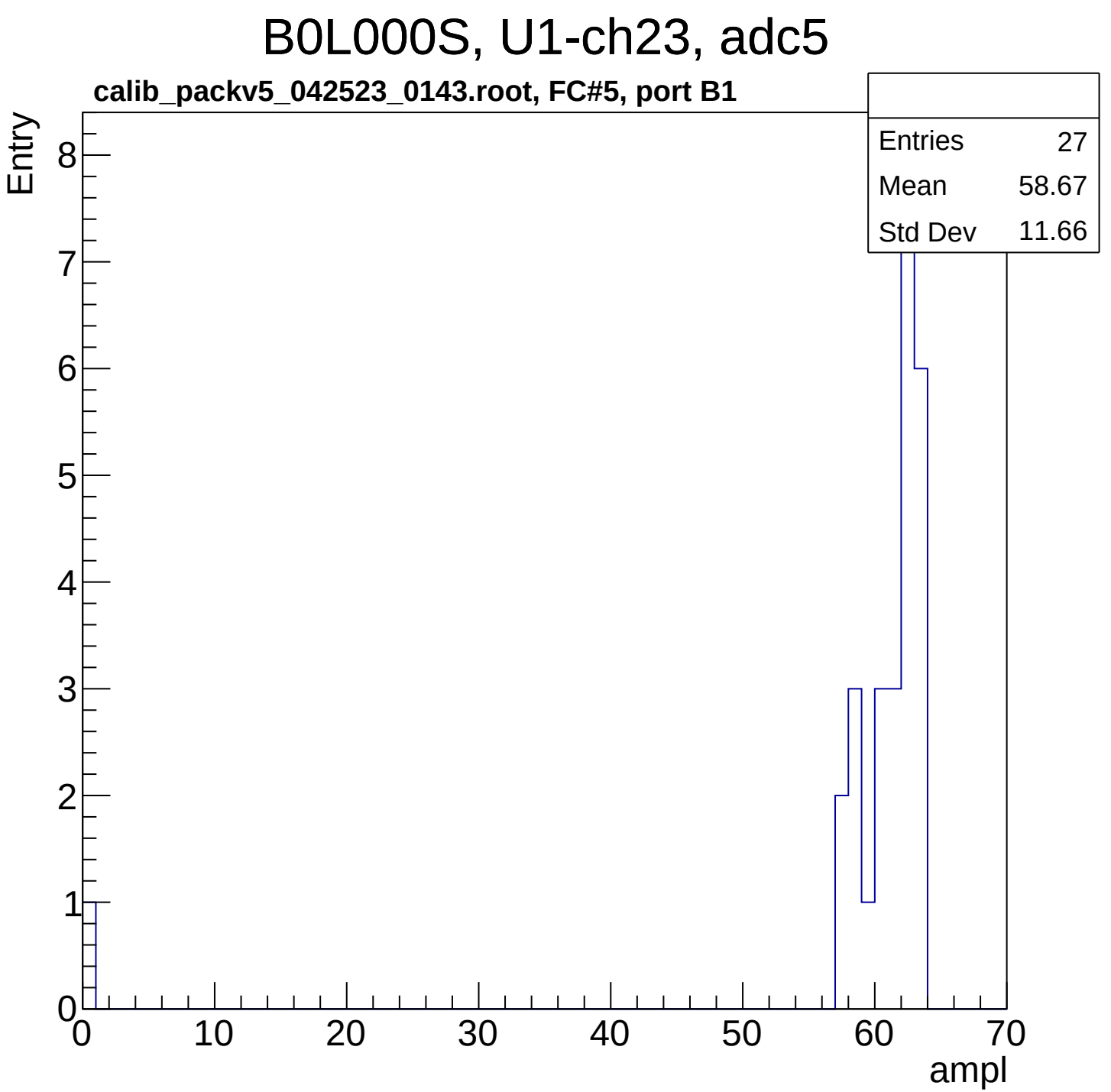
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

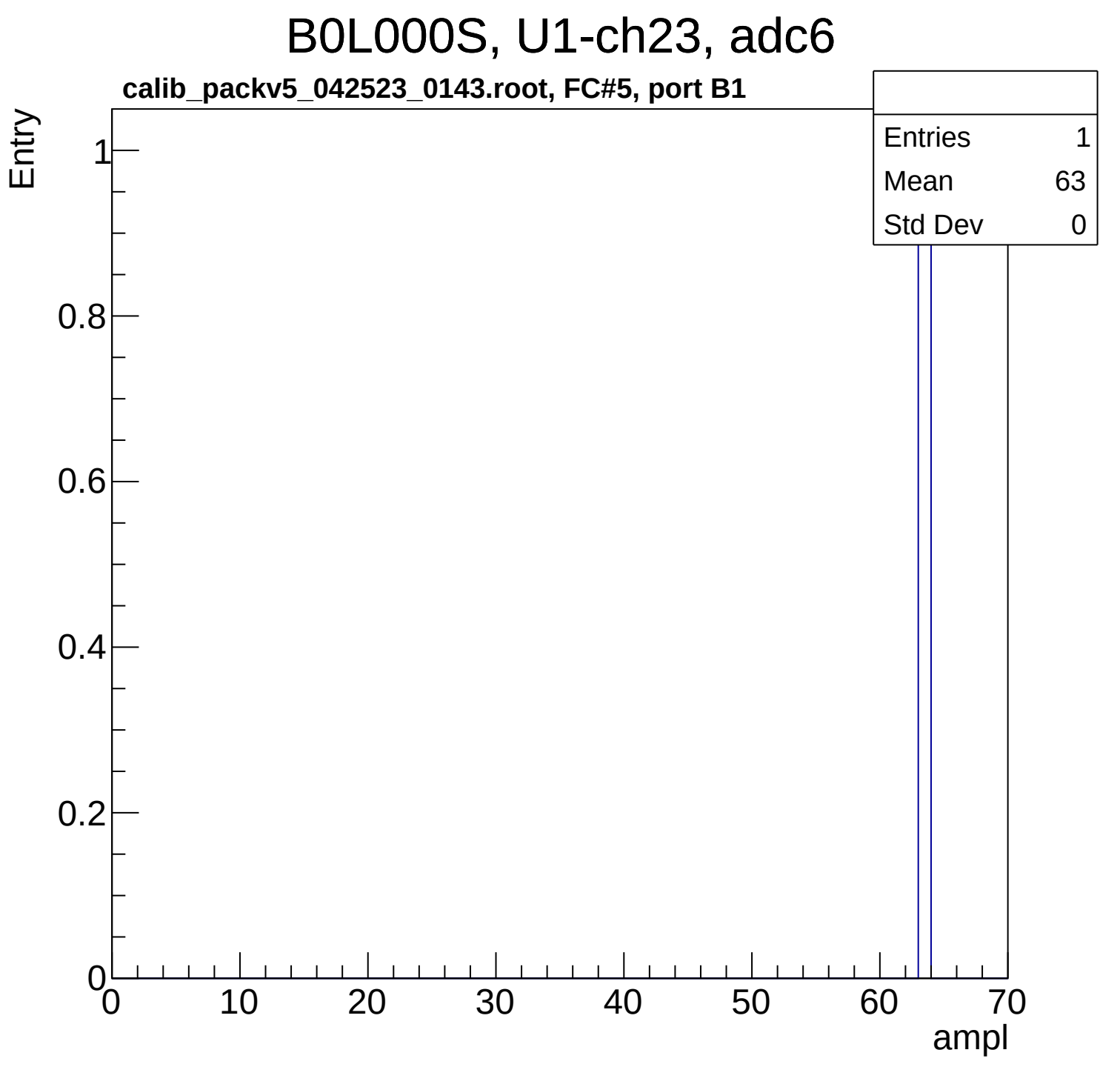

B0L000S, U1-ch23, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 38.28
Std Dev 3.082
Gaus mean : 39.0364
Gaus Width: 2.9528
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

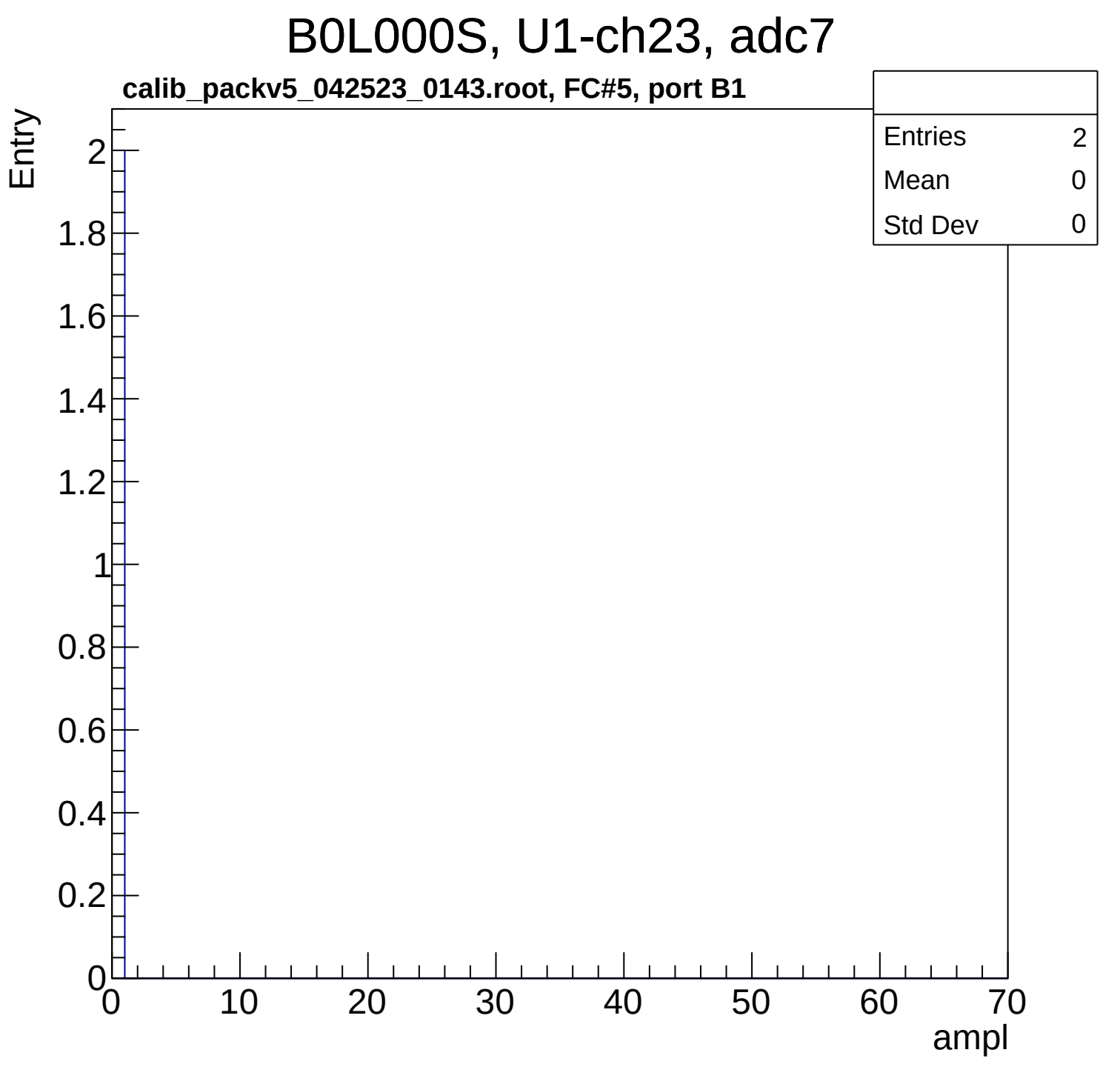

B0L000S, U1-ch23, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 45.81
Std Dev 3.303
Gaus mean : 46.1500
Gaus Width: 3.1545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

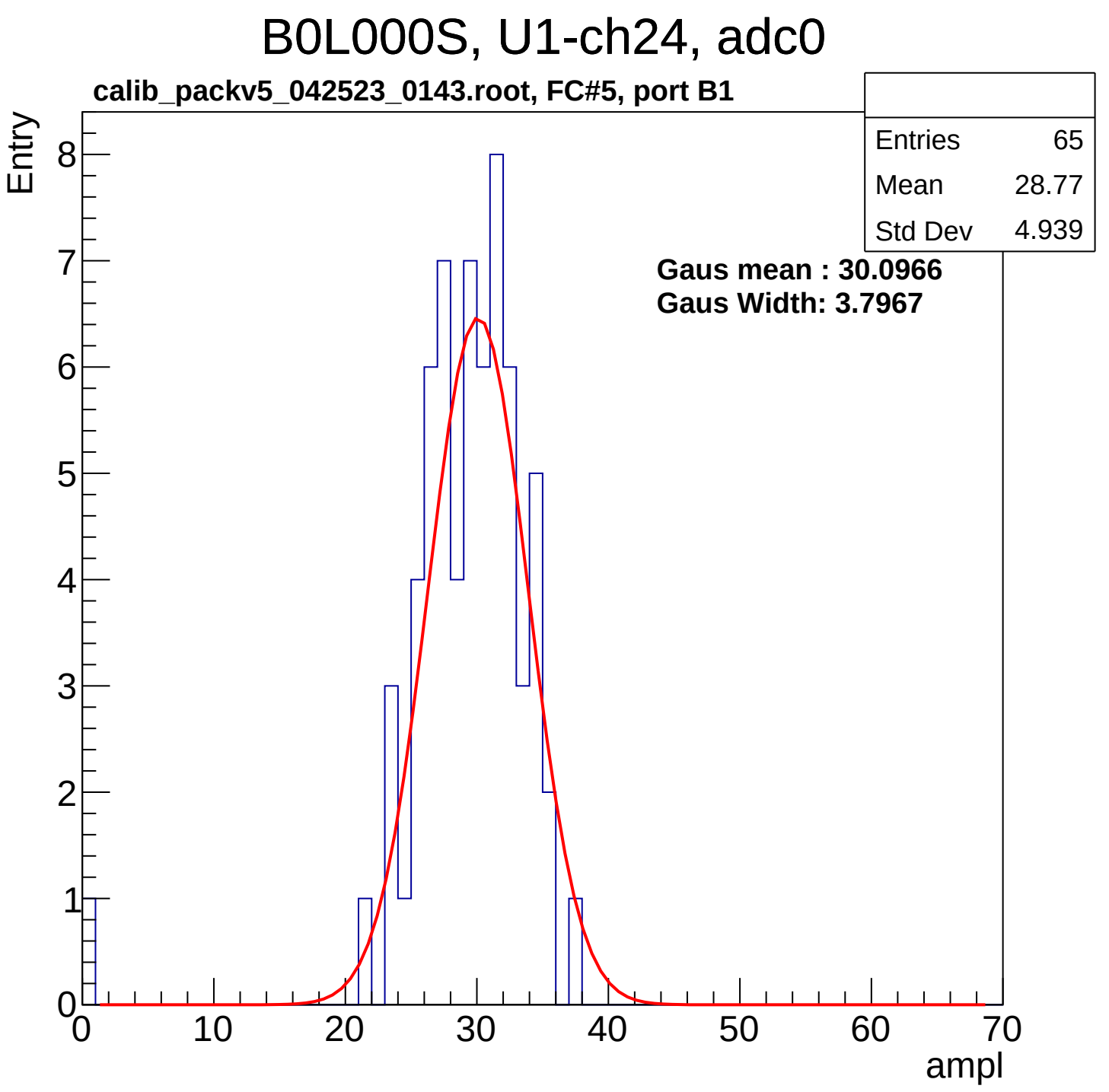

B0L000S, U1-ch23, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 52.2
Std Dev 3.577
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

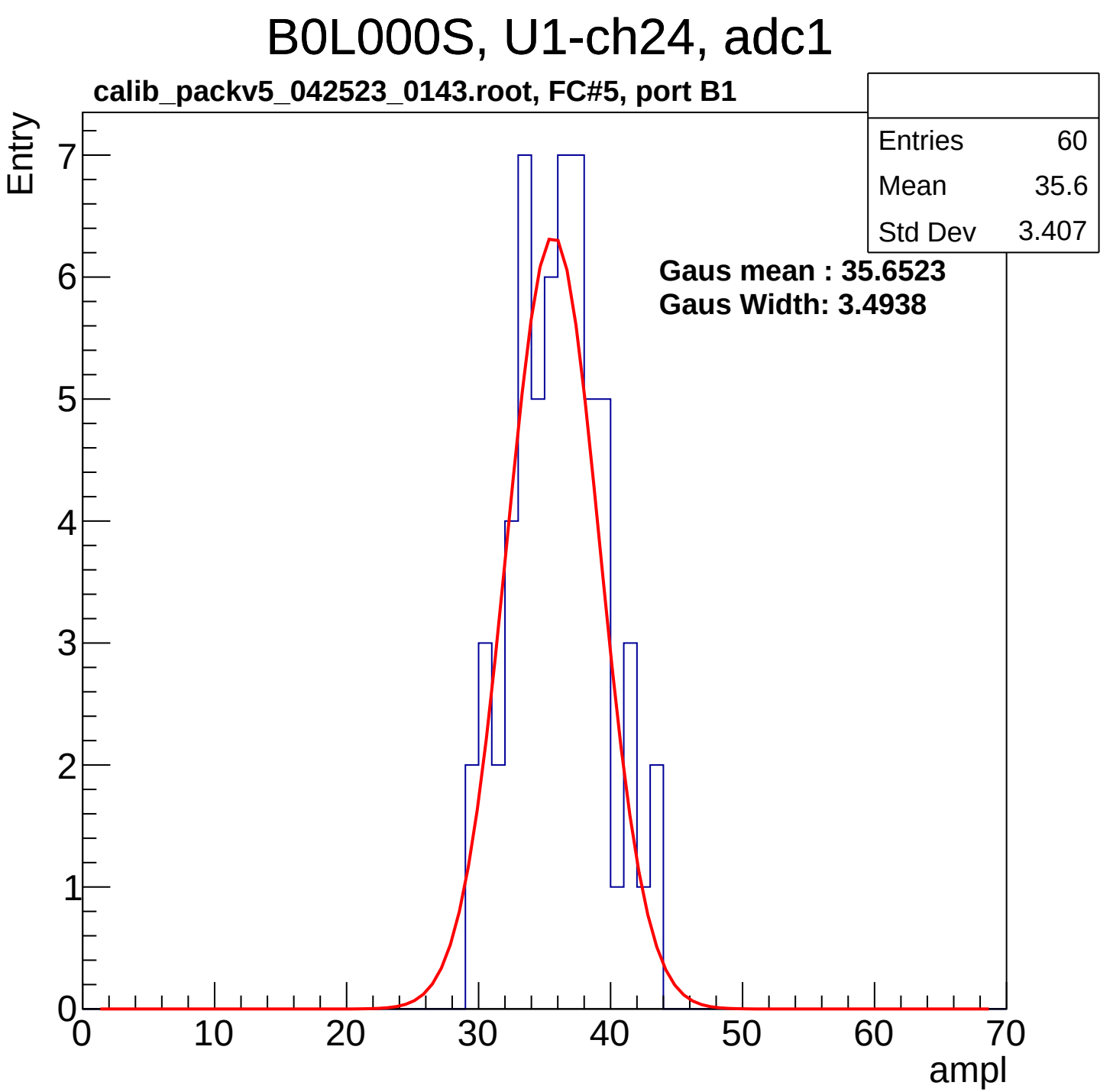

B0L000S, U1-ch23, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.31
Std Dev 3.396
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

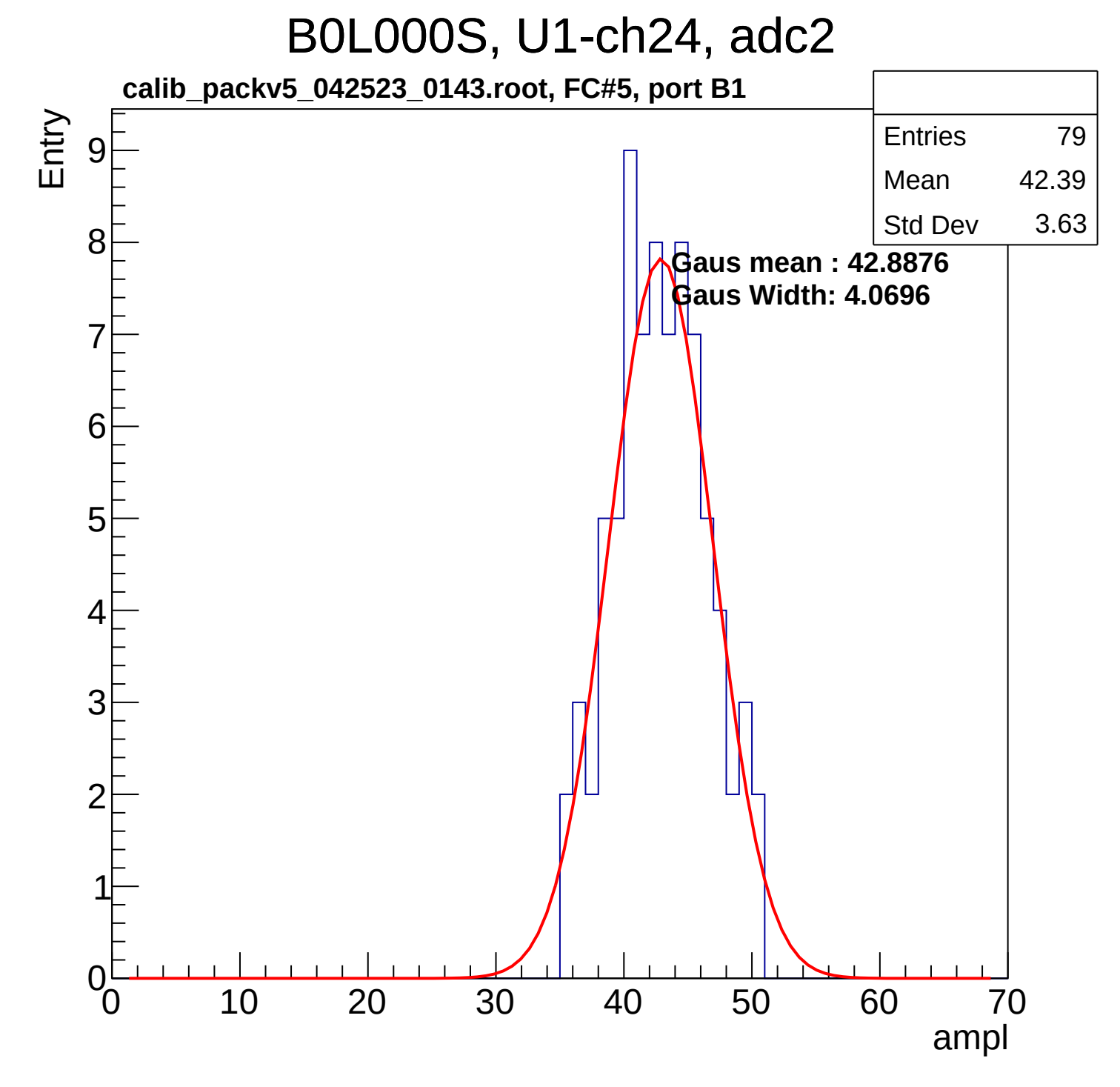

B0L000S, U1-ch23, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.67
Std Dev 11.66
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

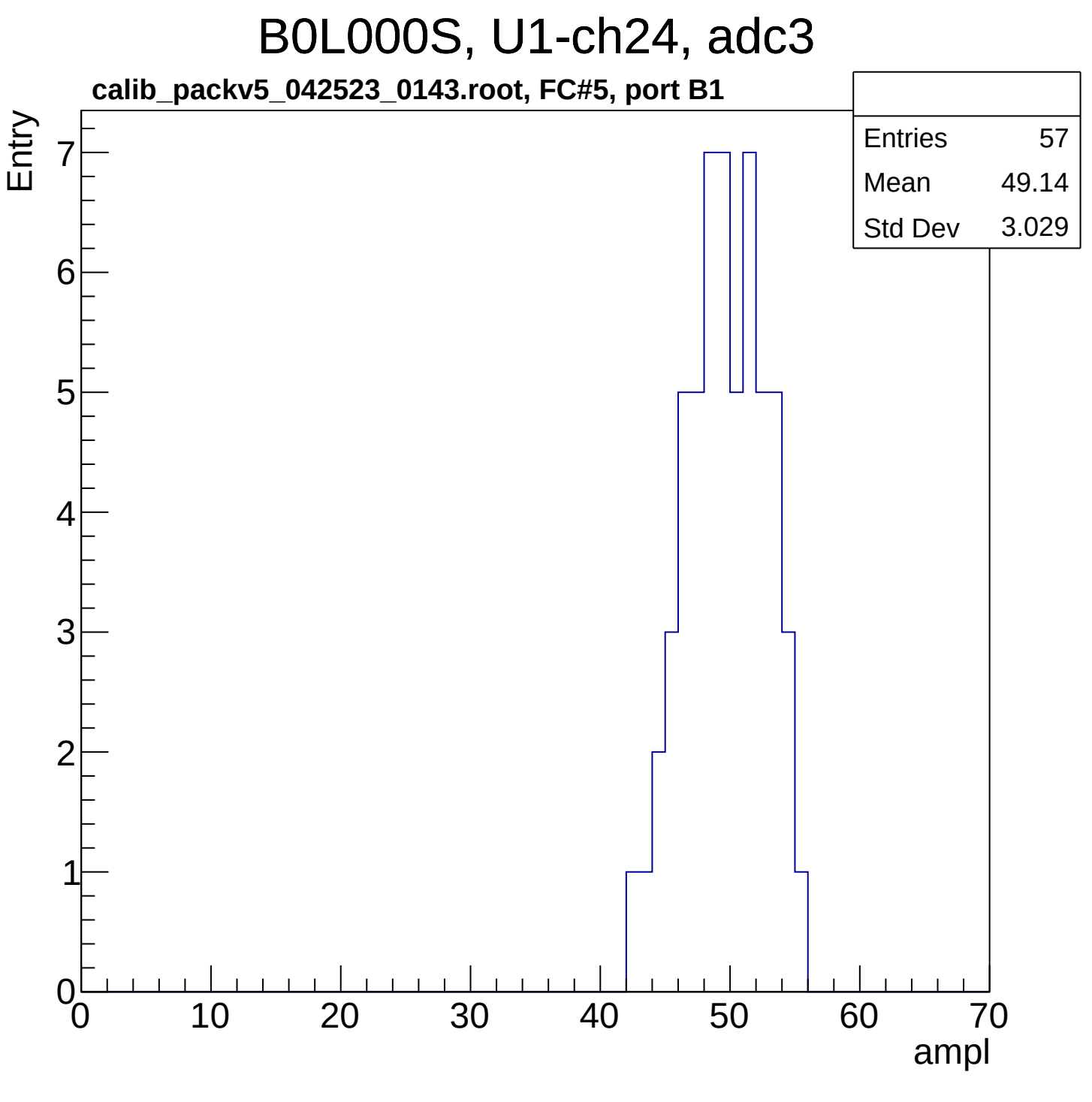

B0L000S, U1-ch23, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

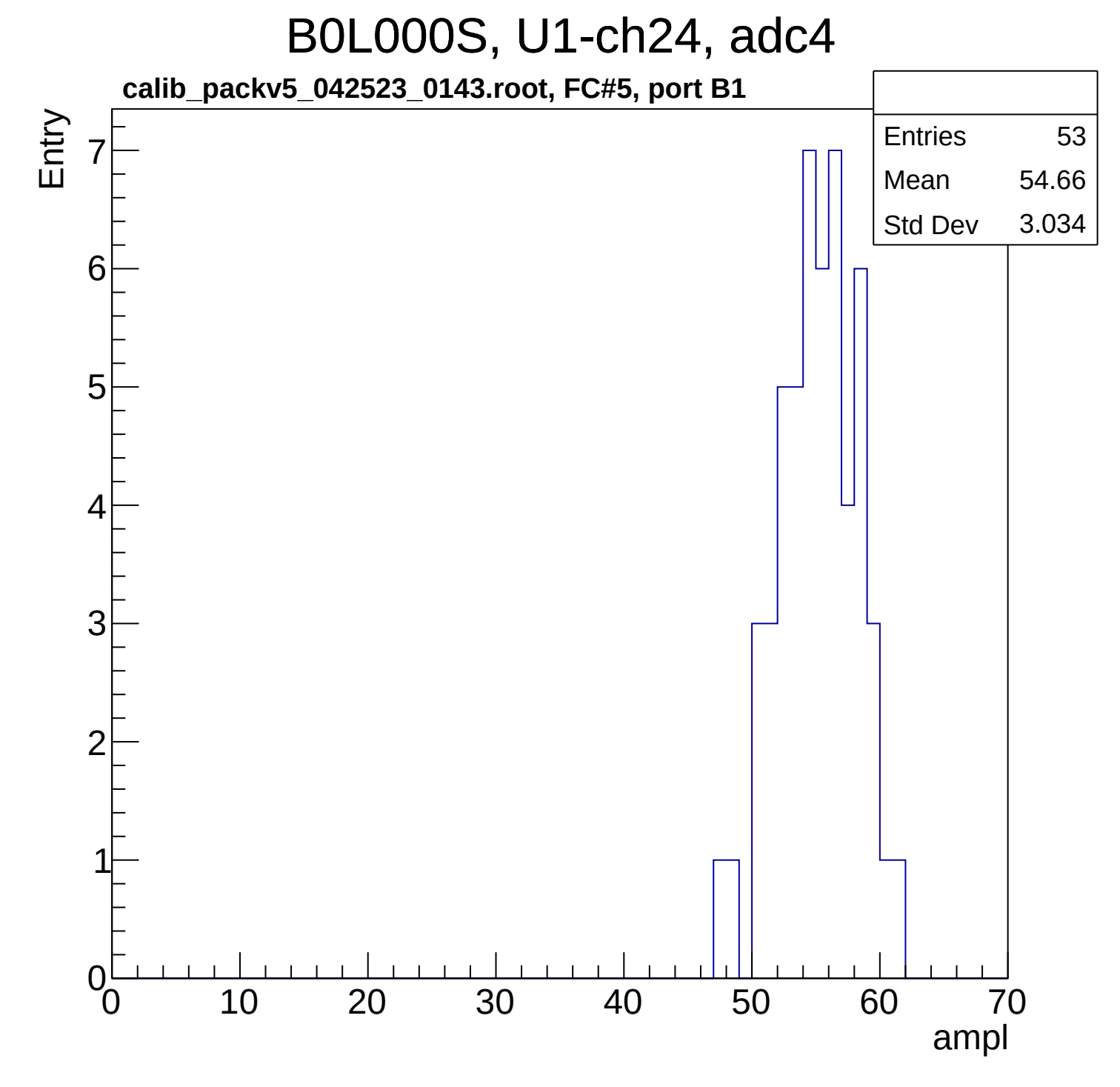

B0L000S, U1-ch23, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

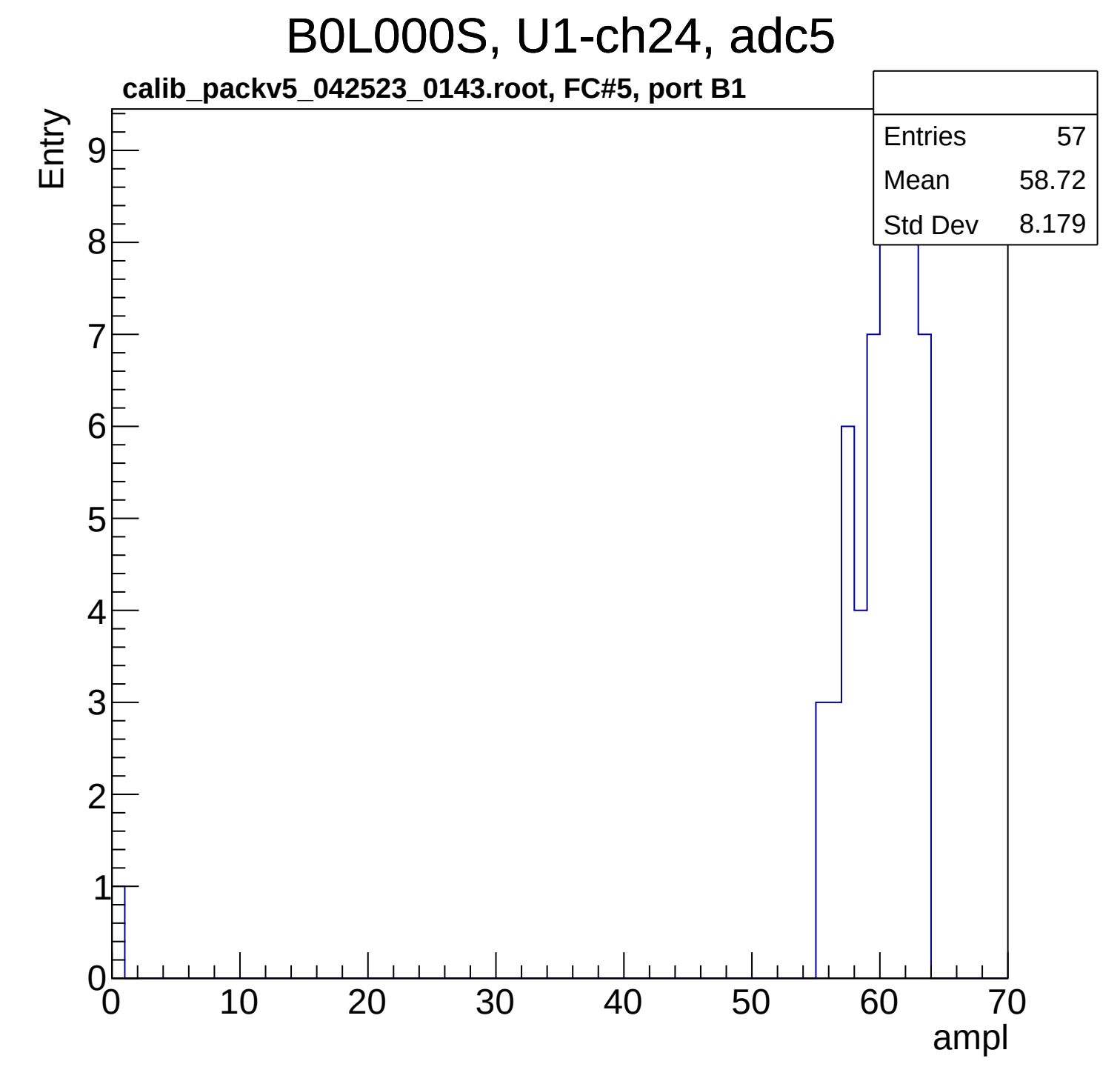

B0L000S, U1-ch24, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.77
Std Dev 4.939
Gaus mean : 30.0966
Gaus Width: 3.7967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

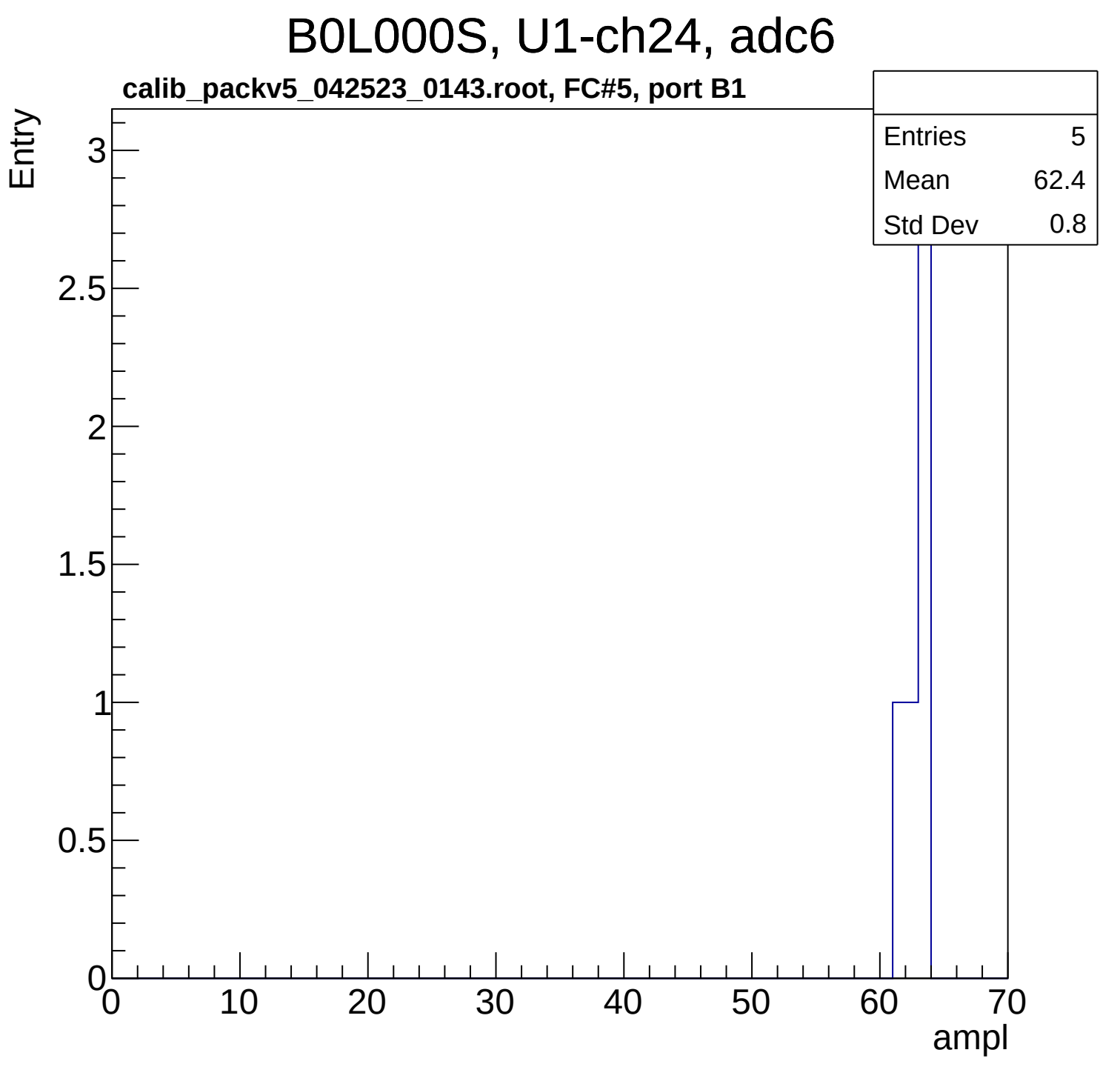

B0L000S, U1-ch24, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 35.6
Std Dev 3.407
Gaus mean : 35.6523
Gaus Width: 3.4938
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

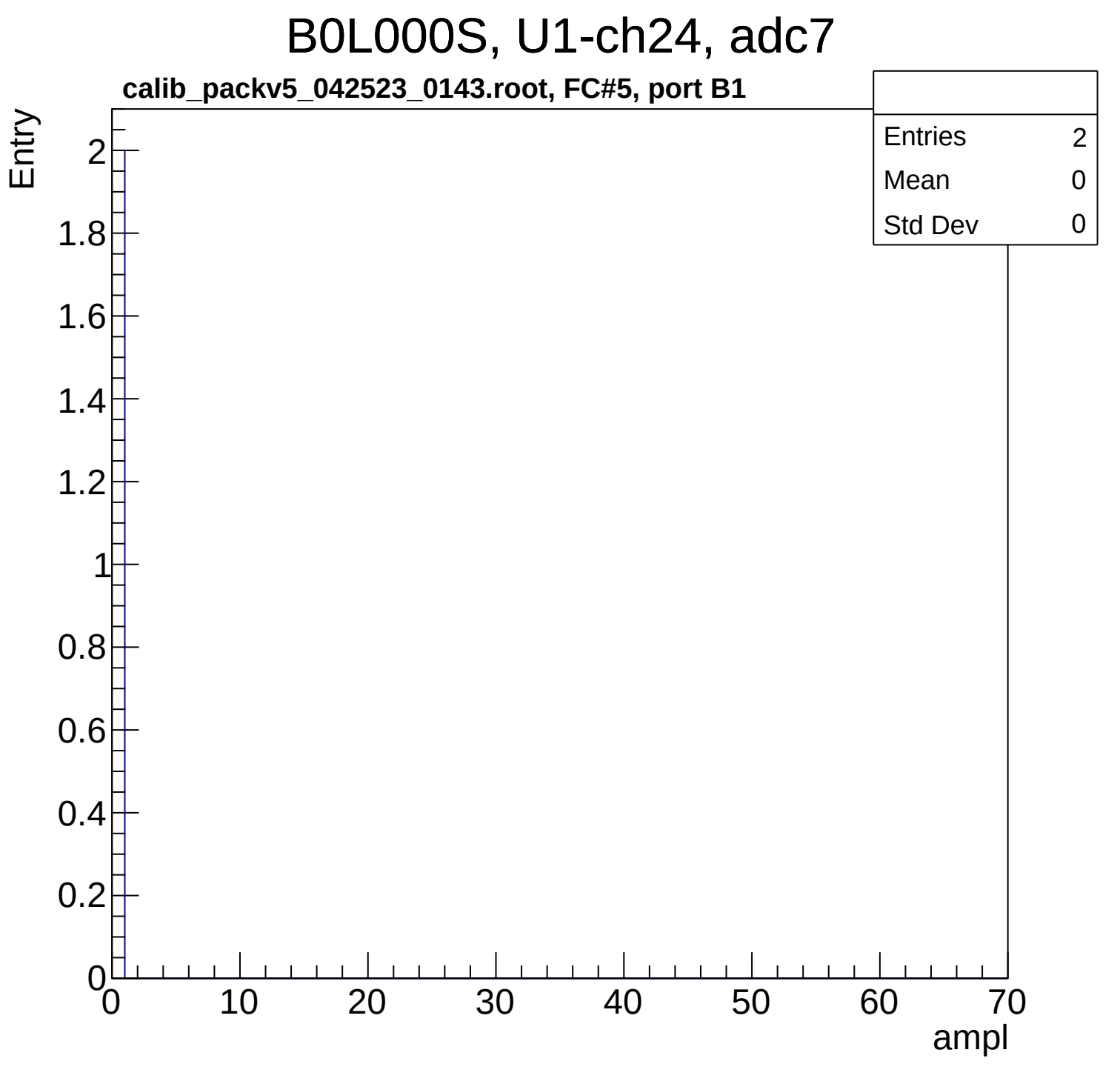

B0L000S, U1-ch24, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 42.39
Std Dev 3.63
Gaus mean : 42.8876
Gaus Width: 4.0696
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

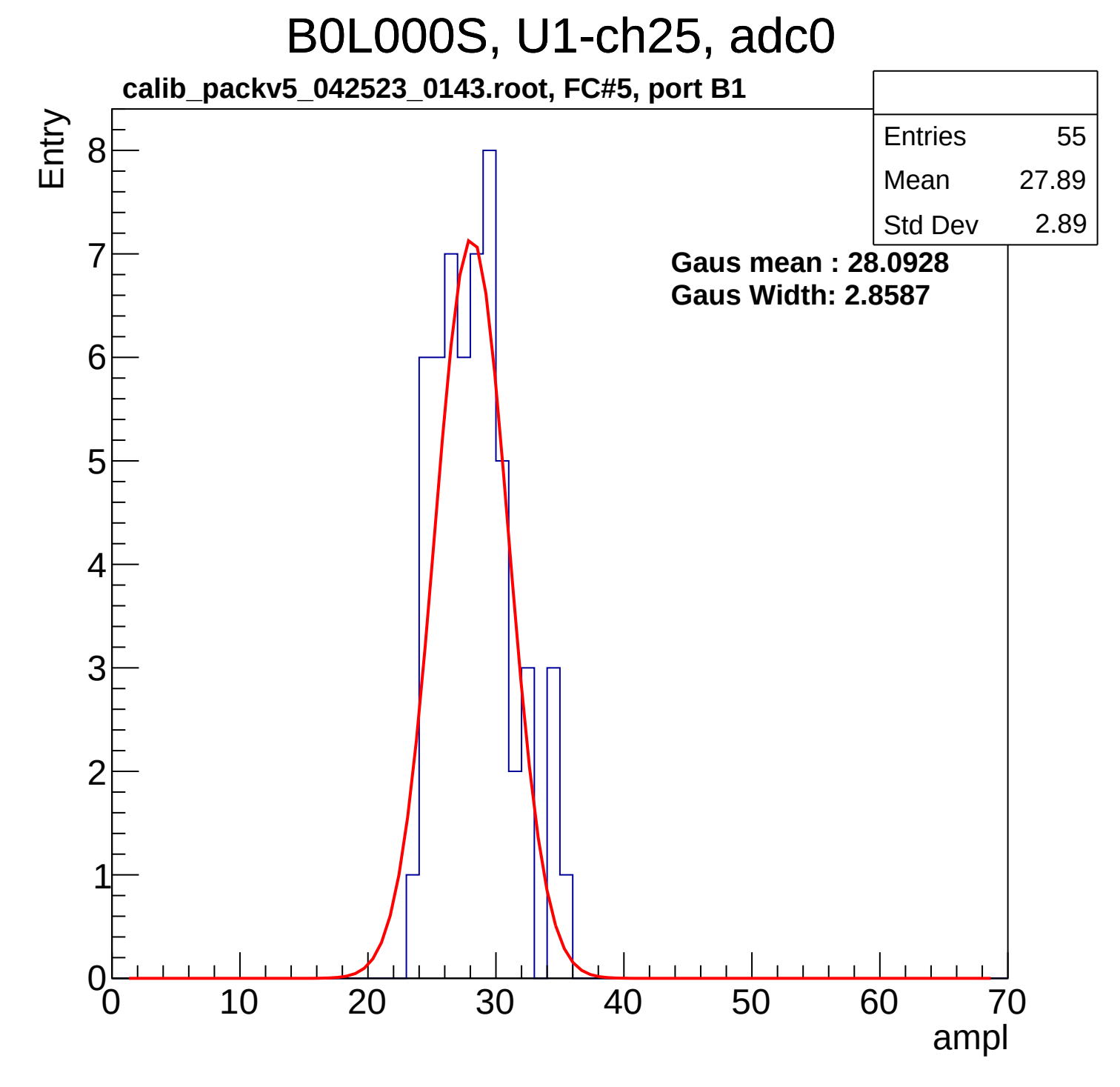

B0L000S, U1-ch24, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.14
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

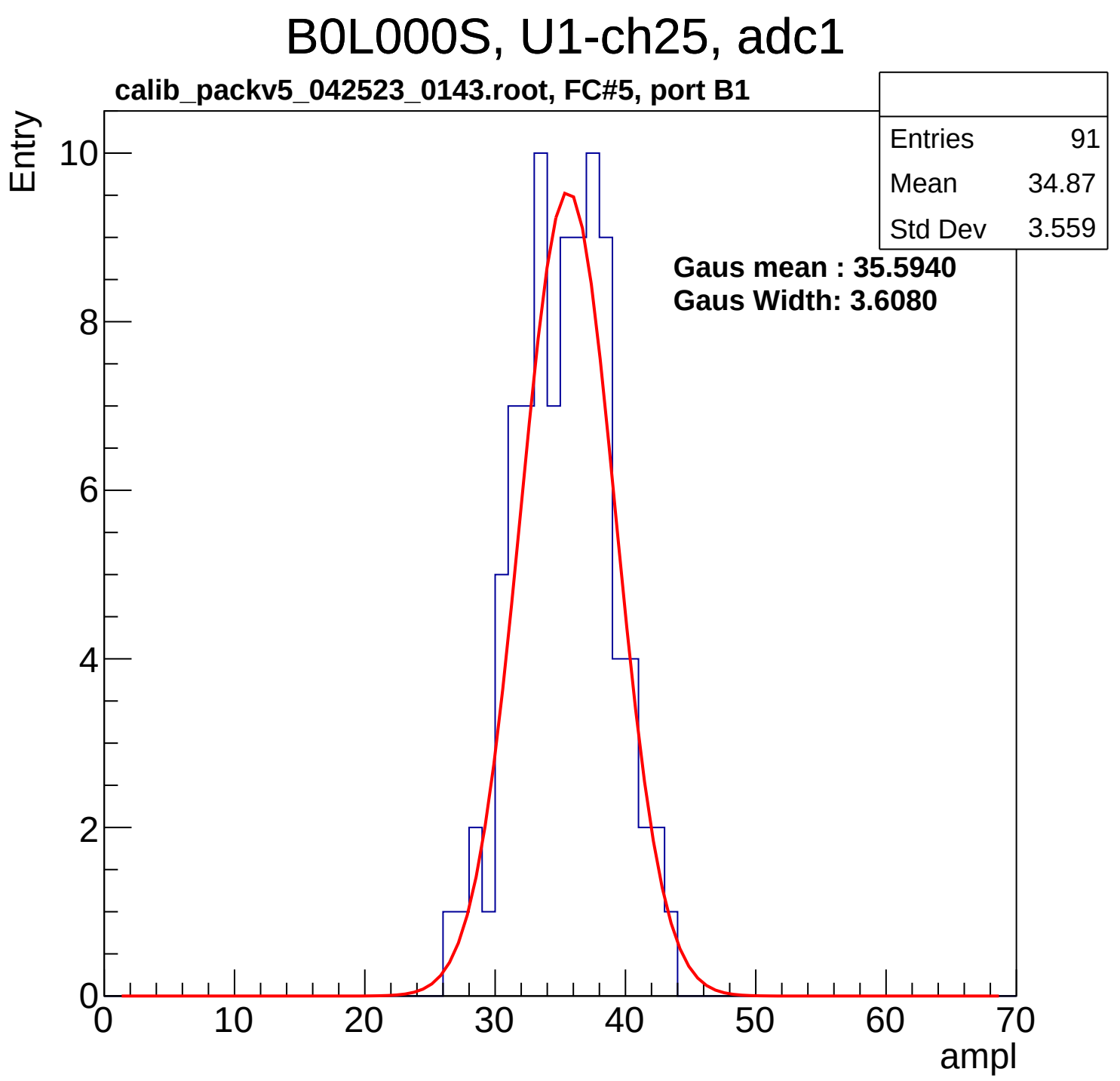

B0L000S, U1-ch24, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.66
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

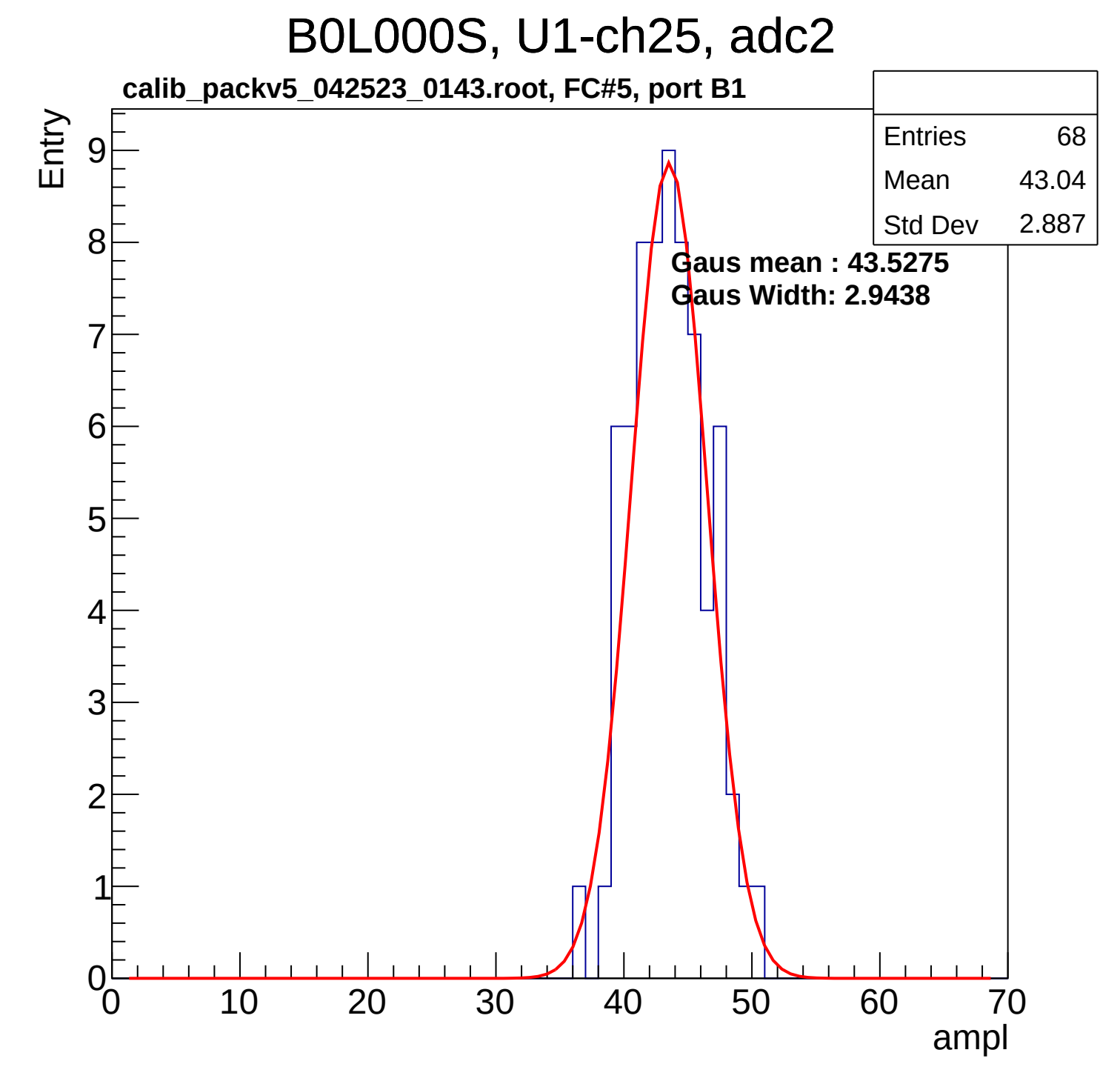

B0L000S, U1-ch24, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.72
Std Dev 8.179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

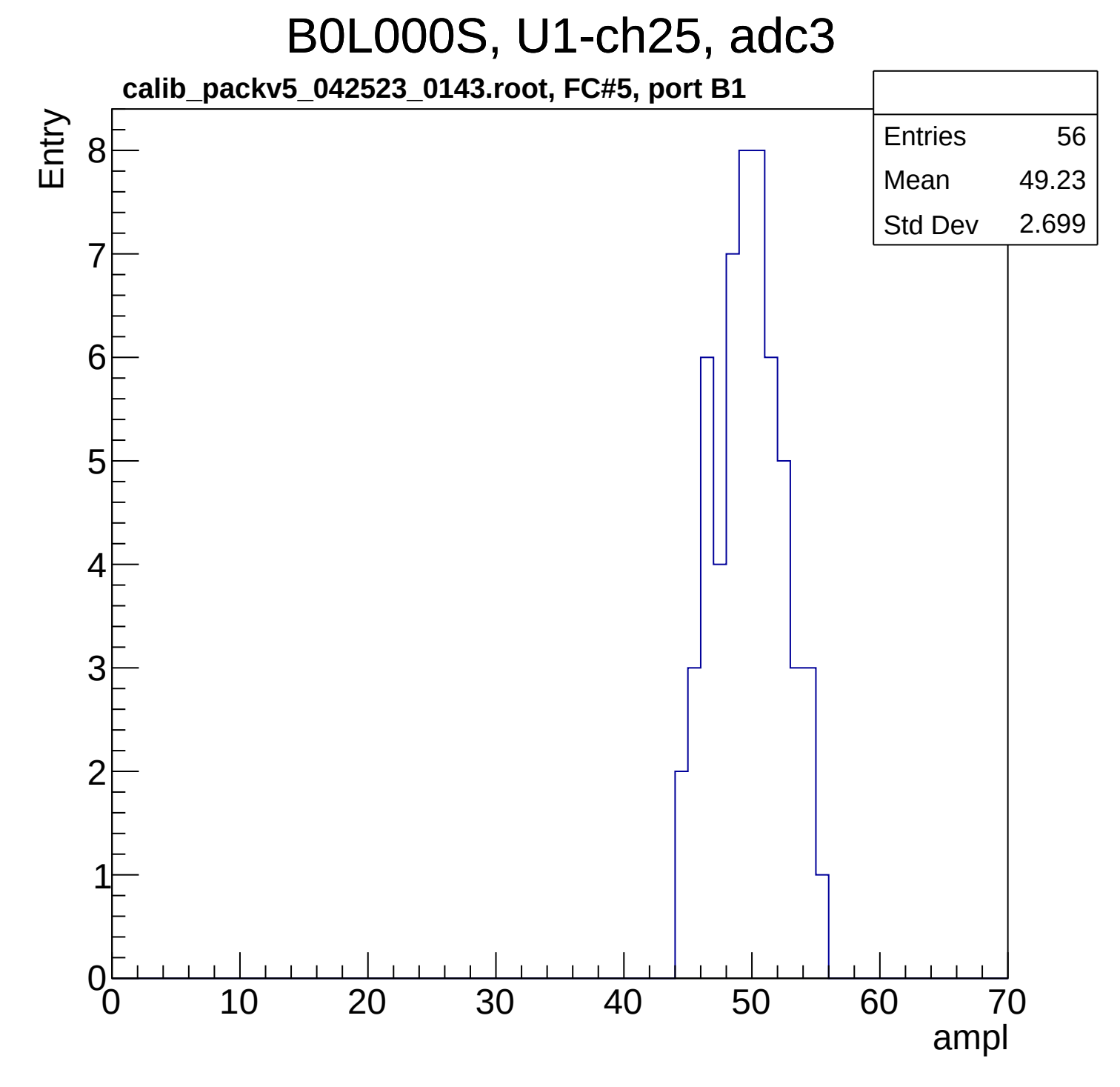

B0L000S, U1-ch24, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch24, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch25, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 27.89
Std Dev 2.89
Gaus mean : 28.0928
Gaus Width: 2.8587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch25, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 91
Mean 34.87
Std Dev 3.559
Gaus mean : 35.5940
Gaus Width: 3.6080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch25, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 43.04
Std Dev 2.887
Gaus mean : 43.5275
Gaus Width: 2.9438
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U1-ch25, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 49.23
Std Dev 2.699
Entry
ampl
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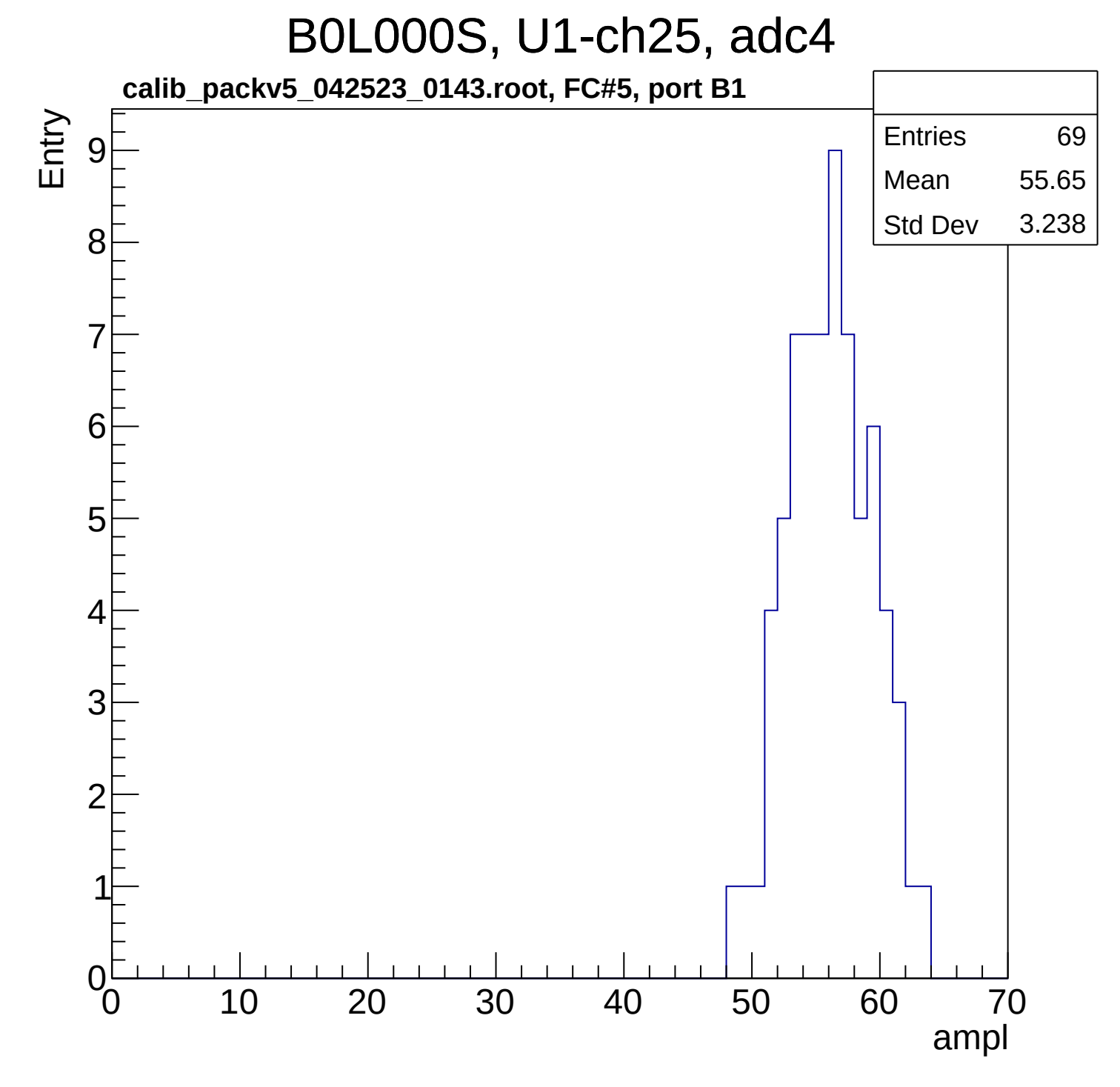
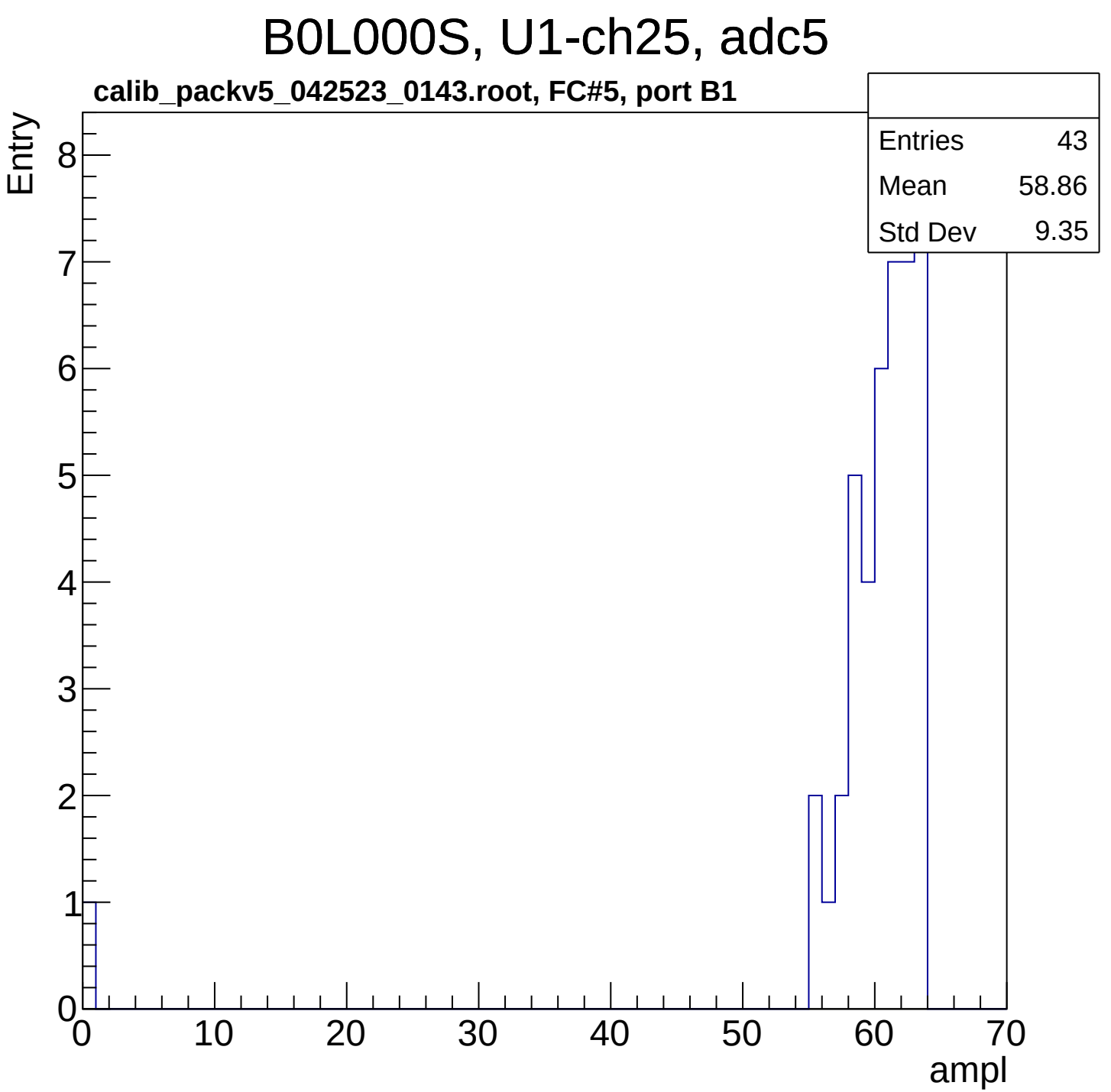
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch25, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 55.65
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch25, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.86
Std Dev 9.35
Entry
ampl
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1
2
3
4
5
6
7
8
0
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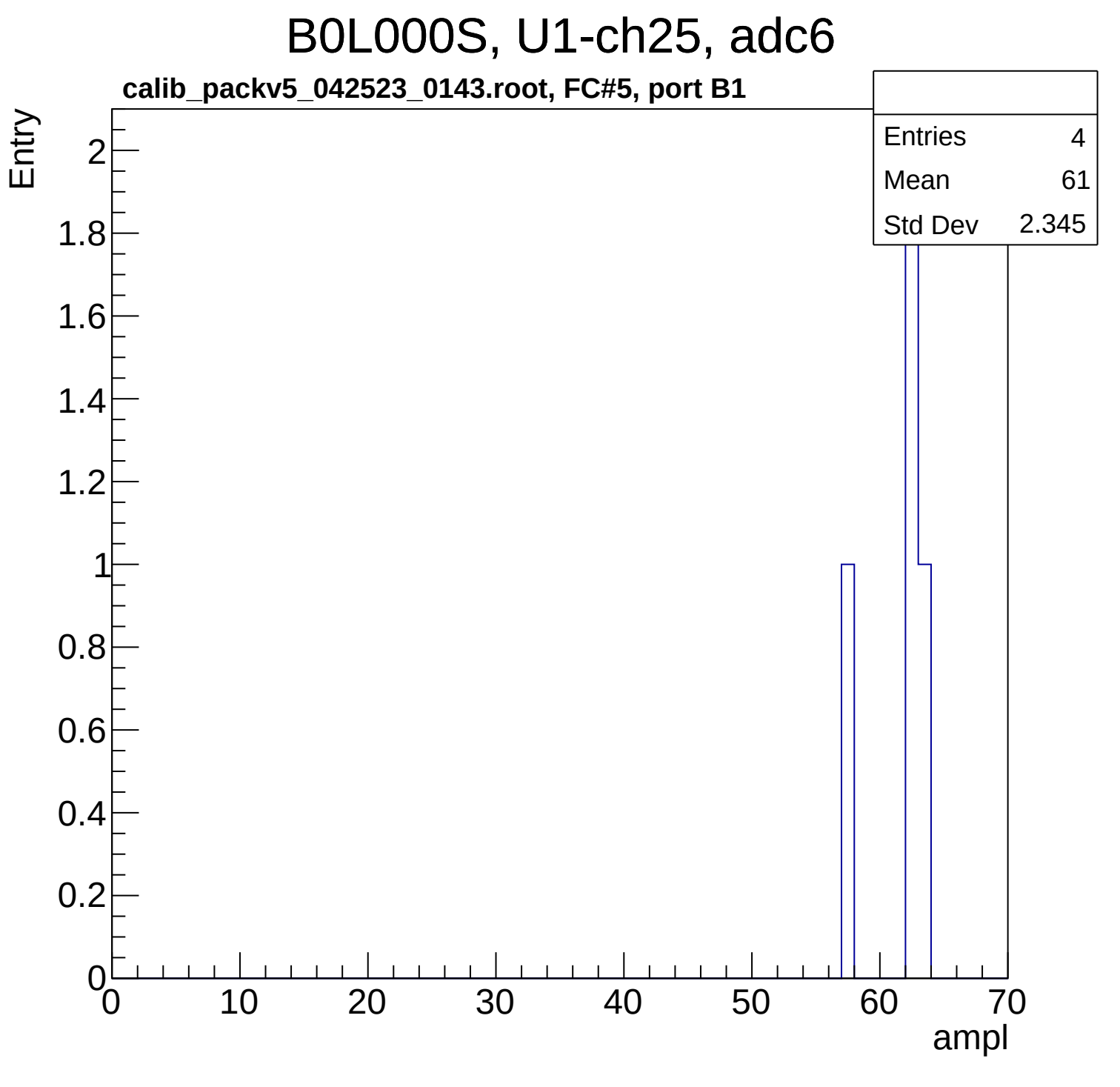
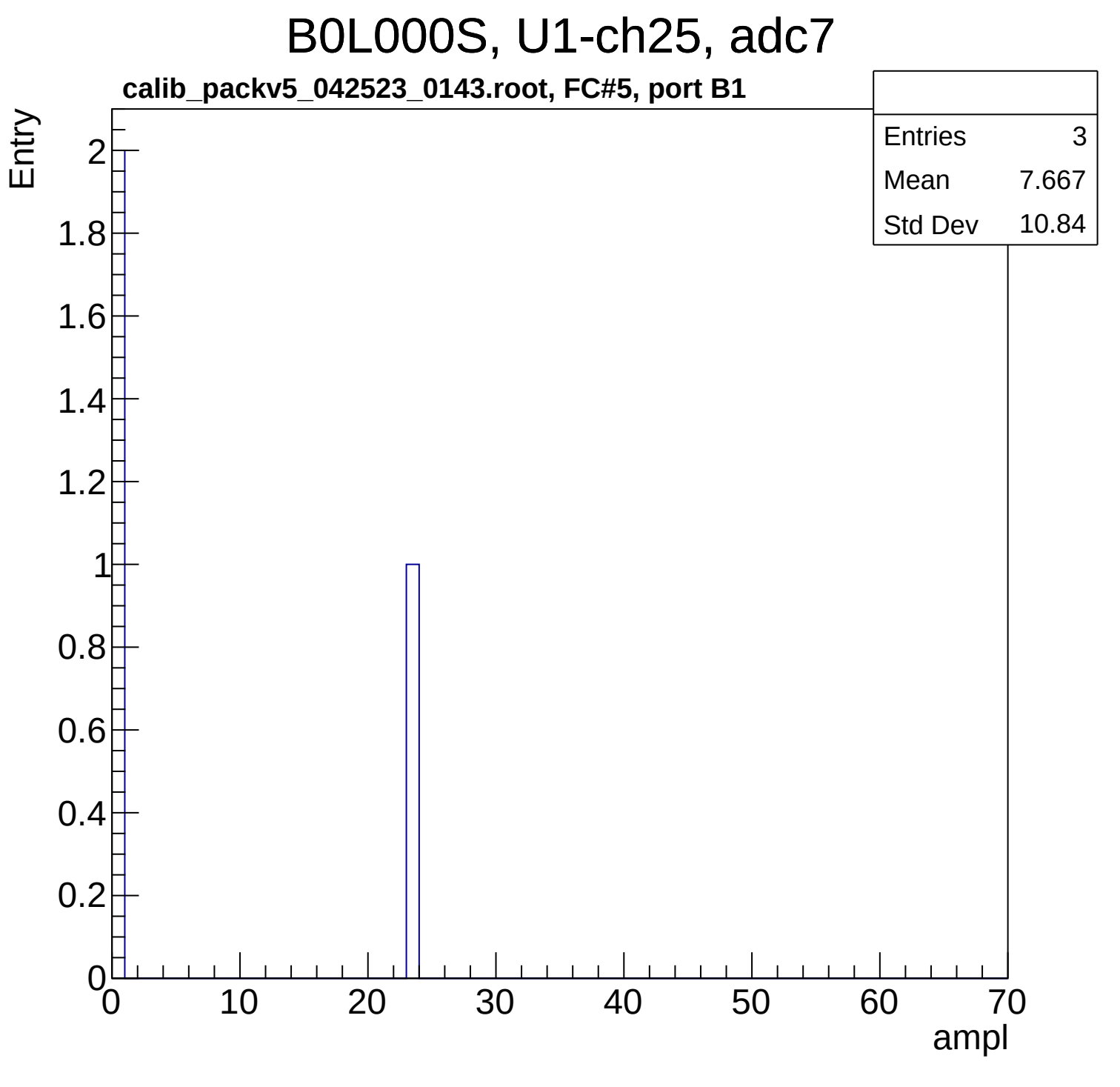
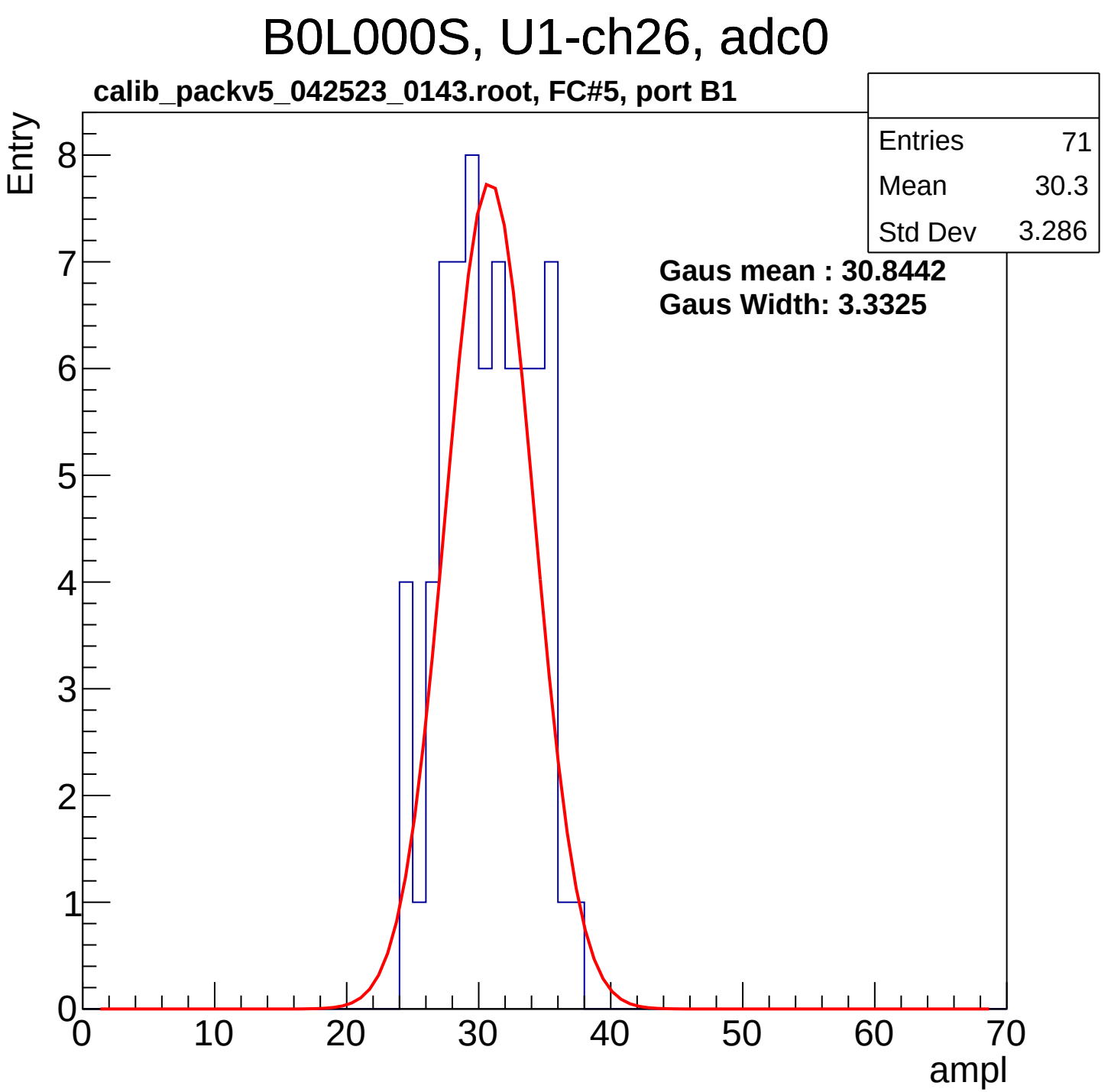
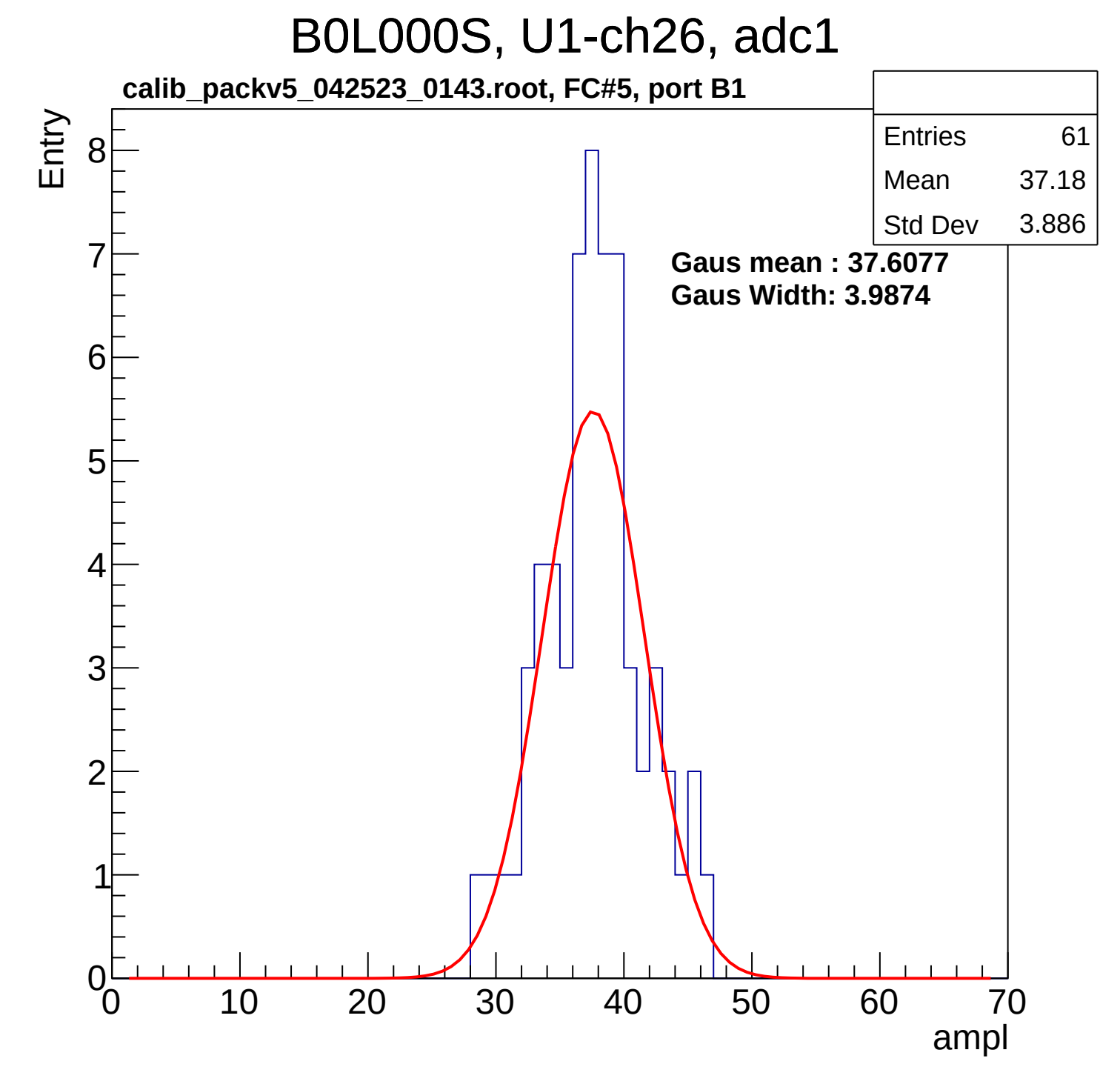
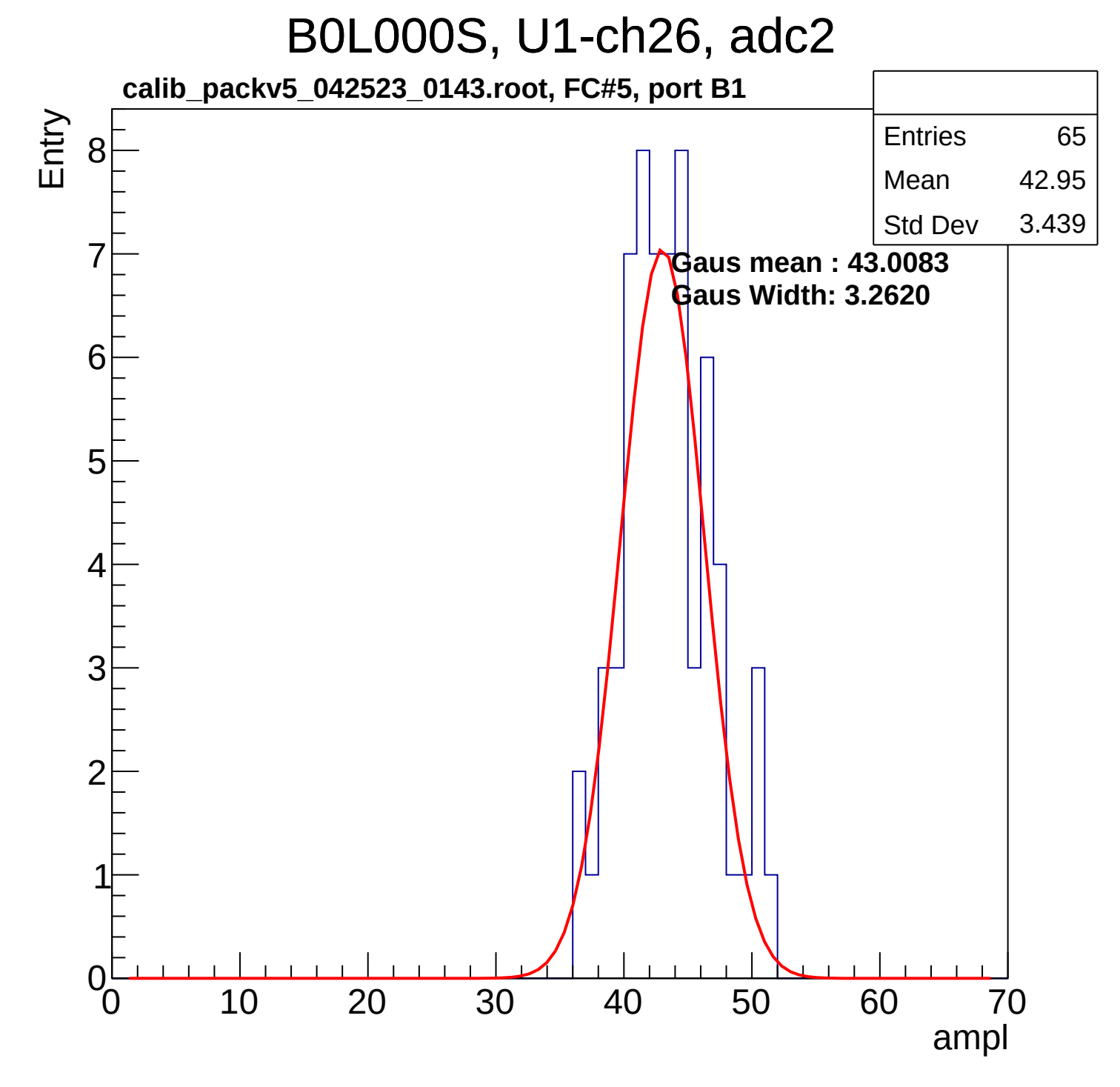
20
30
40
50
60
70


B0L000S, U1-ch25, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61
Std Dev 2.345
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch25, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch26, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.3
Std Dev 3.286
Gaus mean : 30.8442
Gaus Width: 3.3325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch26, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 37.18
Std Dev 3.886
Gaus mean : 37.6077
Gaus Width: 3.9874
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch26, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.95
Std Dev 3.439
Gaus mean : 43.0083
Gaus Width: 3.2620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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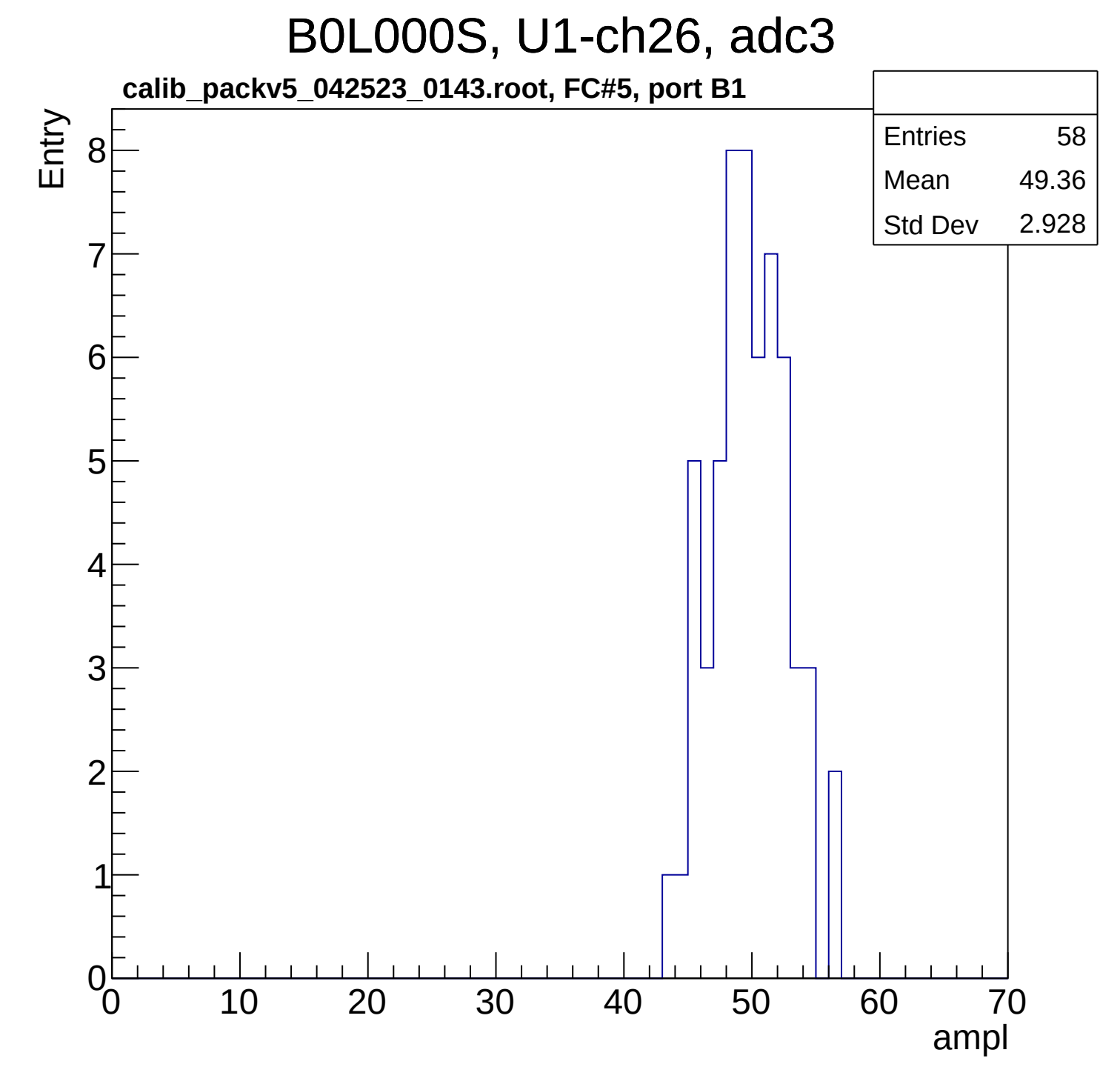
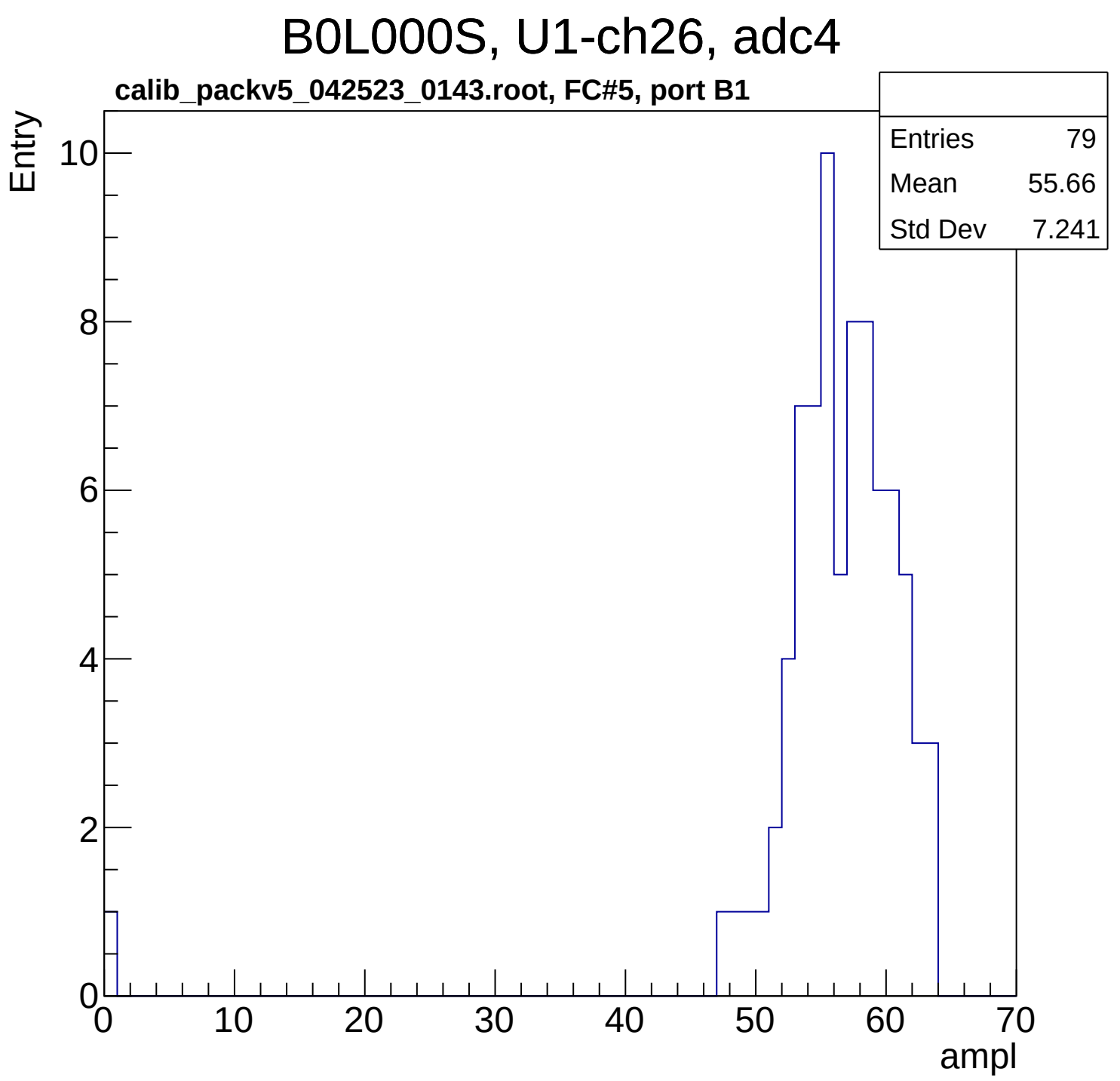
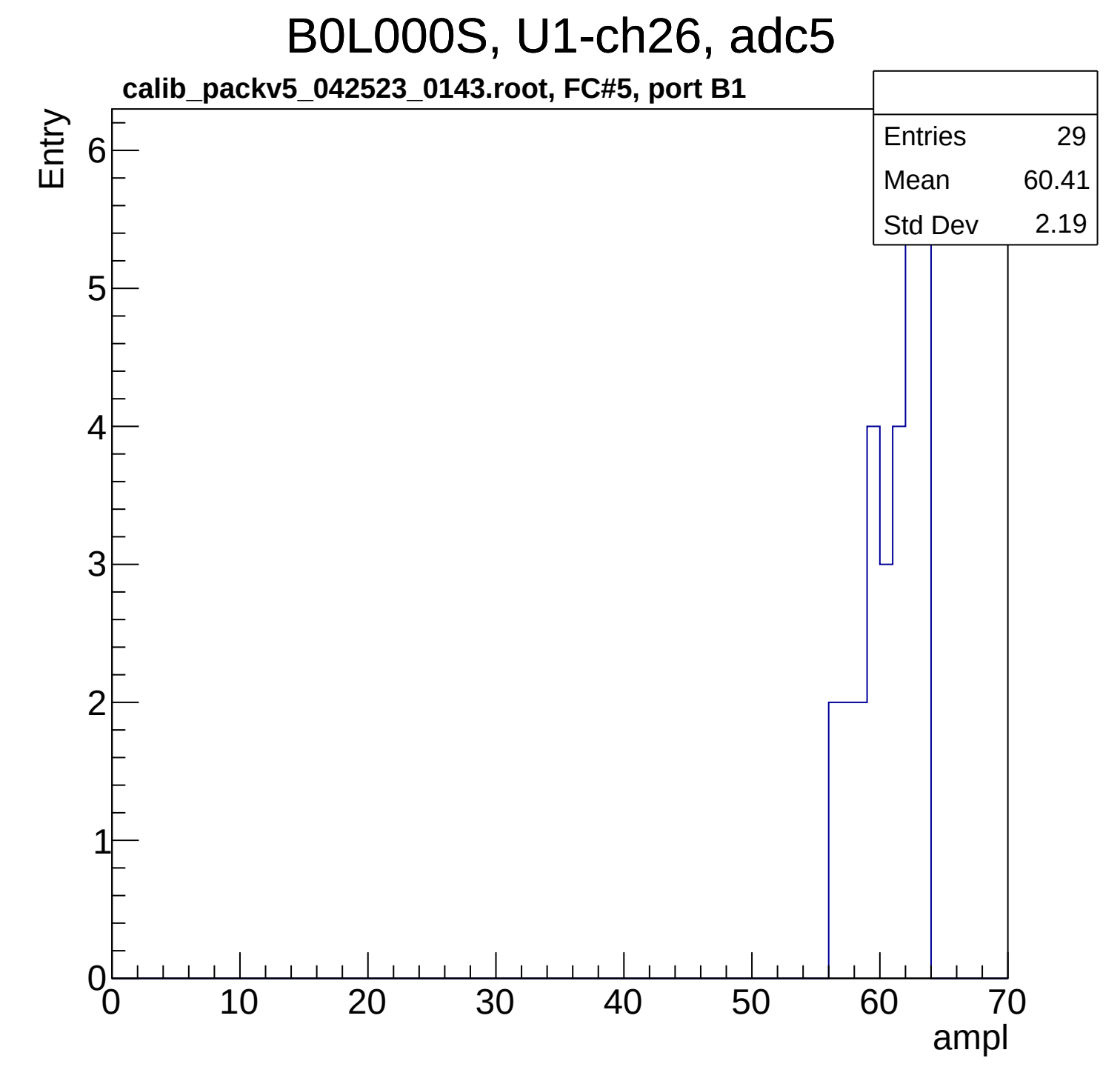
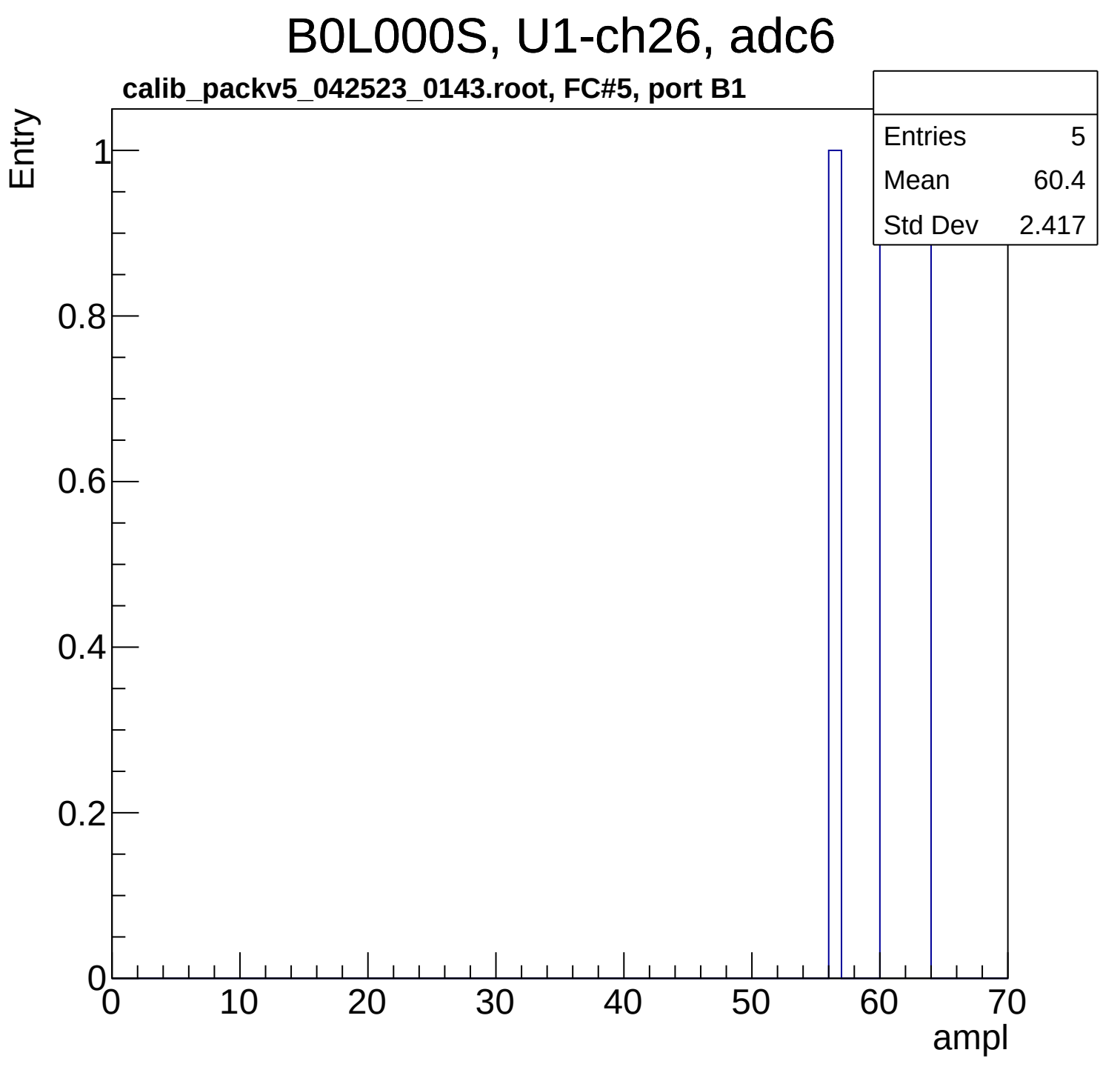

B0L000S, U1-ch26, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 49.36
Std Dev 2.928
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch26, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 55.66
Std Dev 7.241
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch26, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 60.41
Std Dev 2.19
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch26, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 60.4
Std Dev 2.417
Entry
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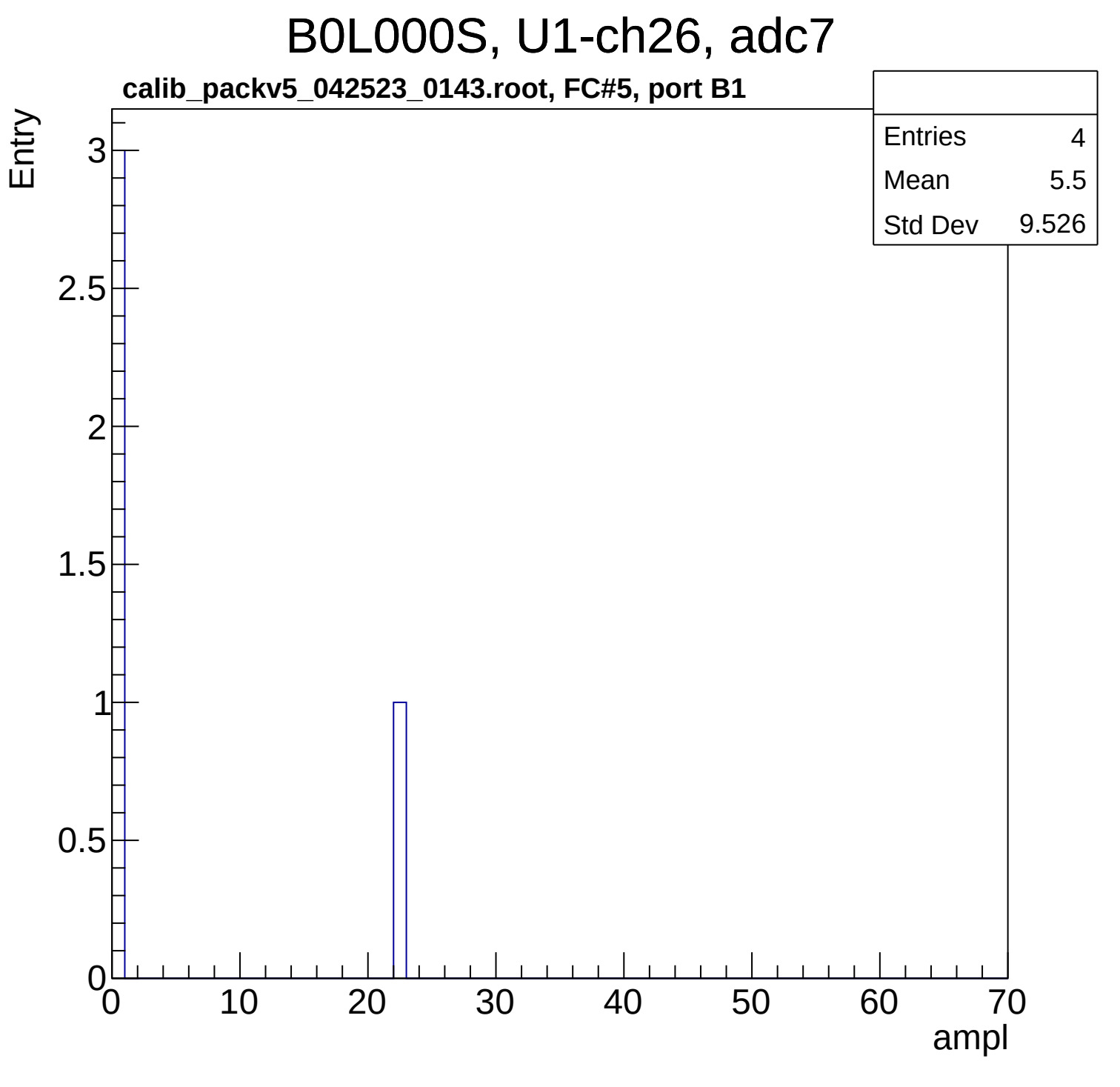
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch26, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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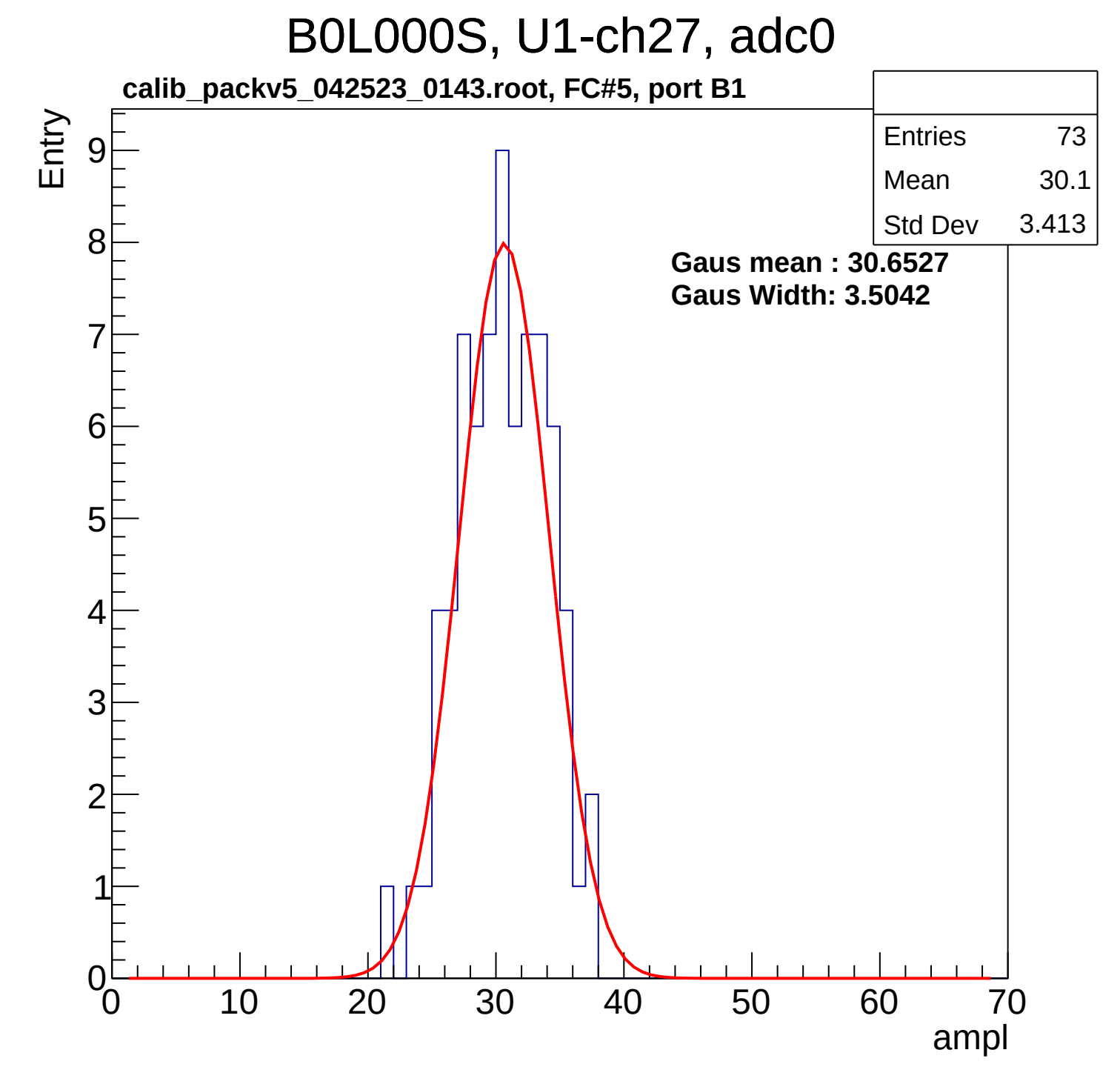
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch27, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.1
Std Dev 3.413
Gaus mean : 30.6527
Gaus Width: 3.5042
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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60
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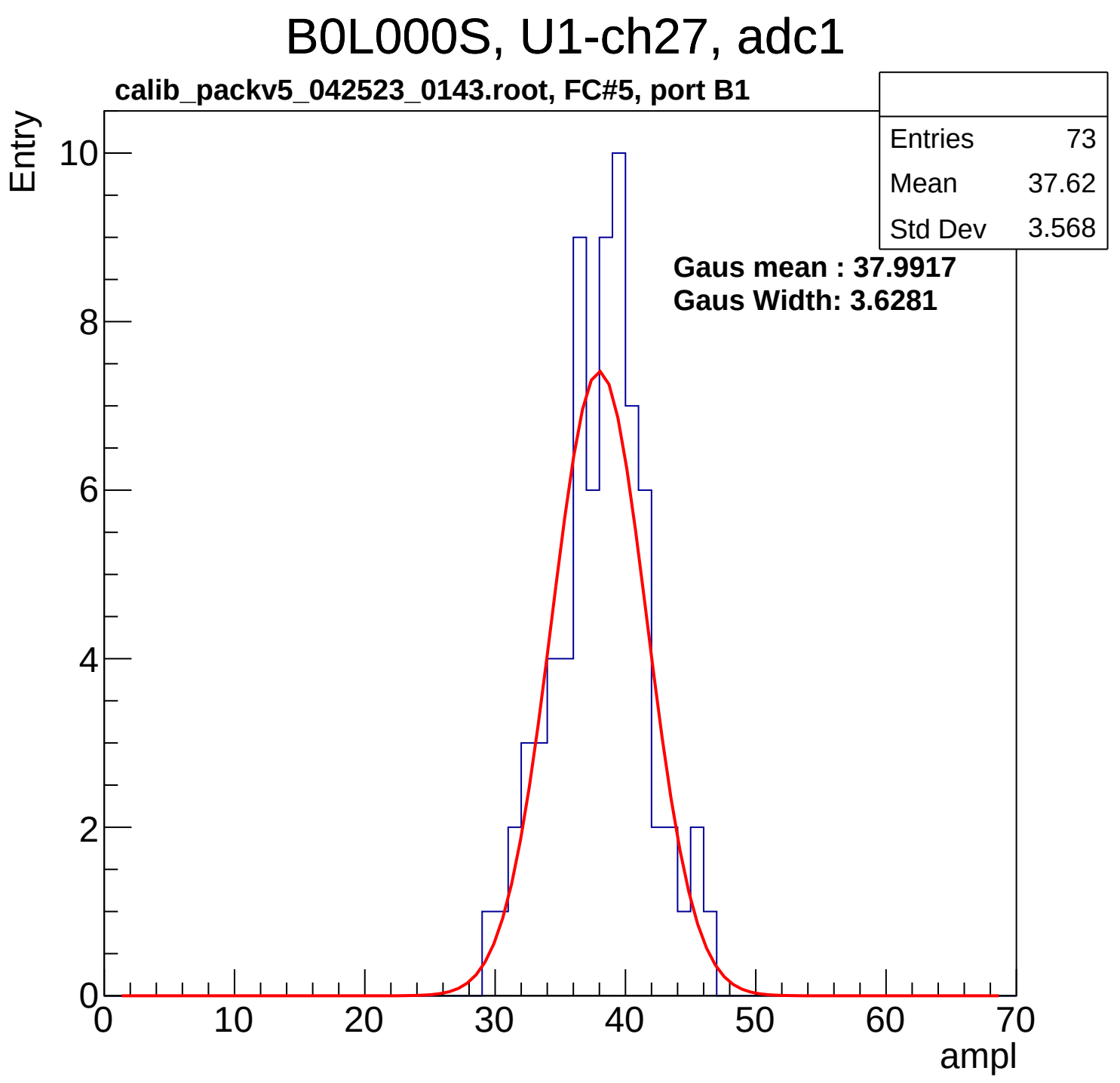
B0L000S, U1-ch27, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 37.62
Std Dev 3.568
Gaus mean : 37.9917
Gaus Width: 3.6281
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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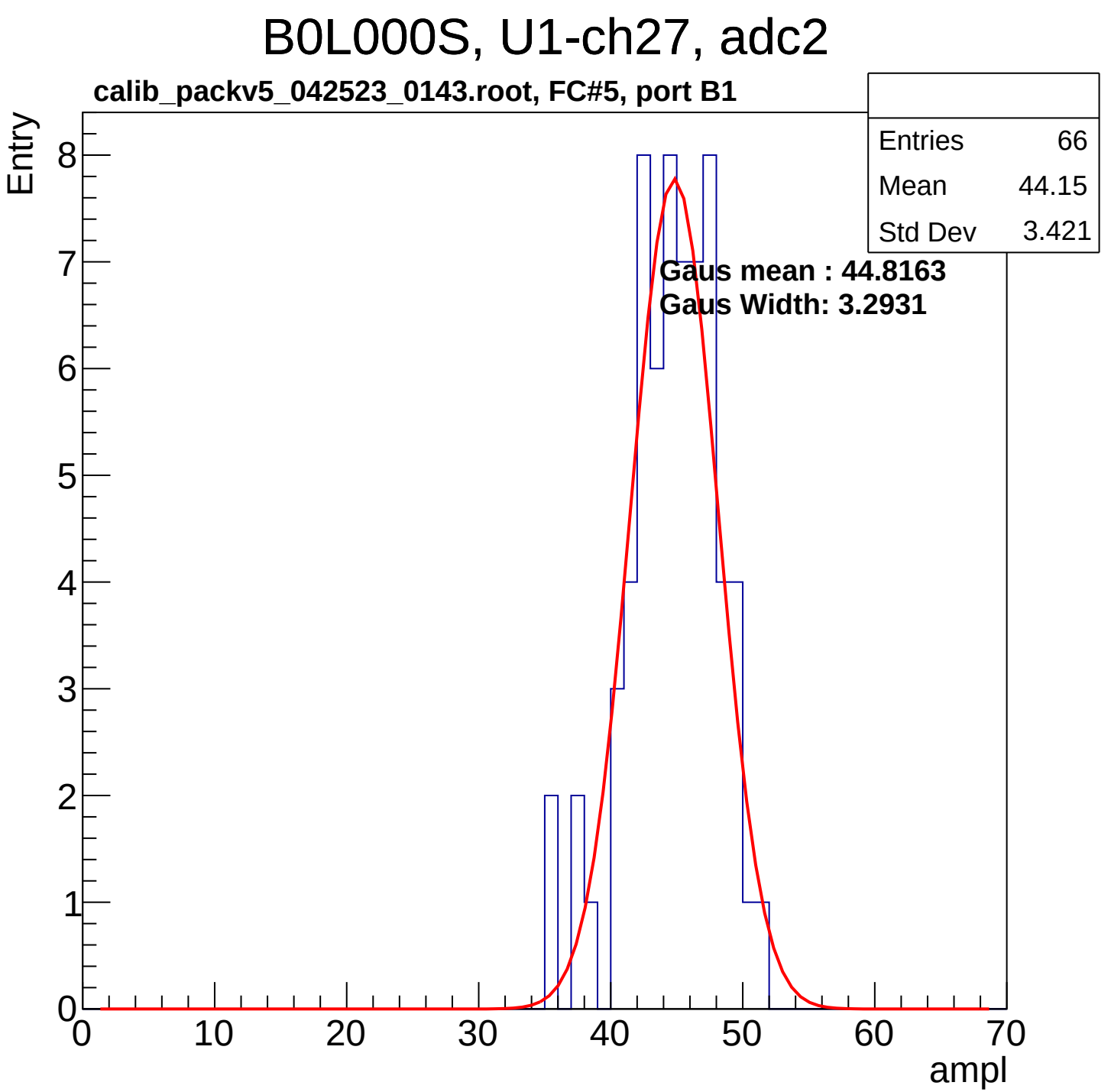
B0L000S, U1-ch27, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 44.15
Std Dev 3.421
Gaus mean : 44.8163
Gaus Width: 3.2931
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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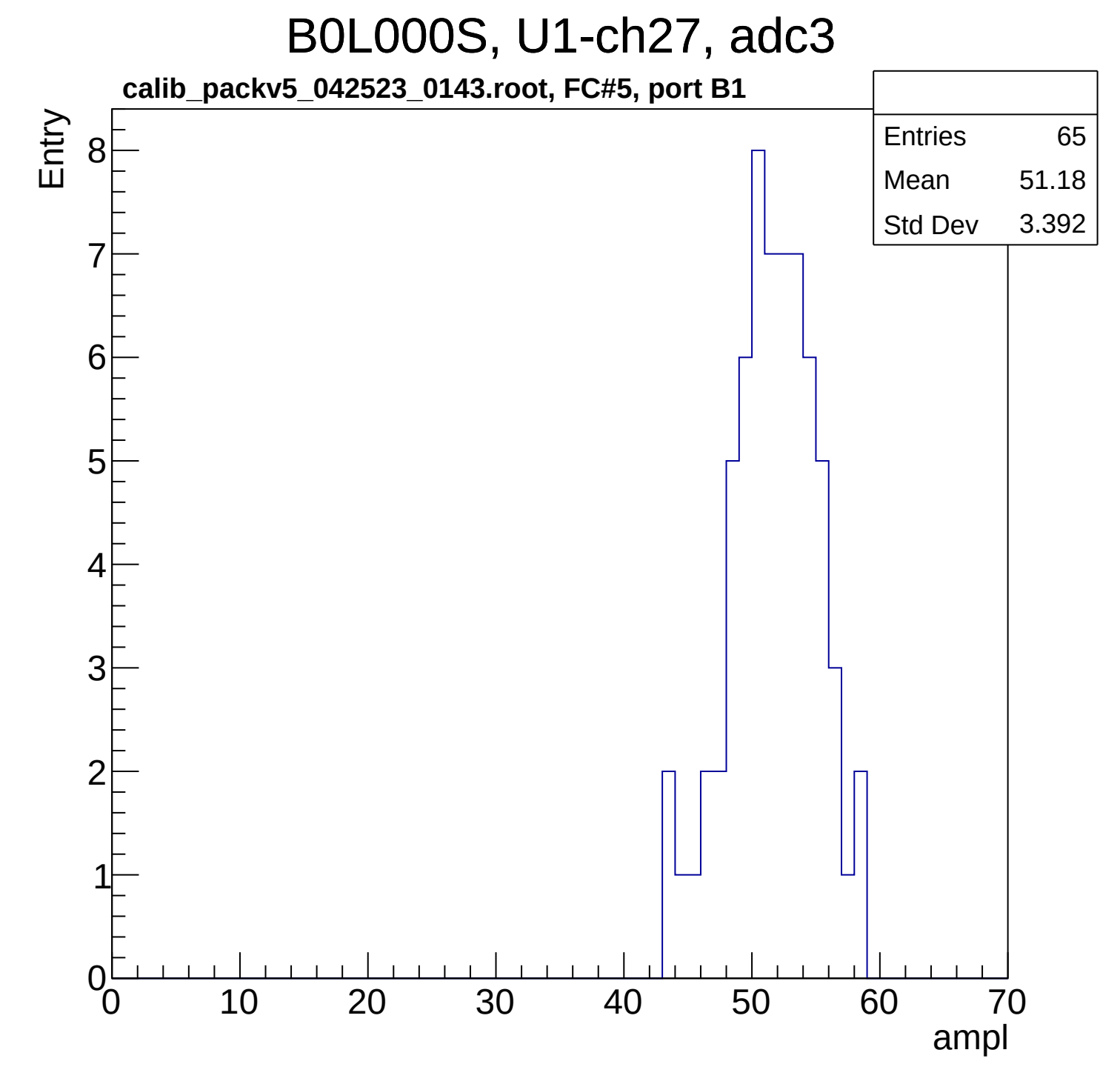
B0L000S, U1-ch27, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 51.18
Std Dev 3.392
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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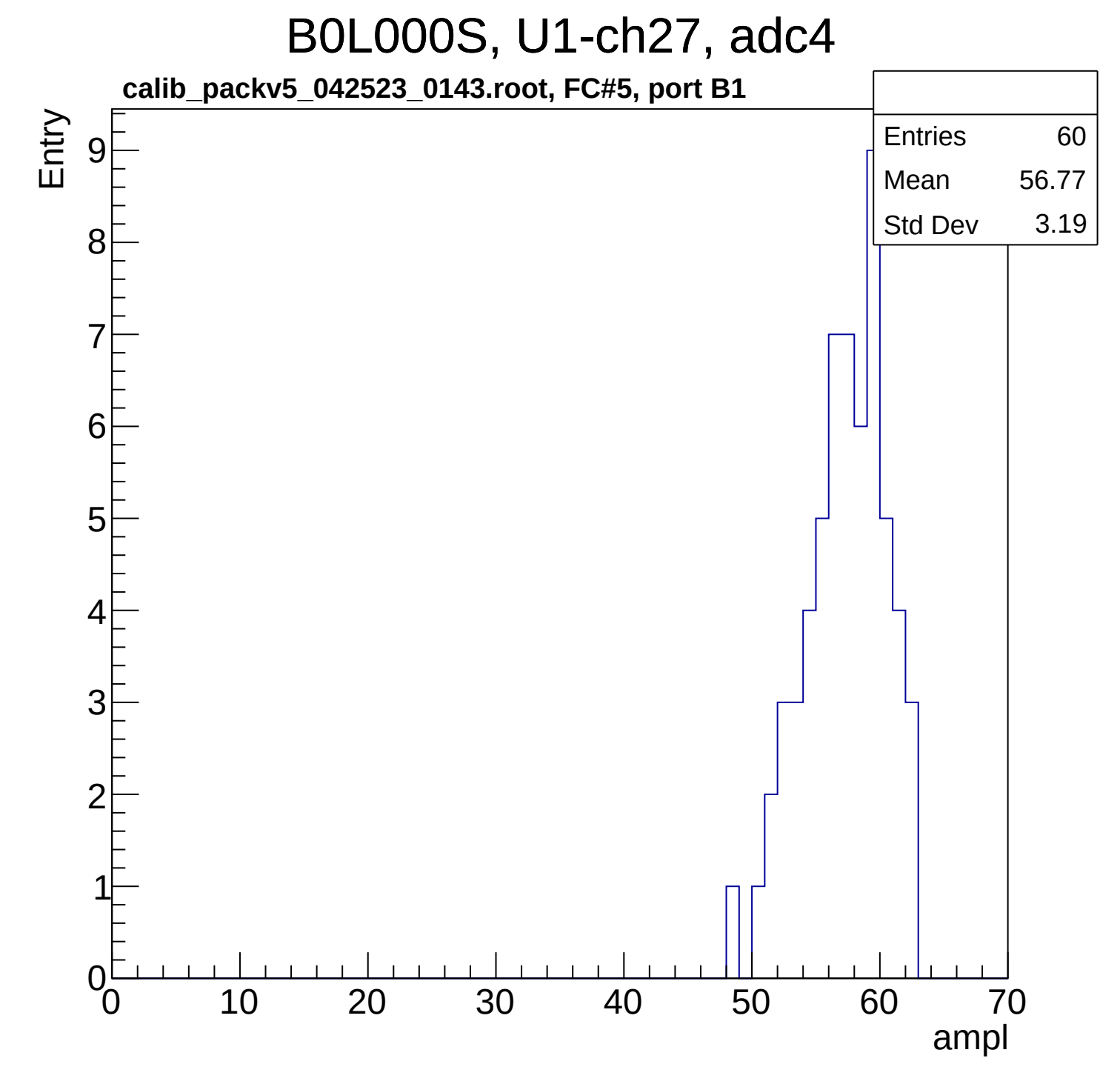
B0L000S, U1-ch27, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.77
Std Dev 3.19
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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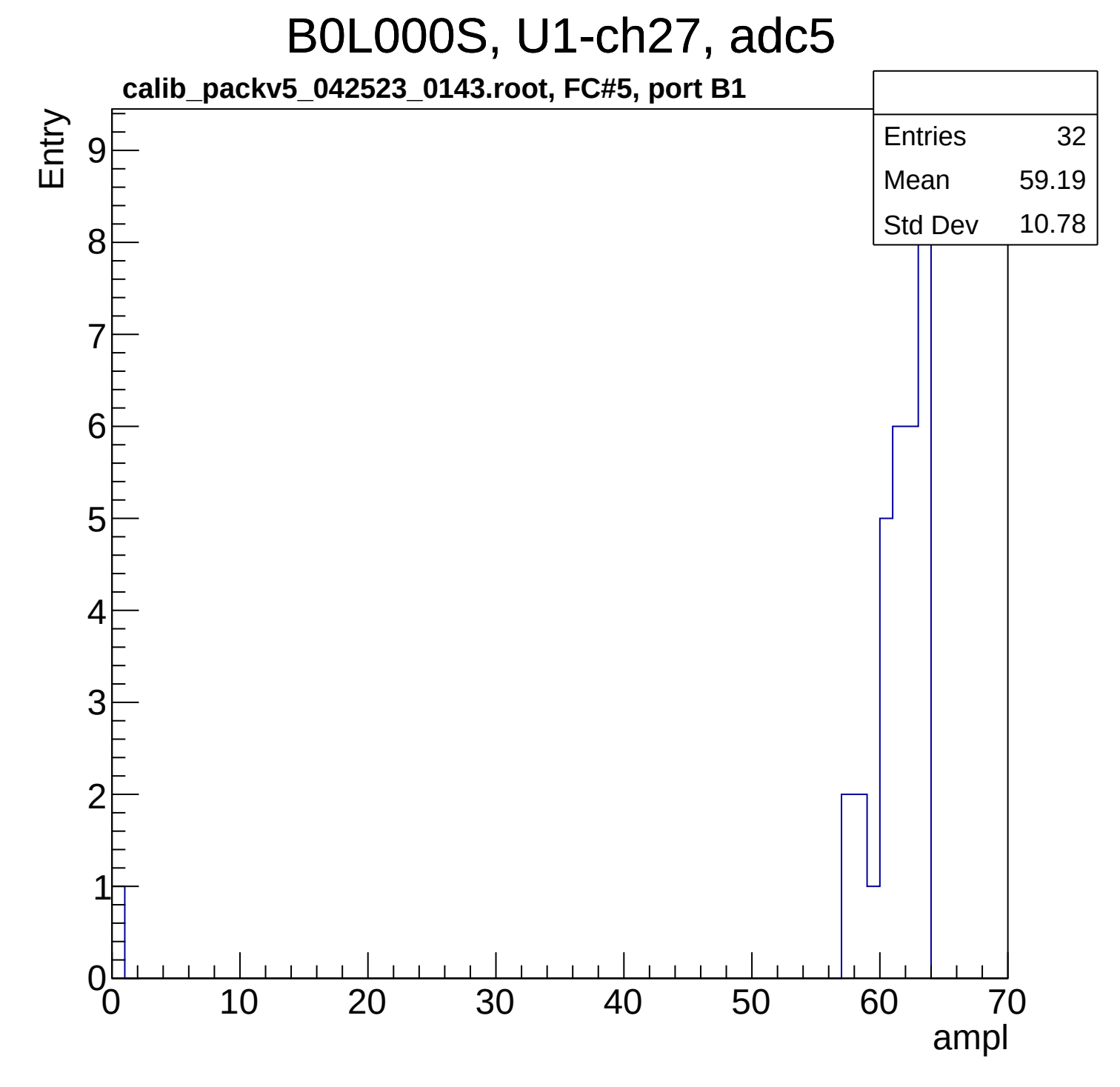
B0L000S, U1-ch27, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 59.19
Std Dev 10.78
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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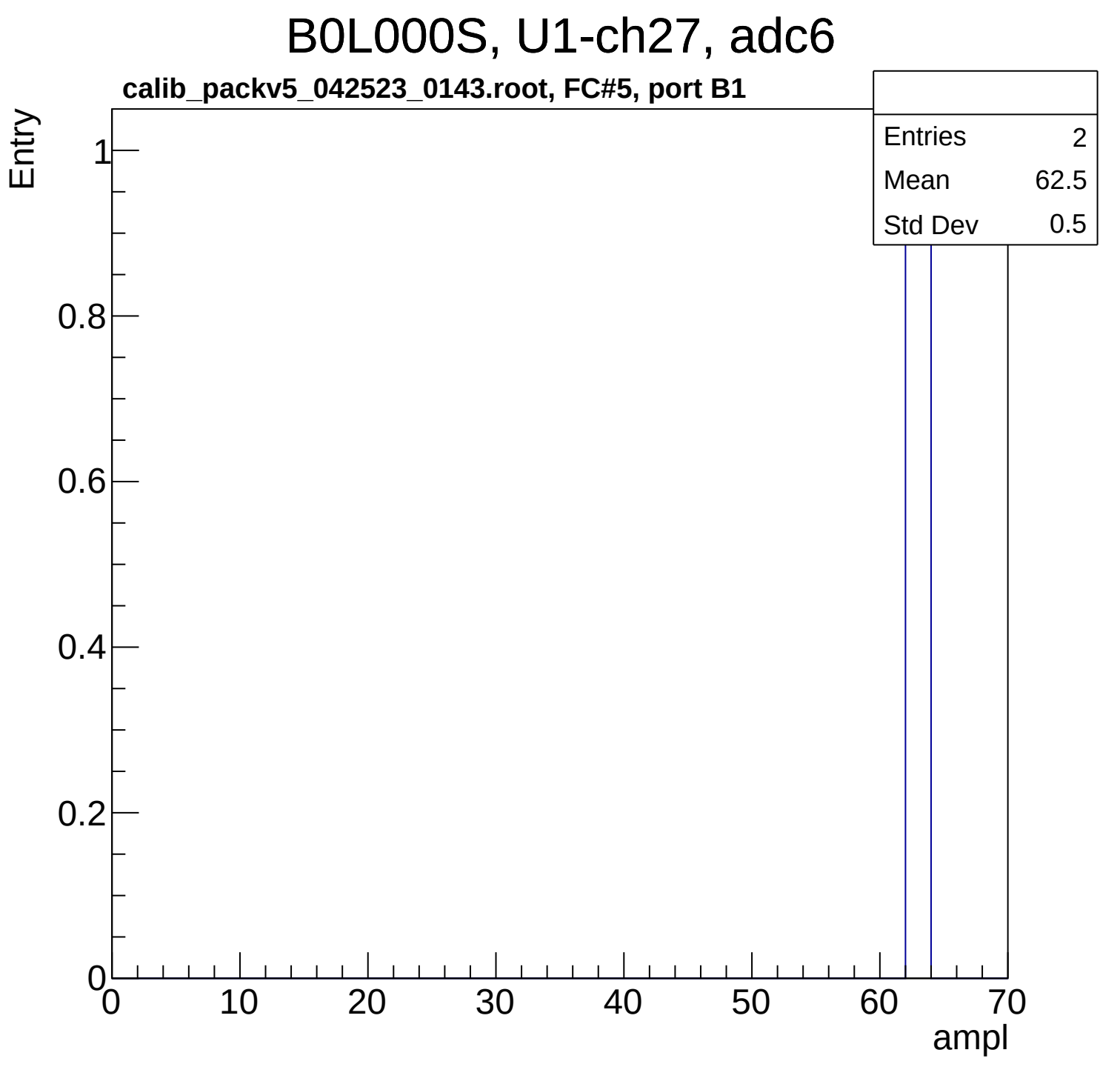
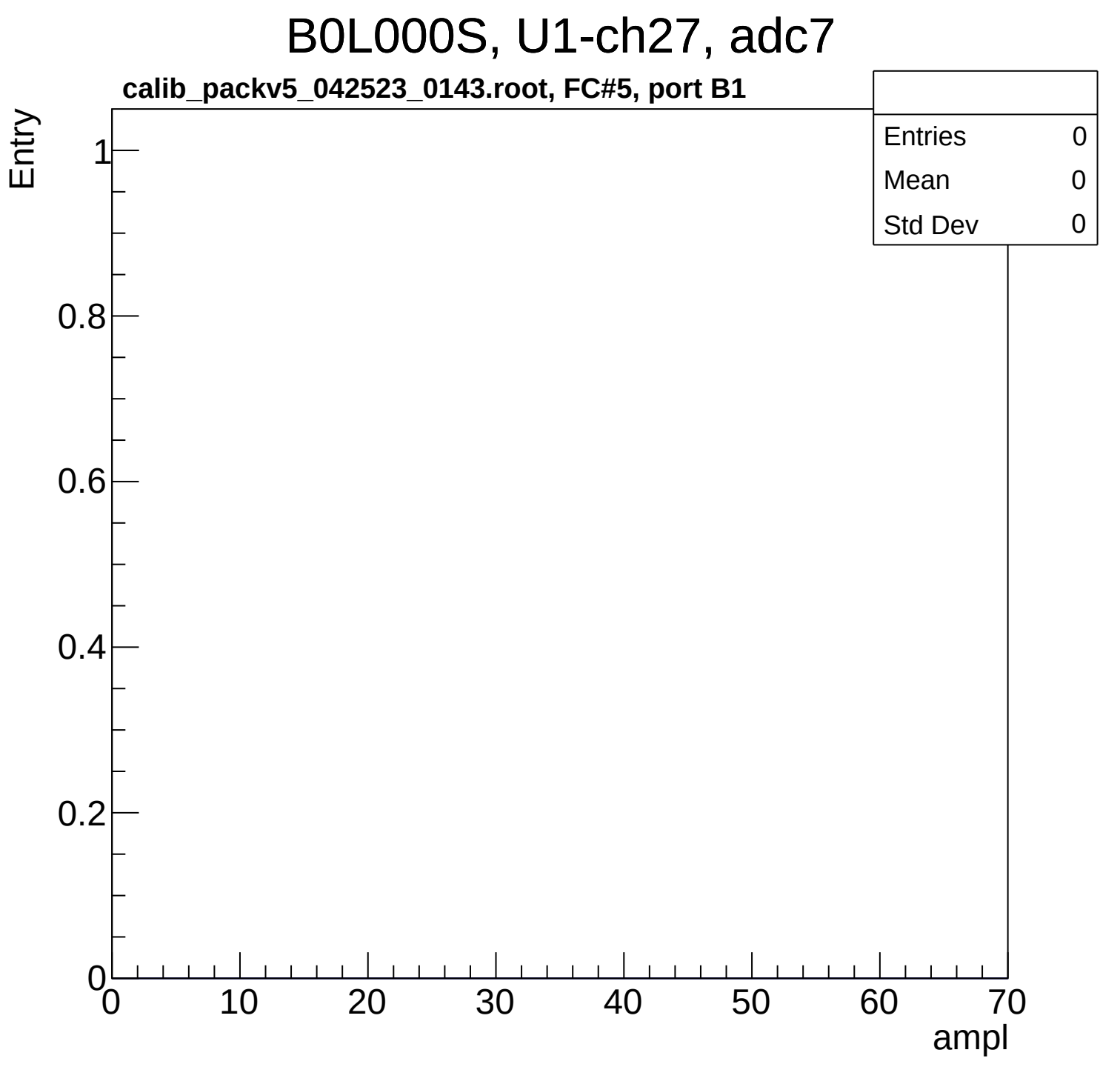
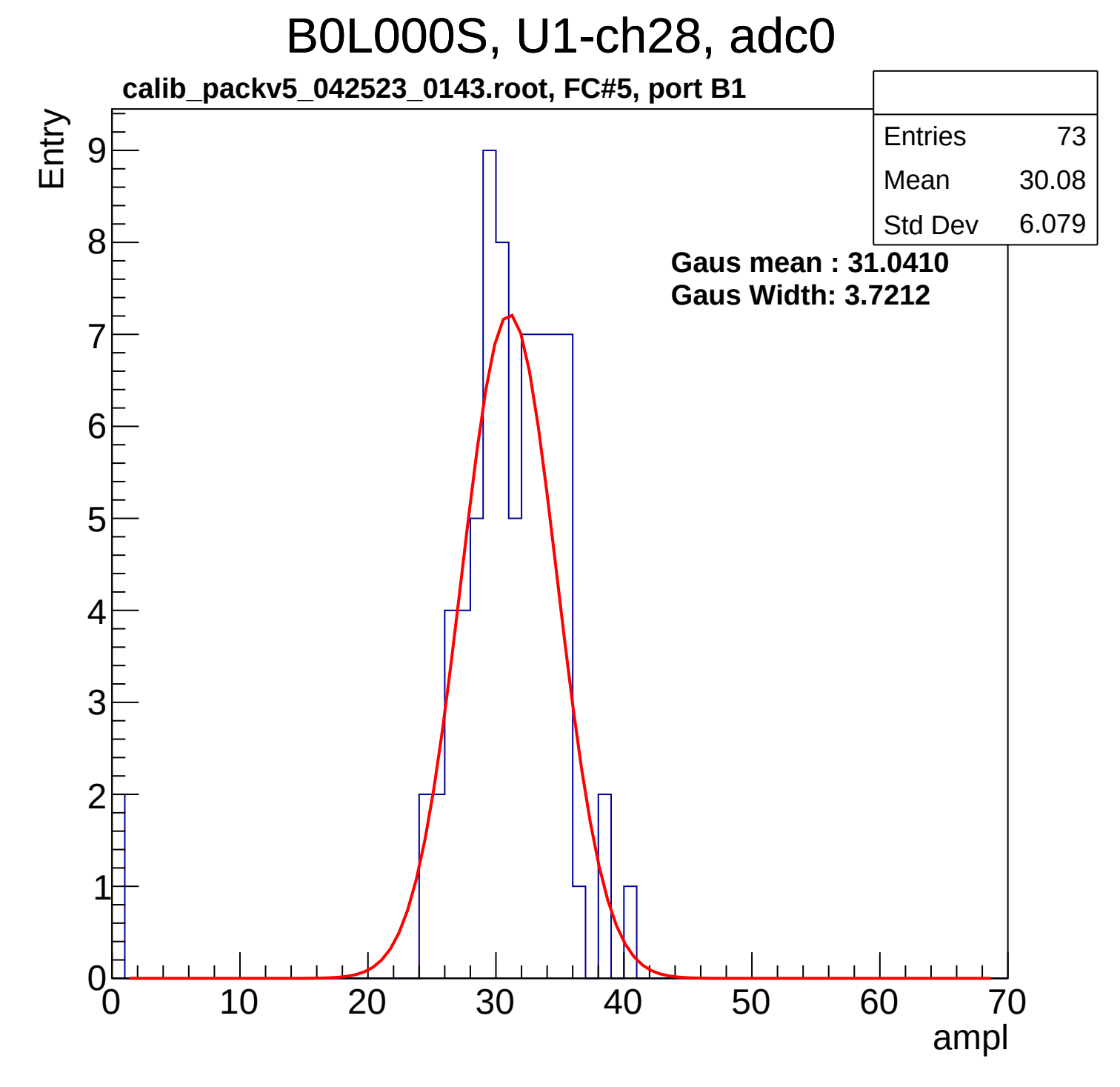

B0L000S, U1-ch27, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch27, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch28, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.08
Std Dev 6.079
Gaus mean : 31.0410
Gaus Width: 3.7212
Entry
ampl
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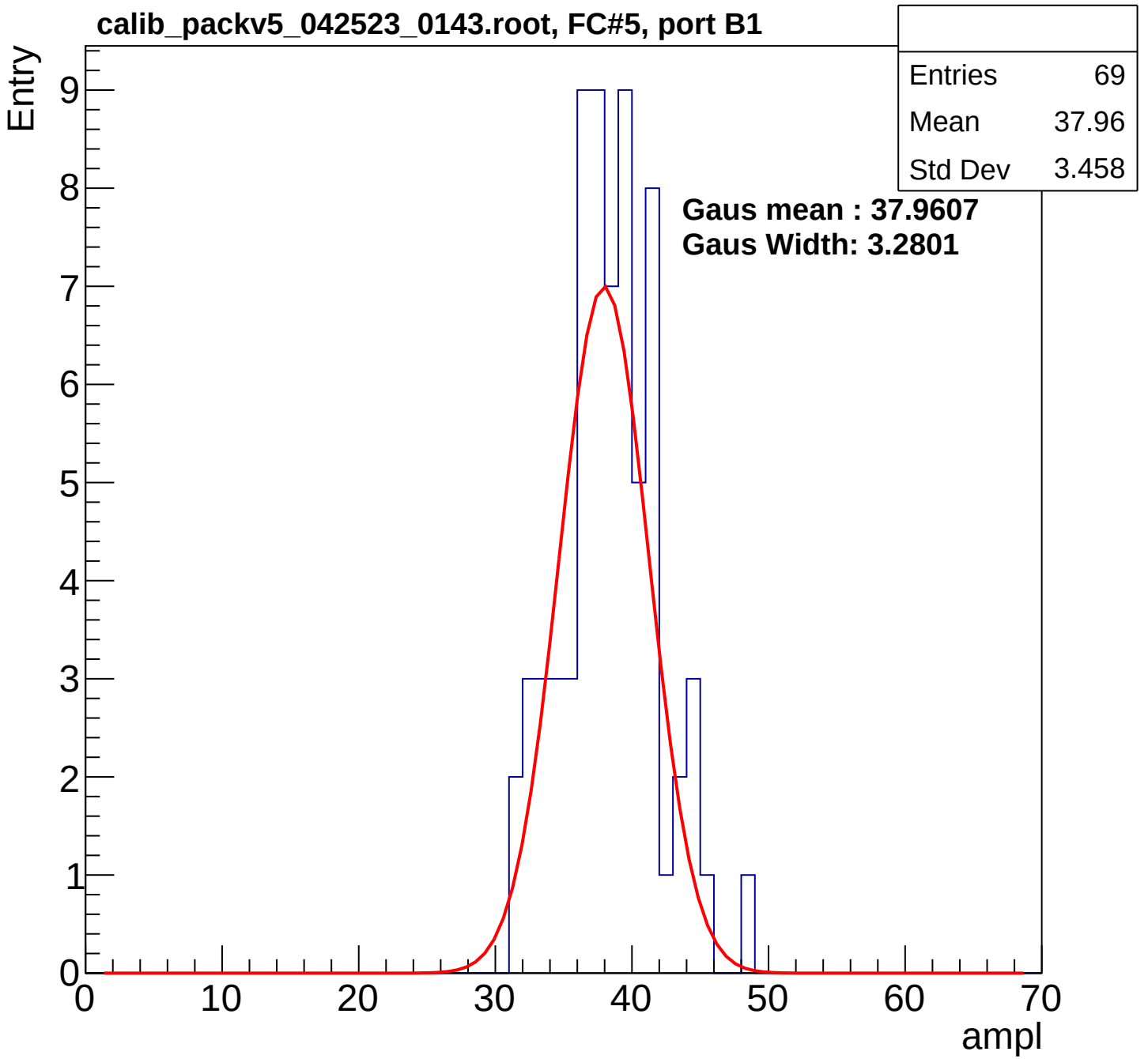
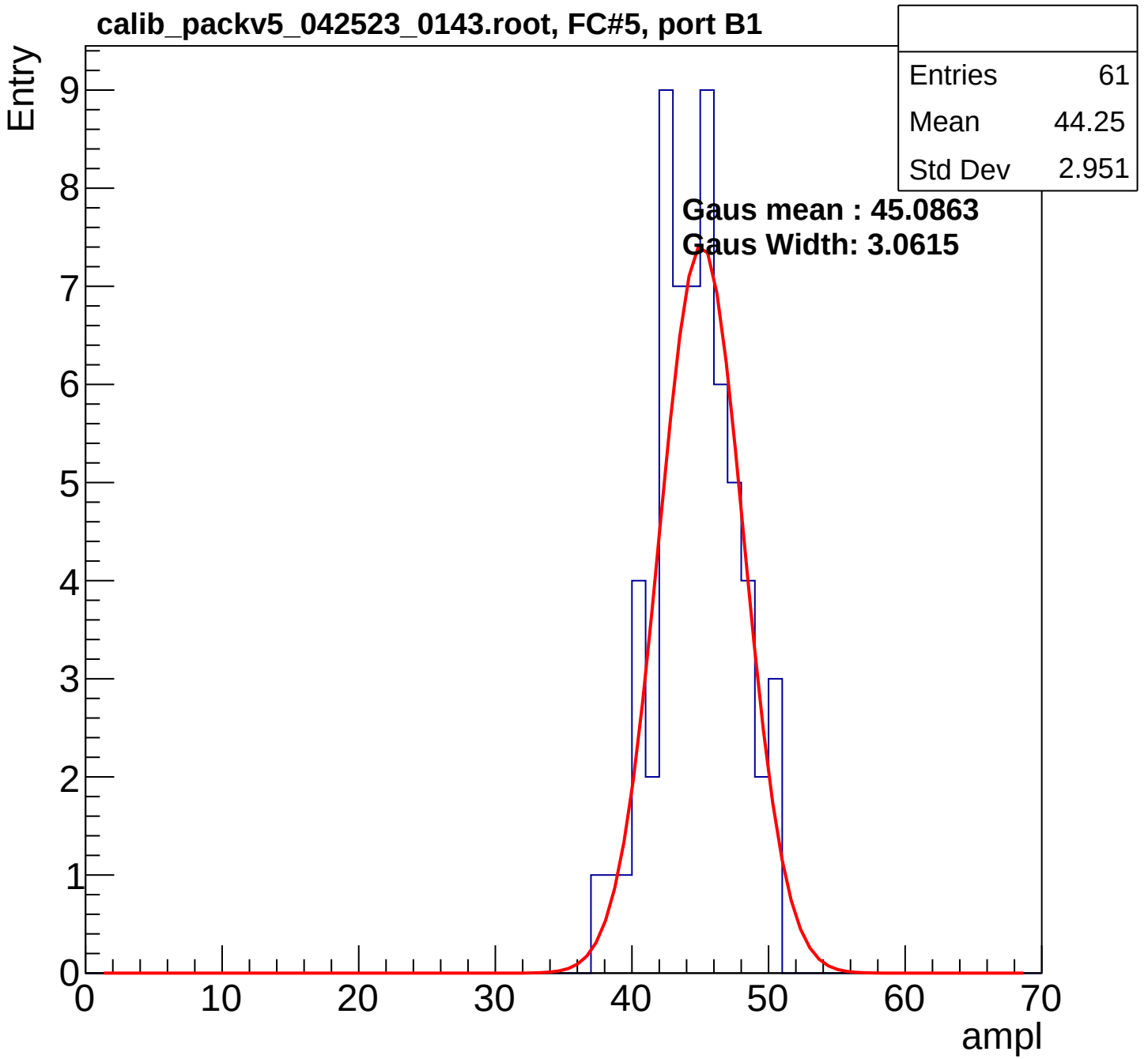
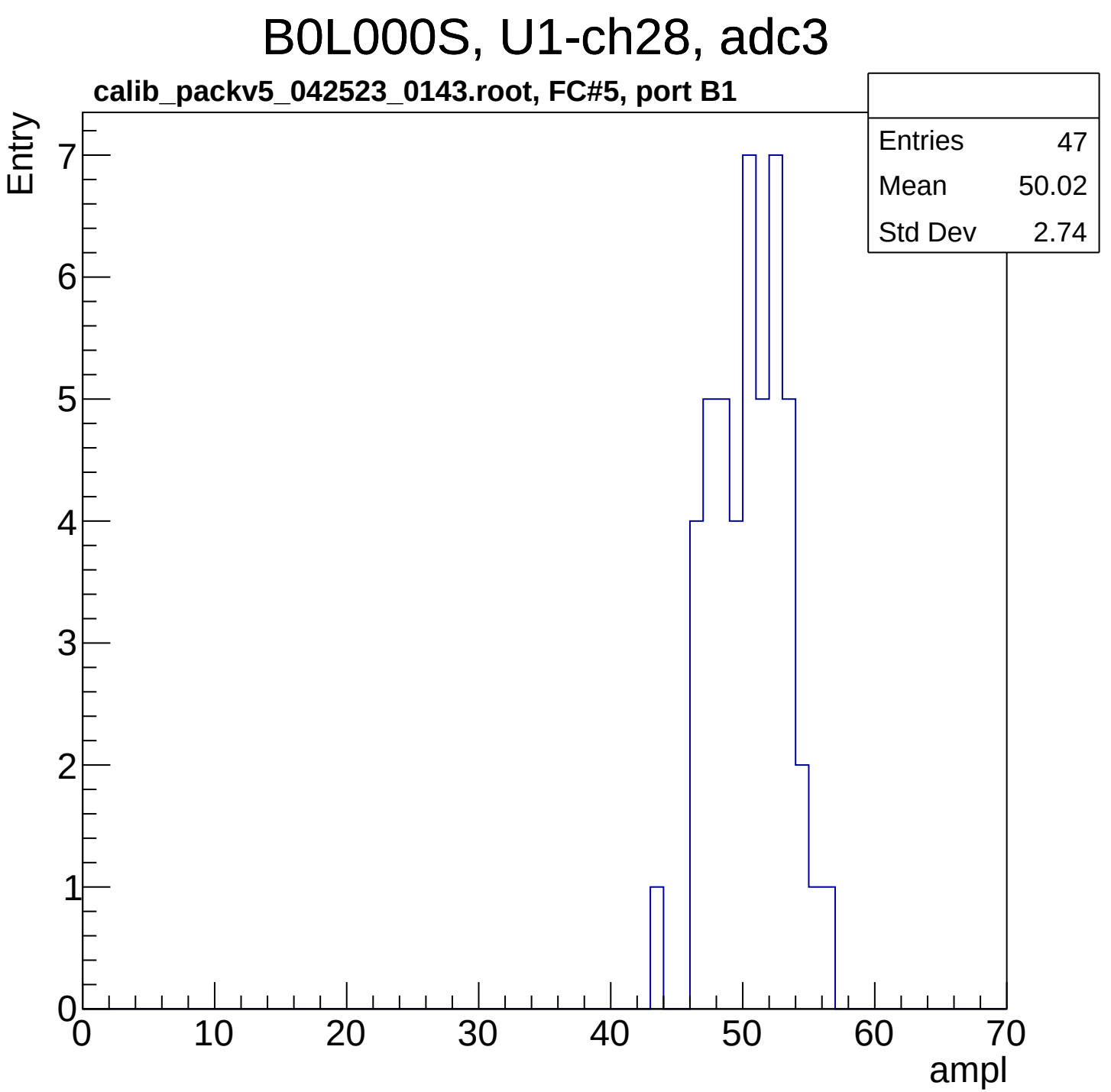
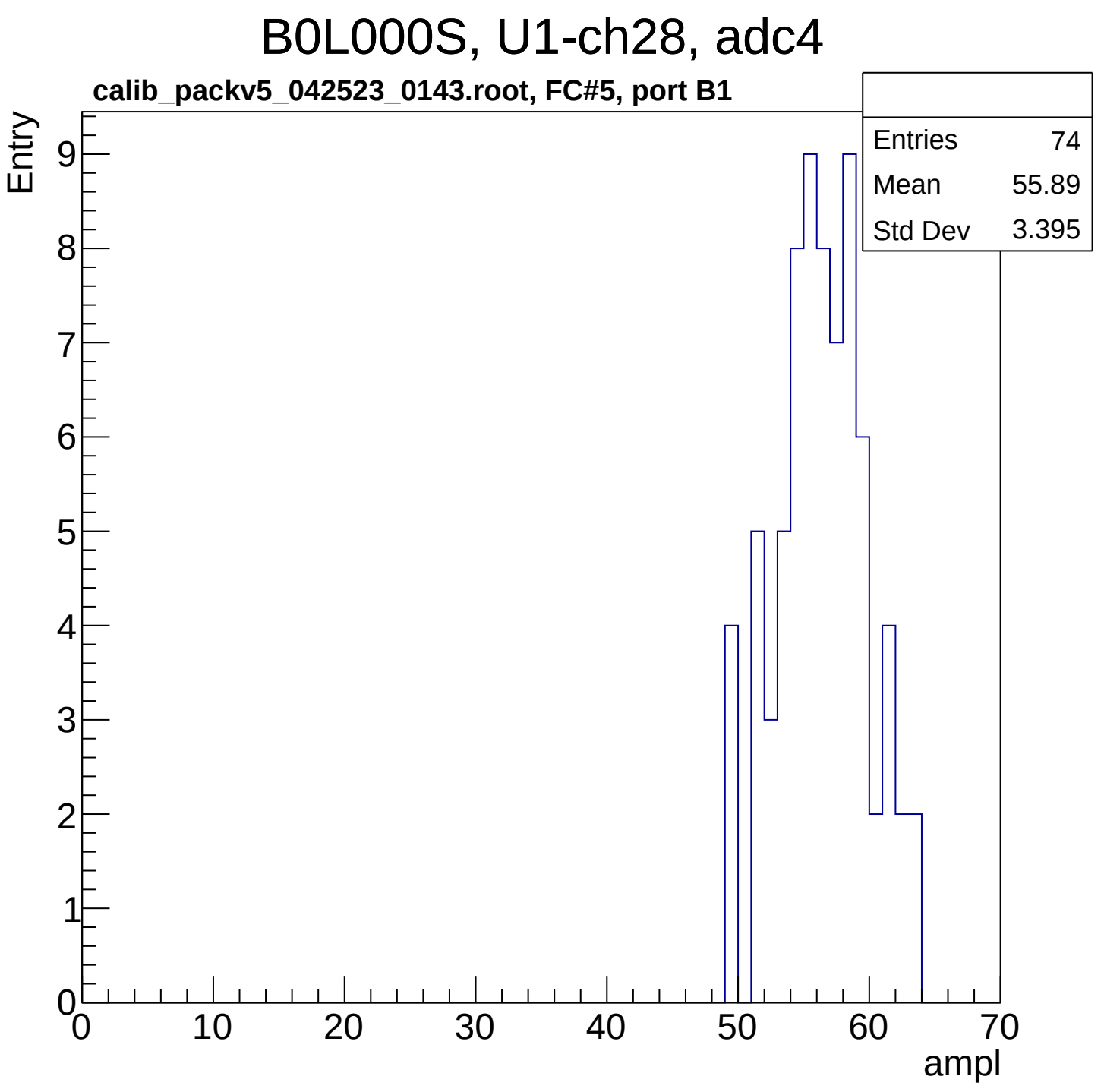
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch28, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 37.96
Std Dev 3.458
Gaus mean : 37.9607
Gaus Width: 3.2801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch28, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 44.25
Std Dev 2.951
Gaus mean : 45.0863
Gaus Width: 3.0615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch28, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 50.02
Std Dev 2.74
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch28, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 55.89
Std Dev 3.395
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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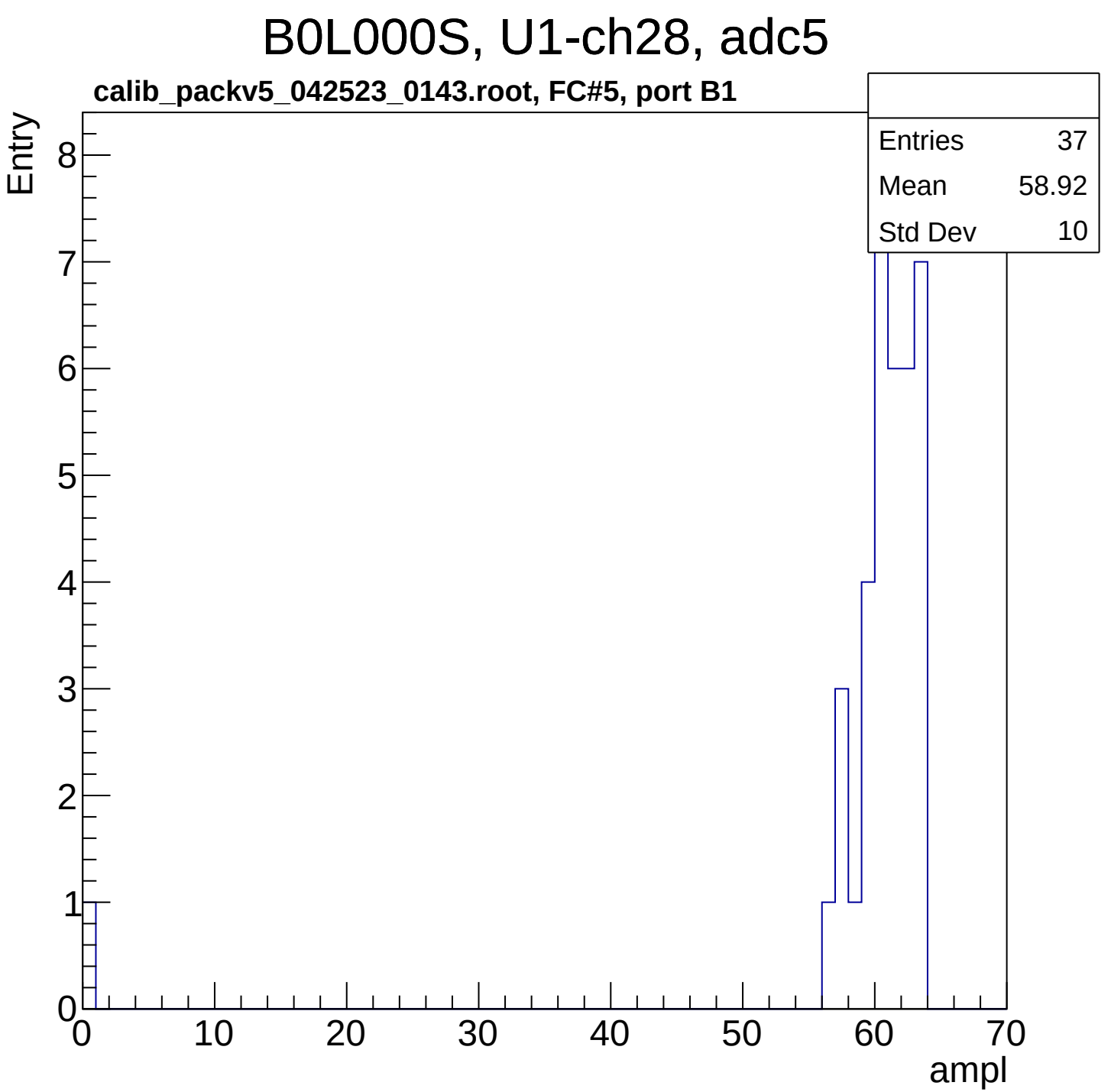
B0L000S, U1-ch28, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.92
Std Dev 10
Entry
ampl
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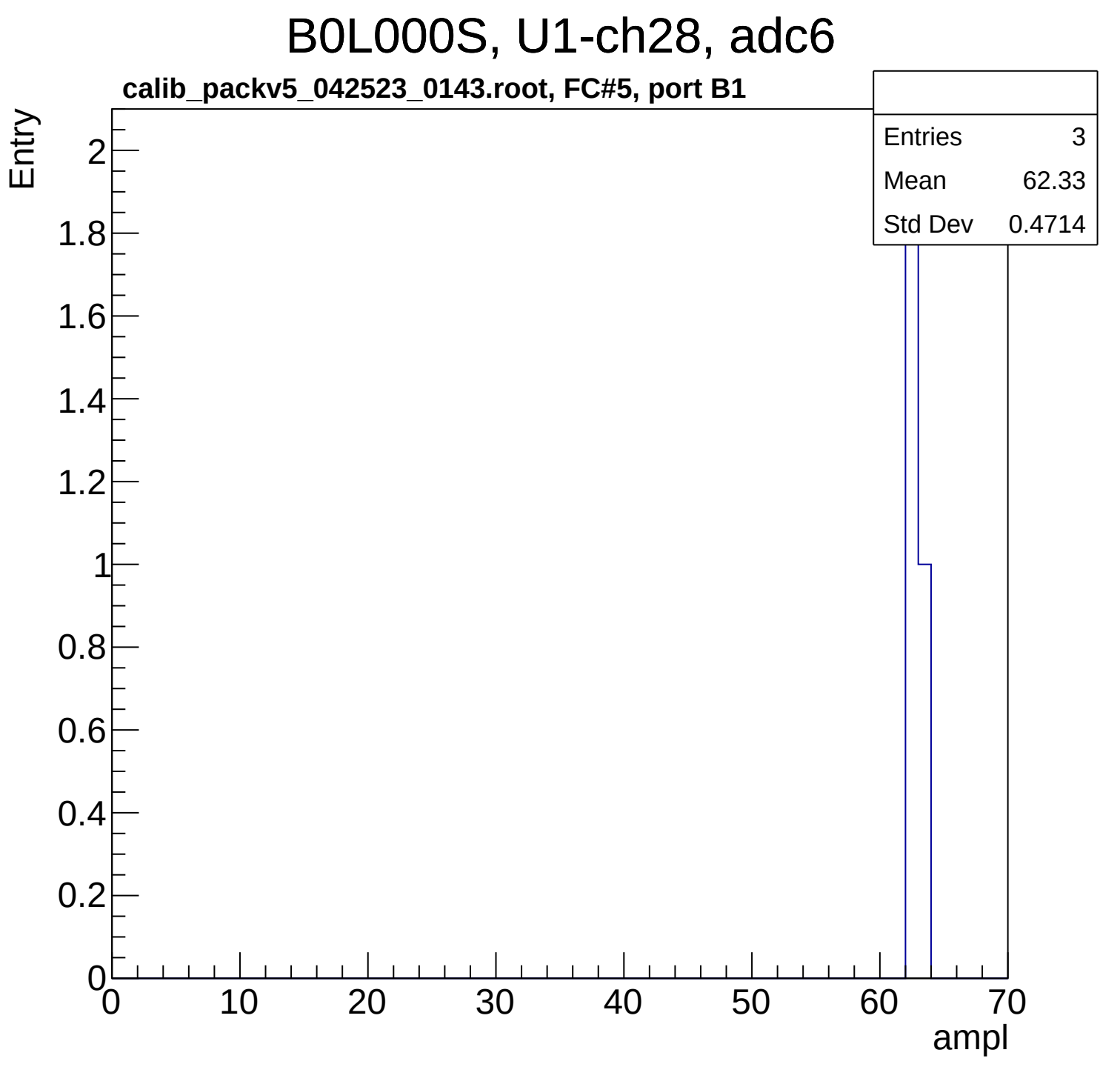
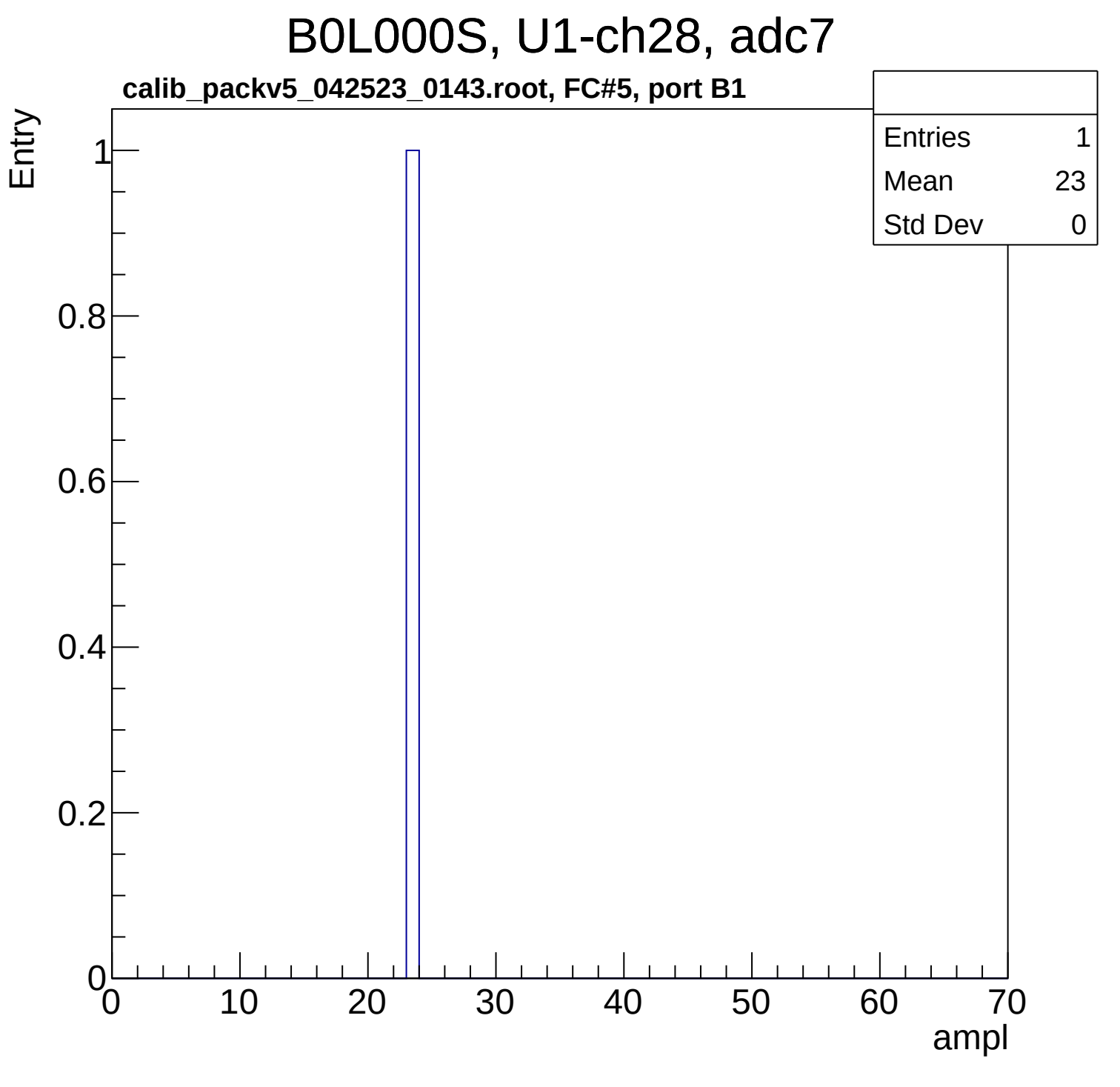
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch28, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch28, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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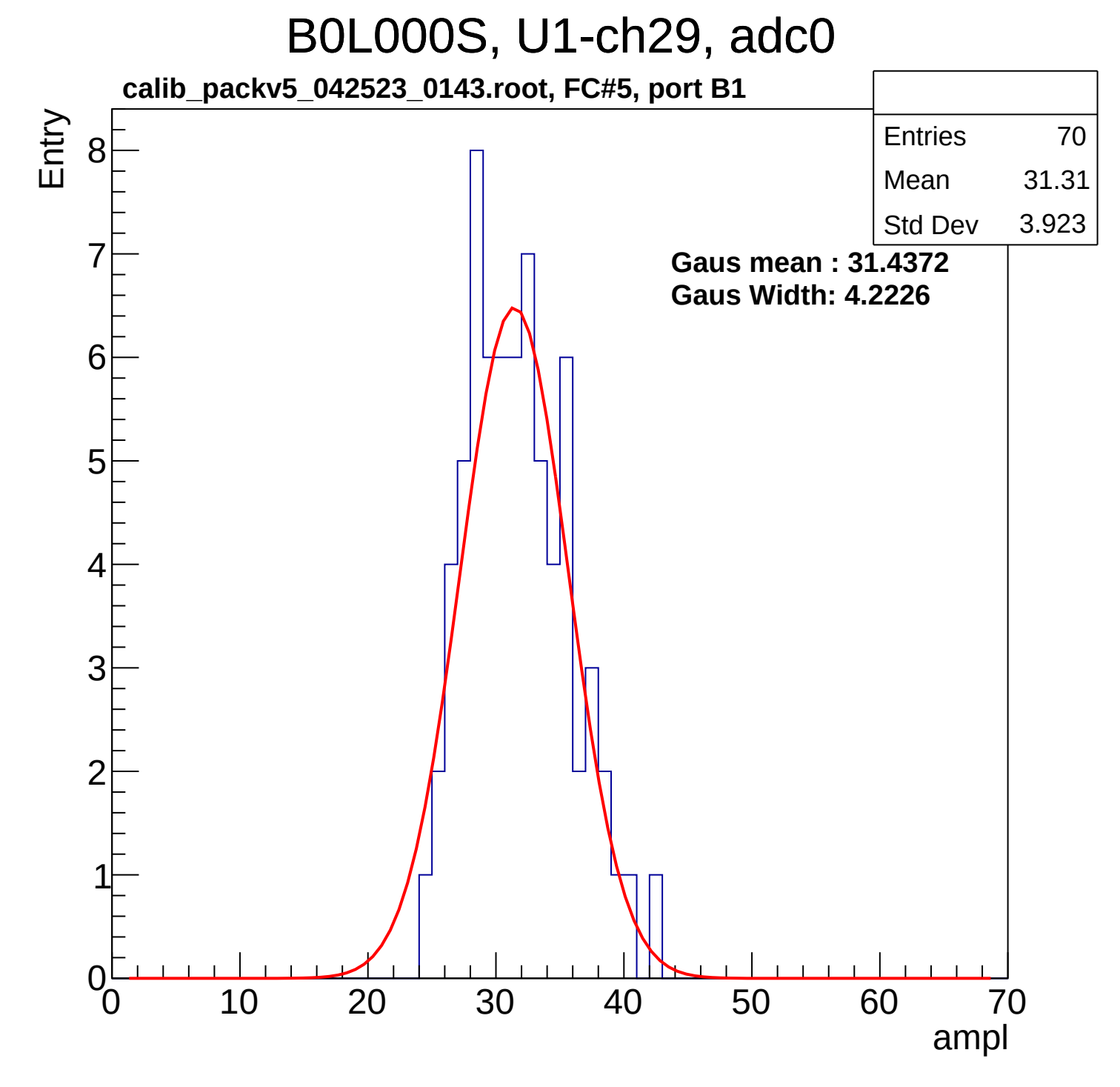
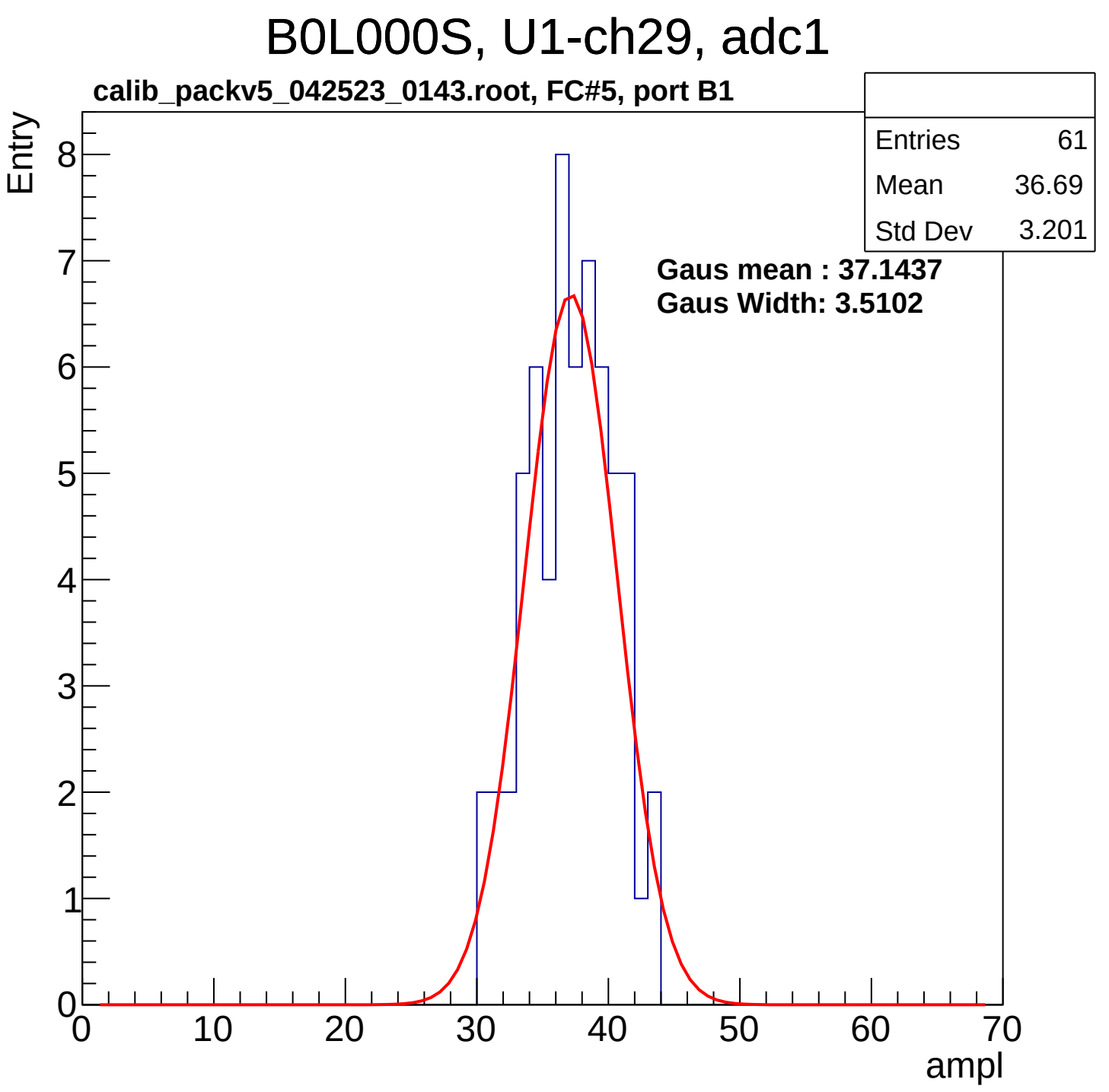
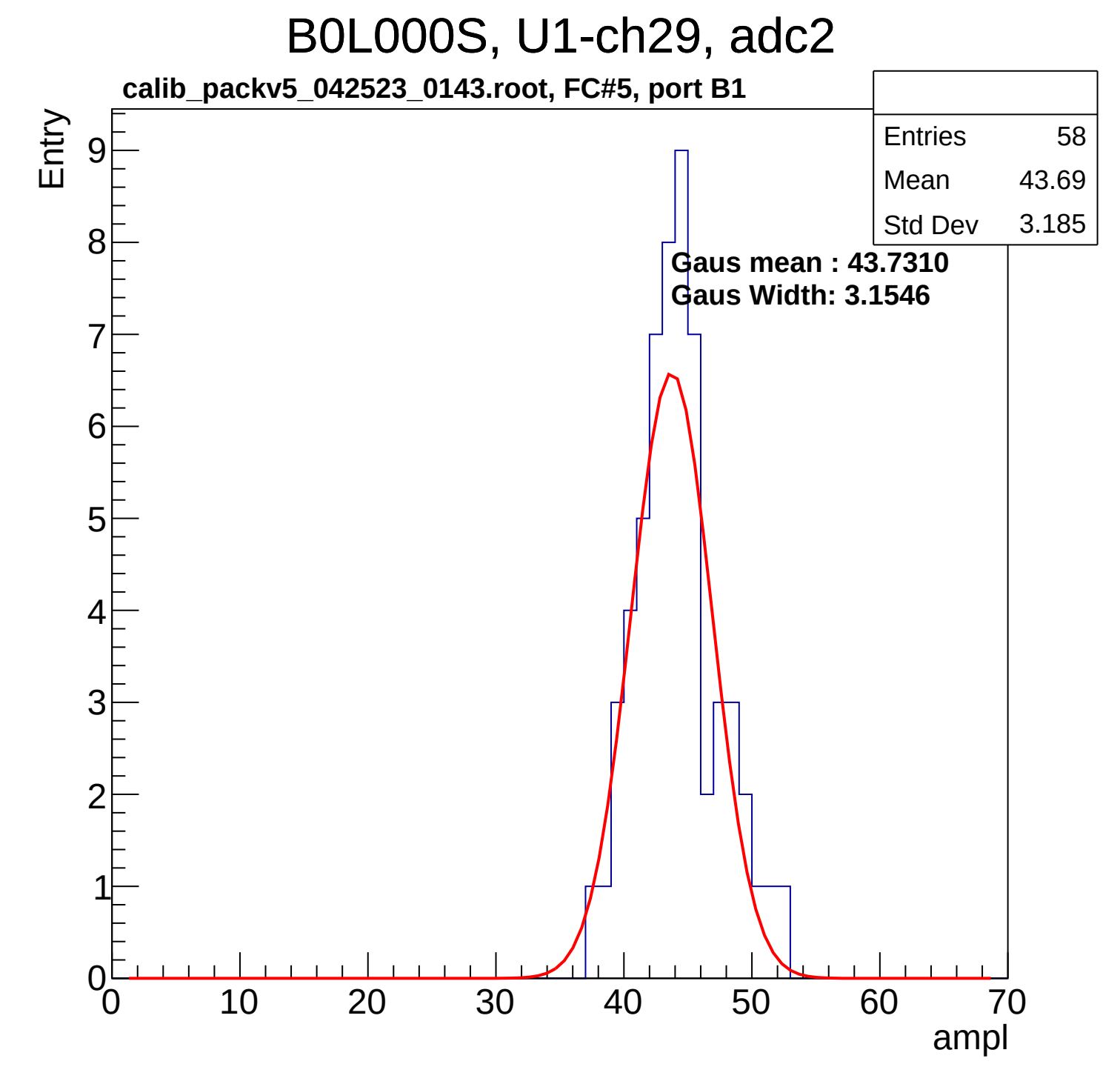
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch29, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 31.31
Std Dev 3.923
Gaus mean : 31.4372
Gaus Width: 4.2226
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch29, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.69
Std Dev 3.201
Gaus mean : 37.1437
Gaus Width: 3.5102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch29, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.69
Std Dev 3.185
Gaus mean : 43.7310
Gaus Width: 3.1546
Entry
ampl
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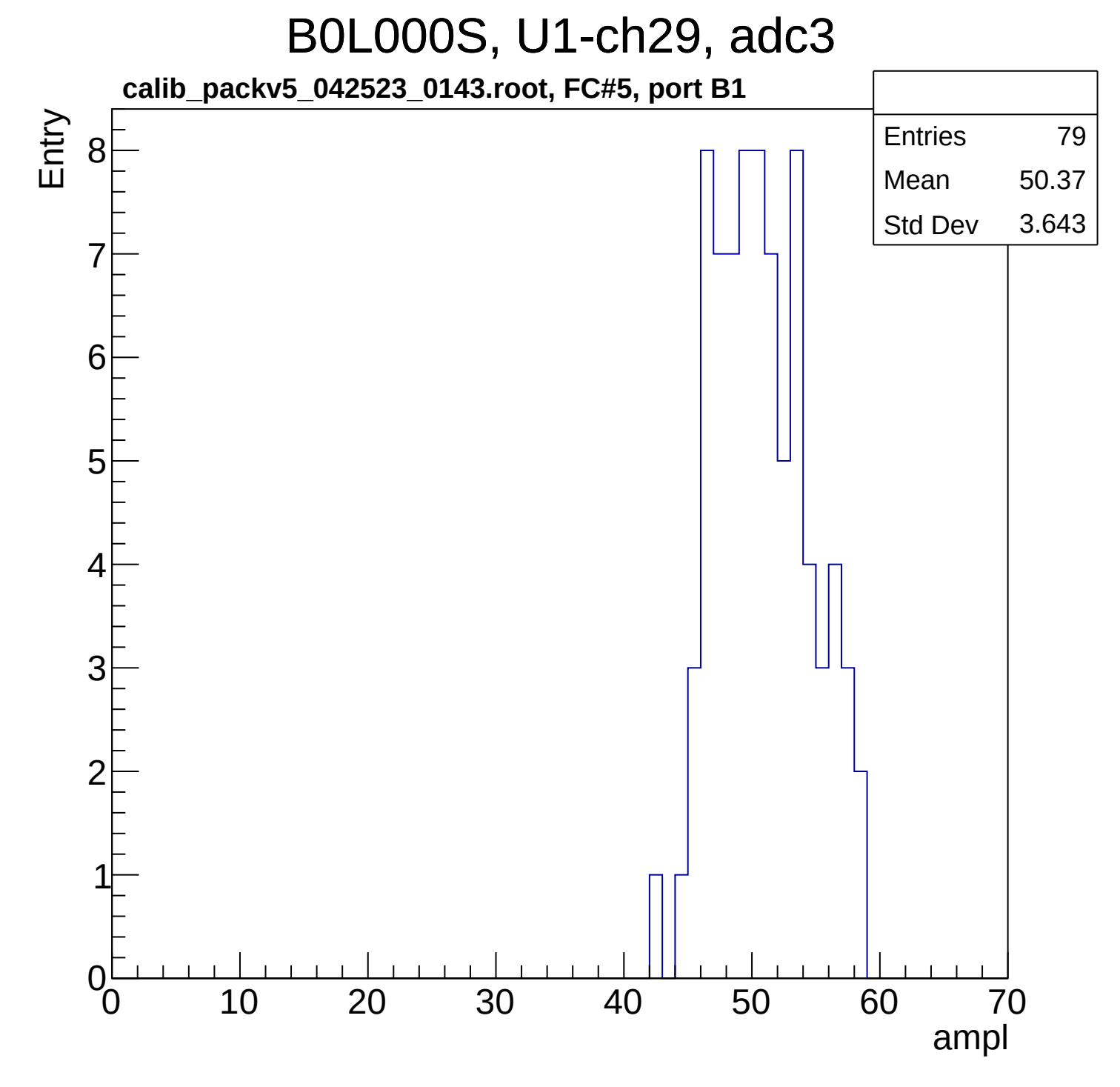
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch29, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 50.37
Std Dev 3.643
Entry
ampl
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2
3
4
5
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7
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0
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20
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40
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60
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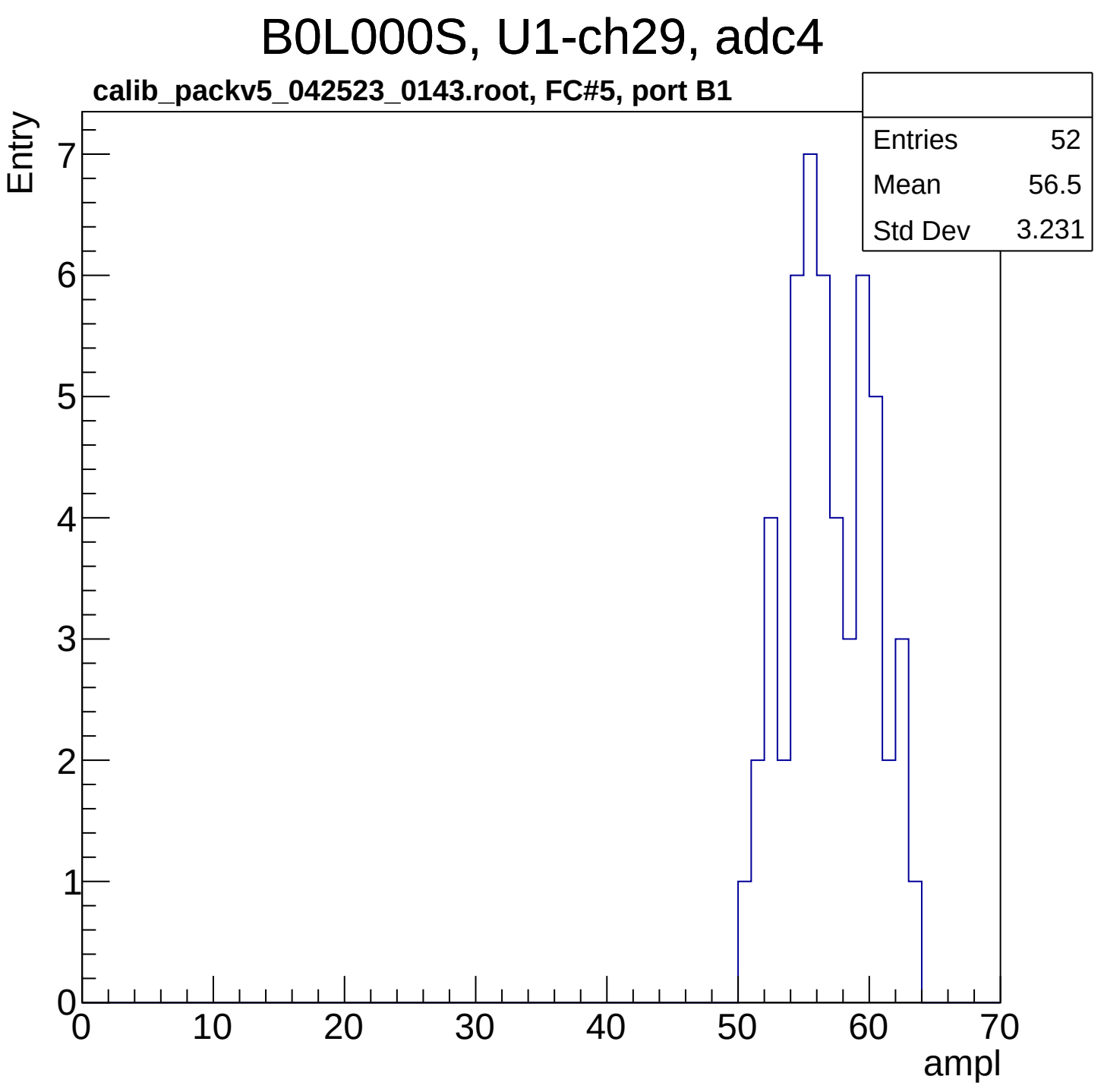
B0L000S, U1-ch29, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.5
Std Dev 3.231
Entry
ampl
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1
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3
4
5
6
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0
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40
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60
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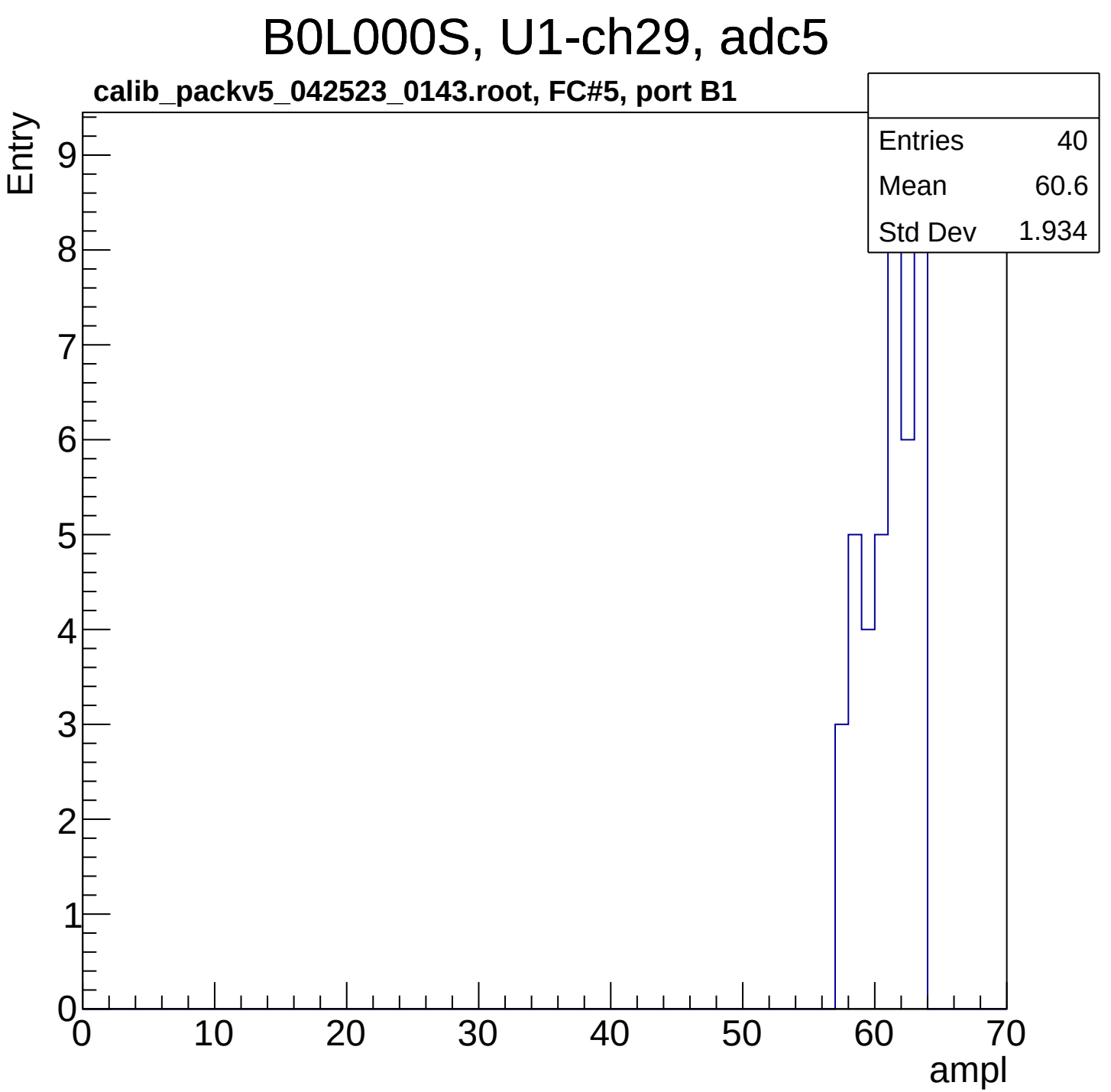
B0L000S, U1-ch29, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 60.6
Std Dev 1.934
Entry
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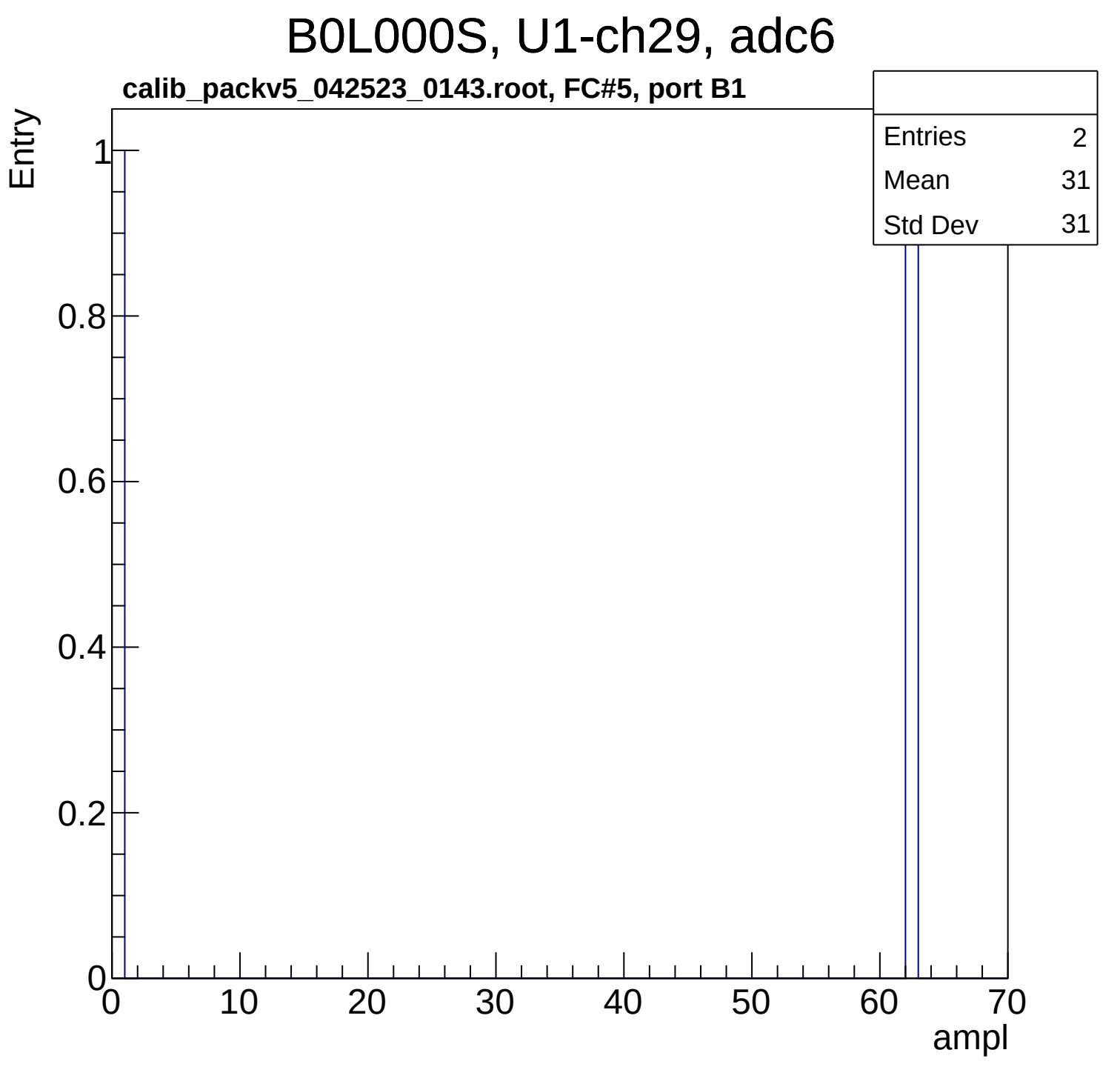
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch29, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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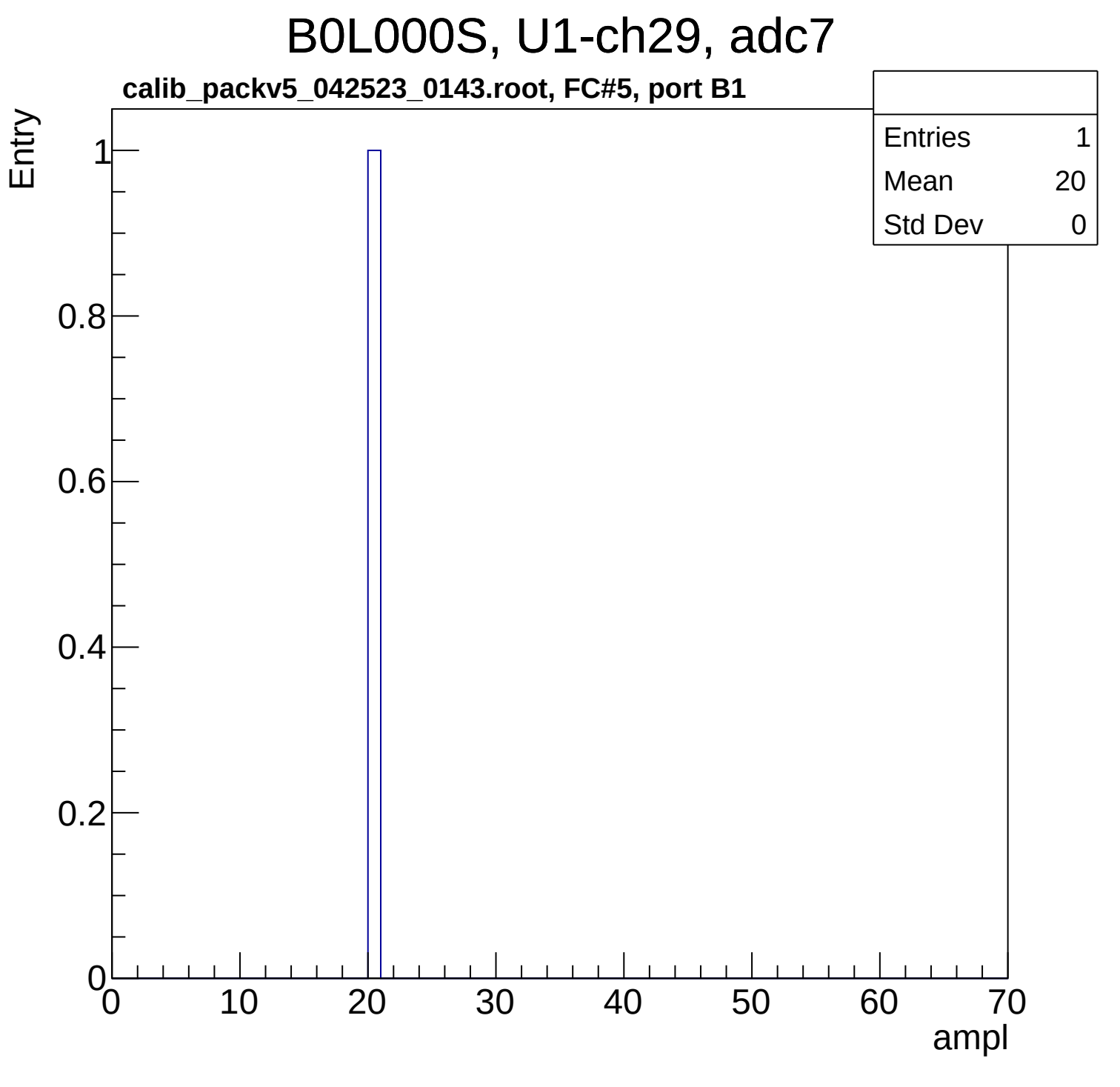
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch29, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 20
Std Dev 0
Entry
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0.6
0.4
0.2
0
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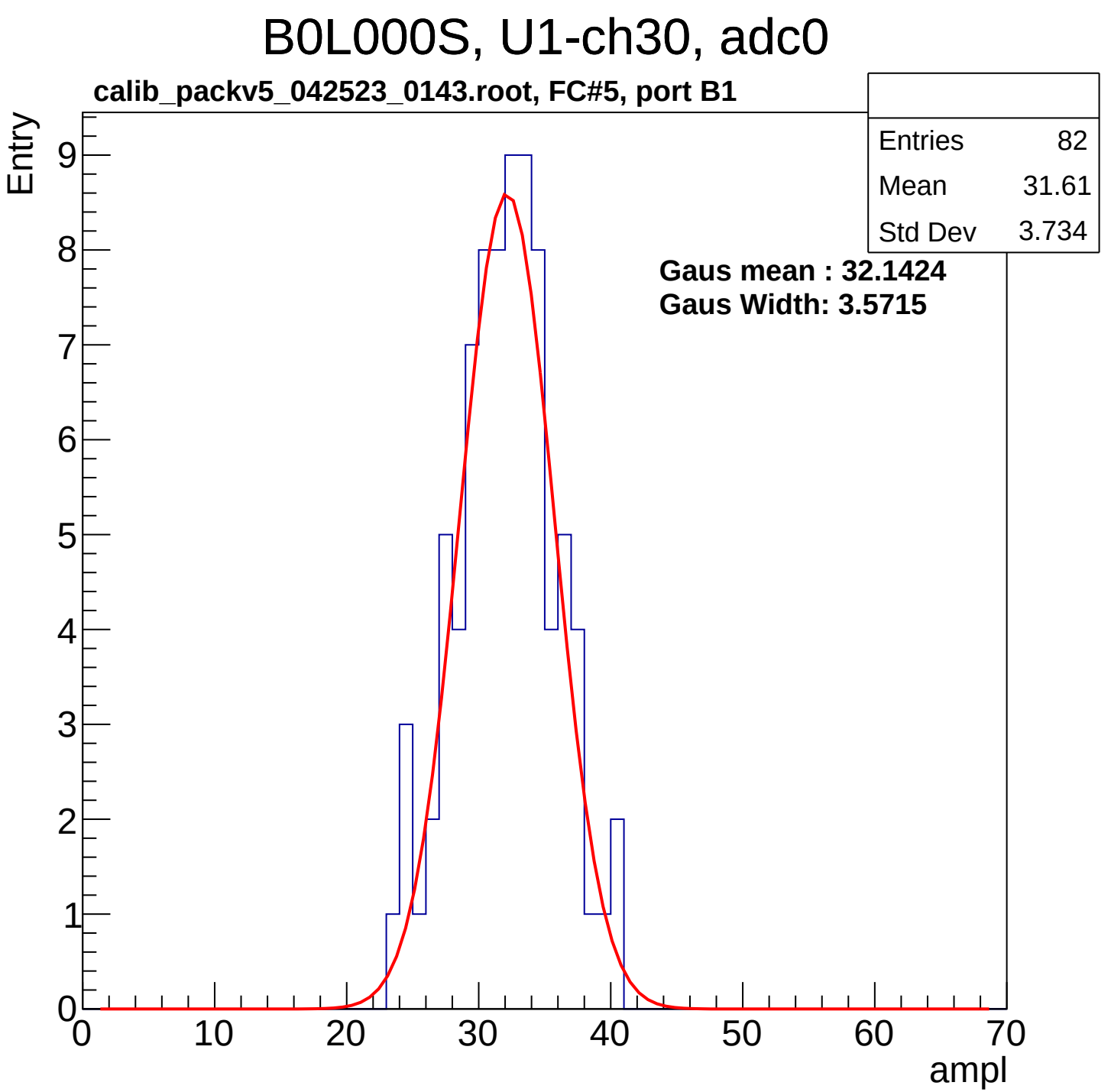
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch30, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 31.61
Std Dev 3.734
Gaus mean : 32.1424
Gaus Width: 3.5715
Entry
ampl
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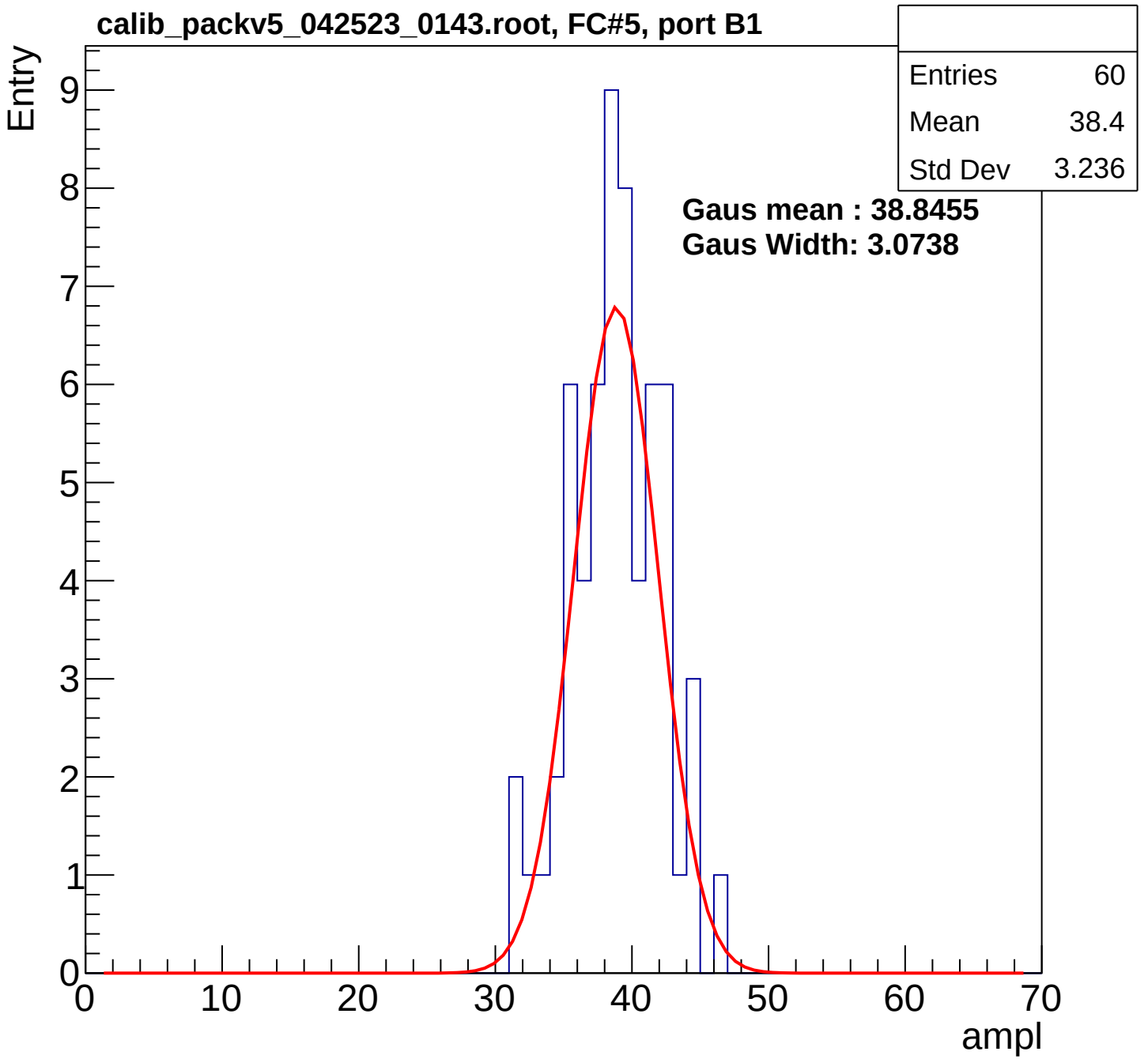
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch30, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 38.4
Std Dev 3.236
Gaus mean : 38.8455
Gaus Width: 3.0738
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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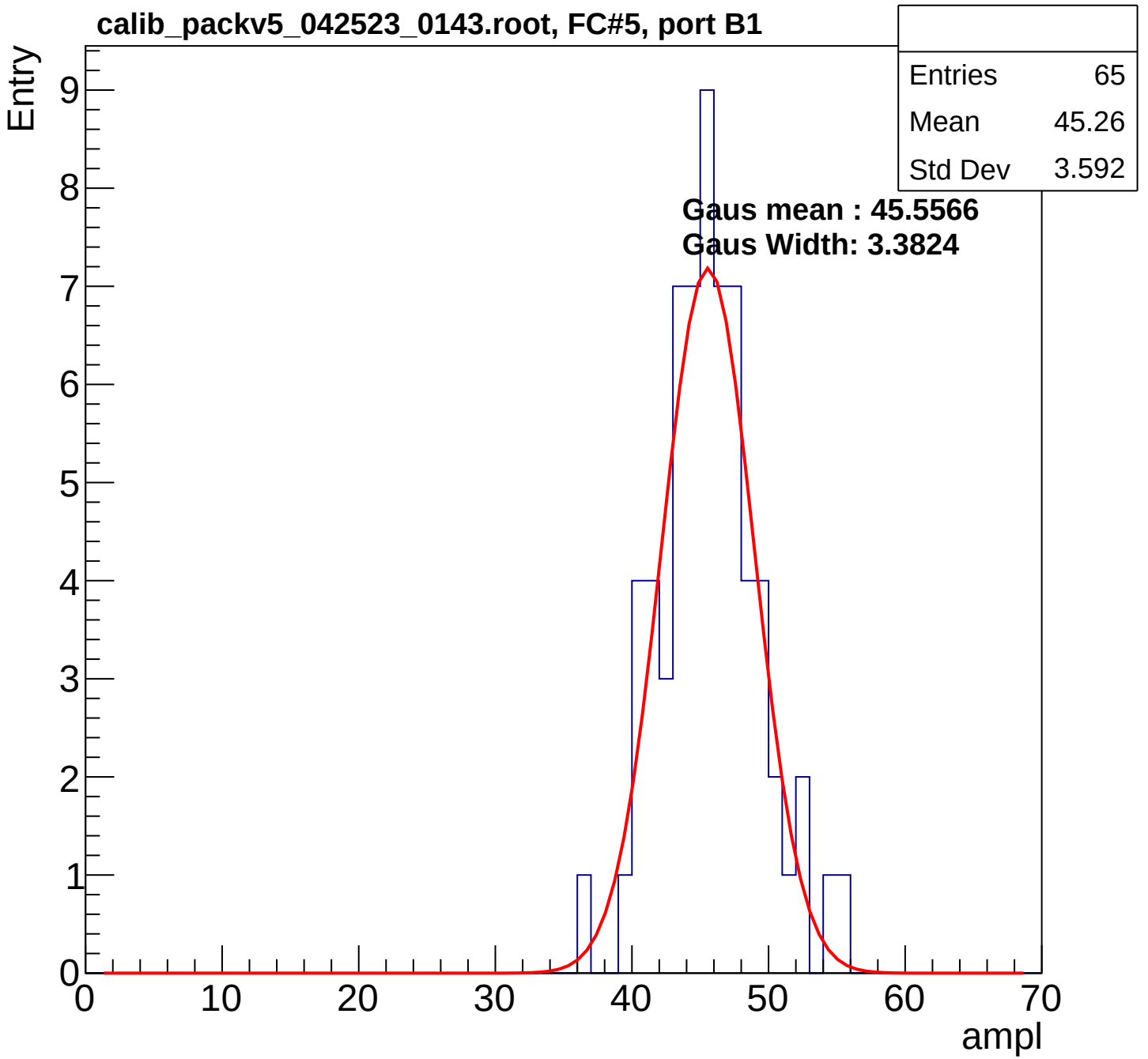
60
70


B0L000S, U1-ch30, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 45.26
Std Dev 3.592
Gaus mean : 45.5566
Gaus Width: 3.3824
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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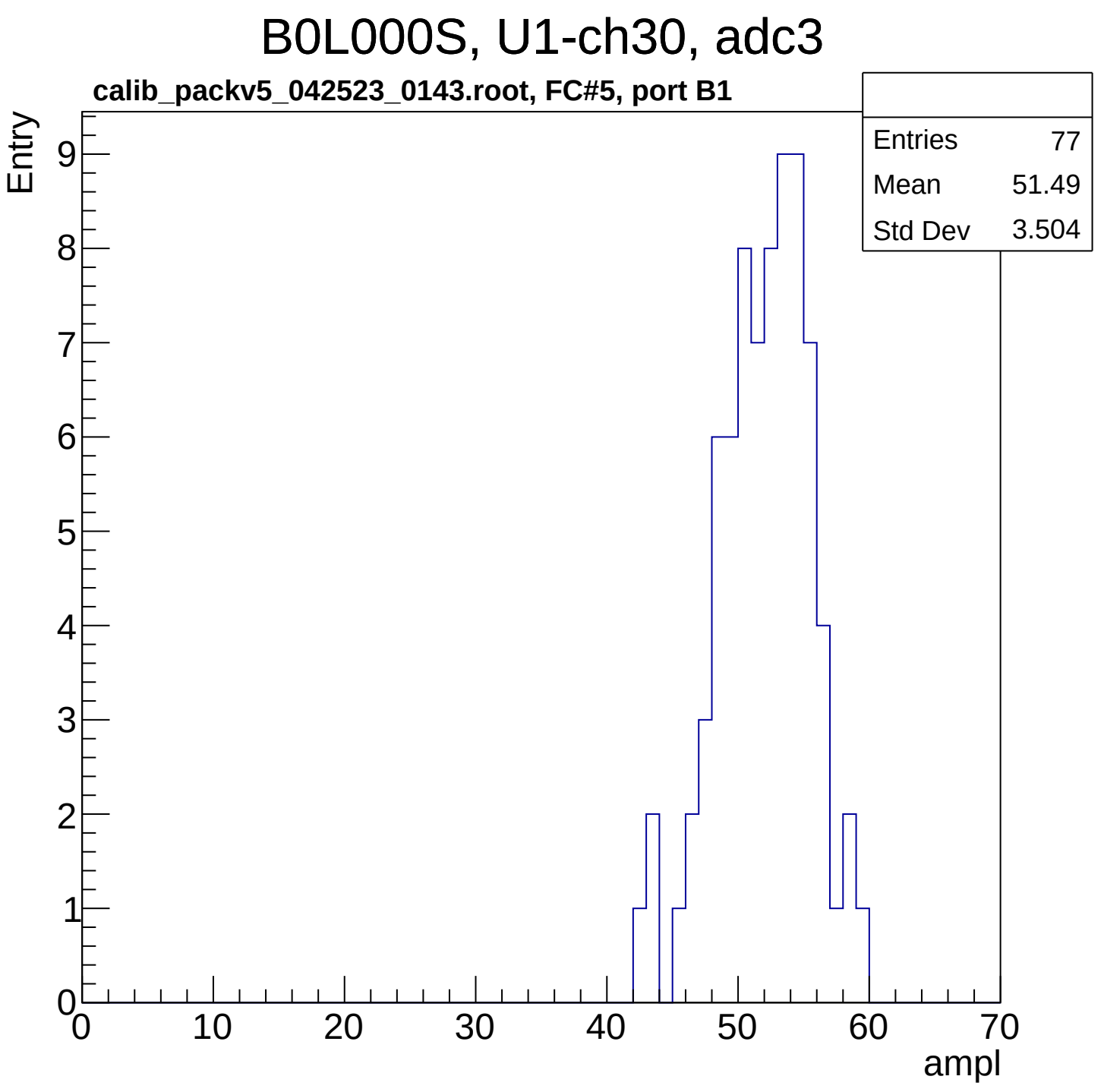
B0L000S, U1-ch30, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 51.49
Std Dev 3.504
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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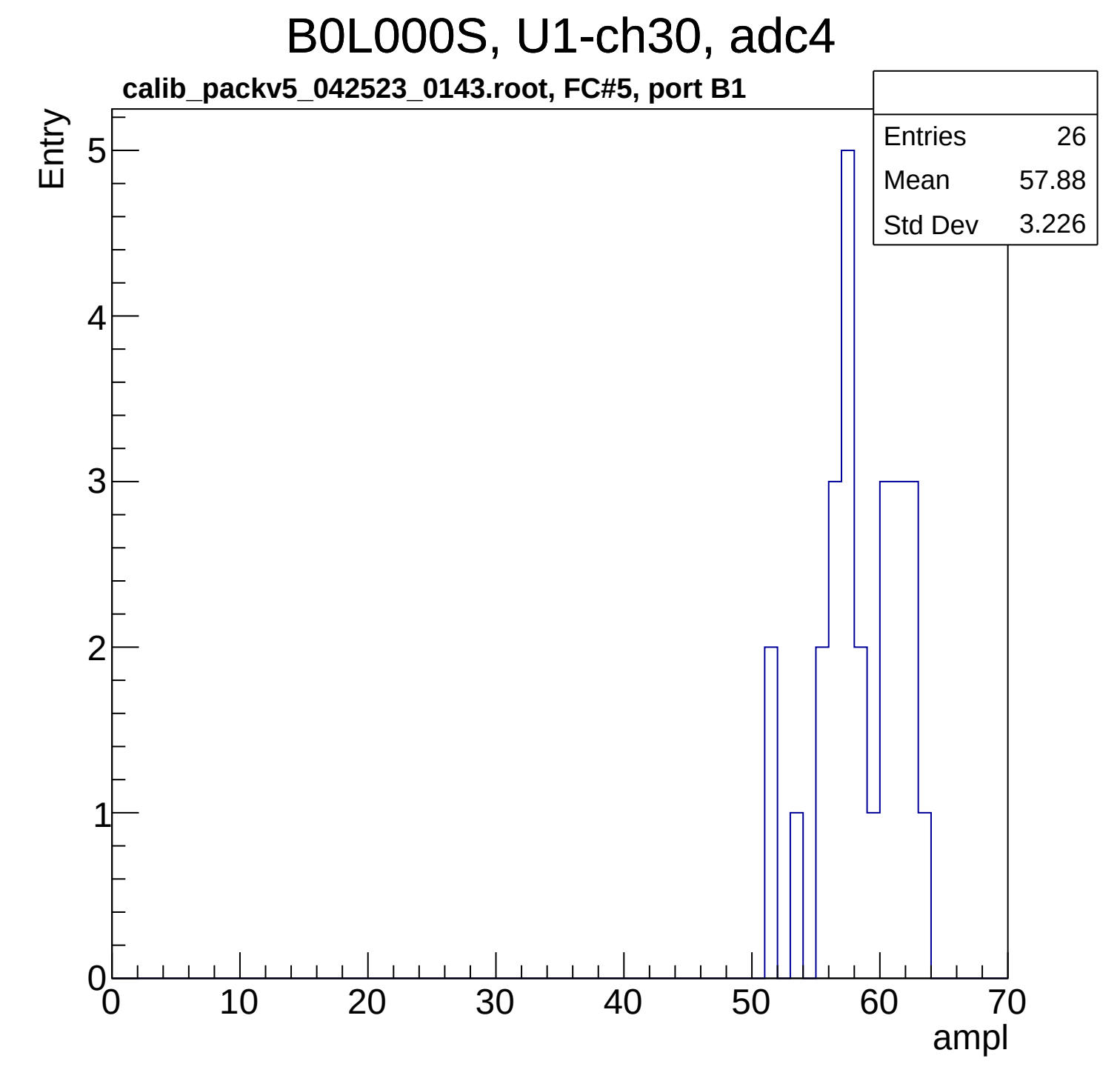
40
50
60
70


B0L000S, U1-ch30, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 57.88
Std Dev 3.226
Entry
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2
3
4
5
0
10
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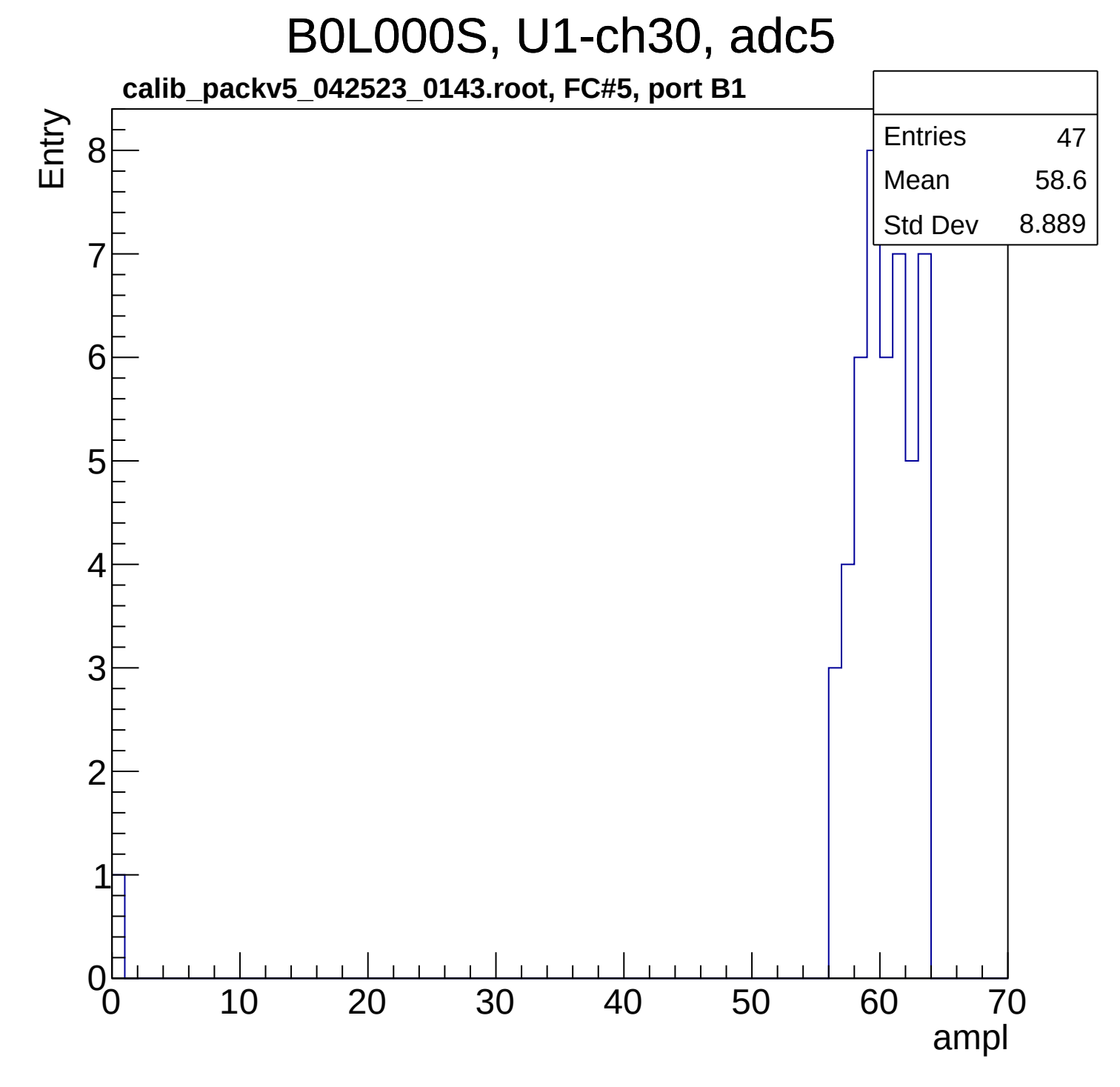
50
60
70
ampl


B0L000S, U1-ch30, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.6
Std Dev 8.889
Entry
ampl
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2
3
4
5
6
7
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0
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40
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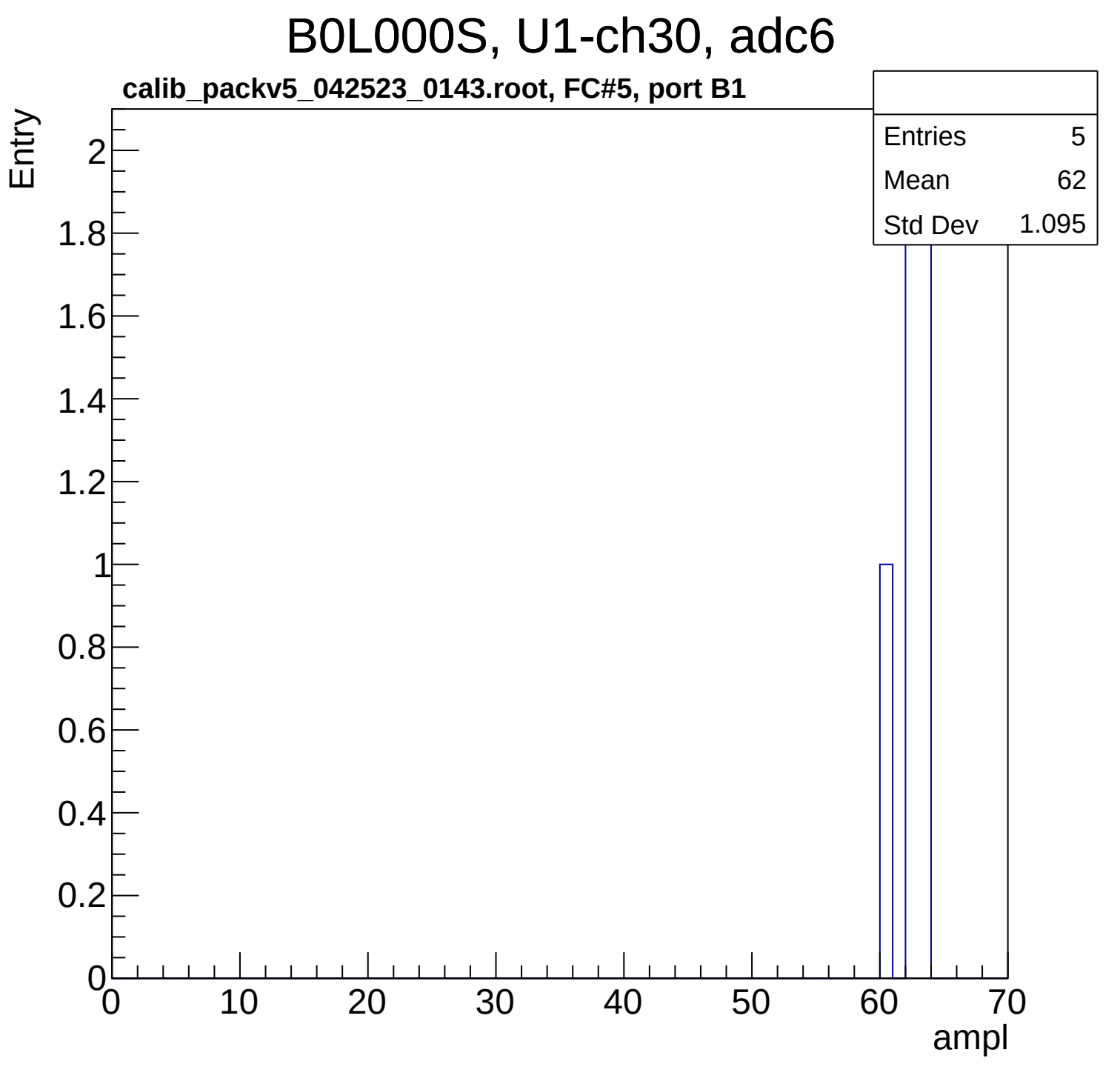
B0L000S, U1-ch30, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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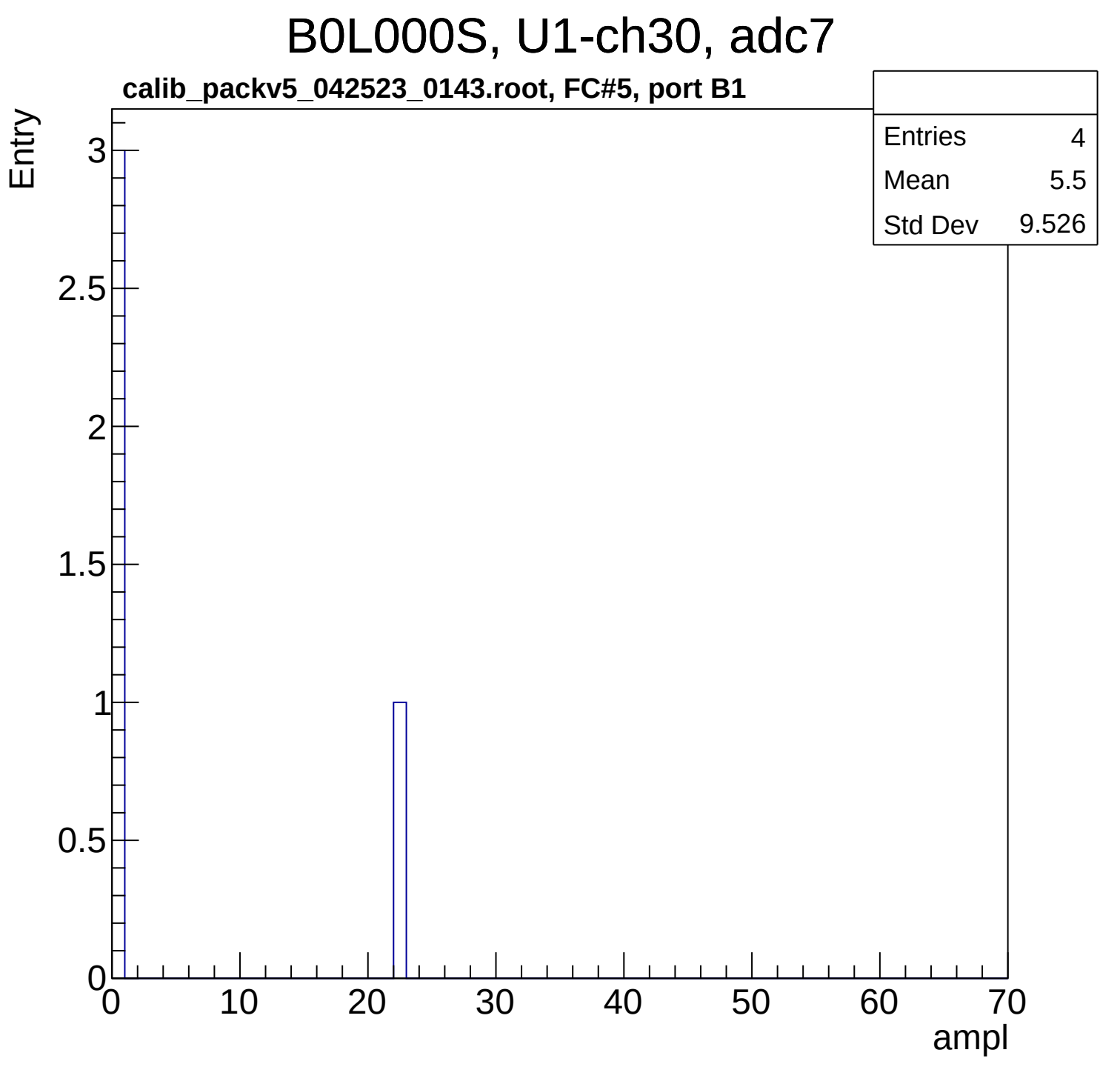
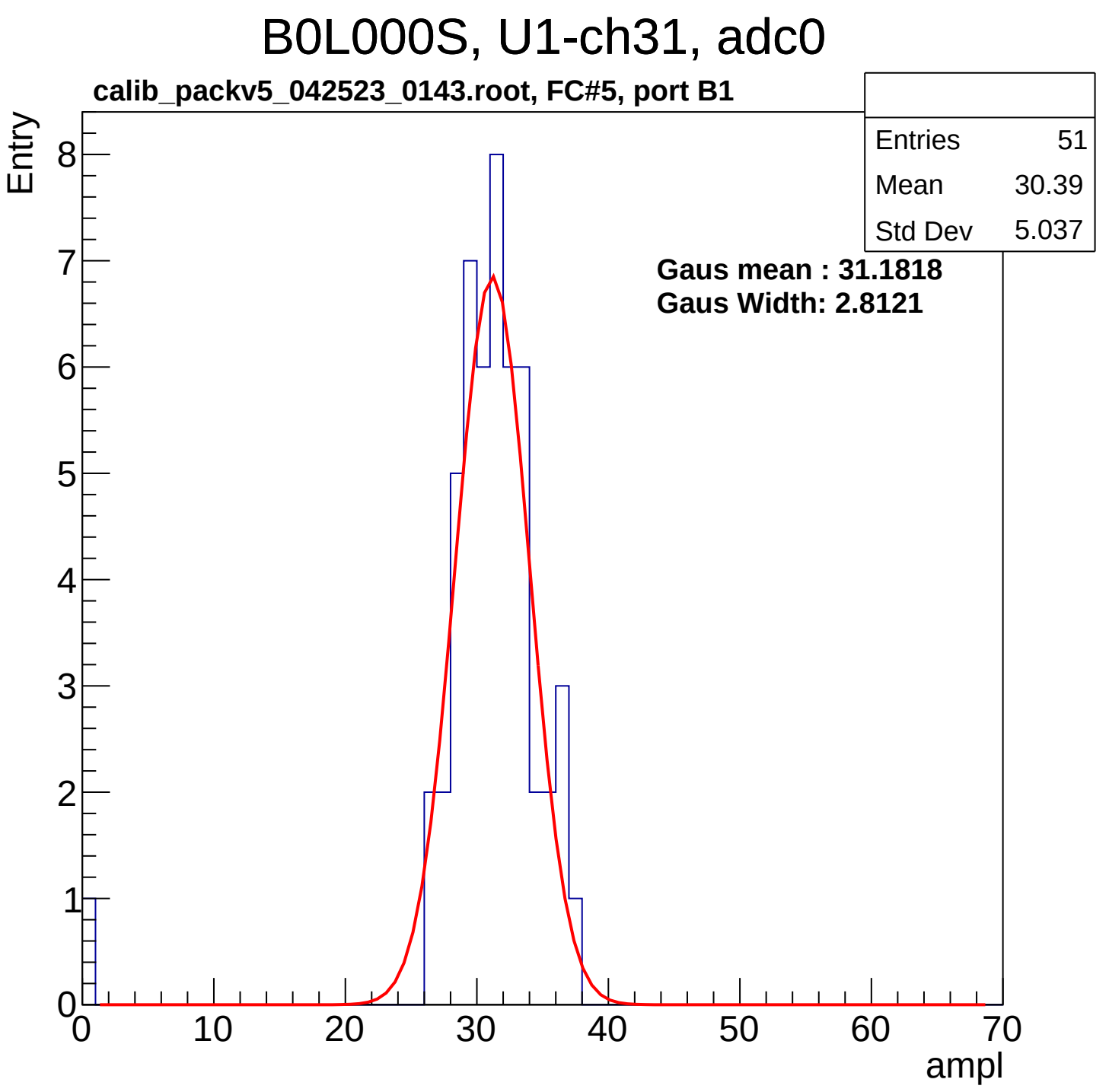
40
50
60
70


B0L000S, U1-ch30, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch31, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 30.39
Std Dev 5.037
Gaus mean : 31.1818
Gaus Width: 2.8121
Entry
ampl
0
1
2
3
4
5
6
7
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0
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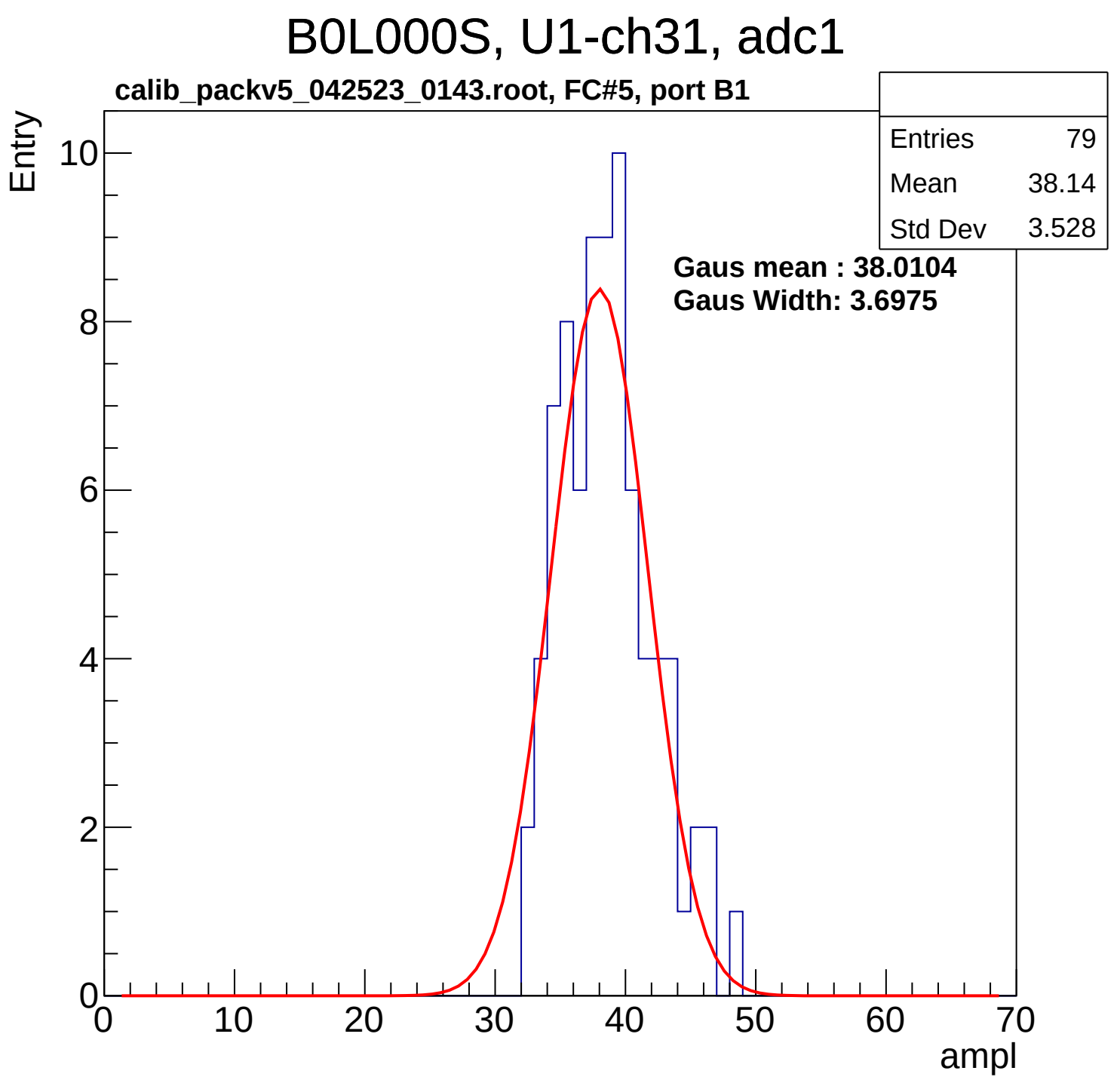
B0L000S, U1-ch31, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 38.14
Std Dev 3.528
Gaus mean : 38.0104
Gaus Width: 3.6975
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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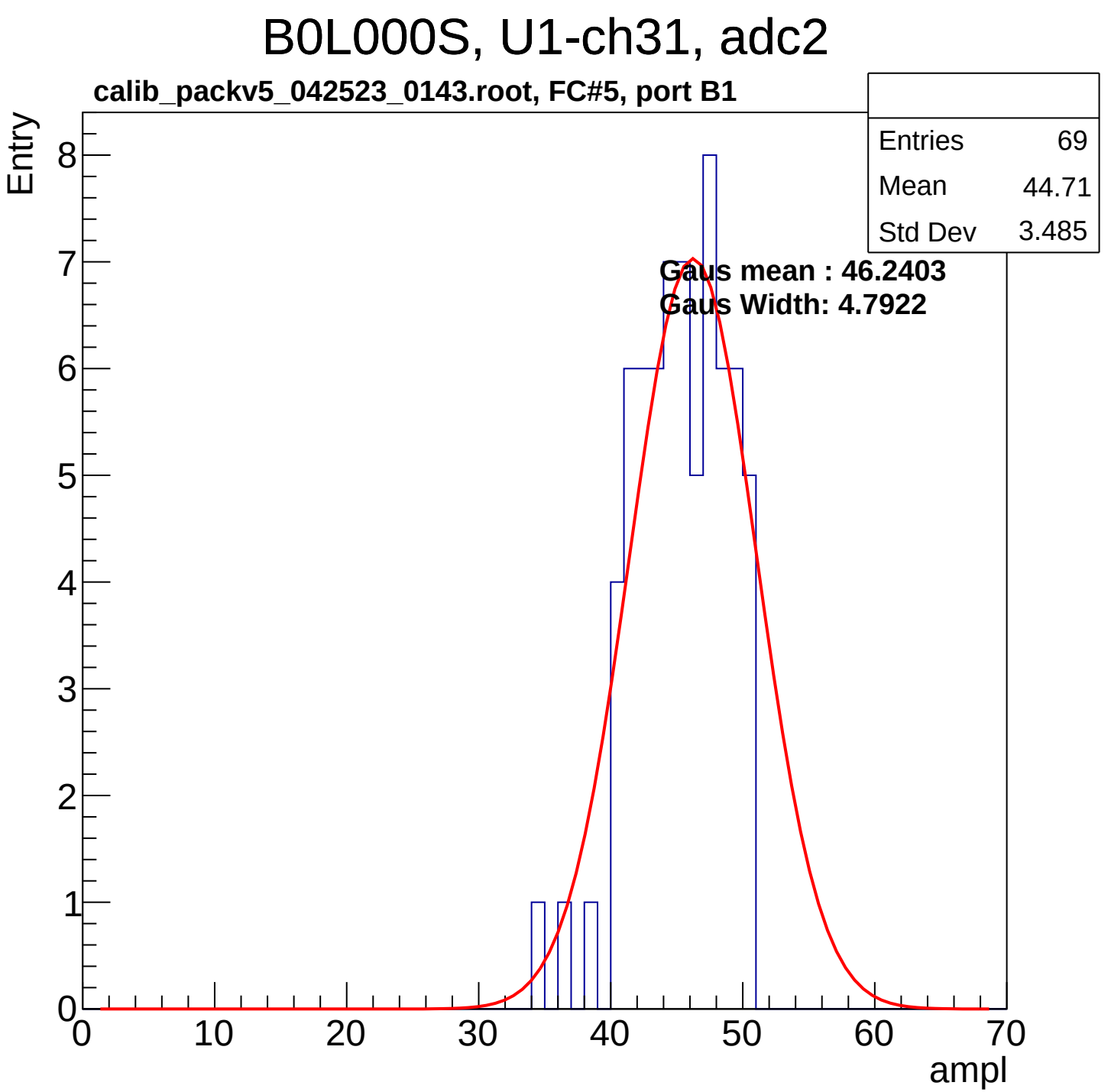
B0L000S, U1-ch31, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.71
Std Dev 3.485
Gaus mean : 46.2403
Gaus Width: 4.7922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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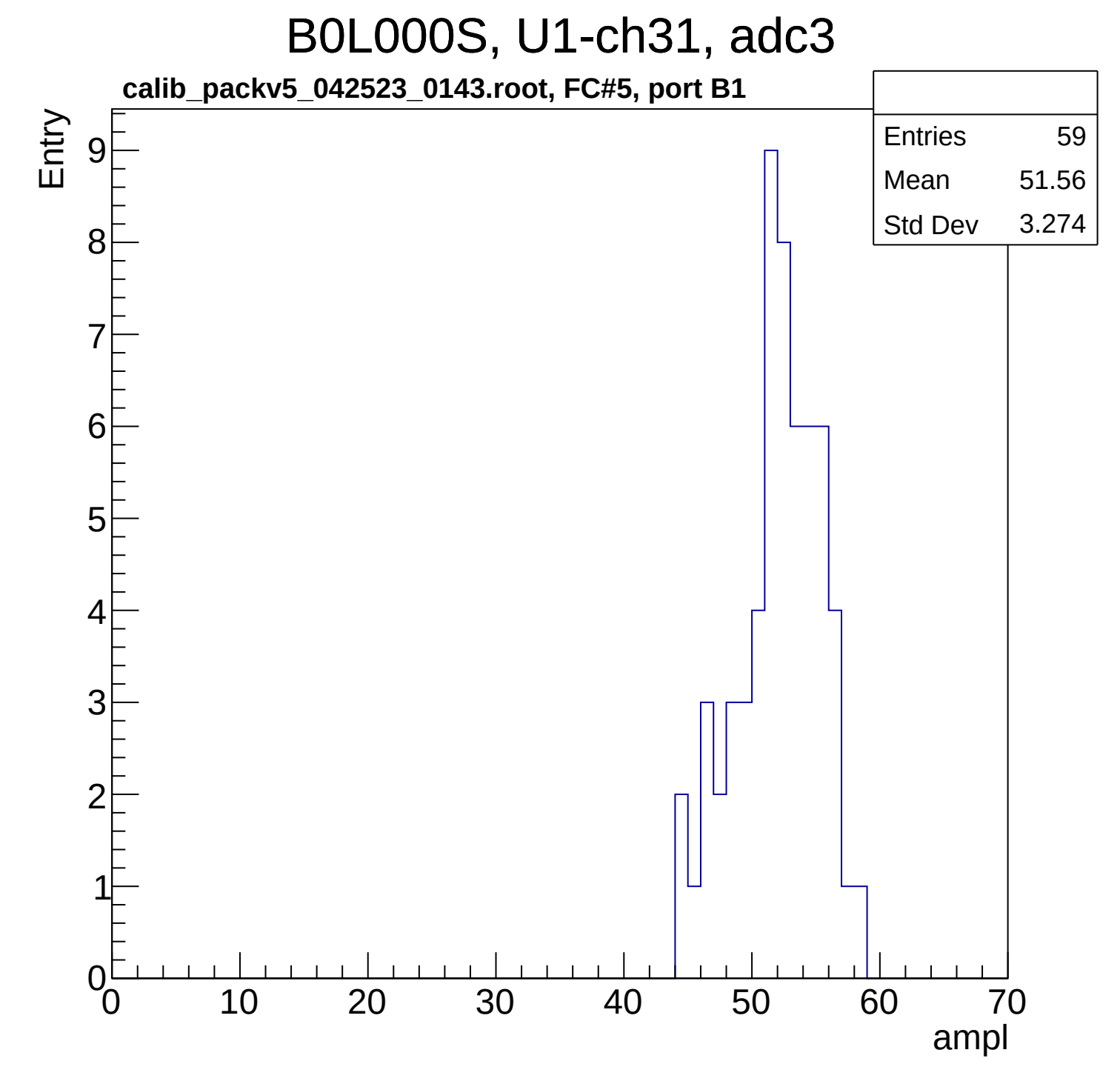
B0L000S, U1-ch31, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 51.56
Std Dev 3.274
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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60
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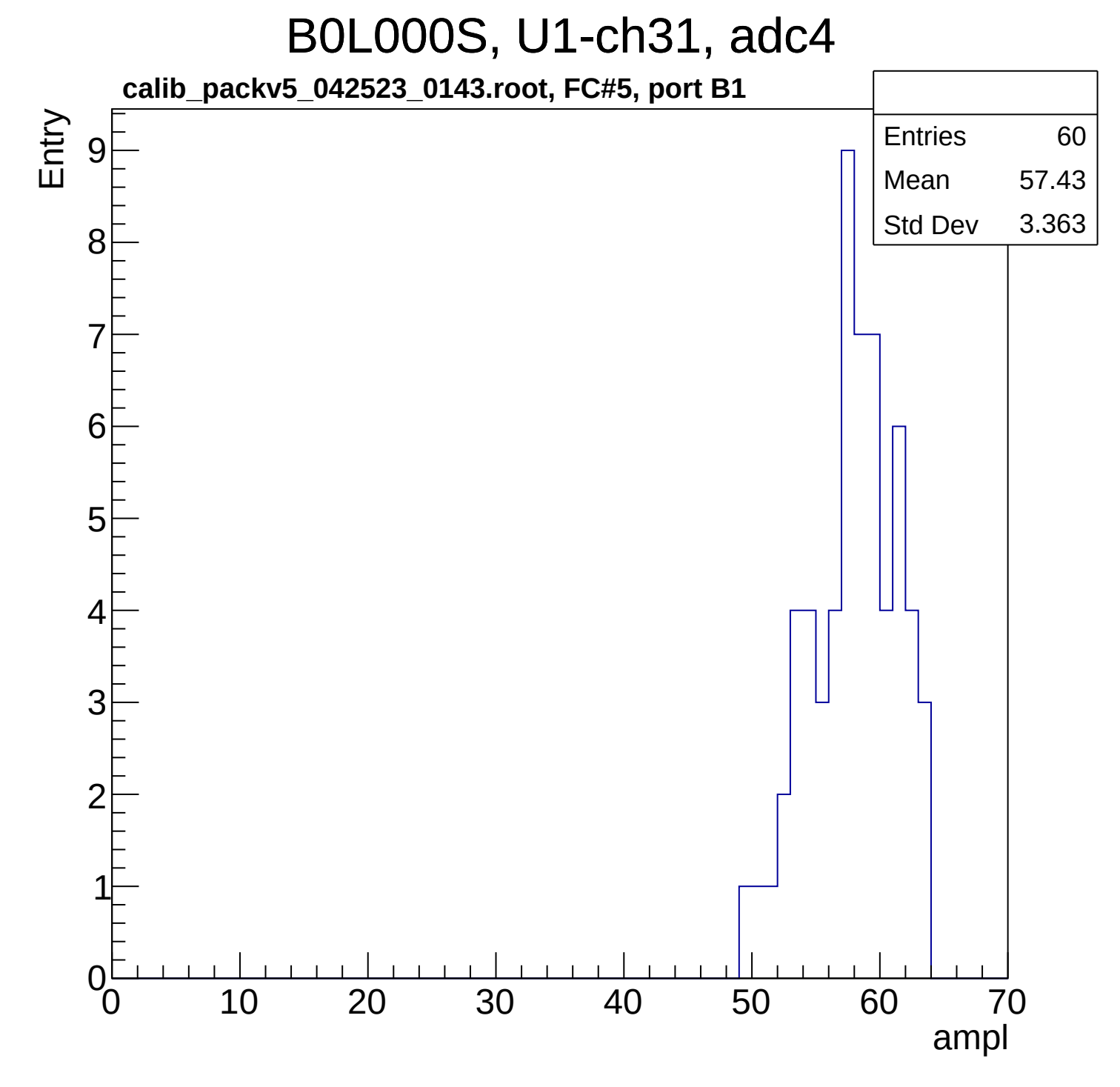
B0L000S, U1-ch31, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.43
Std Dev 3.363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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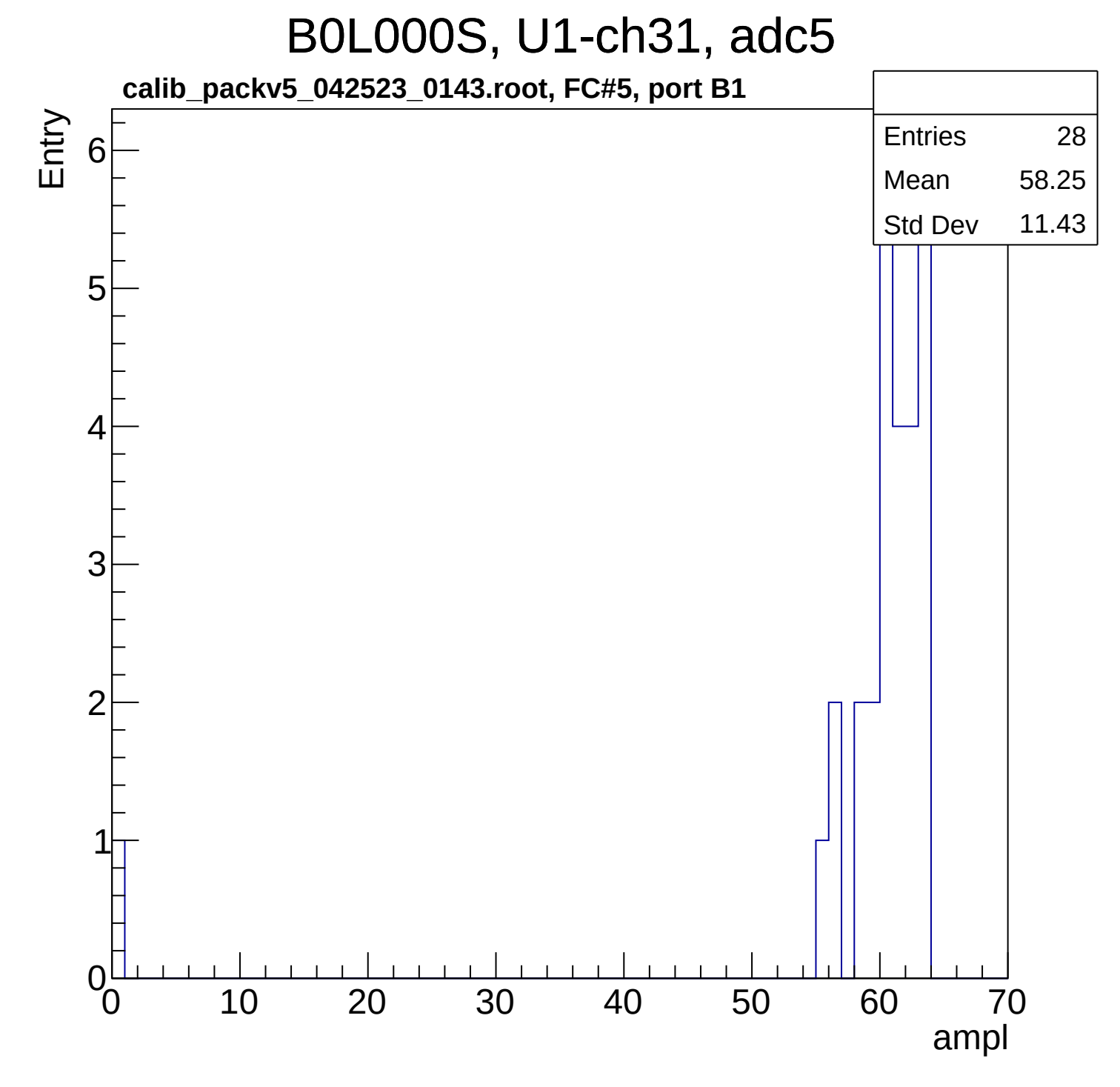
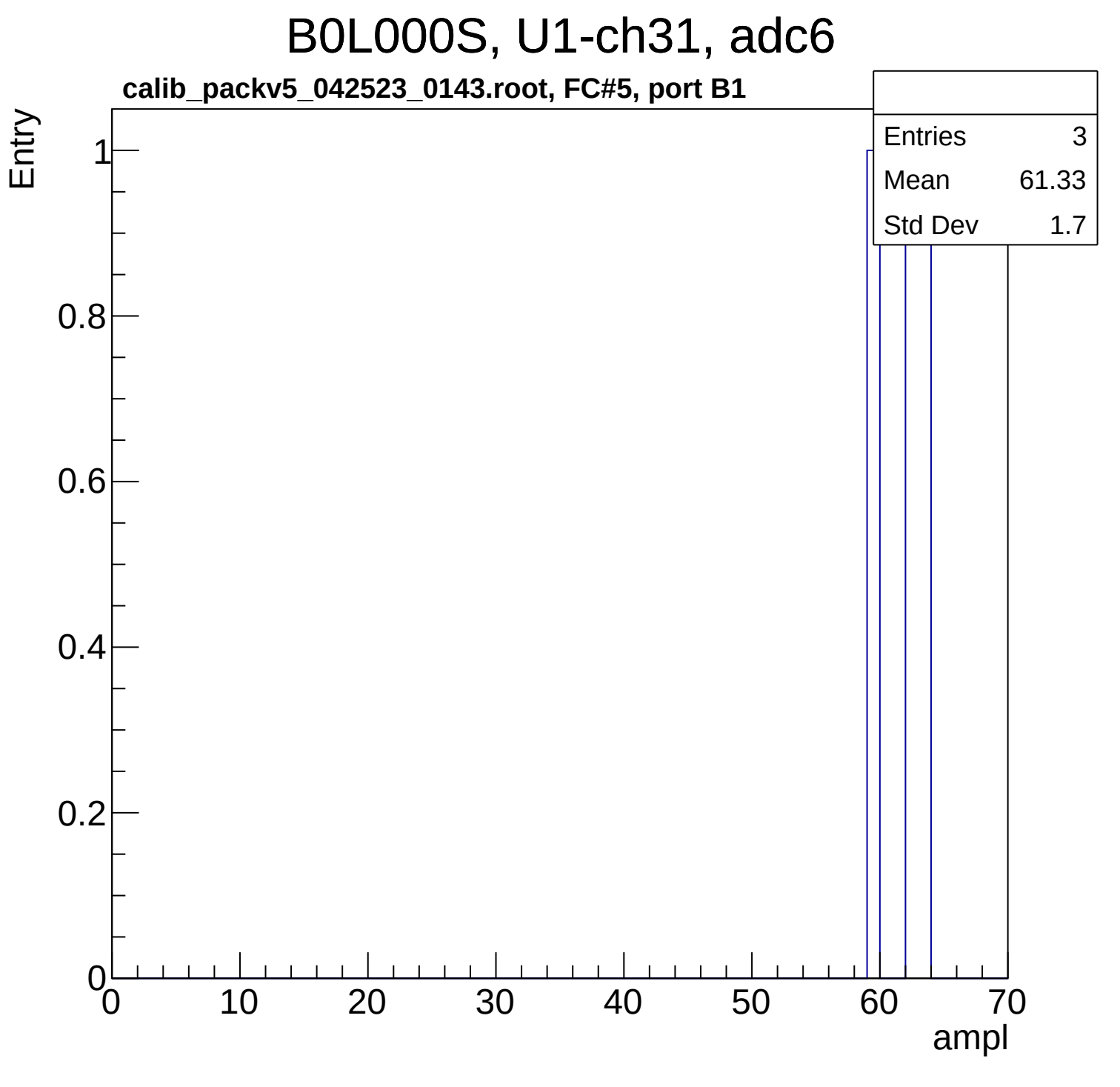

B0L000S, U1-ch31, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.25
Std Dev 11.43
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch31, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.33
Std Dev 1.7
Entry
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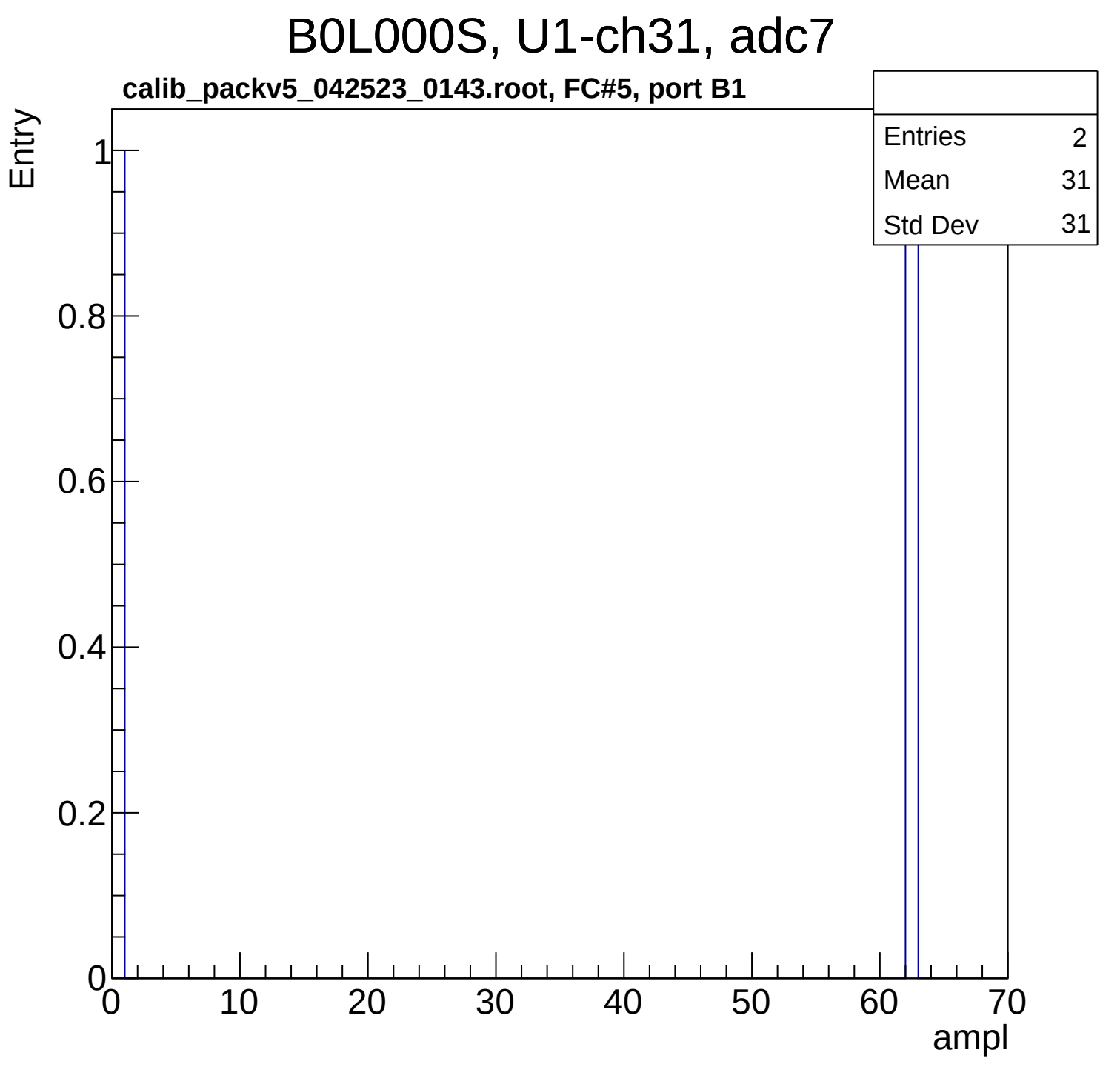
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch31, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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0.6
0.4
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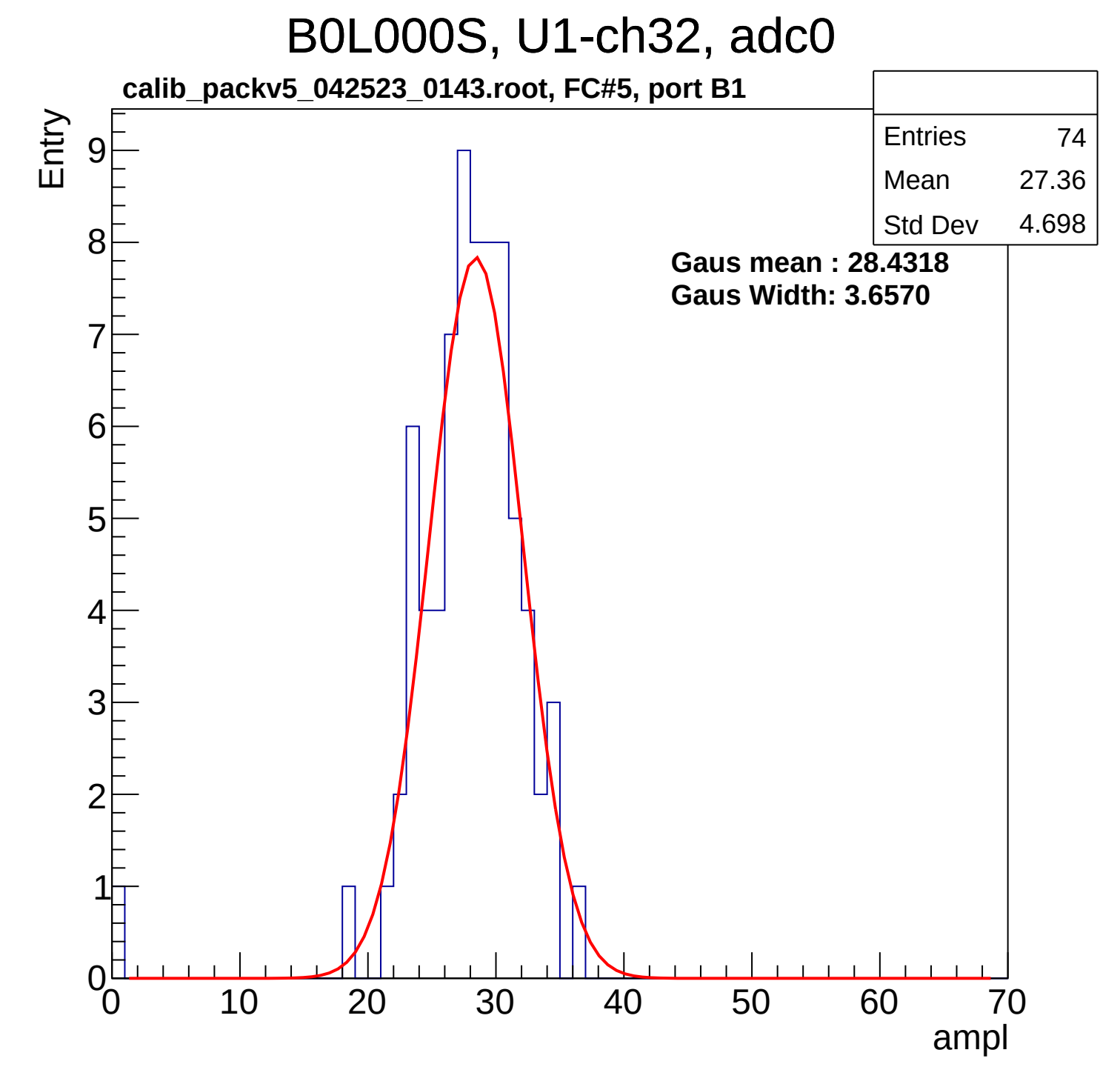
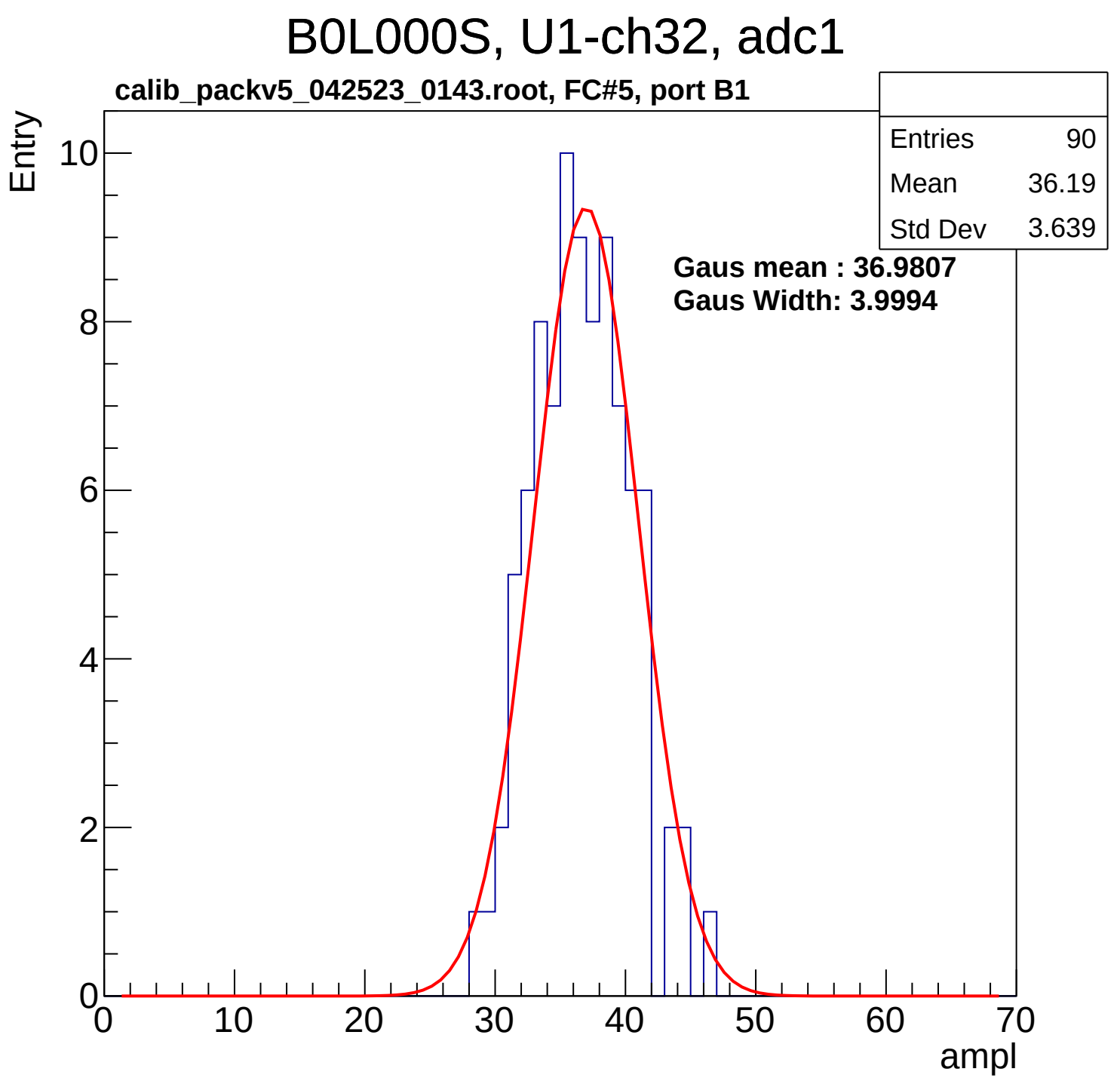
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch32, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 27.36
Std Dev 4.698
Gaus mean : 28.4318
Gaus Width: 3.6570
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch32, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 90
Mean 36.19
Std Dev 3.639
Gaus mean : 36.9807
Gaus Width: 3.9994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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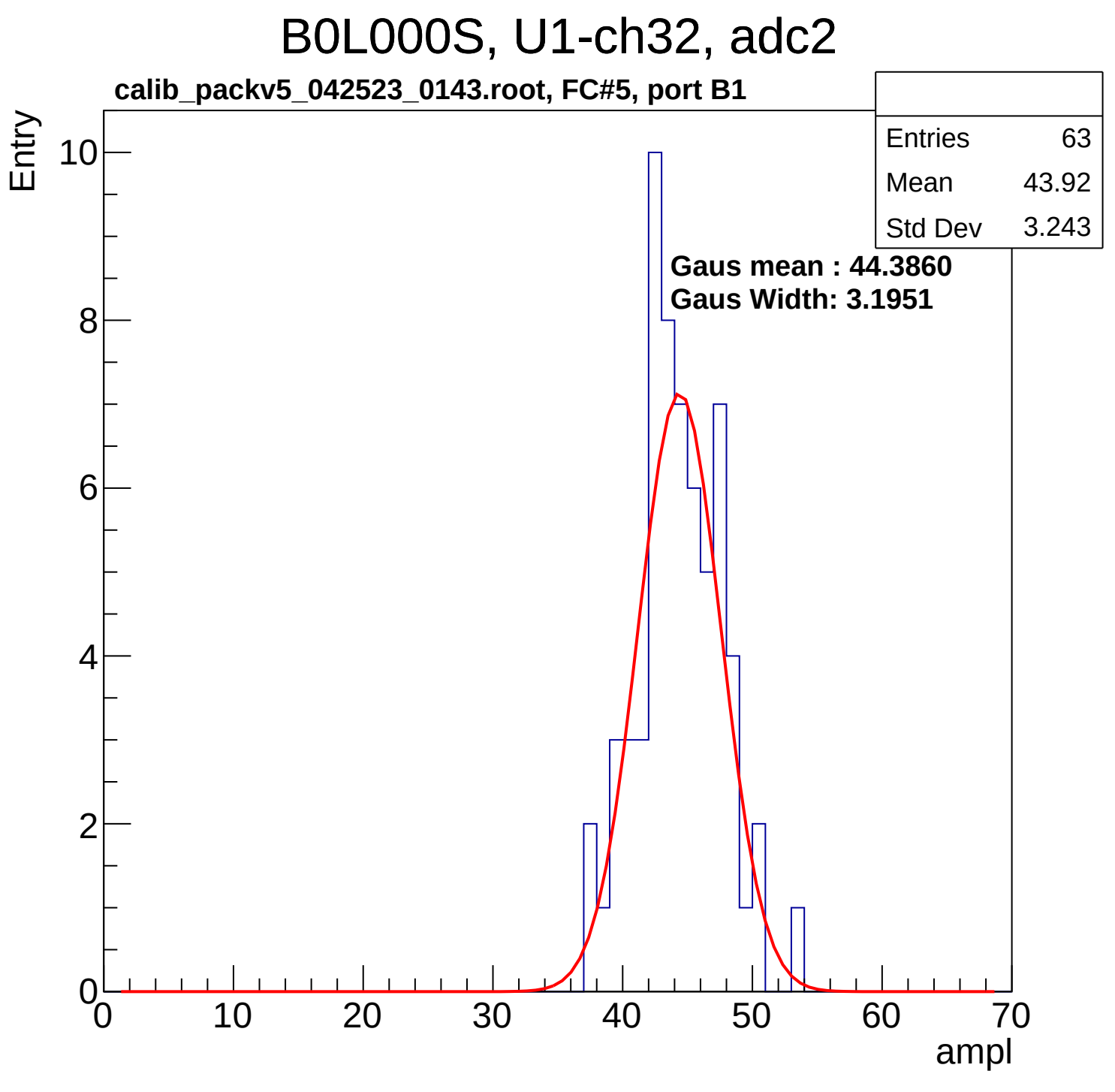
B0L000S, U1-ch32, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.92
Std Dev 3.243
Gaus mean : 44.3860
Gaus Width: 3.1951
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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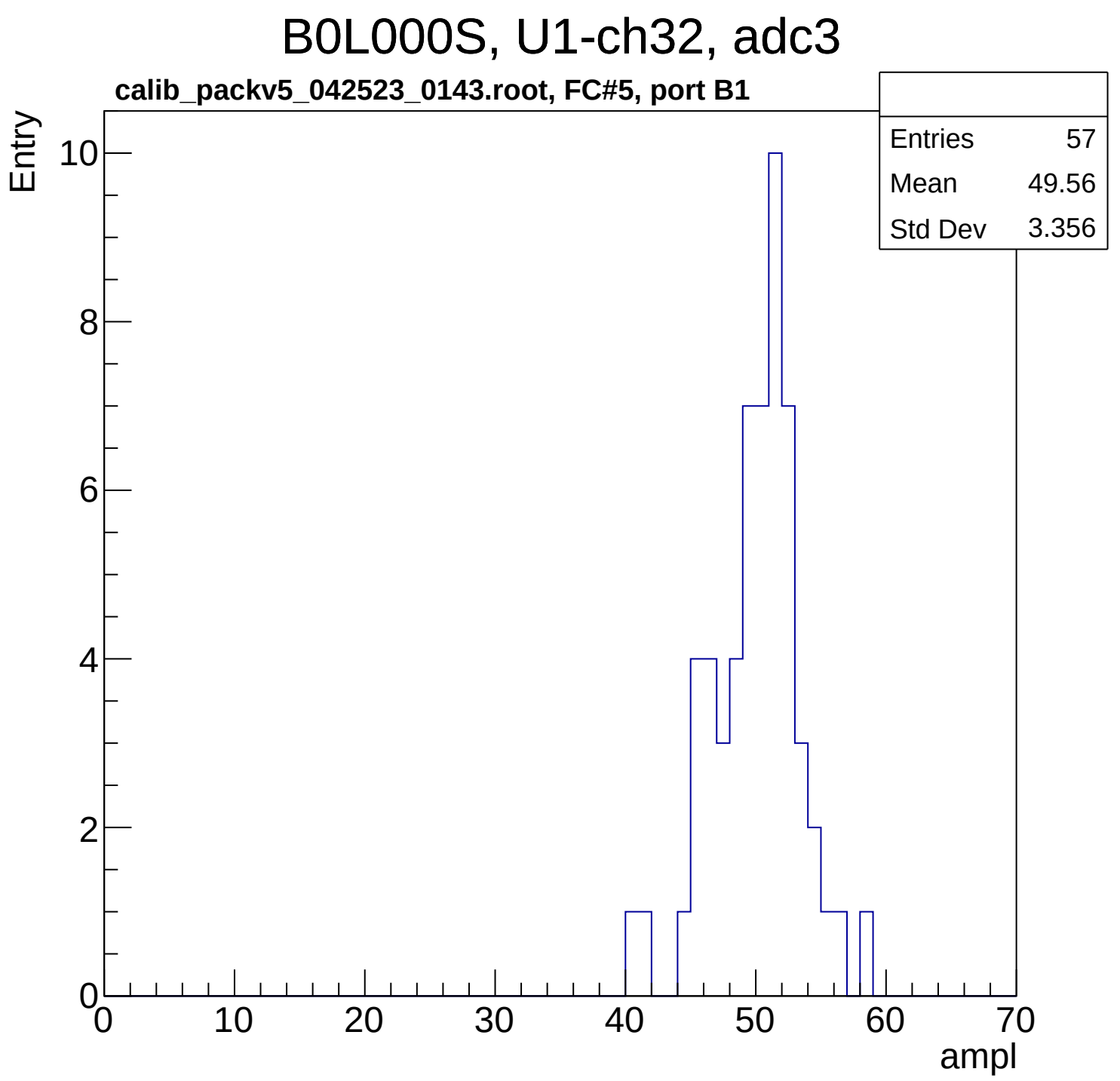
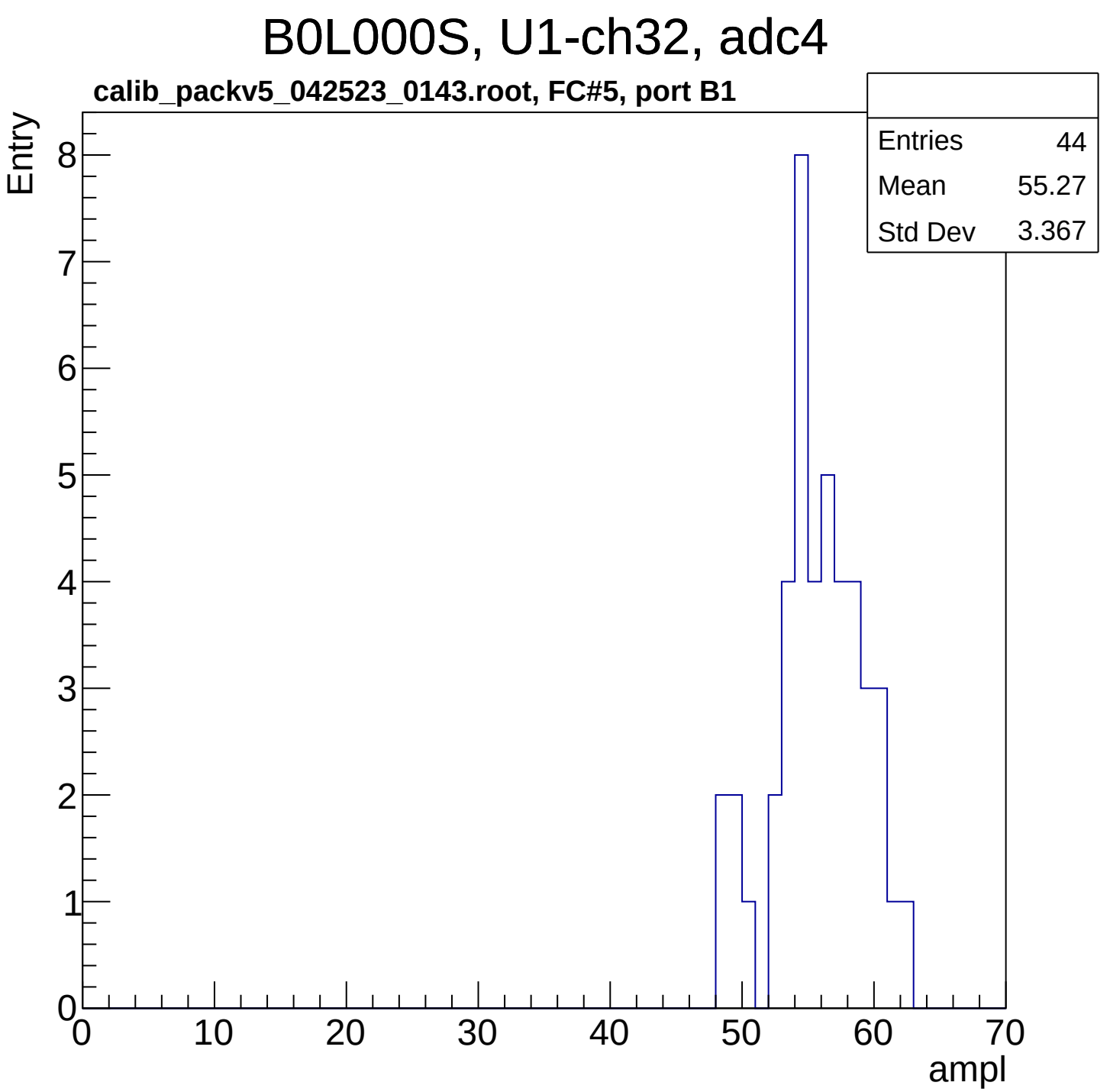
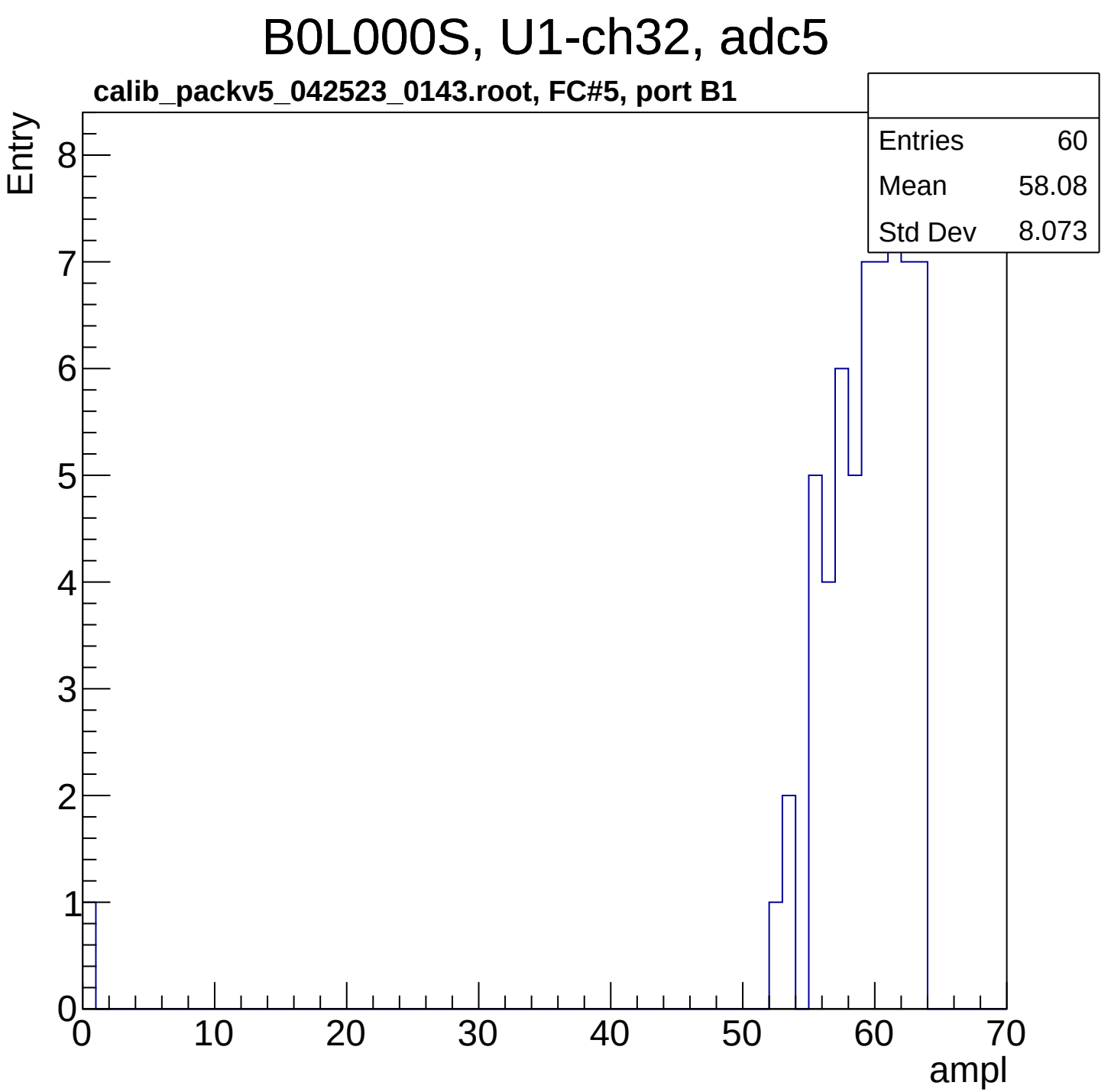

B0L000S, U1-ch32, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.56
Std Dev 3.356
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch32, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 55.27
Std Dev 3.367
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch32, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58.08
Std Dev 8.073
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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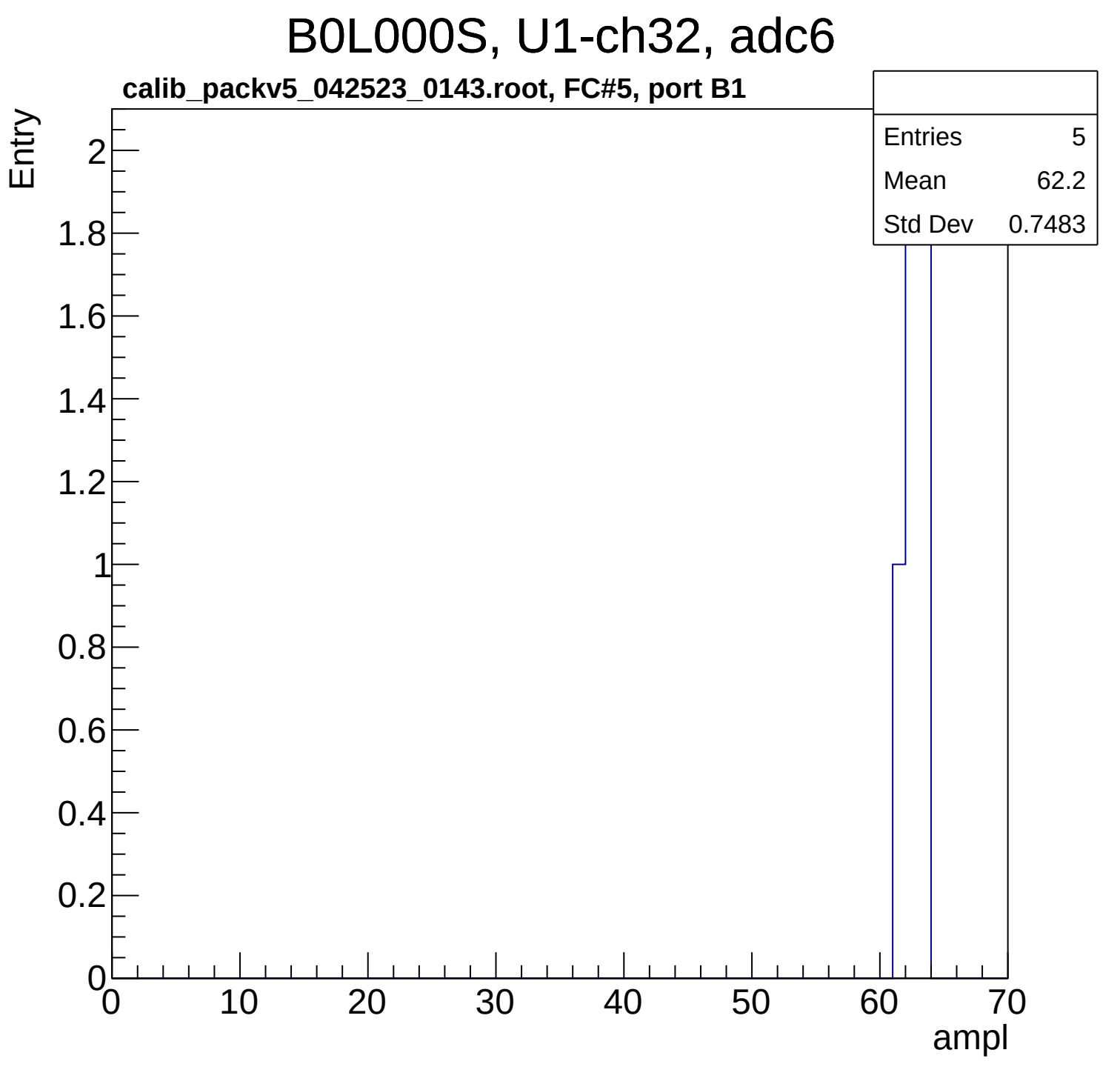
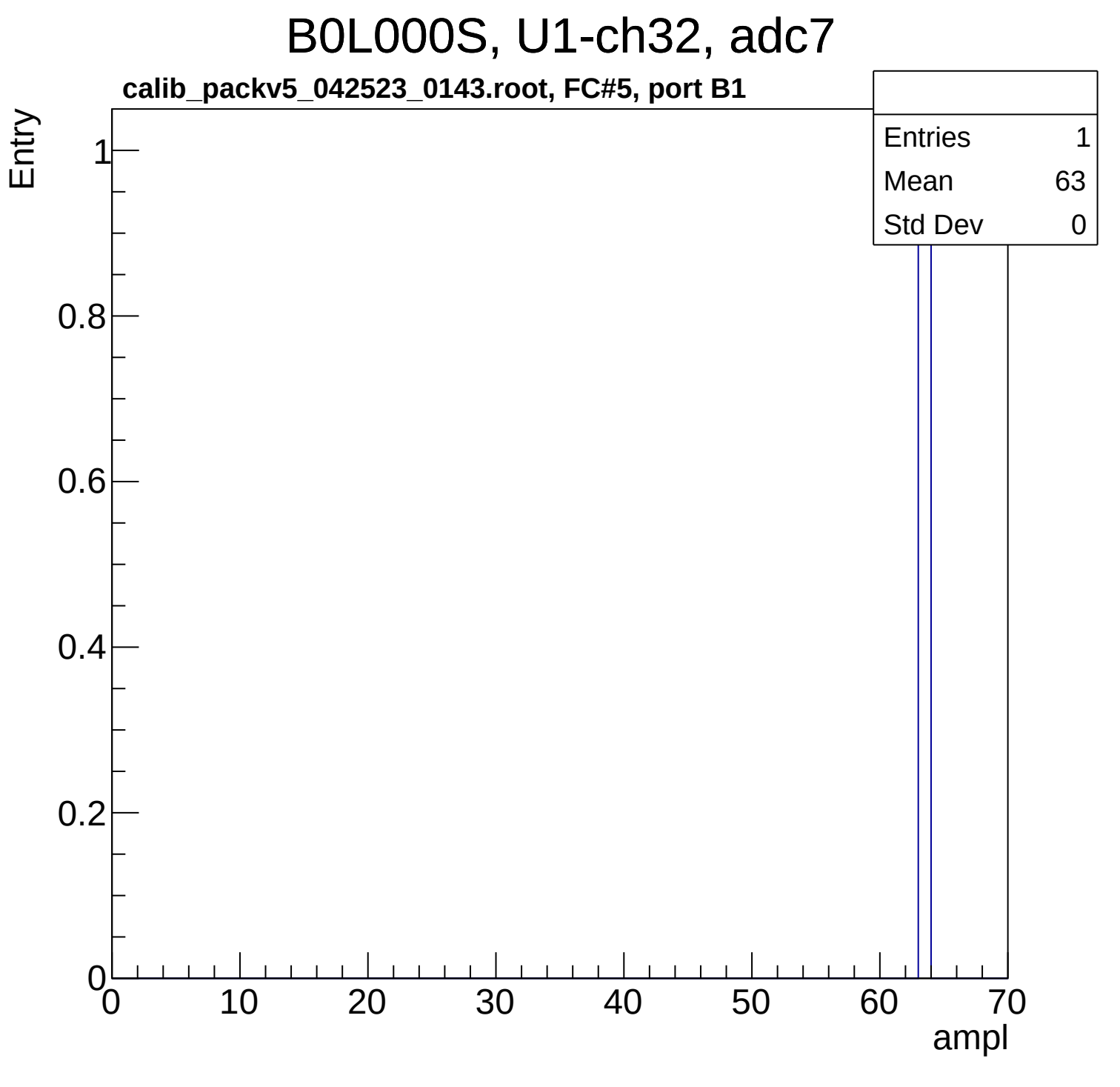
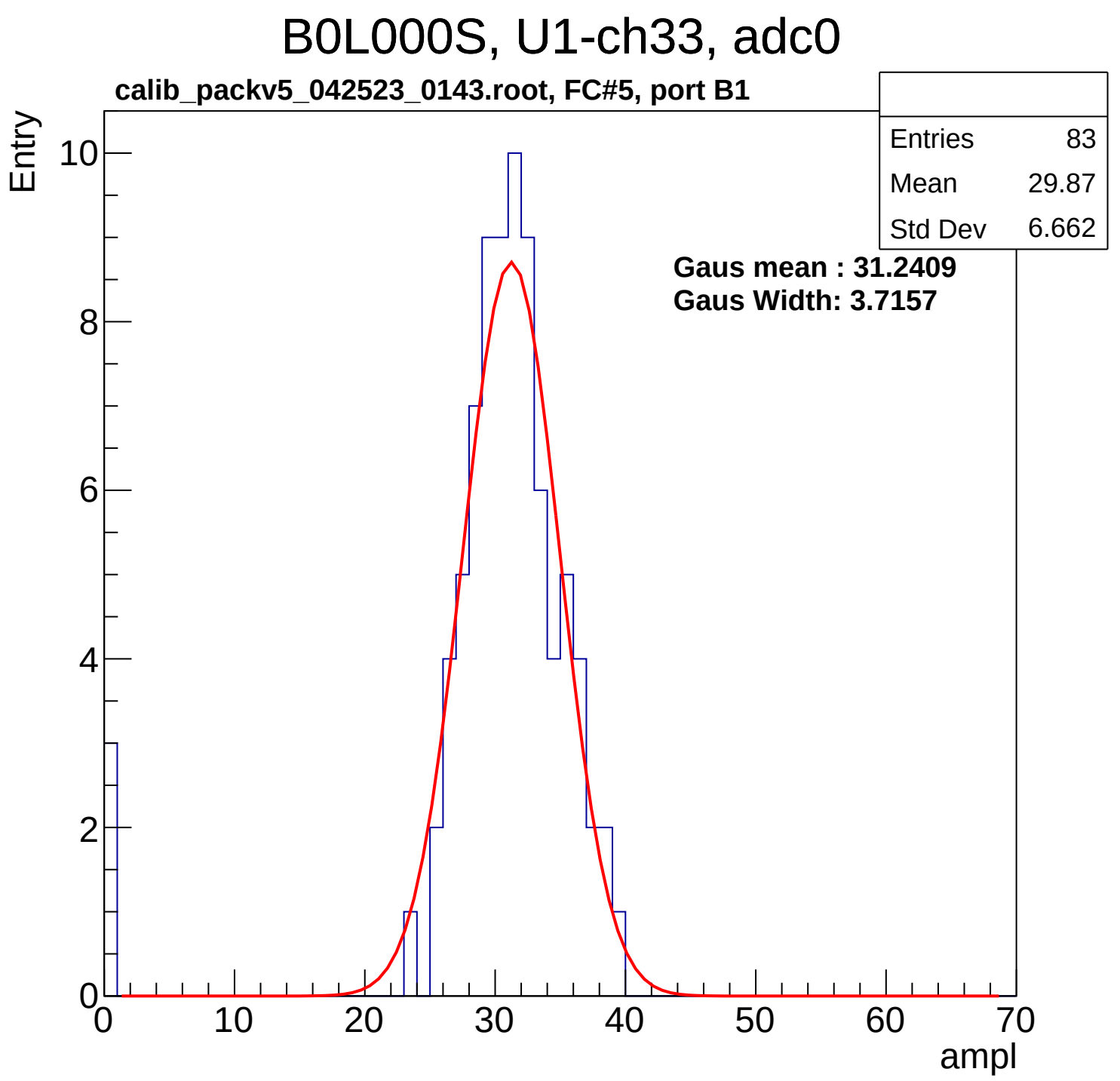

B0L000S, U1-ch32, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch32, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch33, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 29.87
Std Dev 6.662
Gaus mean : 31.2409
Gaus Width: 3.7157
Entry
ampl
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4
6
8
10
0
10
20
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40
50
60
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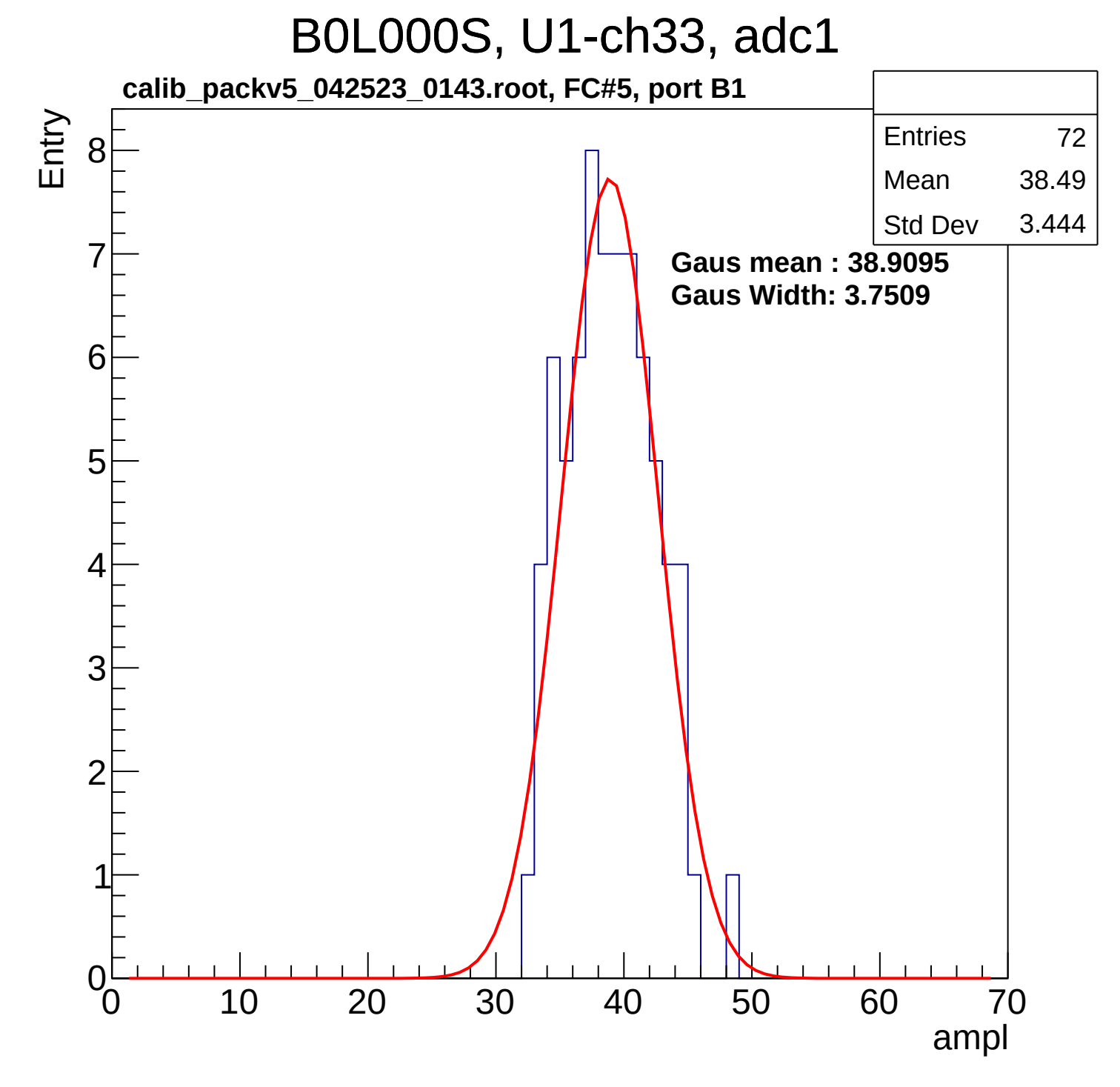
B0L000S, U1-ch33, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 38.49
Std Dev 3.444
Gaus mean : 38.9095
Gaus Width: 3.7509
Entry
ampl
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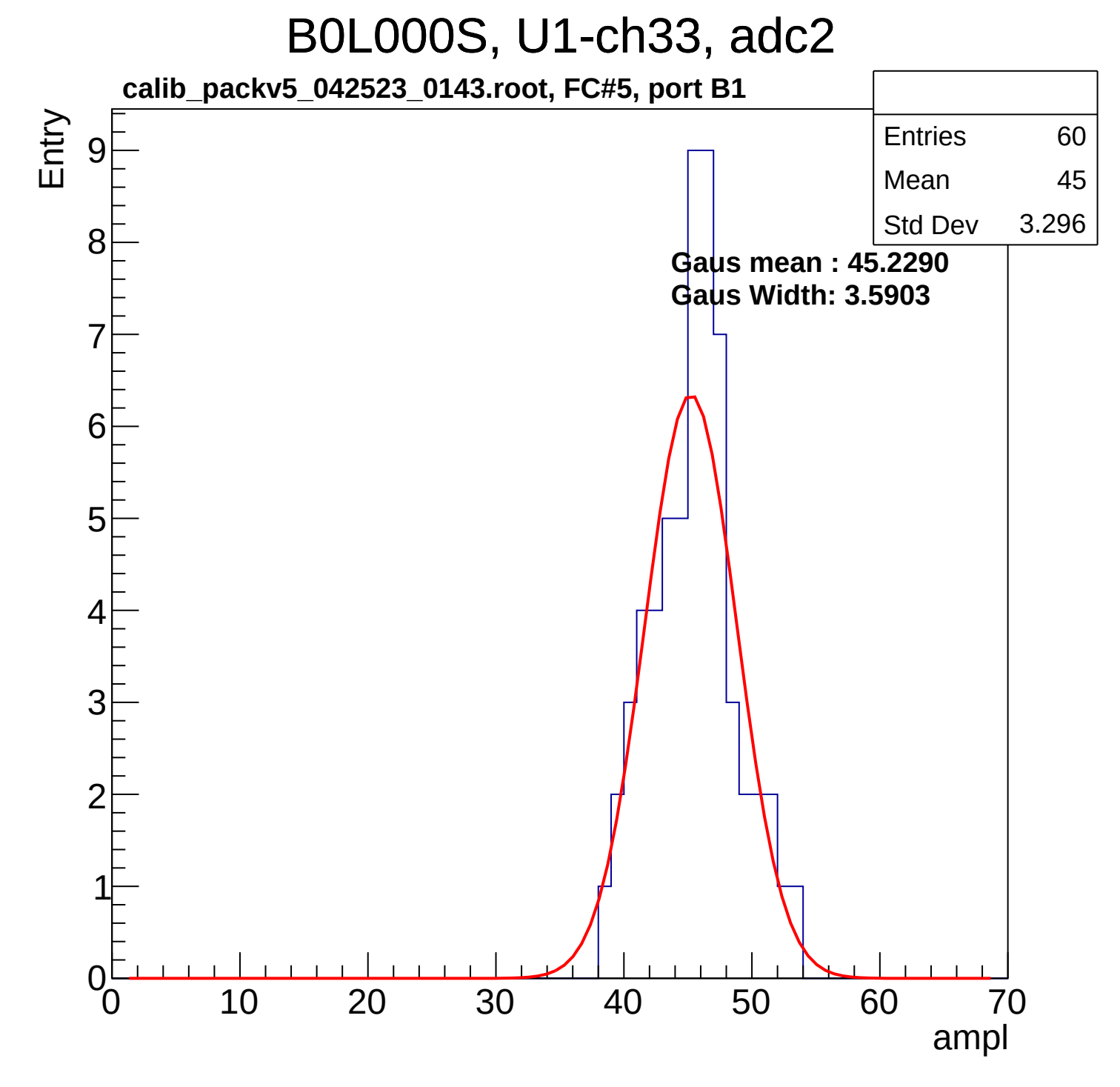
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch33, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 45
Std Dev 3.296
Gaus mean : 45.2290
Gaus Width: 3.5903
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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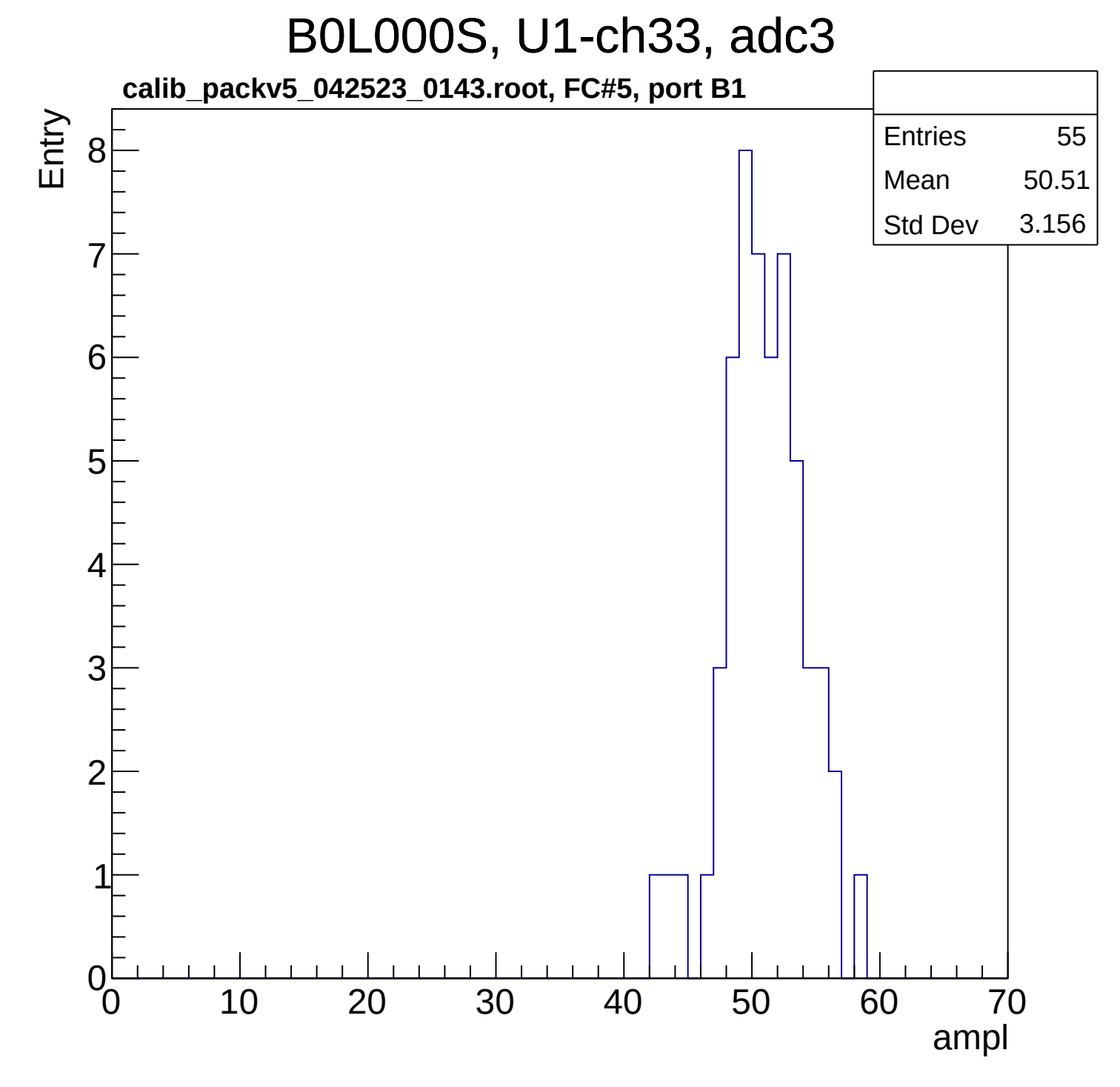
B0L000S, U1-ch33, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 50.51
Std Dev 3.156
Entry
ampl
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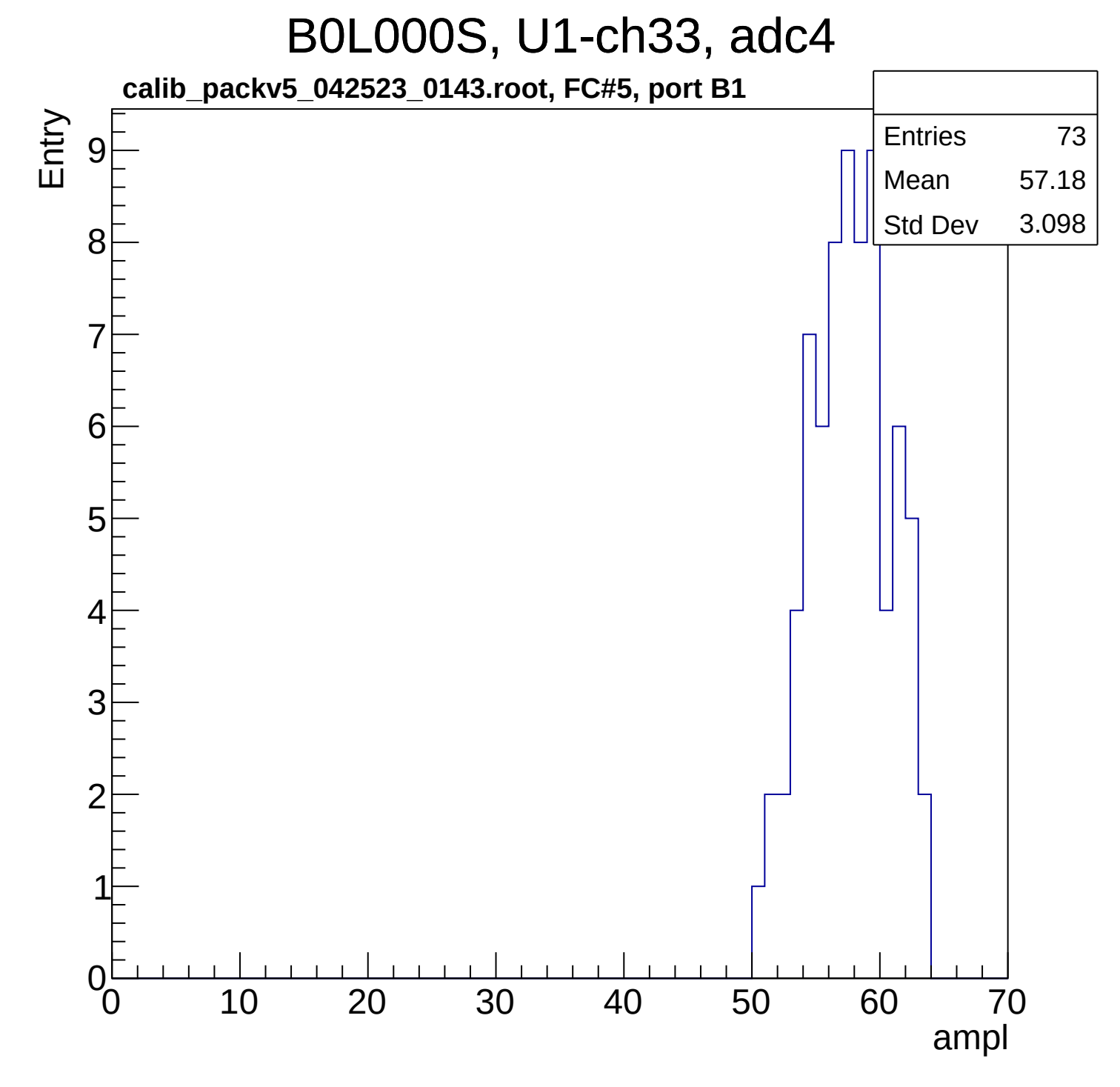
B0L000S, U1-ch33, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 57.18
Std Dev 3.098
Entry
ampl
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3
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60
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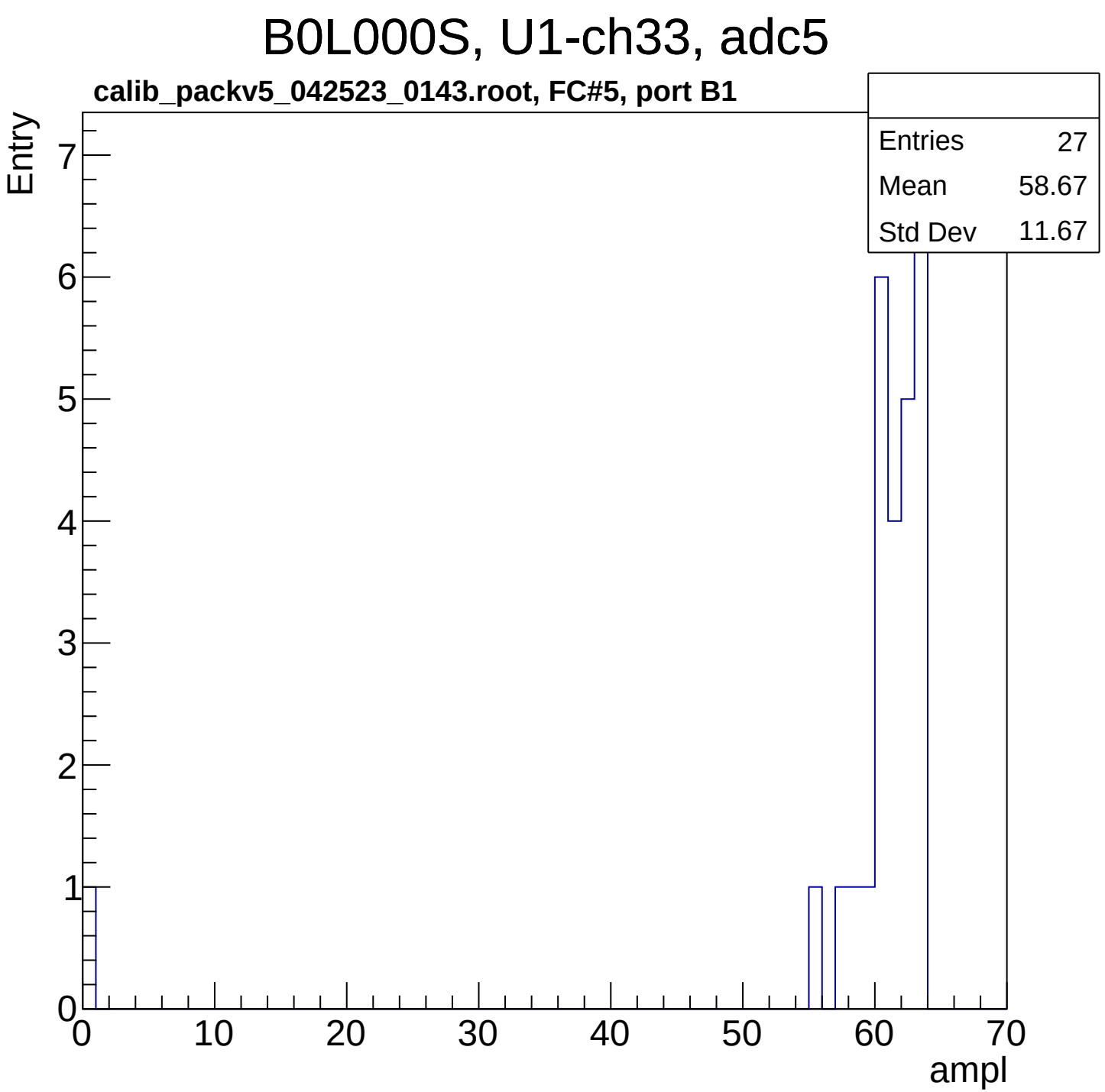
B0L000S, U1-ch33, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.67
Std Dev 11.67
Entry
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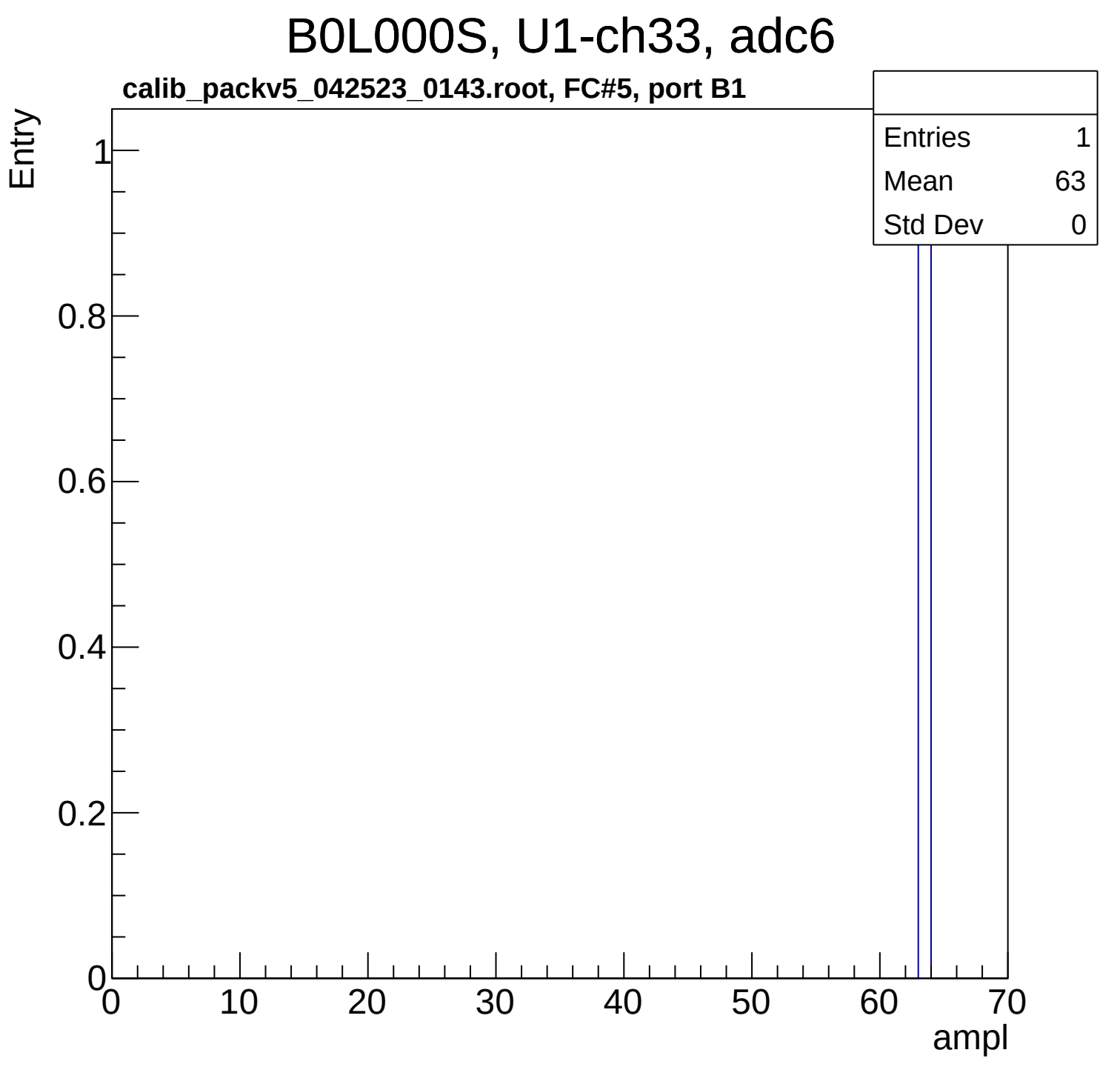
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch33, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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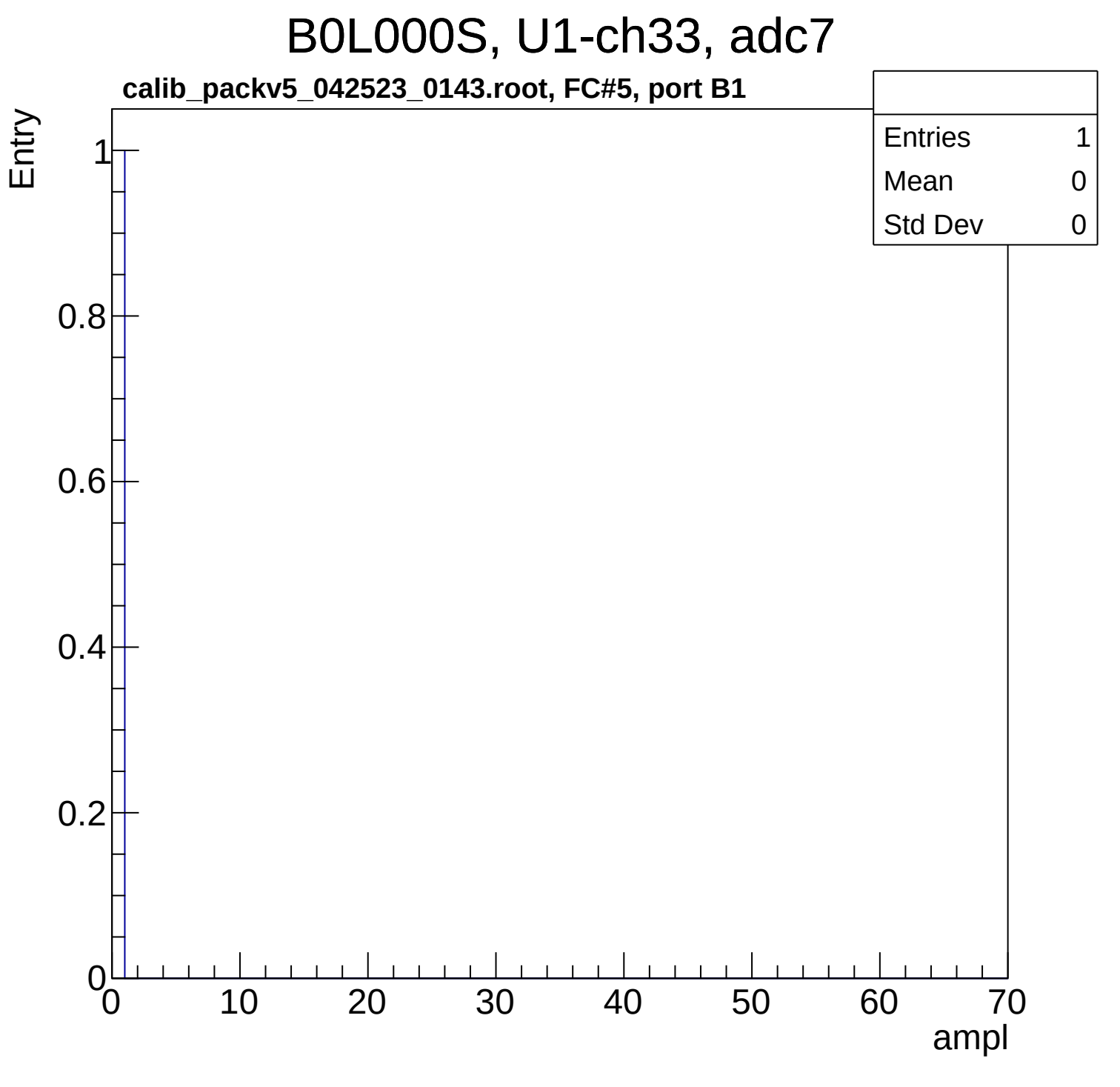
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch33, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
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10
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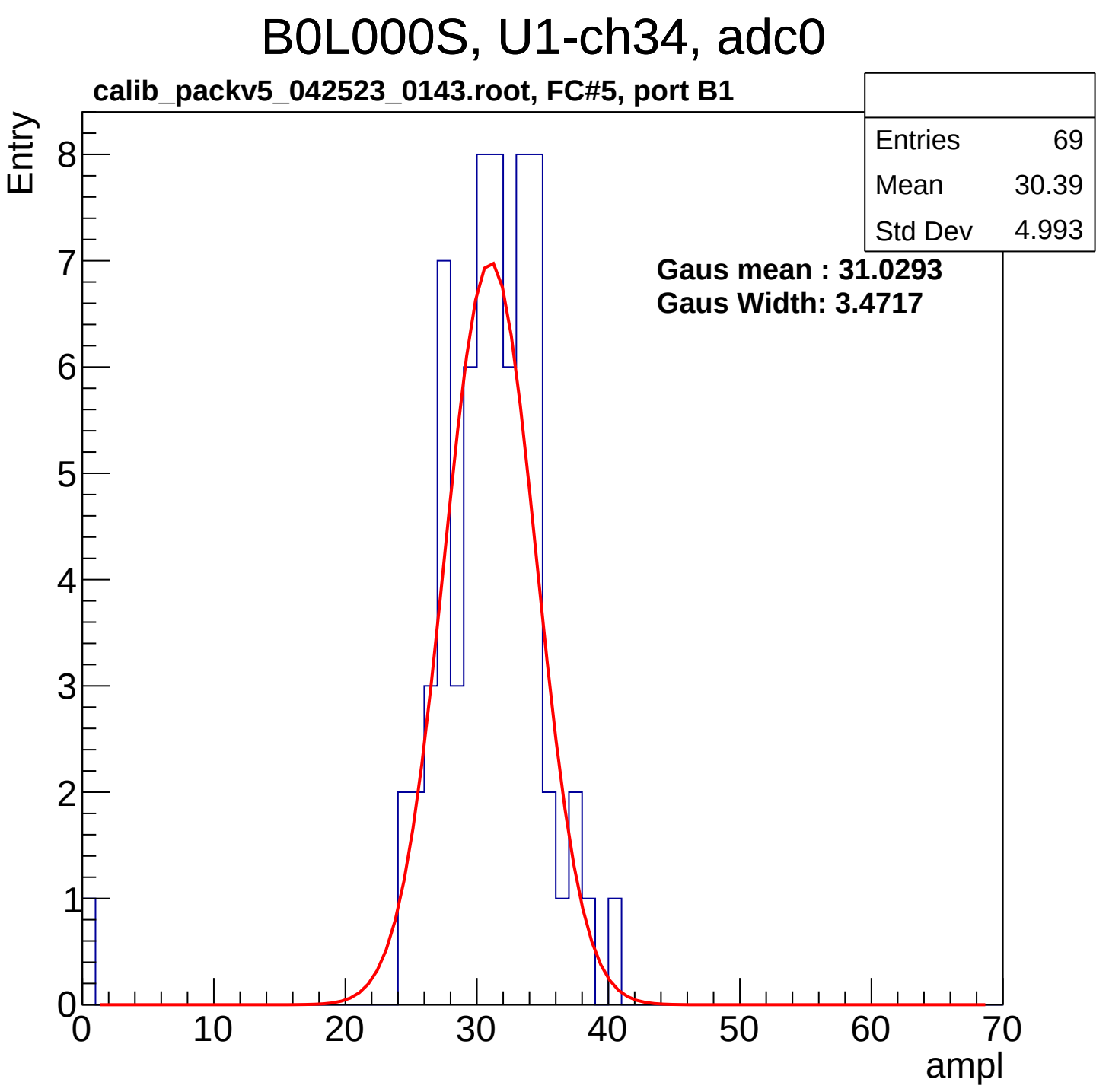
60
70
ampl


B0L000S, U1-ch34, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 30.39
Std Dev 4.993
Gaus mean : 31.0293
Gaus Width: 3.4717
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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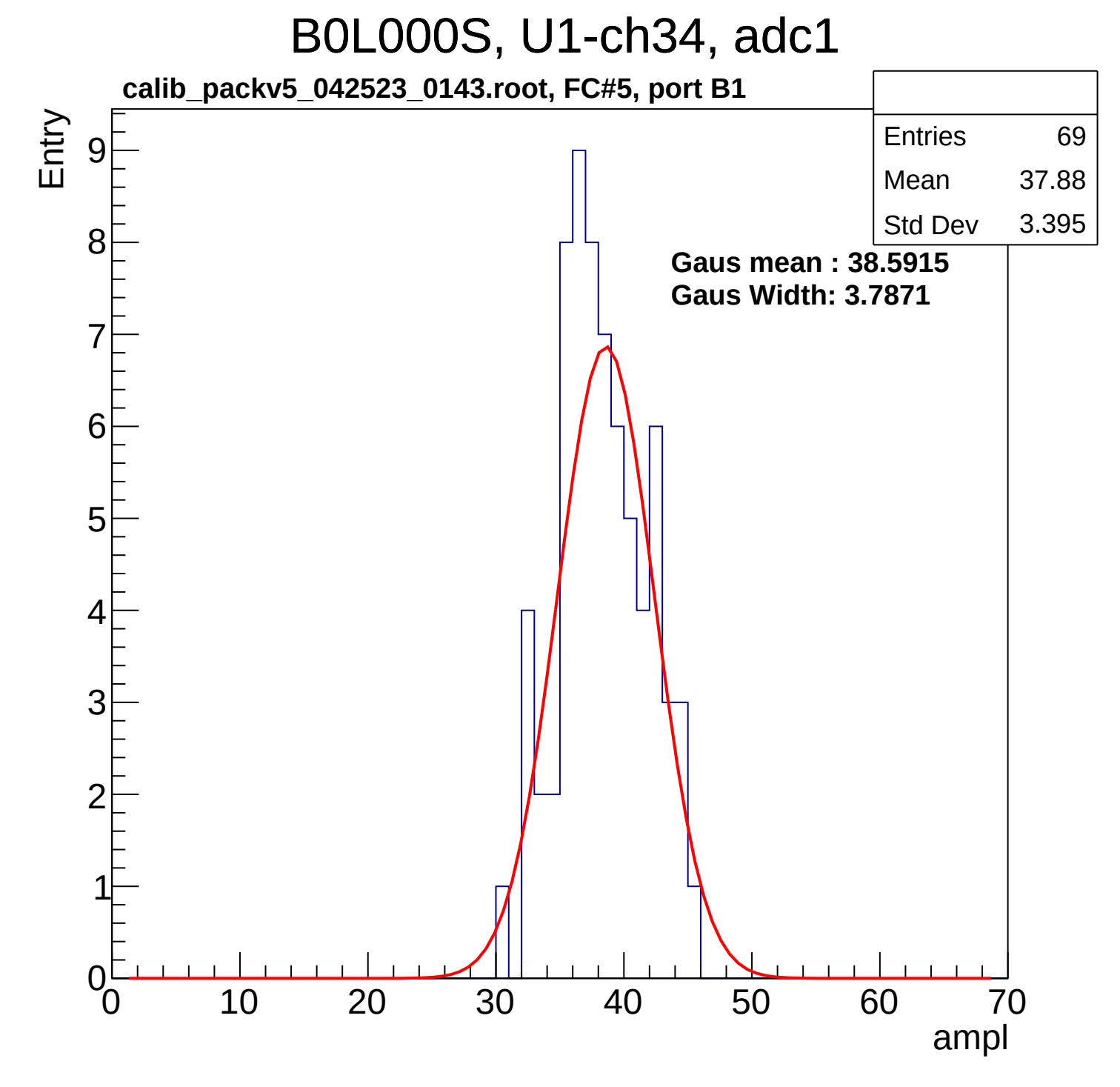
60
70


B0L000S, U1-ch34, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 37.88
Std Dev 3.395
Gaus mean : 38.5915
Gaus Width: 3.7871
Entry
ampl
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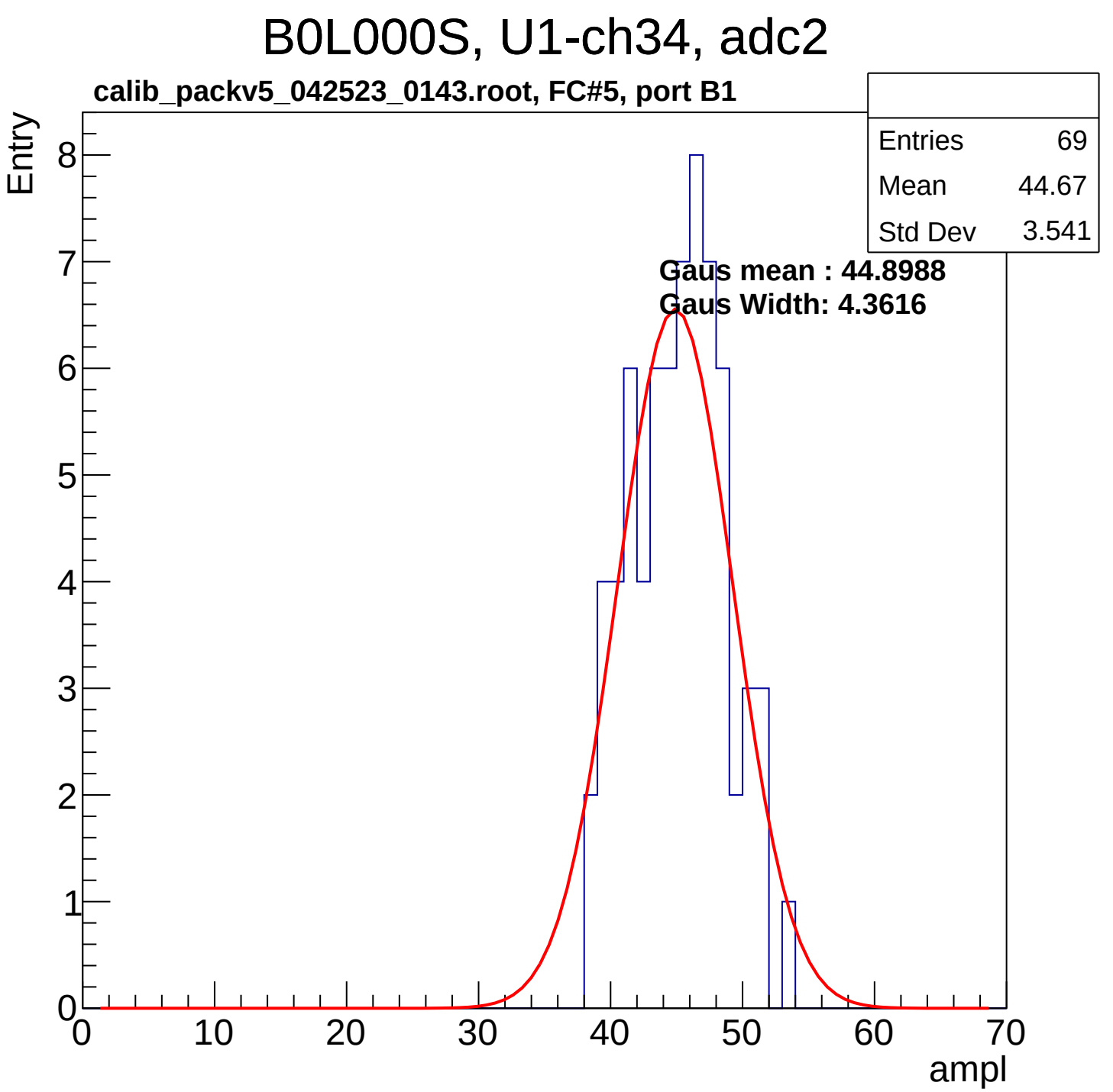
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch34, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.67
Std Dev 3.541
Gaus mean : 44.8988
Gaus Width: 4.3616
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
50
60
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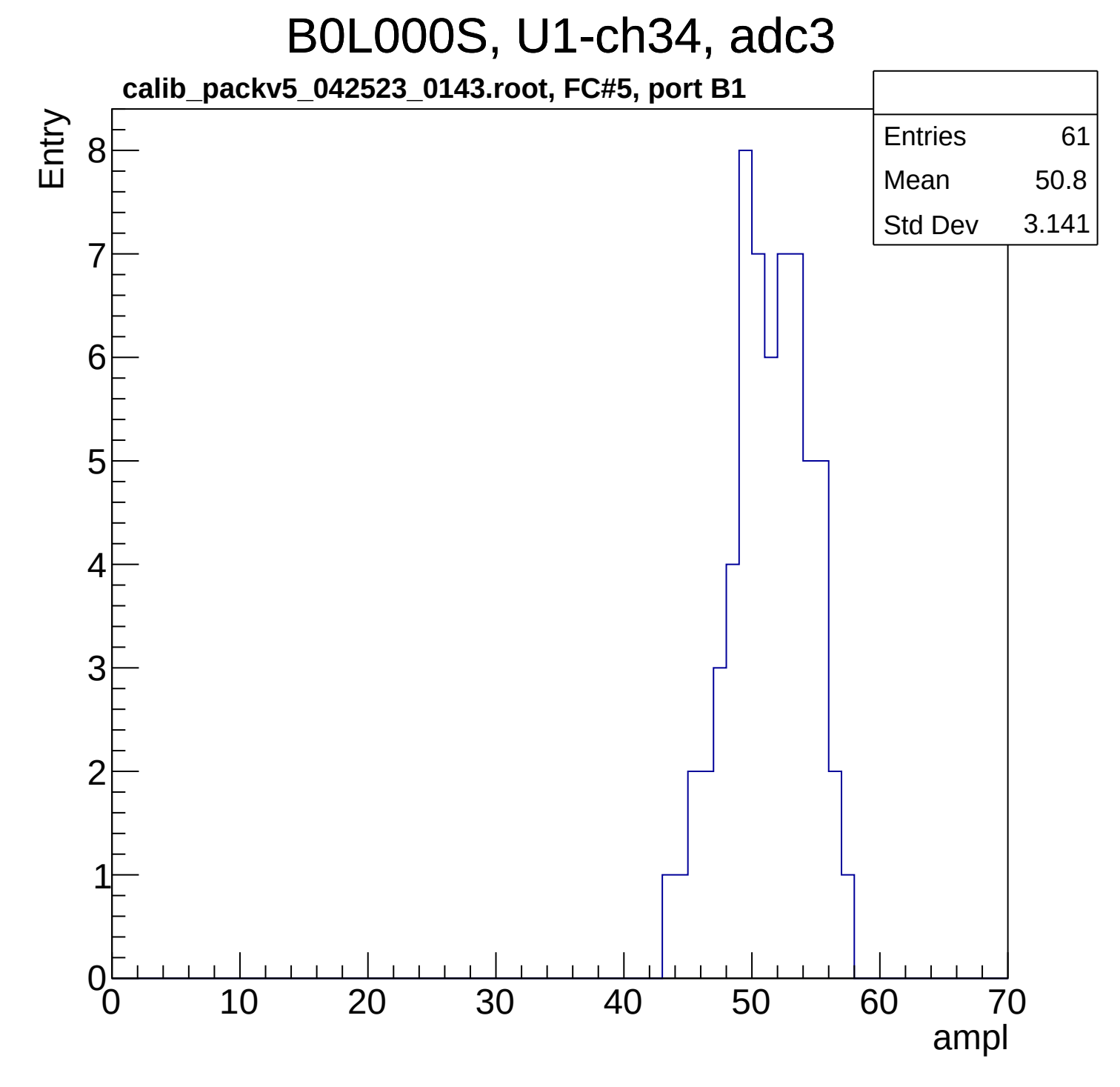
B0L000S, U1-ch34, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.8
Std Dev 3.141
Entry
ampl
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2
3
4
5
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0
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60
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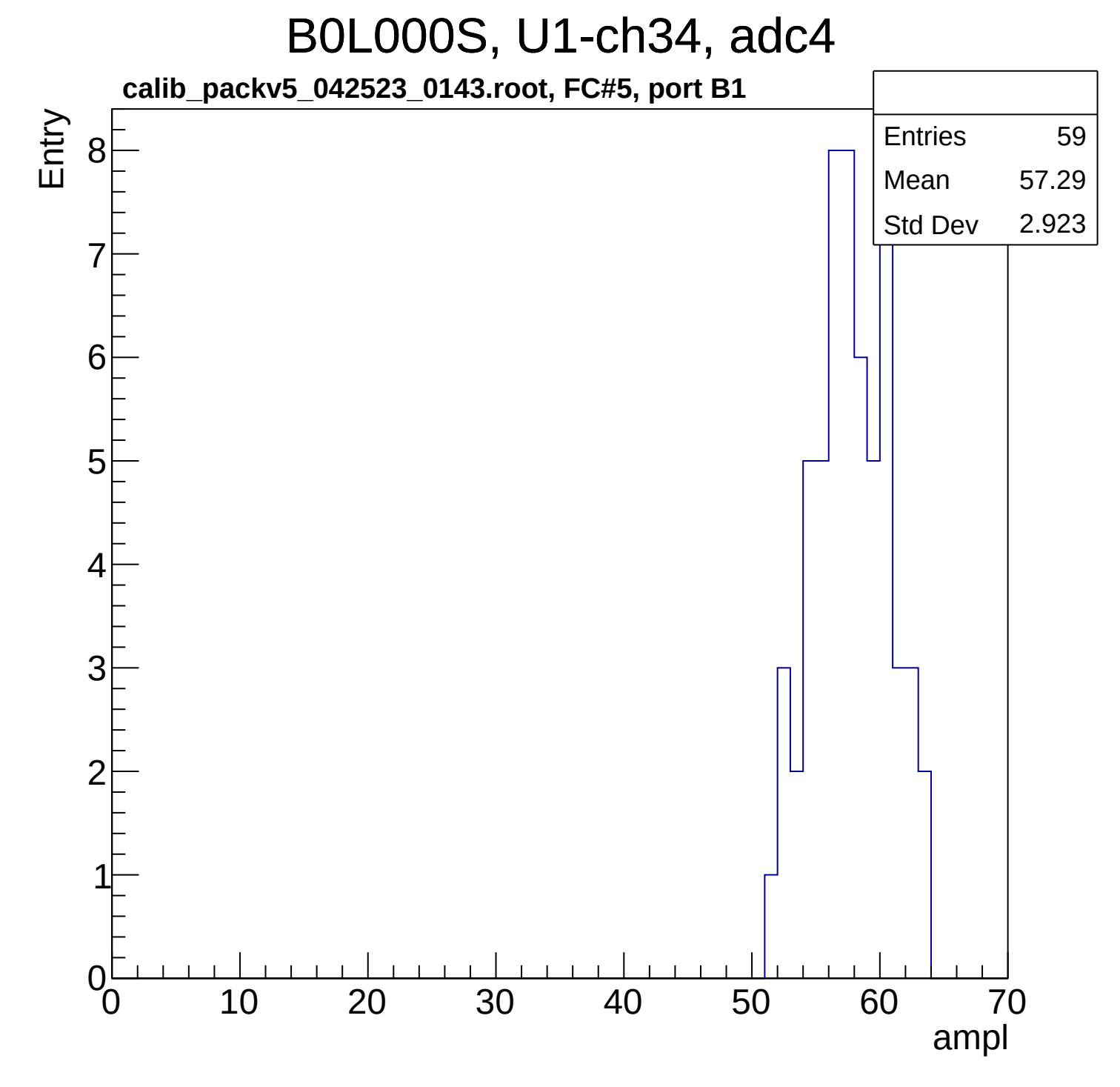
B0L000S, U1-ch34, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.29
Std Dev 2.923
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
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60
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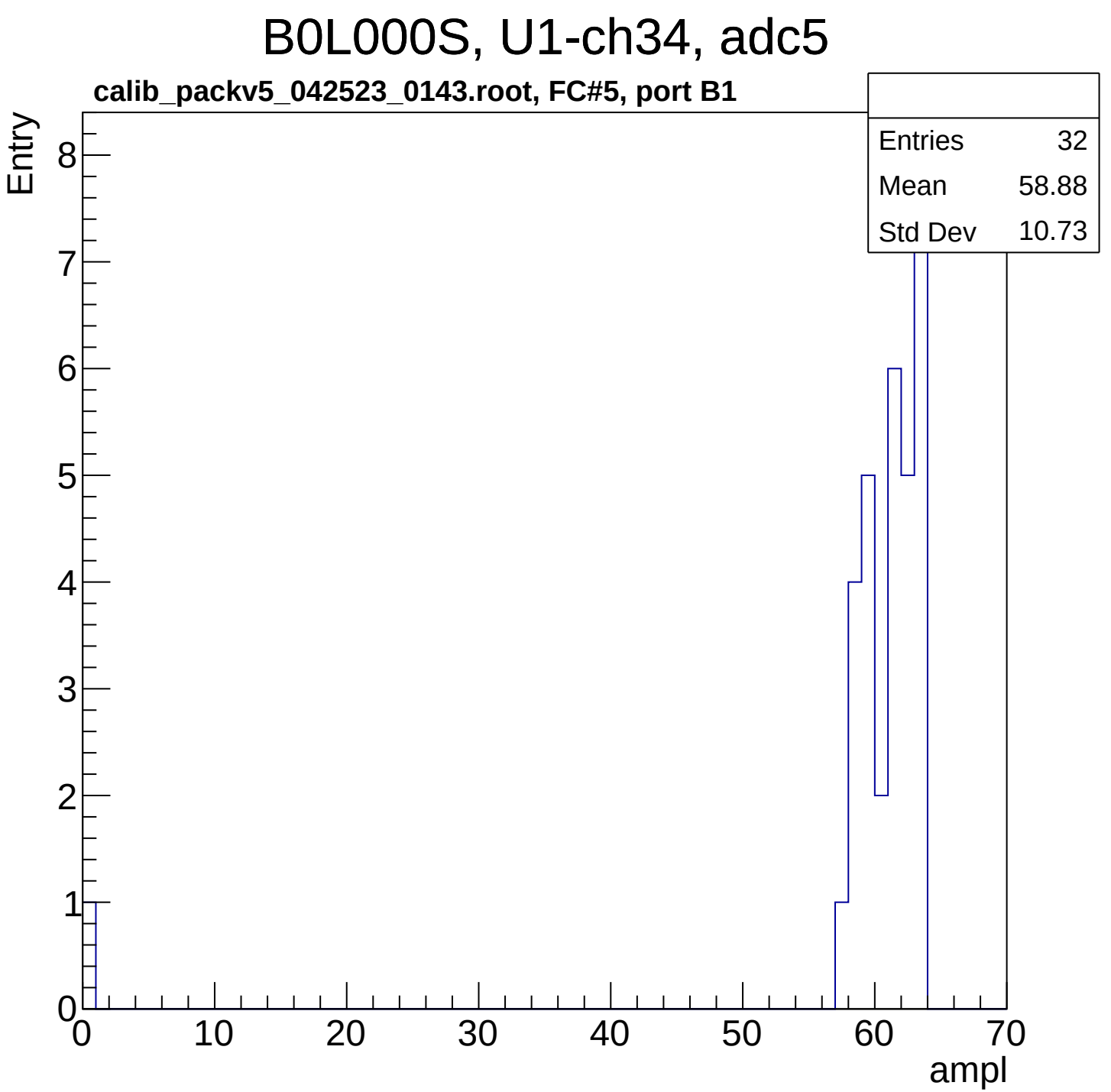
B0L000S, U1-ch34, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.88
Std Dev 10.73
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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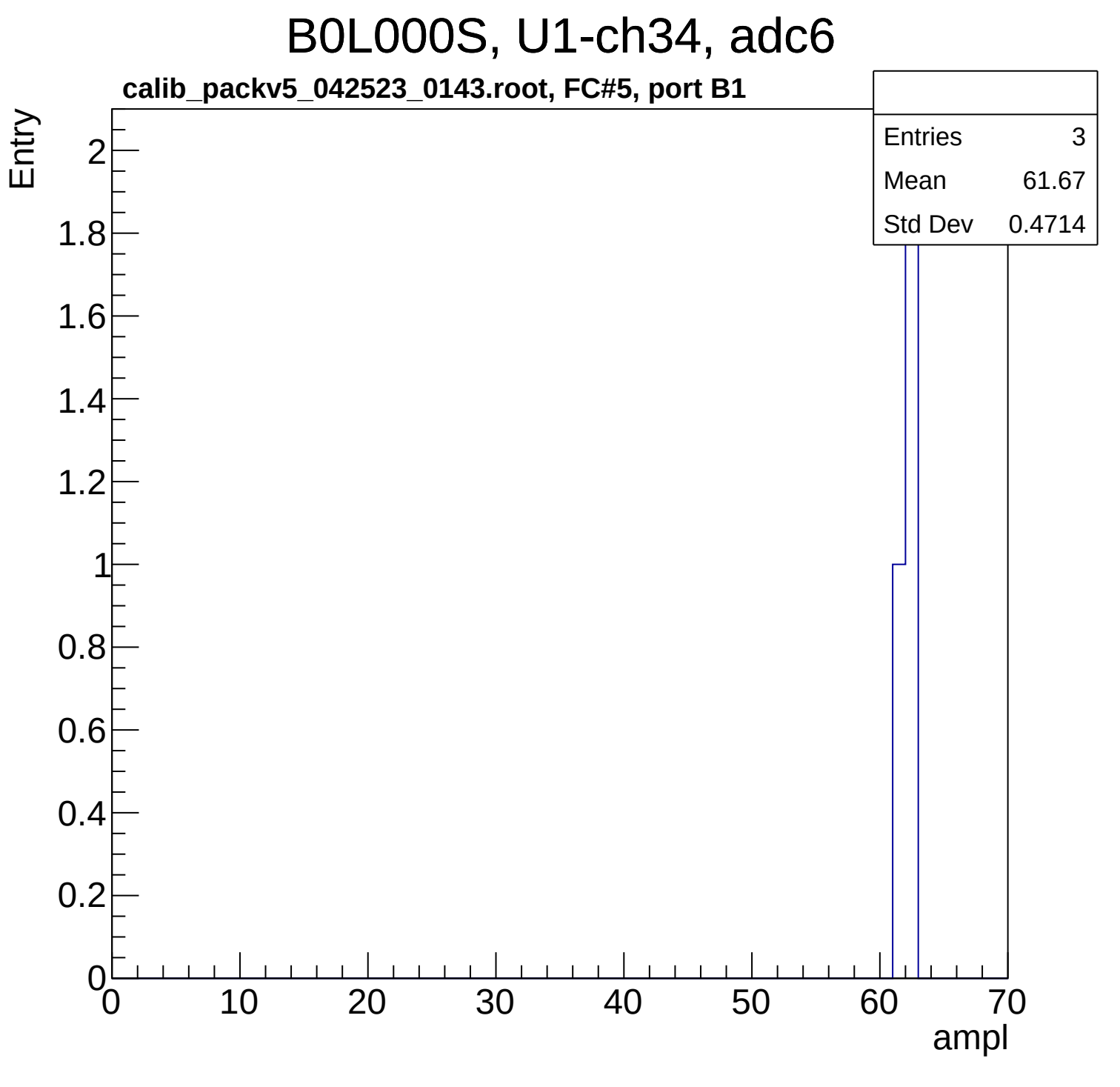
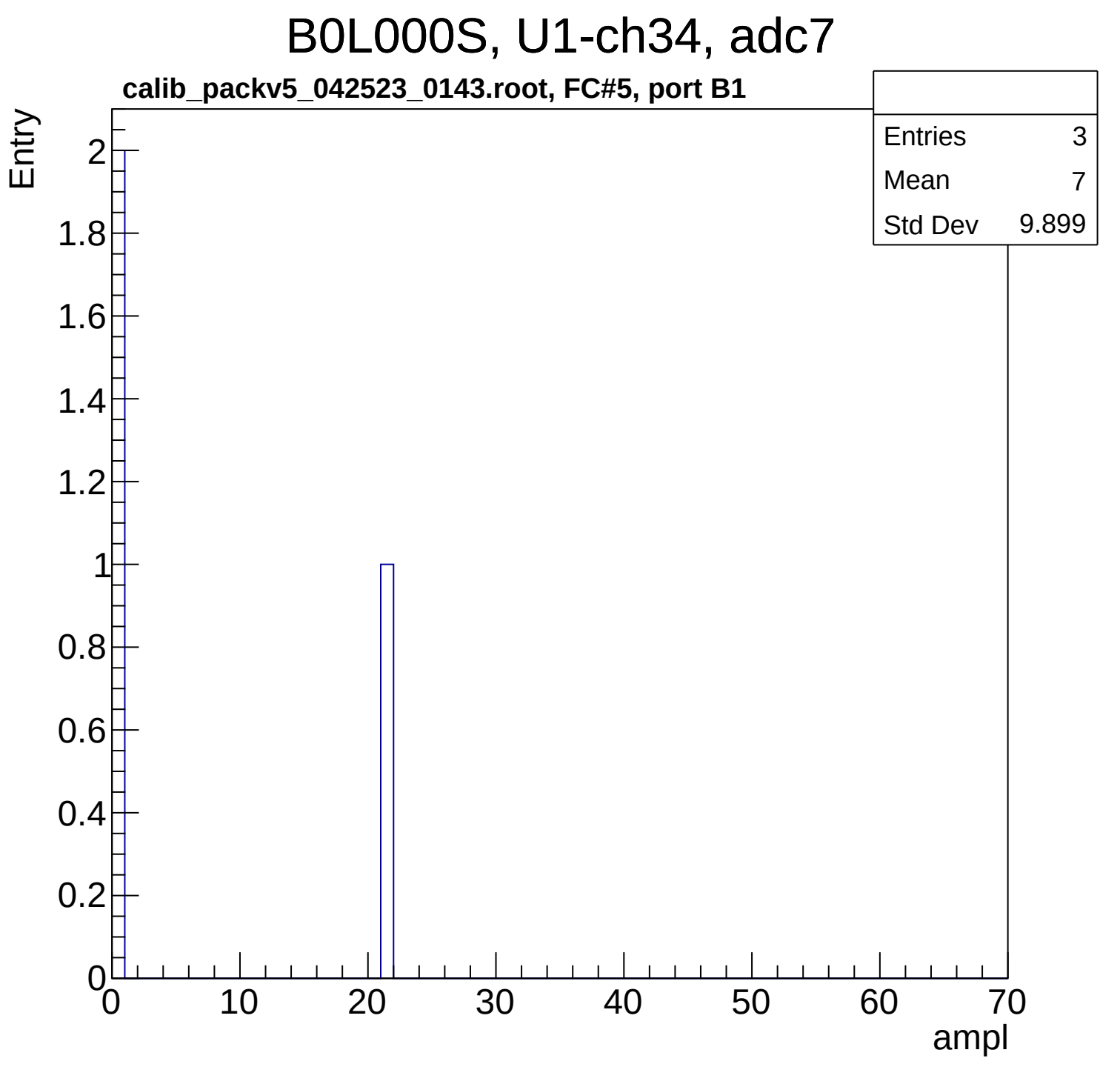

B0L000S, U1-ch34, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch34, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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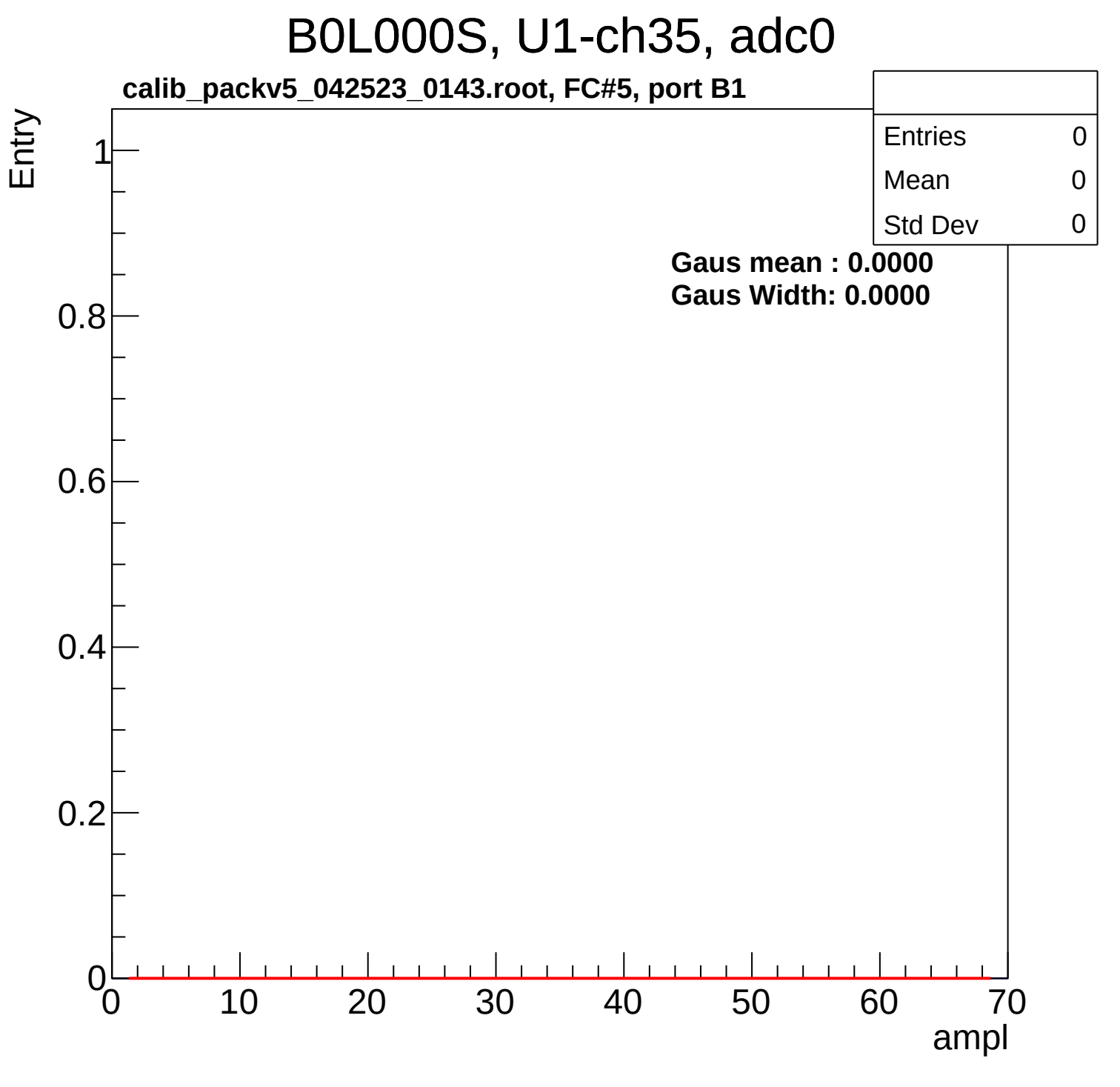
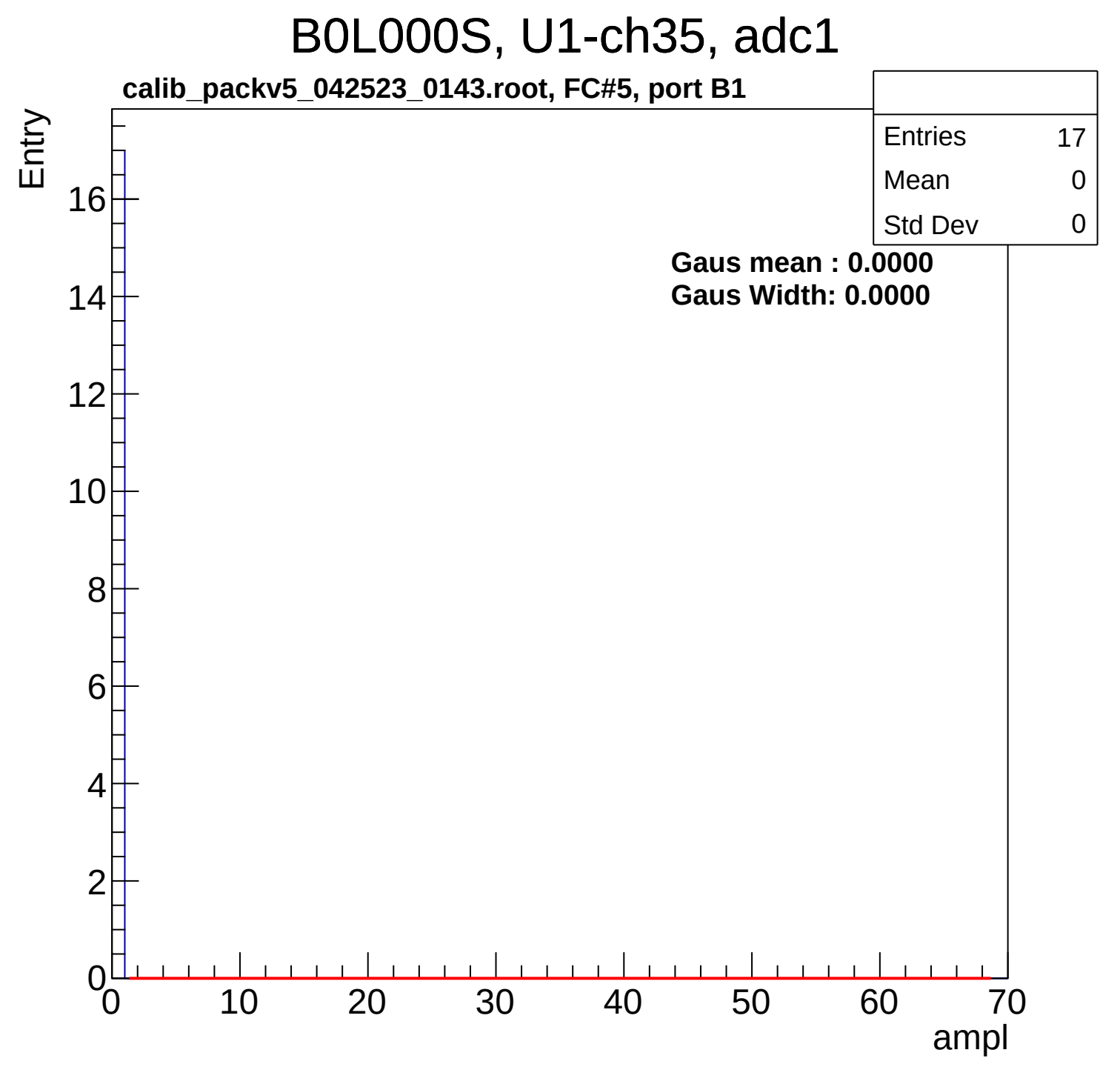
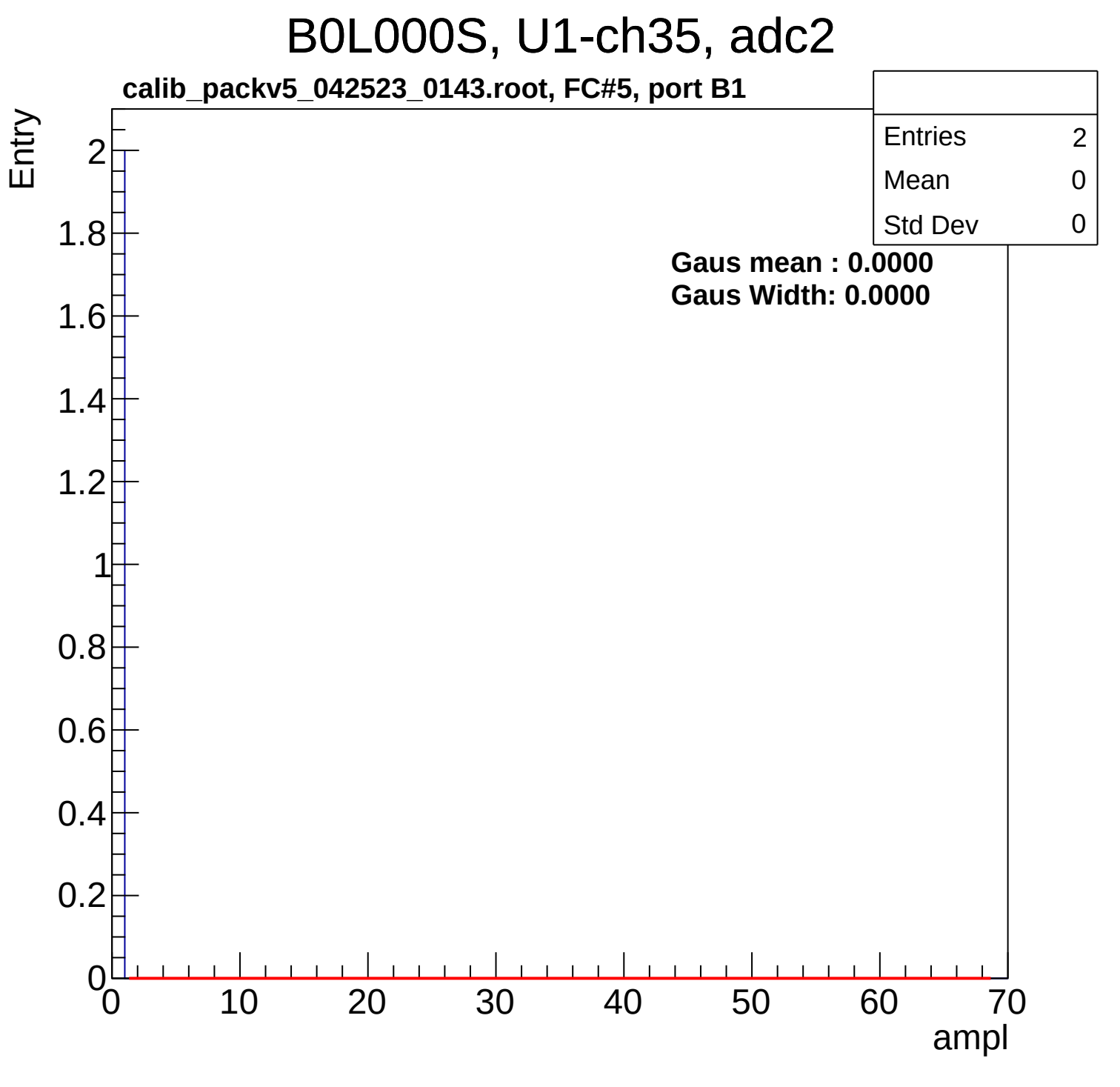
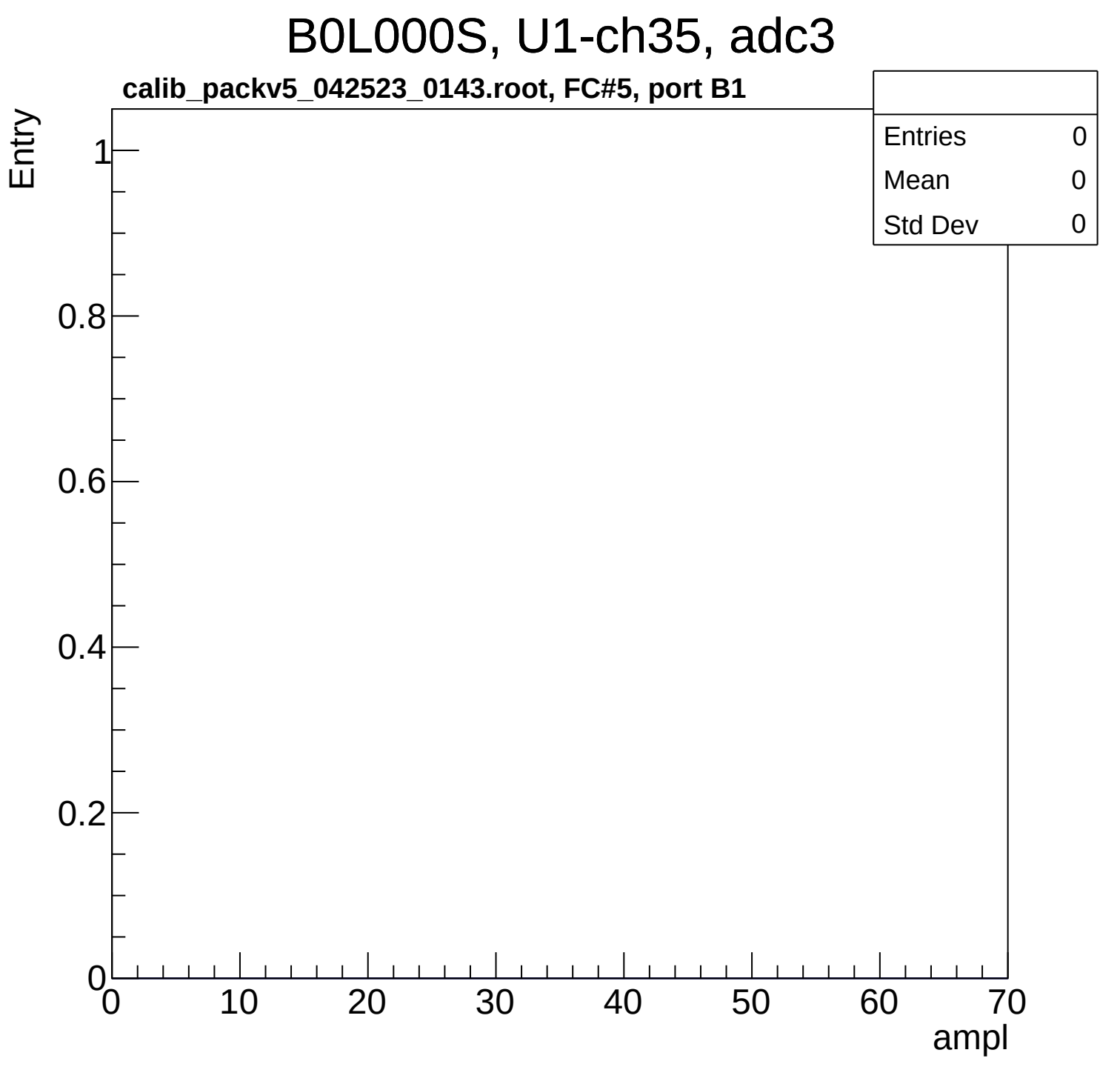
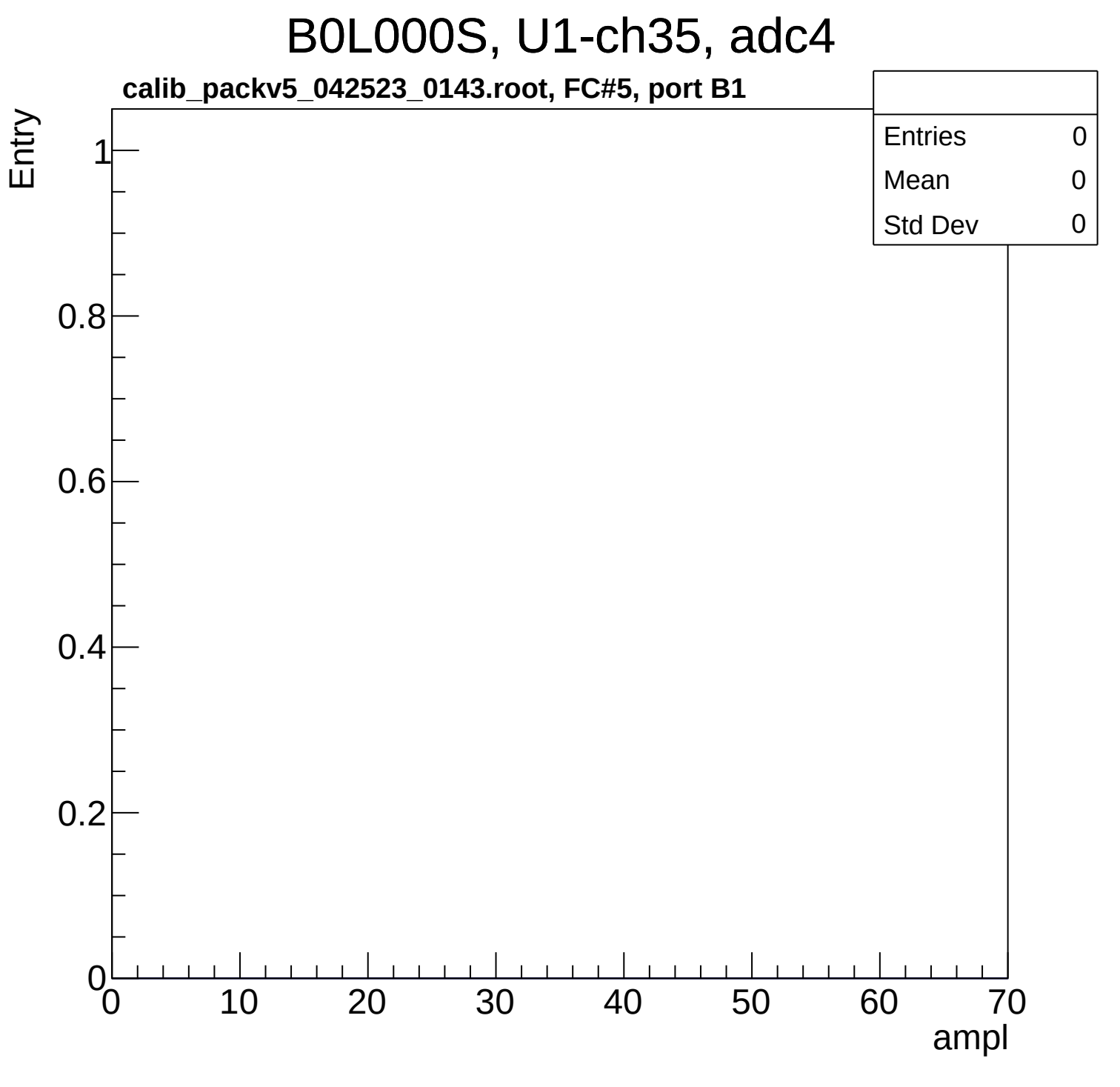
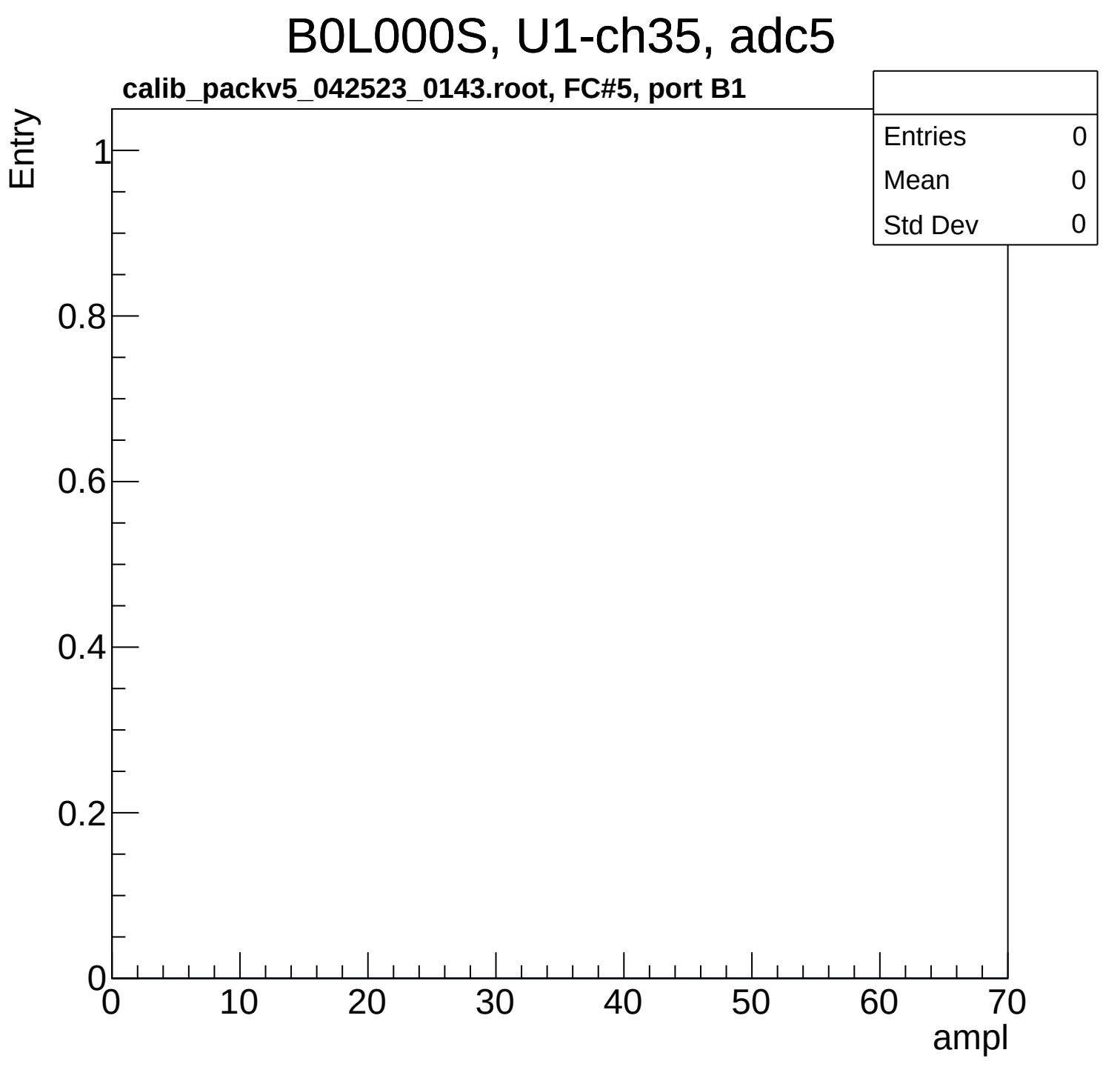

B0L000S, U1-ch35, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

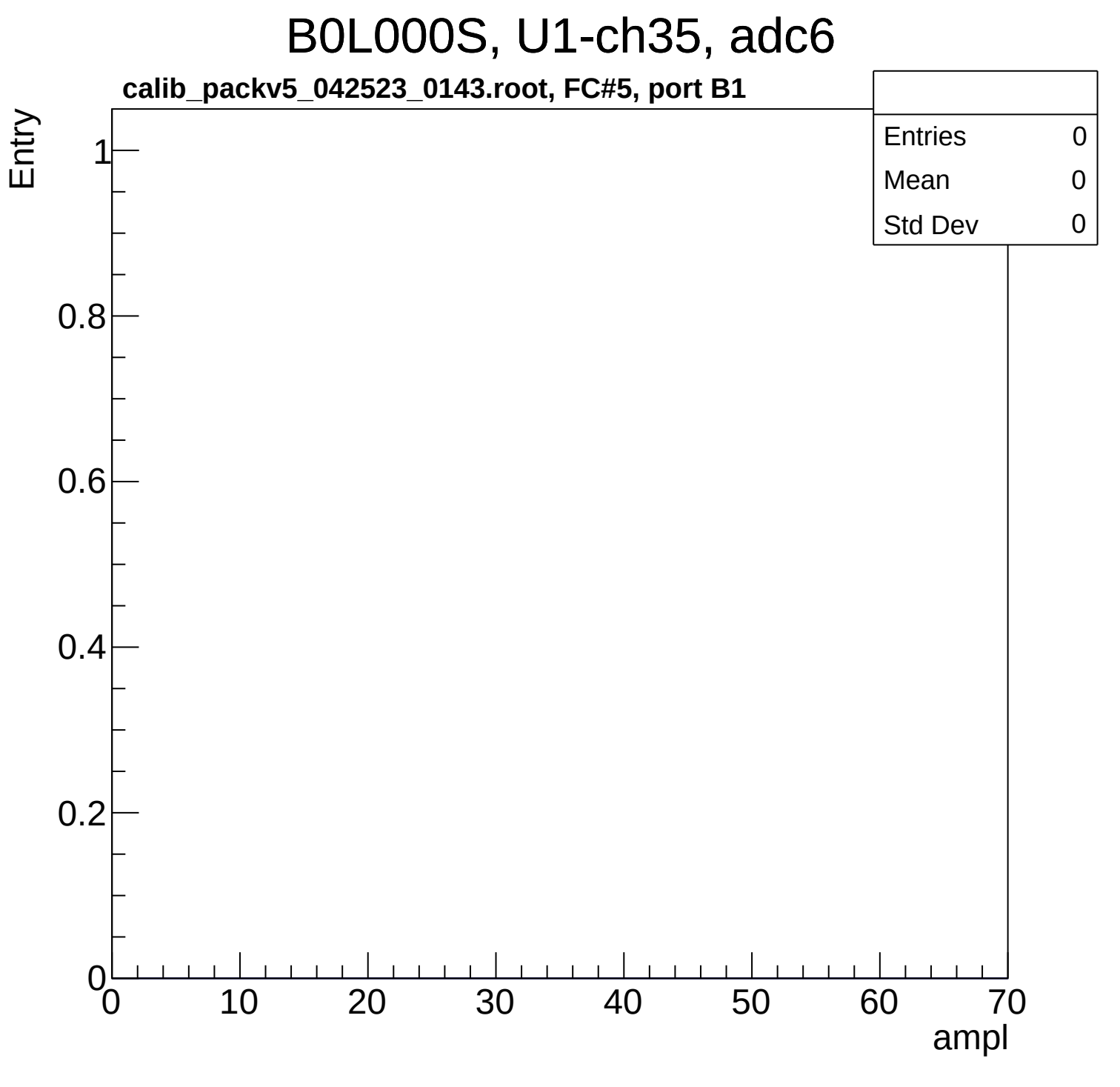

B0L000S, U1-ch35, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

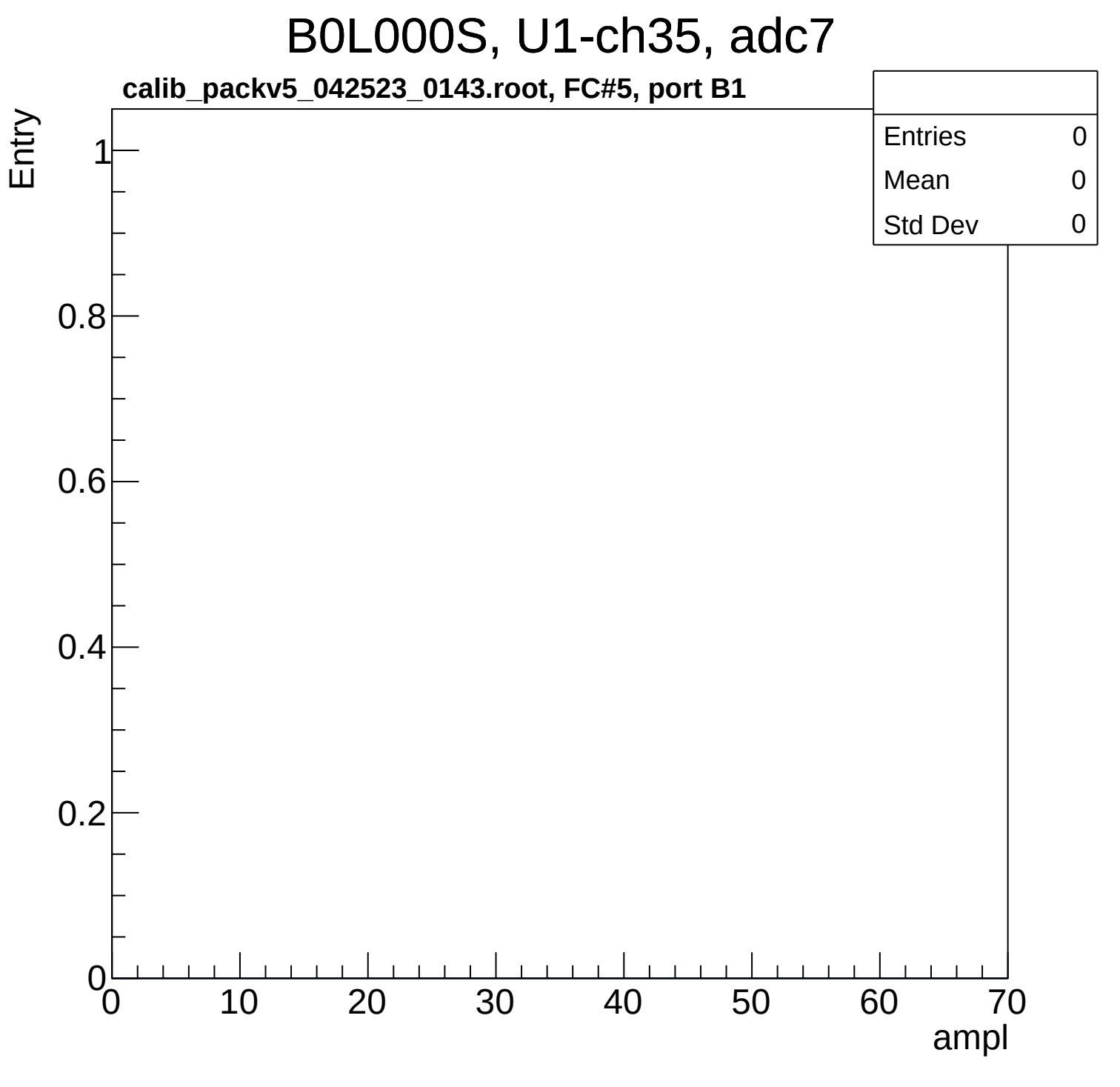

B0L000S, U1-ch35, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

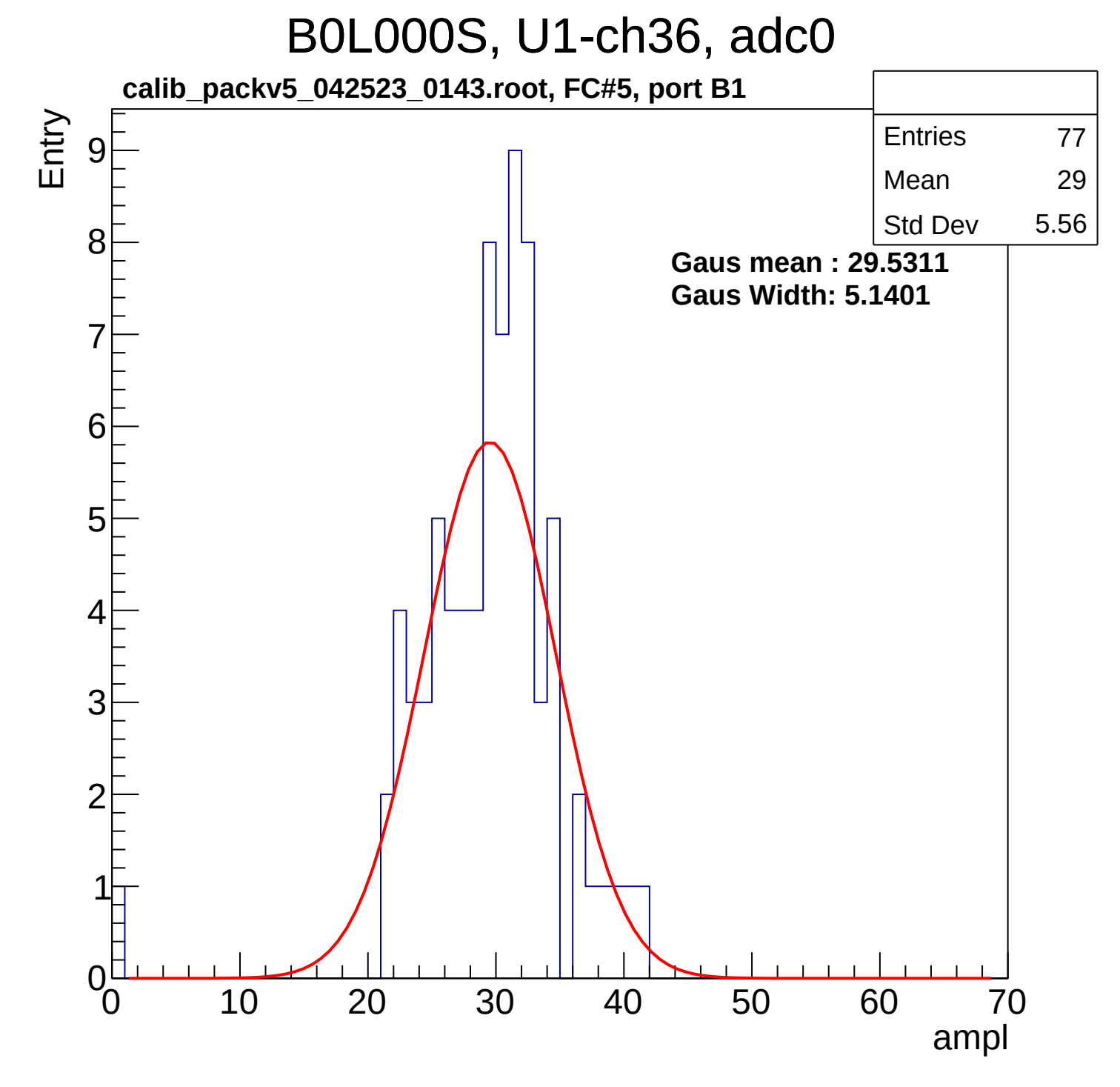

B0L000S, U1-ch35, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

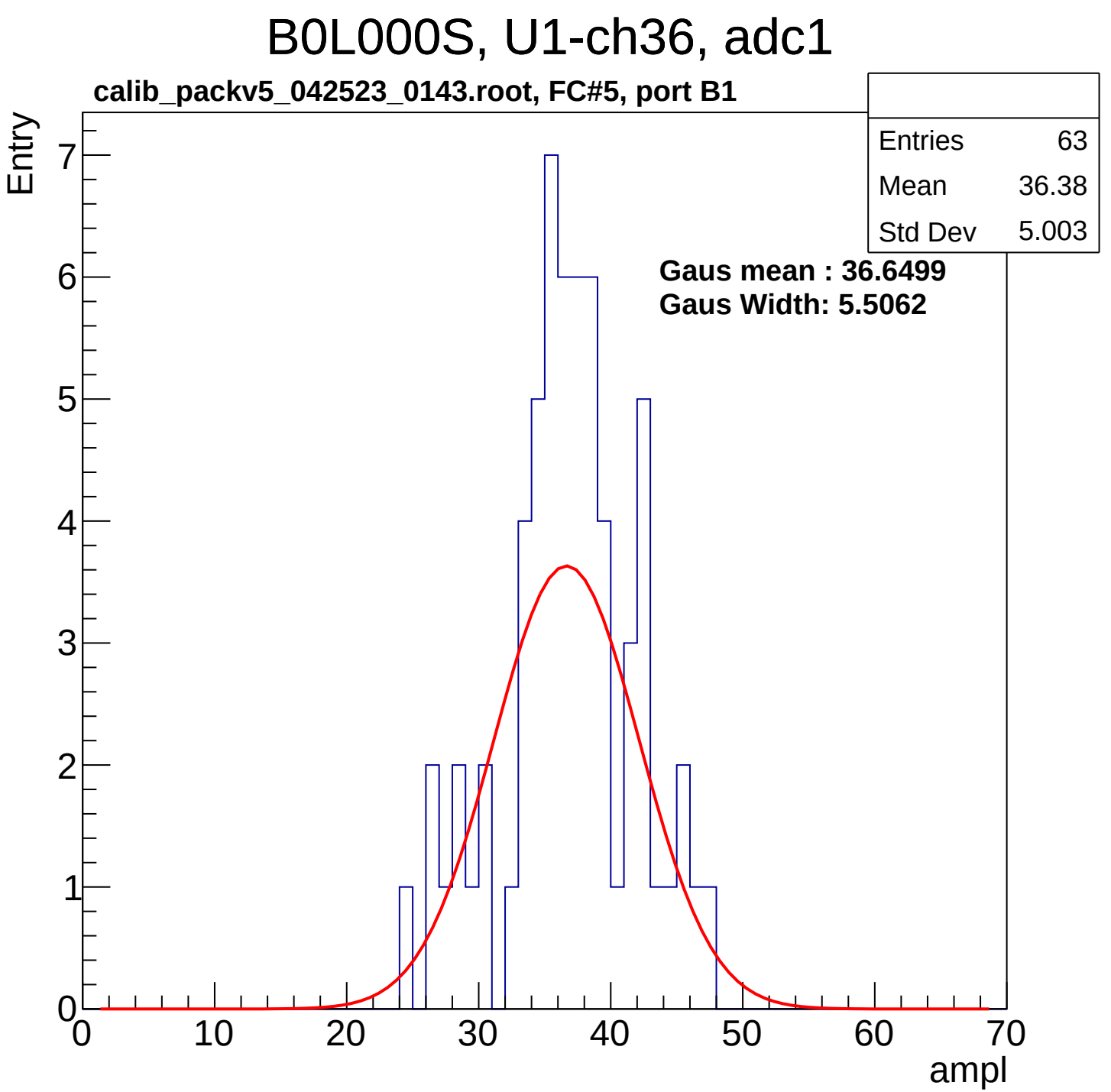

B0L000S, U1-ch35, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch35, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch35, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch35, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch36, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29
Std Dev 5.56
Gaus mean : 29.5311
Gaus Width: 5.1401
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch36, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.38
Std Dev 5.003
Gaus mean : 36.6499
Gaus Width: 5.5062
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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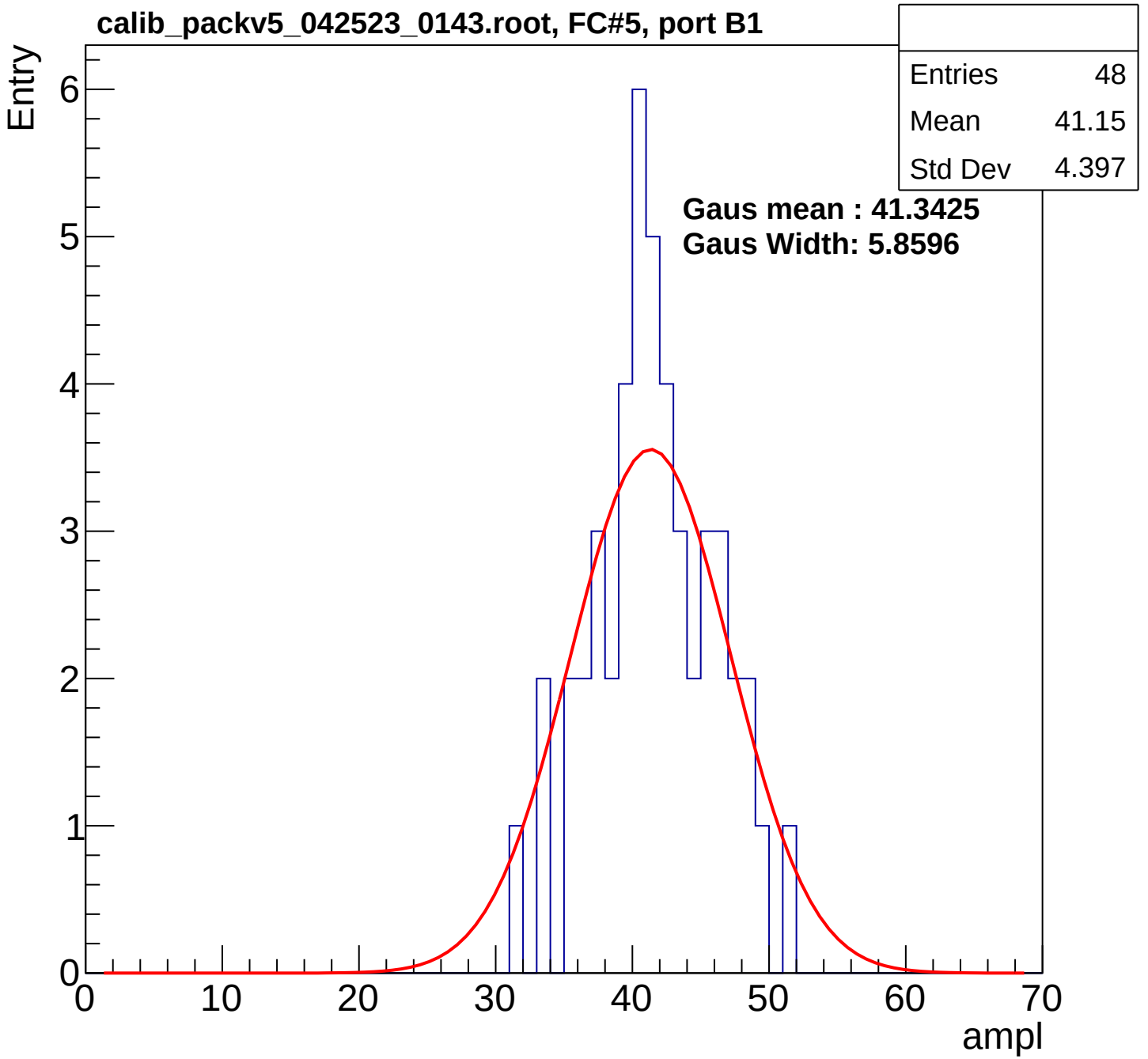
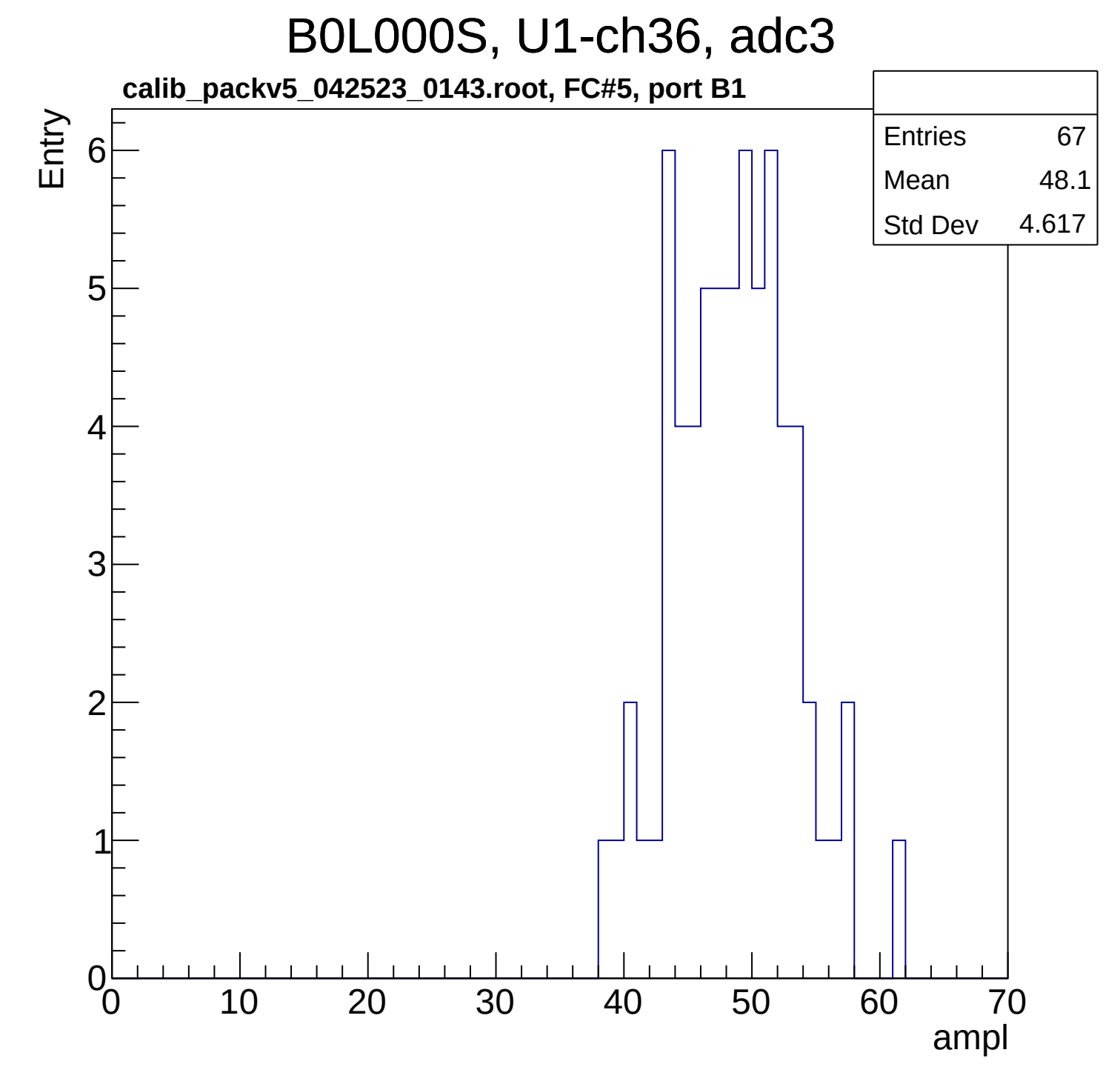
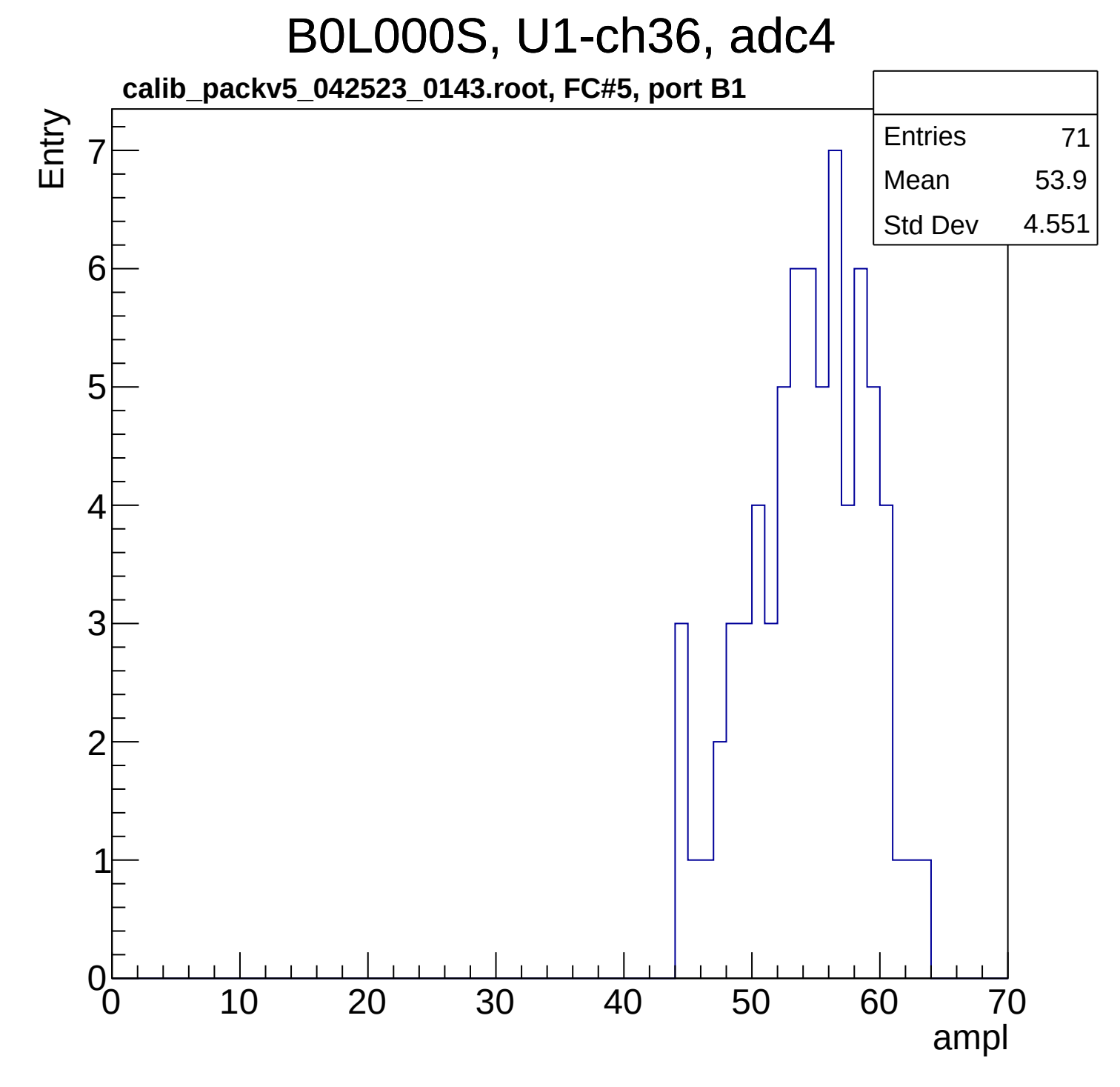
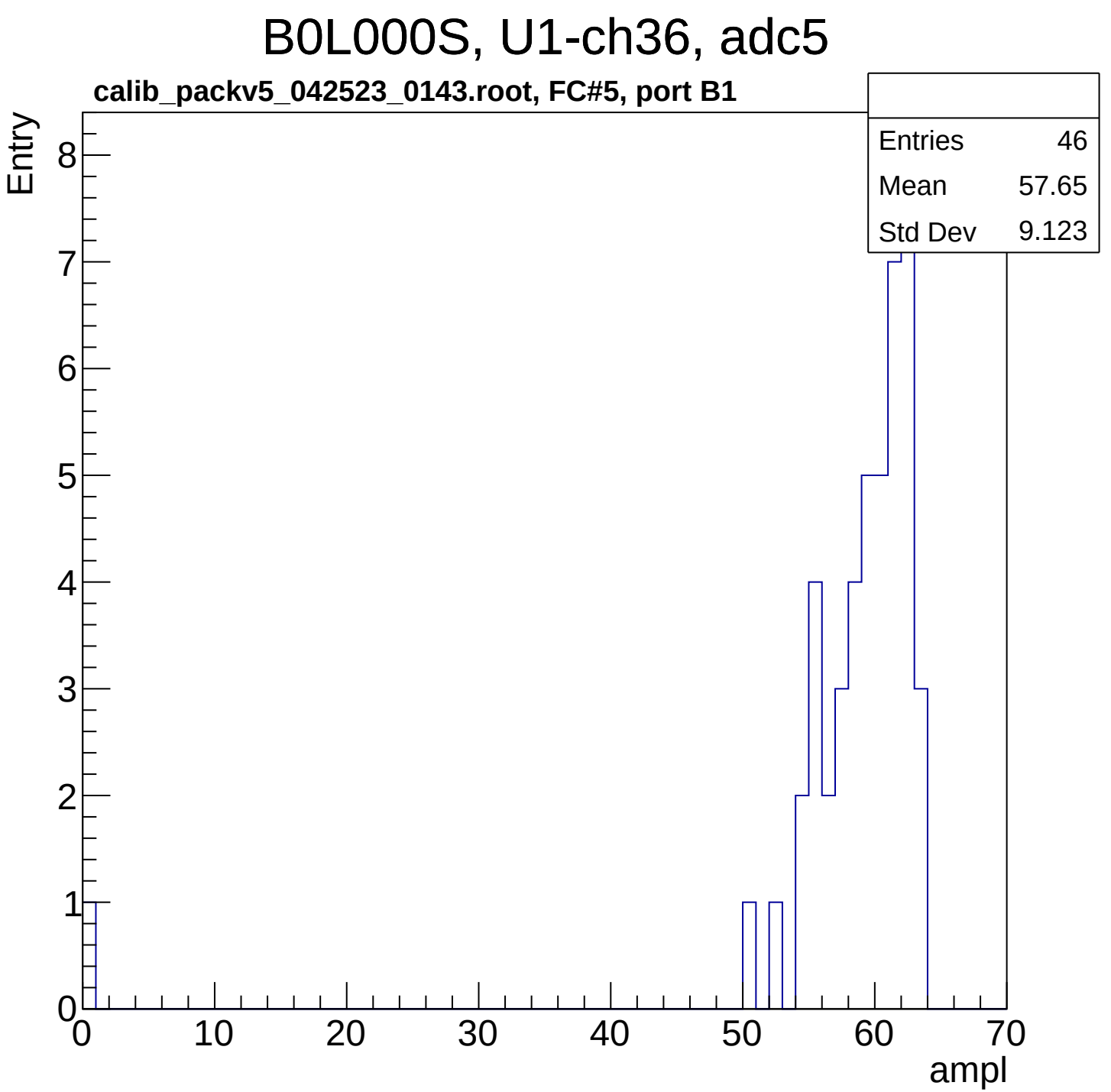
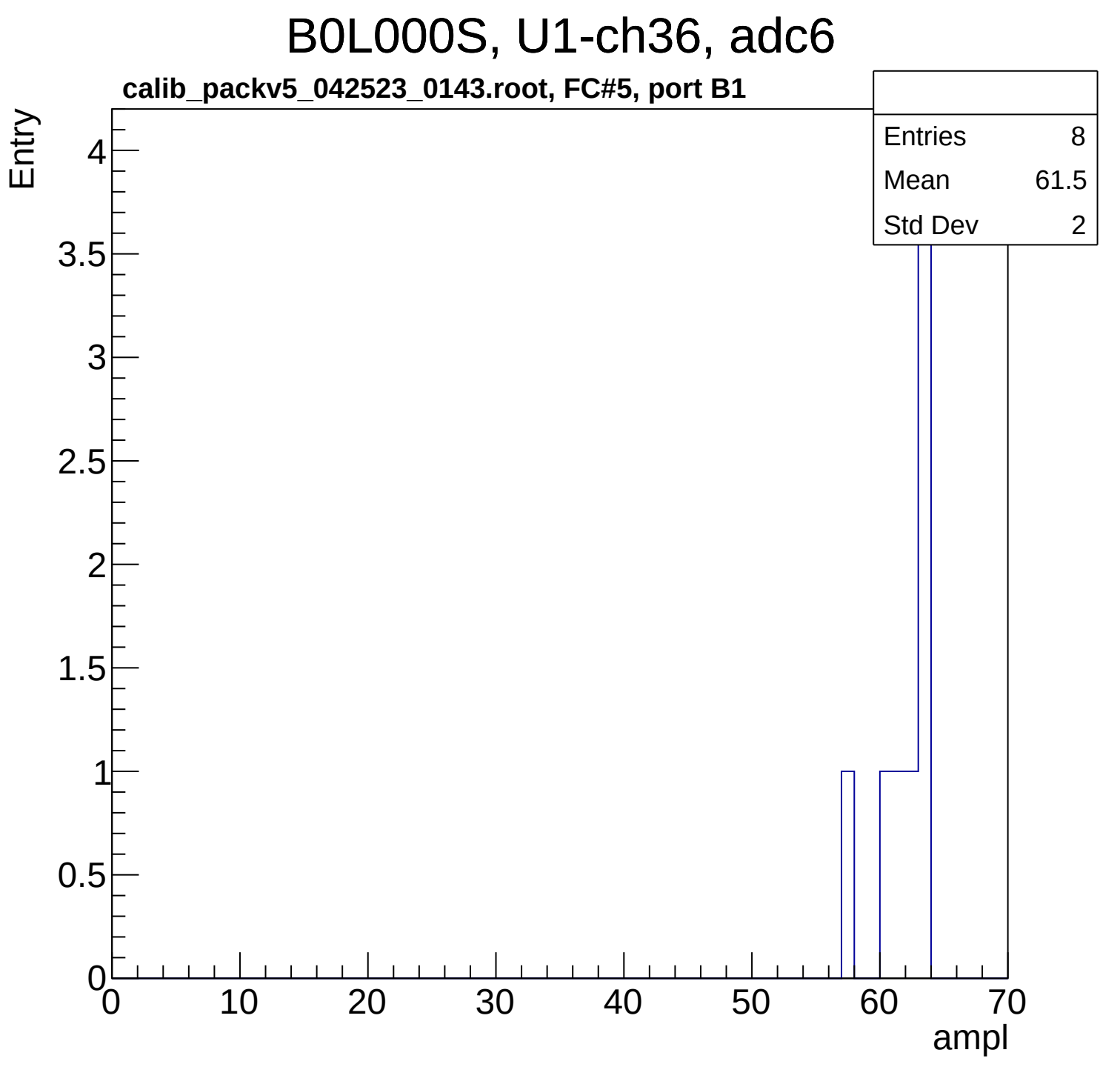
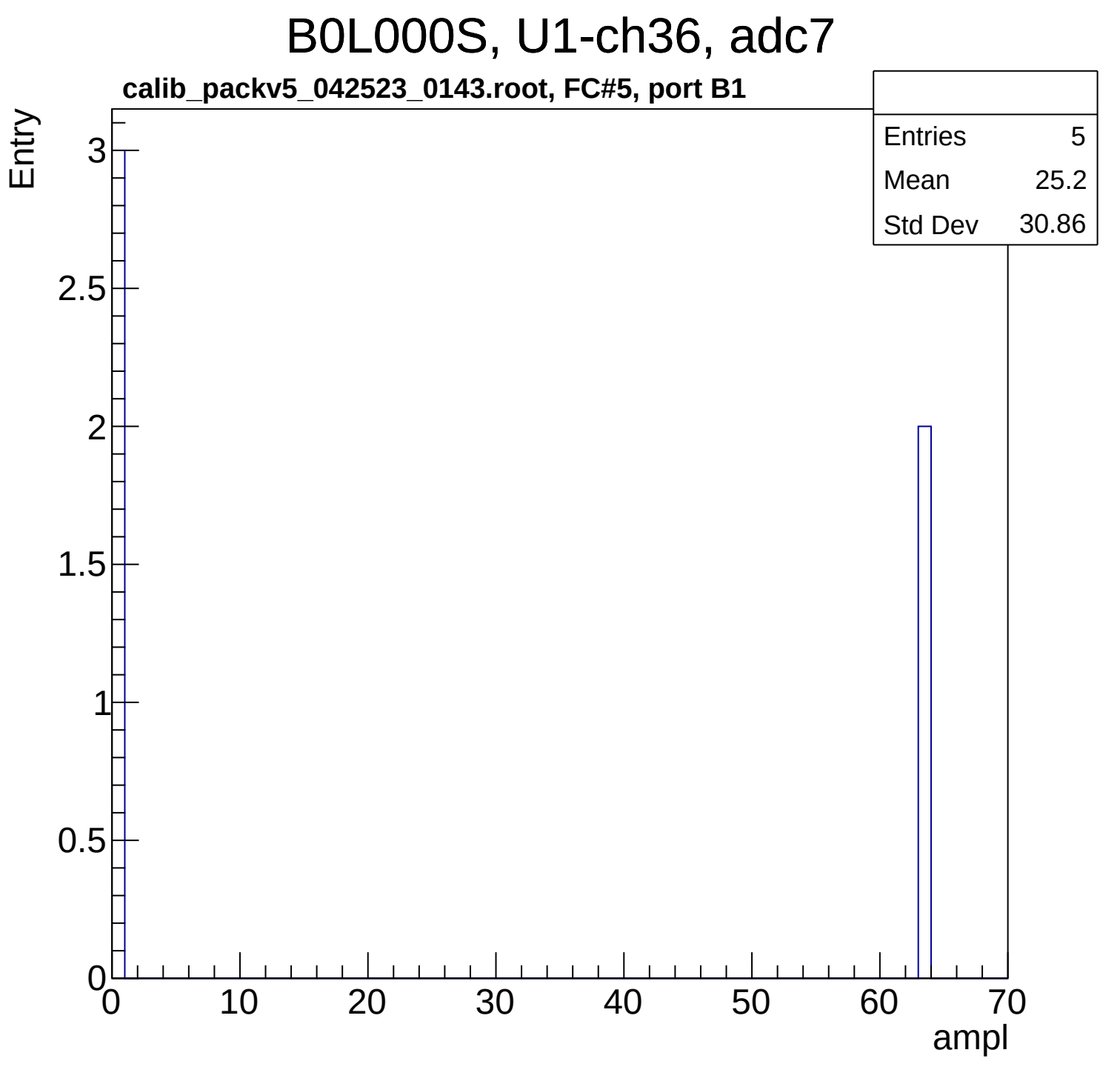

B0L000S, U1-ch36, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 41.15
Std Dev 4.397
Gaus mean : 41.3425
Gaus Width: 5.8596
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

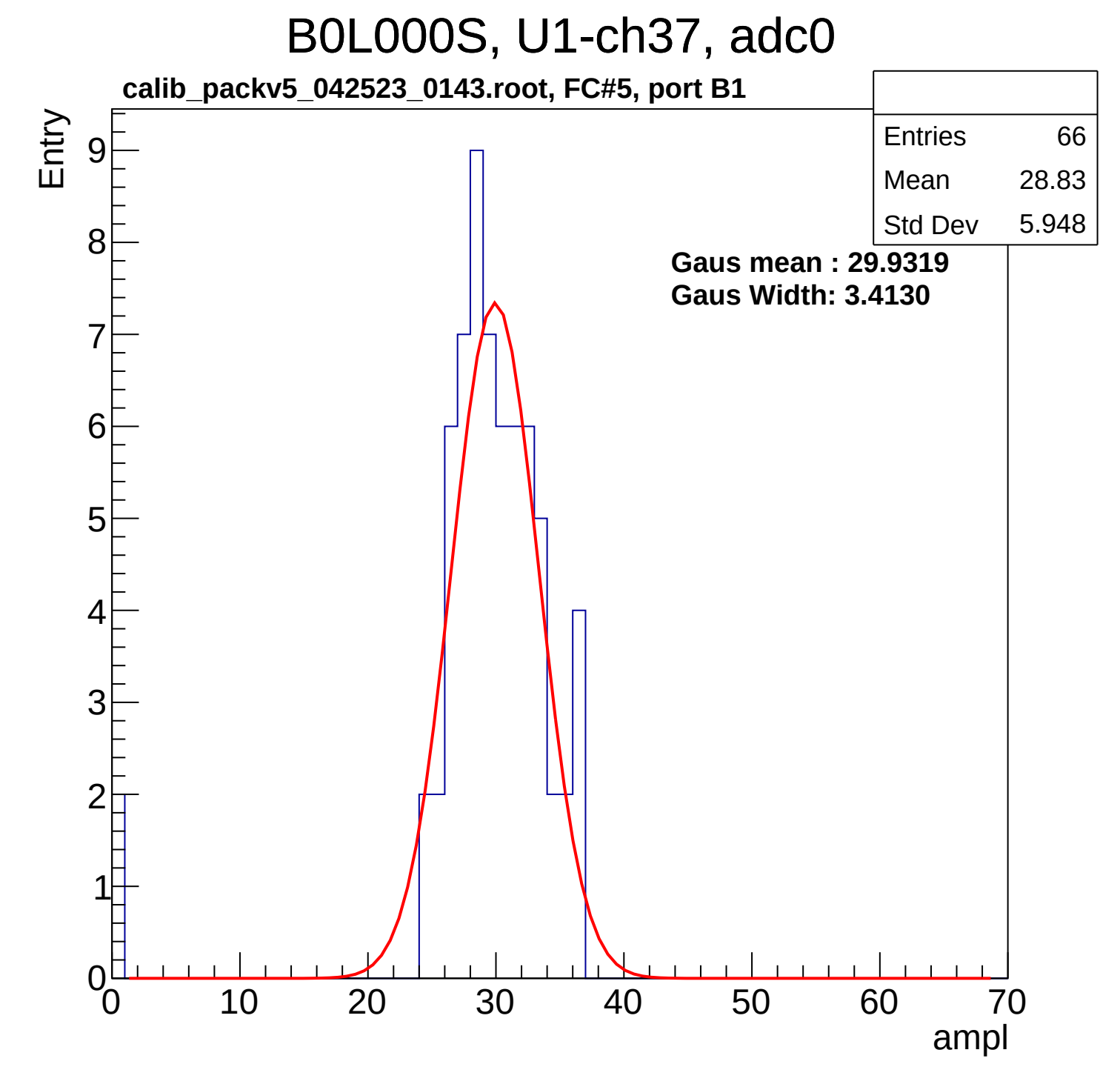

B0L000S, U1-ch36, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.1
Std Dev 4.617
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

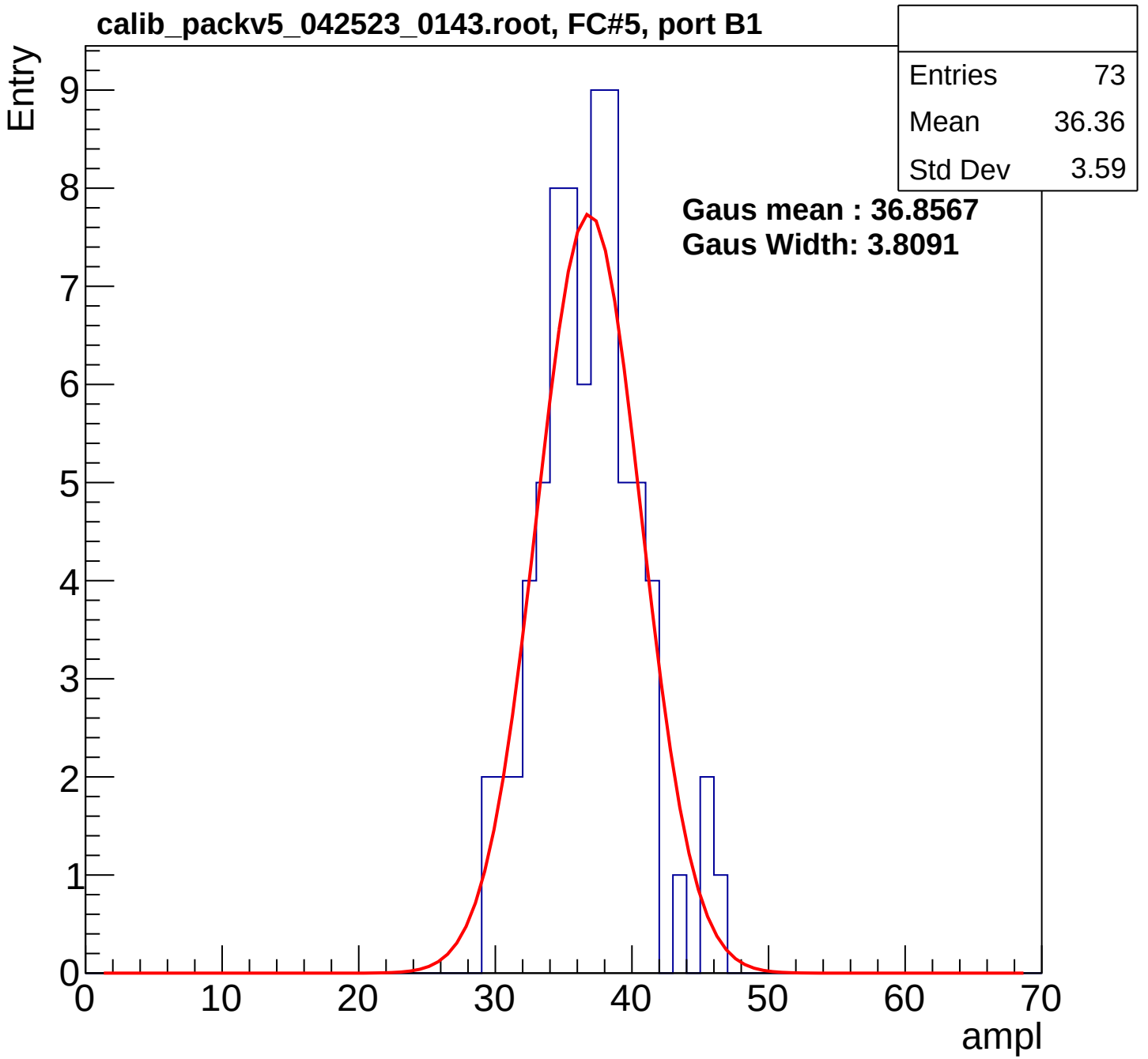

B0L000S, U1-ch36, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 53.9
Std Dev 4.551
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

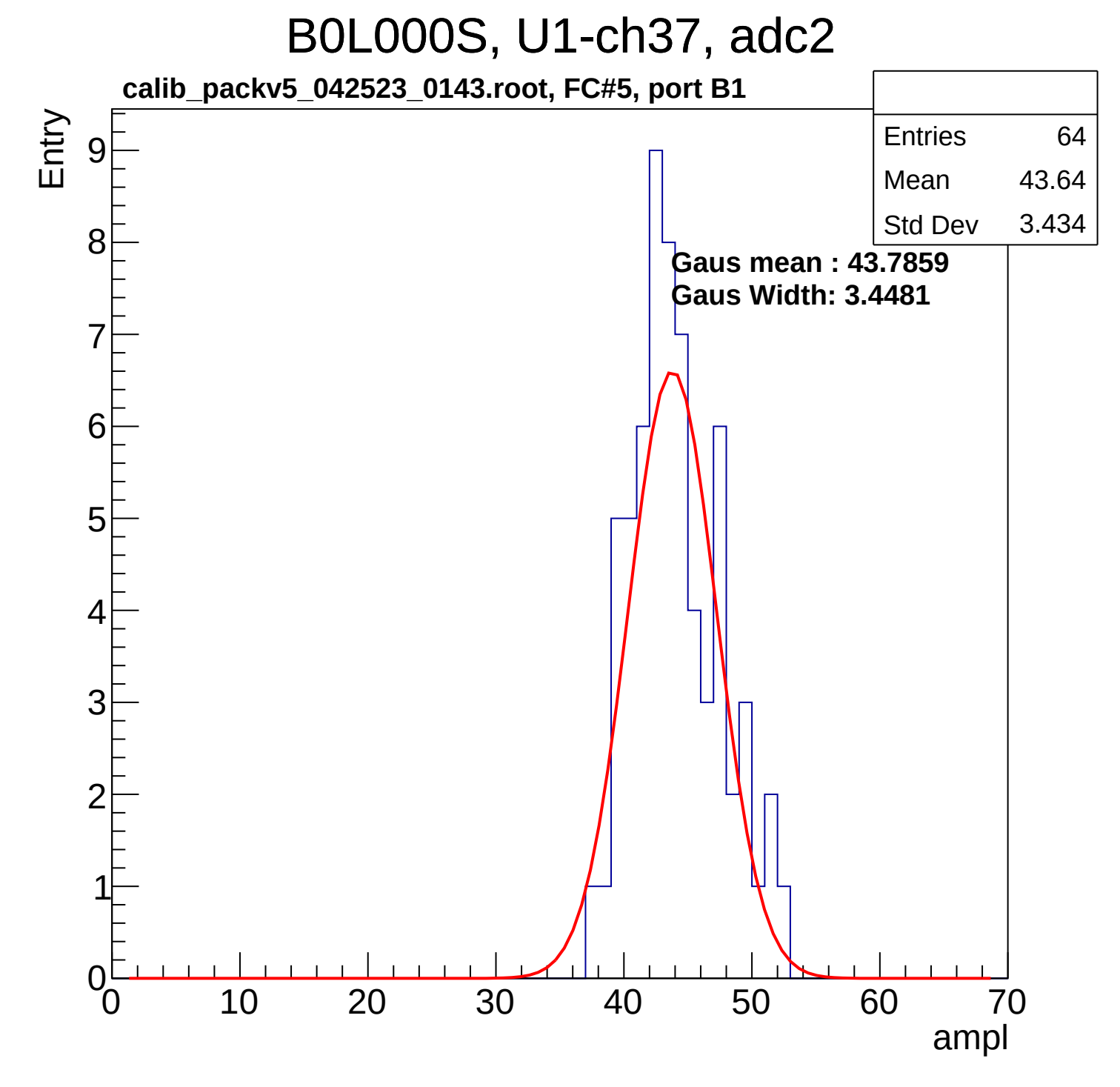

B0L000S, U1-ch36, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 57.65
Std Dev 9.123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

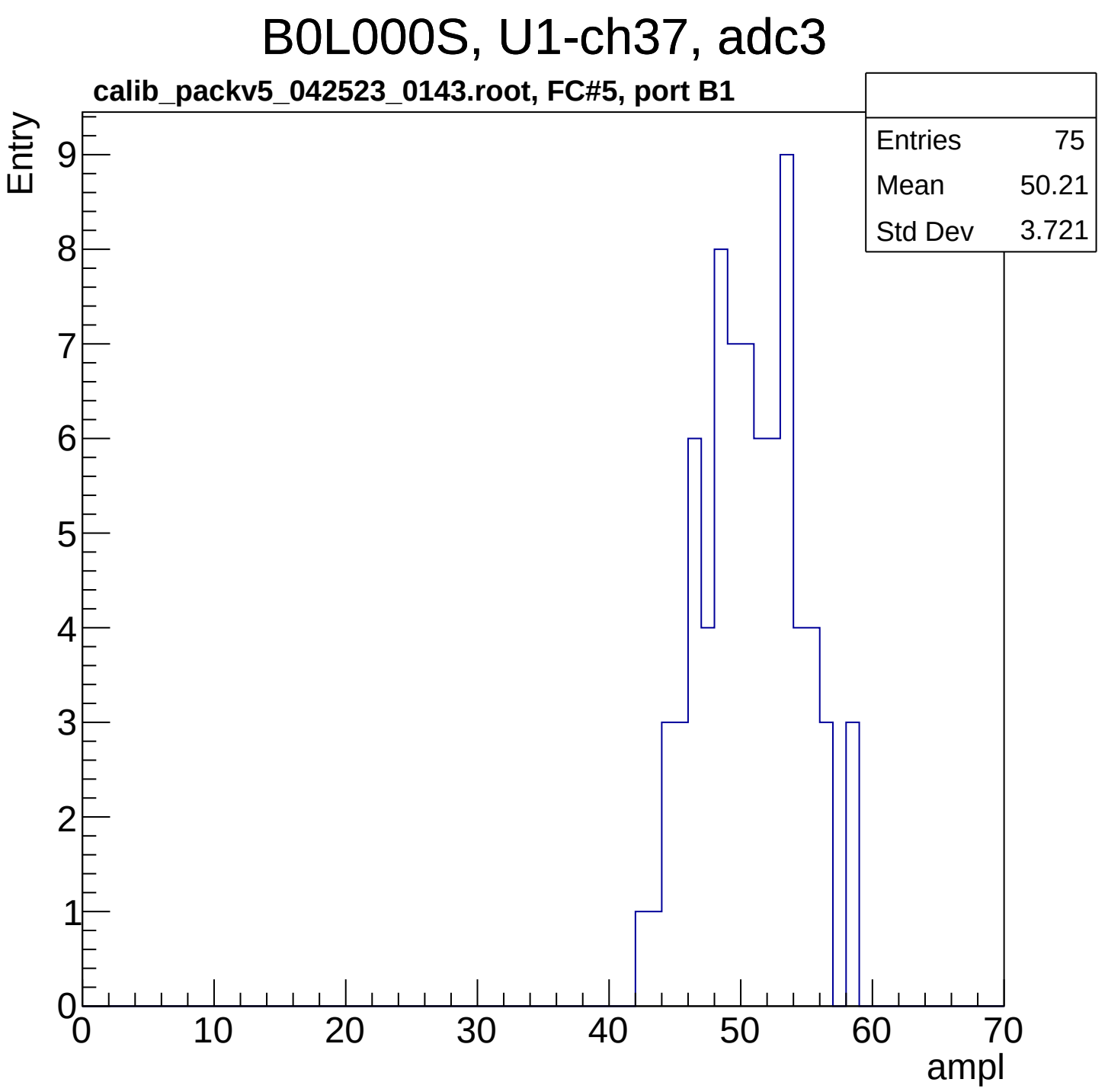

B0L000S, U1-ch36, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.5
Std Dev 2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

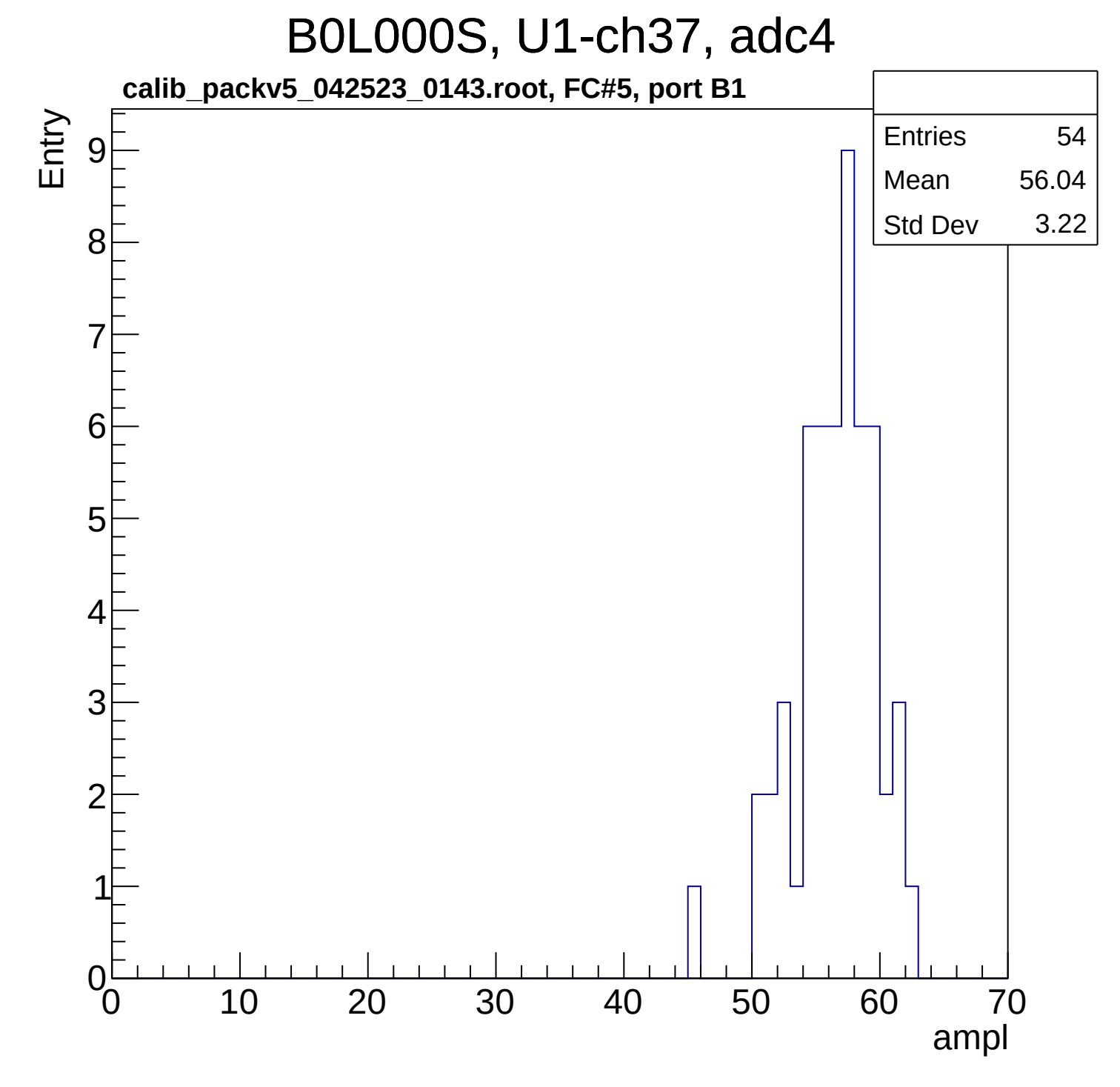

B0L000S, U1-ch36, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 25.2
Std Dev 30.86
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

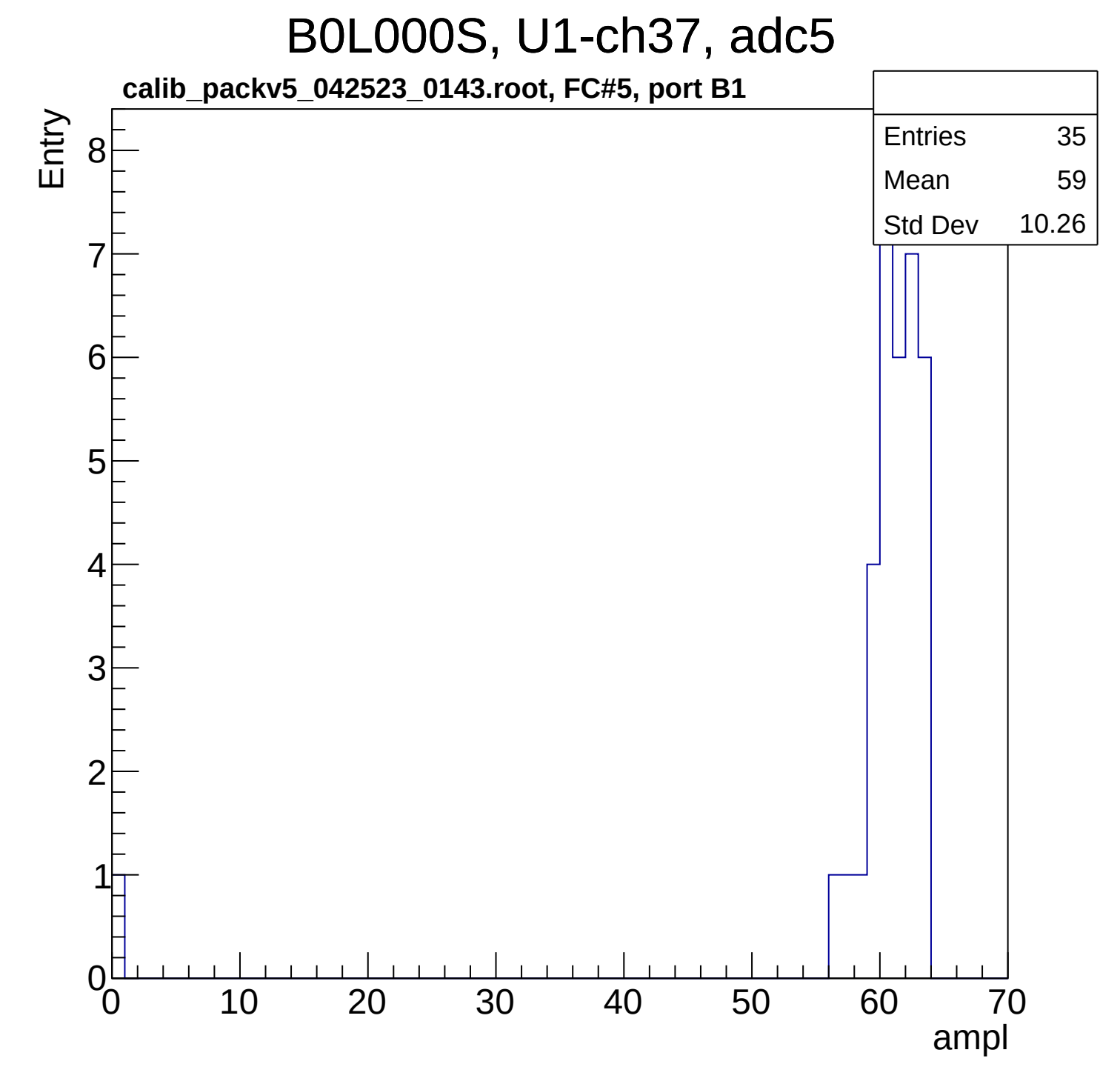

B0L000S, U1-ch37, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.83
Std Dev 5.948
Gaus mean : 29.9319
Gaus Width: 3.4130
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch37, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.36
Std Dev 3.59
Gaus mean : 36.8567
Gaus Width: 3.8091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch37, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.64
Std Dev 3.434
Gaus mean : 43.7859
Gaus Width: 3.4481
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch37, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 50.21
Std Dev 3.721
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch37, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.04
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch37, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 59
Std Dev 10.26
Entry
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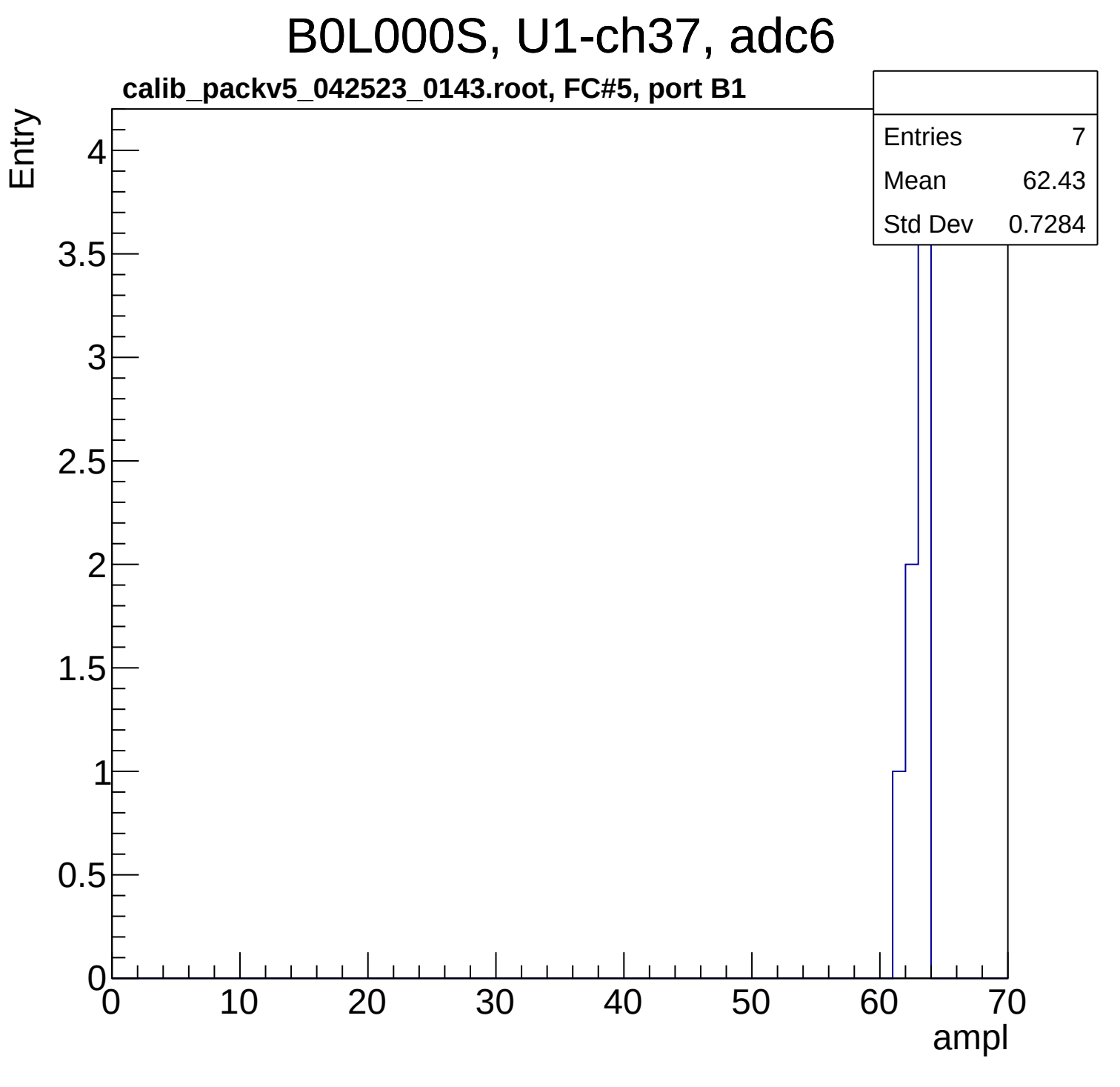
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch37, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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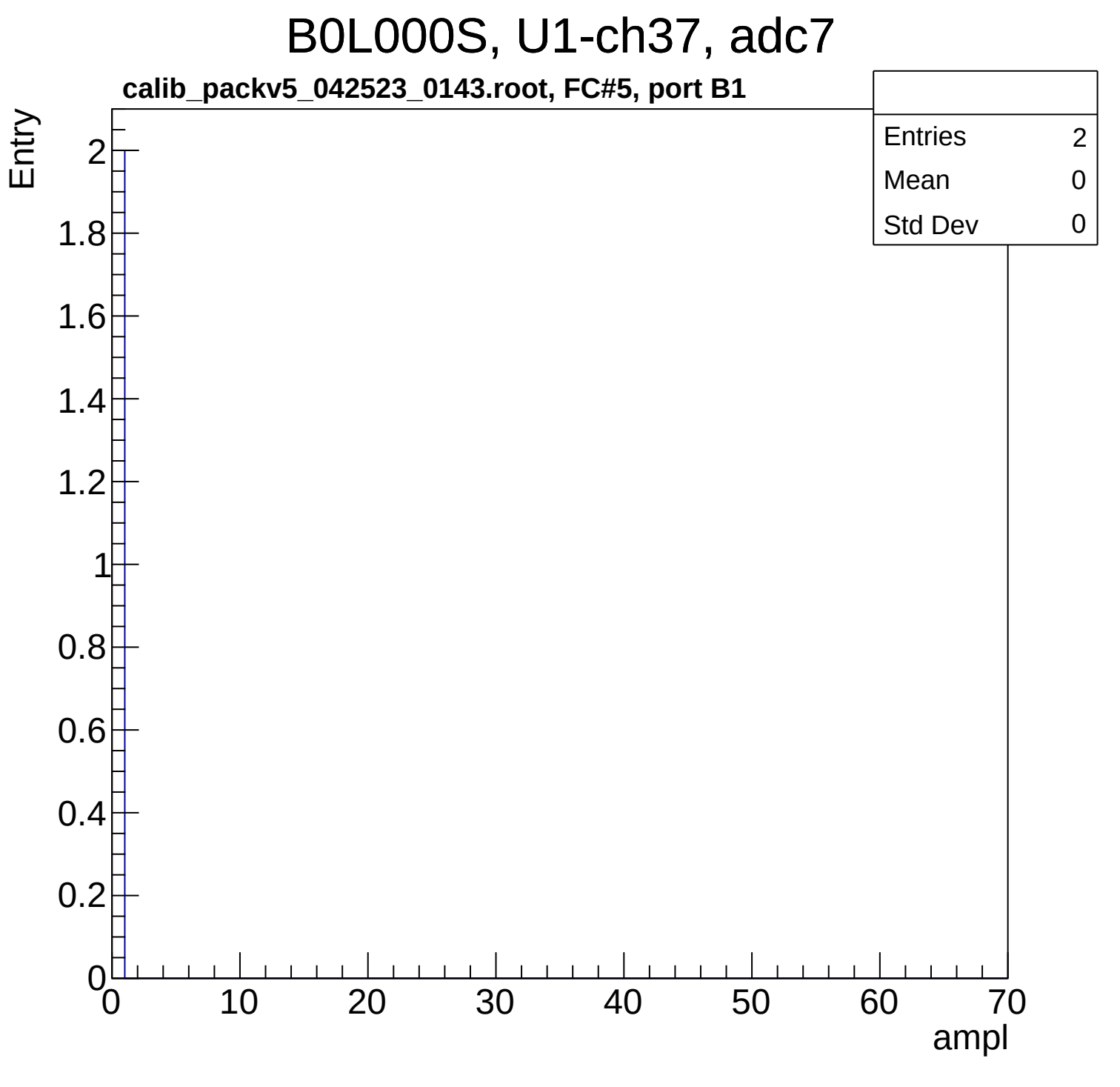
40
50
60
70


B0L000S, U1-ch37, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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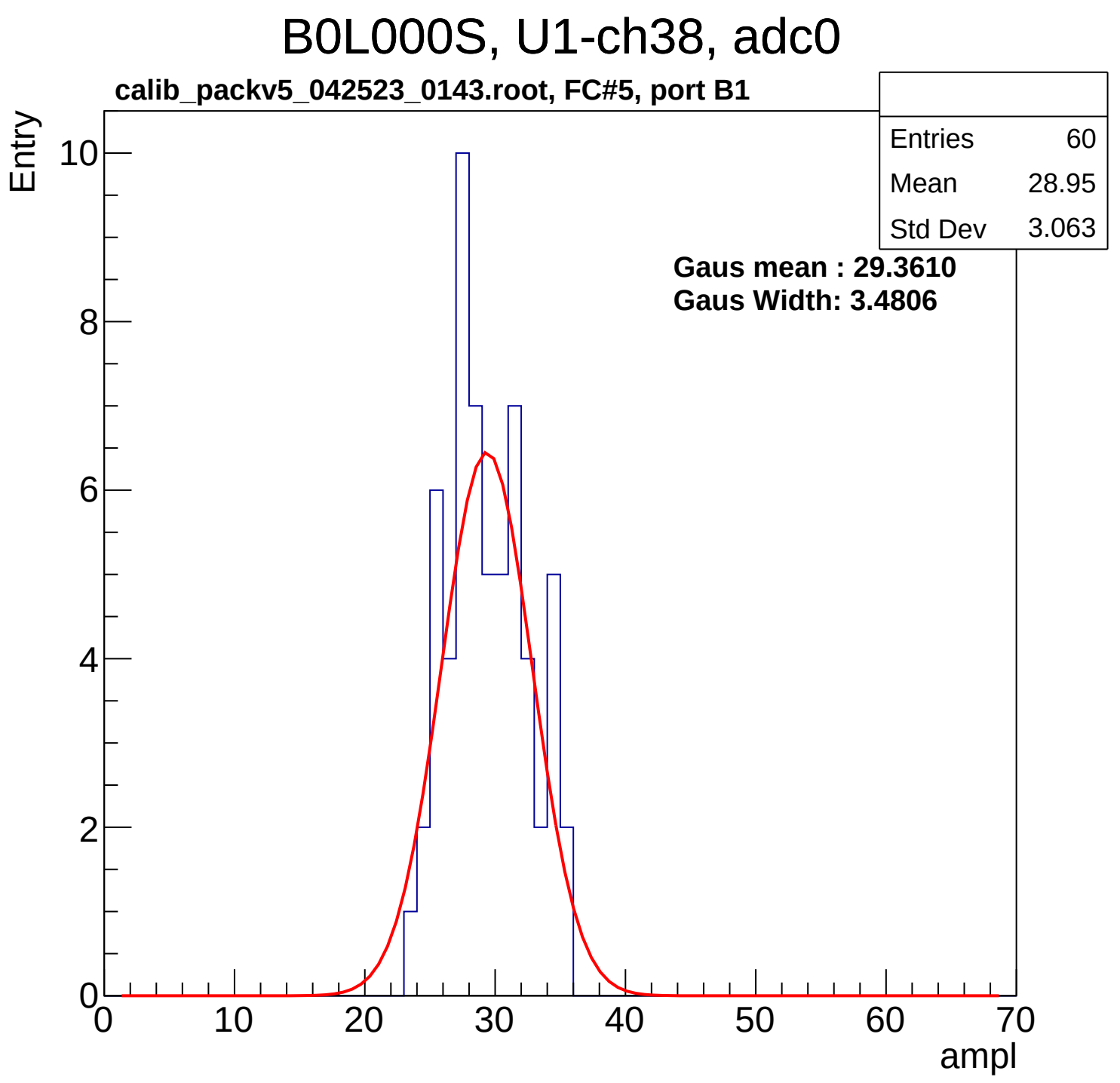
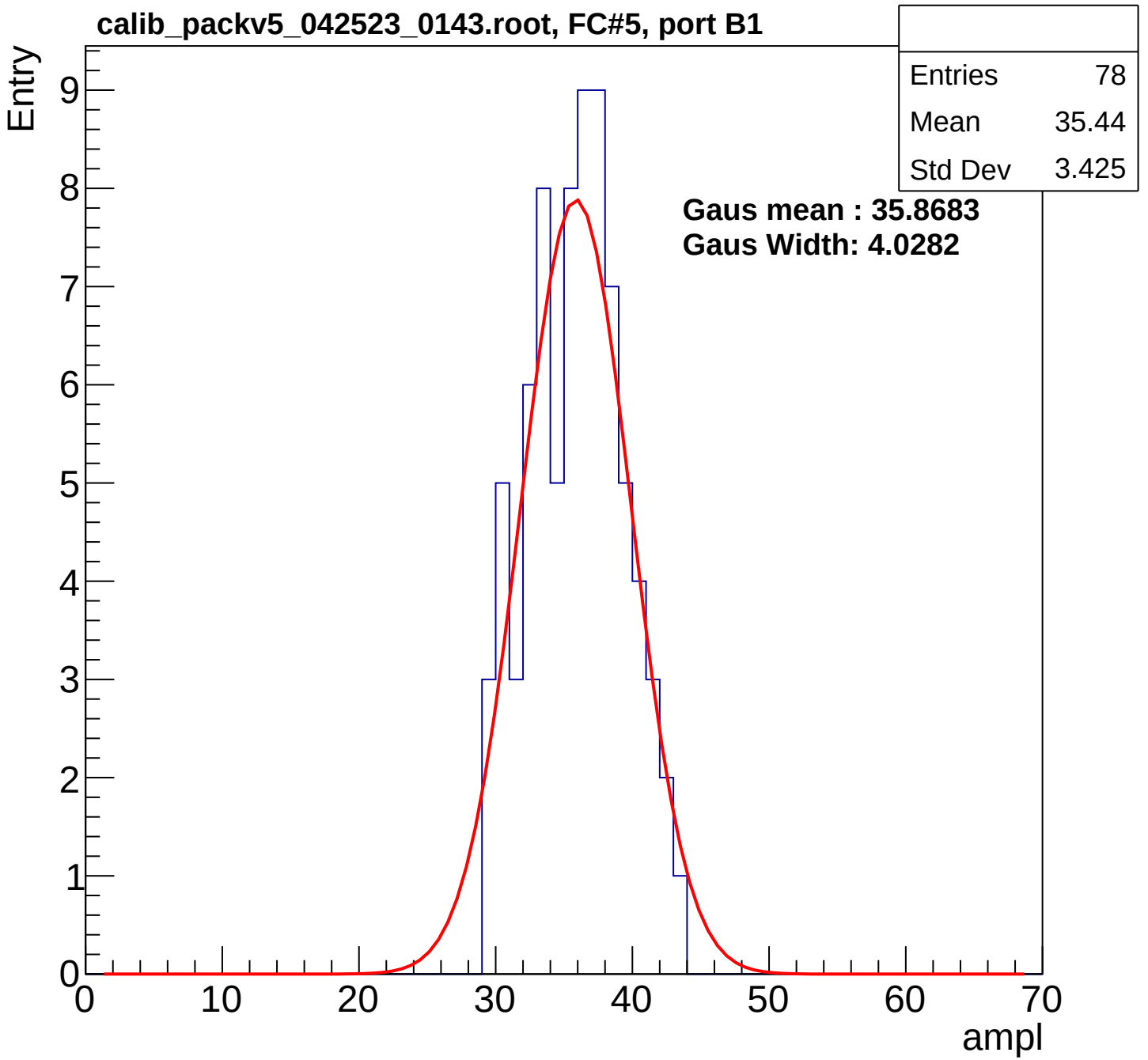
40
50
60
70


B0L000S, U1-ch38, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 28.95
Std Dev 3.063
Gaus mean : 29.3610
Gaus Width: 3.4806
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch38, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 35.44
Std Dev 3.425
Gaus mean : 35.8683
Gaus Width: 4.0282
Entry
ampl
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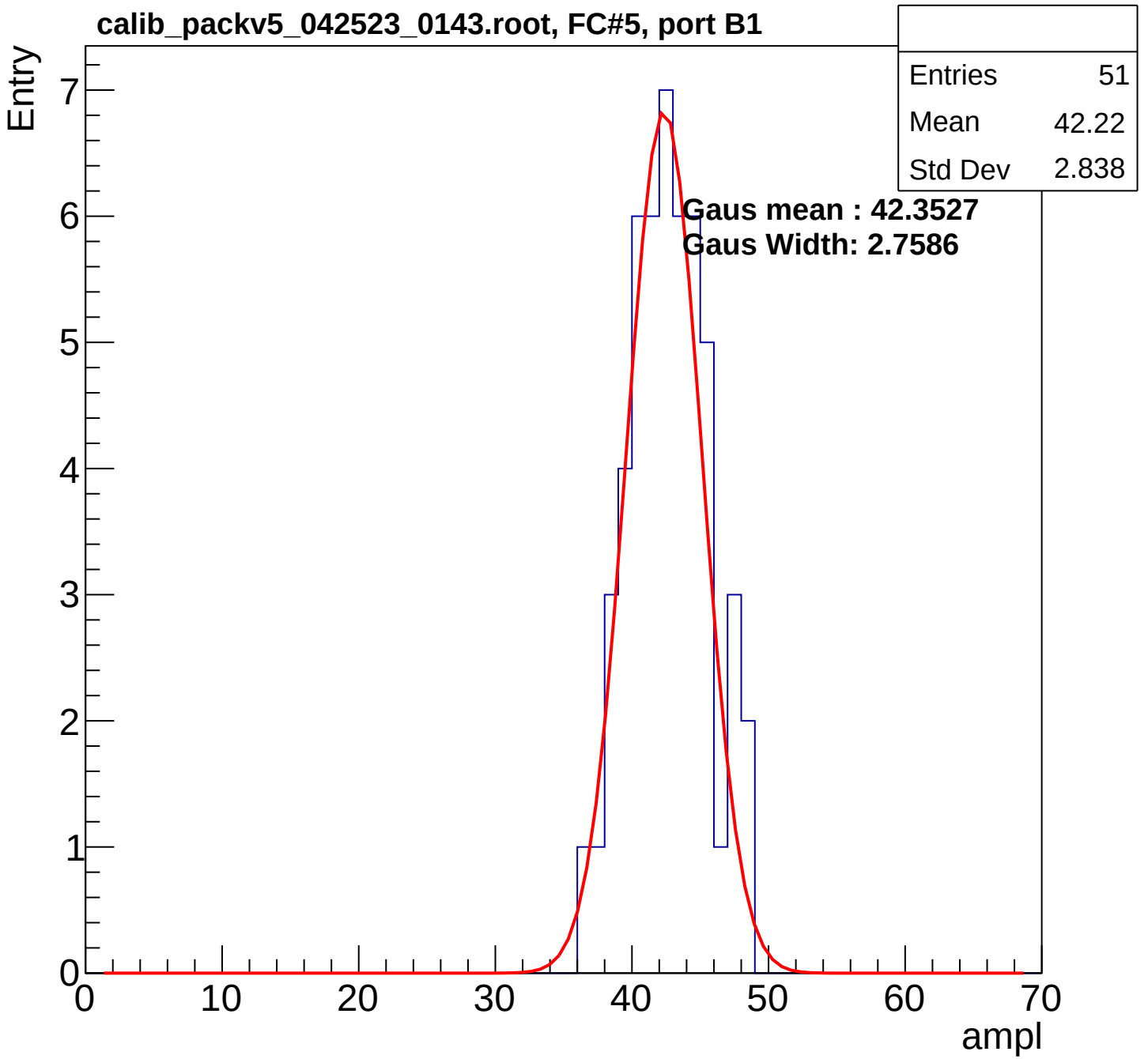
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U1-ch38, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 42.22
Std Dev 2.838
Gaus mean : 42.3527
Gaus Width: 2.7586
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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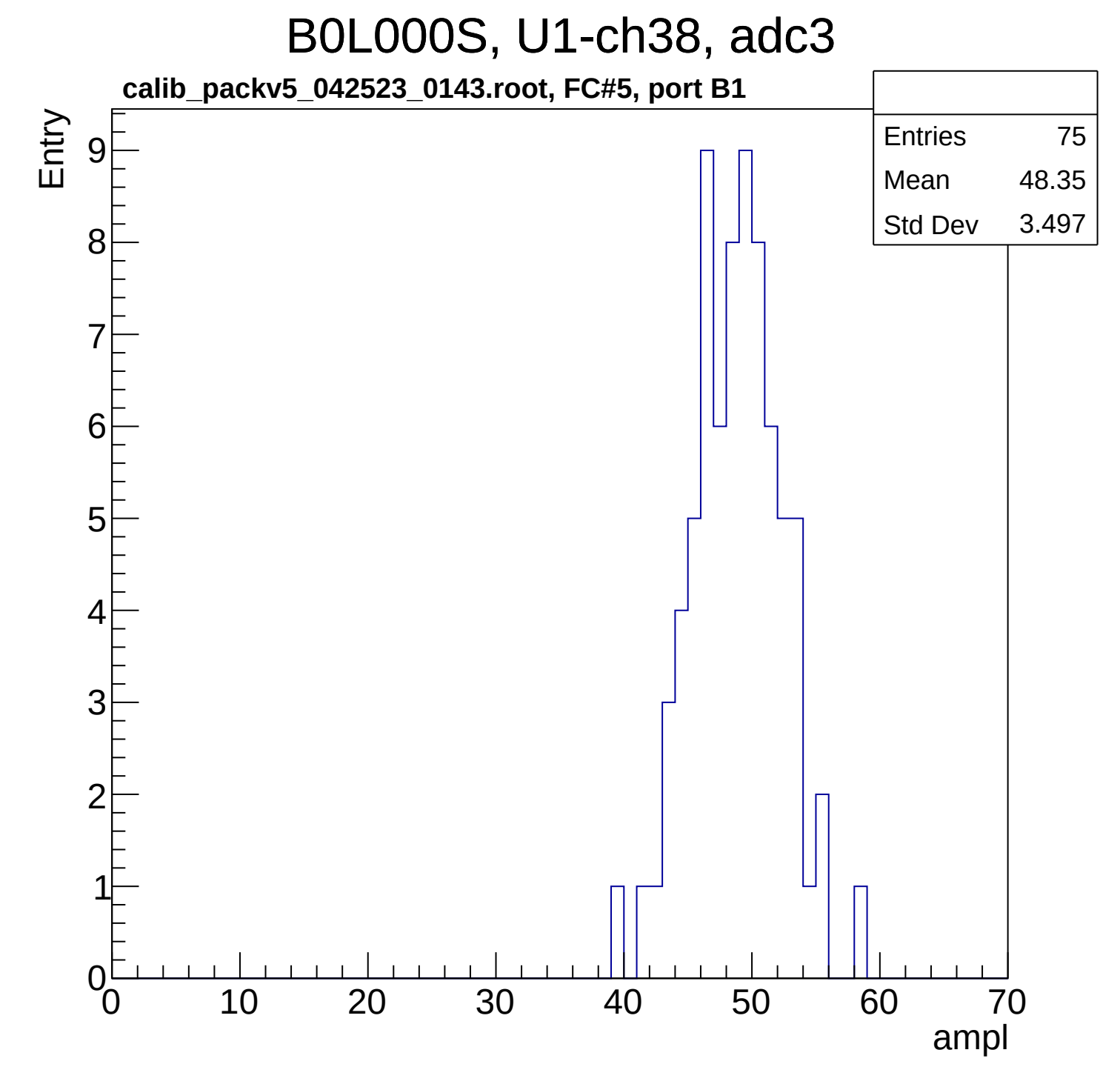
B0L000S, U1-ch38, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.35
Std Dev 3.497
Entry
ampl
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4
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7
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0
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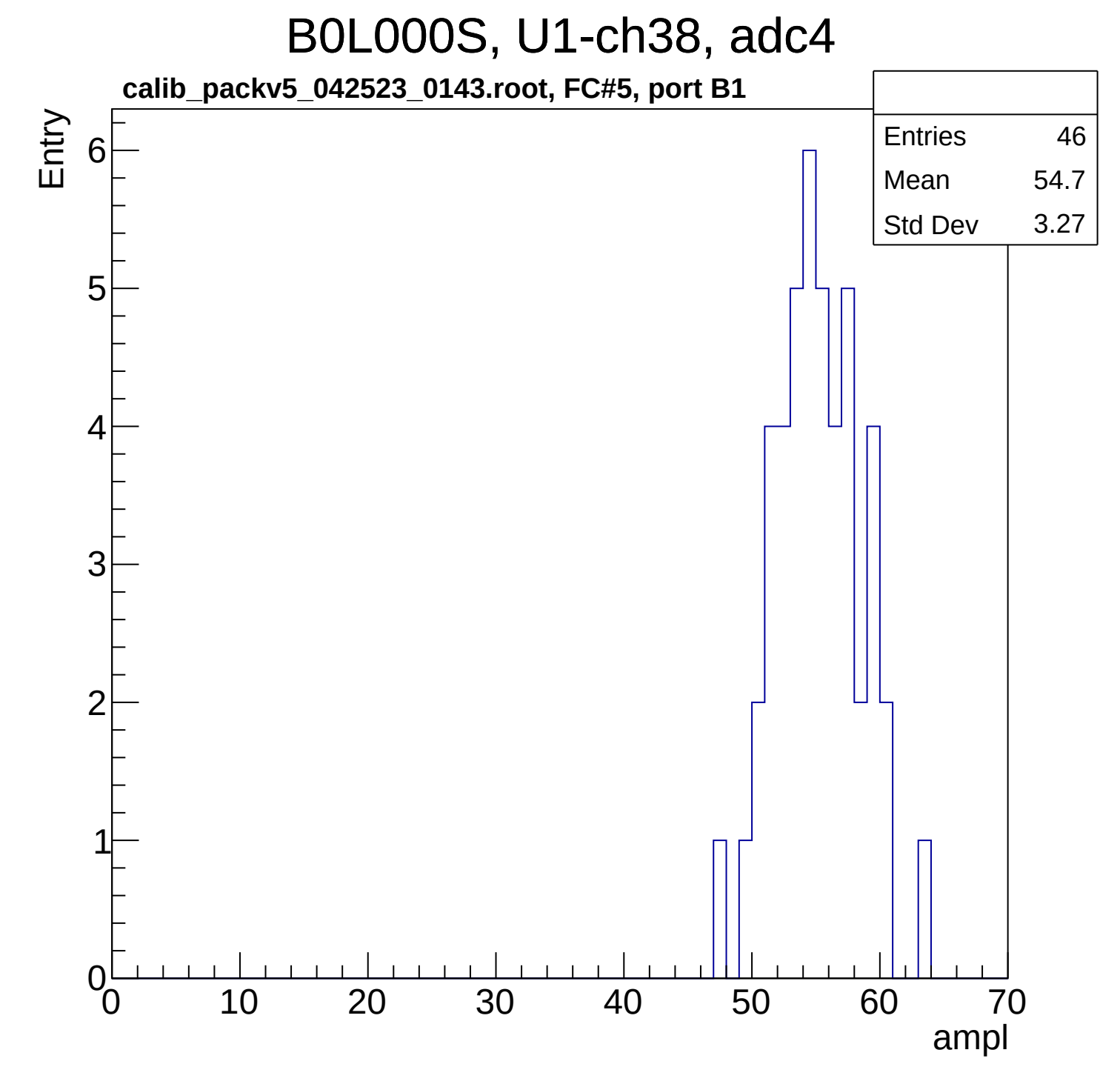
B0L000S, U1-ch38, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 54.7
Std Dev 3.27
Entry
ampl
0
1
2
3
4
5
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0
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20
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40
50
60
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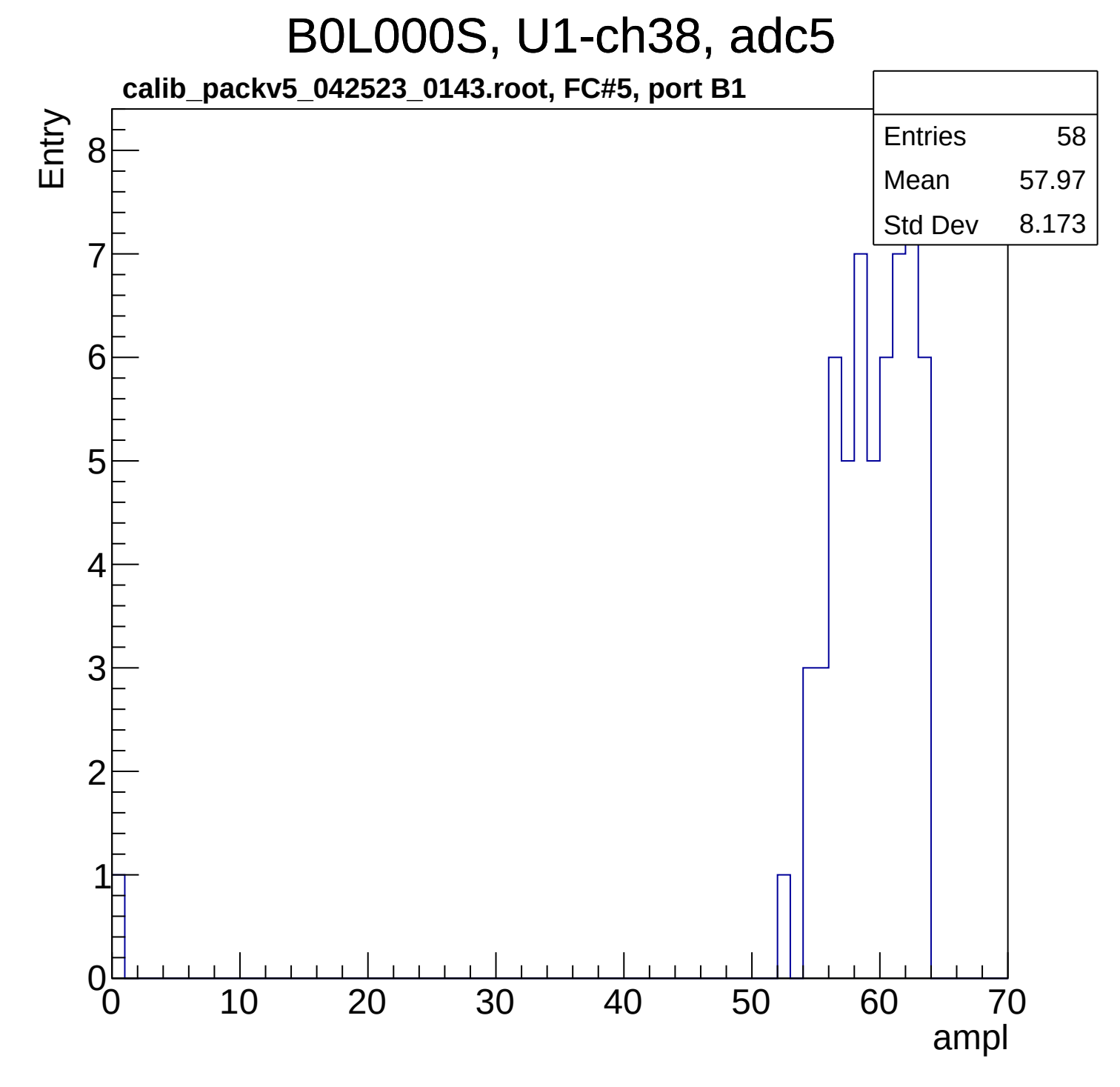
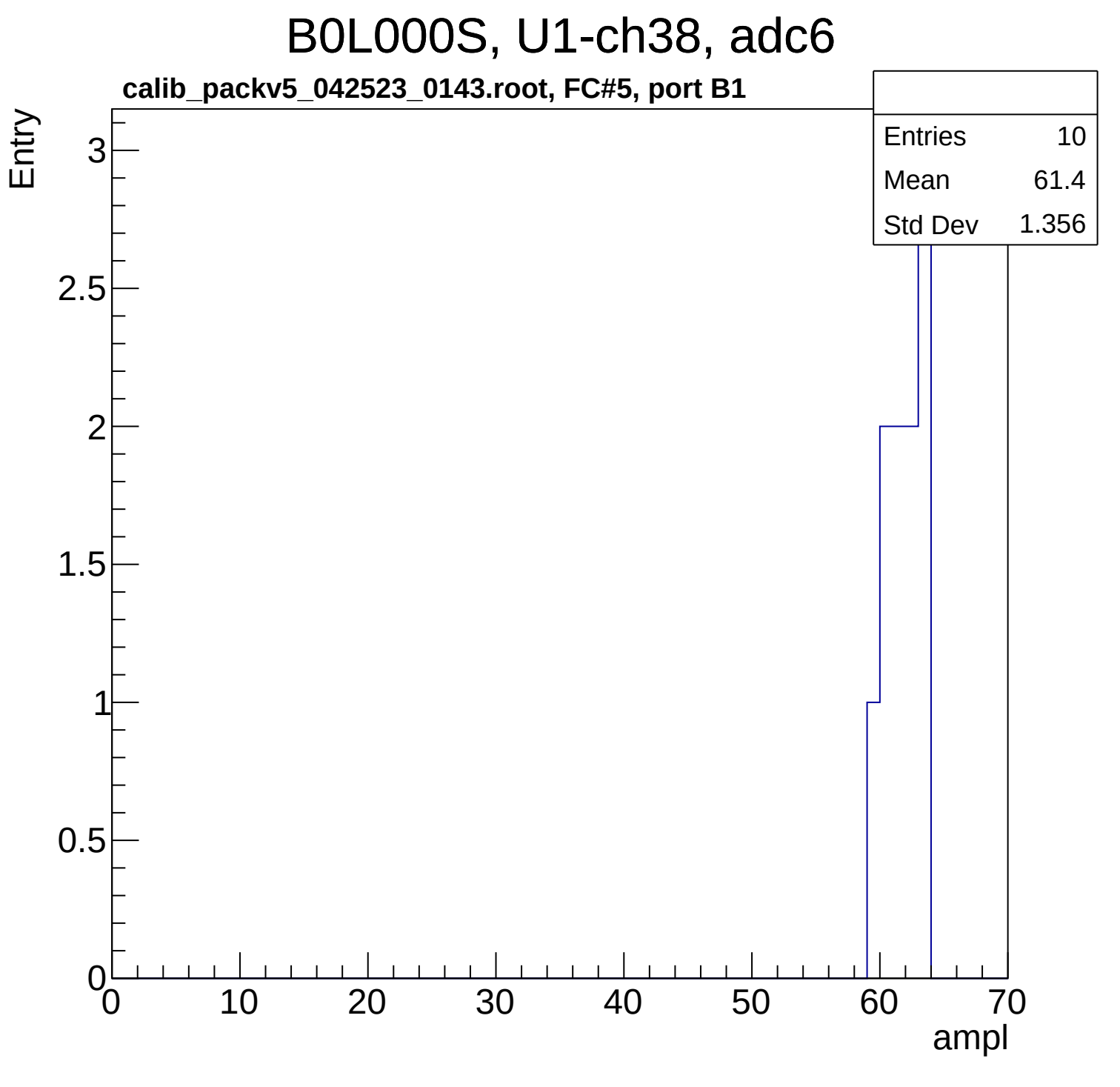

B0L000S, U1-ch38, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.97
Std Dev 8.173
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch38, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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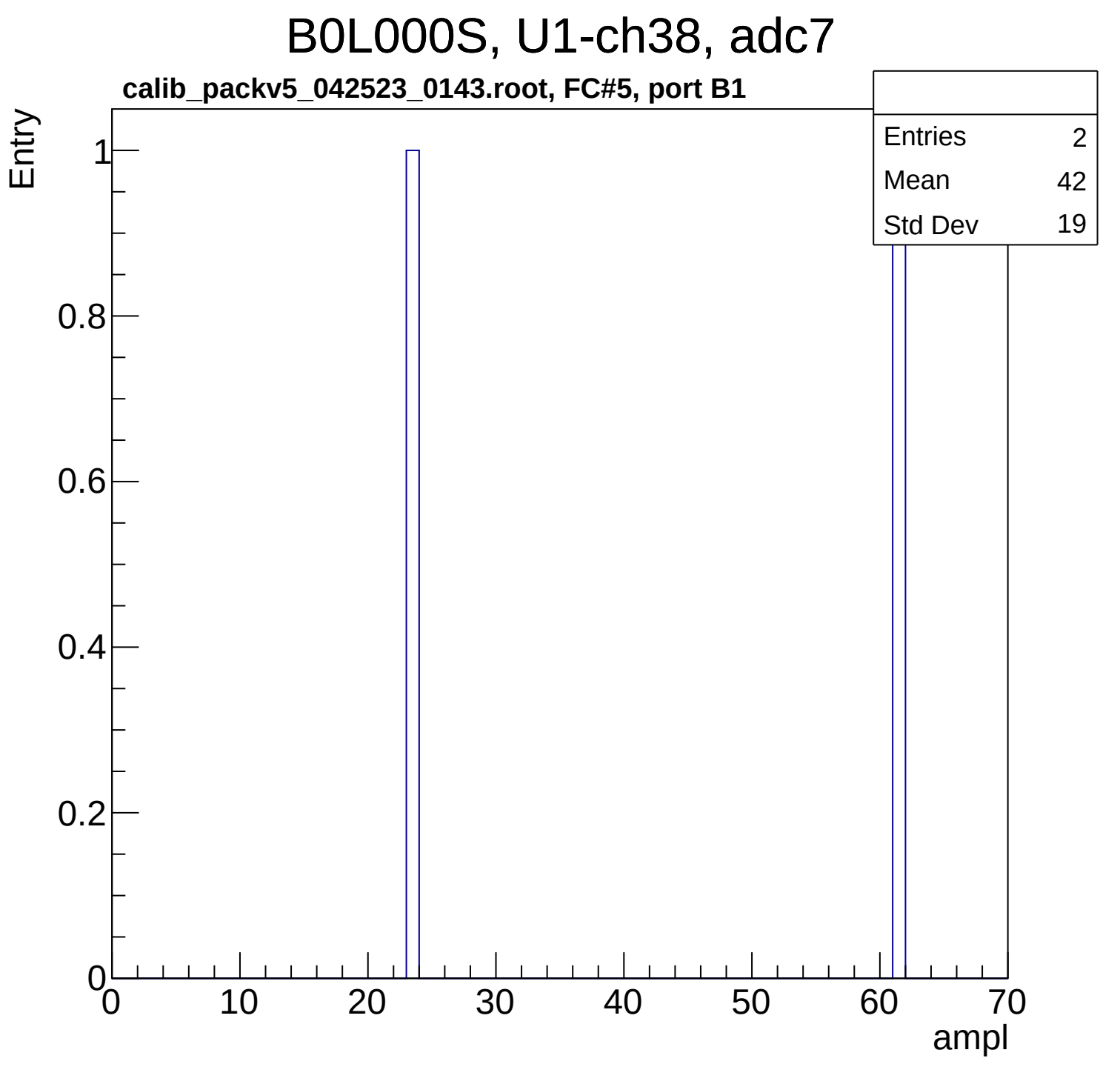
B0L000S, U1-ch38, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 42
Std Dev 19
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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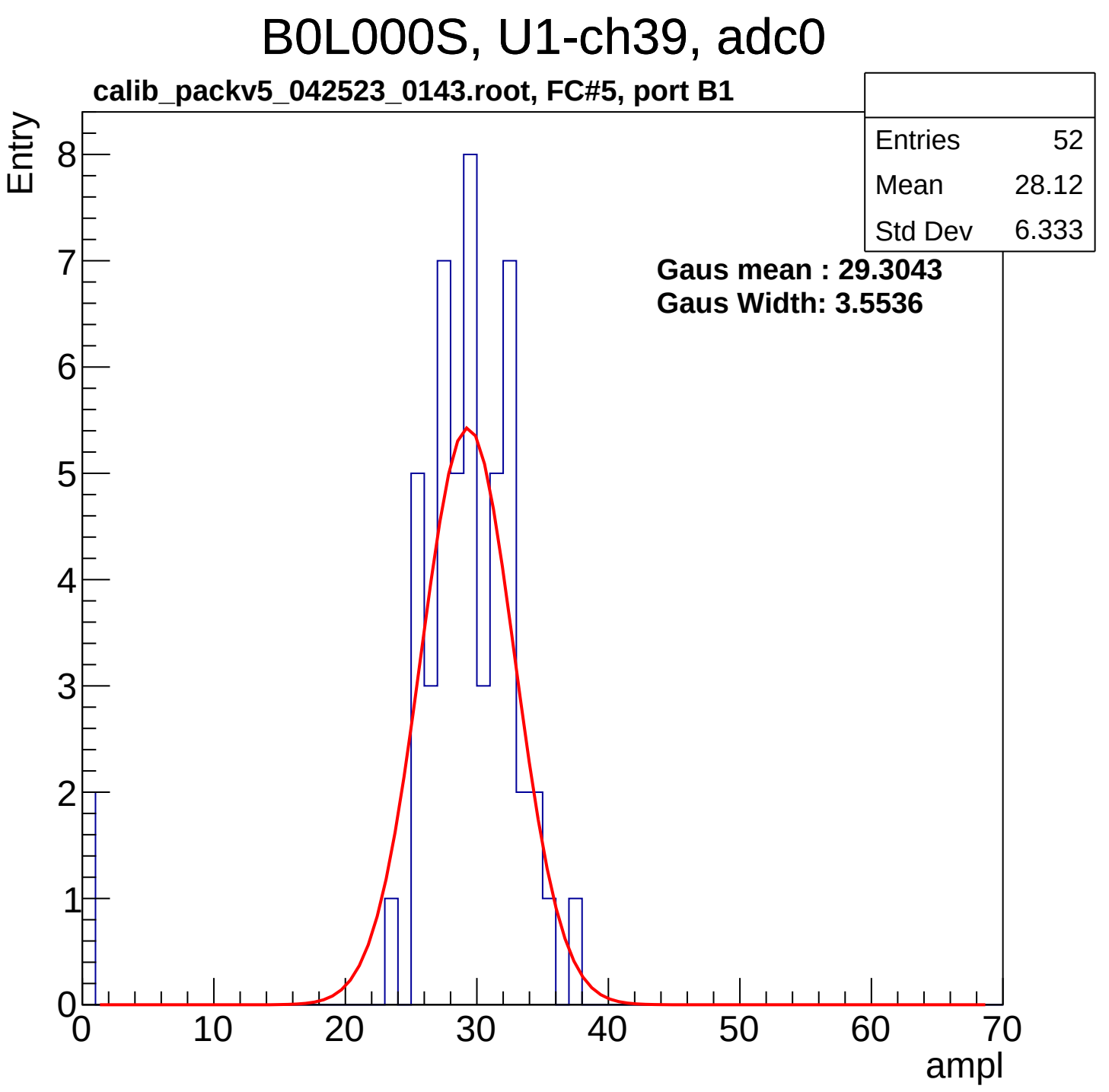
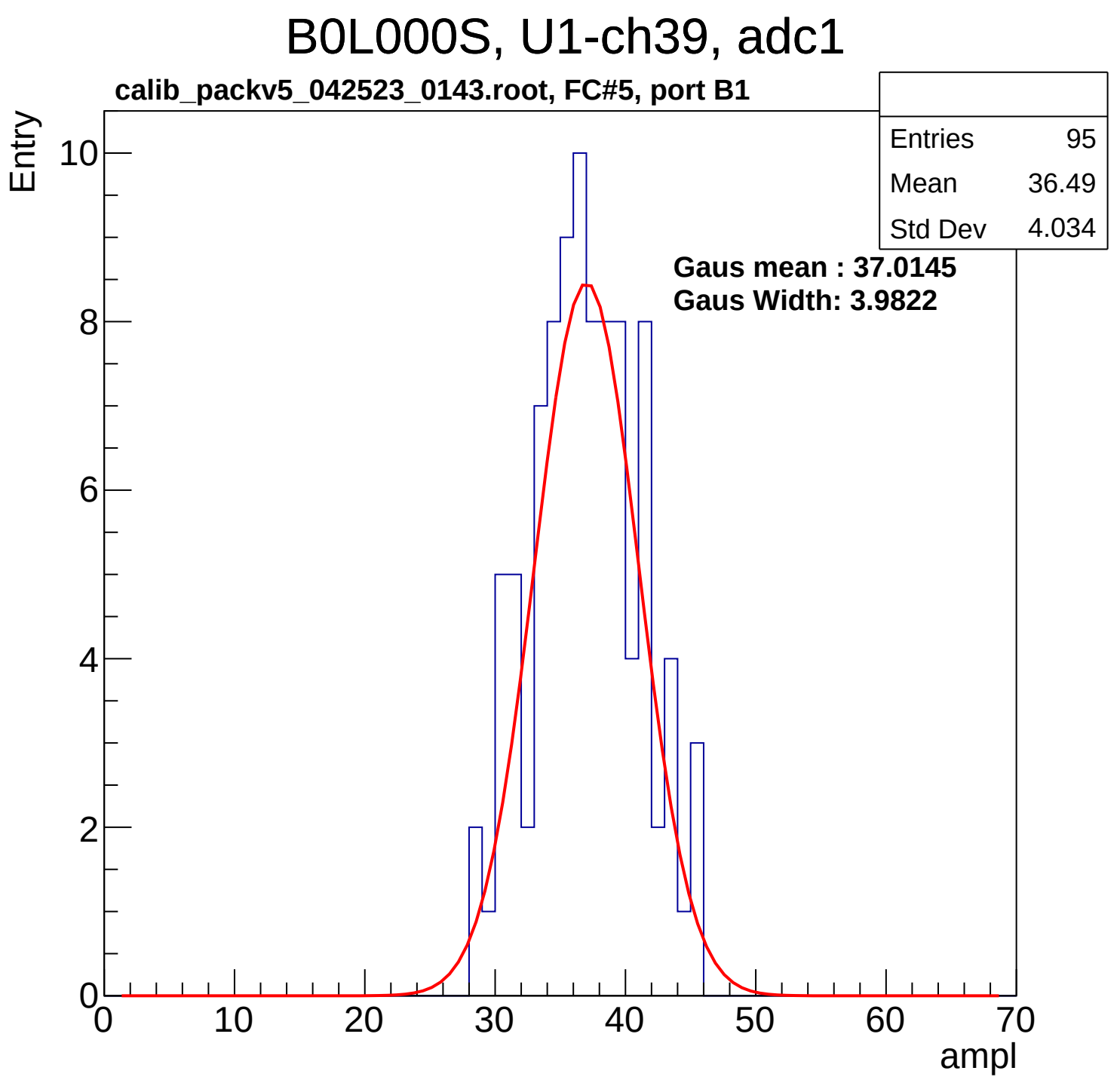
40
50
60
70
ampl


B0L000S, U1-ch39, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 28.12
Std Dev 6.333
Gaus mean : 29.3043
Gaus Width: 3.5536
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch39, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 95
Mean 36.49
Std Dev 4.034
Gaus mean : 37.0145
Gaus Width: 3.9822
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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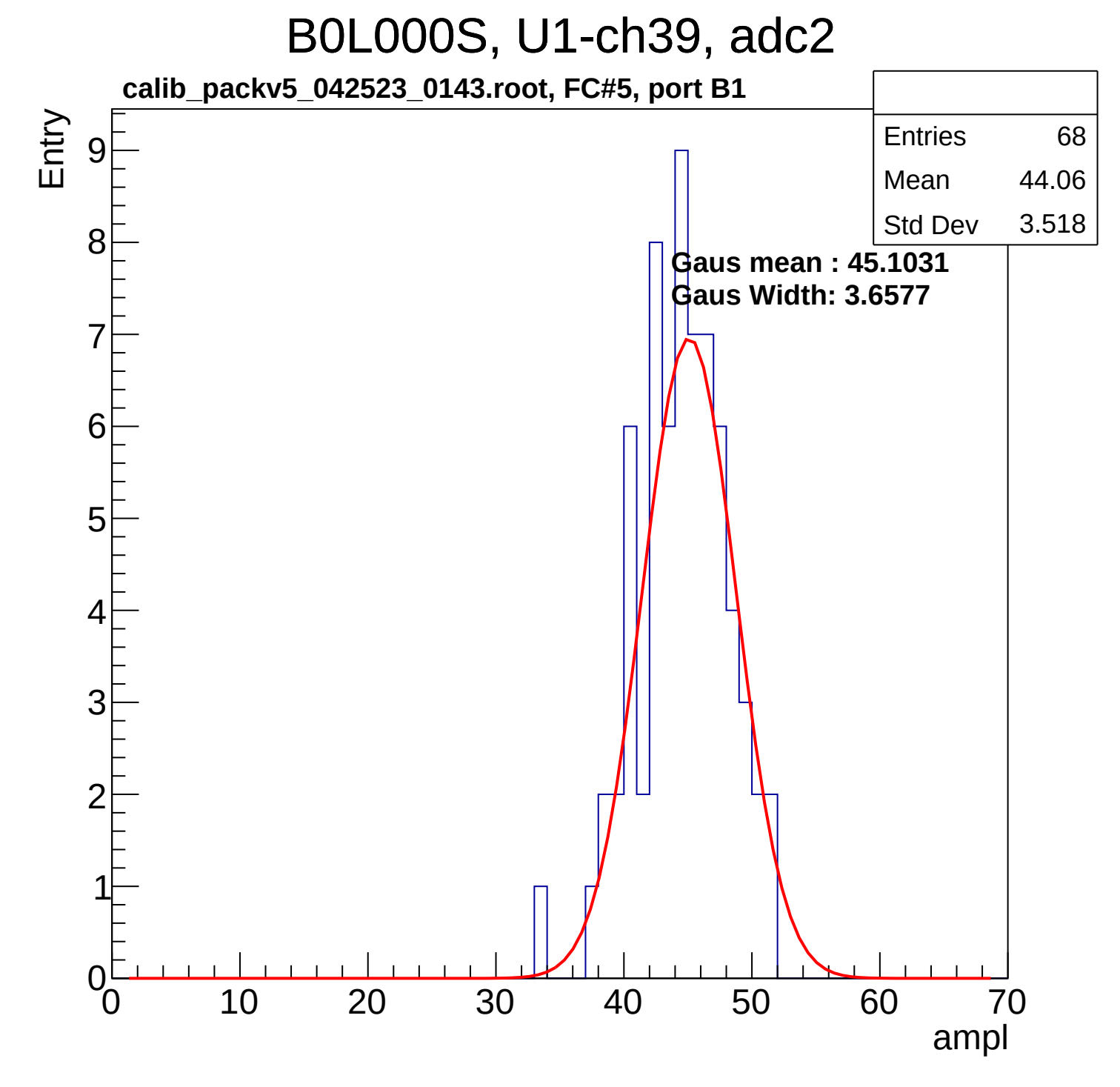
B0L000S, U1-ch39, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 44.06
Std Dev 3.518
Gaus mean : 45.1031
Gaus Width: 3.6577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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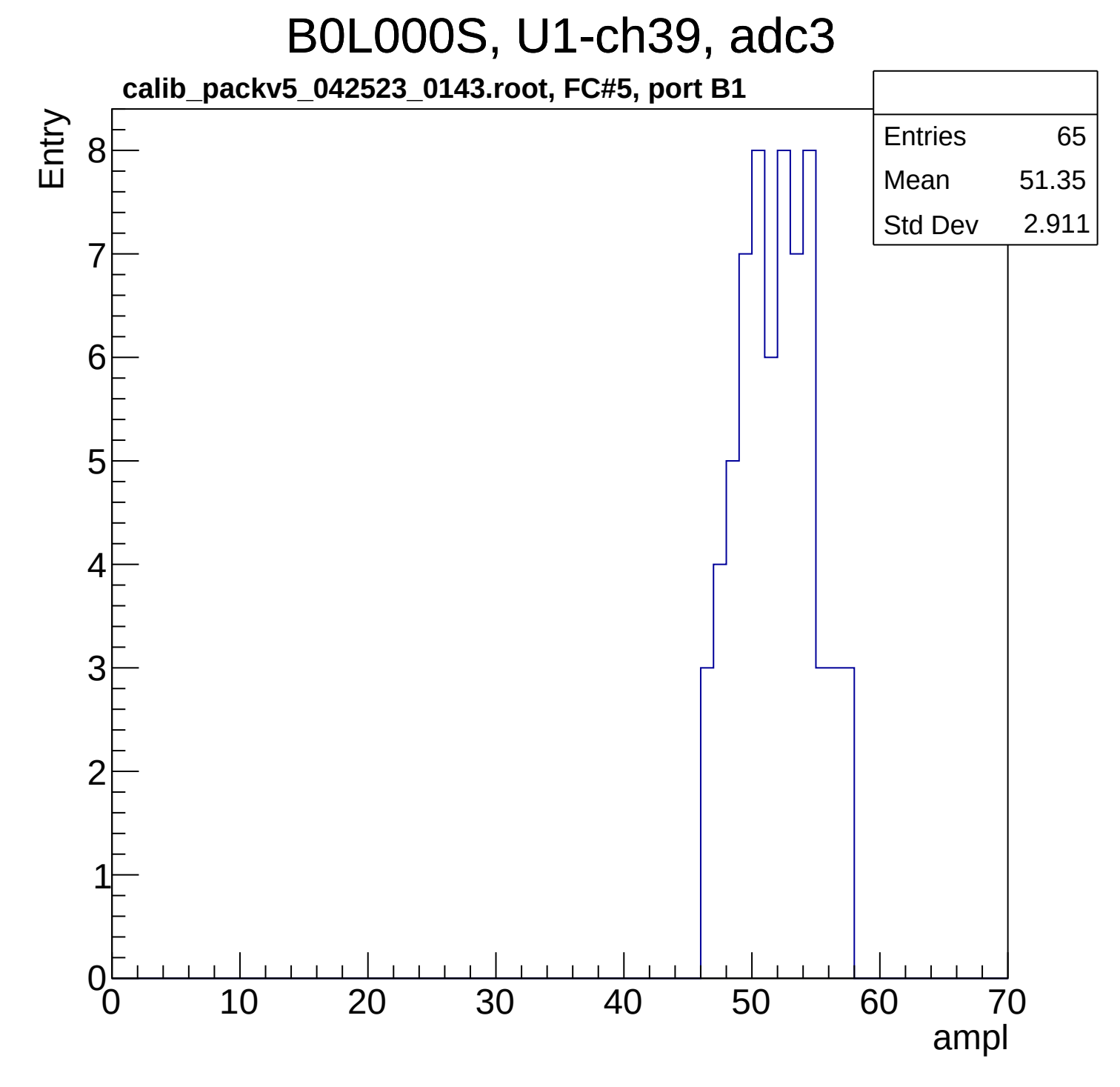
B0L000S, U1-ch39, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 51.35
Std Dev 2.911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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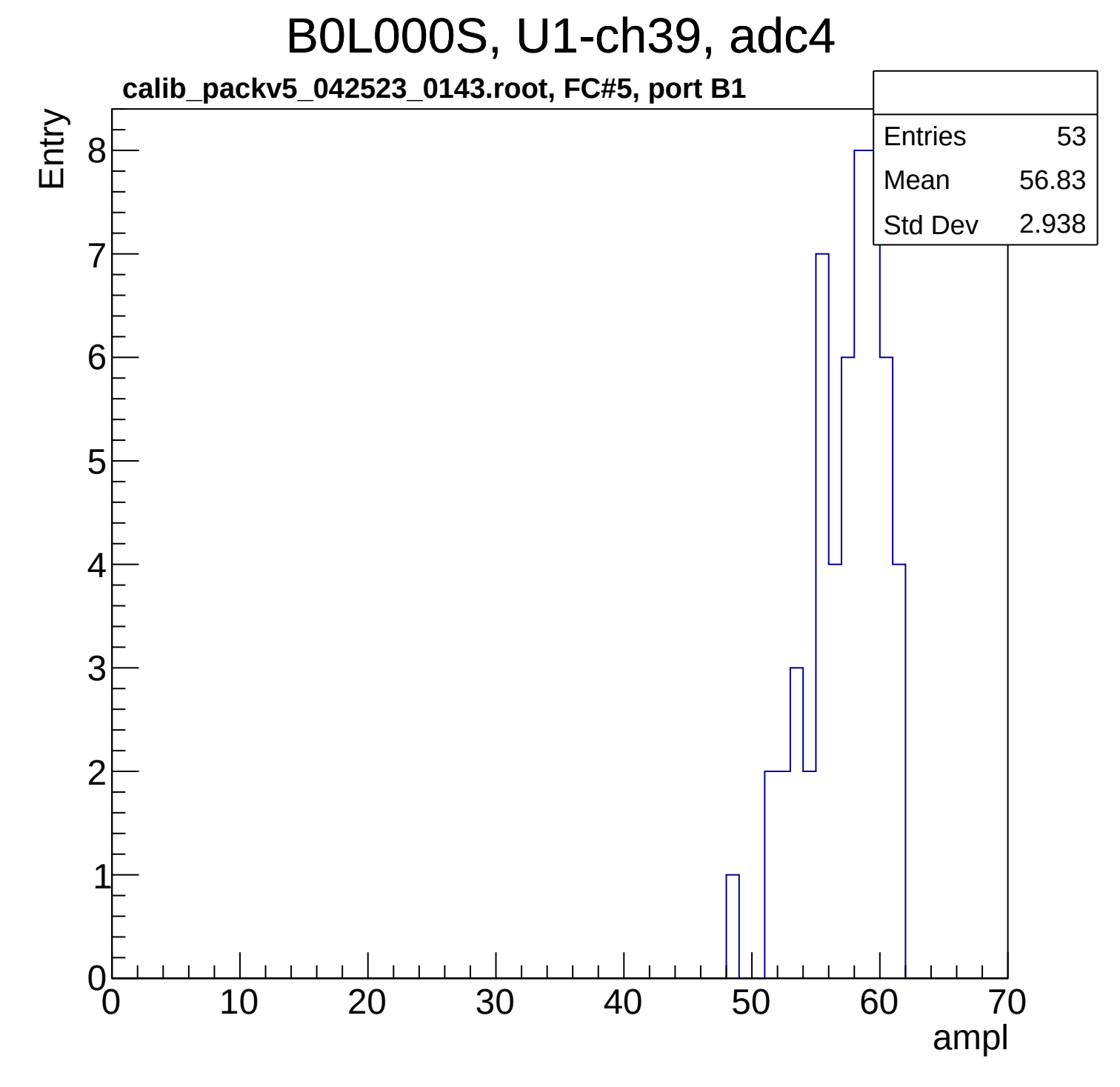
B0L000S, U1-ch39, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.83
Std Dev 2.938
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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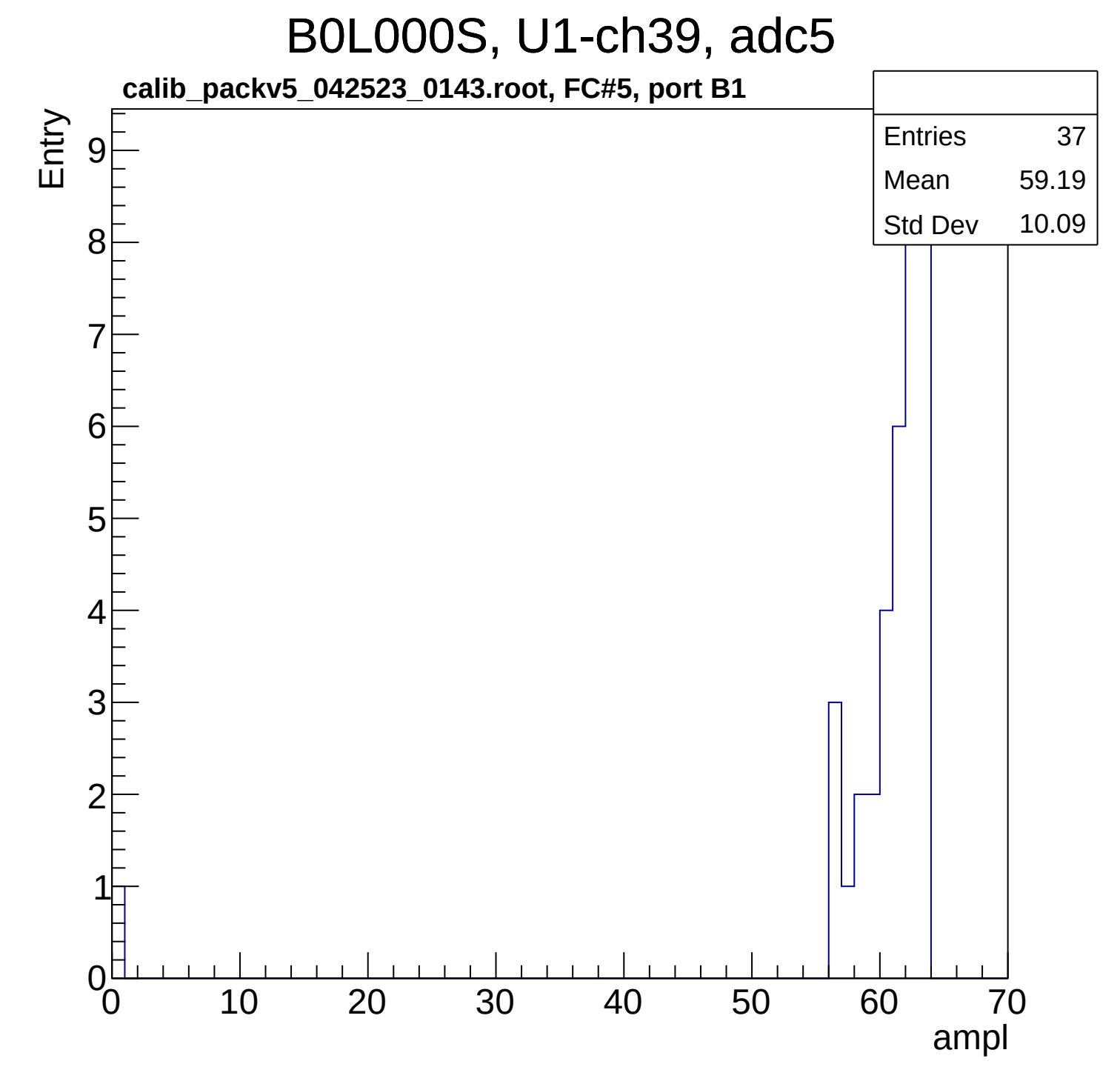
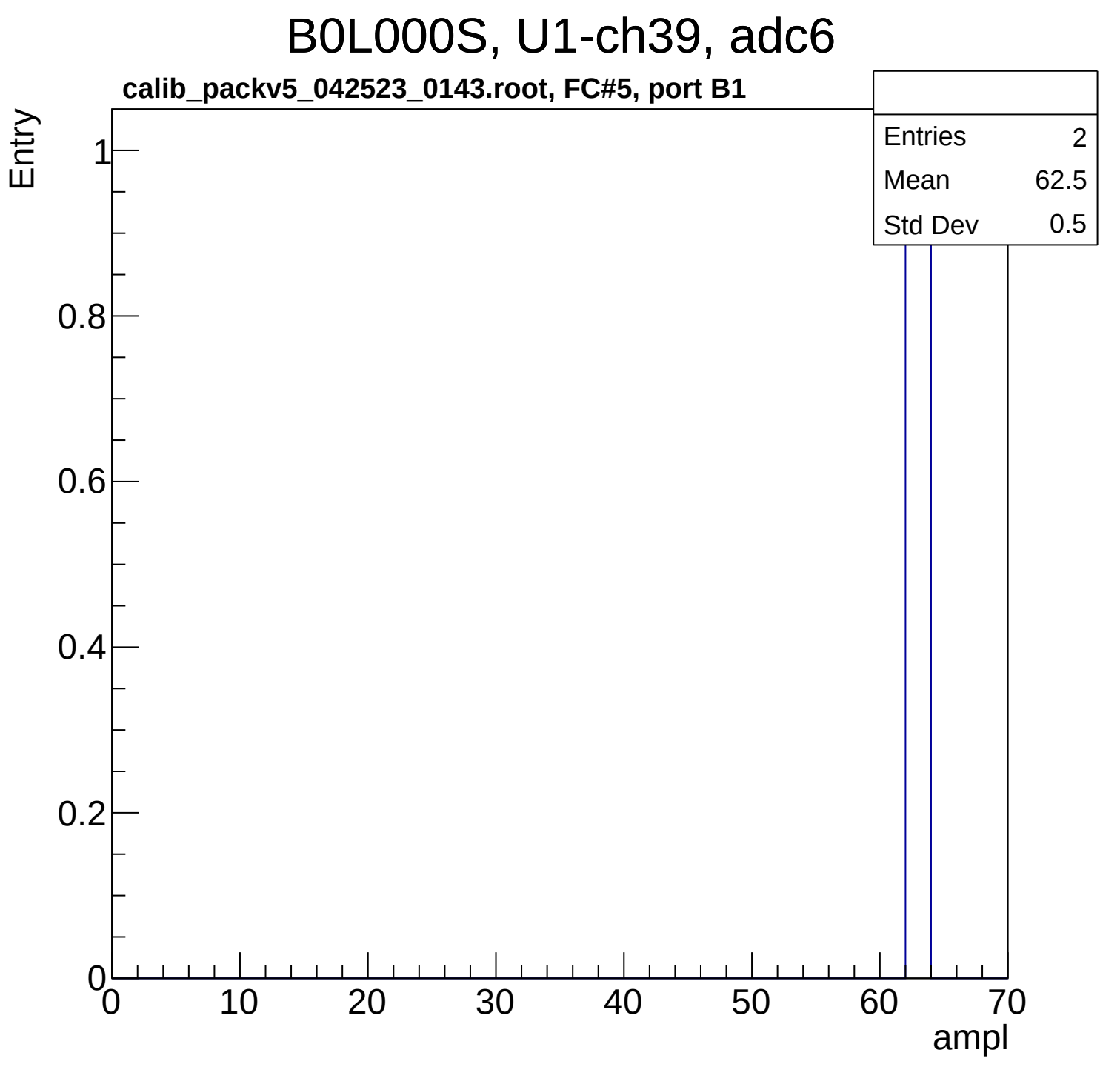
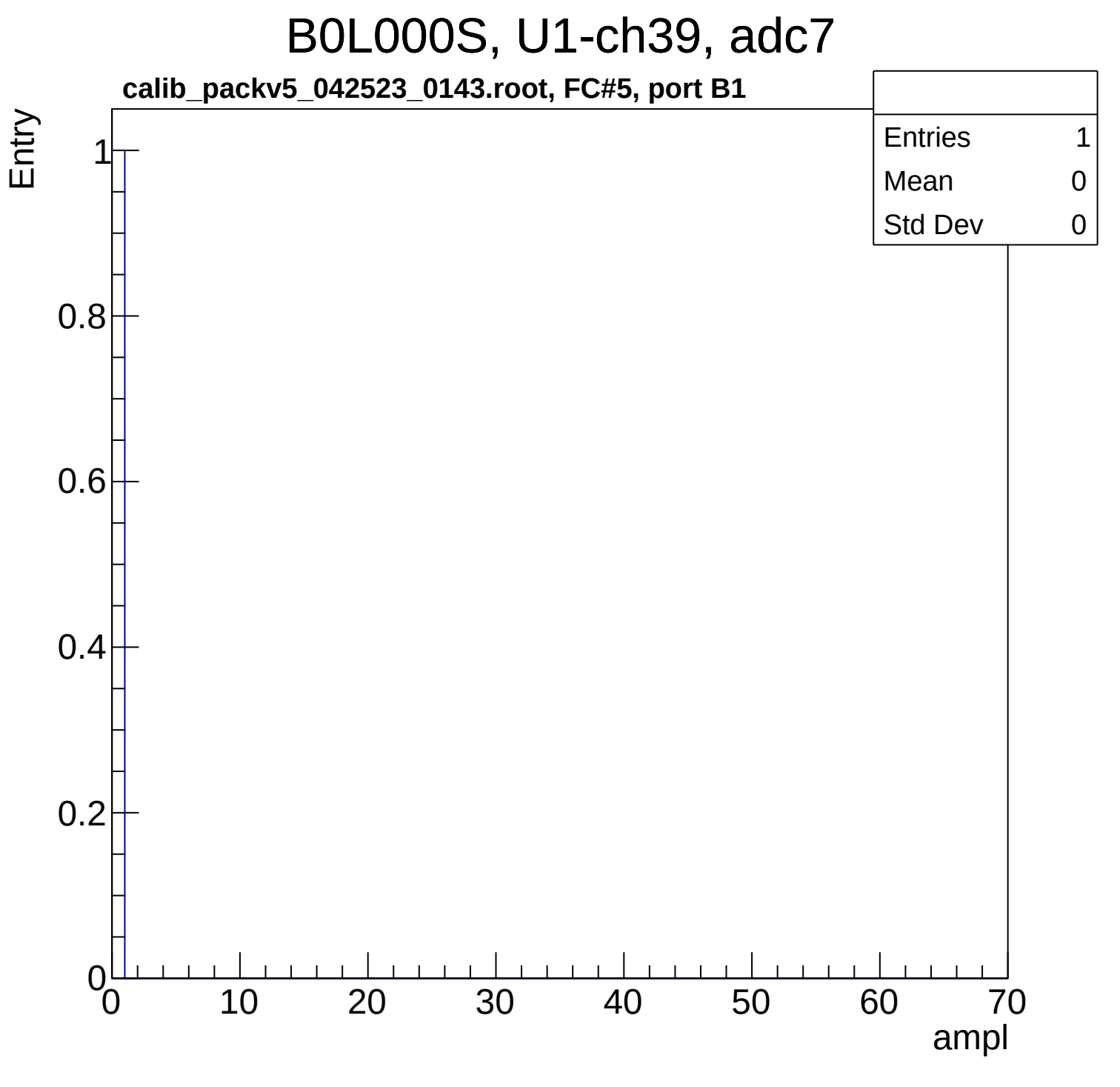
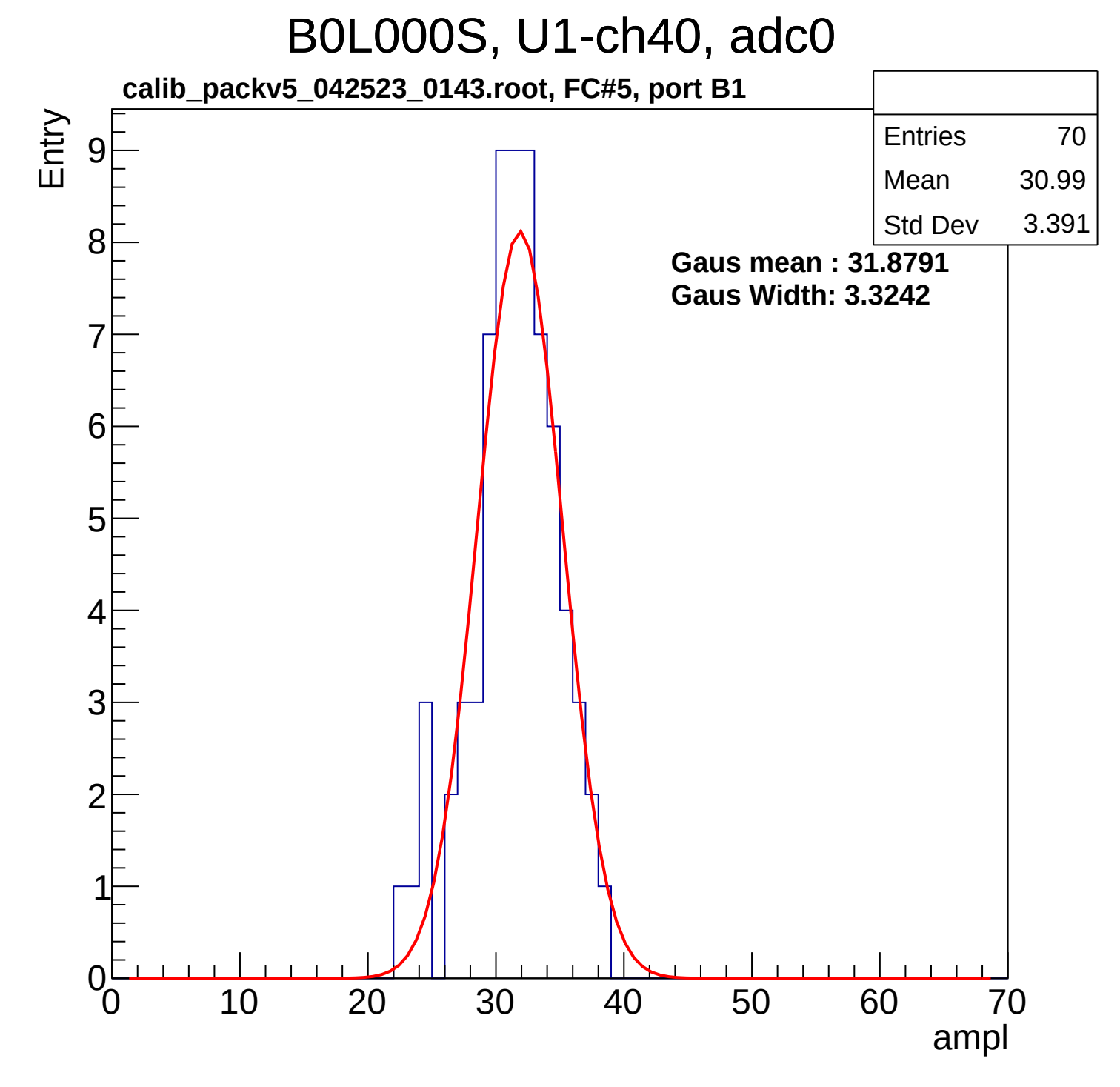

B0L000S, U1-ch39, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.19
Std Dev 10.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch39, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch39, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch40, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 30.99
Std Dev 3.391
Gaus mean : 31.8791
Gaus Width: 3.3242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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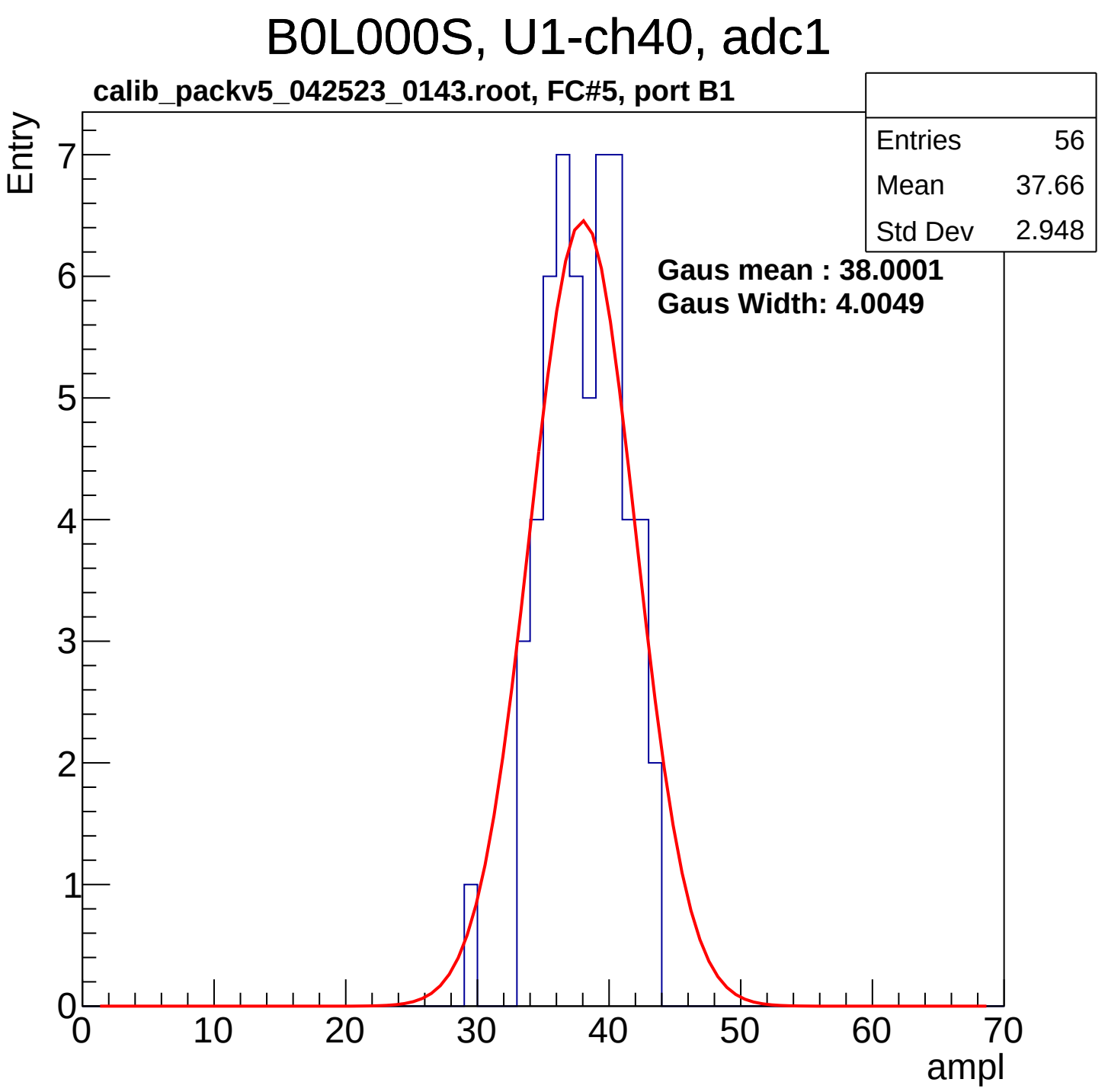
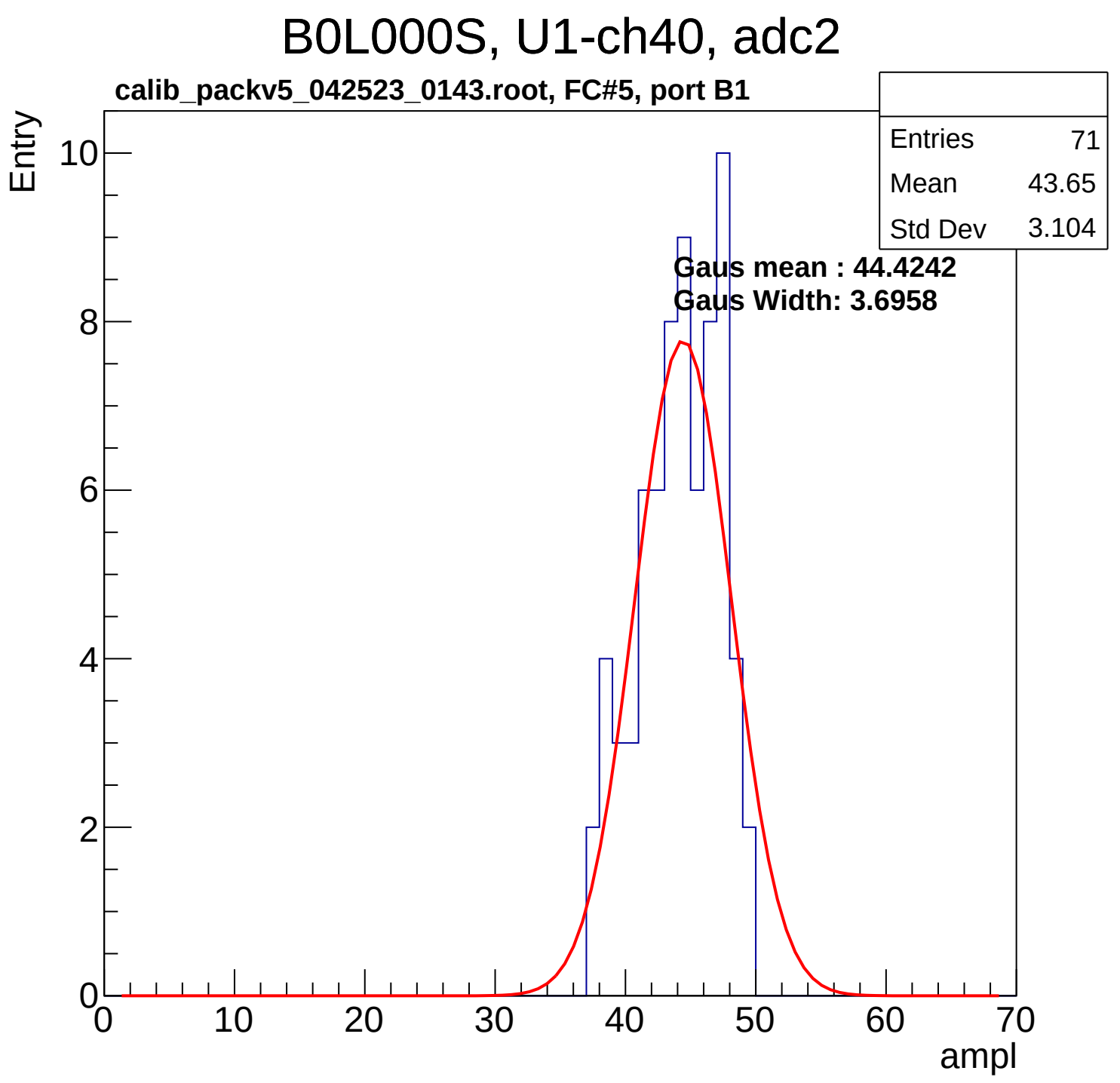
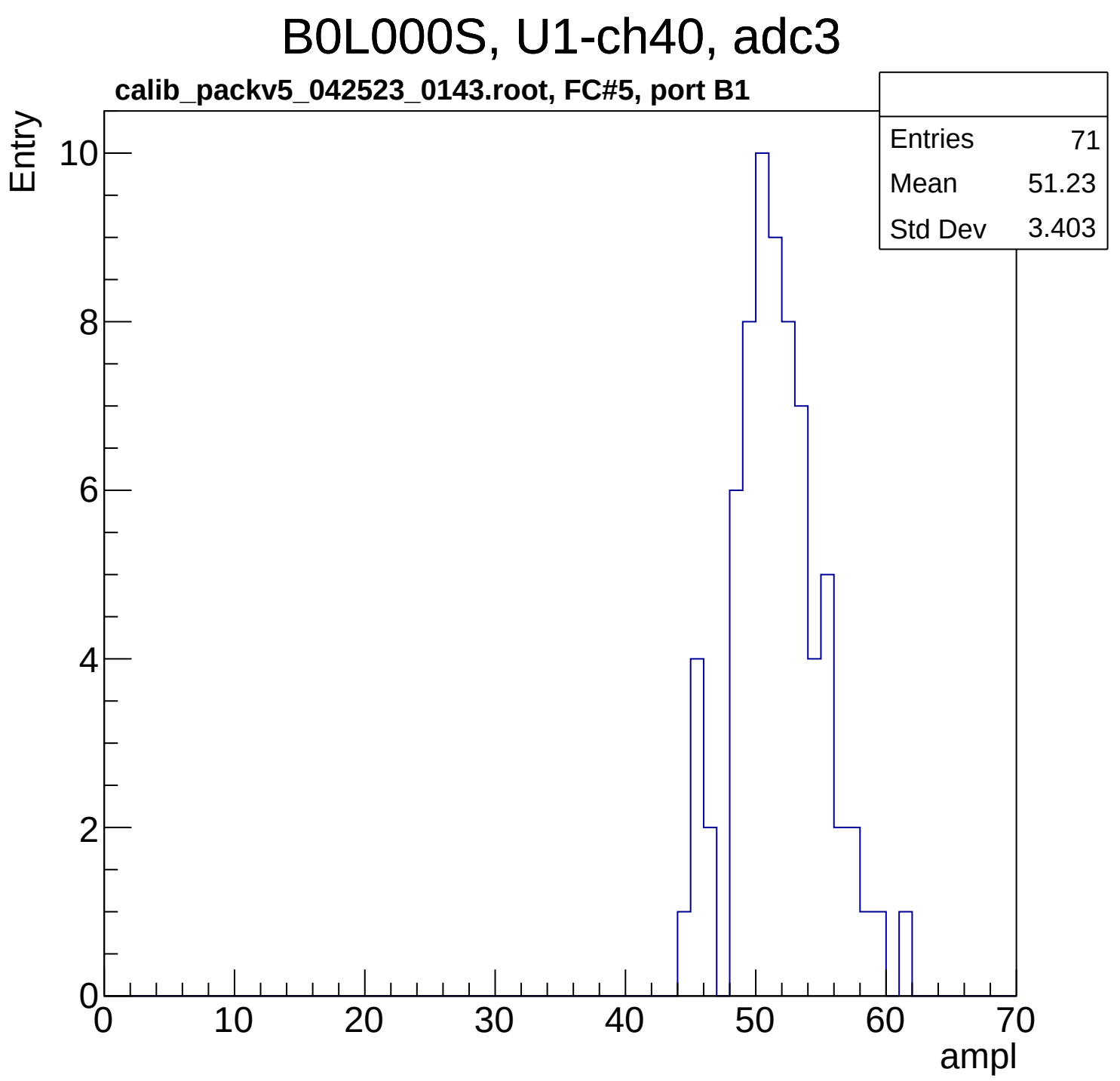
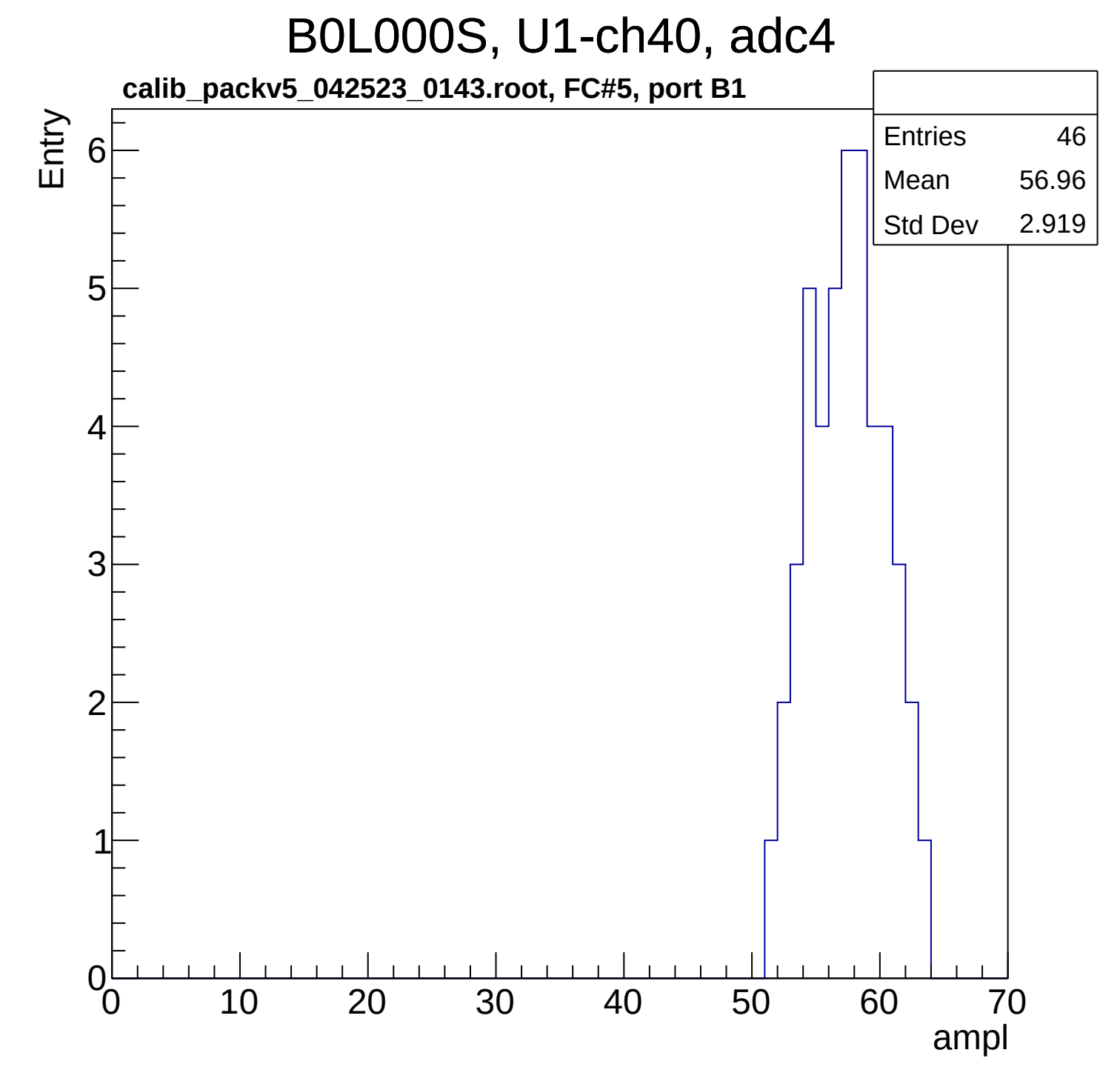
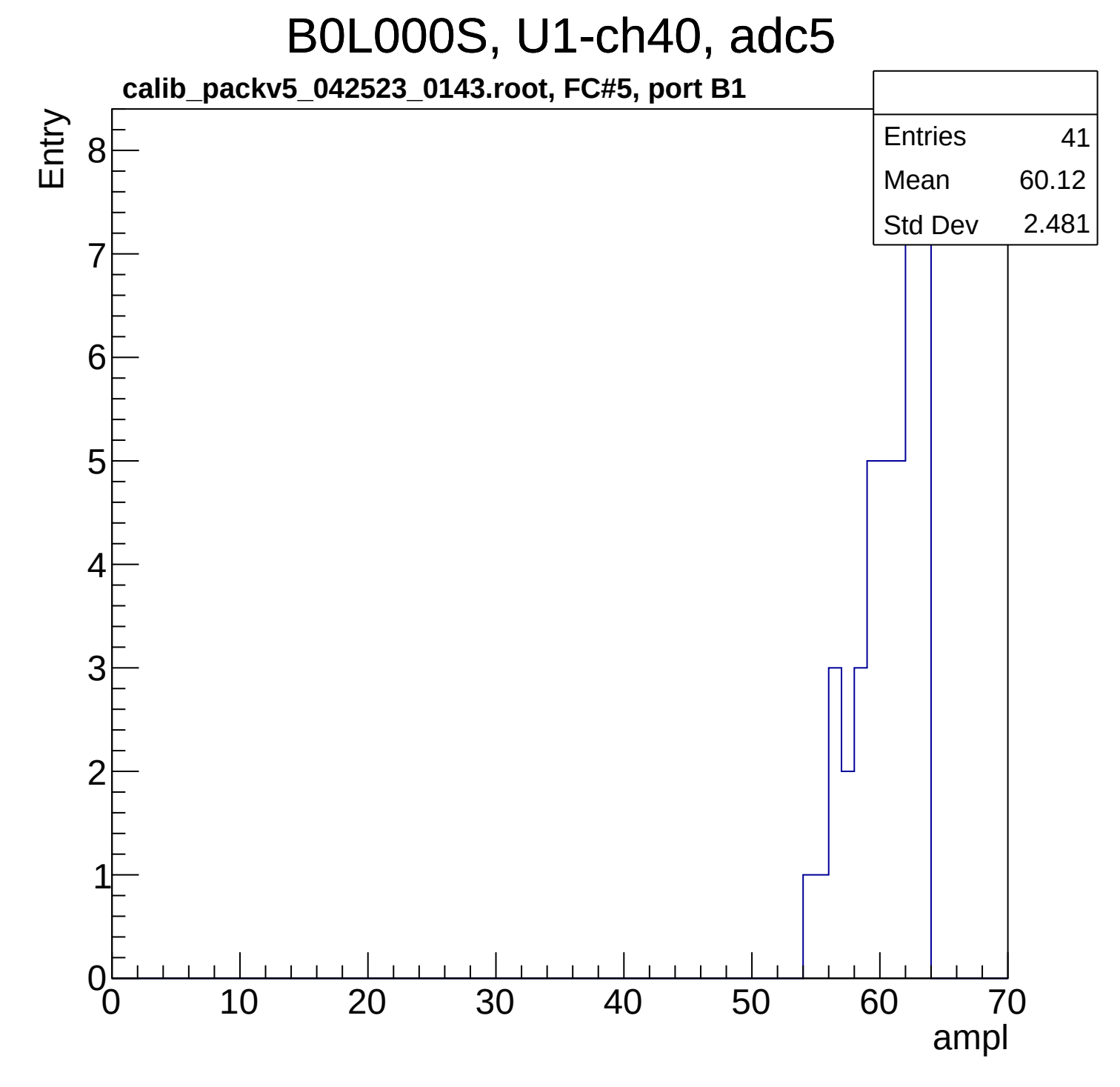

B0L000S, U1-ch40, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.66
Std Dev 2.948
Gaus mean : 38.0001
Gaus Width: 4.0049
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch40, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.65
Std Dev 3.104
Gaus mean : 44.4242
Gaus Width: 3.6958
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch40, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 51.23
Std Dev 3.403
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch40, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 56.96
Std Dev 2.919
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch40, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 60.12
Std Dev 2.481
Entry
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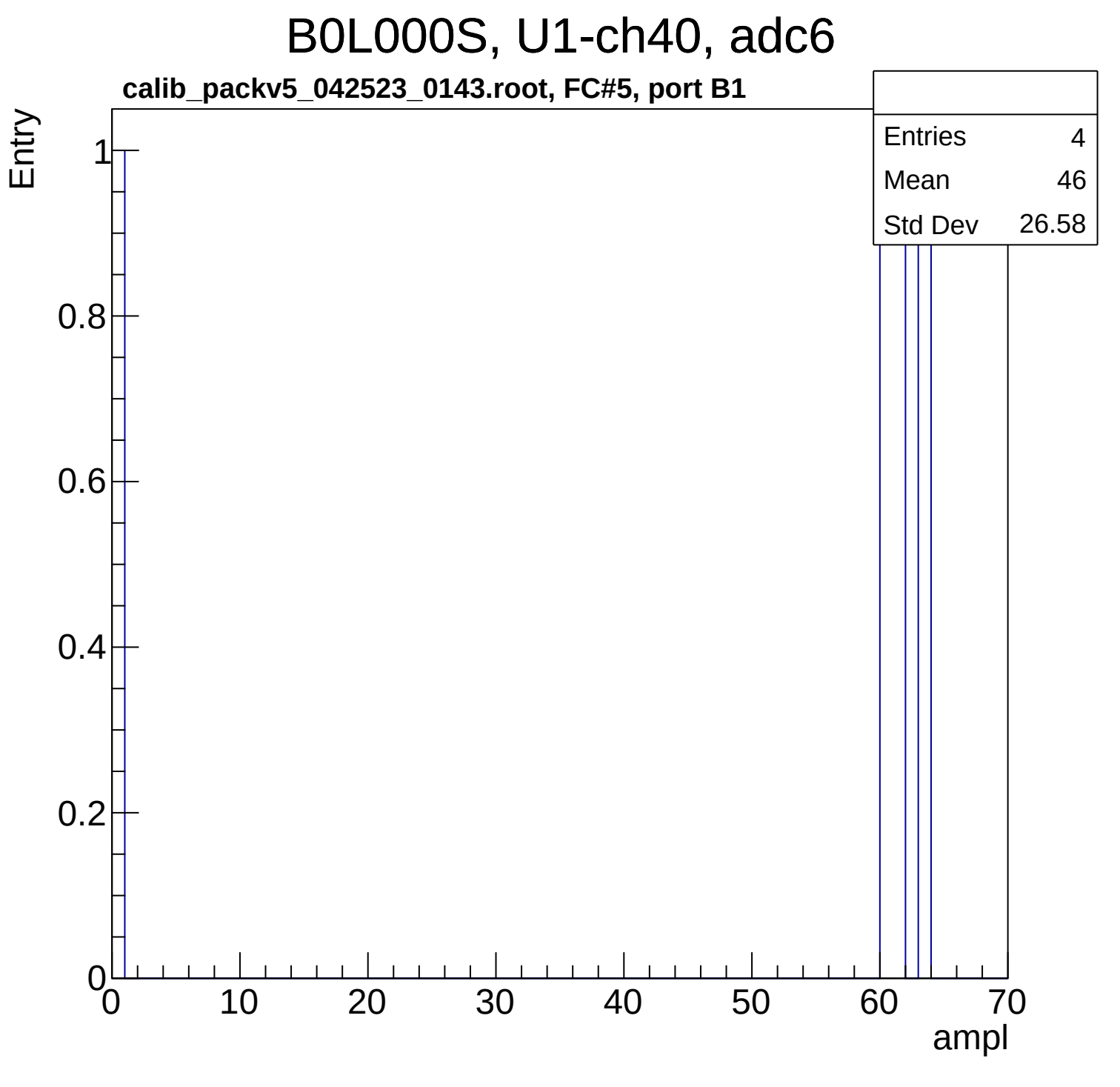
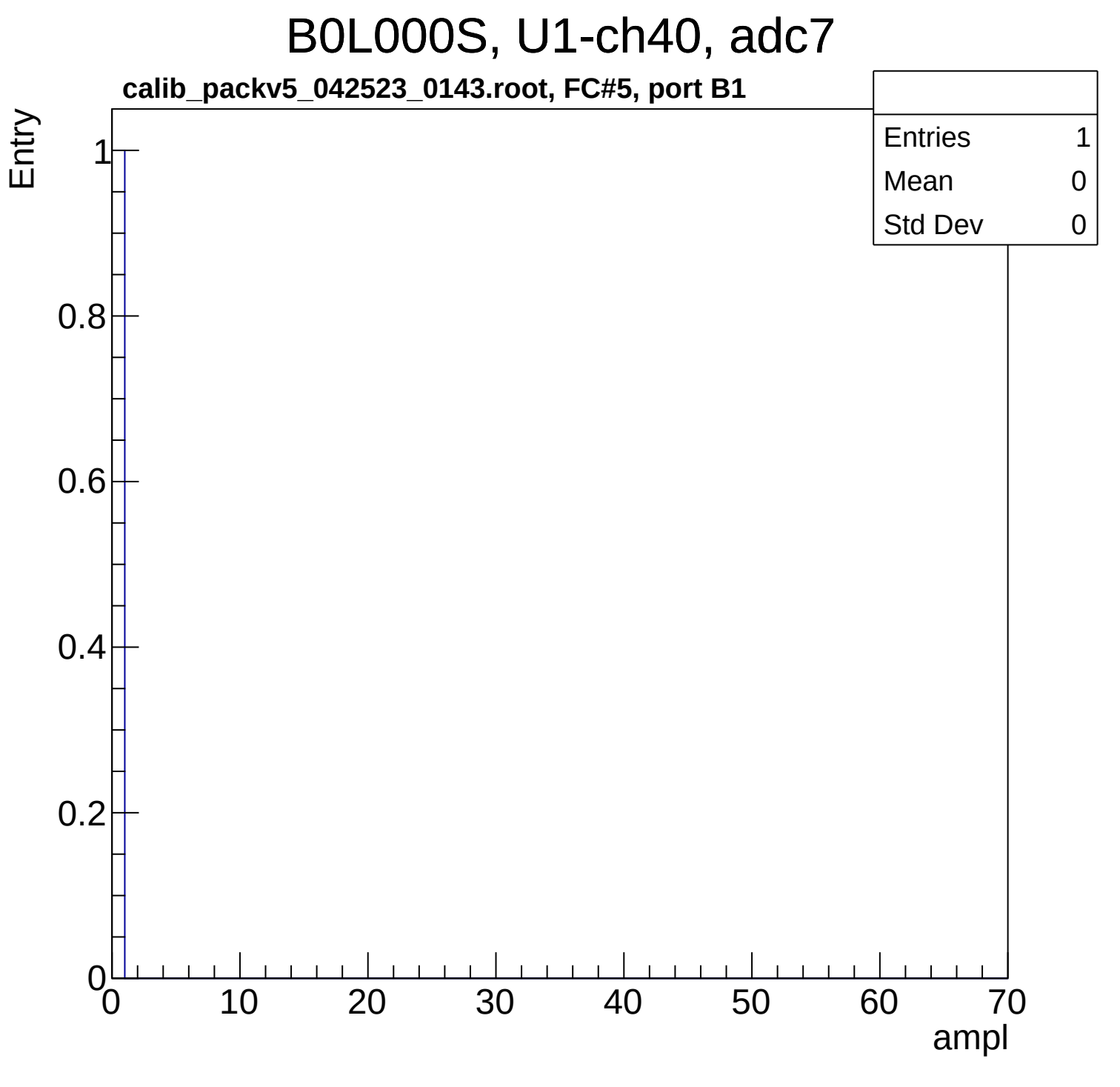
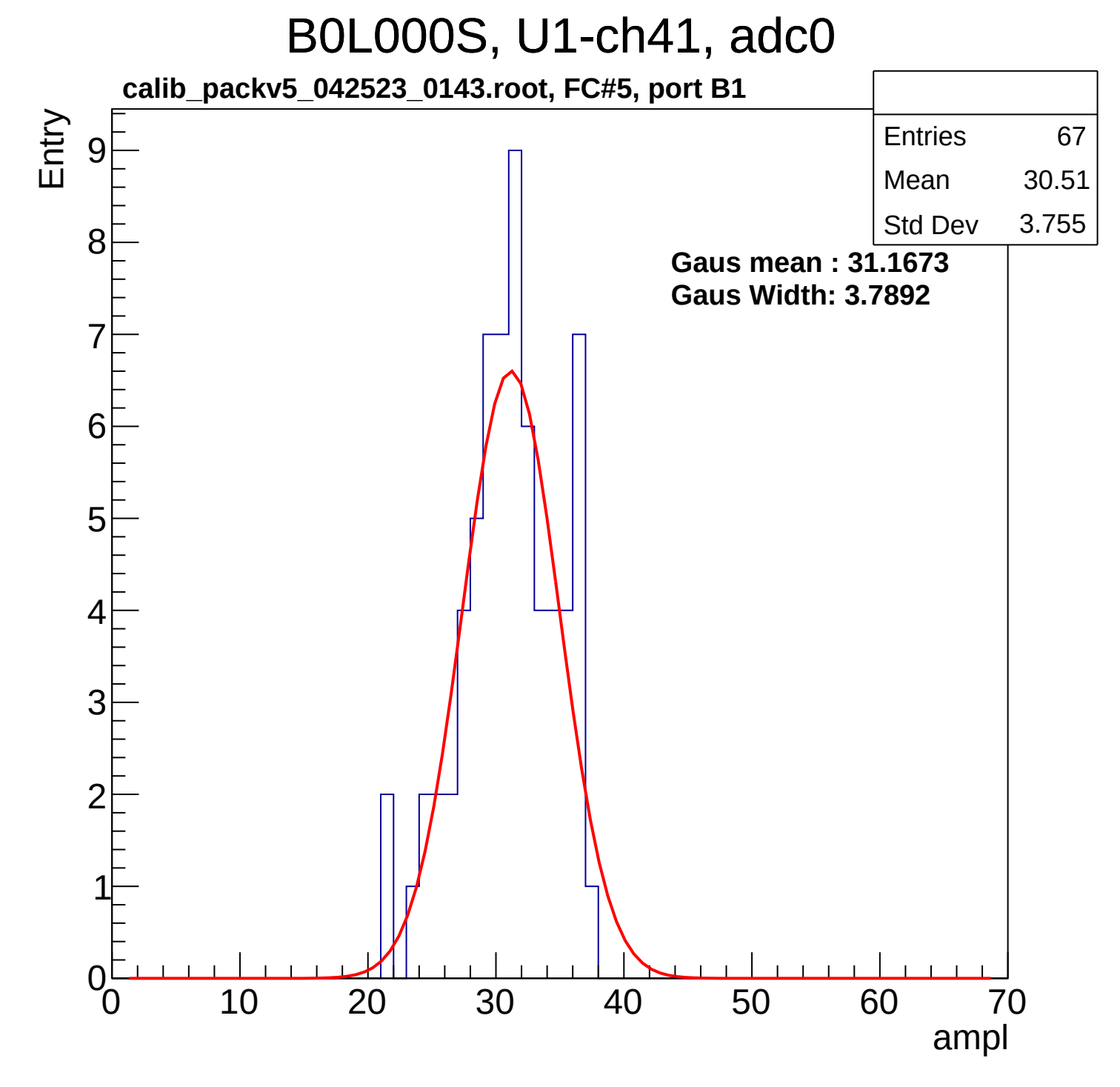
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch40, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46
Std Dev 26.58
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch40, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch41, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.51
Std Dev 3.755
Gaus mean : 31.1673
Gaus Width: 3.7892
Entry
ampl
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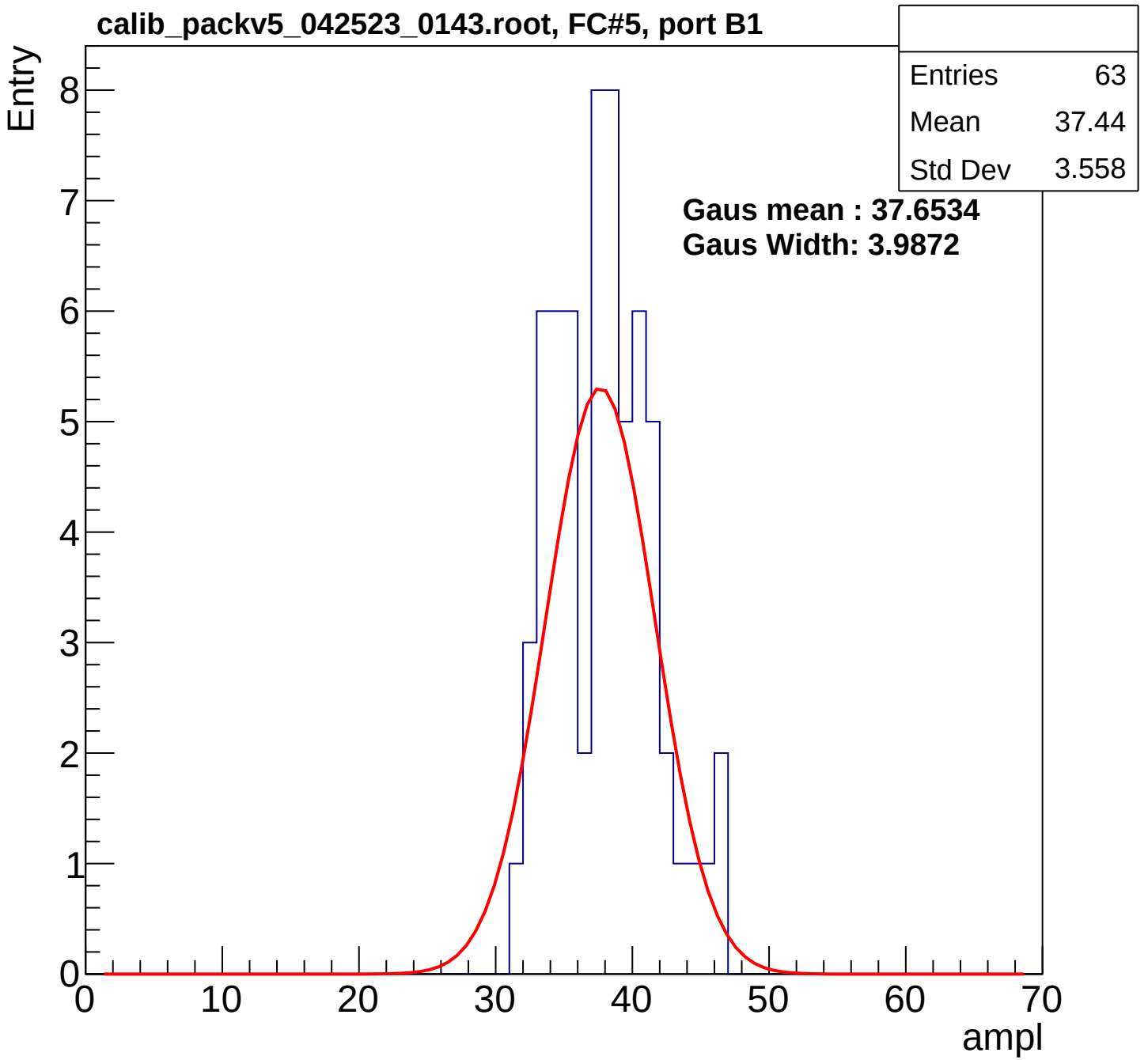
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch41, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 37.44
Std Dev 3.558
Gaus mean : 37.6534
Gaus Width: 3.9872
Entry
ampl
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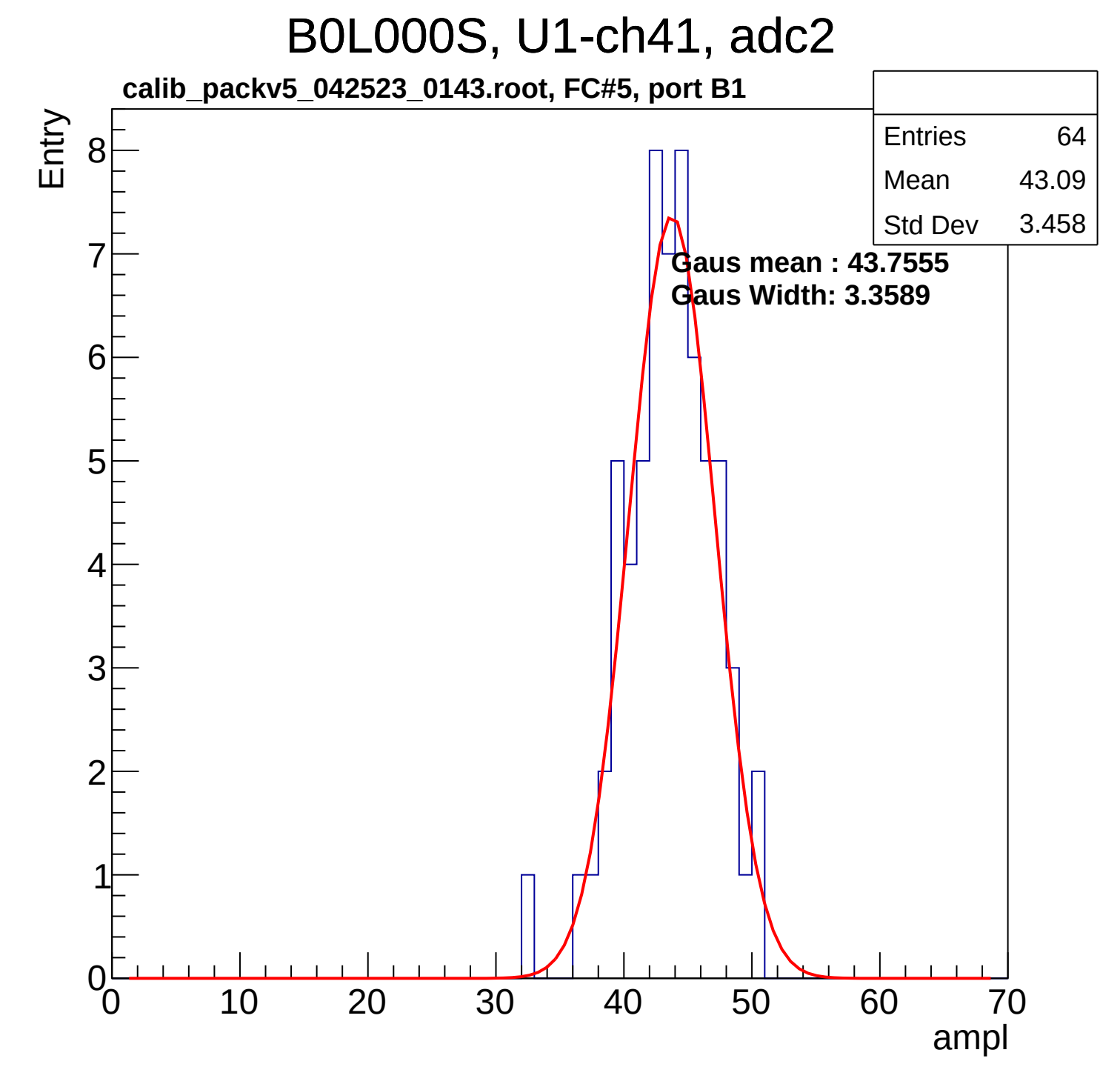
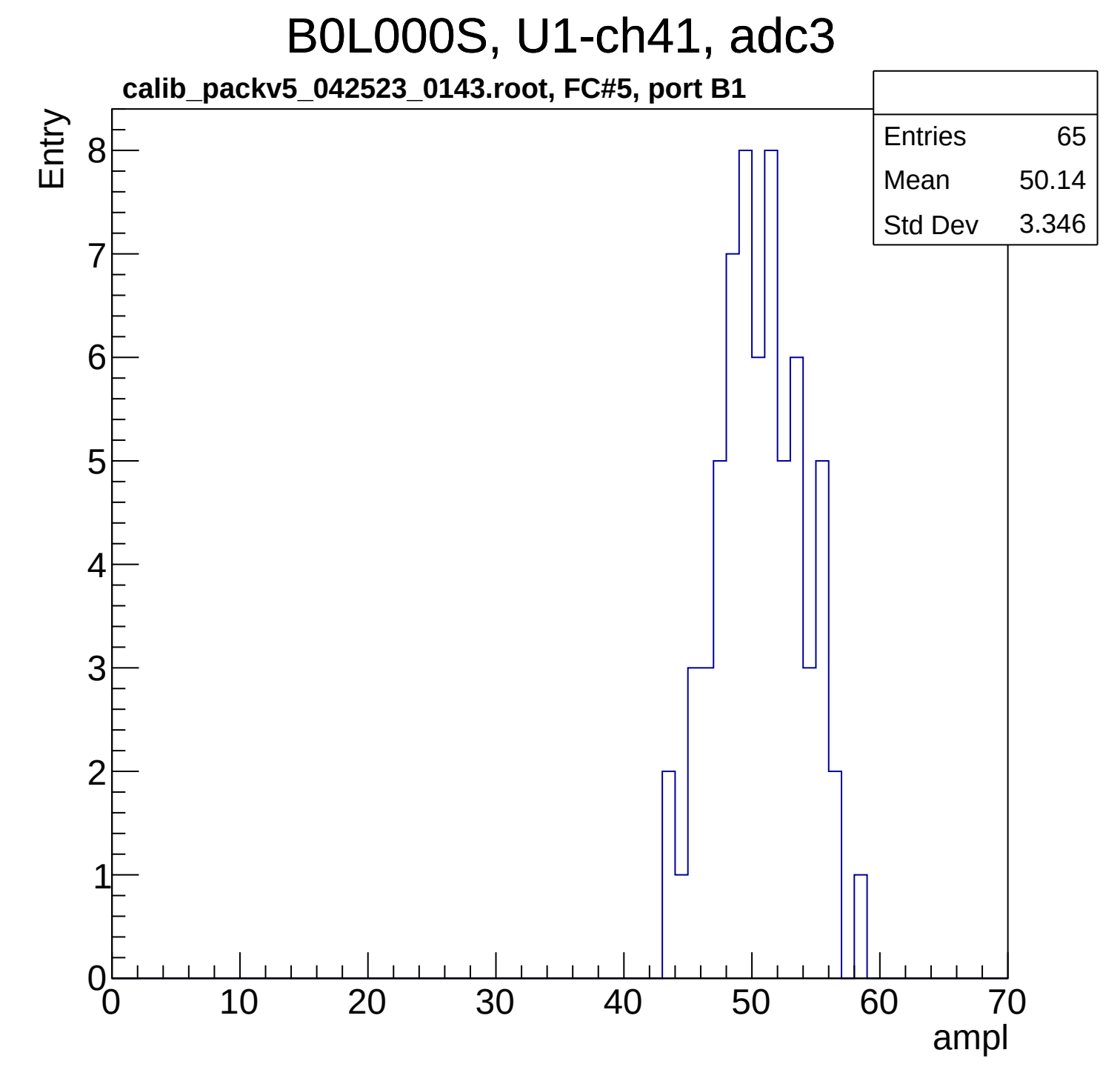
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch41, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.09
Std Dev 3.458
Gaus mean : 43.7555
Gaus Width: 3.3589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch41, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.14
Std Dev 3.346
Entry
ampl
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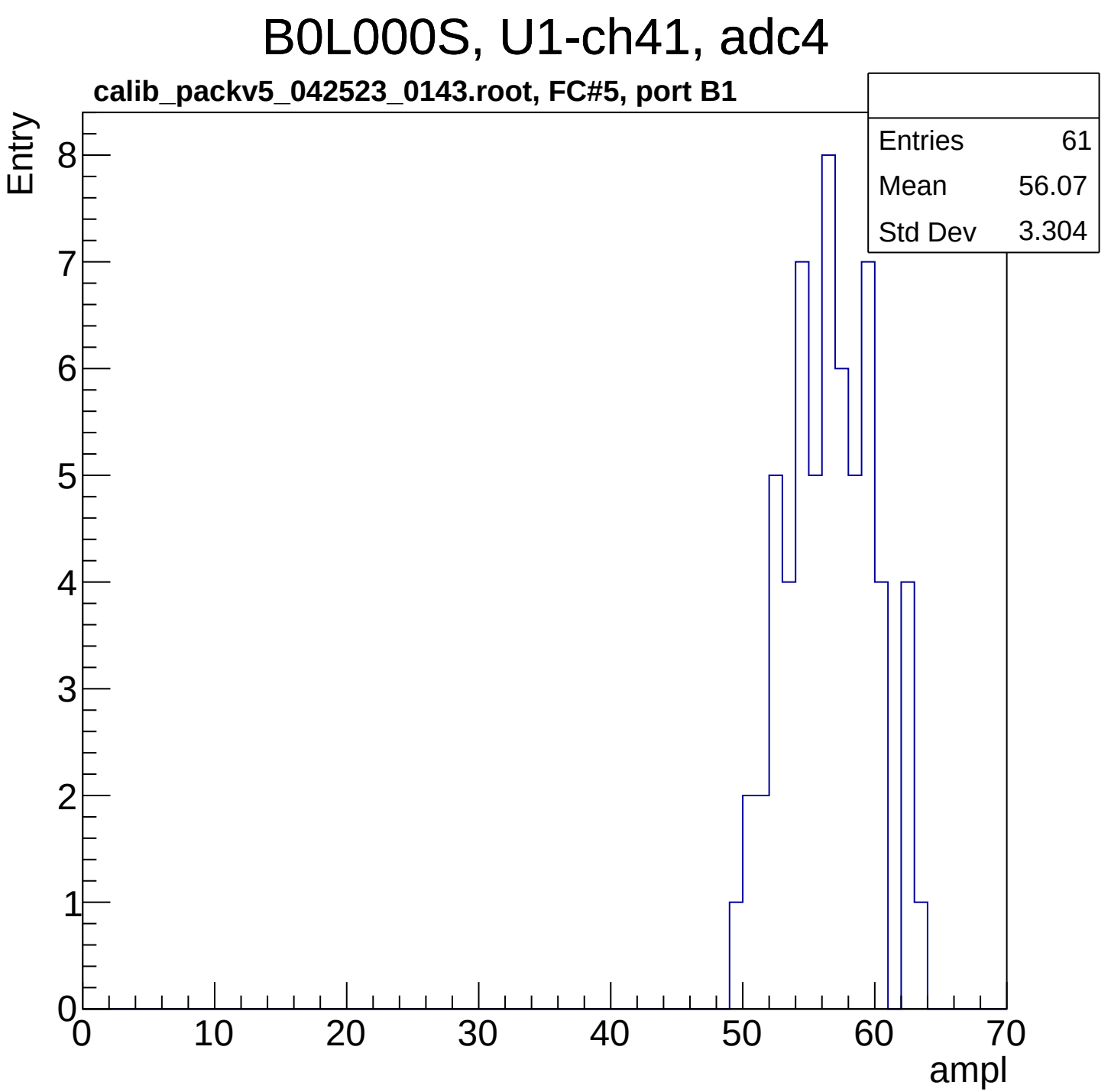
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch41, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.07
Std Dev 3.304
Entry
ampl
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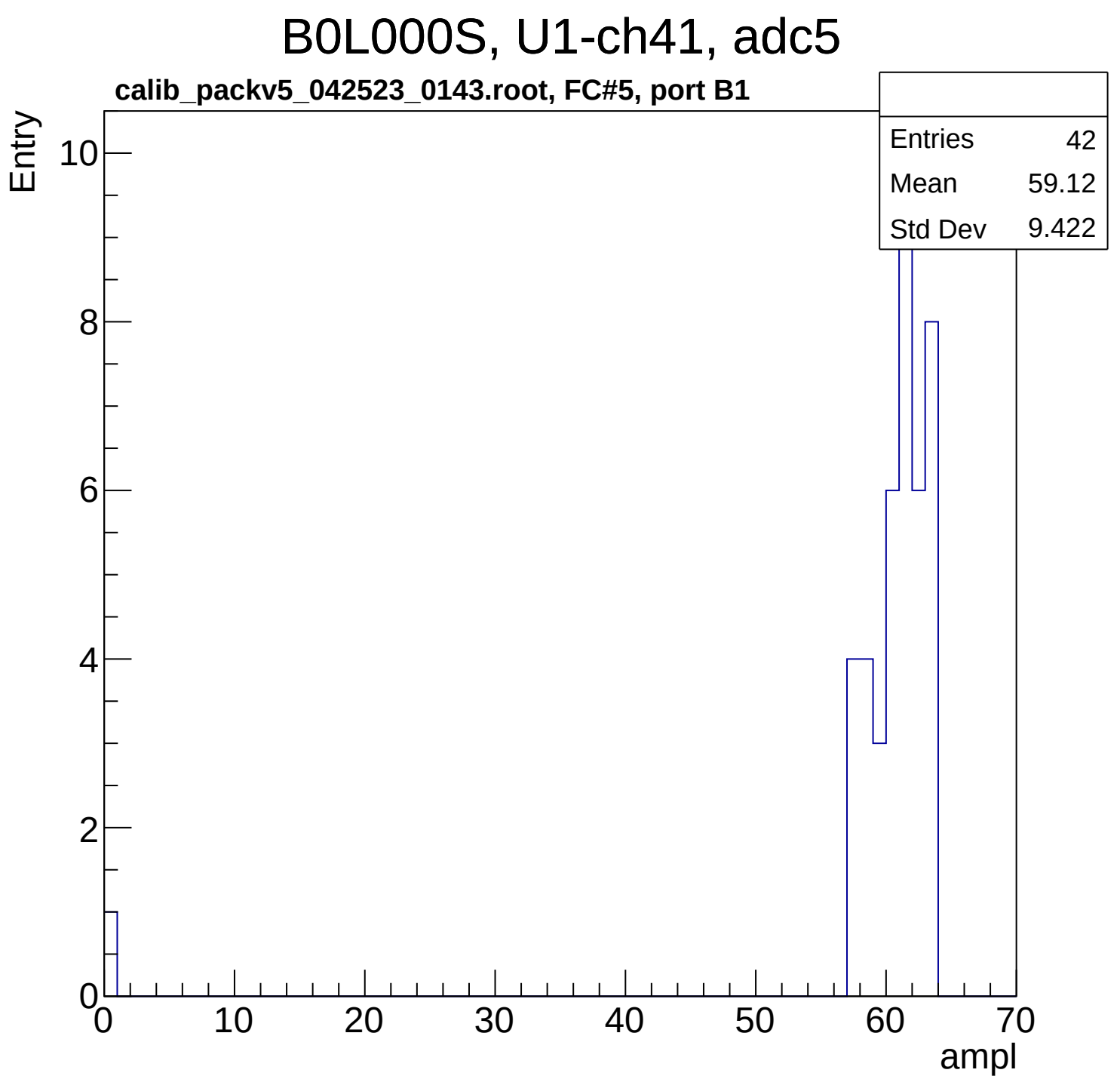
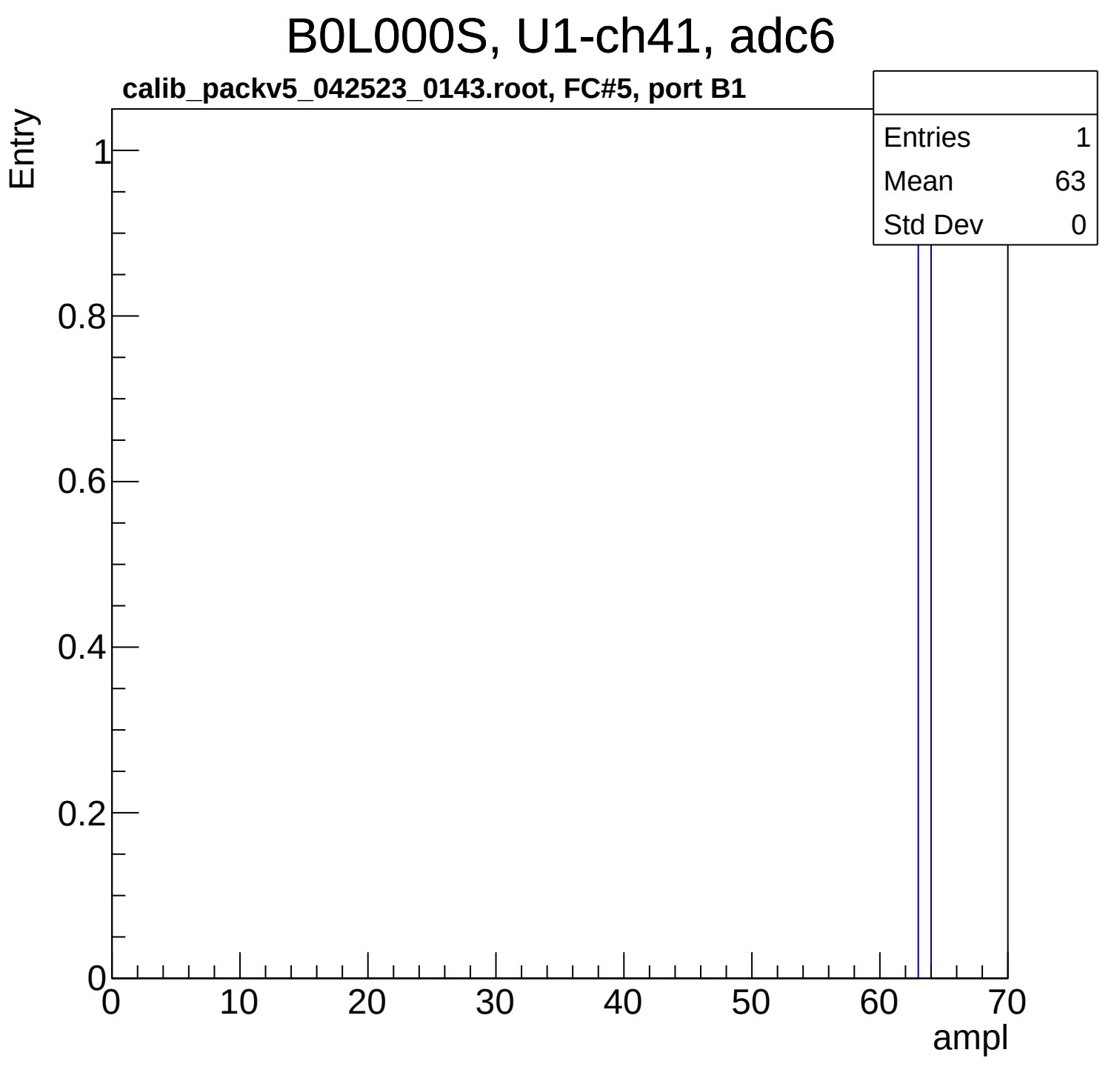
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch41, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.12
Std Dev 9.422
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch41, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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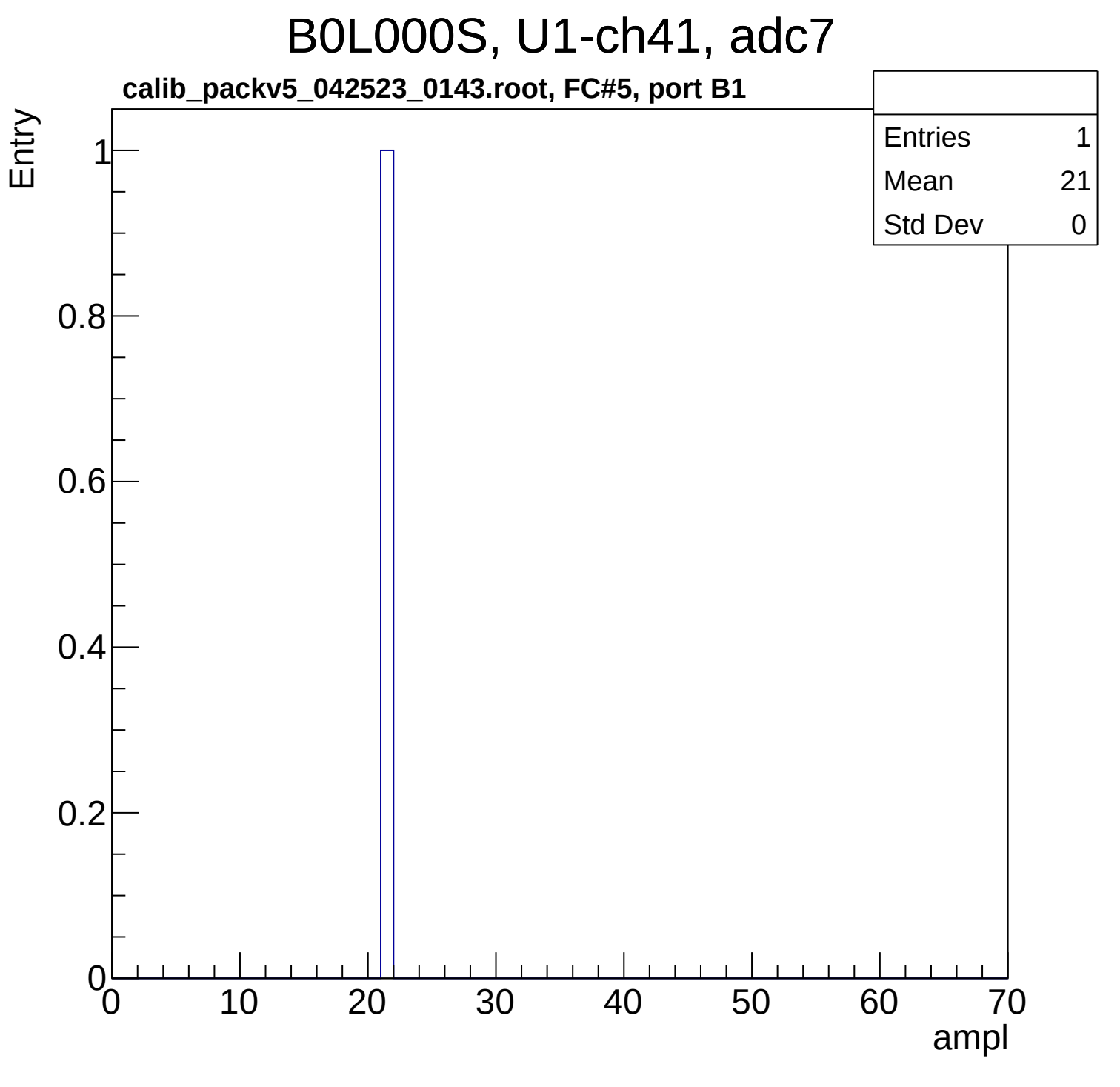
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch41, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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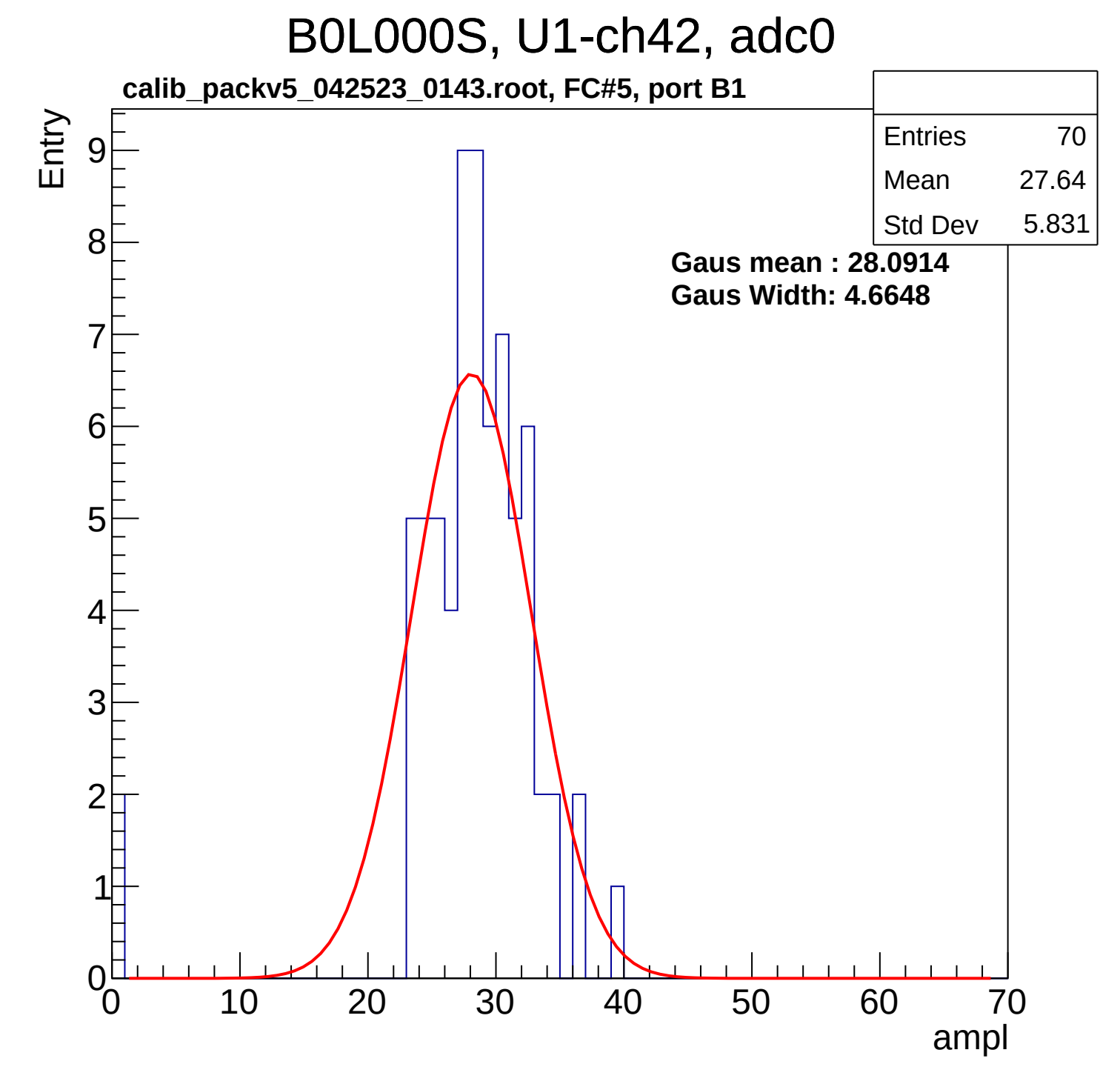
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch42, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 27.64
Std Dev 5.831
Gaus mean : 28.0914
Gaus Width: 4.6648
Entry
ampl
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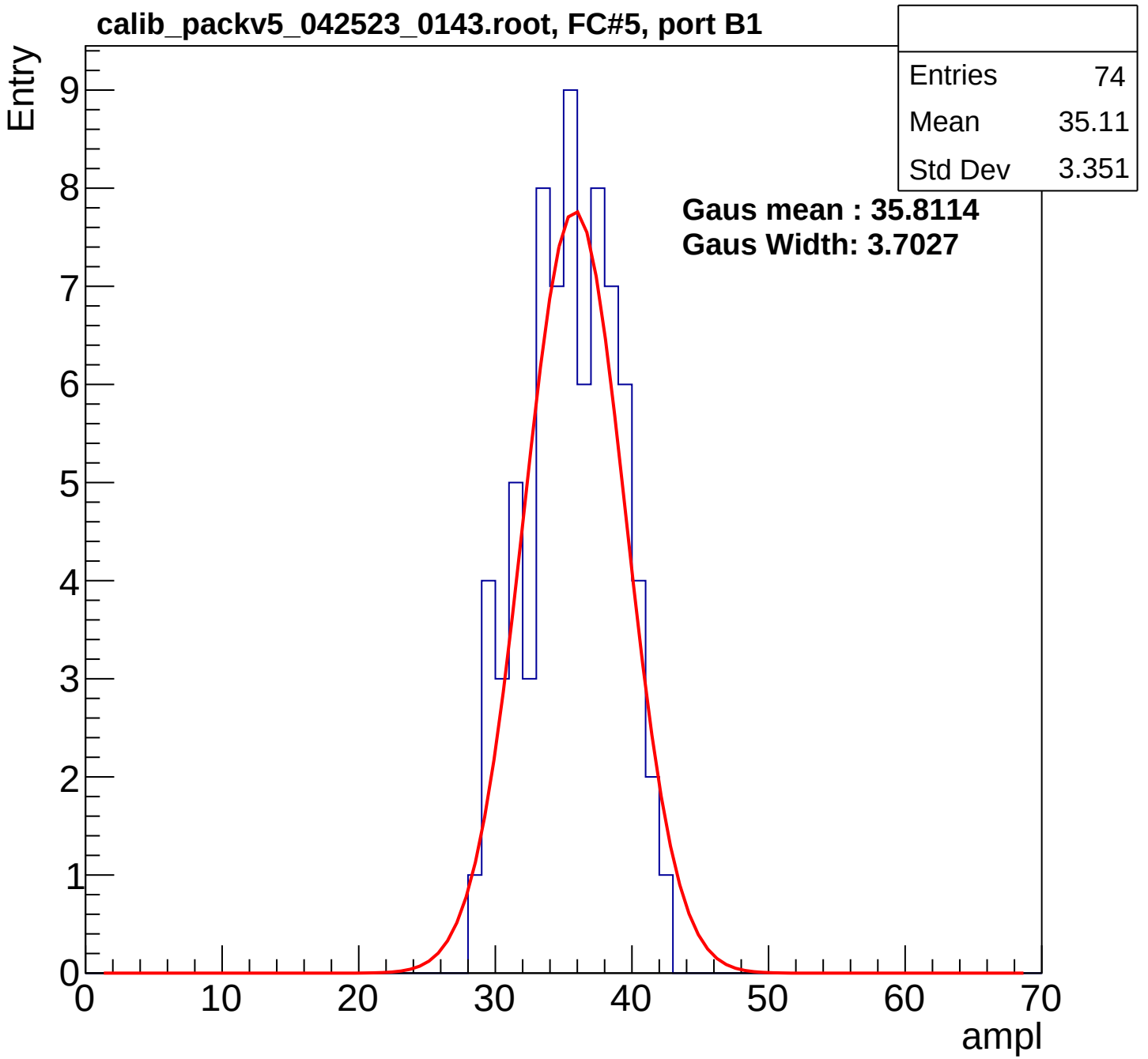
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch42, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 35.11
Std Dev 3.351
Gaus mean : 35.8114
Gaus Width: 3.7027
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
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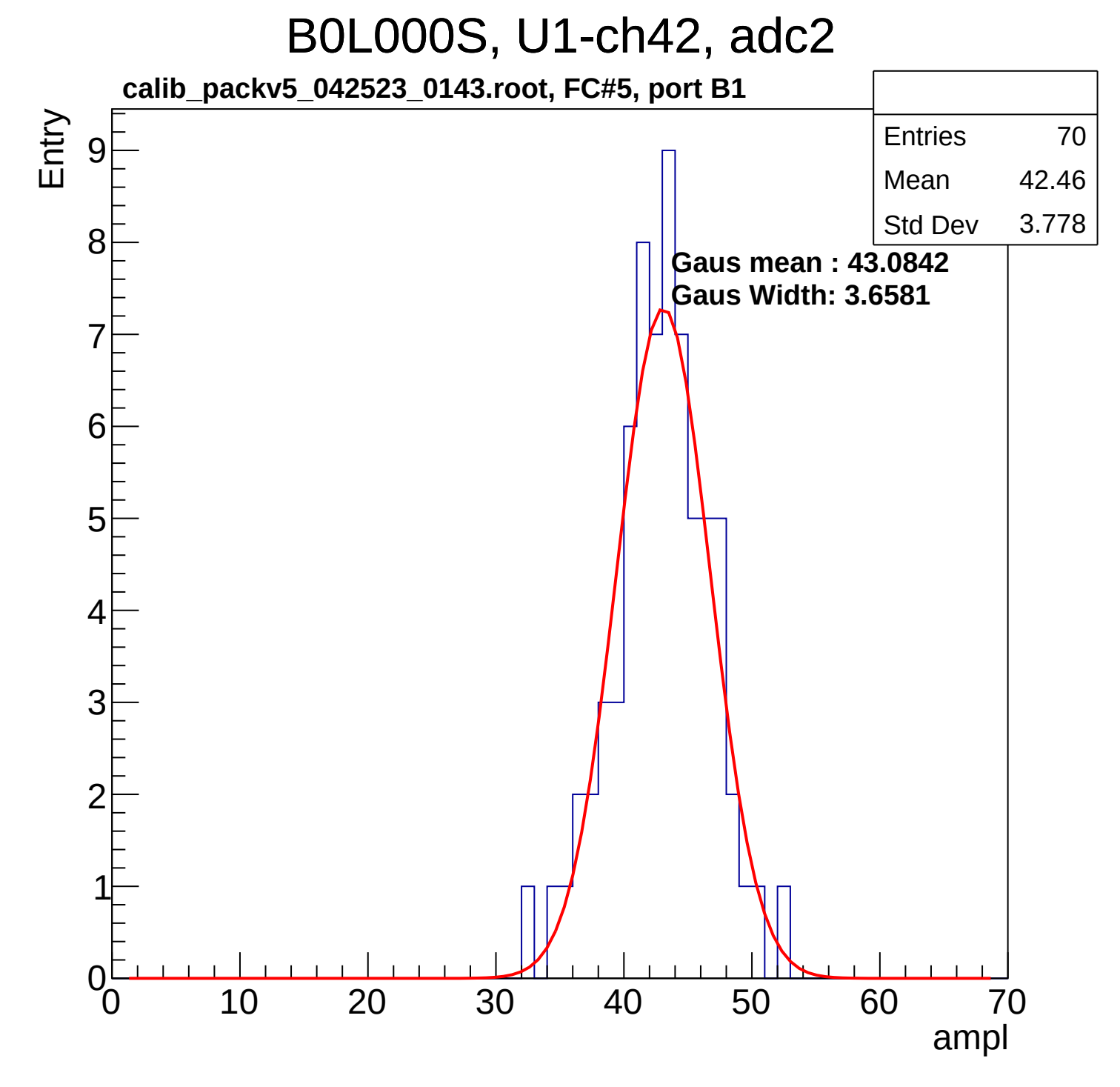
B0L000S, U1-ch42, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.46
Std Dev 3.778
Gaus mean : 43.0842
Gaus Width: 3.6581
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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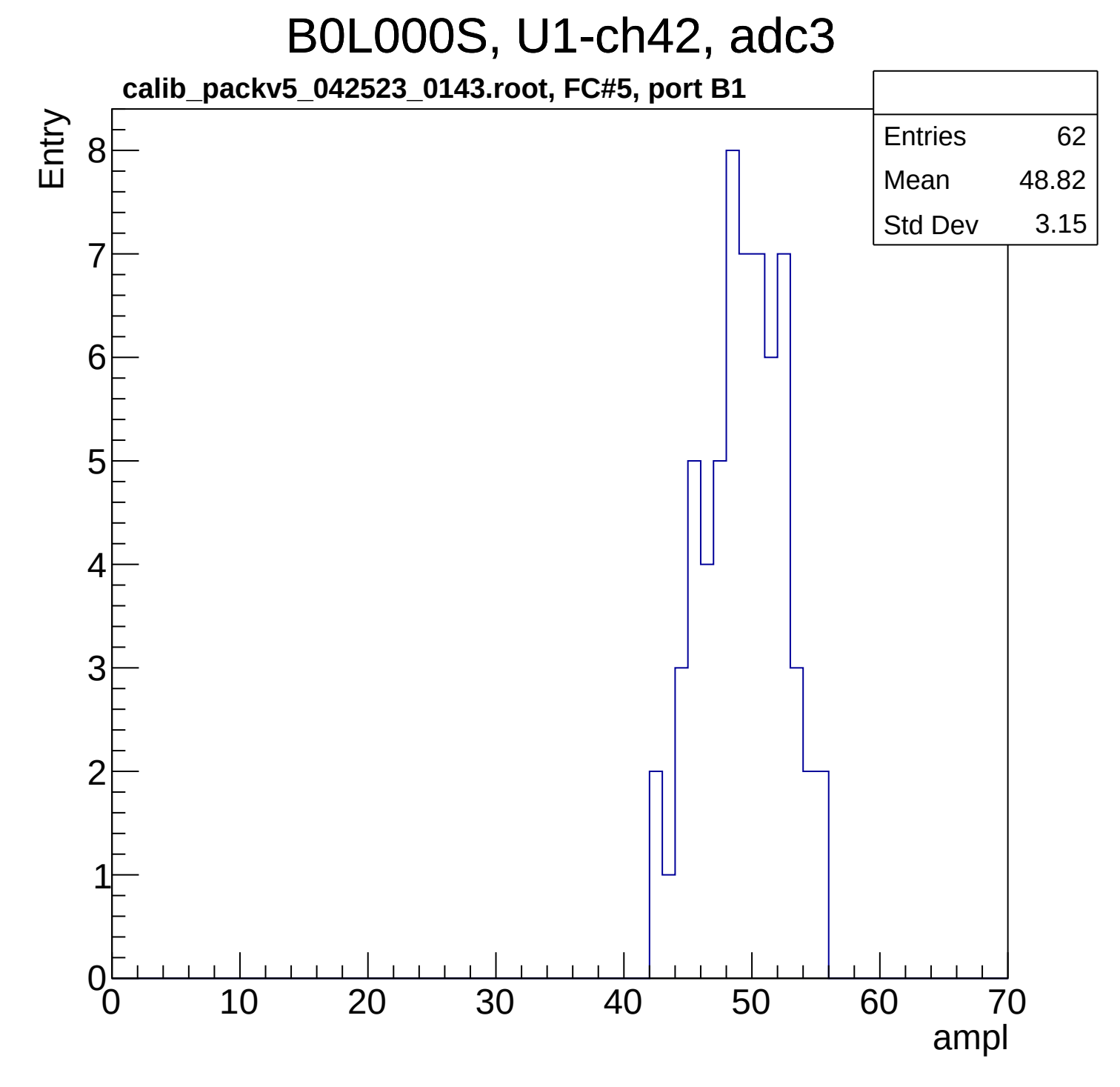
B0L000S, U1-ch42, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.82
Std Dev 3.15
Entry
ampl
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2
3
4
5
6
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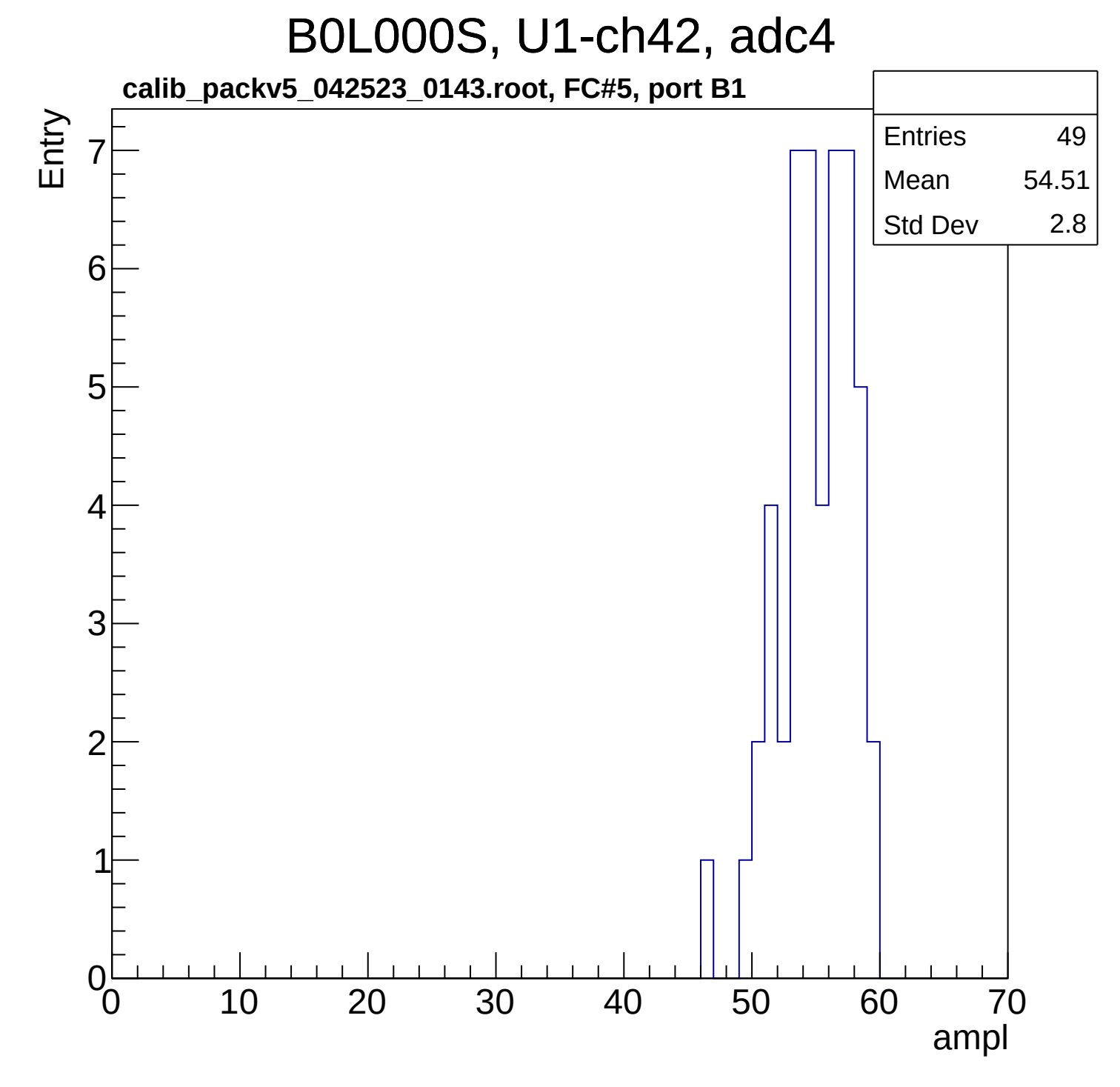
B0L000S, U1-ch42, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 54.51
Std Dev 2.8
Entry
ampl
0
1
2
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4
5
6
7
0
10
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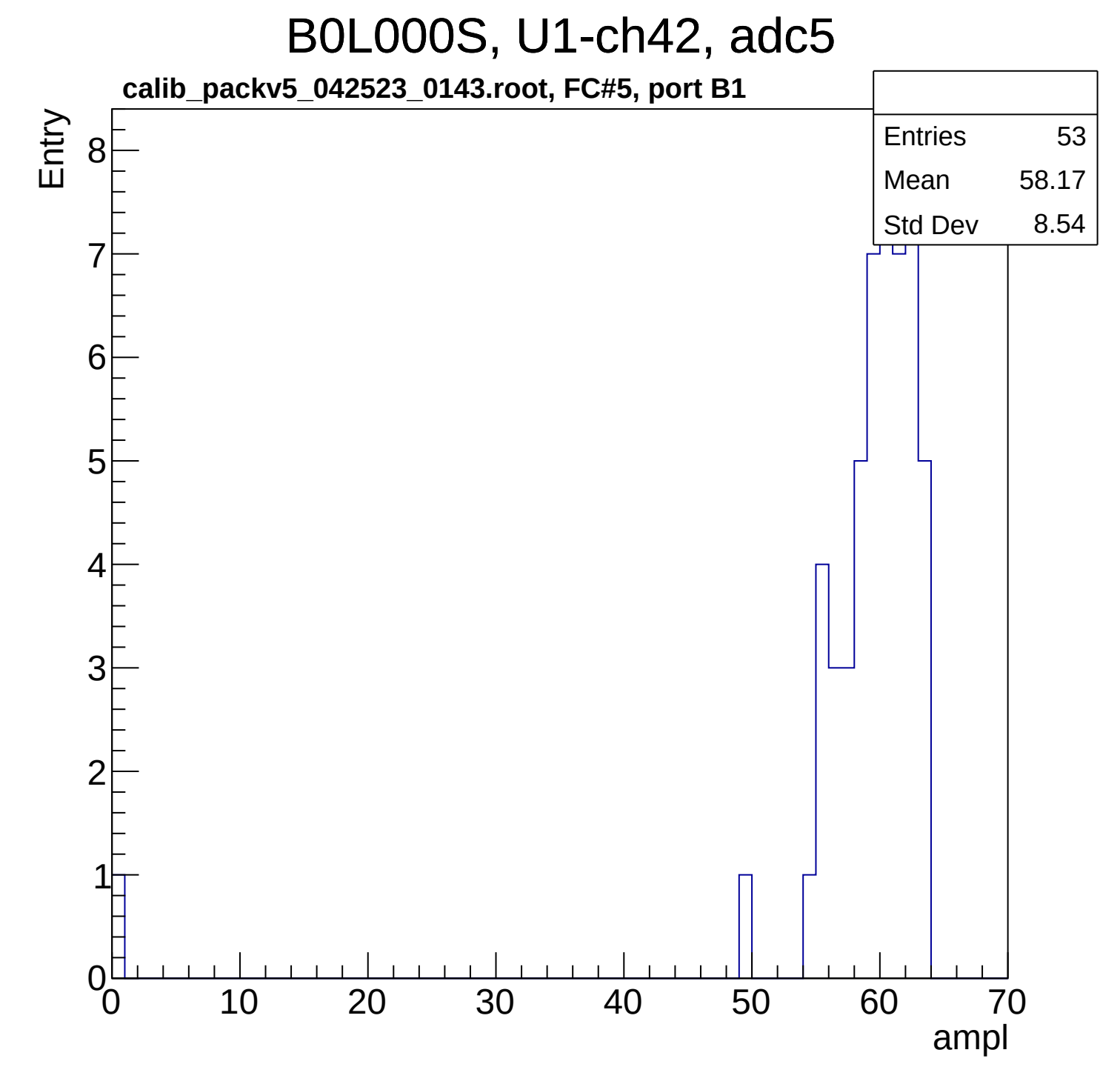
40
50
60
70


B0L000S, U1-ch42, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.17
Std Dev 8.54
Entry
ampl
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2
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40
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60
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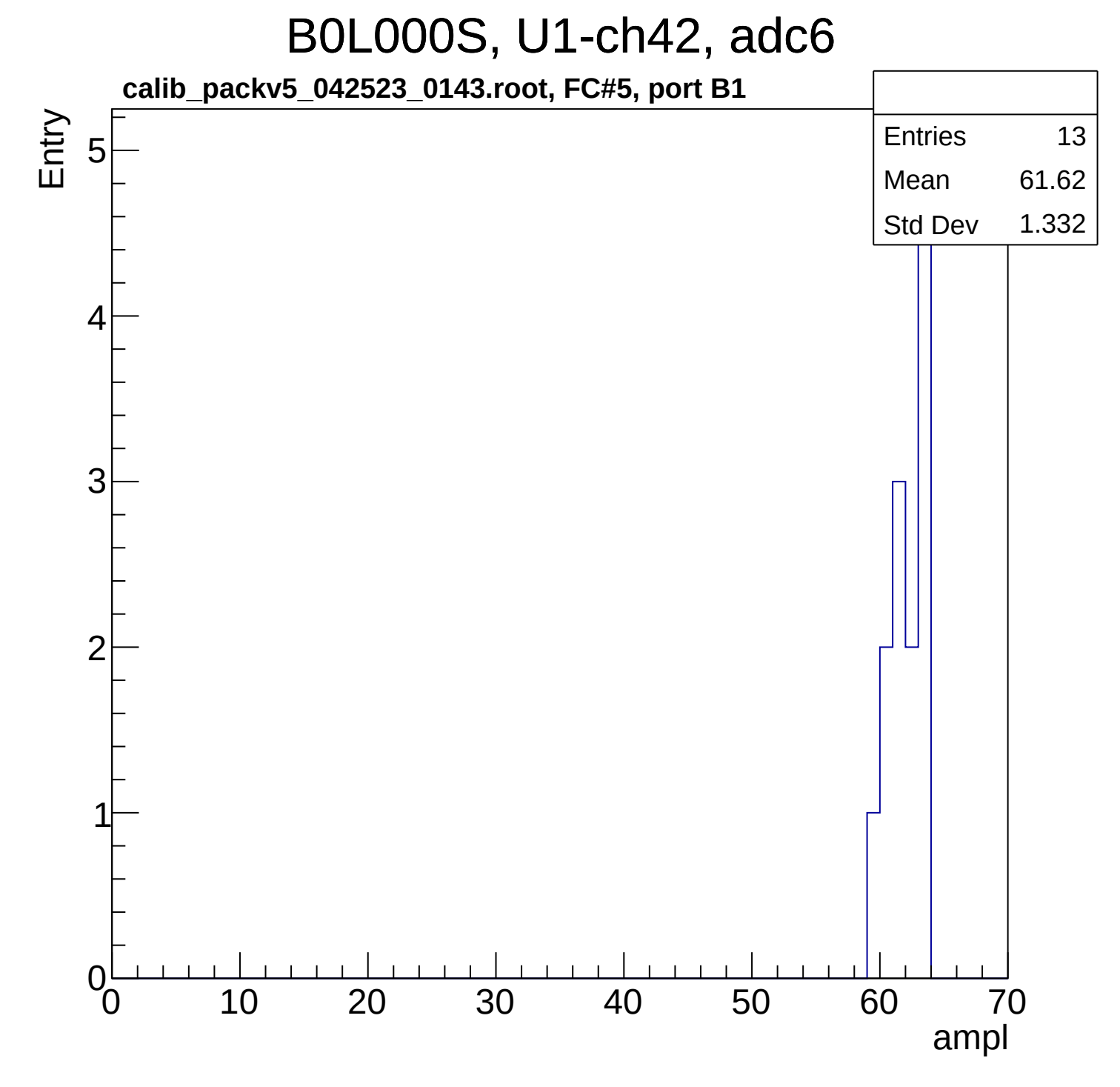
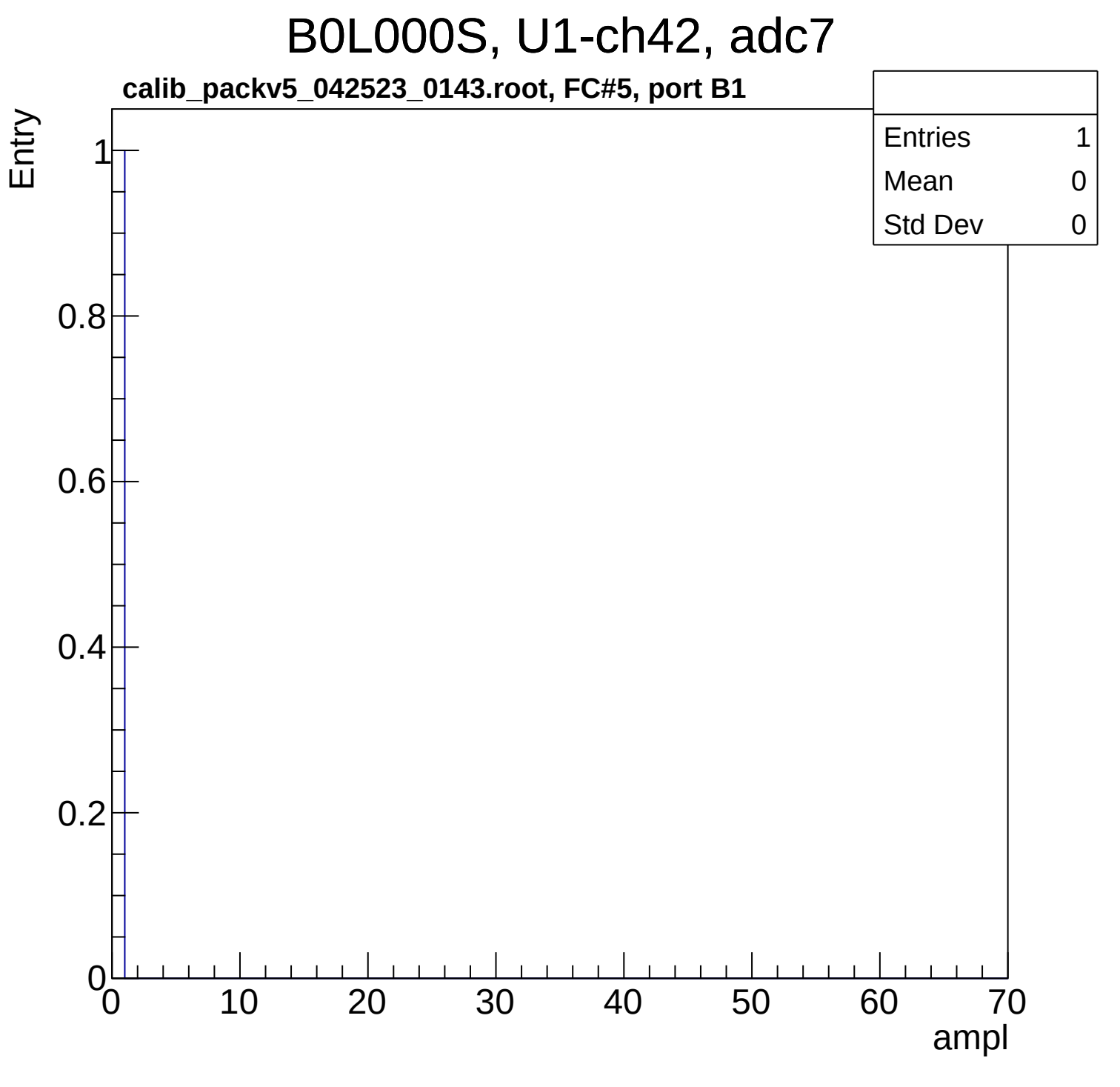
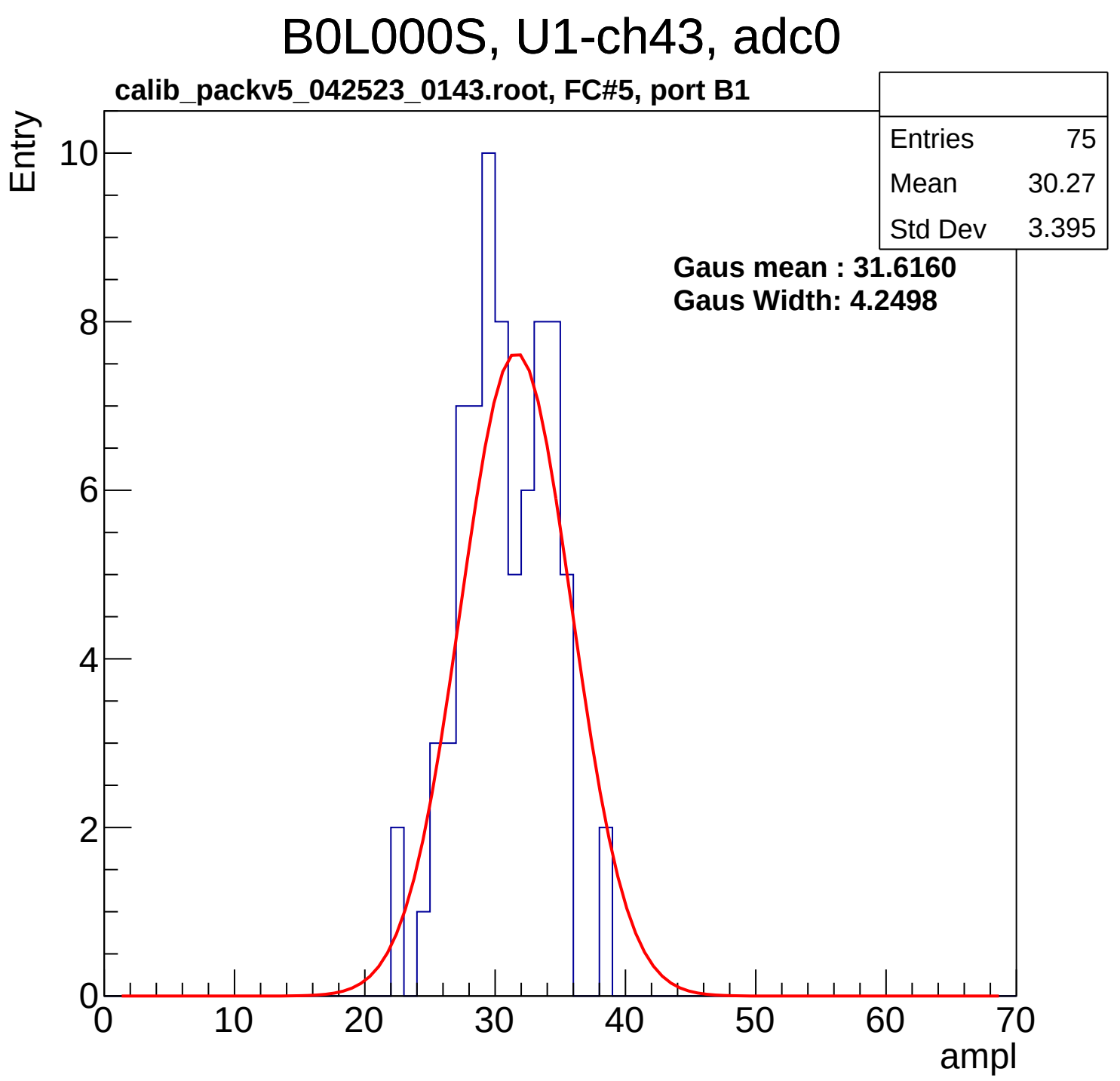

B0L000S, U1-ch42, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.62
Std Dev 1.332
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch42, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch43, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 30.27
Std Dev 3.395
Gaus mean : 31.6160
Gaus Width: 4.2498
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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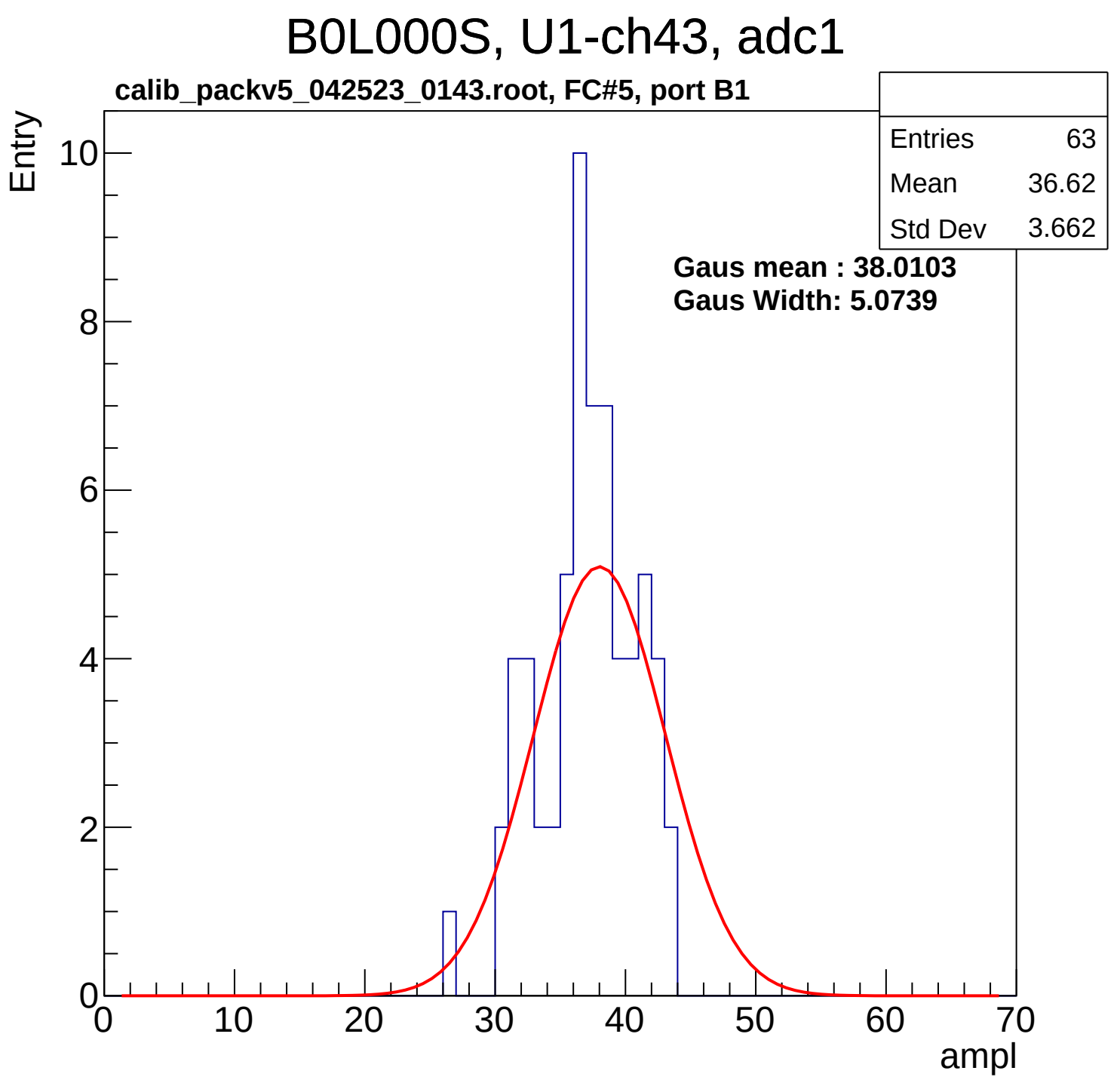
B0L000S, U1-ch43, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.62
Std Dev 3.662
Gaus mean : 38.0103
Gaus Width: 5.0739
Entry
ampl
0
2
4
6
8
10
0
10
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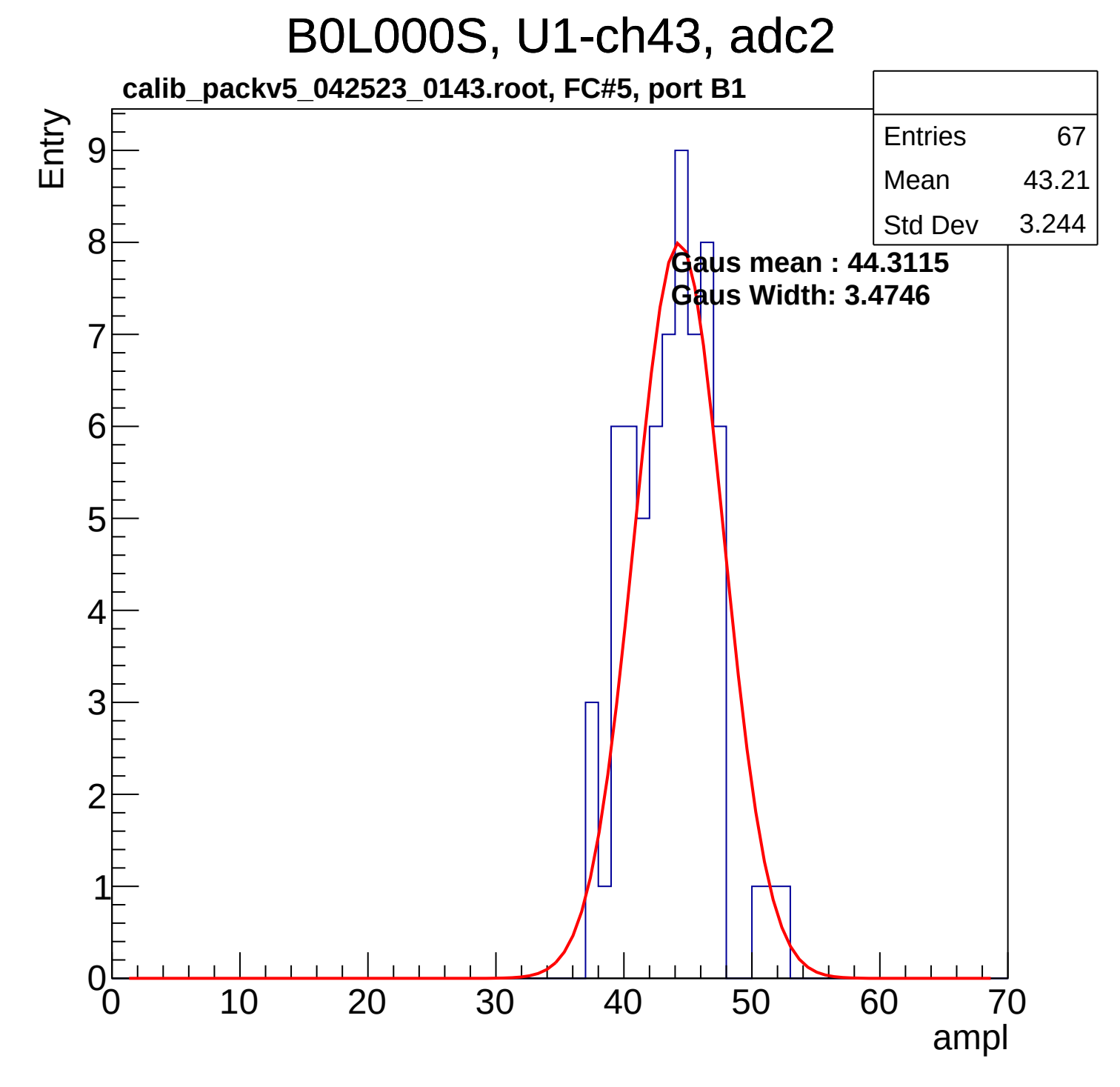
B0L000S, U1-ch43, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.21
Std Dev 3.244
Gaus mean : 44.3115
Gaus Width: 3.4746
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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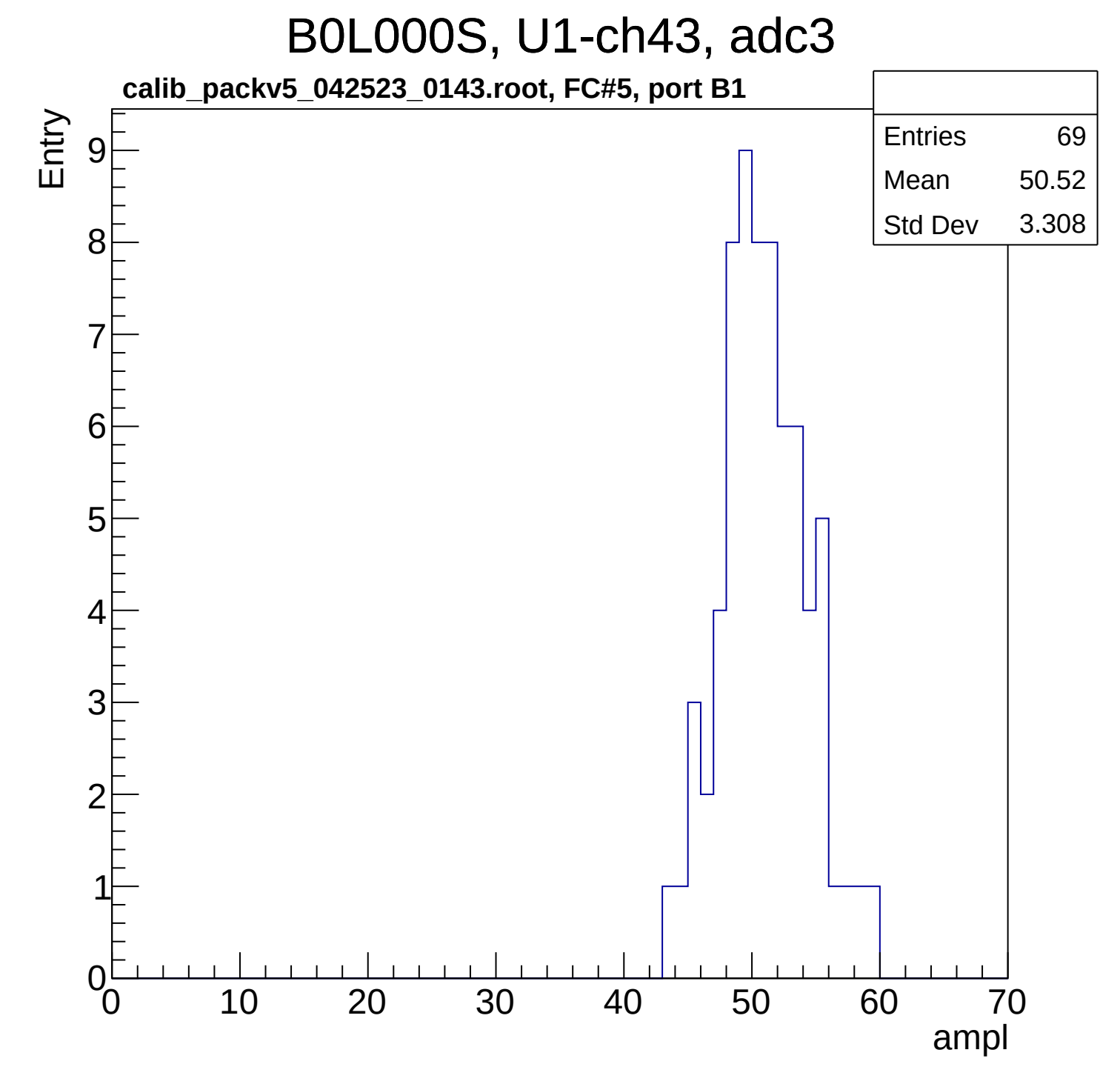
B0L000S, U1-ch43, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.52
Std Dev 3.308
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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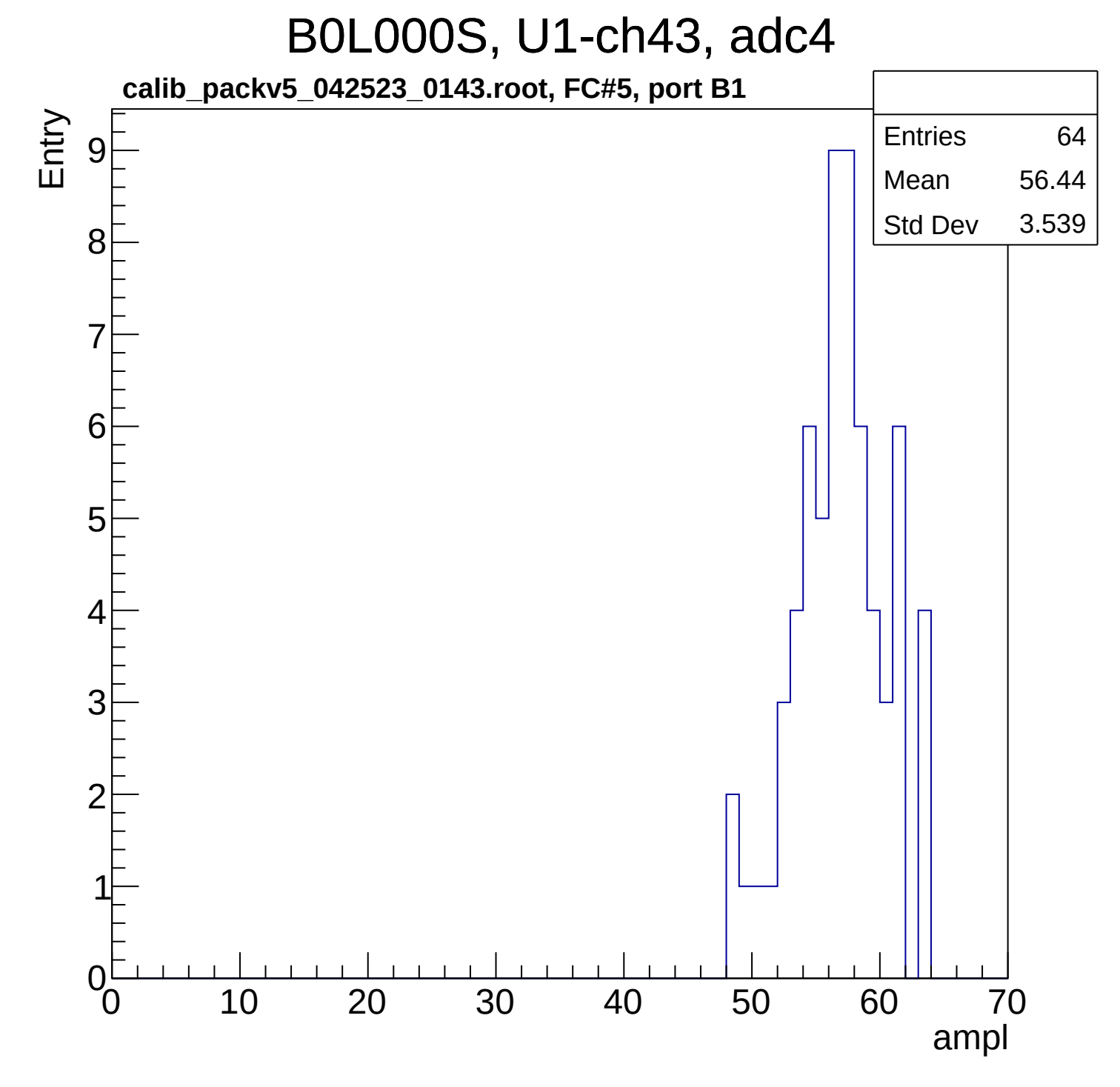
B0L000S, U1-ch43, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 56.44
Std Dev 3.539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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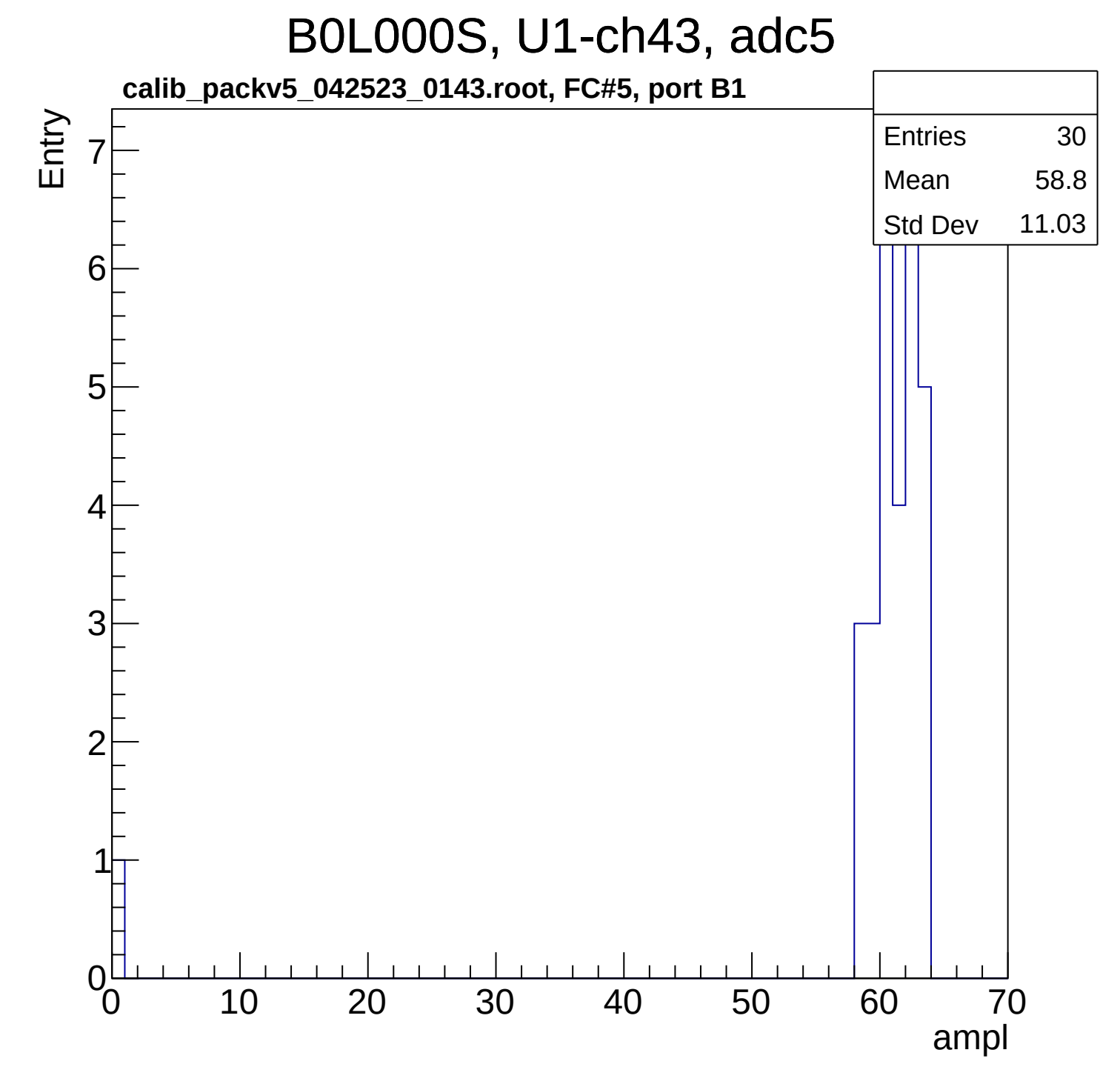
B0L000S, U1-ch43, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.8
Std Dev 11.03
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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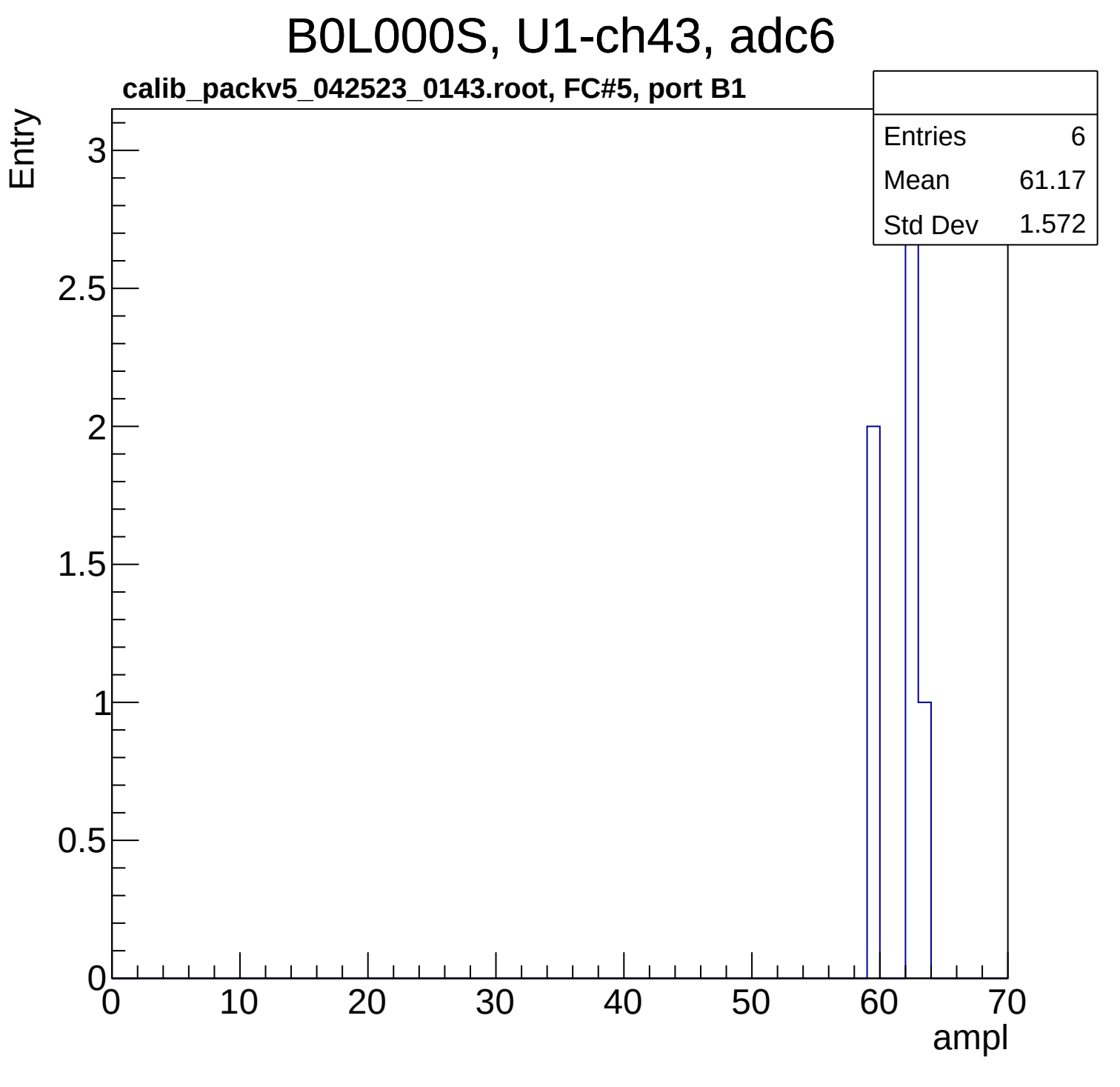
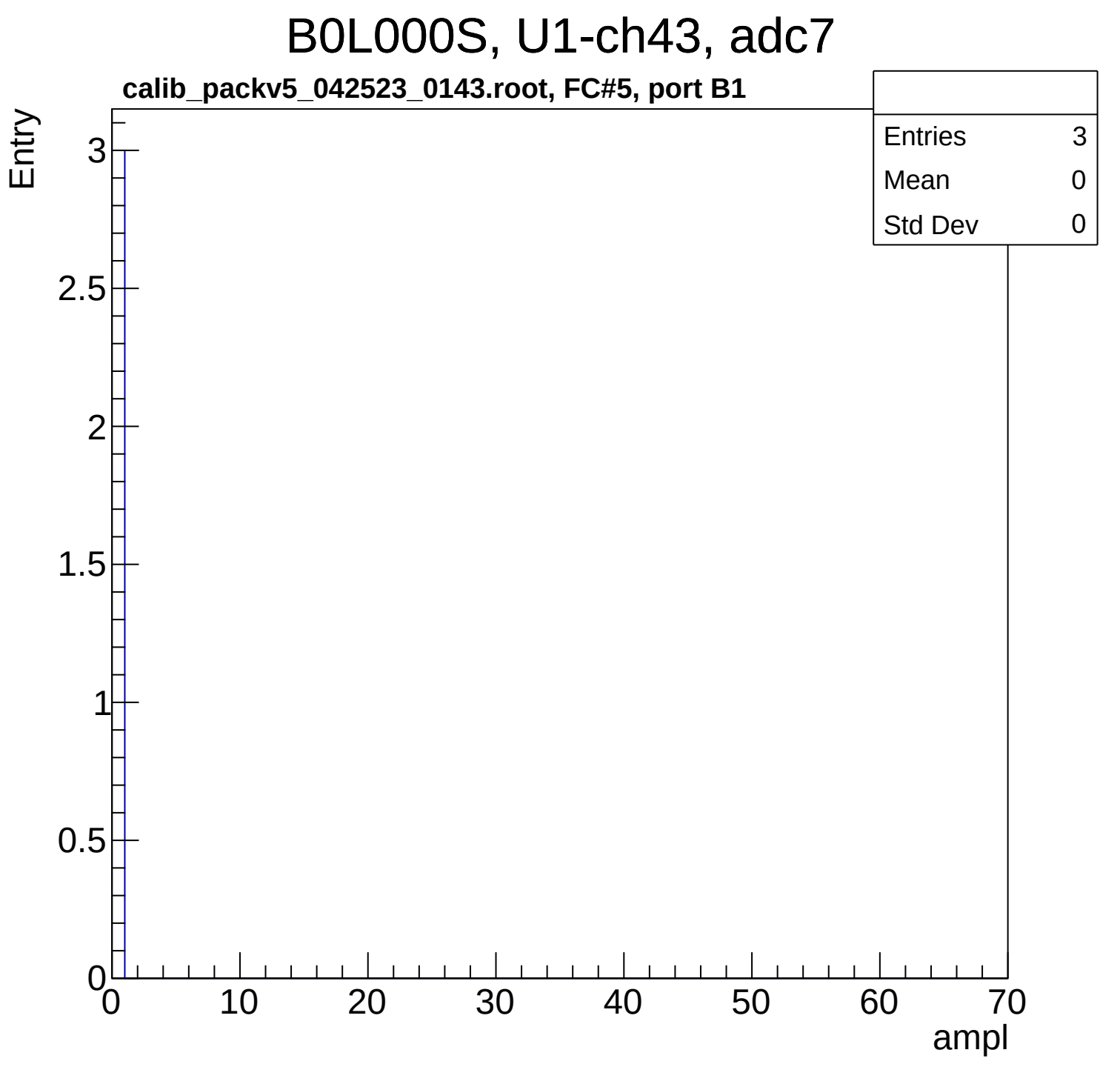
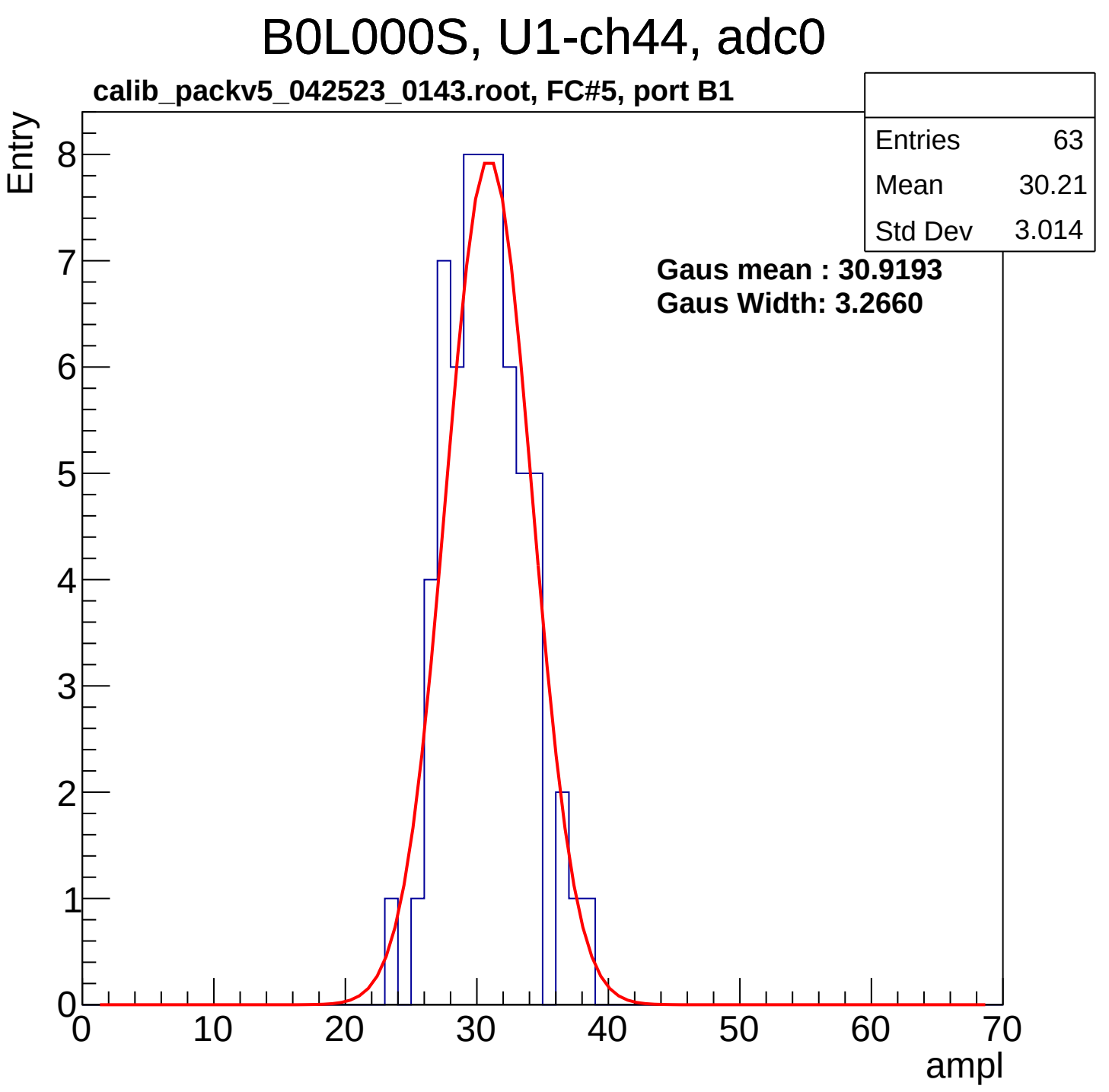
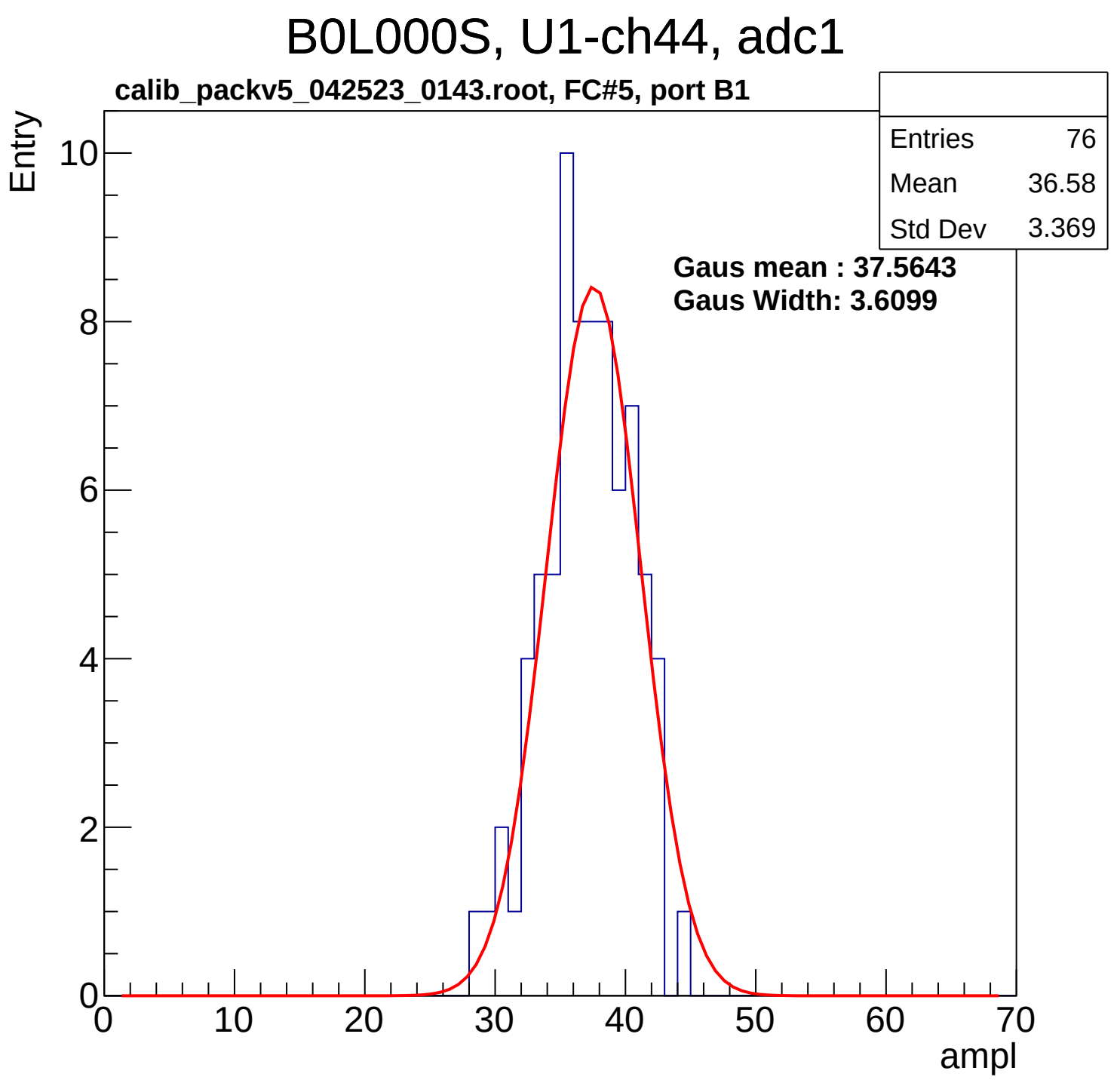
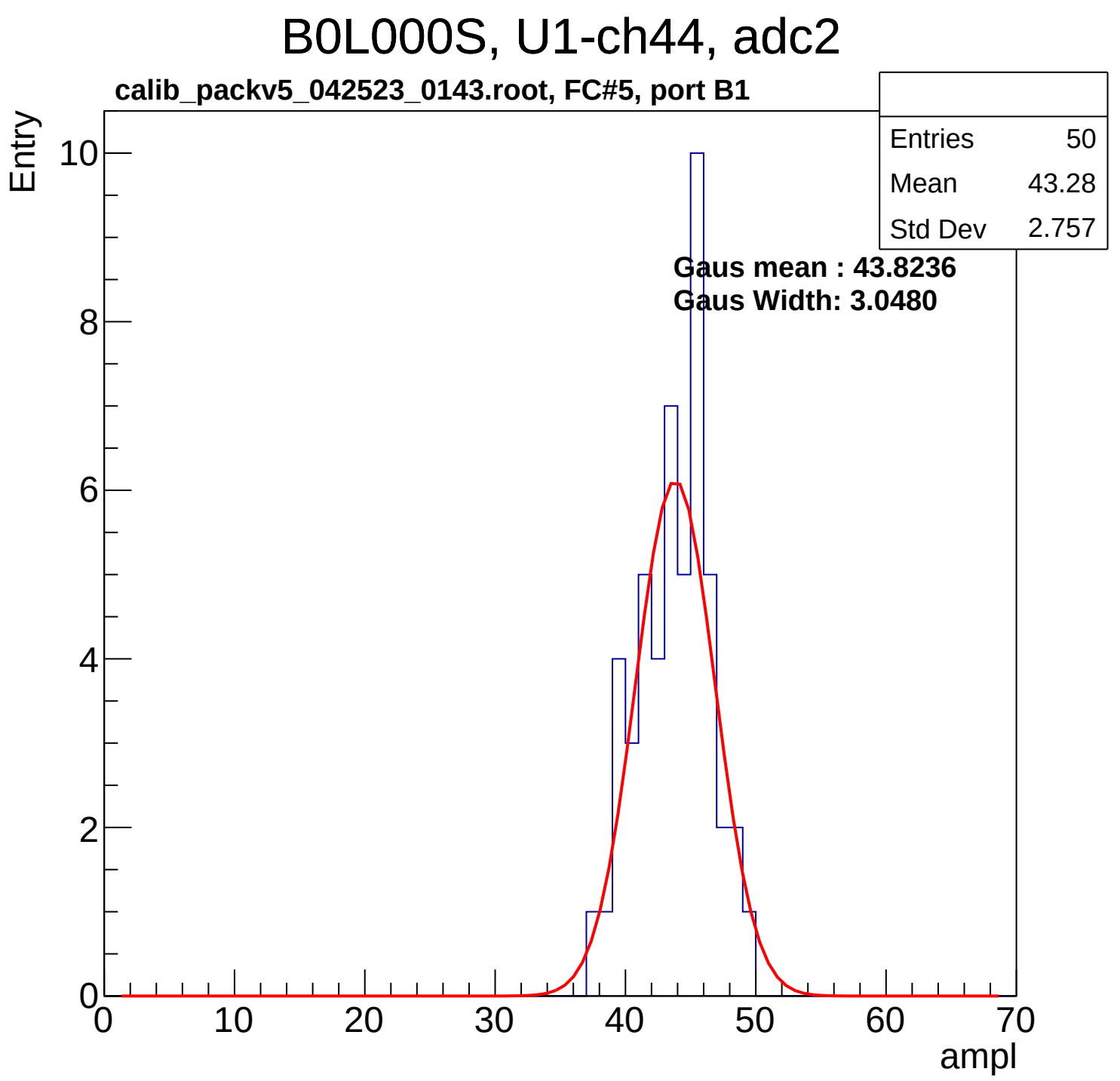
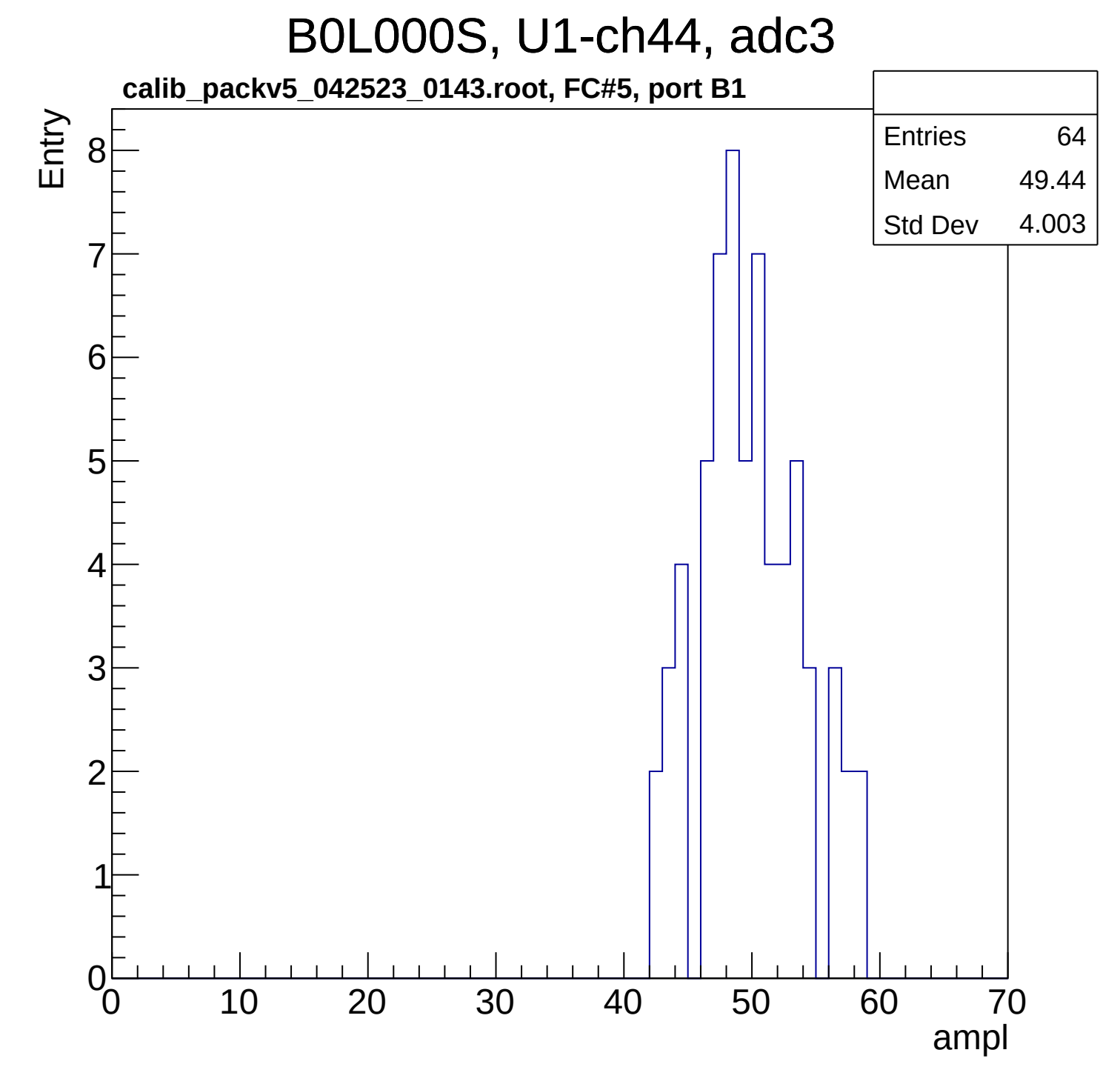

B0L000S, U1-ch43, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.17
Std Dev 1.572
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch43, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch44, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30.21
Std Dev 3.014
Gaus mean : 30.9193
Gaus Width: 3.2660
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch44, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 36.58
Std Dev 3.369
Gaus mean : 37.5643
Gaus Width: 3.6099
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch44, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 43.28
Std Dev 2.757
Gaus mean : 43.8236
Gaus Width: 3.0480
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch44, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.44
Std Dev 4.003
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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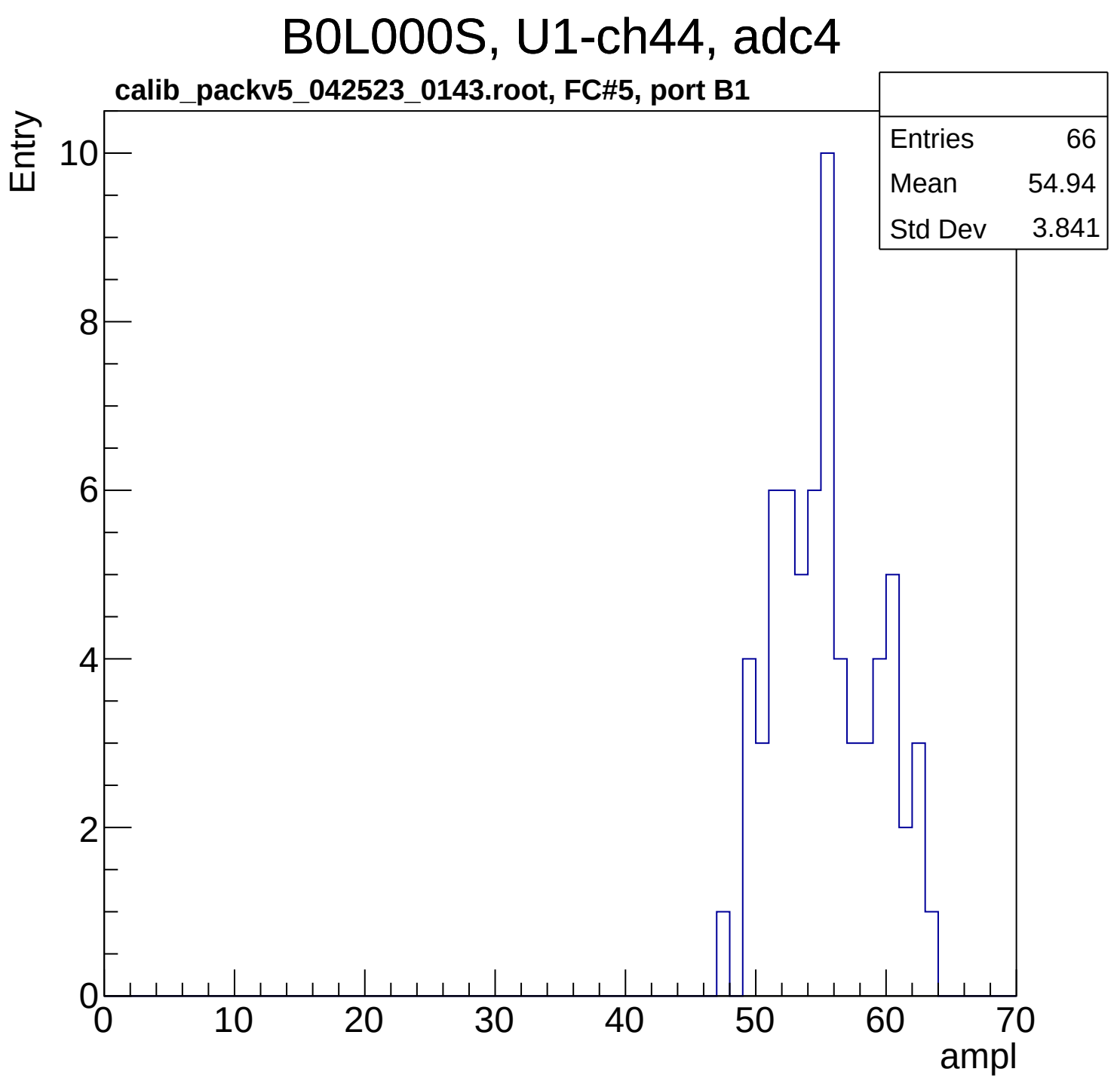
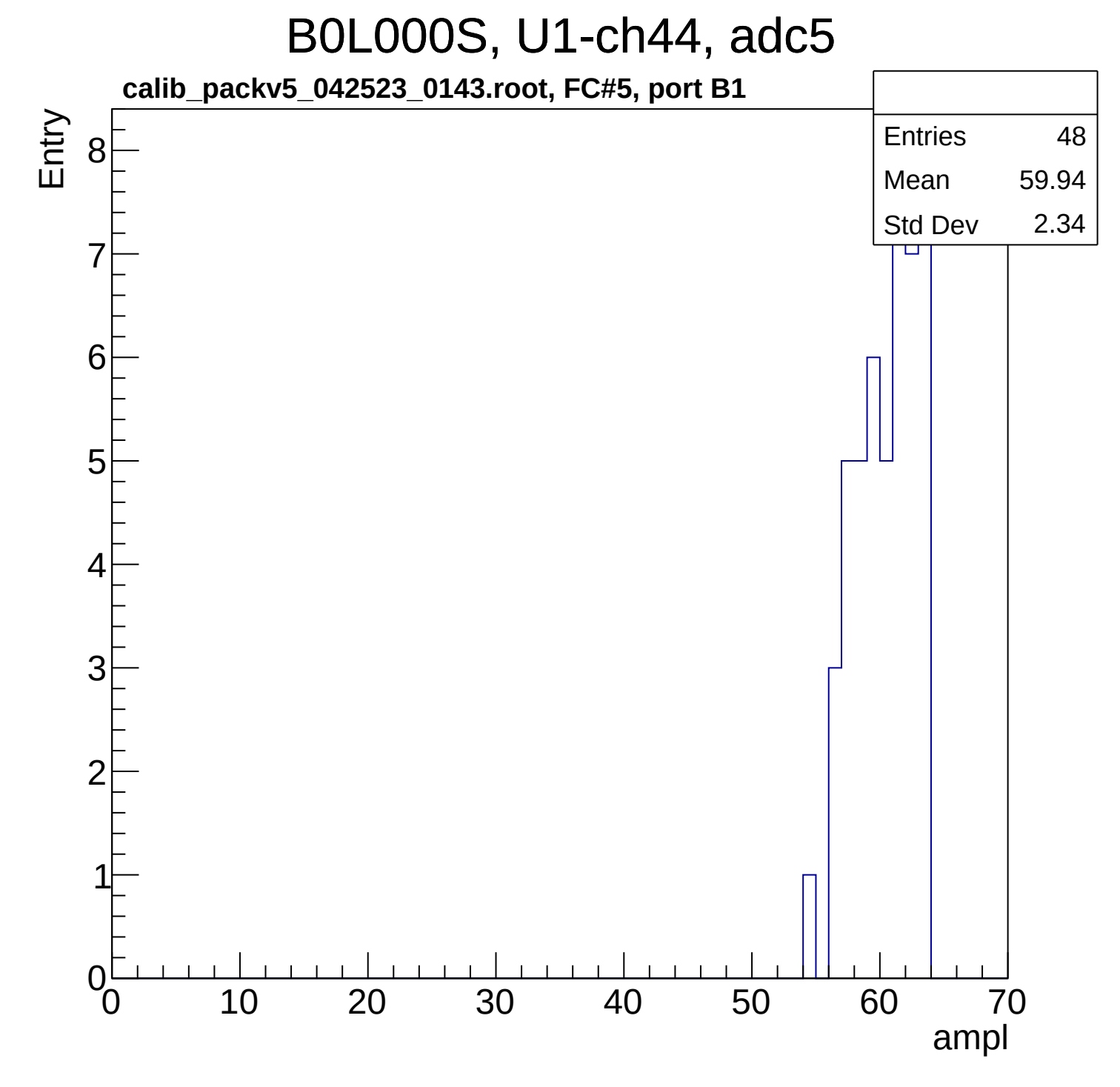

B0L000S, U1-ch44, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.94
Std Dev 3.841
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch44, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.94
Std Dev 2.34
Entry
ampl
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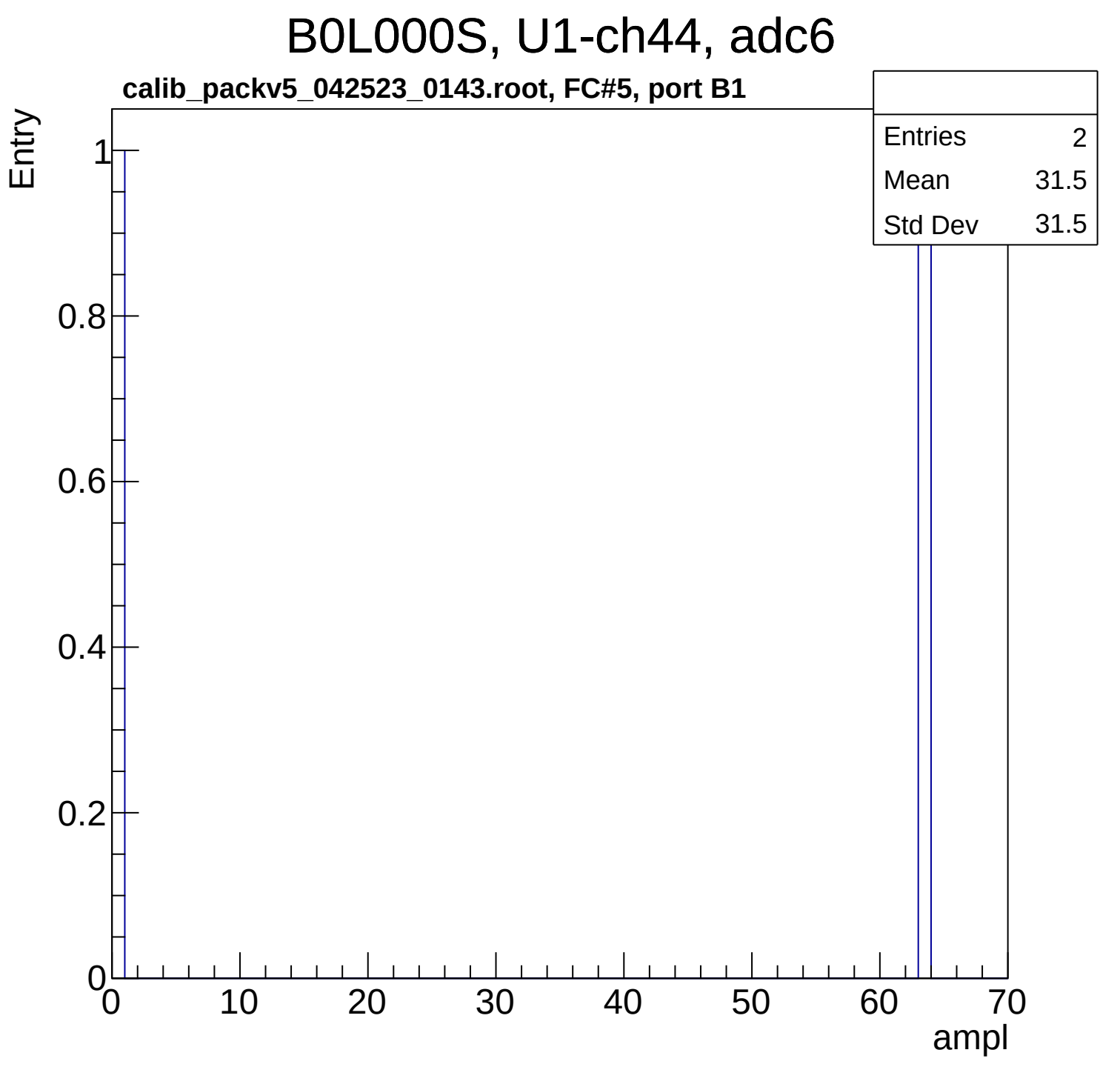
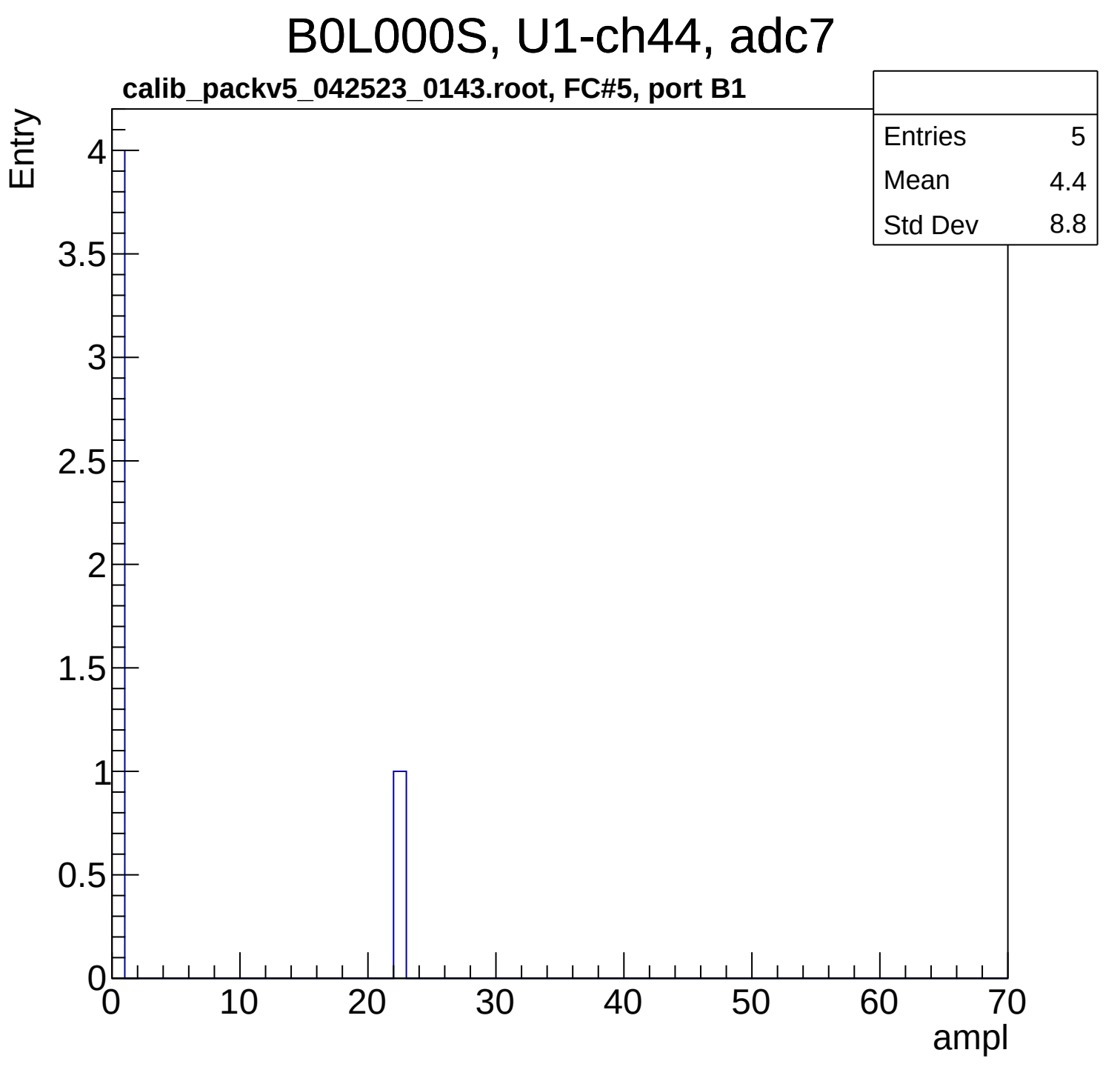
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch44, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch44, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 4.4
Std Dev 8.8
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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60
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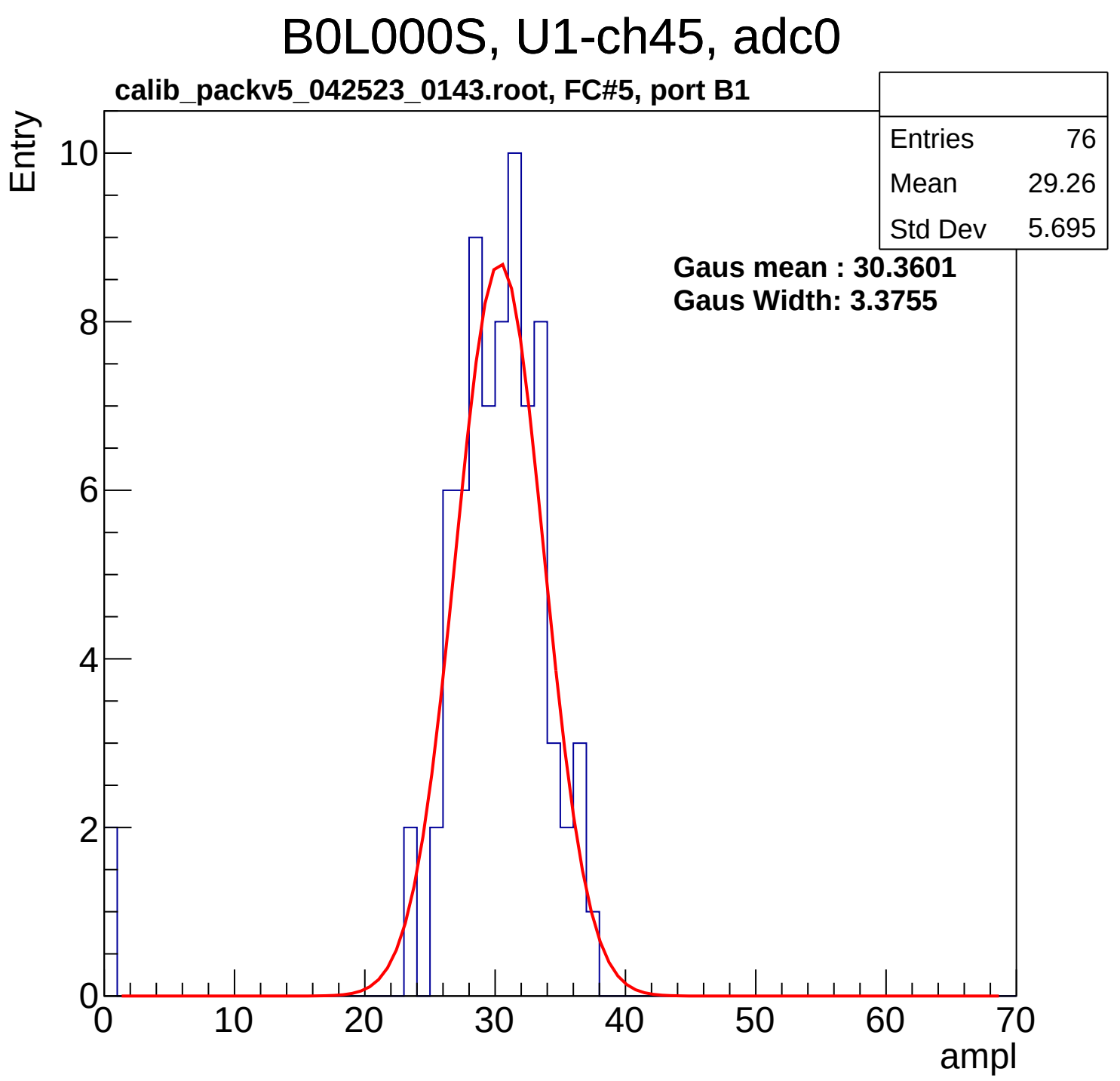
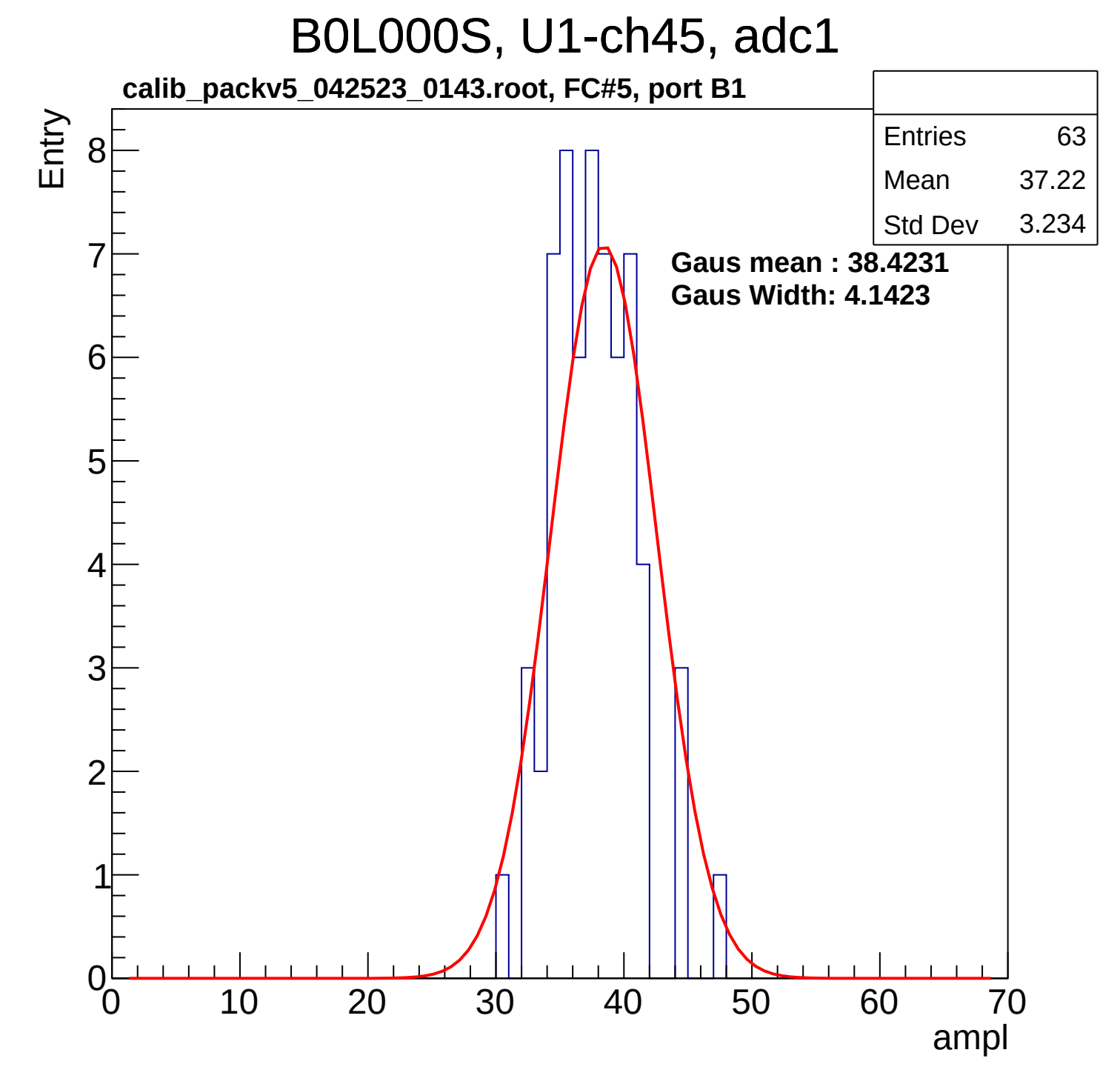
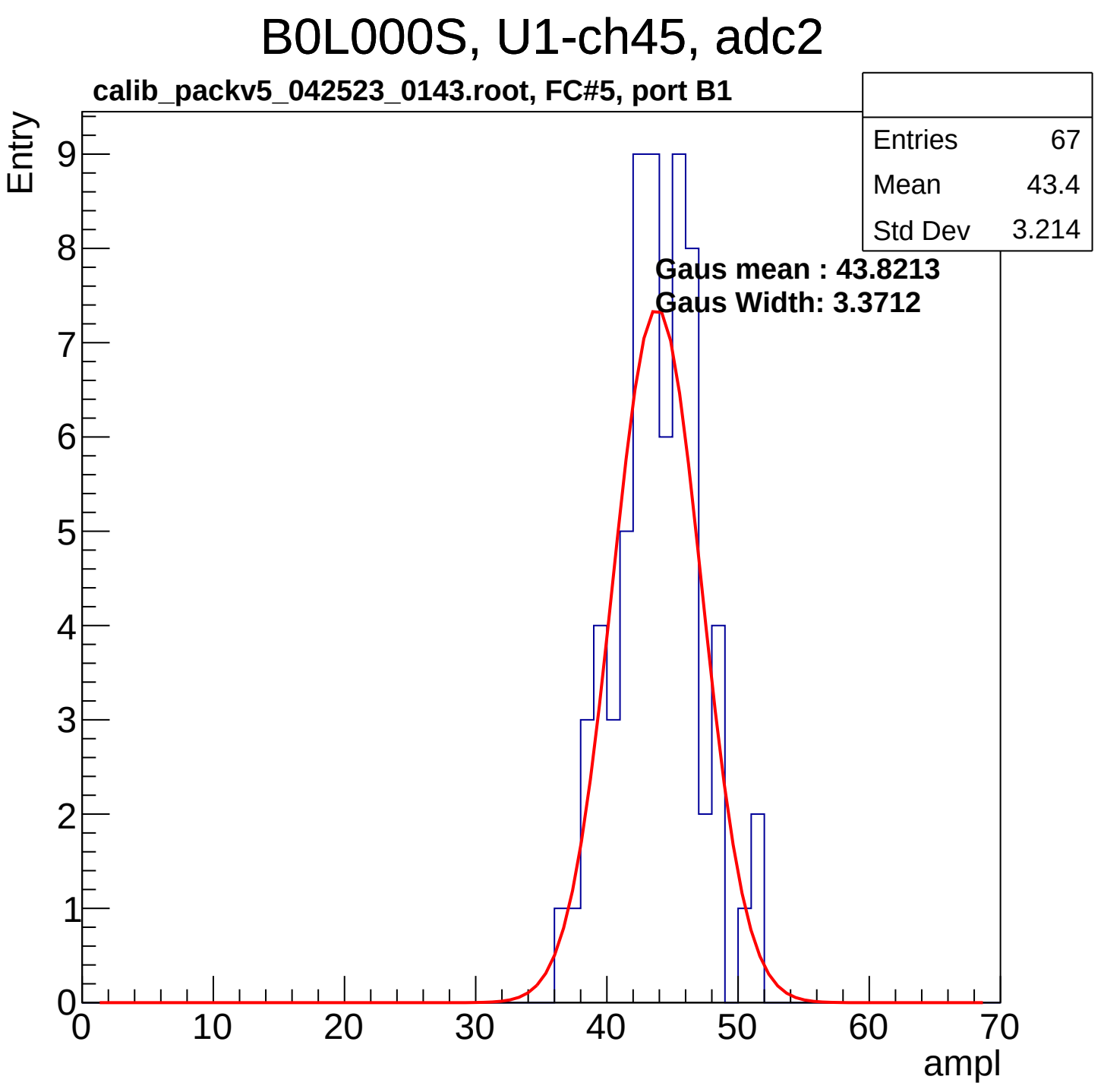
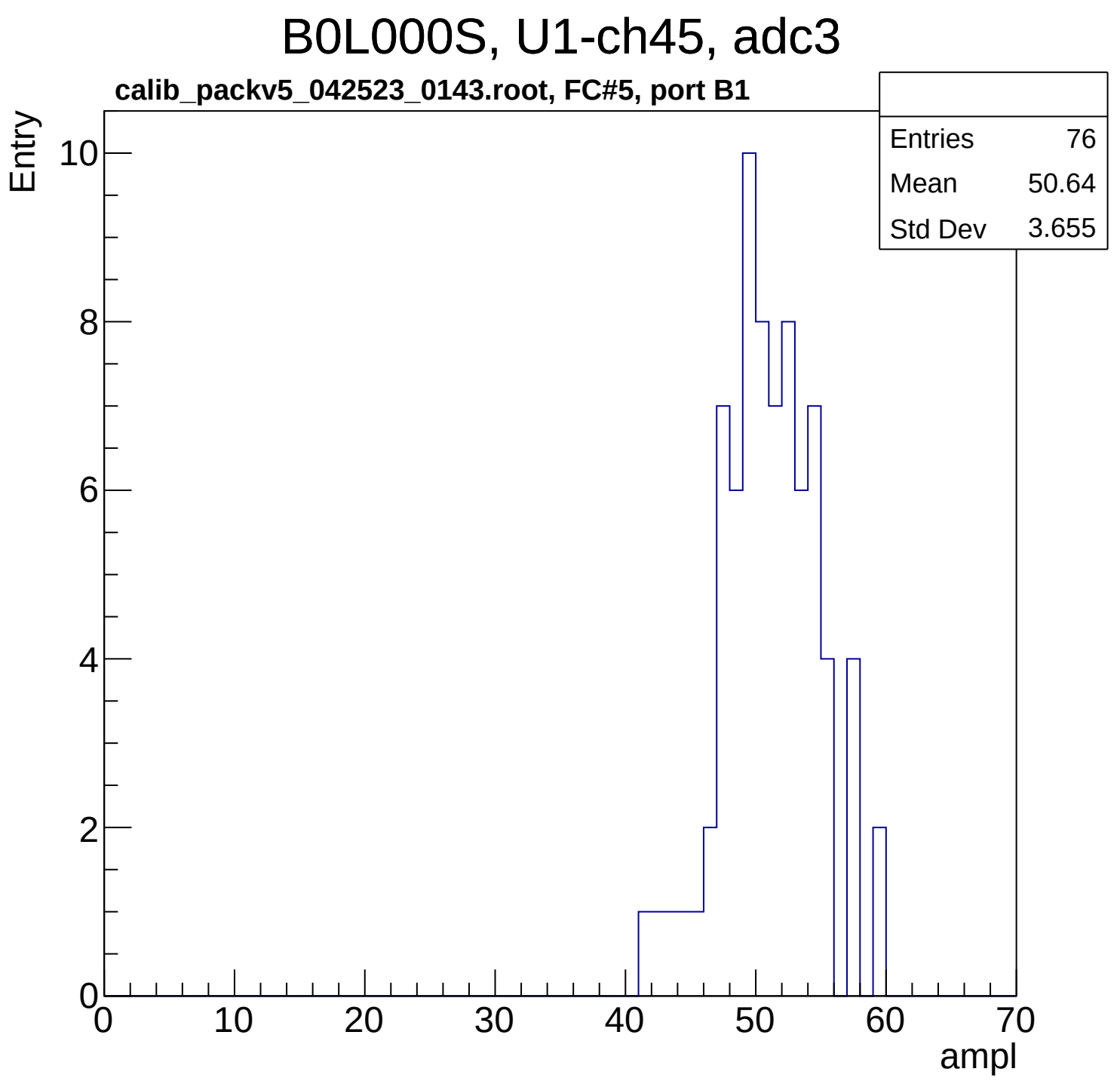

B0L000S, U1-ch45, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.26
Std Dev 5.695
Gaus mean : 30.3601
Gaus Width: 3.3755
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch45, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 37.22
Std Dev 3.234
Gaus mean : 38.4231
Gaus Width: 4.1423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch45, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.4
Std Dev 3.214
Gaus mean : 43.8213
Gaus Width: 3.3712
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U1-ch45, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 50.64
Std Dev 3.655
Entry
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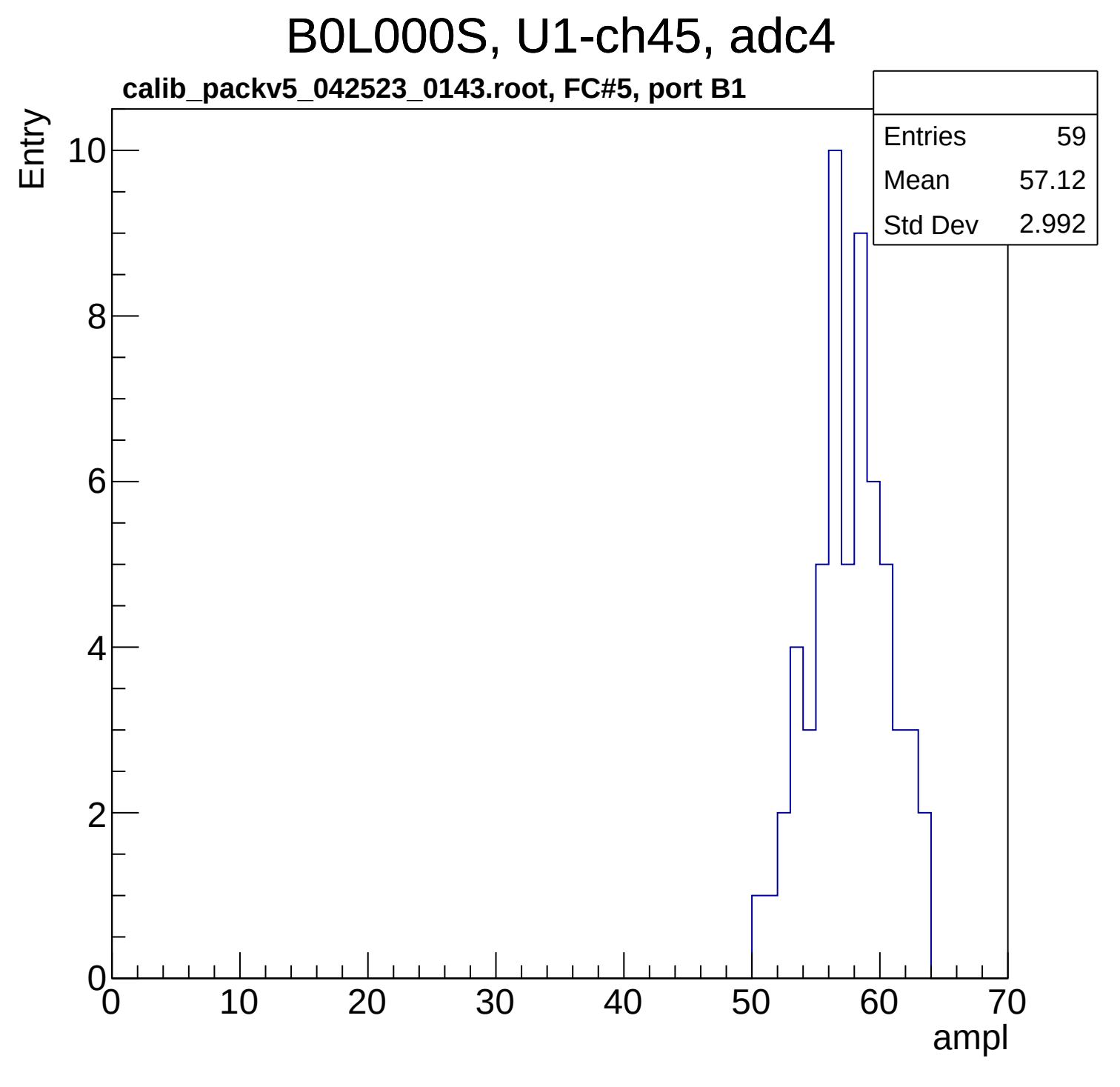
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch45, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.12
Std Dev 2.992
Entry
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4
6
8
10
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10
20
30
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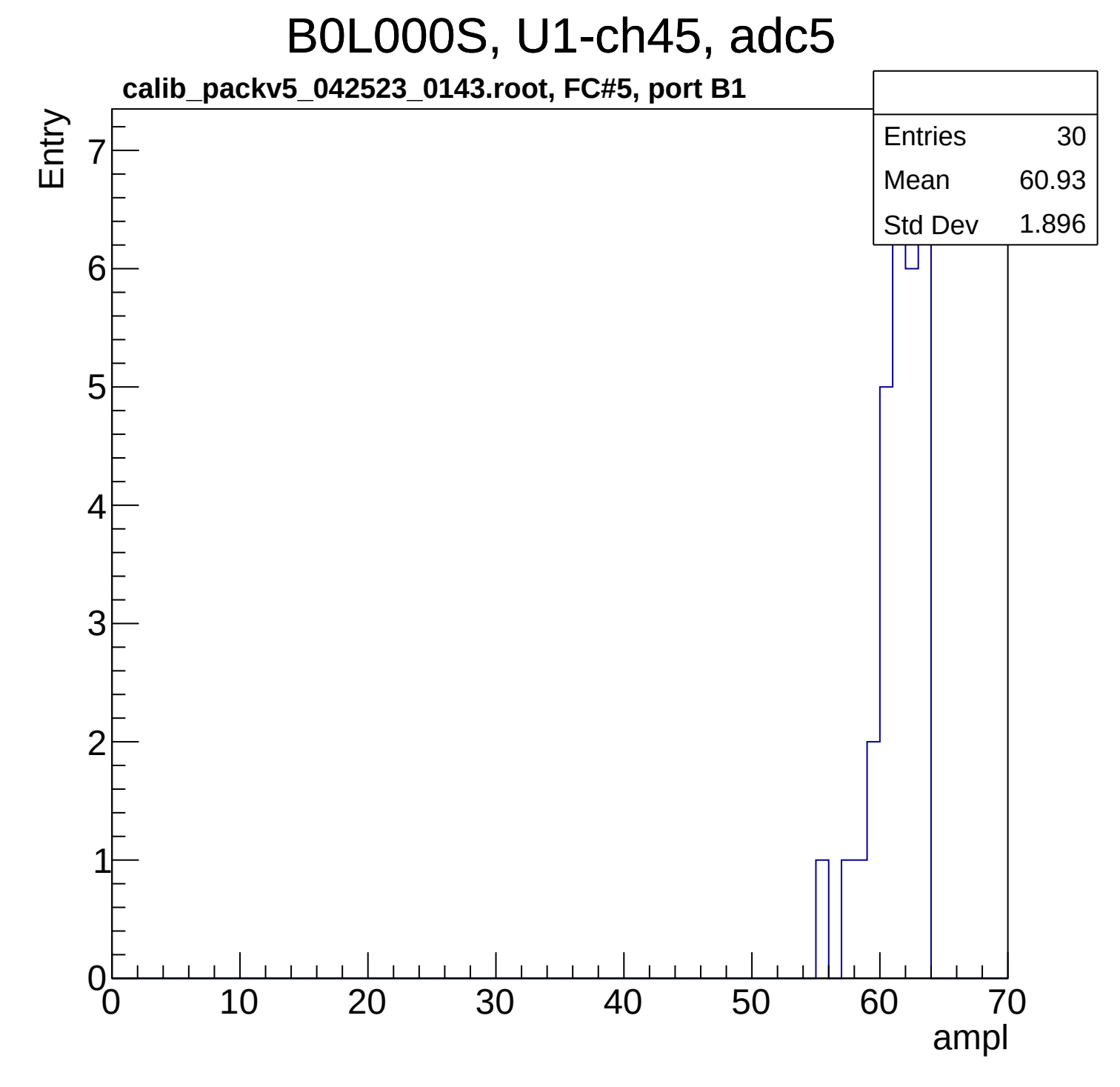
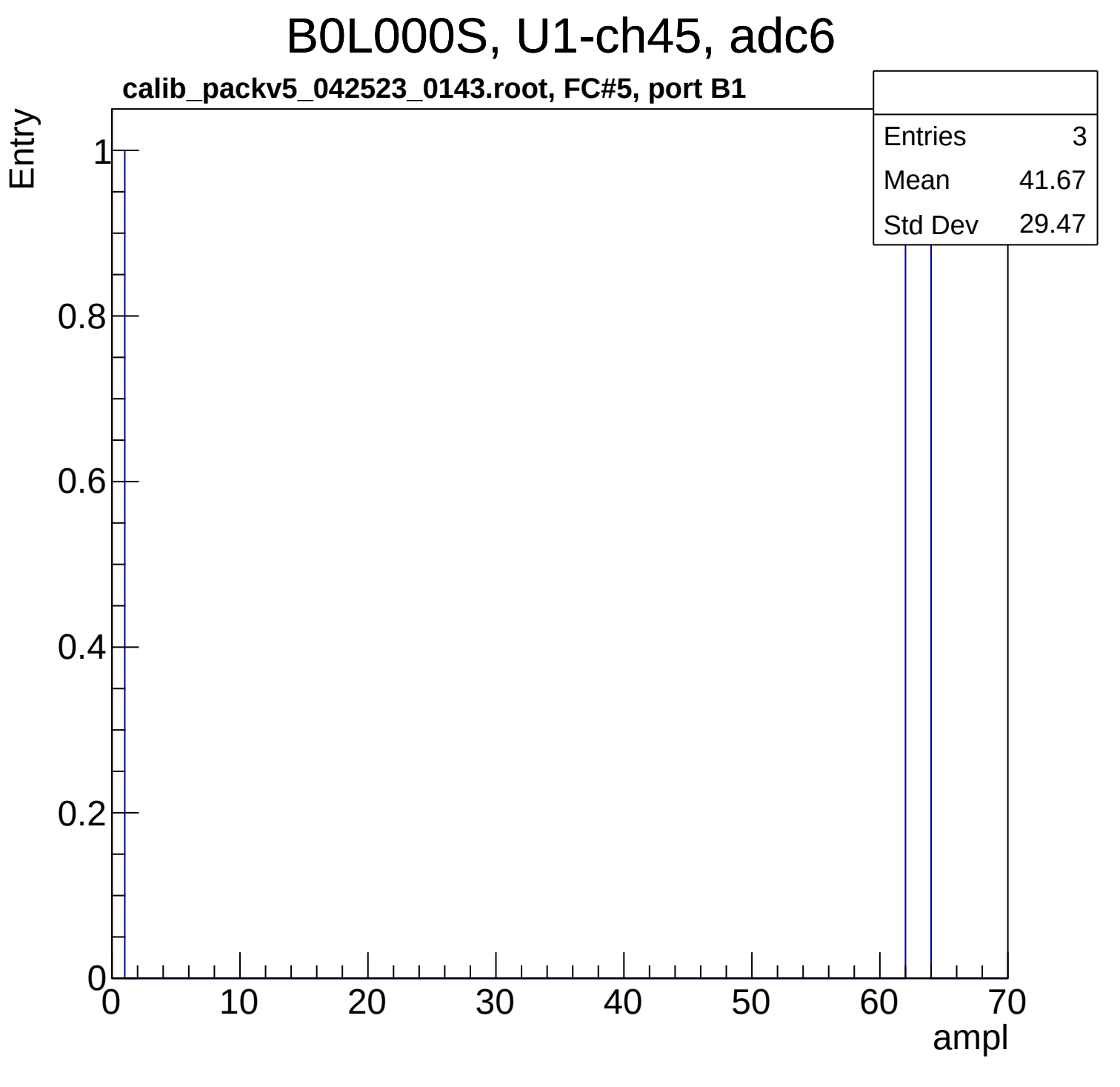
50
60
70
ampl


B0L000S, U1-ch45, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.93
Std Dev 1.896
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch45, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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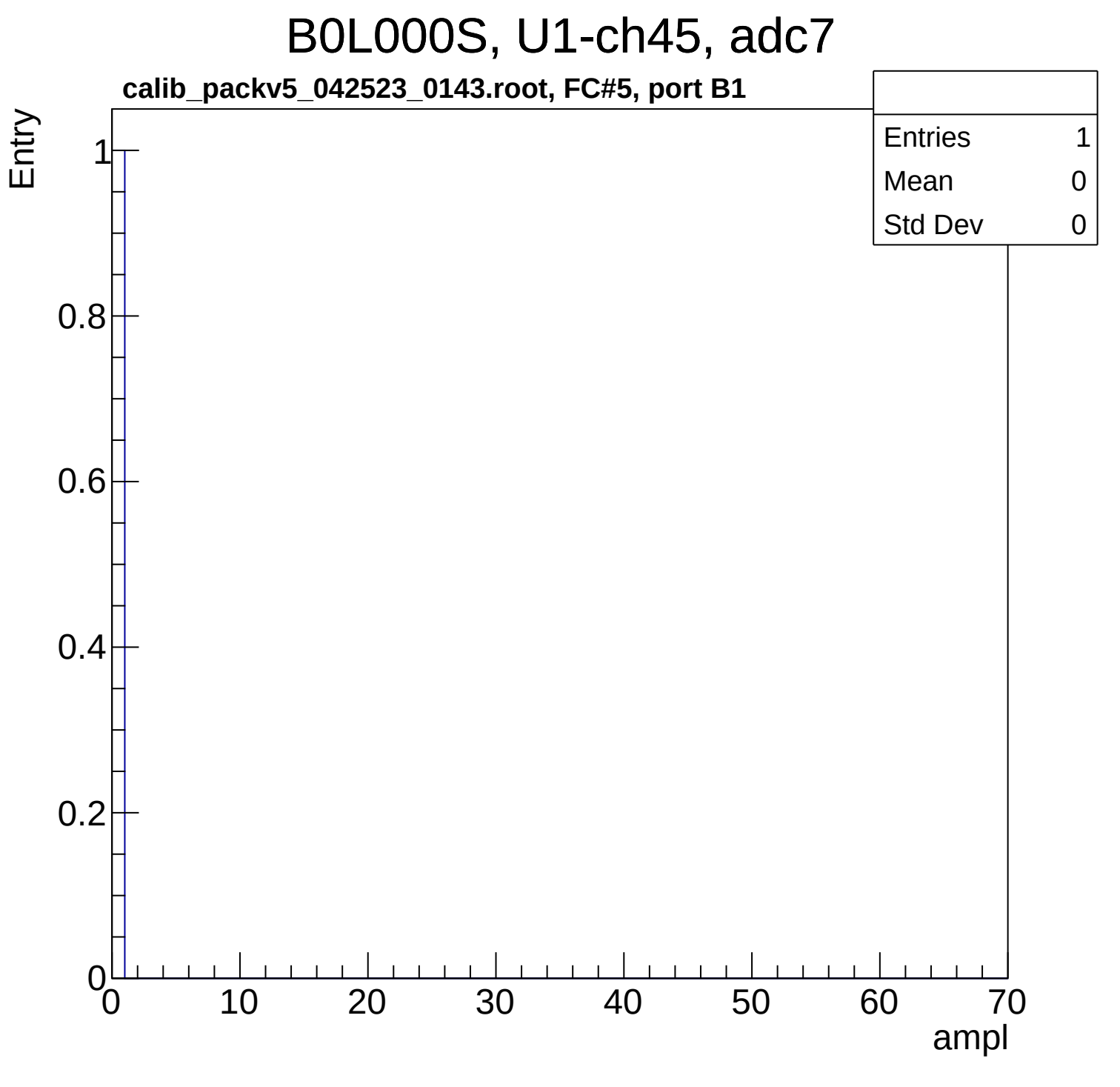
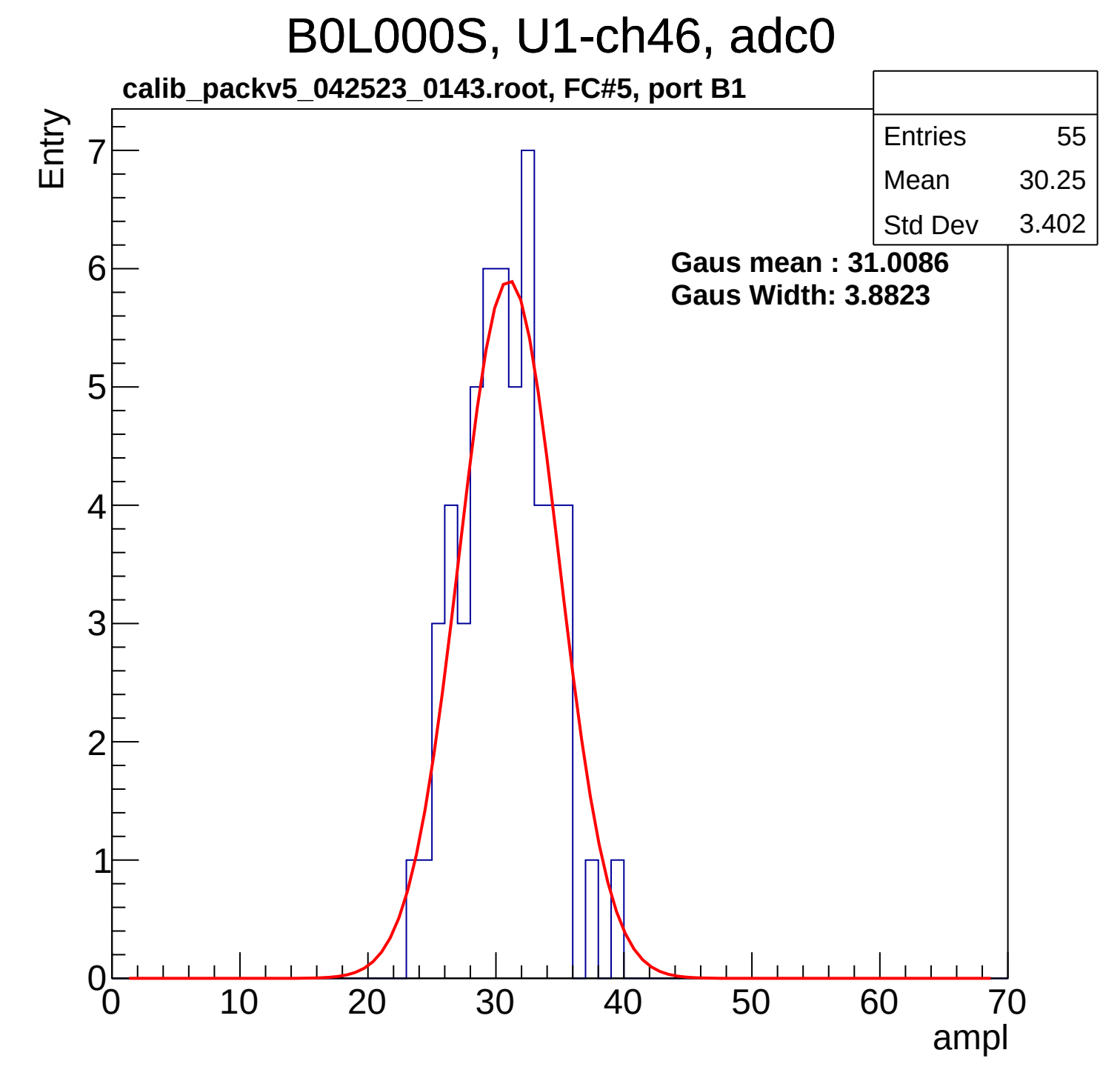
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch45, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch46, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 30.25
Std Dev 3.402
Gaus mean : 31.0086
Gaus Width: 3.8823
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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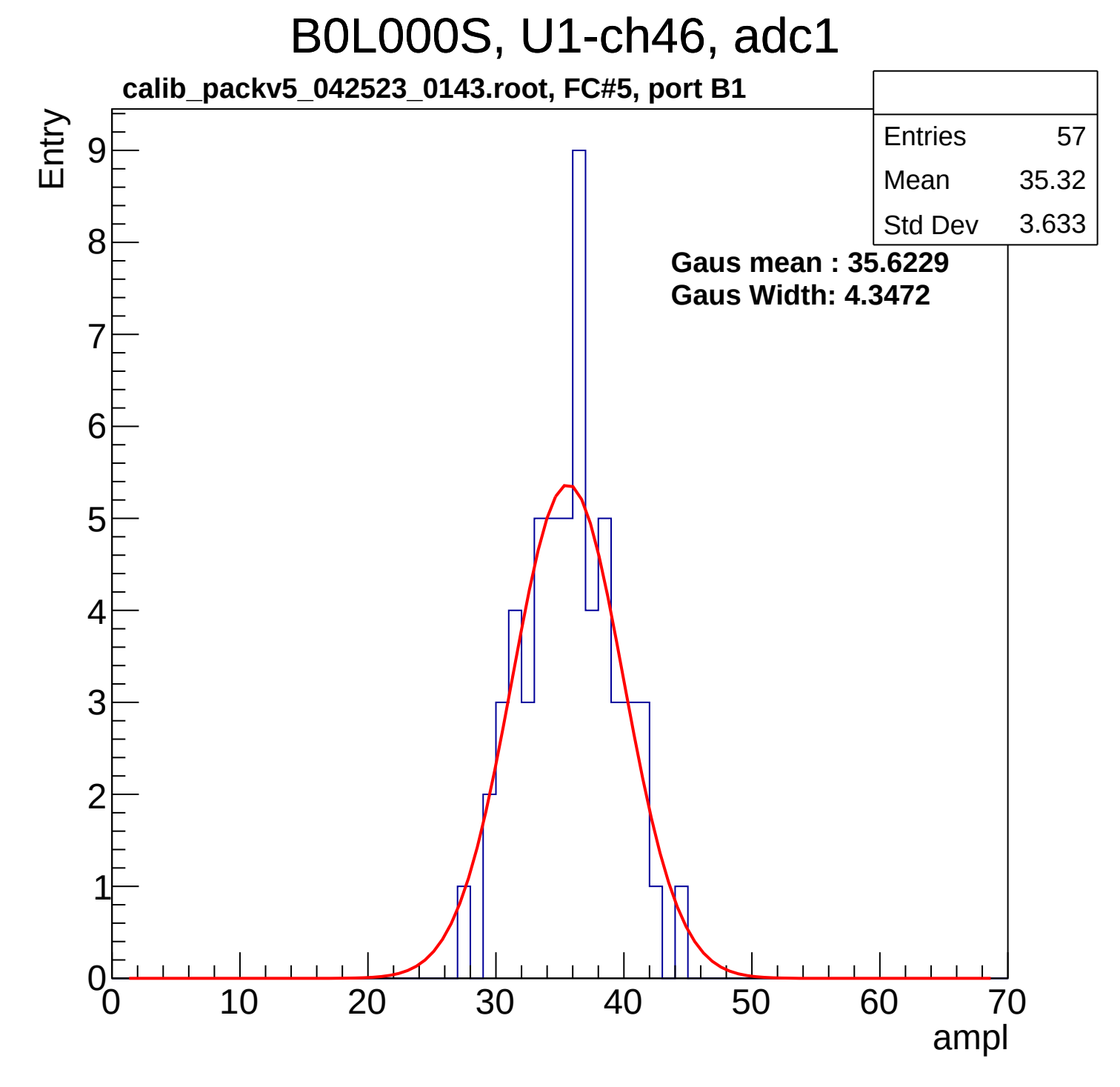
B0L000S, U1-ch46, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.32
Std Dev 3.633
Gaus mean : 35.6229
Gaus Width: 4.3472
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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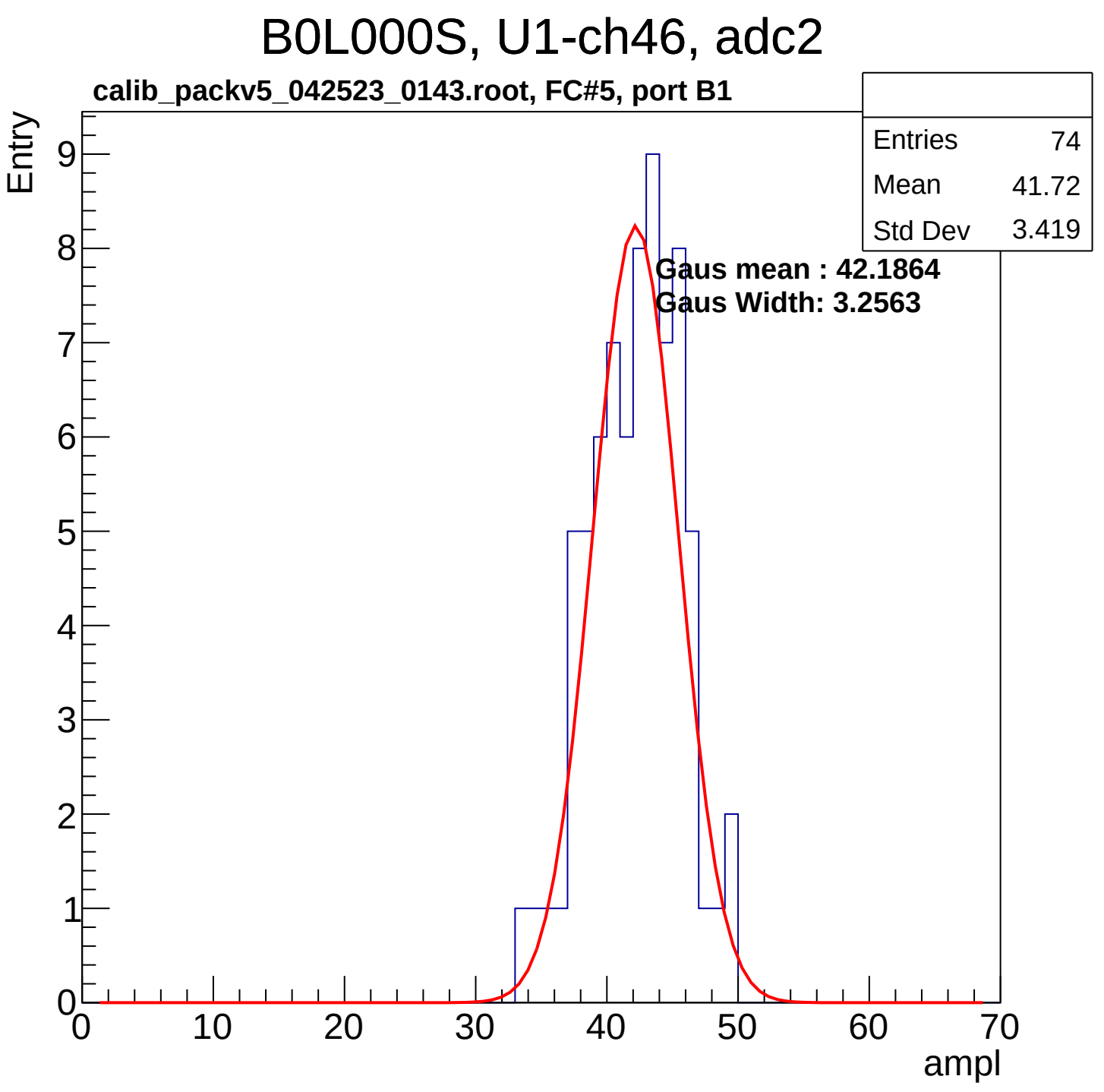
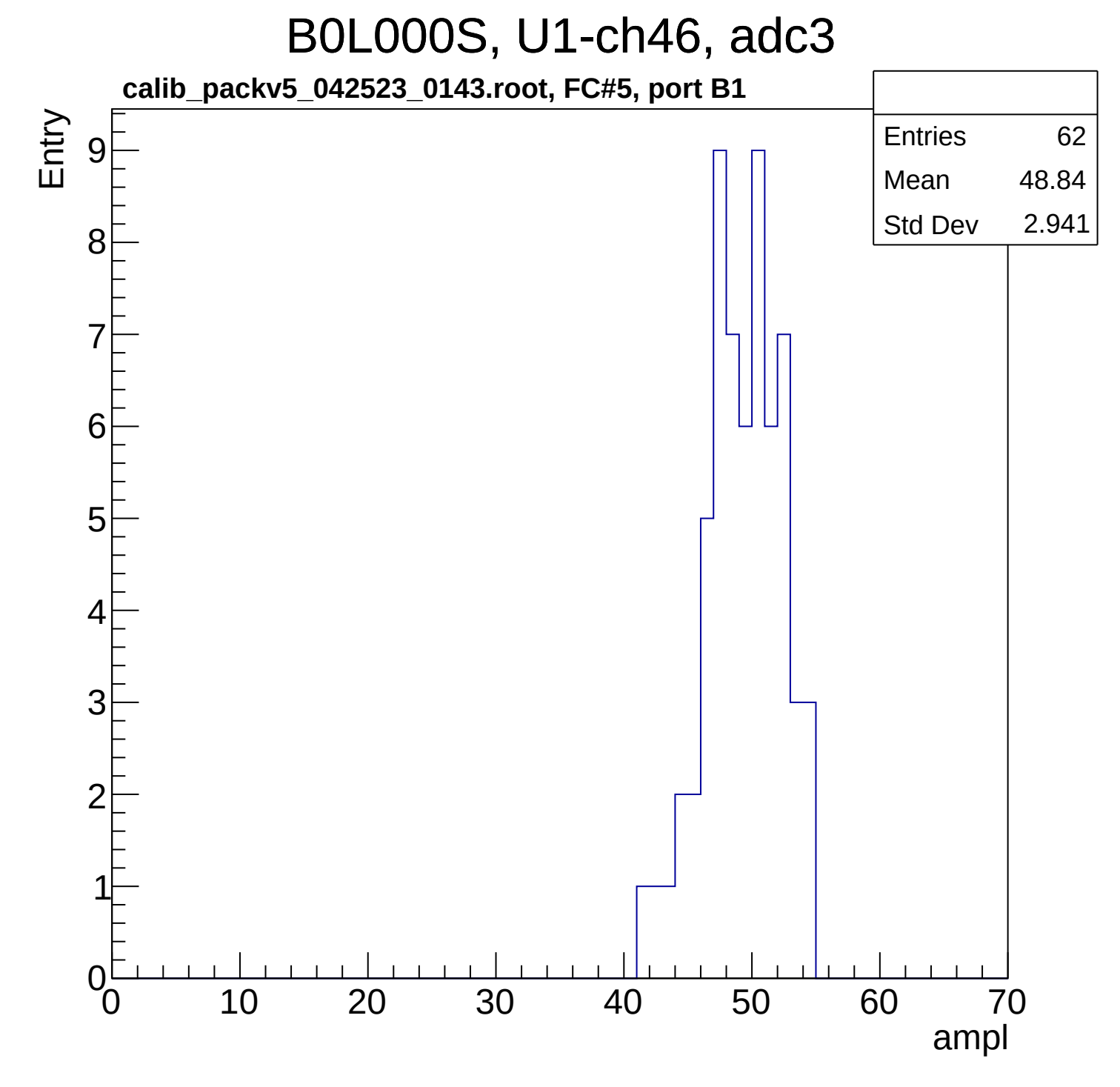

B0L000S, U1-ch46, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 41.72
Std Dev 3.419
Gaus mean : 42.1864
Gaus Width: 3.2563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch46, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.84
Std Dev 2.941
Entry
ampl
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3
4
5
6
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8
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0
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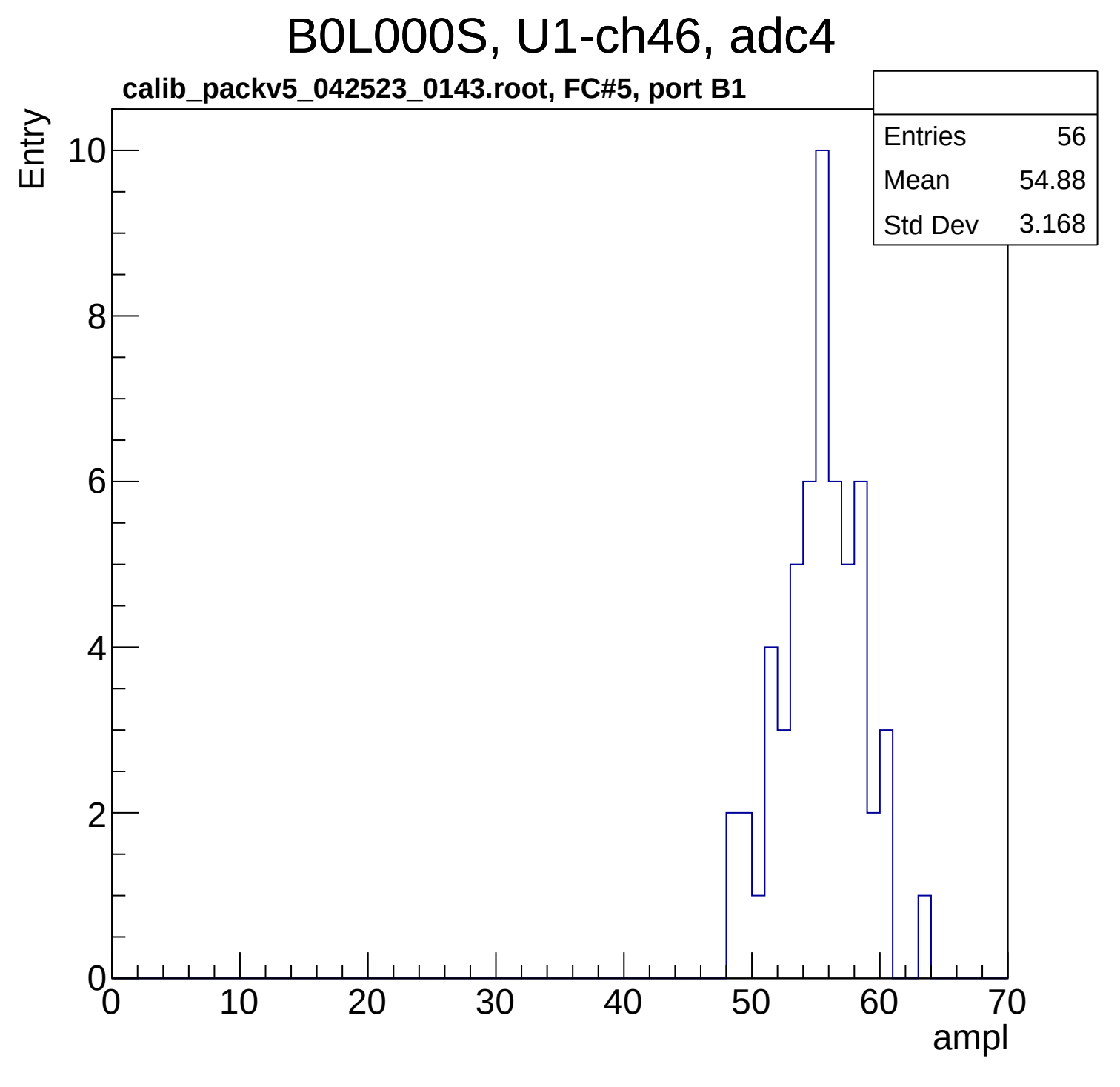
B0L000S, U1-ch46, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.88
Std Dev 3.168
Entry
ampl
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2
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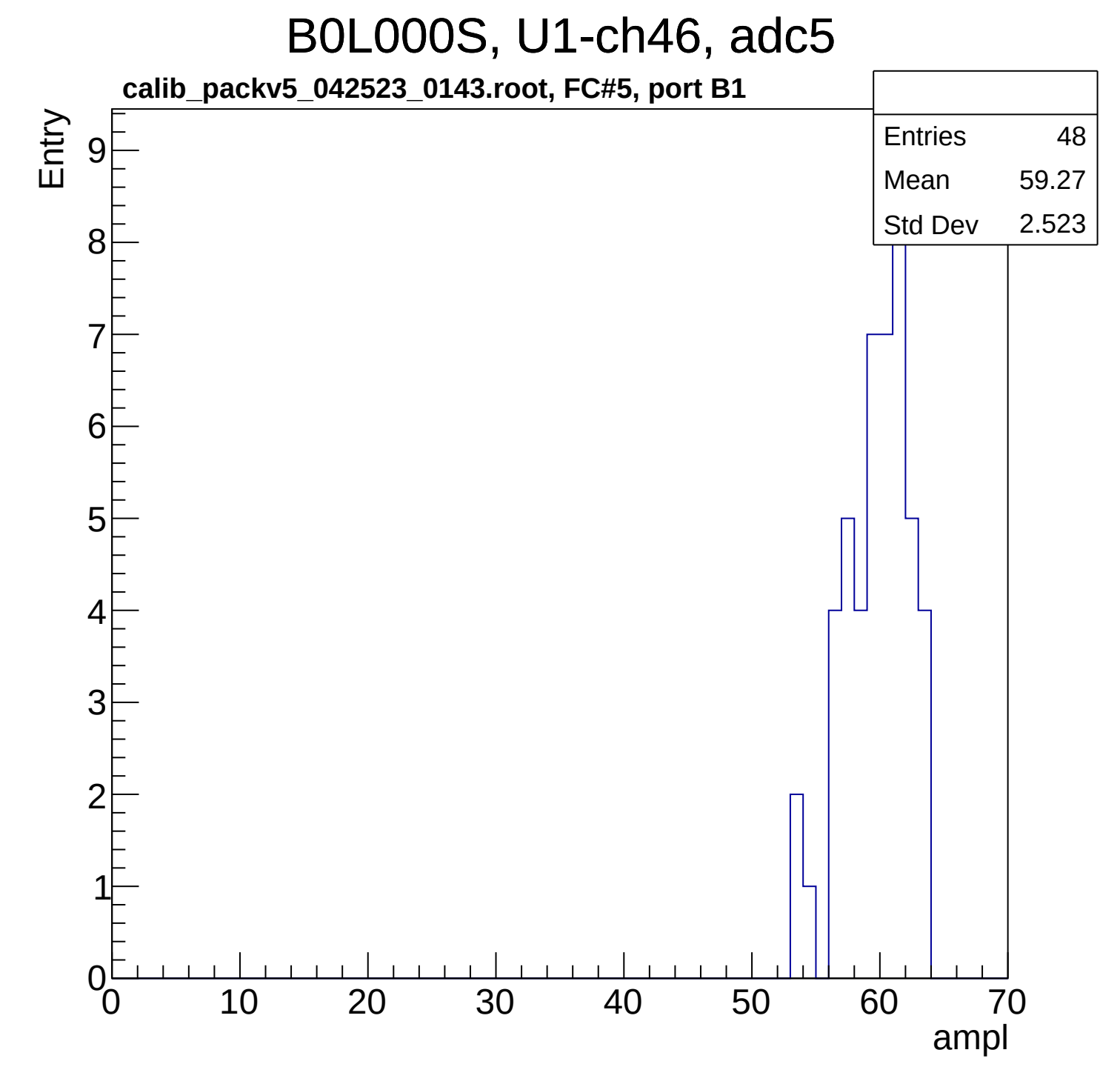
B0L000S, U1-ch46, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.27
Std Dev 2.523
Entry
ampl
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3
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5
6
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0
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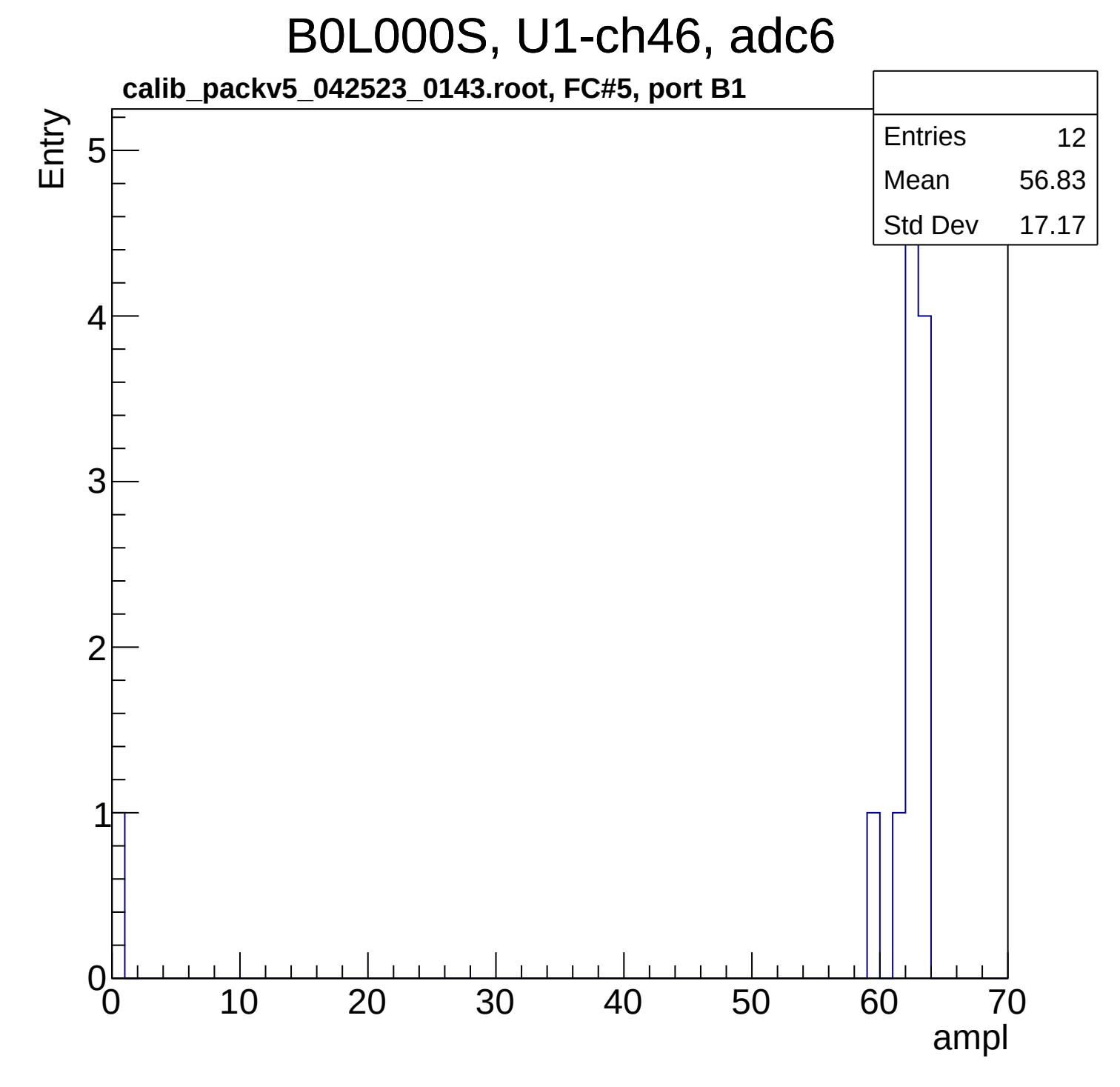
B0L000S, U1-ch46, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
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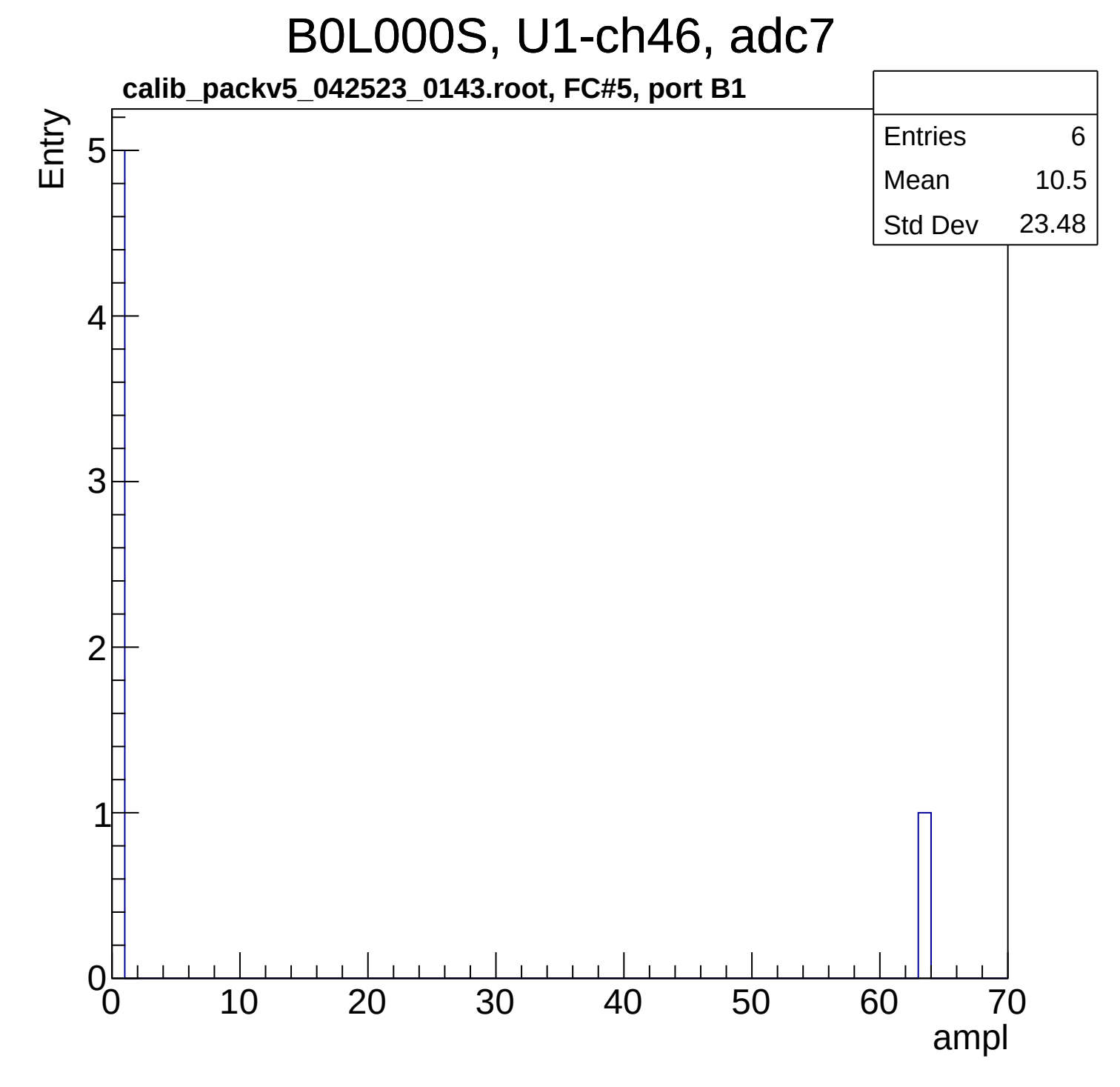
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch46, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 10.5
Std Dev 23.48
Entry
0
1
2
3
4
5
0
10
20
30
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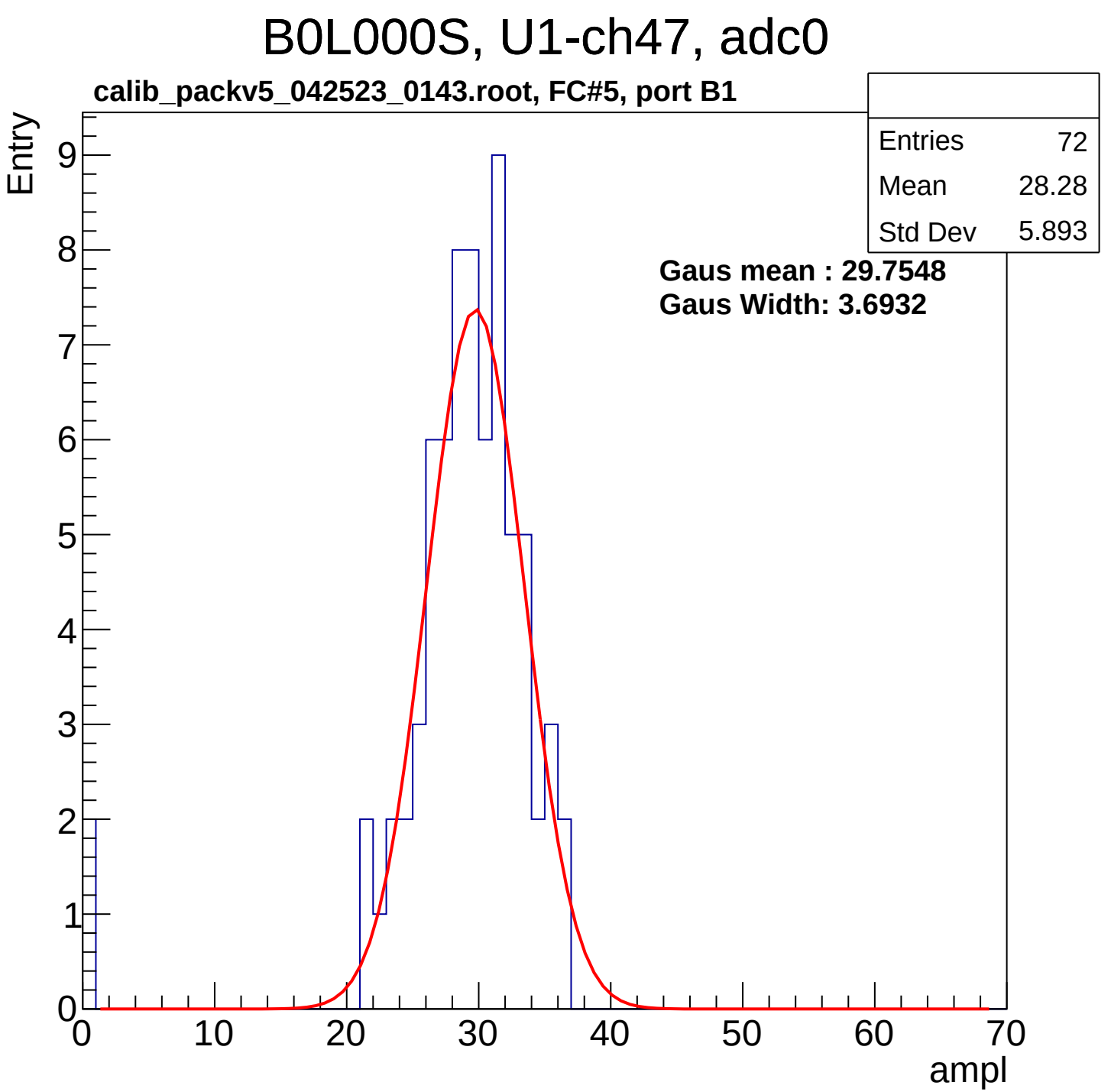
50
60
70
ampl


B0L000S, U1-ch47, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 28.28
Std Dev 5.893
Gaus mean : 29.7548
Gaus Width: 3.6932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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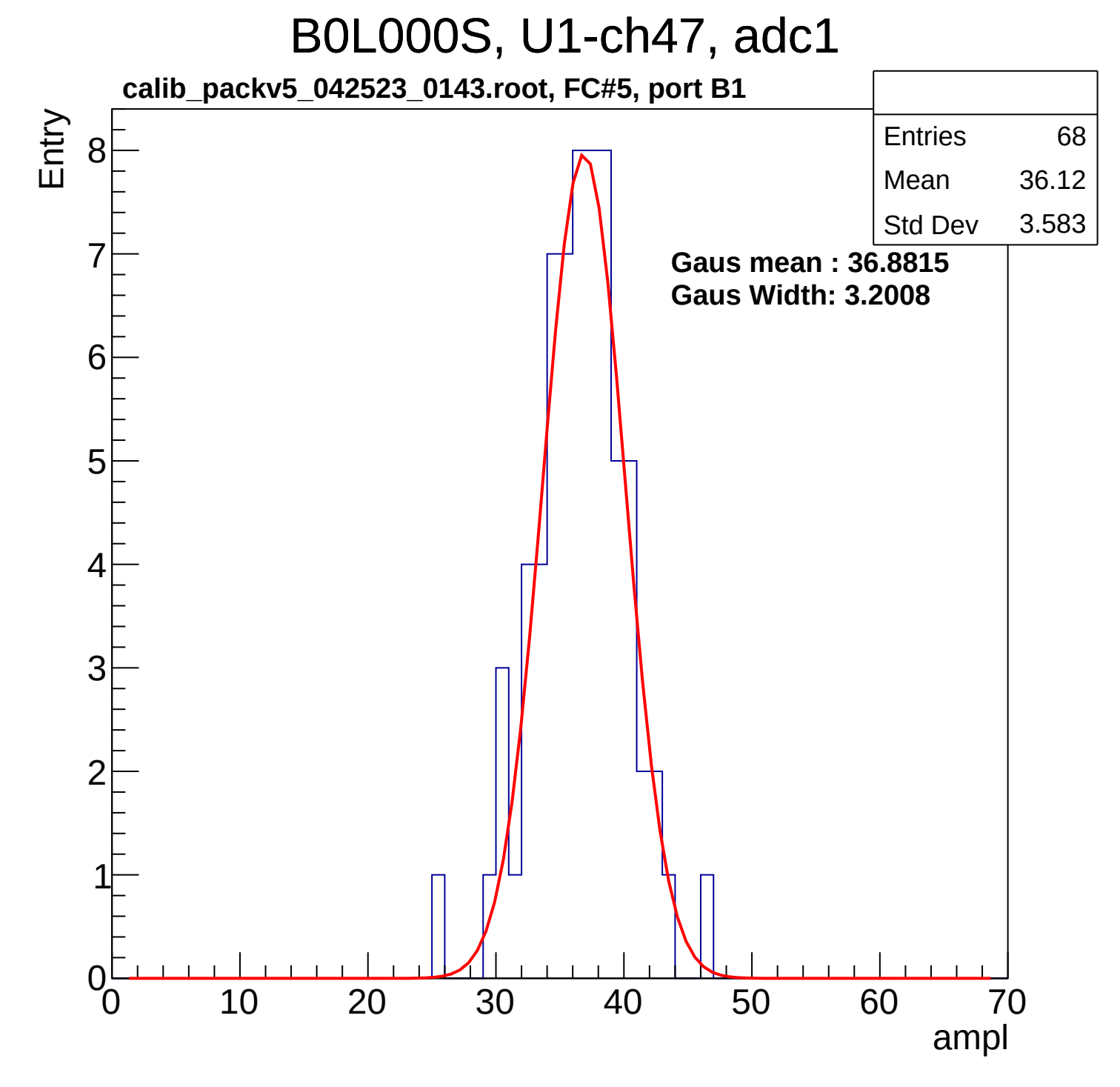
B0L000S, U1-ch47, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.12
Std Dev 3.583
Gaus mean : 36.8815
Gaus Width: 3.2008
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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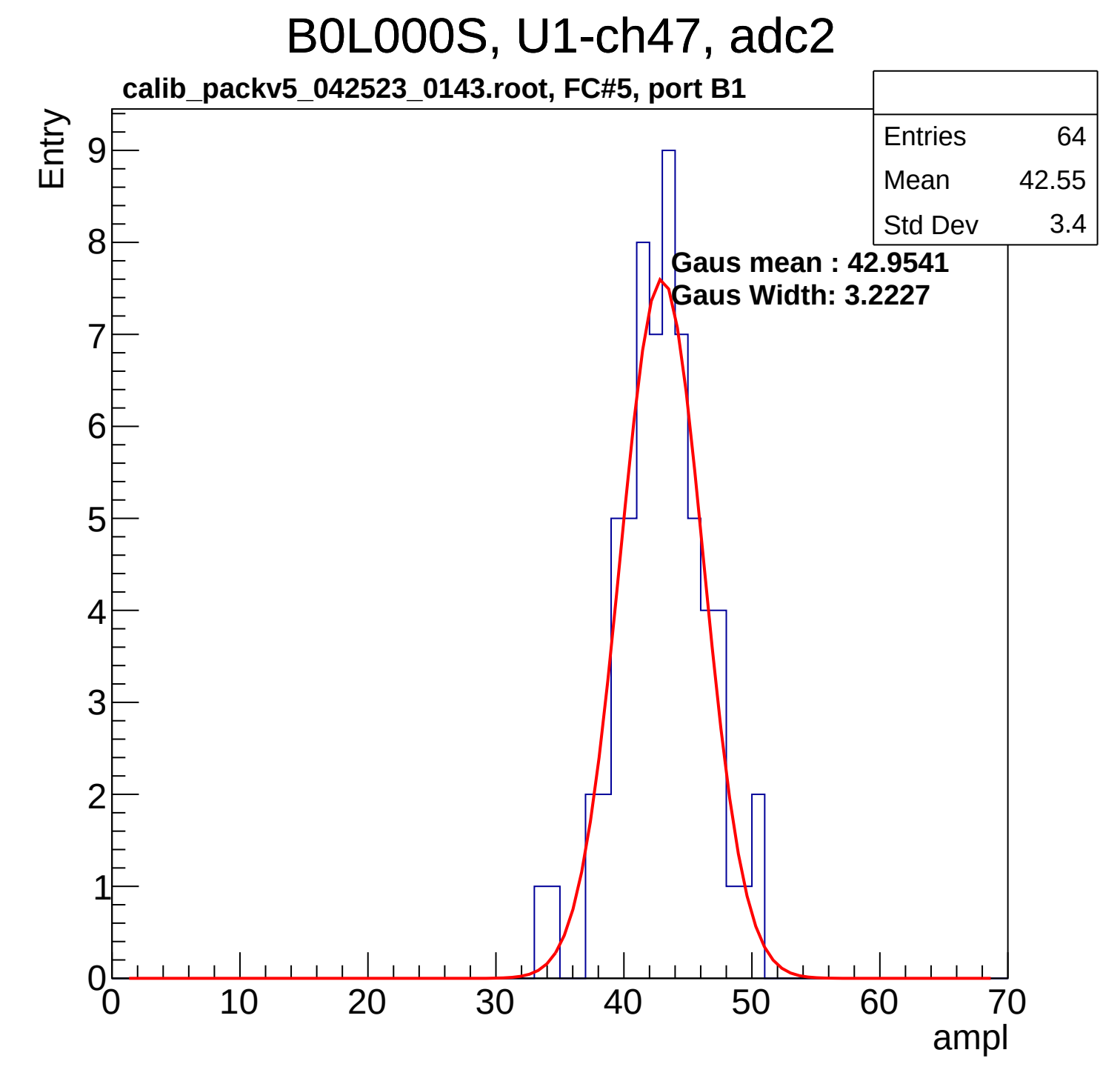
B0L000S, U1-ch47, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.55
Std Dev 3.4
Gaus mean : 42.9541
Gaus Width: 3.2227
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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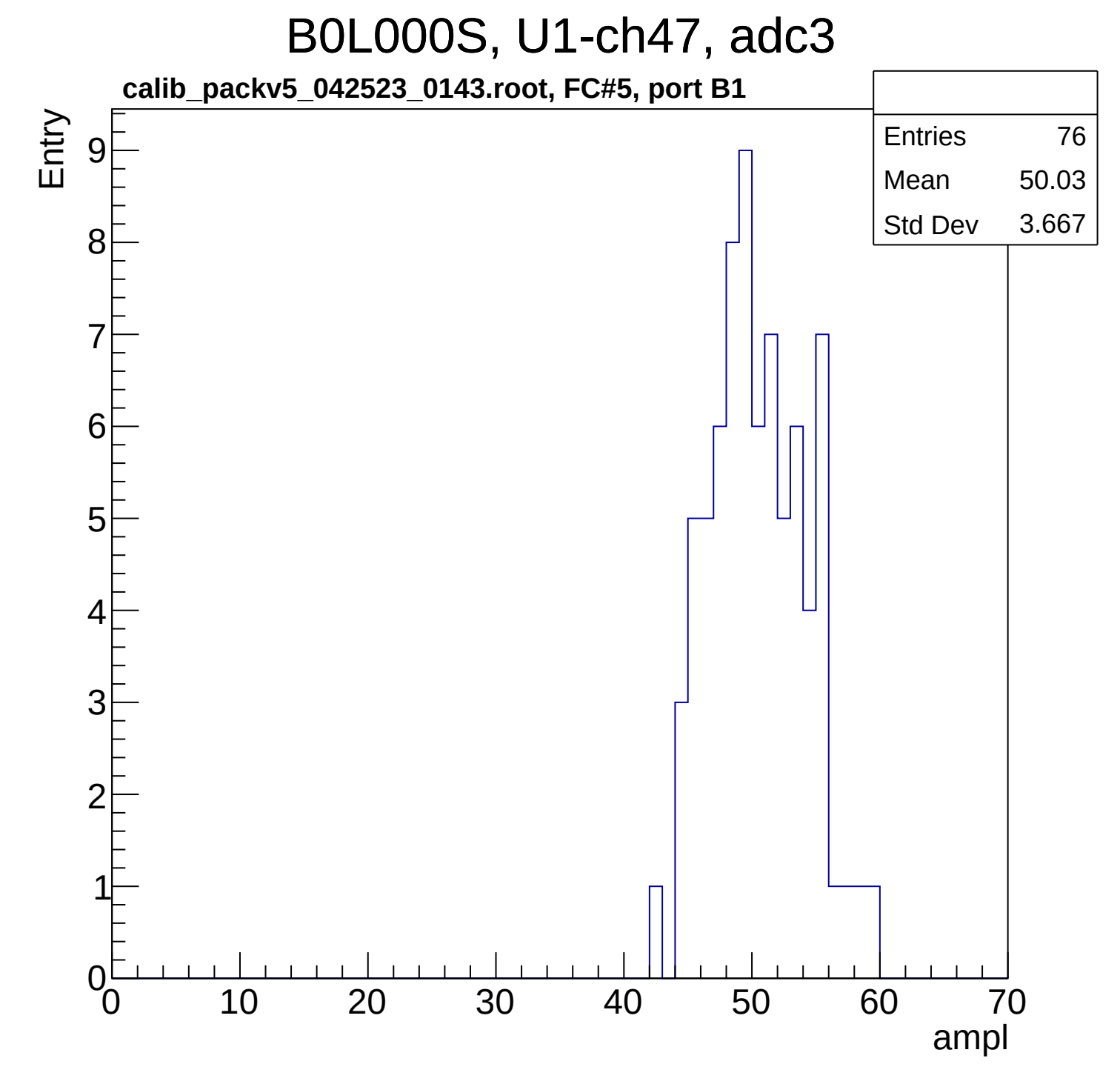
B0L000S, U1-ch47, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 50.03
Std Dev 3.667
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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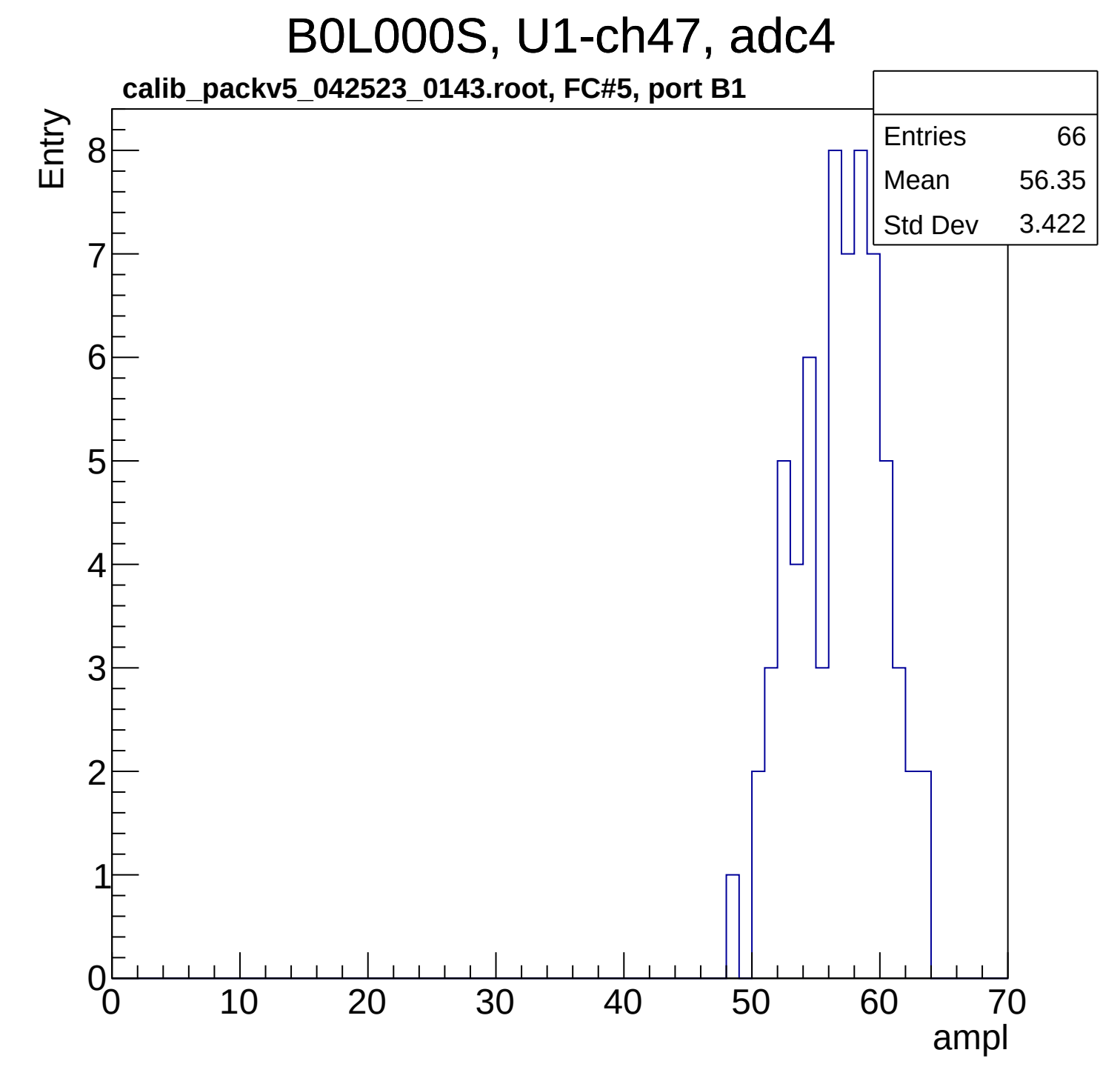
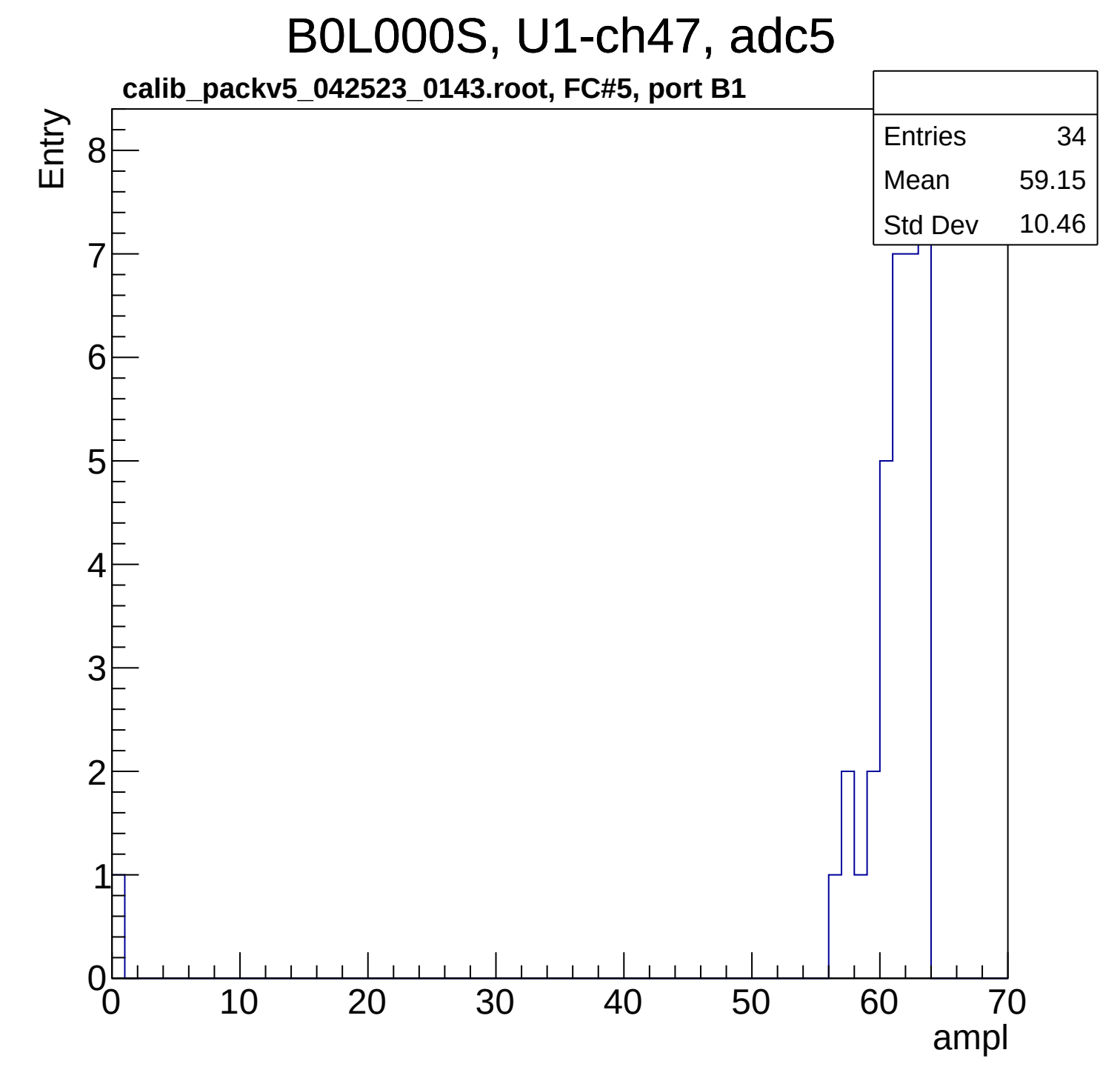

B0L000S, U1-ch47, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 56.35
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch47, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 59.15
Std Dev 10.46
Entry
0
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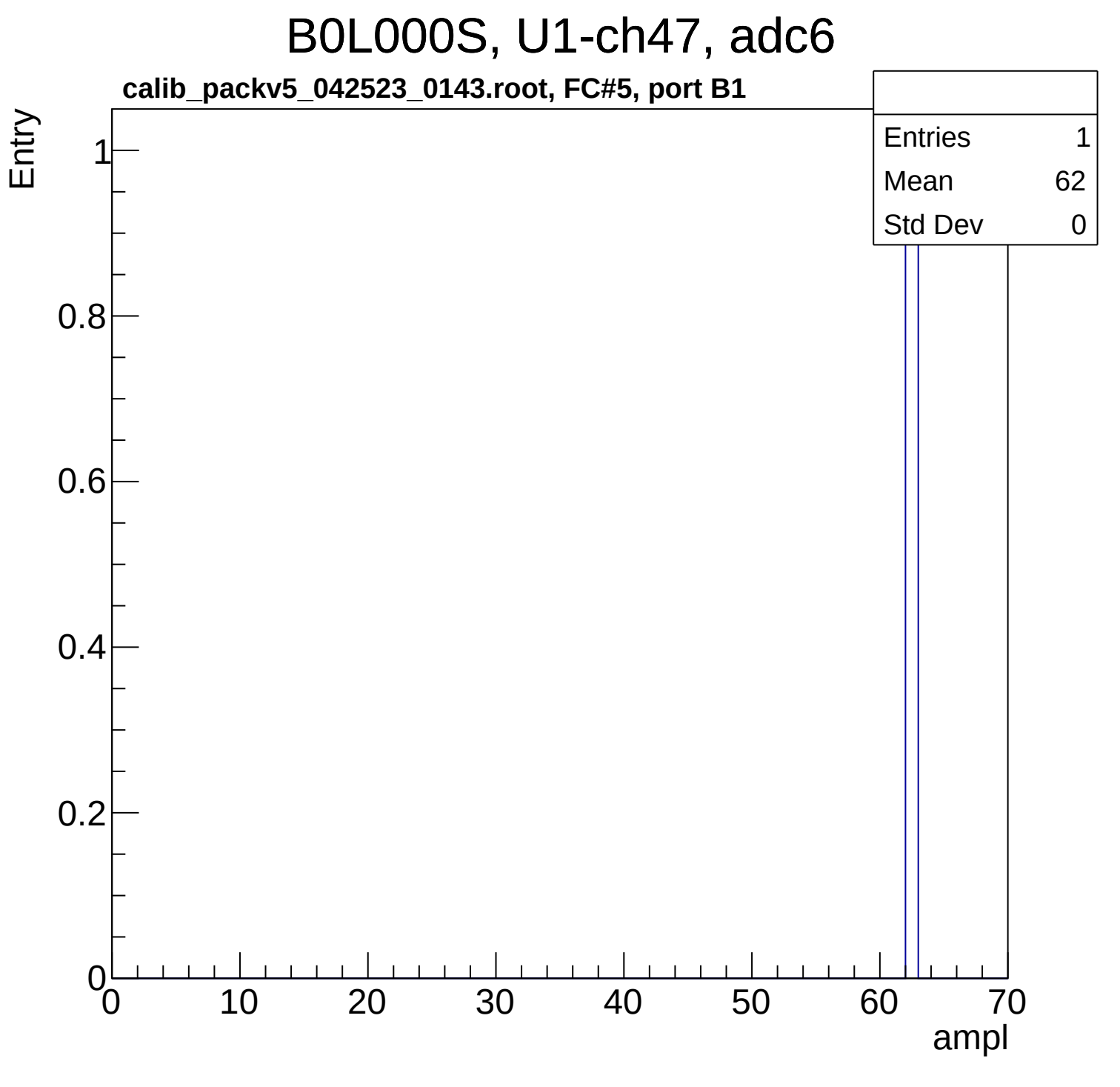
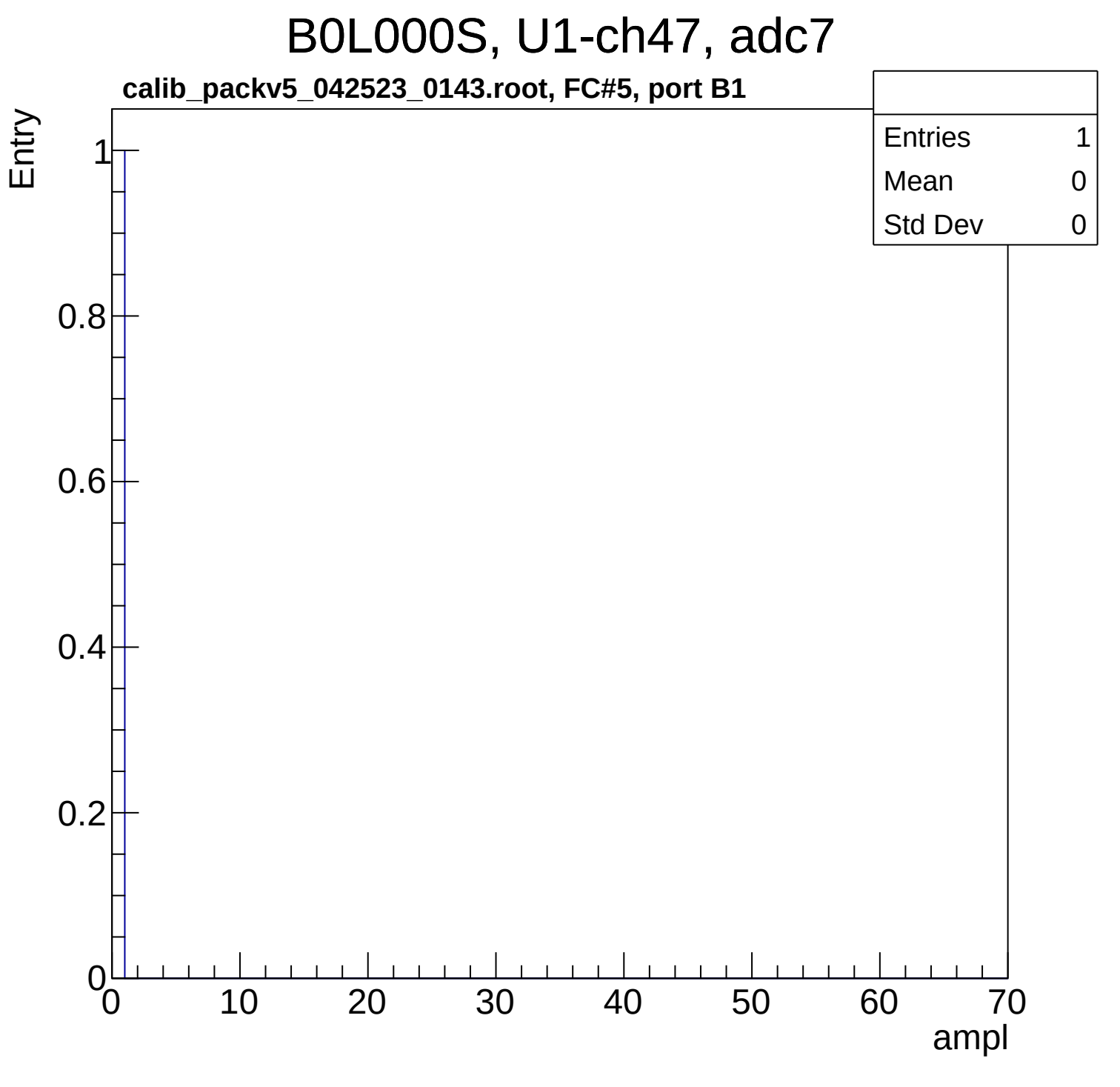
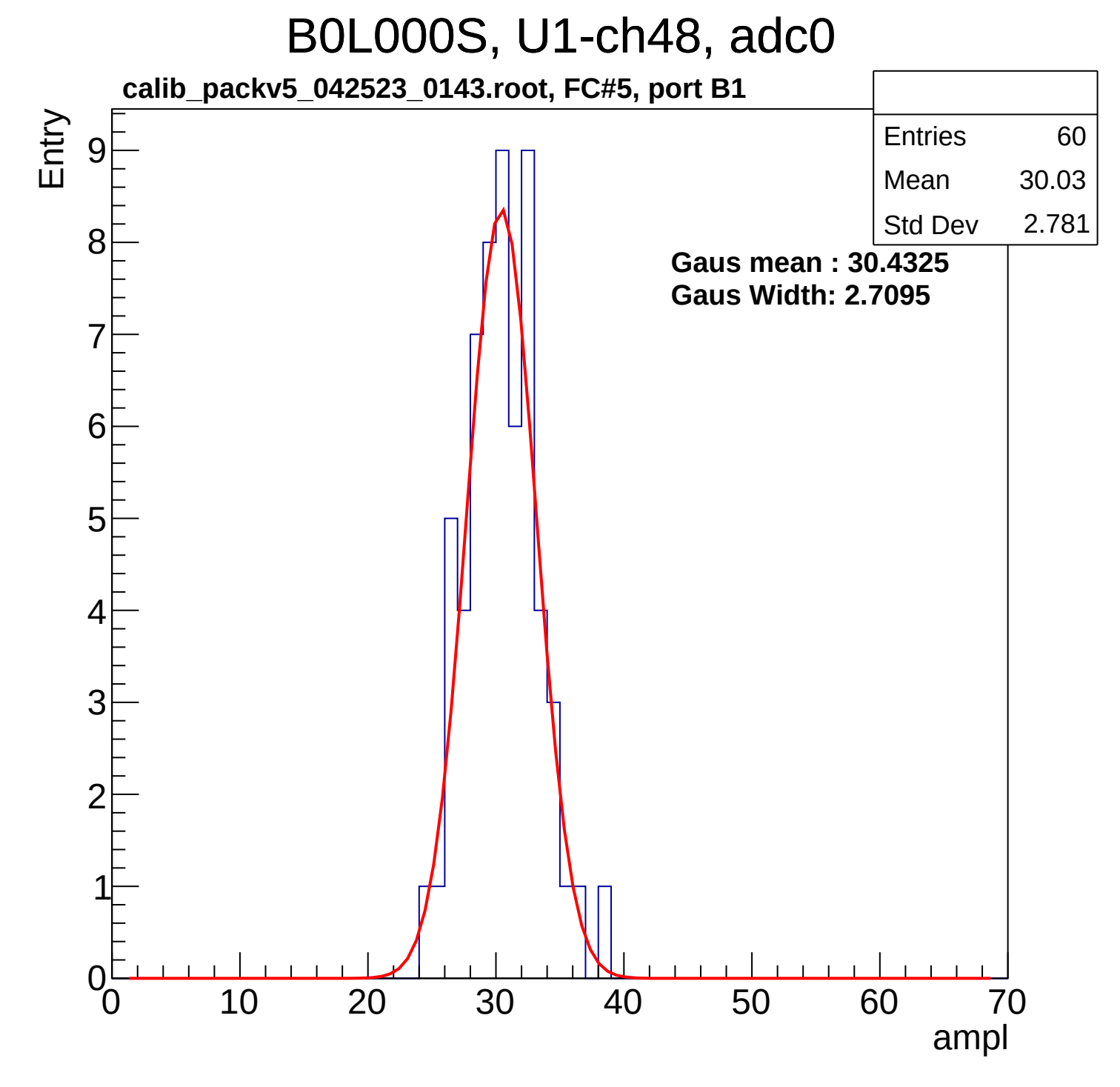
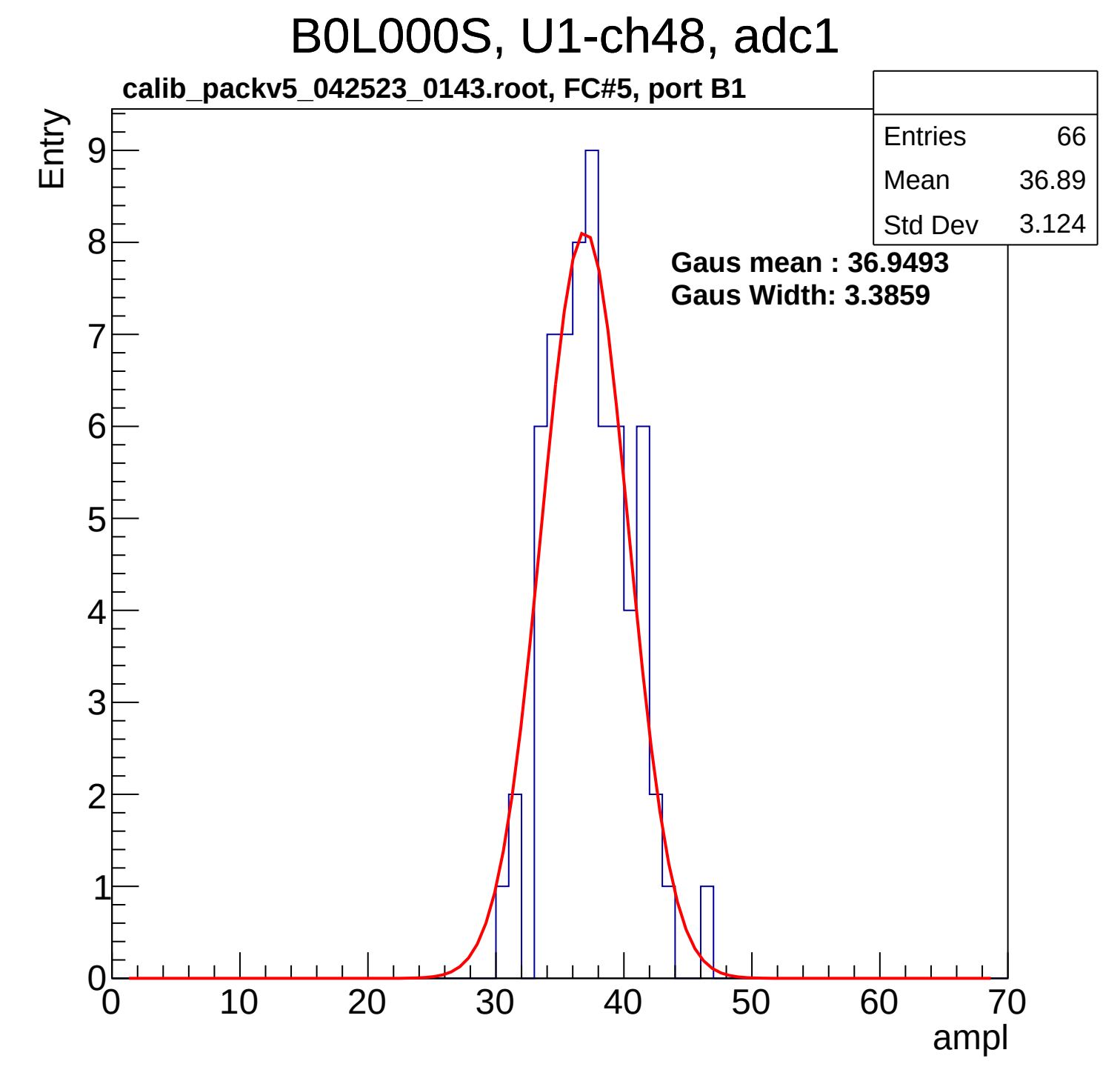
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch47, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch47, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch48, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 30.03
Std Dev 2.781
Gaus mean : 30.4325
Gaus Width: 2.7095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch48, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.89
Std Dev 3.124
Gaus mean : 36.9493
Gaus Width: 3.3859
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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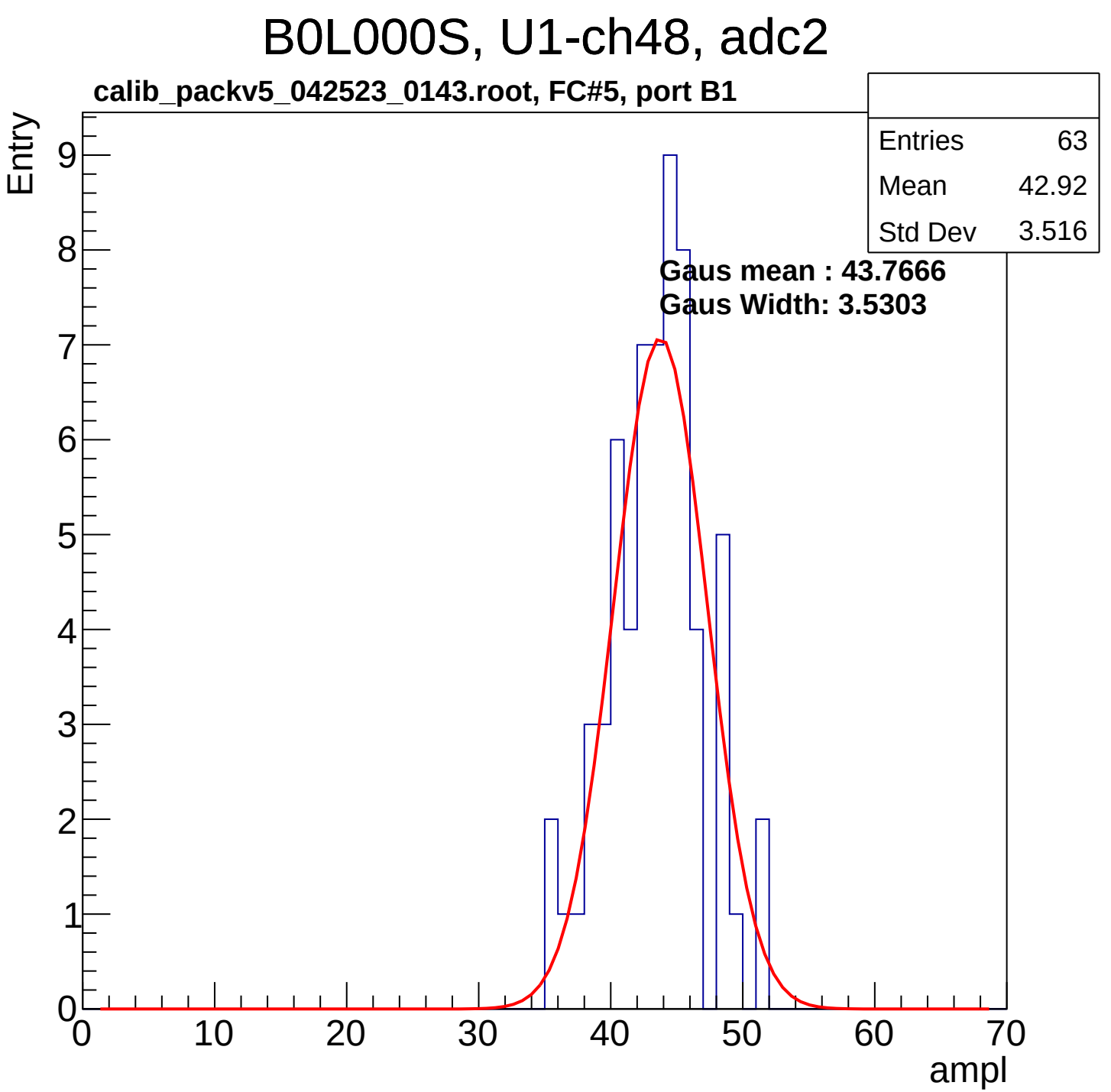
B0L000S, U1-ch48, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.92
Std Dev 3.516
Gaus mean : 43.7666
Gaus Width: 3.5303
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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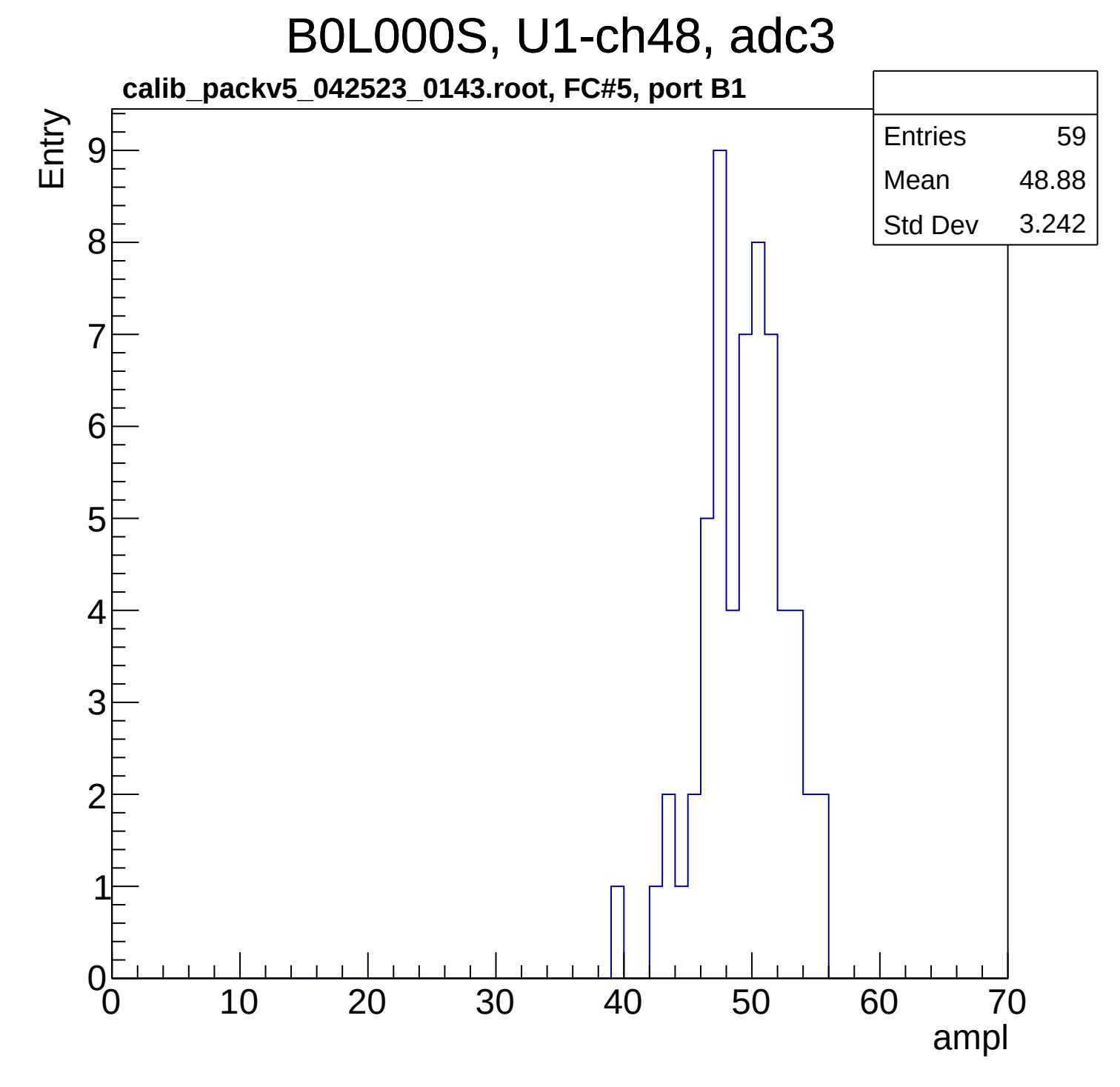
B0L000S, U1-ch48, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48.88
Std Dev 3.242
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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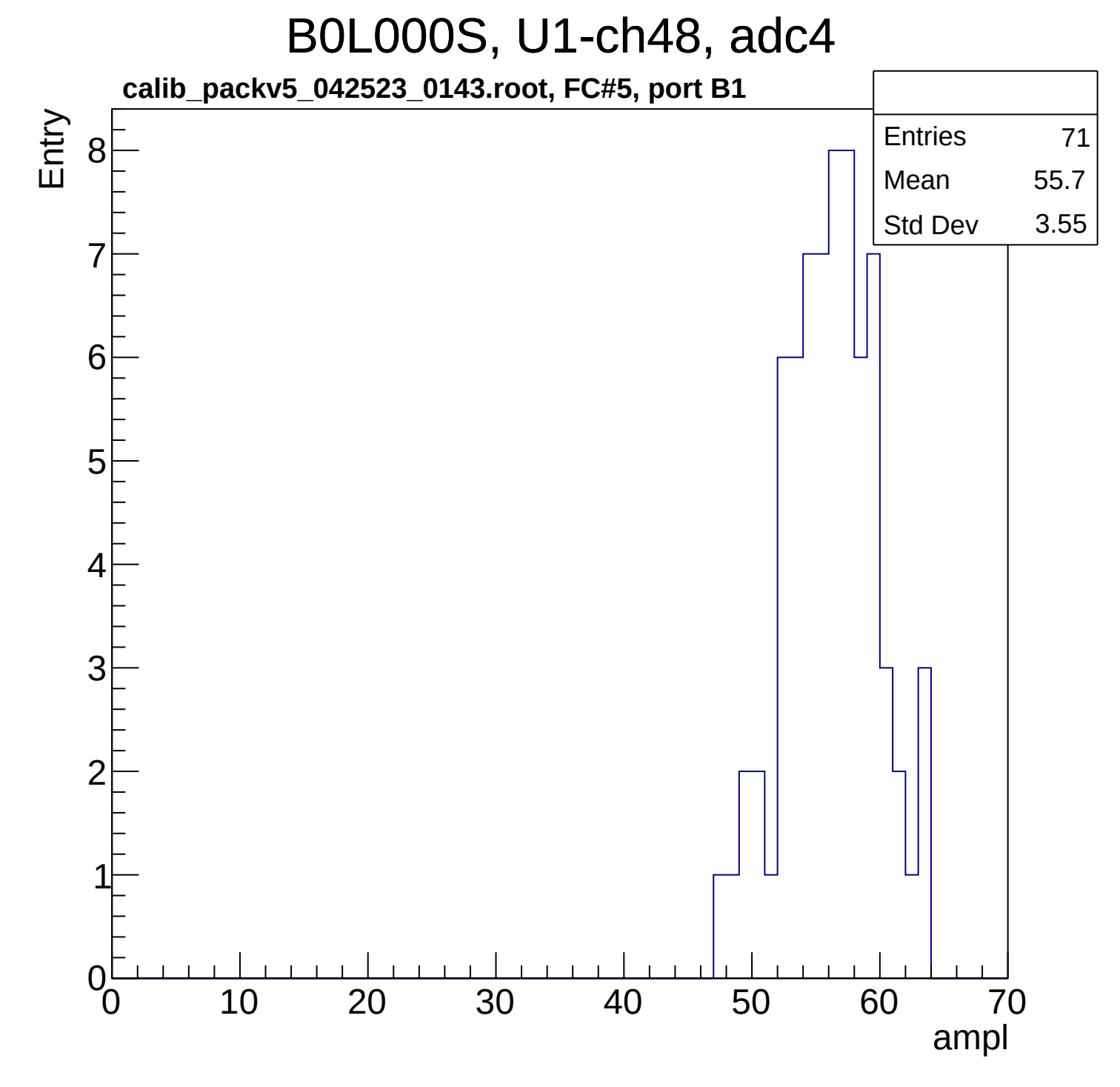
40
50
60
70


B0L000S, U1-ch48, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 55.7
Std Dev 3.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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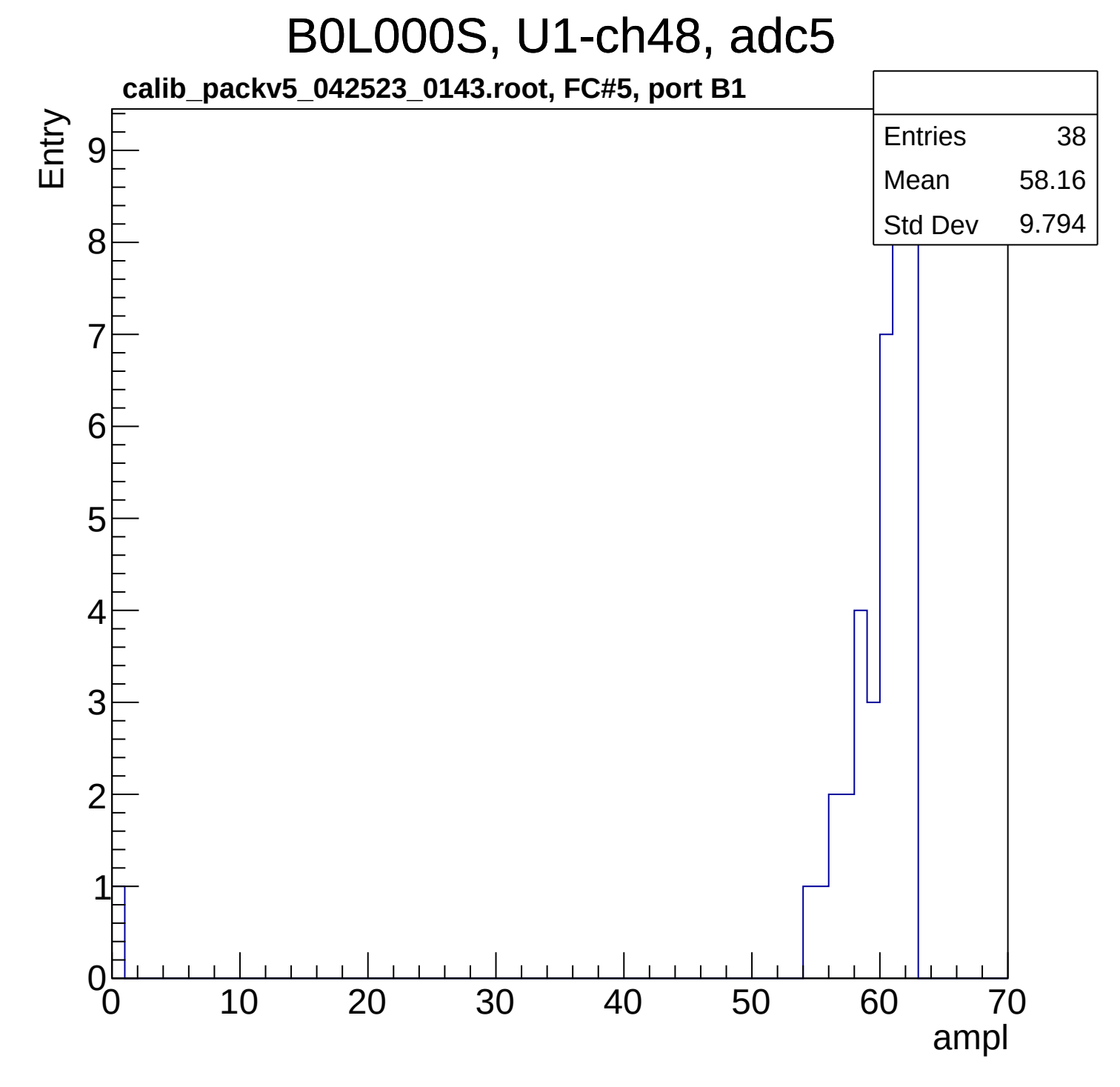
B0L000S, U1-ch48, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.16
Std Dev 9.794
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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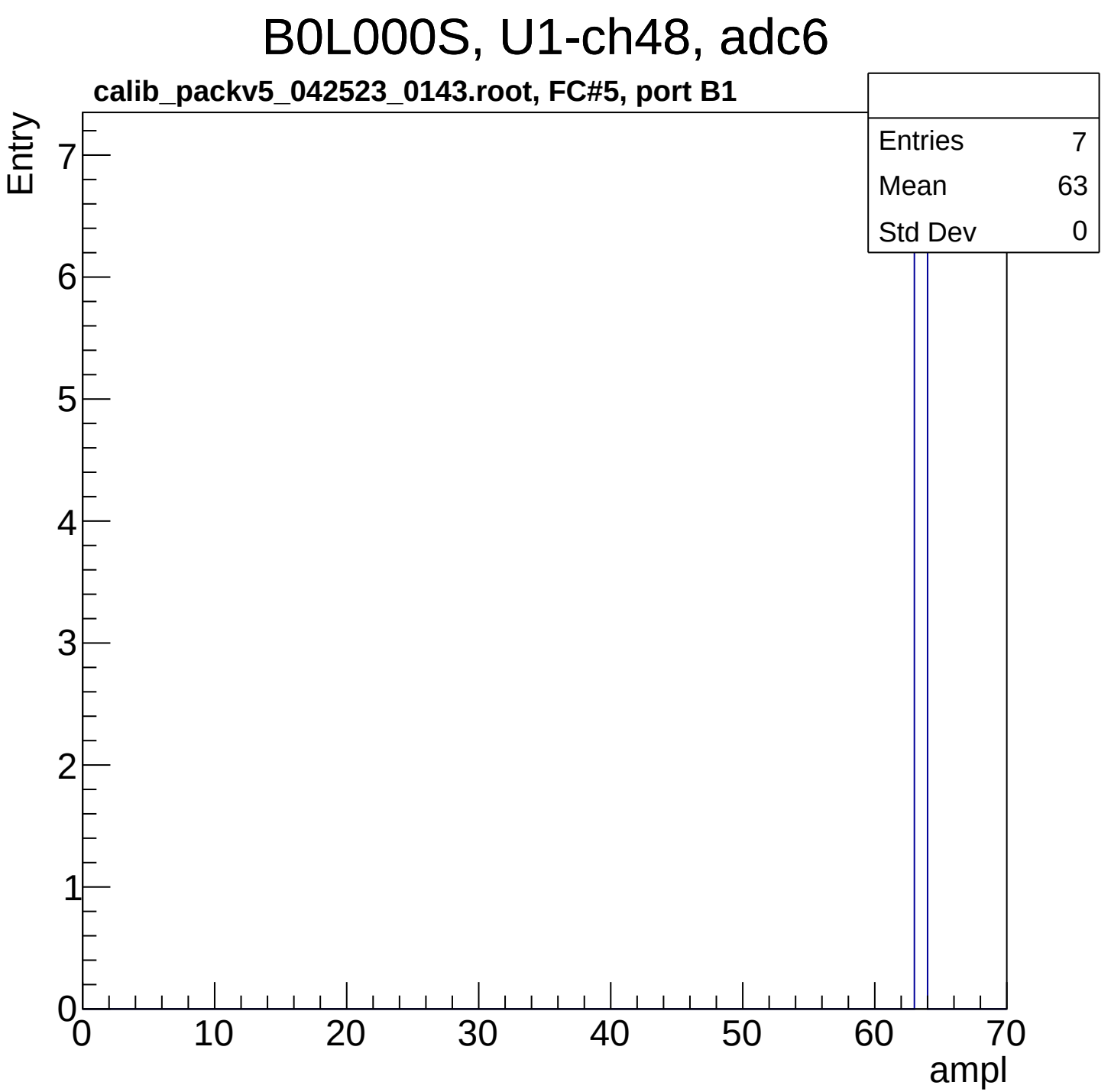
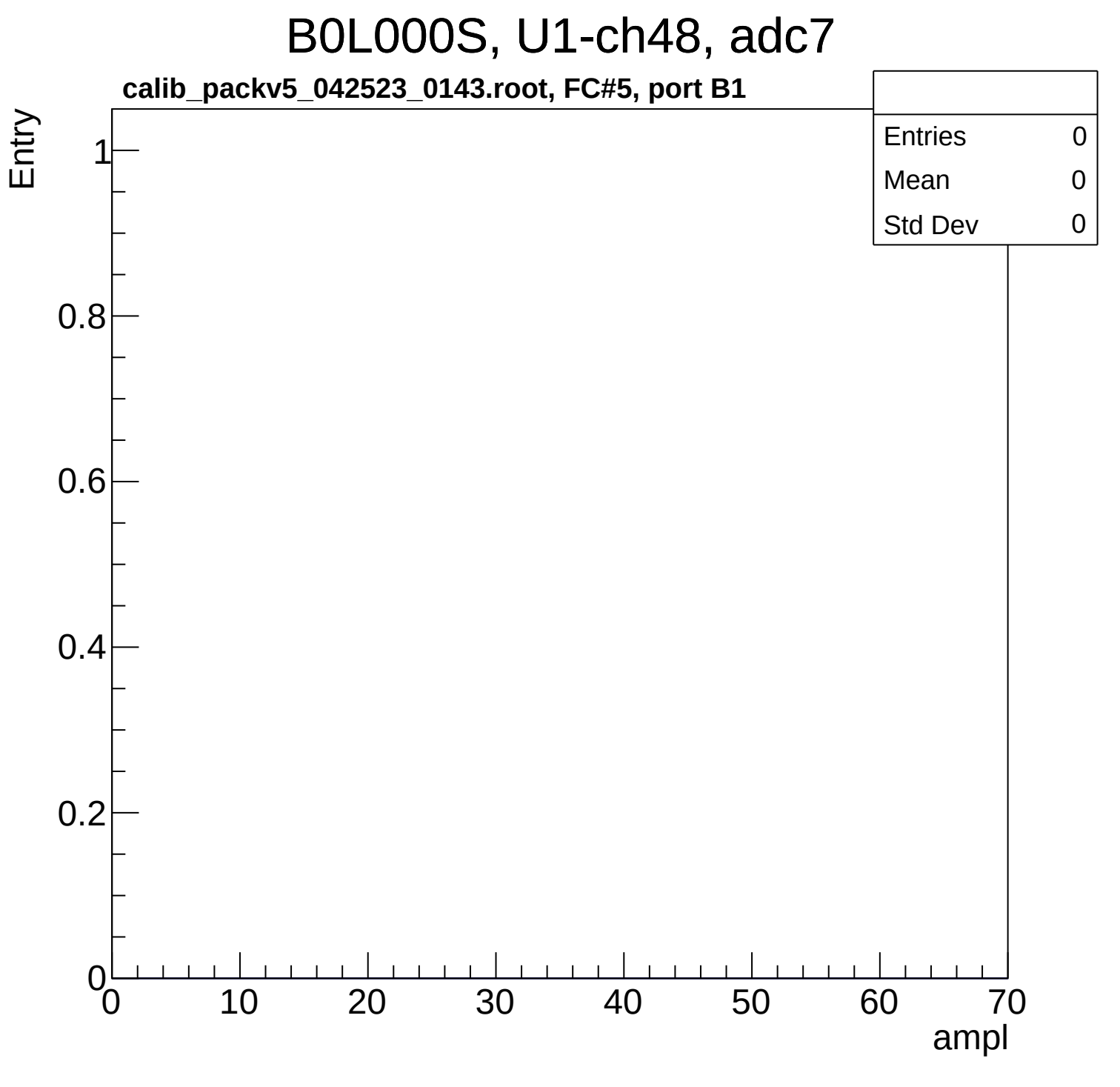

B0L000S, U1-ch48, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 63
Std Dev 0
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch48, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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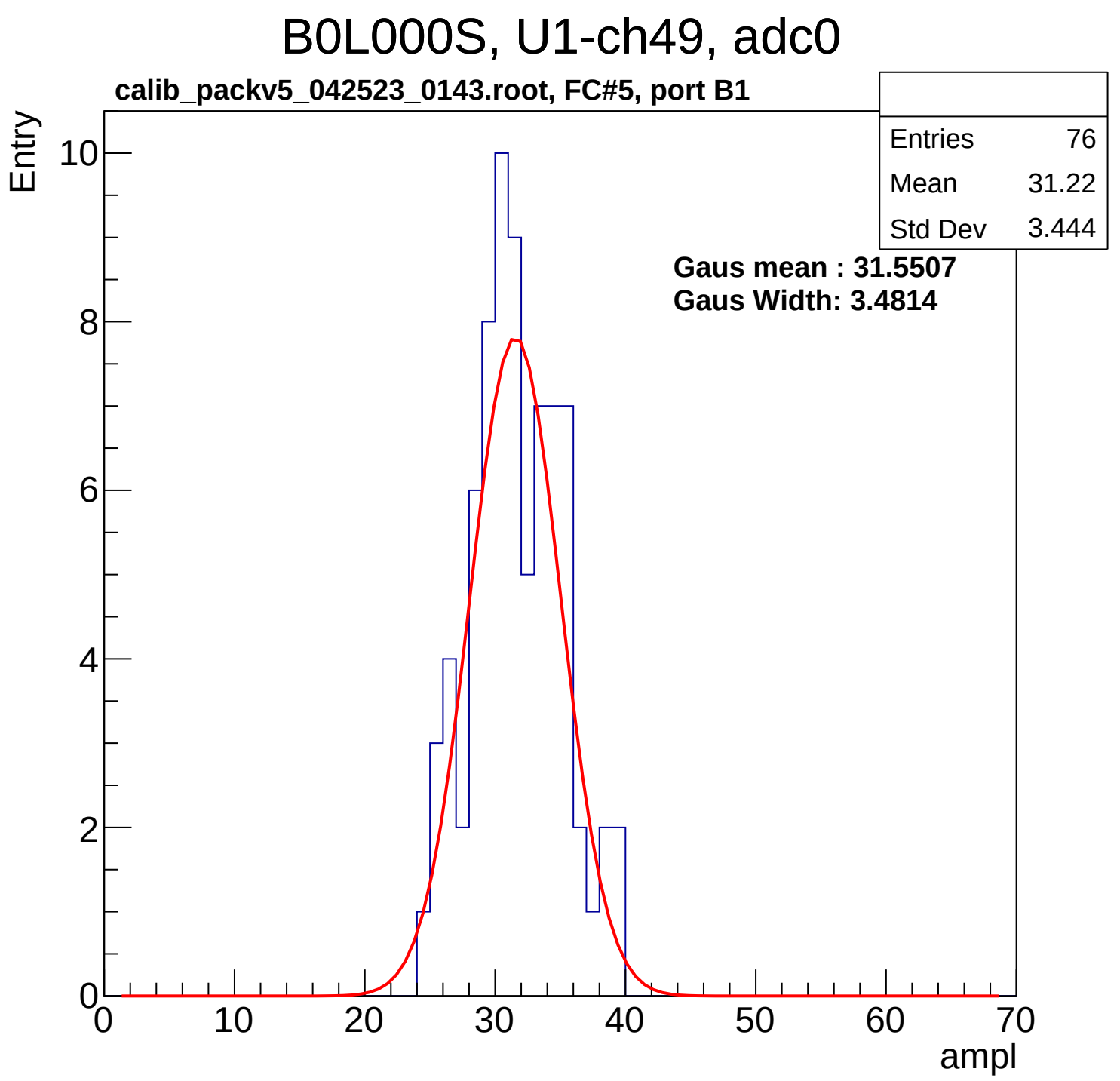
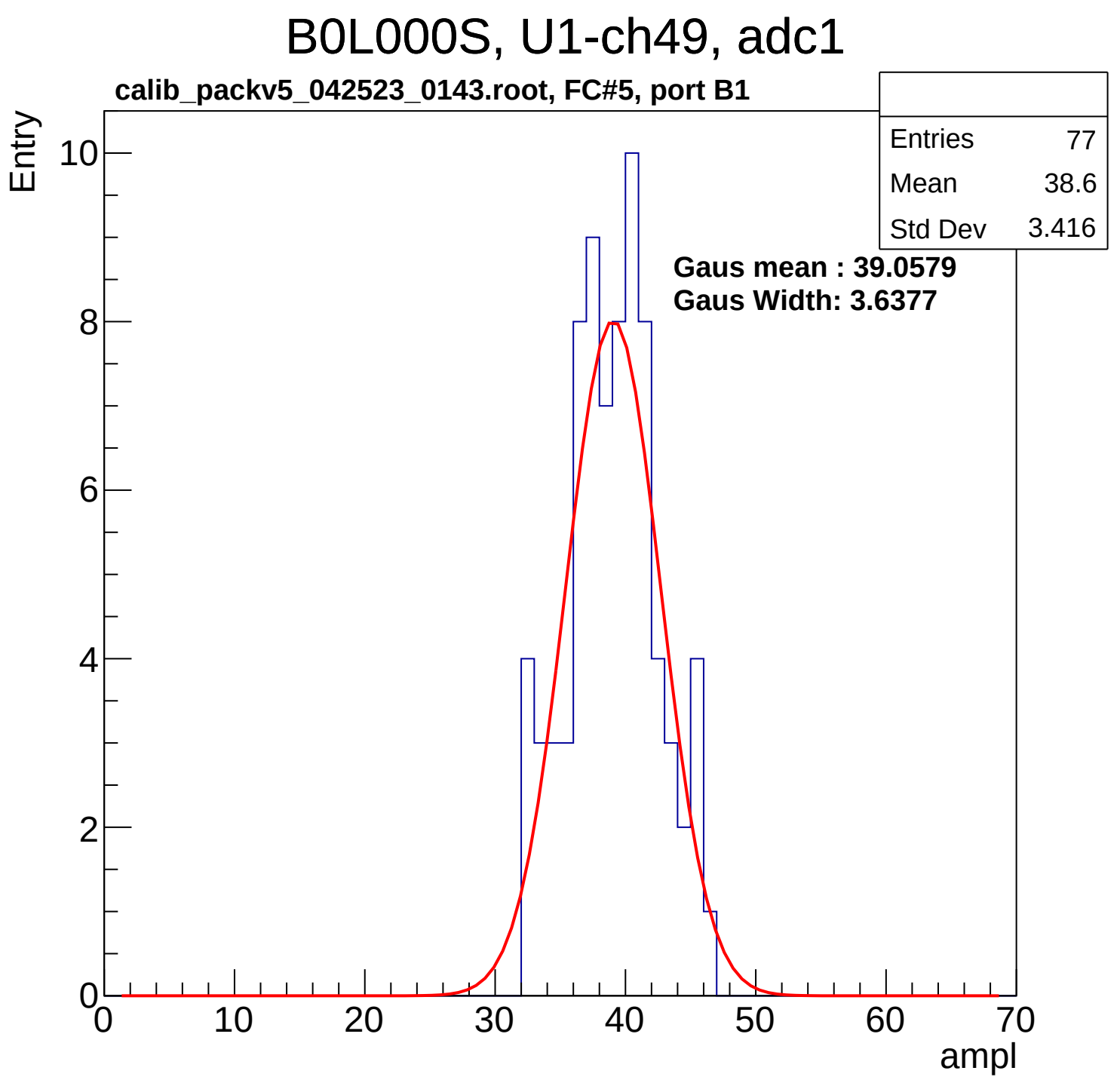
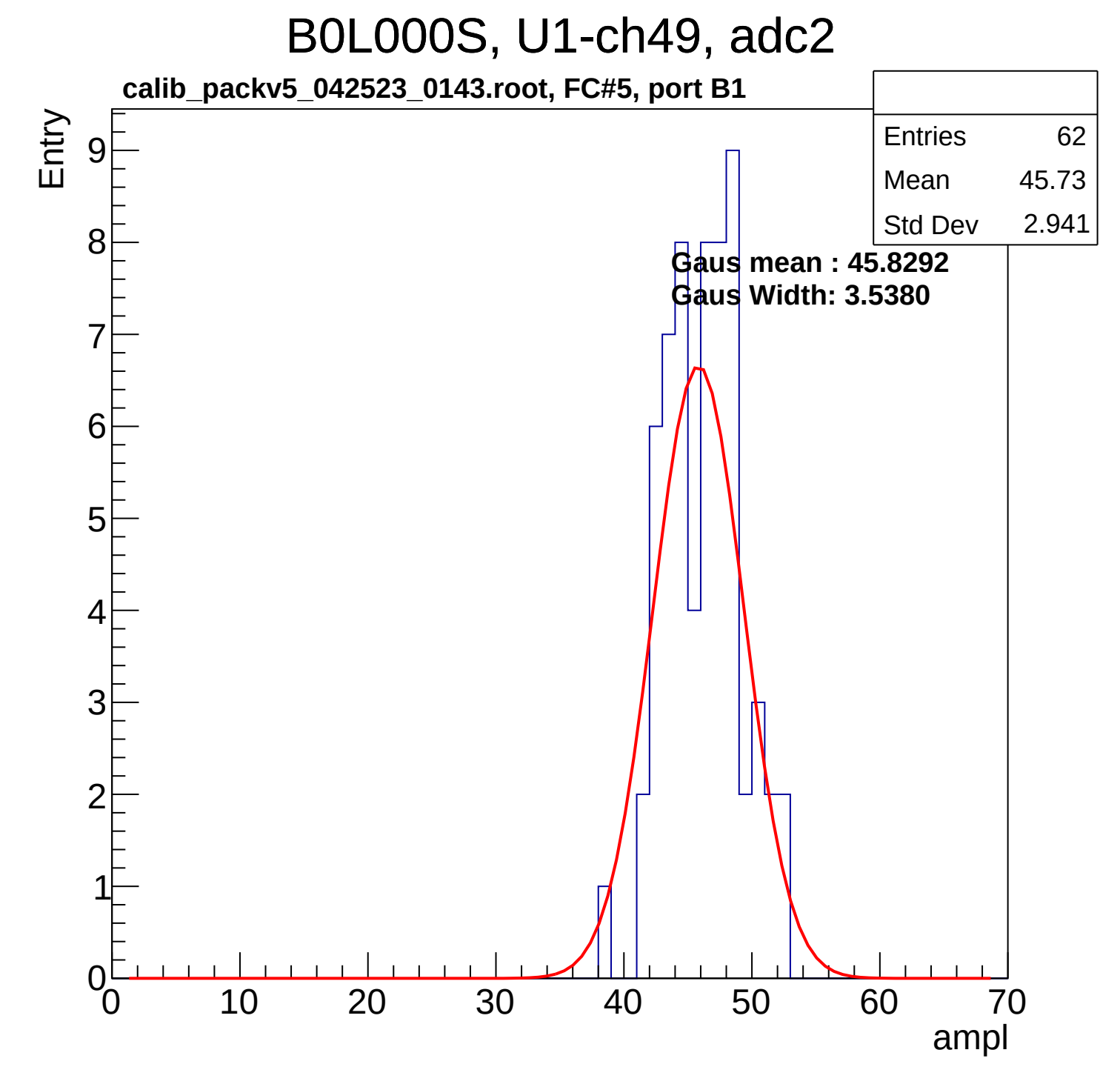
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch49, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 31.22
Std Dev 3.444
Gaus mean : 31.5507
Gaus Width: 3.4814
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch49, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 38.6
Std Dev 3.416
Gaus mean : 39.0579
Gaus Width: 3.6377
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch49, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 45.73
Std Dev 2.941
Gaus mean : 45.8292
Gaus Width: 3.5380
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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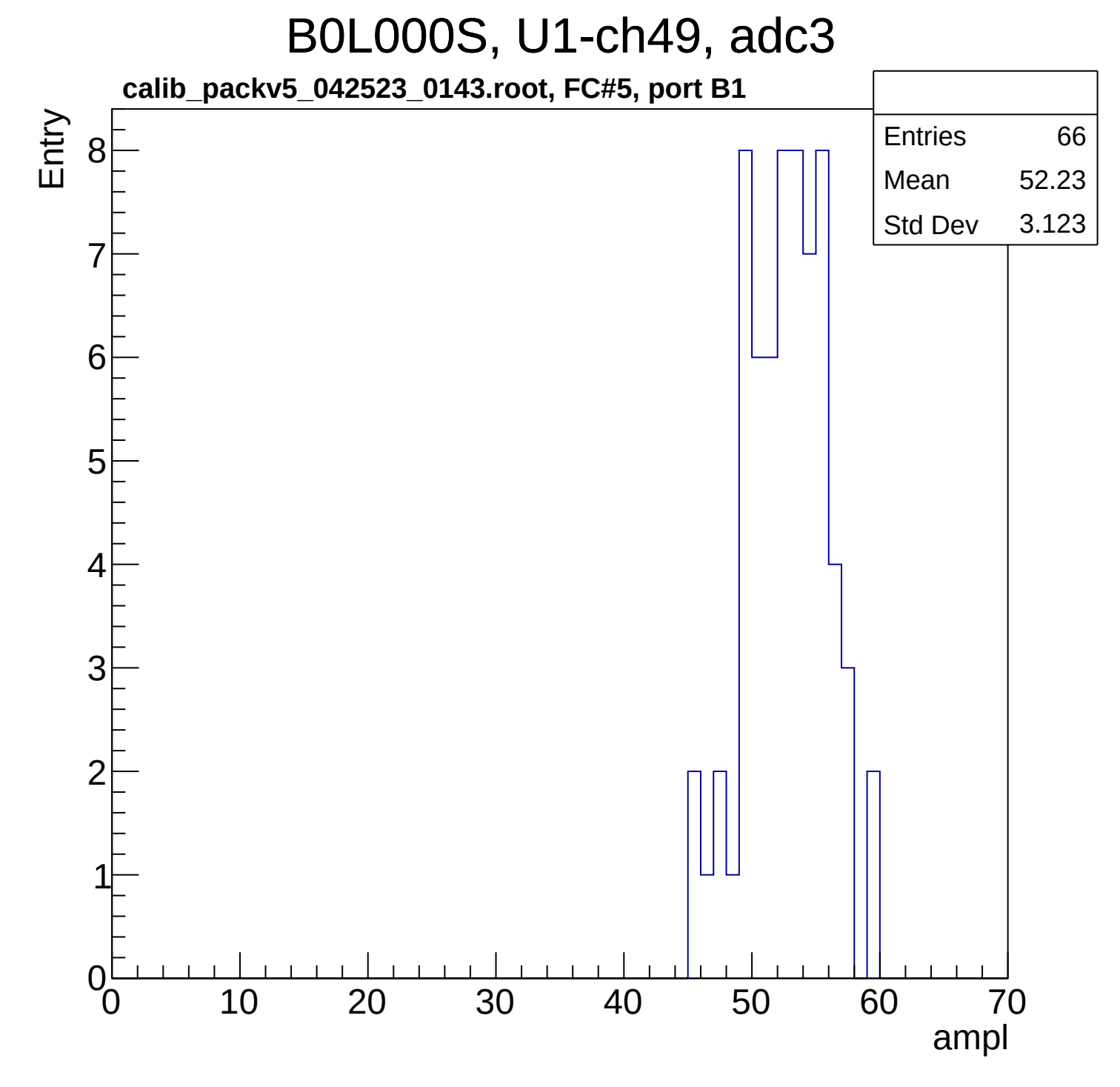
B0L000S, U1-ch49, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 52.23
Std Dev 3.123
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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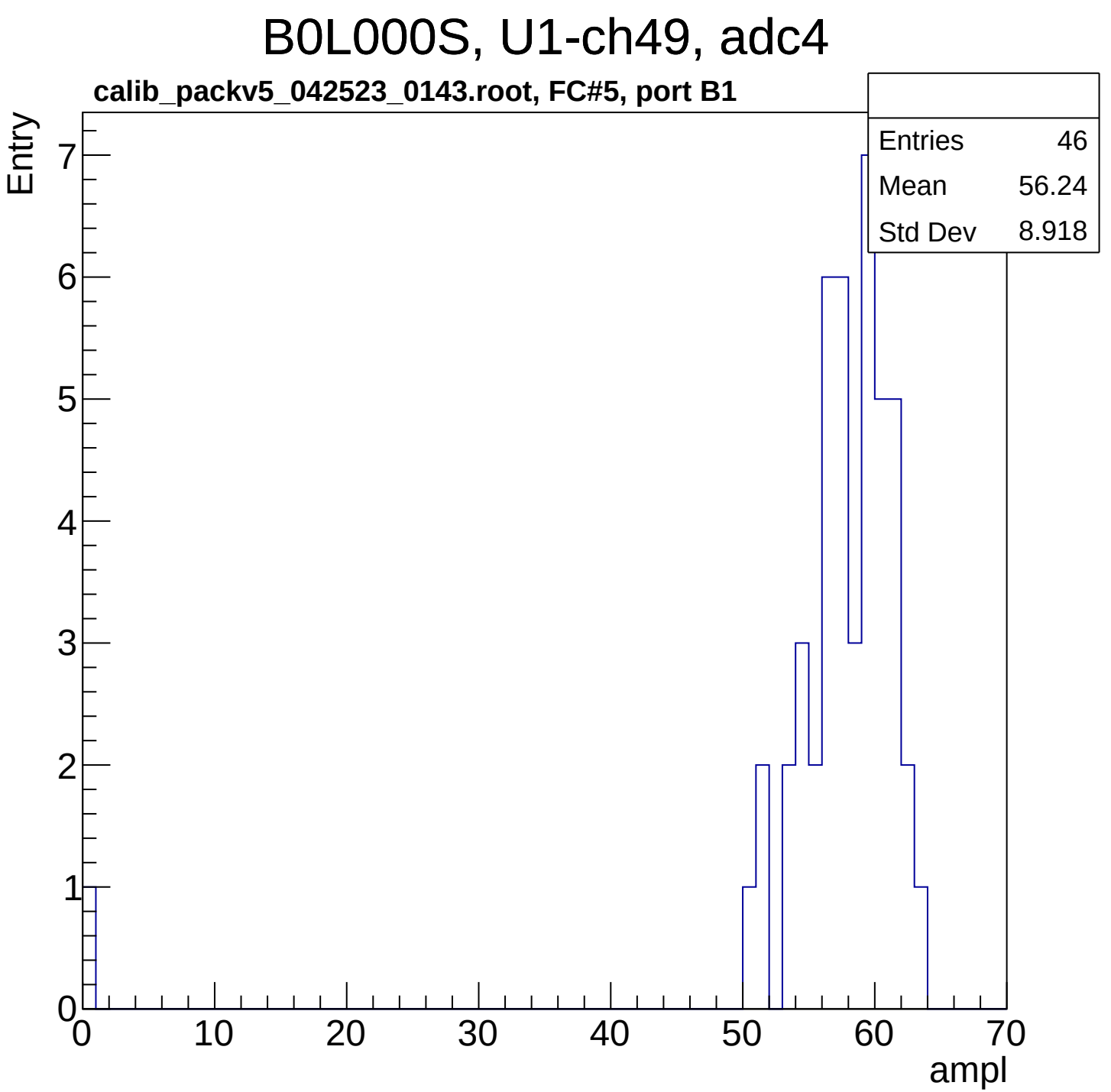
B0L000S, U1-ch49, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 56.24
Std Dev 8.918
Entry
ampl
0
1
2
3
4
5
6
7
0
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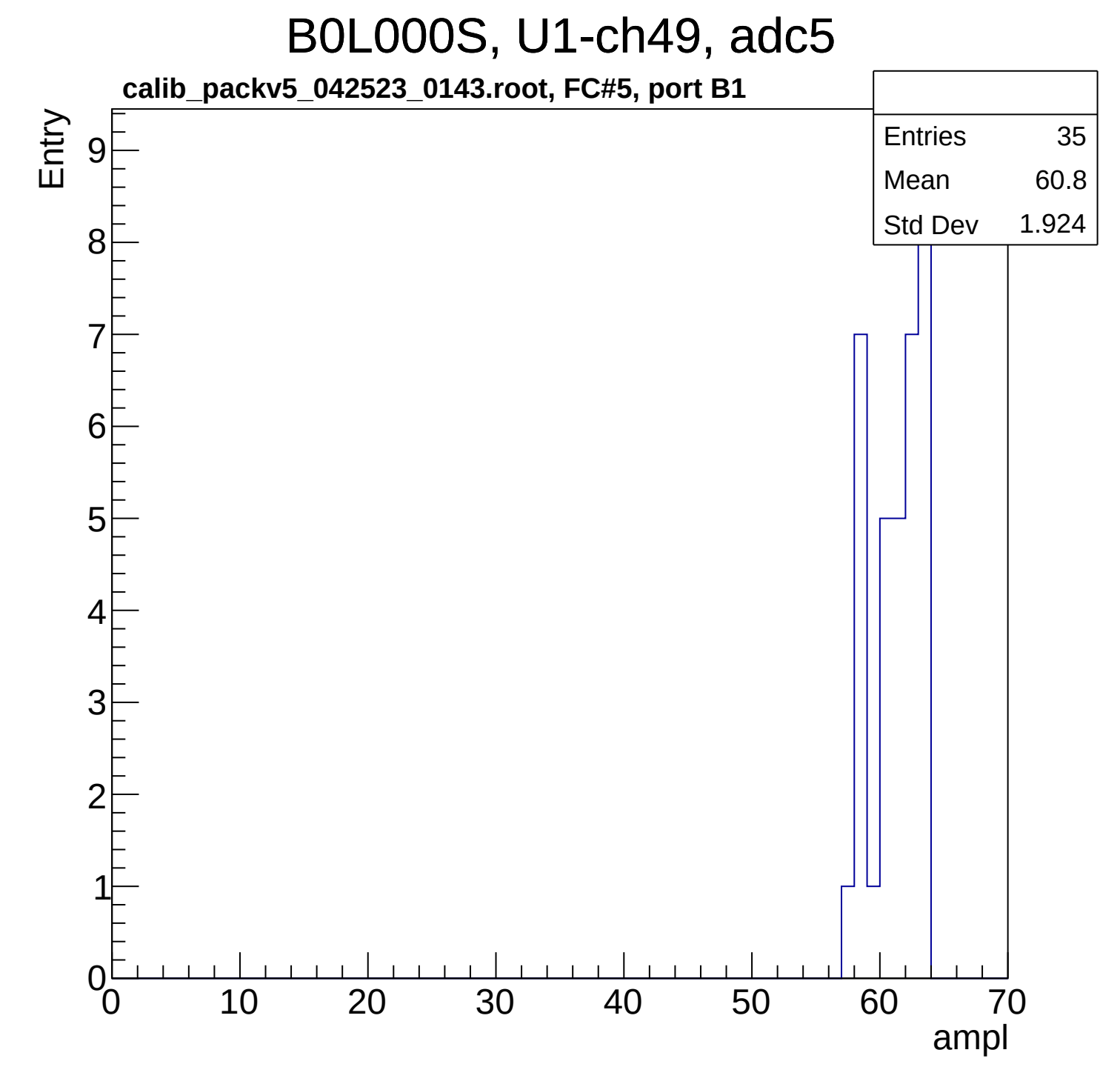
B0L000S, U1-ch49, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 60.8
Std Dev 1.924
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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60
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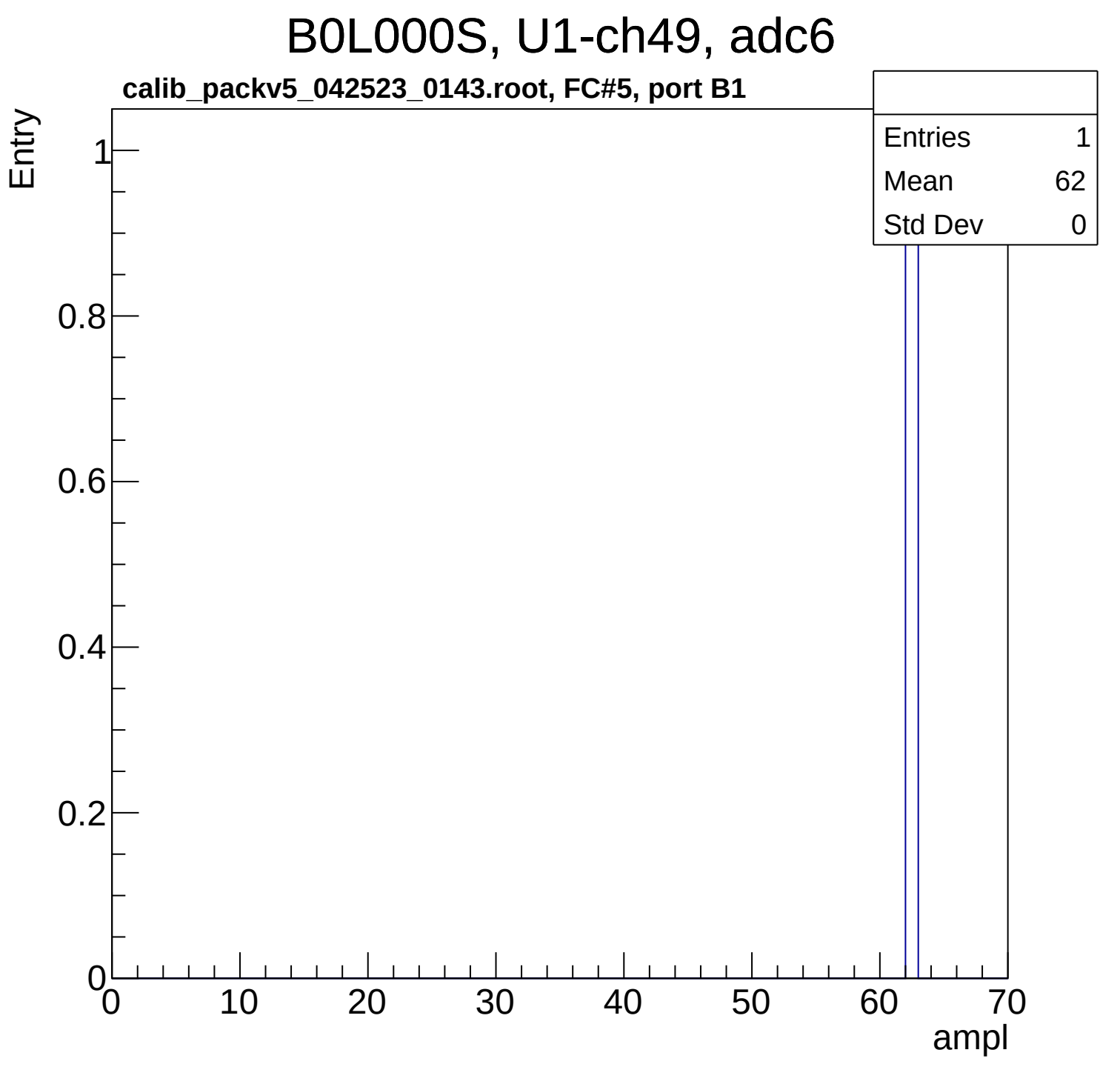
B0L000S, U1-ch49, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
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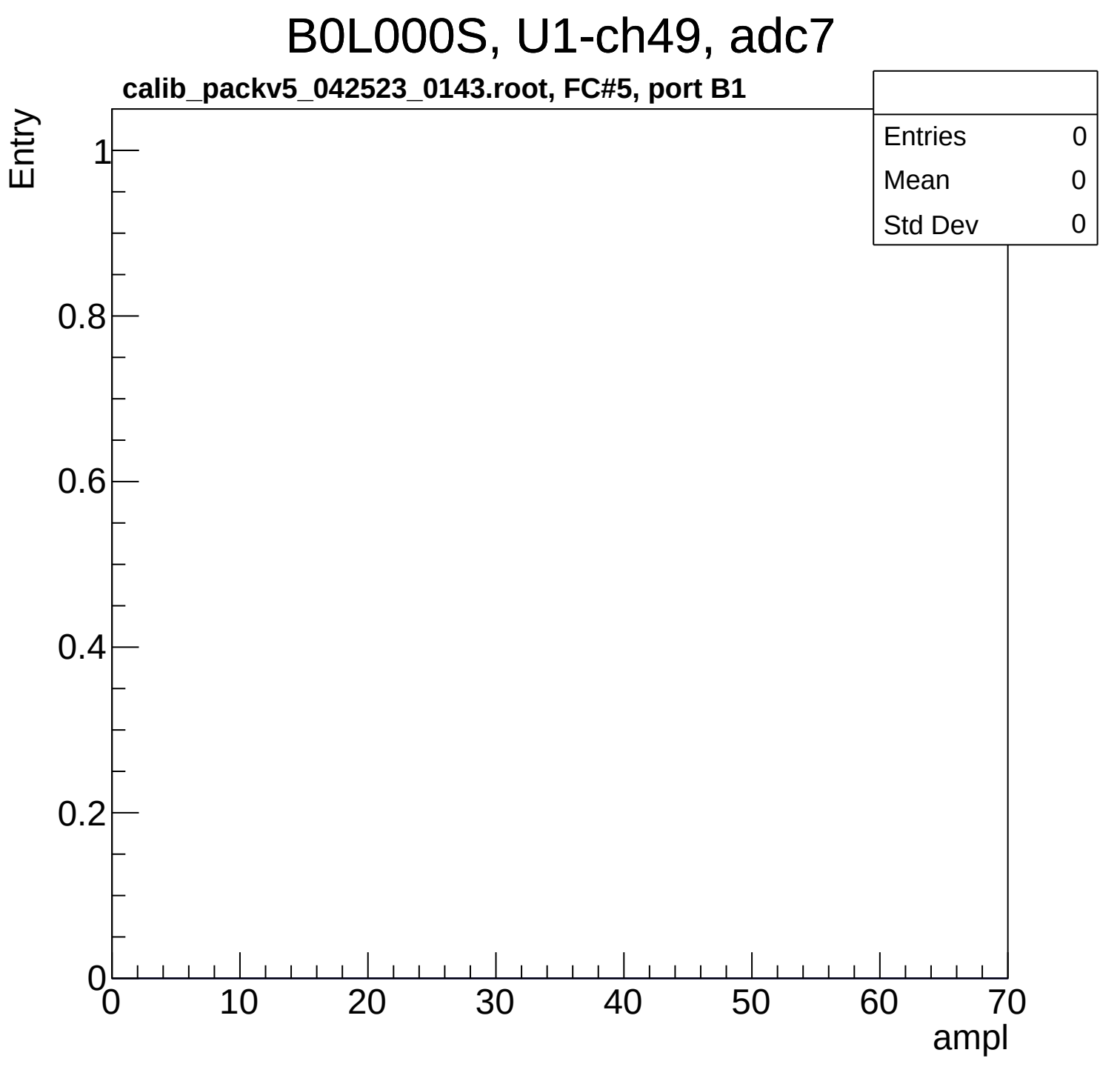
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch49, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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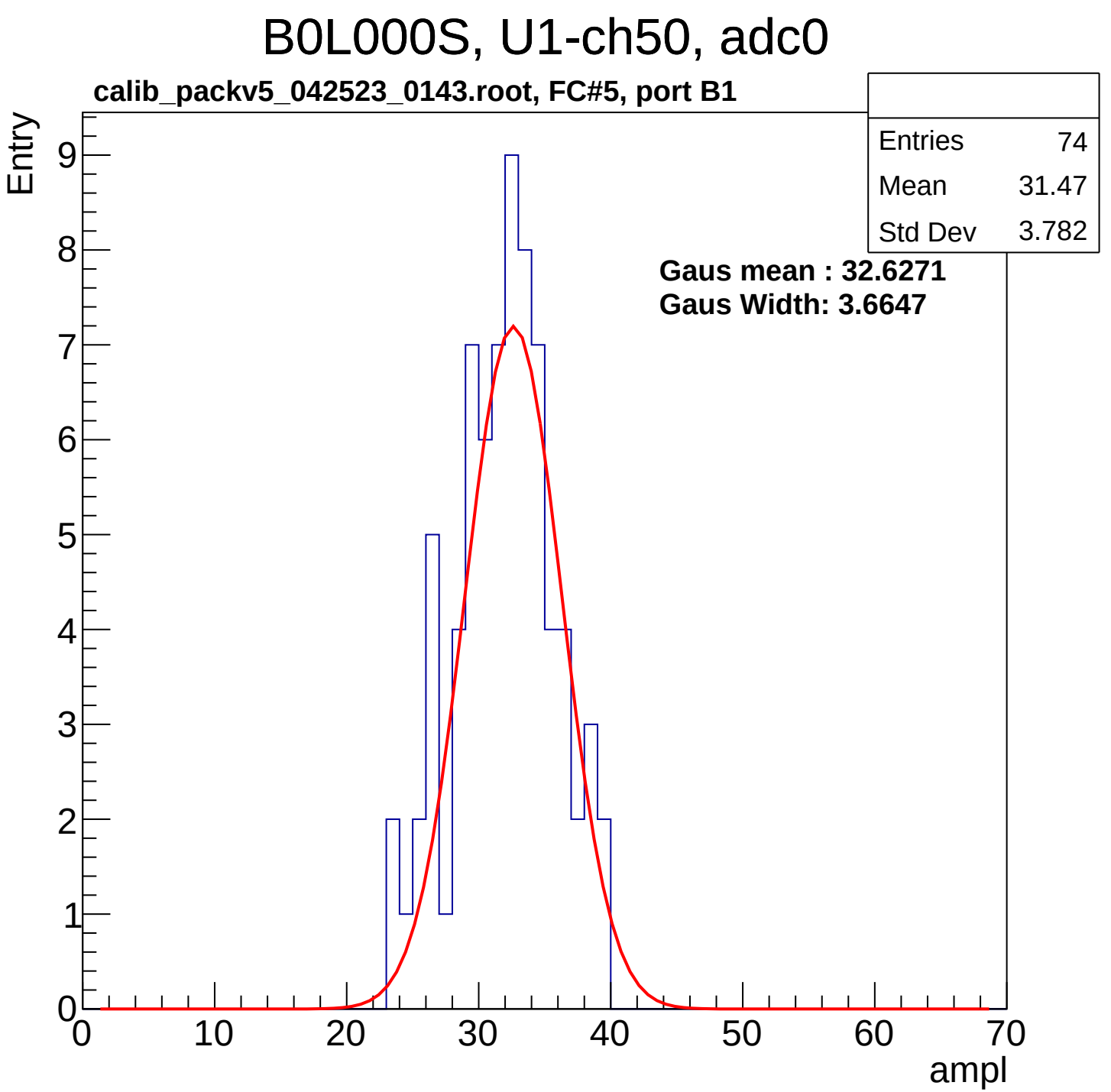
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch50, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 31.47
Std Dev 3.782
Gaus mean : 32.6271
Gaus Width: 3.6647
Entry
ampl
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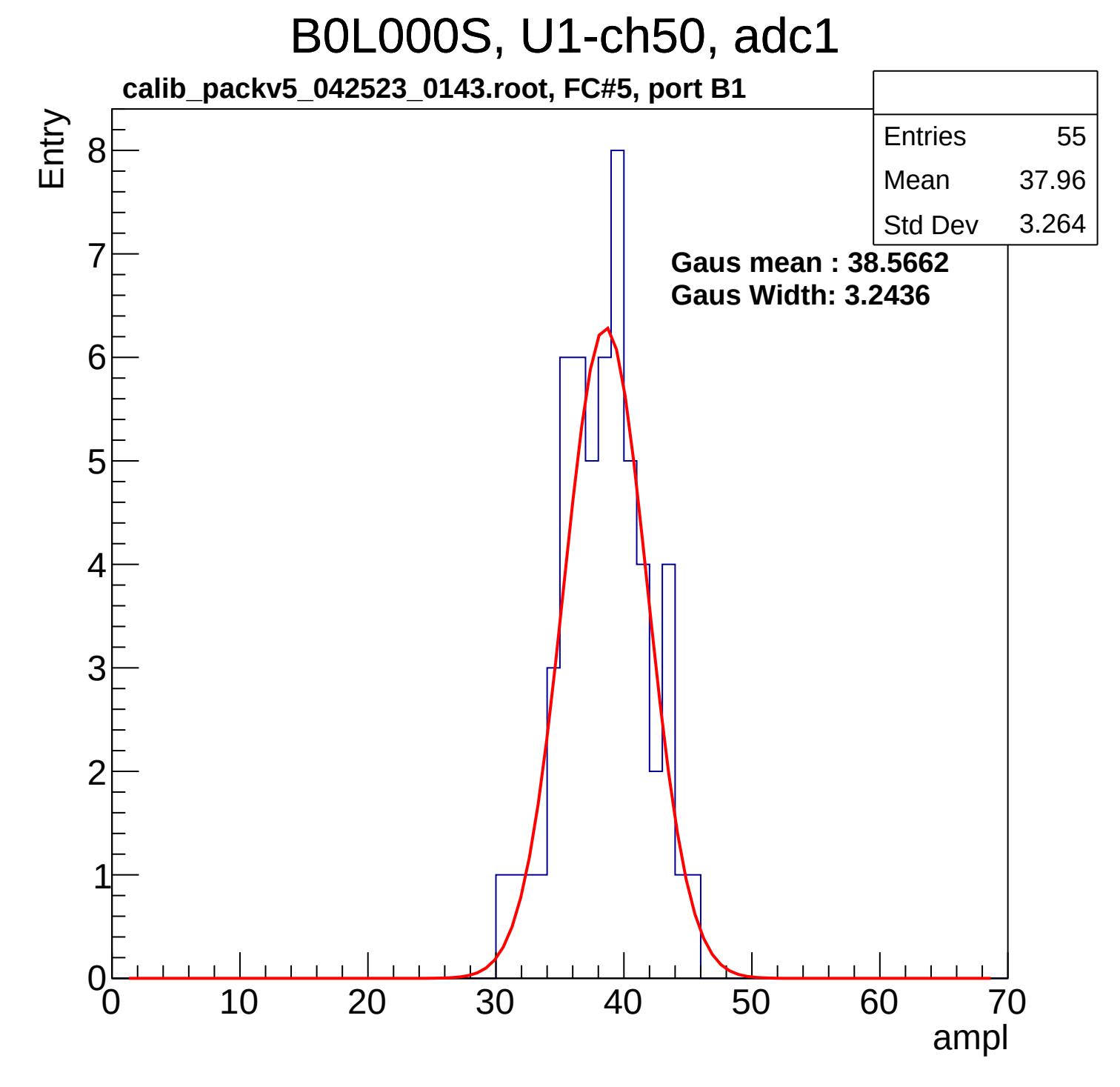
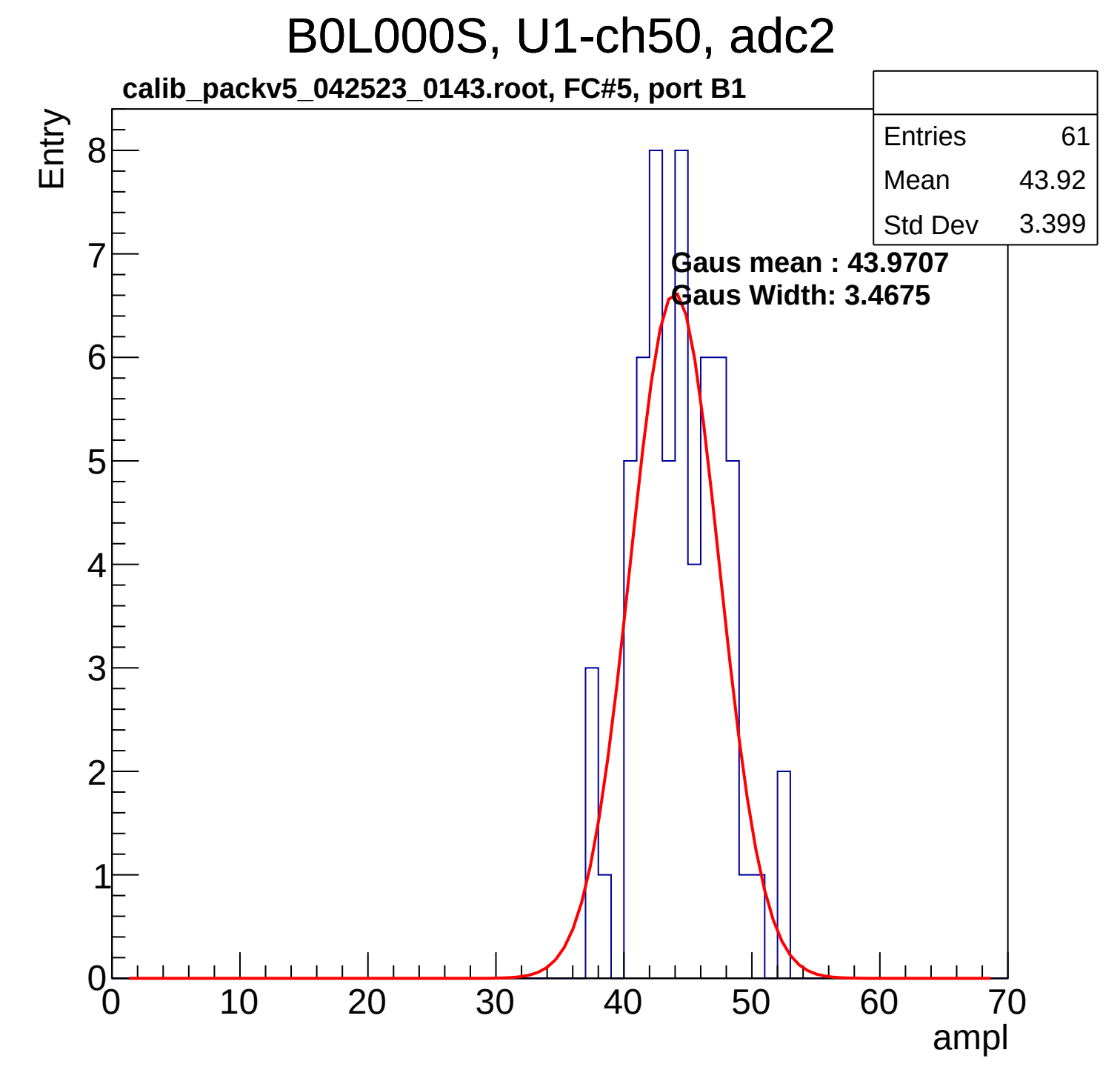
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch50, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 37.96
Std Dev 3.264
Gaus mean : 38.5662
Gaus Width: 3.2436
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch50, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 43.92
Std Dev 3.399
Gaus mean : 43.9707
Gaus Width: 3.4675
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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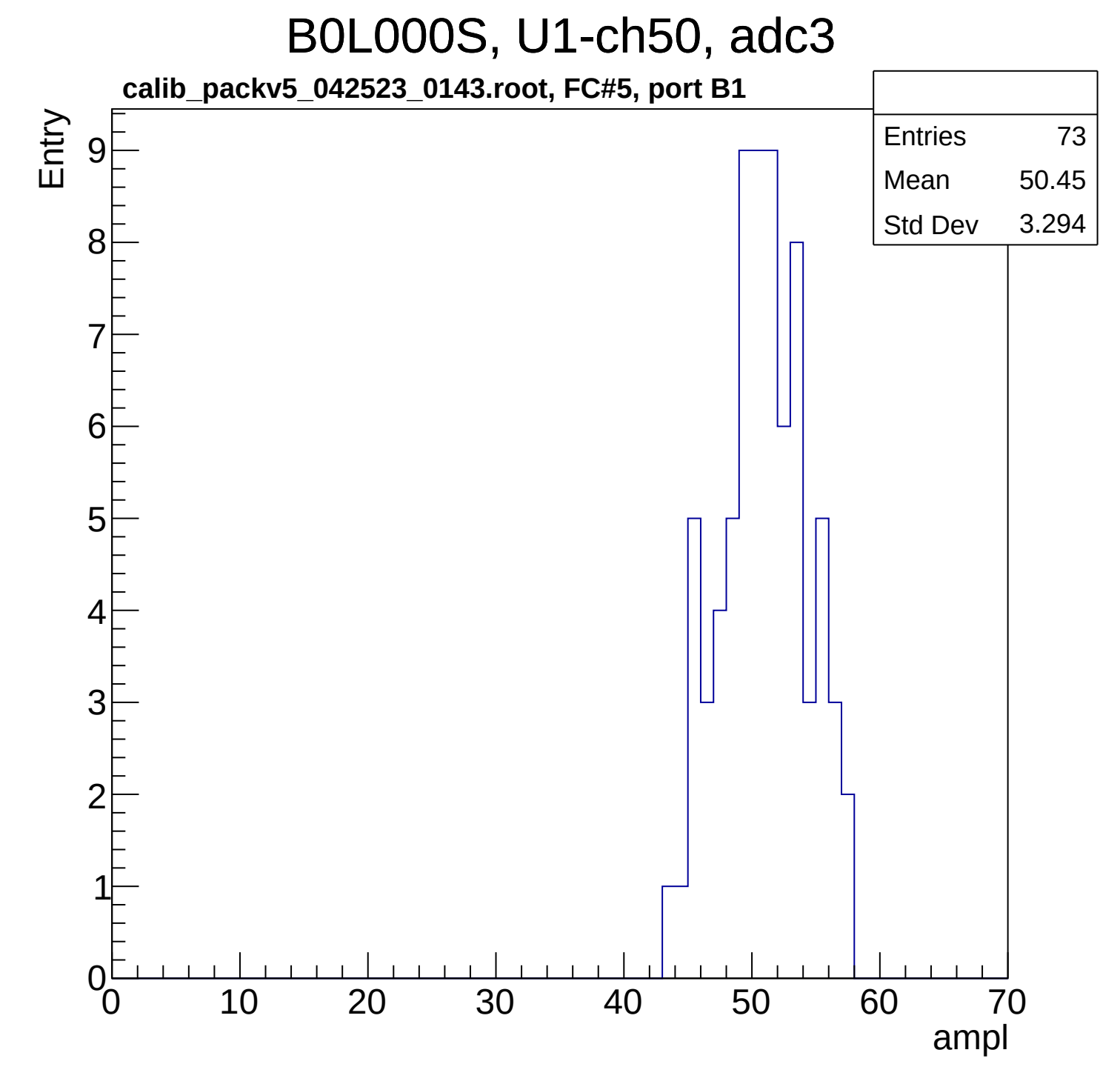
B0L000S, U1-ch50, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 50.45
Std Dev 3.294
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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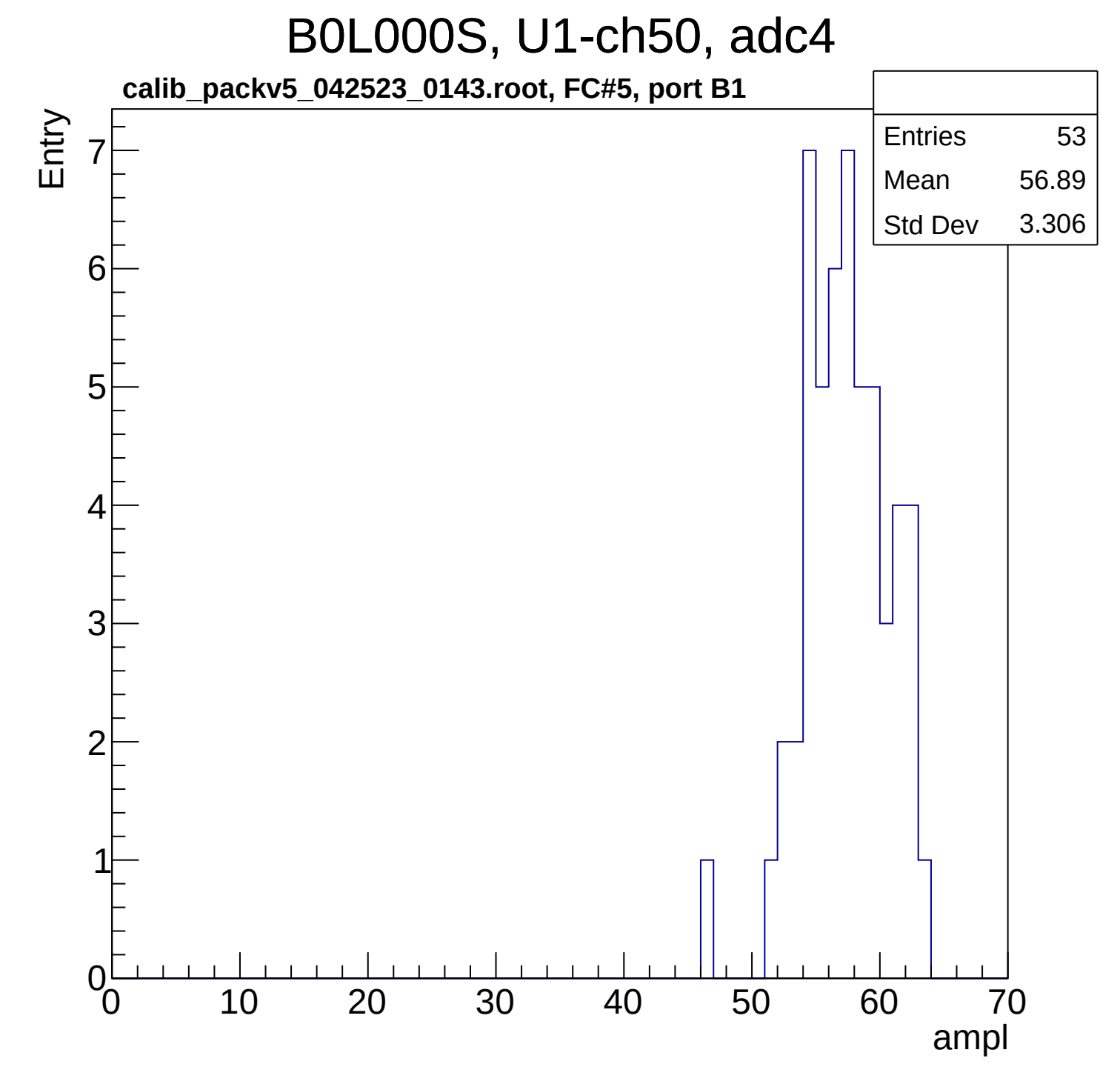
B0L000S, U1-ch50, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.89
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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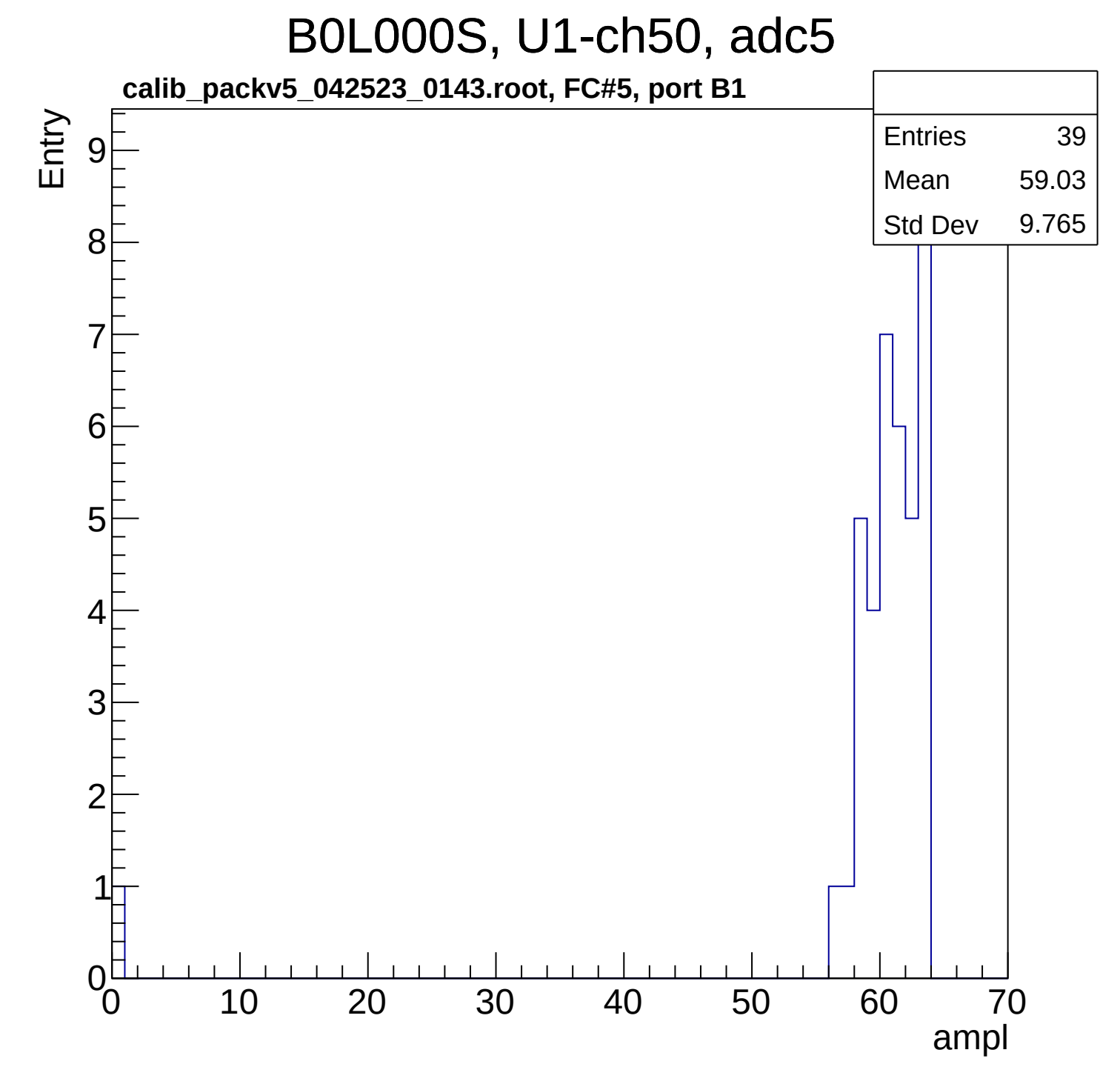
B0L000S, U1-ch50, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 59.03
Std Dev 9.765
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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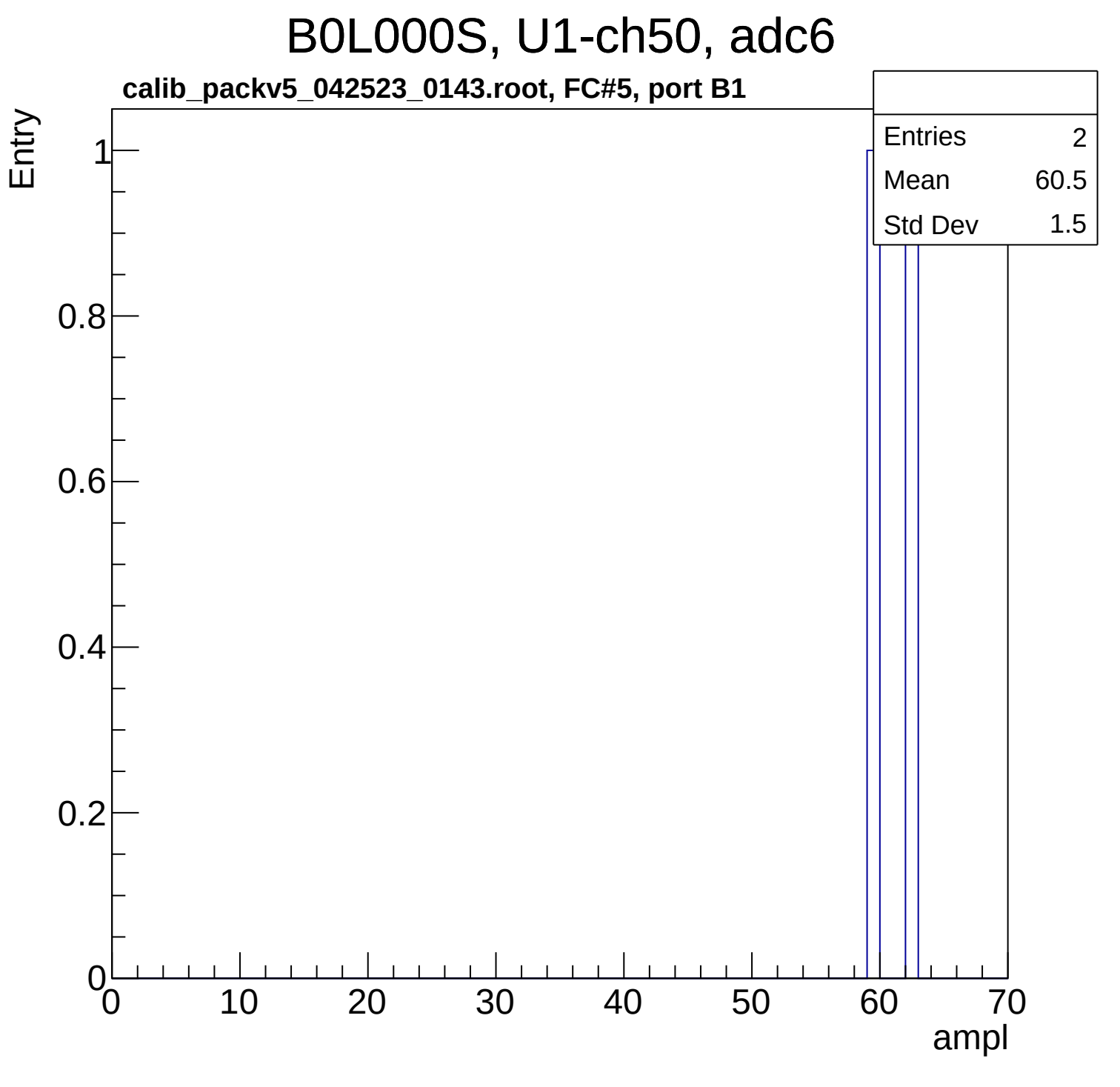
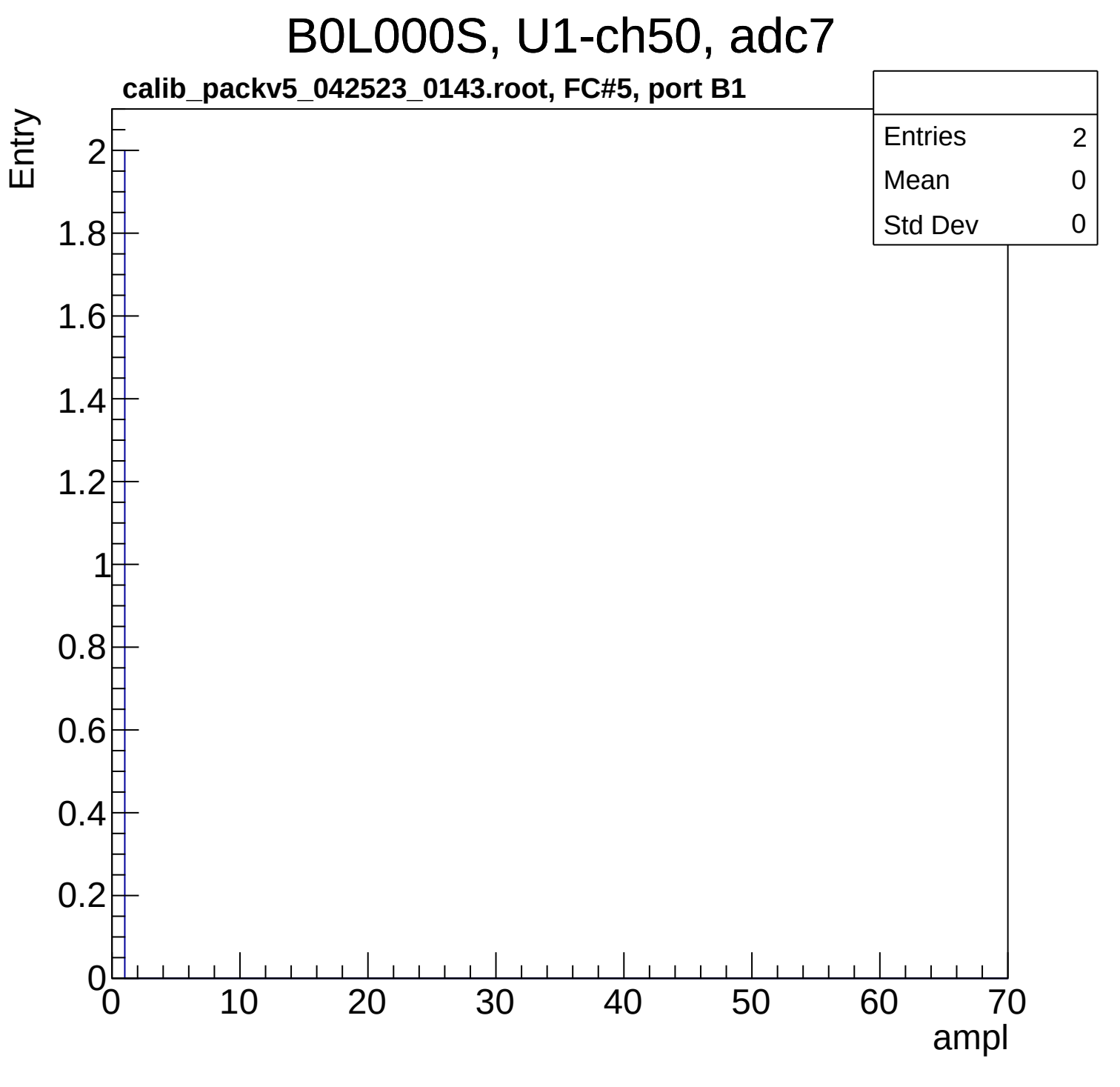

B0L000S, U1-ch50, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 60.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch50, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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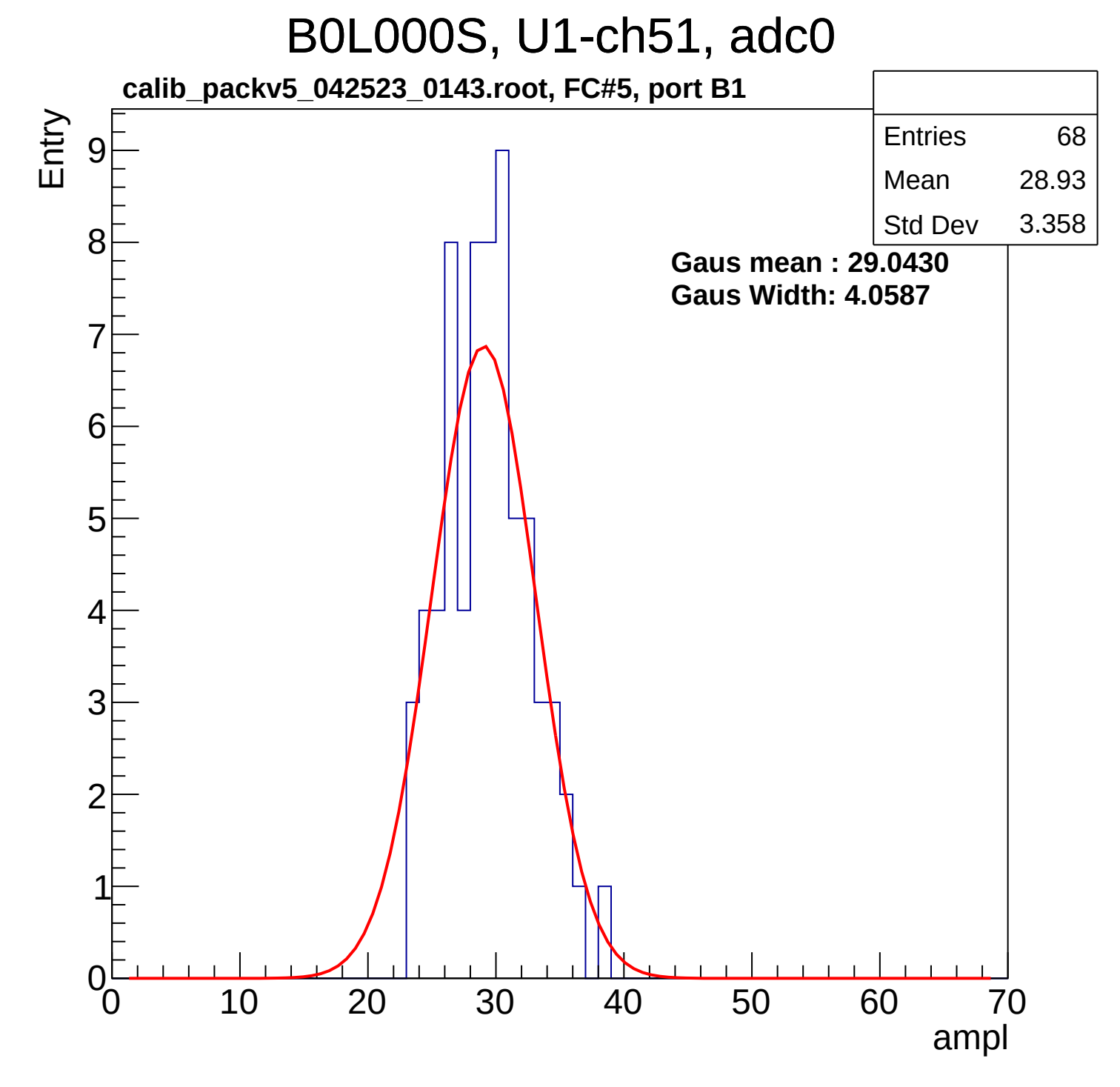
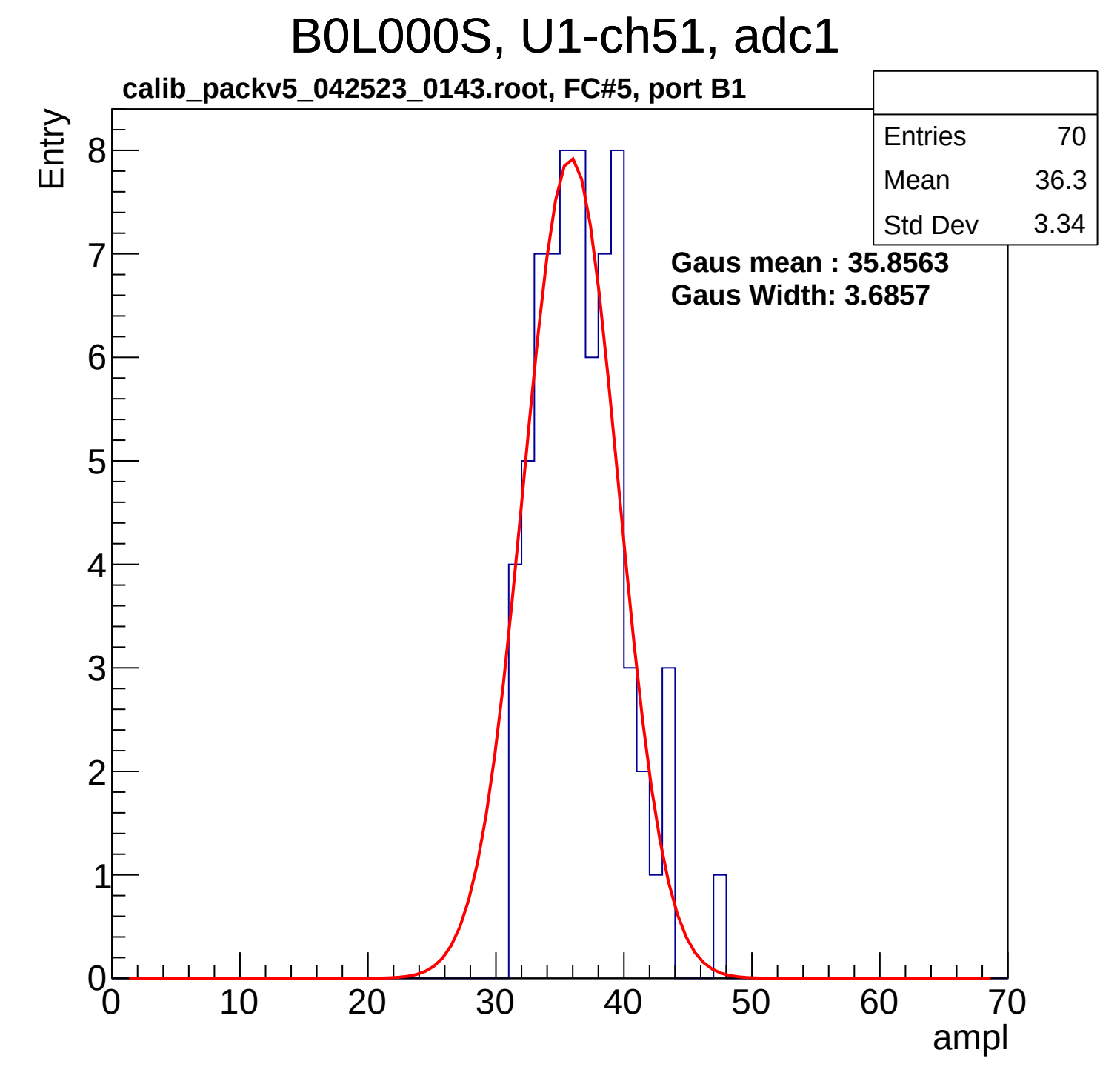
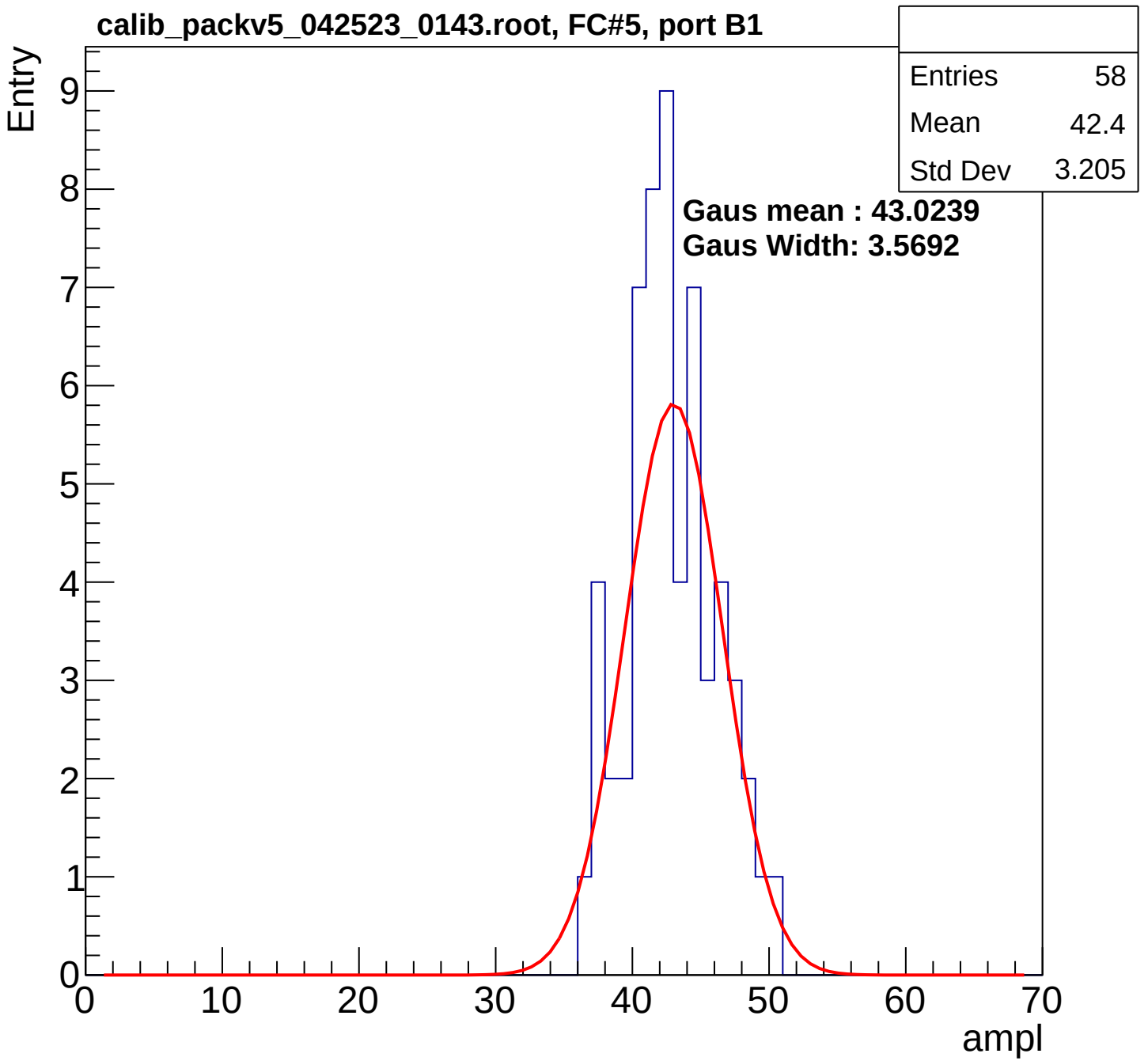
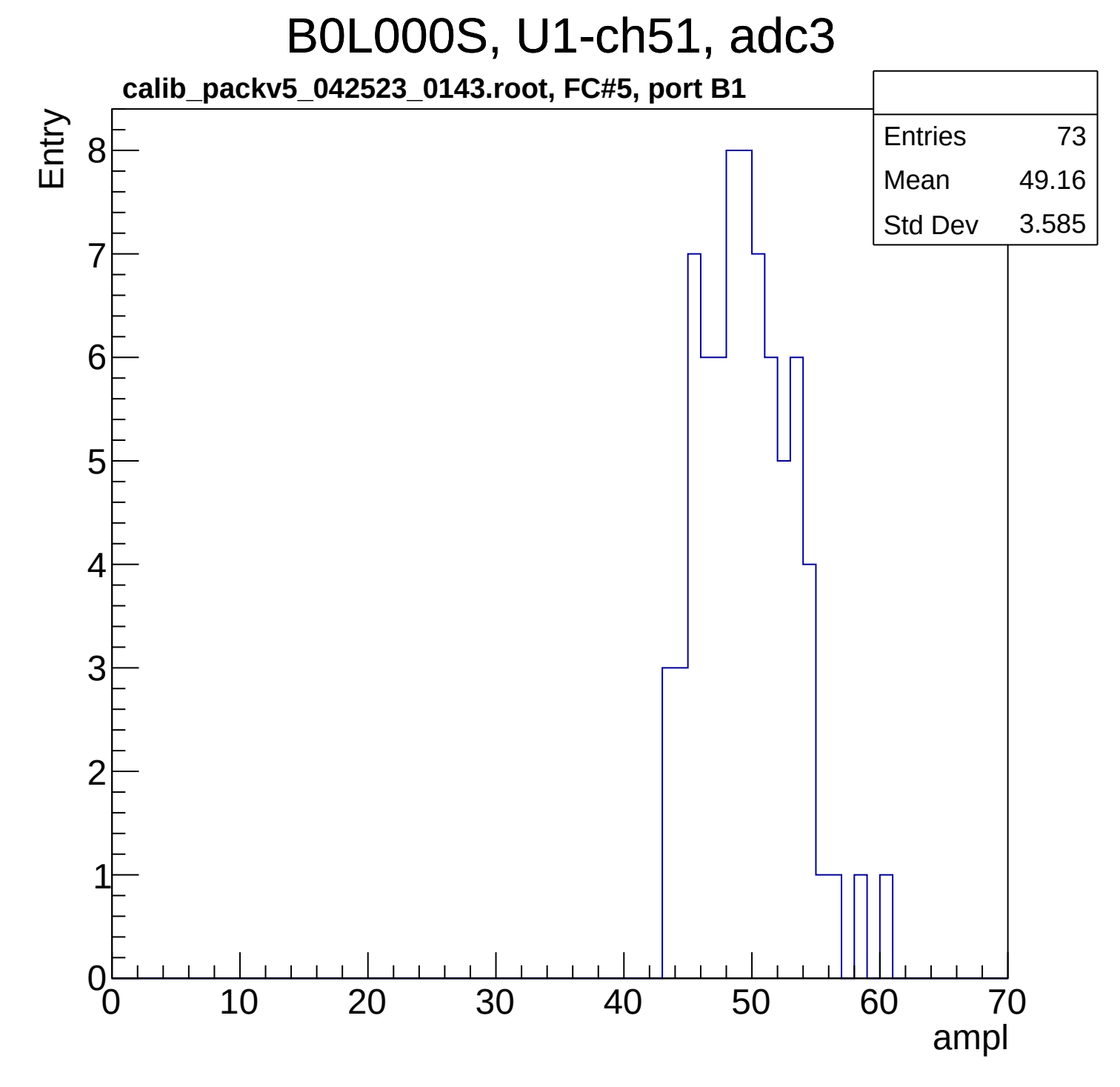
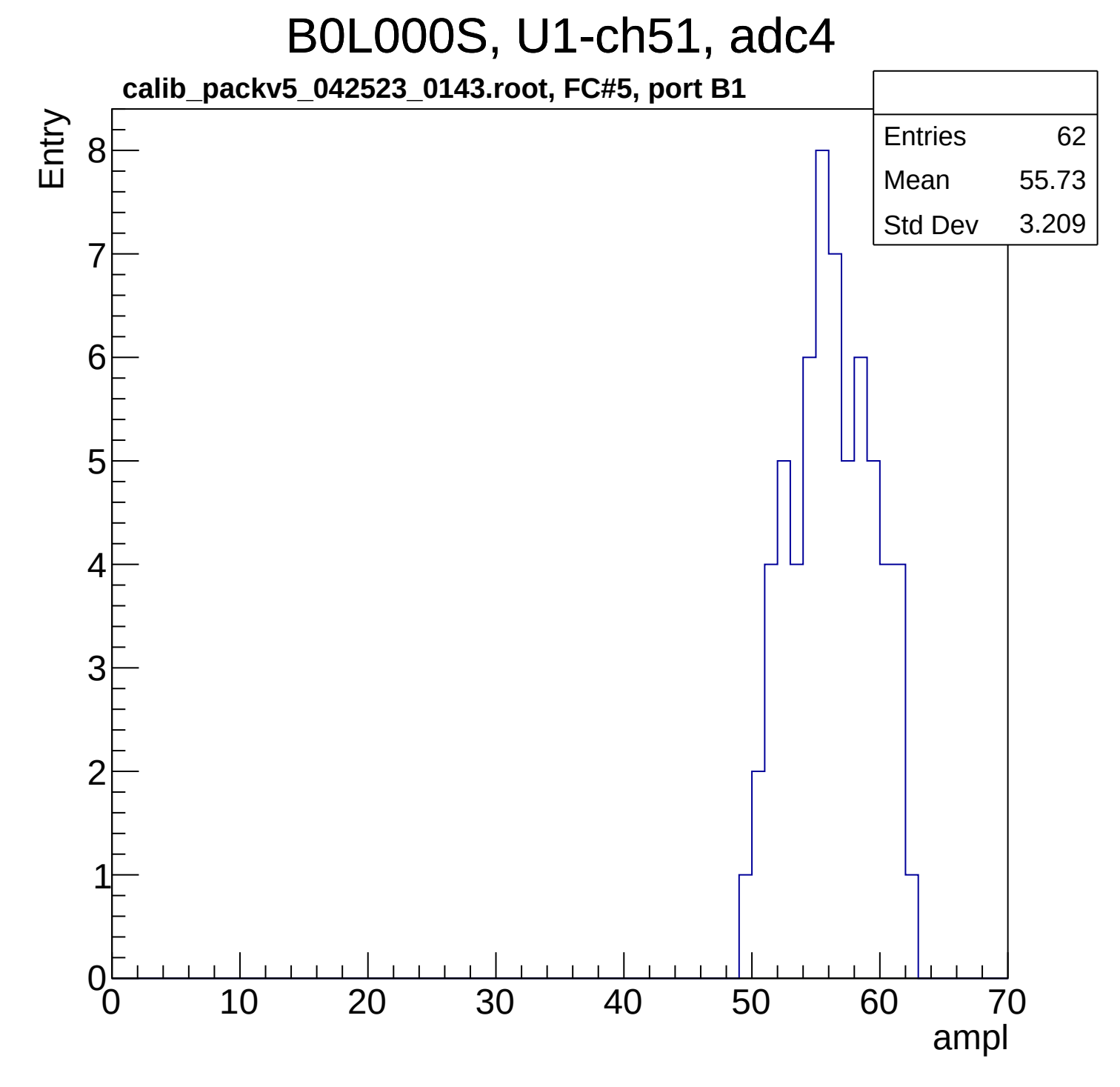
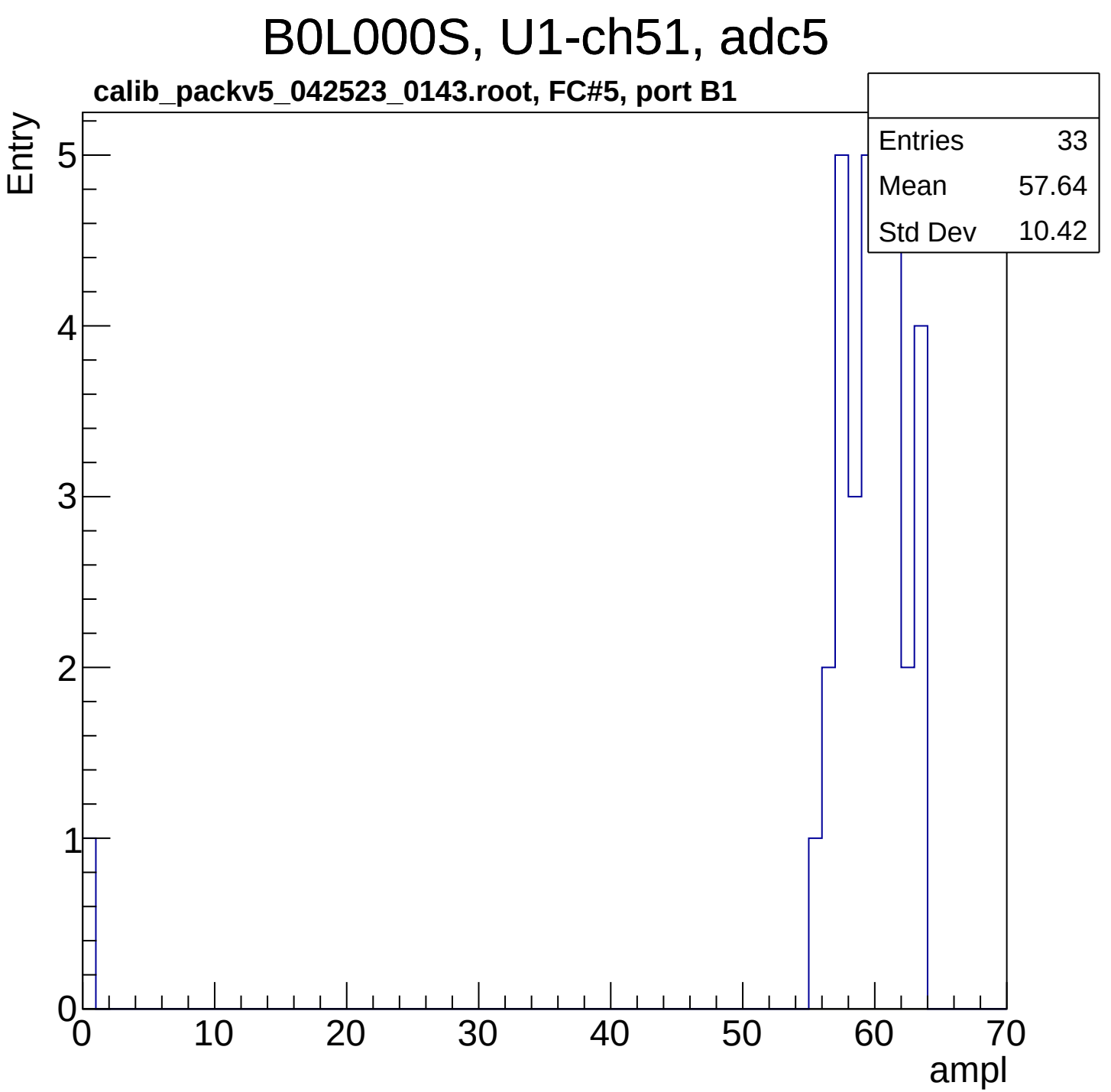

B0L000S, U1-ch51, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.93
Std Dev 3.358
Gaus mean : 29.0430
Gaus Width: 4.0587
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

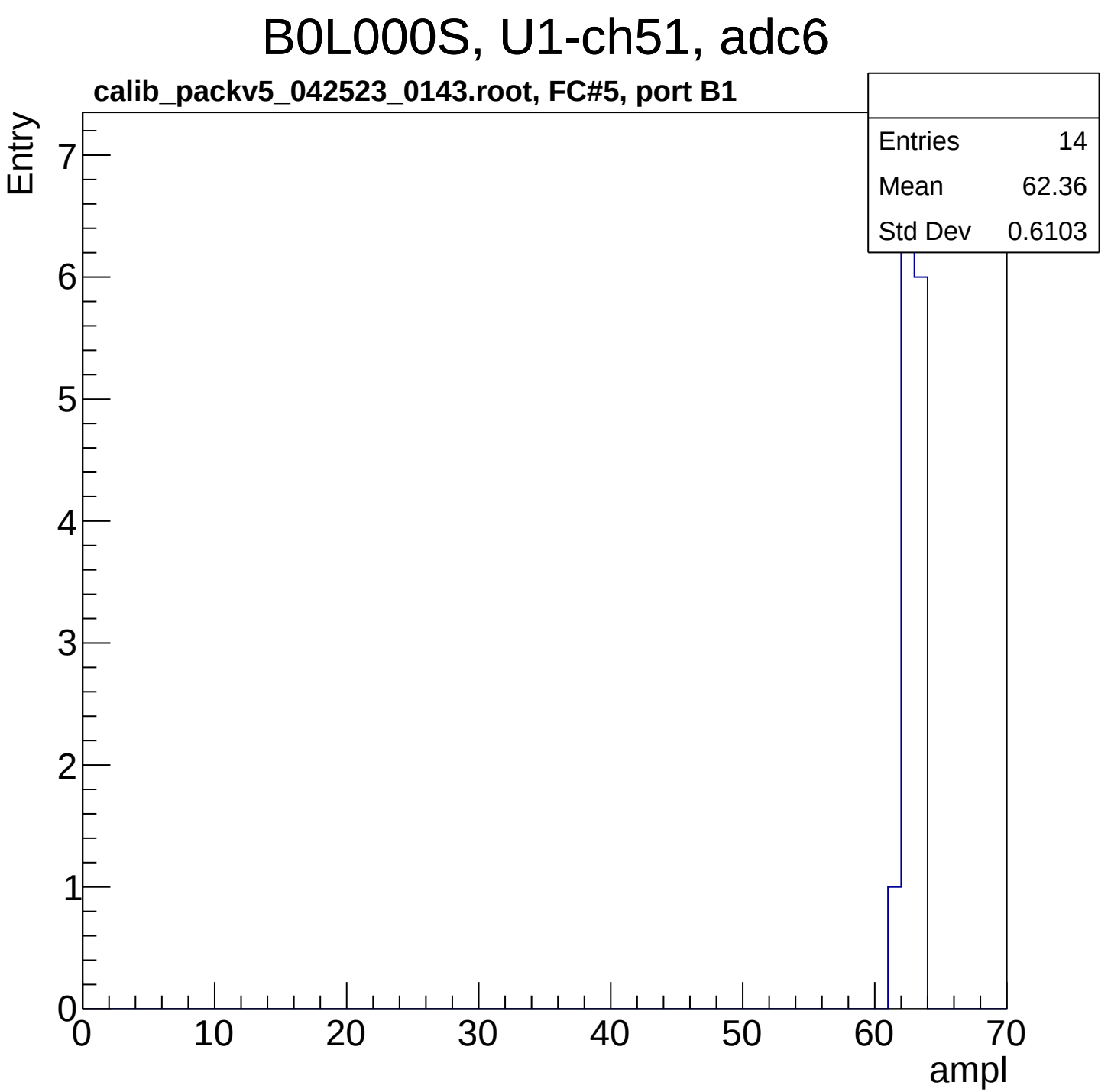

B0L000S, U1-ch51, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.3
Std Dev 3.34
Gaus mean : 35.8563
Gaus Width: 3.6857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

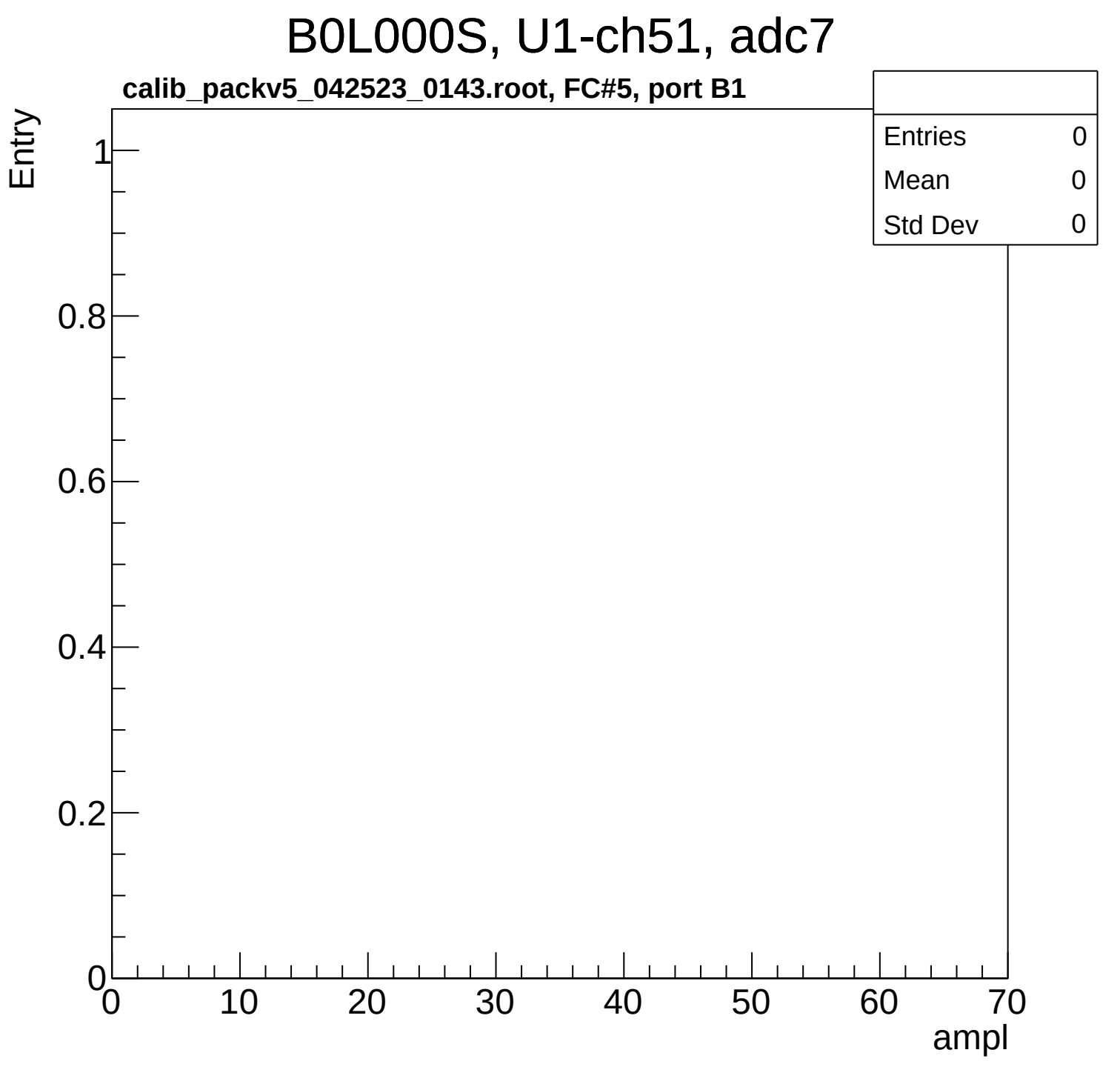

B0L000S, U1-ch51, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 42.4
Std Dev 3.205
Gaus mean : 43.0239
Gaus Width: 3.5692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

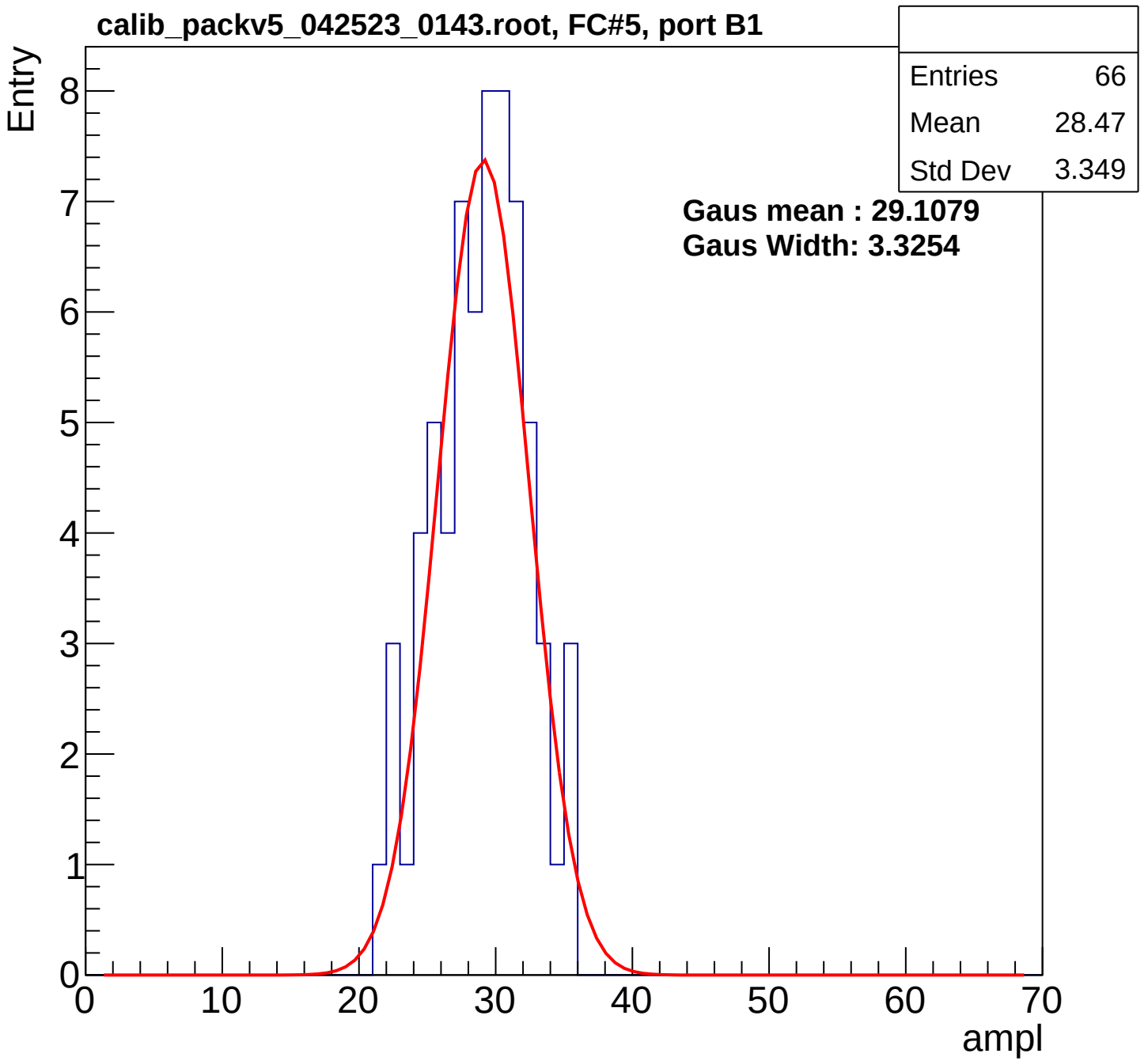

B0L000S, U1-ch51, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.16
Std Dev 3.585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch51, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 55.73
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch51, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 57.64
Std Dev 10.42
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch51, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 62.36
Std Dev 0.6103
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch51, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch52, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.47
Std Dev 3.349
Gaus mean : 29.1079
Gaus Width: 3.3254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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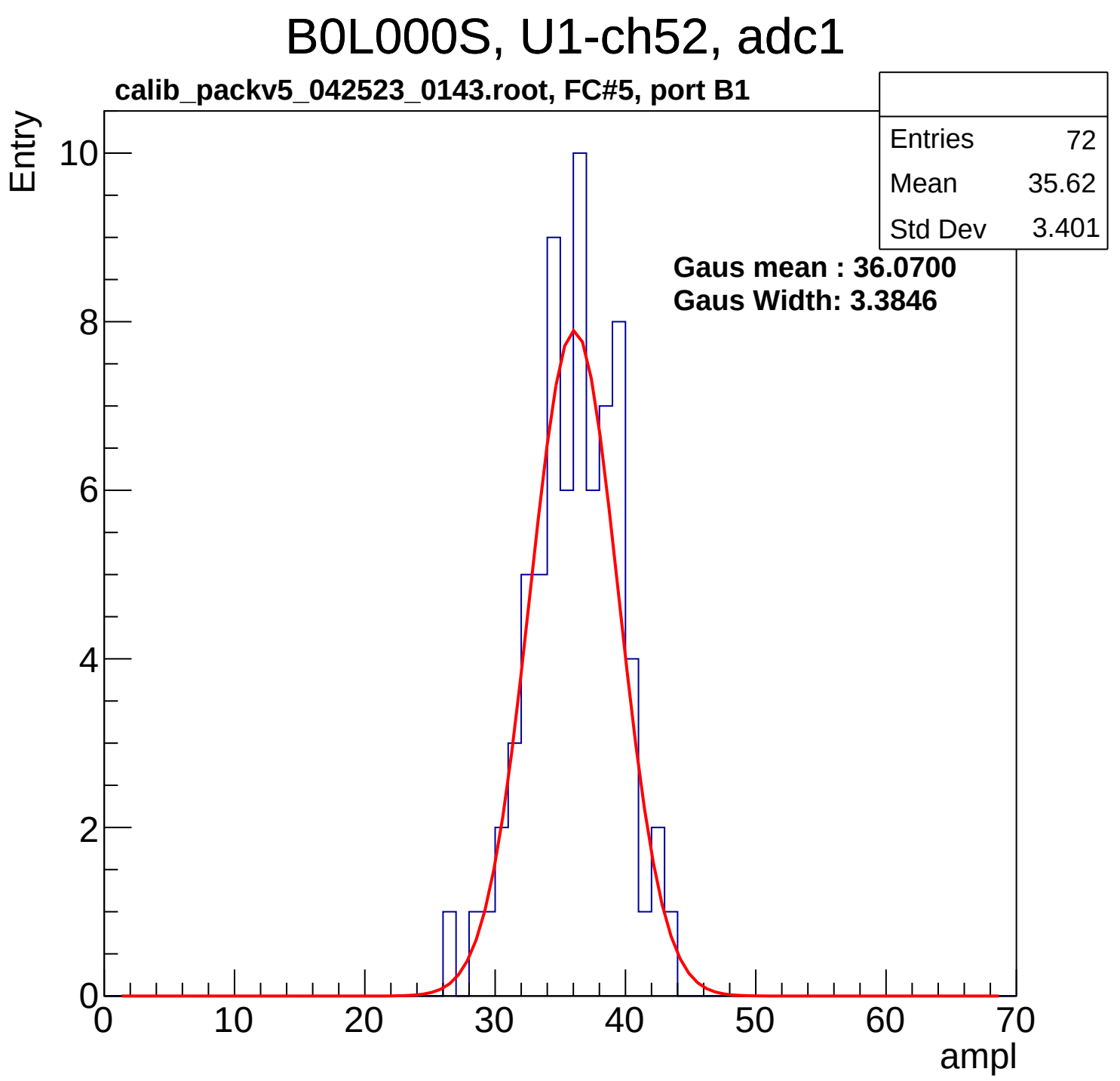
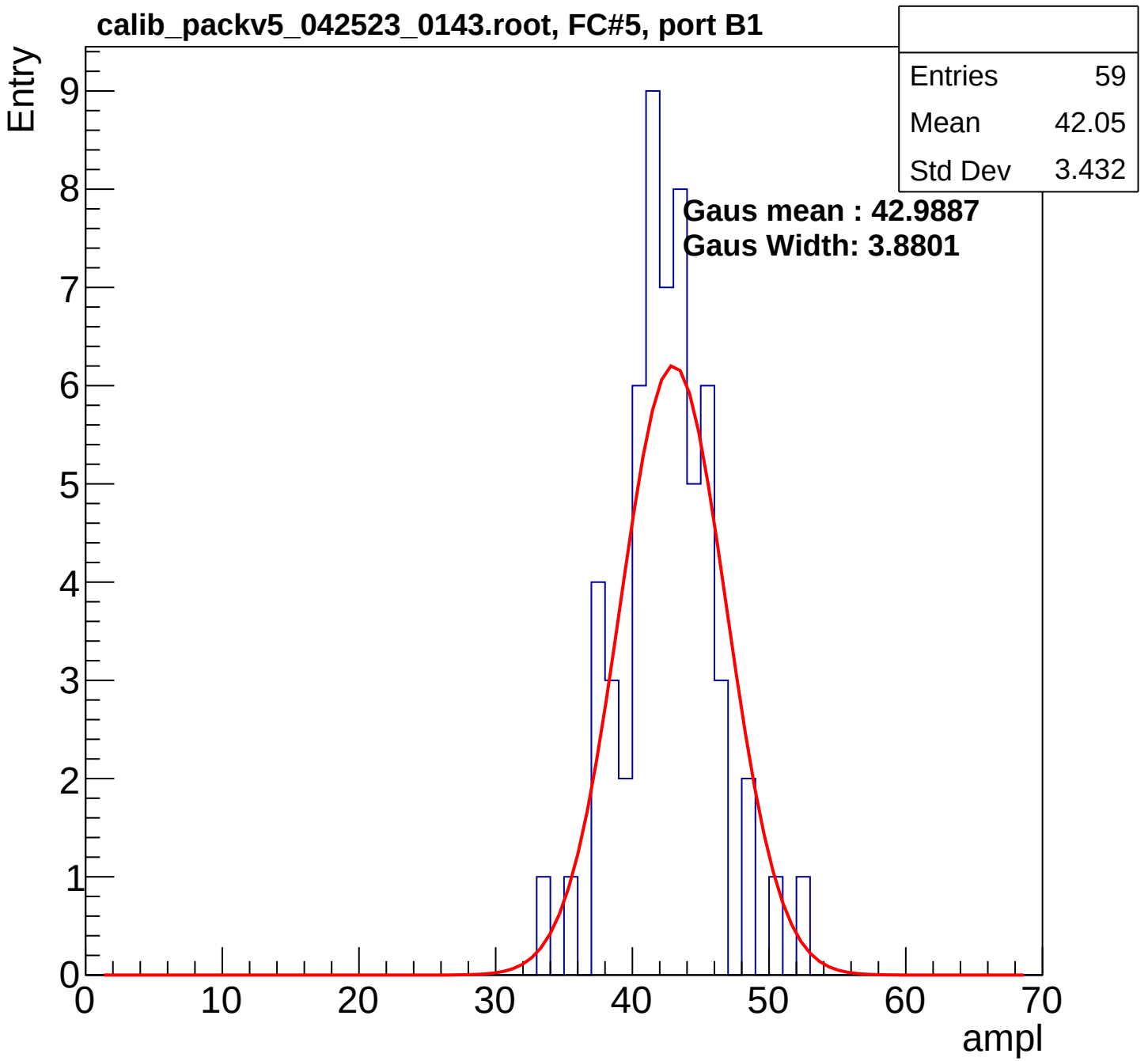
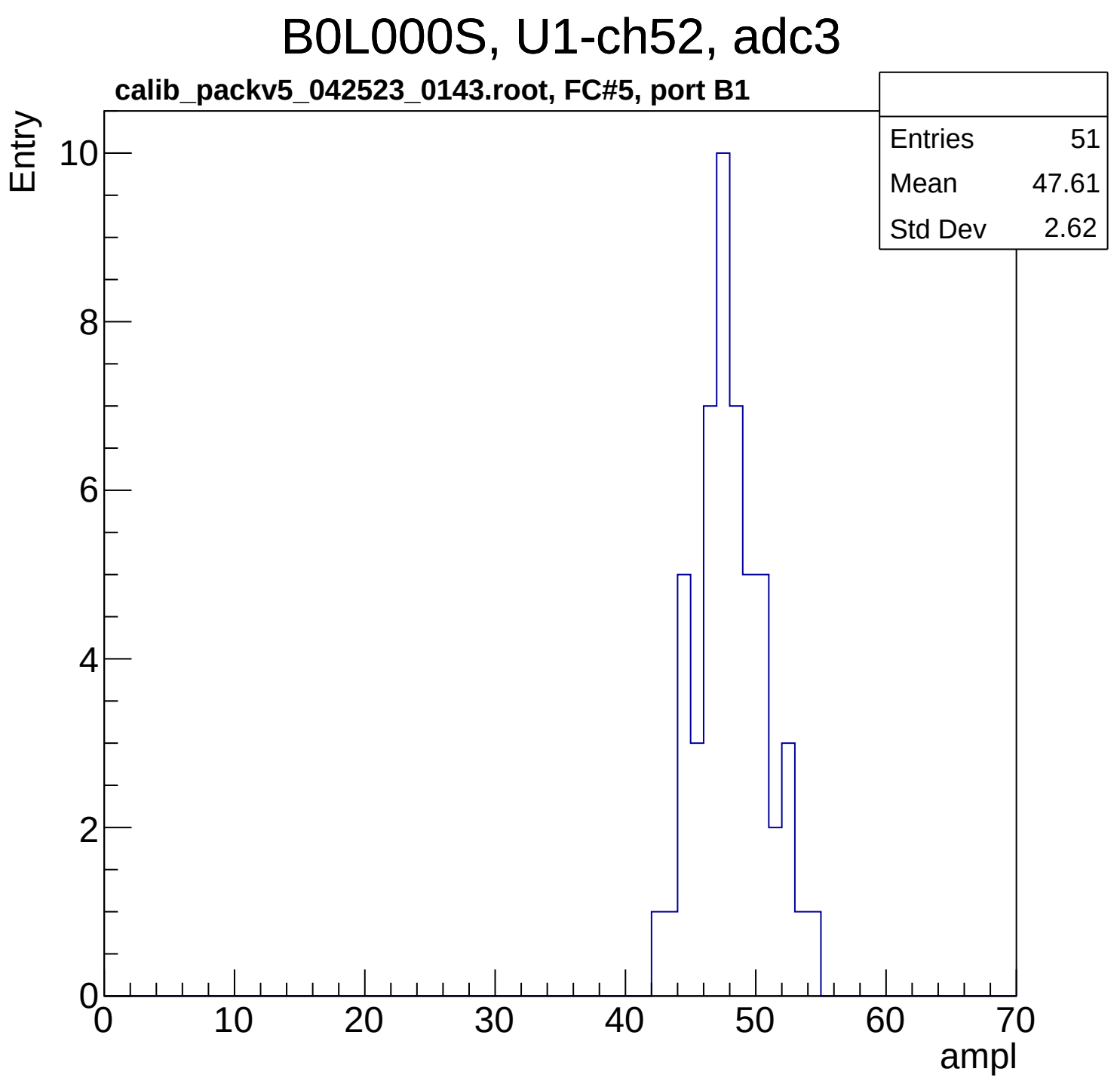
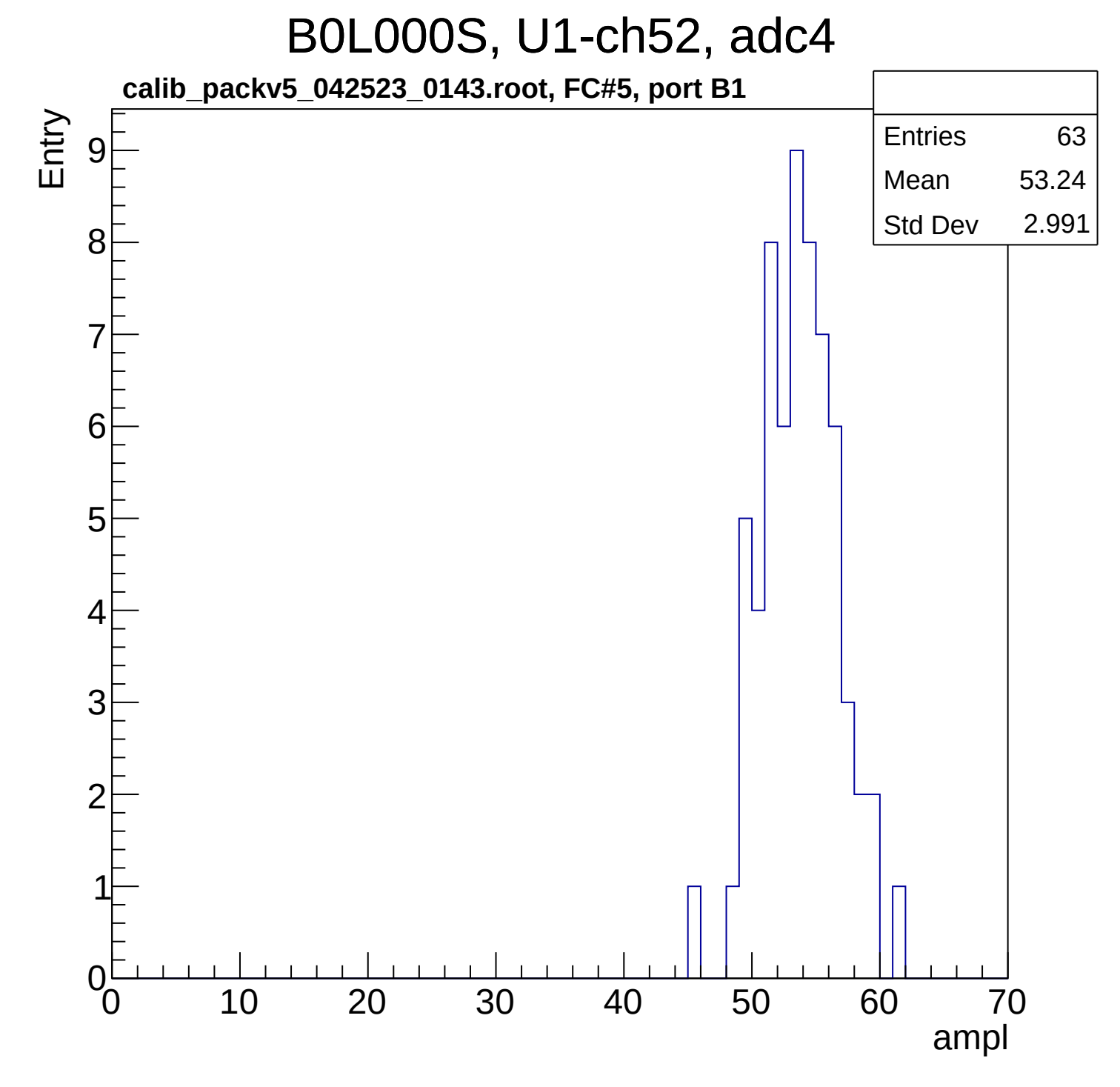
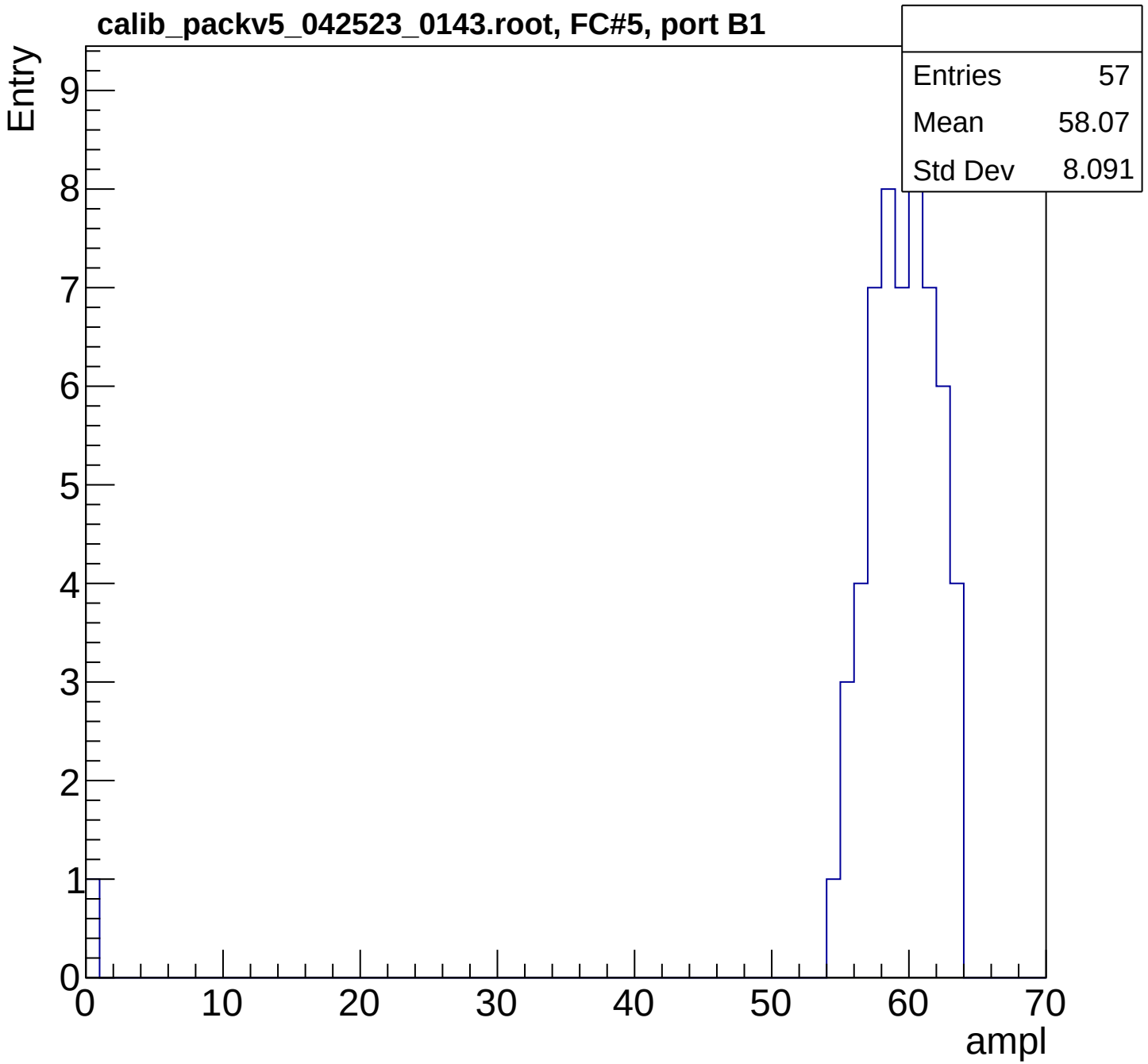

B0L000S, U1-ch52, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.62
Std Dev 3.401
Gaus mean : 36.0700
Gaus Width: 3.3846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch52, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.05
Std Dev 3.432
Gaus mean : 42.9887
Gaus Width: 3.8801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch52, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 47.61
Std Dev 2.62
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch52, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 53.24
Std Dev 2.991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch52, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.07
Std Dev 8.091
Entry
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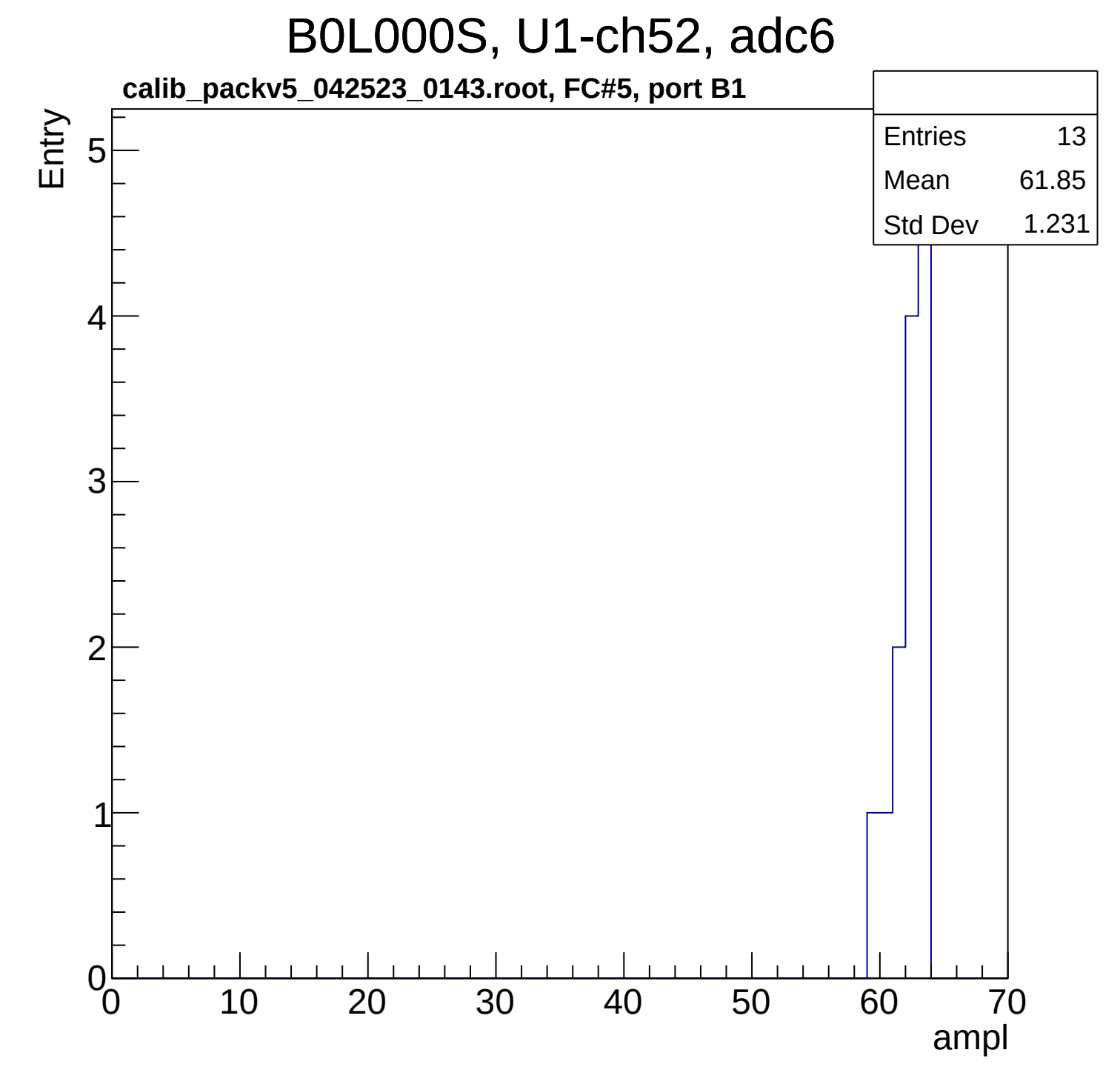
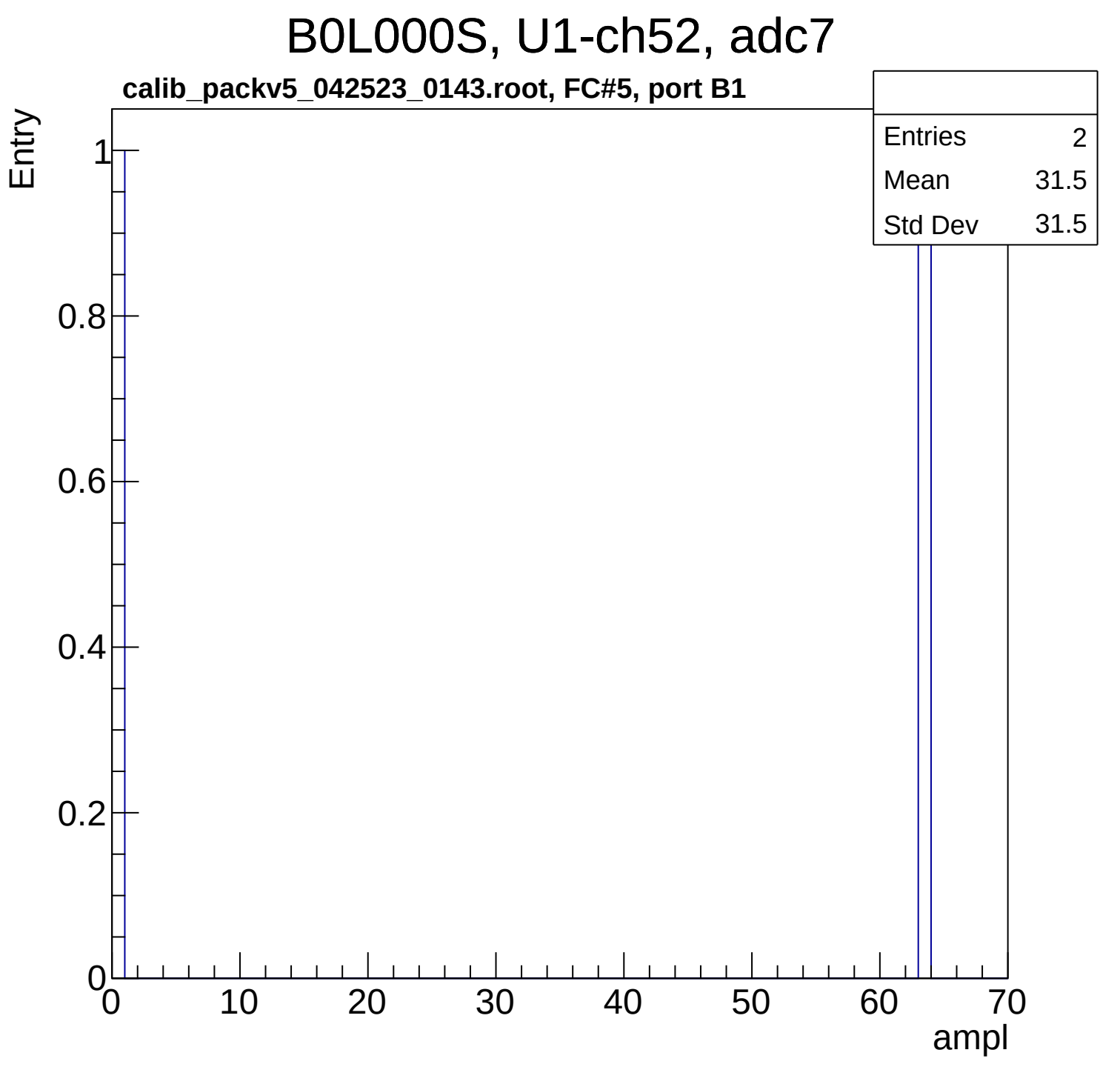
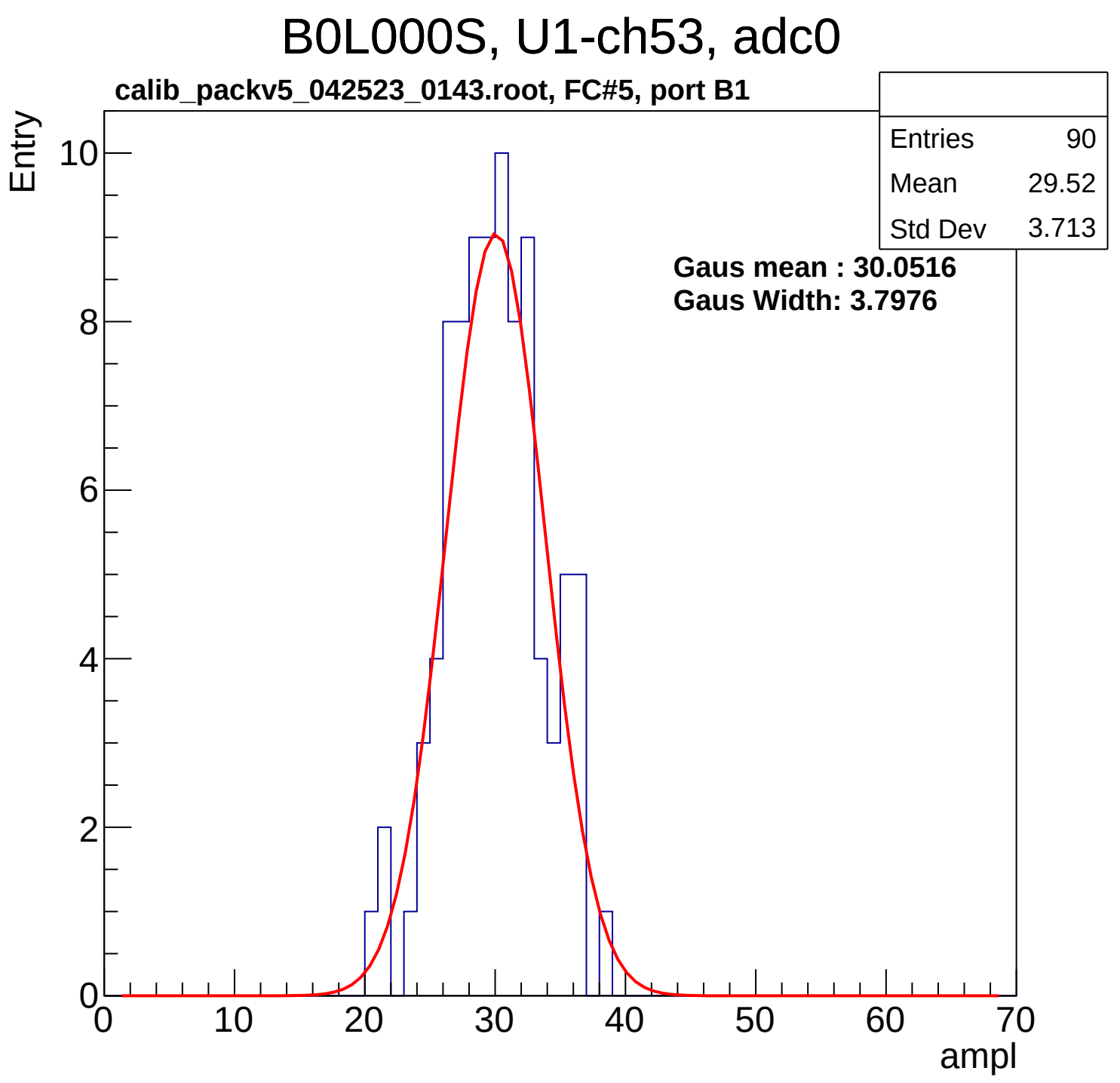
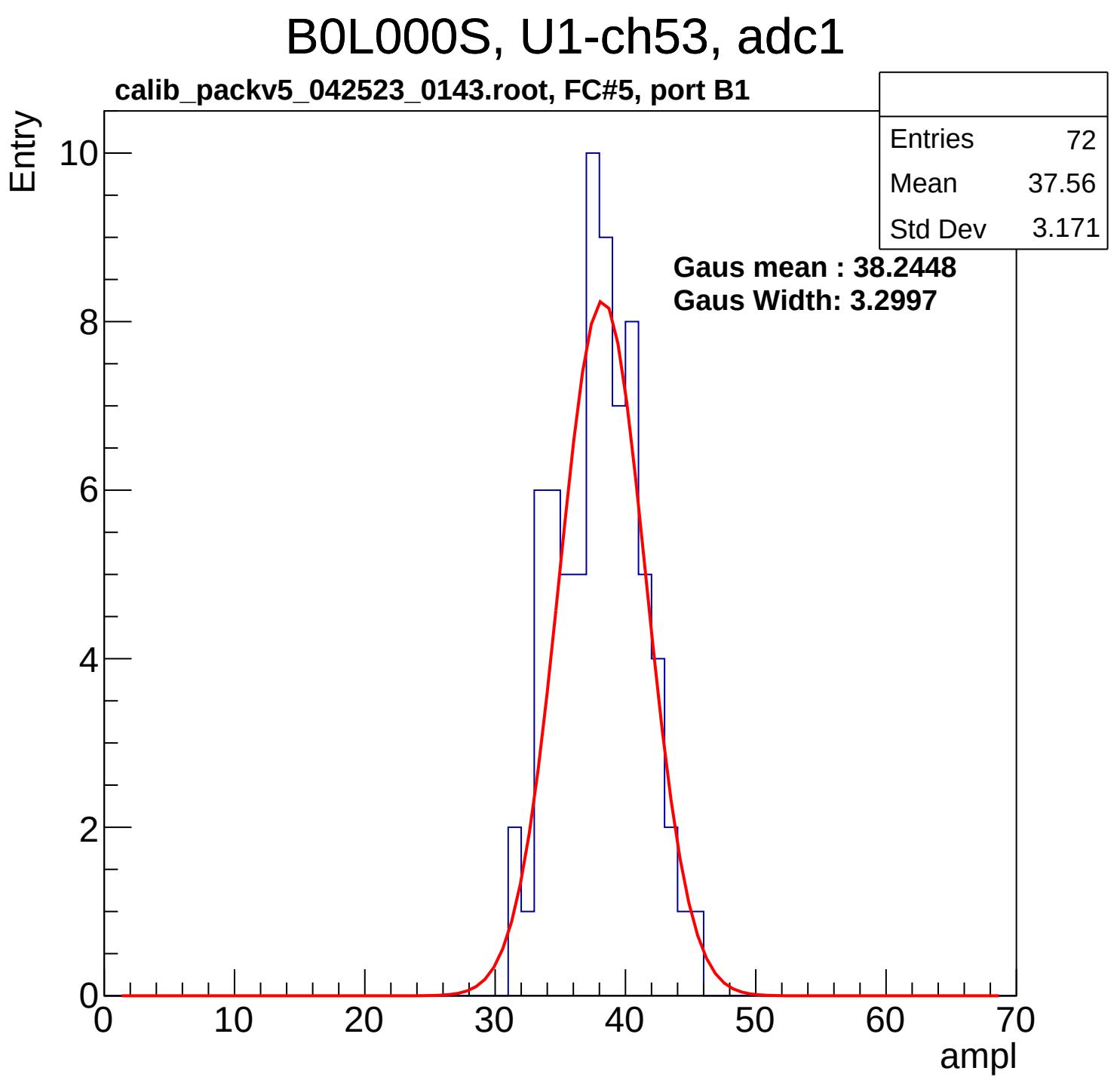
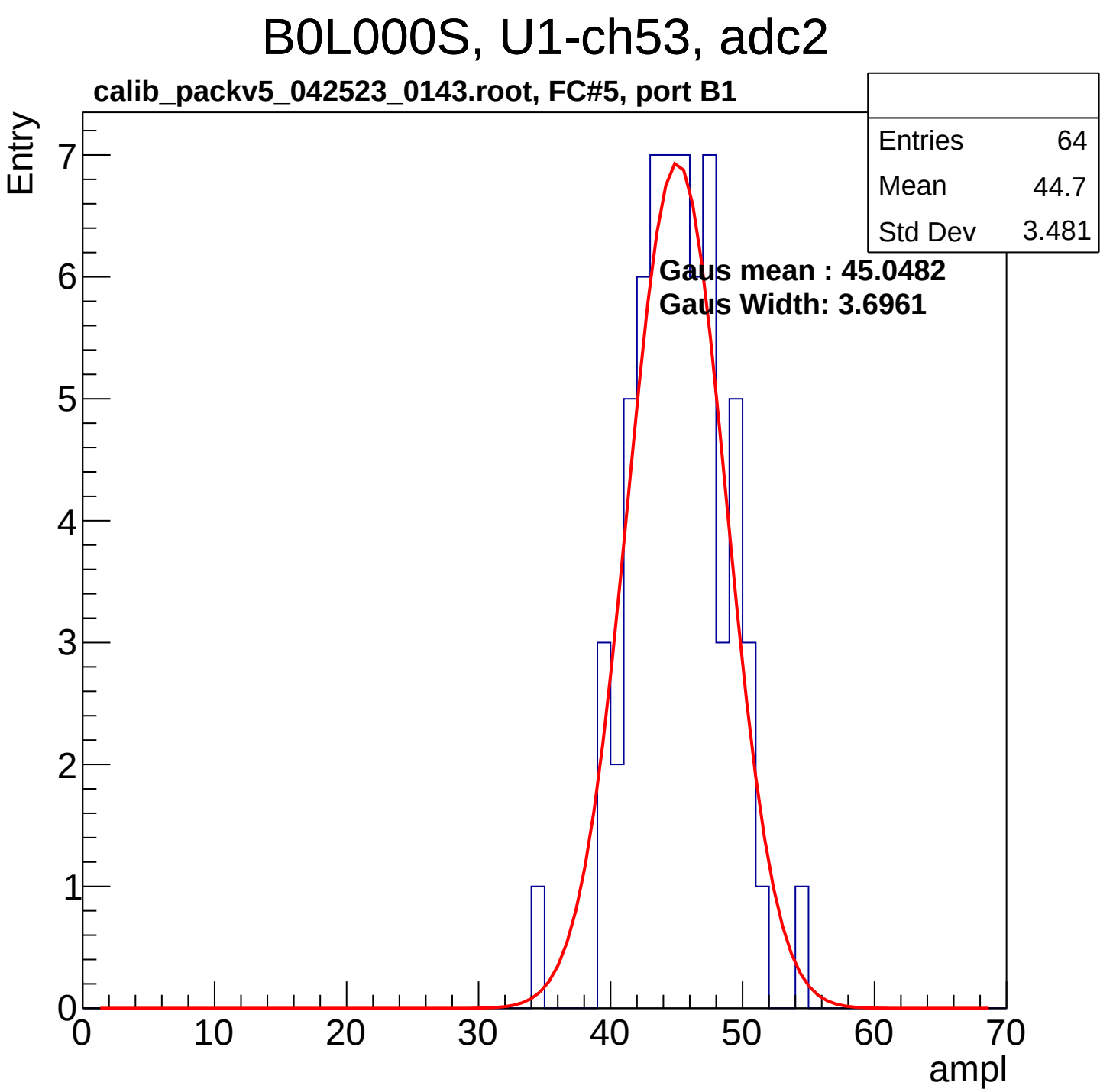
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

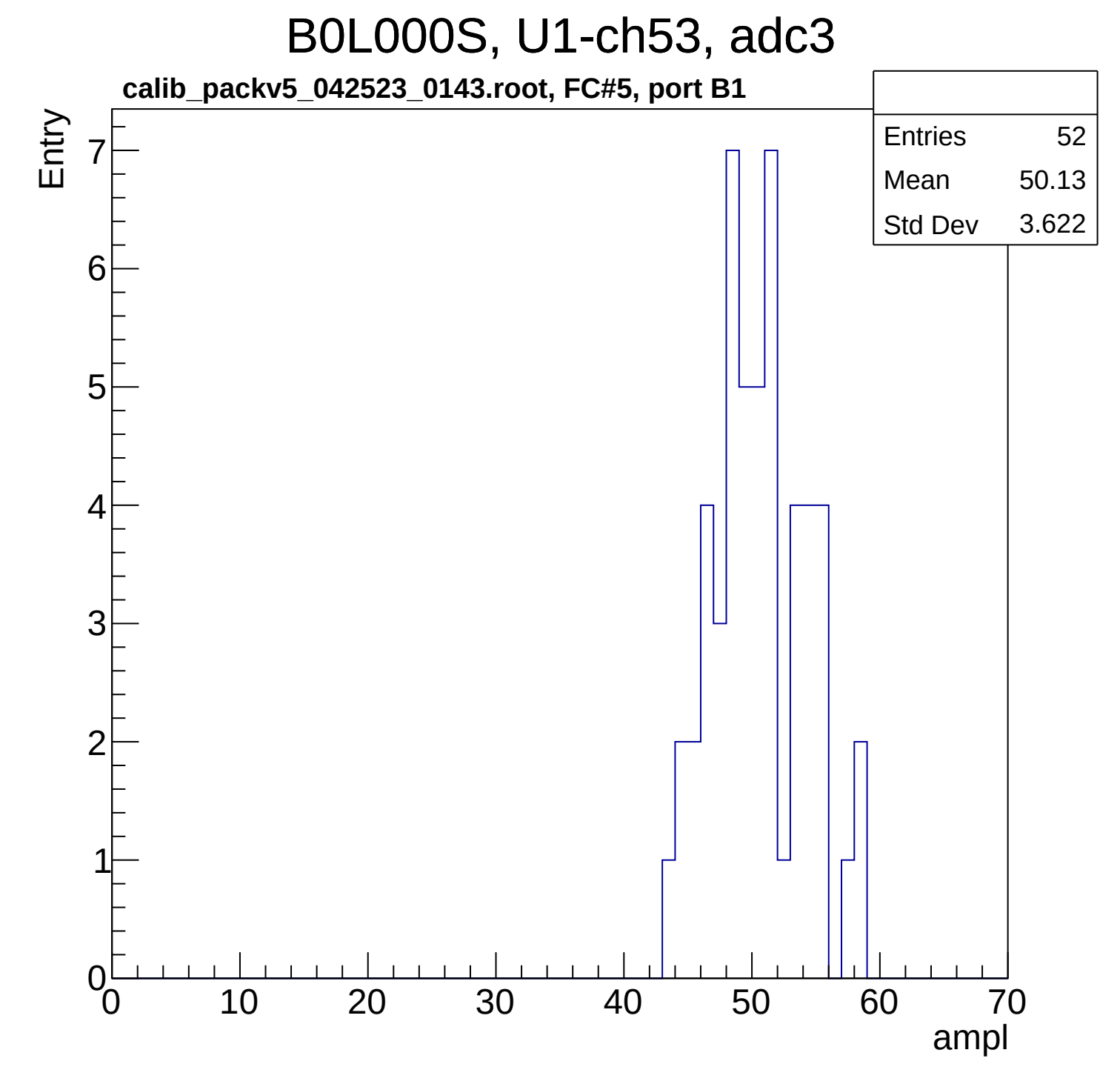

B0L000S, U1-ch52, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.85
Std Dev 1.231
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

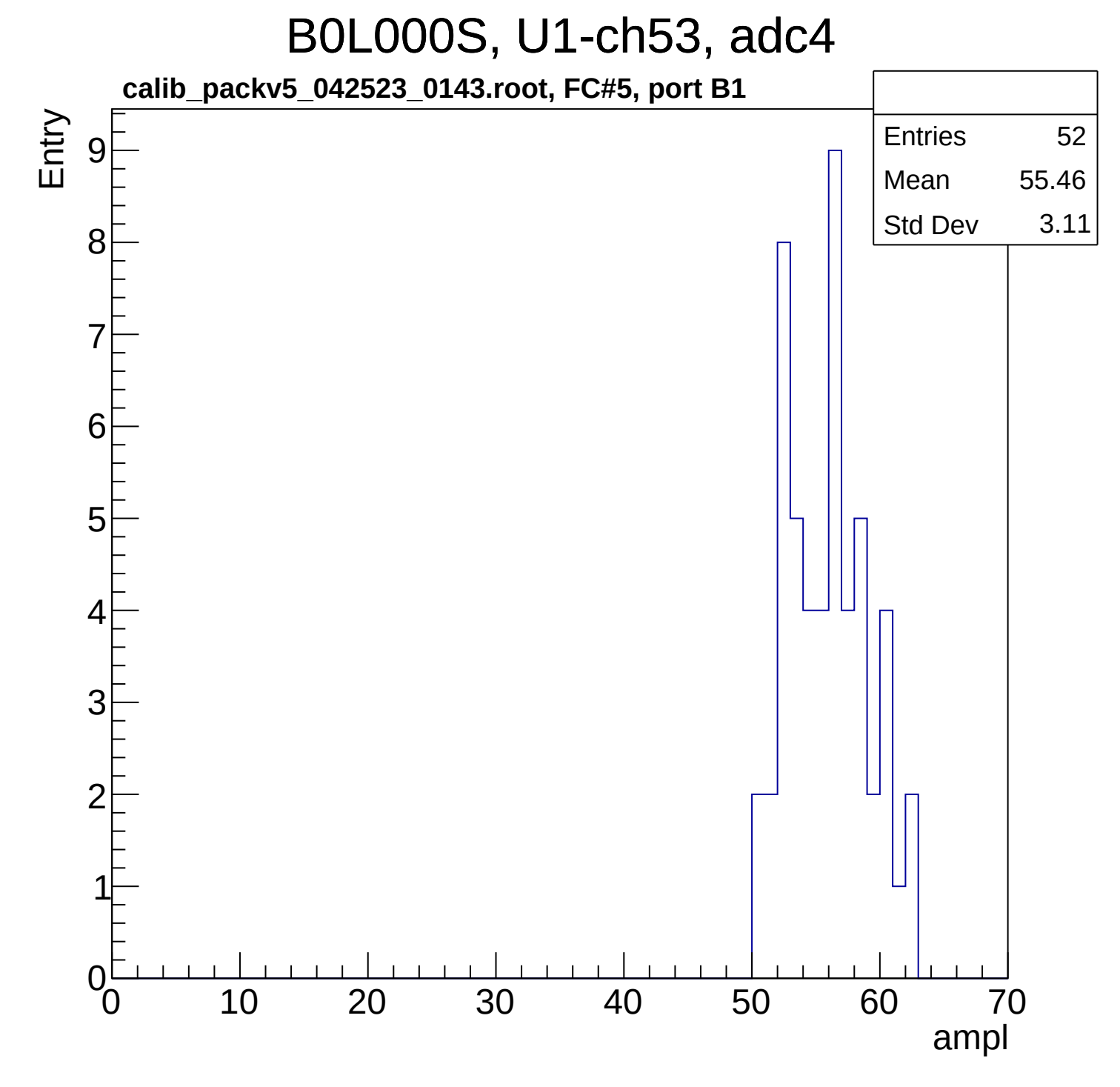

B0L000S, U1-ch52, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch53, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 90
Mean 29.52
Std Dev 3.713
Gaus mean : 30.0516
Gaus Width: 3.7976
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch53, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.56
Std Dev 3.171
Gaus mean : 38.2448
Gaus Width: 3.2997
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch53, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.7
Std Dev 3.481
Gaus mean : 45.0482
Gaus Width: 3.6961
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch53, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 50.13
Std Dev 3.622
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch53, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 55.46
Std Dev 3.11
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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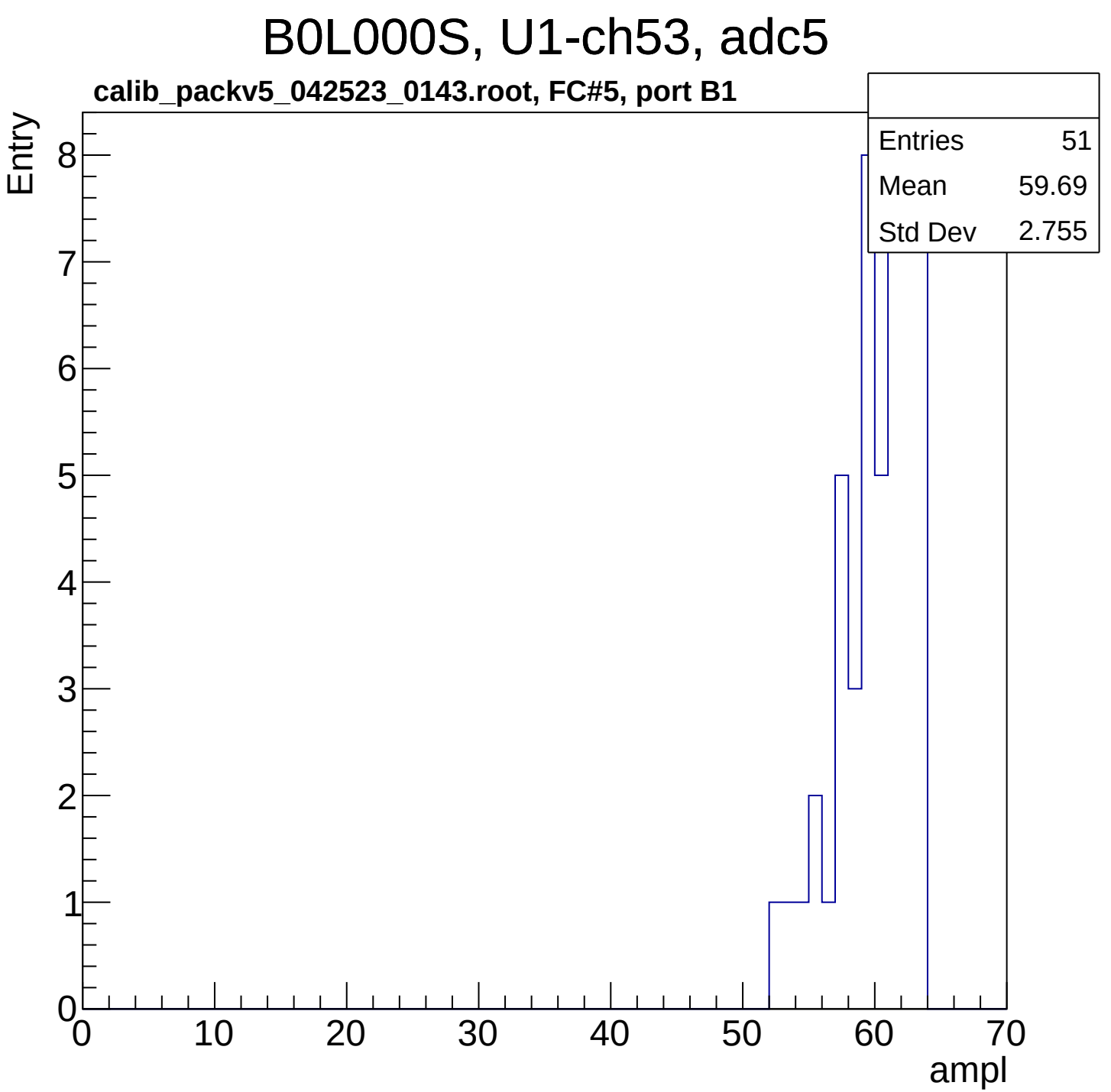
B0L000S, U1-ch53, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.69
Std Dev 2.755
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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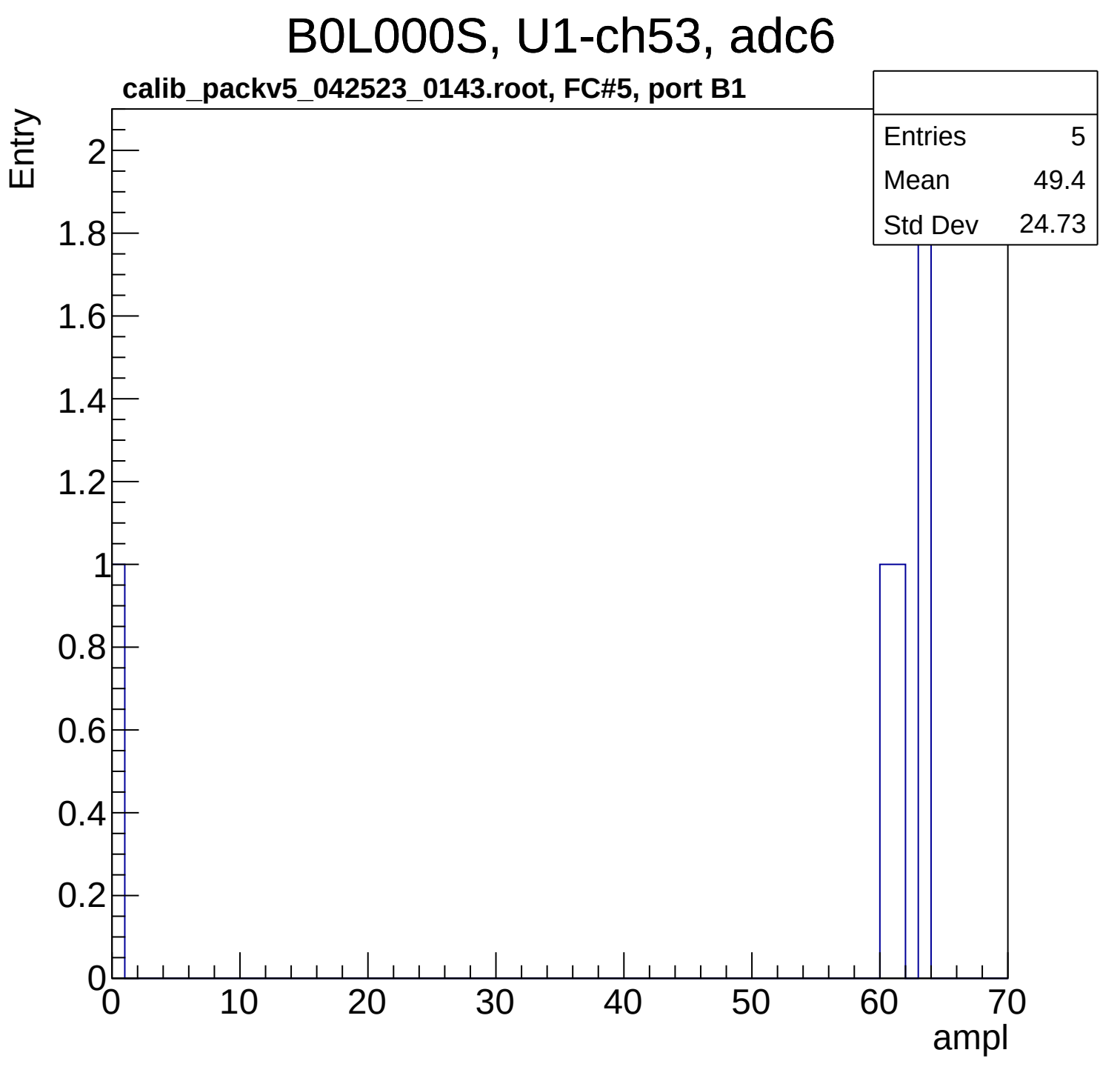
B0L000S, U1-ch53, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.4
Std Dev 24.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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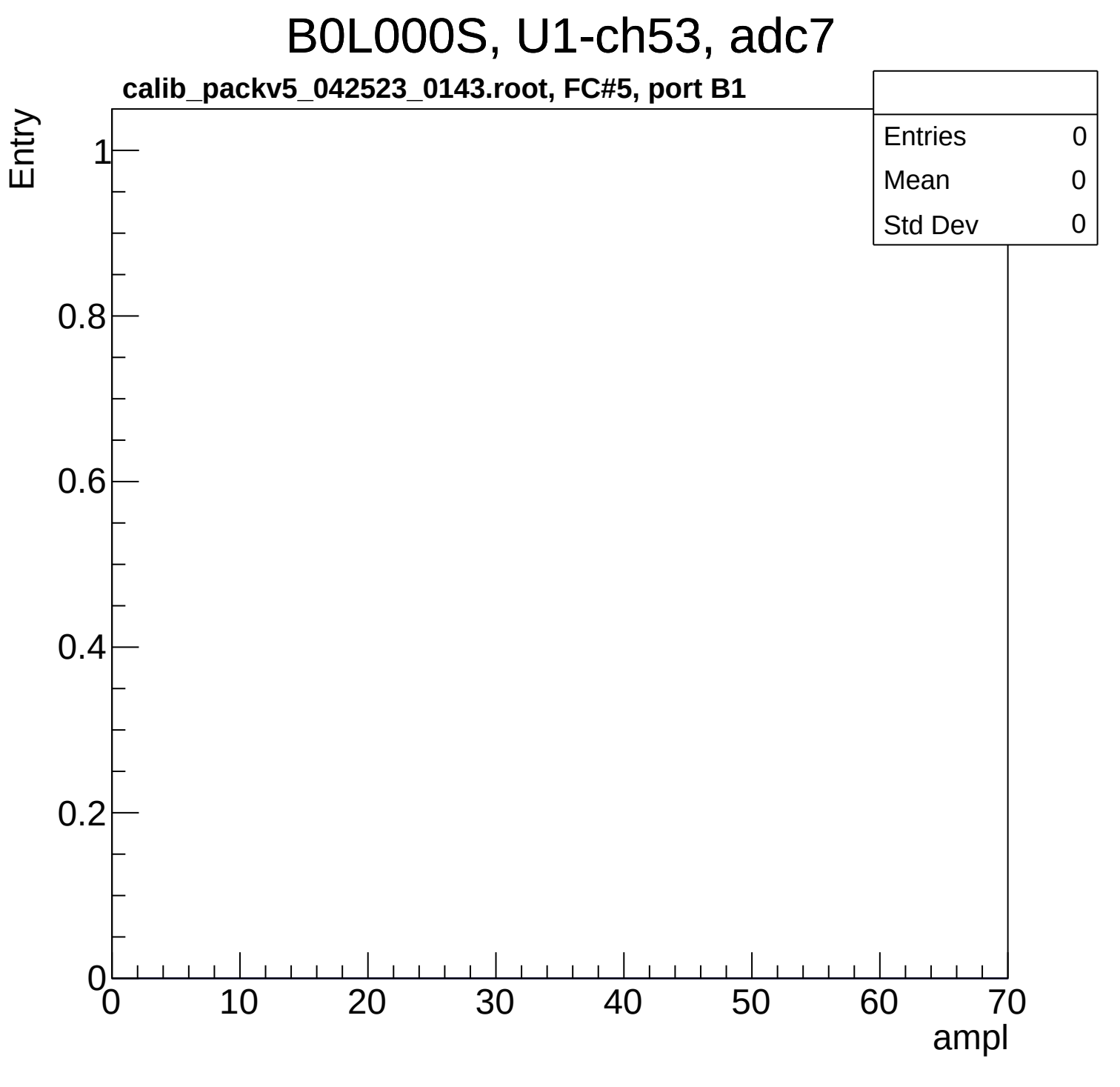
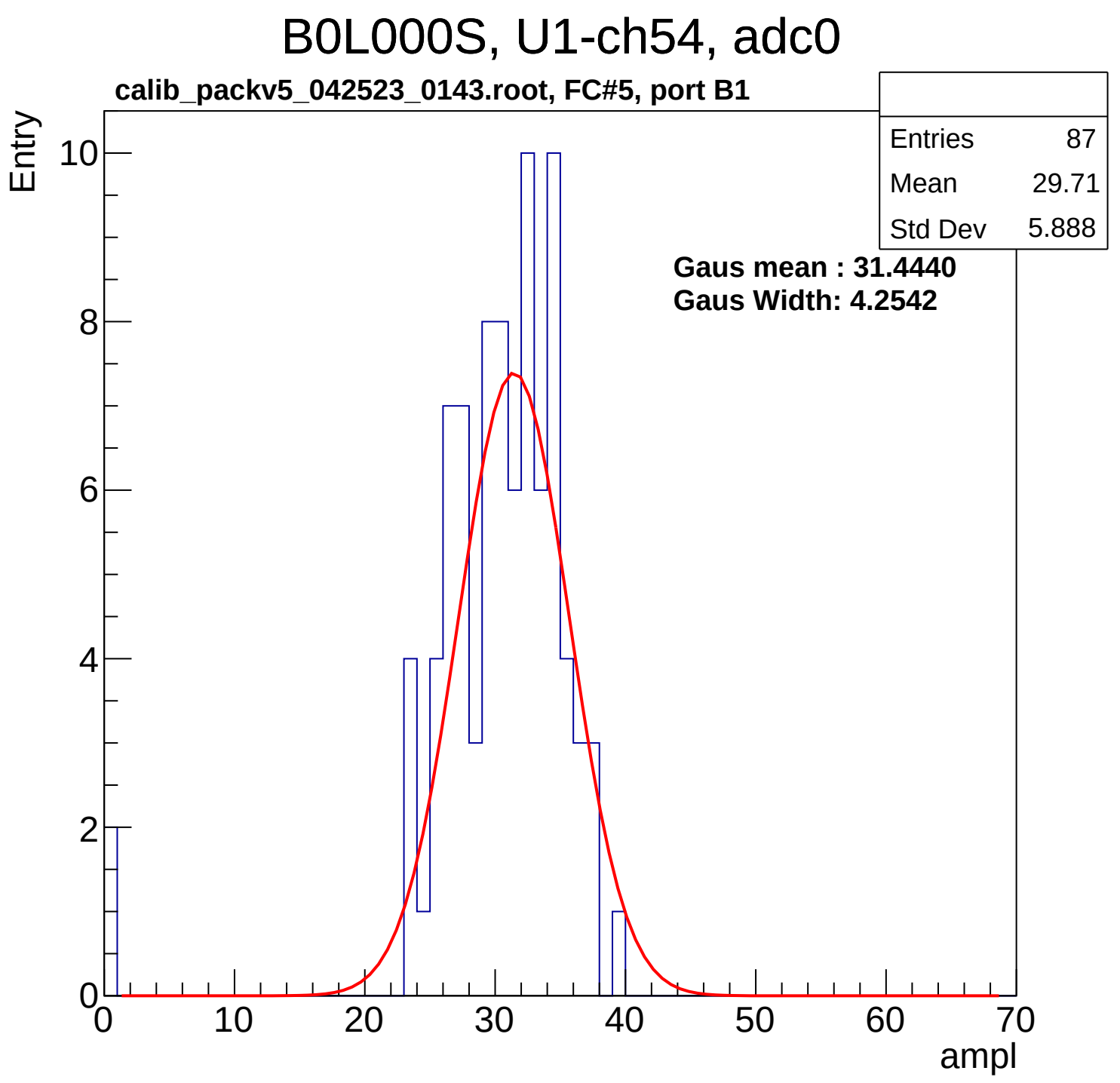
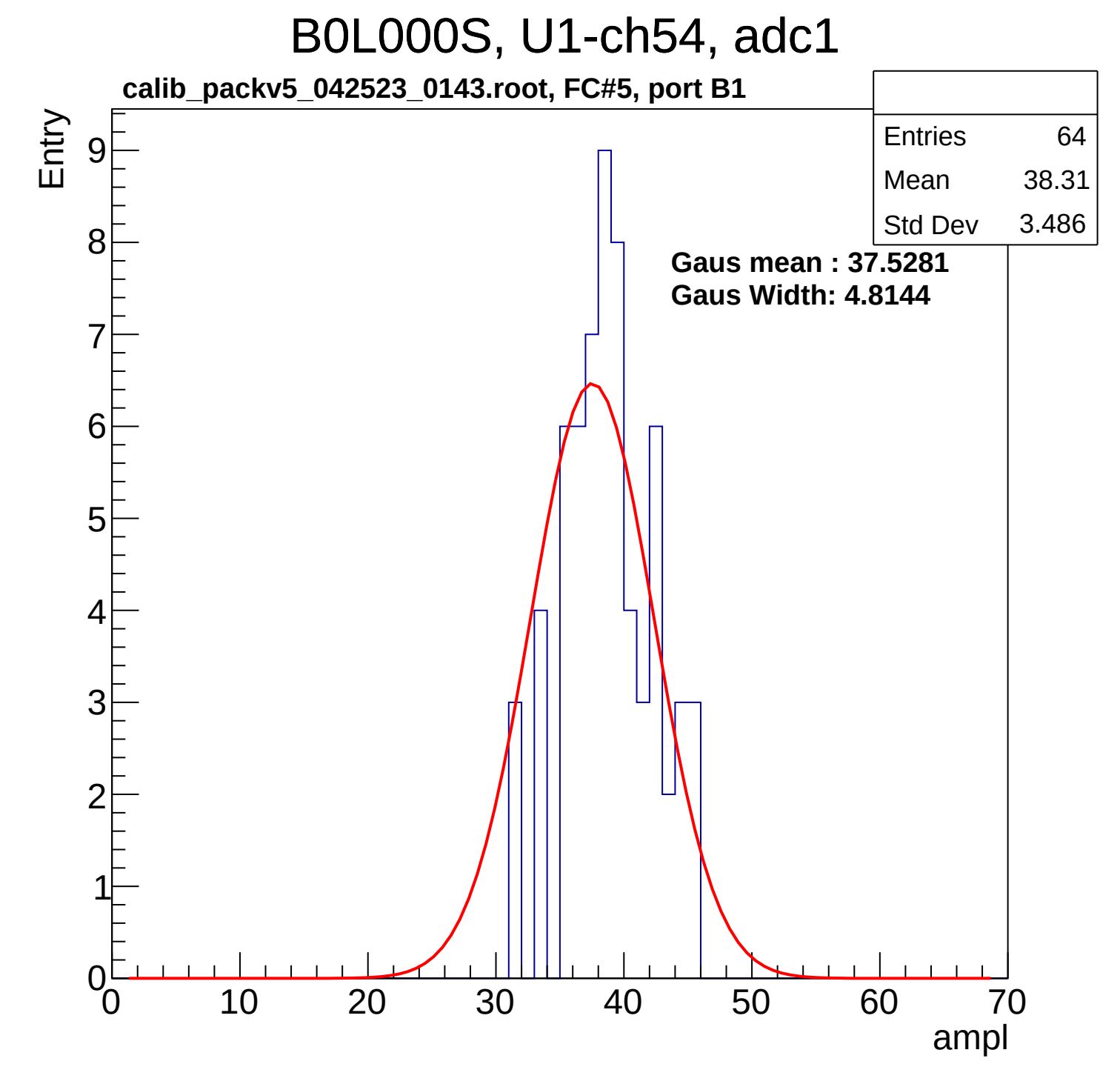

B0L000S, U1-ch53, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch54, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 29.71
Std Dev 5.888
Gaus mean : 31.4440
Gaus Width: 4.2542
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch54, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 38.31
Std Dev 3.486
Gaus mean : 37.5281
Gaus Width: 4.8144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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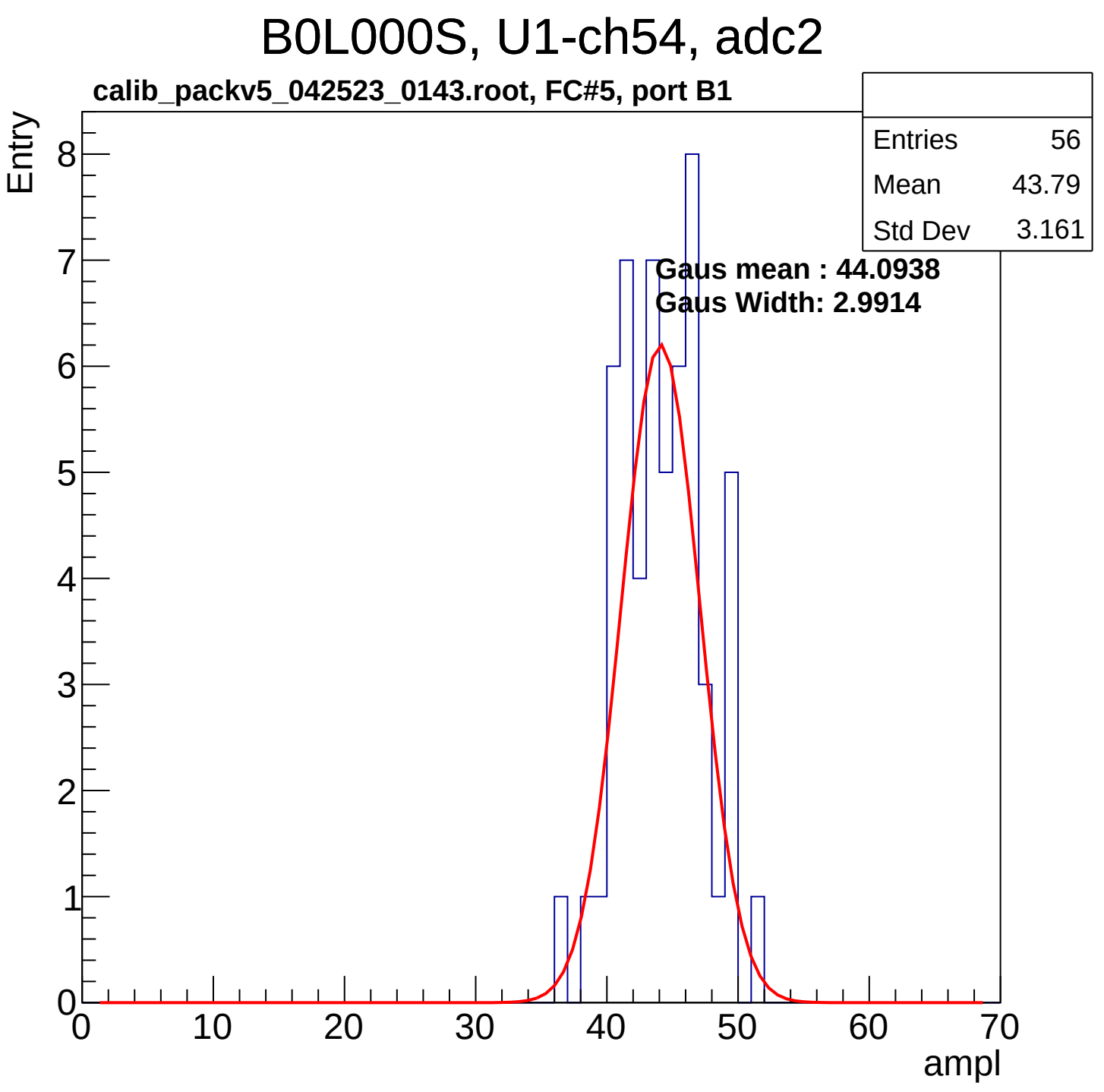
B0L000S, U1-ch54, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.79
Std Dev 3.161
Gaus mean : 44.0938
Gaus Width: 2.9914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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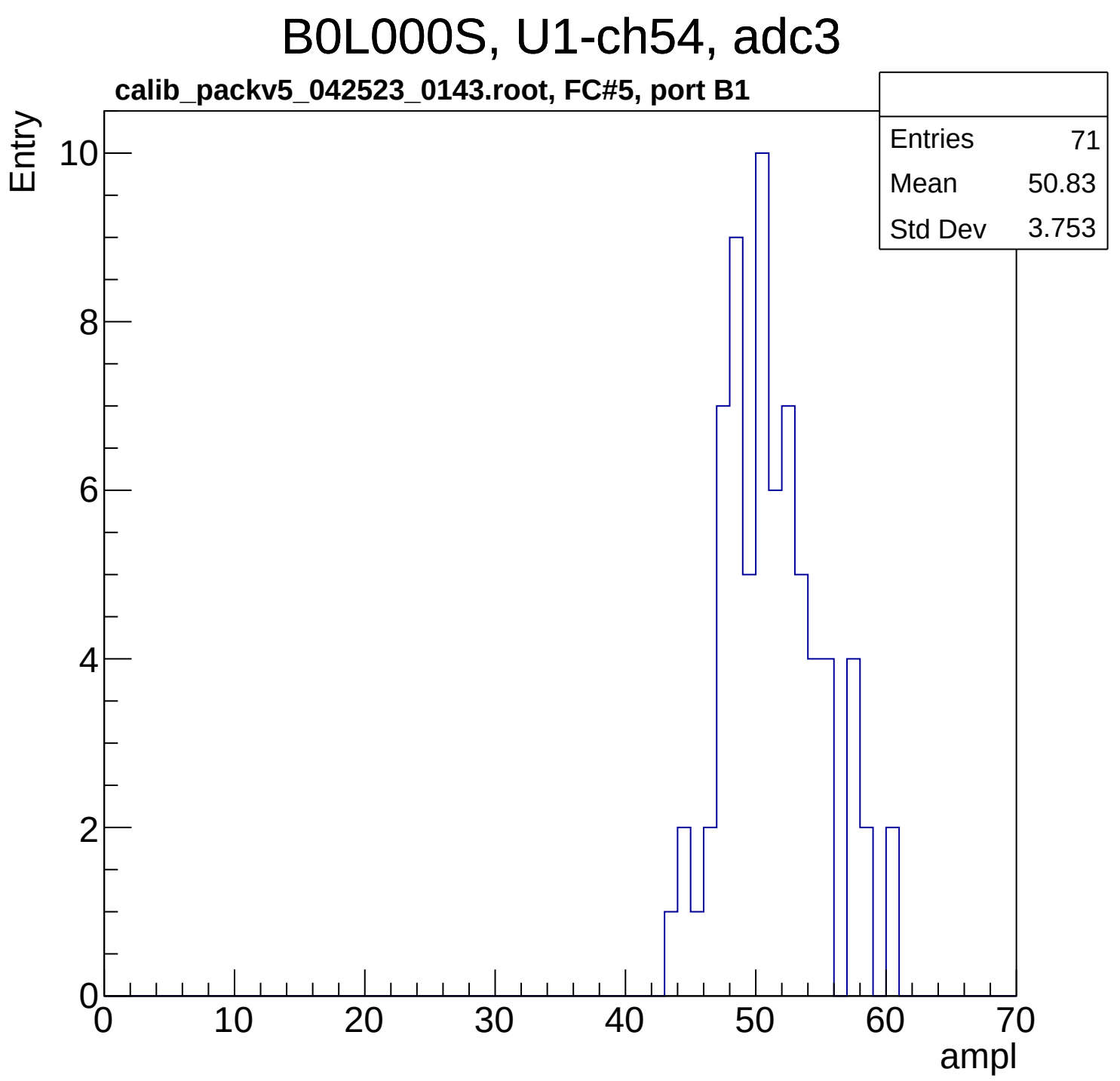
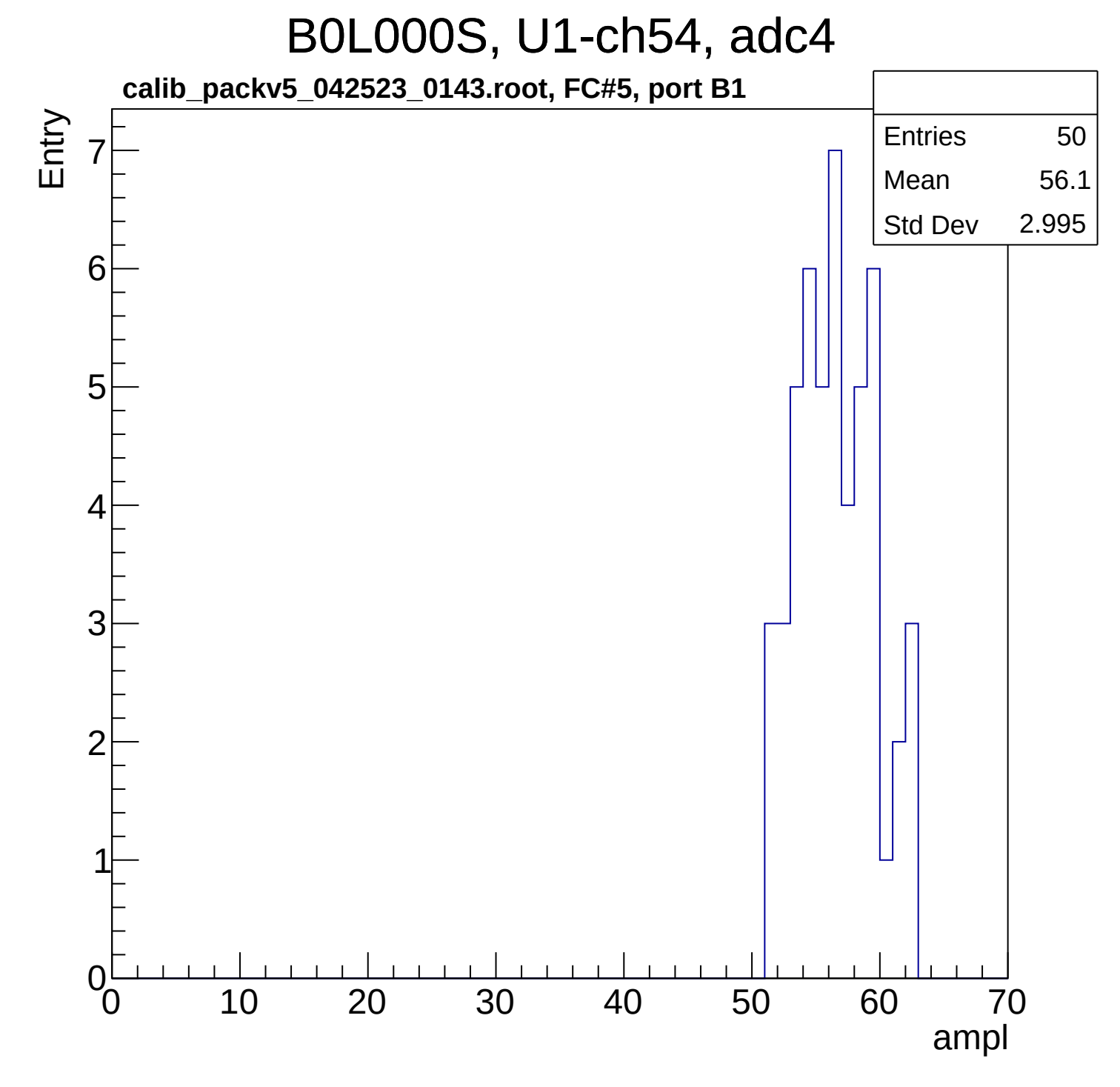
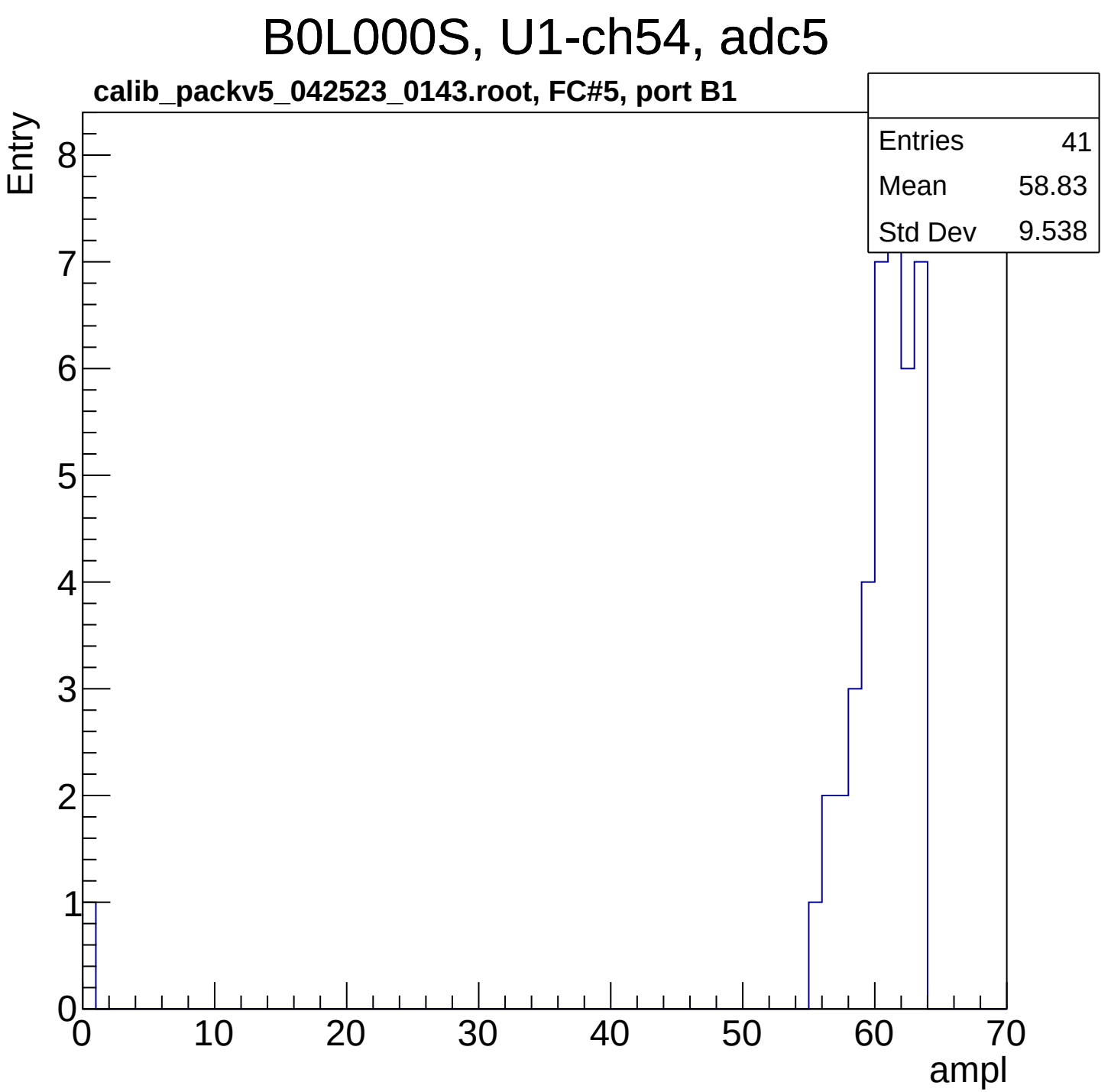
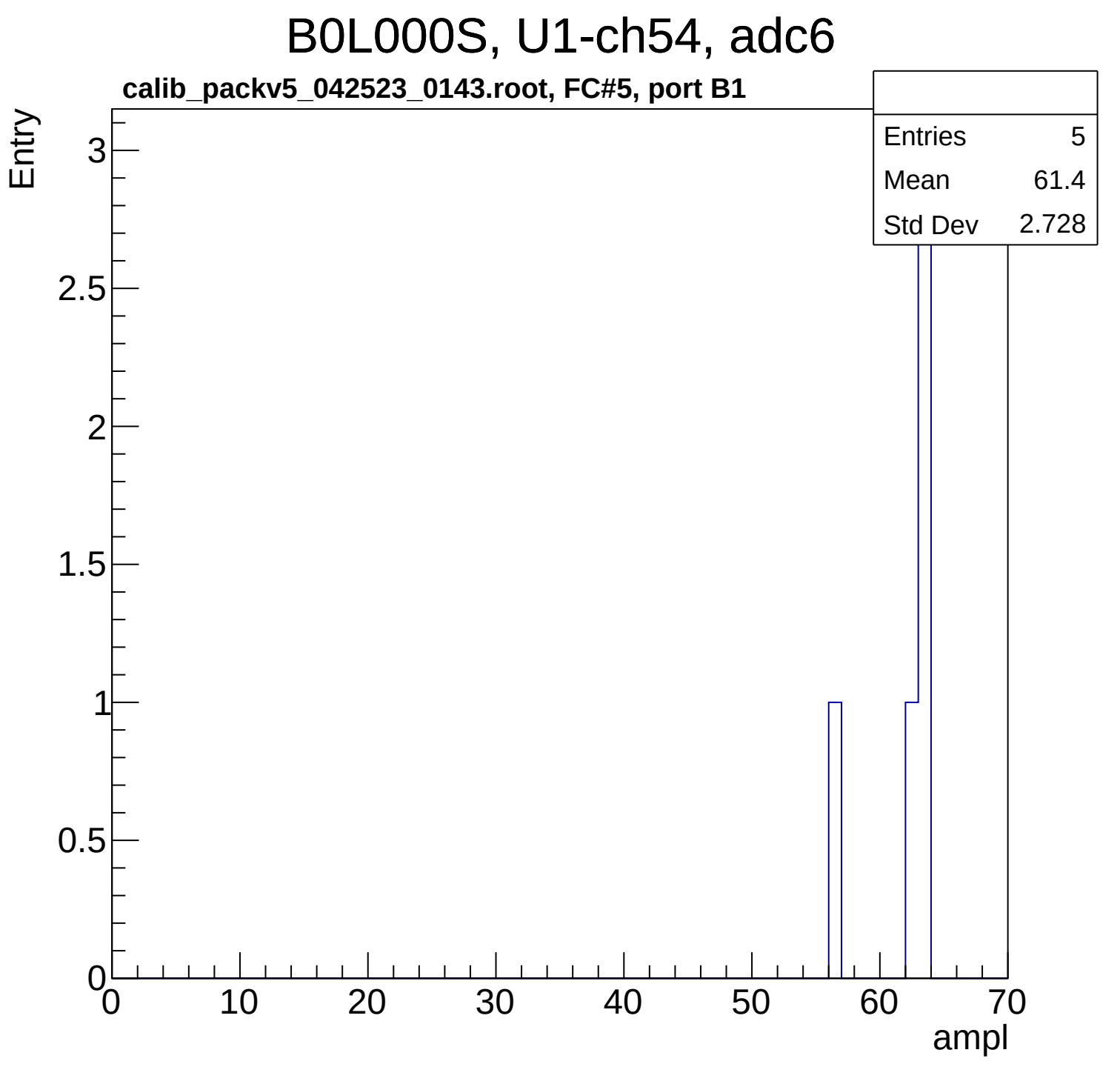
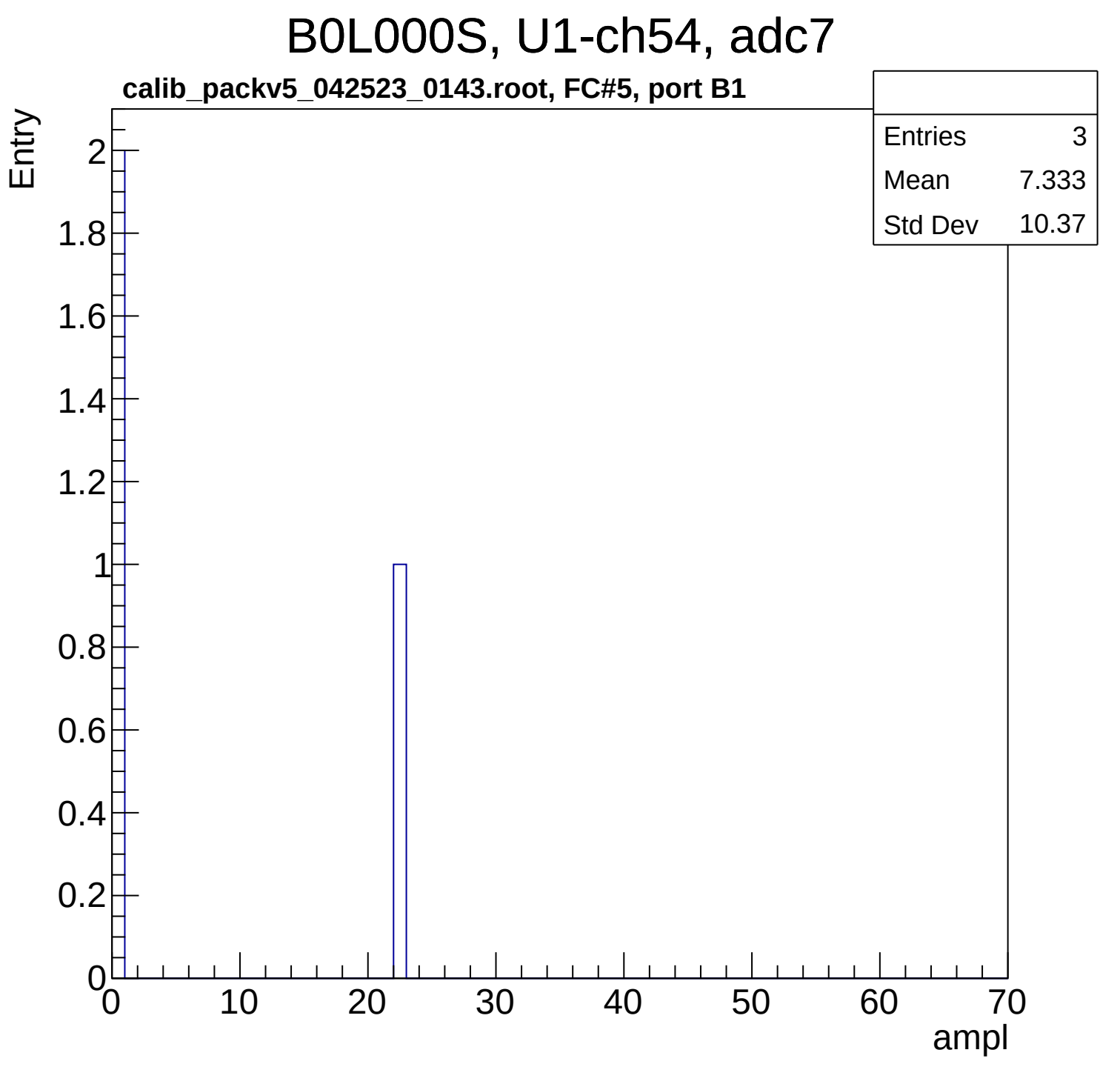
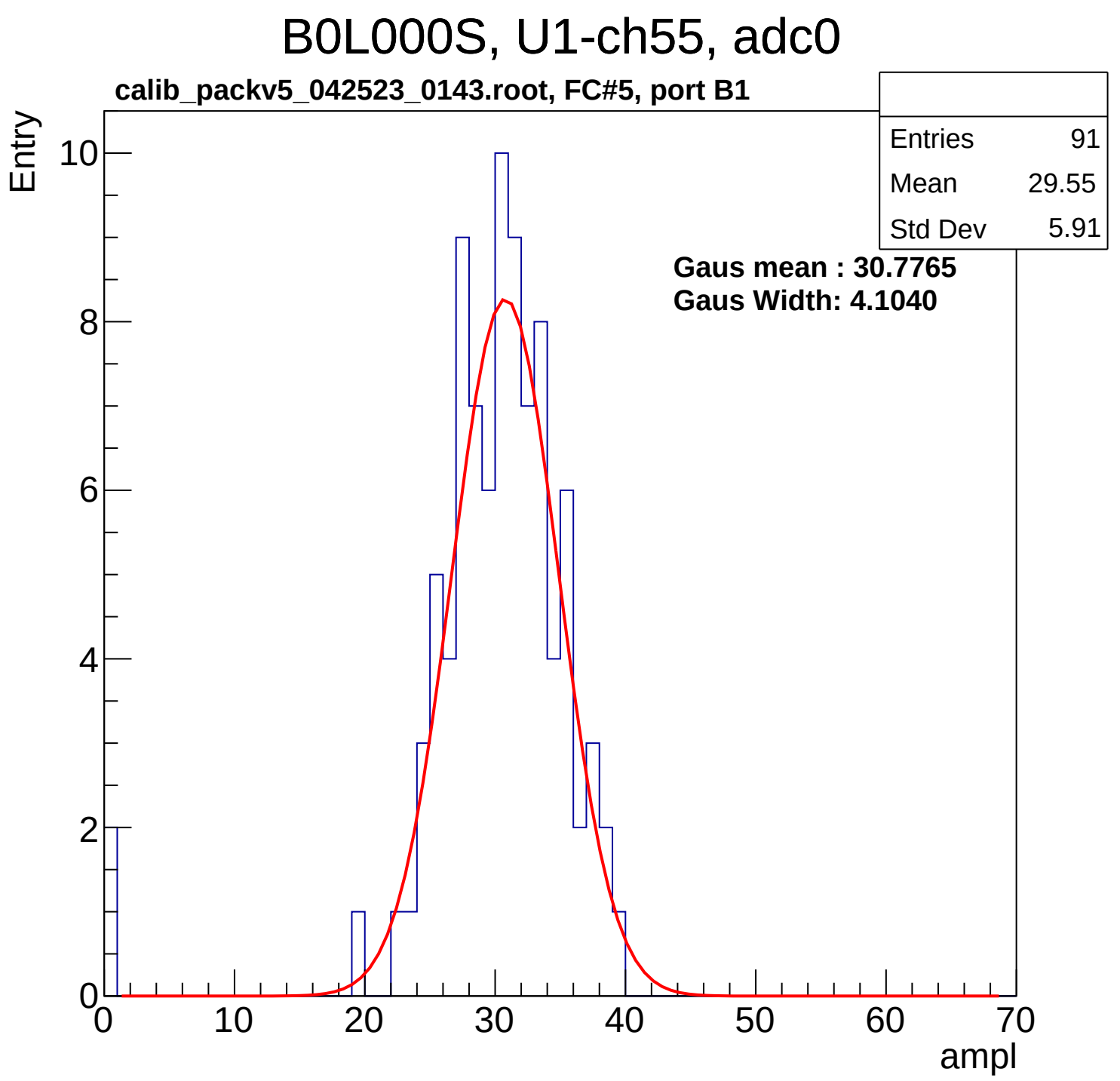

B0L000S, U1-ch54, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.83
Std Dev 3.753
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

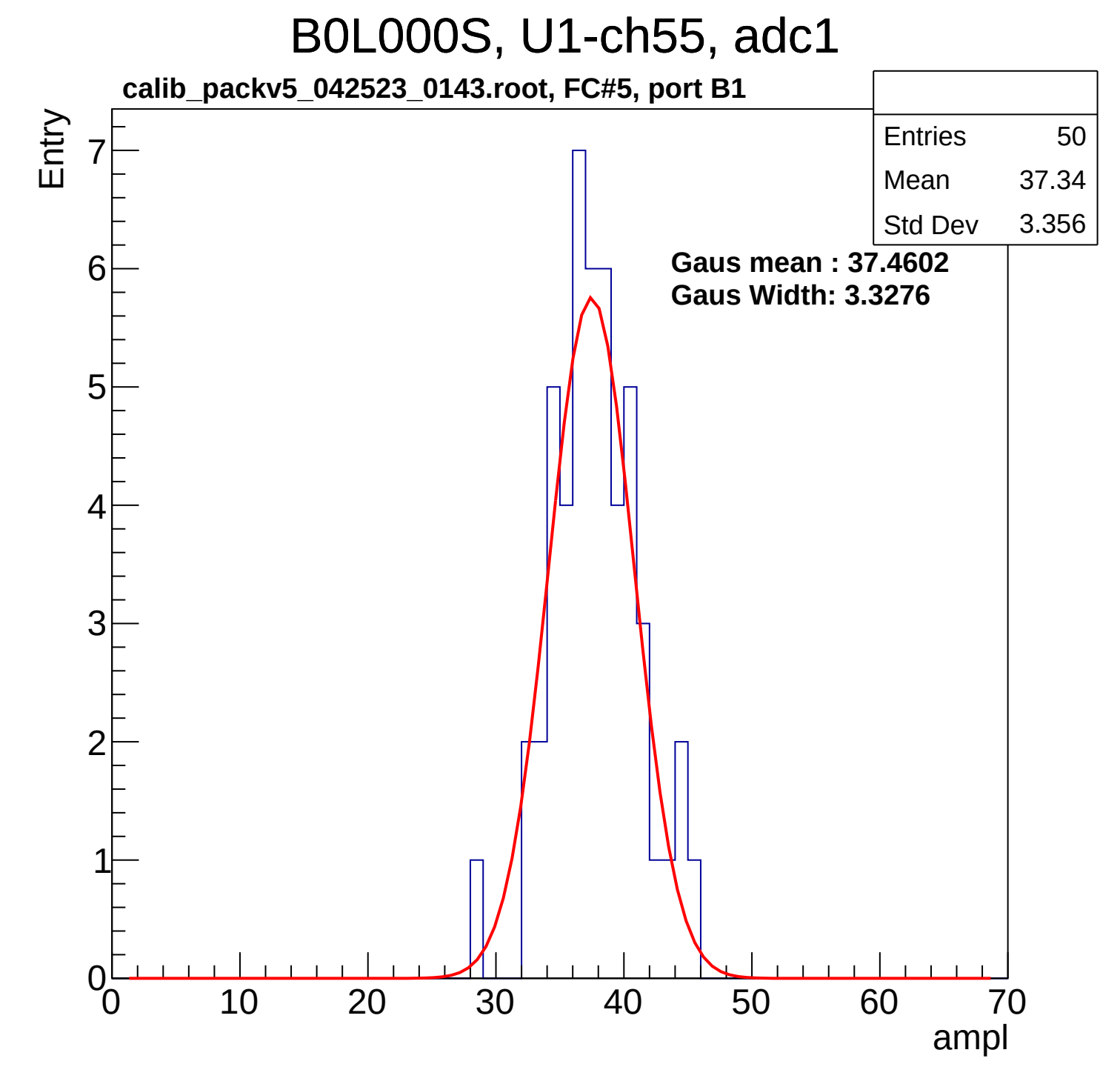

B0L000S, U1-ch54, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 56.1
Std Dev 2.995
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

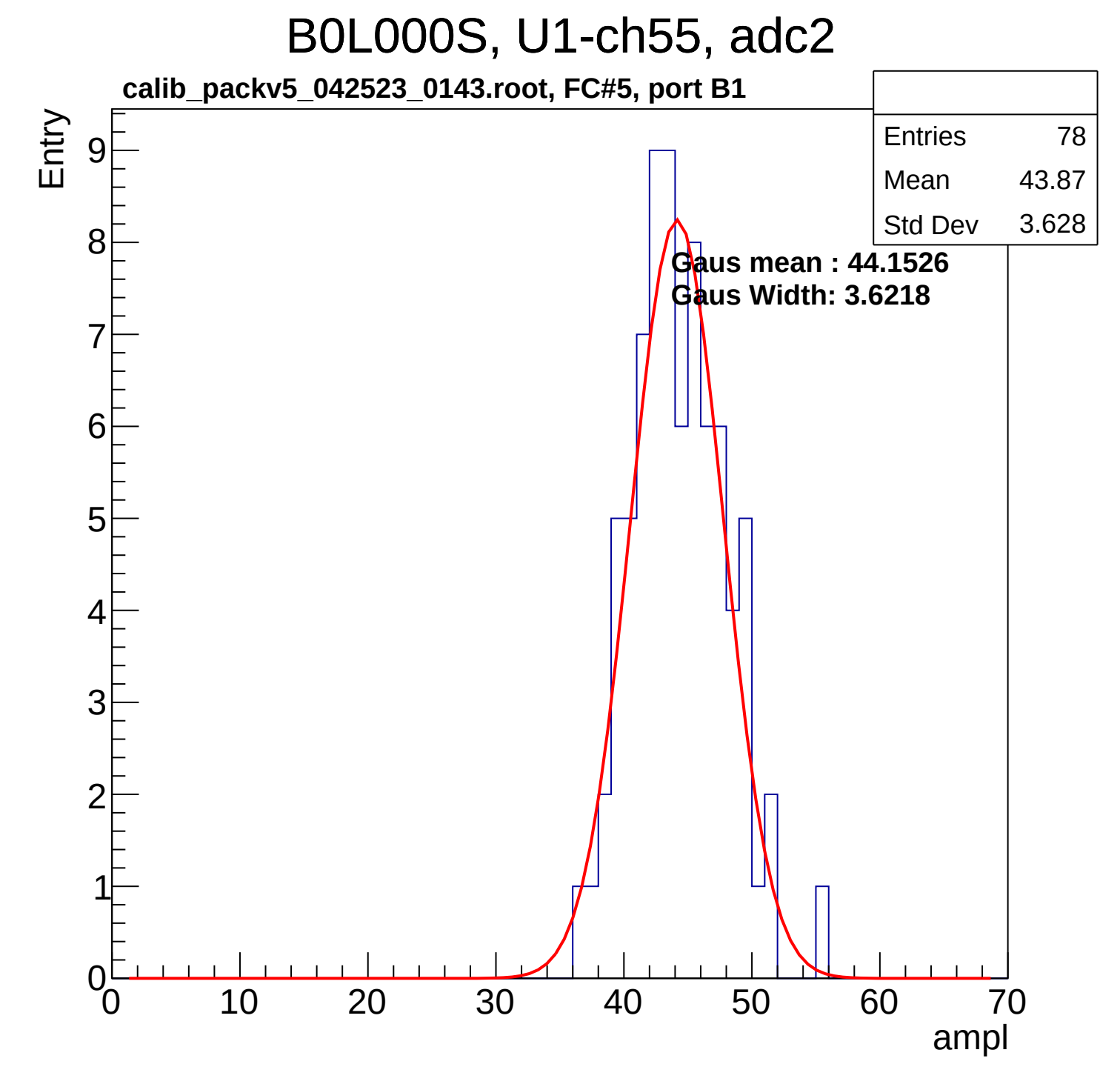

B0L000S, U1-ch54, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.83
Std Dev 9.538
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

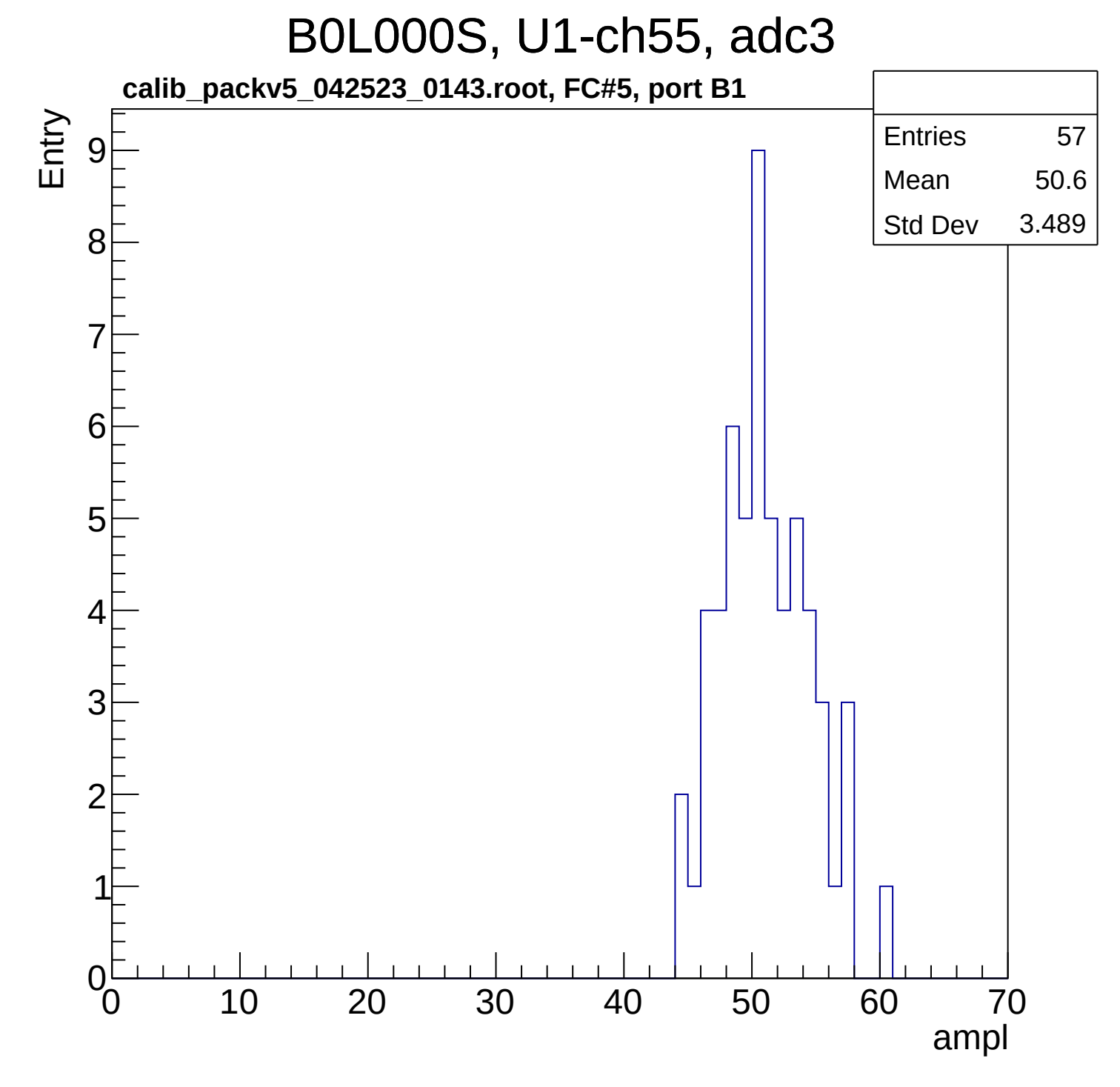

B0L000S, U1-ch54, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.4
Std Dev 2.728
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

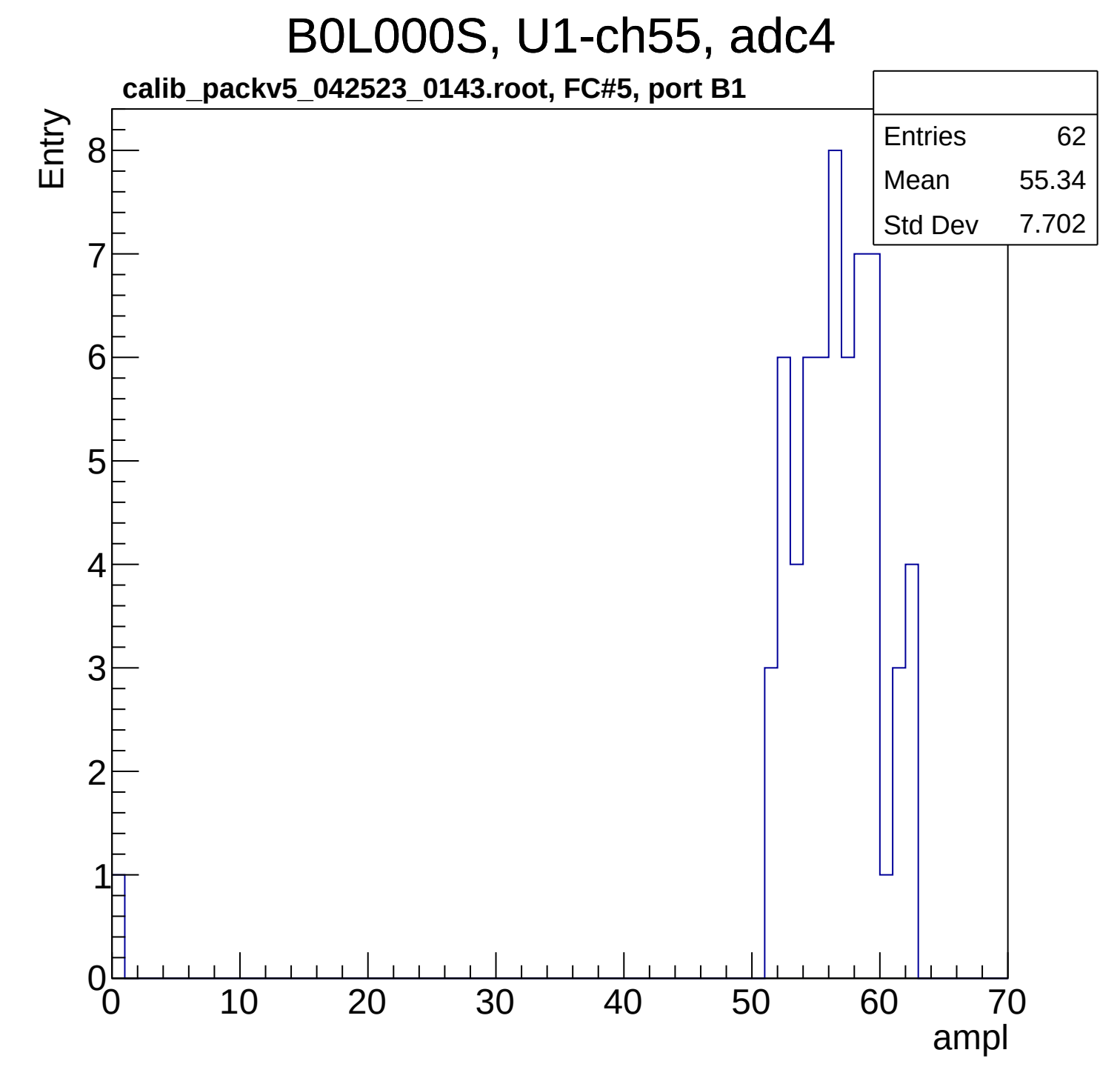

B0L000S, U1-ch54, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

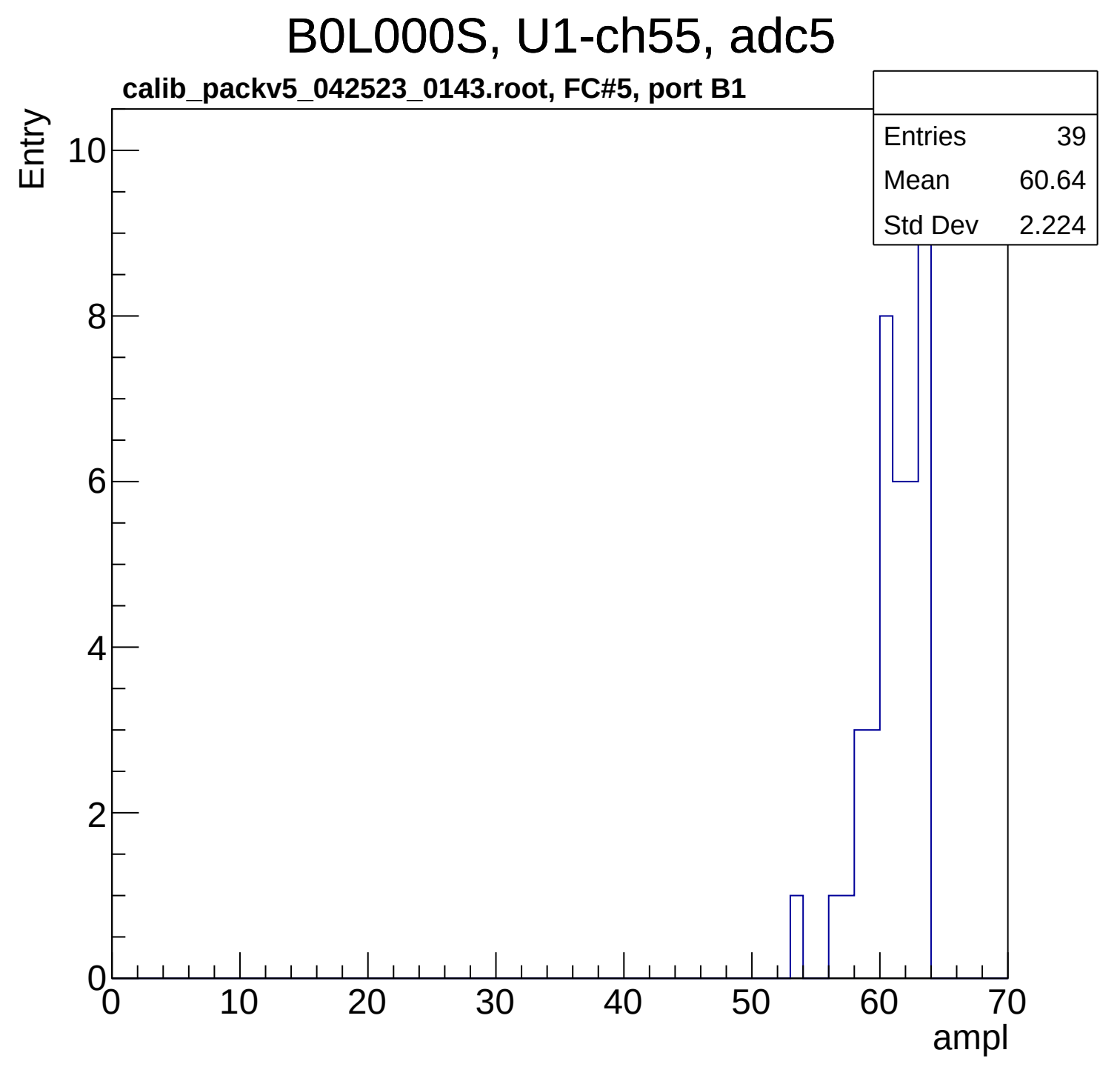

B0L000S, U1-ch55, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 91
Mean 29.55
Std Dev 5.91
Gaus mean : 30.7765
Gaus Width: 4.1040
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

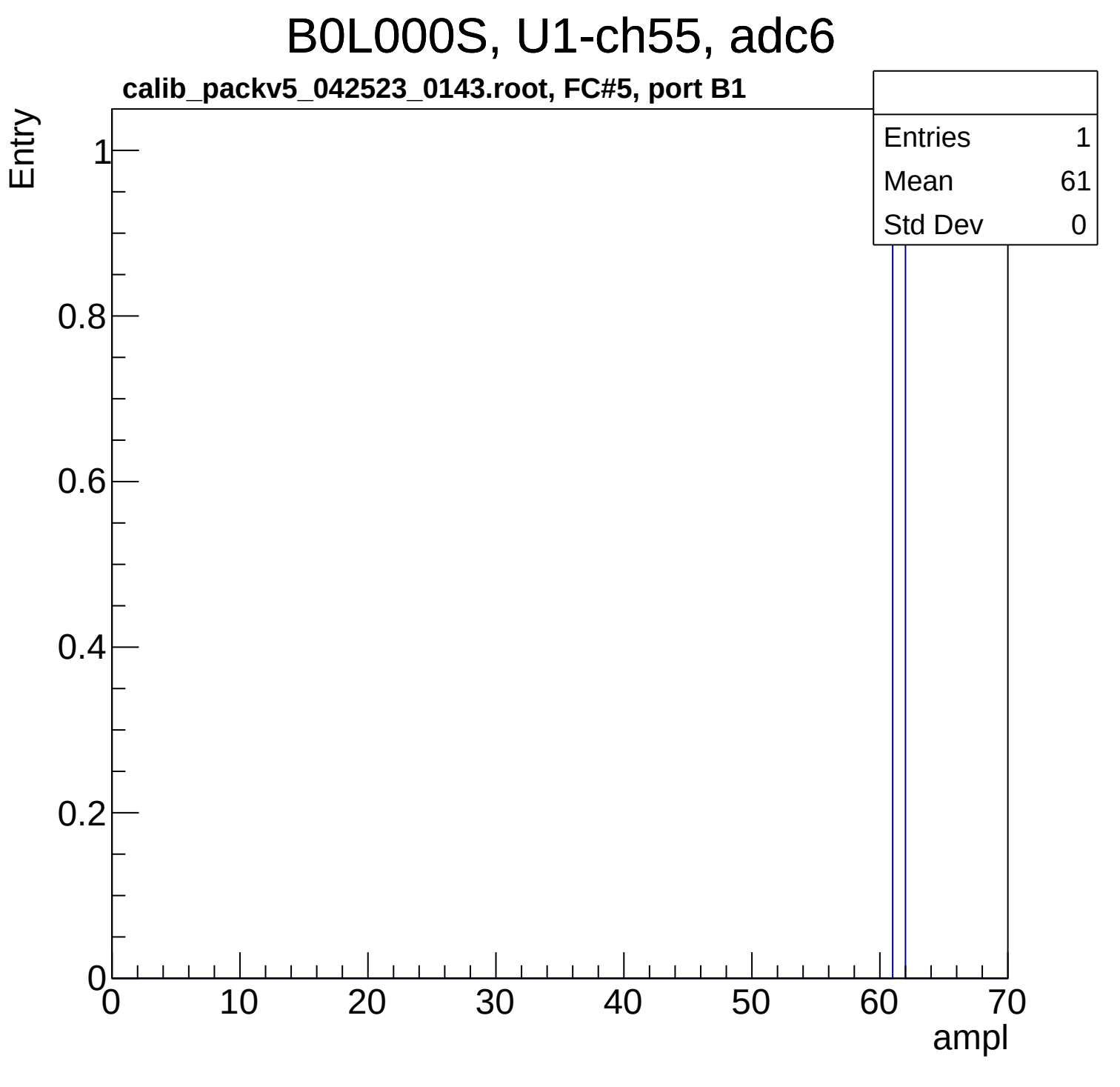

B0L000S, U1-ch55, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 37.34
Std Dev 3.356
Gaus mean : 37.4602
Gaus Width: 3.3276
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

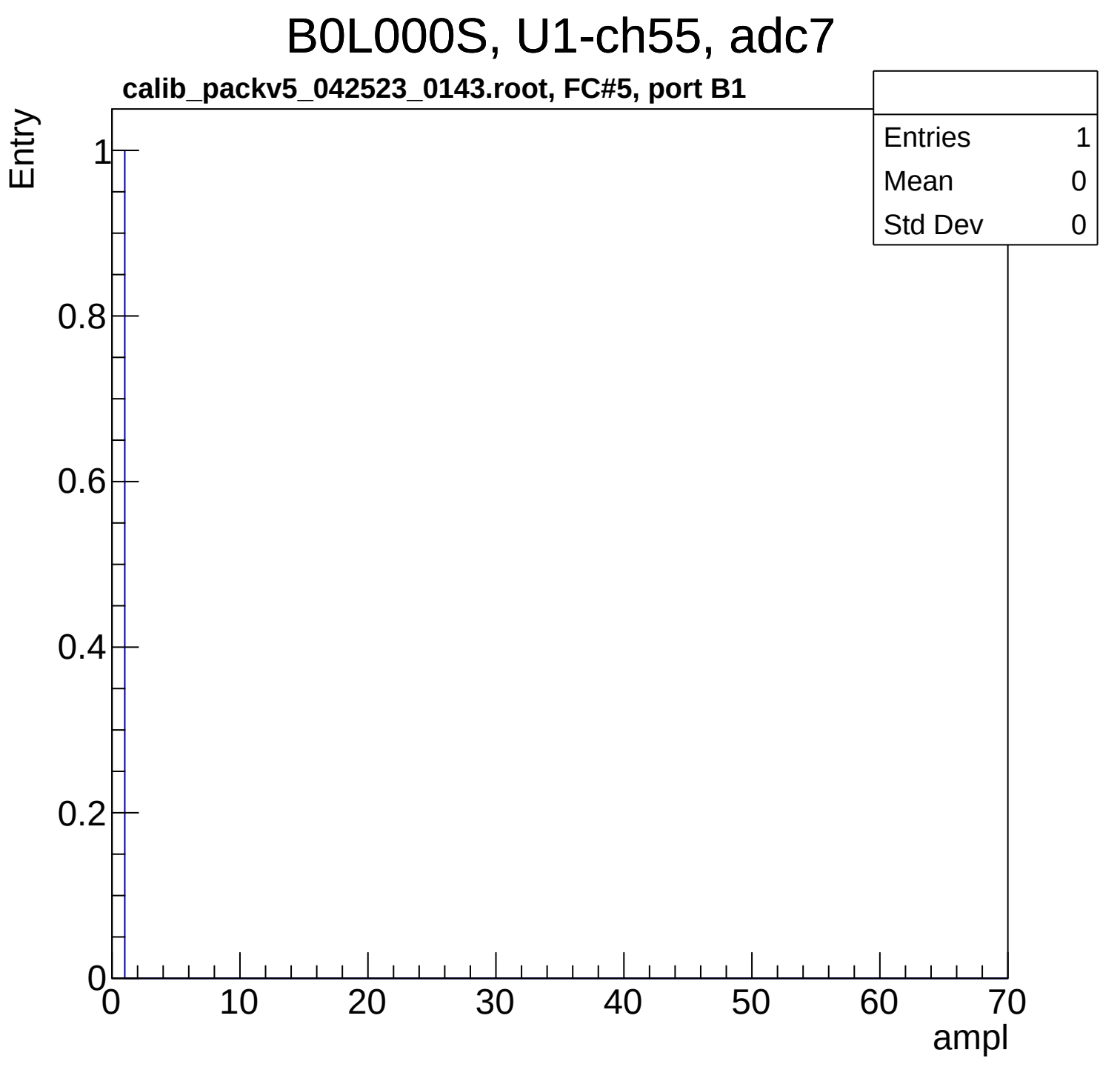

B0L000S, U1-ch55, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 43.87
Std Dev 3.628
Gaus mean : 44.1526
Gaus Width: 3.6218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

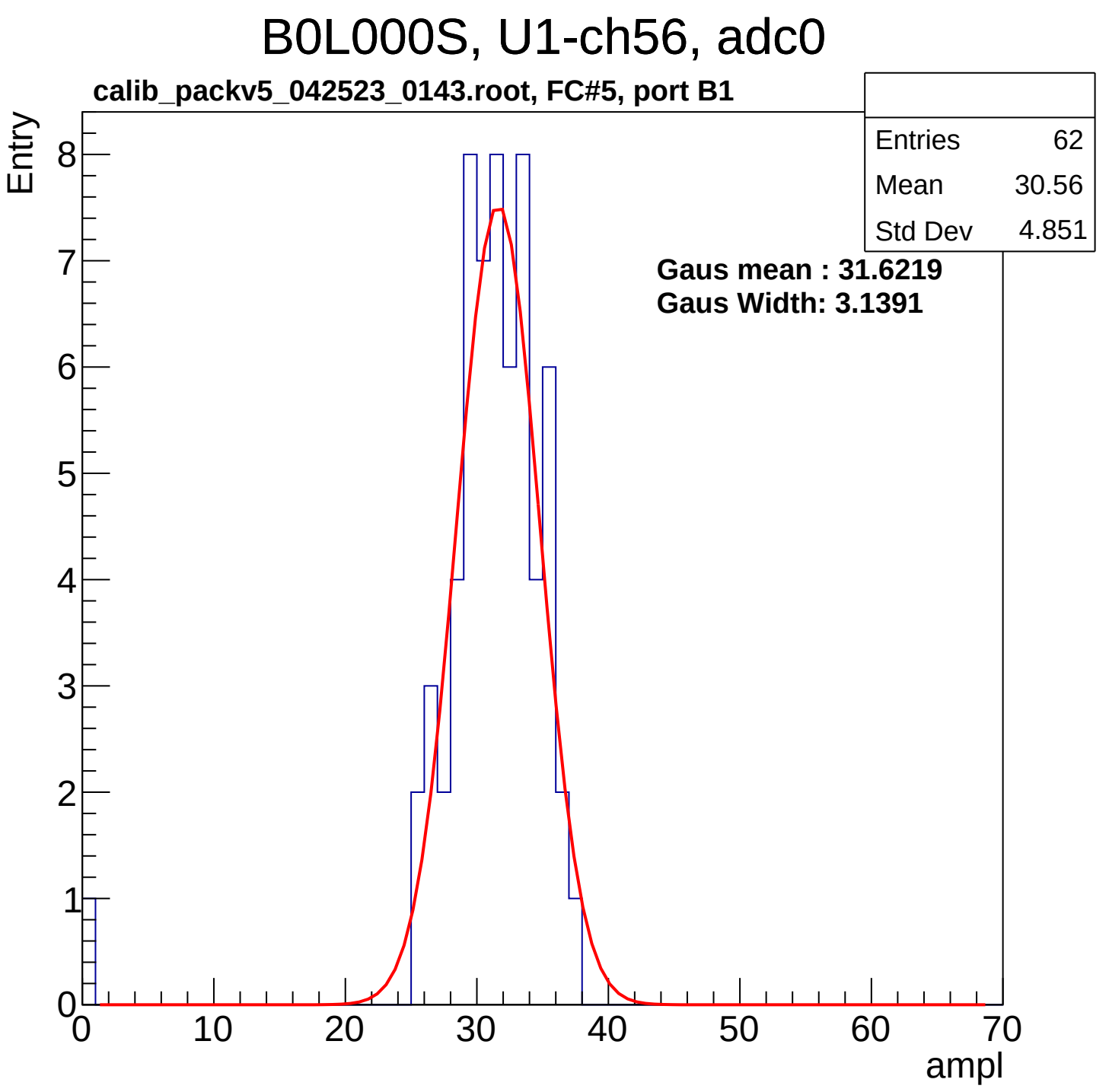

B0L000S, U1-ch55, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 50.6
Std Dev 3.489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

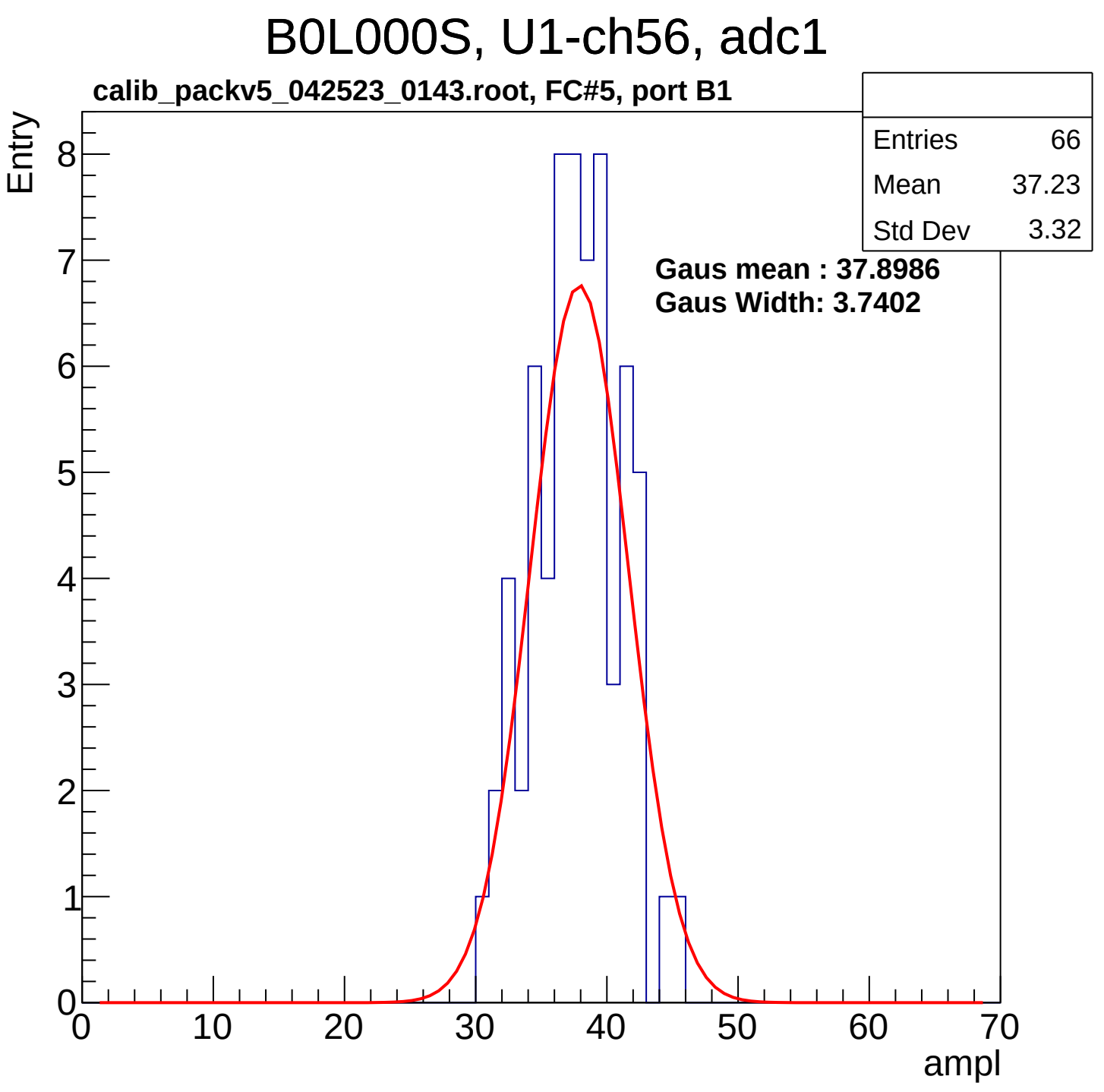

B0L000S, U1-ch55, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 55.34
Std Dev 7.702
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

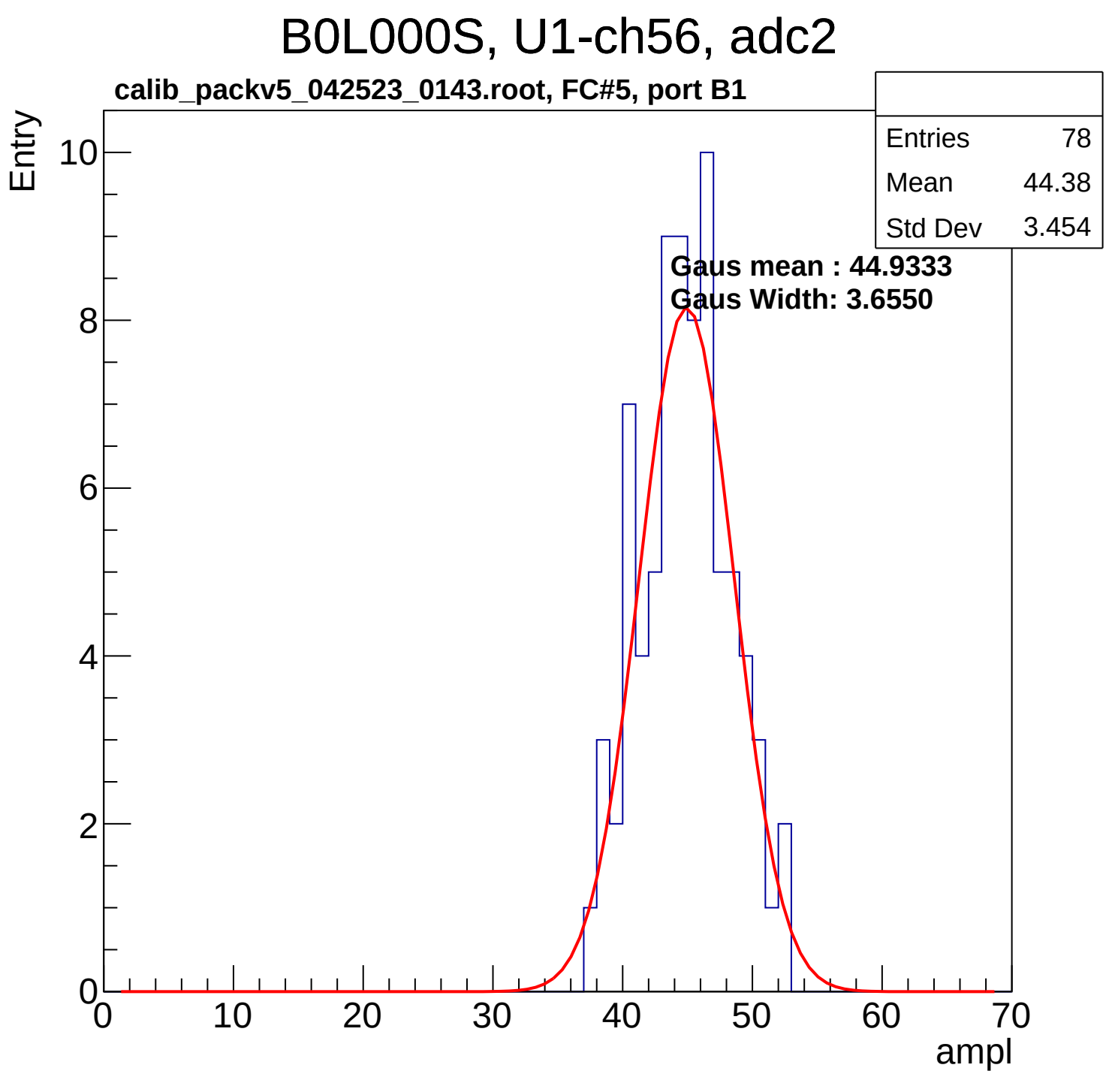

B0L000S, U1-ch55, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 60.64
Std Dev 2.224
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

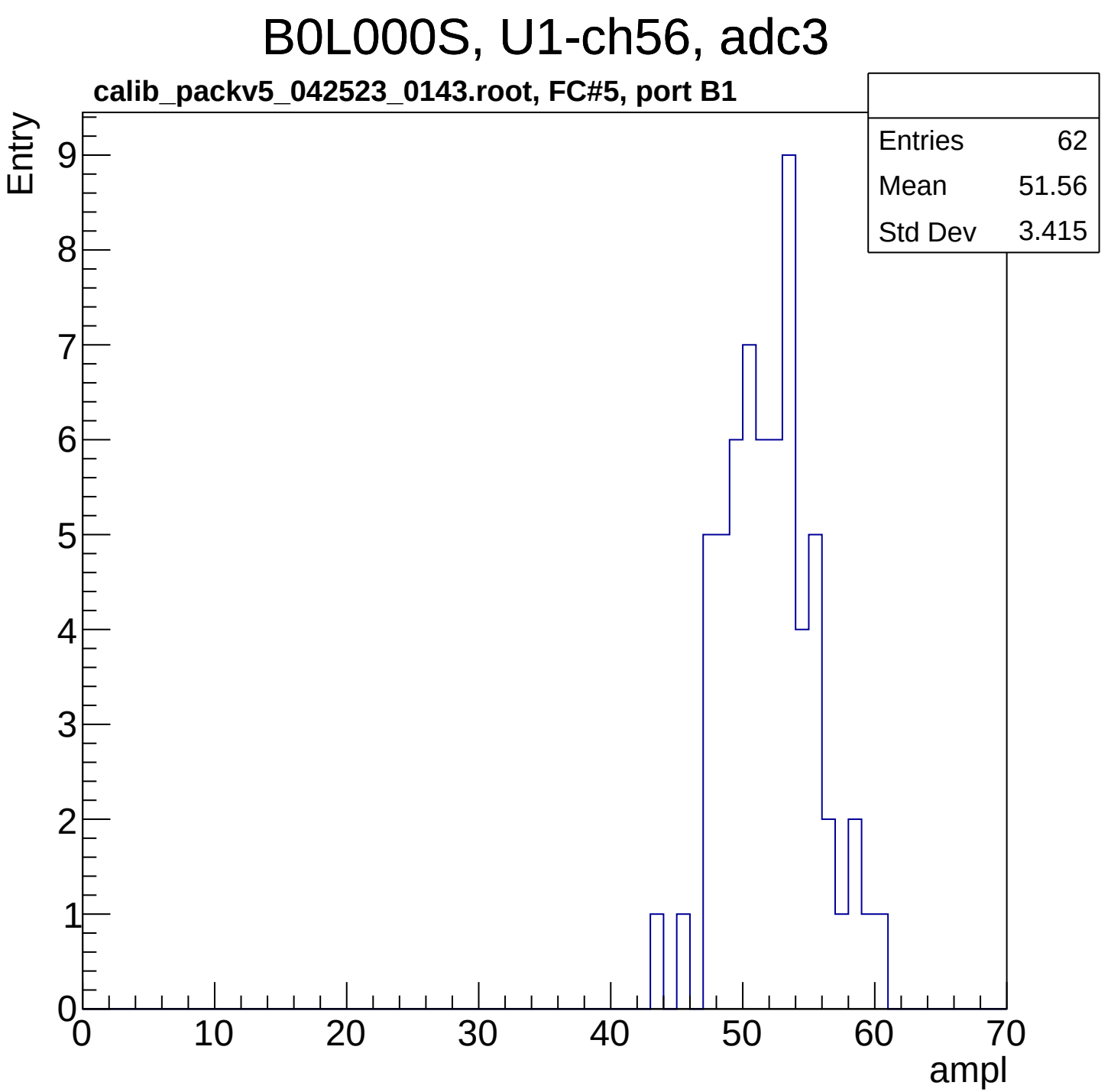

B0L000S, U1-ch55, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch55, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch56, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 30.56
Std Dev 4.851
Gaus mean : 31.6219
Gaus Width: 3.1391
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch56, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 37.23
Std Dev 3.32
Gaus mean : 37.8986
Gaus Width: 3.7402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch56, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 44.38
Std Dev 3.454
Gaus mean : 44.9333
Gaus Width: 3.6550
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch56, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 51.56
Std Dev 3.415
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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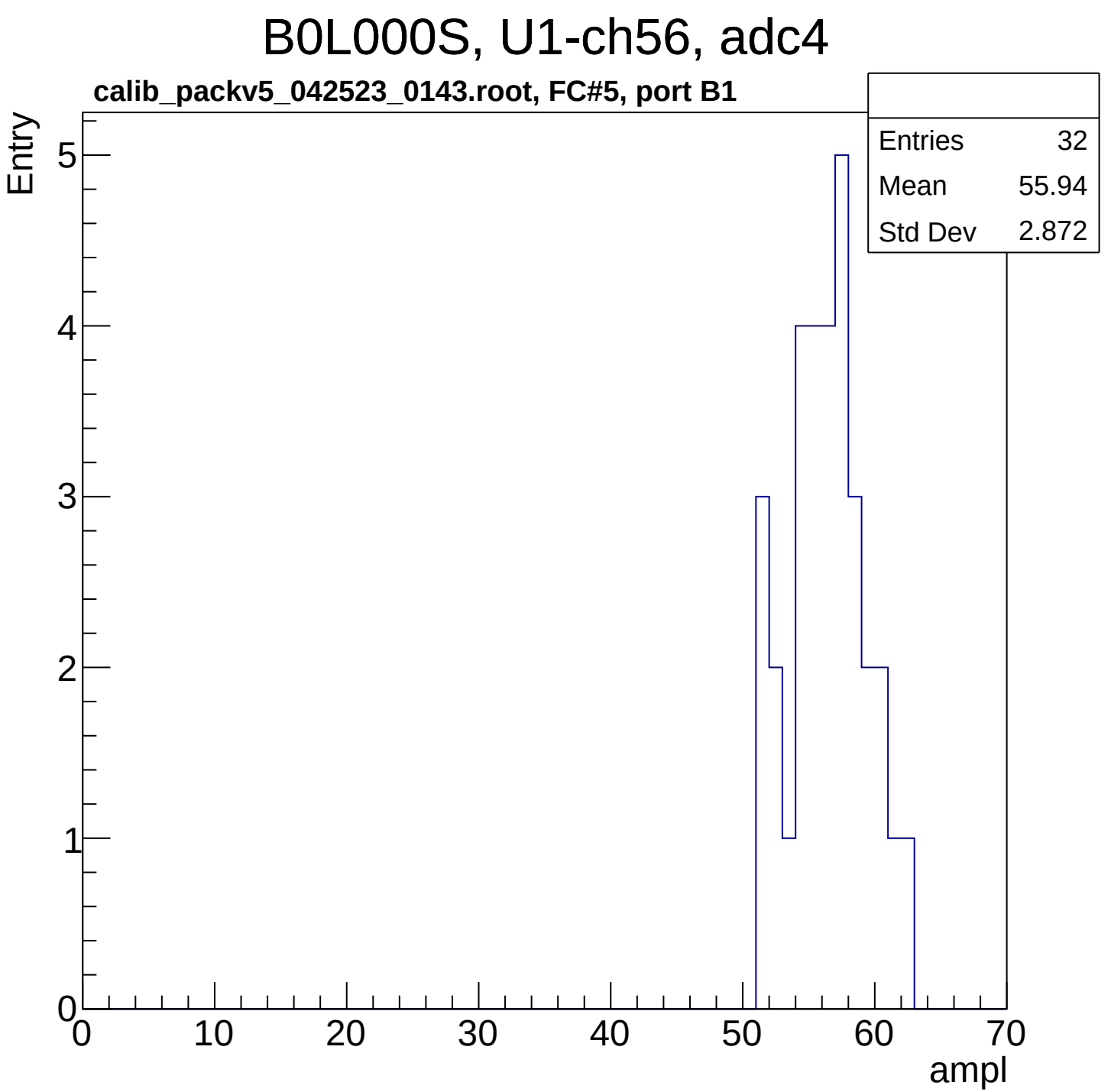
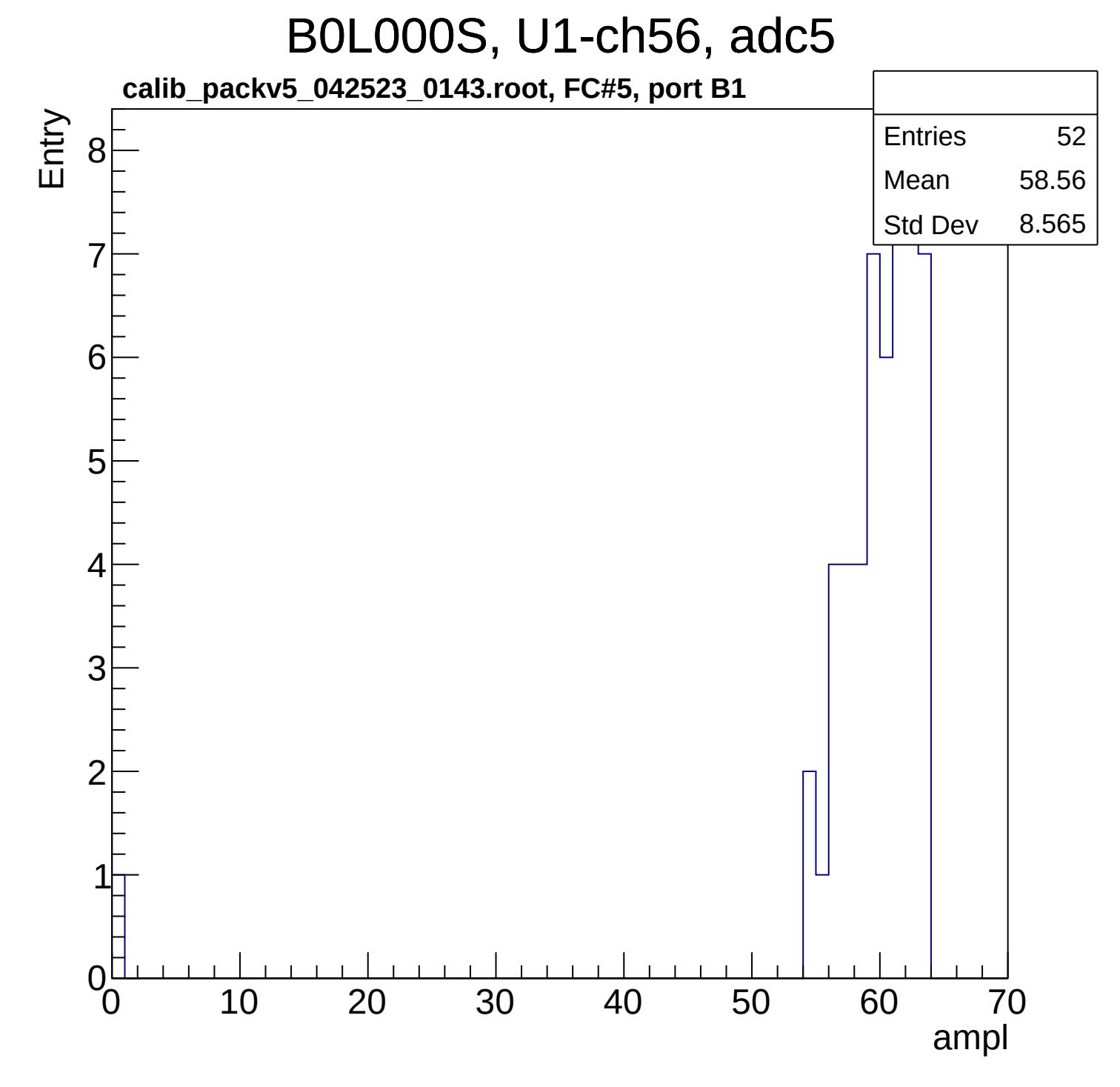
20
30
40
50
60
70


B0L000S, U1-ch56, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 55.94
Std Dev 2.872
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch56, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.56
Std Dev 8.565
Entry
ampl
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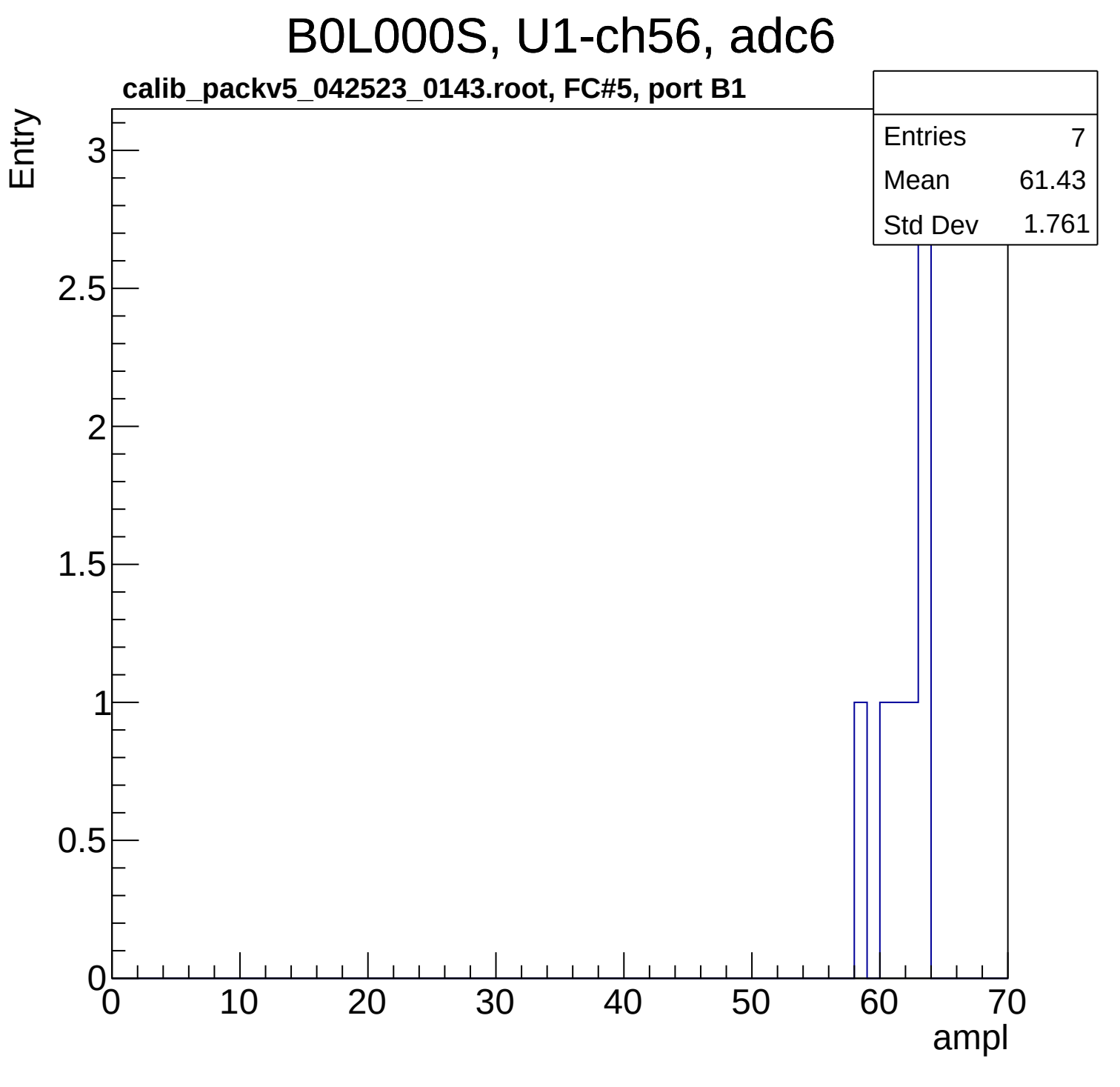
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch56, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.43
Std Dev 1.761
Entry
ampl
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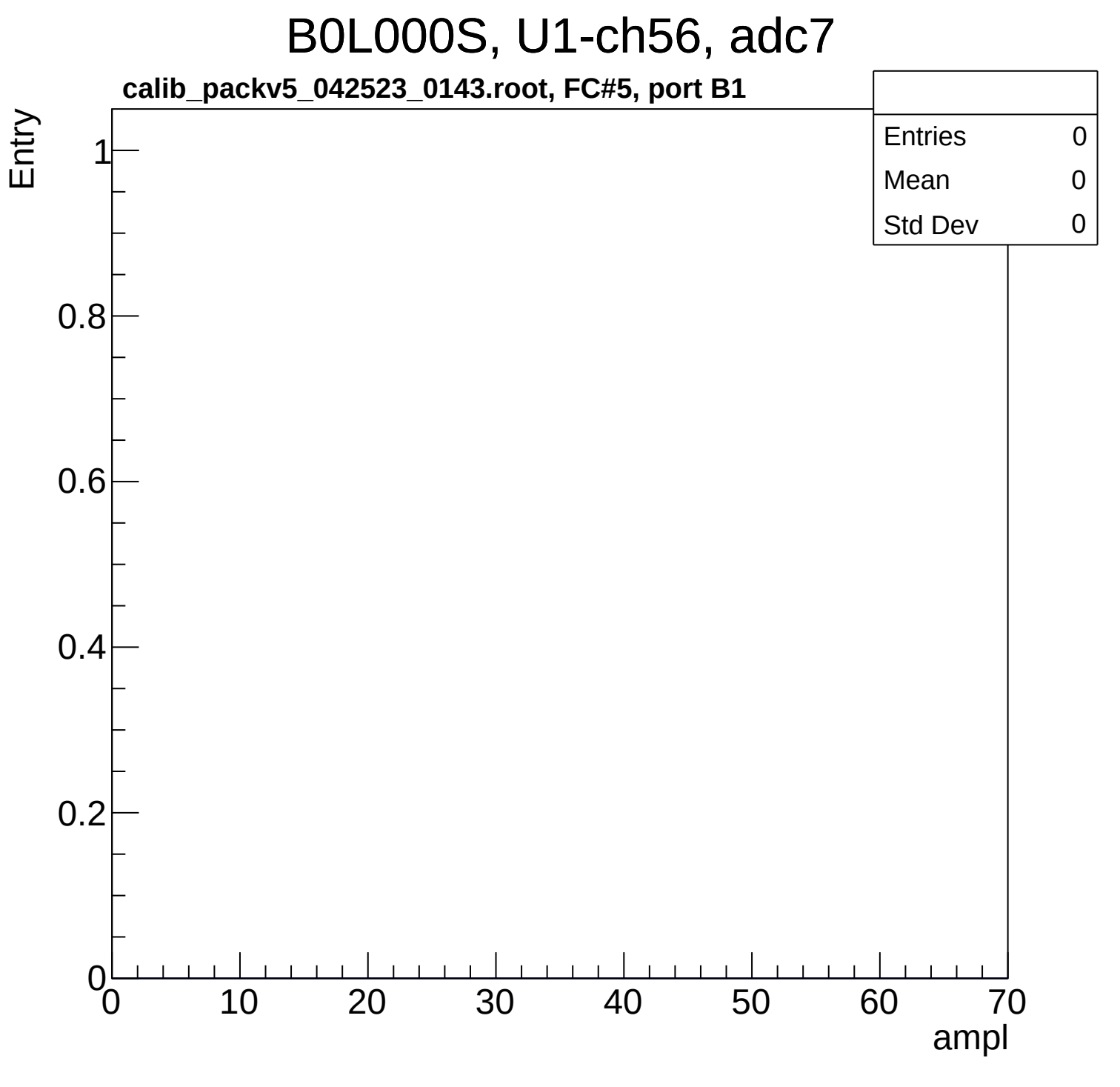
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch56, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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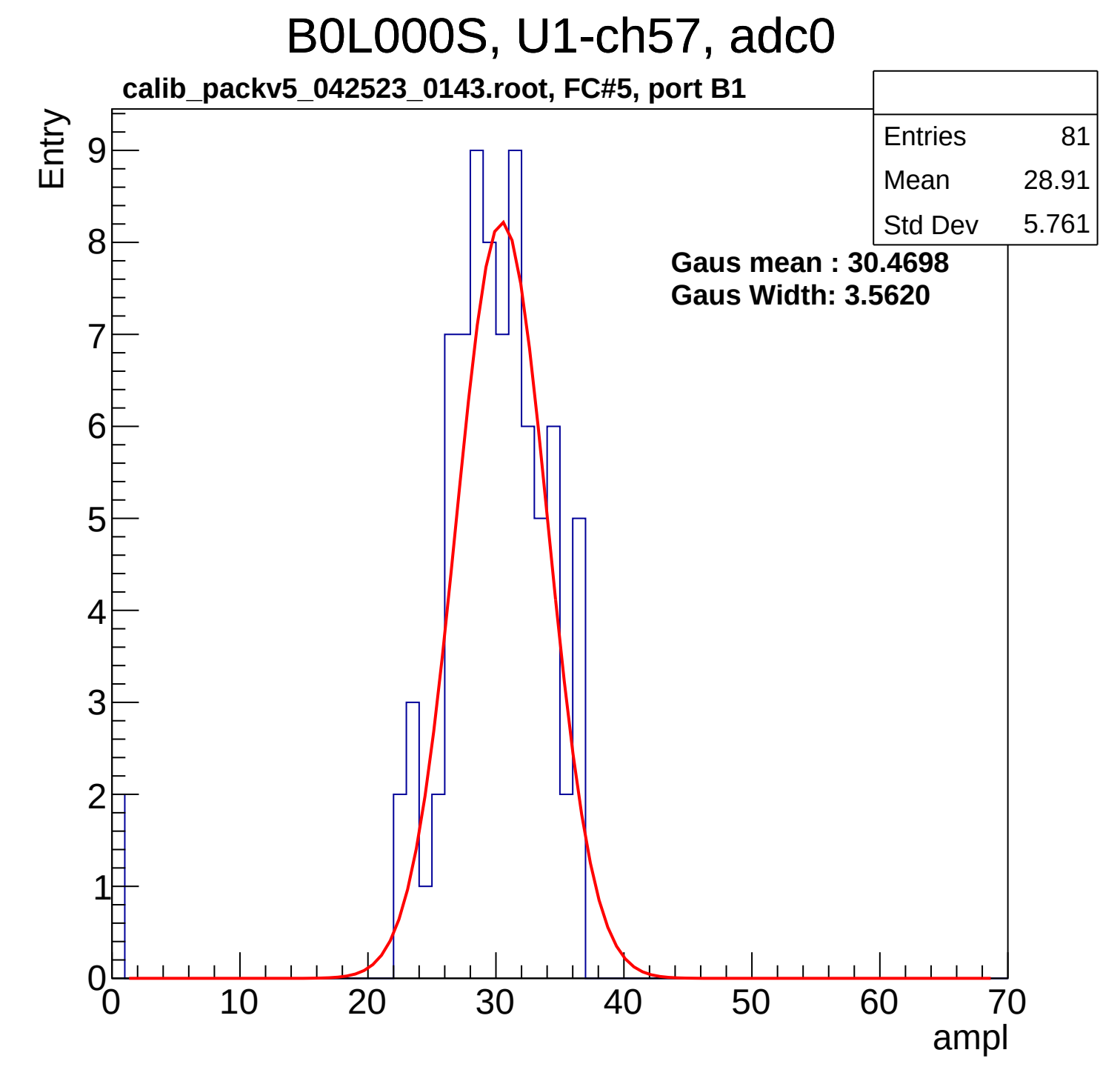
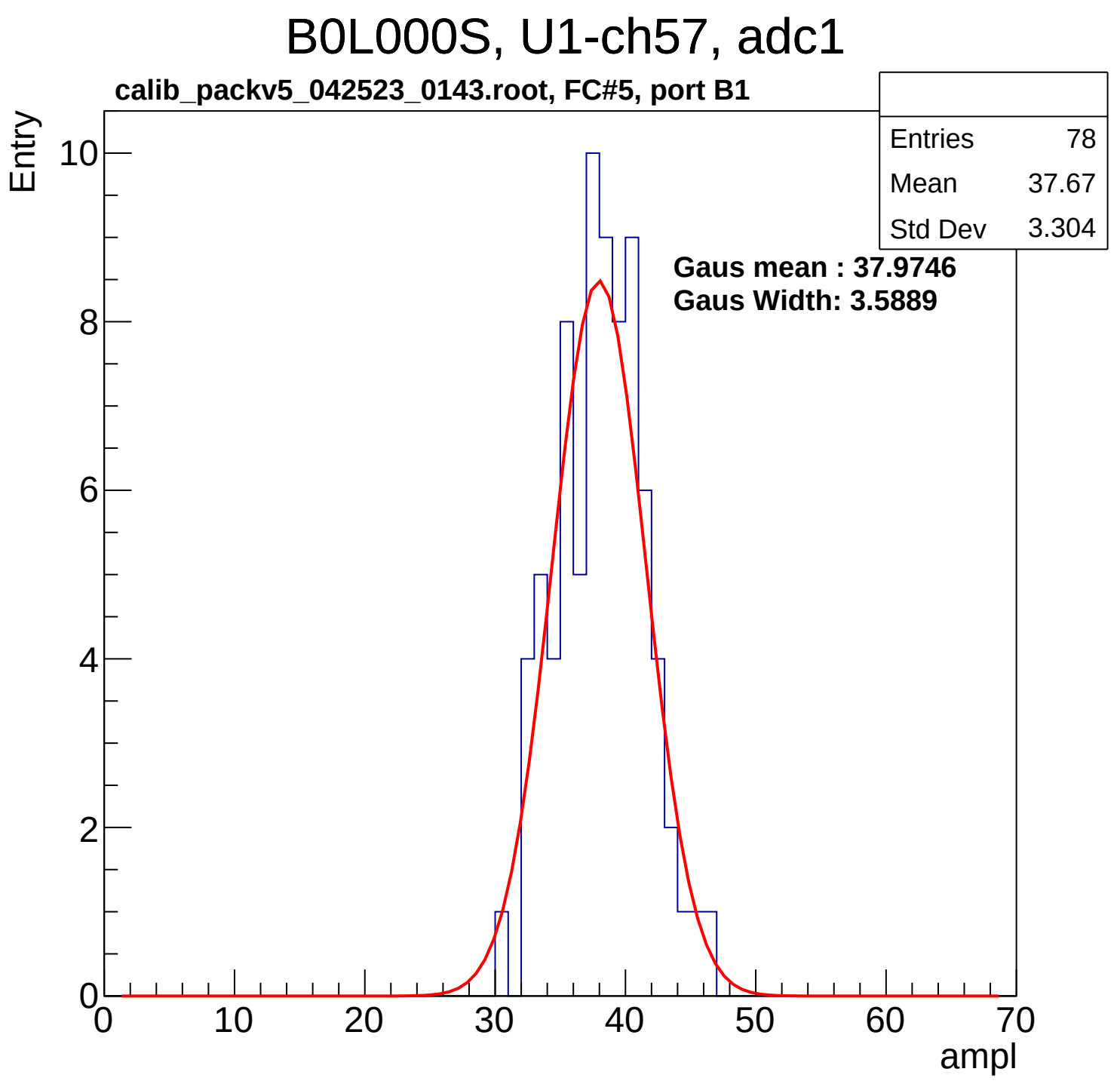
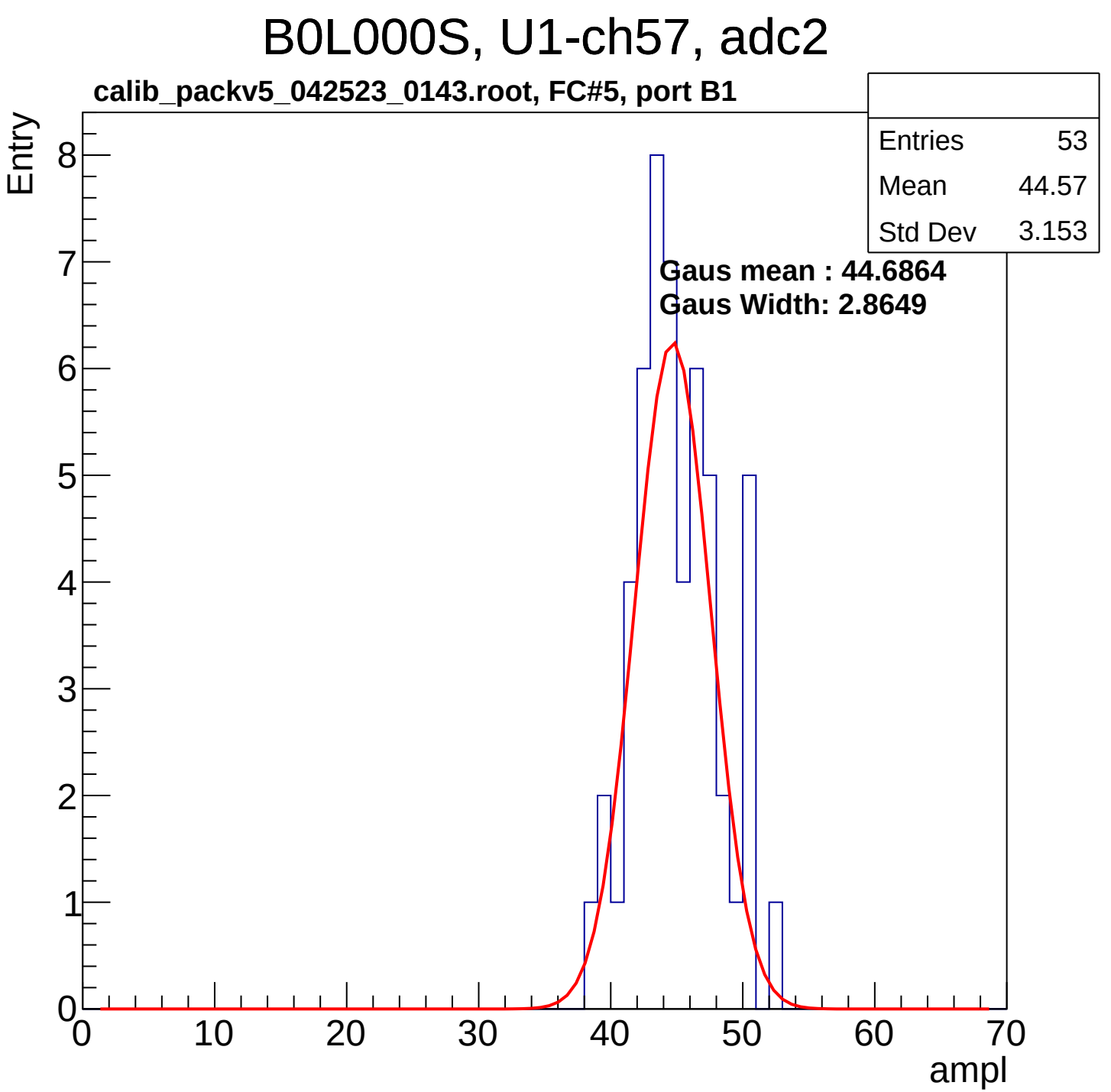
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch57, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 28.91
Std Dev 5.761
Gaus mean : 30.4698
Gaus Width: 3.5620
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch57, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 37.67
Std Dev 3.304
Gaus mean : 37.9746
Gaus Width: 3.5889
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch57, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 44.57
Std Dev 3.153
Gaus mean : 44.6864
Gaus Width: 2.8649
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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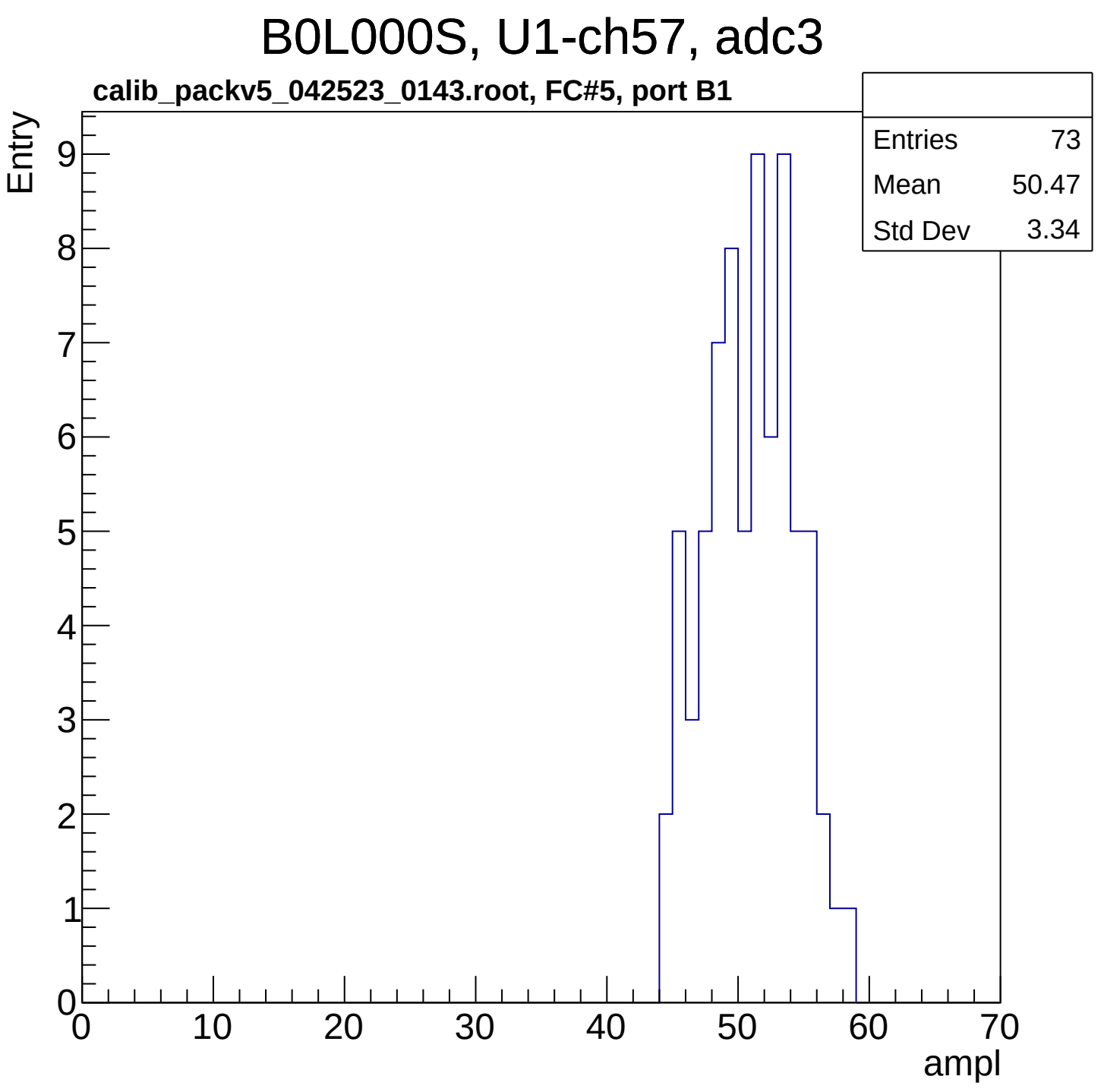
B0L000S, U1-ch57, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 50.47
Std Dev 3.34
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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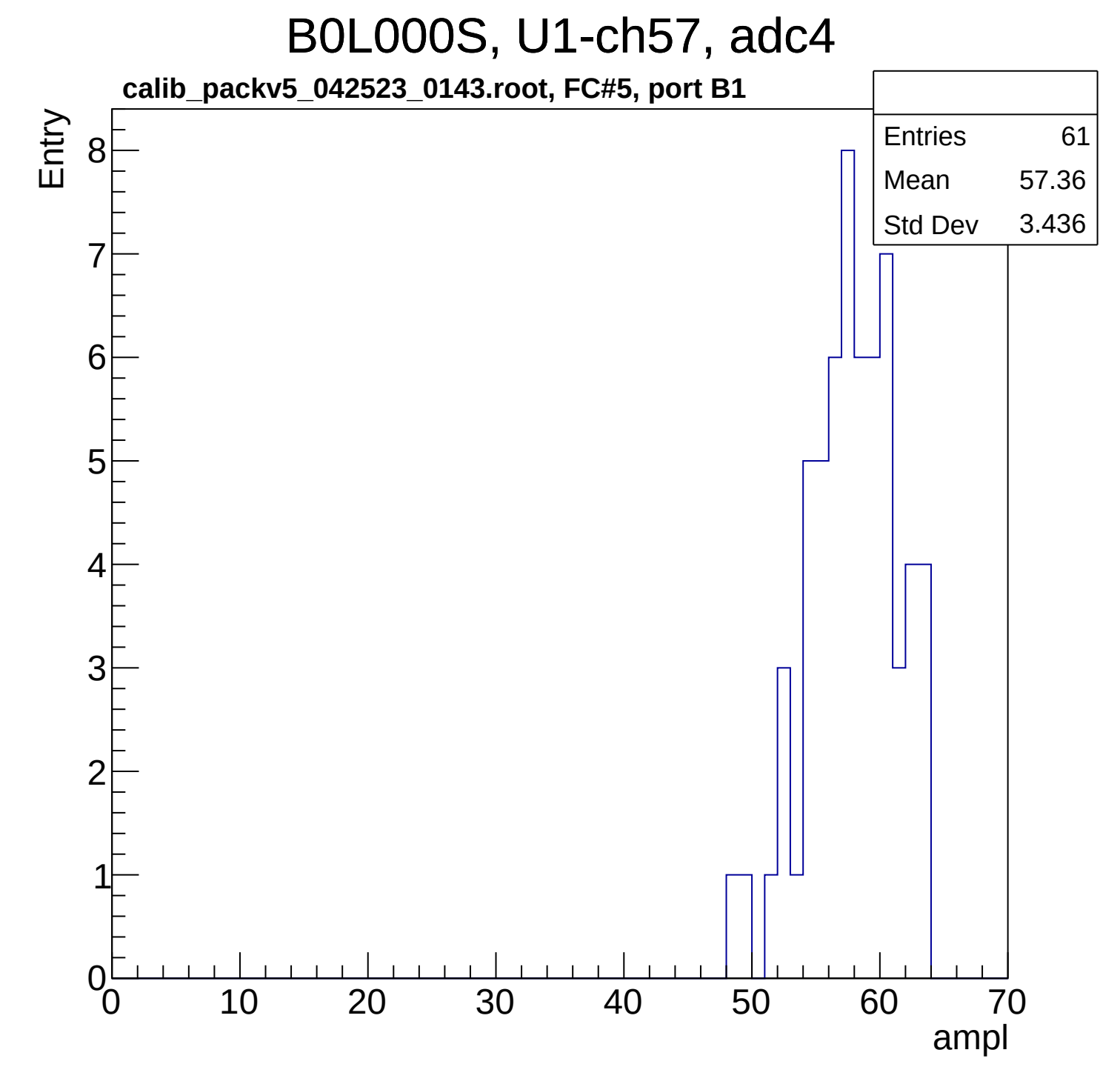
B0L000S, U1-ch57, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 57.36
Std Dev 3.436
Entry
ampl
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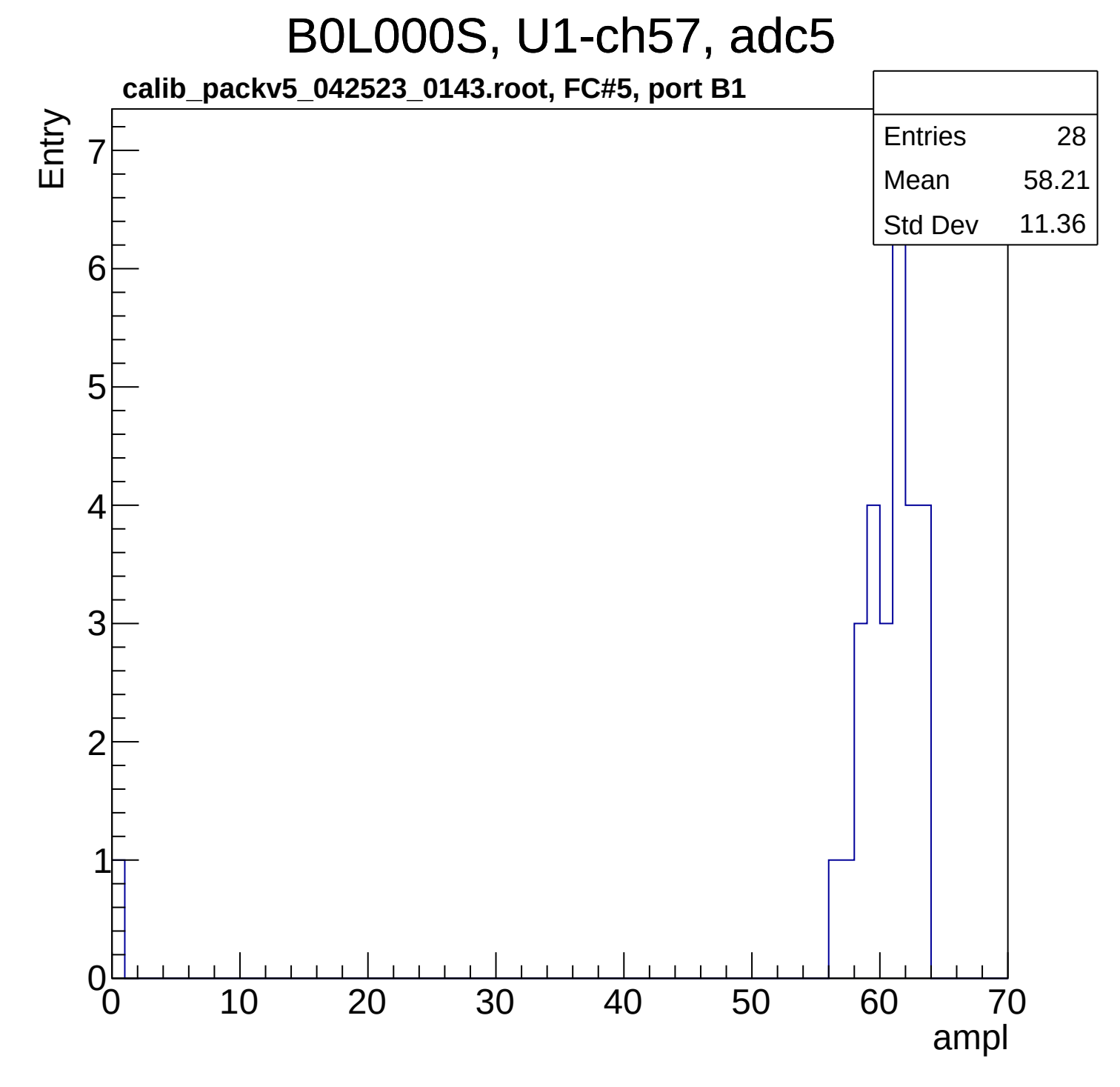
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch57, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.21
Std Dev 11.36
Entry
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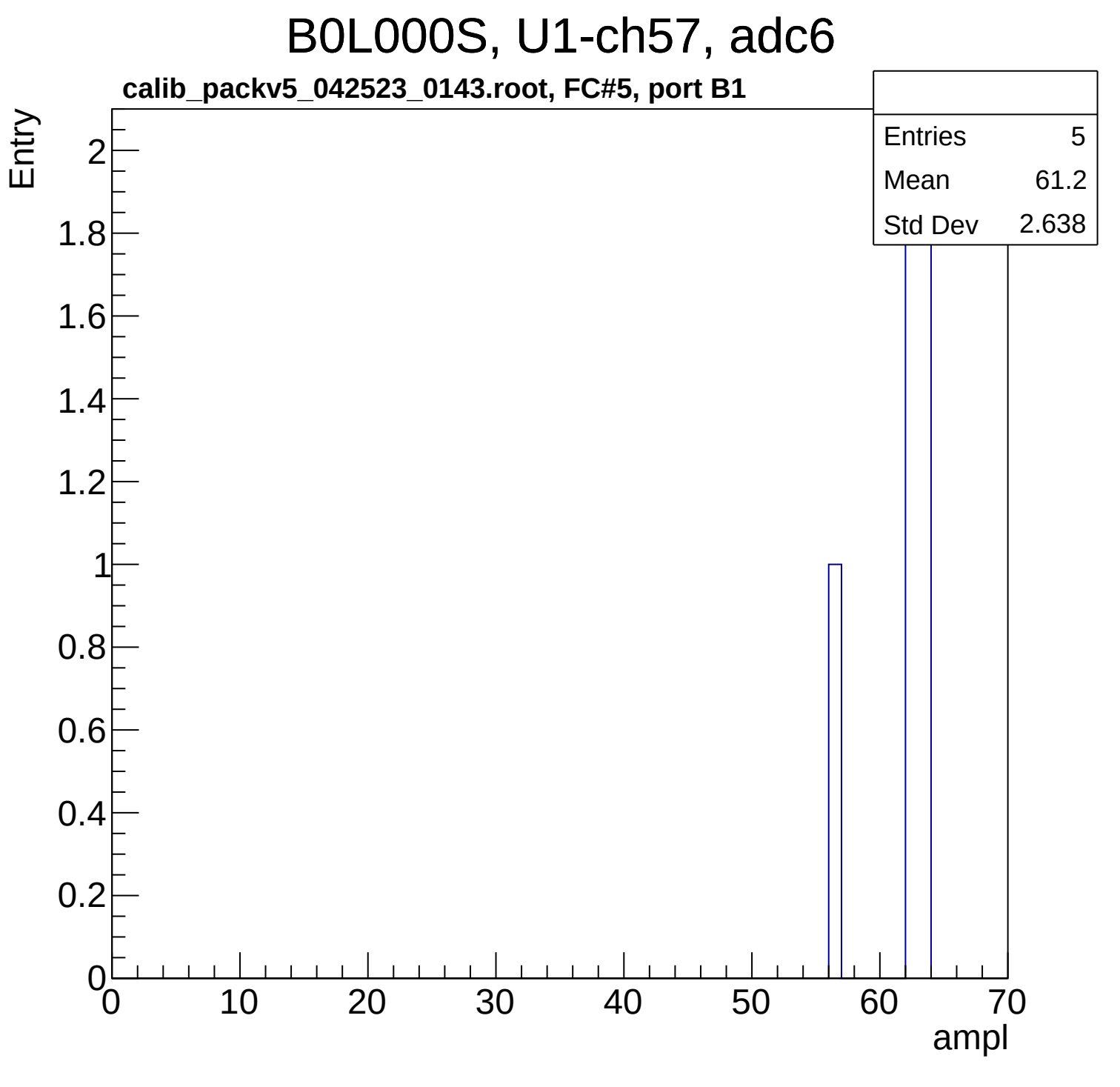
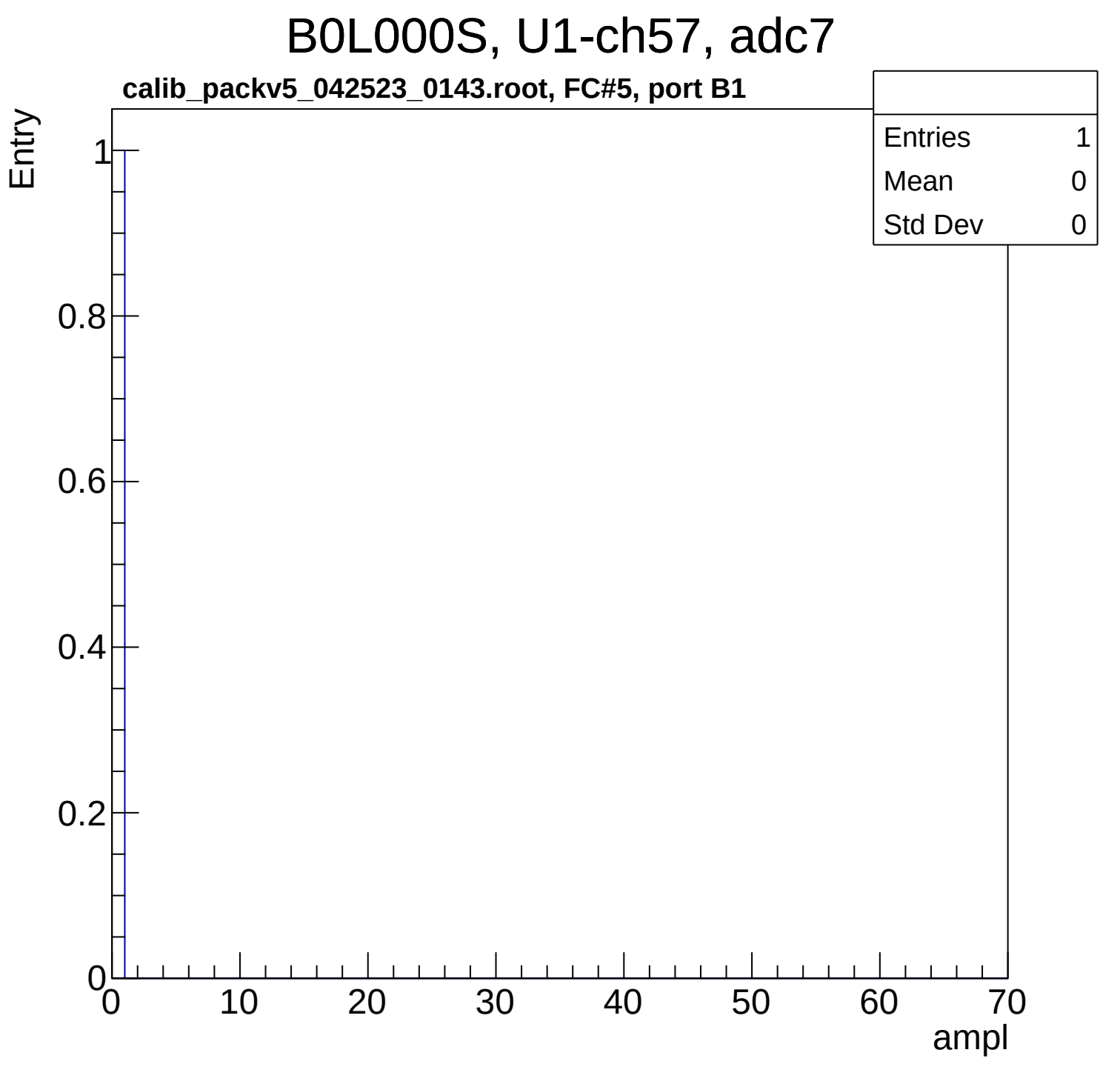
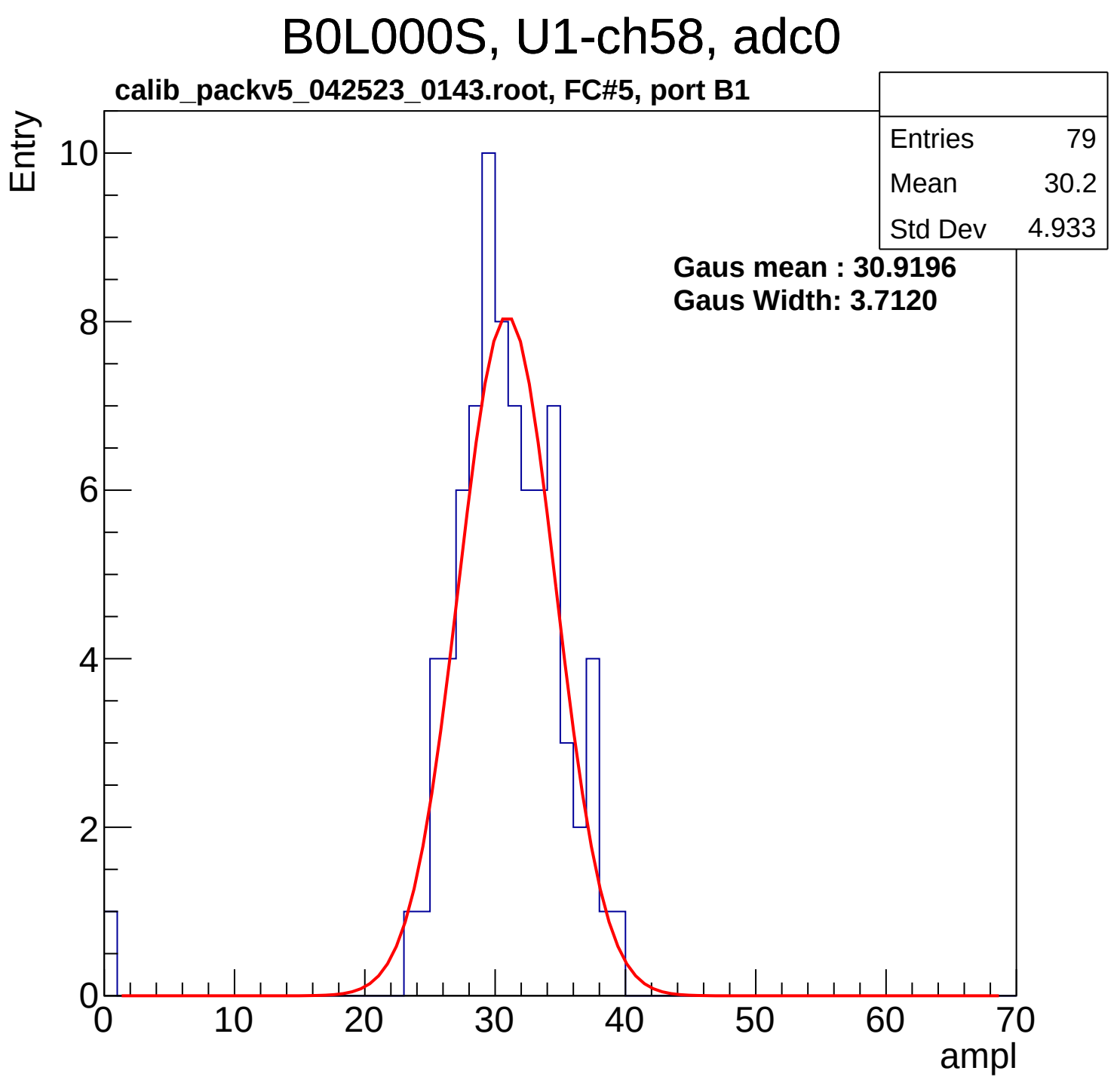
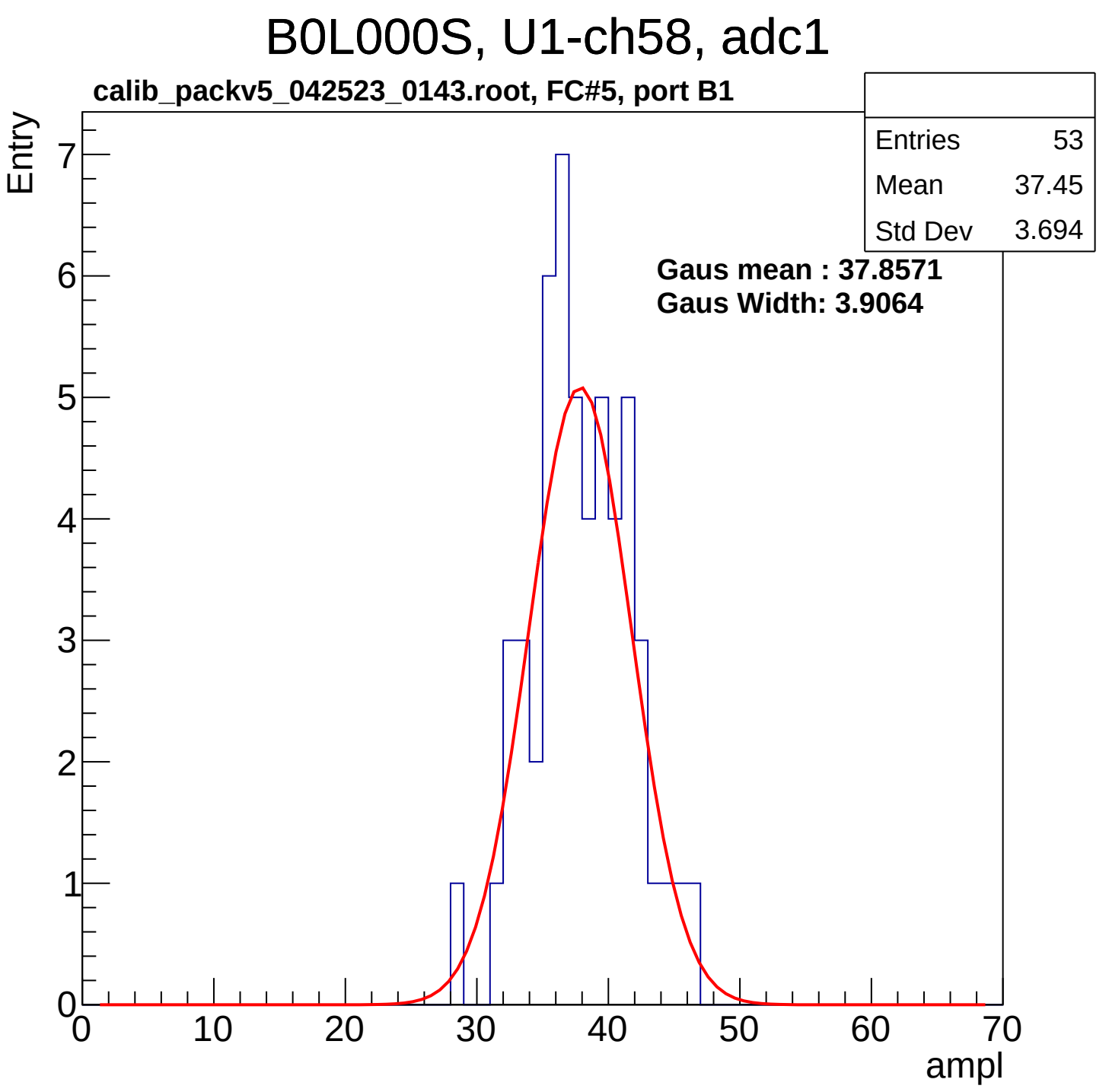
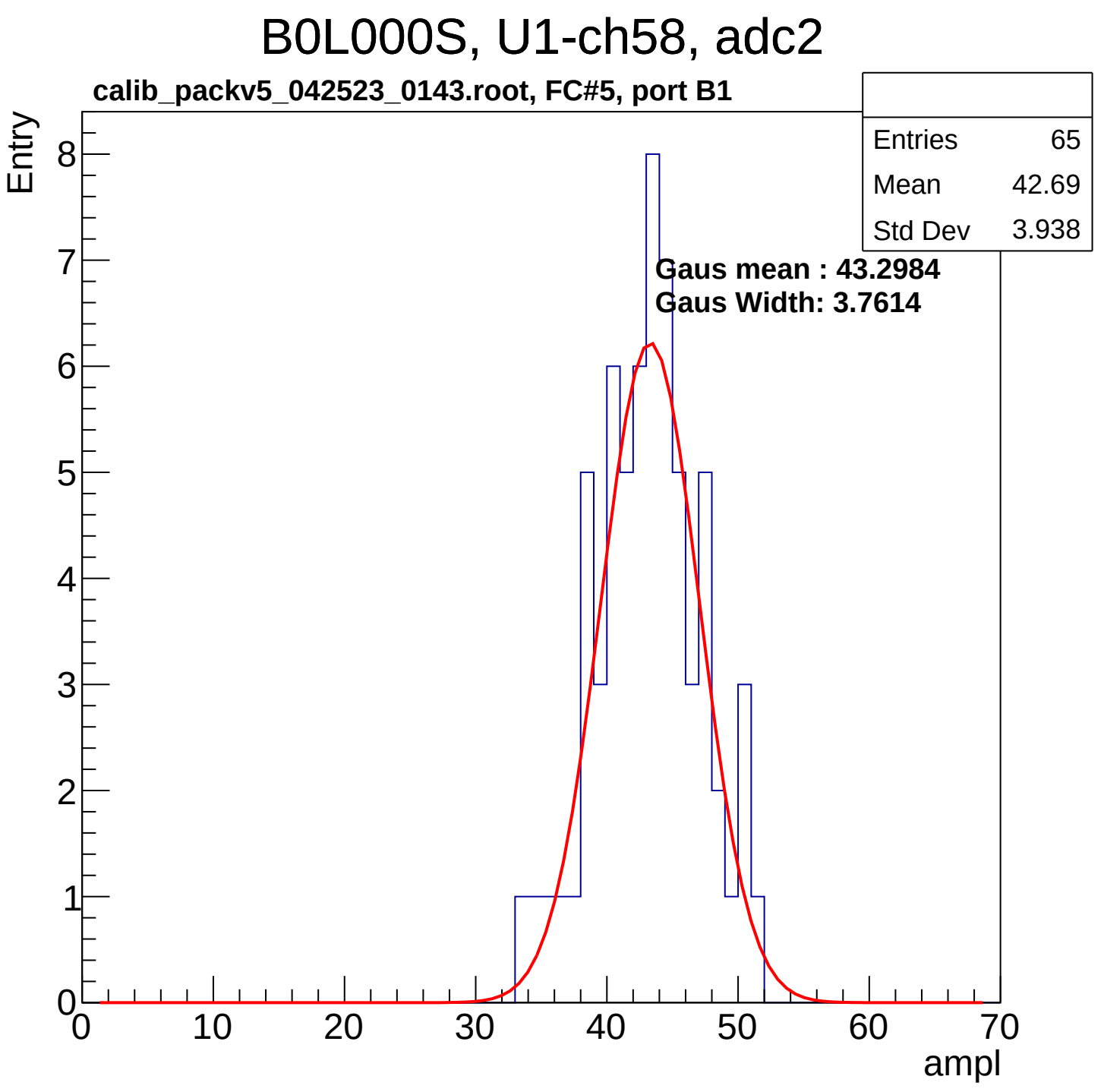
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

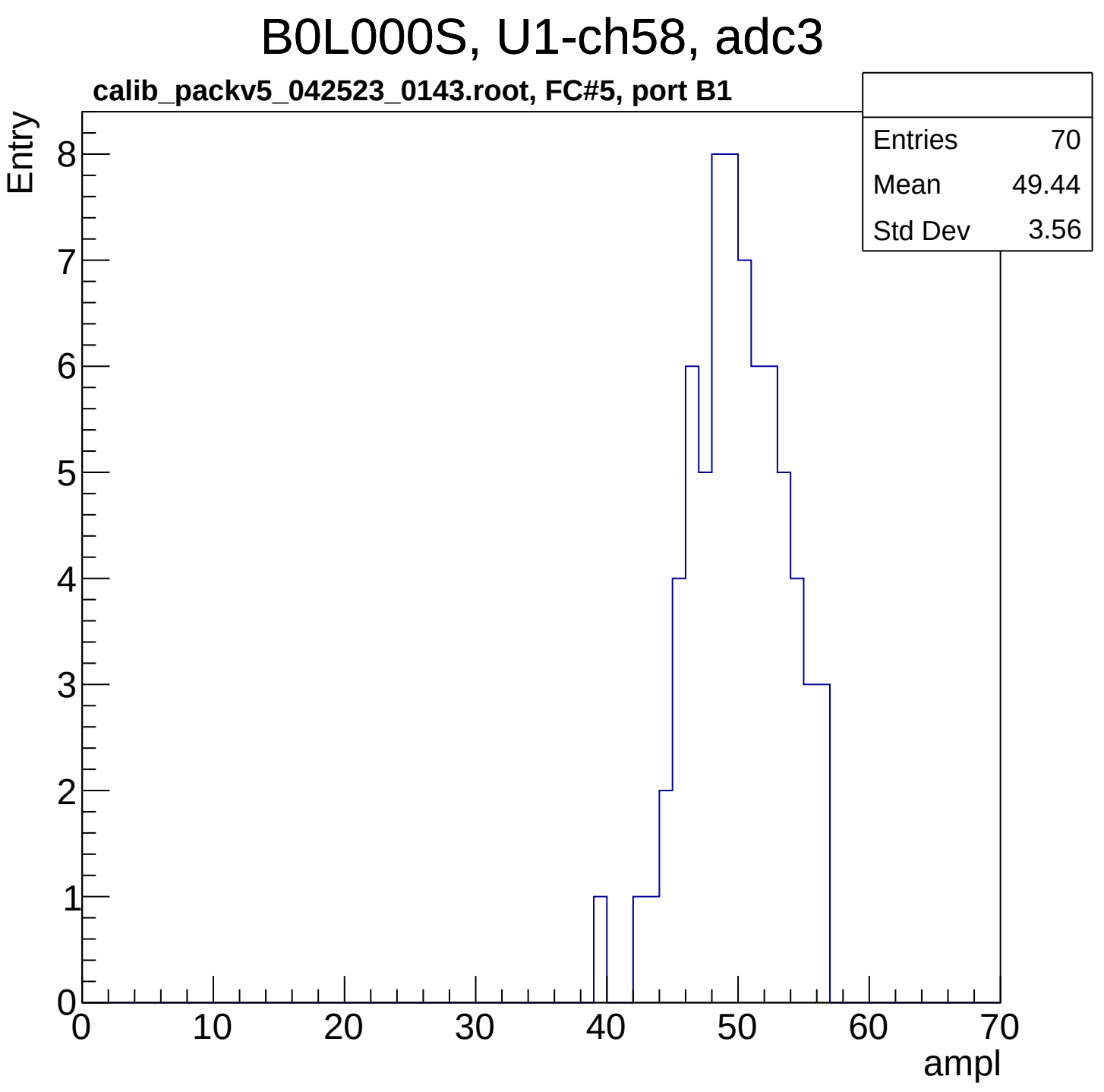

B0L000S, U1-ch57, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.2
Std Dev 2.638
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

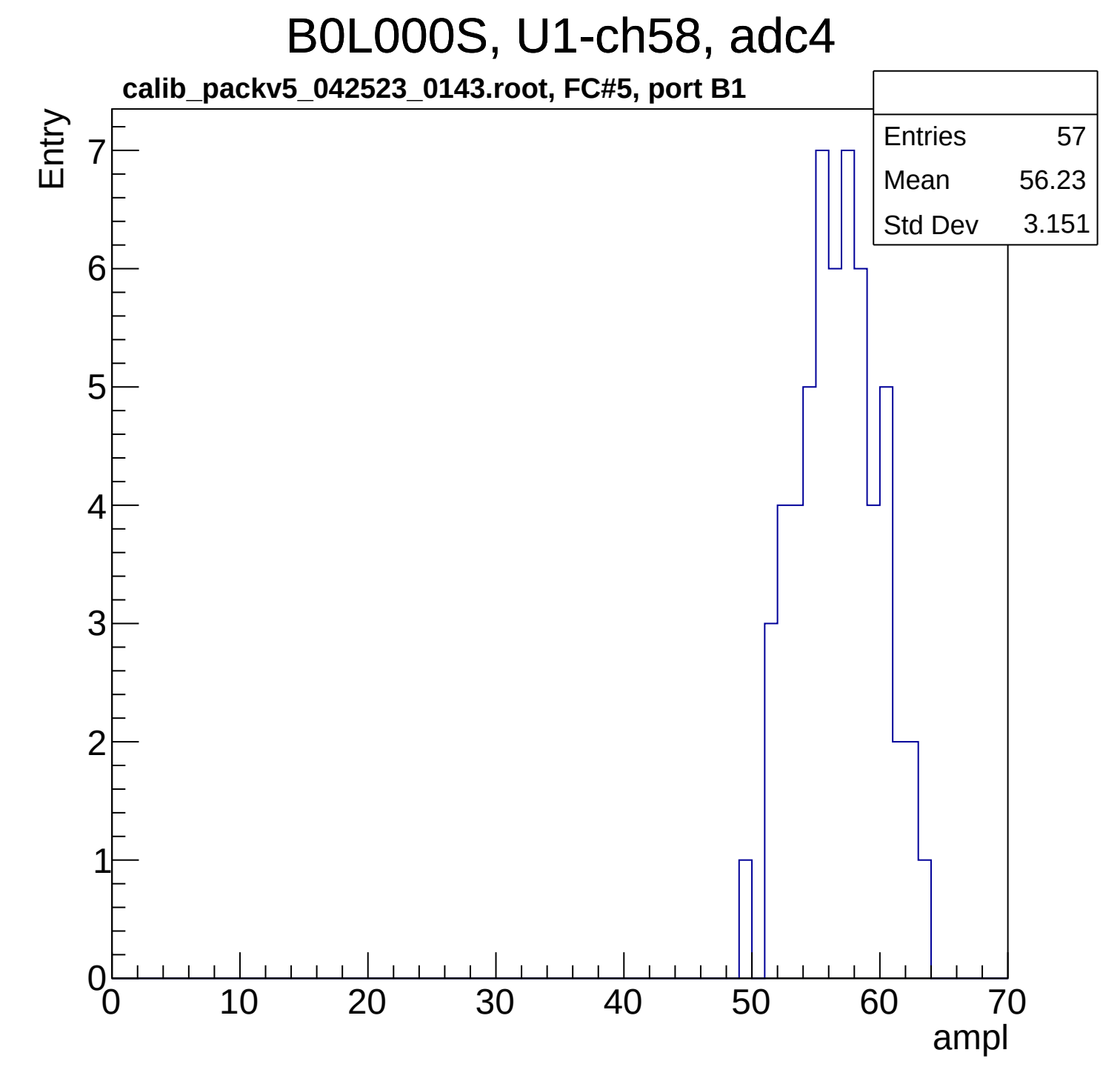

B0L000S, U1-ch57, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

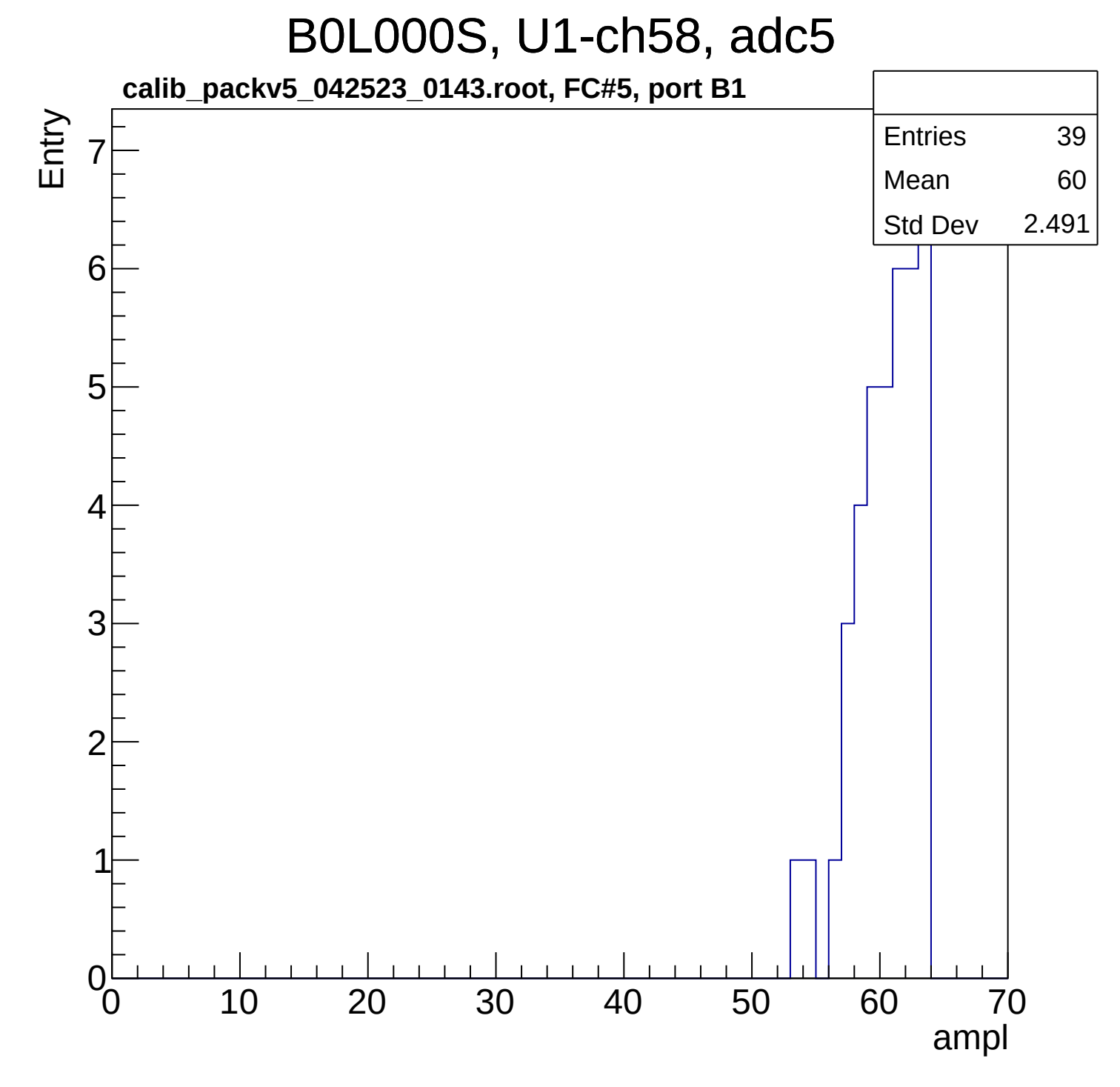

B0L000S, U1-ch58, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 30.2
Std Dev 4.933
Gaus mean : 30.9196
Gaus Width: 3.7120
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

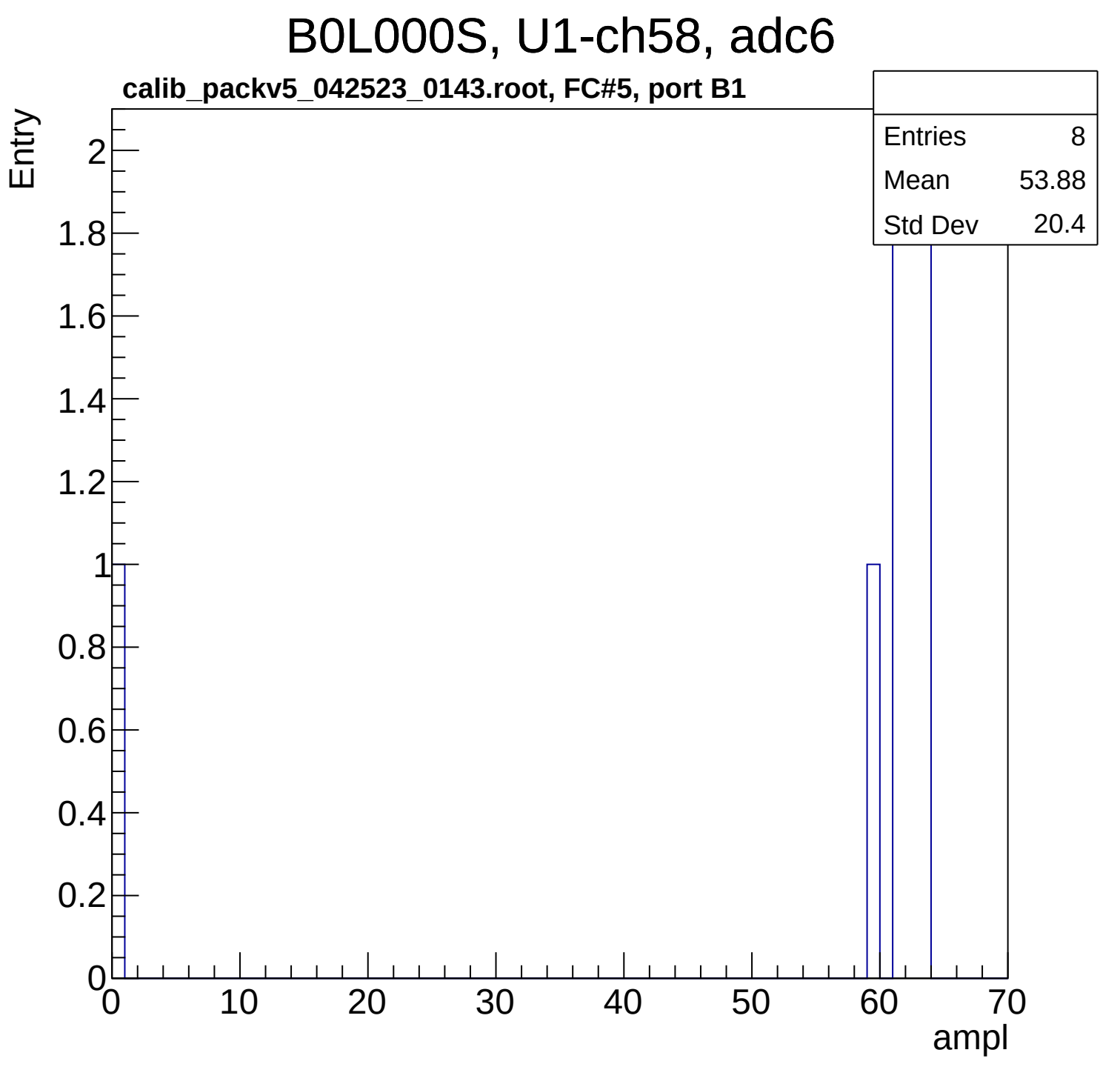

B0L000S, U1-ch58, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 37.45
Std Dev 3.694
Gaus mean : 37.8571
Gaus Width: 3.9064
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

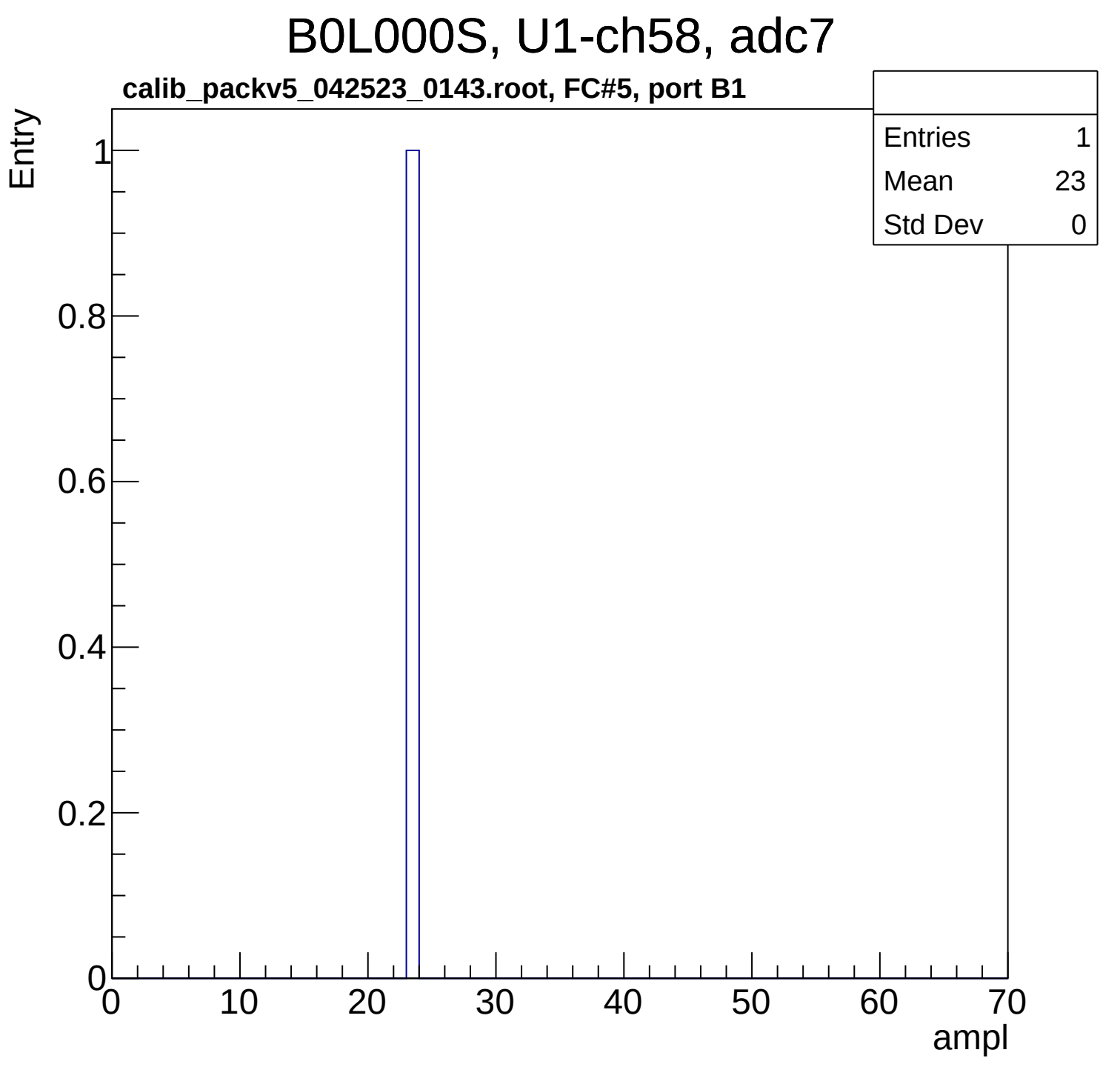

B0L000S, U1-ch58, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.69
Std Dev 3.938
Gaus mean : 43.2984
Gaus Width: 3.7614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

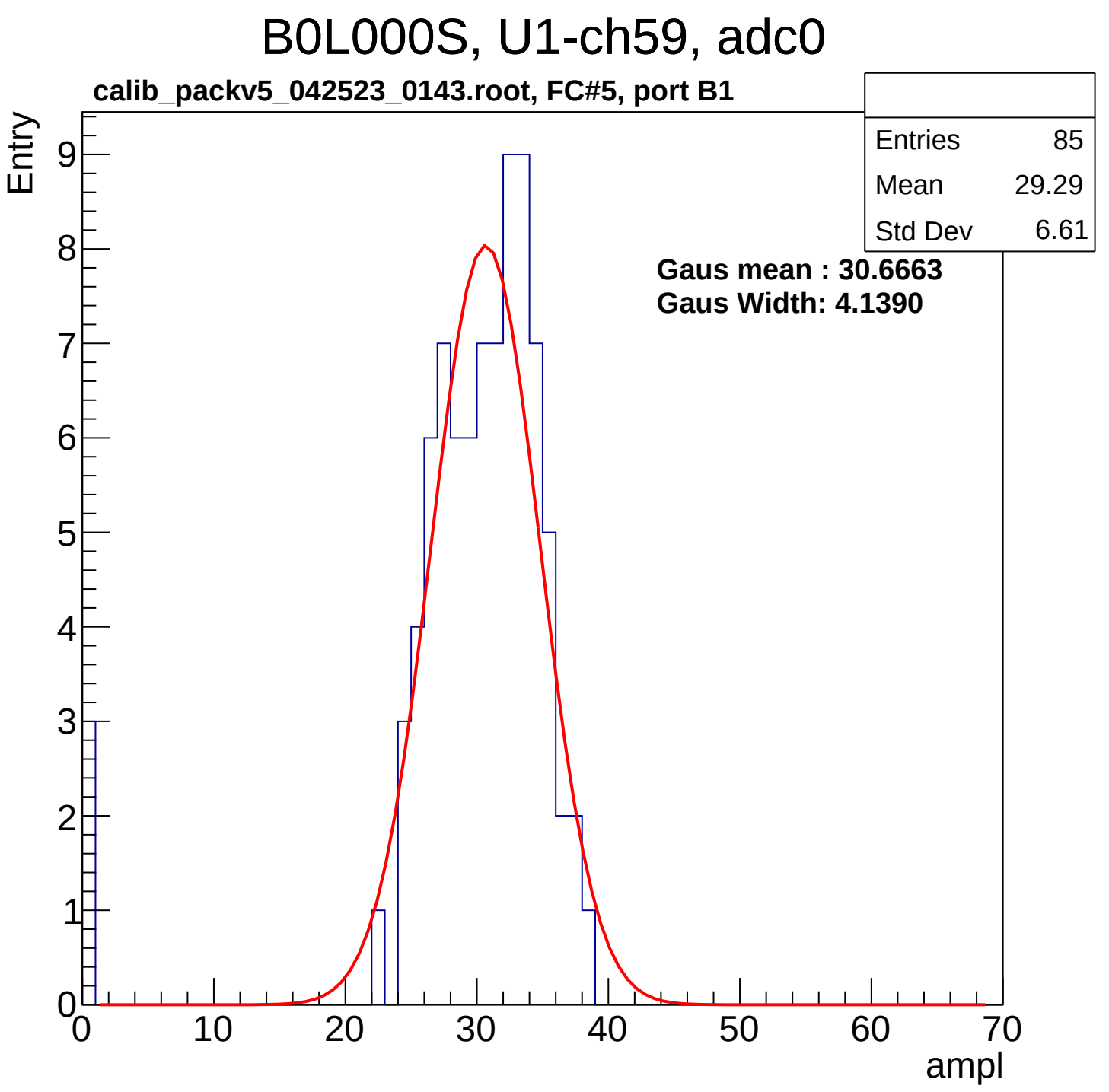

B0L000S, U1-ch58, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.44
Std Dev 3.56
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

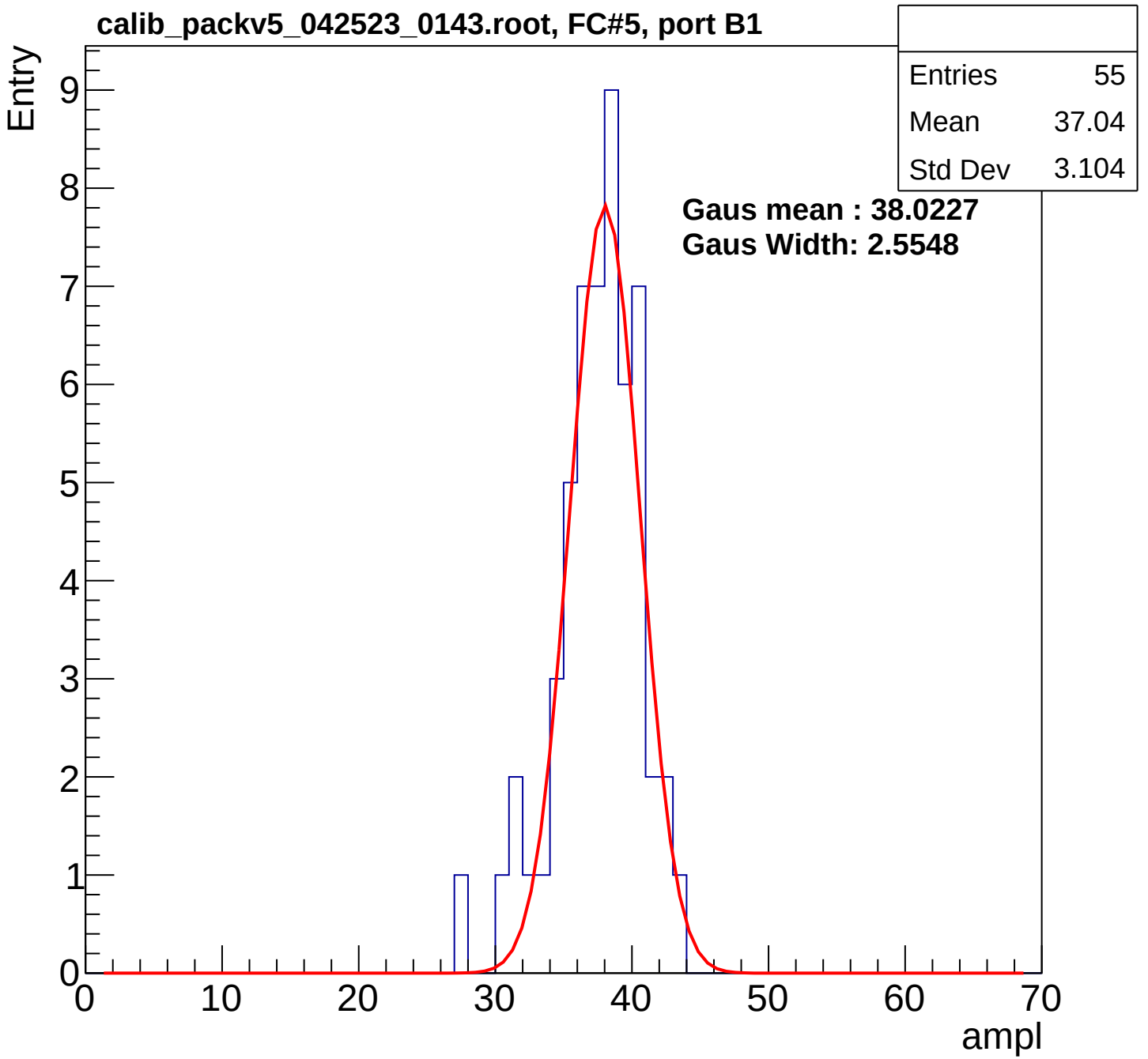

B0L000S, U1-ch58, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 56.23
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

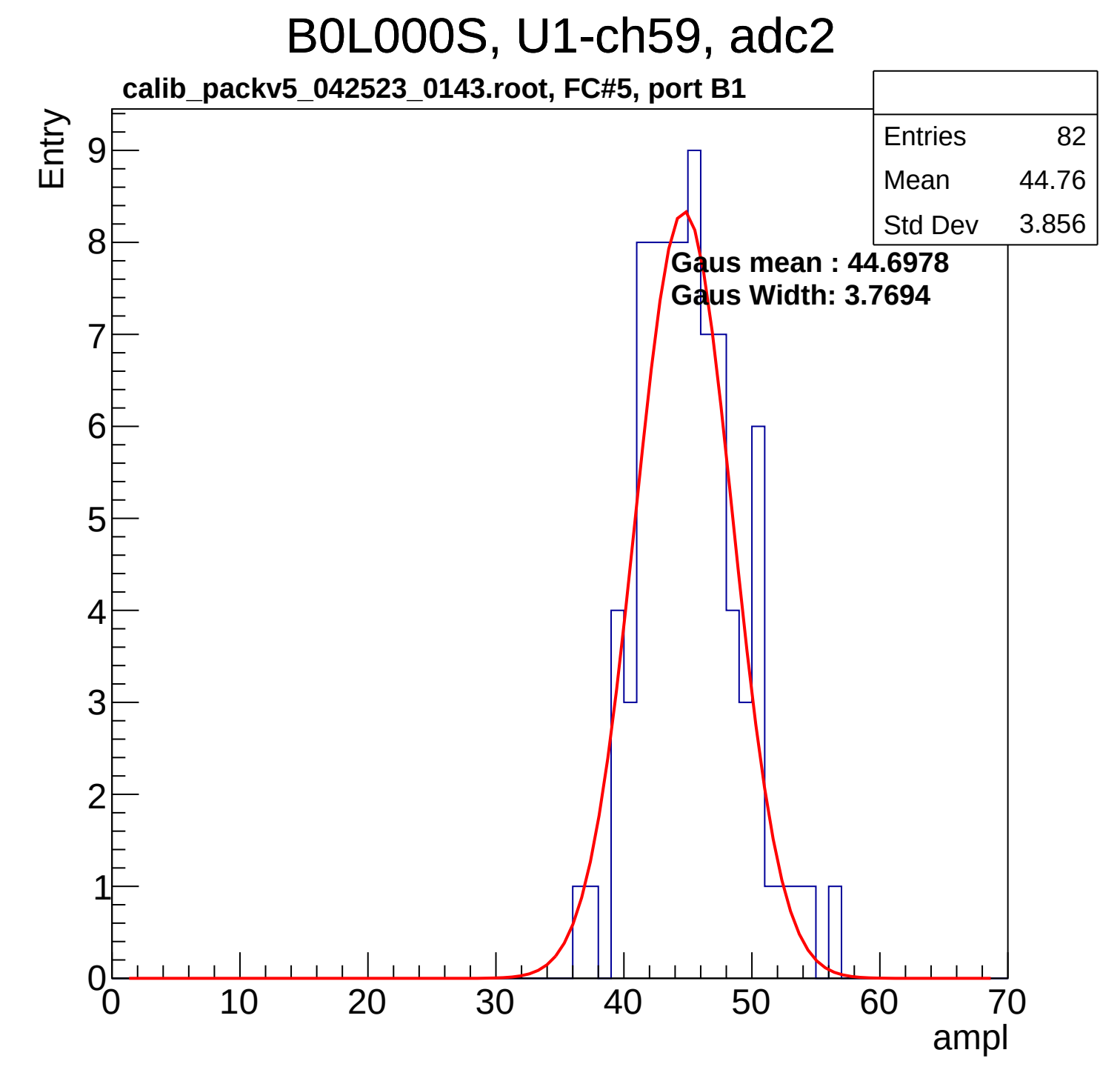

B0L000S, U1-ch58, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 60
Std Dev 2.491
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

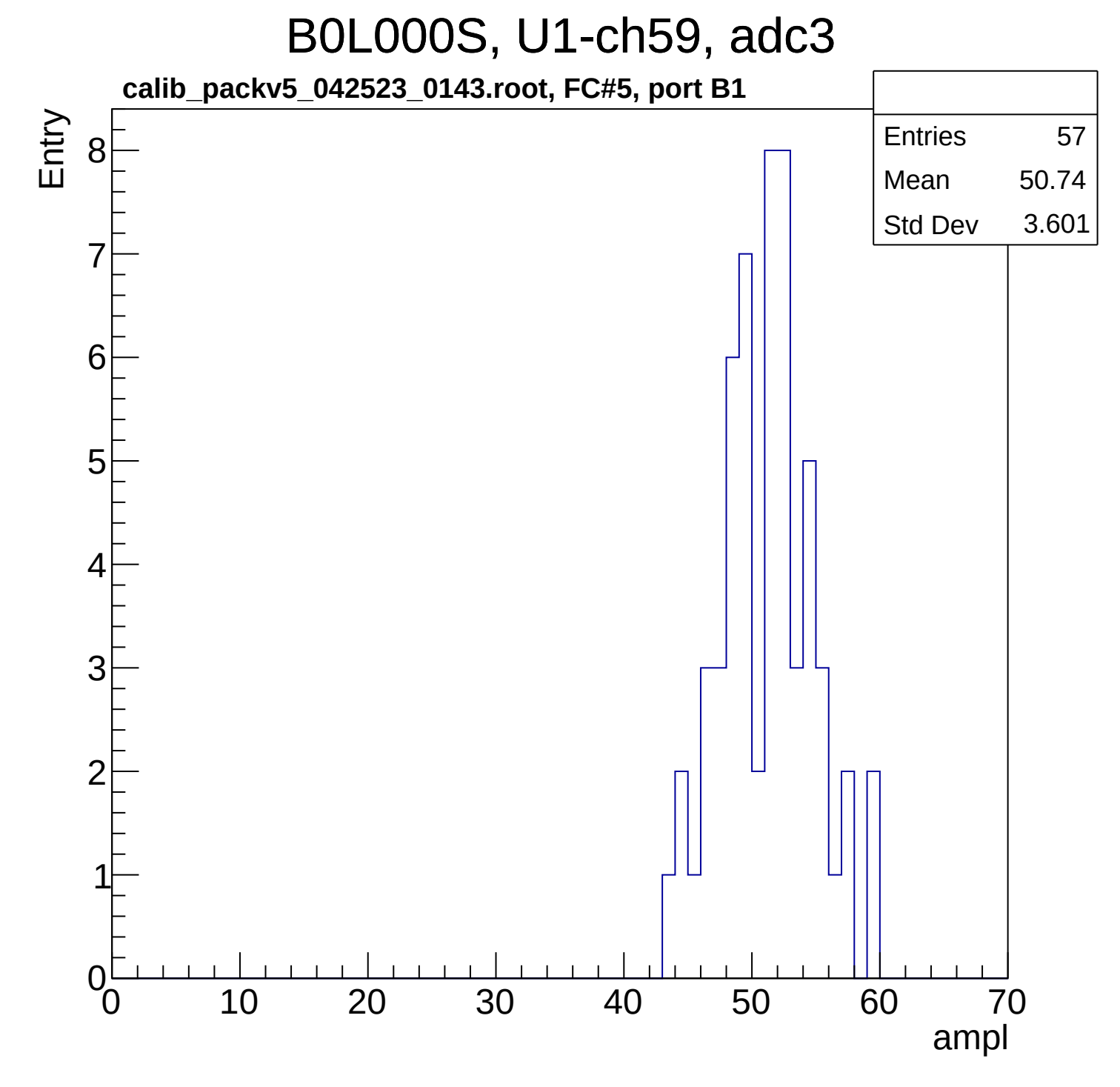

B0L000S, U1-ch58, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 53.88
Std Dev 20.4
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

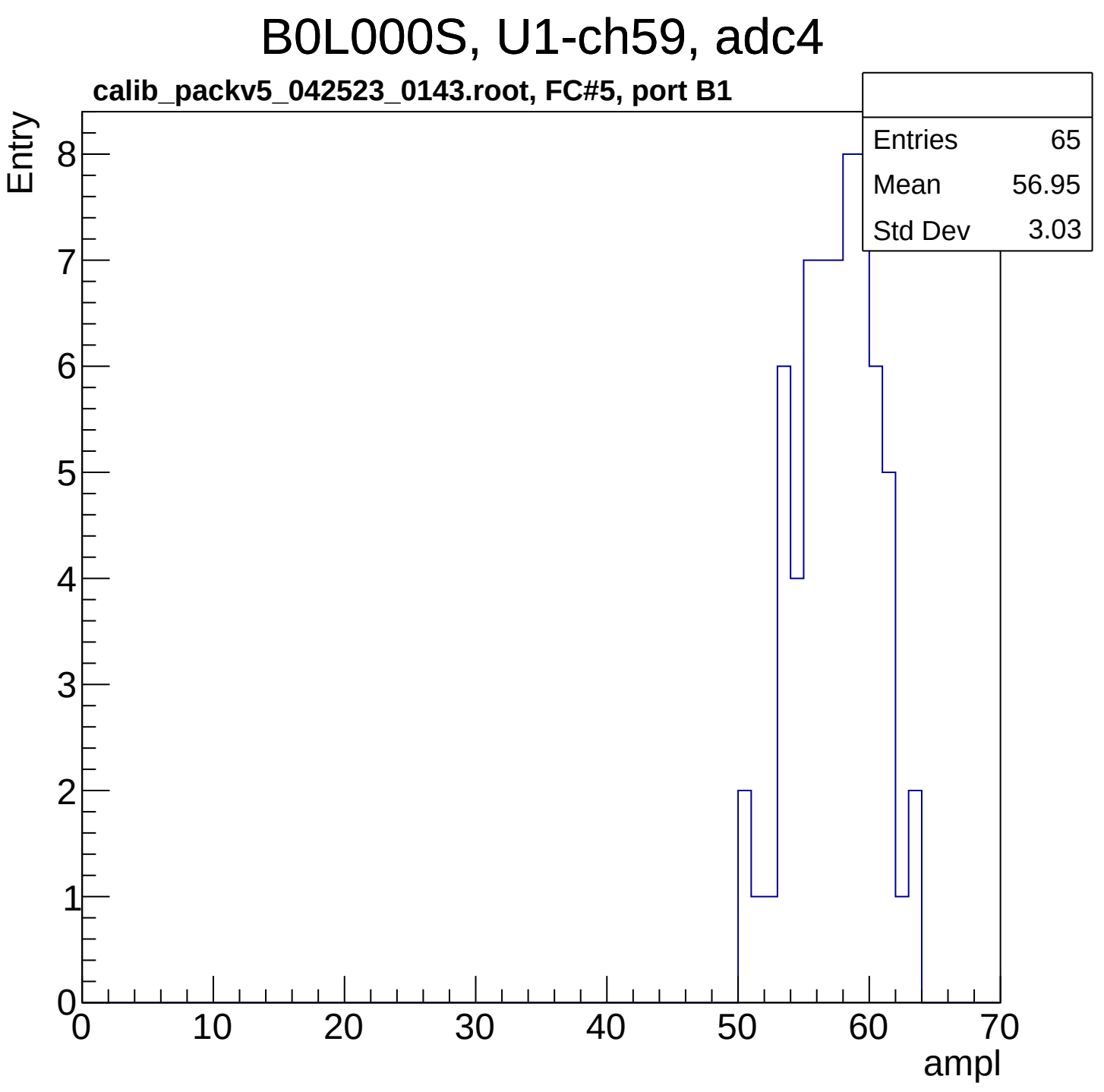

B0L000S, U1-ch58, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

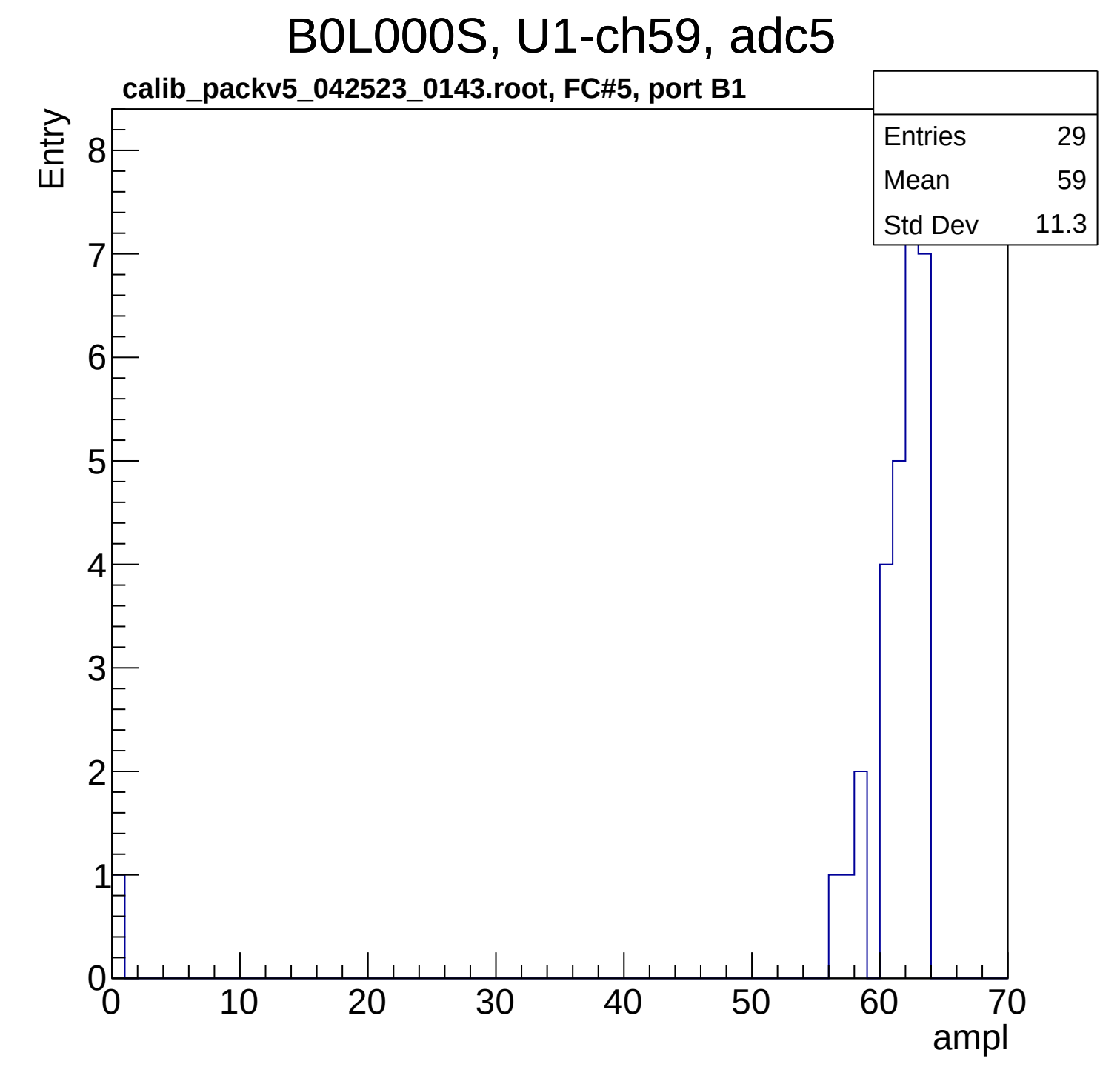

B0L000S, U1-ch59, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 29.29
Std Dev 6.61
Gaus mean : 30.6663
Gaus Width: 4.1390
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

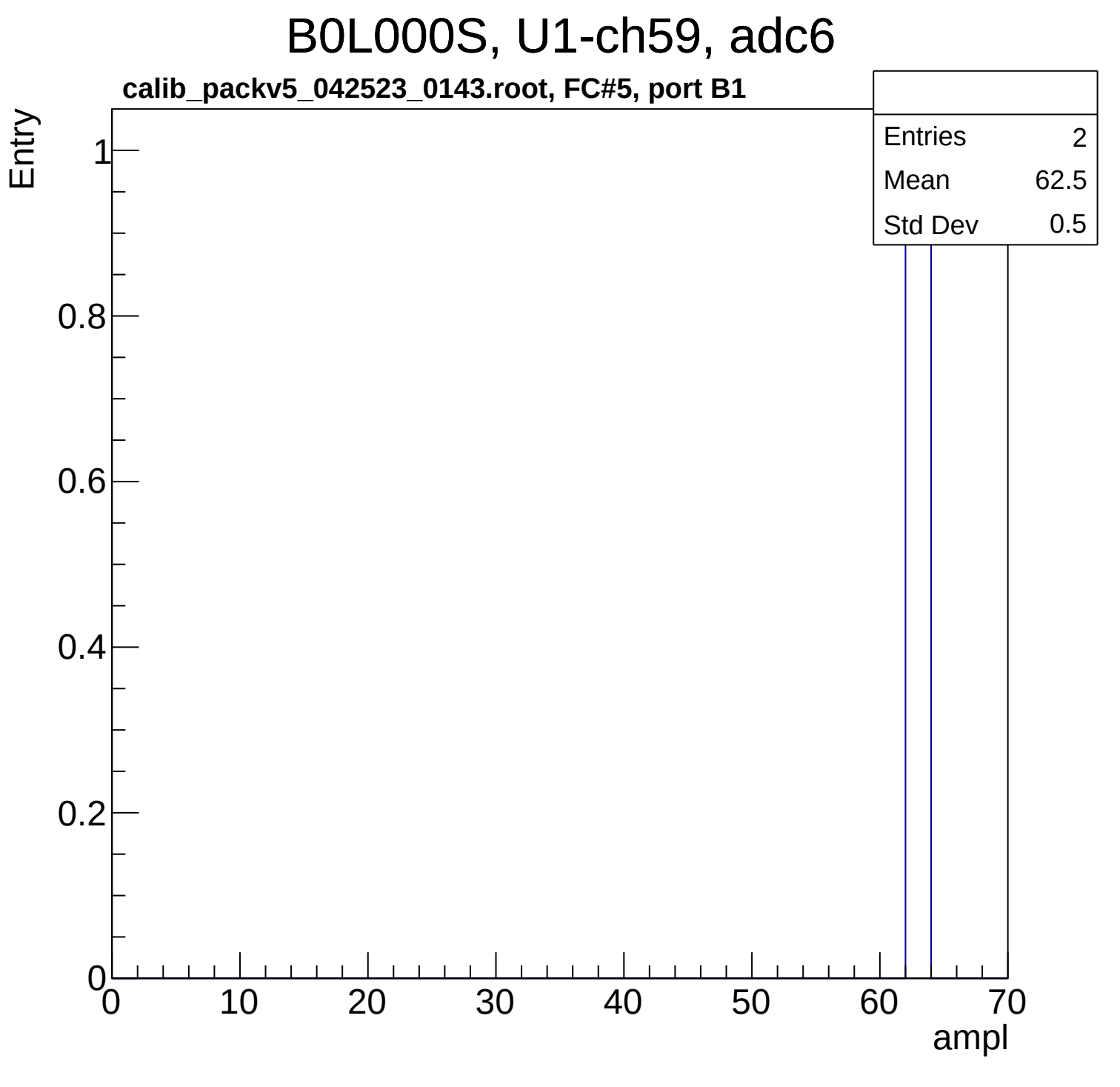

B0L000S, U1-ch59, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 37.04
Std Dev 3.104
Gaus mean : 38.0227
Gaus Width: 2.5548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

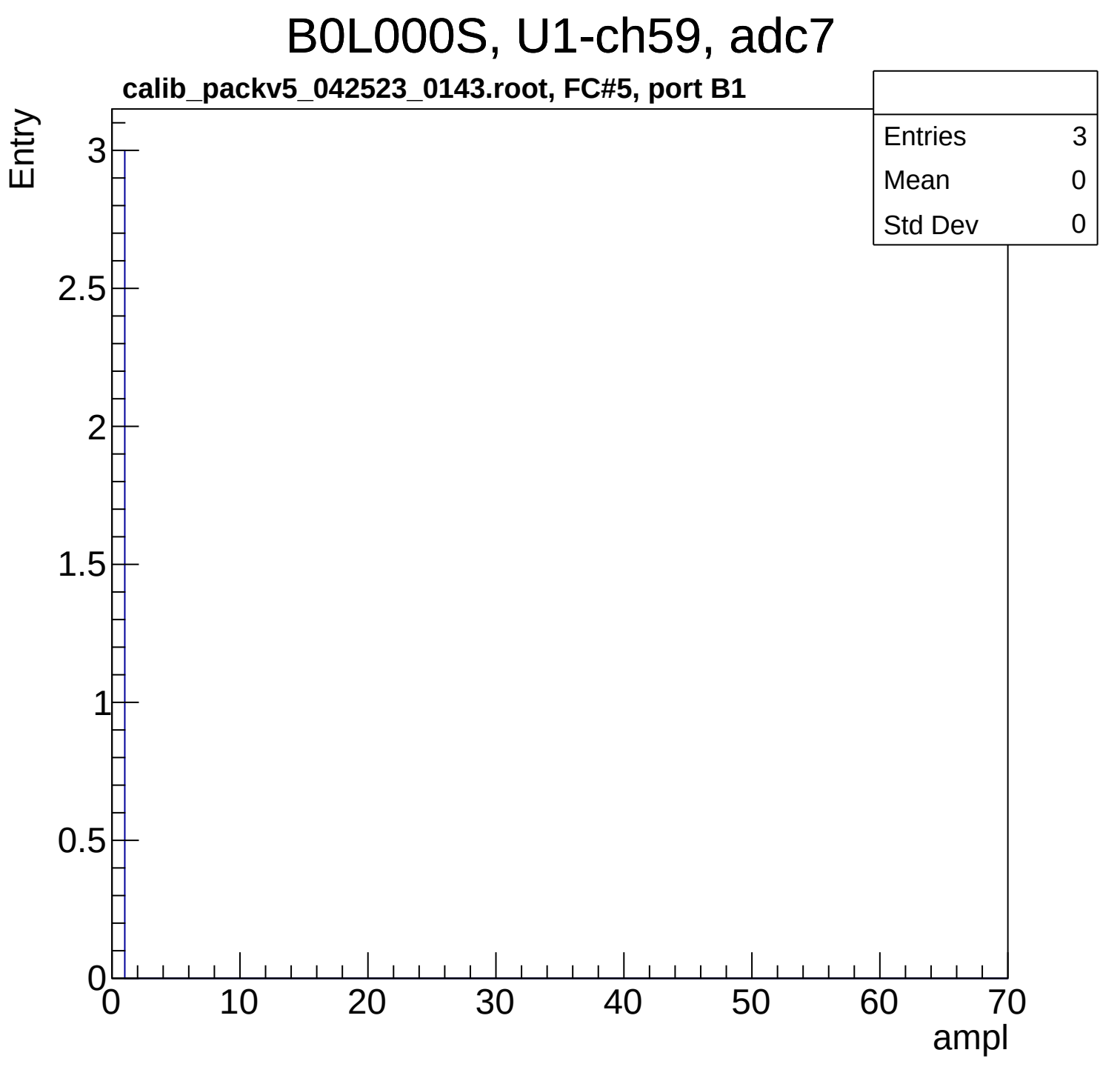

B0L000S, U1-ch59, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 44.76
Std Dev 3.856
Gaus mean : 44.6978
Gaus Width: 3.7694
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

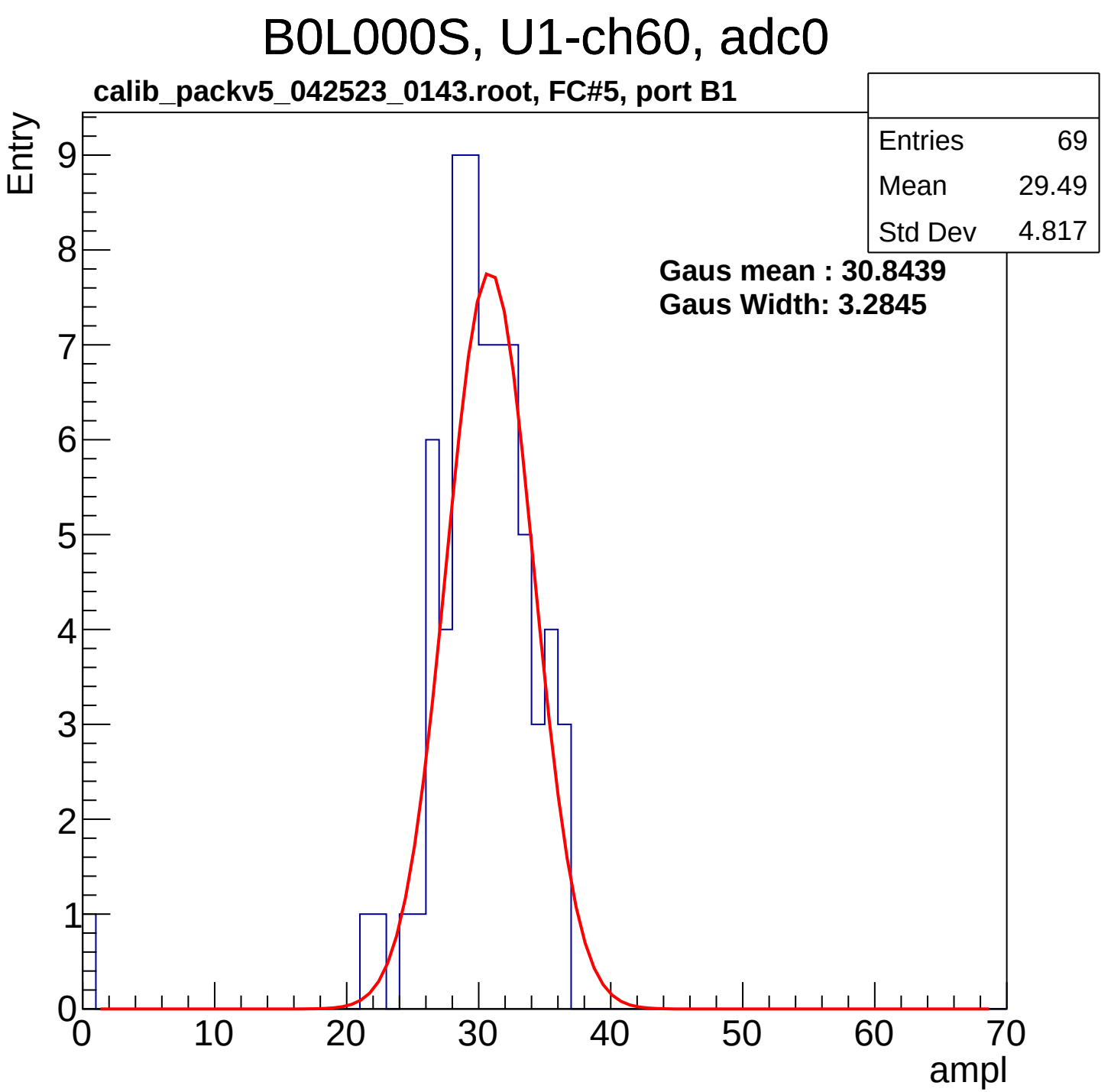

B0L000S, U1-ch59, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 50.74
Std Dev 3.601
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

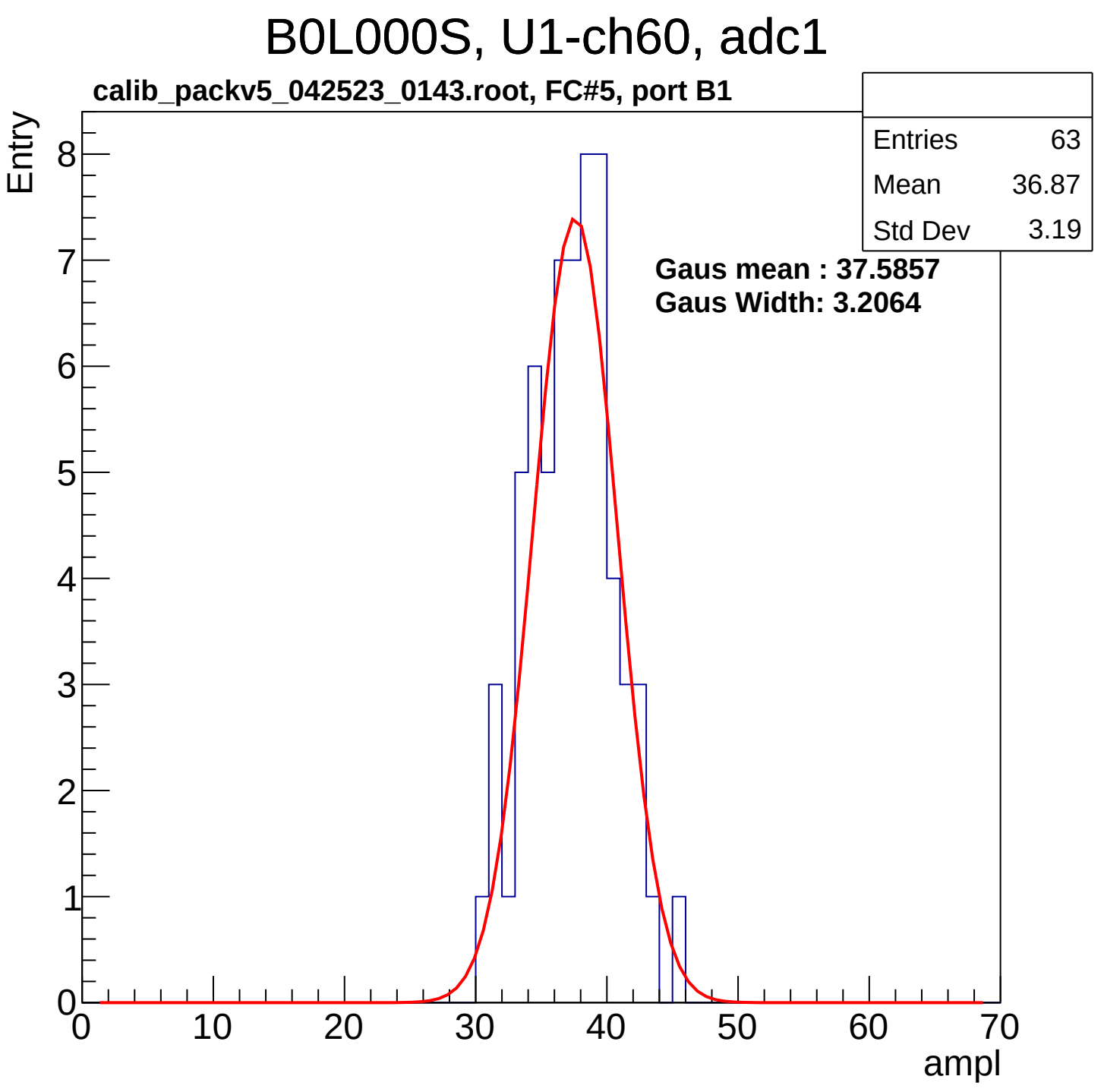

B0L000S, U1-ch59, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 56.95
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

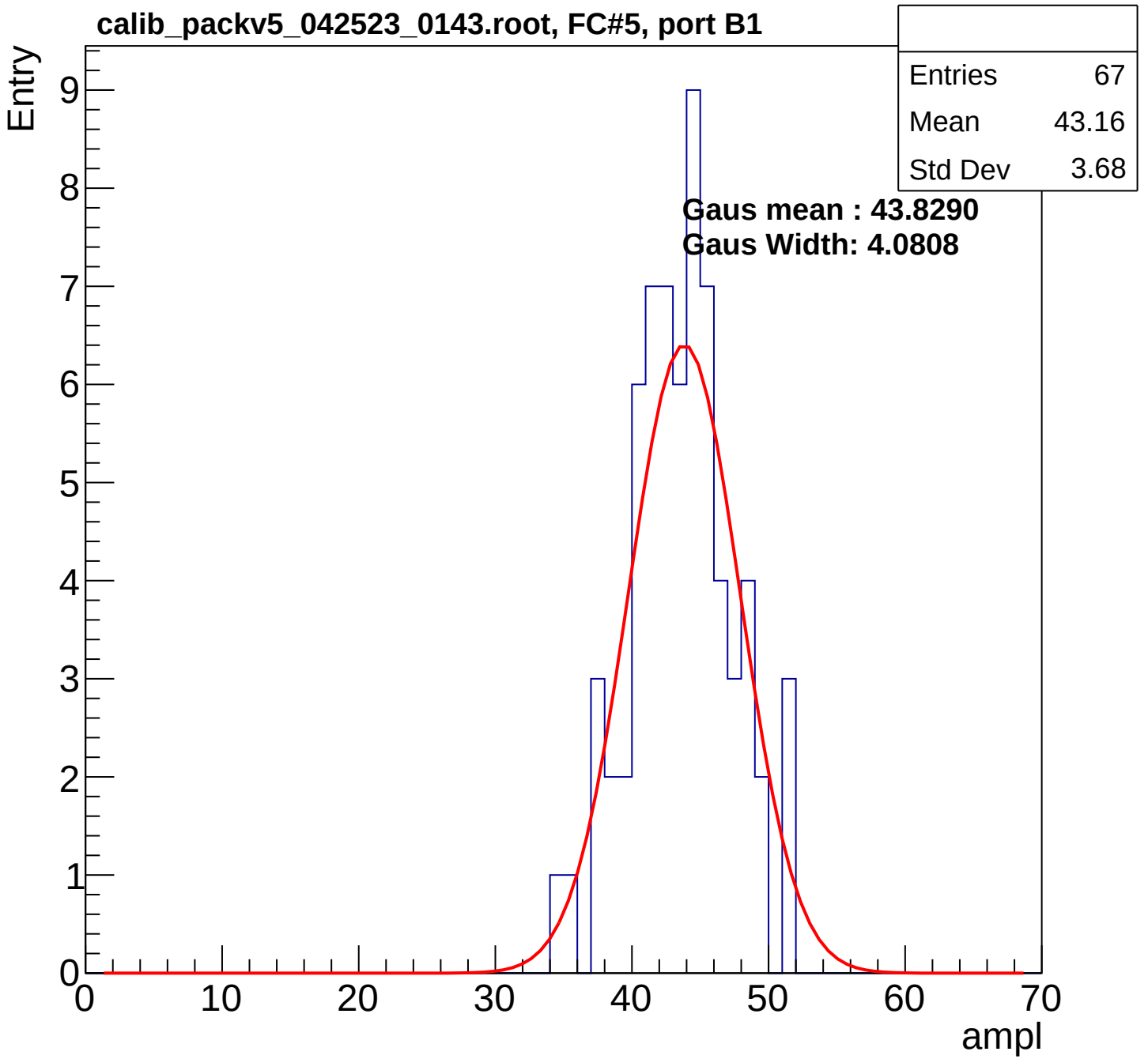

B0L000S, U1-ch59, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 59
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

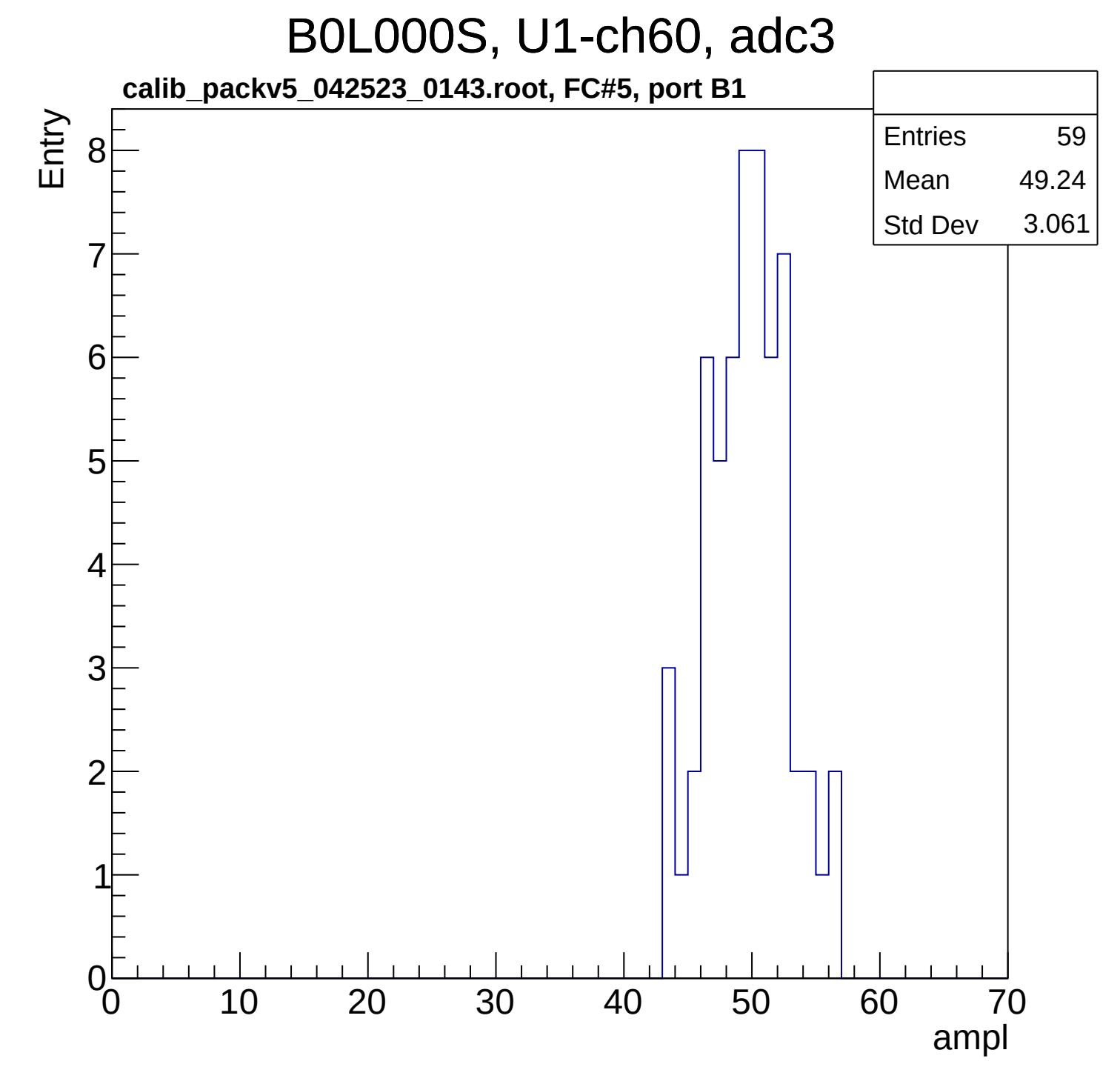

B0L000S, U1-ch59, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch59, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch60, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.49
Std Dev 4.817
Gaus mean : 30.8439
Gaus Width: 3.2845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch60, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.87
Std Dev 3.19
Gaus mean : 37.5857
Gaus Width: 3.2064
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch60, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.16
Std Dev 3.68
Gaus mean : 43.8290
Gaus Width: 4.0808
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch60, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 49.24
Std Dev 3.061
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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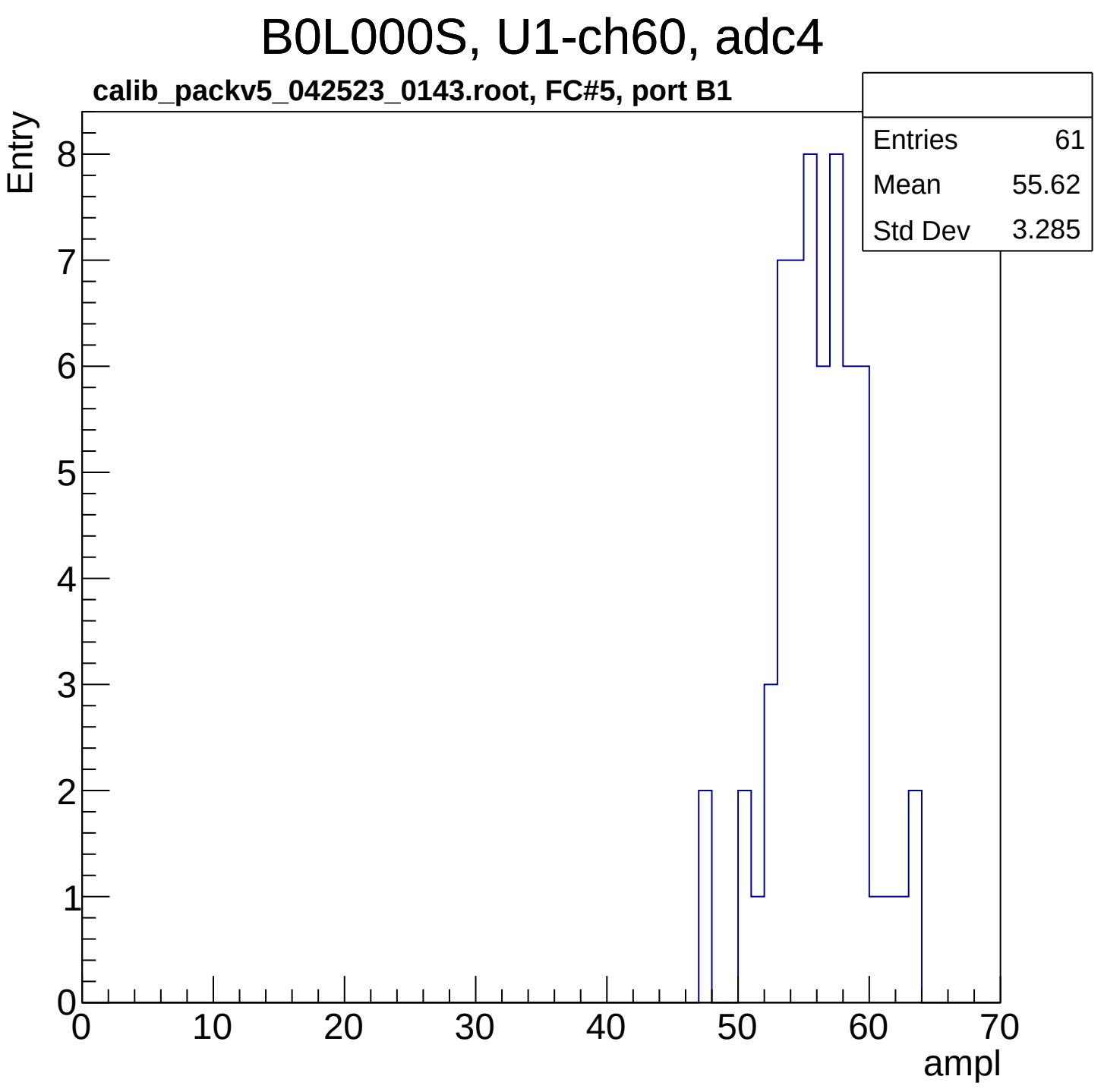
60
70


B0L000S, U1-ch60, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.62
Std Dev 3.285
Entry
ampl
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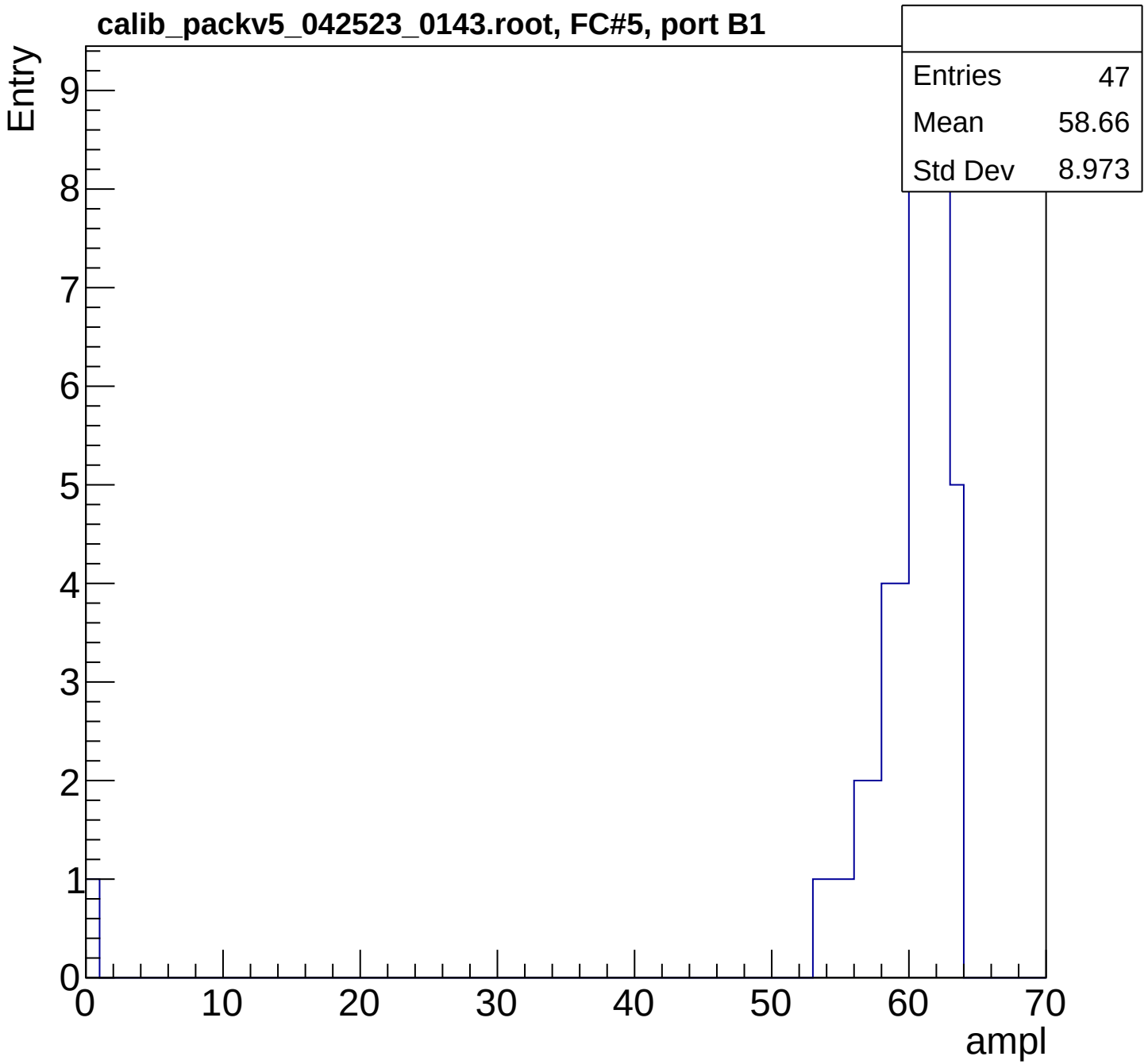
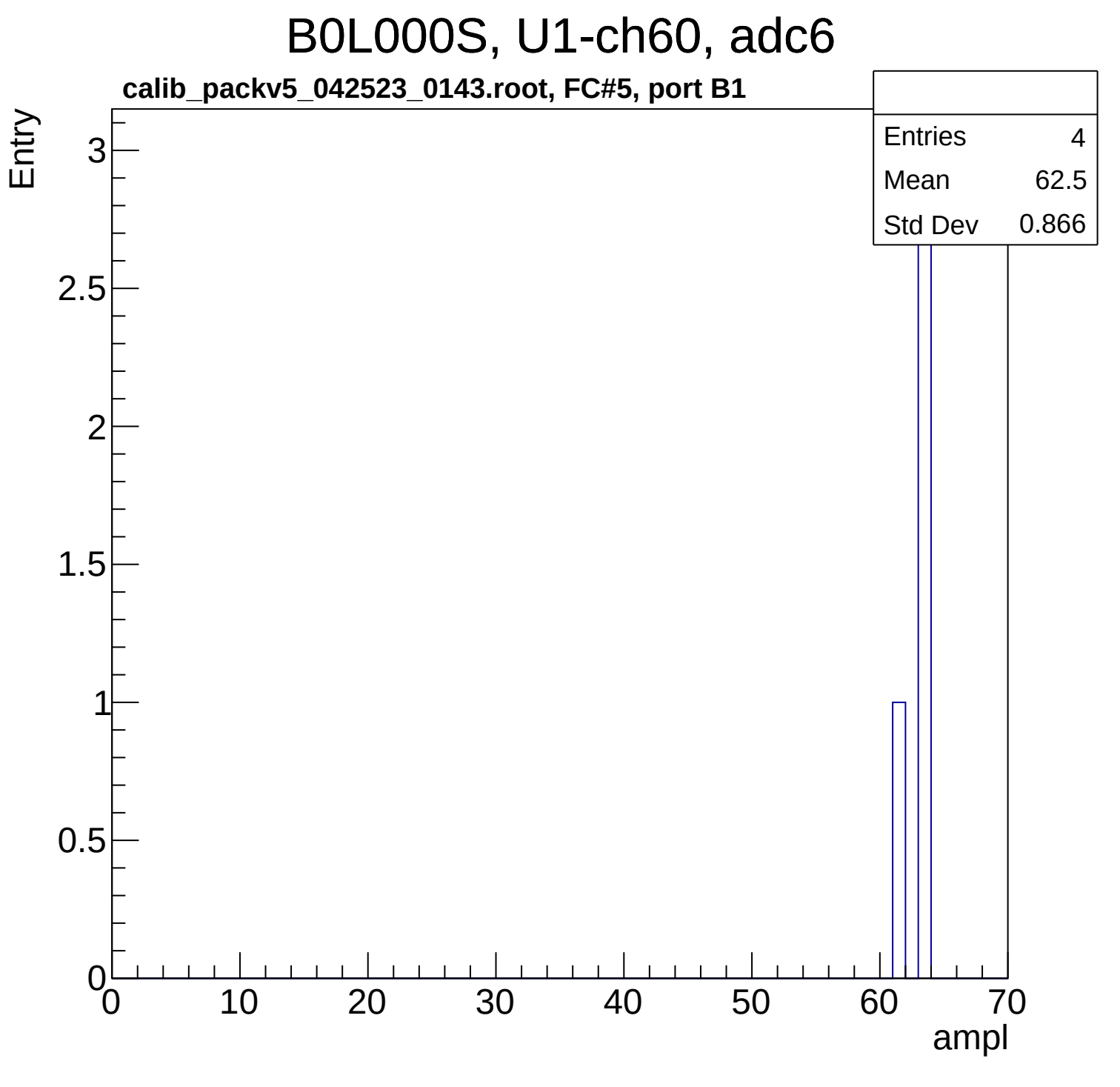
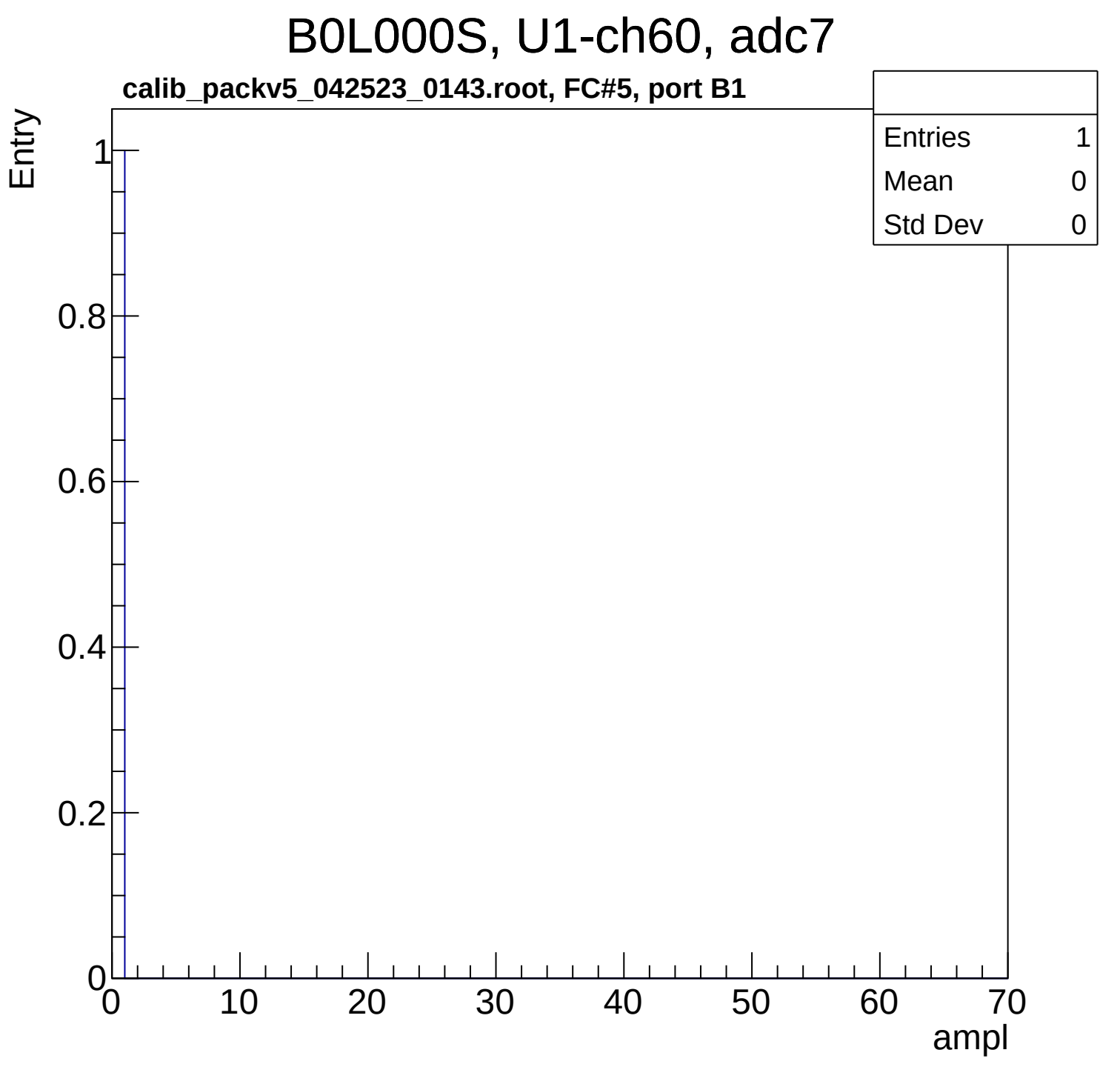
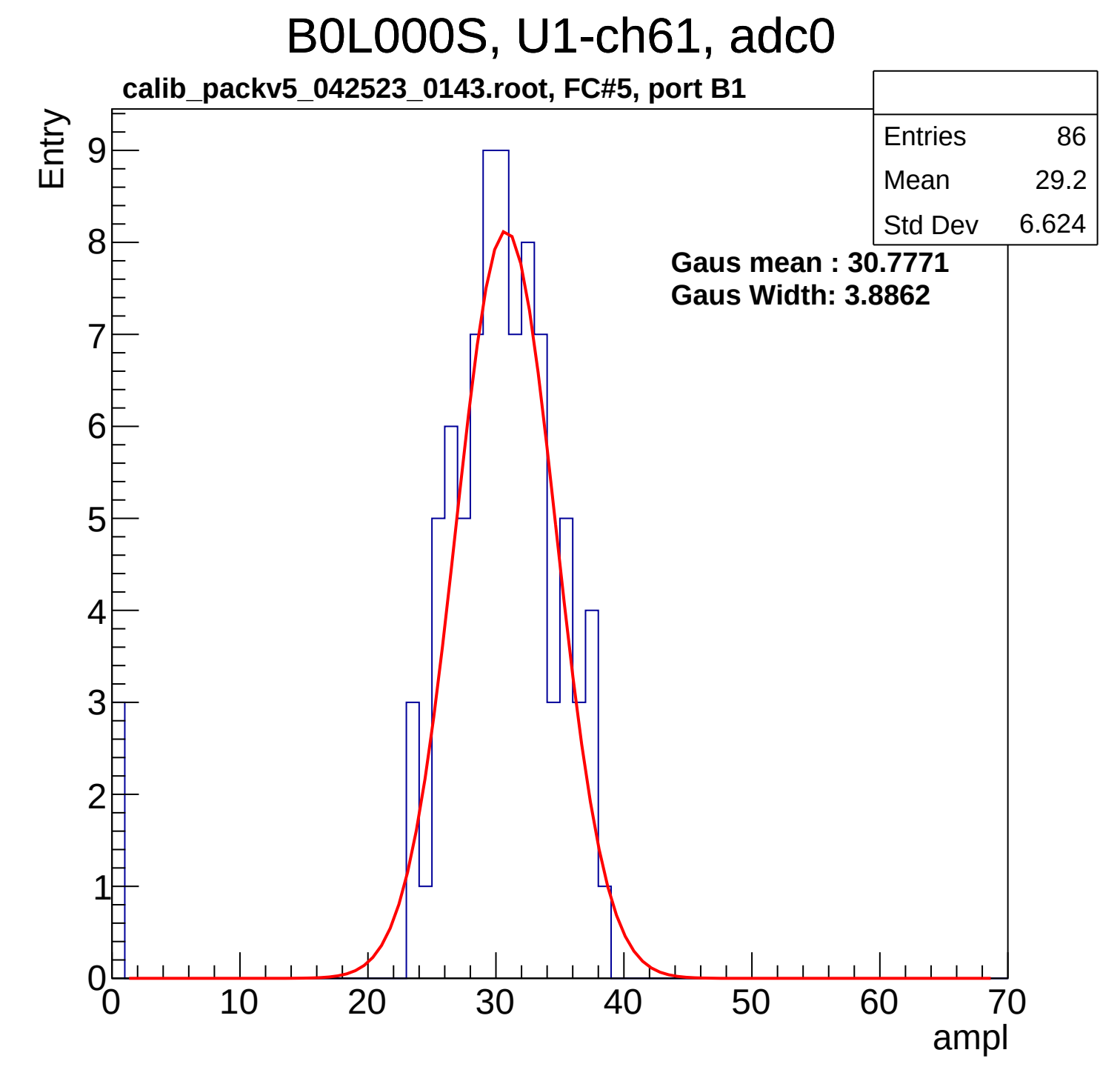
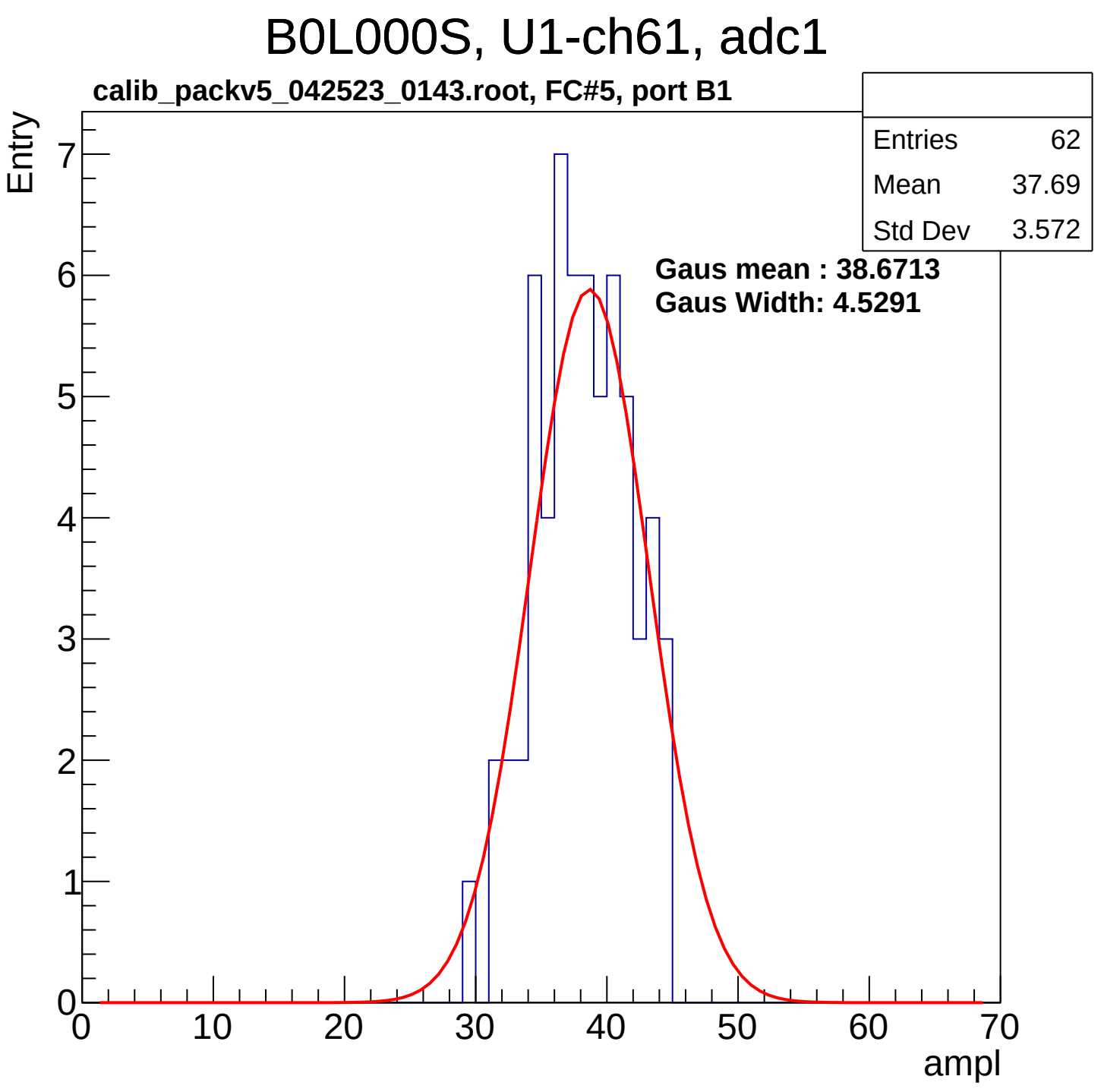
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

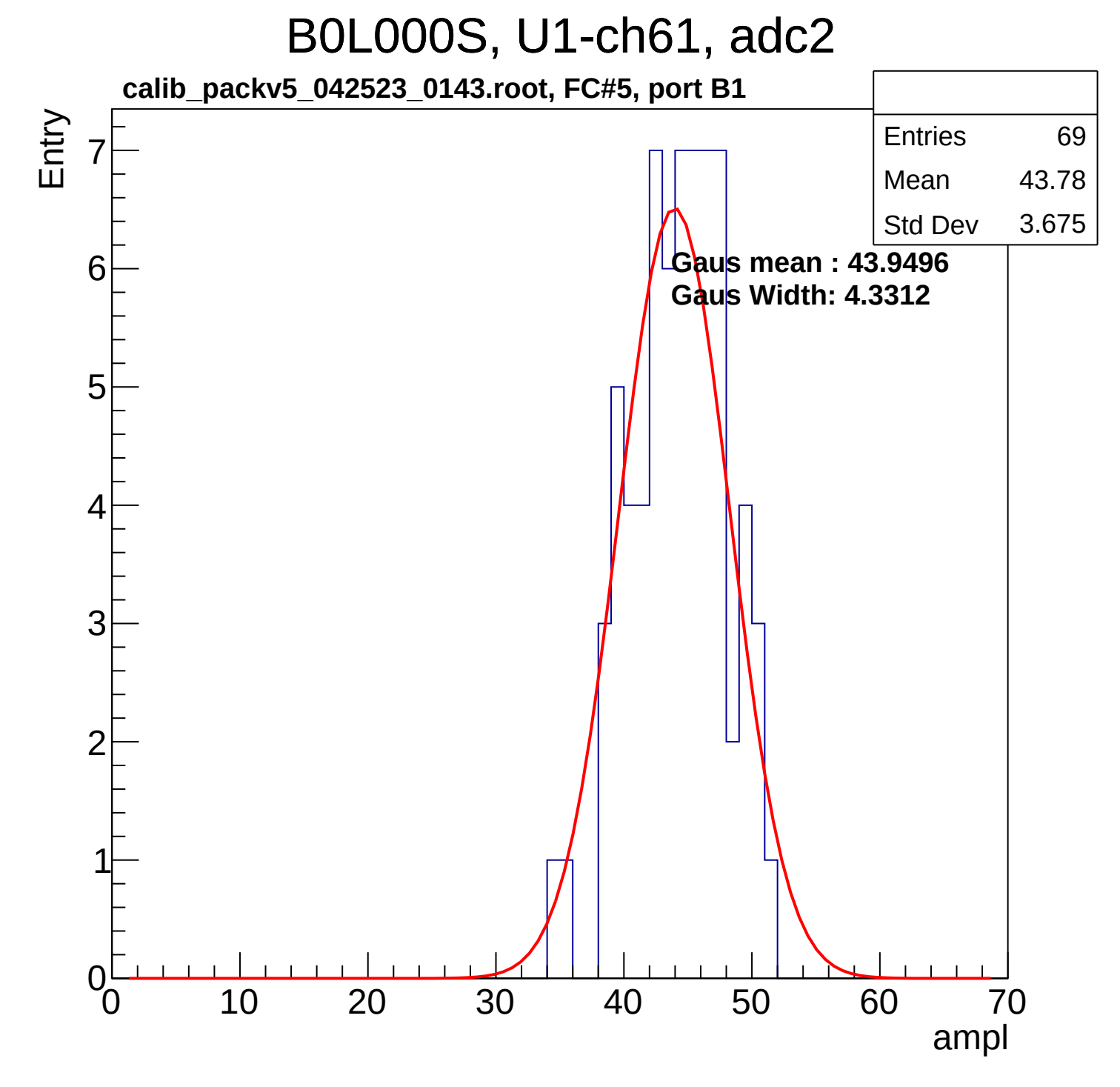

B0L000S, U1-ch60, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.66
Std Dev 8.973
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

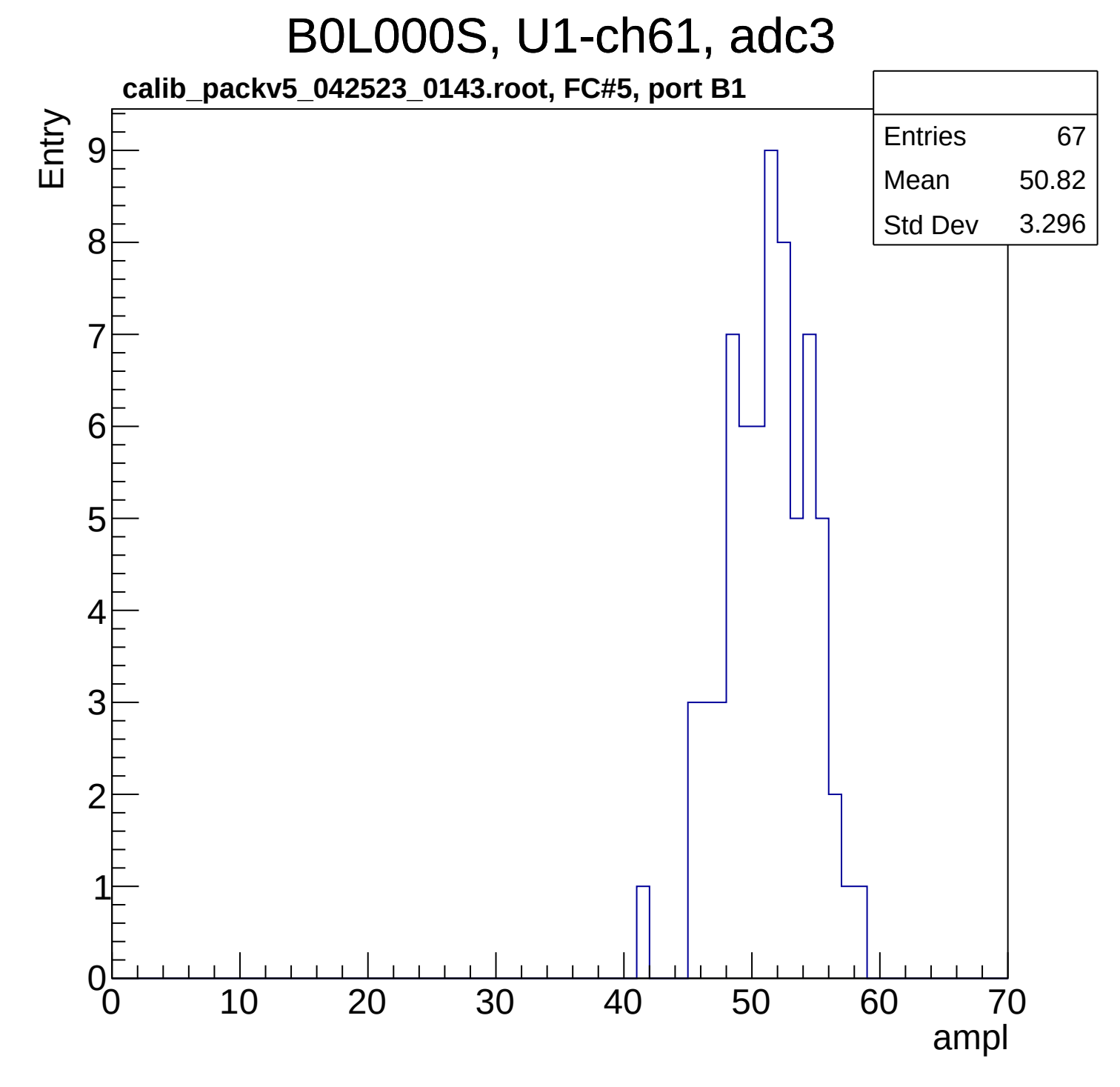

B0L000S, U1-ch60, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

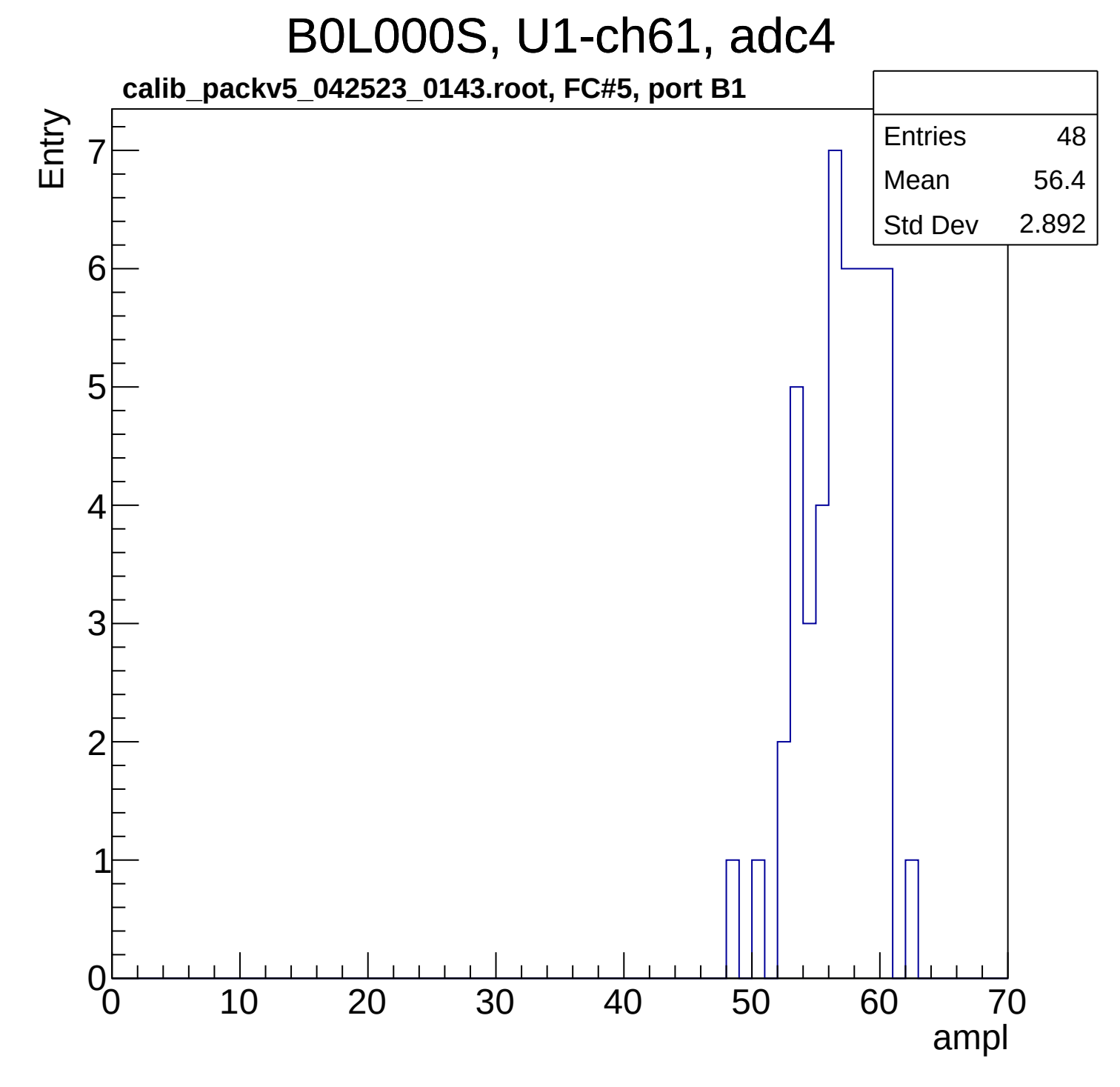

B0L000S, U1-ch60, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

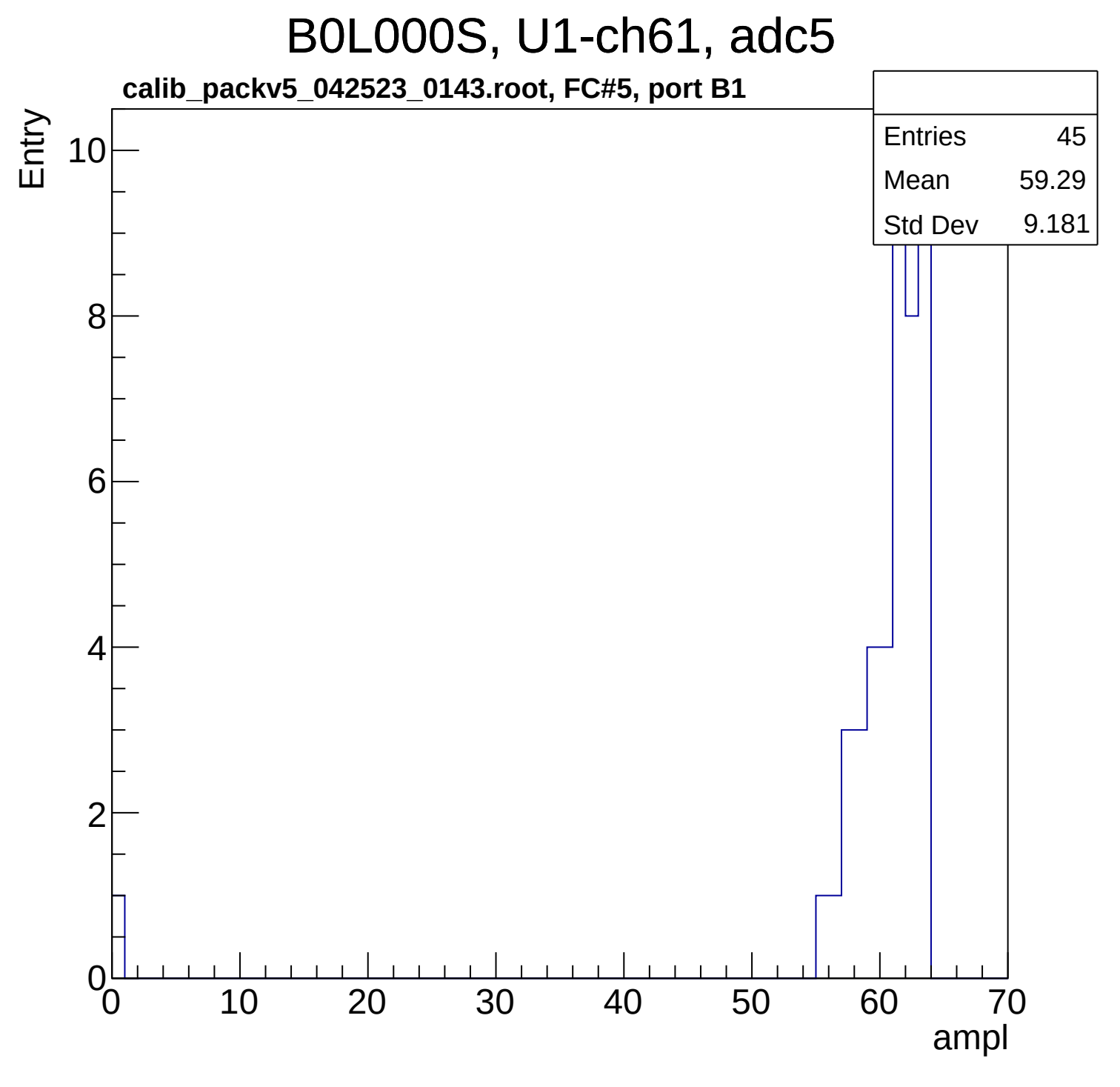

B0L000S, U1-ch61, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 29.2
Std Dev 6.624
Gaus mean : 30.7771
Gaus Width: 3.8862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

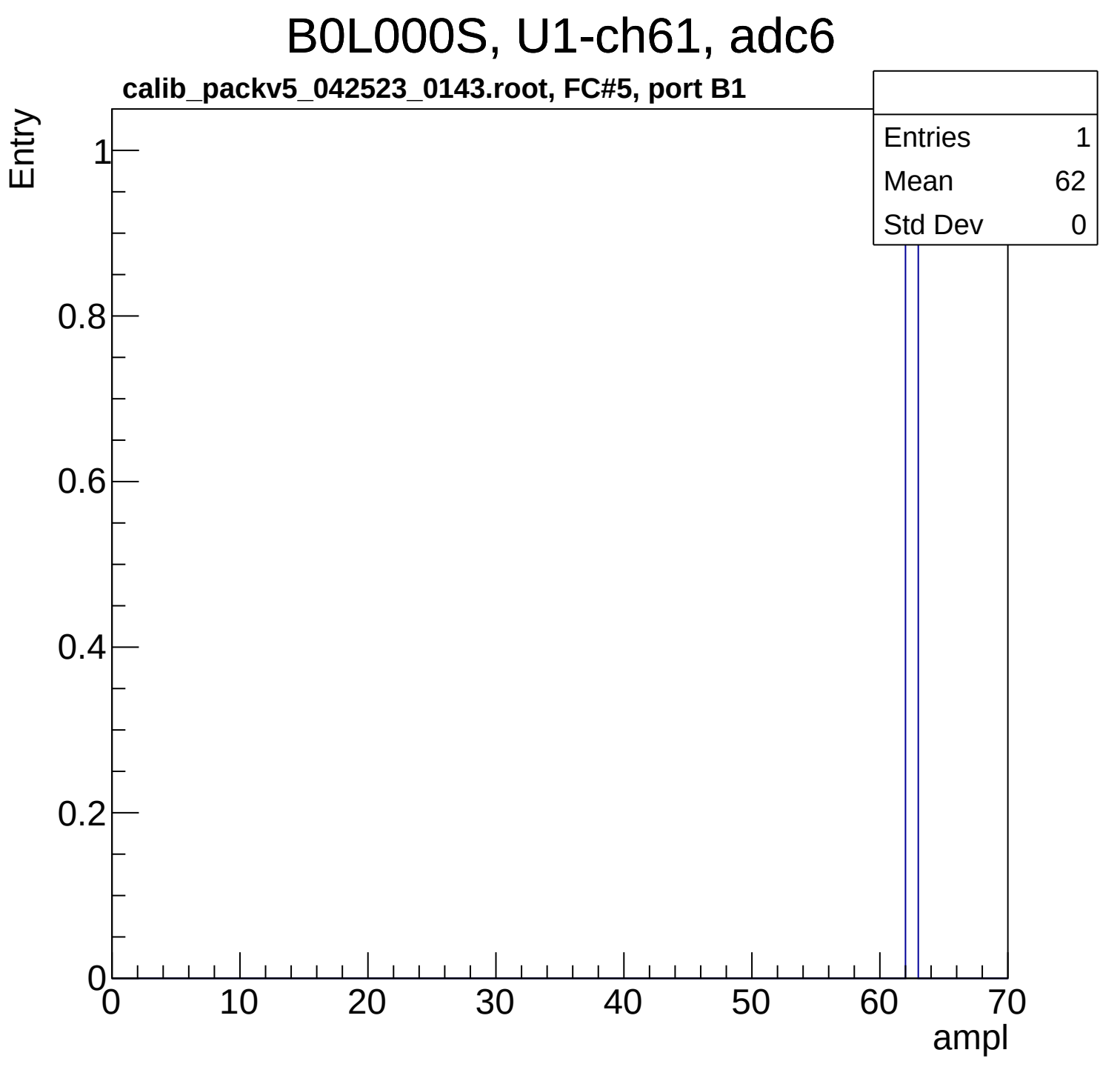

B0L000S, U1-ch61, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37.69
Std Dev 3.572
Gaus mean : 38.6713
Gaus Width: 4.5291
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

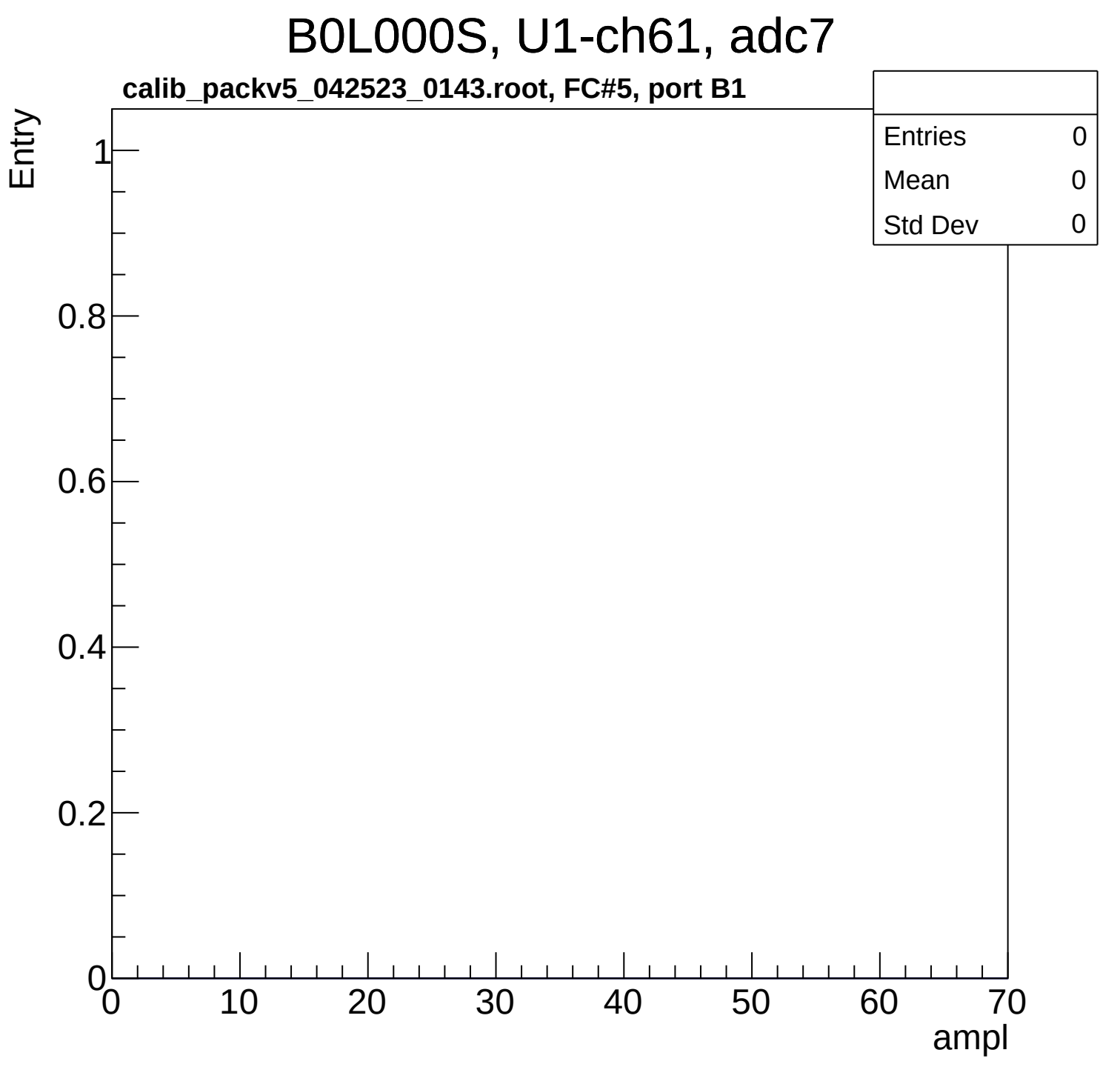

B0L000S, U1-ch61, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.78
Std Dev 3.675
Gaus mean : 43.9496
Gaus Width: 4.3312
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch61, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.82
Std Dev 3.296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch61, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 56.4
Std Dev 2.892
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch61, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.29
Std Dev 9.181
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch61, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch61, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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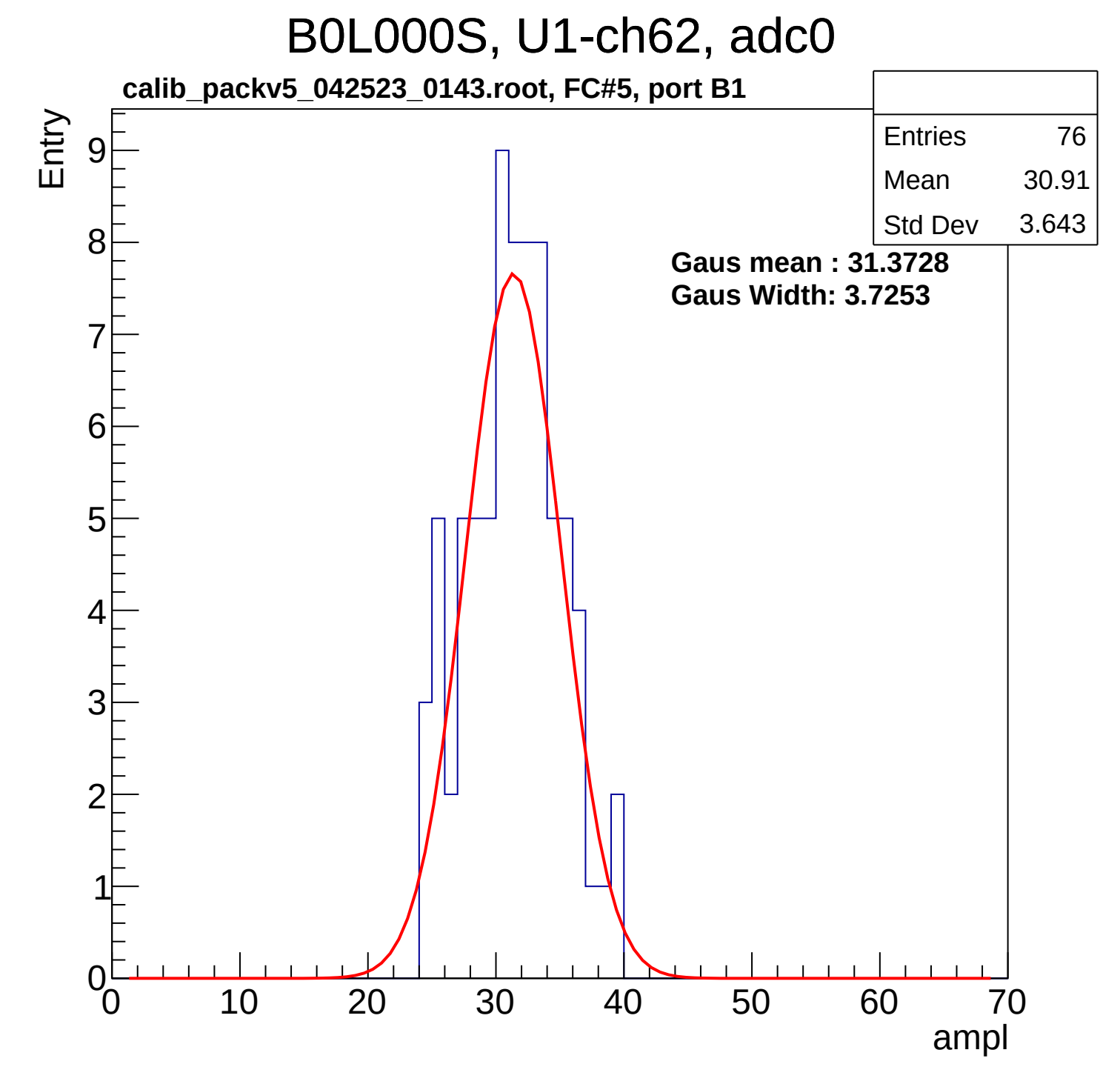
50
60
70
ampl


B0L000S, U1-ch62, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 30.91
Std Dev 3.643
Gaus mean : 31.3728
Gaus Width: 3.7253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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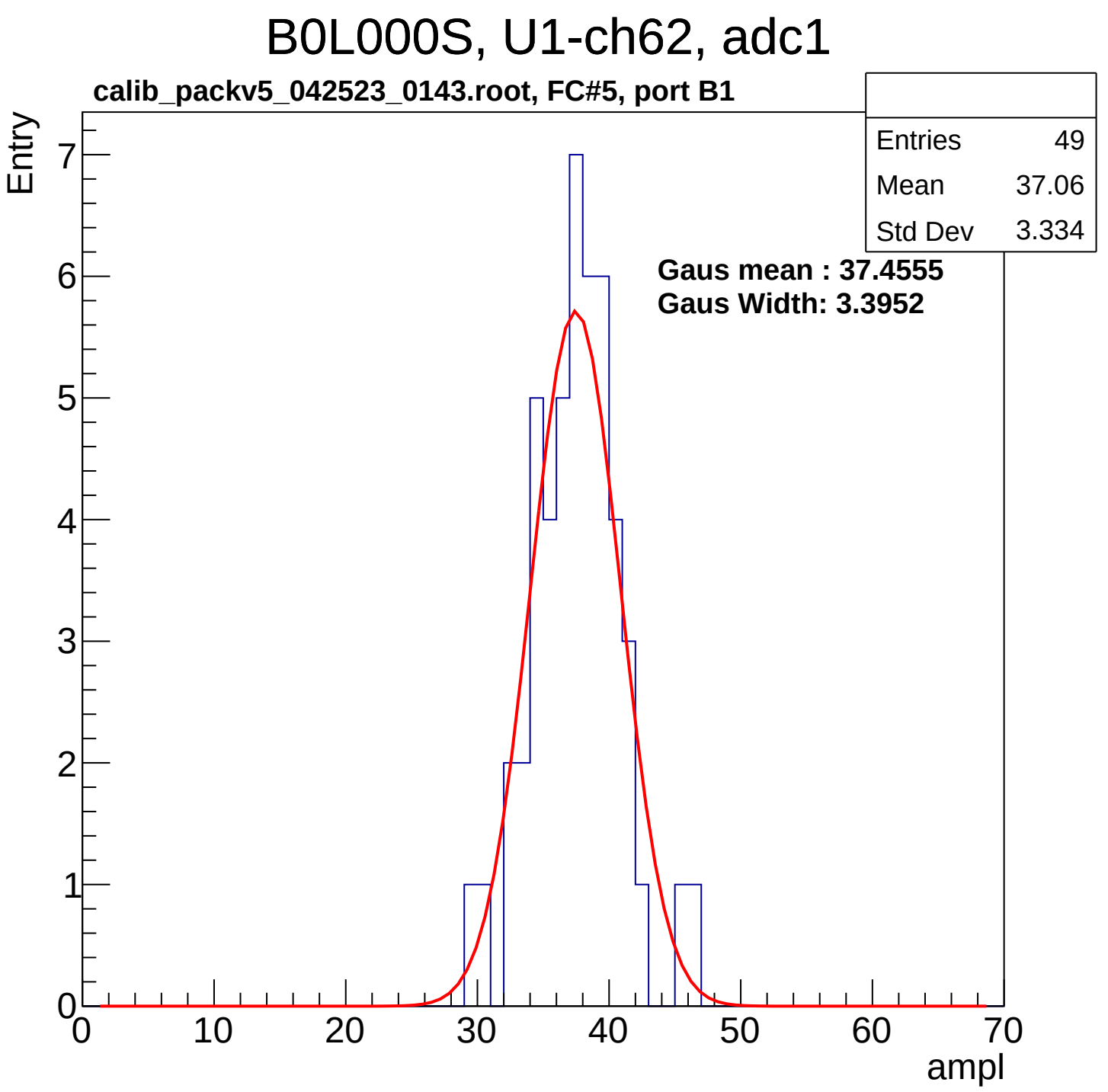
B0L000S, U1-ch62, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 37.06
Std Dev 3.334
Gaus mean : 37.4555
Gaus Width: 3.3952
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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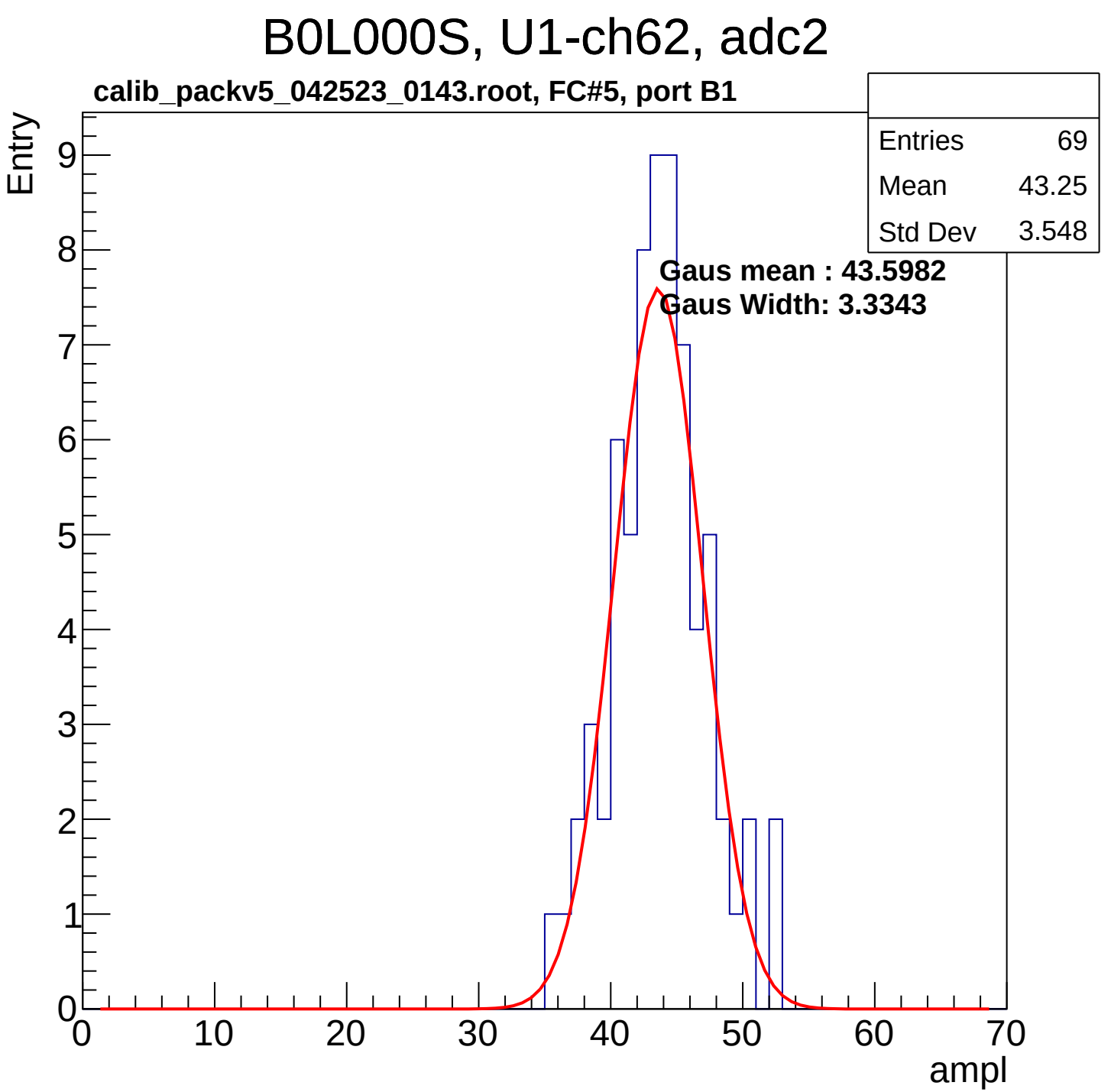
B0L000S, U1-ch62, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.25
Std Dev 3.548
Gaus mean : 43.5982
Gaus Width: 3.3343
Entry
ampl
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10
20
30
40
50
60
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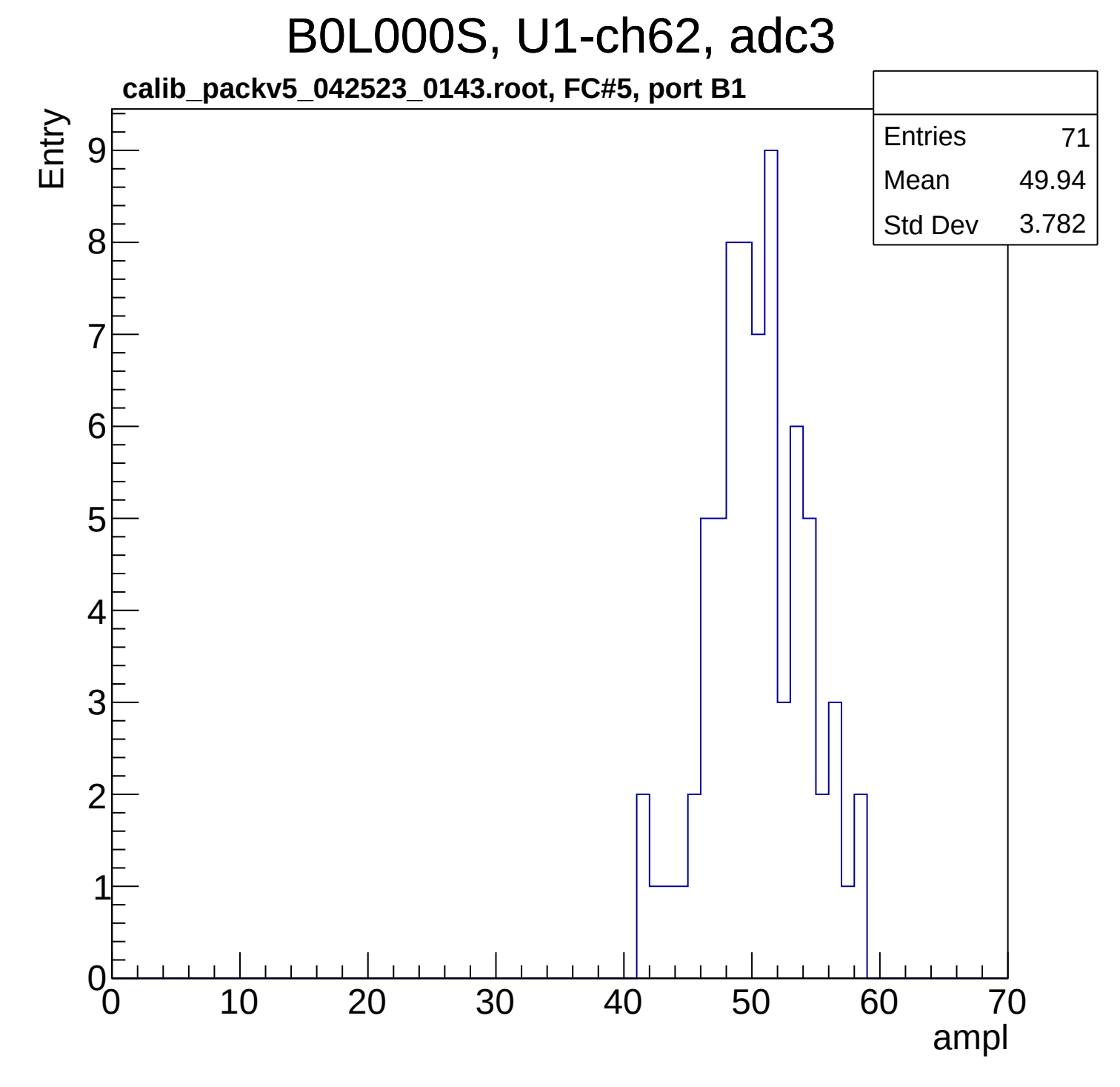
1
2
3
4
5
6
7
8
9


B0L000S, U1-ch62, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 49.94
Std Dev 3.782
Entry
ampl
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2
3
4
5
6
7
8
9
0
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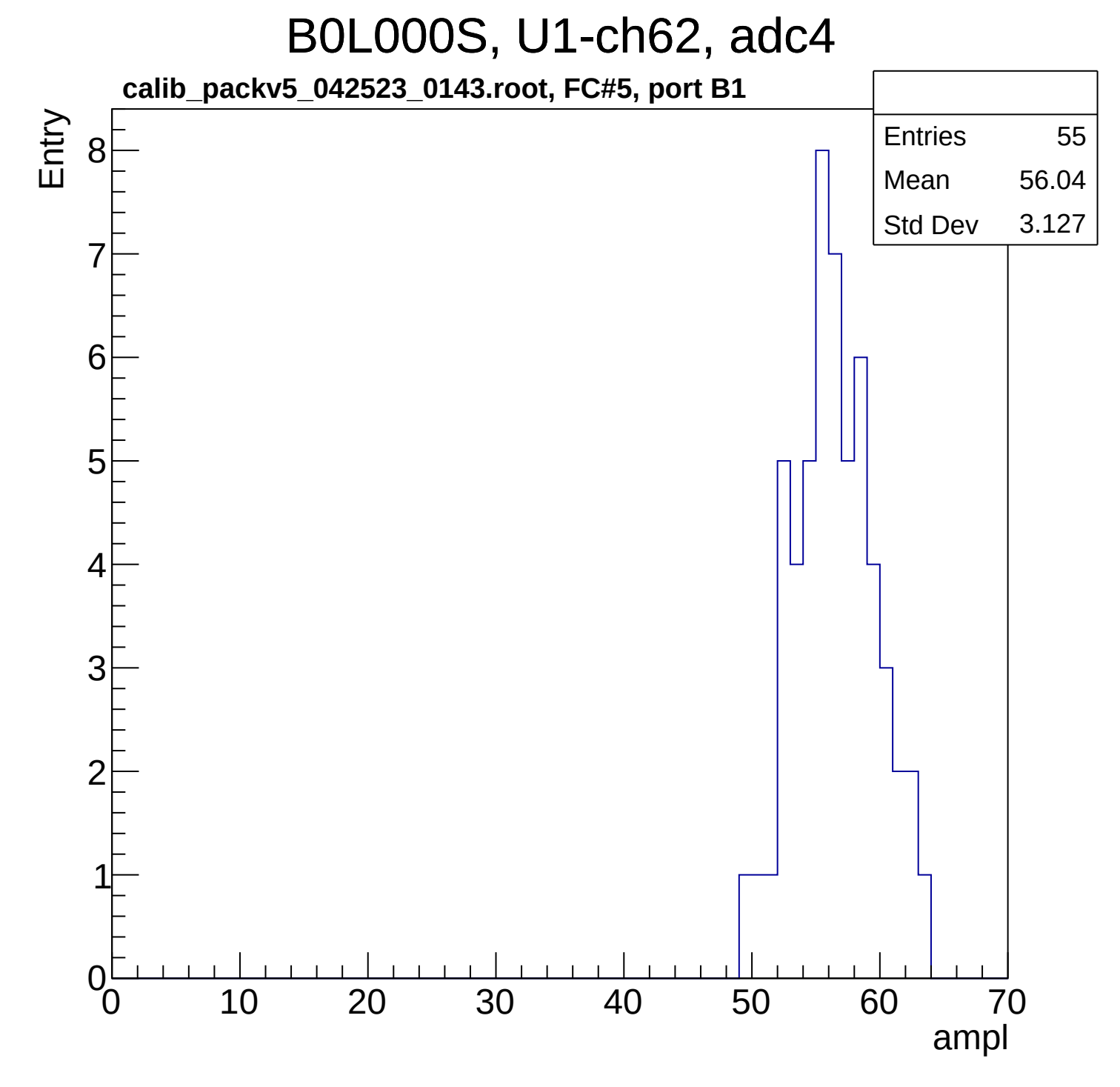
B0L000S, U1-ch62, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.04
Std Dev 3.127
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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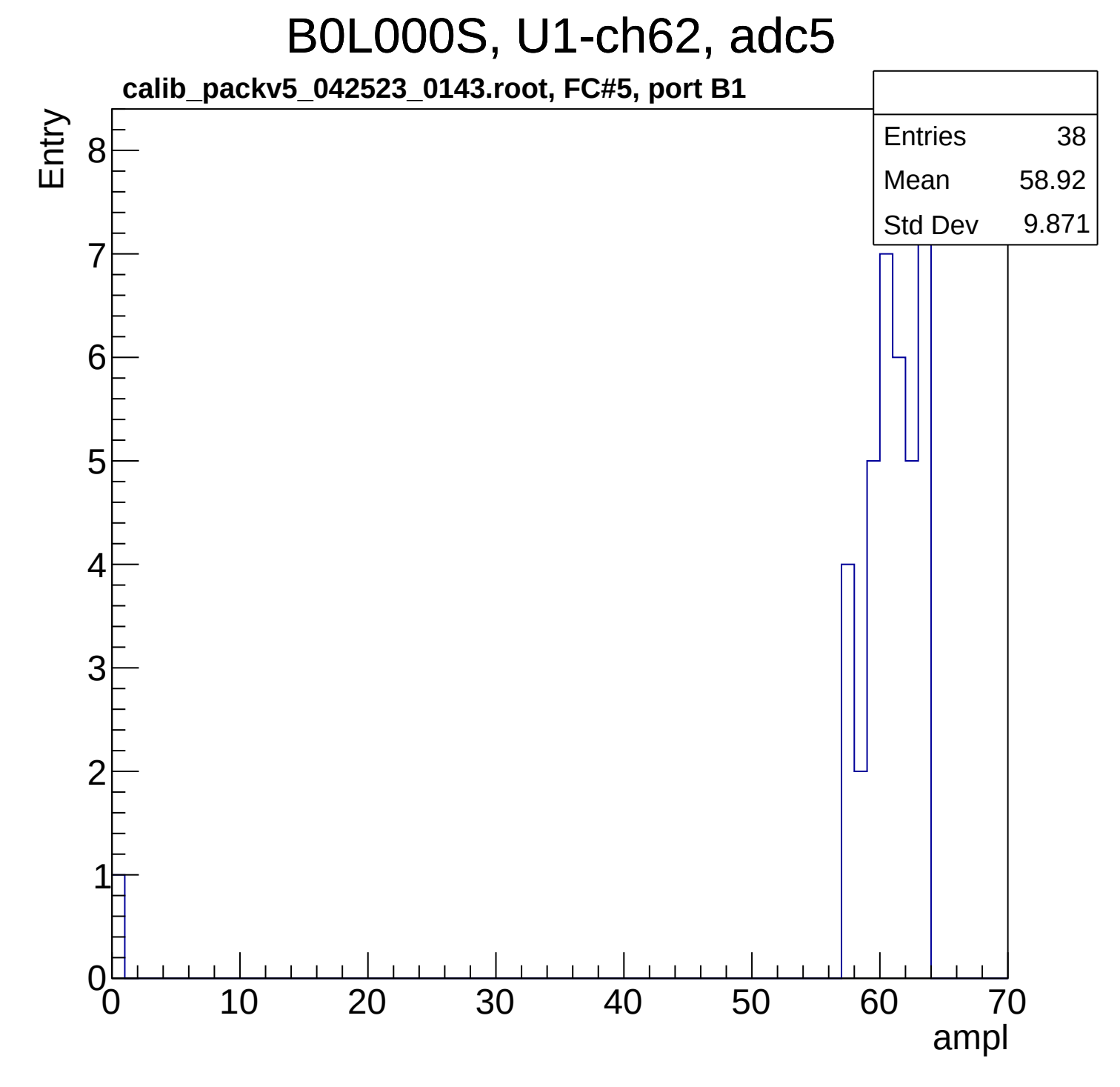
B0L000S, U1-ch62, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.92
Std Dev 9.871
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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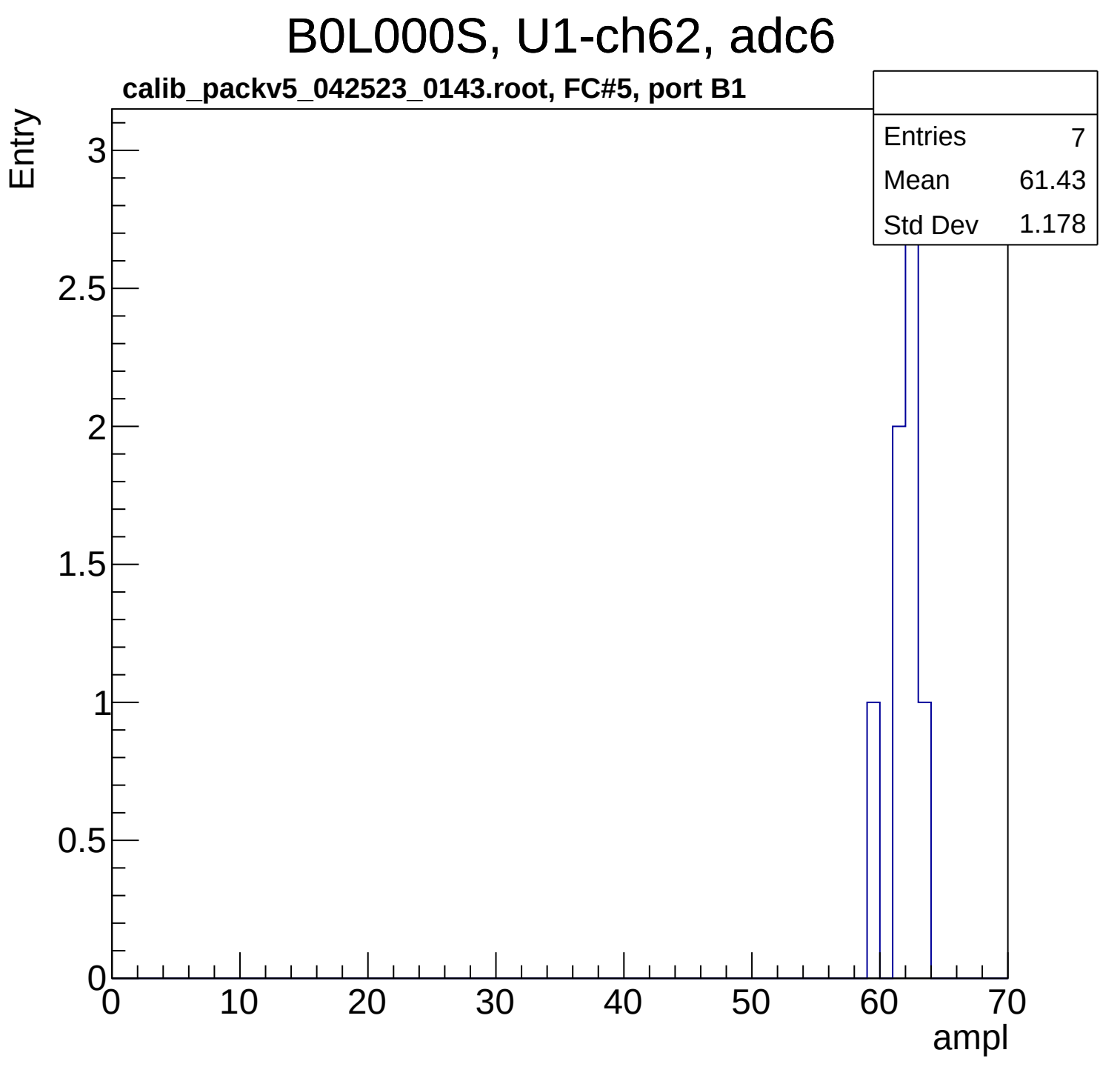
B0L000S, U1-ch62, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.43
Std Dev 1.178
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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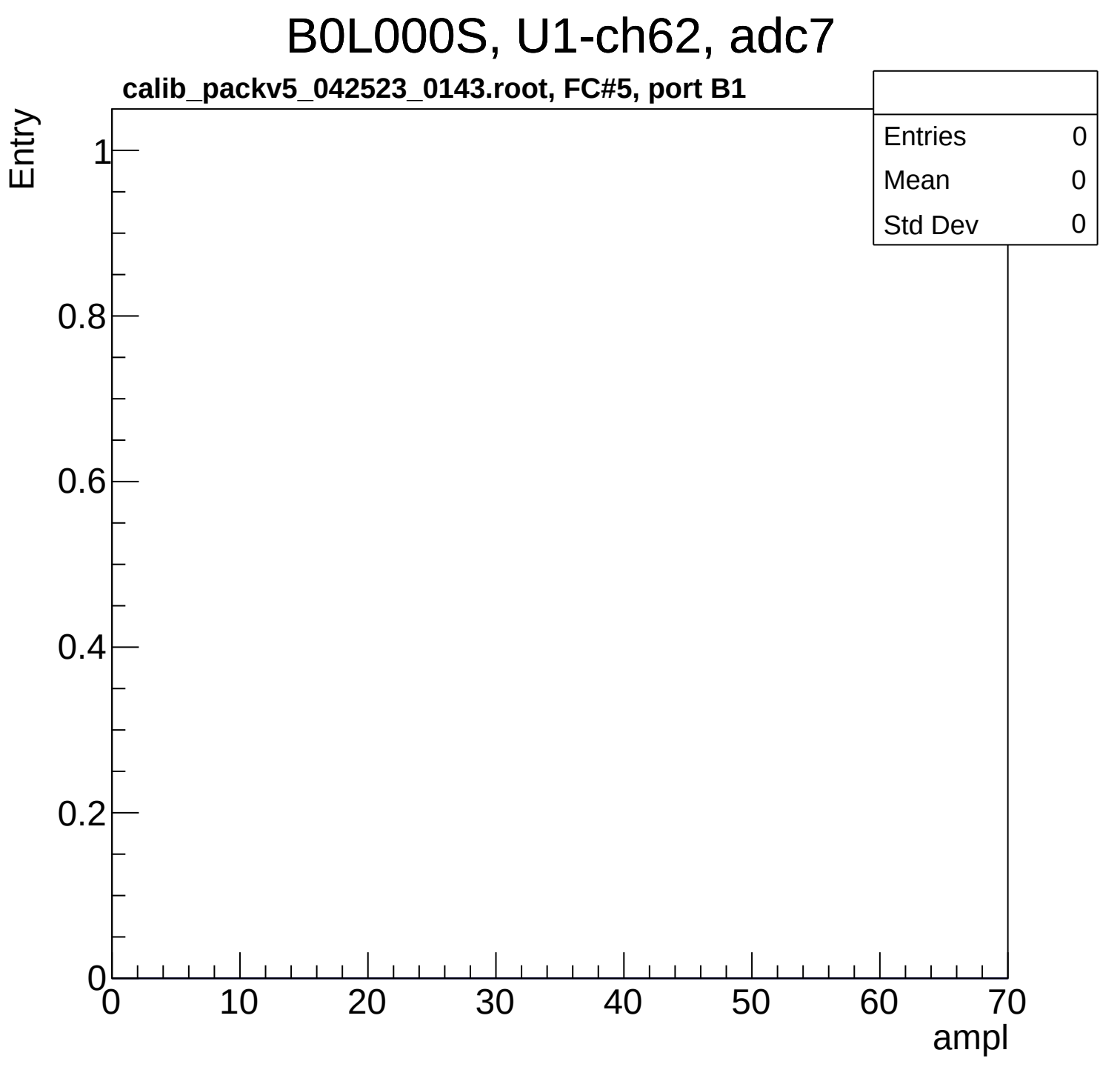
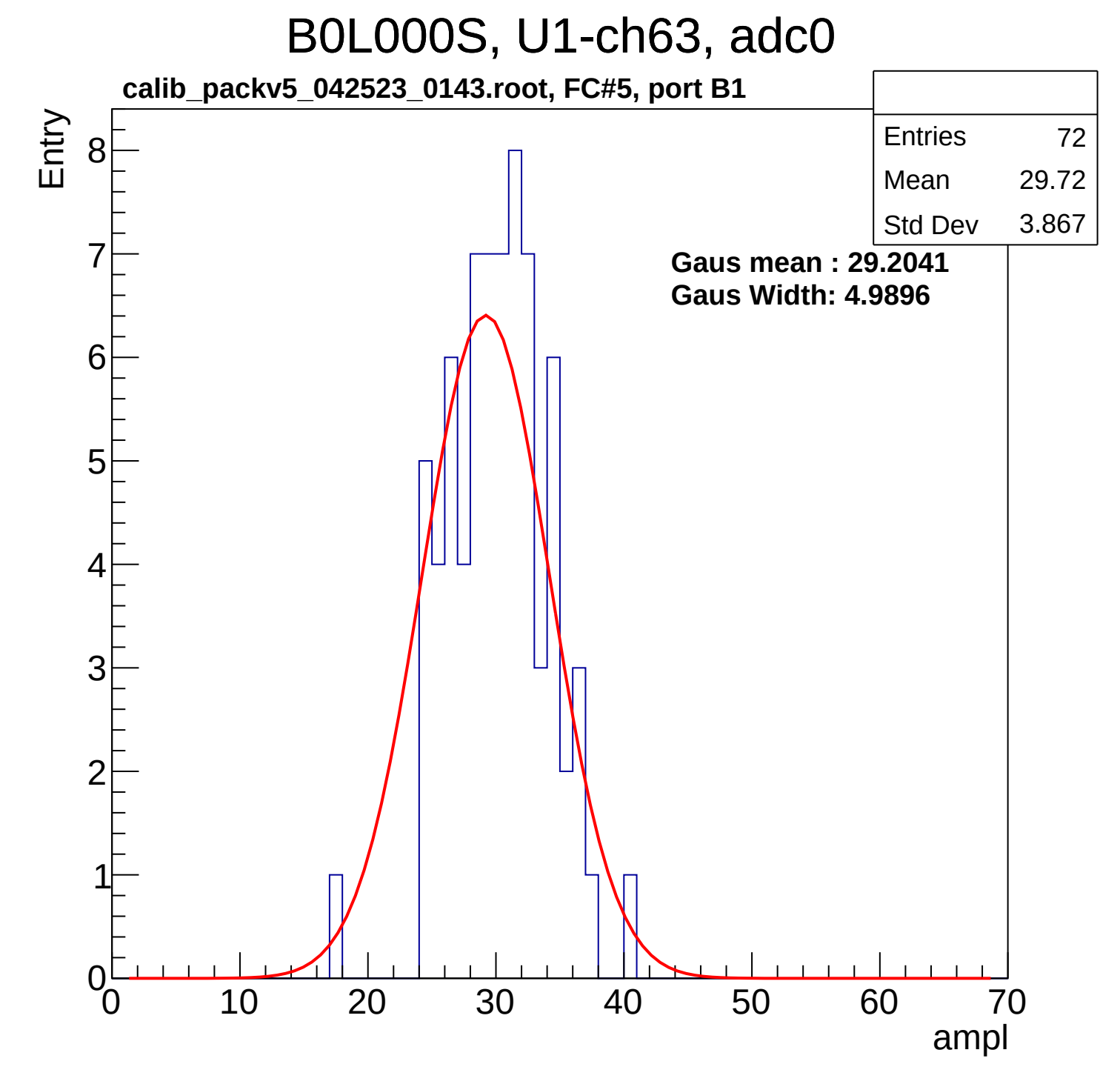
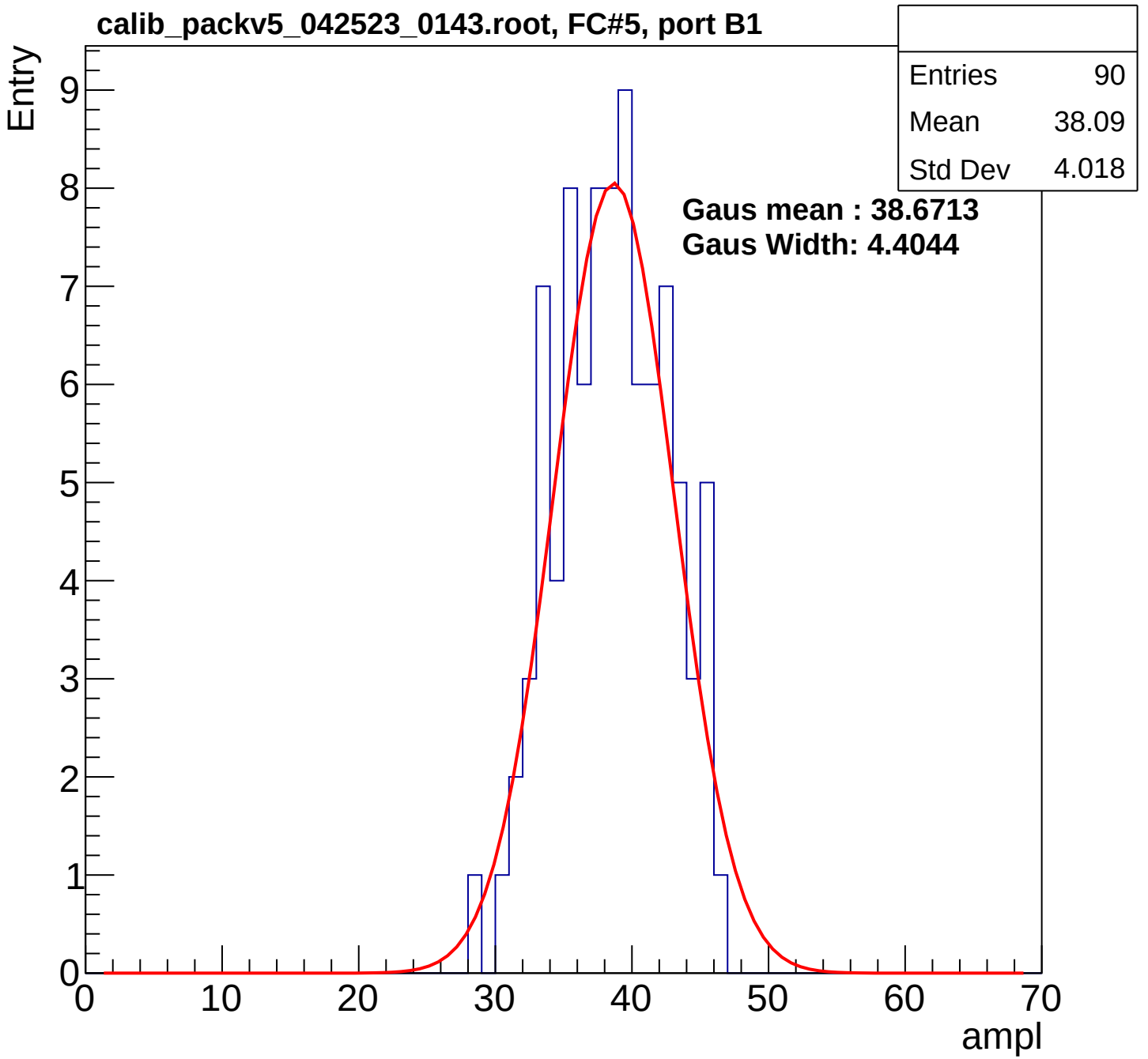
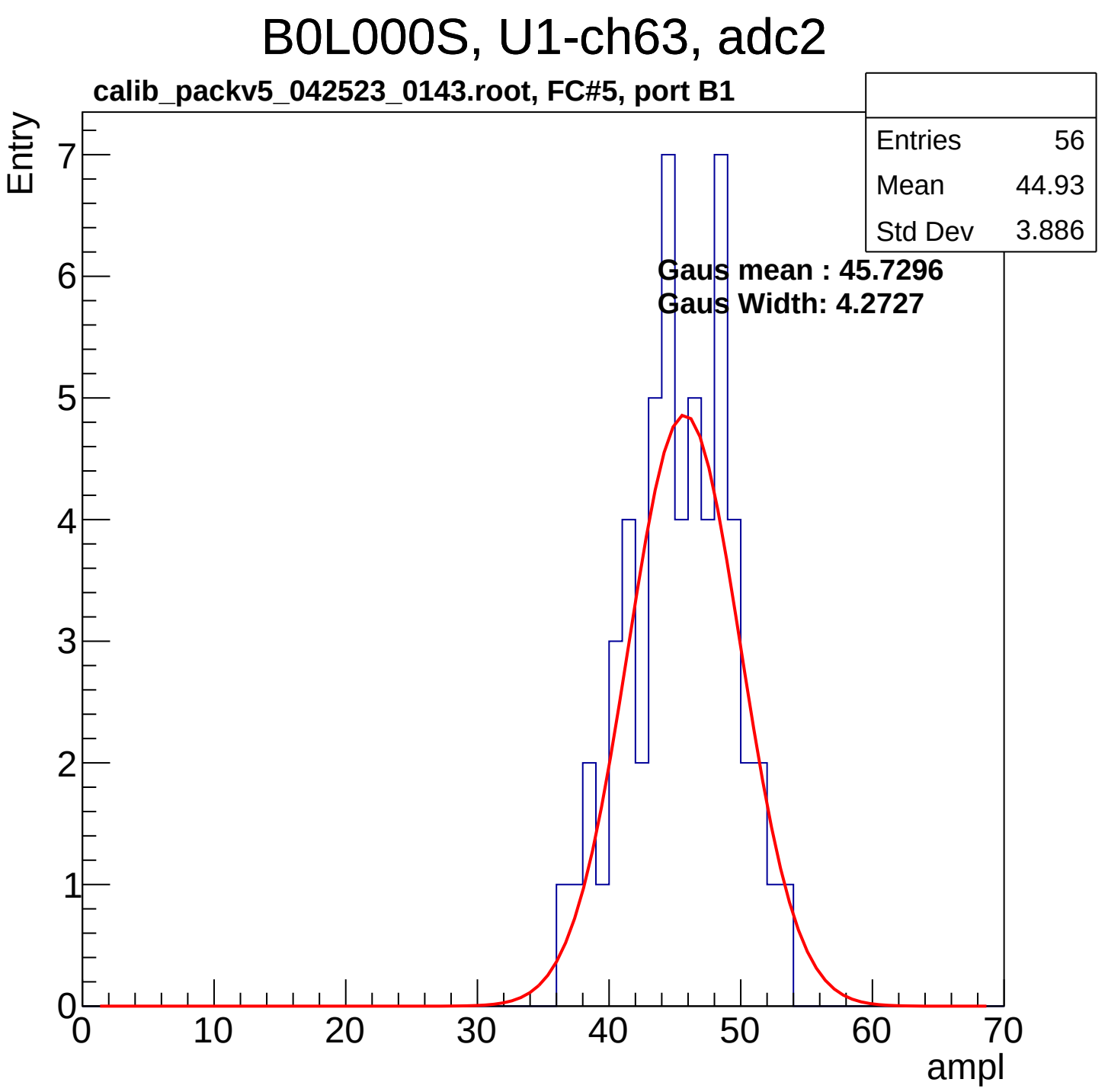
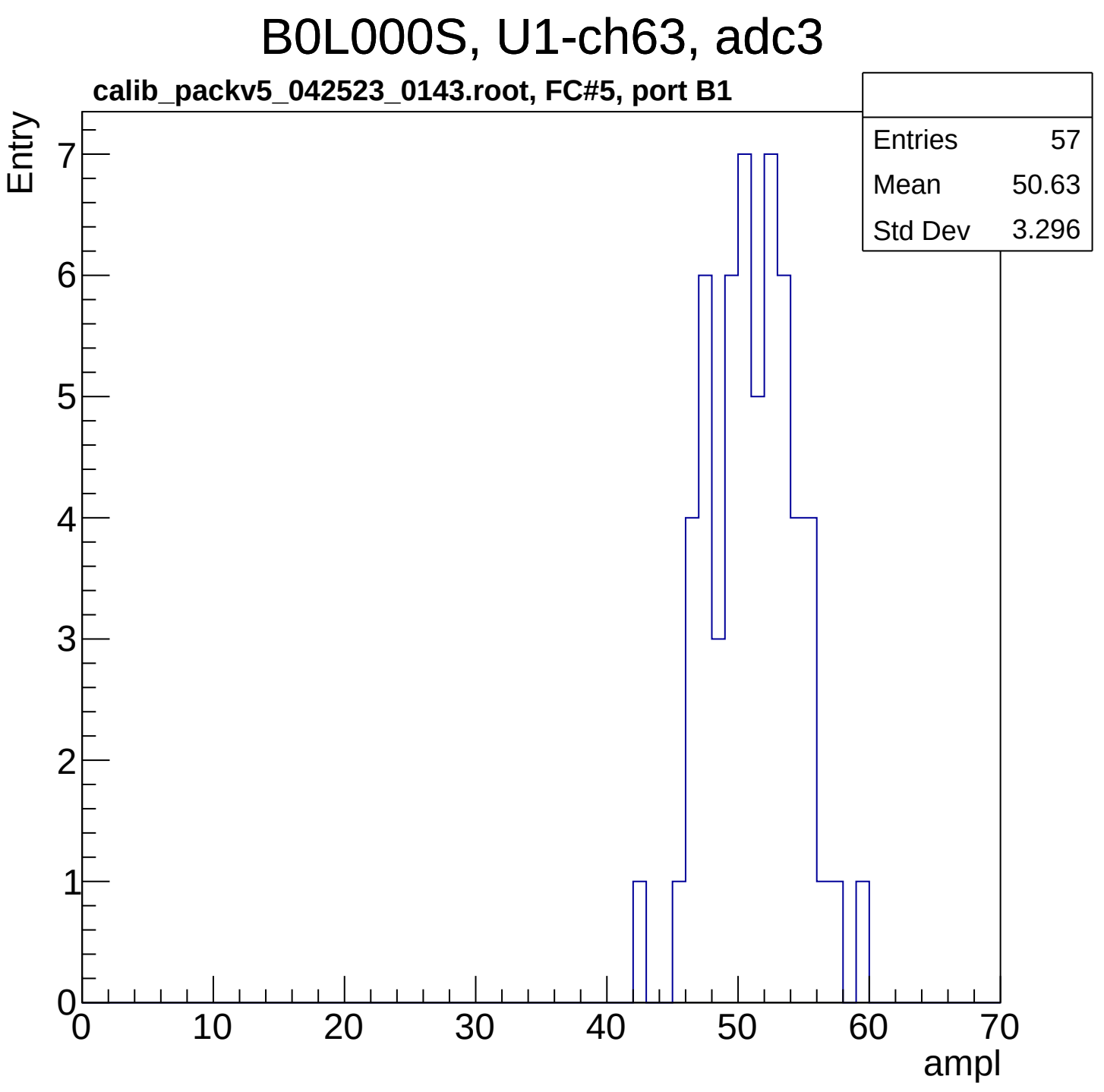
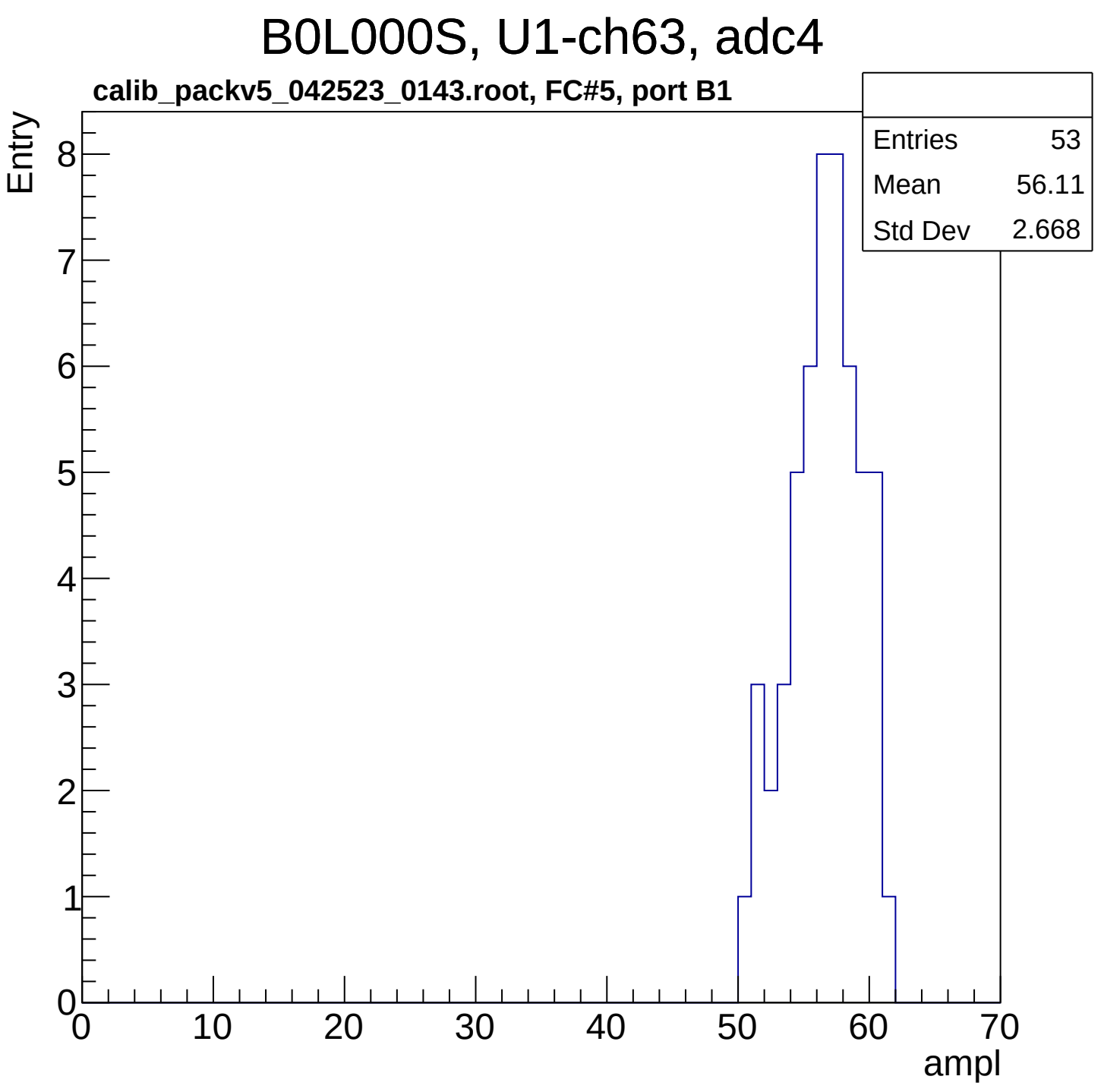

B0L000S, U1-ch62, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

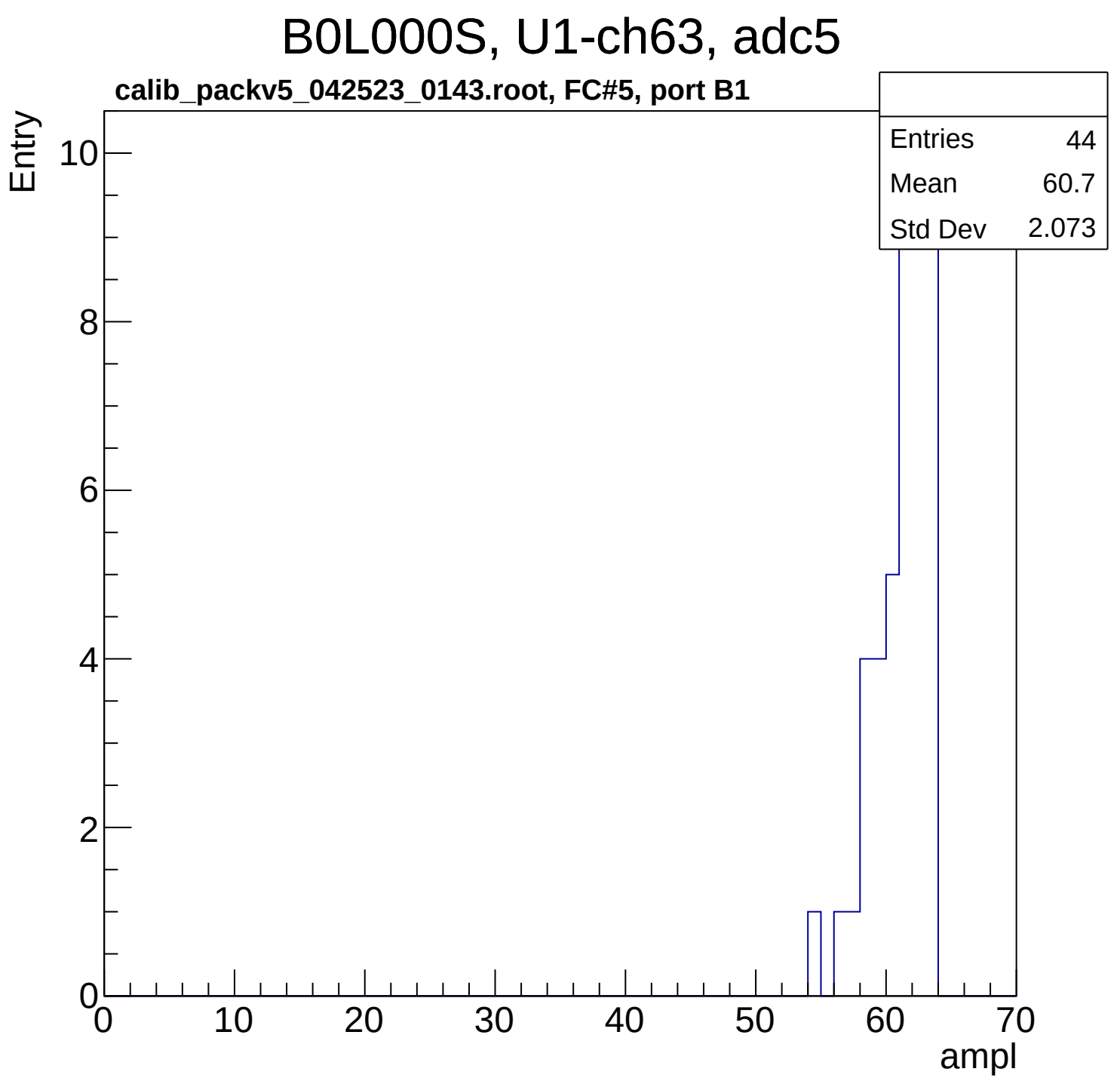

B0L000S, U1-ch63, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.72
Std Dev 3.867
Gaus mean : 29.2041
Gaus Width: 4.9896
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

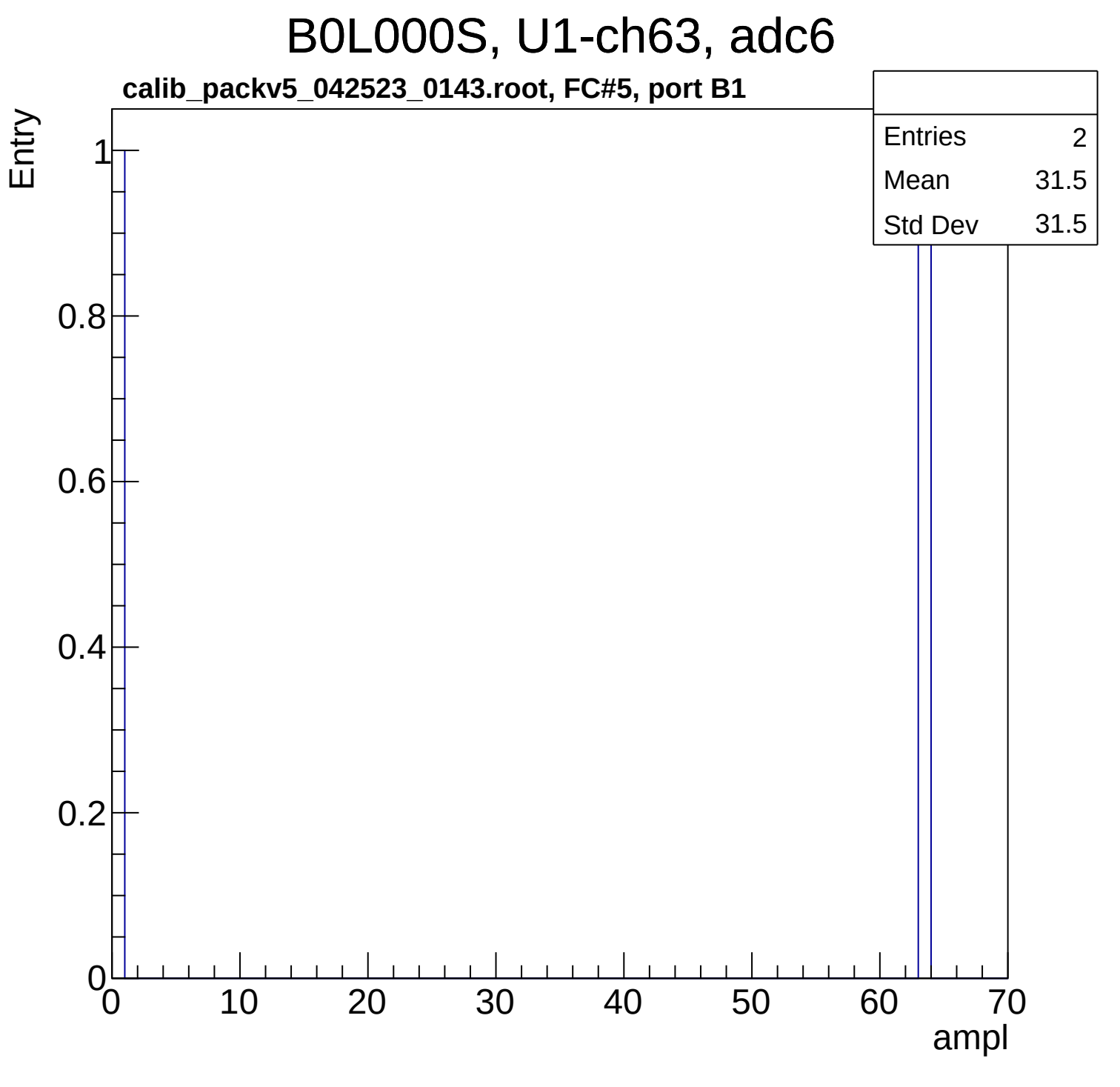

B0L000S, U1-ch63, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 90
Mean 38.09
Std Dev 4.018
Gaus mean : 38.6713
Gaus Width: 4.4044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

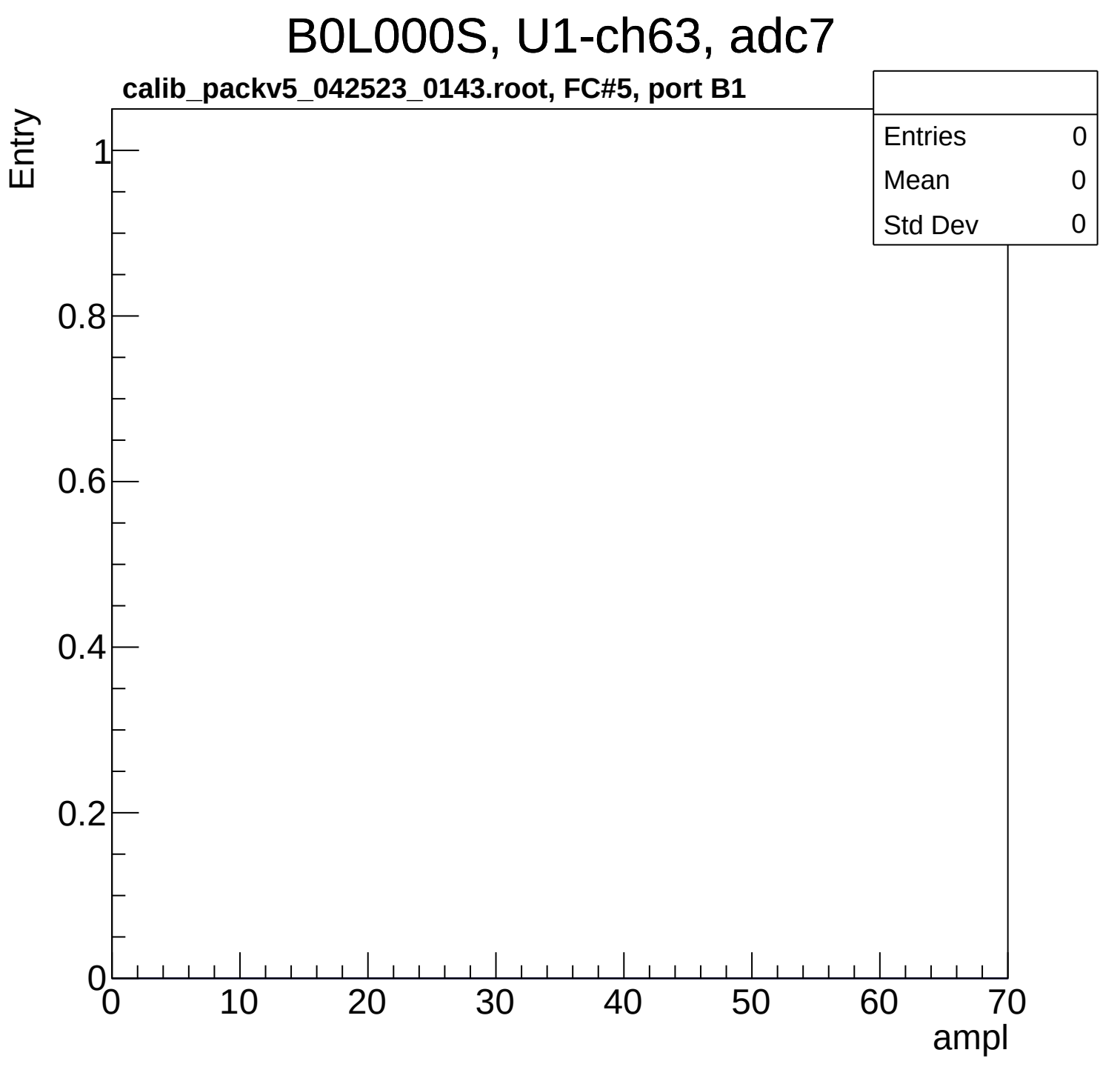

B0L000S, U1-ch63, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.93
Std Dev 3.886
Gaus mean : 45.7296
Gaus Width: 4.2727
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

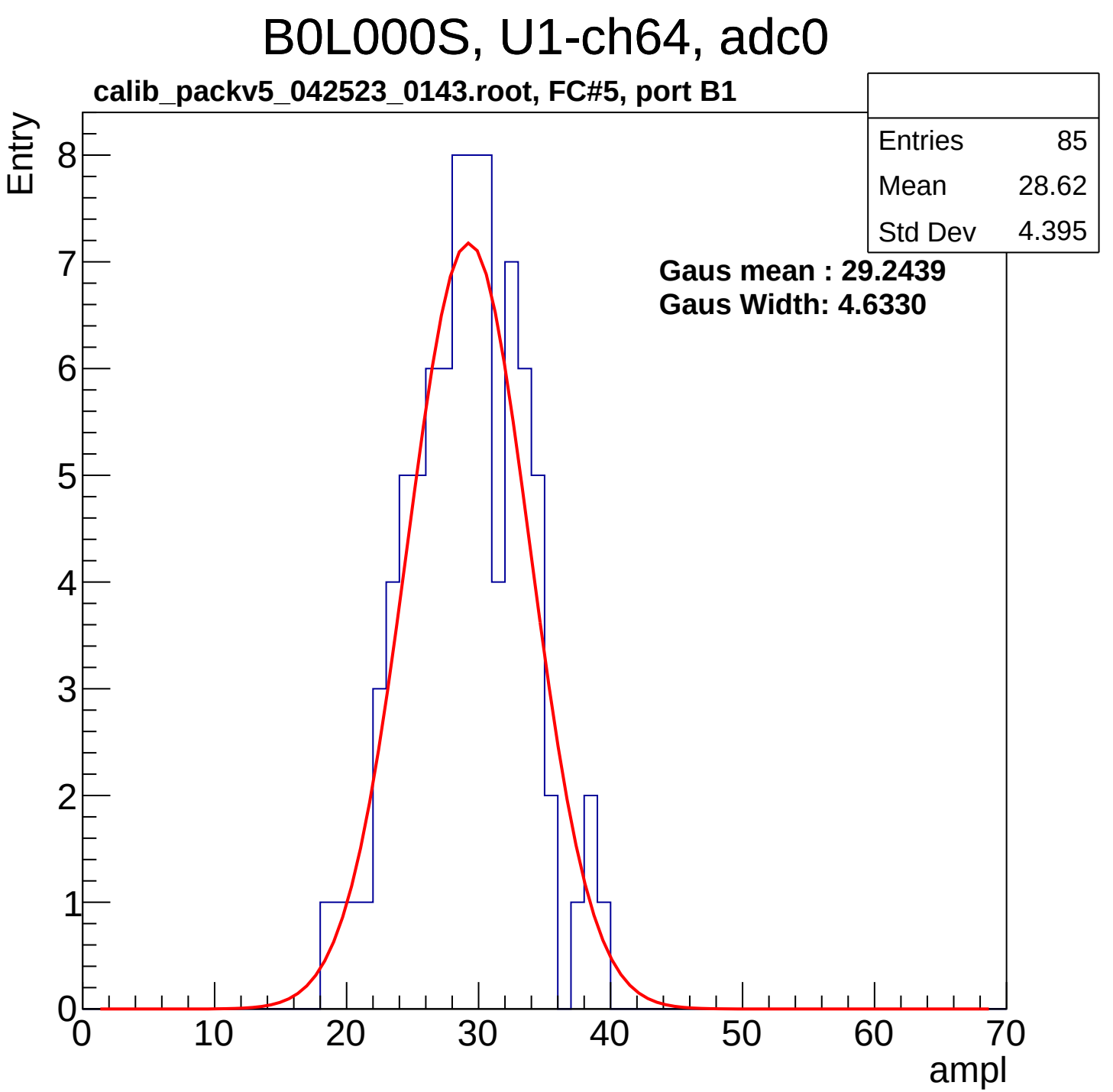

B0L000S, U1-ch63, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 50.63
Std Dev 3.296
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

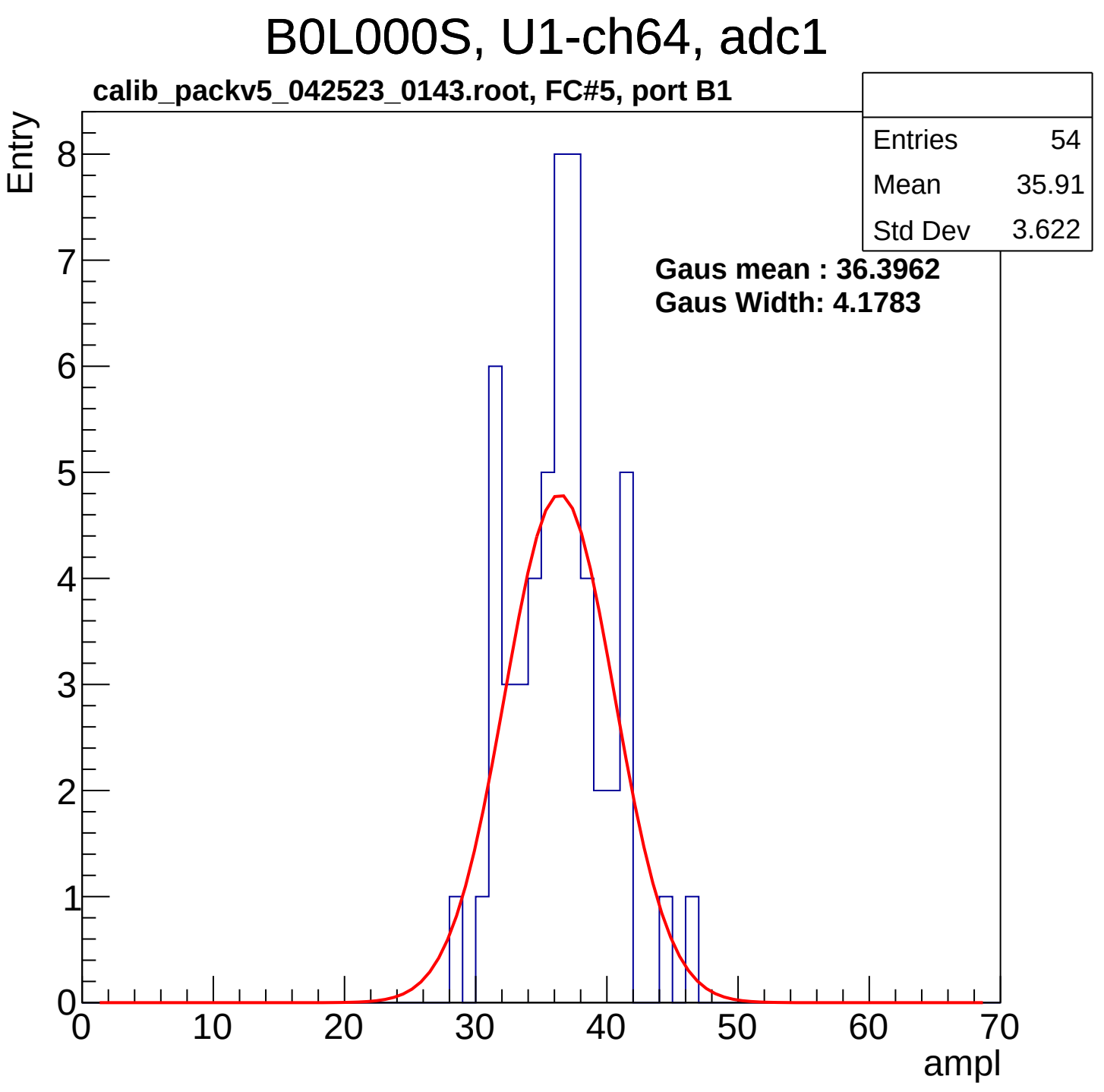

B0L000S, U1-ch63, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.11
Std Dev 2.668
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch63, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 60.7
Std Dev 2.073
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch63, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch63, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch64, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 28.62
Std Dev 4.395
Gaus mean : 29.2439
Gaus Width: 4.6330
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch64, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 35.91
Std Dev 3.622
Gaus mean : 36.3962
Gaus Width: 4.1783
Entry
ampl
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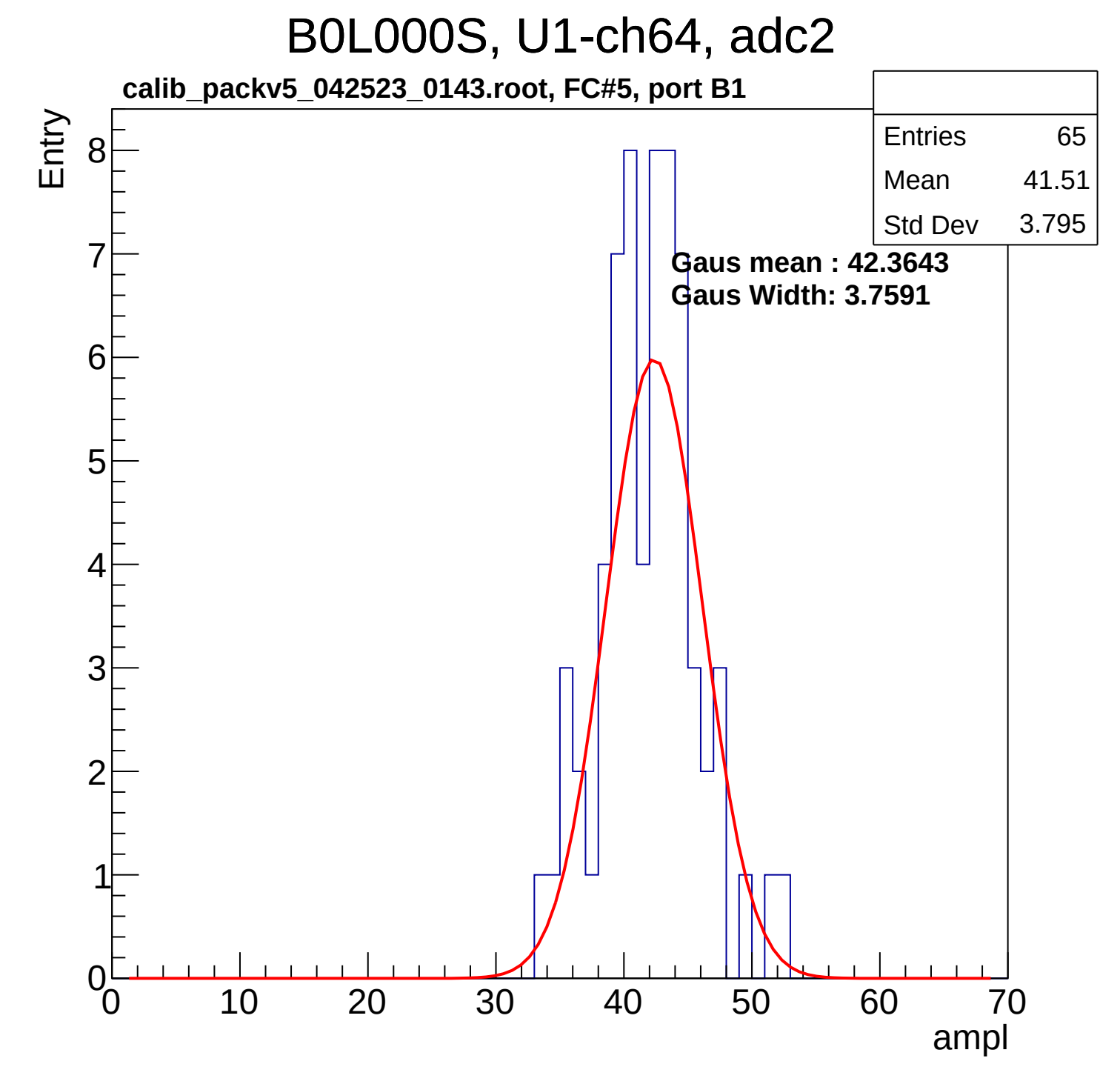
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch64, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.51
Std Dev 3.795
Gaus mean : 42.3643
Gaus Width: 3.7591
Entry
ampl
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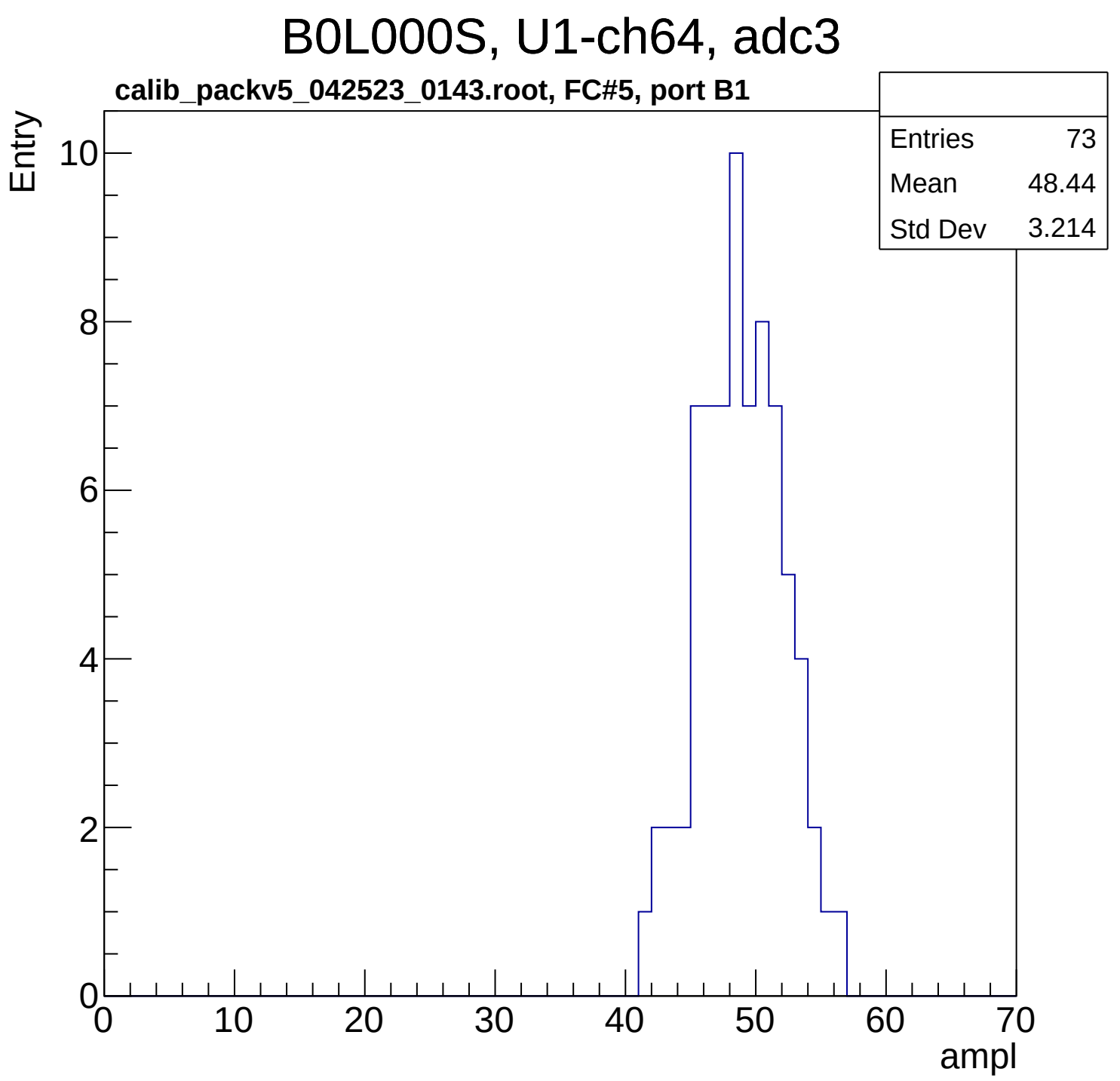
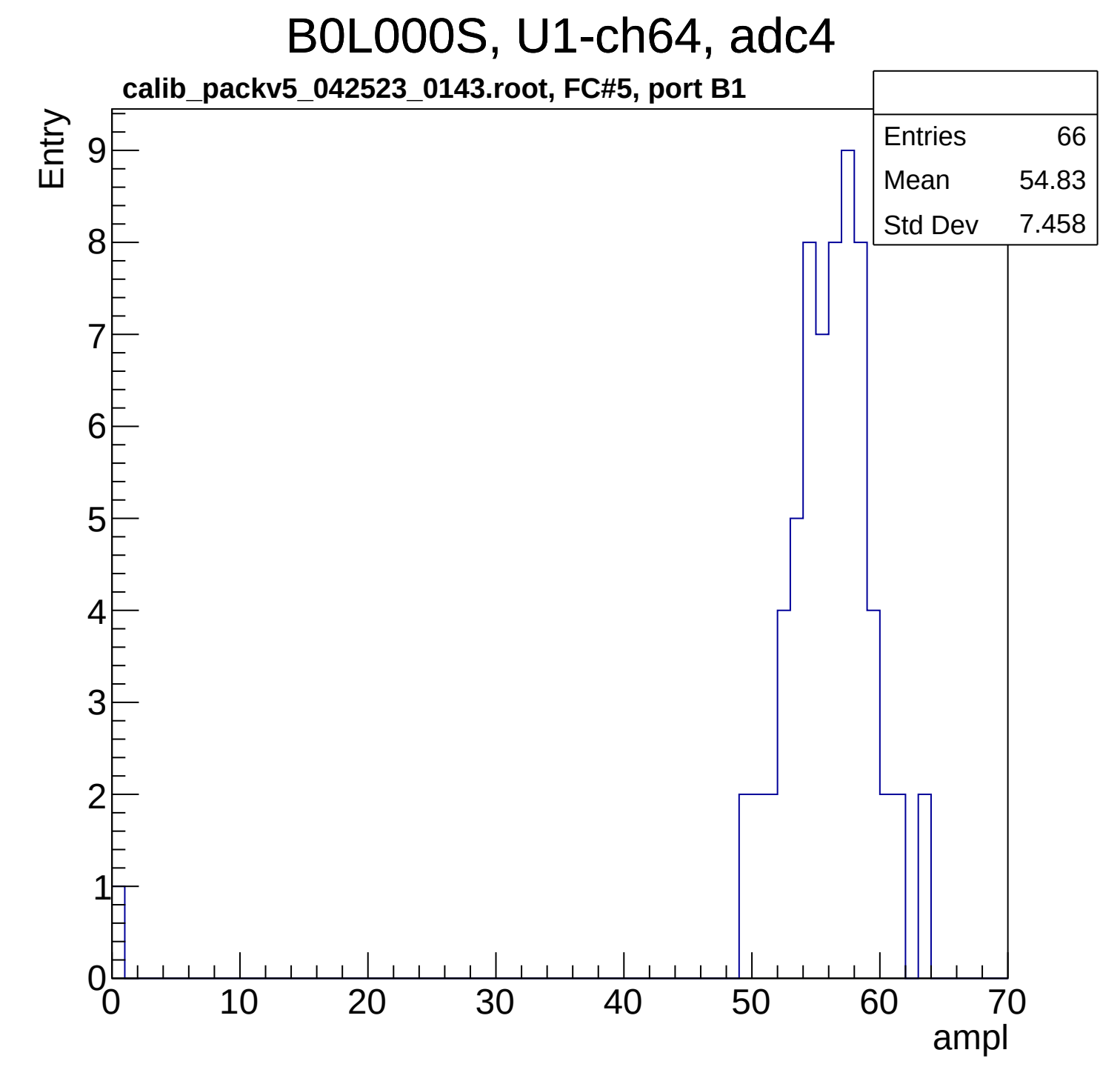
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch64, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.44
Std Dev 3.214
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch64, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.83
Std Dev 7.458
Entry
ampl
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4
5
6
7
8
9
0
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40
50
60
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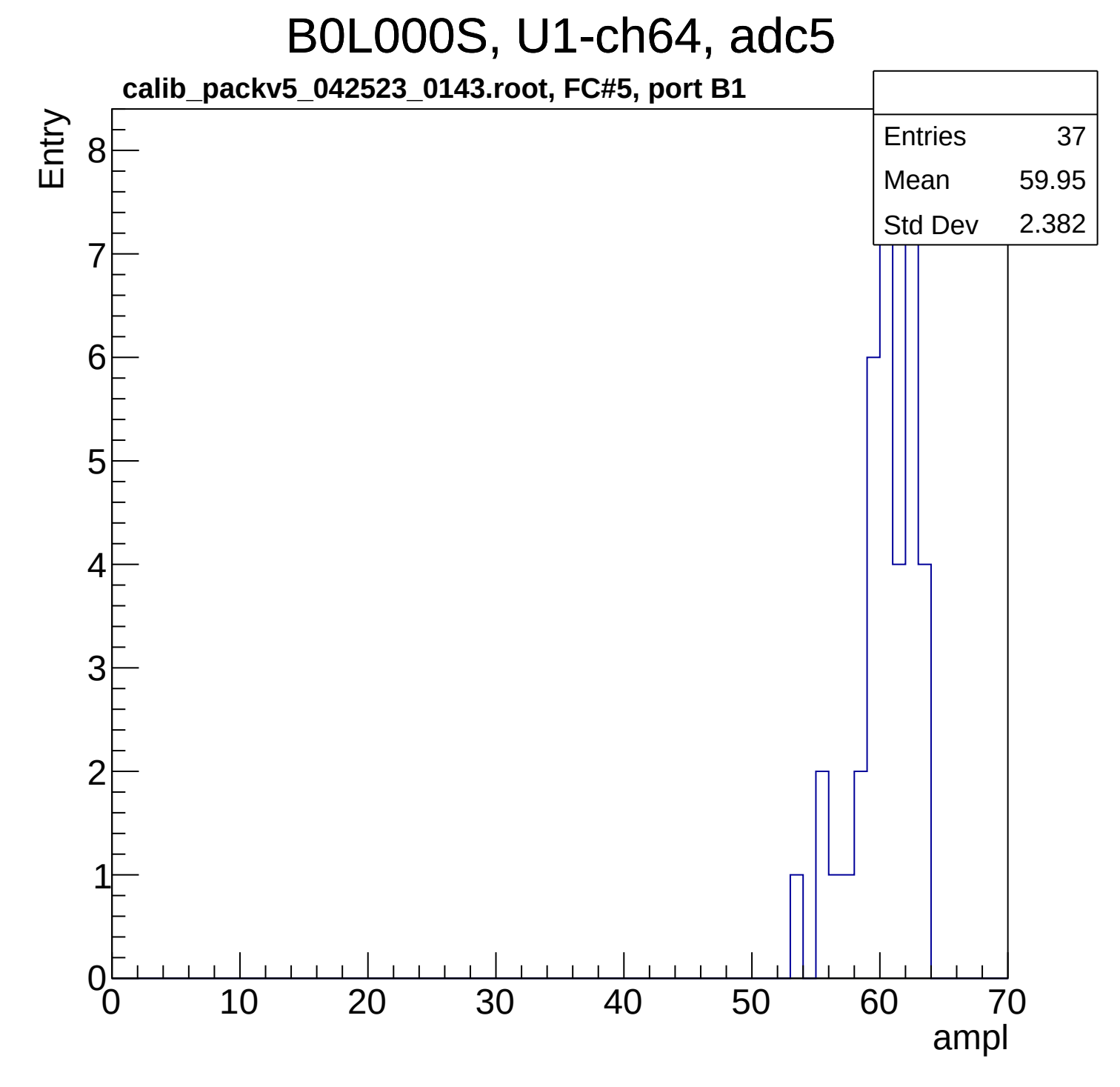
B0L000S, U1-ch64, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.95
Std Dev 2.382
Entry
ampl
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1
2
3
4
5
6
7
8
0
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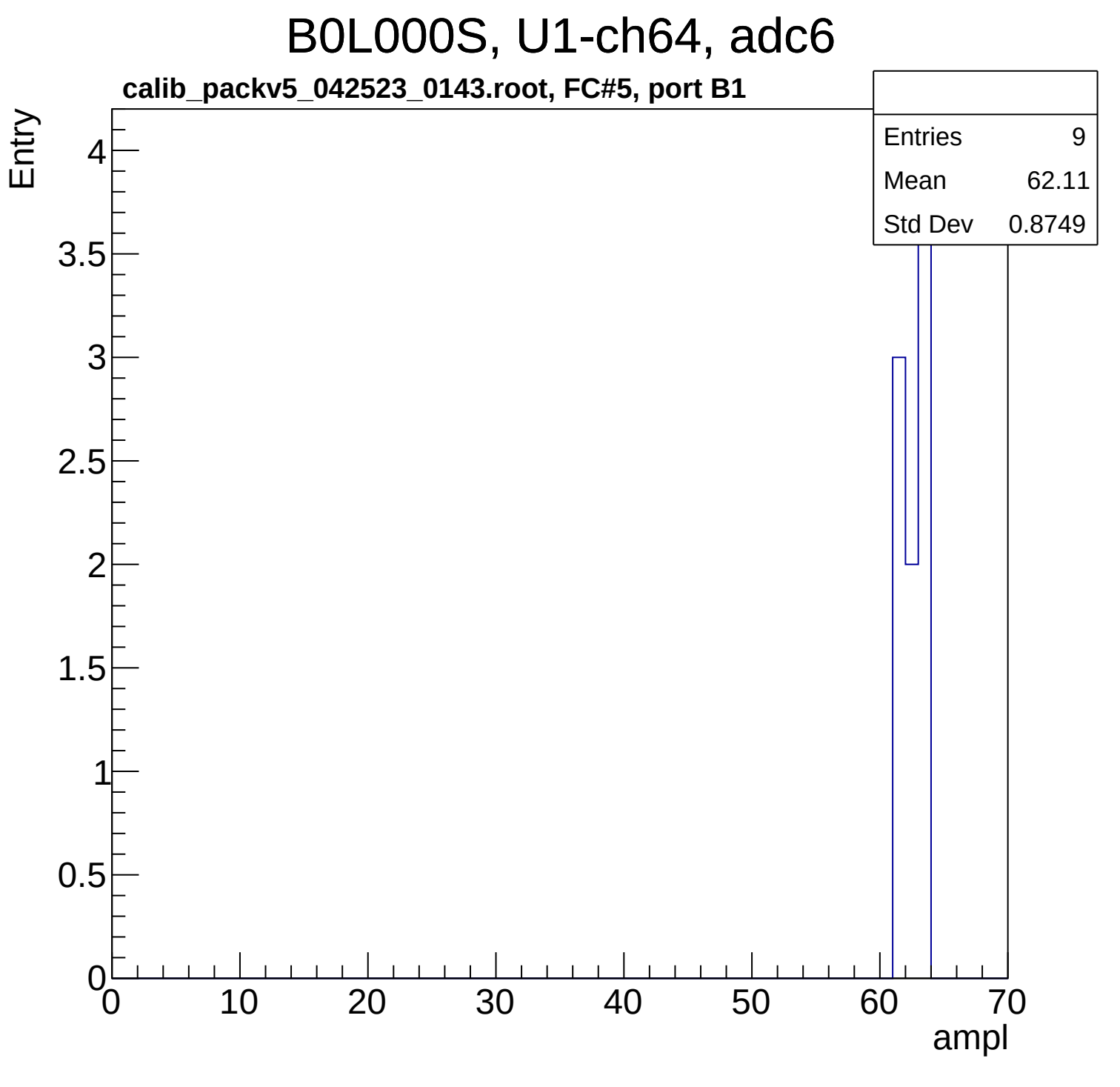
20
30
40
50
60
70


B0L000S, U1-ch64, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62.11
Std Dev 0.8749
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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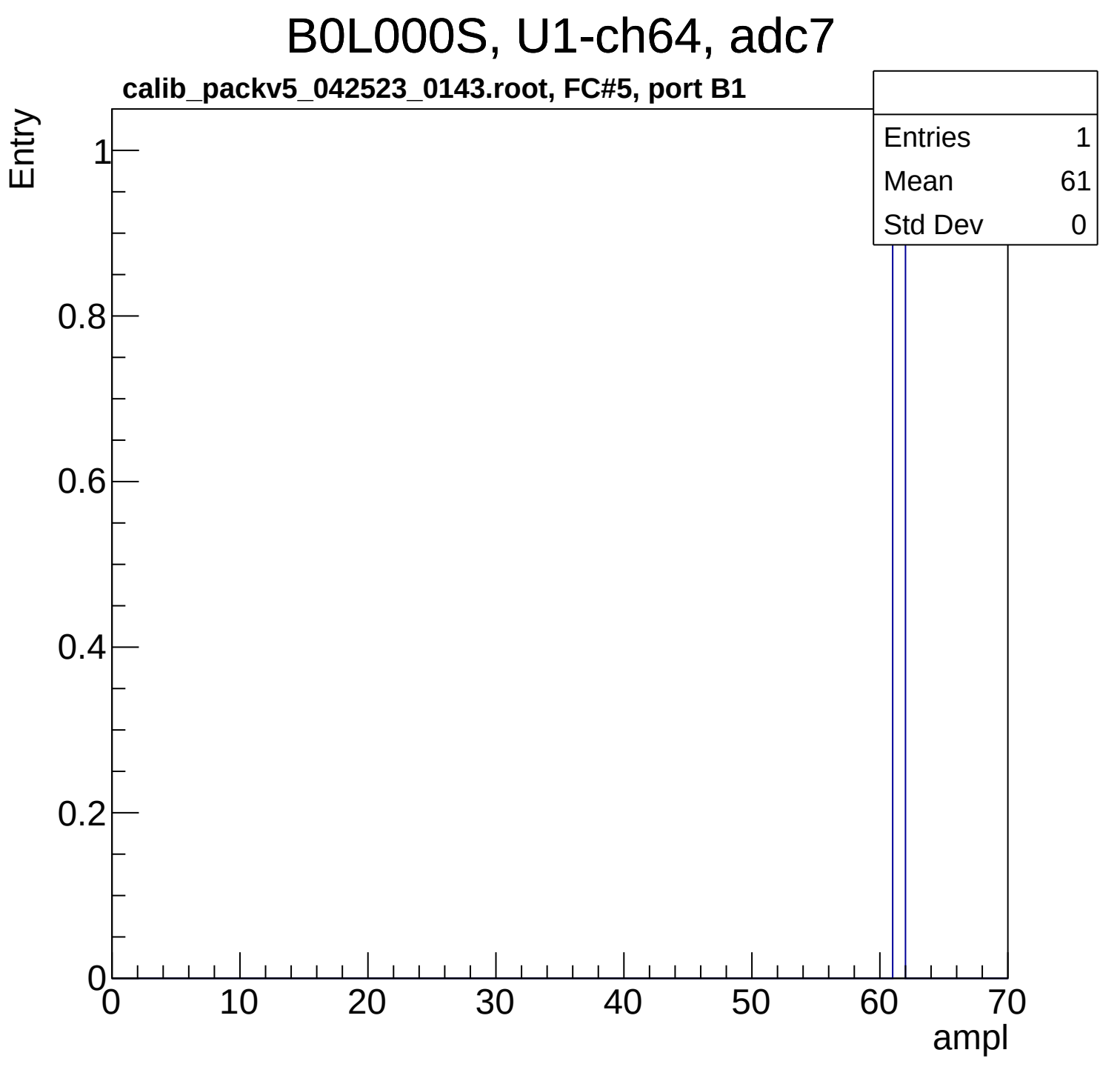
20
30
40
50
60
70


B0L000S, U1-ch64, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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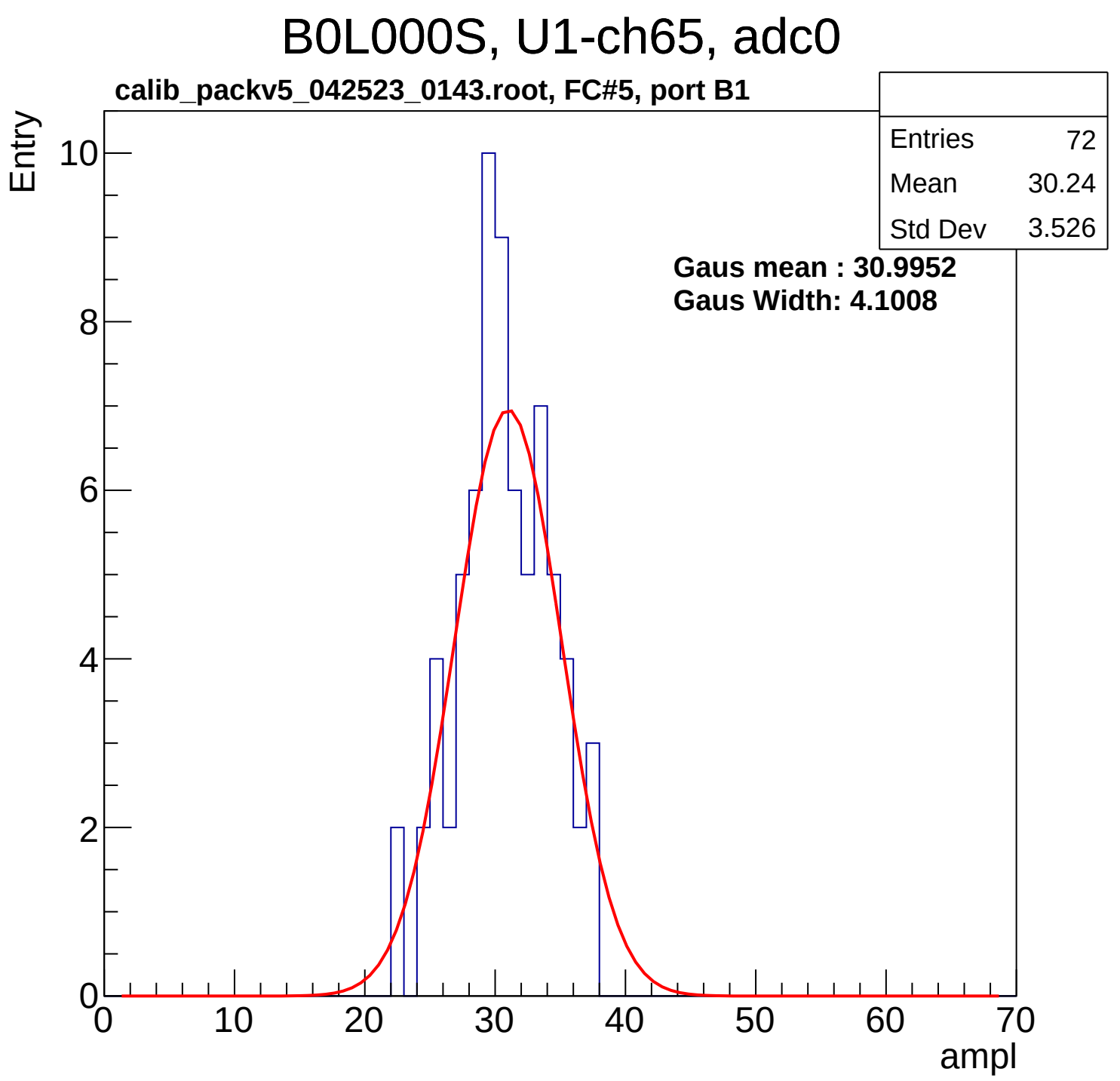
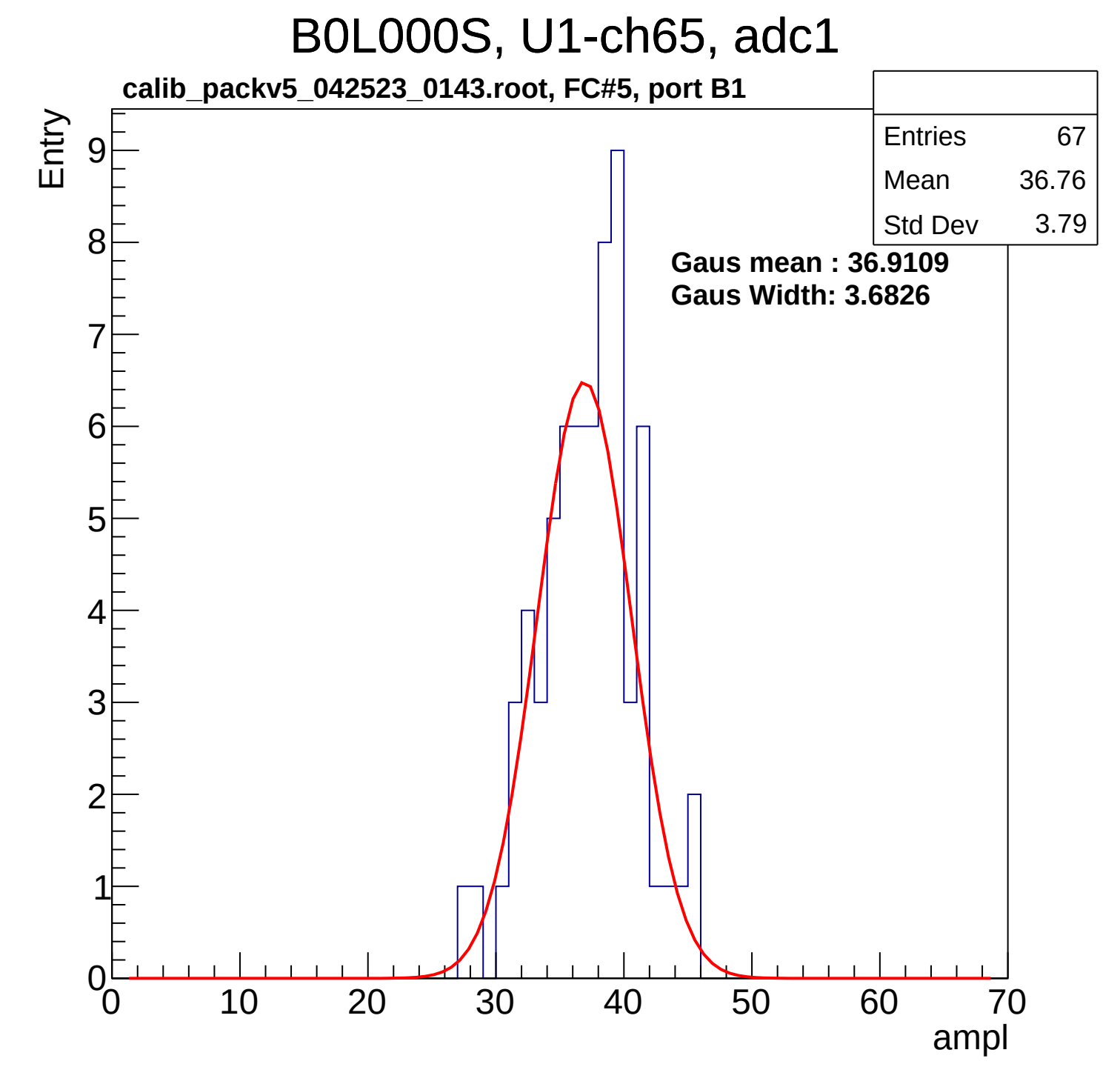
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch65, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 30.24
Std Dev 3.526
Gaus mean : 30.9952
Gaus Width: 4.1008
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch65, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.76
Std Dev 3.79
Gaus mean : 36.9109
Gaus Width: 3.6826
Entry
ampl
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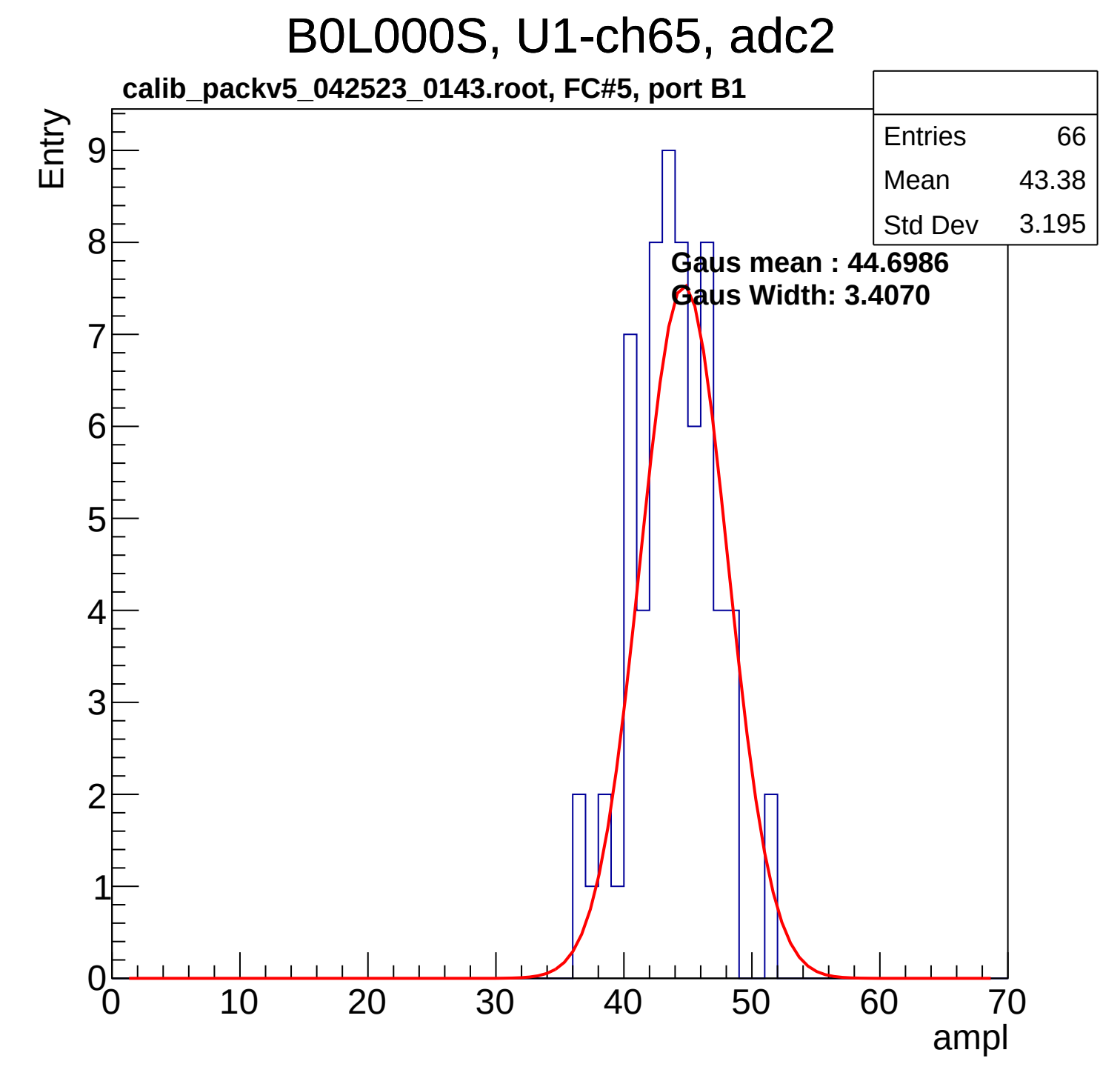
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U1-ch65, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.38
Std Dev 3.195
Gaus mean : 44.6986
Gaus Width: 3.4070
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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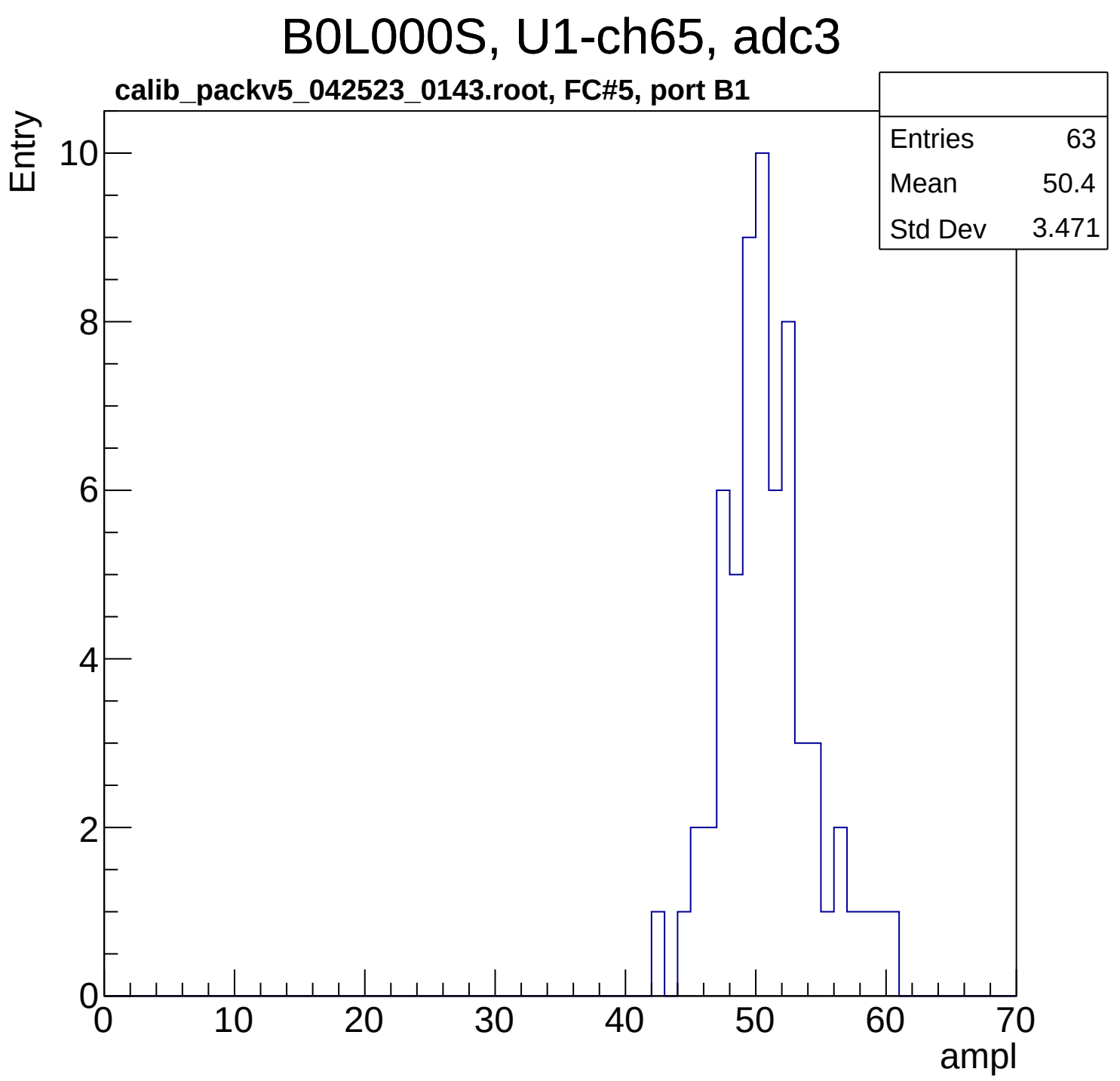
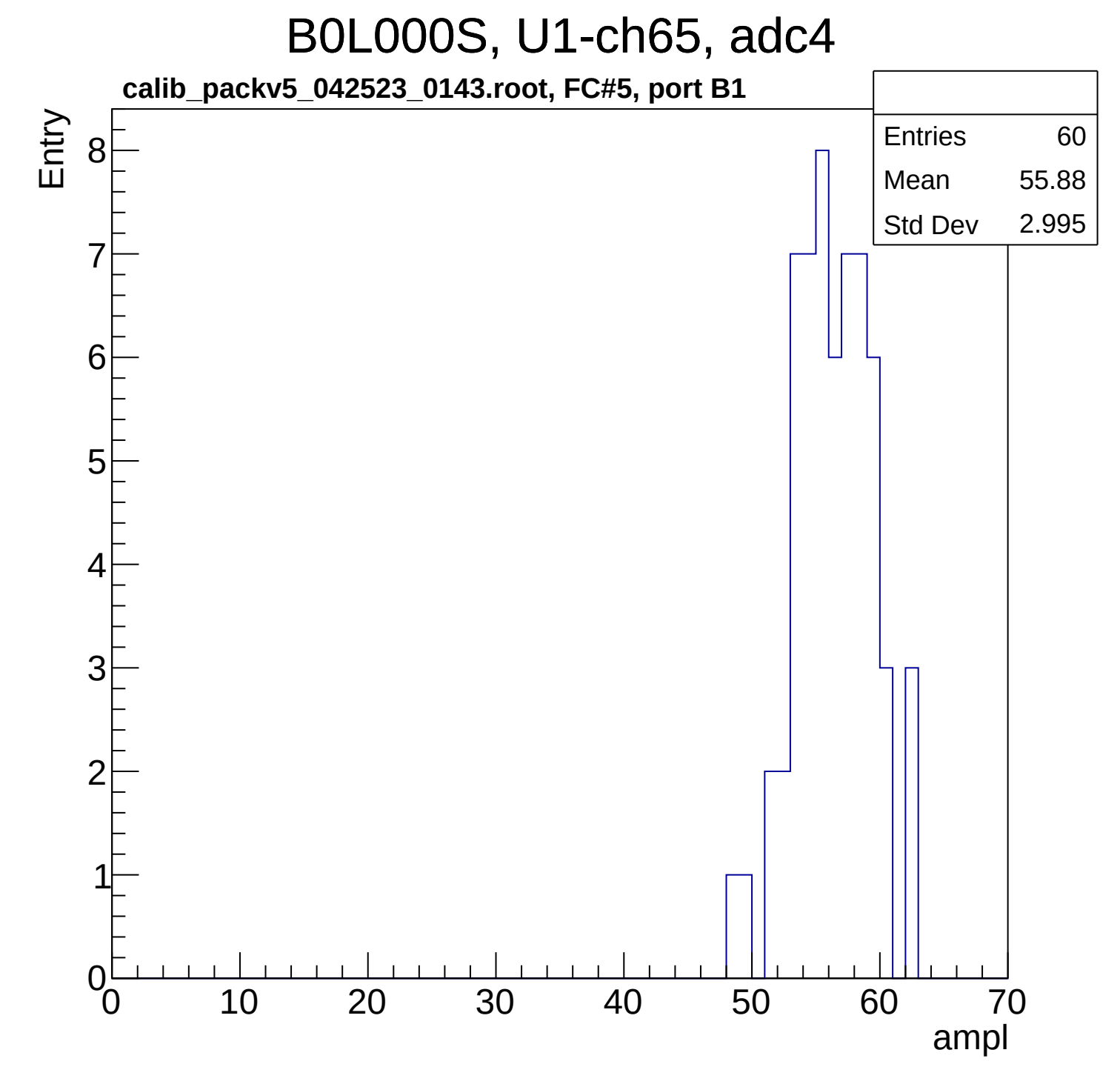

B0L000S, U1-ch65, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 50.4
Std Dev 3.471
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch65, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 55.88
Std Dev 2.995
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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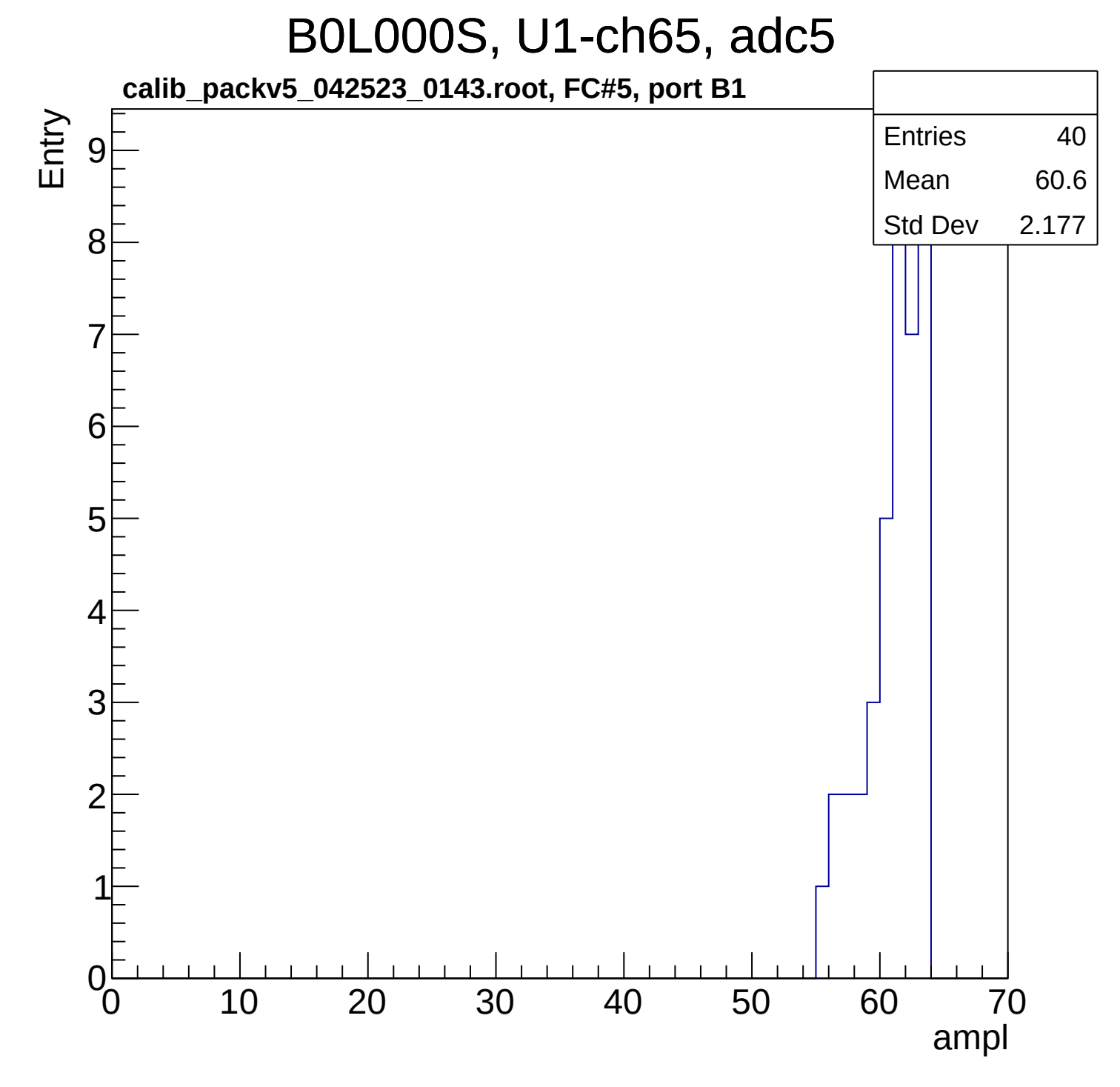
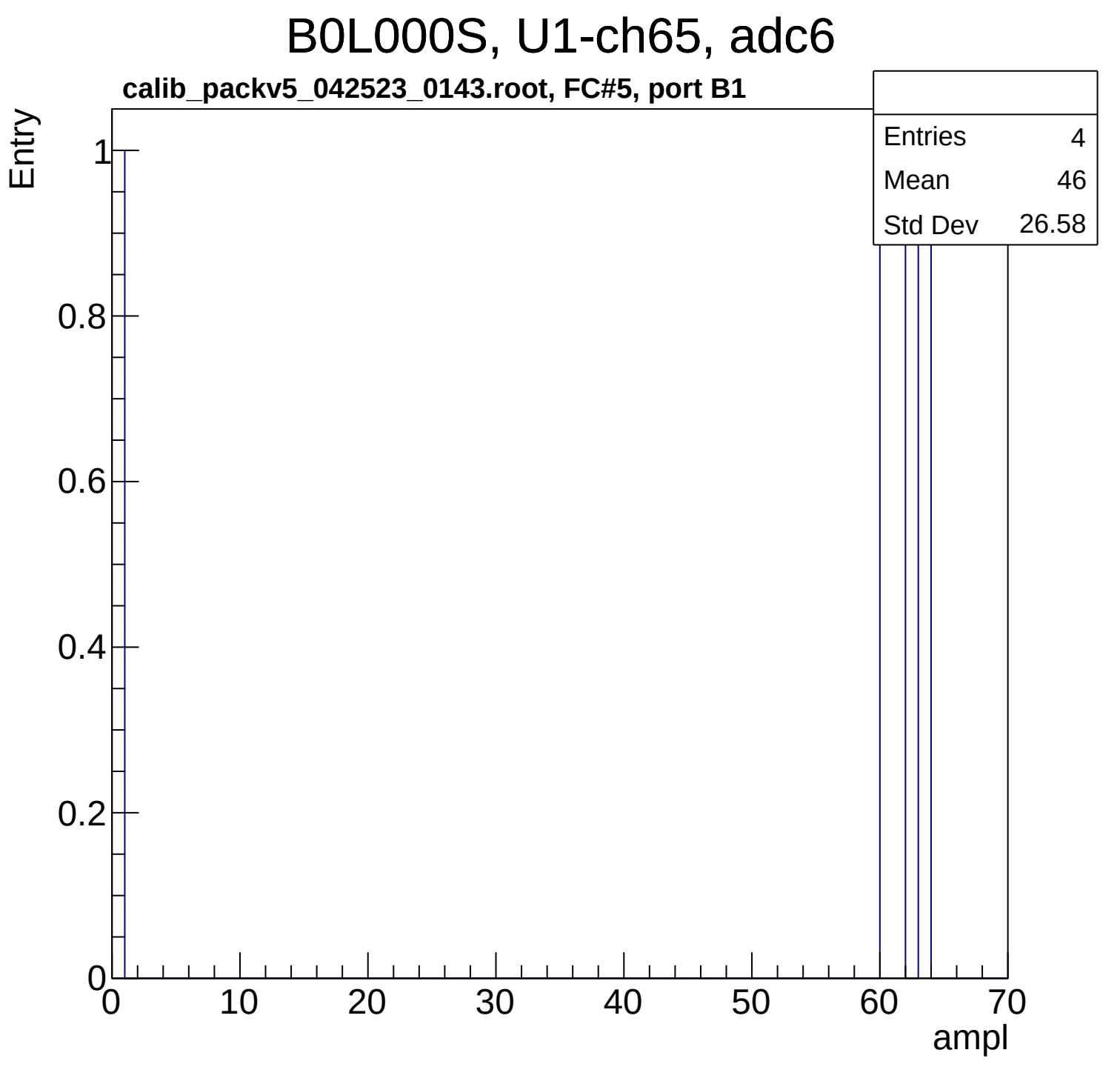
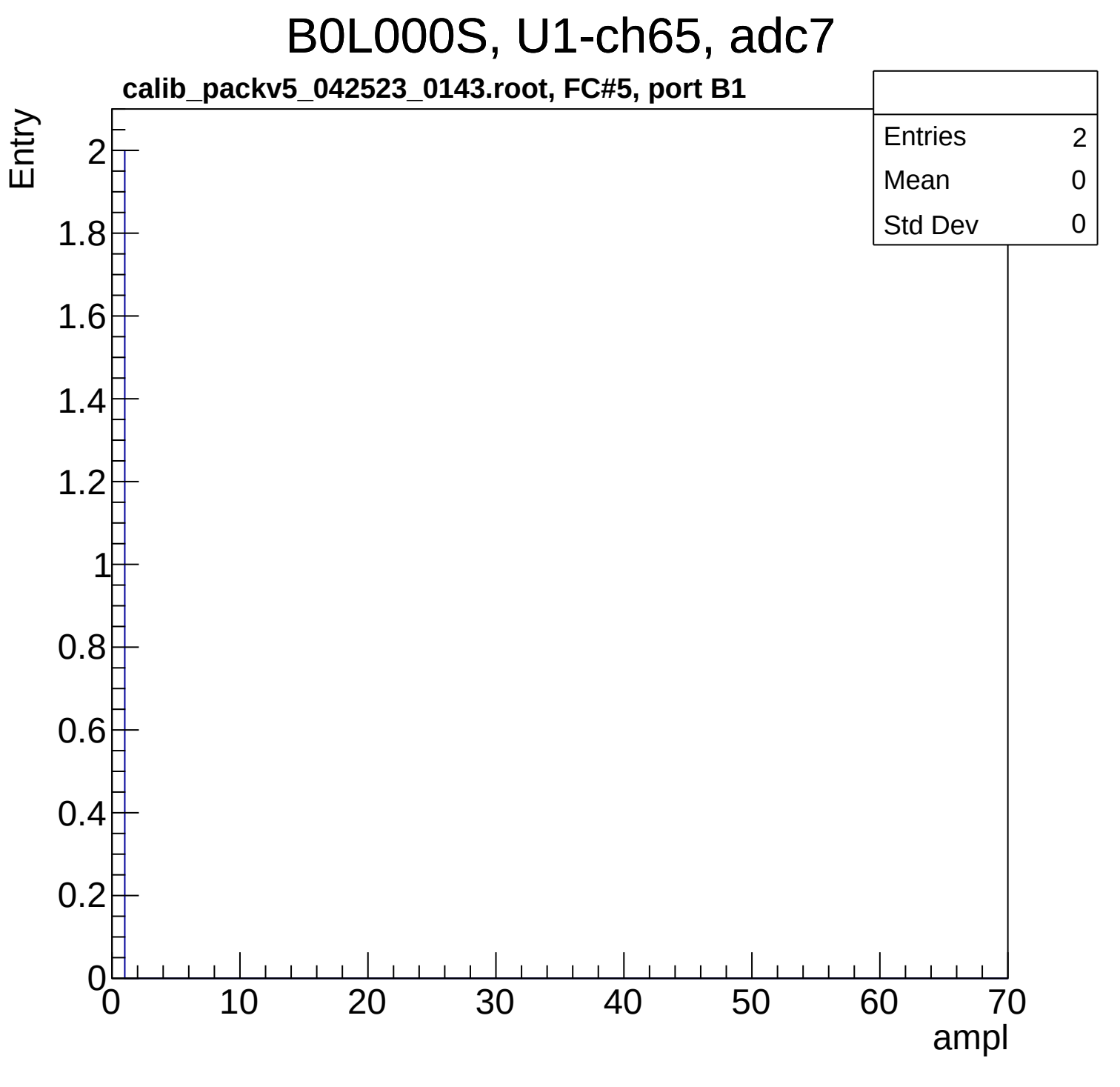
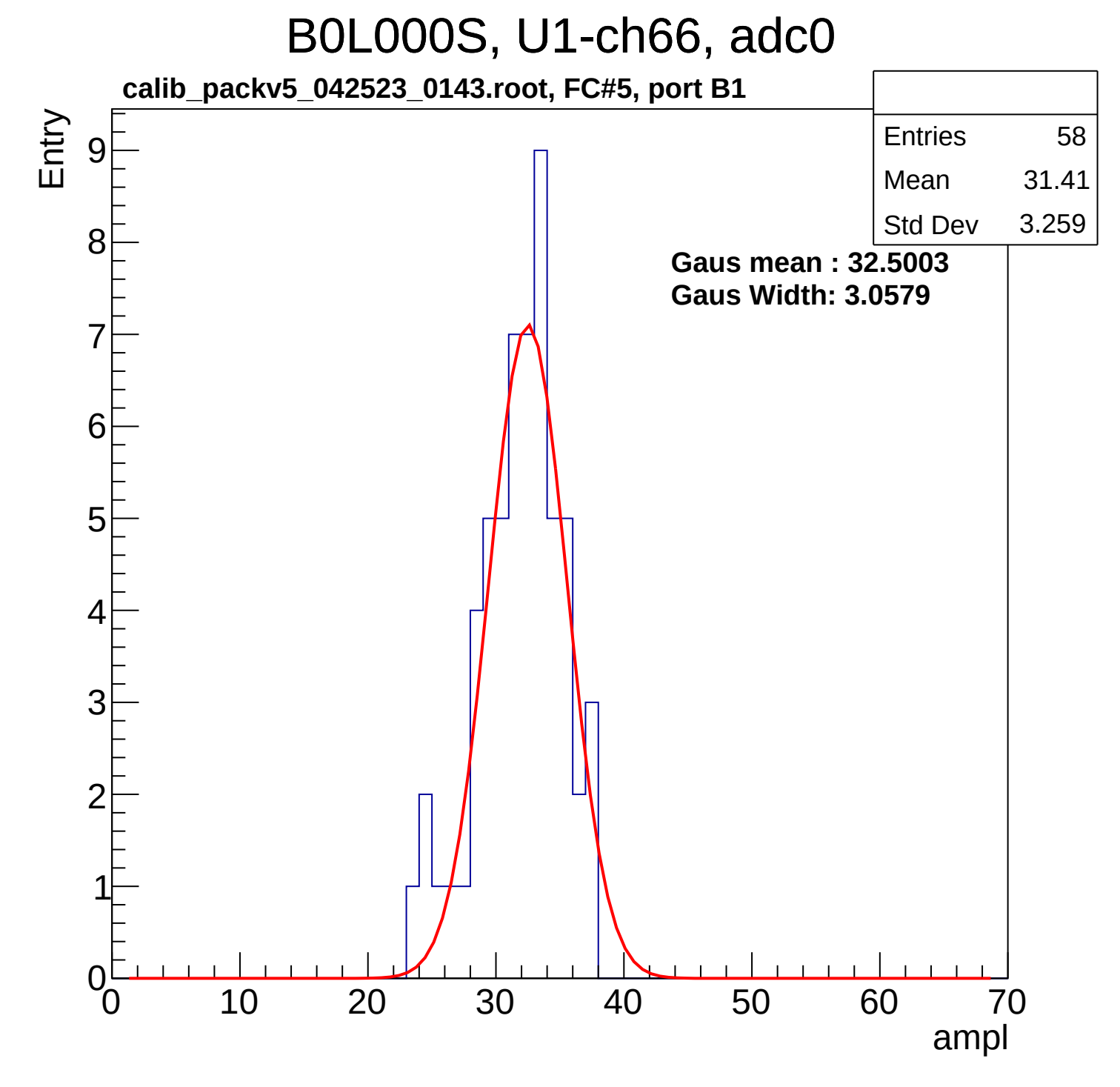
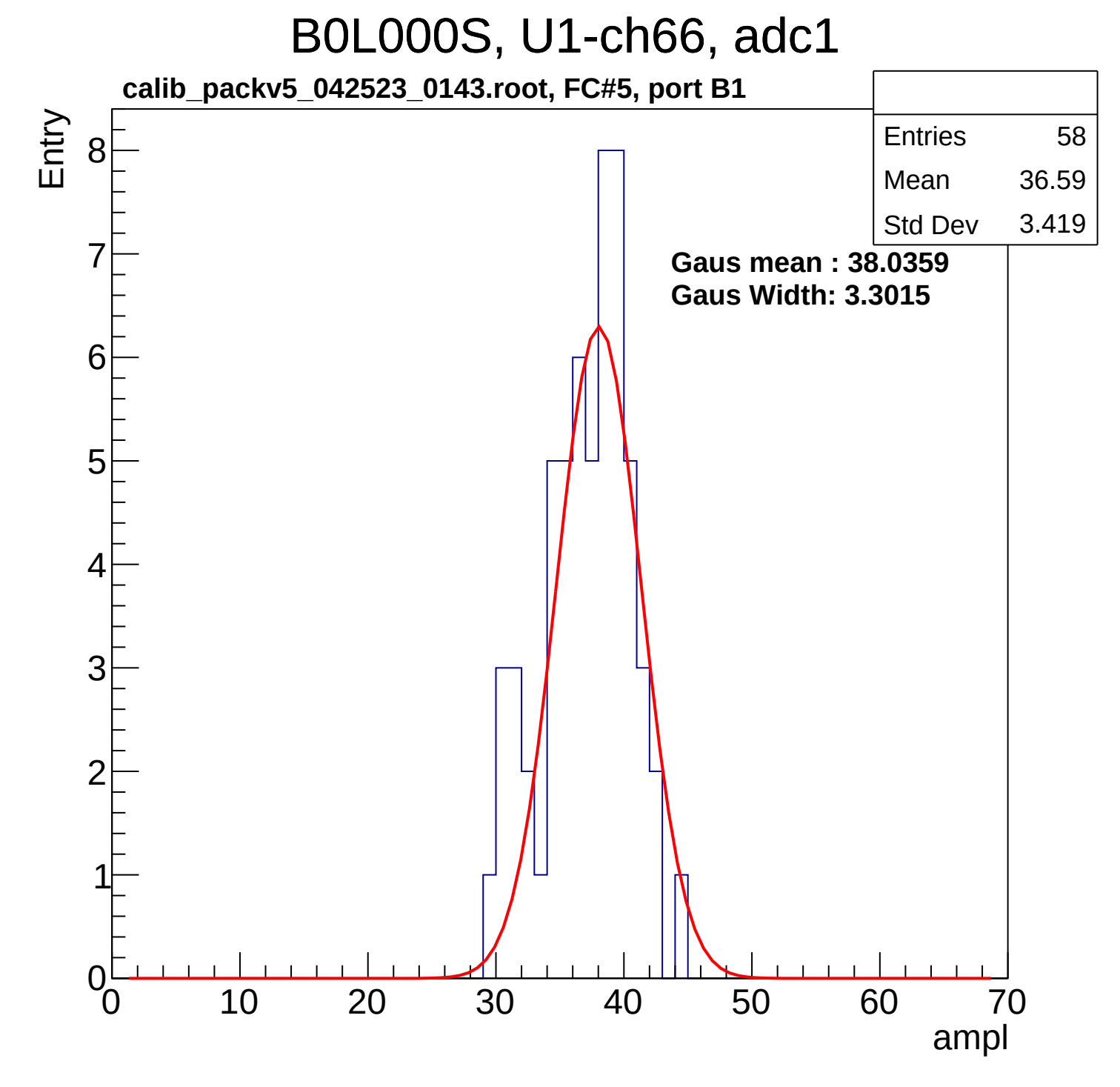
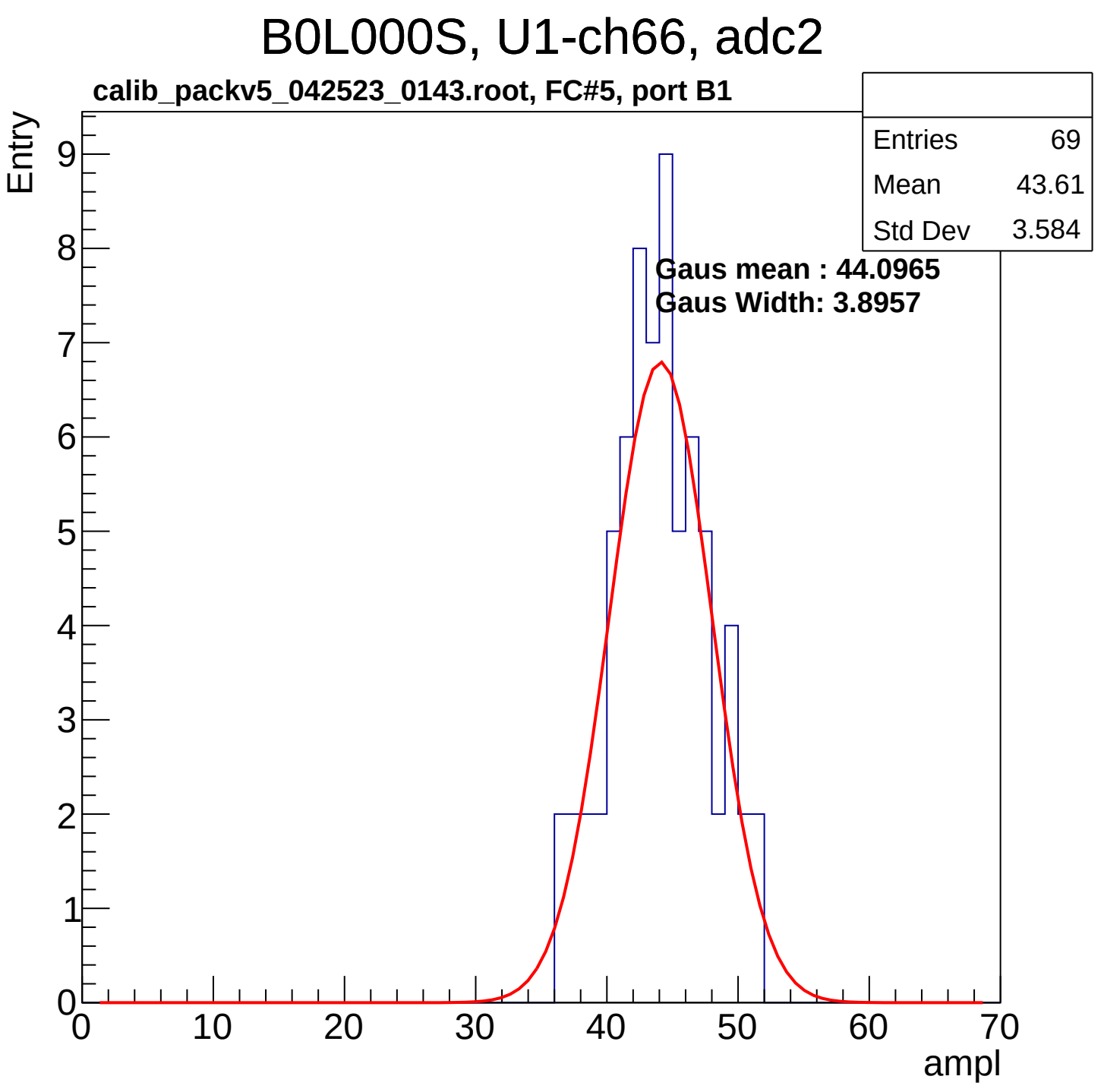

B0L000S, U1-ch65, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 60.6
Std Dev 2.177
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

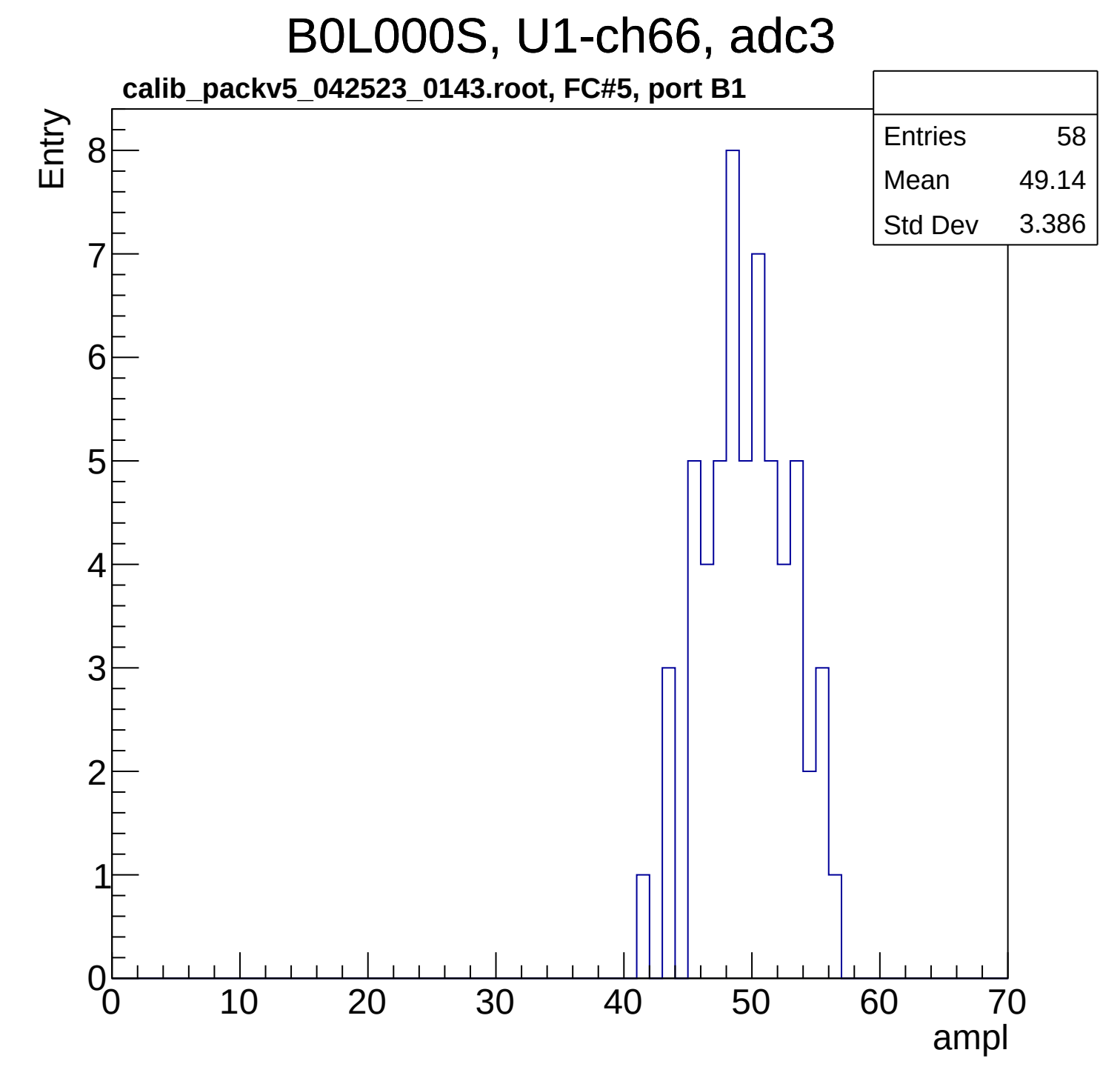

B0L000S, U1-ch65, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46
Std Dev 26.58
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

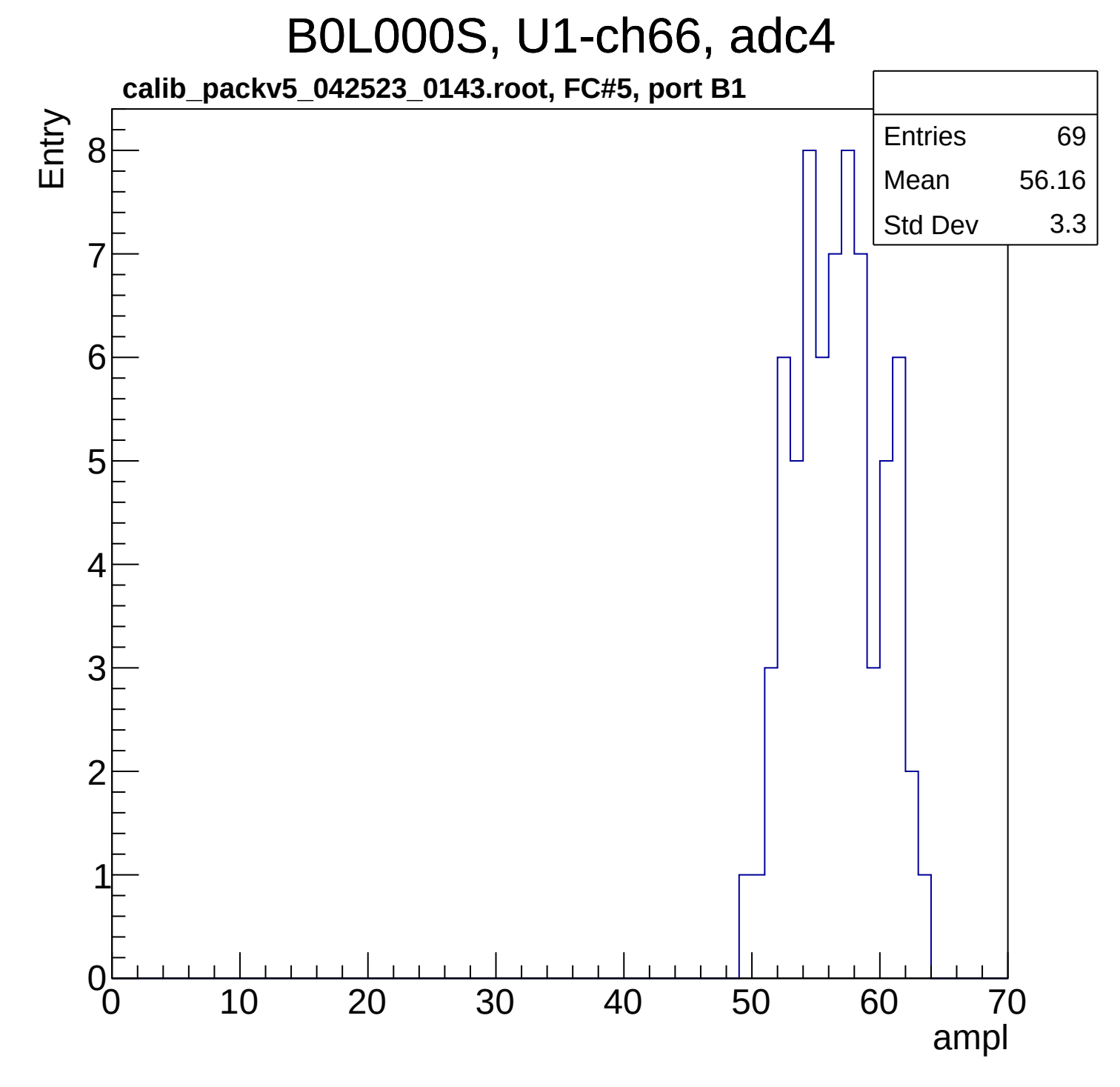

B0L000S, U1-ch65, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

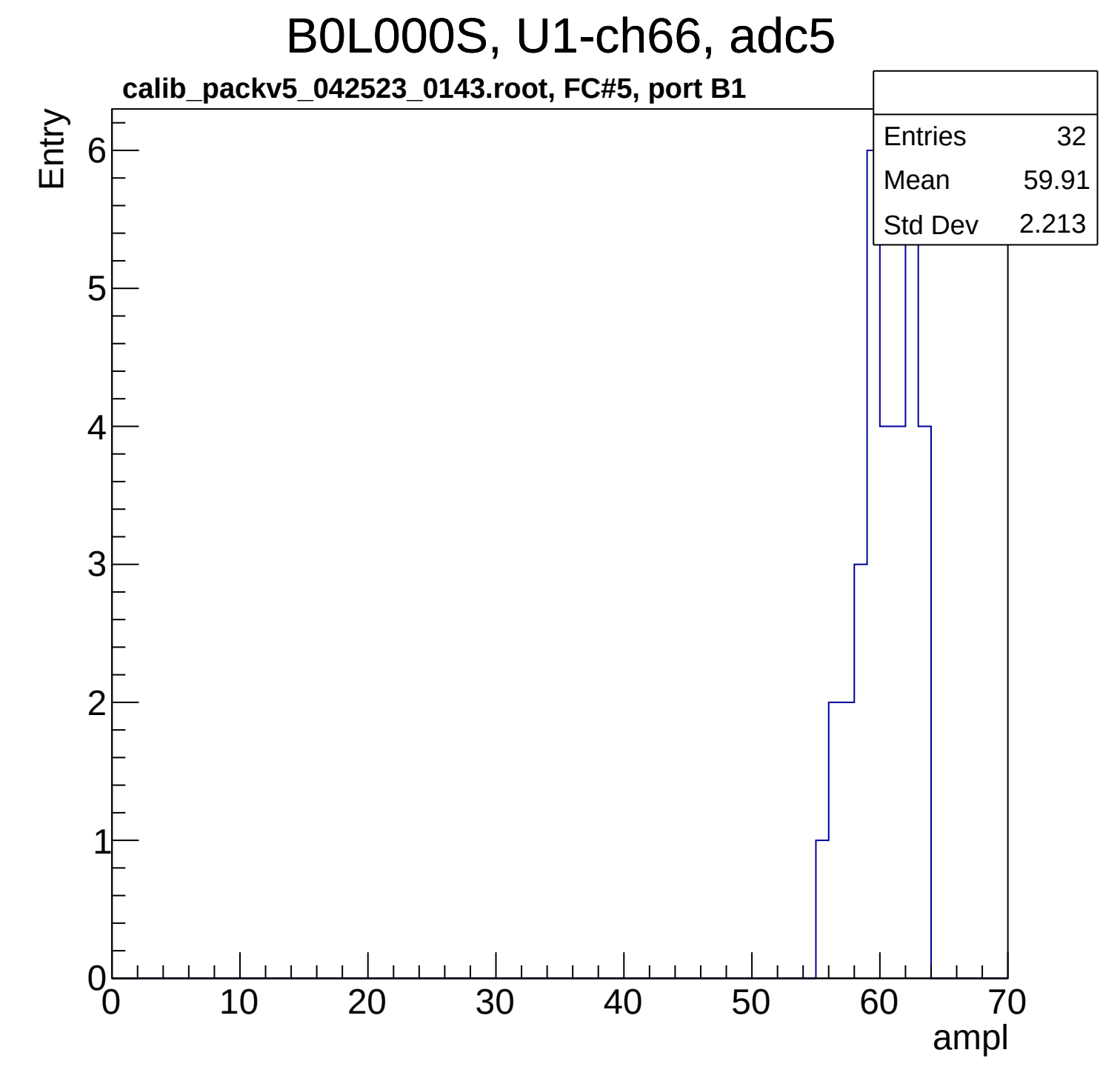

B0L000S, U1-ch66, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 31.41
Std Dev 3.259
Gaus mean : 32.5003
Gaus Width: 3.0579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

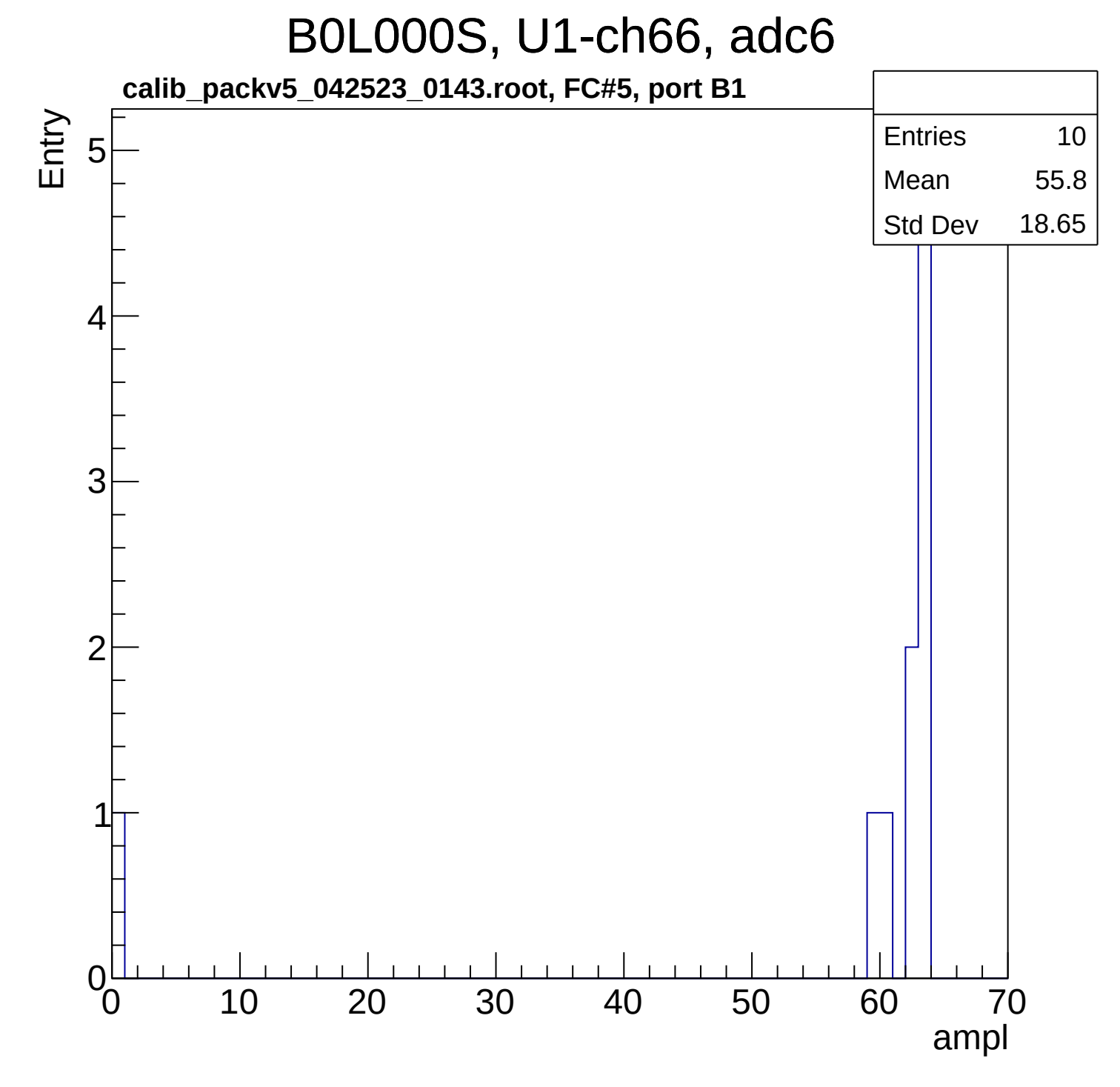

B0L000S, U1-ch66, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 36.59
Std Dev 3.419
Gaus mean : 38.0359
Gaus Width: 3.3015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

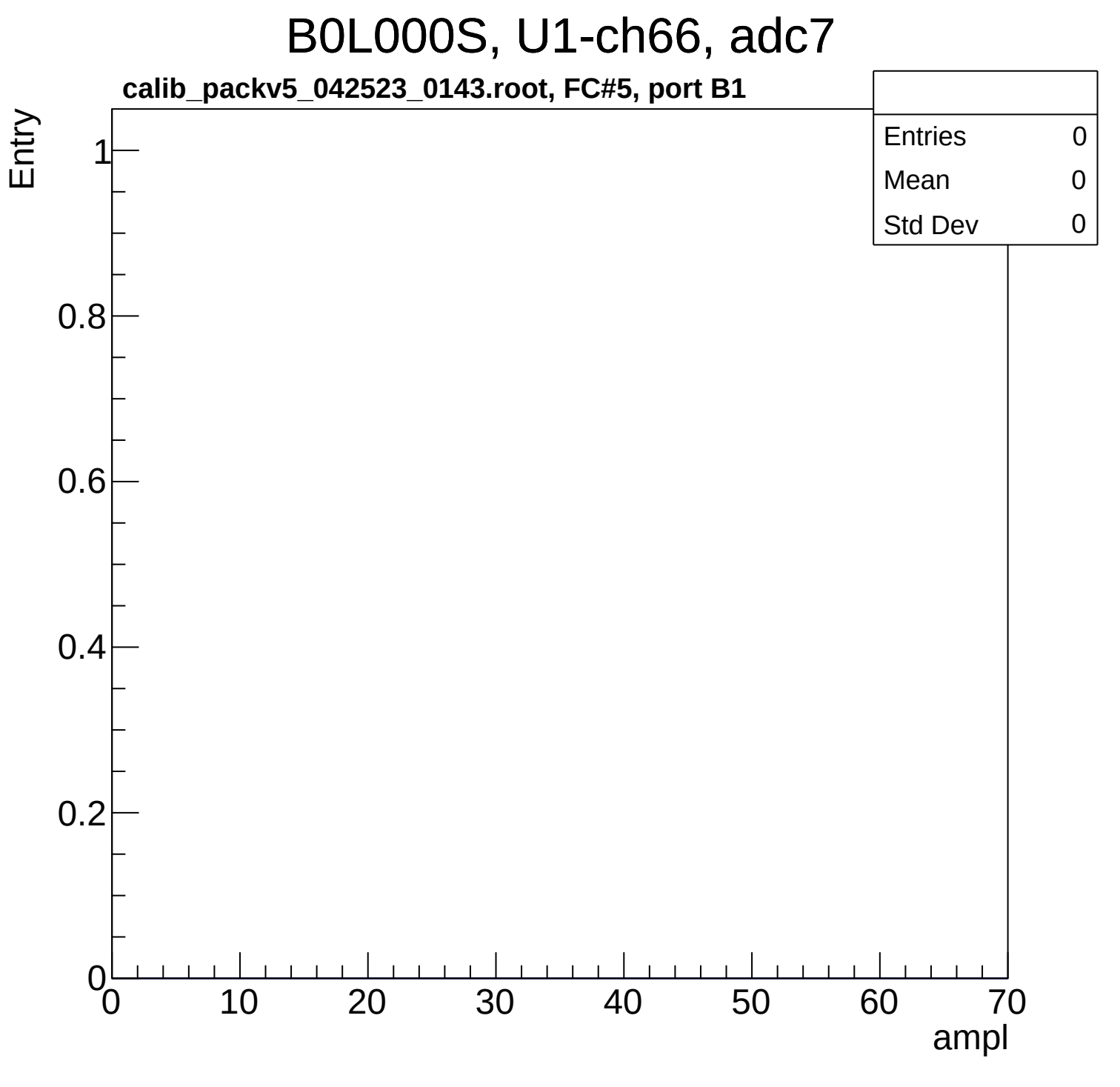

B0L000S, U1-ch66, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.61
Std Dev 3.584
Gaus mean : 44.0965
Gaus Width: 3.8957
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

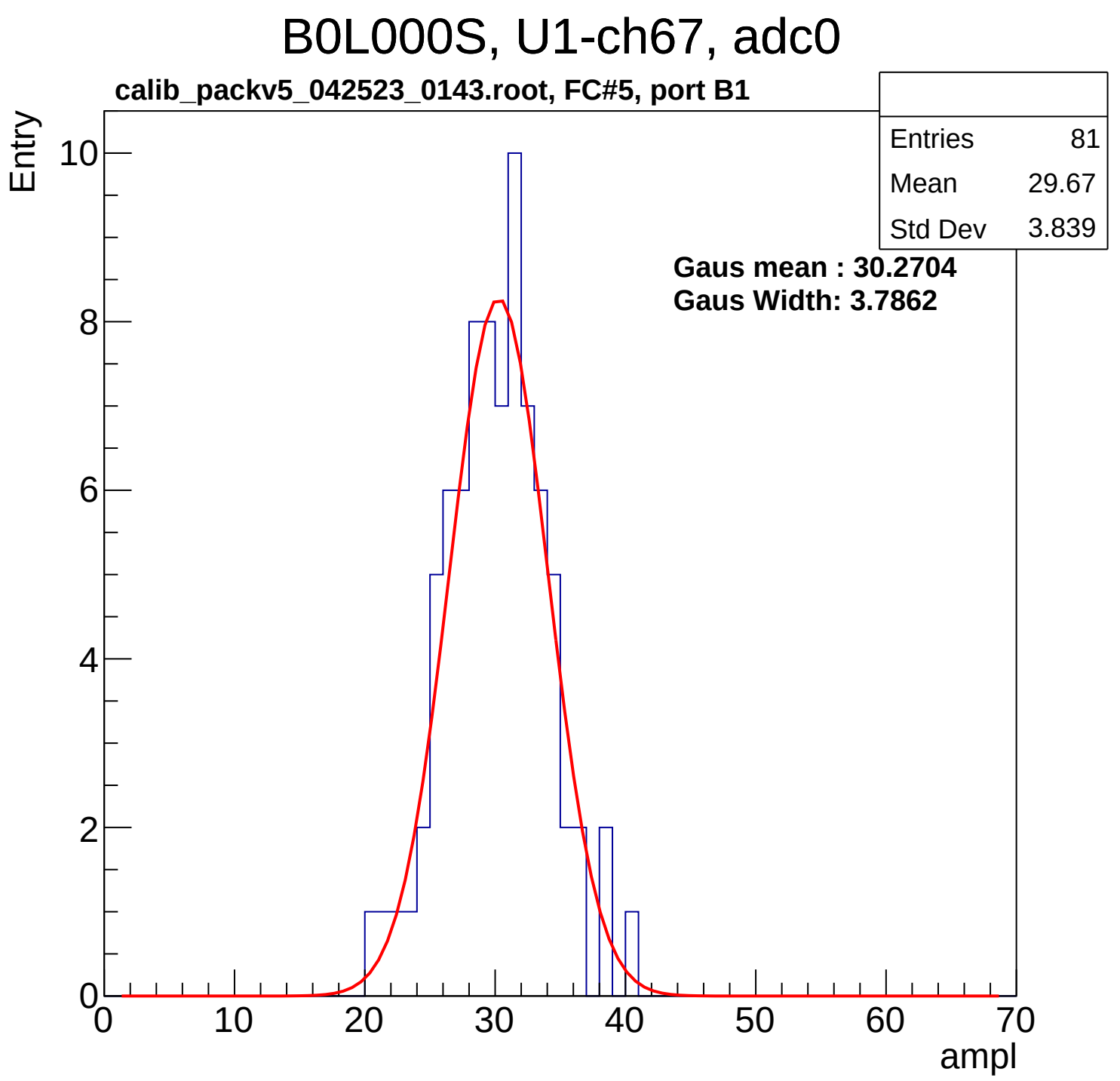

B0L000S, U1-ch66, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 49.14
Std Dev 3.386
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

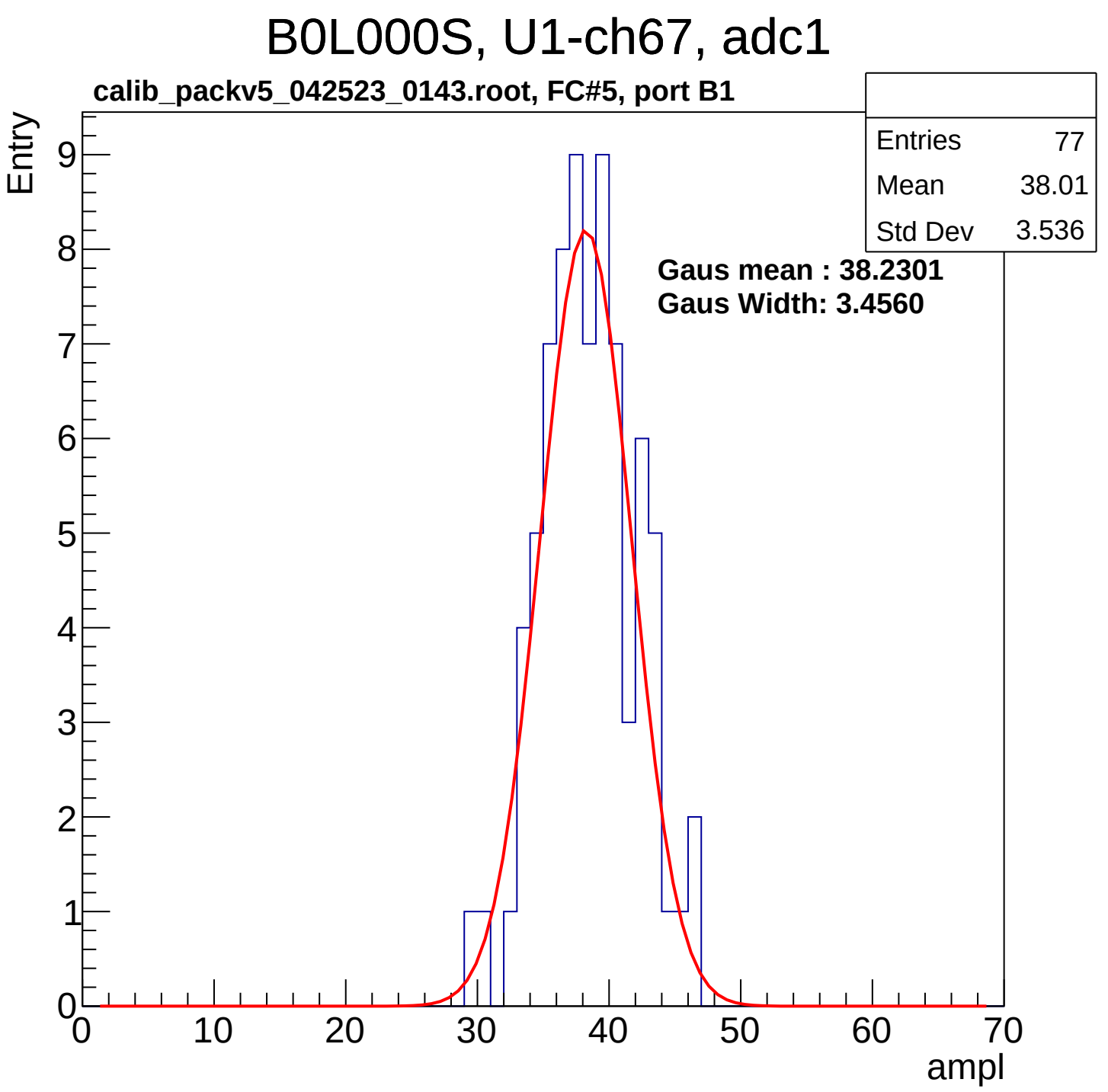

B0L000S, U1-ch66, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 56.16
Std Dev 3.3
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

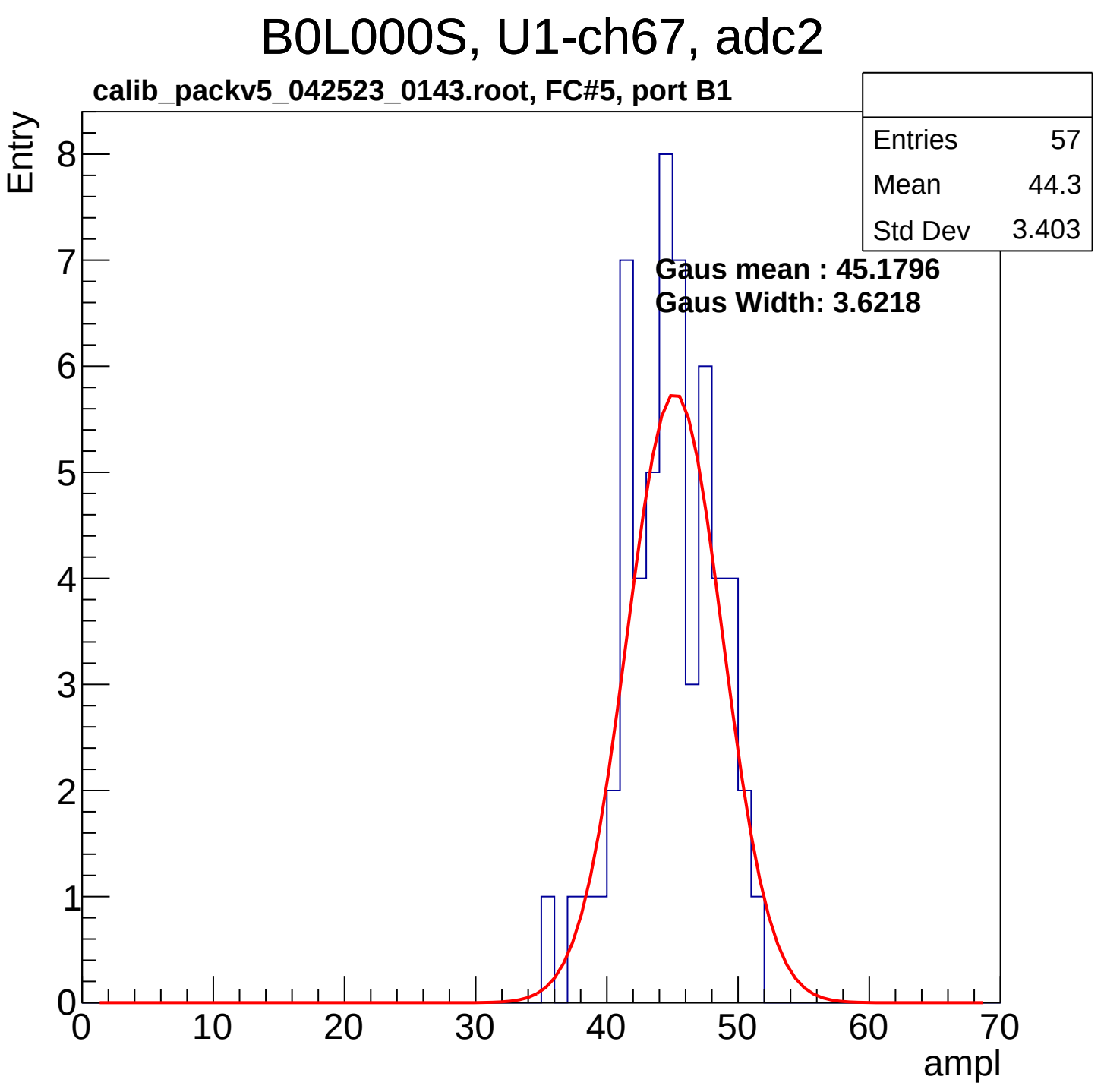

B0L000S, U1-ch66, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 59.91
Std Dev 2.213
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

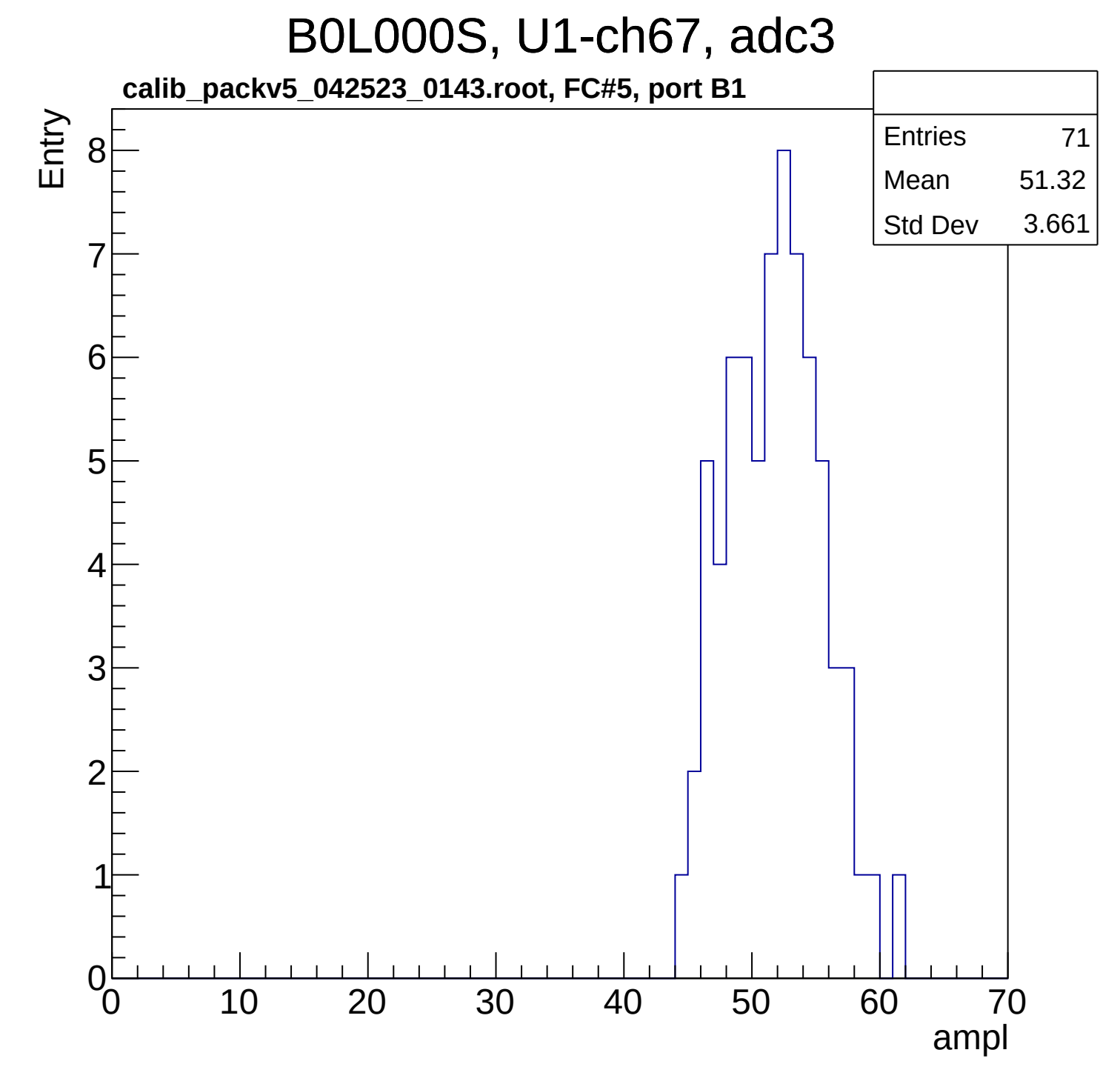

B0L000S, U1-ch66, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.8
Std Dev 18.65
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

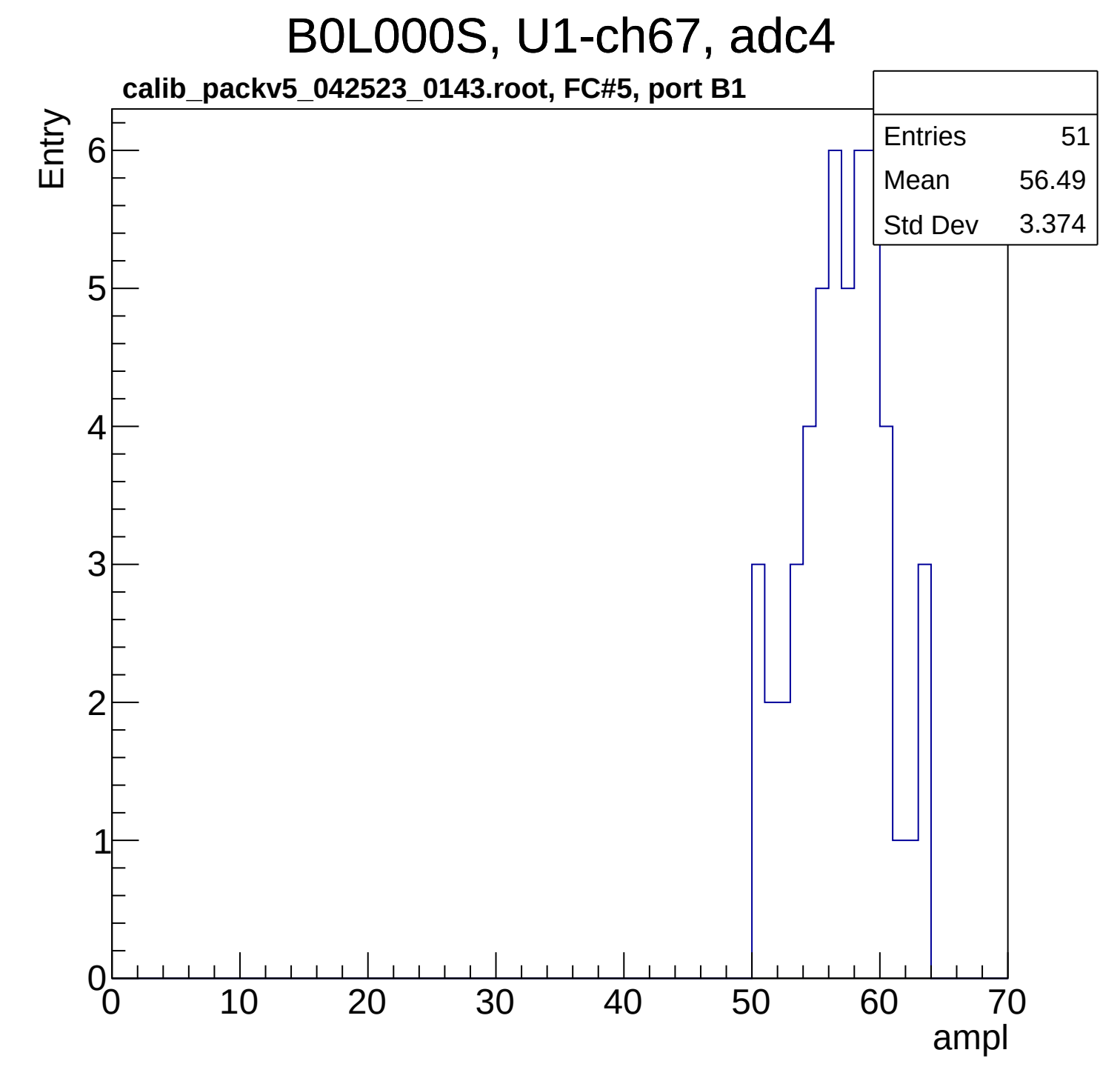

B0L000S, U1-ch66, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

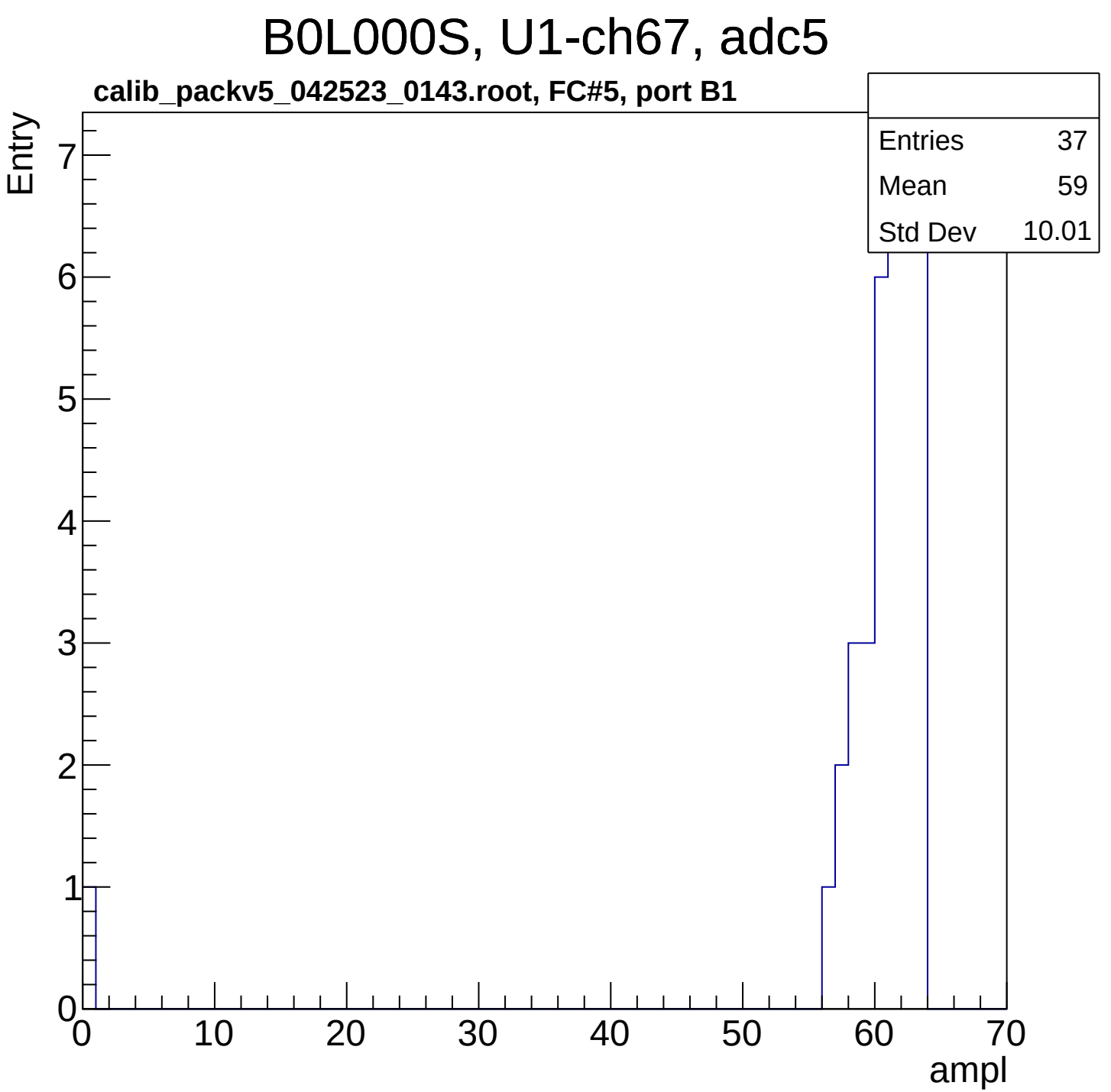

B0L000S, U1-ch67, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 29.67
Std Dev 3.839
Gaus mean : 30.2704
Gaus Width: 3.7862
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

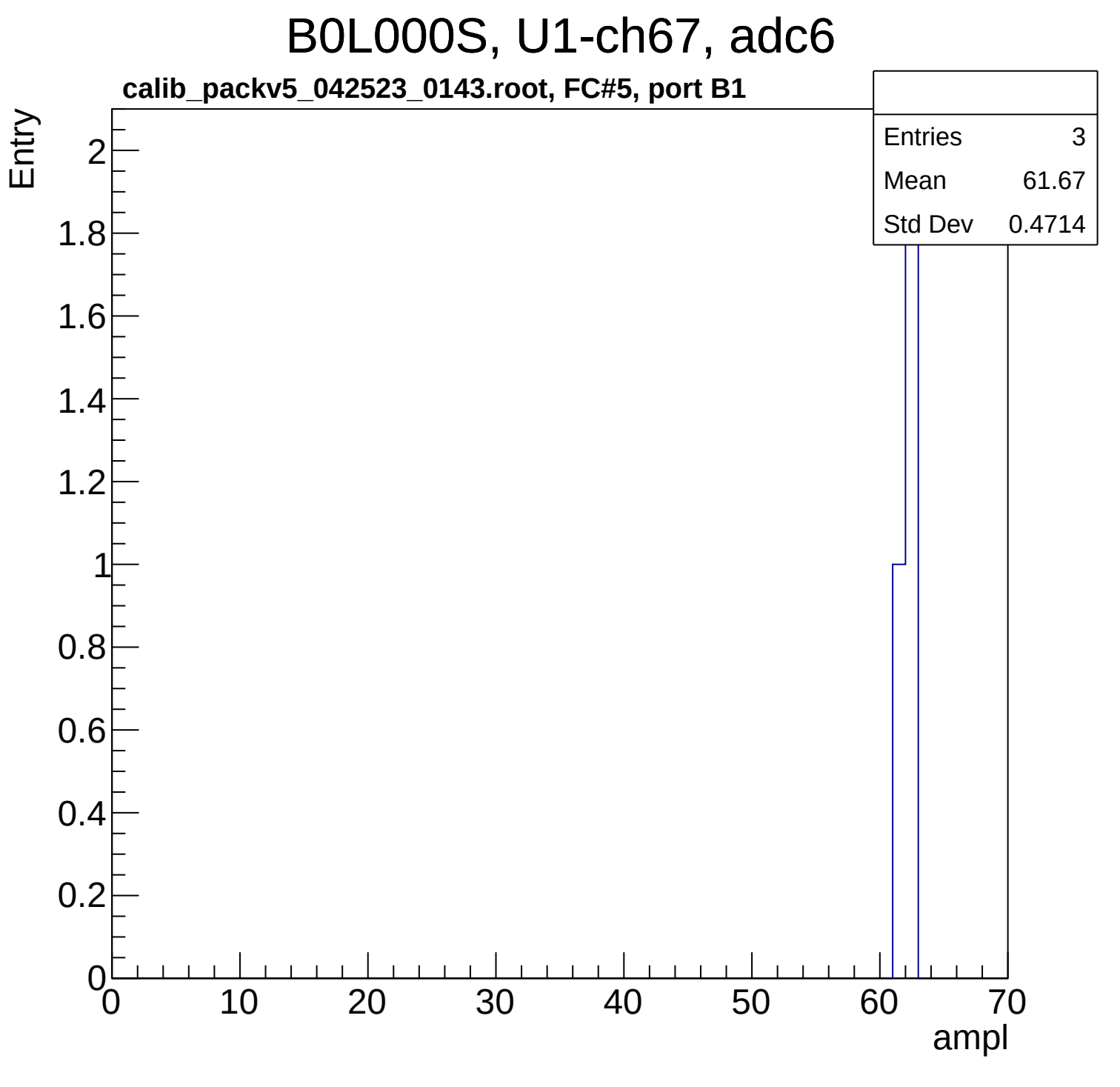

B0L000S, U1-ch67, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 38.01
Std Dev 3.536
Gaus mean : 38.2301
Gaus Width: 3.4560
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

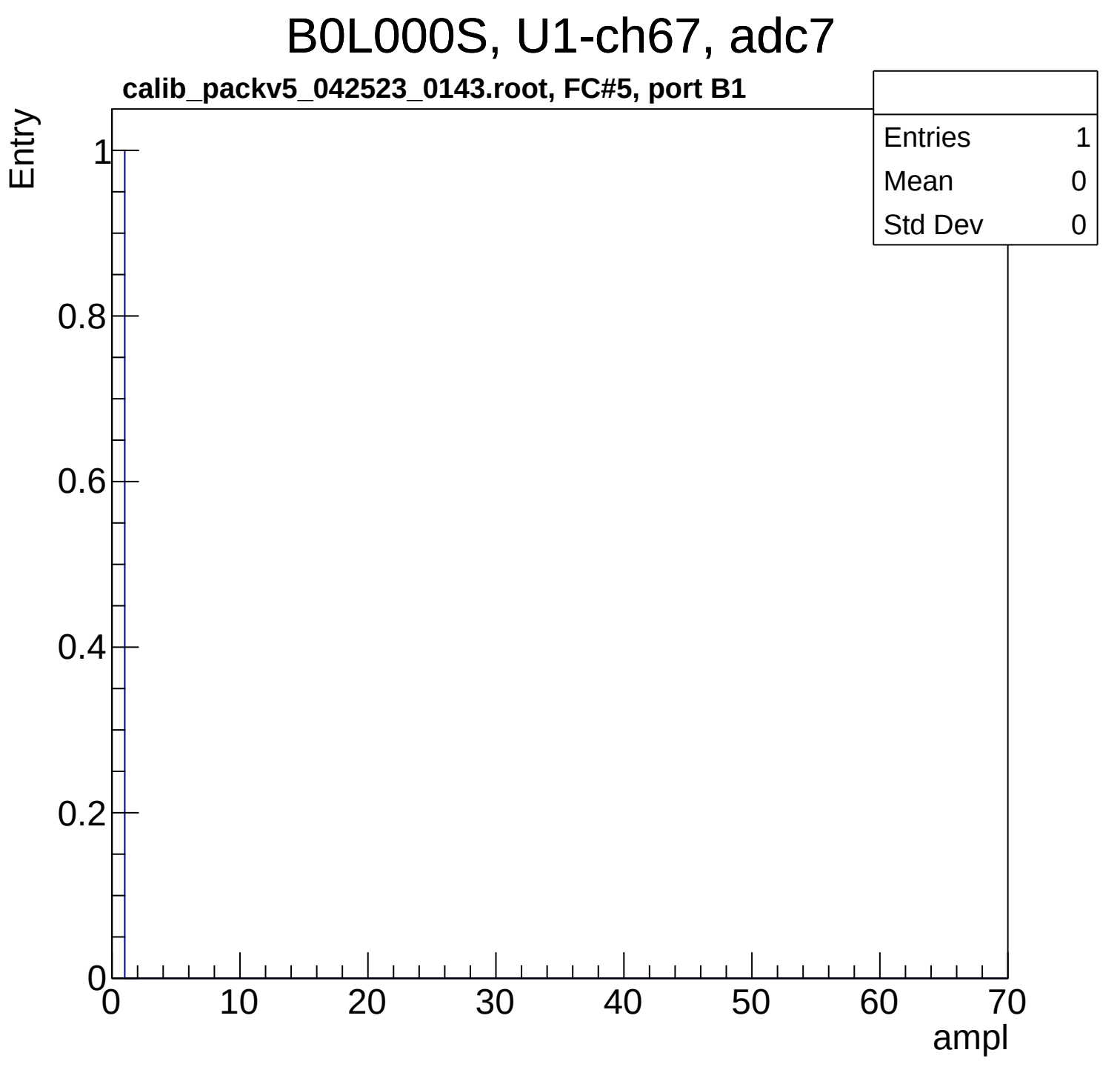

B0L000S, U1-ch67, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 44.3
Std Dev 3.403
Gaus mean : 45.1796
Gaus Width: 3.6218
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

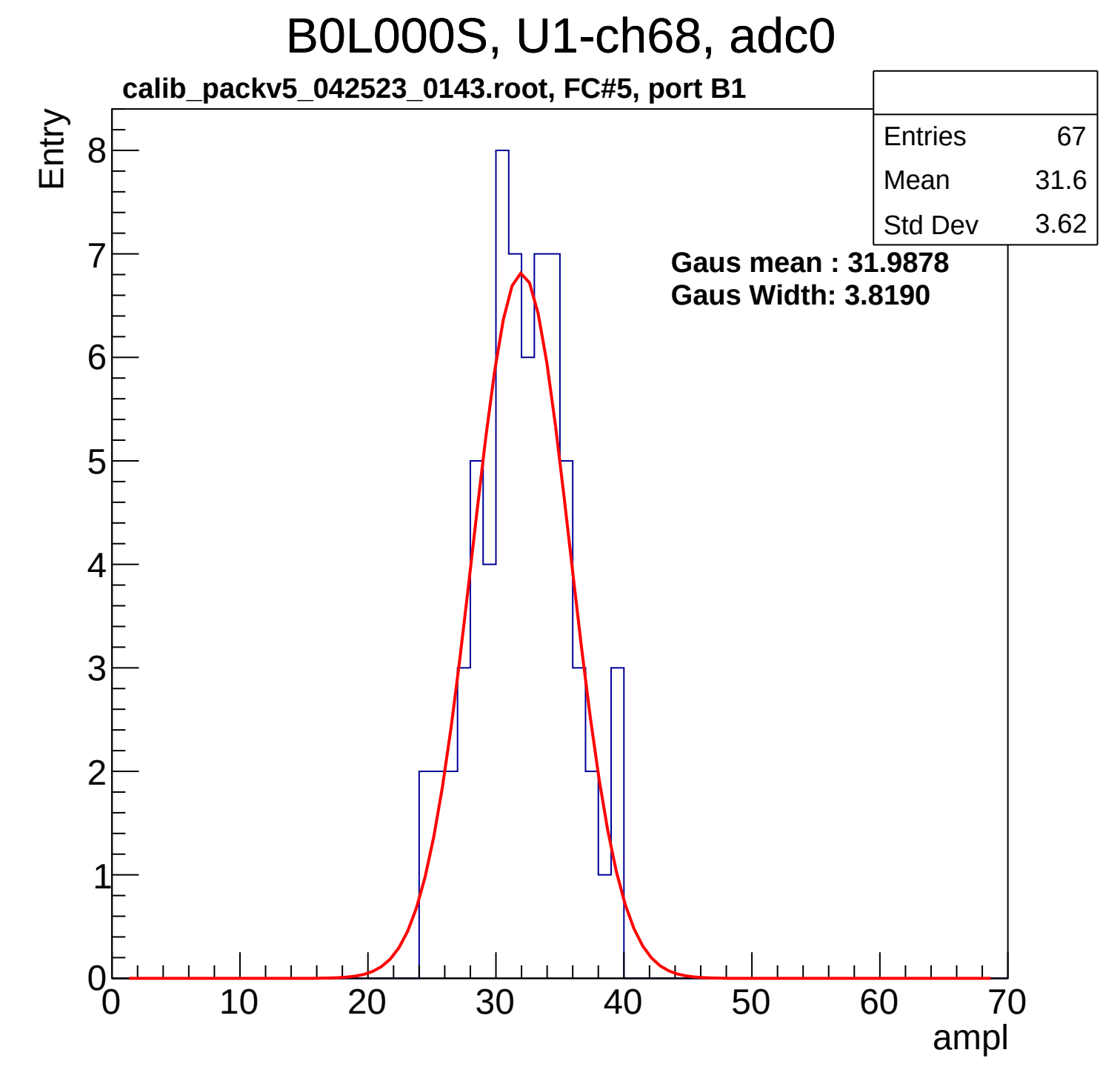

B0L000S, U1-ch67, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 51.32
Std Dev 3.661
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

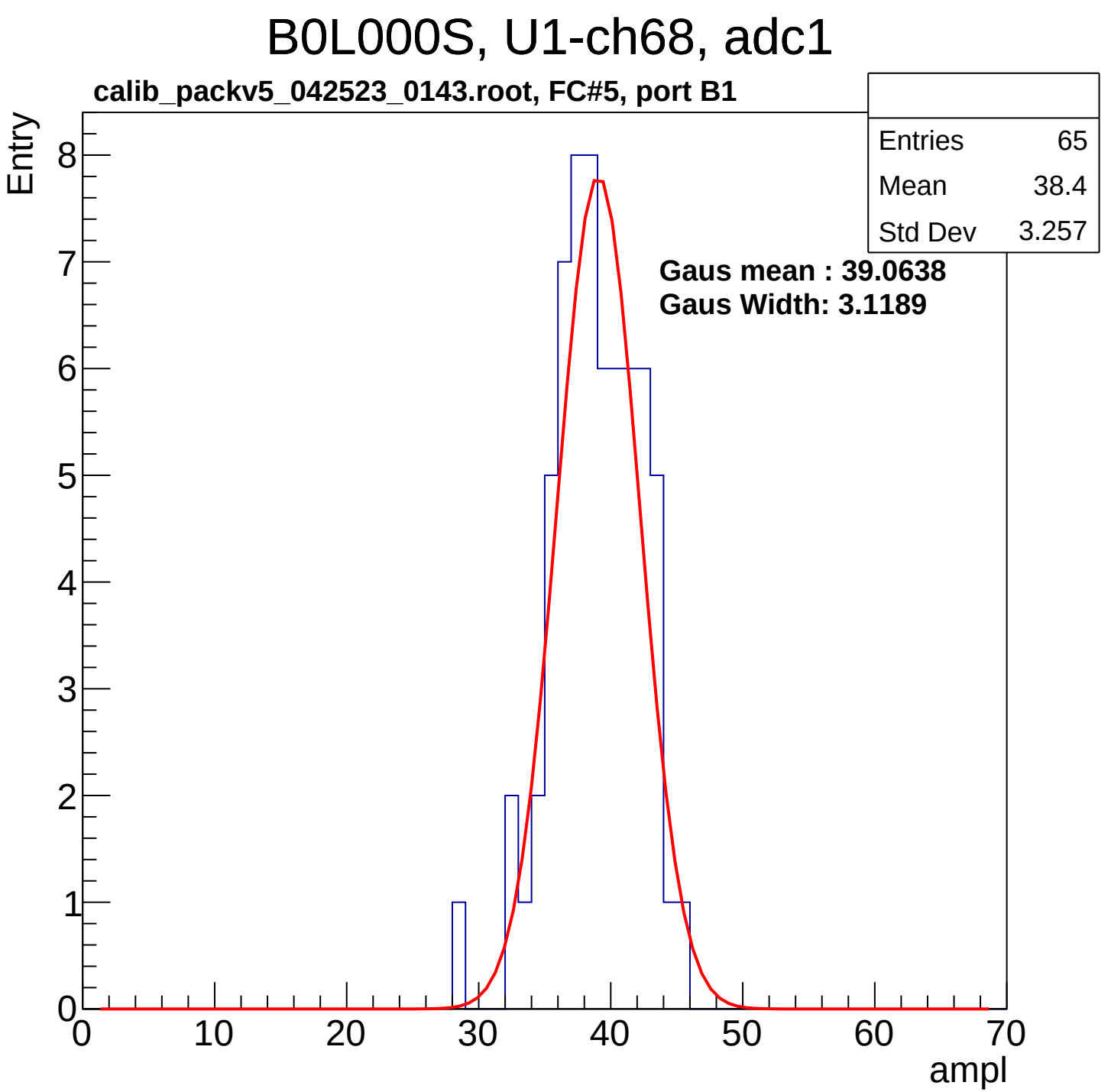

B0L000S, U1-ch67, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.49
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

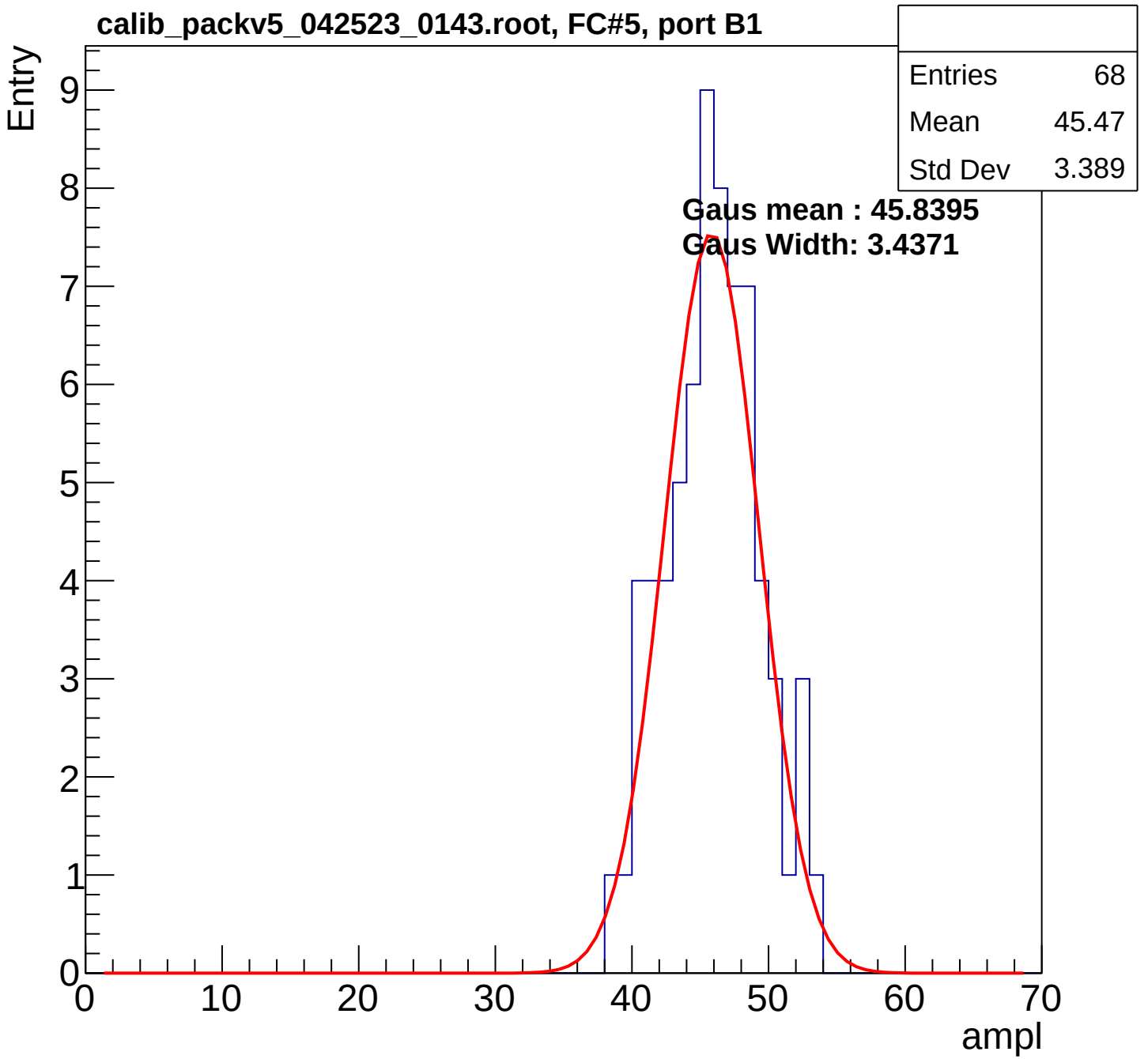

B0L000S, U1-ch67, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59
Std Dev 10.01
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

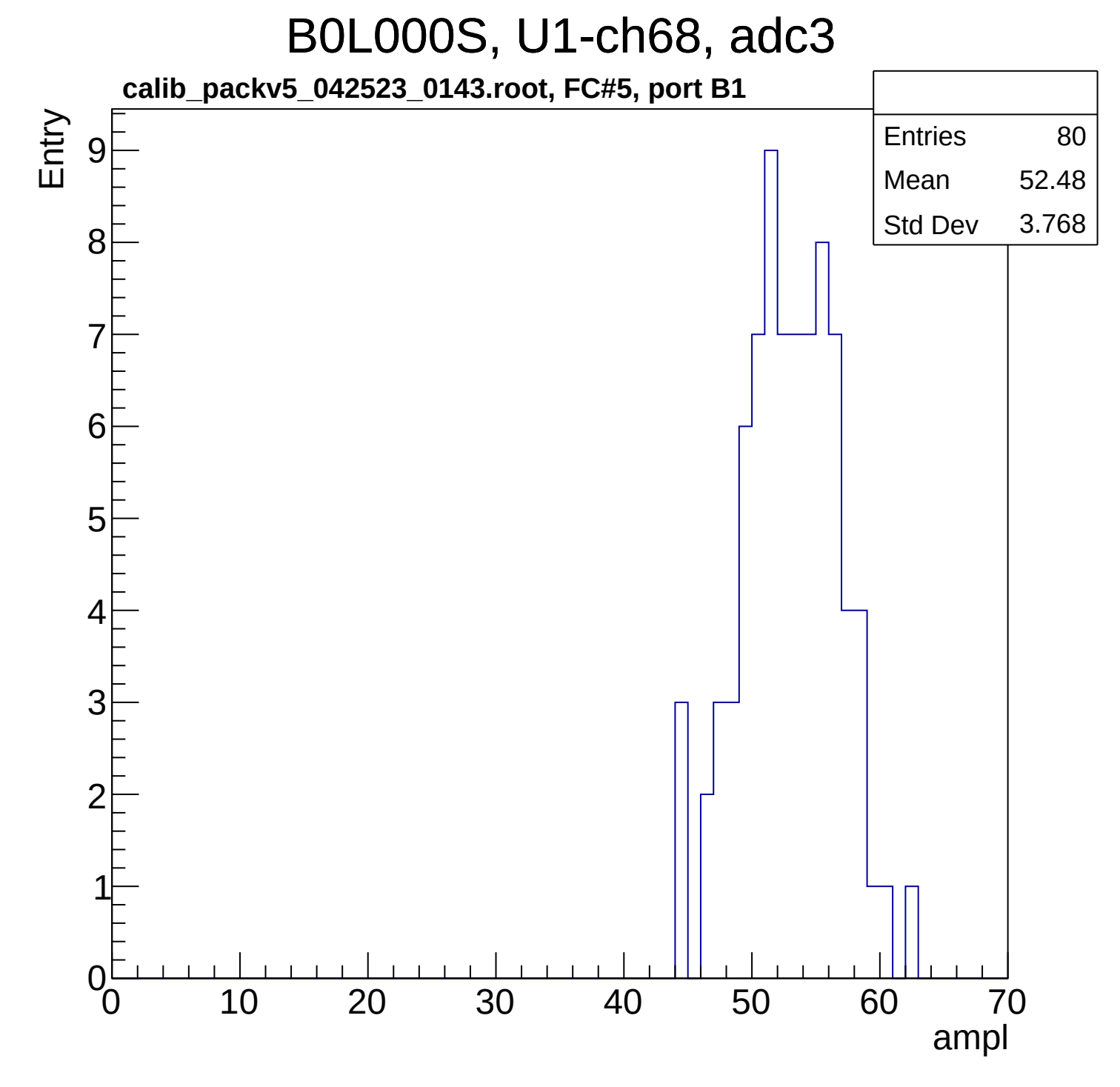

B0L000S, U1-ch67, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch67, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch68, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 31.6
Std Dev 3.62
Gaus mean : 31.9878
Gaus Width: 3.8190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch68, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 38.4
Std Dev 3.257
Gaus mean : 39.0638
Gaus Width: 3.1189
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch68, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 45.47
Std Dev 3.389
Gaus mean : 45.8395
Gaus Width: 3.4371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch68, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 52.48
Std Dev 3.768
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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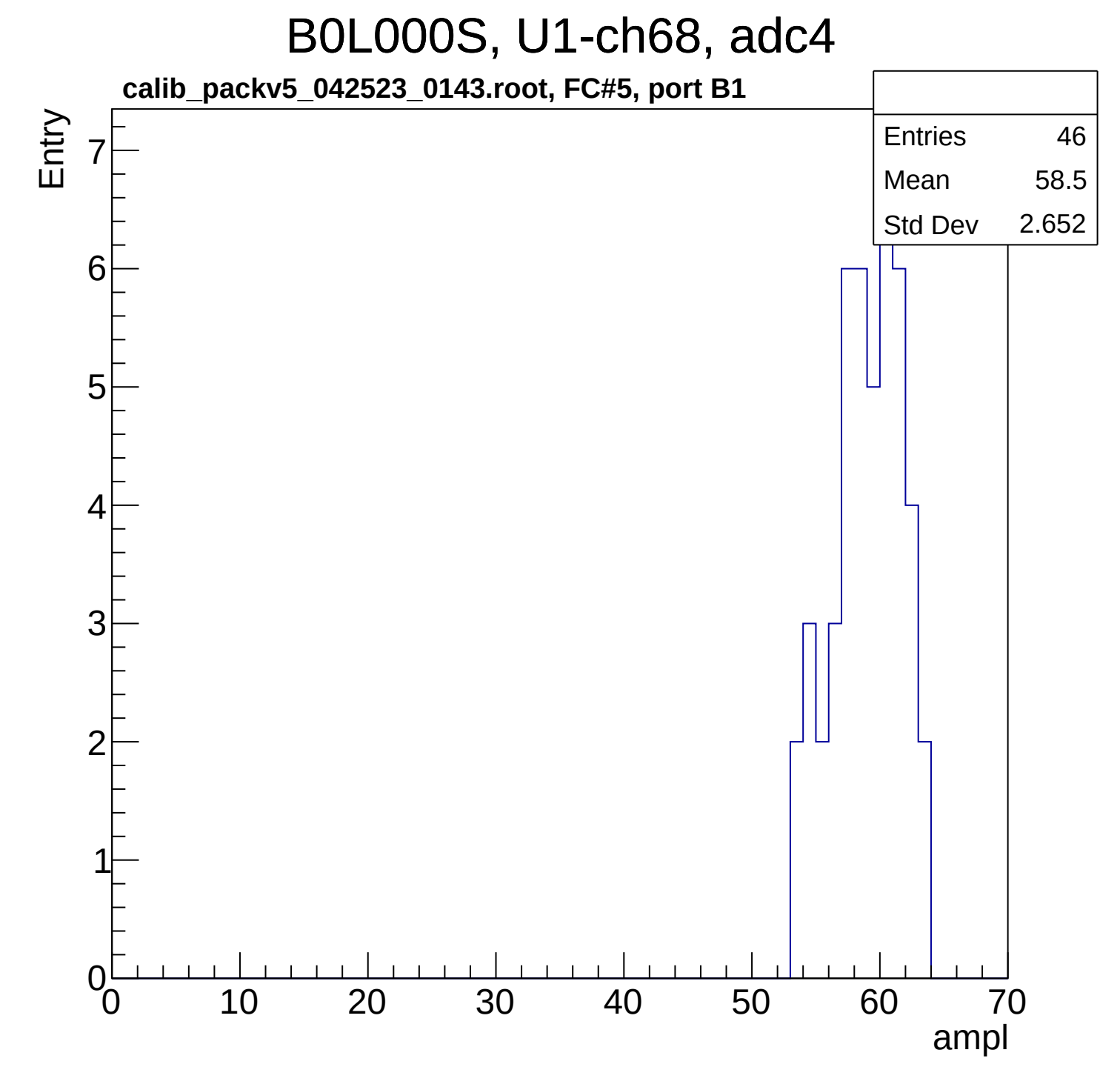
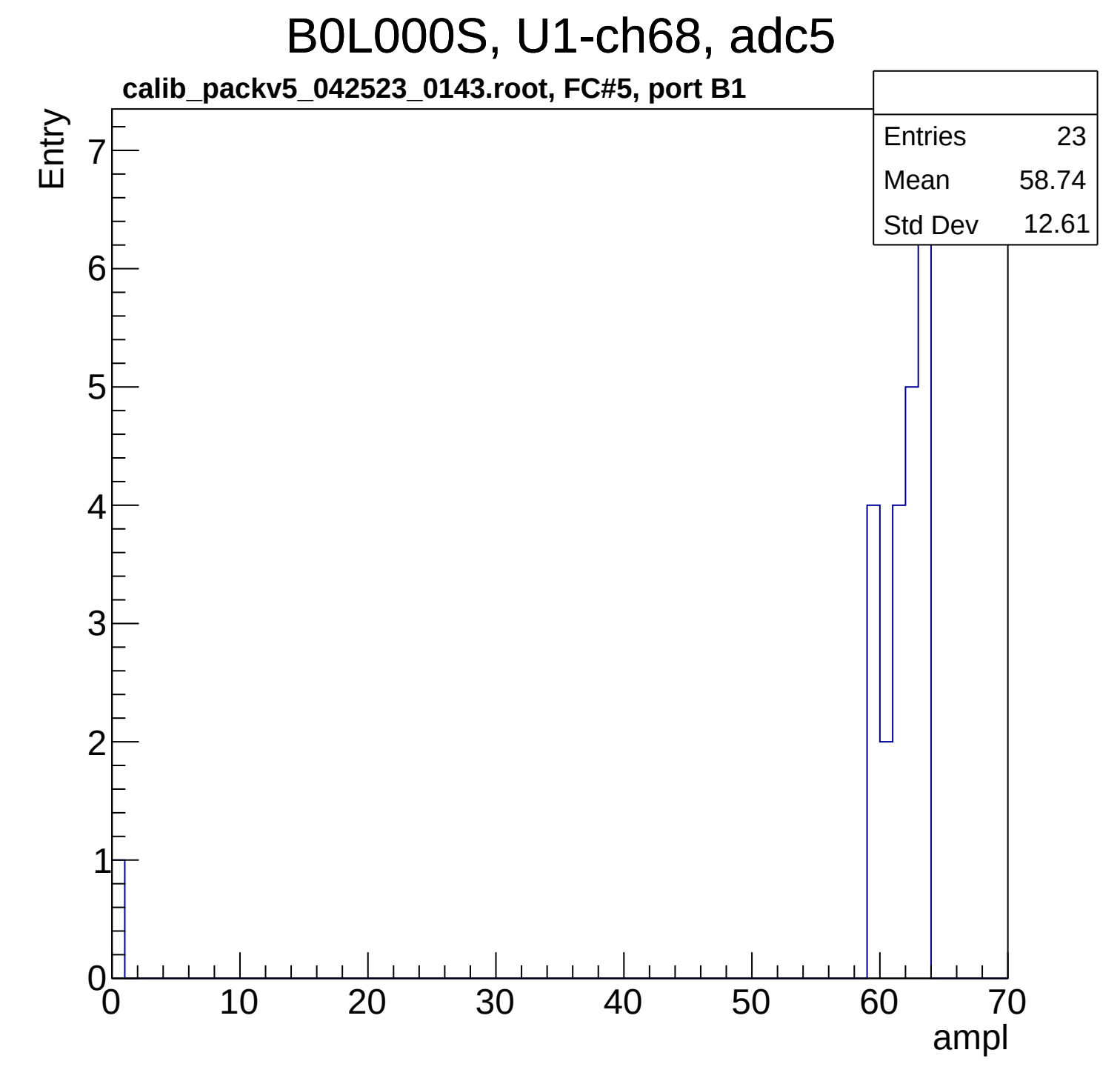
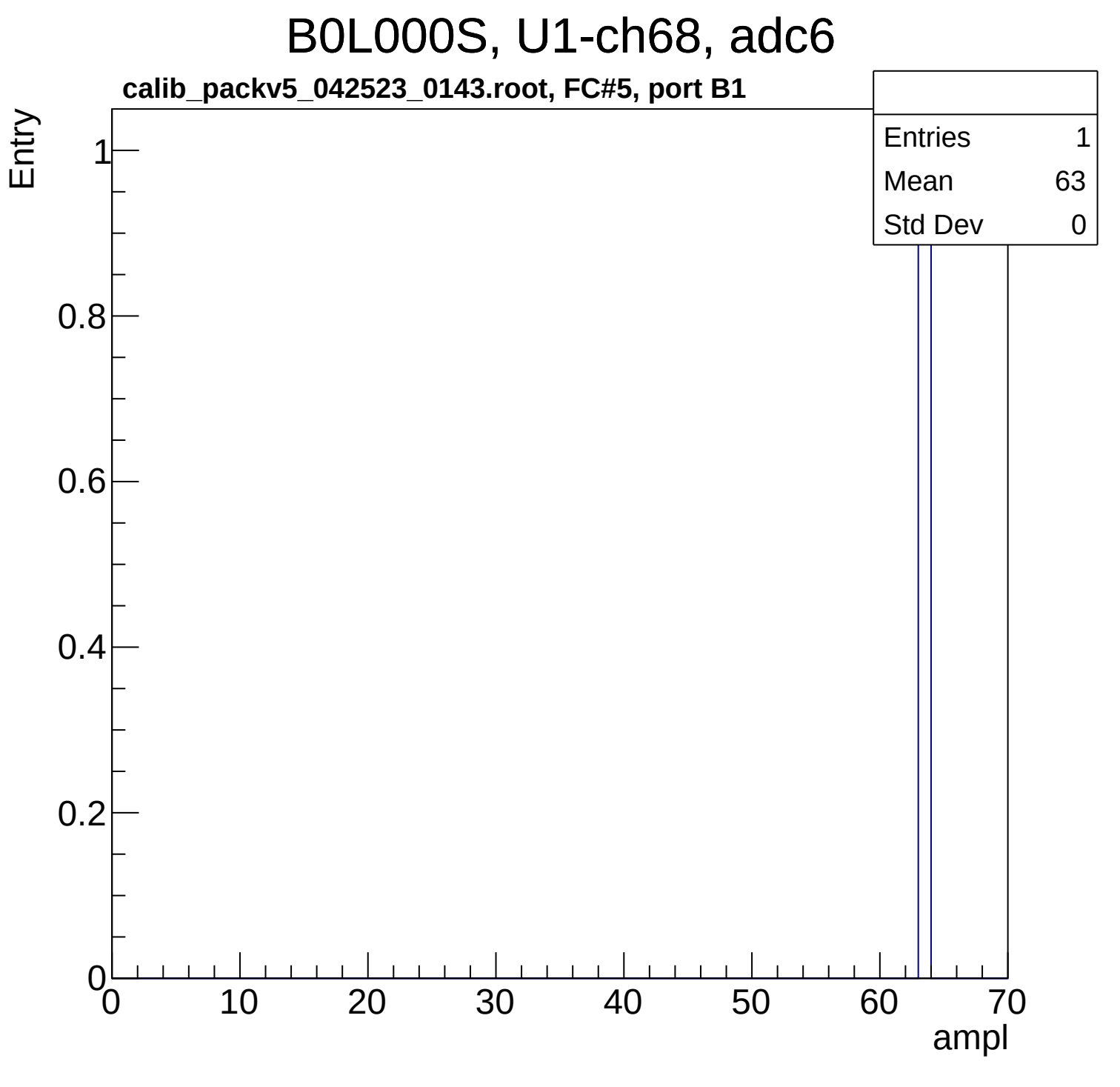
40
50
60
70


B0L000S, U1-ch68, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.5
Std Dev 2.652
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch68, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.74
Std Dev 12.61
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch68, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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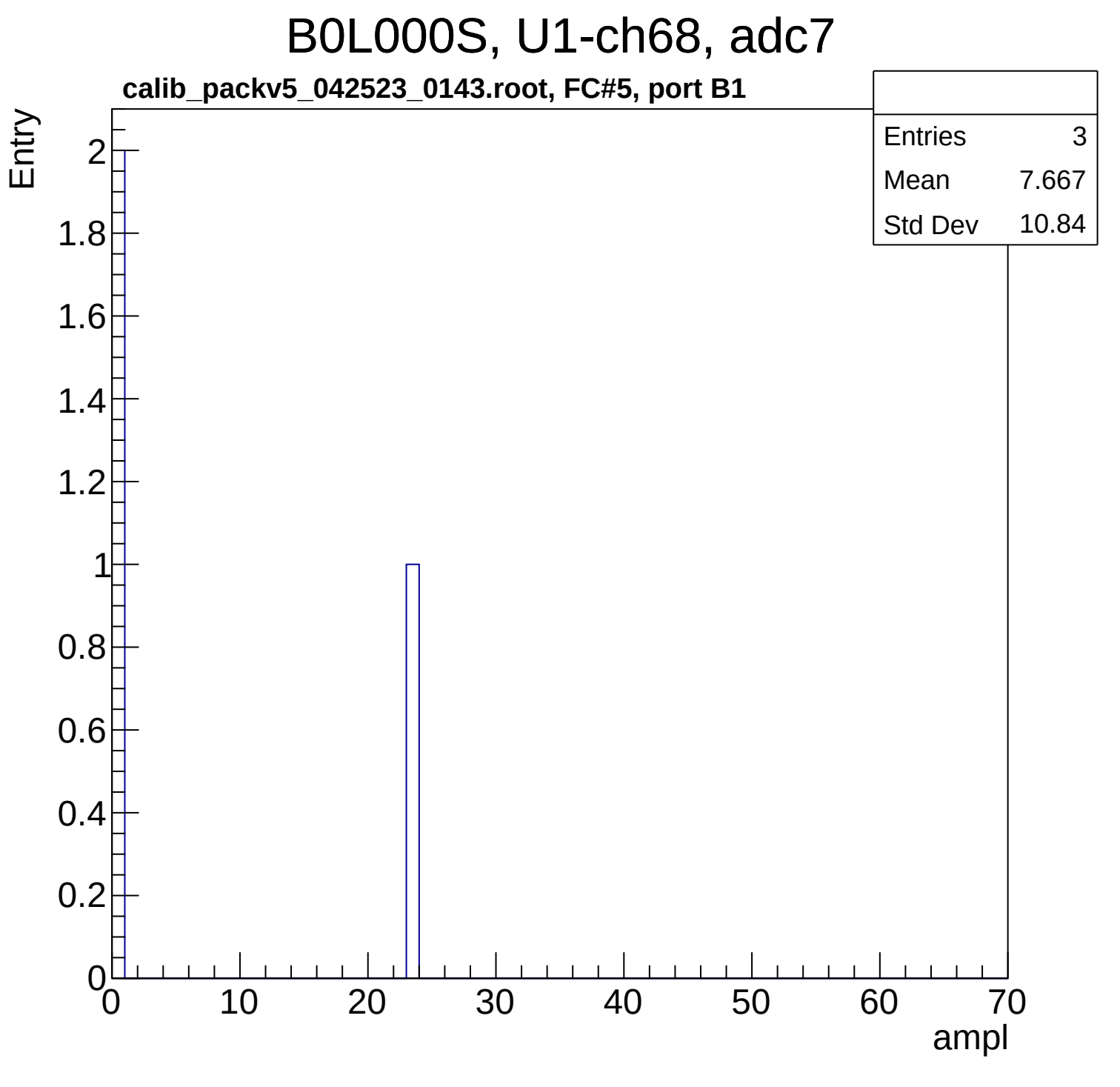
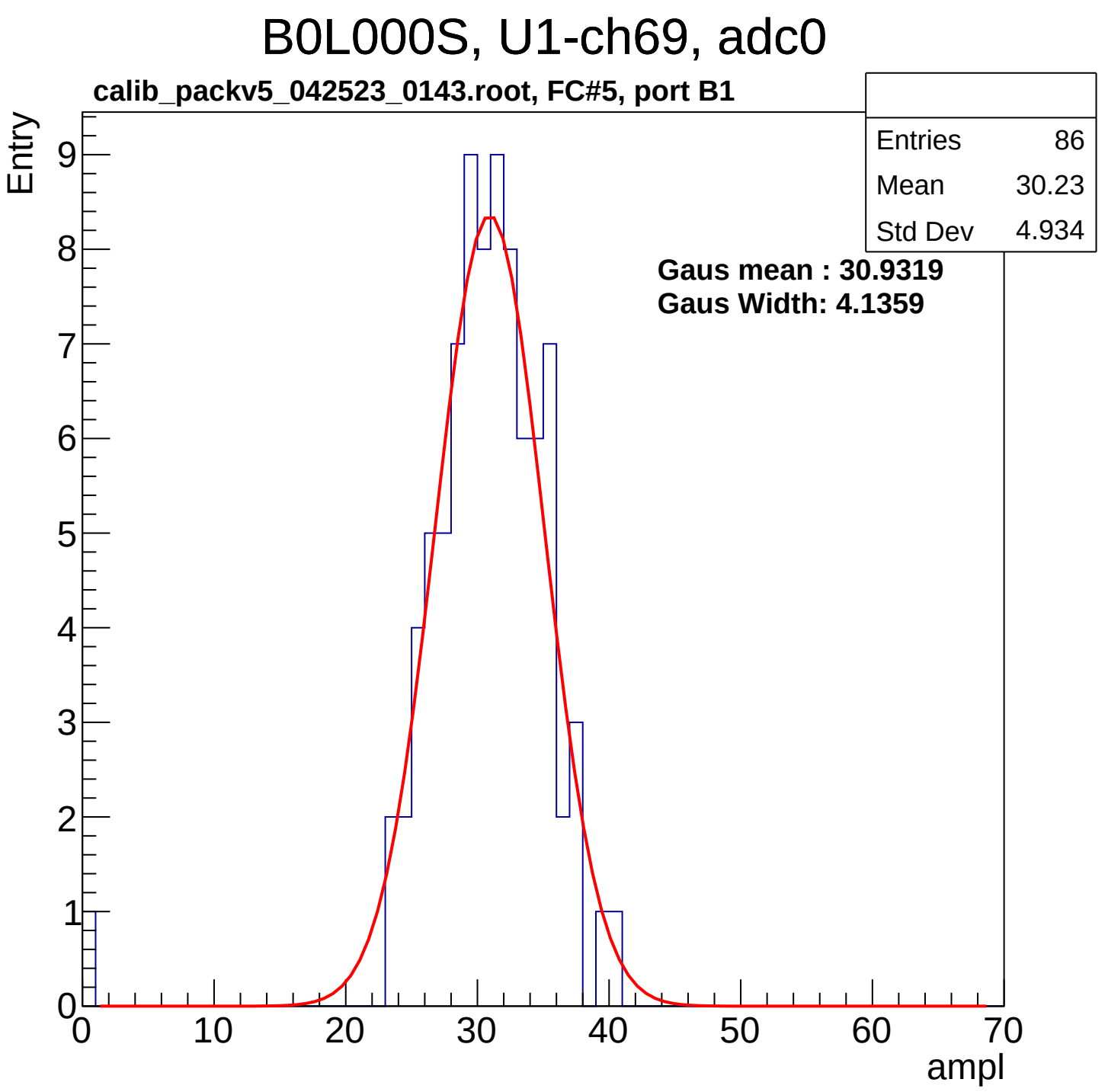
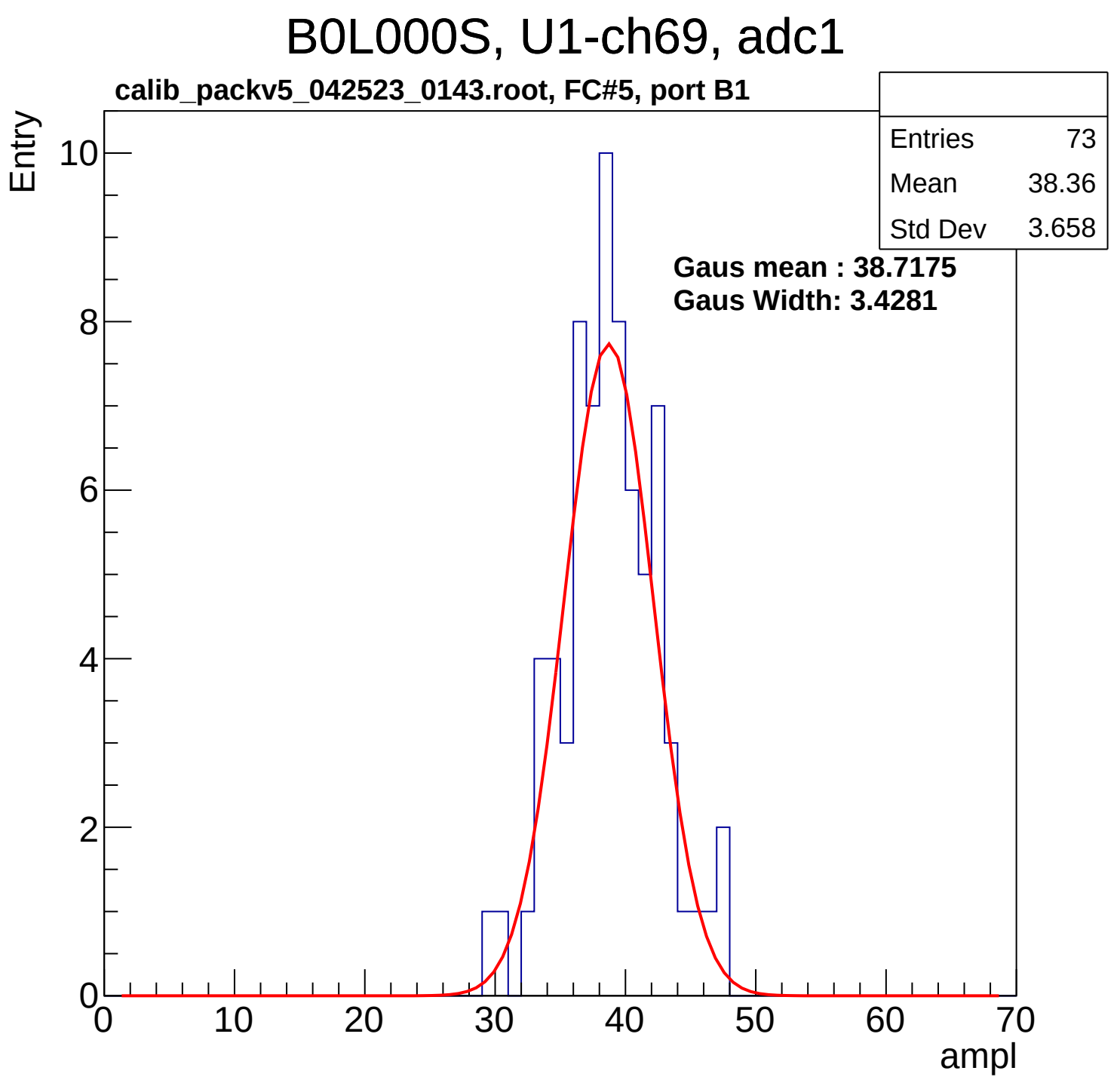
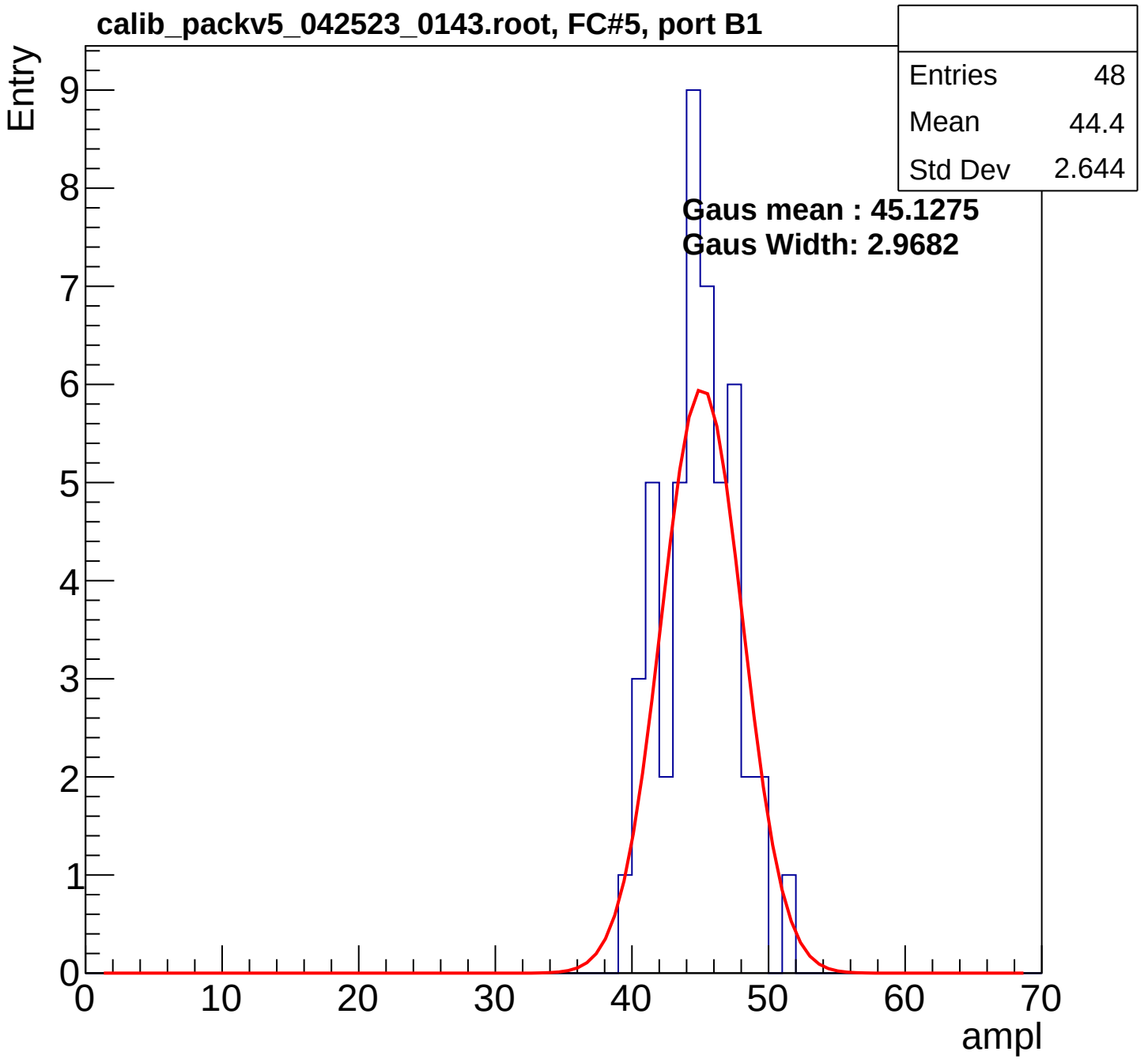
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch68, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch69, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 30.23
Std Dev 4.934
Gaus mean : 30.9319
Gaus Width: 4.1359
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch69, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 38.36
Std Dev 3.658
Gaus mean : 38.7175
Gaus Width: 3.4281
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch69, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 44.4
Std Dev 2.644
Gaus mean : 45.1275
Gaus Width: 2.9682
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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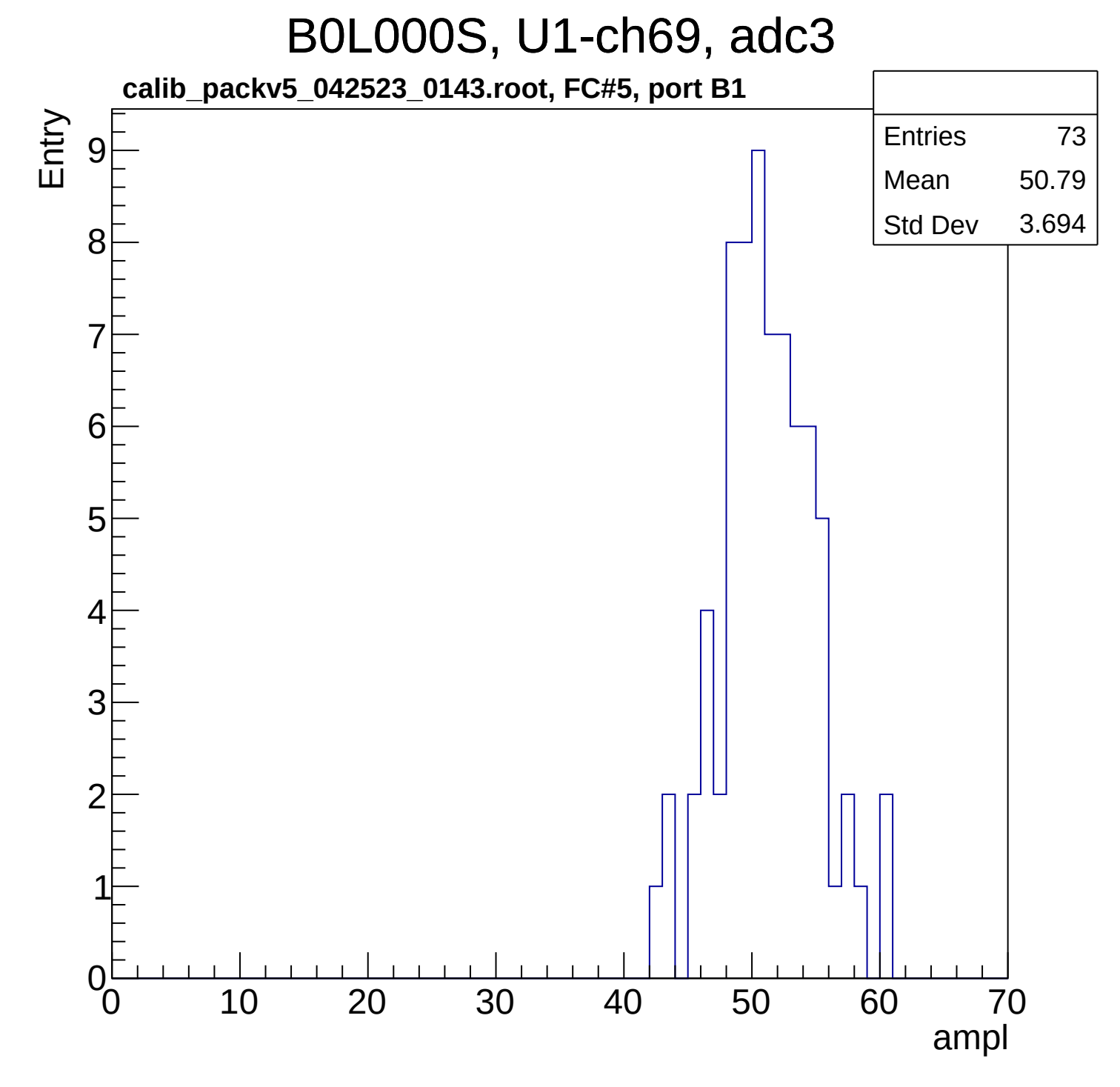
B0L000S, U1-ch69, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 50.79
Std Dev 3.694
Entry
ampl
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3
4
5
6
7
8
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0
10
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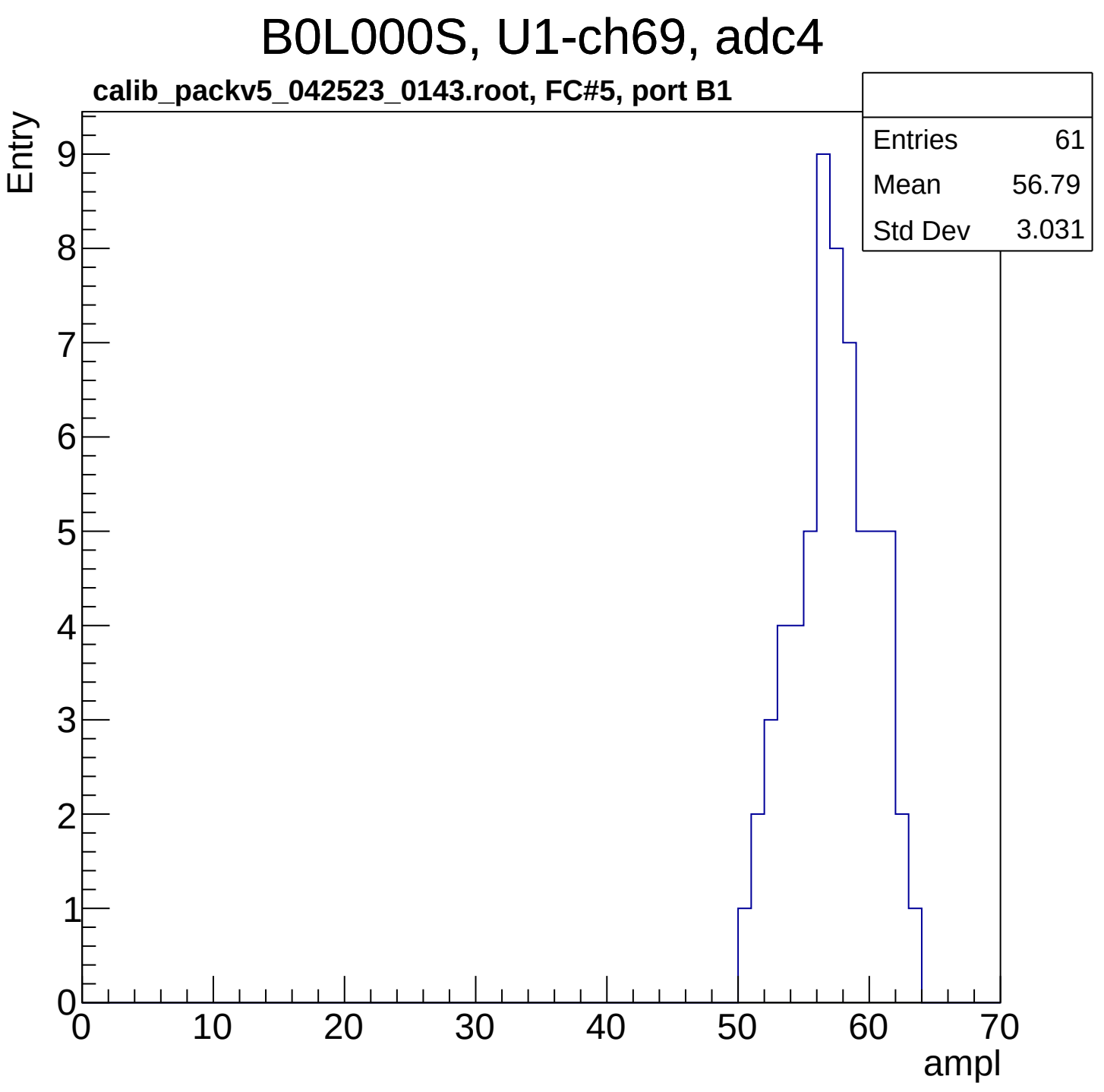
40
50
60
70


B0L000S, U1-ch69, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.79
Std Dev 3.031
Entry
ampl
0
1
2
3
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6
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0
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60
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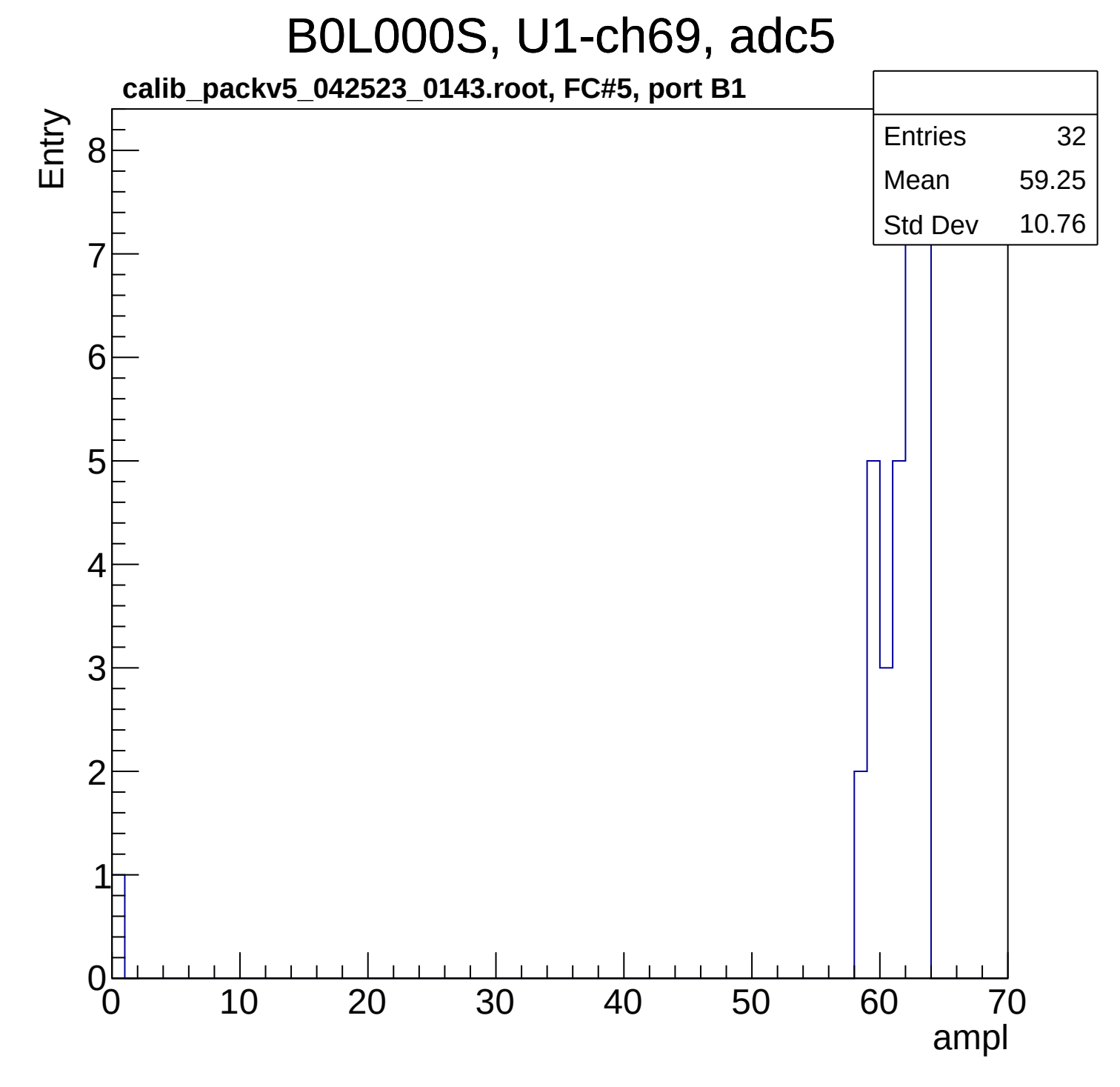
B0L000S, U1-ch69, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 59.25
Std Dev 10.76
Entry
ampl
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1
2
3
4
5
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7
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0
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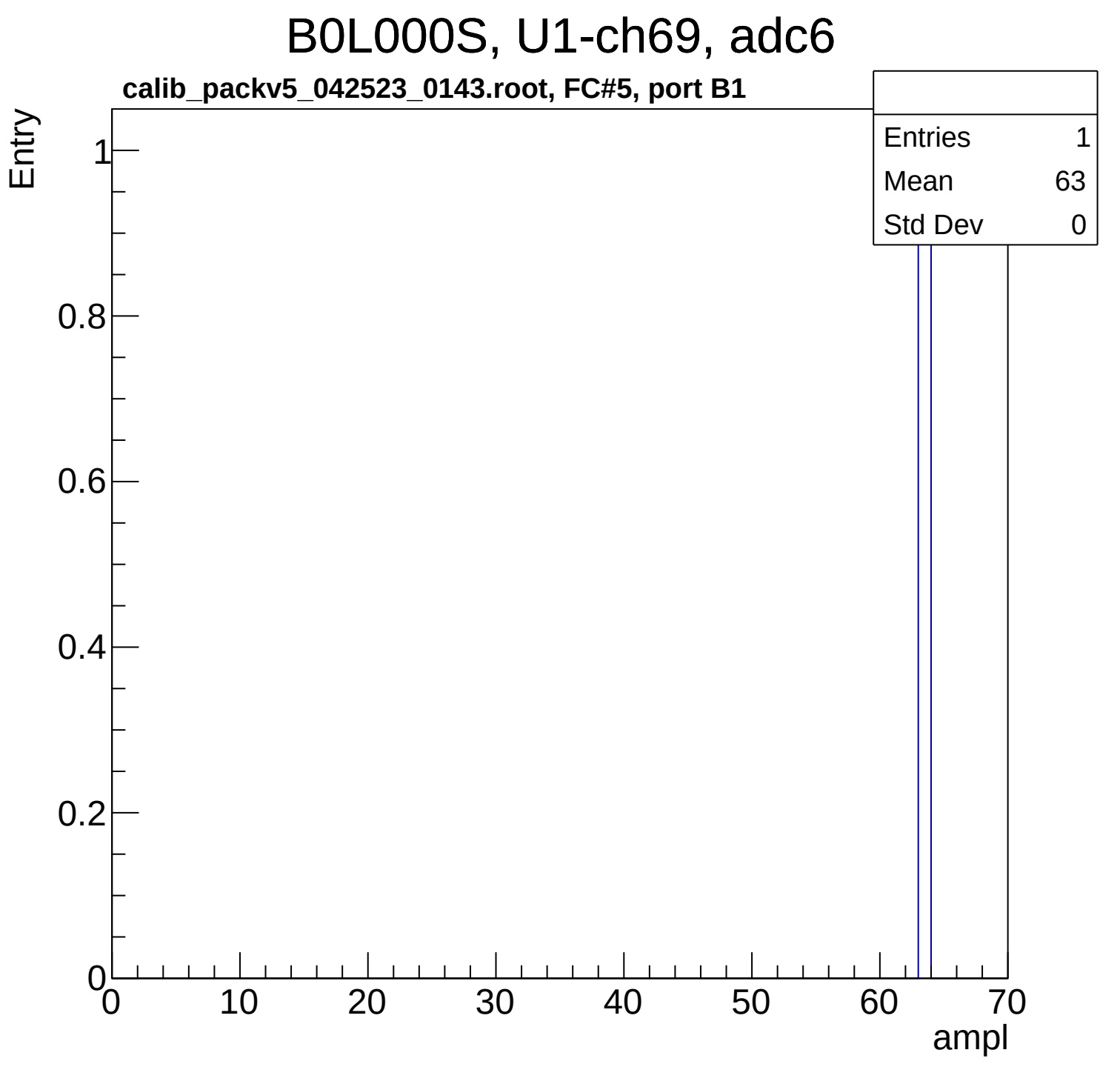
B0L000S, U1-ch69, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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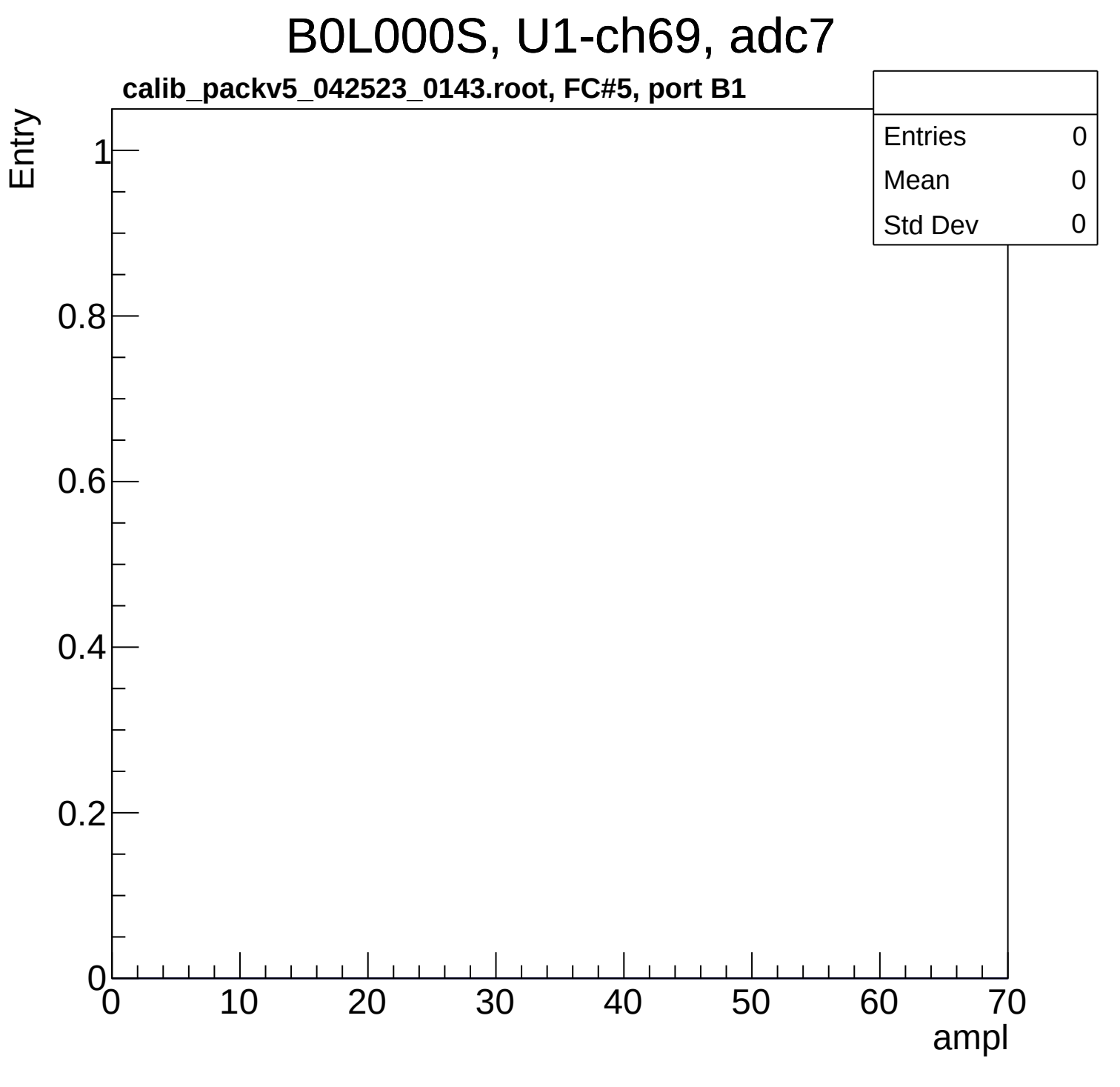
50
60
70
ampl


B0L000S, U1-ch69, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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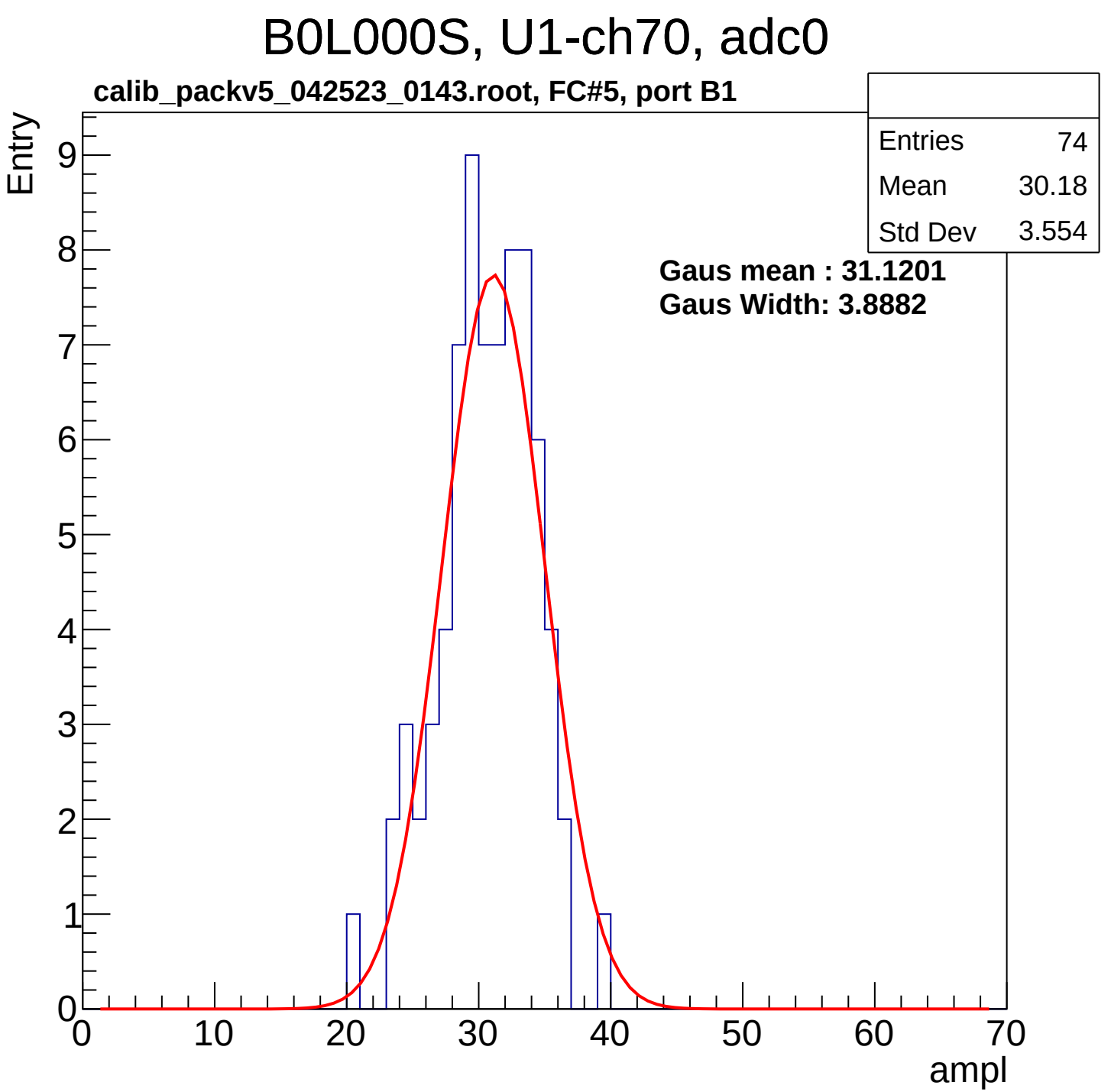
50
60
70
ampl


B0L000S, U1-ch70, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 30.18
Std Dev 3.554
Gaus mean : 31.1201
Gaus Width: 3.8882
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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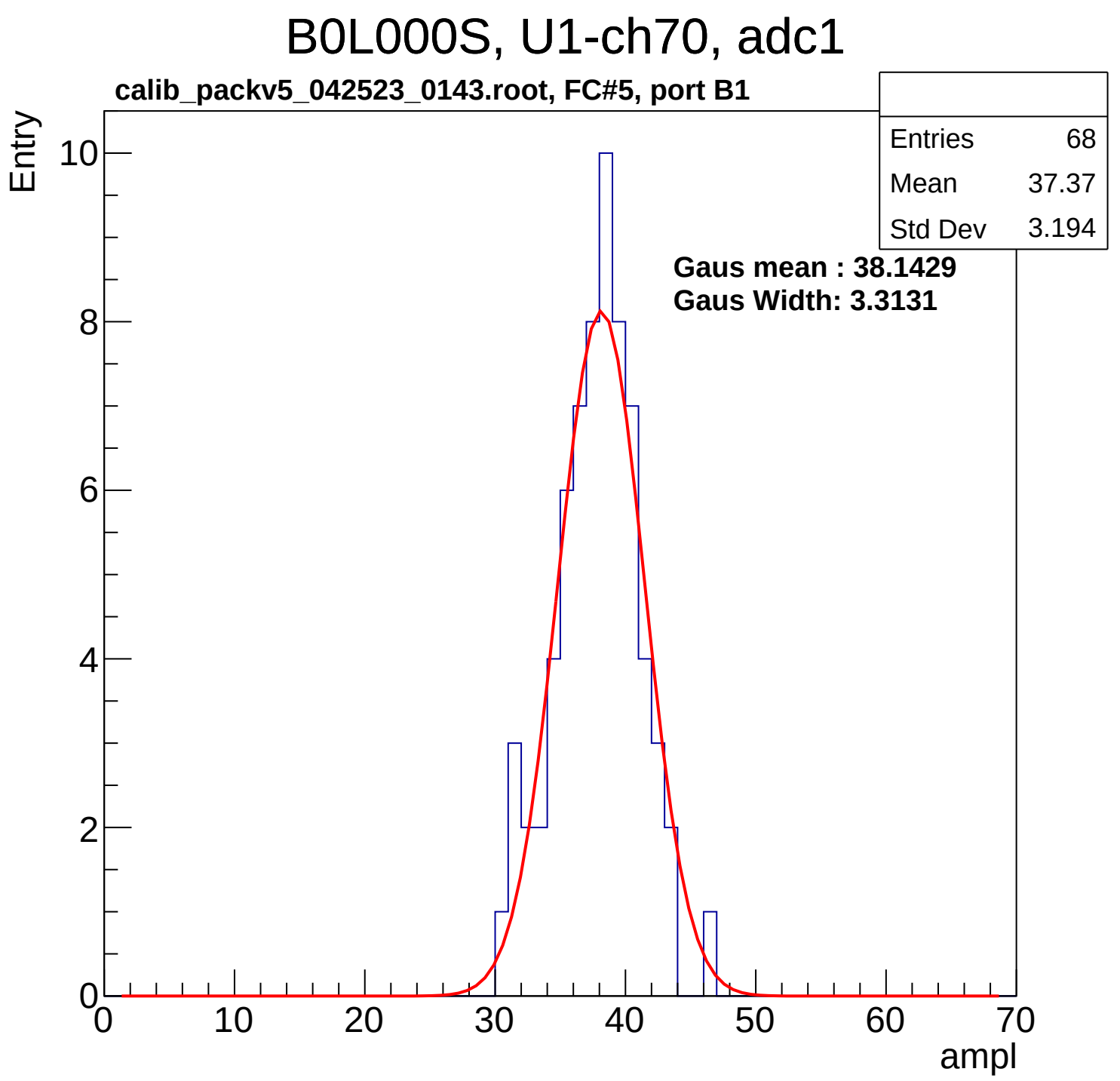
B0L000S, U1-ch70, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 37.37
Std Dev 3.194
Gaus mean : 38.1429
Gaus Width: 3.3131
Entry
ampl
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2
4
6
8
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0
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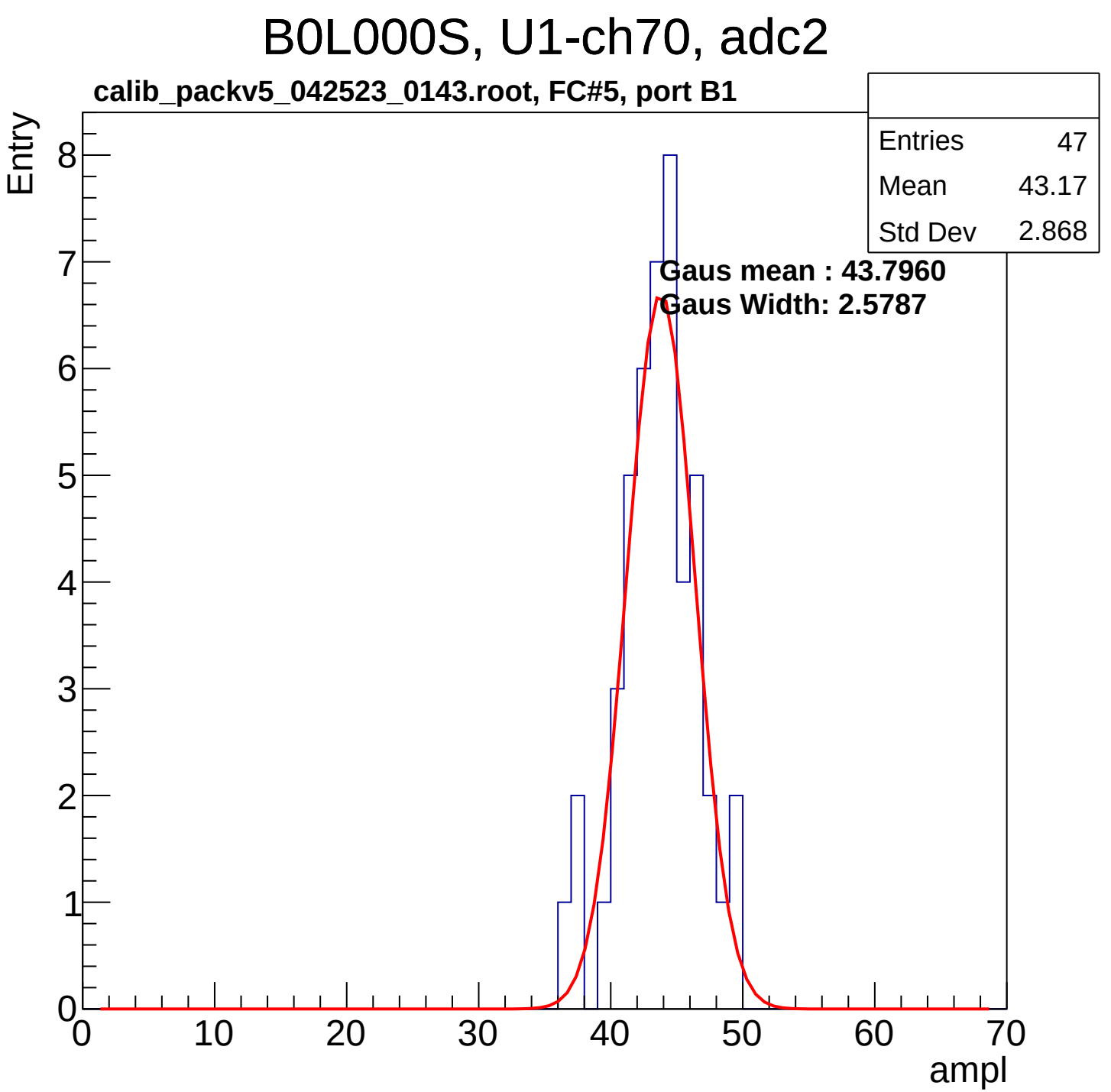
B0L000S, U1-ch70, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 43.17
Std Dev 2.868
Gaus mean : 43.7960
Gaus Width: 2.5787
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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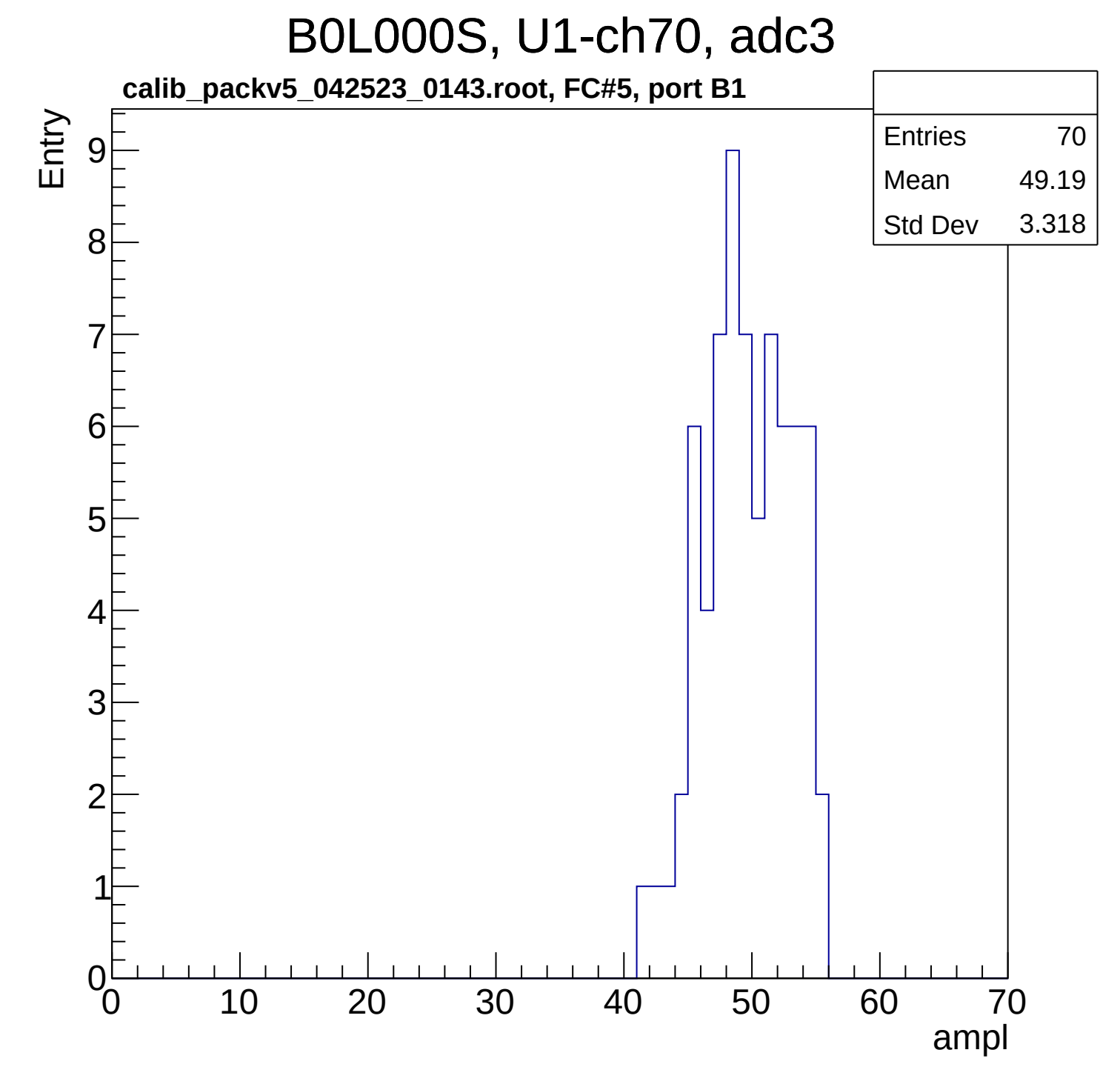
B0L000S, U1-ch70, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.19
Std Dev 3.318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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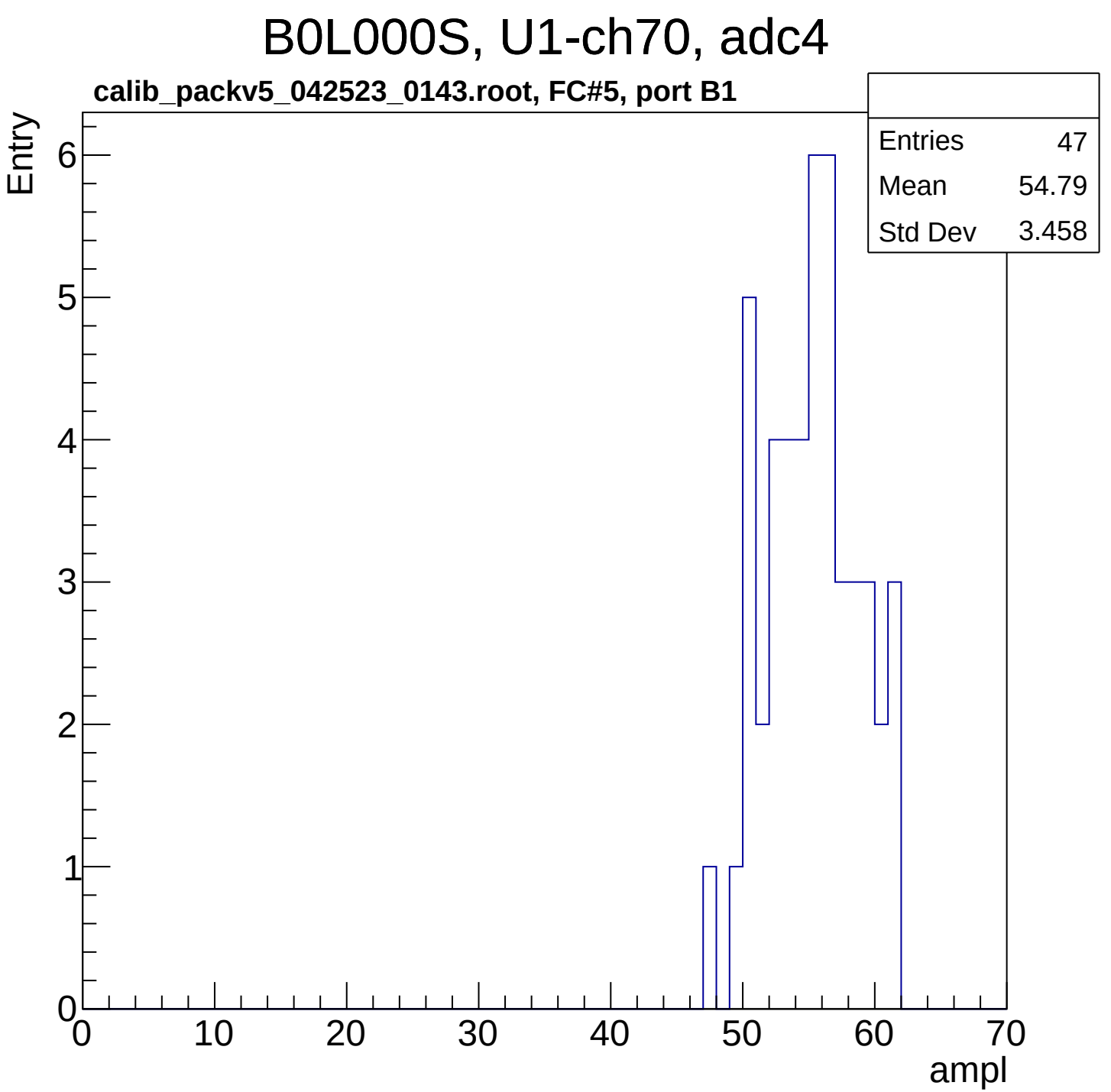
B0L000S, U1-ch70, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 54.79
Std Dev 3.458
Entry
ampl
0
1
2
3
4
5
6
0
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60
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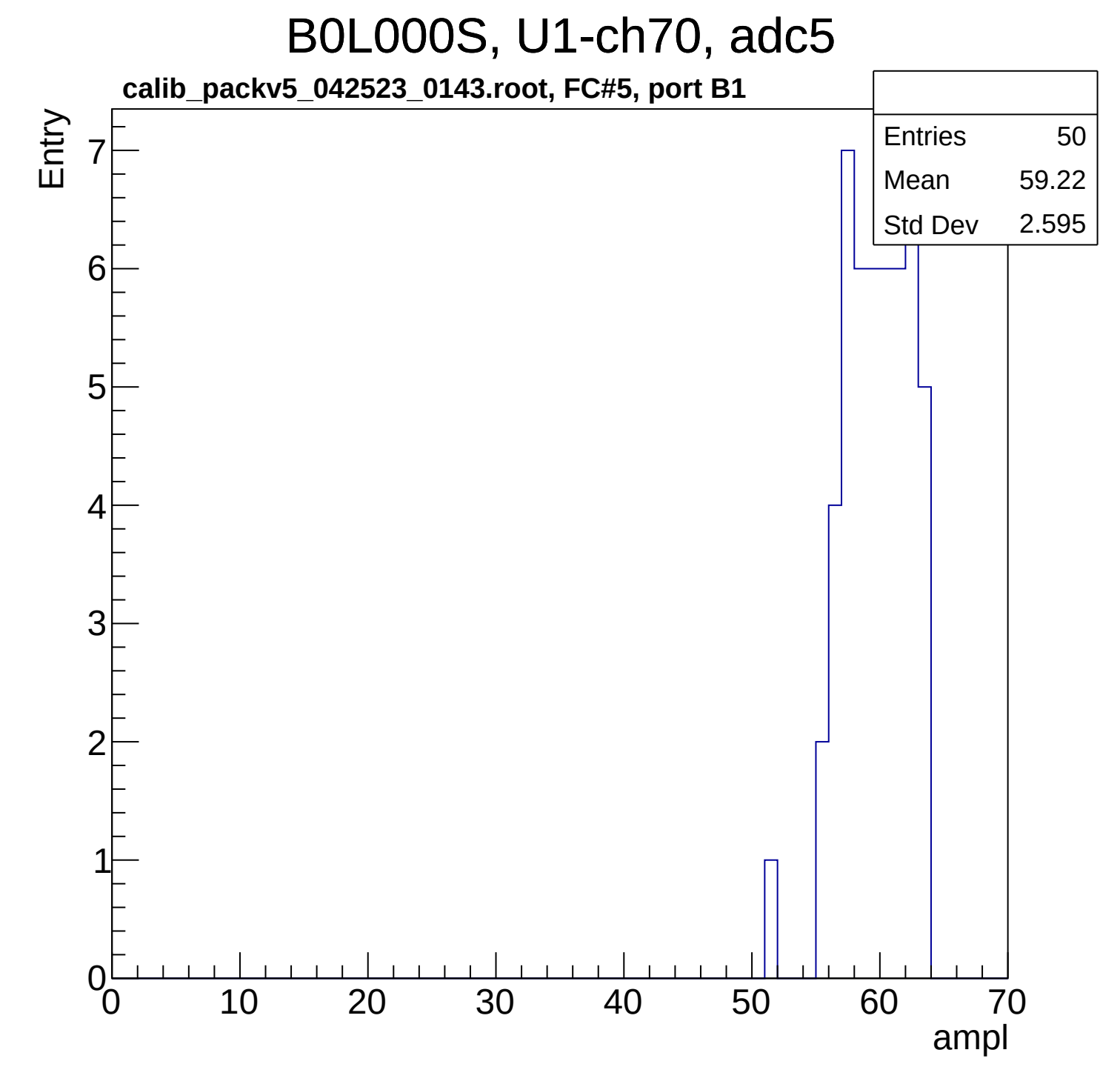
B0L000S, U1-ch70, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.22
Std Dev 2.595
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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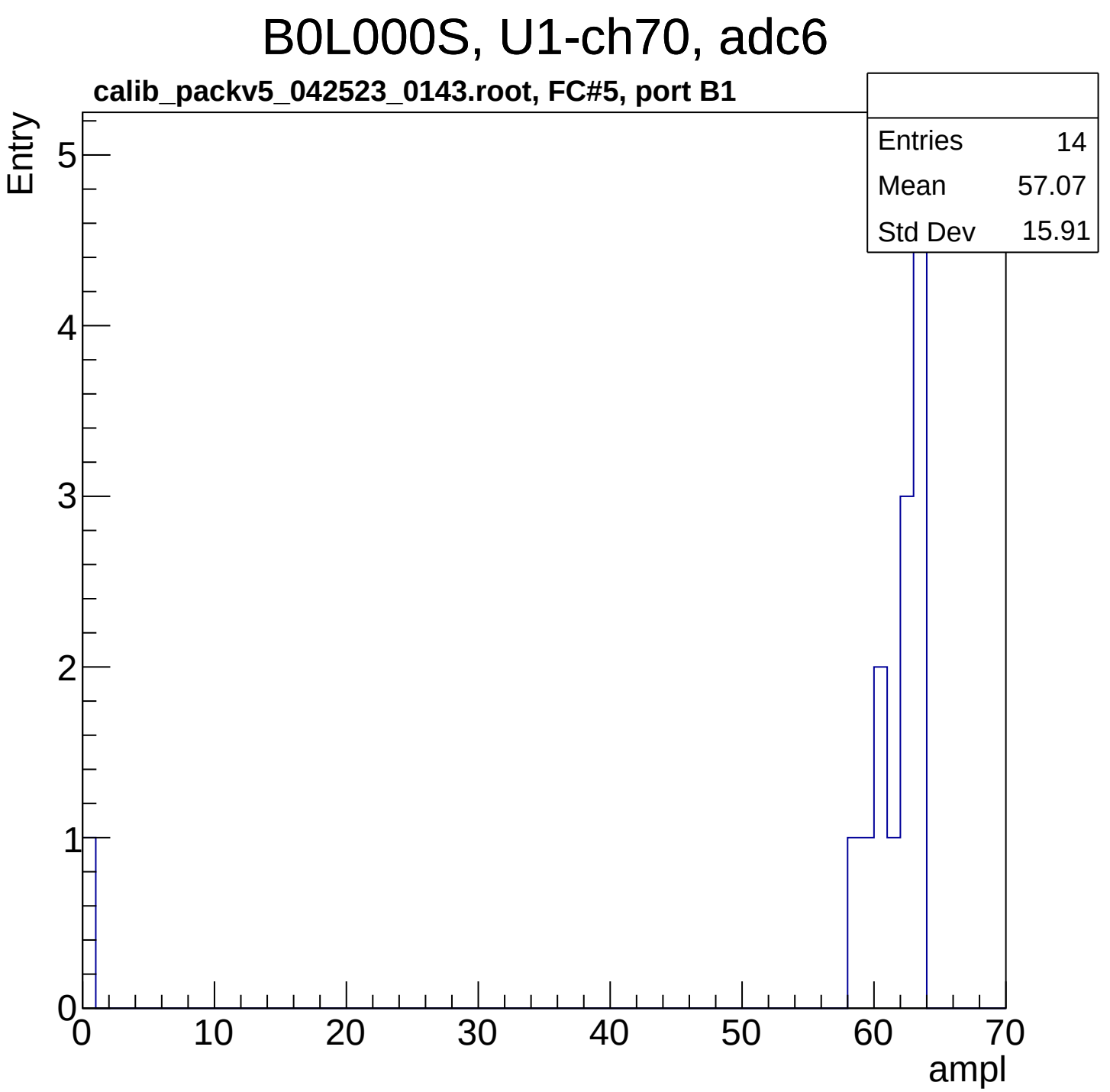
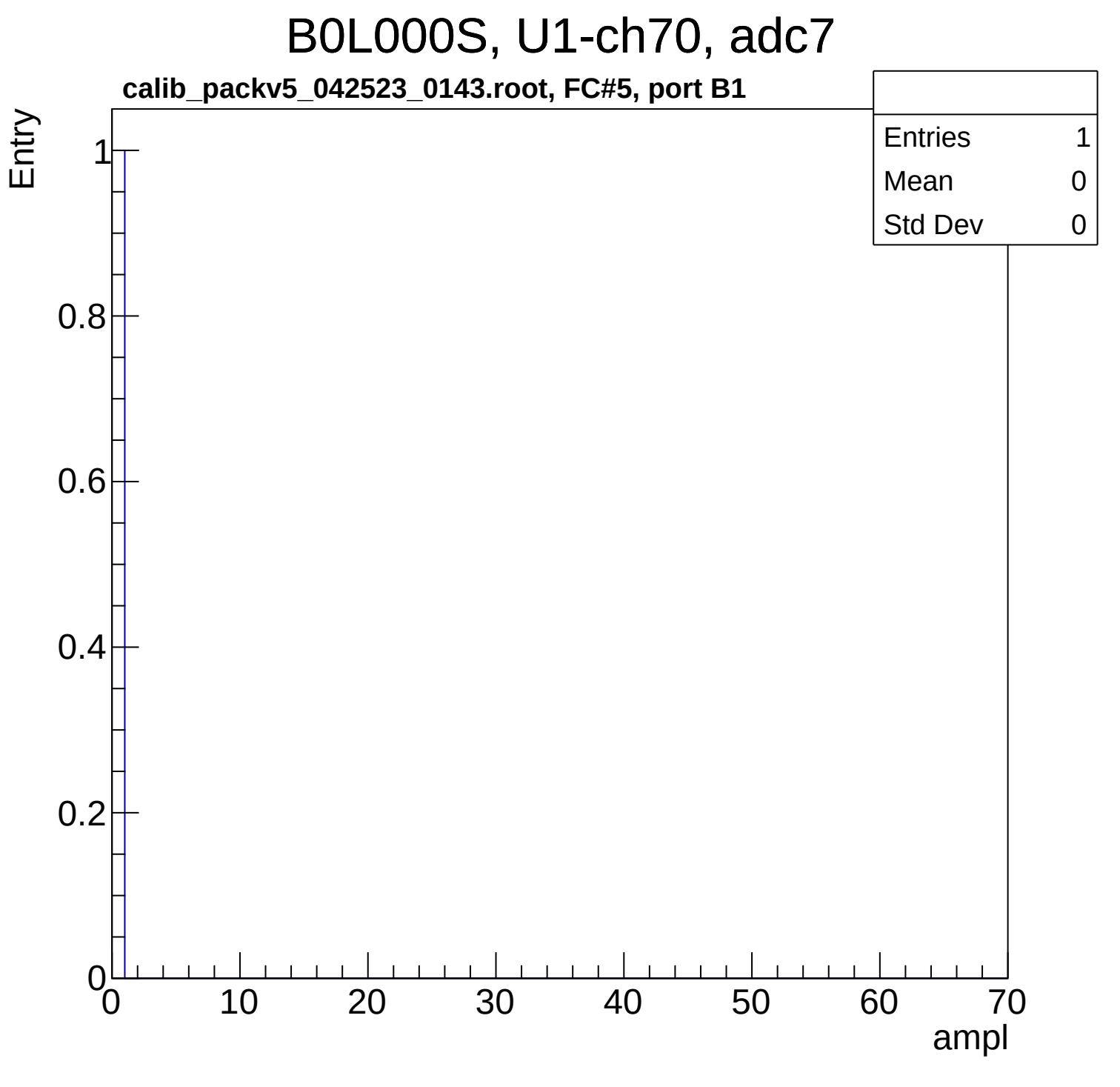

B0L000S, U1-ch70, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.07
Std Dev 15.91
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch70, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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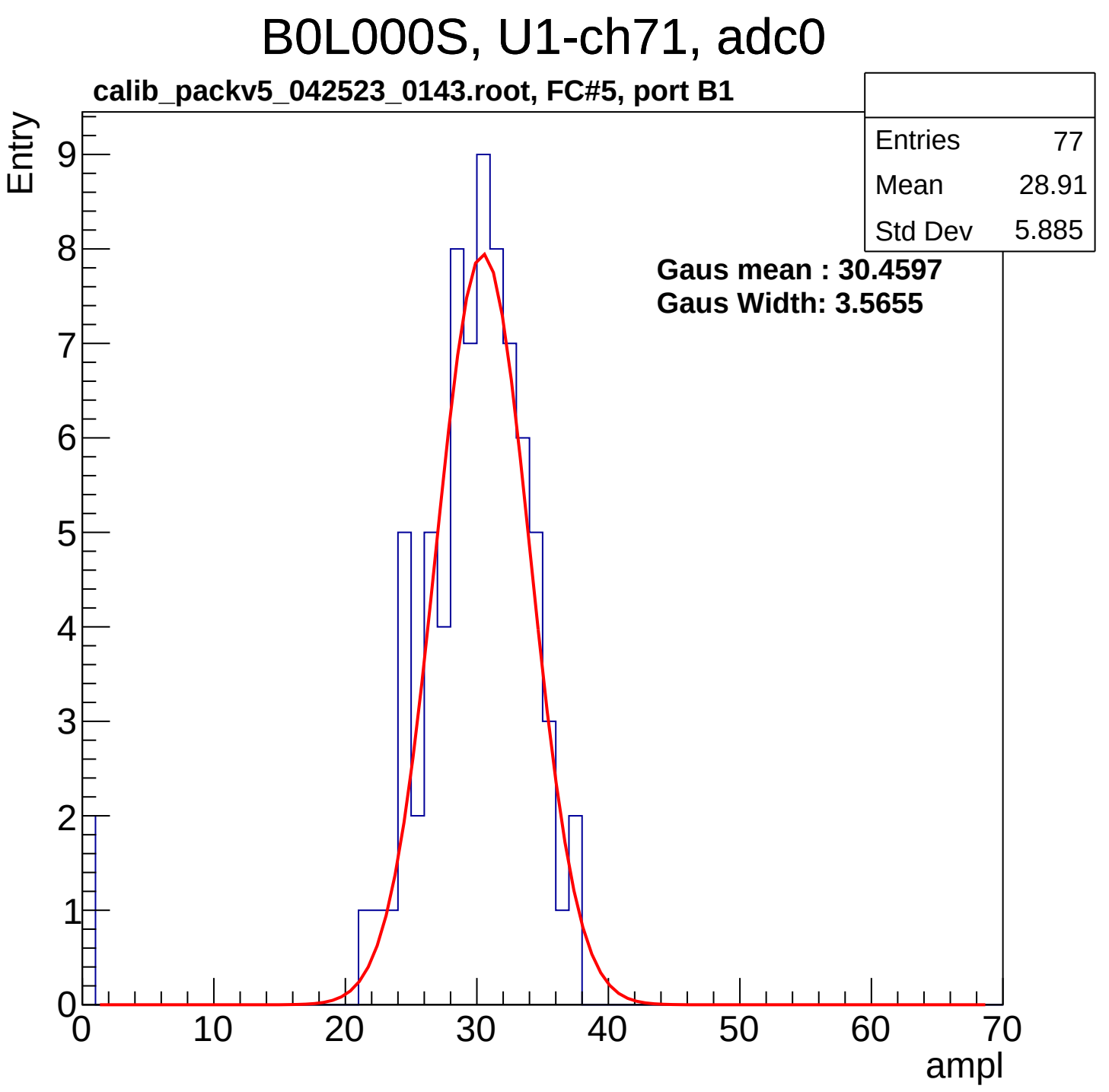
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch71, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.91
Std Dev 5.885
Gaus mean : 30.4597
Gaus Width: 3.5655
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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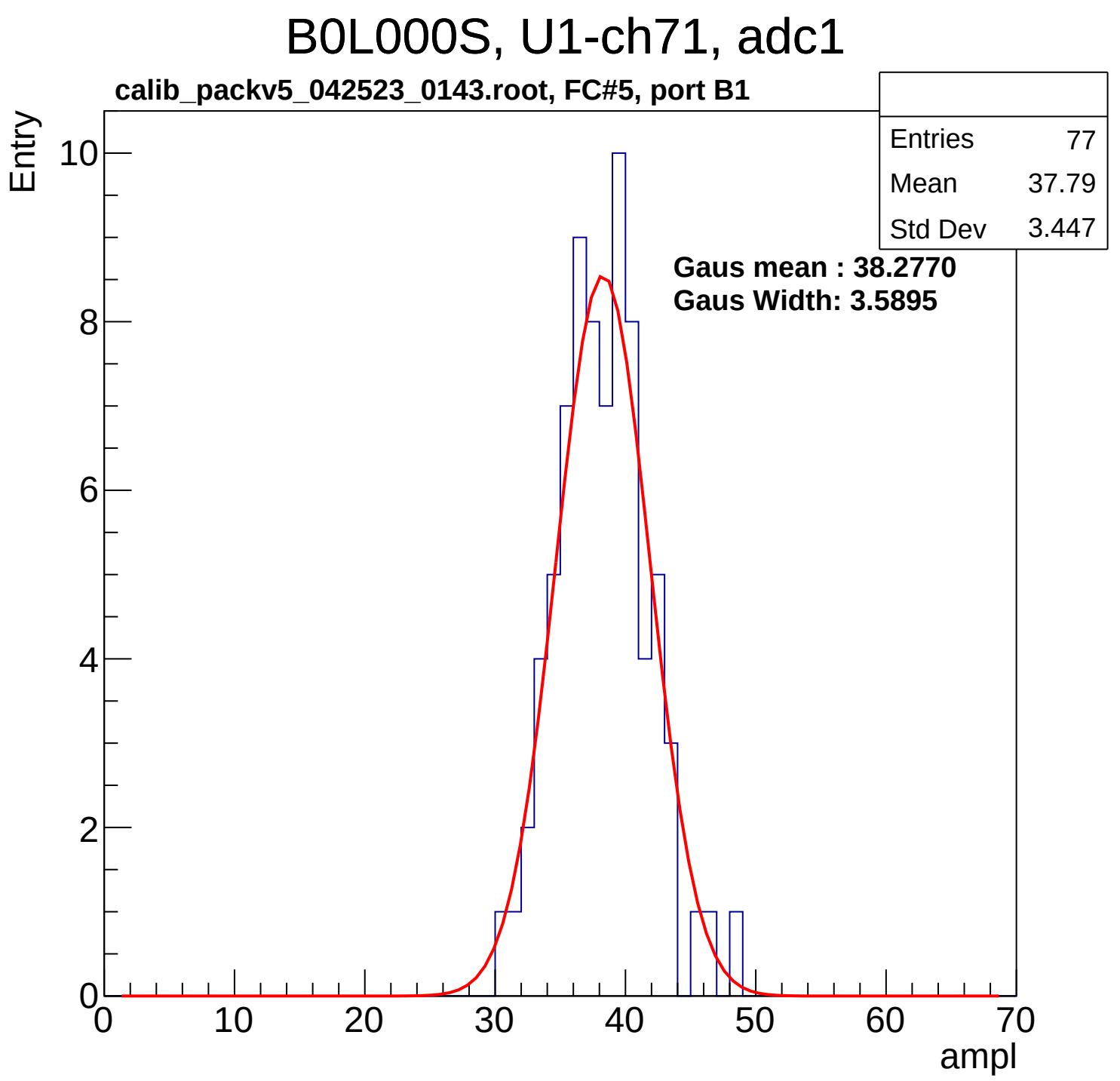
B0L000S, U1-ch71, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 37.79
Std Dev 3.447
Gaus mean : 38.2770
Gaus Width: 3.5895
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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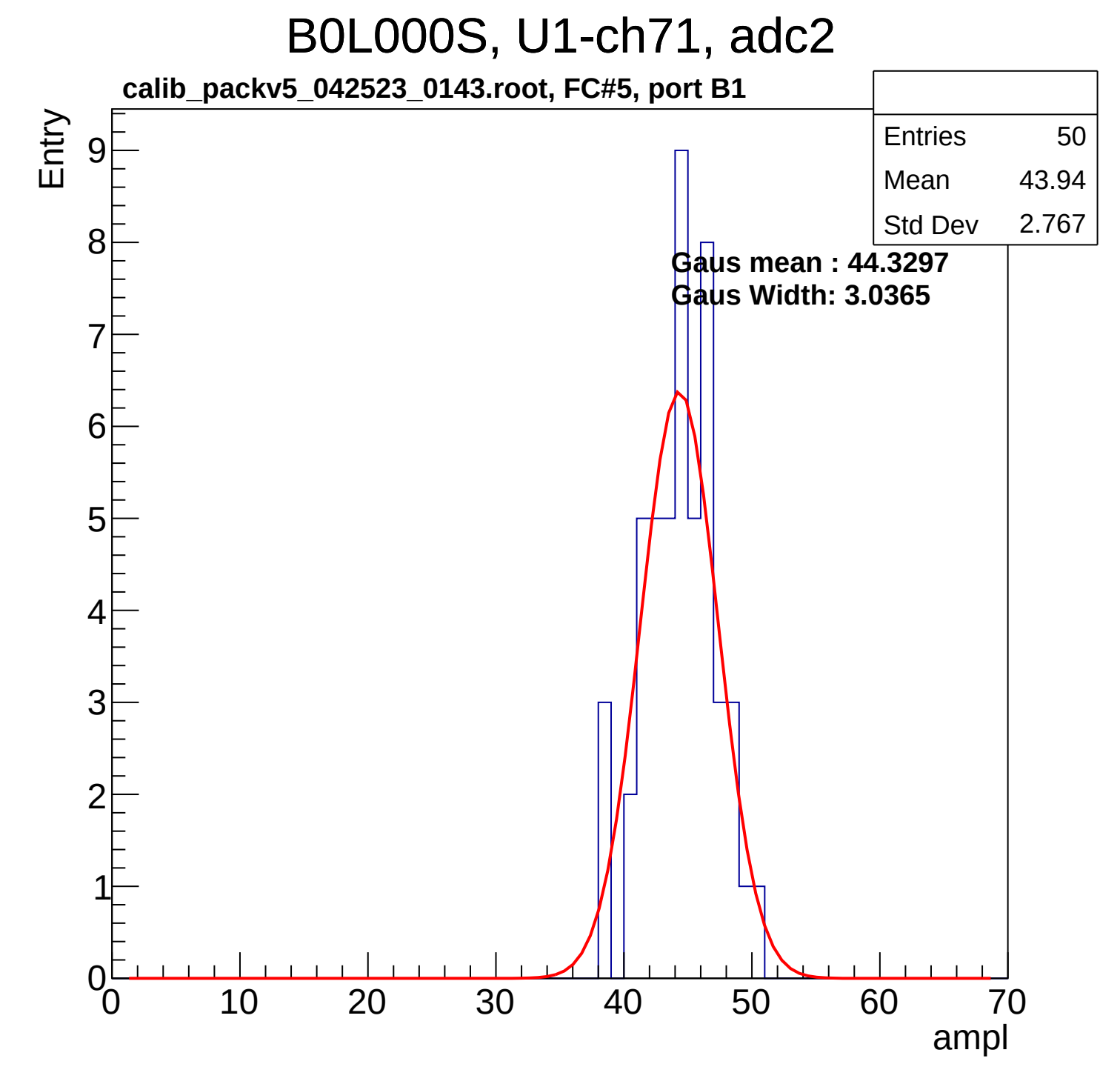
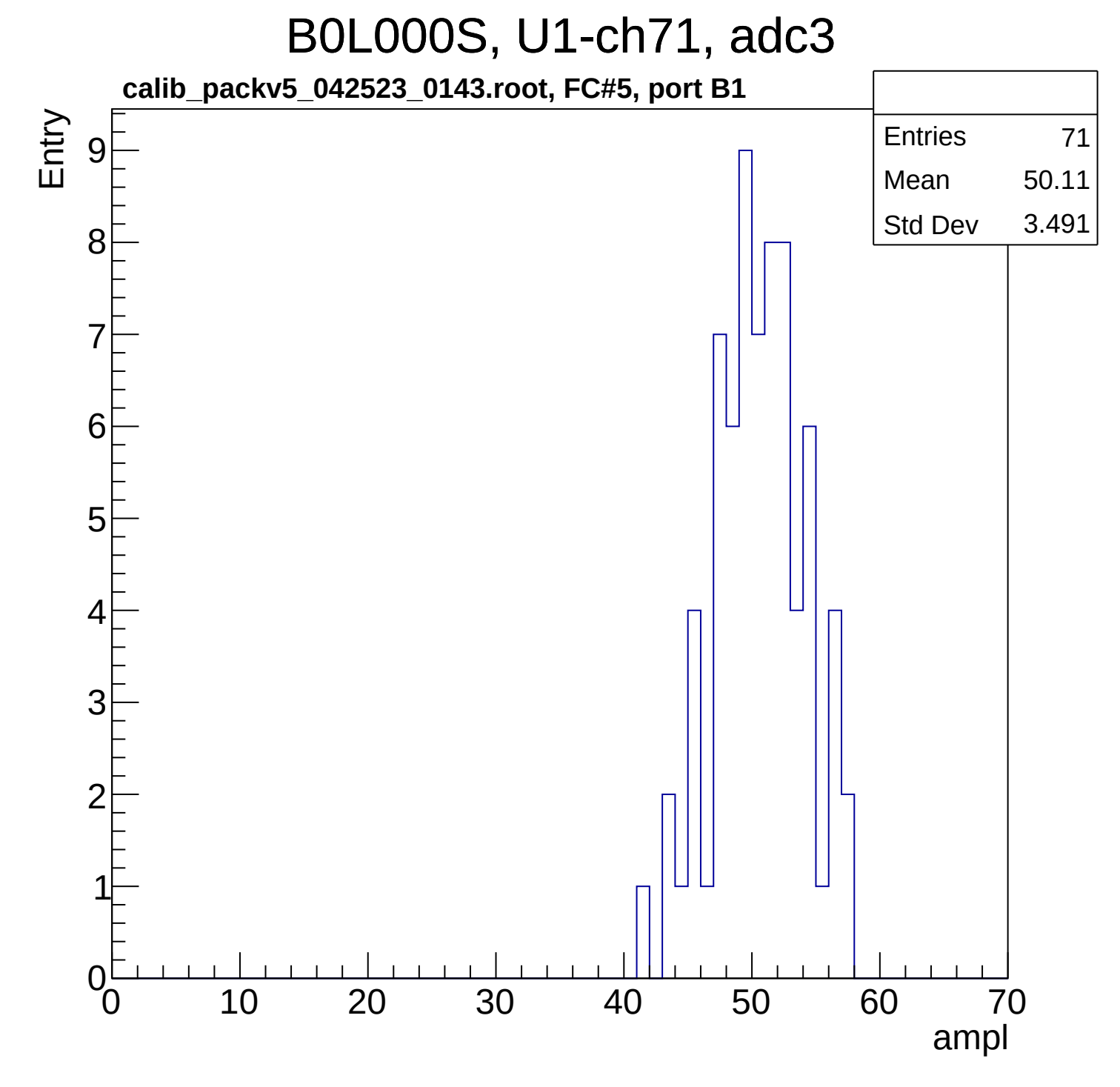

B0L000S, U1-ch71, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 43.94
Std Dev 2.767
Gaus mean : 44.3297
Gaus Width: 3.0365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch71, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.11
Std Dev 3.491
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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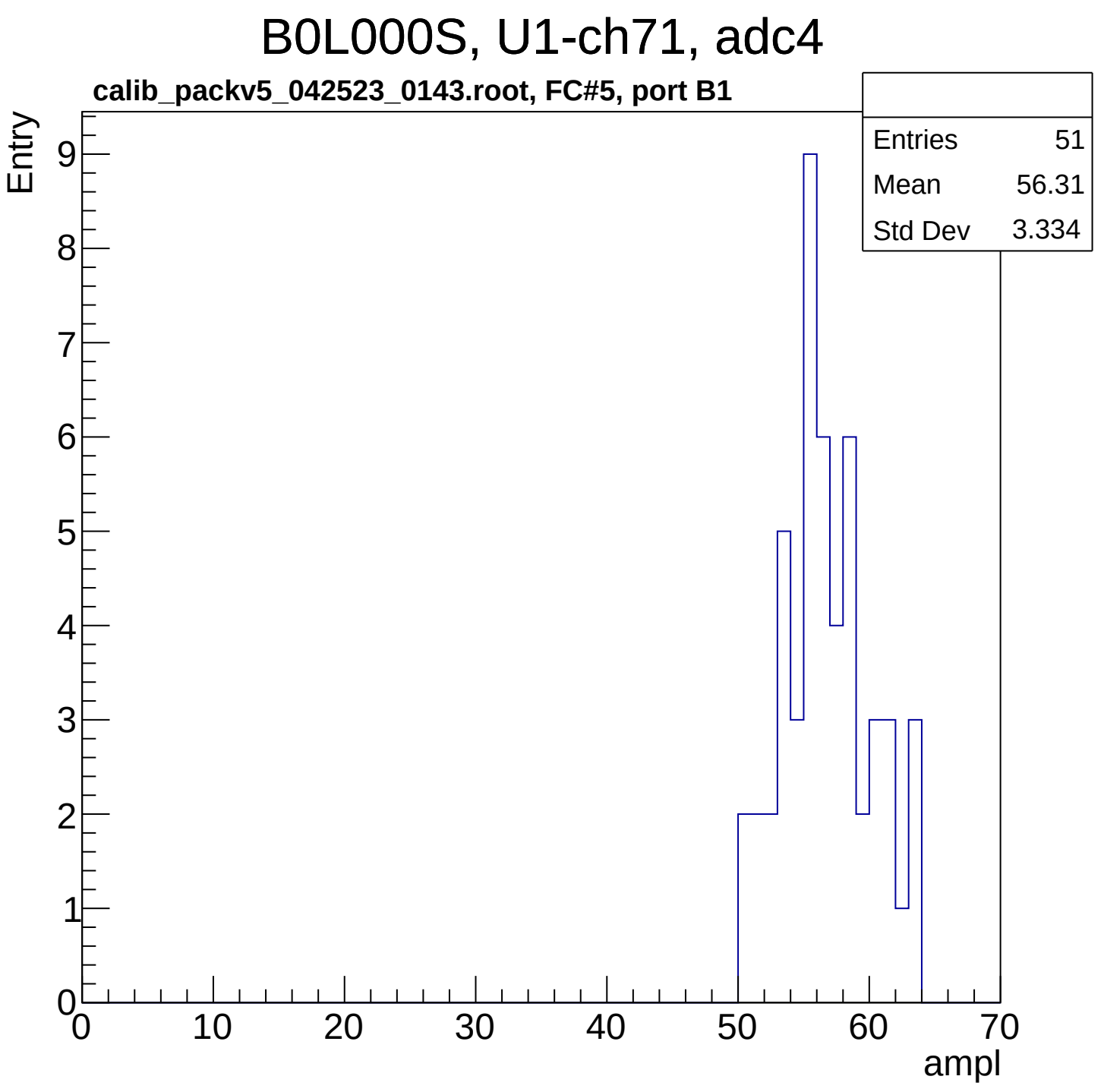
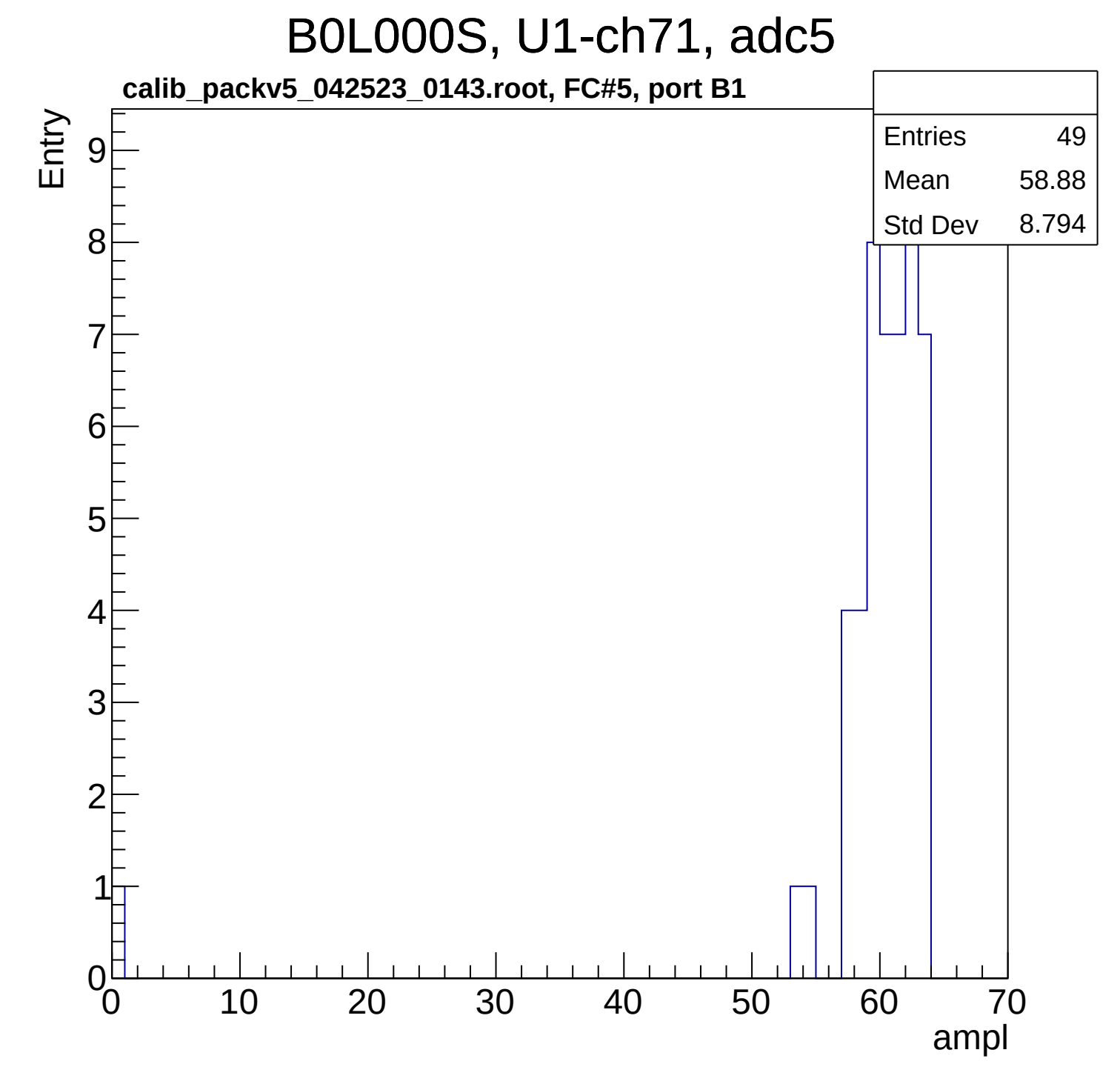

B0L000S, U1-ch71, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.31
Std Dev 3.334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch71, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.88
Std Dev 8.794
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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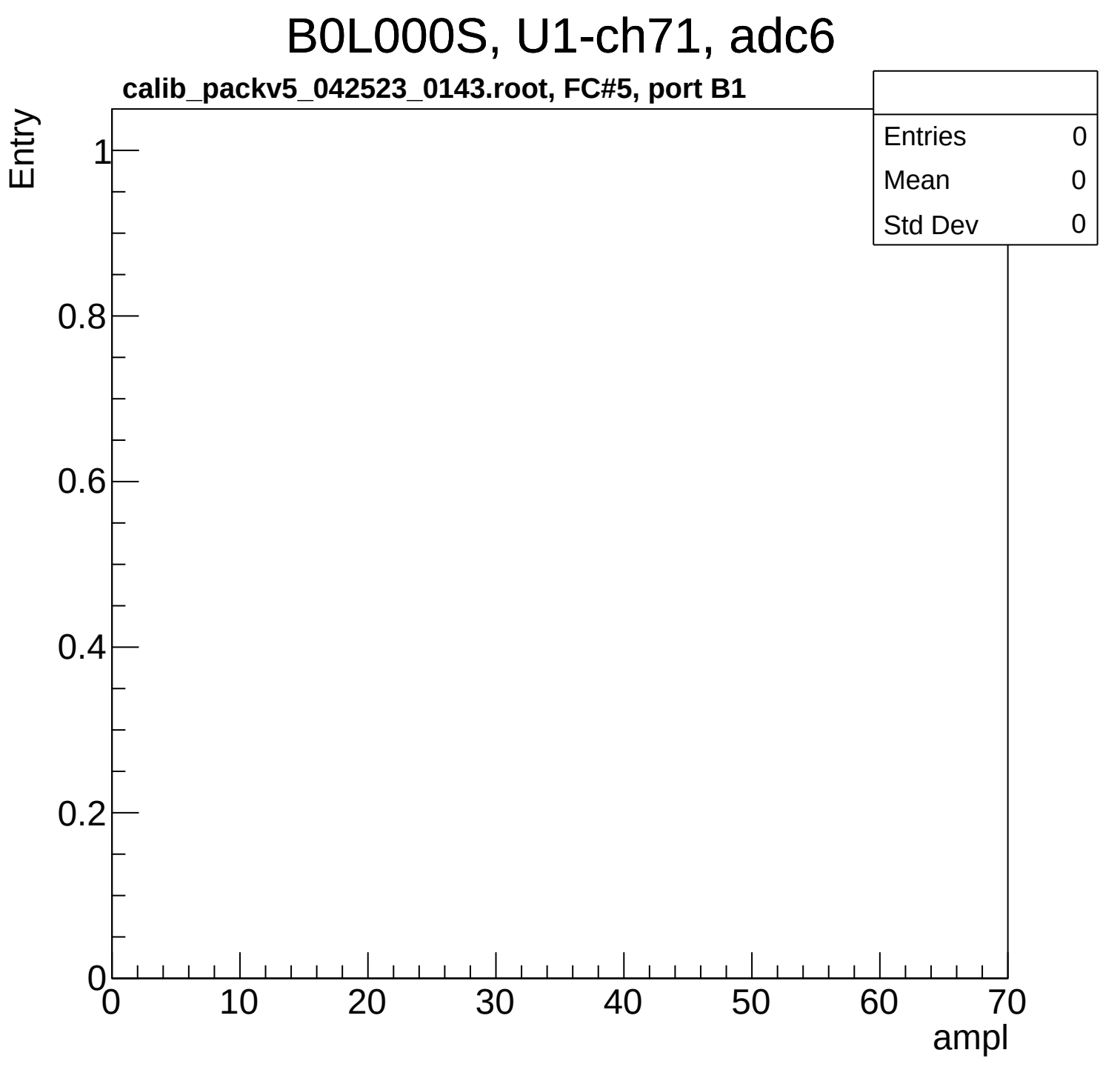
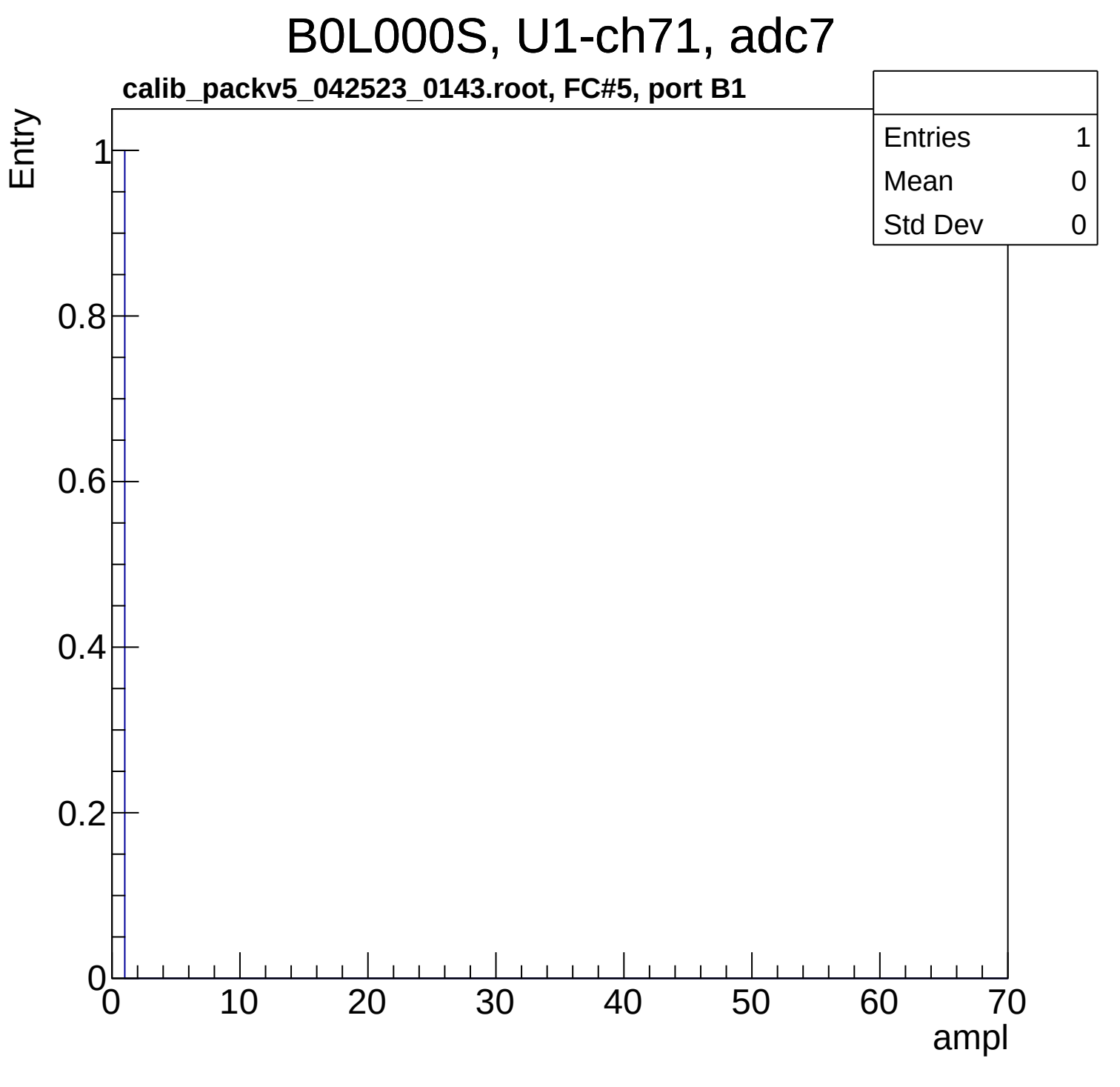

B0L000S, U1-ch71, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch71, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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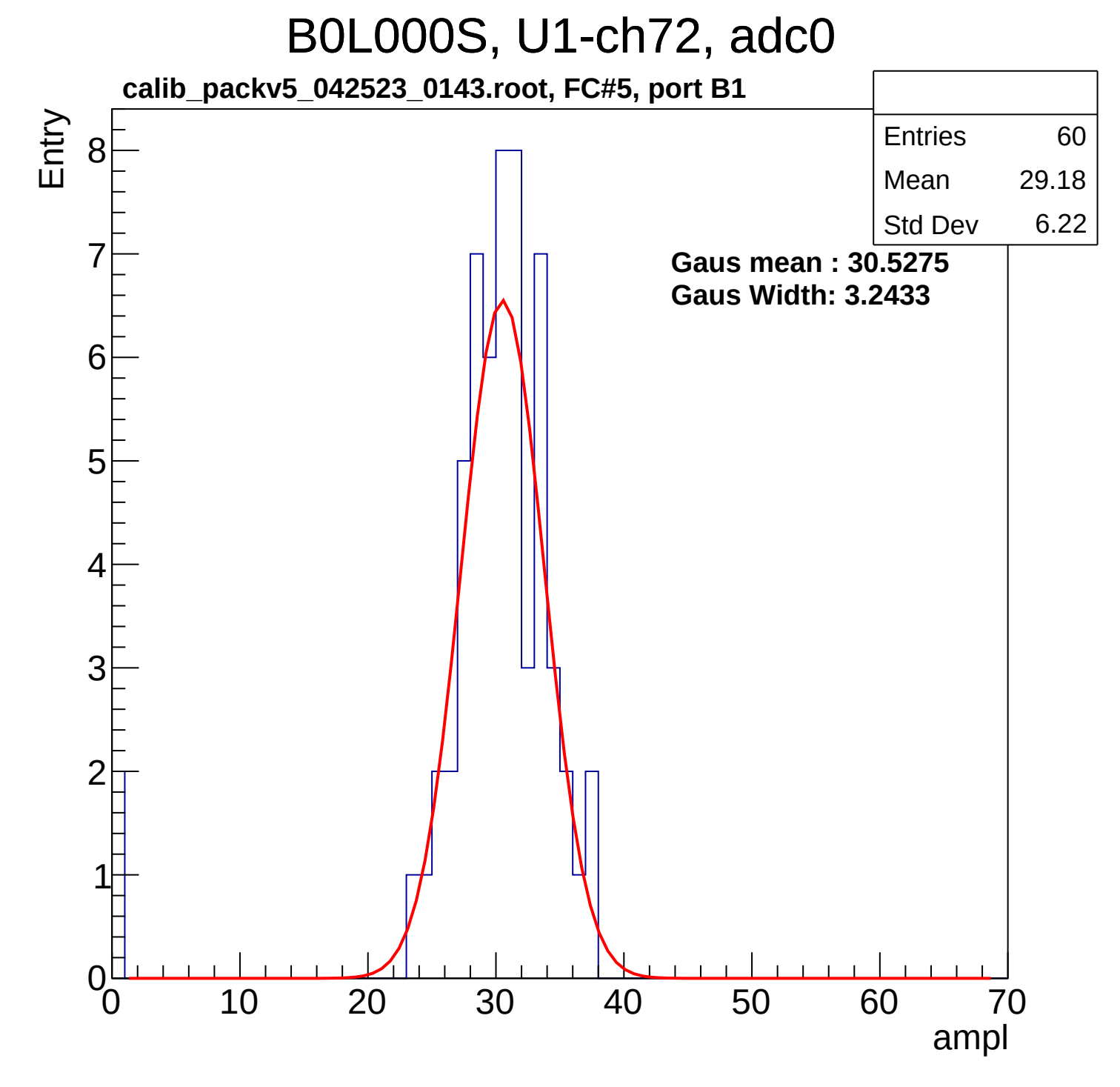
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch72, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.18
Std Dev 6.22
Gaus mean : 30.5275
Gaus Width: 3.2433
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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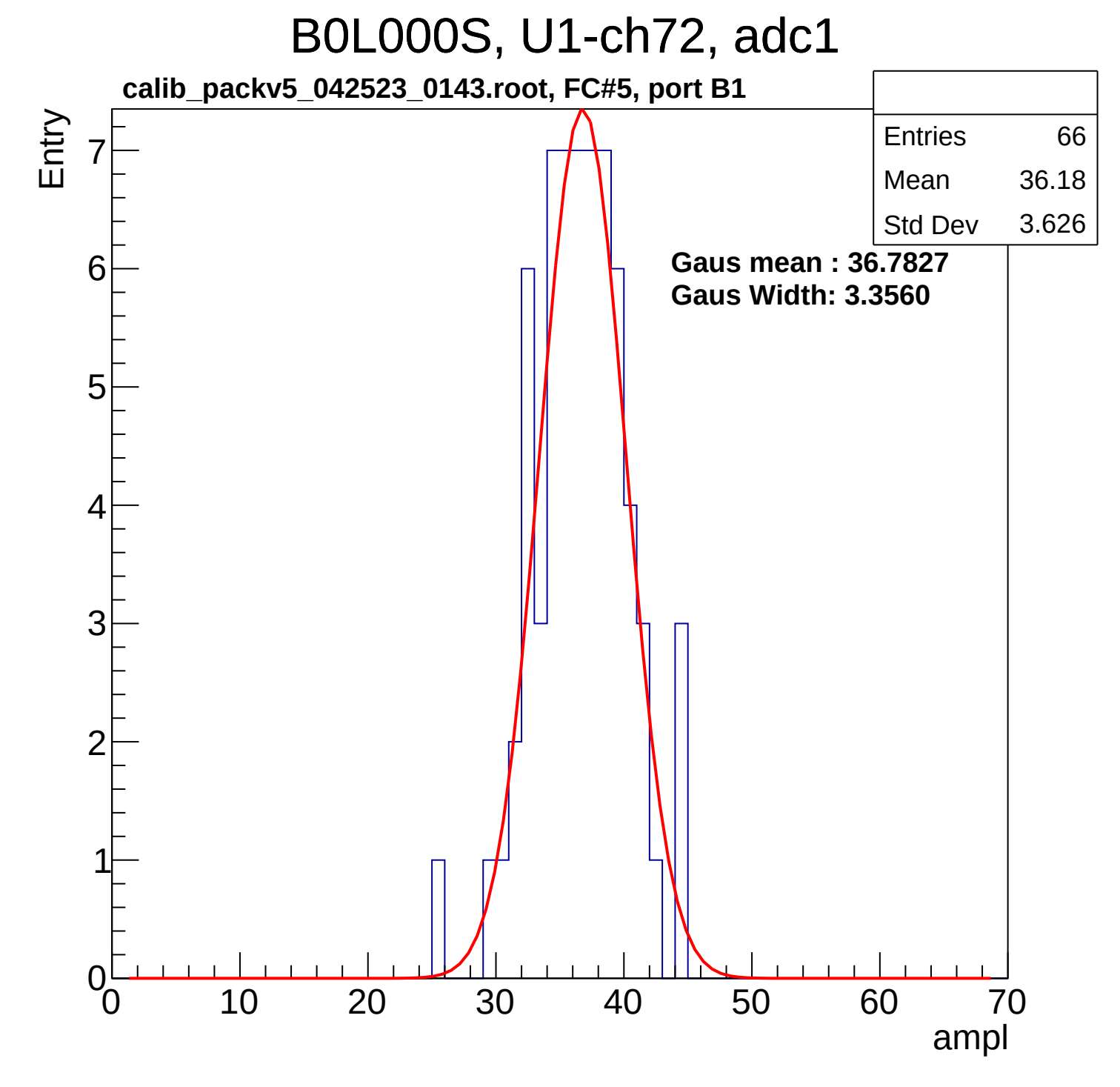
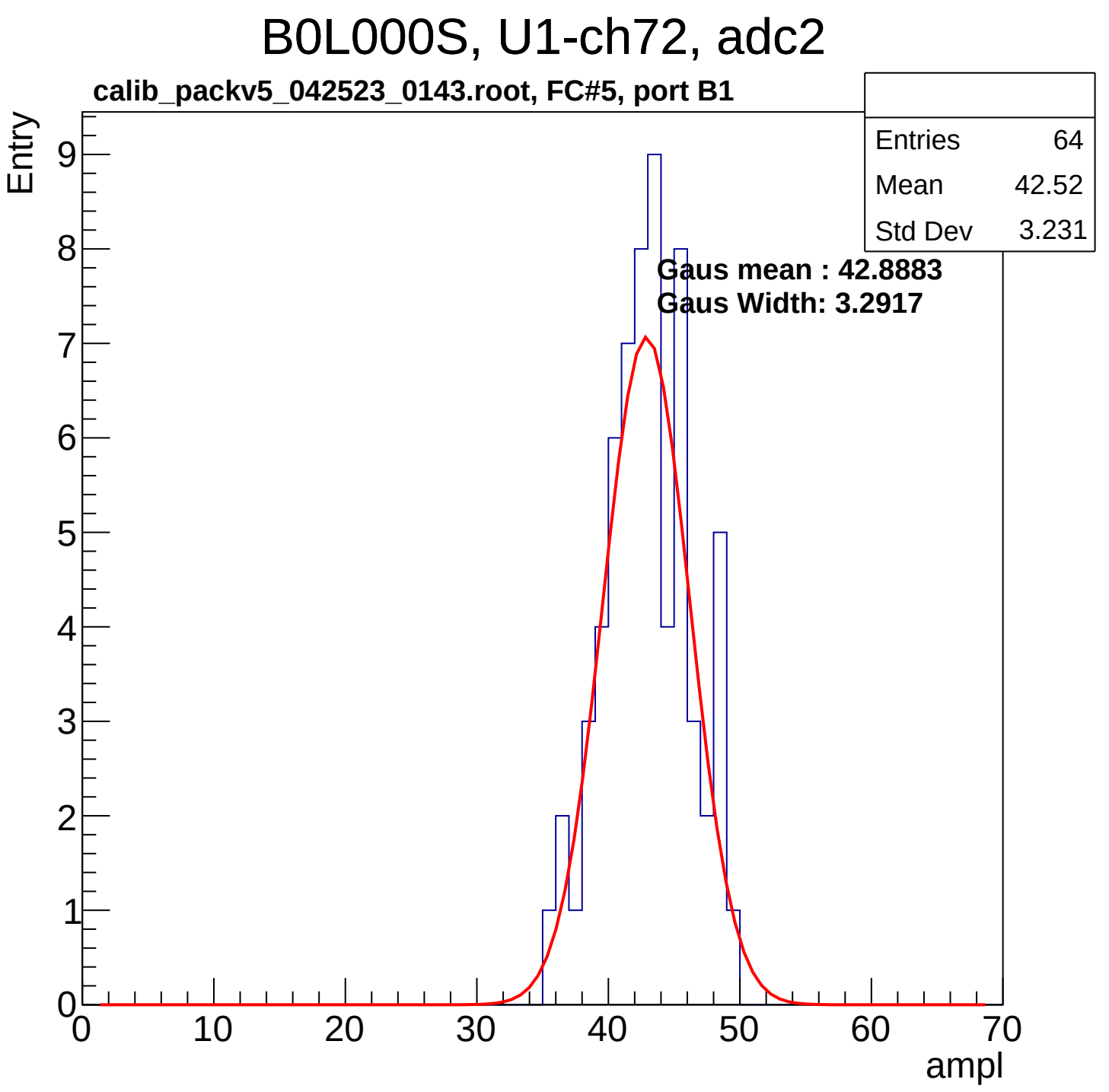

B0L000S, U1-ch72, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.18
Std Dev 3.626
Gaus mean : 36.7827
Gaus Width: 3.3560
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch72, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.52
Std Dev 3.231
Gaus mean : 42.8883
Gaus Width: 3.2917
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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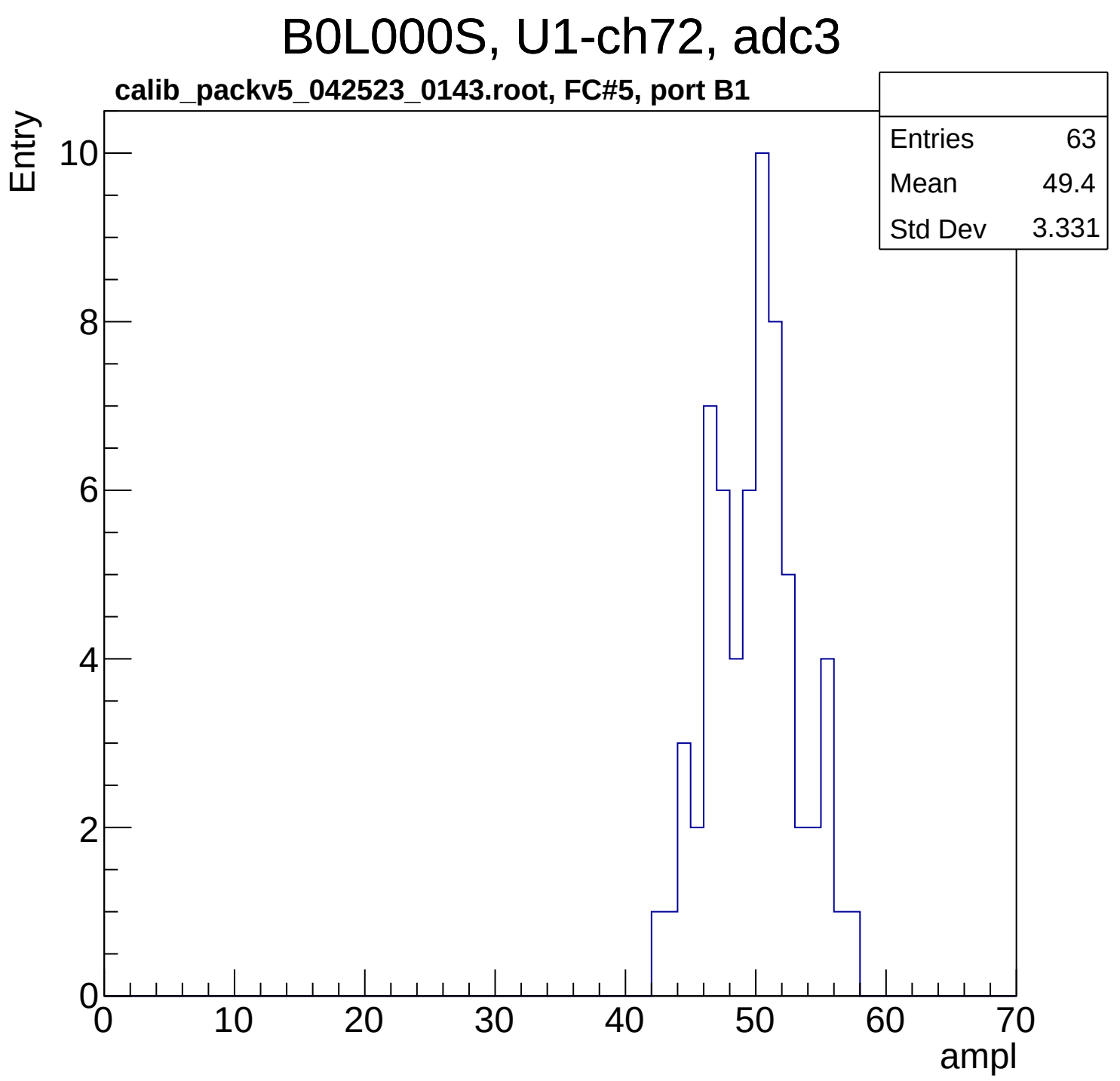
B0L000S, U1-ch72, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.4
Std Dev 3.331
Entry
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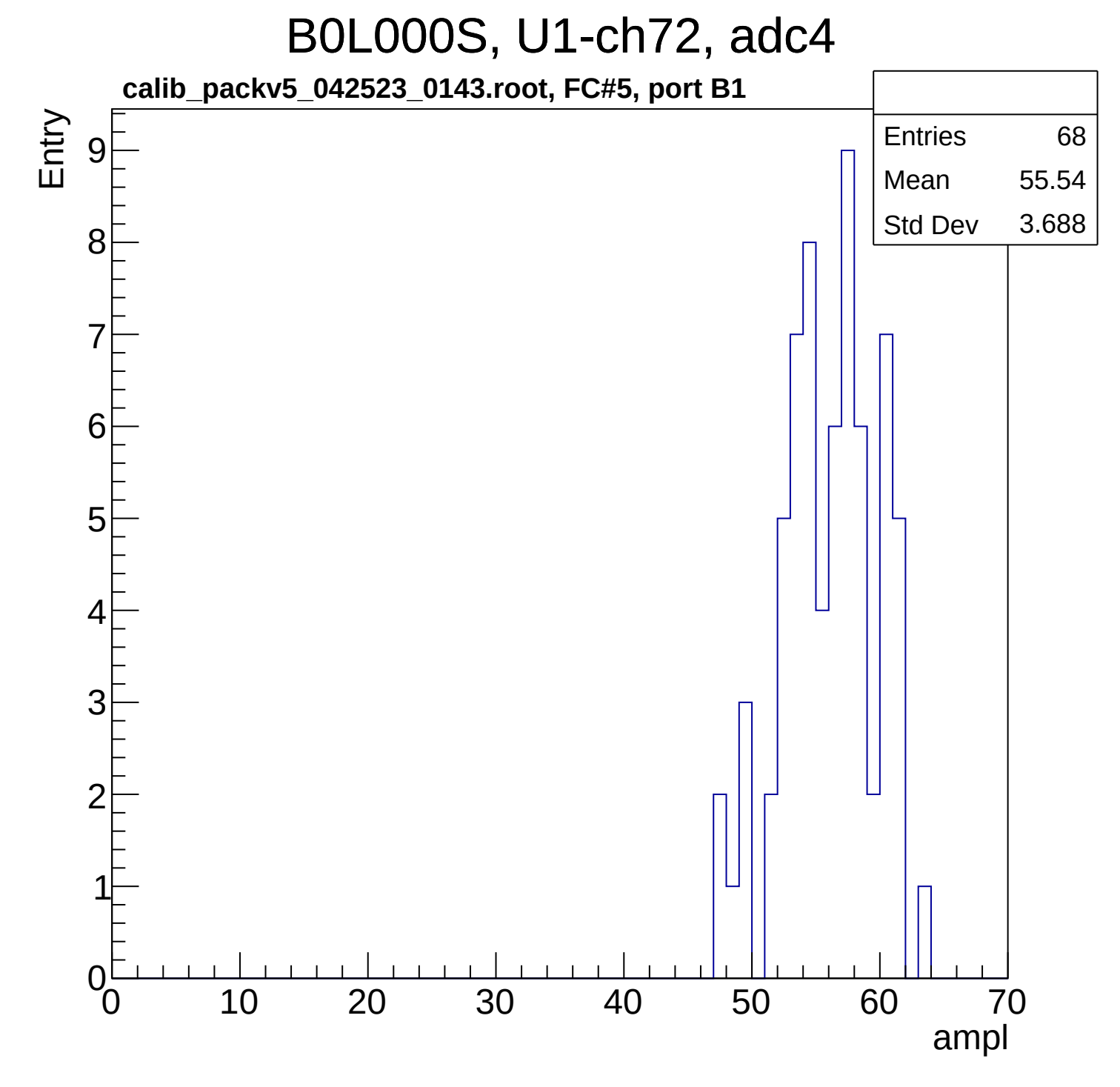
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch72, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 55.54
Std Dev 3.688
Entry
ampl
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3
4
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6
7
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0
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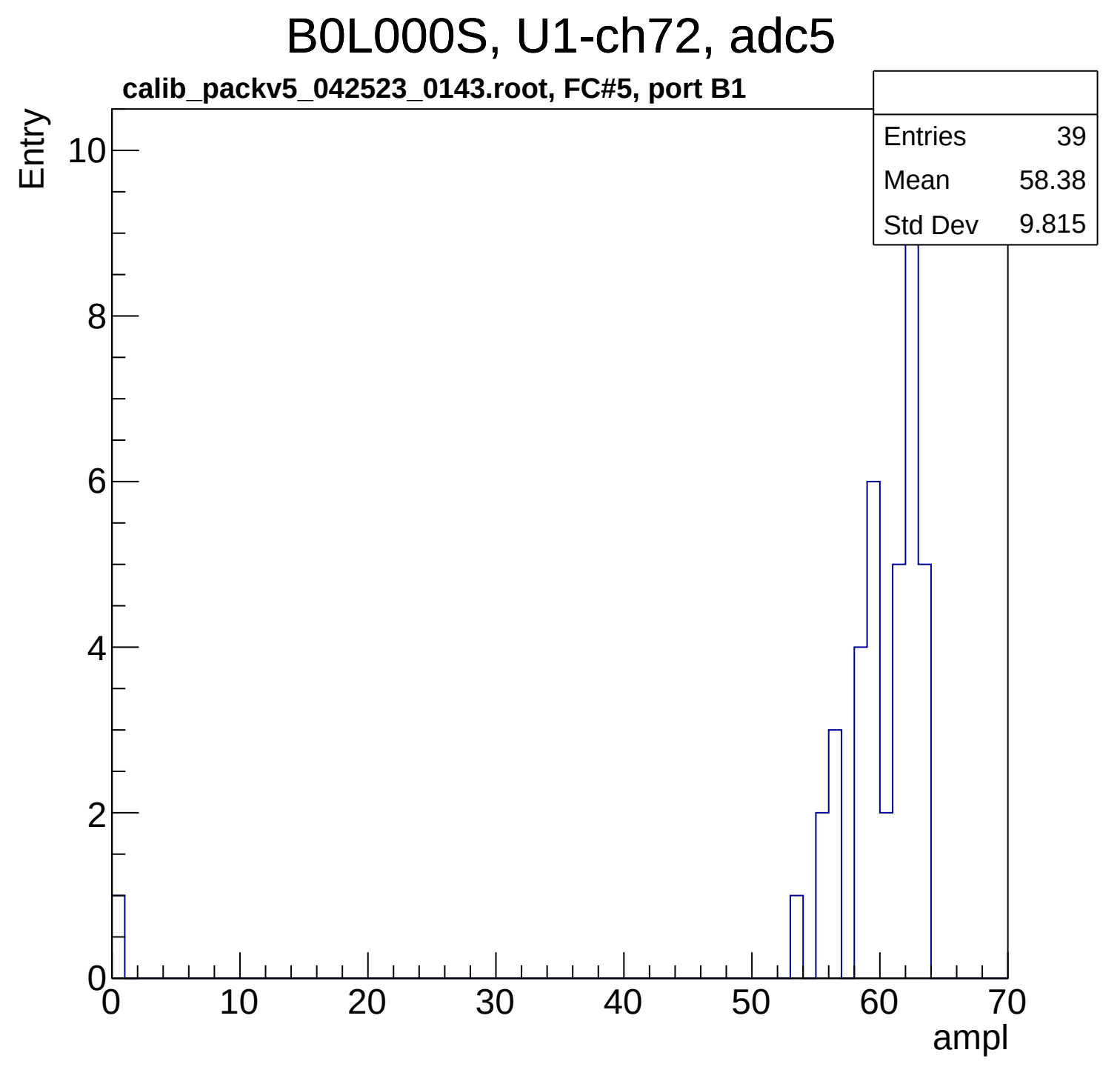
50
60
70


B0L000S, U1-ch72, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.38
Std Dev 9.815
Entry
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4
6
8
10
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10
20
30
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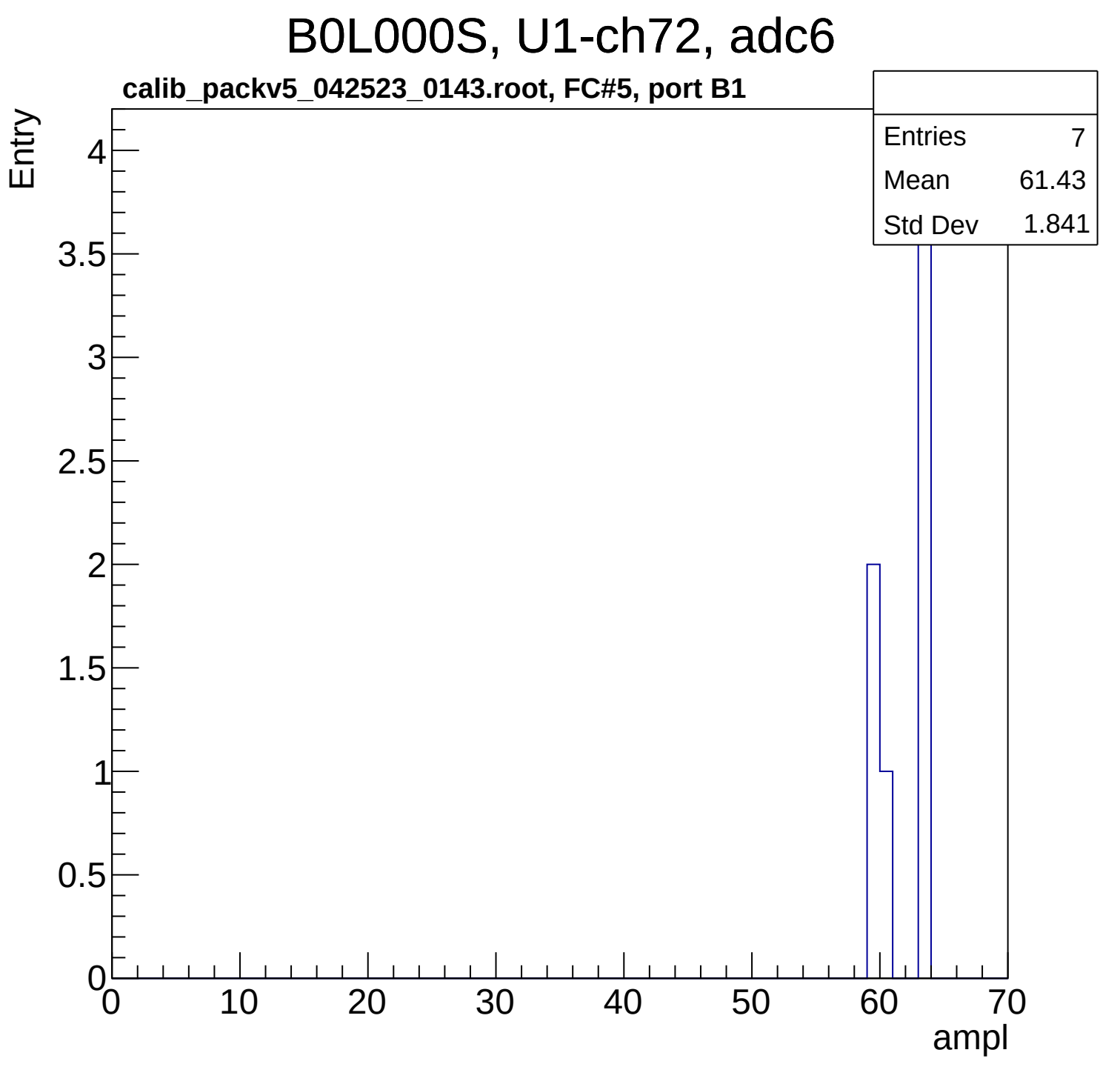
50
60
70
ampl


B0L000S, U1-ch72, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.43
Std Dev 1.841
Entry
ampl
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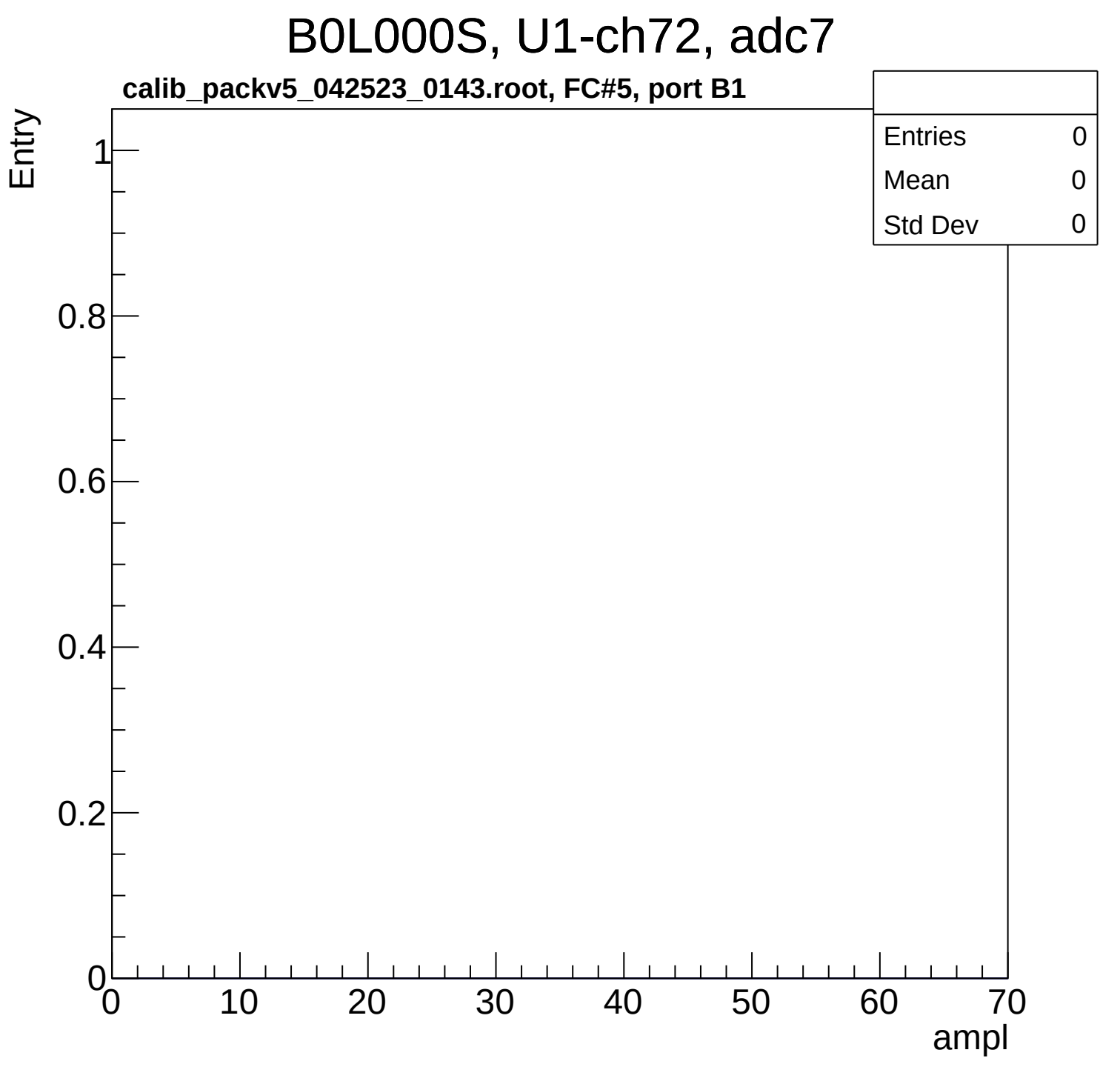
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U1-ch72, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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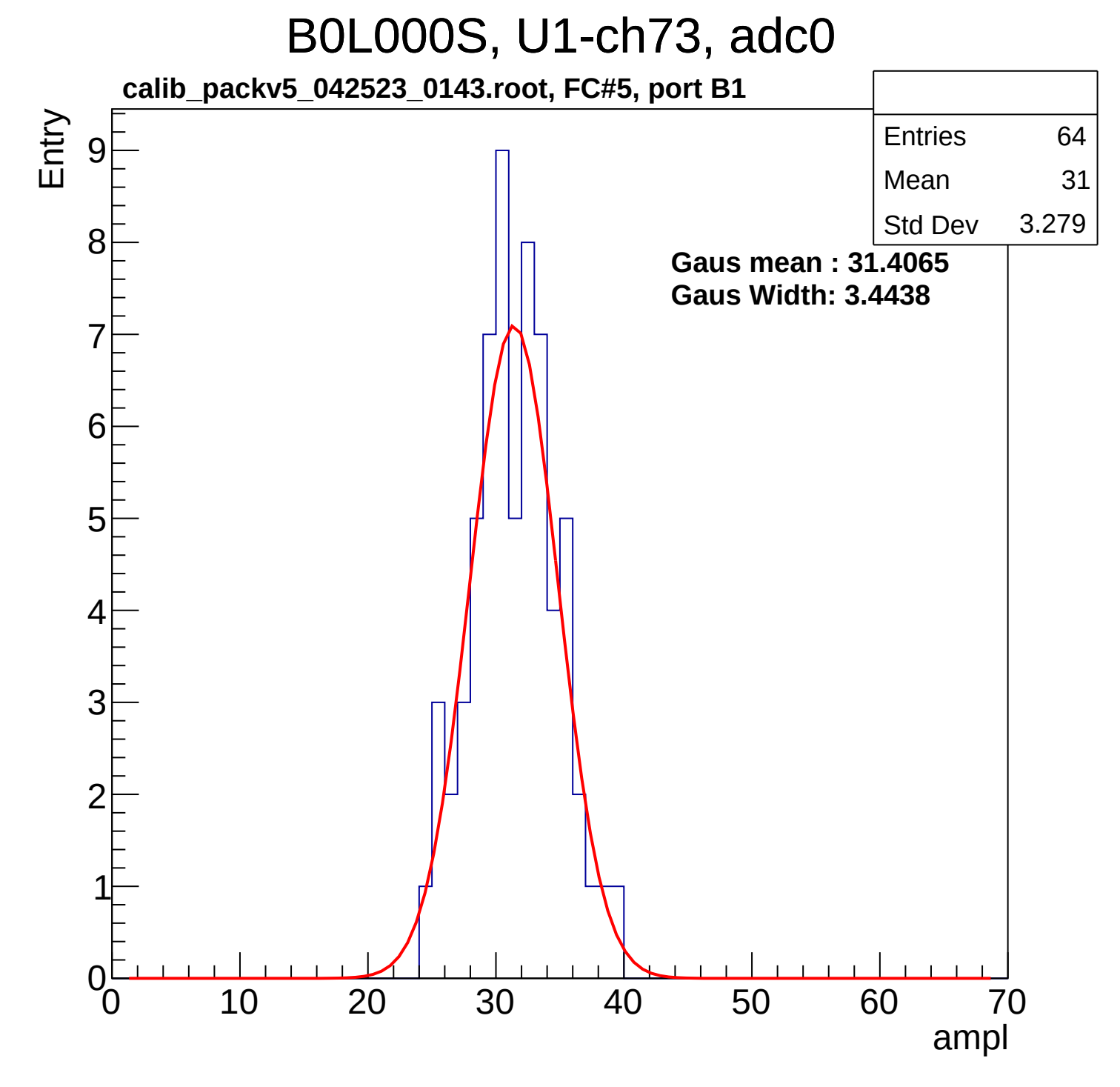
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch73, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 31
Std Dev 3.279
Gaus mean : 31.4065
Gaus Width: 3.4438
Entry
ampl
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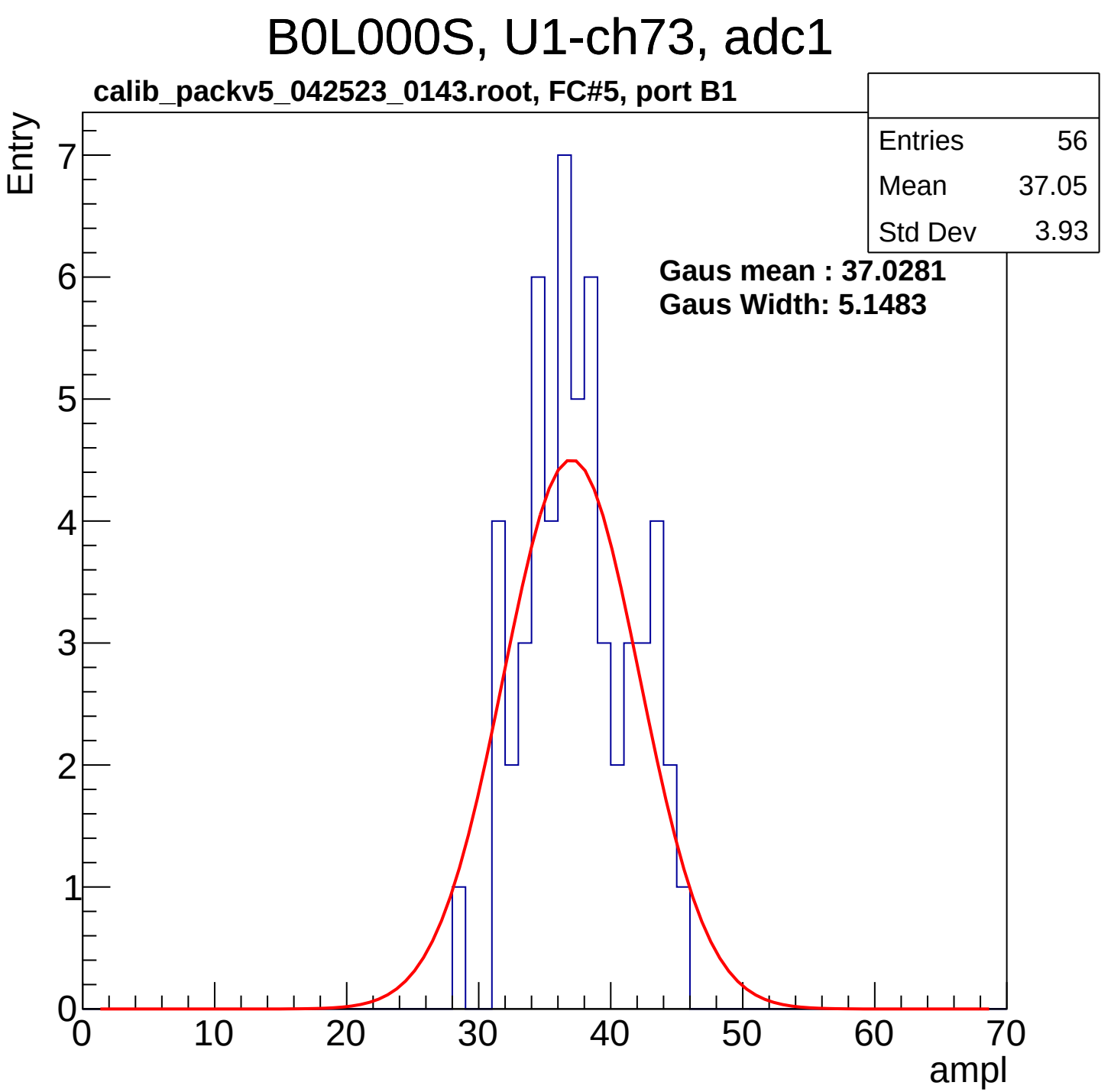
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch73, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.05
Std Dev 3.93
Gaus mean : 37.0281
Gaus Width: 5.1483
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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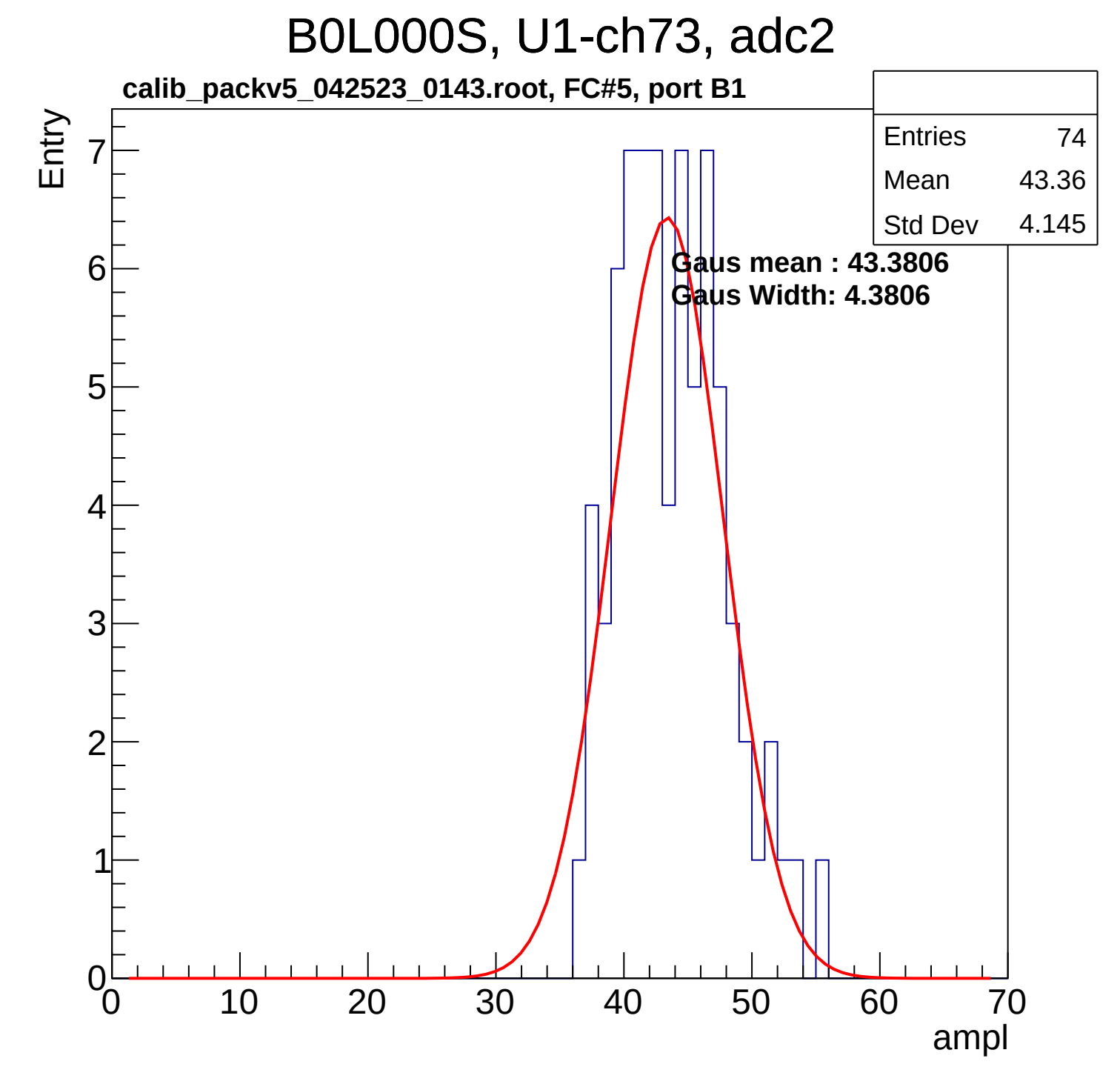
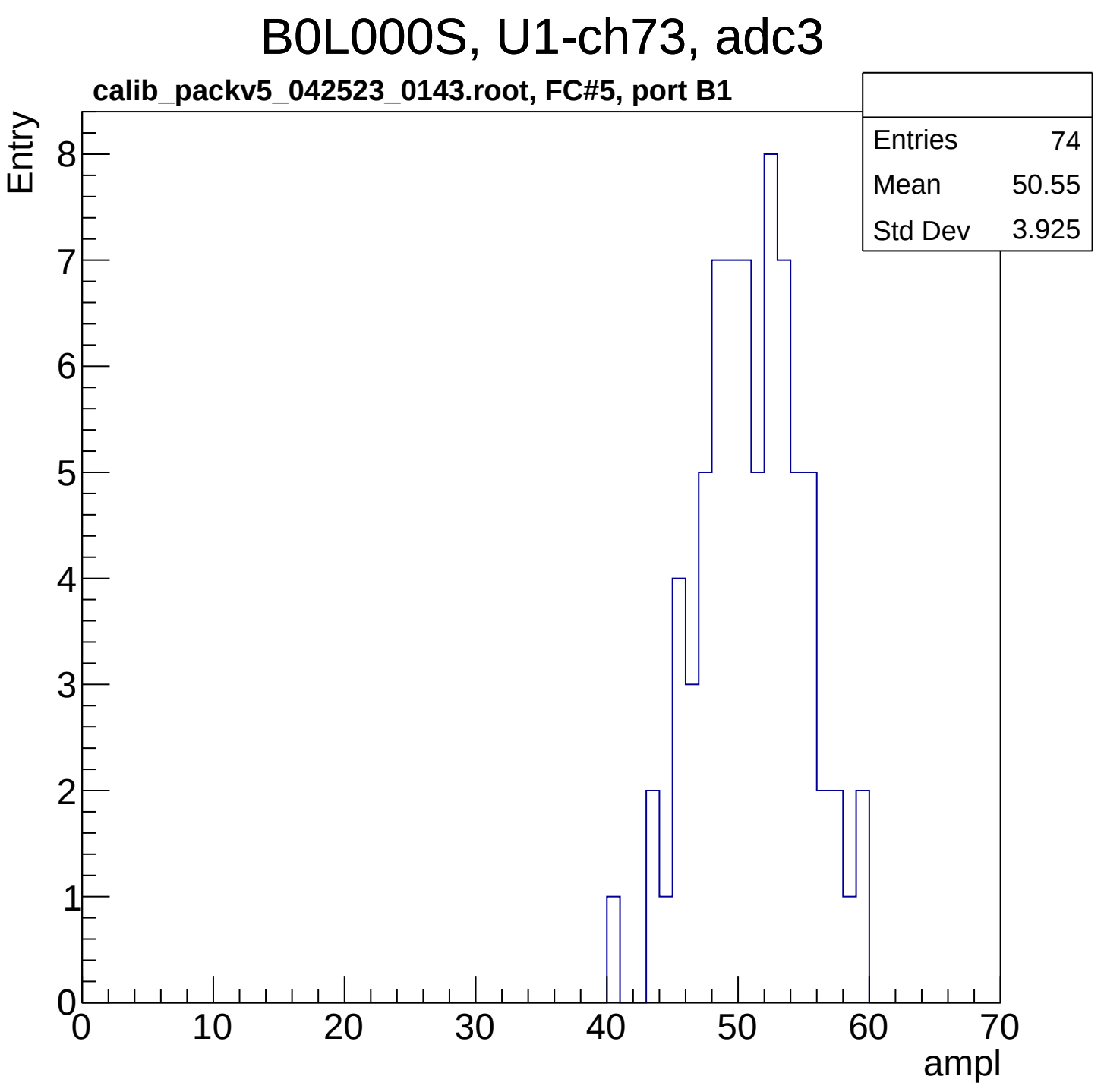

B0L000S, U1-ch73, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 43.36
Std Dev 4.145
Gaus mean : 43.3806
Gaus Width: 4.3806
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch73, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 50.55
Std Dev 3.925
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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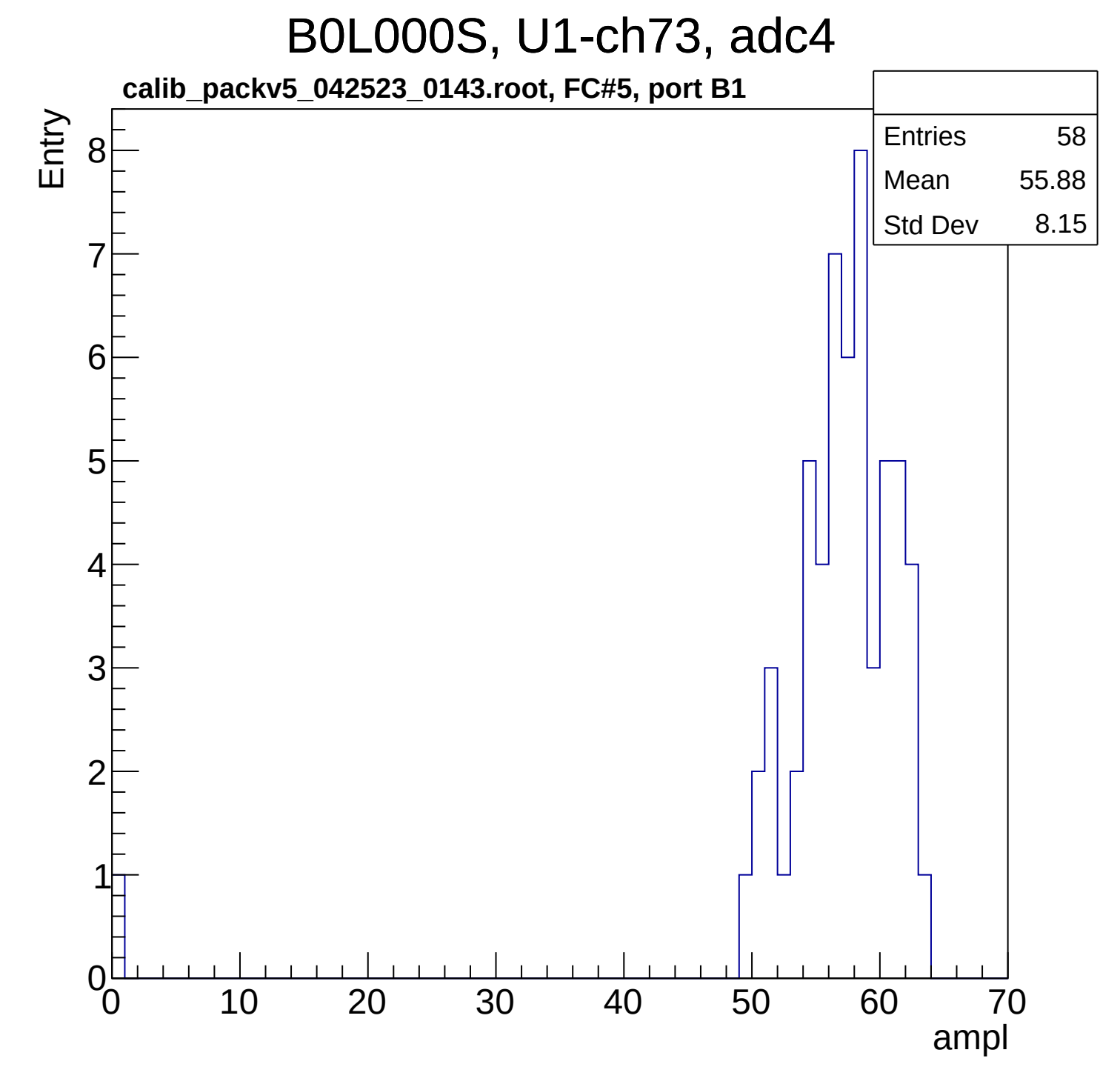
70
ampl


B0L000S, U1-ch73, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.88
Std Dev 8.15
Entry
ampl
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1
2
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4
5
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7
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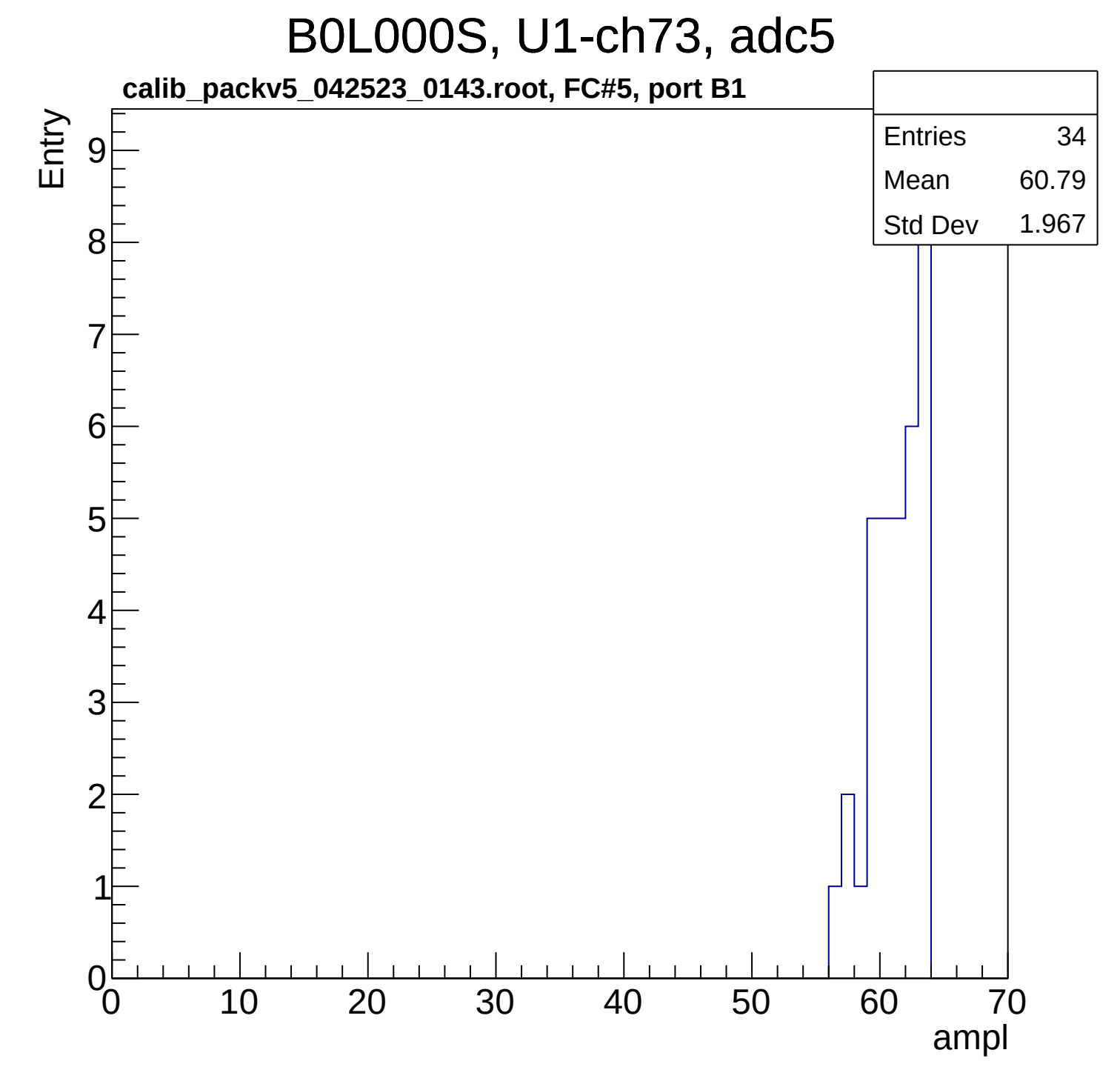
B0L000S, U1-ch73, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 60.79
Std Dev 1.967
Entry
ampl
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1
2
3
4
5
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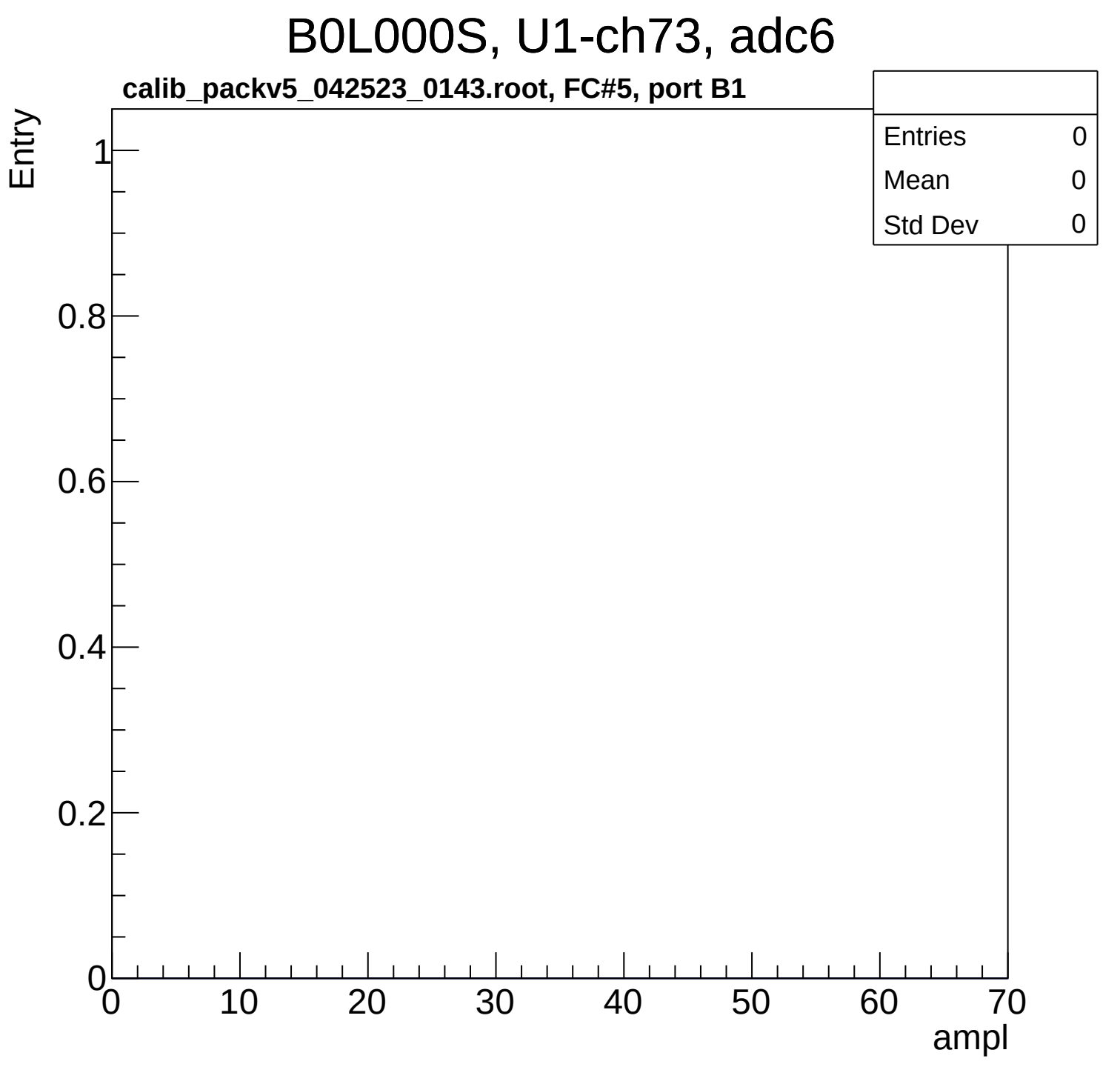
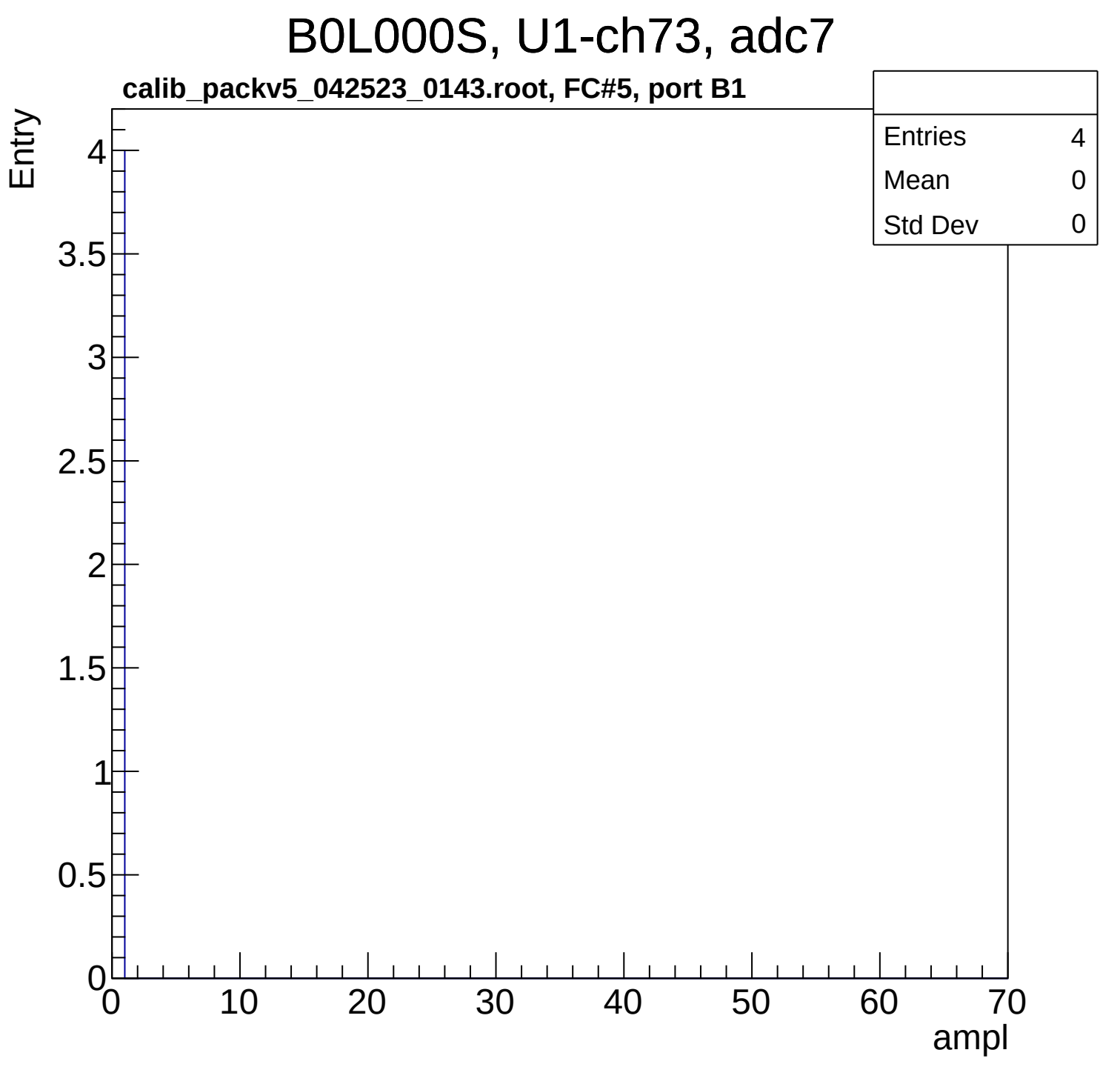
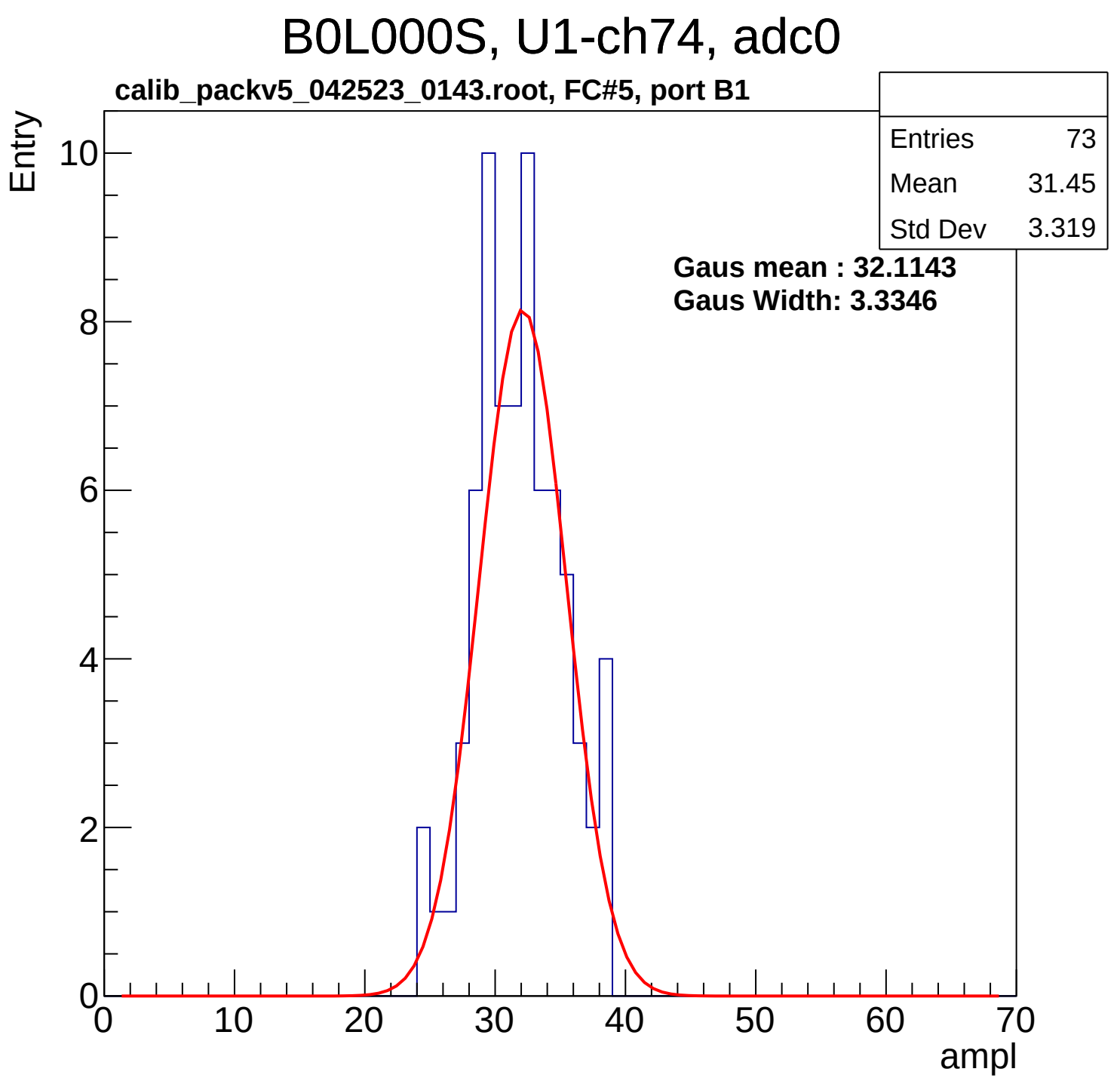

B0L000S, U1-ch73, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch73, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch74, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 31.45
Std Dev 3.319
Gaus mean : 32.1143
Gaus Width: 3.3346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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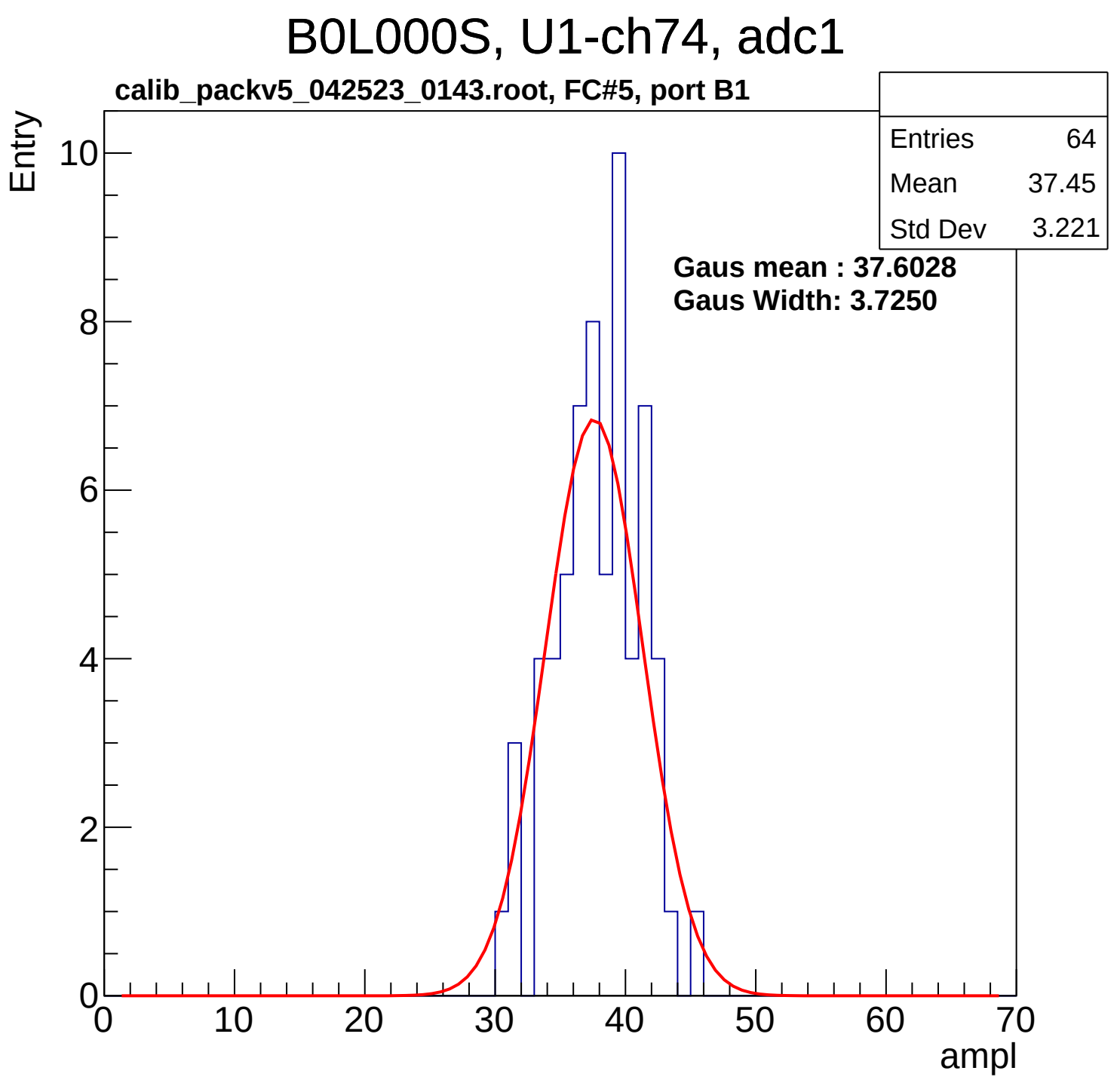
B0L000S, U1-ch74, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 37.45
Std Dev 3.221
Gaus mean : 37.6028
Gaus Width: 3.7250
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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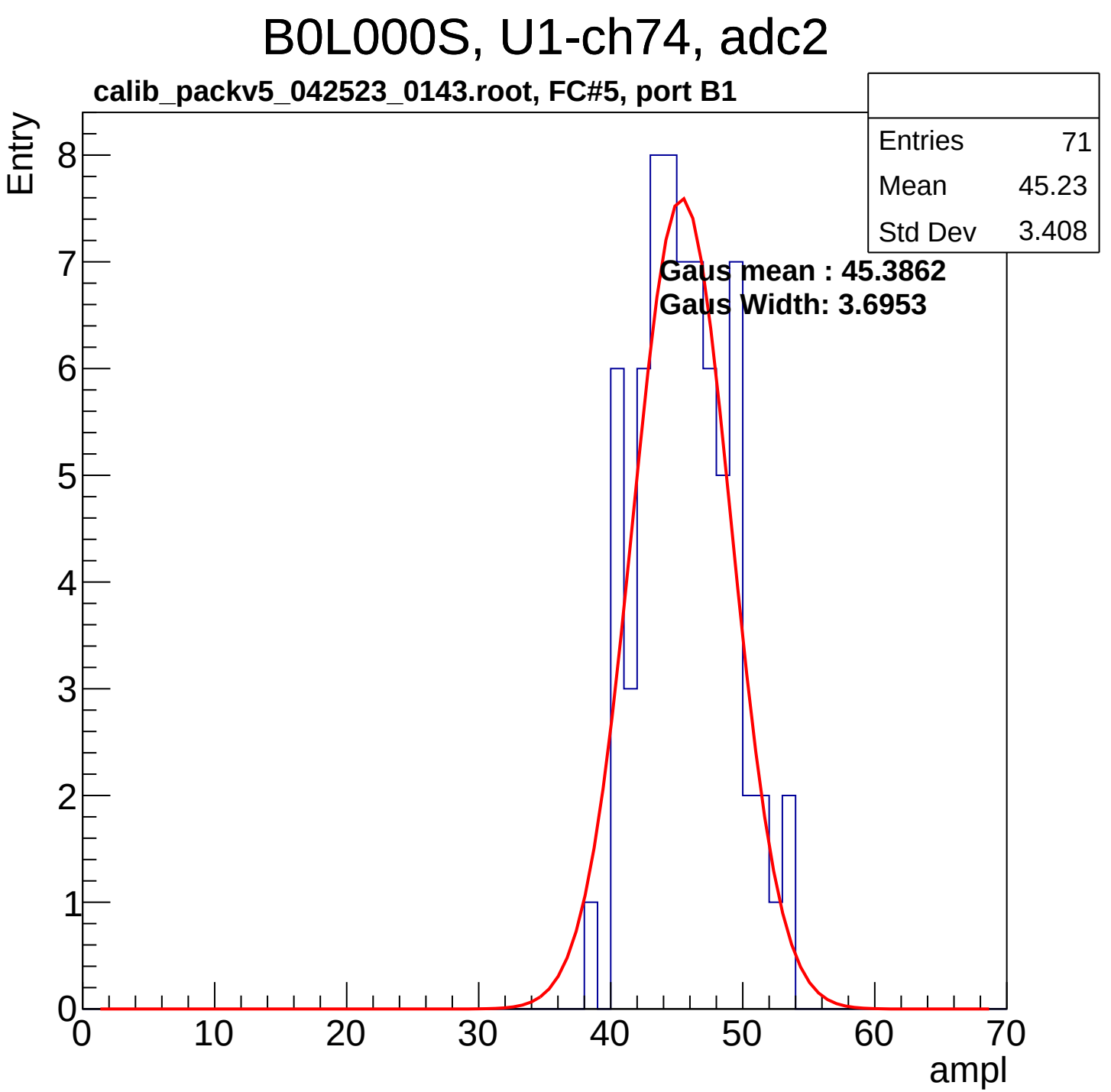
B0L000S, U1-ch74, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 45.23
Std Dev 3.408
Gaus mean : 45.3862
Gaus Width: 3.6953
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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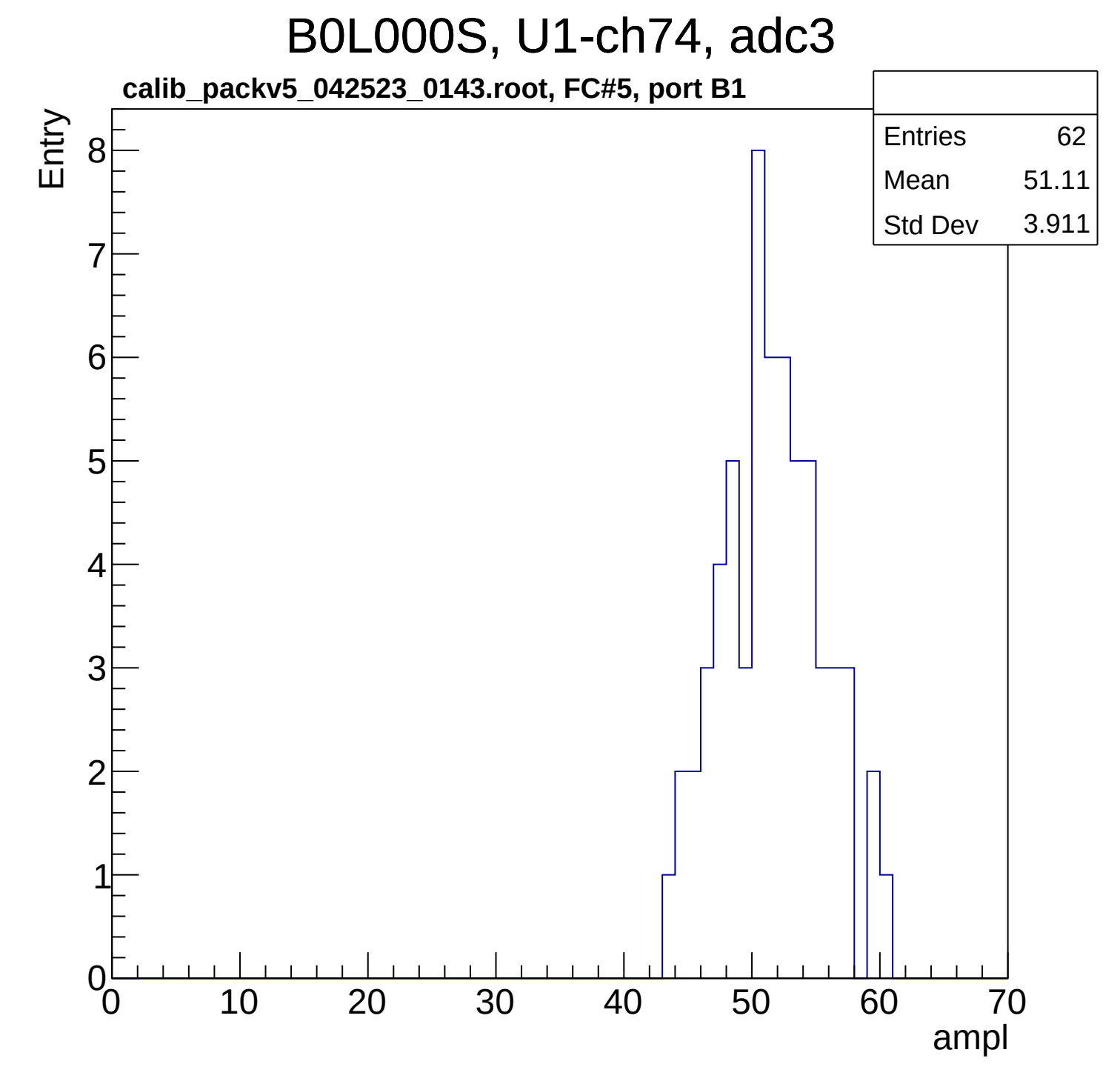
B0L000S, U1-ch74, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 51.11
Std Dev 3.911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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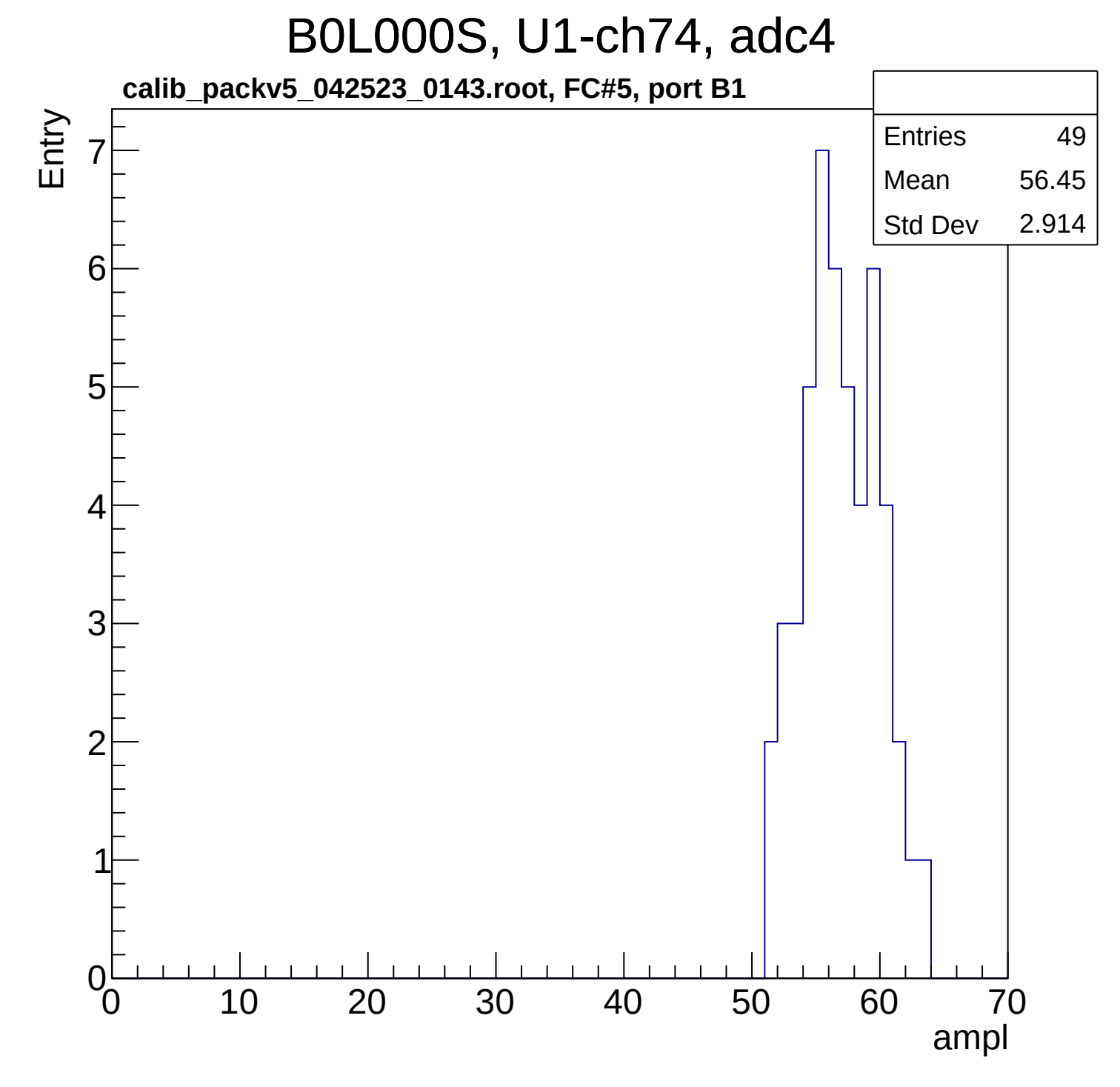
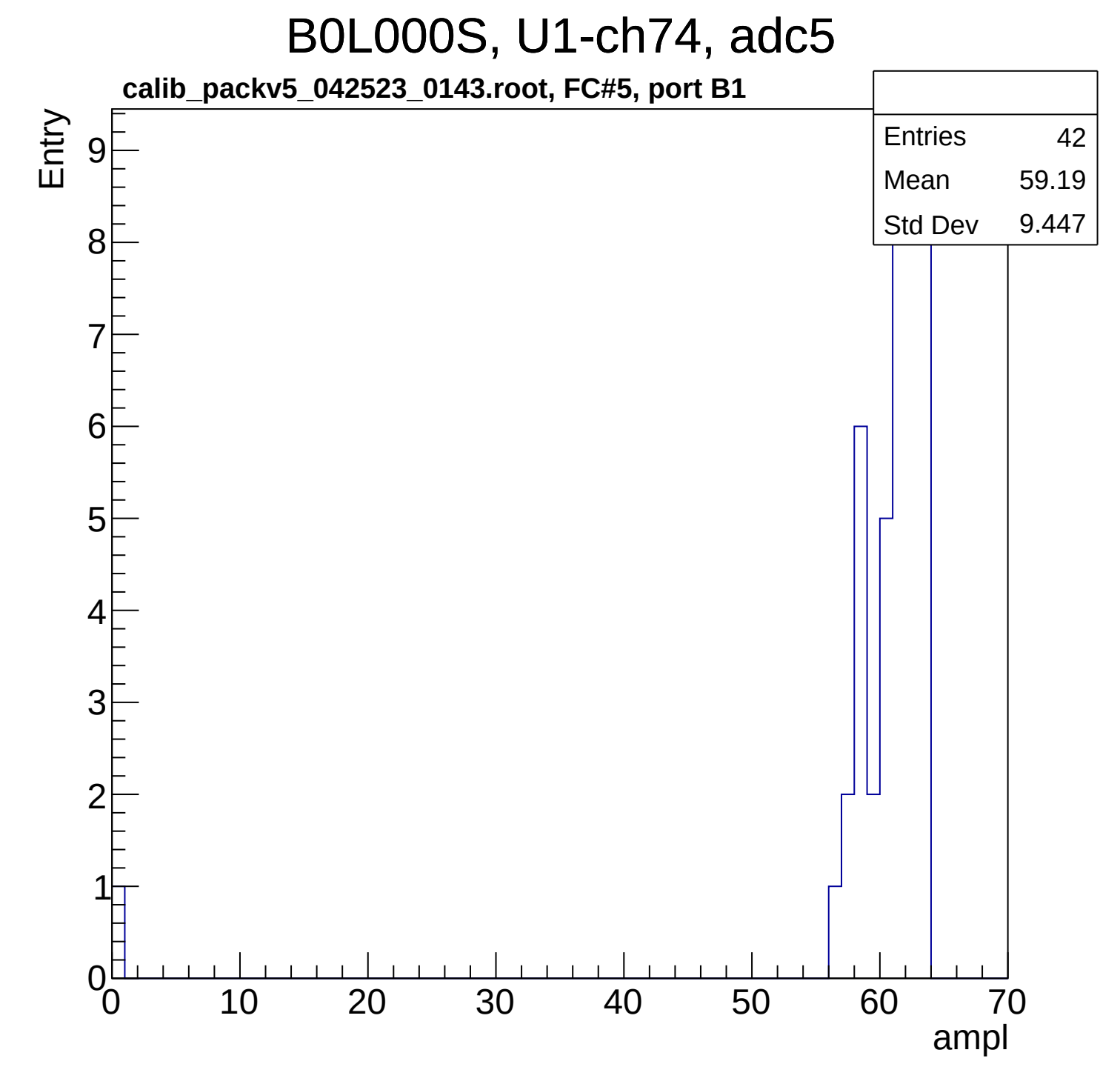
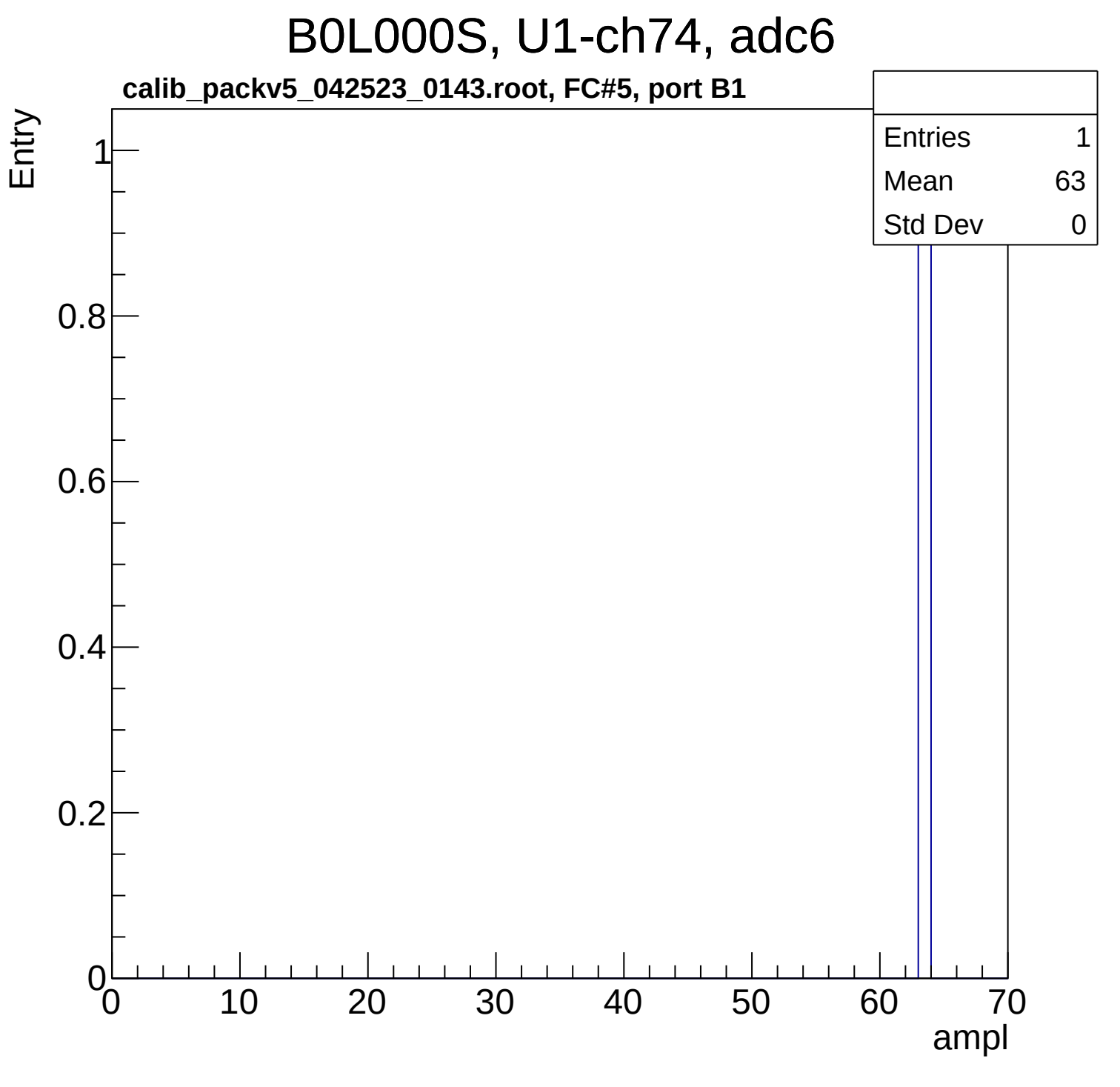
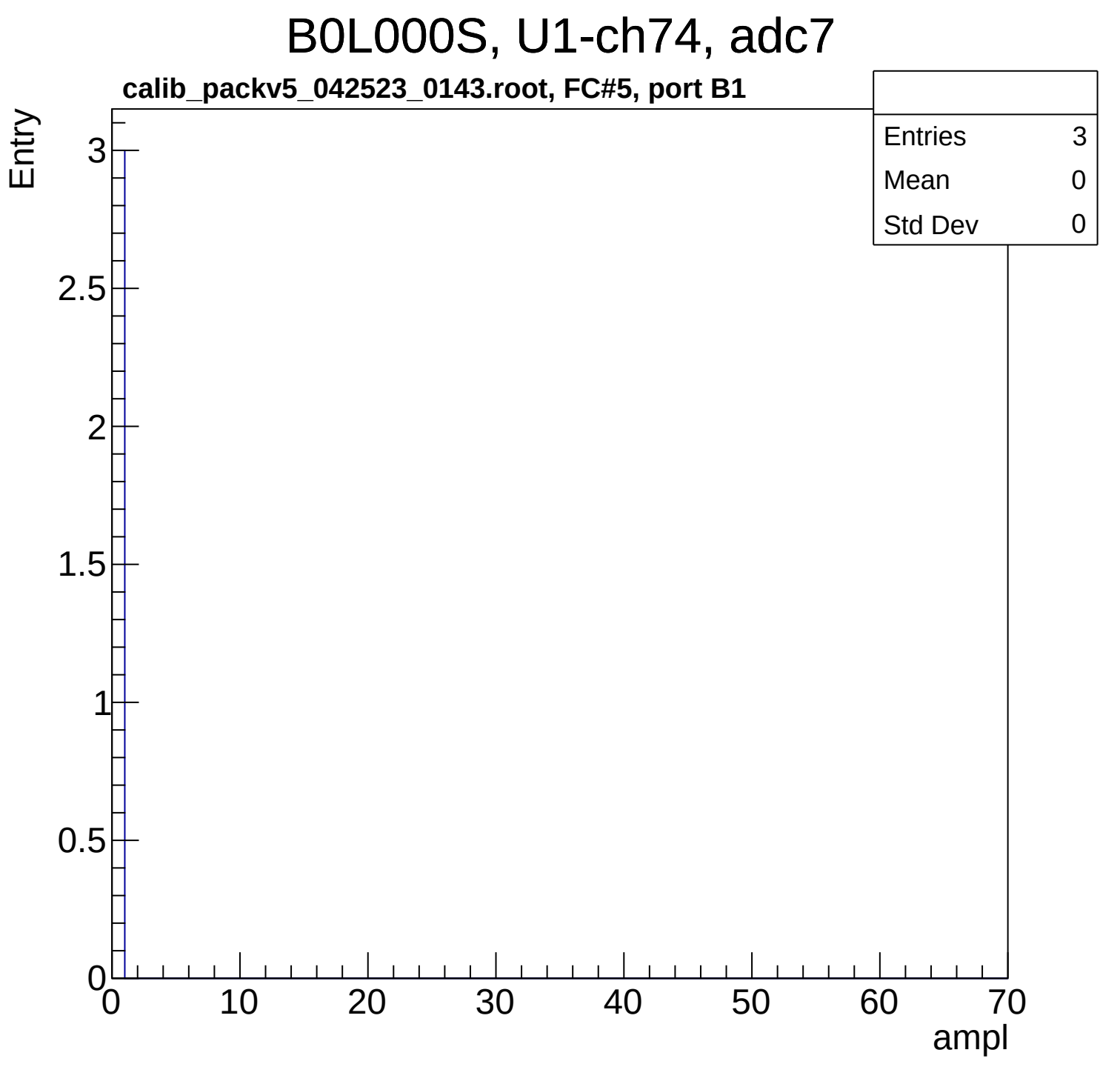
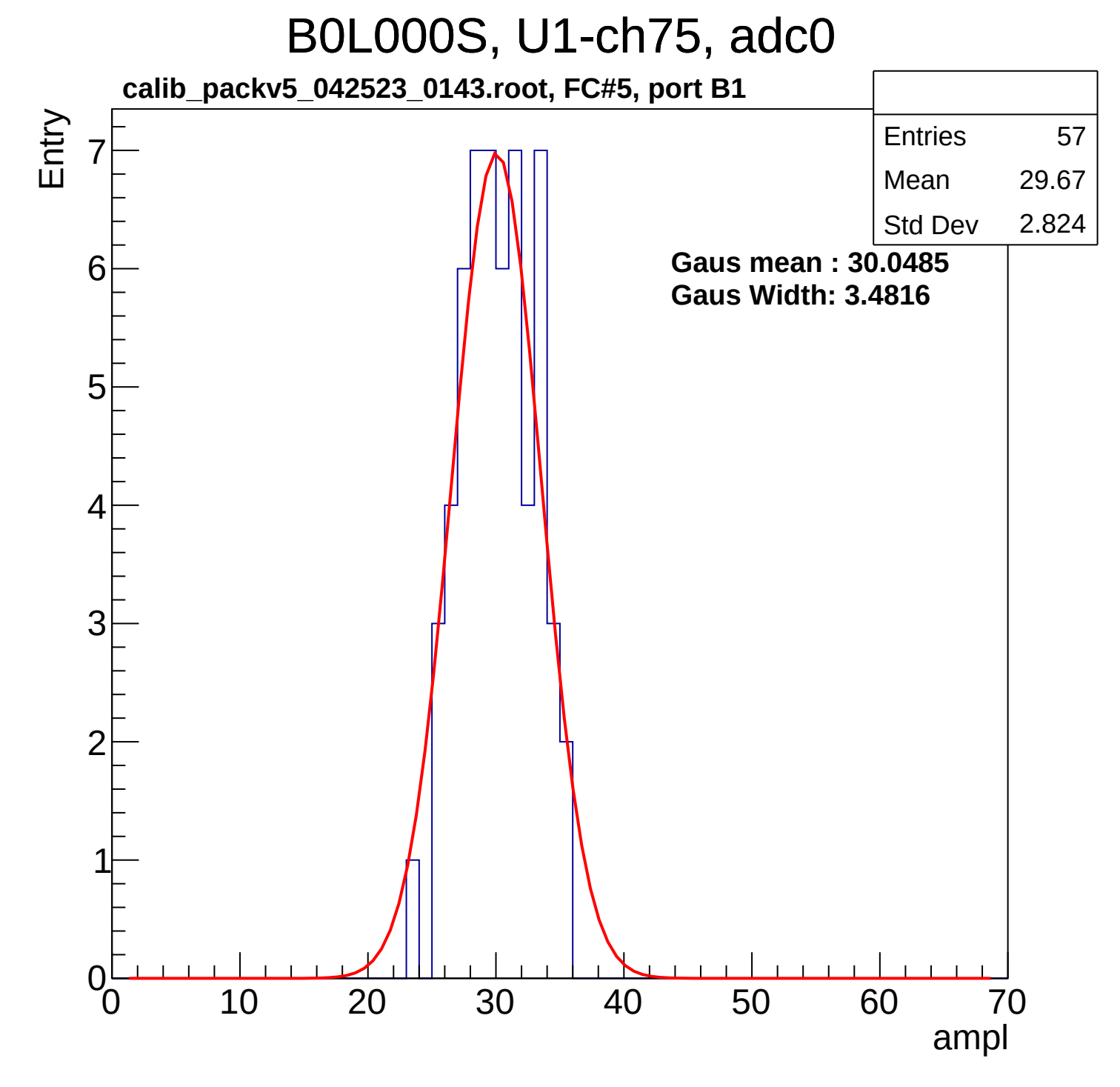
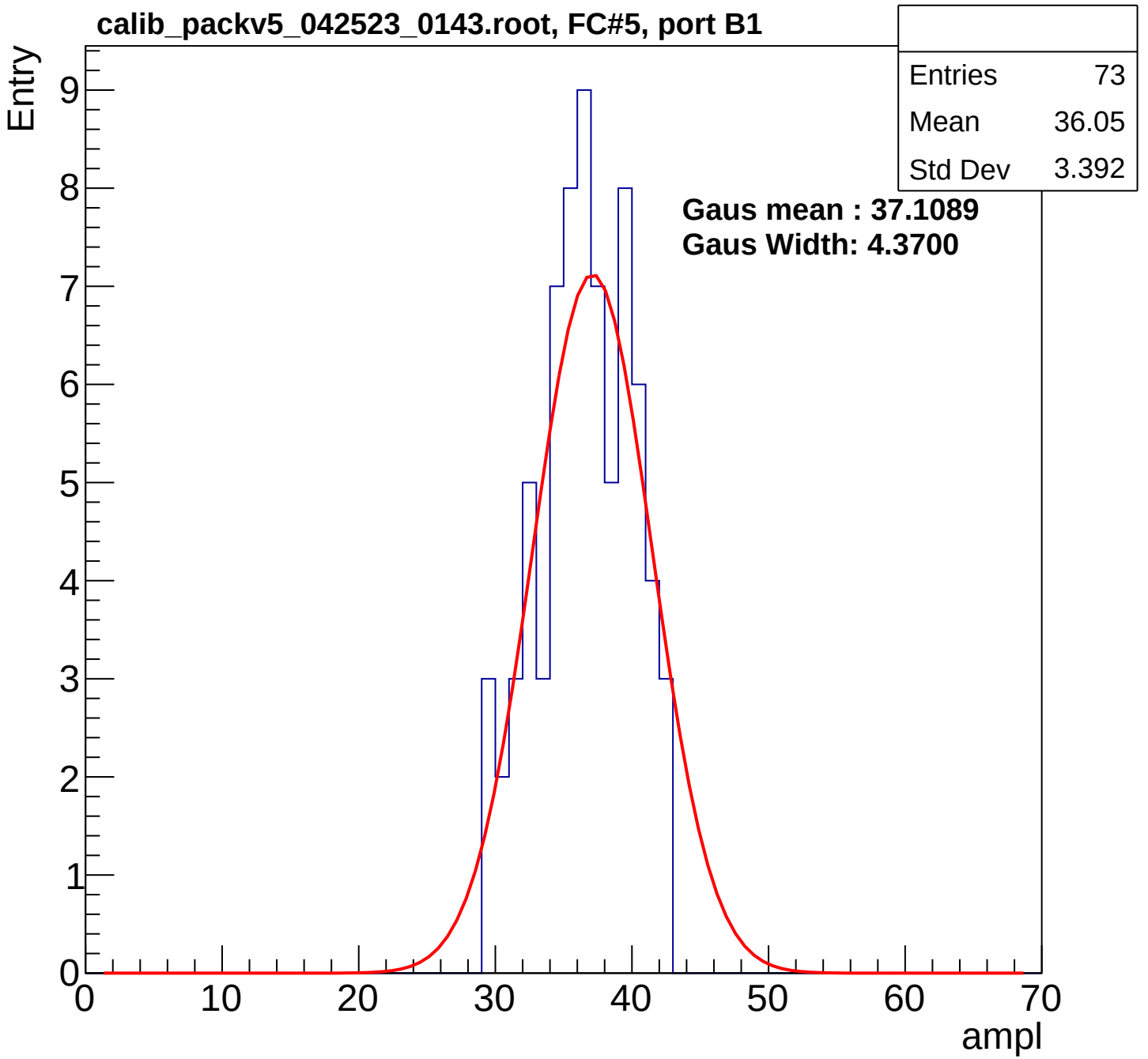

B0L000S, U1-ch74, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 56.45
Std Dev 2.914
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

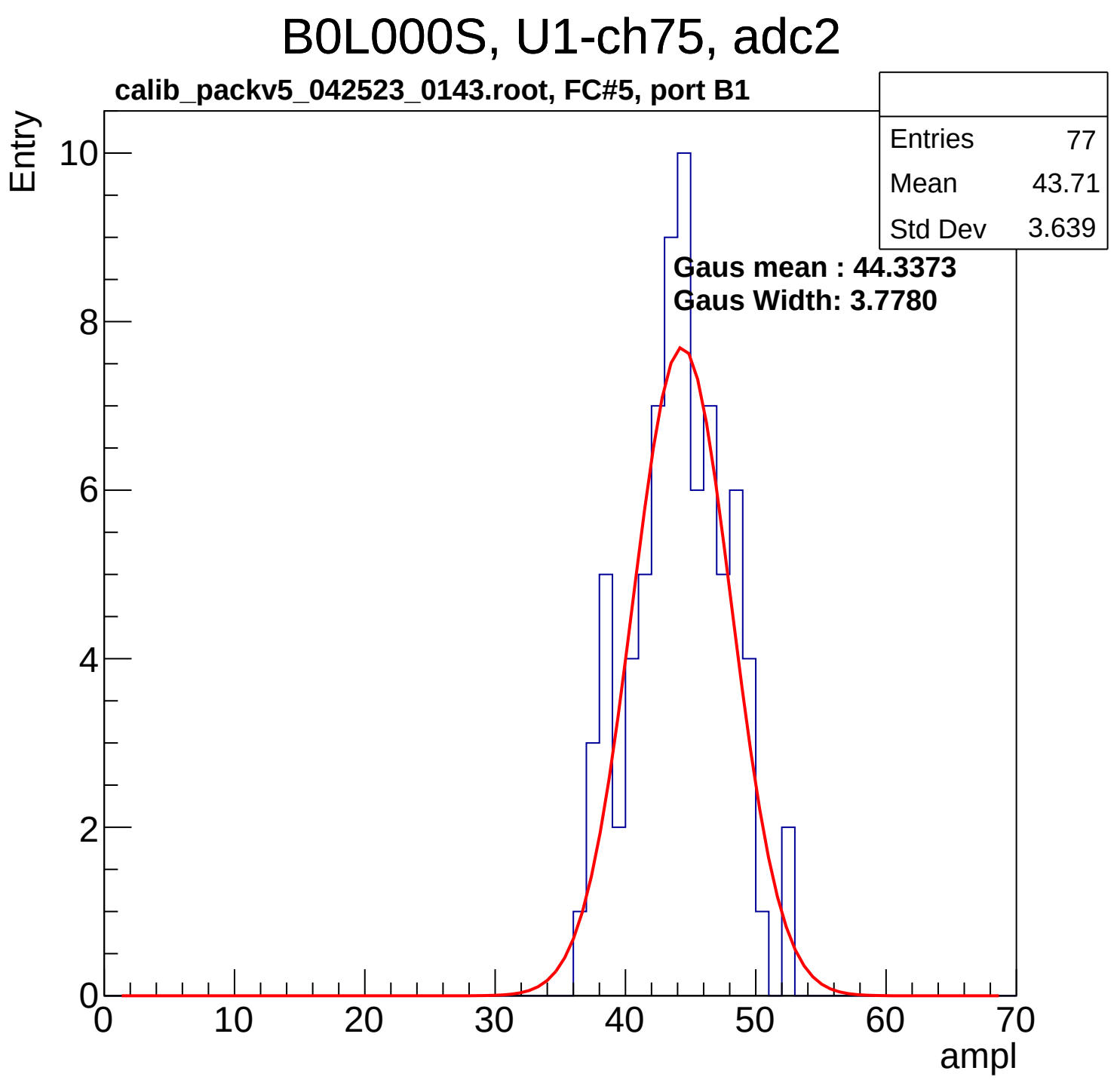

B0L000S, U1-ch74, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.19
Std Dev 9.447
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

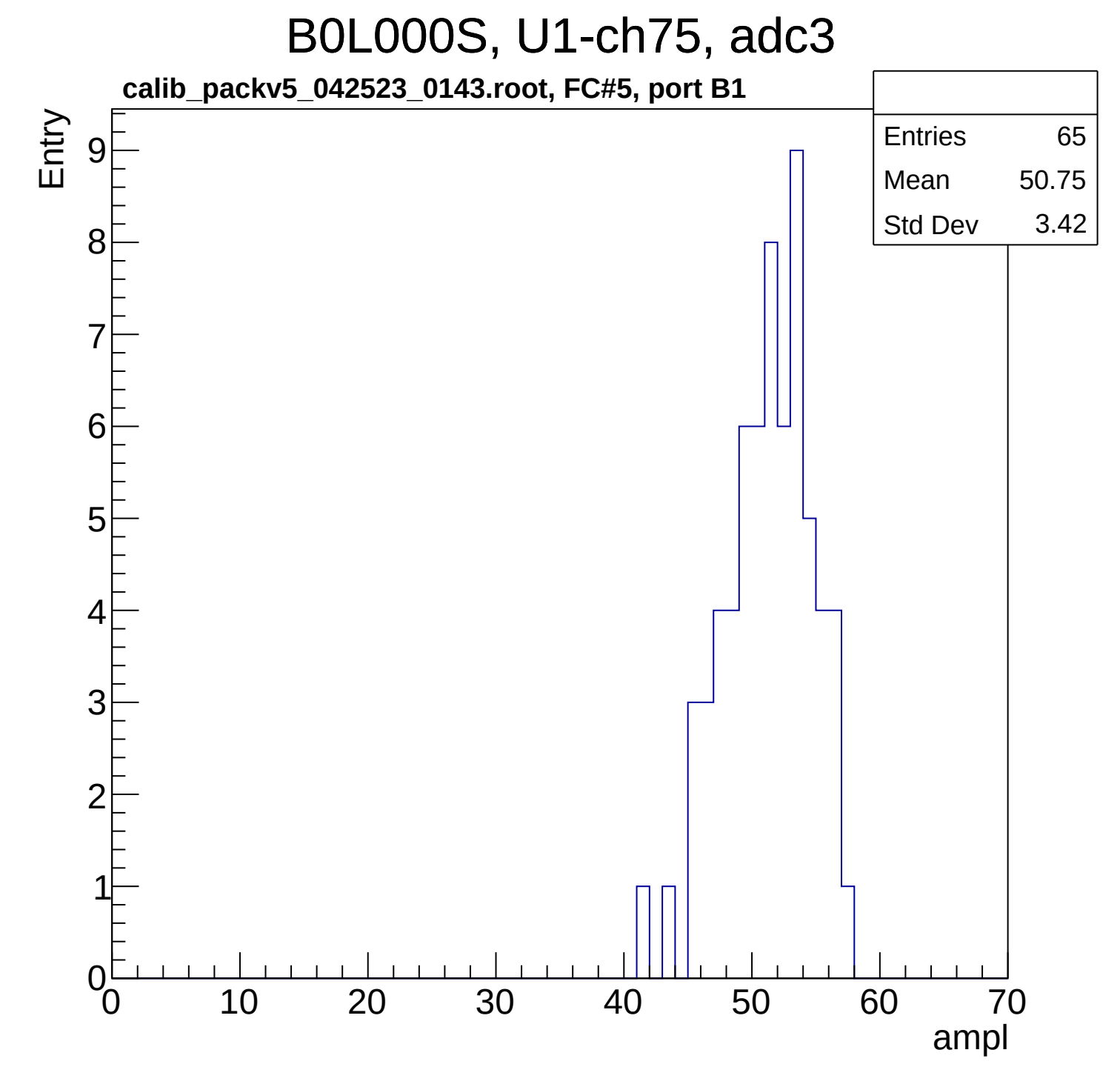

B0L000S, U1-ch74, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

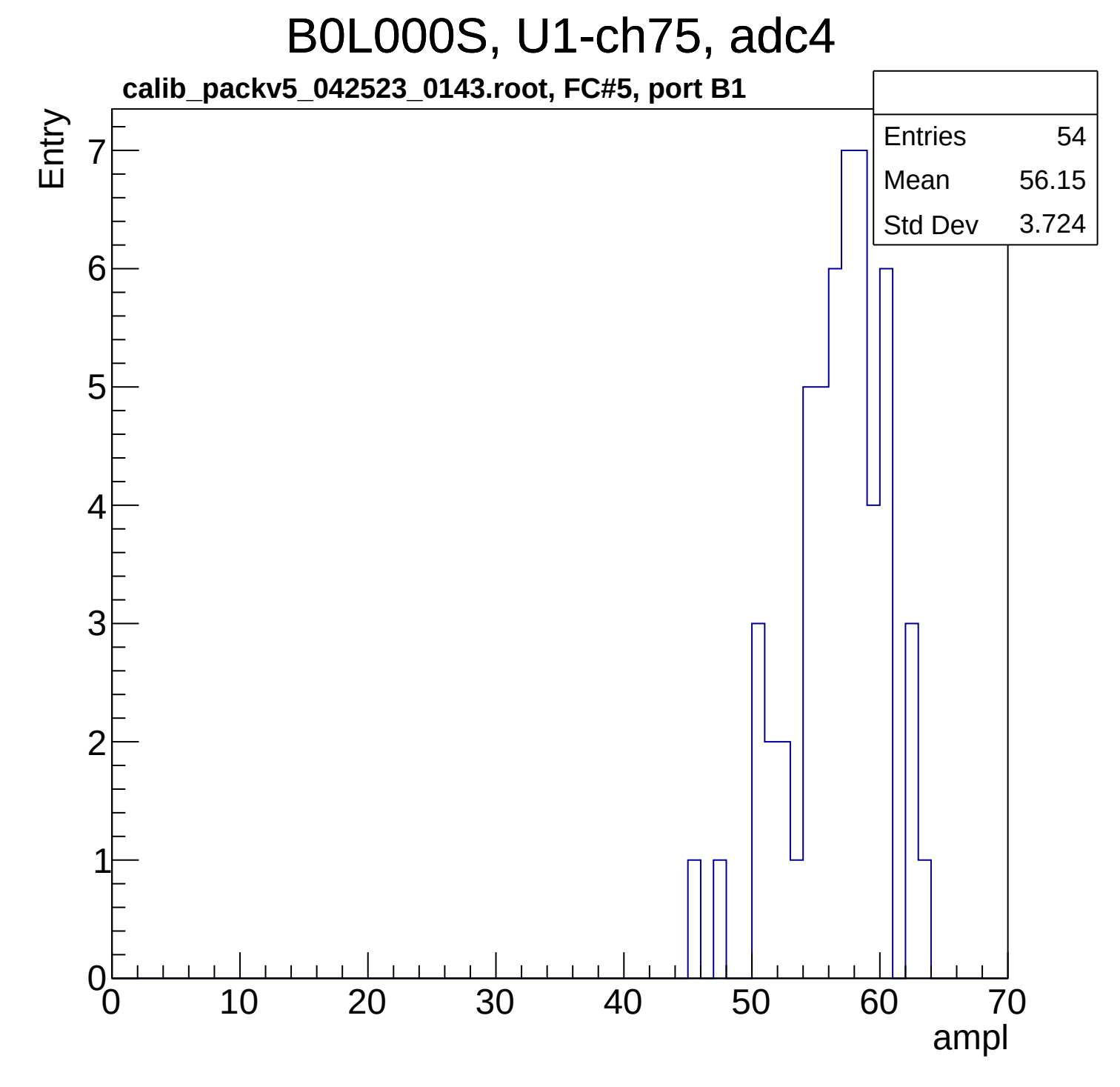

B0L000S, U1-ch74, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

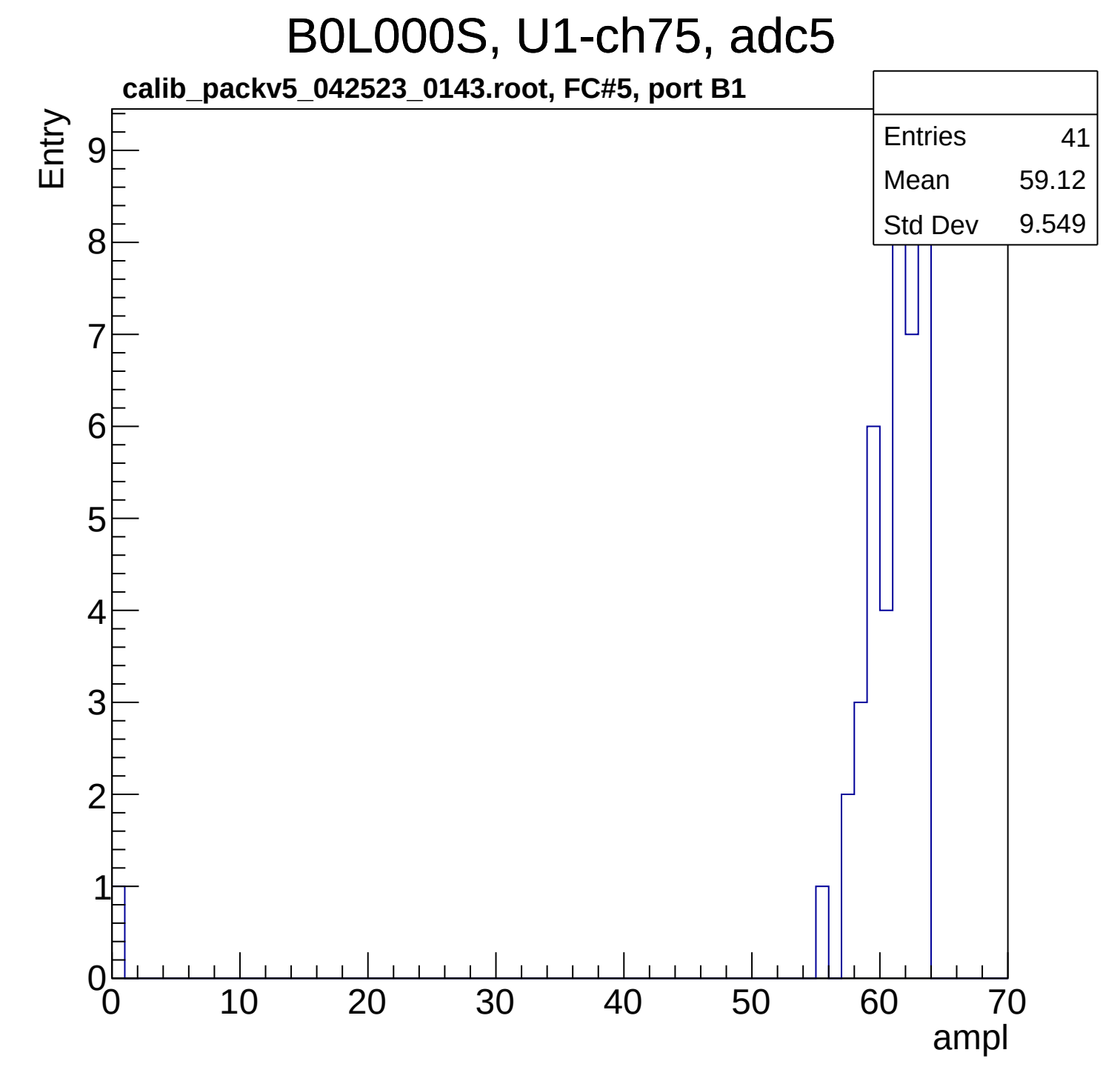

B0L000S, U1-ch75, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 29.67
Std Dev 2.824
Gaus mean : 30.0485
Gaus Width: 3.4816
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch75, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.05
Std Dev 3.392
Gaus mean : 37.1089
Gaus Width: 4.3700
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch75, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 43.71
Std Dev 3.639
Gaus mean : 44.3373
Gaus Width: 3.7780
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch75, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.75
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch75, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.15
Std Dev 3.724
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch75, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.12
Std Dev 9.549
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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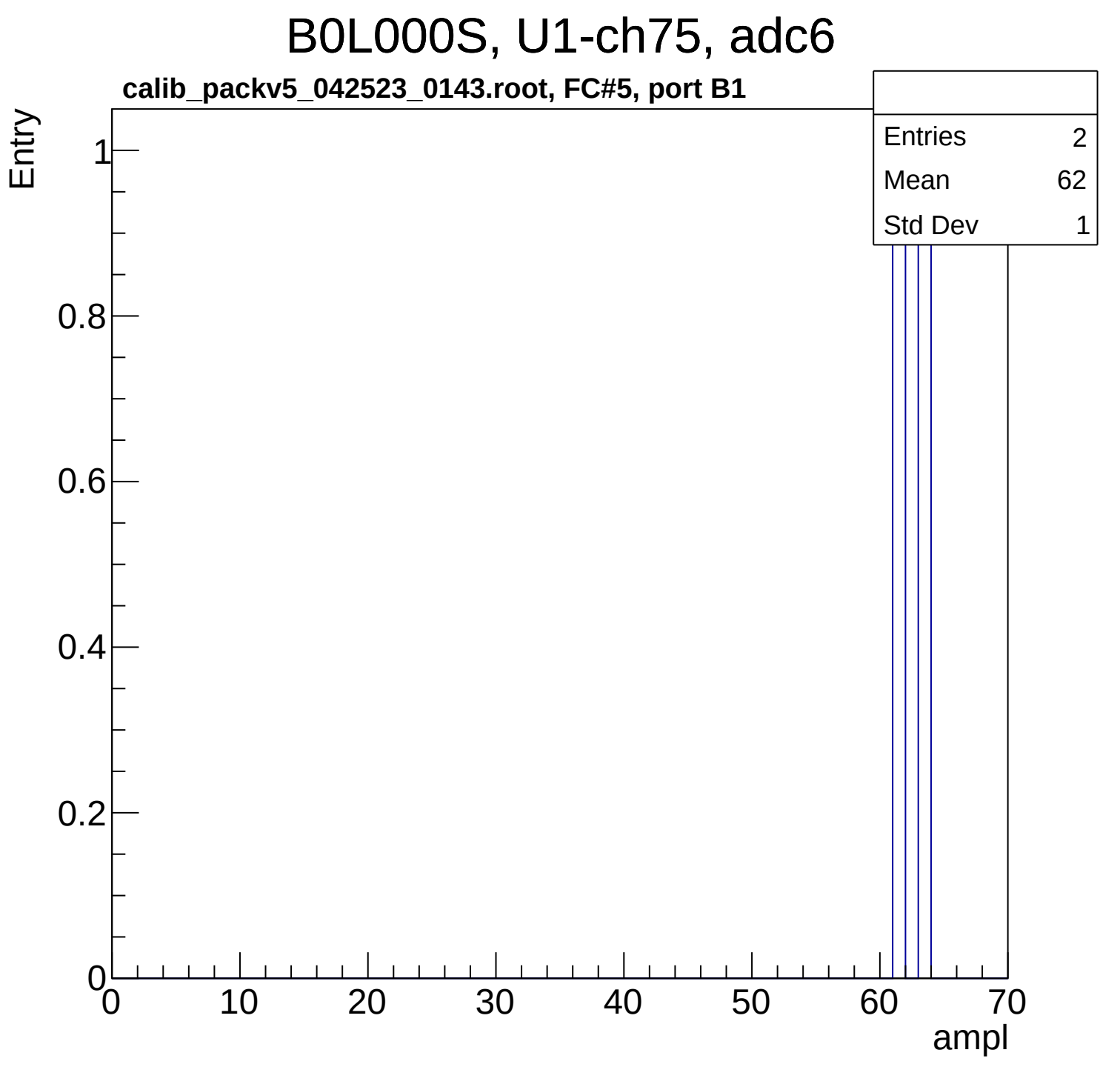
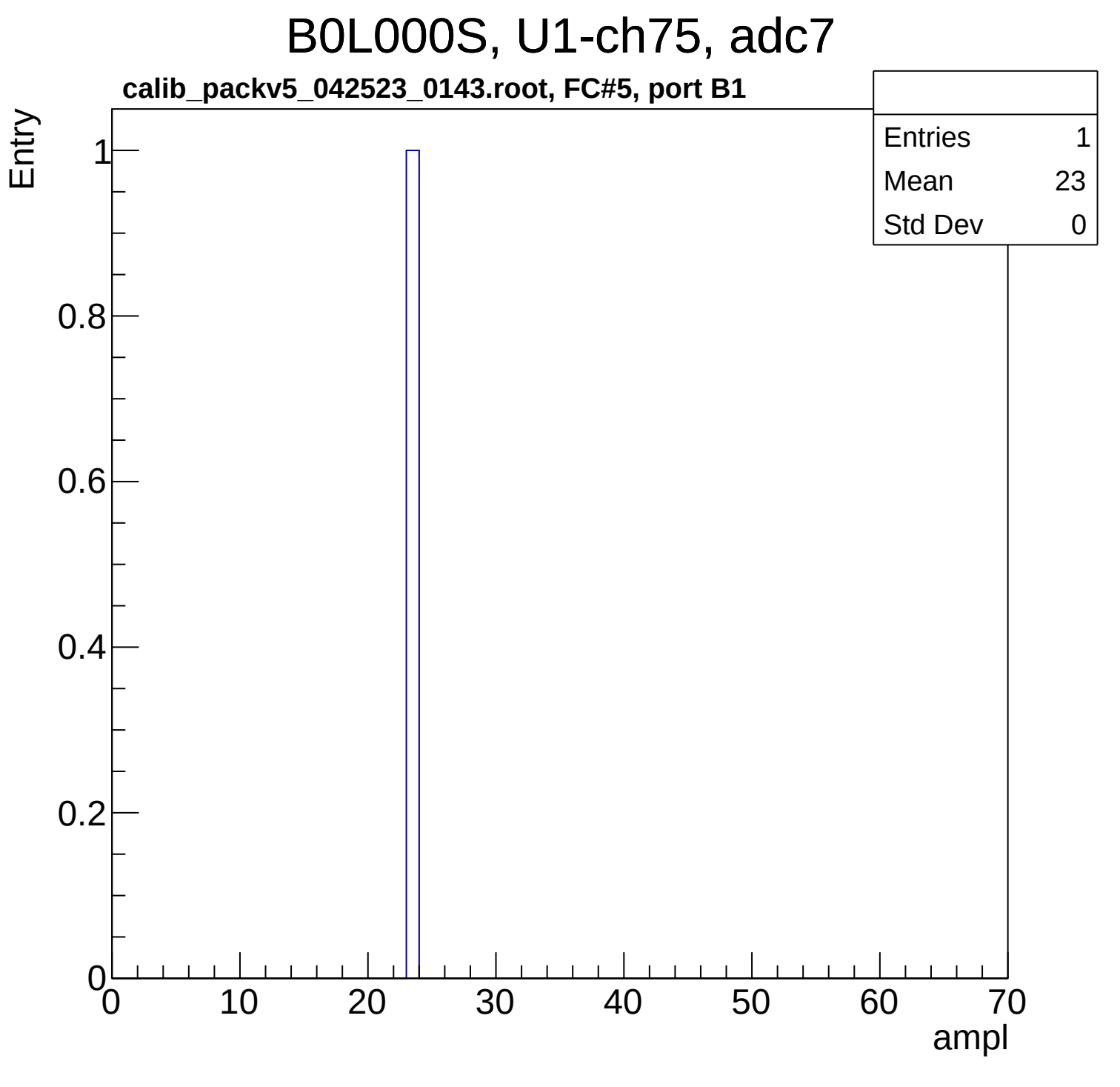

B0L000S, U1-ch75, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch75, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
0
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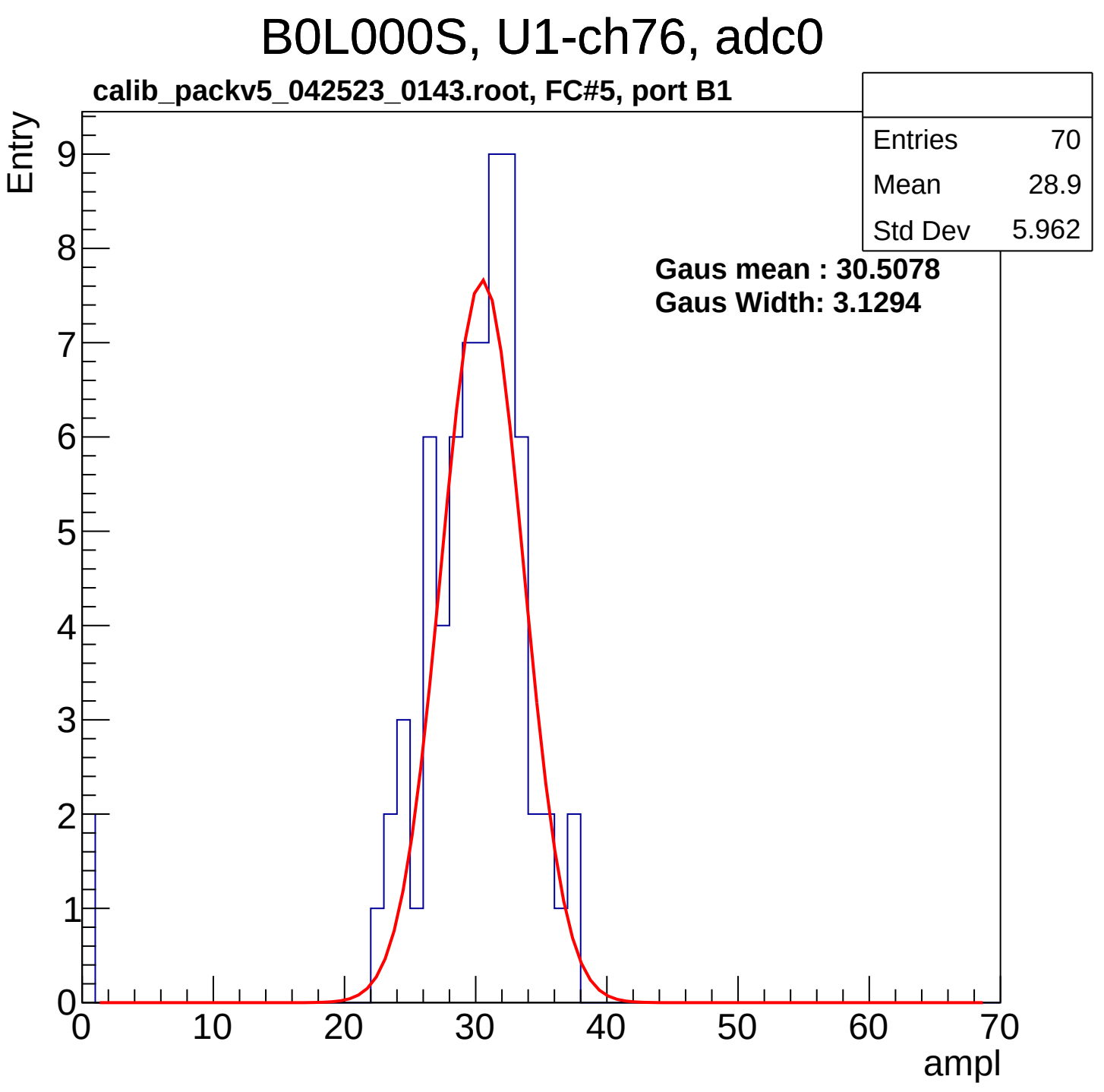
50
60
70
ampl


B0L000S, U1-ch76, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.9
Std Dev 5.962
Gaus mean : 30.5078
Gaus Width: 3.1294
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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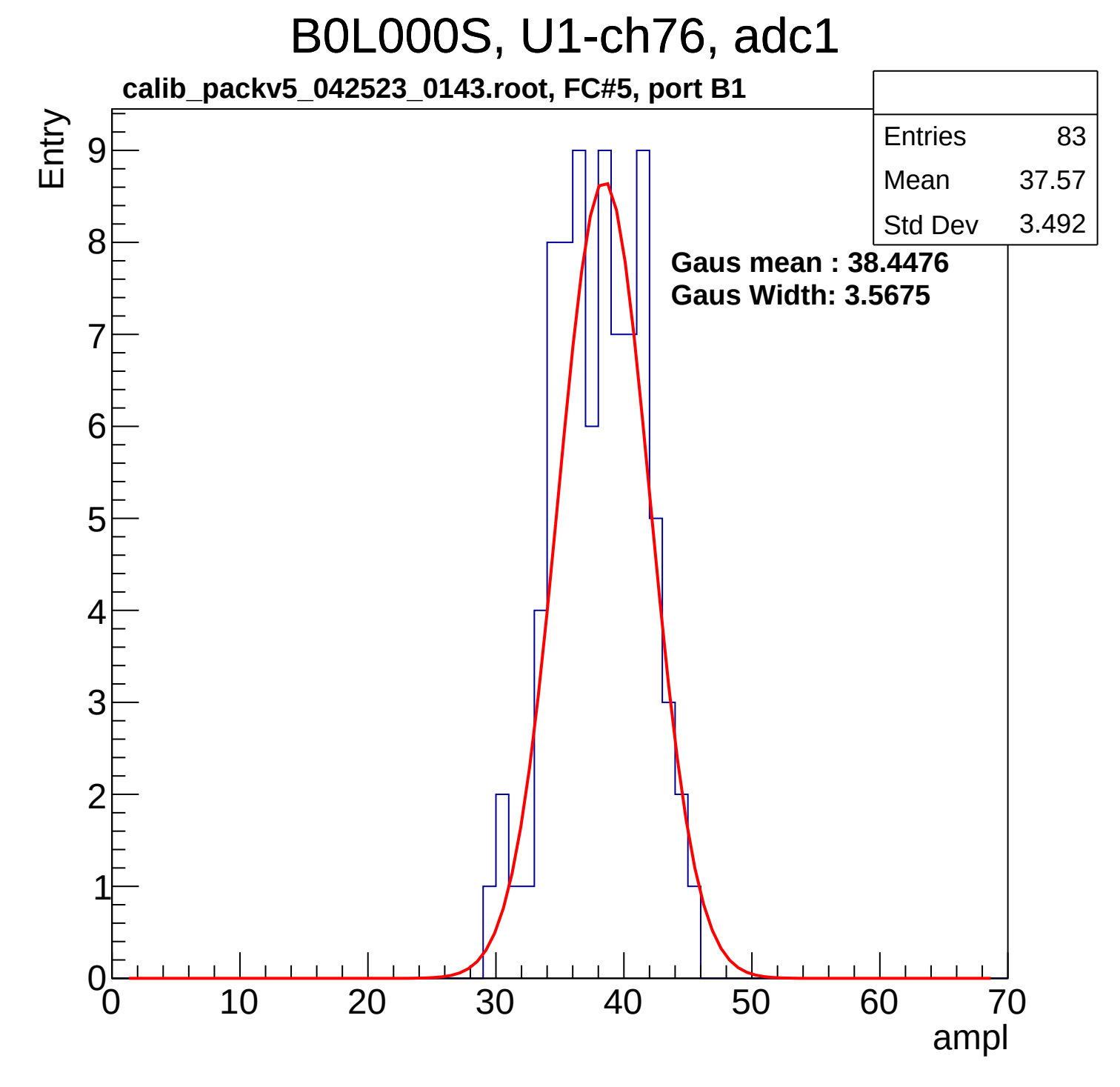
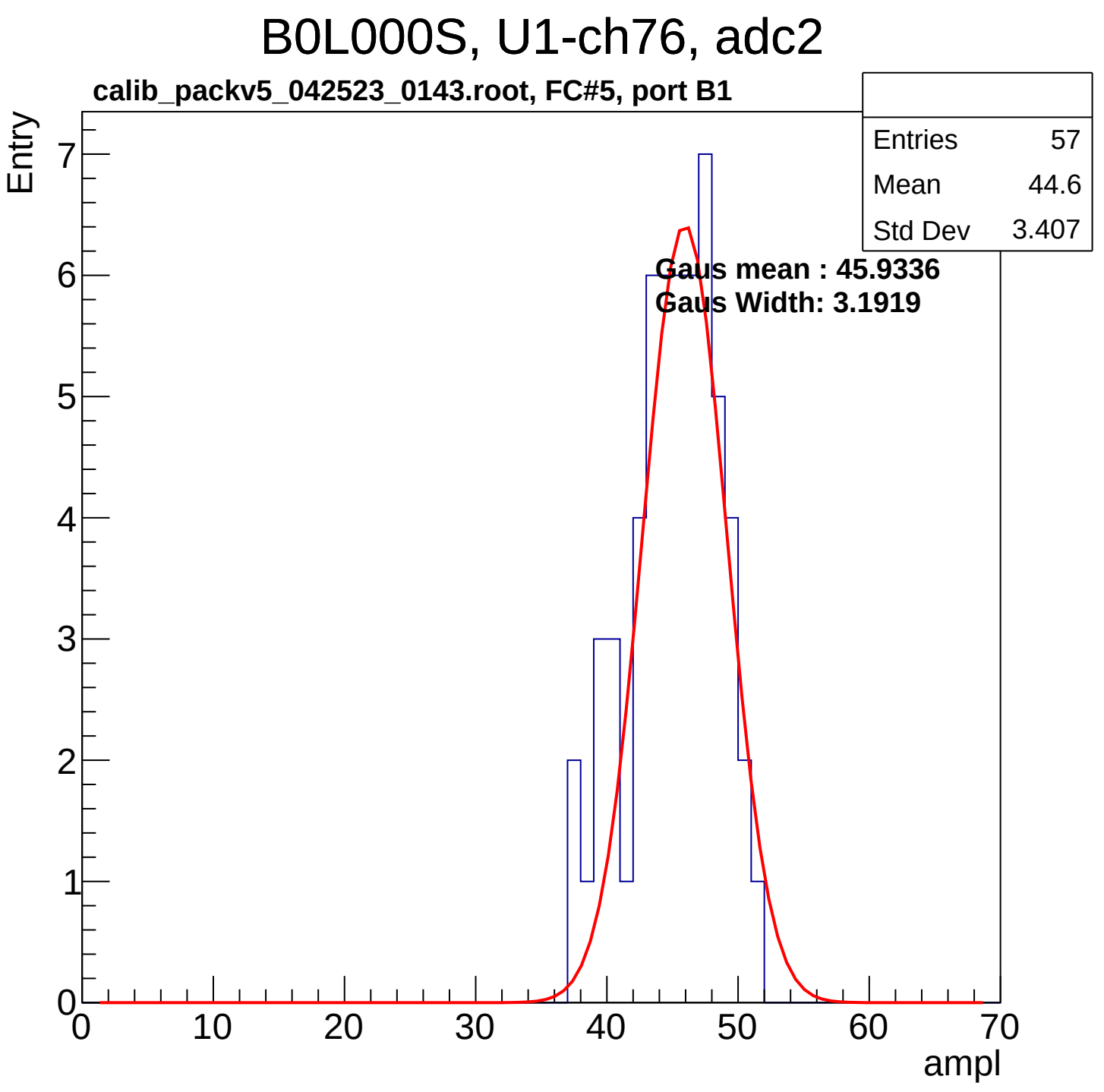

B0L000S, U1-ch76, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 37.57
Std Dev 3.492
Gaus mean : 38.4476
Gaus Width: 3.5675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch76, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 44.6
Std Dev 3.407
Gaus mean : 45.9336
Gaus Width: 3.1919
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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50
60
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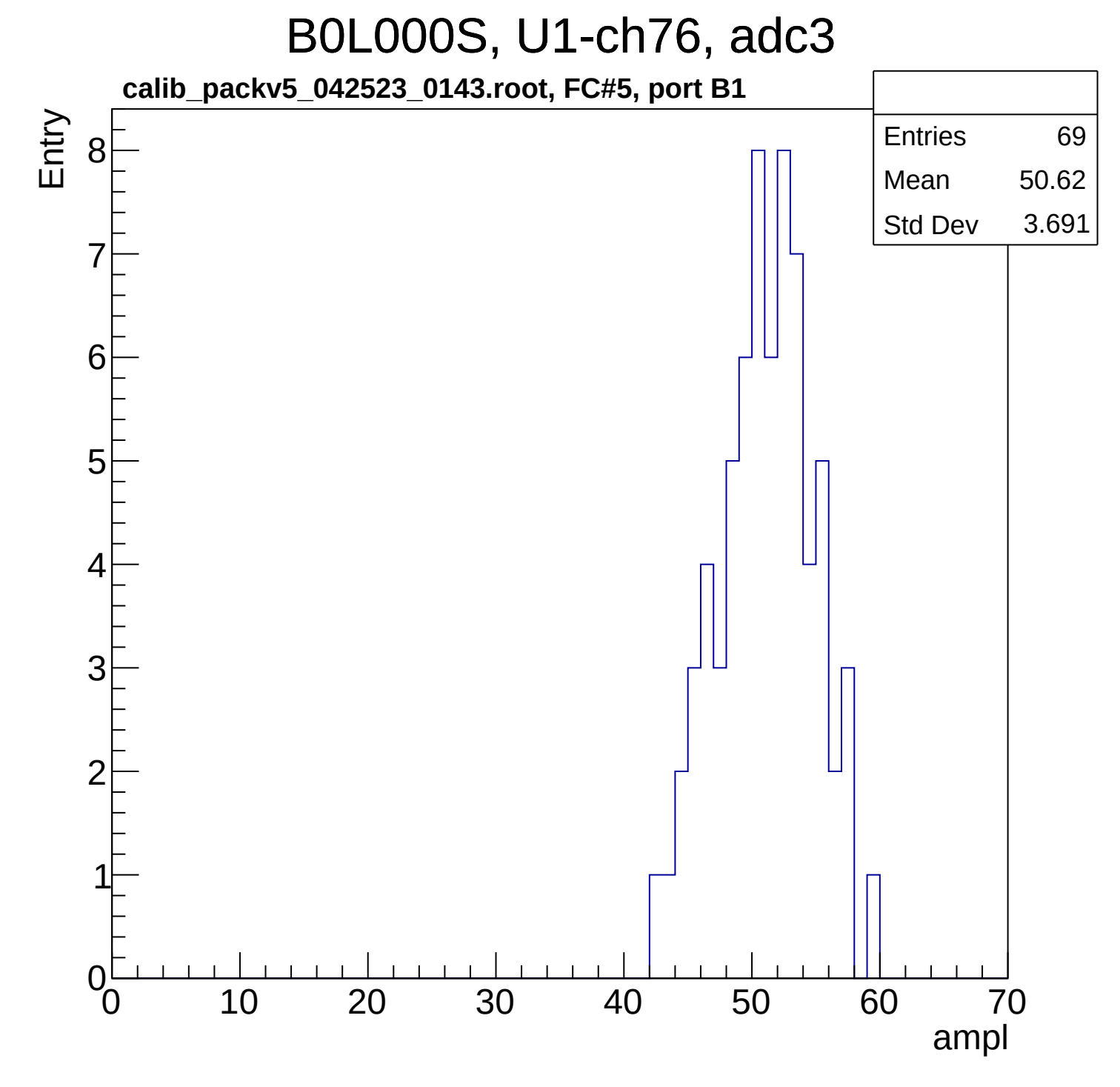
B0L000S, U1-ch76, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.62
Std Dev 3.691
Entry
ampl
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4
5
6
7
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0
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20
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40
50
60
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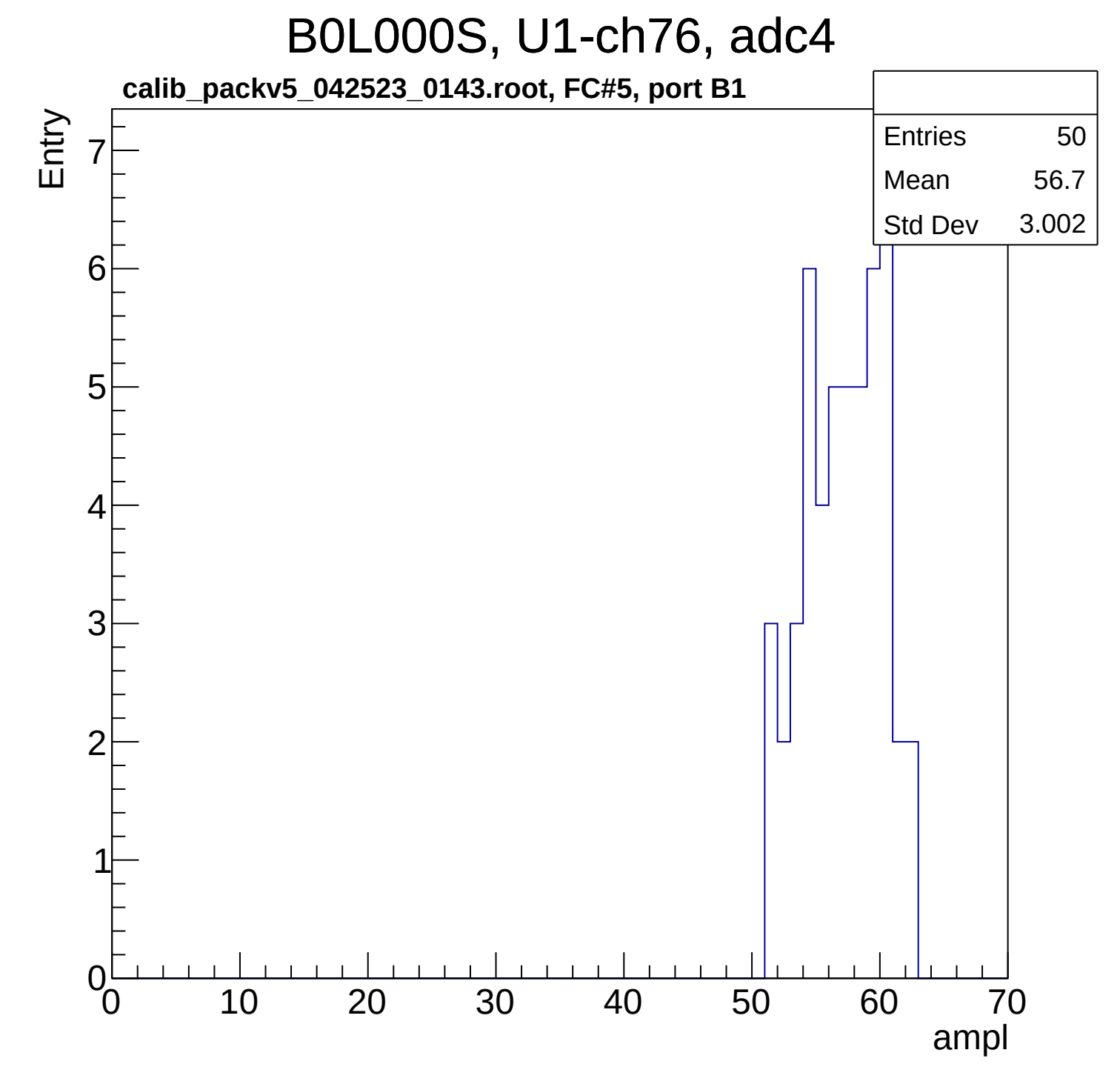
B0L000S, U1-ch76, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 56.7
Std Dev 3.002
Entry
ampl
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3
4
5
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7
0
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60
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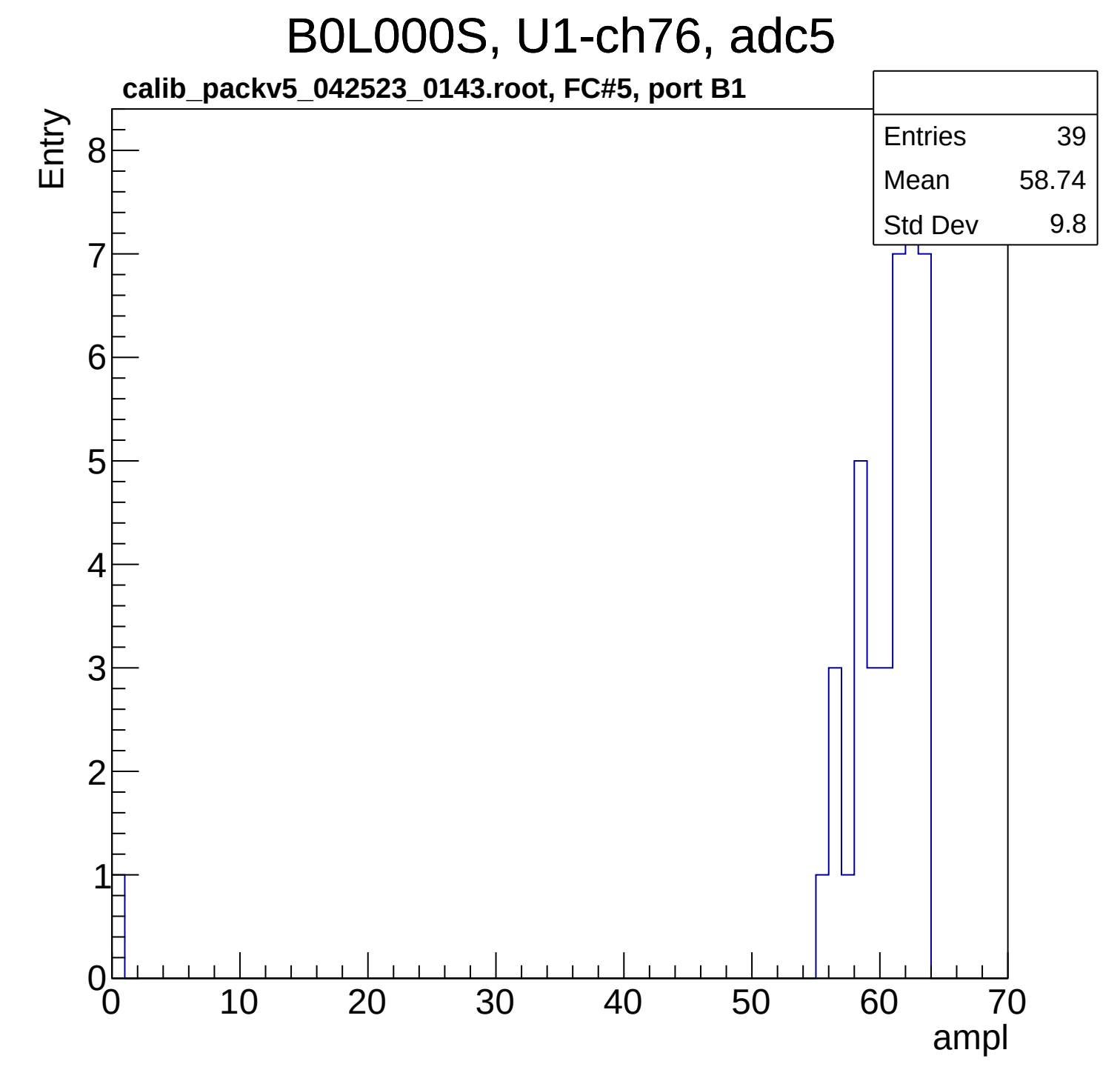
B0L000S, U1-ch76, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.74
Std Dev 9.8
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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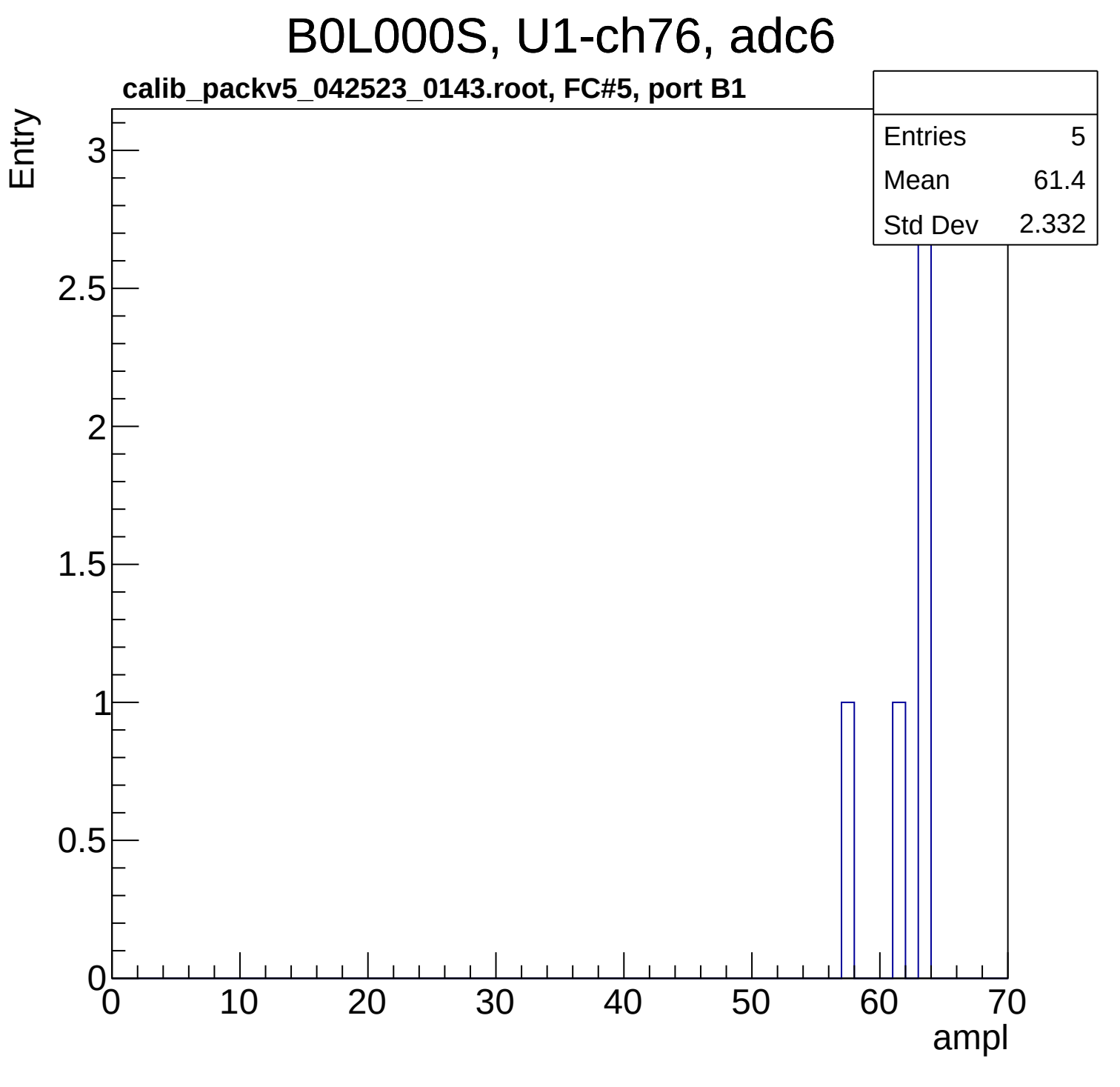
B0L000S, U1-ch76, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.4
Std Dev 2.332
Entry
ampl
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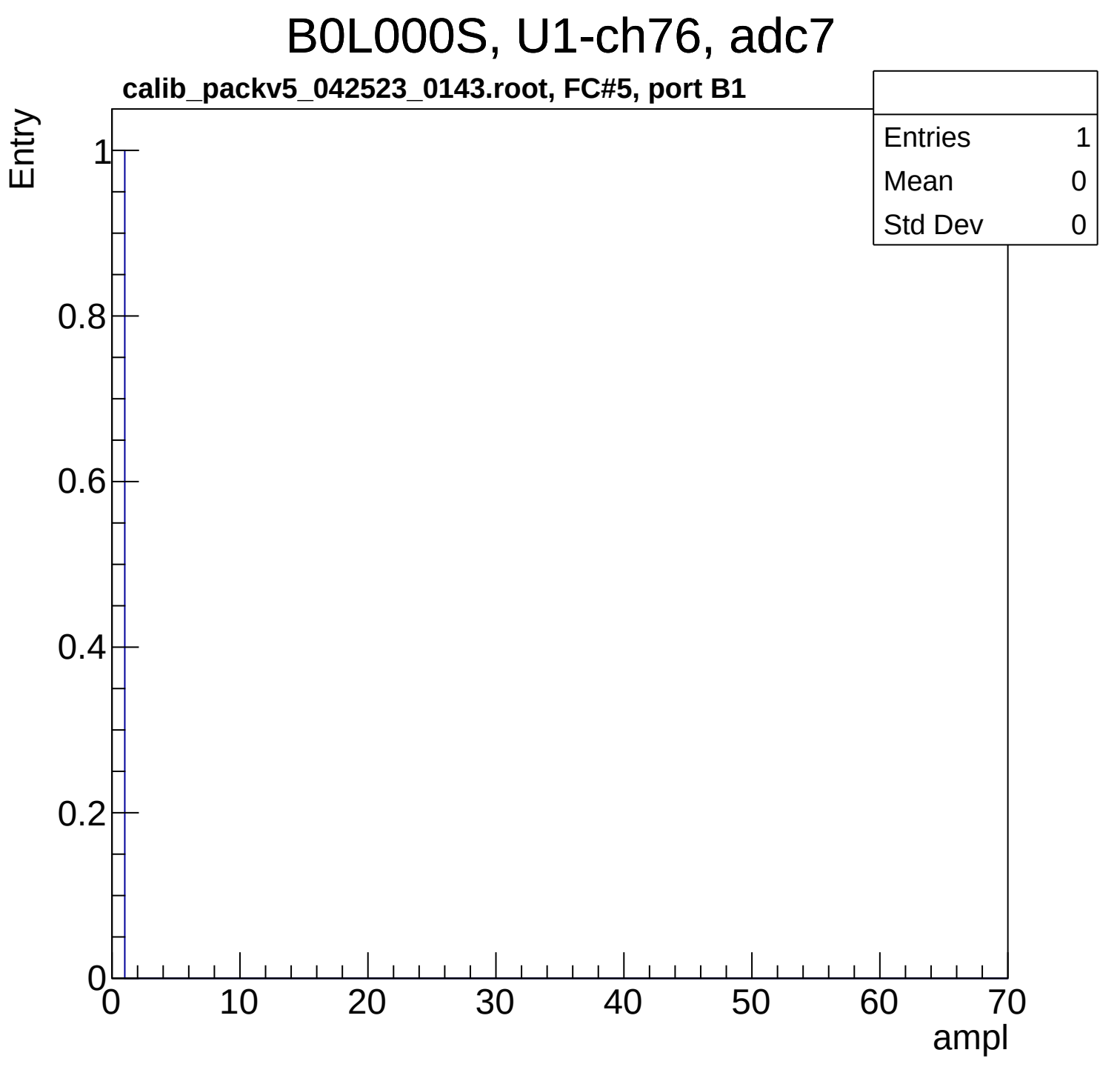
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch76, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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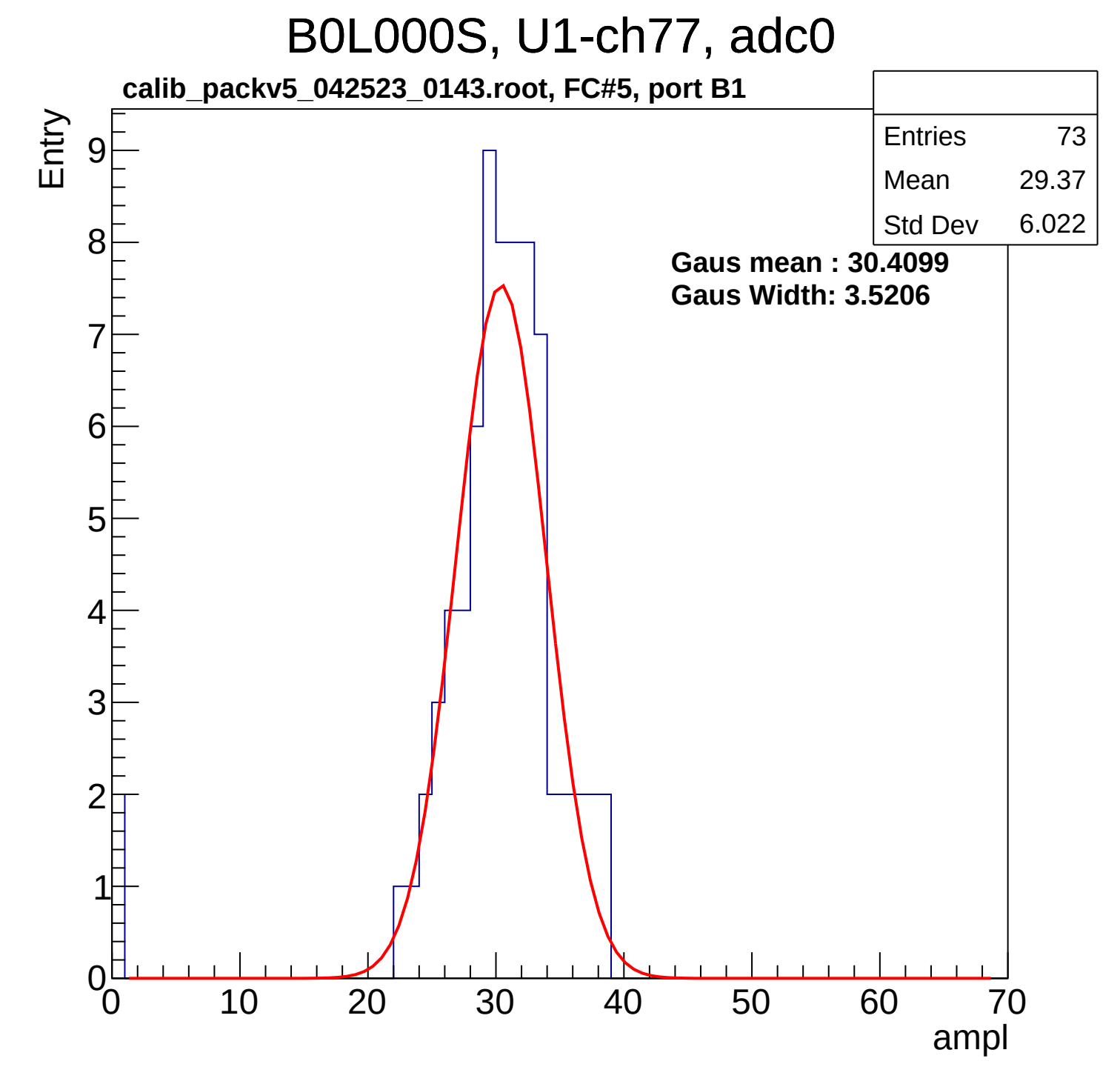
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch77, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.37
Std Dev 6.022
Gaus mean : 30.4099
Gaus Width: 3.5206
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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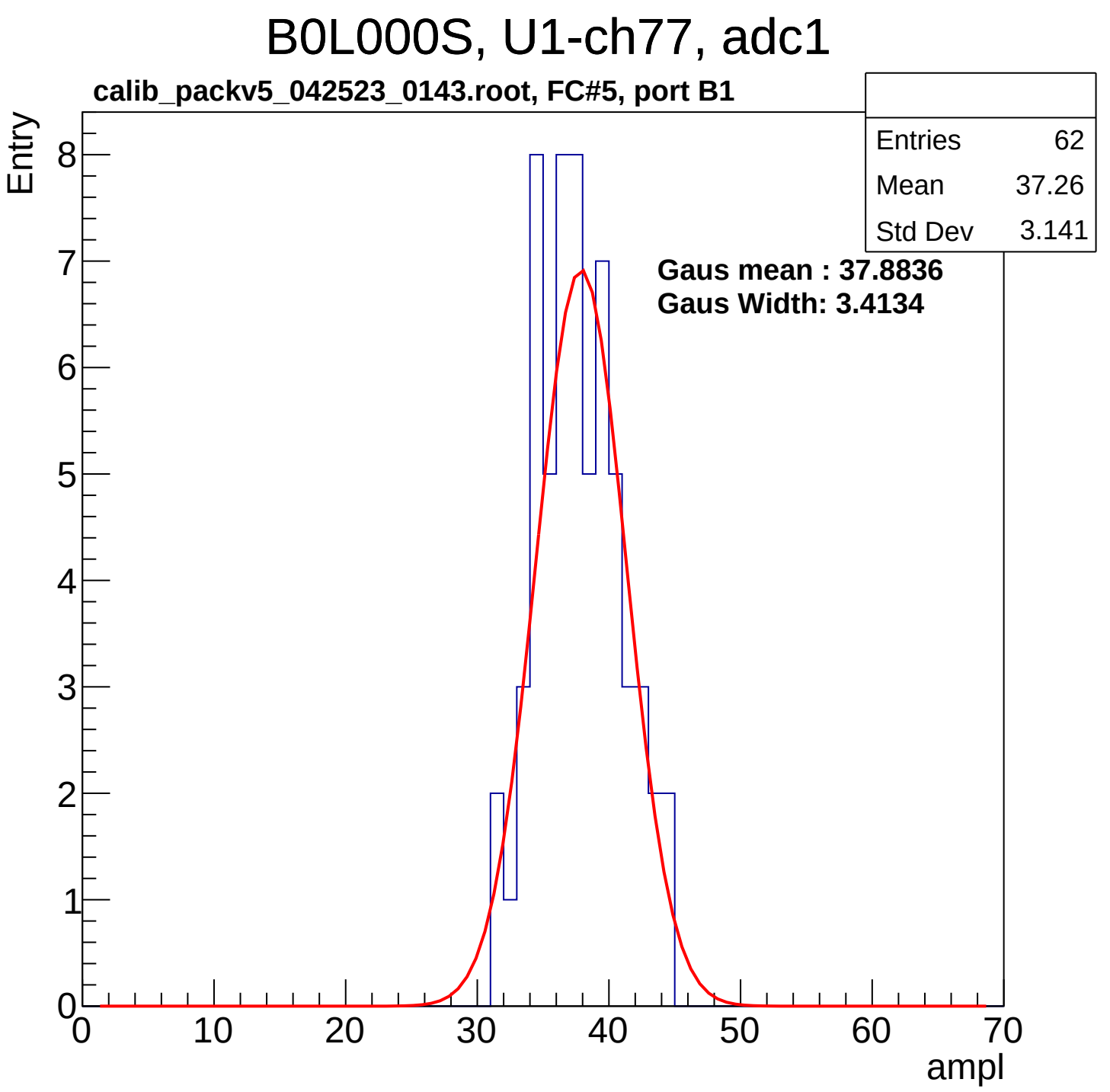
B0L000S, U1-ch77, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37.26
Std Dev 3.141
Gaus mean : 37.8836
Gaus Width: 3.4134
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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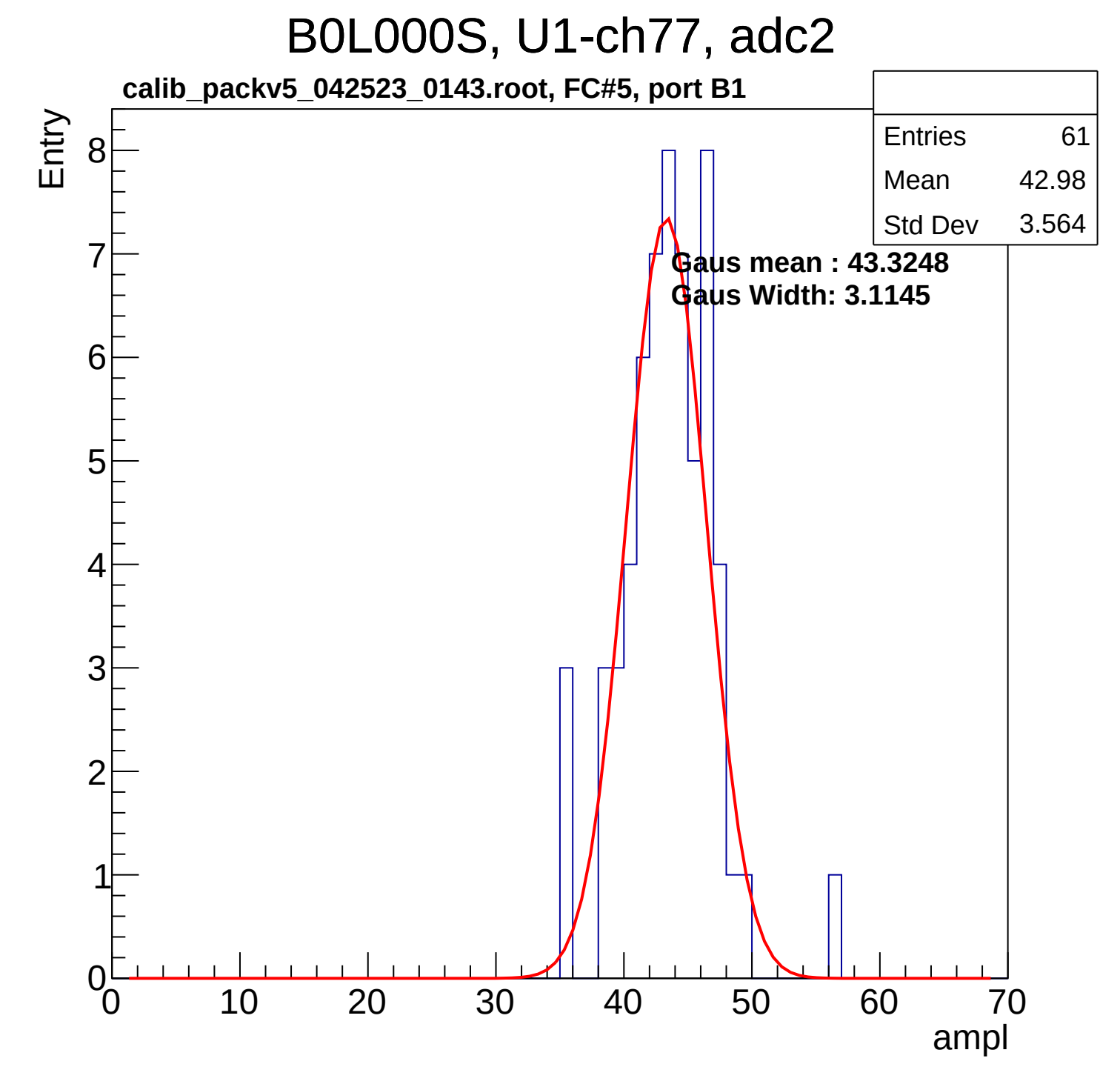
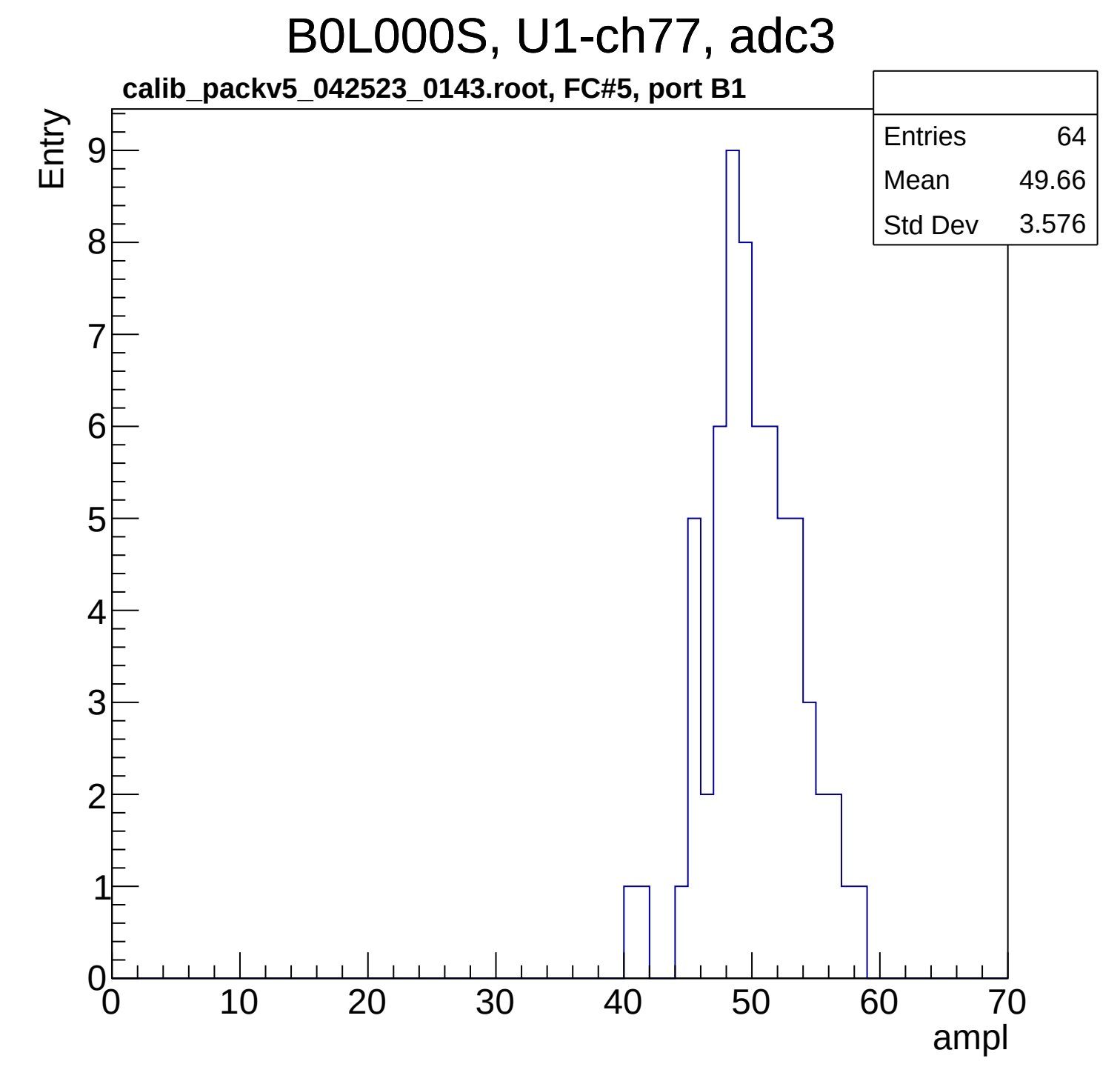

B0L000S, U1-ch77, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.98
Std Dev 3.564
Gaus mean : 43.3248
Gaus Width: 3.1145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch77, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.66
Std Dev 3.576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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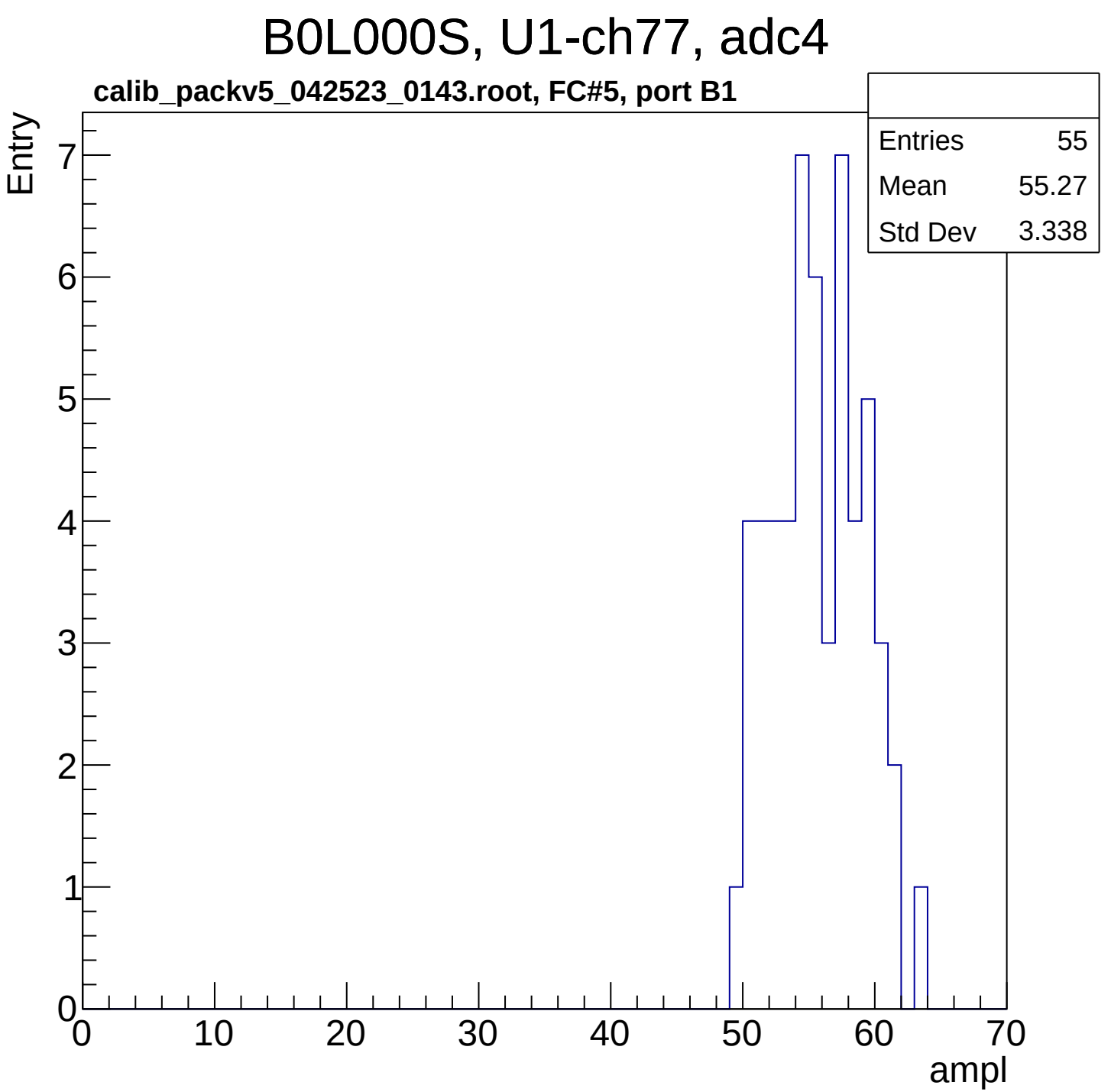
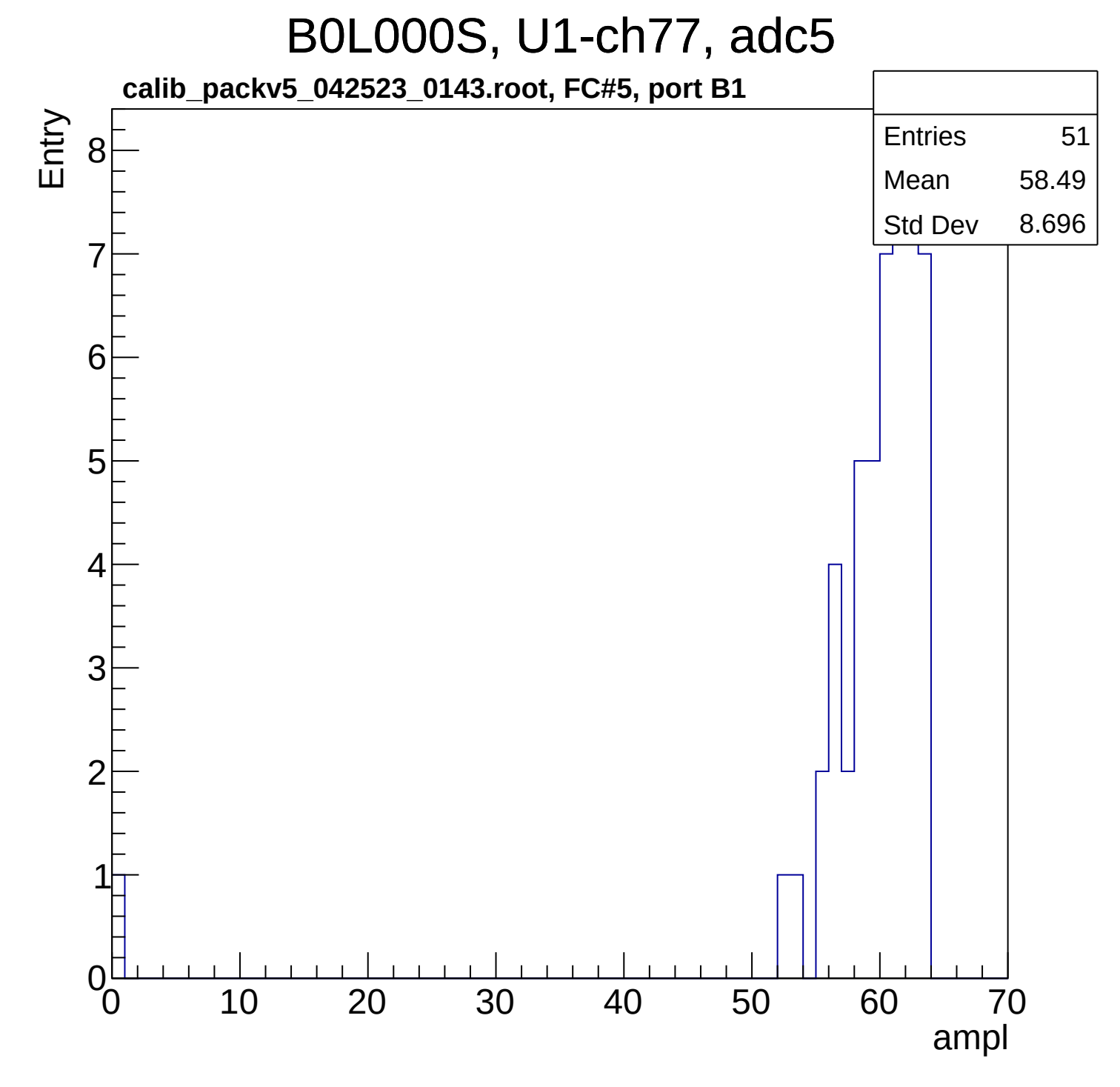

B0L000S, U1-ch77, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.27
Std Dev 3.338
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch77, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.49
Std Dev 8.696
Entry
ampl
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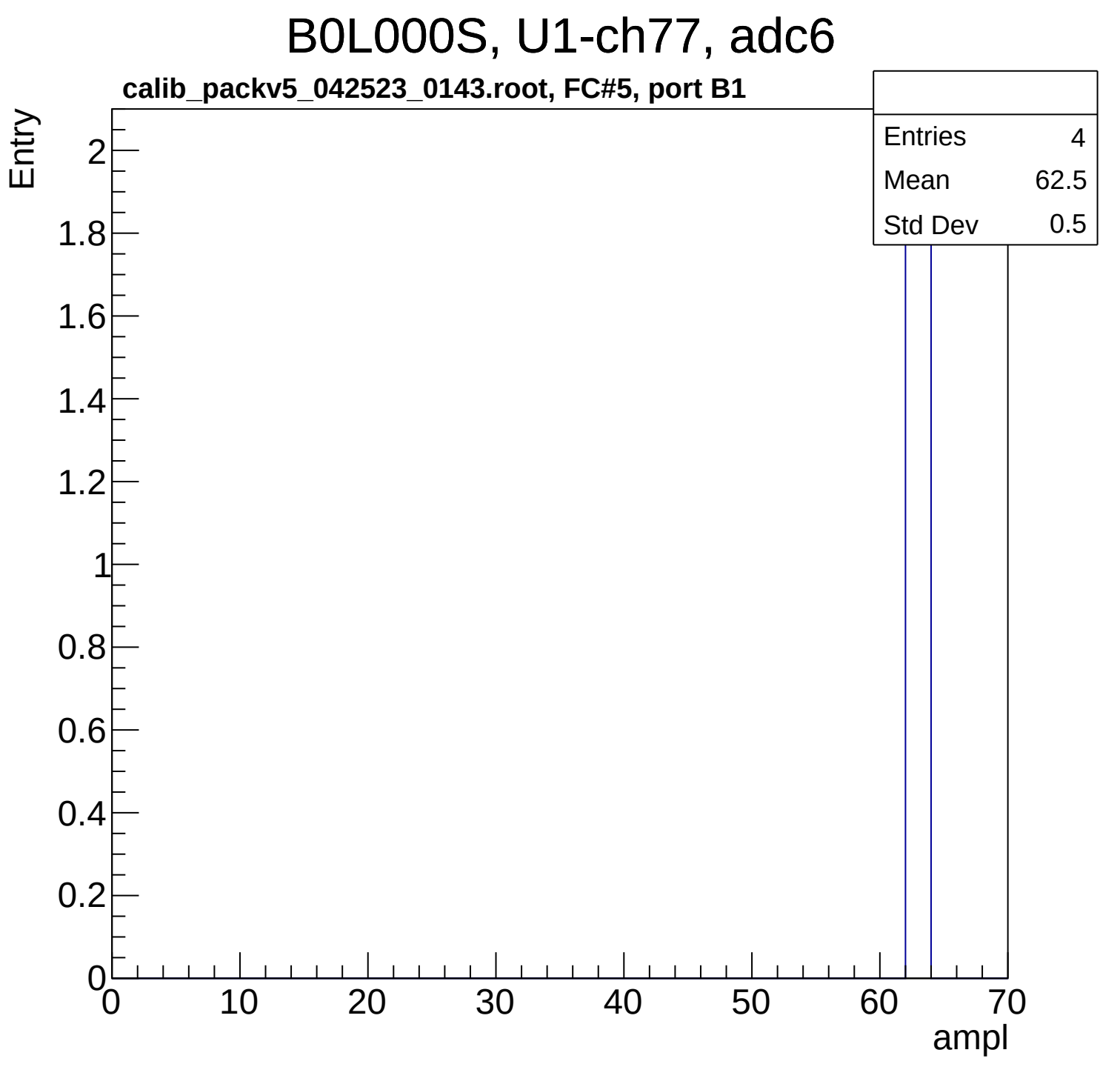
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch77, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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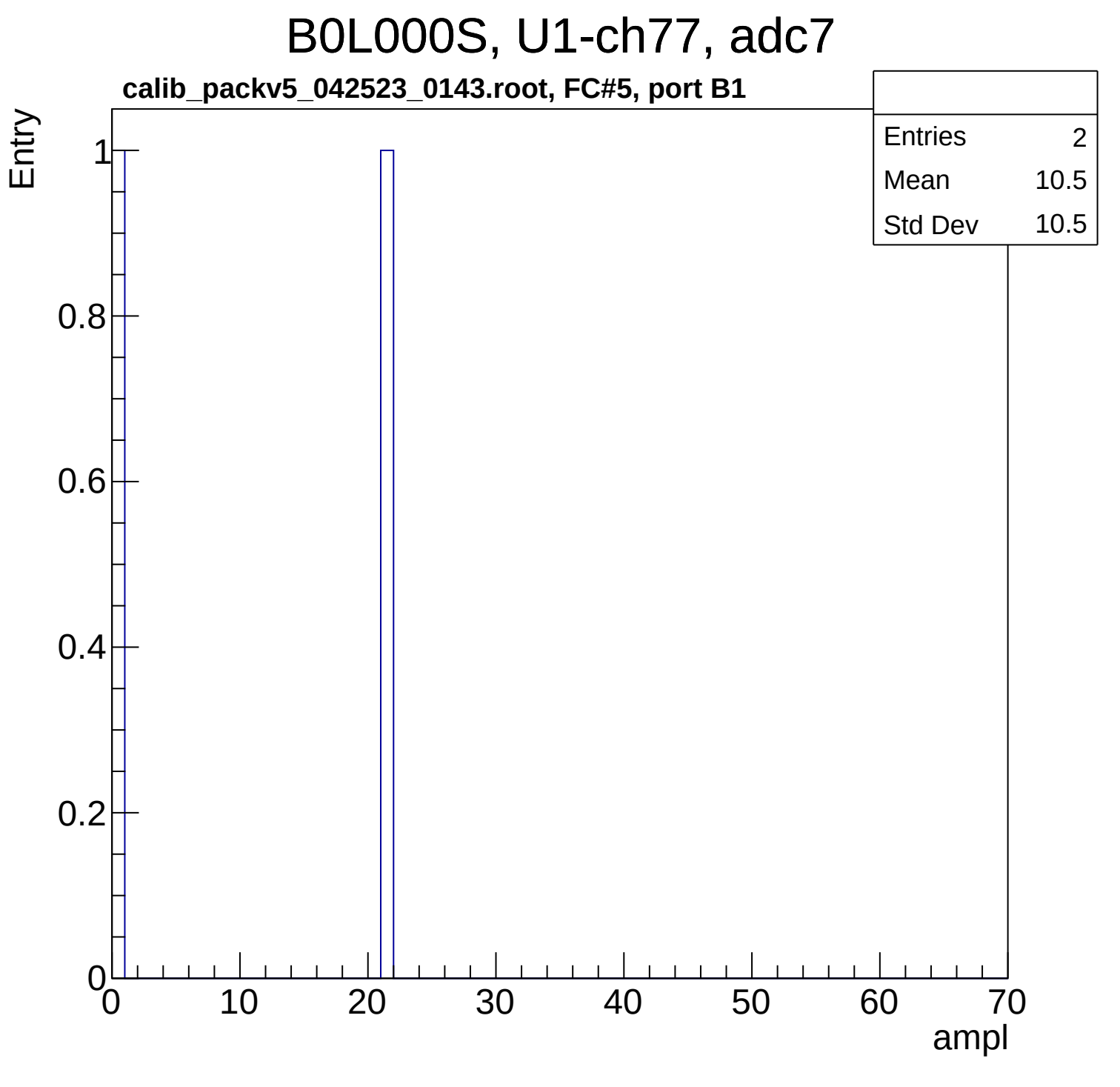
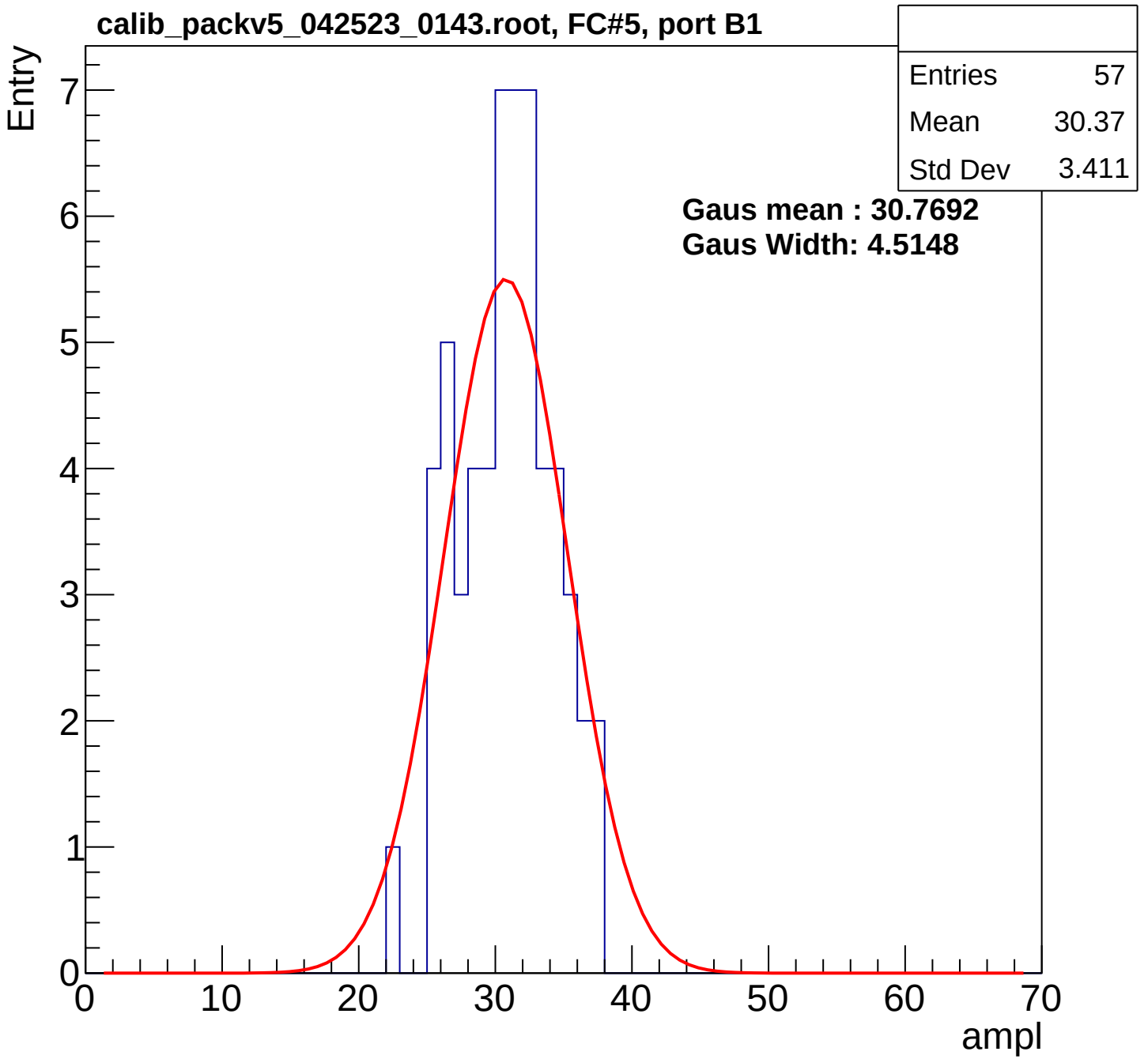
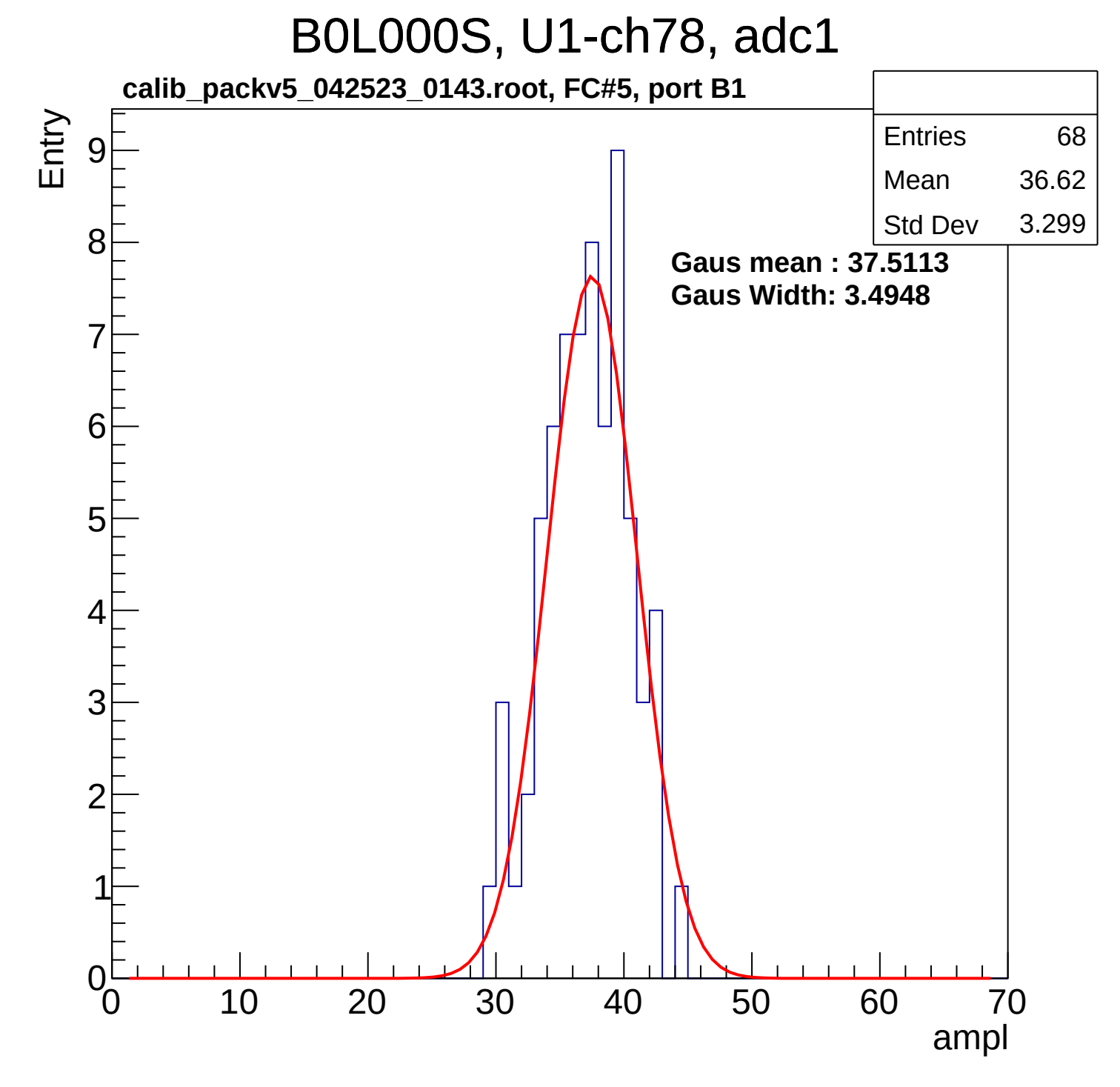
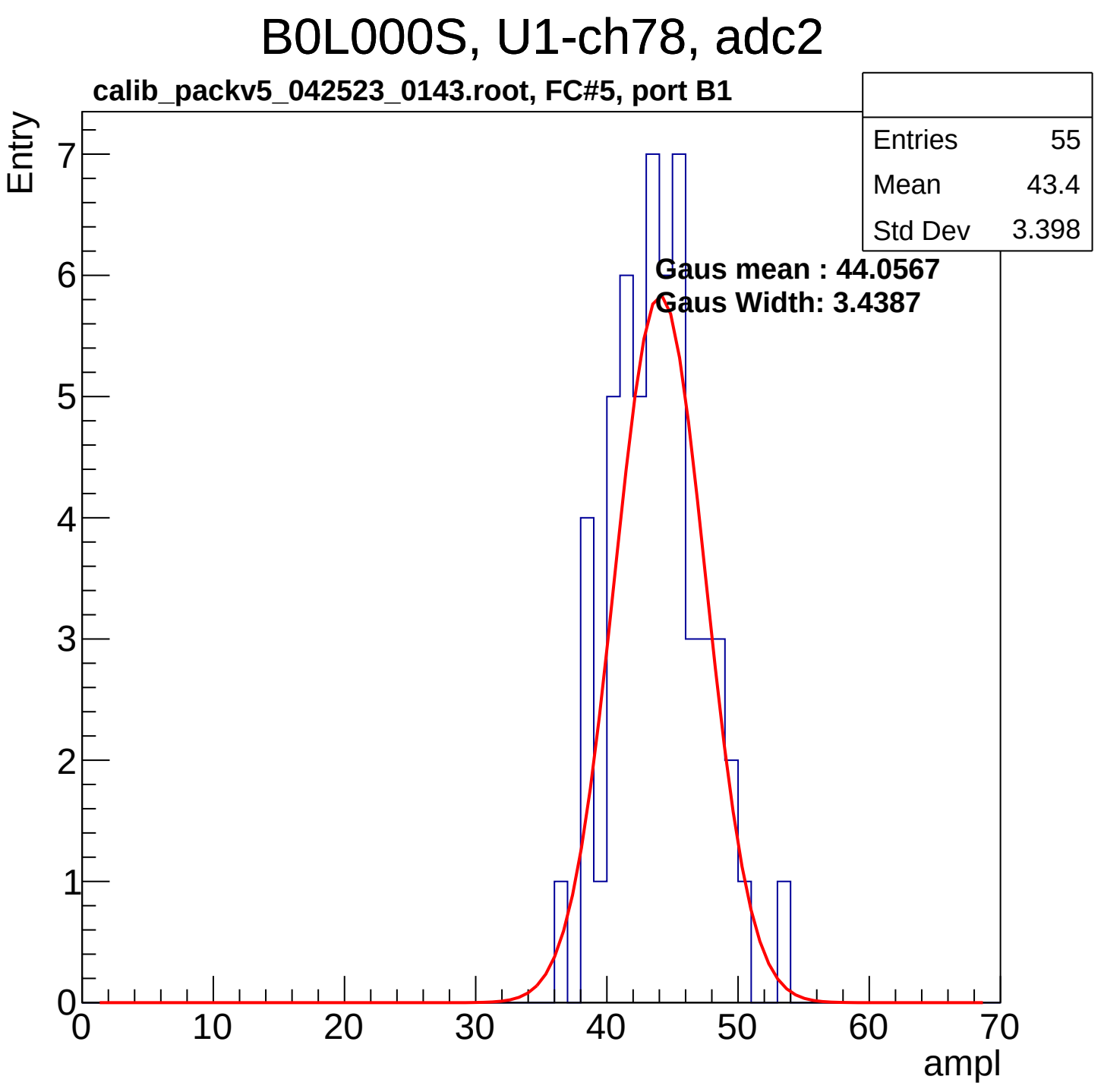
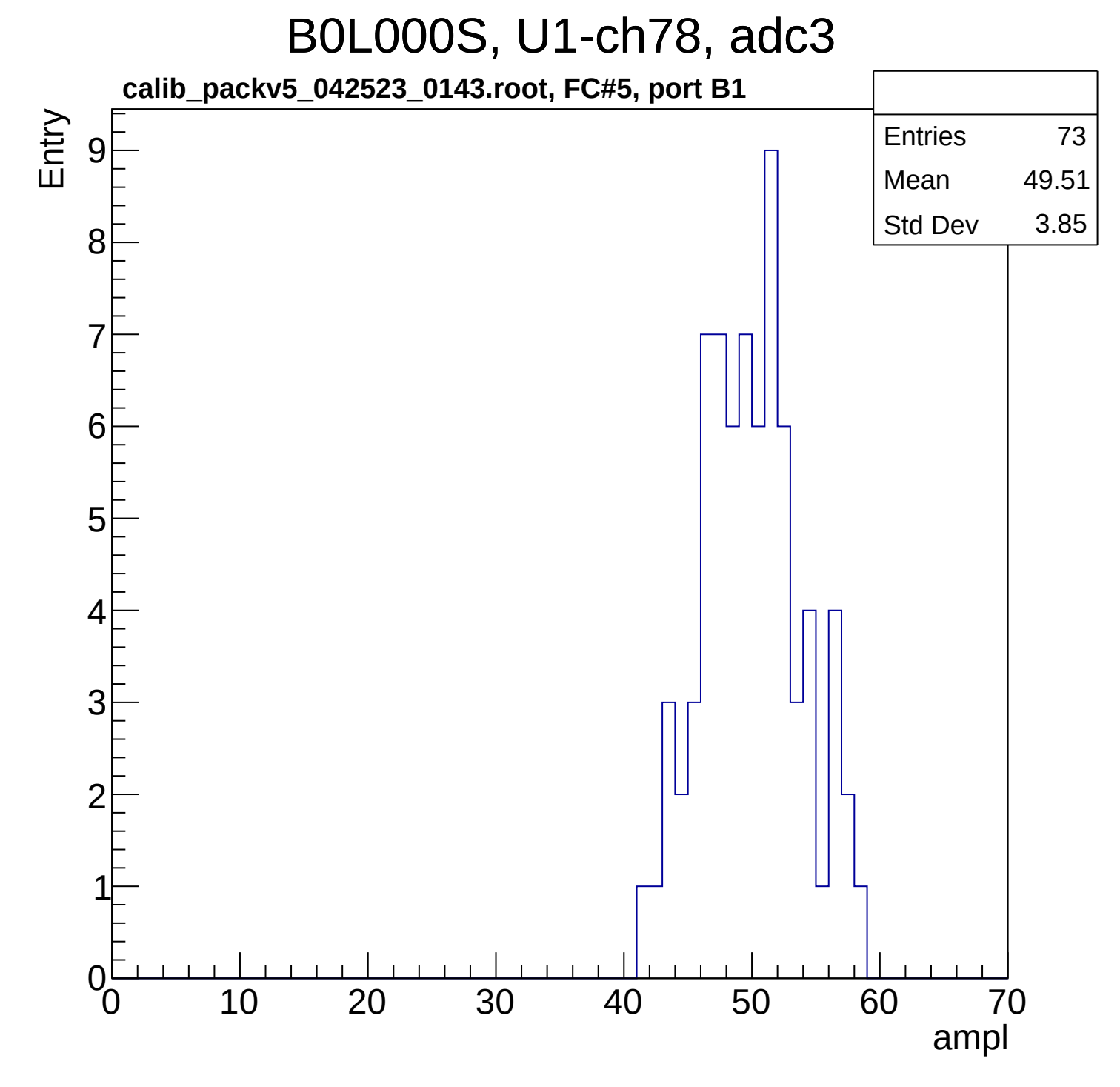
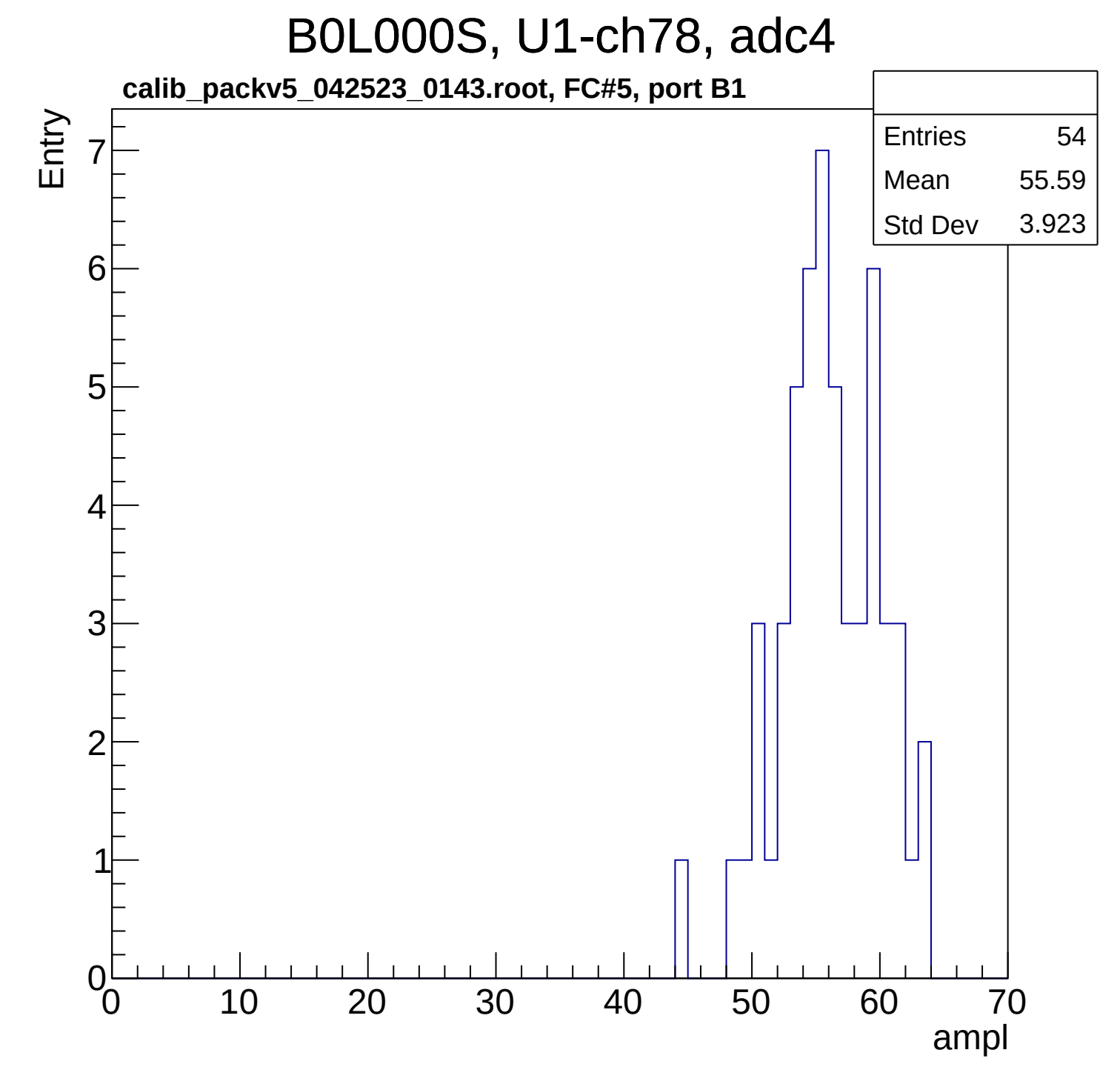

B0L000S, U1-ch77, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

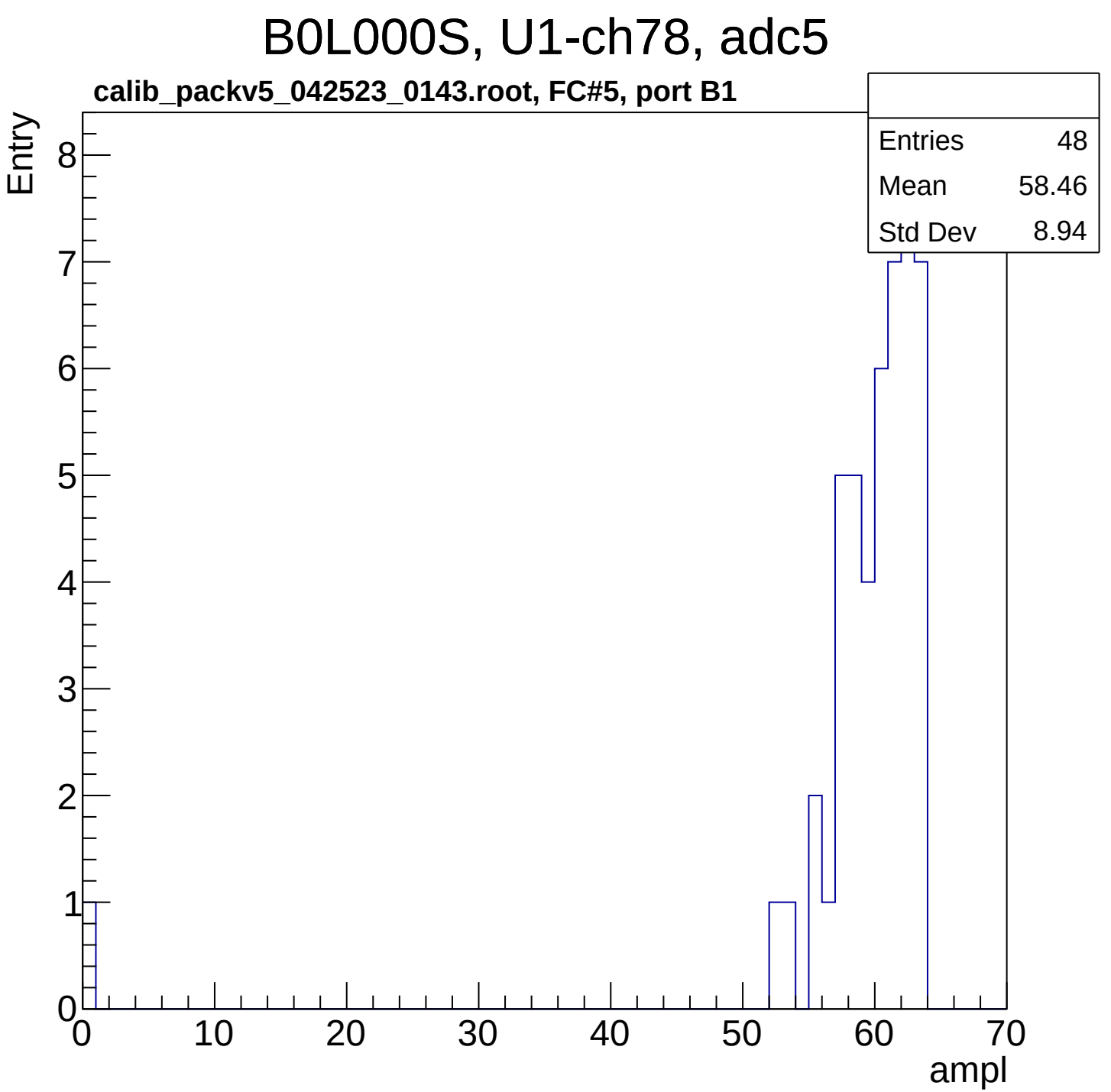

B0L000S, U1-ch78, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 30.37
Std Dev 3.411
Gaus mean : 30.7692
Gaus Width: 4.5148
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch78, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.62
Std Dev 3.299
Gaus mean : 37.5113
Gaus Width: 3.4948
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch78, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 43.4
Std Dev 3.398
Gaus mean : 44.0567
Gaus Width: 3.4387
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch78, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.51
Std Dev 3.85
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch78, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.59
Std Dev 3.923
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch78, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 58.46
Std Dev 8.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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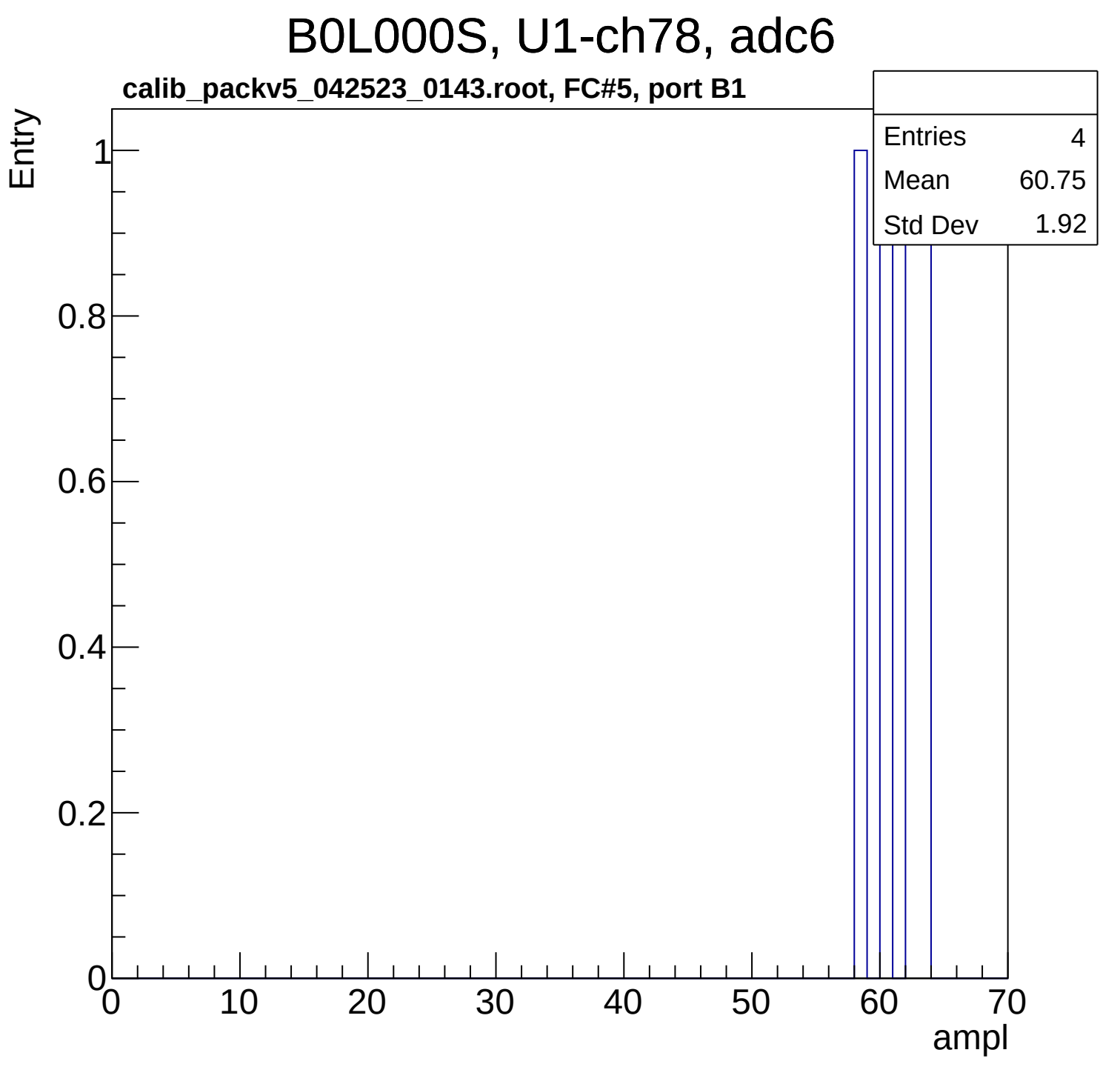
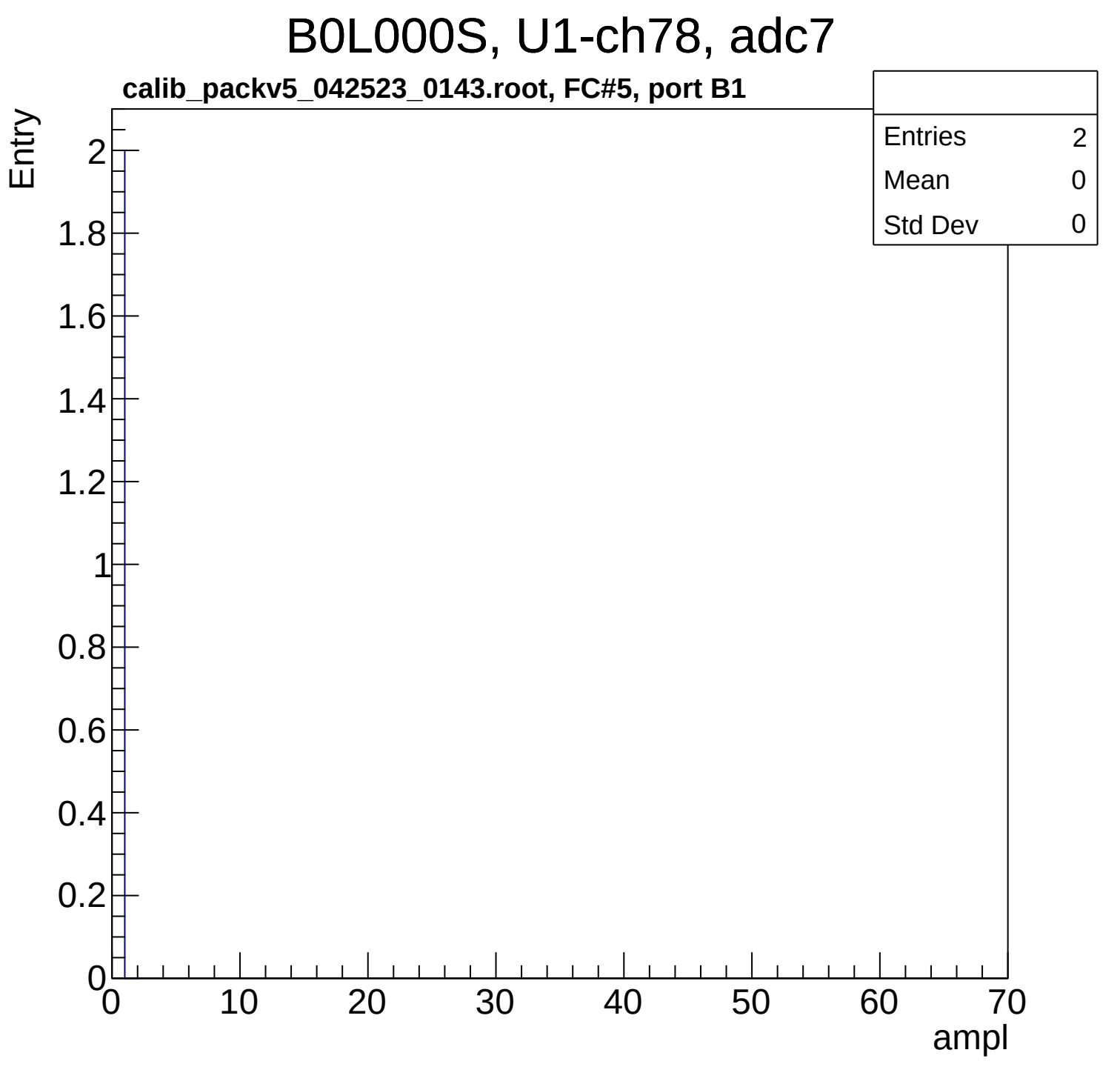
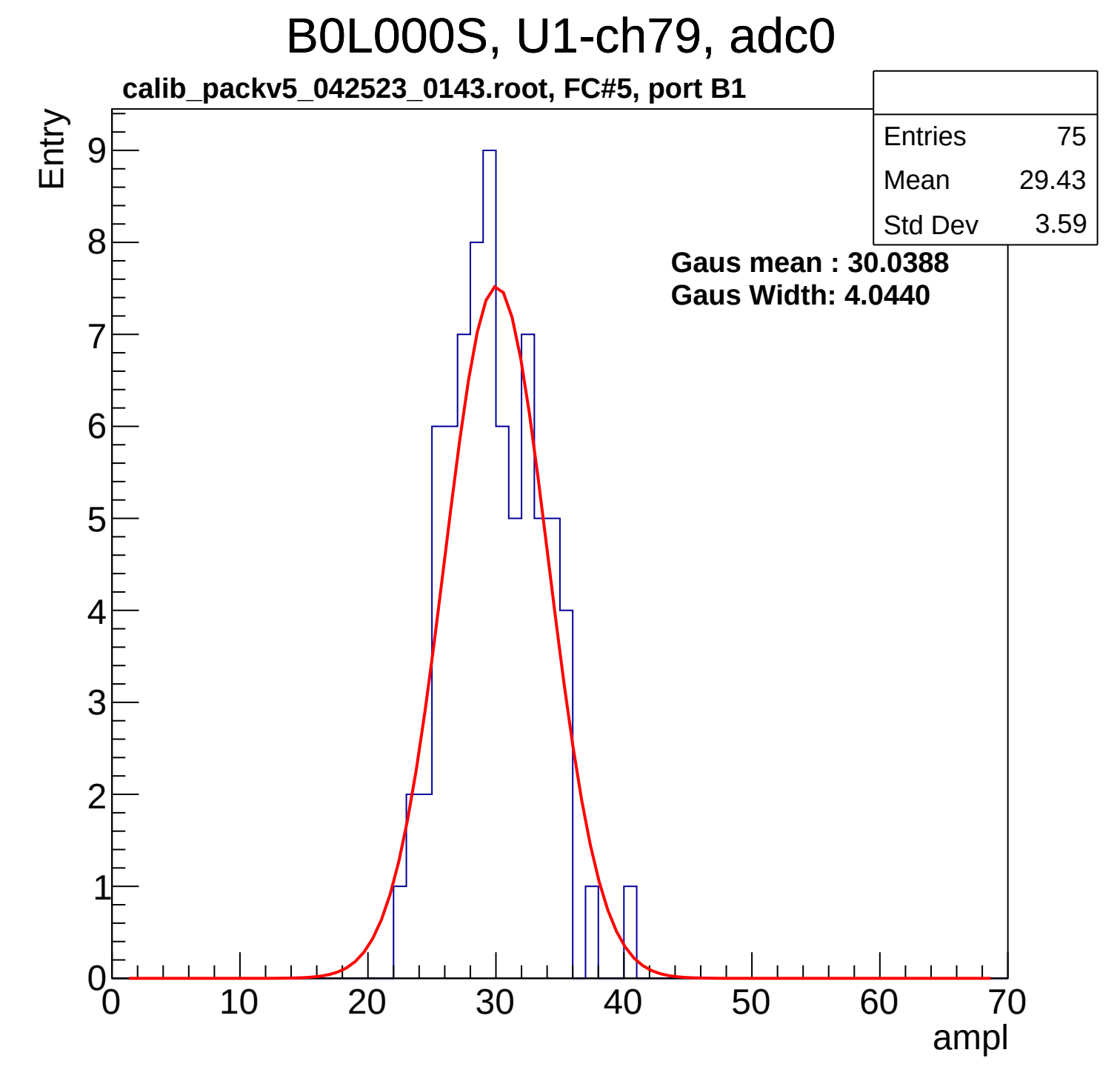
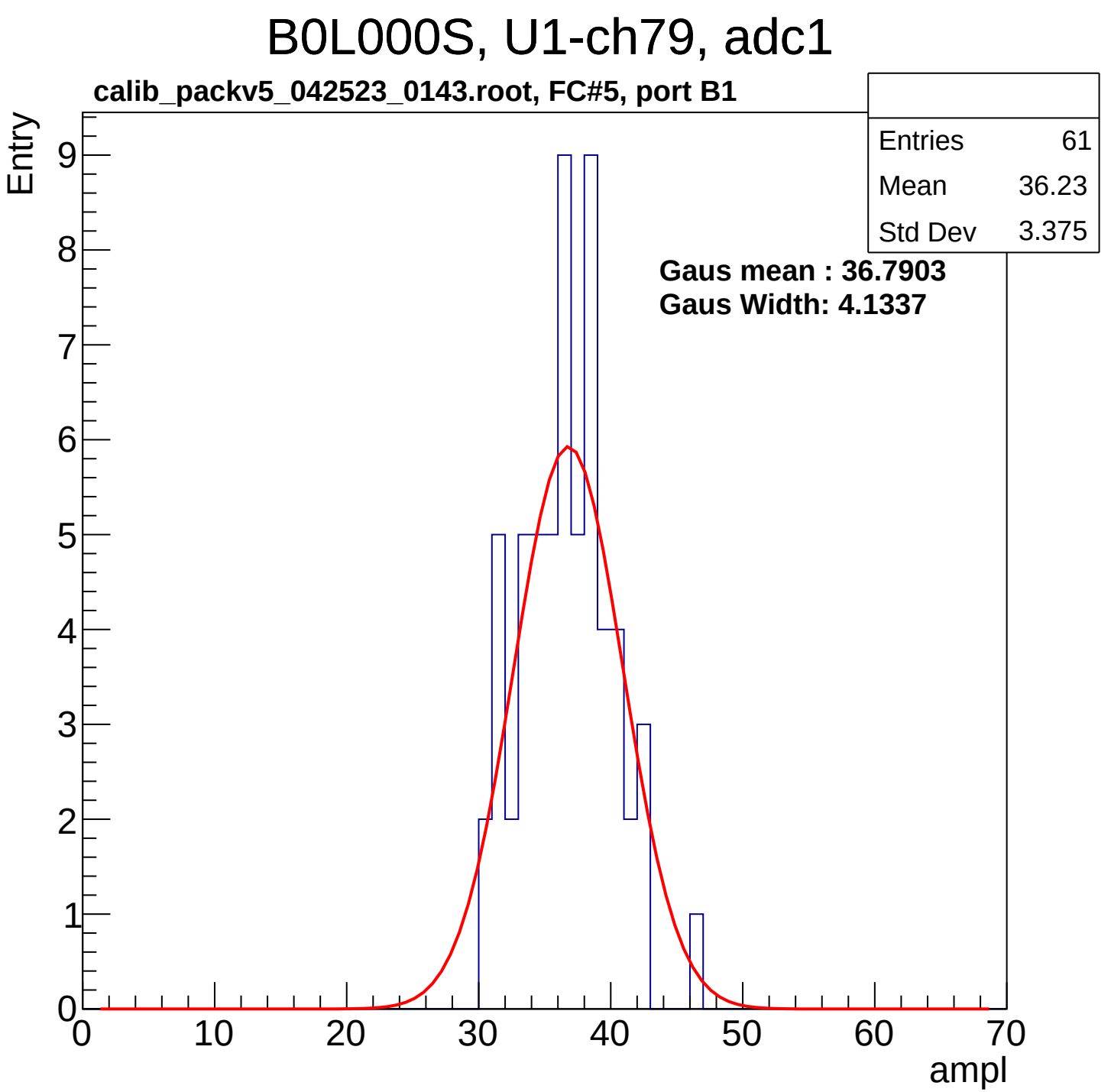
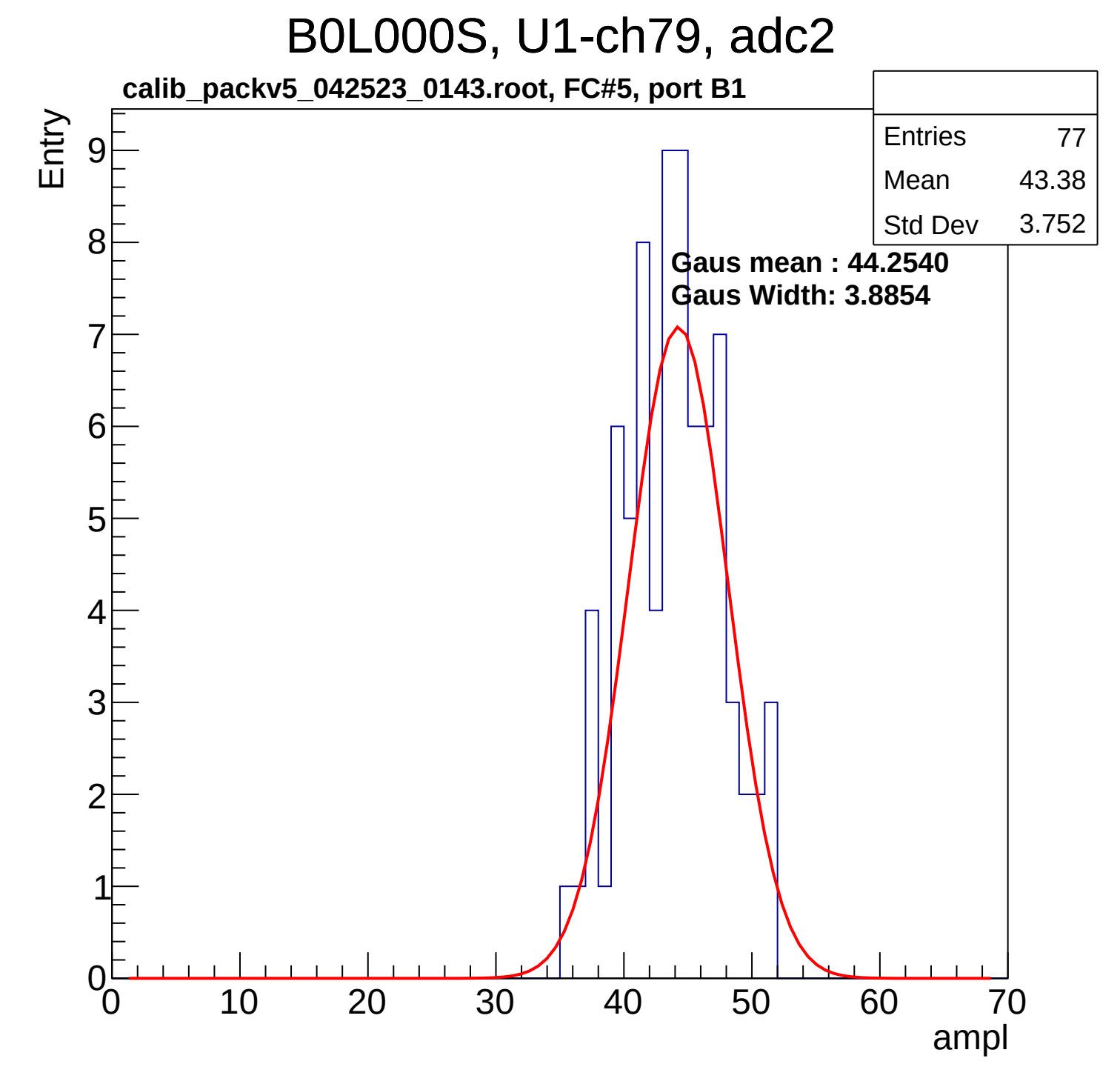
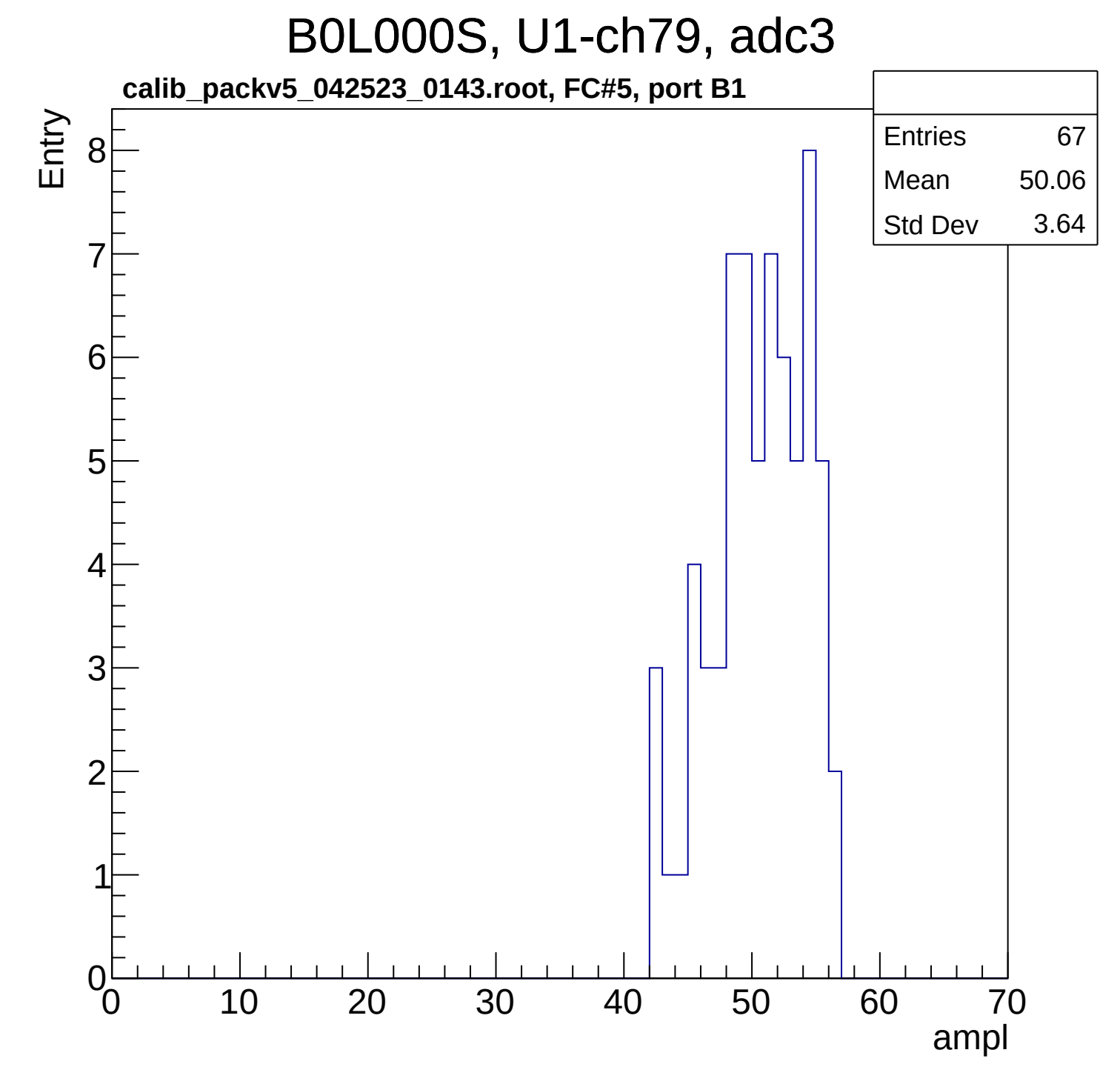

B0L000S, U1-ch78, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.75
Std Dev 1.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U1-ch78, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch79, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.43
Std Dev 3.59
Gaus mean : 30.0388
Gaus Width: 4.0440
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch79, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.23
Std Dev 3.375
Gaus mean : 36.7903
Gaus Width: 4.1337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch79, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 43.38
Std Dev 3.752
Gaus mean : 44.2540
Gaus Width: 3.8854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch79, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.06
Std Dev 3.64
Entry
ampl
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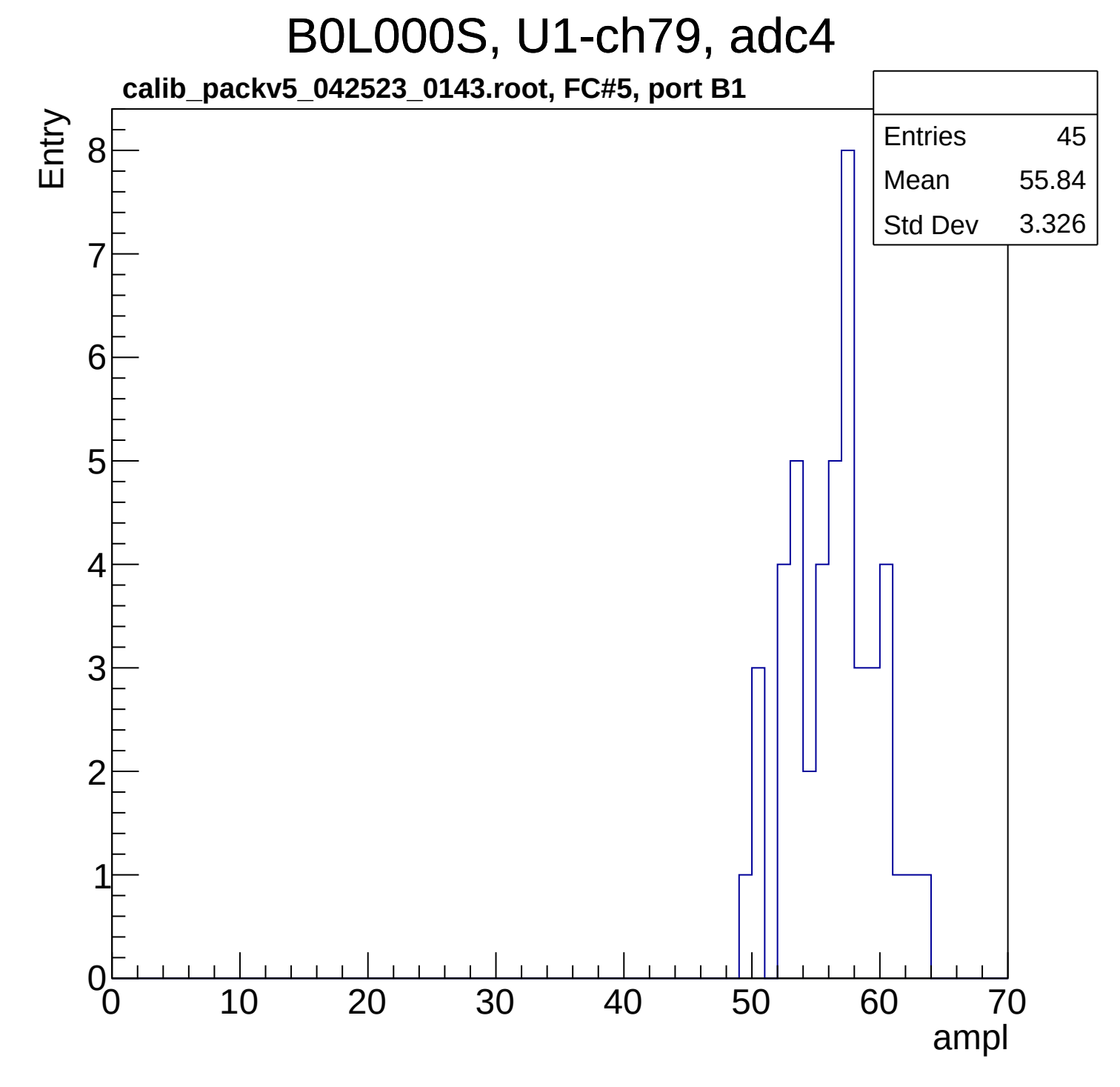
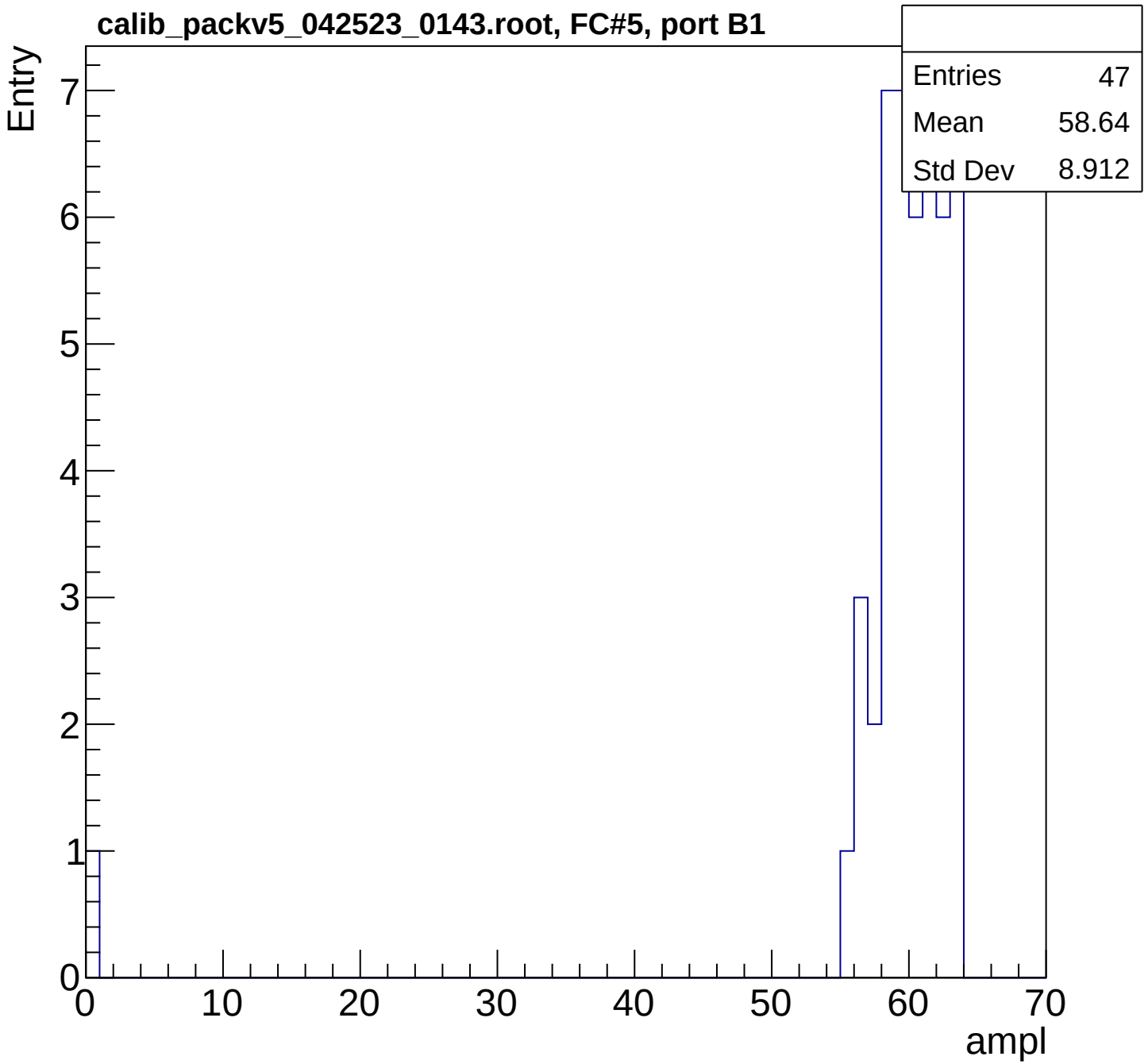
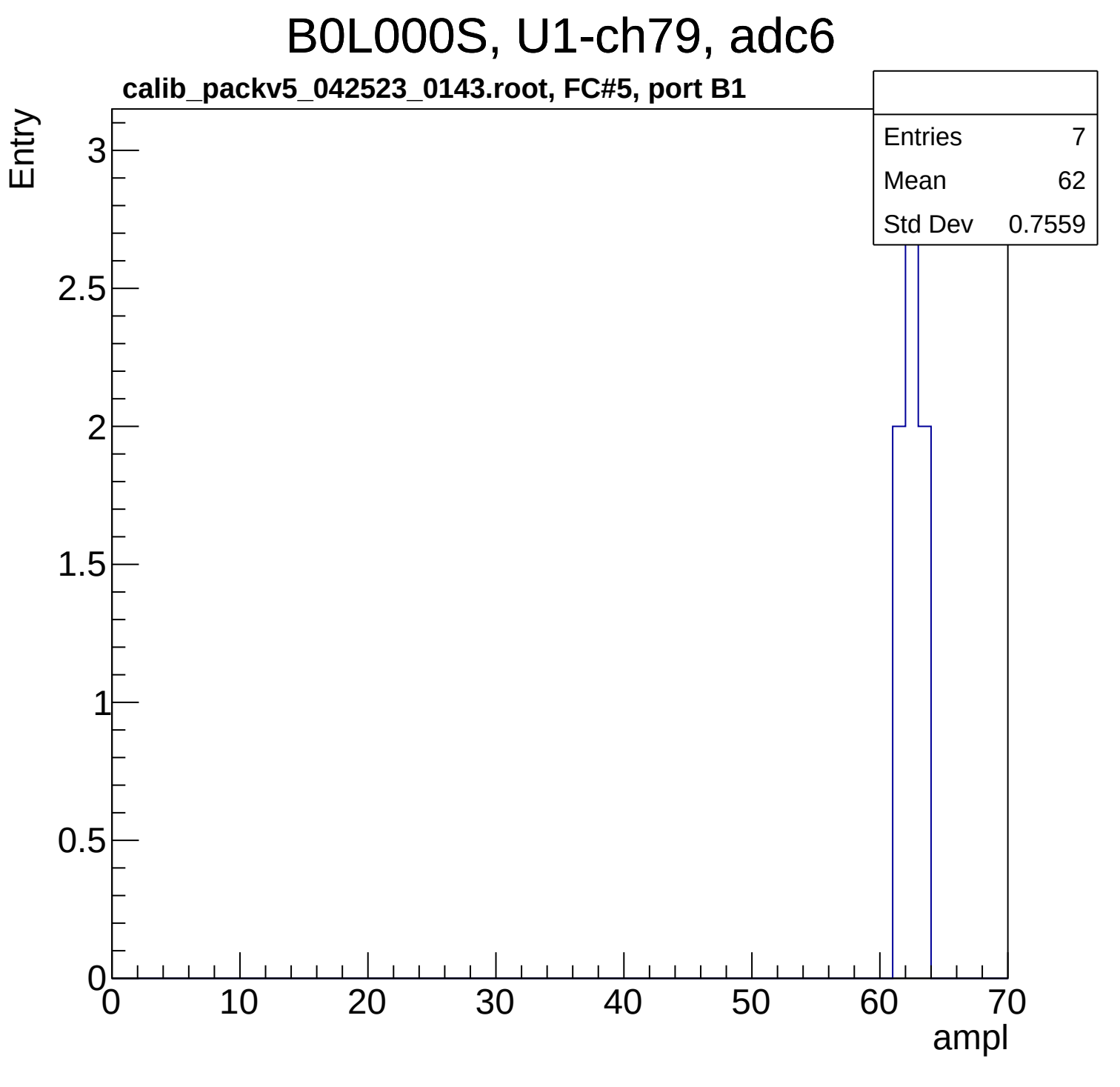
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch79, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 55.84
Std Dev 3.326
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch79, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.64
Std Dev 8.912
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch79, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62
Std Dev 0.7559
Entry
ampl
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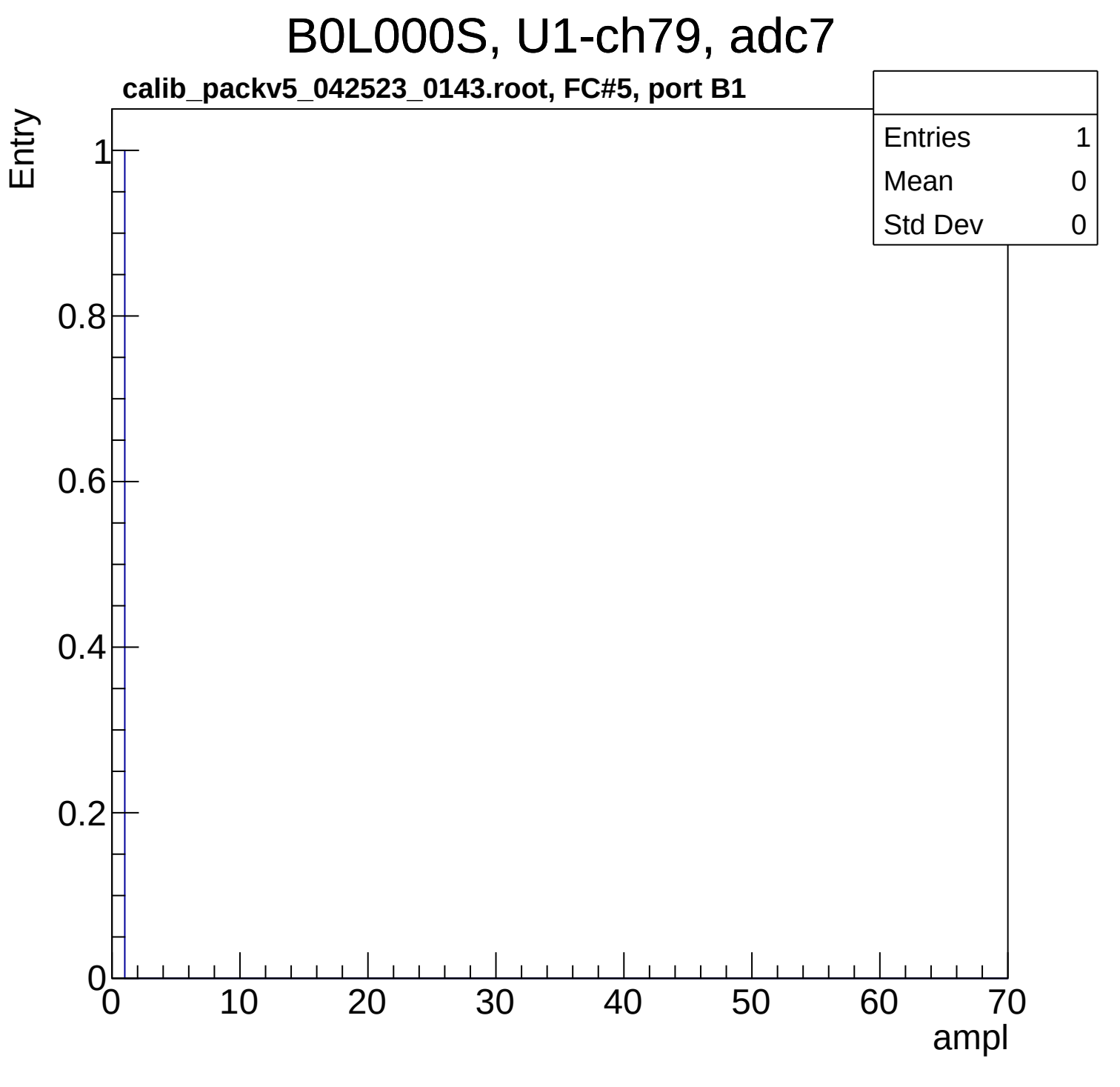
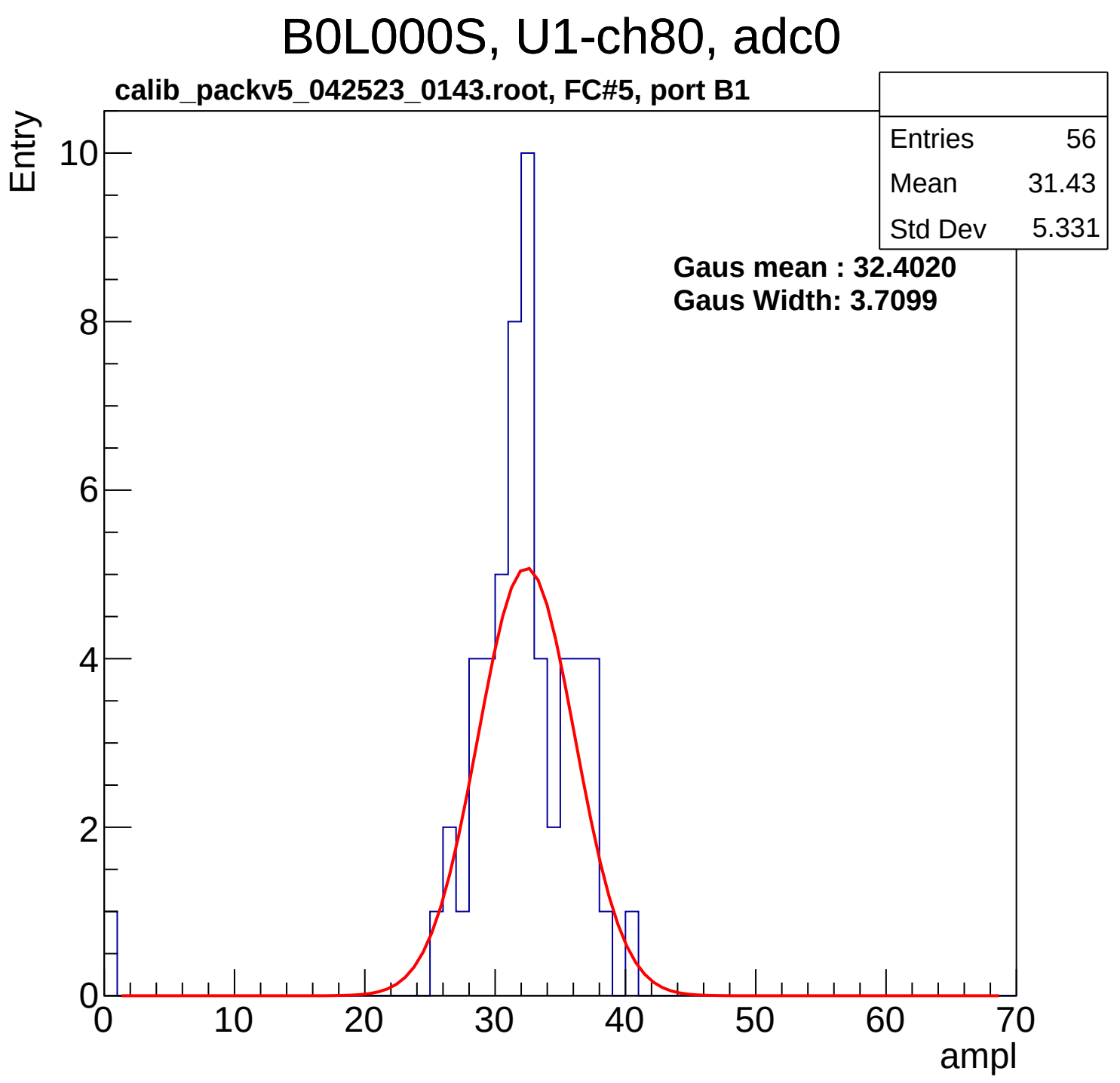
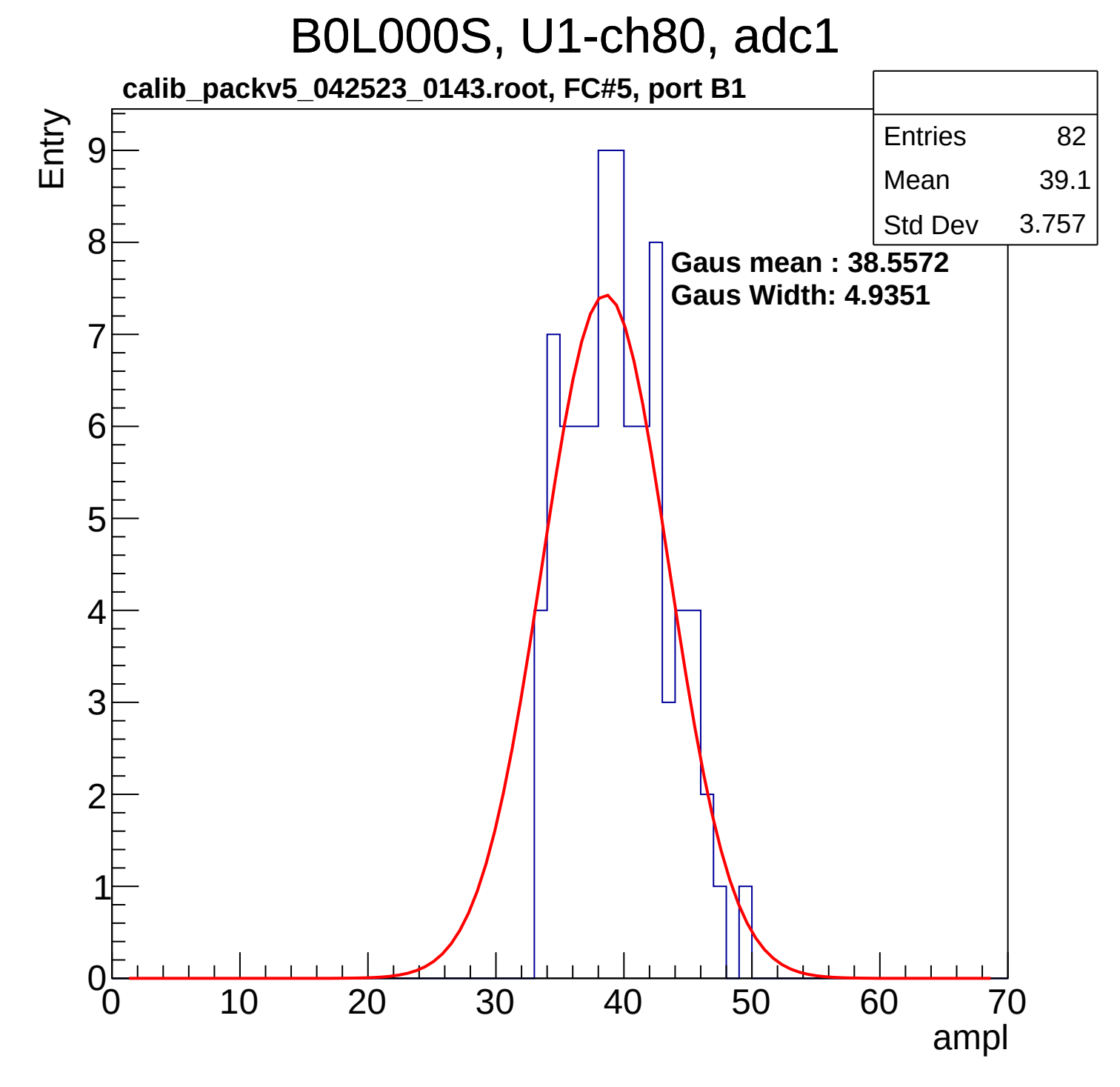
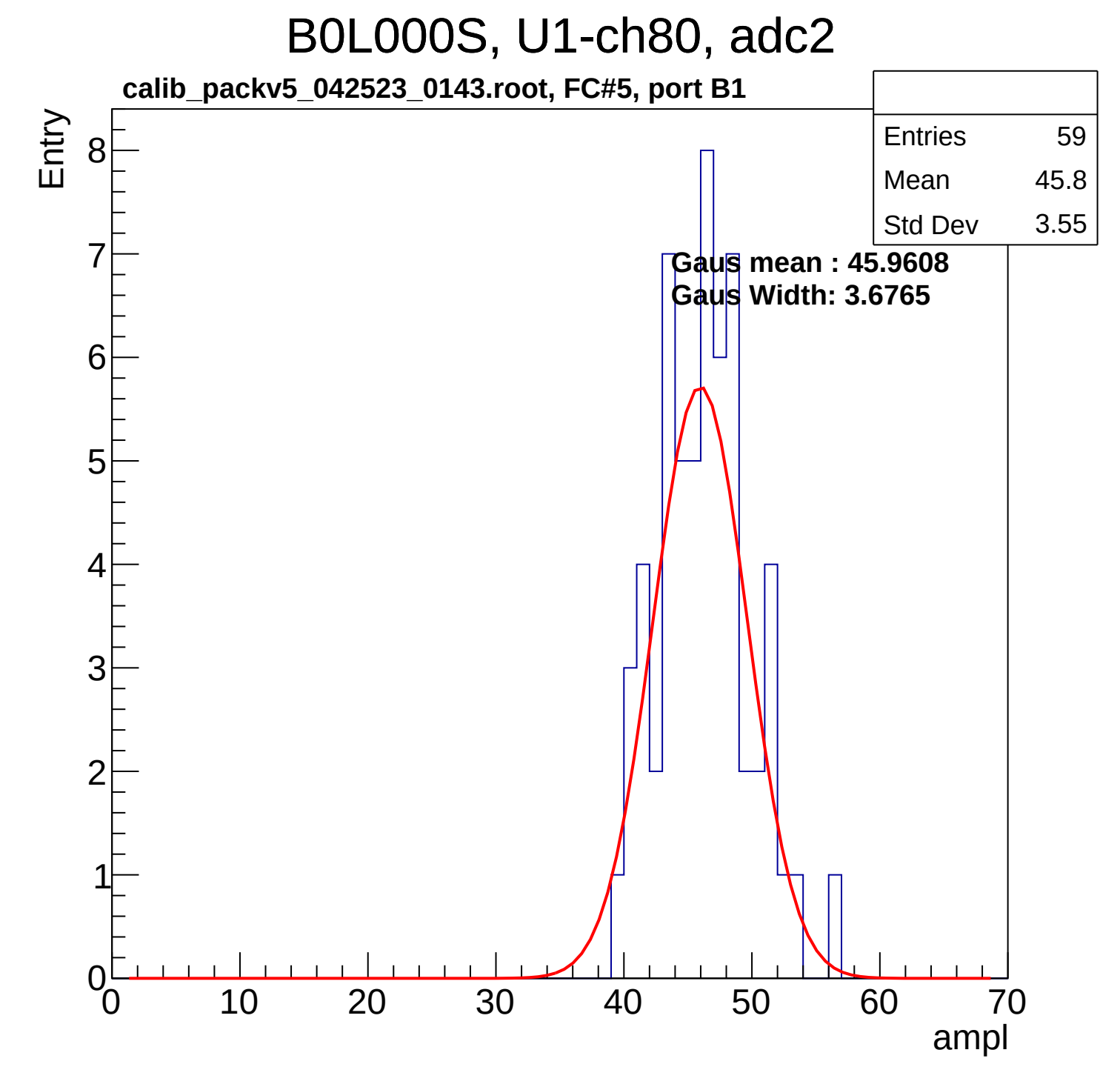
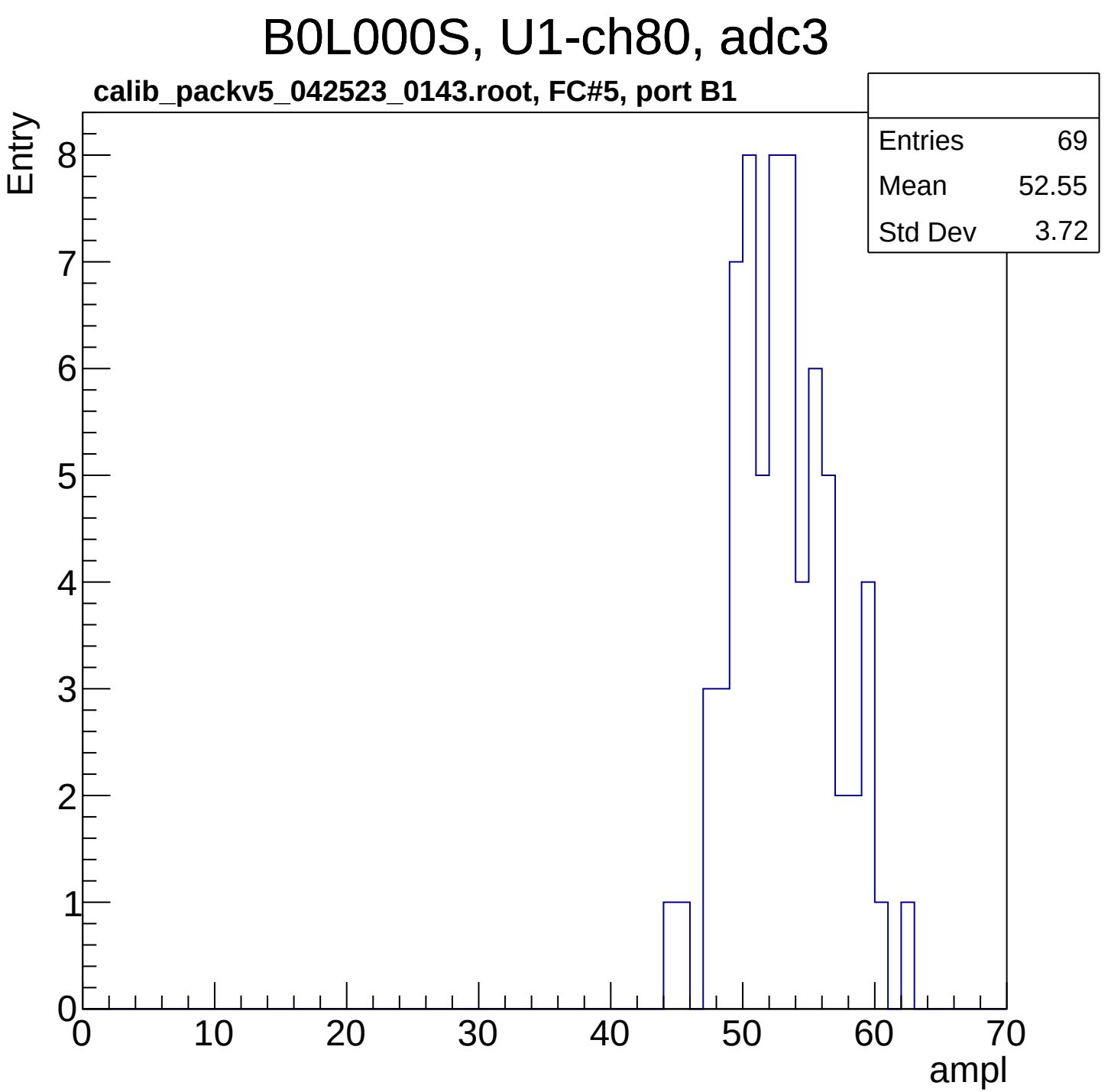
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch79, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch80, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 31.43
Std Dev 5.331
Gaus mean : 32.4020
Gaus Width: 3.7099
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch80, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 39.1
Std Dev 3.757
Gaus mean : 38.5572
Gaus Width: 4.9351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch80, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 45.8
Std Dev 3.55
Gaus mean : 45.9608
Gaus Width: 3.6765
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch80, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 52.55
Std Dev 3.72
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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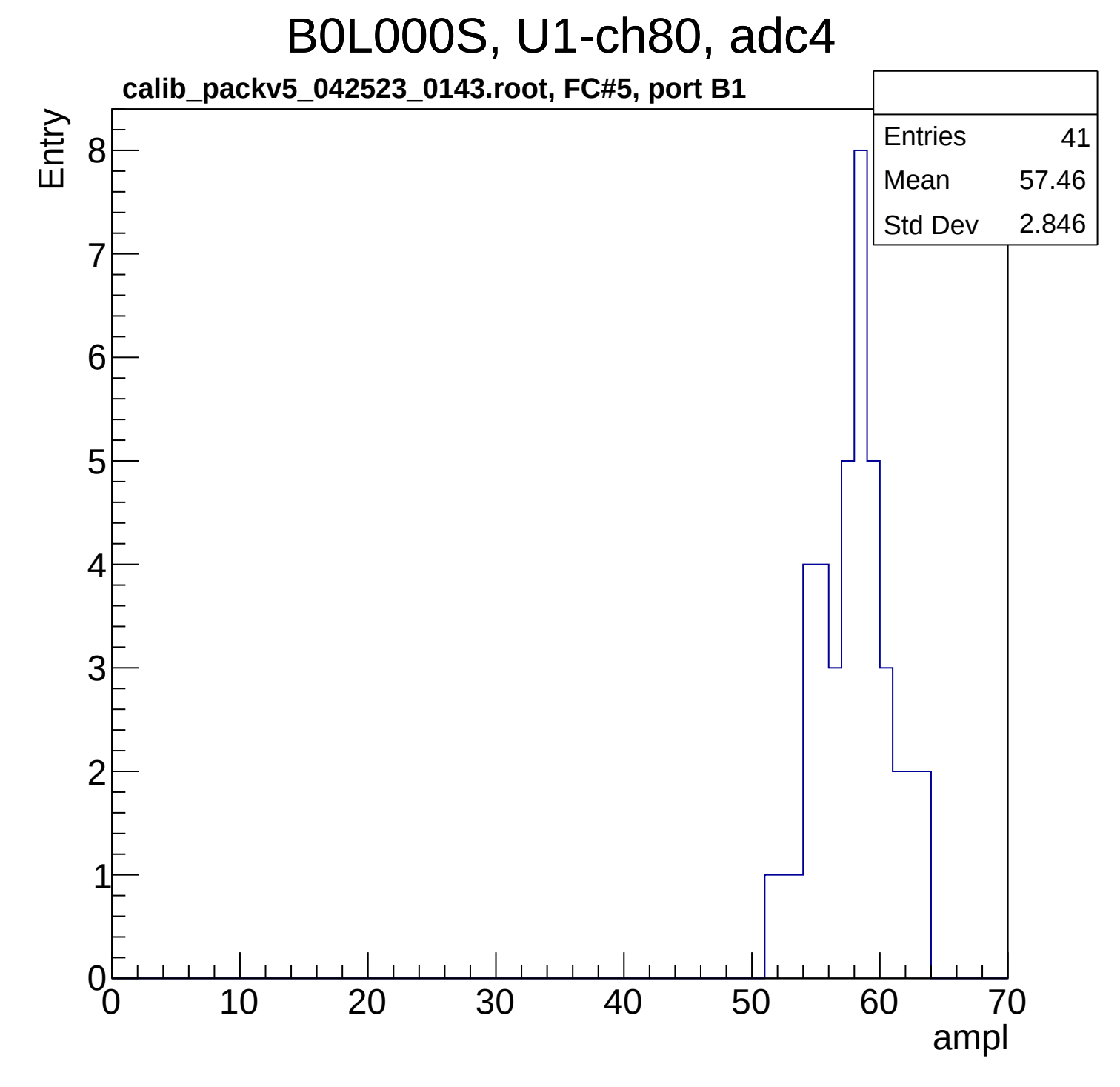
40
50
60
70


B0L000S, U1-ch80, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 57.46
Std Dev 2.846
Entry
ampl
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1
2
3
4
5
6
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0
10
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40
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60
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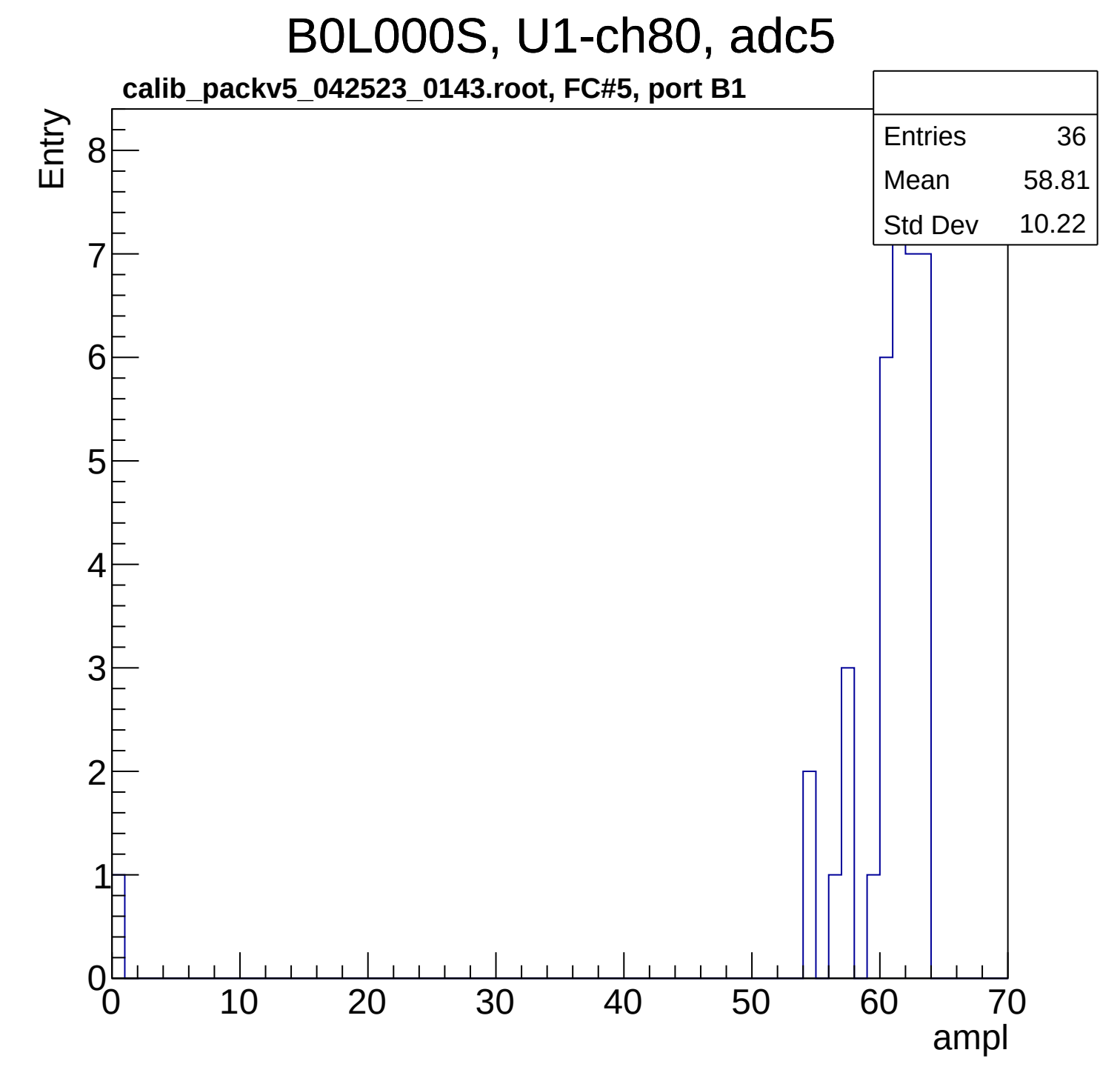
B0L000S, U1-ch80, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.81
Std Dev 10.22
Entry
ampl
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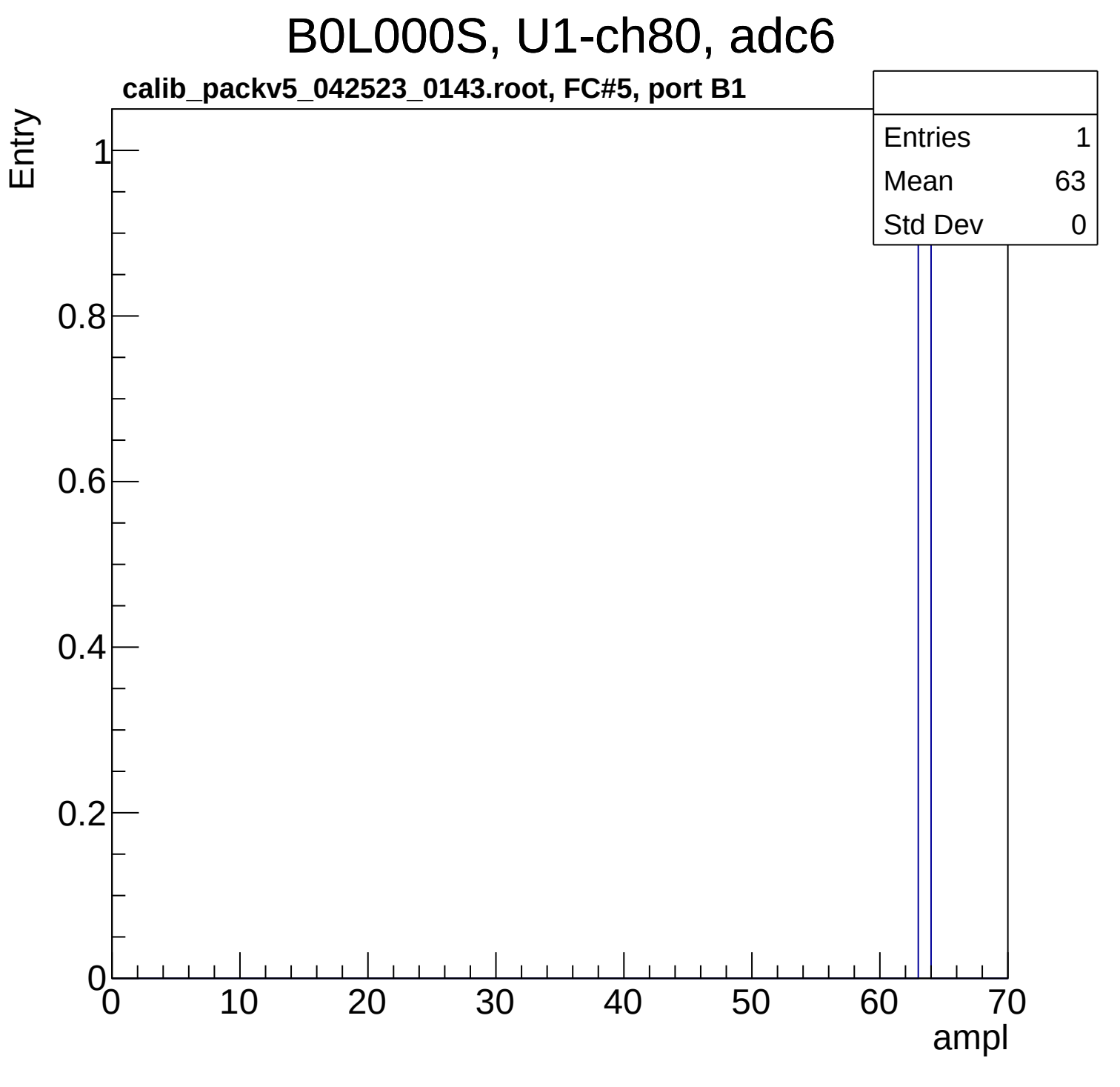
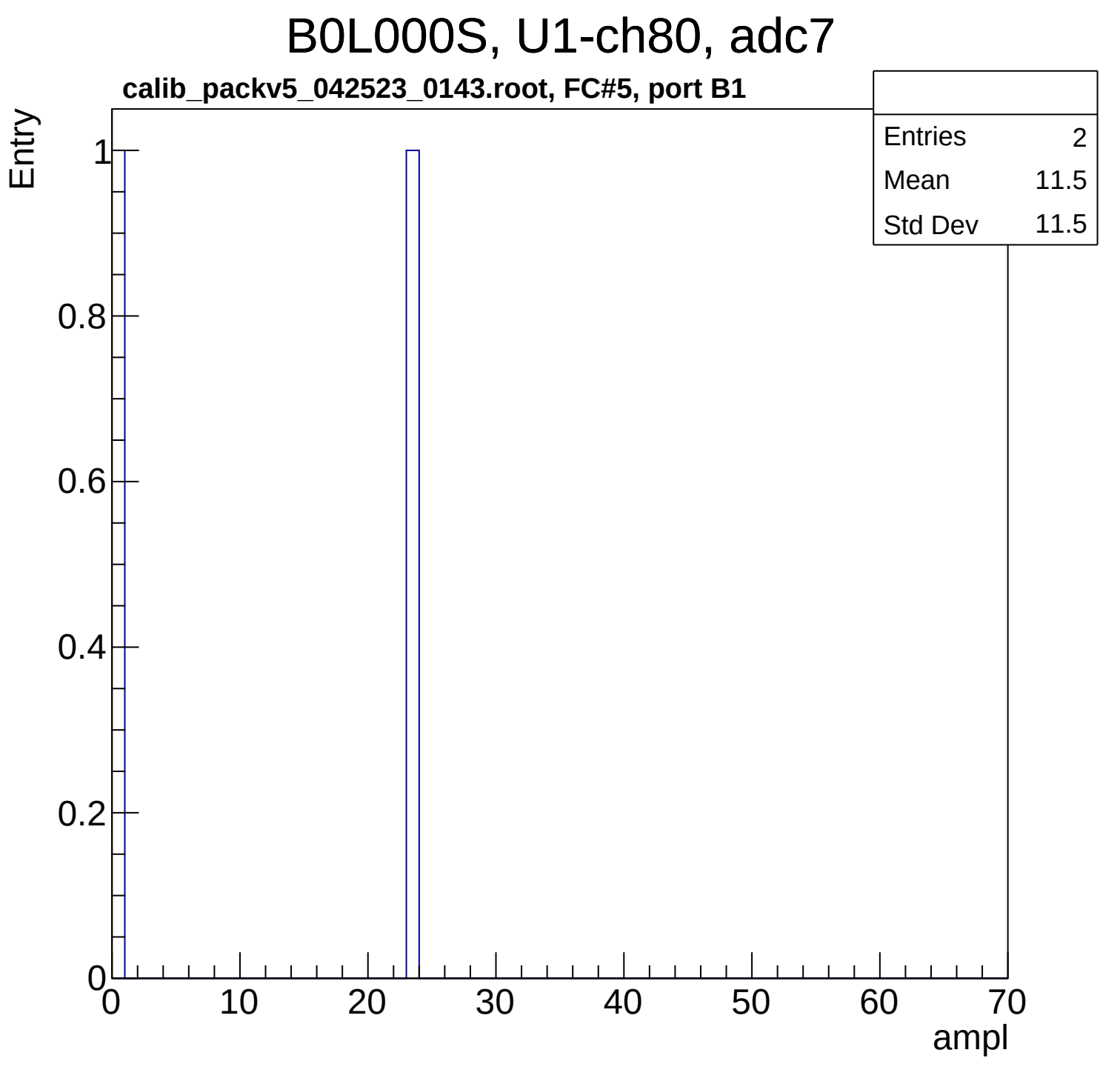
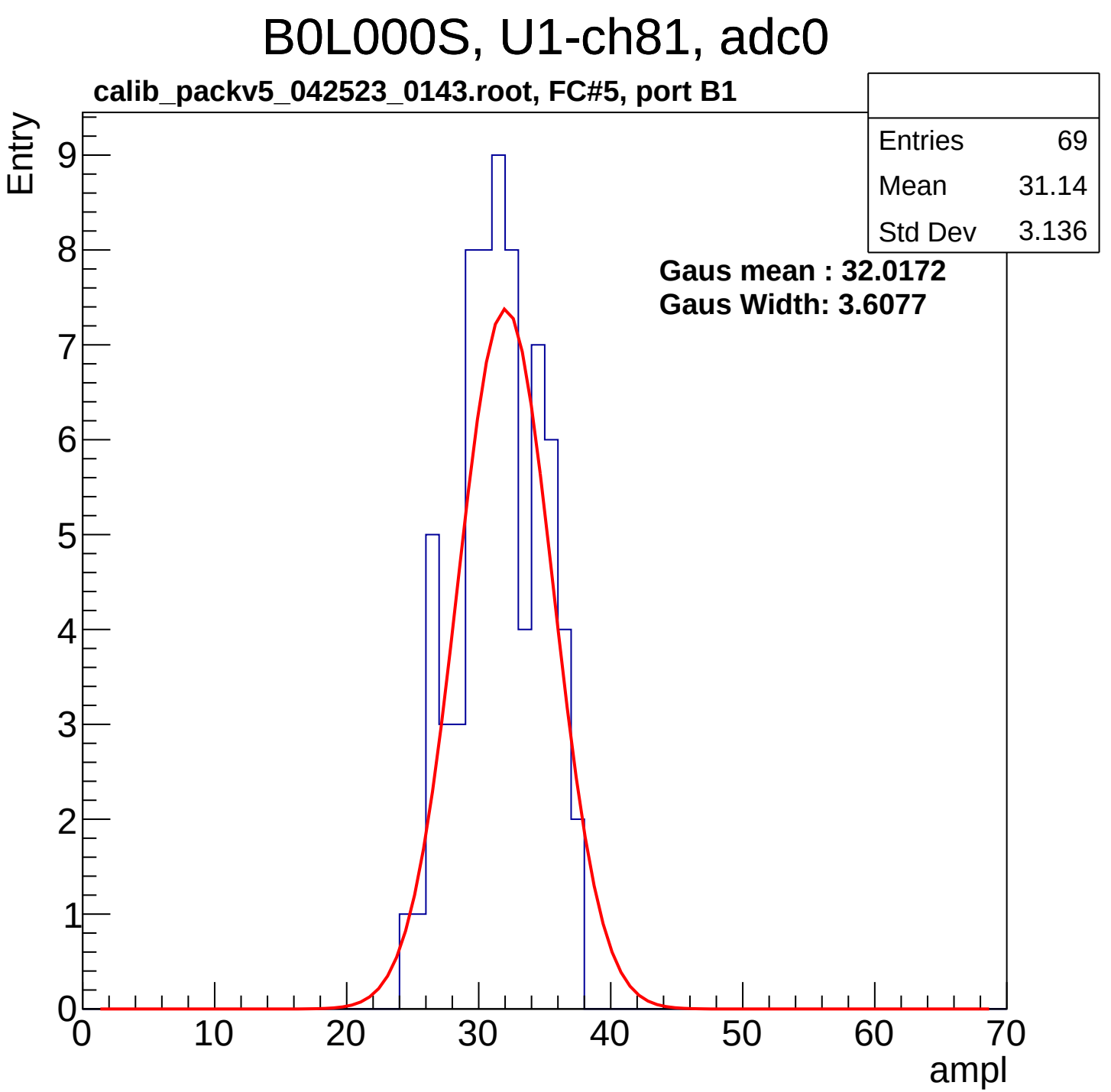
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch80, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch80, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch81, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 31.14
Std Dev 3.136
Gaus mean : 32.0172
Gaus Width: 3.6077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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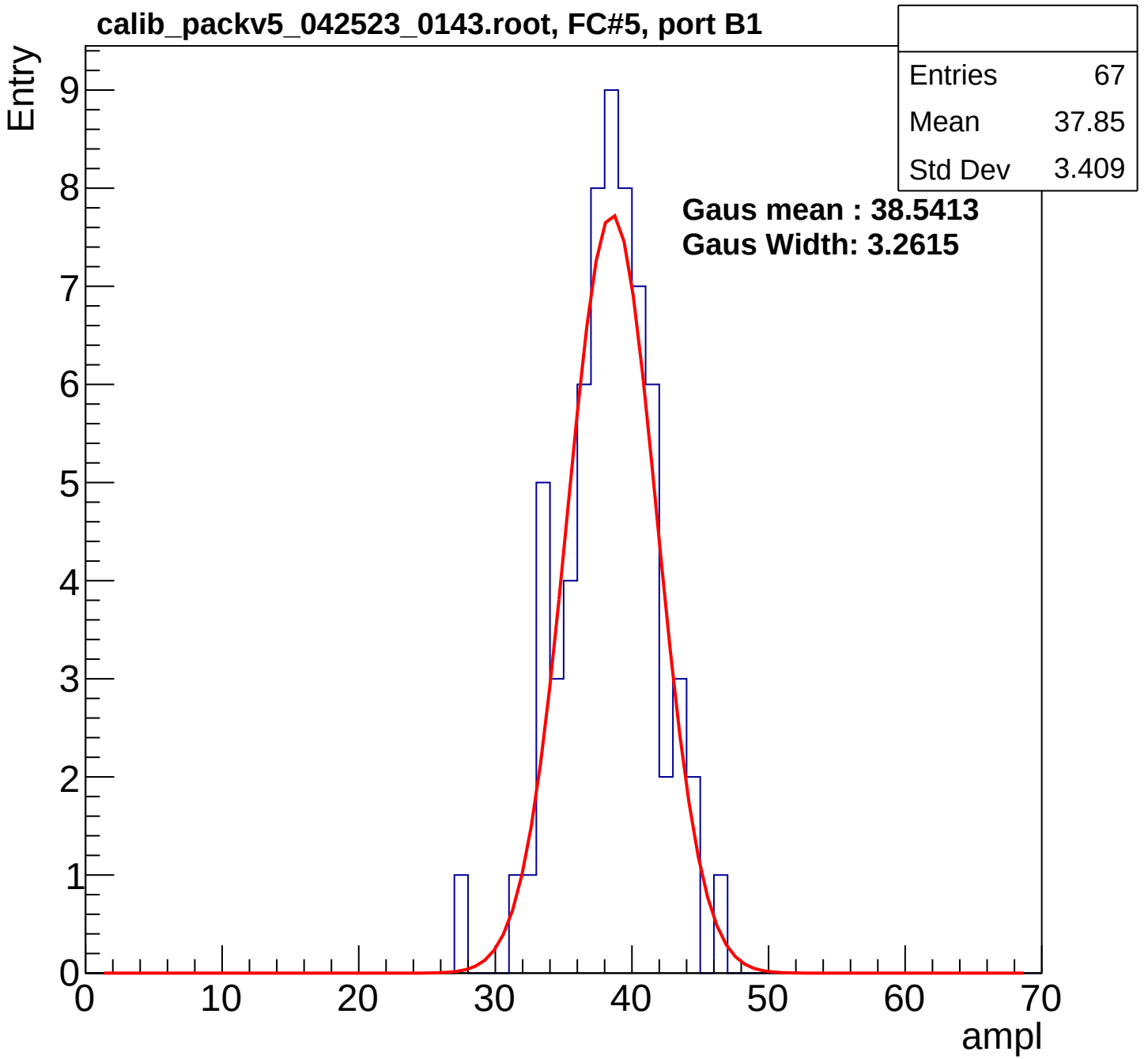
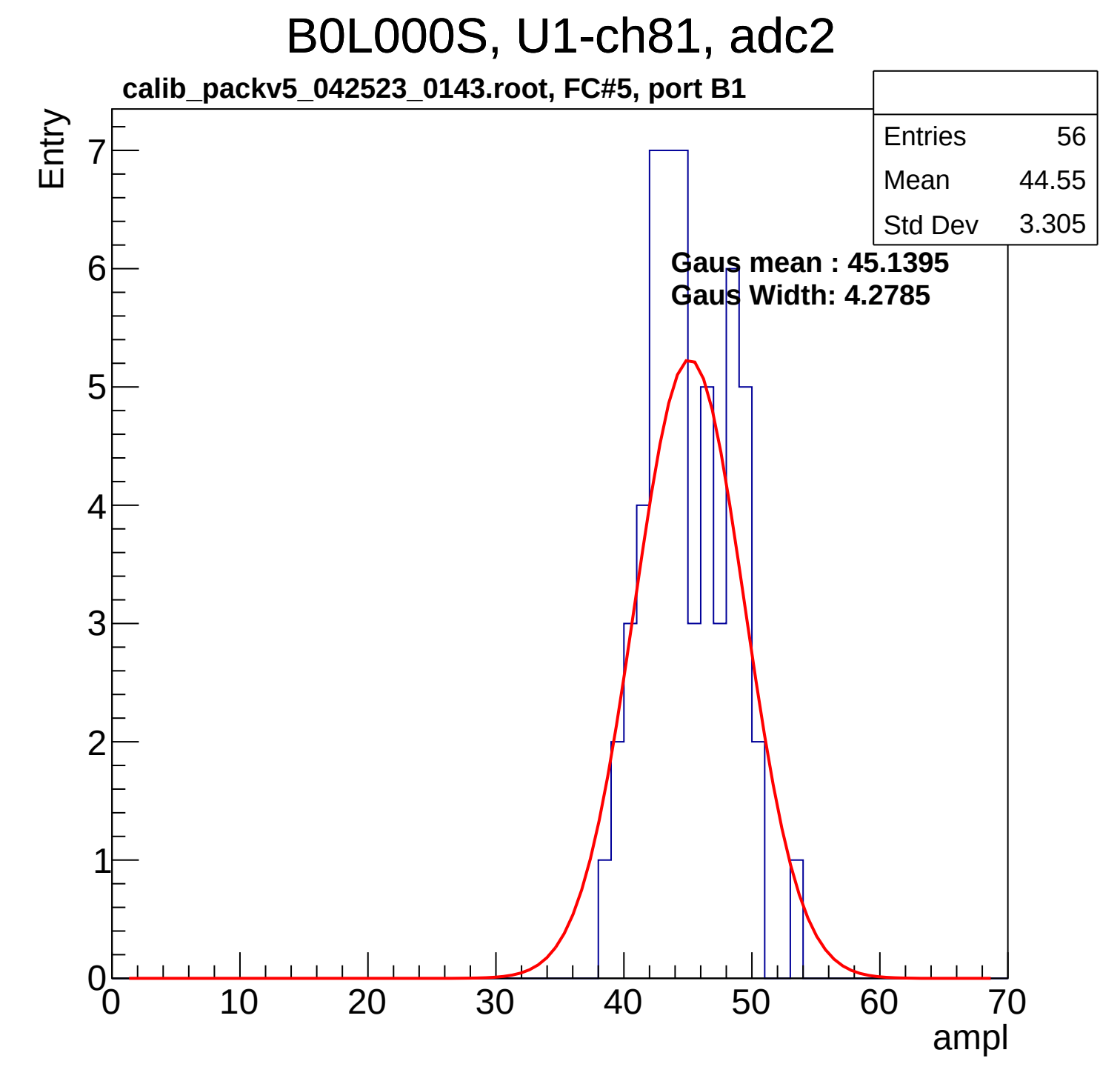
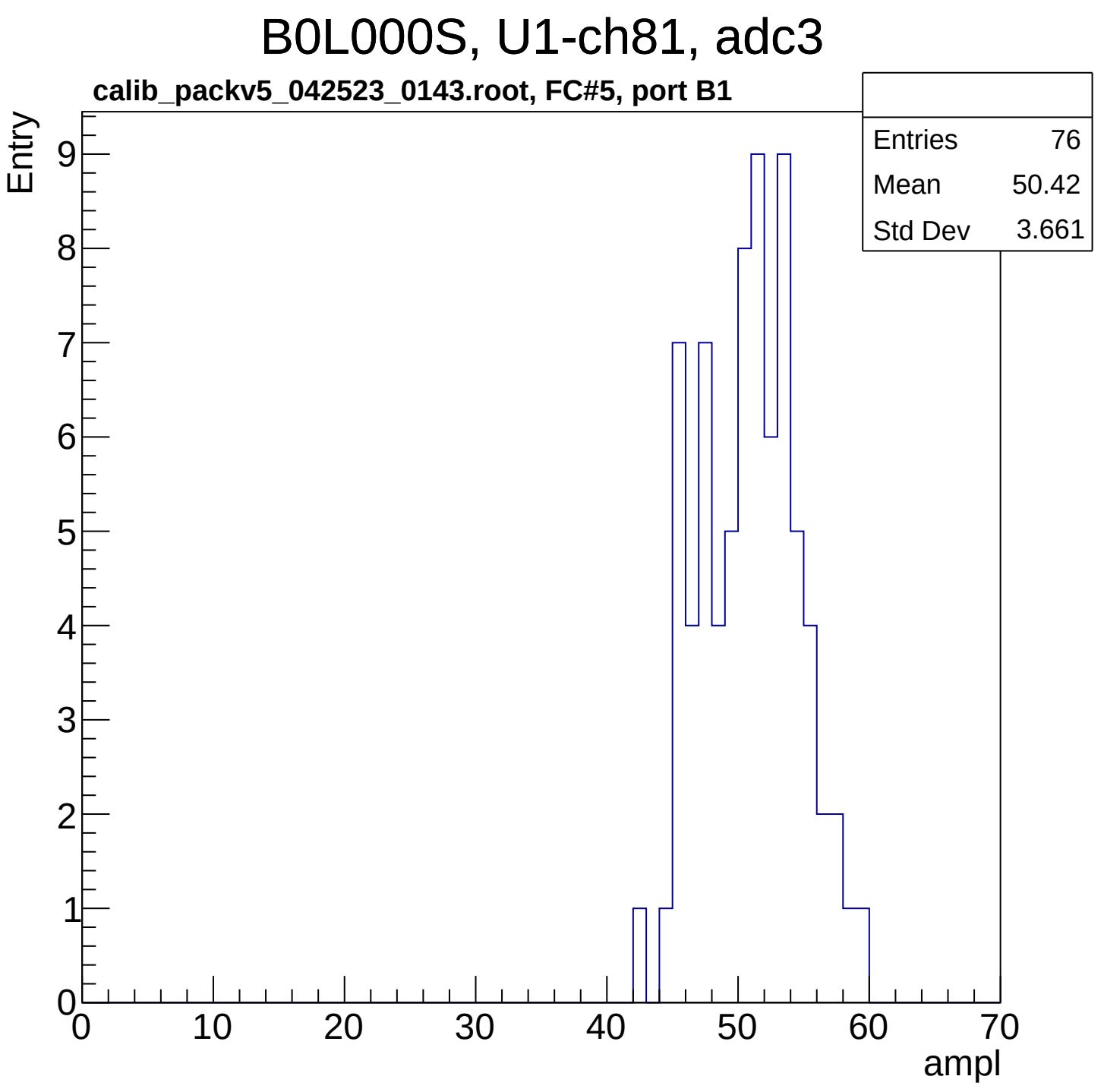
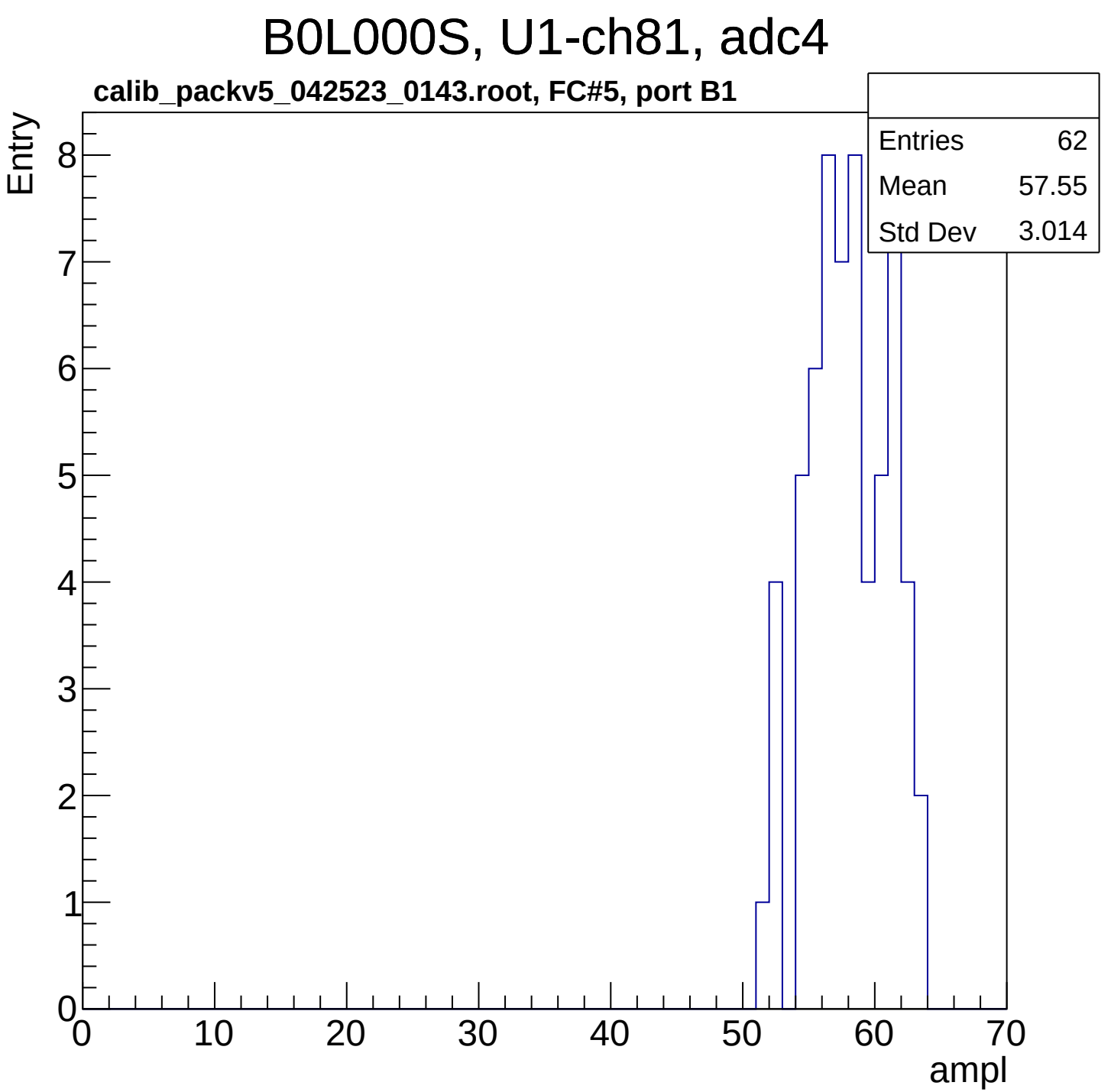
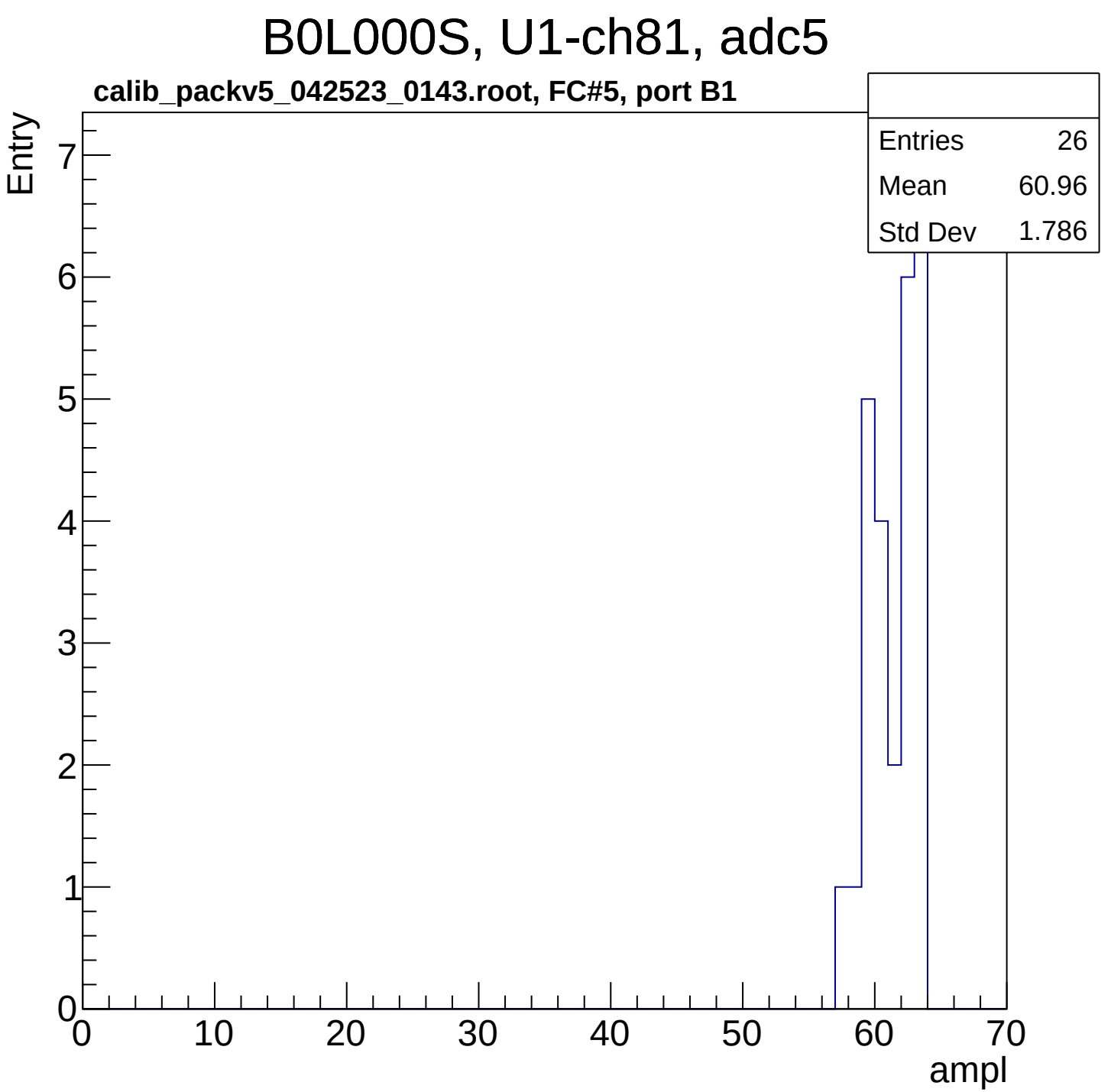
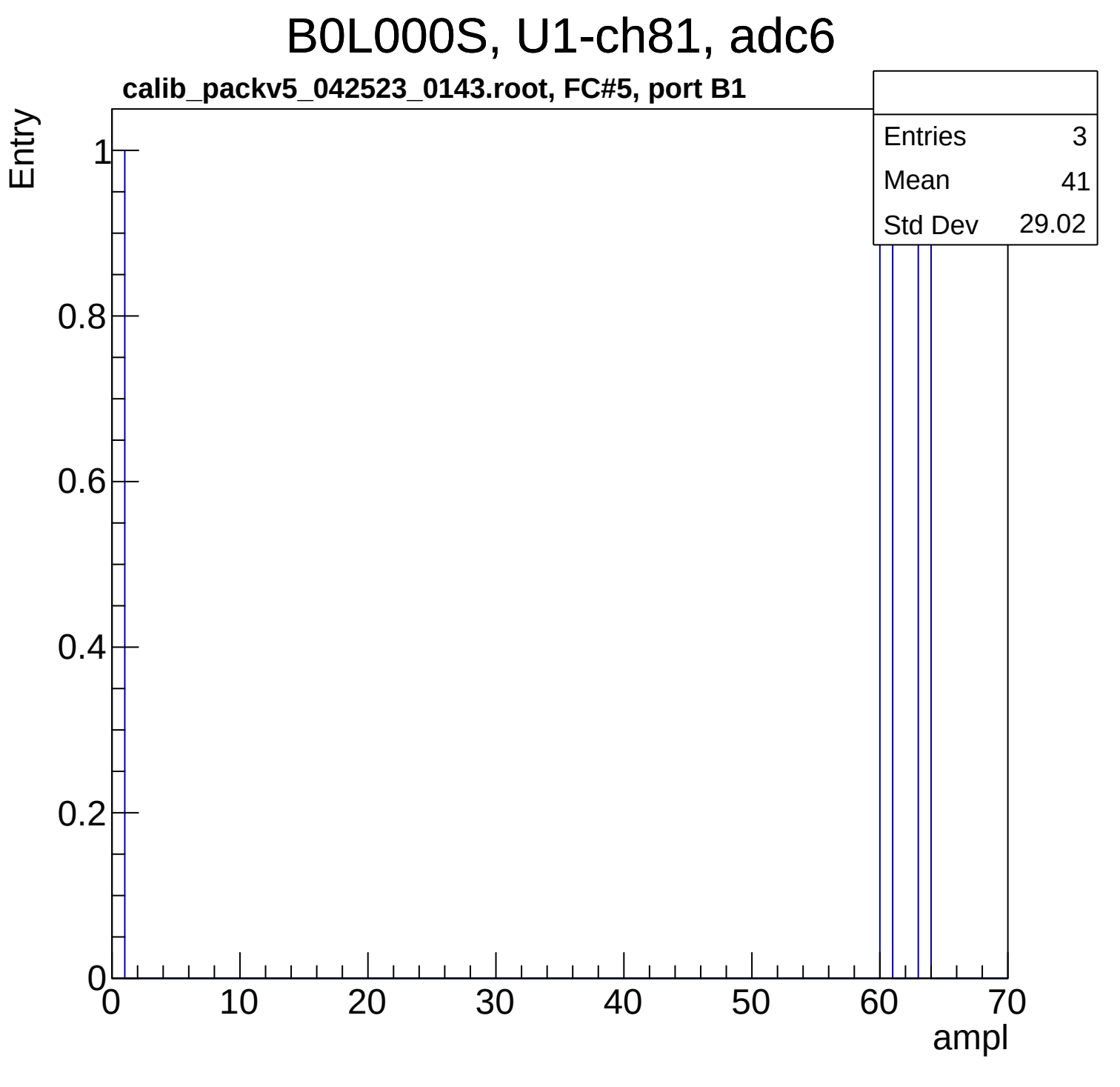

B0L000S, U1-ch81, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.85
Std Dev 3.409
Gaus mean : 38.5413
Gaus Width: 3.2615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

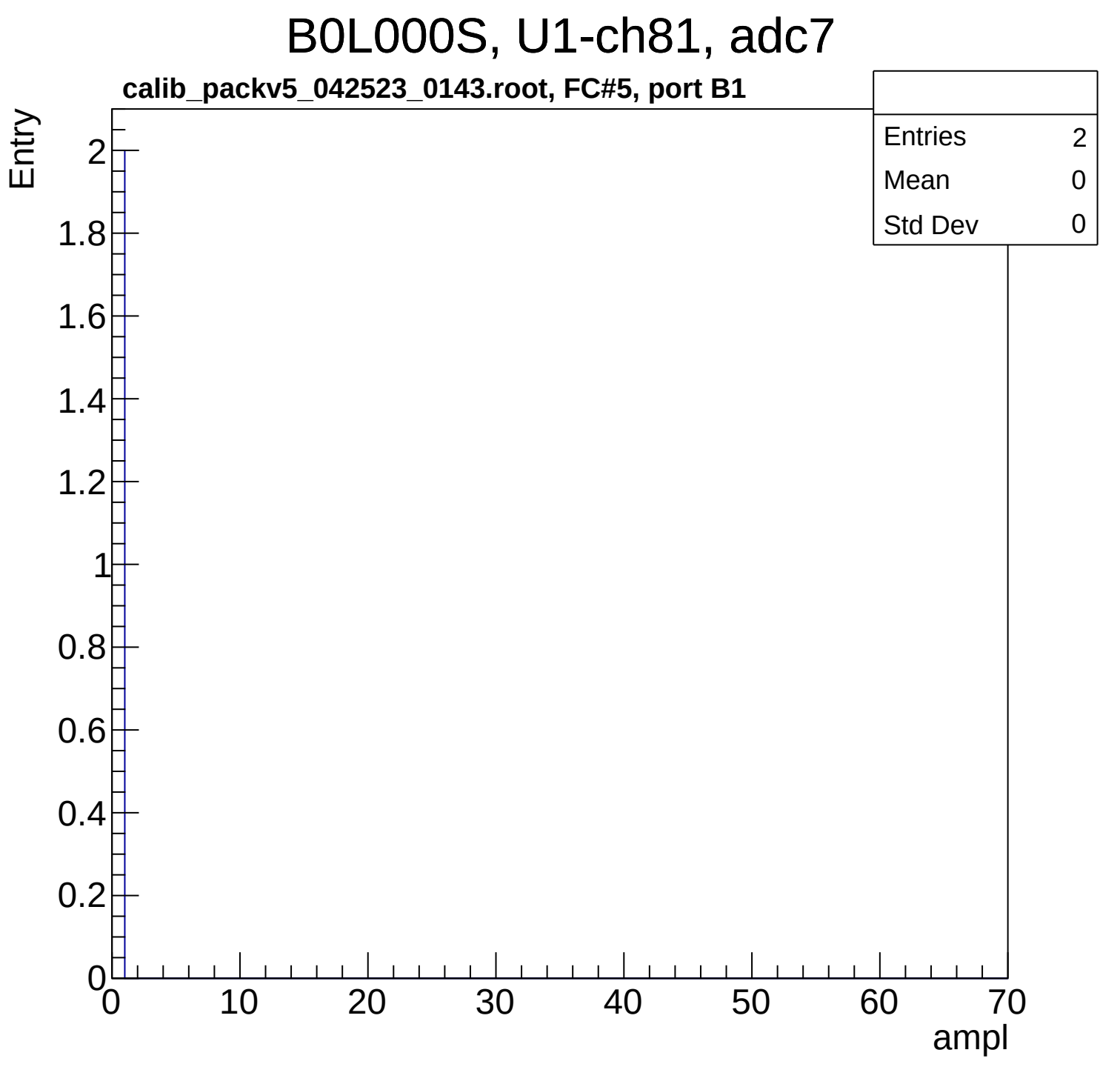

B0L000S, U1-ch81, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.55
Std Dev 3.305
Gaus mean : 45.1395
Gaus Width: 4.2785
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

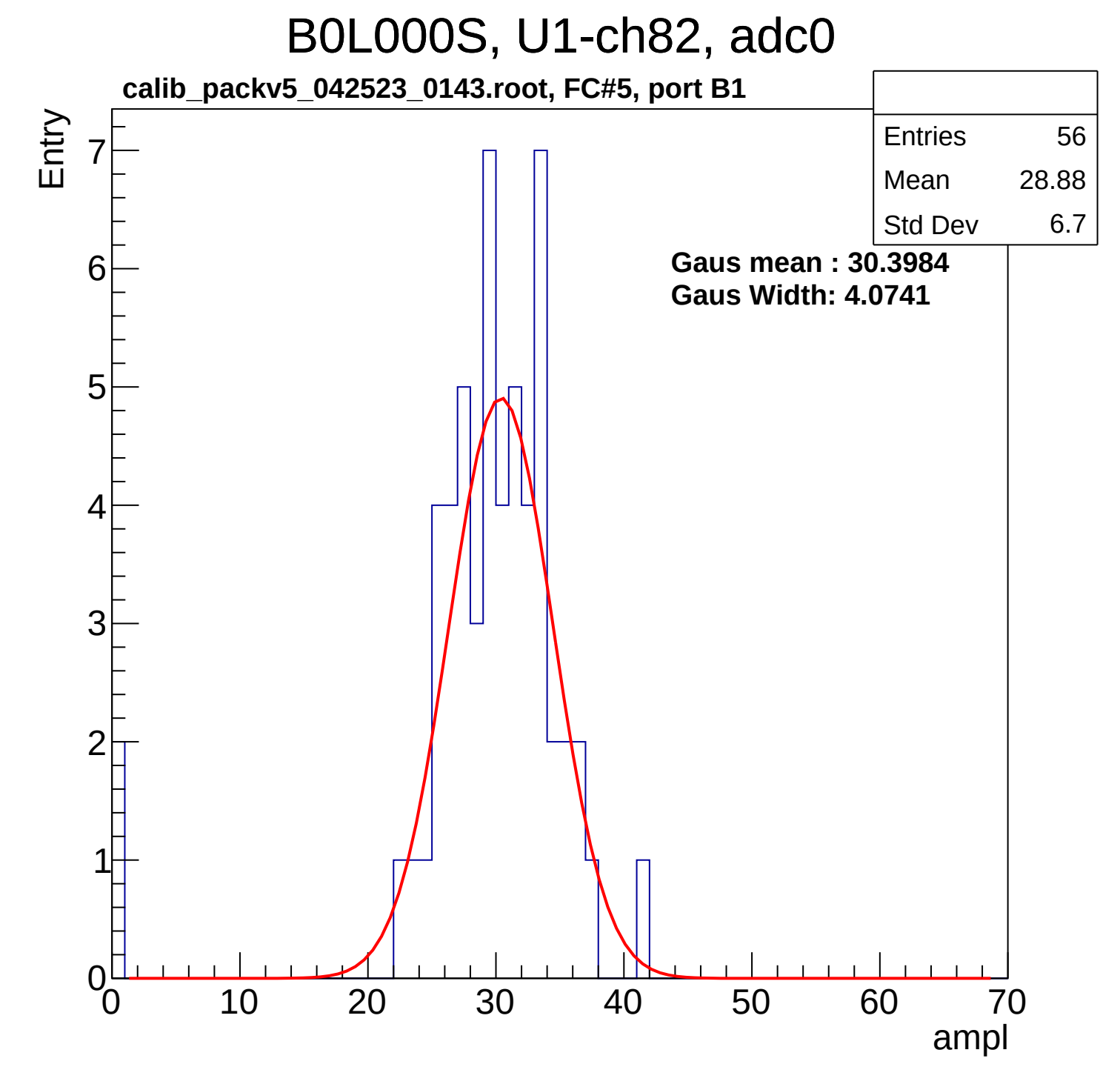

B0L000S, U1-ch81, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 50.42
Std Dev 3.661
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

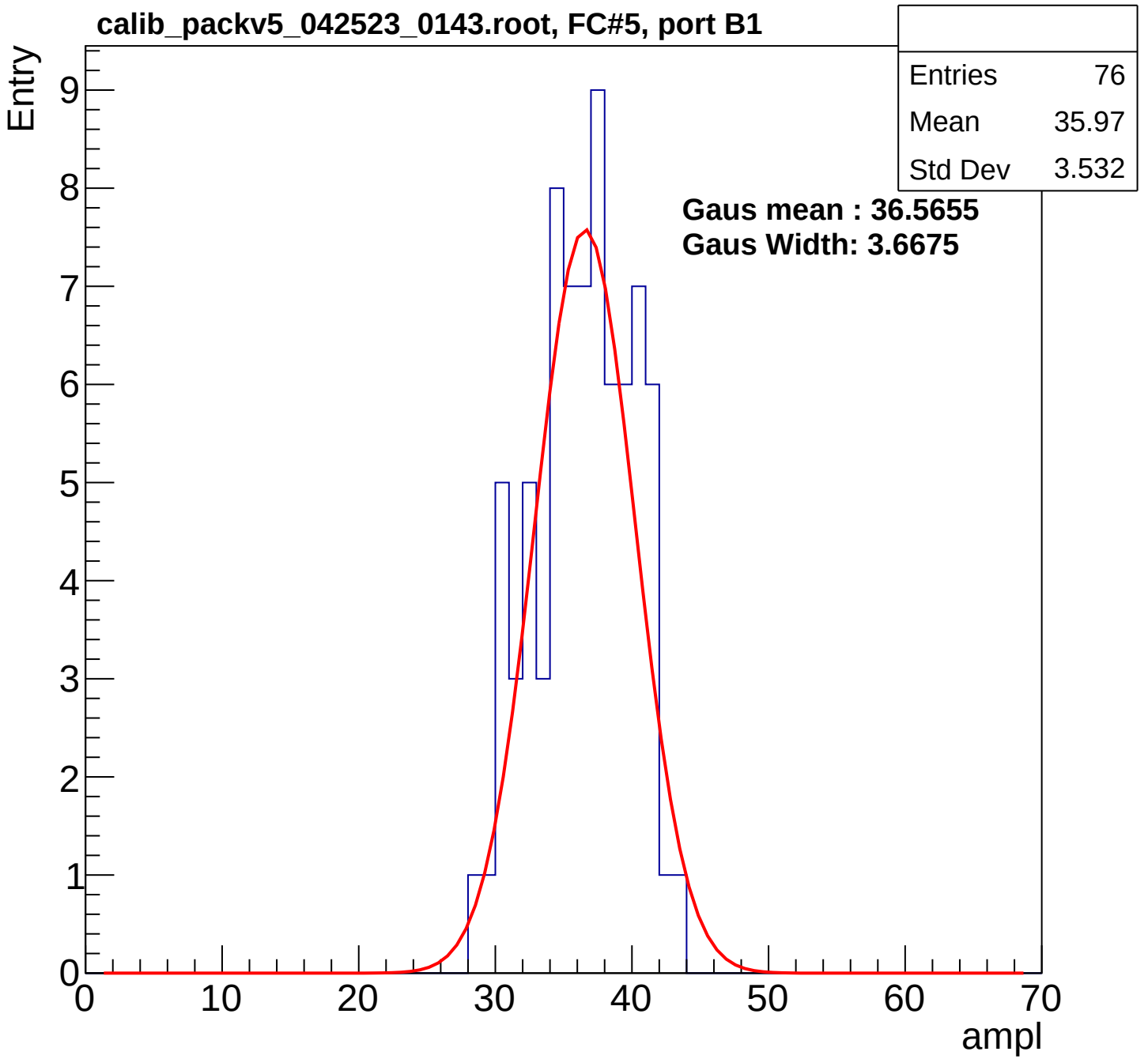

B0L000S, U1-ch81, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 57.55
Std Dev 3.014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

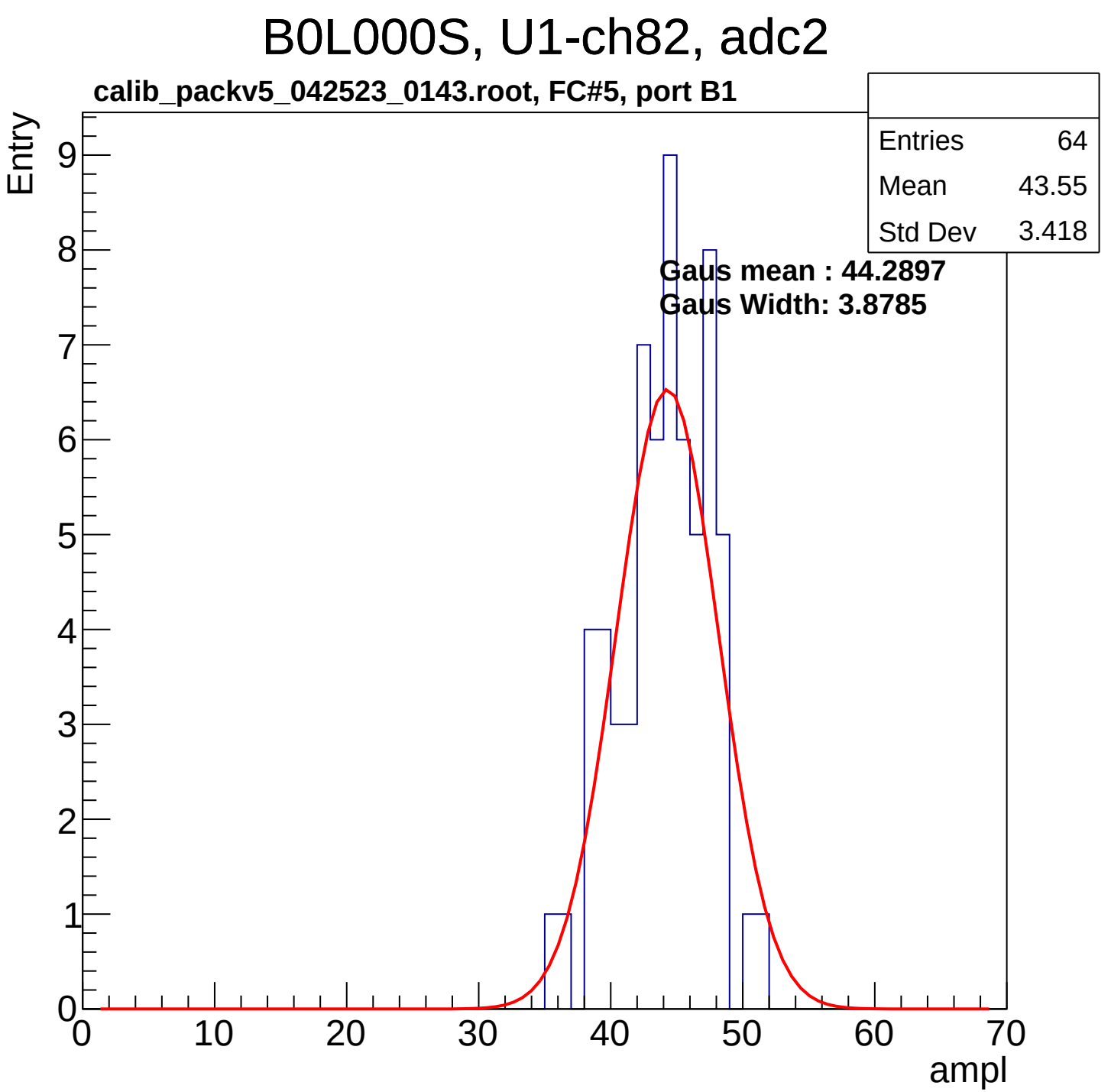

B0L000S, U1-ch81, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 60.96
Std Dev 1.786
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

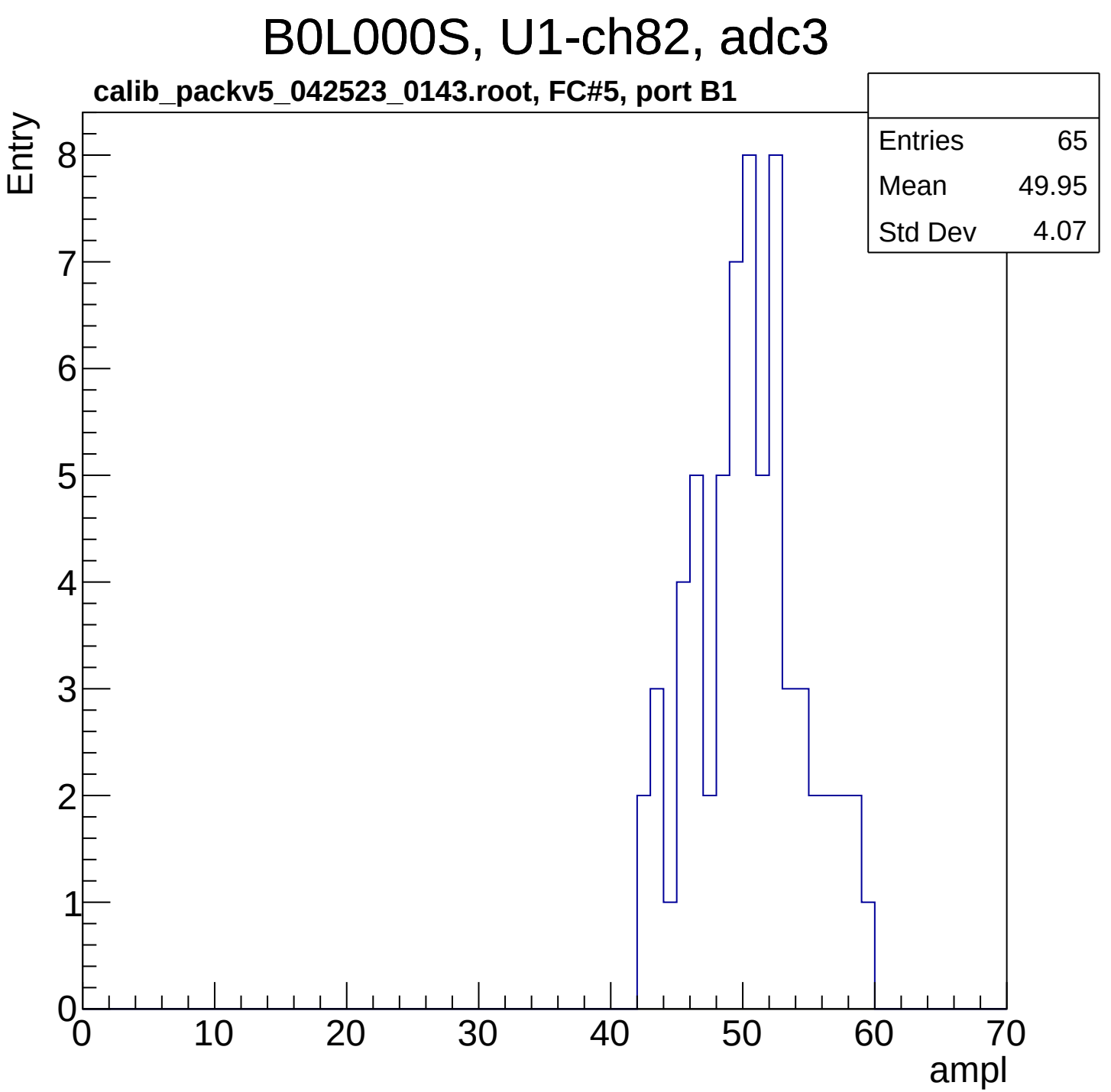

B0L000S, U1-ch81, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

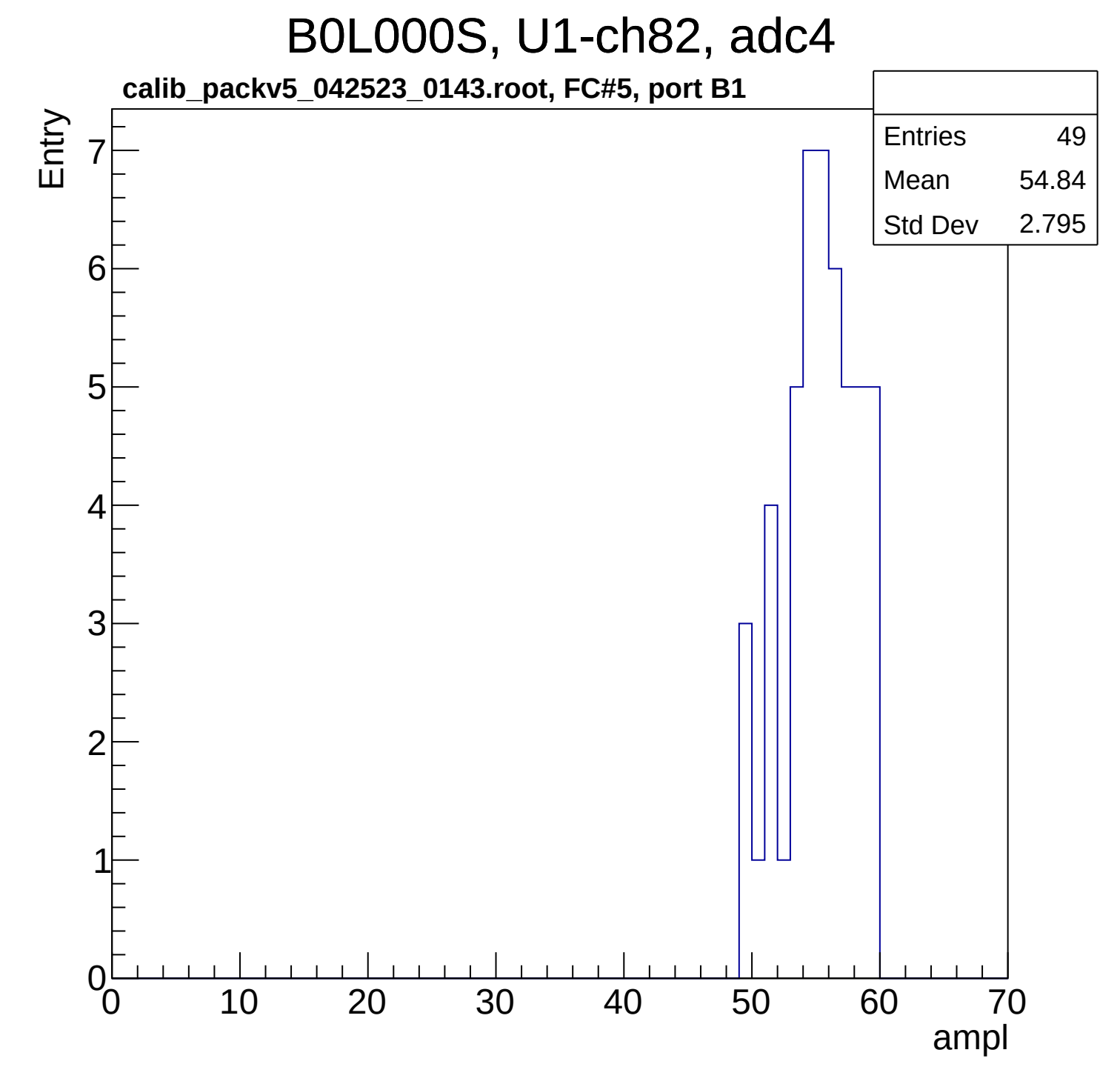

B0L000S, U1-ch81, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

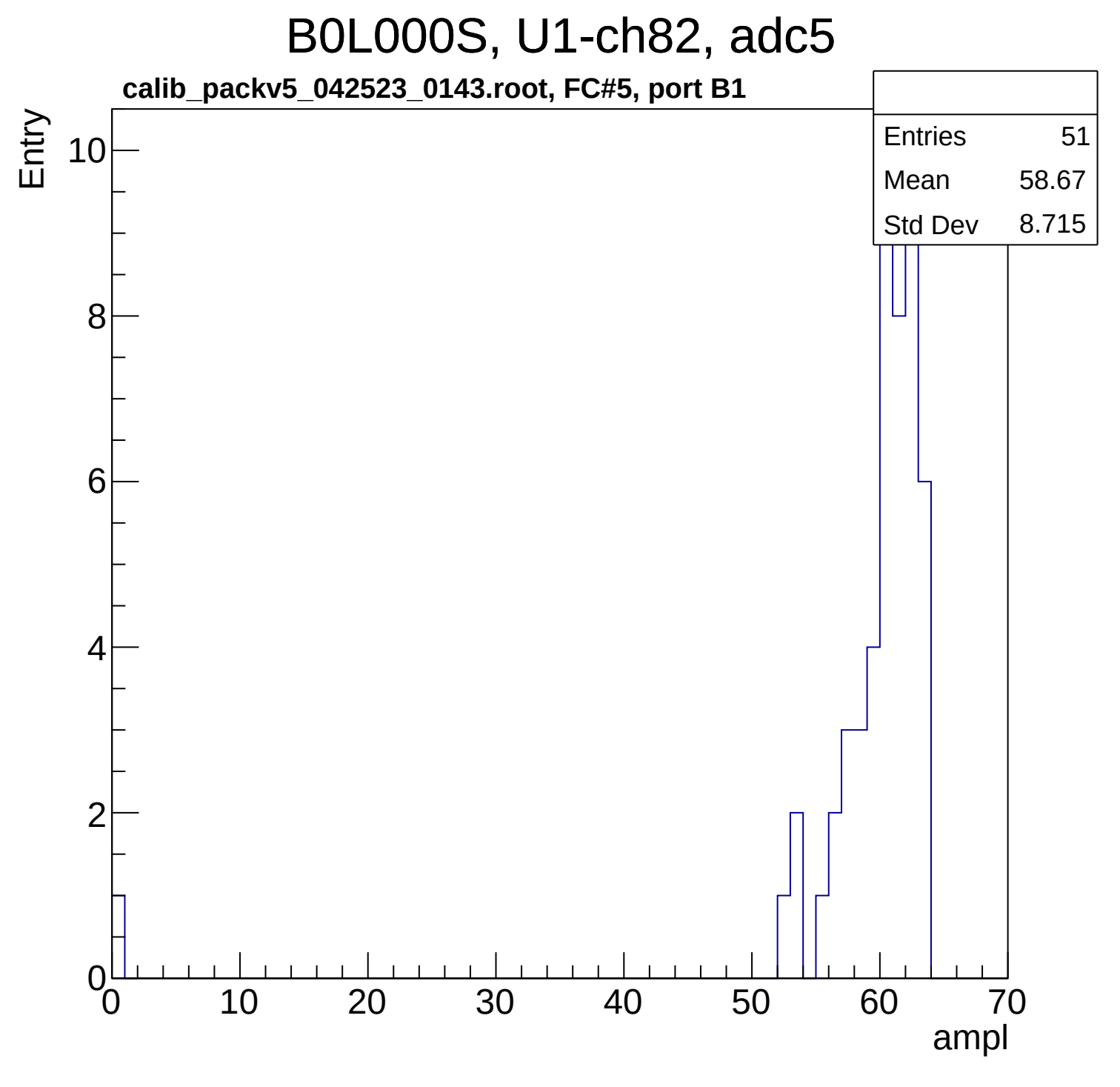

B0L000S, U1-ch82, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 28.88
Std Dev 6.7
Gaus mean : 30.3984
Gaus Width: 4.0741
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

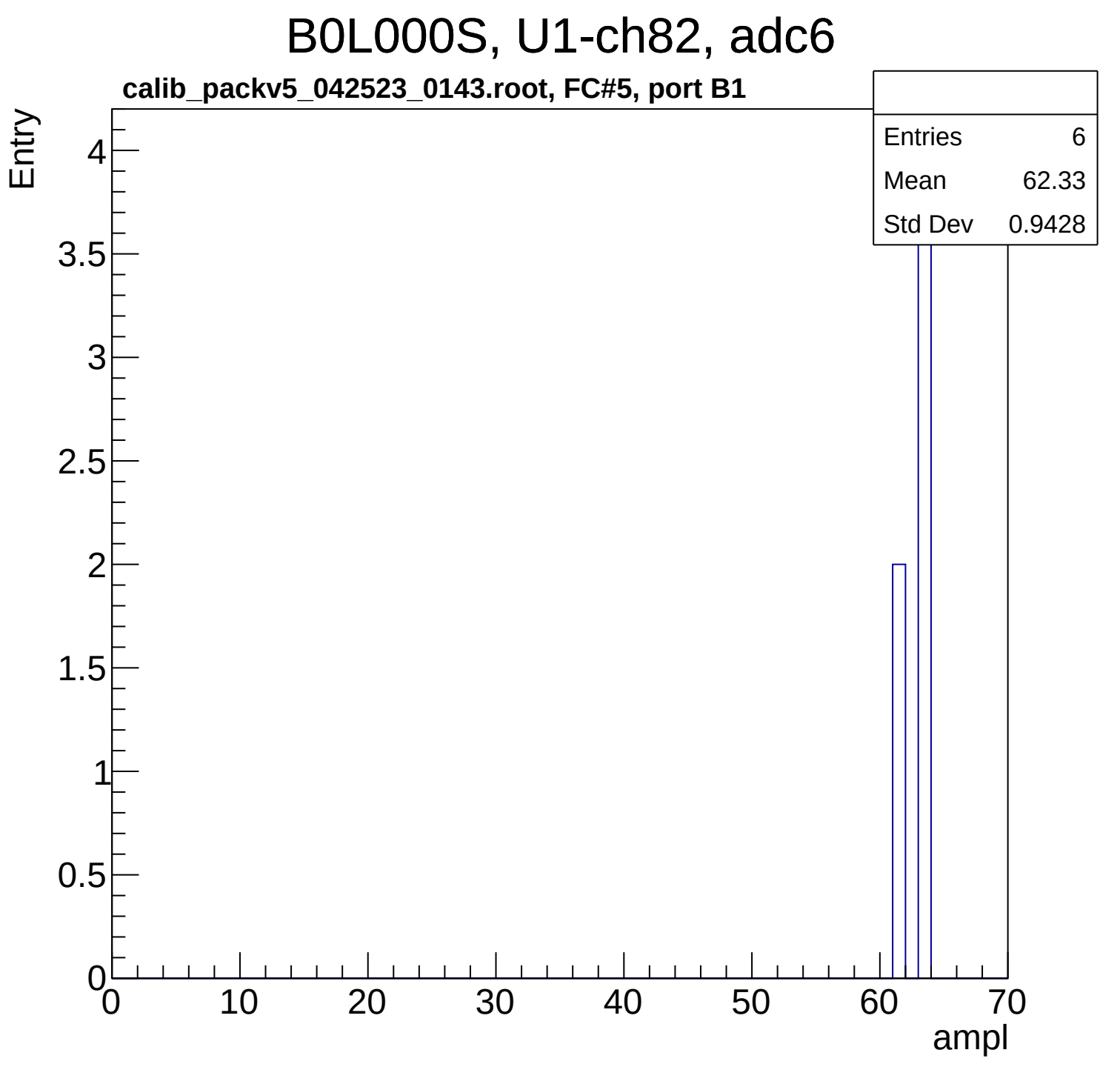

B0L000S, U1-ch82, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 35.97
Std Dev 3.532
Gaus mean : 36.5655
Gaus Width: 3.6675
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

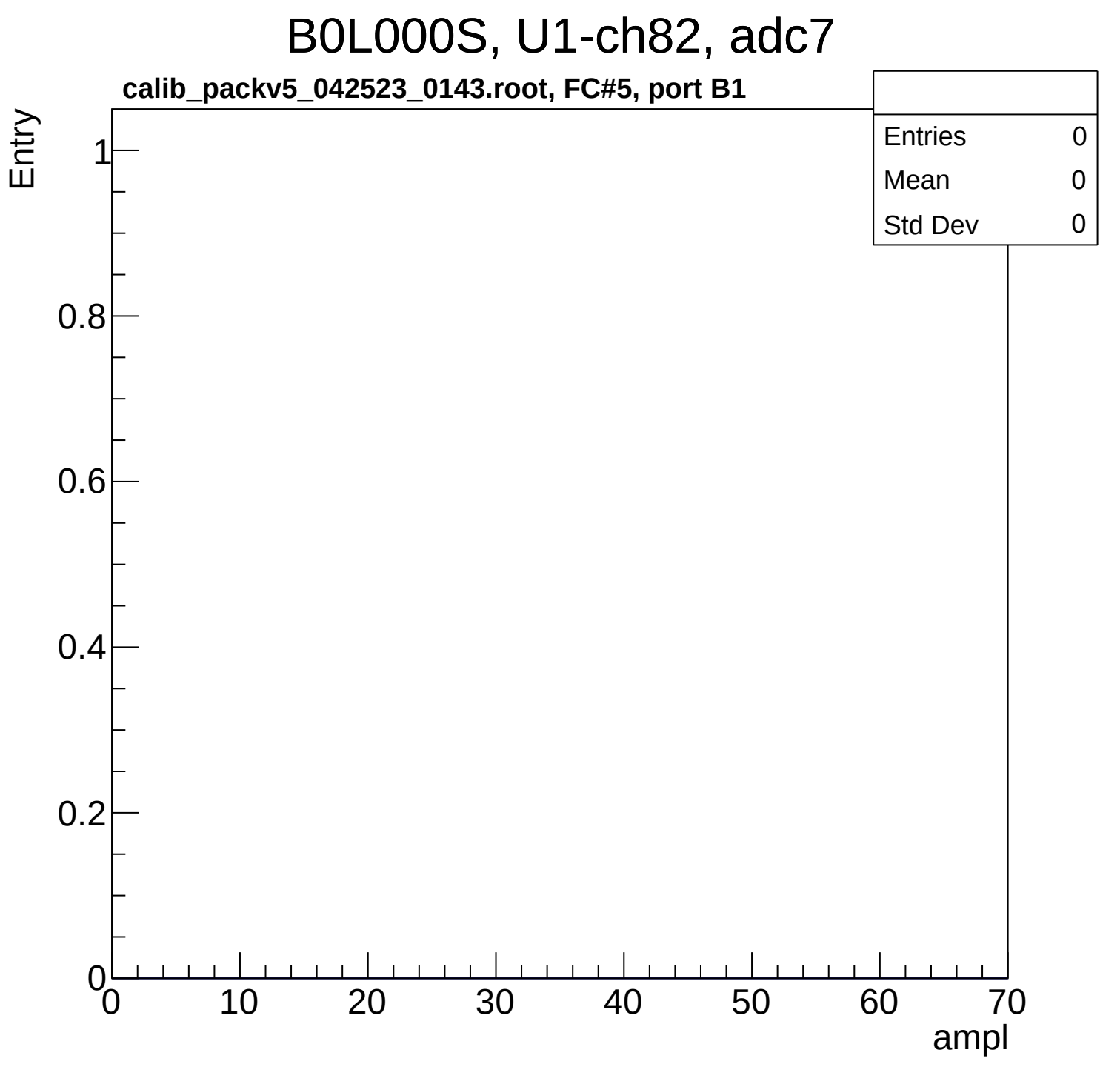

B0L000S, U1-ch82, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.55
Std Dev 3.418
Gaus mean : 44.2897
Gaus Width: 3.8785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

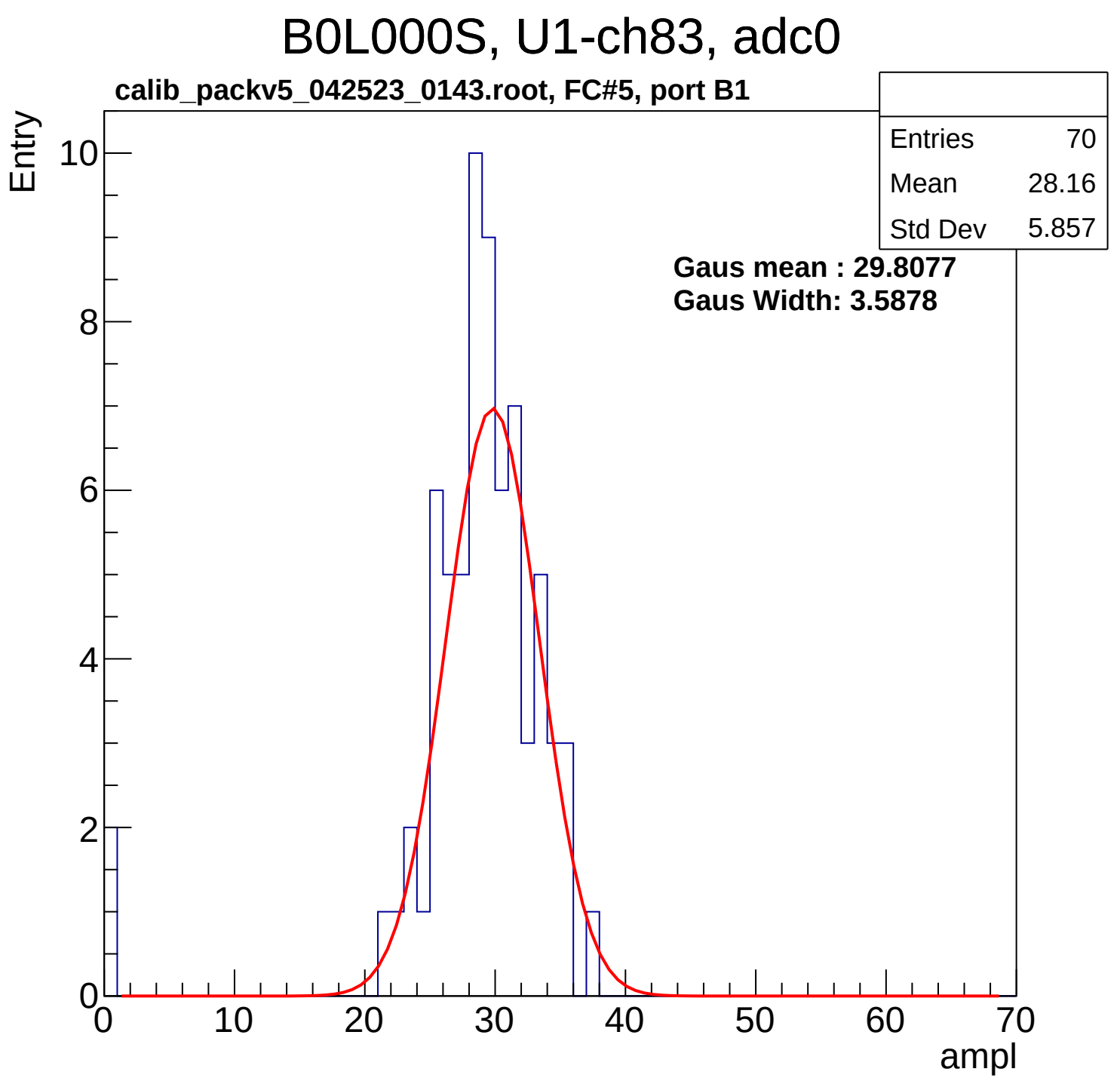

B0L000S, U1-ch82, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.95
Std Dev 4.07
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

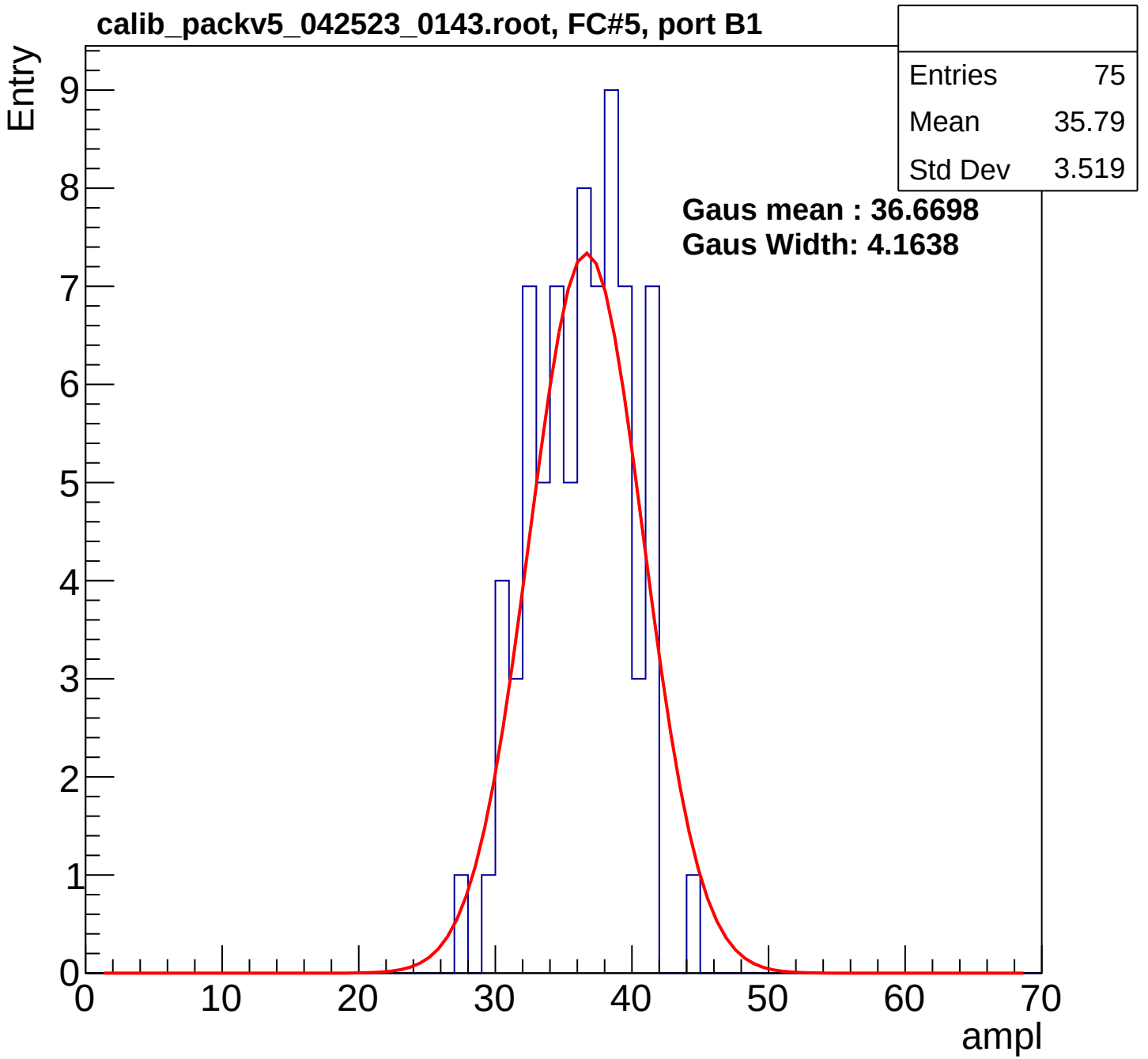

B0L000S, U1-ch82, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 54.84
Std Dev 2.795
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

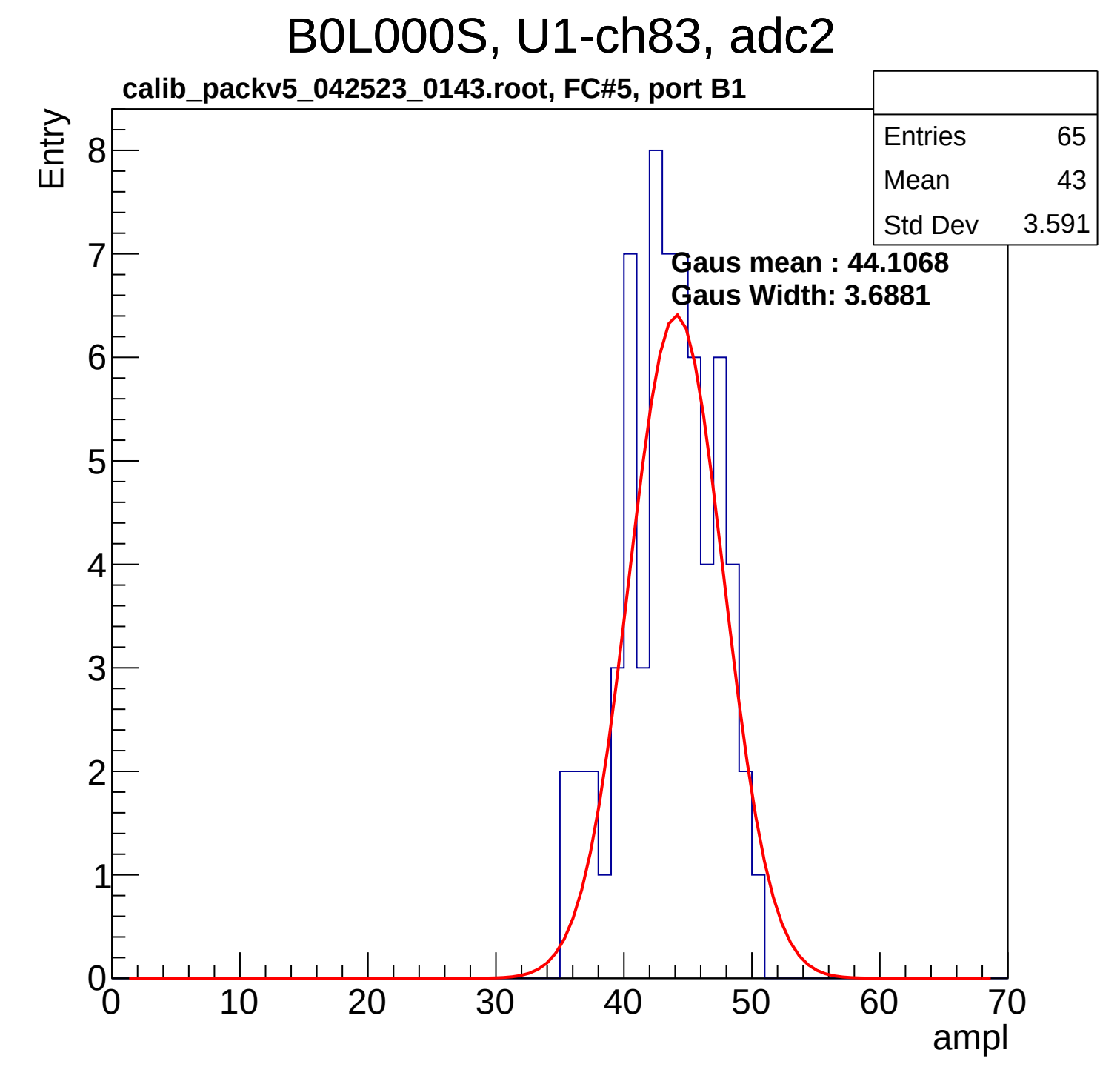

B0L000S, U1-ch82, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.67
Std Dev 8.715
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

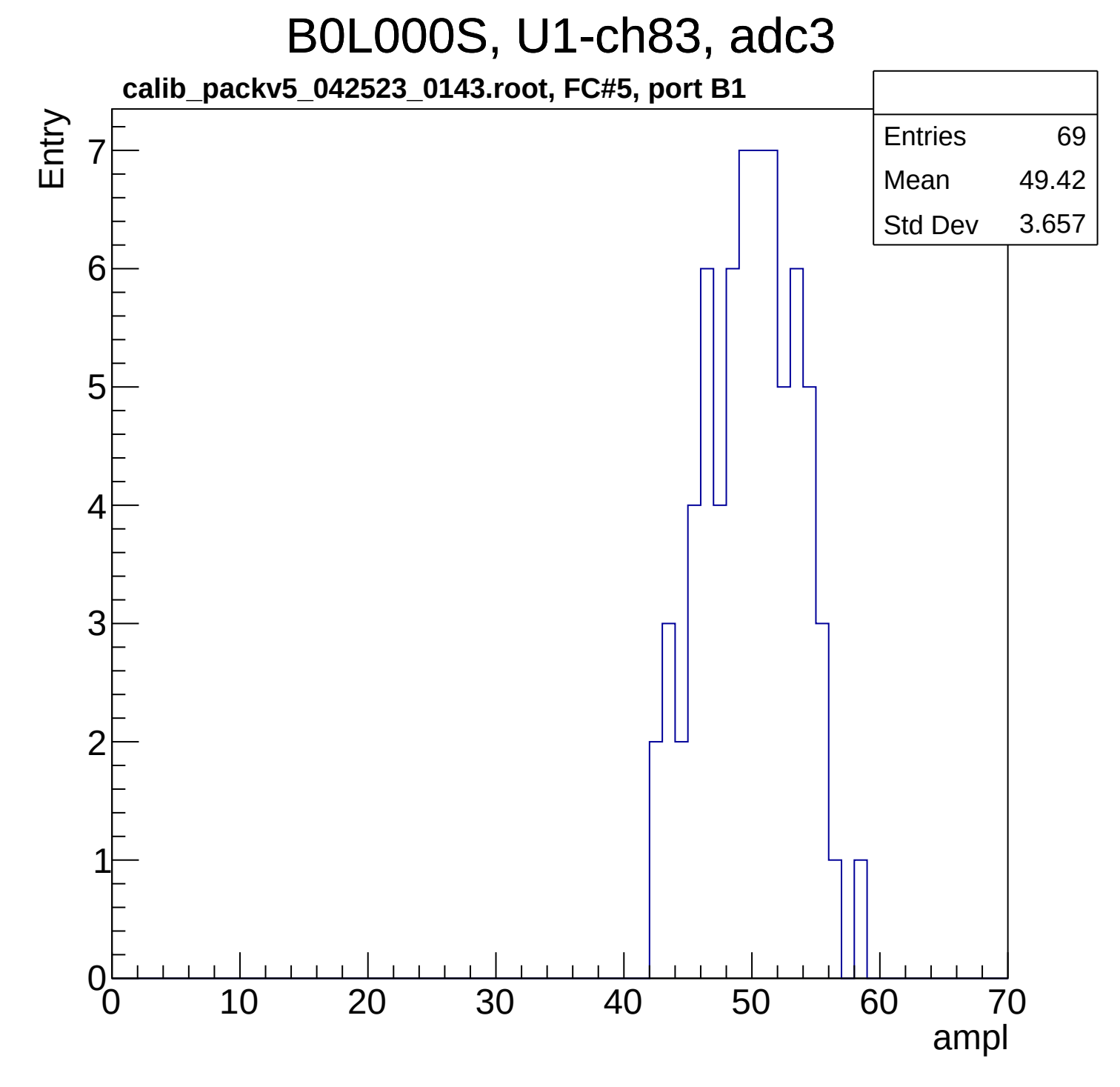

B0L000S, U1-ch82, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

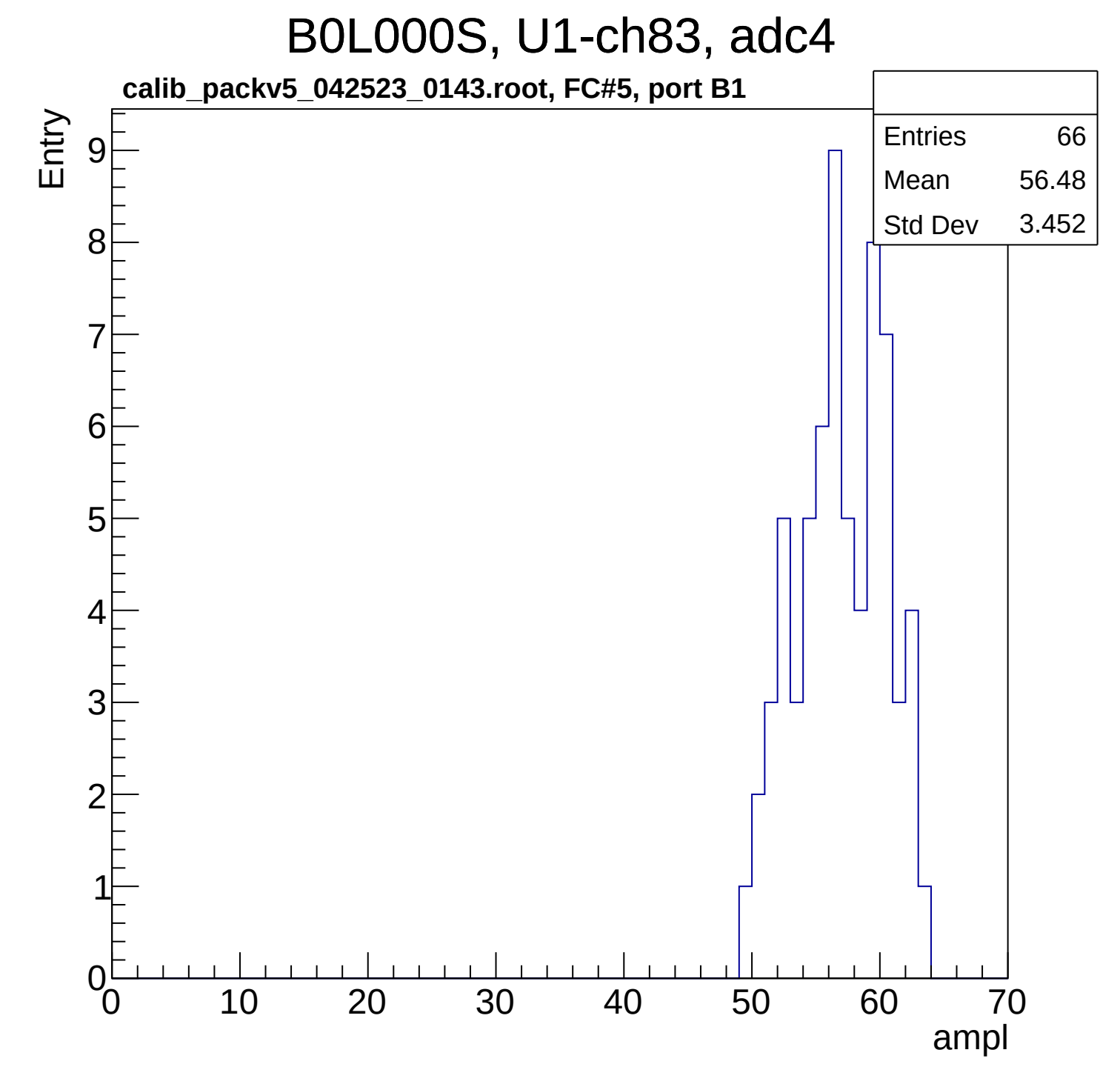

B0L000S, U1-ch82, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

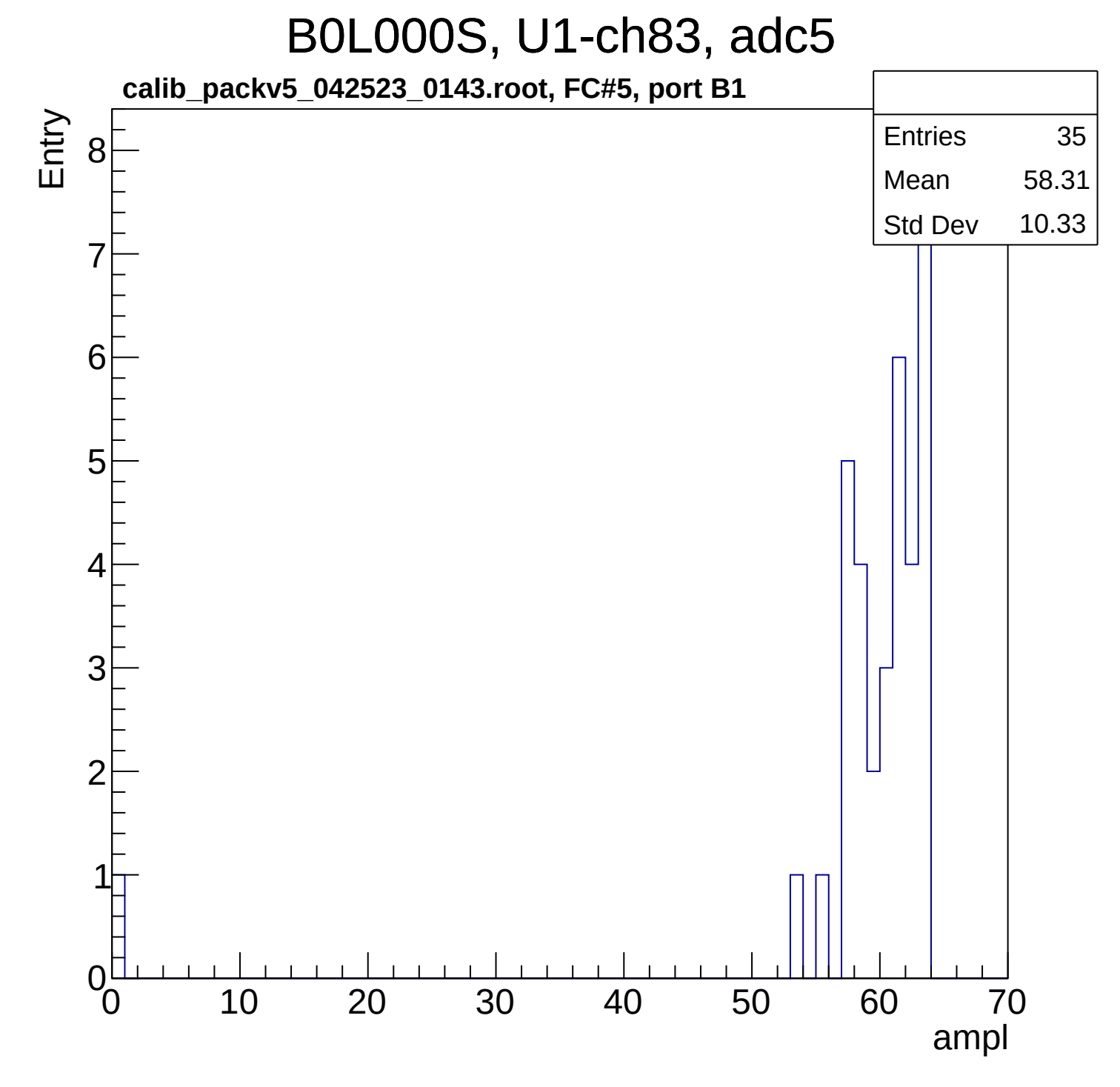

B0L000S, U1-ch83, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.16
Std Dev 5.857
Gaus mean : 29.8077
Gaus Width: 3.5878
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

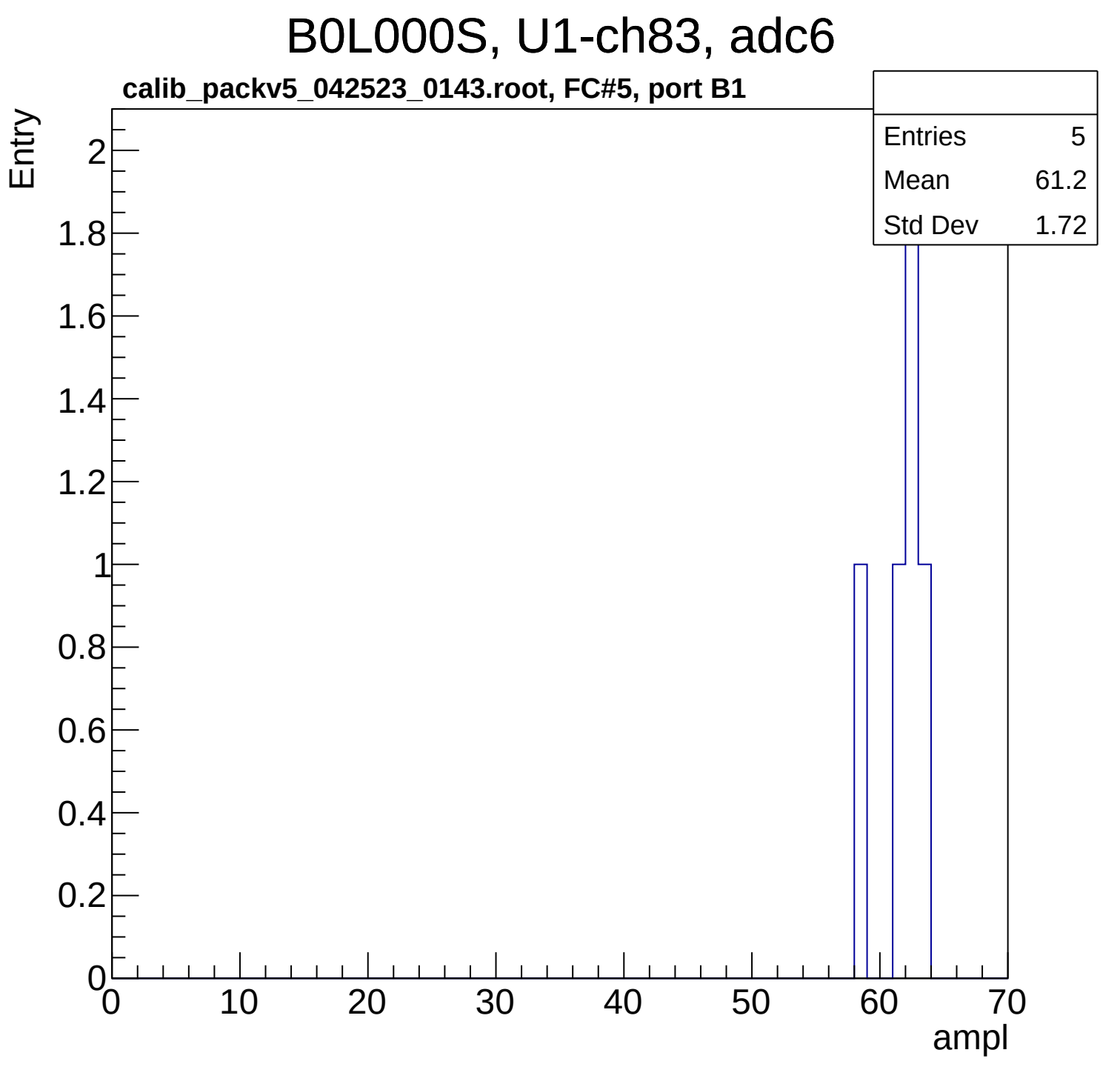

B0L000S, U1-ch83, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.79
Std Dev 3.519
Gaus mean : 36.6698
Gaus Width: 4.1638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

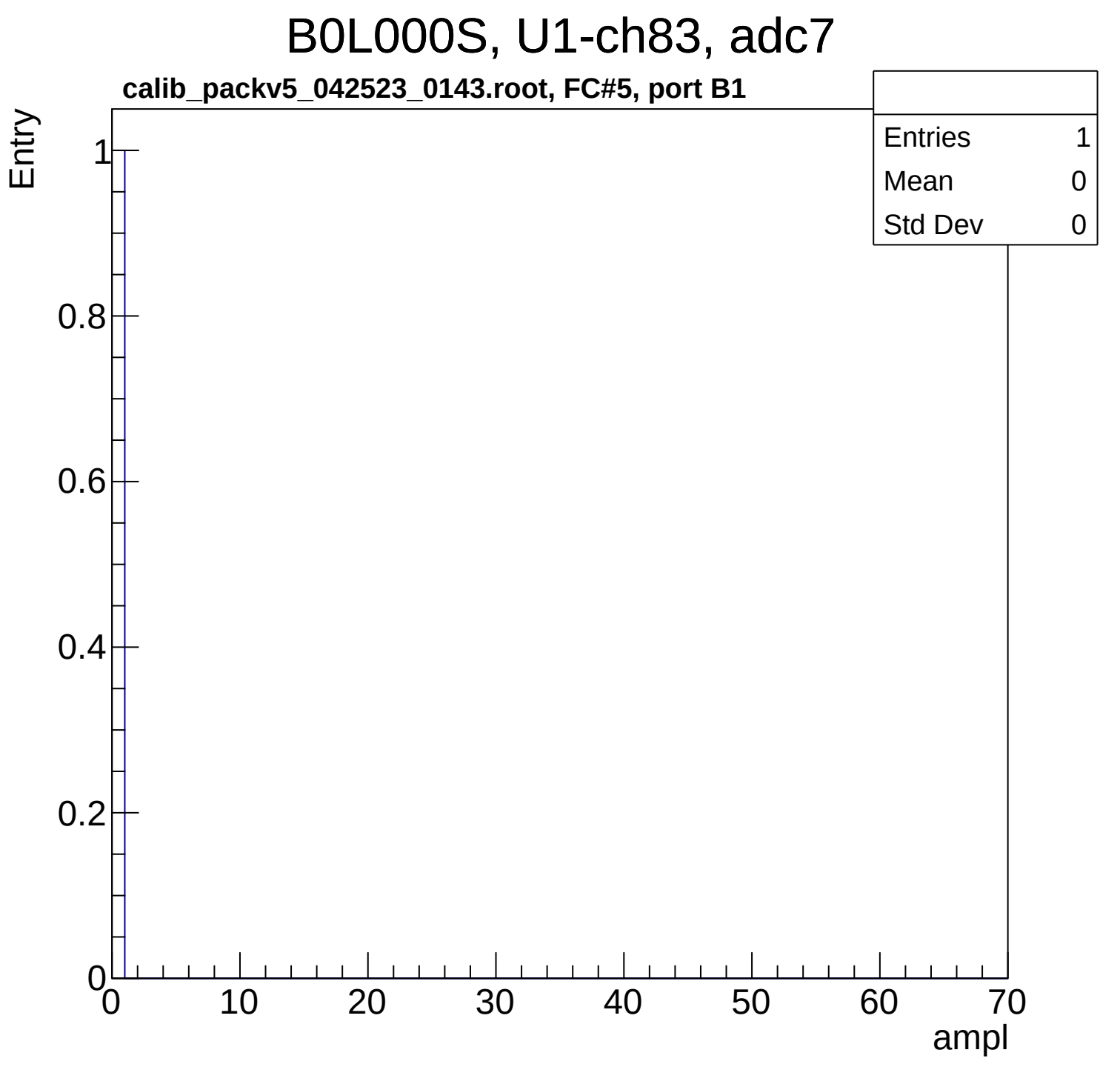

B0L000S, U1-ch83, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43
Std Dev 3.591
Gaus mean : 44.1068
Gaus Width: 3.6881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

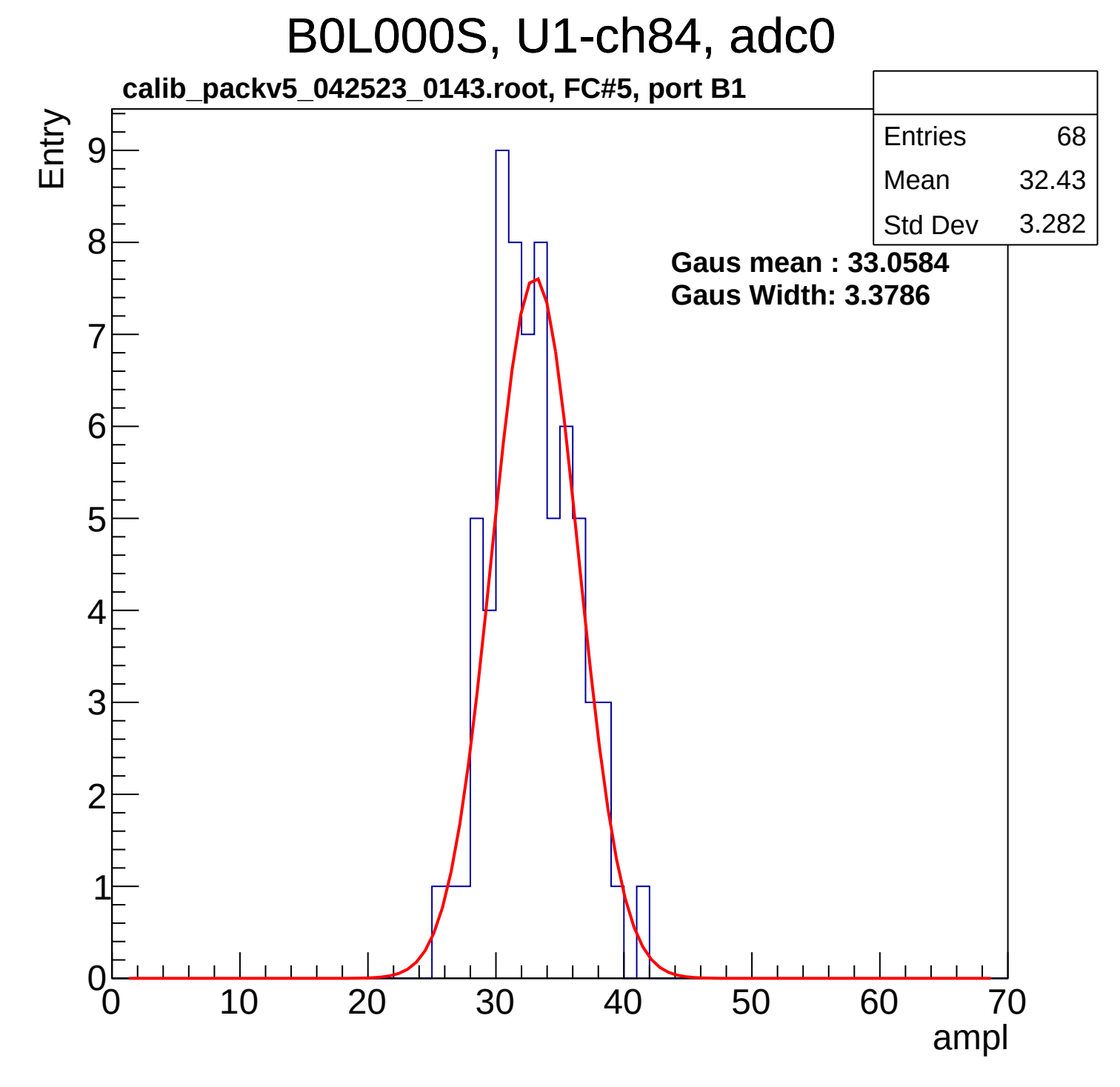

B0L000S, U1-ch83, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.42
Std Dev 3.657
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

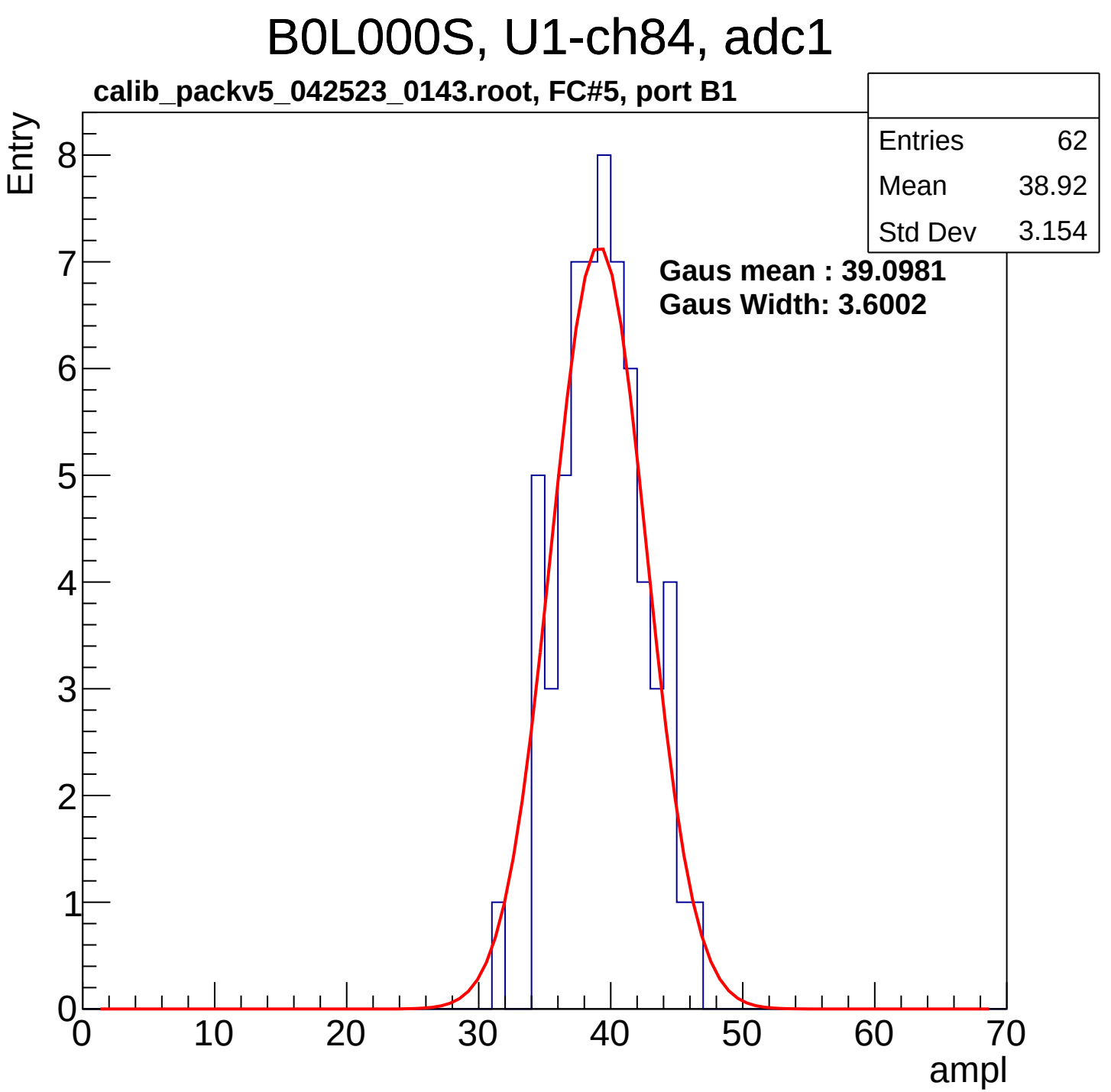

B0L000S, U1-ch83, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 56.48
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

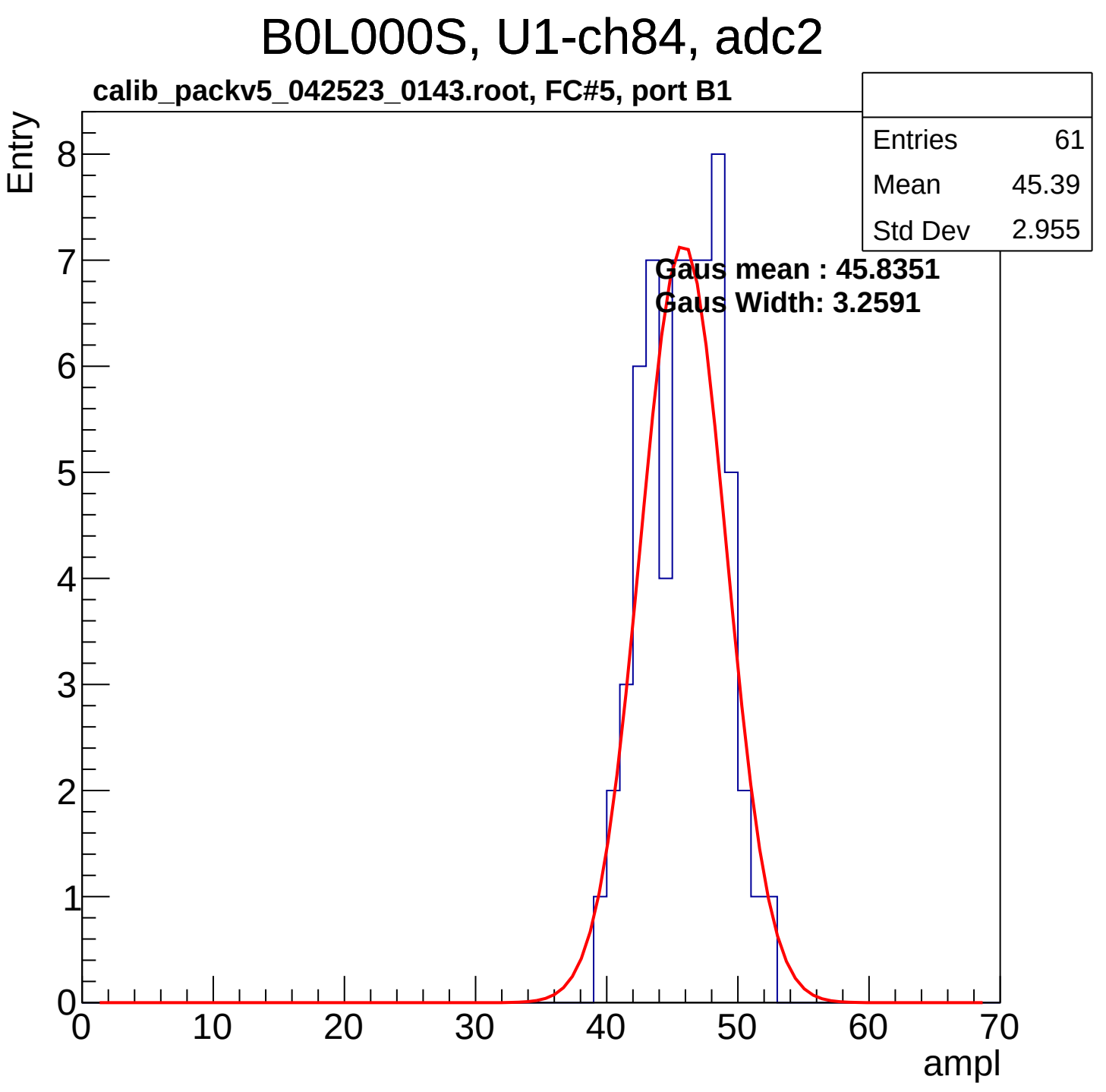

B0L000S, U1-ch83, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.31
Std Dev 10.33
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

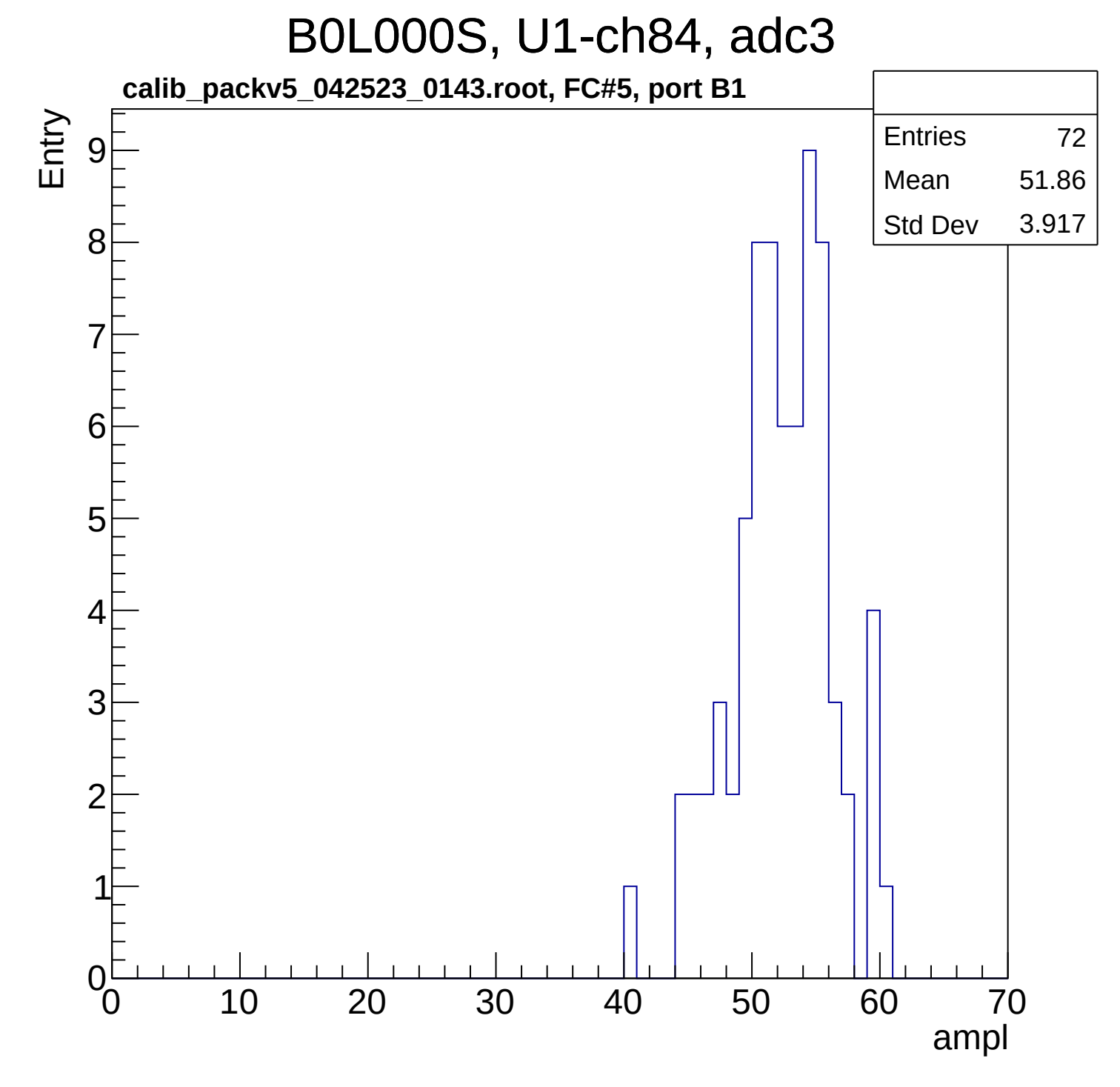

B0L000S, U1-ch83, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.2
Std Dev 1.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

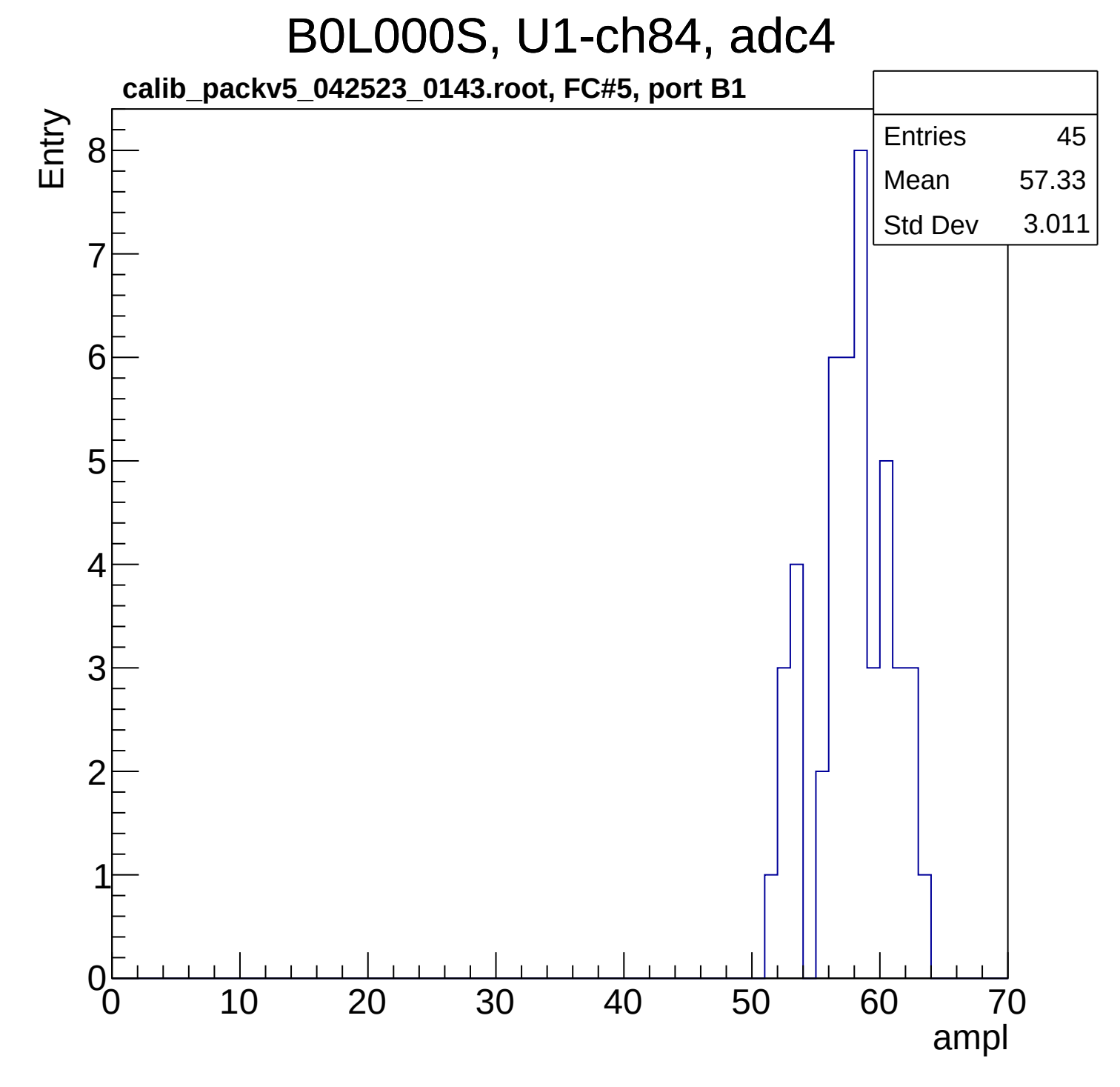

B0L000S, U1-ch83, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

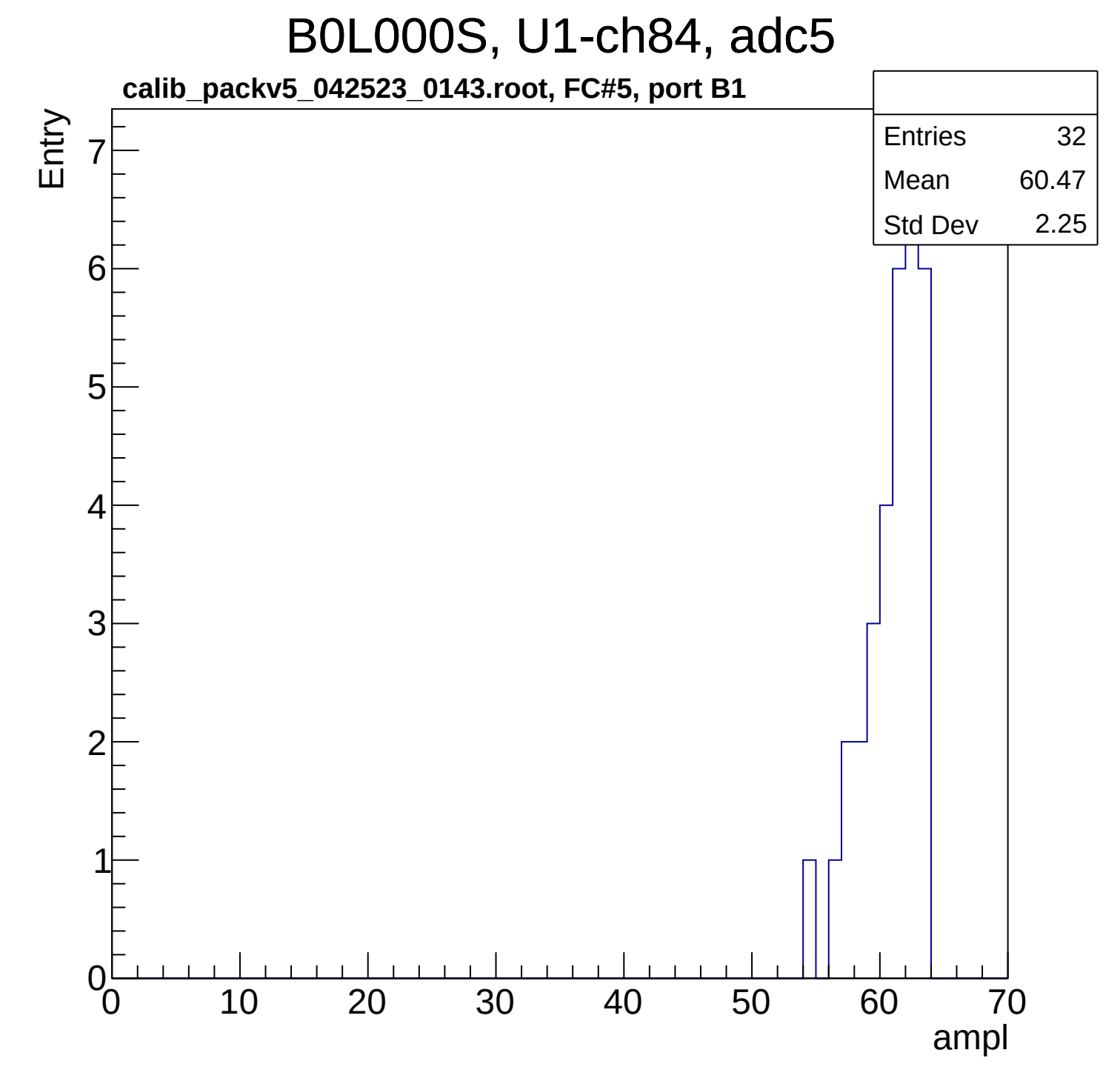

B0L000S, U1-ch84, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 32.43
Std Dev 3.282
Gaus mean : 33.0584
Gaus Width: 3.3786
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

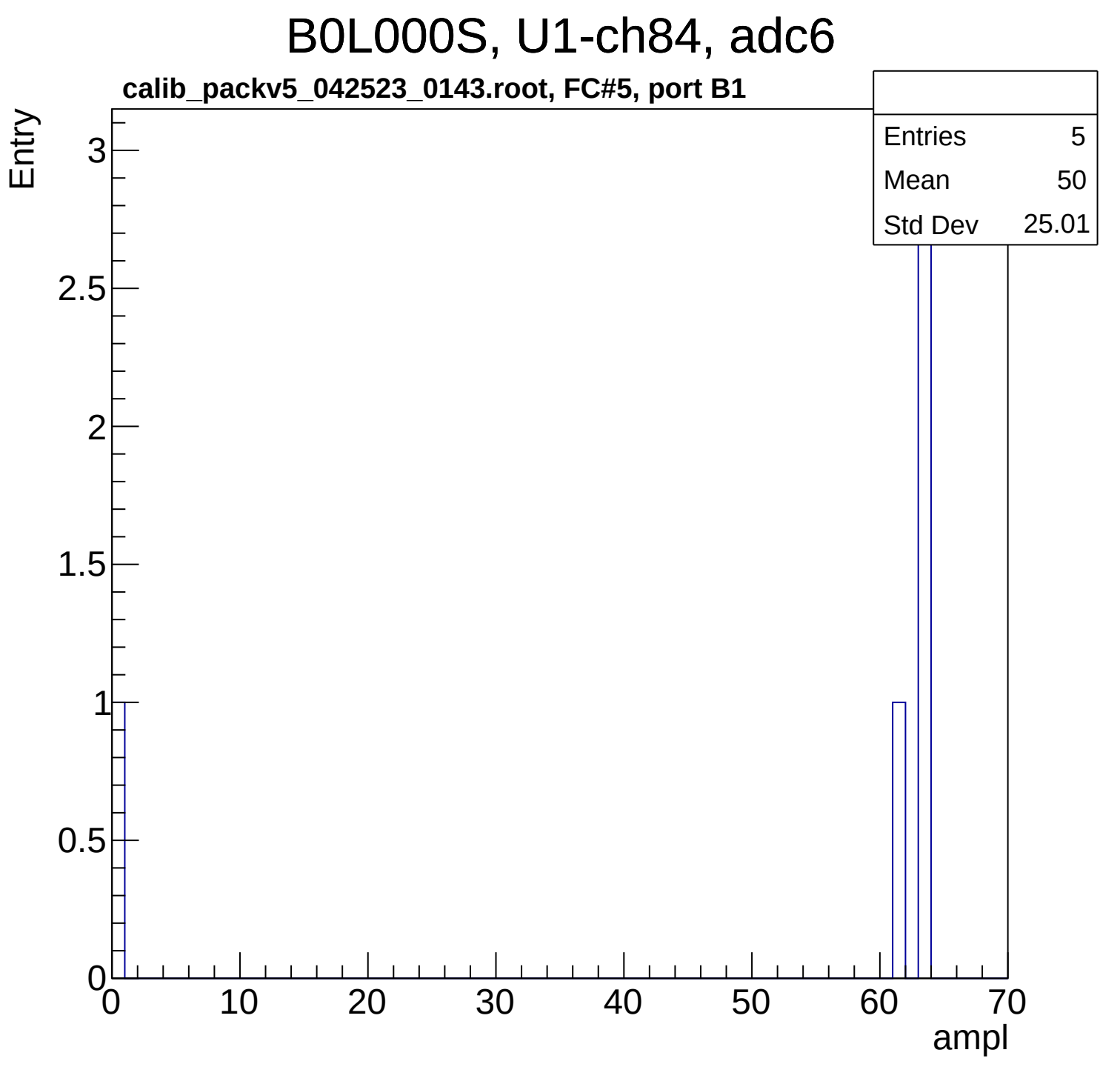

B0L000S, U1-ch84, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 38.92
Std Dev 3.154
Gaus mean : 39.0981
Gaus Width: 3.6002
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

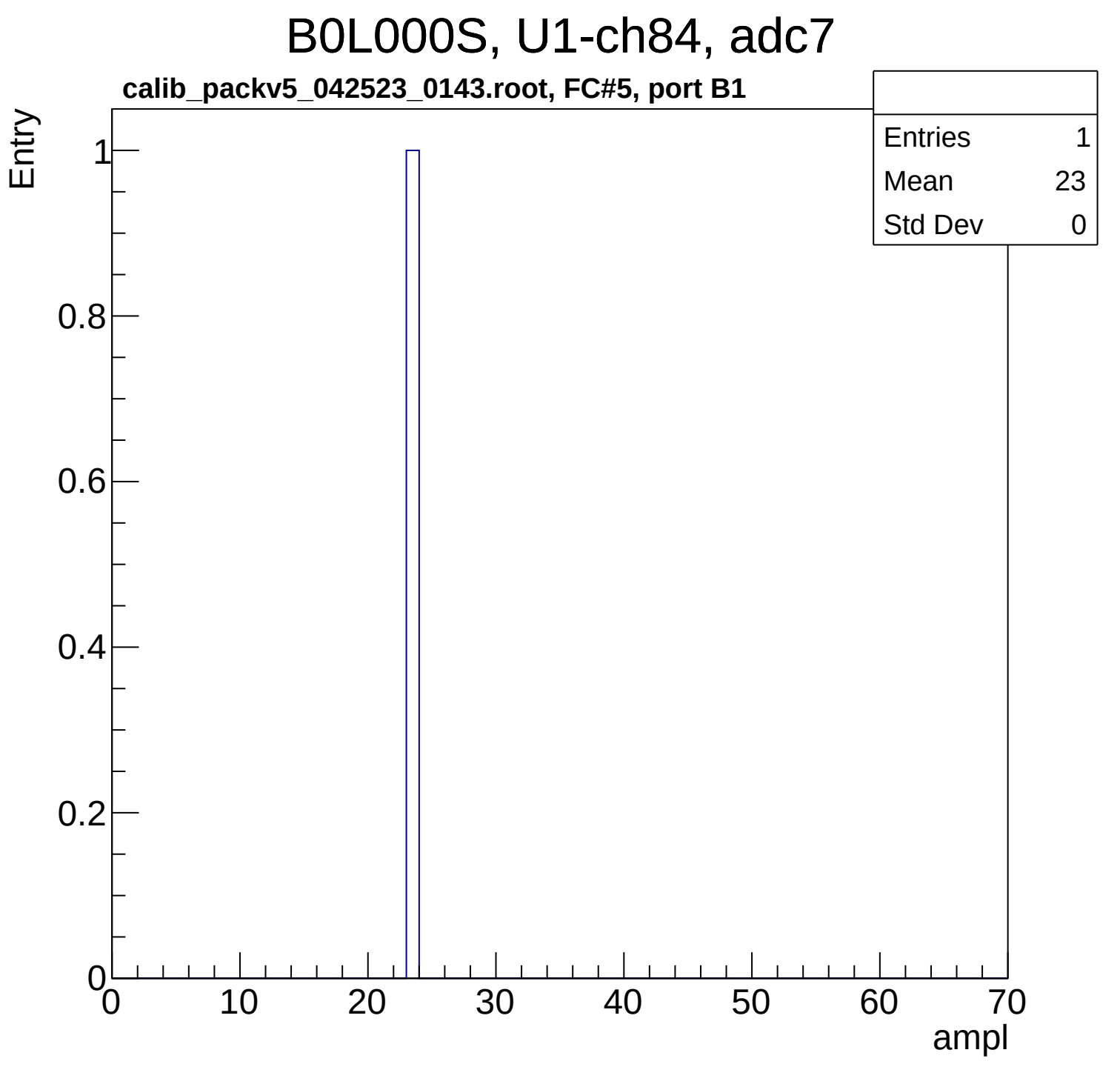

B0L000S, U1-ch84, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 45.39
Std Dev 2.955
Gaus mean : 45.8351
Gaus Width: 3.2591
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

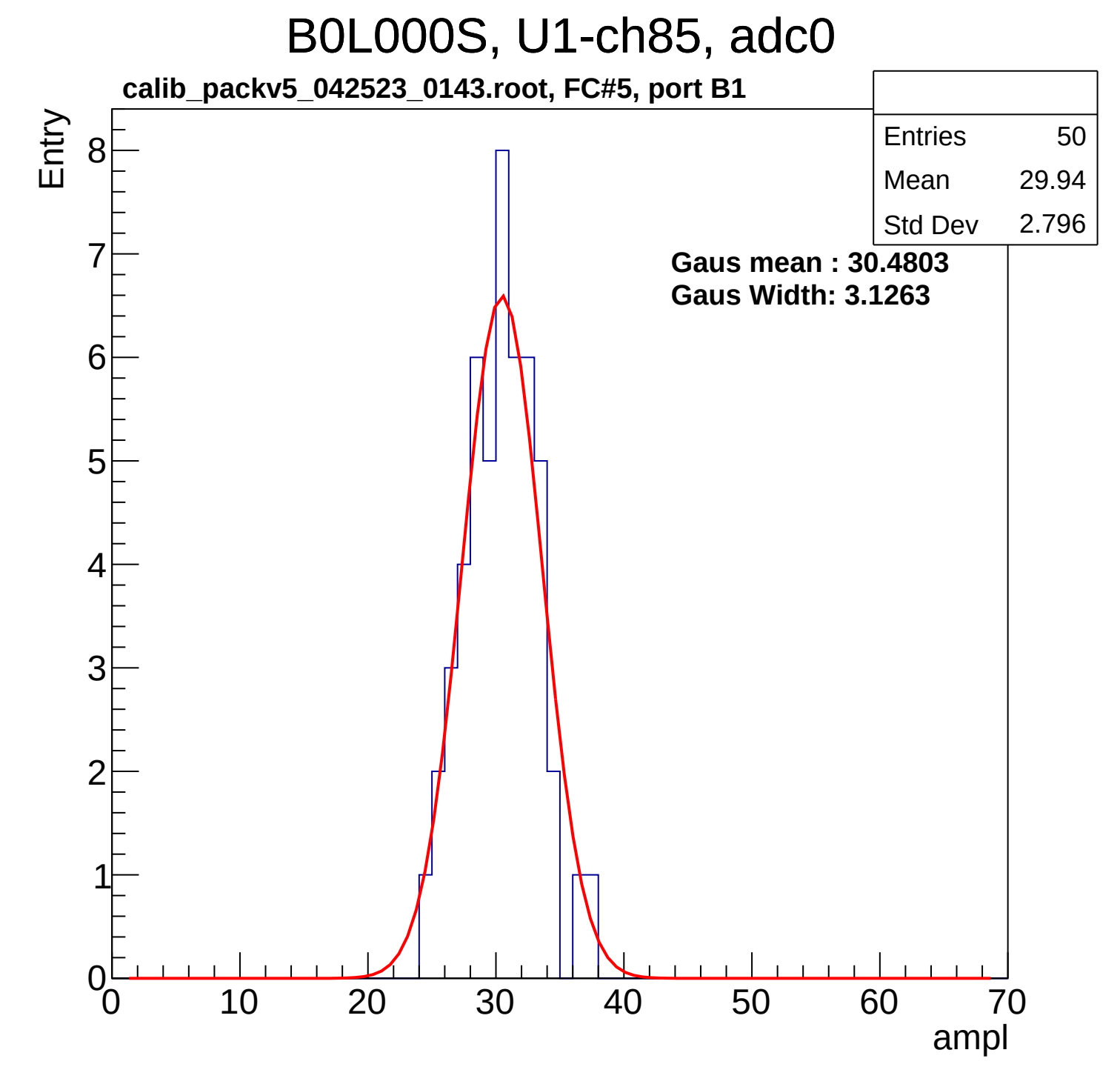

B0L000S, U1-ch84, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 51.86
Std Dev 3.917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

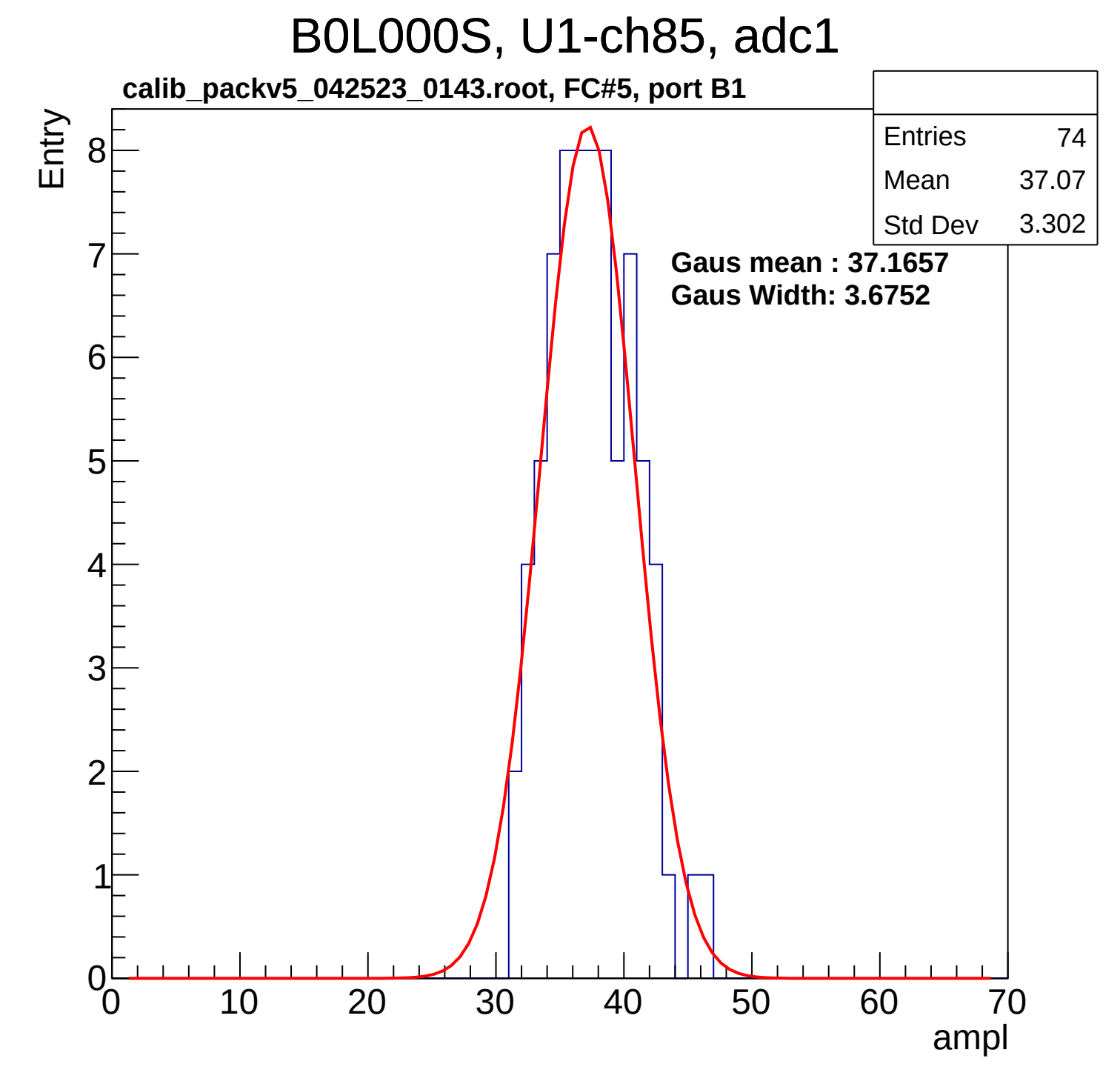

B0L000S, U1-ch84, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 57.33
Std Dev 3.011
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch84, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 60.47
Std Dev 2.25
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch84, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 50
Std Dev 25.01
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch84, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch85, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 29.94
Std Dev 2.796
Gaus mean : 30.4803
Gaus Width: 3.1263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch85, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 37.07
Std Dev 3.302
Gaus mean : 37.1657
Gaus Width: 3.6752
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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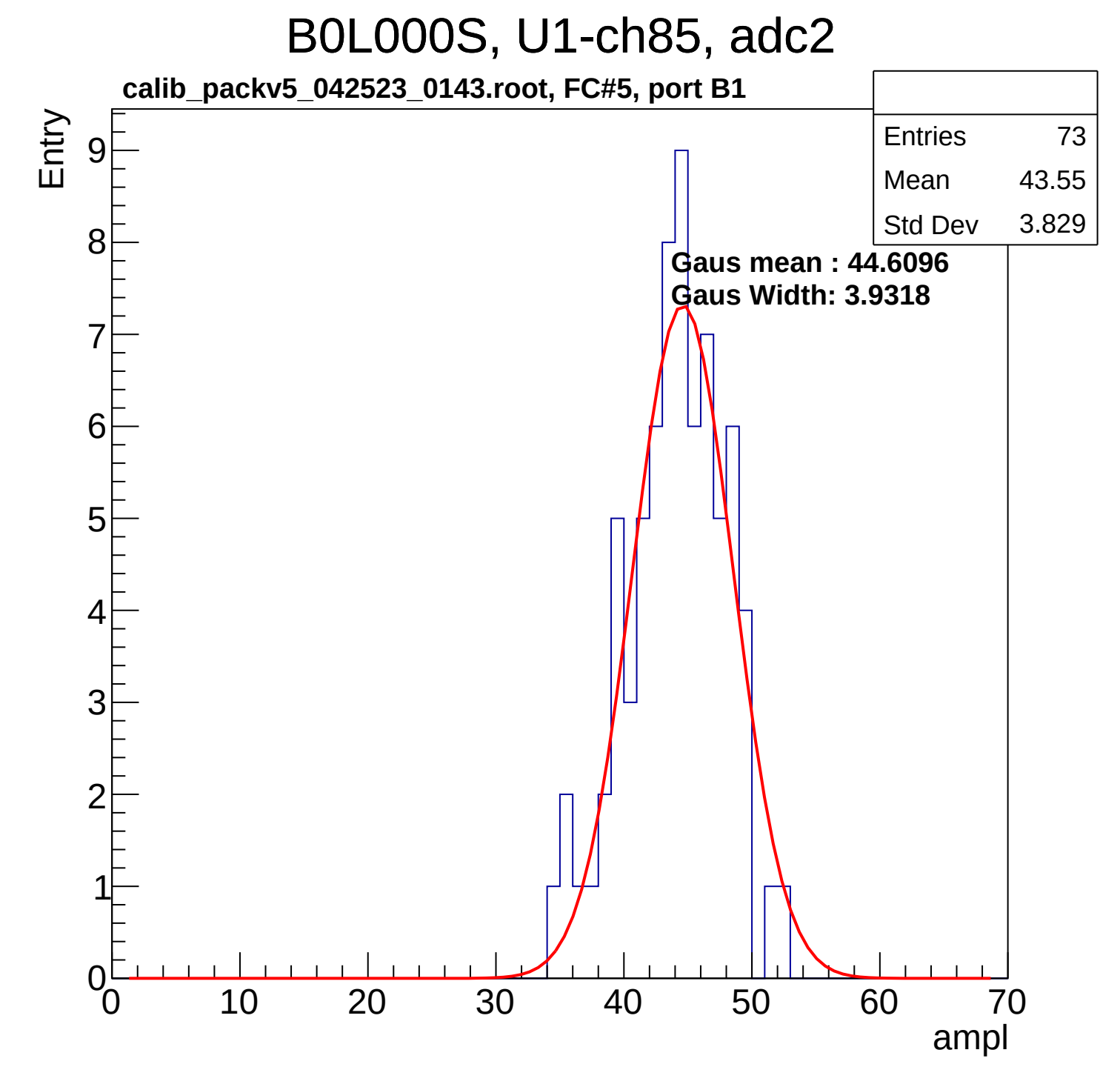
B0L000S, U1-ch85, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.55
Std Dev 3.829
Gaus mean : 44.6096
Gaus Width: 3.9318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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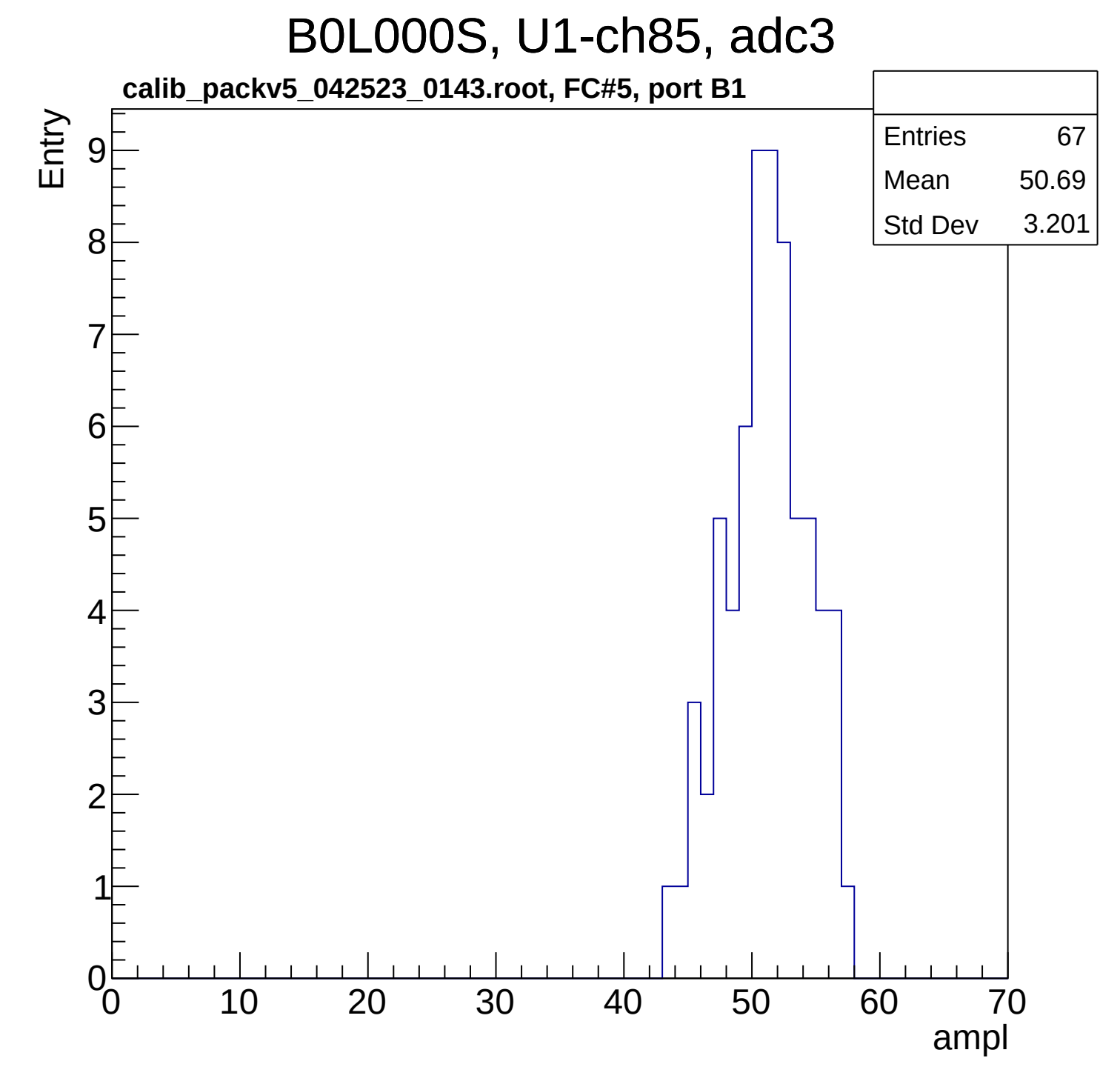
B0L000S, U1-ch85, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.69
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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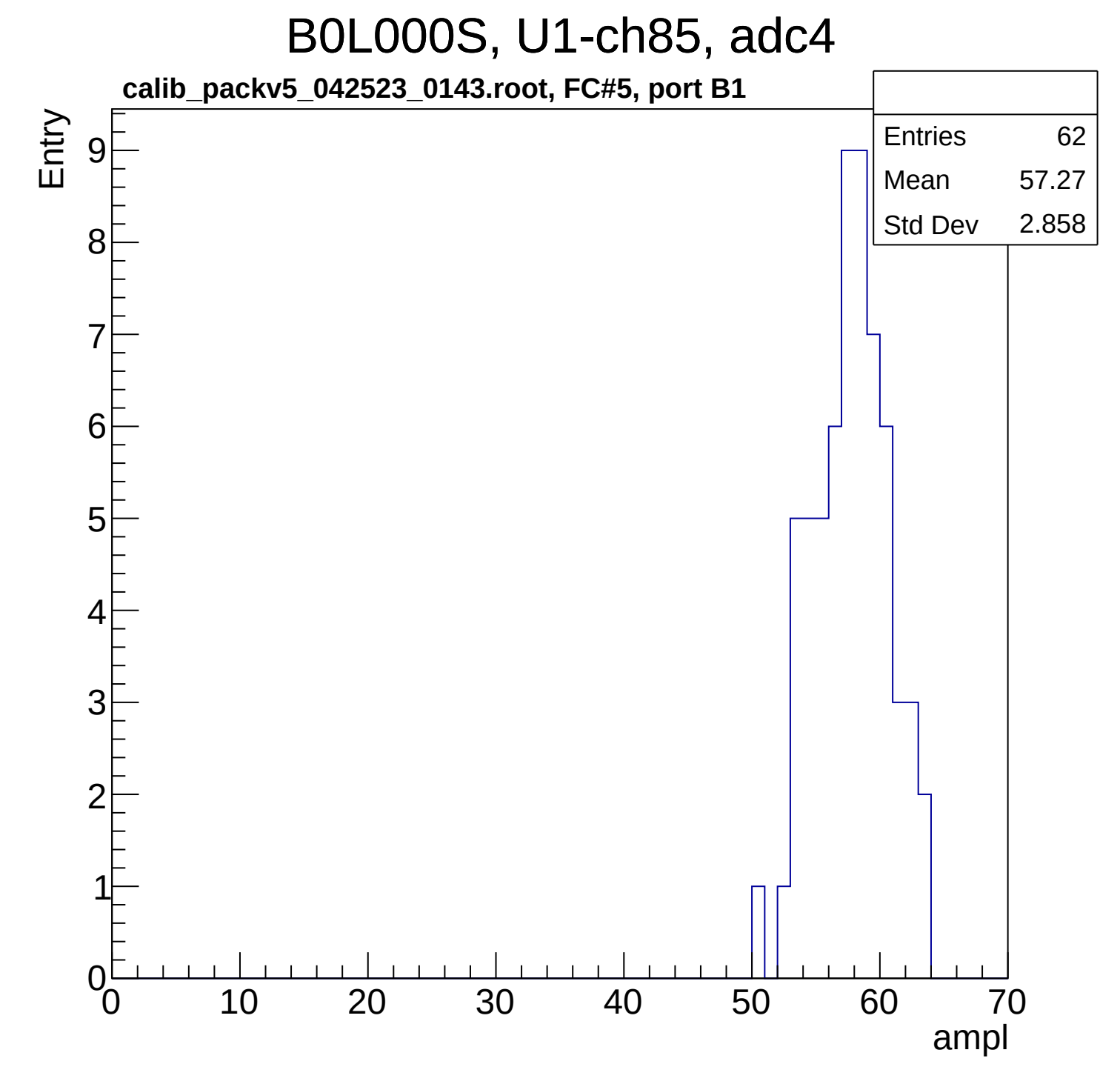
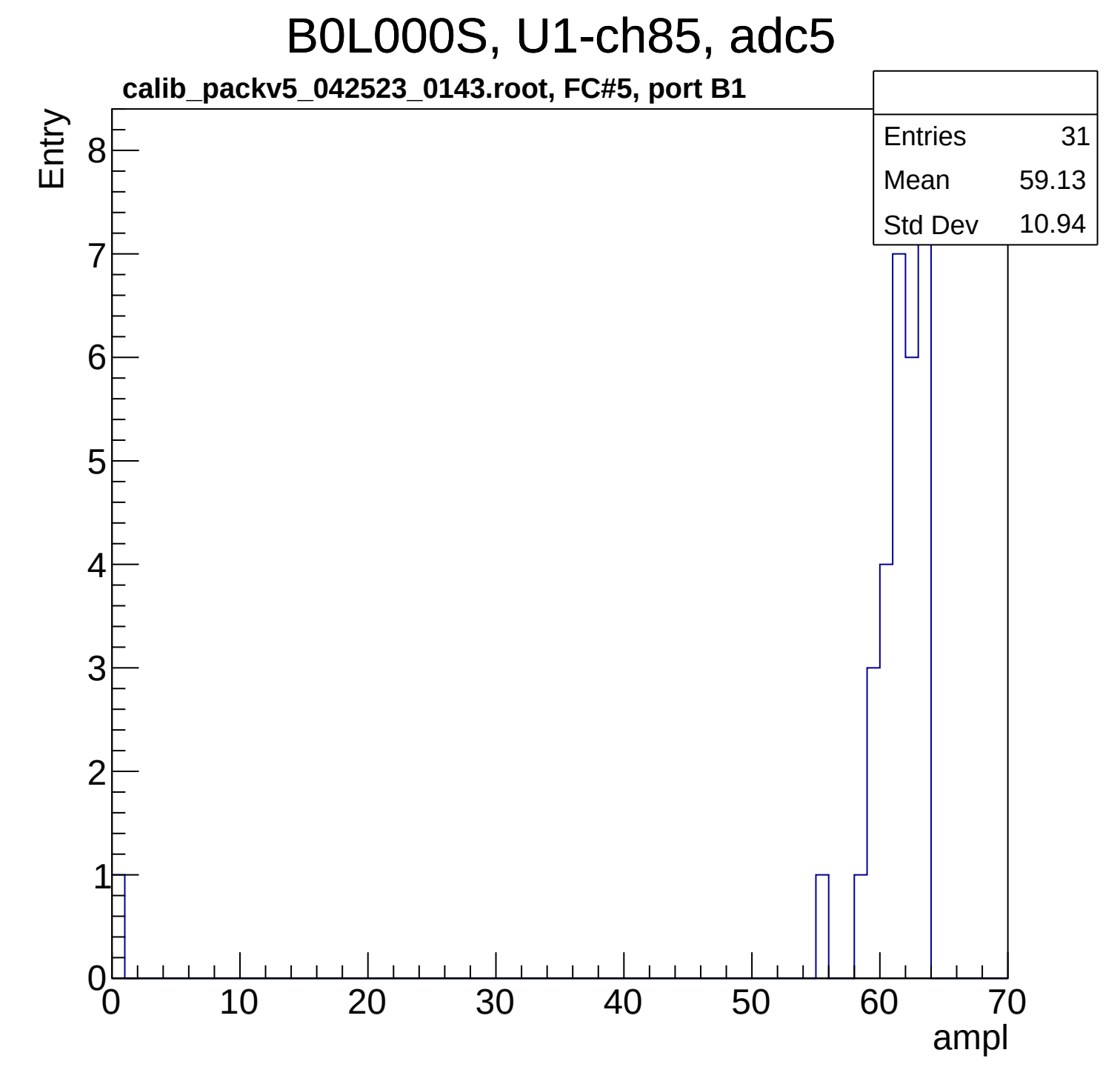
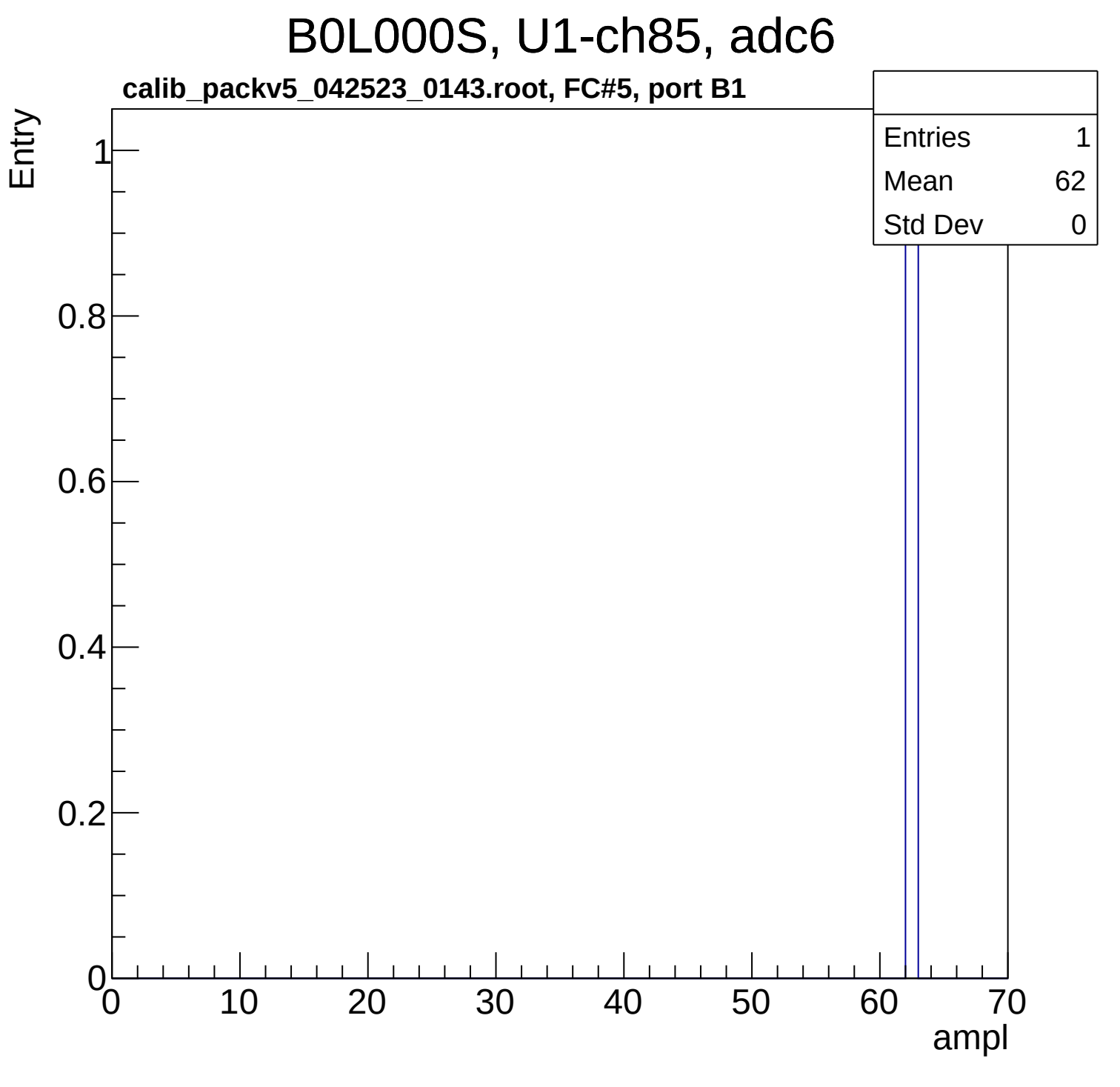
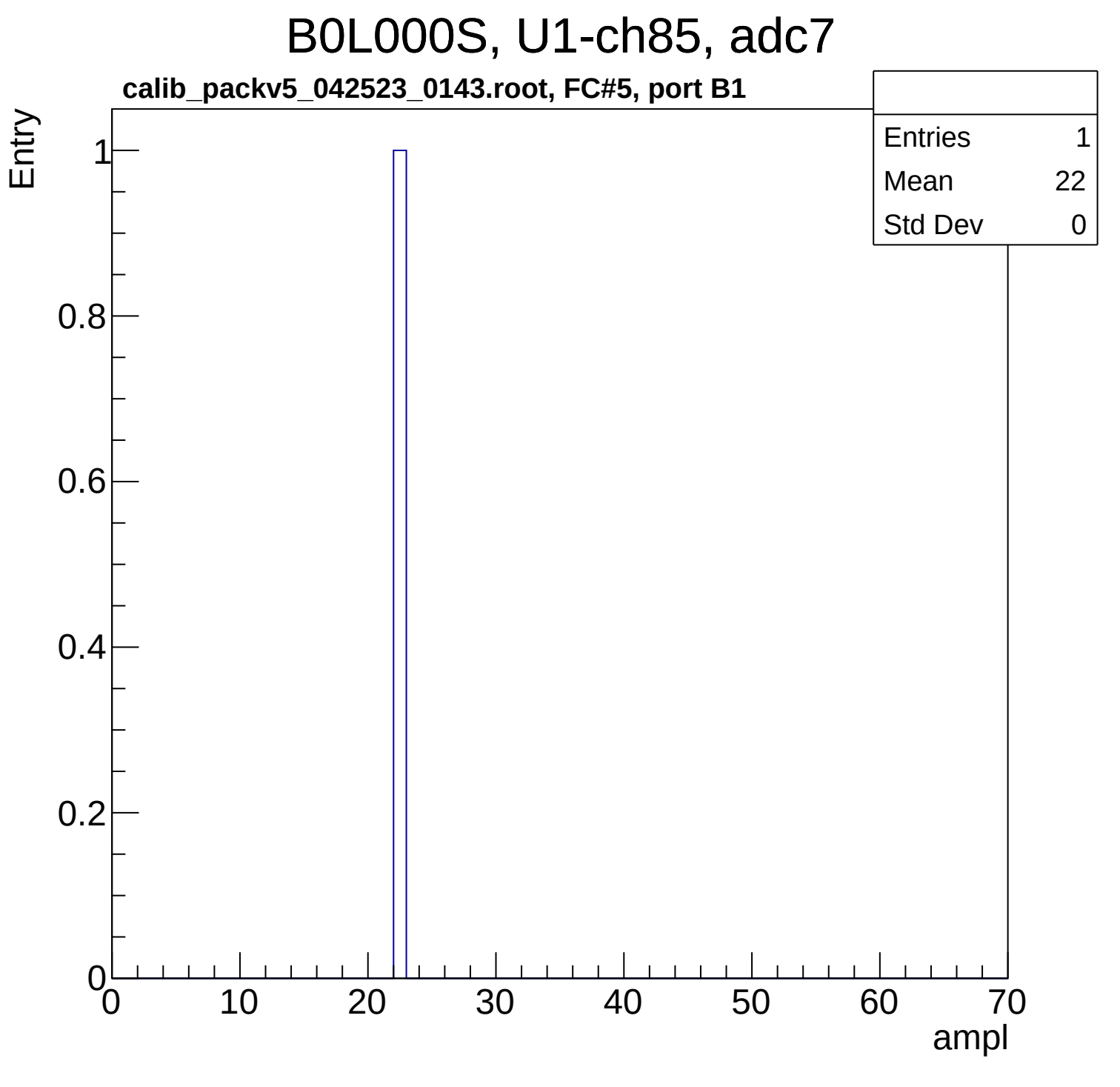

B0L000S, U1-ch85, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 57.27
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch85, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 59.13
Std Dev 10.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch85, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch85, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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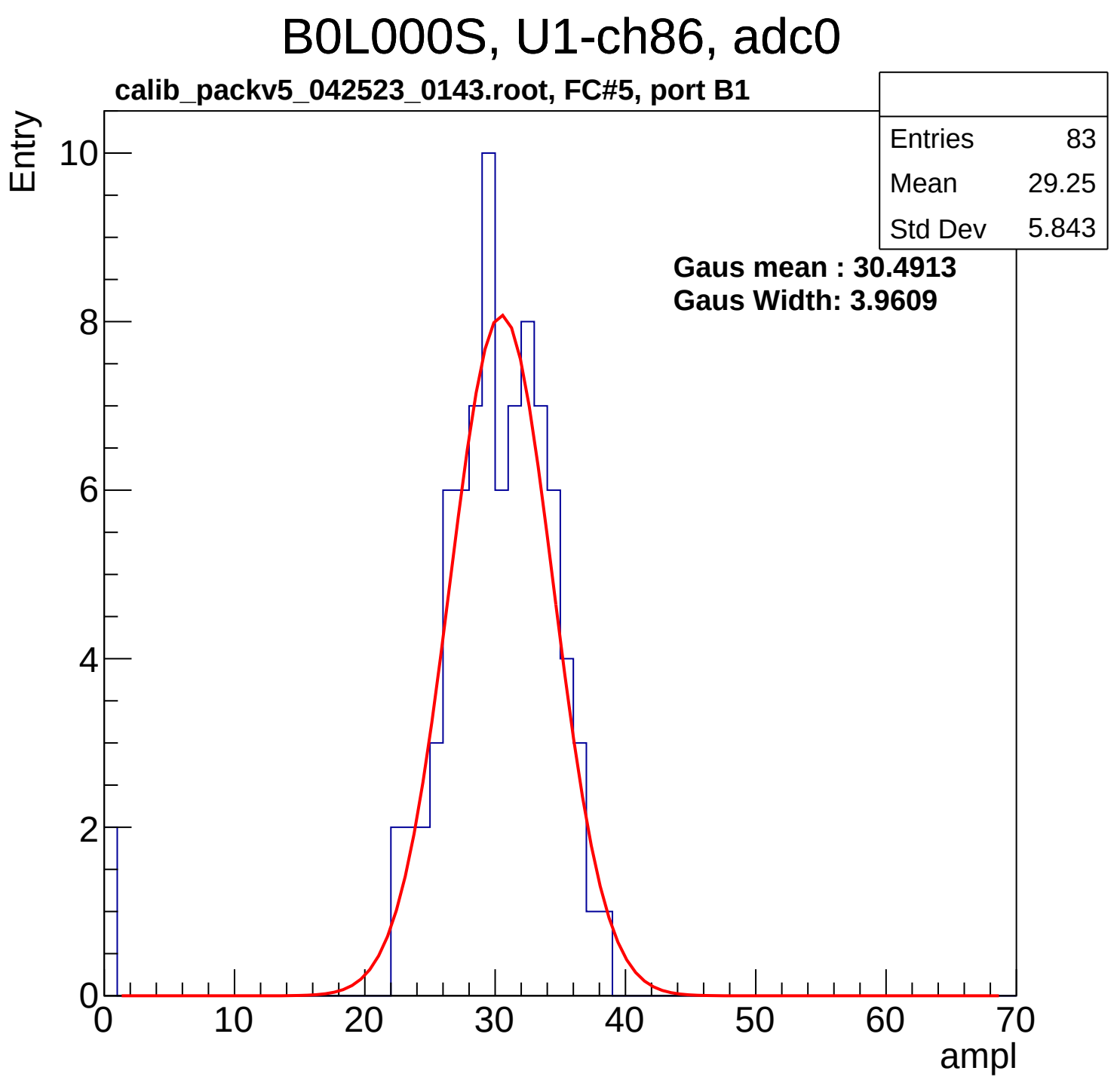
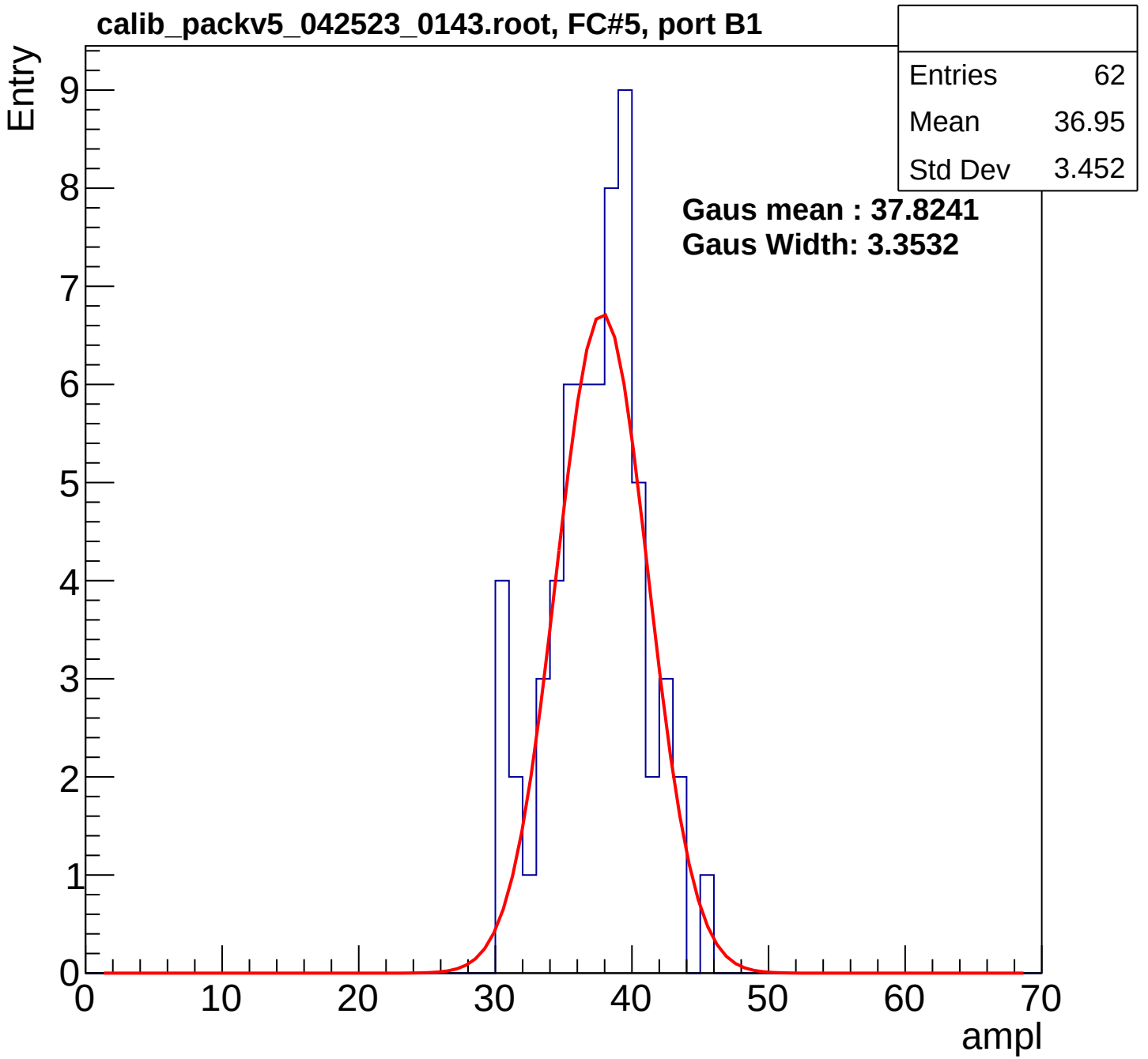
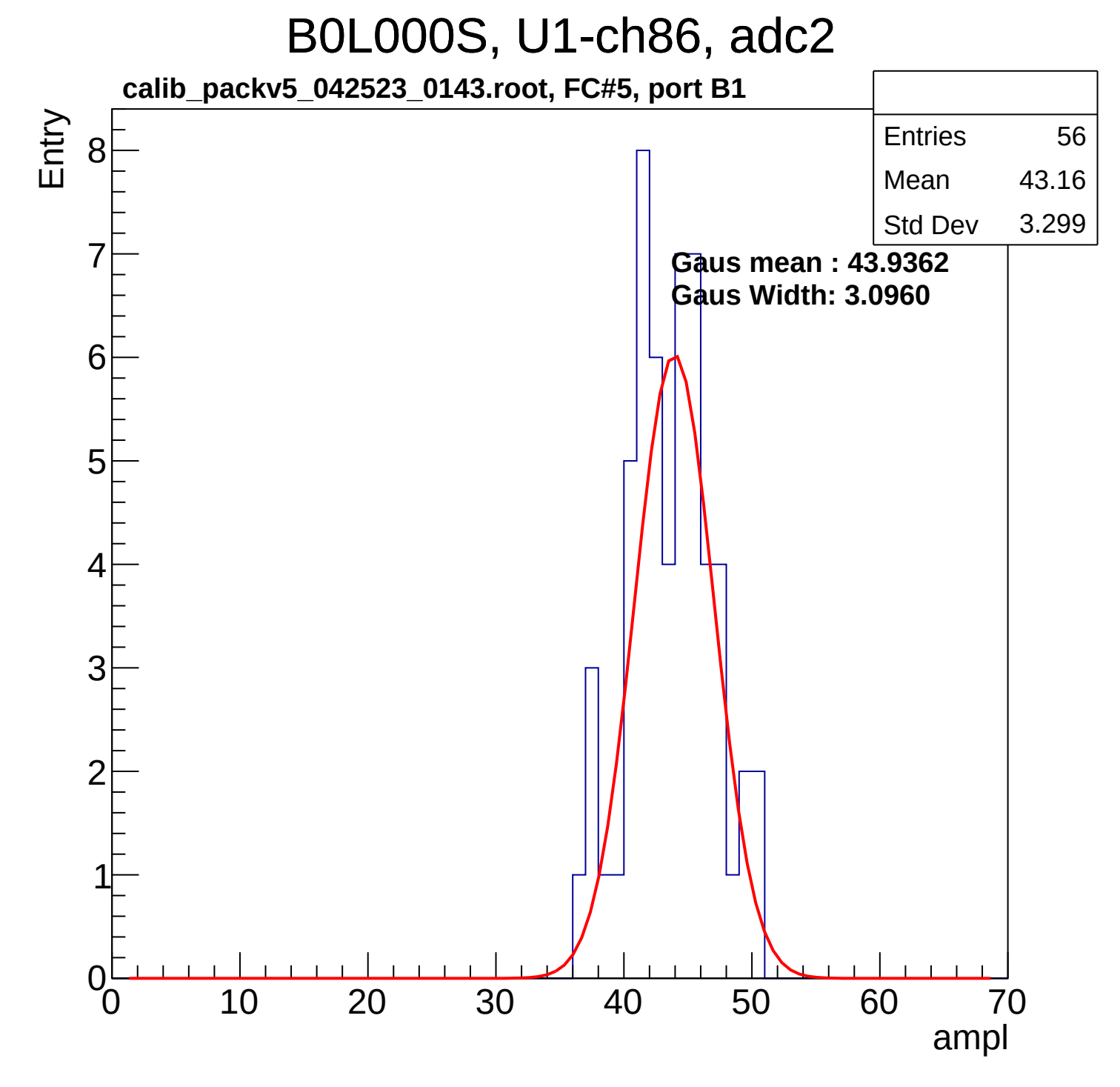
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch86, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 29.25
Std Dev 5.843
Gaus mean : 30.4913
Gaus Width: 3.9609
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch86, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.95
Std Dev 3.452
Gaus mean : 37.8241
Gaus Width: 3.3532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch86, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.16
Std Dev 3.299
Gaus mean : 43.9362
Gaus Width: 3.0960
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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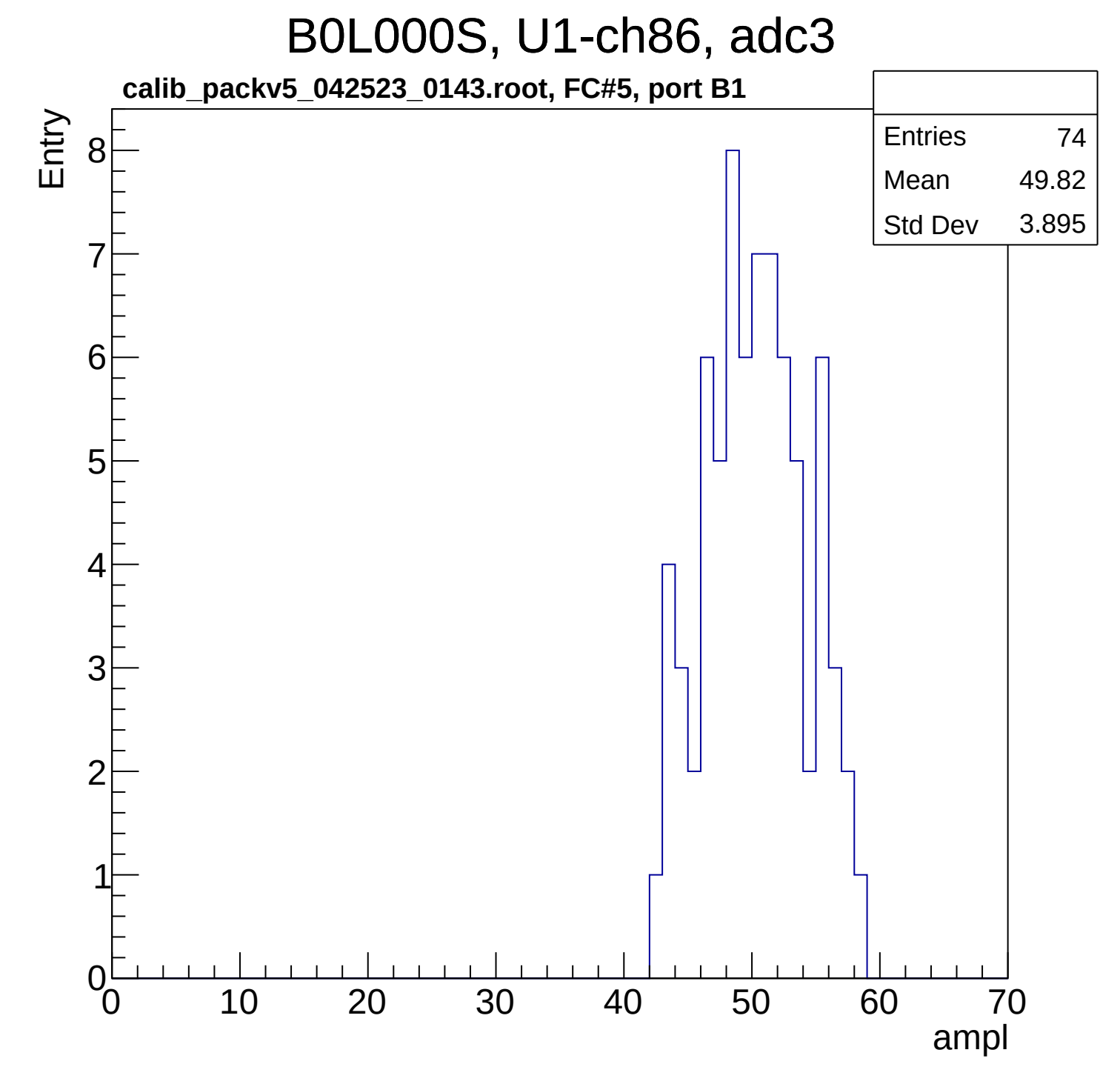
B0L000S, U1-ch86, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 49.82
Std Dev 3.895
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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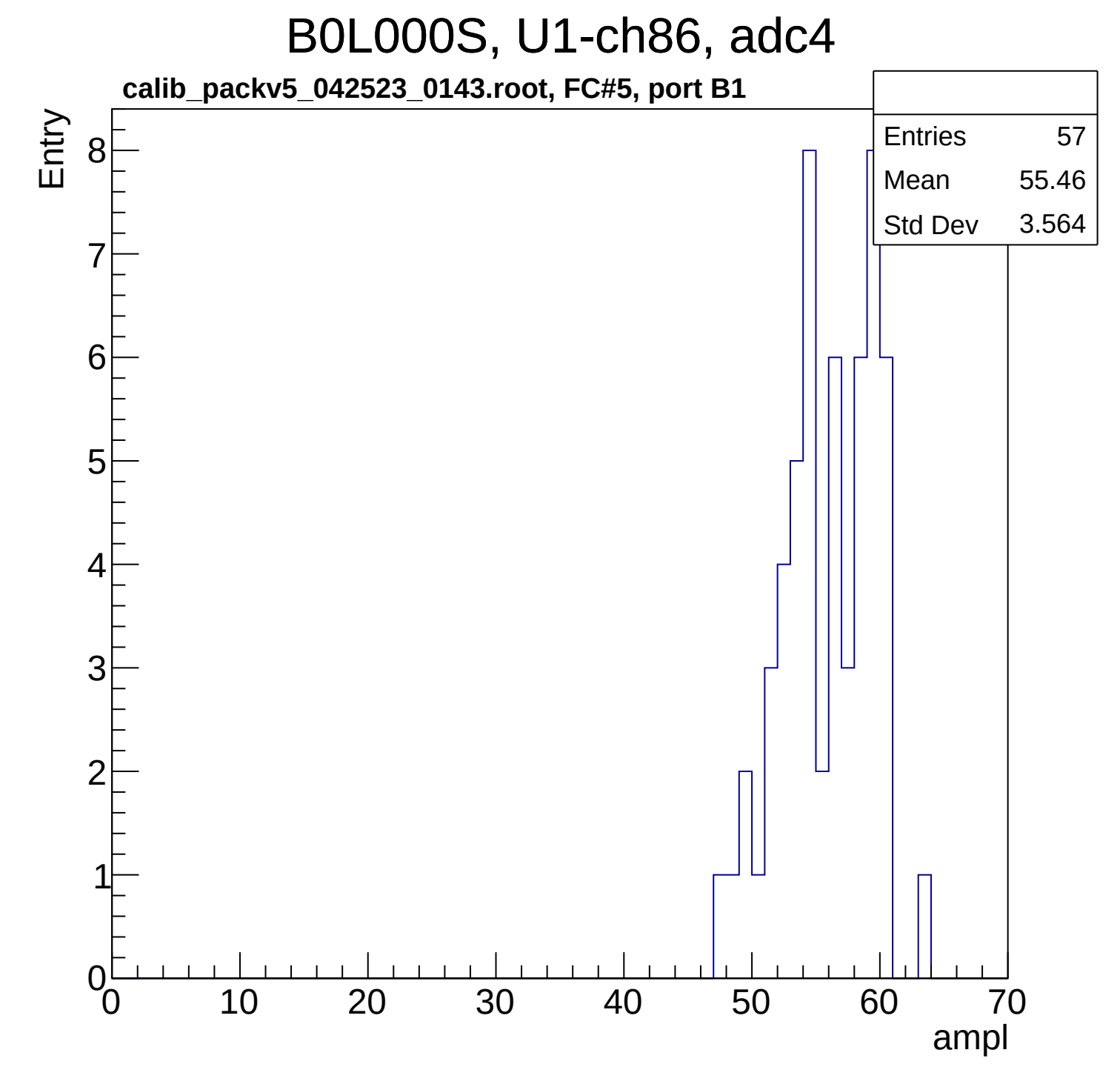
B0L000S, U1-ch86, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.46
Std Dev 3.564
Entry
ampl
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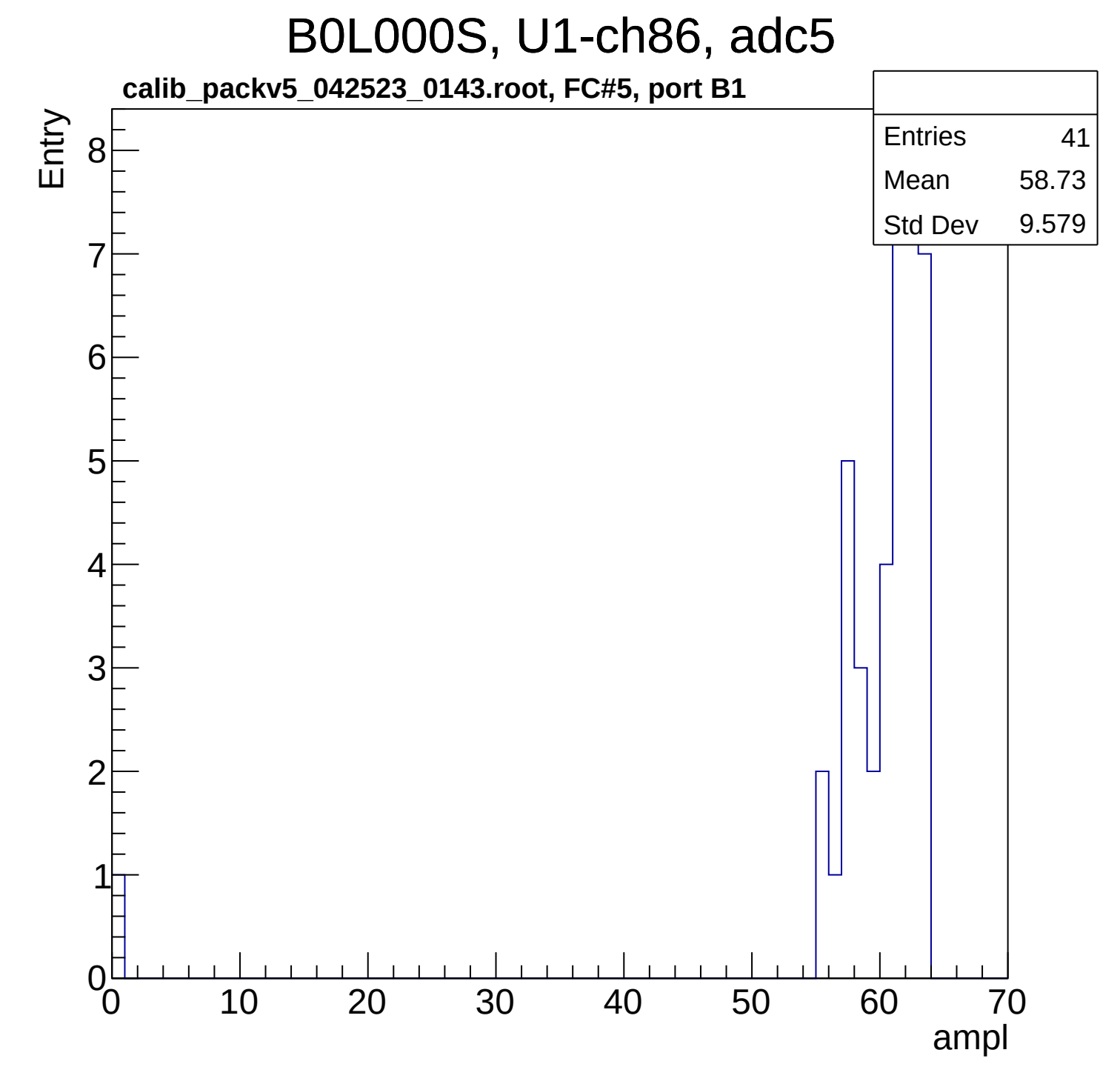
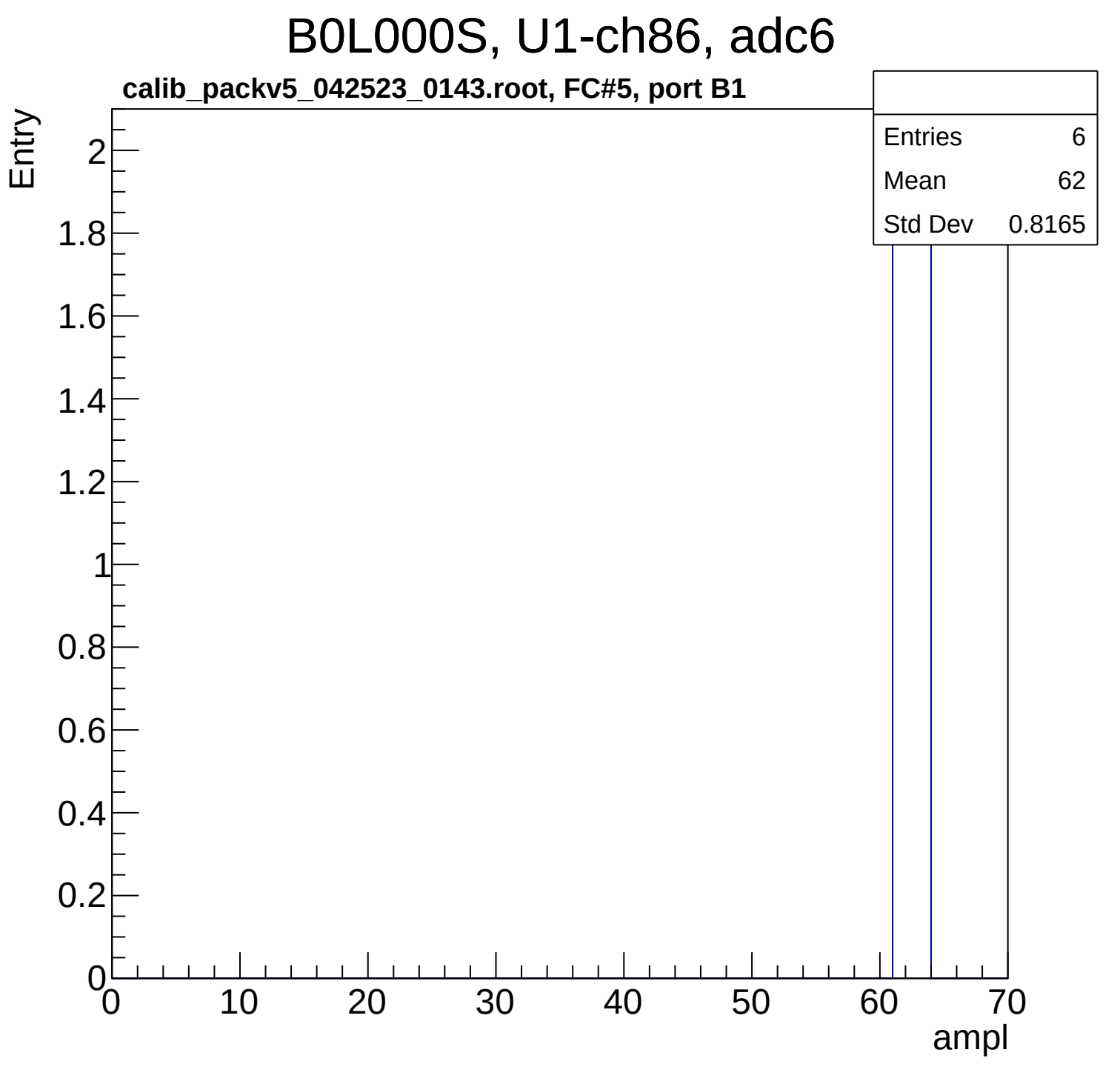
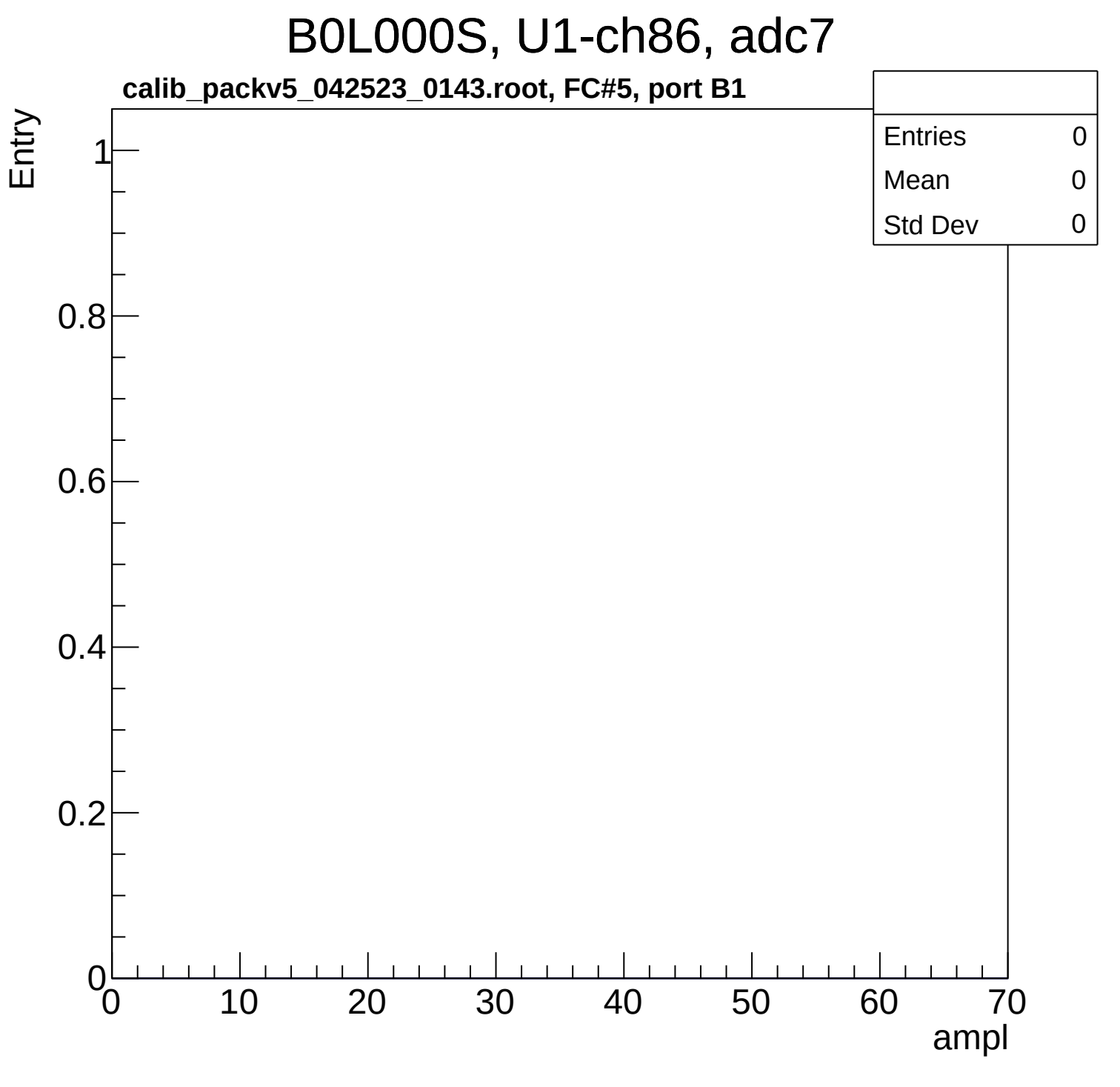
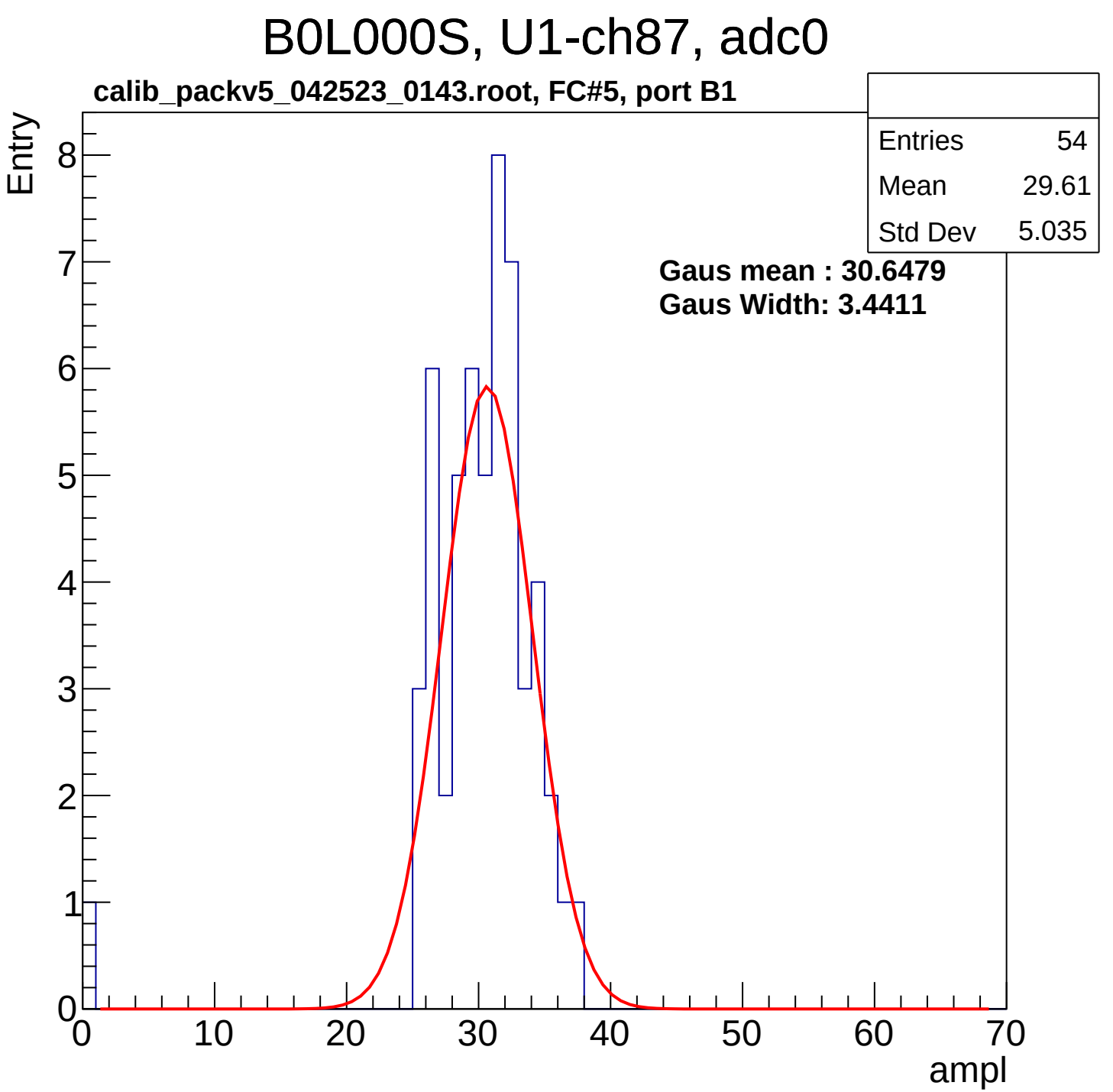
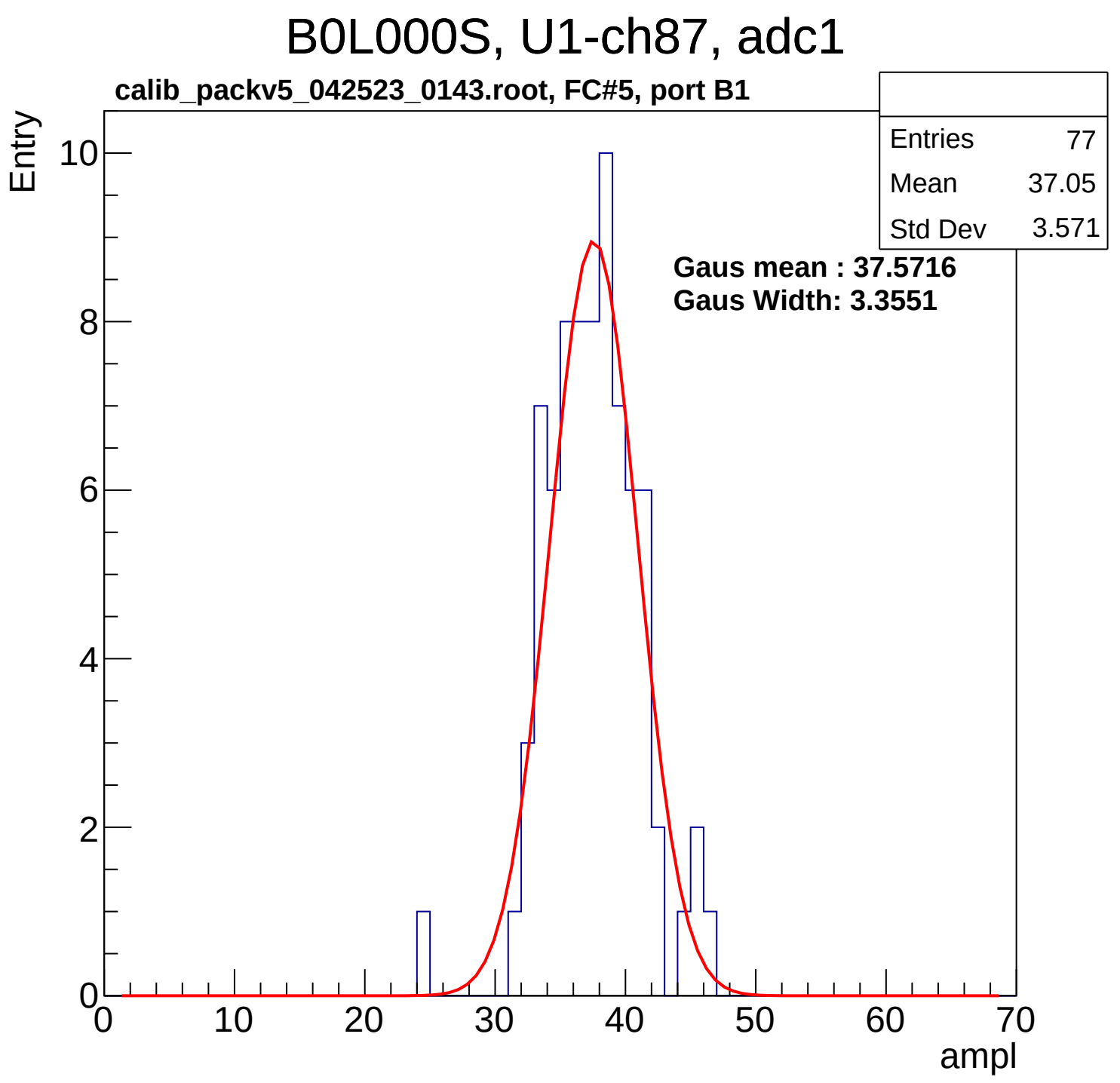
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

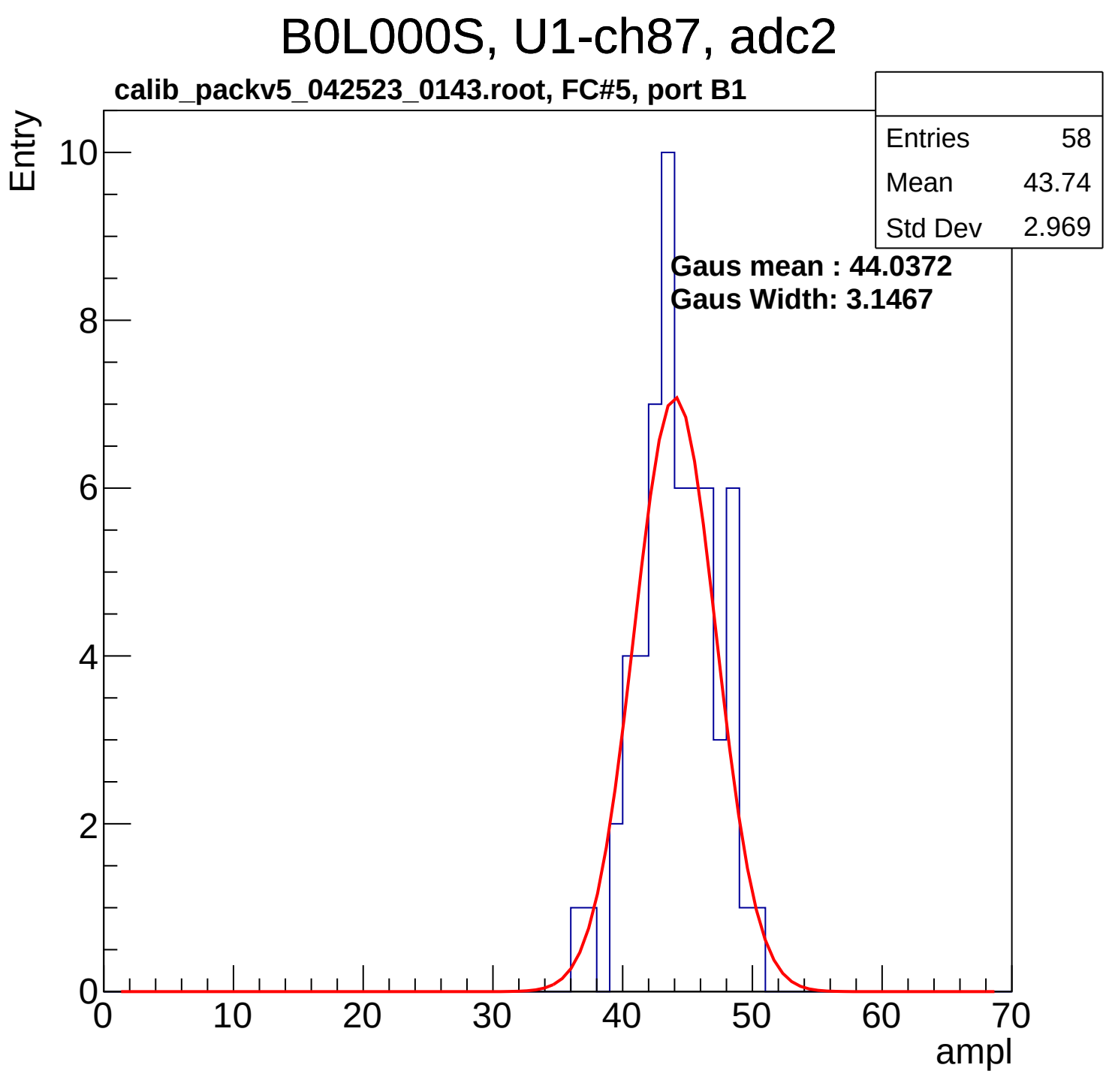

B0L000S, U1-ch86, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.73
Std Dev 9.579
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

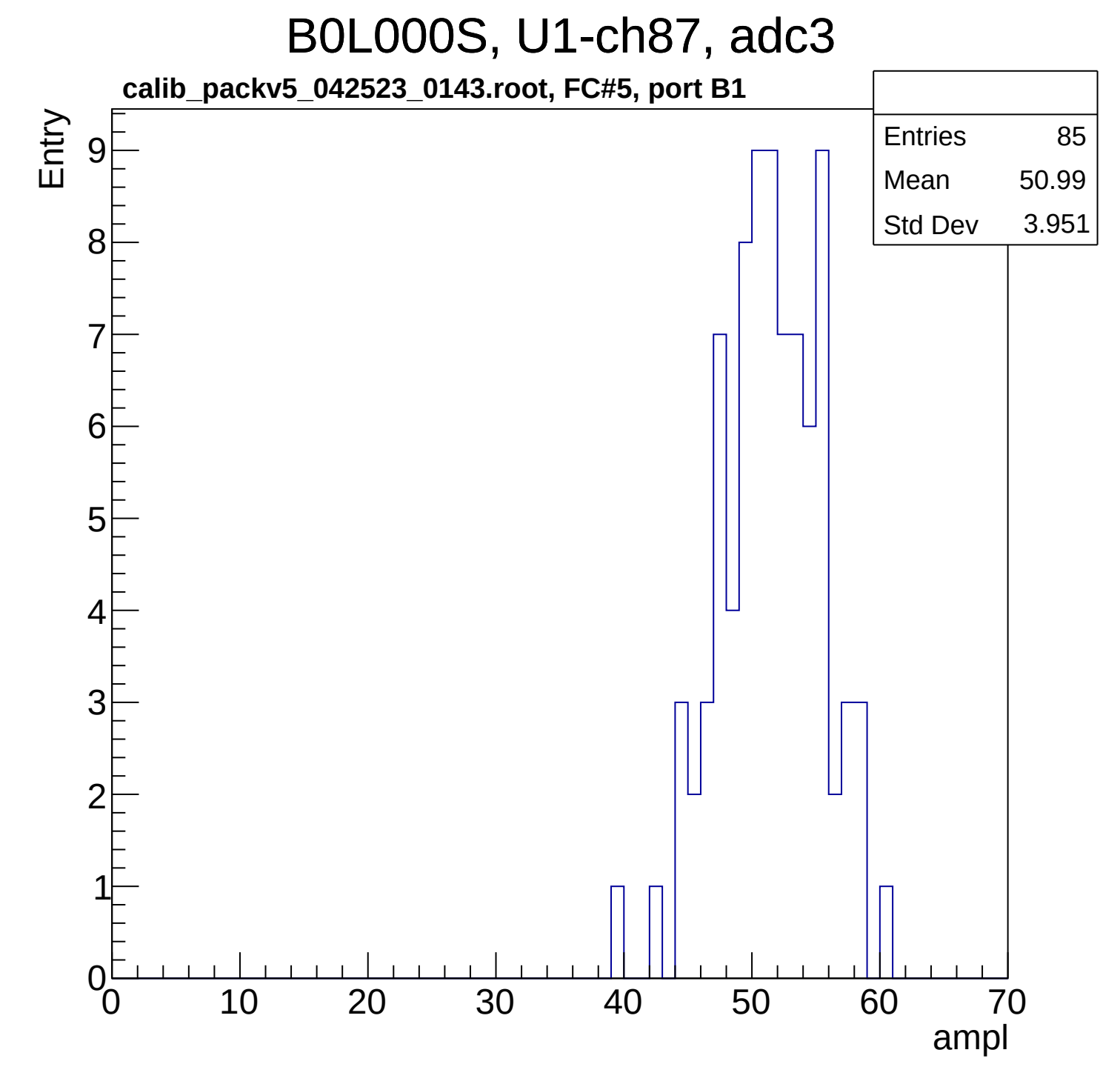

B0L000S, U1-ch86, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch86, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch87, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 29.61
Std Dev 5.035
Gaus mean : 30.6479
Gaus Width: 3.4411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch87, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 37.05
Std Dev 3.571
Gaus mean : 37.5716
Gaus Width: 3.3551
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch87, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.74
Std Dev 2.969
Gaus mean : 44.0372
Gaus Width: 3.1467
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch87, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 50.99
Std Dev 3.951
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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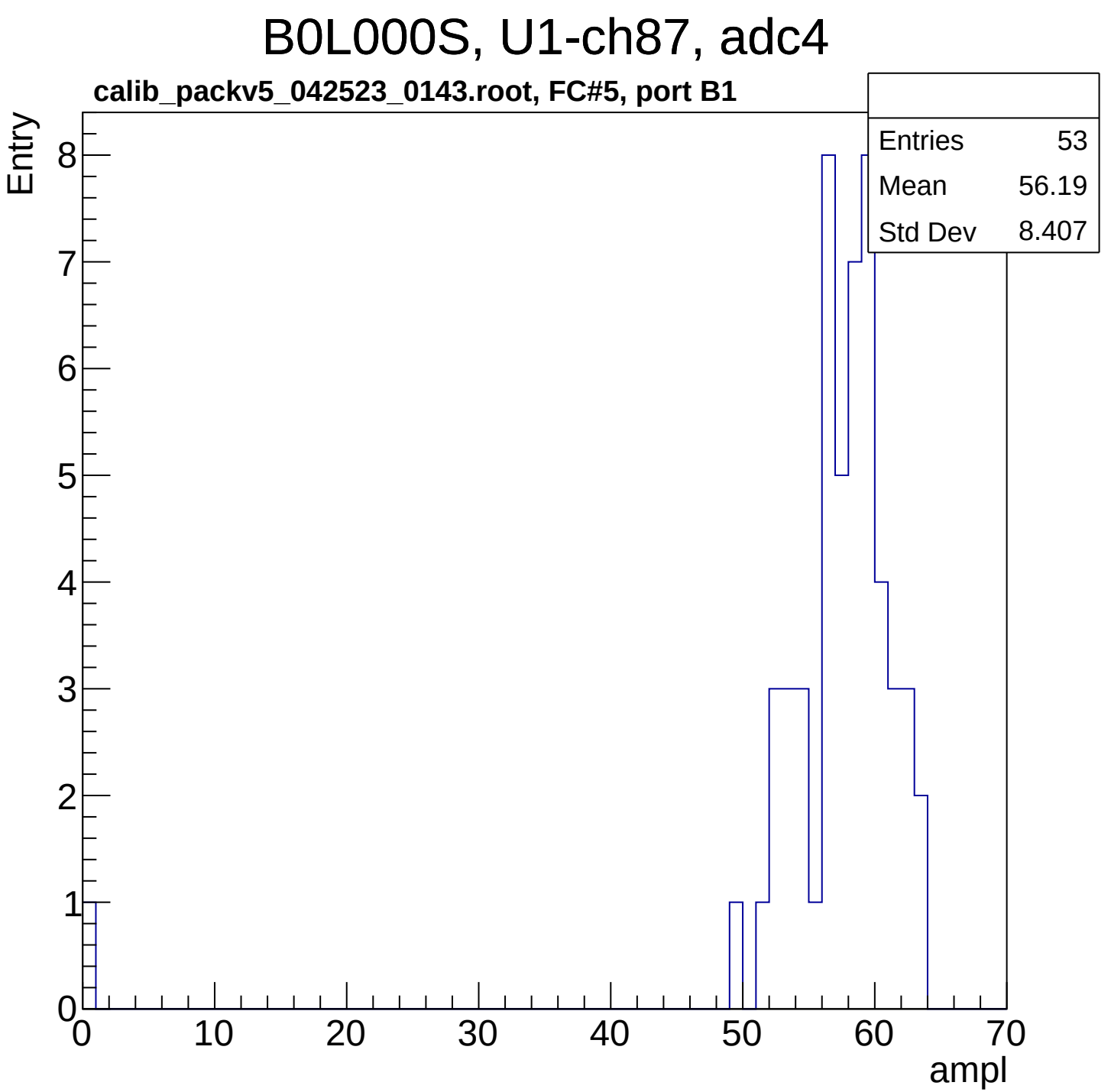
40
50
60
70


B0L000S, U1-ch87, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.19
Std Dev 8.407
Entry
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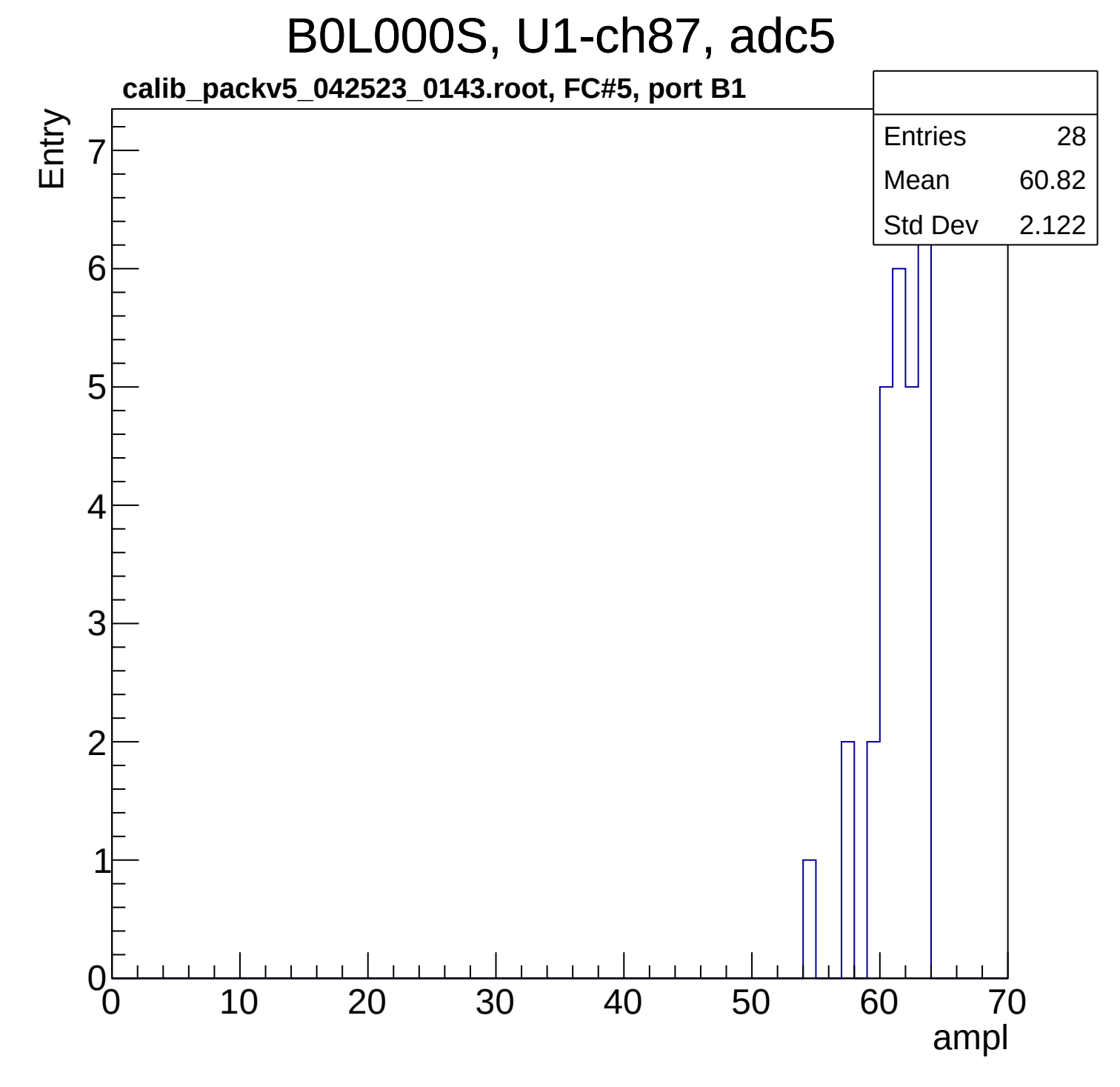
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch87, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 60.82
Std Dev 2.122
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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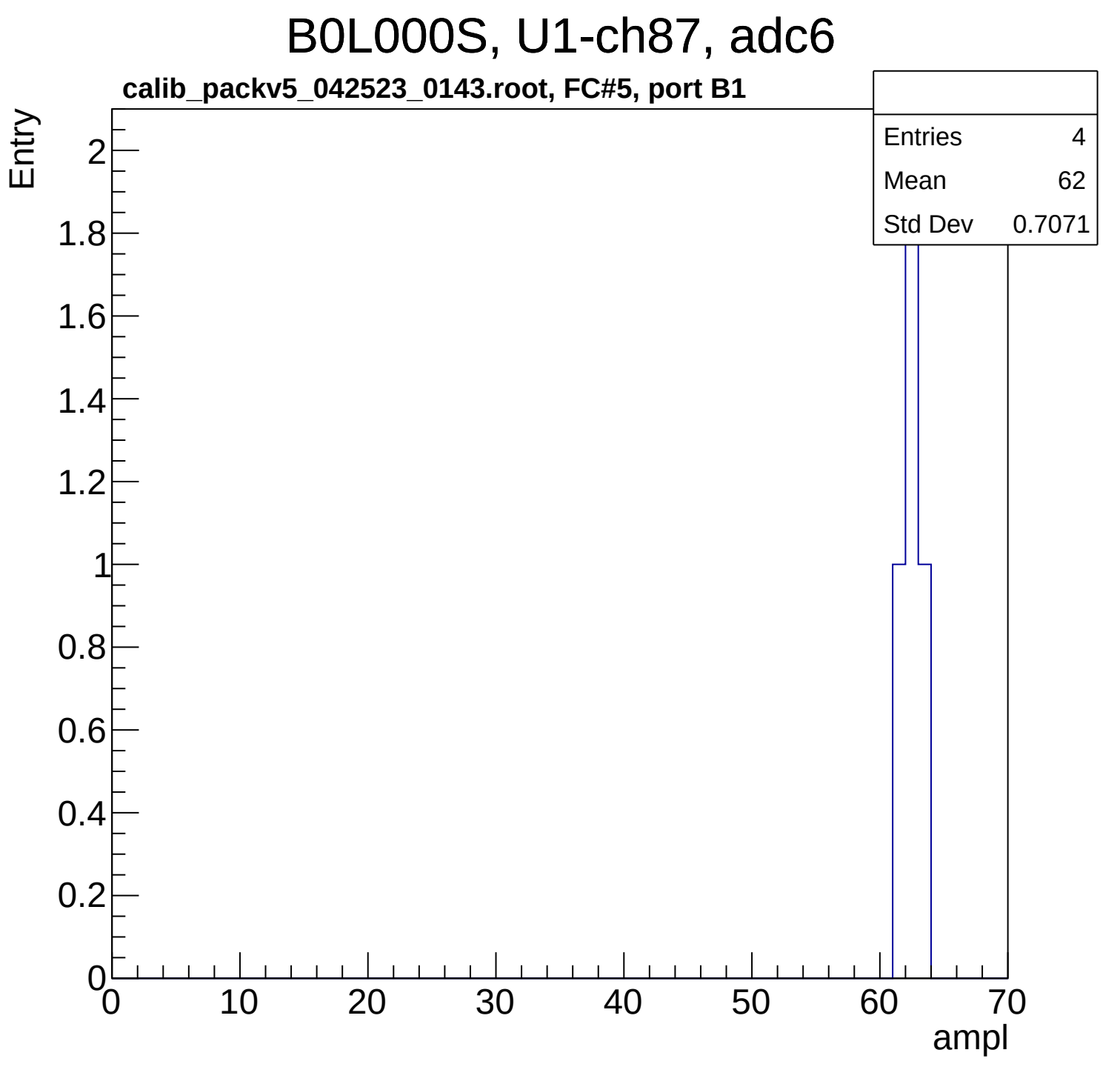
40
50
60
70


B0L000S, U1-ch87, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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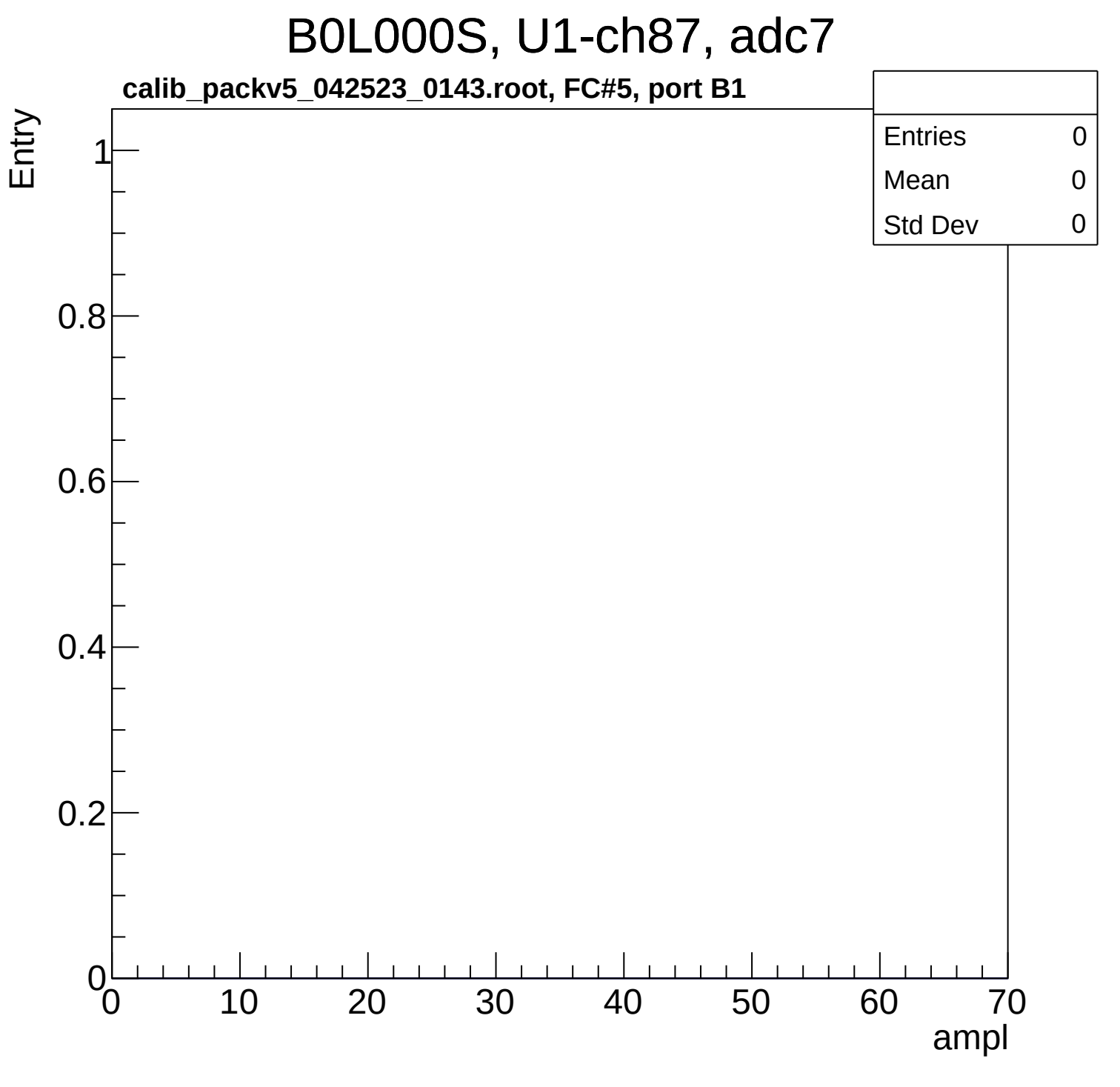
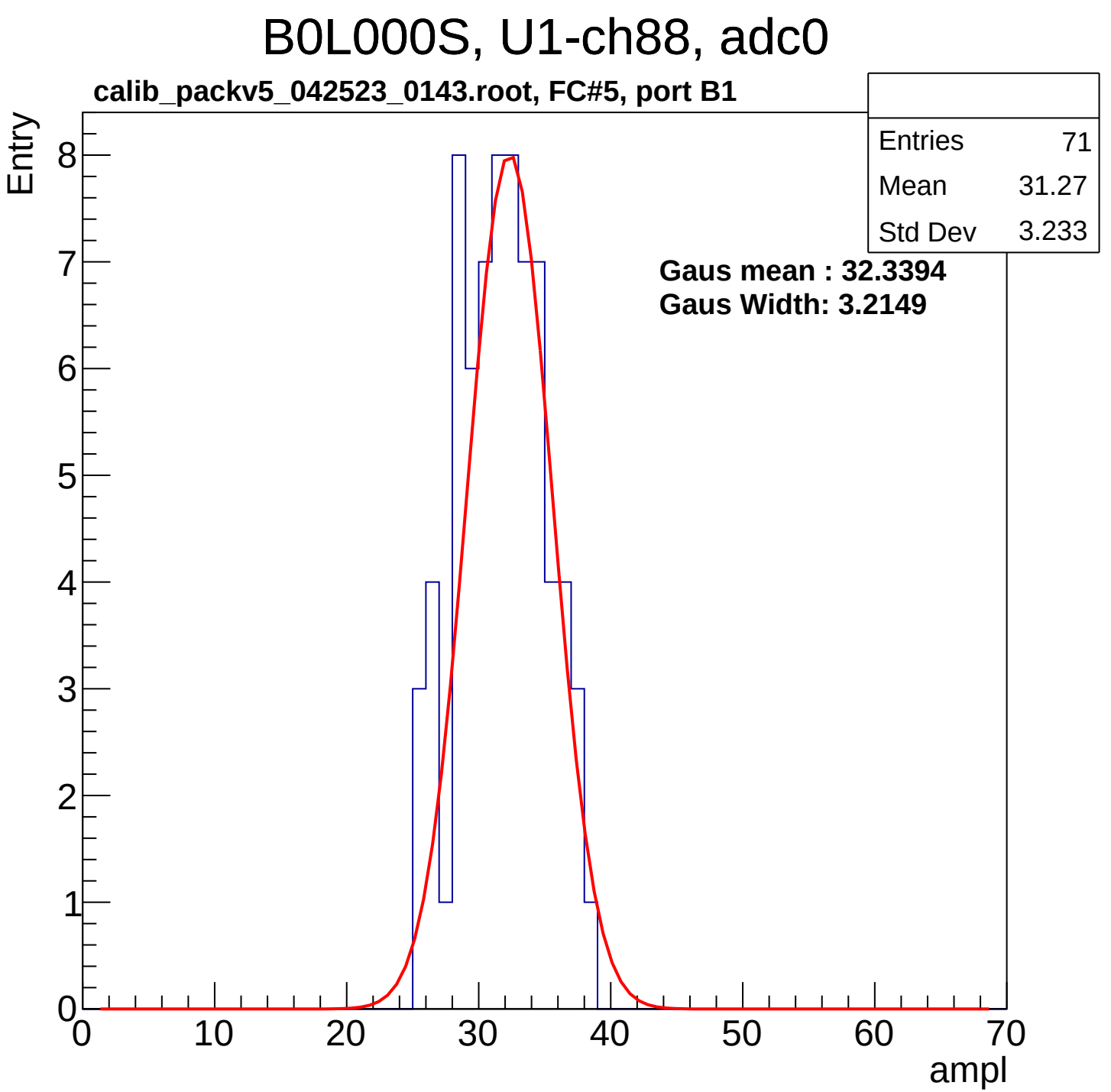

B0L000S, U1-ch87, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch88, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 31.27
Std Dev 3.233
Gaus mean : 32.3394
Gaus Width: 3.2149
Entry
ampl
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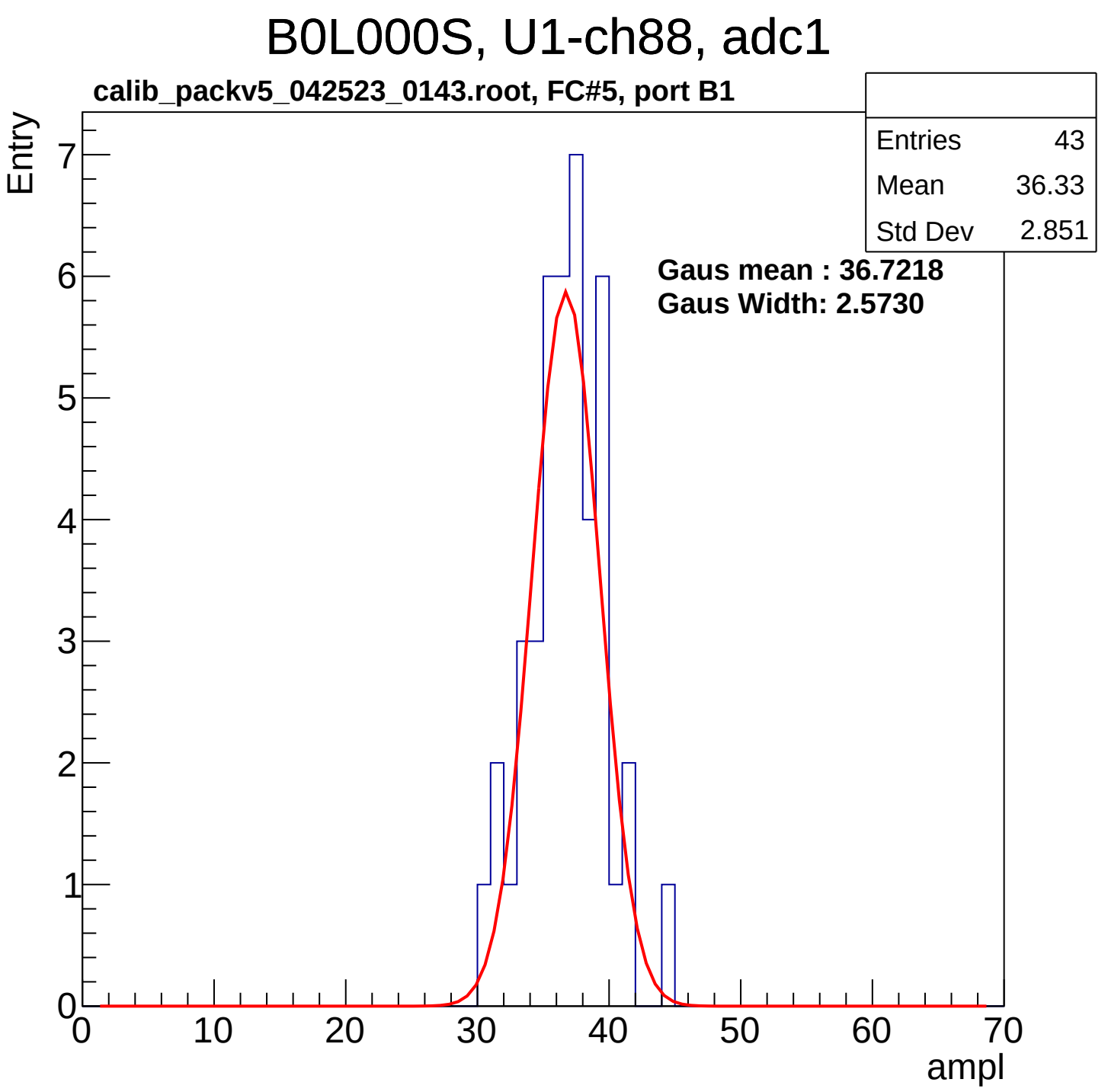
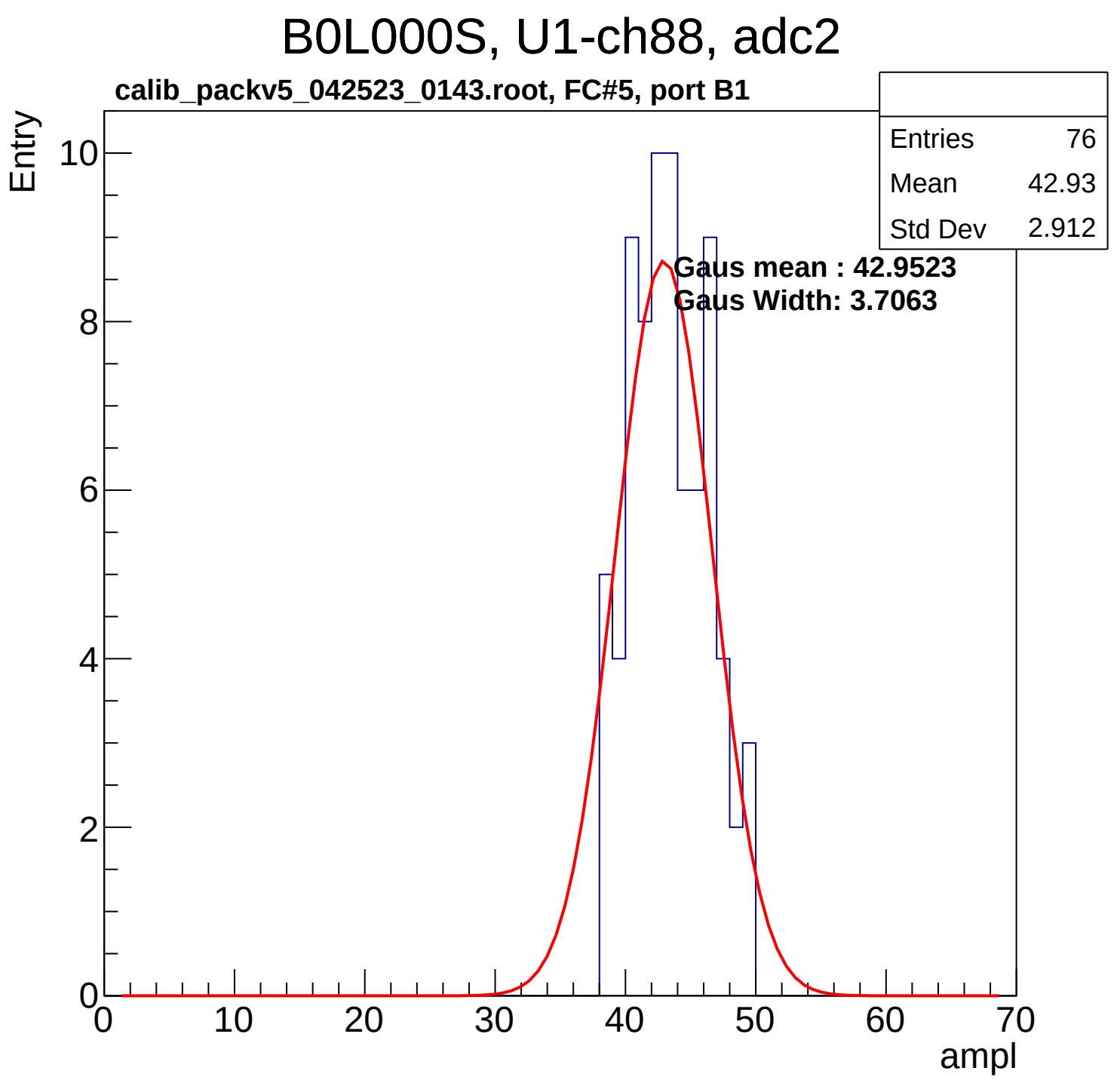
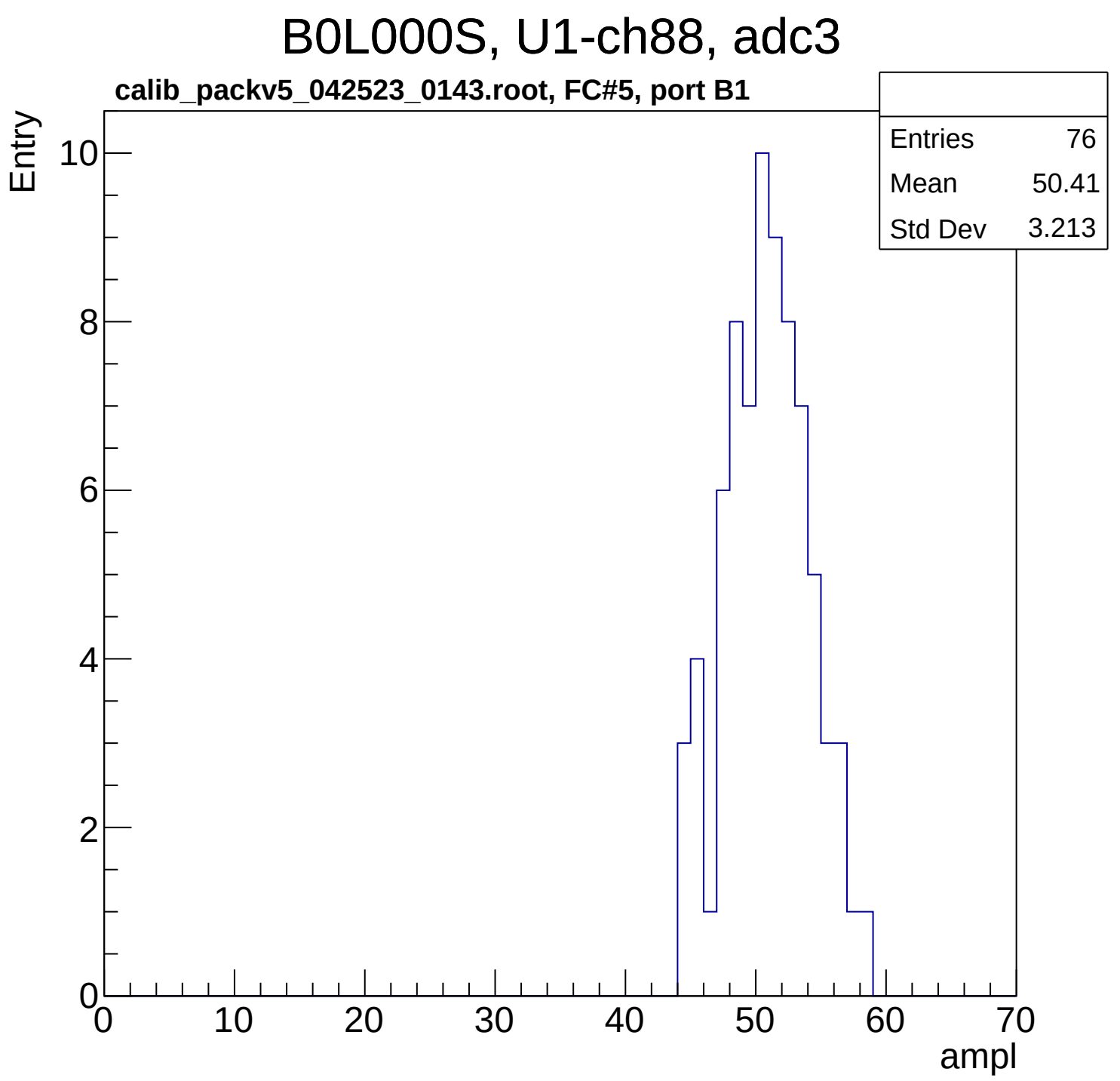
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch88, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 36.33
Std Dev 2.851
Gaus mean : 36.7218
Gaus Width: 2.5730
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch88, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 42.93
Std Dev 2.912
Gaus mean : 42.9523
Gaus Width: 3.7063
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch88, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 50.41
Std Dev 3.213
Entry
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2
4
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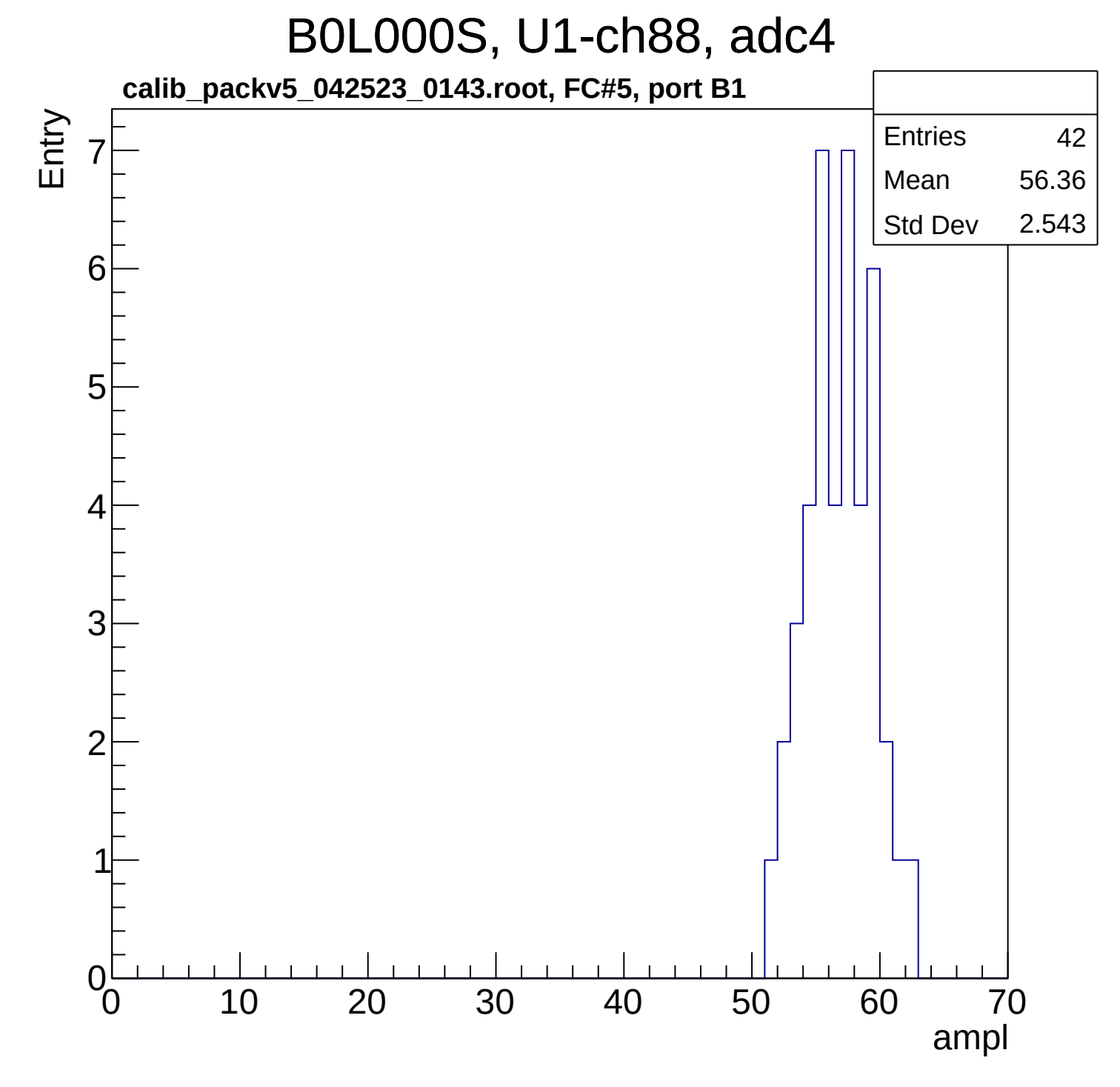
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch88, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 56.36
Std Dev 2.543
Entry
ampl
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1
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7
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60
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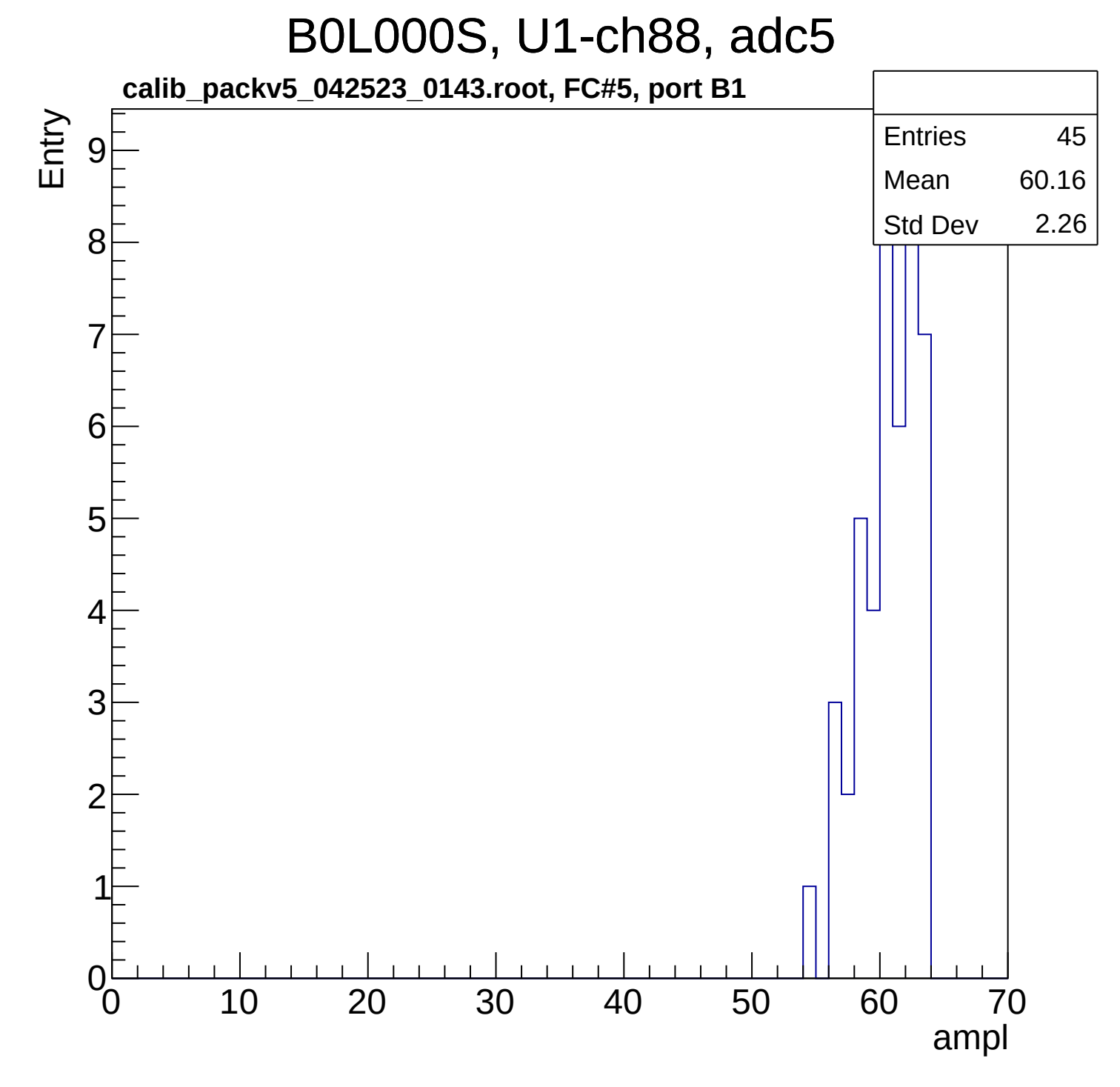
B0L000S, U1-ch88, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.16
Std Dev 2.26
Entry
ampl
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3
4
5
6
7
8
9
0
10
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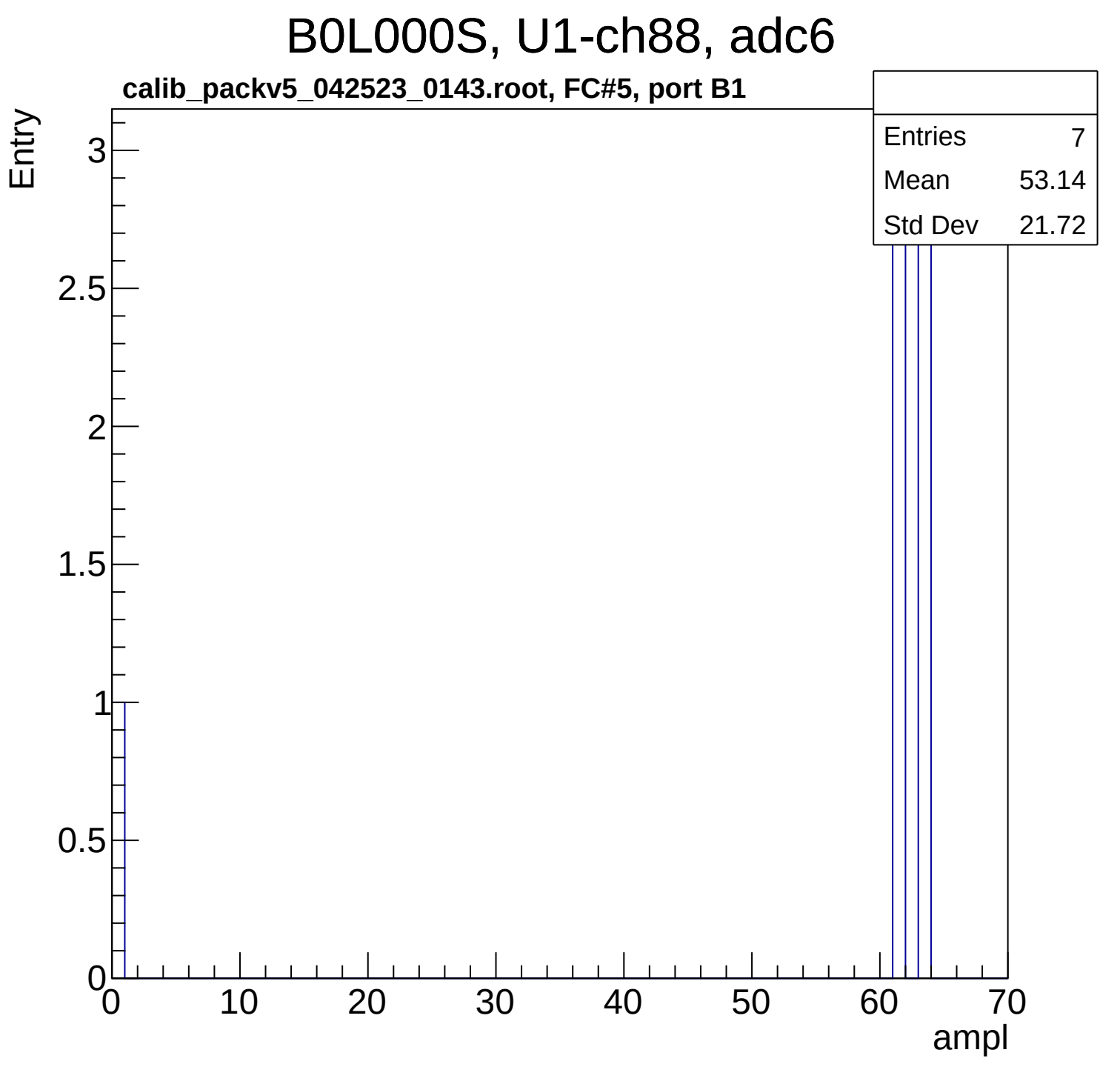
B0L000S, U1-ch88, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.14
Std Dev 21.72
Entry
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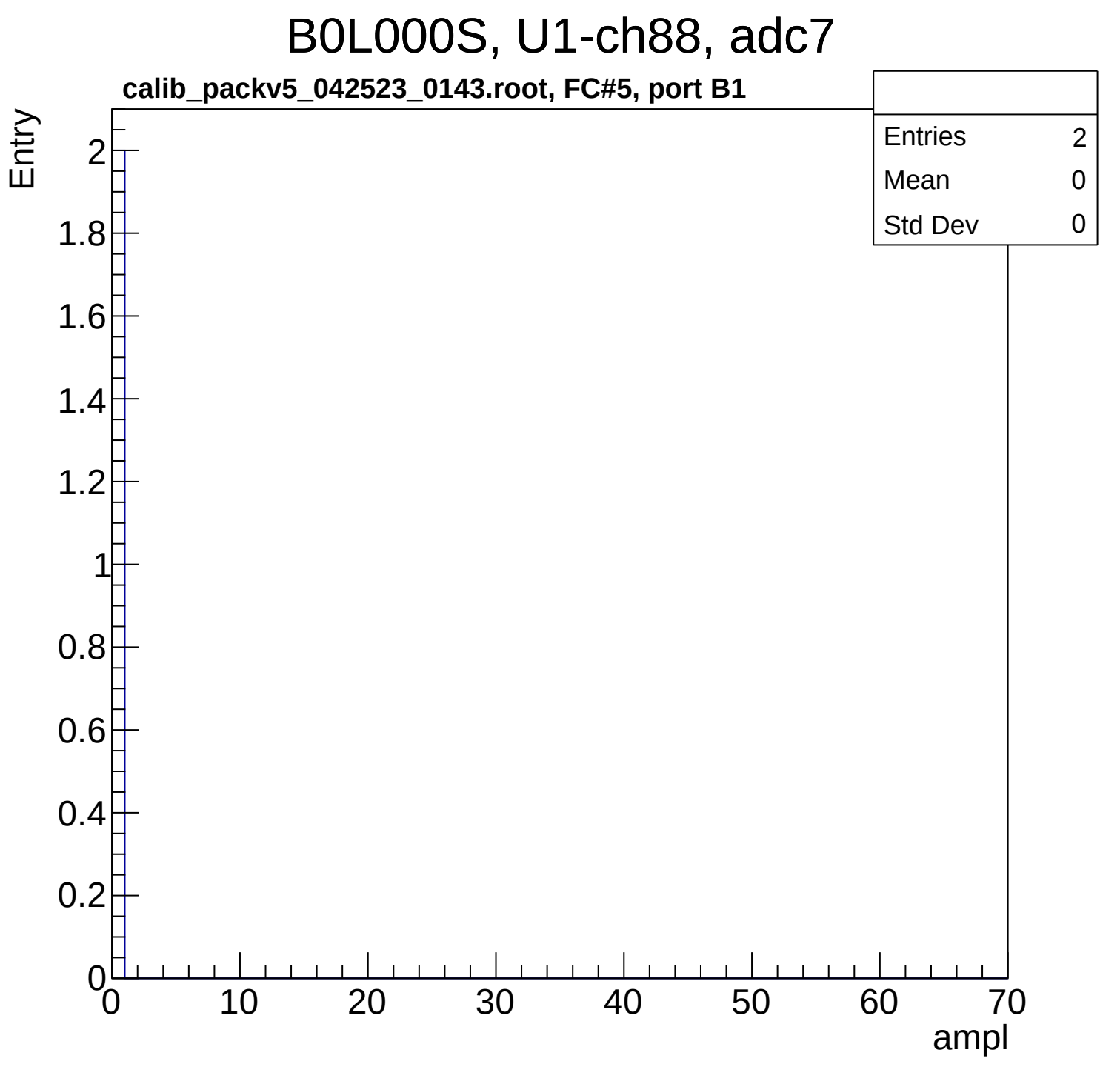
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch88, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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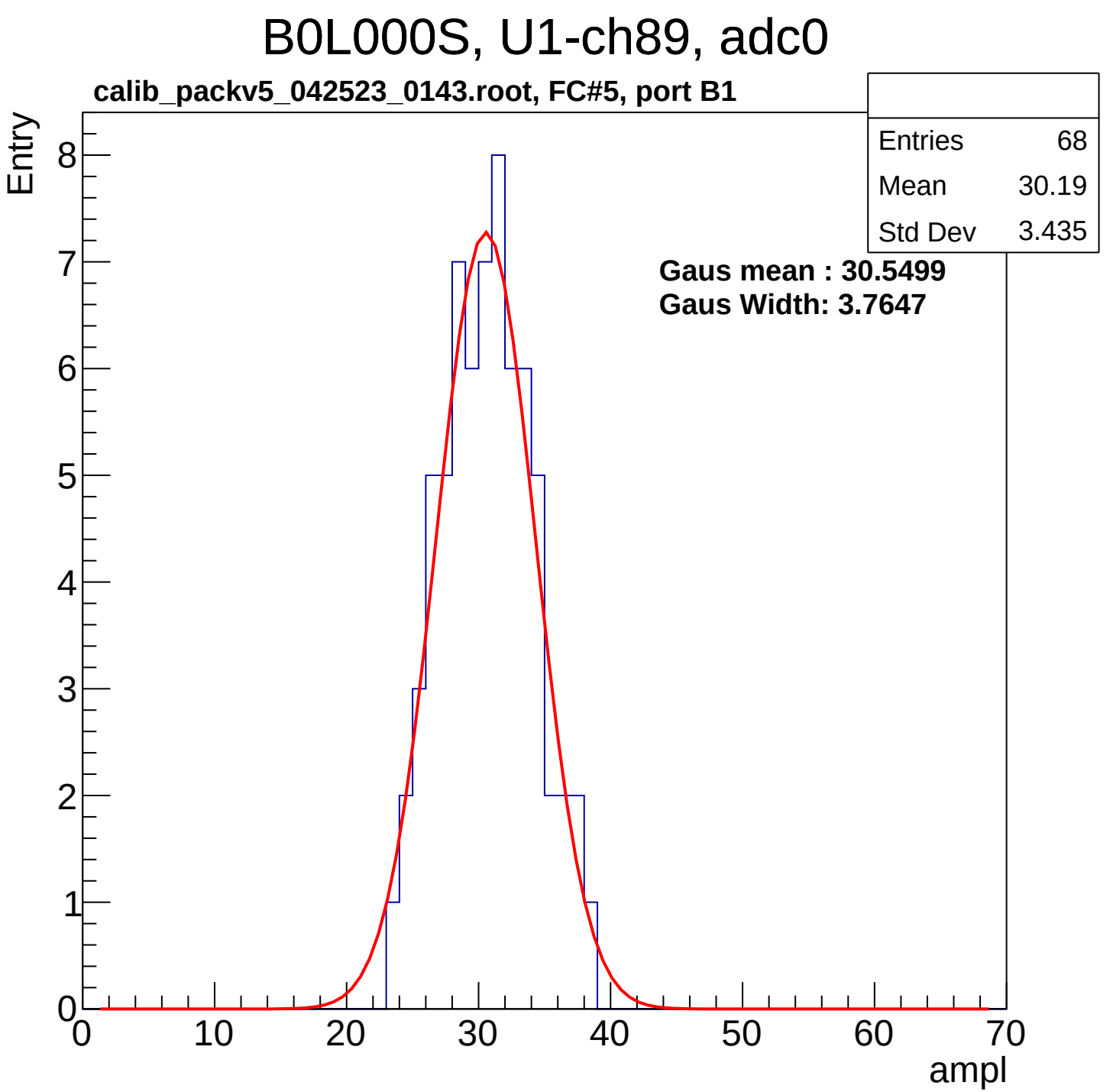
B0L000S, U1-ch89, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.19
Std Dev 3.435
Gaus mean : 30.5499
Gaus Width: 3.7647
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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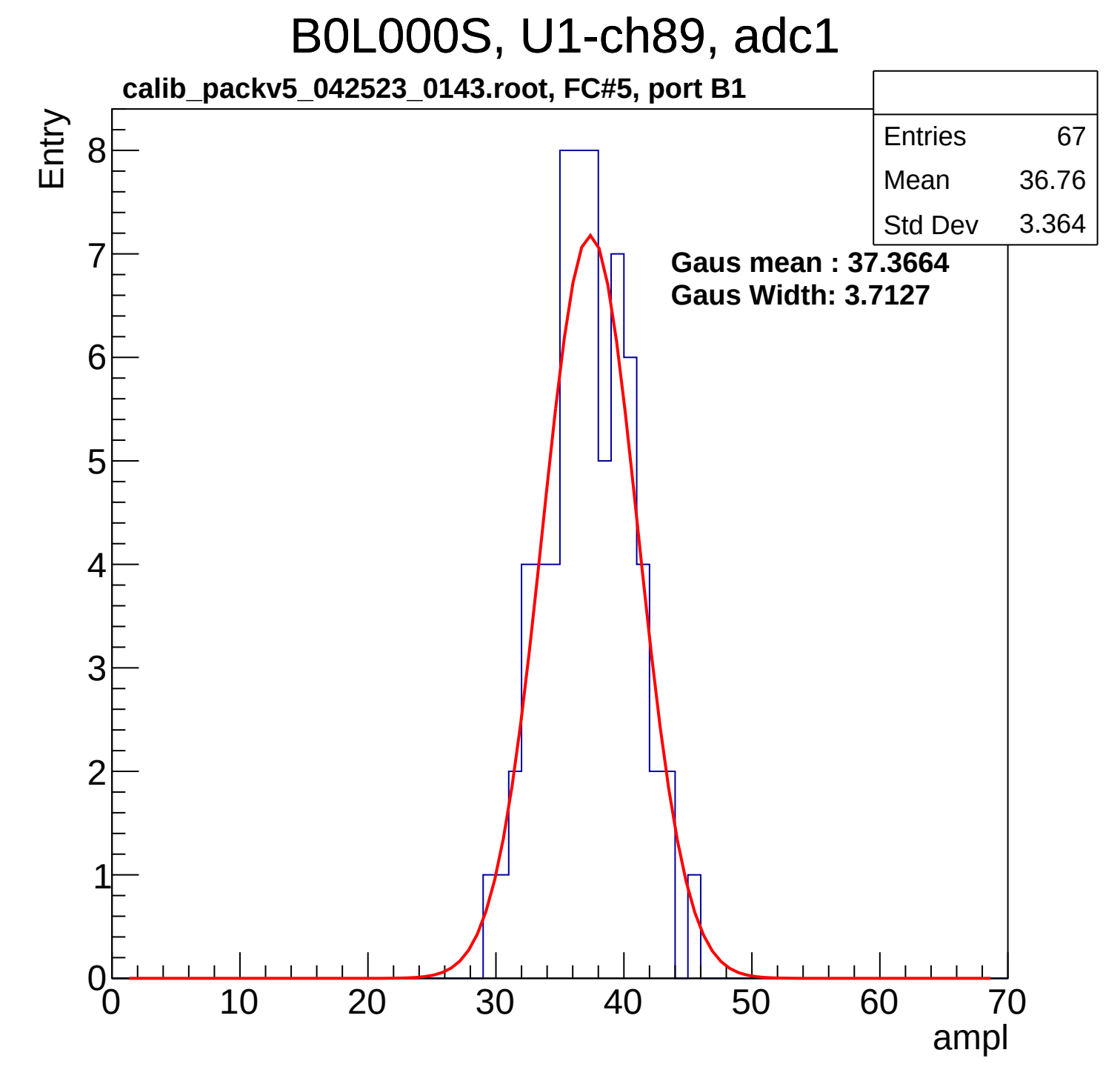
B0L000S, U1-ch89, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.76
Std Dev 3.364
Gaus mean : 37.3664
Gaus Width: 3.7127
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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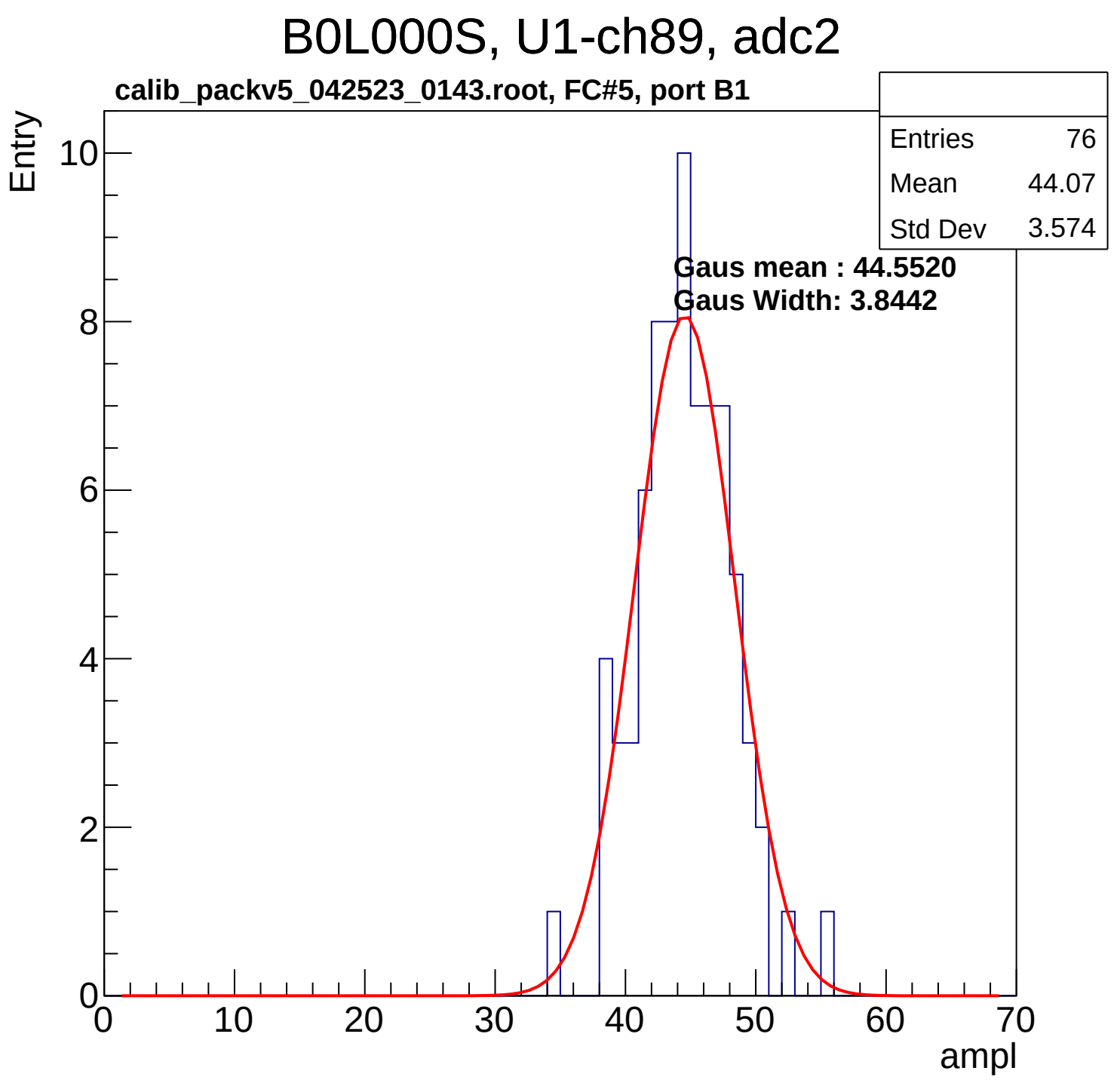
B0L000S, U1-ch89, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 44.07
Std Dev 3.574
Gaus mean : 44.5520
Gaus Width: 3.8442
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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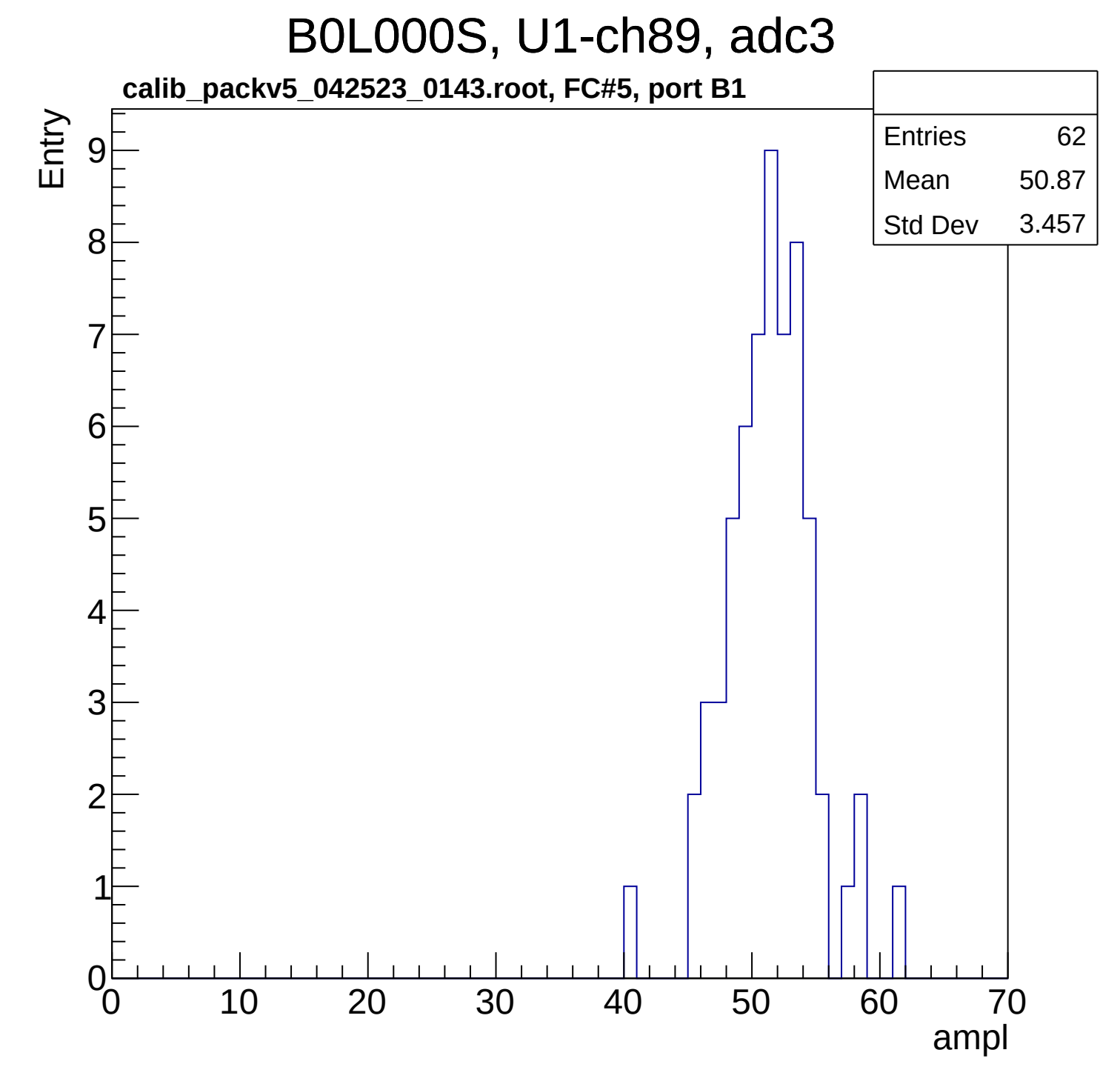
B0L000S, U1-ch89, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.87
Std Dev 3.457
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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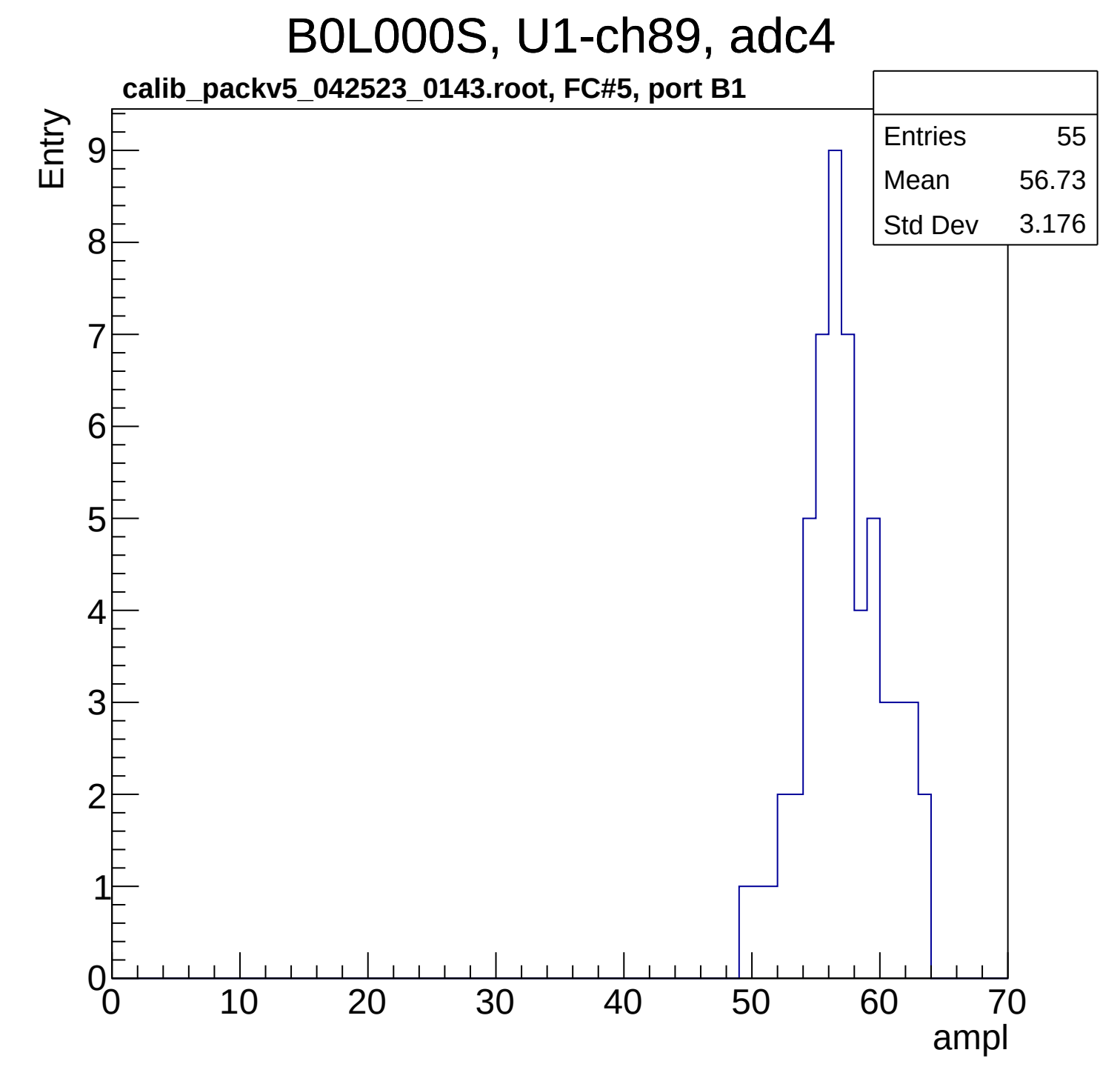
B0L000S, U1-ch89, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.73
Std Dev 3.176
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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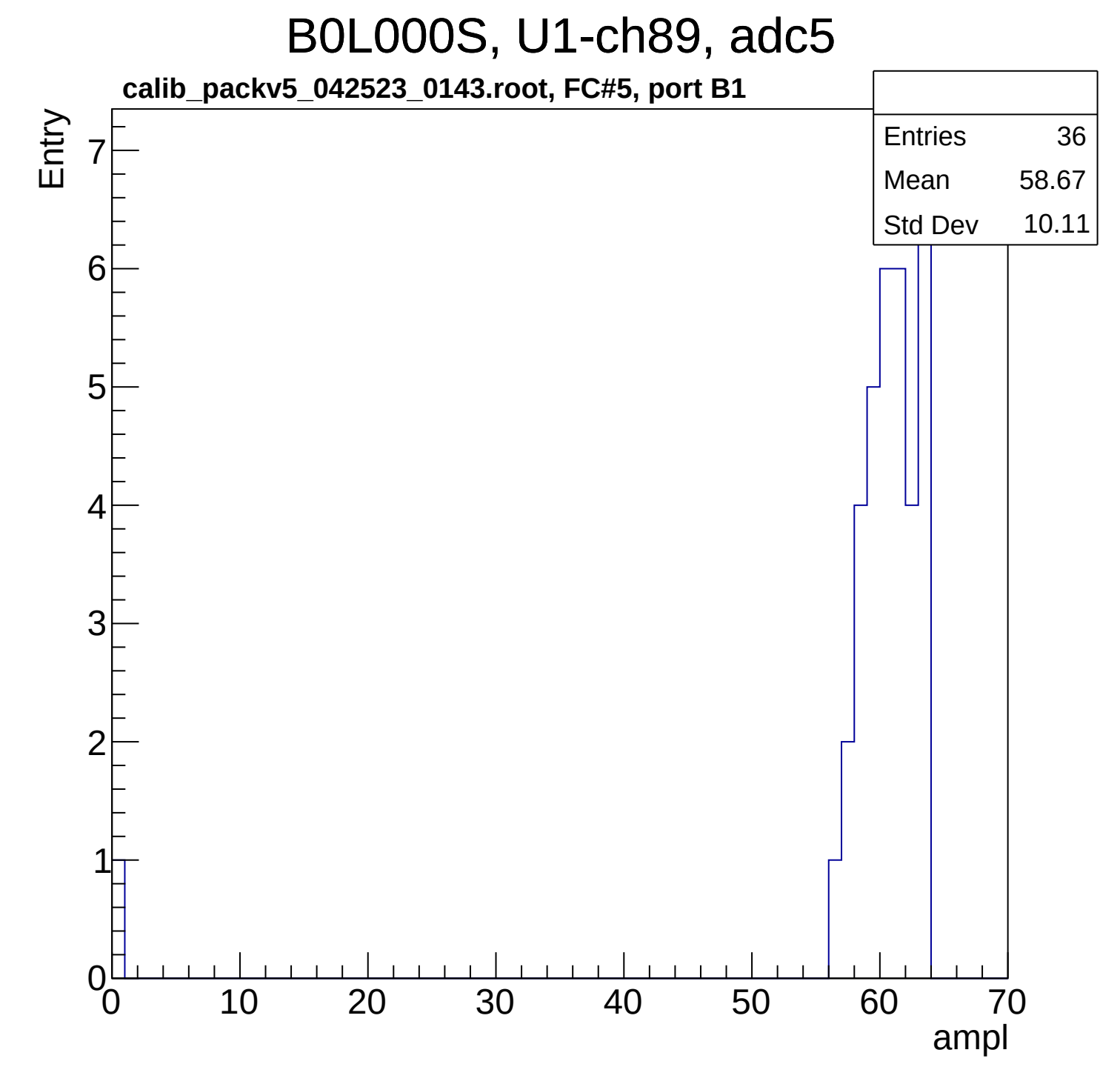
B0L000S, U1-ch89, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.67
Std Dev 10.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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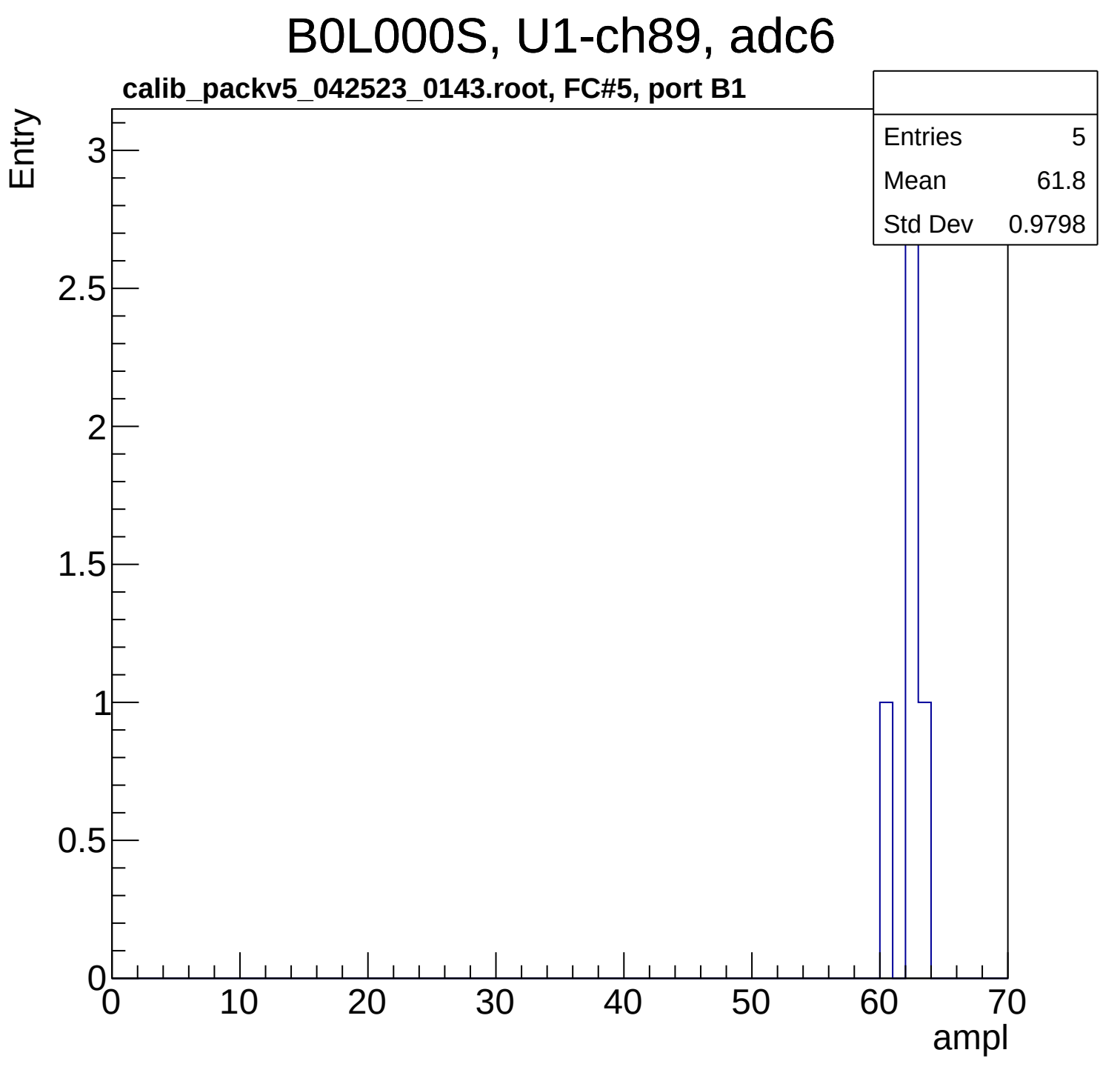
B0L000S, U1-ch89, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.8
Std Dev 0.9798
Entry
ampl
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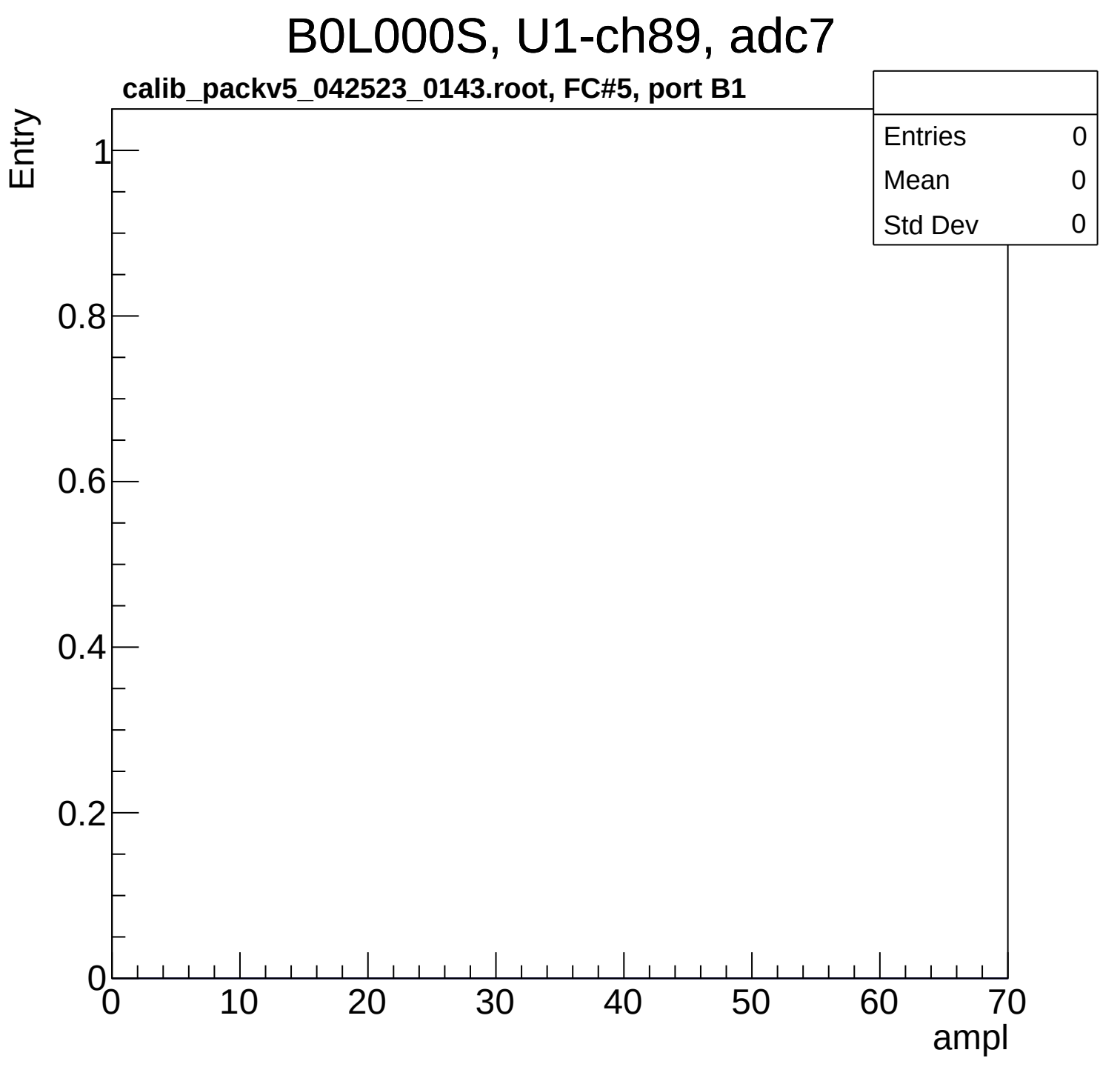
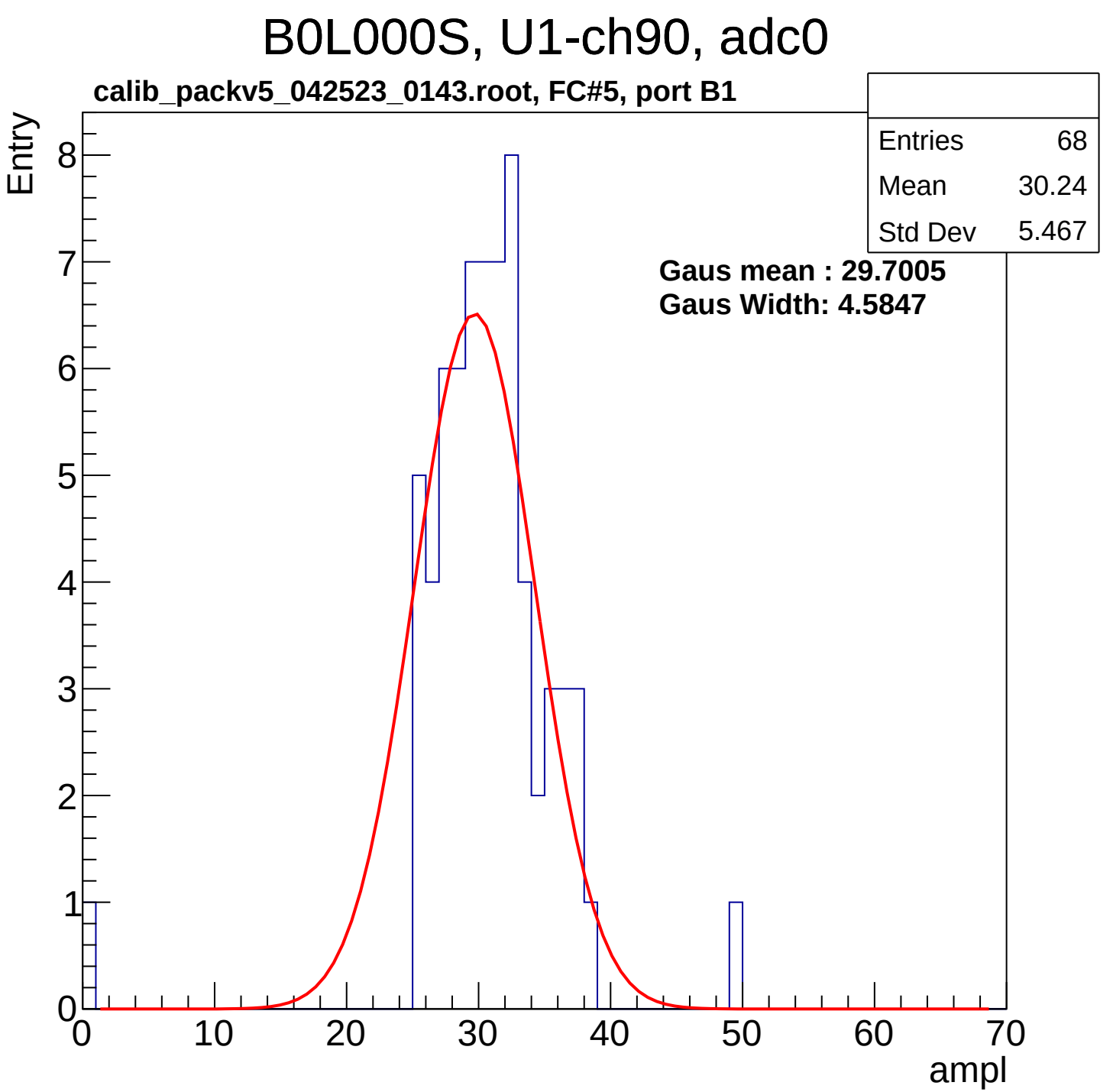
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch89, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch90, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.24
Std Dev 5.467
Gaus mean : 29.7005
Gaus Width: 4.5847
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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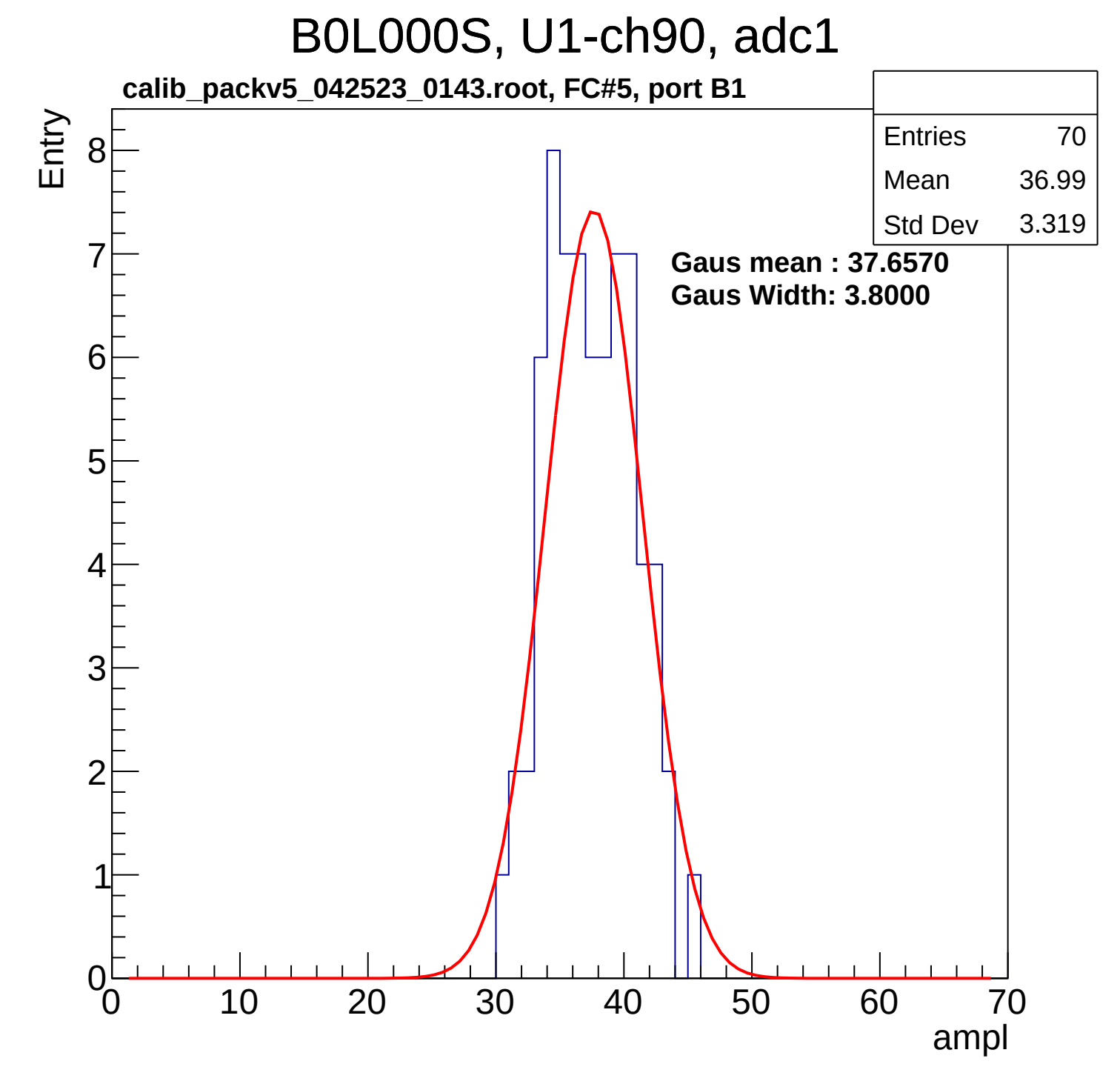
B0L000S, U1-ch90, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.99
Std Dev 3.319
Gaus mean : 37.6570
Gaus Width: 3.8000
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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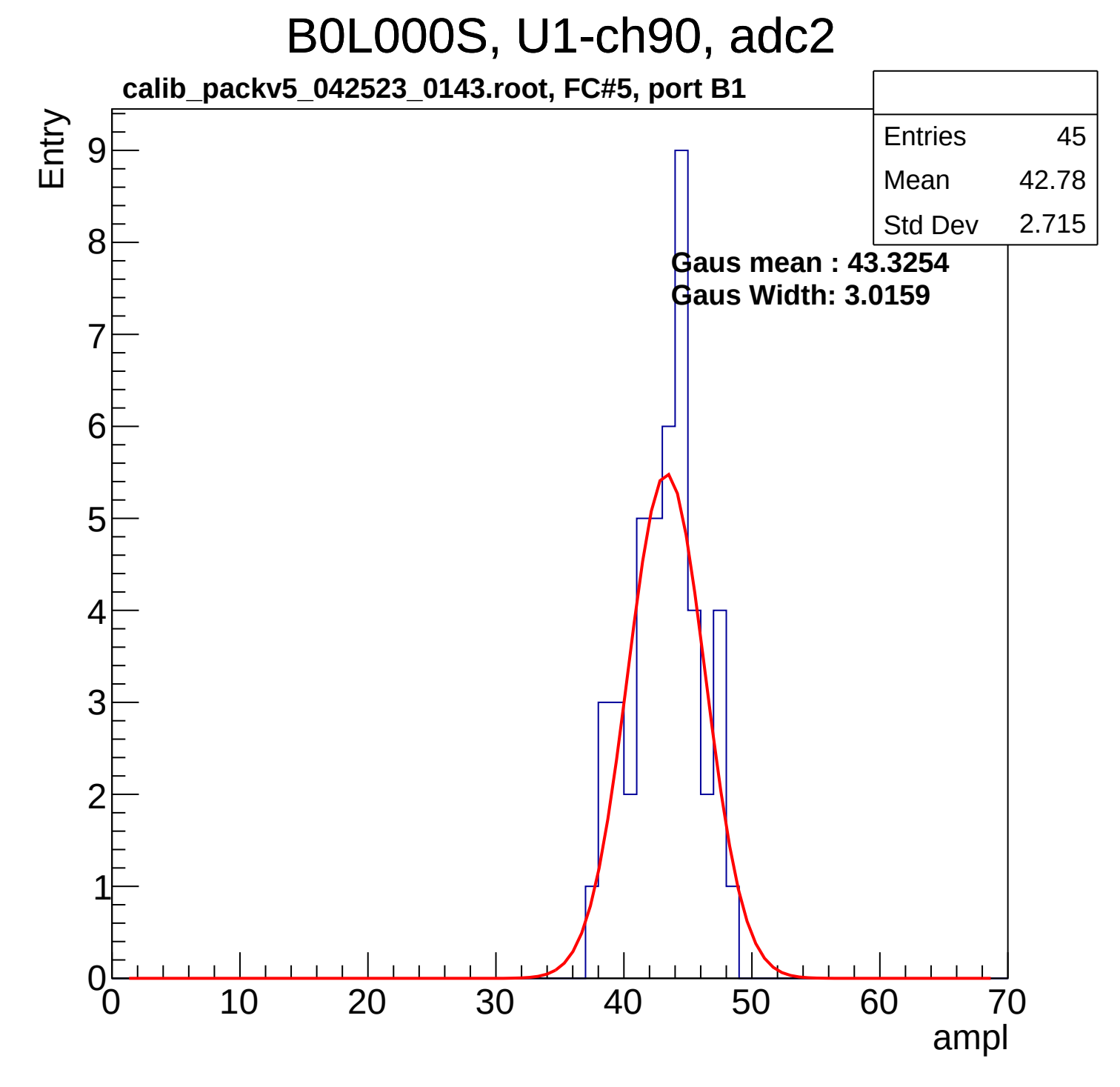
B0L000S, U1-ch90, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 42.78
Std Dev 2.715
Gaus mean : 43.3254
Gaus Width: 3.0159
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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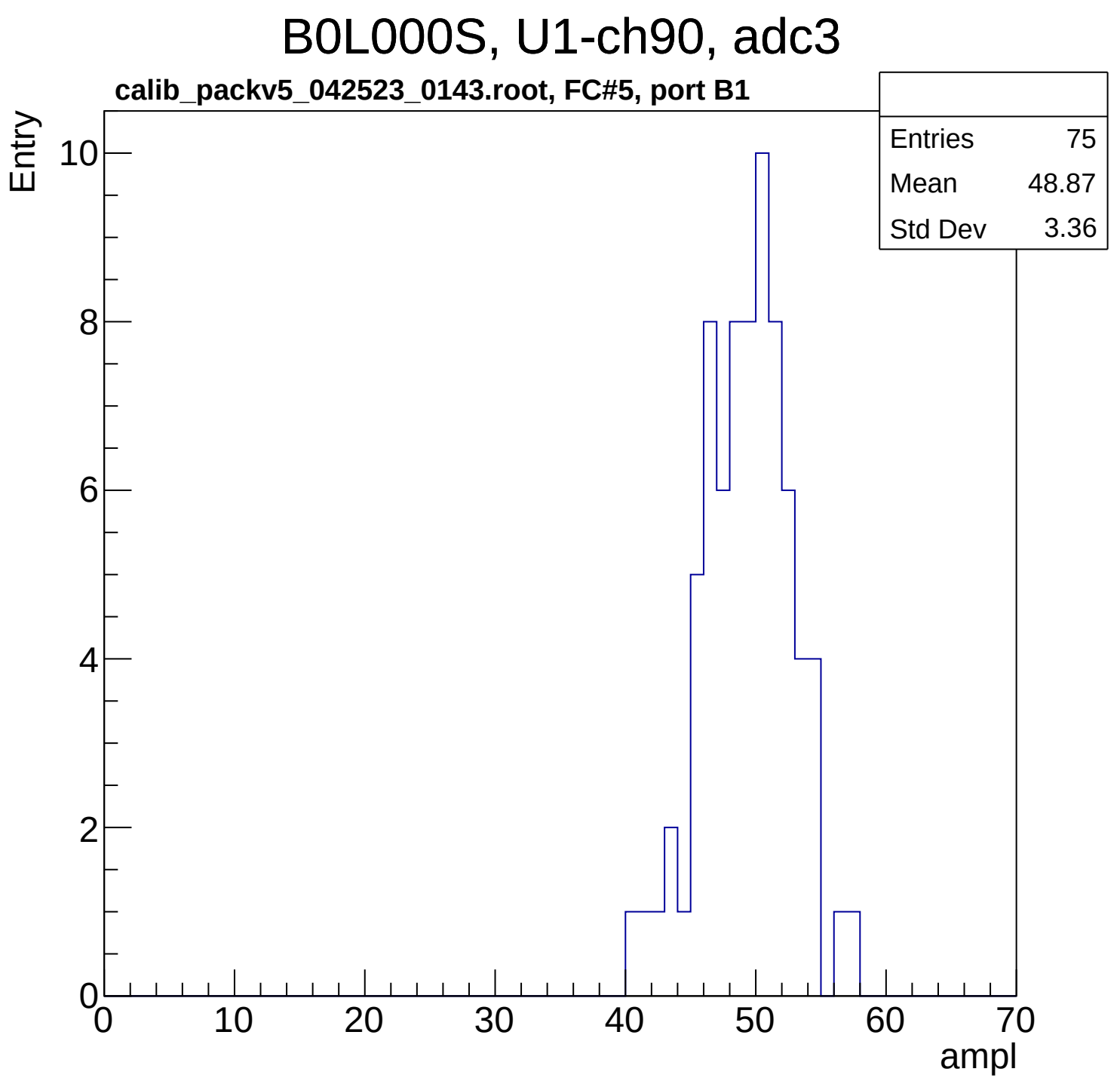
B0L000S, U1-ch90, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.87
Std Dev 3.36
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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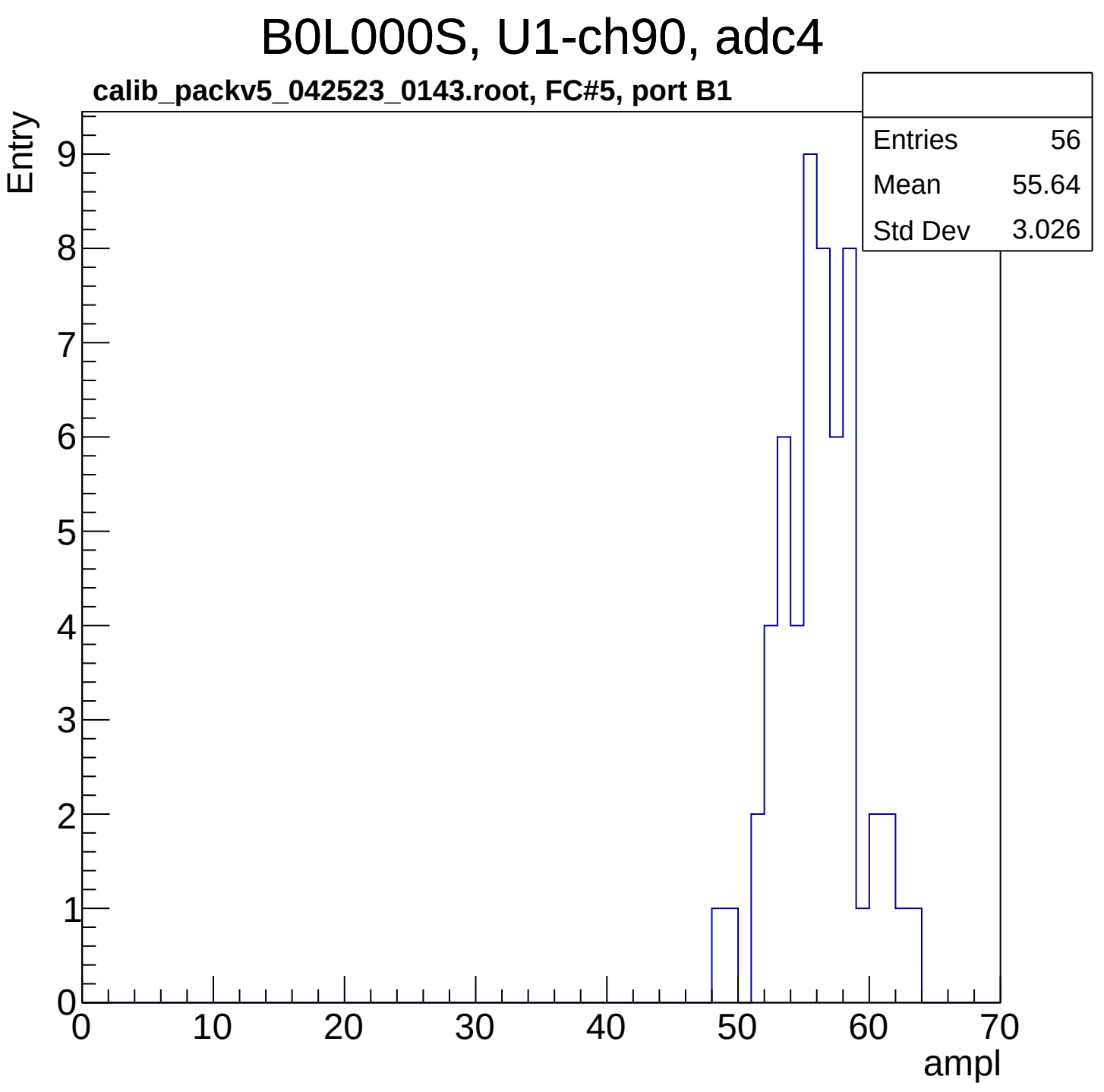
B0L000S, U1-ch90, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.64
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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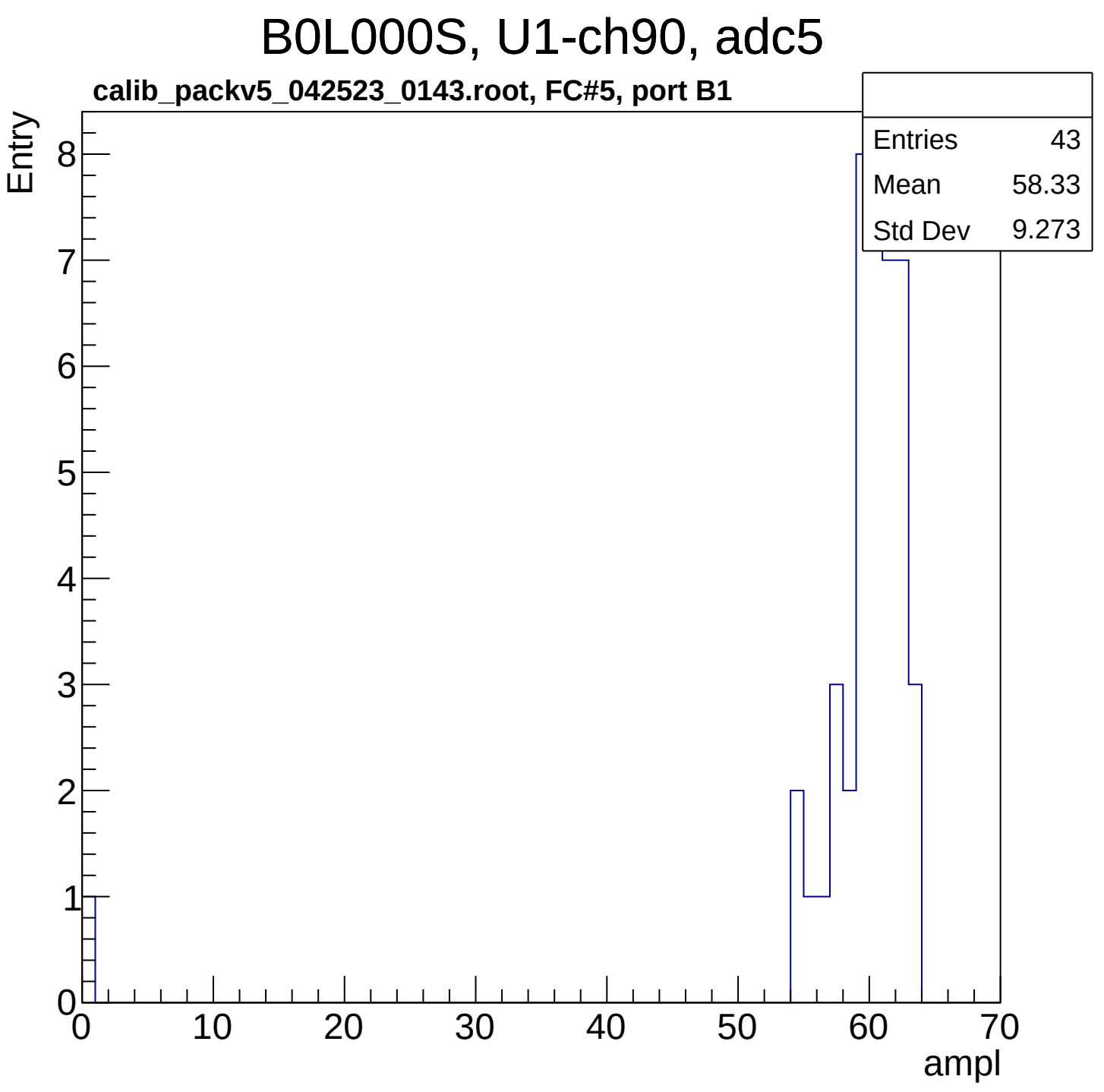
B0L000S, U1-ch90, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.33
Std Dev 9.273
Entry
ampl
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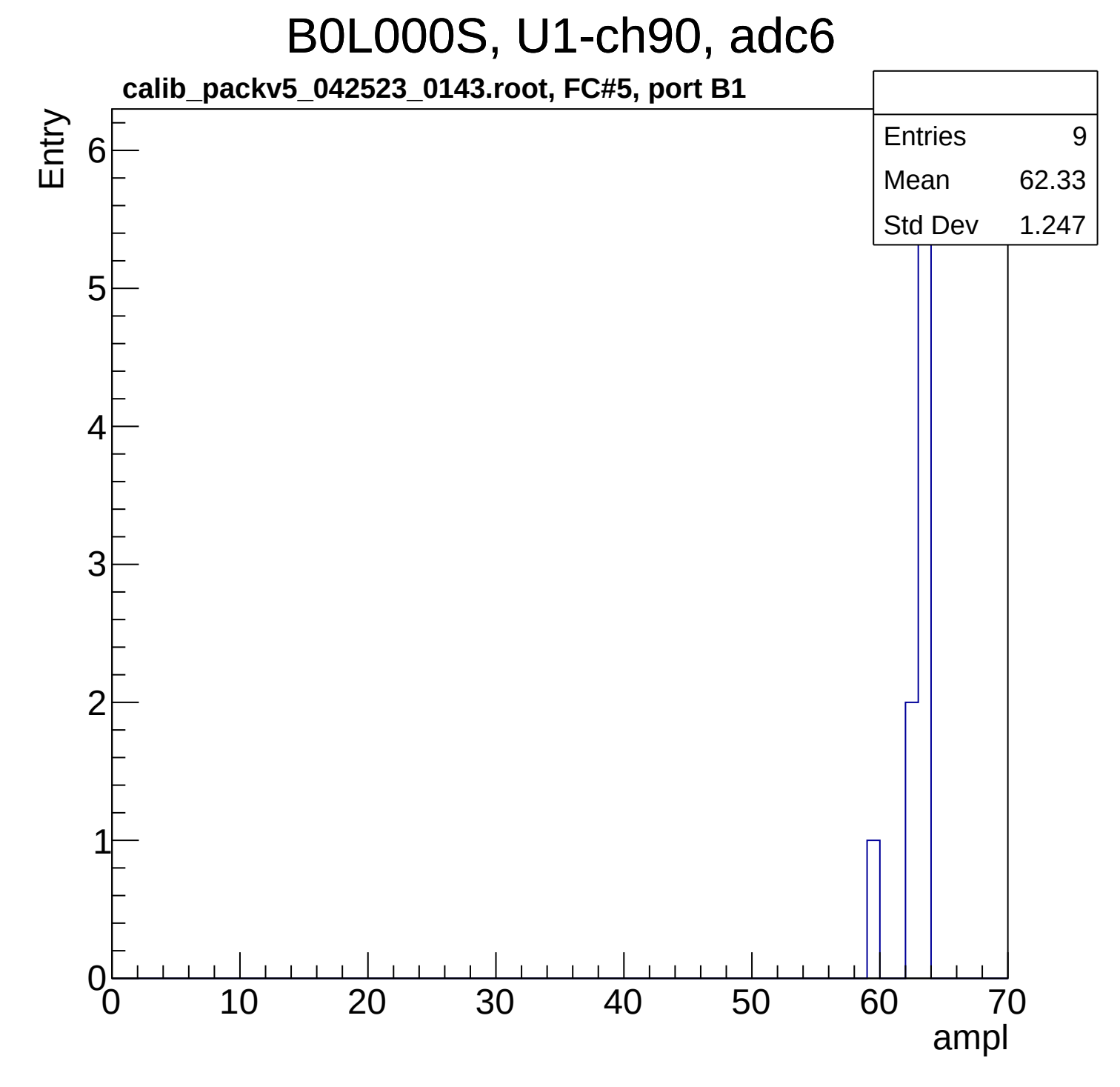
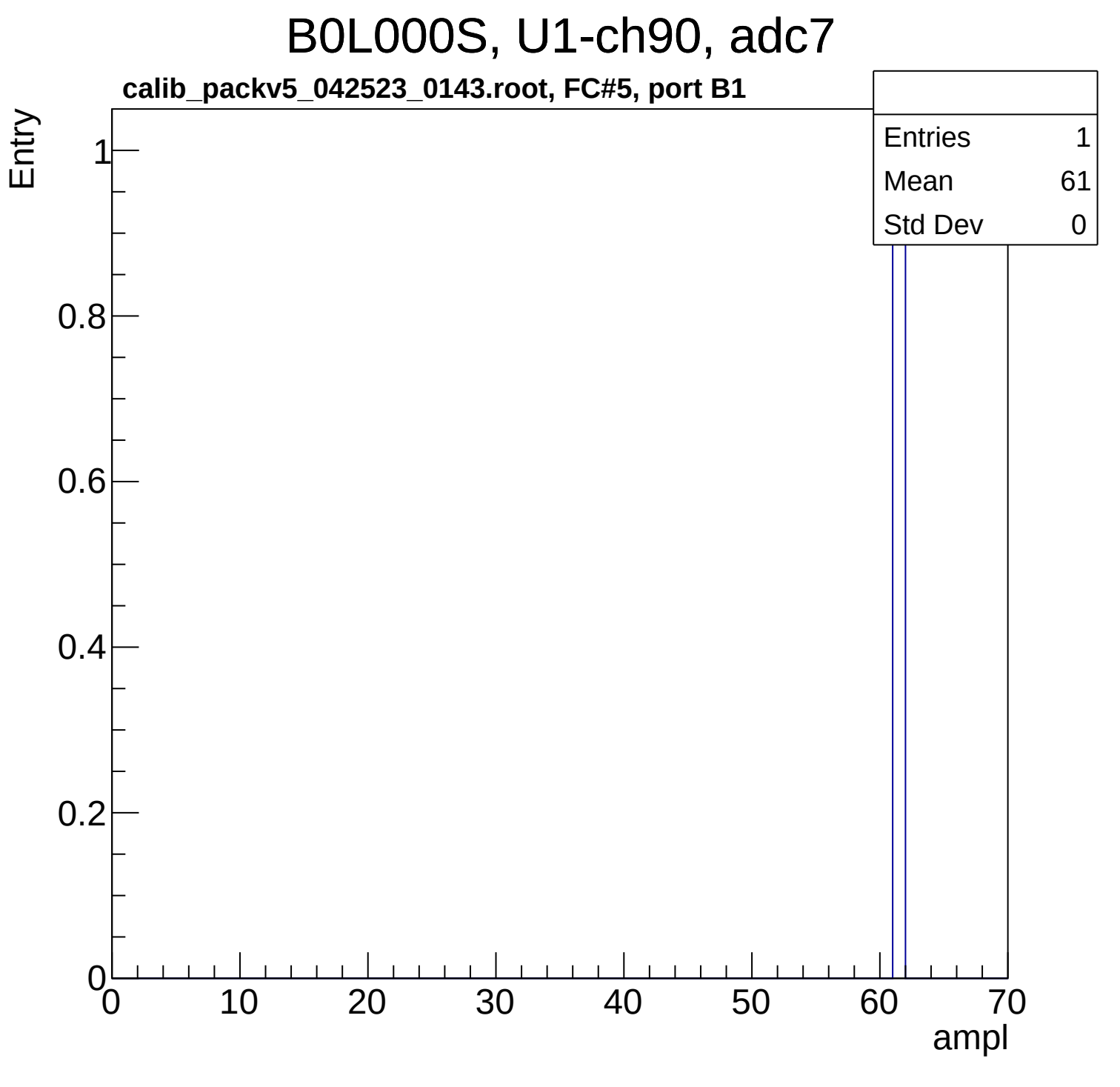
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch90, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62.33
Std Dev 1.247
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch90, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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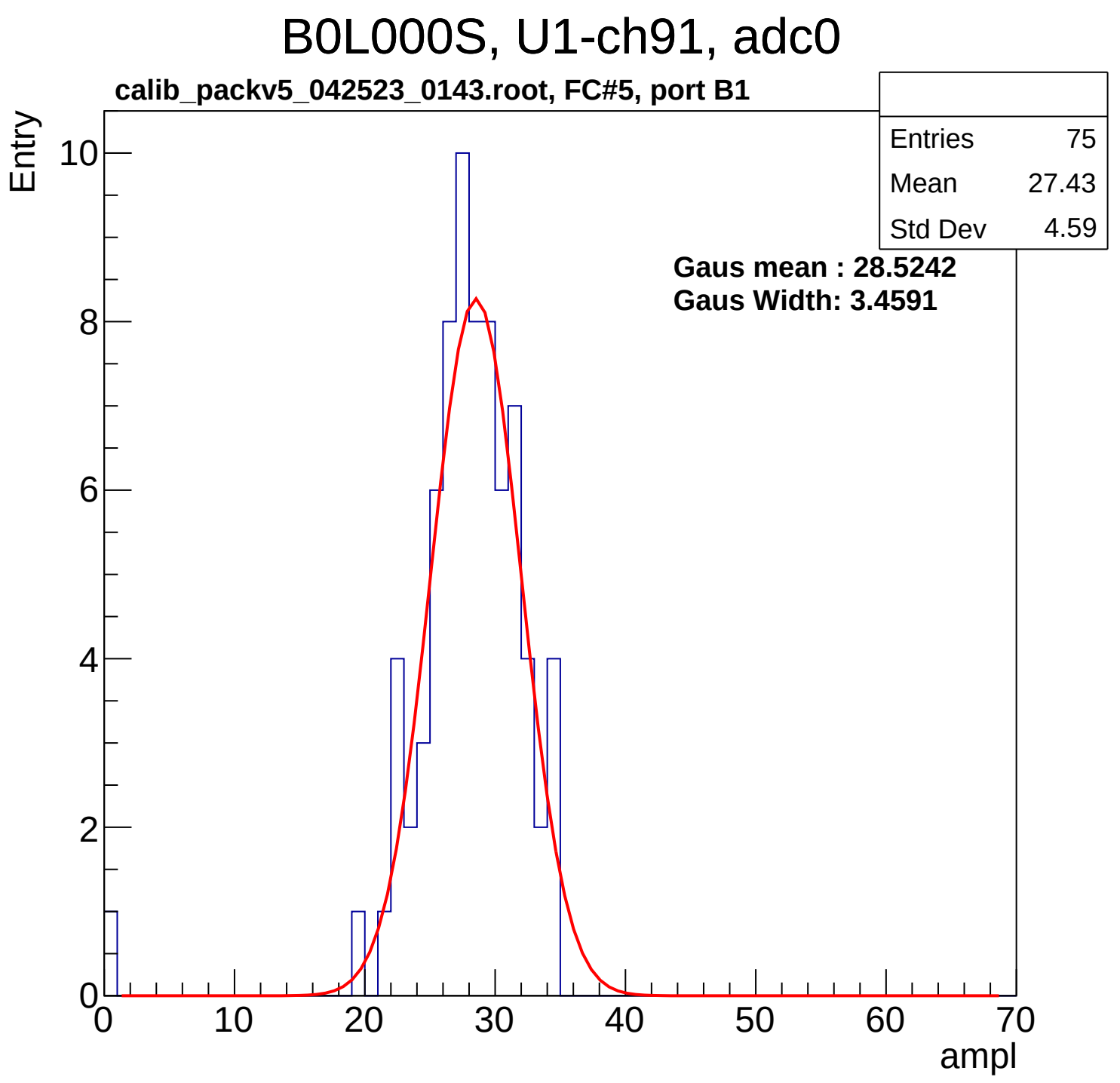
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch91, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 27.43
Std Dev 4.59
Gaus mean : 28.5242
Gaus Width: 3.4591
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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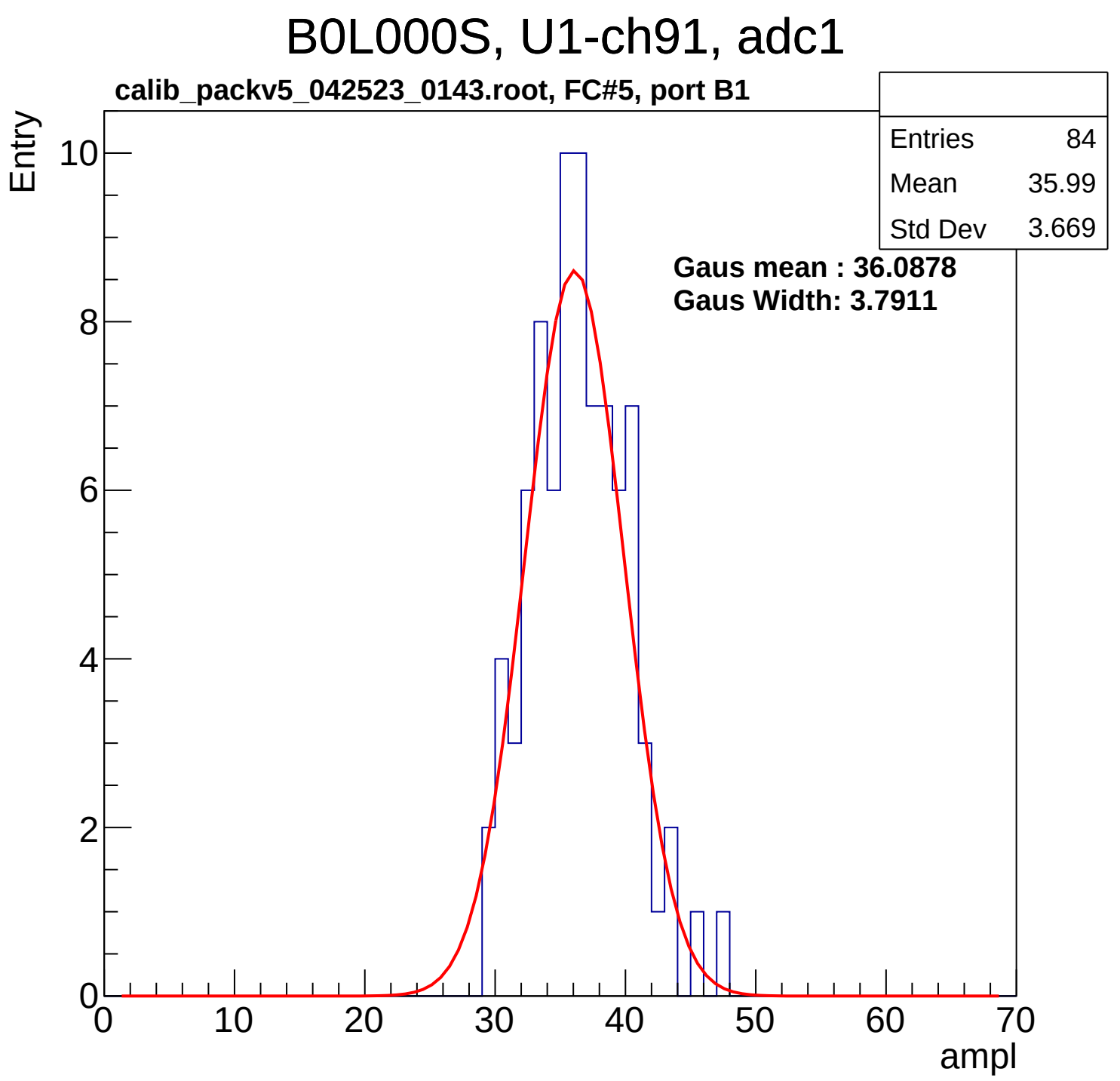
B0L000S, U1-ch91, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 35.99
Std Dev 3.669
Gaus mean : 36.0878
Gaus Width: 3.7911
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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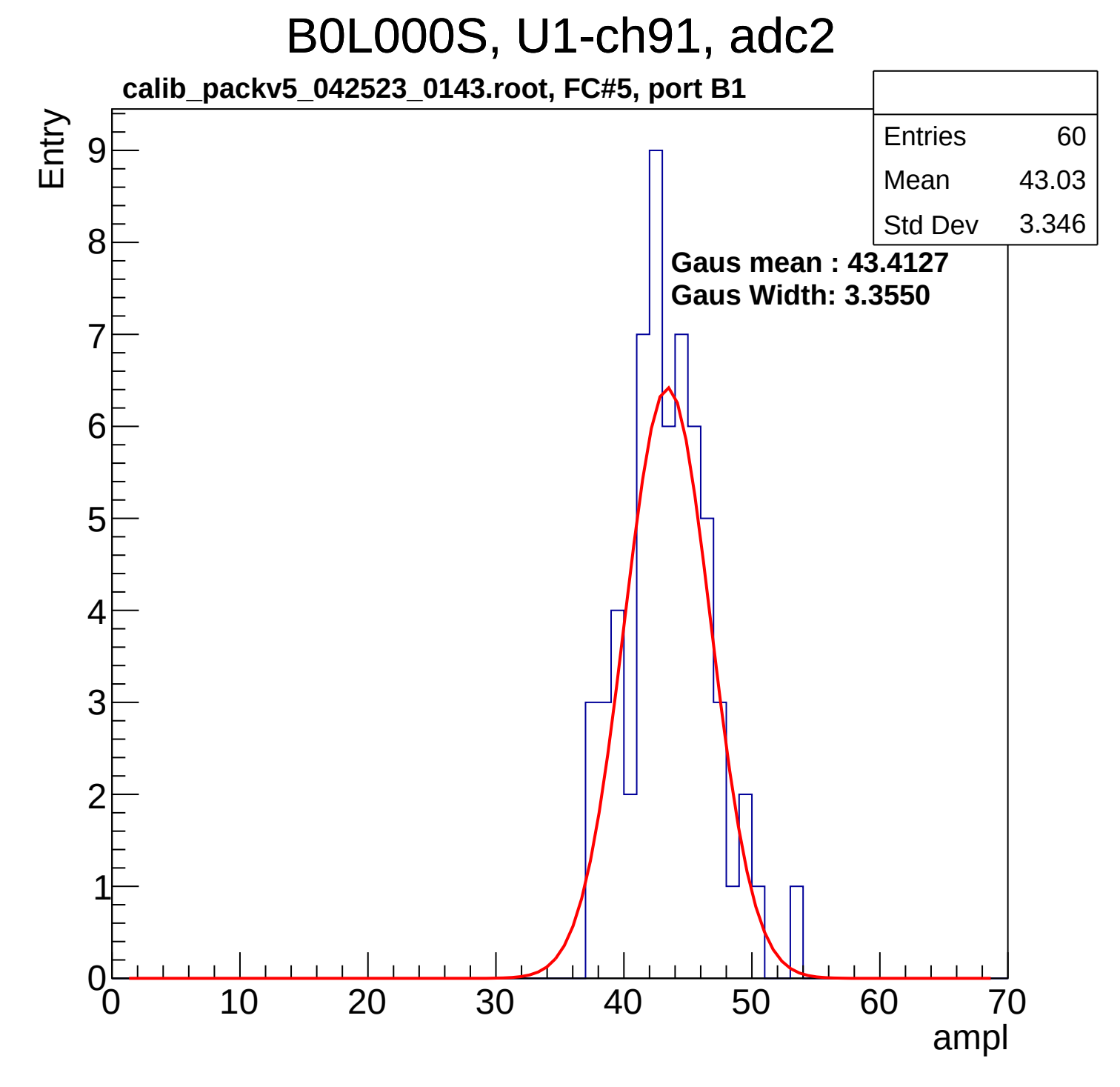
B0L000S, U1-ch91, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.03
Std Dev 3.346
Gaus mean : 43.4127
Gaus Width: 3.3550
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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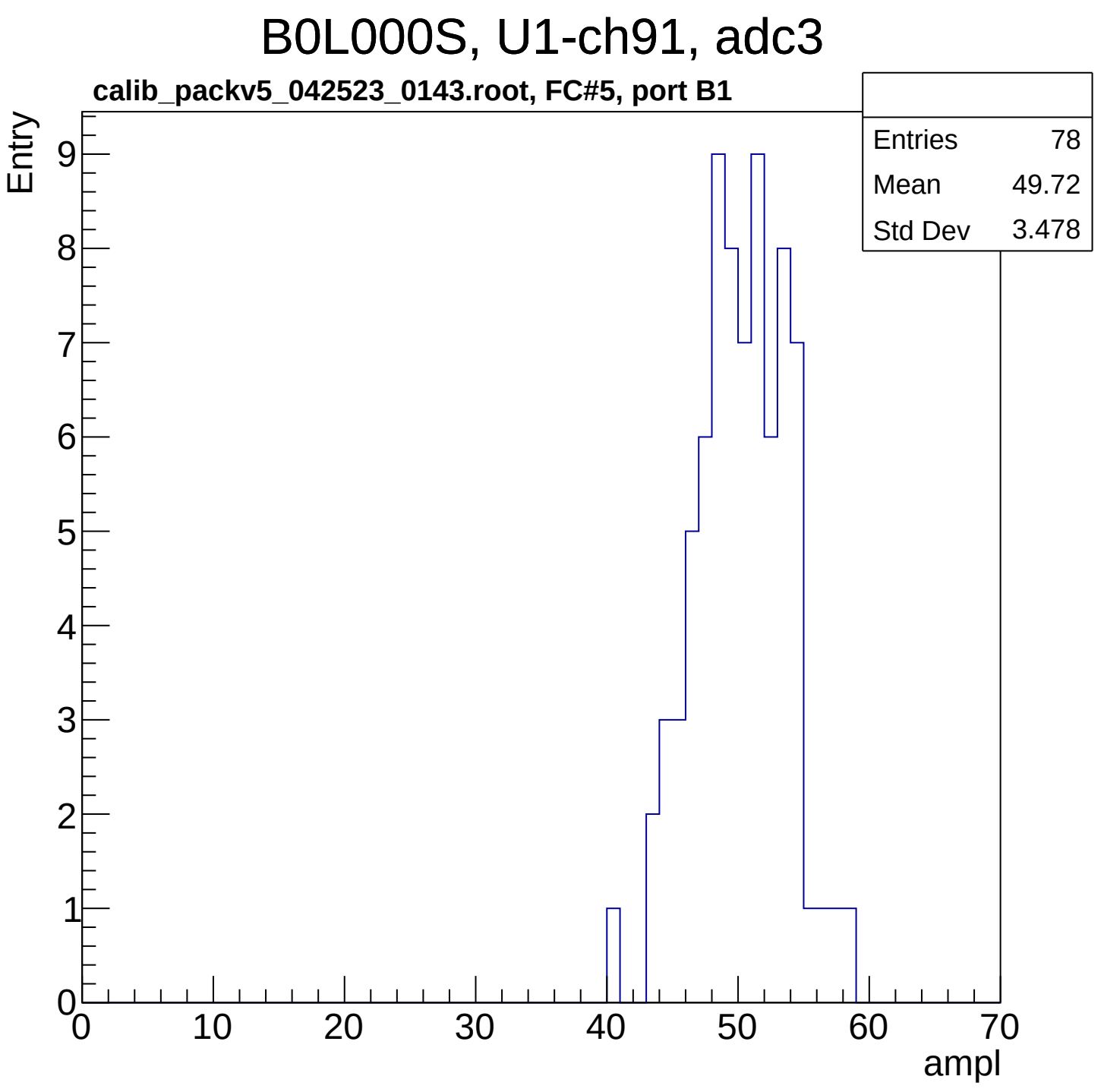
B0L000S, U1-ch91, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 49.72
Std Dev 3.478
Entry
ampl
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6
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8
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0
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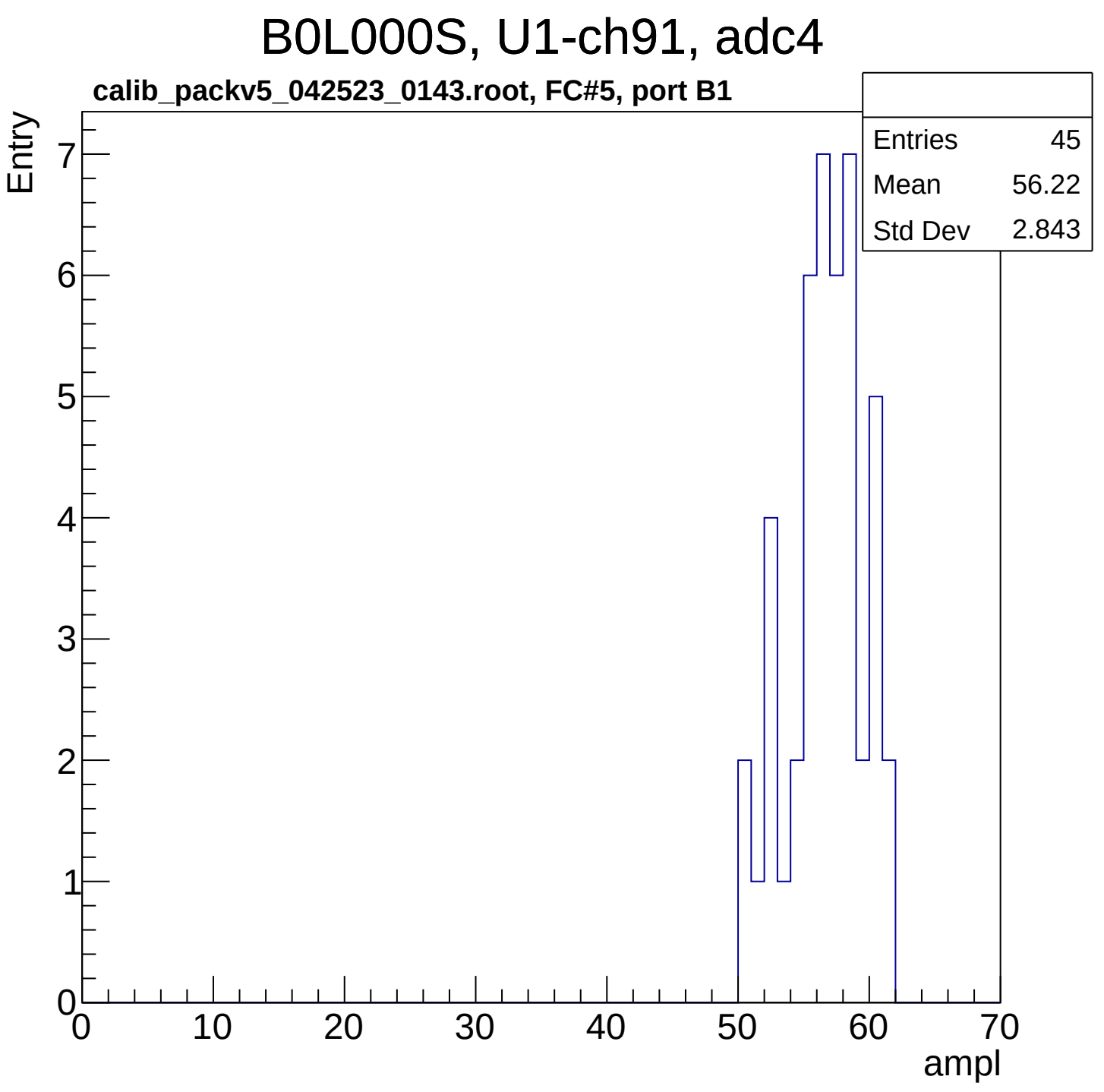
B0L000S, U1-ch91, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 56.22
Std Dev 2.843
Entry
ampl
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4
5
6
7
0
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60
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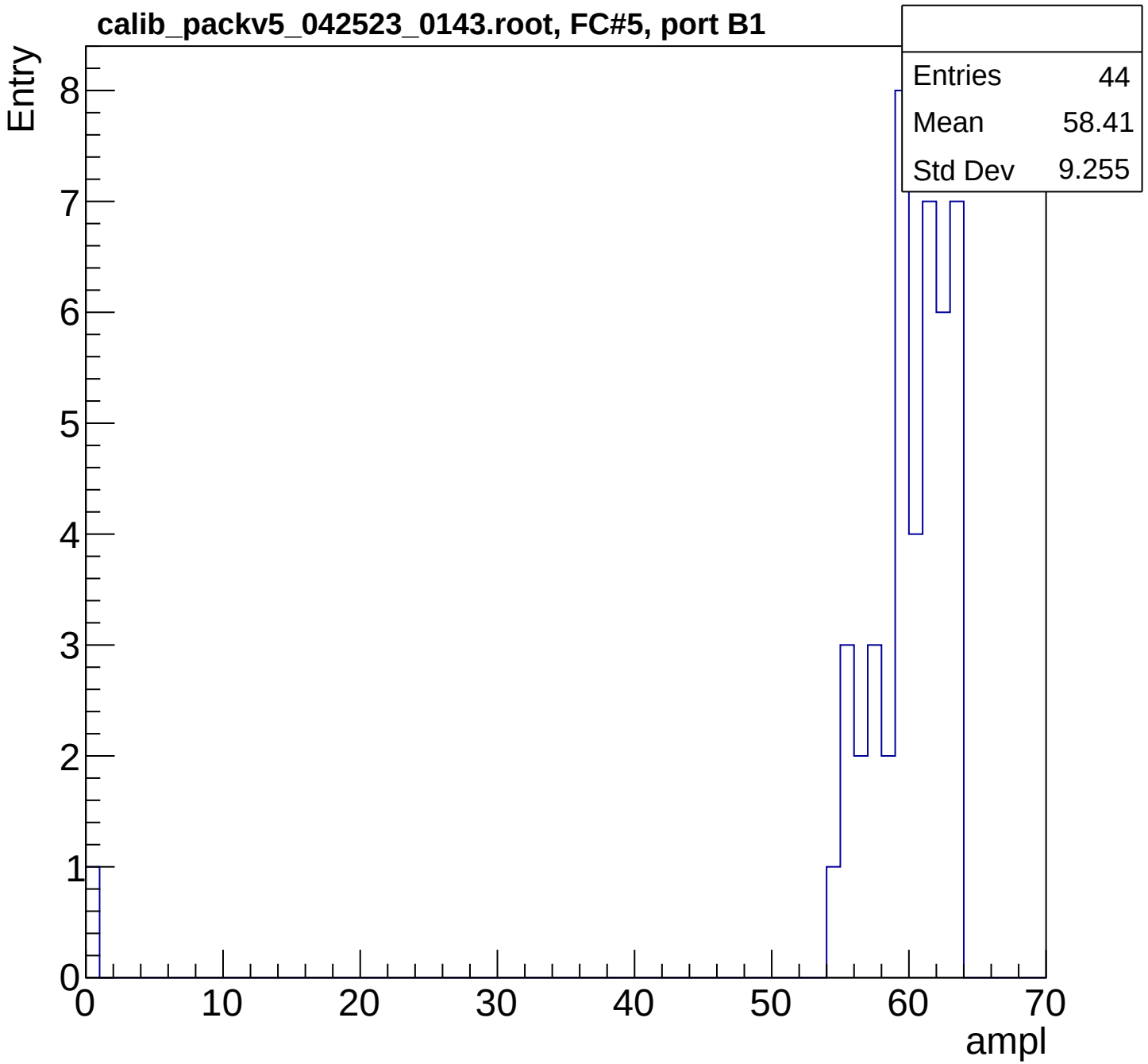
B0L000S, U1-ch91, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.41
Std Dev 9.255
Entry
ampl
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4
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0
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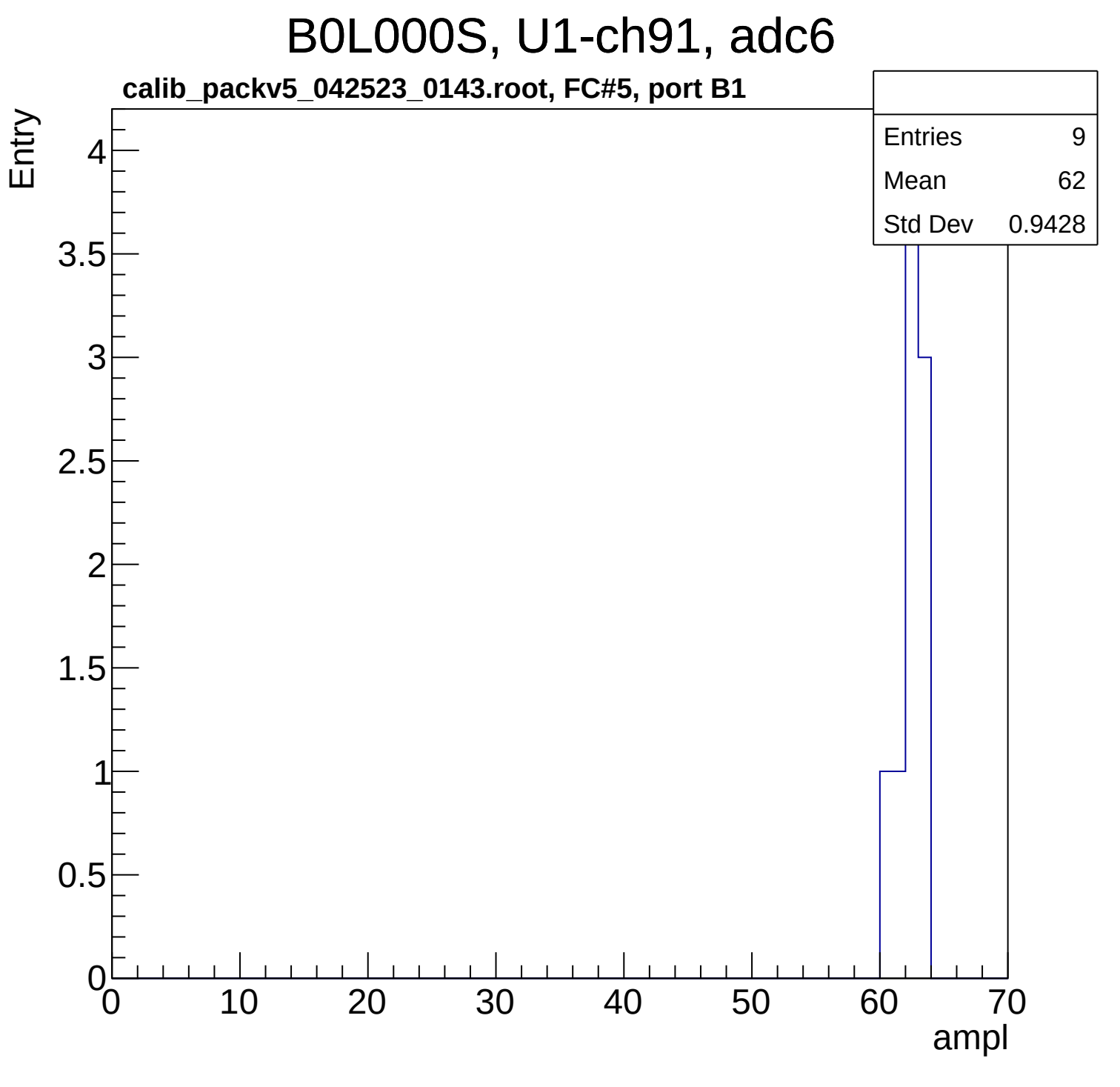
B0L000S, U1-ch91, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62
Std Dev 0.9428
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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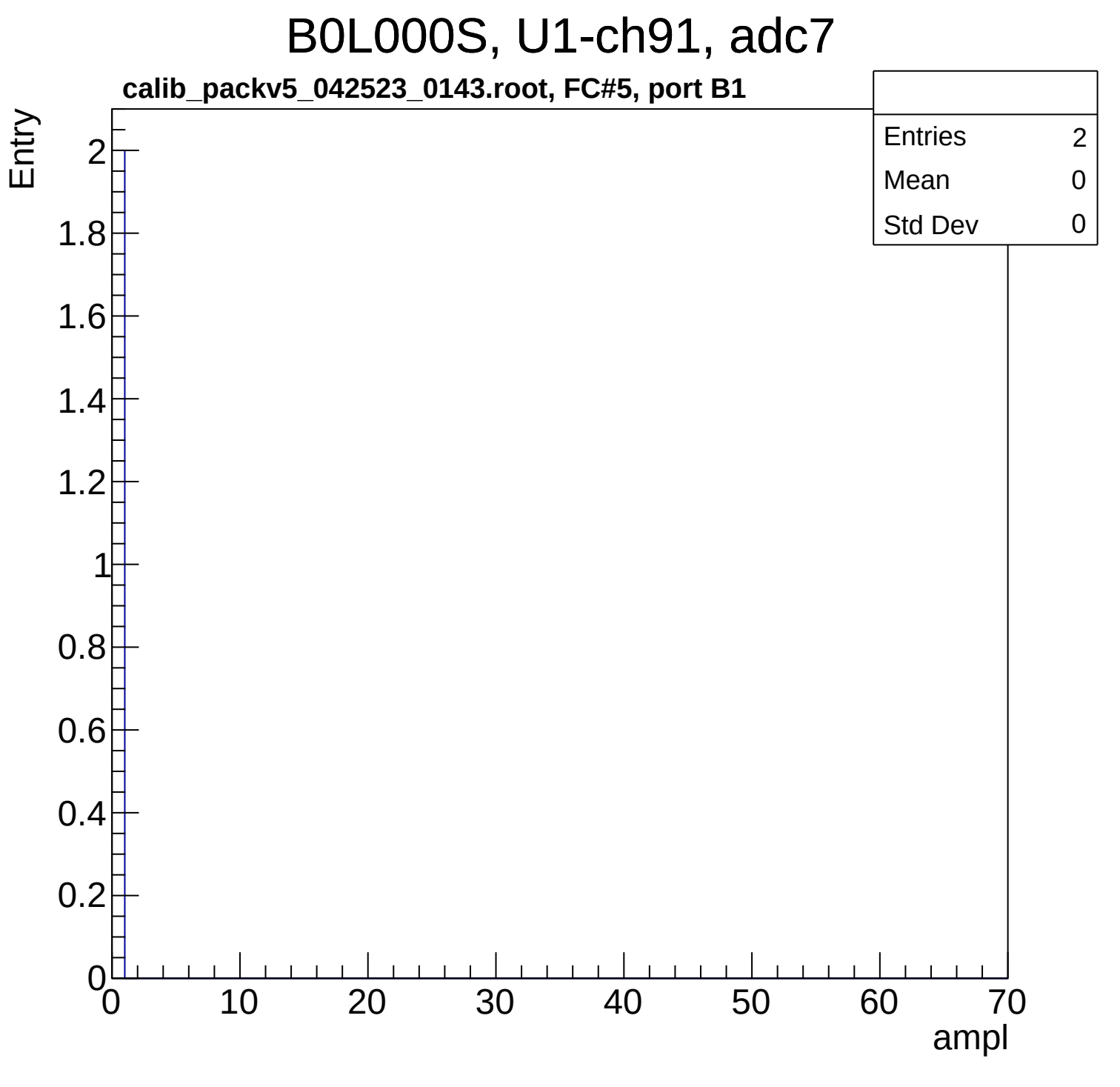
B0L000S, U1-ch91, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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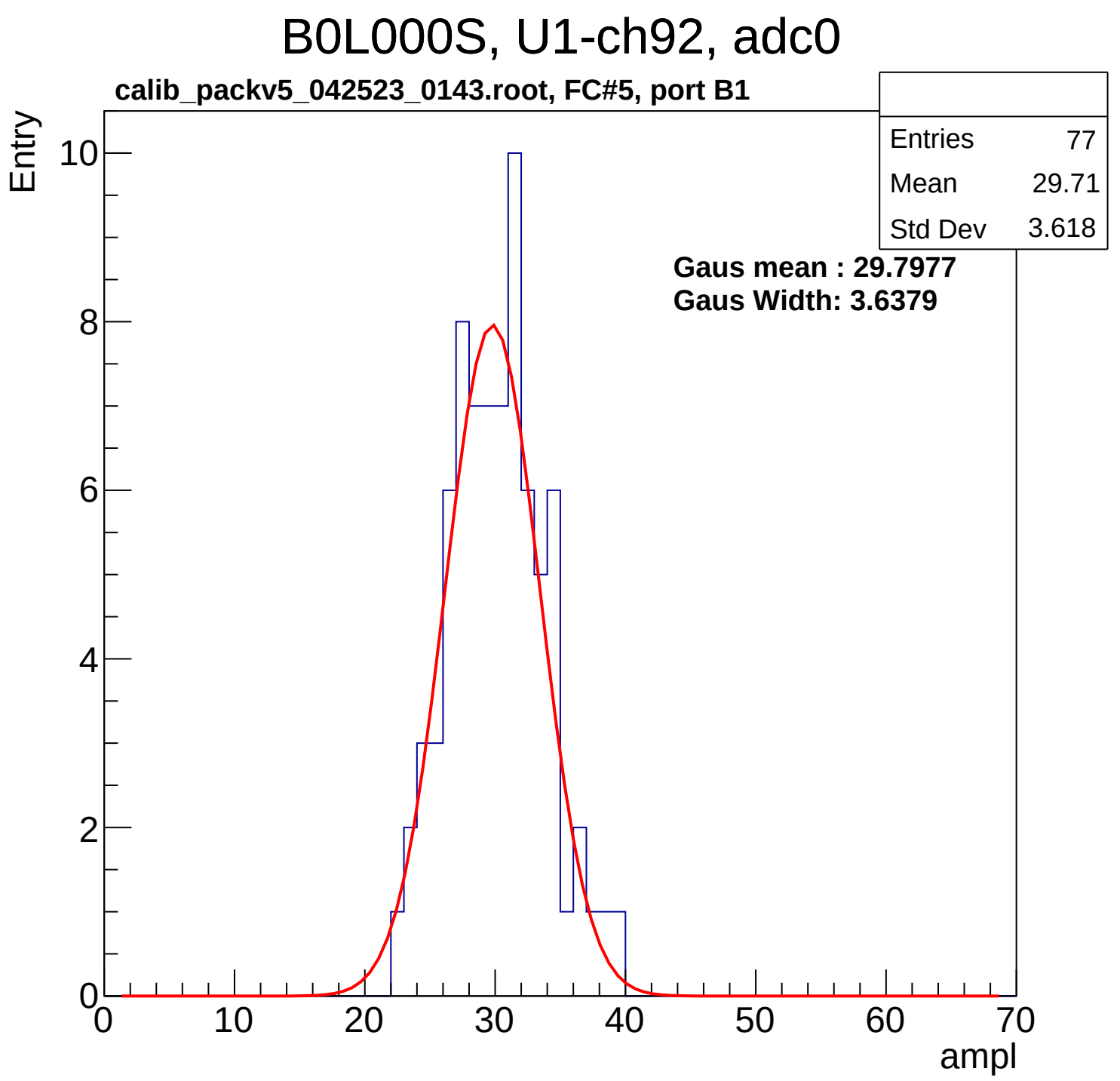
B0L000S, U1-ch92, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29.71
Std Dev 3.618
Gaus mean : 29.7977
Gaus Width: 3.6379
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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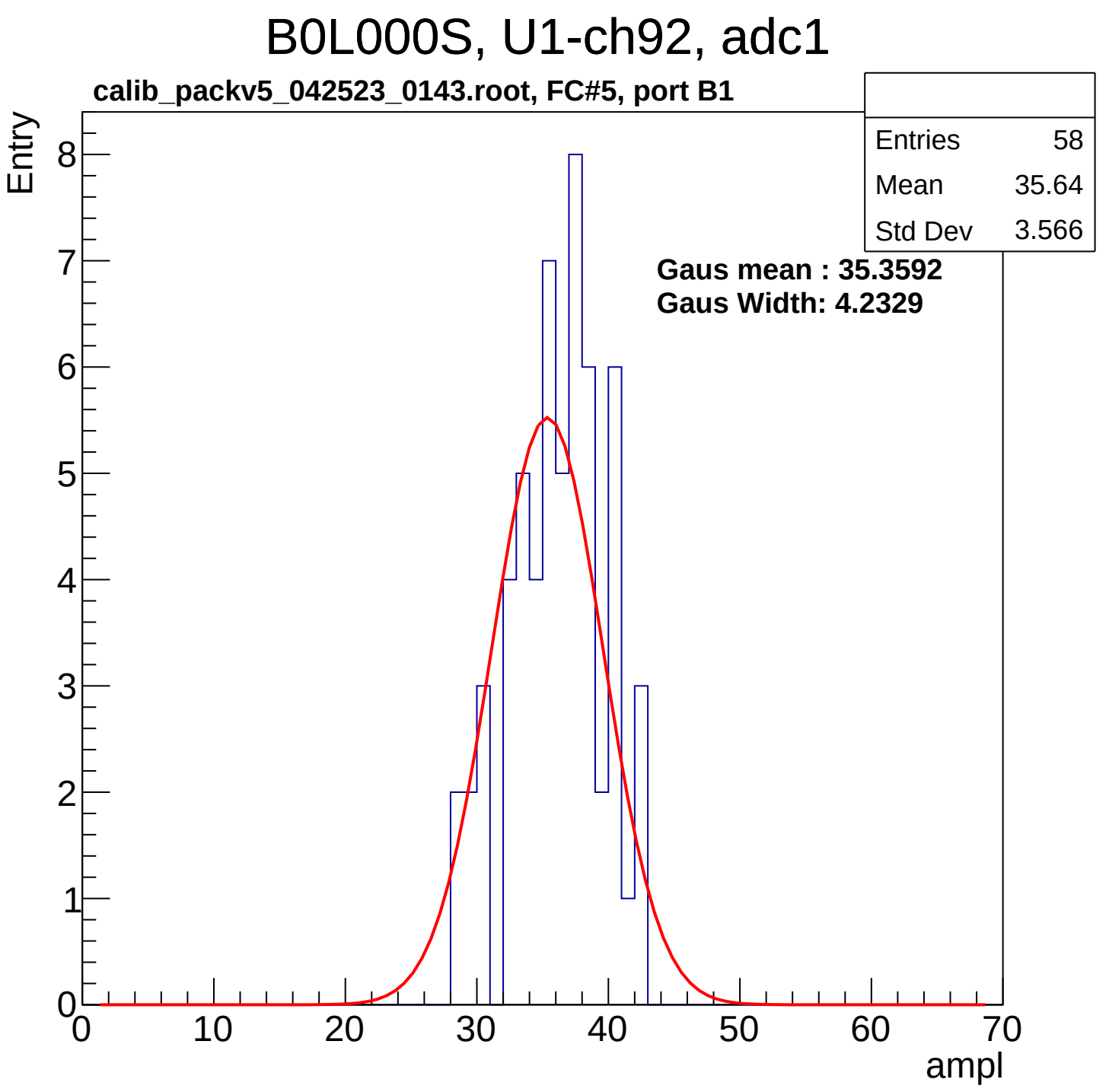
B0L000S, U1-ch92, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.64
Std Dev 3.566
Gaus mean : 35.3592
Gaus Width: 4.2329
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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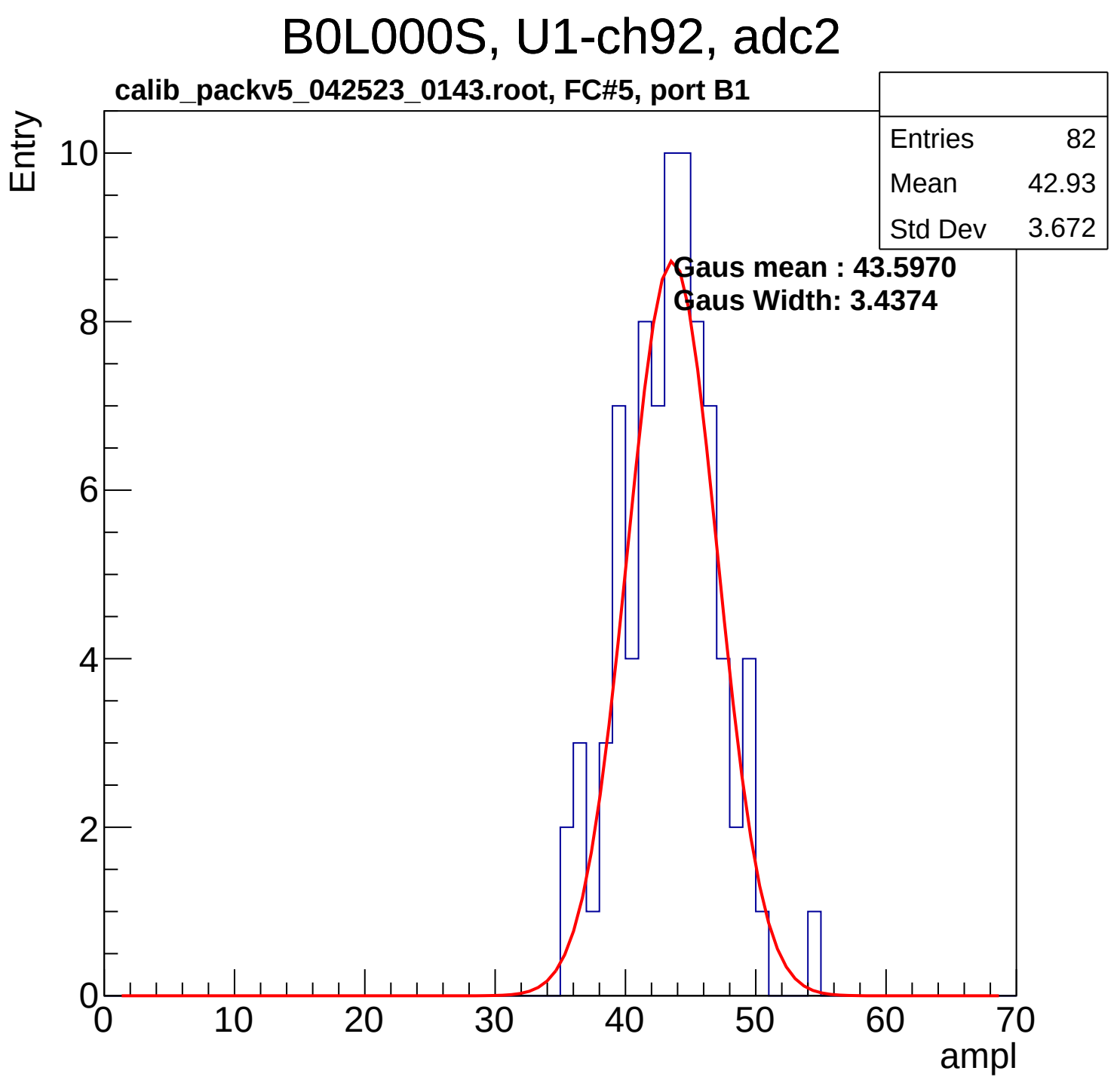
B0L000S, U1-ch92, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 42.93
Std Dev 3.672
Gaus mean : 43.5970
Gaus Width: 3.4374
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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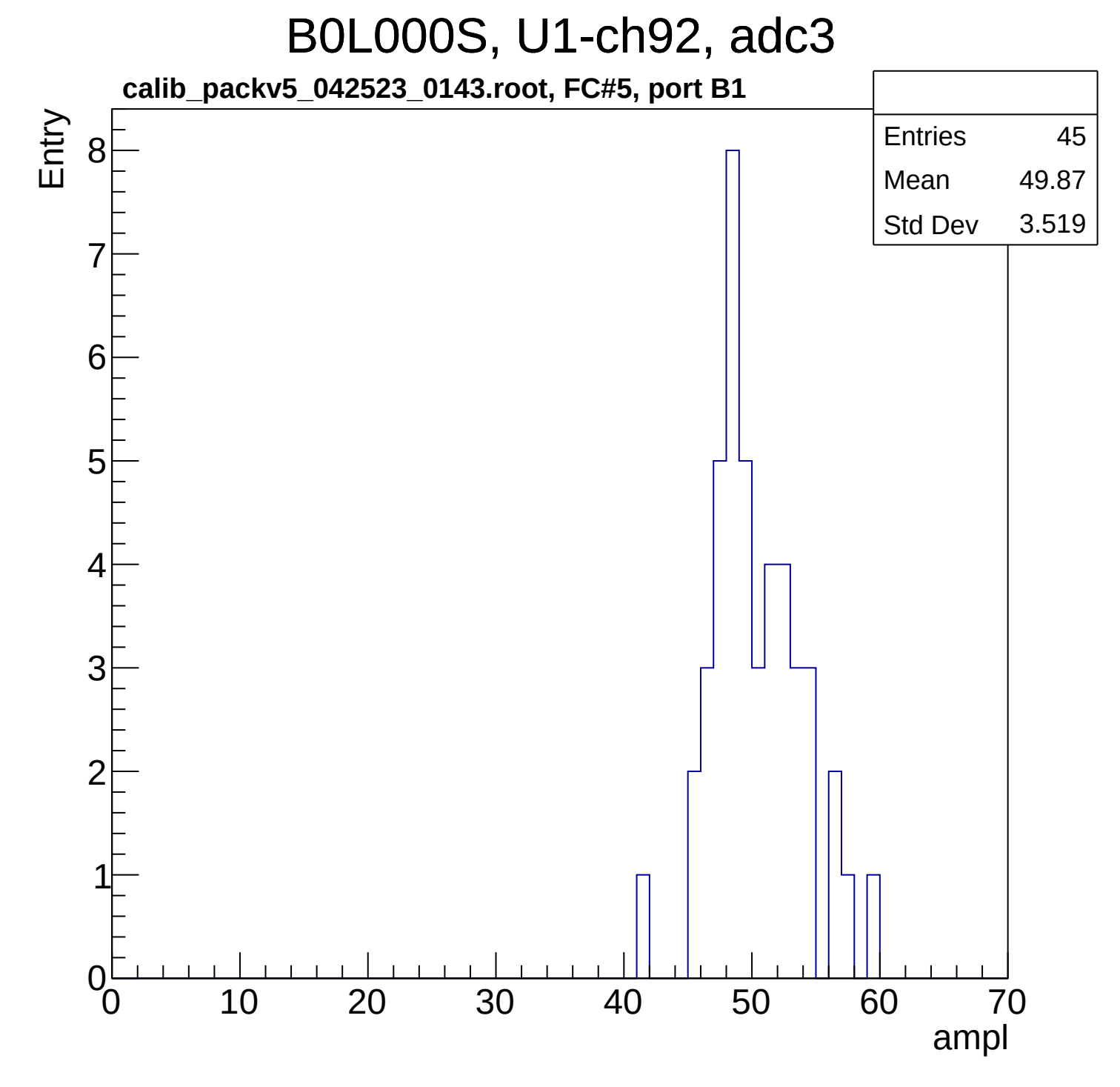
B0L000S, U1-ch92, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 49.87
Std Dev 3.519
Entry
ampl
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2
3
4
5
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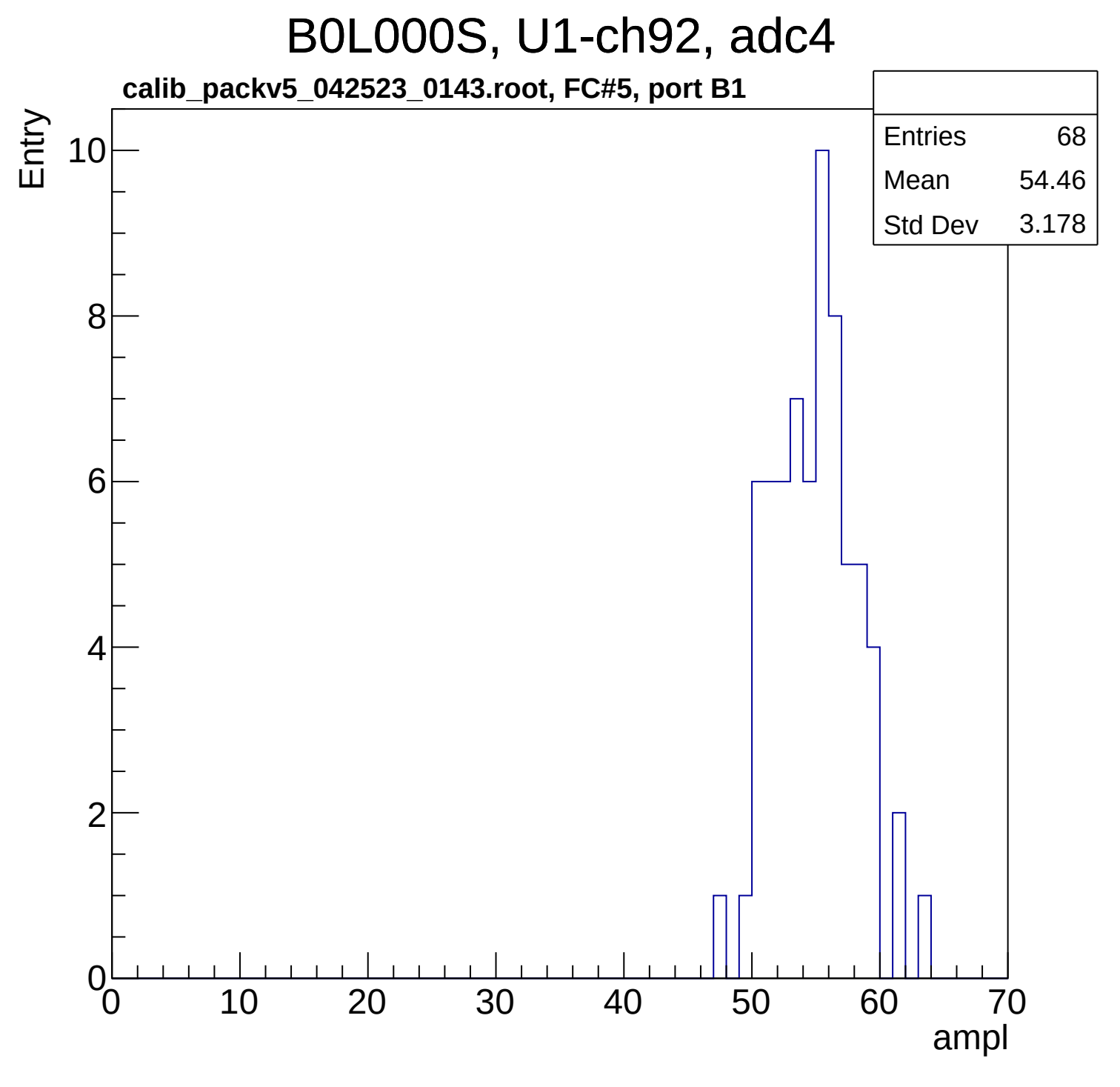
B0L000S, U1-ch92, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 54.46
Std Dev 3.178
Entry
ampl
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6
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60
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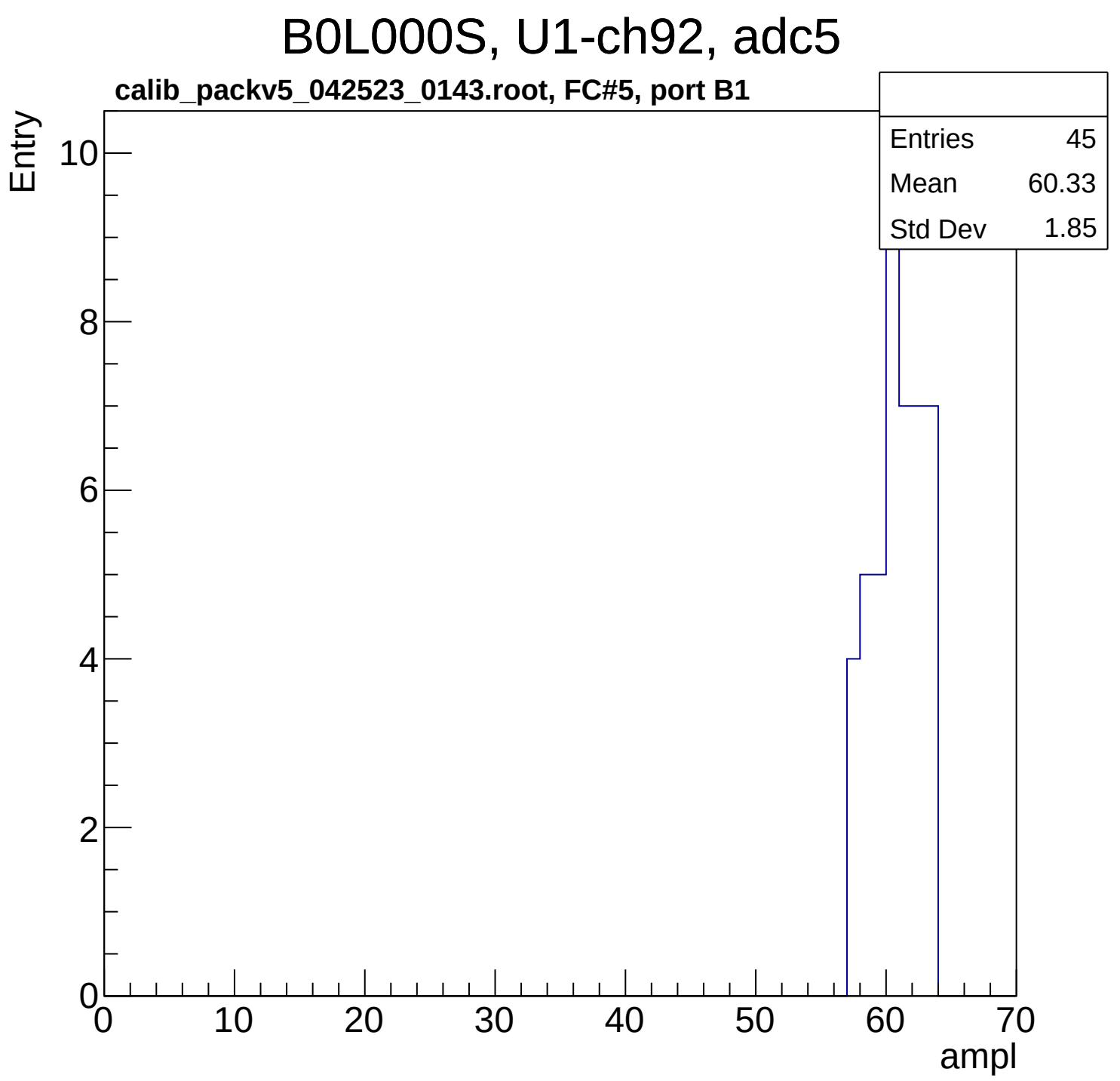
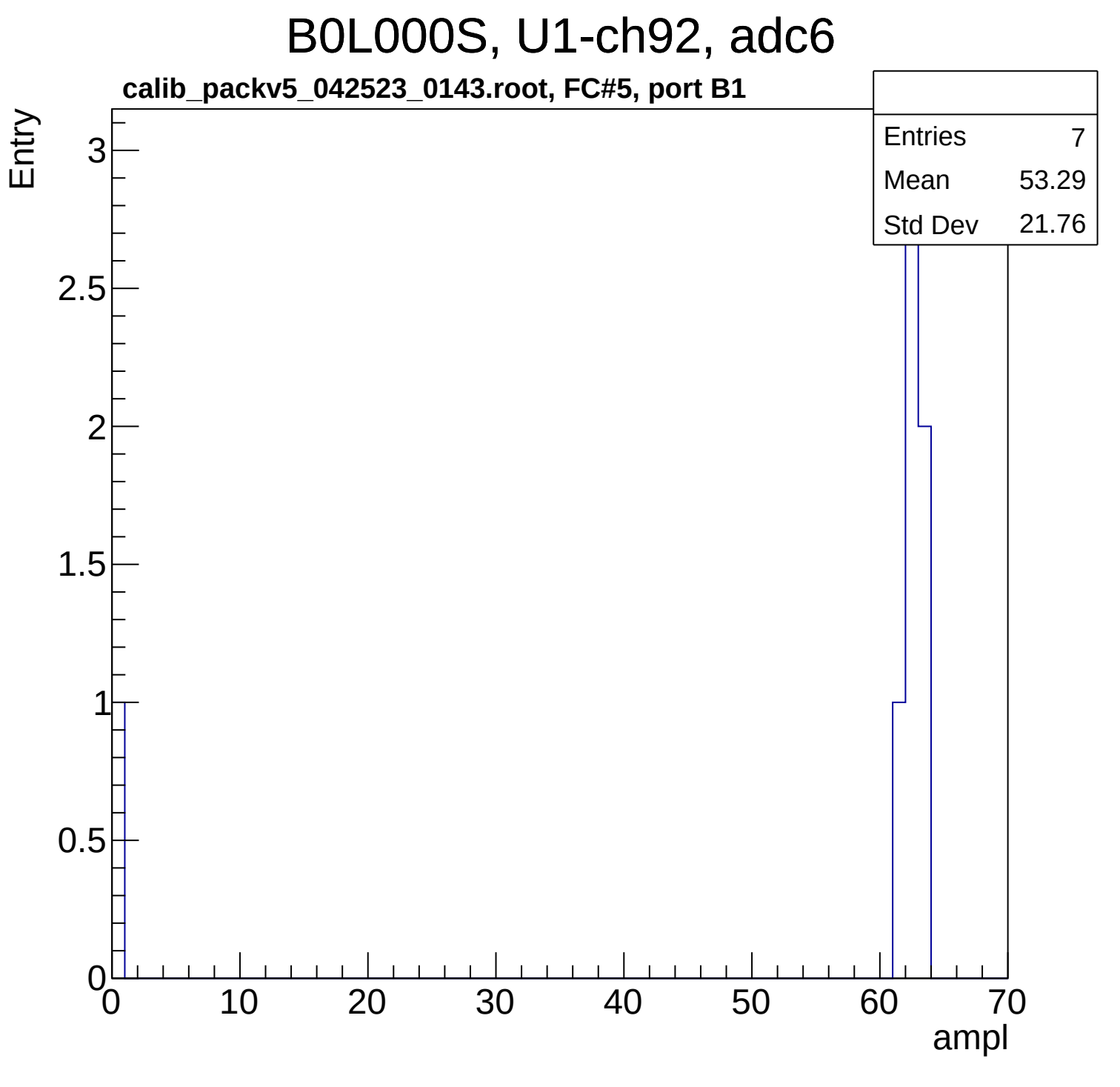

B0L000S, U1-ch92, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.33
Std Dev 1.85
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch92, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.29
Std Dev 21.76
Entry
0
0.5
1
1.5
2
2.5
3
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10
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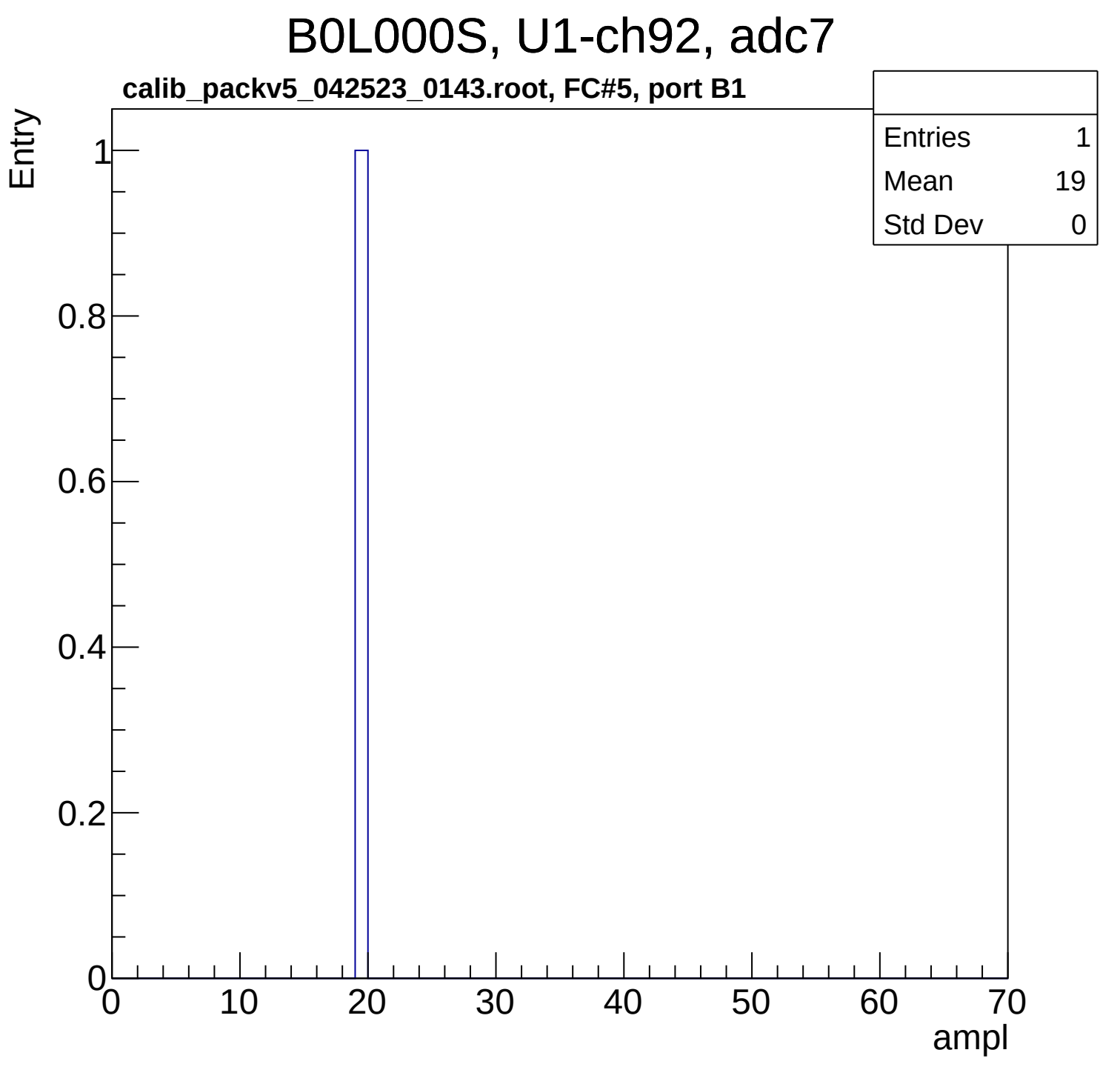
70
ampl


B0L000S, U1-ch92, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 19
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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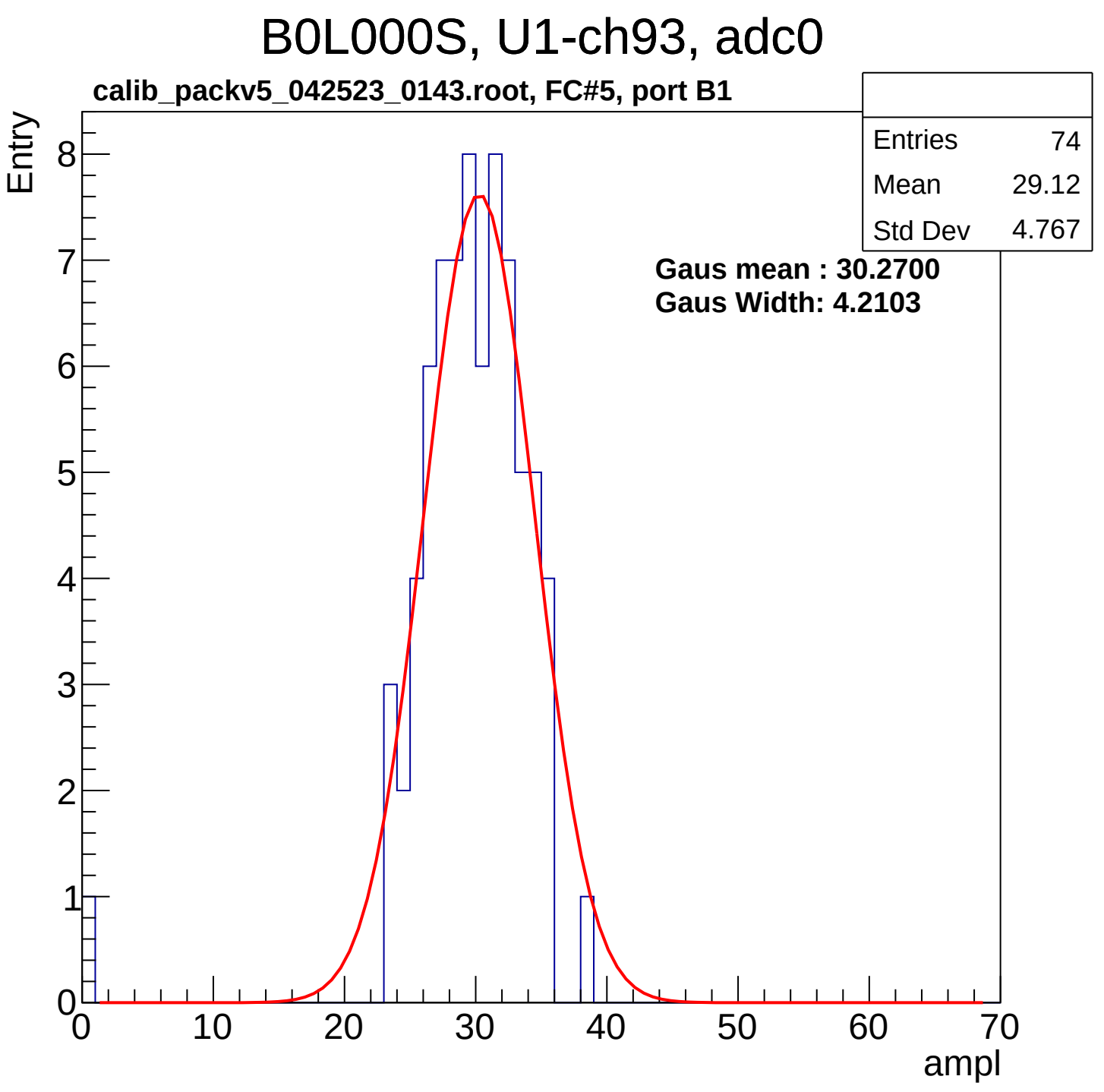
50
60
70
ampl


B0L000S, U1-ch93, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 29.12
Std Dev 4.767
Gaus mean : 30.2700
Gaus Width: 4.2103
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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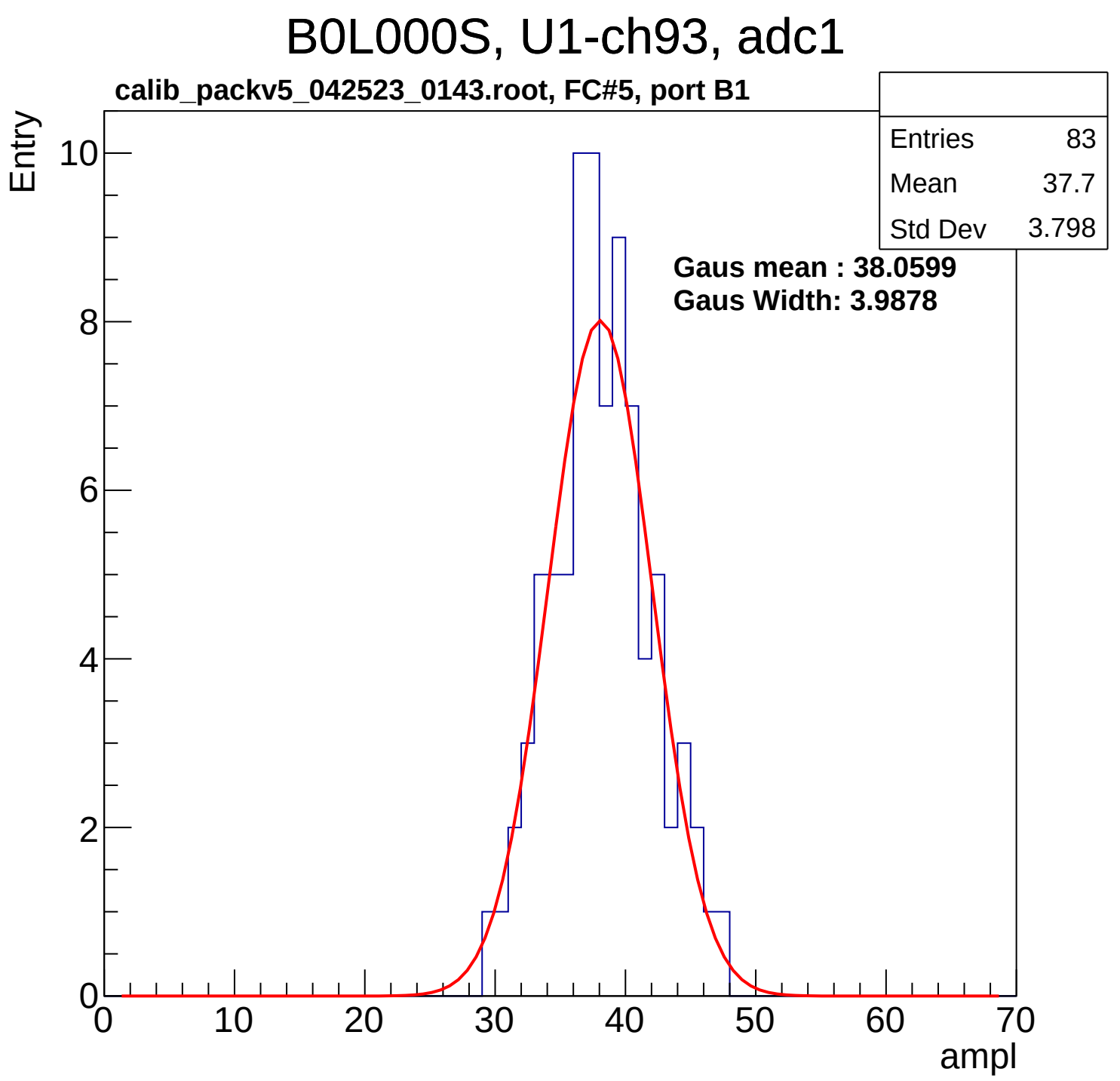
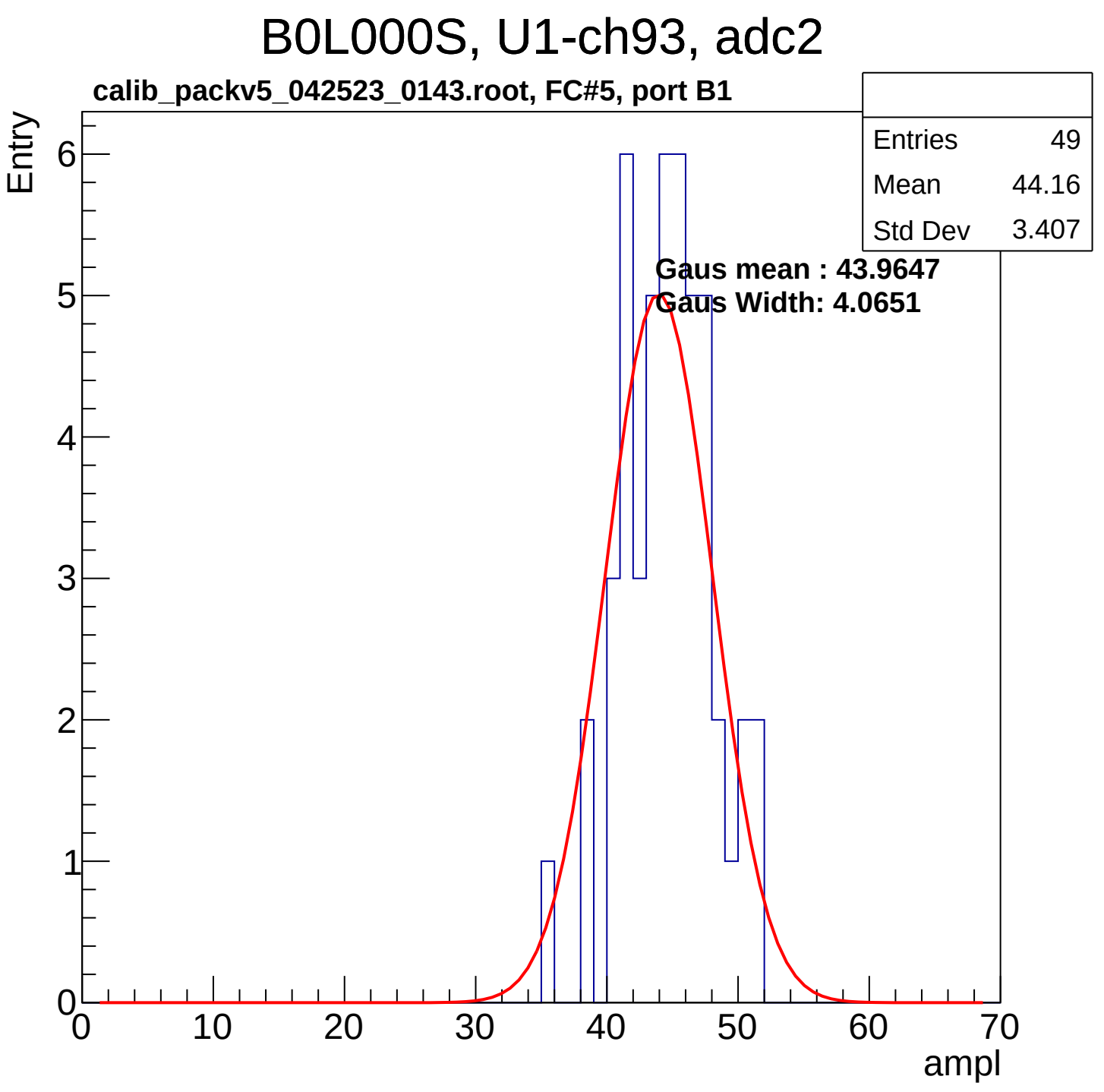

B0L000S, U1-ch93, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 37.7
Std Dev 3.798
Gaus mean : 38.0599
Gaus Width: 3.9878
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch93, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 44.16
Std Dev 3.407
Gaus mean : 43.9647
Gaus Width: 4.0651
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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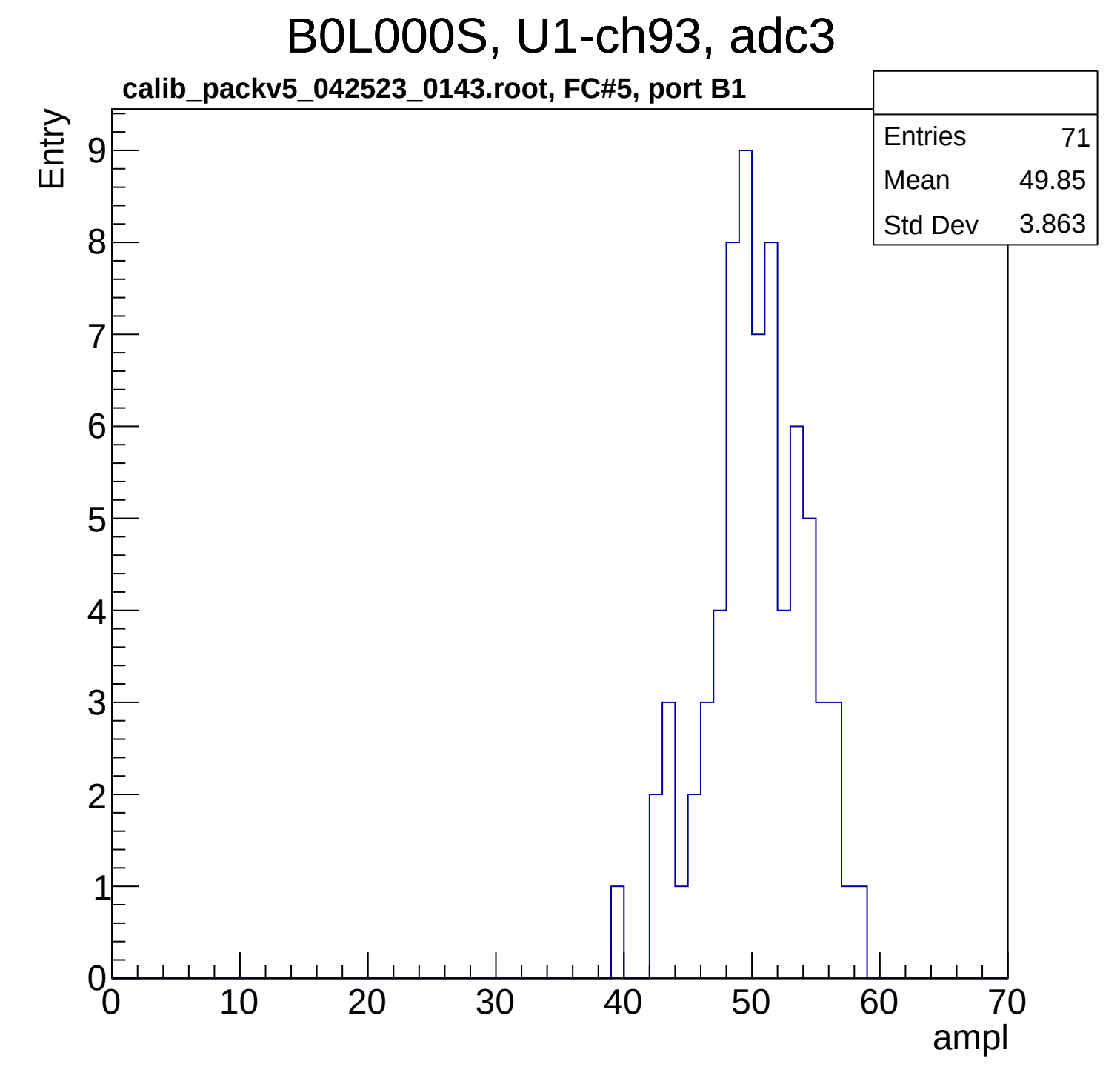
B0L000S, U1-ch93, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 49.85
Std Dev 3.863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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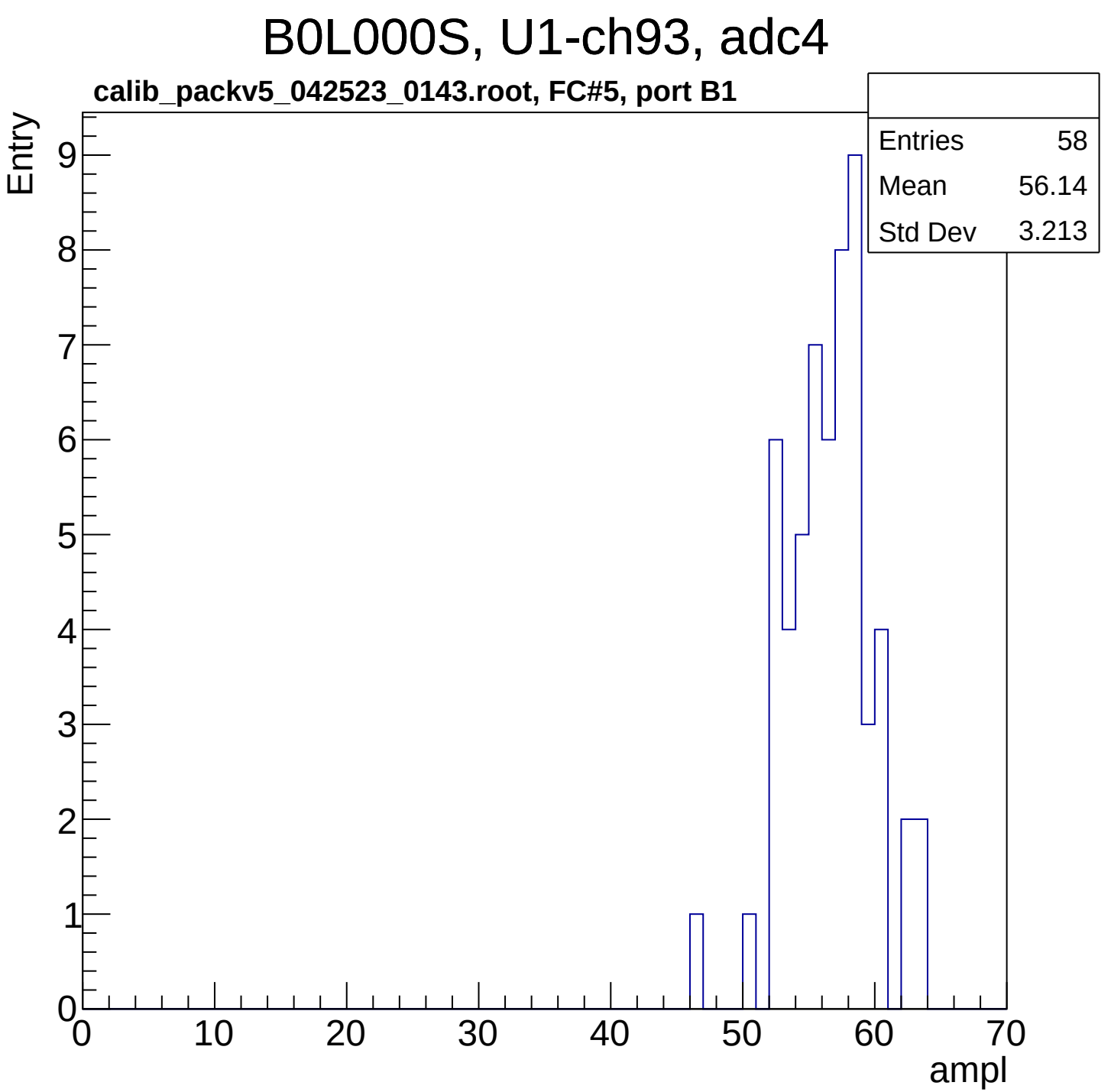
B0L000S, U1-ch93, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.14
Std Dev 3.213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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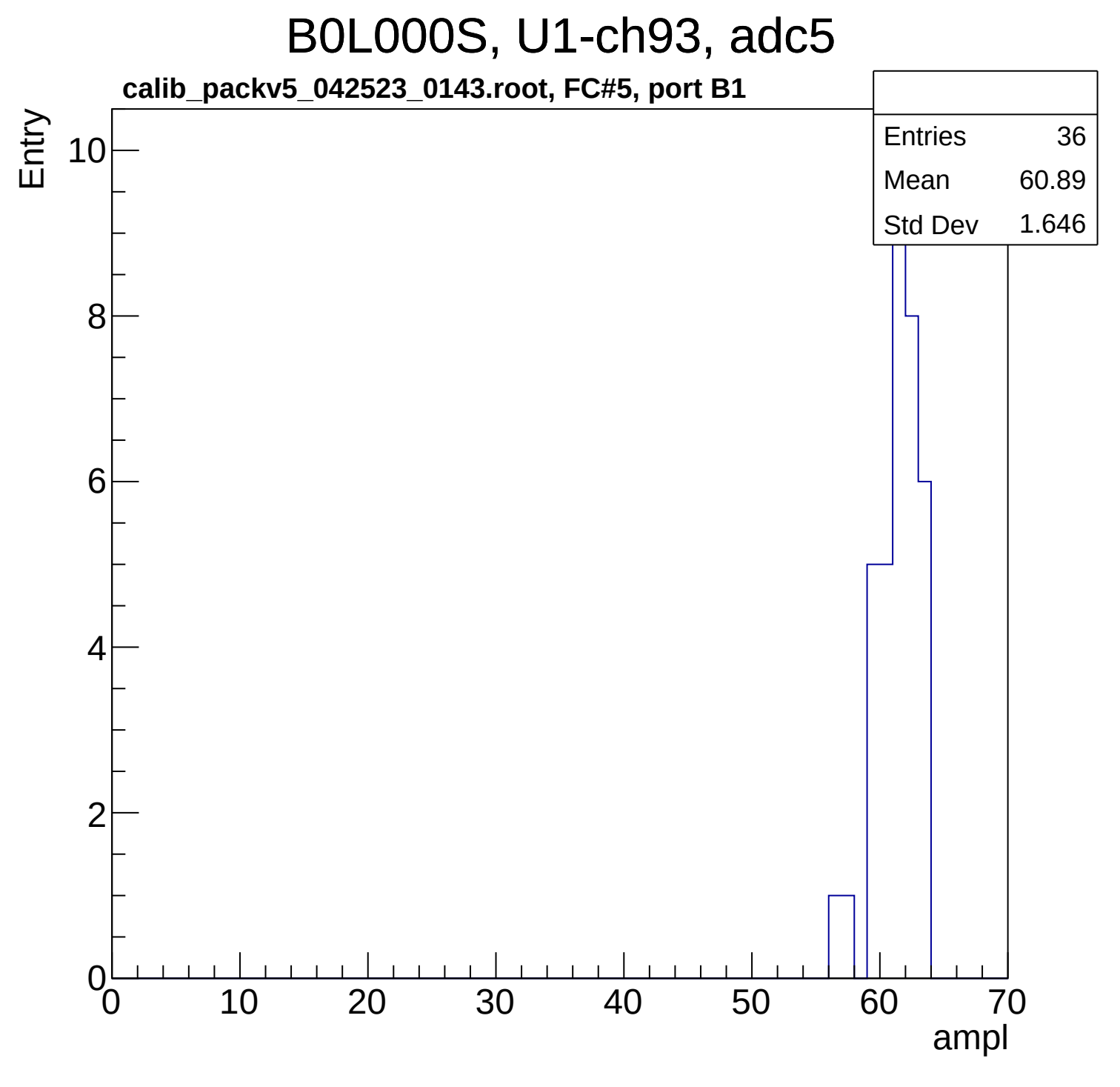
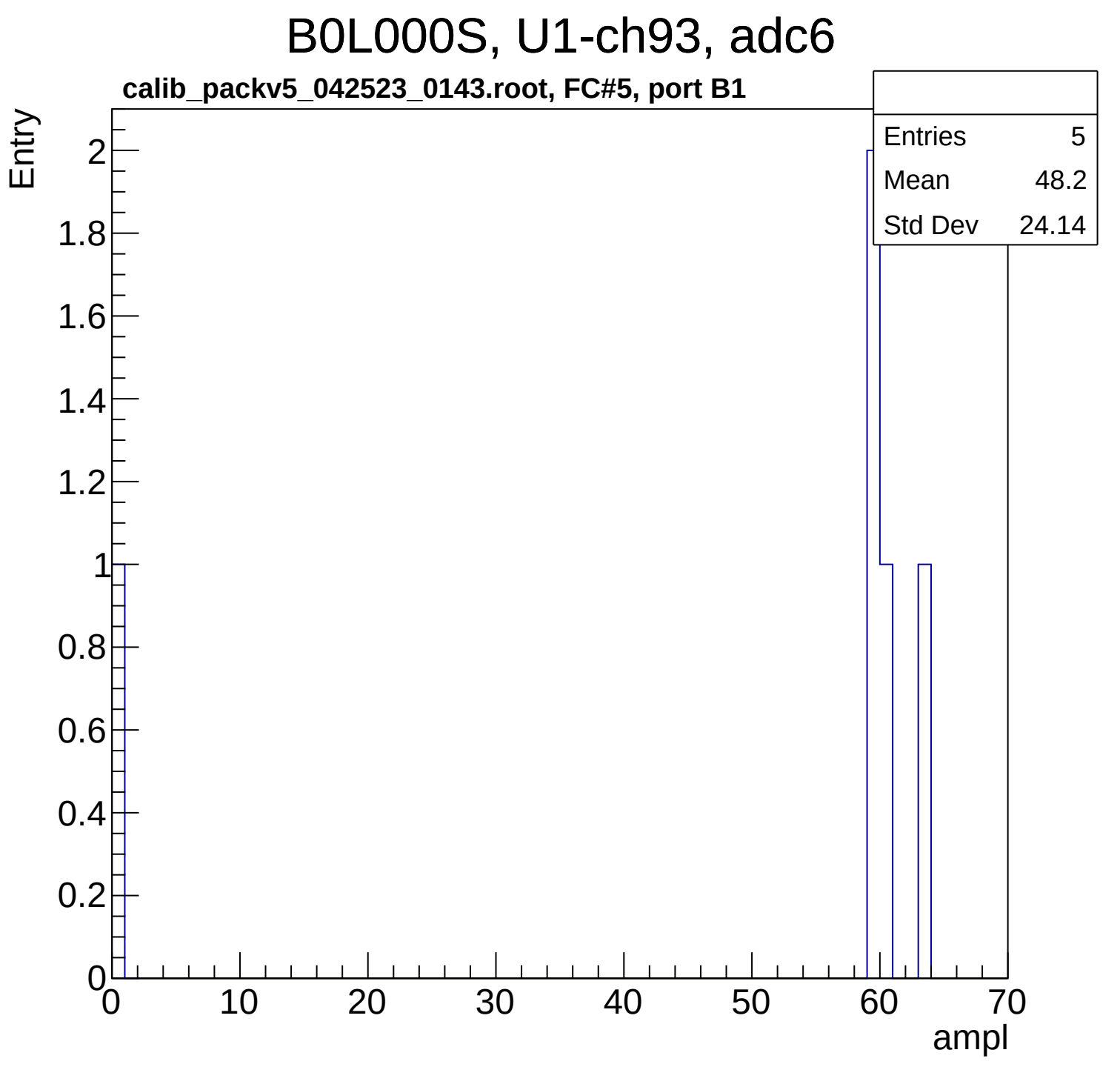
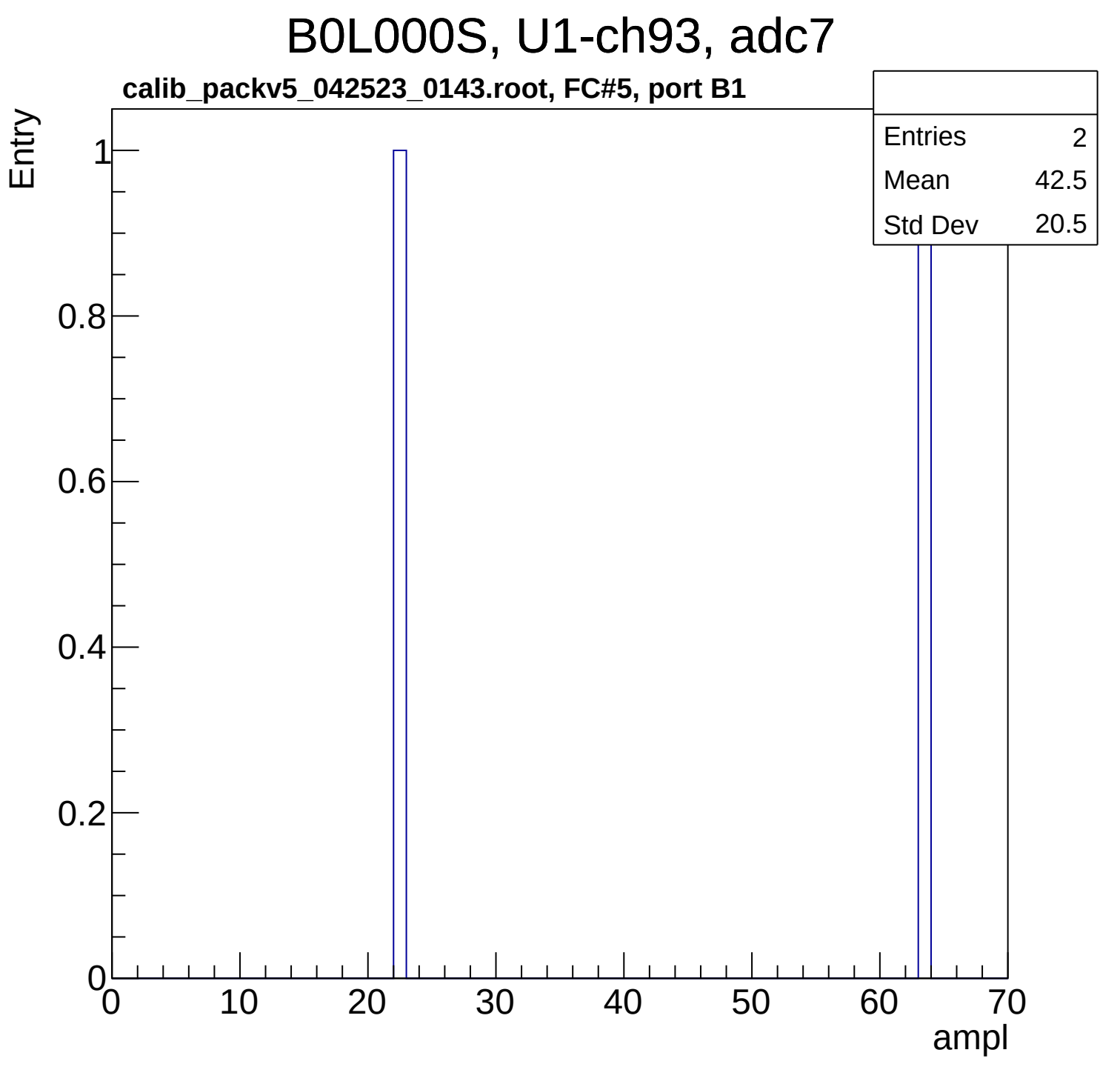
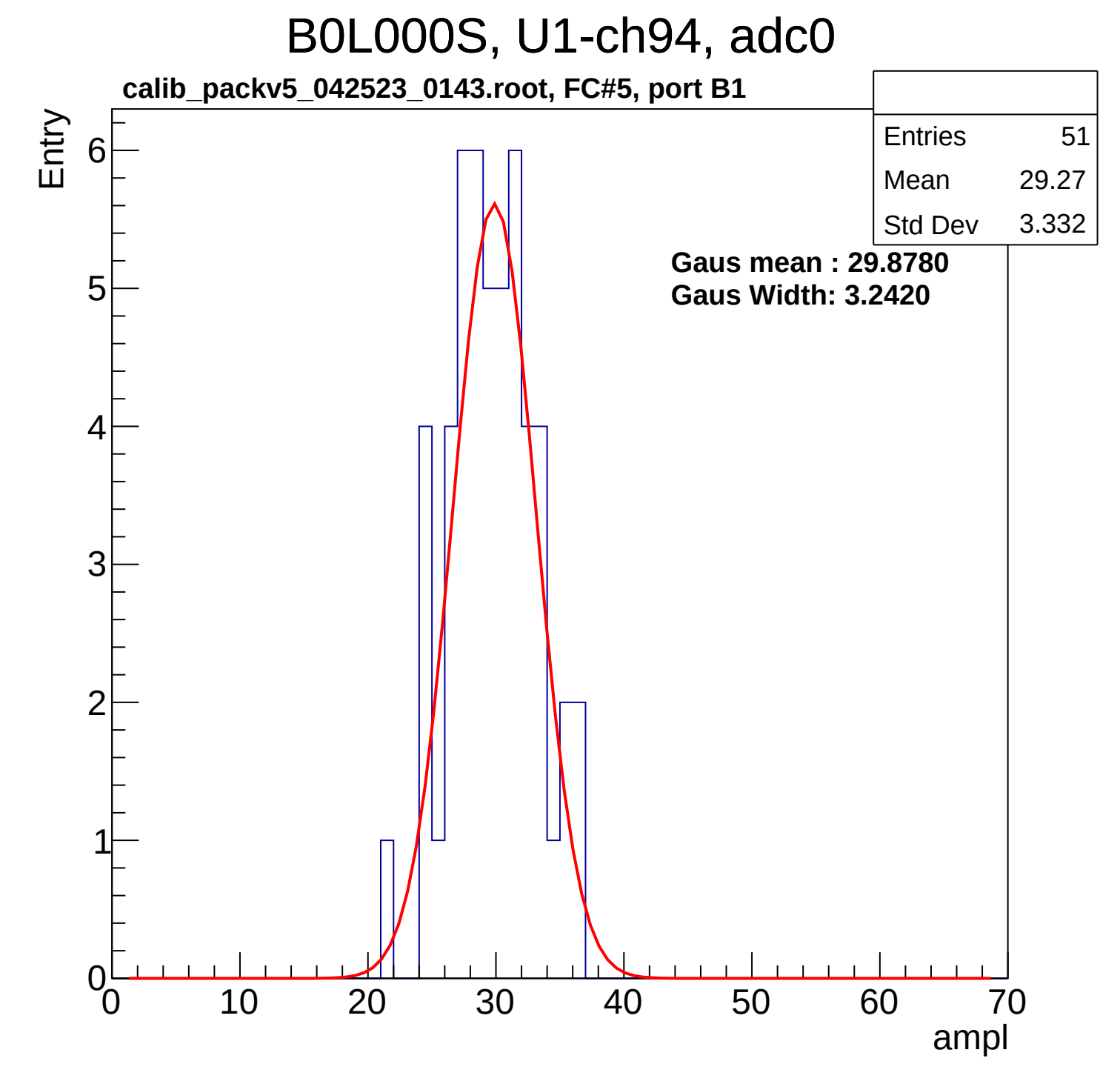
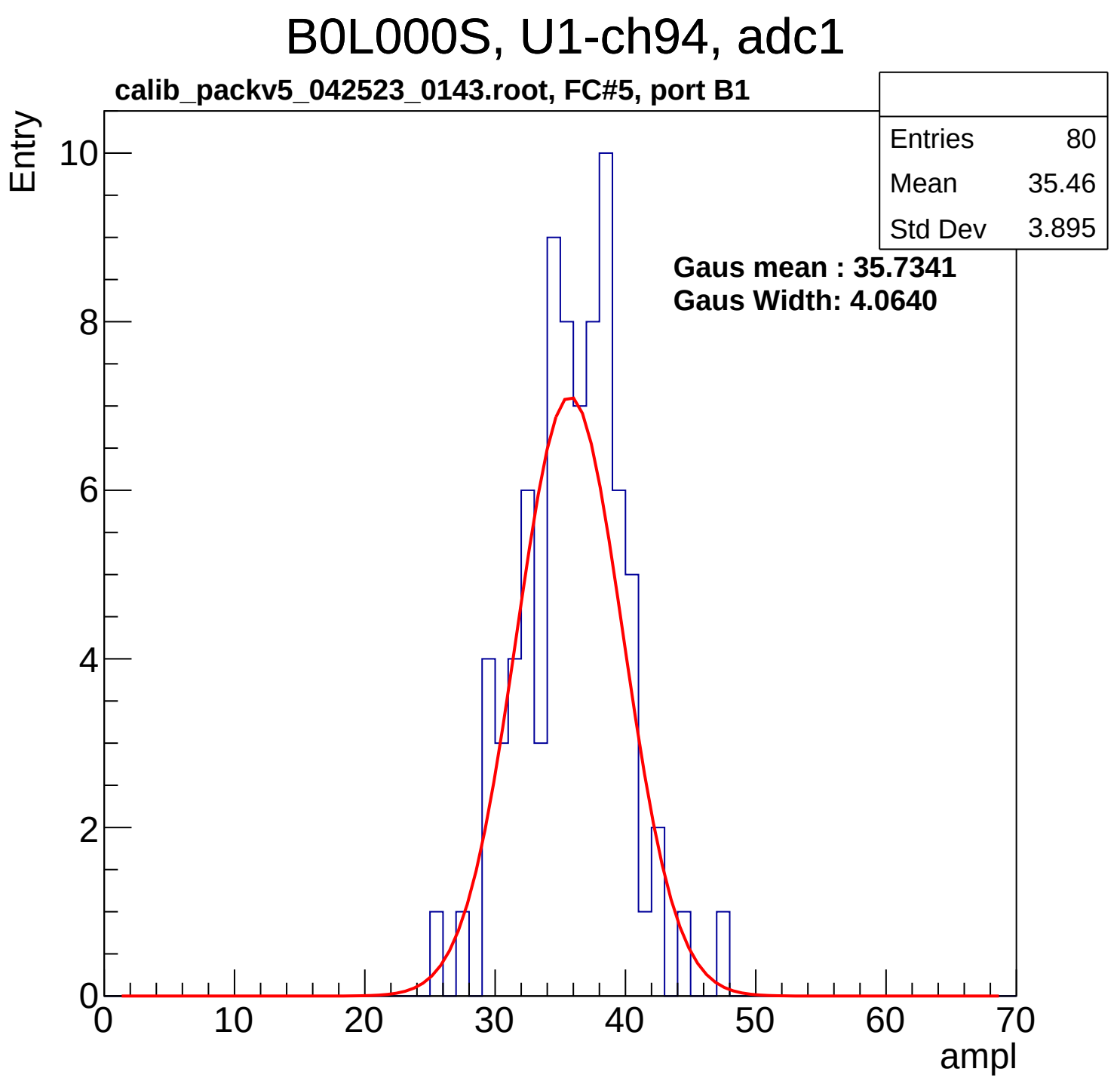
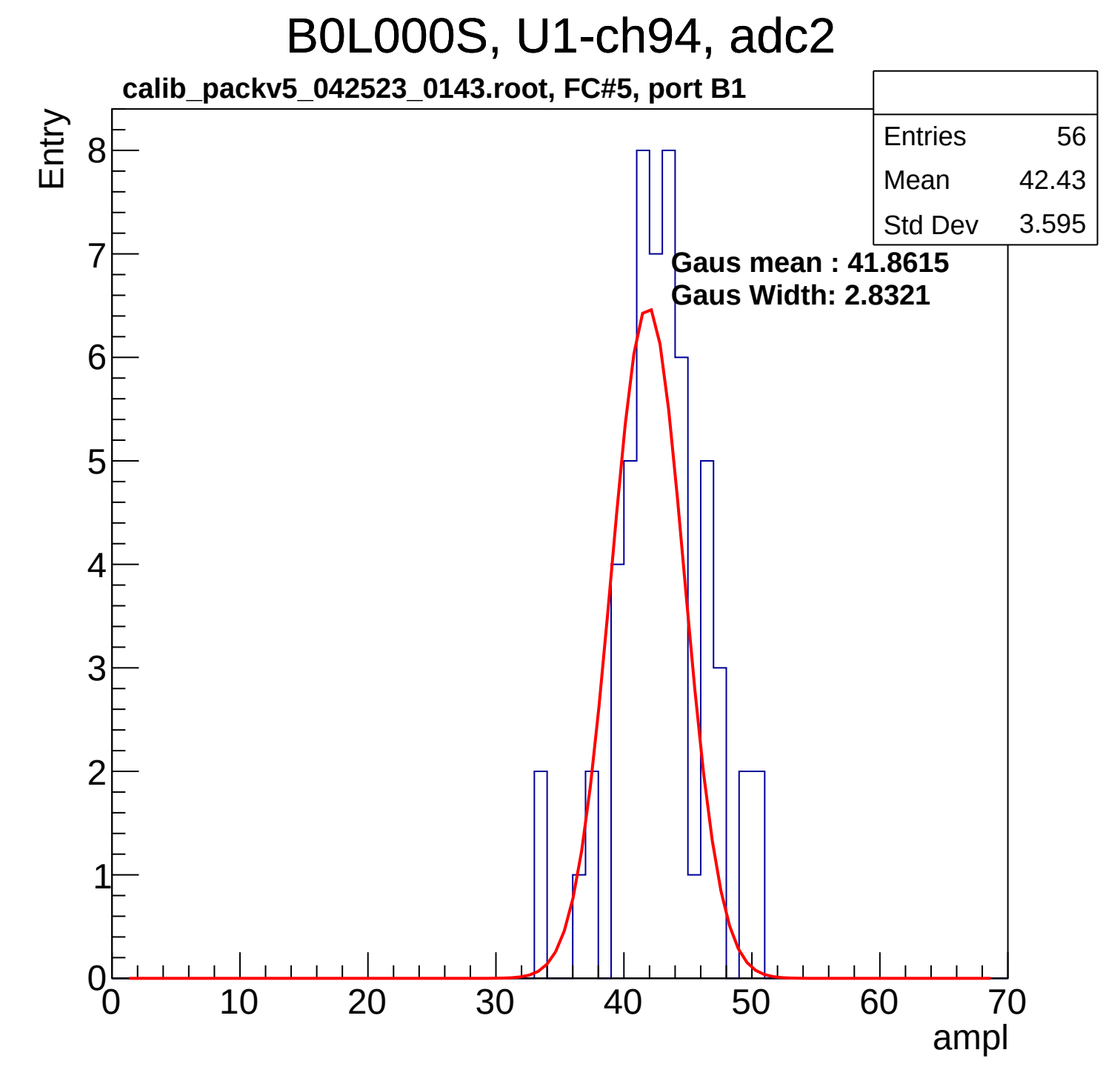

B0L000S, U1-ch93, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 60.89
Std Dev 1.646
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch93, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 48.2
Std Dev 24.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch93, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch94, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 29.27
Std Dev 3.332
Gaus mean : 29.8780
Gaus Width: 3.2420
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U1-ch94, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 35.46
Std Dev 3.895
Gaus mean : 35.7341
Gaus Width: 4.0640
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch94, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.43
Std Dev 3.595
Gaus mean : 41.8615
Gaus Width: 2.8321
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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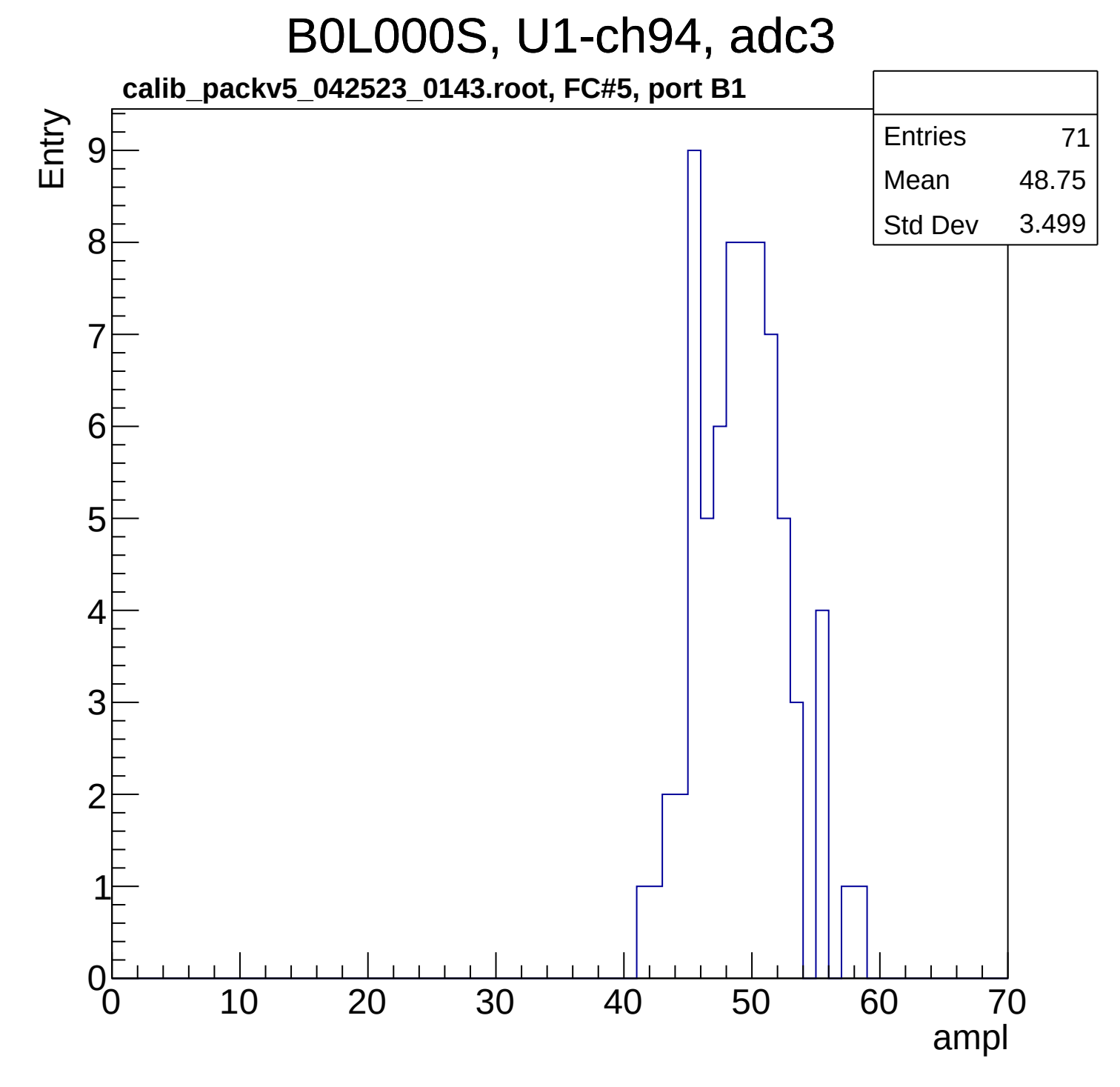
B0L000S, U1-ch94, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 48.75
Std Dev 3.499
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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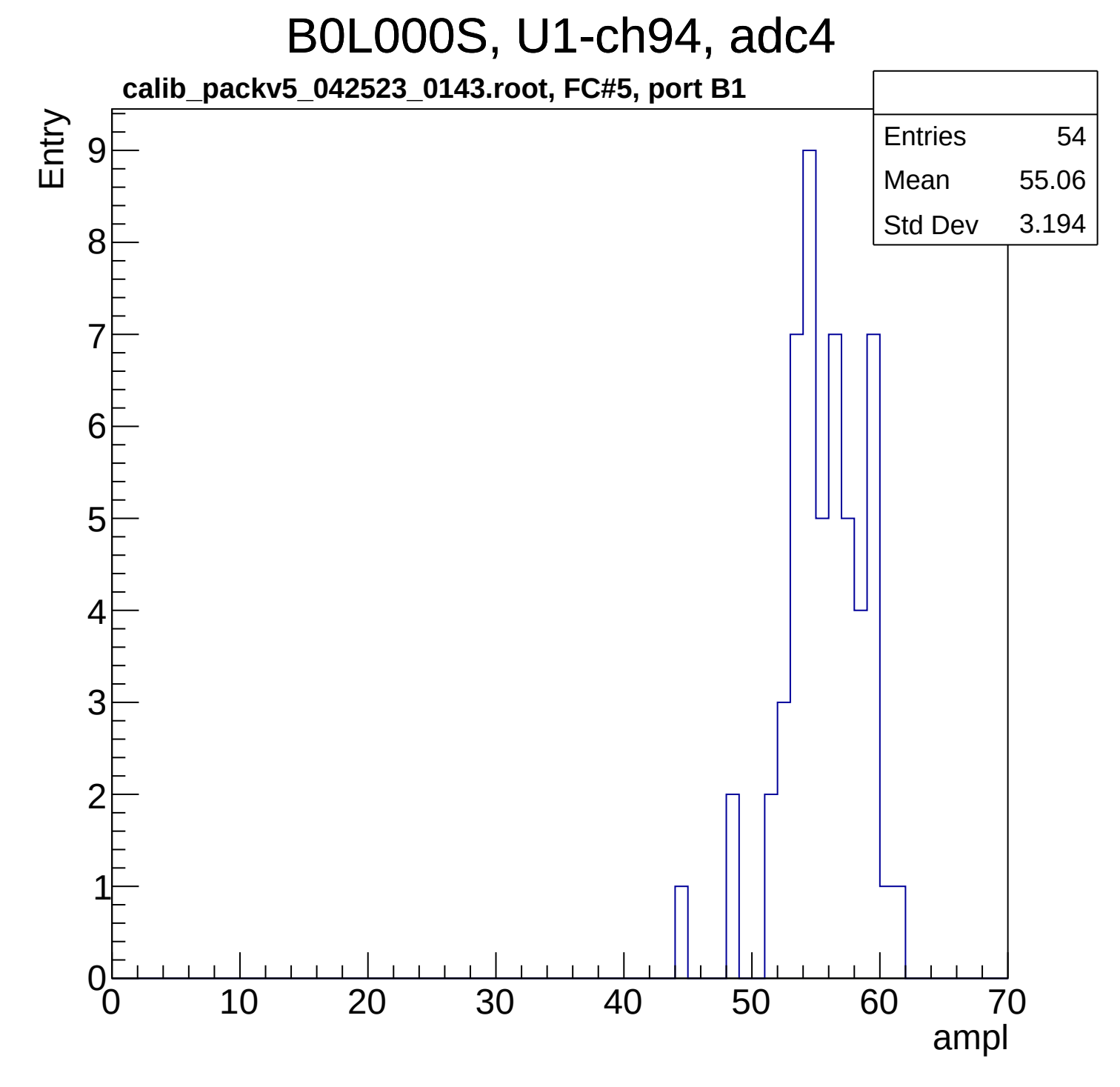
B0L000S, U1-ch94, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.06
Std Dev 3.194
Entry
ampl
0
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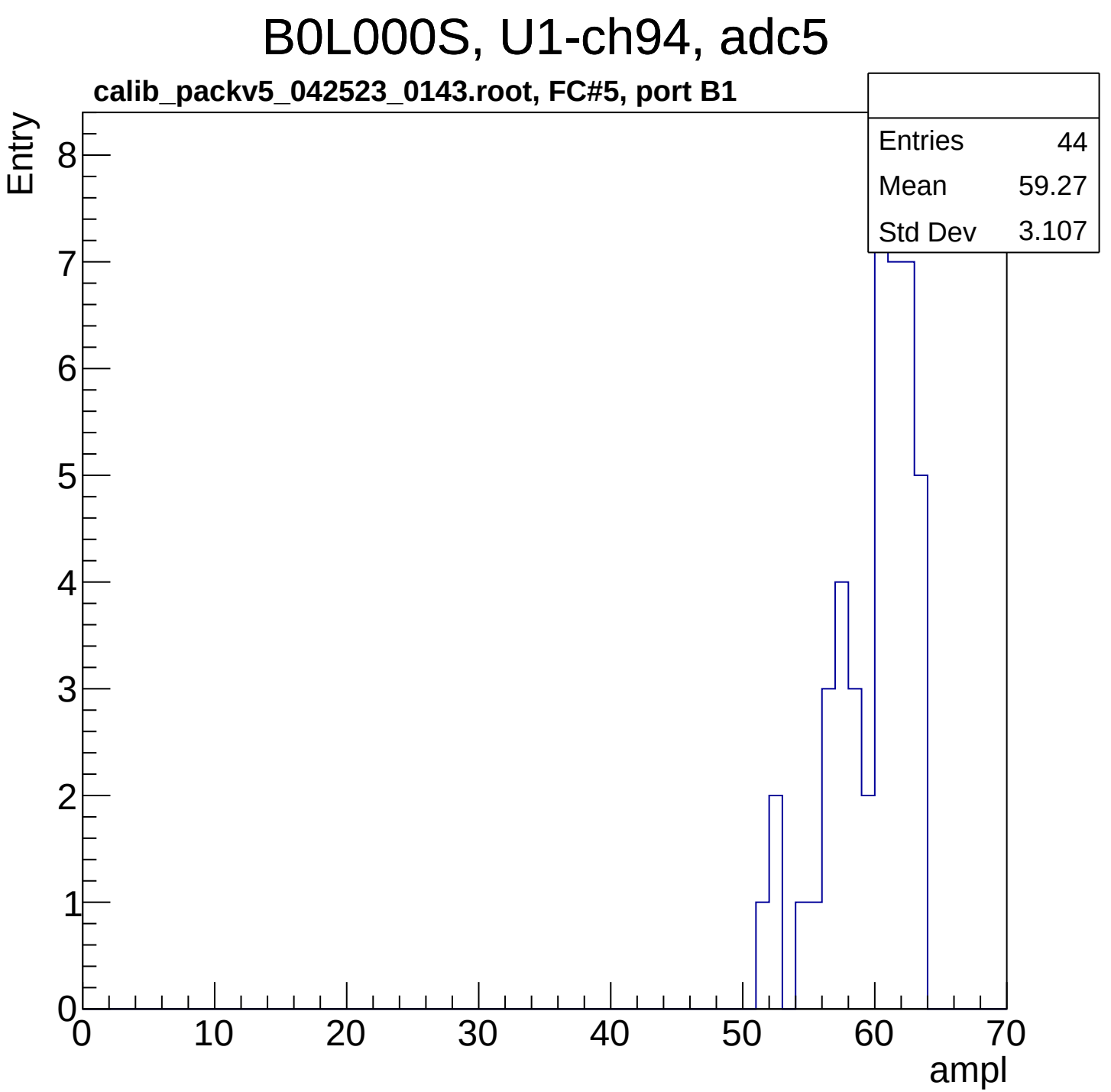
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch94, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.27
Std Dev 3.107
Entry
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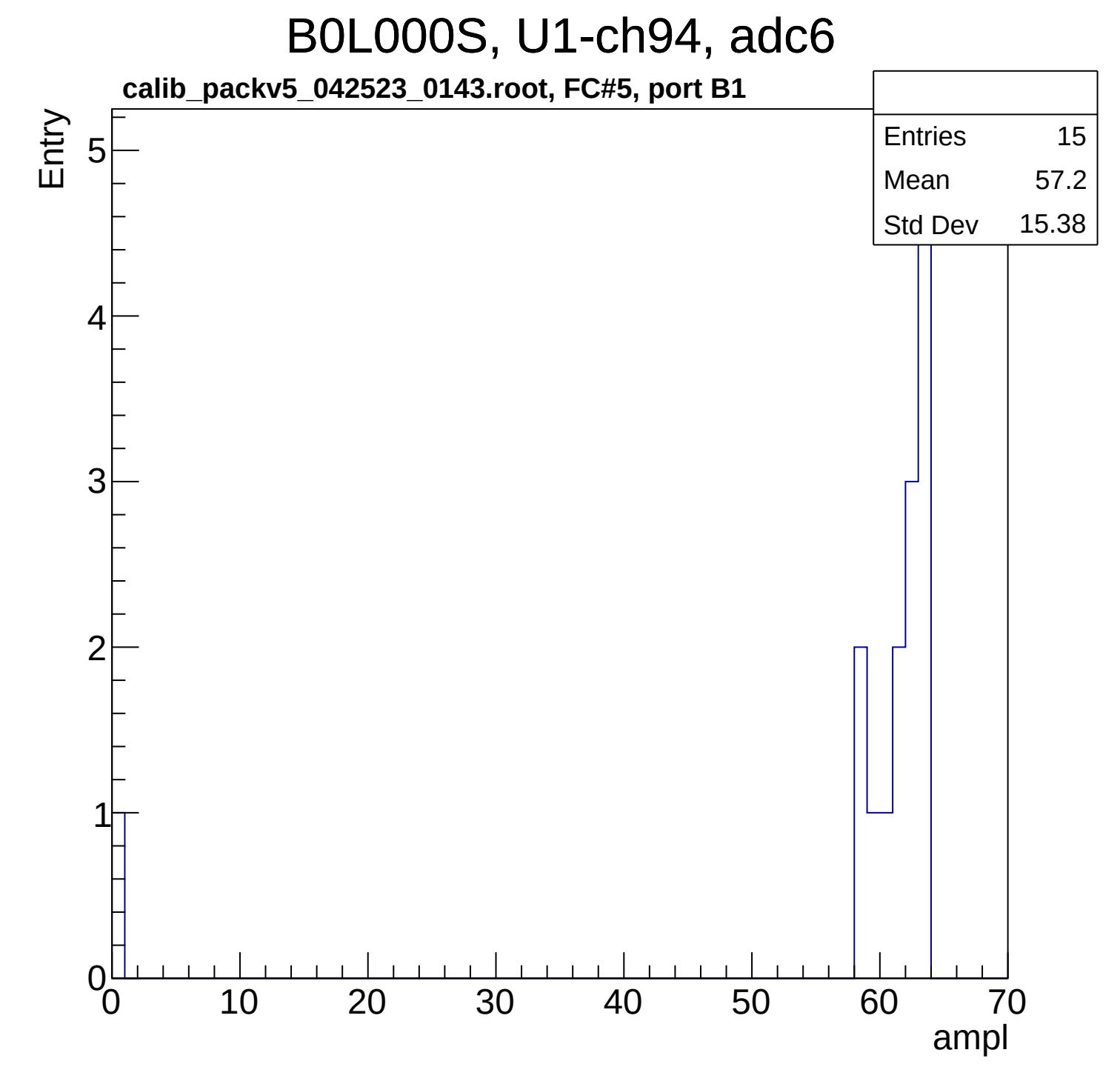
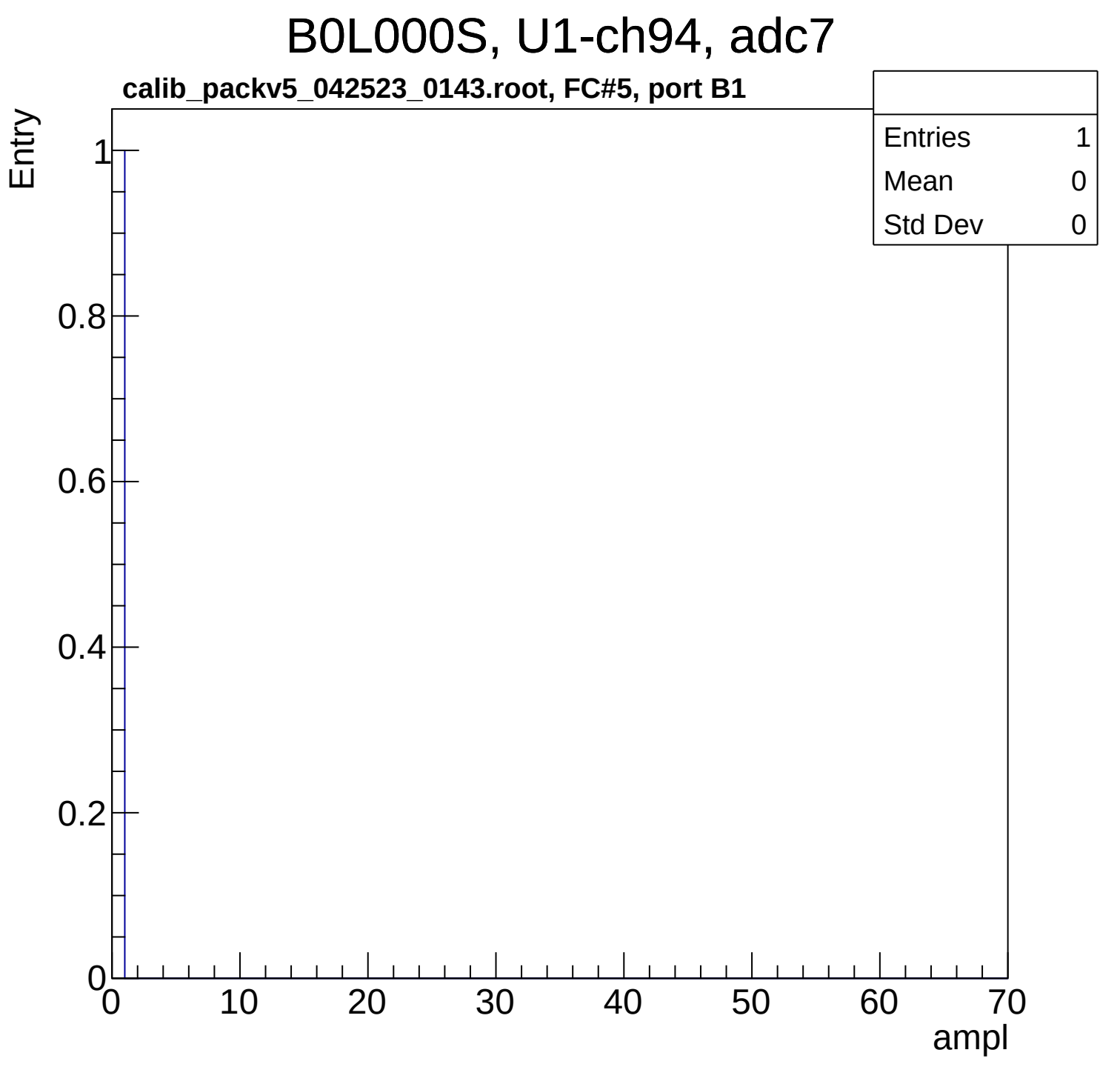
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch94, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.2
Std Dev 15.38
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch94, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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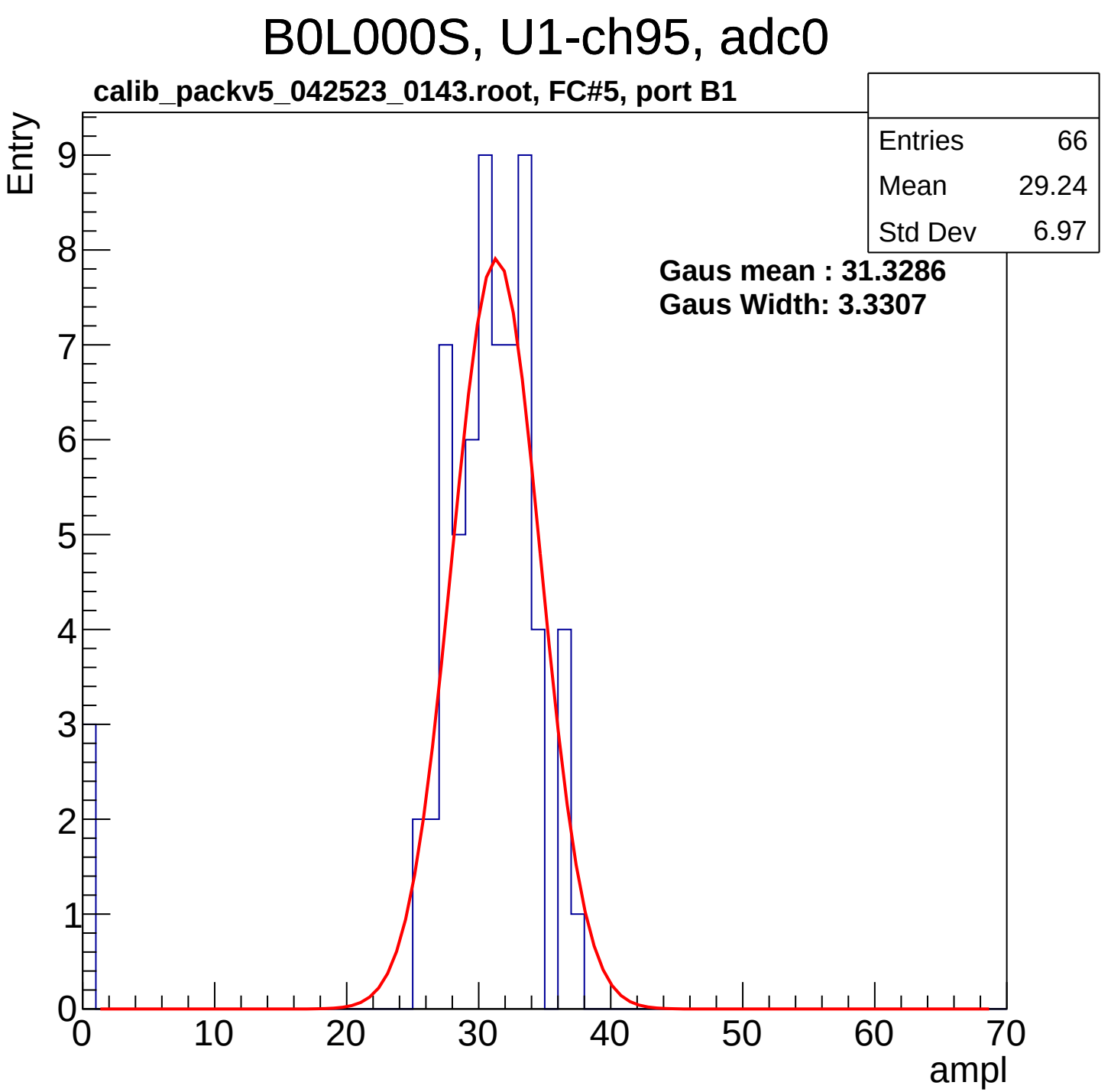
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch95, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.24
Std Dev 6.97
Gaus mean : 31.3286
Gaus Width: 3.3307
Entry
ampl
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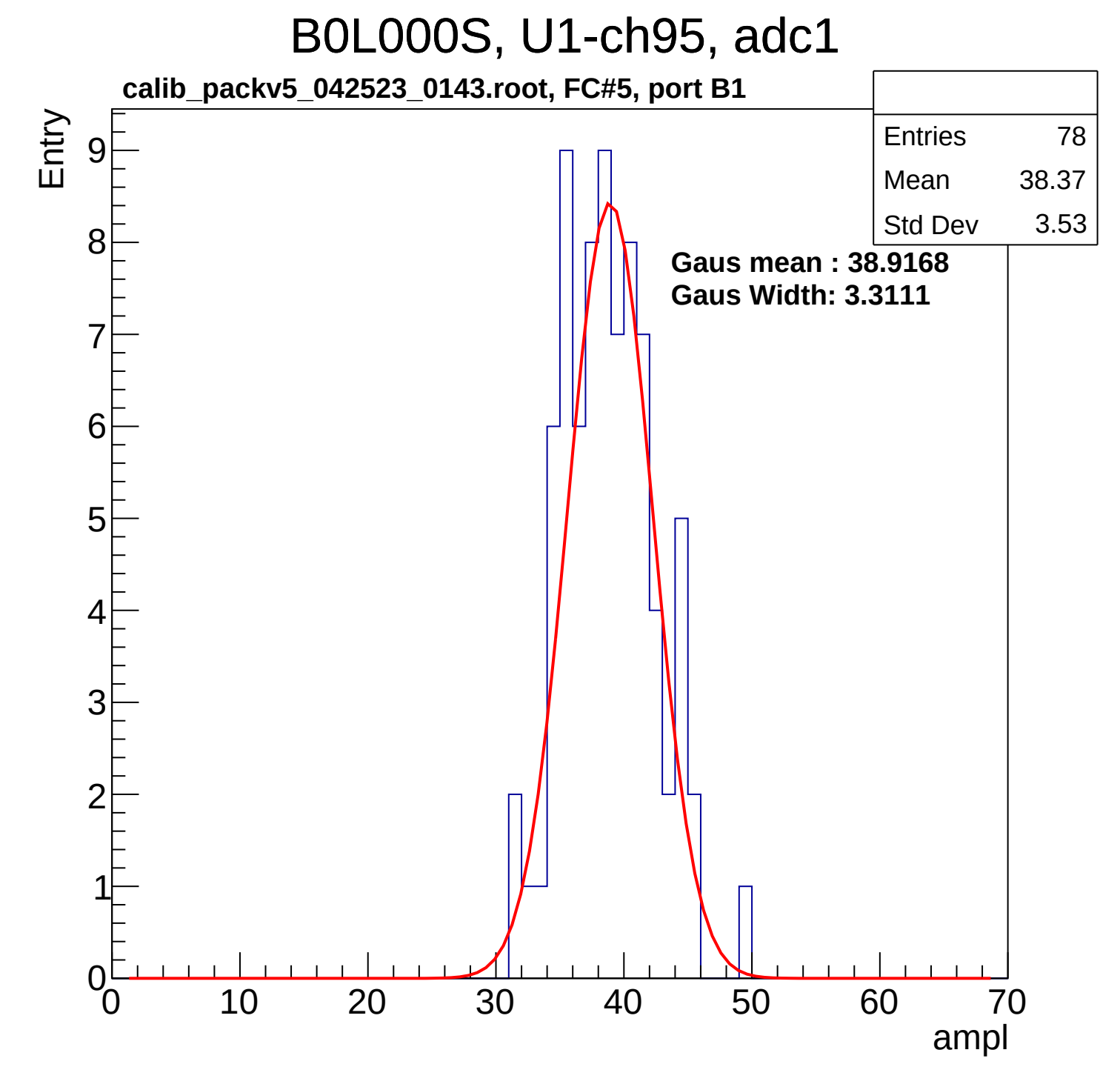
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch95, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 38.37
Std Dev 3.53
Gaus mean : 38.9168
Gaus Width: 3.3111
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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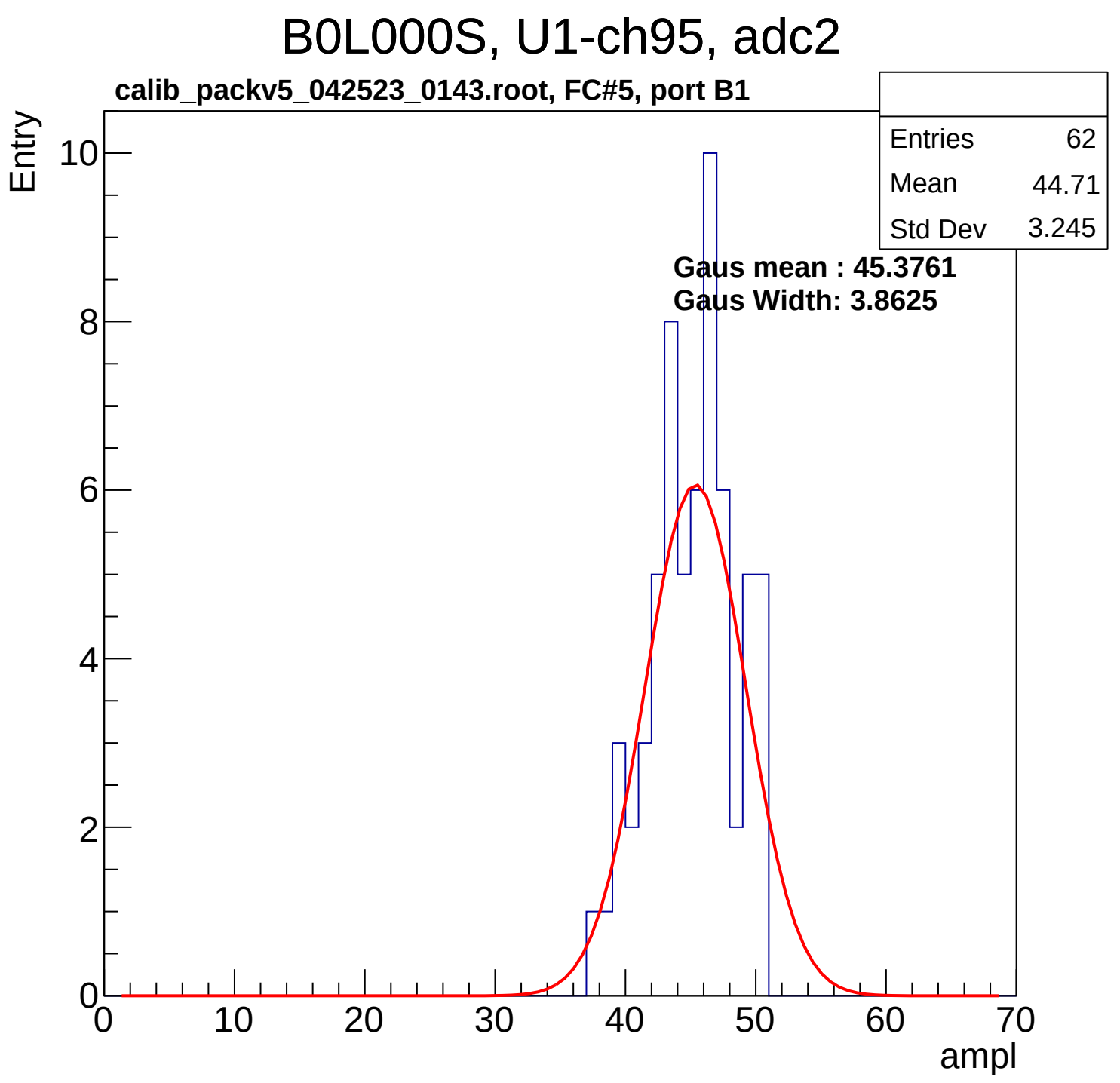
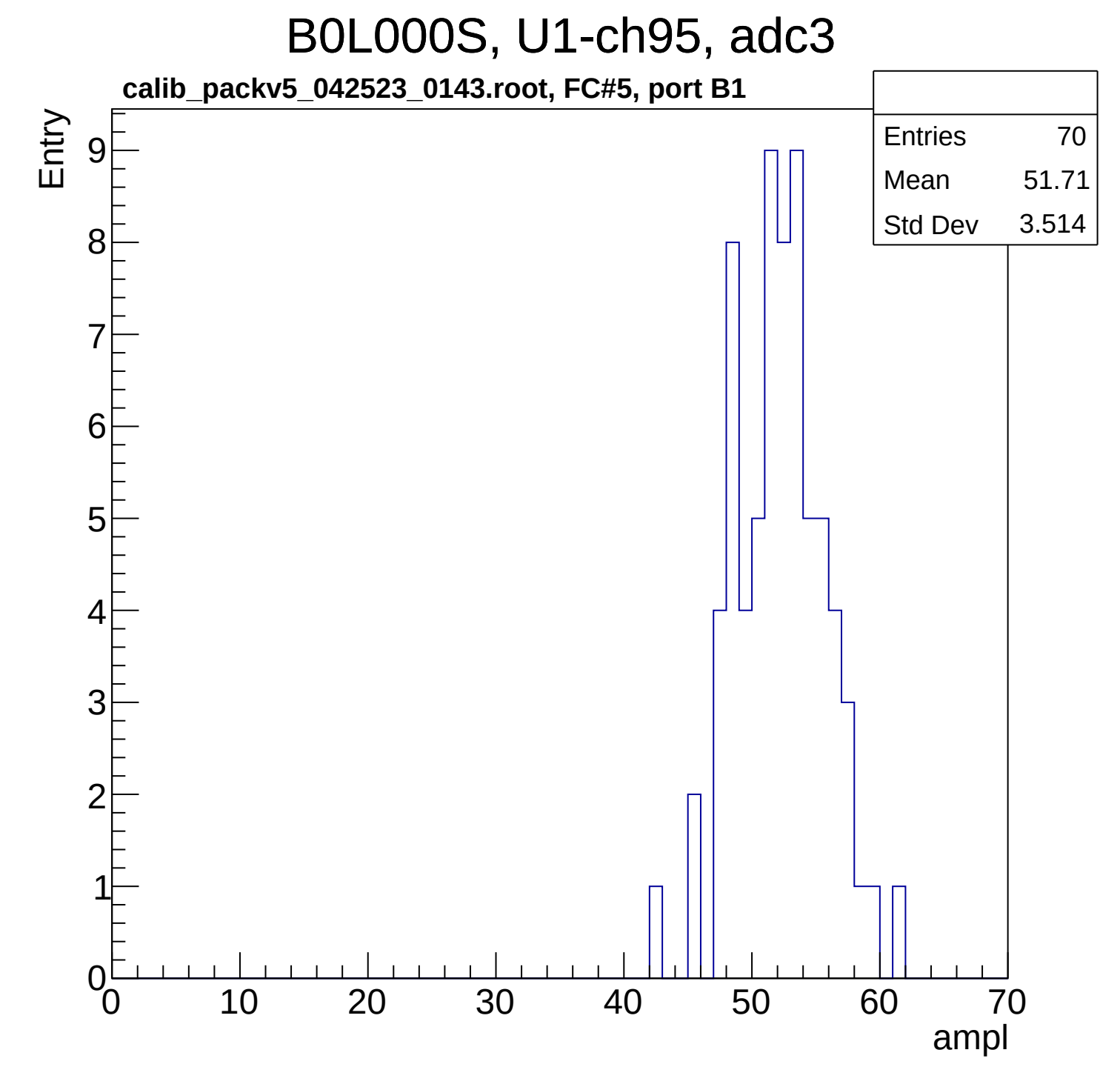
60
70


B0L000S, U1-ch95, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 44.71
Std Dev 3.245
Gaus mean : 45.3761
Gaus Width: 3.8625
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch95, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 51.71
Std Dev 3.514
Entry
ampl
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3
4
5
6
7
8
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0
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60
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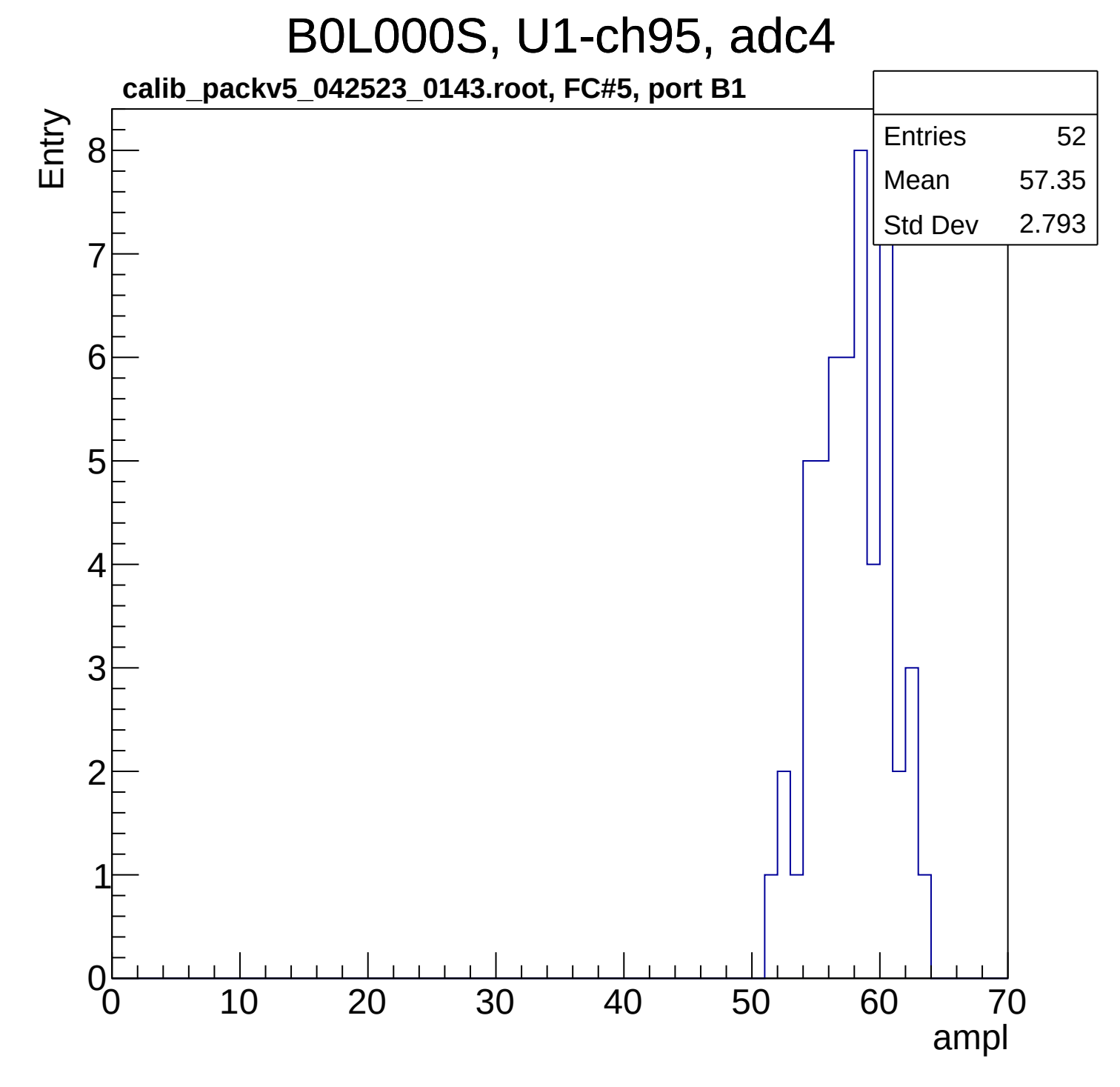
B0L000S, U1-ch95, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.35
Std Dev 2.793
Entry
ampl
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4
5
6
7
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0
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60
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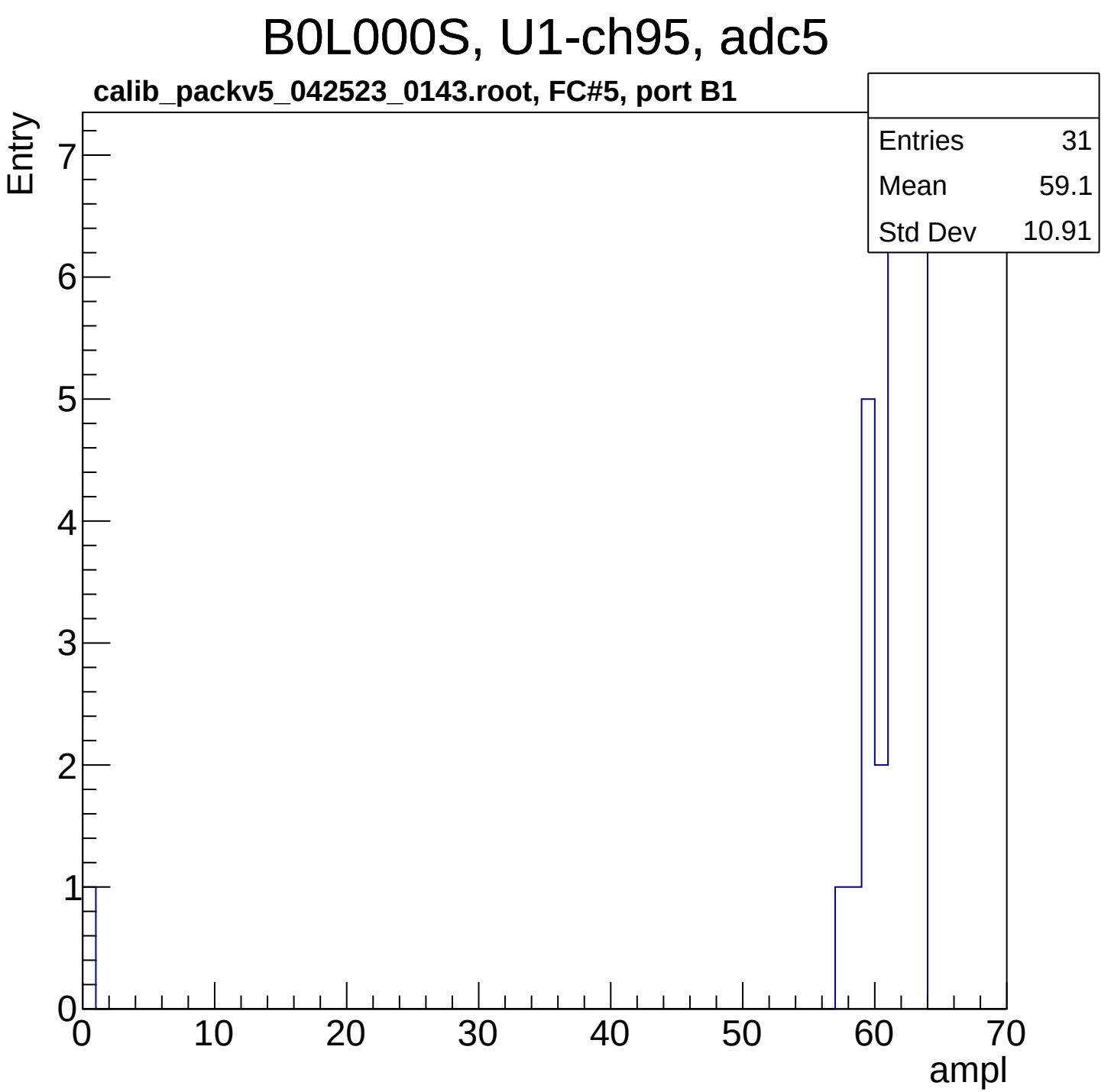
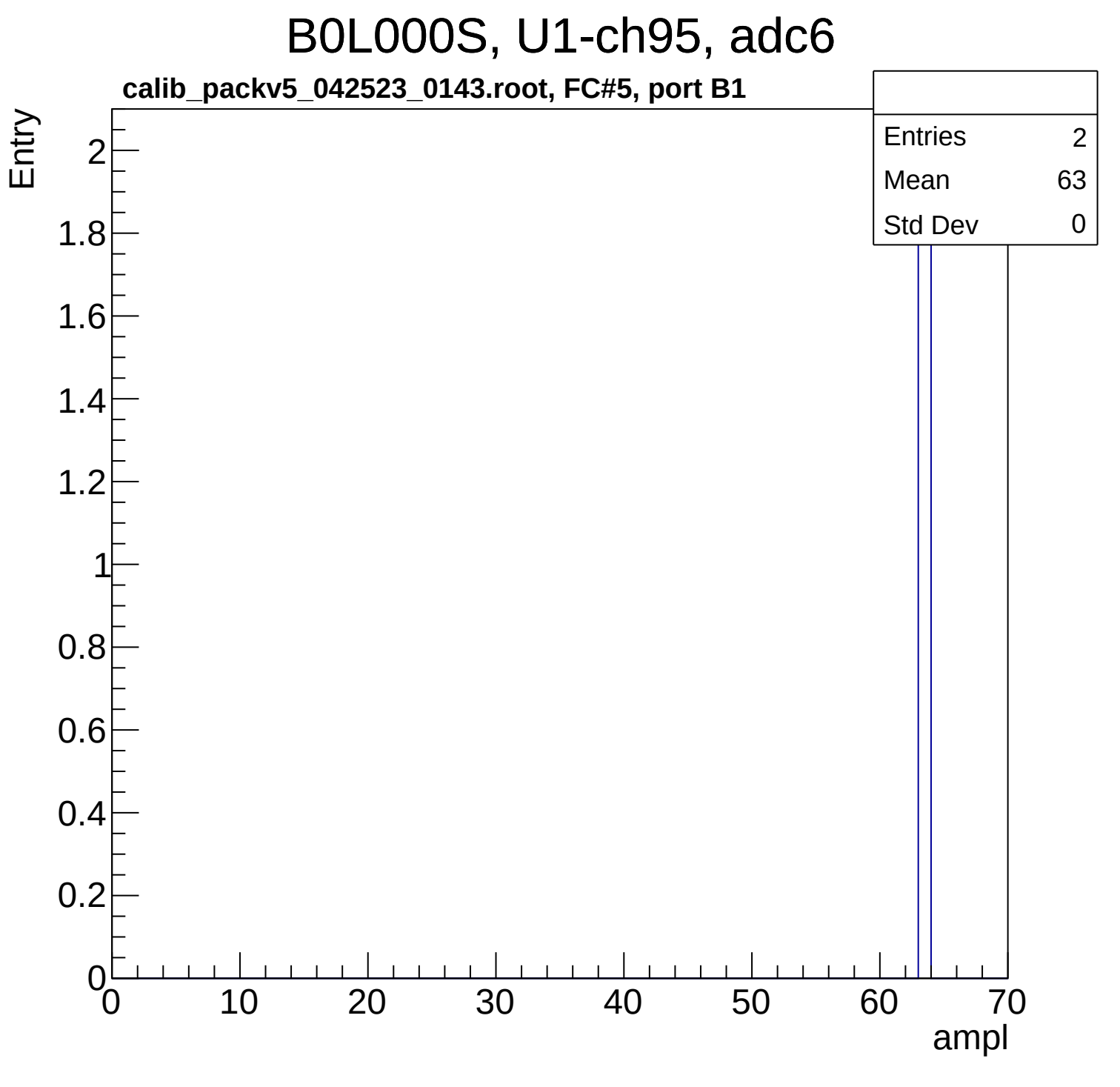

B0L000S, U1-ch95, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 59.1
Std Dev 10.91
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch95, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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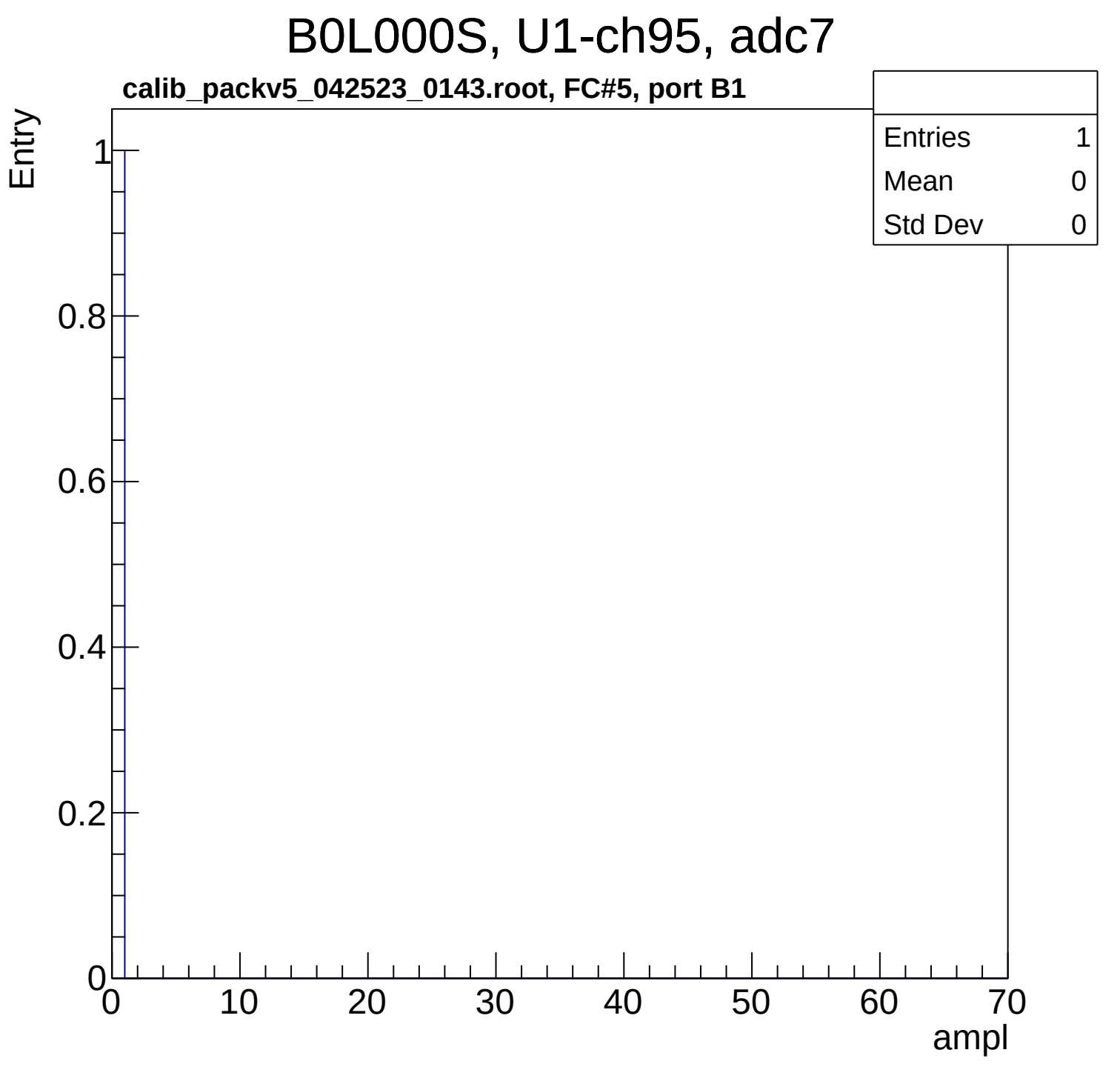
B0L000S, U1-ch95, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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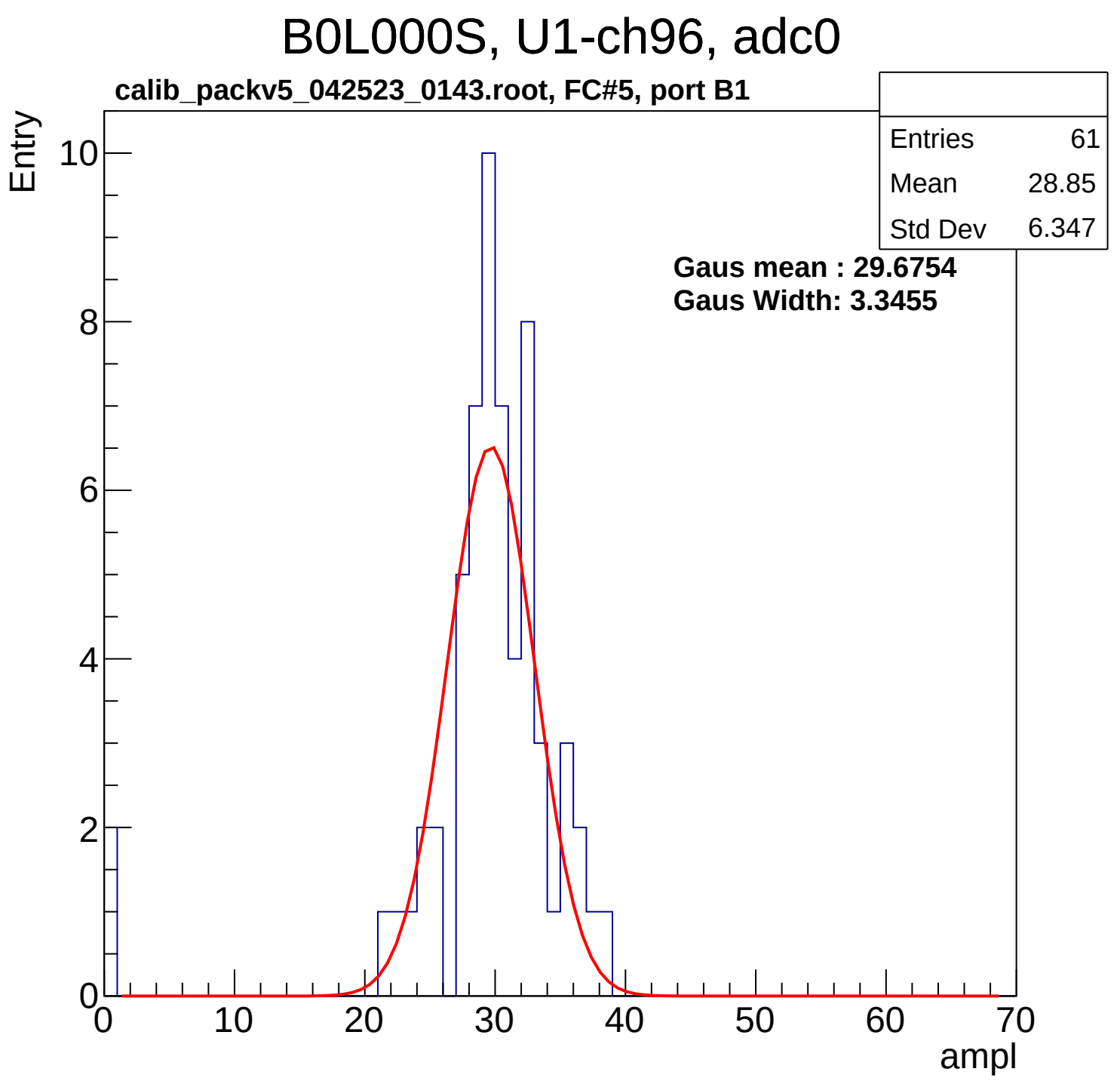
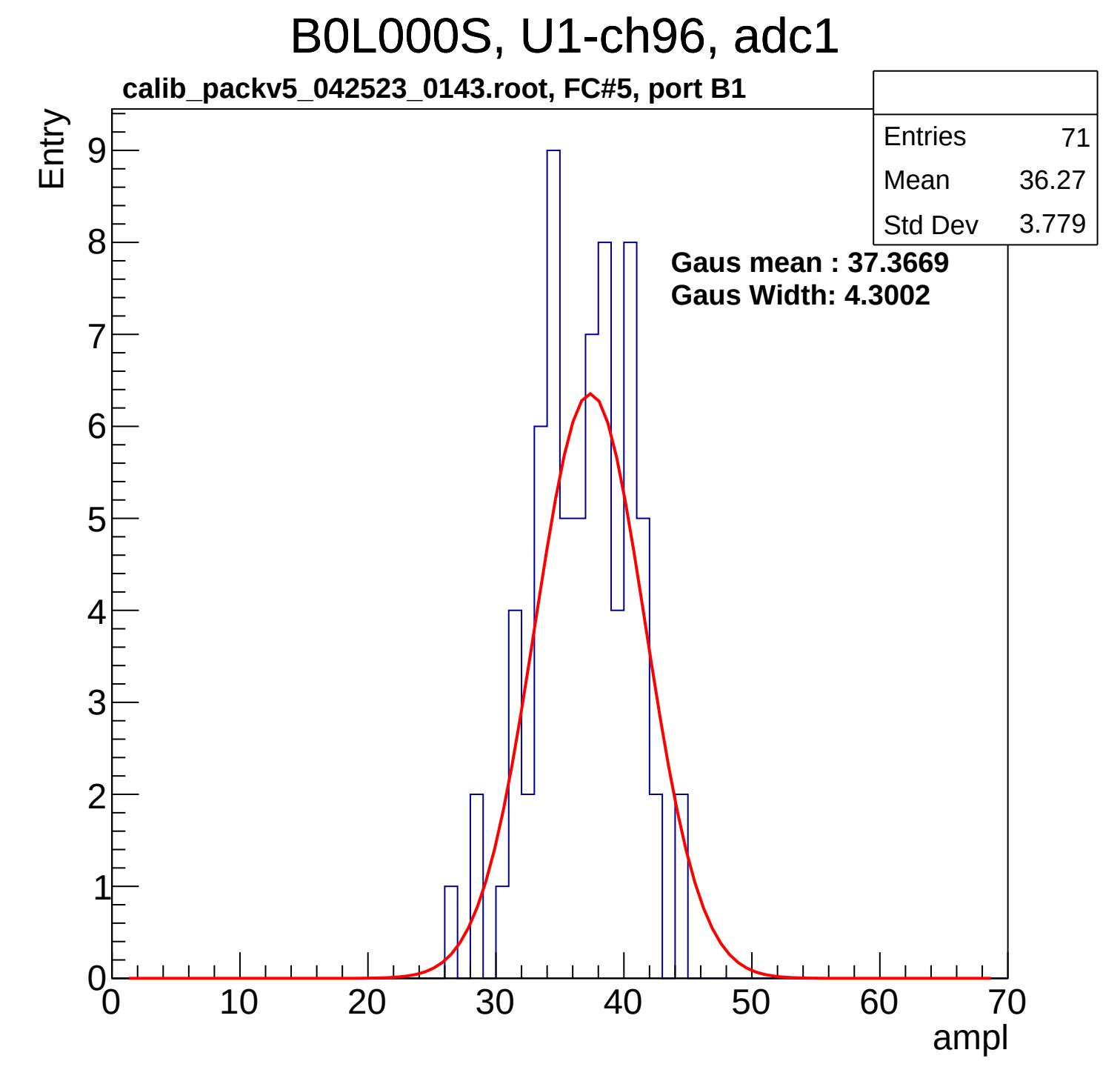
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch96, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.85
Std Dev 6.347
Gaus mean : 29.6754
Gaus Width: 3.3455
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch96, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.27
Std Dev 3.779
Gaus mean : 37.3669
Gaus Width: 4.3002
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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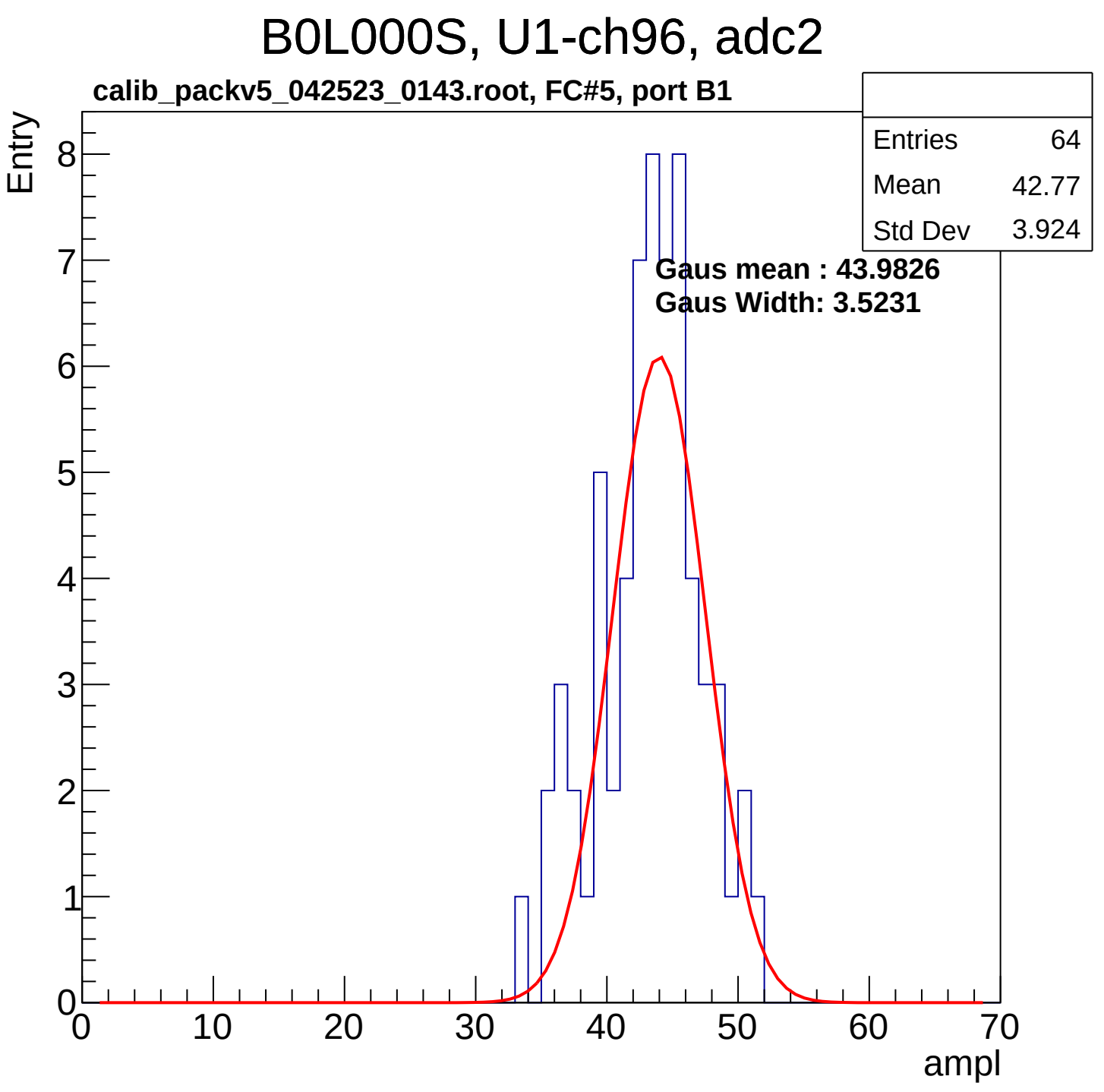
B0L000S, U1-ch96, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.77
Std Dev 3.924
Gaus mean : 43.9826
Gaus Width: 3.5231
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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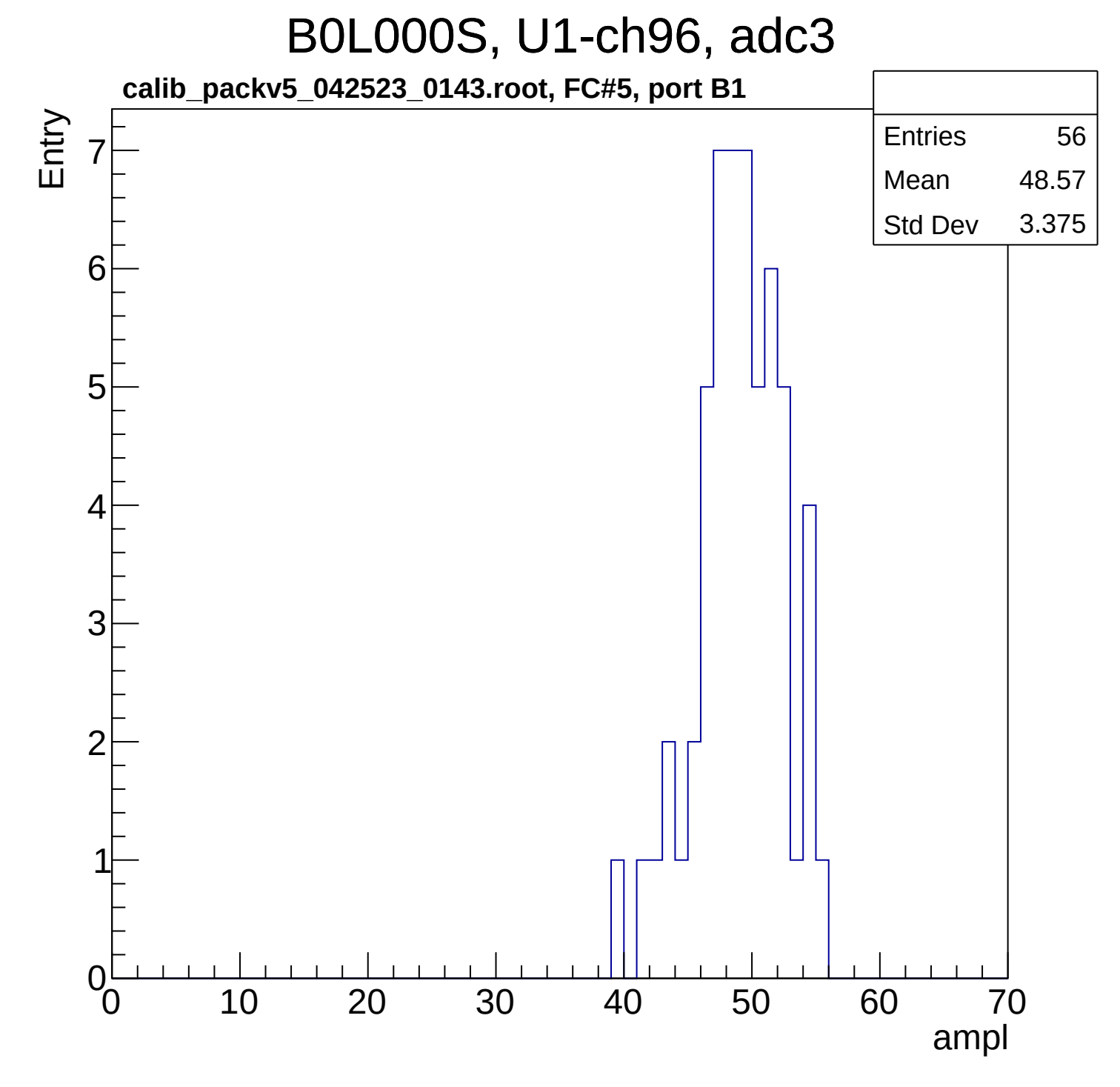
B0L000S, U1-ch96, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 48.57
Std Dev 3.375
Entry
ampl
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3
4
5
6
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0
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40
50
60
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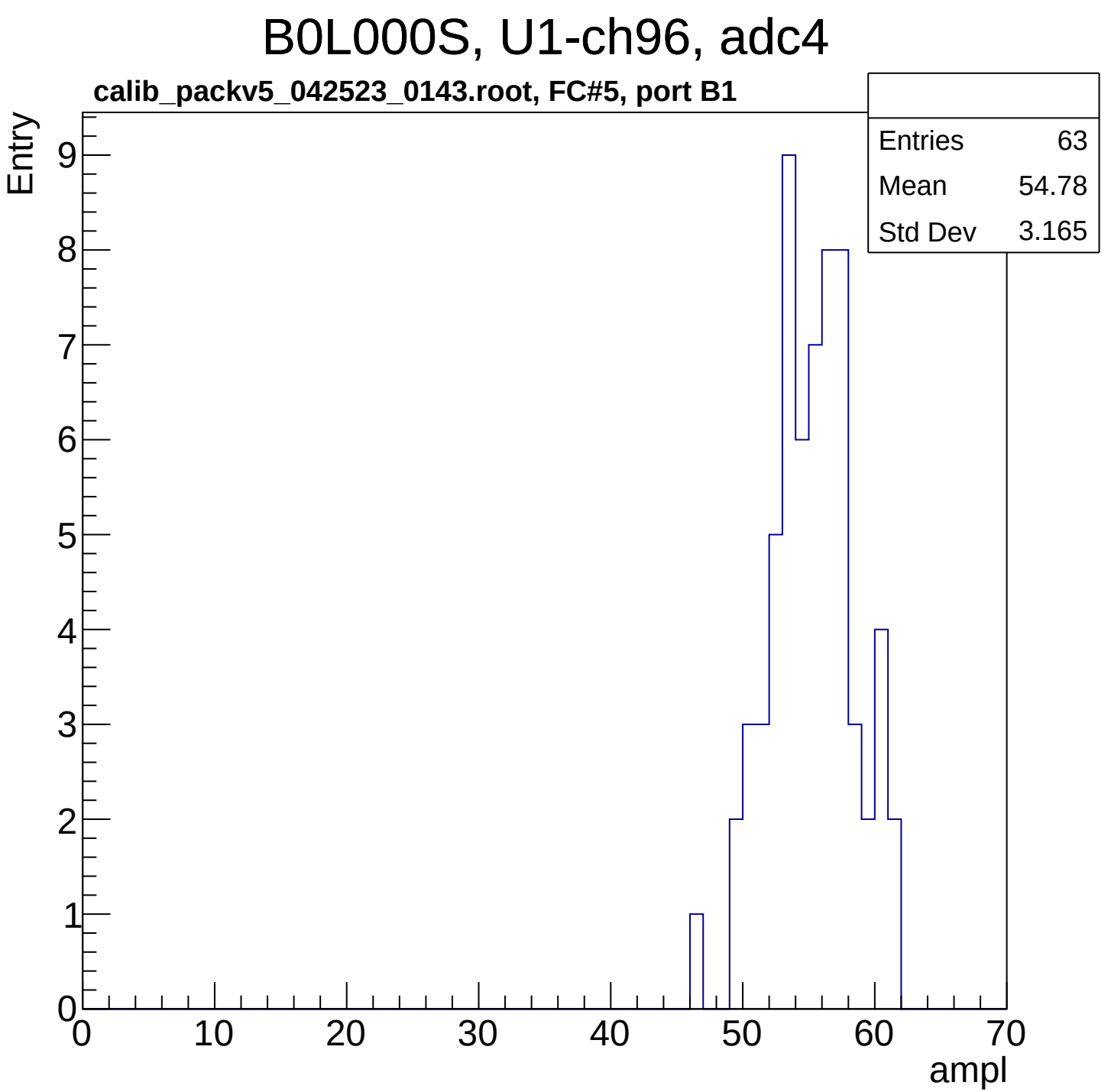
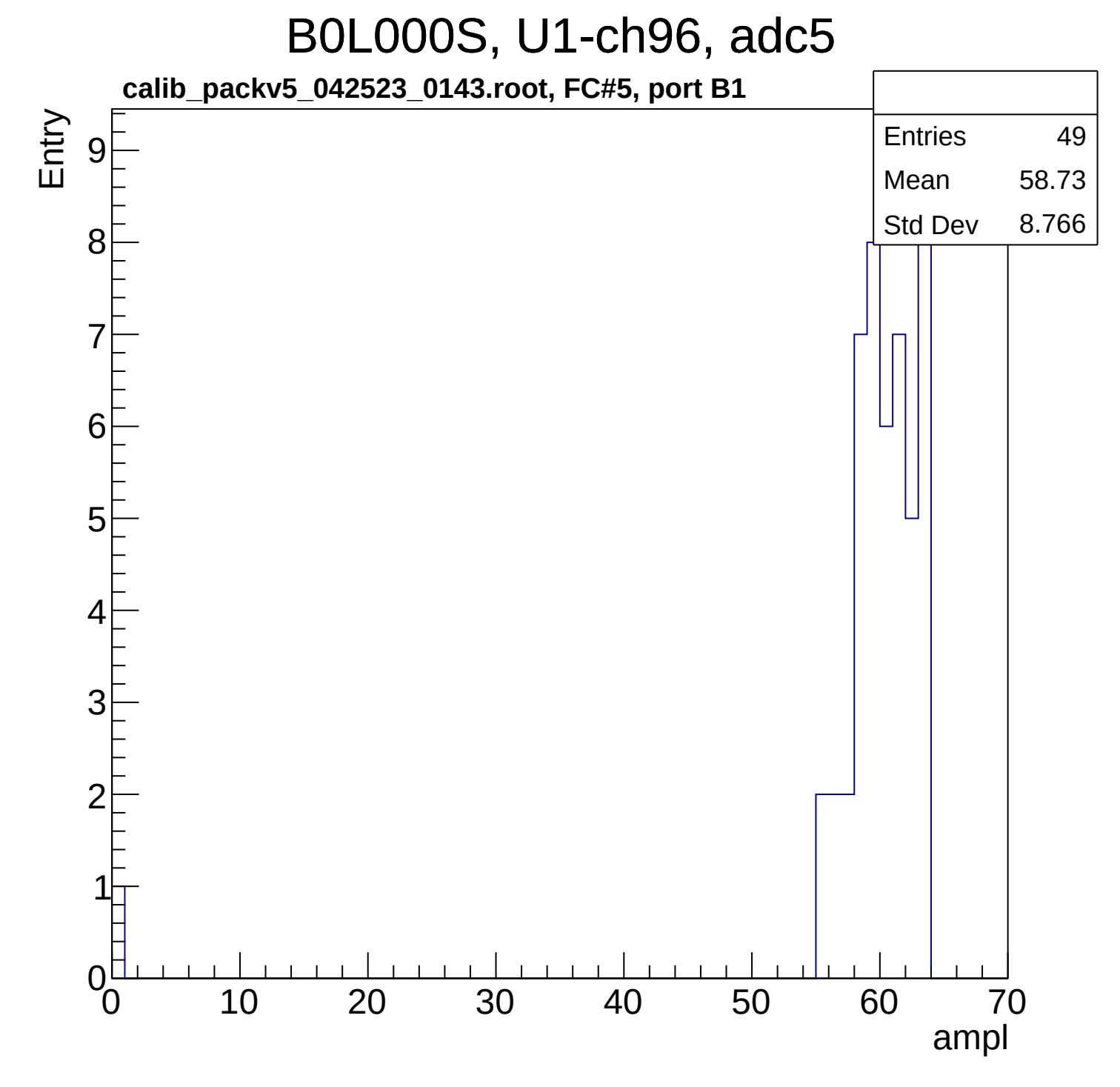

B0L000S, U1-ch96, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.78
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch96, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.73
Std Dev 8.766
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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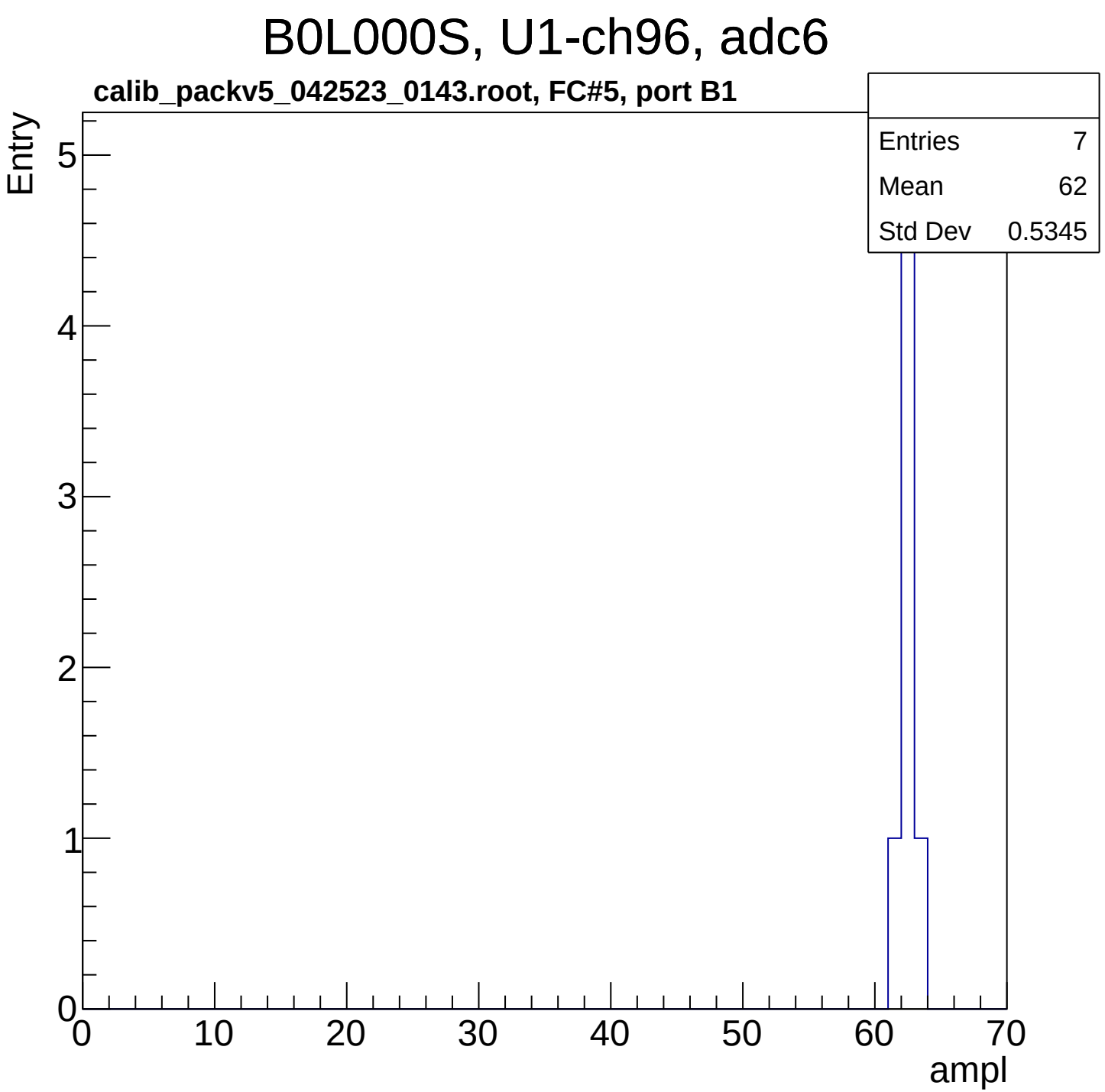
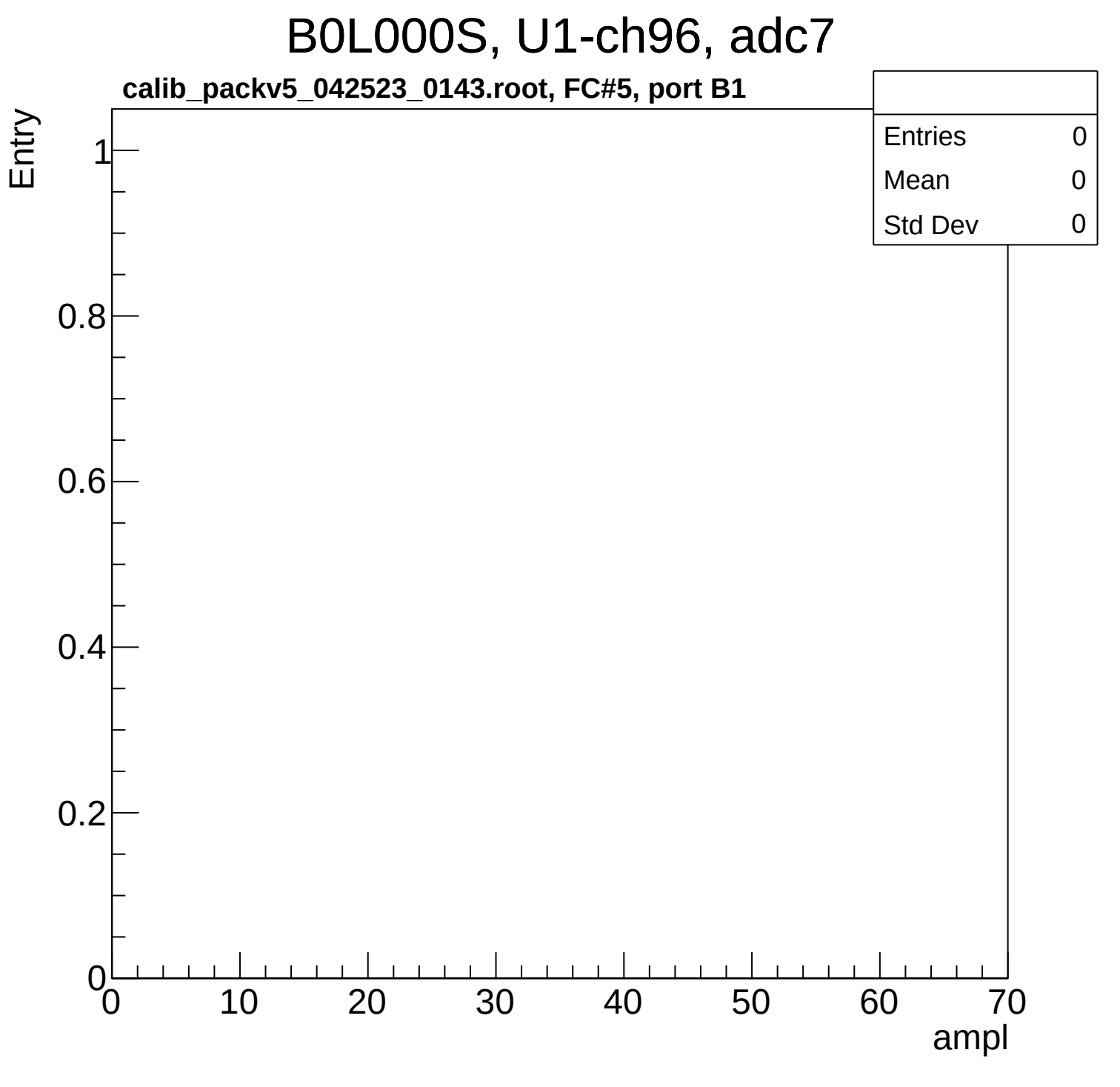

B0L000S, U1-ch96, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62
Std Dev 0.5345
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch96, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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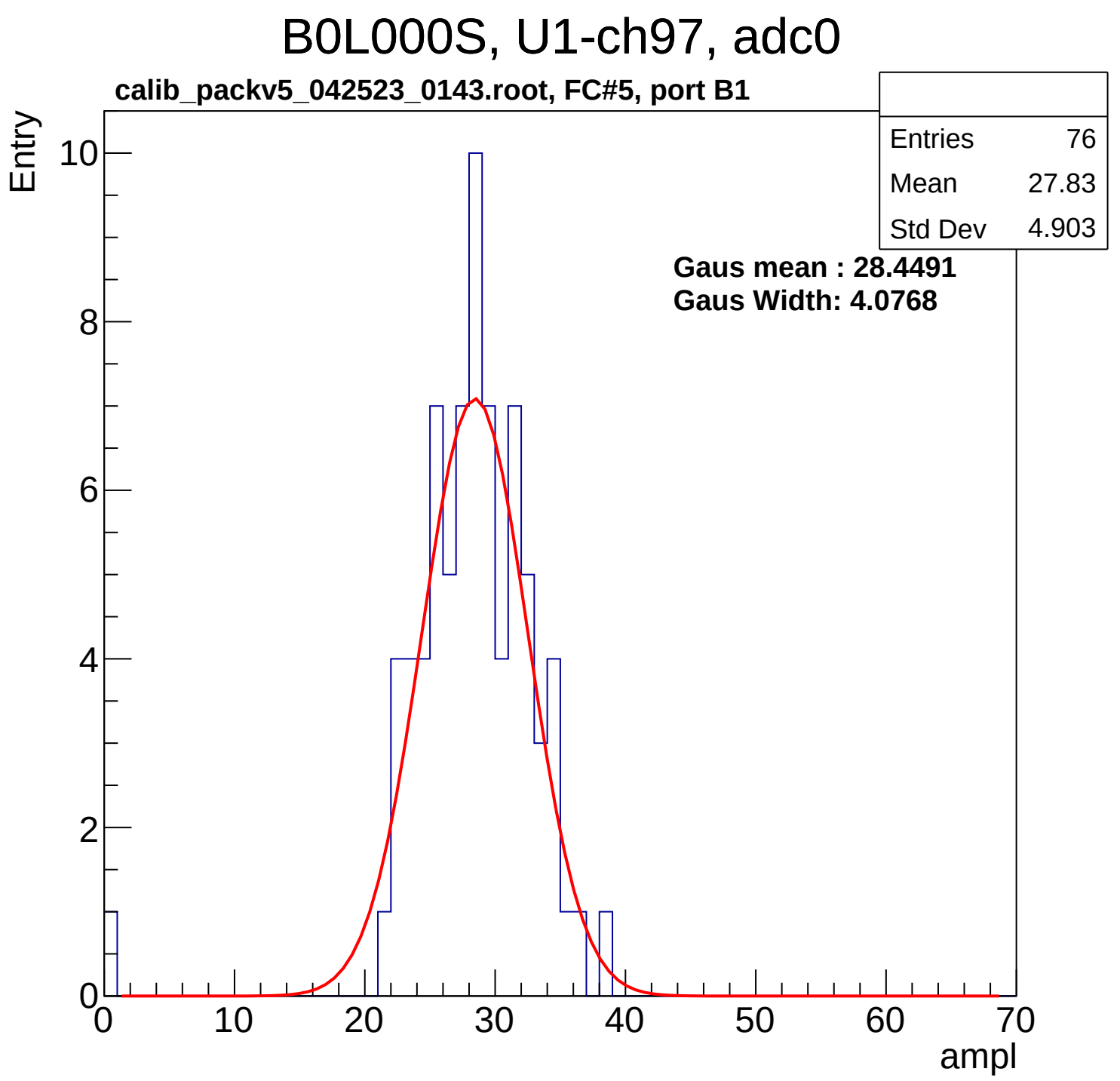
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch97, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 27.83
Std Dev 4.903
Gaus mean : 28.4491
Gaus Width: 4.0768
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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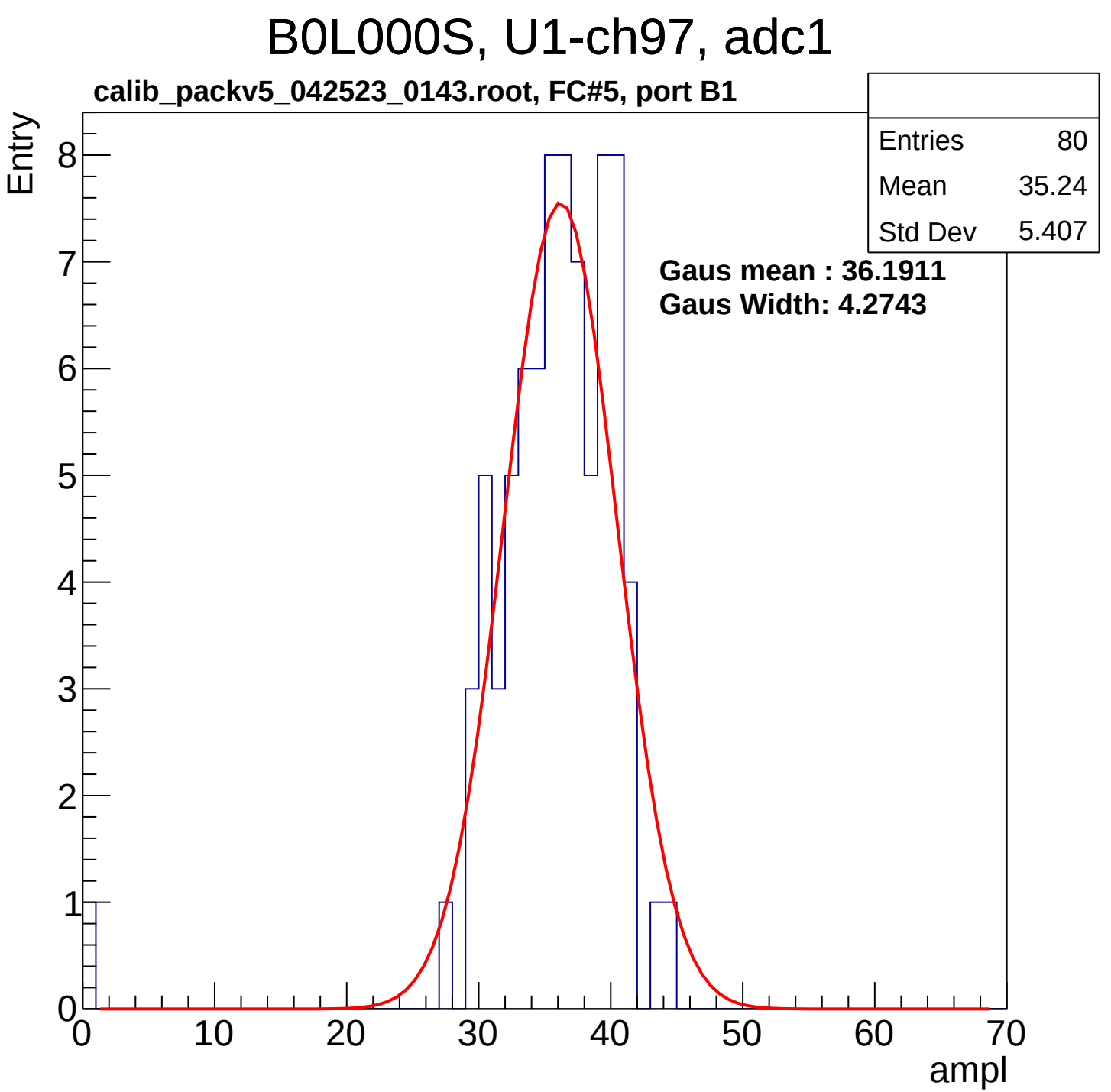
B0L000S, U1-ch97, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 35.24
Std Dev 5.407
Gaus mean : 36.1911
Gaus Width: 4.2743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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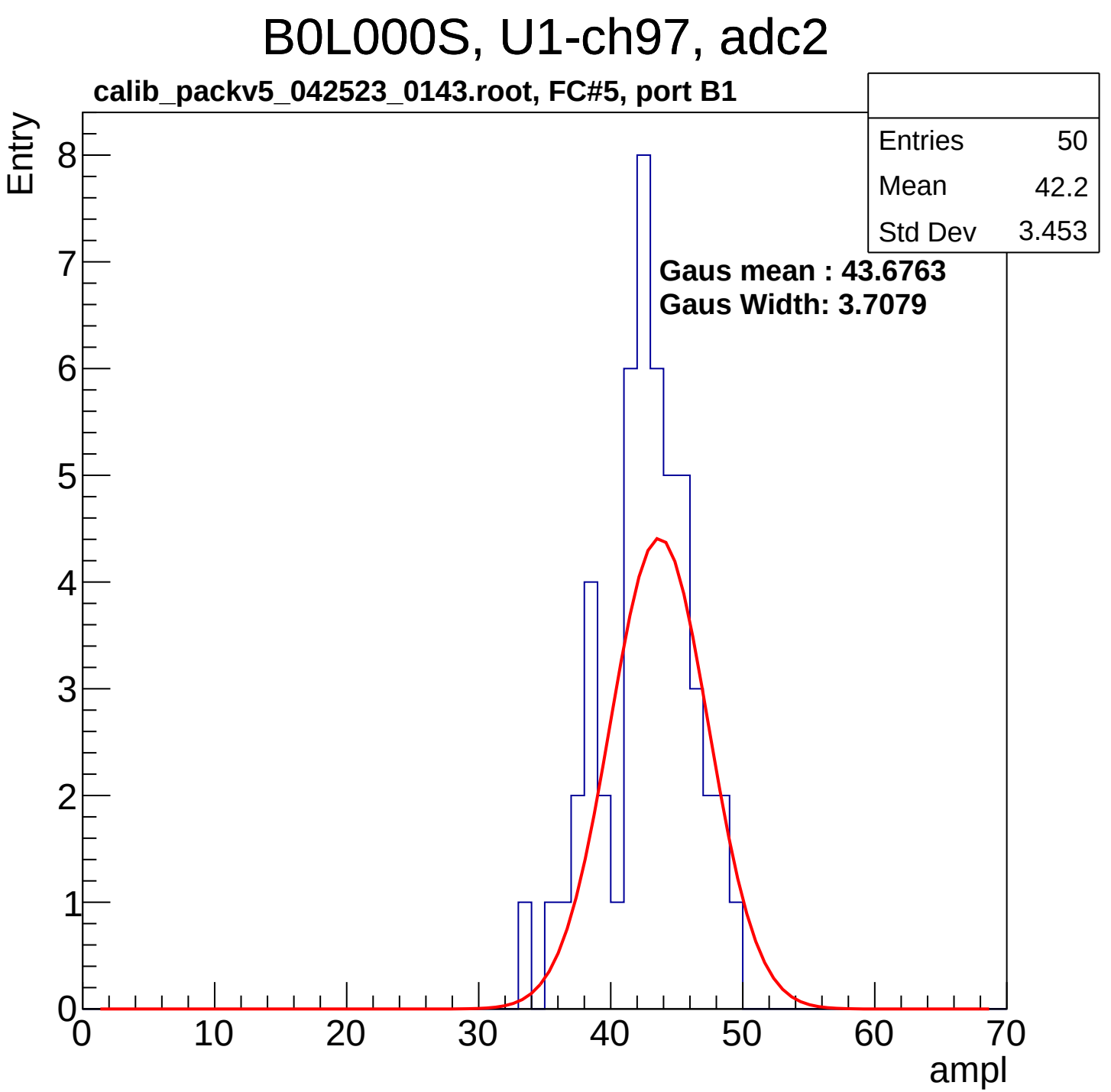
B0L000S, U1-ch97, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.2
Std Dev 3.453
Gaus mean : 43.6763
Gaus Width: 3.7079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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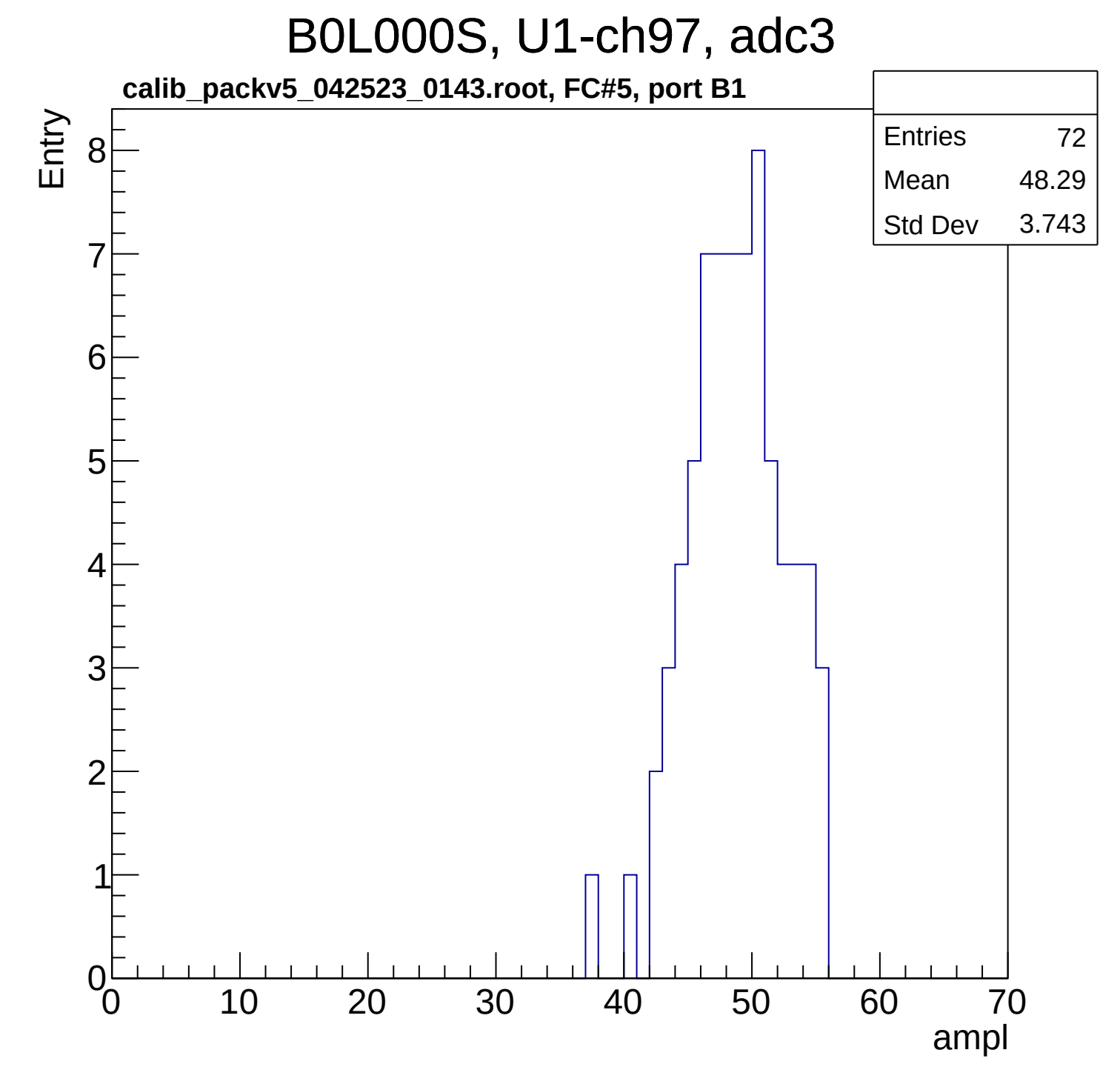
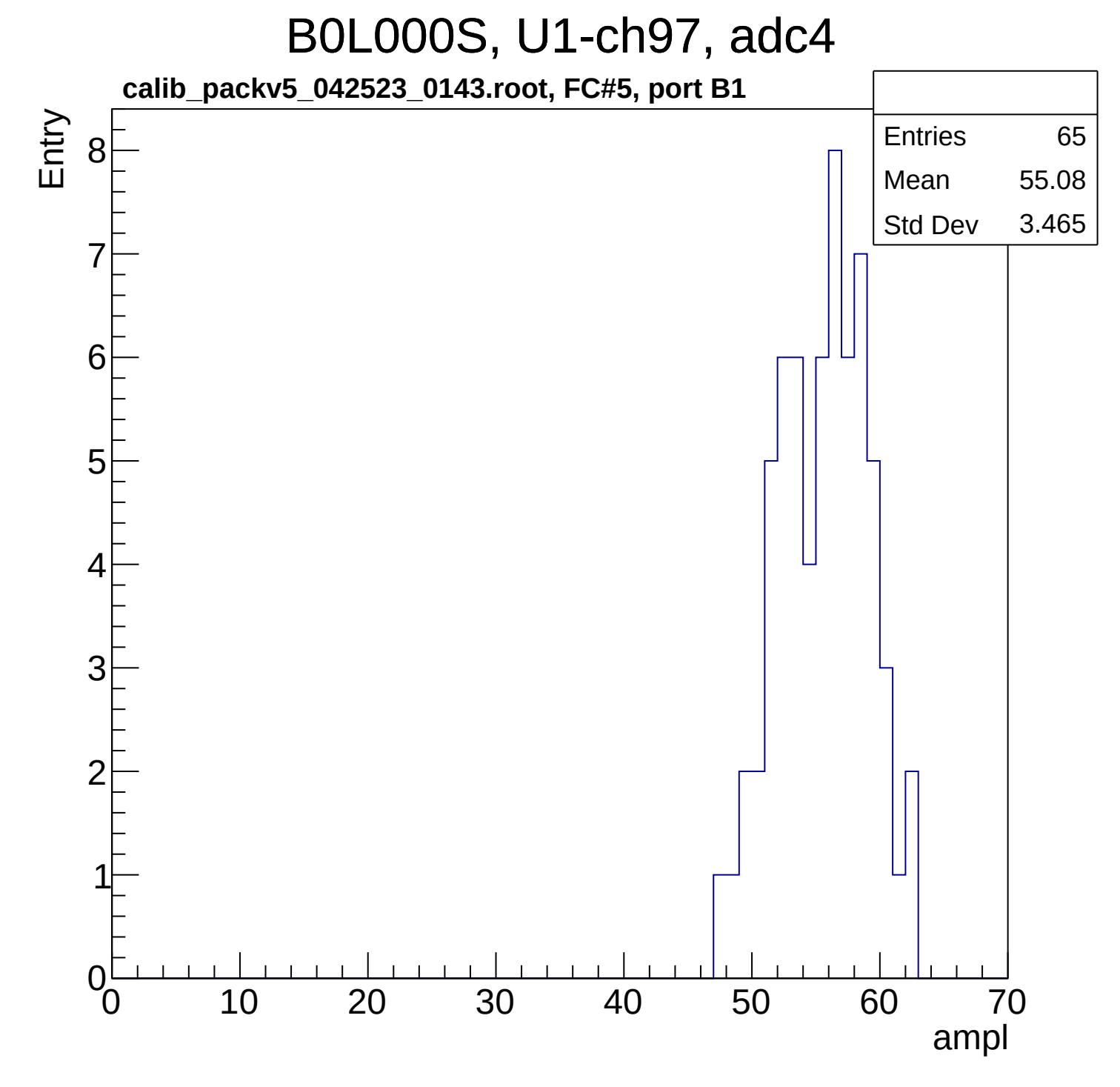

B0L000S, U1-ch97, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.29
Std Dev 3.743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch97, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 55.08
Std Dev 3.465
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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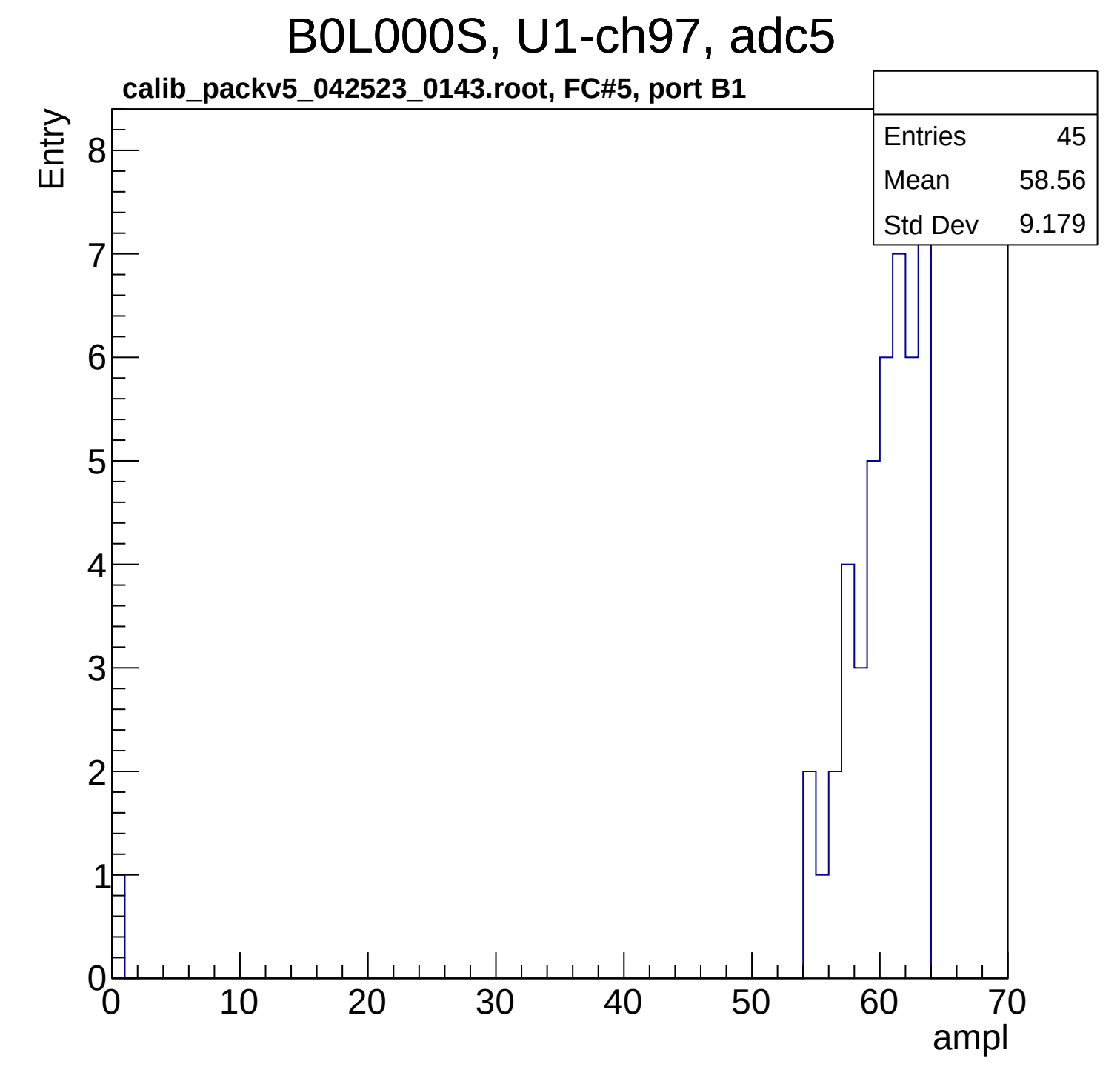
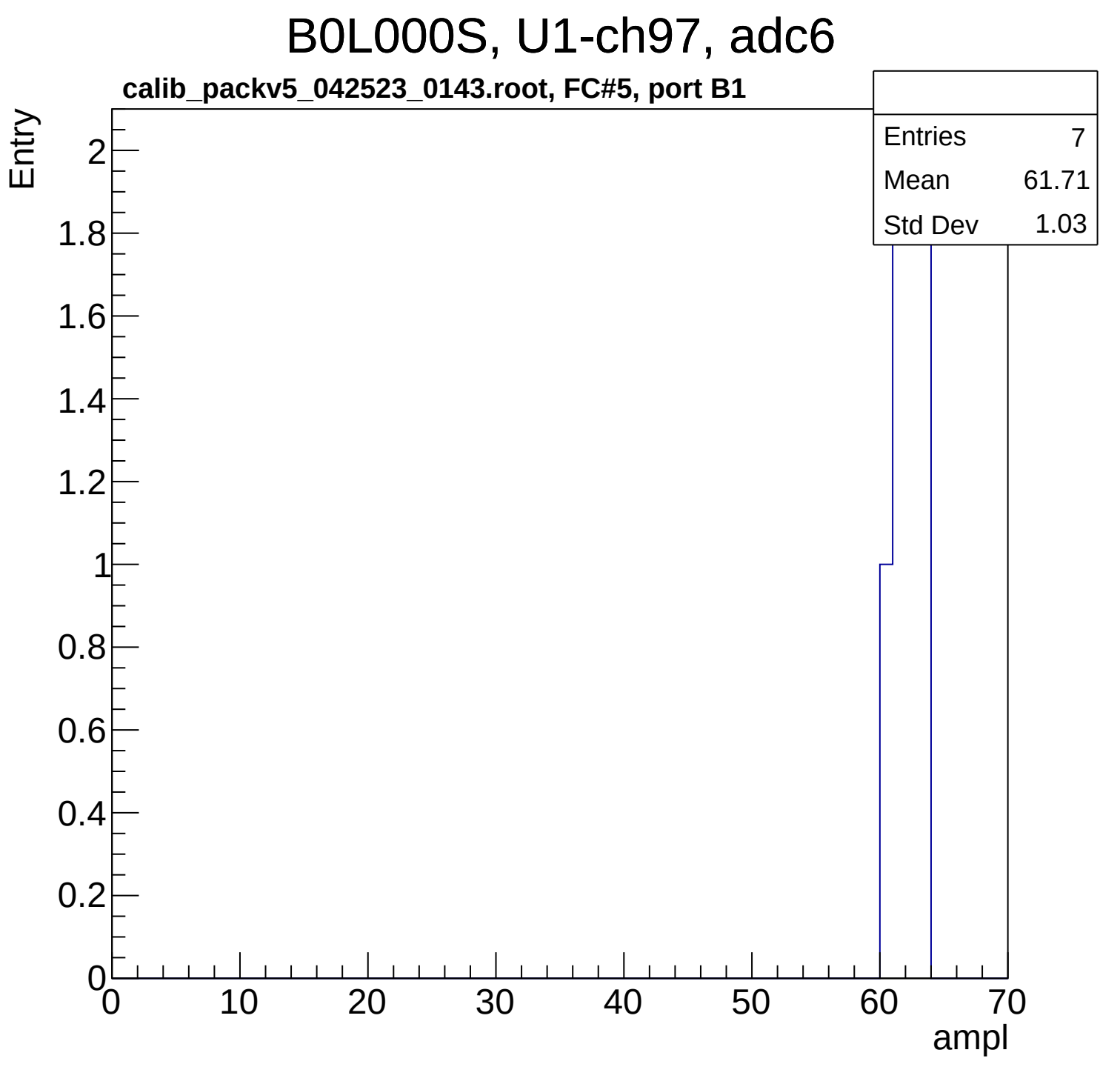
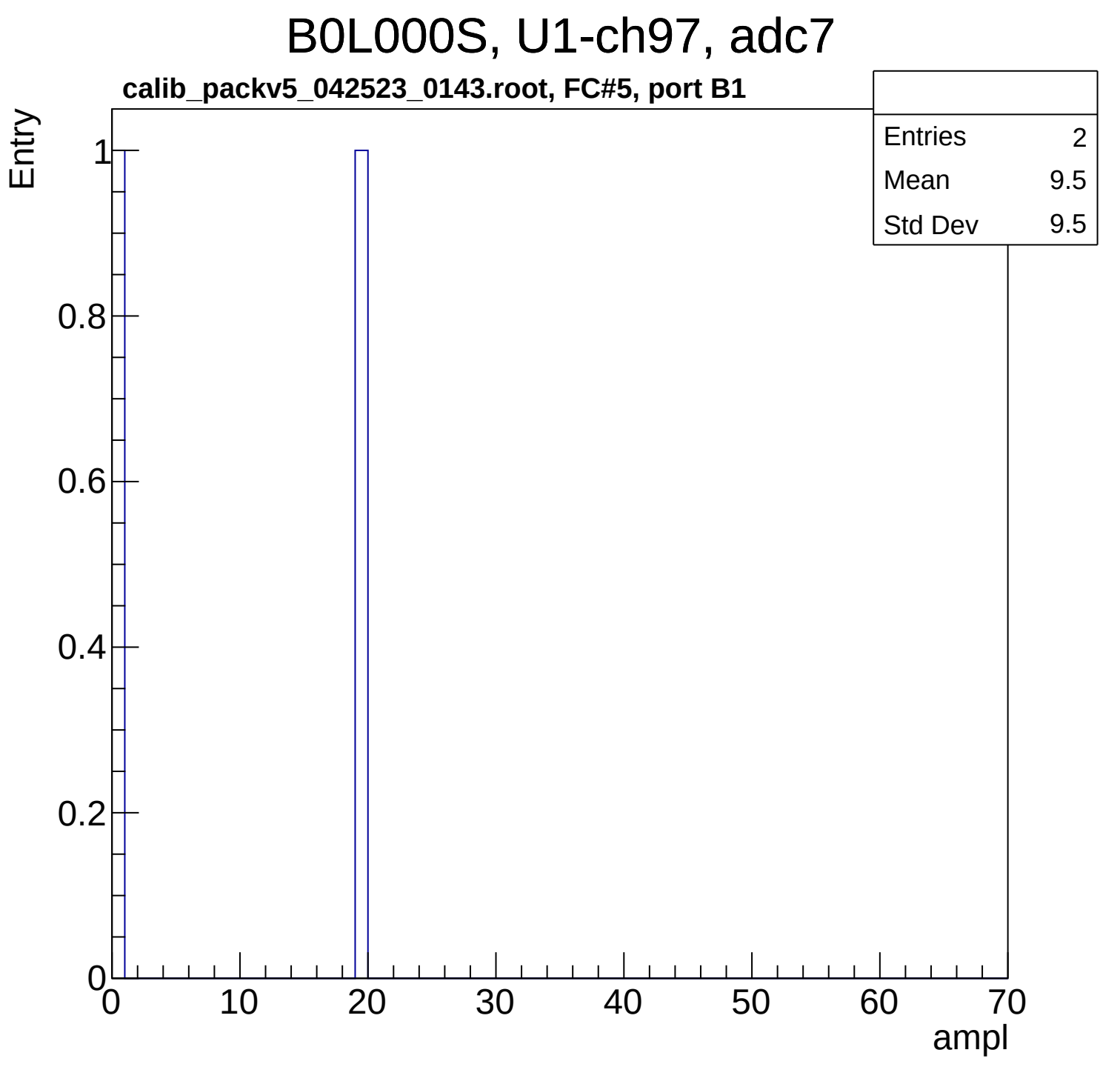
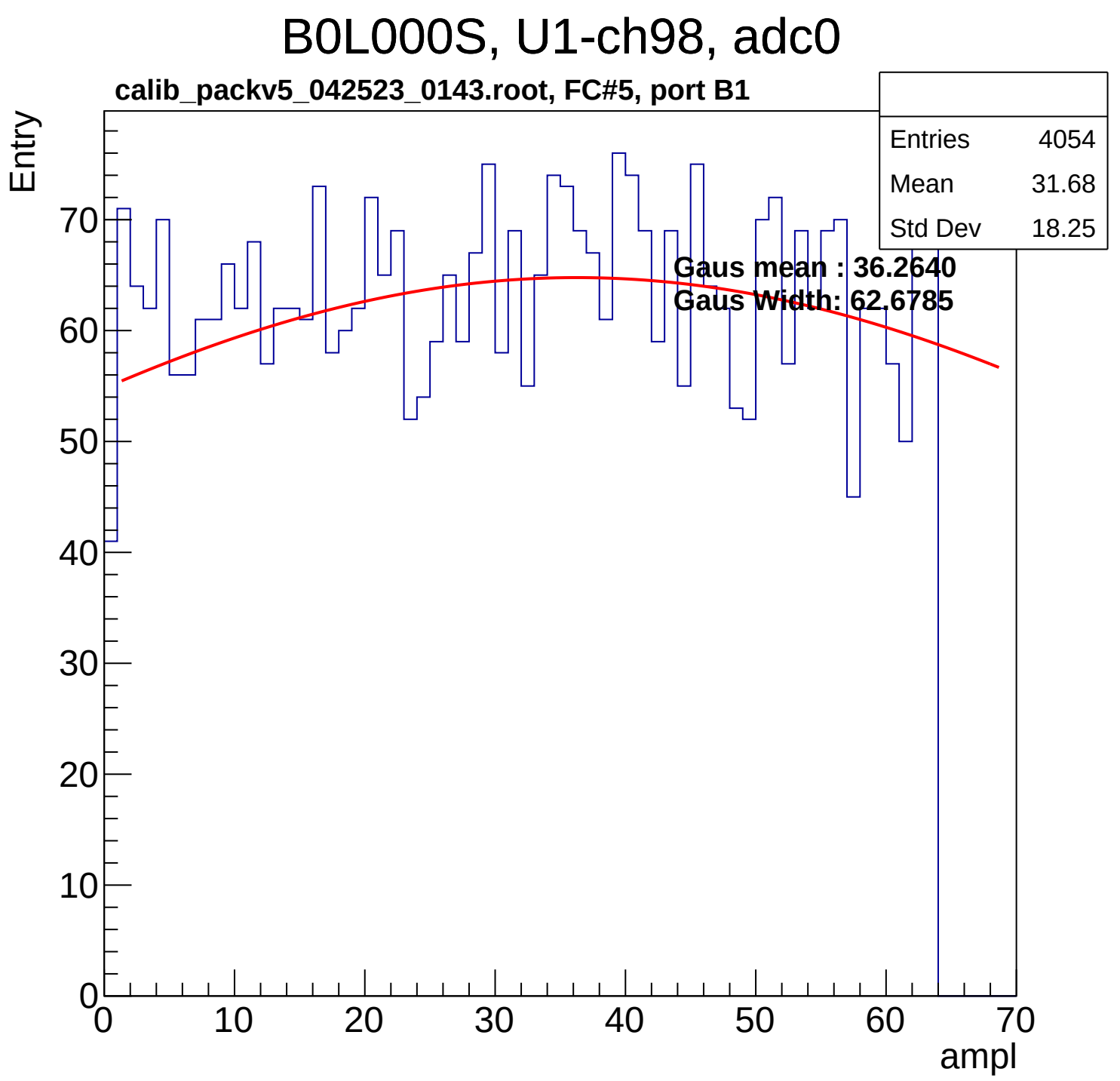
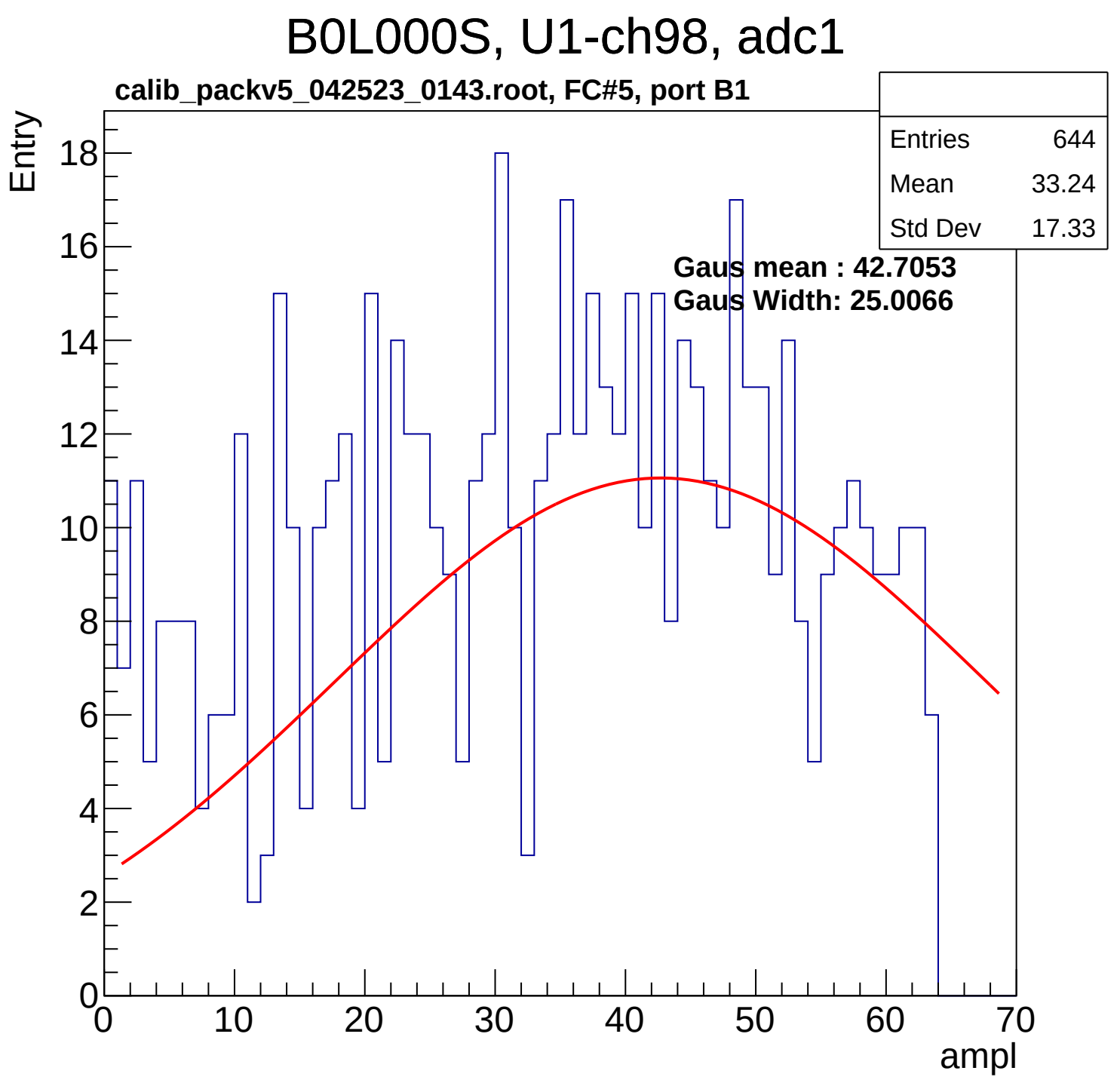
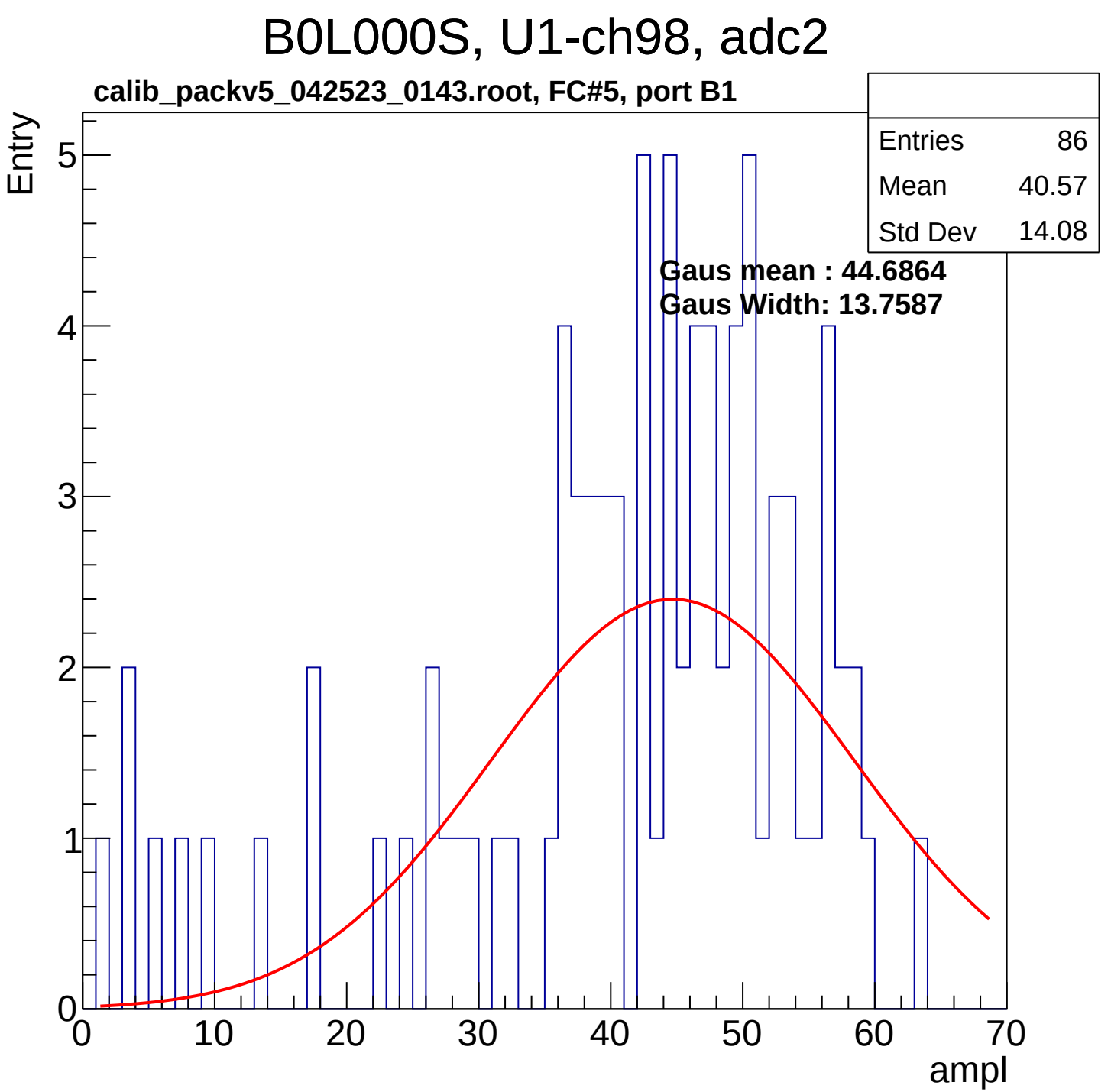

B0L000S, U1-ch97, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.56
Std Dev 9.179
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

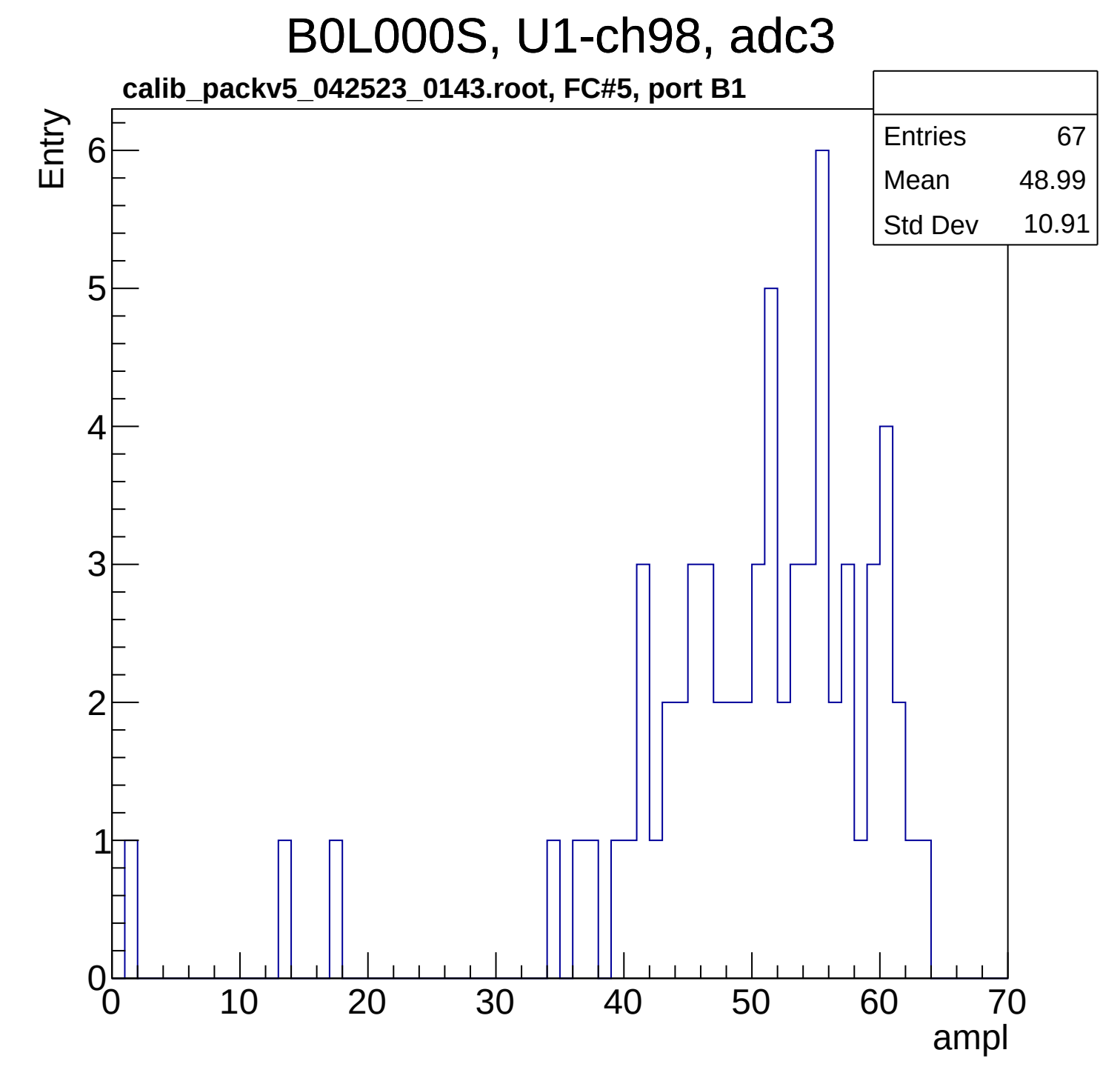

B0L000S, U1-ch97, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.71
Std Dev 1.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

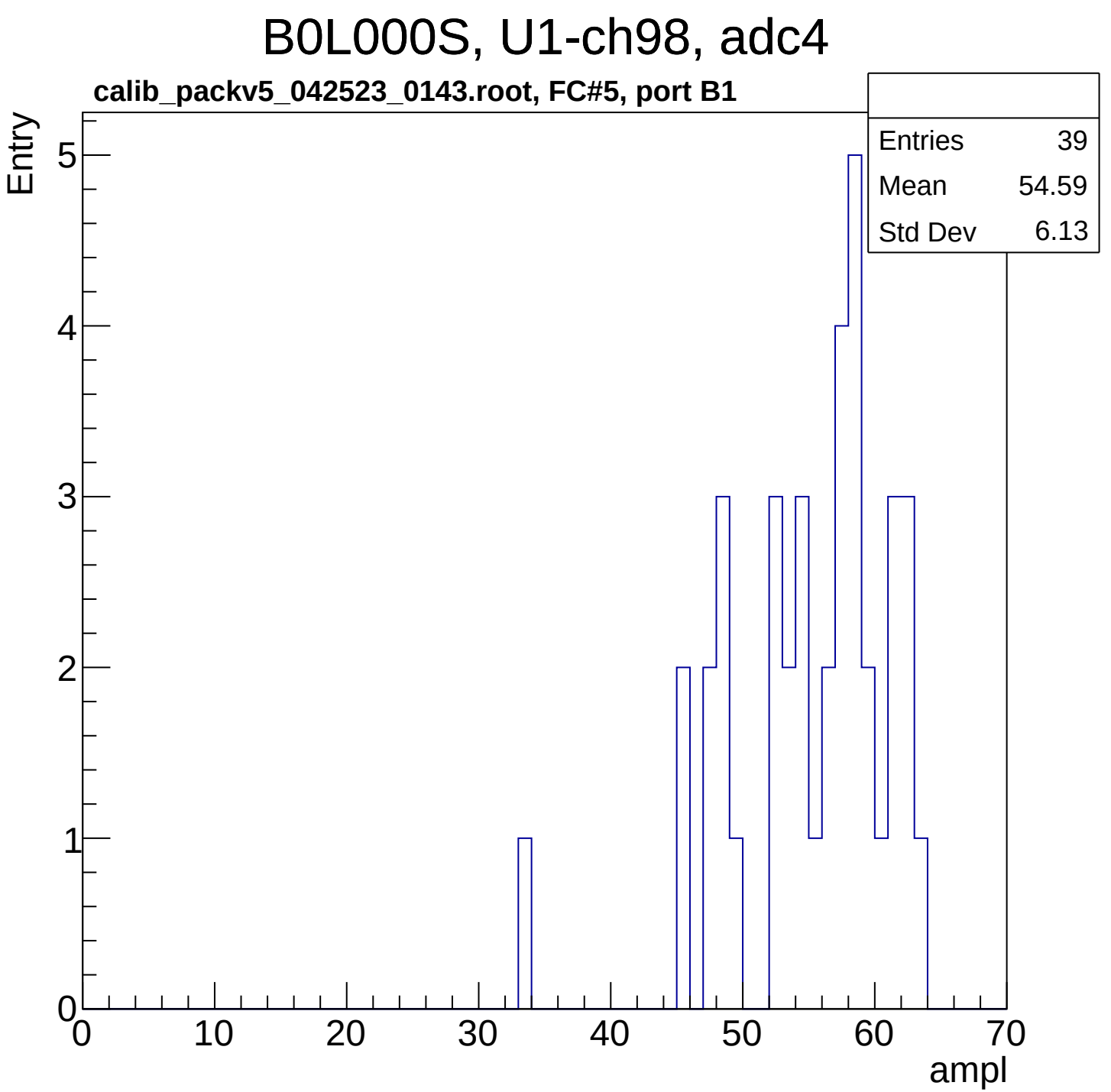

B0L000S, U1-ch97, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

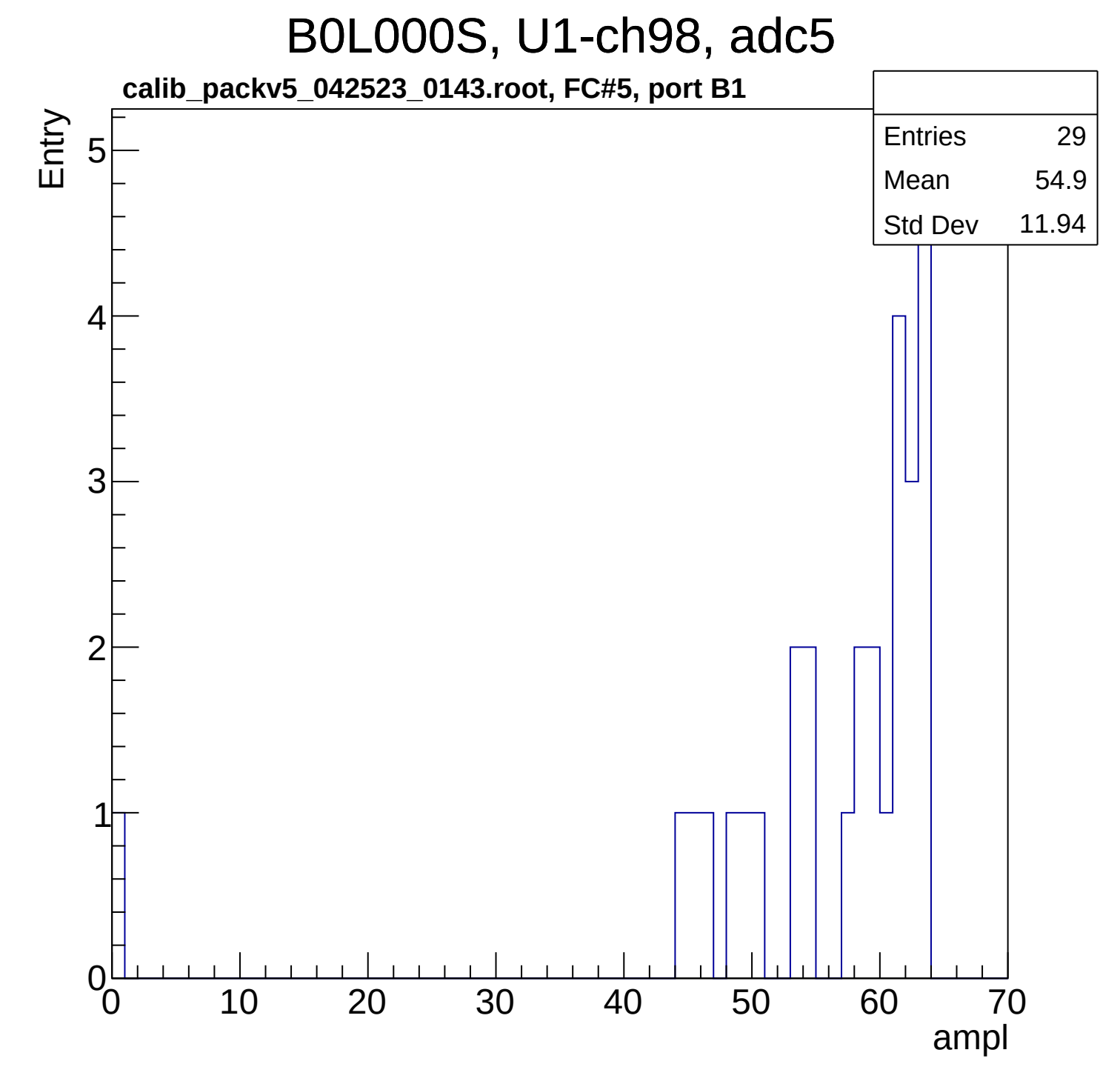

B0L000S, U1-ch98, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4054
Mean 31.68
Std Dev 18.25
Gaus mean : 36.2640
Gaus Width: 62.6785
Entry
ampl
0
10
20
30
40
50
60
70

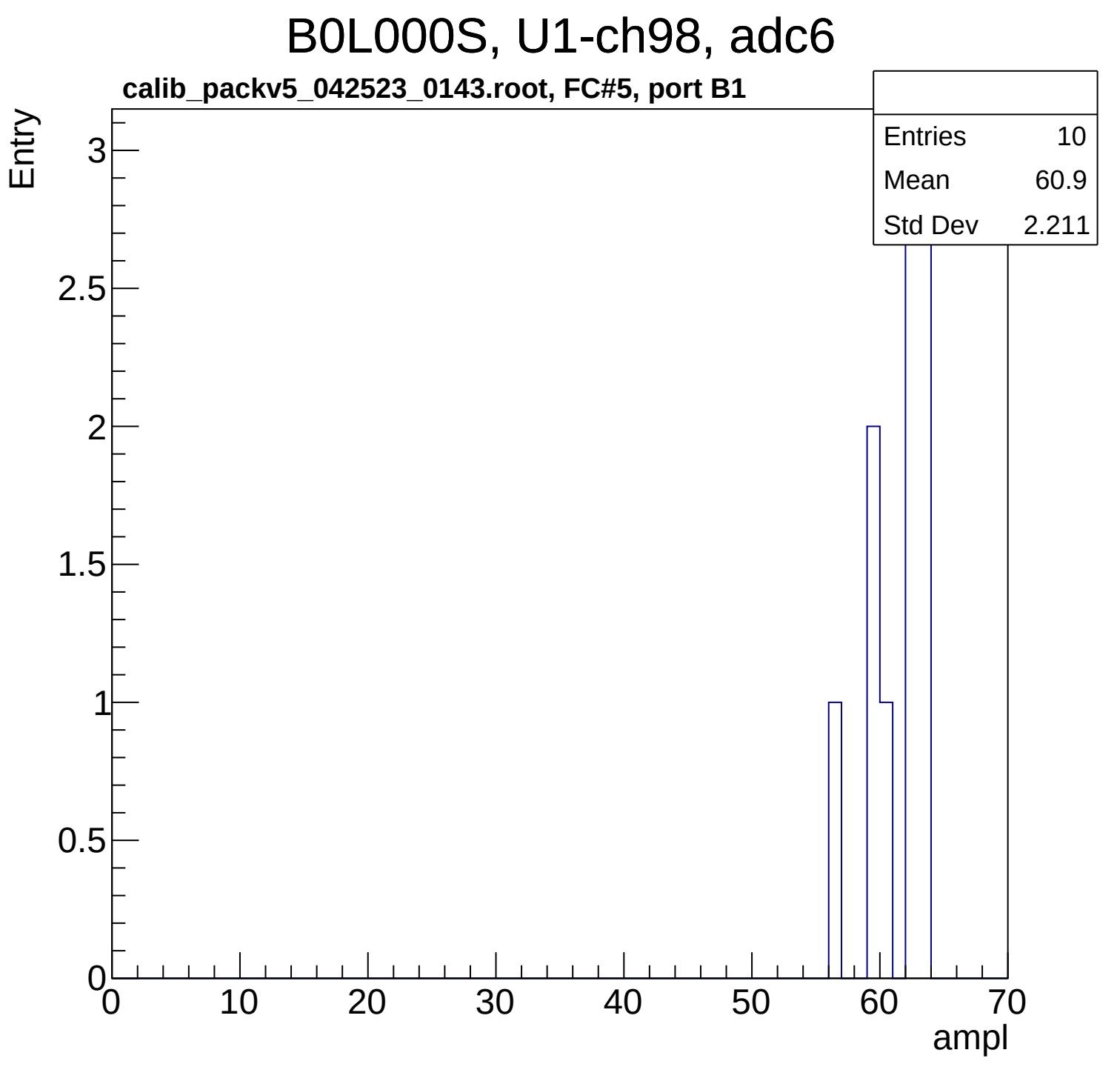

B0L000S, U1-ch98, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 644
Mean 33.24
Std Dev 17.33
Gaus mean : 42.7053
Gaus Width: 25.0066
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B0L000S, U1-ch98, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 40.57
Std Dev 14.08
Gaus mean : 44.6864
Gaus Width: 13.7587
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U1-ch98, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.99
Std Dev 10.91
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch98, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 54.59
Std Dev 6.13
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch98, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 54.9
Std Dev 11.94
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch98, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 60.9
Std Dev 2.211
Entry
ampl
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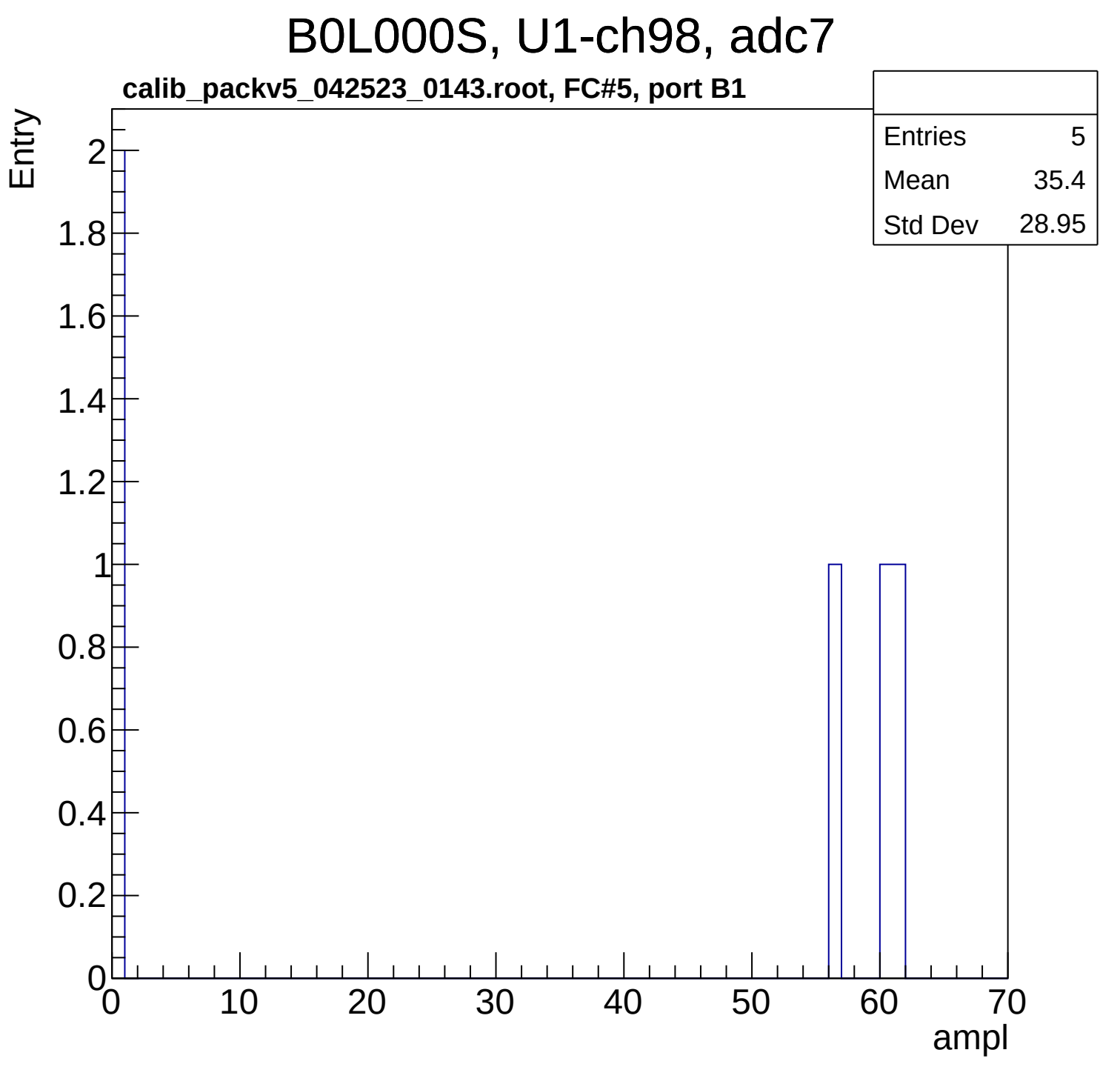
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch98, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 35.4
Std Dev 28.95
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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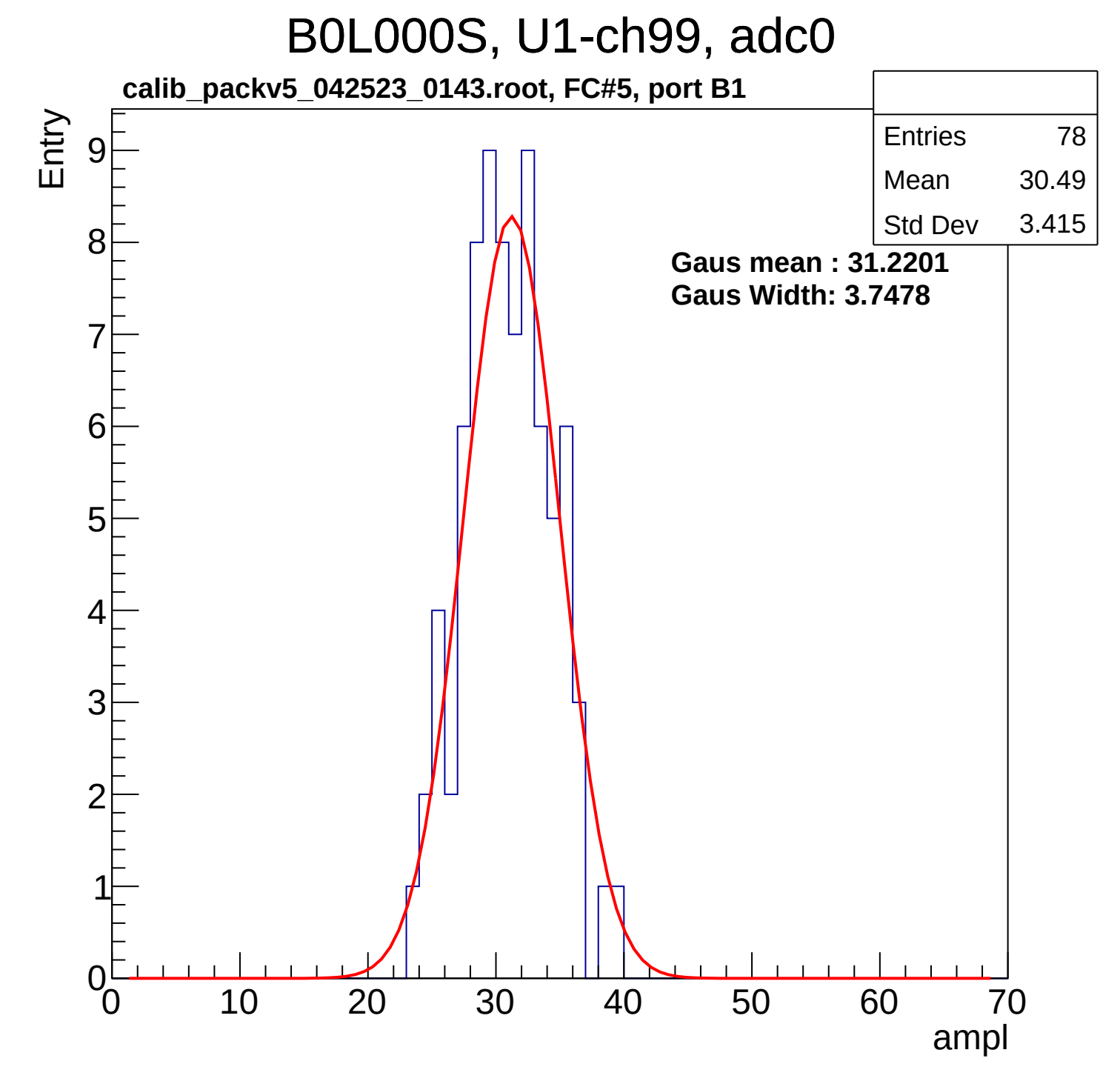
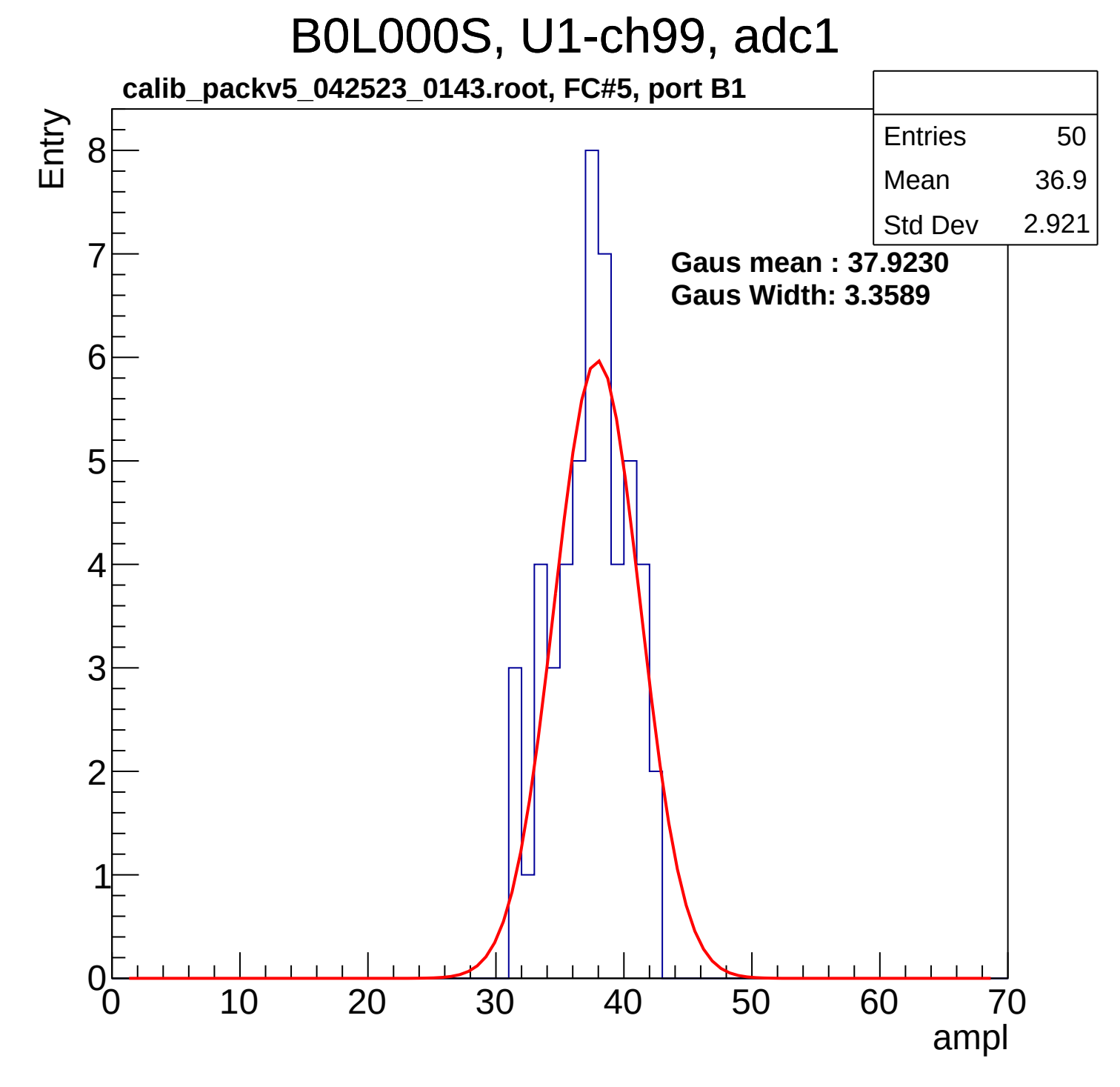
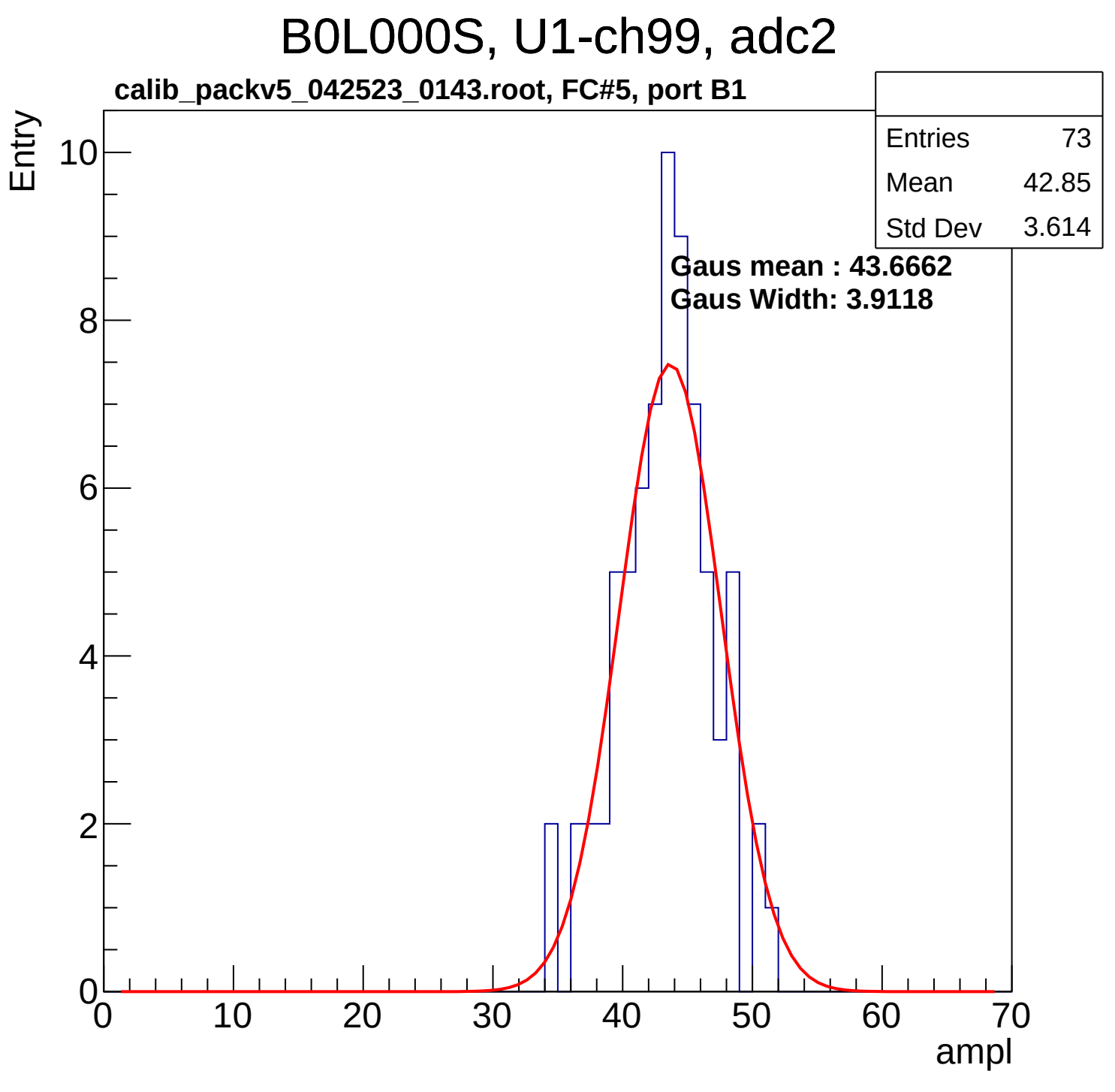
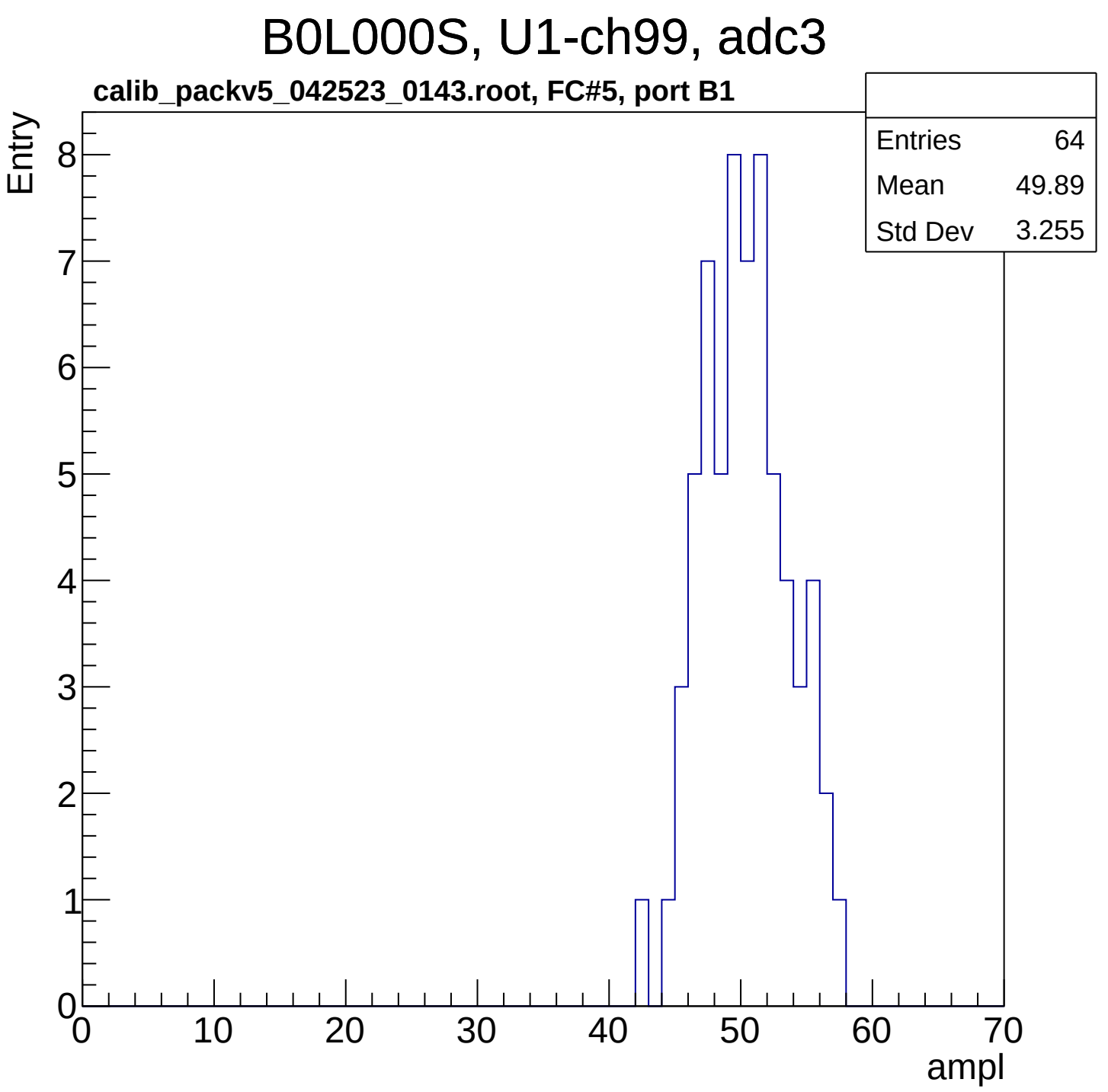

B0L000S, U1-ch99, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 30.49
Std Dev 3.415
Gaus mean : 31.2201
Gaus Width: 3.7478
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch99, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 36.9
Std Dev 2.921
Gaus mean : 37.9230
Gaus Width: 3.3589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch99, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.85
Std Dev 3.614
Gaus mean : 43.6662
Gaus Width: 3.9118
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch99, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.89
Std Dev 3.255
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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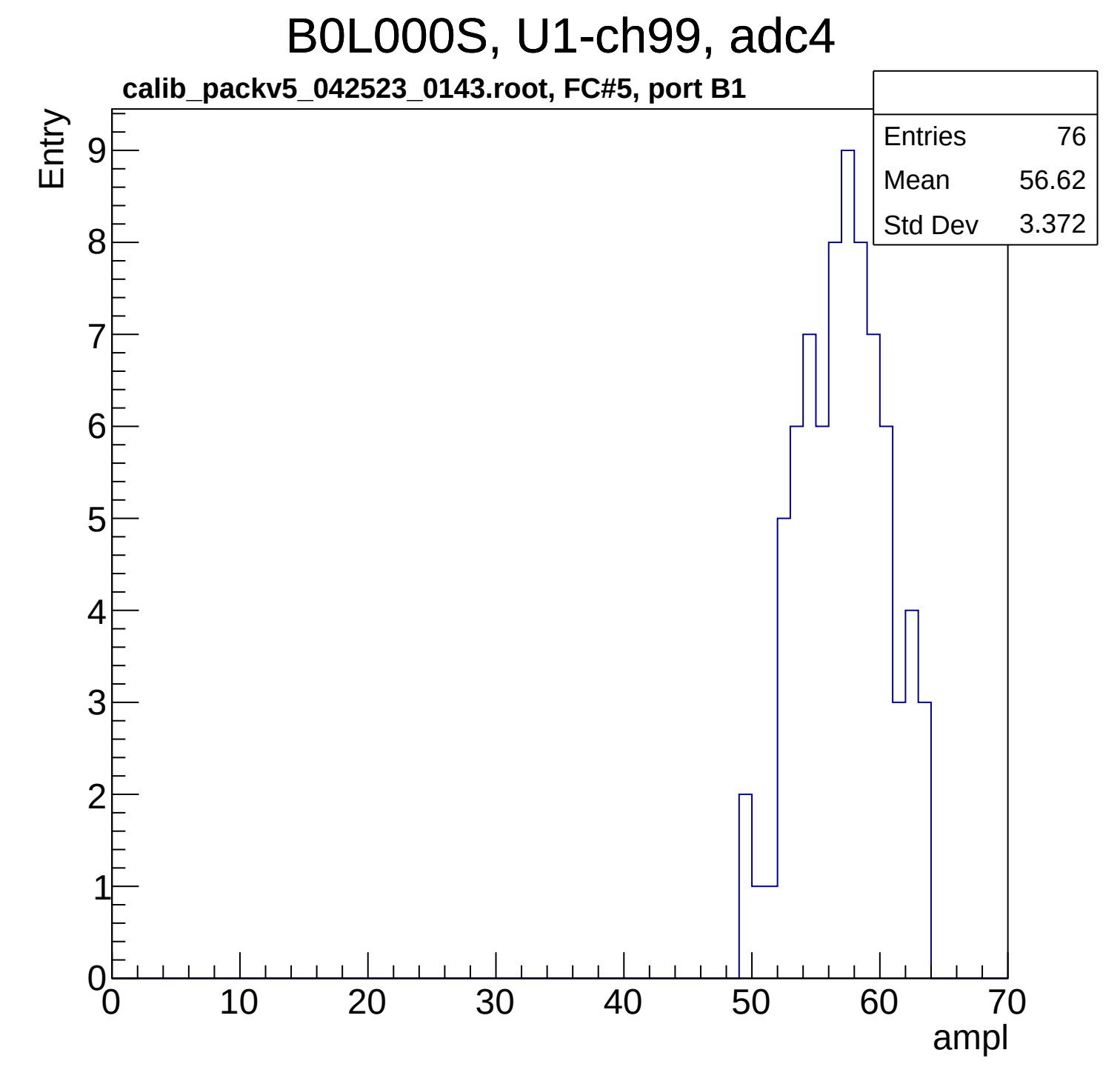
40
50
60
70


B0L000S, U1-ch99, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 56.62
Std Dev 3.372
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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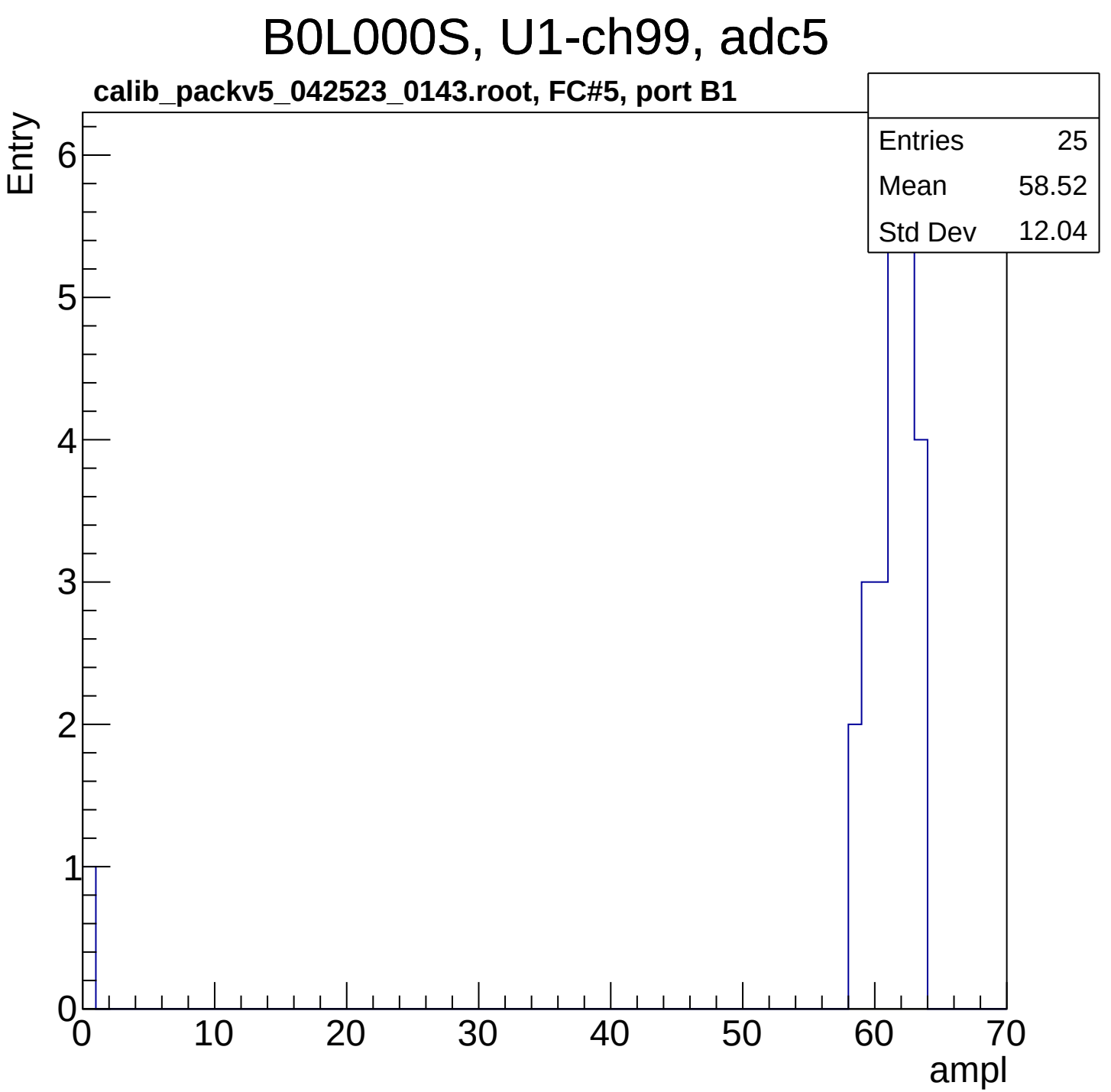
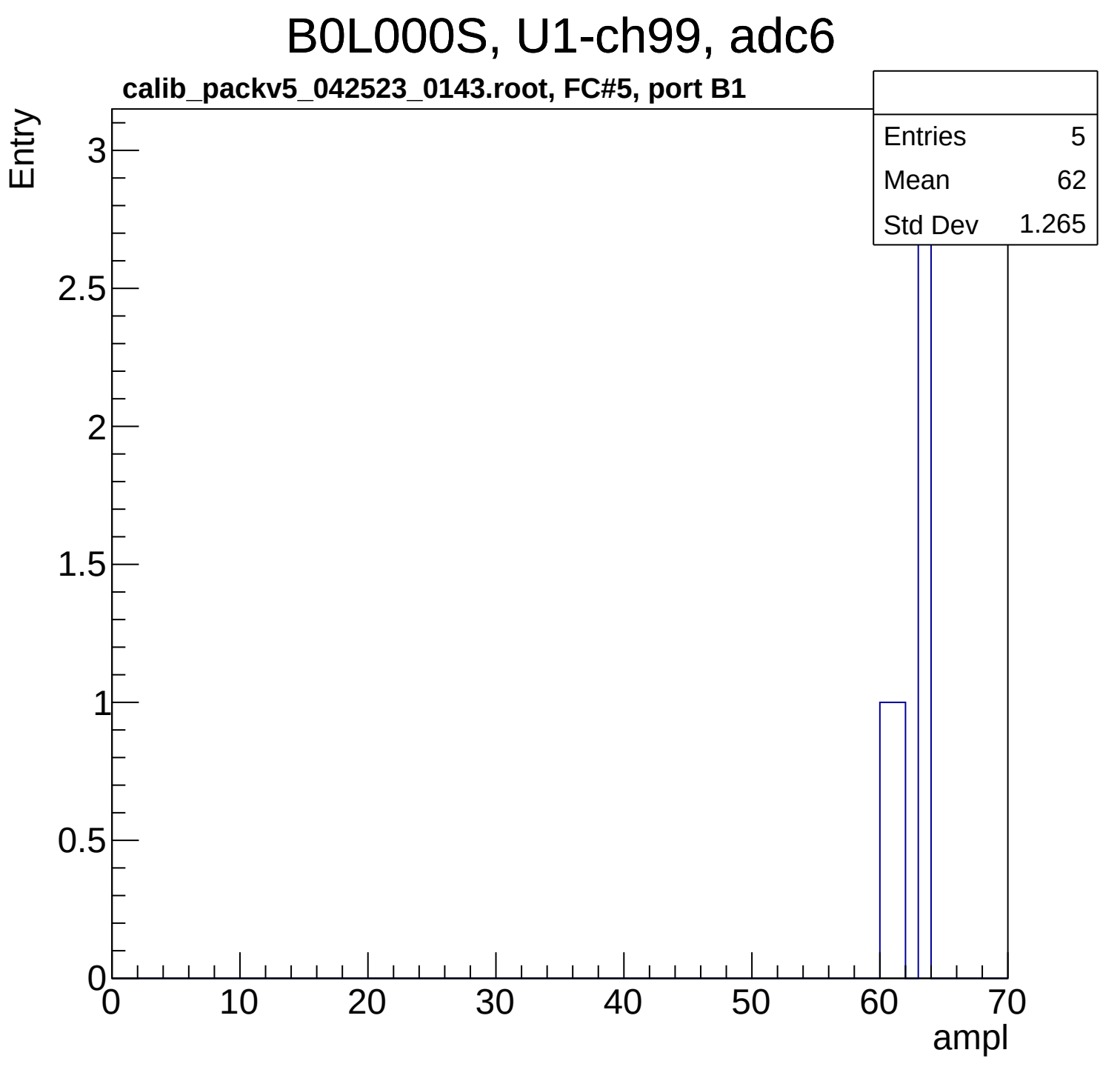

B0L000S, U1-ch99, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 58.52
Std Dev 12.04
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch99, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 1.265
Entry
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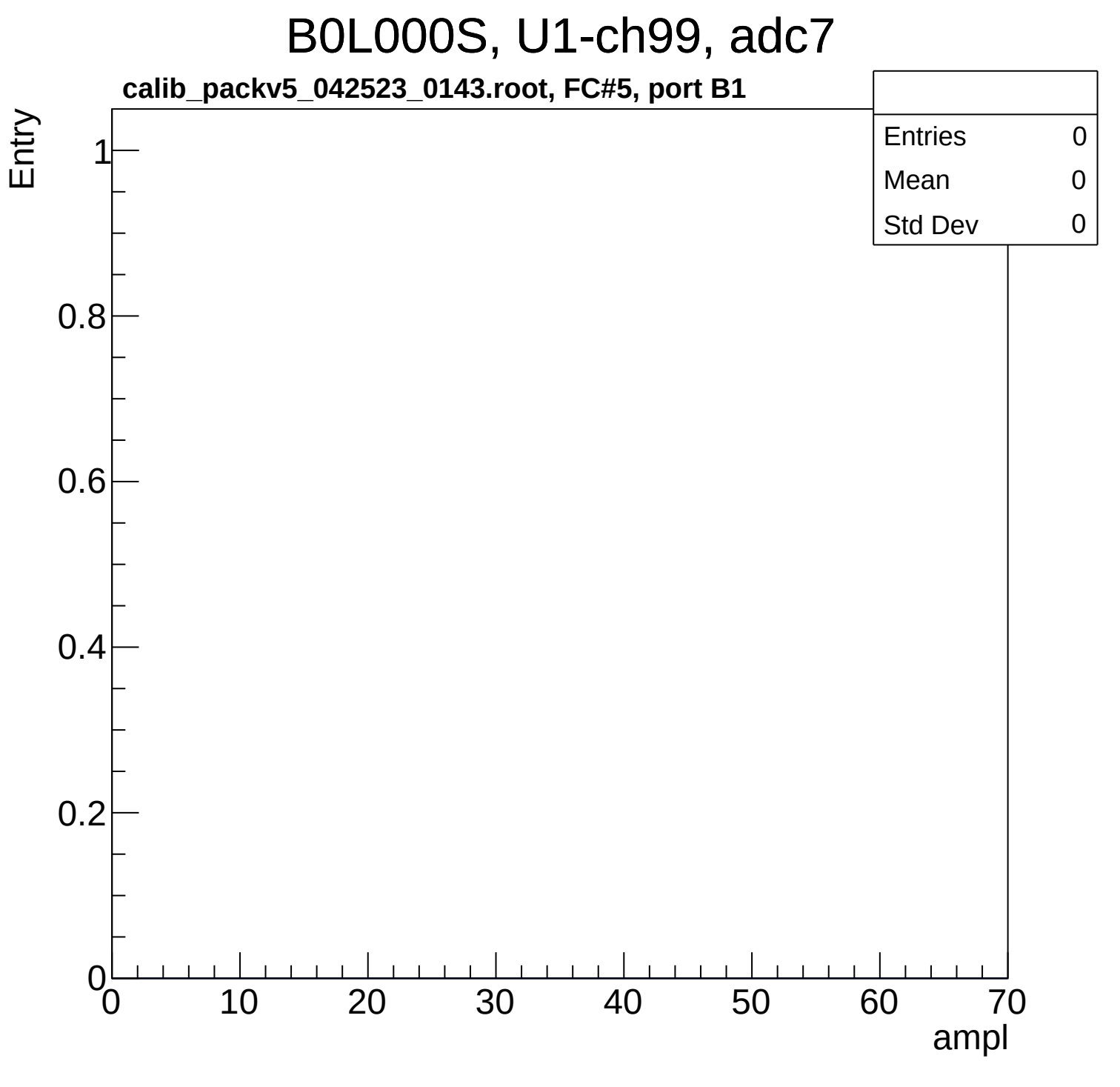
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch99, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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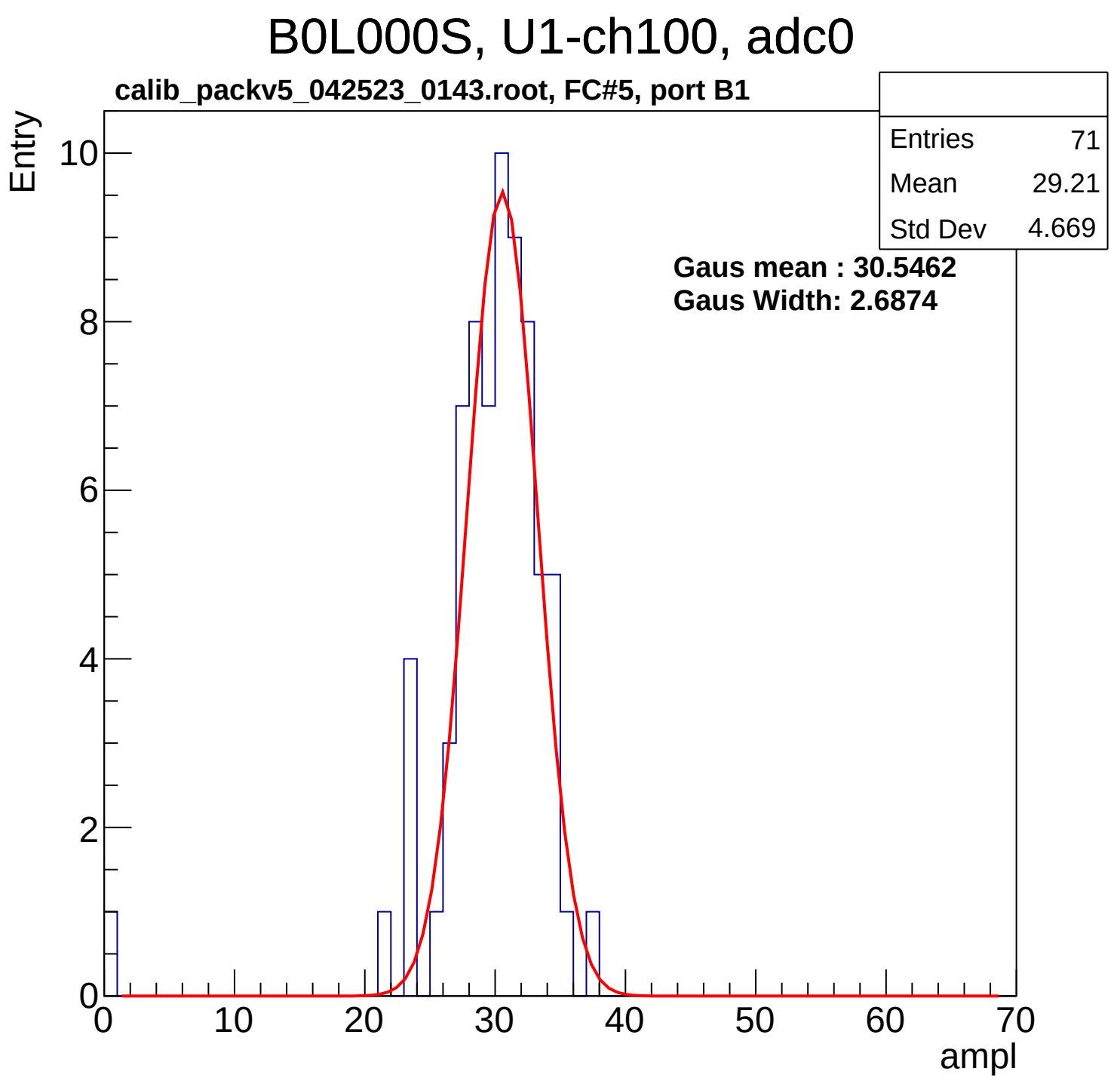
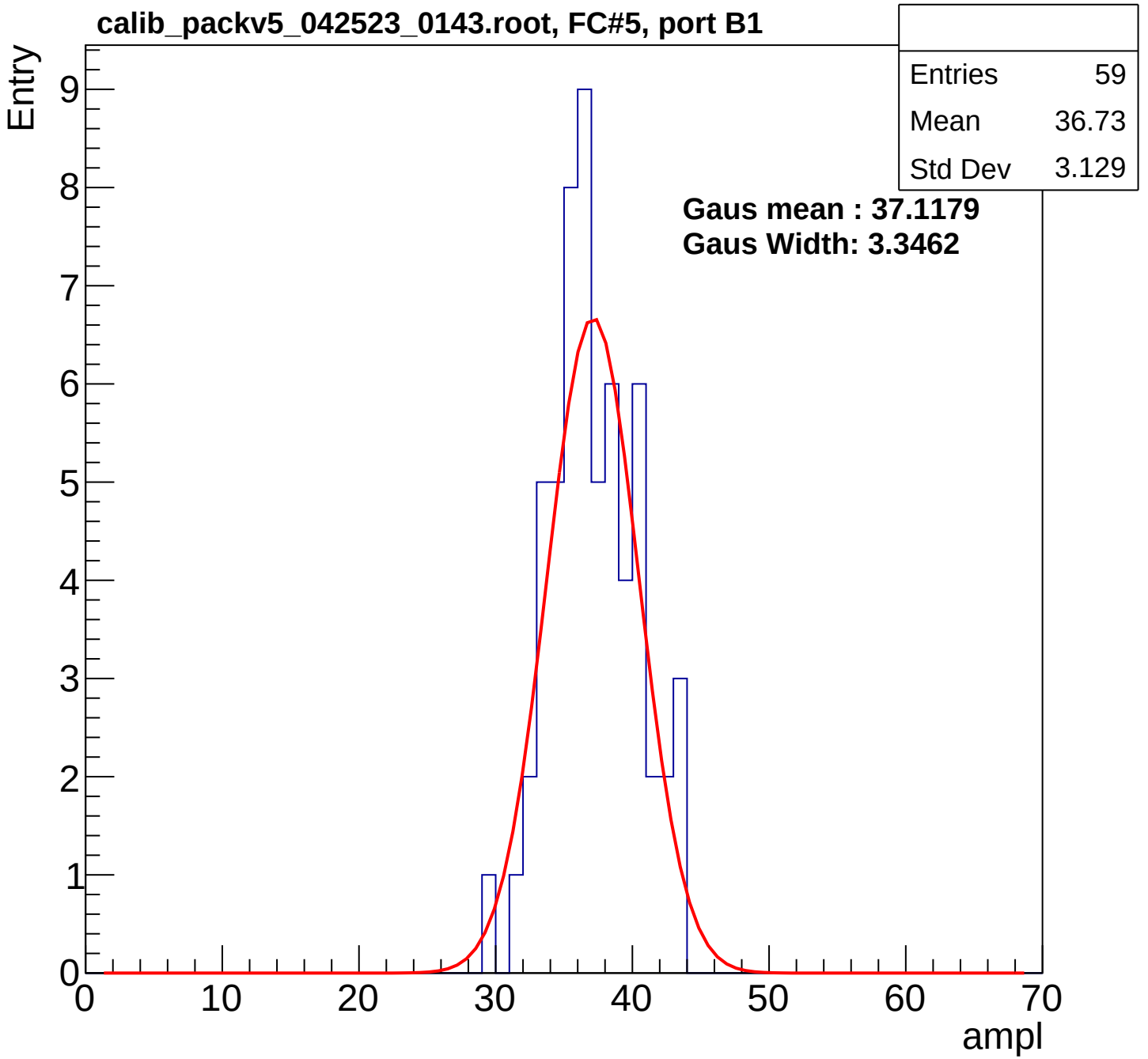
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch100, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.21
Std Dev 4.669
Gaus mean : 30.5462
Gaus Width: 2.6874
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch100, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 36.73
Std Dev 3.129
Gaus mean : 37.1179
Gaus Width: 3.3462
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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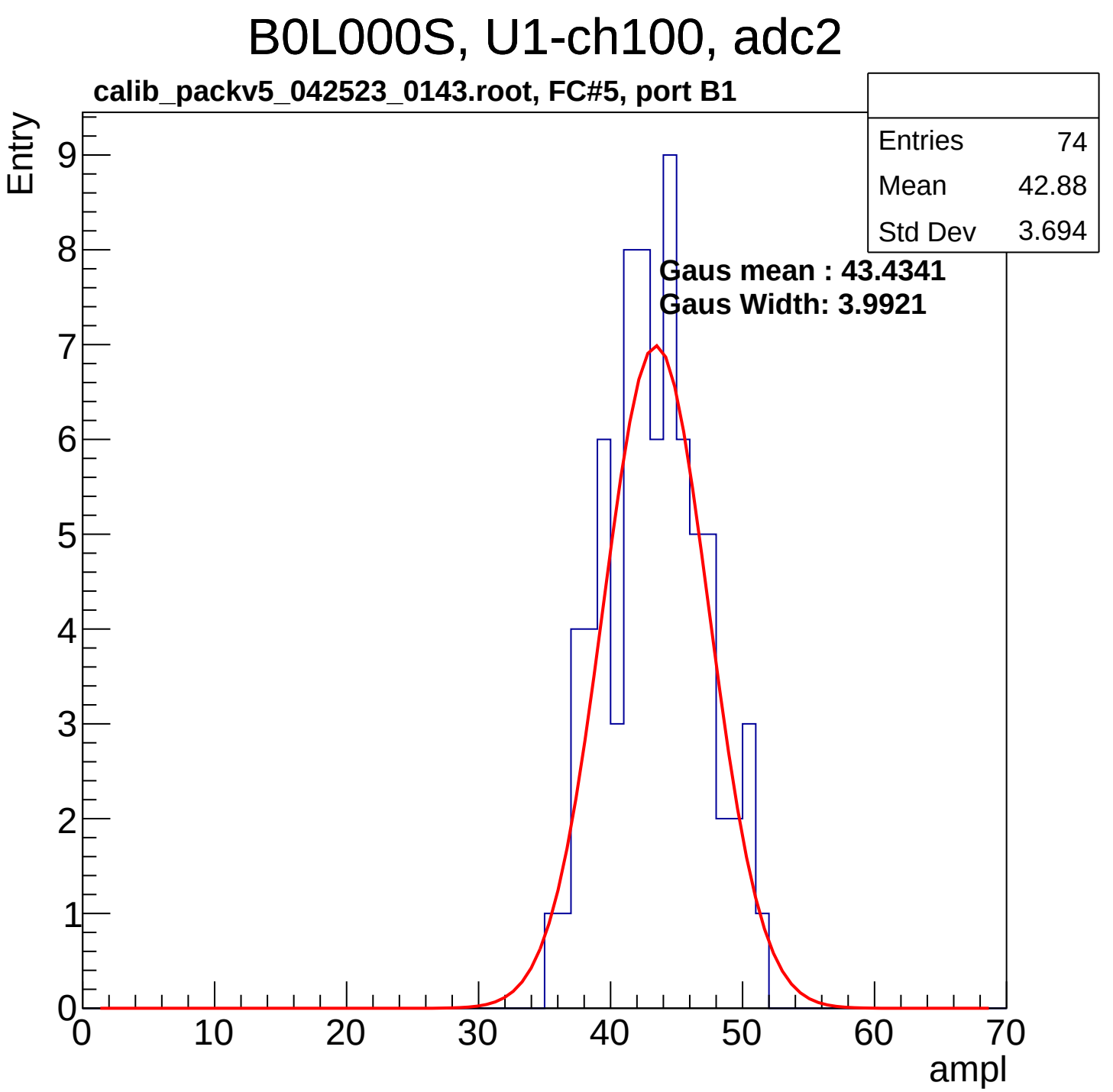
B0L000S, U1-ch100, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 42.88
Std Dev 3.694
Gaus mean : 43.4341
Gaus Width: 3.9921
Entry
ampl
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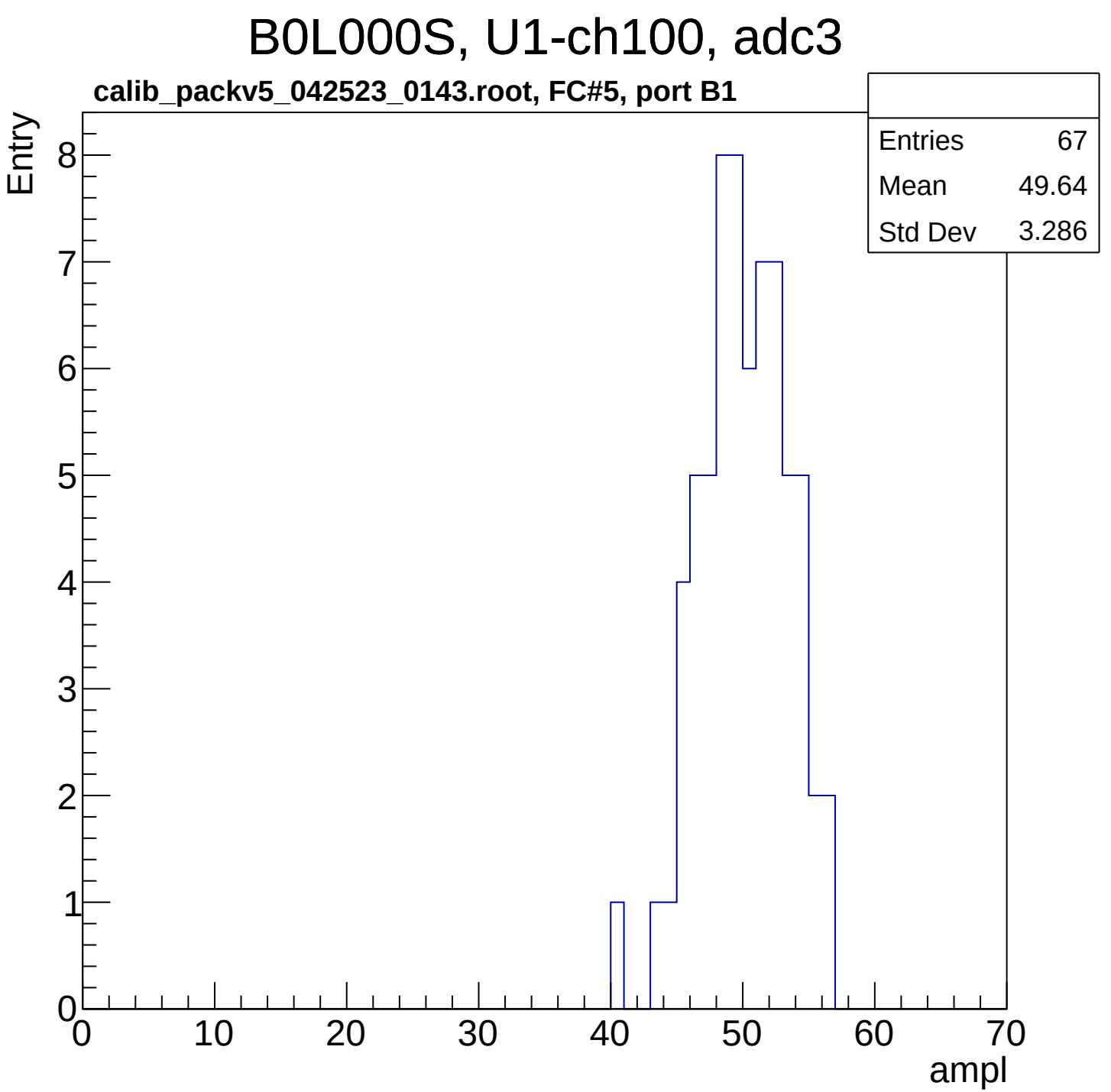
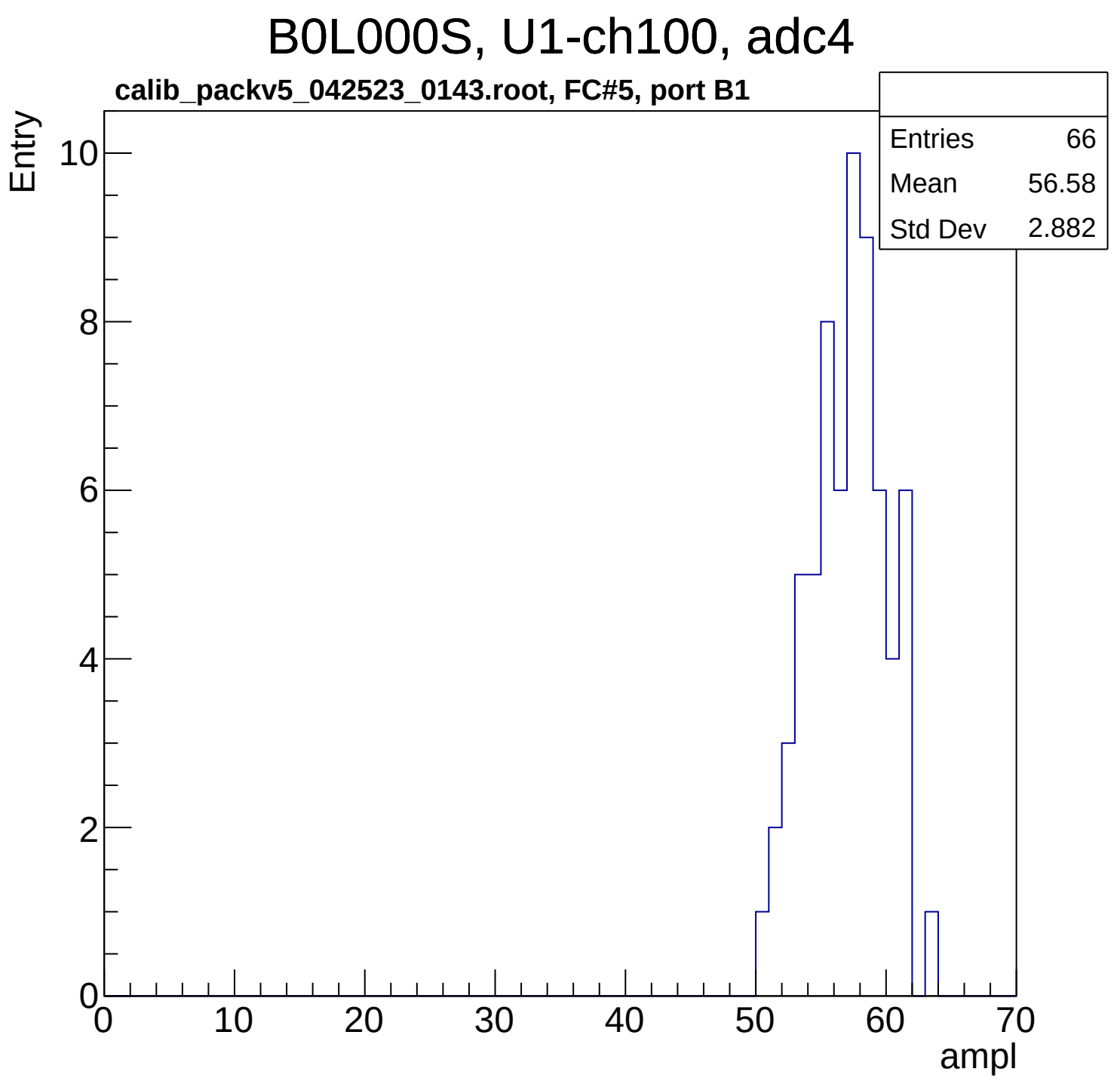
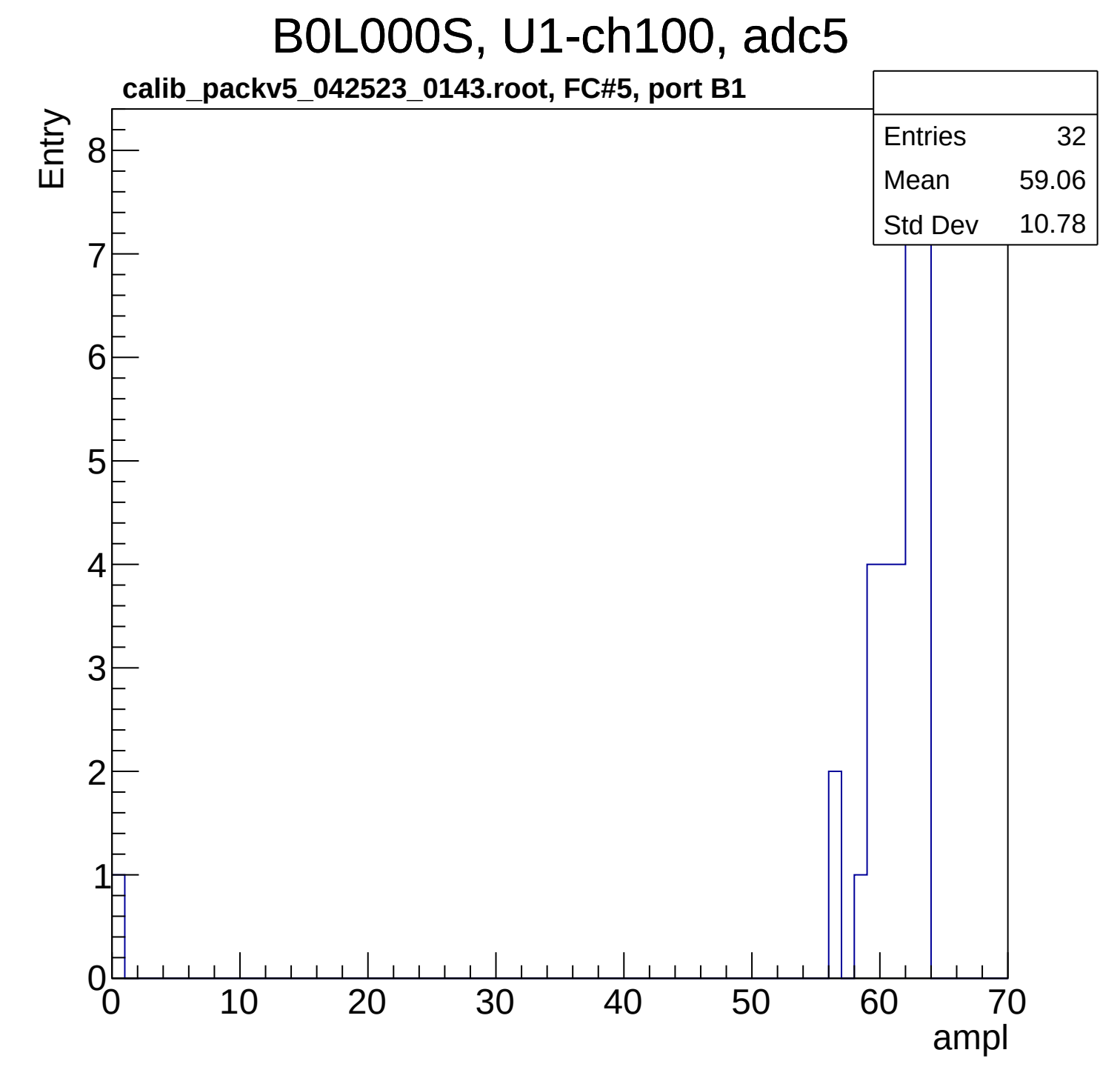
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch100, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.64
Std Dev 3.286
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch100, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 56.58
Std Dev 2.882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch100, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 59.06
Std Dev 10.78
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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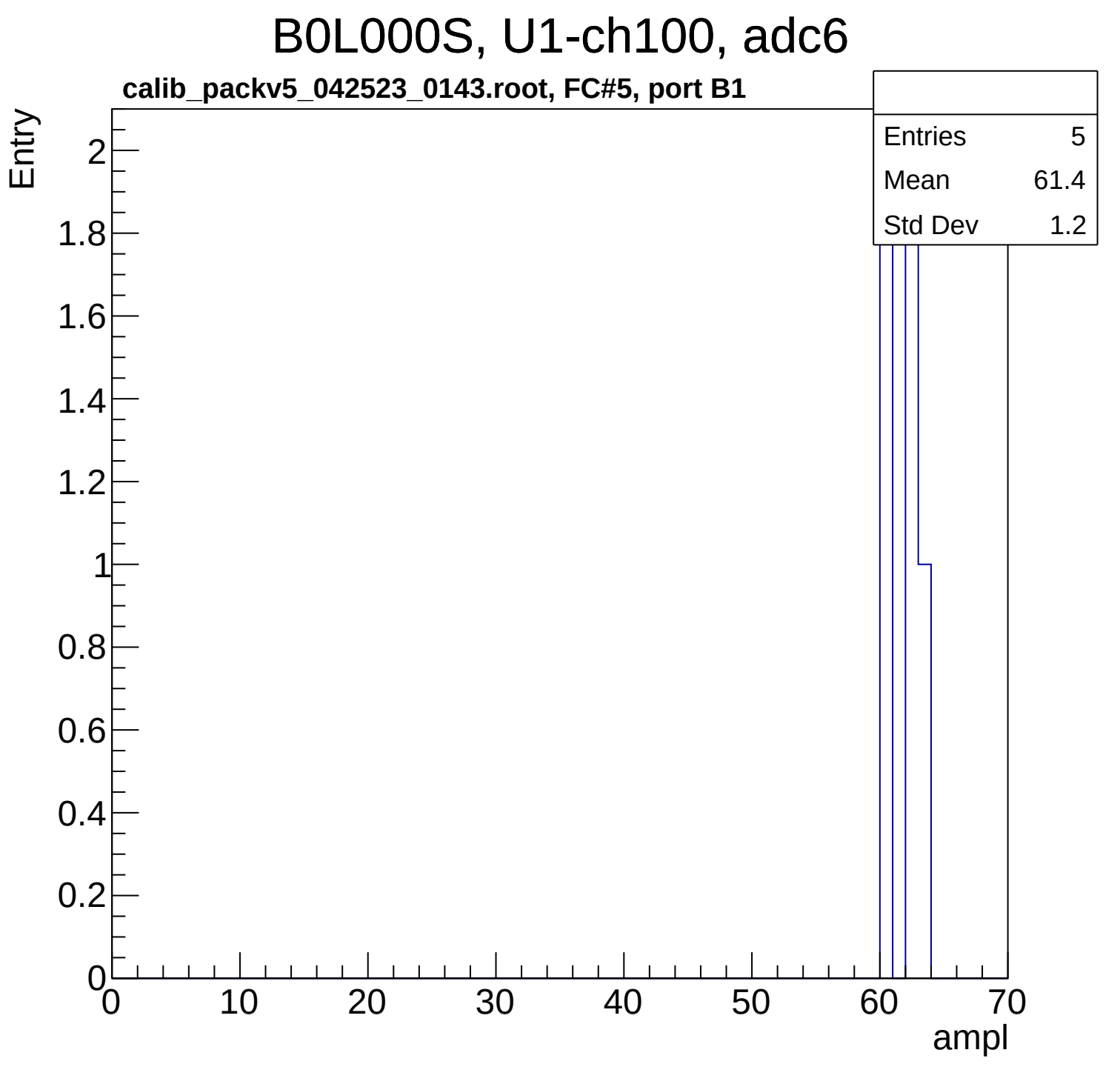
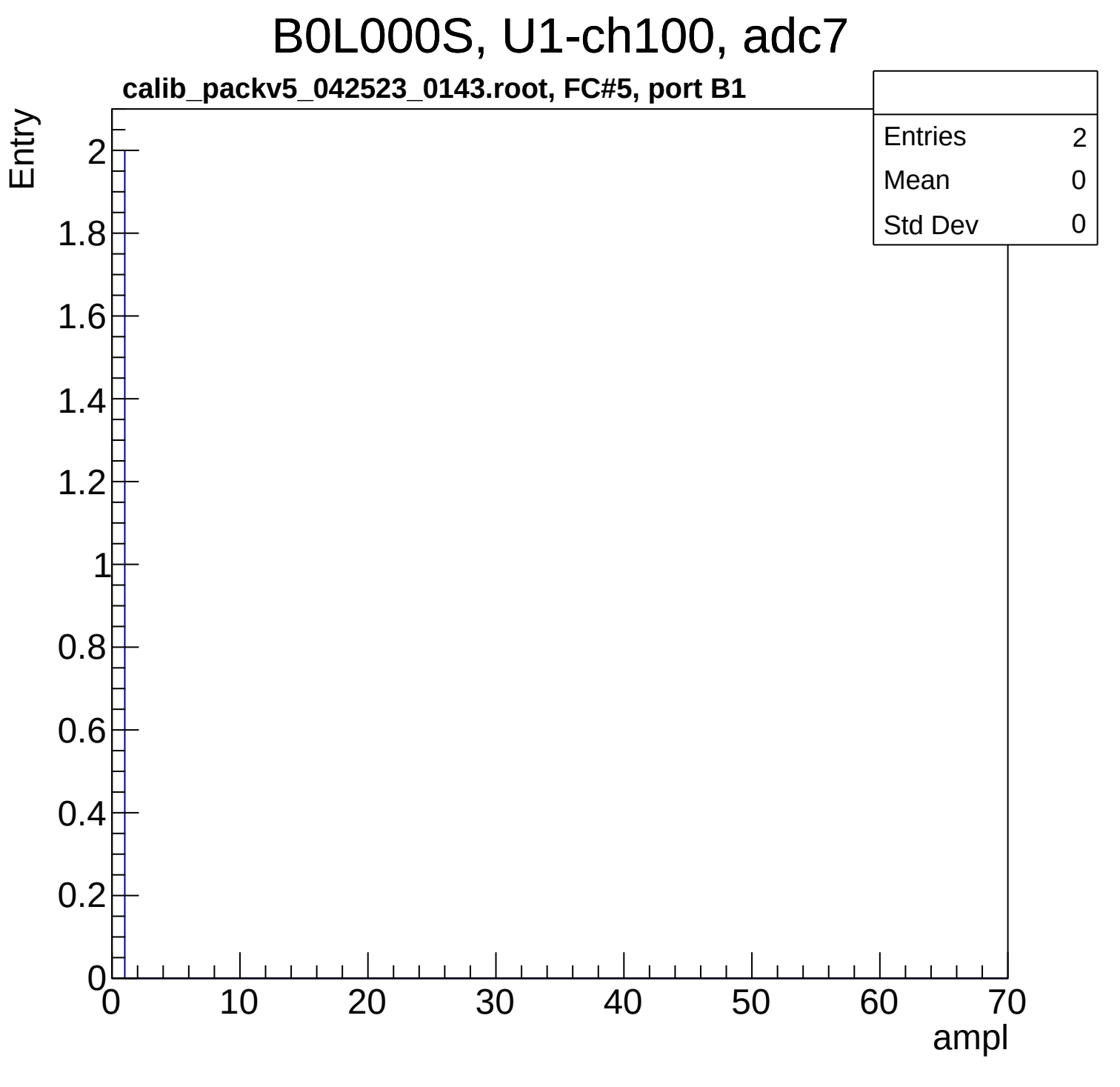
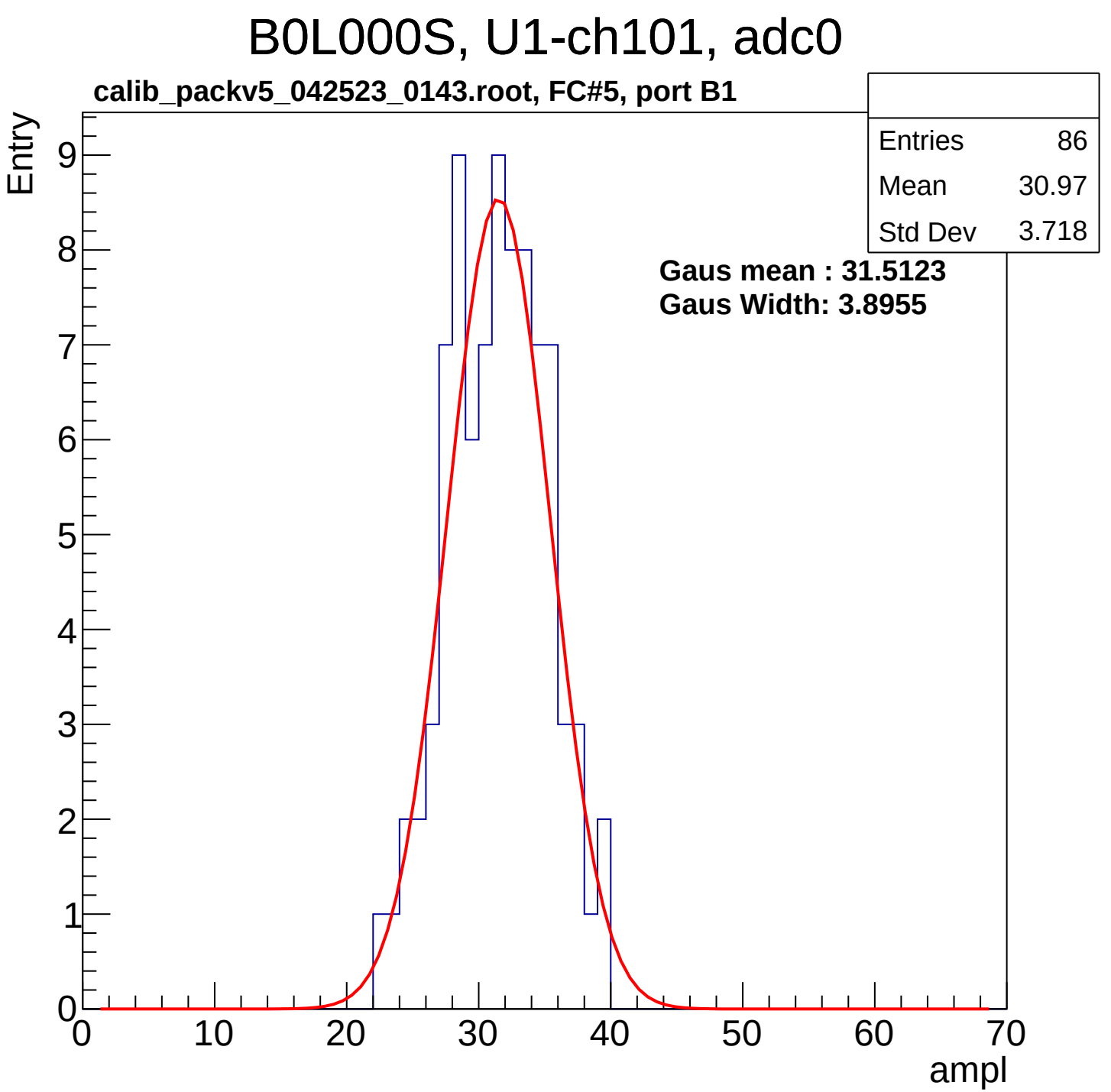
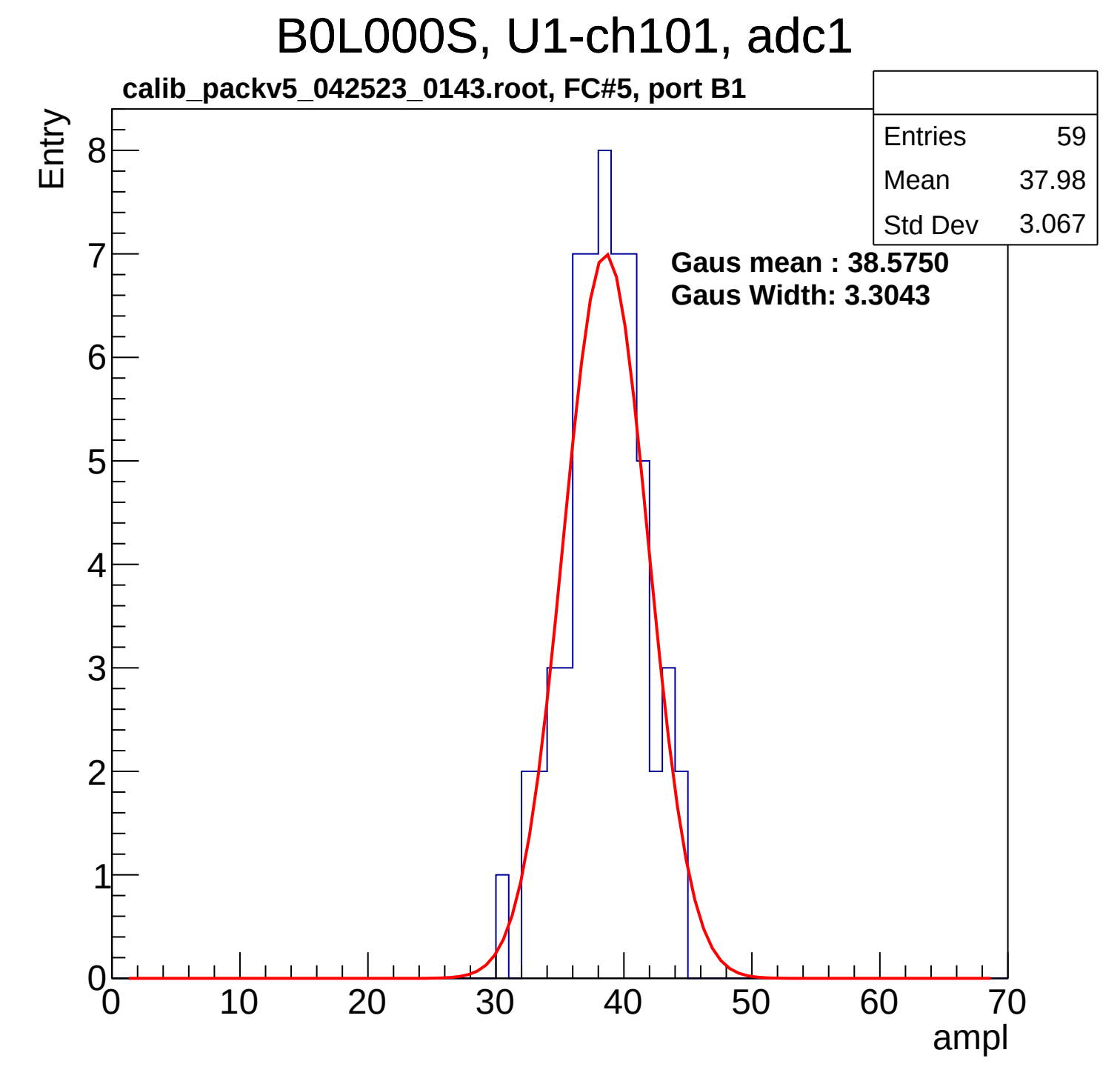
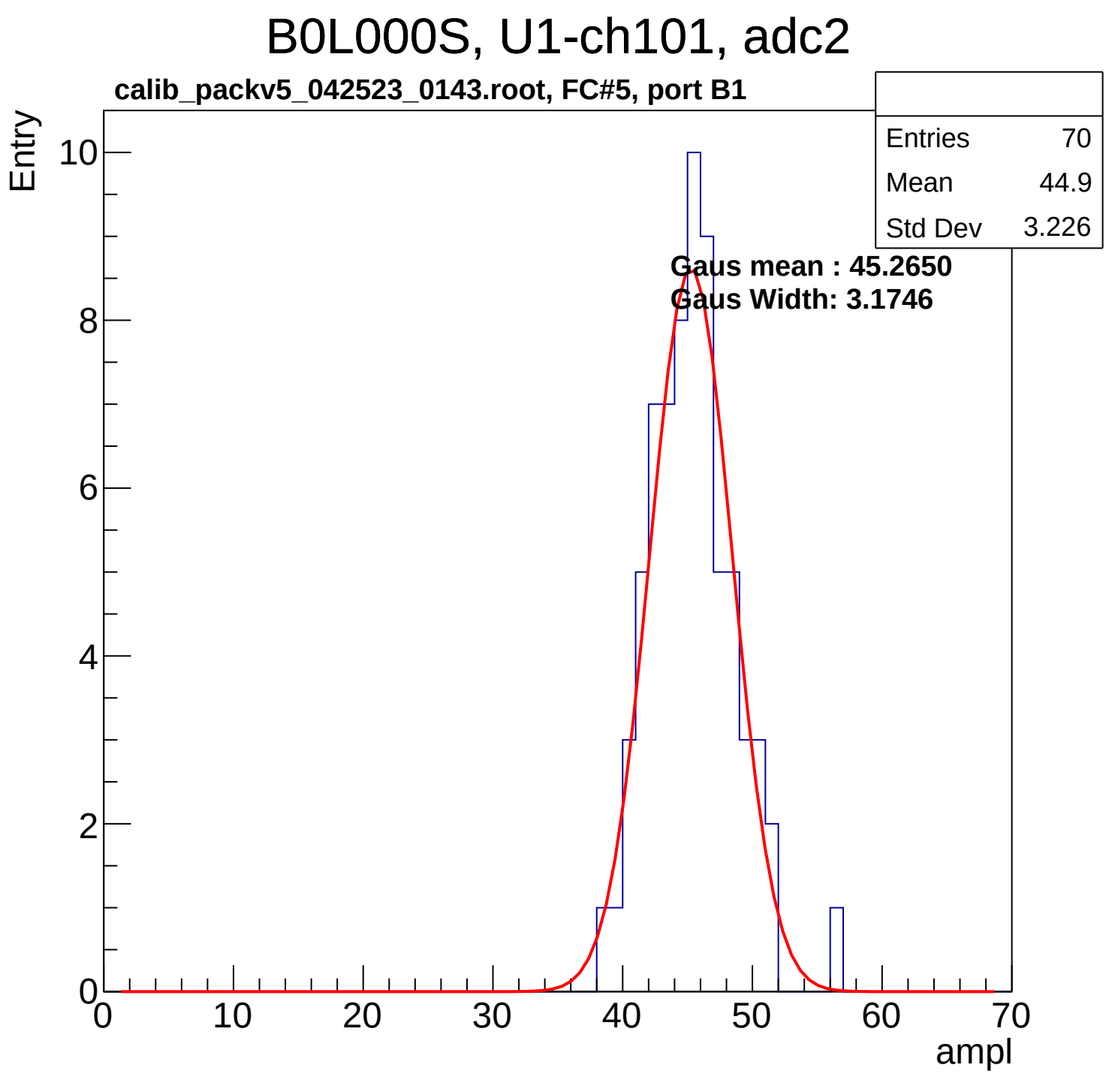
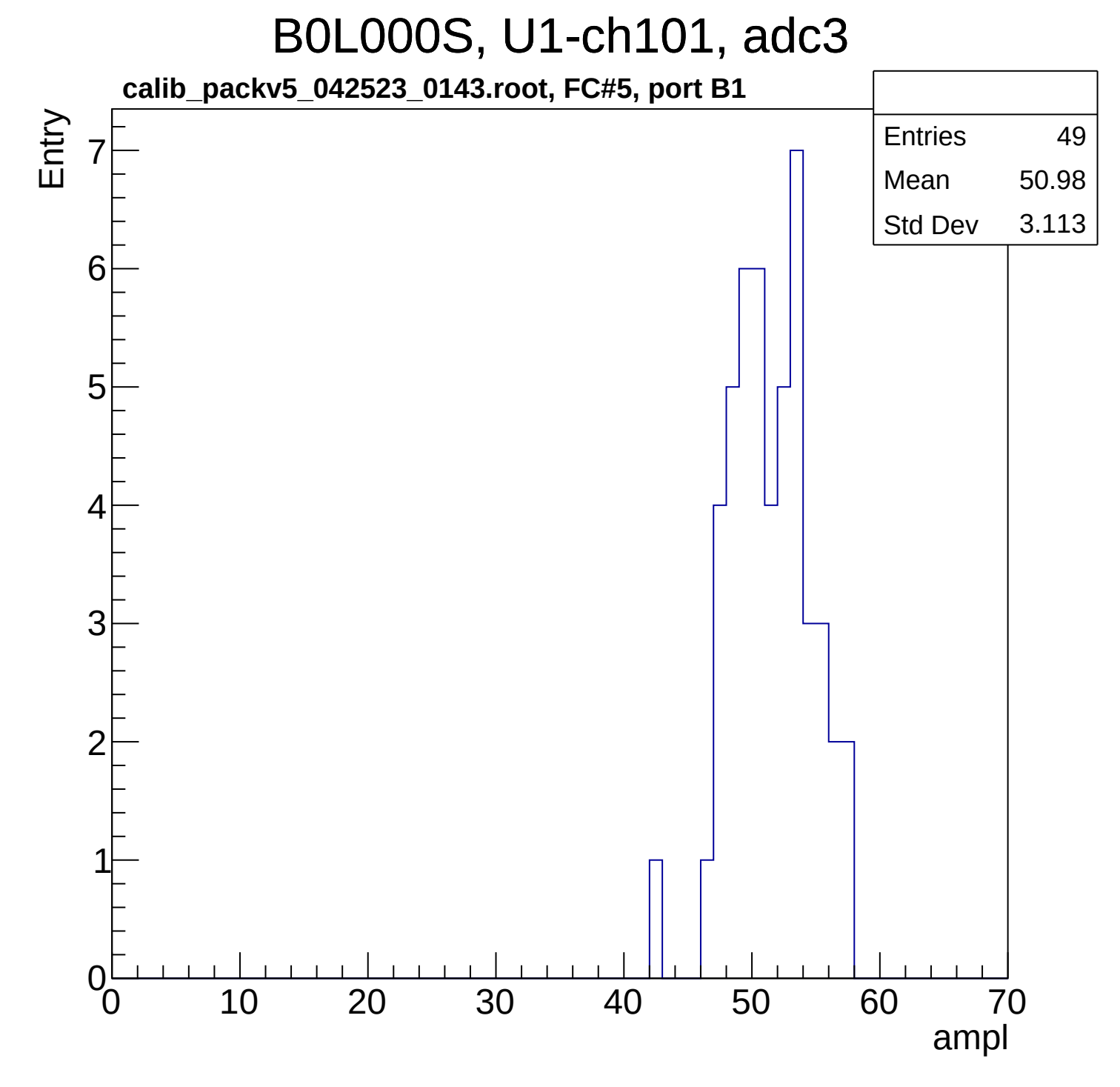

B0L000S, U1-ch100, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.4
Std Dev 1.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

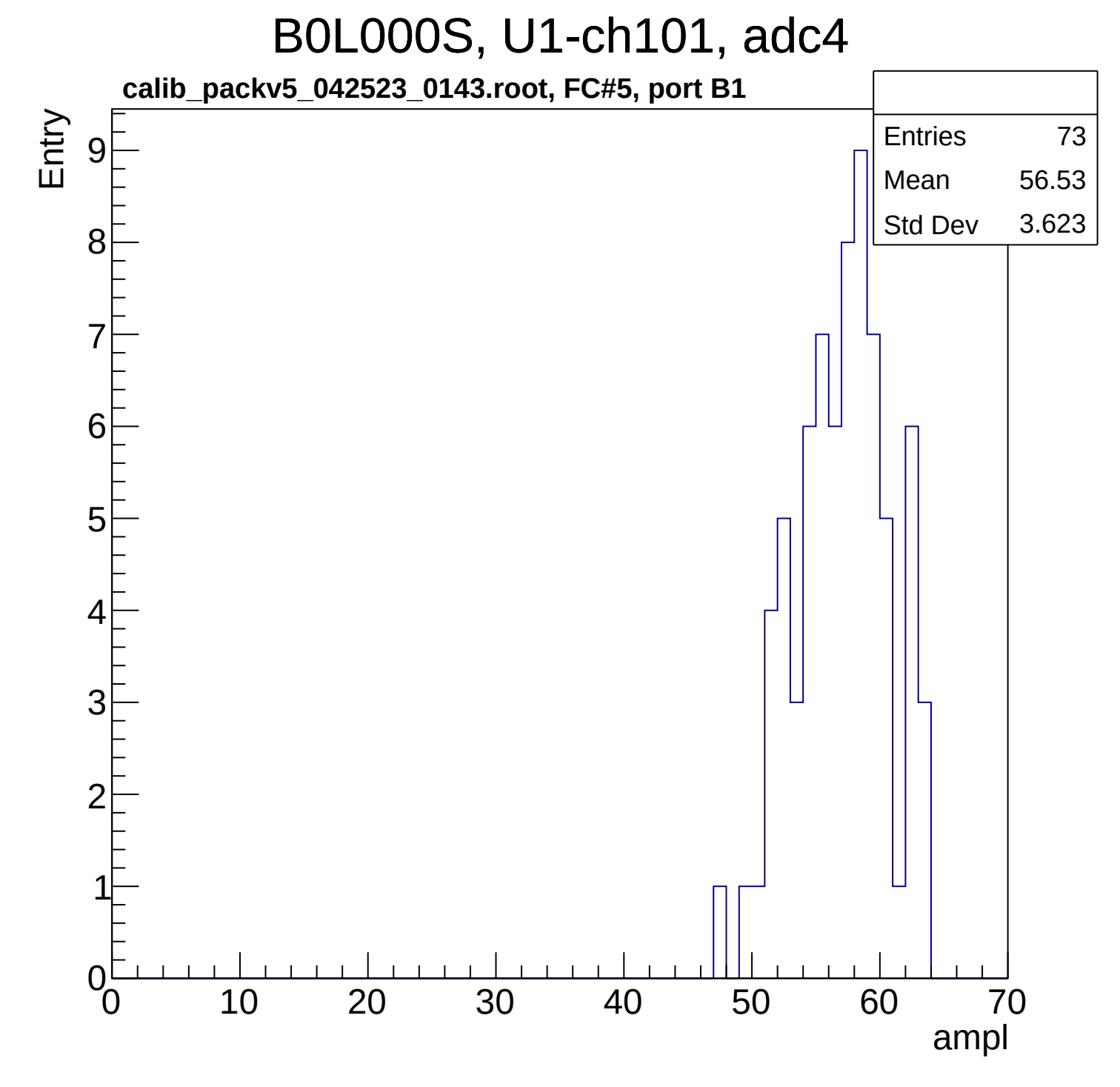

B0L000S, U1-ch100, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

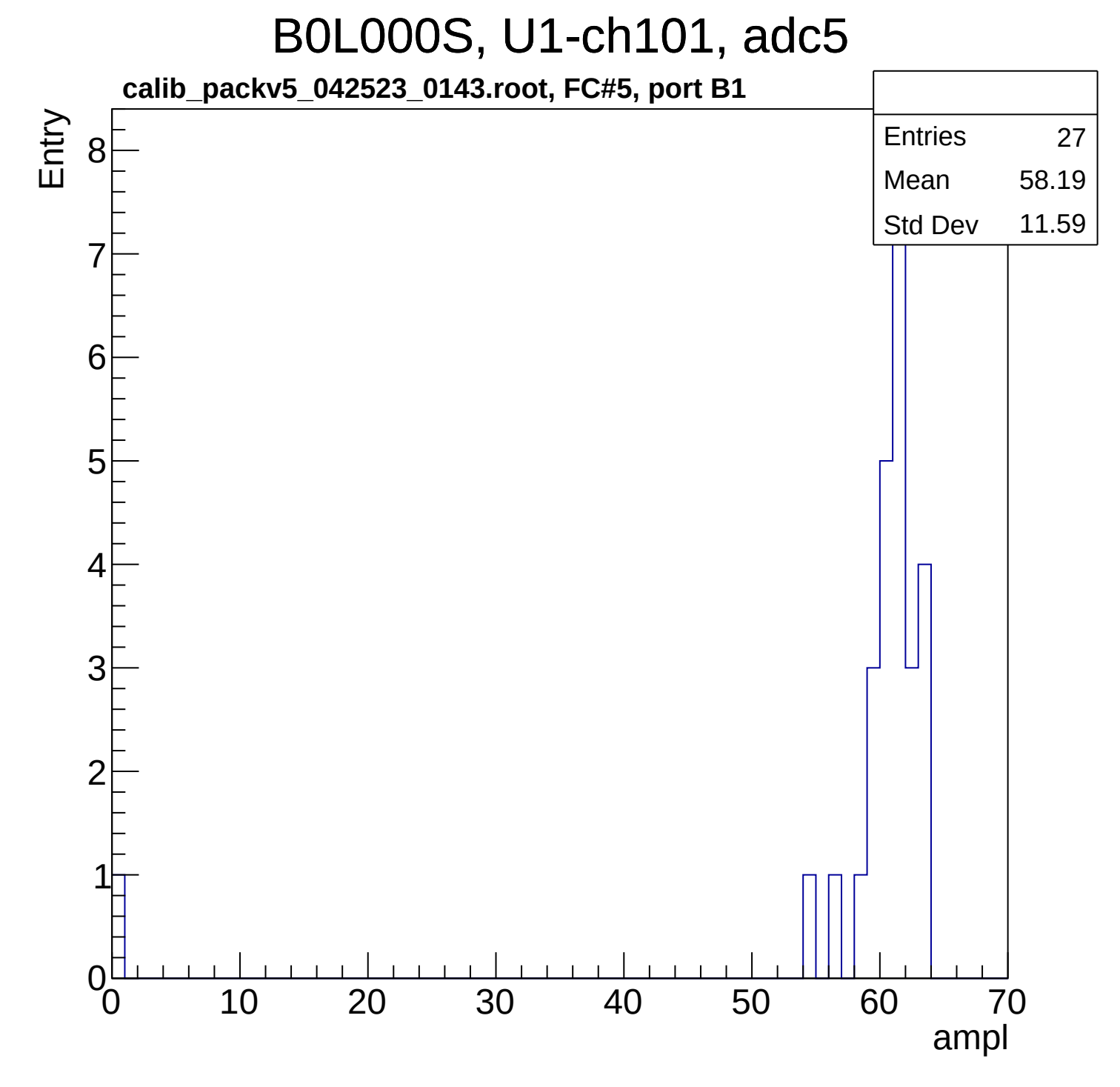

B0L000S, U1-ch101, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 30.97
Std Dev 3.718
Gaus mean : 31.5123
Gaus Width: 3.8955
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

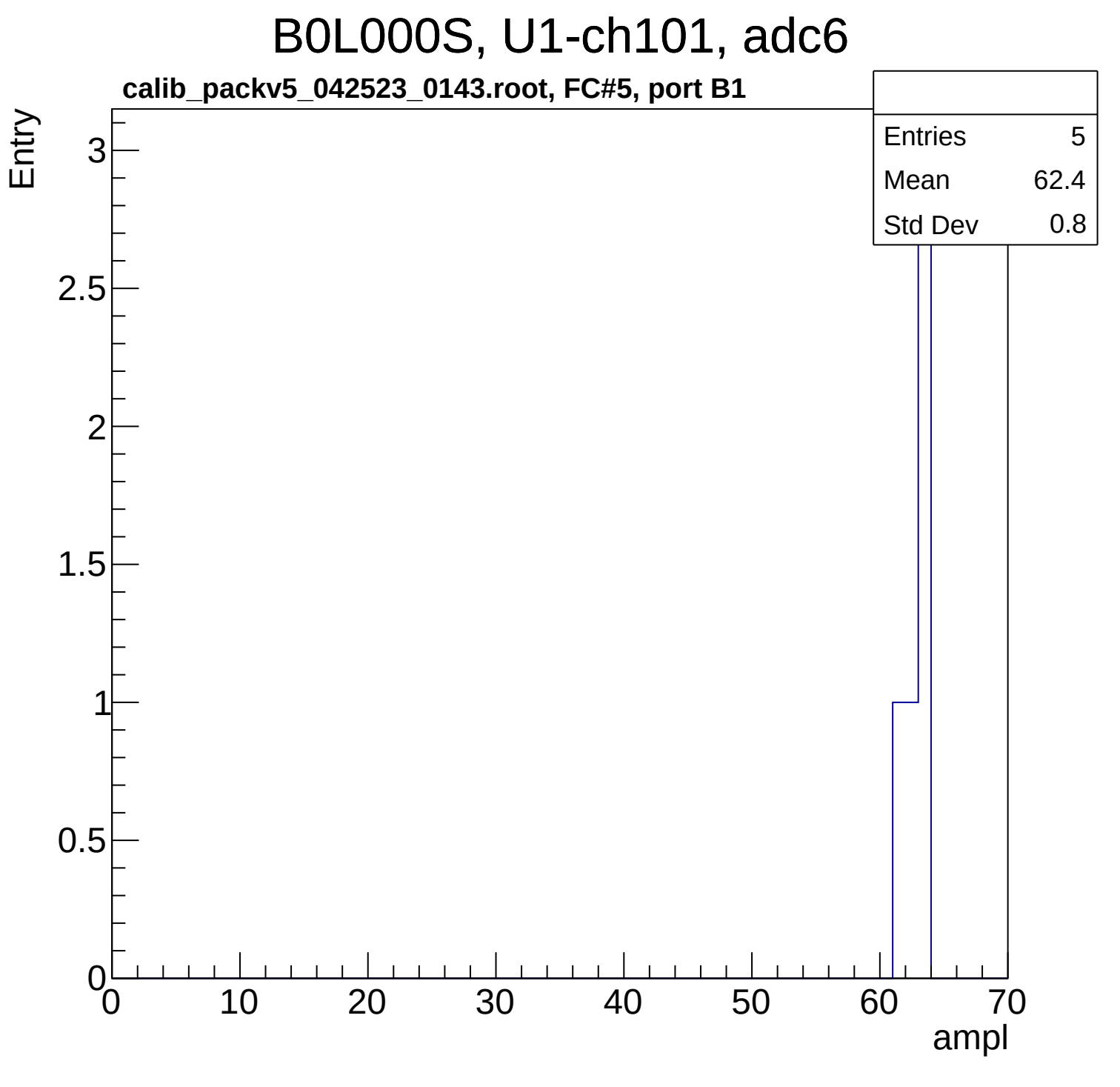

B0L000S, U1-ch101, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 37.98
Std Dev 3.067
Gaus mean : 38.5750
Gaus Width: 3.3043
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

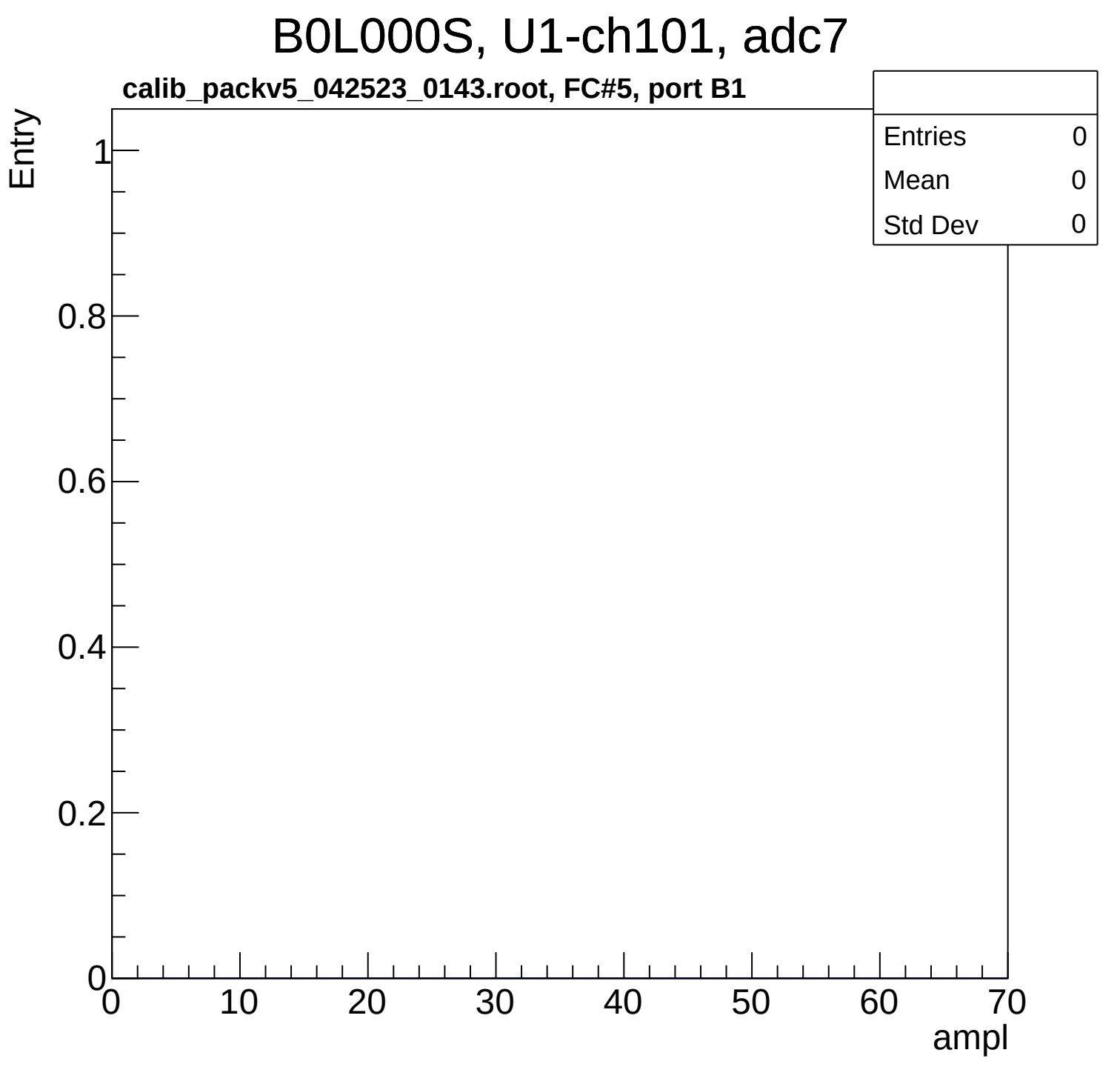

B0L000S, U1-ch101, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 44.9
Std Dev 3.226
Gaus mean : 45.2650
Gaus Width: 3.1746
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

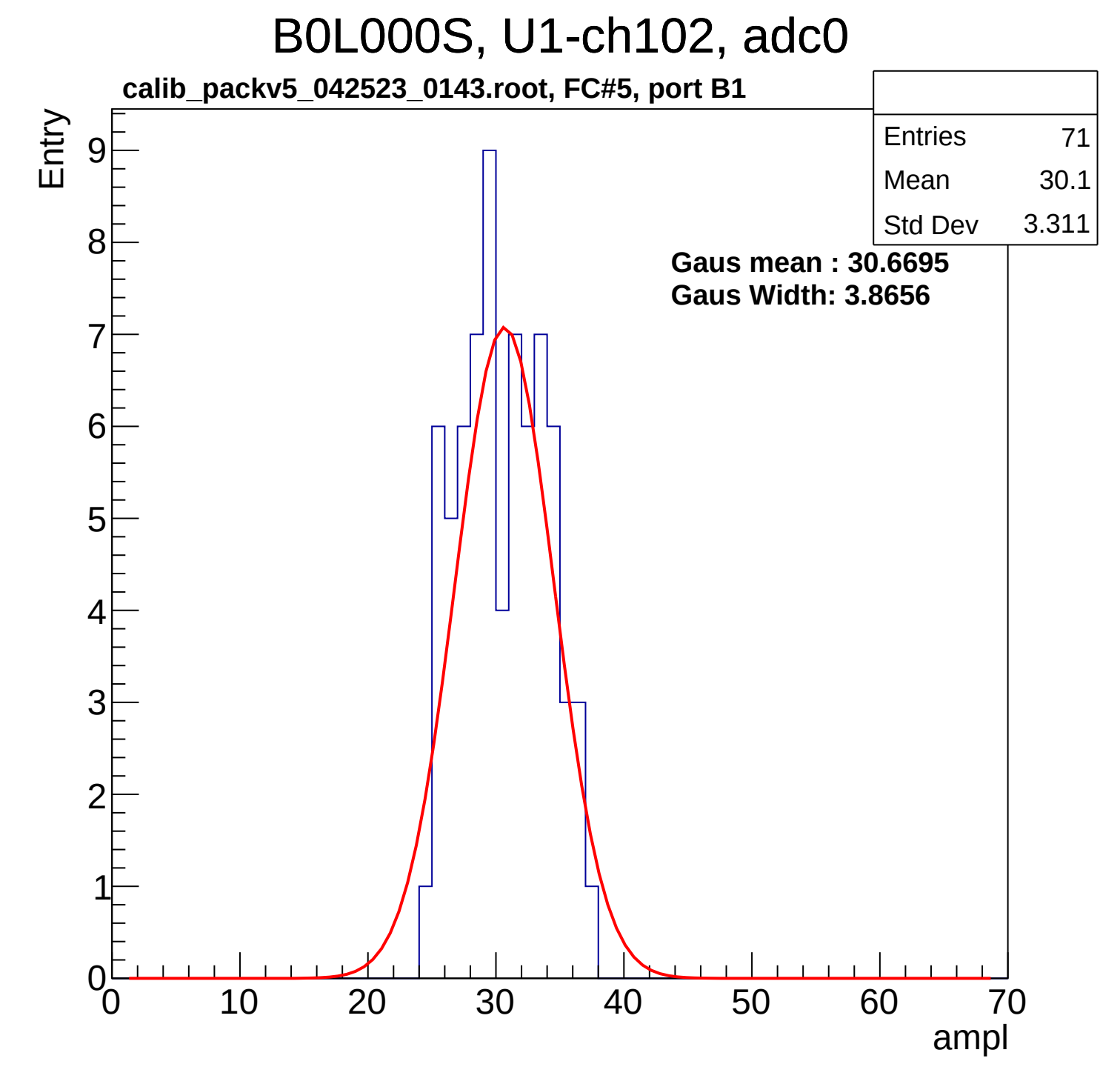

B0L000S, U1-ch101, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 50.98
Std Dev 3.113
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

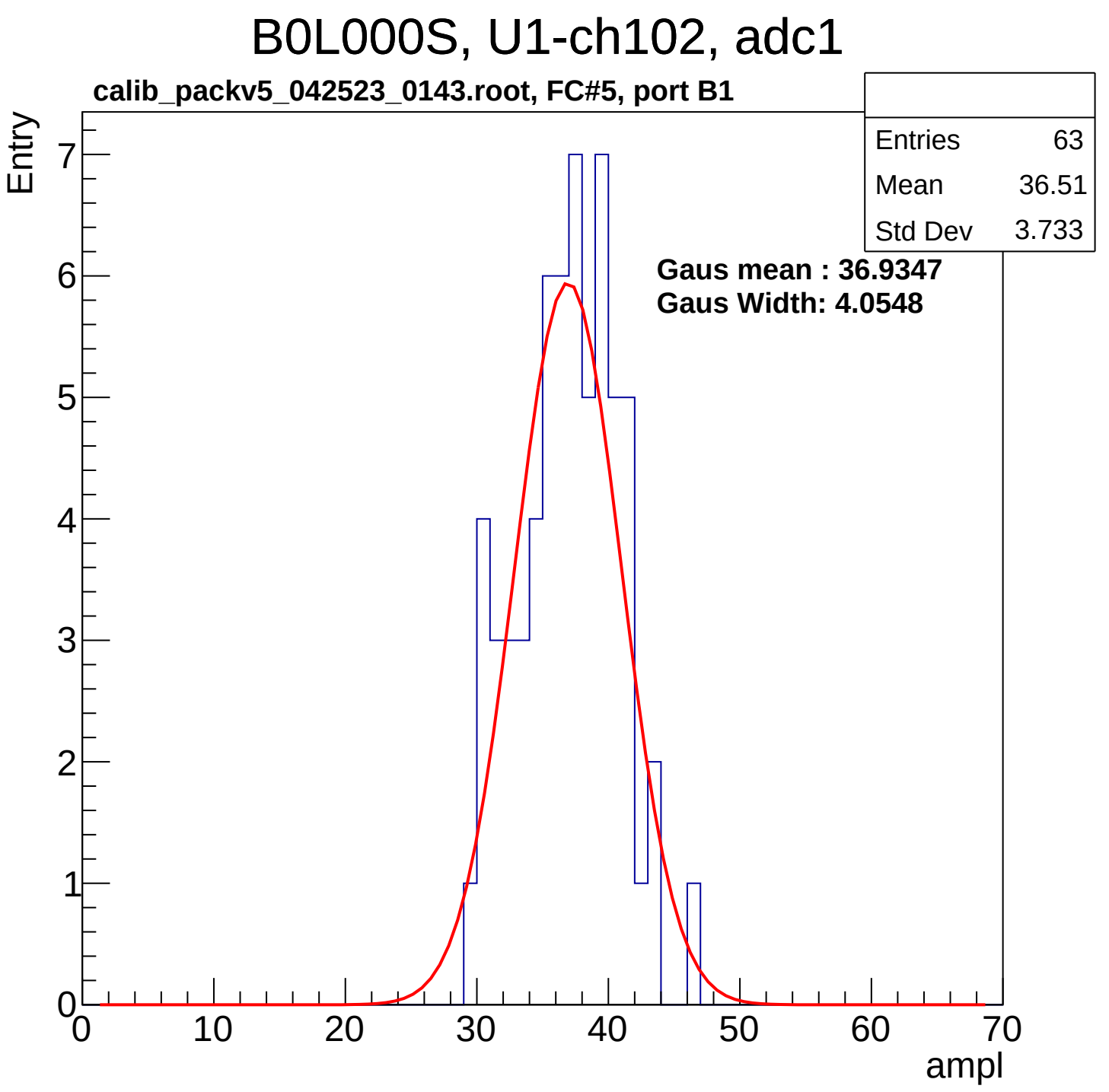

B0L000S, U1-ch101, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 56.53
Std Dev 3.623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

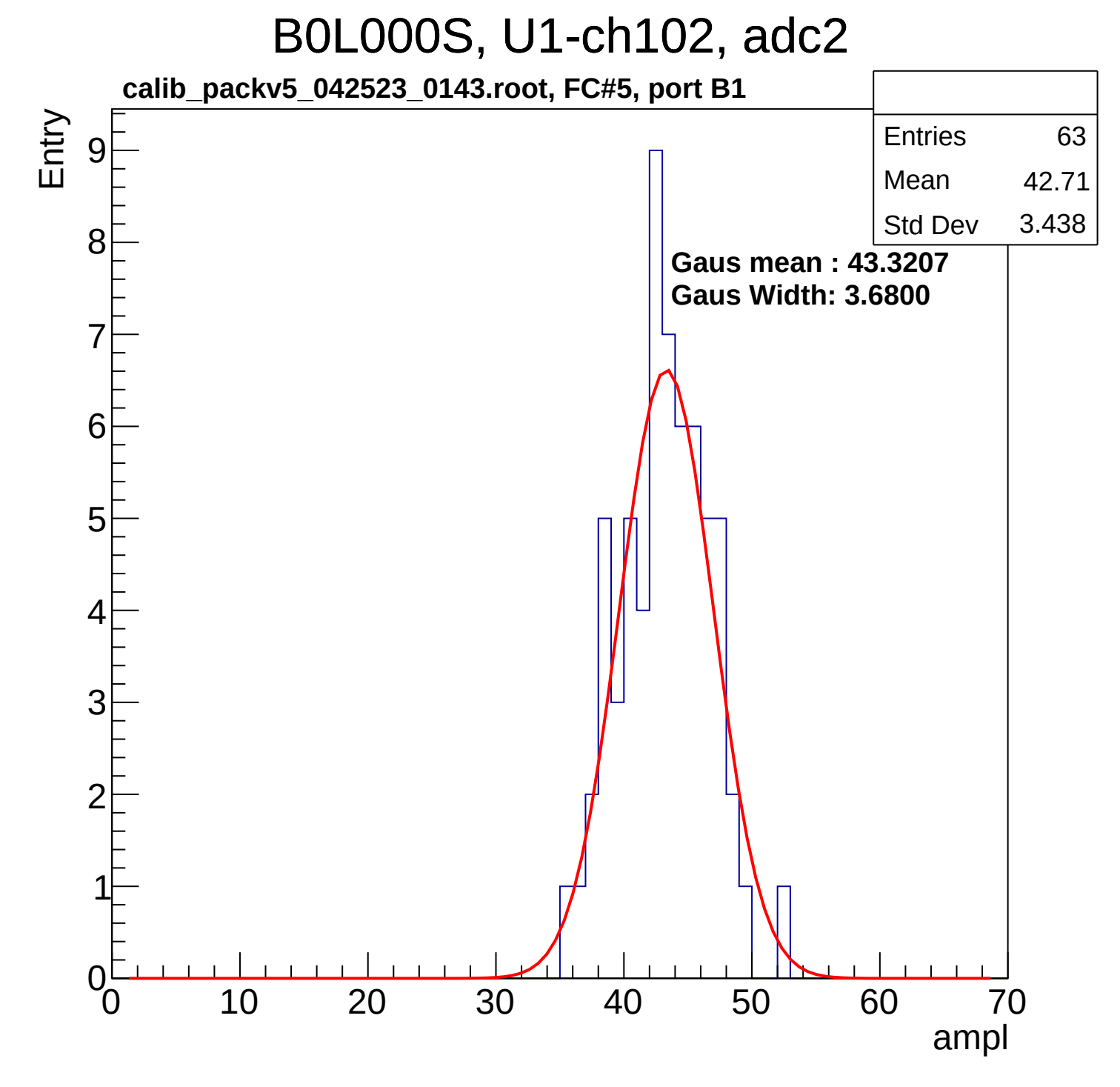

B0L000S, U1-ch101, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.19
Std Dev 11.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

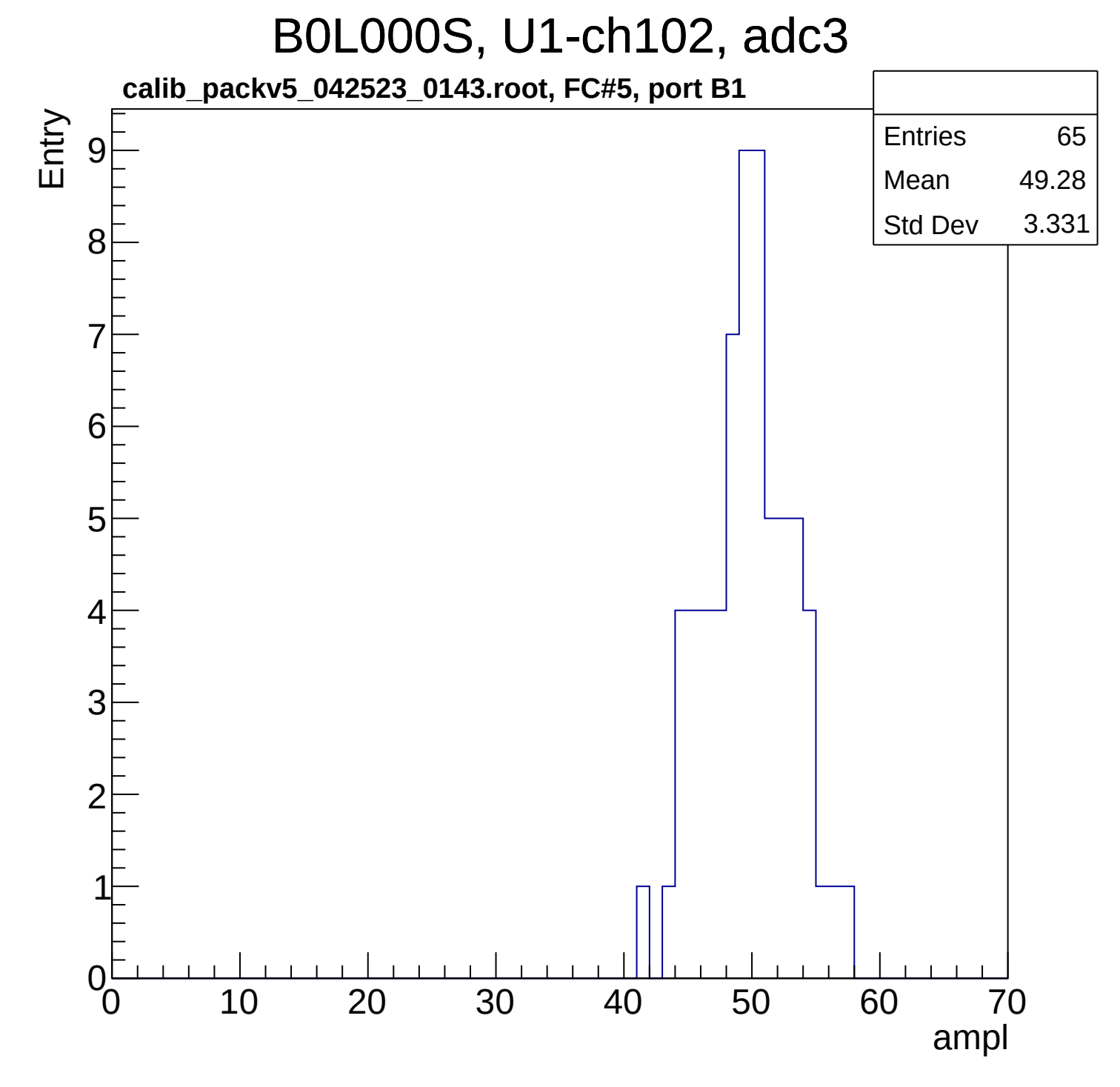

B0L000S, U1-ch101, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

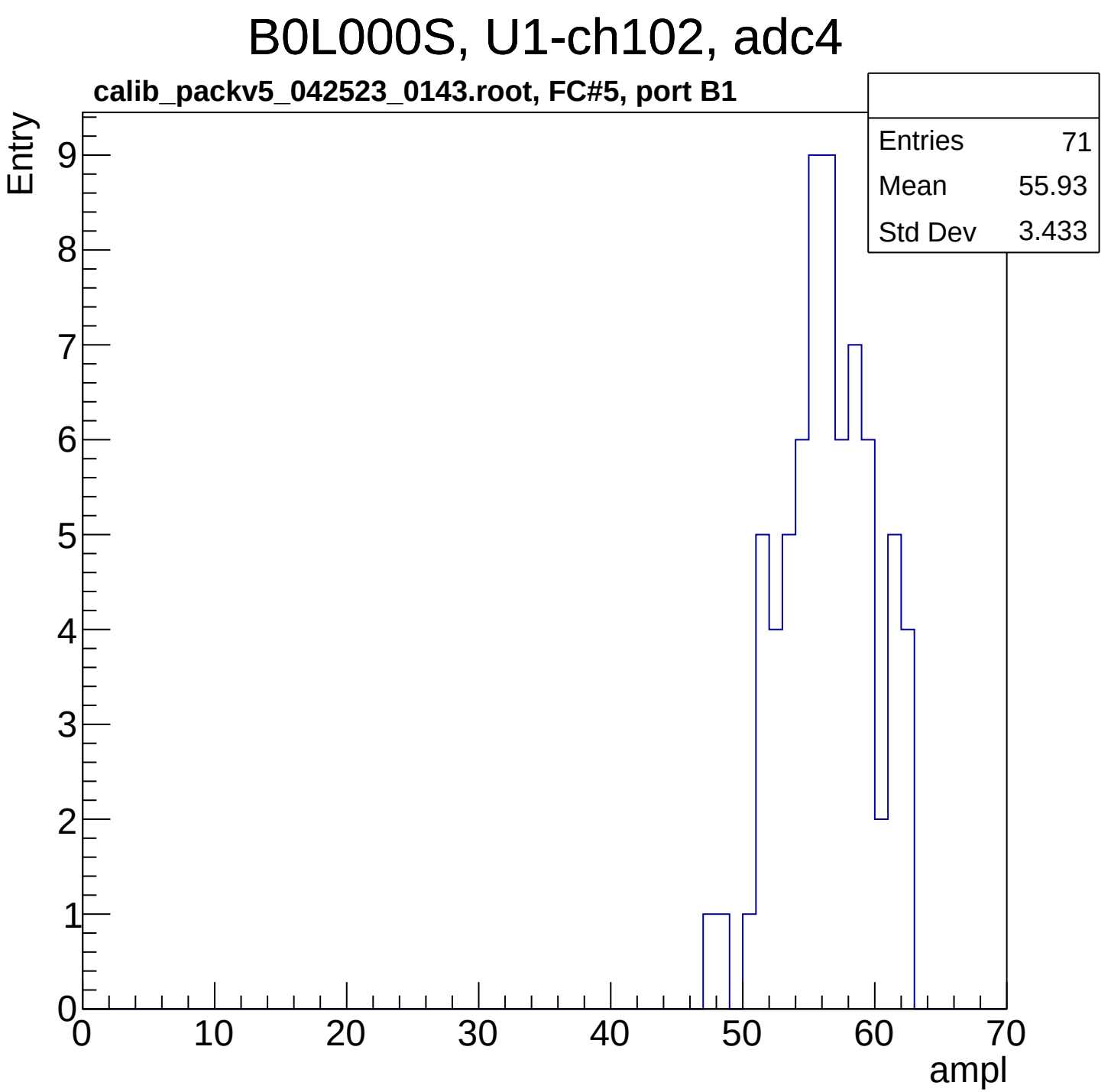

B0L000S, U1-ch101, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

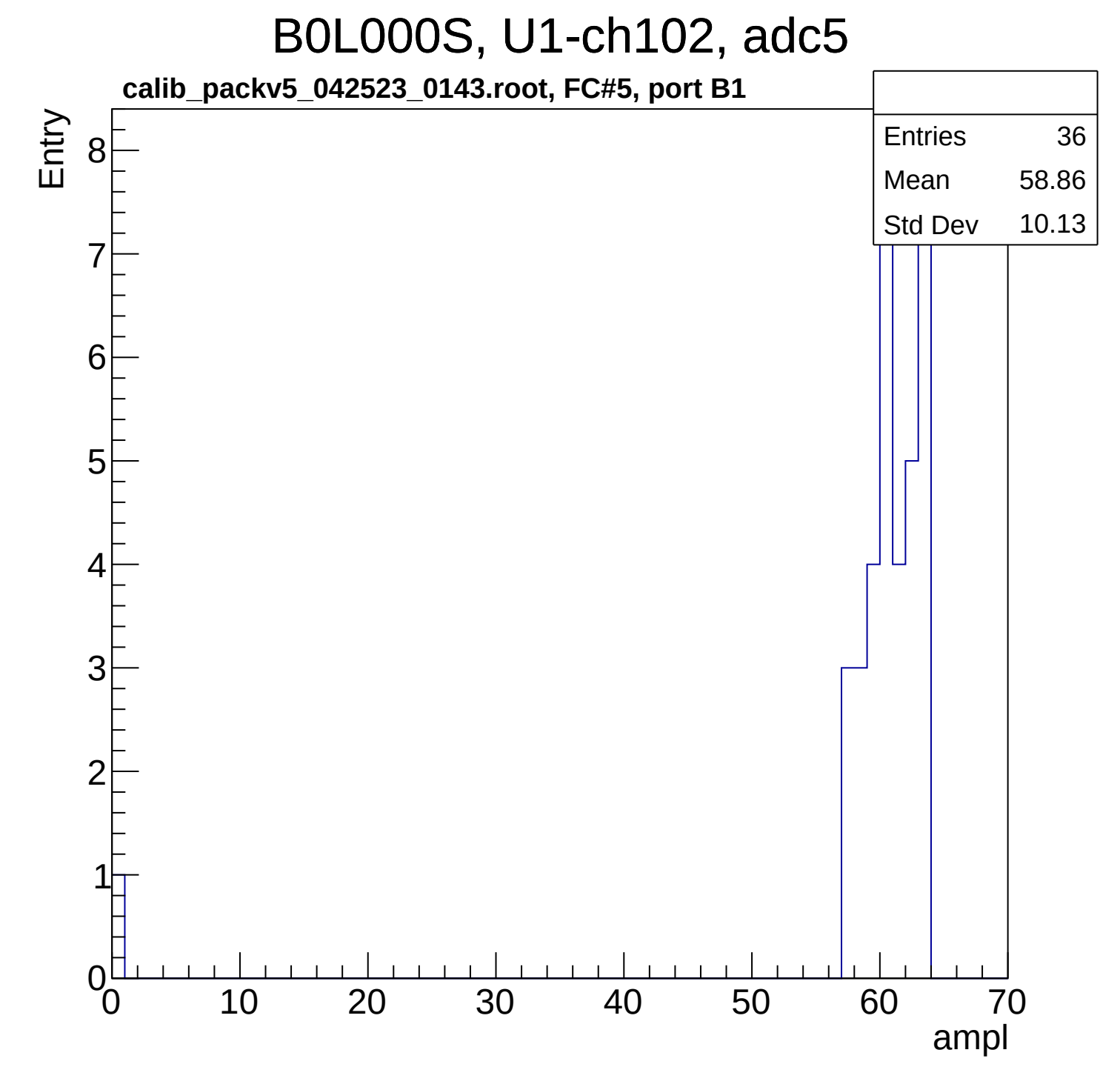

B0L000S, U1-ch102, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.1
Std Dev 3.311
Gaus mean : 30.6695
Gaus Width: 3.8656
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch102, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.51
Std Dev 3.733
Gaus mean : 36.9347
Gaus Width: 4.0548
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch102, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.71
Std Dev 3.438
Gaus mean : 43.3207
Gaus Width: 3.6800
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch102, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.28
Std Dev 3.331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch102, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 55.93
Std Dev 3.433
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch102, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.86
Std Dev 10.13
Entry
ampl
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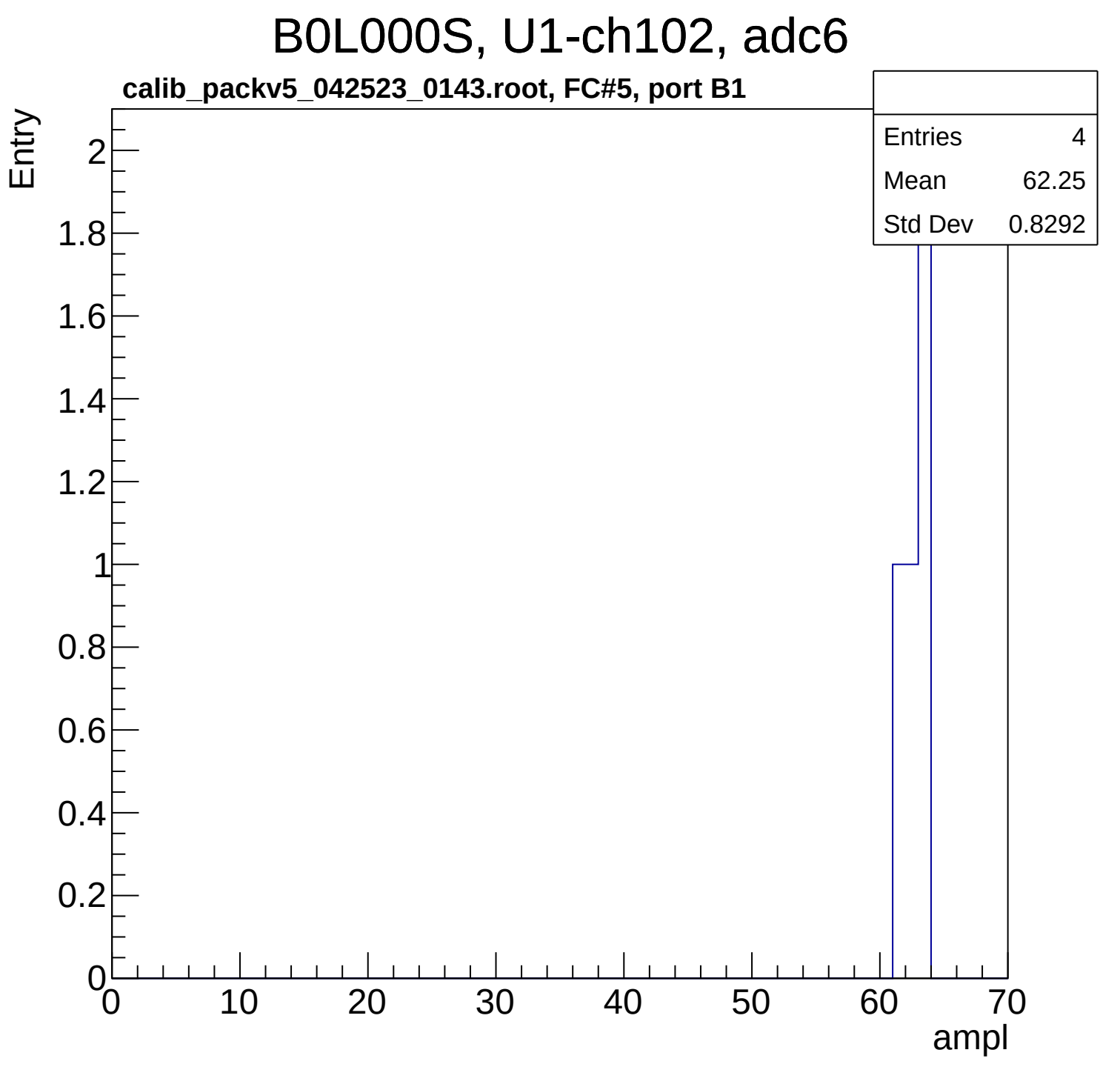
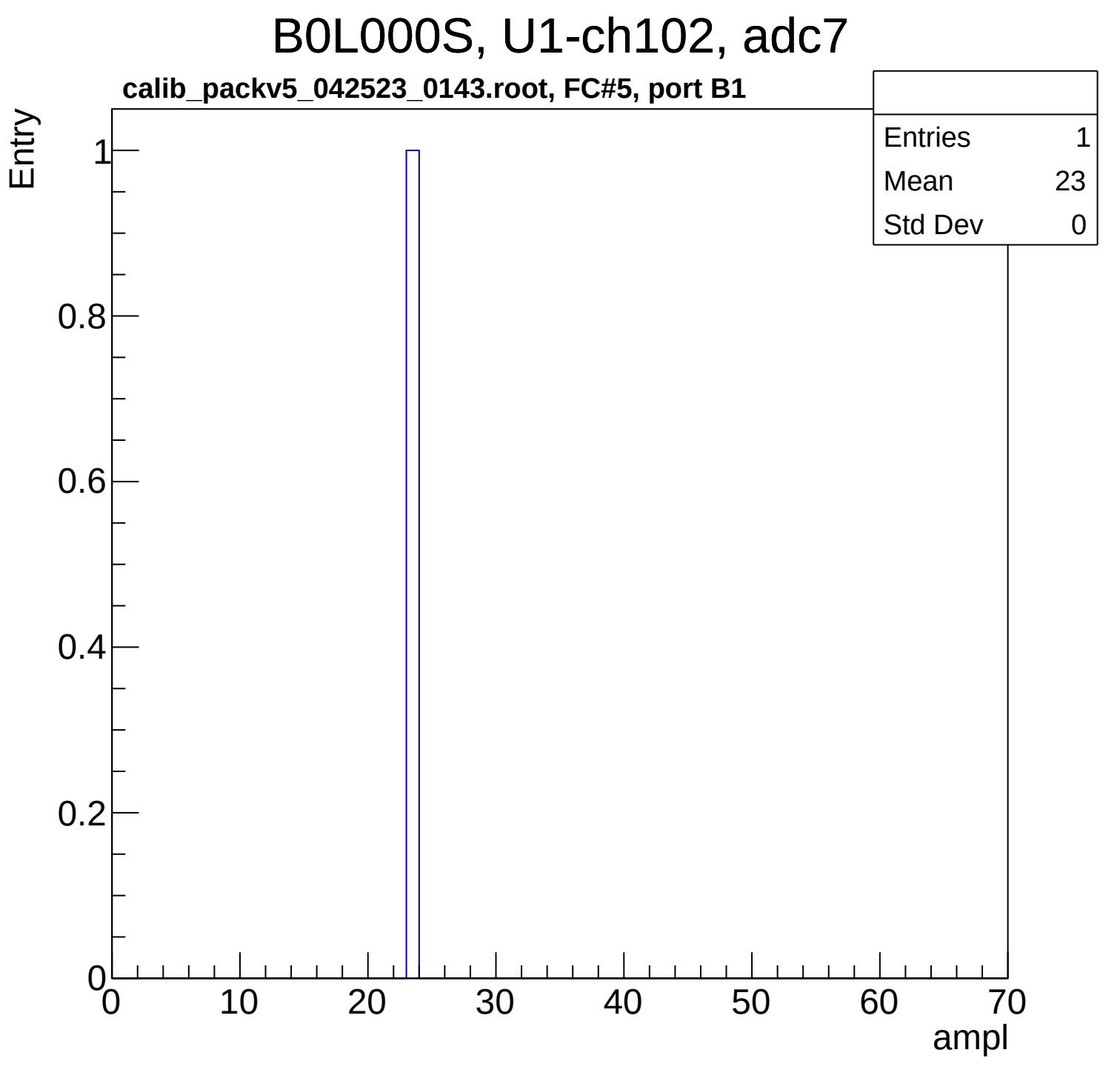
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch102, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch102, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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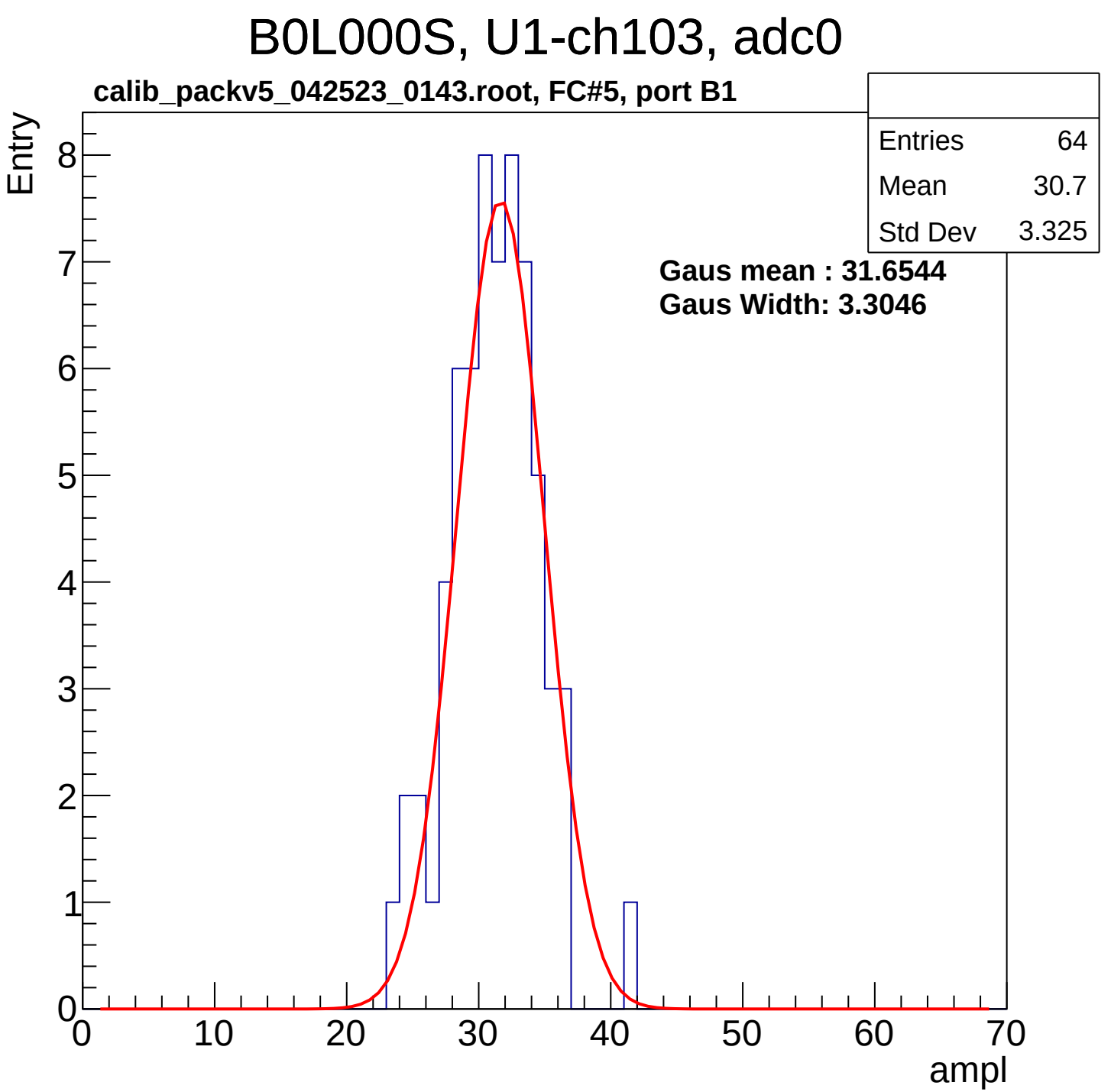
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch103, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 30.7
Std Dev 3.325
Gaus mean : 31.6544
Gaus Width: 3.3046
Entry
ampl
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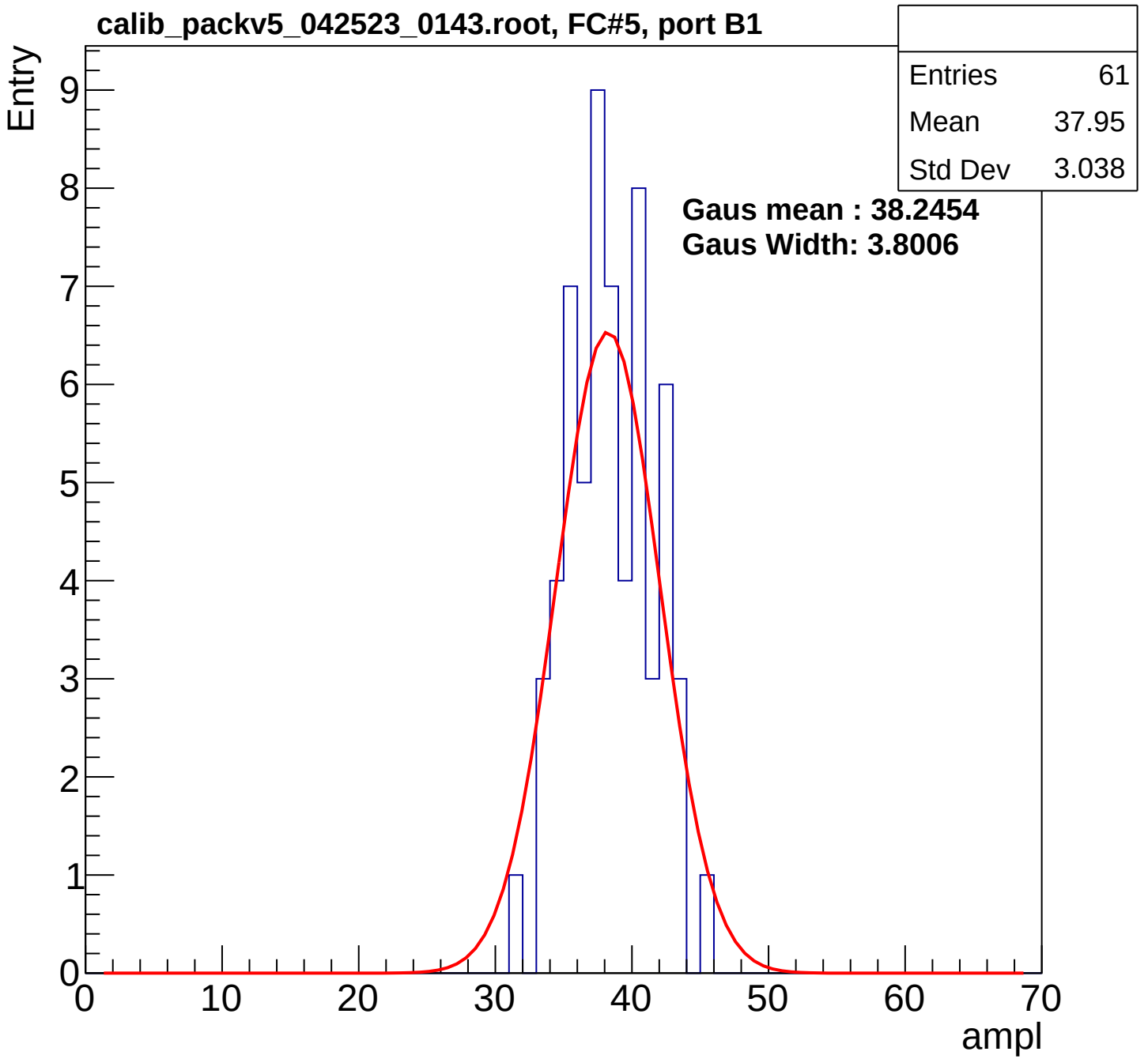
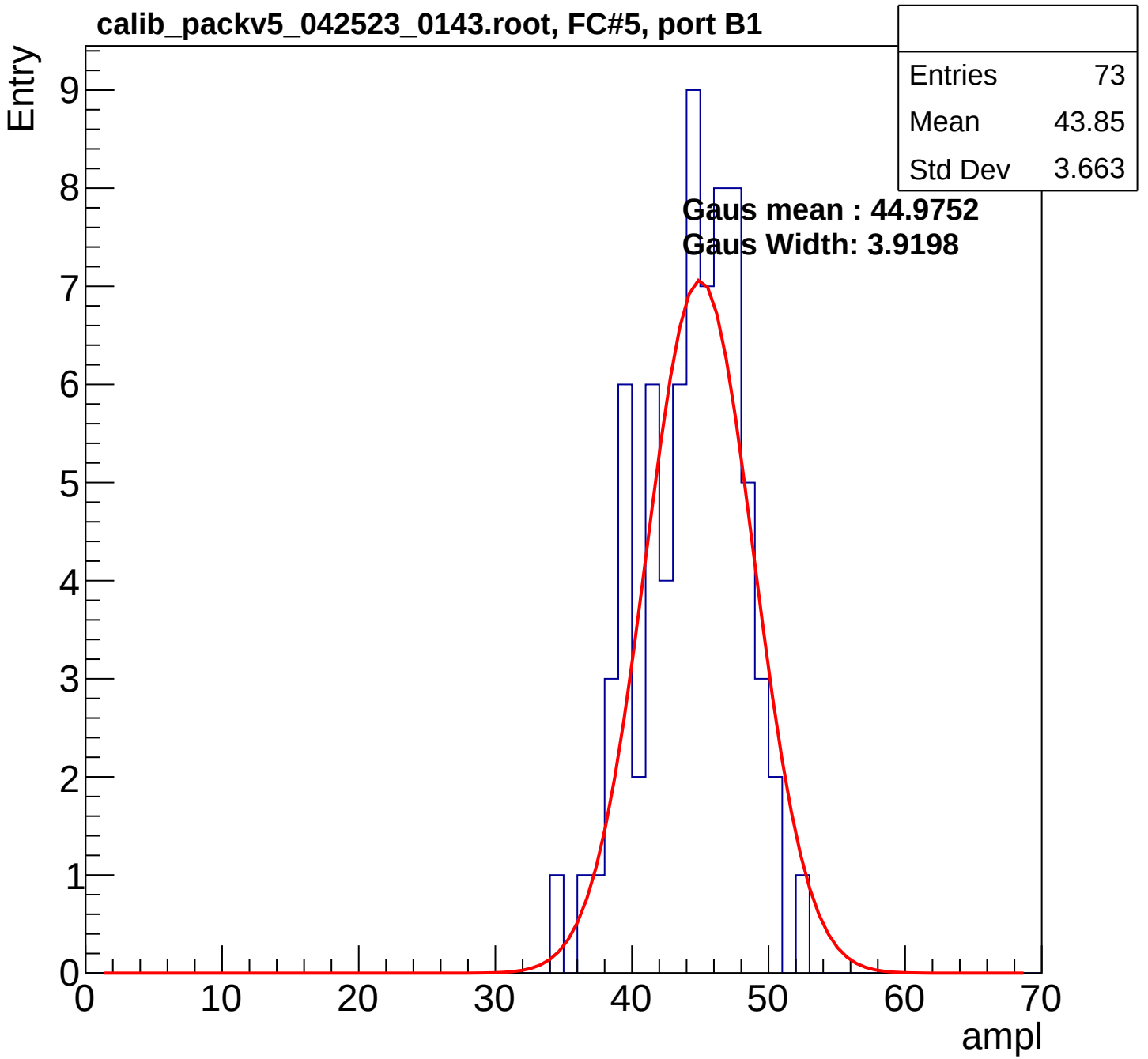
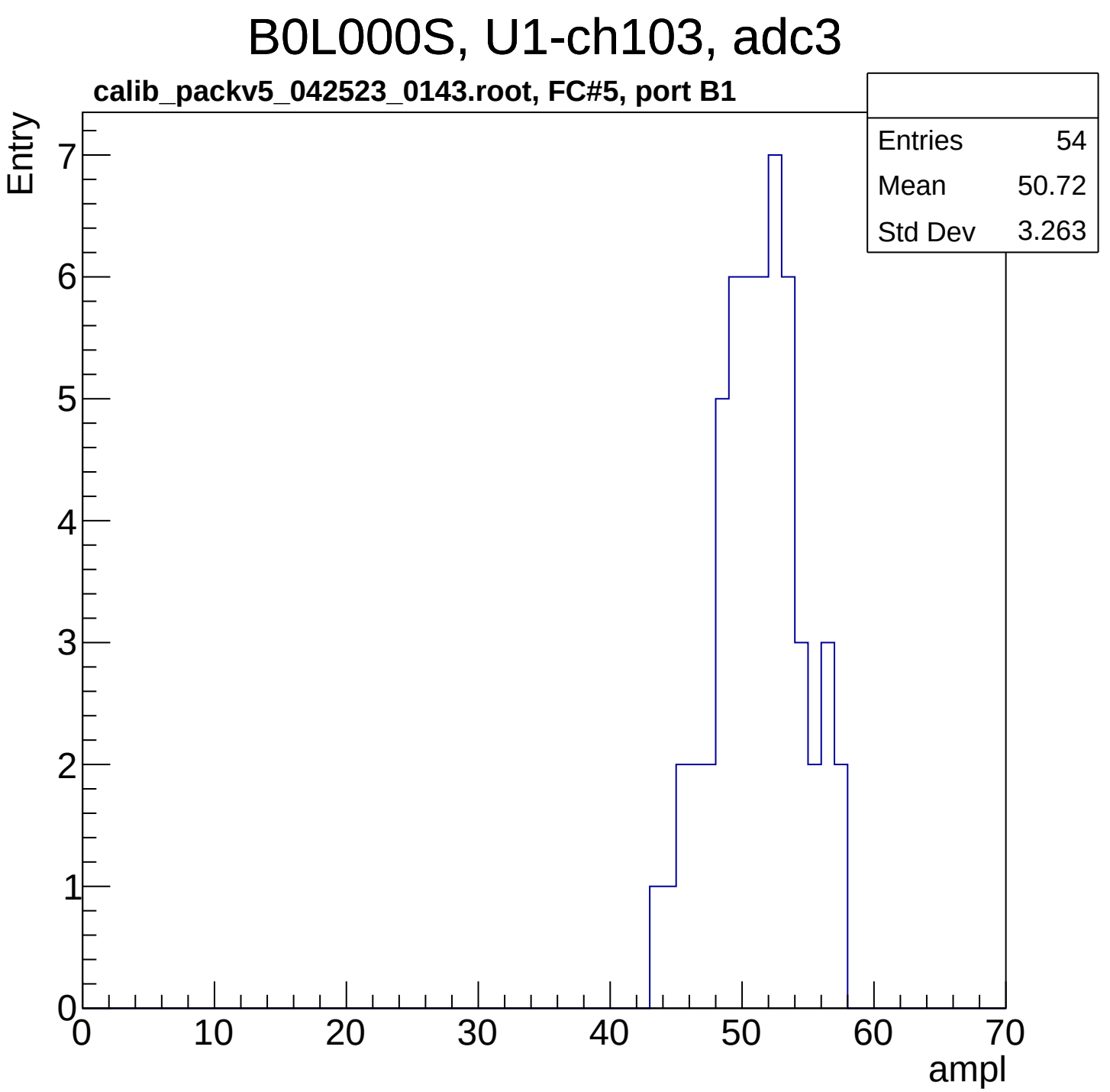
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch103, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 37.95
Std Dev 3.038
Gaus mean : 38.2454
Gaus Width: 3.8006
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch103, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.85
Std Dev 3.663
Gaus mean : 44.9752
Gaus Width: 3.9198
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch103, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 50.72
Std Dev 3.263
Entry
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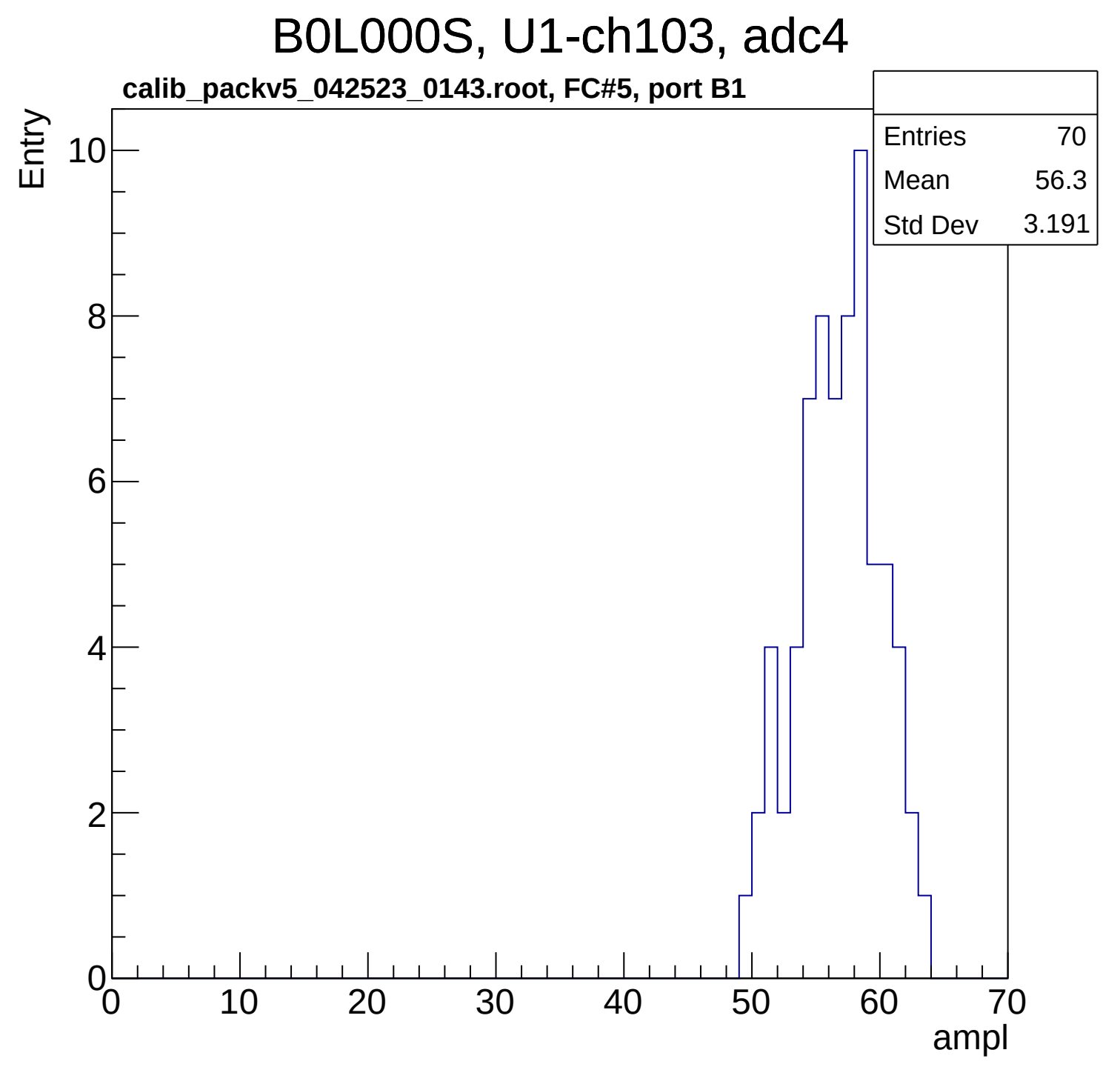
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch103, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 56.3
Std Dev 3.191
Entry
0
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4
6
8
10
0
10
20
30
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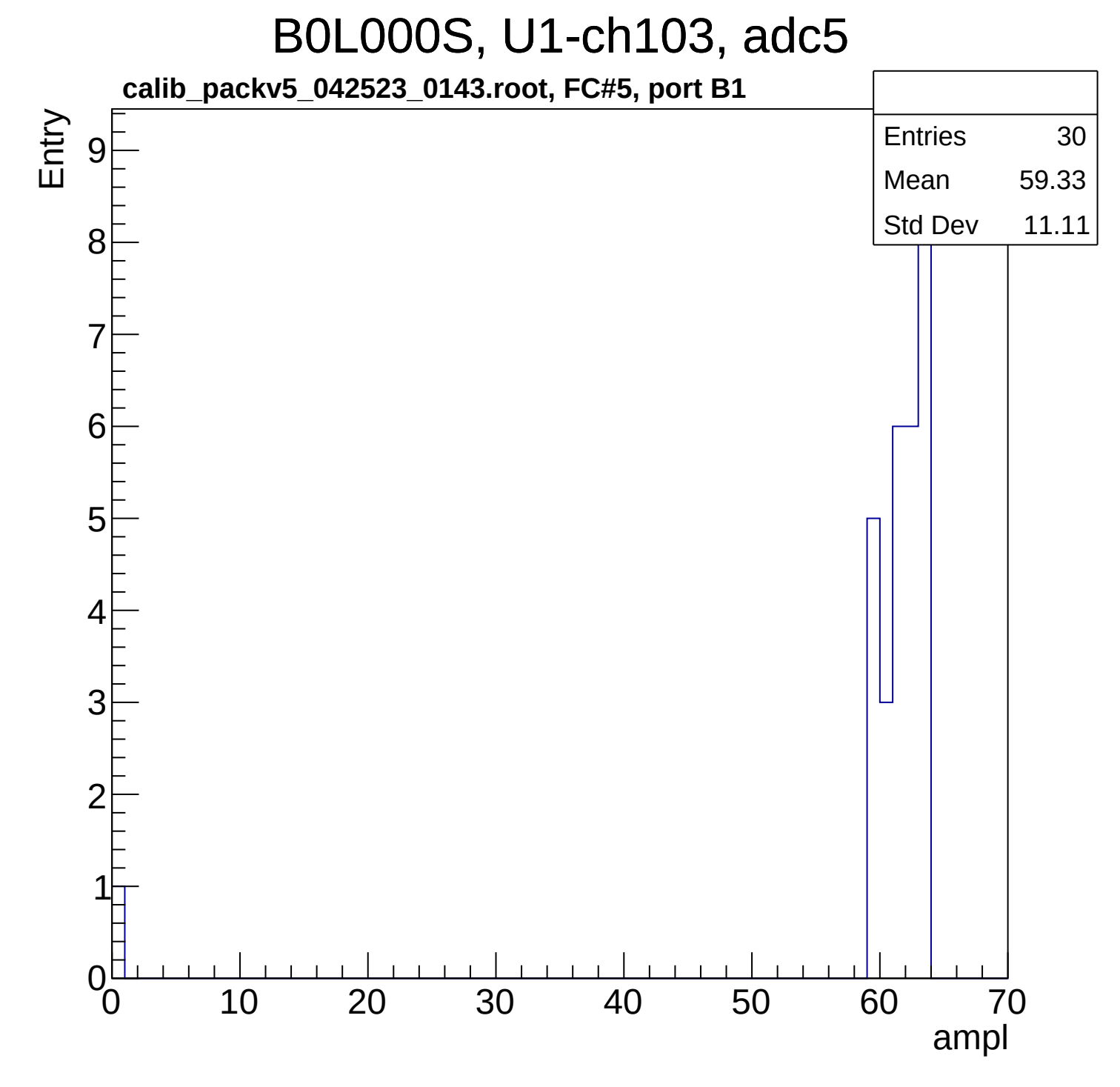
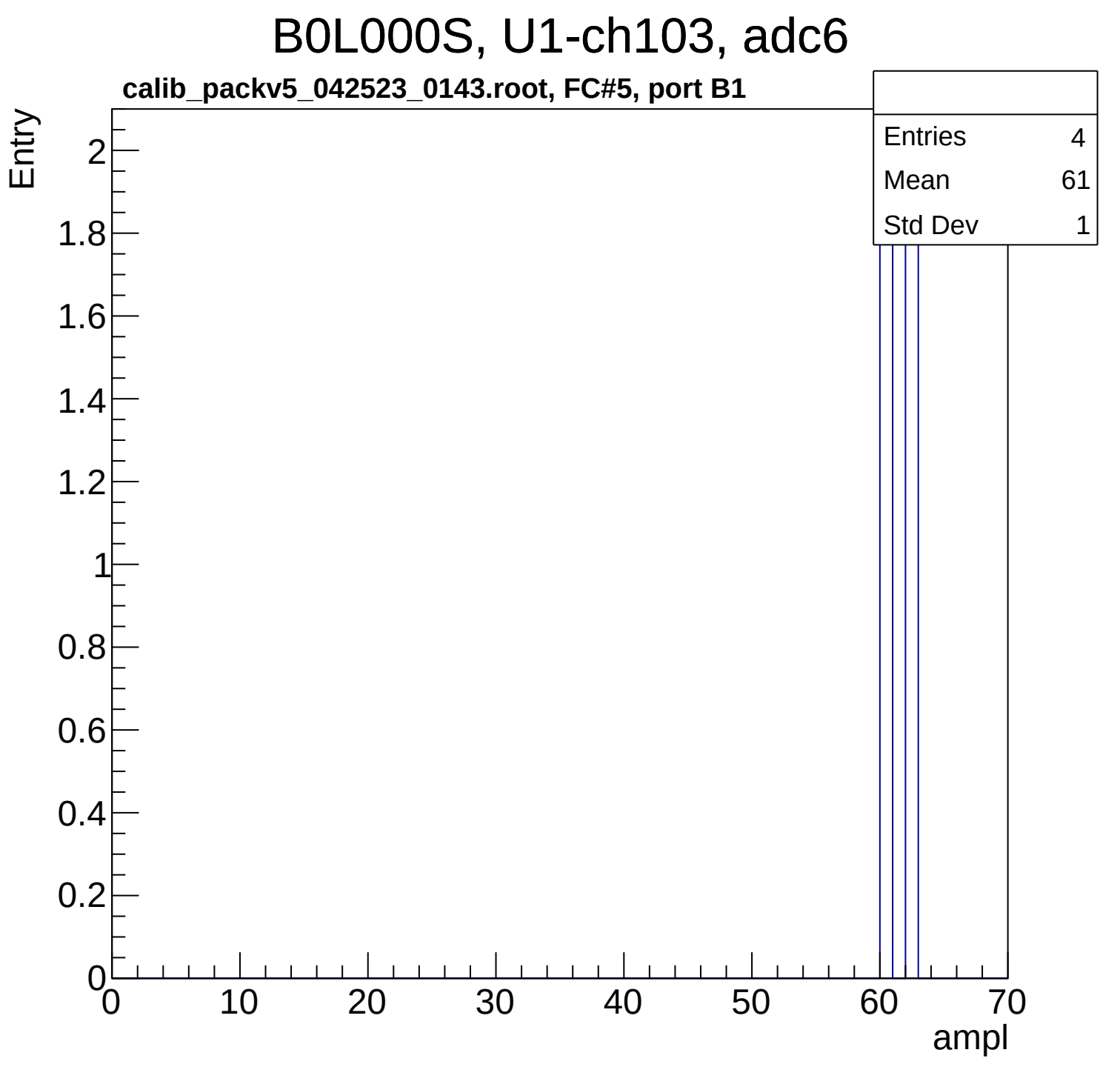
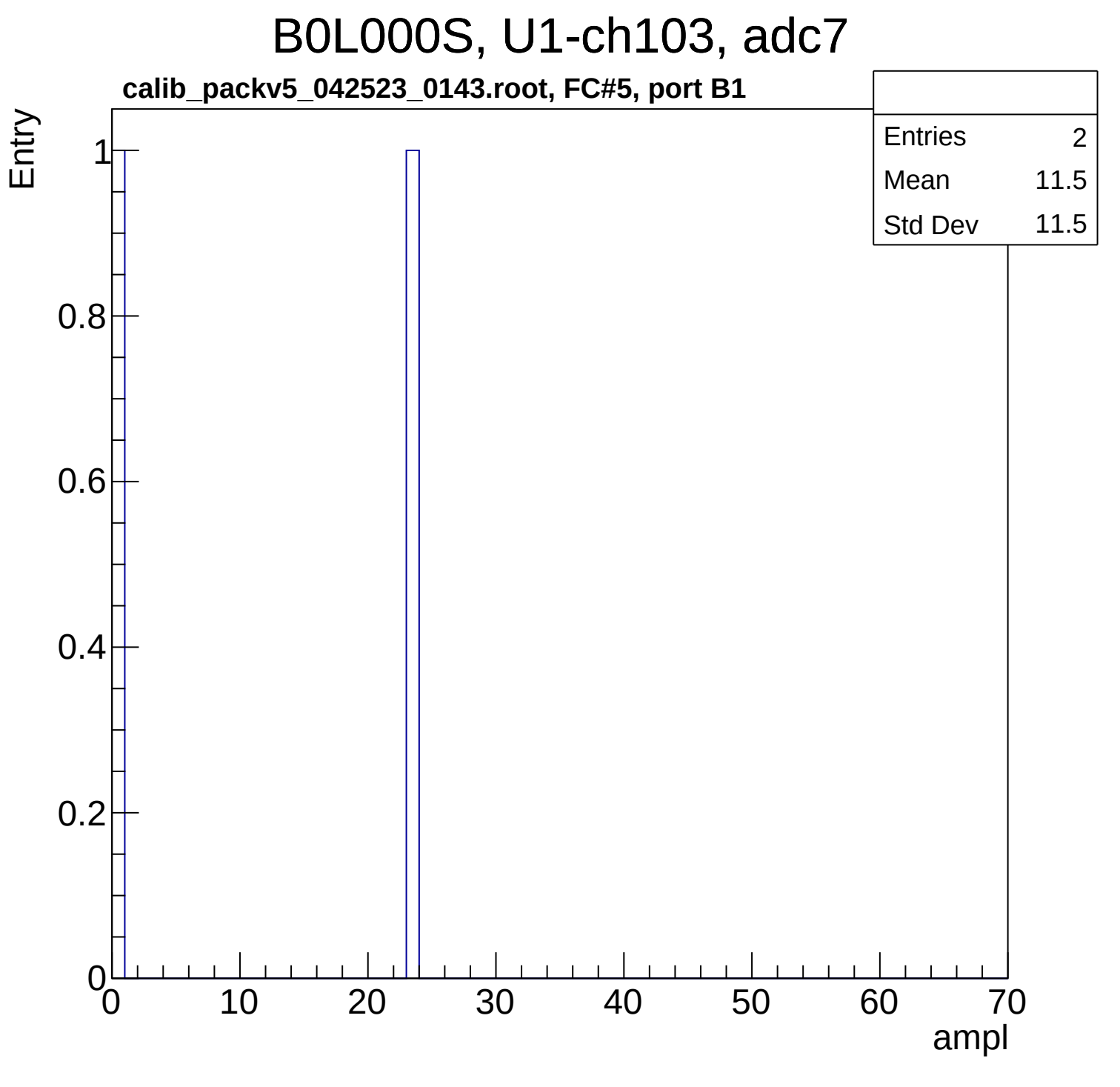
50
60
70
ampl


B0L000S, U1-ch103, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 59.33
Std Dev 11.11
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch103, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch103, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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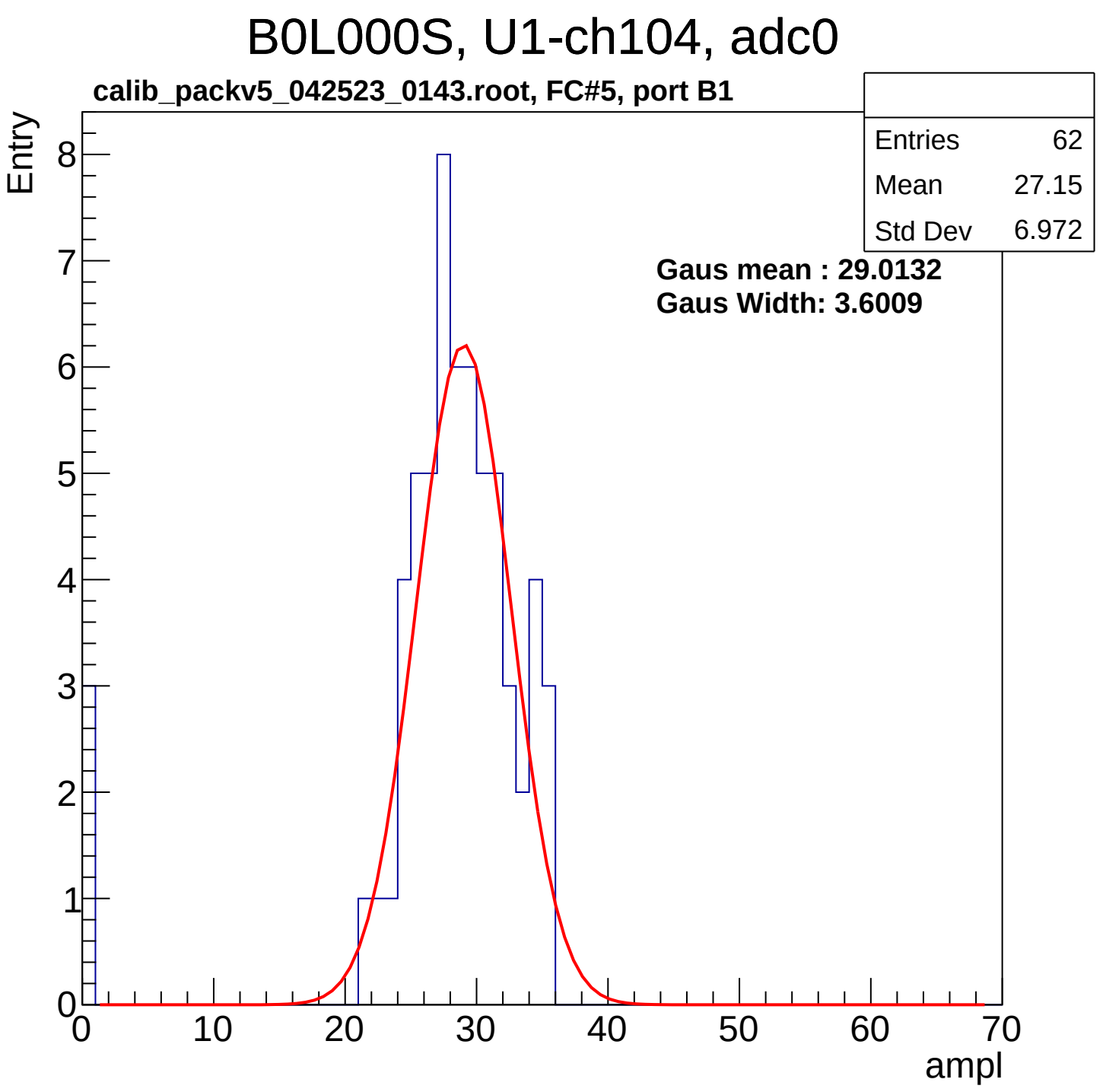
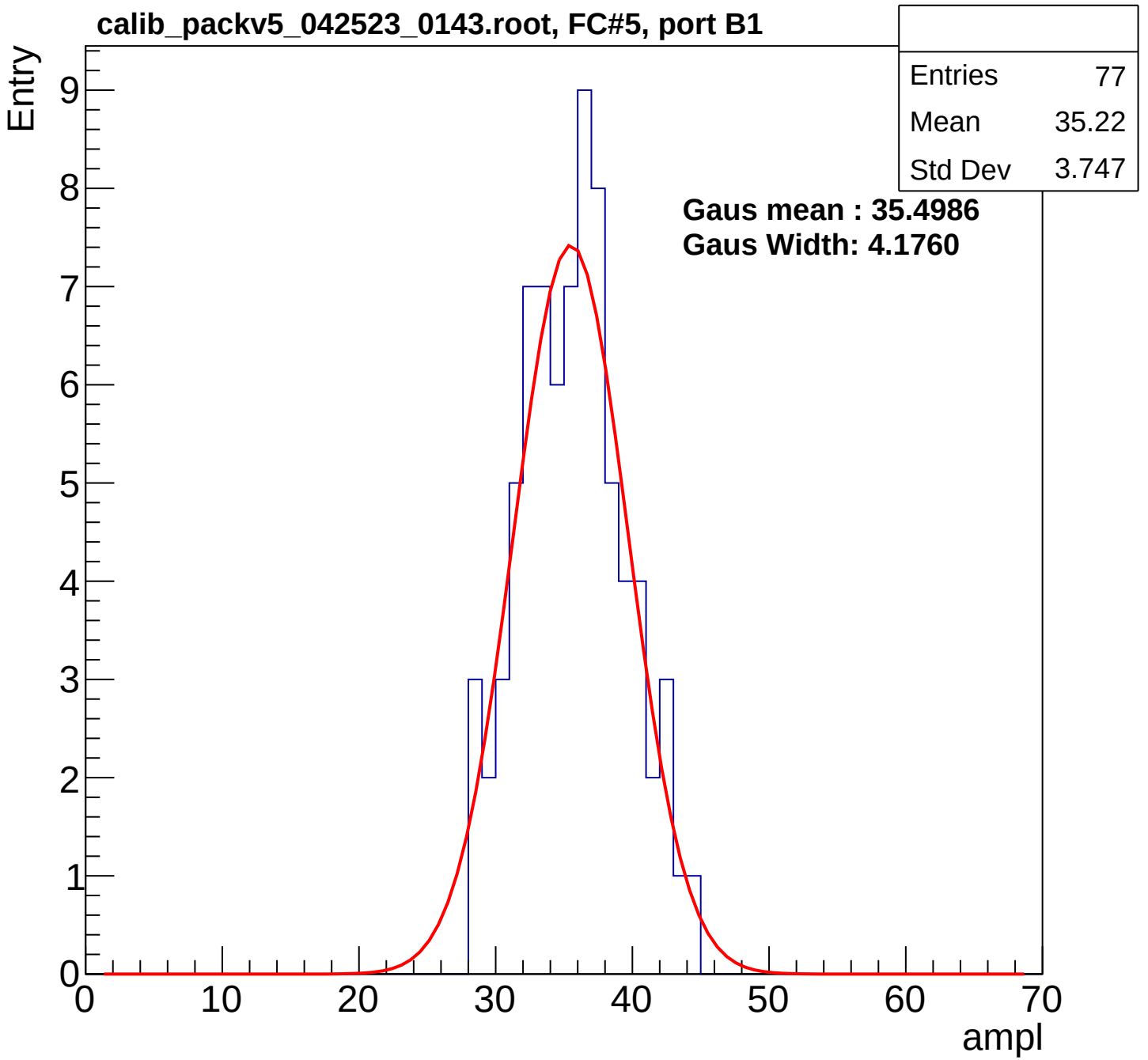
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch104, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 27.15
Std Dev 6.972
Gaus mean : 29.0132
Gaus Width: 3.6009
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch104, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 35.22
Std Dev 3.747
Gaus mean : 35.4986
Gaus Width: 4.1760
Entry
ampl
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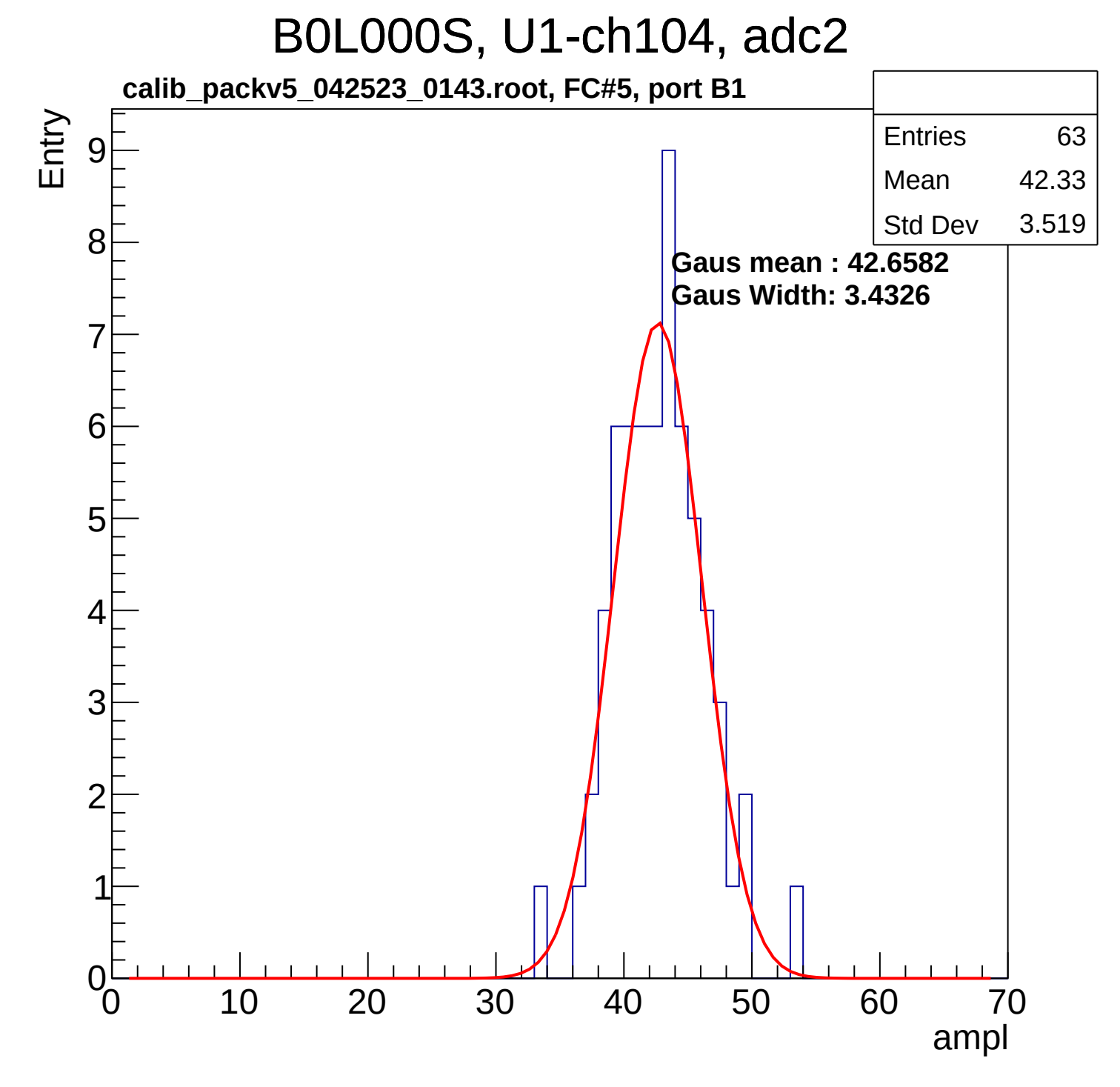
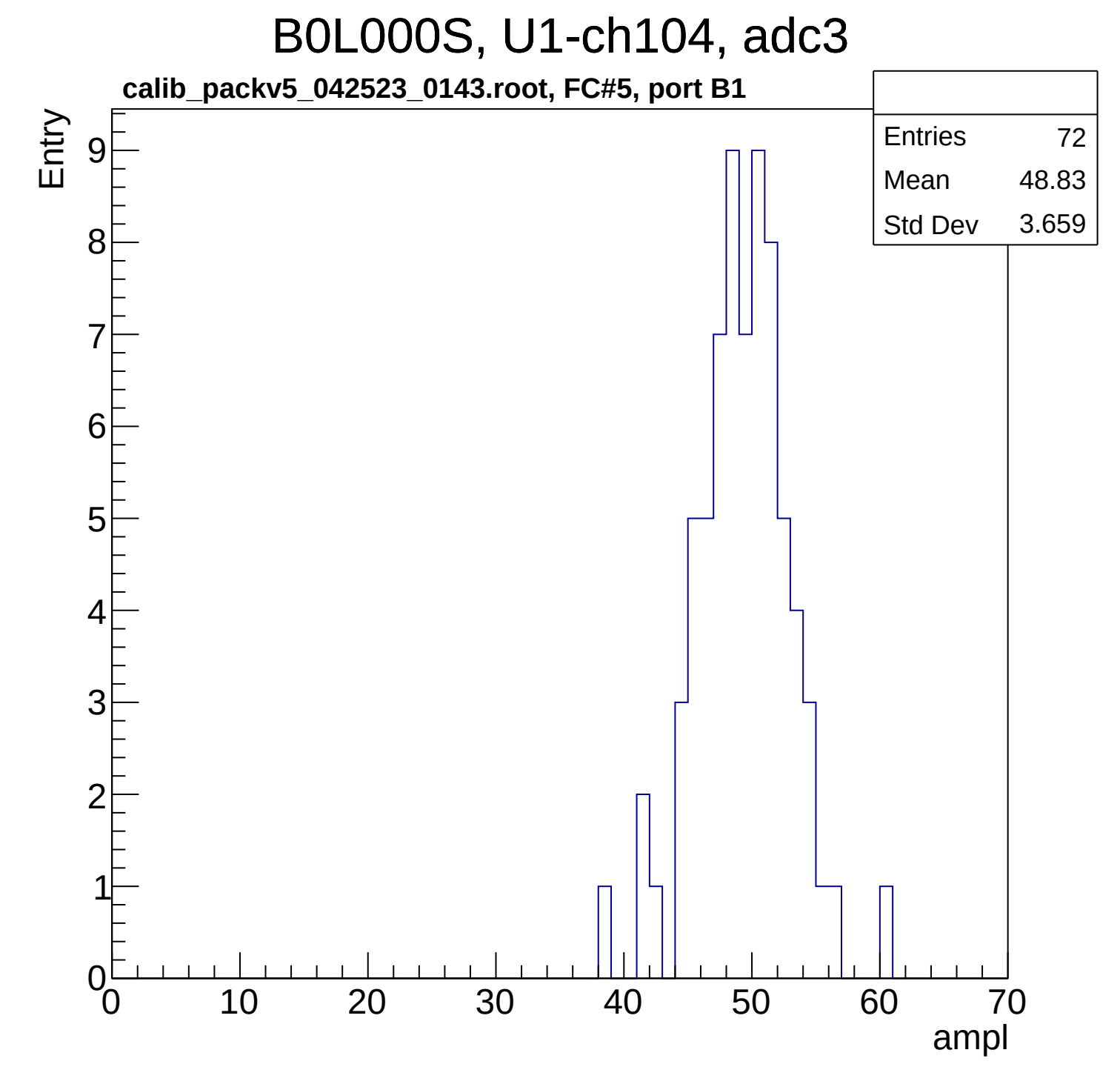
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch104, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.33
Std Dev 3.519
Gaus mean : 42.6582
Gaus Width: 3.4326
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch104, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.83
Std Dev 3.659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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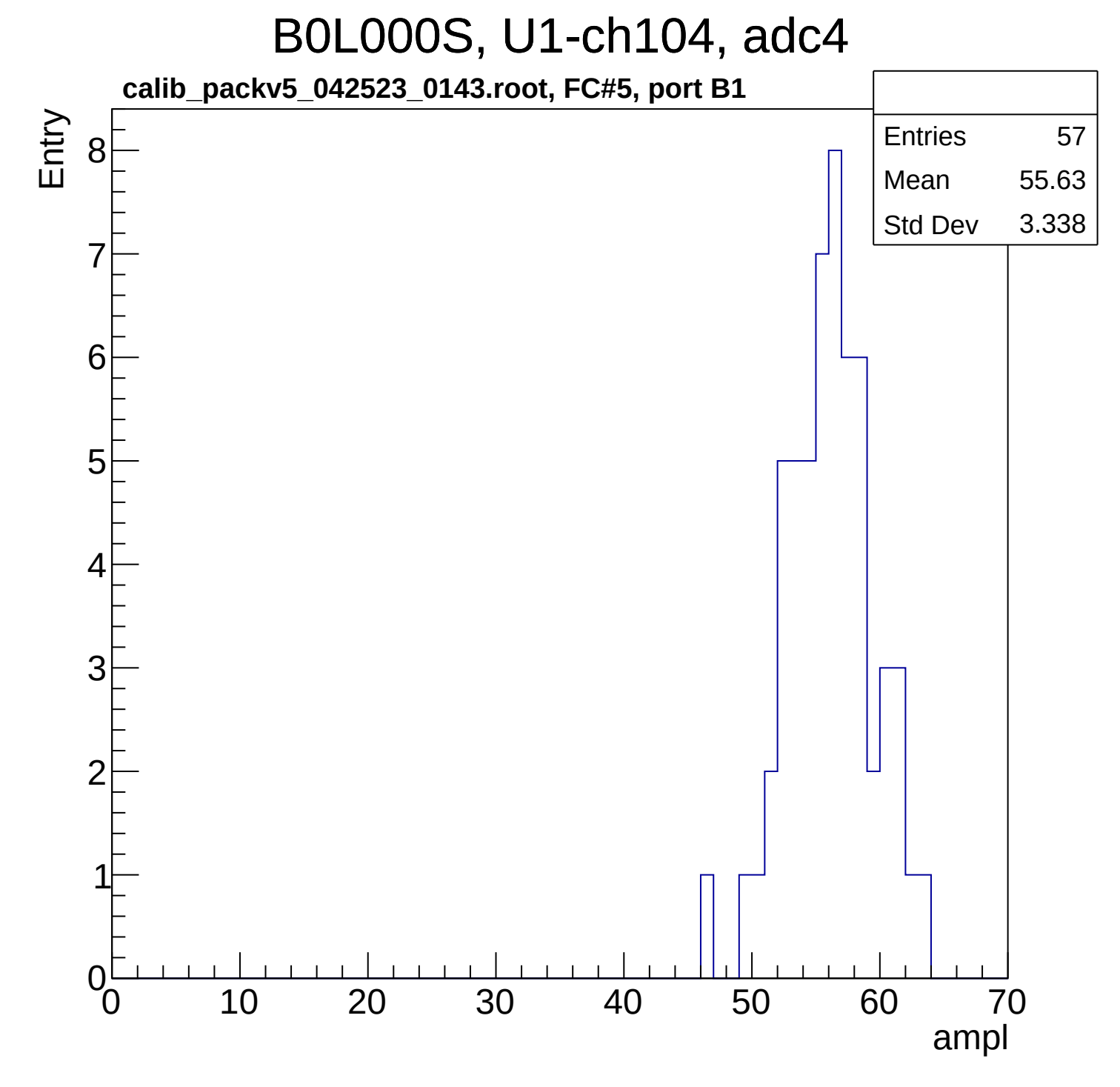
B0L000S, U1-ch104, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.63
Std Dev 3.338
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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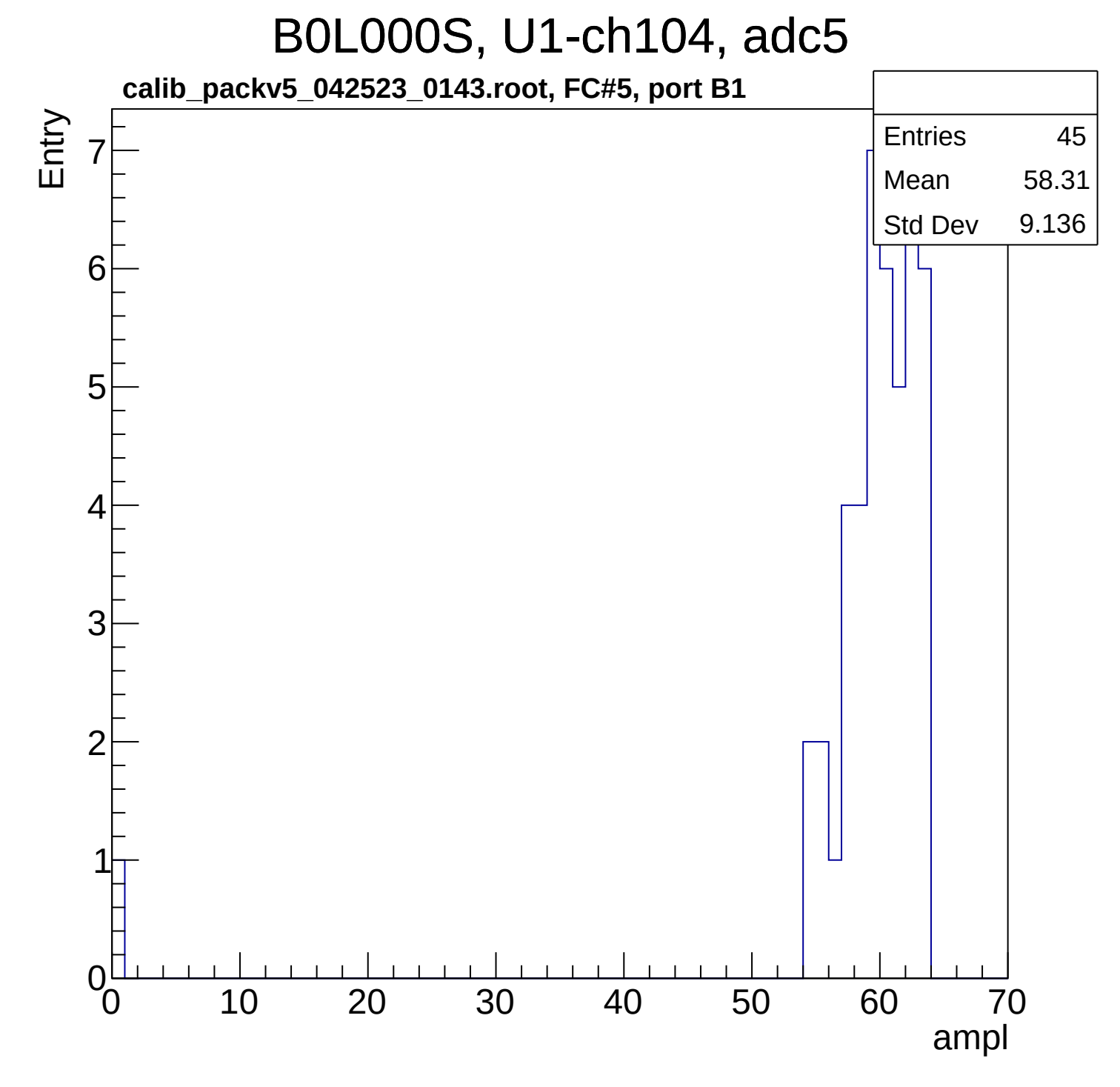
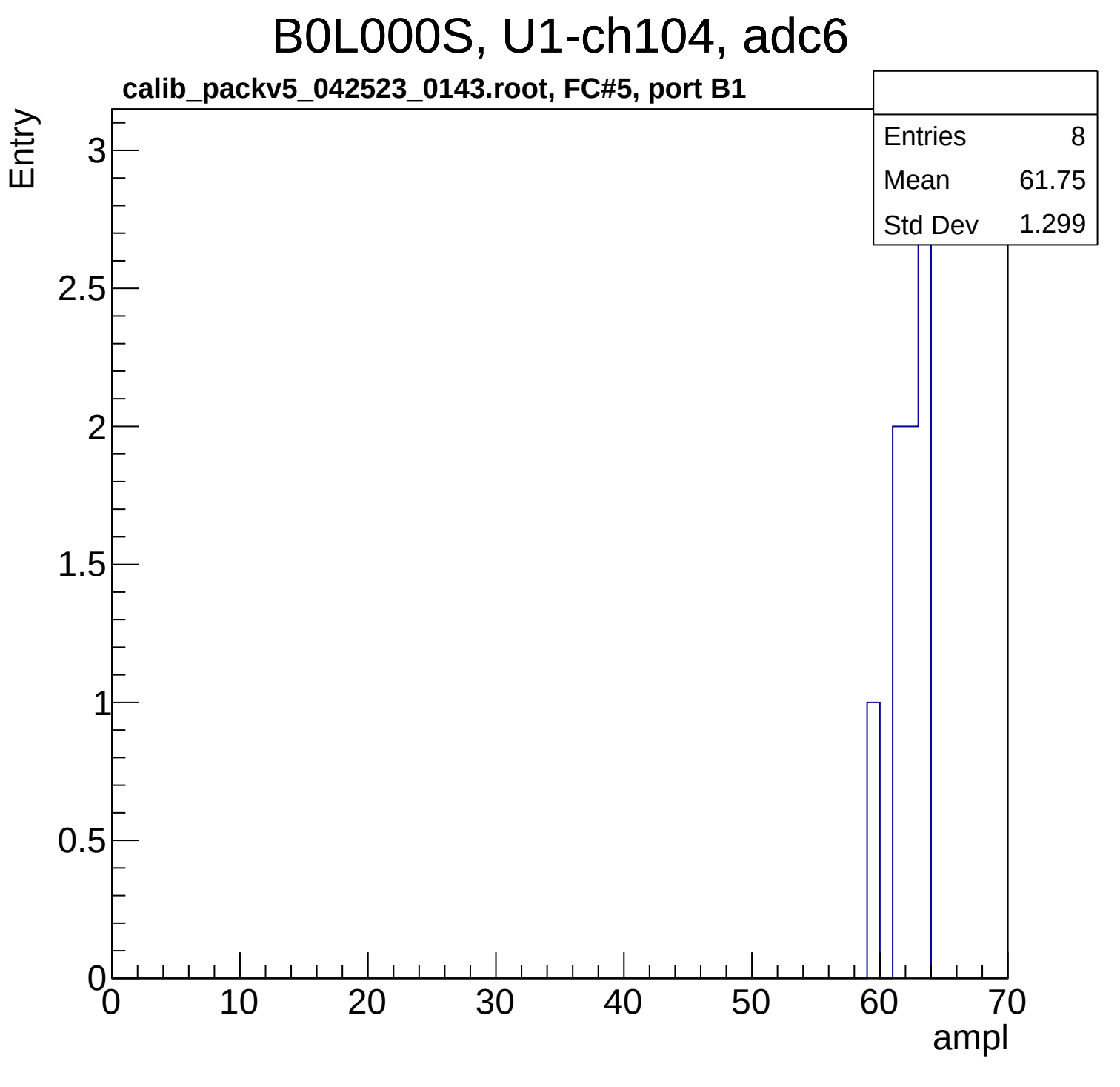

B0L000S, U1-ch104, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.31
Std Dev 9.136
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch104, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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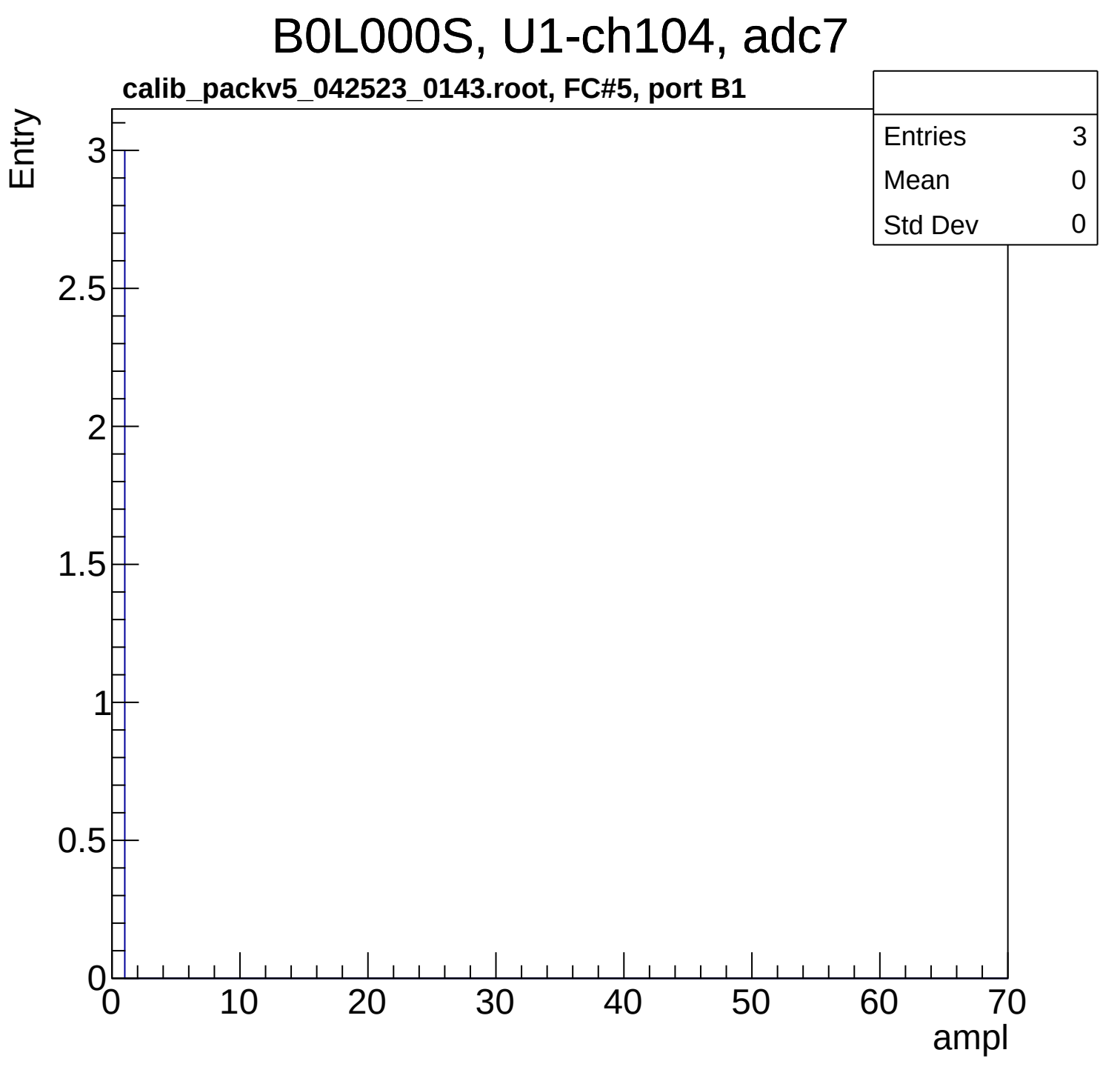
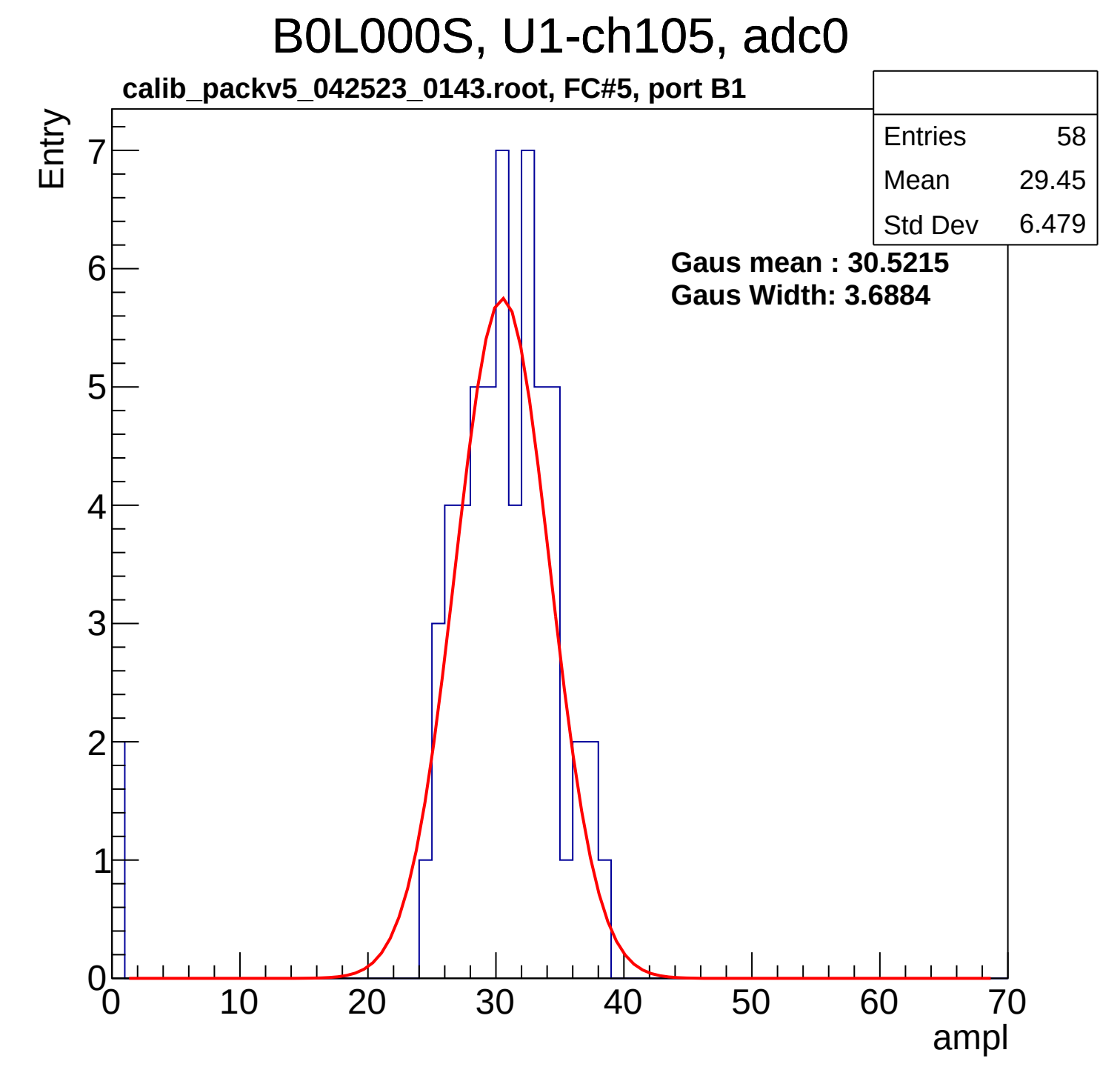
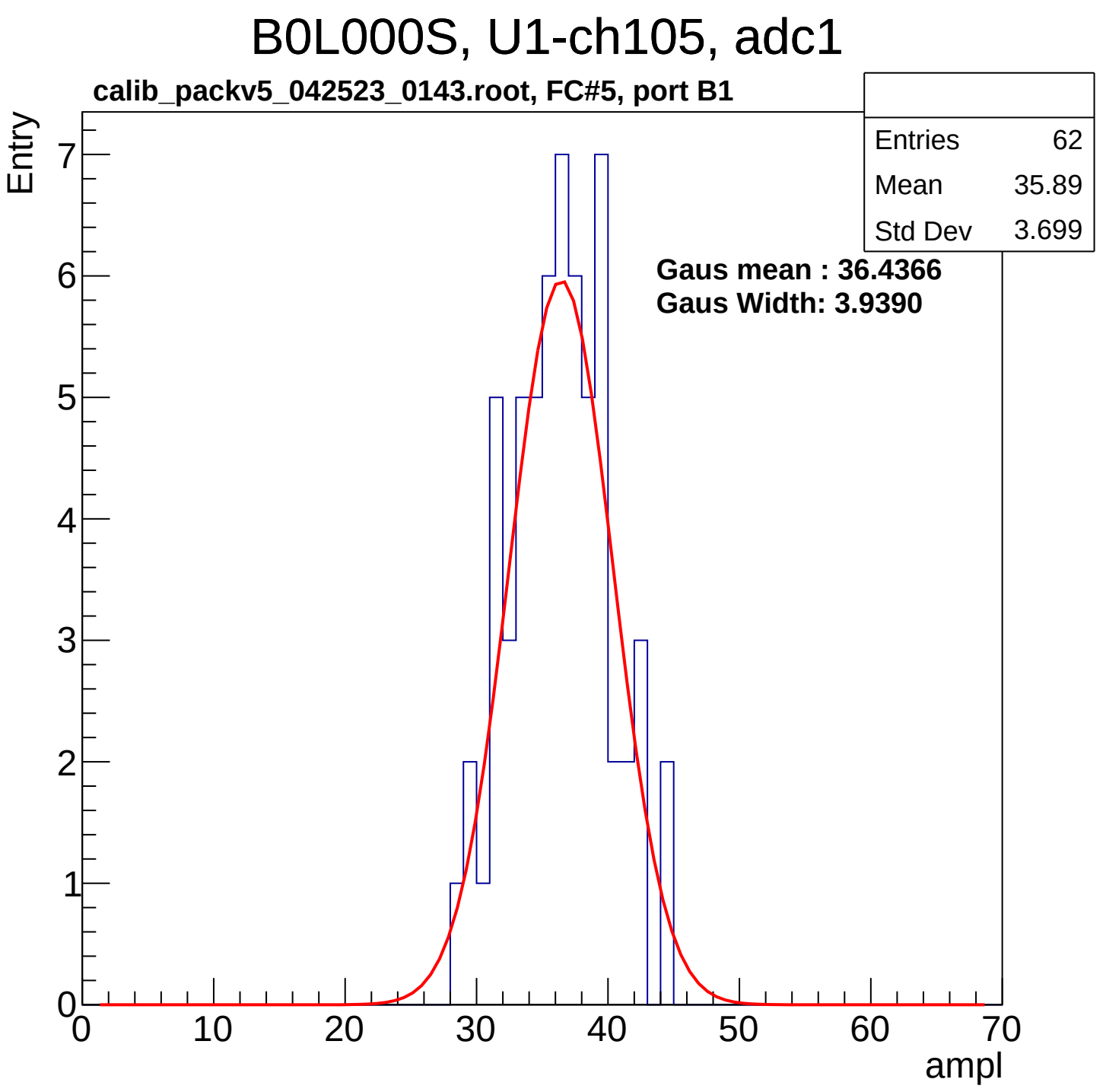
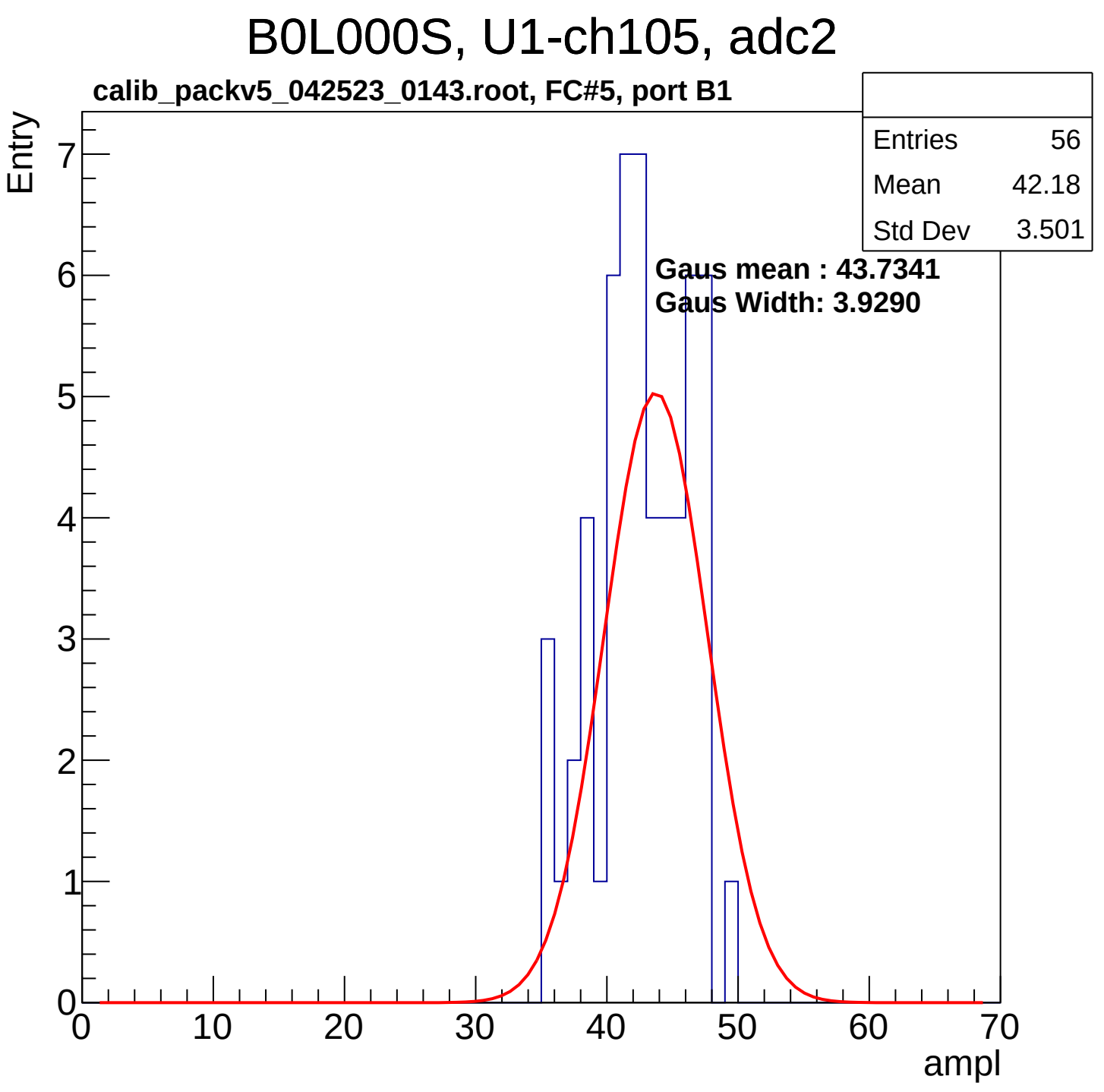
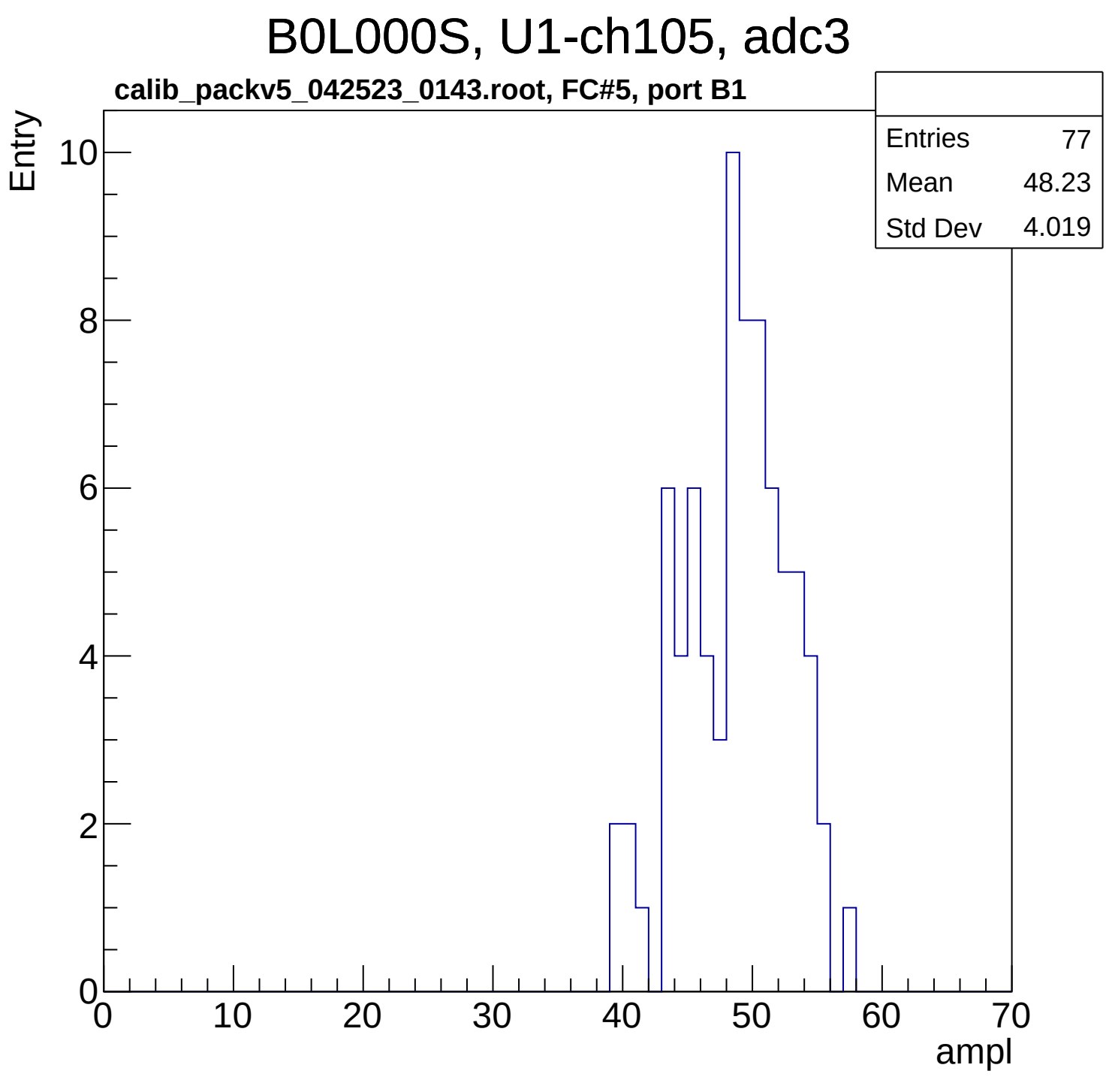
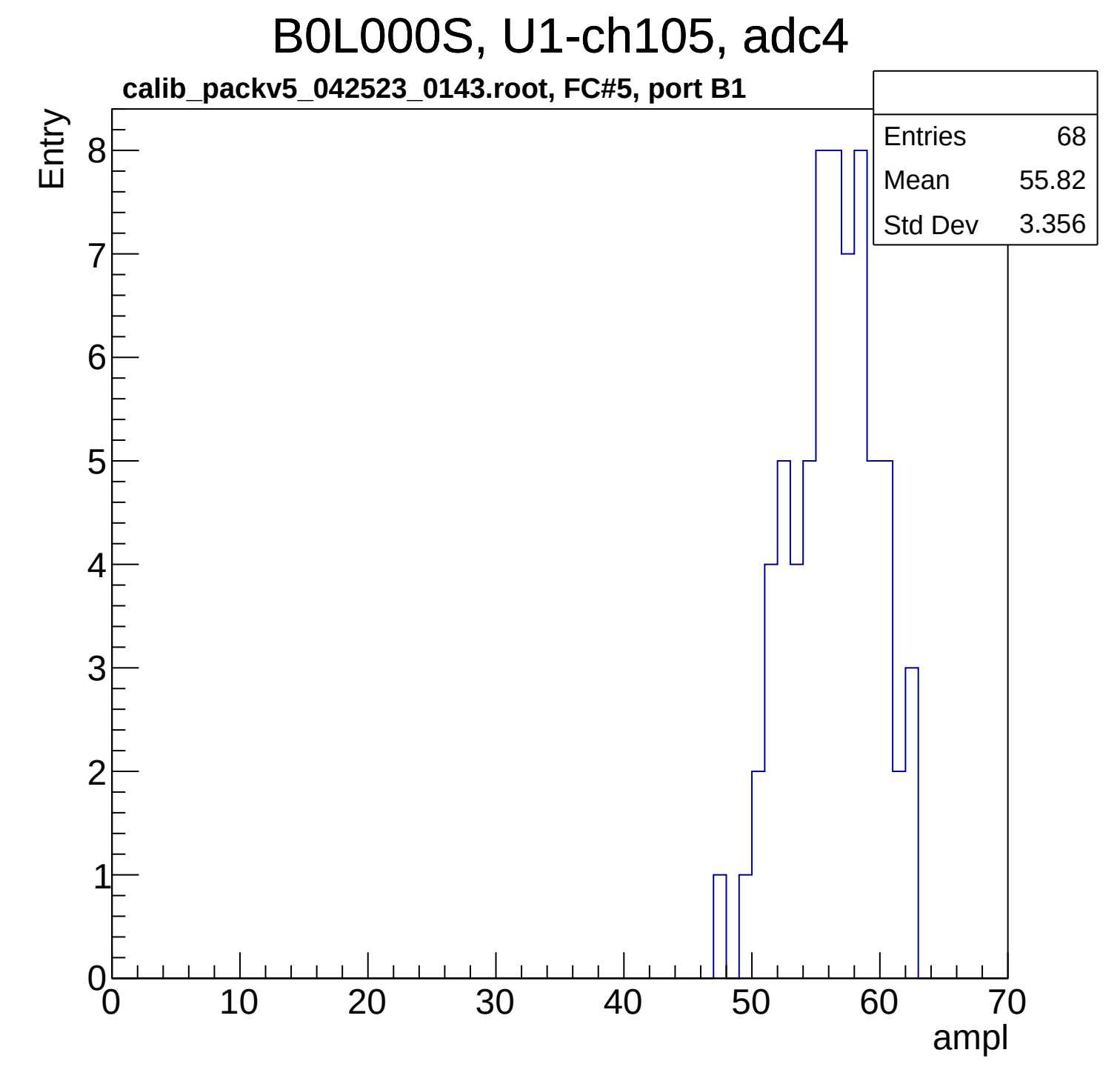

B0L000S, U1-ch104, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

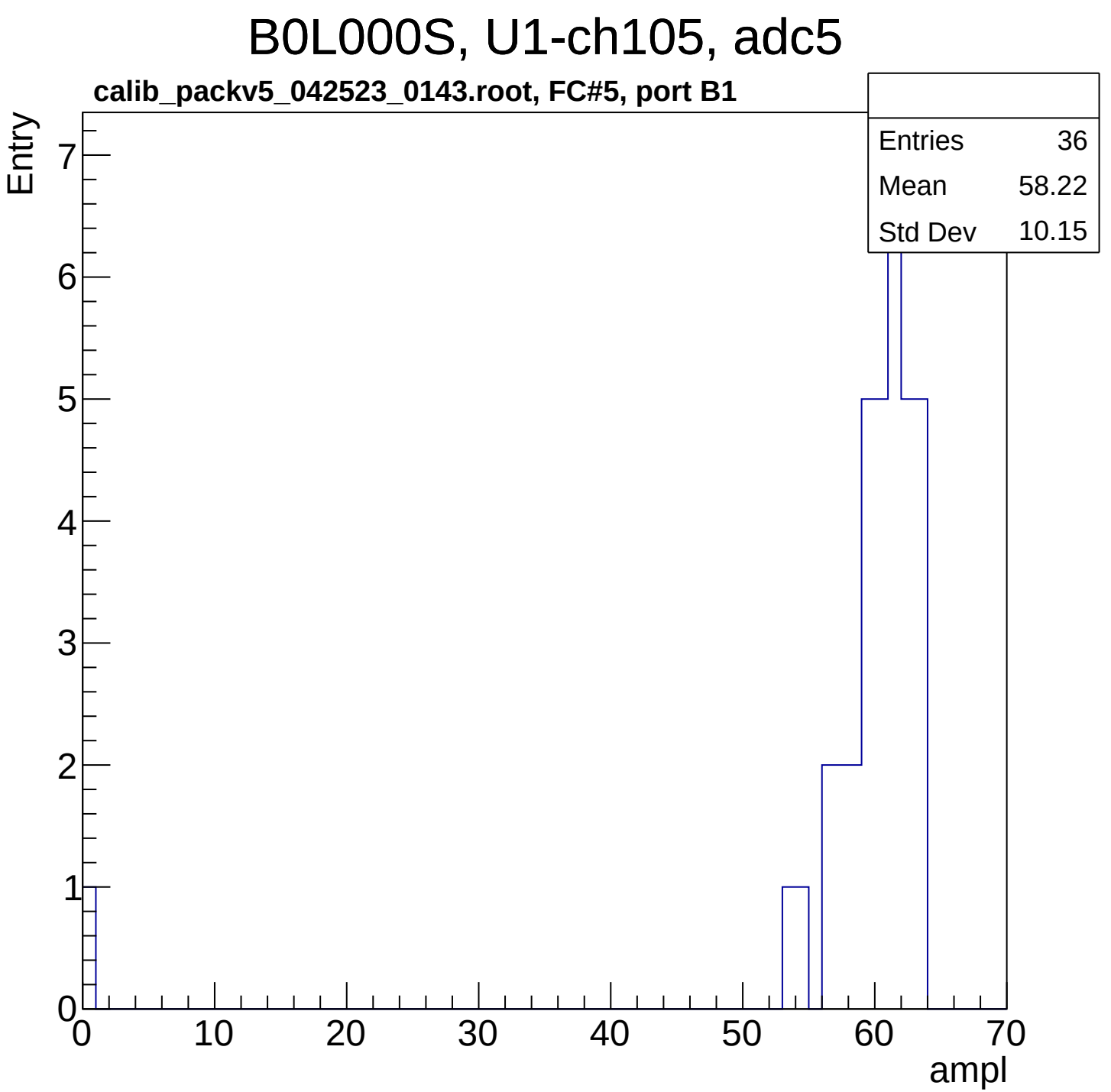

B0L000S, U1-ch105, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 29.45
Std Dev 6.479
Gaus mean : 30.5215
Gaus Width: 3.6884
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

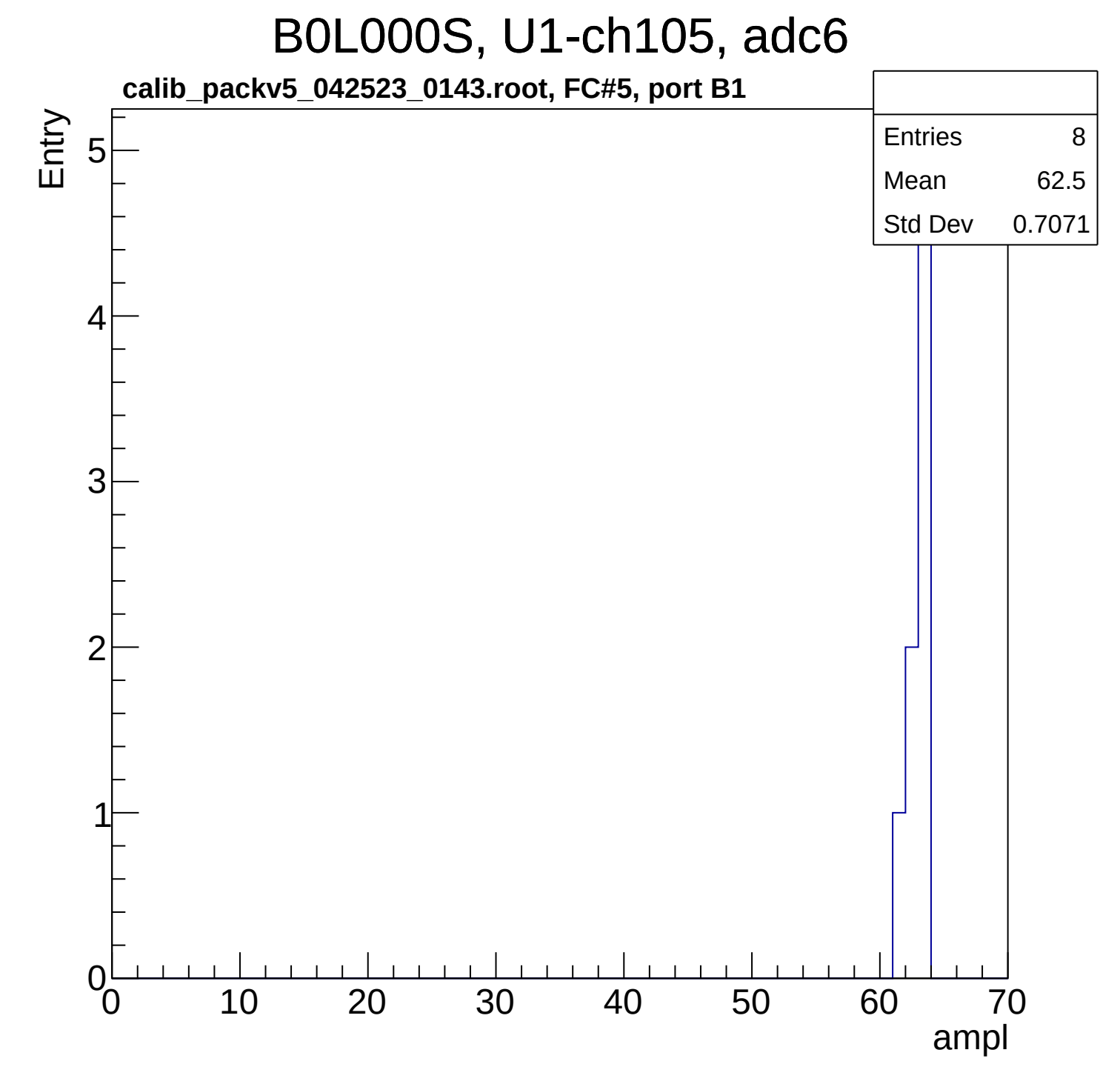

B0L000S, U1-ch105, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.89
Std Dev 3.699
Gaus mean : 36.4366
Gaus Width: 3.9390
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

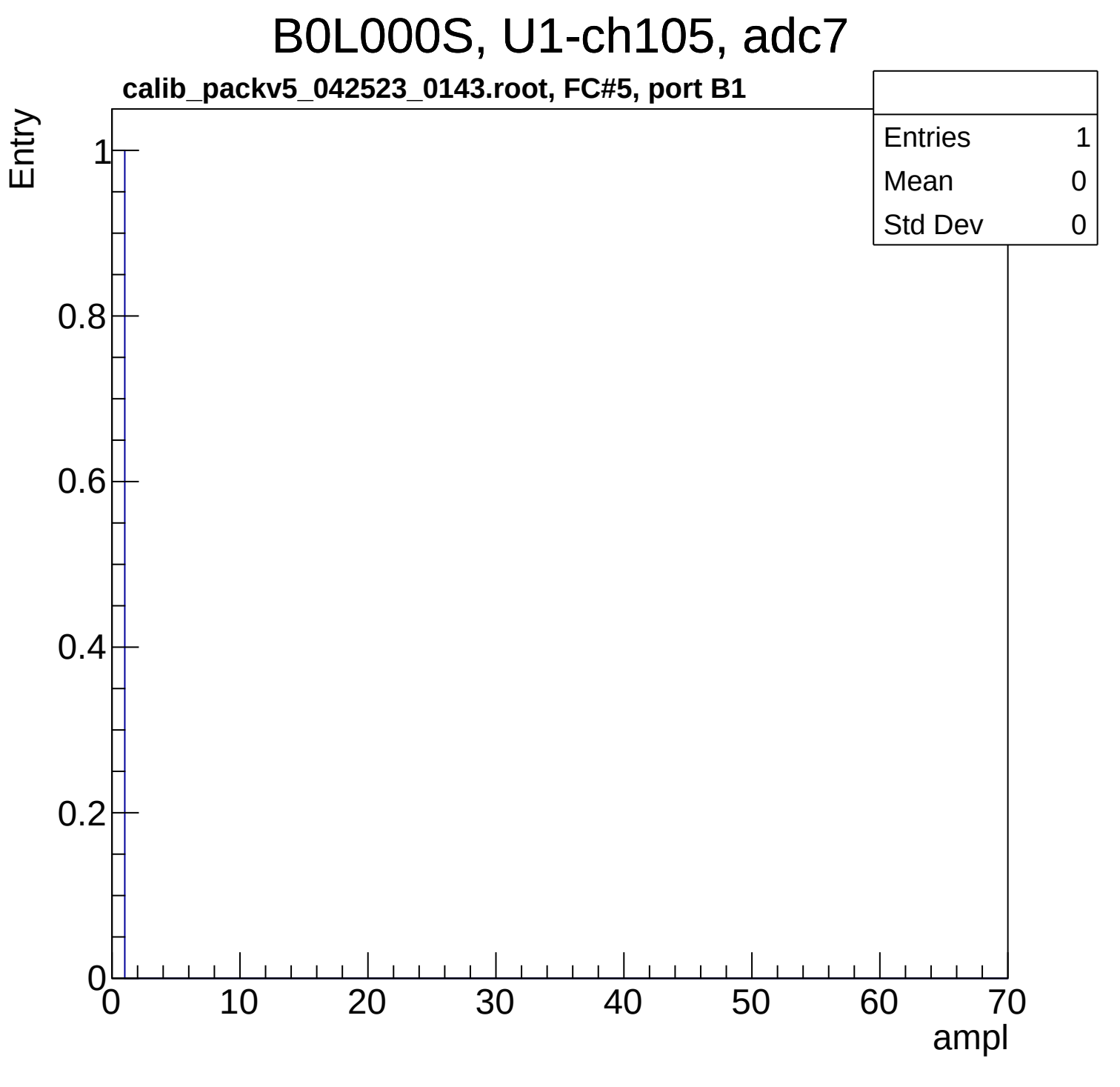

B0L000S, U1-ch105, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.18
Std Dev 3.501
Gaus mean : 43.7341
Gaus Width: 3.9290
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

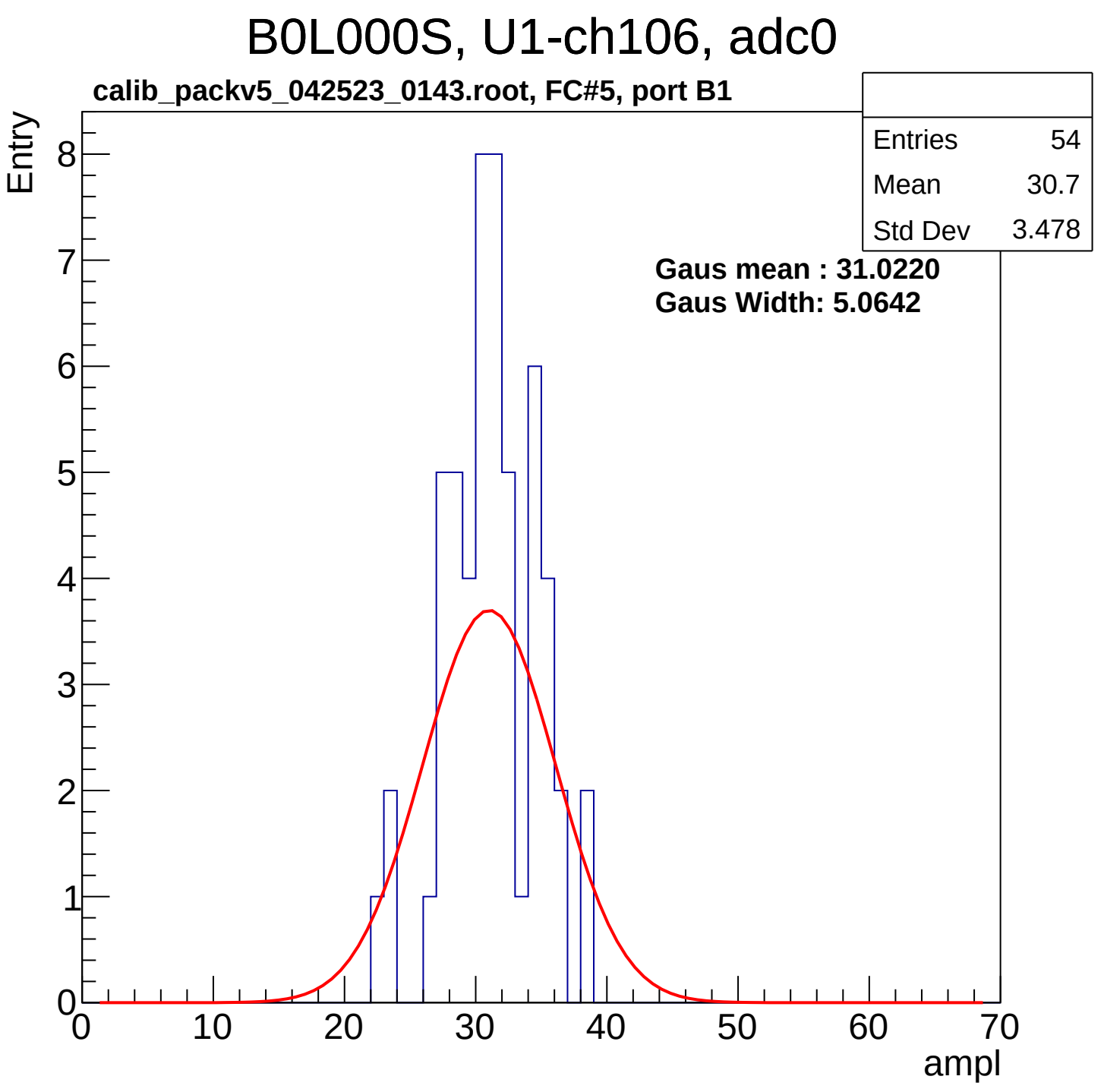

B0L000S, U1-ch105, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 48.23
Std Dev 4.019
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

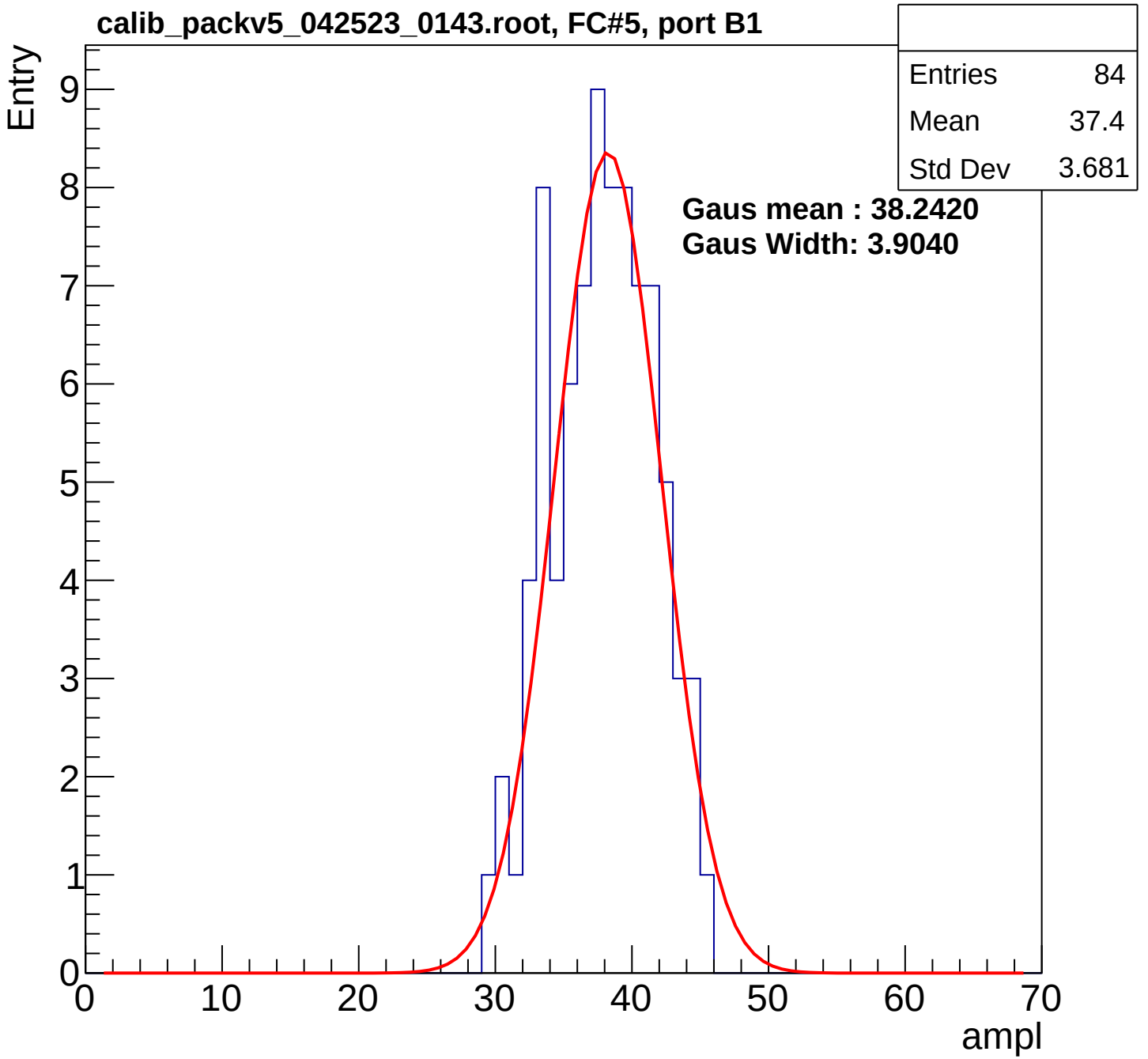

B0L000S, U1-ch105, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 55.82
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

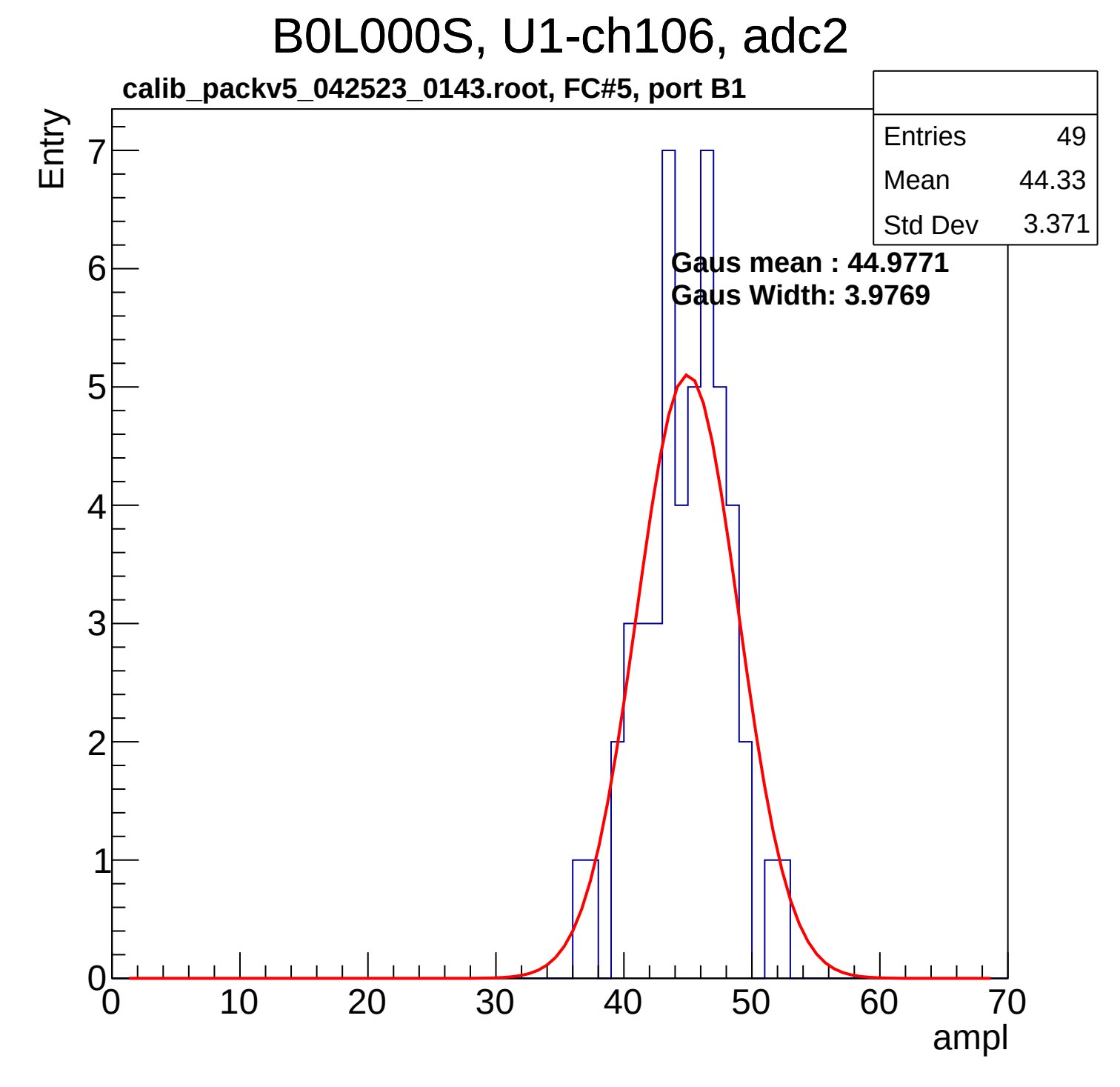

B0L000S, U1-ch105, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.22
Std Dev 10.15
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

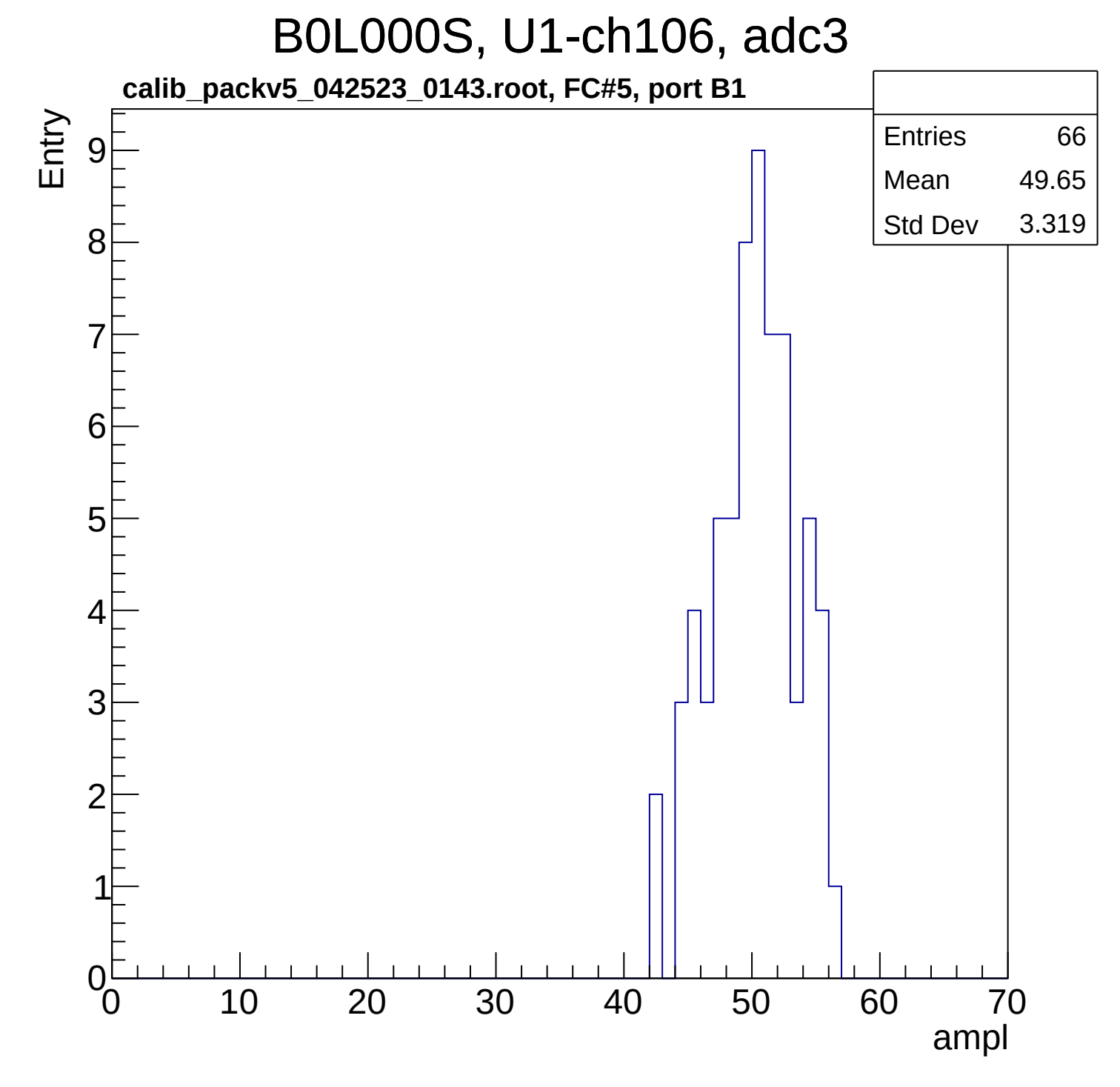

B0L000S, U1-ch105, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.5
Std Dev 0.7071
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

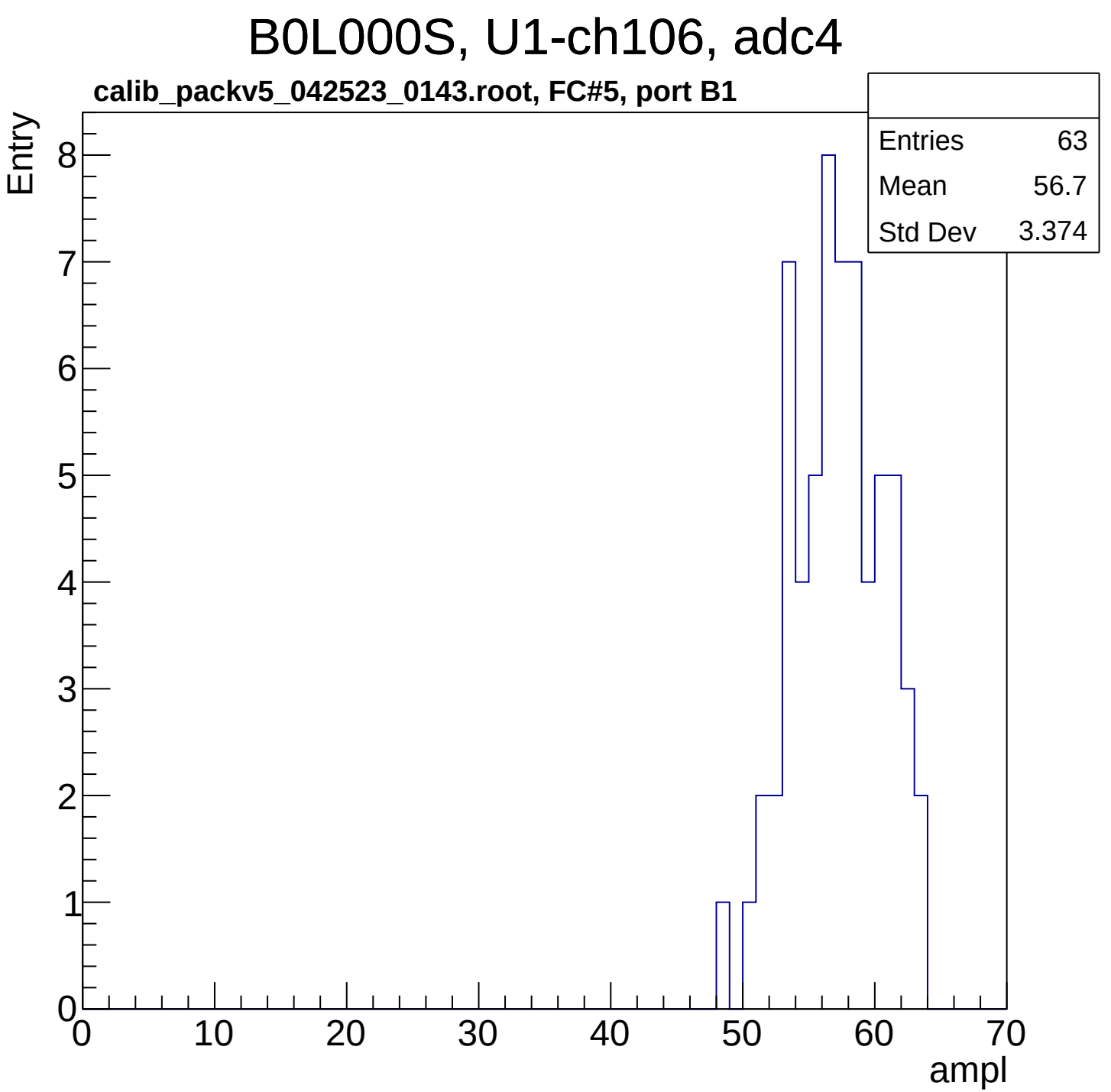

B0L000S, U1-ch105, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch106, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 30.7
Std Dev 3.478
Gaus mean : 31.0220
Gaus Width: 5.0642
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch106, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 37.4
Std Dev 3.681
Gaus mean : 38.2420
Gaus Width: 3.9040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch106, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 44.33
Std Dev 3.371
Gaus mean : 44.9771
Gaus Width: 3.9769
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch106, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.65
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch106, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 56.7
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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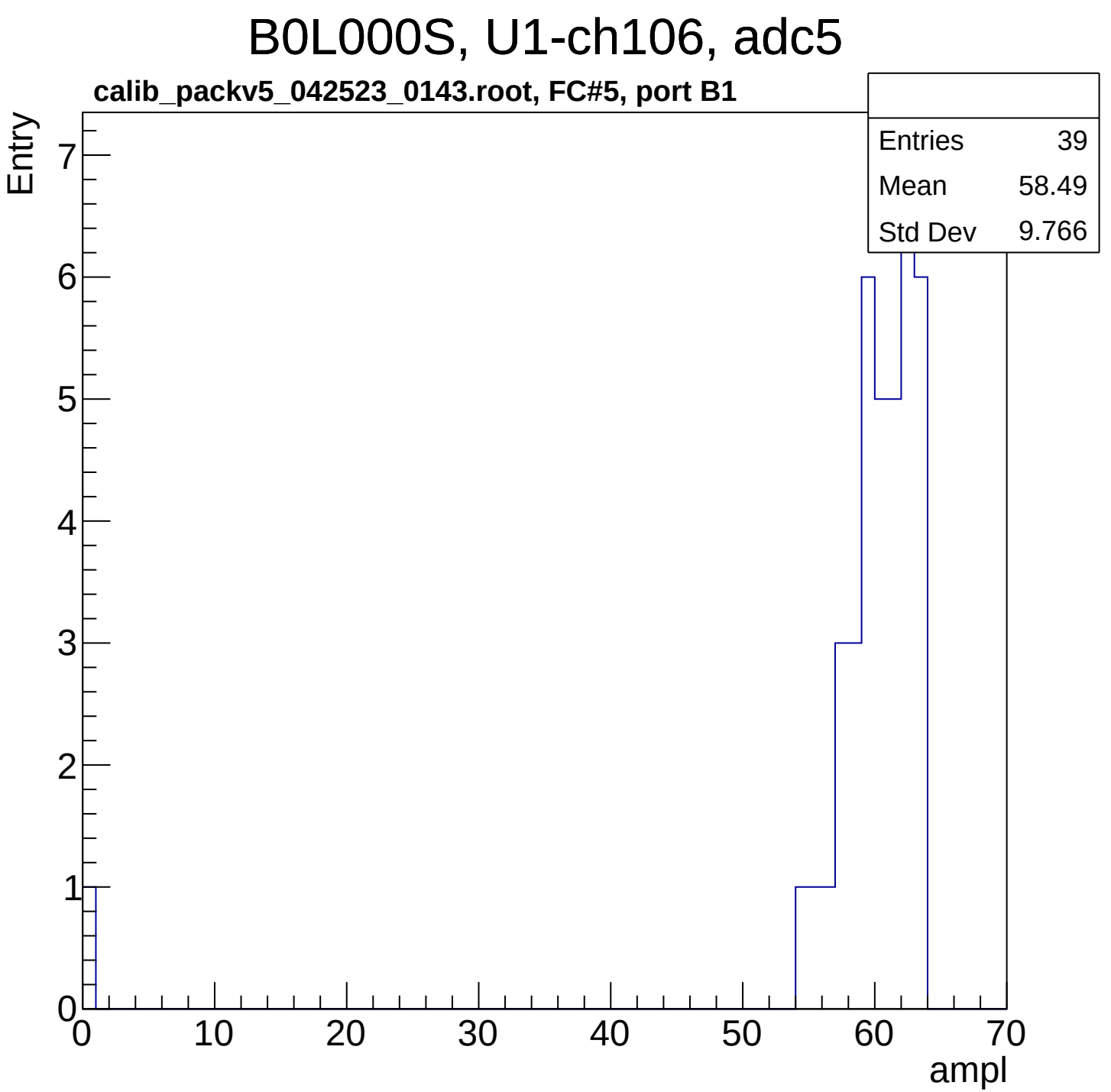
B0L000S, U1-ch106, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.49
Std Dev 9.766
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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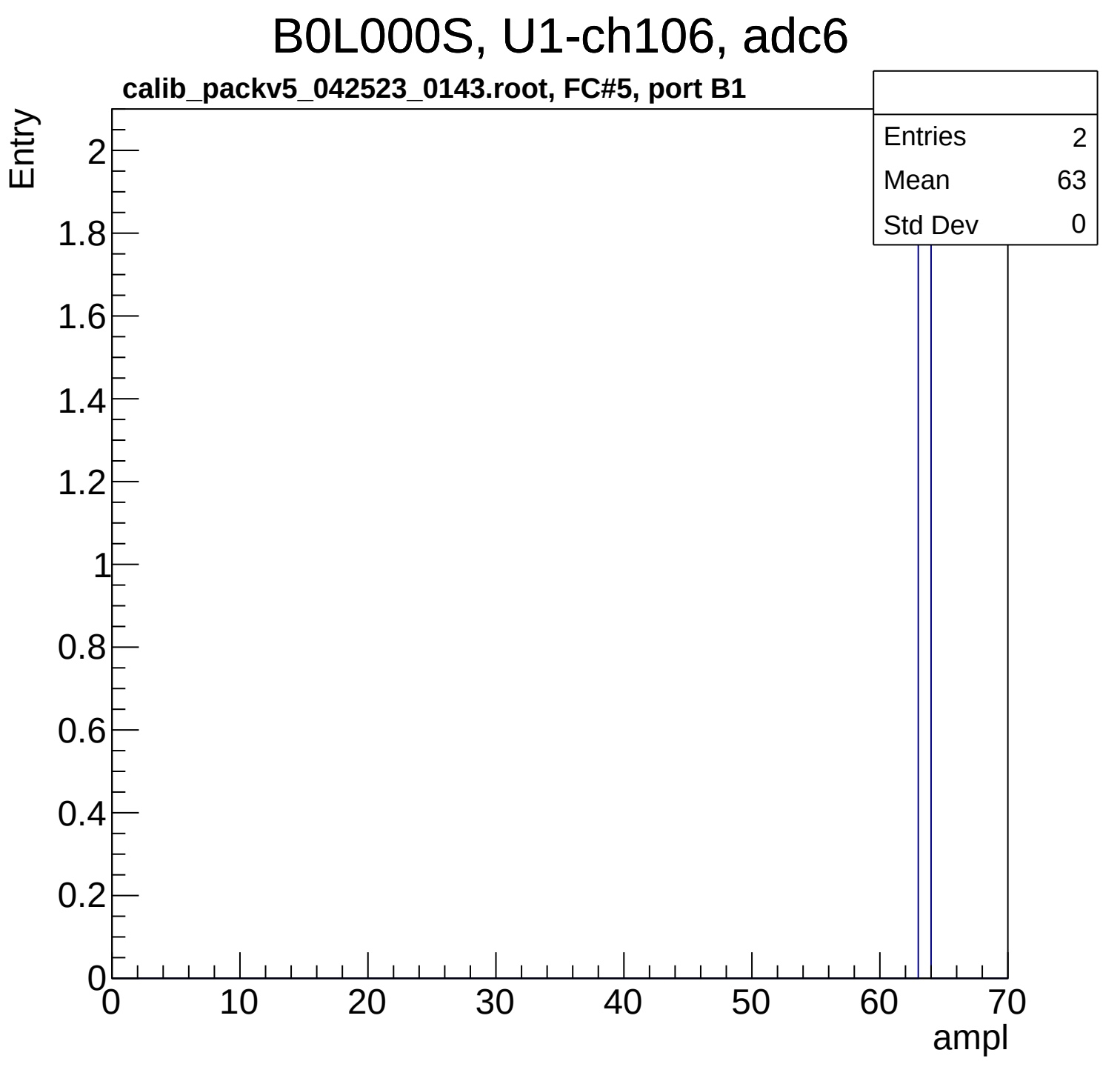
B0L000S, U1-ch106, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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30
40
50
60
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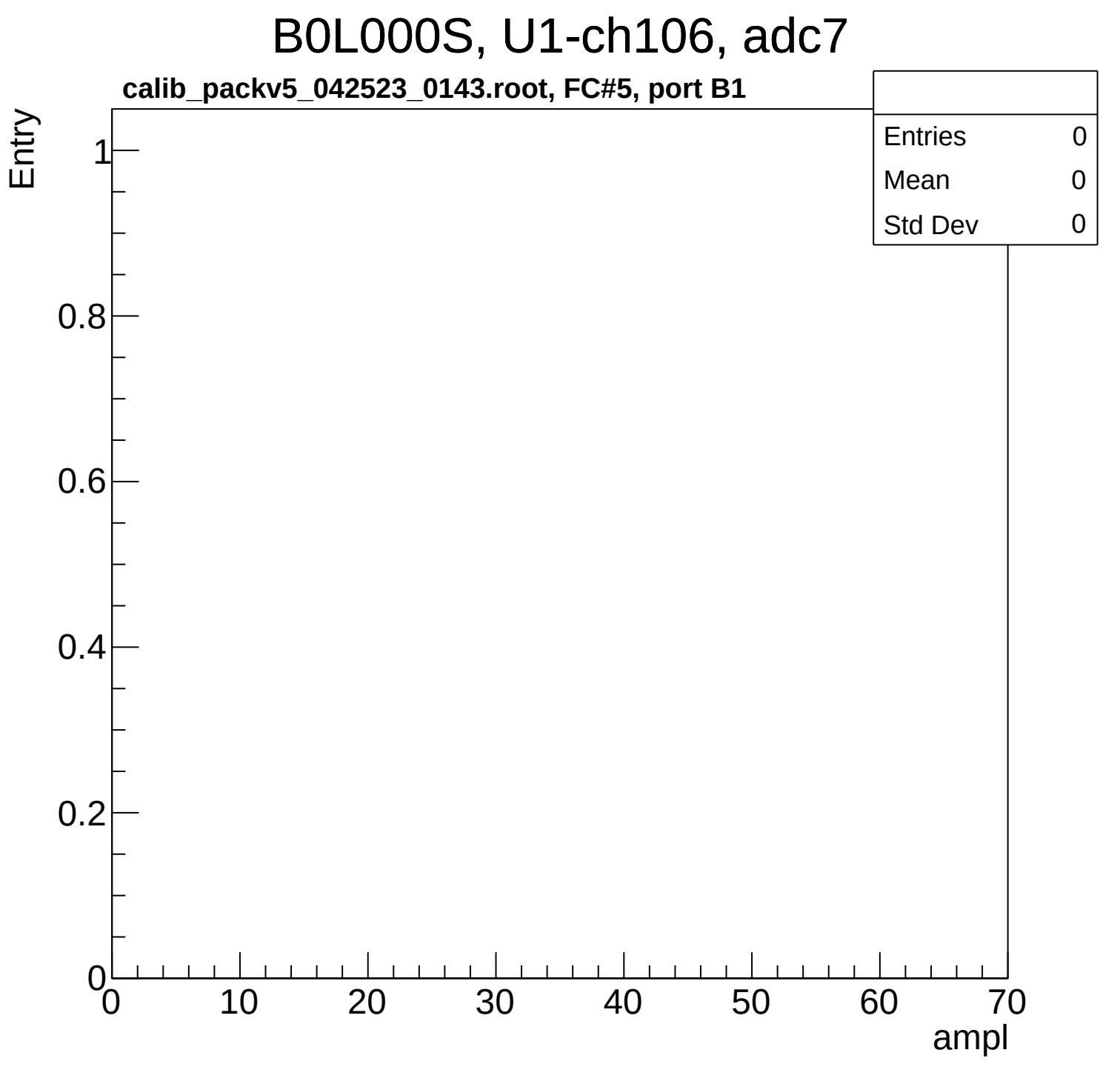
B0L000S, U1-ch106, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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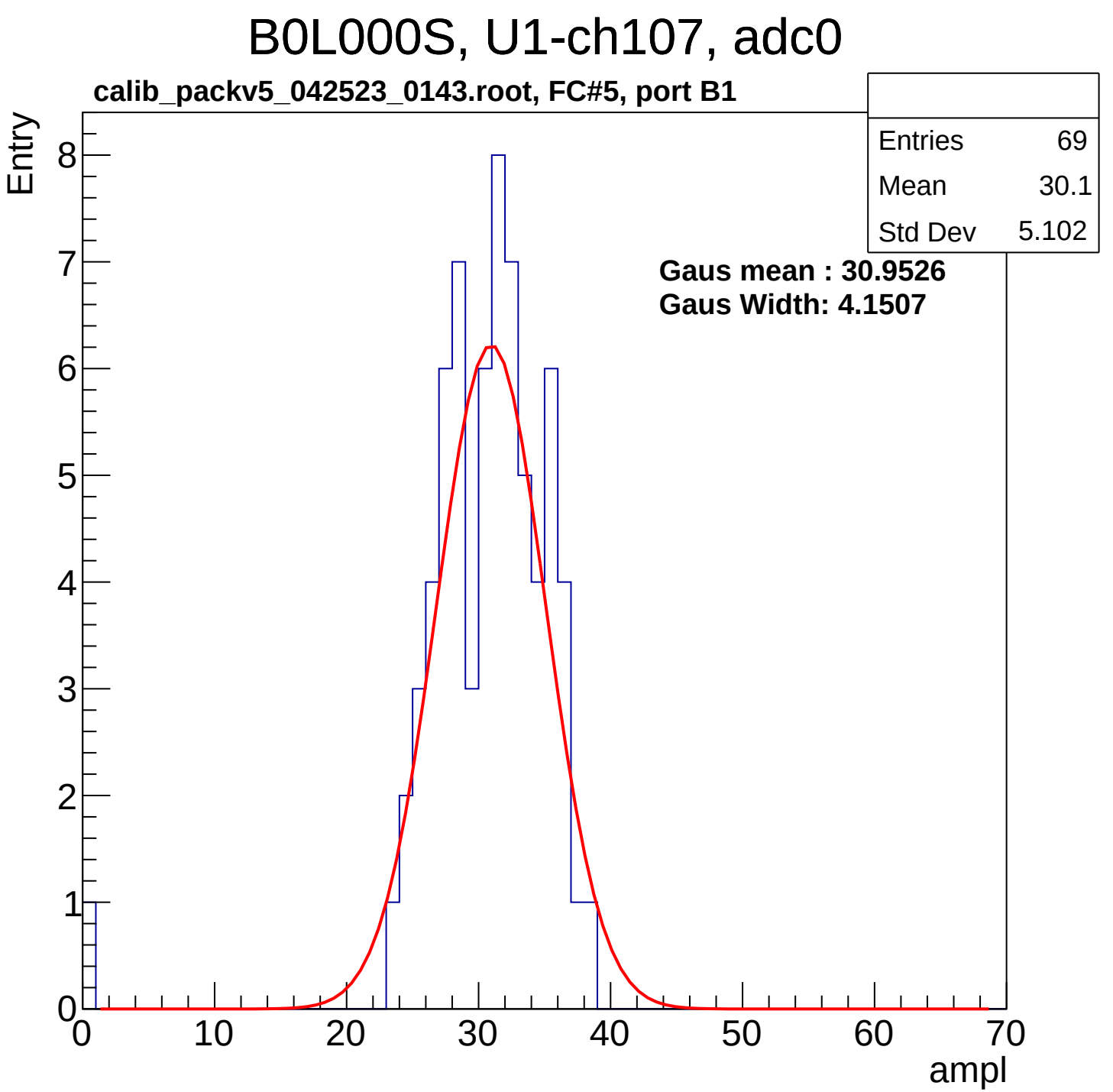
30
40
50
60
70
ampl


B0L000S, U1-ch107, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 30.1
Std Dev 5.102
Gaus mean : 30.9526
Gaus Width: 4.1507
Entry
ampl
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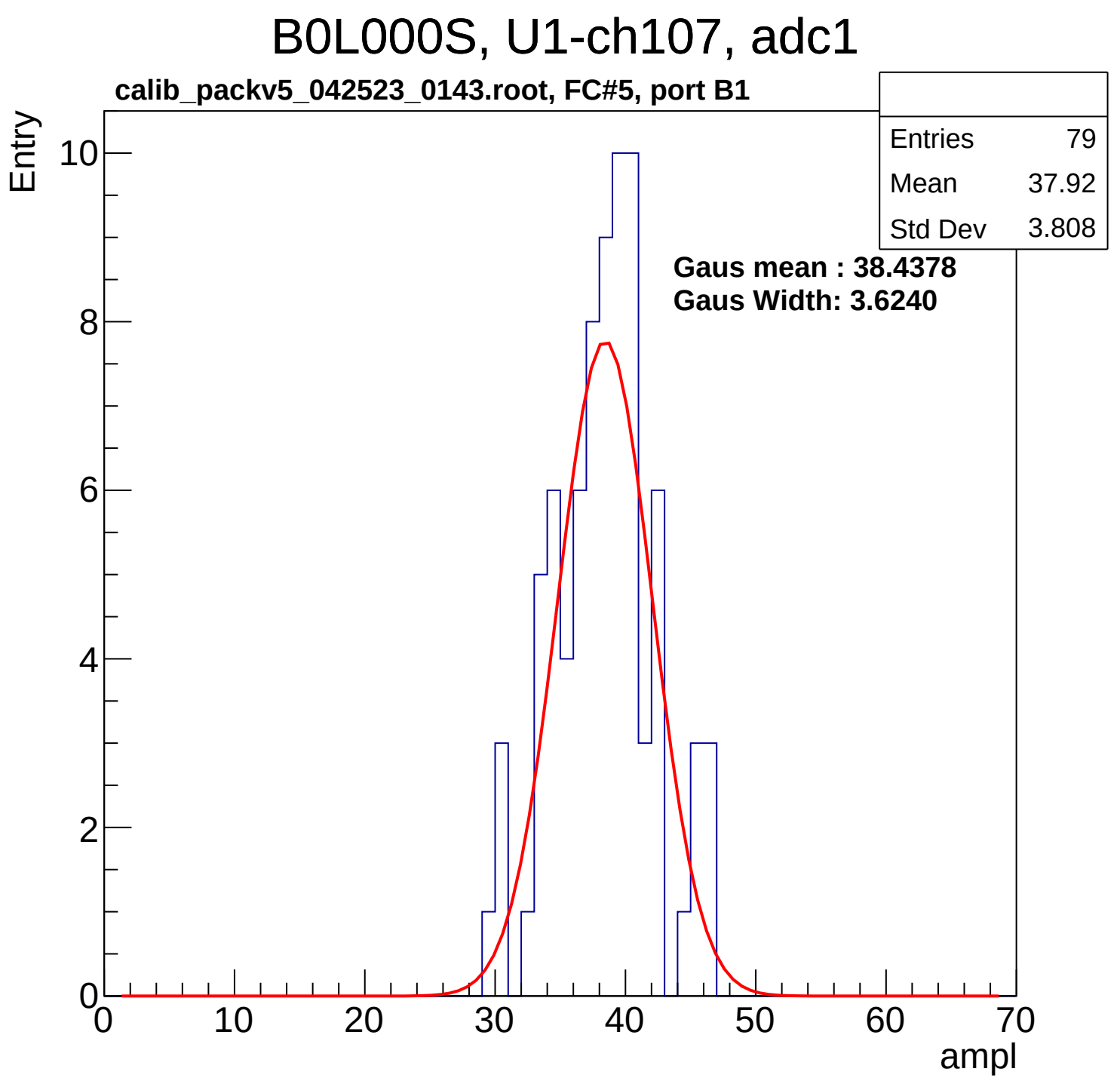
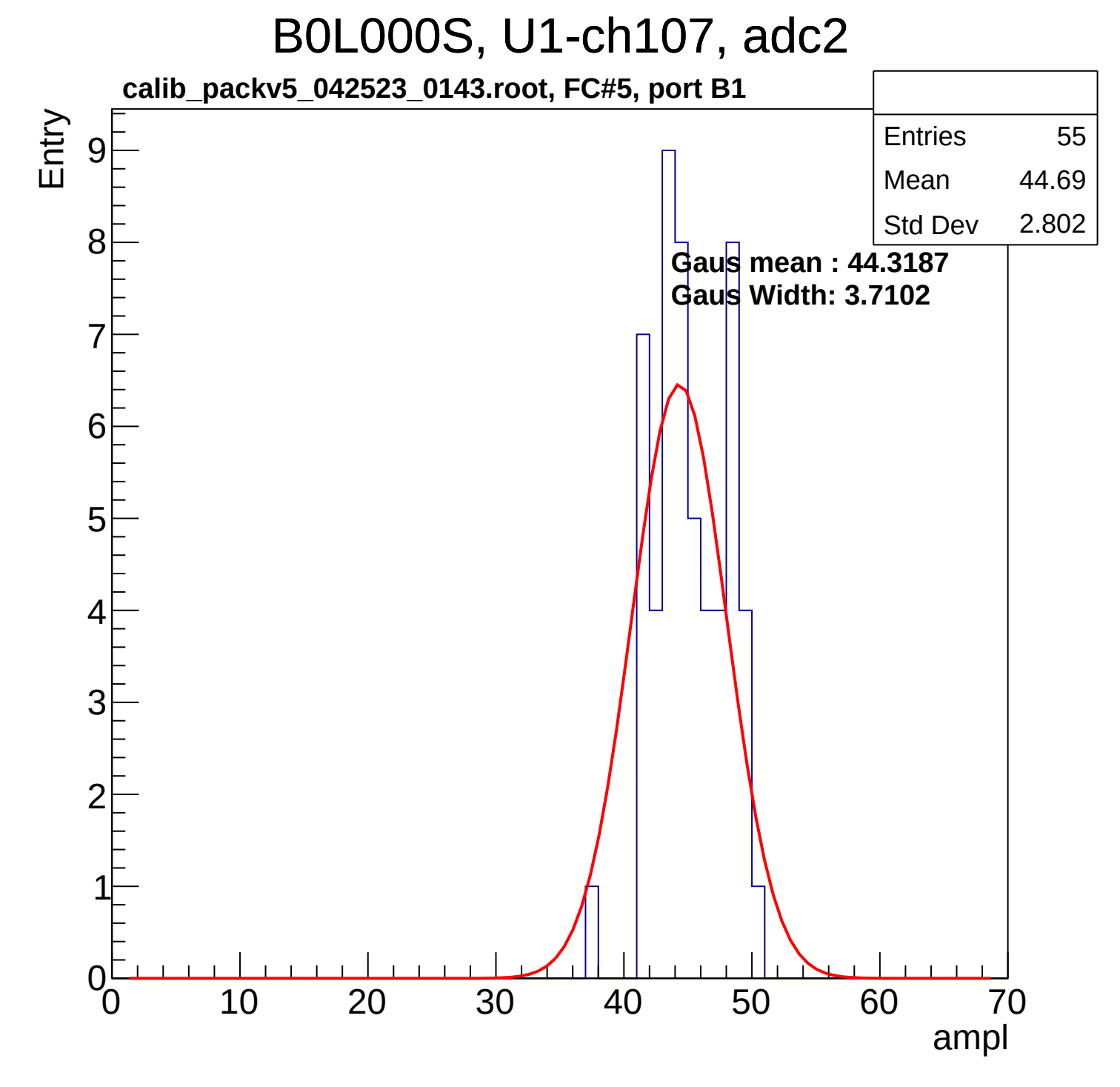
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch107, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 37.92
Std Dev 3.808
Gaus mean : 38.4378
Gaus Width: 3.6240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch107, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 44.69
Std Dev 2.802
Gaus mean : 44.3187
Gaus Width: 3.7102
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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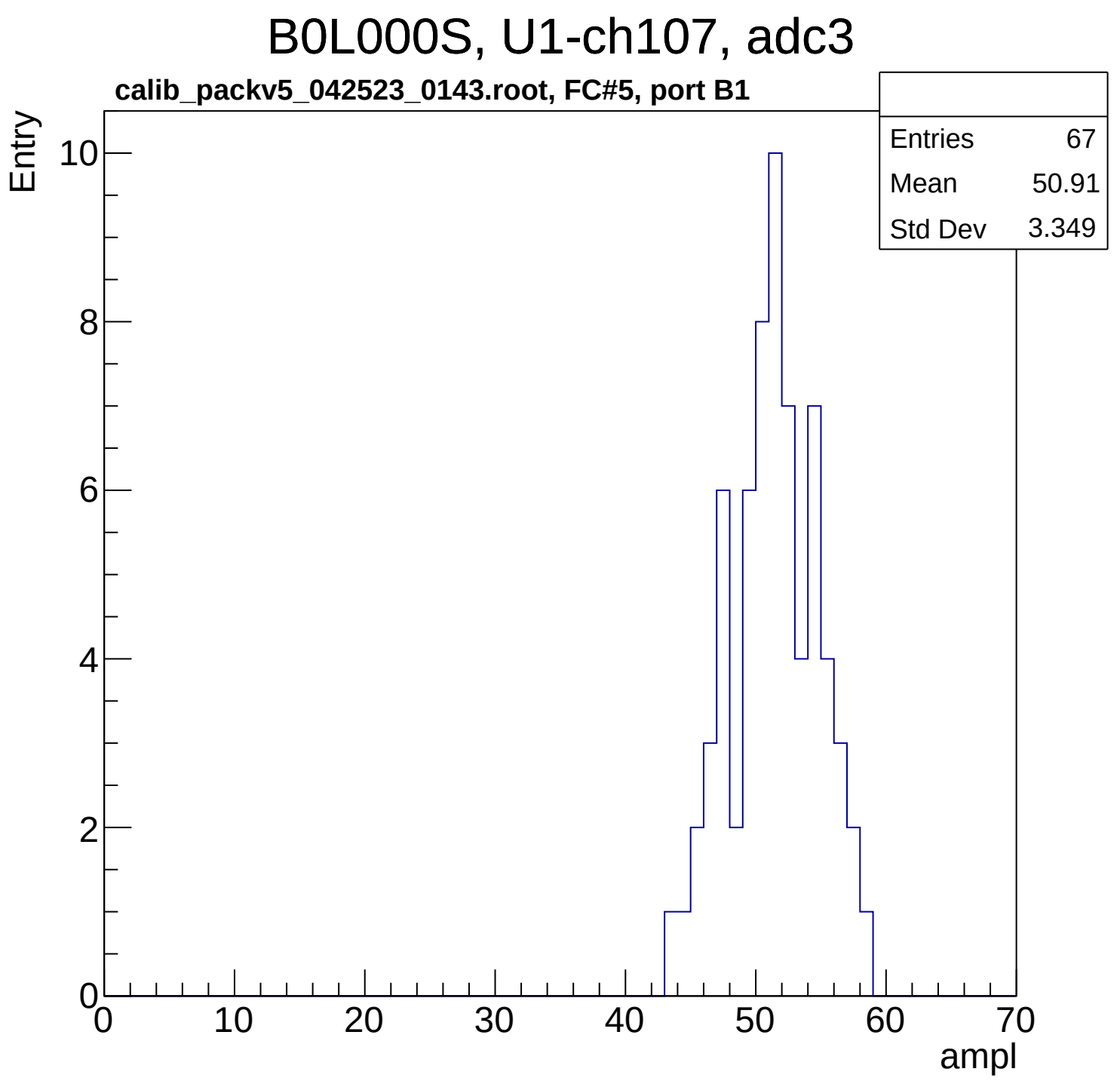
B0L000S, U1-ch107, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.91
Std Dev 3.349
Entry
ampl
0
2
4
6
8
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0
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20
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40
50
60
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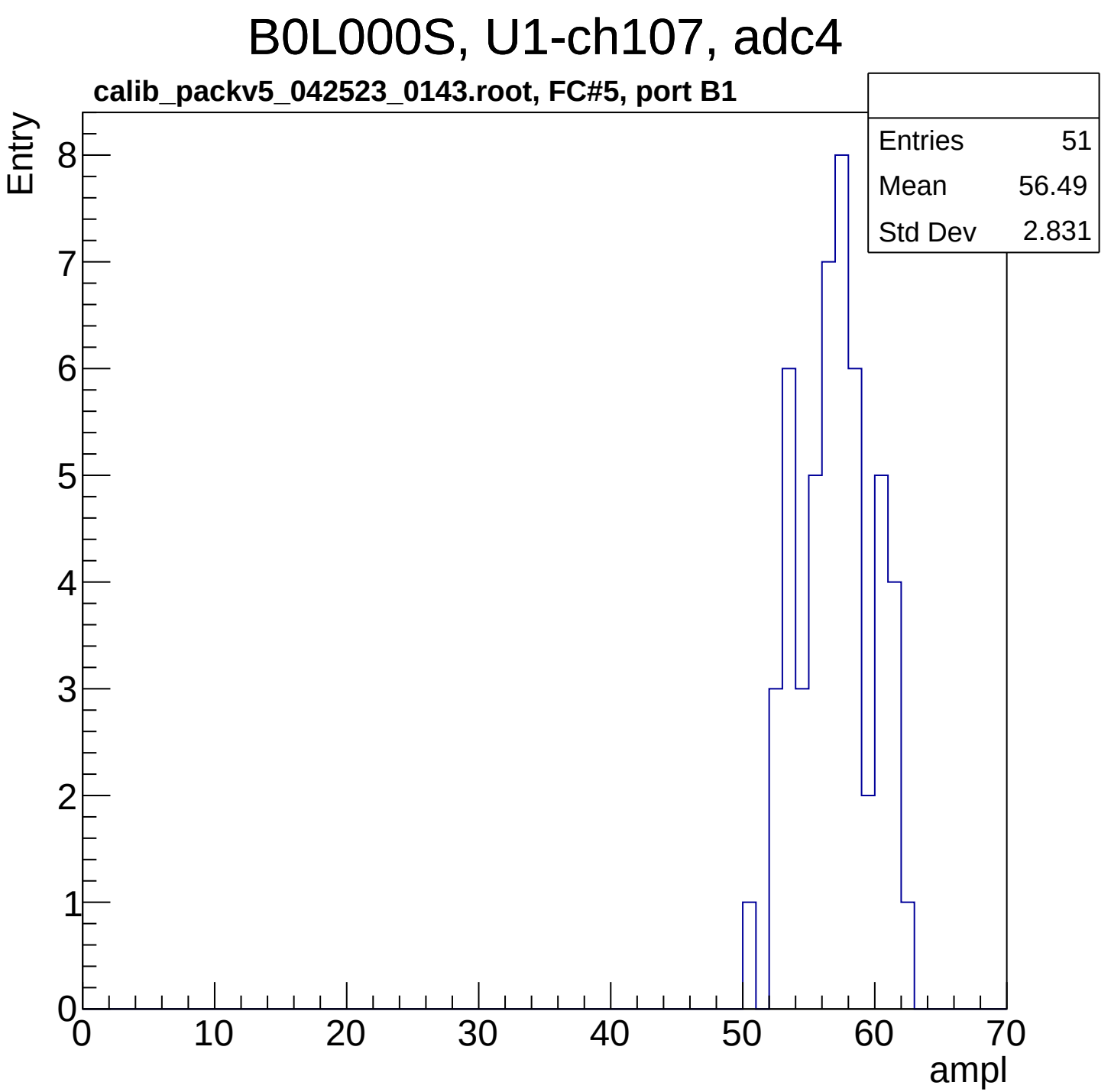
B0L000S, U1-ch107, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.49
Std Dev 2.831
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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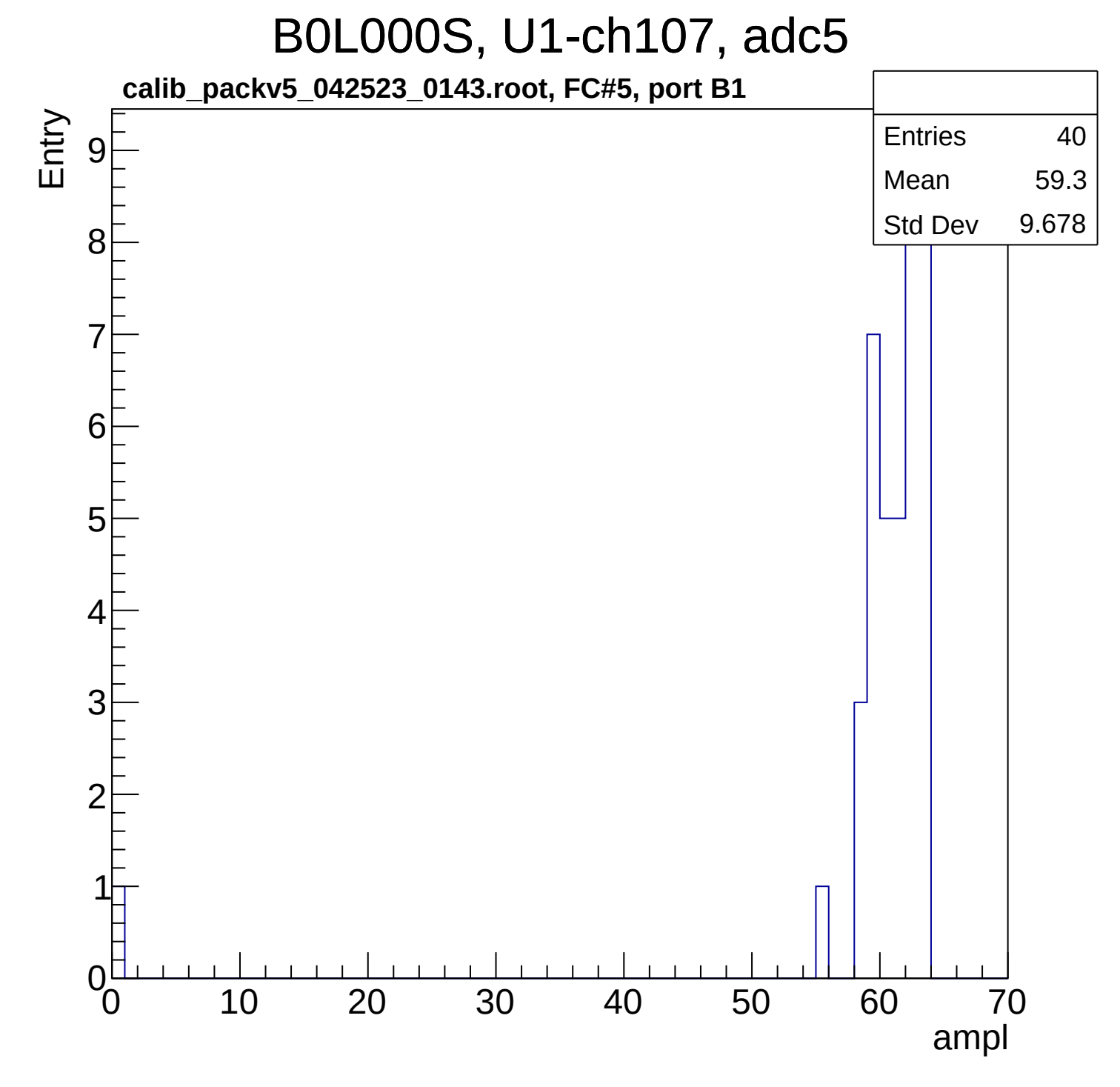
B0L000S, U1-ch107, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.3
Std Dev 9.678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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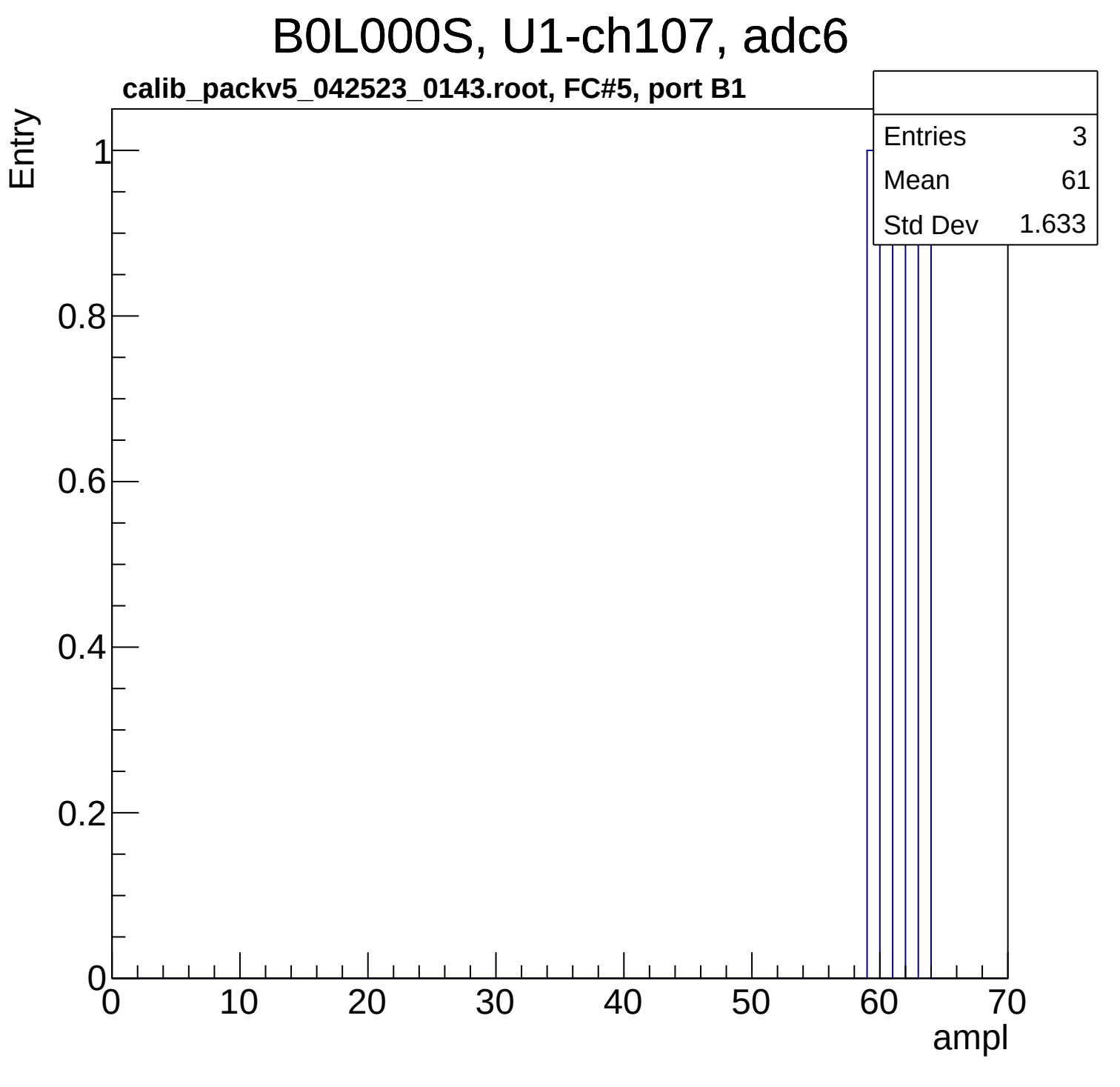
B0L000S, U1-ch107, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61
Std Dev 1.633
Entry
1
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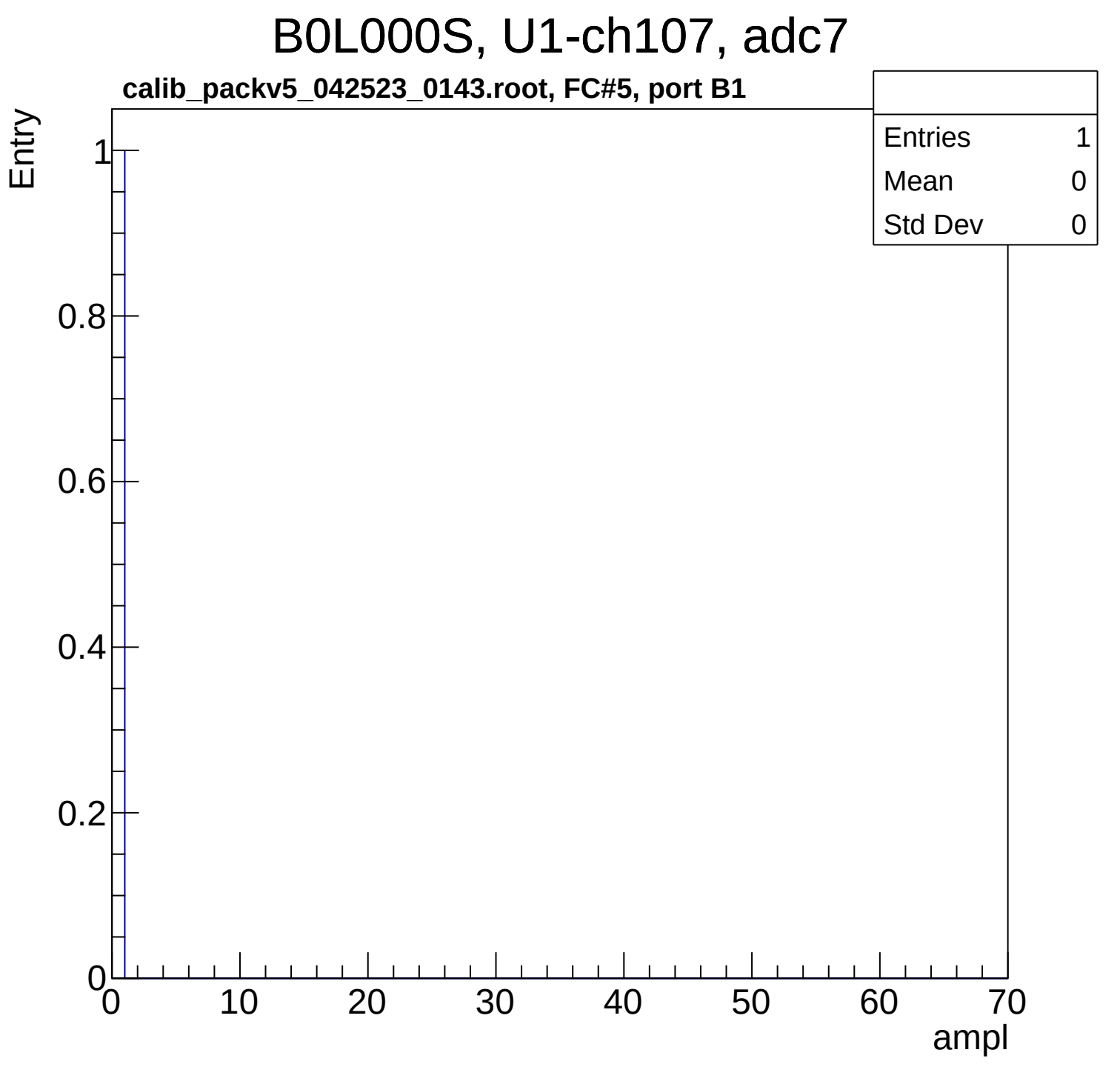
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch107, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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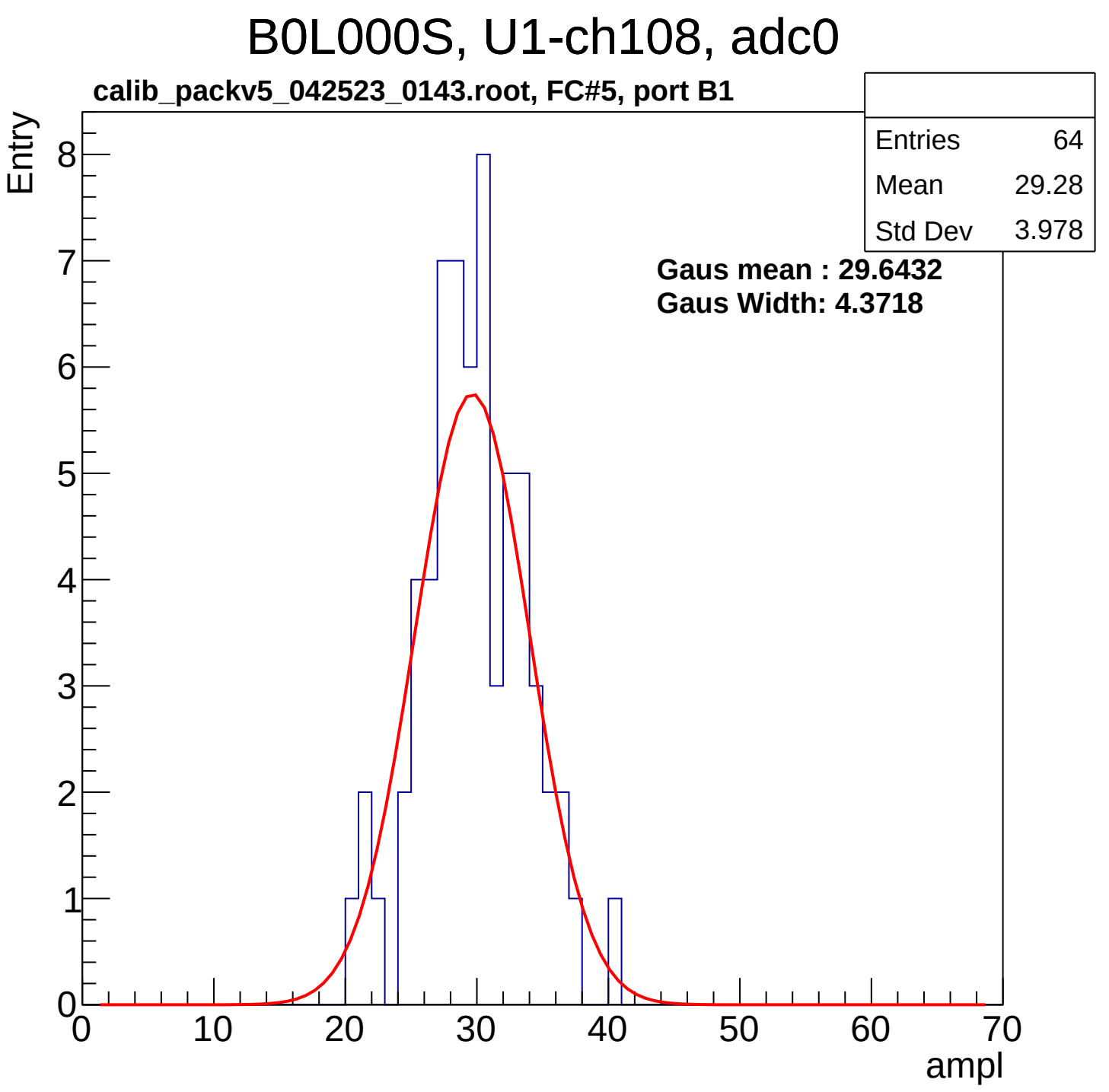
50
60
70
ampl


B0L000S, U1-ch108, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.28
Std Dev 3.978
Gaus mean : 29.6432
Gaus Width: 4.3718
Entry
ampl
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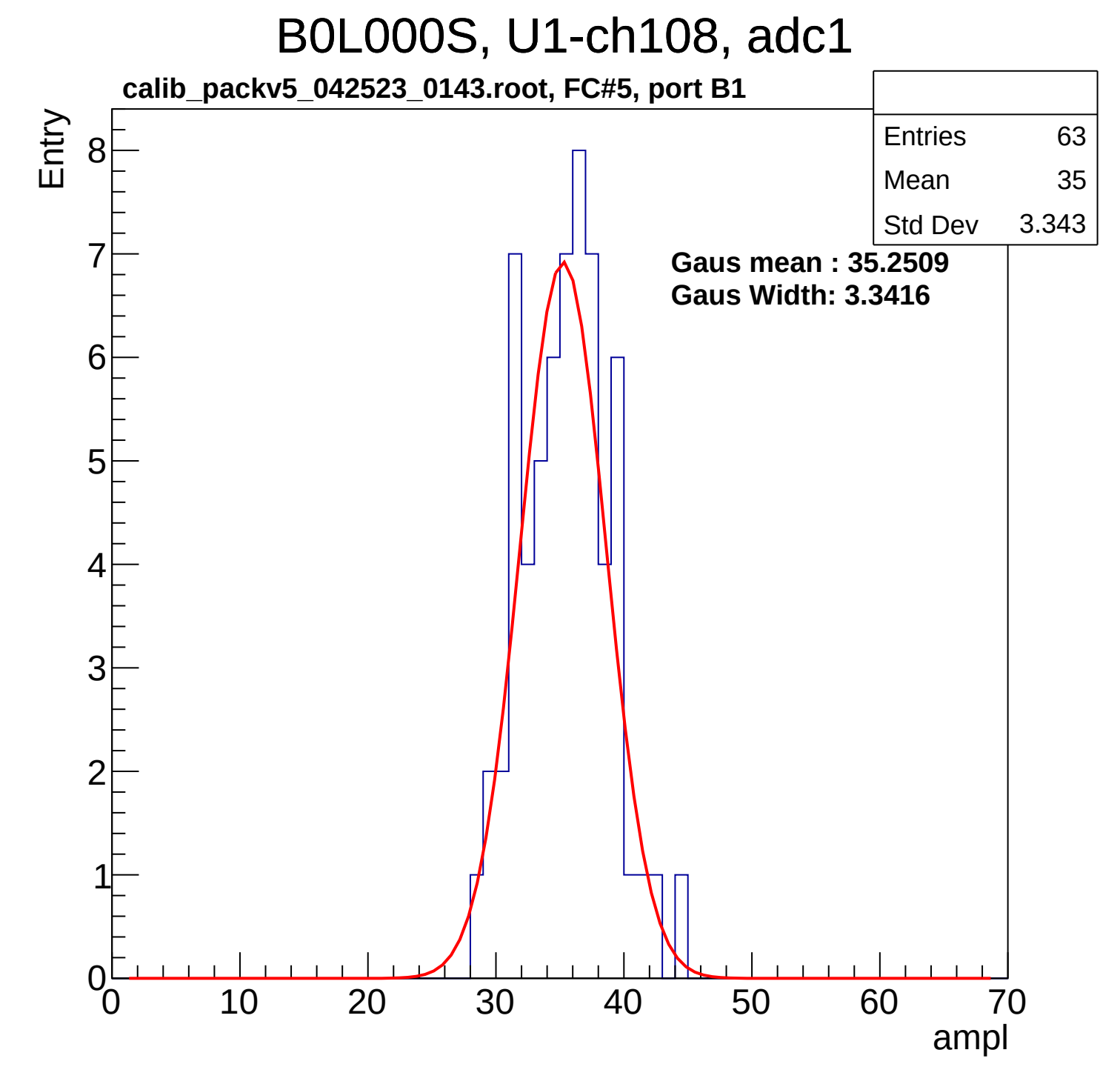
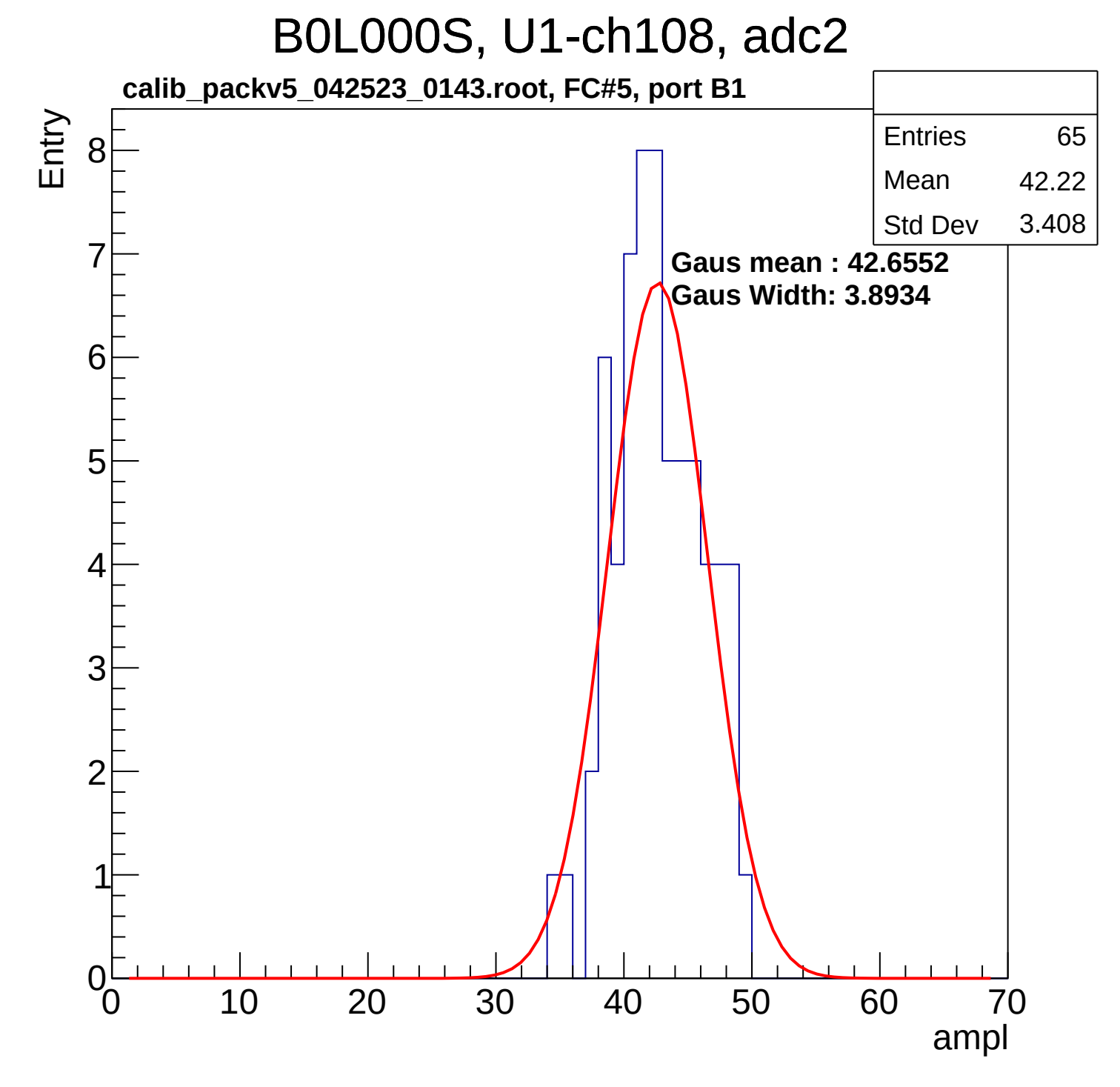
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch108, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35
Std Dev 3.343
Gaus mean : 35.2509
Gaus Width: 3.3416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch108, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.22
Std Dev 3.408
Gaus mean : 42.6552
Gaus Width: 3.8934
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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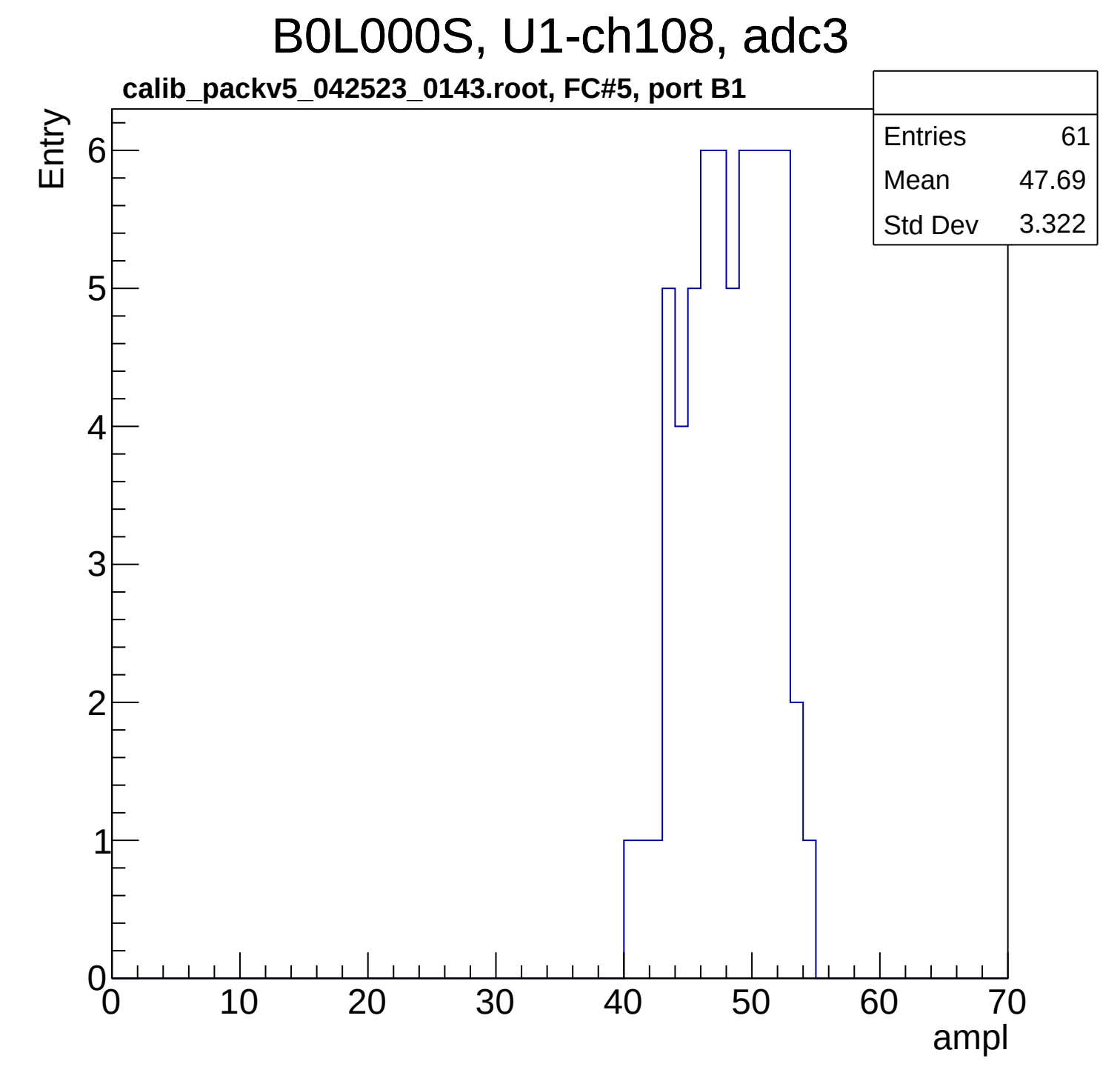
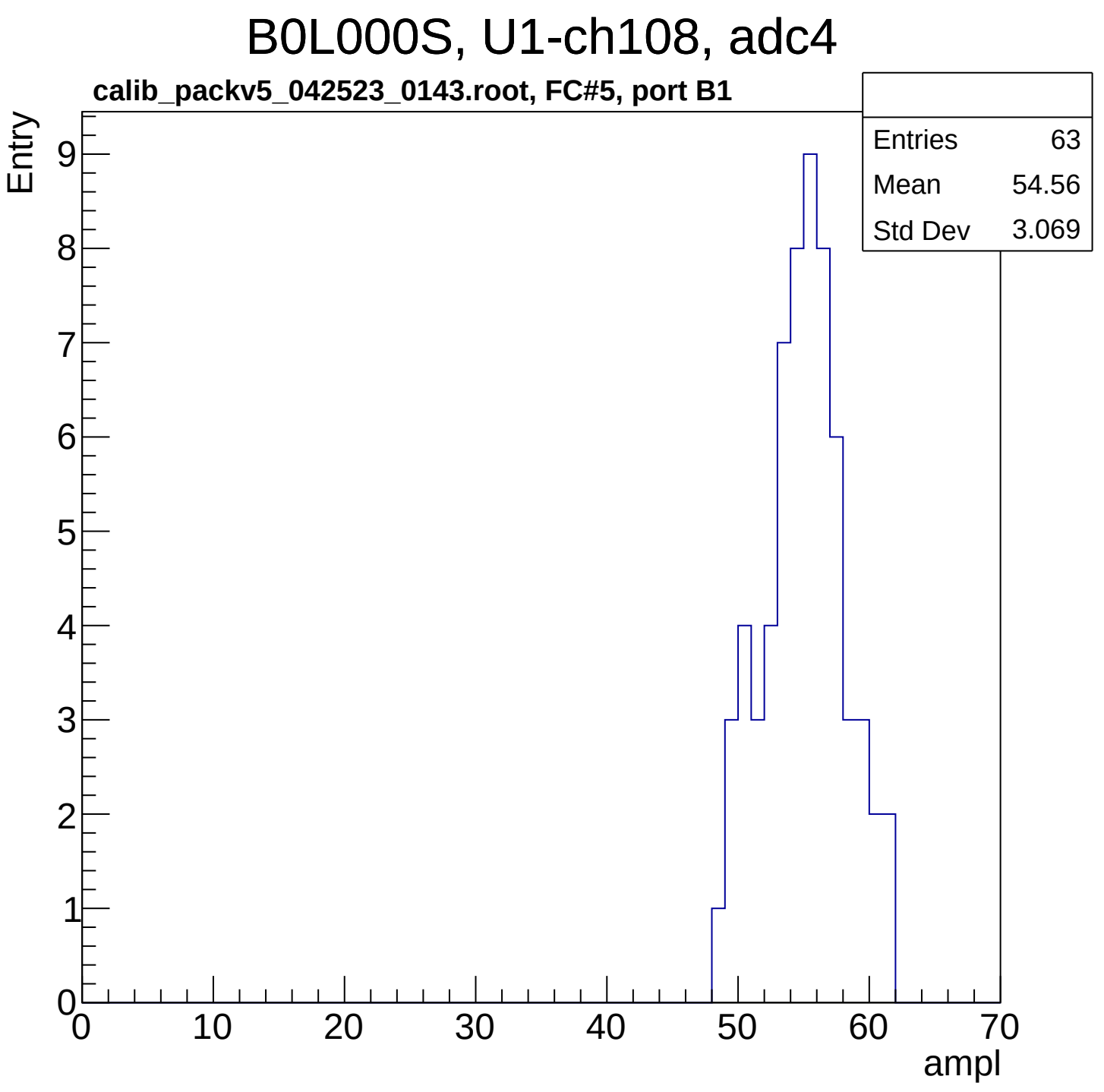
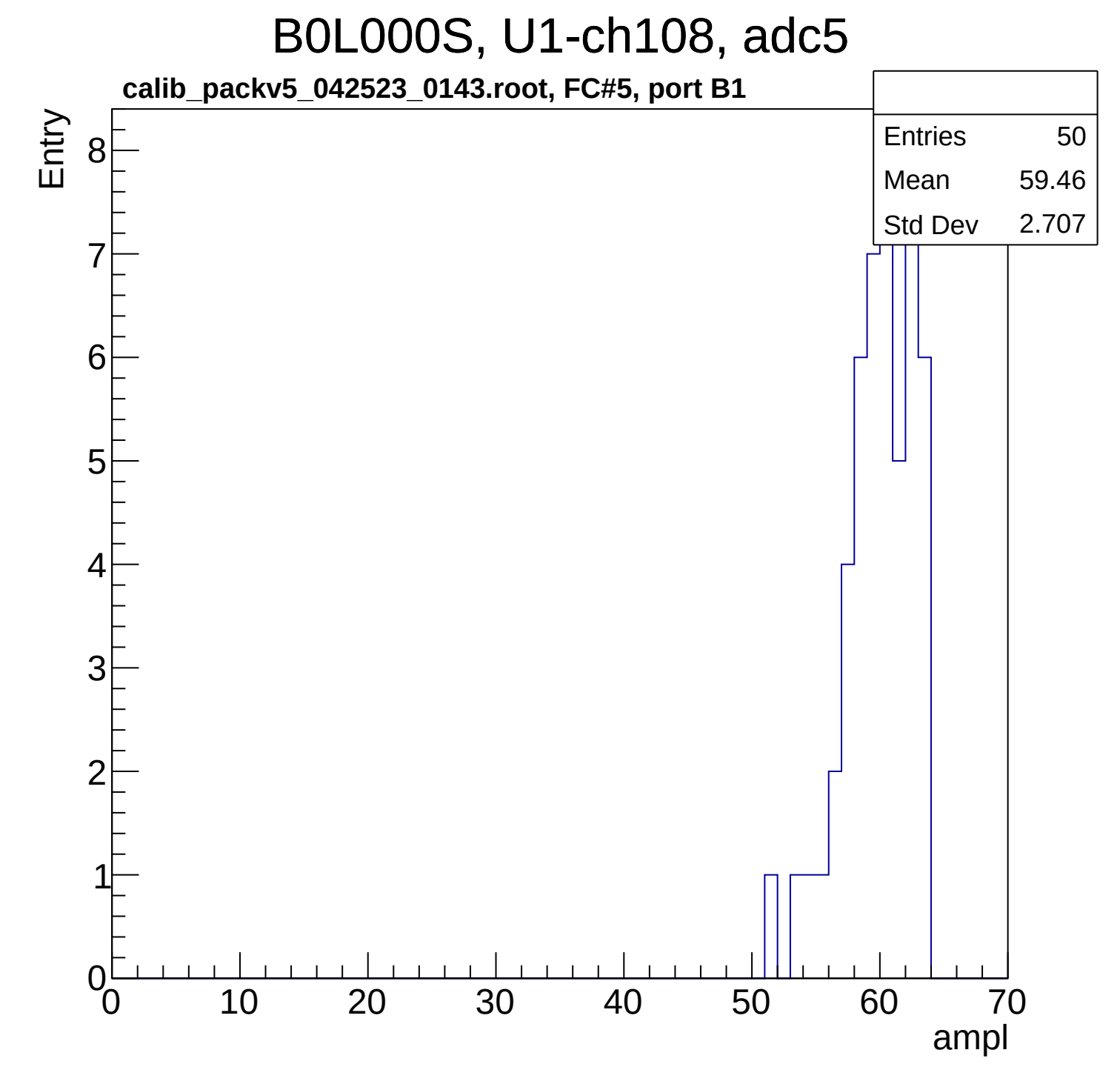

B0L000S, U1-ch108, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.69
Std Dev 3.322
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch108, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.56
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch108, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.46
Std Dev 2.707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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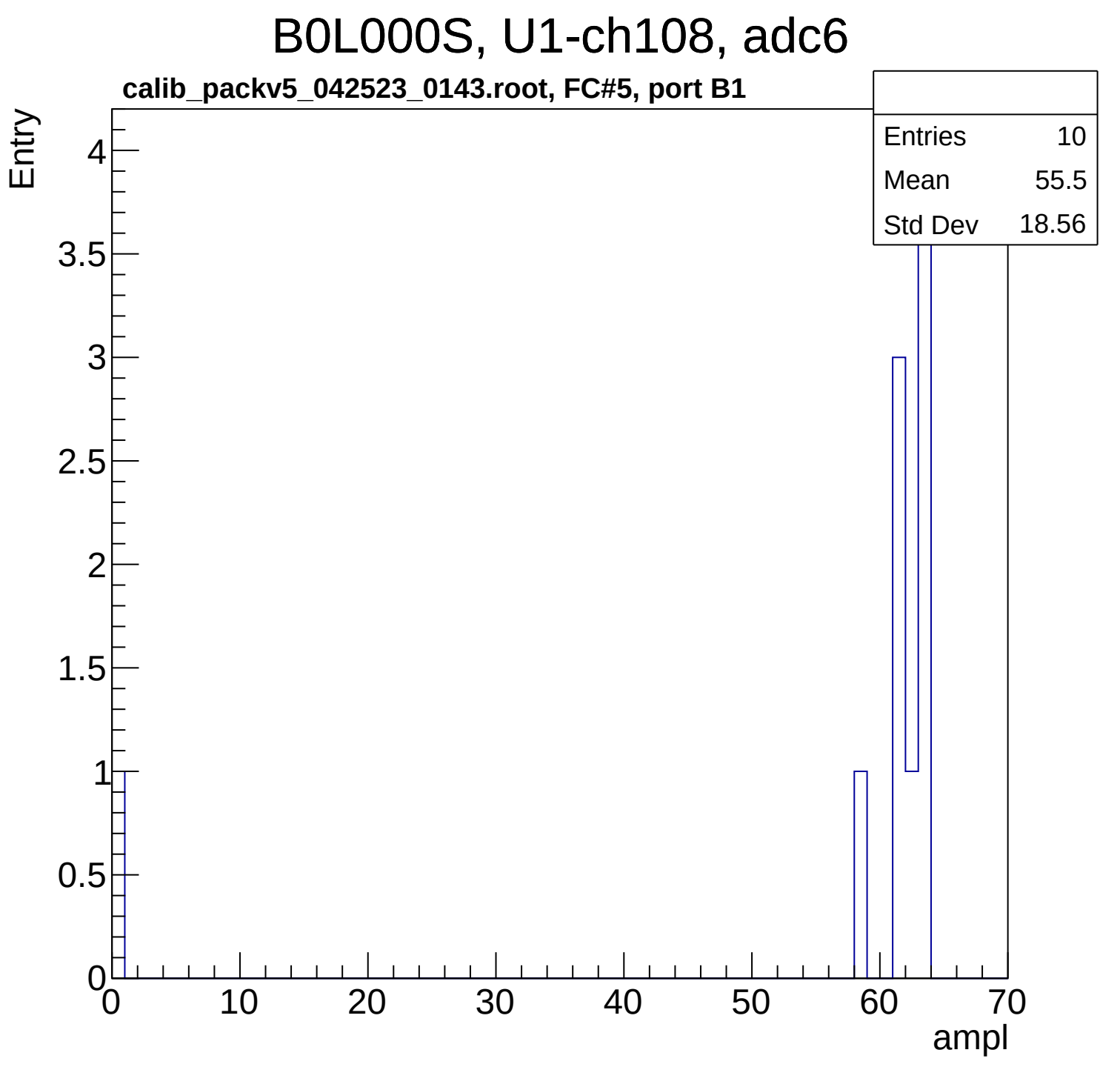
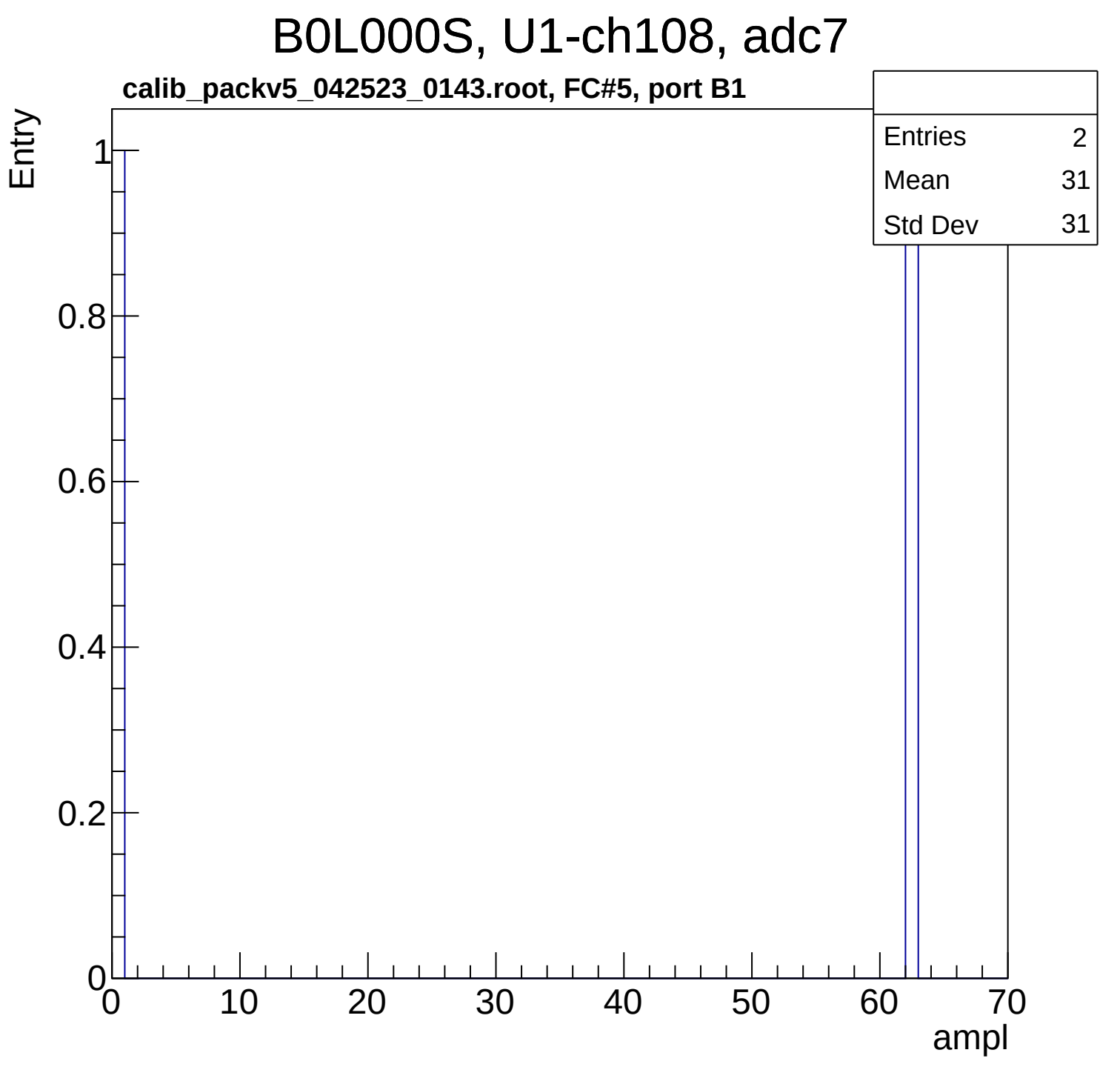
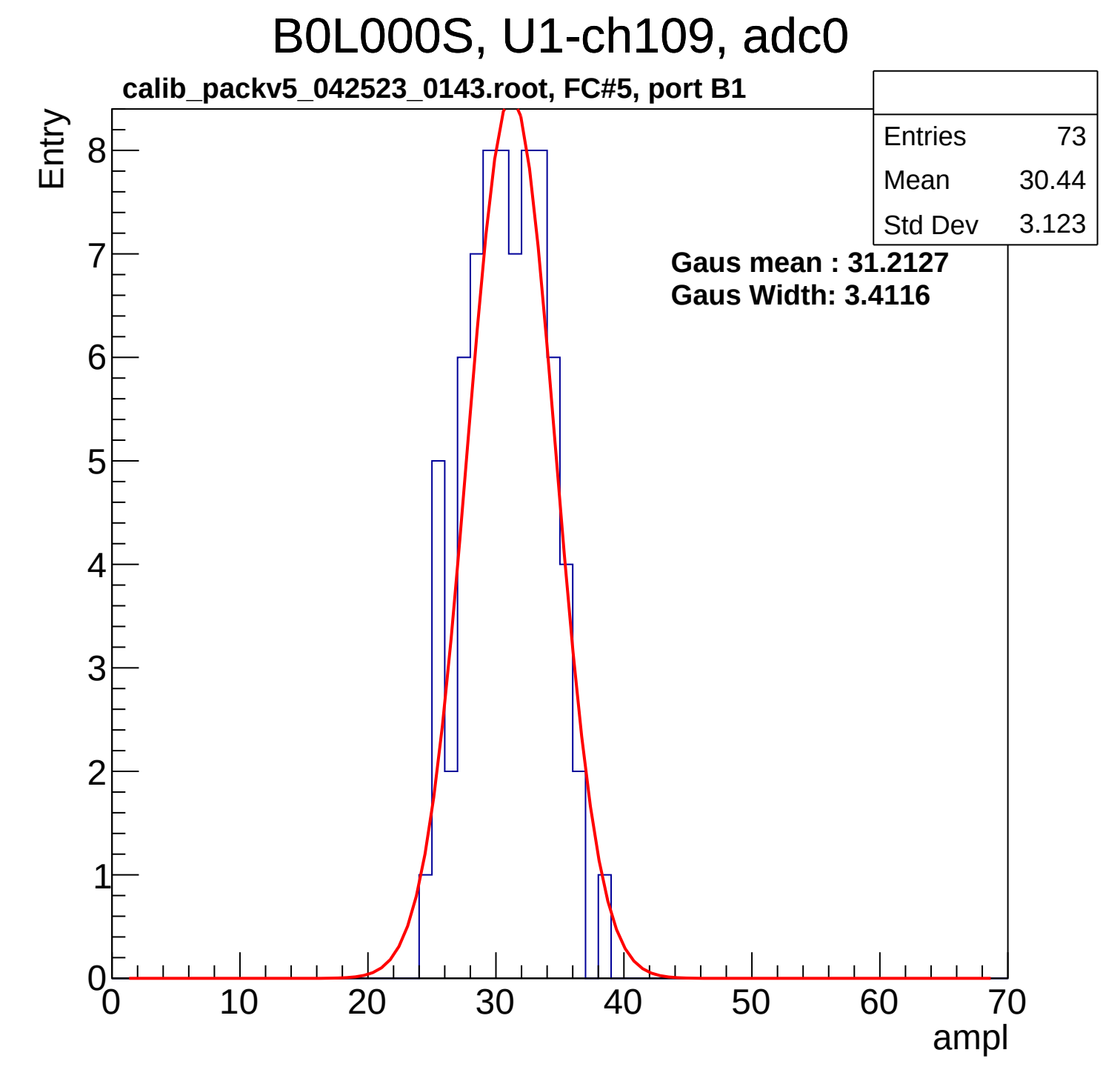
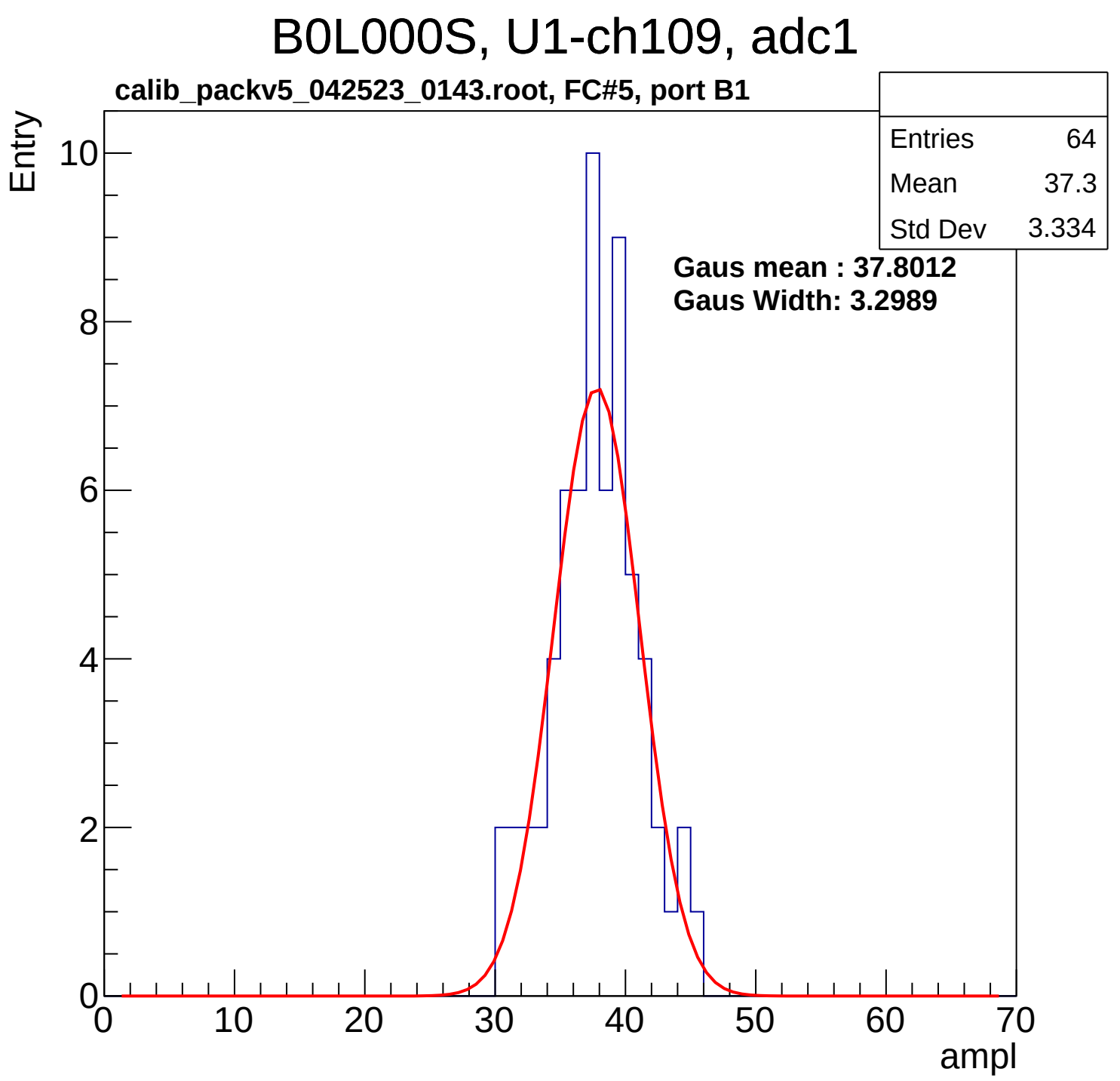
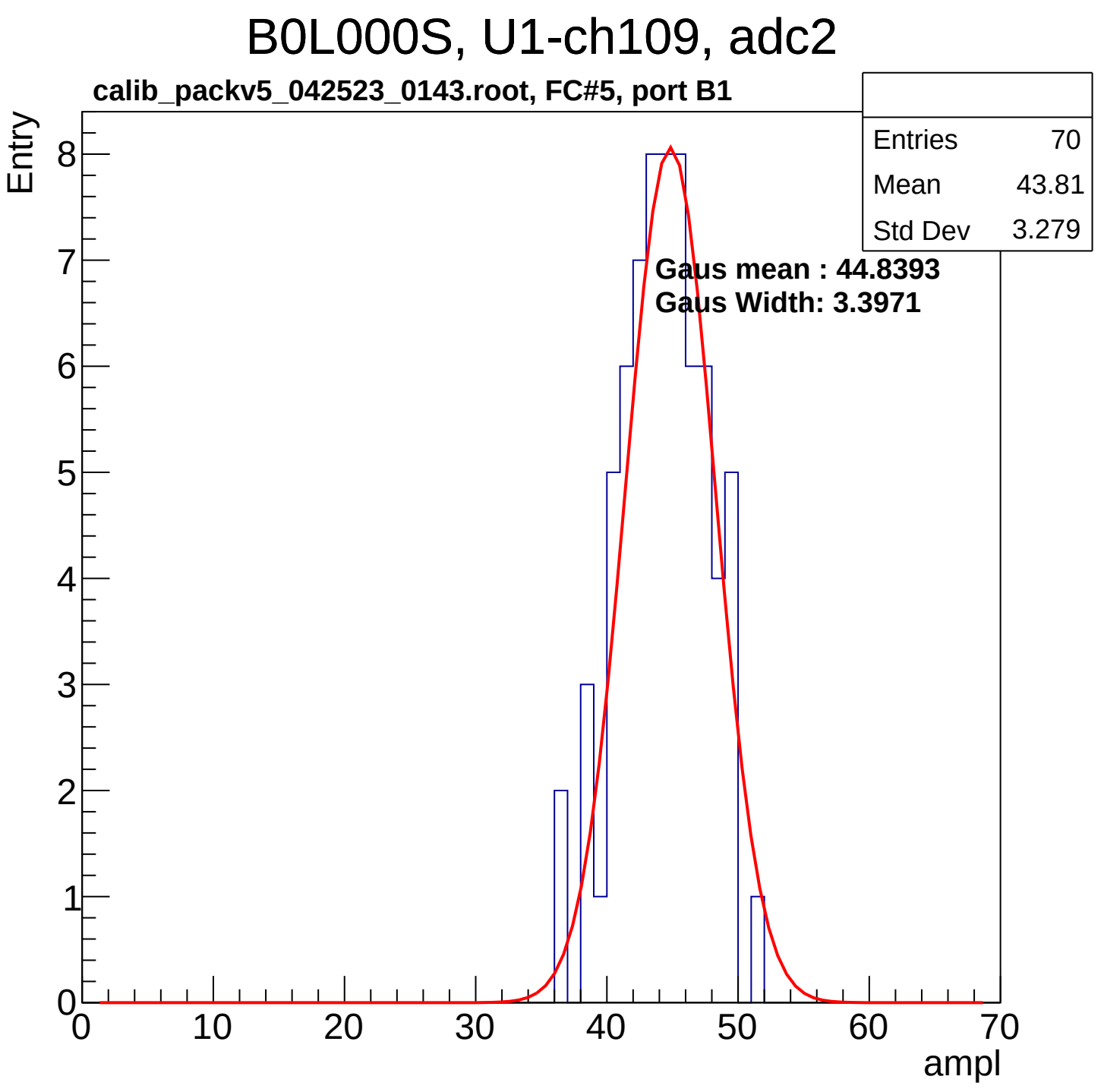
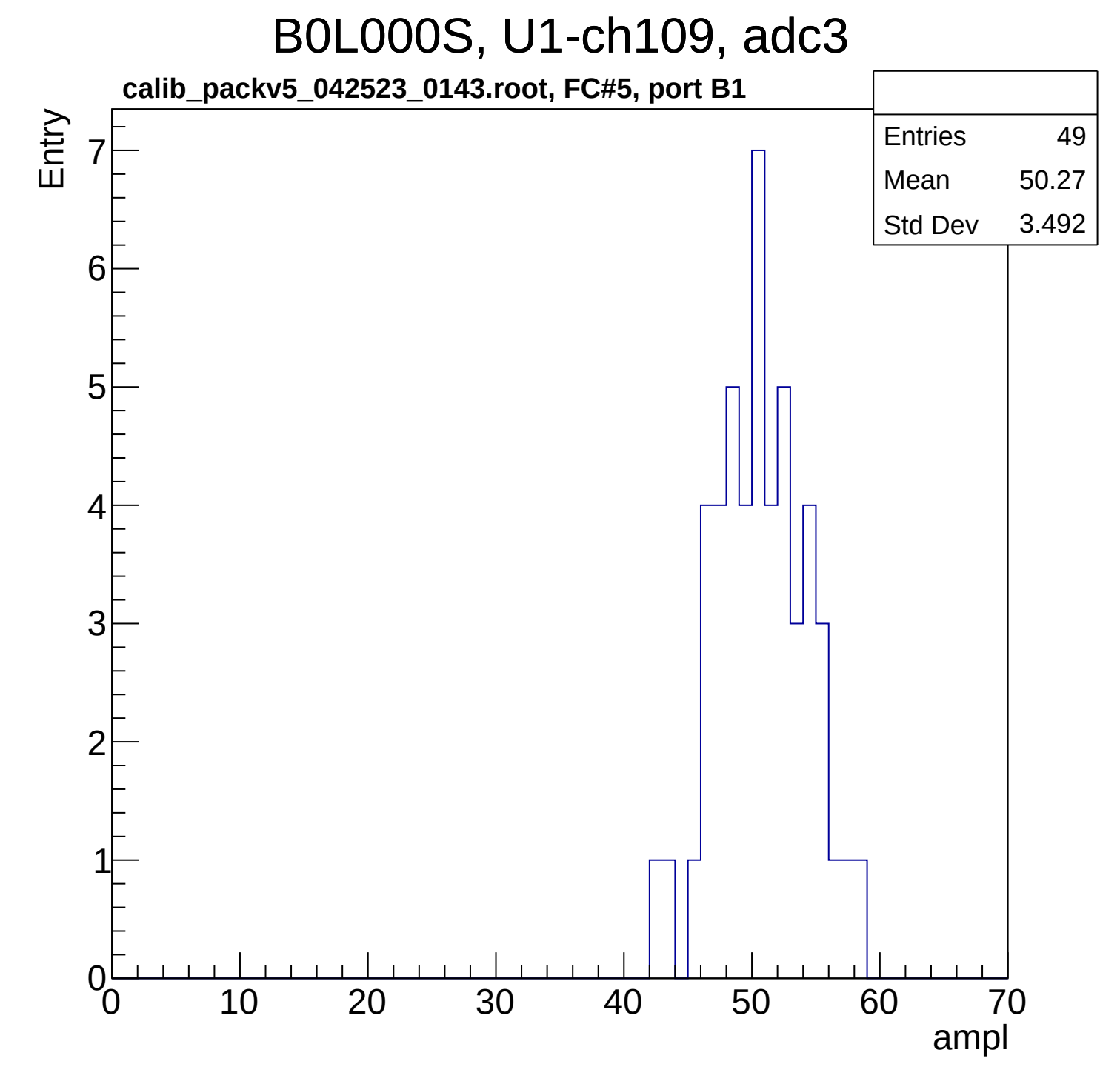

B0L000S, U1-ch108, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.5
Std Dev 18.56
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

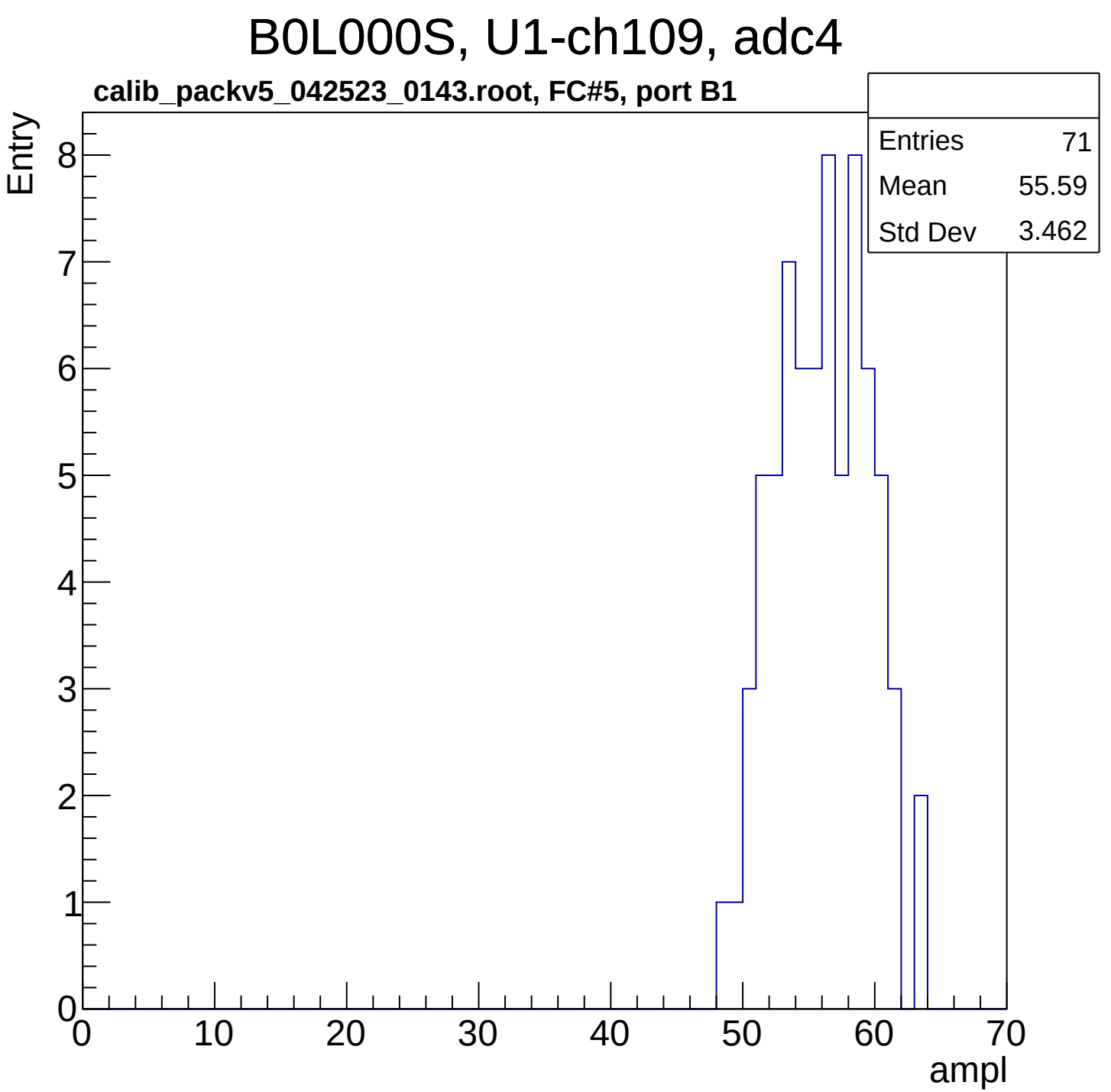

B0L000S, U1-ch108, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

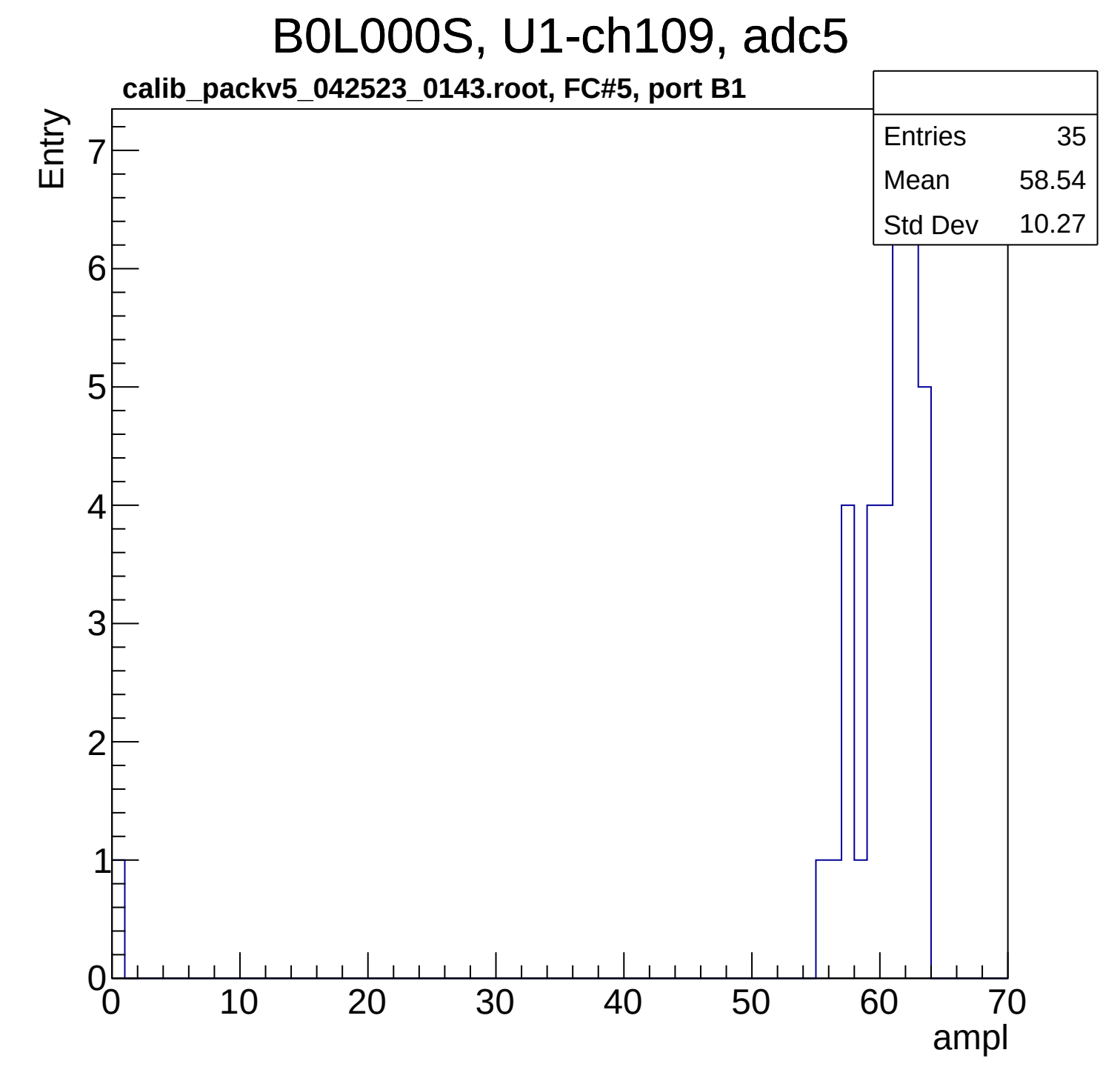

B0L000S, U1-ch109, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.44
Std Dev 3.123
Gaus mean : 31.2127
Gaus Width: 3.4116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

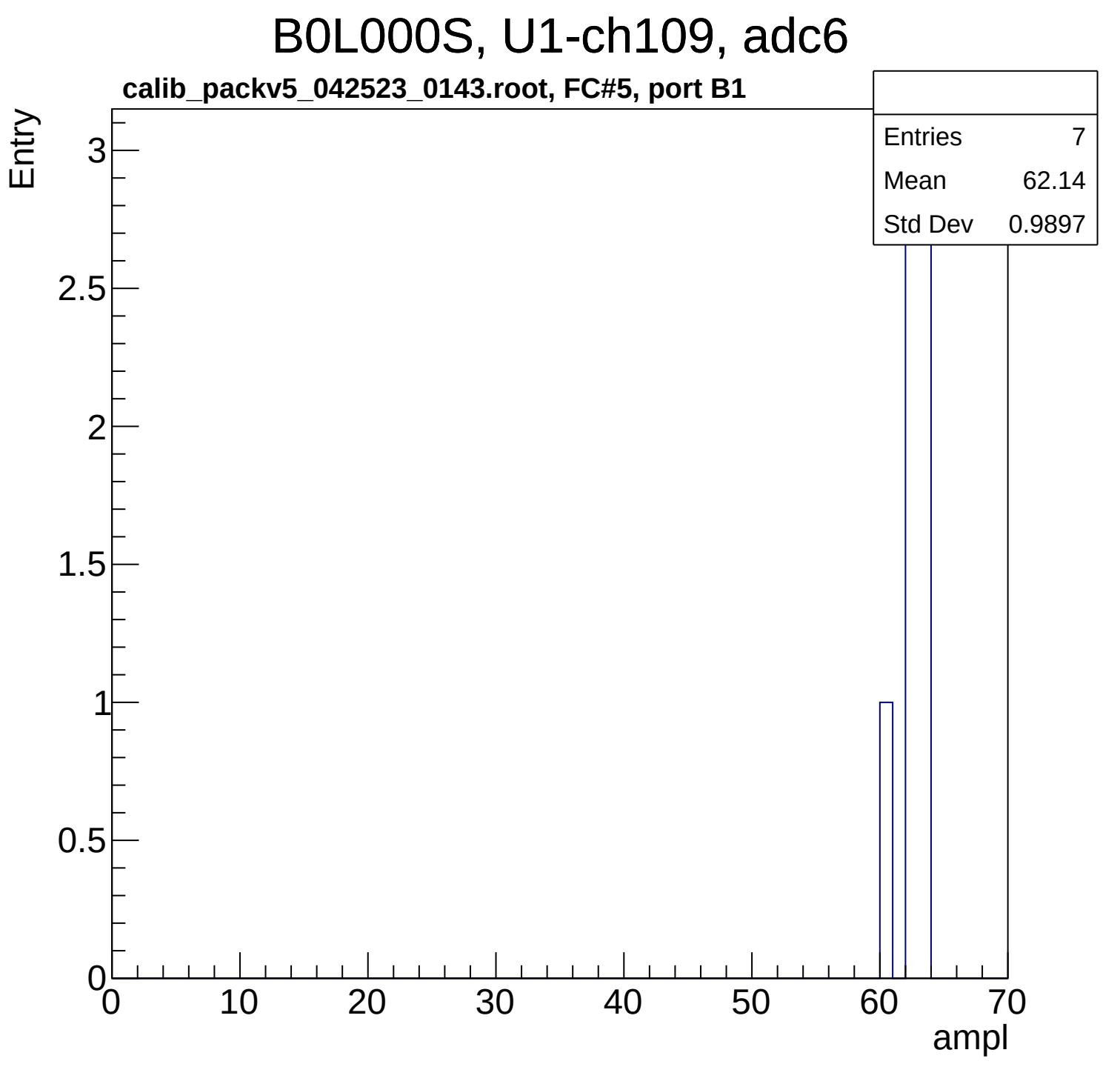

B0L000S, U1-ch109, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 37.3
Std Dev 3.334
Gaus mean : 37.8012
Gaus Width: 3.2989
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

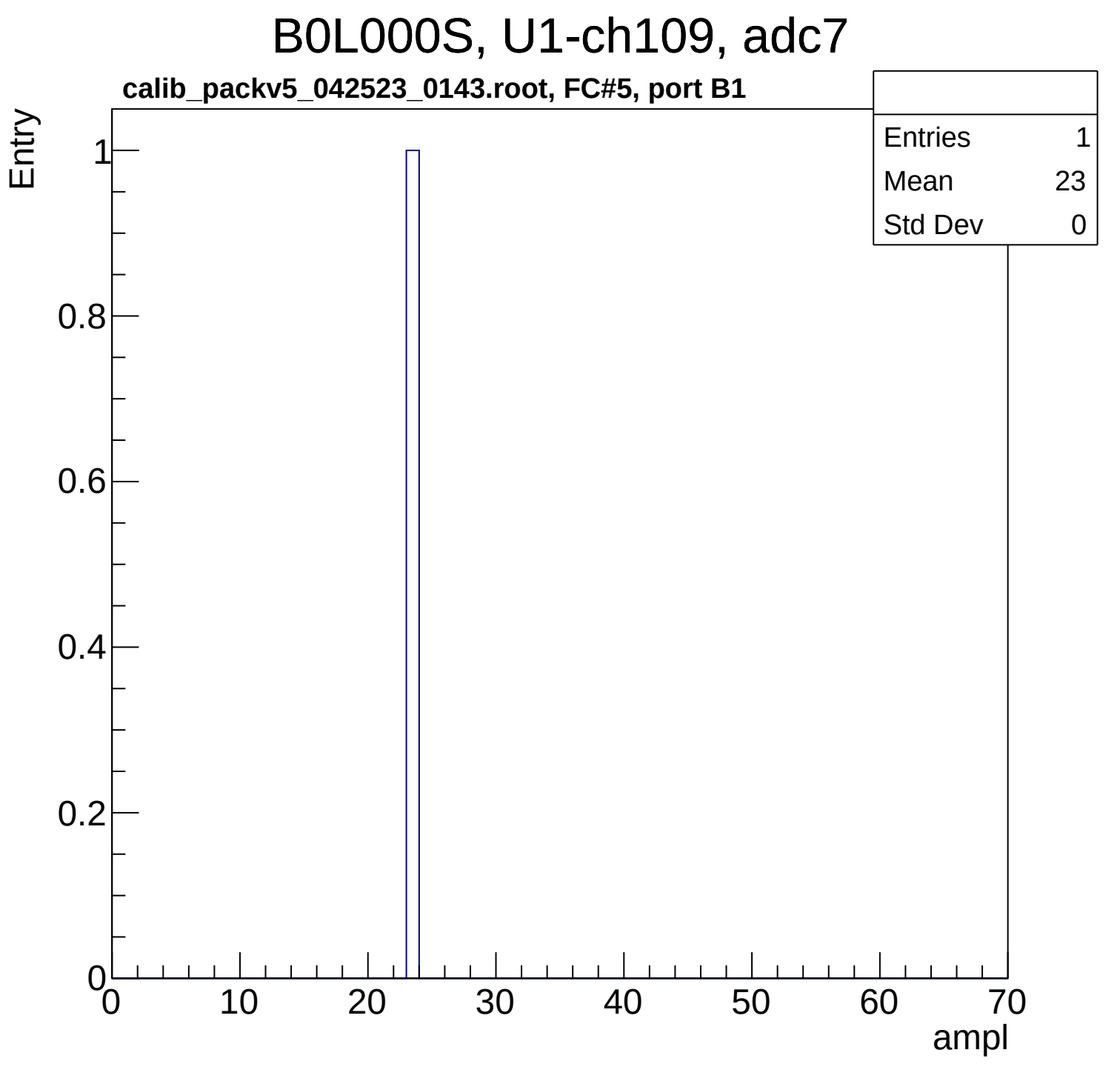

B0L000S, U1-ch109, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 43.81
Std Dev 3.279
Gaus mean : 44.8393
Gaus Width: 3.3971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch109, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 50.27
Std Dev 3.492
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch109, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 55.59
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch109, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.54
Std Dev 10.27
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch109, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch109, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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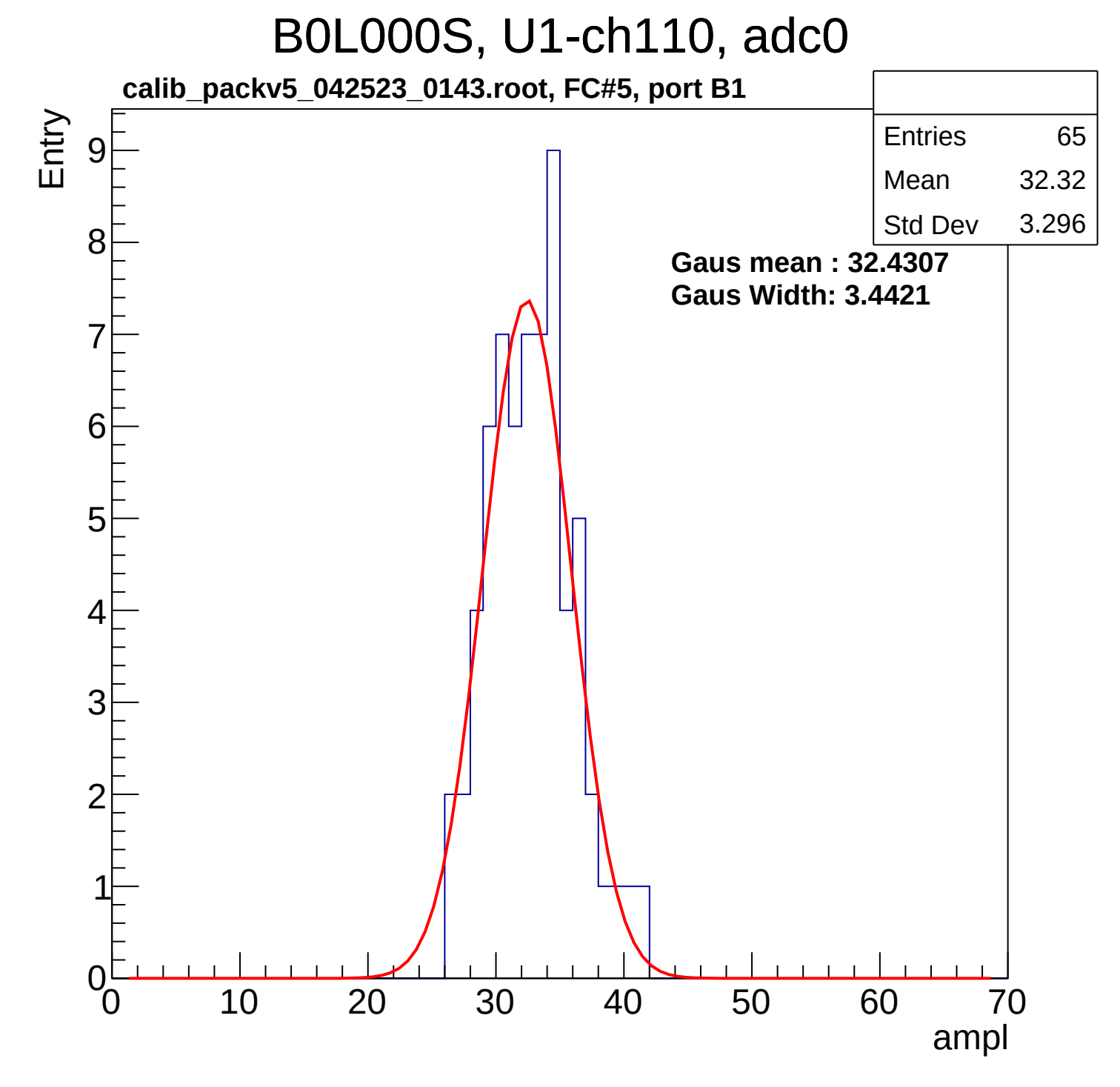
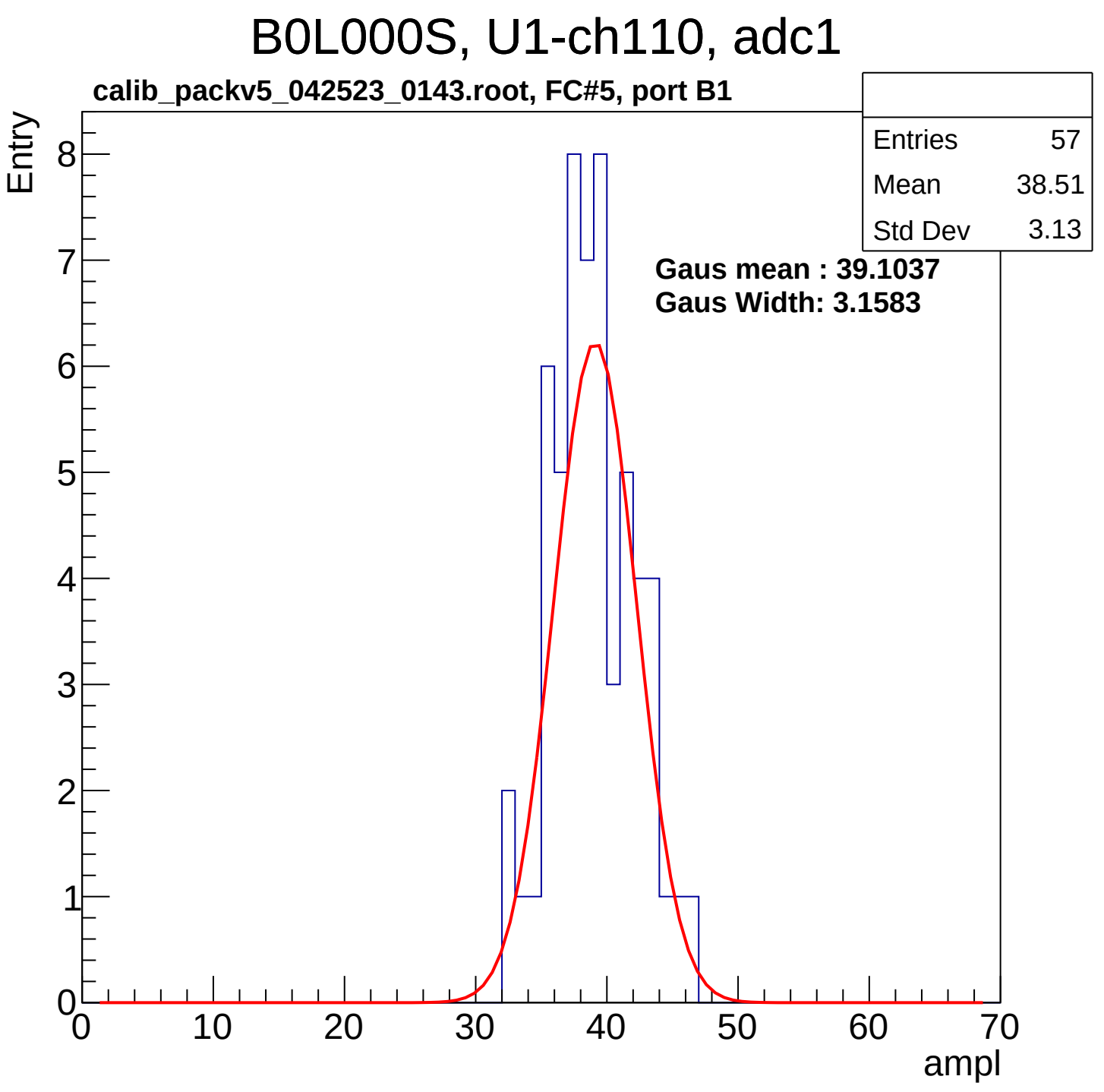
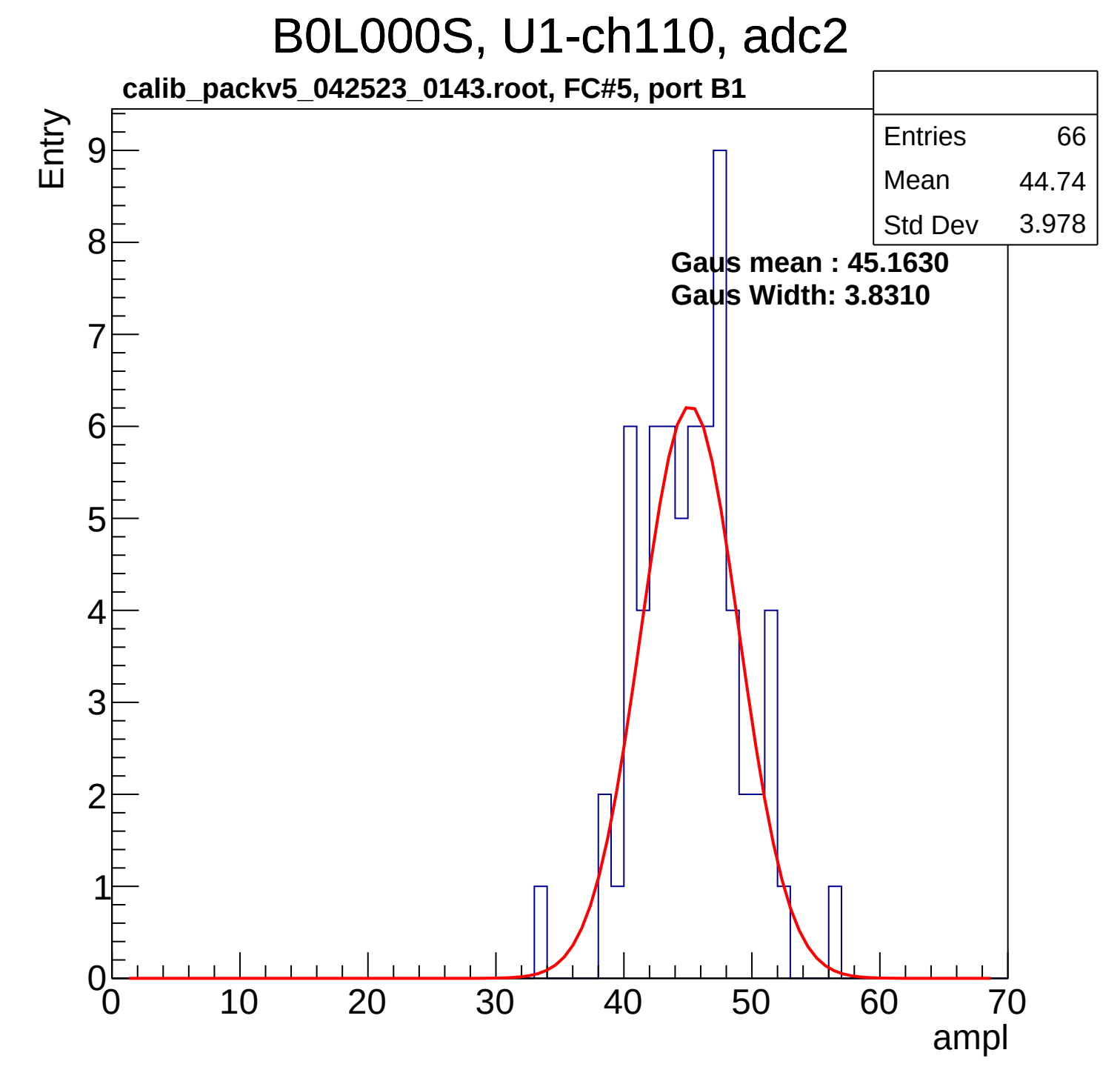
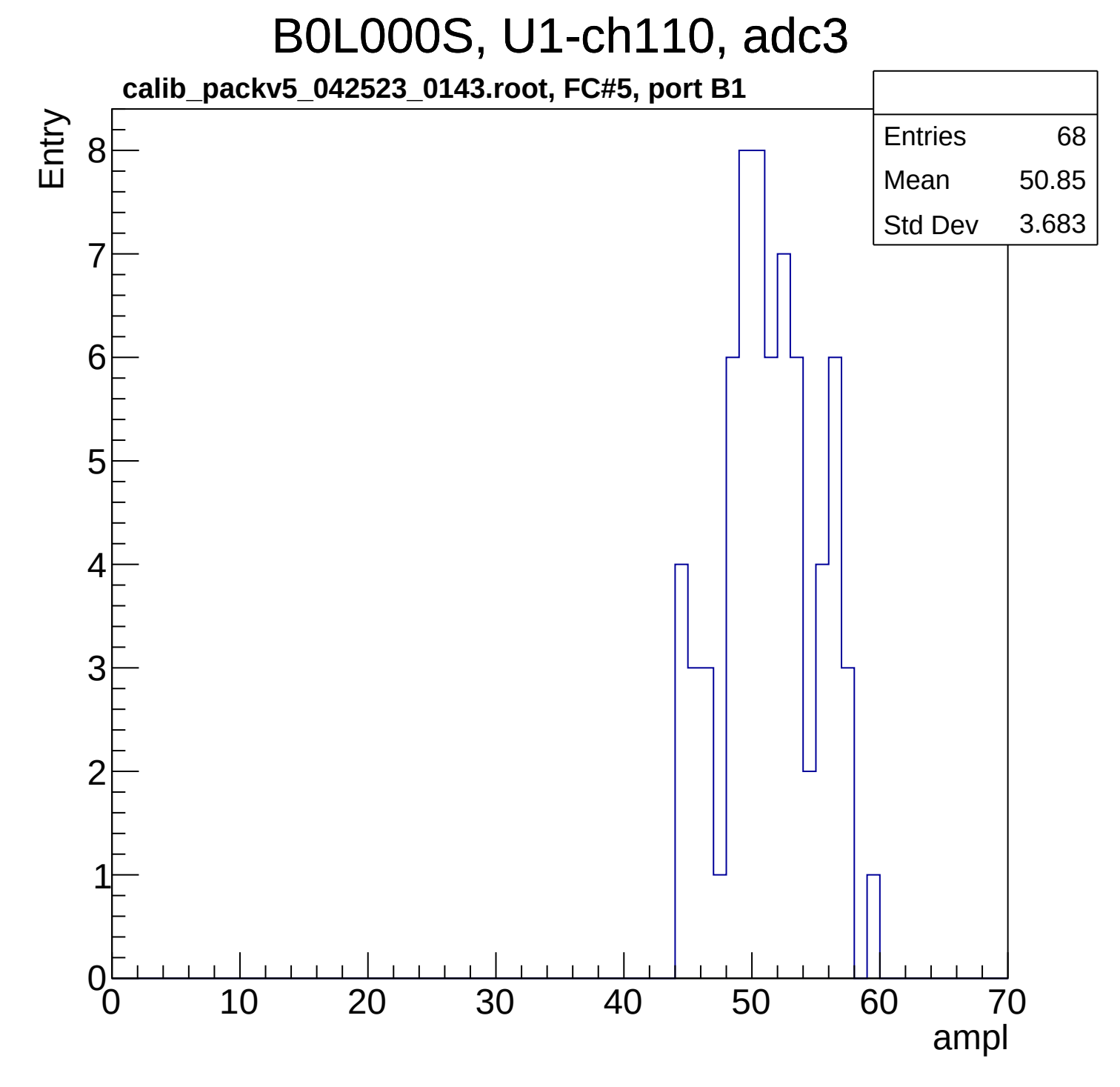
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch110, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 32.32
Std Dev 3.296
Gaus mean : 32.4307
Gaus Width: 3.4421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch110, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 38.51
Std Dev 3.13
Gaus mean : 39.1037
Gaus Width: 3.1583
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch110, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 44.74
Std Dev 3.978
Gaus mean : 45.1630
Gaus Width: 3.8310
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch110, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 50.85
Std Dev 3.683
Entry
ampl
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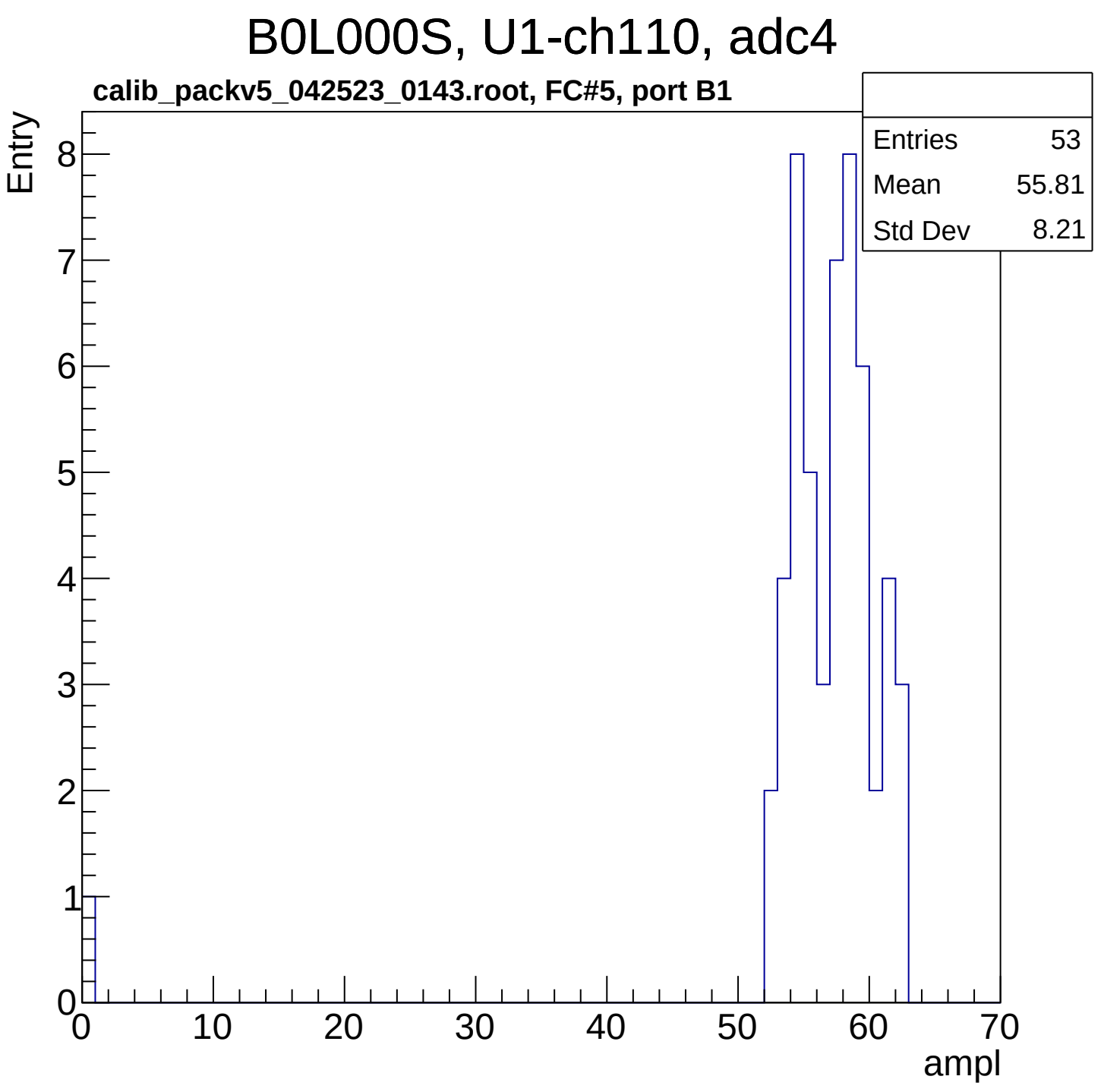
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch110, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.81
Std Dev 8.21
Entry
ampl
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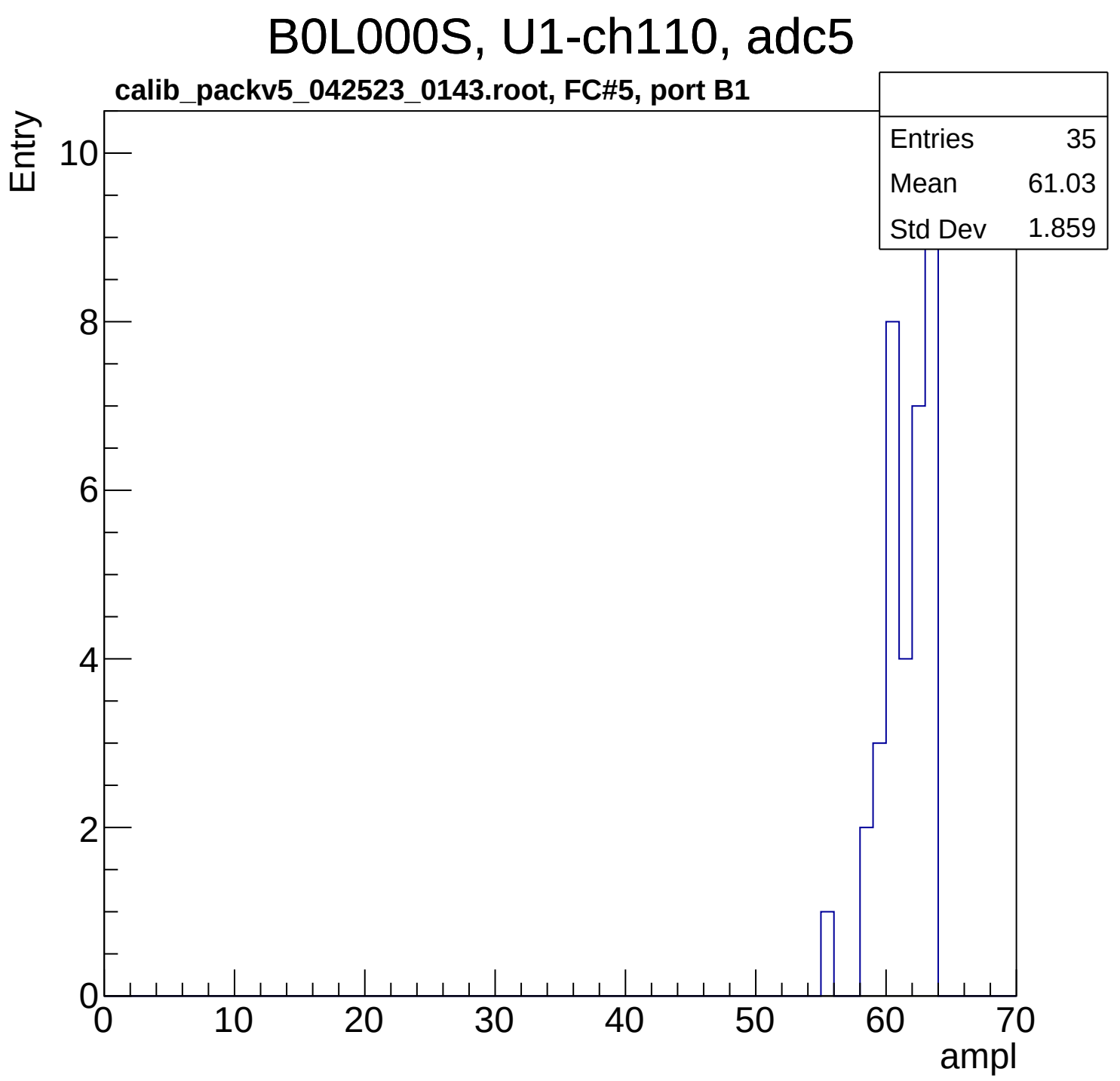
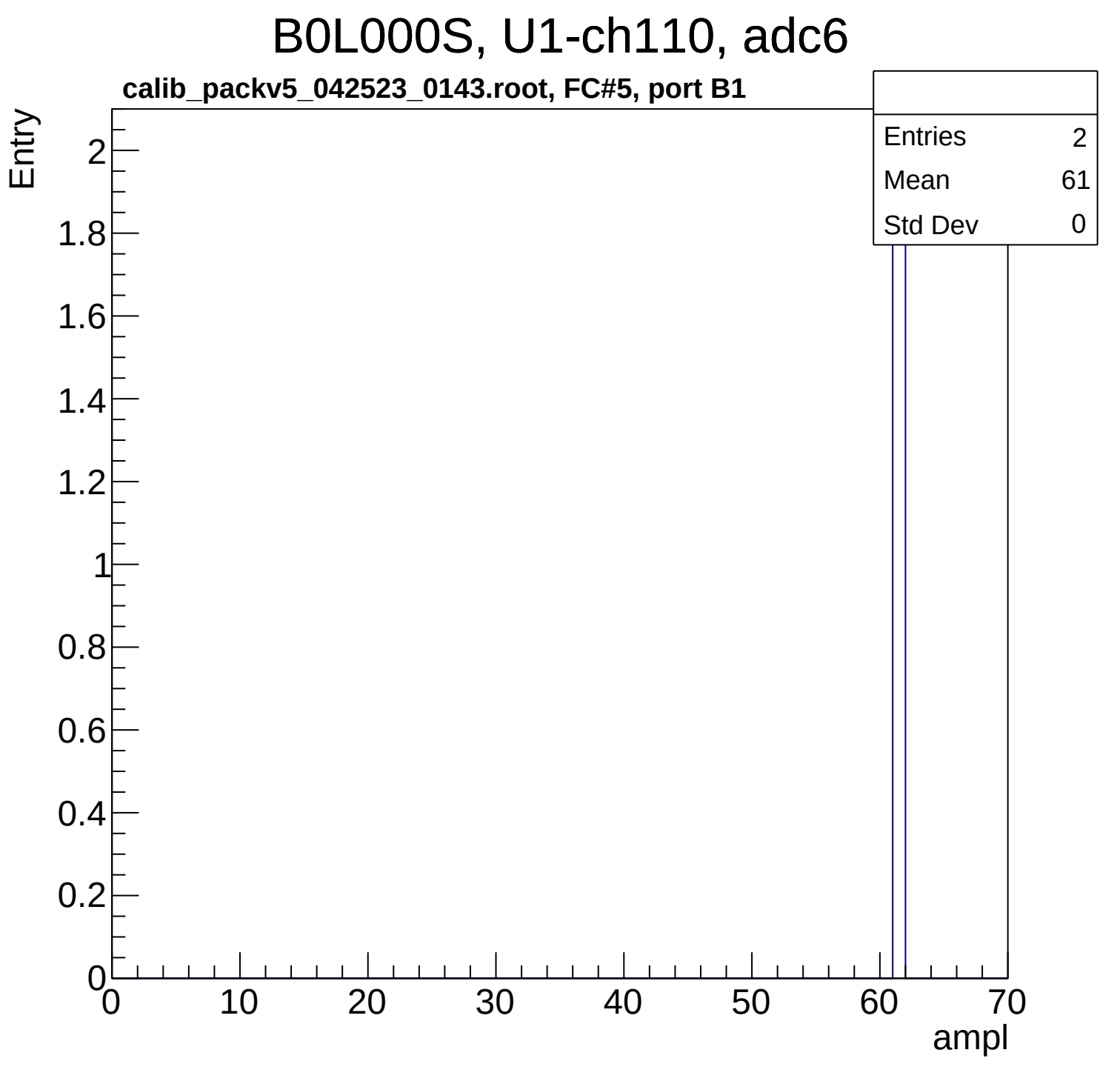
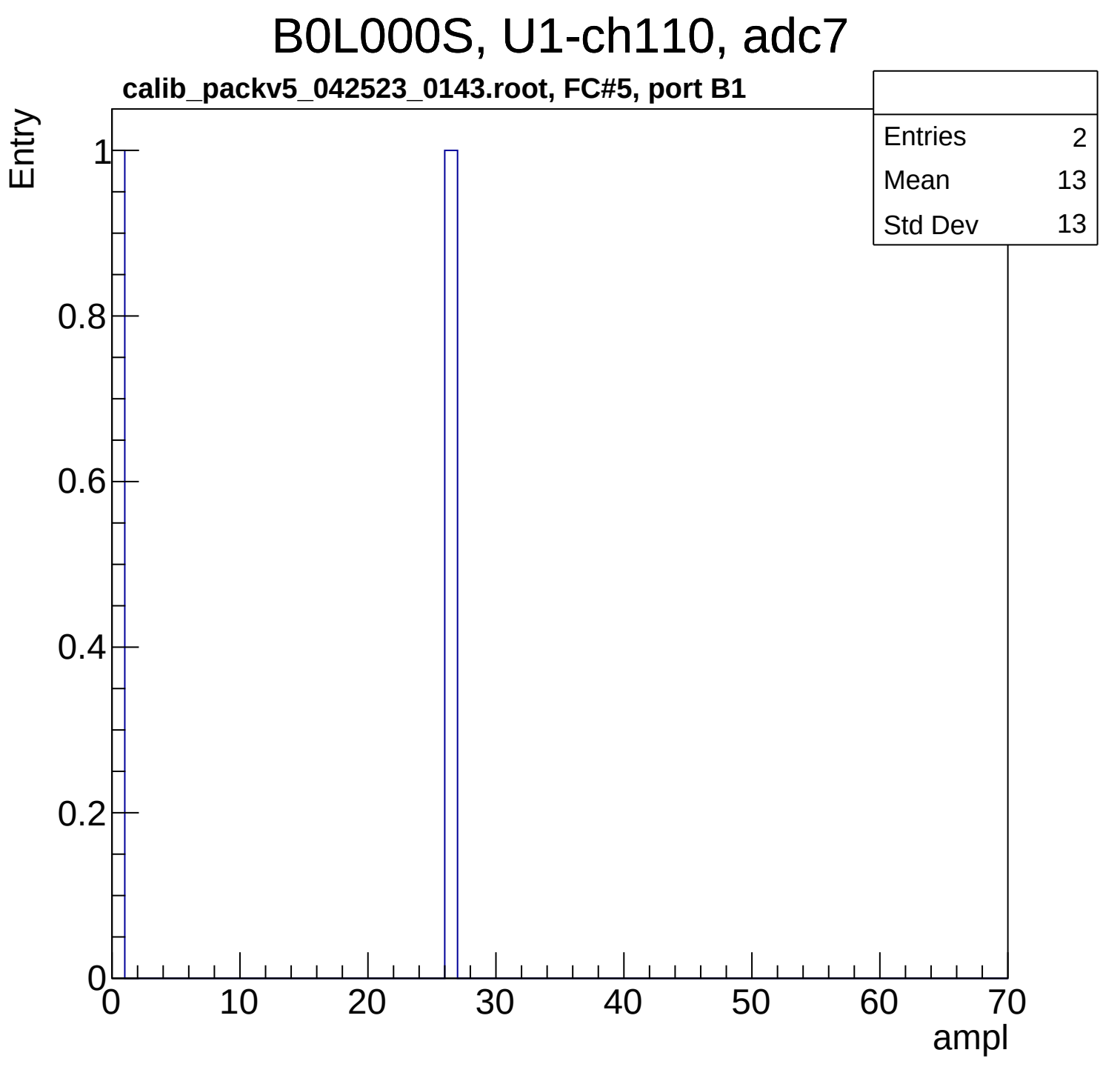
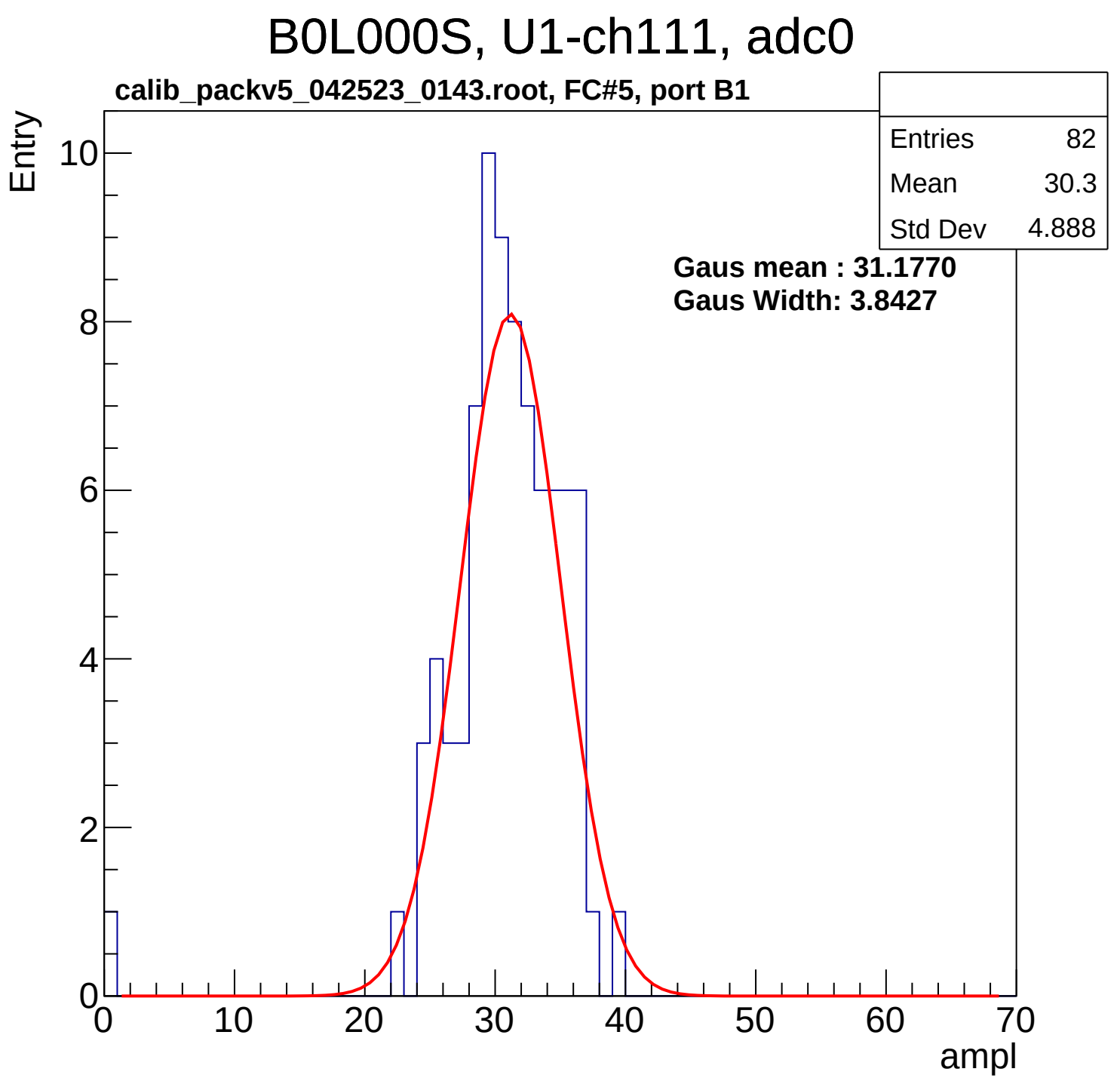
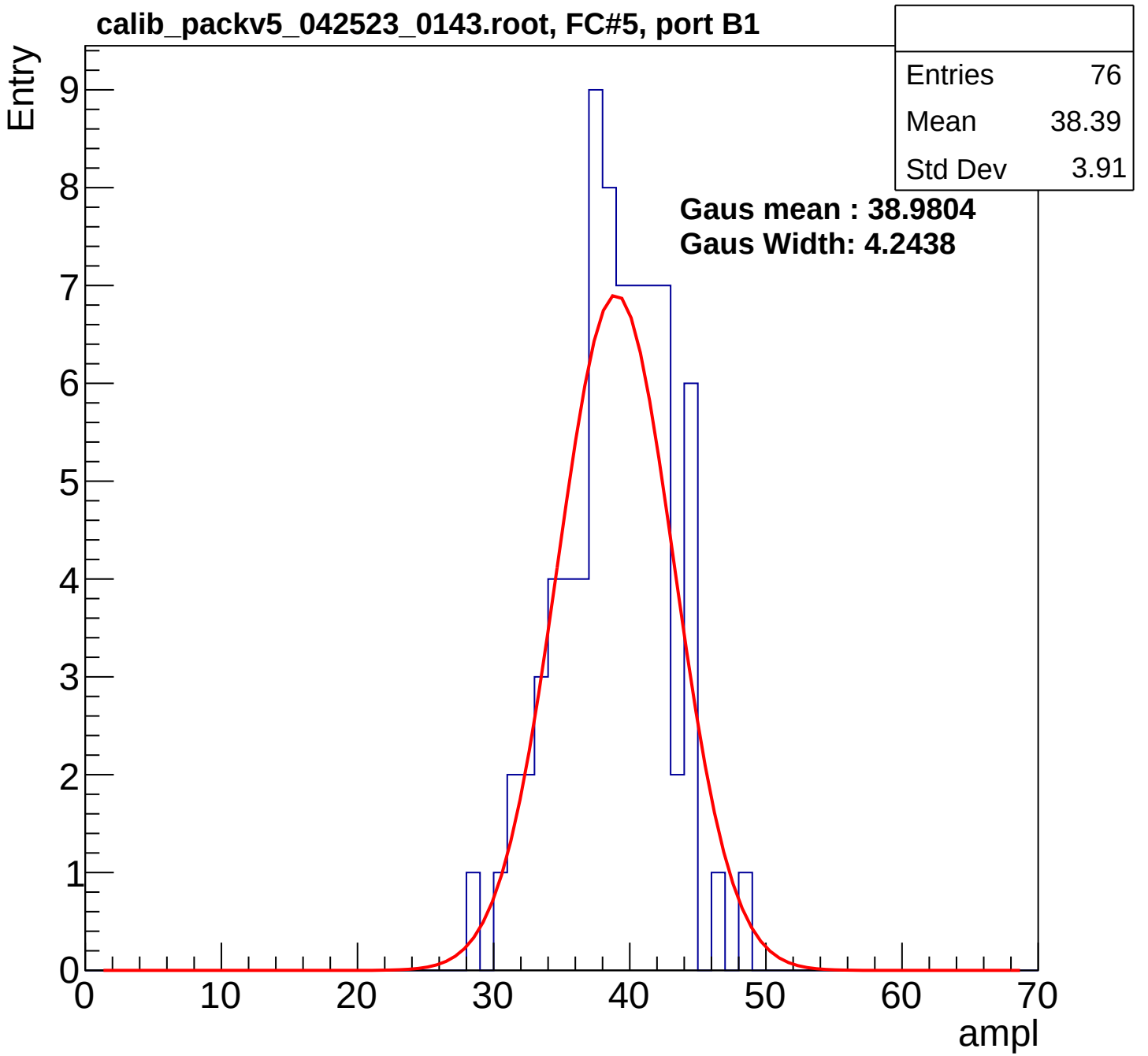
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

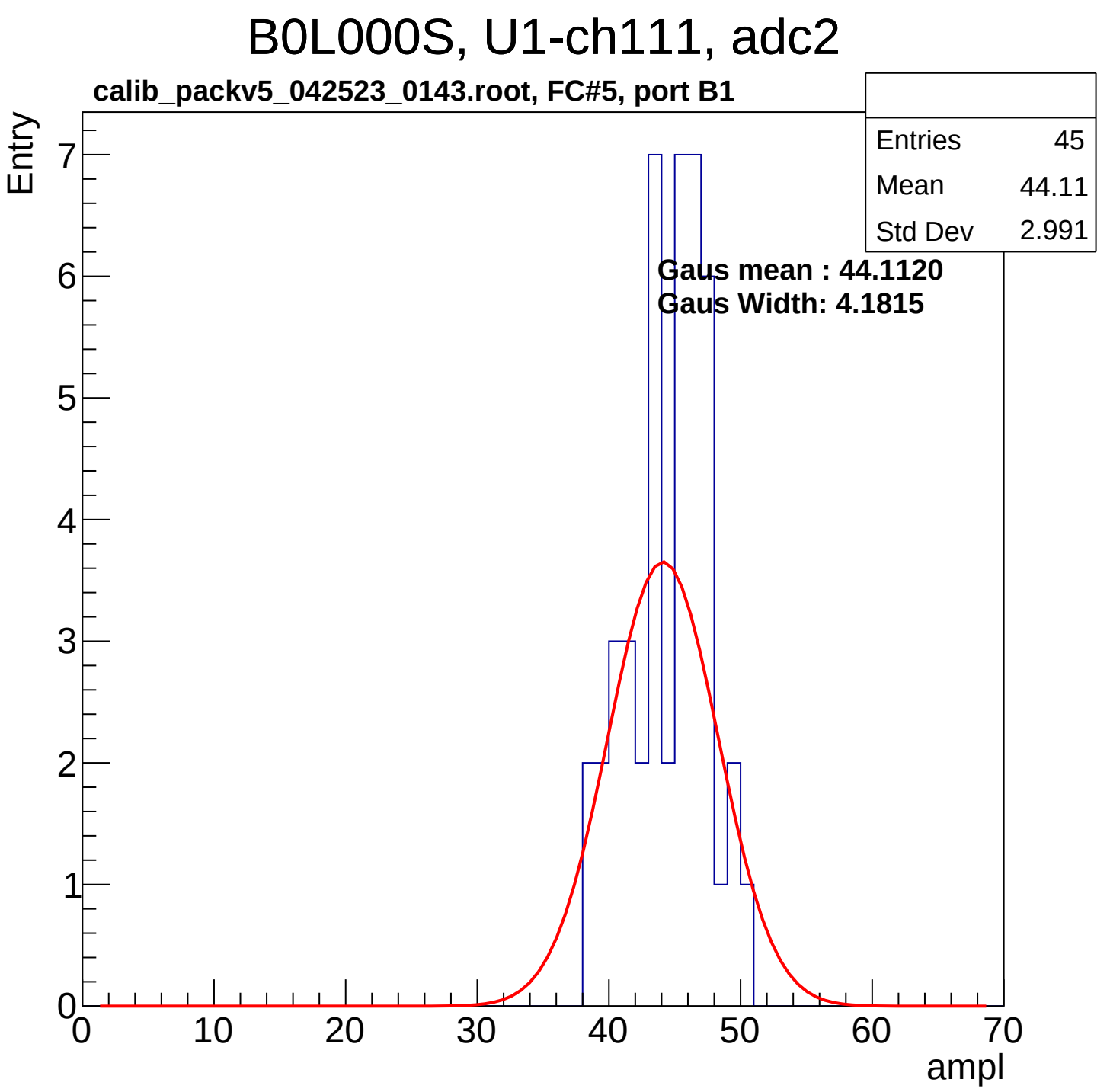

B0L000S, U1-ch110, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 61.03
Std Dev 1.859
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

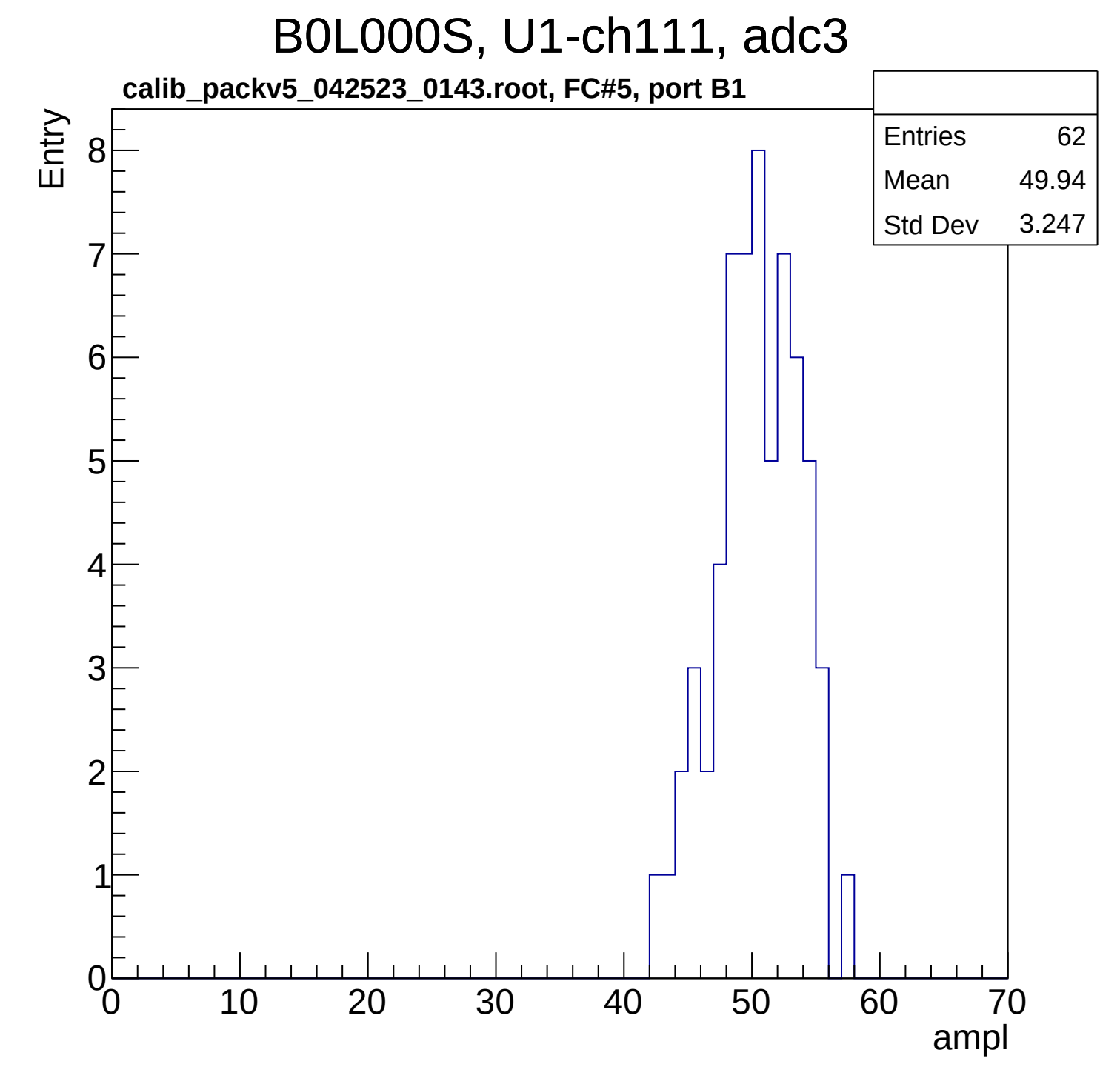

B0L000S, U1-ch110, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch110, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch111, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 30.3
Std Dev 4.888
Gaus mean : 31.1770
Gaus Width: 3.8427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch111, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 38.39
Std Dev 3.91
Gaus mean : 38.9804
Gaus Width: 4.2438
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch111, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 44.11
Std Dev 2.991
Gaus mean : 44.1120
Gaus Width: 4.1815
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch111, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.94
Std Dev 3.247
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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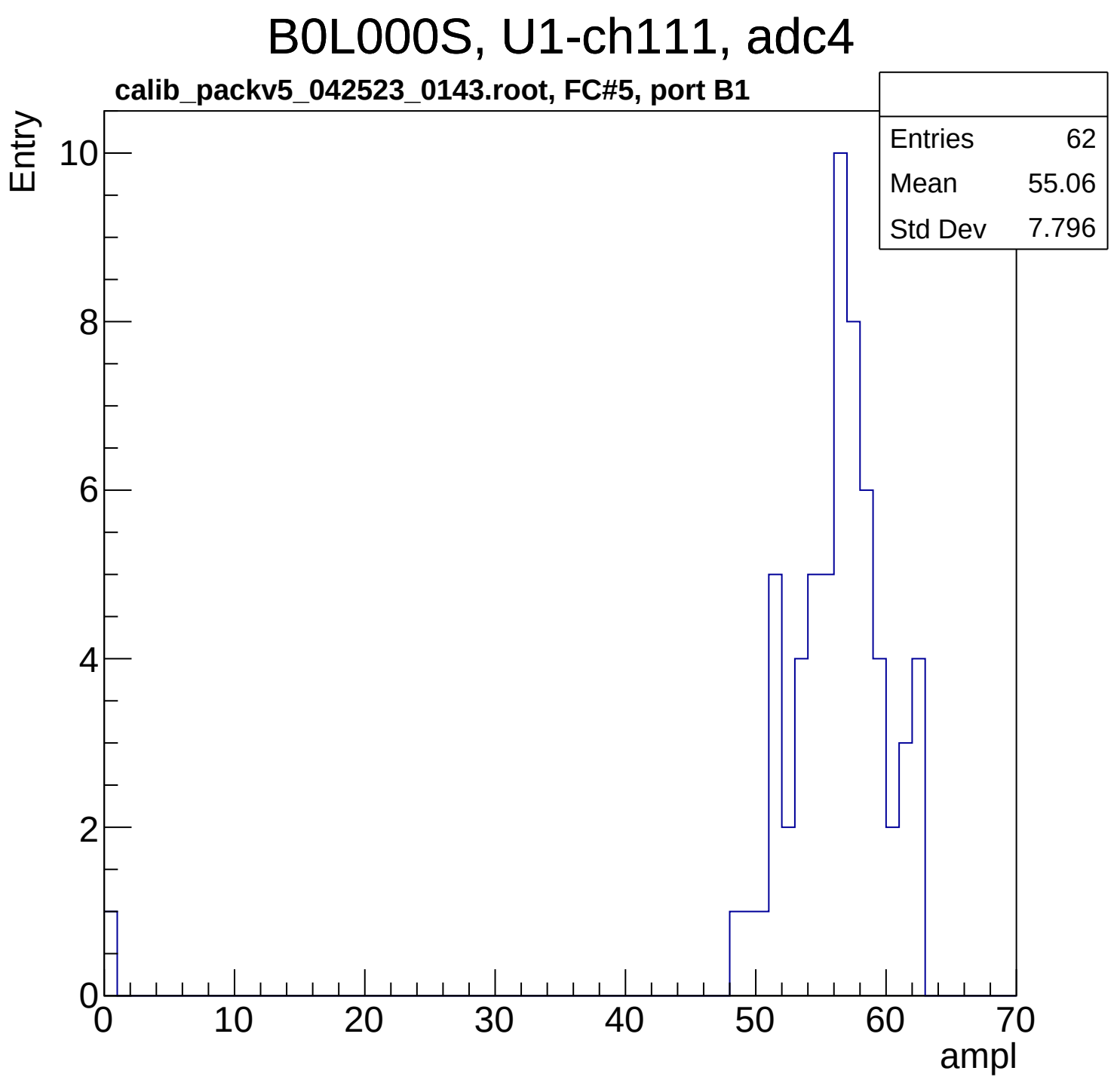
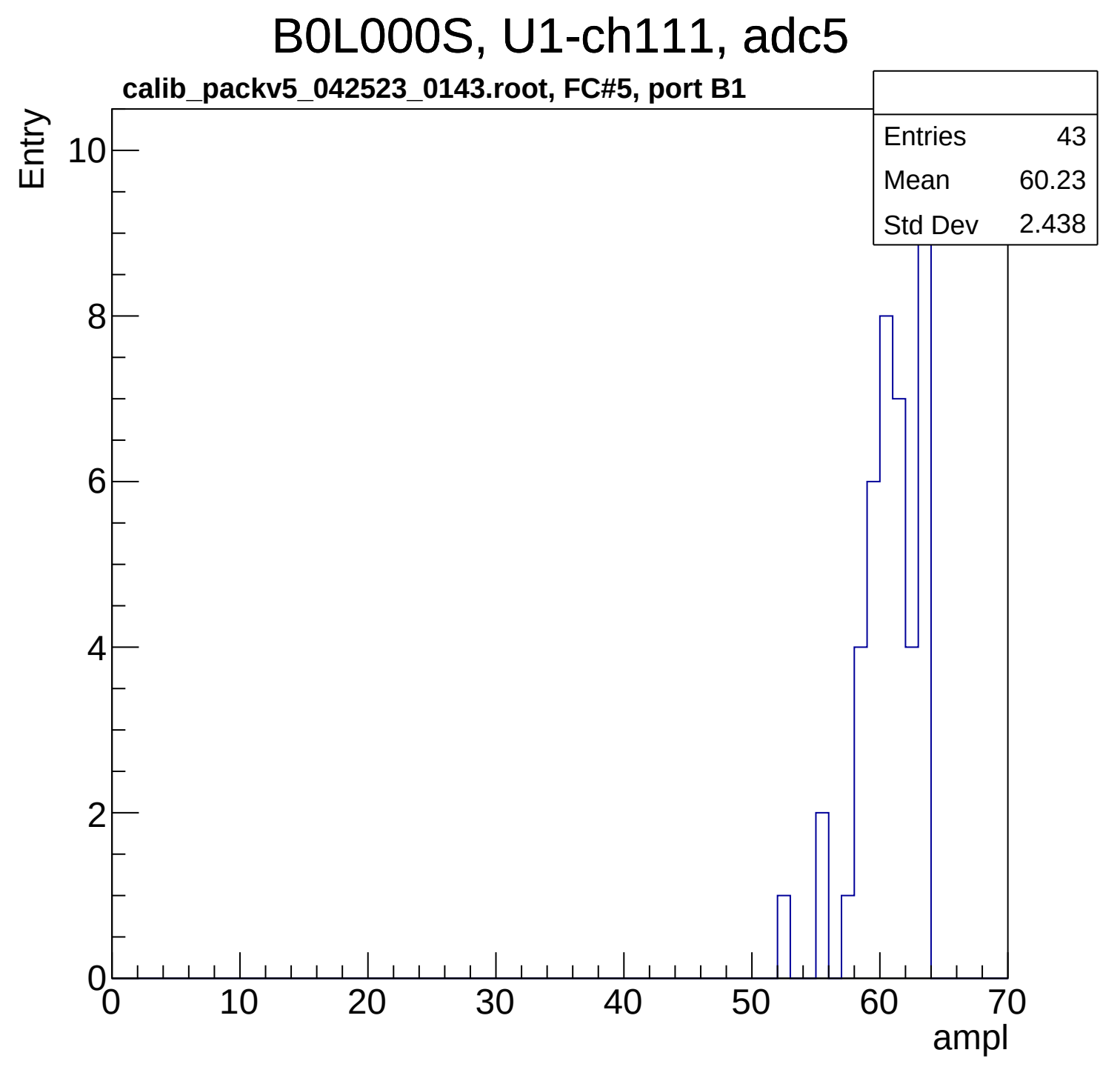
40
50
60
70


B0L000S, U1-ch111, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 55.06
Std Dev 7.796
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch111, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.23
Std Dev 2.438
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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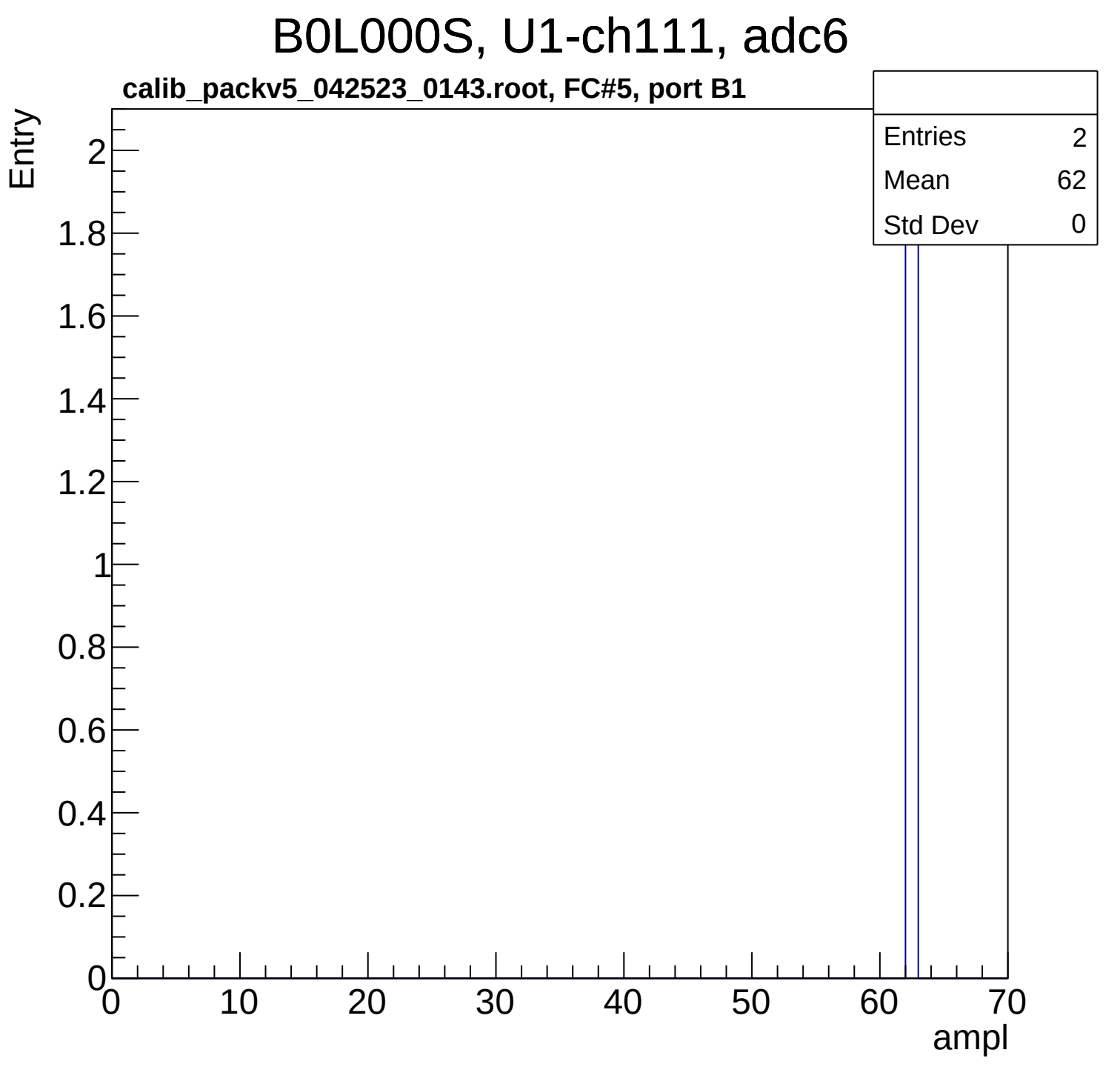
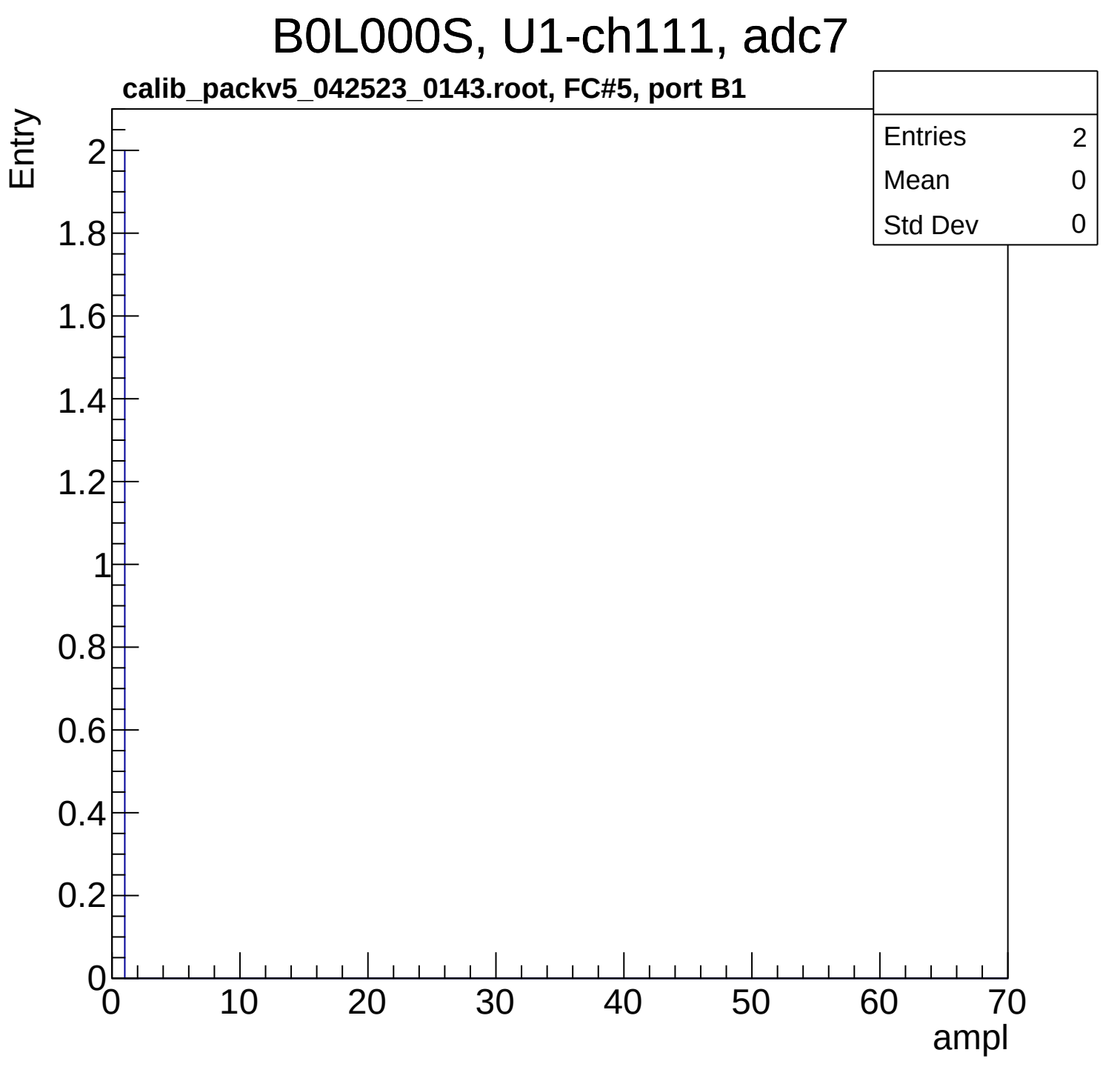
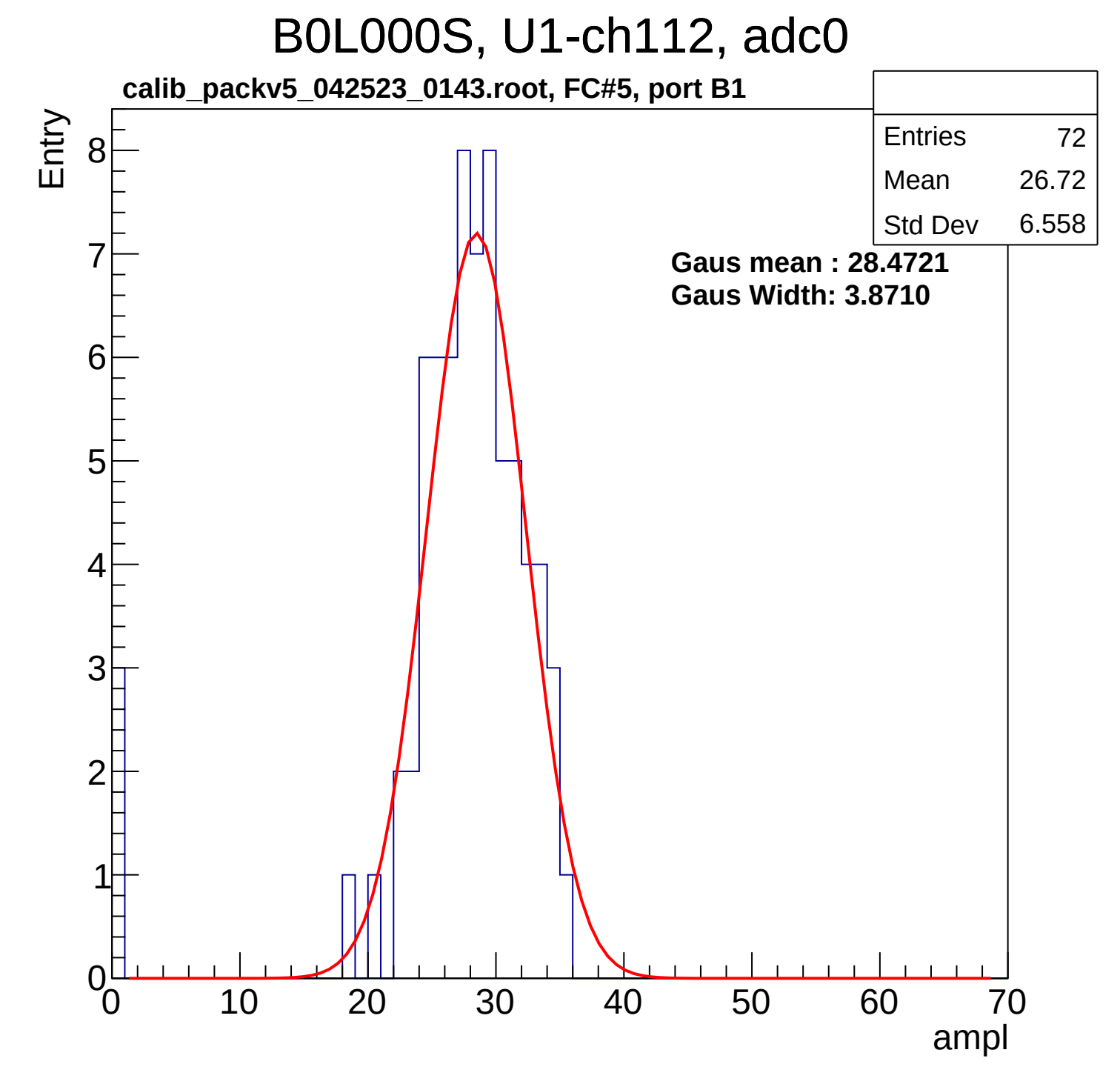

B0L000S, U1-ch111, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch111, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch112, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 26.72
Std Dev 6.558
Gaus mean : 28.4721
Gaus Width: 3.8710
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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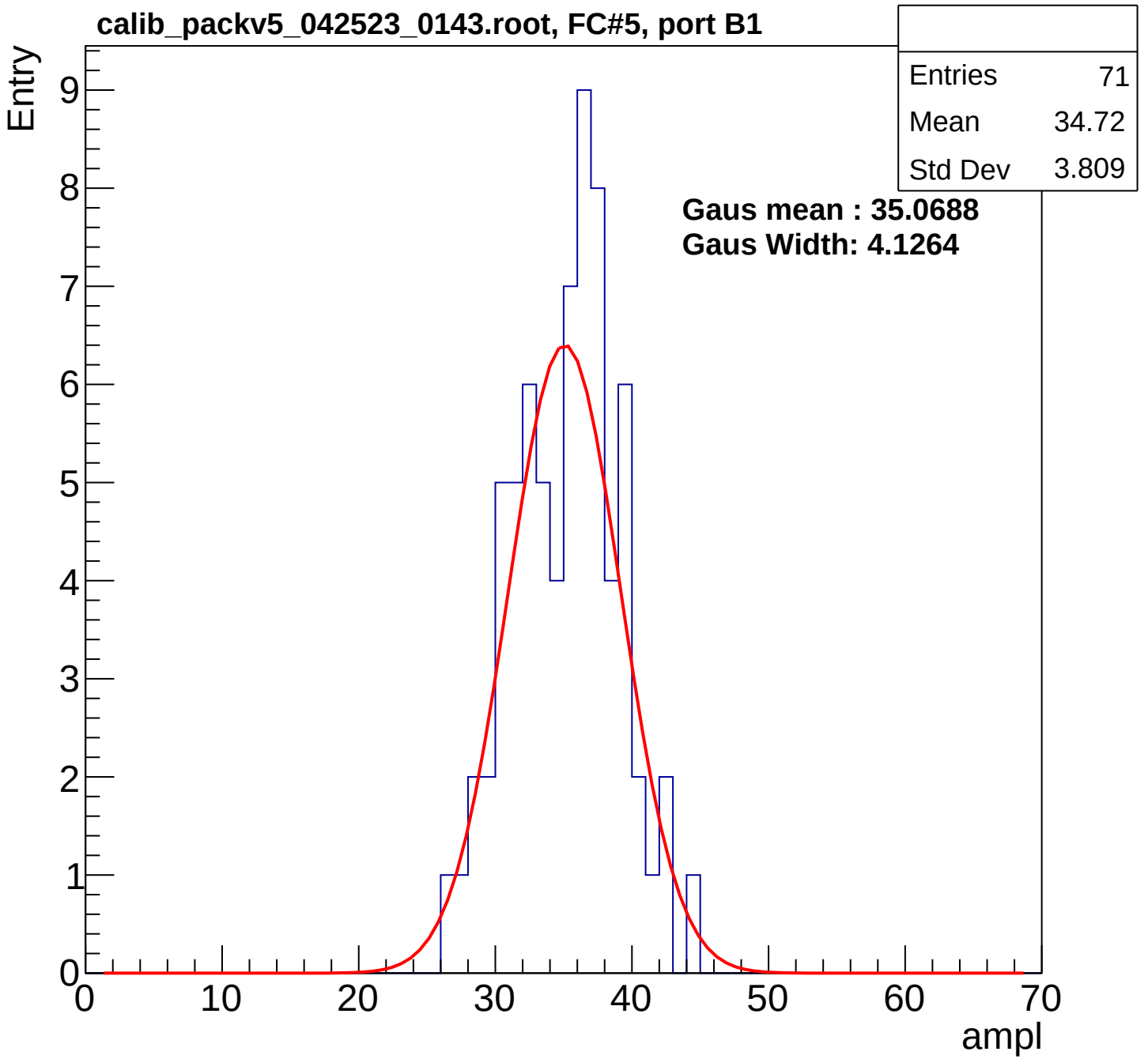
B0L000S, U1-ch112, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 34.72
Std Dev 3.809
Gaus mean : 35.0688
Gaus Width: 4.1264
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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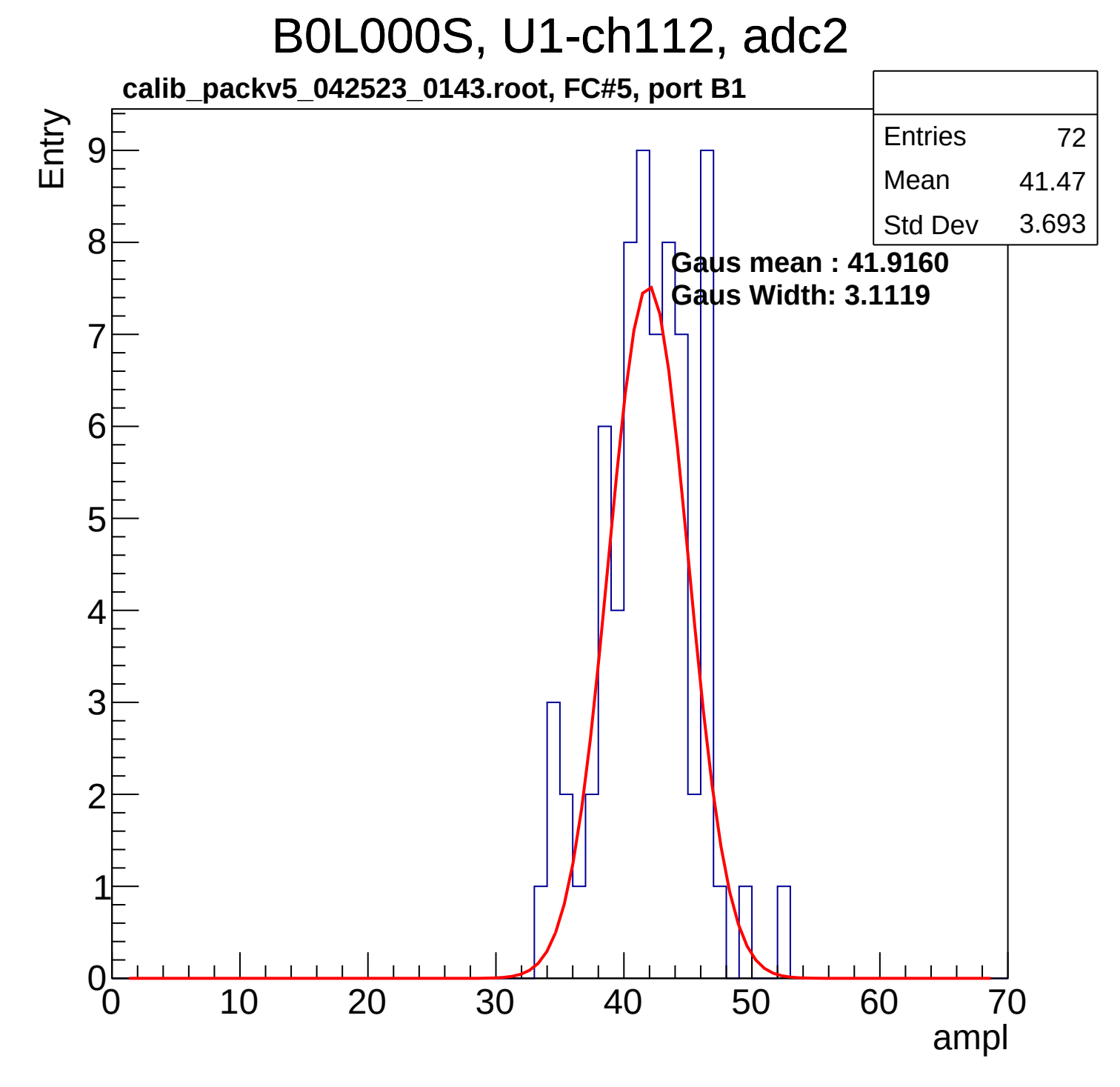
B0L000S, U1-ch112, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 41.47
Std Dev 3.693
Gaus mean : 41.9160
Gaus Width: 3.1119
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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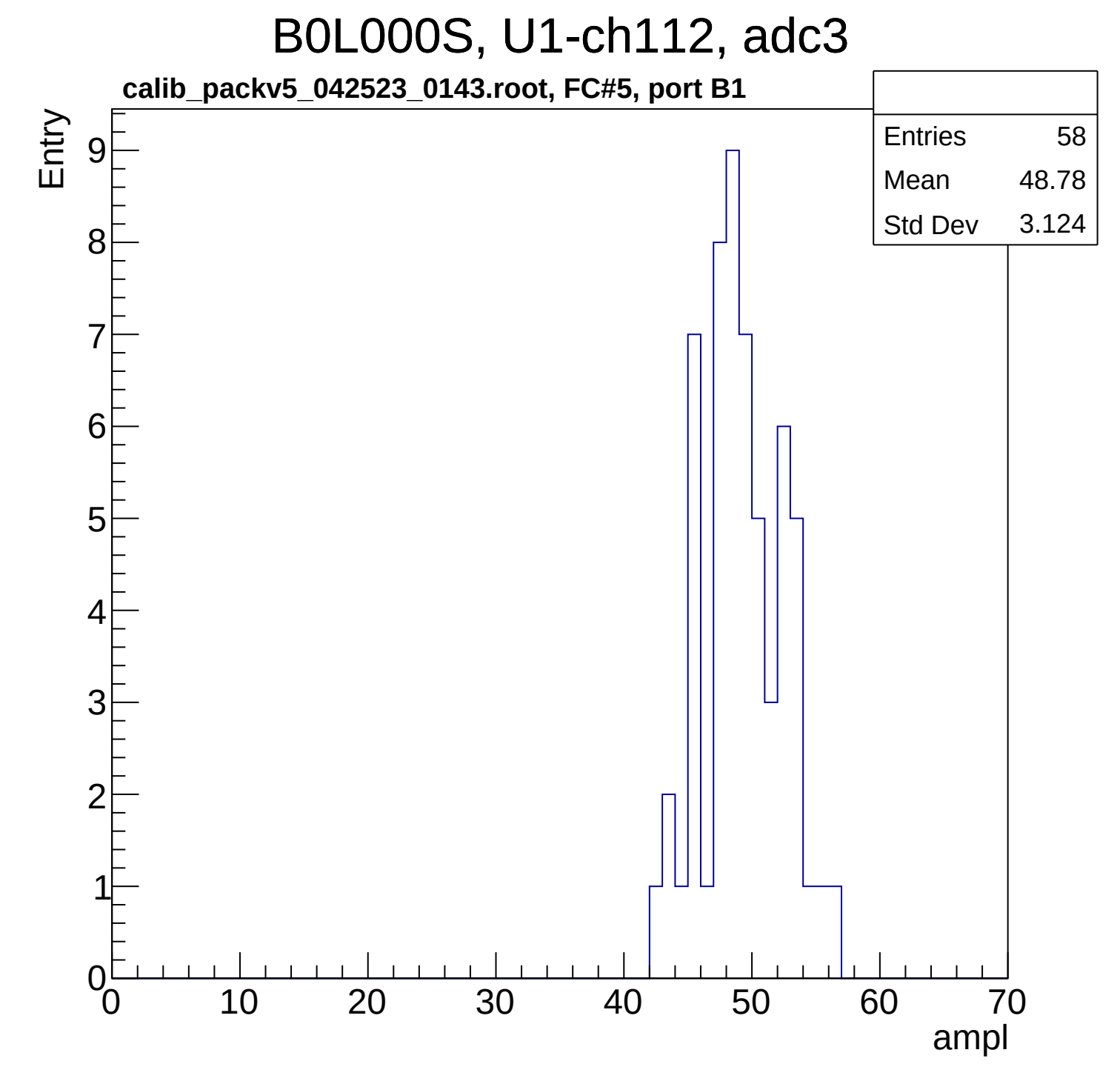
B0L000S, U1-ch112, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.78
Std Dev 3.124
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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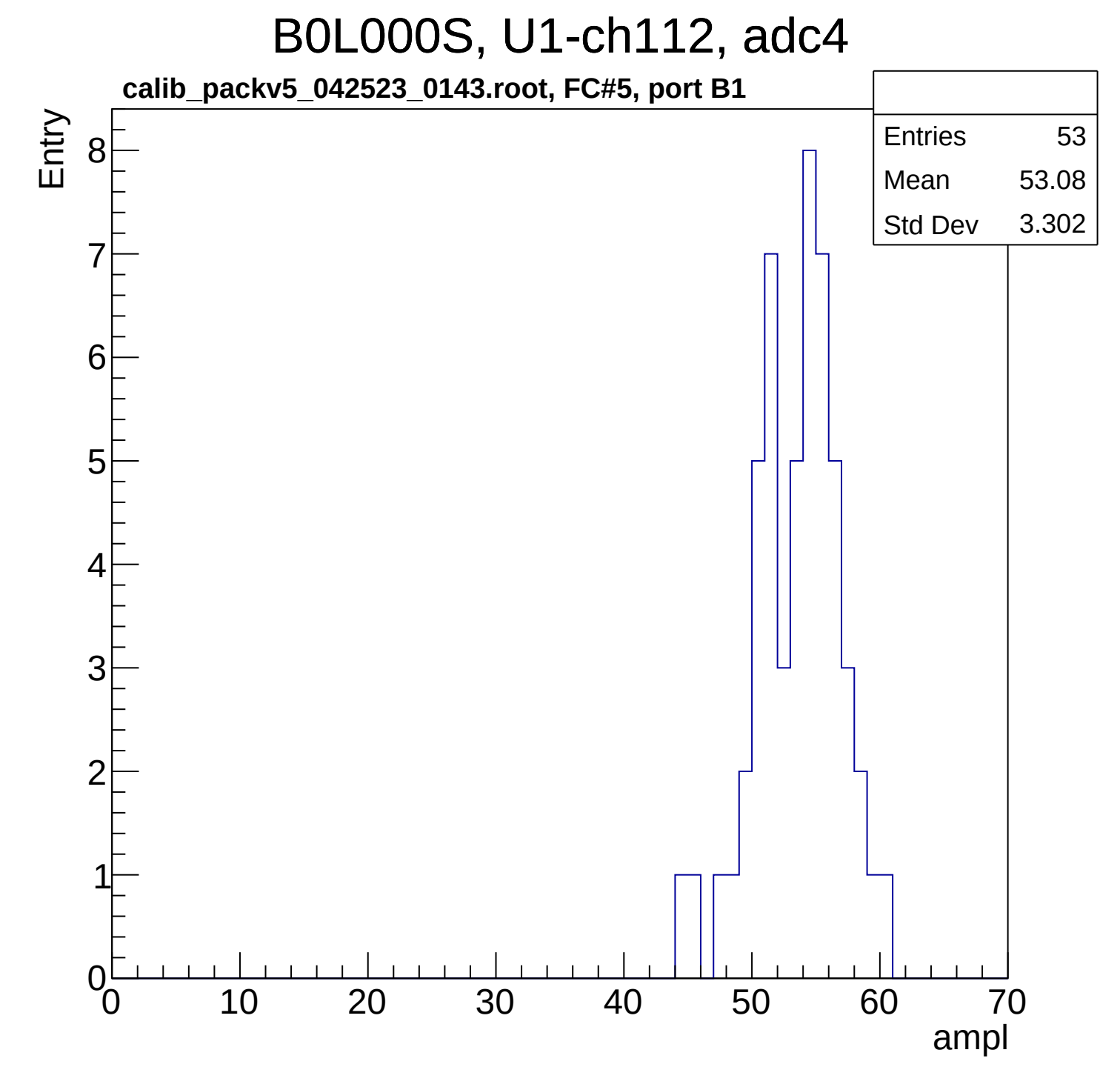
B0L000S, U1-ch112, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.08
Std Dev 3.302
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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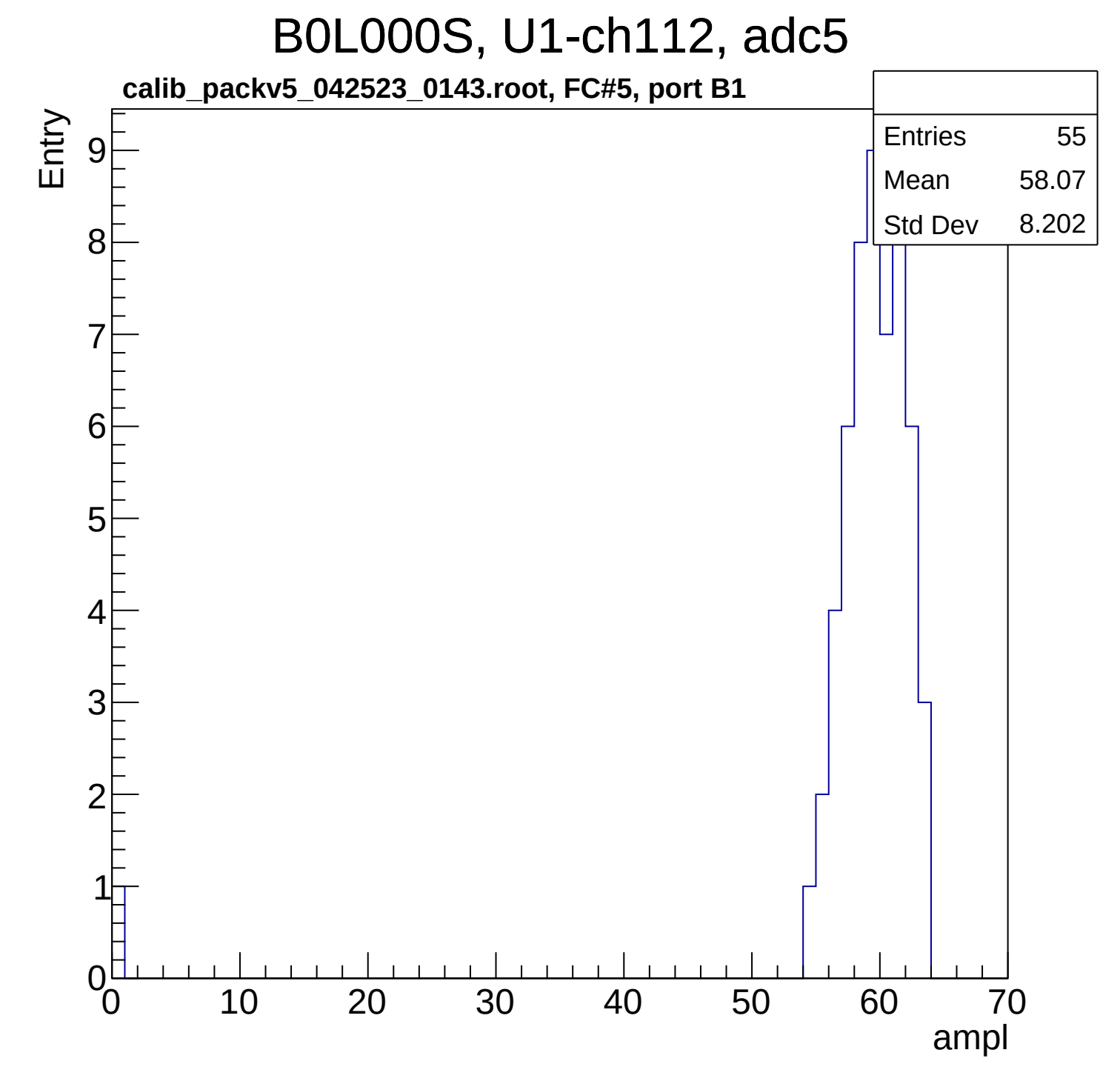
B0L000S, U1-ch112, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.07
Std Dev 8.202
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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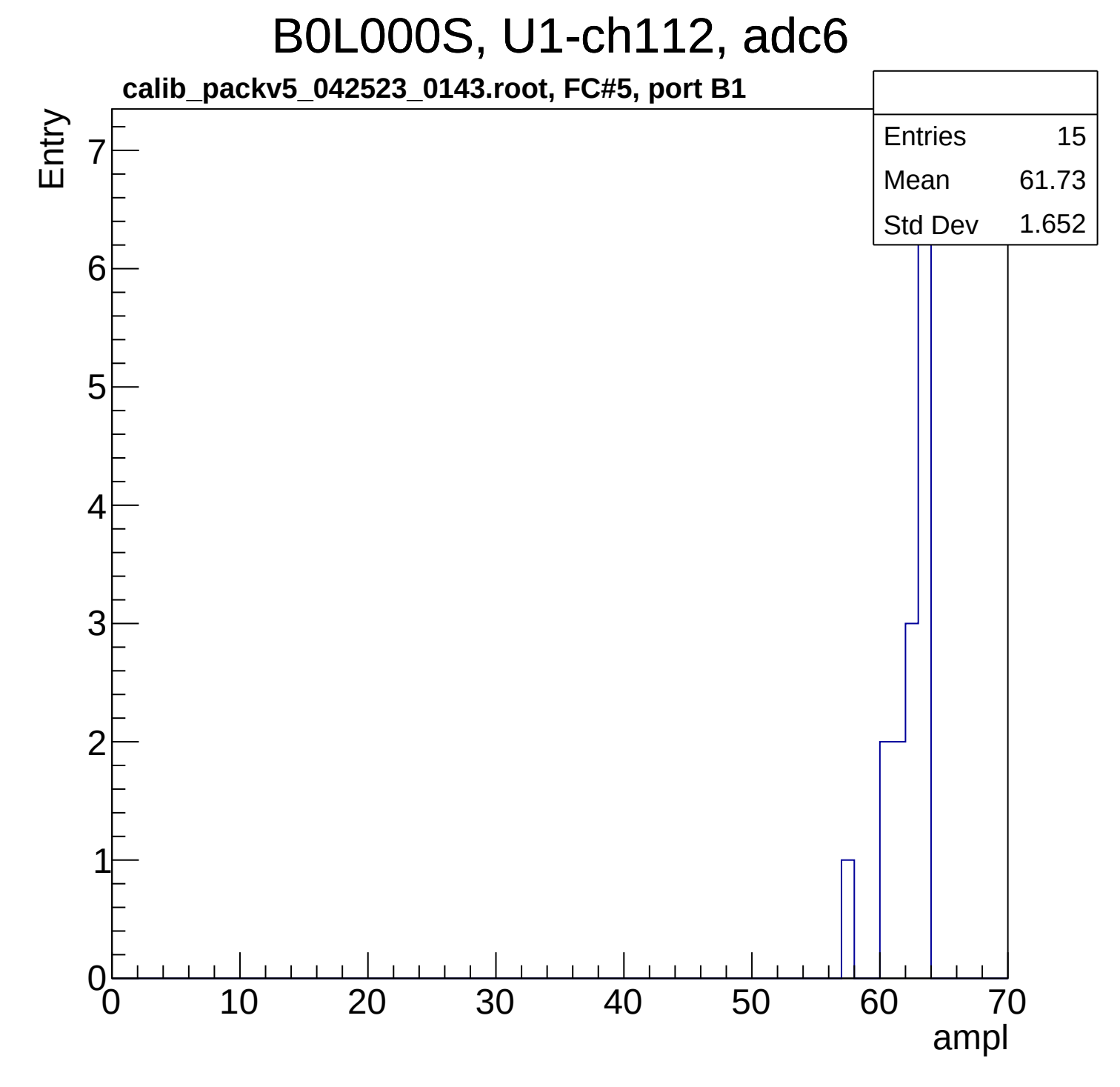
B0L000S, U1-ch112, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.73
Std Dev 1.652
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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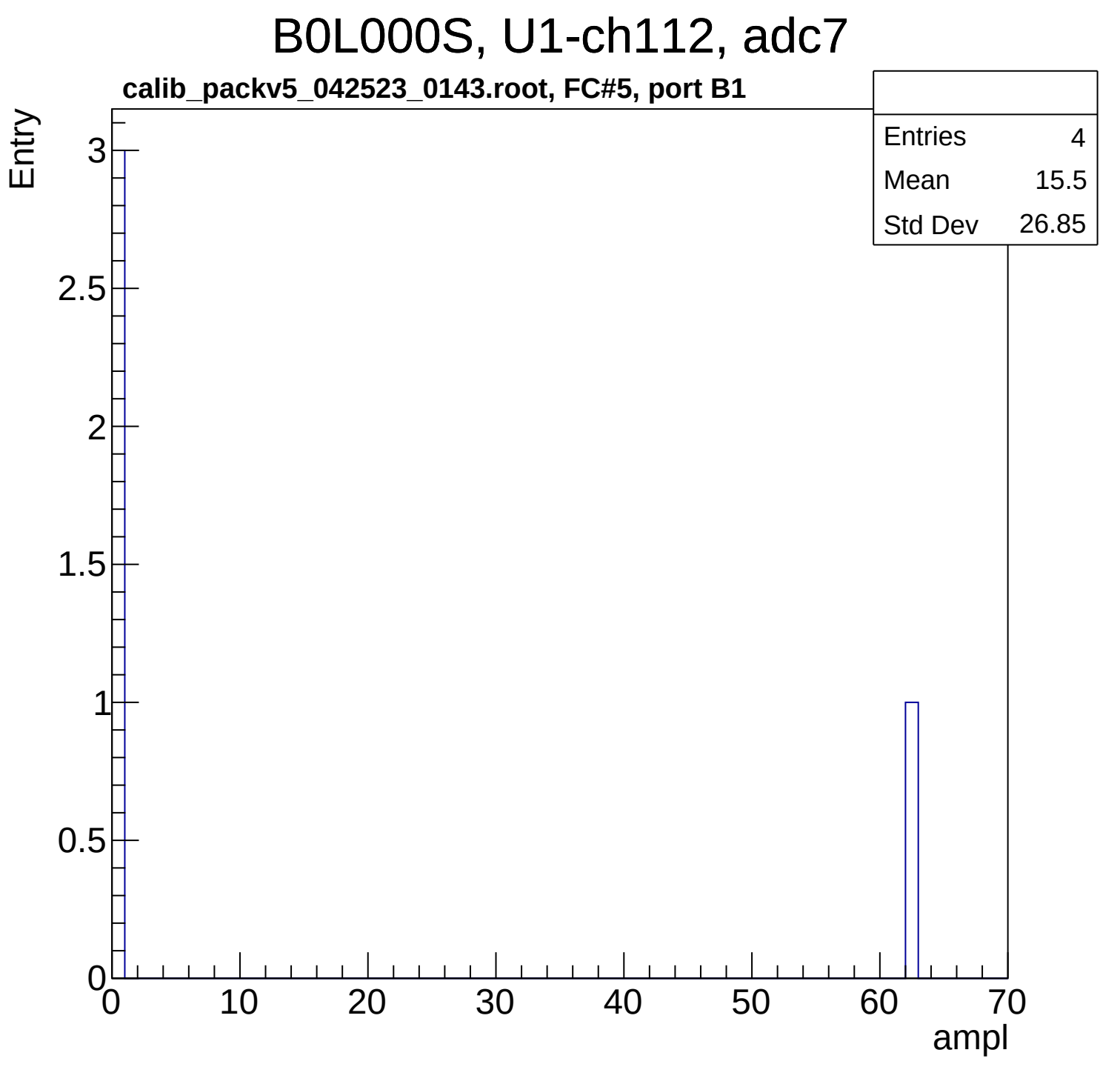
B0L000S, U1-ch112, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 15.5
Std Dev 26.85
Entry
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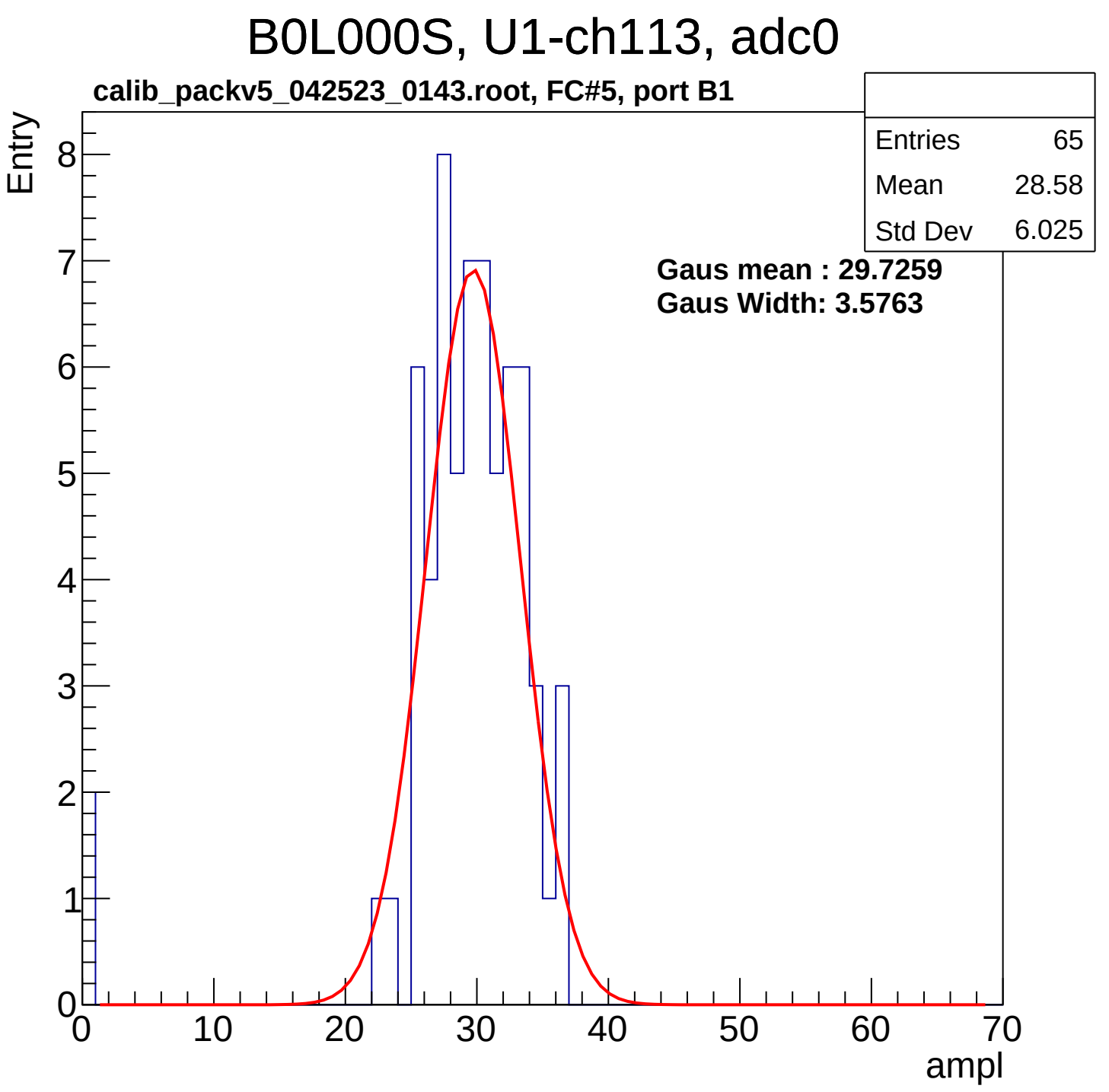
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch113, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.58
Std Dev 6.025
Gaus mean : 29.7259
Gaus Width: 3.5763
Entry
ampl
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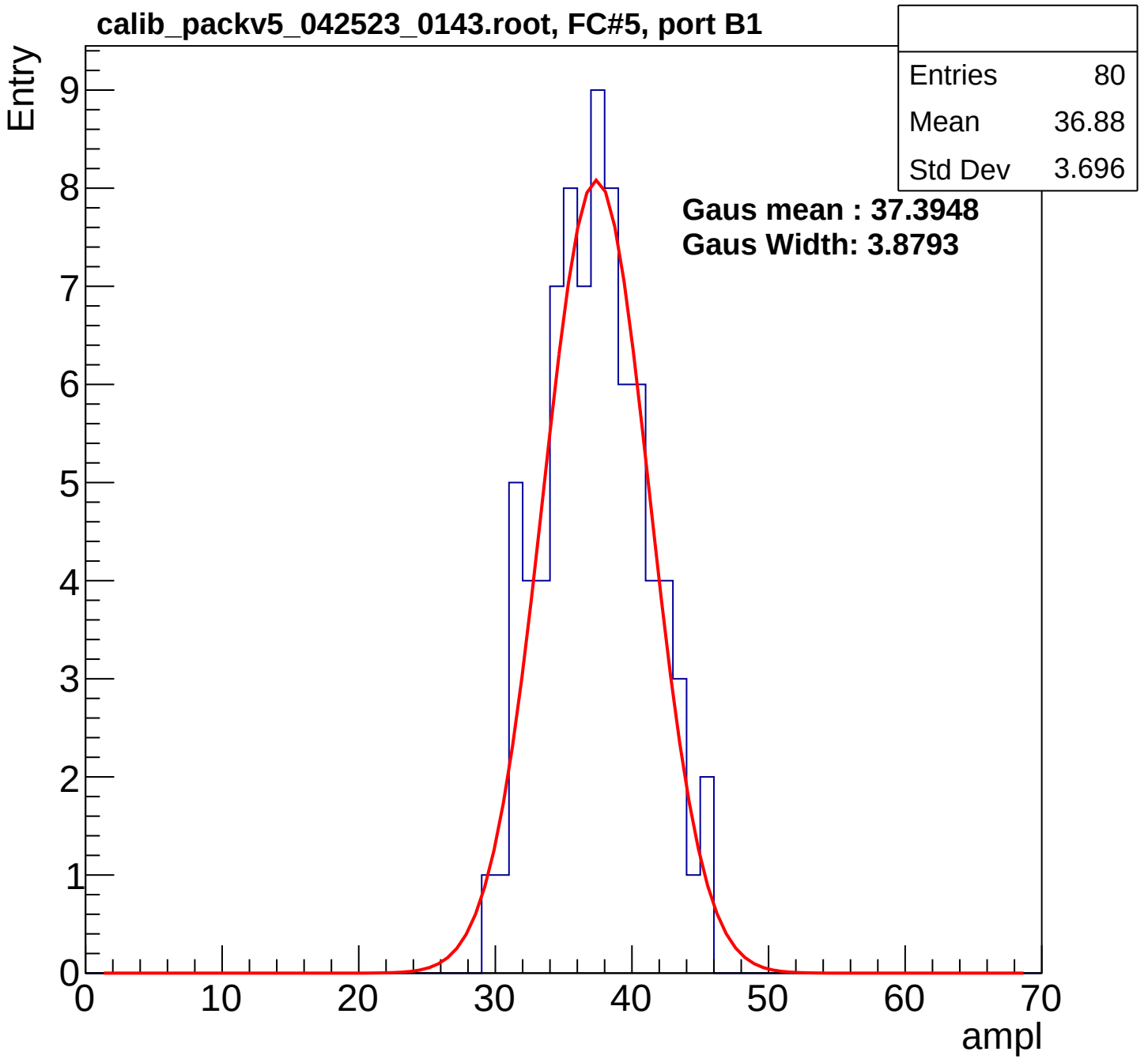
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch113, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 36.88
Std Dev 3.696
Gaus mean : 37.3948
Gaus Width: 3.8793
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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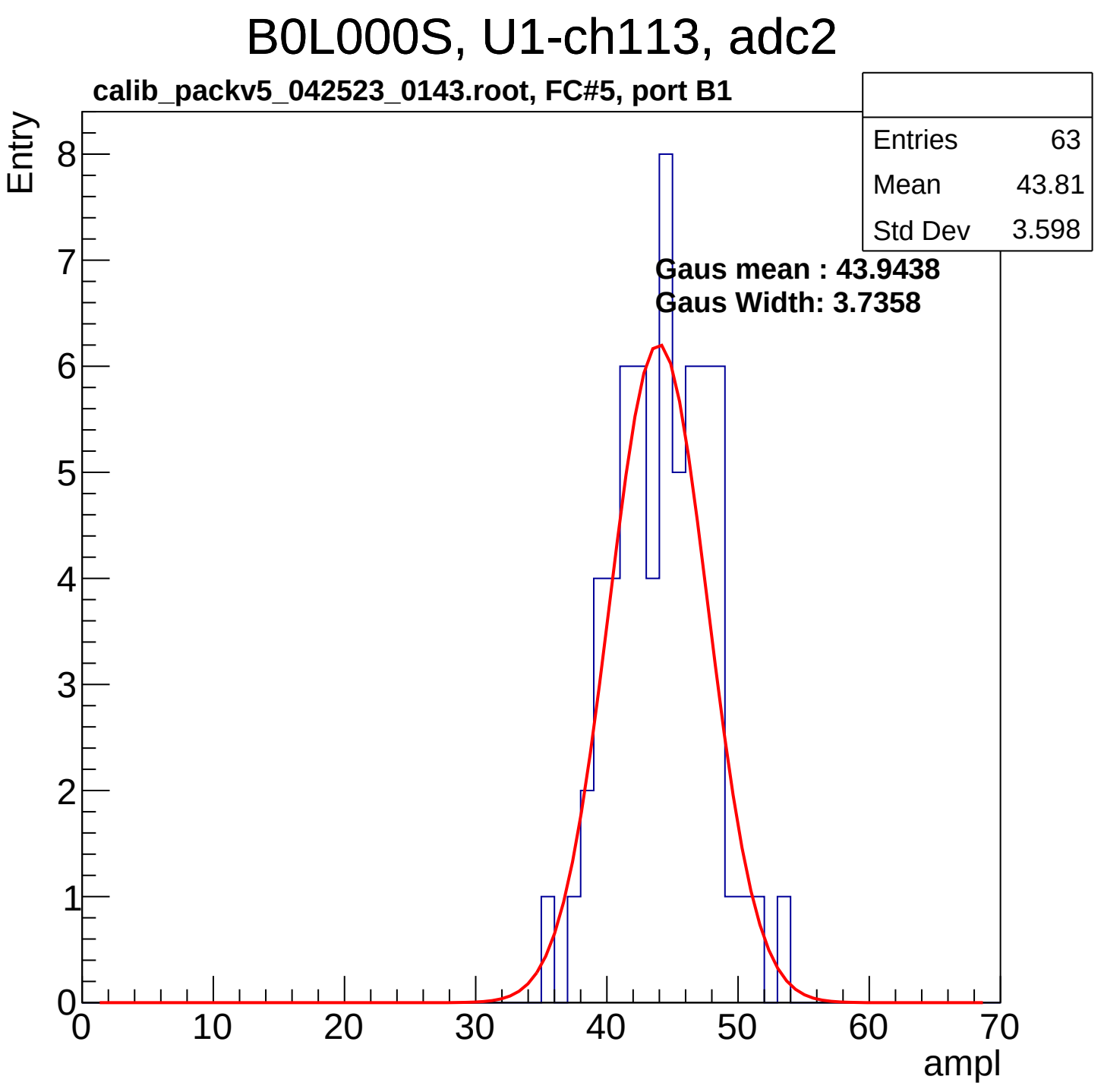
B0L000S, U1-ch113, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.81
Std Dev 3.598
Gaus mean : 43.9438
Gaus Width: 3.7358
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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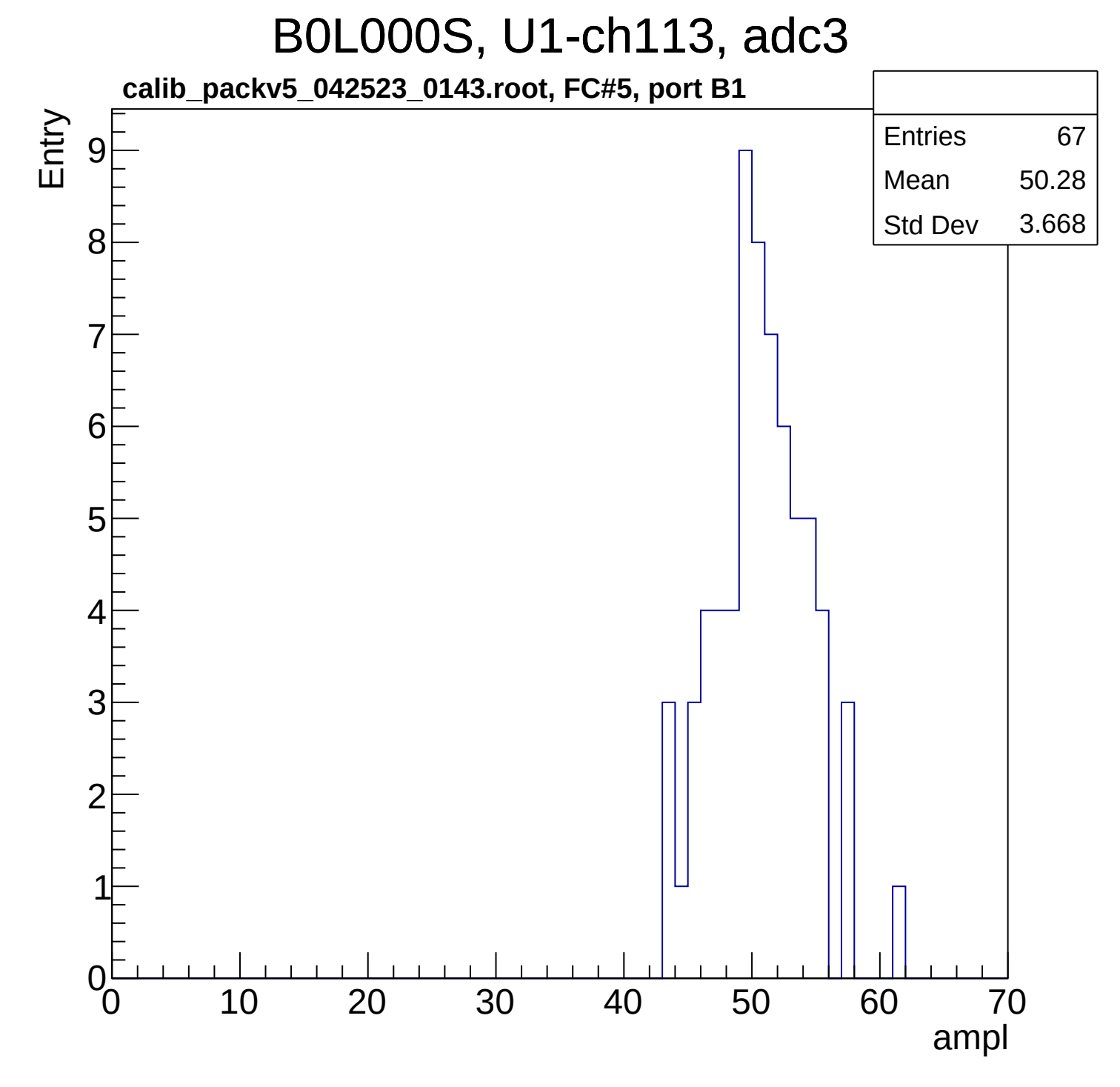
B0L000S, U1-ch113, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.28
Std Dev 3.668
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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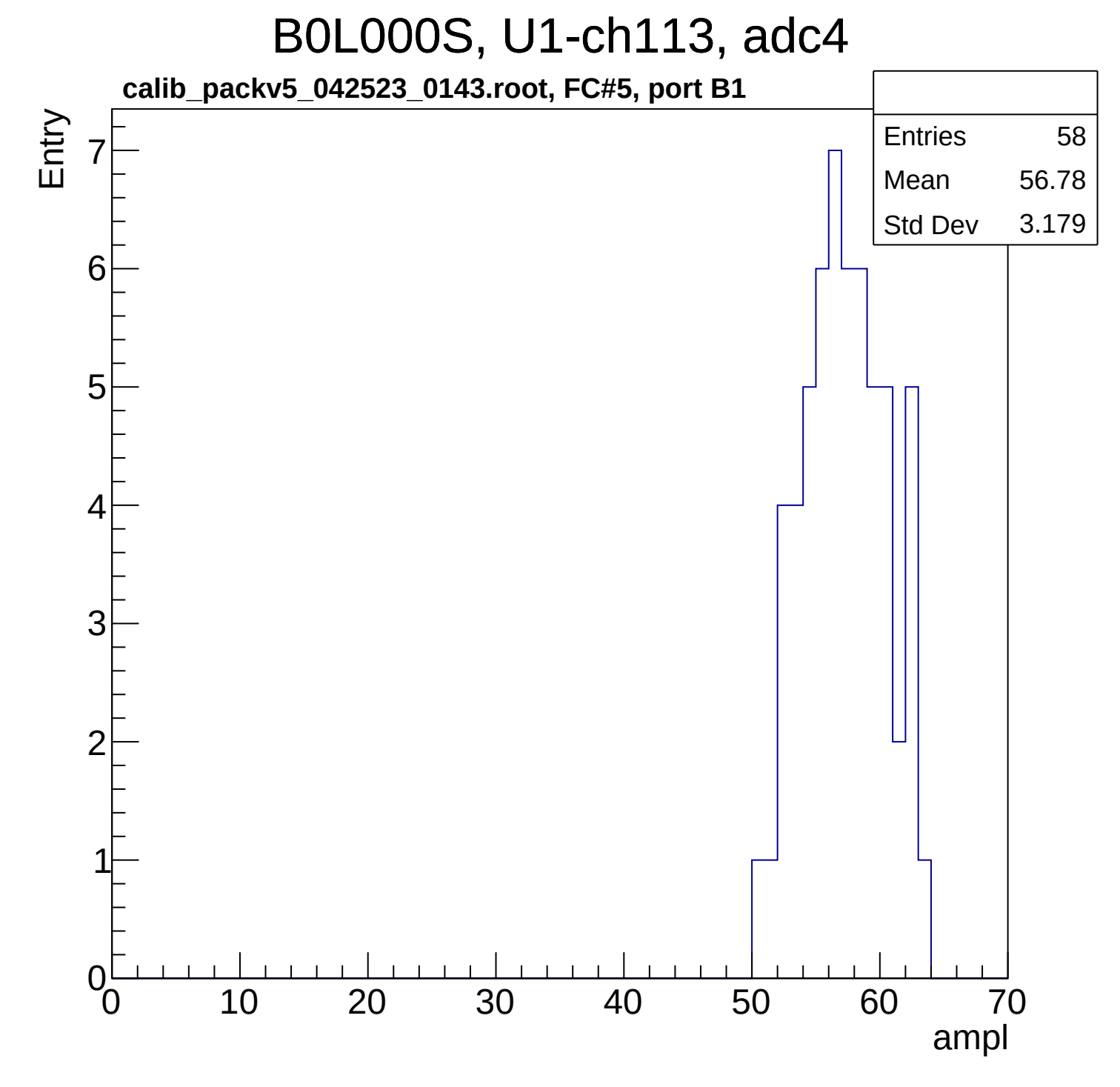
B0L000S, U1-ch113, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.78
Std Dev 3.179
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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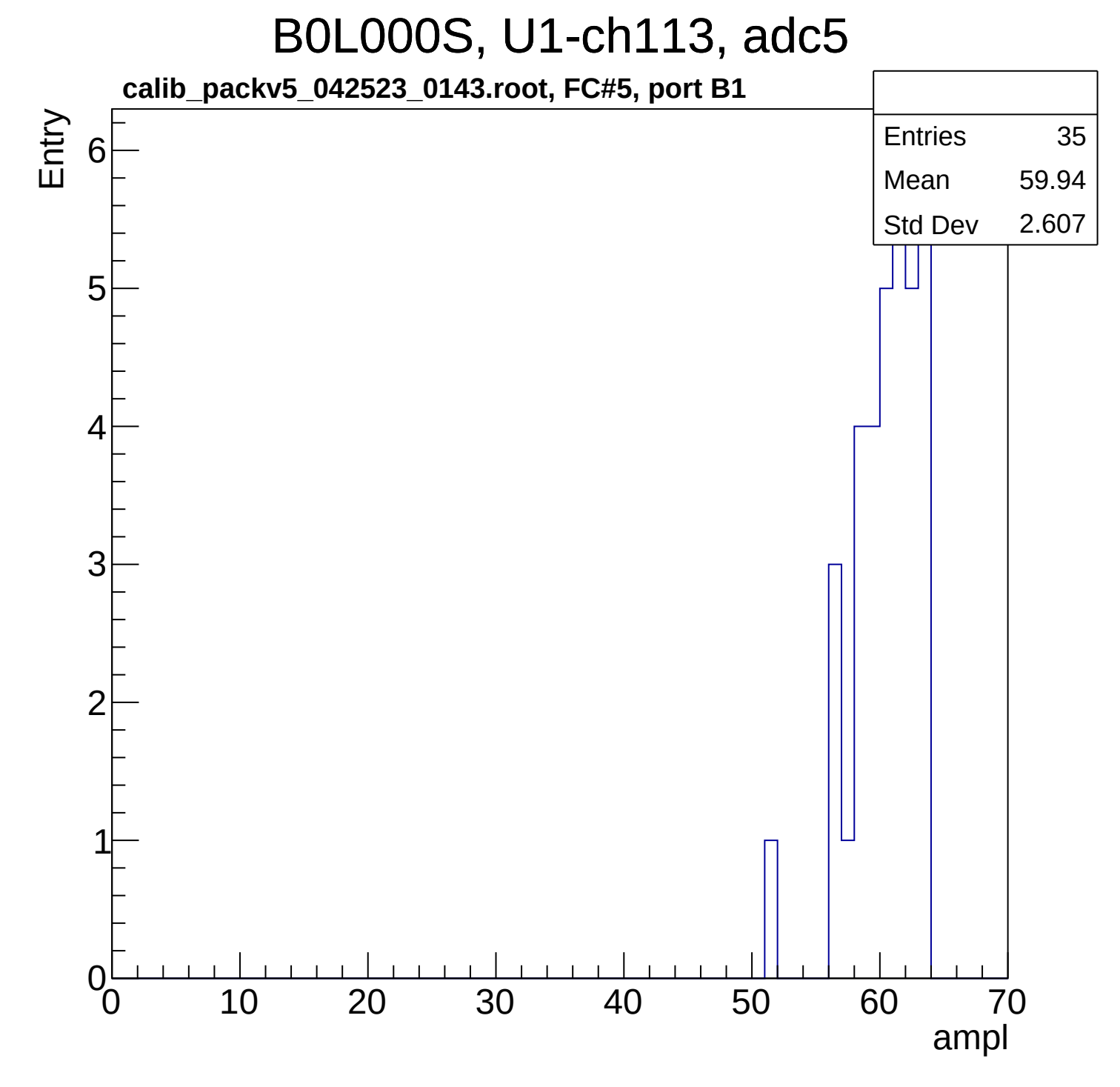
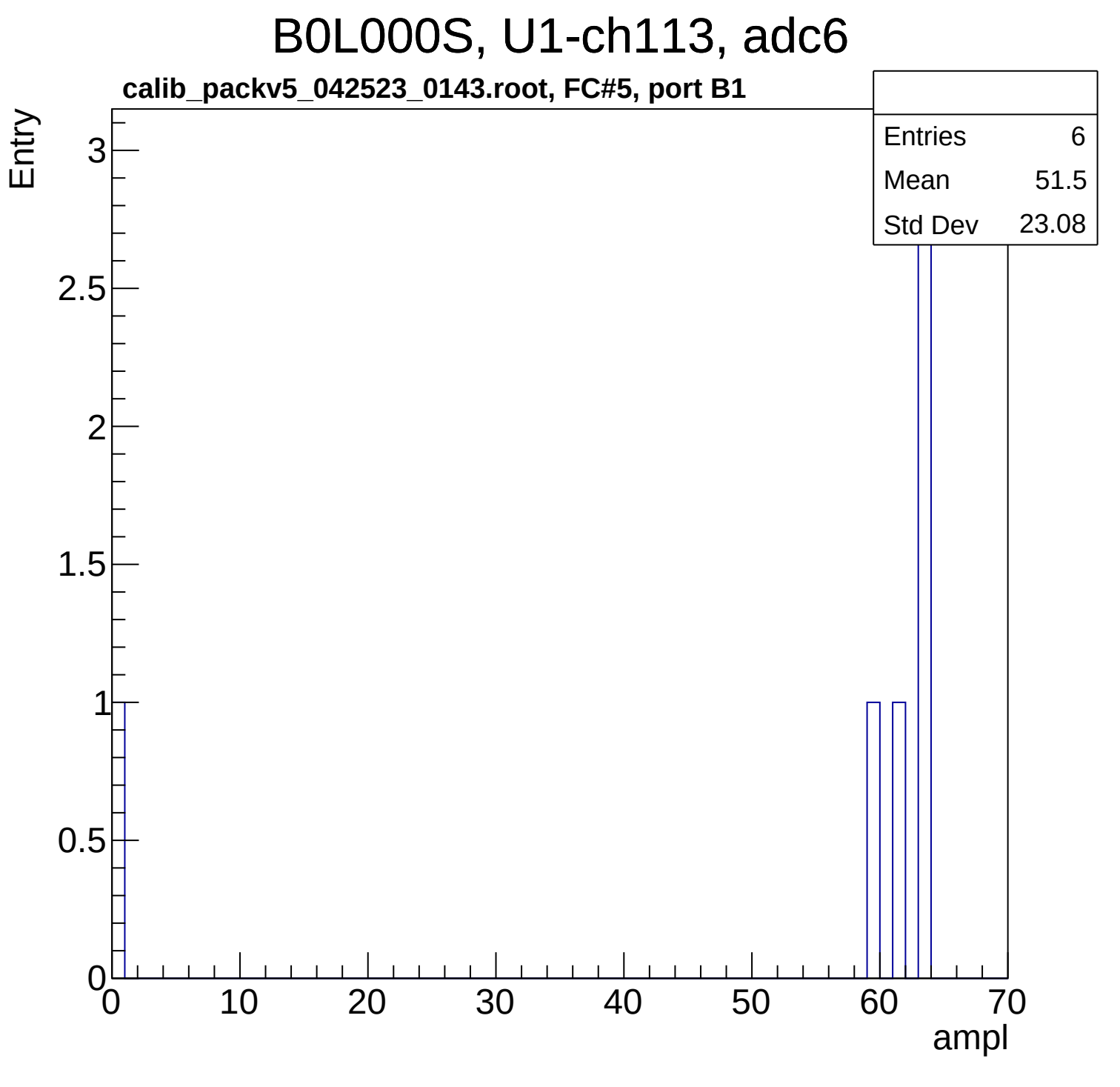

B0L000S, U1-ch113, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 59.94
Std Dev 2.607
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U1-ch113, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 51.5
Std Dev 23.08
Entry
ampl
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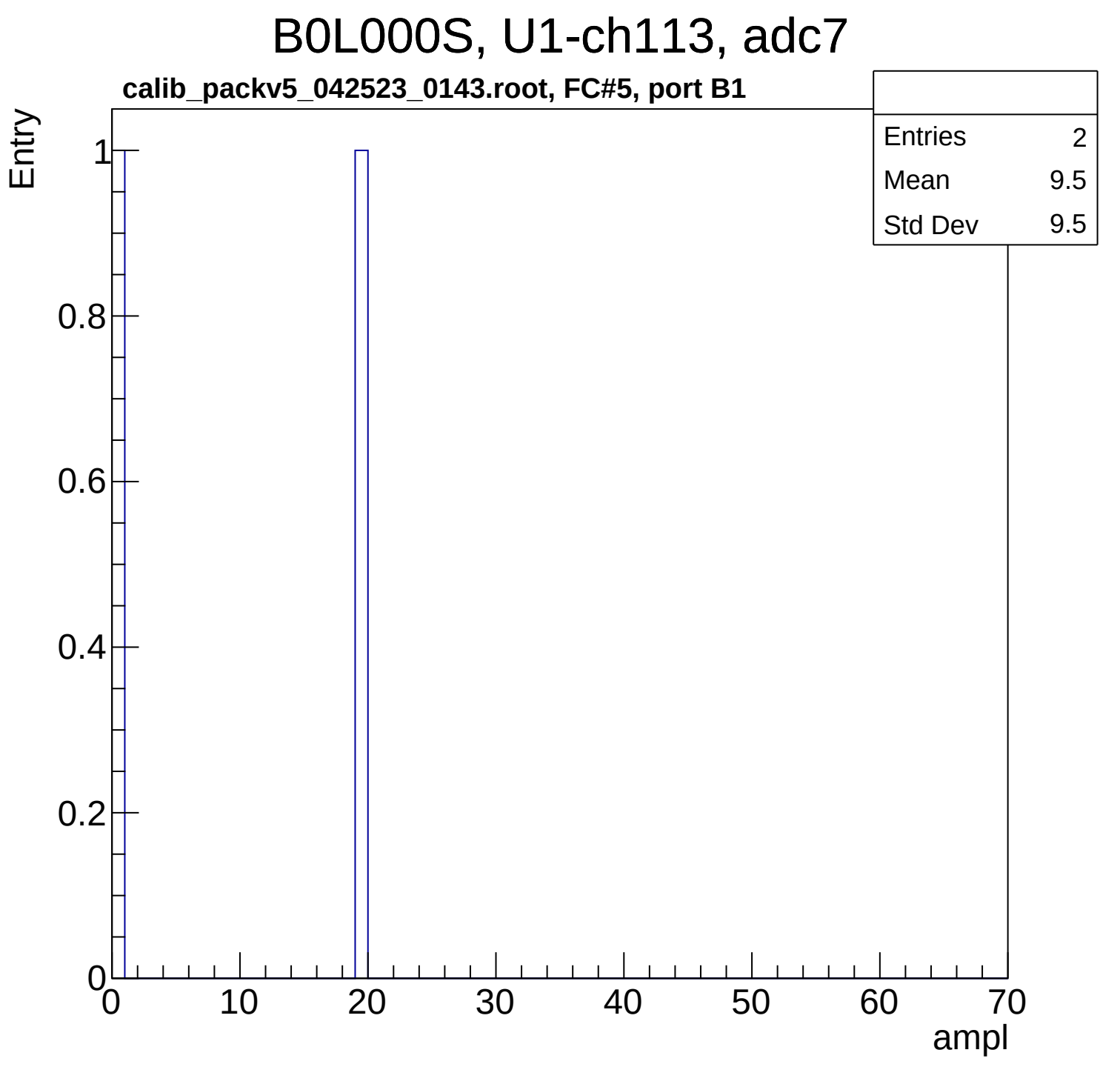
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch113, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
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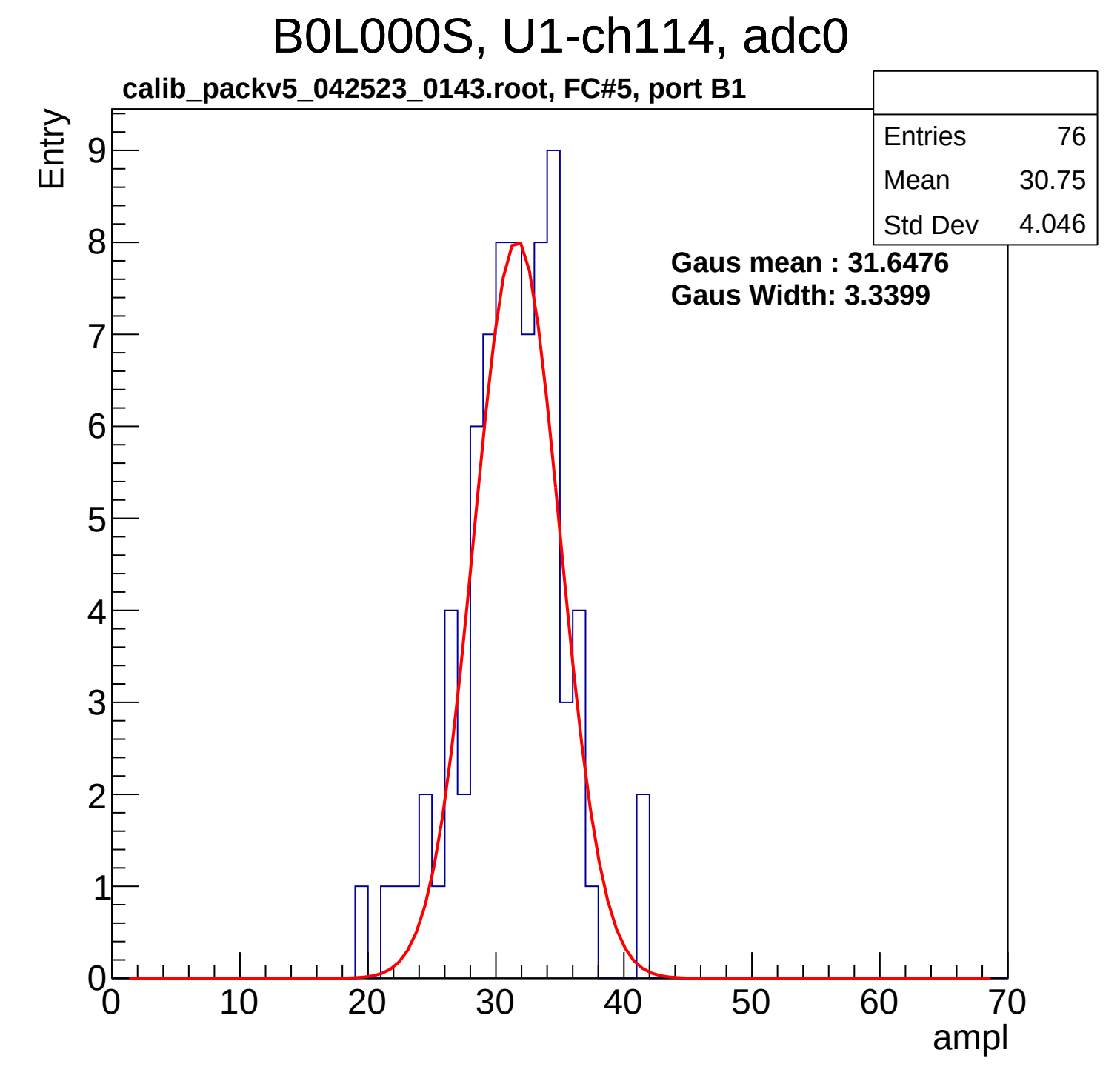
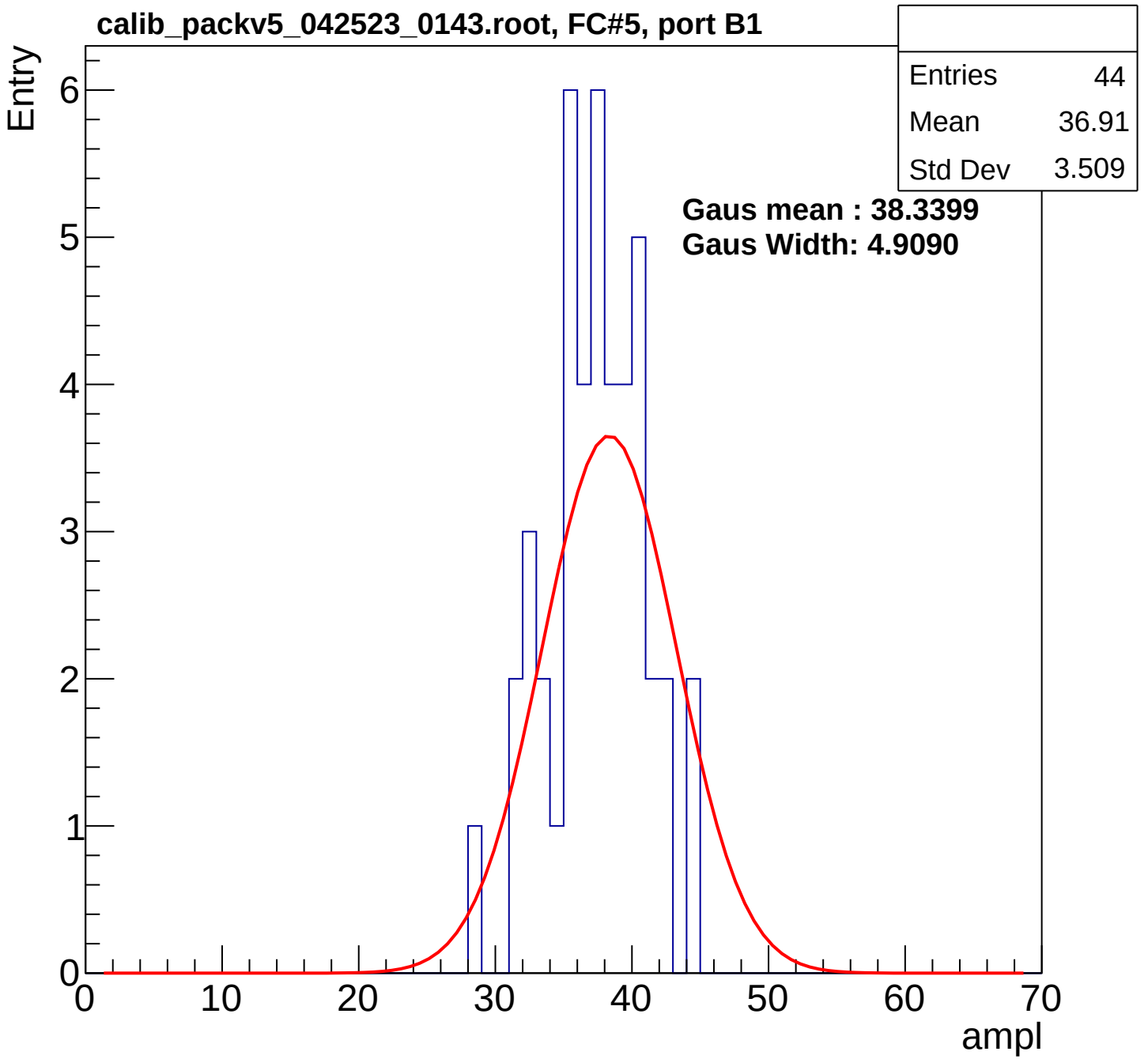
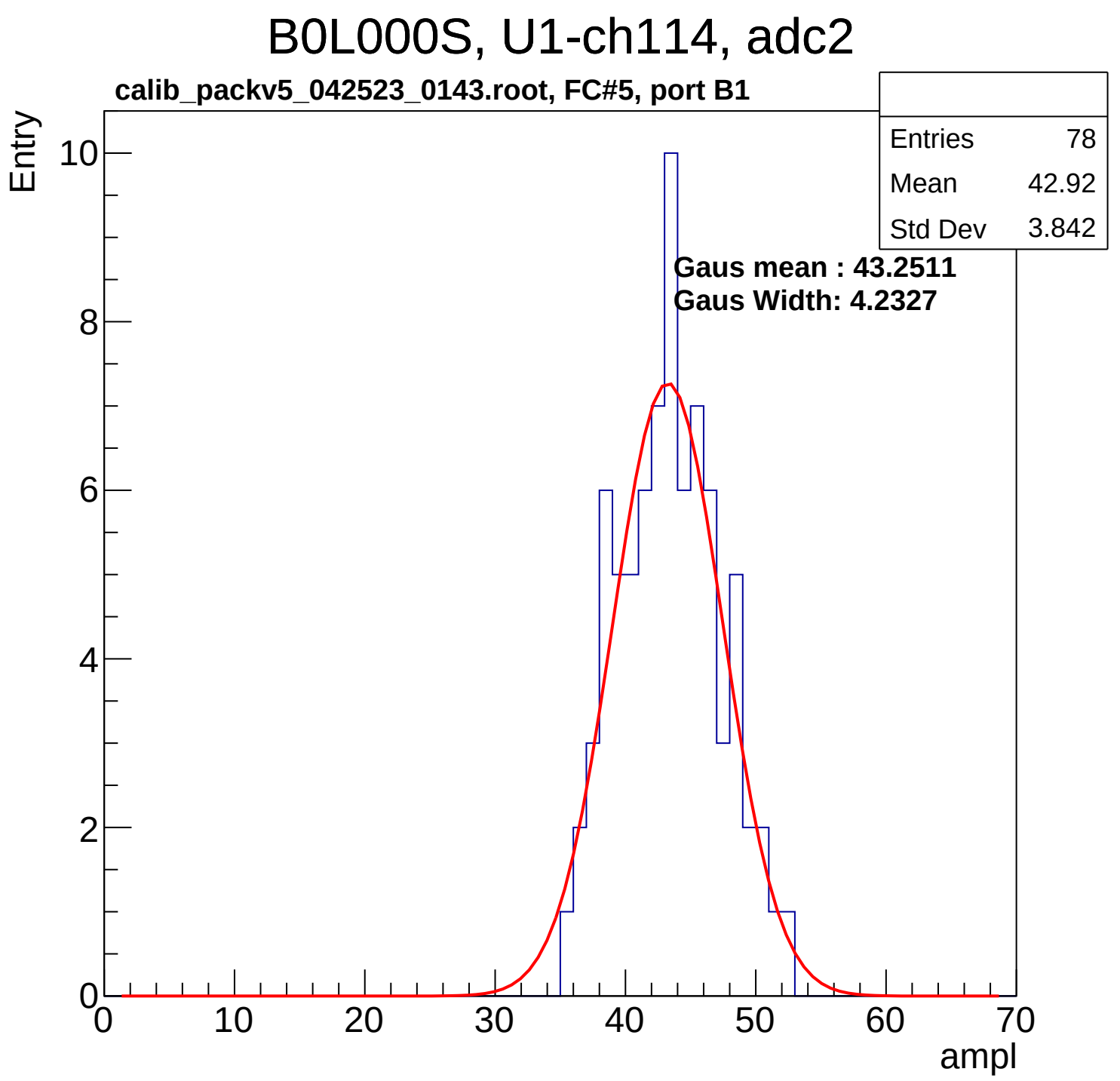
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch114, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 30.75
Std Dev 4.046
Gaus mean : 31.6476
Gaus Width: 3.3399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch114, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 36.91
Std Dev 3.509
Gaus mean : 38.3399
Gaus Width: 4.9090
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U1-ch114, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 42.92
Std Dev 3.842
Gaus mean : 43.2511
Gaus Width: 4.2327
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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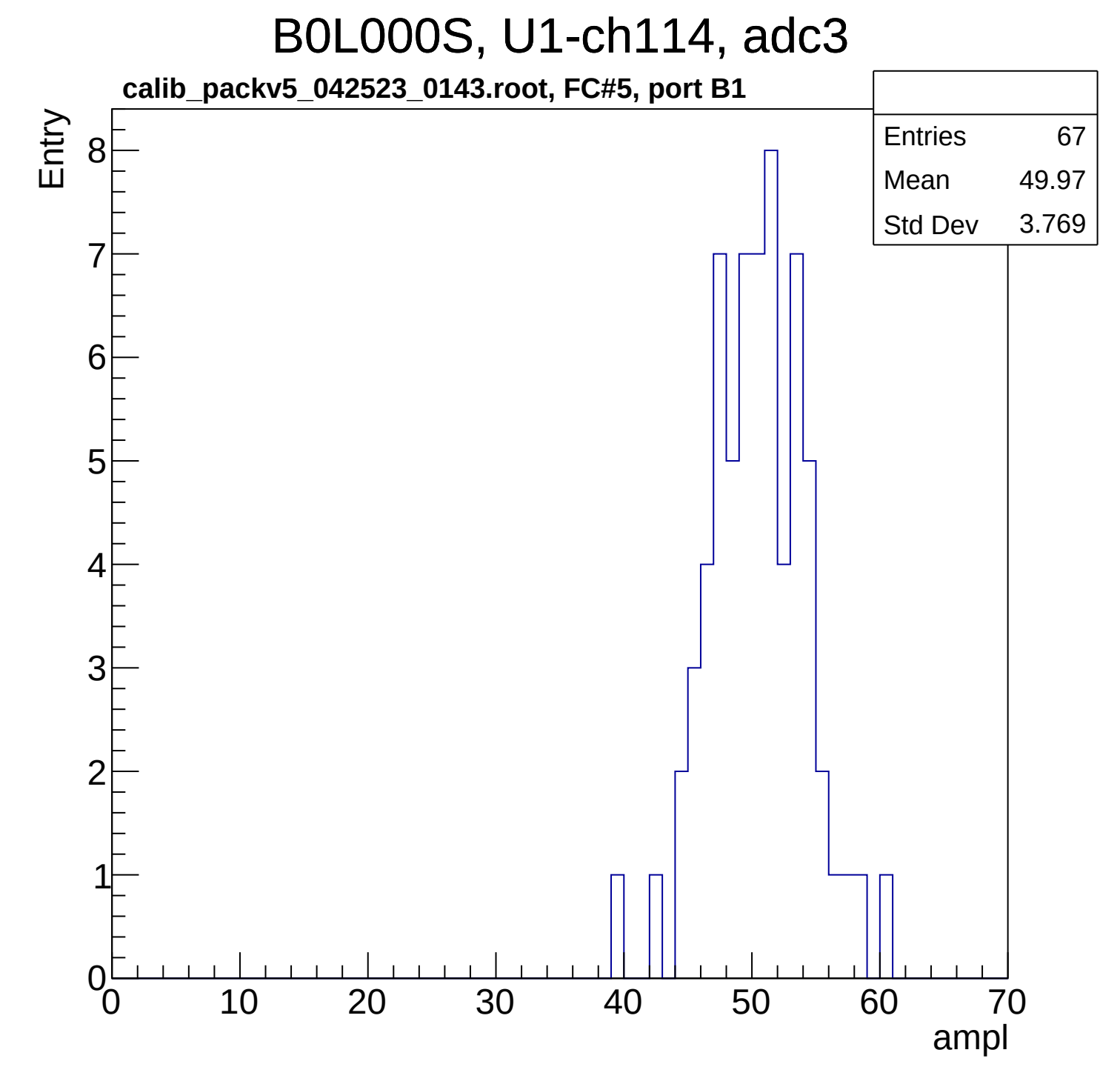
B0L000S, U1-ch114, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.97
Std Dev 3.769
Entry
ampl
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4
5
6
7
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0
10
20
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40
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60
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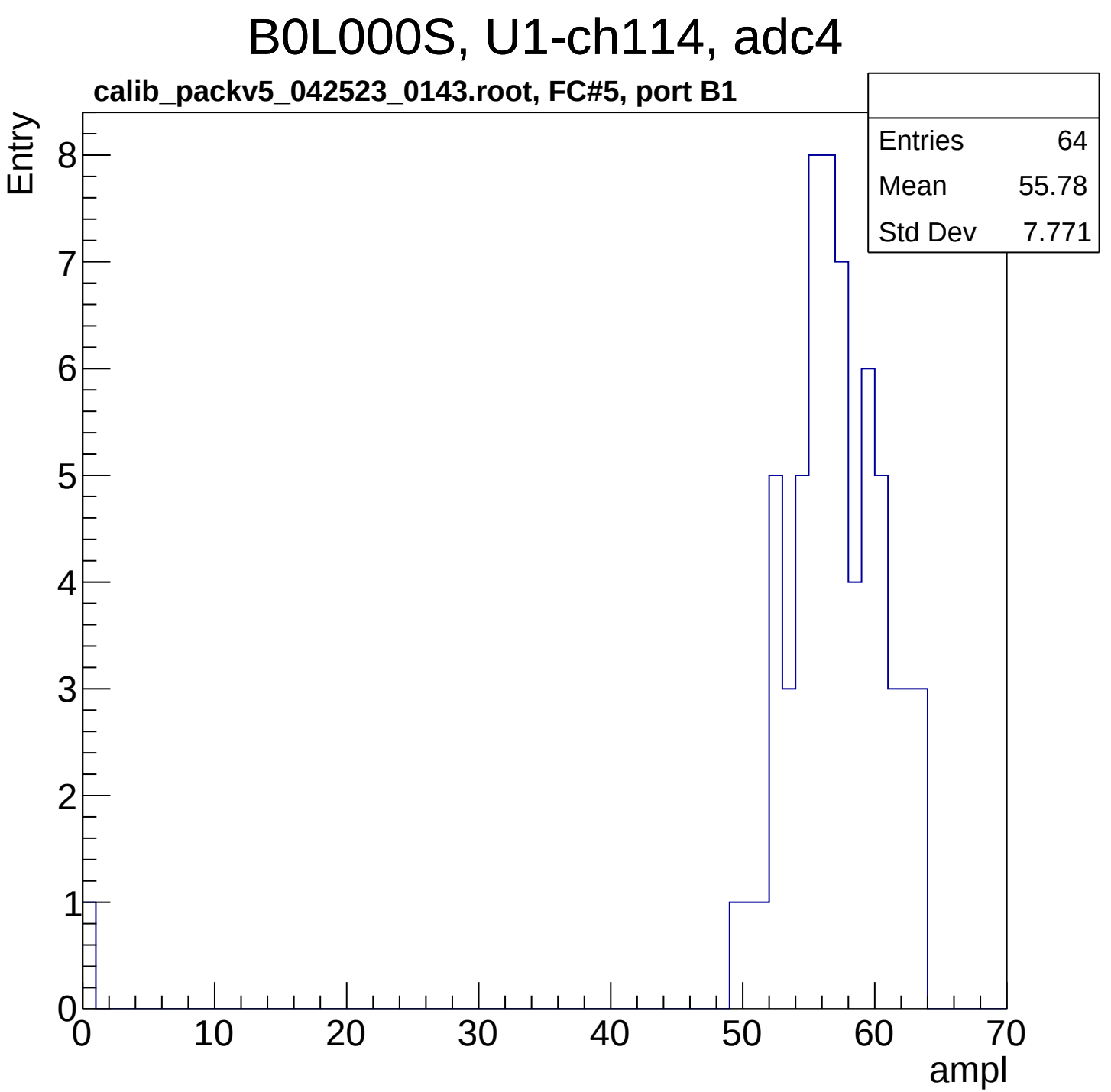
B0L000S, U1-ch114, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 55.78
Std Dev 7.771
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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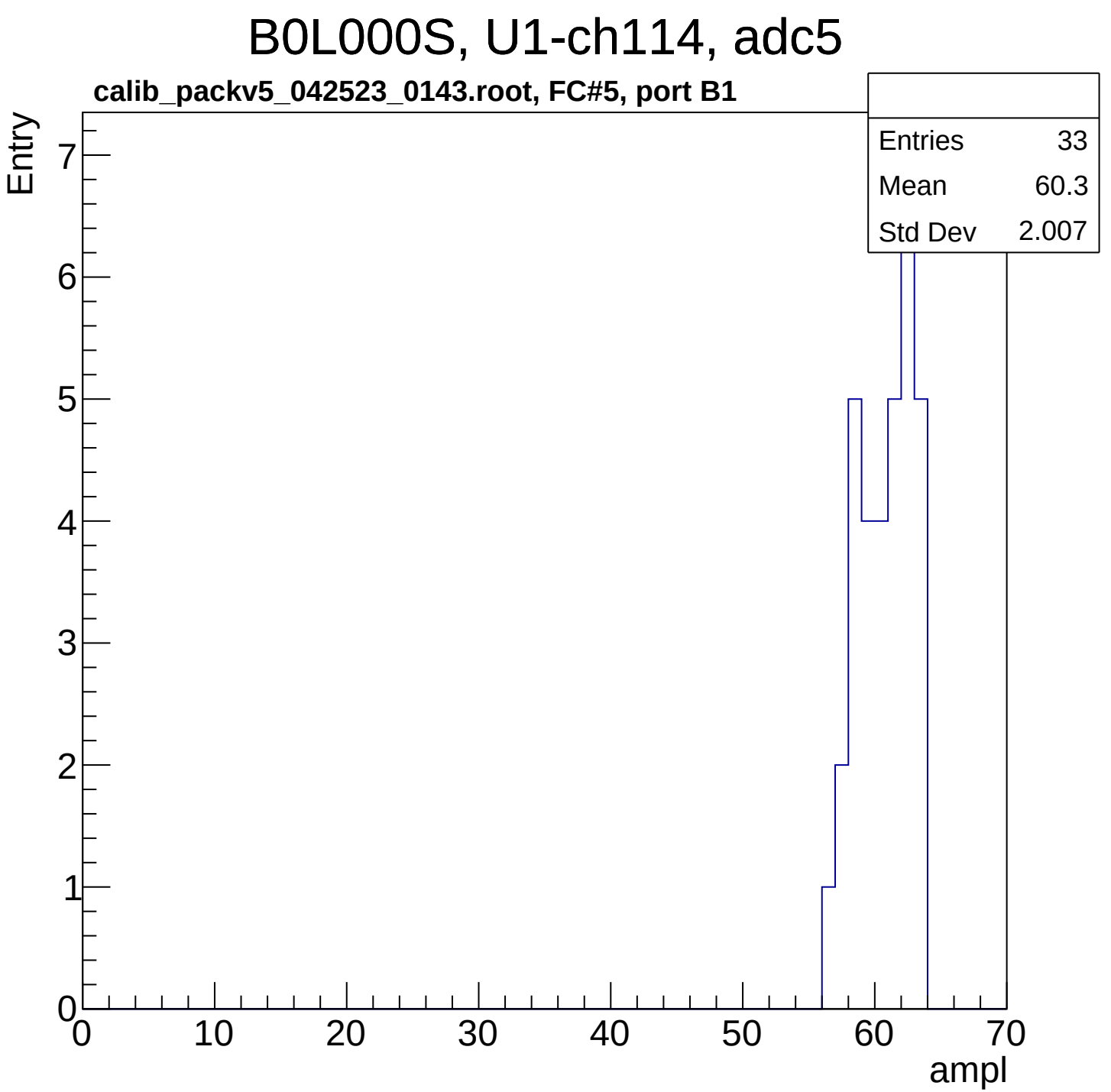
B0L000S, U1-ch114, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 60.3
Std Dev 2.007
Entry
ampl
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4
5
6
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0
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20
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60
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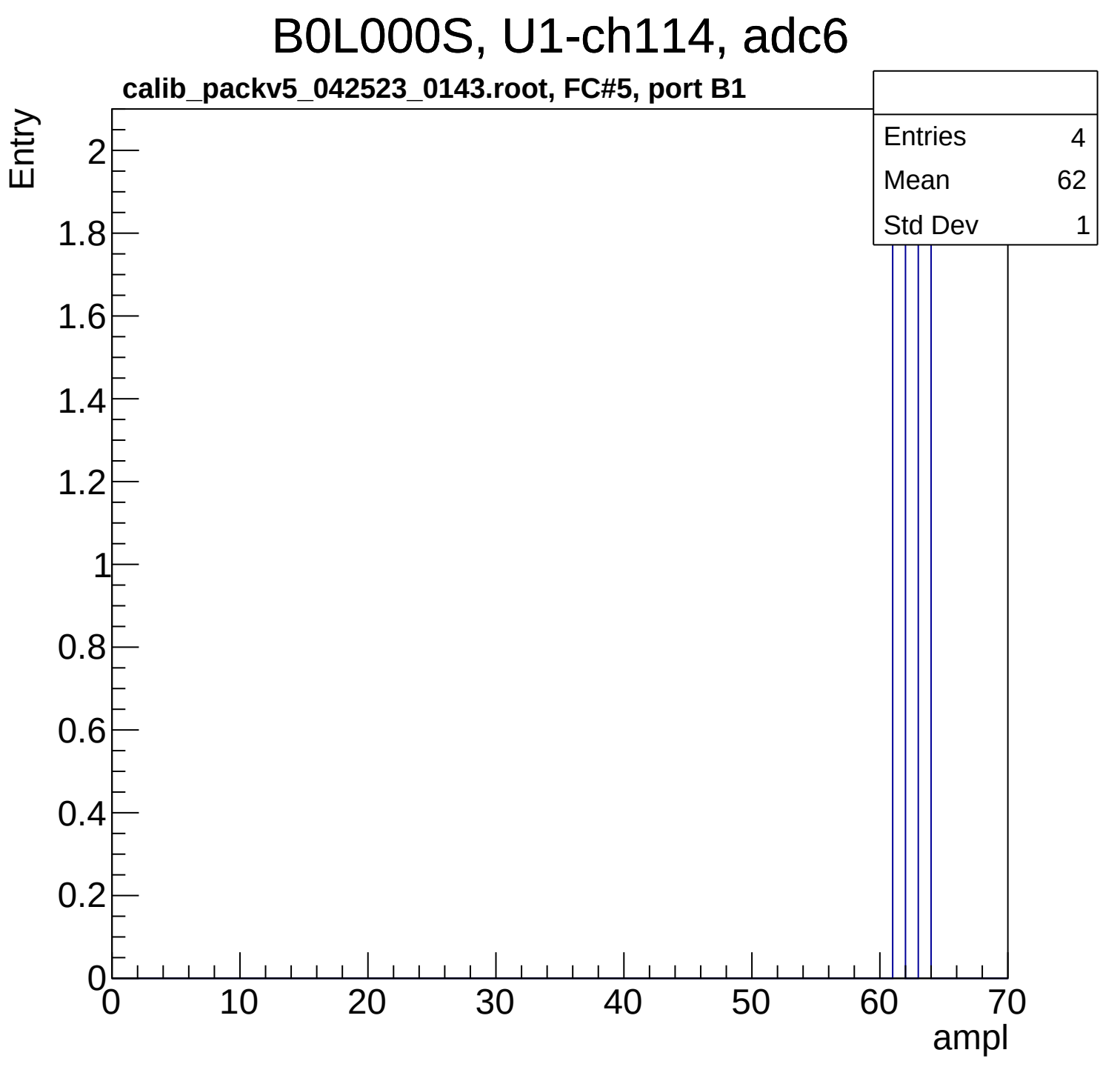
B0L000S, U1-ch114, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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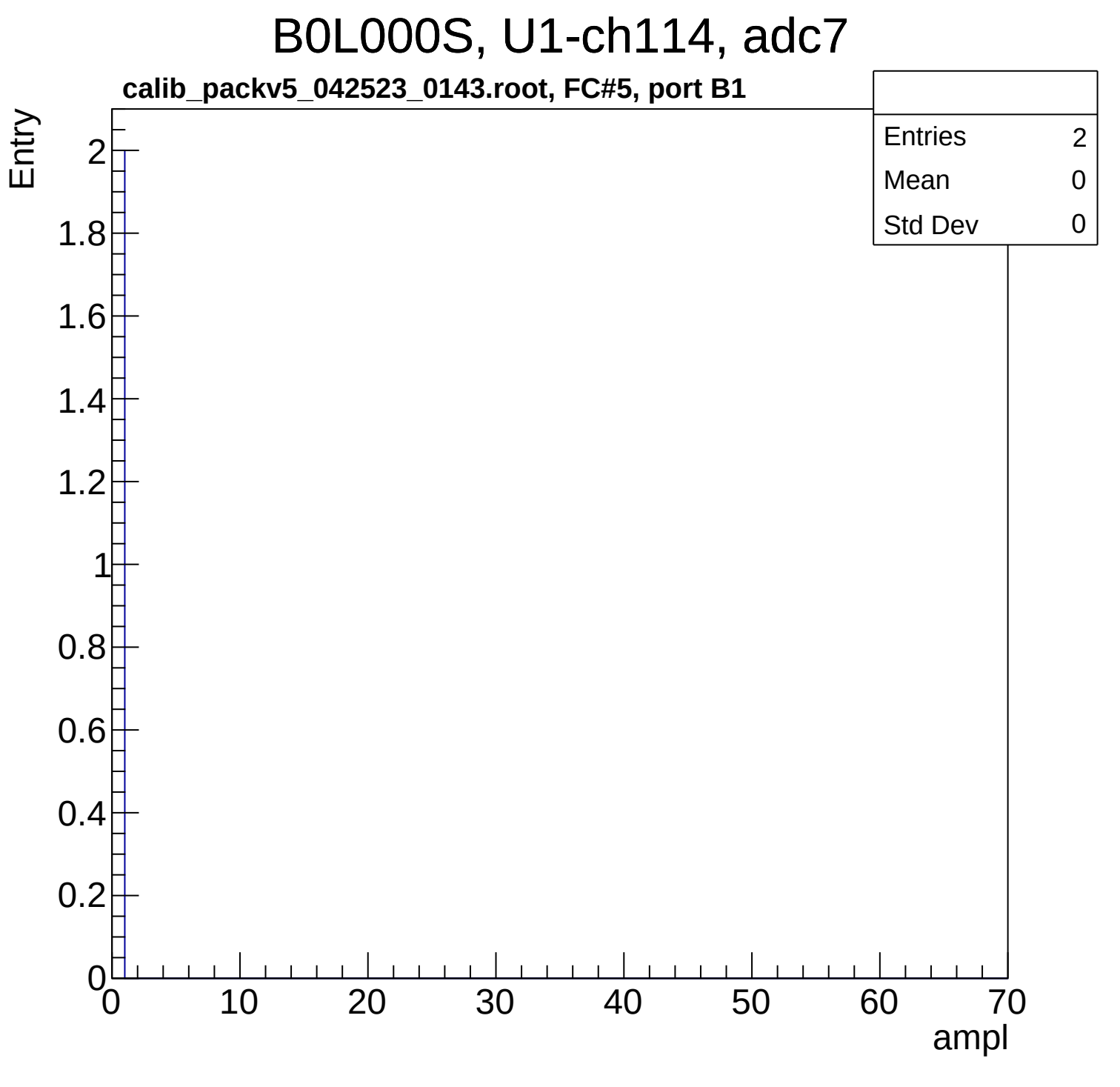
B0L000S, U1-ch114, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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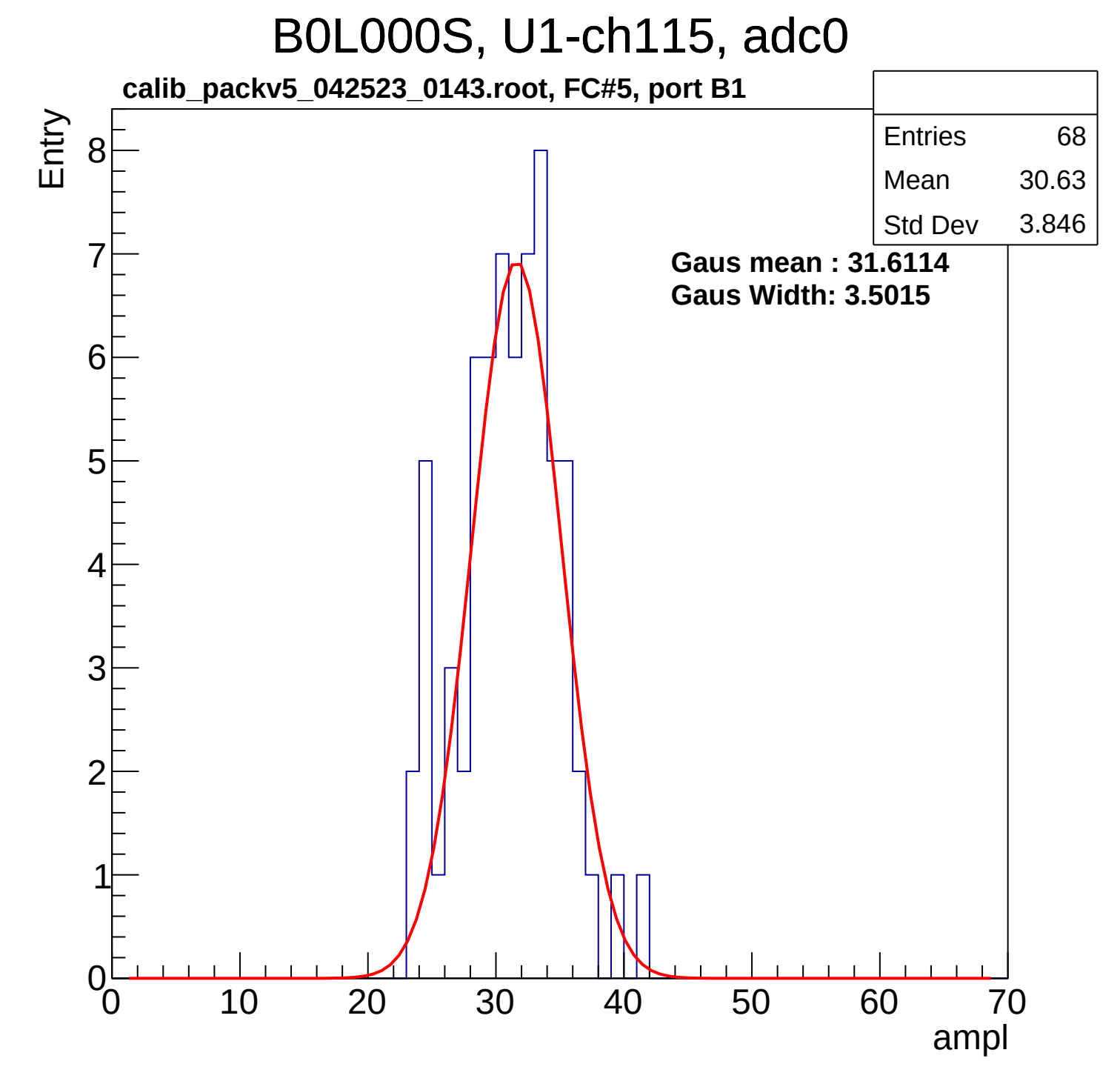
B0L000S, U1-ch115, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.63
Std Dev 3.846
Gaus mean : 31.6114
Gaus Width: 3.5015
Entry
ampl
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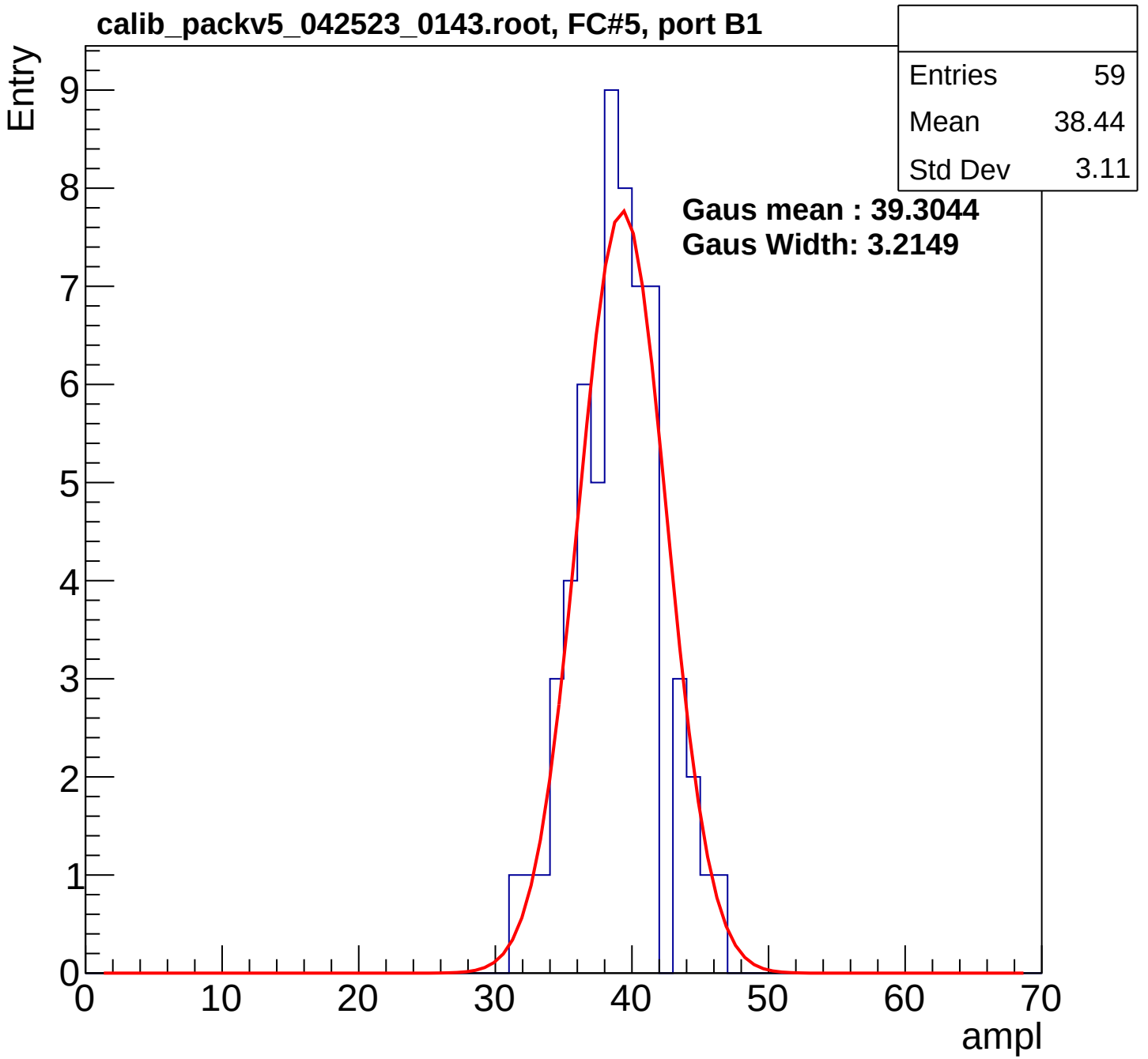
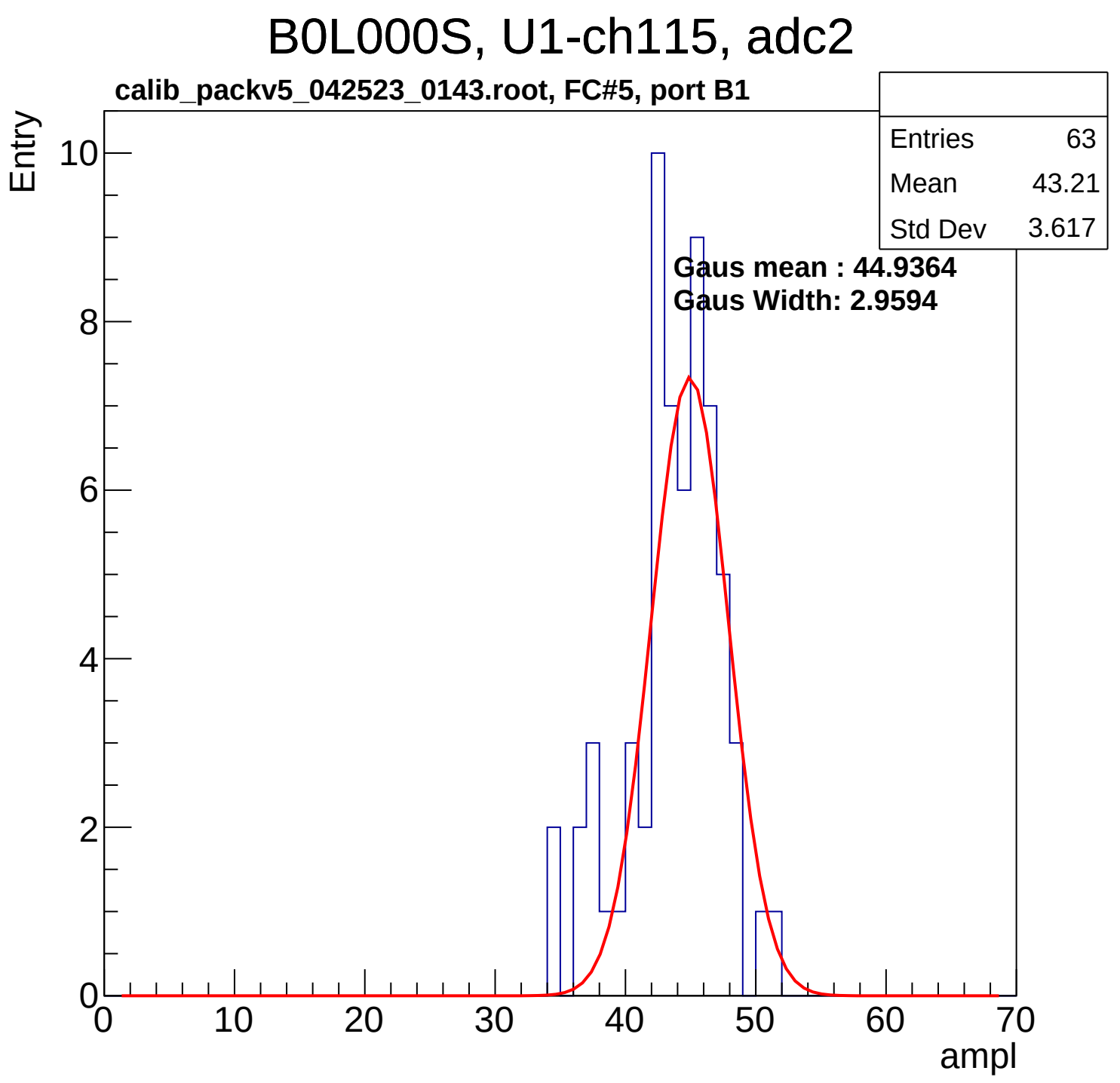
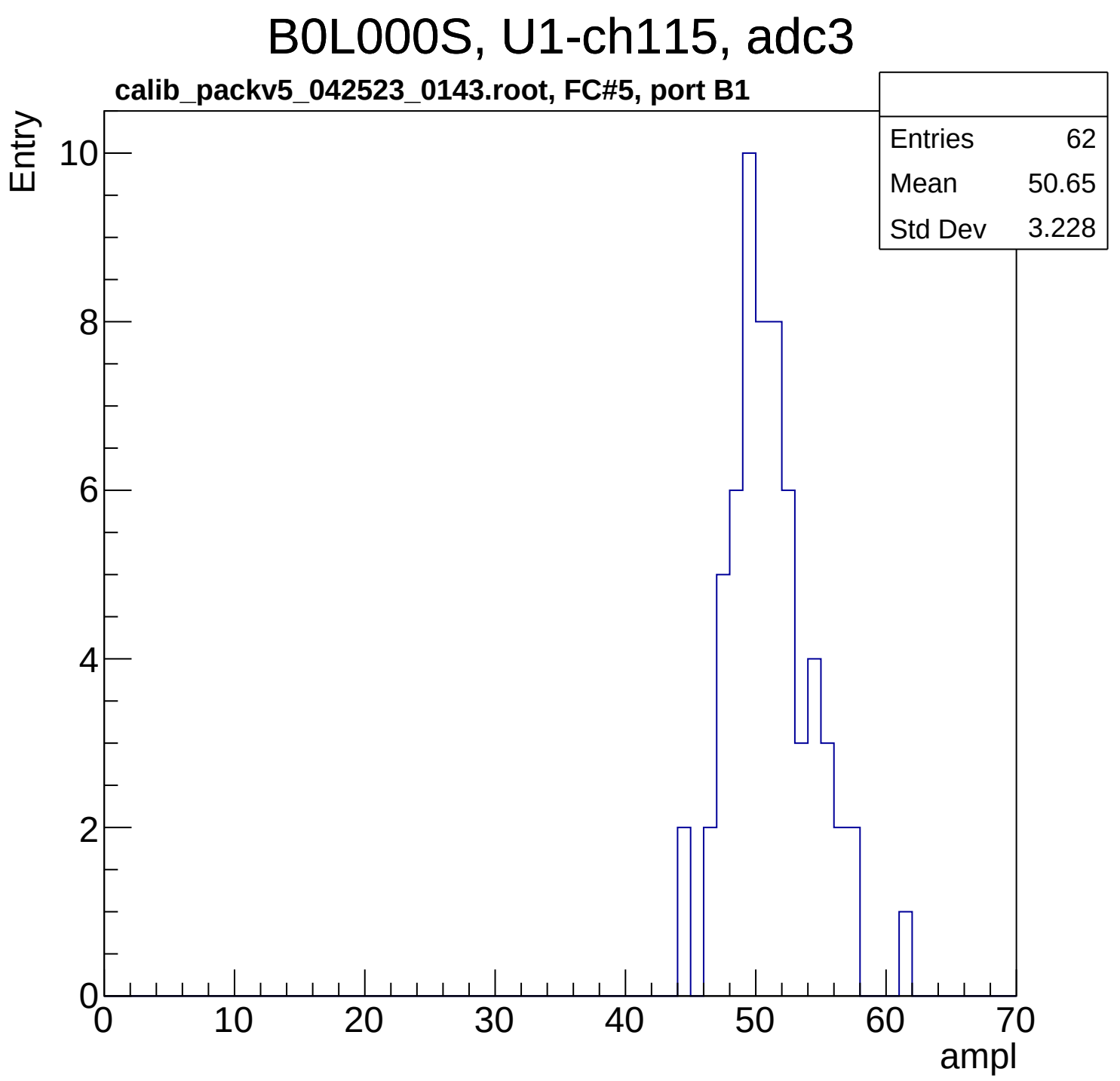
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch115, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 38.44
Std Dev 3.11
Gaus mean : 39.3044
Gaus Width: 3.2149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch115, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.21
Std Dev 3.617
Gaus mean : 44.9364
Gaus Width: 2.9594
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch115, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.65
Std Dev 3.228
Entry
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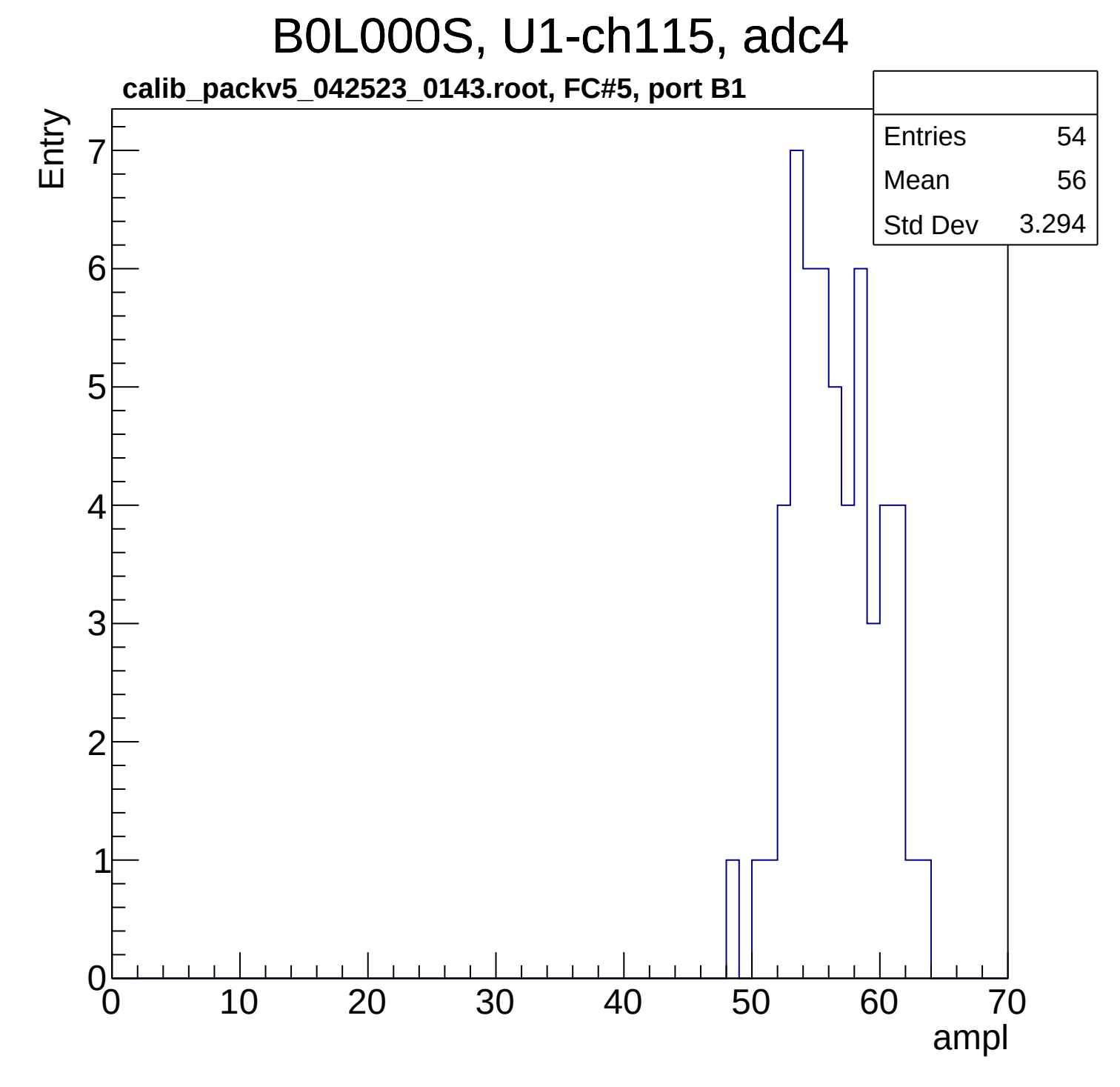
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch115, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56
Std Dev 3.294
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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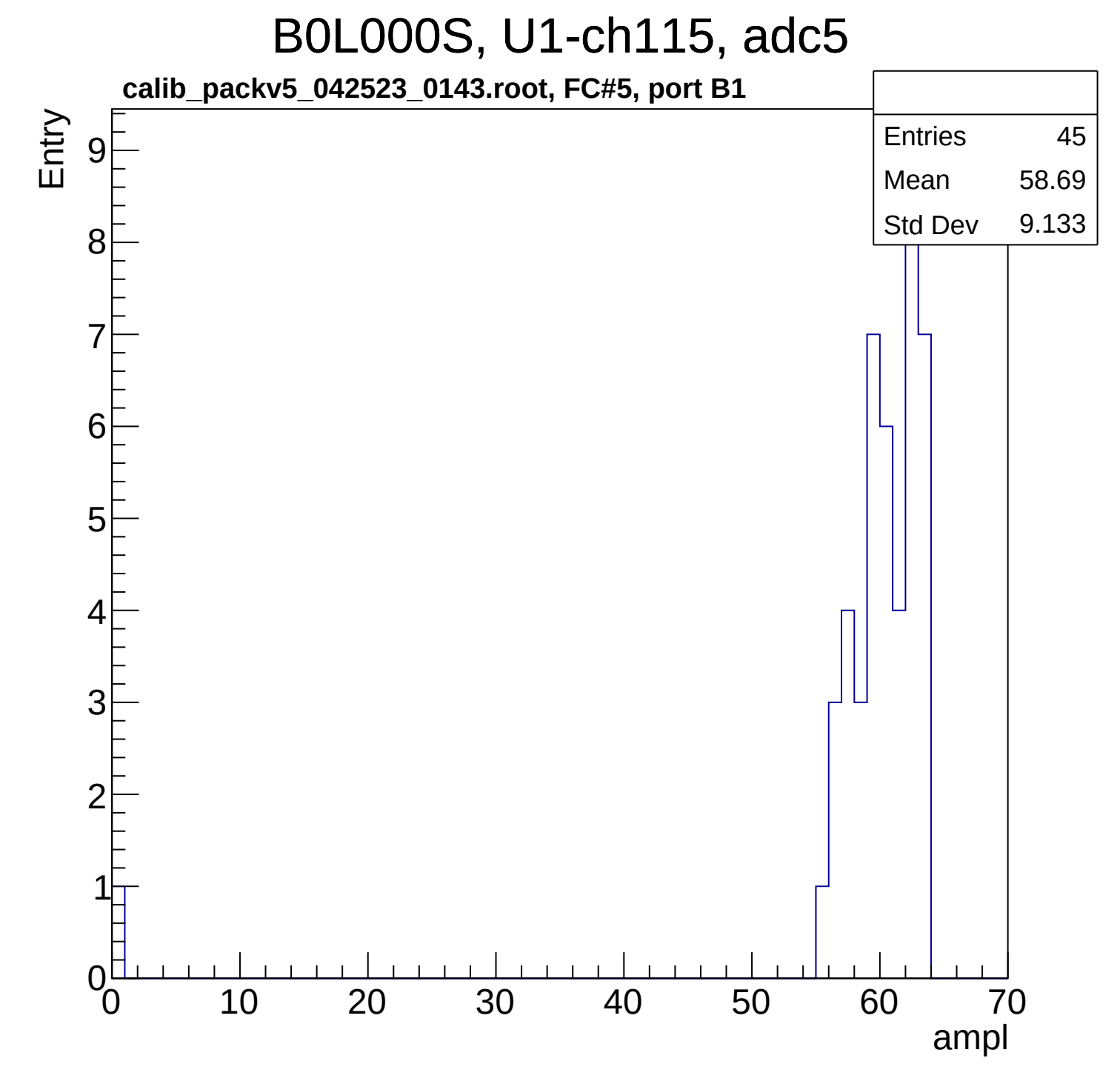
B0L000S, U1-ch115, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.69
Std Dev 9.133
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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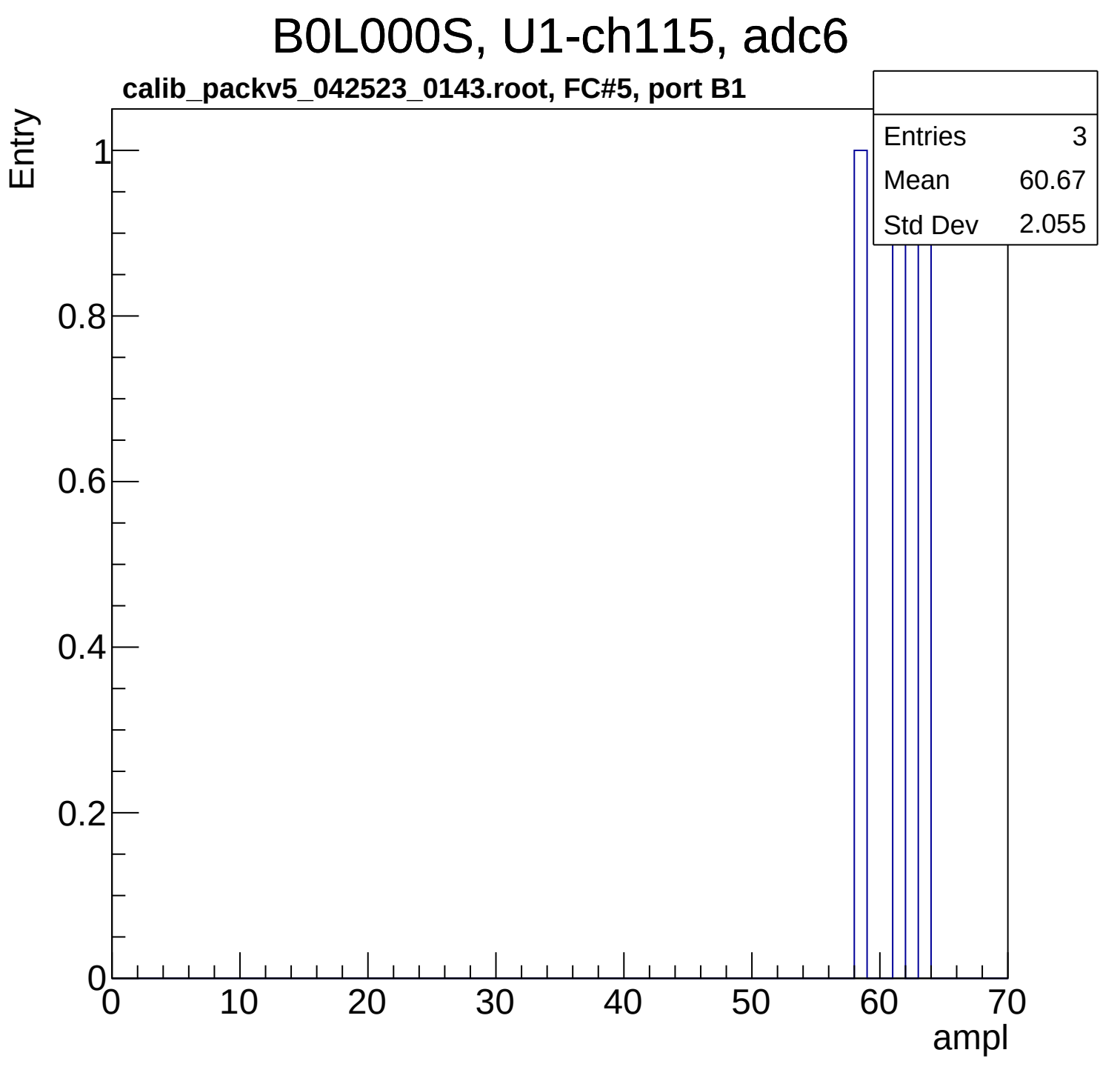
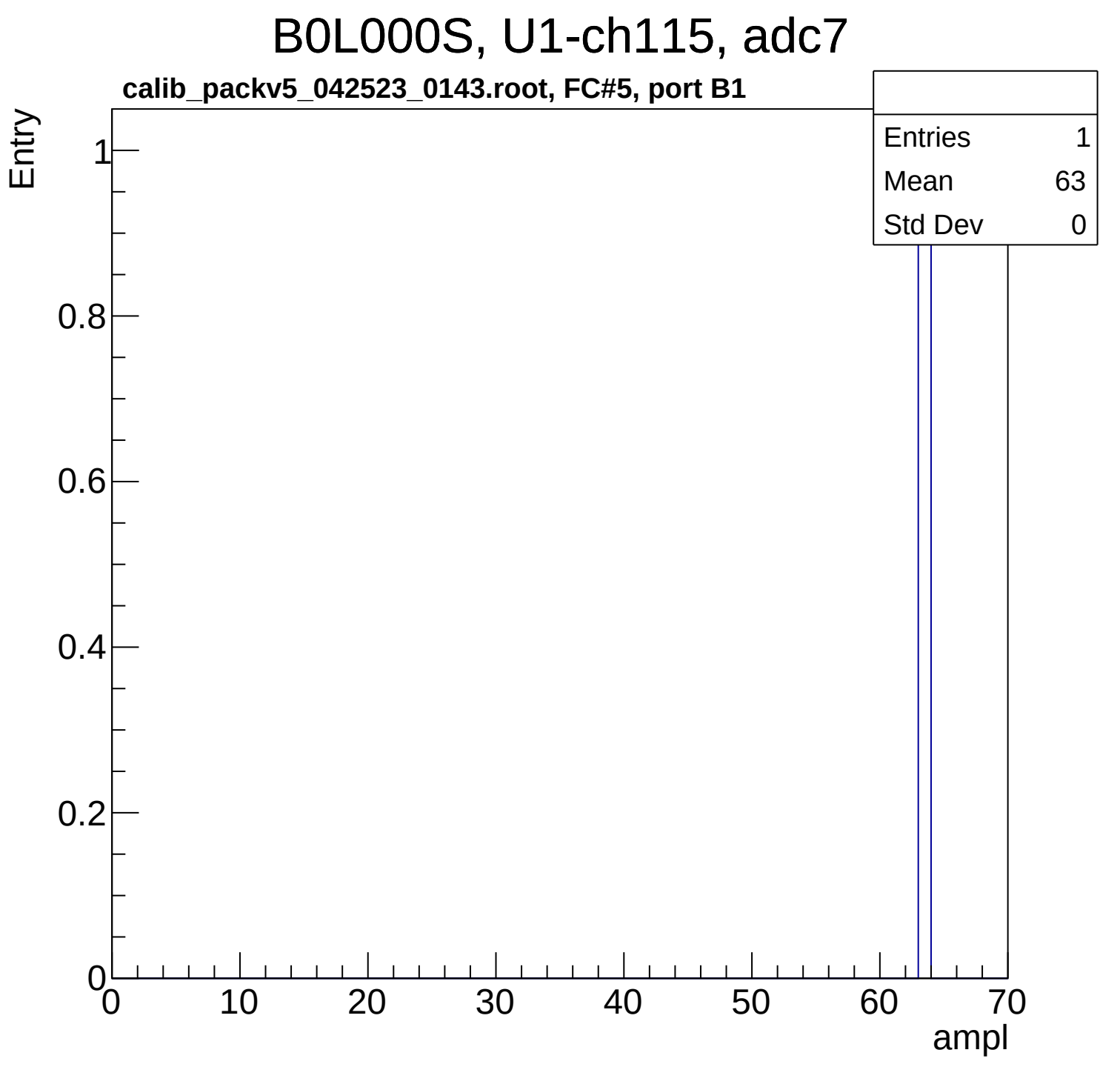
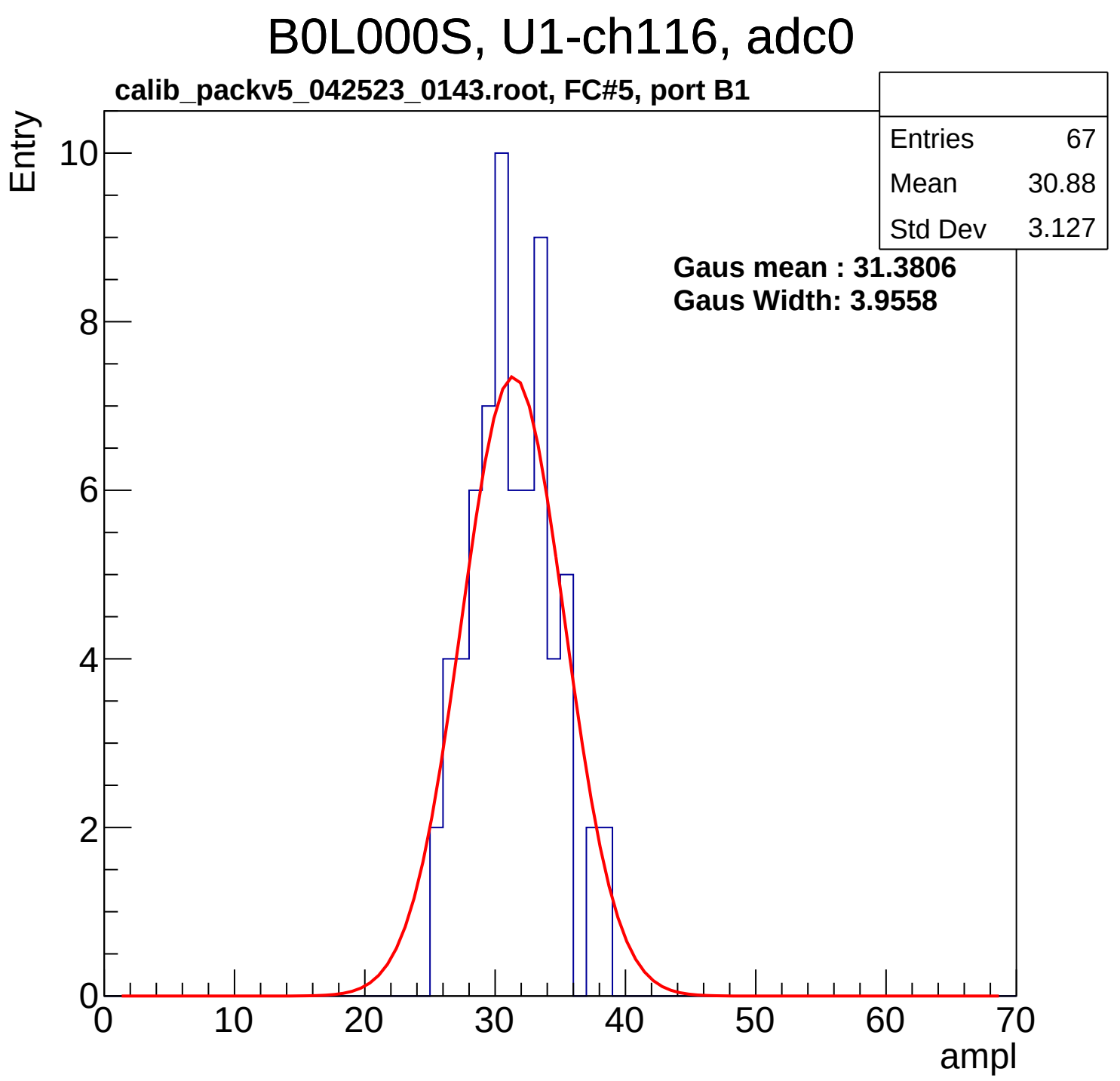

B0L000S, U1-ch115, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 60.67
Std Dev 2.055
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch115, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch116, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.88
Std Dev 3.127
Gaus mean : 31.3806
Gaus Width: 3.9558
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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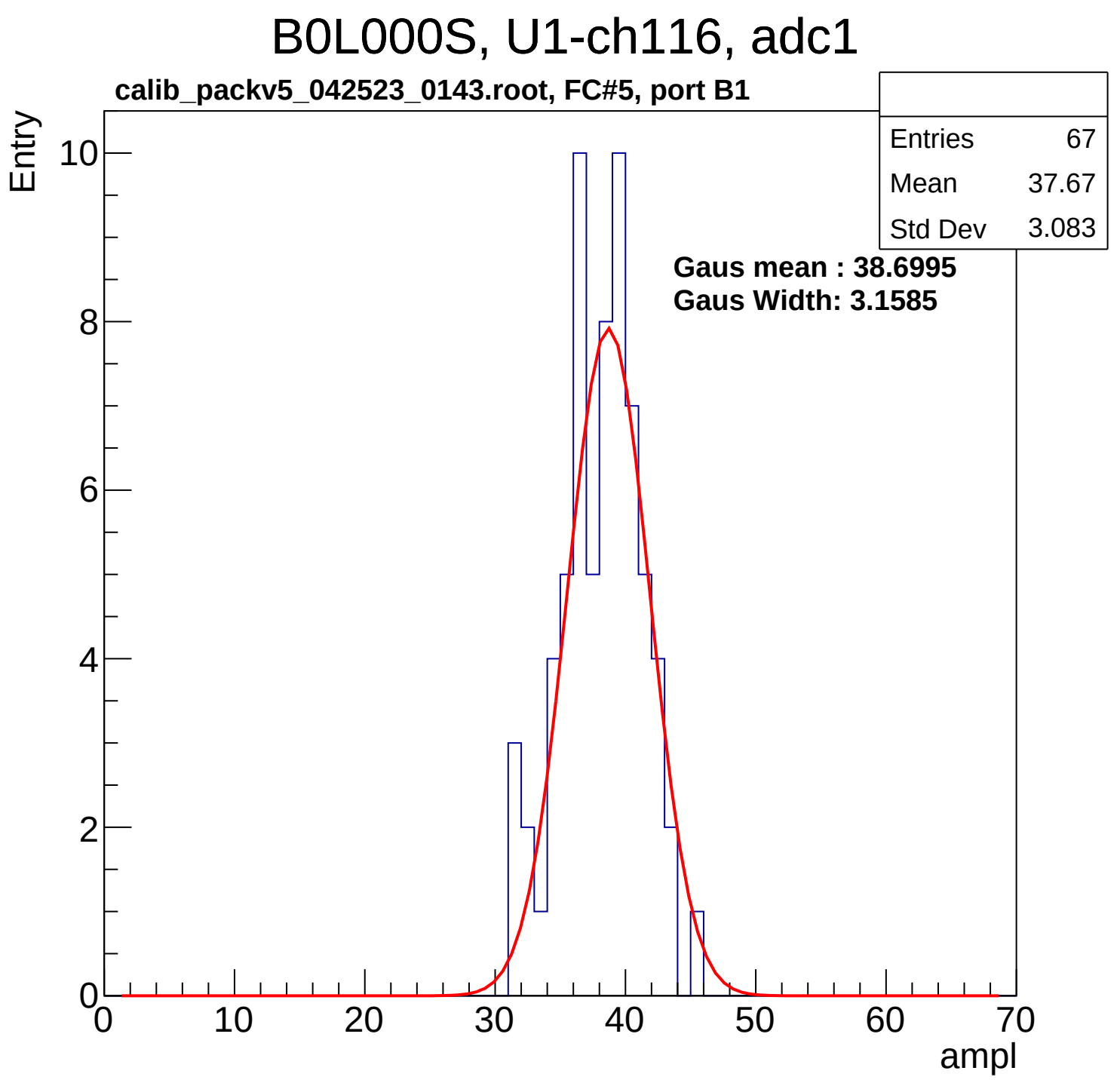
B0L000S, U1-ch116, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.67
Std Dev 3.083
Gaus mean : 38.6995
Gaus Width: 3.1585
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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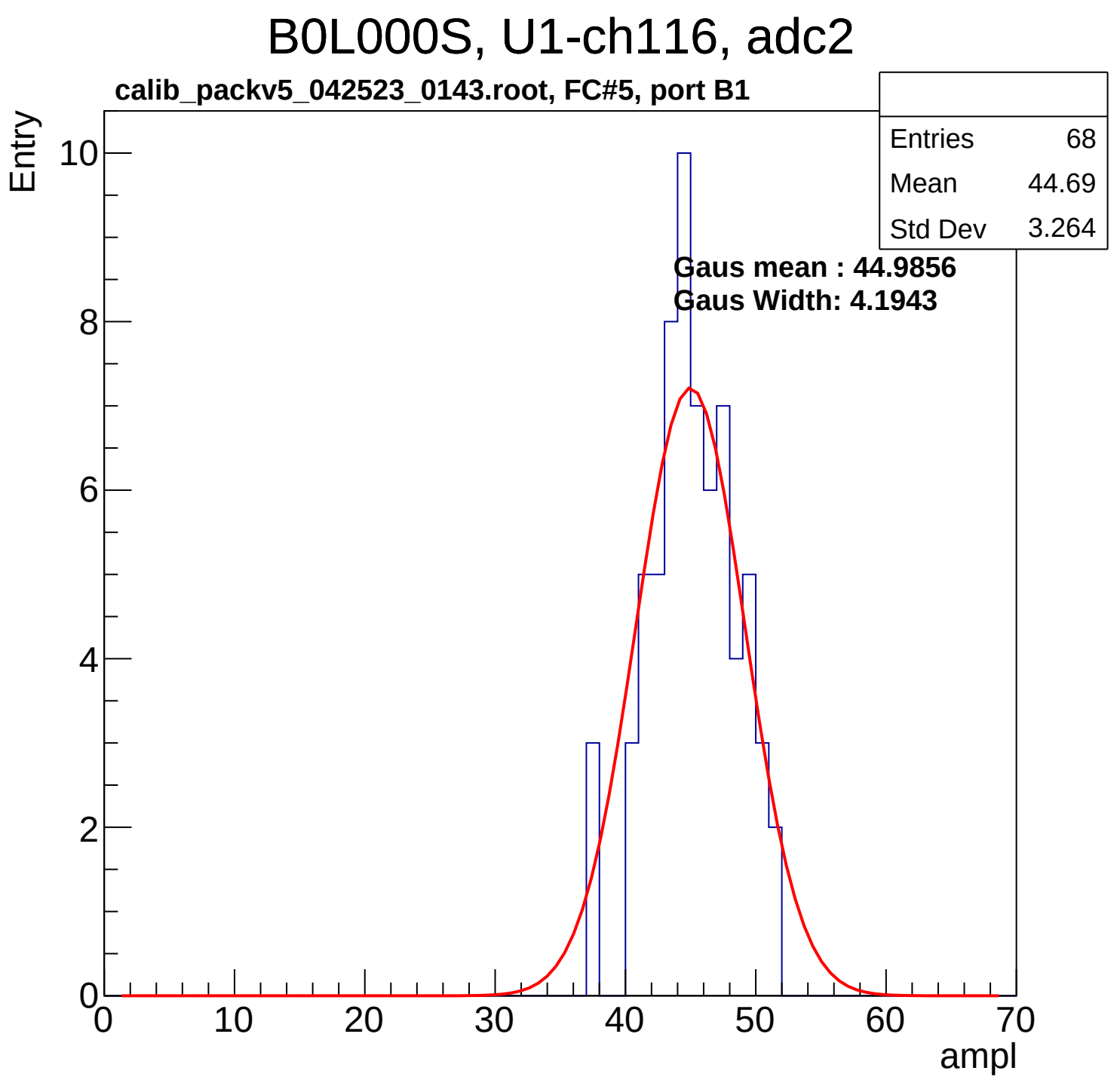
B0L000S, U1-ch116, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 44.69
Std Dev 3.264
Gaus mean : 44.9856
Gaus Width: 4.1943
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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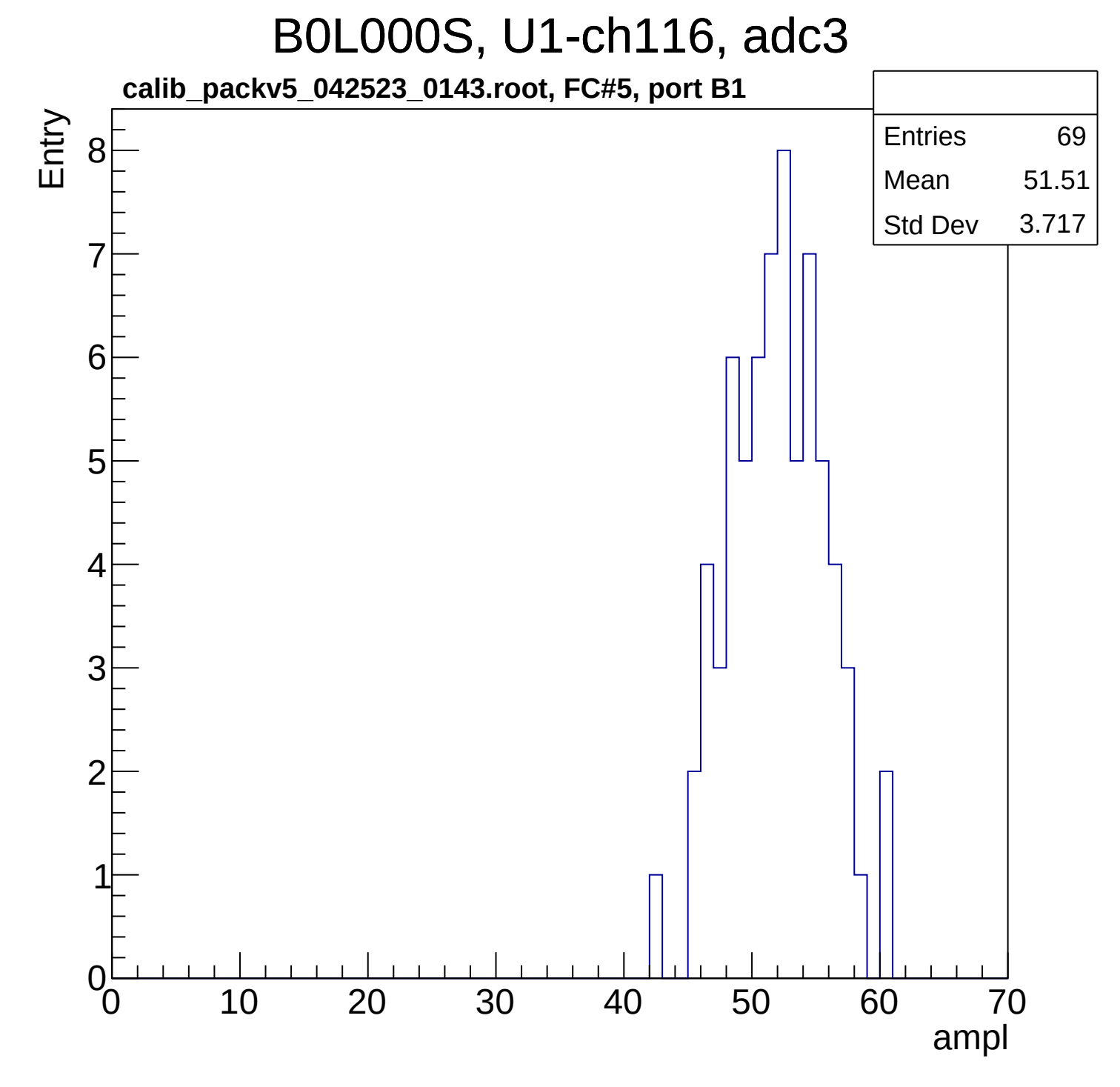
B0L000S, U1-ch116, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 51.51
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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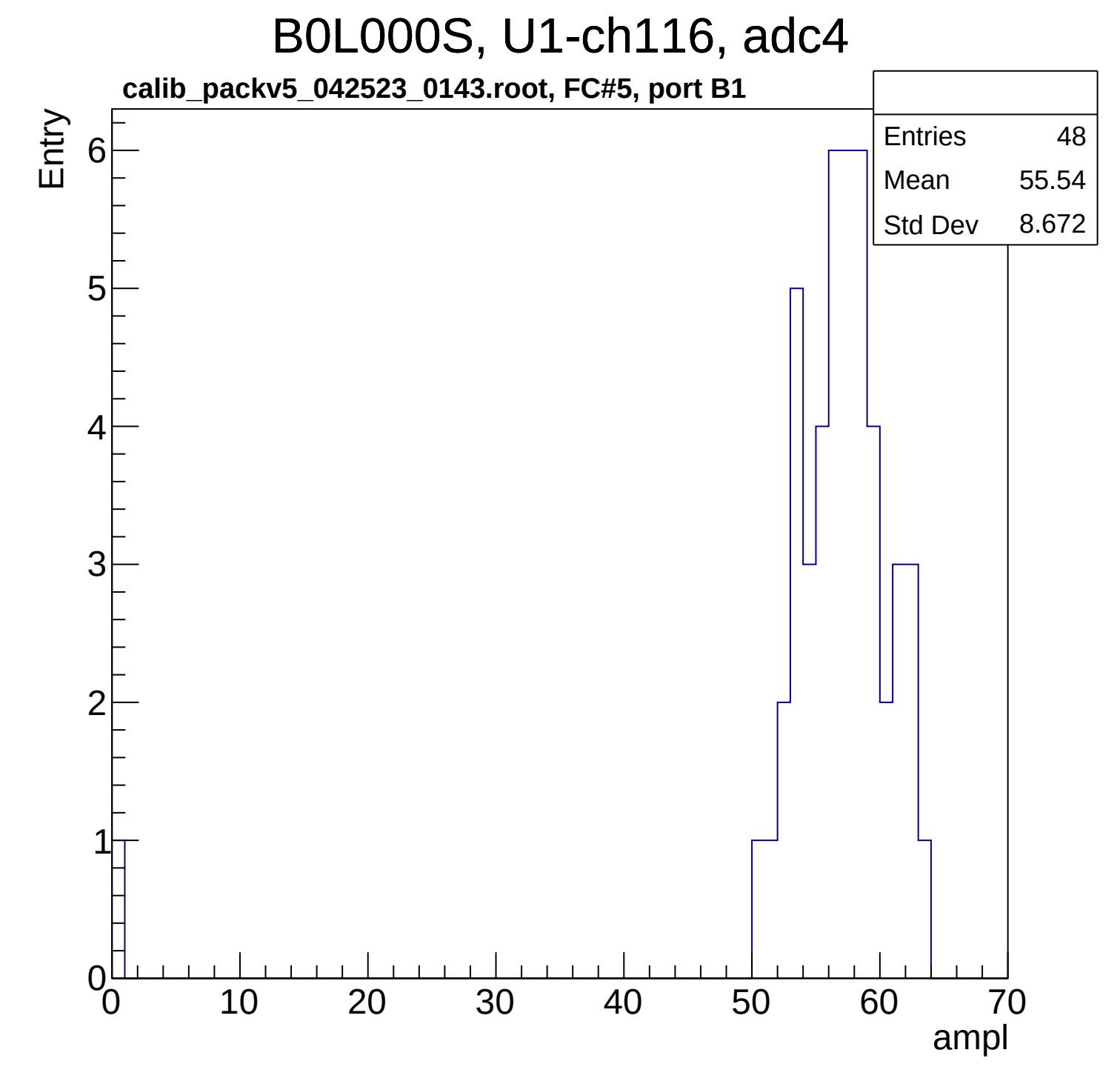
B0L000S, U1-ch116, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 55.54
Std Dev 8.672
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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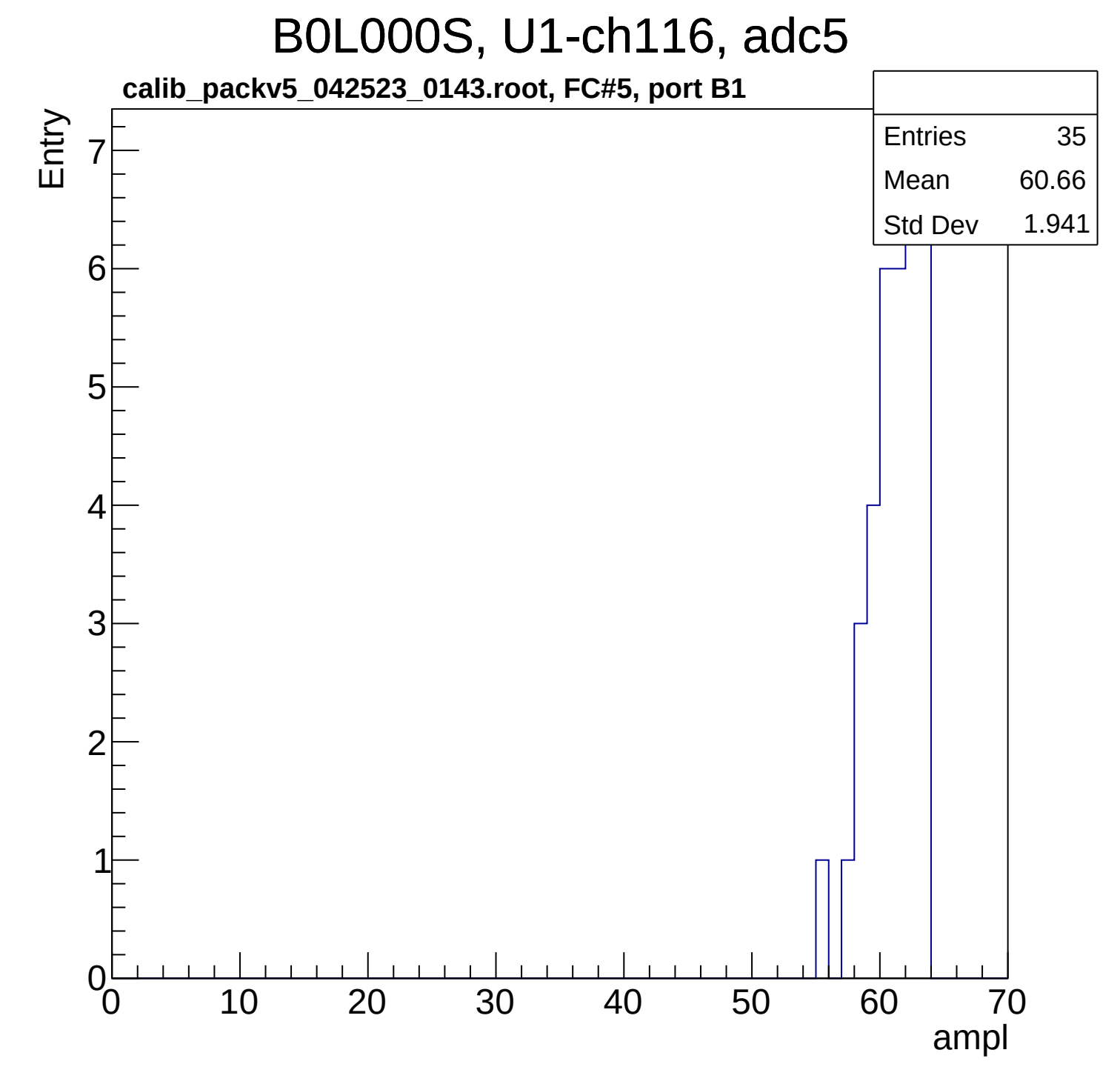
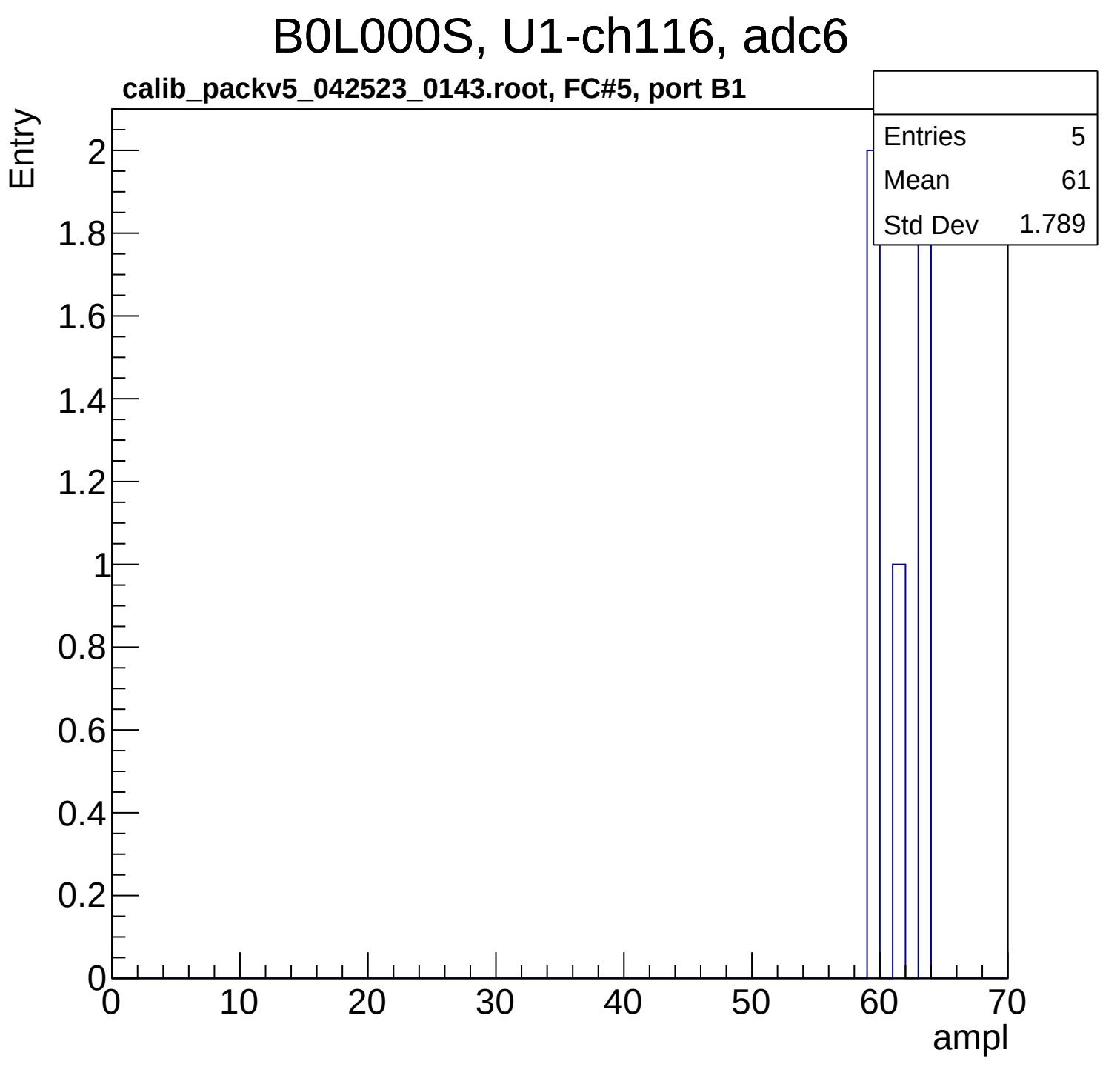

B0L000S, U1-ch116, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 60.66
Std Dev 1.941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch116, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61
Std Dev 1.789
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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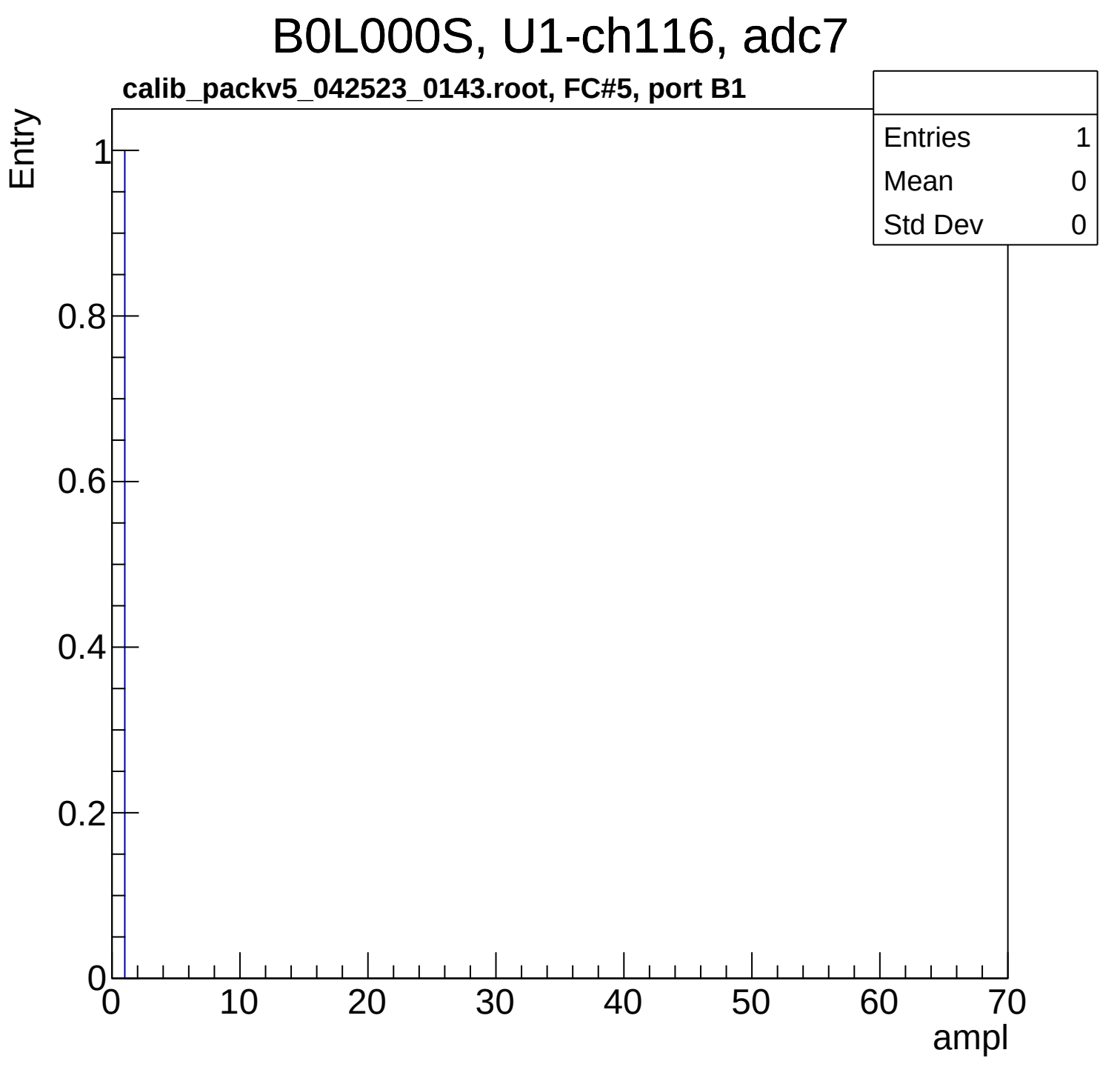
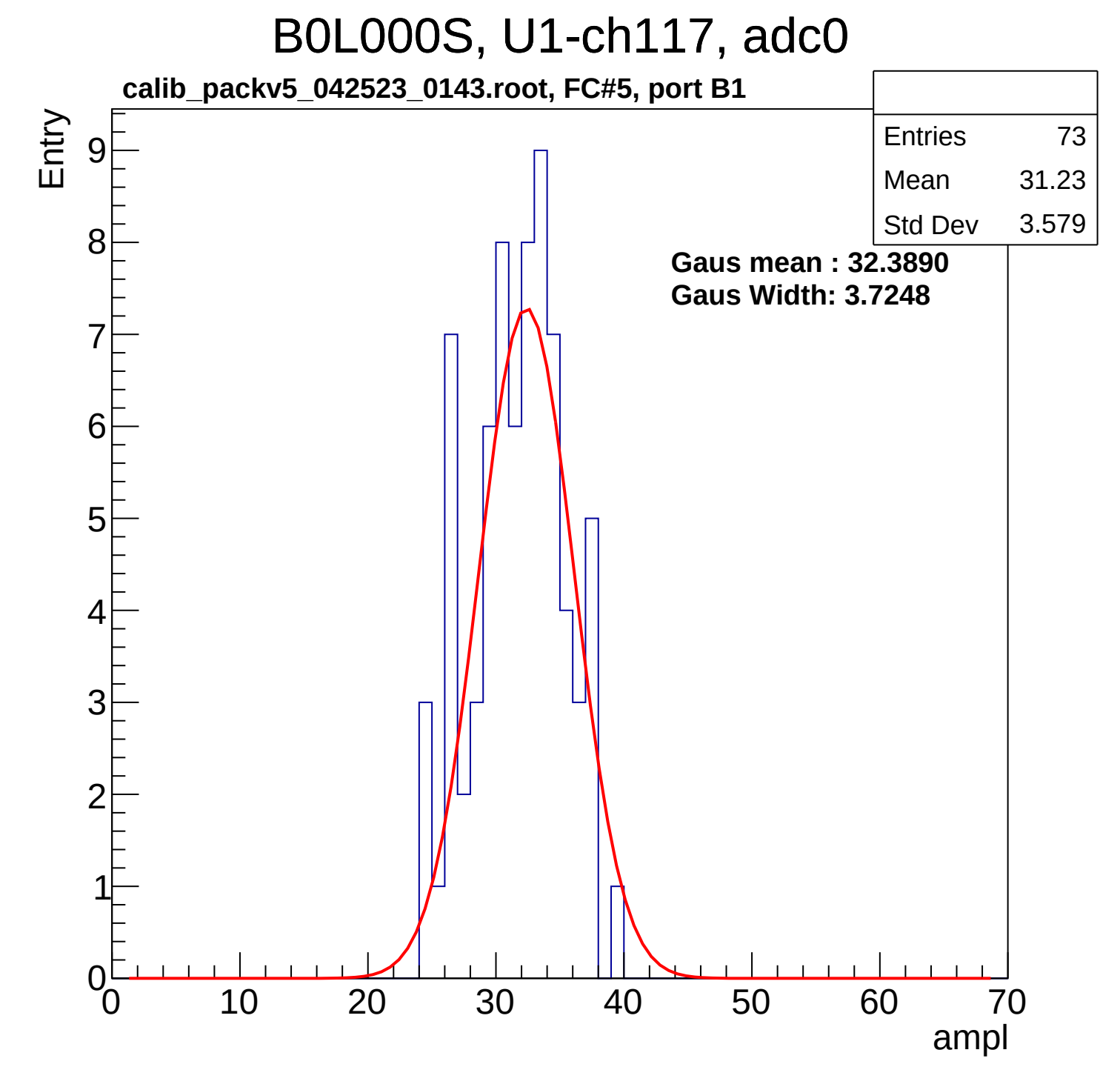
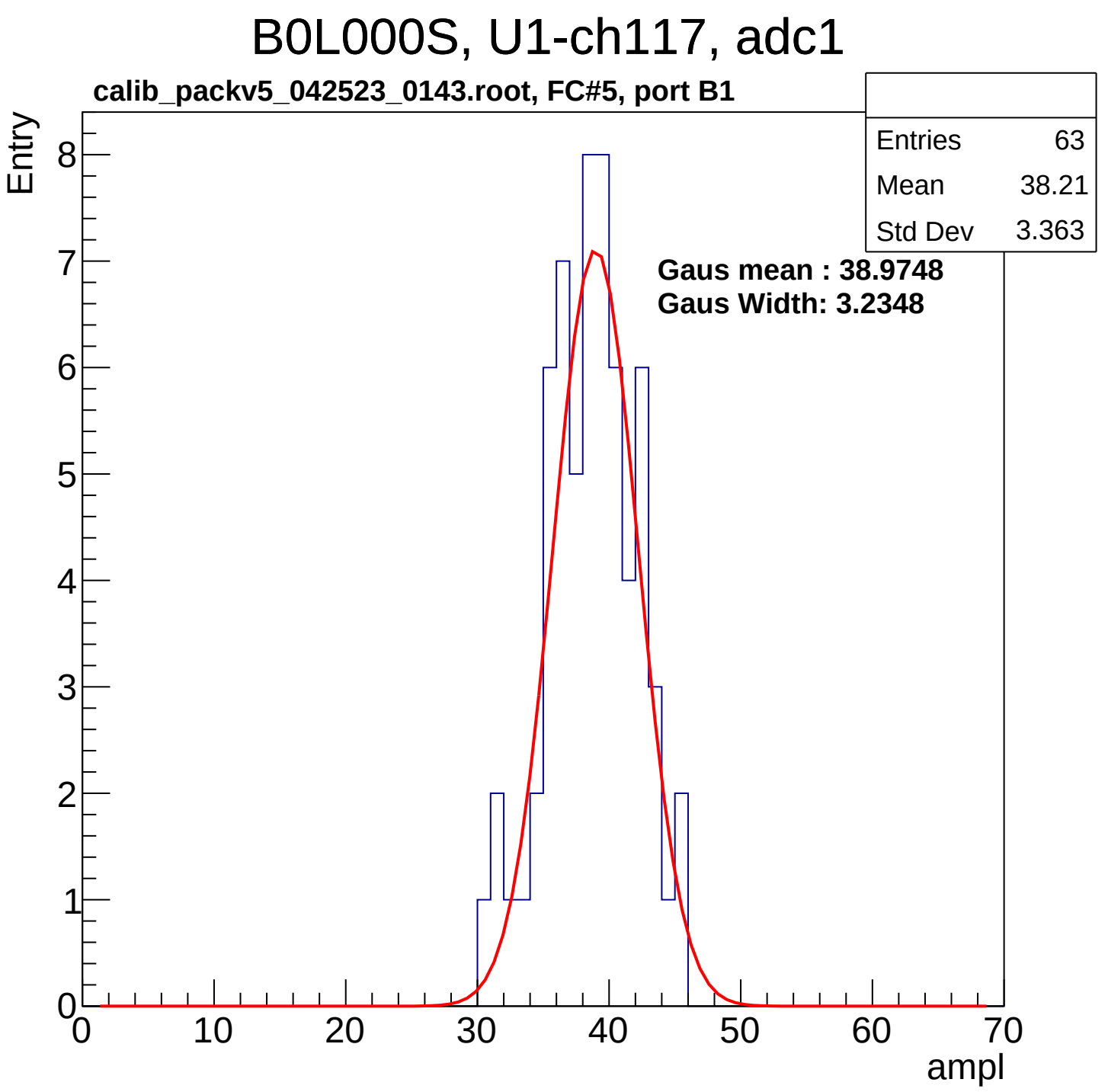
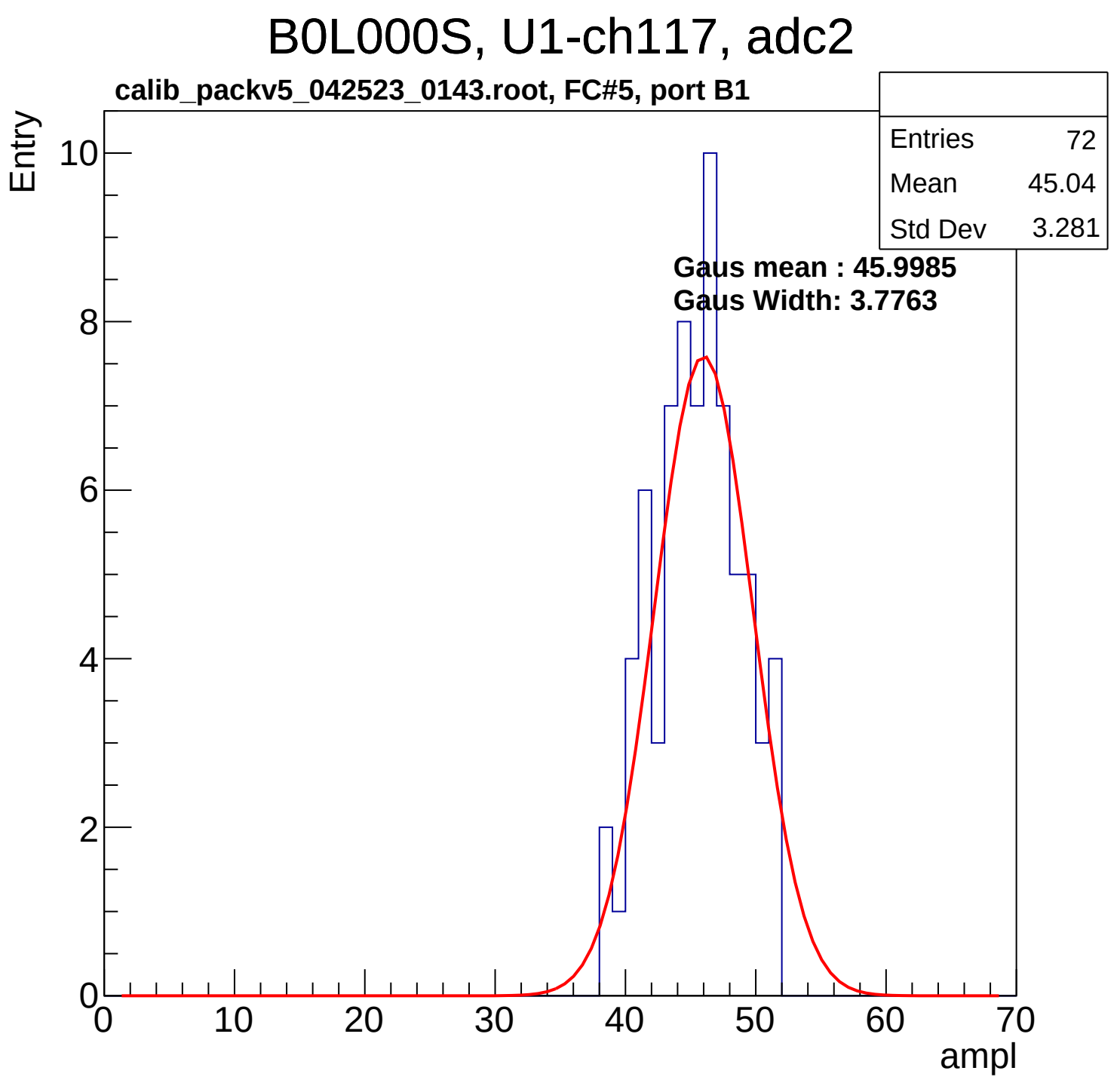
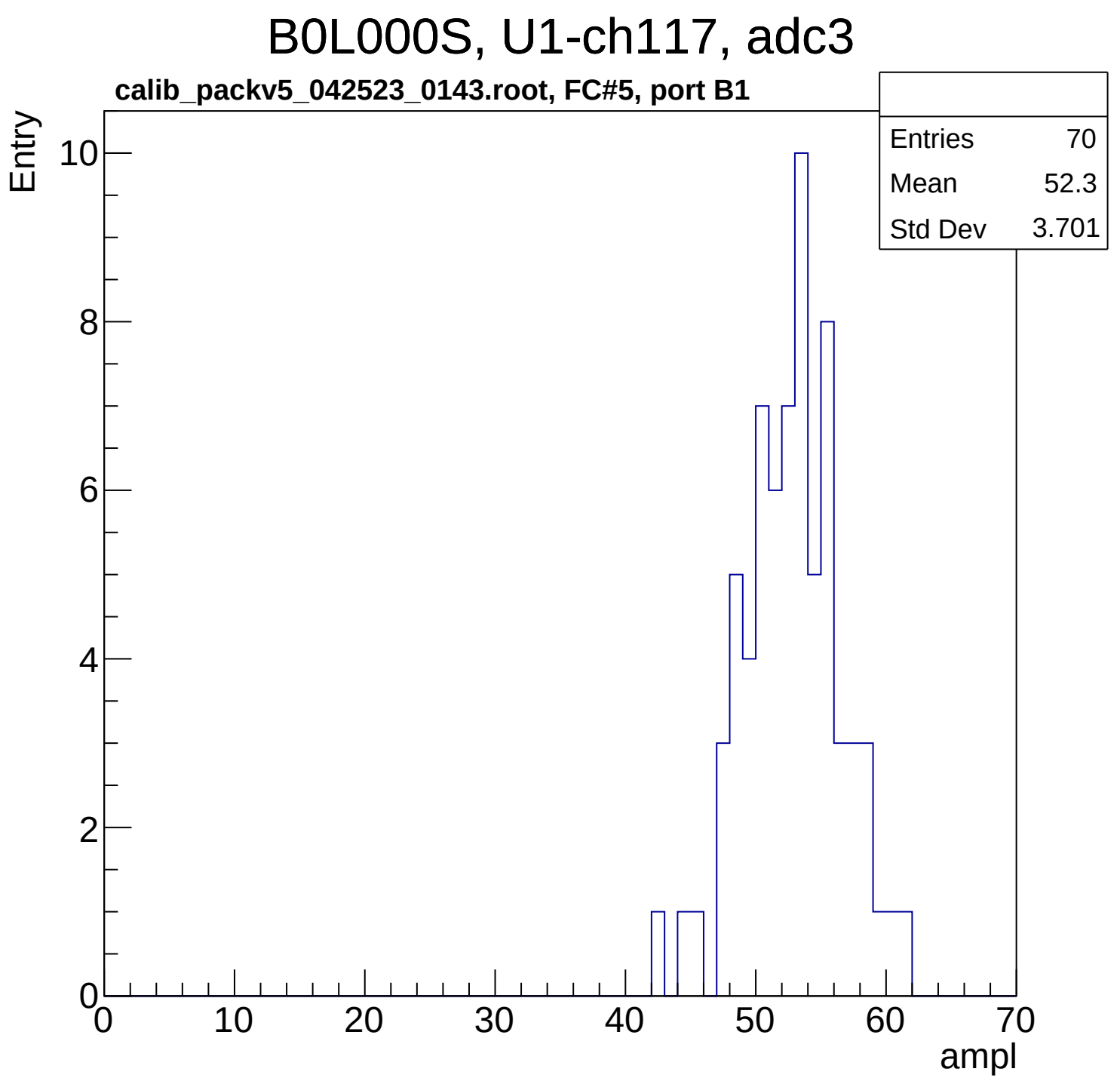
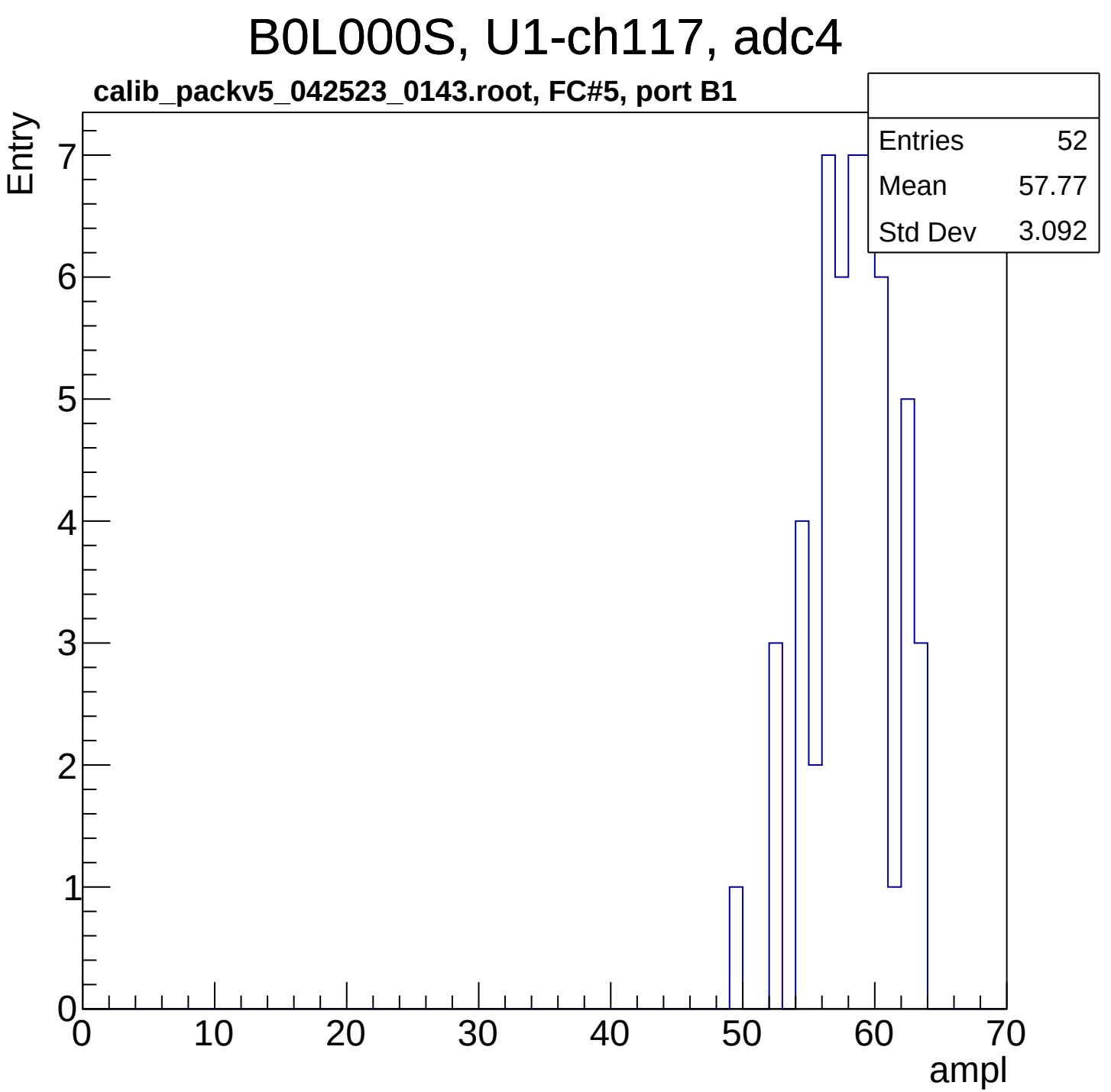

B0L000S, U1-ch116, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

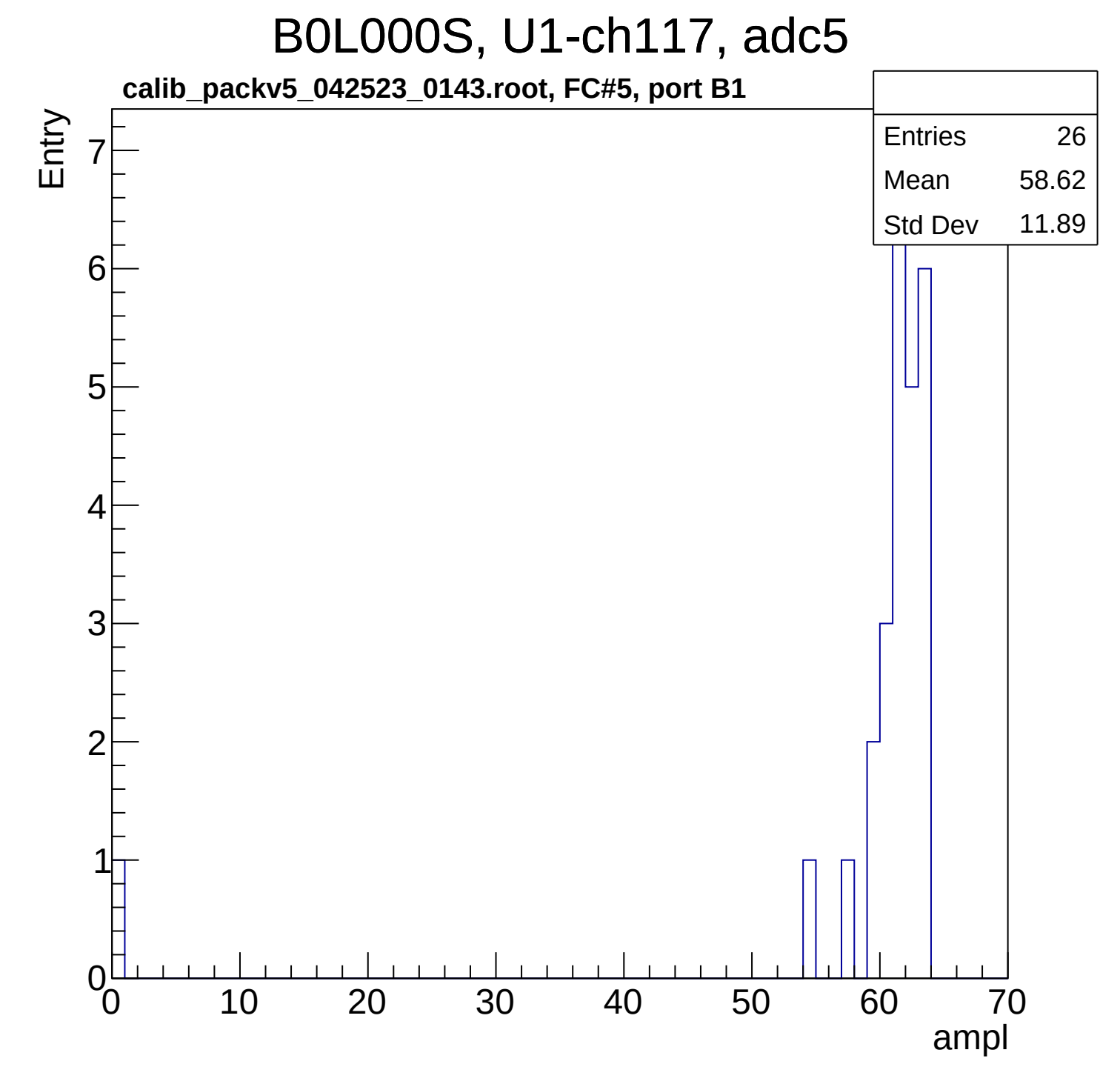

B0L000S, U1-ch117, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 31.23
Std Dev 3.579
Gaus mean : 32.3890
Gaus Width: 3.7248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

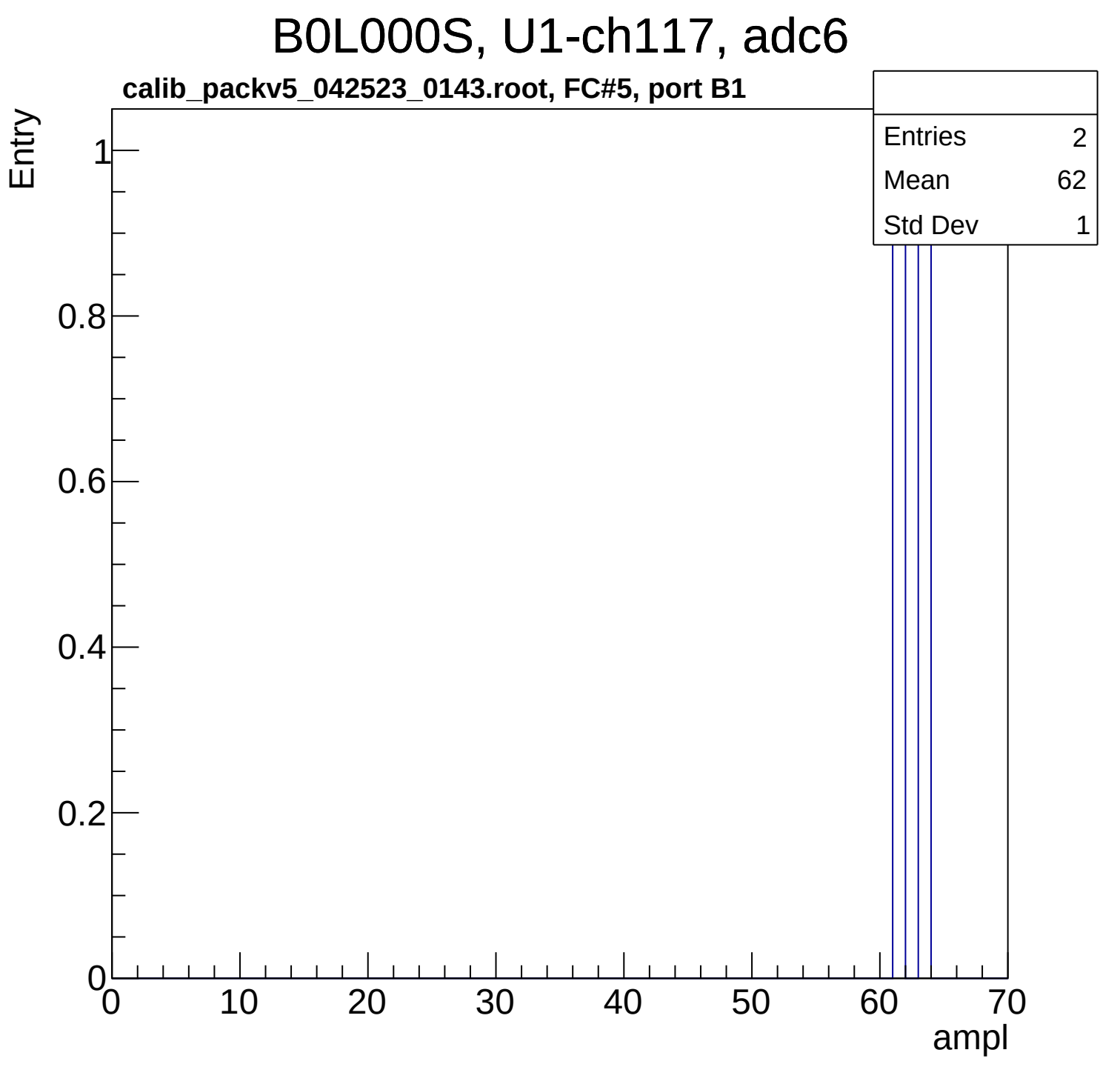

B0L000S, U1-ch117, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 38.21
Std Dev 3.363
Gaus mean : 38.9748
Gaus Width: 3.2348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

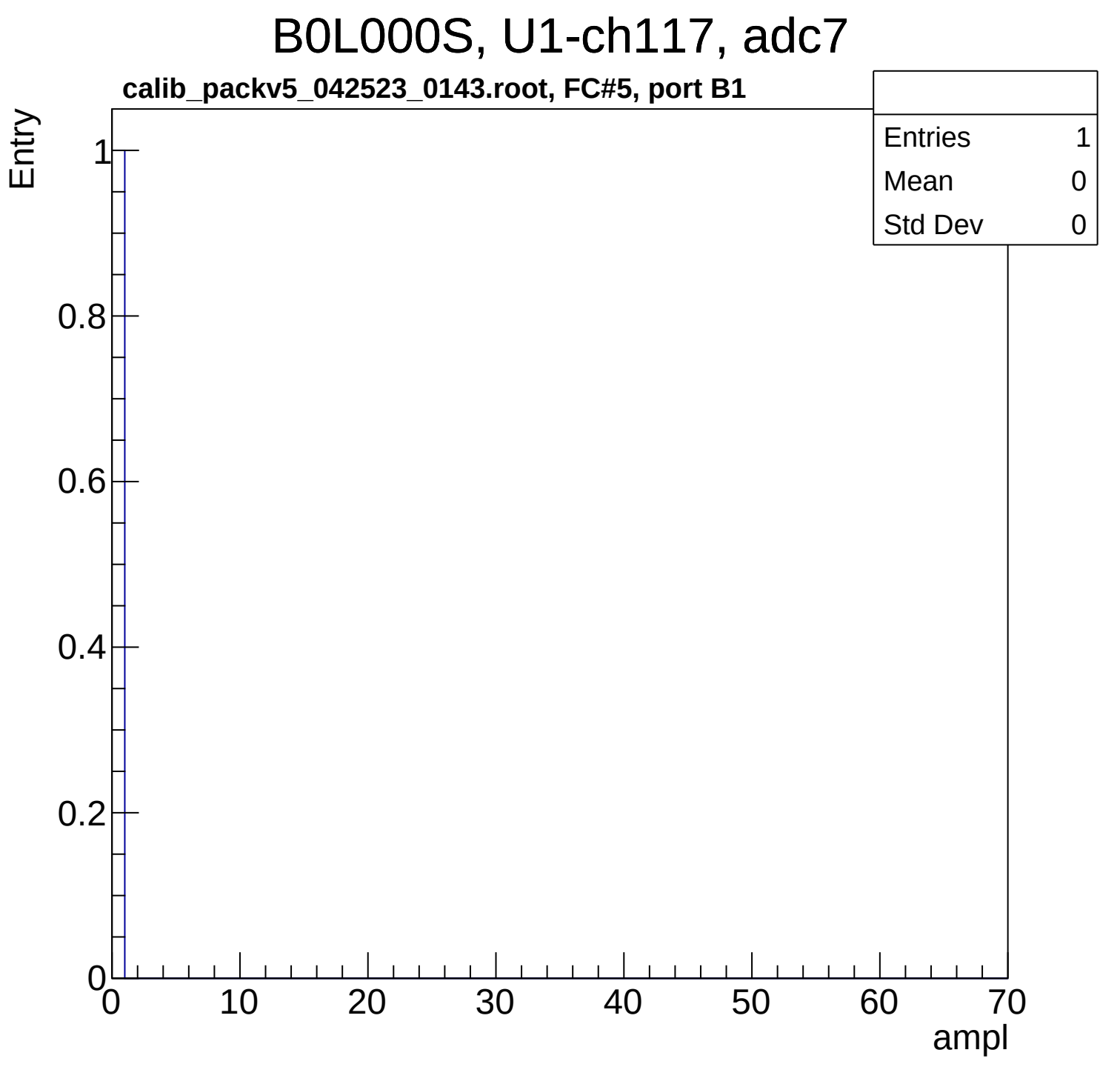

B0L000S, U1-ch117, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 45.04
Std Dev 3.281
Gaus mean : 45.9985
Gaus Width: 3.7763
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

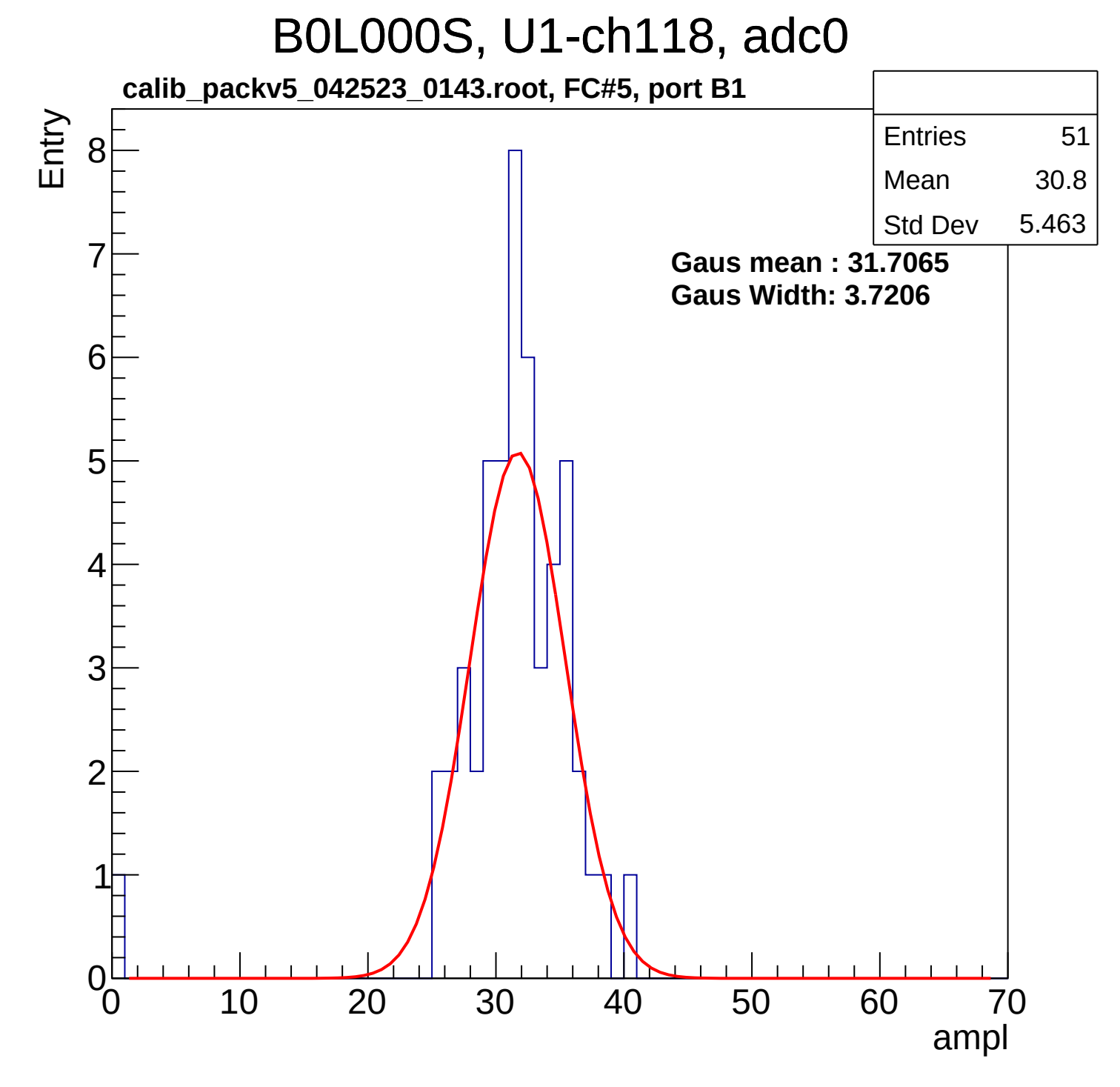

B0L000S, U1-ch117, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 52.3
Std Dev 3.701
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch117, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.77
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch117, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 58.62
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch117, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch117, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch118, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 30.8
Std Dev 5.463
Gaus mean : 31.7065
Gaus Width: 3.7206
Entry
ampl
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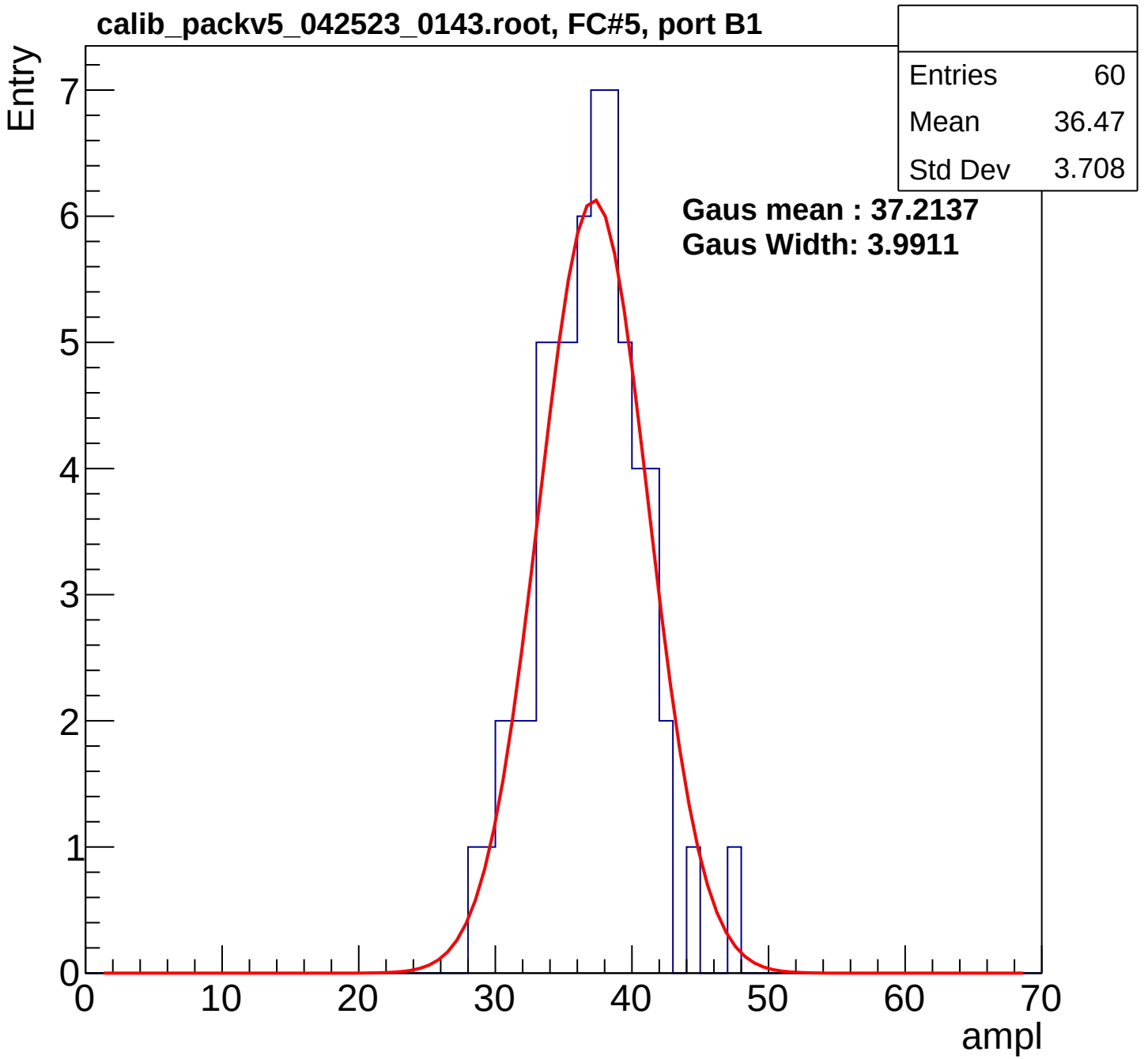
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch118, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.47
Std Dev 3.708
Gaus mean : 37.2137
Gaus Width: 3.9911
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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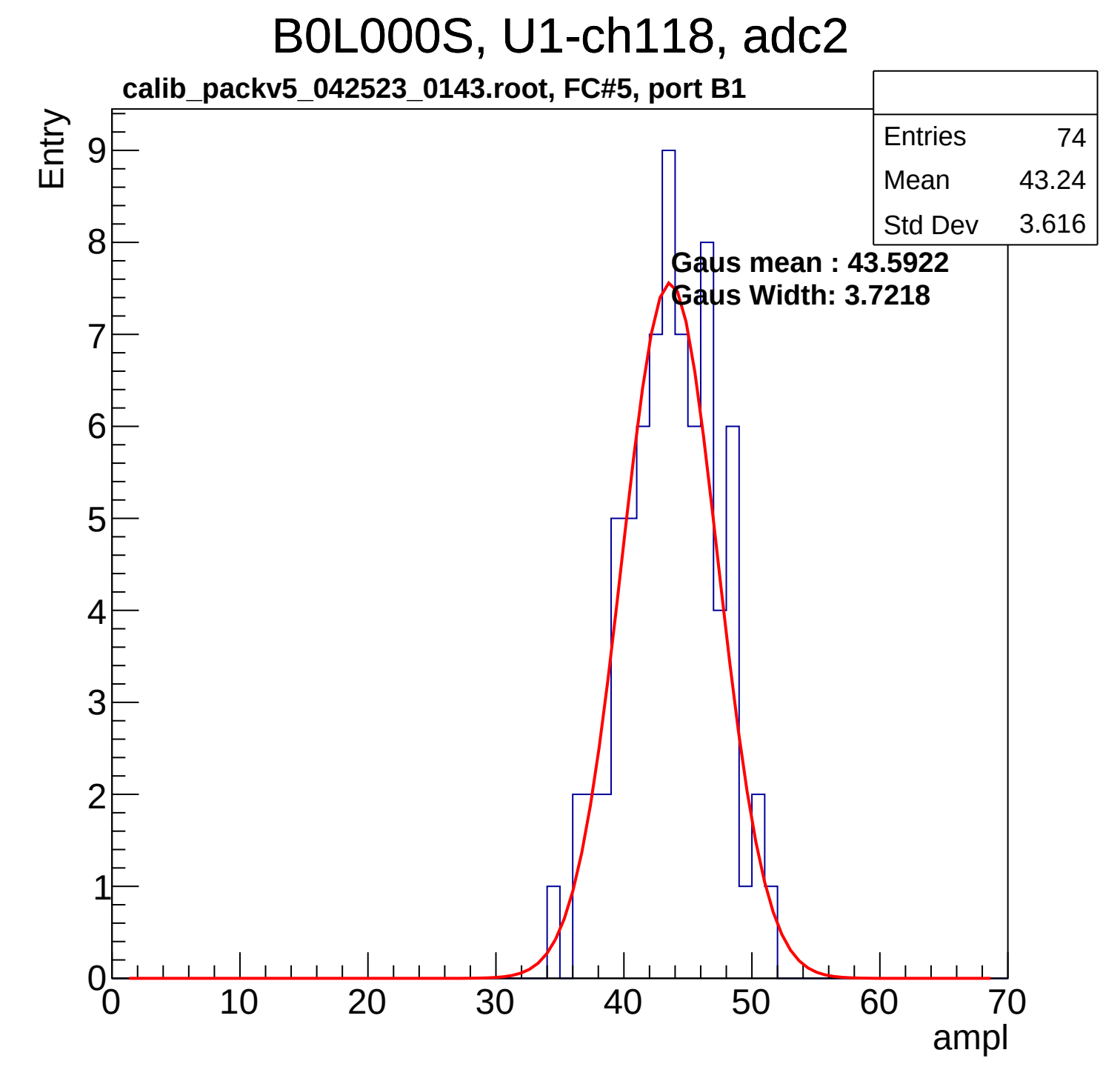
B0L000S, U1-ch118, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 43.24
Std Dev 3.616
Gaus mean : 43.5922
Gaus Width: 3.7218
Entry
ampl
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10
20
30
40
50
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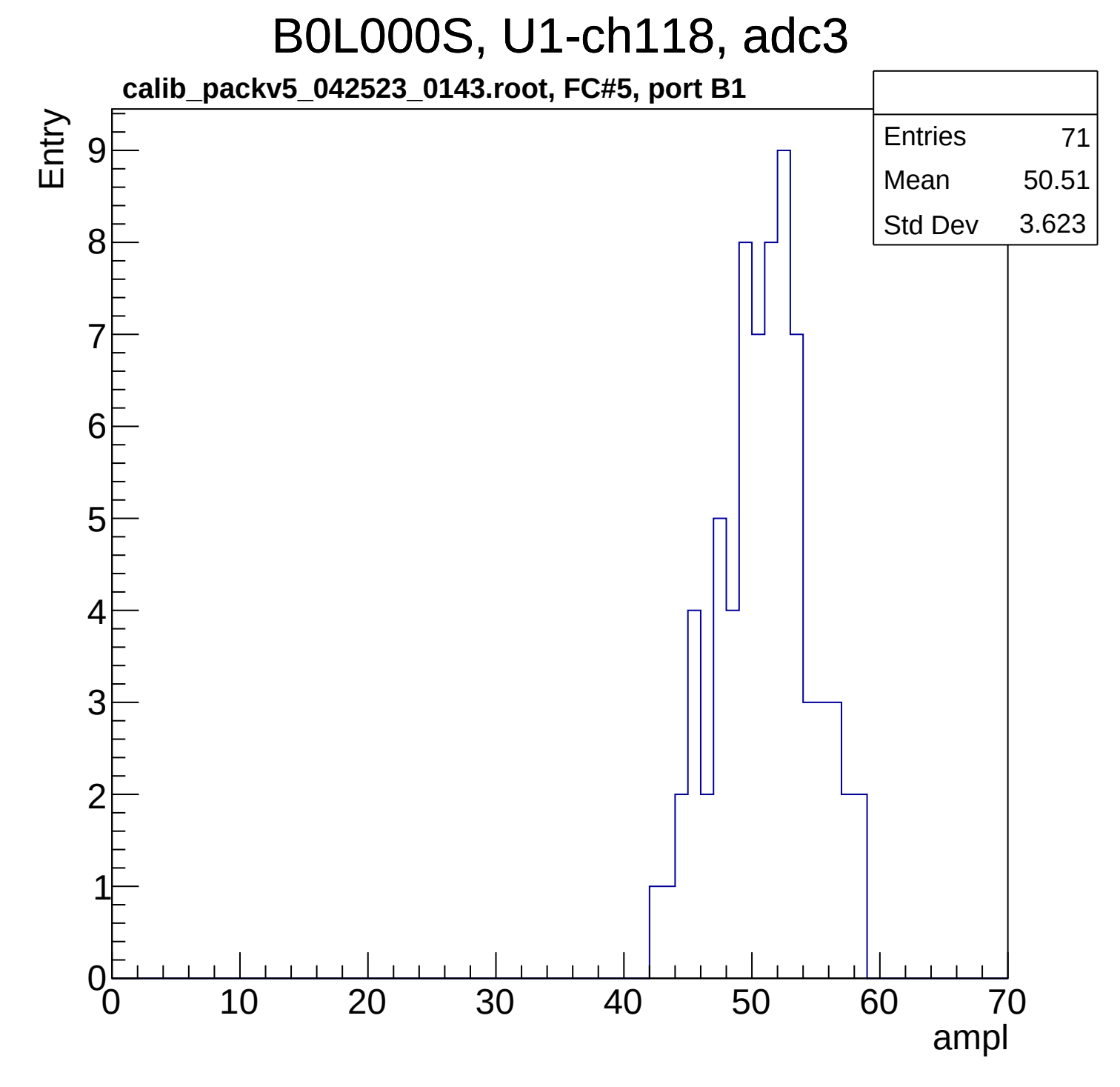
1
2
3
4
5
6
7
8
9


B0L000S, U1-ch118, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.51
Std Dev 3.623
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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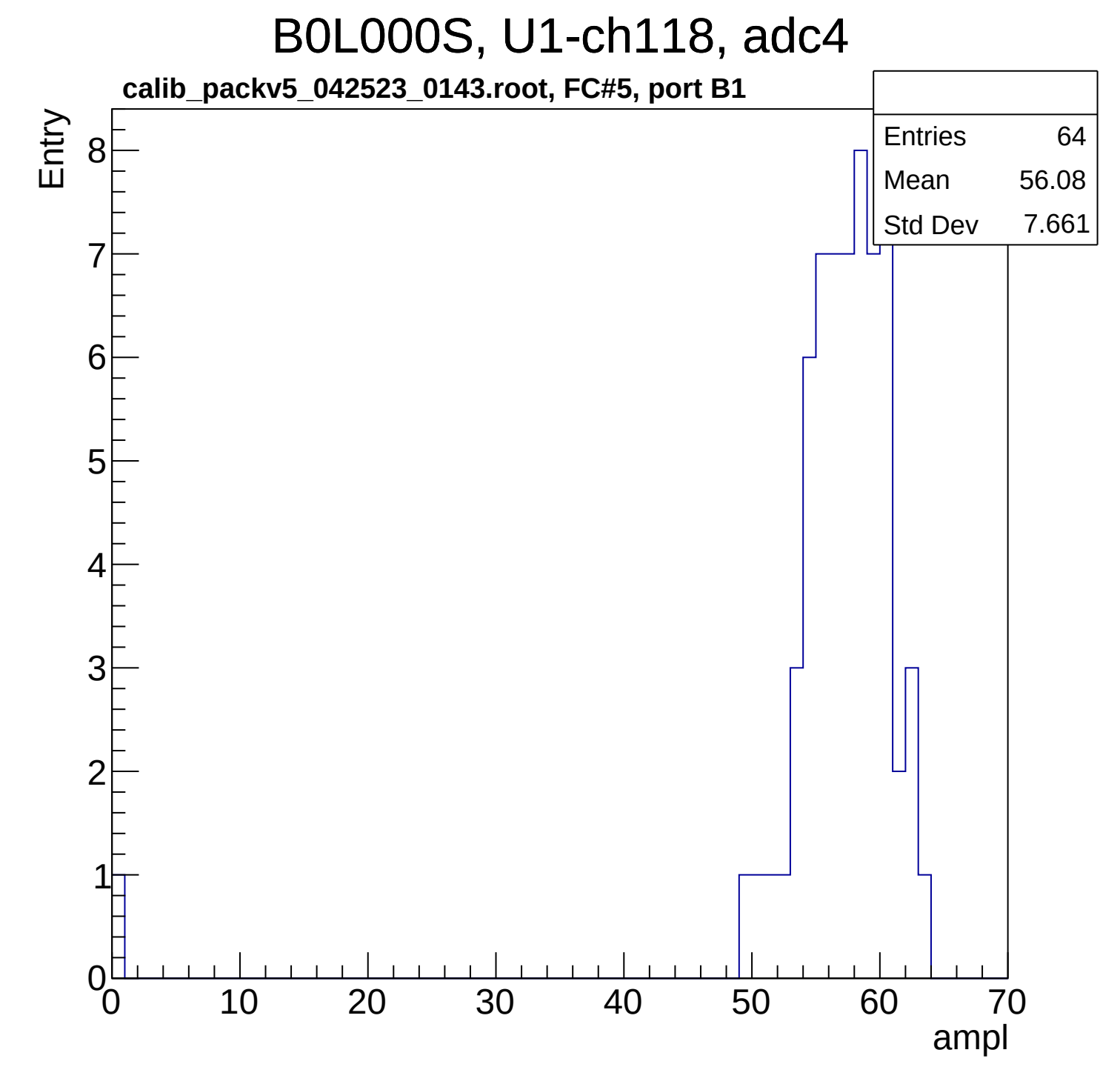
B0L000S, U1-ch118, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 56.08
Std Dev 7.661
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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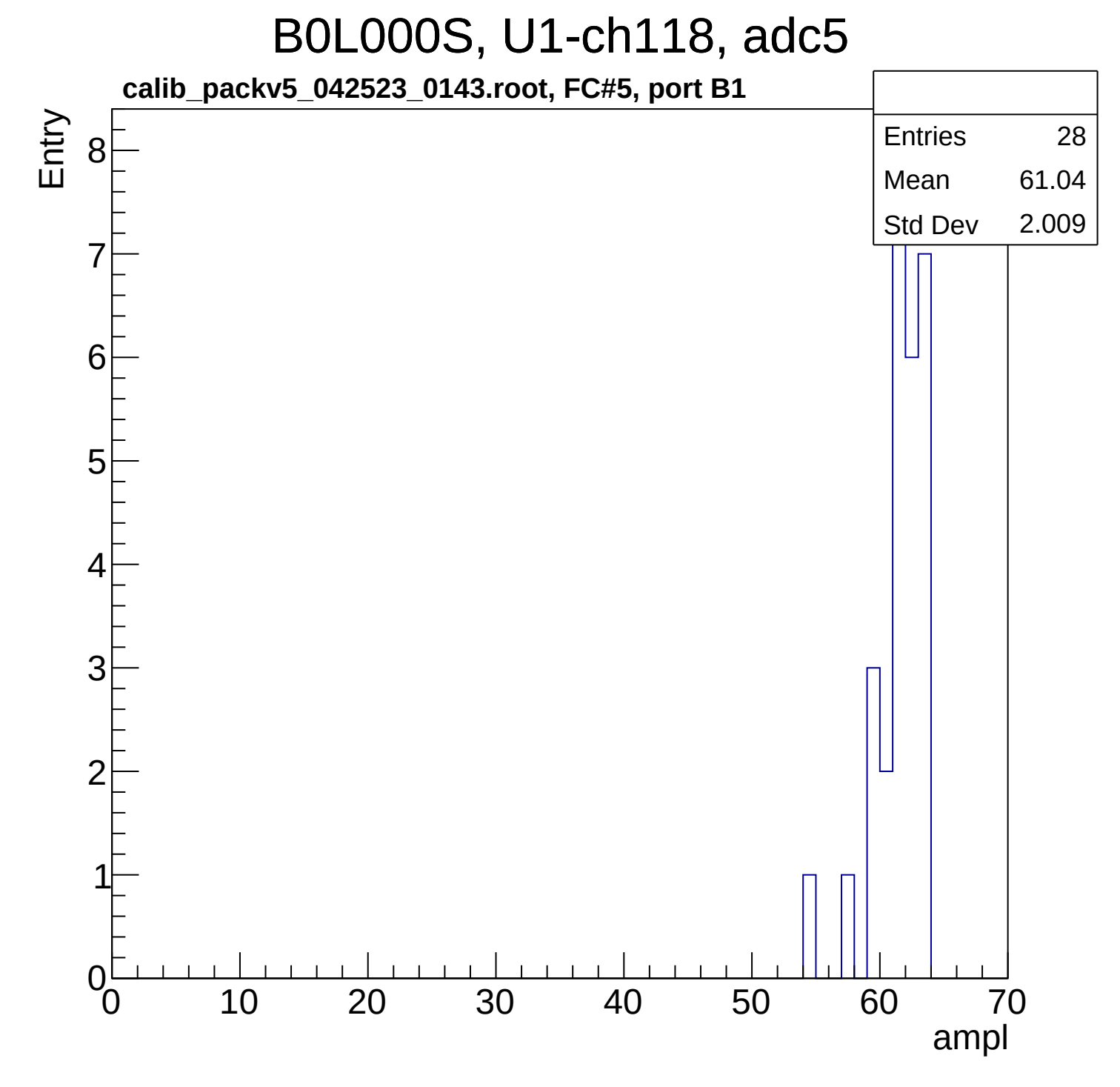
B0L000S, U1-ch118, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 61.04
Std Dev 2.009
Entry
ampl
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4
5
6
7
8
0
10
20
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60
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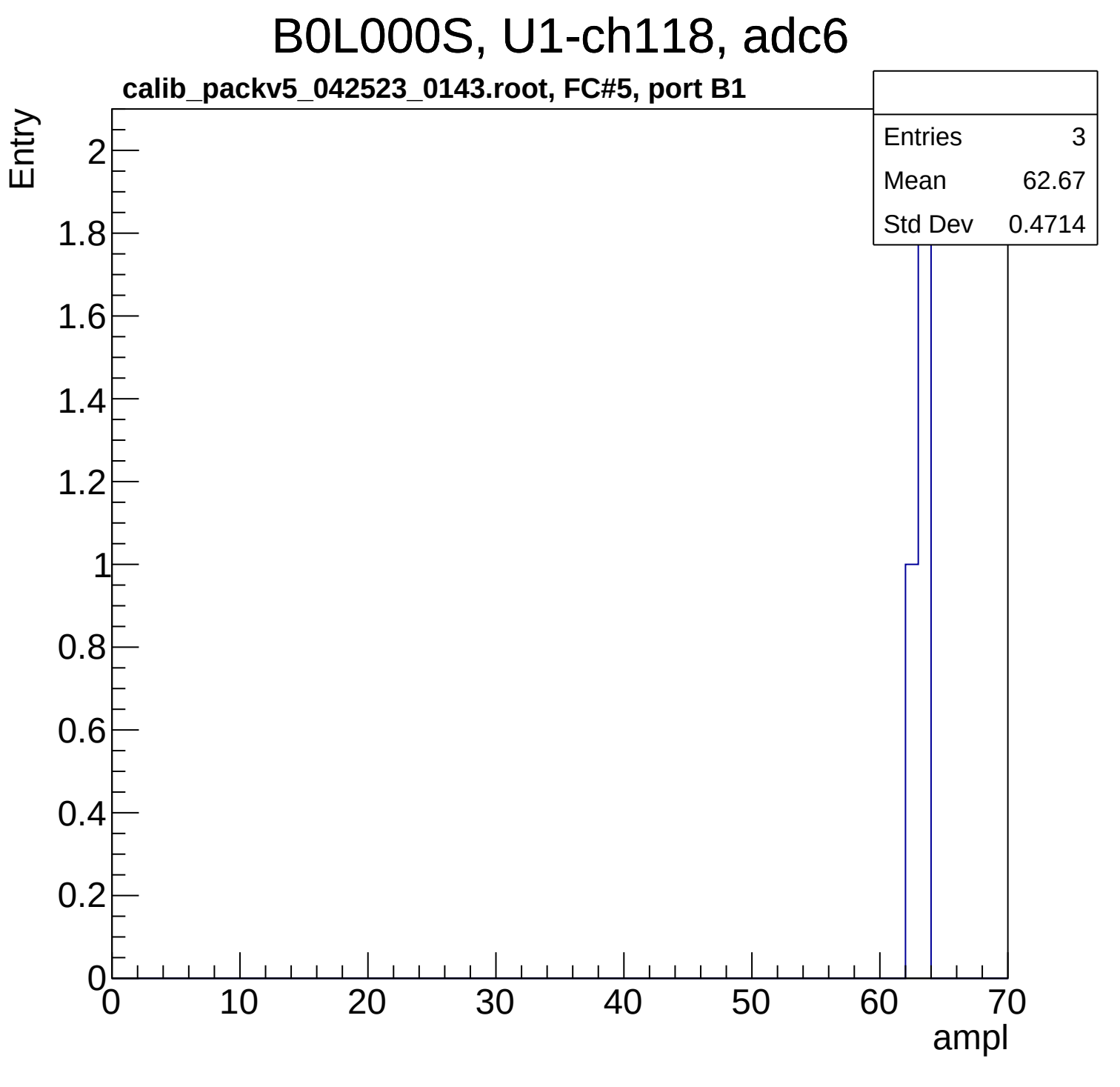
B0L000S, U1-ch118, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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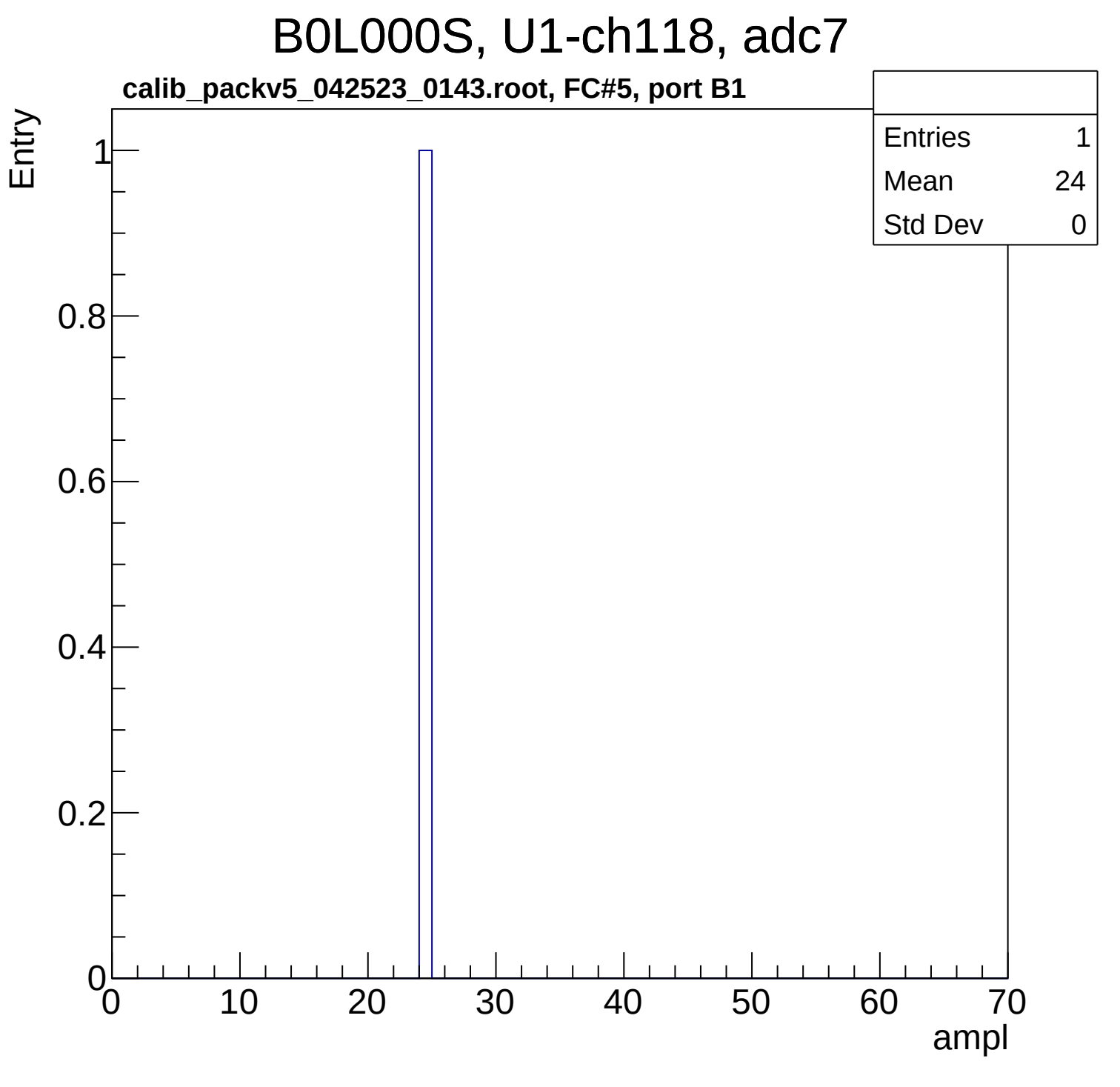
B0L000S, U1-ch118, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
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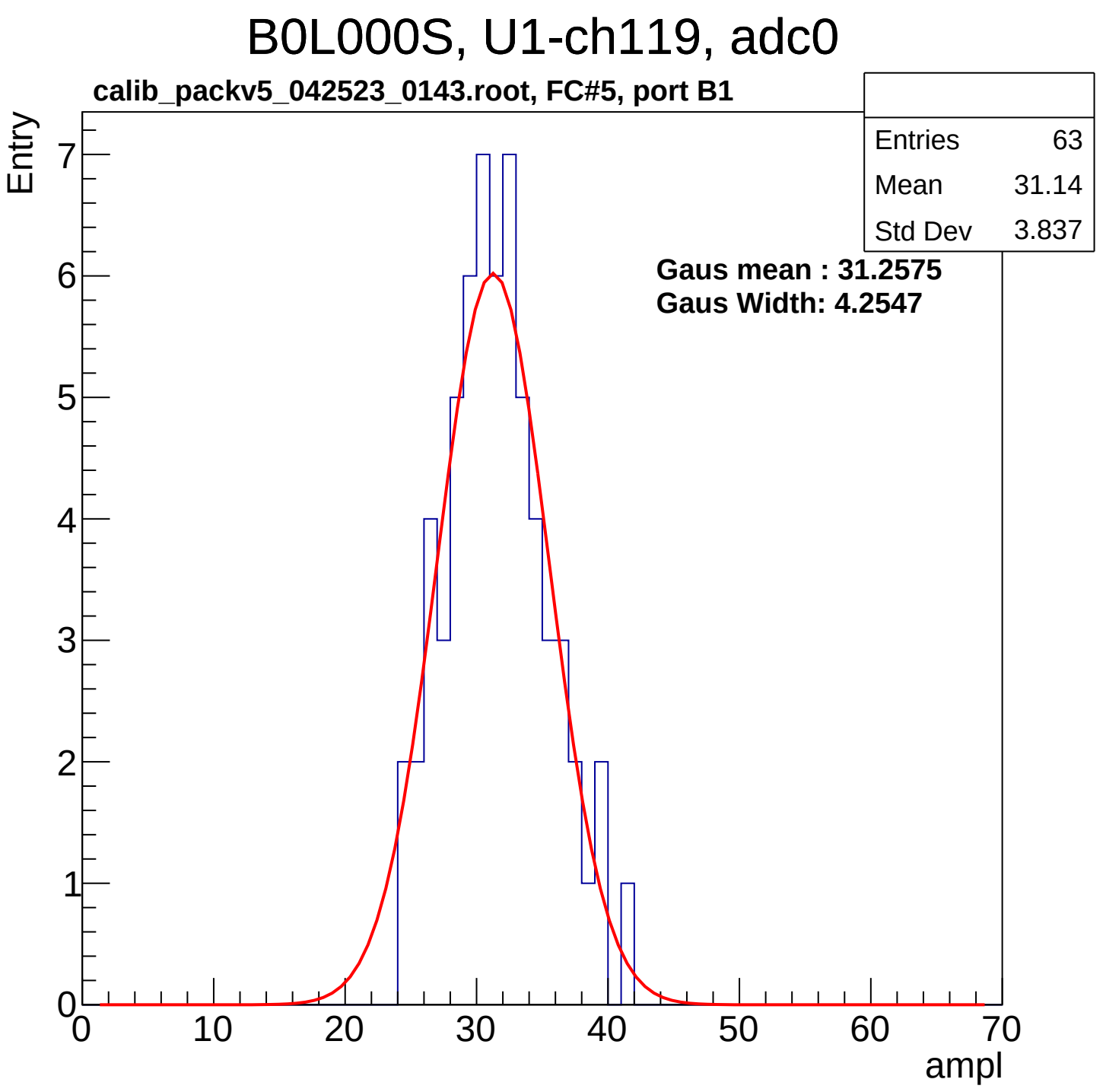
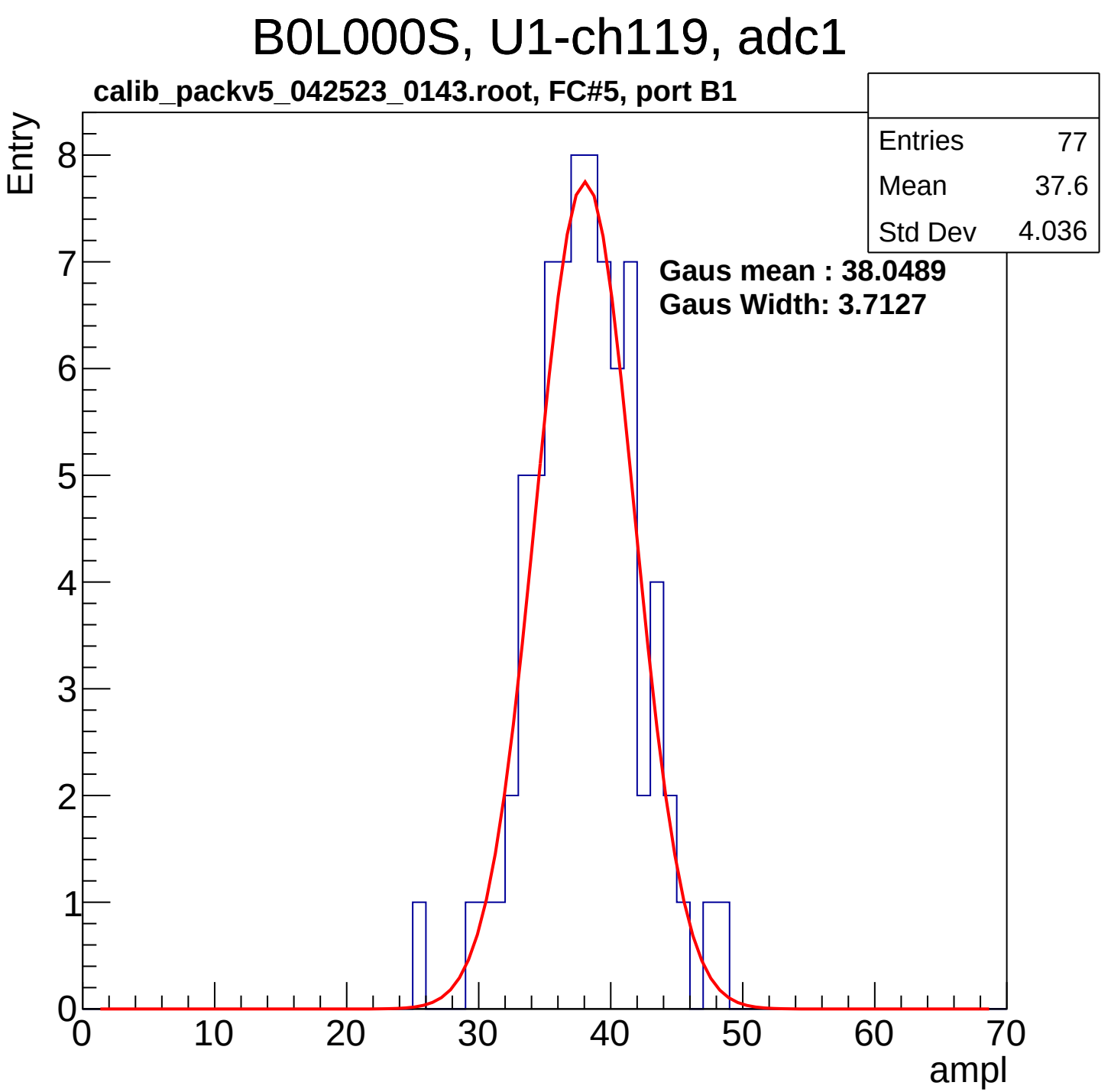
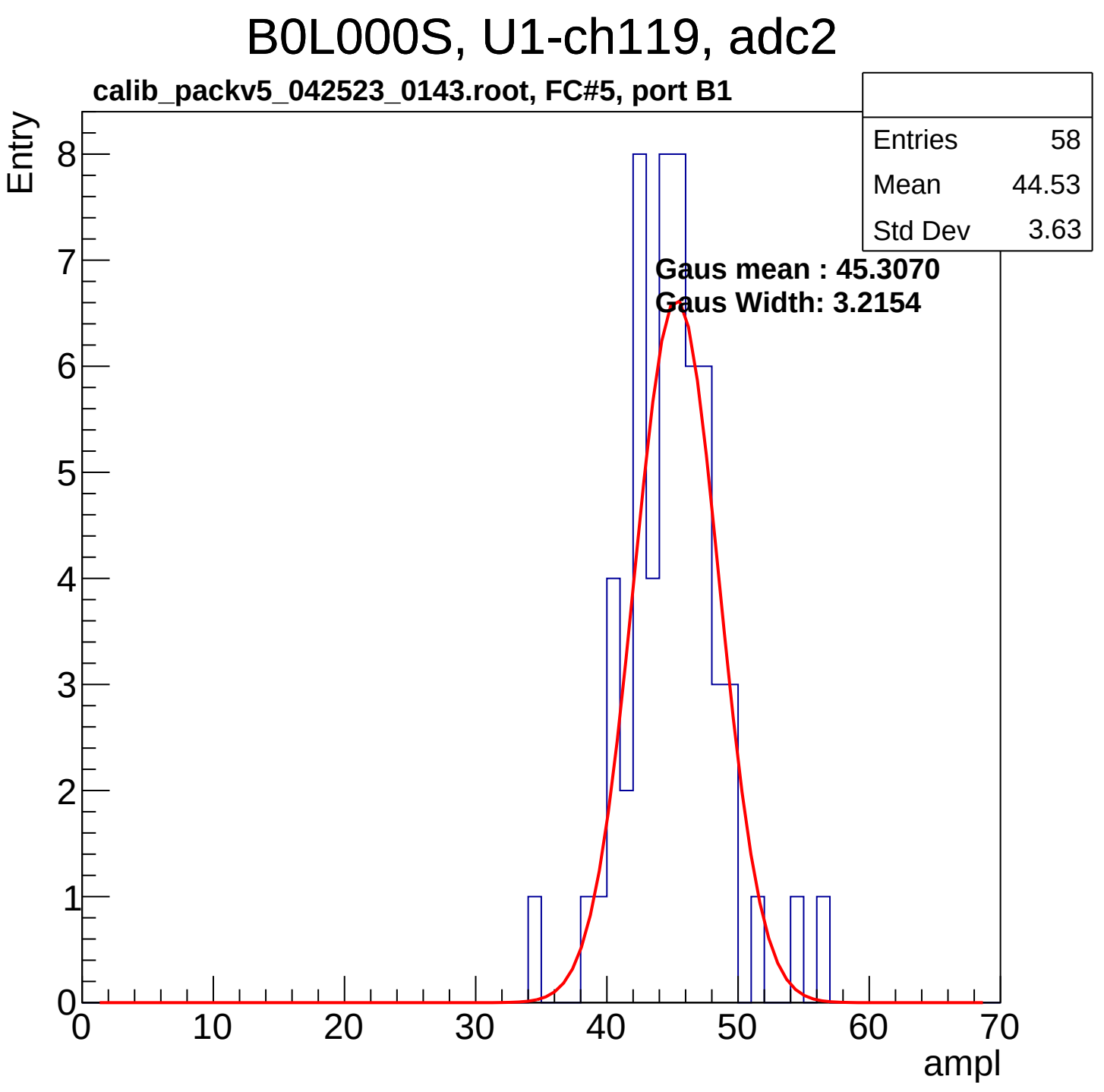
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch119, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 31.14
Std Dev 3.837
Gaus mean : 31.2575
Gaus Width: 4.2547
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch119, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 37.6
Std Dev 4.036
Gaus mean : 38.0489
Gaus Width: 3.7127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch119, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 44.53
Std Dev 3.63
Gaus mean : 45.3070
Gaus Width: 3.2154
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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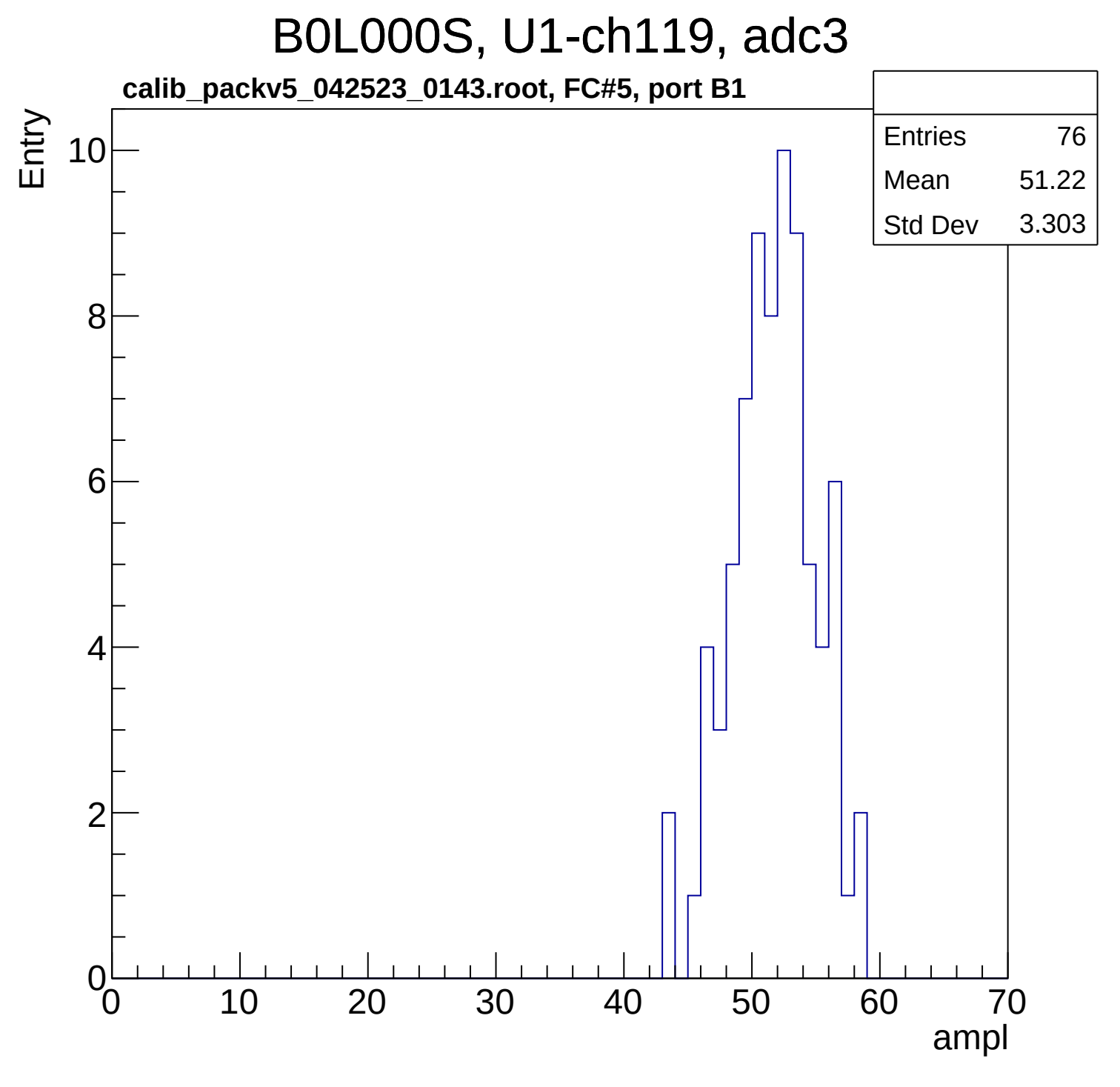
B0L000S, U1-ch119, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 51.22
Std Dev 3.303
Entry
ampl
0
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4
6
8
10
0
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20
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40
50
60
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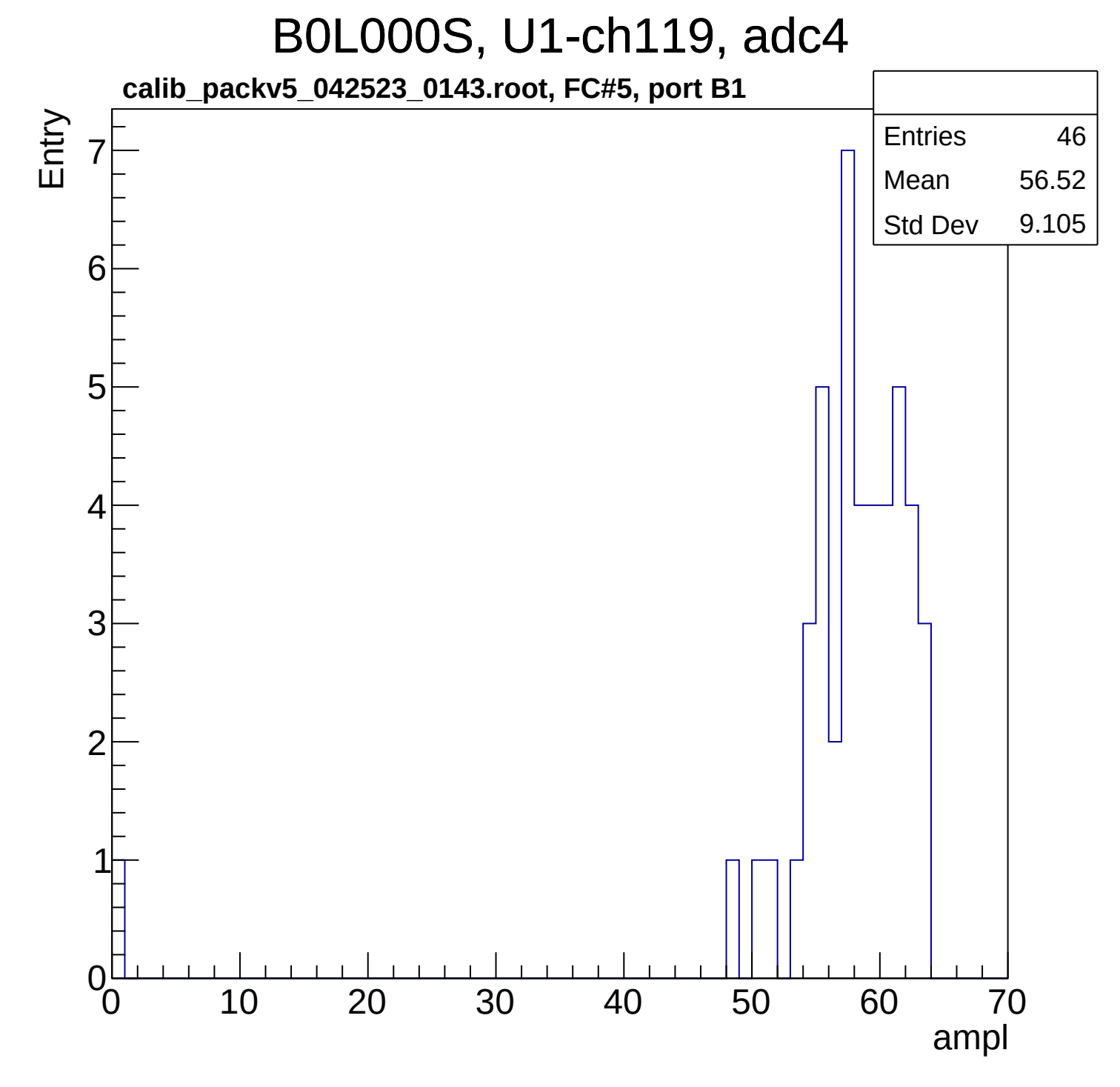
B0L000S, U1-ch119, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 56.52
Std Dev 9.105
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
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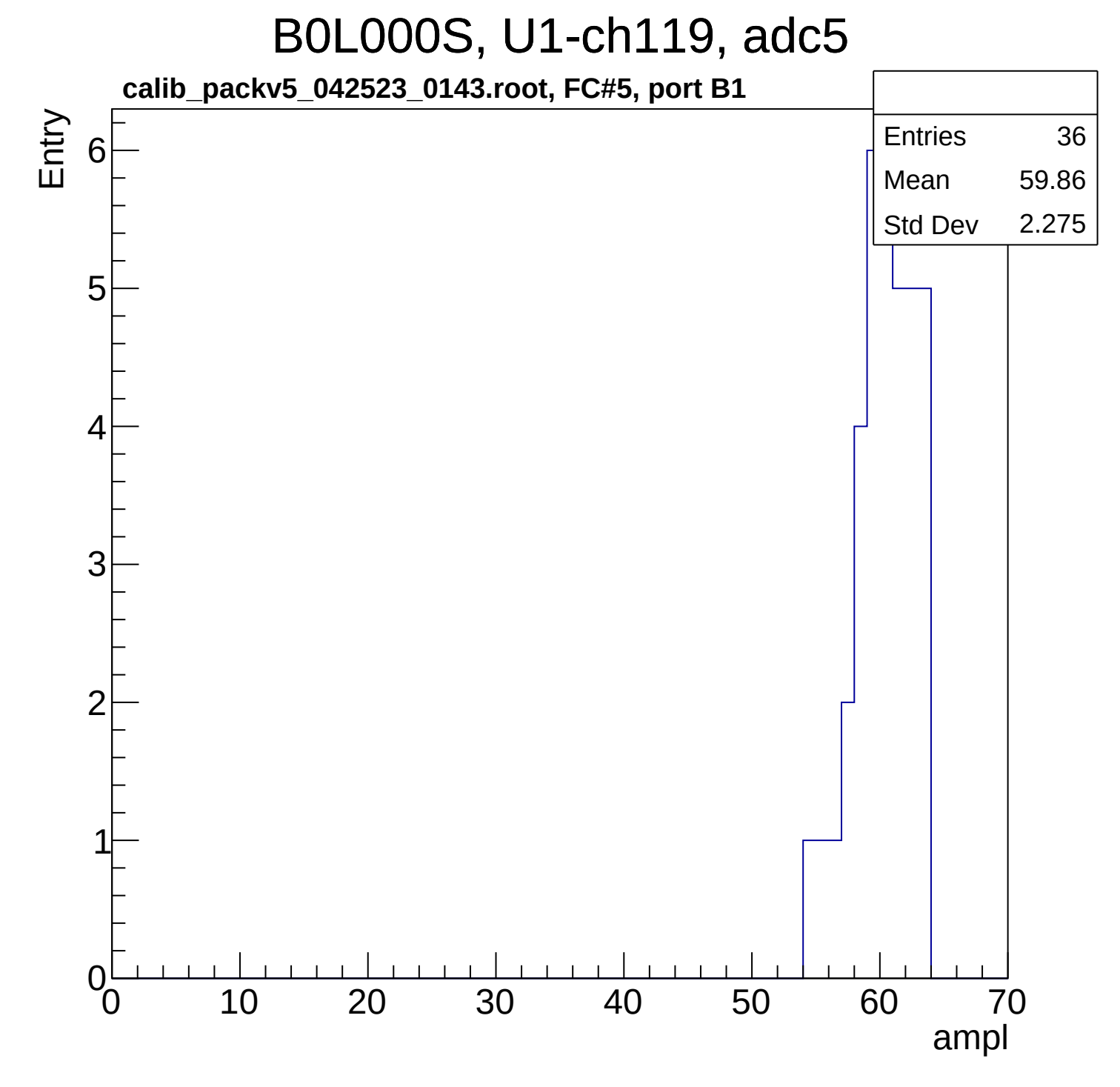
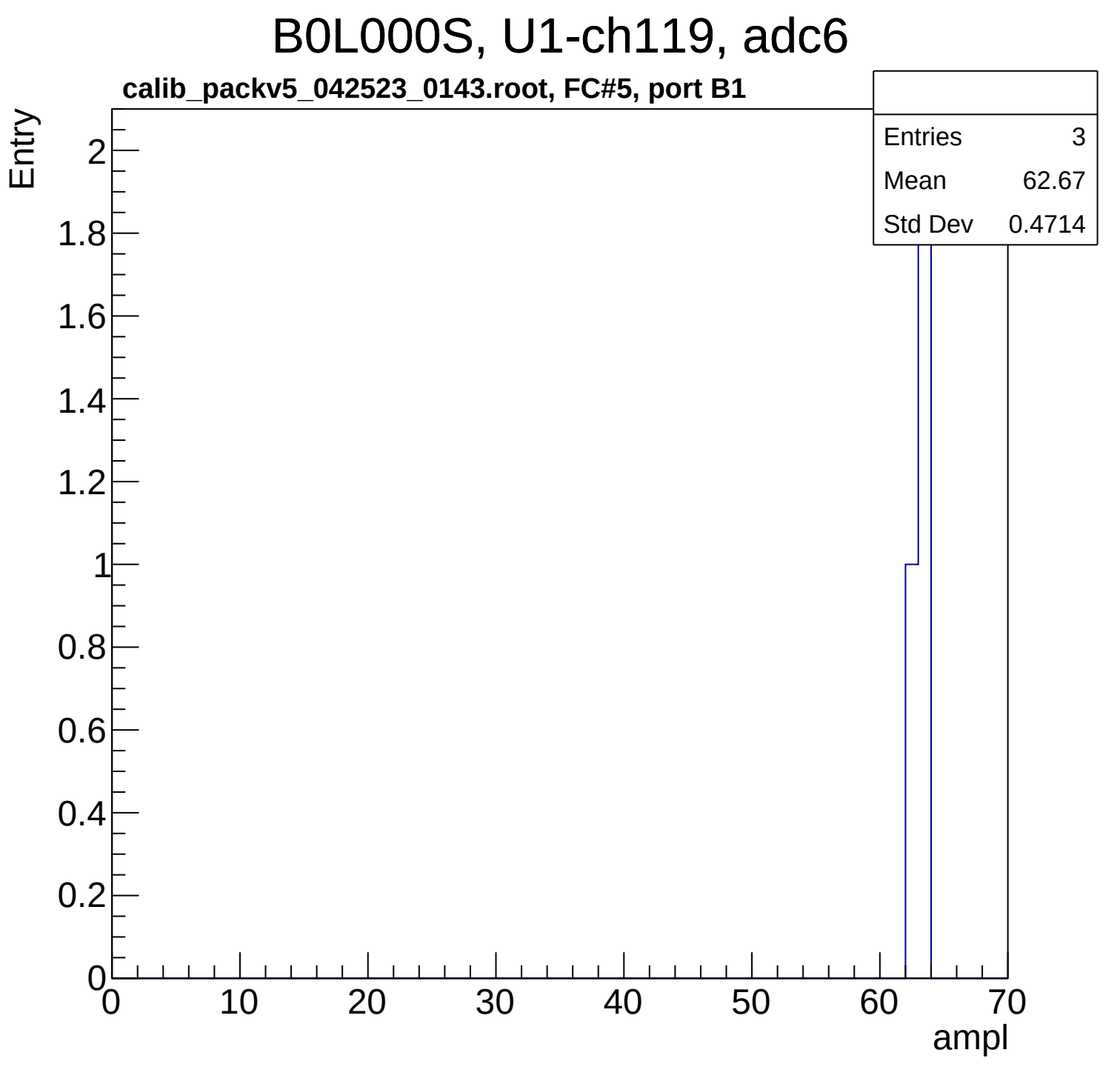
40
50
60
70


B0L000S, U1-ch119, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 59.86
Std Dev 2.275
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U1-ch119, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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60
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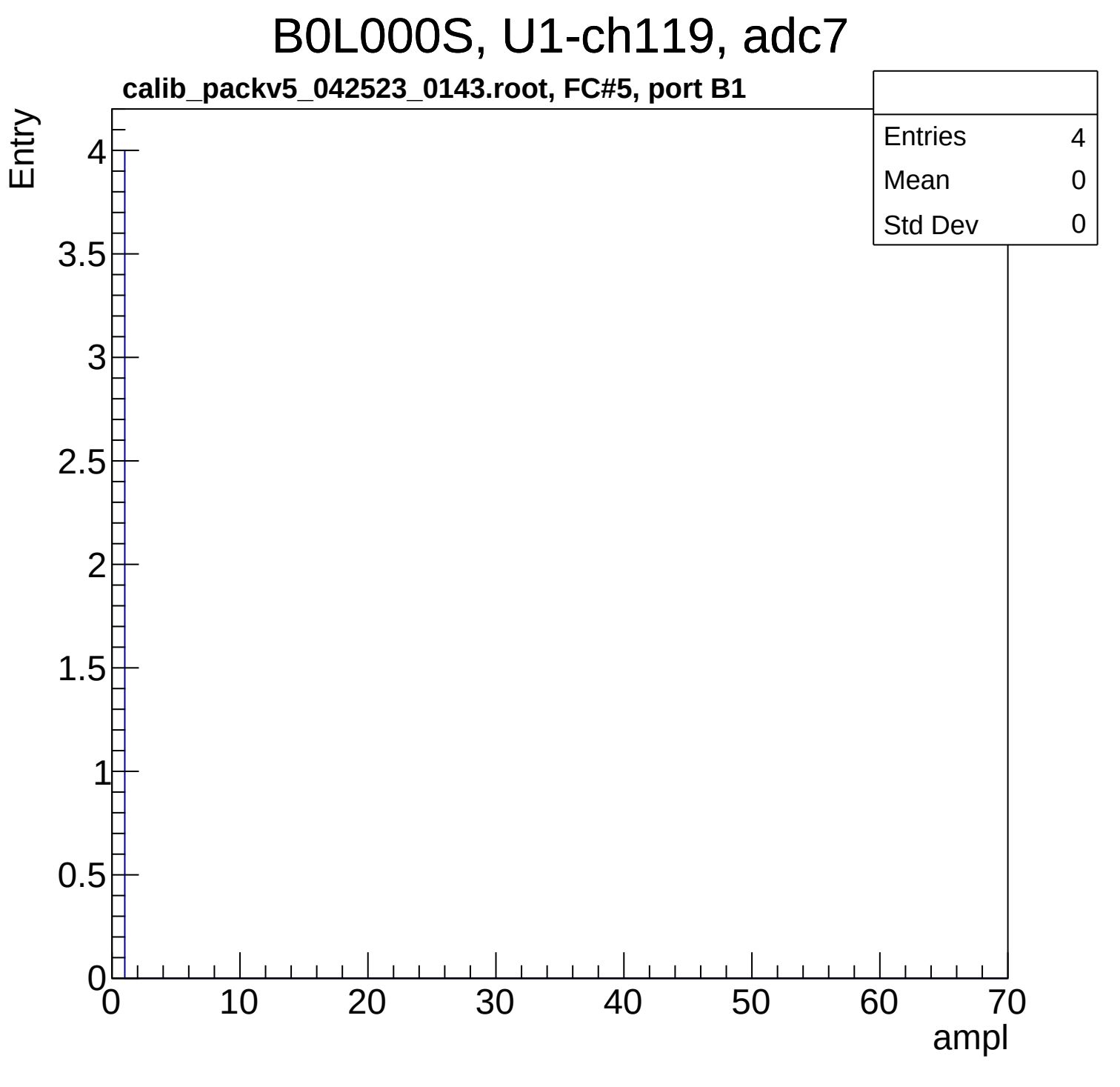
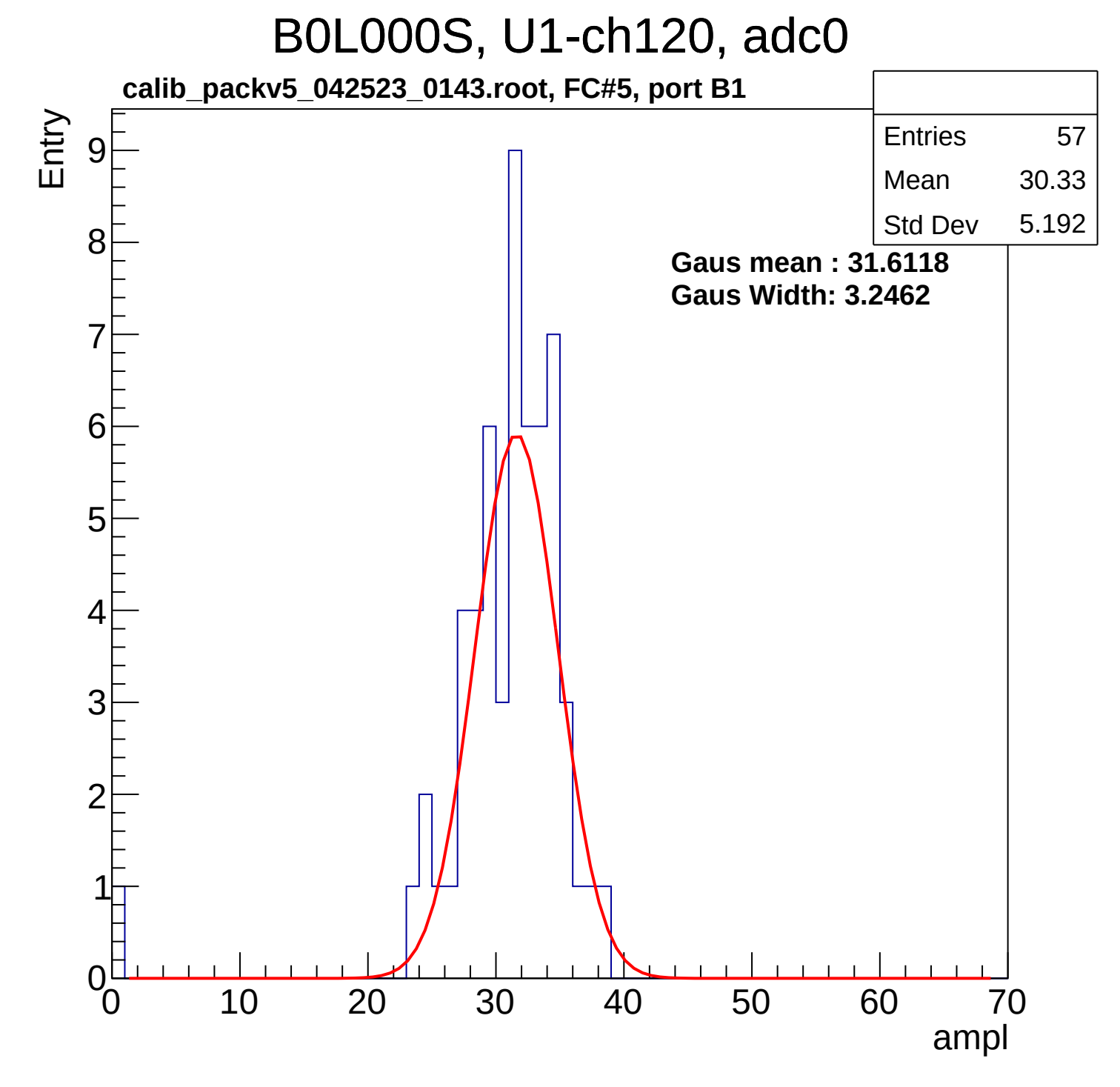

B0L000S, U1-ch119, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch120, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 30.33
Std Dev 5.192
Gaus mean : 31.6118
Gaus Width: 3.2462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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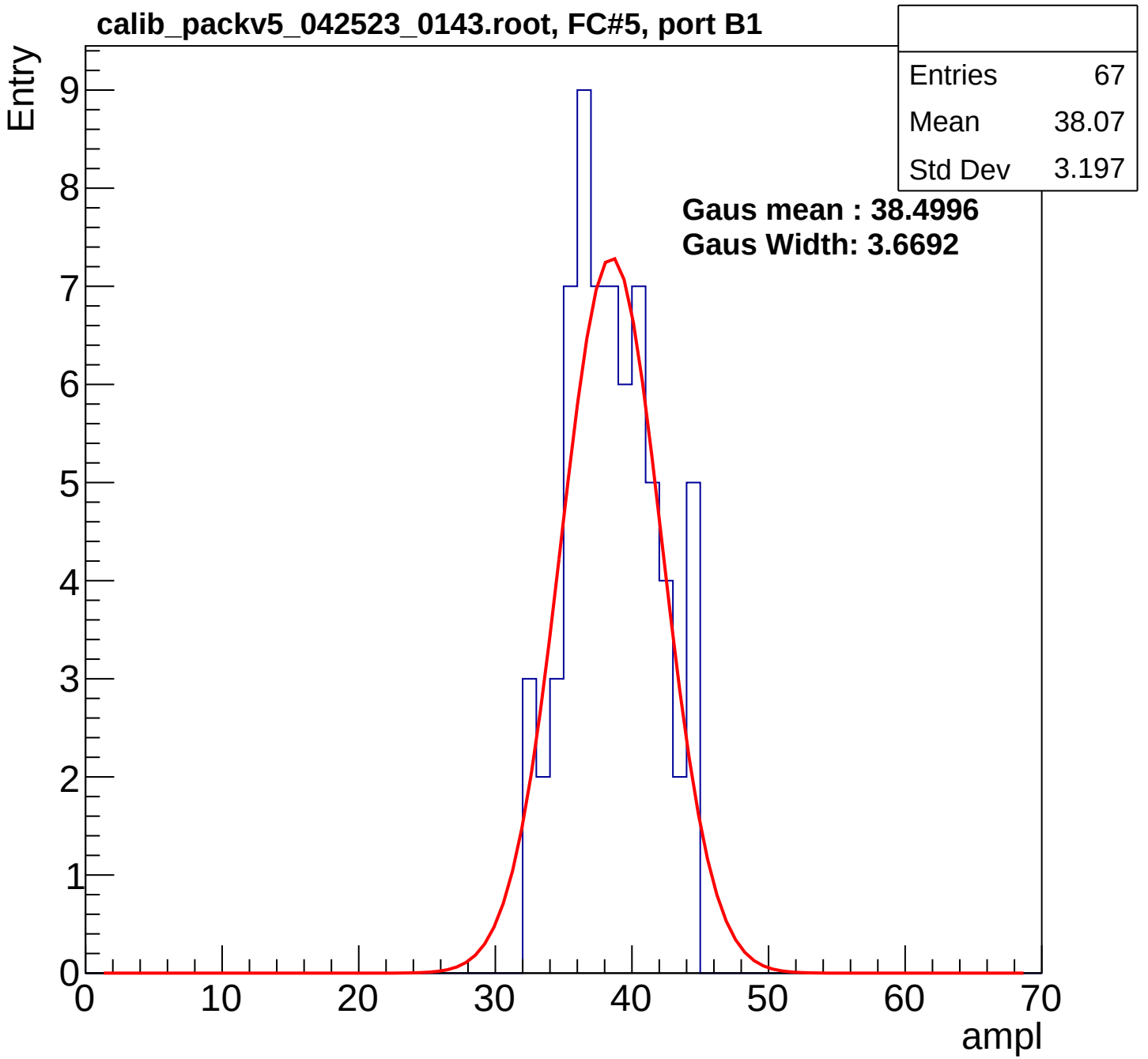
B0L000S, U1-ch120, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 38.07
Std Dev 3.197
Gaus mean : 38.4996
Gaus Width: 3.6692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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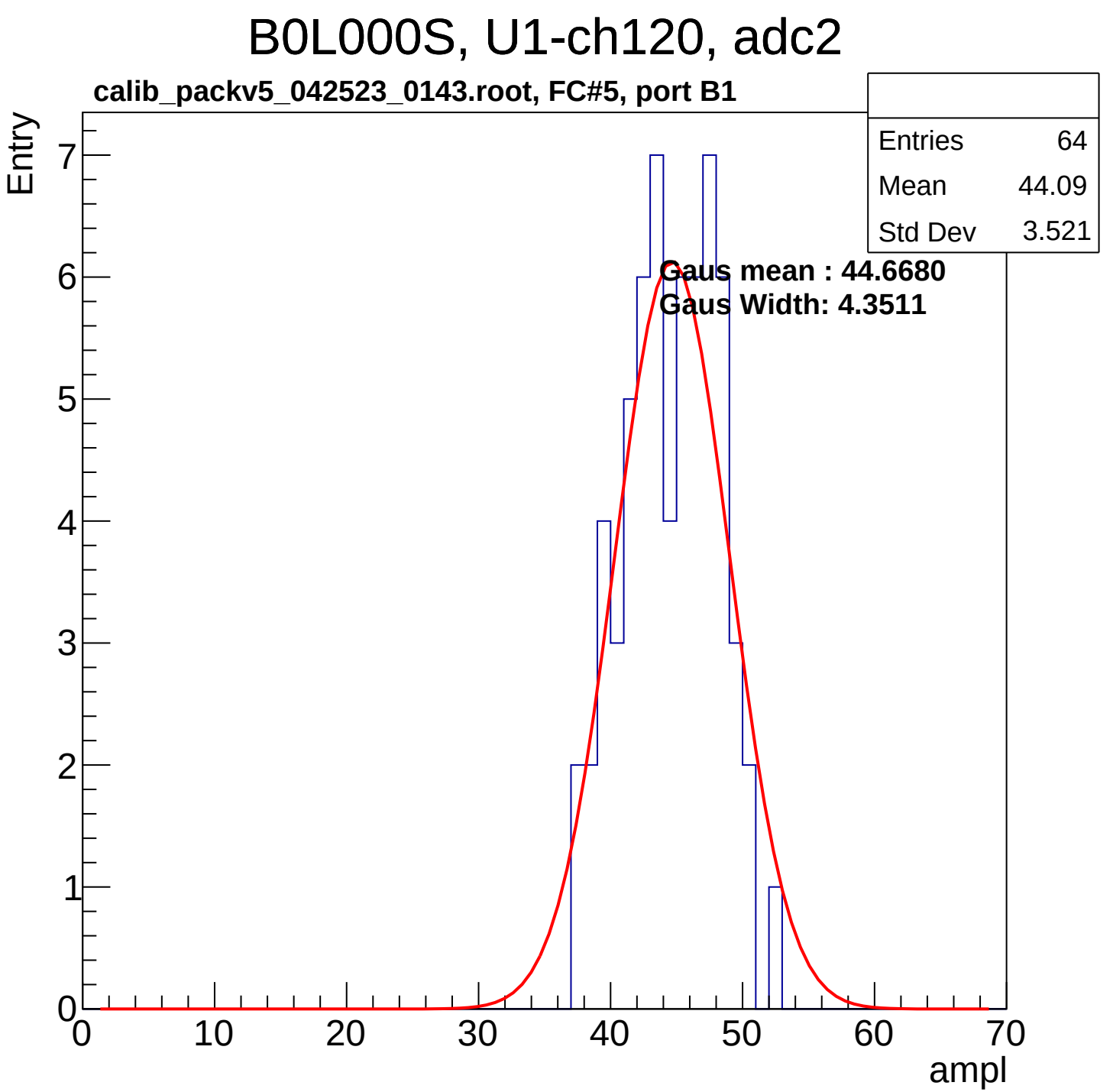
B0L000S, U1-ch120, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.09
Std Dev 3.521
Gaus mean : 44.6680
Gaus Width: 4.3511
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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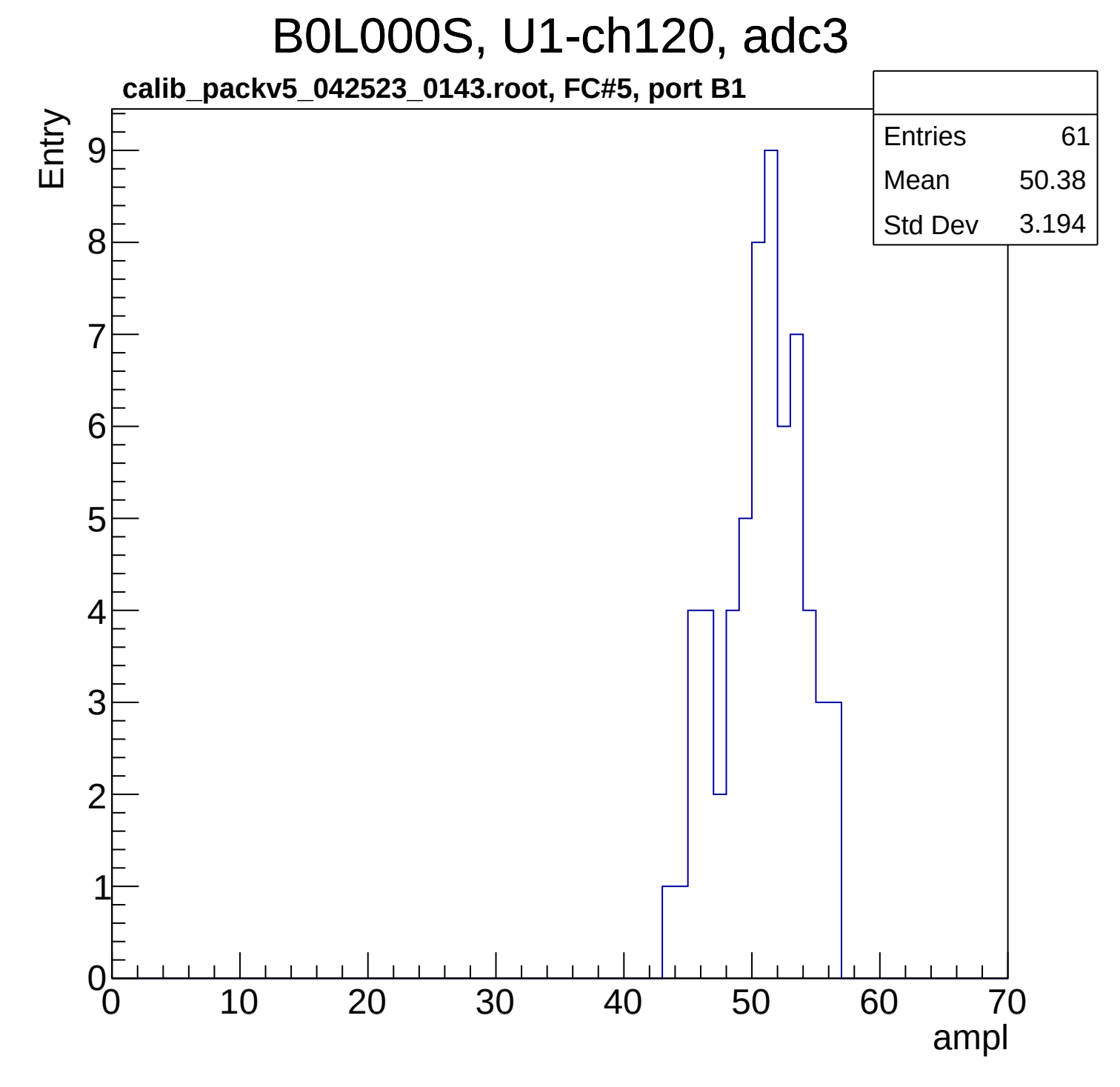
B0L000S, U1-ch120, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.38
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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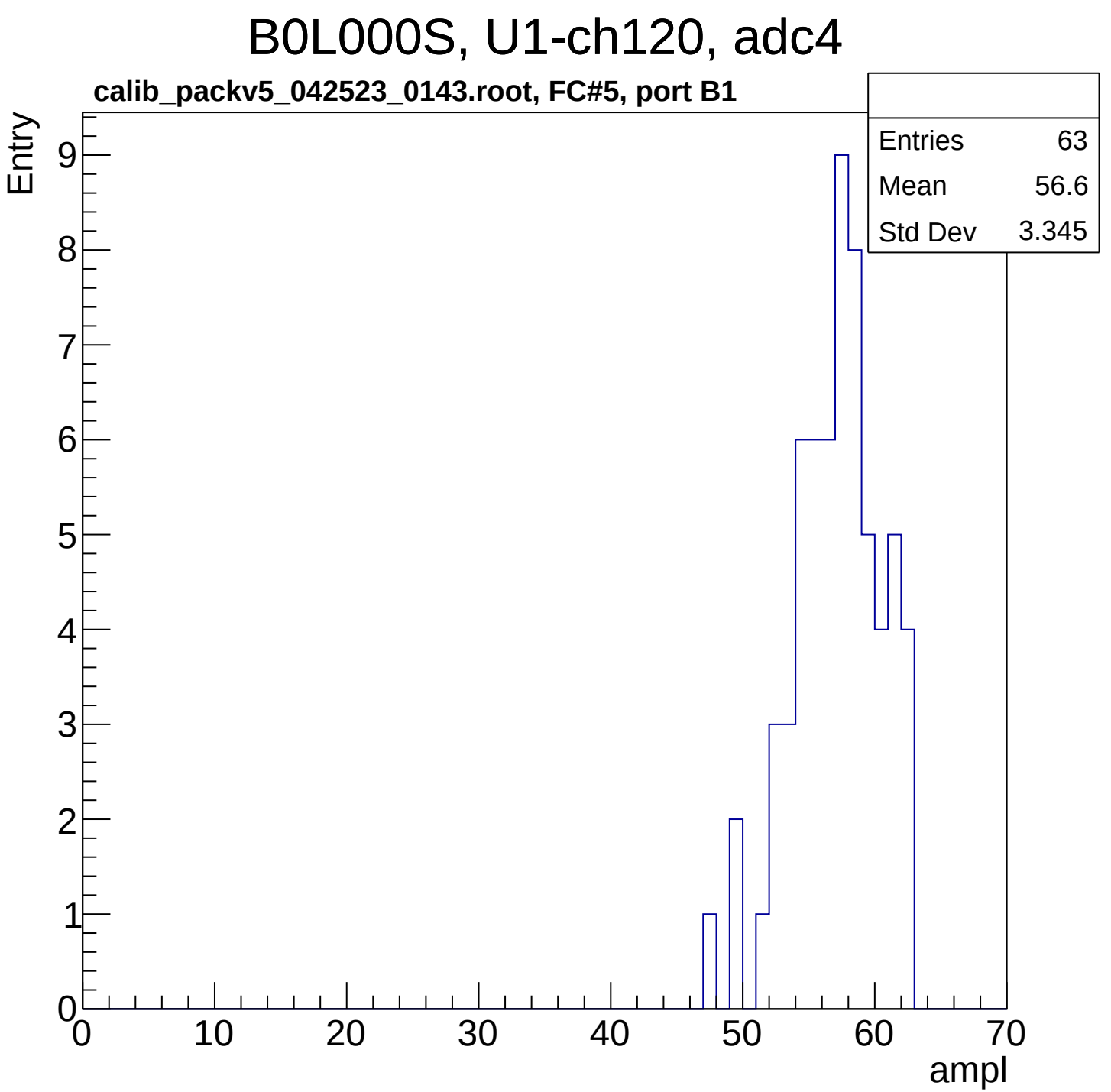
B0L000S, U1-ch120, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 56.6
Std Dev 3.345
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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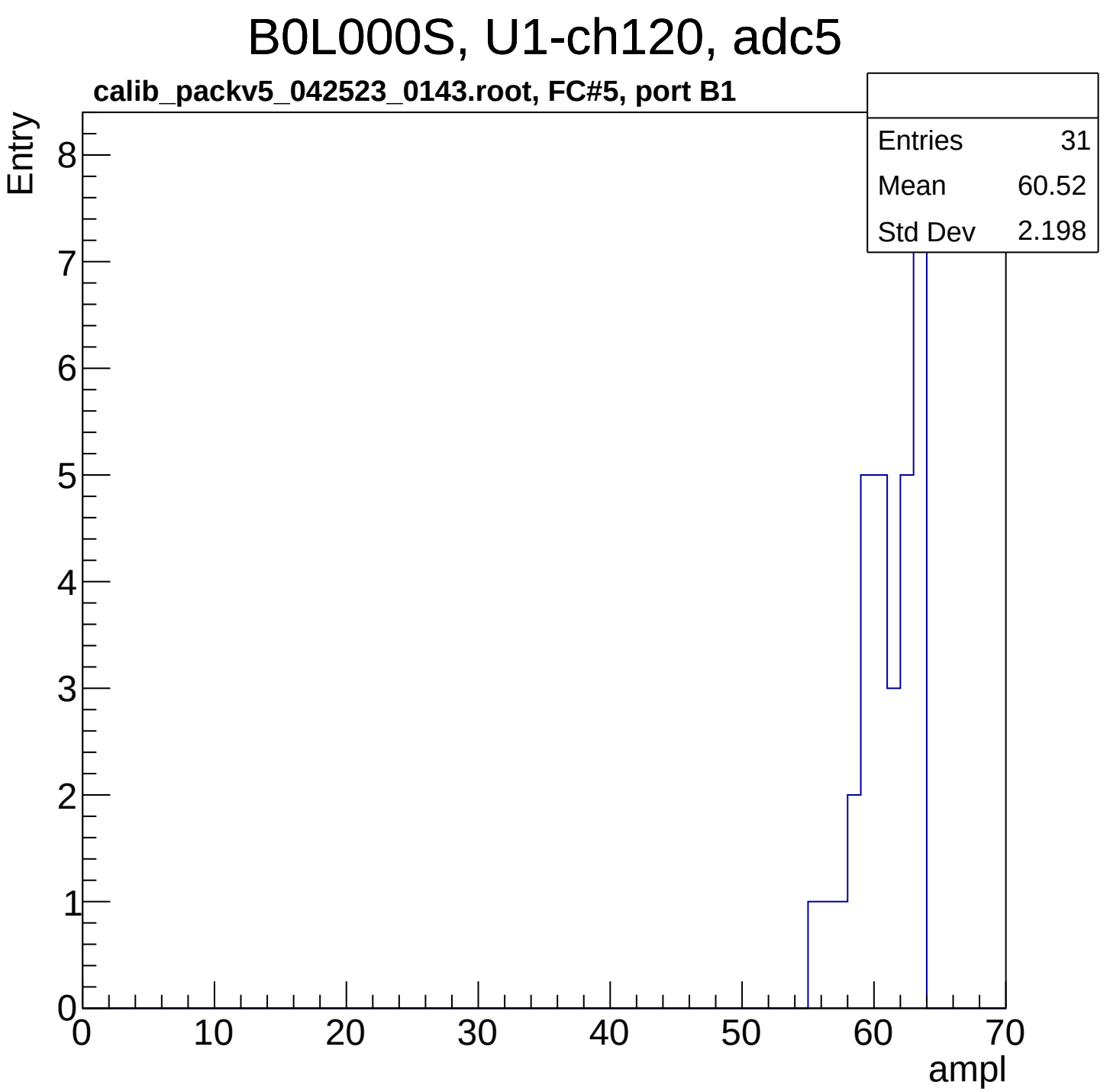
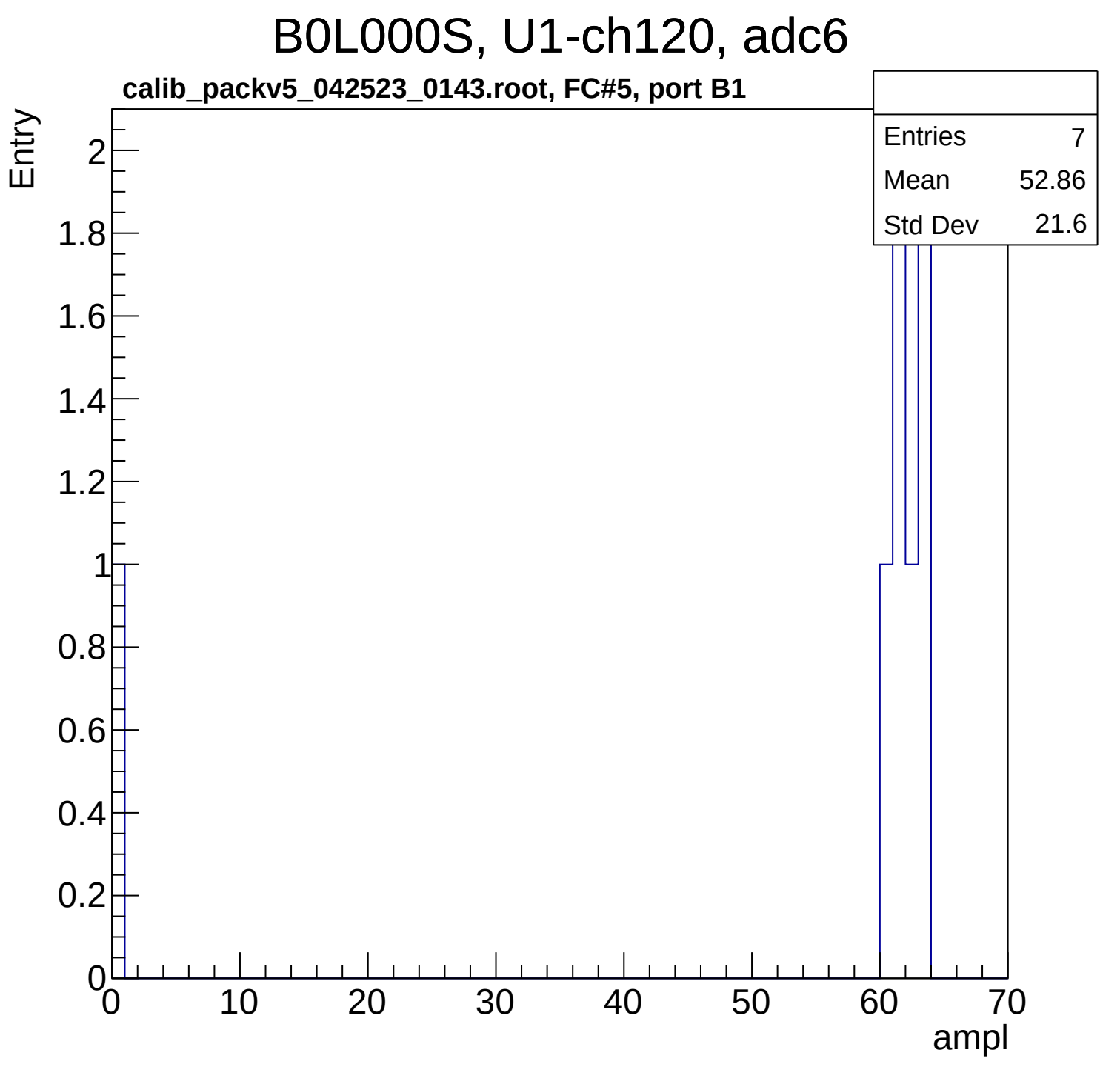

B0L000S, U1-ch120, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 60.52
Std Dev 2.198
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch120, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 52.86
Std Dev 21.6
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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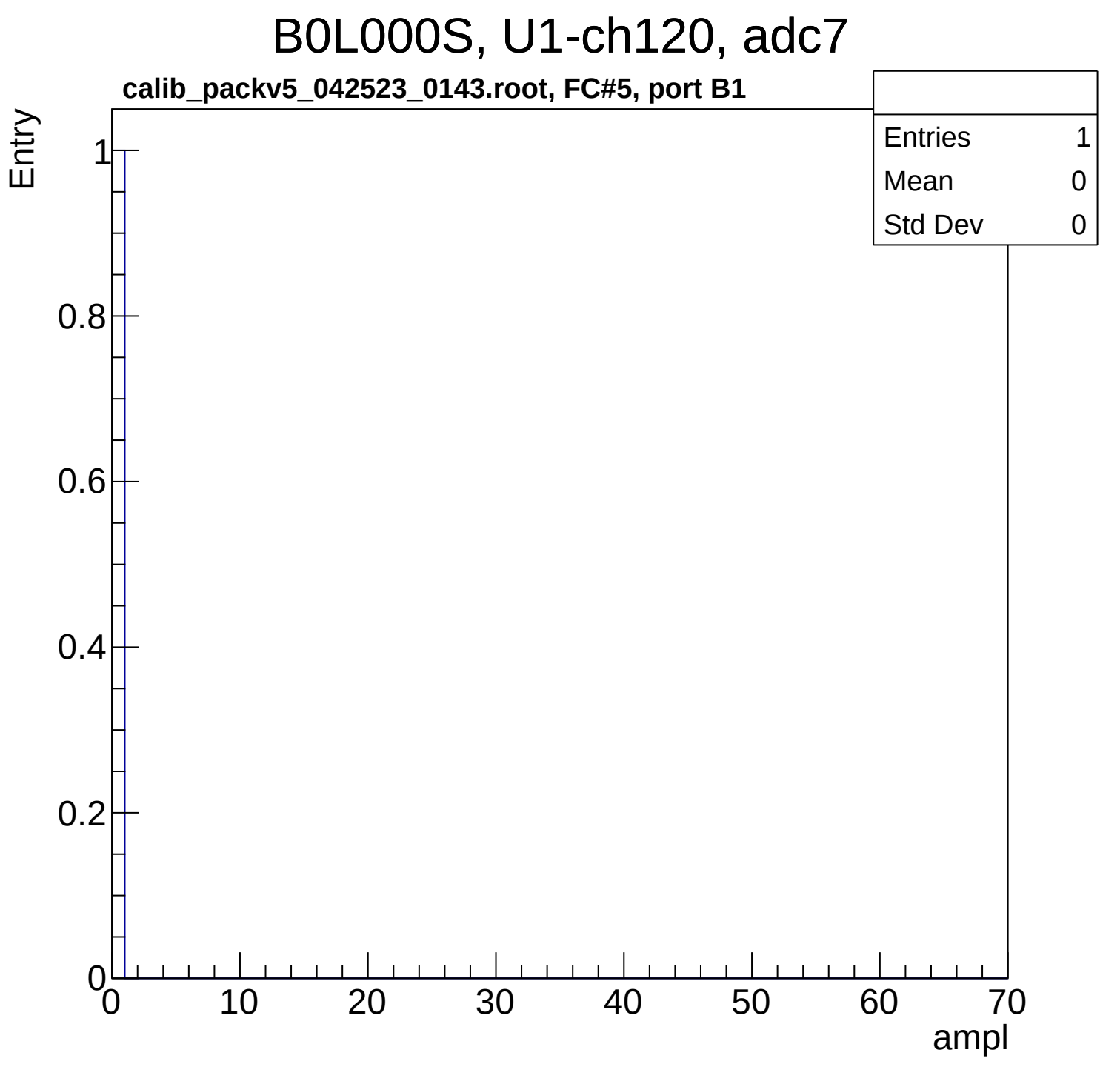
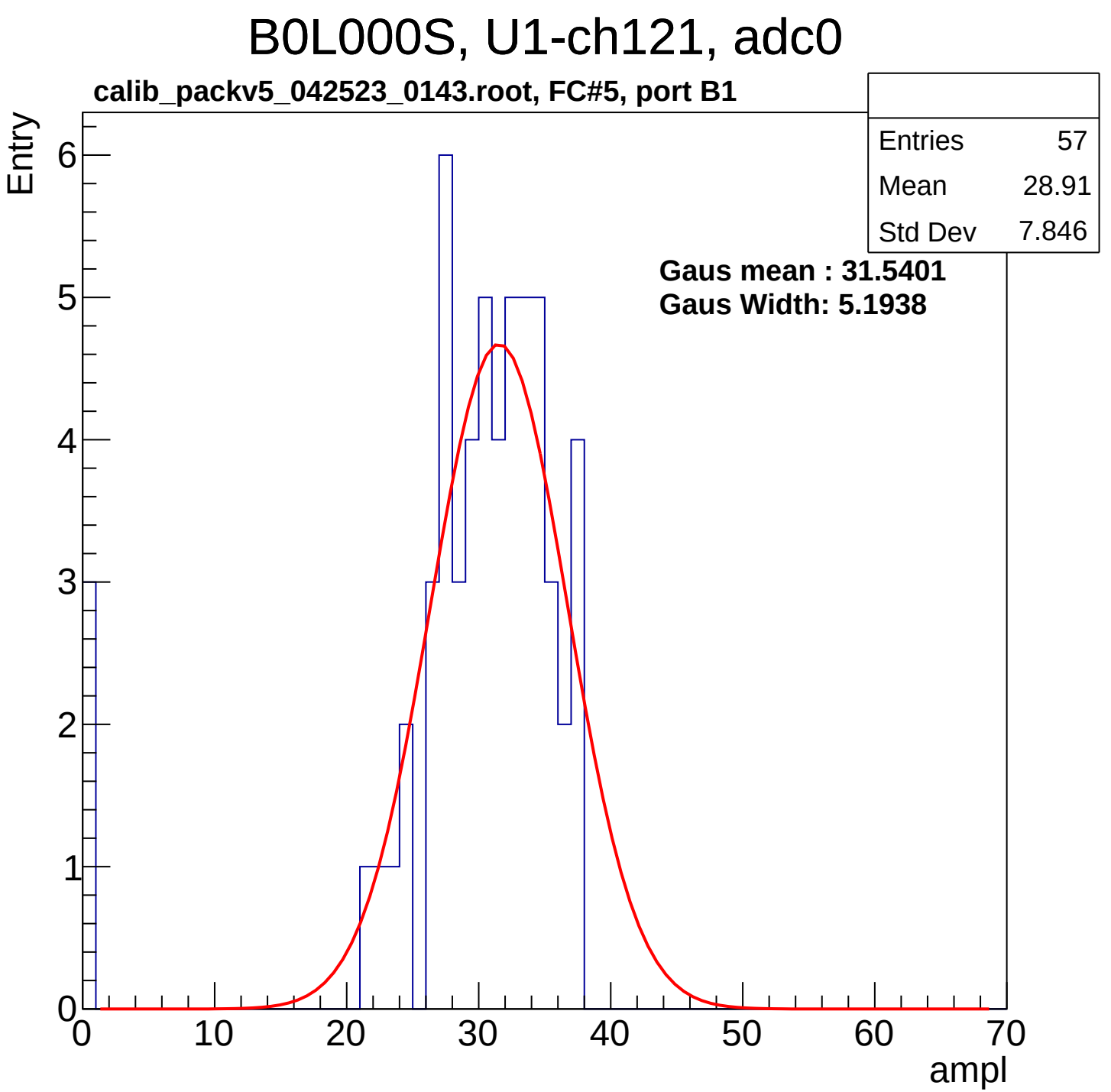

B0L000S, U1-ch120, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch121, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 28.91
Std Dev 7.846
Gaus mean : 31.5401
Gaus Width: 5.1938
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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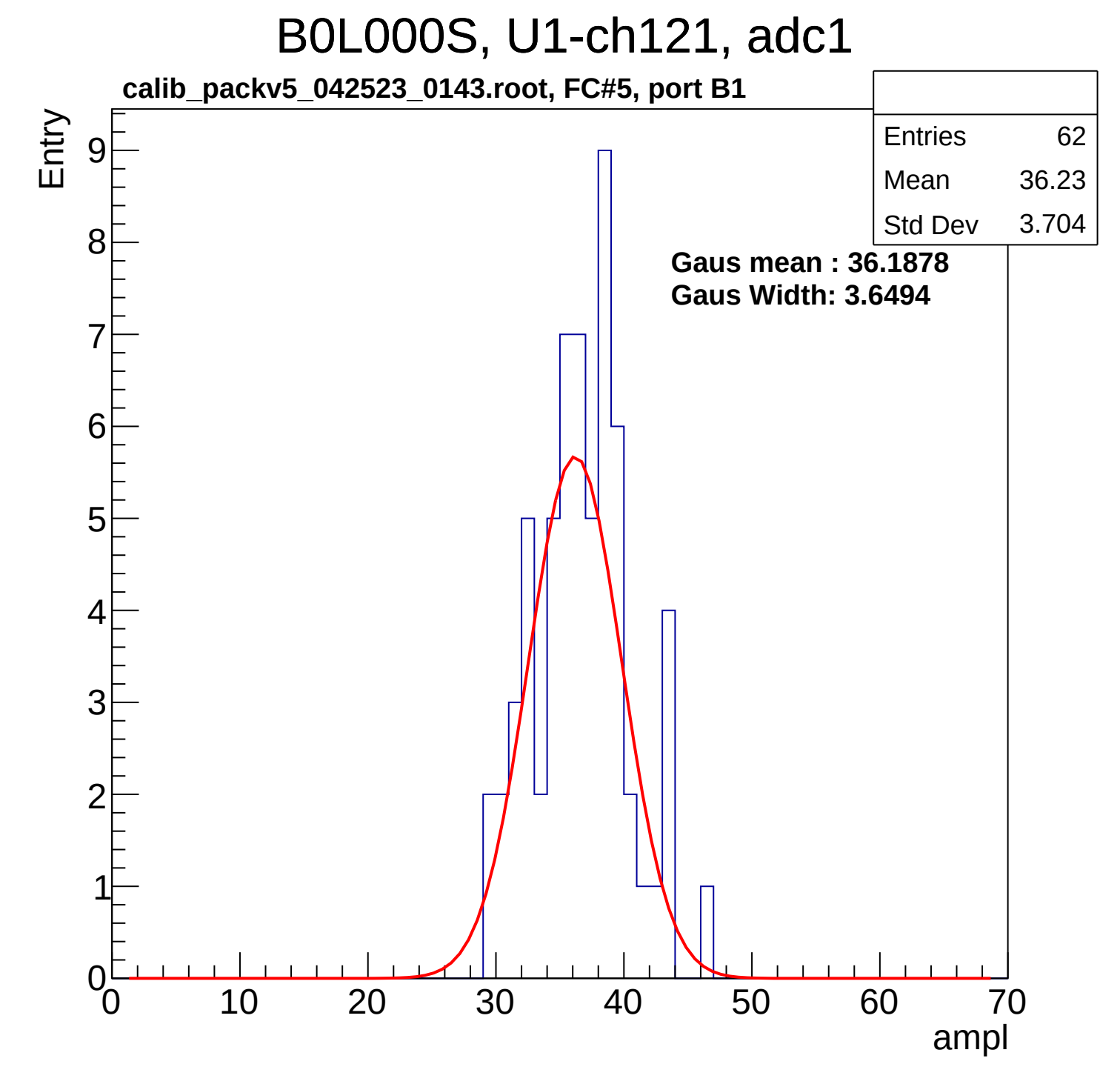
B0L000S, U1-ch121, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.23
Std Dev 3.704
Gaus mean : 36.1878
Gaus Width: 3.6494
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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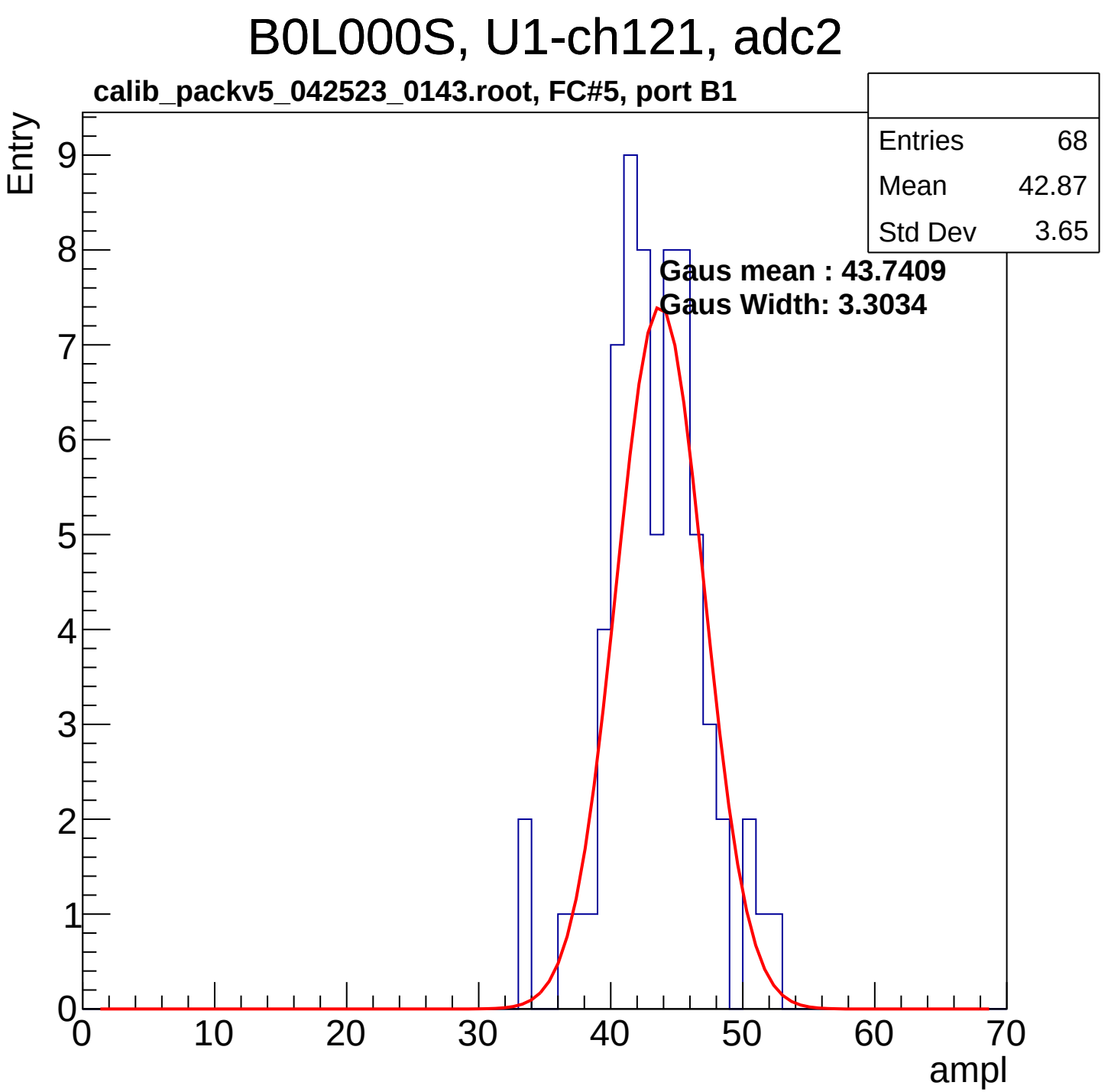
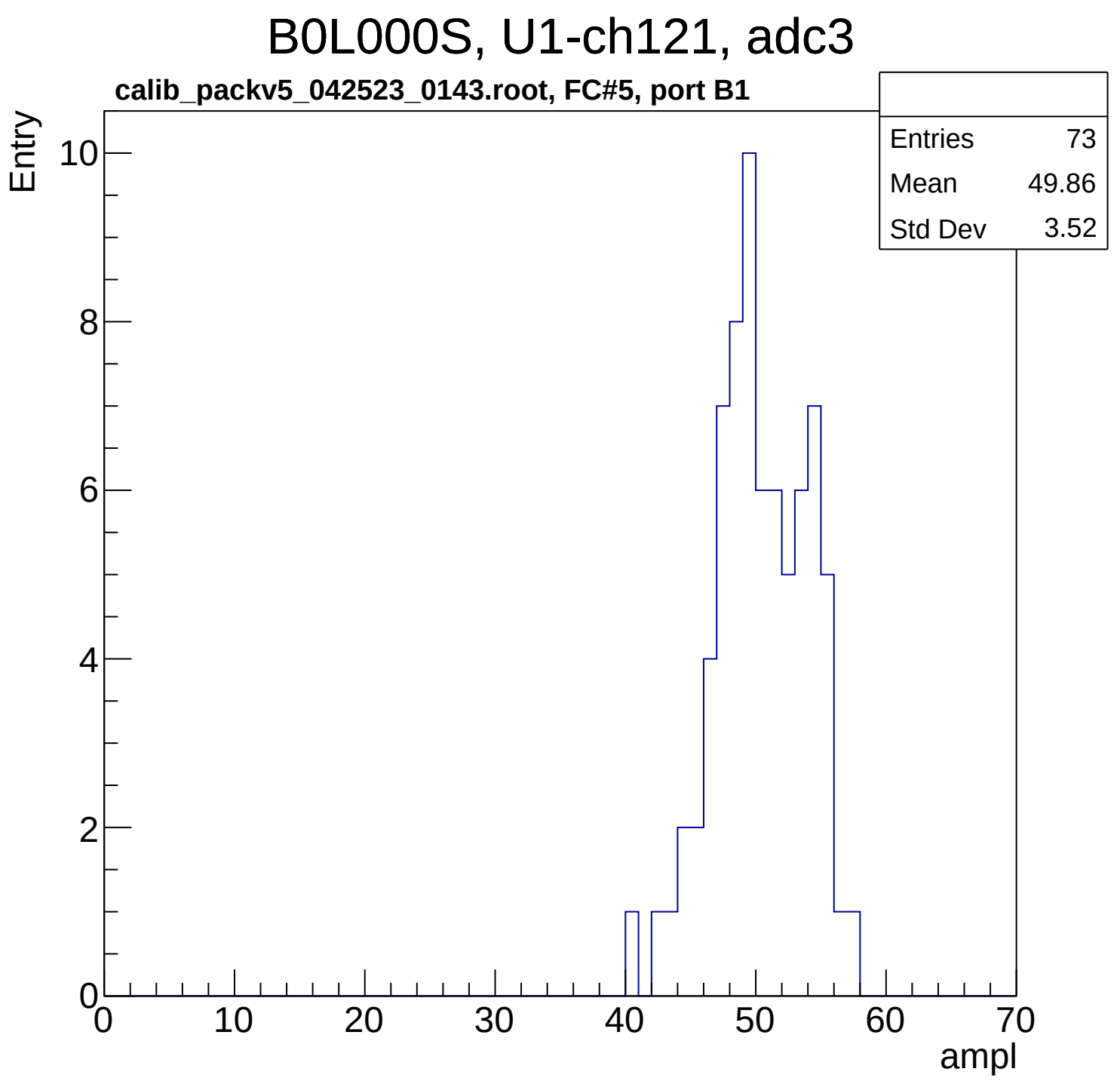
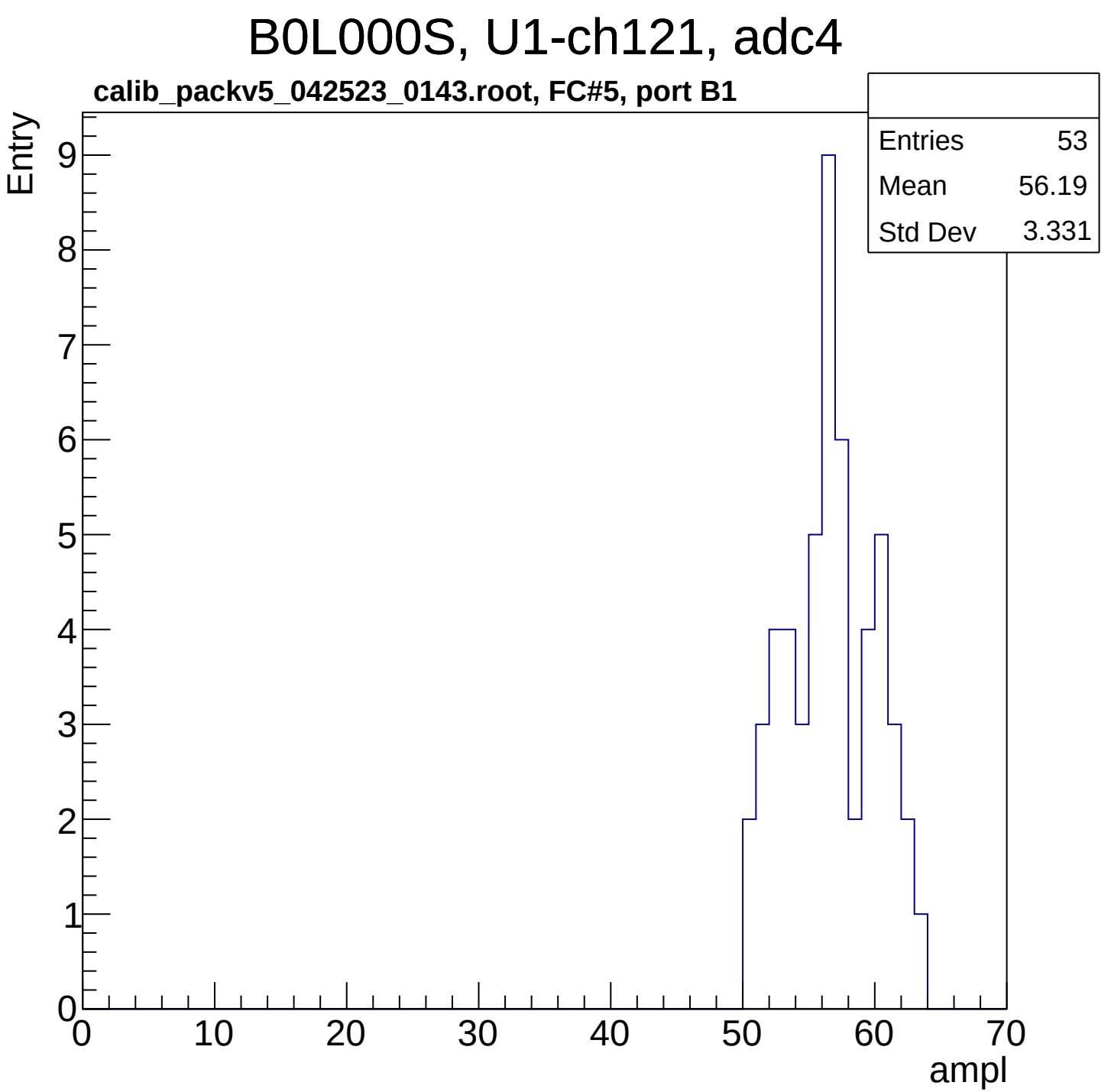
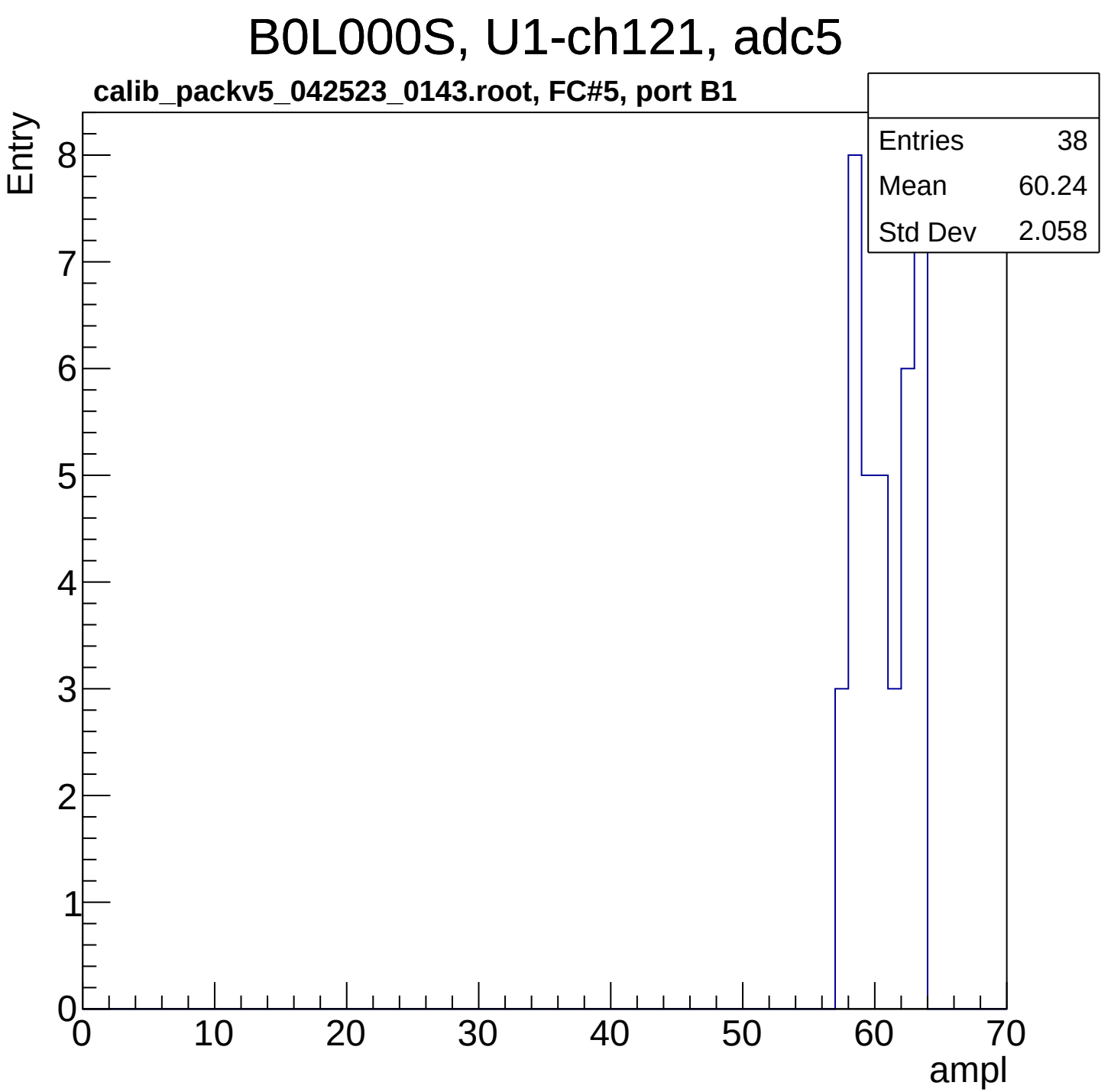

B0L000S, U1-ch121, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.87
Std Dev 3.65
Gaus mean : 43.7409
Gaus Width: 3.3034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch121, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.86
Std Dev 3.52
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch121, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.19
Std Dev 3.331
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch121, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 60.24
Std Dev 2.058
Entry
ampl
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1
2
3
4
5
6
7
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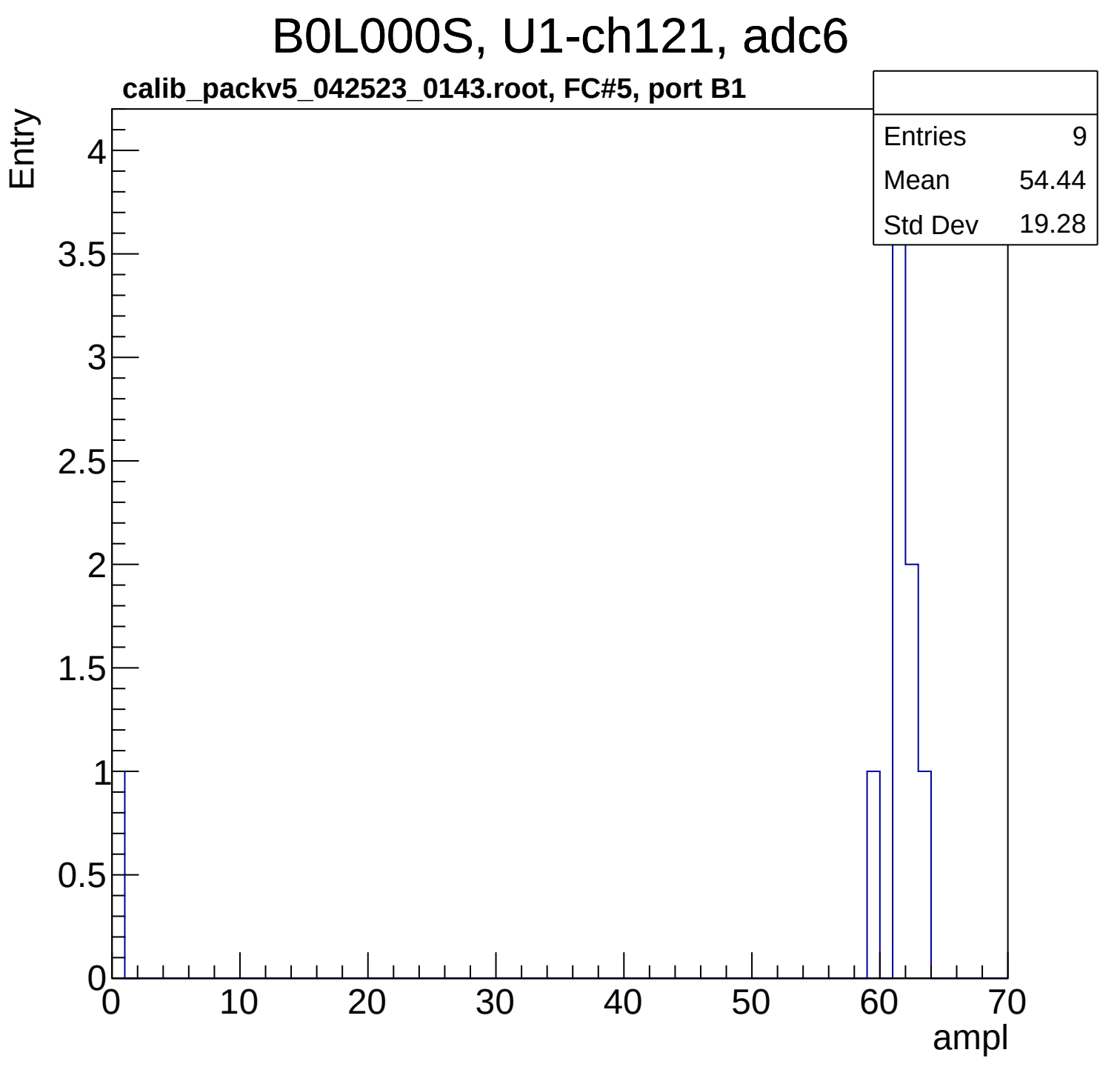
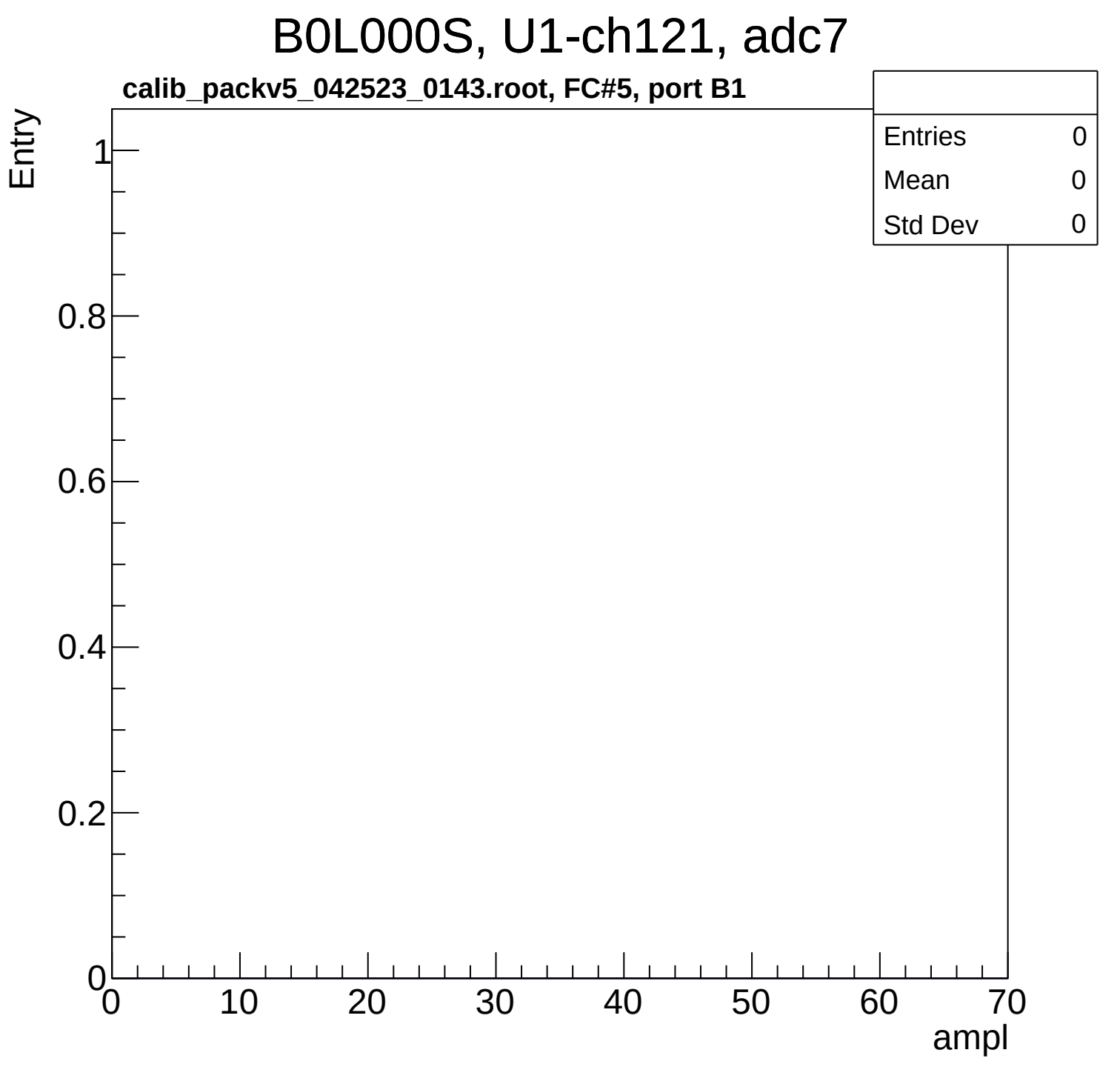
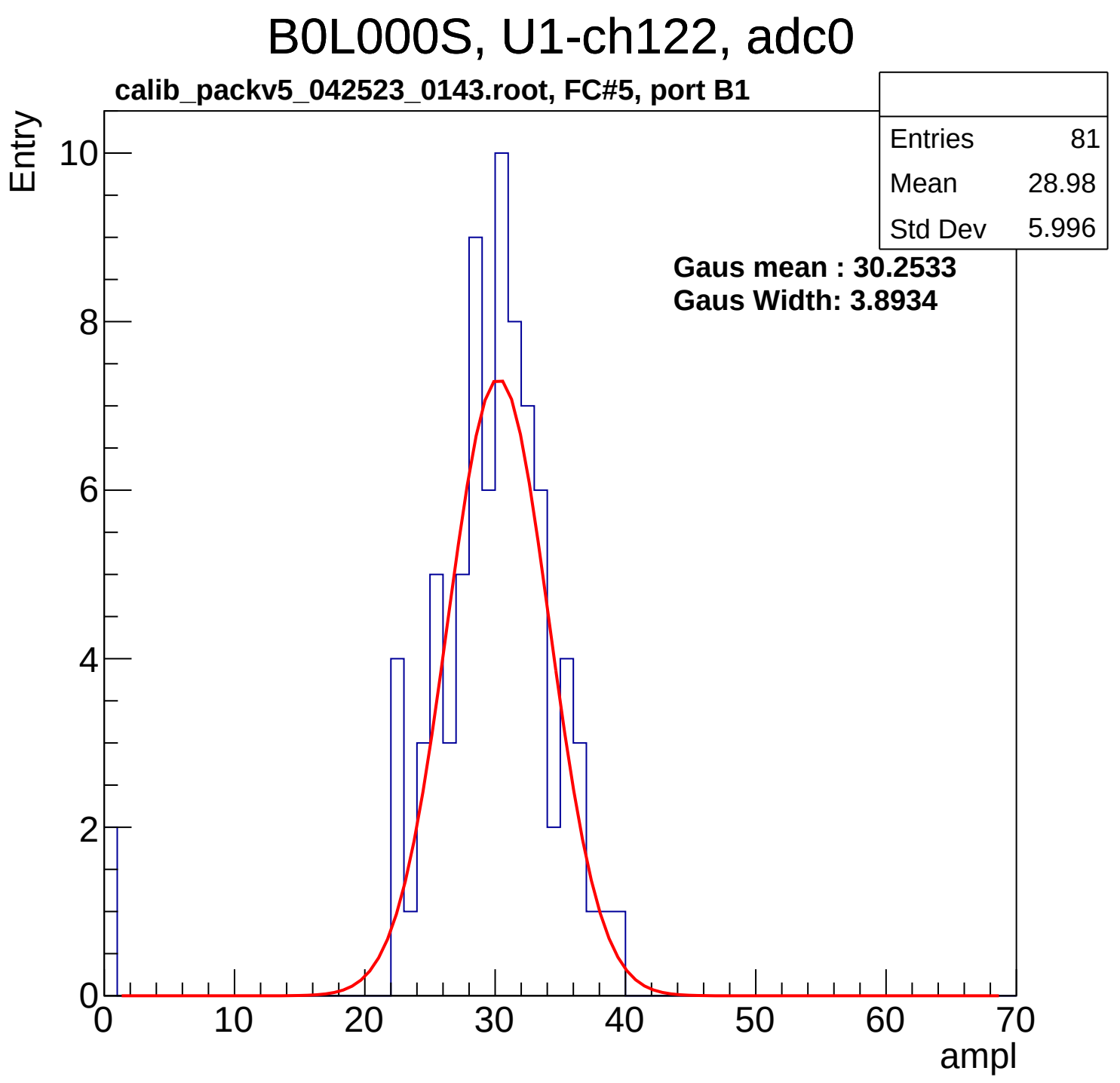
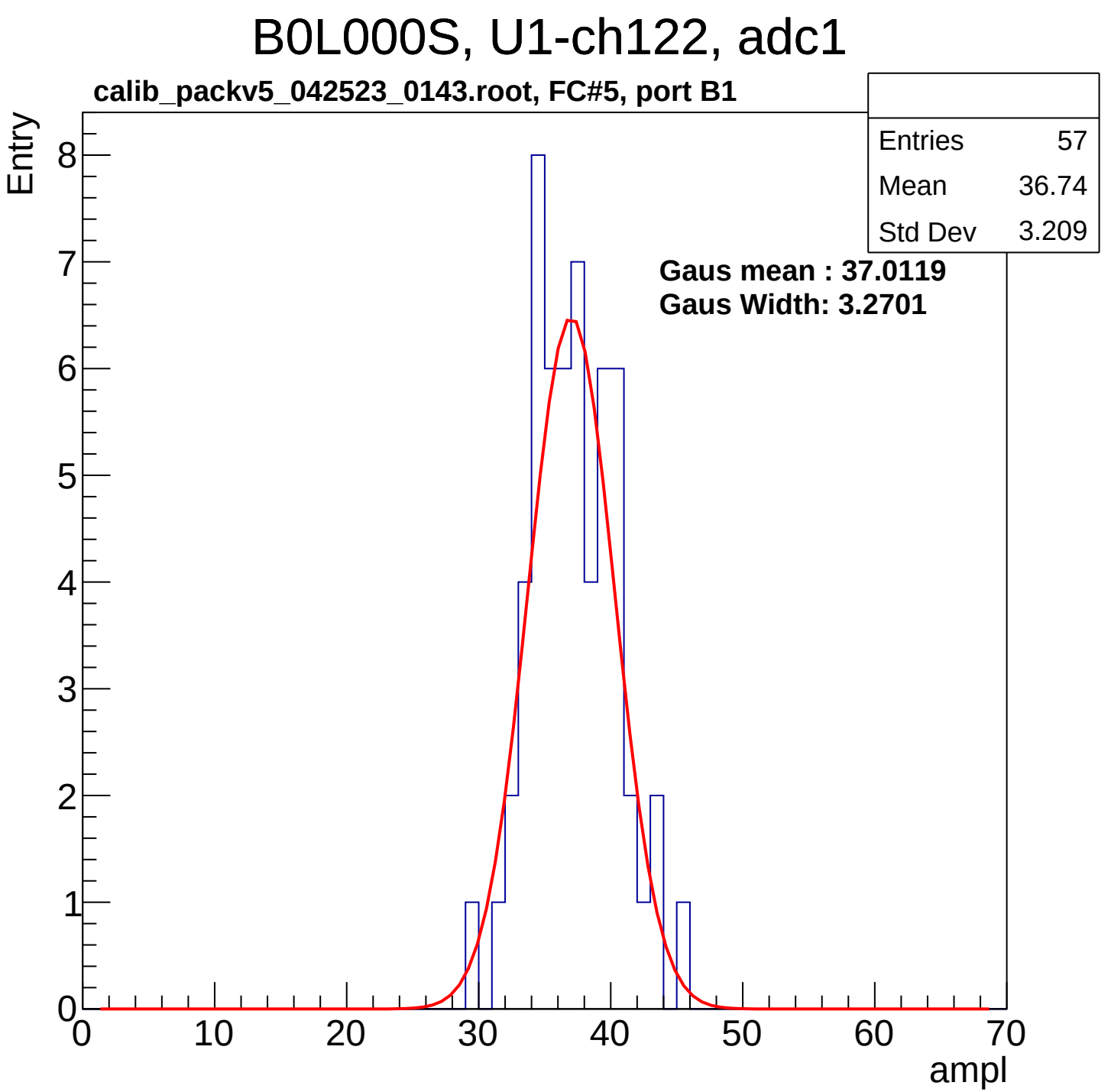
0
10
20
30
40
50
60
70


B0L000S, U1-ch121, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 54.44
Std Dev 19.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U1-ch121, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch122, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 28.98
Std Dev 5.996
Gaus mean : 30.2533
Gaus Width: 3.8934
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch122, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.74
Std Dev 3.209
Gaus mean : 37.0119
Gaus Width: 3.2701
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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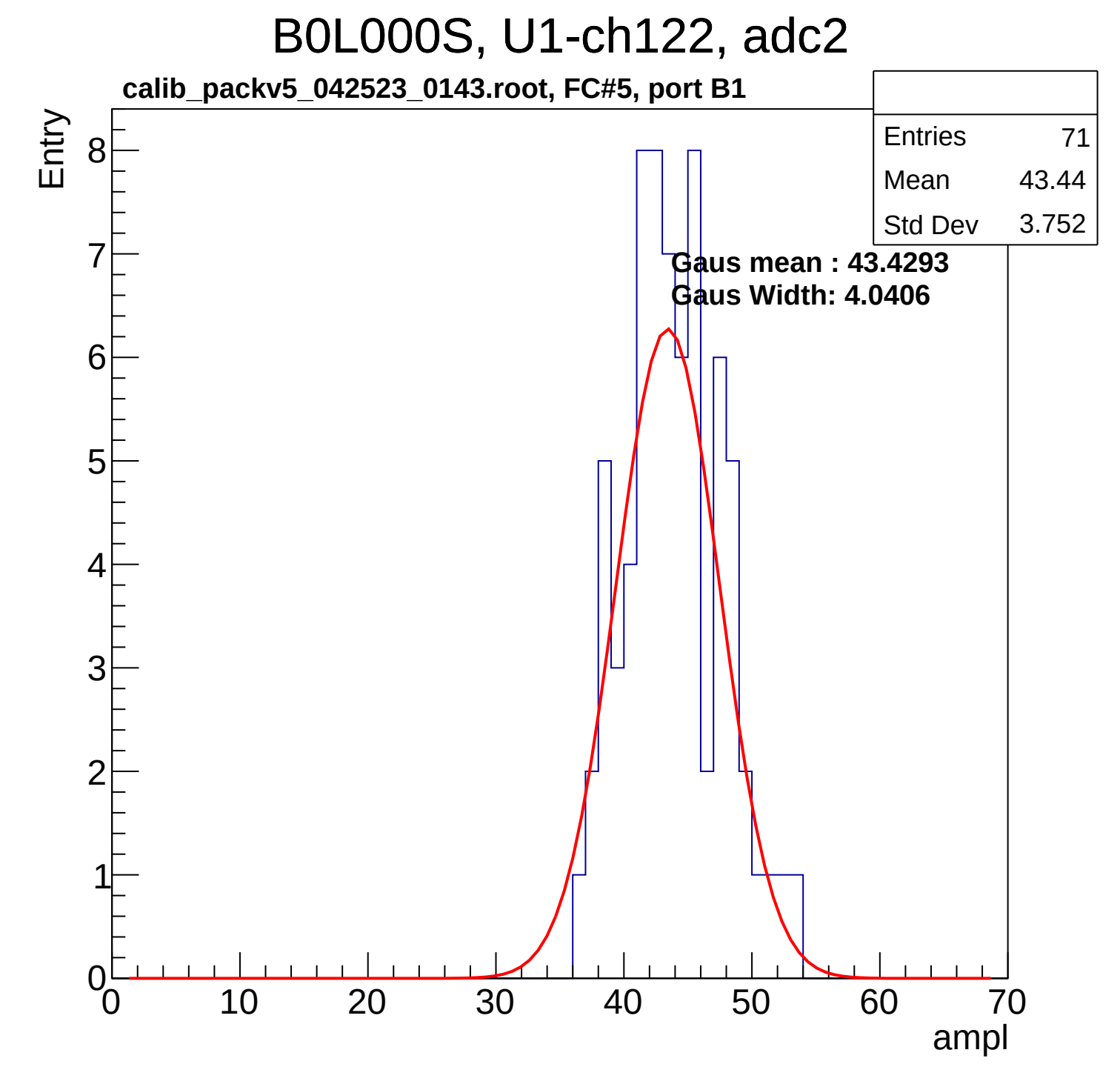
B0L000S, U1-ch122, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.44
Std Dev 3.752
Gaus mean : 43.4293
Gaus Width: 4.0406
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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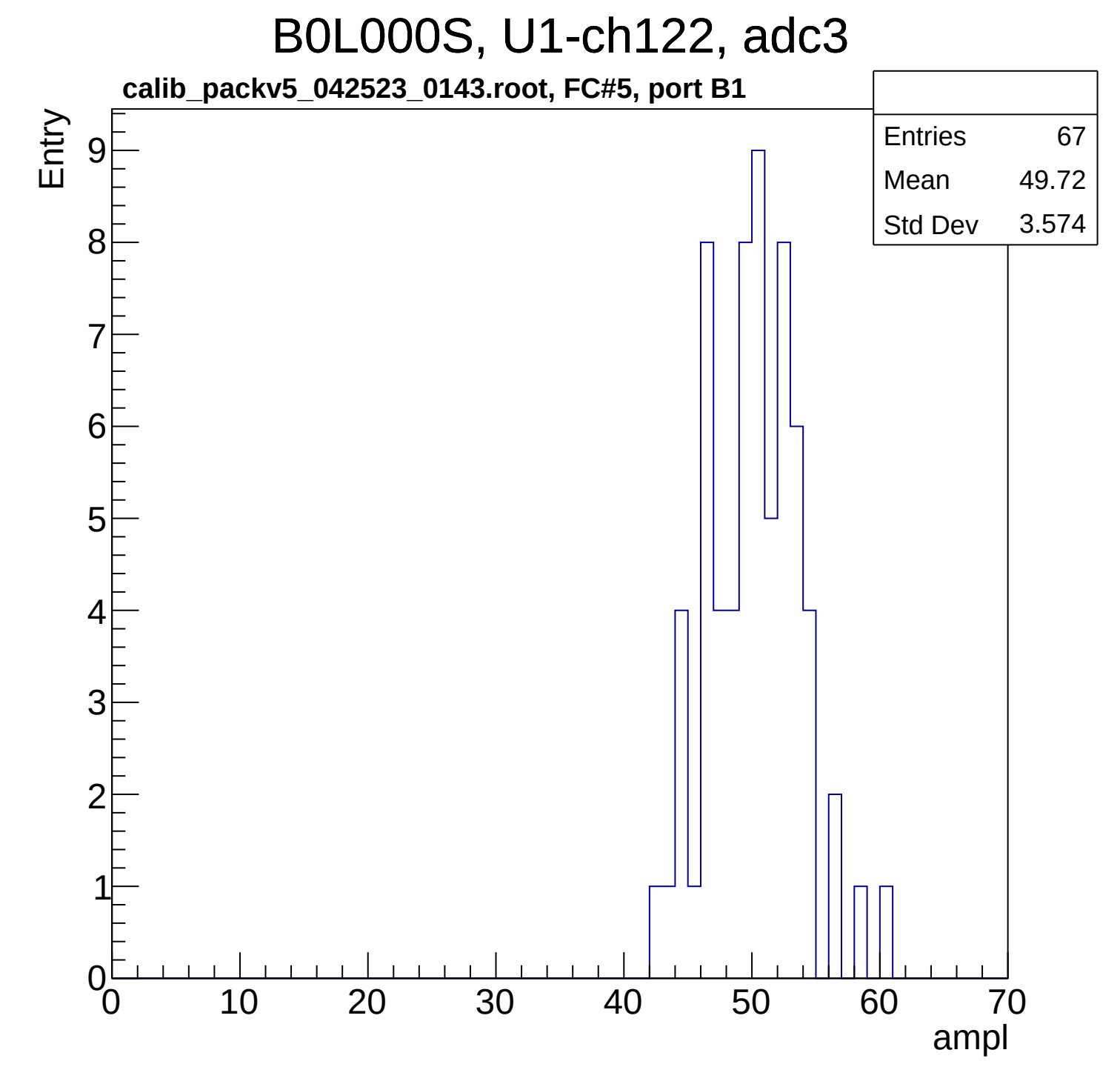
B0L000S, U1-ch122, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.72
Std Dev 3.574
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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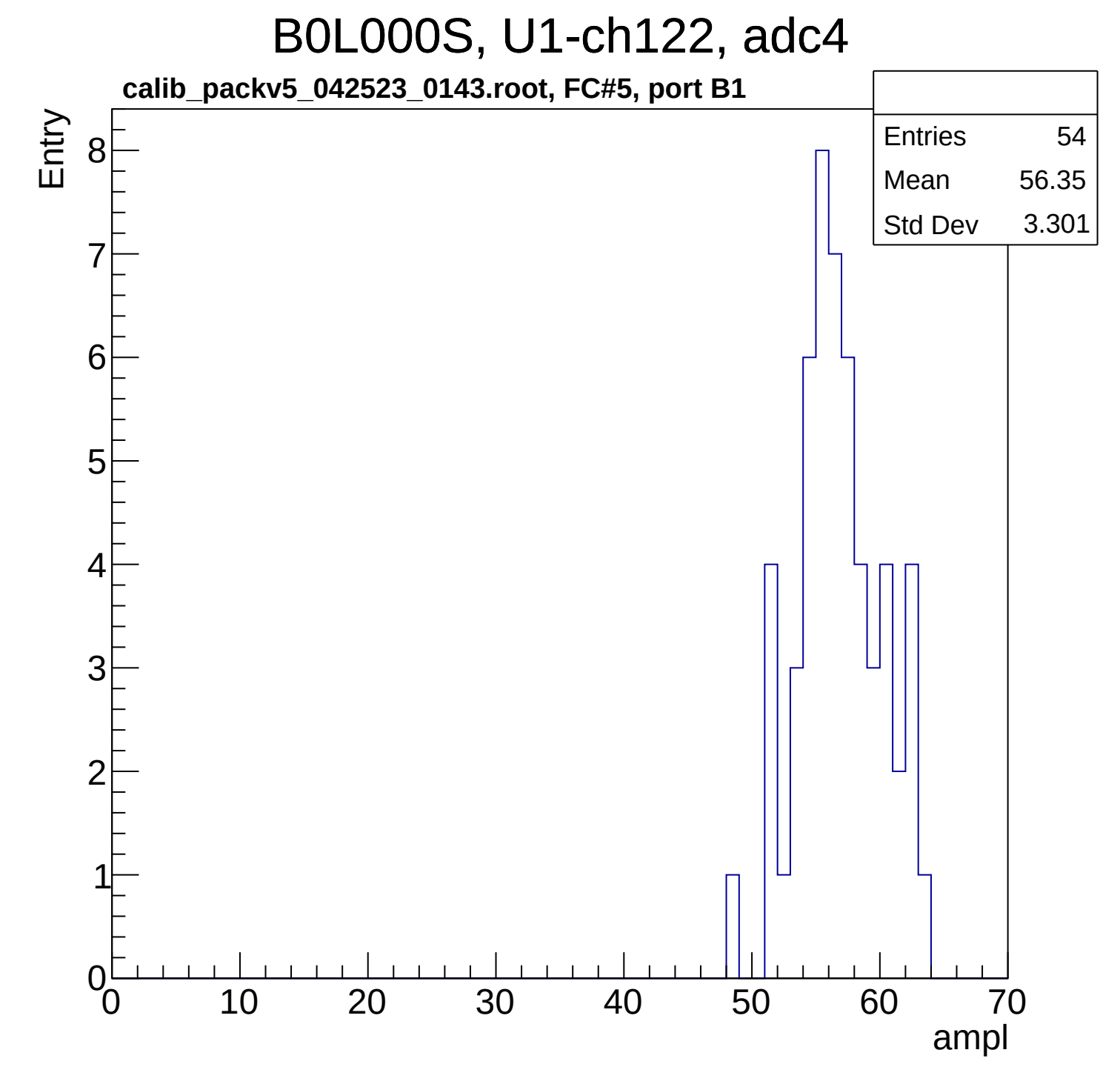
B0L000S, U1-ch122, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.35
Std Dev 3.301
Entry
ampl
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1
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4
5
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7
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0
10
20
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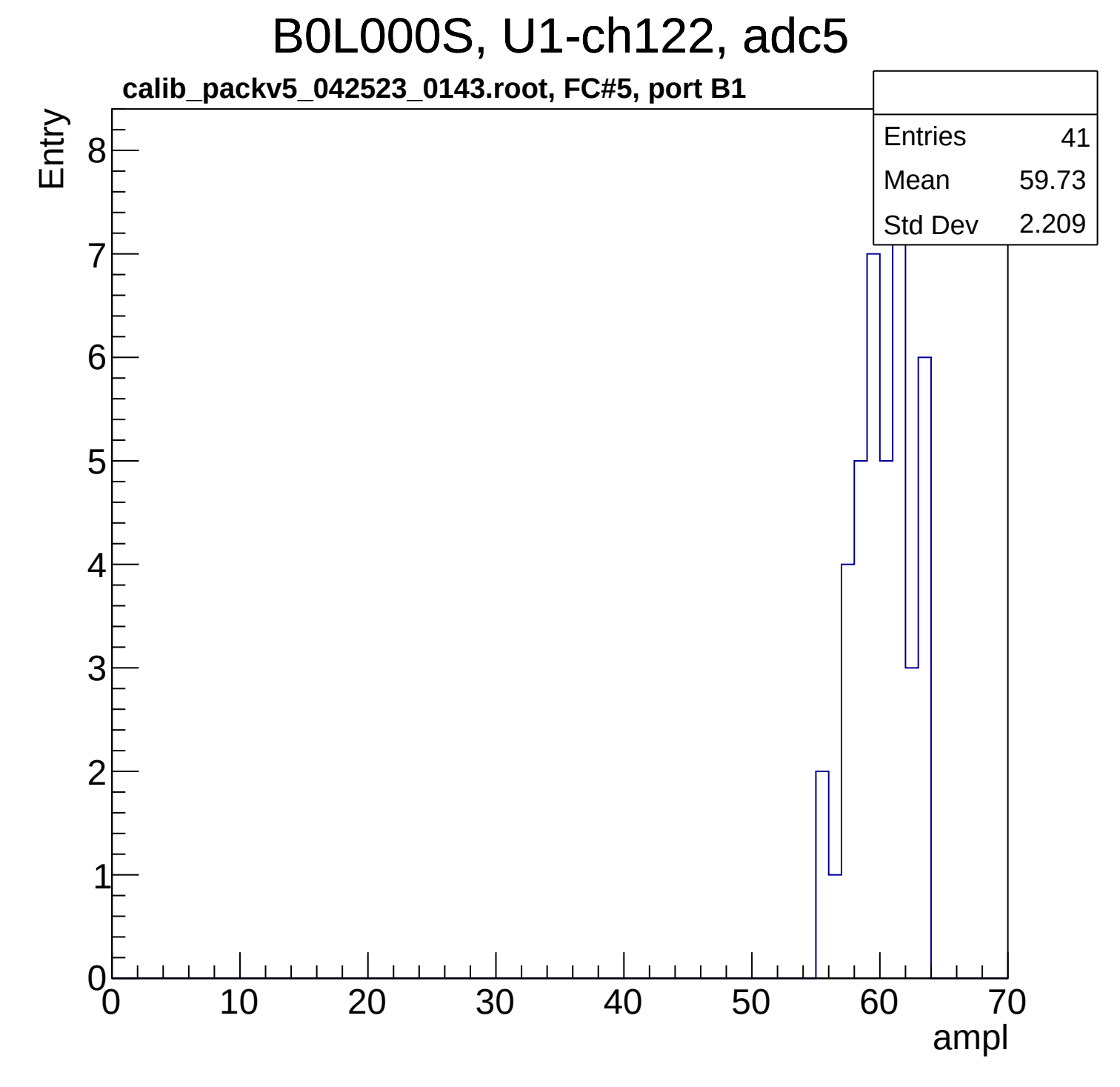
40
50
60
70


B0L000S, U1-ch122, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.73
Std Dev 2.209
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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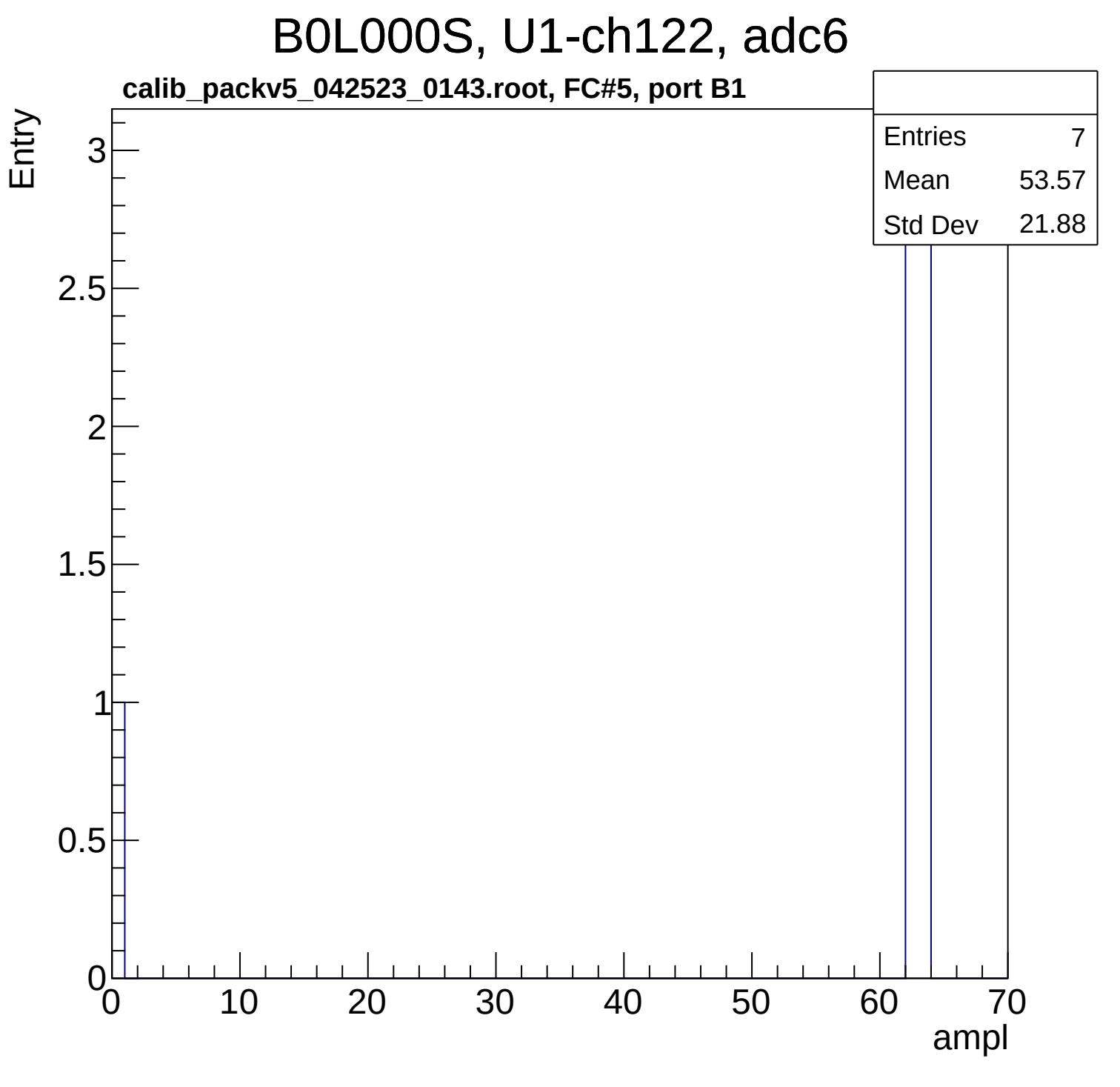
B0L000S, U1-ch122, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
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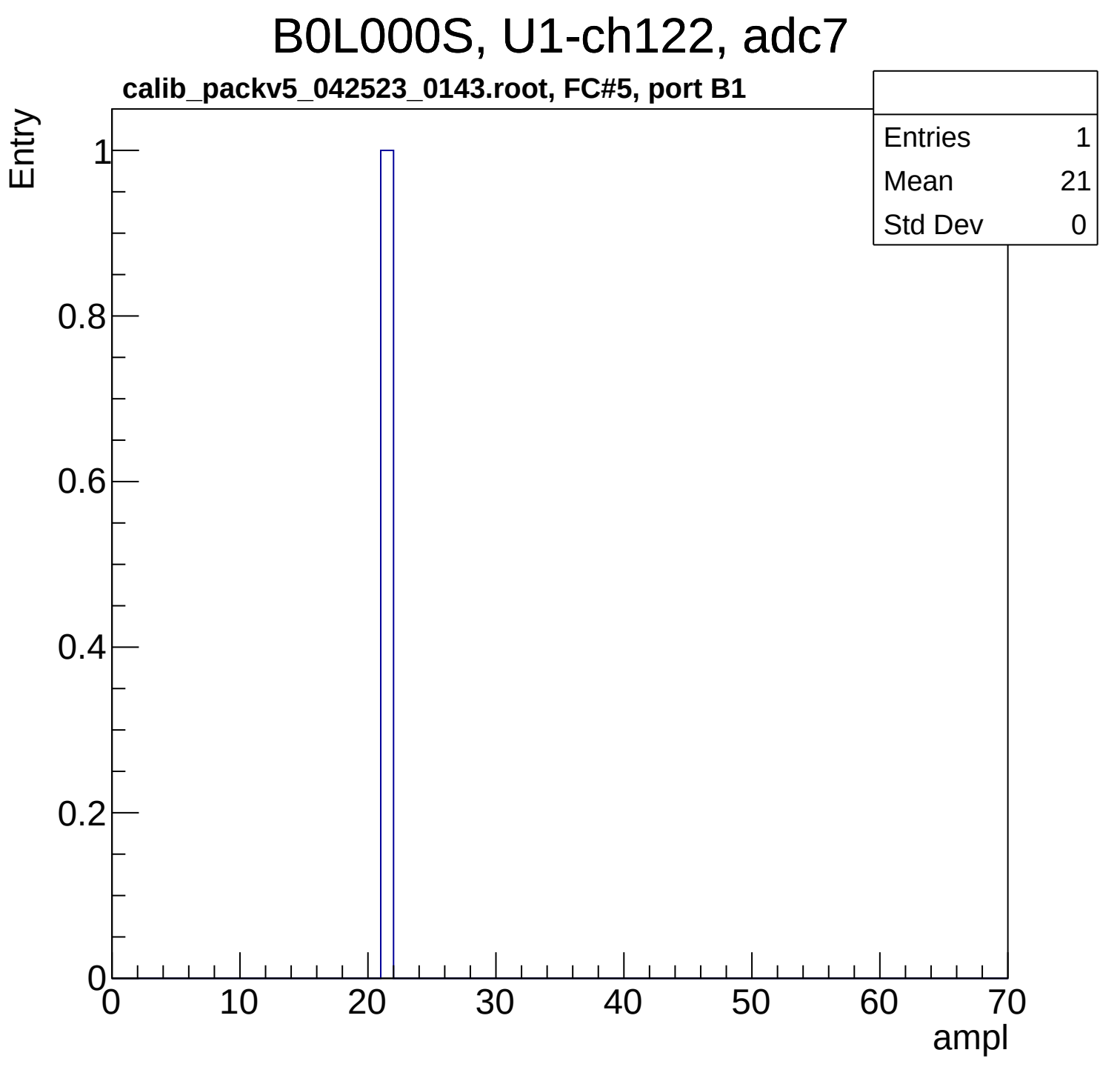
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch122, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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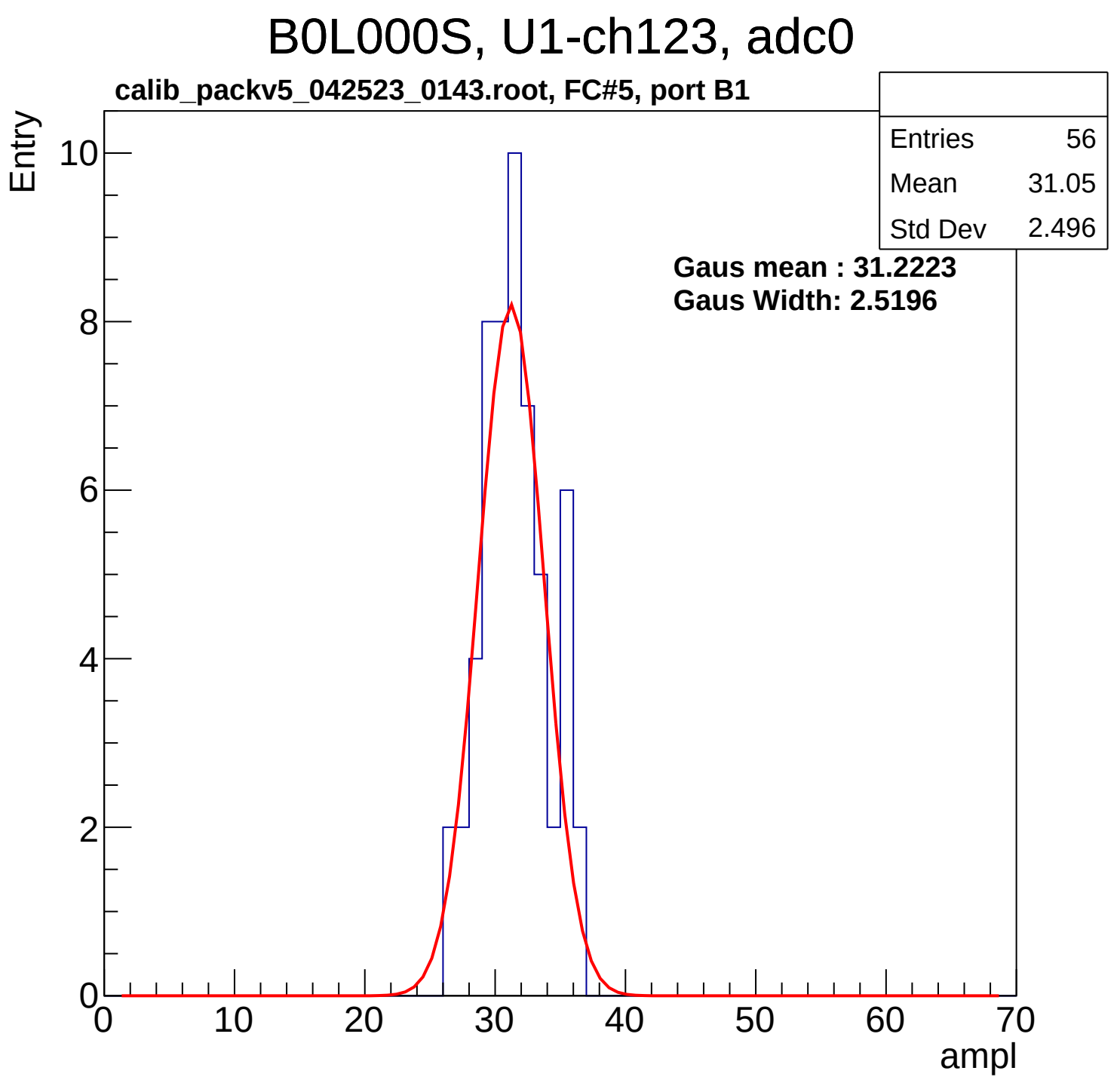
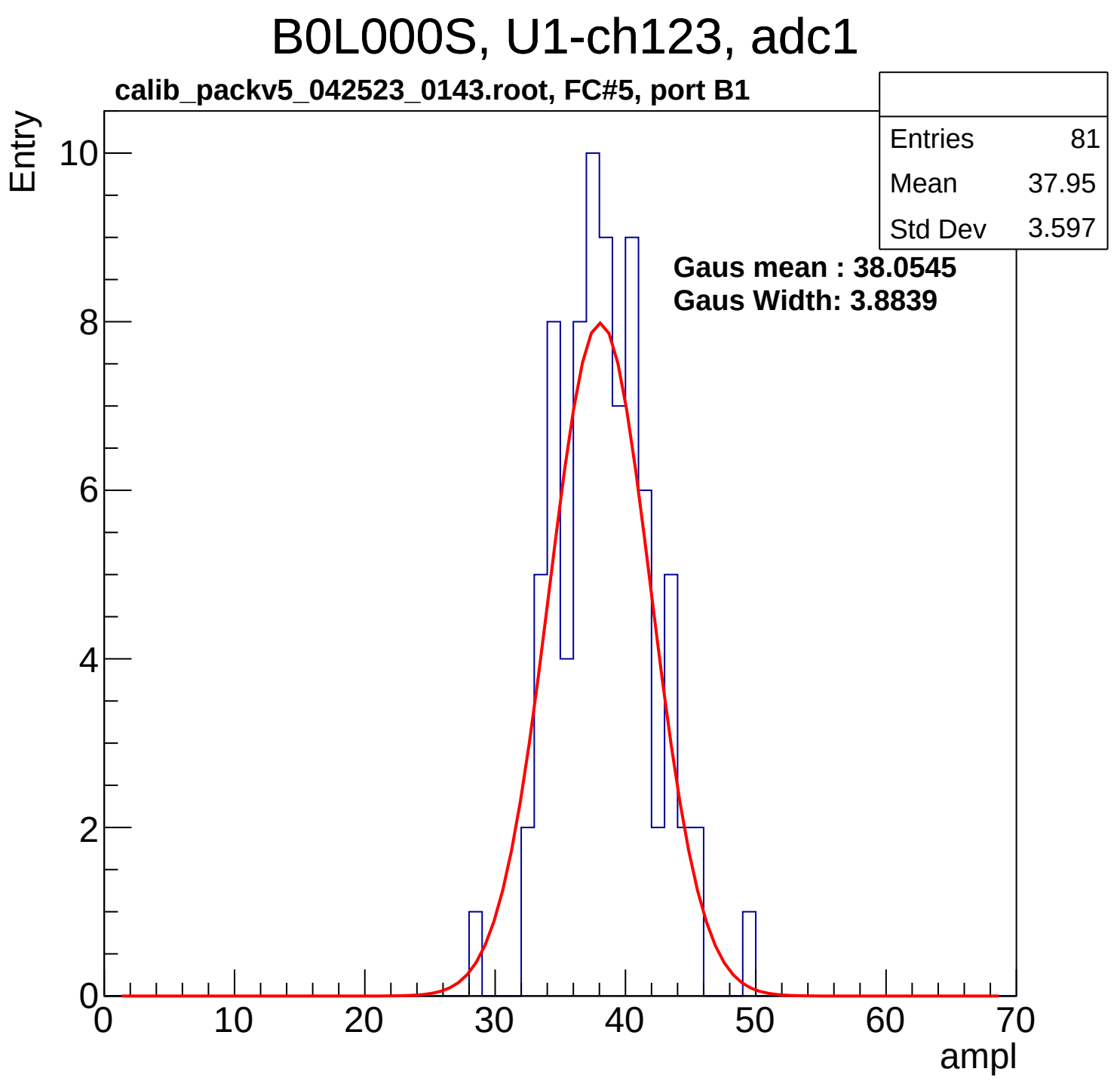
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch123, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 31.05
Std Dev 2.496
Gaus mean : 31.2223
Gaus Width: 2.5196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U1-ch123, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 37.95
Std Dev 3.597
Gaus mean : 38.0545
Gaus Width: 3.8839
Entry
ampl
0
2
4
6
8
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0
10
20
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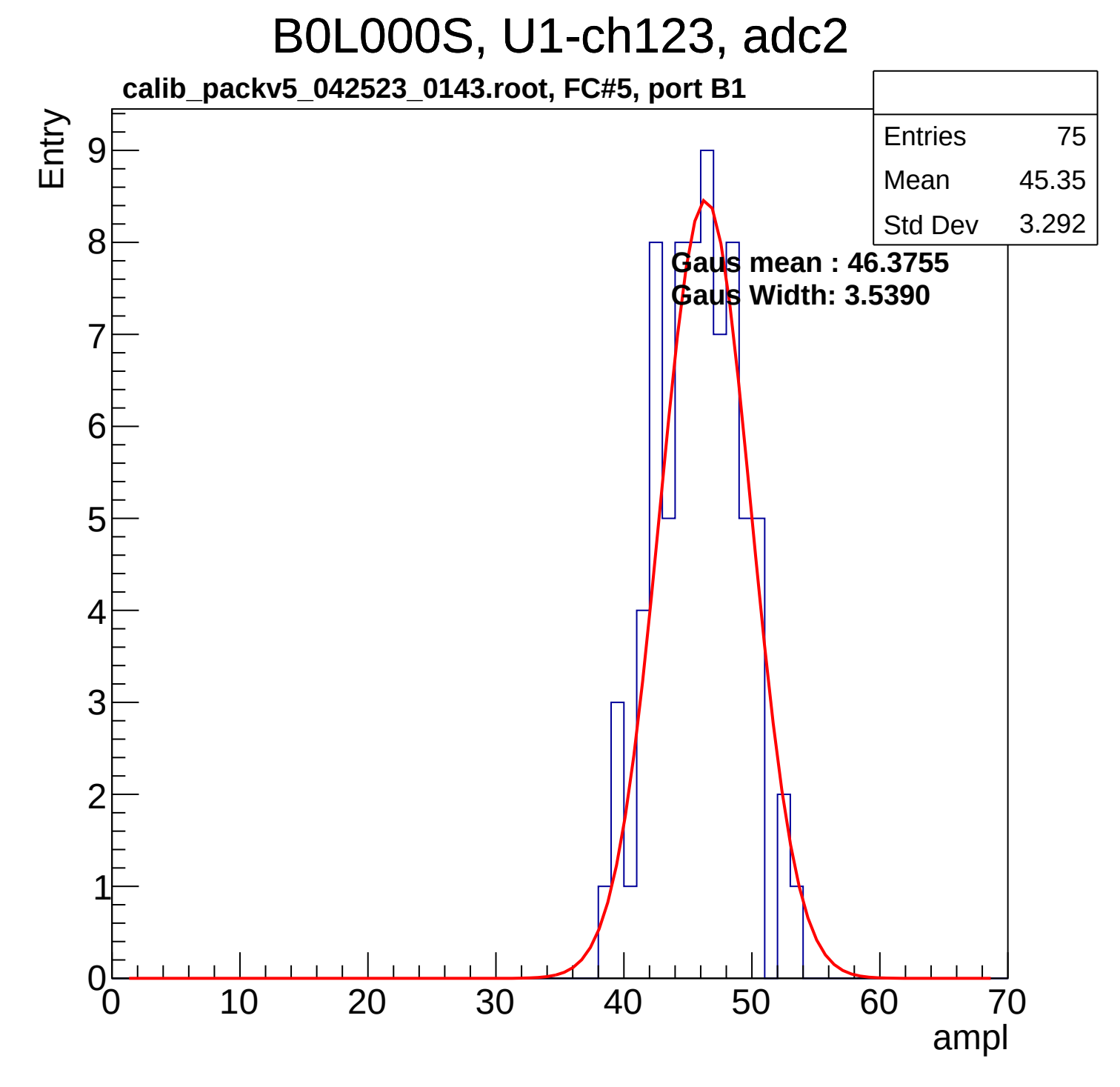
40
50
60
70


B0L000S, U1-ch123, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 45.35
Std Dev 3.292
Gaus mean : 46.3755
Gaus Width: 3.5390
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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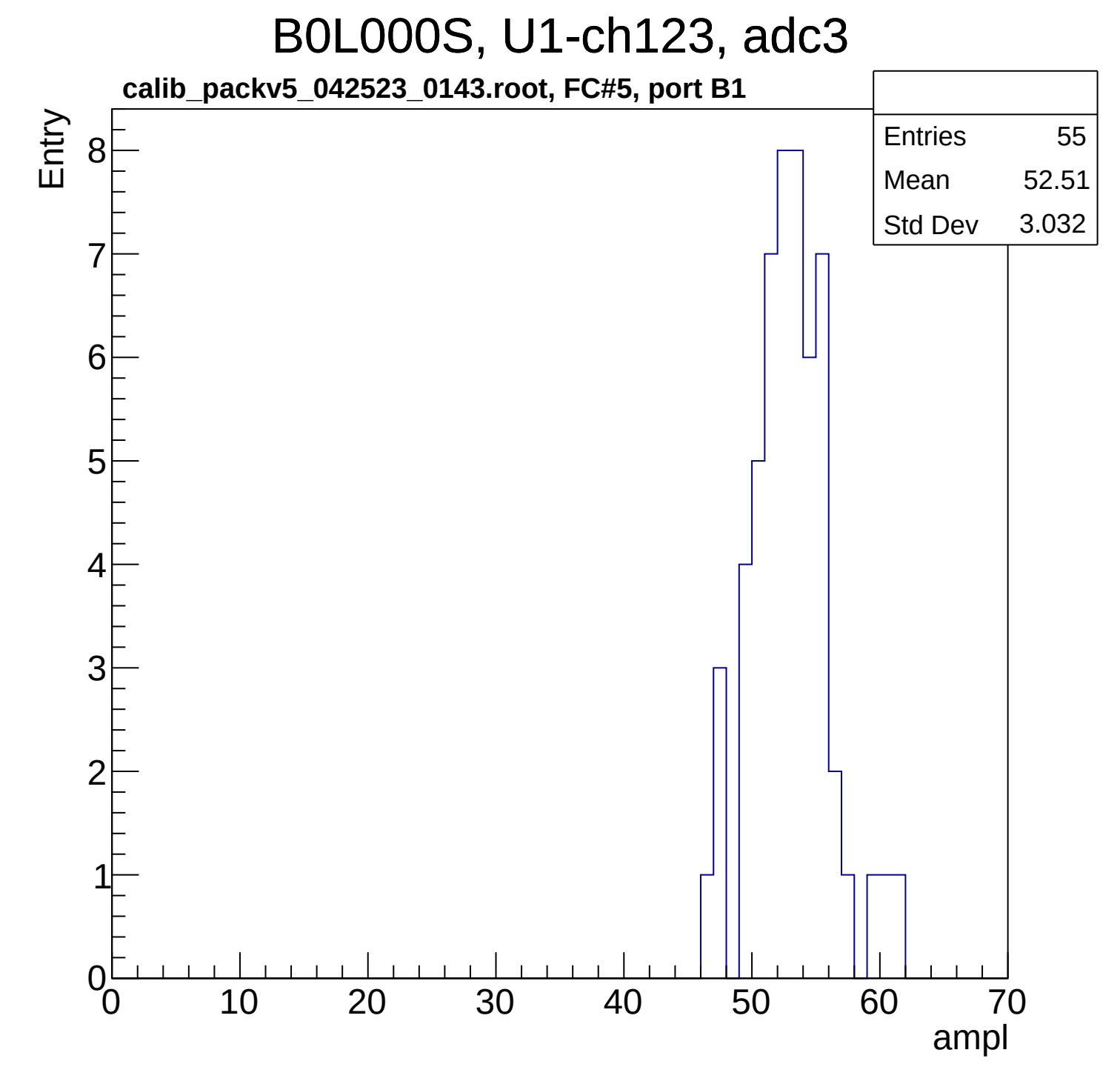
B0L000S, U1-ch123, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 52.51
Std Dev 3.032
Entry
ampl
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2
3
4
5
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0
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60
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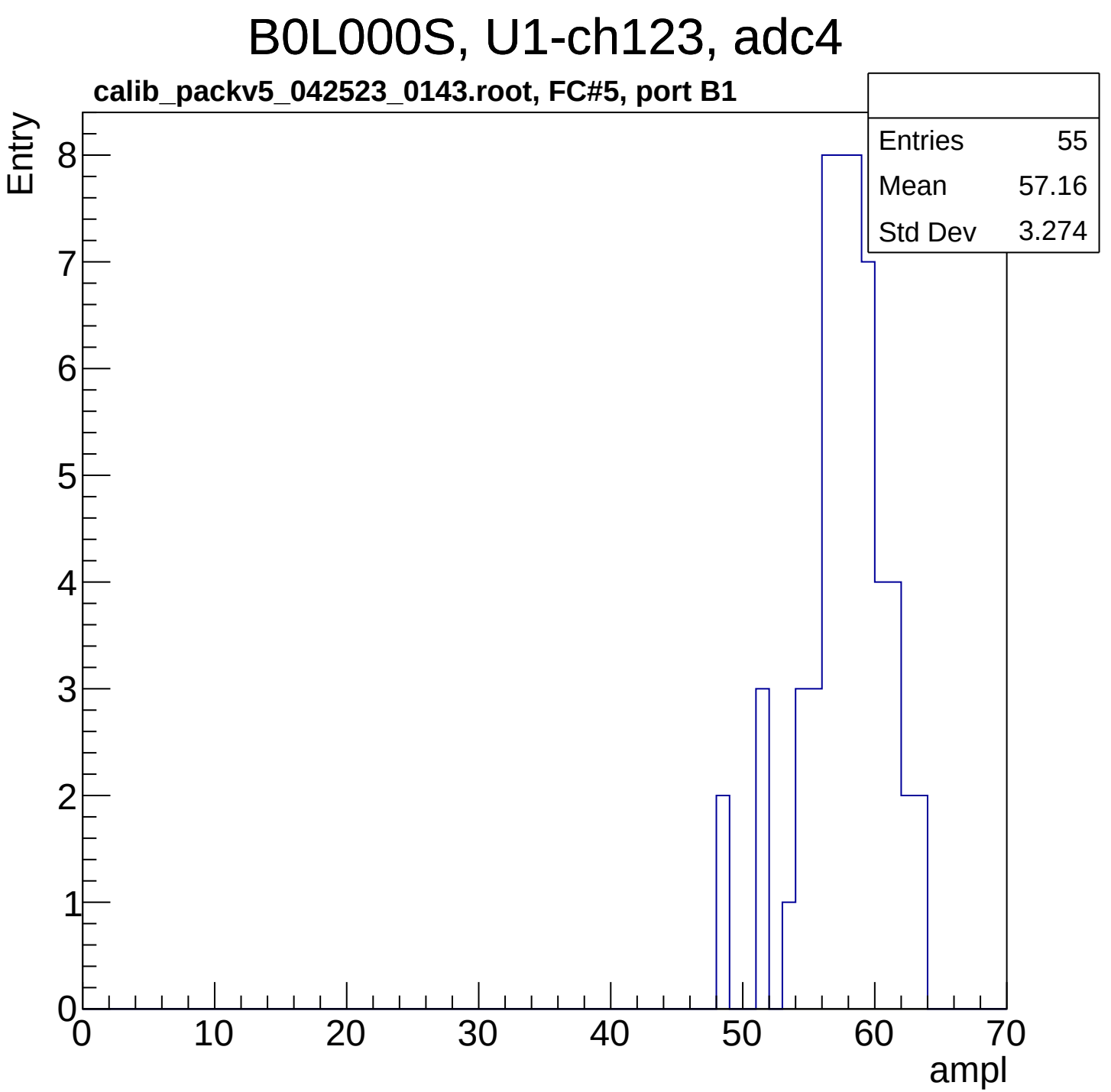
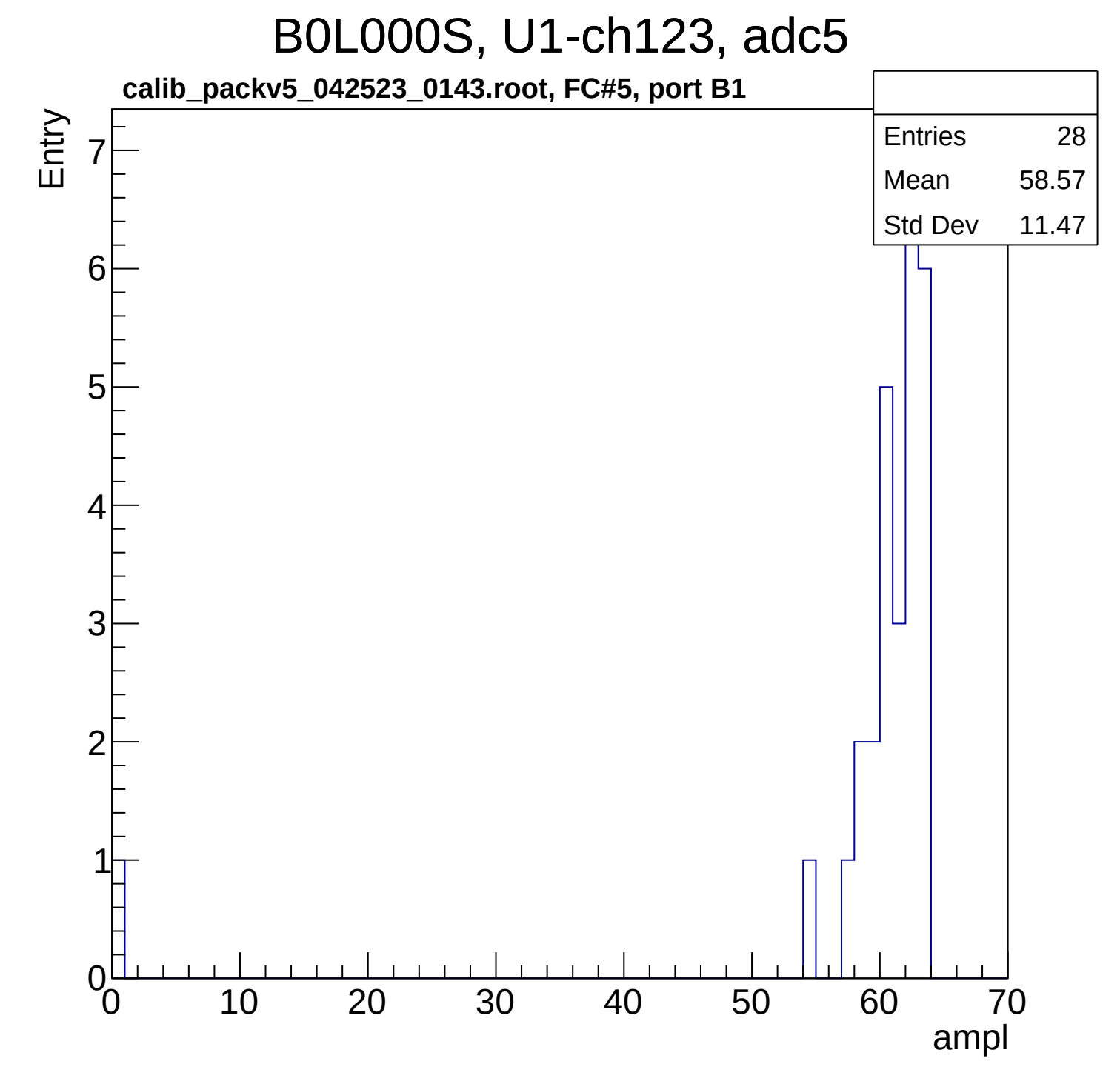
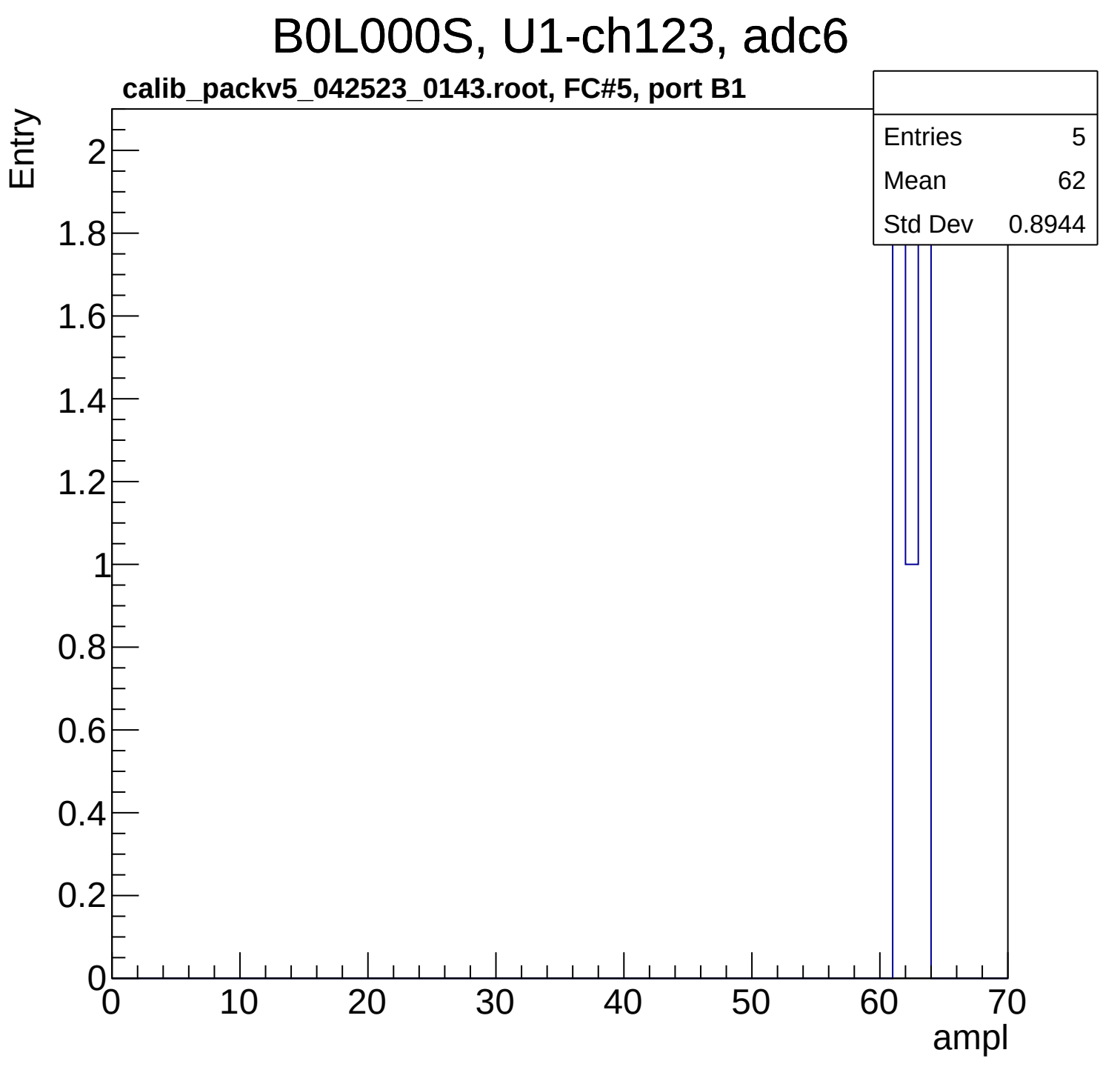
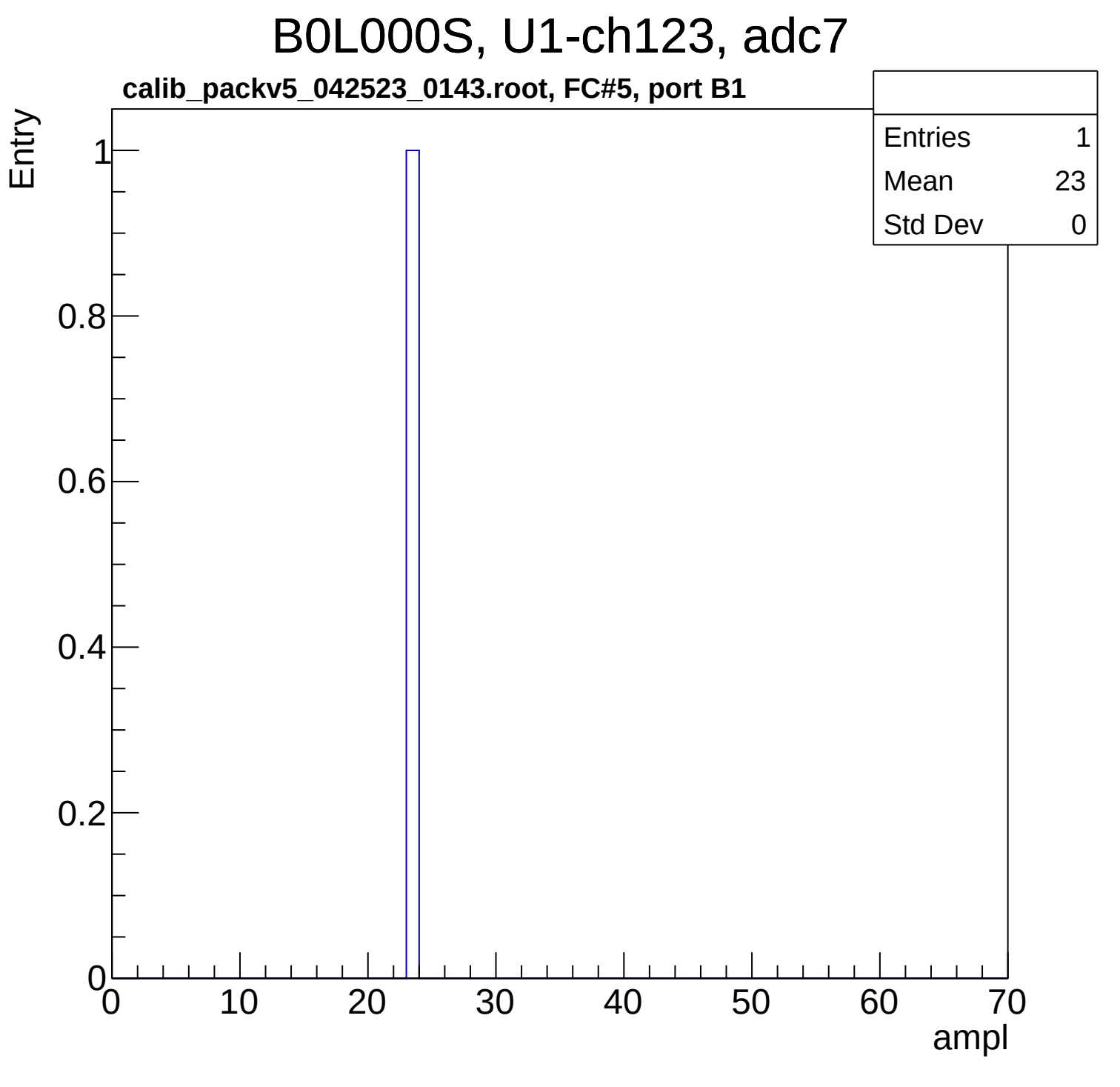
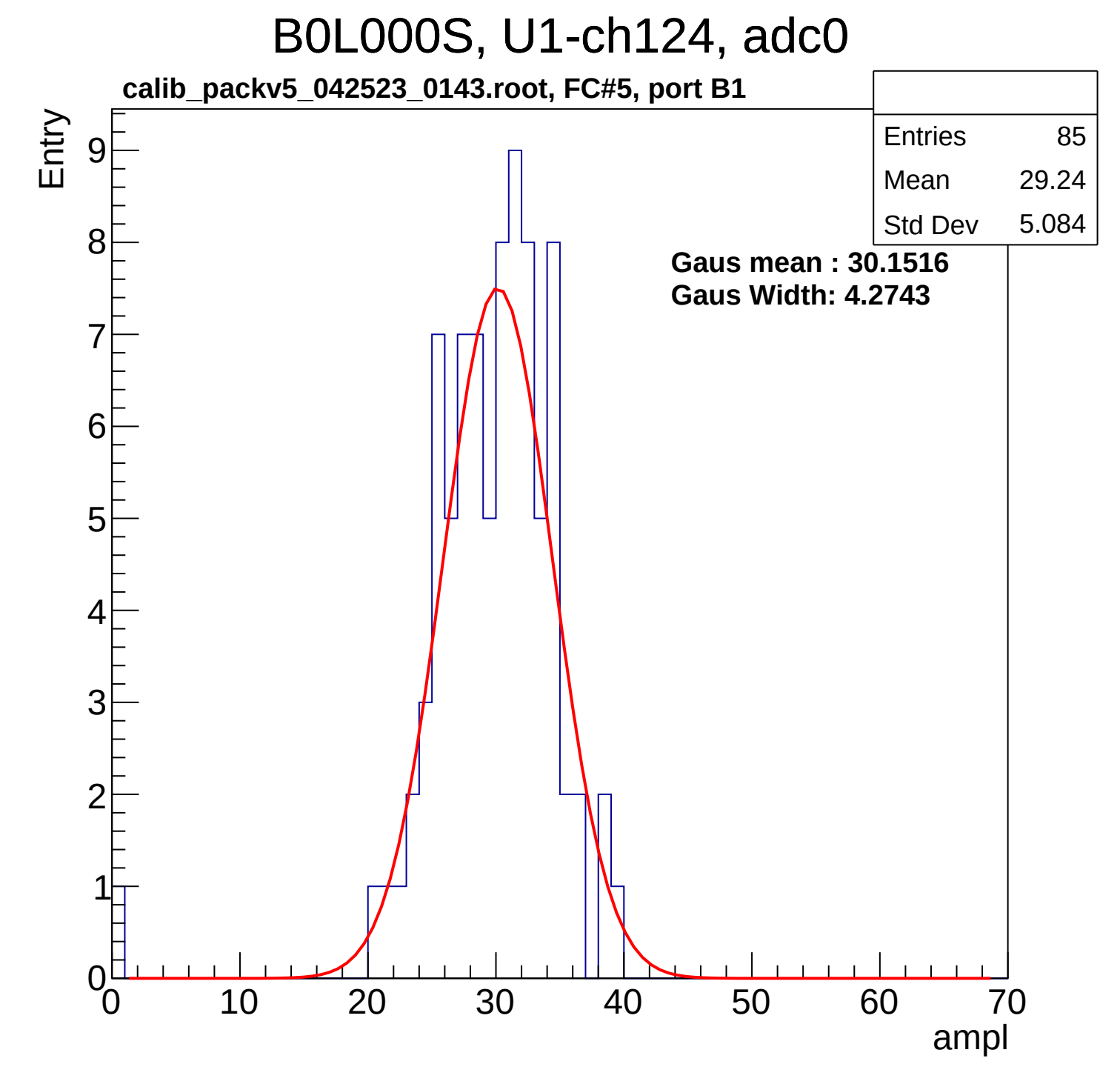
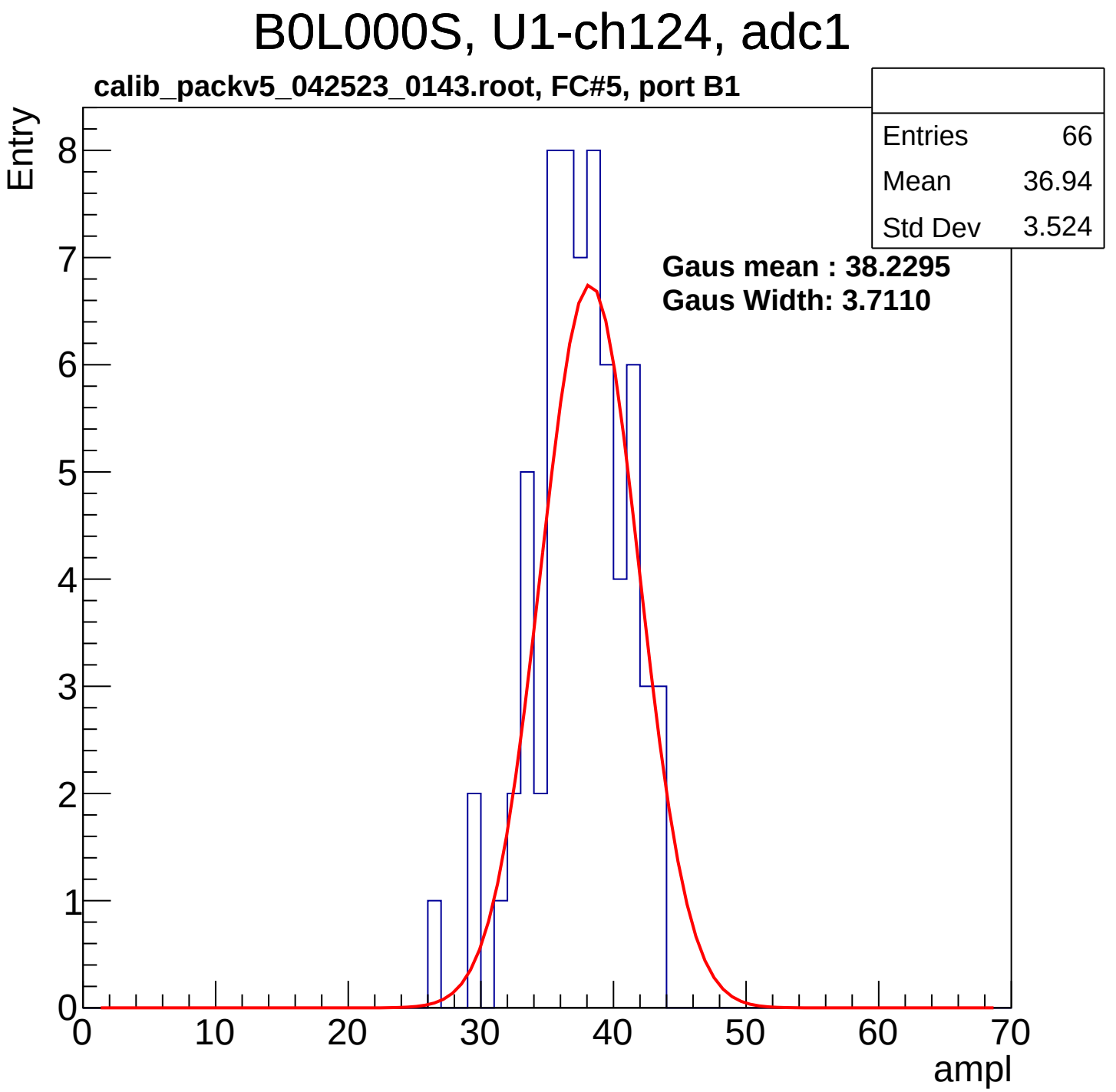

B0L000S, U1-ch123, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.16
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

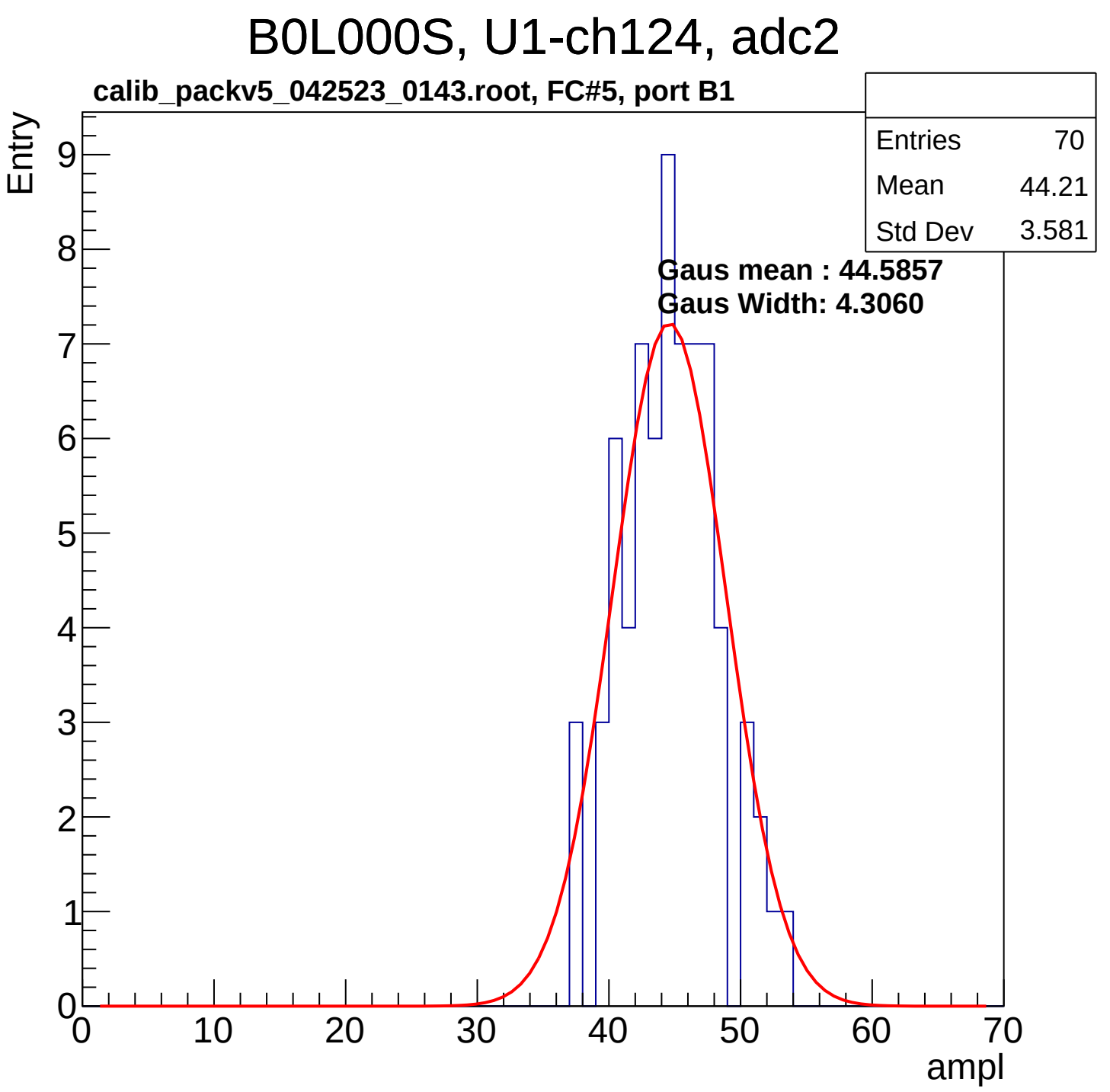

B0L000S, U1-ch123, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.57
Std Dev 11.47
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

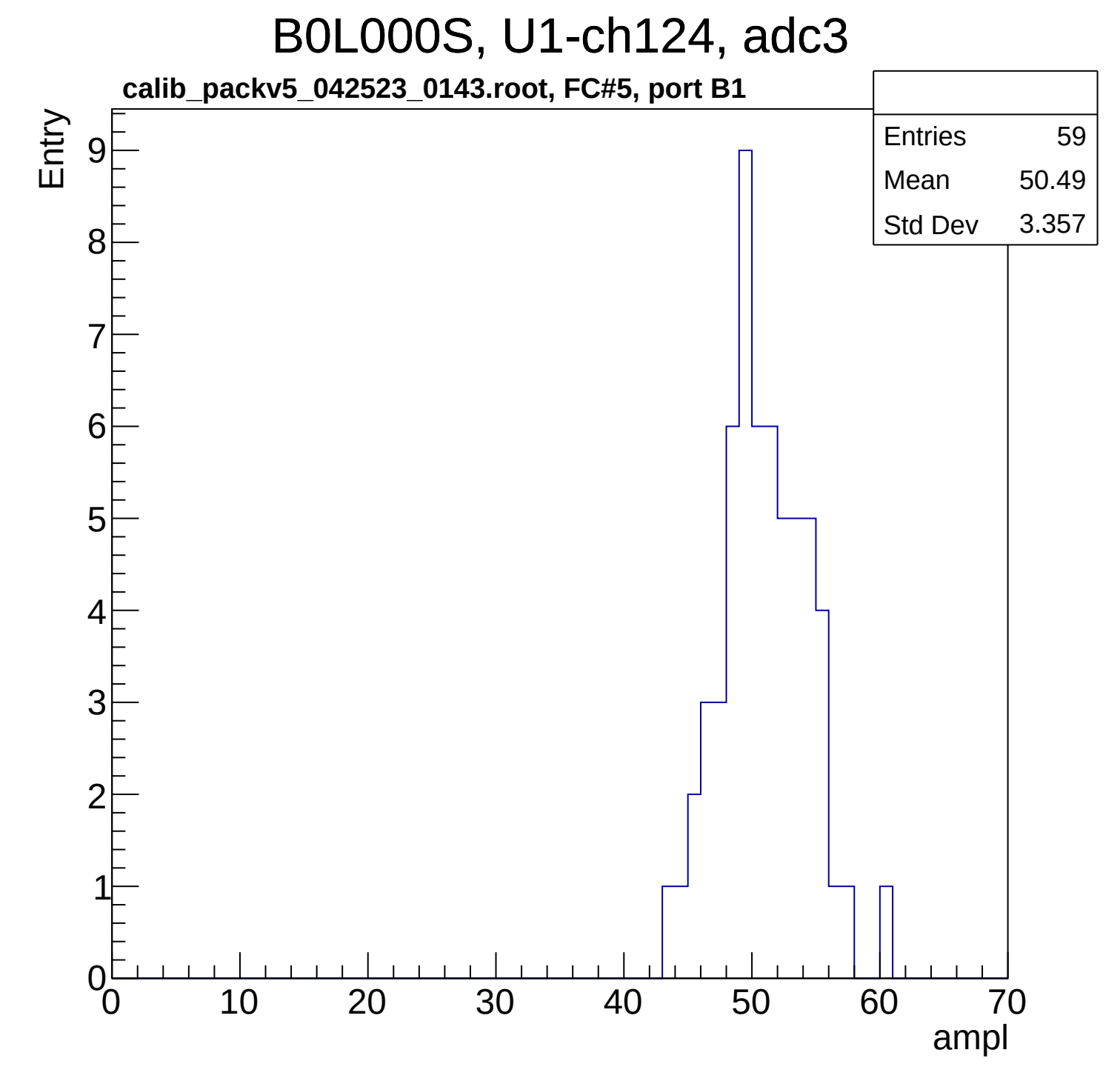

B0L000S, U1-ch123, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

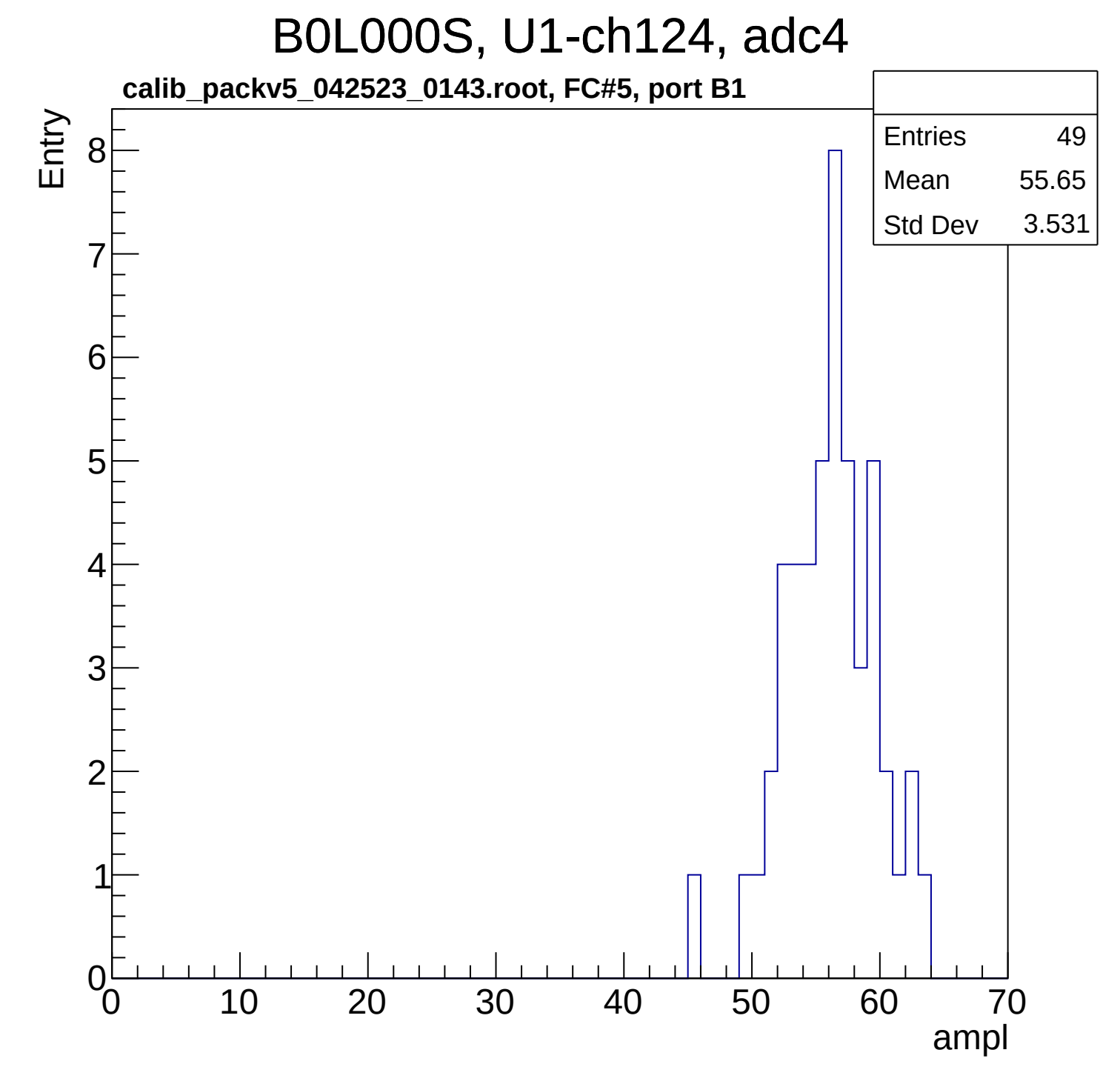

B0L000S, U1-ch123, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

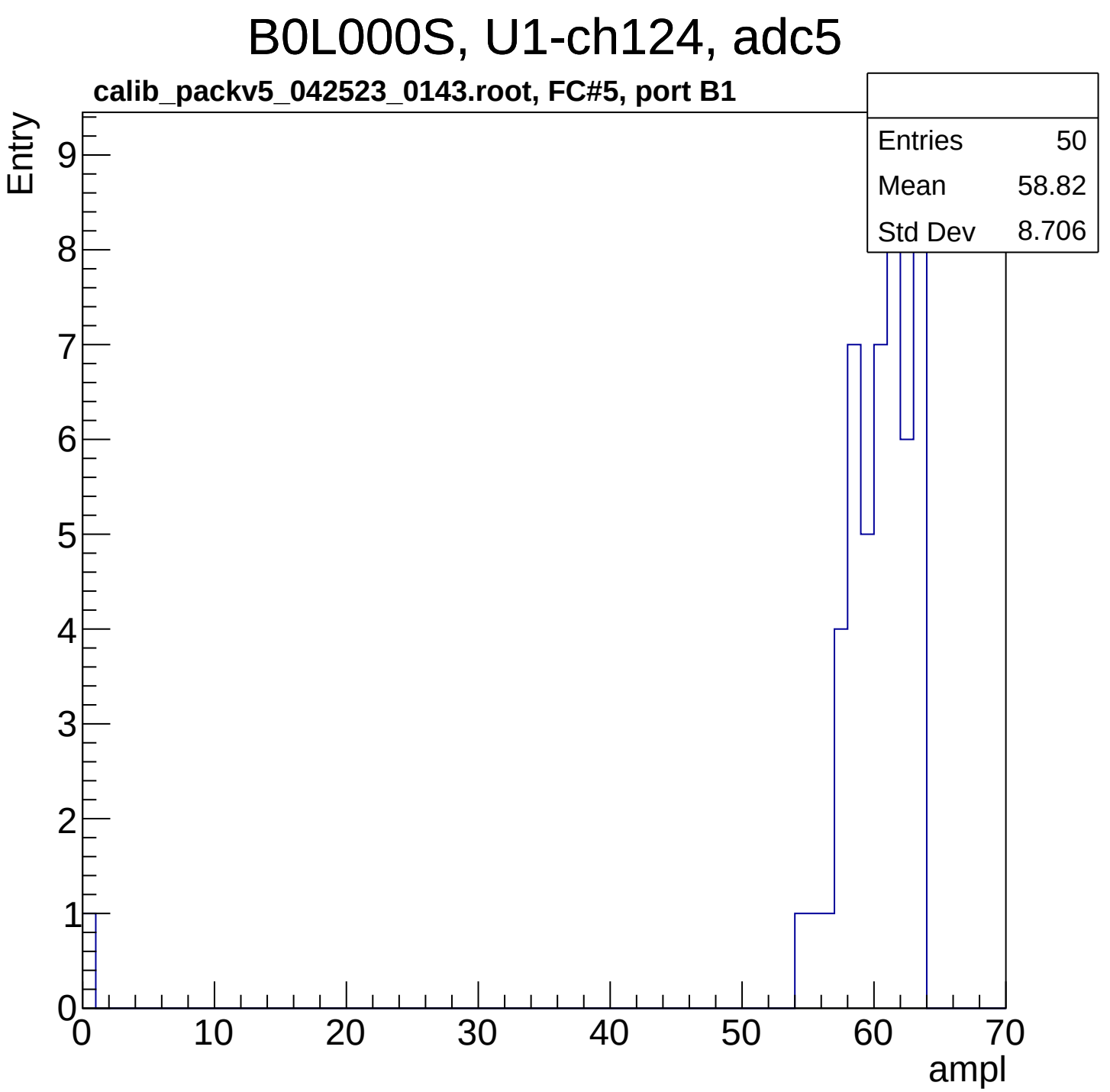

B0L000S, U1-ch124, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 29.24
Std Dev 5.084
Gaus mean : 30.1516
Gaus Width: 4.2743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

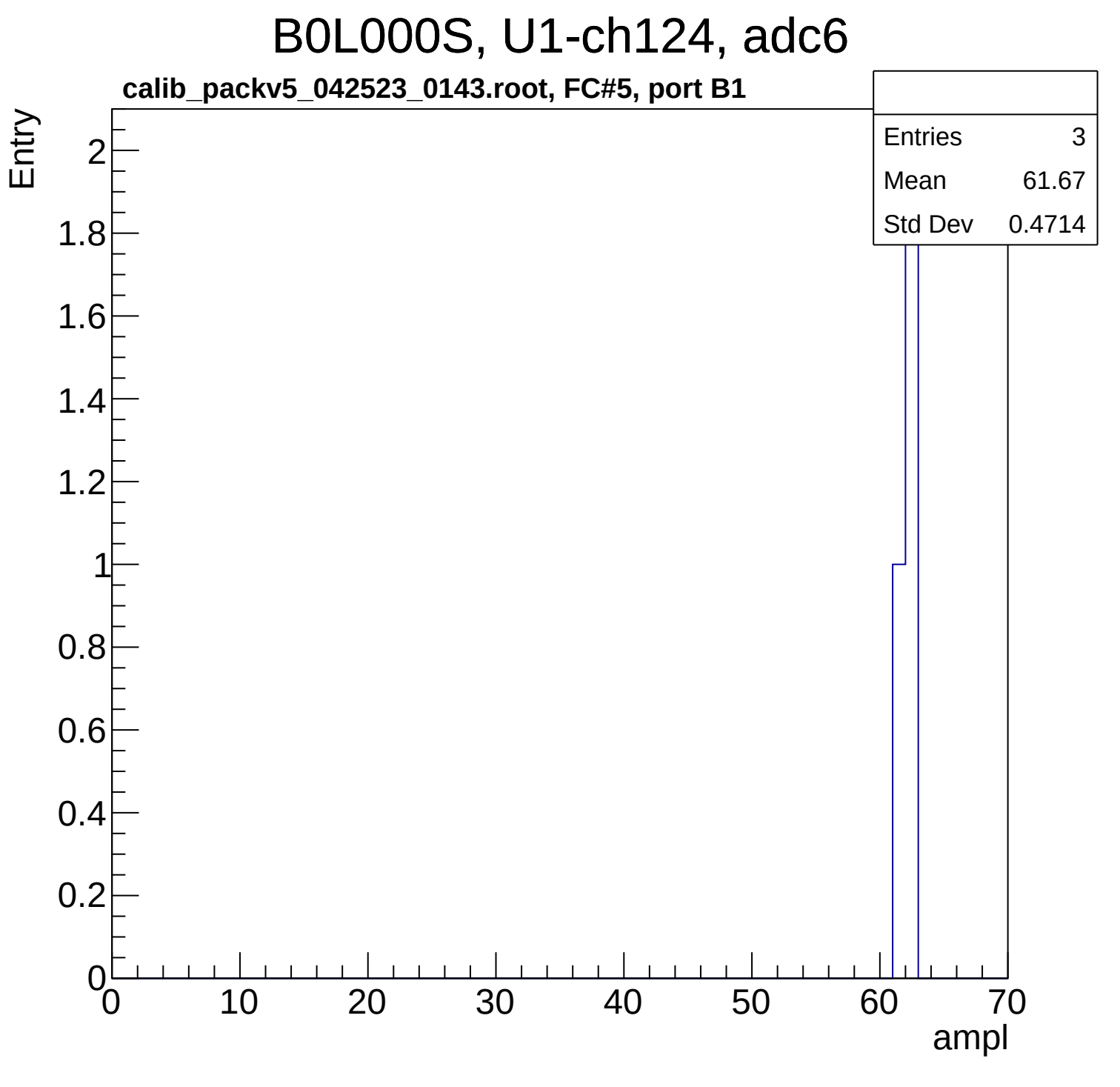

B0L000S, U1-ch124, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.94
Std Dev 3.524
Gaus mean : 38.2295
Gaus Width: 3.7110
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

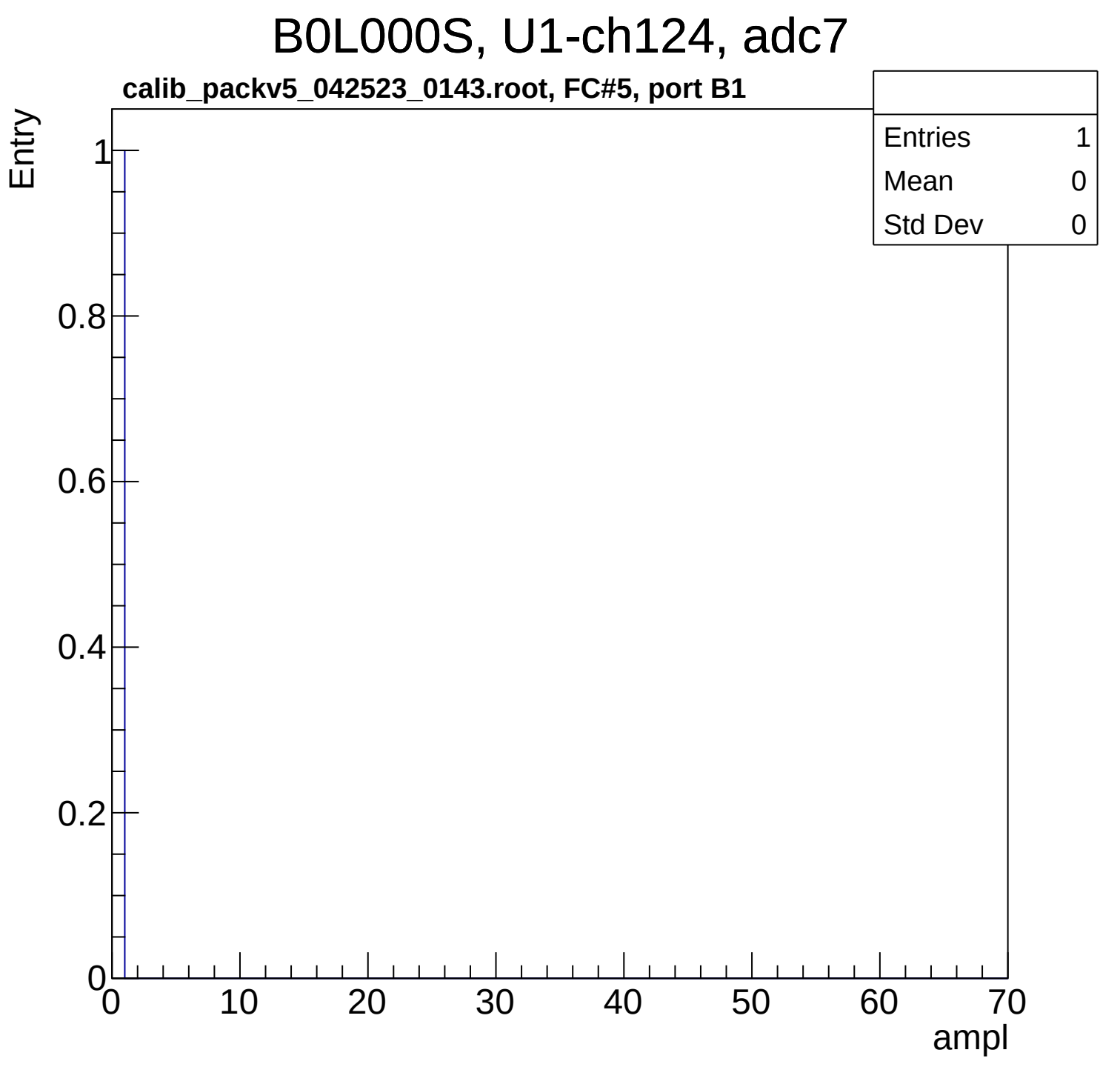

B0L000S, U1-ch124, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 44.21
Std Dev 3.581
Gaus mean : 44.5857
Gaus Width: 4.3060
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

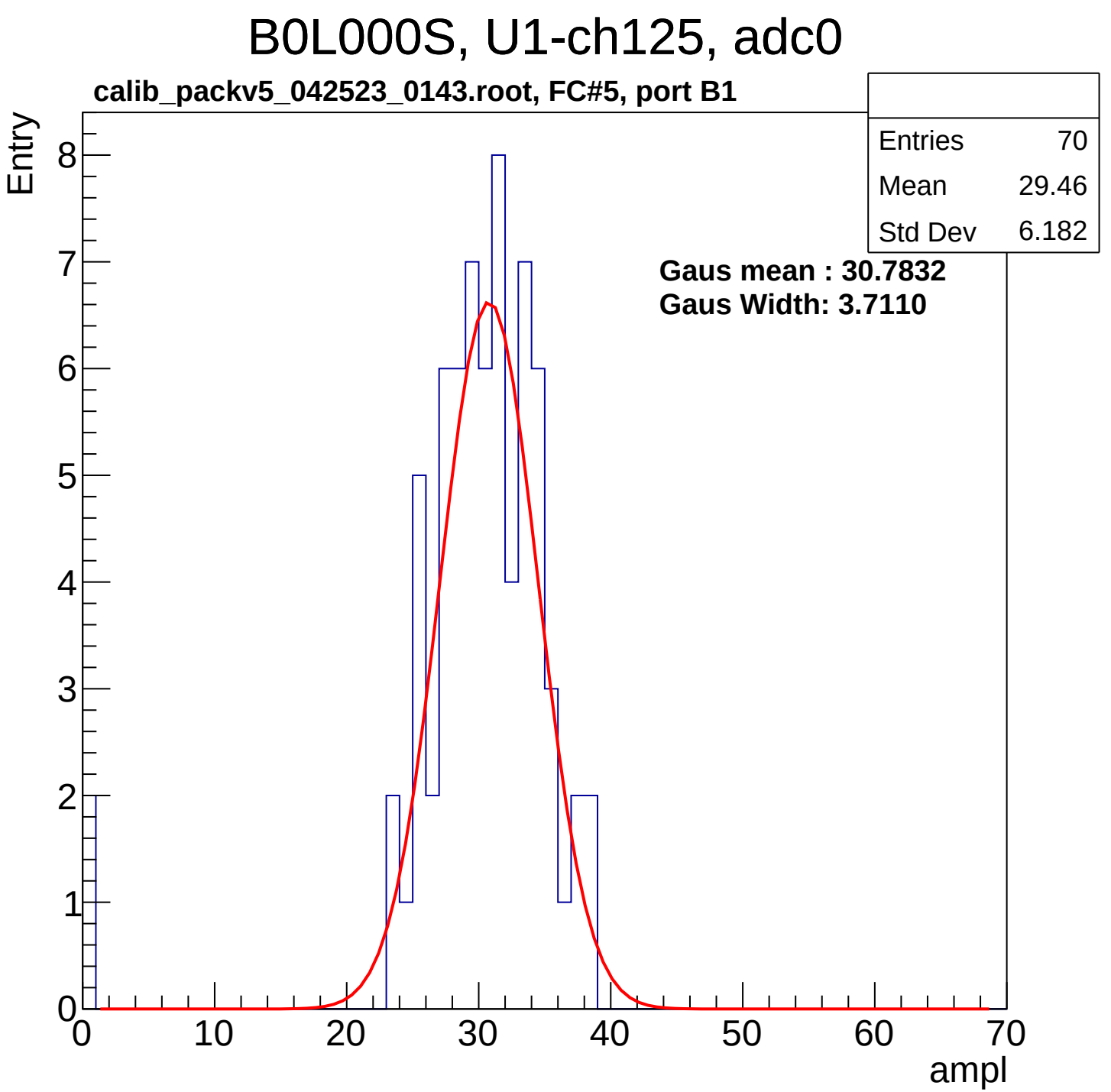

B0L000S, U1-ch124, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 50.49
Std Dev 3.357
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

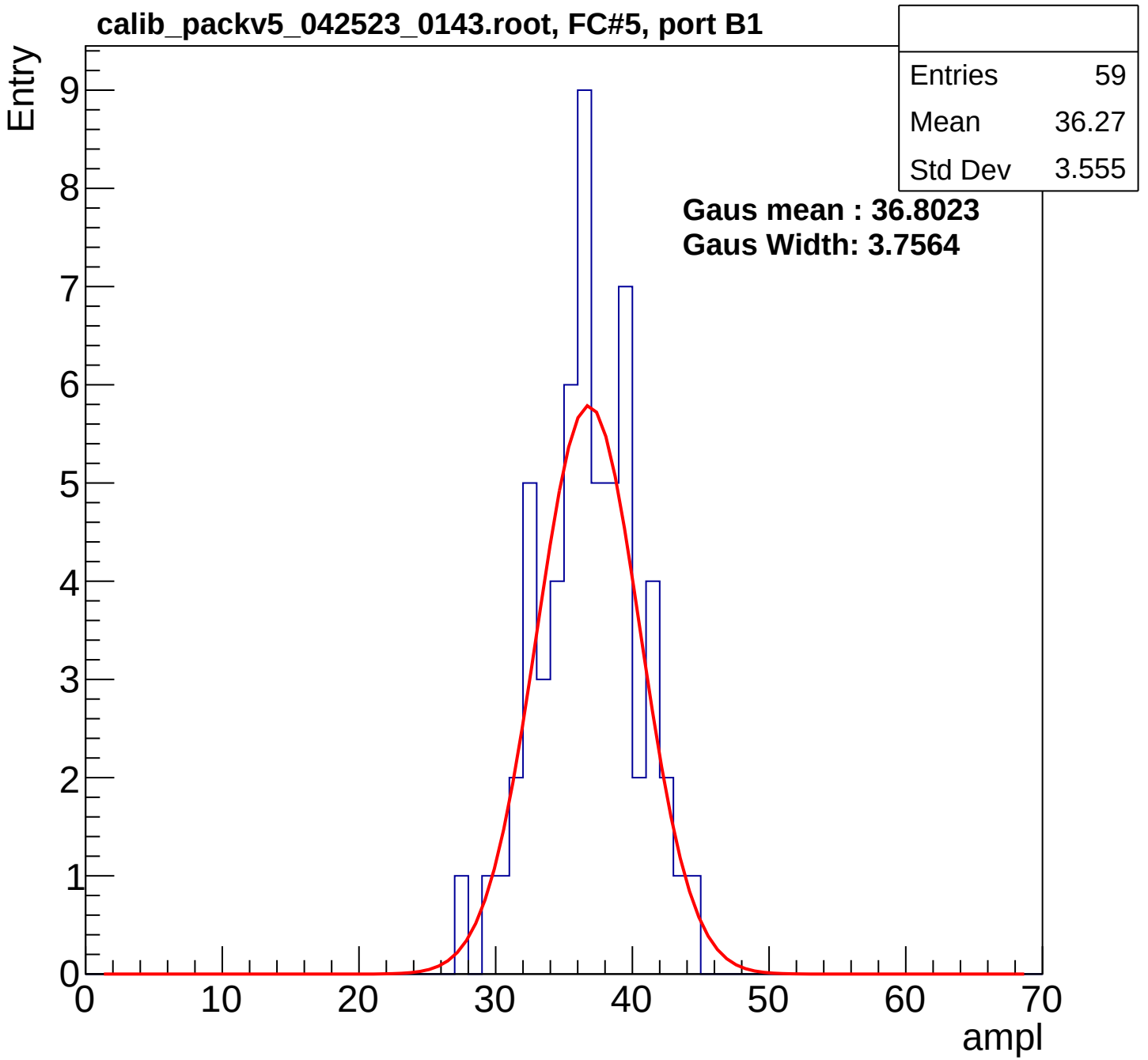

B0L000S, U1-ch124, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.65
Std Dev 3.531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

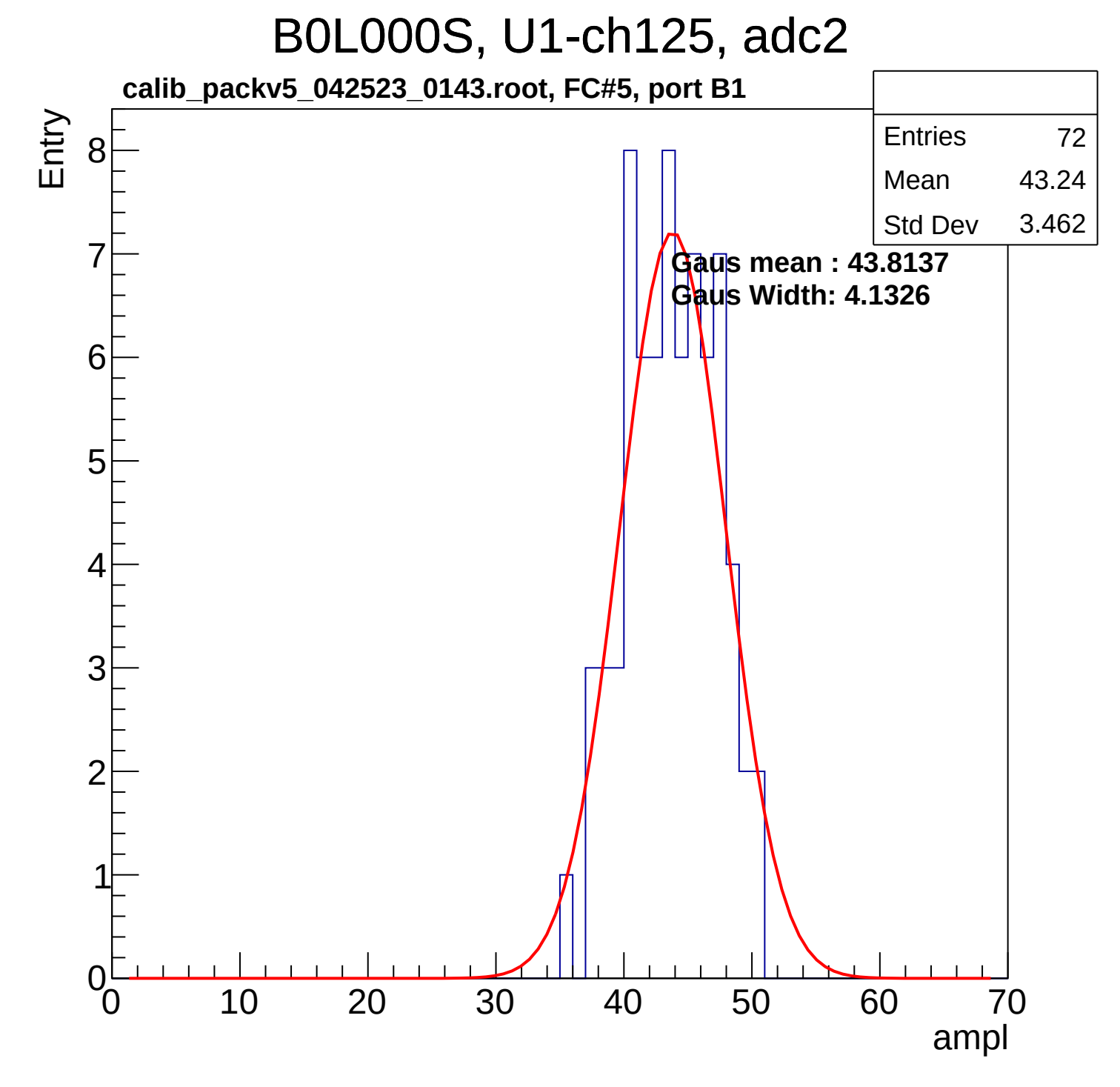

B0L000S, U1-ch124, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.82
Std Dev 8.706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

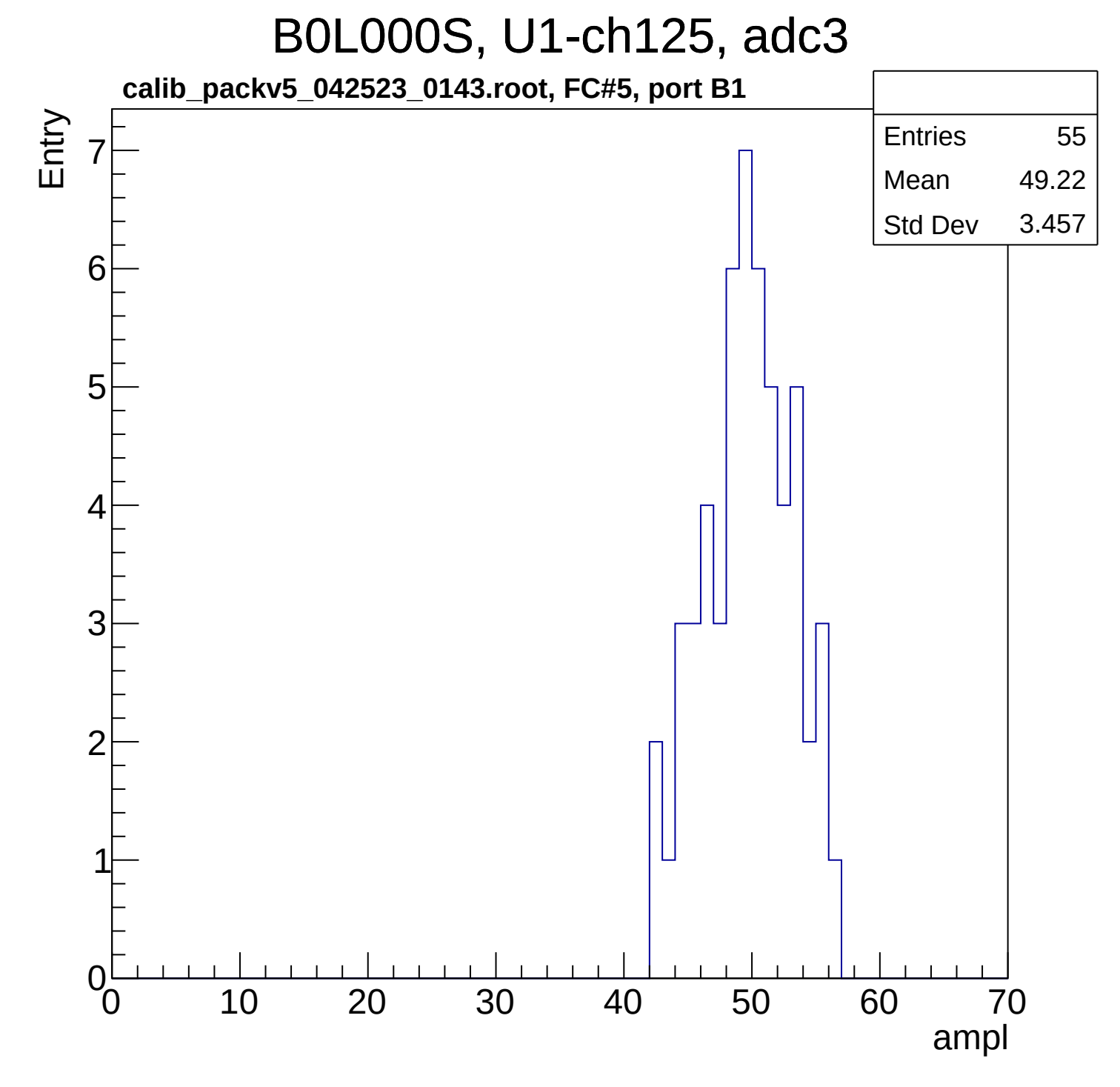

B0L000S, U1-ch124, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

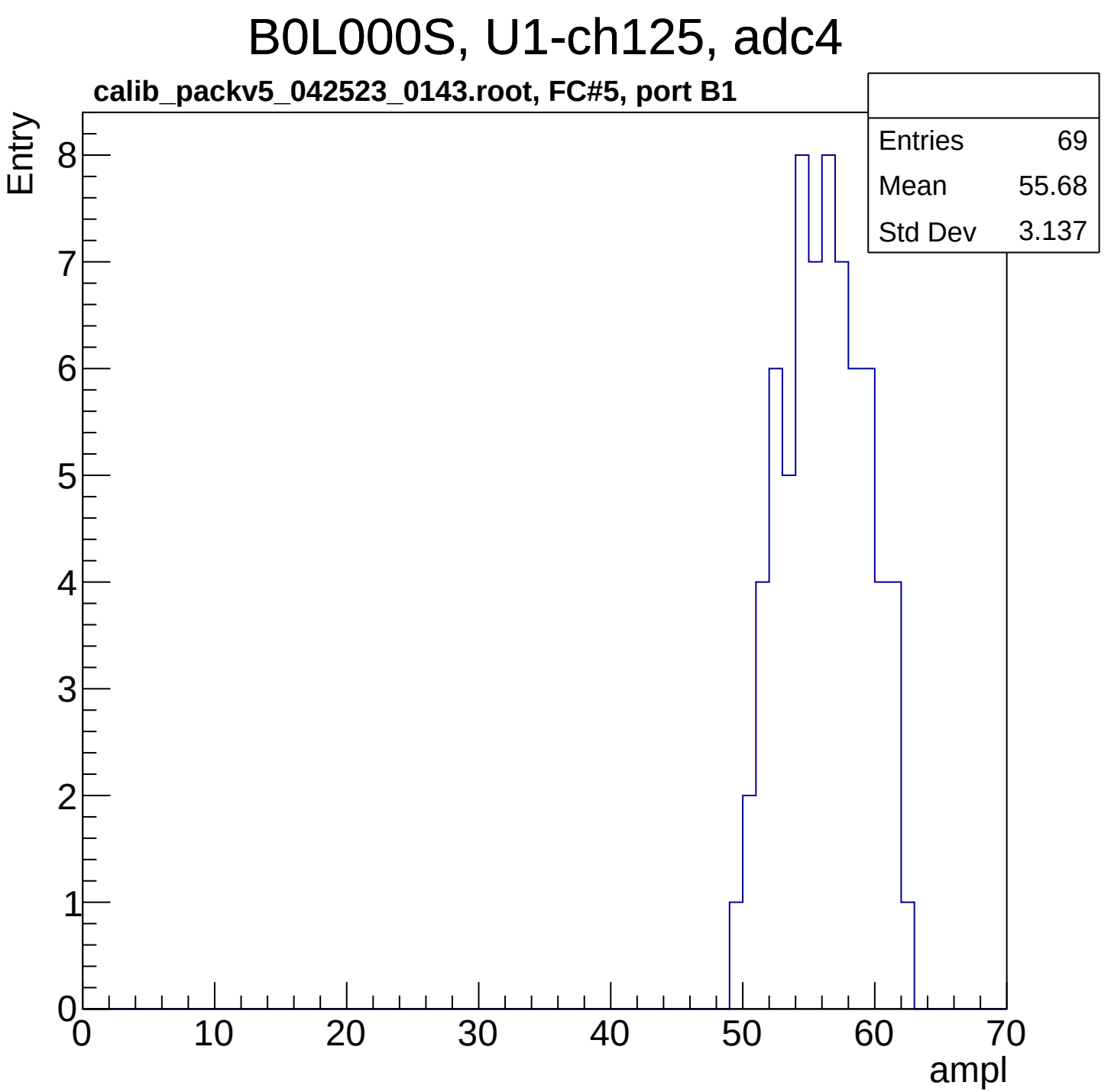

B0L000S, U1-ch124, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch125, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.46
Std Dev 6.182
Gaus mean : 30.7832
Gaus Width: 3.7110
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch125, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 36.27
Std Dev 3.555
Gaus mean : 36.8023
Gaus Width: 3.7564
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch125, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 43.24
Std Dev 3.462
Gaus mean : 43.8137
Gaus Width: 4.1326
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch125, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 49.22
Std Dev 3.457
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch125, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 55.68
Std Dev 3.137
Entry
ampl
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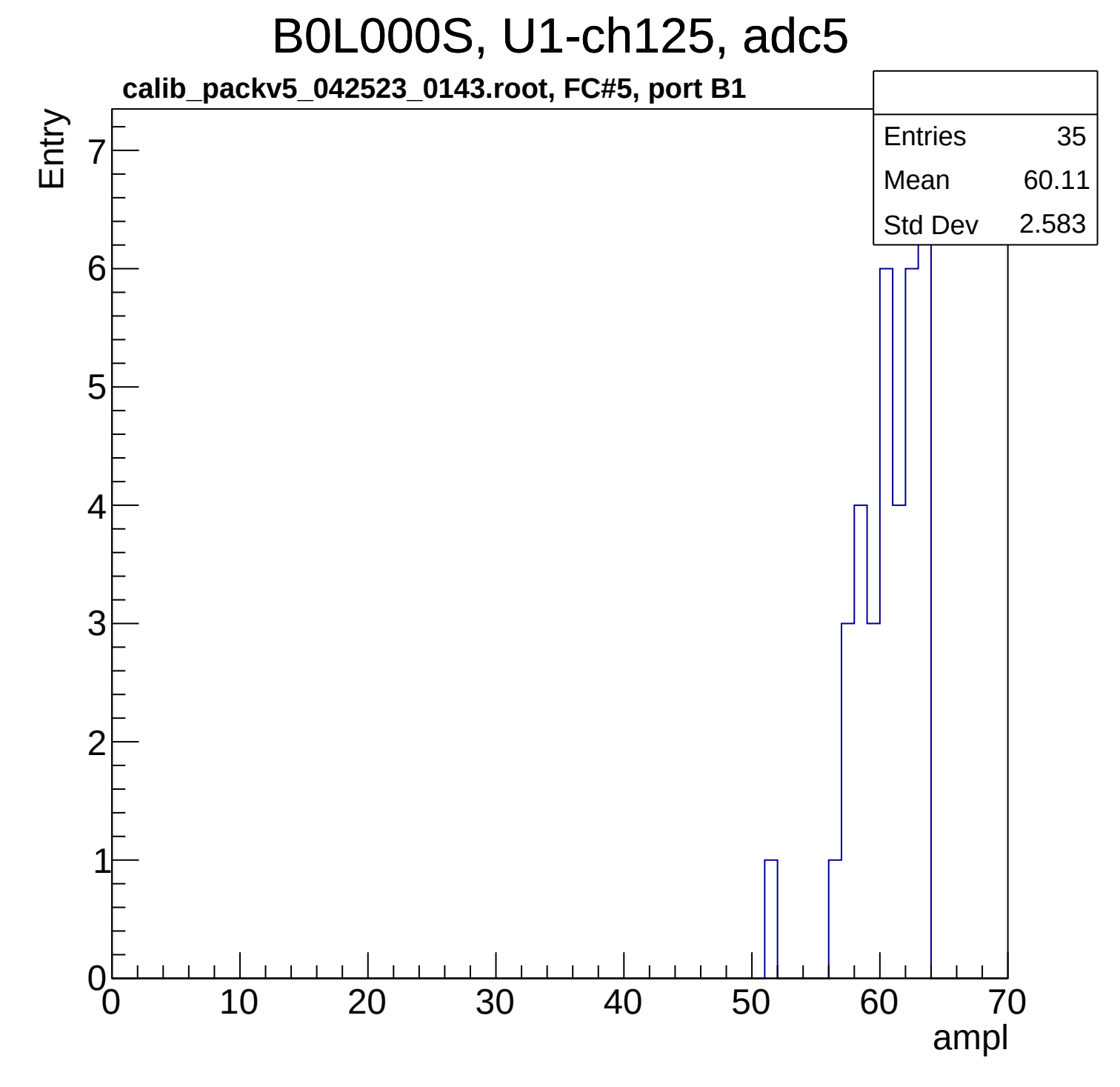
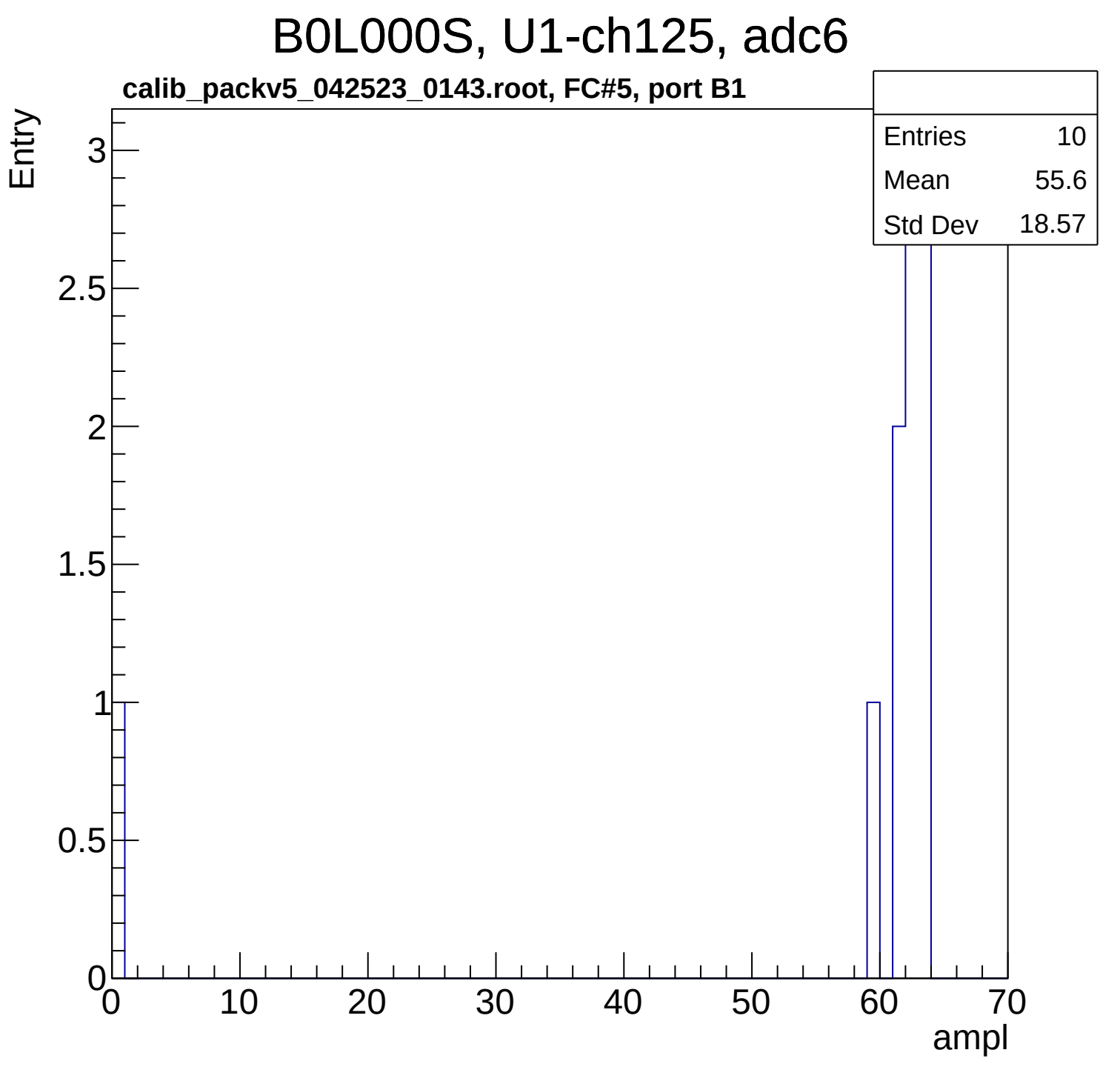
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch125, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 60.11
Std Dev 2.583
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch125, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.6
Std Dev 18.57
Entry
ampl
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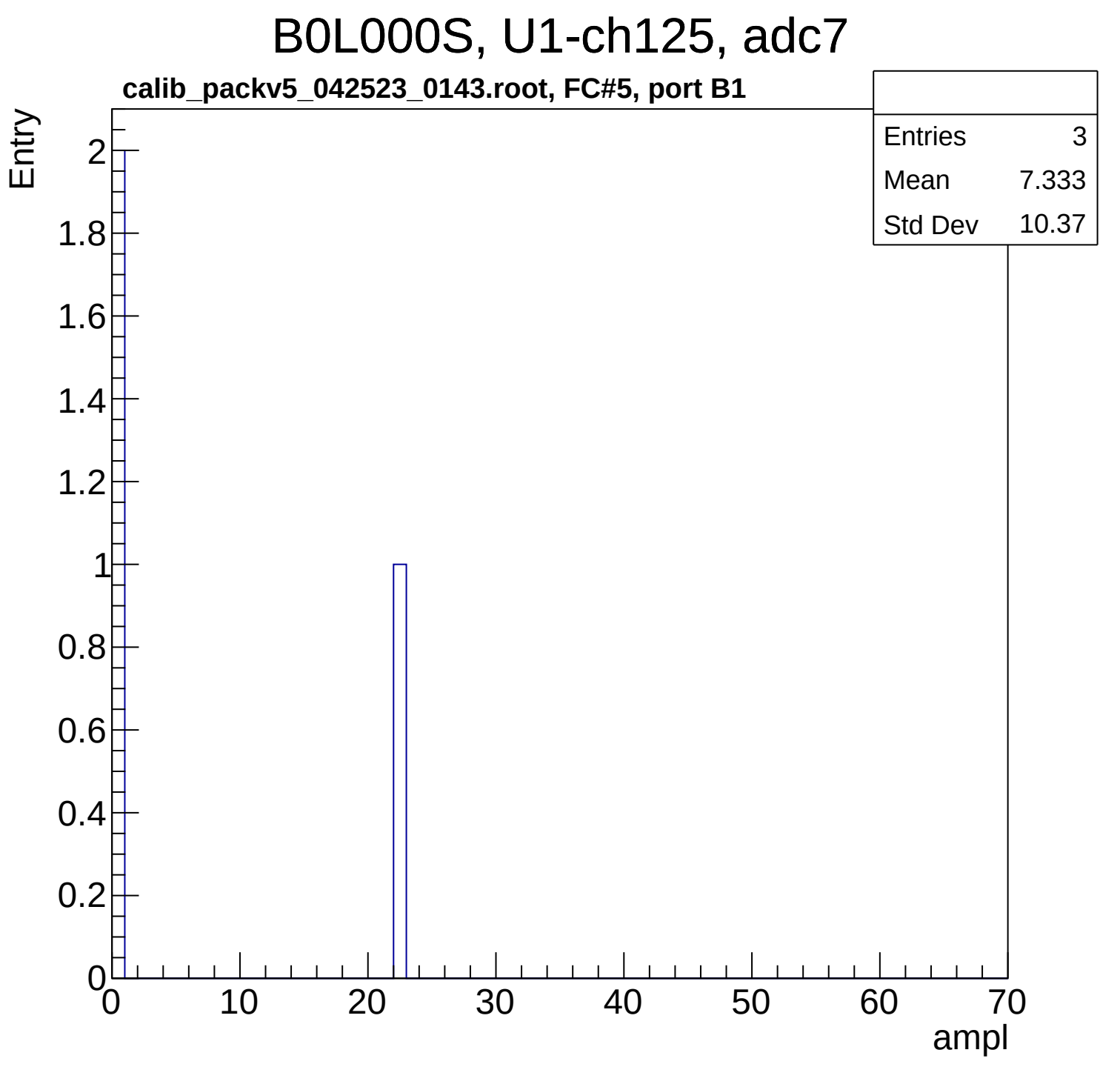
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch125, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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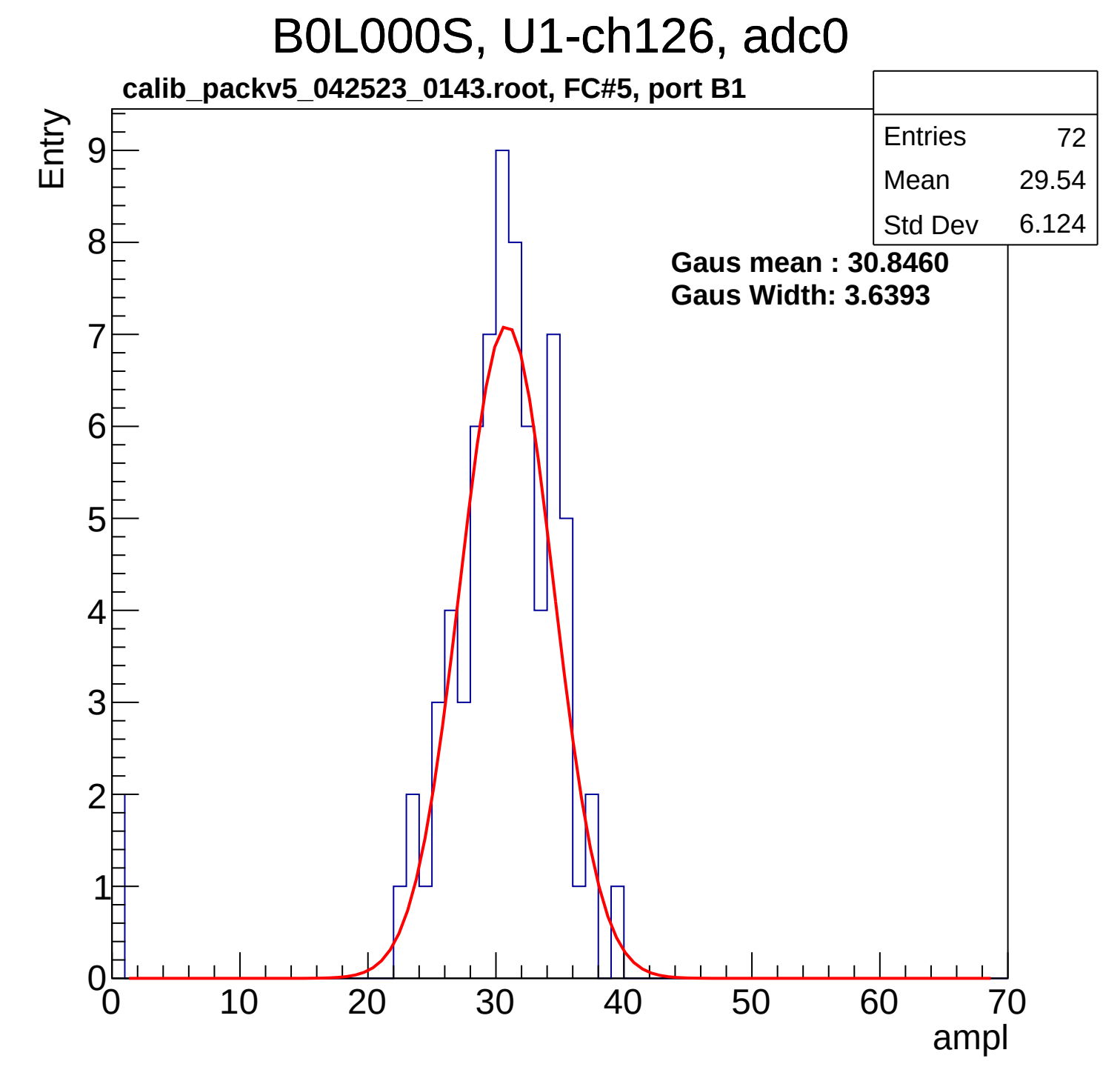
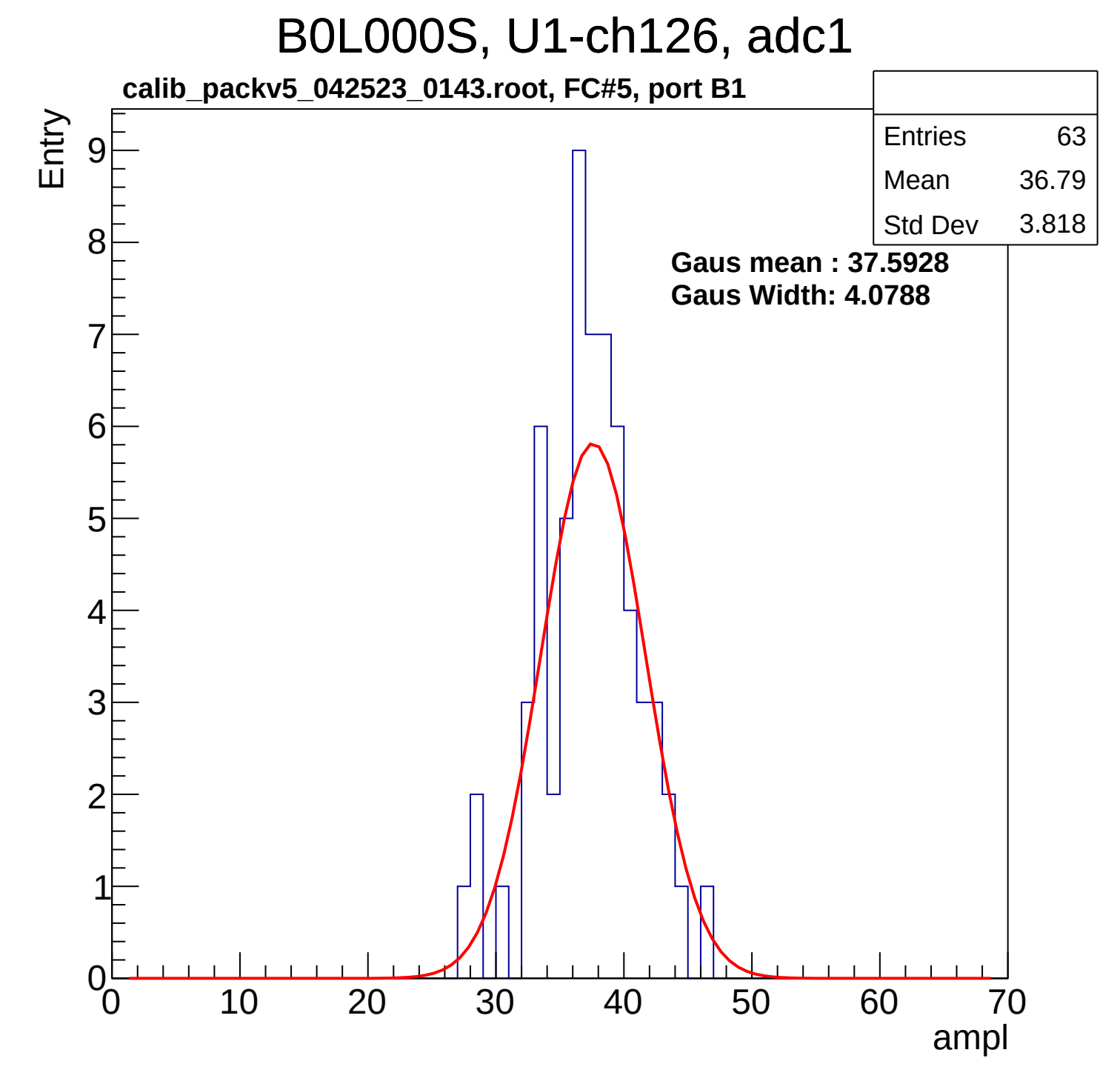
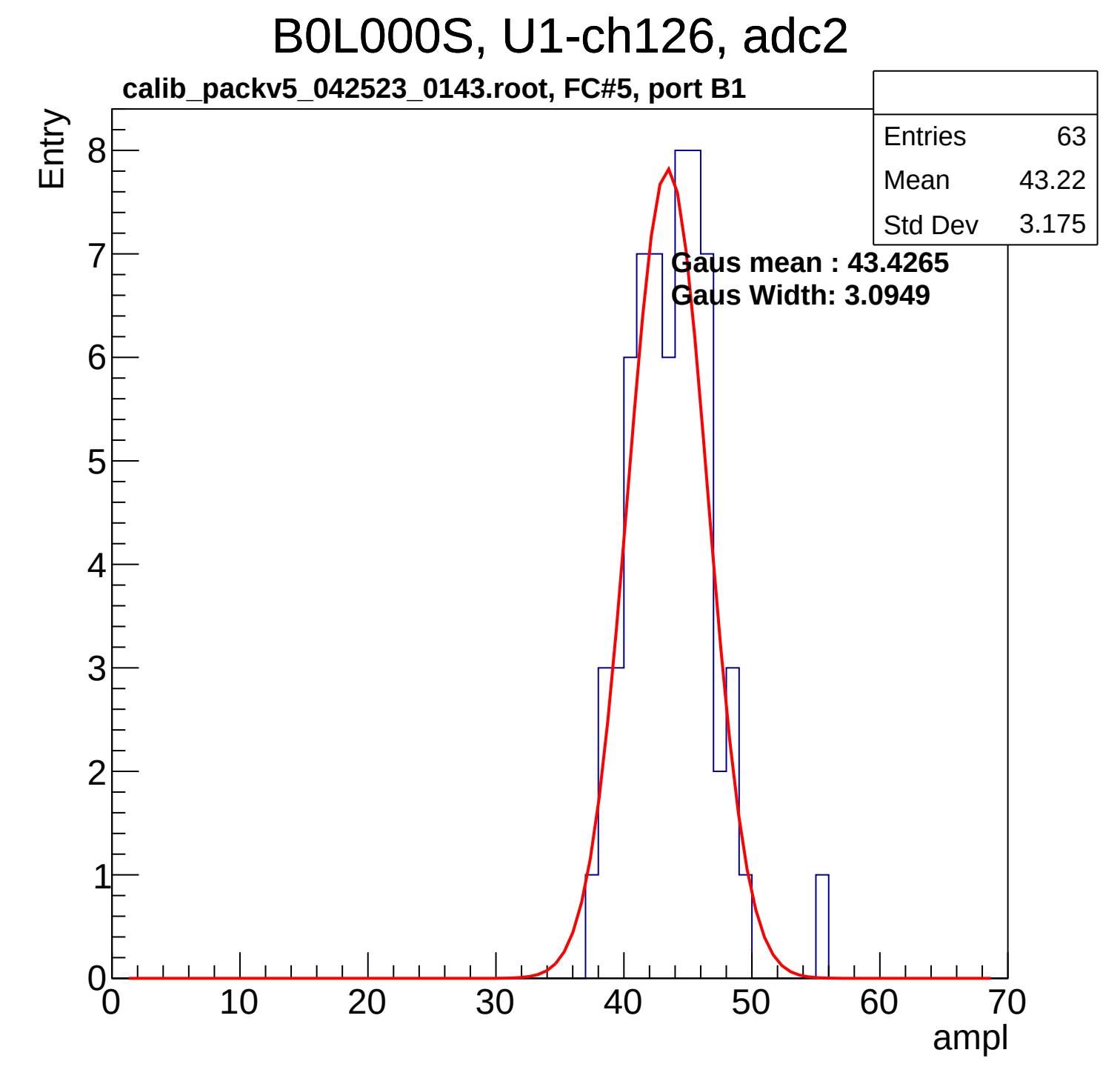
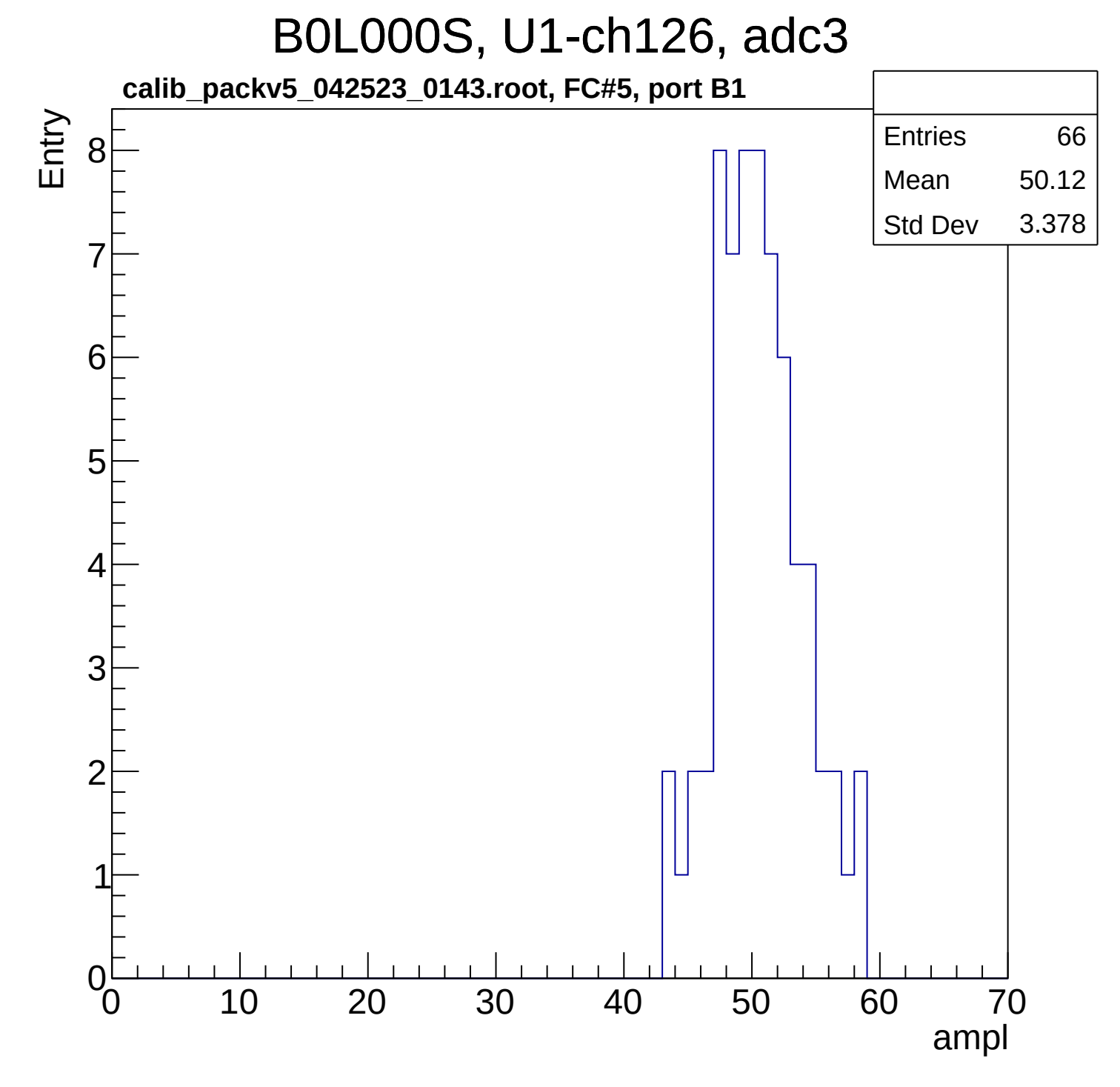
60
70


B0L000S, U1-ch126, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.54
Std Dev 6.124
Gaus mean : 30.8460
Gaus Width: 3.6393
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch126, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.79
Std Dev 3.818
Gaus mean : 37.5928
Gaus Width: 4.0788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch126, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.22
Std Dev 3.175
Gaus mean : 43.4265
Gaus Width: 3.0949
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch126, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.12
Std Dev 3.378
Entry
ampl
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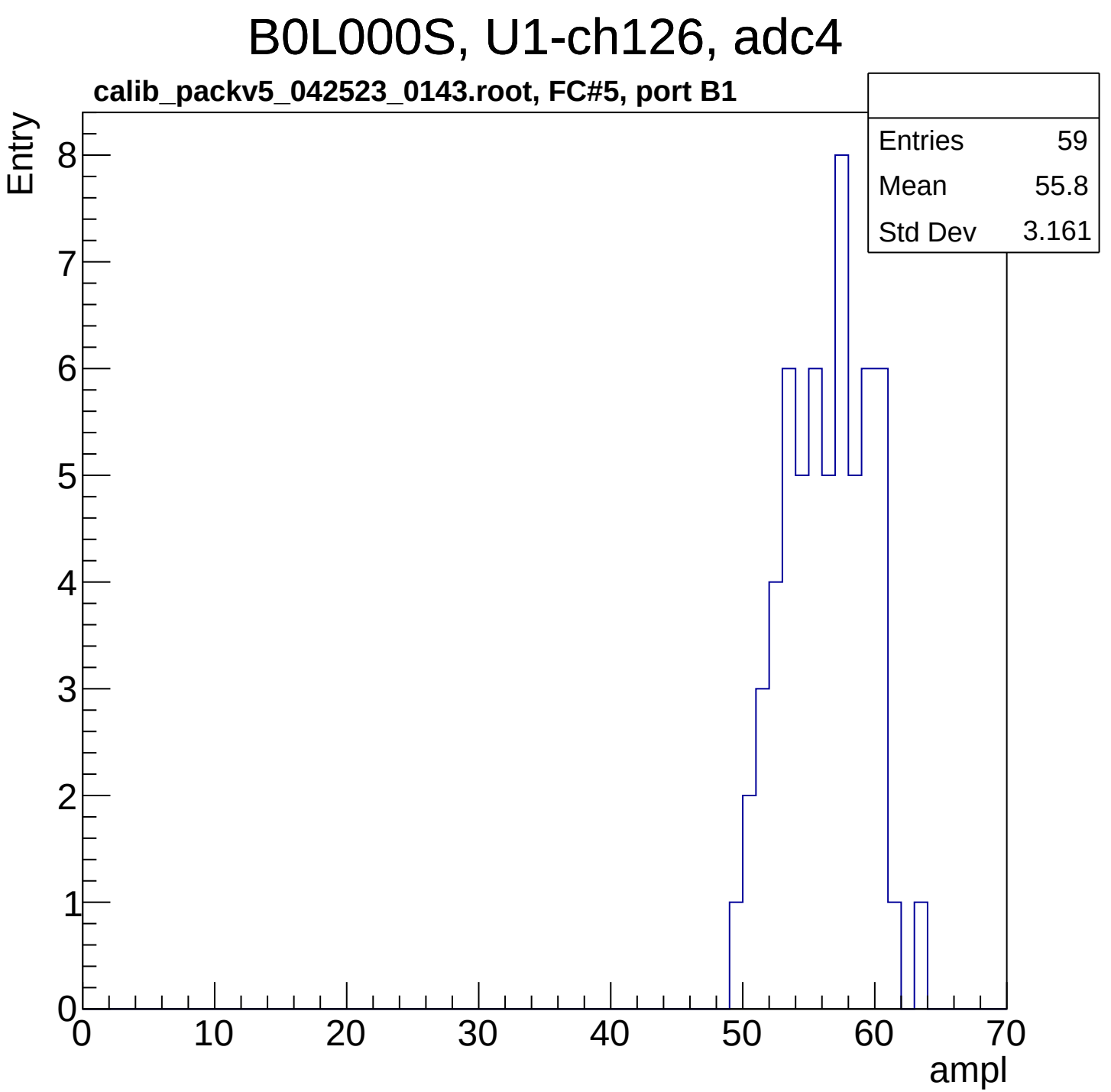
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch126, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.8
Std Dev 3.161
Entry
0
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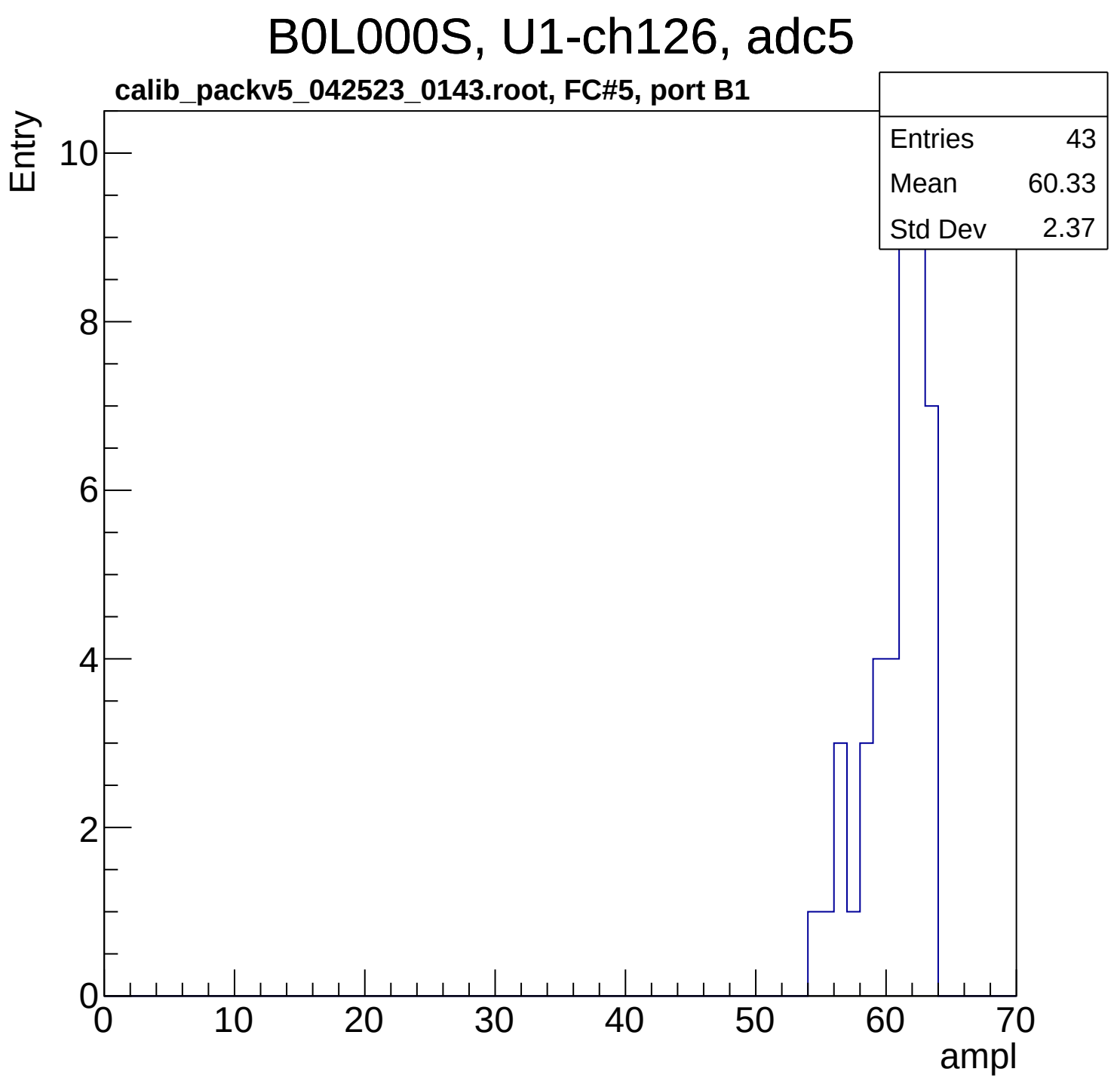
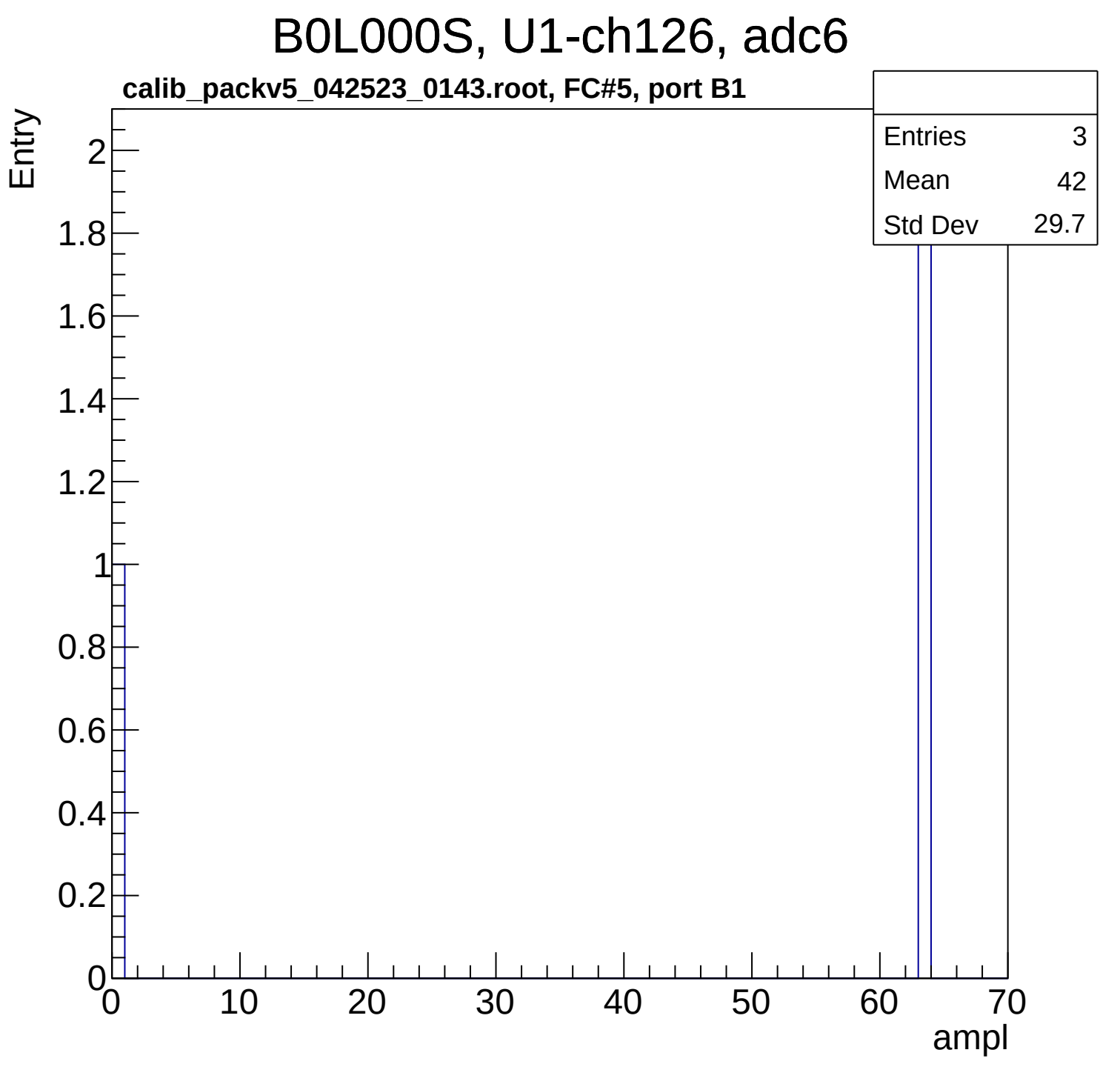
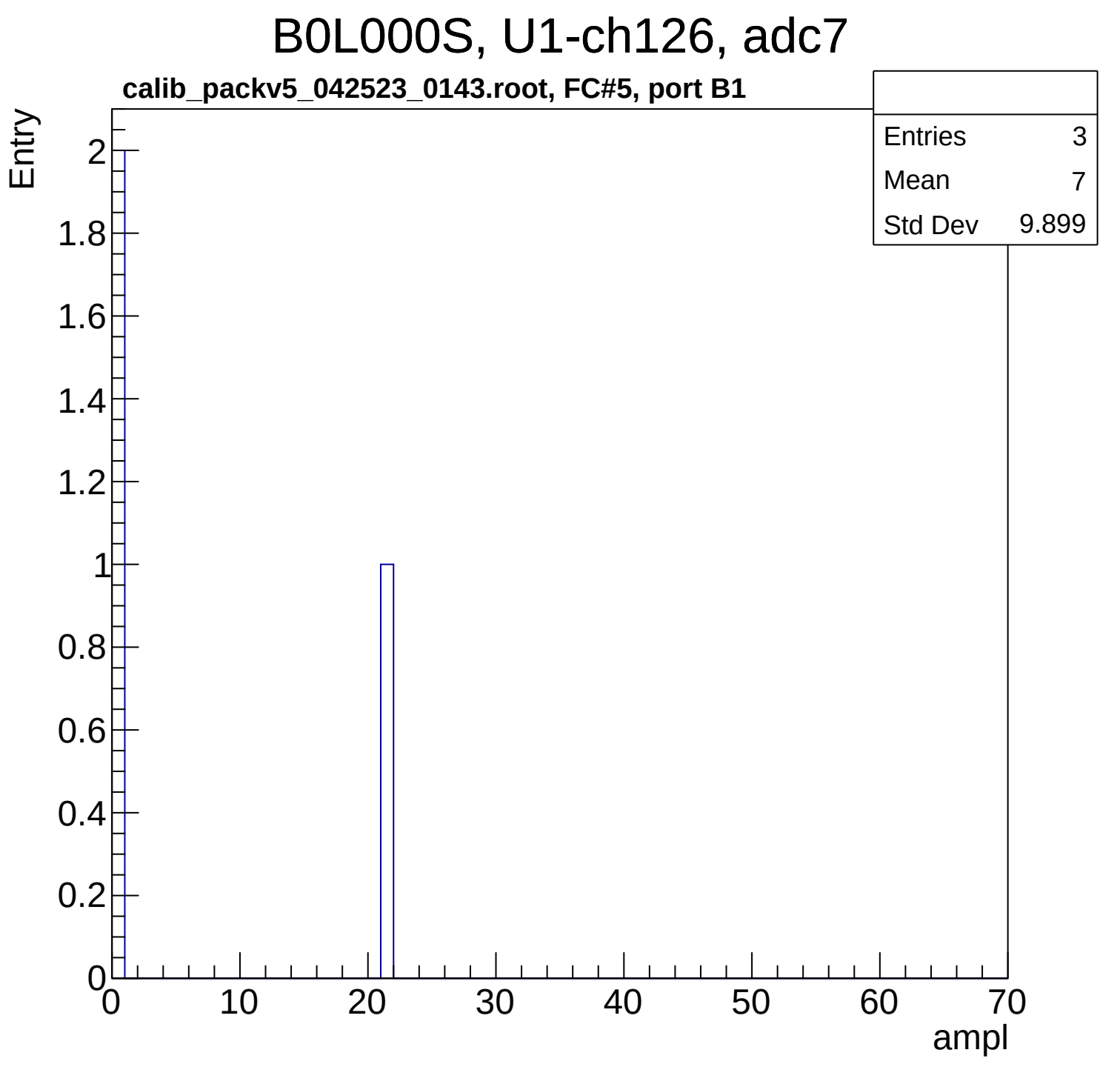
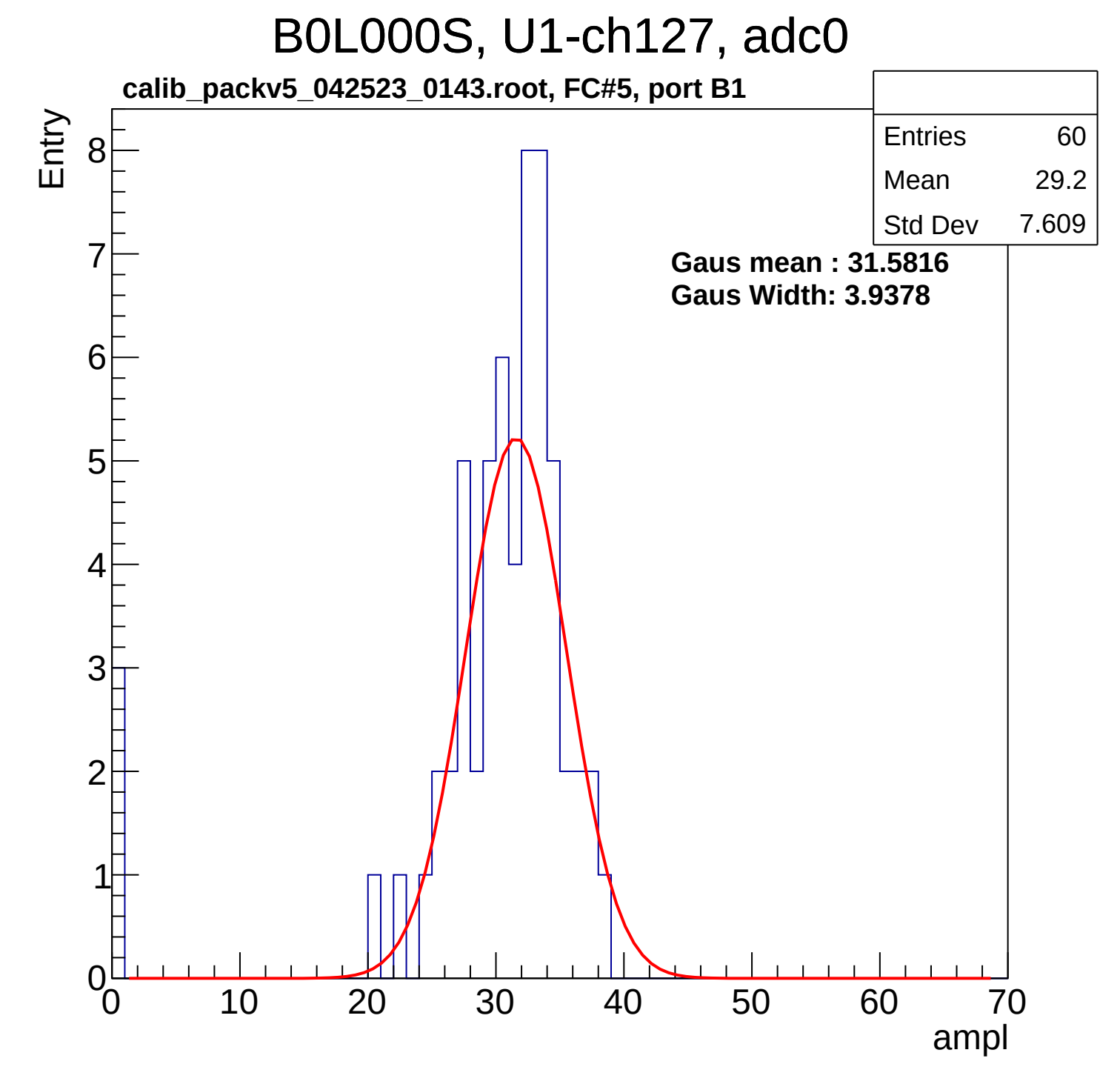
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch126, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.33
Std Dev 2.37
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch126, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch126, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U1-ch127, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.2
Std Dev 7.609
Gaus mean : 31.5816
Gaus Width: 3.9378
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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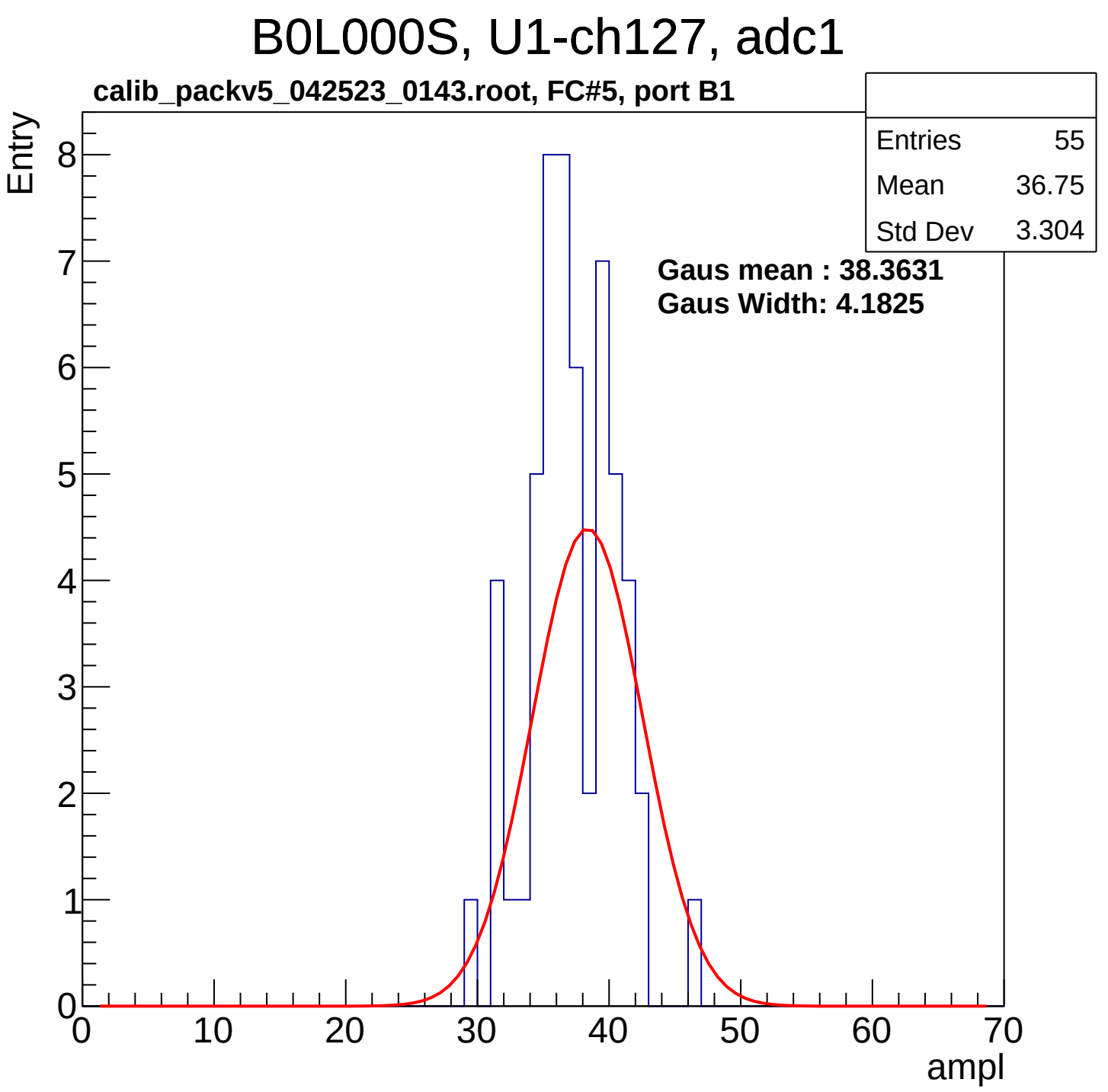
B0L000S, U1-ch127, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 36.75
Std Dev 3.304
Gaus mean : 38.3631
Gaus Width: 4.1825
Entry
ampl
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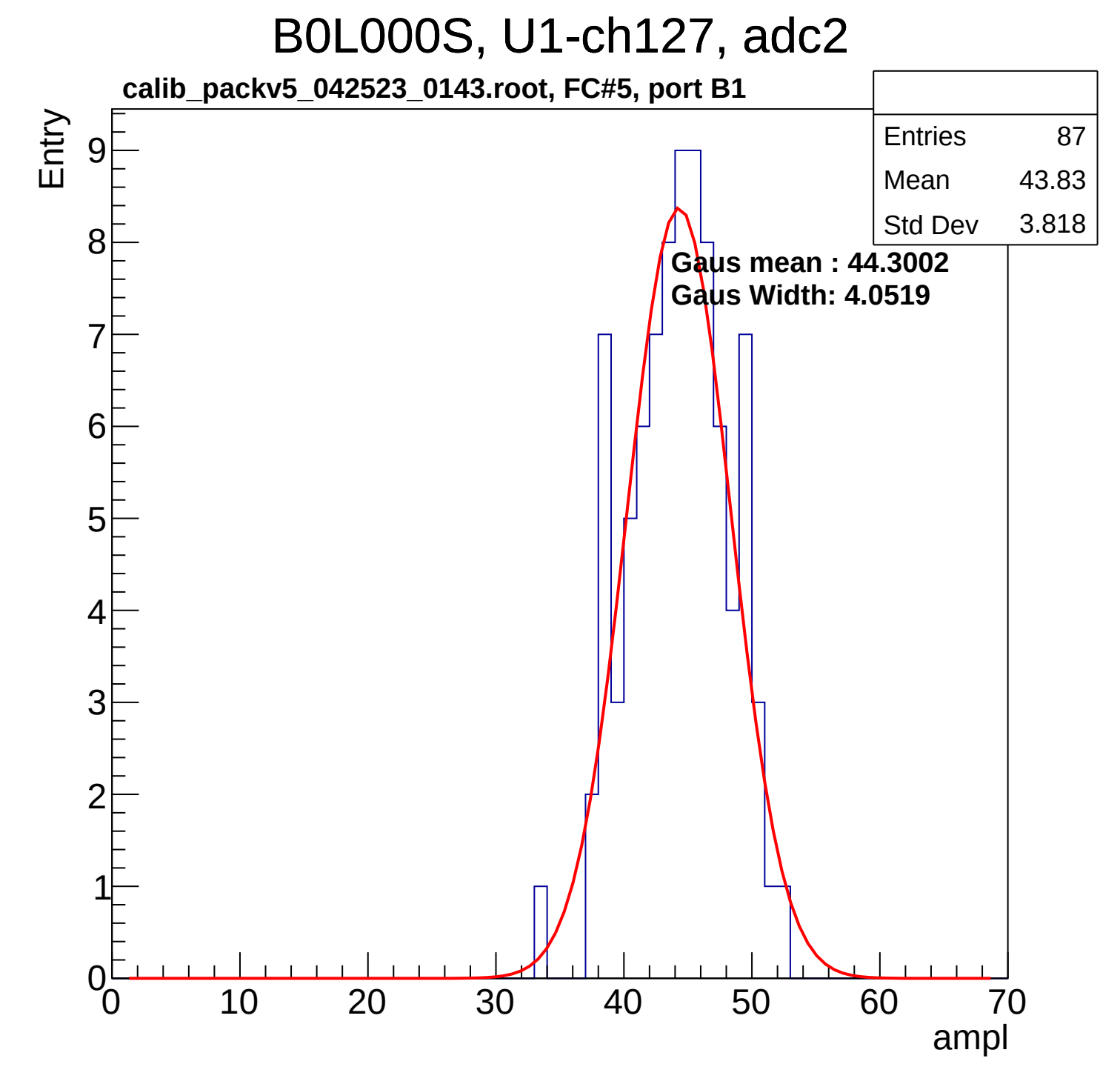
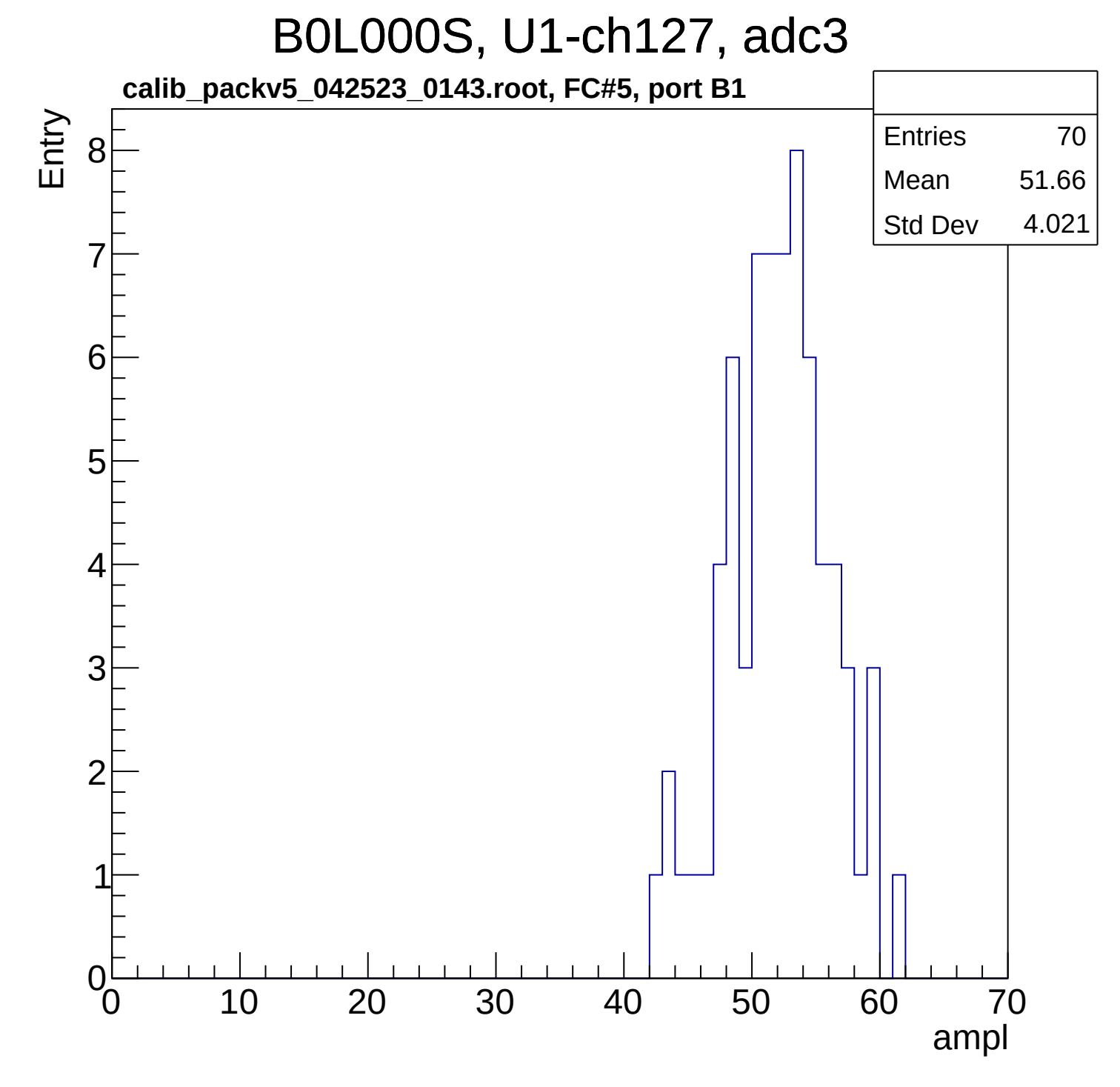
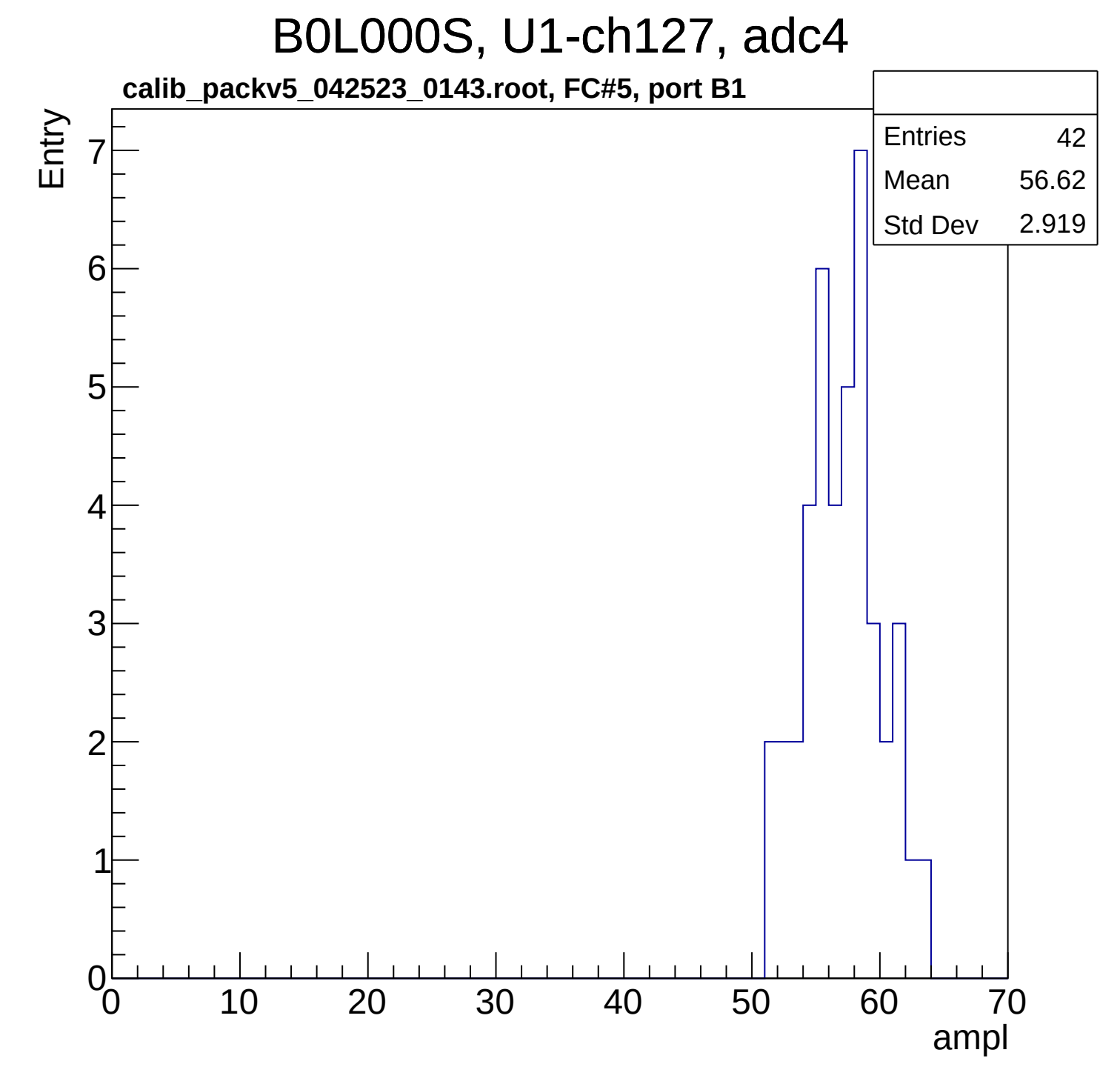
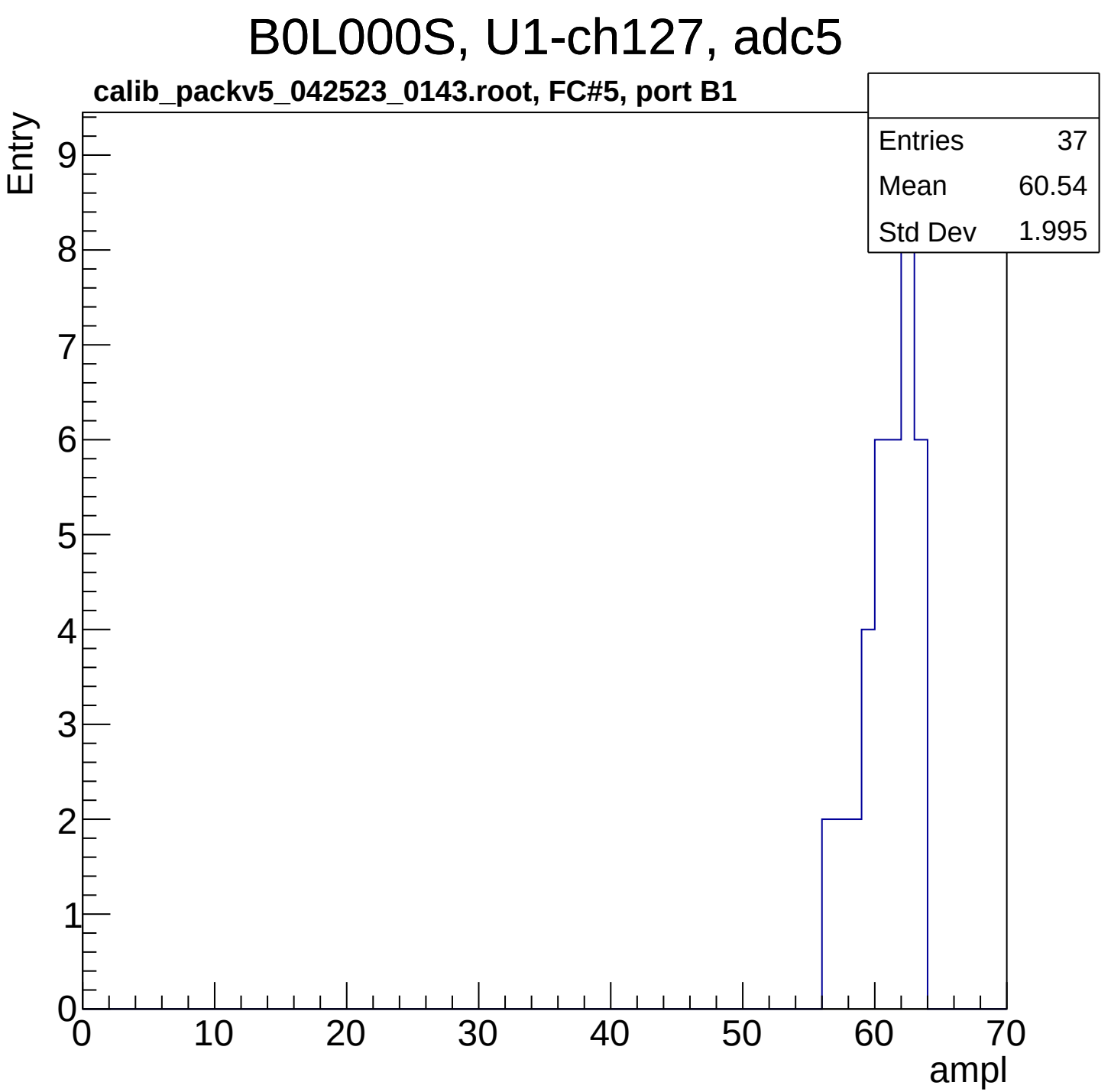
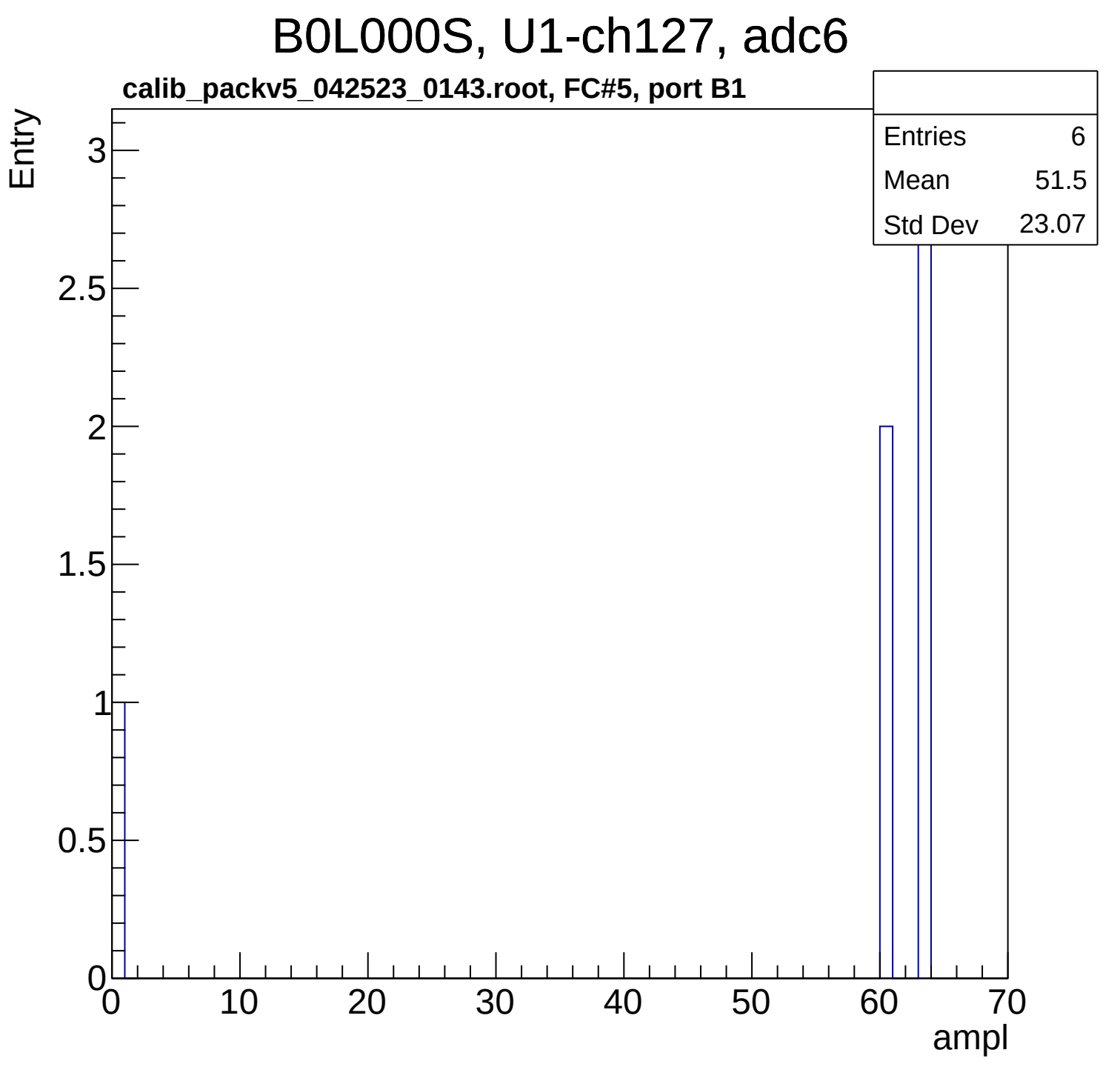
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

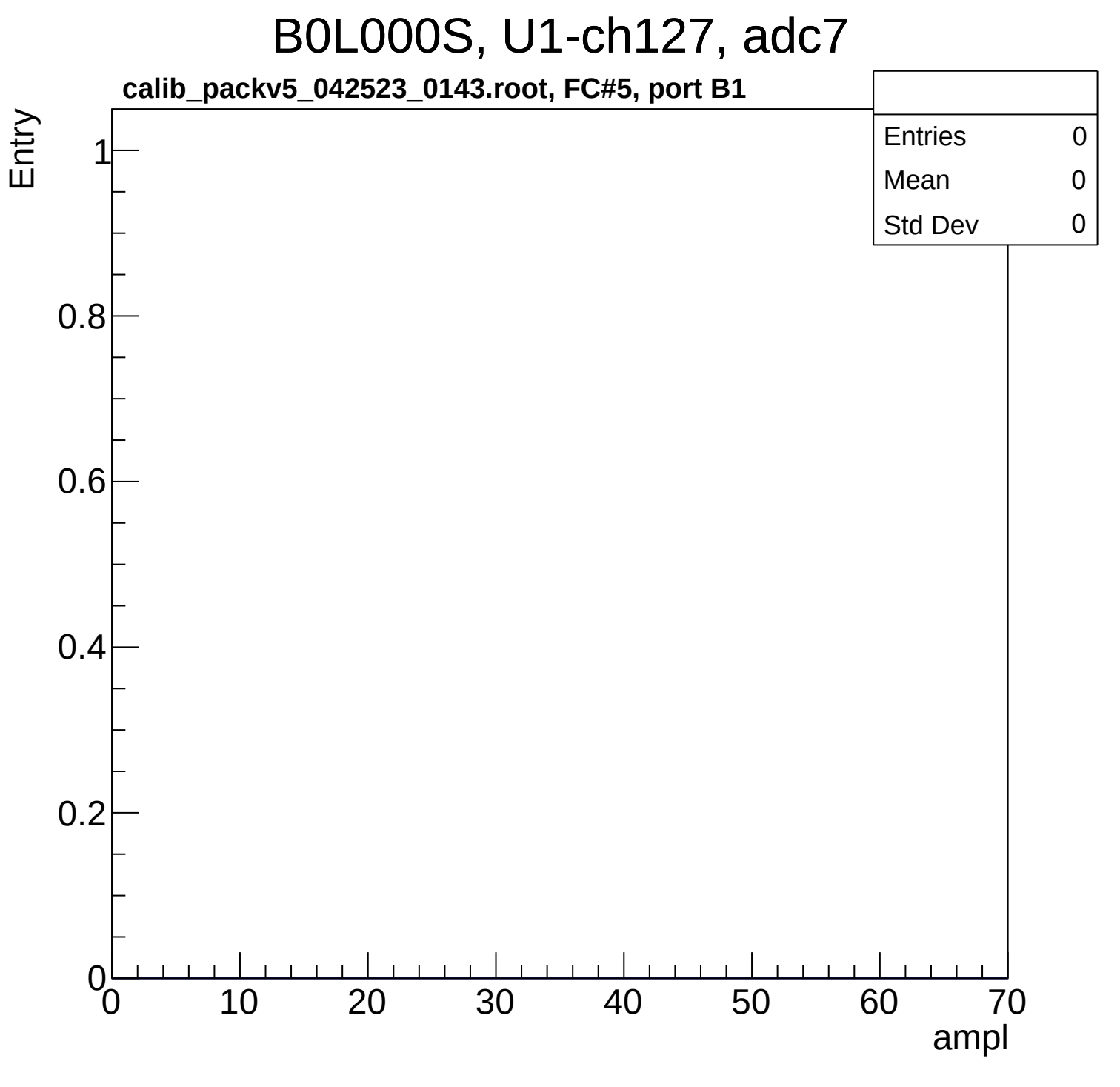

B0L000S, U1-ch127, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 43.83
Std Dev 3.818
Gaus mean : 44.3002
Gaus Width: 4.0519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

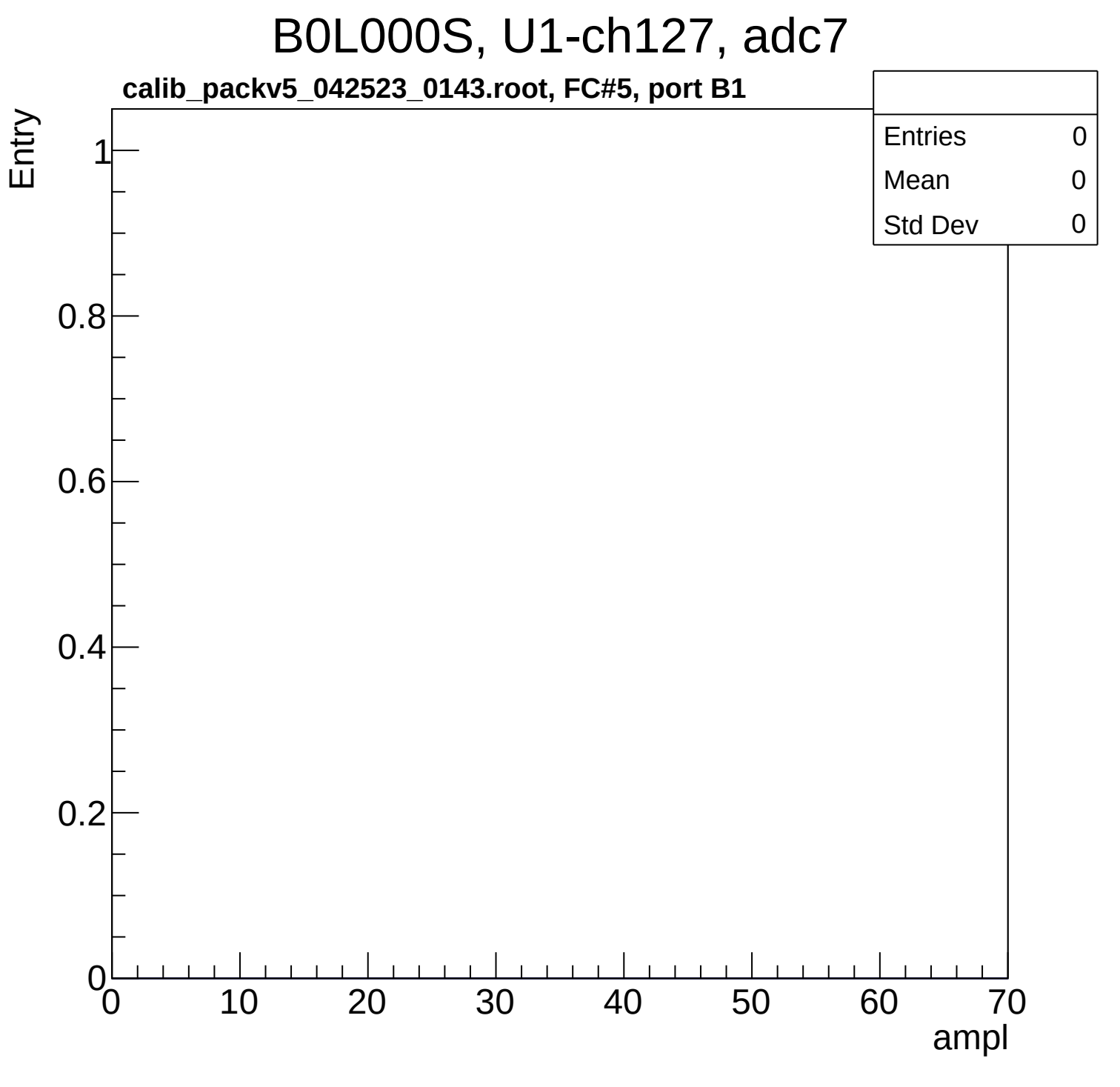

B0L000S, U1-ch127, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 51.66
Std Dev 4.021
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U1-ch127, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 56.62
Std Dev 2.919
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U1-ch127, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.54
Std Dev 1.995
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U1-ch127, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 51.5
Std Dev 23.07
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl