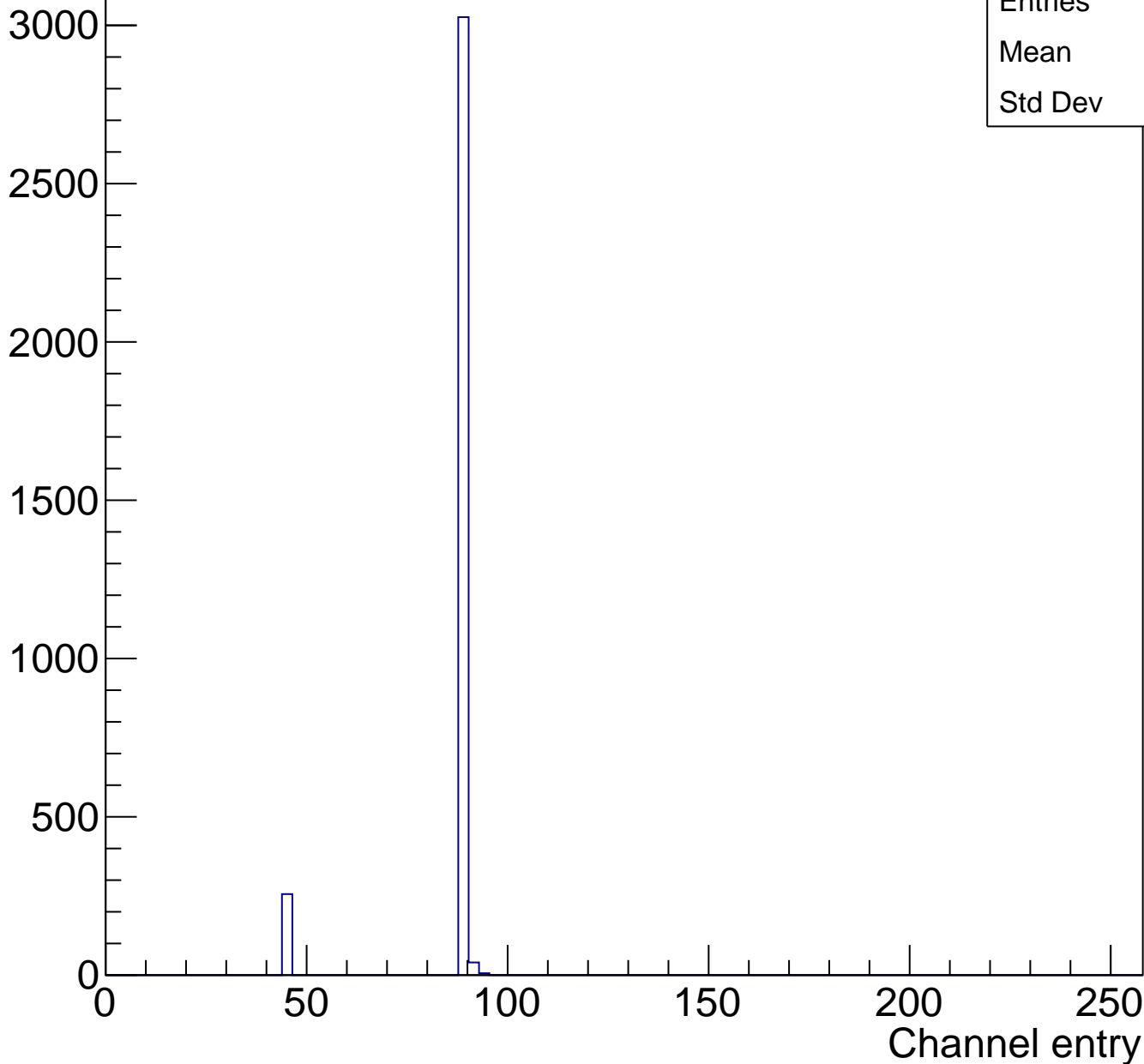


B1L103S, channel entry distribution (ampl > 54)

calib_packv5_041523_1651.root, FC#0, port C2

Events



Entries	3328
Mean	86.56
Std Dev	12