

B1L002S, U24-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.89
Std Dev	11.51

Turn on : 25.3616

Width : 2.196

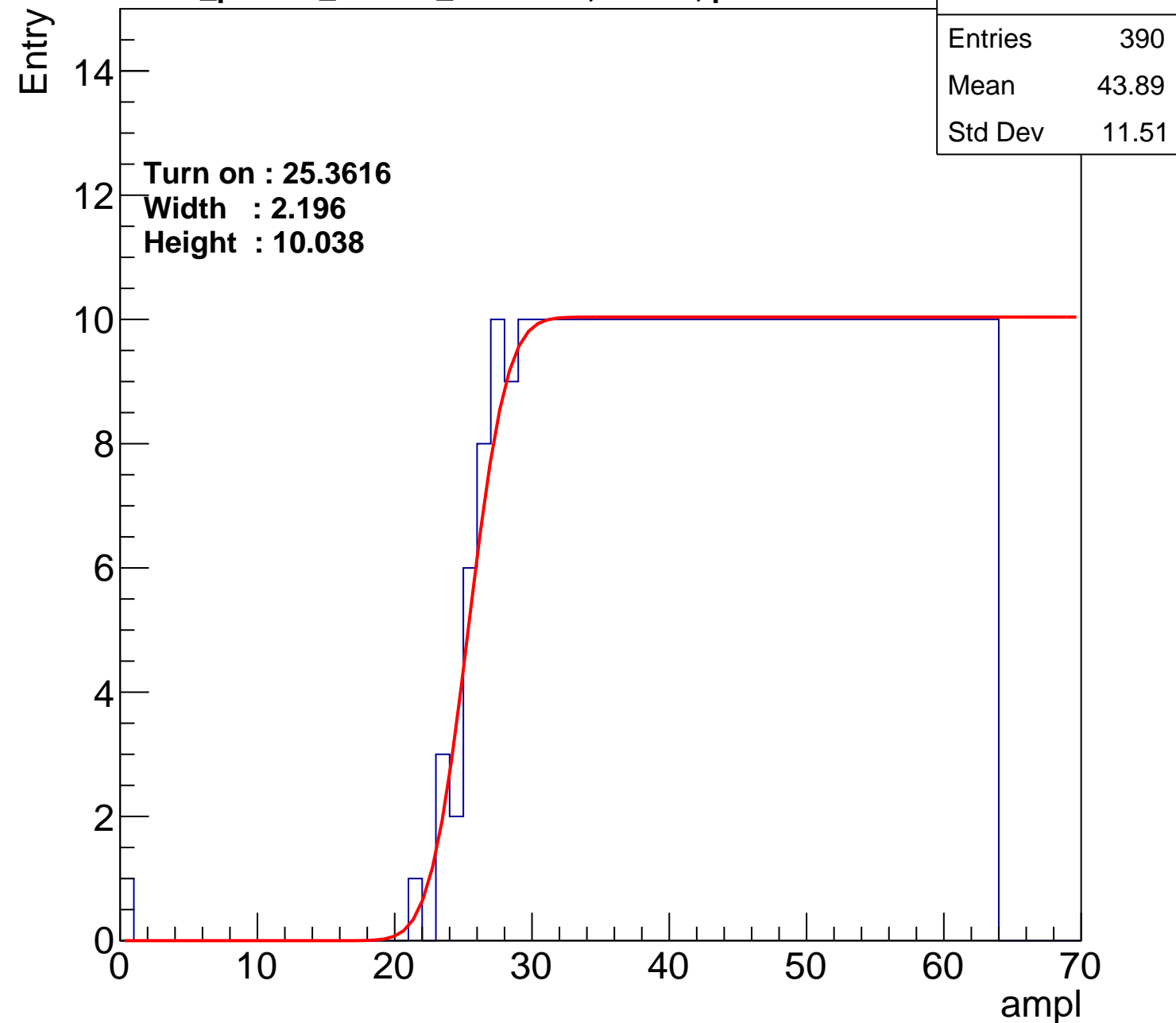
Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U24-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 25.7058

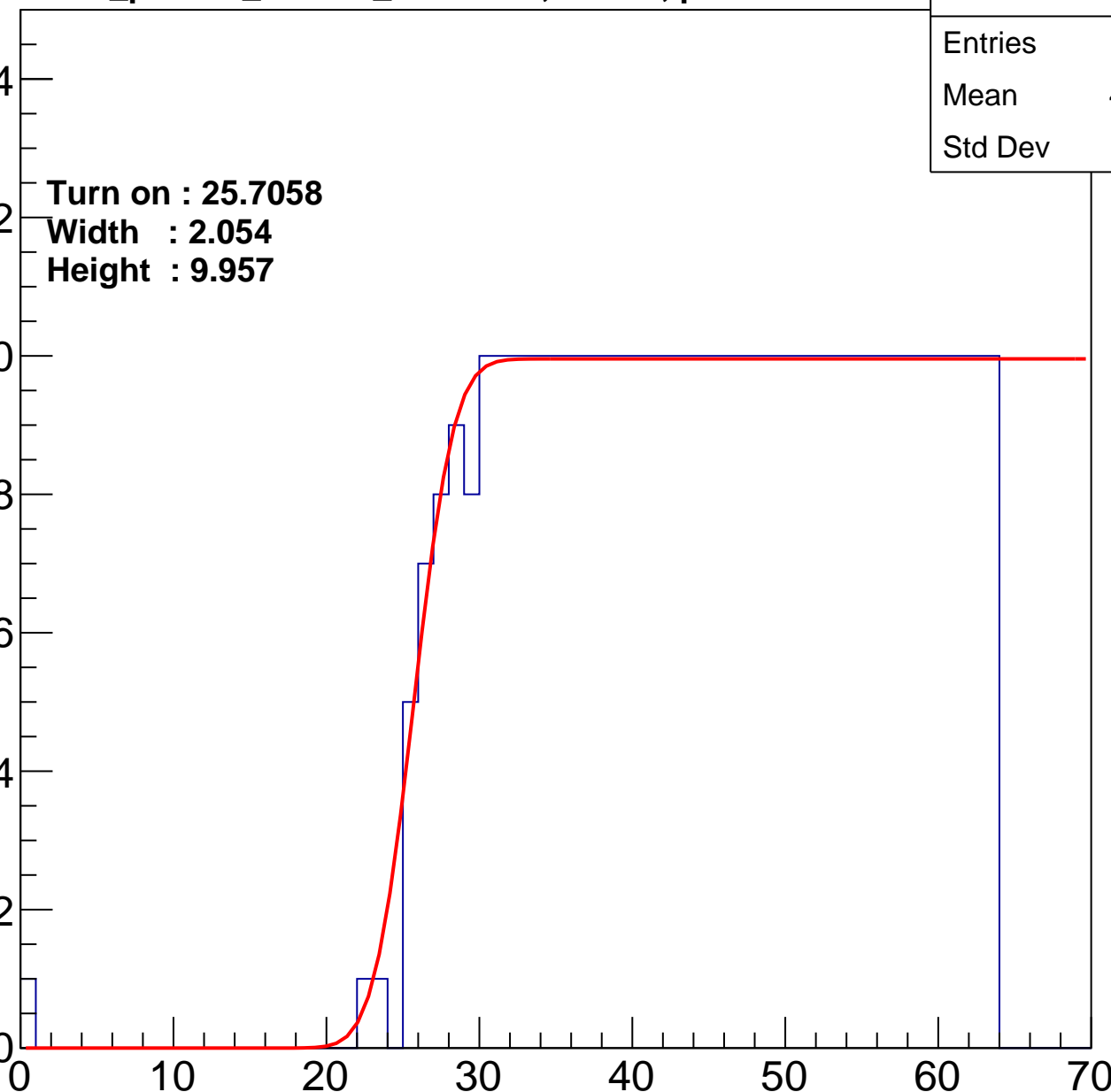
Width : 2.054

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.1953

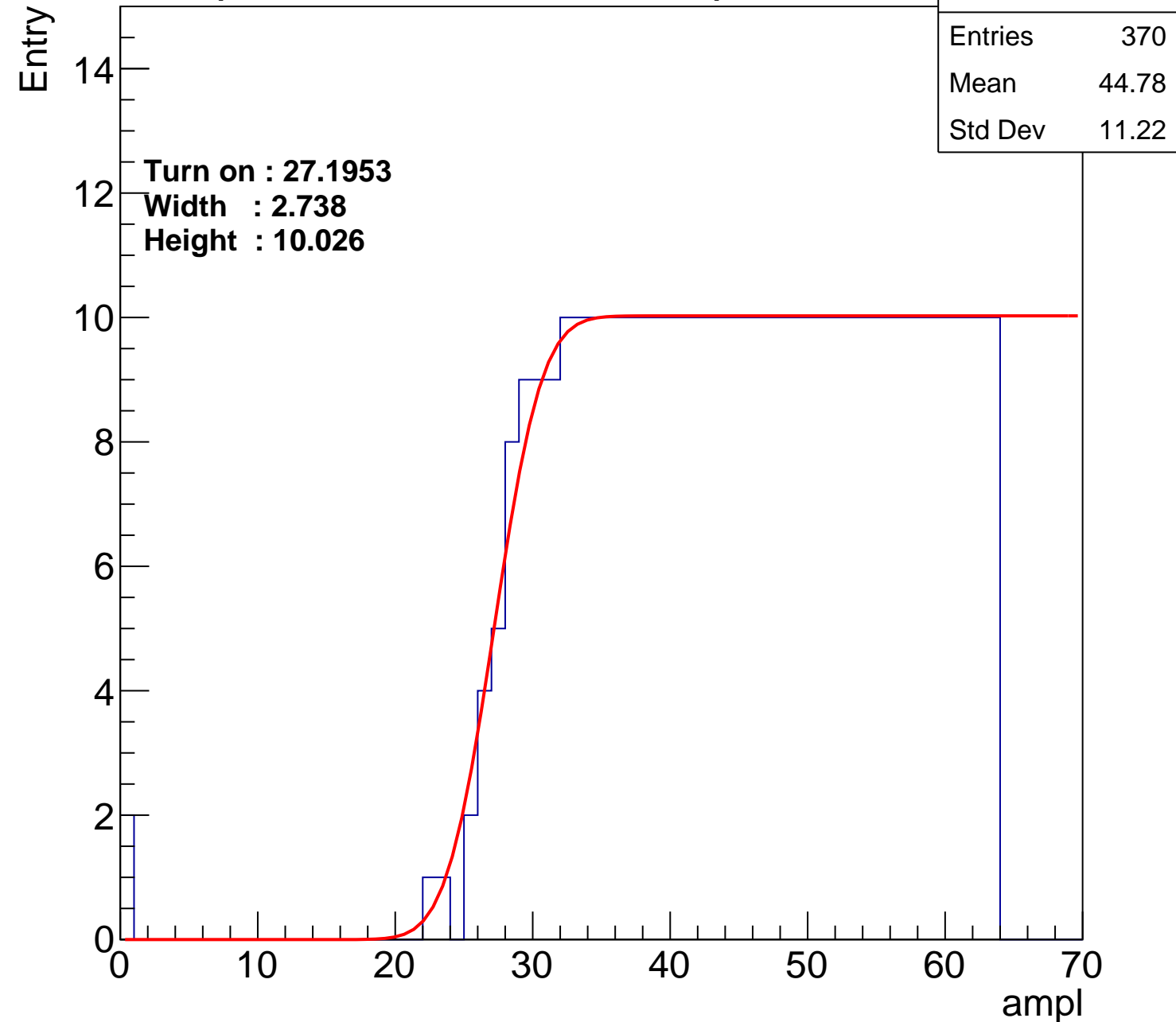
Width : 2.738

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.36
Std Dev	12.06

Turn on : 24.6918

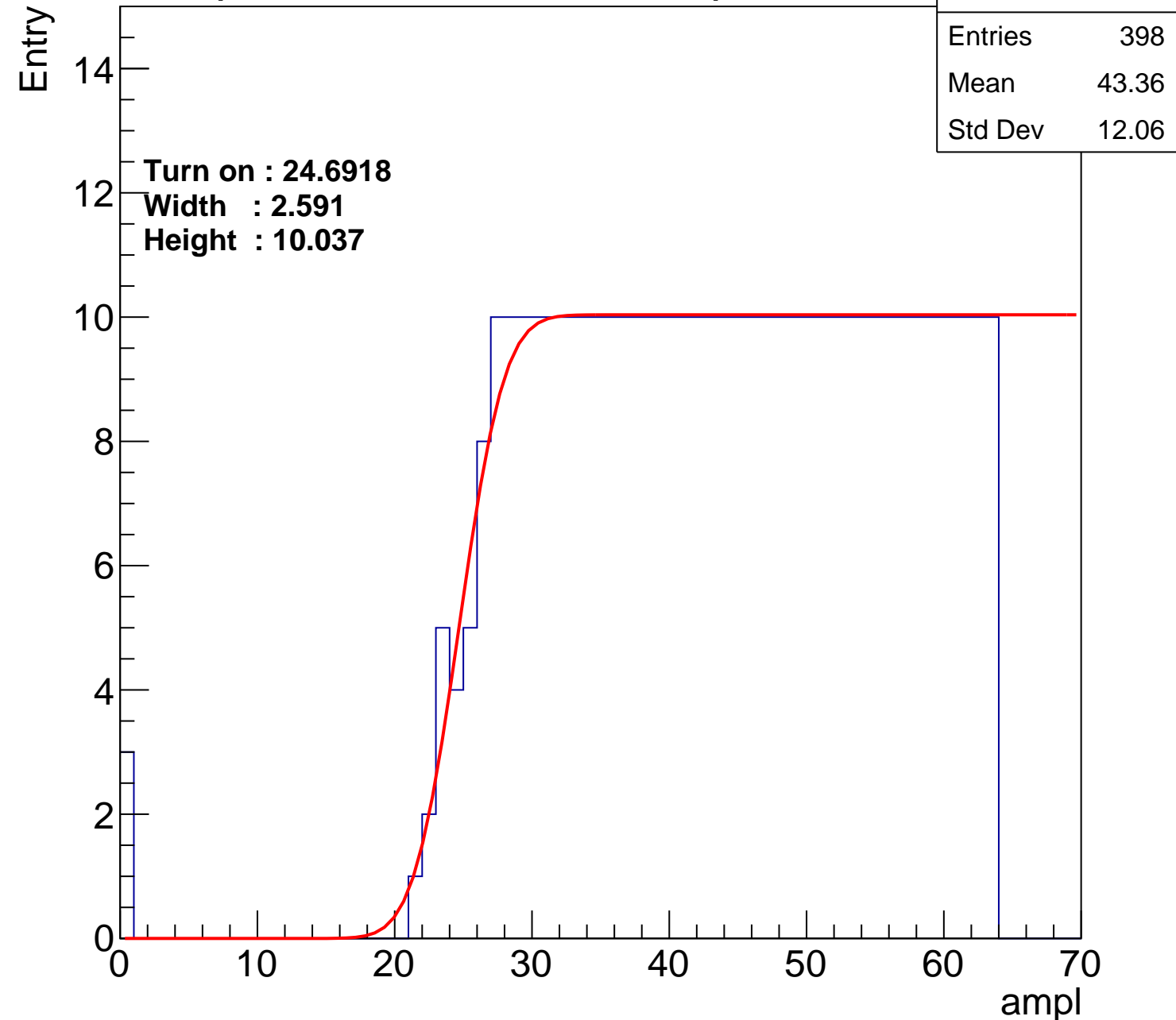
Width : 2.591

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.62
Std Dev	11.7

Turn on : 25.2004

Width : 2.933

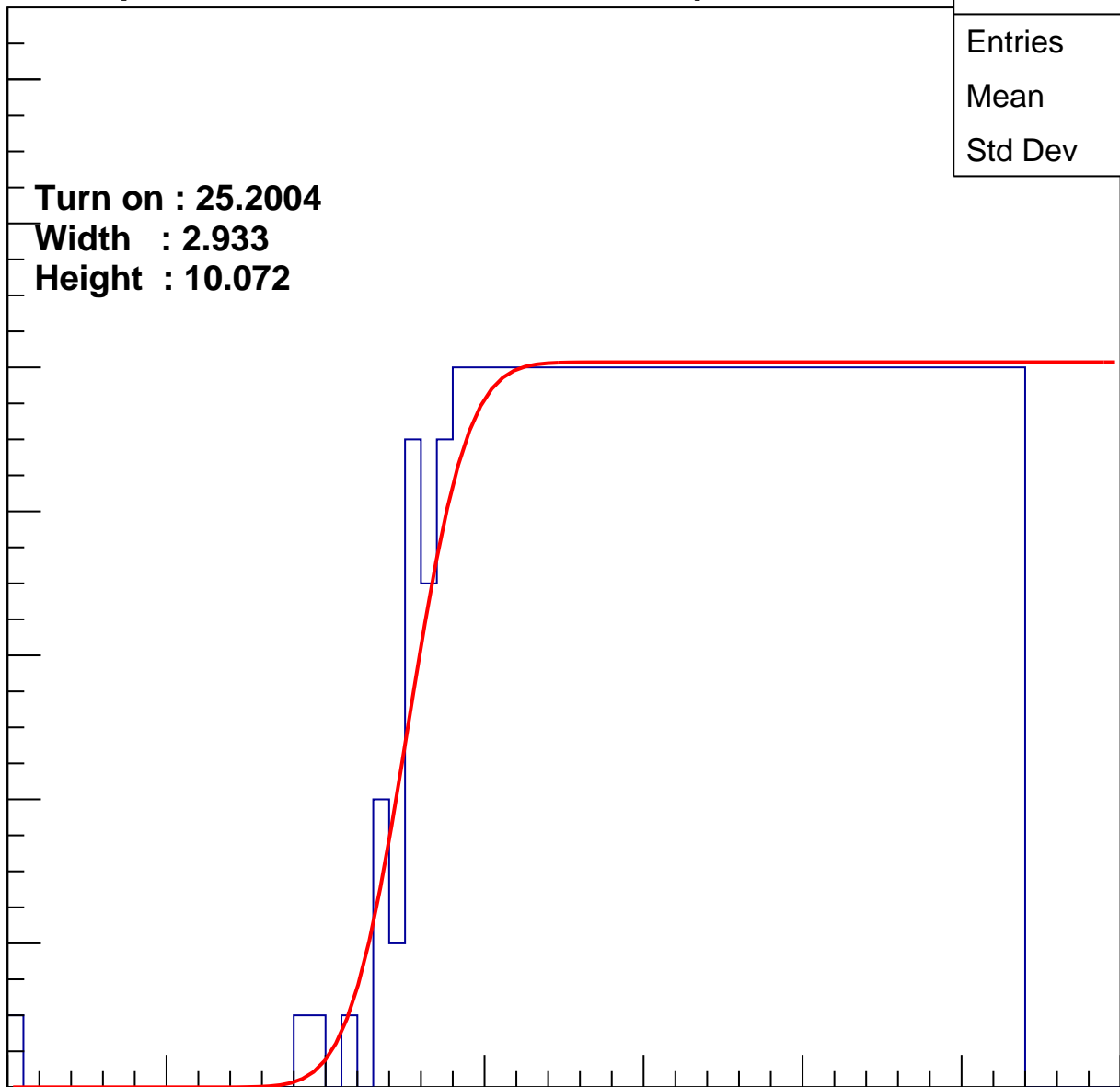
Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U24-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.18
Std Dev	11.68

Turn on : 26.6393

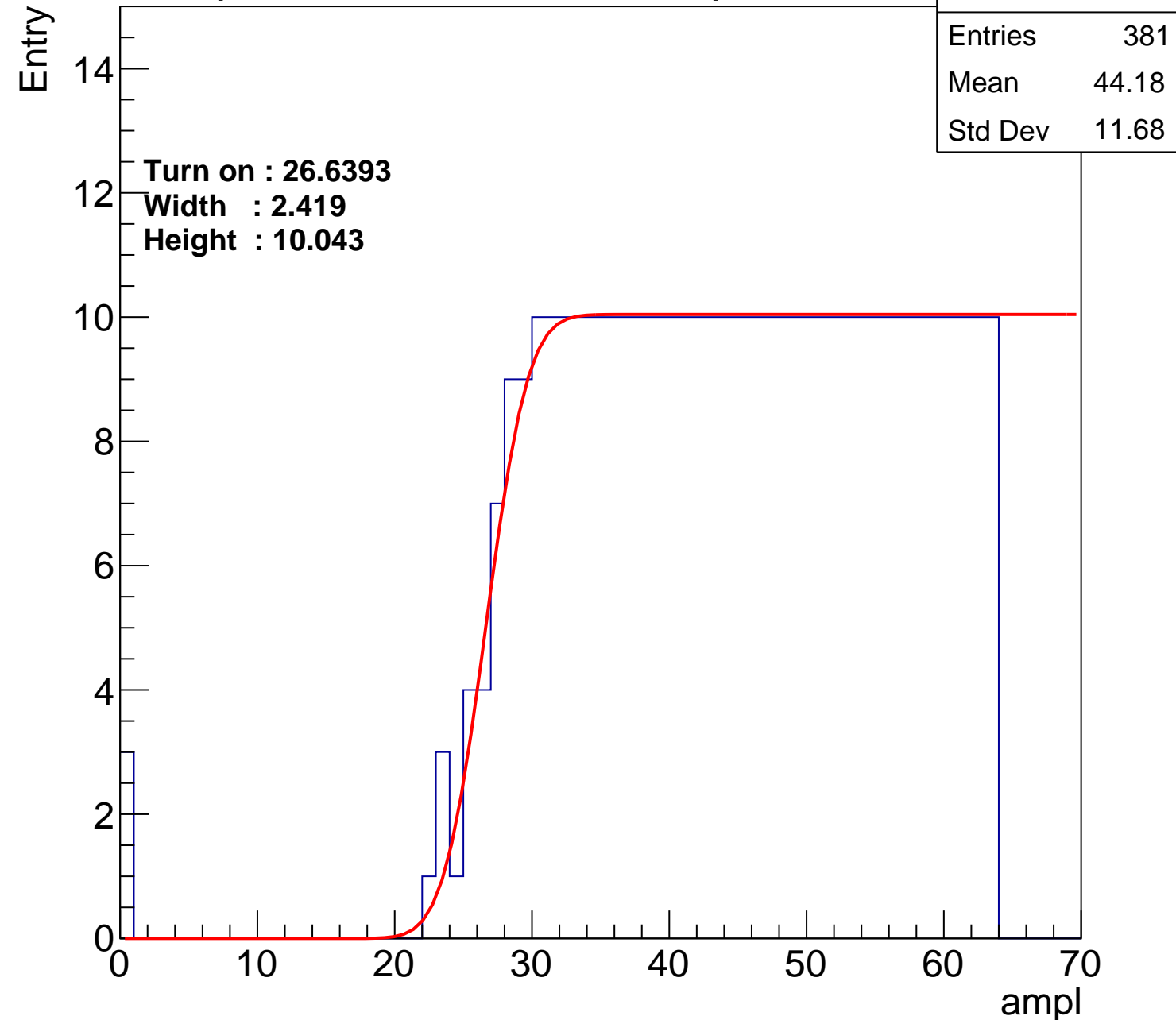
Width : 2.419

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch6

calib_packv5_042523_0143.root, FC#10, port B3

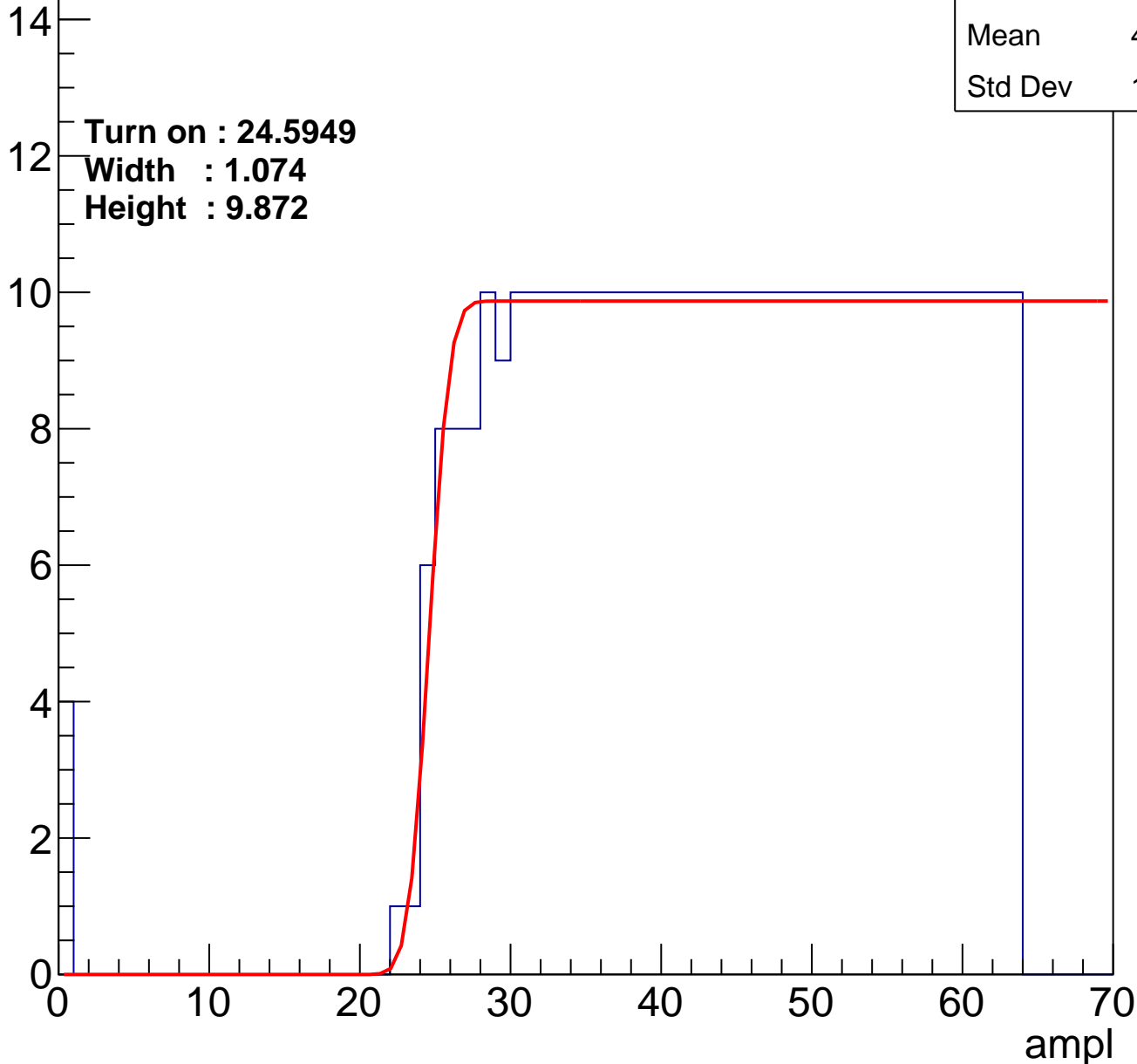
Entry

Entries	395
Mean	43.45
Std Dev	12.14

Turn on : 24.5949

Width : 1.074

Height : 9.872



B1L002S, U24-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 24.8908

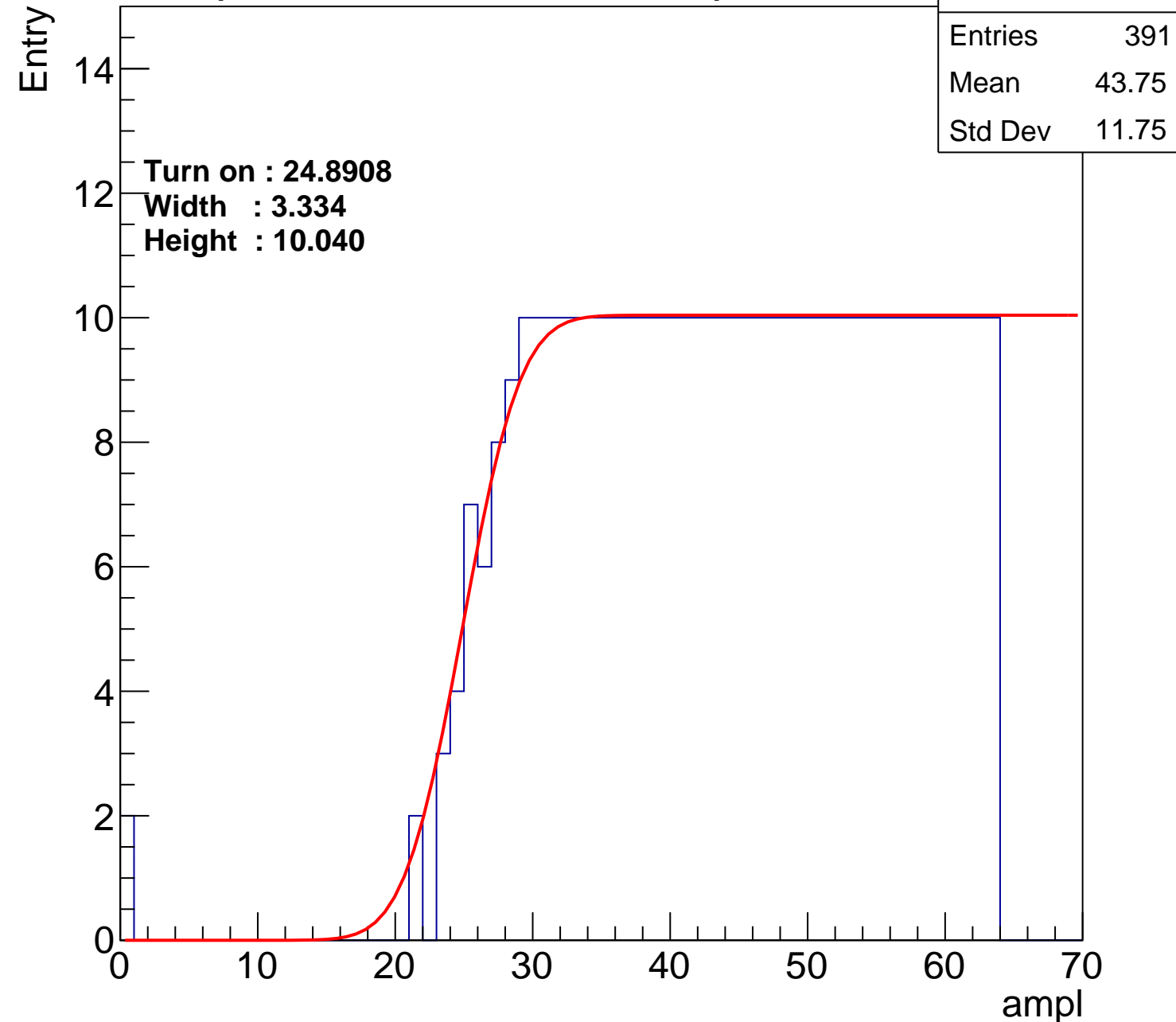
Width : 3.334

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.48
Std Dev	11.86

Turn on : 24.7118

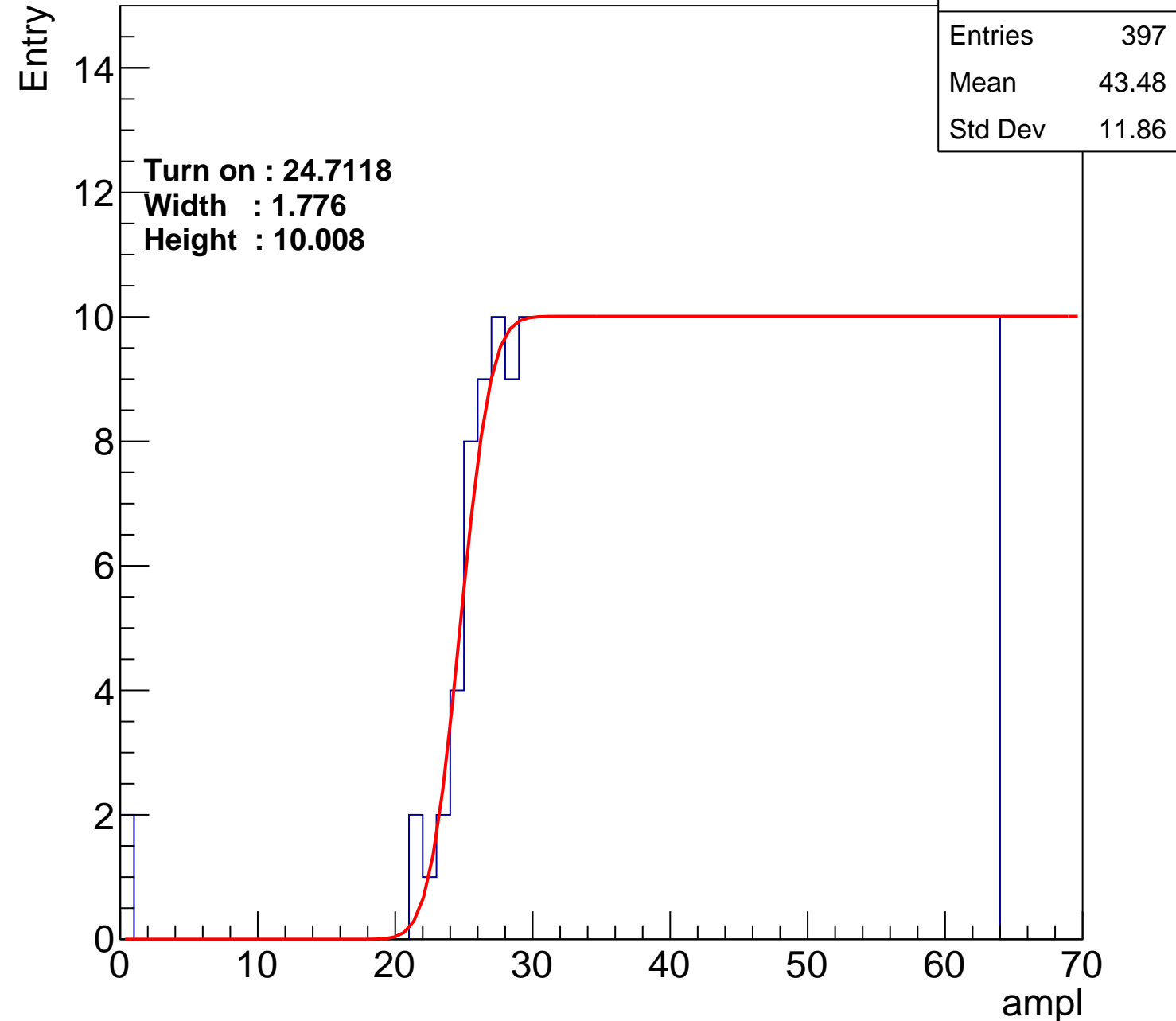
Width : 1.776

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch9

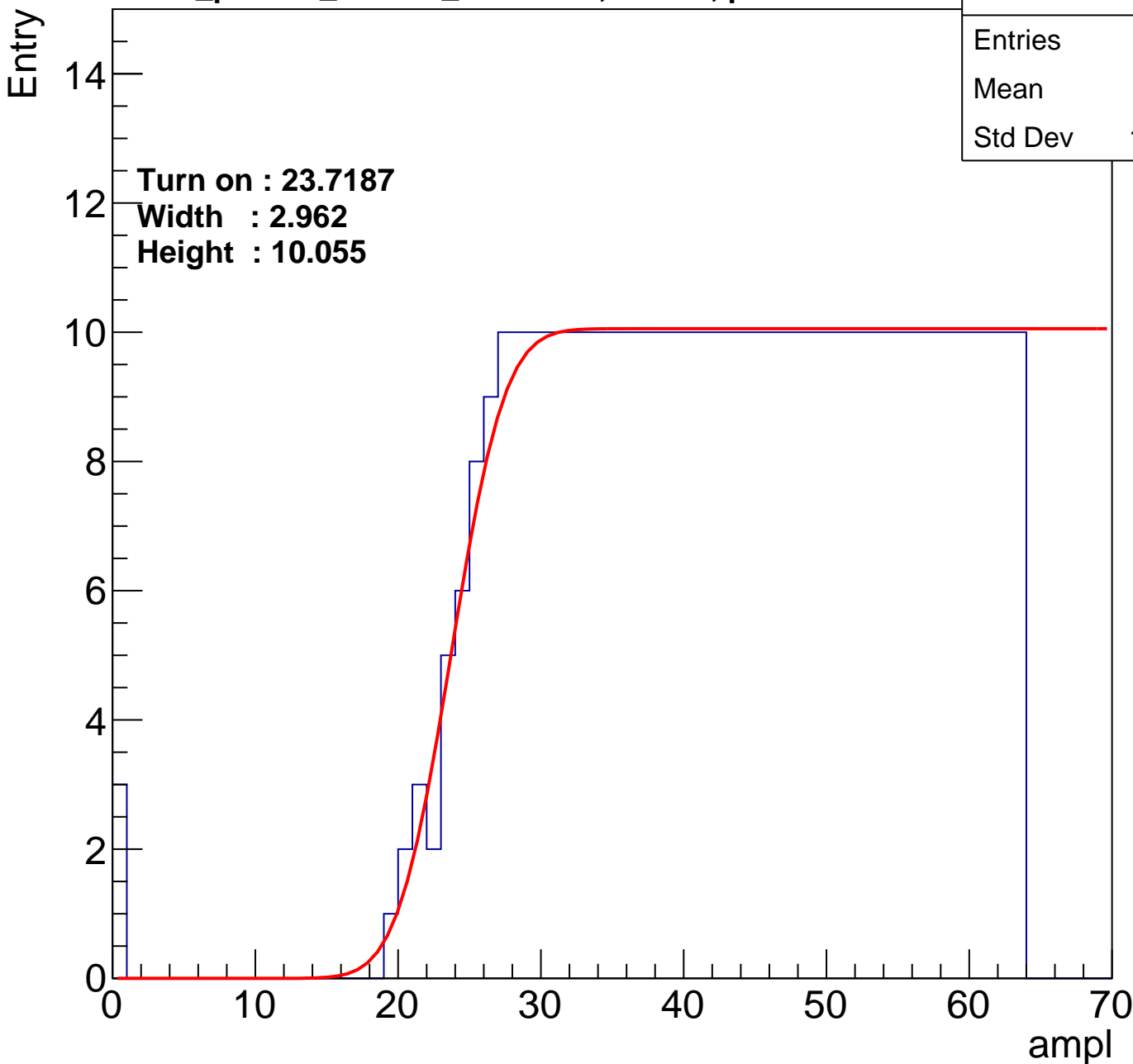
calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.81
Std Dev	12.36

Turn on : 23.7187

Width : 2.962

Height : 10.055



B1L002S, U24-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.51
Std Dev	11.87

Turn on : 24.3570

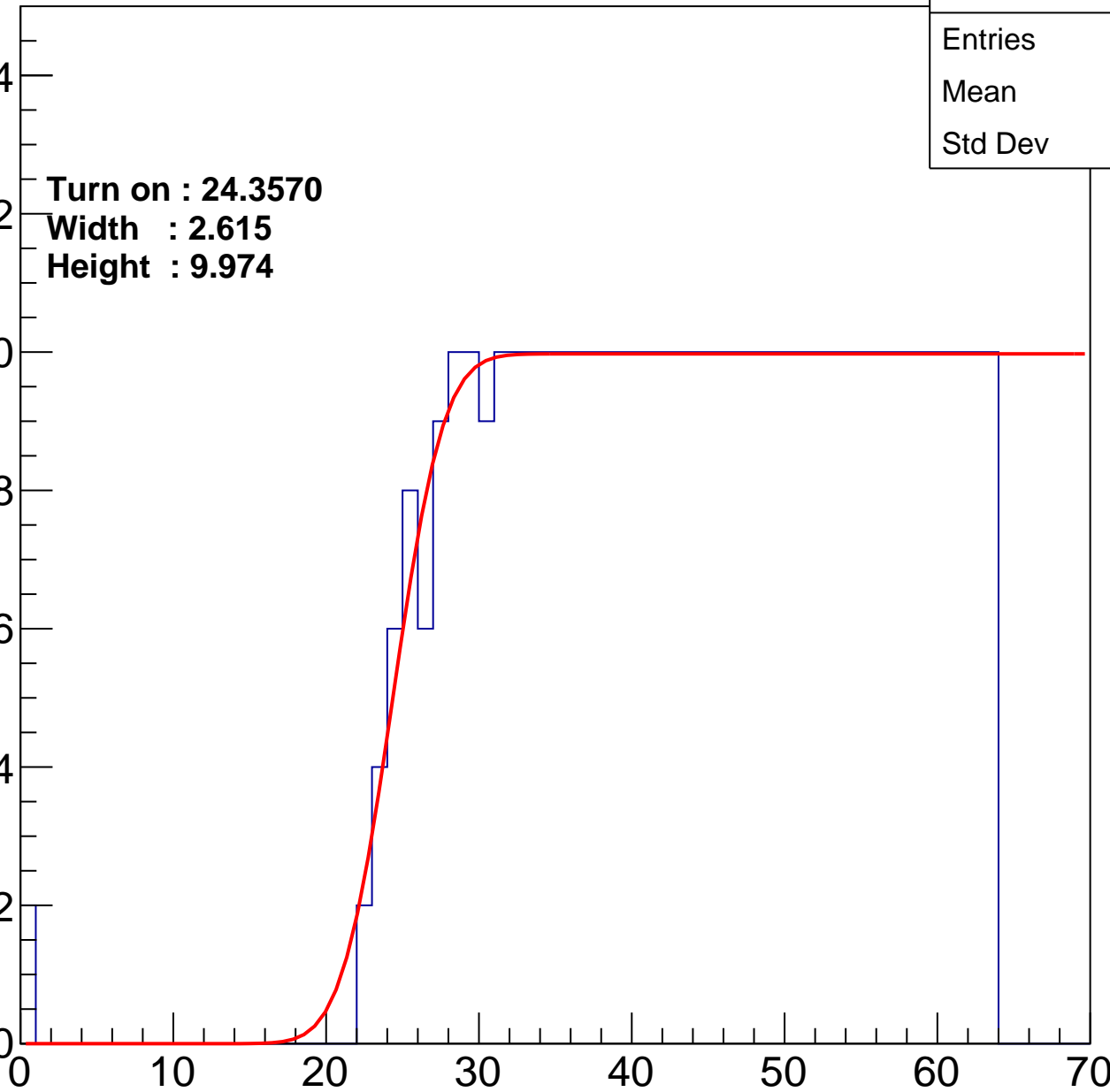
Width : 2.615

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 26.3153

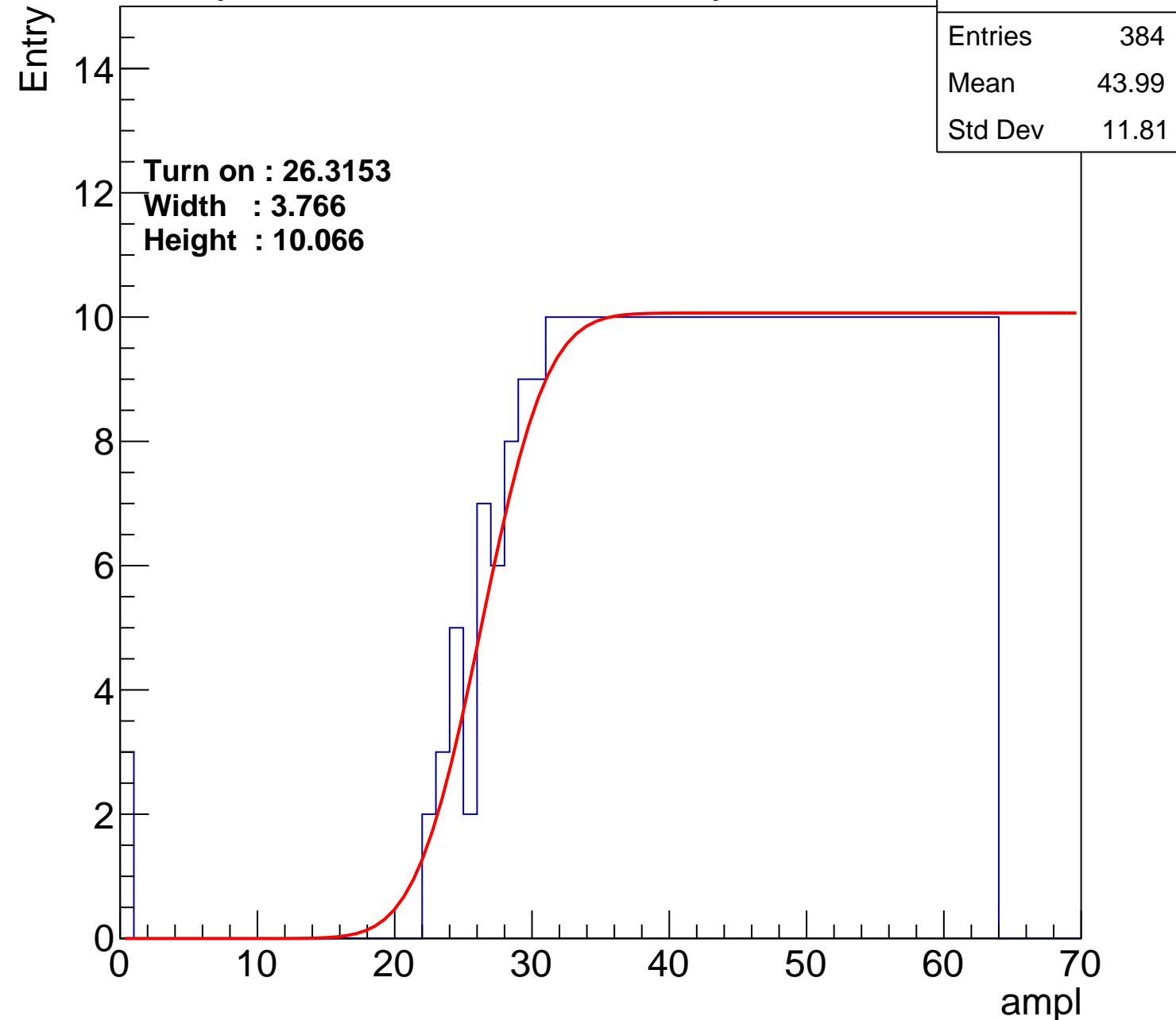
Width : 3.766

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	417
Mean	42.5
Std Dev	12.33

Turn on : 22.3472

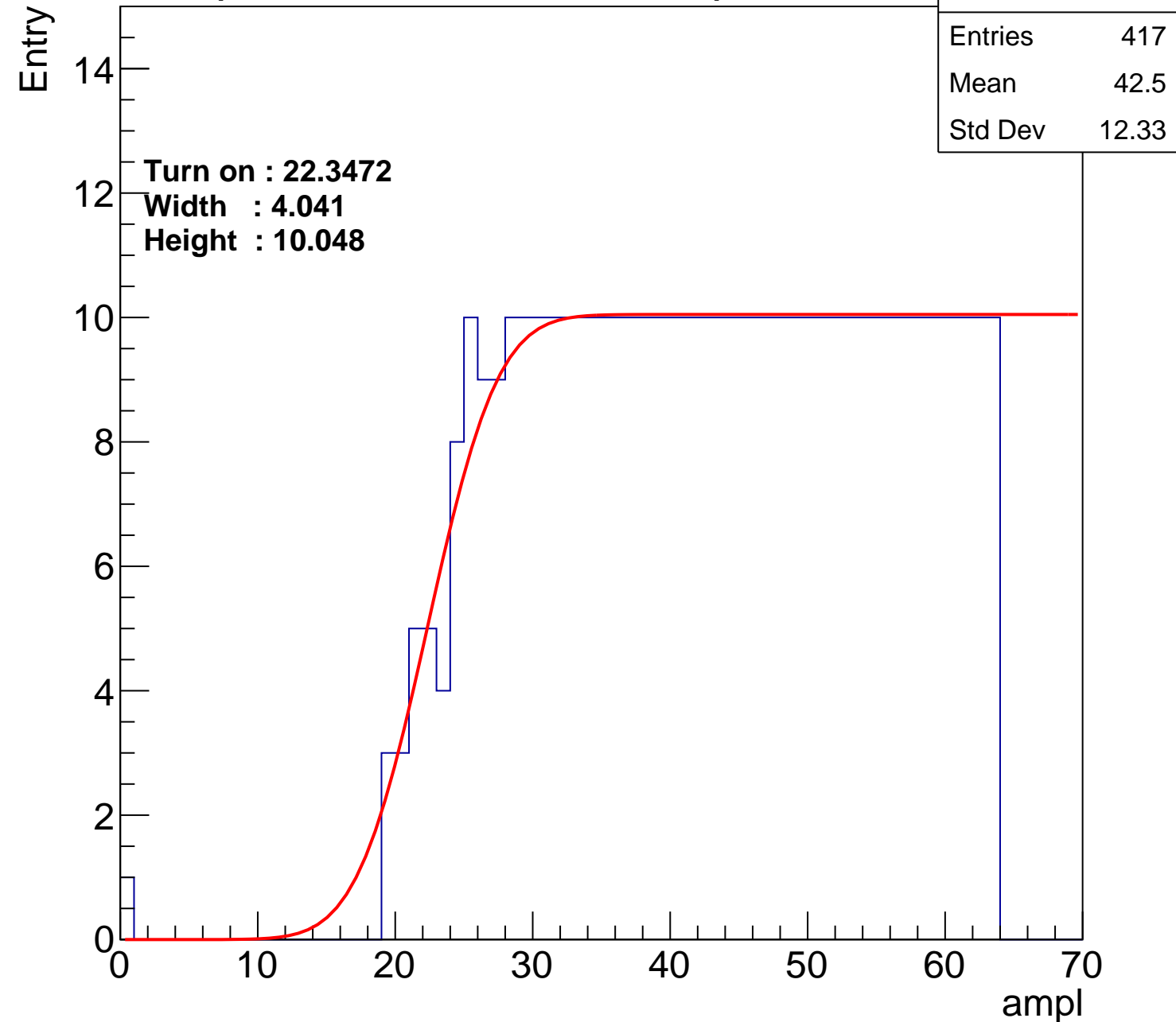
Width : 4.041

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 25.7631

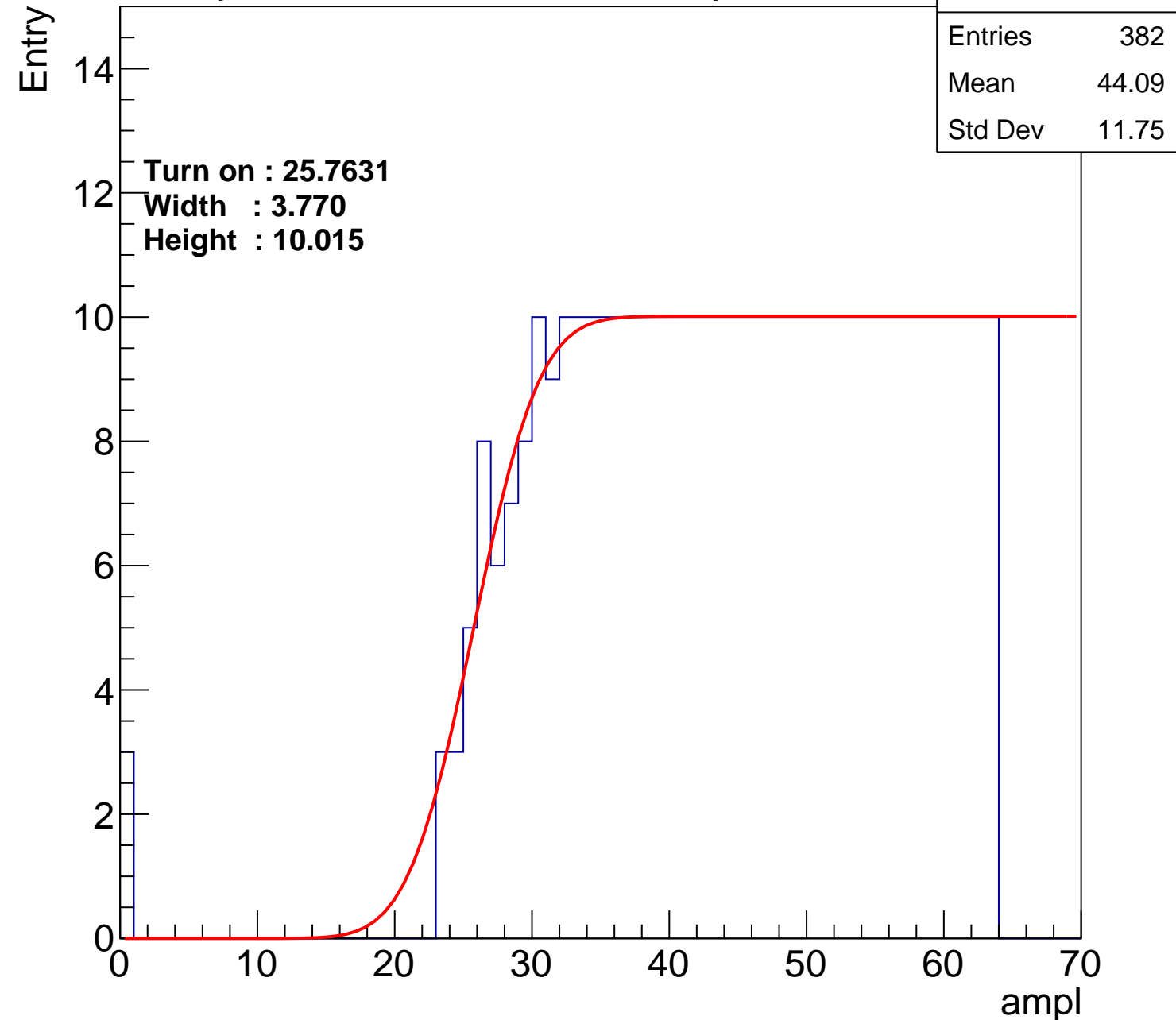
Width : 3.770

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.06
Std Dev	11.57

Turn on : 25.5879

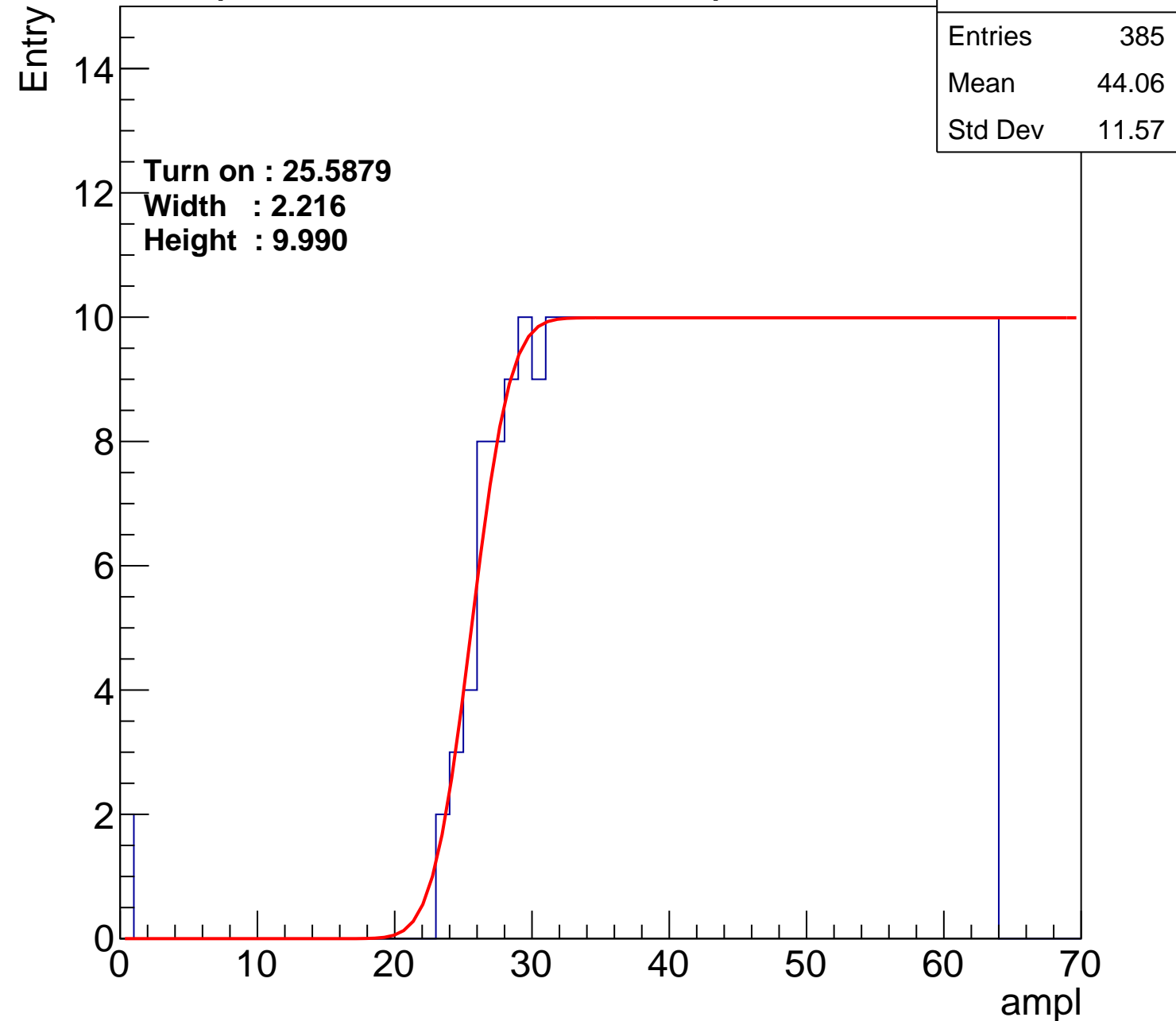
Width : 2.216

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.4104

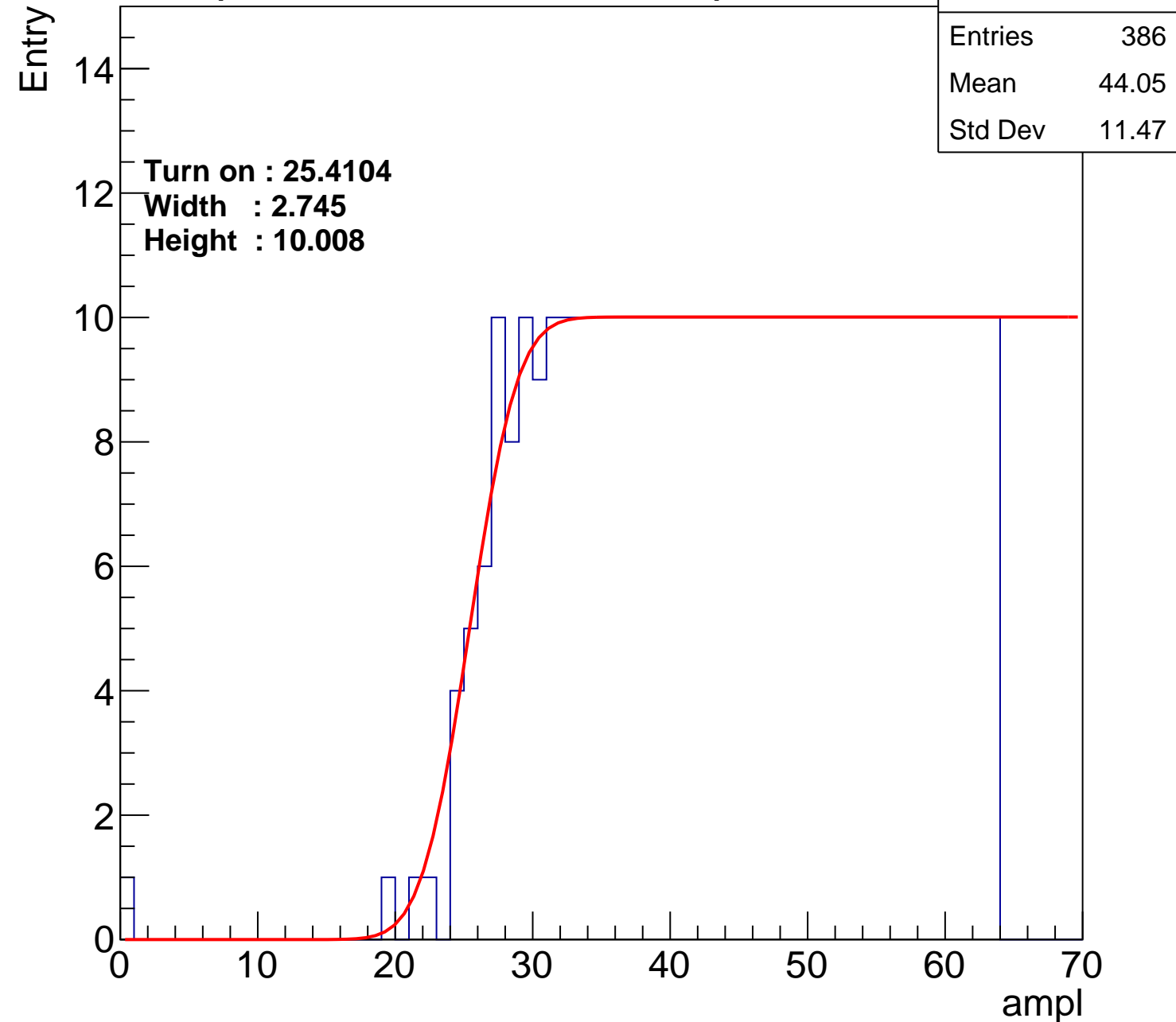
Width : 2.745

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 25.6216

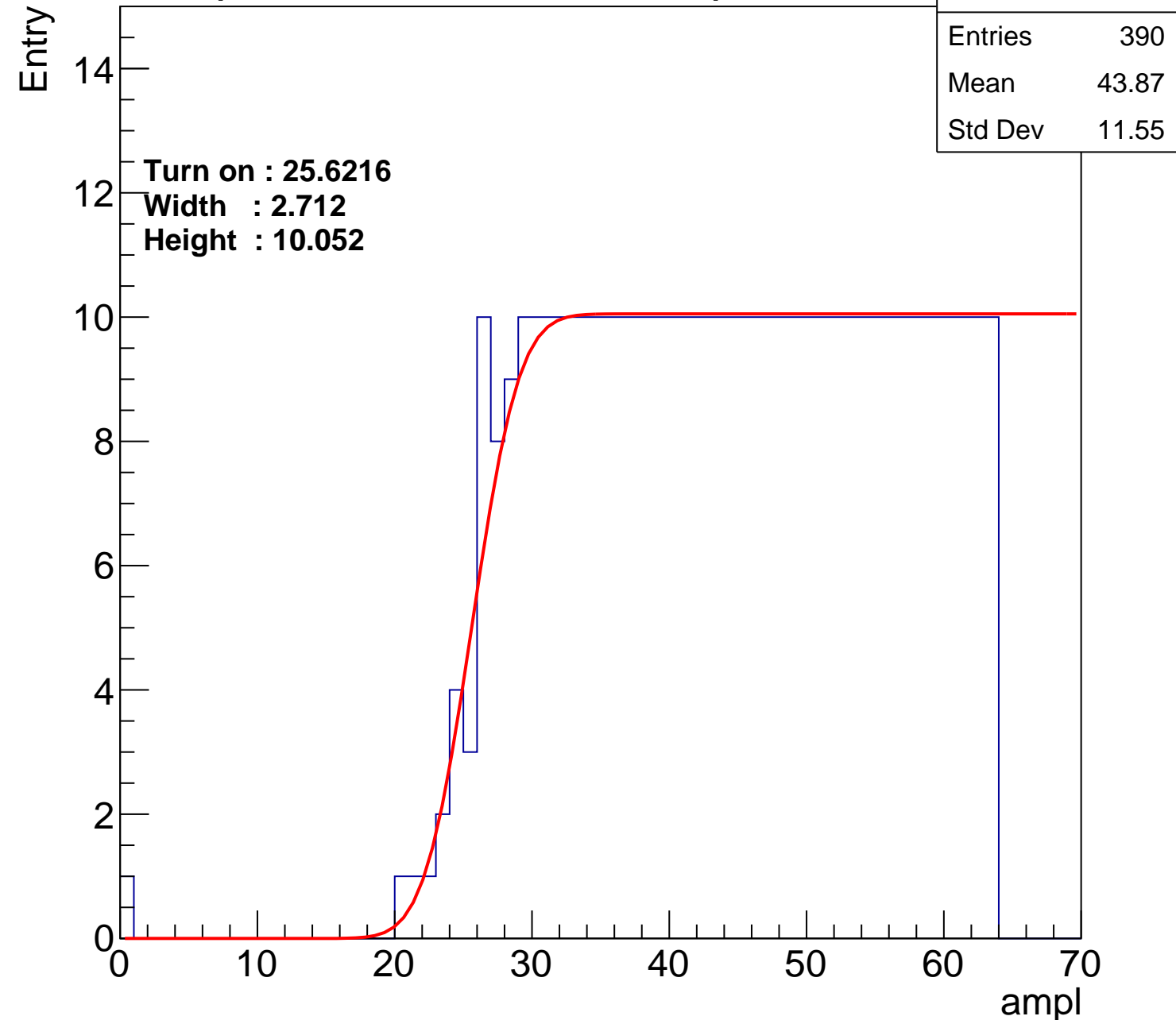
Width : 2.712

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch17

calib_packv5_042523_0143.root, FC#10, port B3

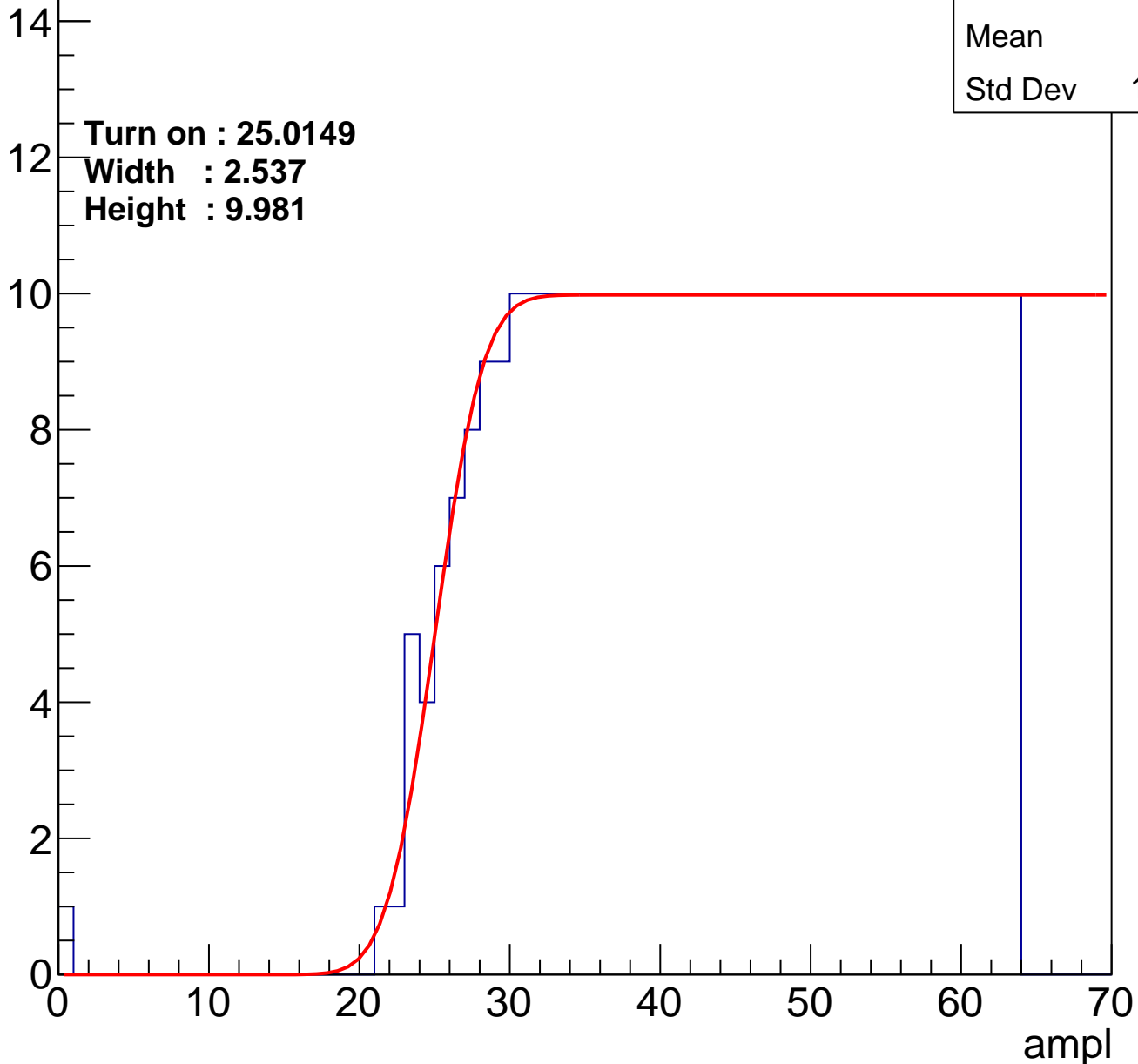
Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 25.0149

Width : 2.537

Height : 9.981

Entry



B1L002S, U24-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.97
Std Dev	11.74

Turn on : 26.0742

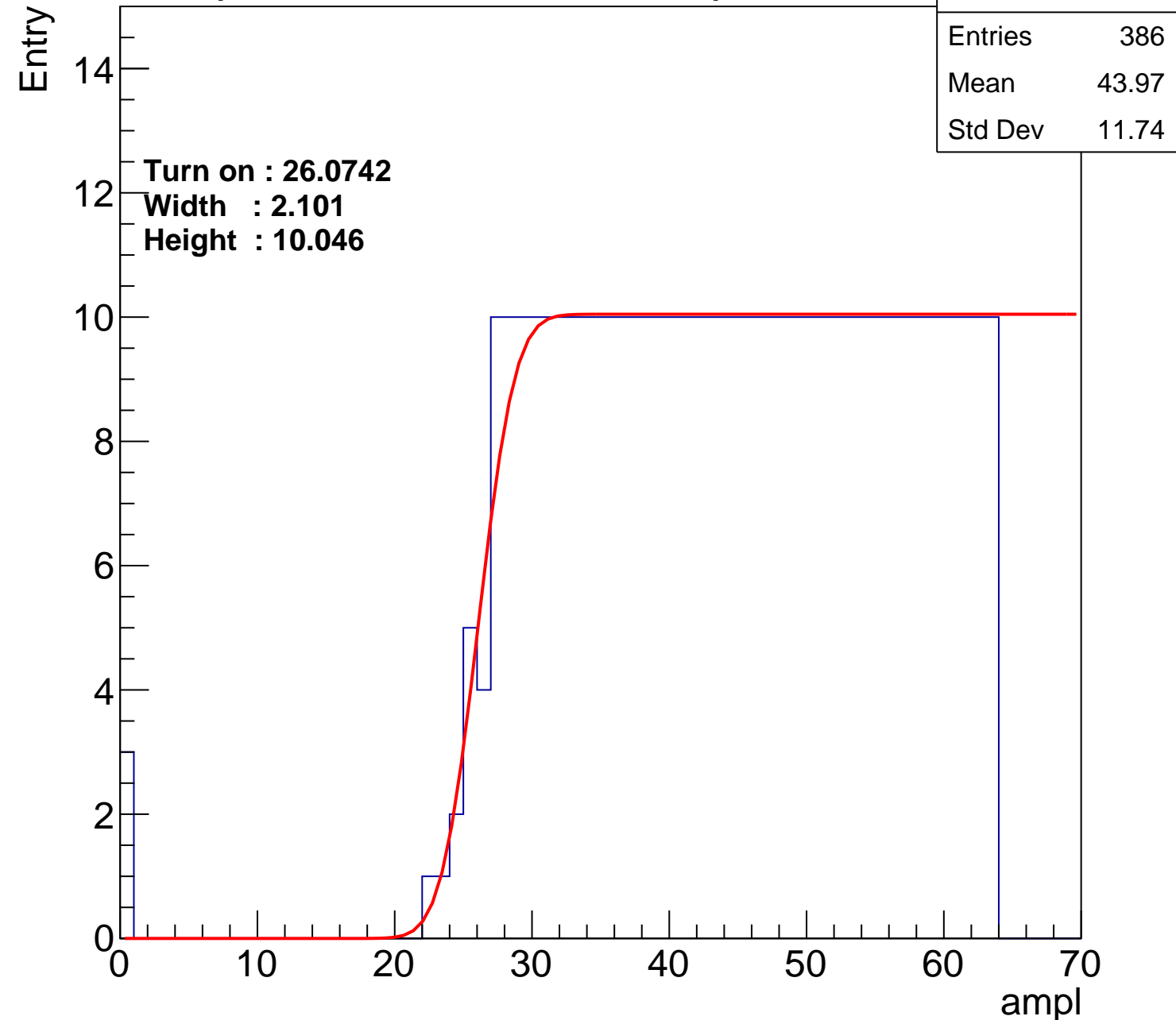
Width : 2.101

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.55
Std Dev	11.98

Turn on : 24.9771

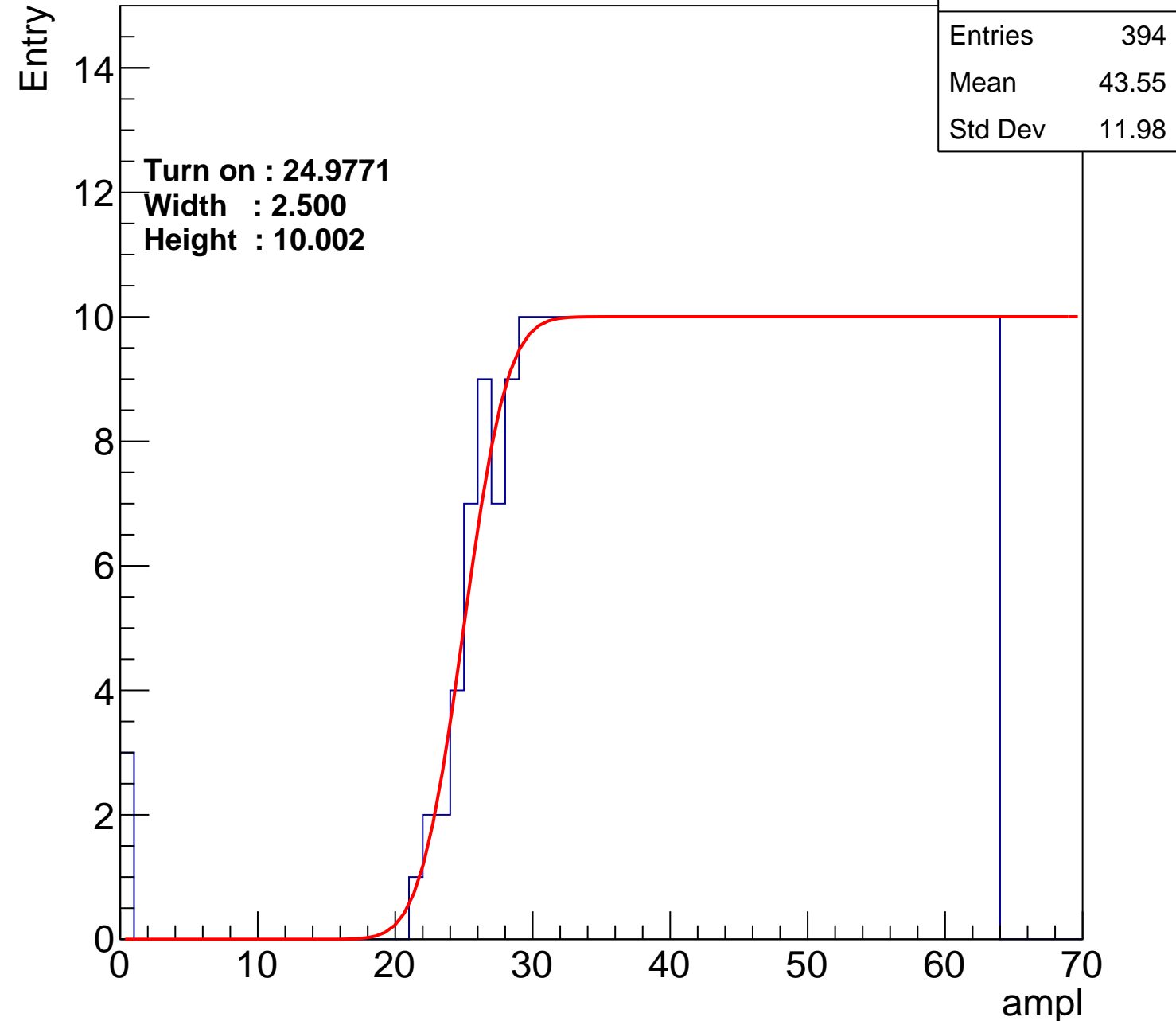
Width : 2.500

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch20

calib_packv5_042523_0143.root, FC#10, port B3

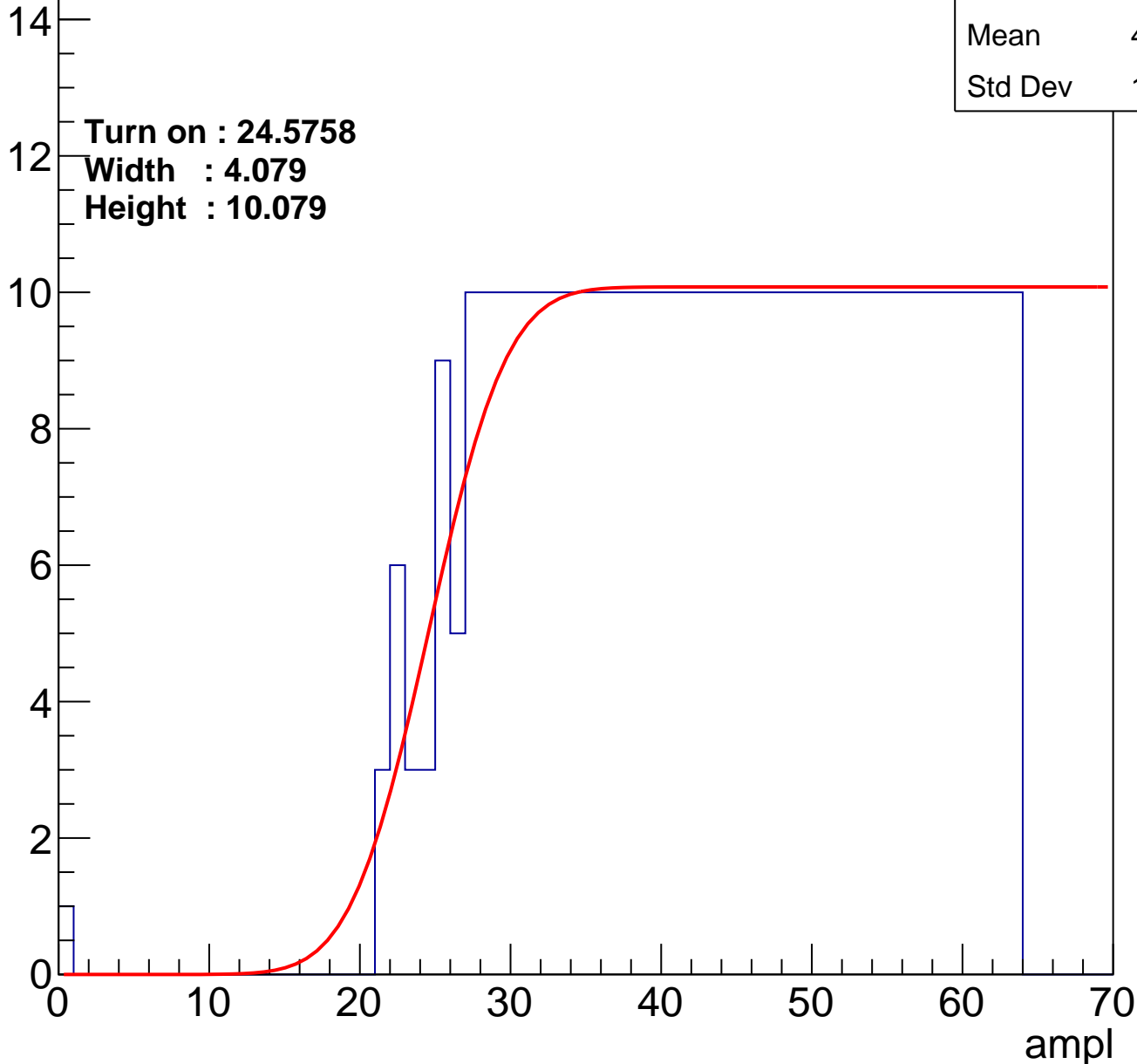
Entries	400
Mean	43.35
Std Dev	11.85

Turn on : 24.5758

Width : 4.079

Height : 10.079

Entry



B1L002S, U24-ch21

calib_packv5_042523_0143.root, FC#10, port B3

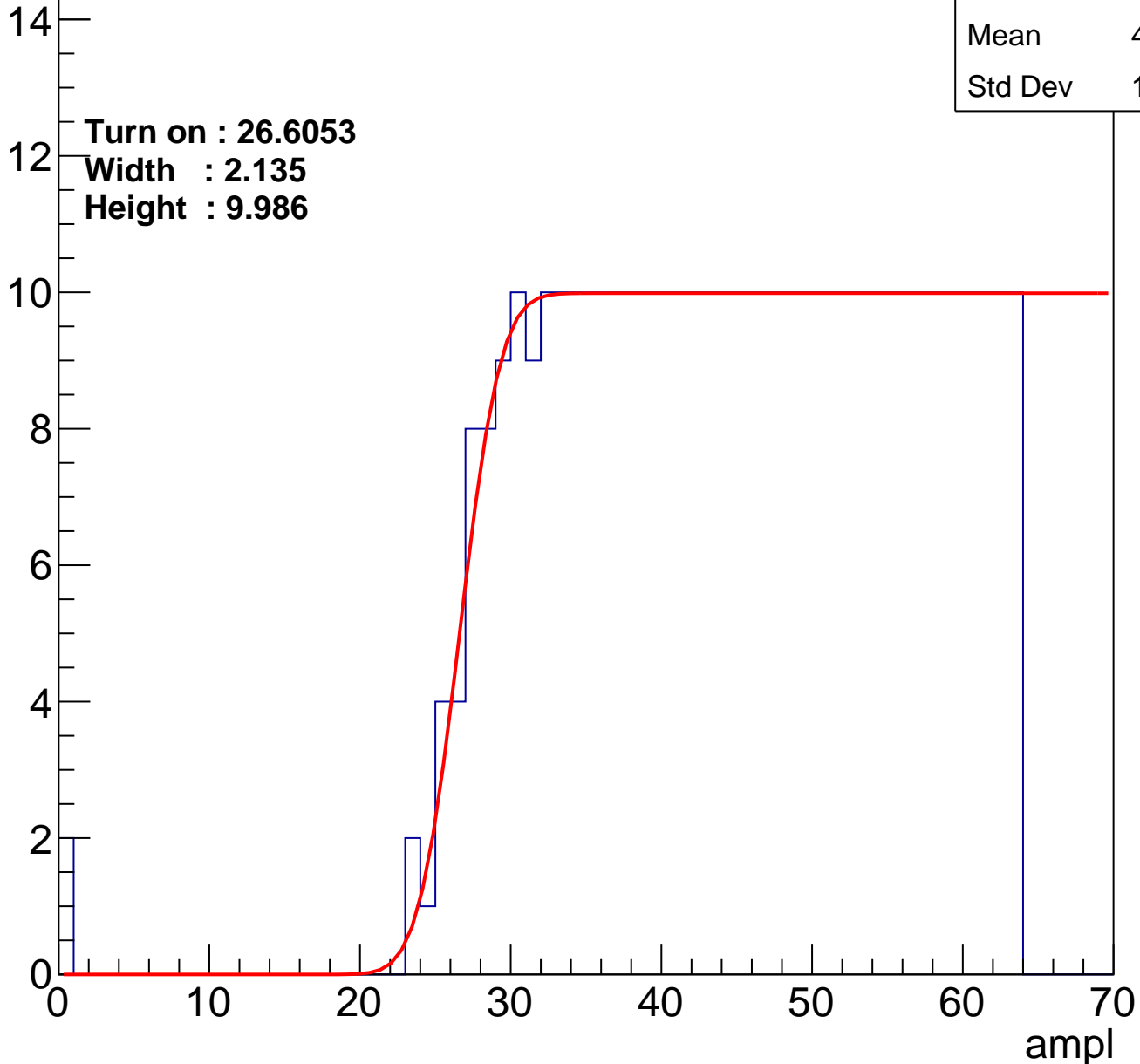
Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.6053

Width : 2.135

Height : 9.986

Entry



B1L002S, U24-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.77
Std Dev	11.77

Turn on : 25.7674

Width : 2.987

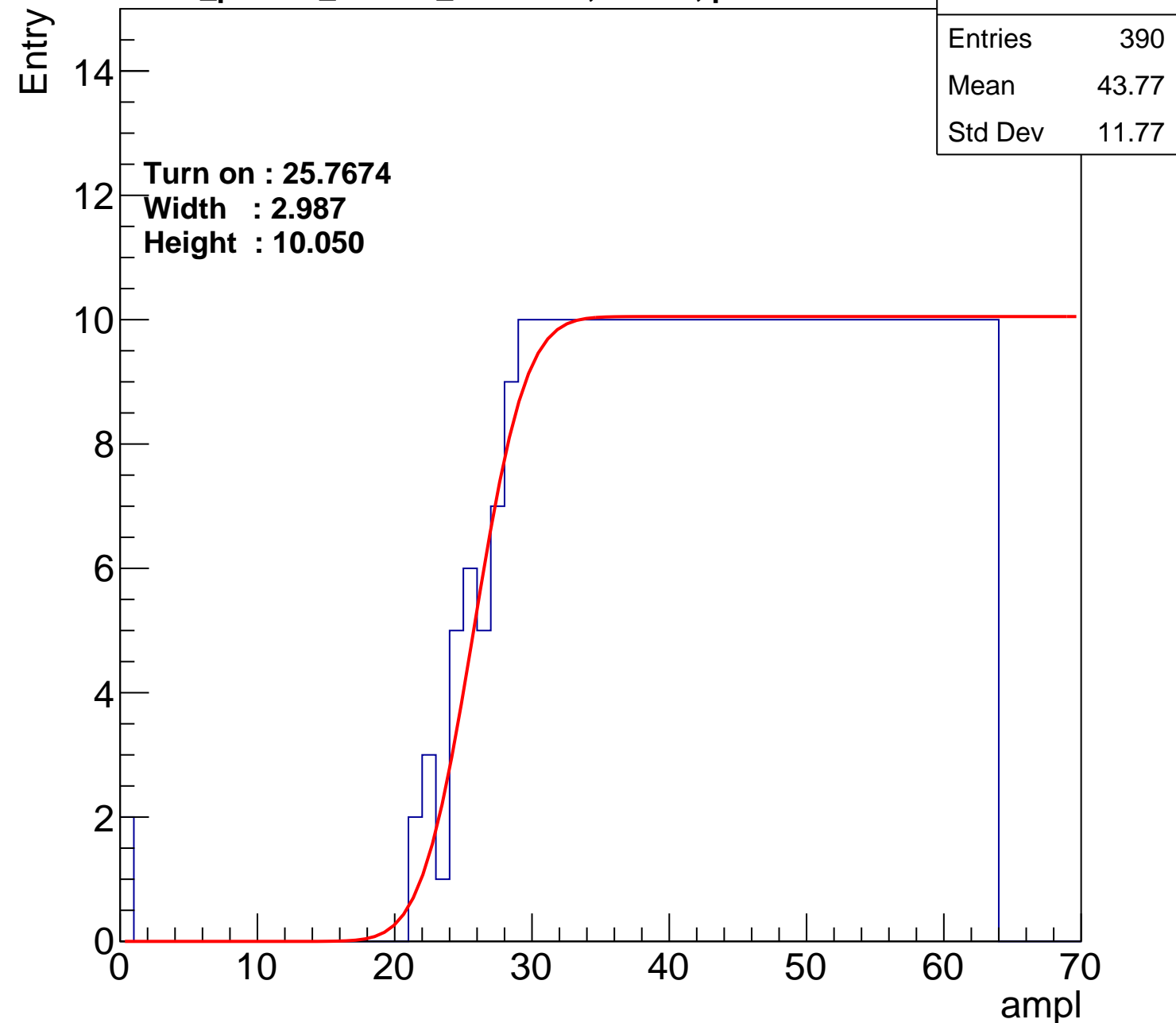
Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U24-ch23

calib_packv5_042523_0143.root, FC#10, port B3

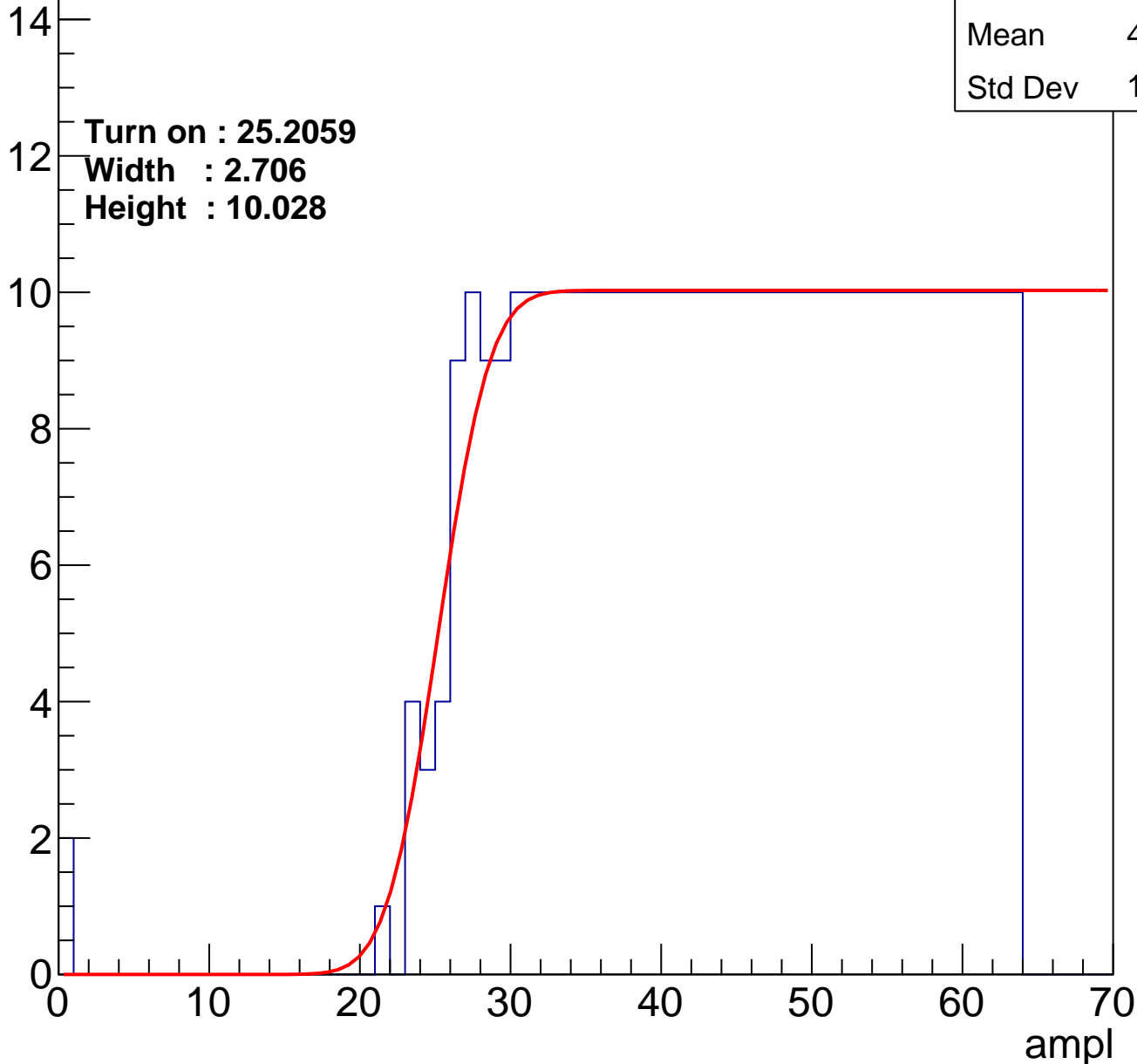
Entries	391
Mean	43.76
Std Dev	11.73

Turn on : 25.2059

Width : 2.706

Height : 10.028

Entry



B1L002S, U24-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.84
Std Dev	11.83

Turn on : 25.7768

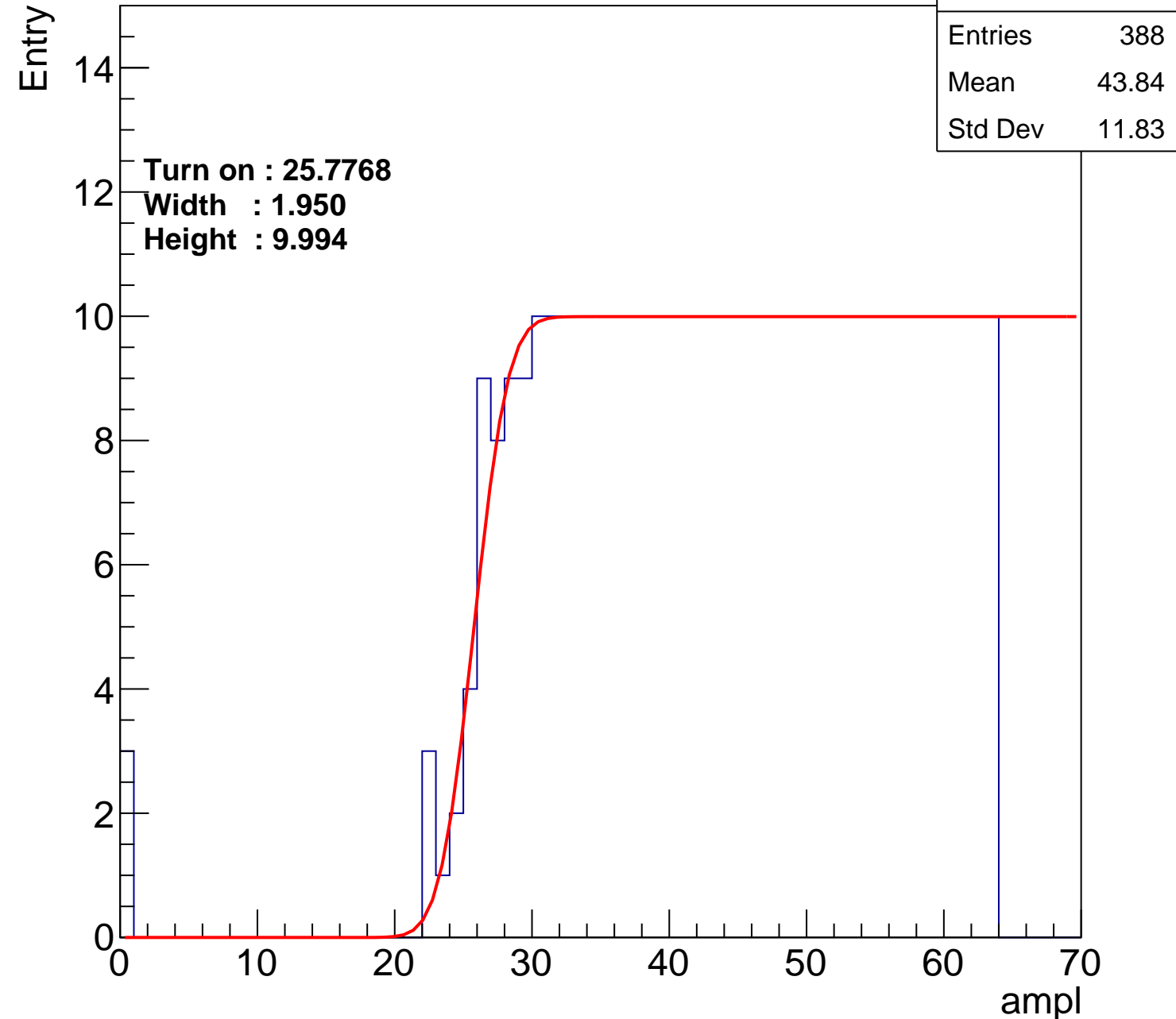
Width : 1.950

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.38
Std Dev	11.29

Turn on : 26.1365

Width : 2.440

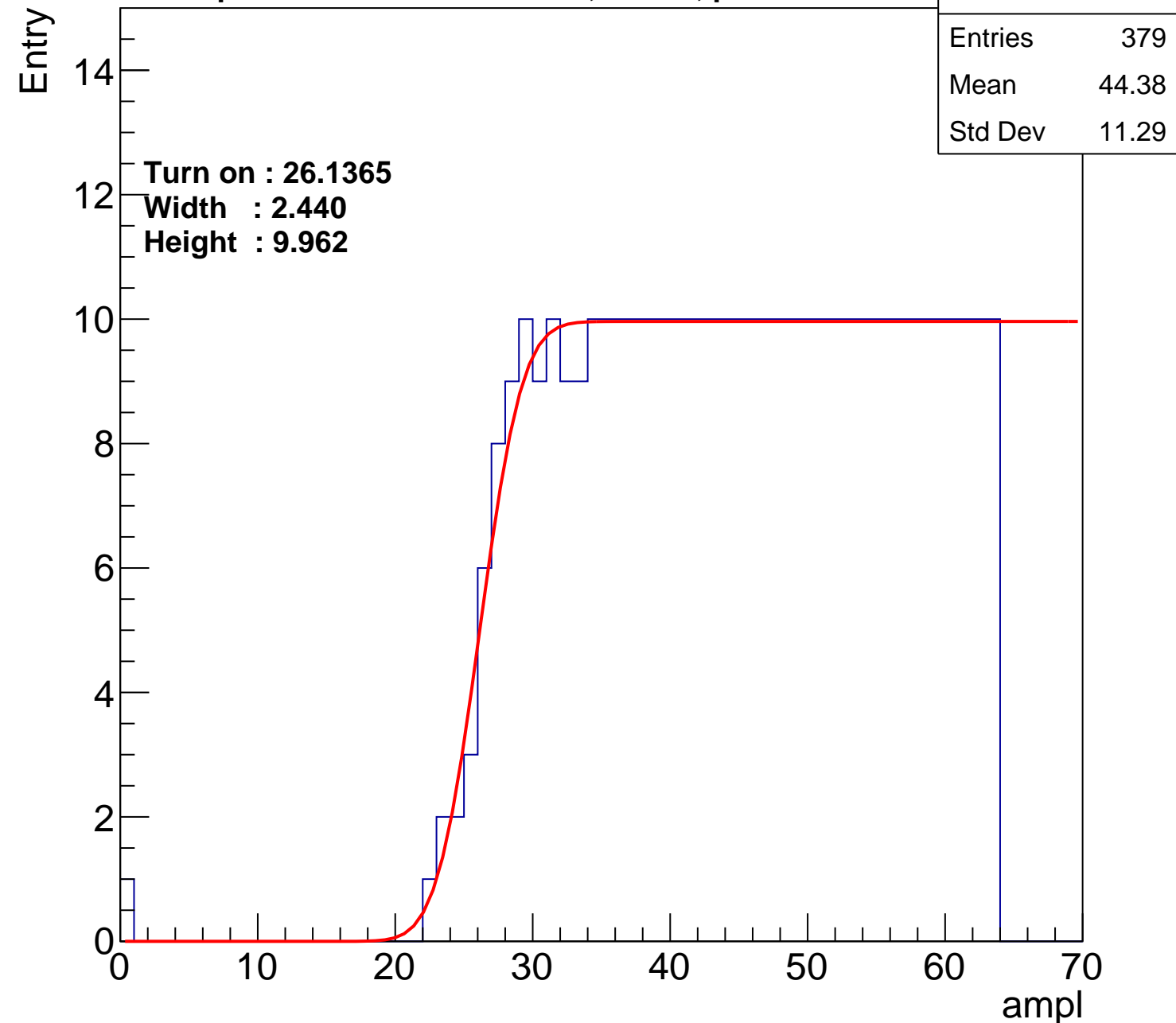
Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U24-ch26

calib_packv5_042523_0143.root, FC#10, port B3

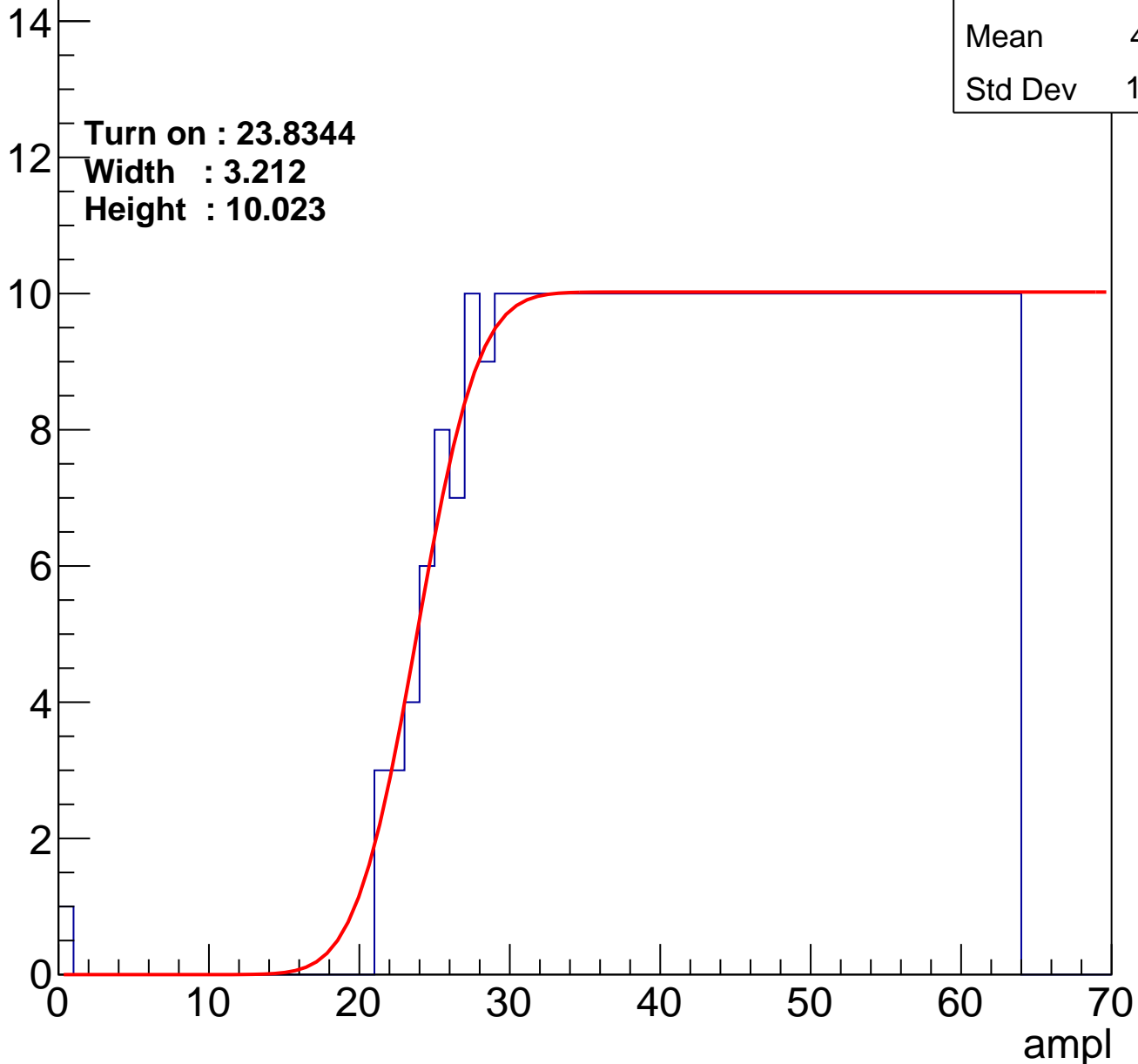
Entries	401
Mean	43.31
Std Dev	11.86

Turn on : 23.8344

Width : 3.212

Height : 10.023

Entry



B1L002S, U24-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.52
Std Dev	11.91

Turn on : 24.8527

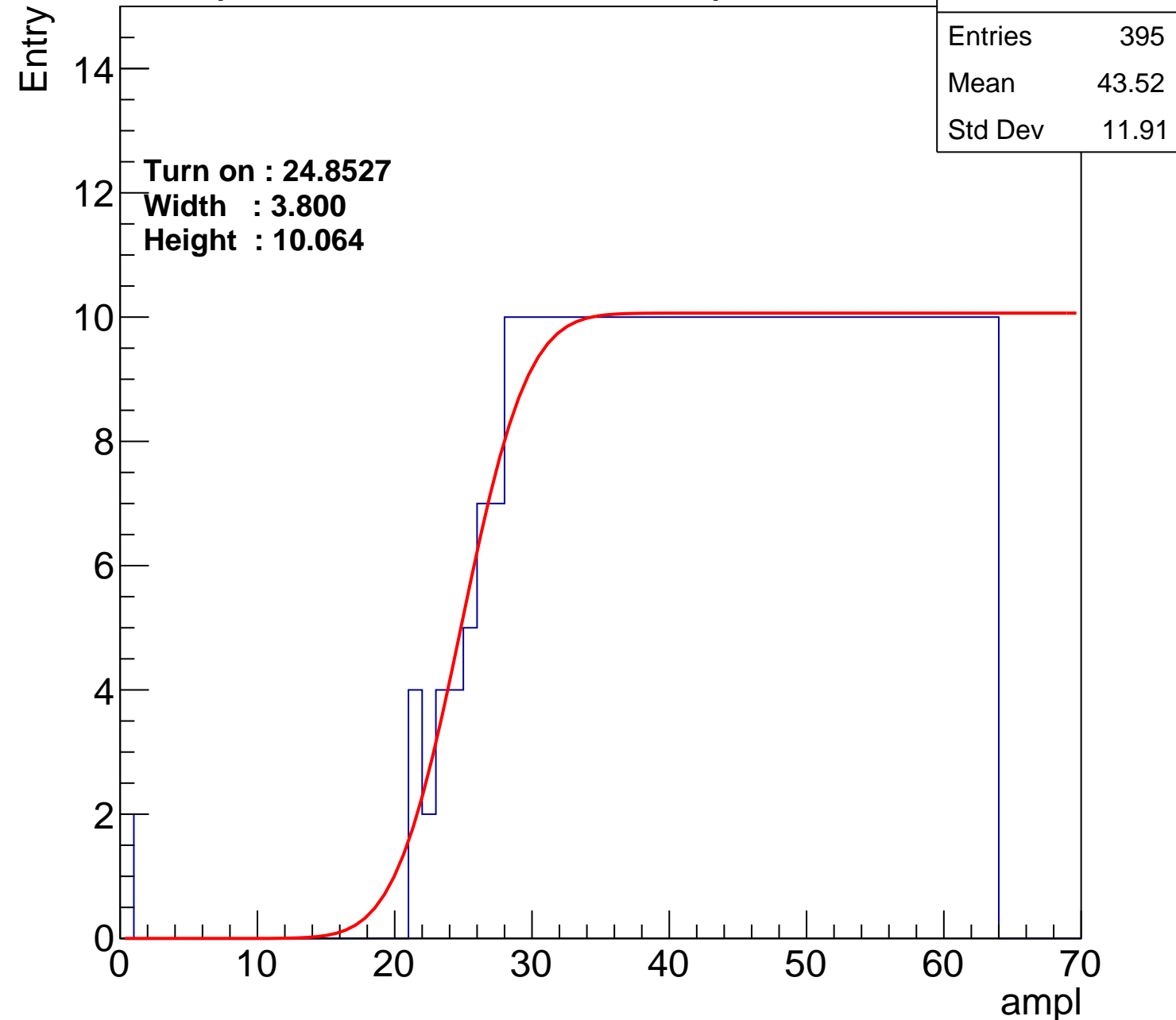
Width : 3.800

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch28

calib_packv5_042523_0143.root, FC#10, port B3

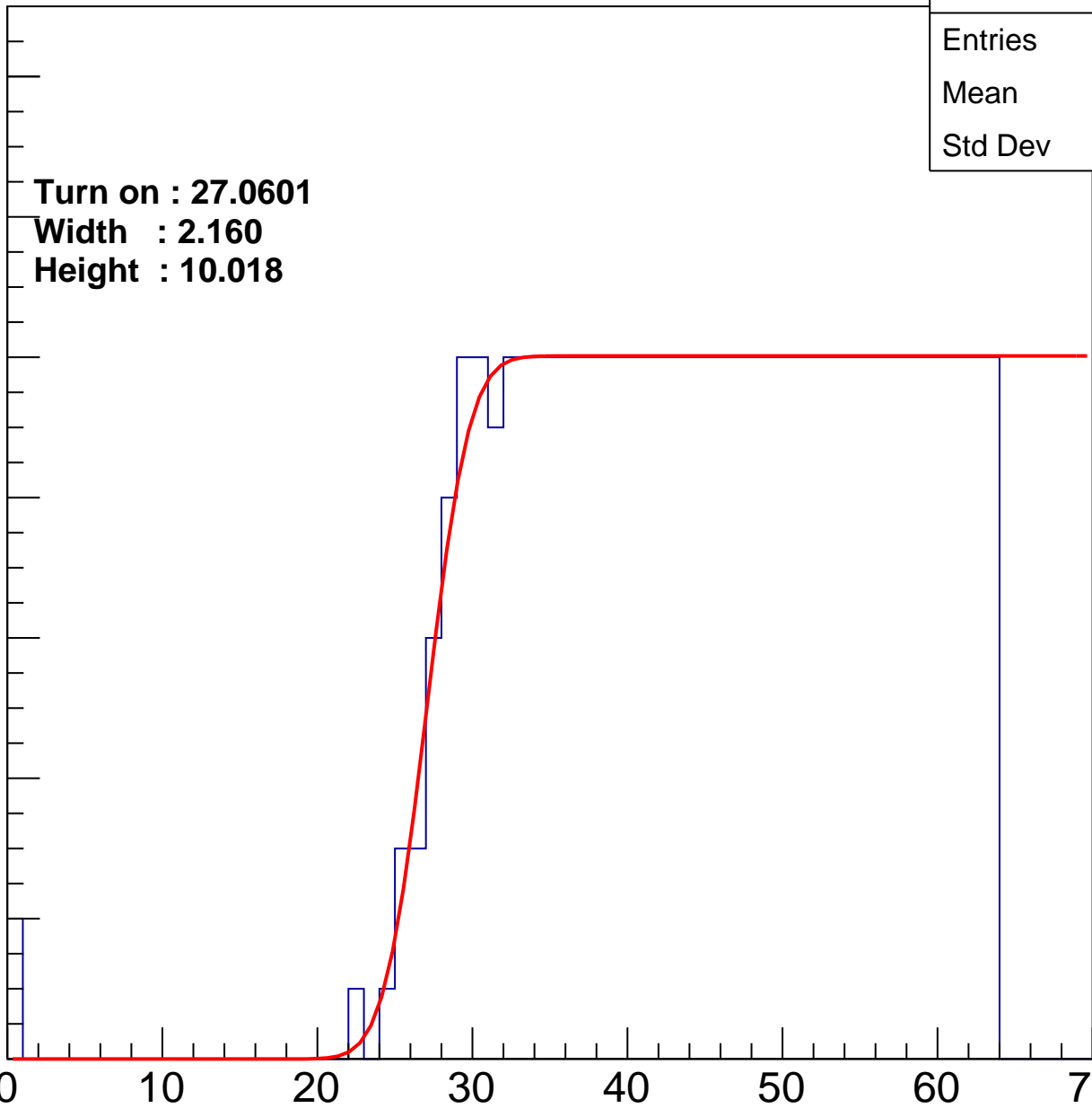
Entry

14
12
10
8
6
4
2
0

Turn on : 27.0601
Width : 2.160
Height : 10.018

Entries	373
Mean	44.65
Std Dev	11.27

ampl



B1L002S, U24-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.18
Std Dev	12.41

Turn on : 24.2030

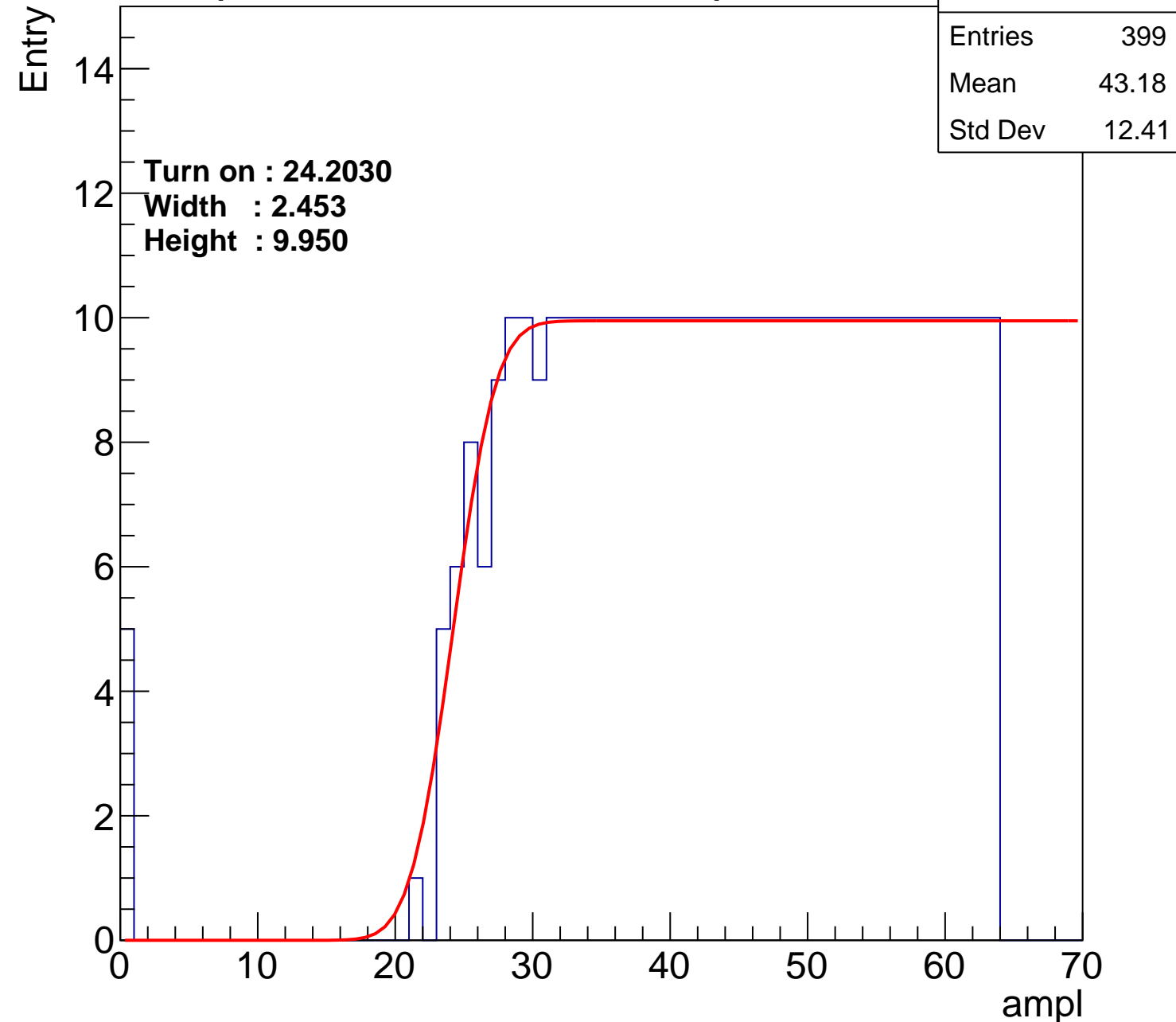
Width : 2.453

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.22
Std Dev	11.34

Turn on : 26.1524

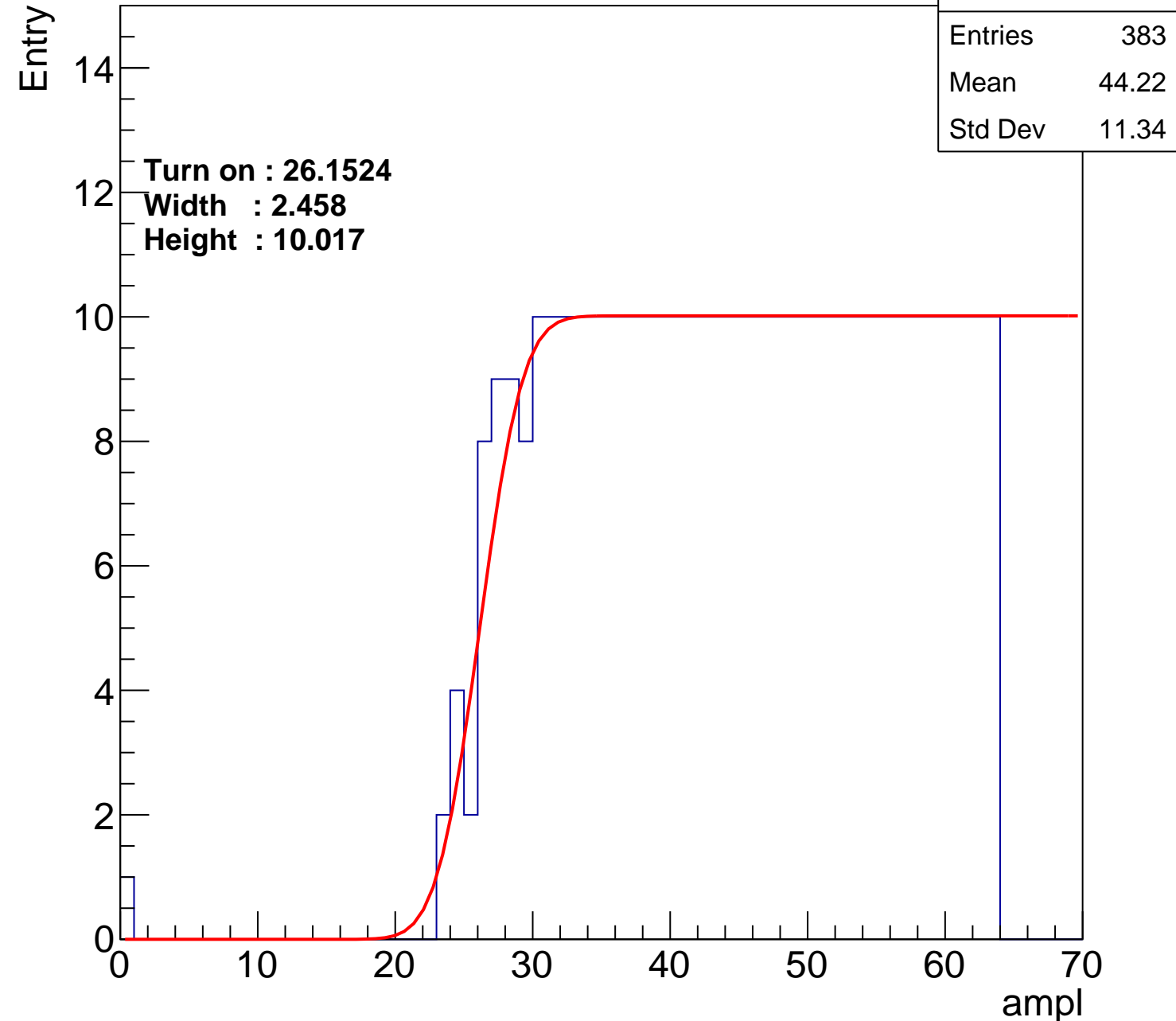
Width : 2.458

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch31

calib_packv5_042523_0143.root, FC#10, port B3

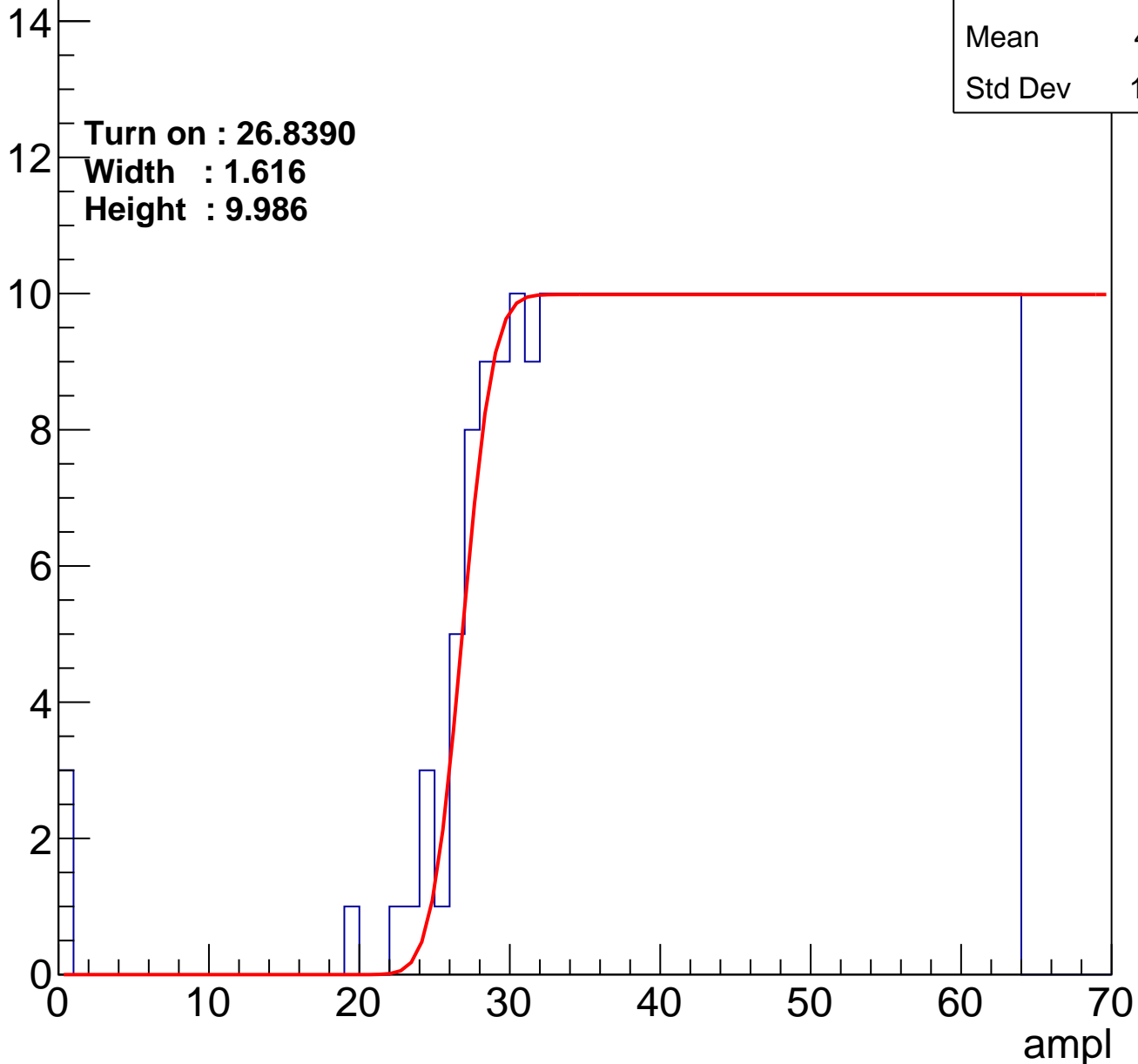
Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.8390

Width : 1.616

Height : 9.986

Entry



B1L002S, U24-ch32

calib_packv5_042523_0143.root, FC#10, port B3

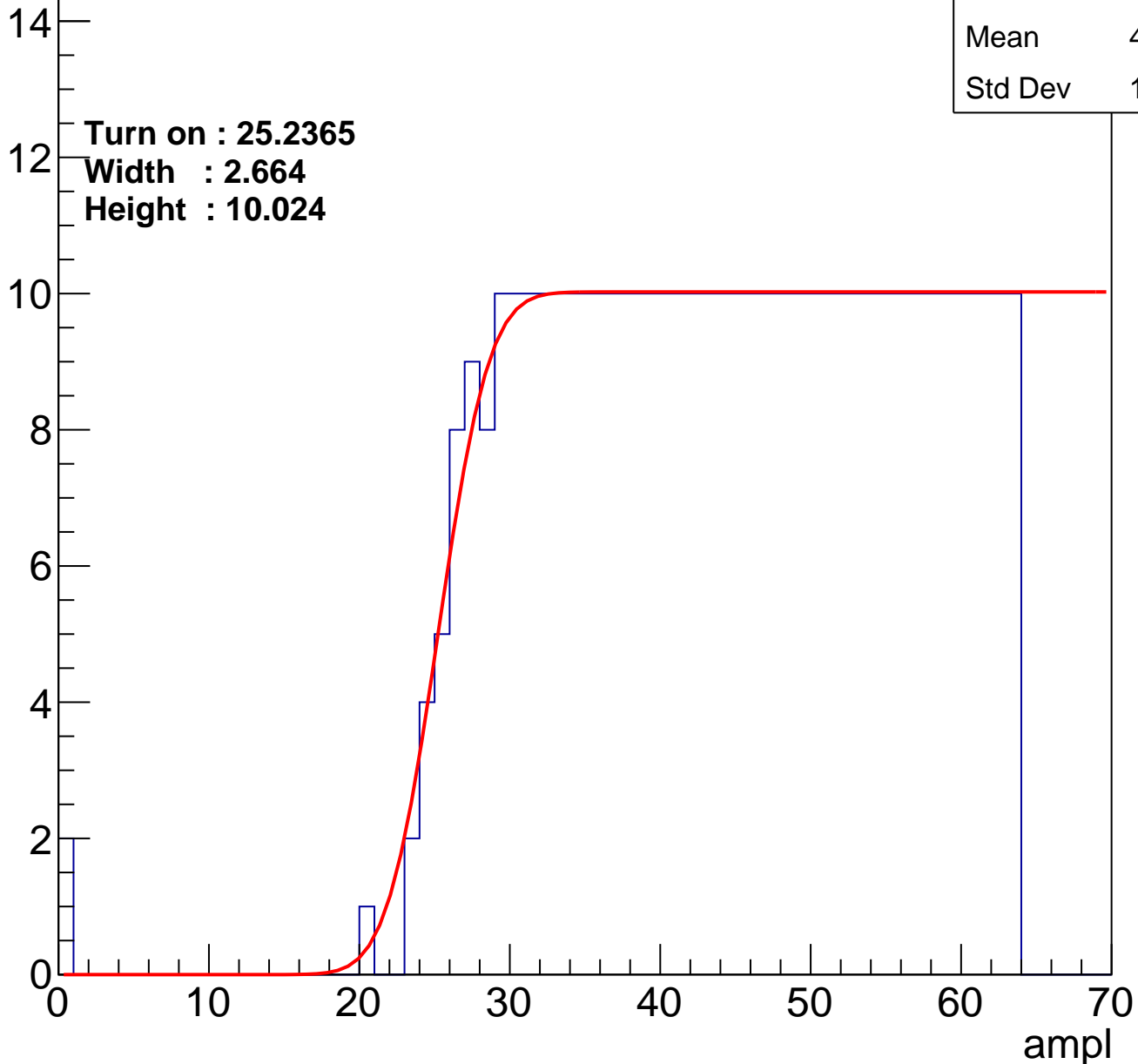
Entries	389
Mean	43.86
Std Dev	11.68

Turn on : 25.2365

Width : 2.664

Height : 10.024

Entry



B1L002S, U24-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.24
Std Dev	11.51

Turn on : 26.4462

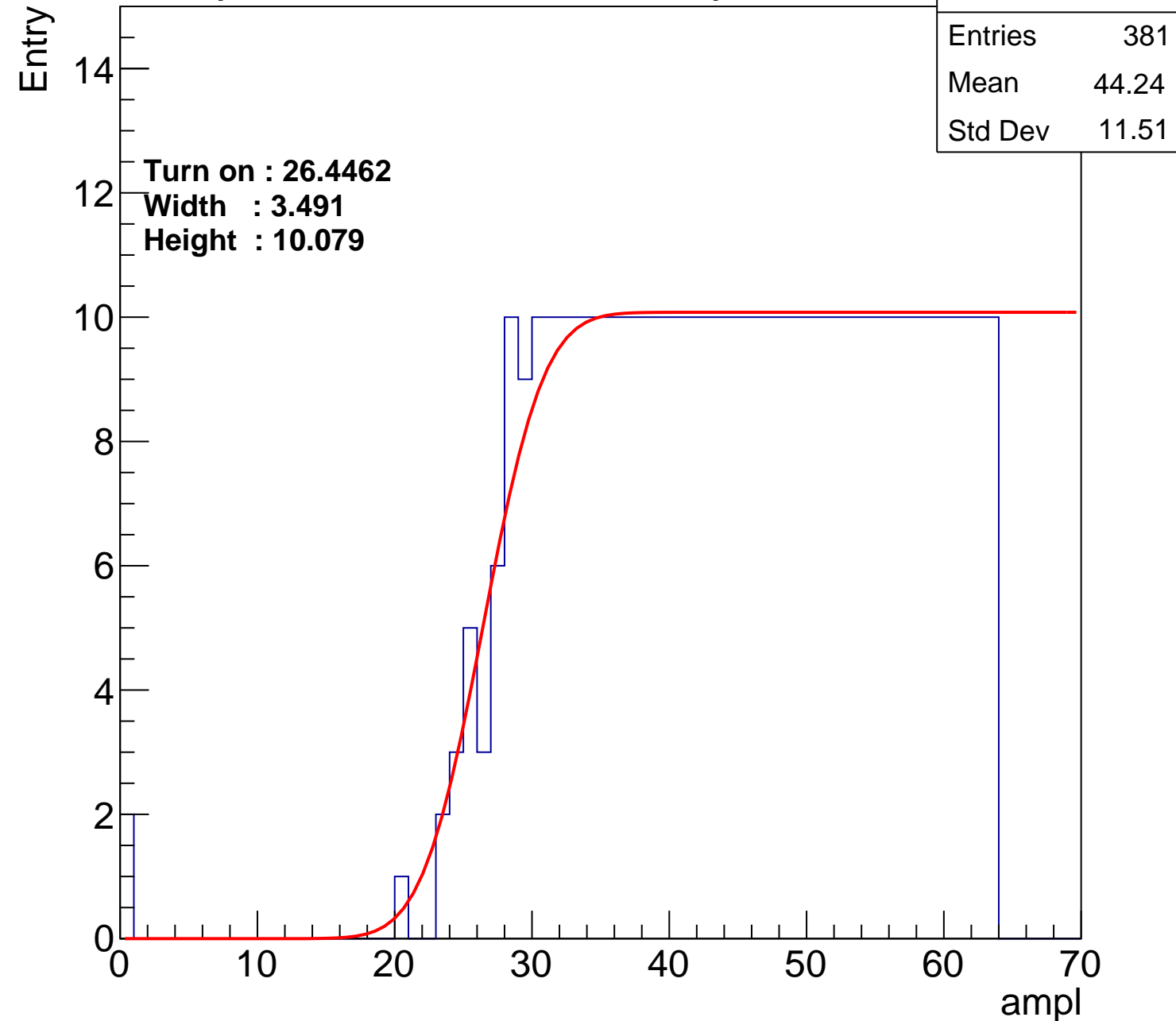
Width : 3.491

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.8
Std Dev	11.85

Turn on : 25.3710

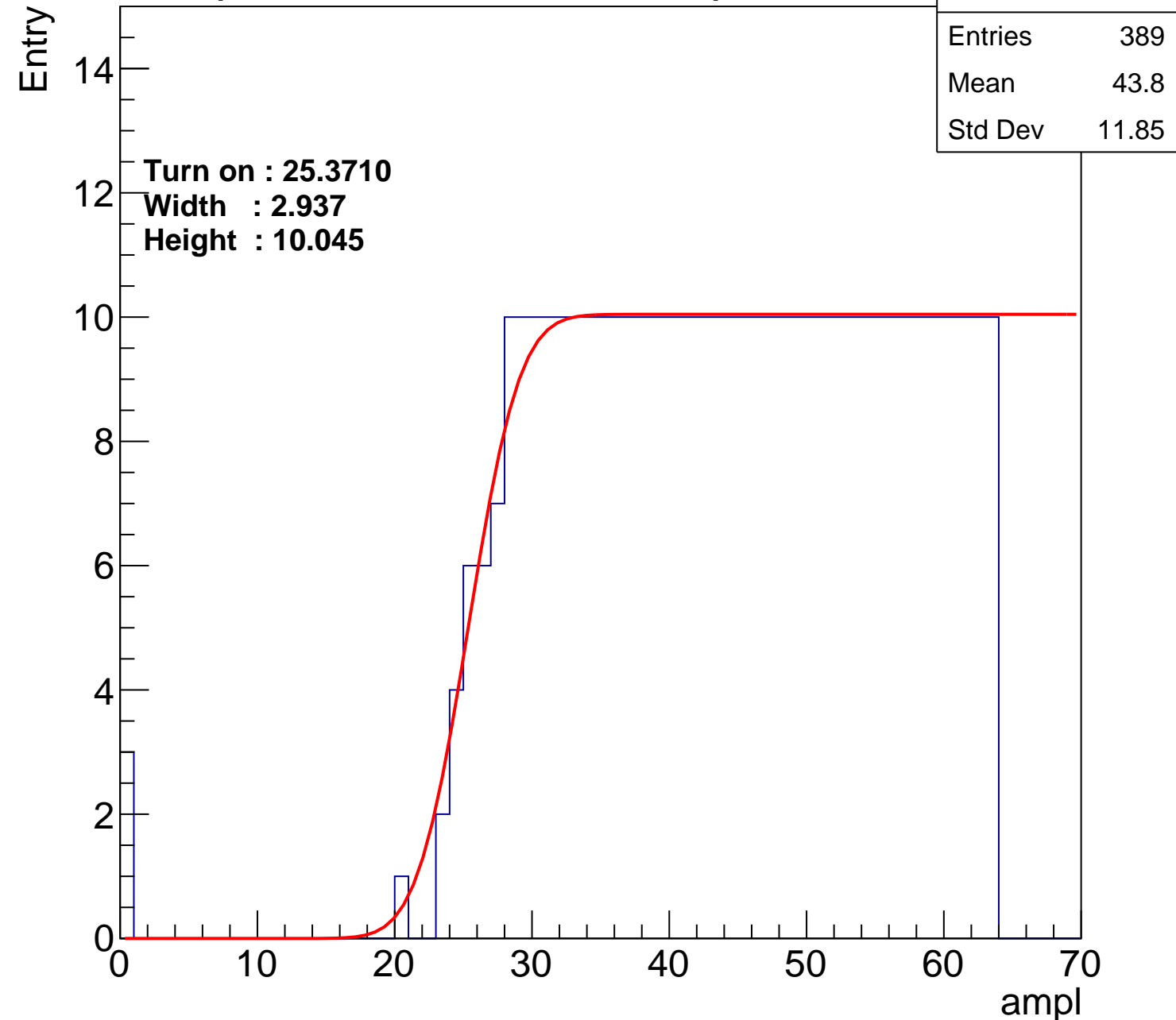
Width : 2.937

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.07
Std Dev	12.37

Turn on : 24.4332

Width : 2.196

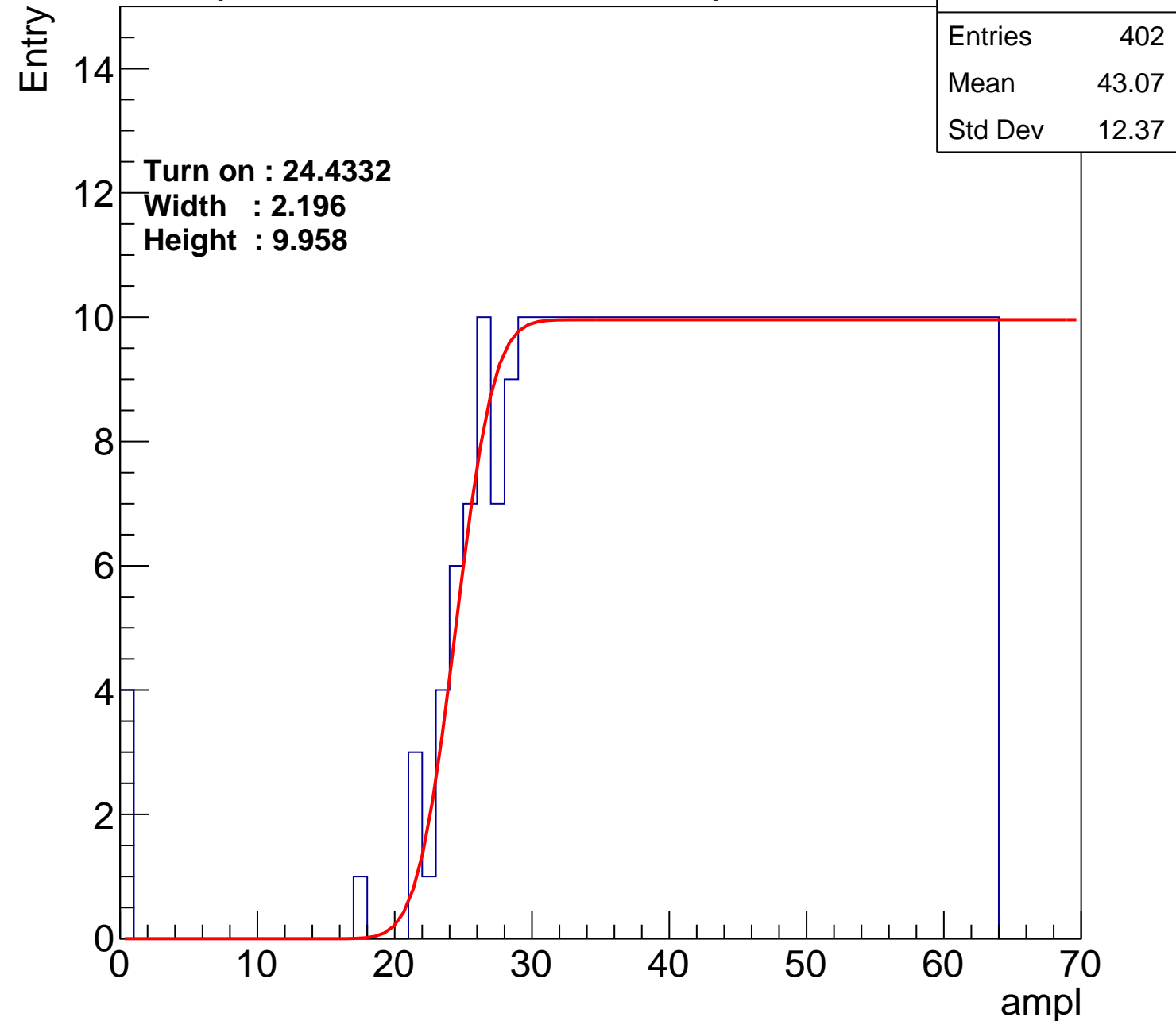
Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U24-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.46
Std Dev	11.28

Turn on : 27.0189

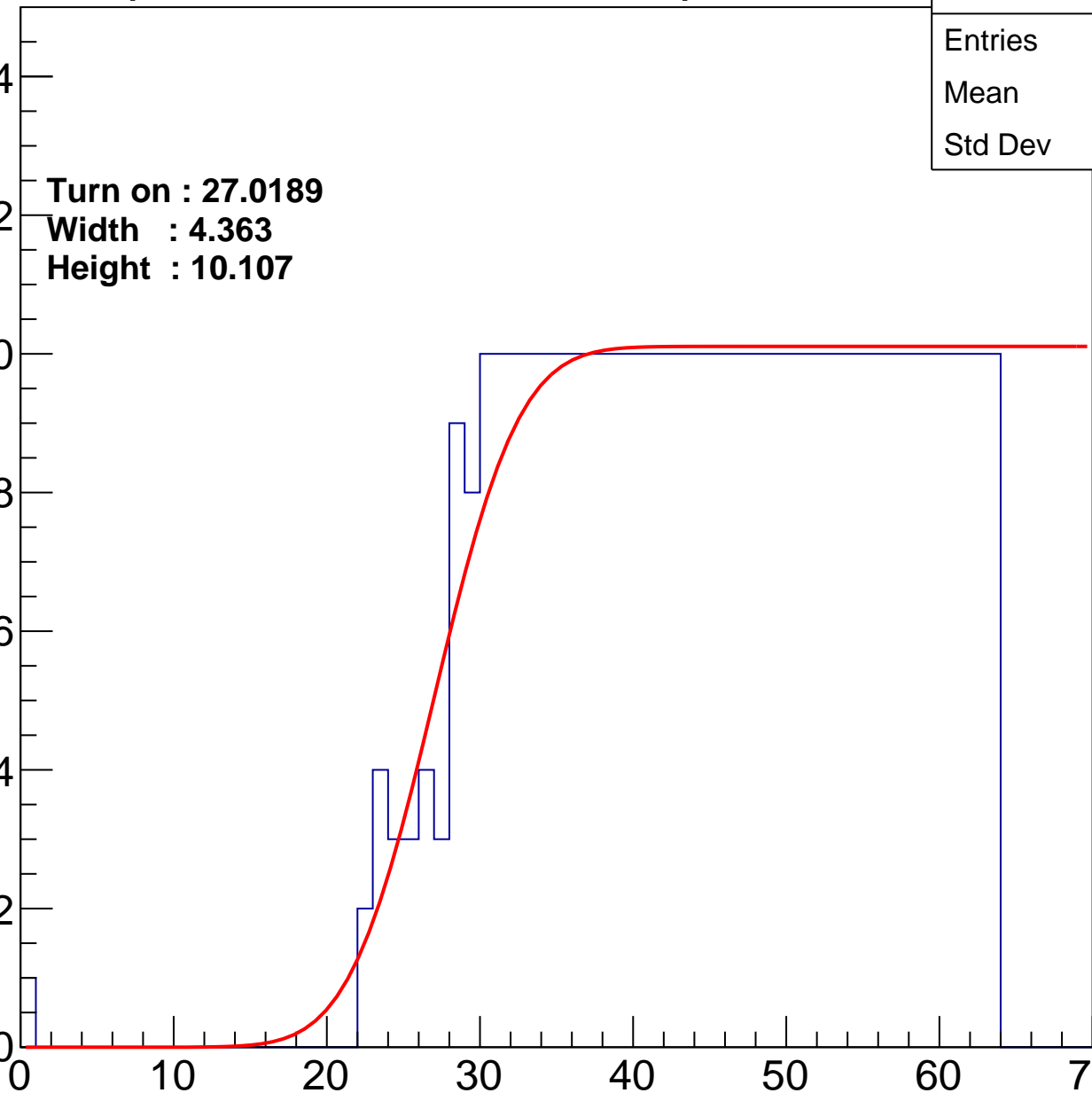
Width : 4.363

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.43
Std Dev	11.9

Turn on : 24.8199

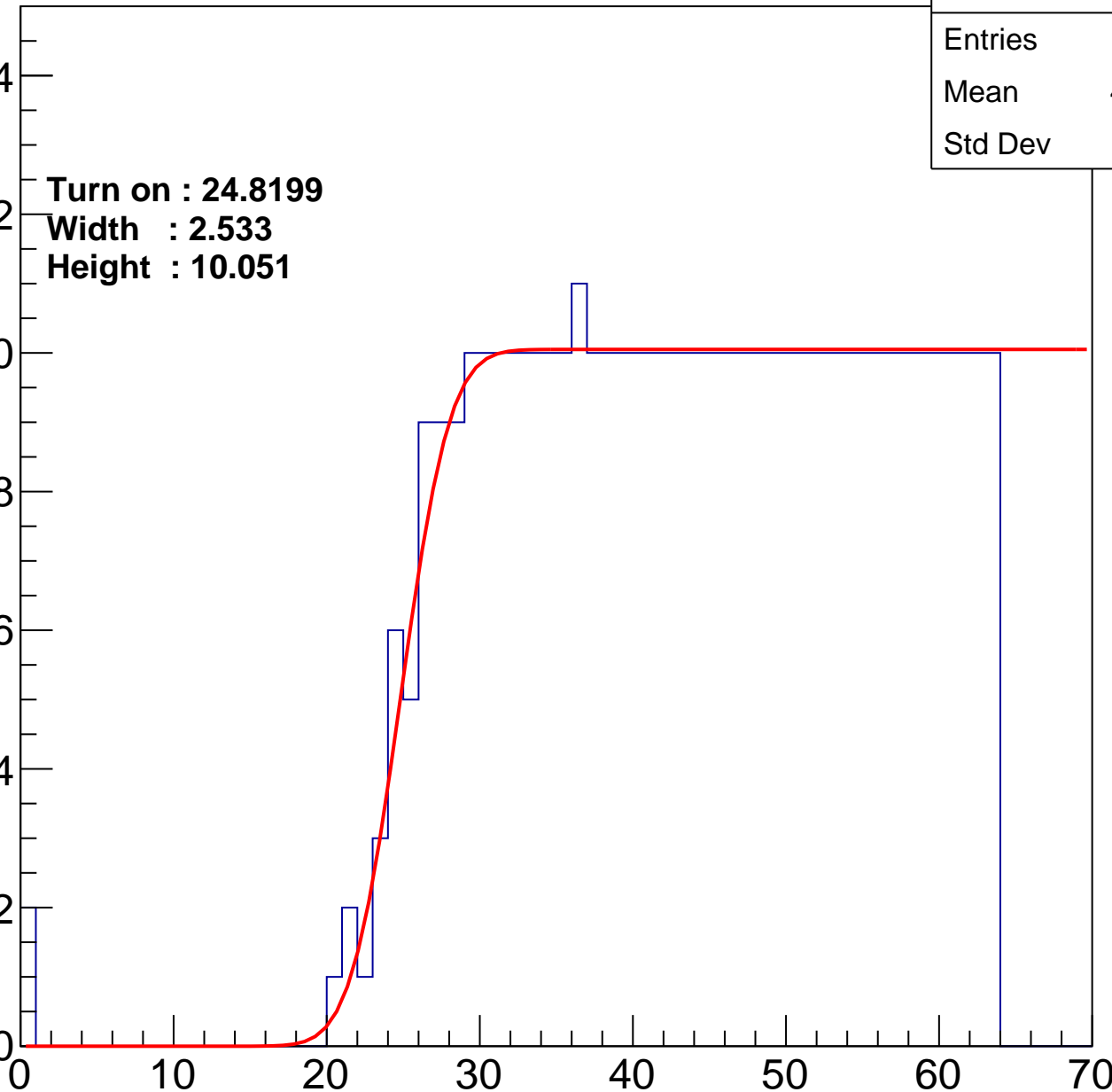
Width : 2.533

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.72
Std Dev	11.81

Turn on : 25.3284

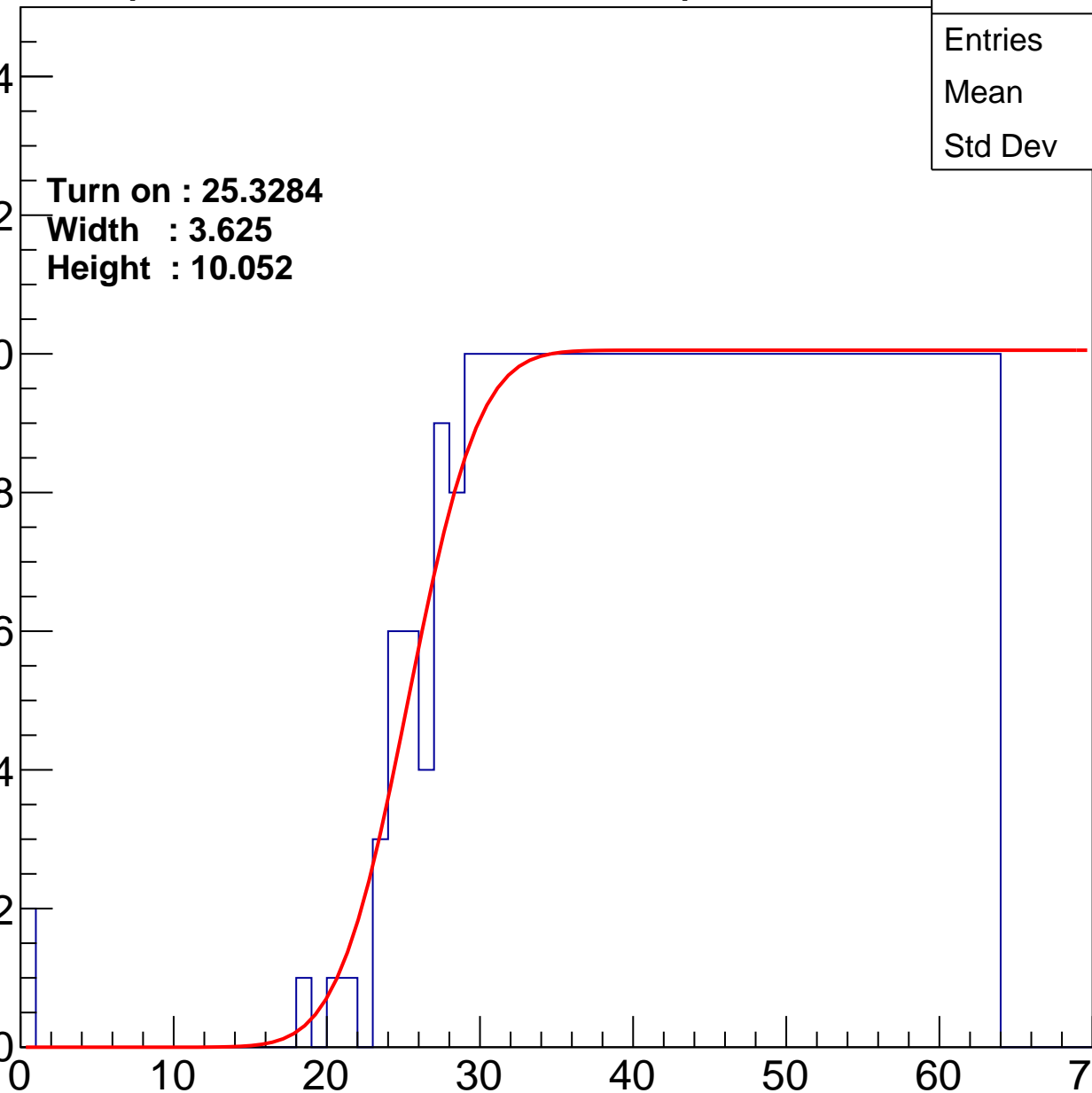
Width : 3.625

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.2523

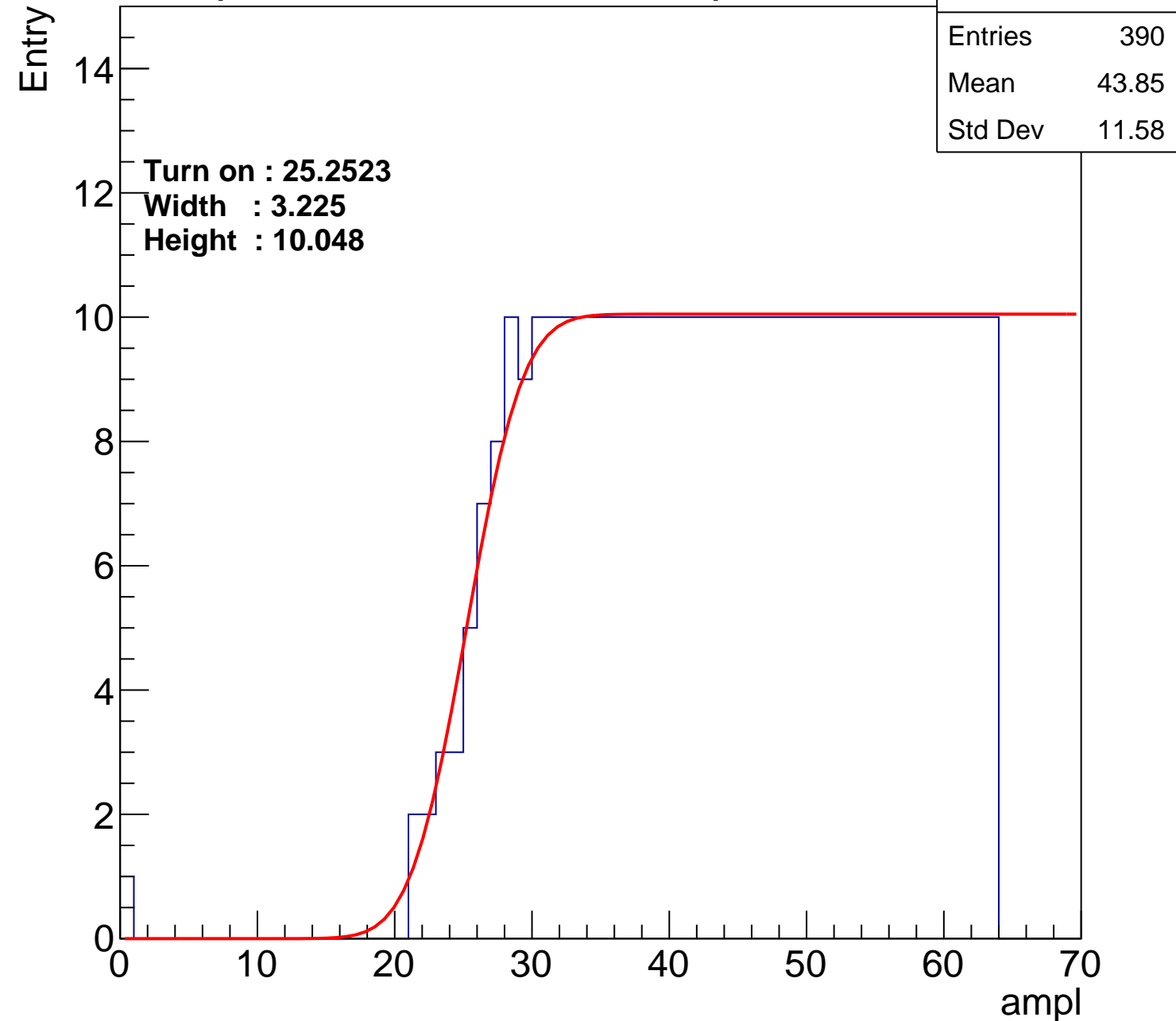
Width : 3.225

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch40

calib_packv5_042523_0143.root, FC#10, port B3

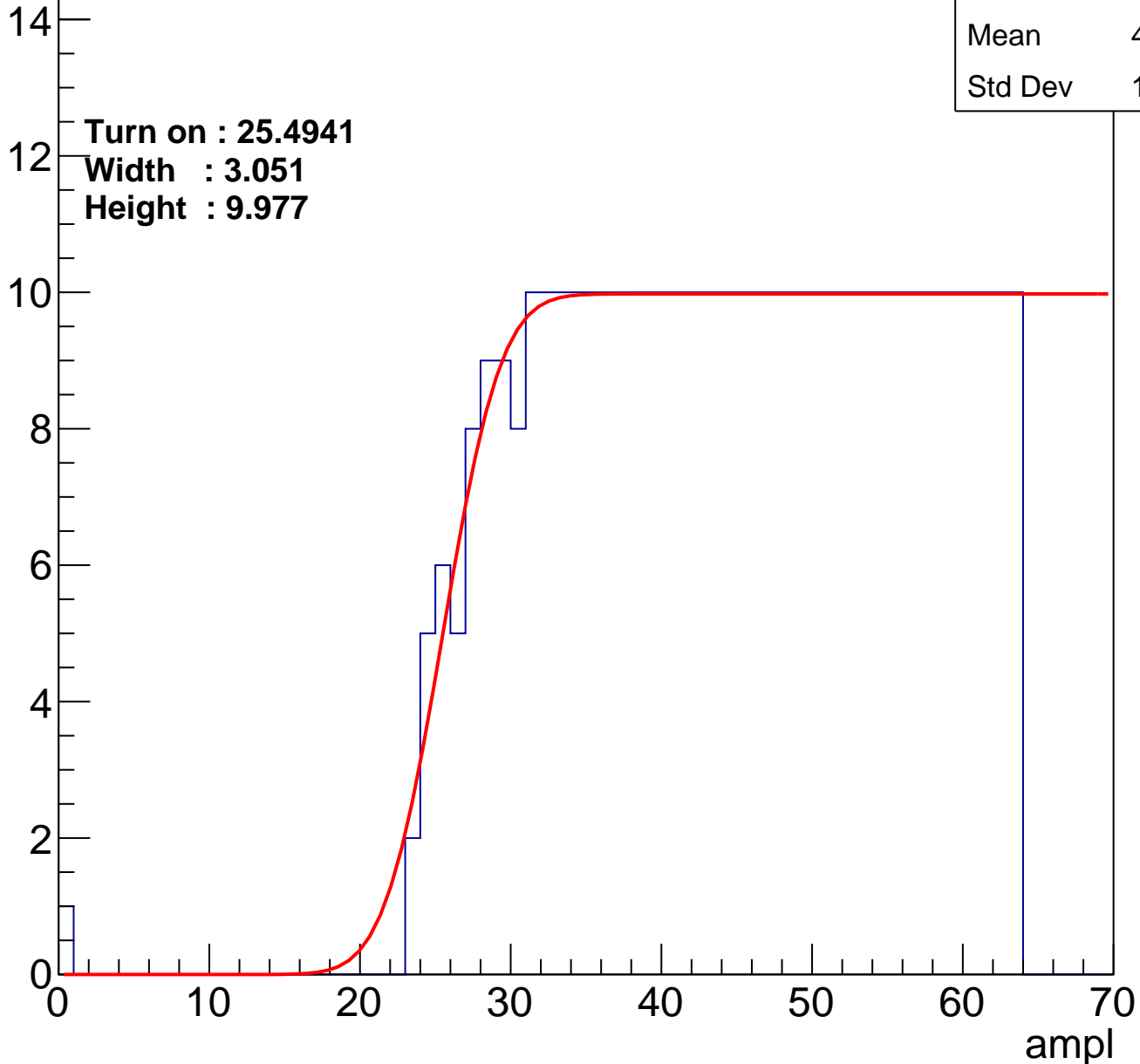
Entries	383
Mean	44.19
Std Dev	11.39

Turn on : 25.4941

Width : 3.051

Height : 9.977

Entry



B1L002S, U24-ch41

calib_packv5_042523_0143.root, FC#10, port B3

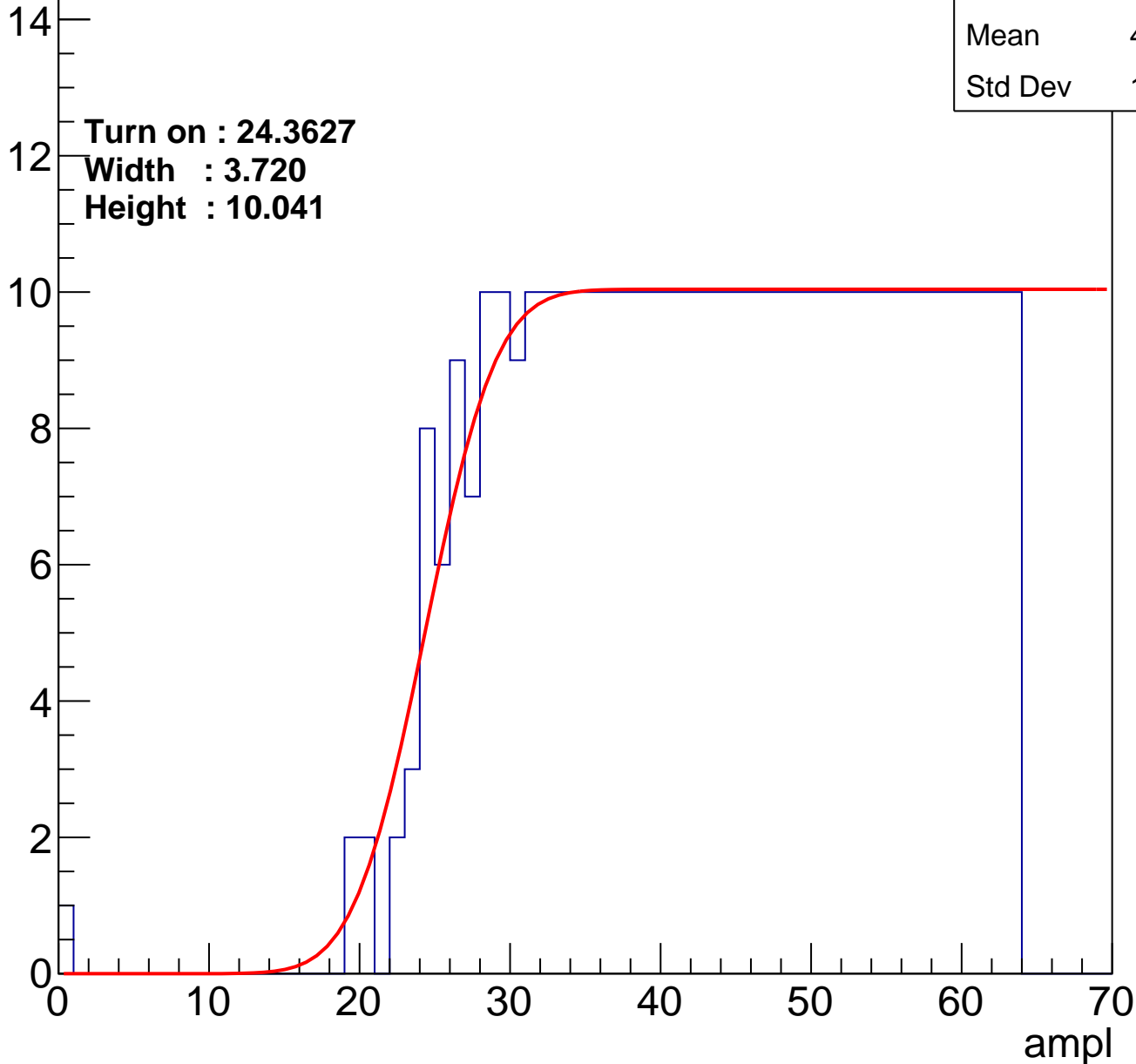
Entries	399
Mean	43.37
Std Dev	11.87

Turn on : 24.3627

Width : 3.720

Height : 10.041

Entry



B1L002S, U24-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.01
Std Dev	12.46

Turn on : 24.1777

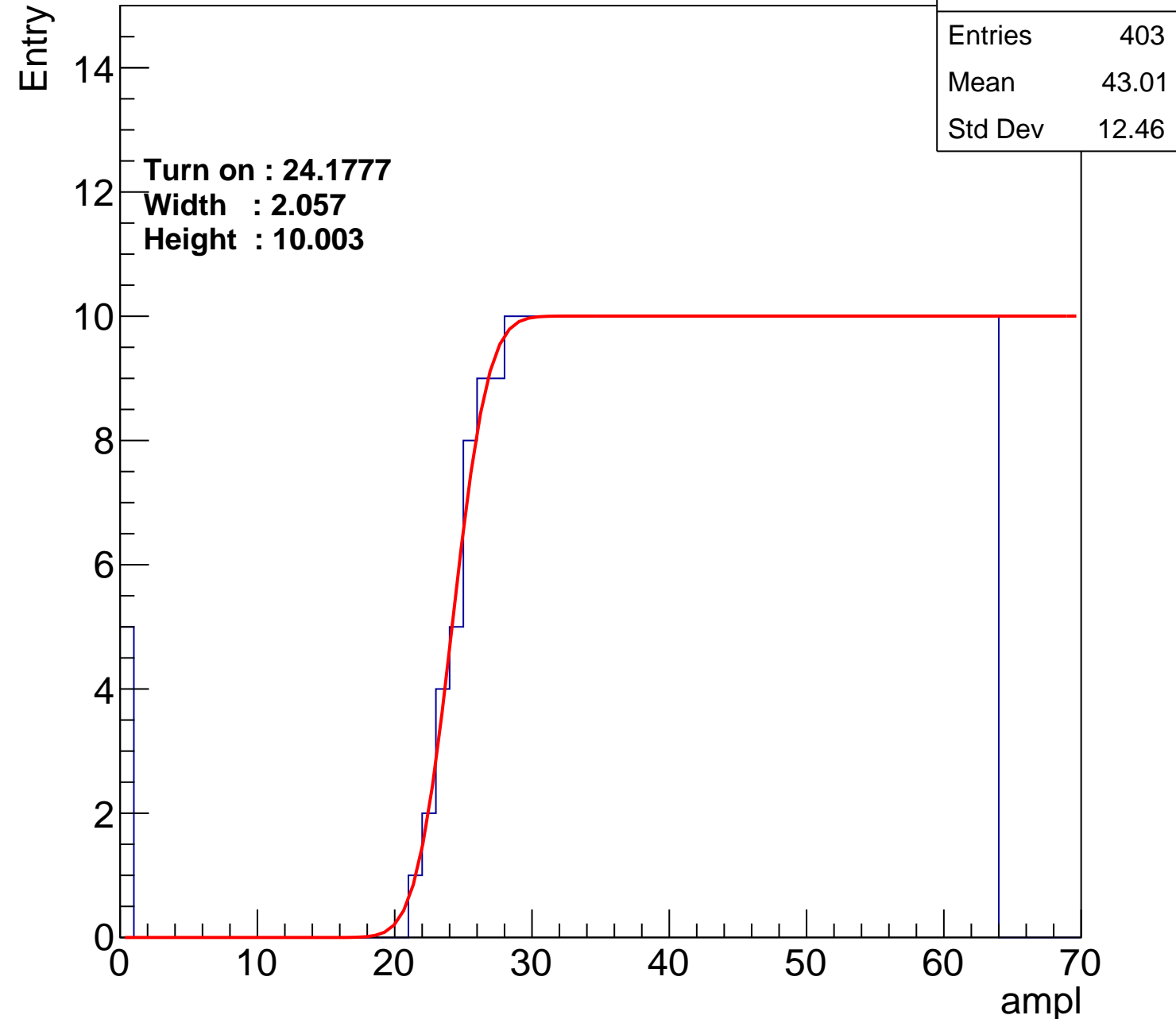
Width : 2.057

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch43

calib_packv5_042523_0143.root, FC#10, port B3

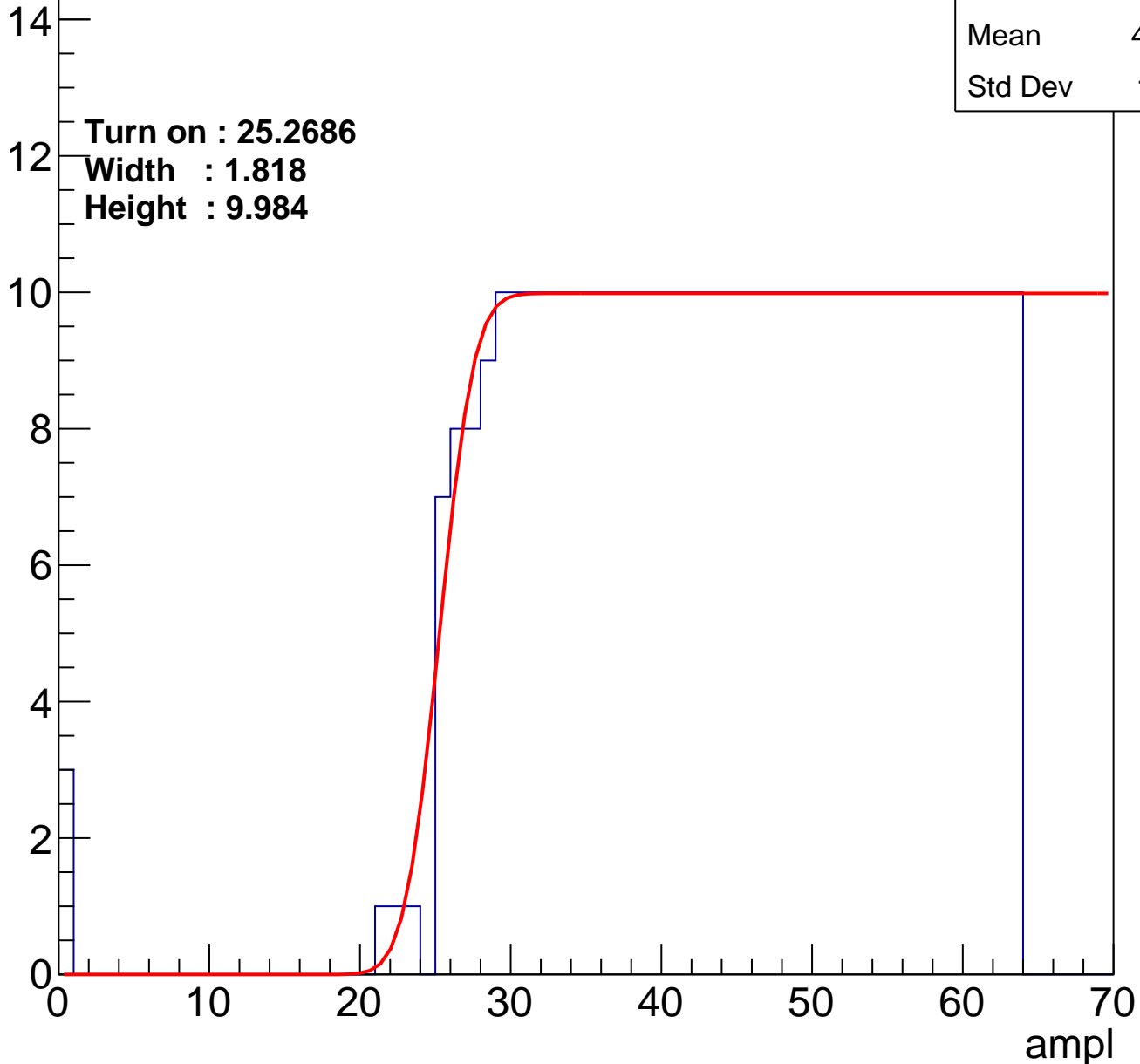
Entries	388
Mean	43.86
Std Dev	11.81

Turn on : 25.2686

Width : 1.818

Height : 9.984

Entry



B1L002S, U24-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.46
Std Dev	11.9

Turn on : 24.6053

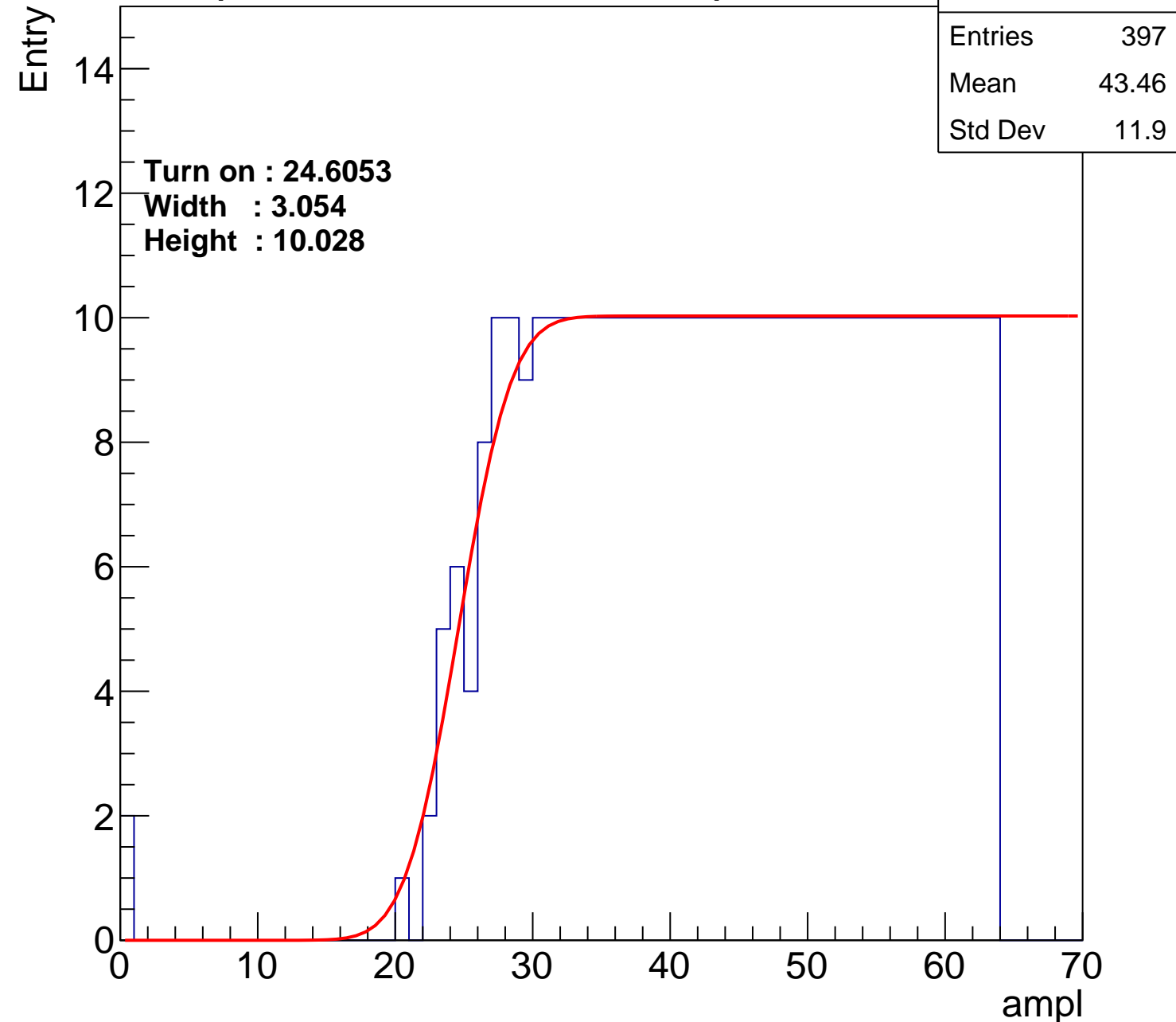
Width : 3.054

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch45

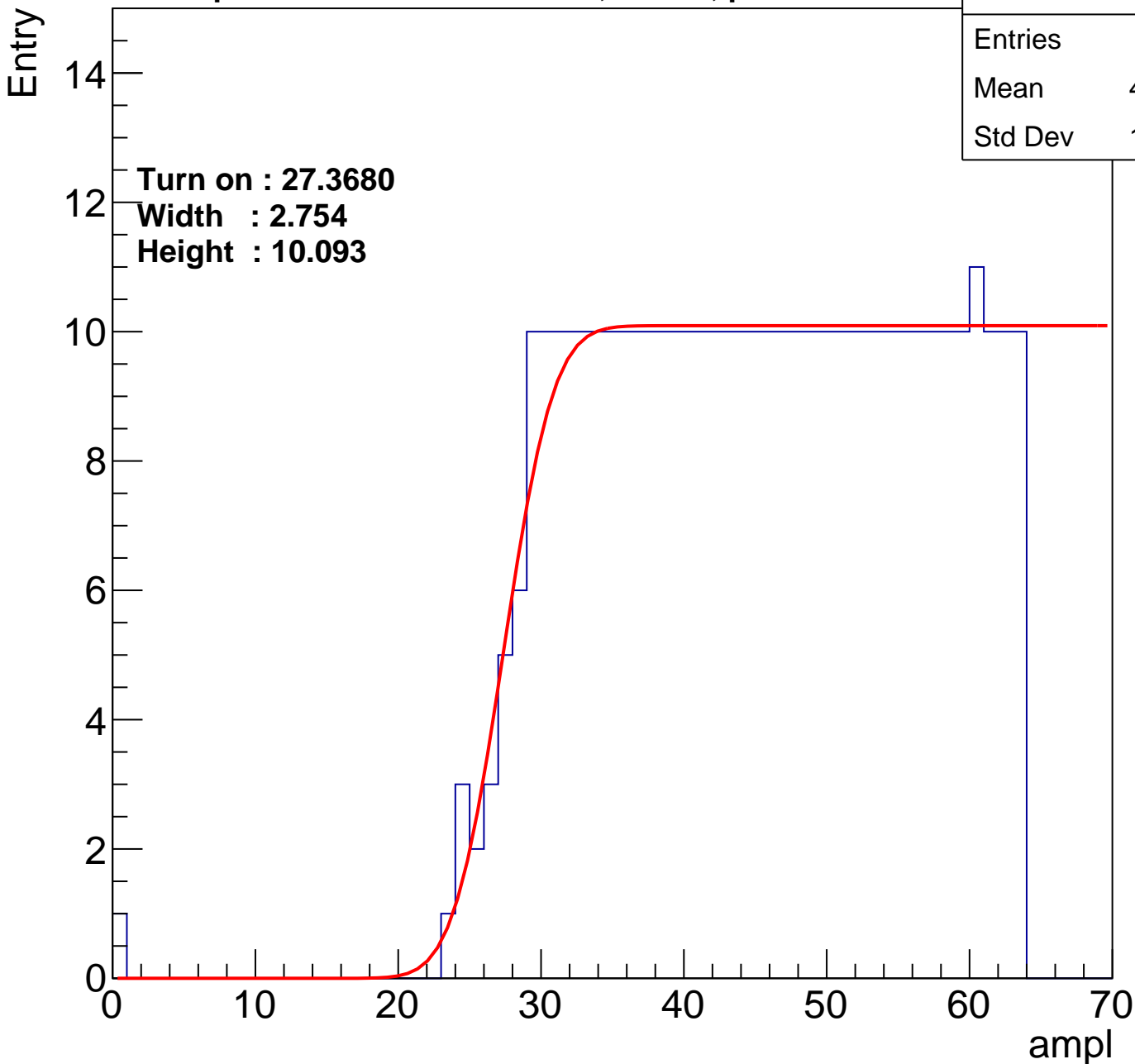
calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.85
Std Dev	11.04

Turn on : 27.3680

Width : 2.754

Height : 10.093



B1L002S, U24-ch46

calib_packv5_042523_0143.root, FC#10, port B3

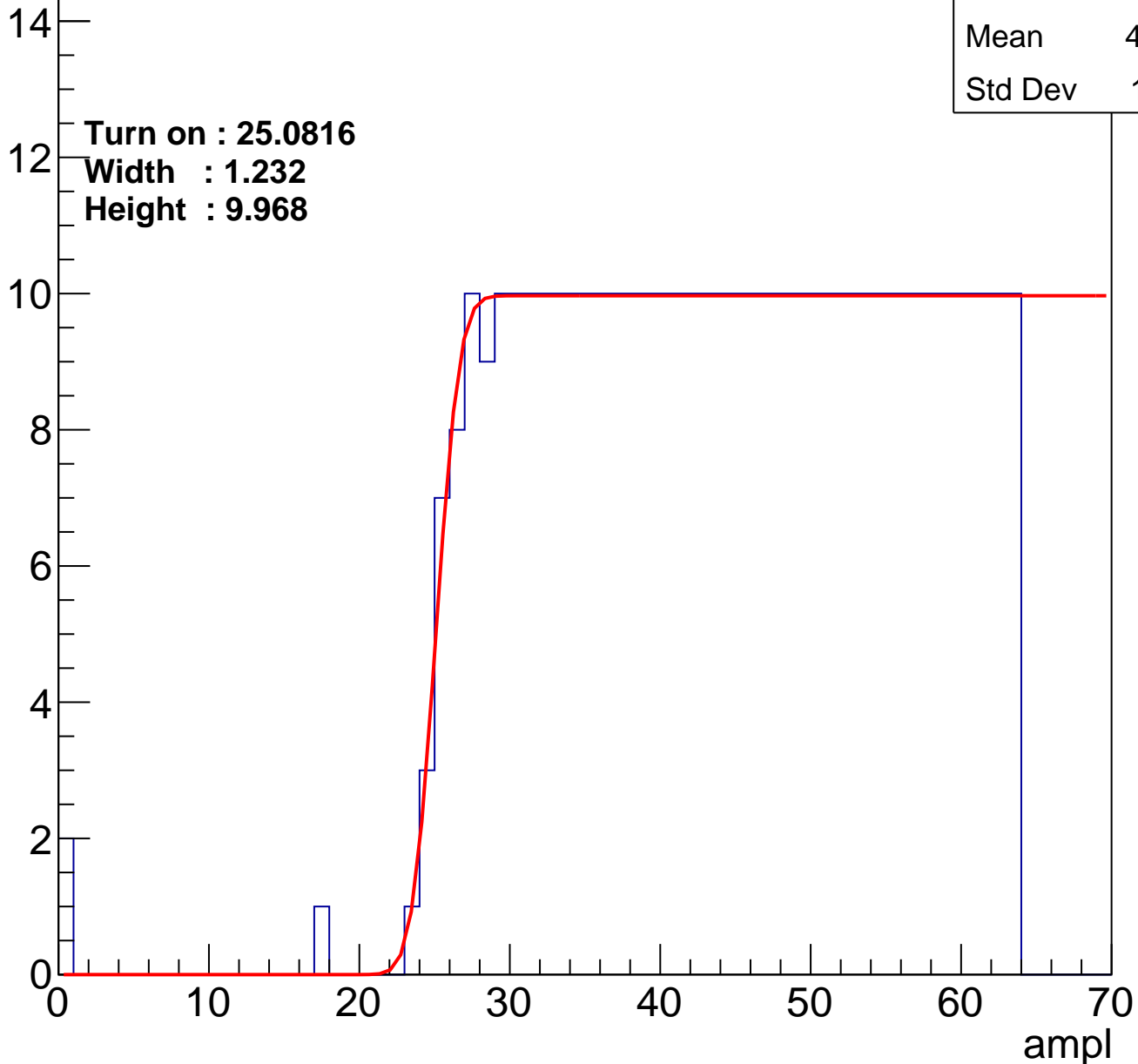
Entries	391
Mean	43.78
Std Dev	11.71

Turn on : 25.0816

Width : 1.232

Height : 9.968

Entry



B1L002S, U24-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.55
Std Dev	11.86

Turn on : 25.0049

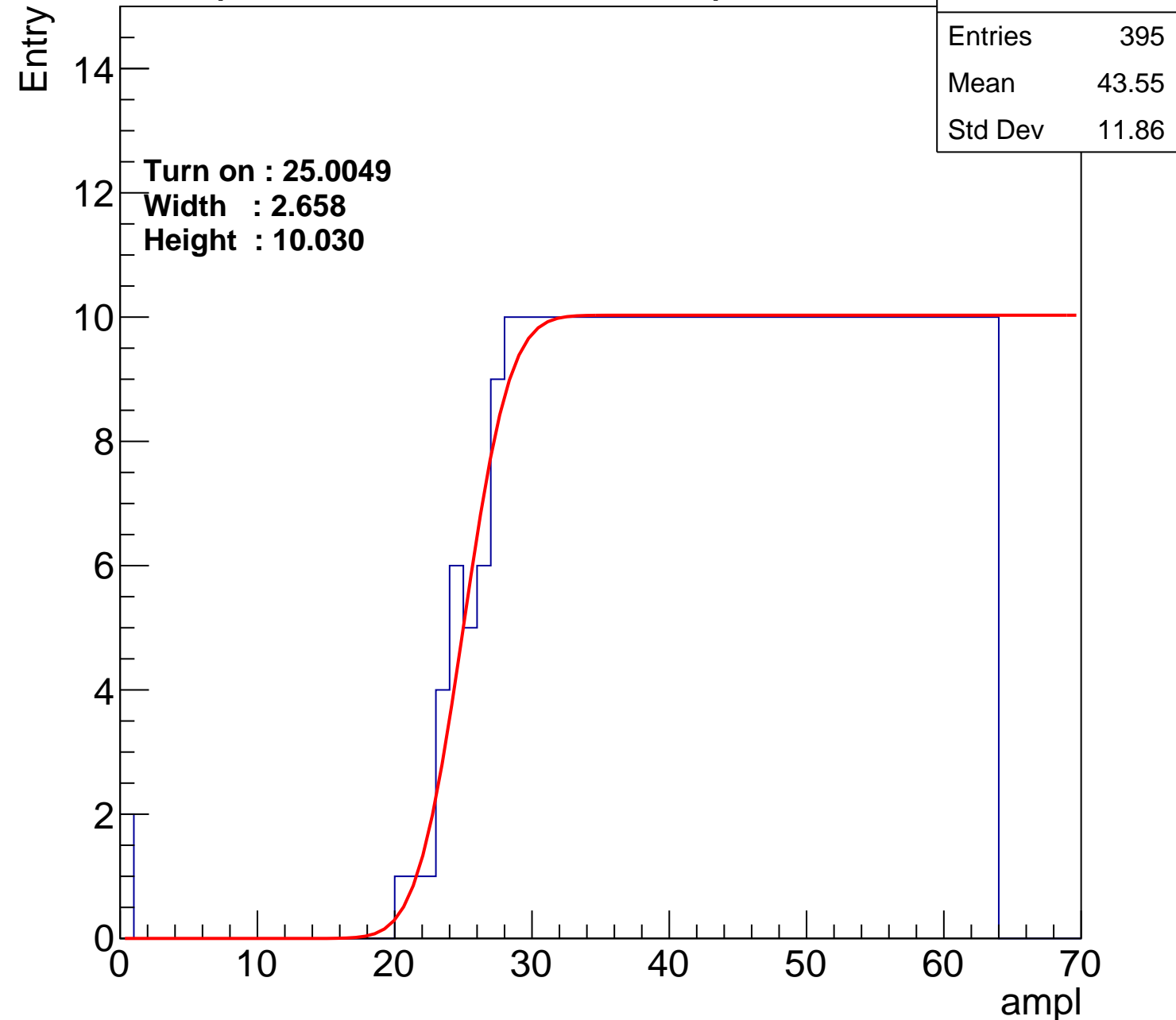
Width : 2.658

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch48

calib_packv5_042523_0143.root, FC#10, port B3

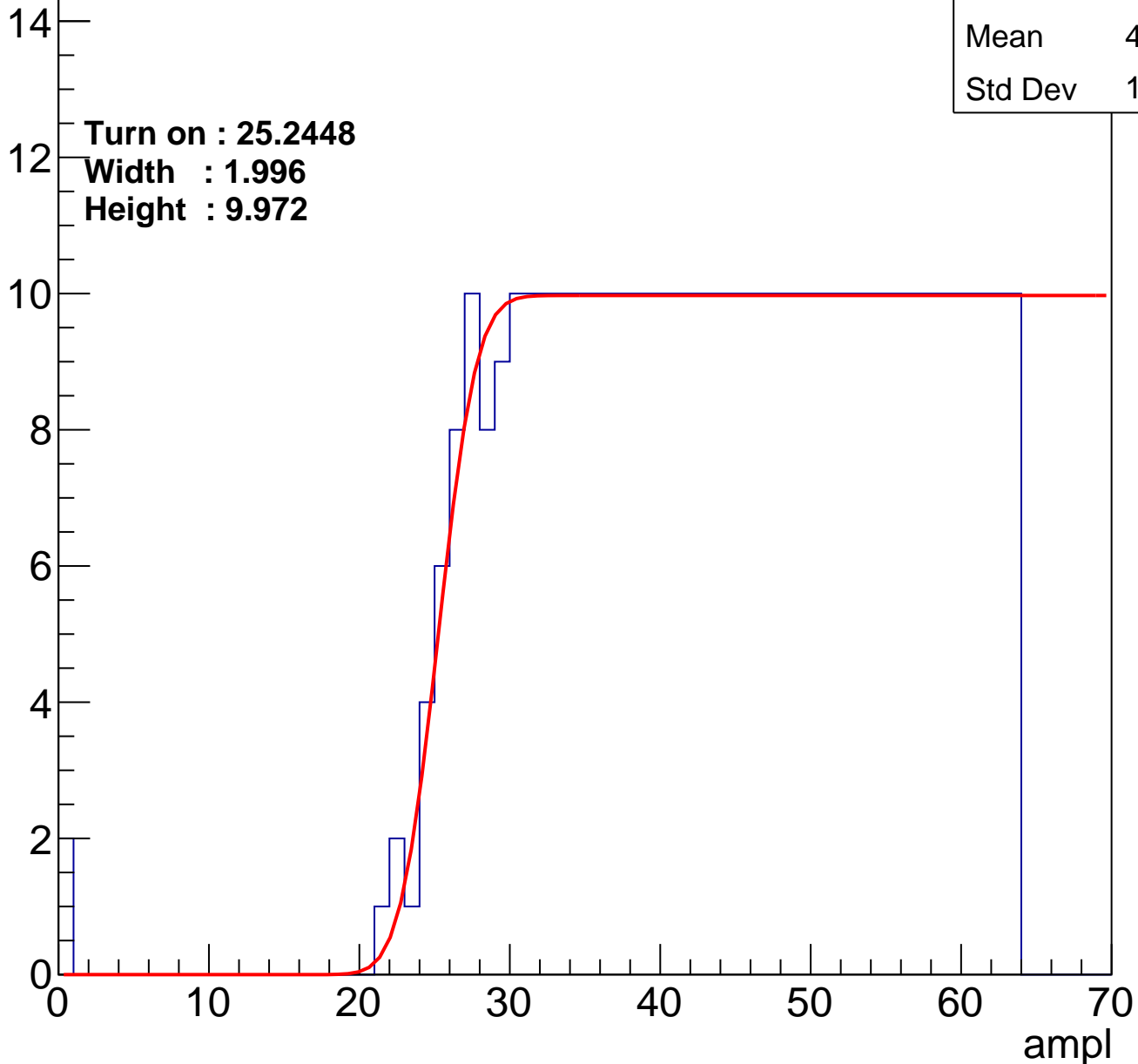
Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.2448

Width : 1.996

Height : 9.972

Entry



B1L002S, U24-ch49

calib_packv5_042523_0143.root, FC#10, port B3

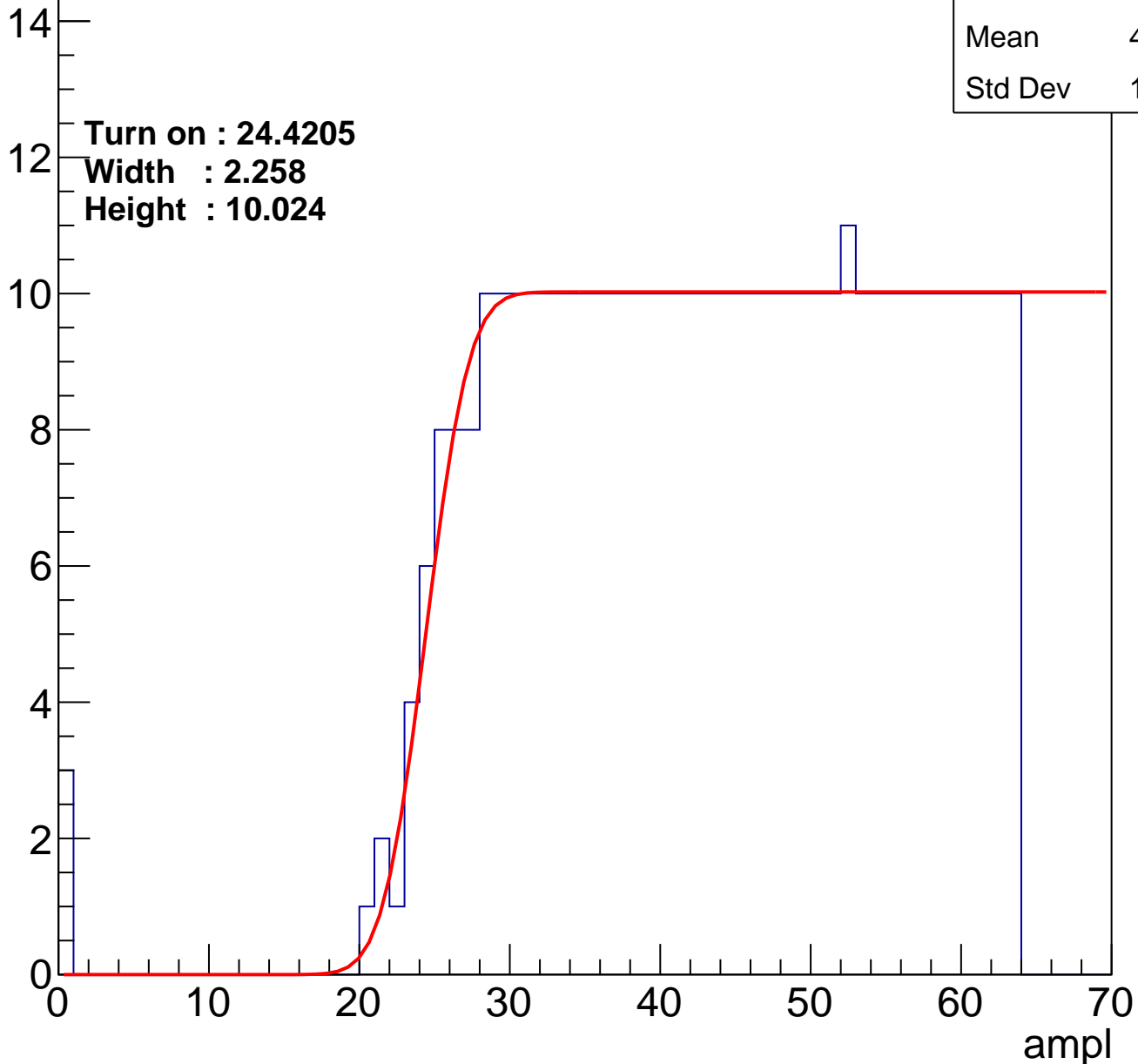
Entries	402
Mean	43.22
Std Dev	12.15

Turn on : 24.4205

Width : 2.258

Height : 10.024

Entry



B1L002S, U24-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.3641

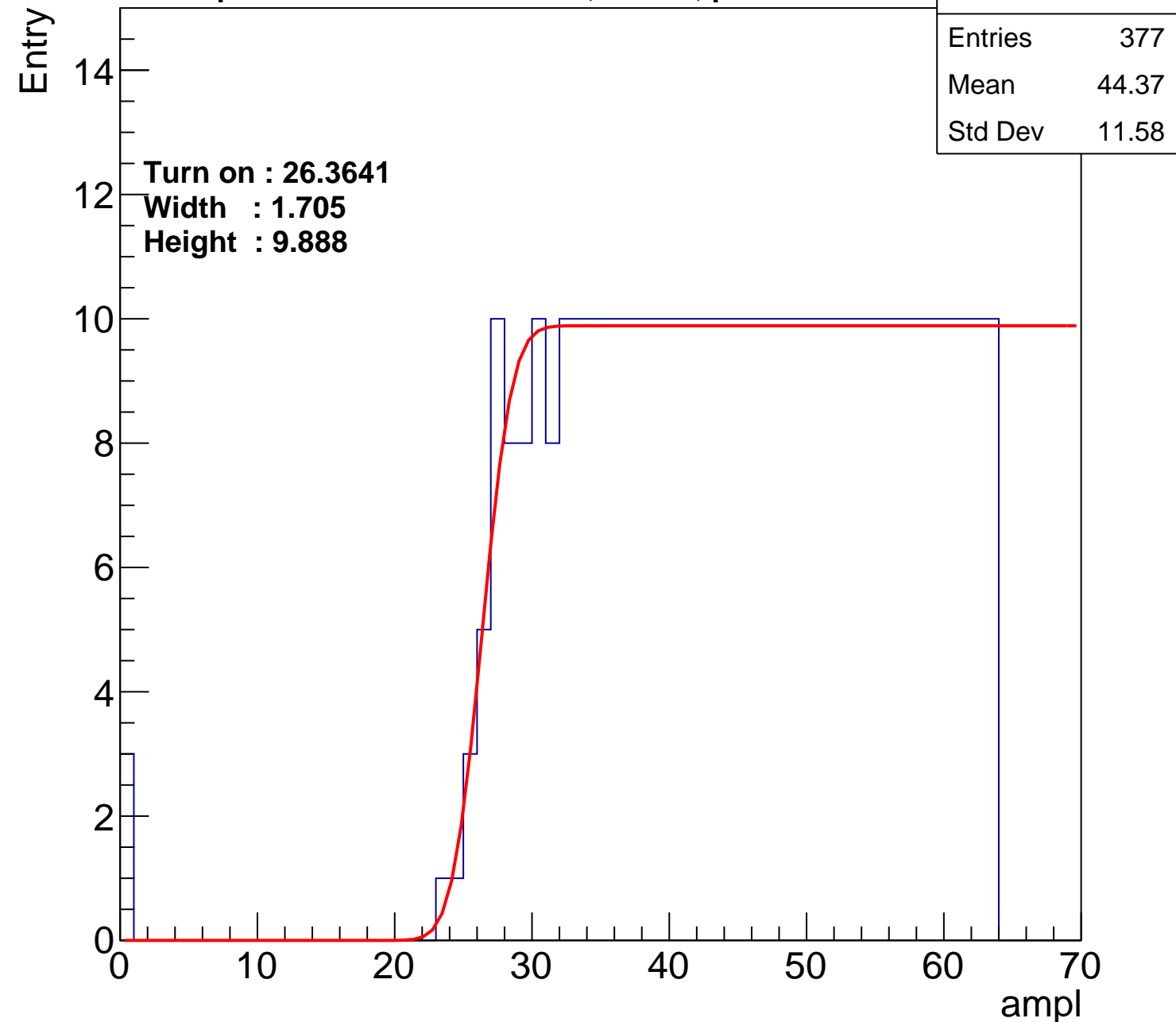
Width : 1.705

Height : 9.888

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 25.5054

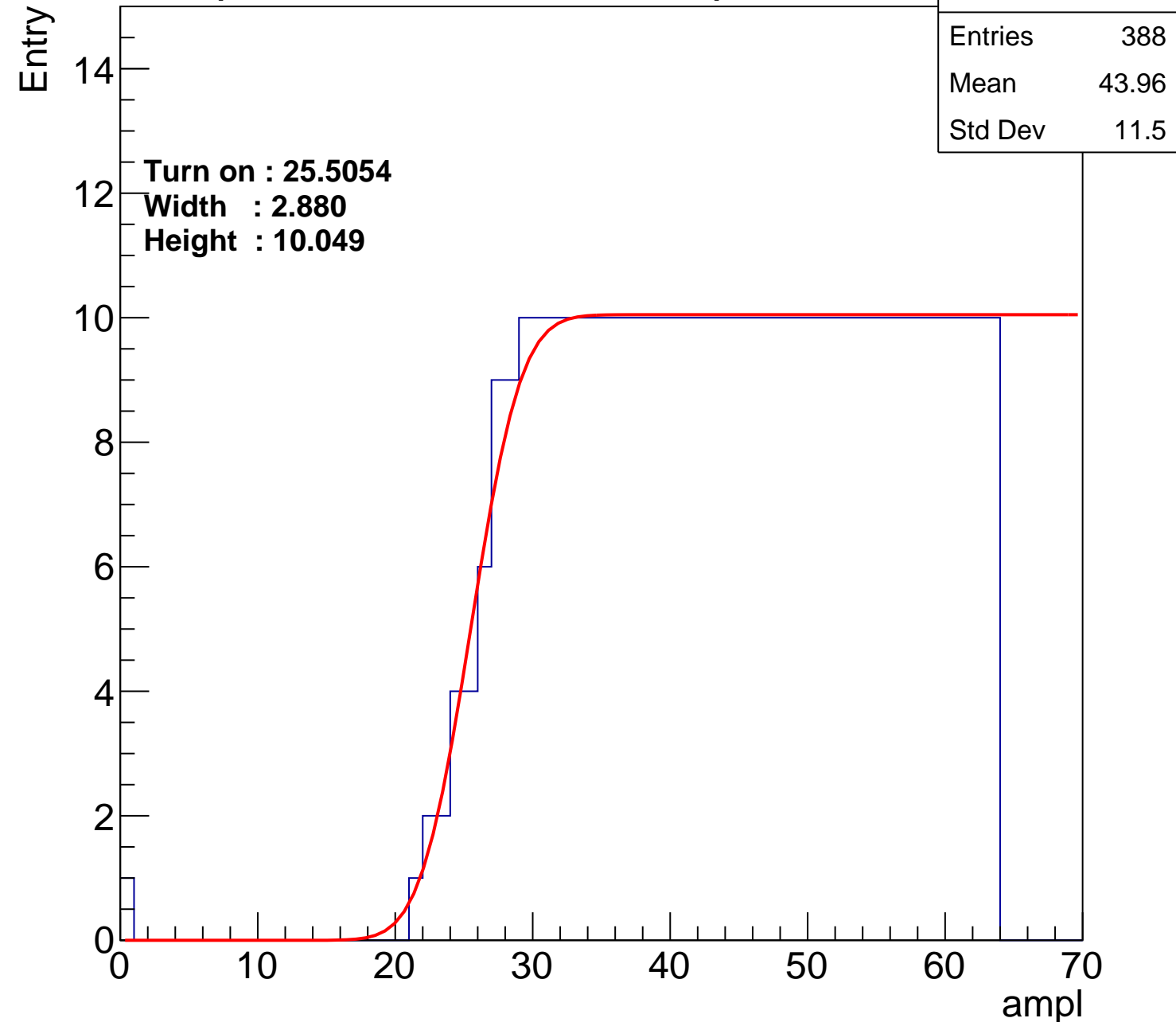
Width : 2.880

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.7291

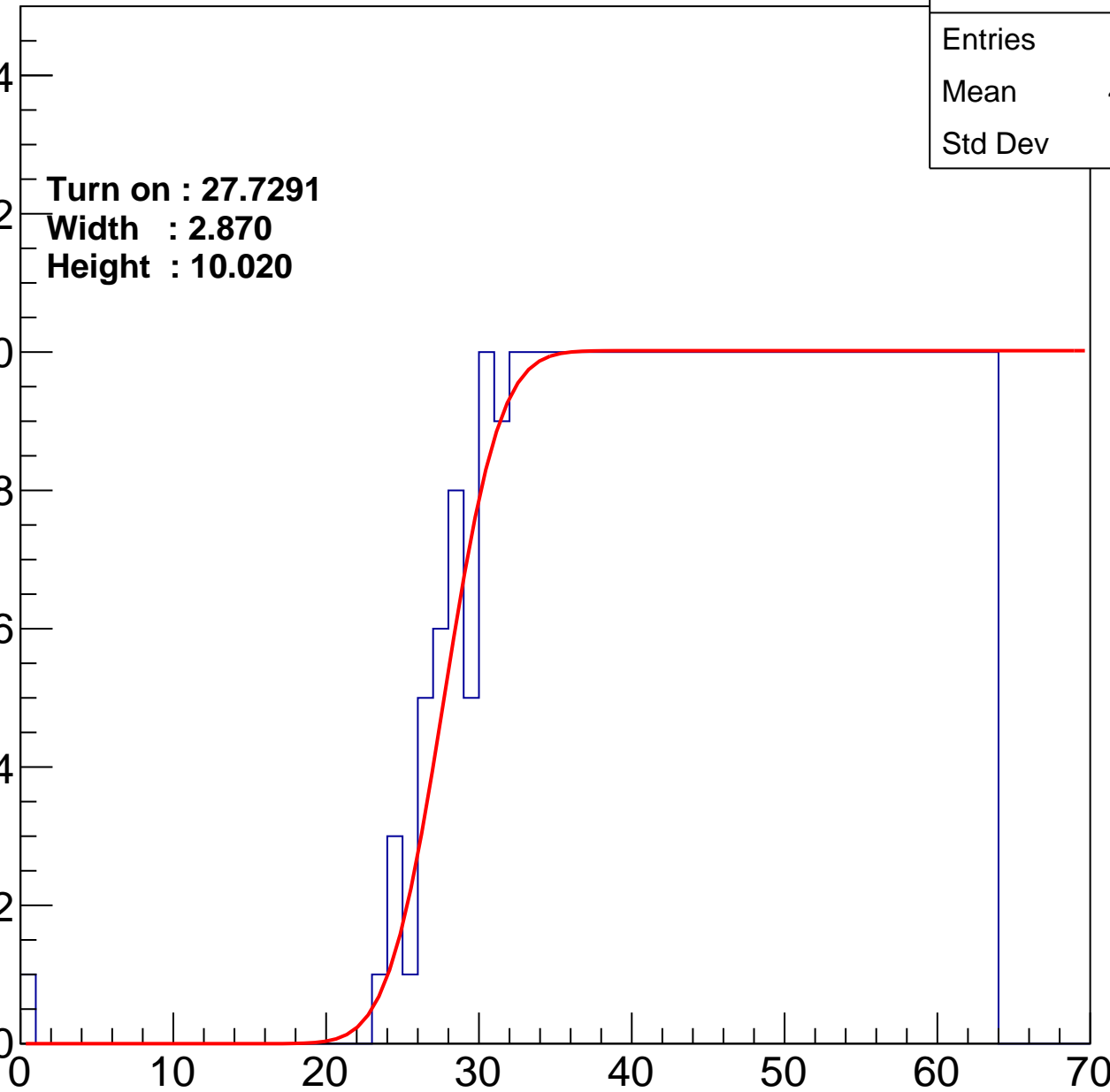
Width : 2.870

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch53

calib_packv5_042523_0143.root, FC#10, port B3

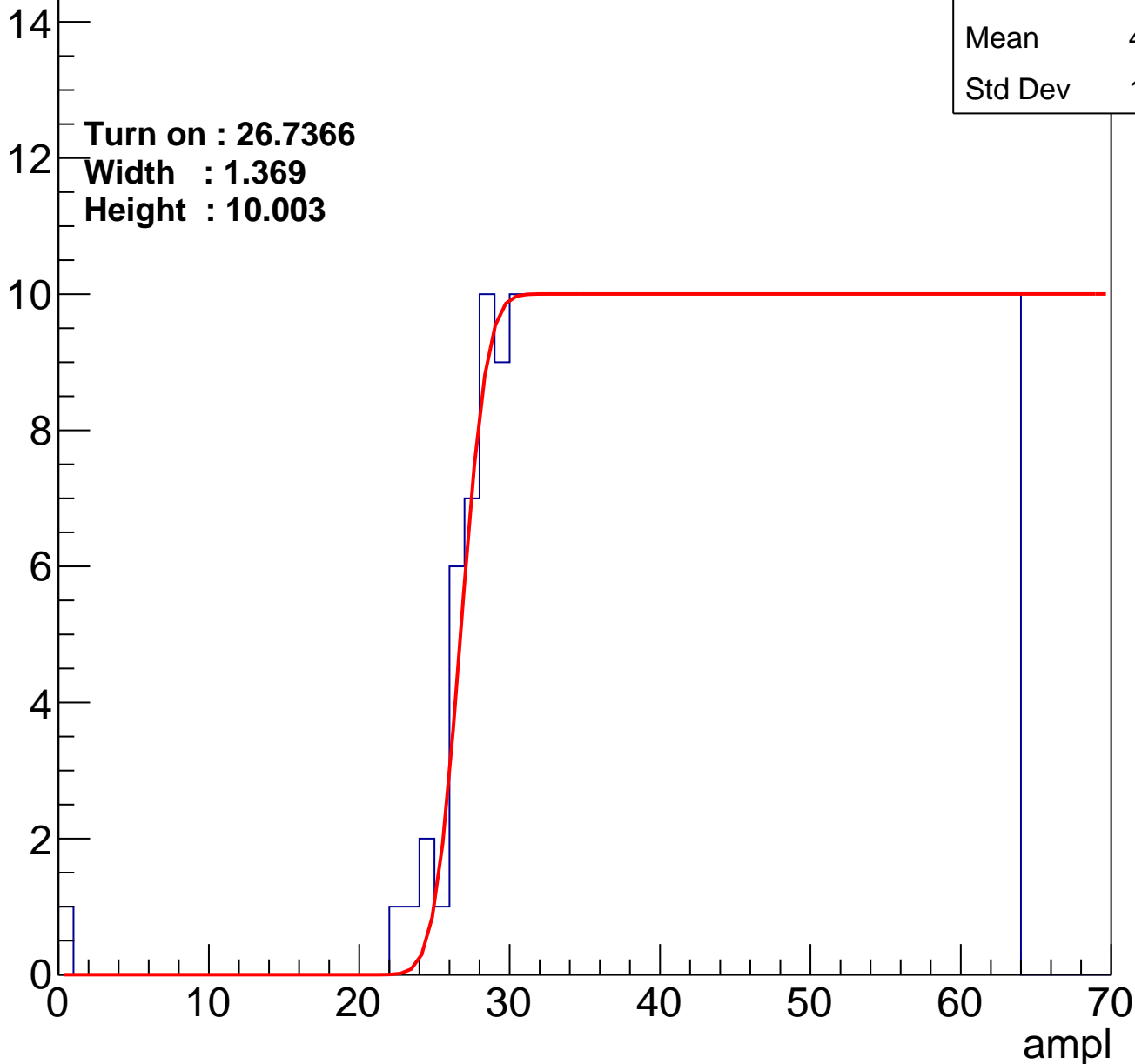
Entry

Entries	378
Mean	44.48
Std Dev	11.19

Turn on : 26.7366

Width : 1.369

Height : 10.003



B1L002S, U24-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.46
Std Dev	11.59

Turn on : 27.0234

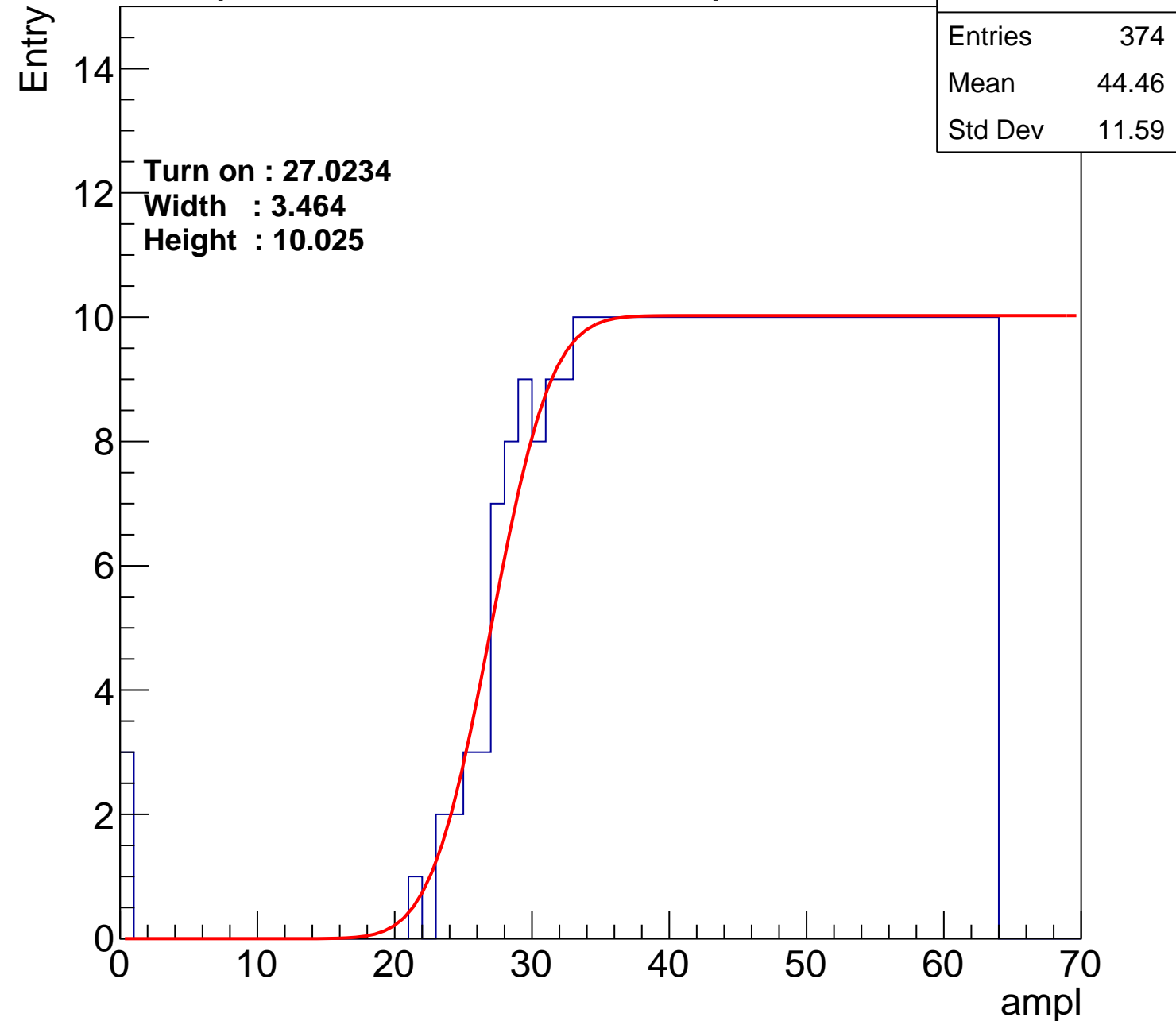
Width : 3.464

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch55

calib_packv5_042523_0143.root, FC#10, port B3

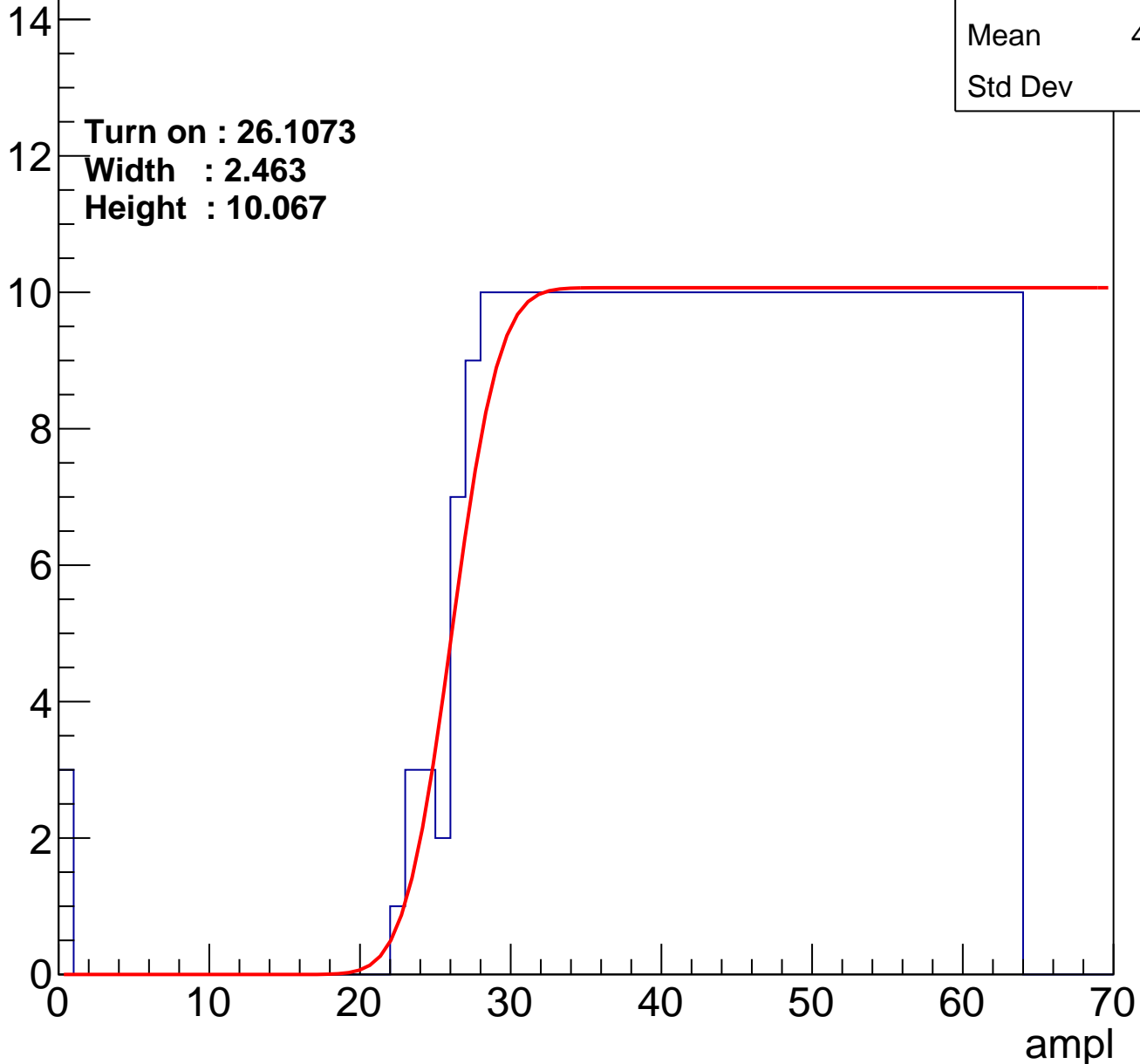
Entries	388
Mean	43.86
Std Dev	11.8

Turn on : 26.1073

Width : 2.463

Height : 10.067

Entry



B1L002S, U24-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.53
Std Dev	11.72

Turn on : 25.0712

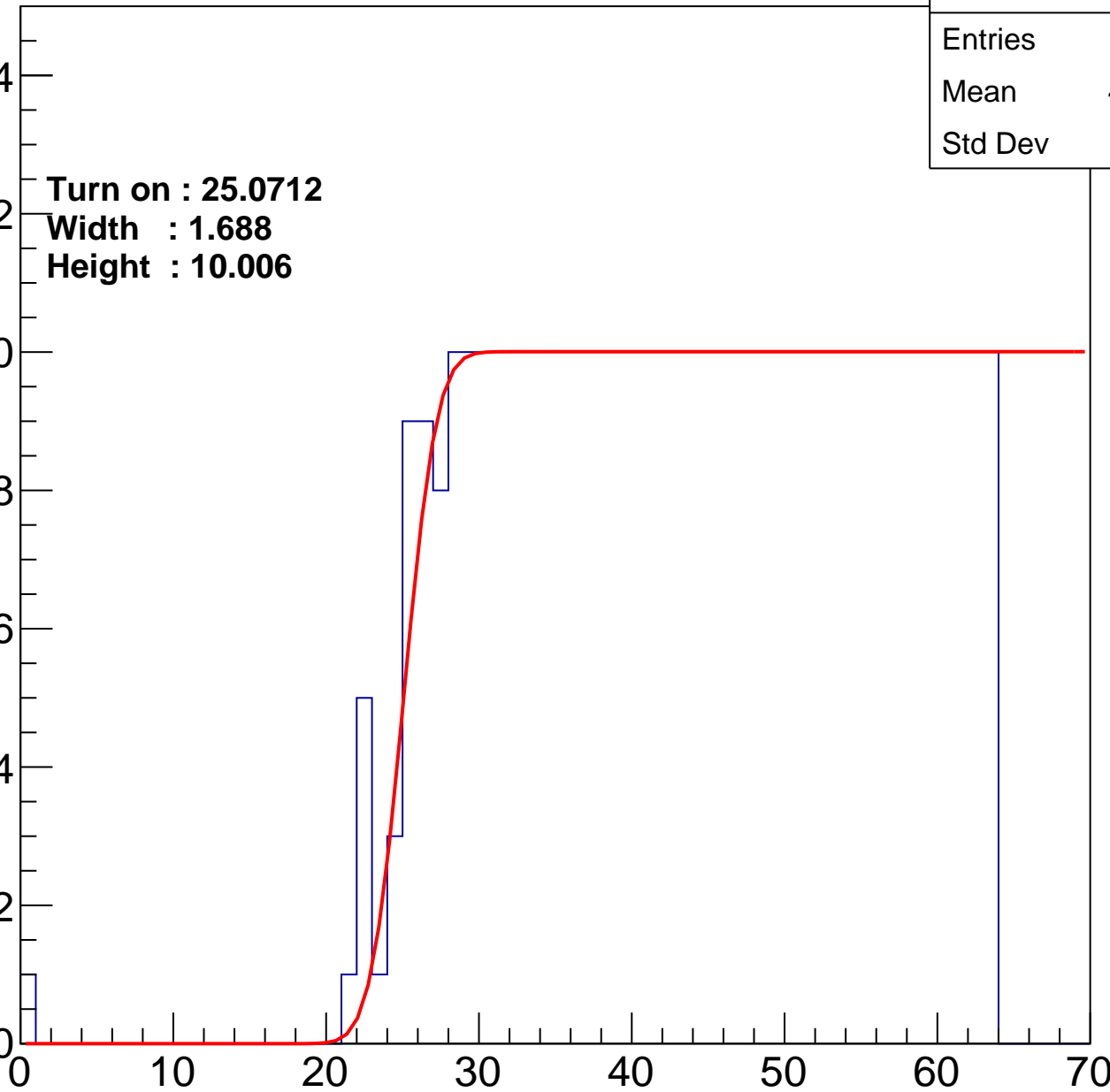
Width : 1.688

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.52
Std Dev	11.19

Turn on : 26.7294

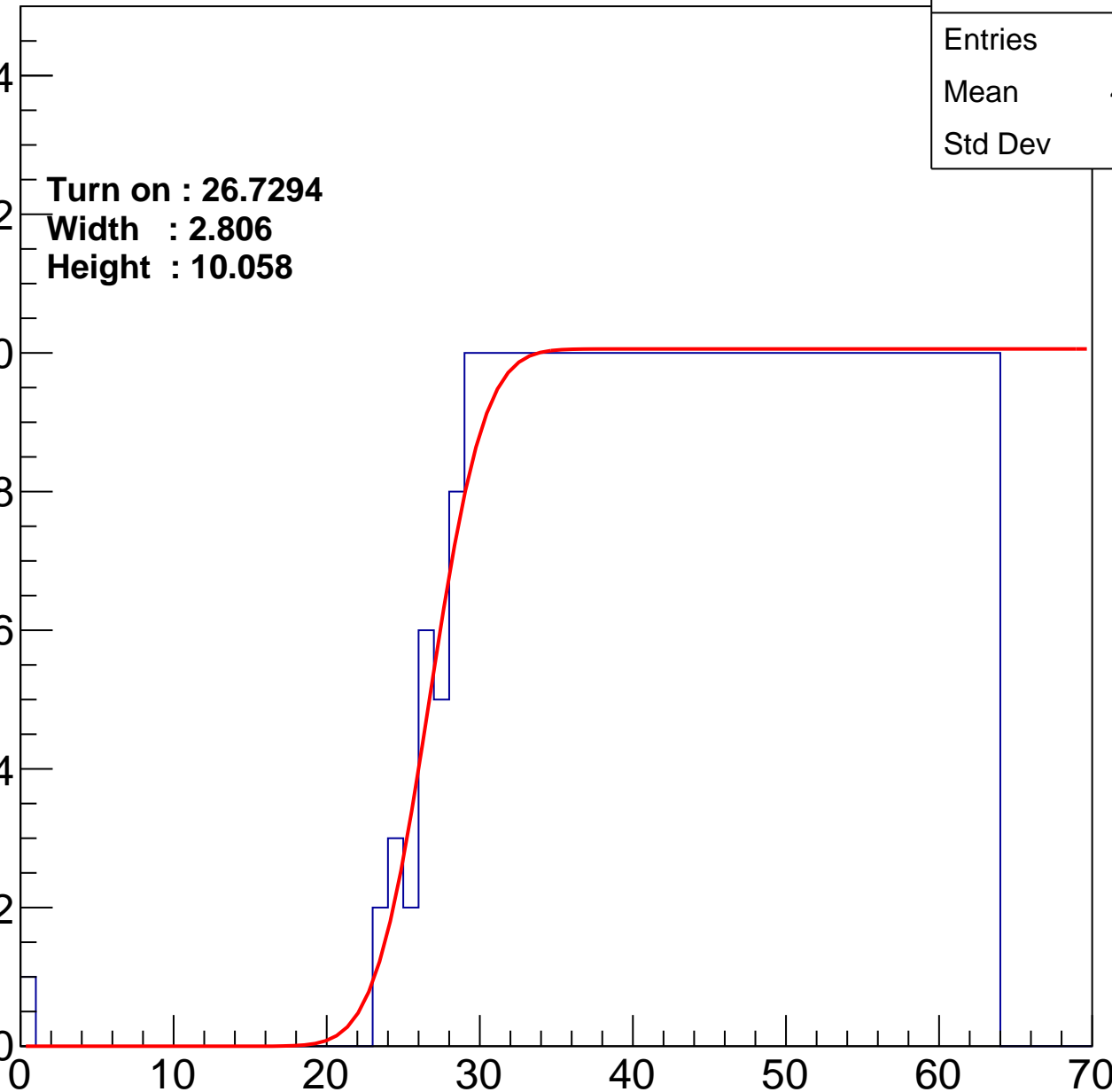
Width : 2.806

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch58

calib_packv5_042523_0143.root, FC#10, port B3

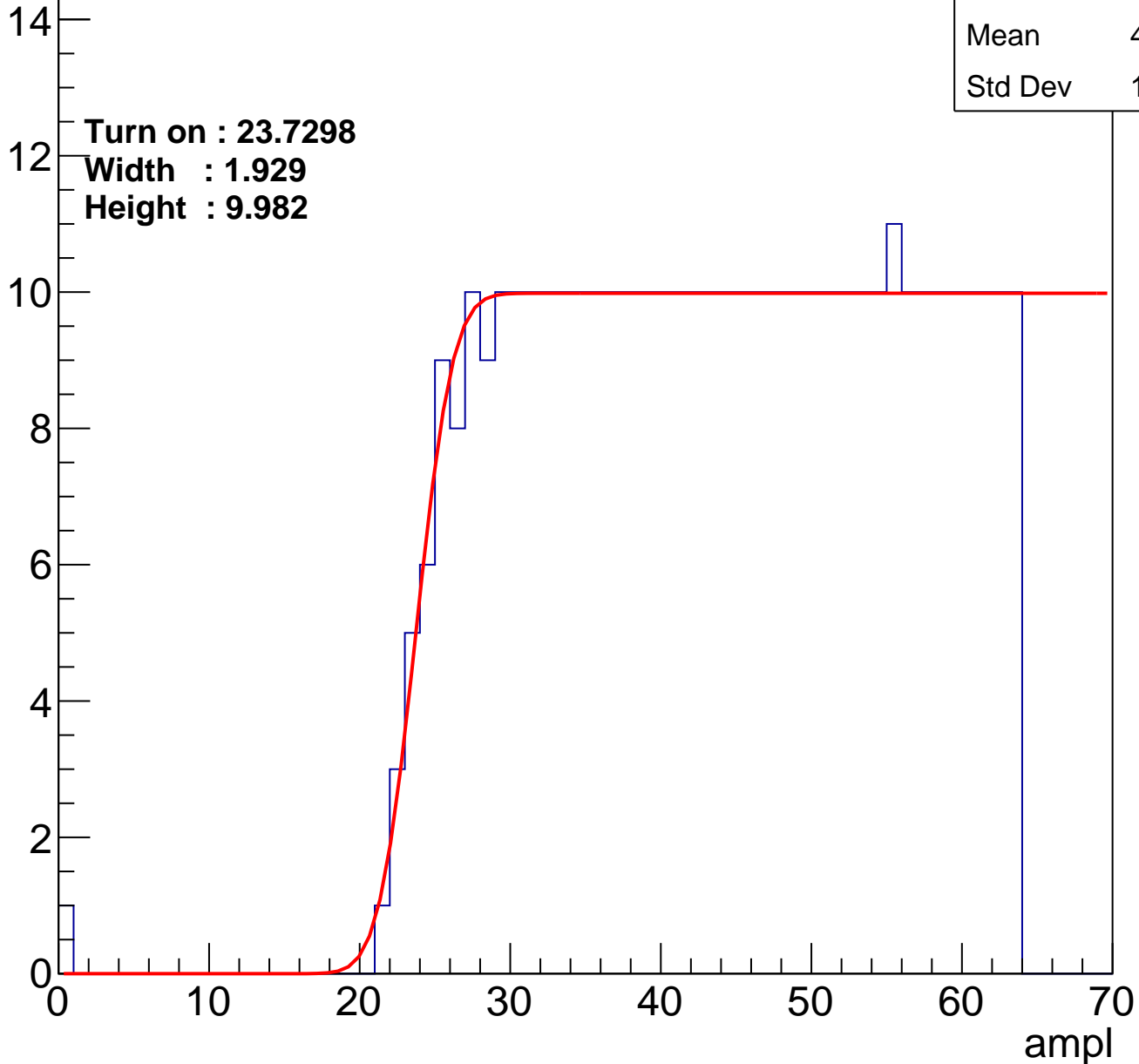
Entries	403
Mean	43.32
Std Dev	11.85

Turn on : 23.7298

Width : 1.929

Height : 9.982

Entry



B1L002S, U24-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.1393

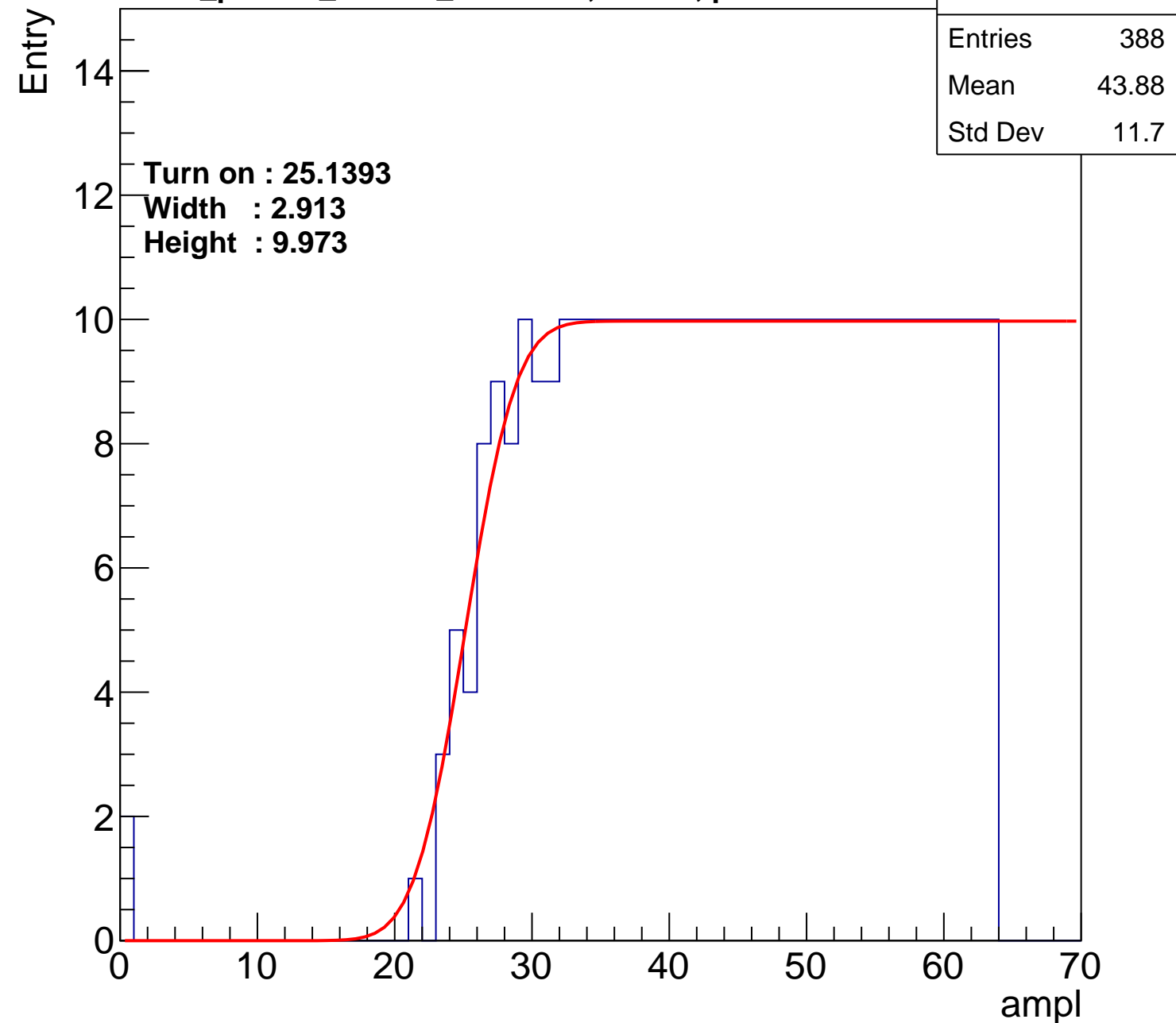
Width : 2.913

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch60

calib_packv5_042523_0143.root, FC#10, port B3

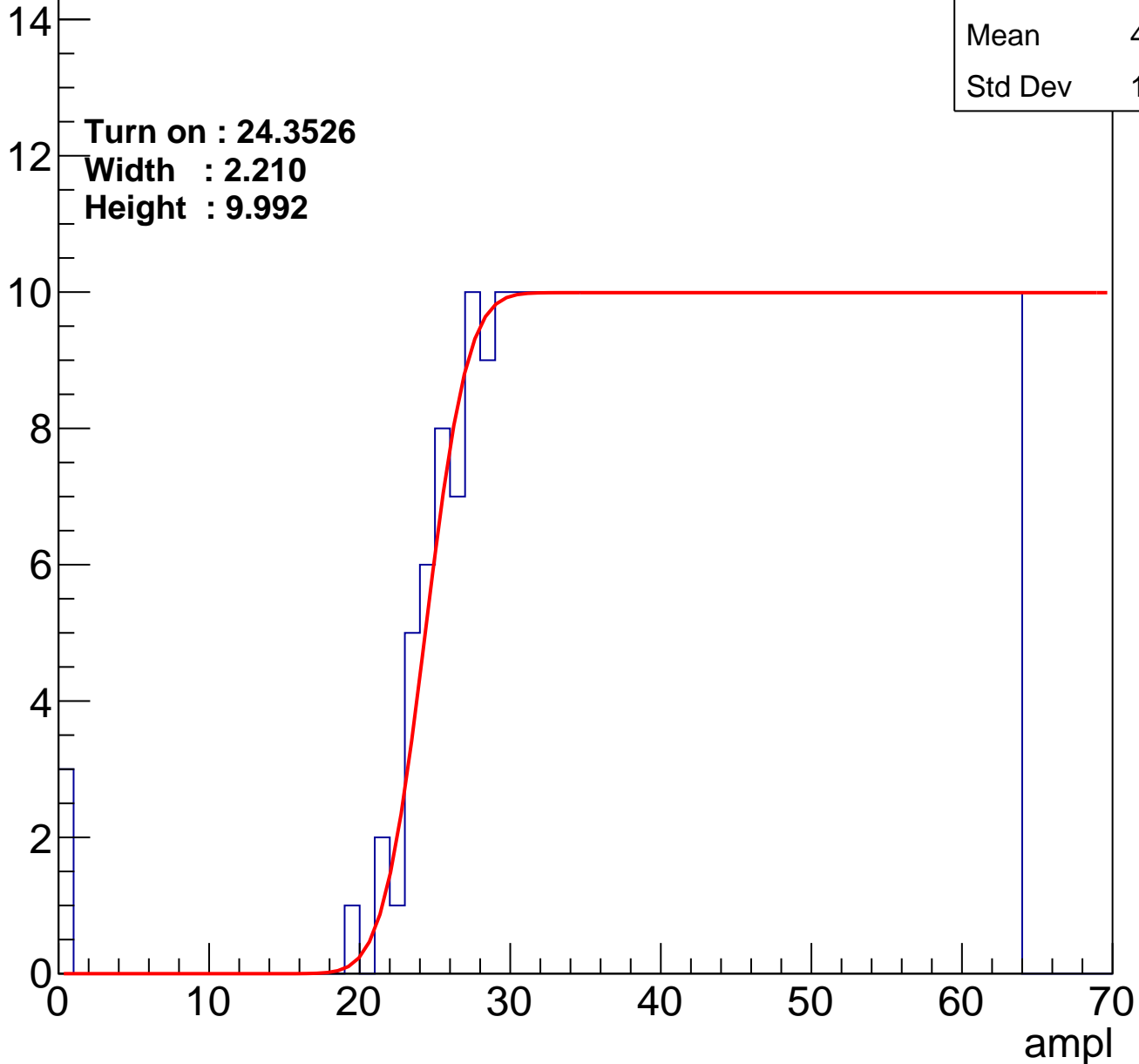
Entries	402
Mean	43.15
Std Dev	12.19

Turn on : 24.3526

Width : 2.210

Height : 9.992

Entry



B1L002S, U24-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.96
Std Dev	12.07

Turn on : 23.3522

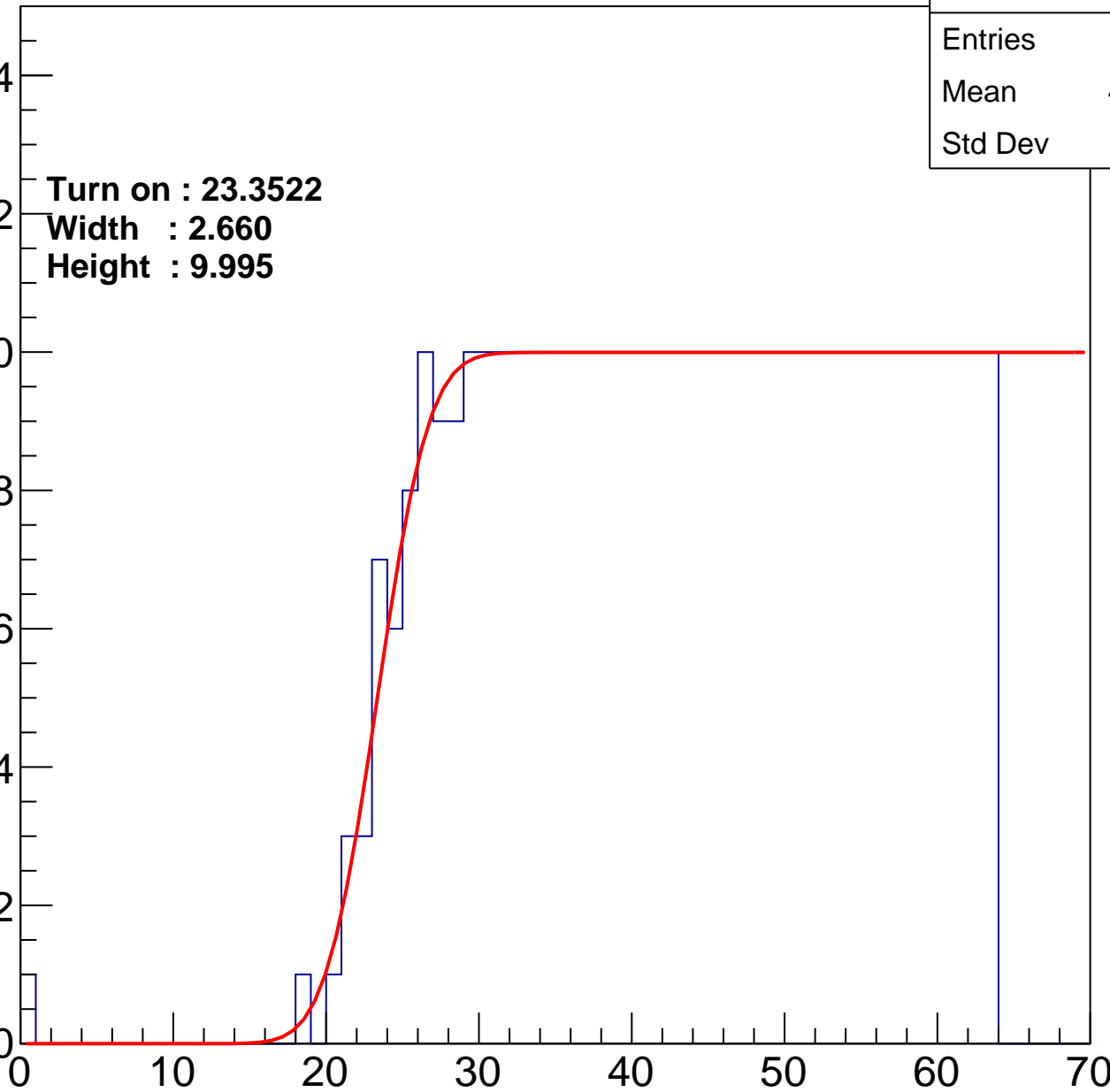
Width : 2.660

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch62

calib_packv5_042523_0143.root, FC#10, port B3

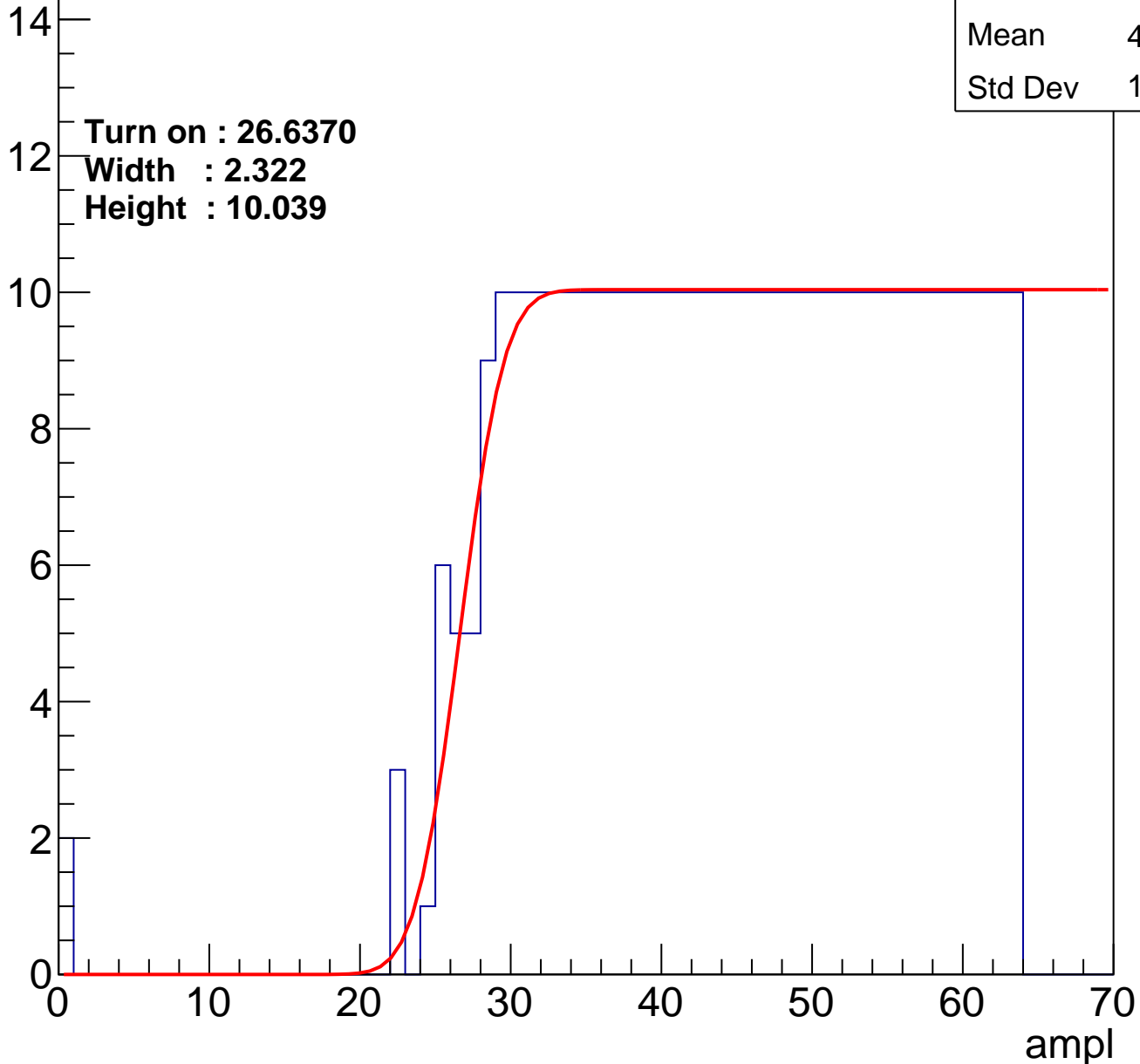
Entries	381
Mean	44.24
Std Dev	11.49

Turn on : 26.6370

Width : 2.322

Height : 10.039

Entry



B1L002S, U24-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.71
Std Dev	11.79

Turn on : 25.0907

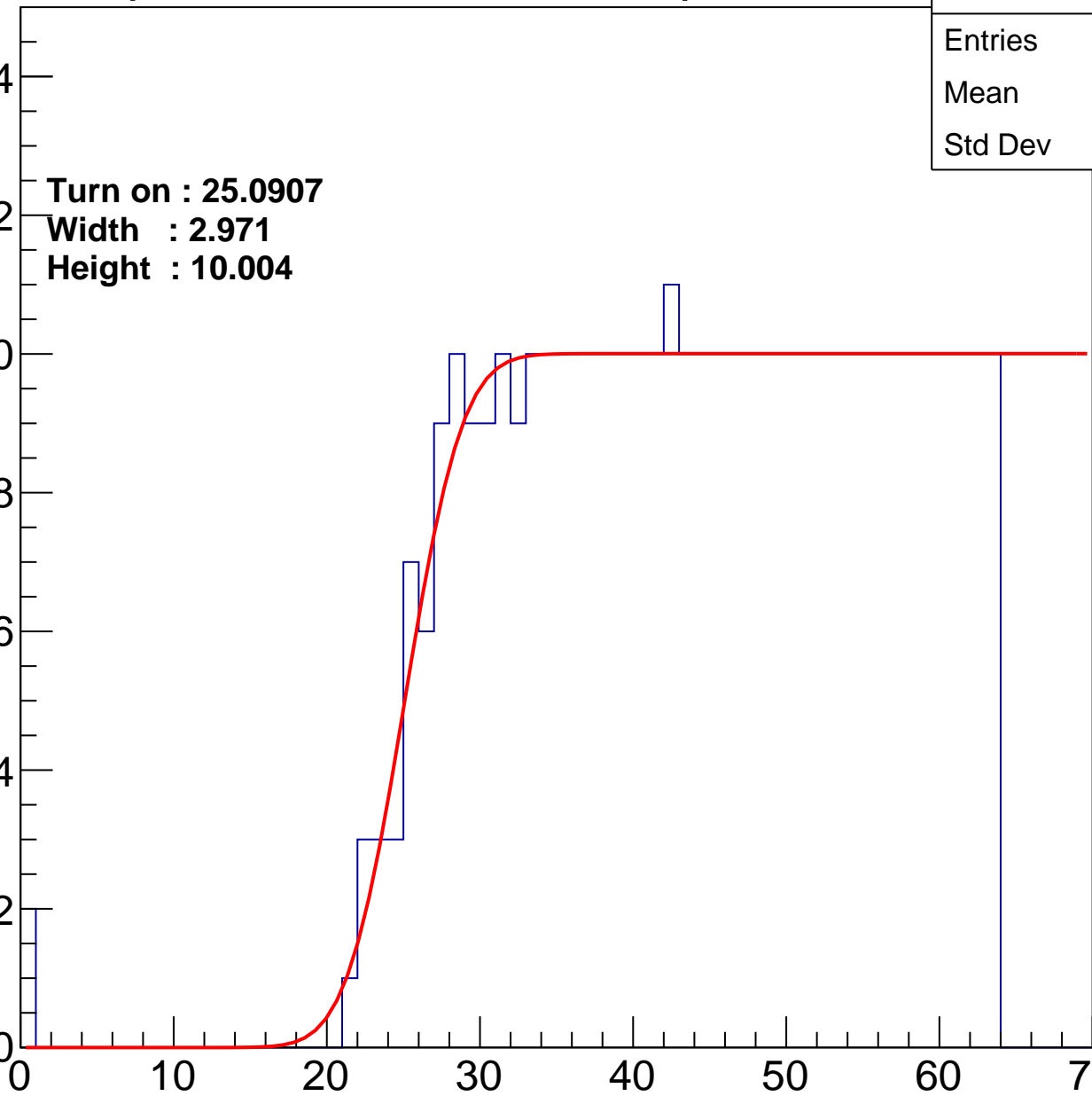
Width : 2.971

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch64

calib_packv5_042523_0143.root, FC#10, port B3

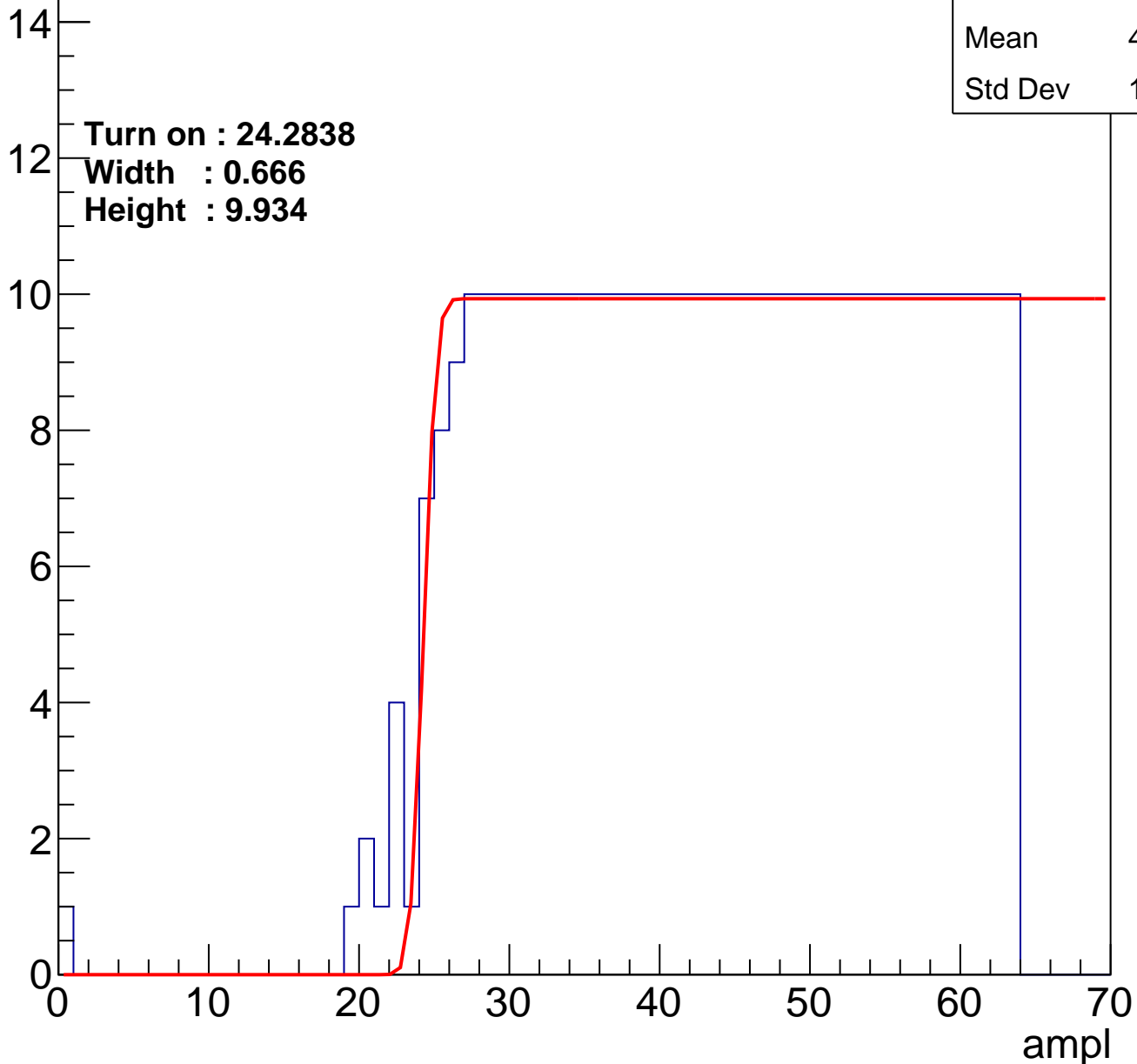
Entries	404
Mean	43.18
Std Dev	11.93

Turn on : 24.2838

Width : 0.666

Height : 9.934

Entry



B1L002S, U24-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.64
Std Dev	11.82

Turn on : 25.1884

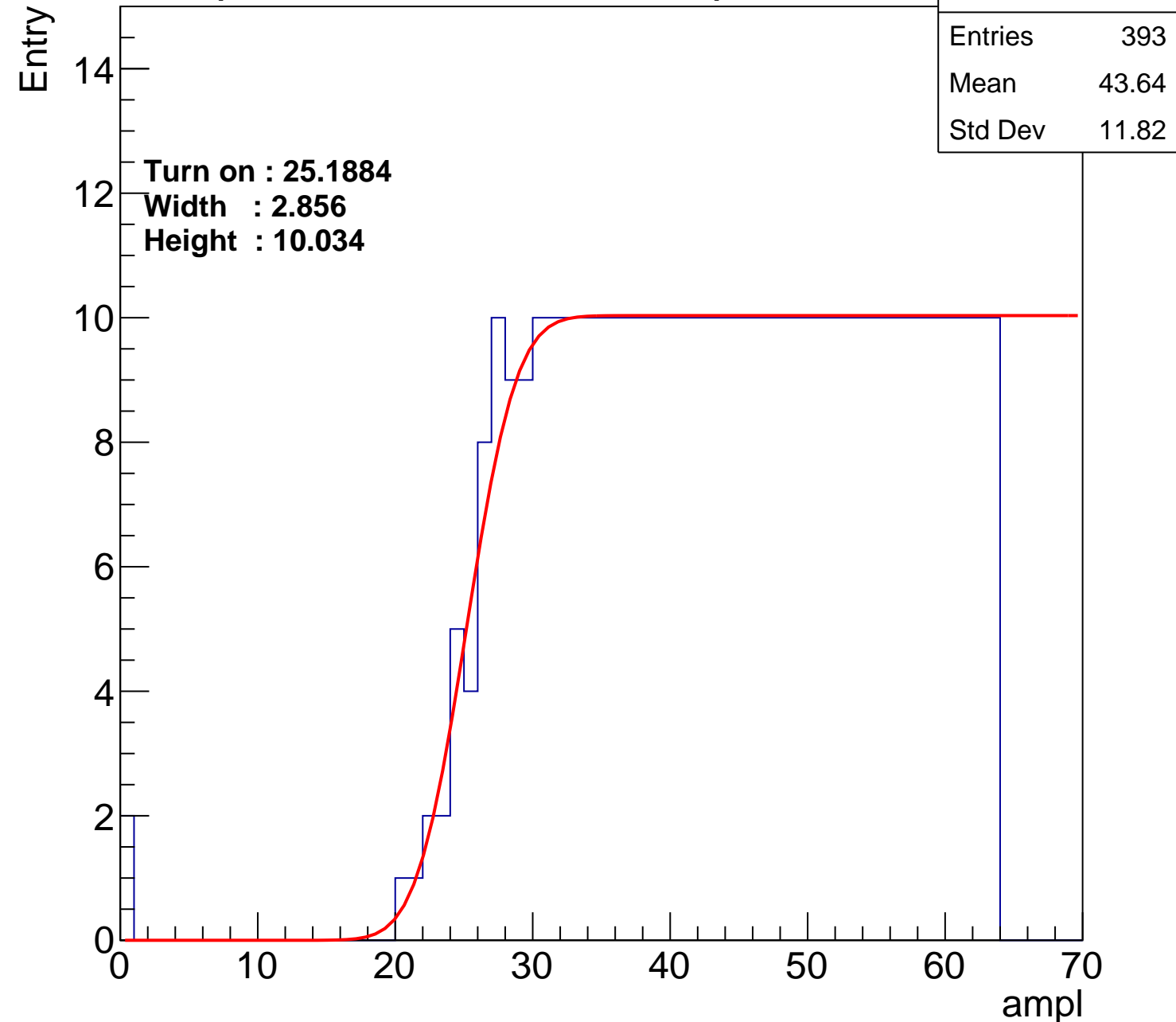
Width : 2.856

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.29
Std Dev	11.33

Turn on : 25.9067

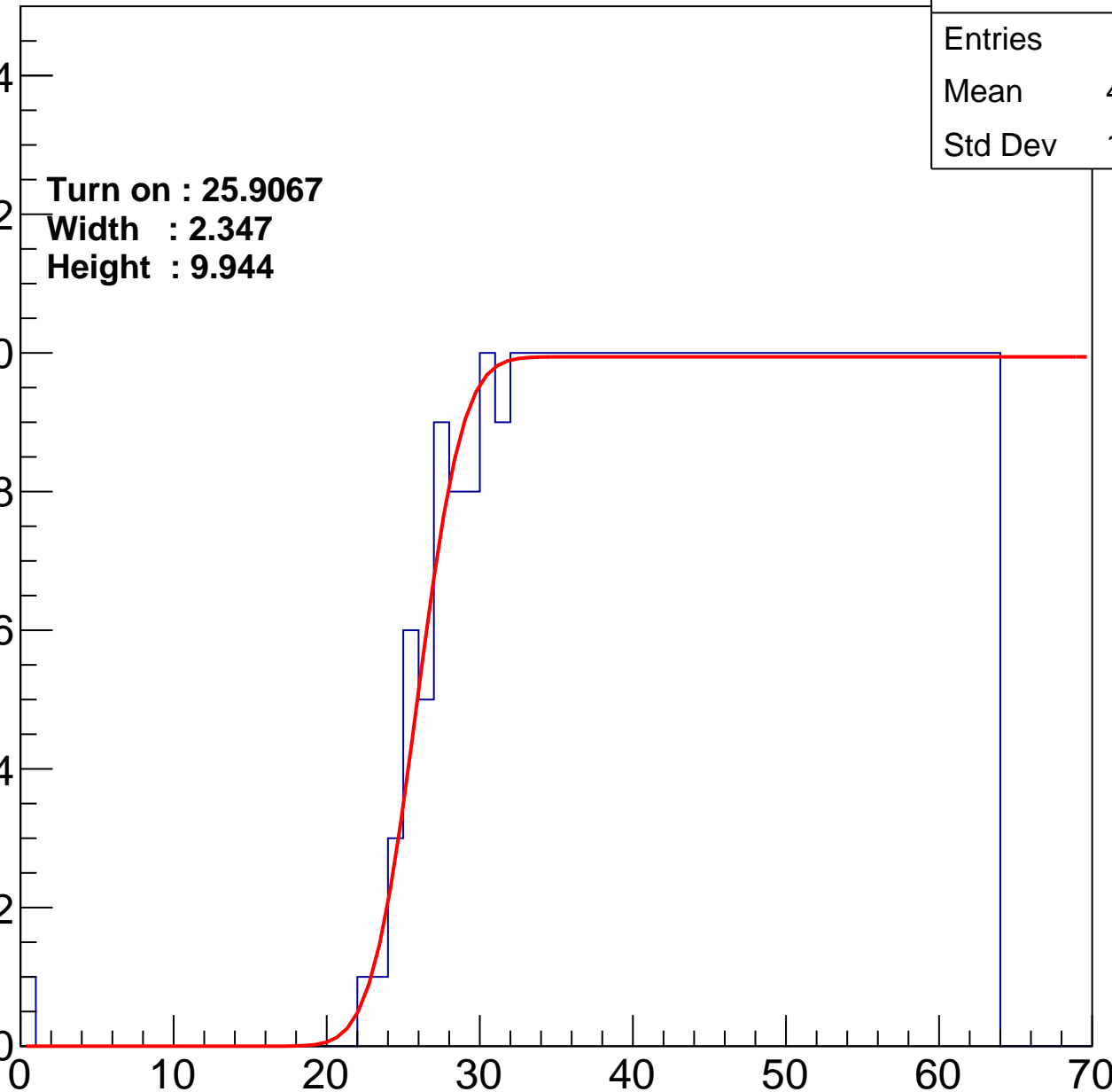
Width : 2.347

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch67

calib_packv5_042523_0143.root, FC#10, port B3

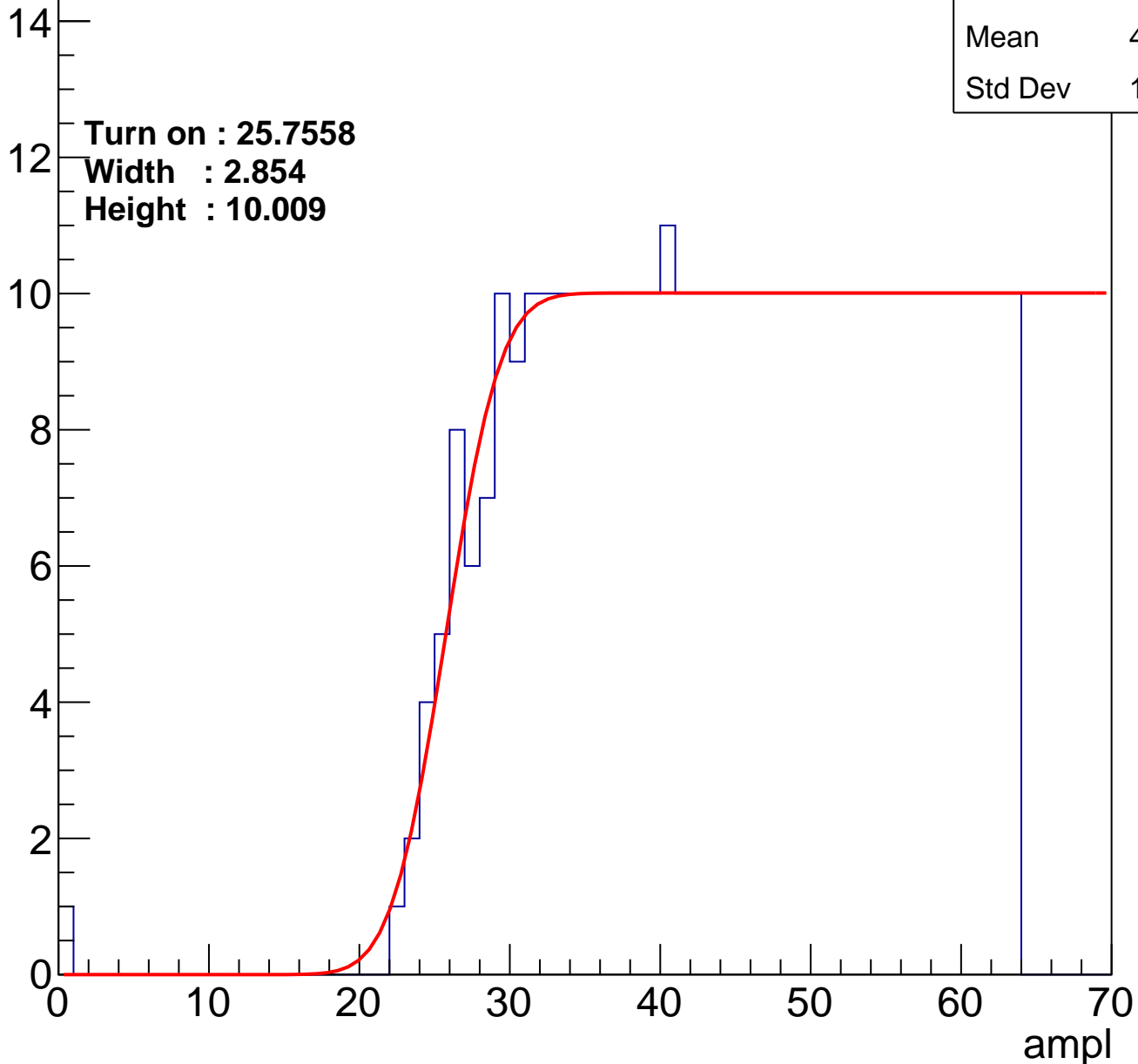
Entries	384
Mean	44.18
Std Dev	11.38

Turn on : 25.7558

Width : 2.854

Height : 10.009

Entry



B1L002S, U24-ch68

calib_packv5_042523_0143.root, FC#10, port B3

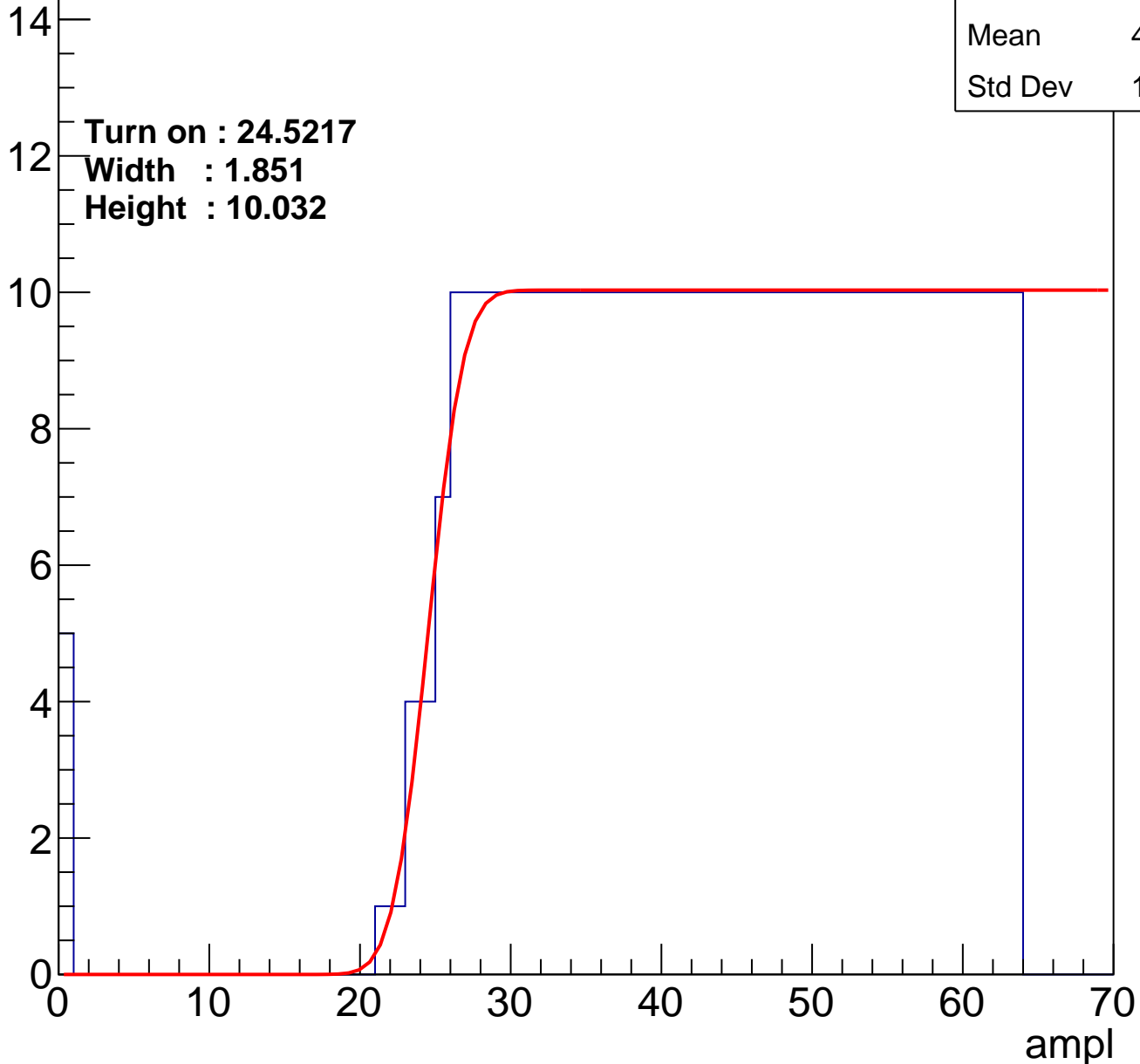
Entries	402
Mean	43.07
Std Dev	12.42

Turn on : 24.5217

Width : 1.851

Height : 10.032

Entry



B1L002S, U24-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.53
Std Dev	11.72

Turn on : 24.4371

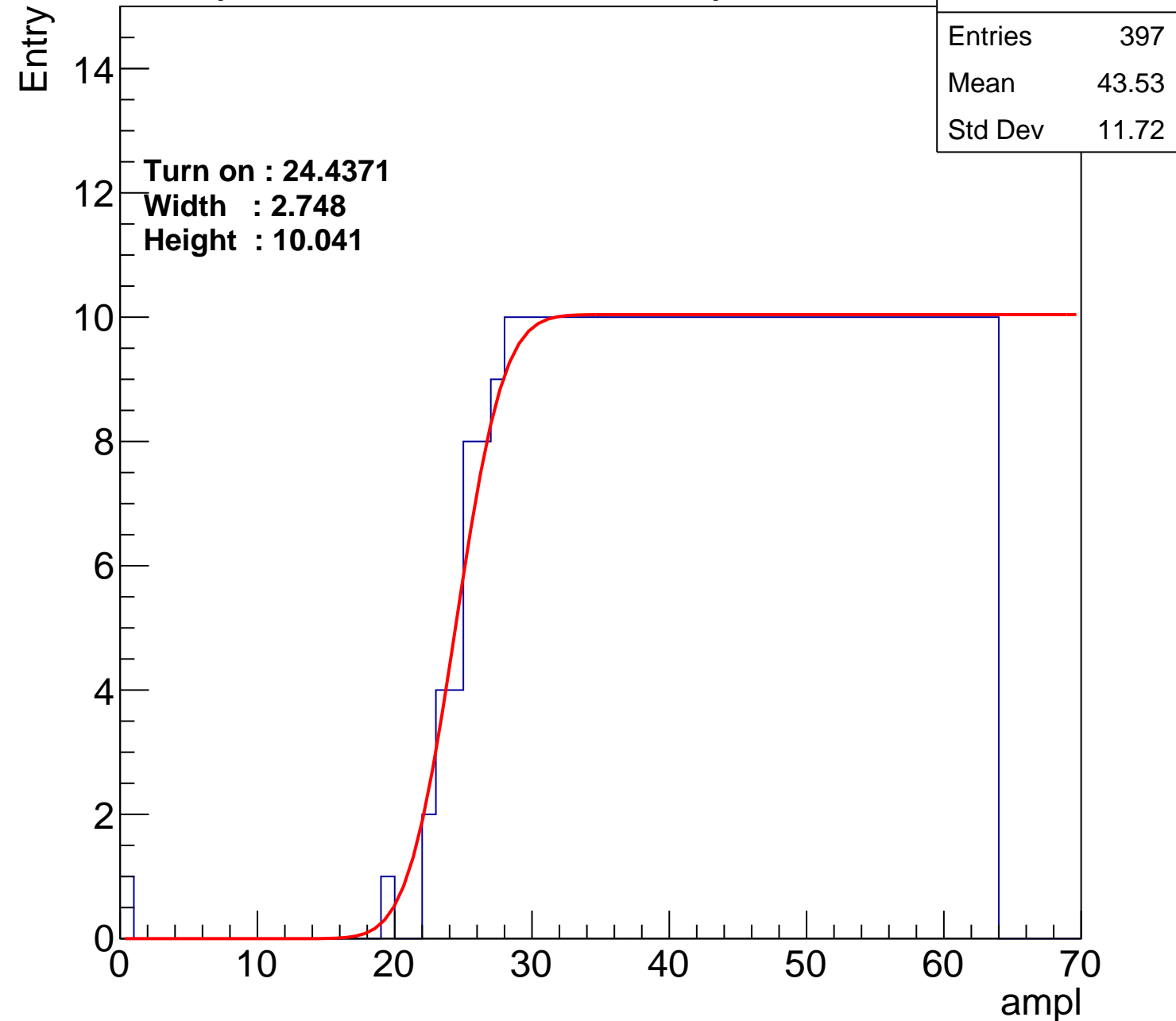
Width : 2.748

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl

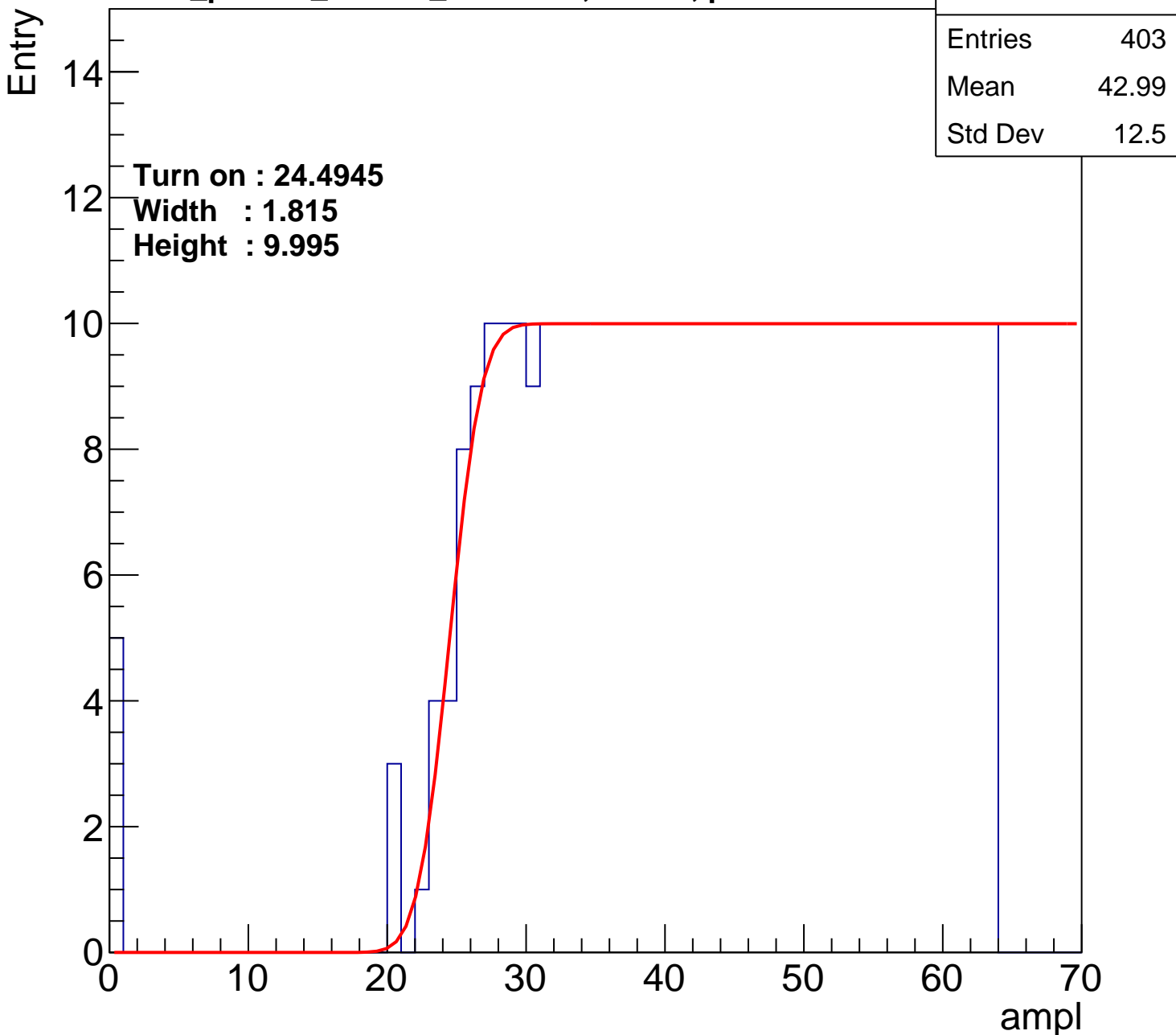


calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	42.99
Std Dev	12.5

Height : 9.995



B1L002S, U24-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.67
Std Dev	12.07

Turn on : 25.6849

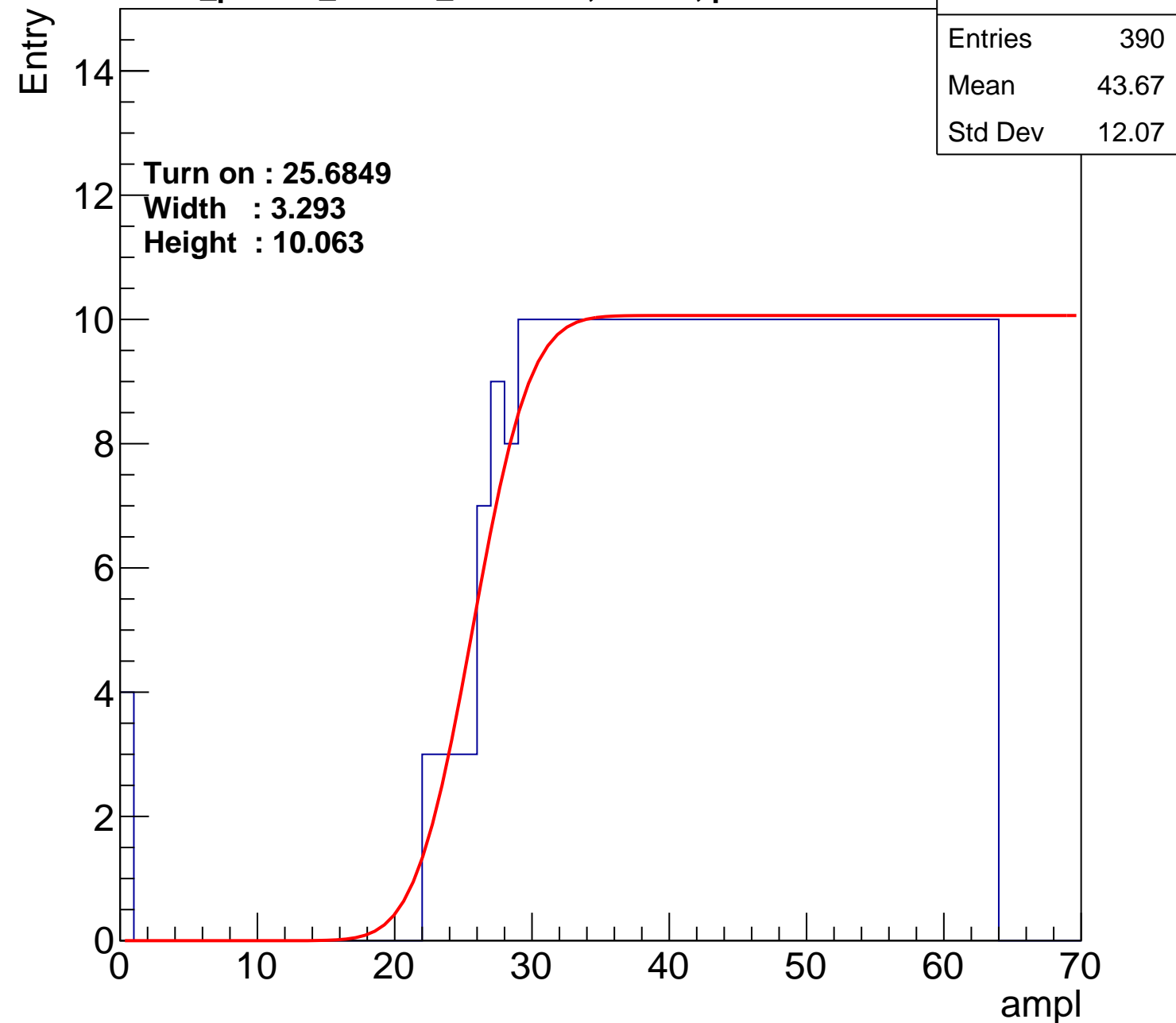
Width : 3.293

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch72

calib_packv5_042523_0143.root, FC#10, port B3

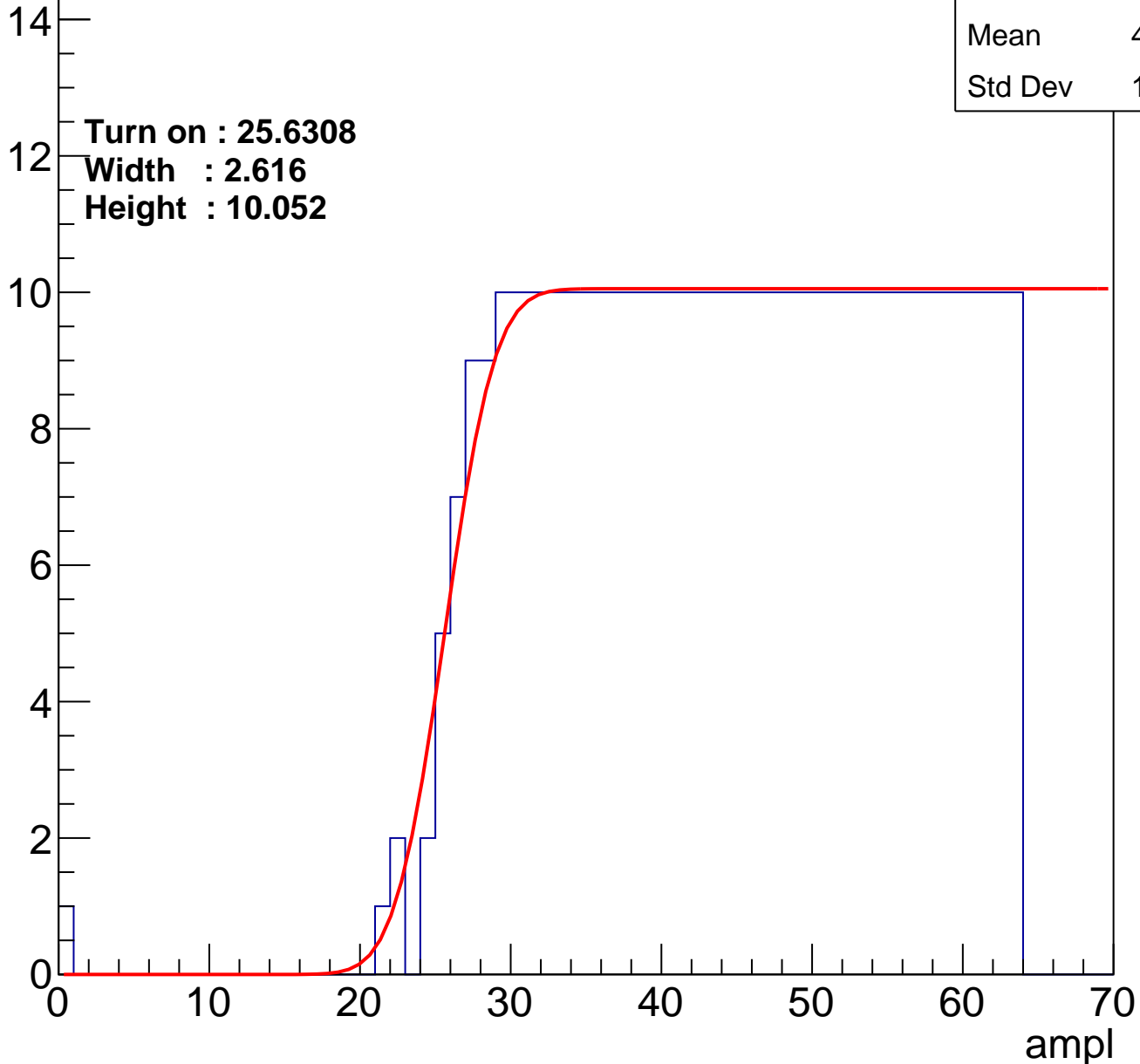
Entries	386
Mean	44.08
Std Dev	11.42

Turn on : 25.6308

Width : 2.616

Height : 10.052

Entry



B1L002S, U24-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 25.2092

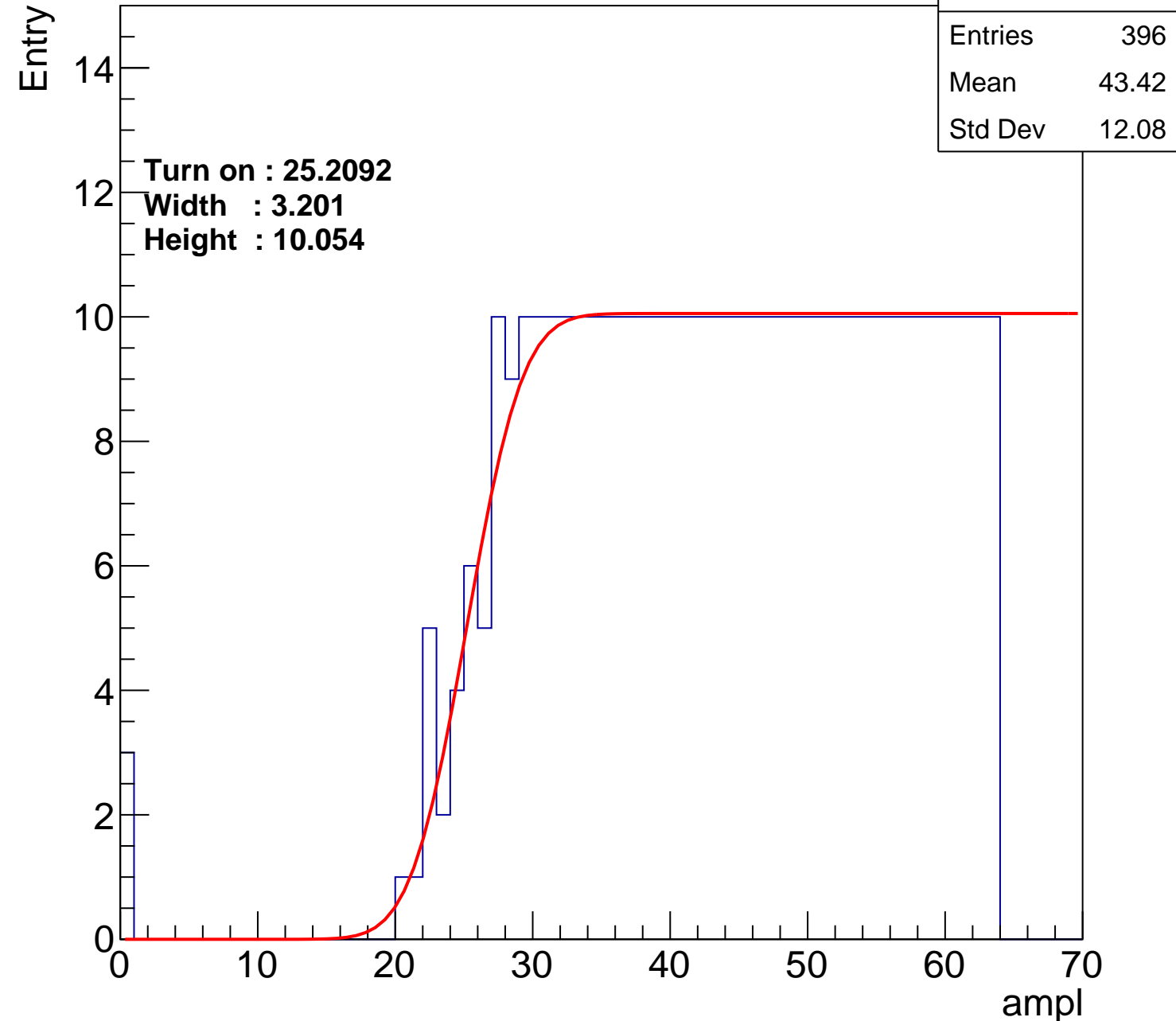
Width : 3.201

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.64
Std Dev	11.7

Std Dev	11.7
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Height : 9.976



B1L002S, U24-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.69
Std Dev	11.44

Turn on : 27.5312

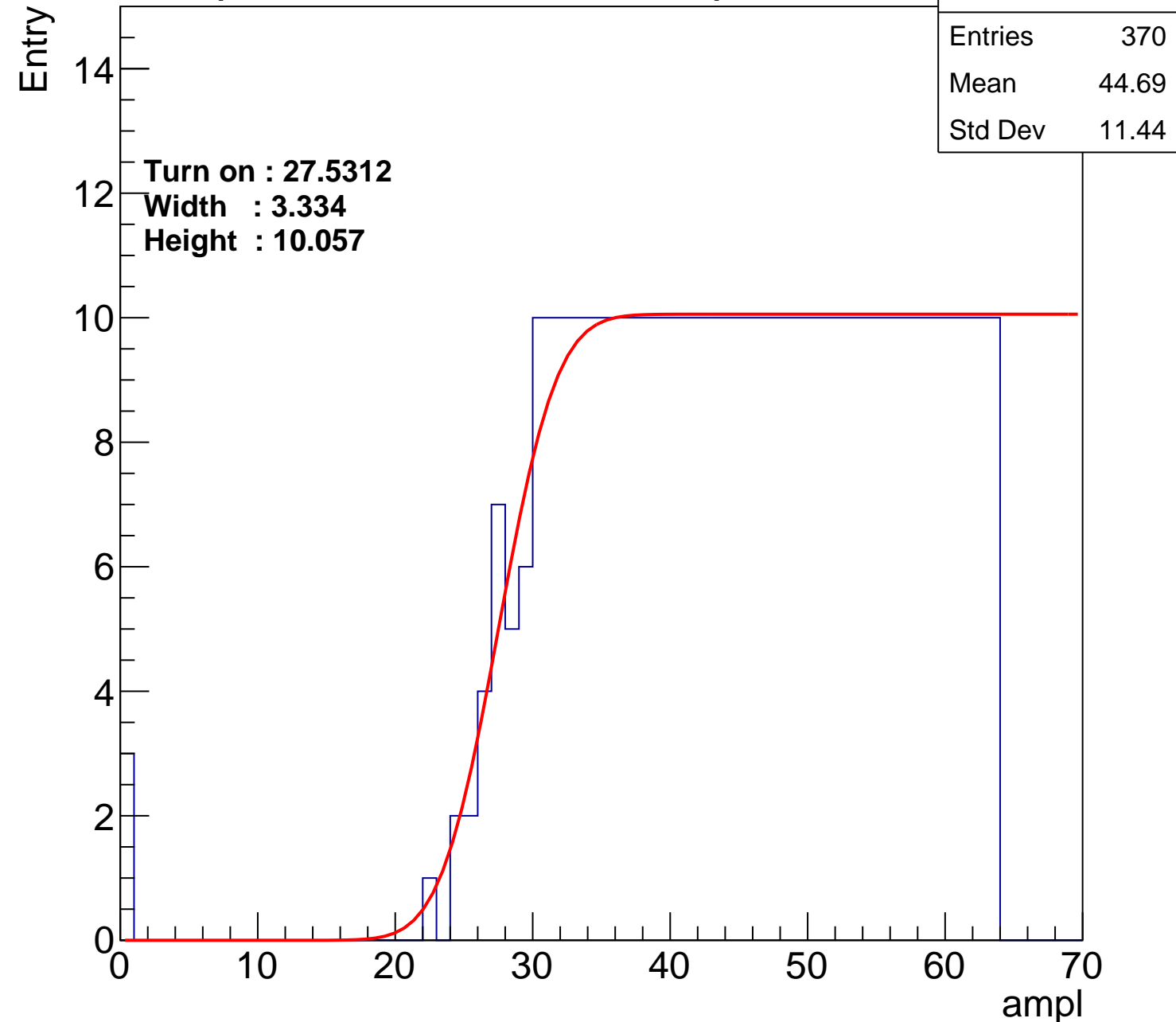
Width : 3.334

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch76

calib_packv5_042523_0143.root, FC#10, port B3

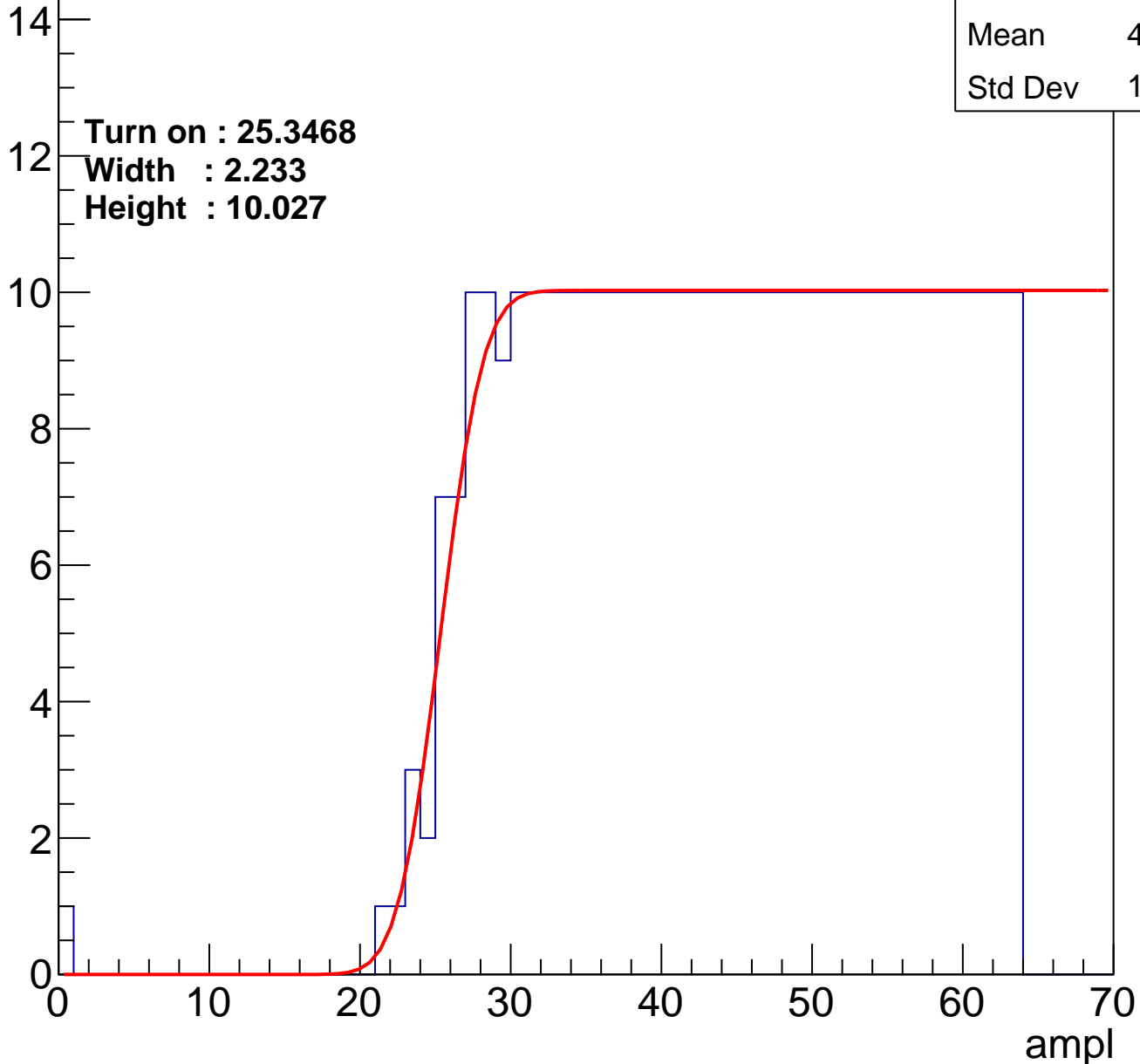
Entries	391
Mean	43.83
Std Dev	11.55

Turn on : 25.3468

Width : 2.233

Height : 10.027

Entry



B1L002S, U24-ch77

calib_packv5_042523_0143.root, FC#10, port B3

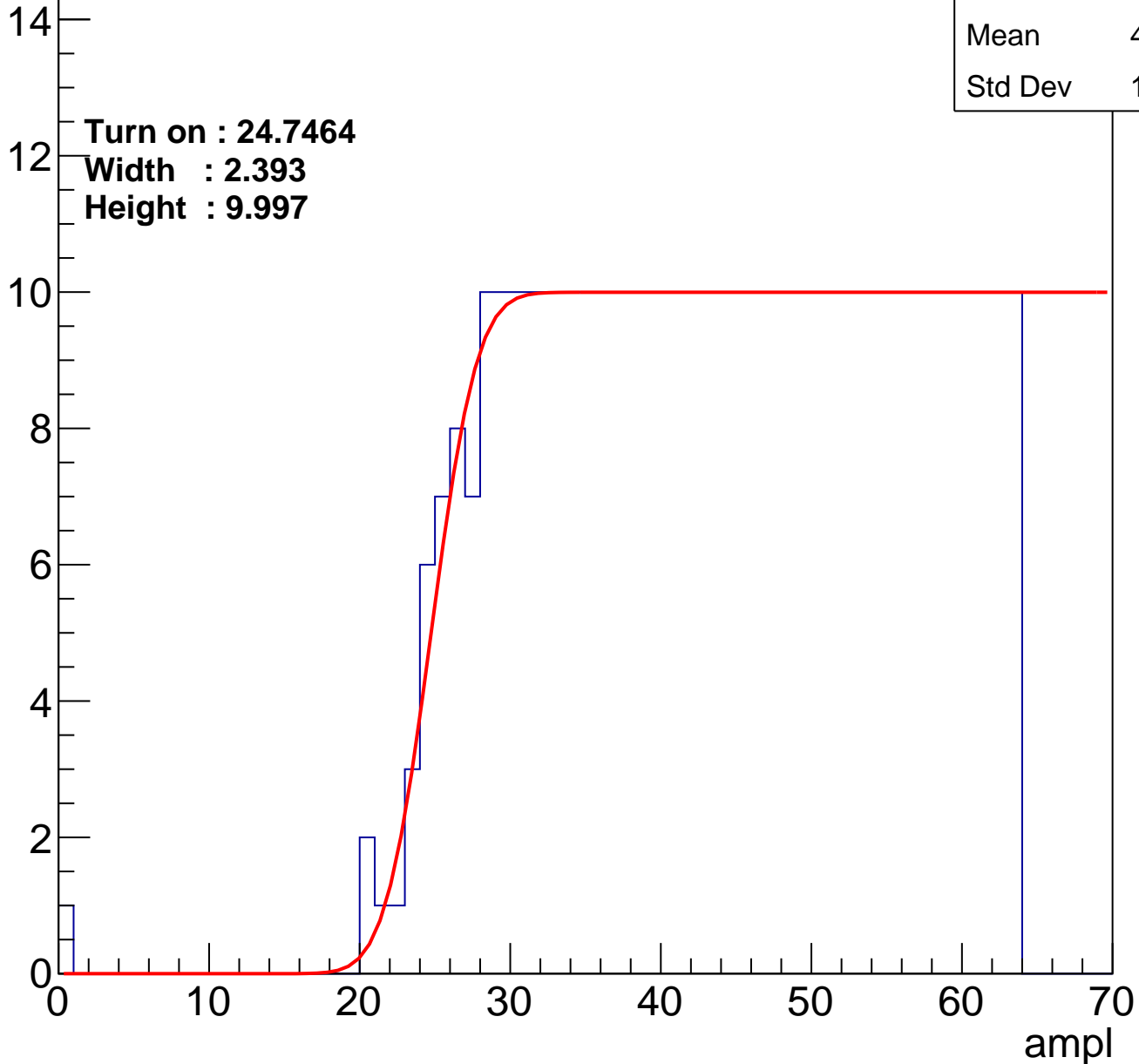
Entries	396
Mean	43.56
Std Dev	11.74

Turn on : 24.7464

Width : 2.393

Height : 9.997

Entry



B1L002S, U24-ch78

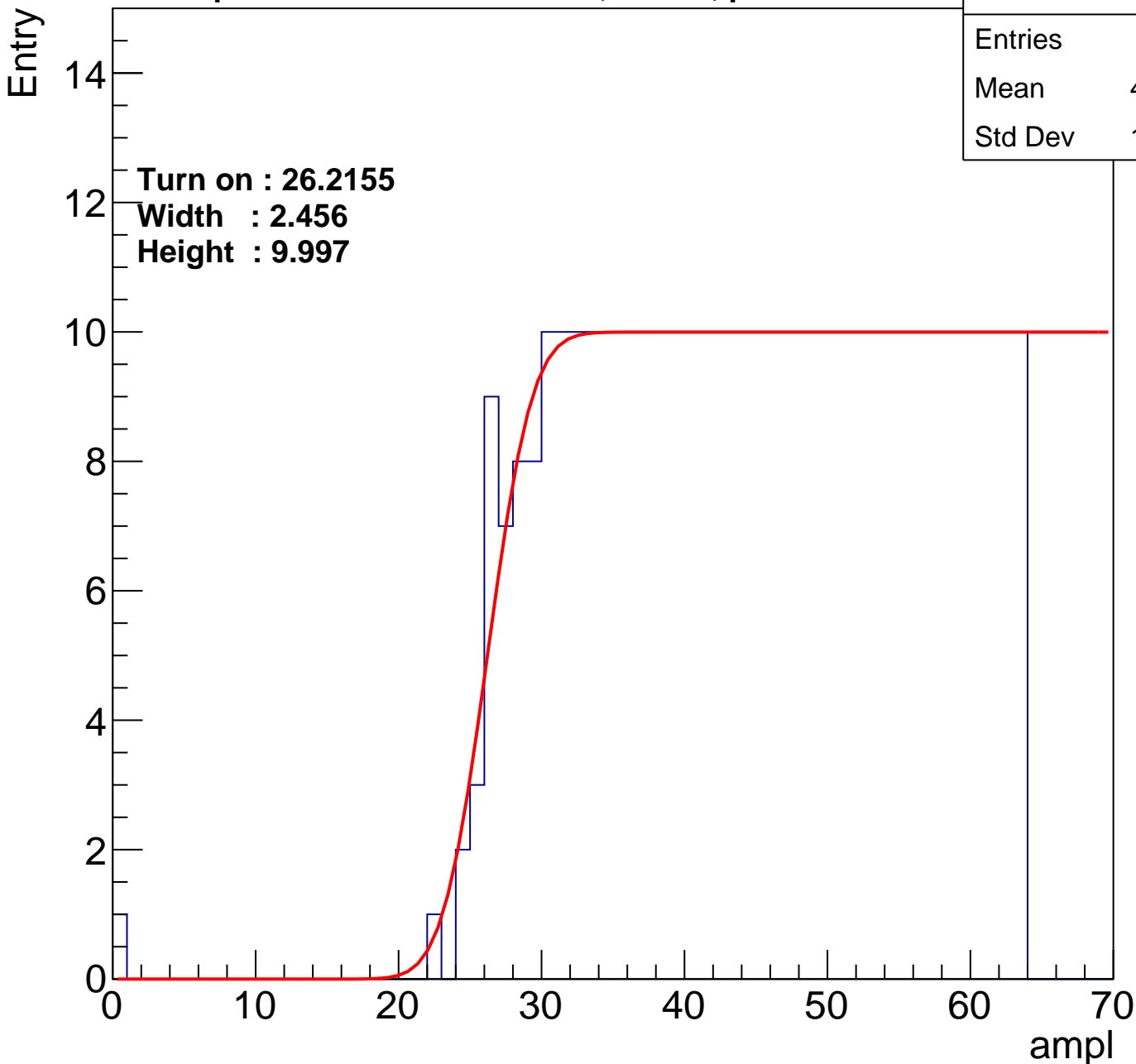
calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.2155

Width : 2.456

Height : 9.997



B1L002S, U24-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.36
Std Dev	11.96

Turn on : 24.2994

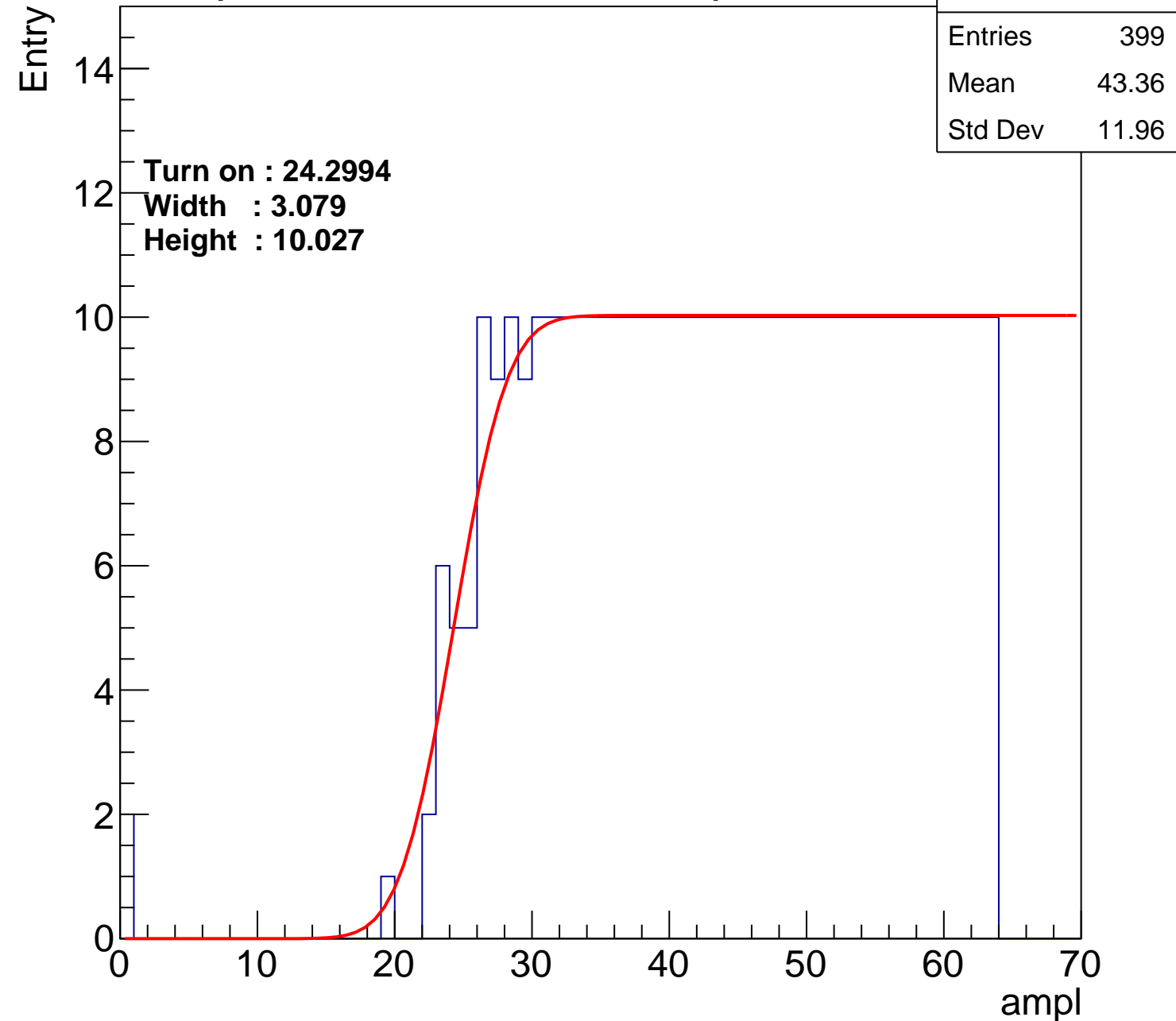
Width : 3.079

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.55
Std Dev	11.74

Turn on : 25.0717

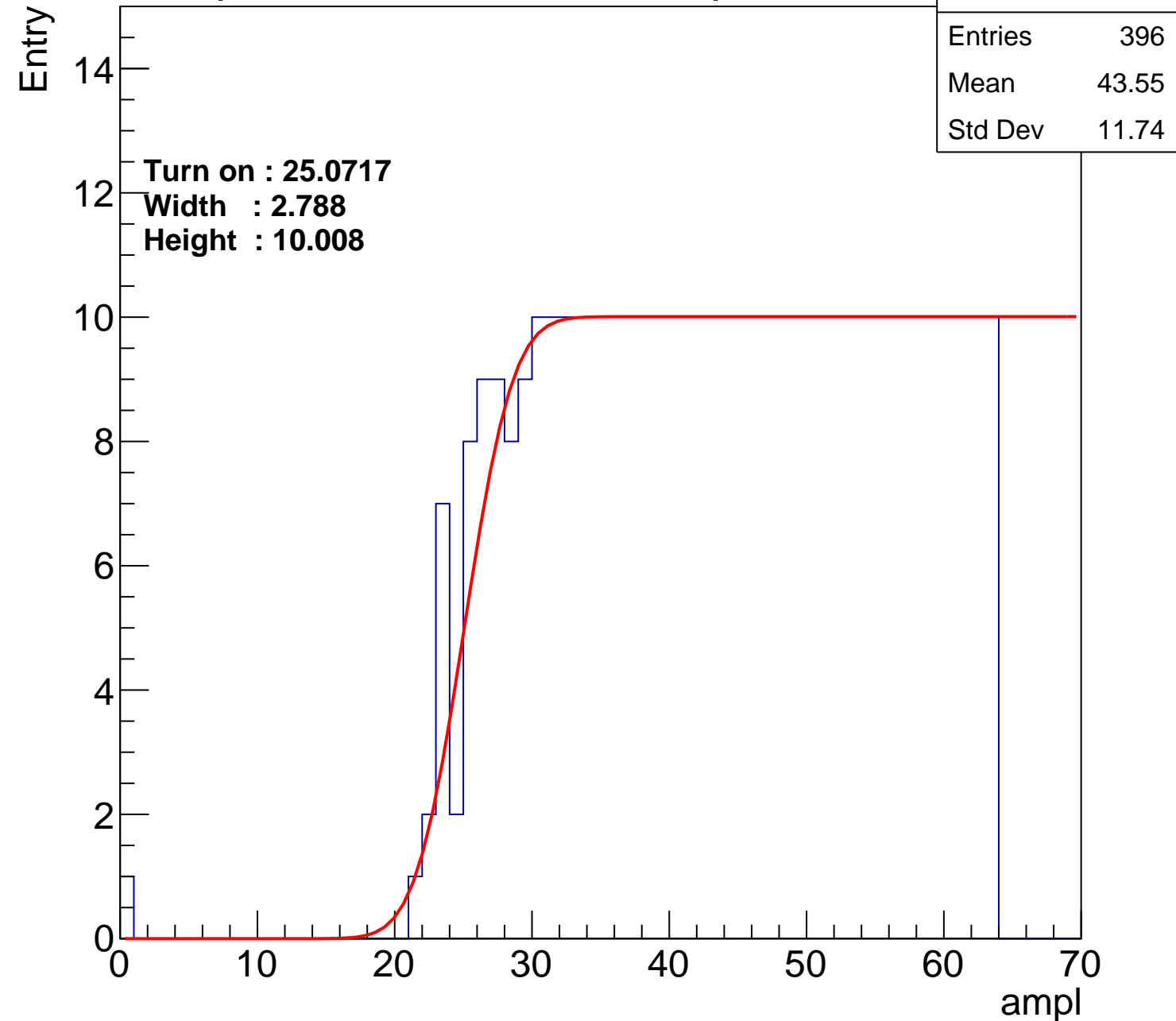
Width : 2.788

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.08
Std Dev	12.19

Turn on : 24.0141

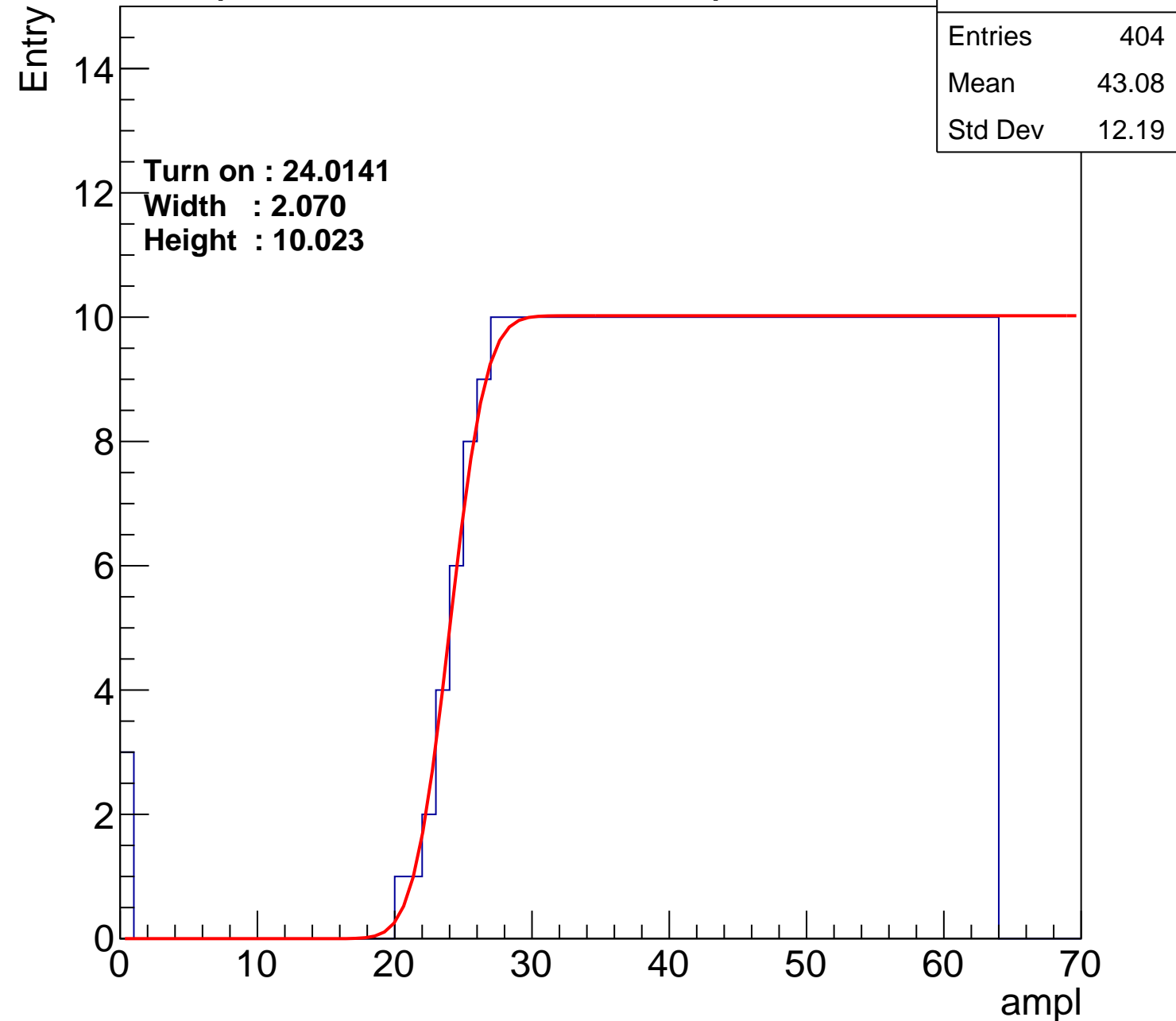
Width : 2.070

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch82

calib_packv5_042523_0143.root, FC#10, port B3

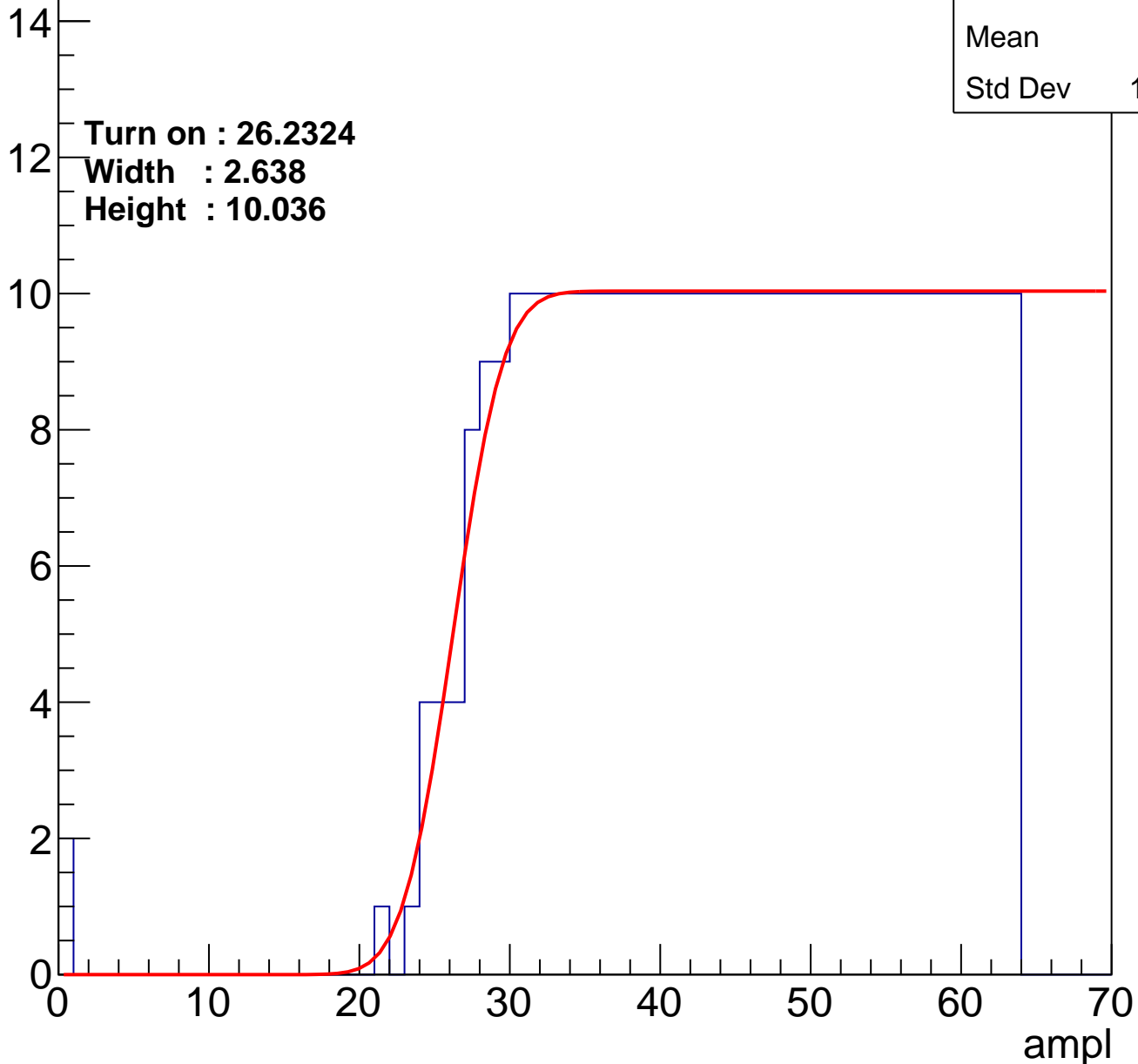
Entries	382
Mean	44.2
Std Dev	11.52

Turn on : 26.2324

Width : 2.638

Height : 10.036

Entry



B1L002S, U24-ch83

calib_packv5_042523_0143.root, FC#10, port B3

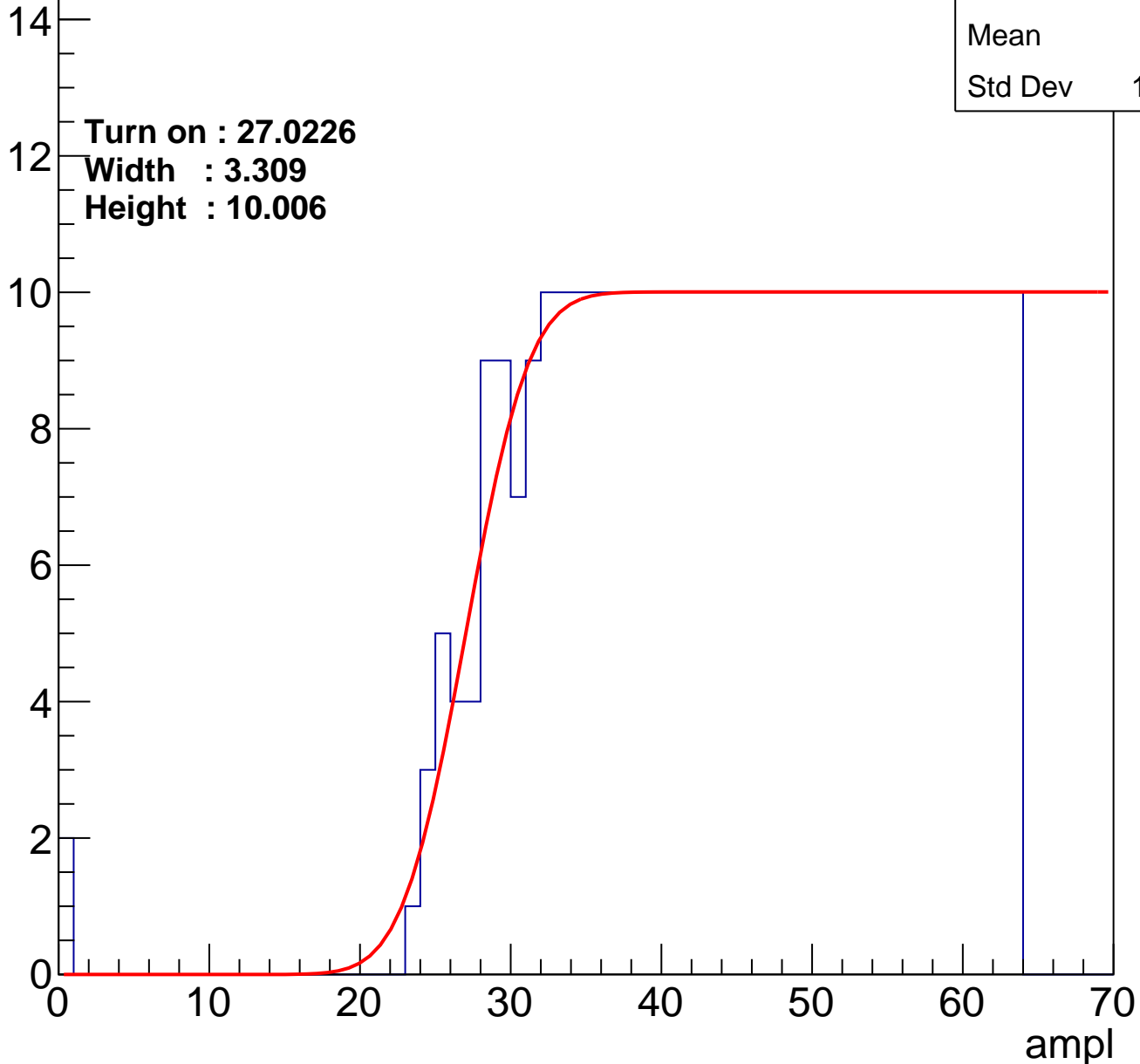
Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 27.0226

Width : 3.309

Height : 10.006

Entry



B1L002S, U24-ch84

calib_packv5_042523_0143.root, FC#10, port B3

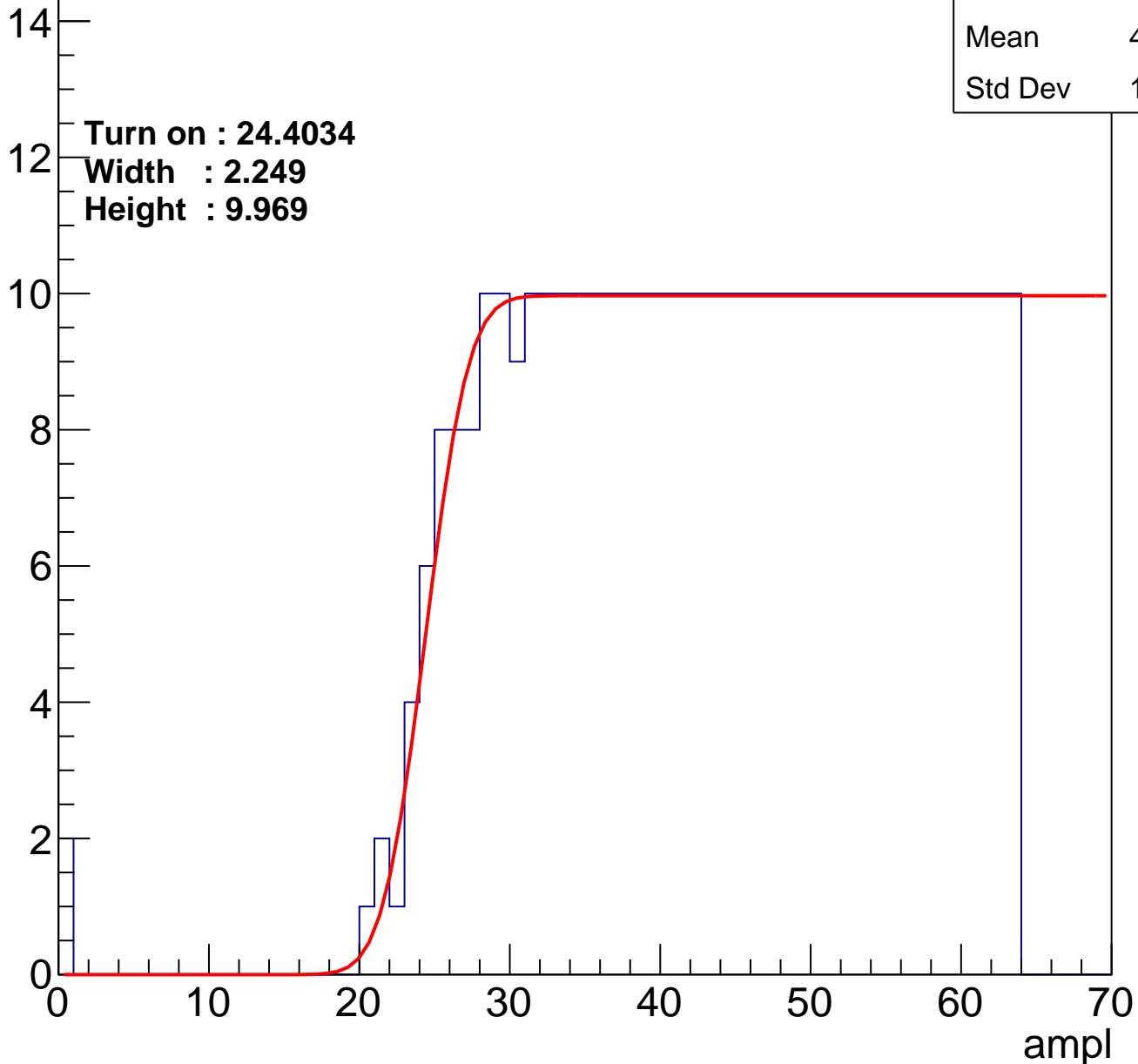
Entries	399
Mean	43.34
Std Dev	11.98

Turn on : 24.4034

Width : 2.249

Height : 9.969

Entry



B1L002S, U24-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.93
Std Dev	11.63

Turn on : 25.6180

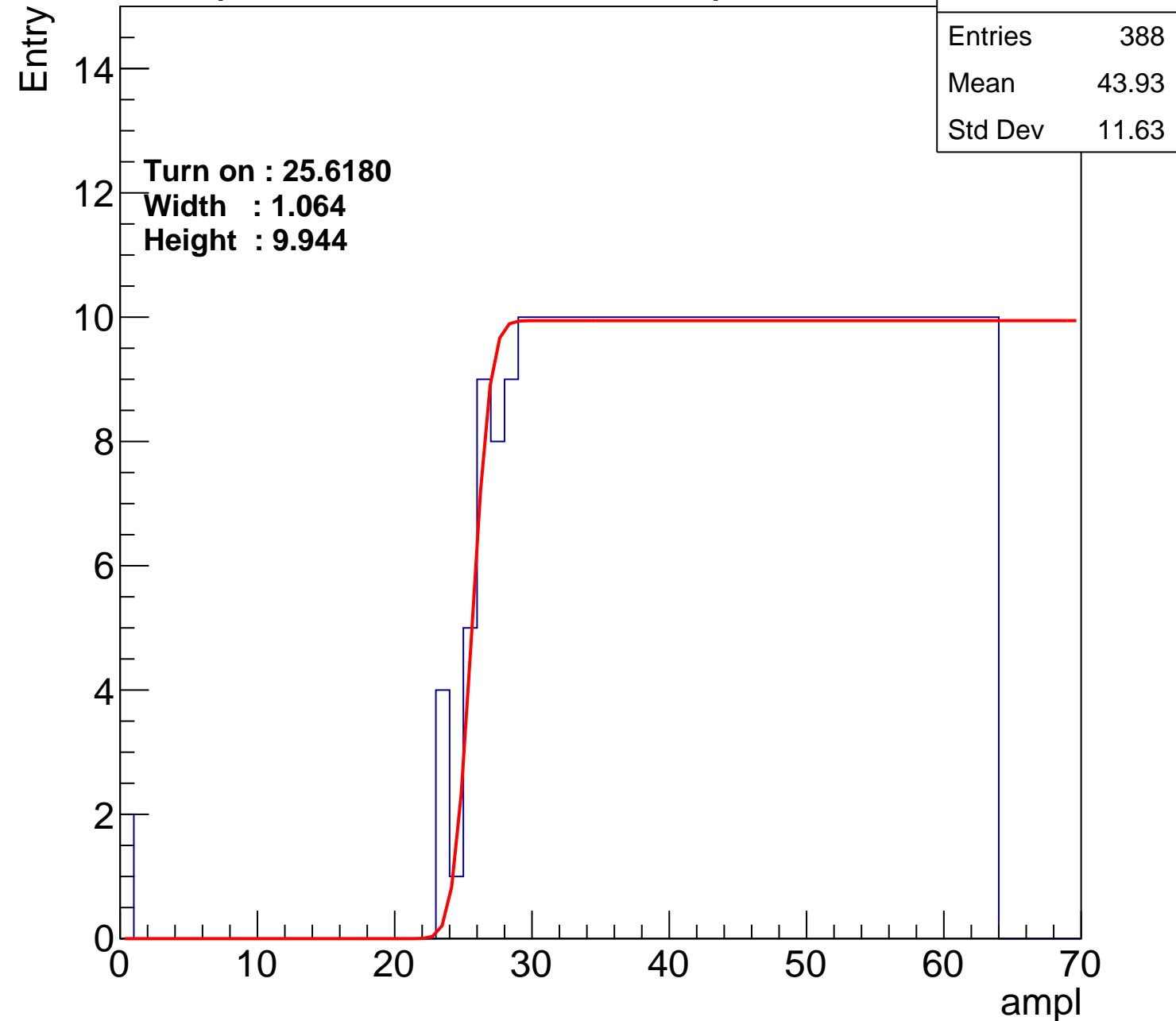
Width : 1.064

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch86

calib_packv5_042523_0143.root, FC#10, port B3

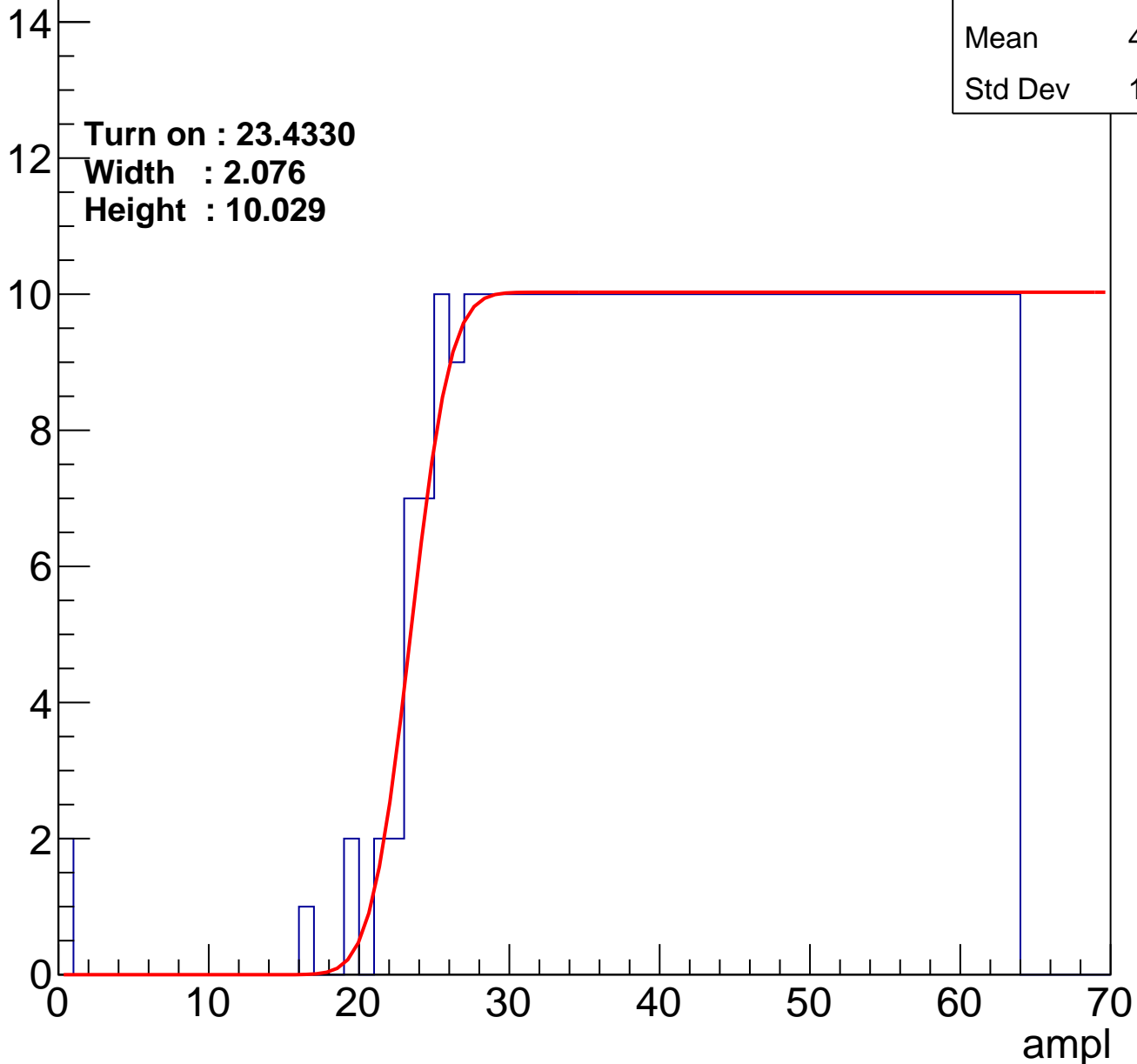
Entries	412
Mean	42.73
Std Dev	12.29

Turn on : 23.4330

Width : 2.076

Height : 10.029

Entry



B1L002S, U24-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.72
Std Dev	11.62

Turn on : 25.9247

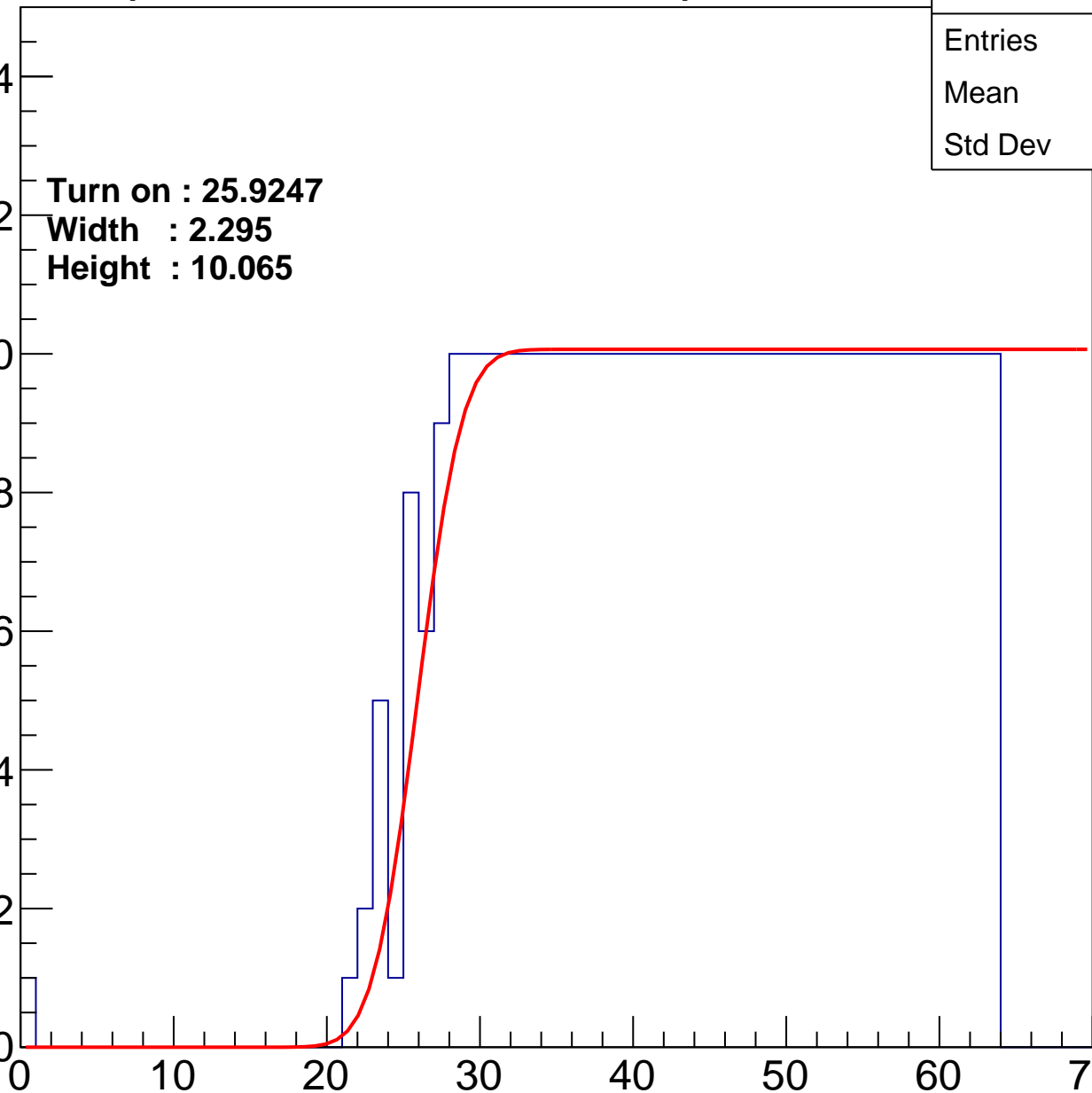
Width : 2.295

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.07
Std Dev	11.86

Turn on : 26.5620

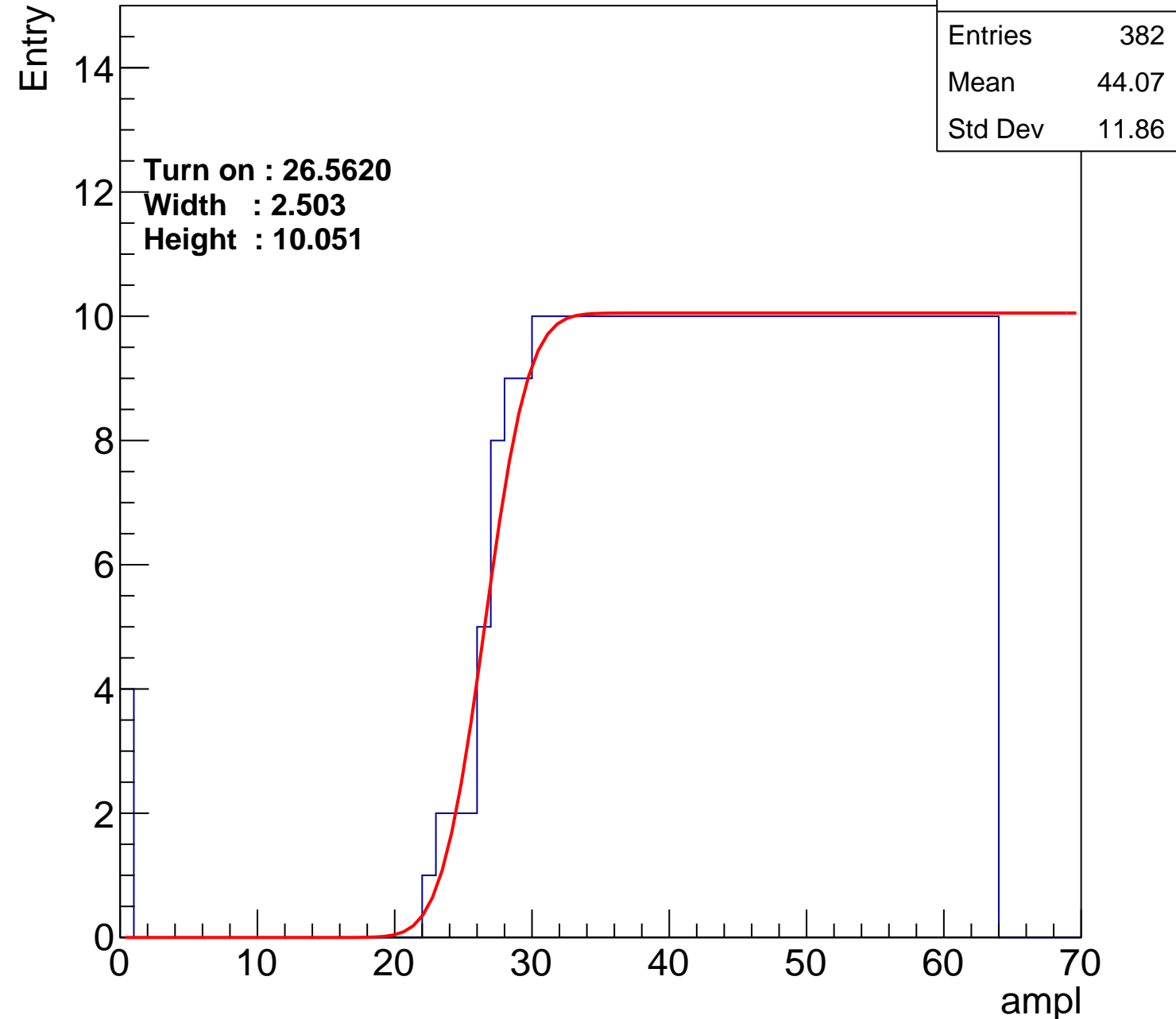
Width : 2.503

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.74
Std Dev	11.88

Turn on : 25.0672

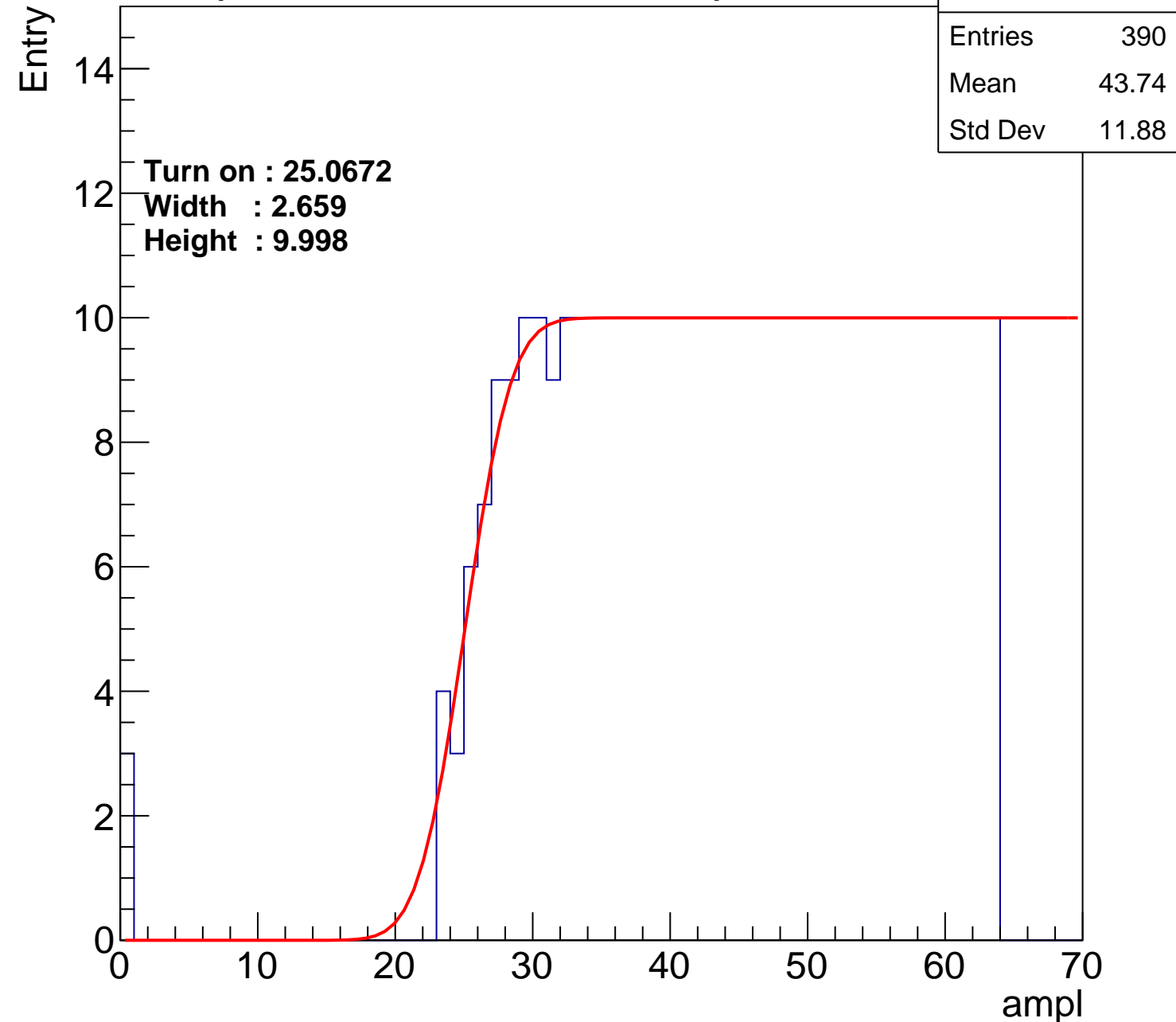
Width : 2.659

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.42
Std Dev	11.74

Turn on : 26.9094

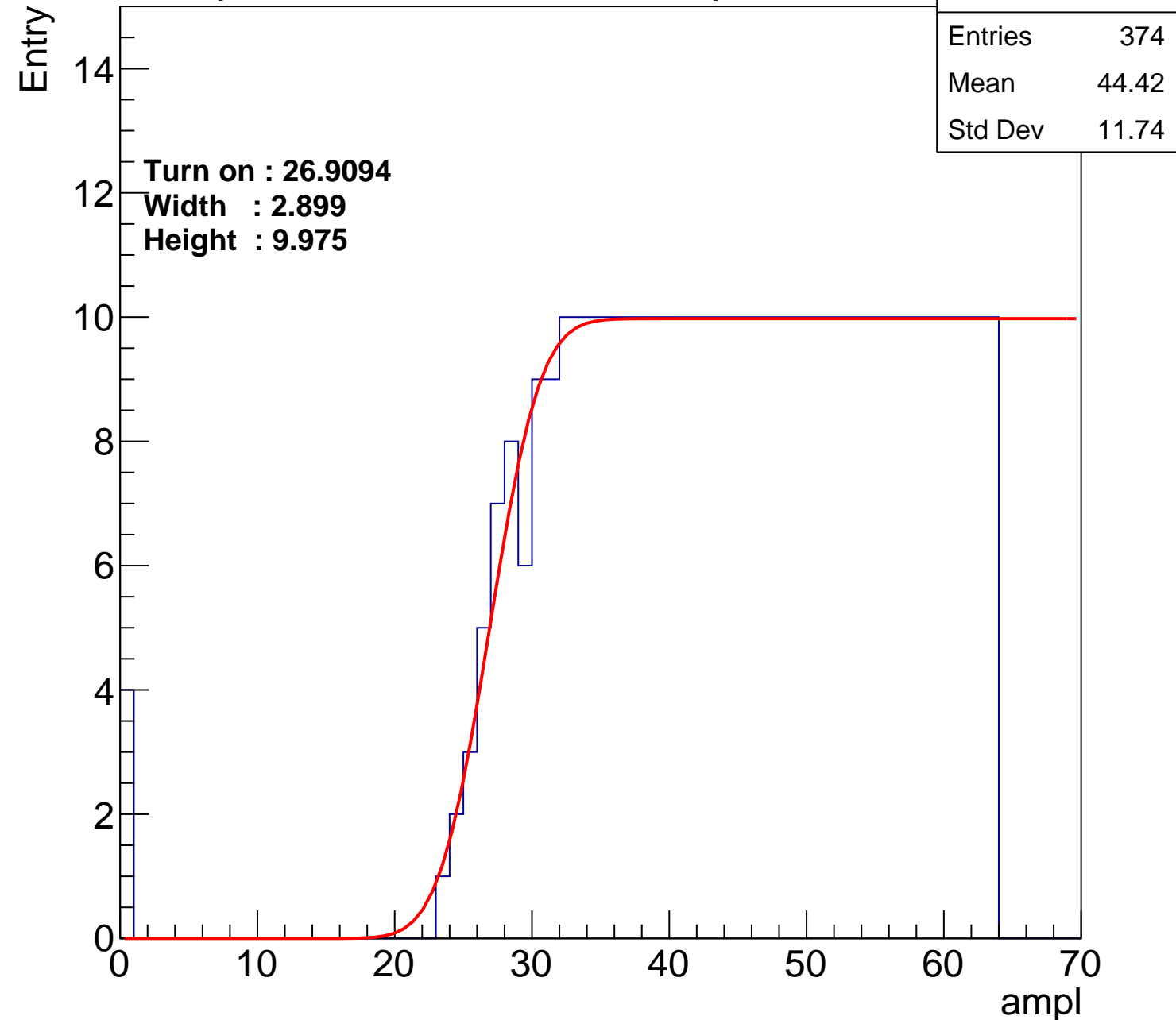
Width : 2.899

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch91

calib_packv5_042523_0143.root, FC#10, port B3

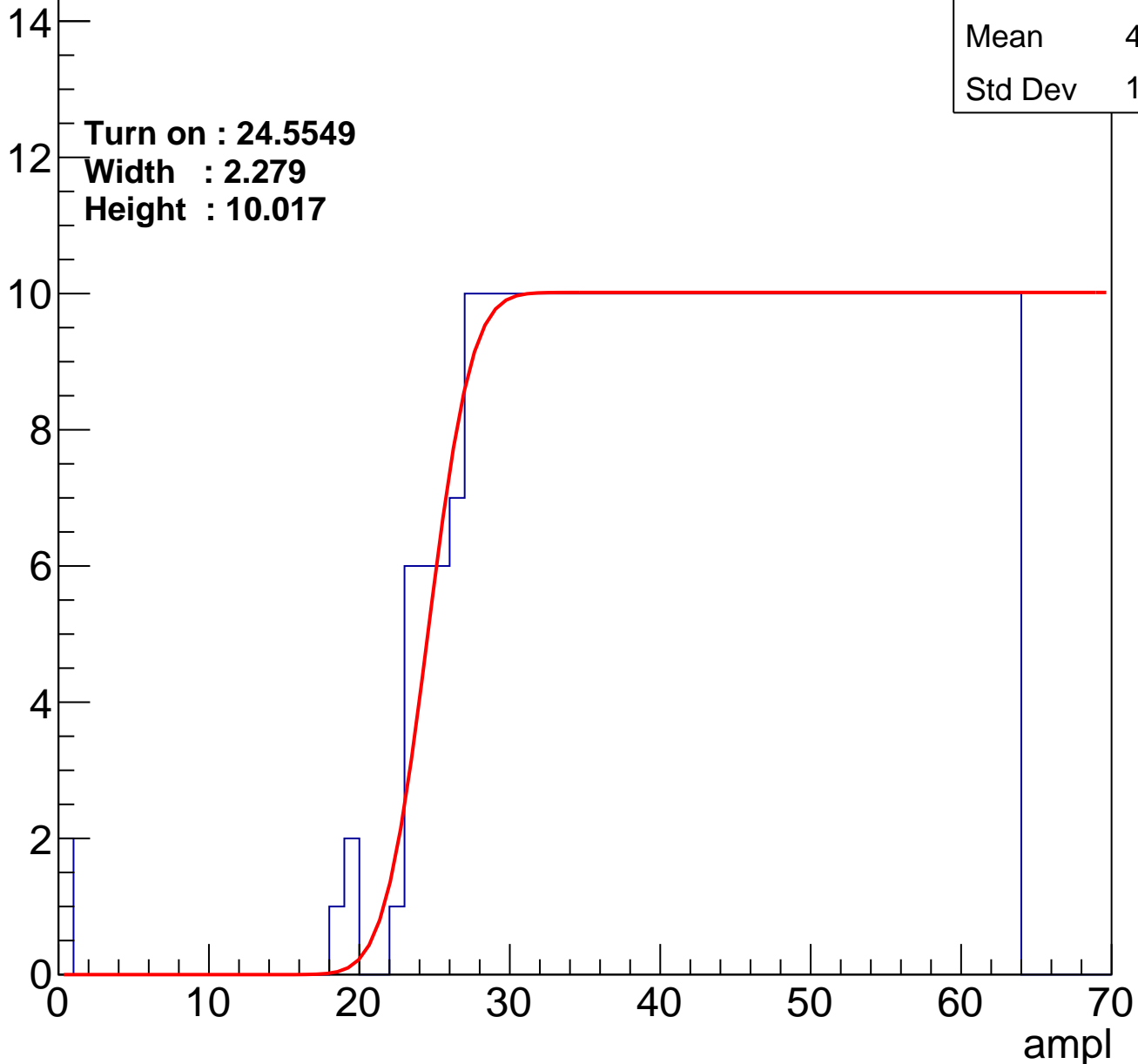
Entries	401
Mean	43.25
Std Dev	12.04

Turn on : 24.5549

Width : 2.279

Height : 10.017

Entry



B1L002S, U24-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.37
Std Dev	11.82

Turn on : 24.0109

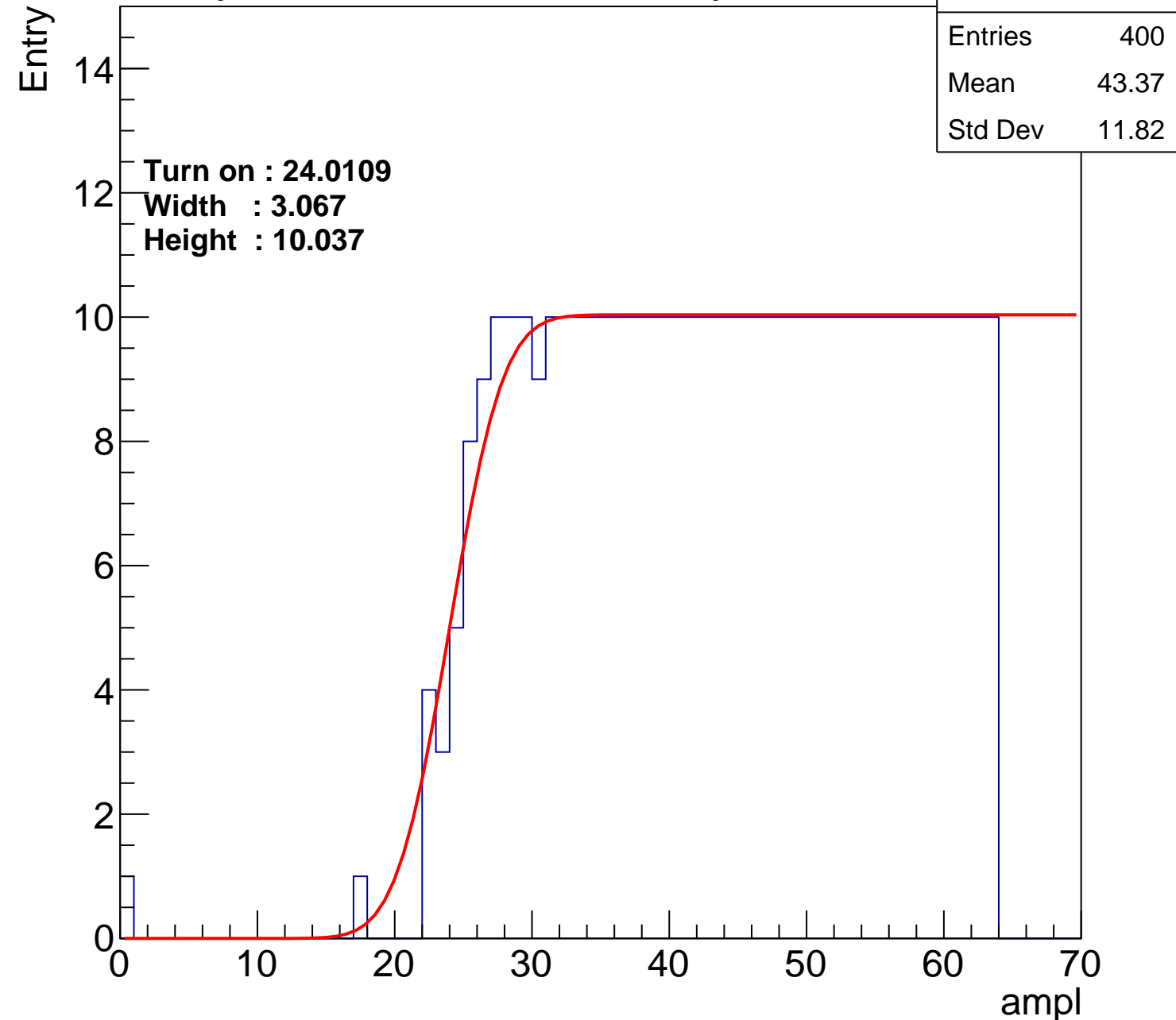
Width : 3.067

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.95
Std Dev	11.76

Turn on : 25.6318

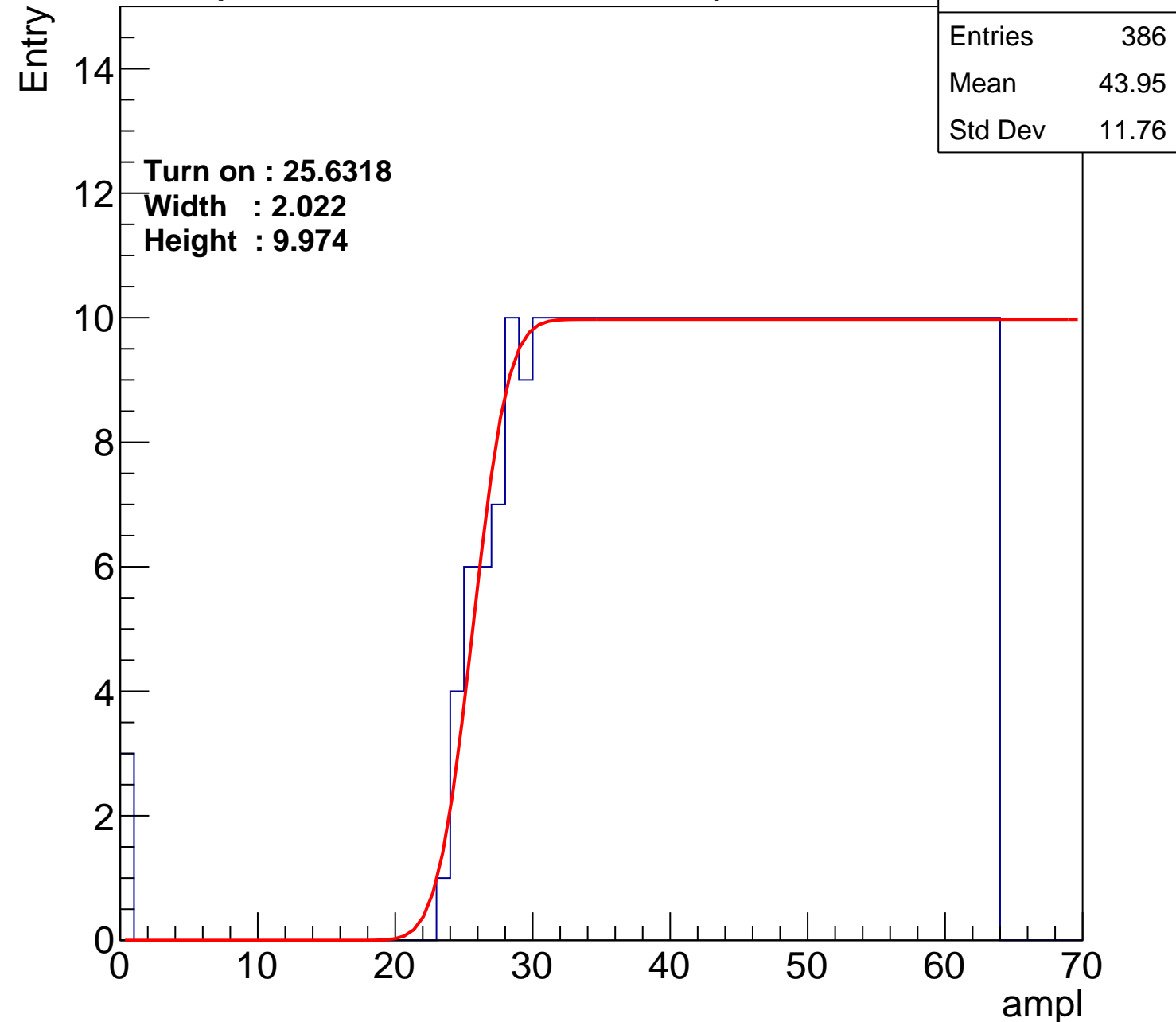
Width : 2.022

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch94

calib_packv5_042523_0143.root, FC#10, port B3

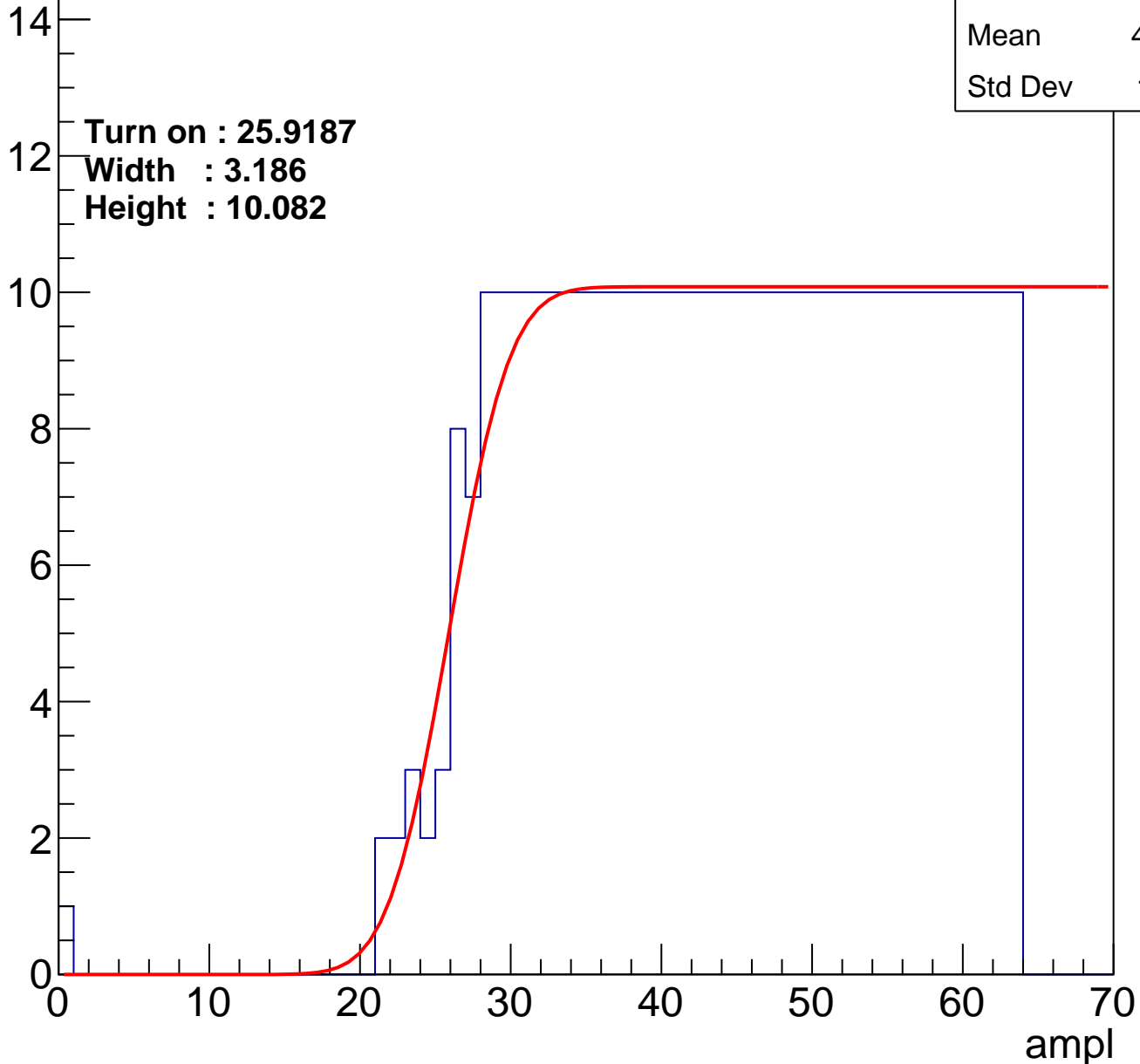
Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 25.9187

Width : 3.186

Height : 10.082

Entry



B1L002S, U24-ch95

calib_packv5_042523_0143.root, FC#10, port B3

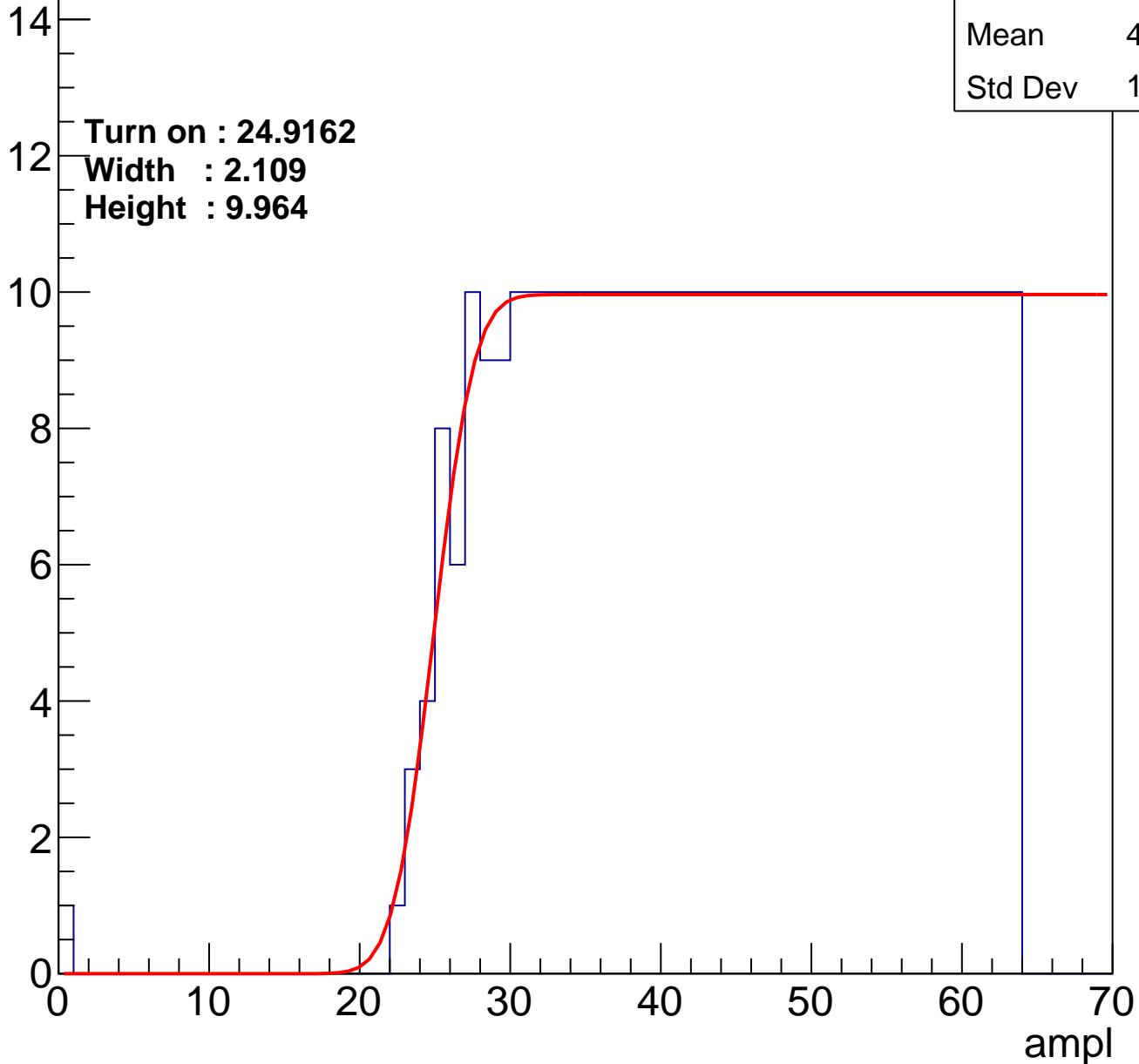
Entries	391
Mean	43.83
Std Dev	11.56

Turn on : 24.9162

Width : 2.109

Height : 9.964

Entry



B1L002S, U24-ch96

calib_packv5_042523_0143.root, FC#10, port B3

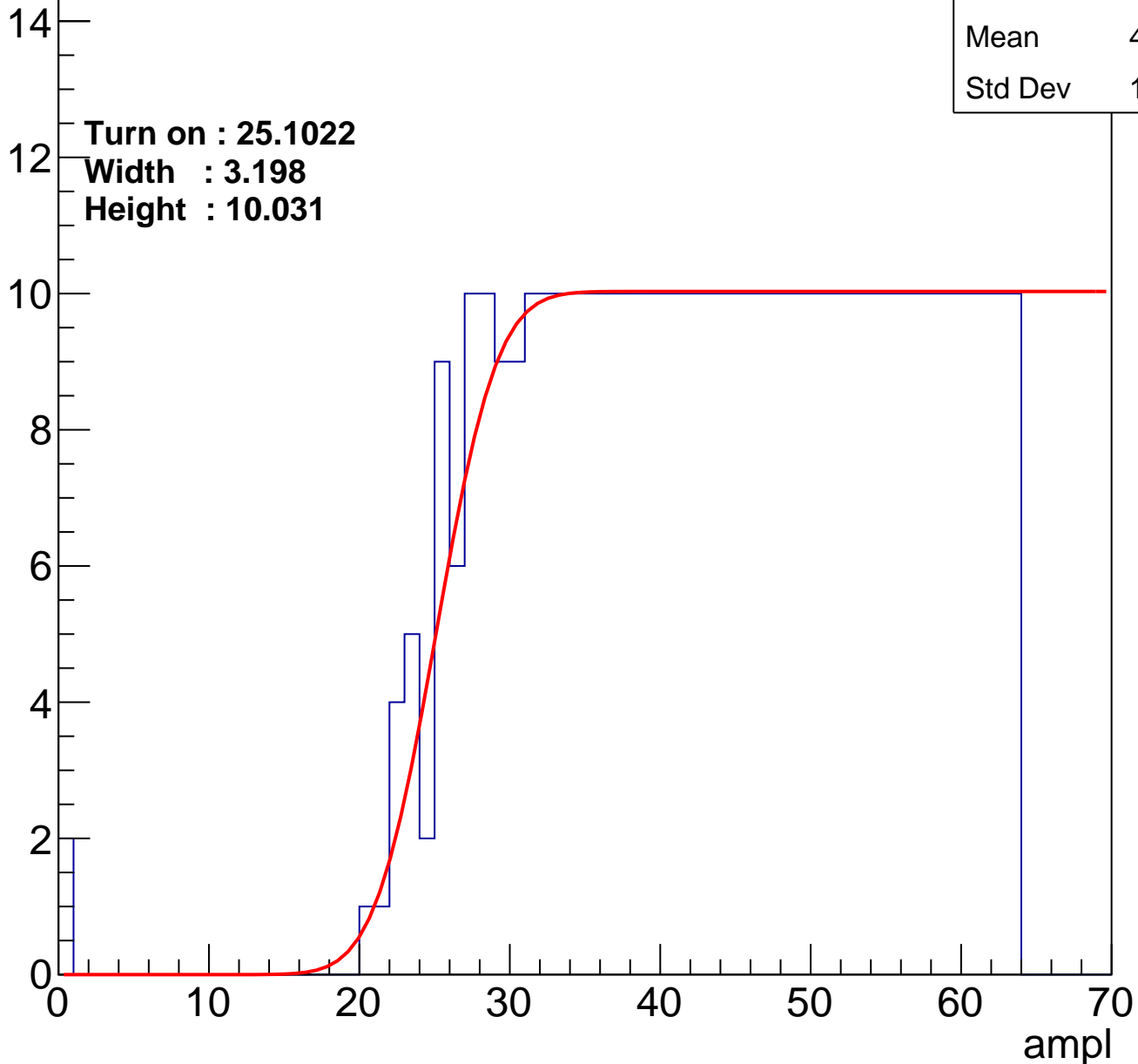
Entries	398
Mean	43.38
Std Dev	11.98

Turn on : 25.1022

Width : 3.198

Height : 10.031

Entry



B1L002S, U24-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.52
Std Dev	11.32

Turn on : 26.5788

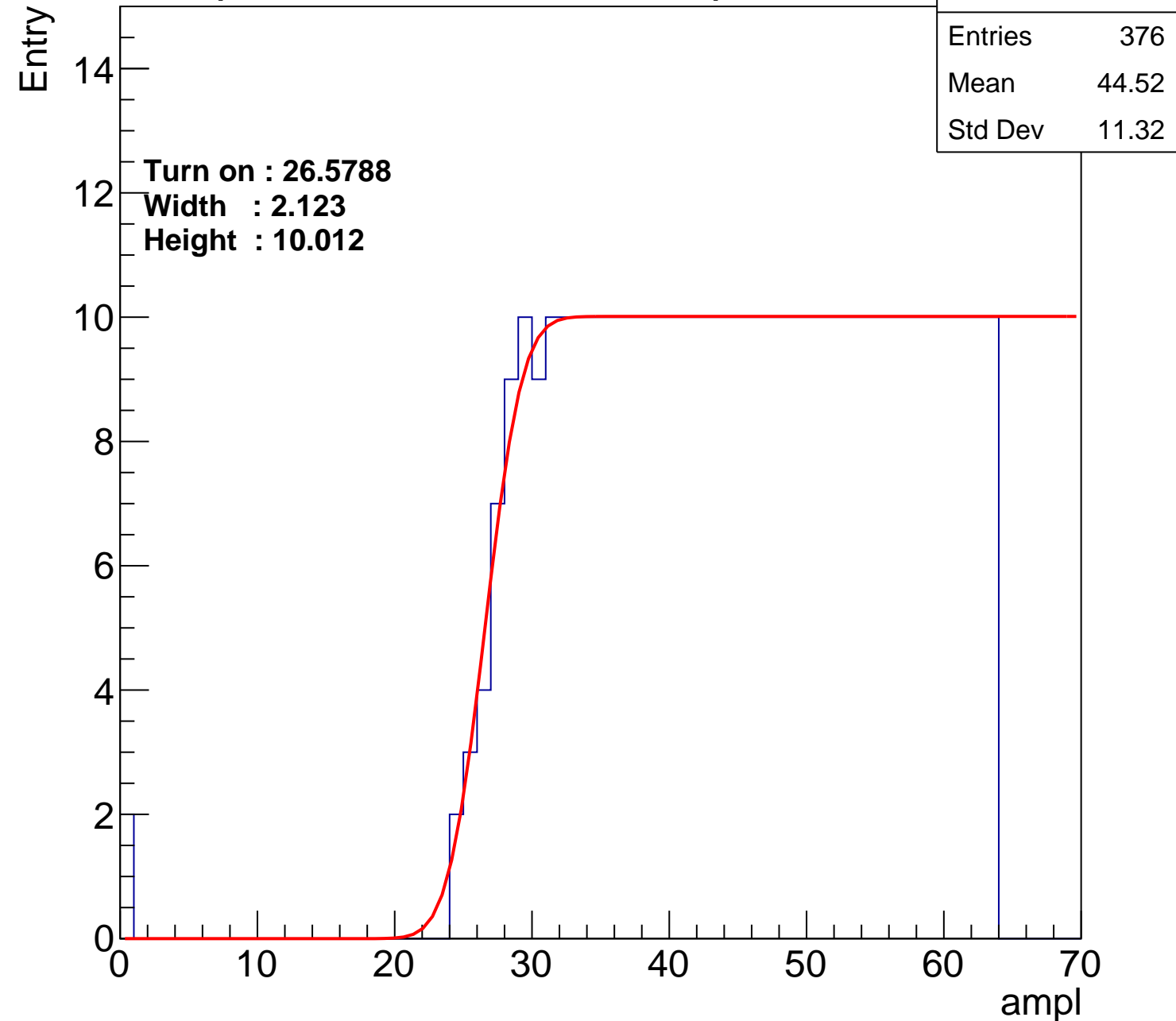
Width : 2.123

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.9
Std Dev	11.54

Turn on : 25.5351

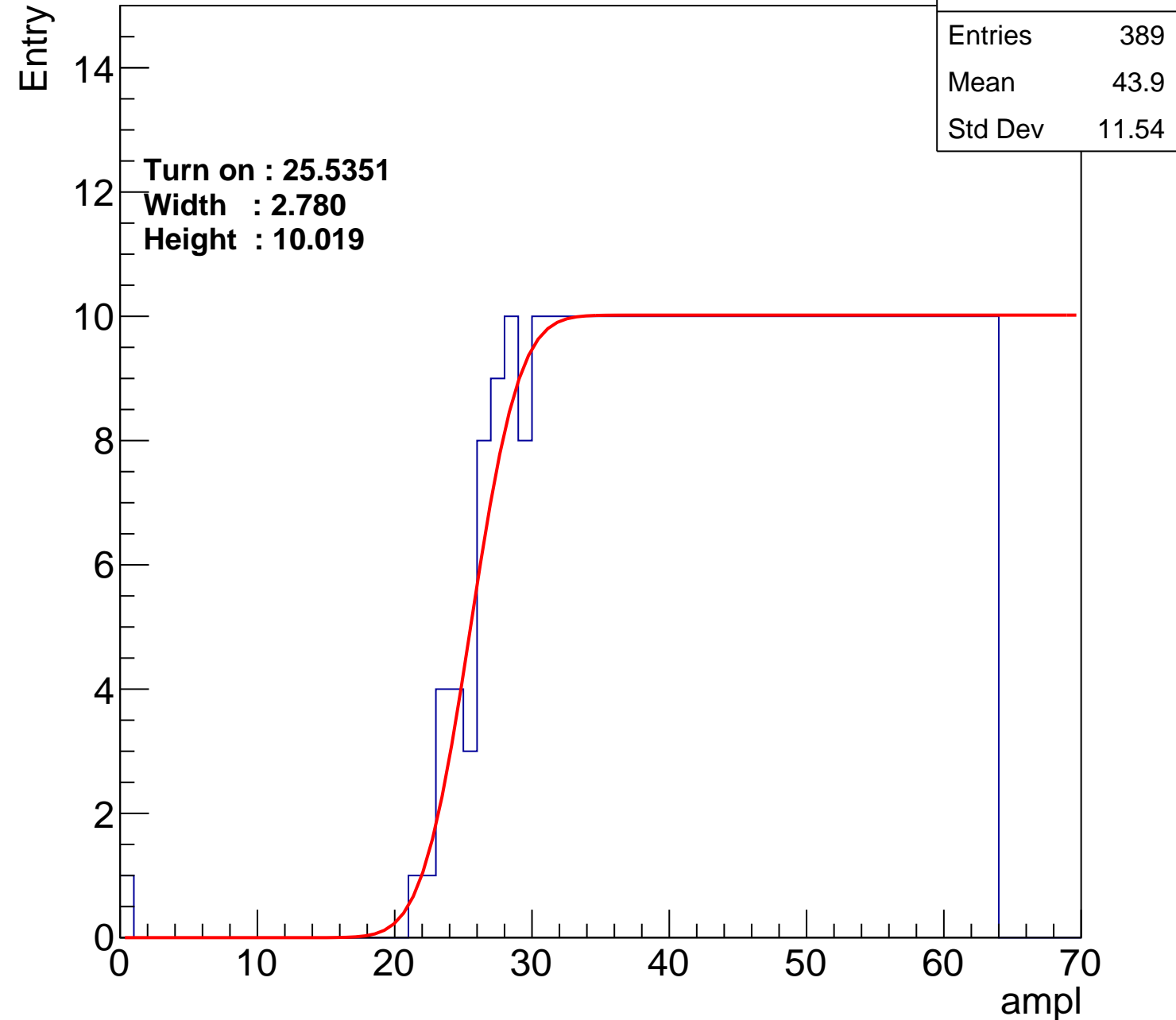
Width : 2.780

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch99

calib_packv5_042523_0143.root, FC#10, port B3

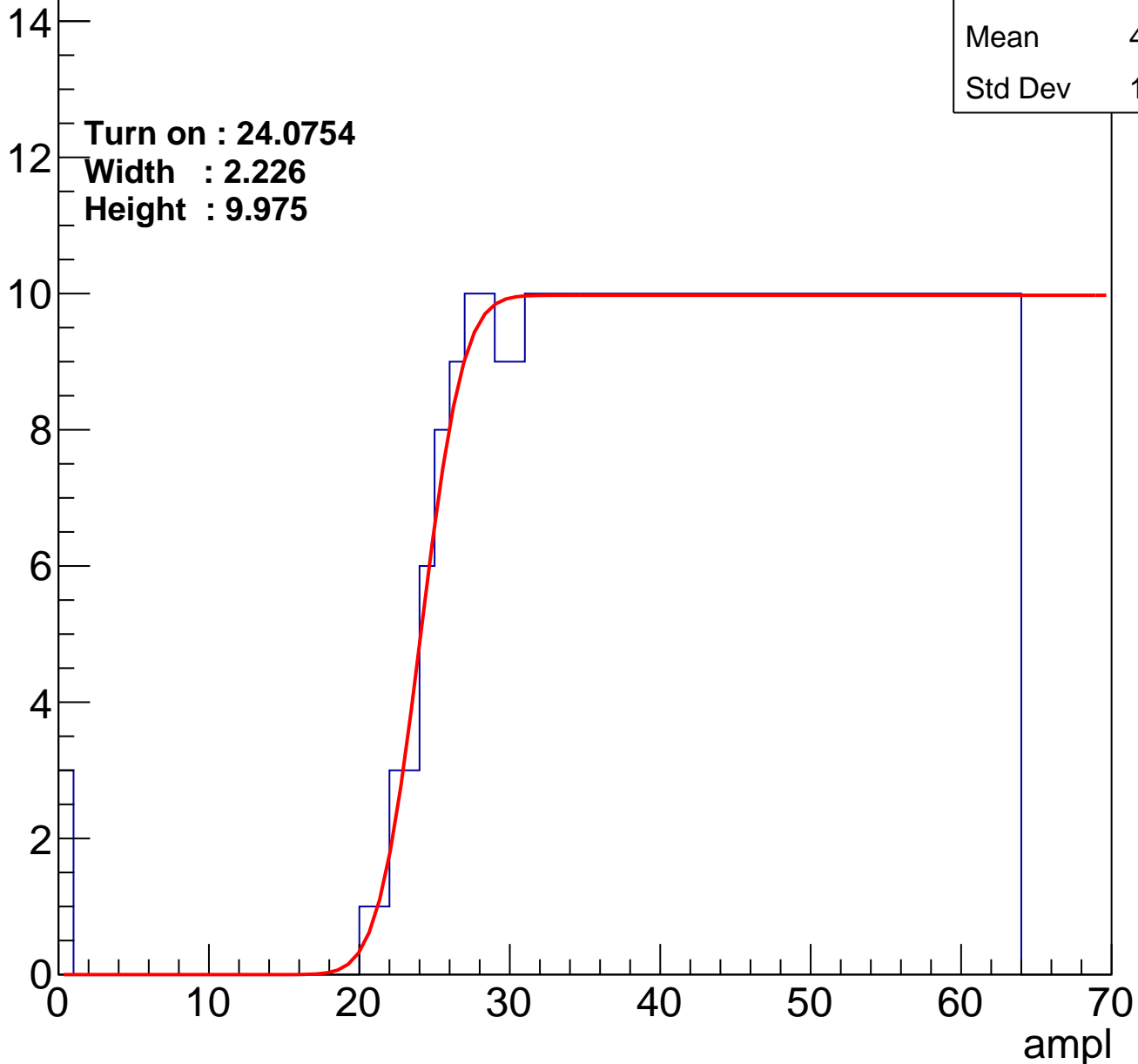
Entries	402
Mean	43.15
Std Dev	12.19

Turn on : 24.0754

Width : 2.226

Height : 9.975

Entry



B1L002S, U24-ch100

calib_packv5_042523_0143.root, FC#10, port B3

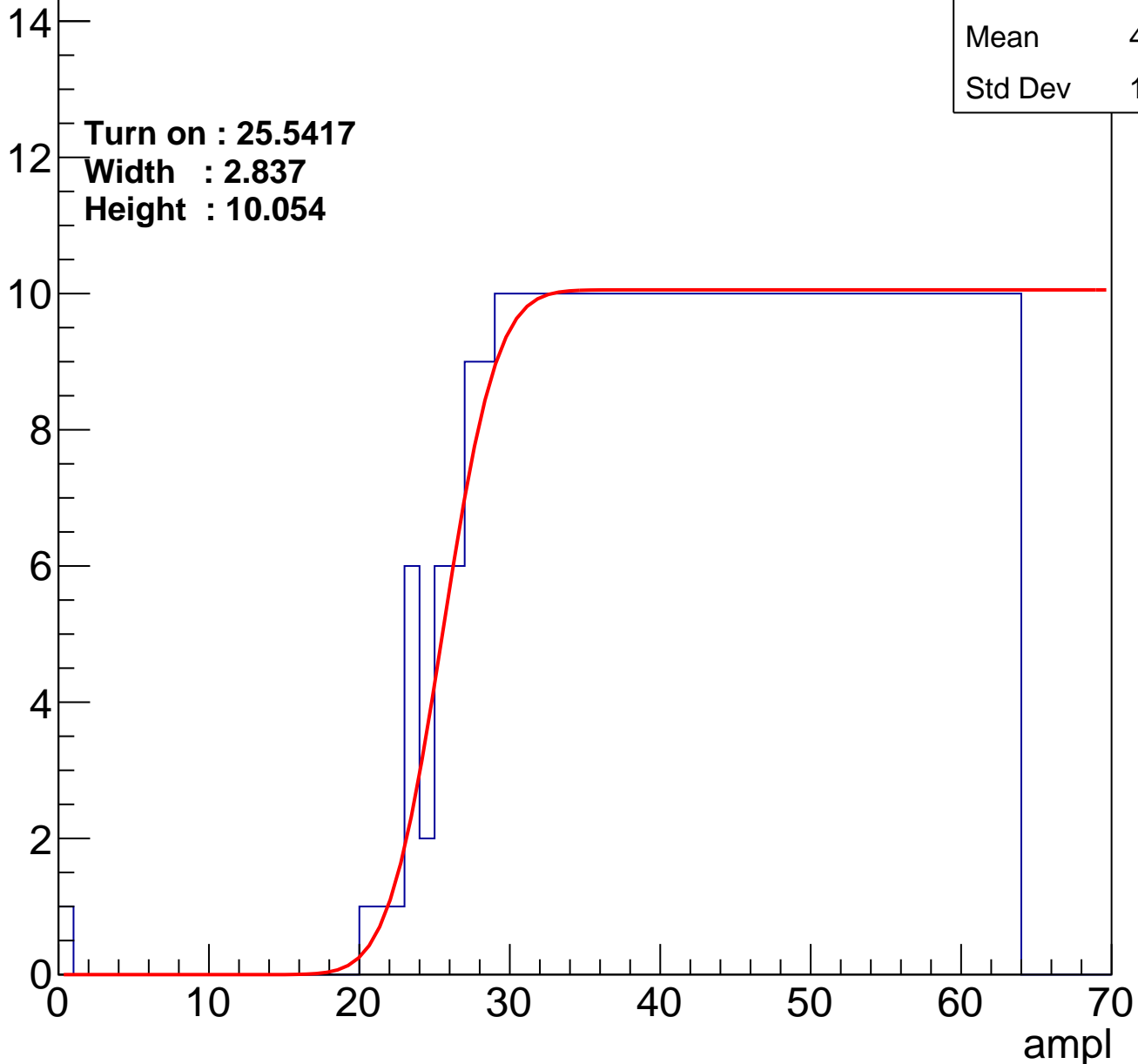
Entries	392
Mean	43.75
Std Dev	11.63

Turn on : 25.5417

Width : 2.837

Height : 10.054

Entry



B1L002S, U24-ch101

calib_packv5_042523_0143.root, FC#10, port B3

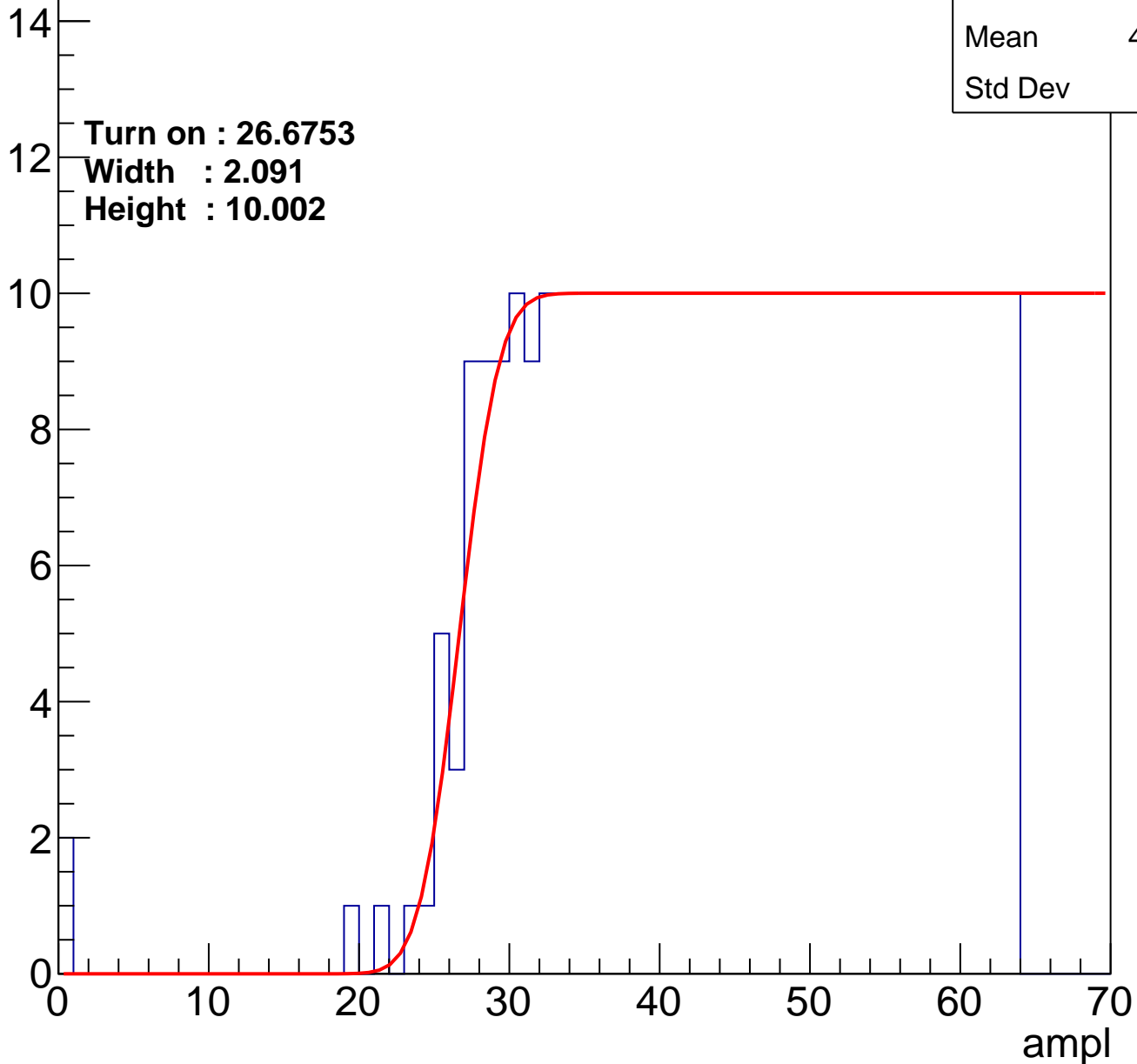
Entries	380
Mean	44.28
Std Dev	11.5

Turn on : 26.6753

Width : 2.091

Height : 10.002

Entry



B1L002S, U24-ch102

calib_packv5_042523_0143.root, FC#10, port B3

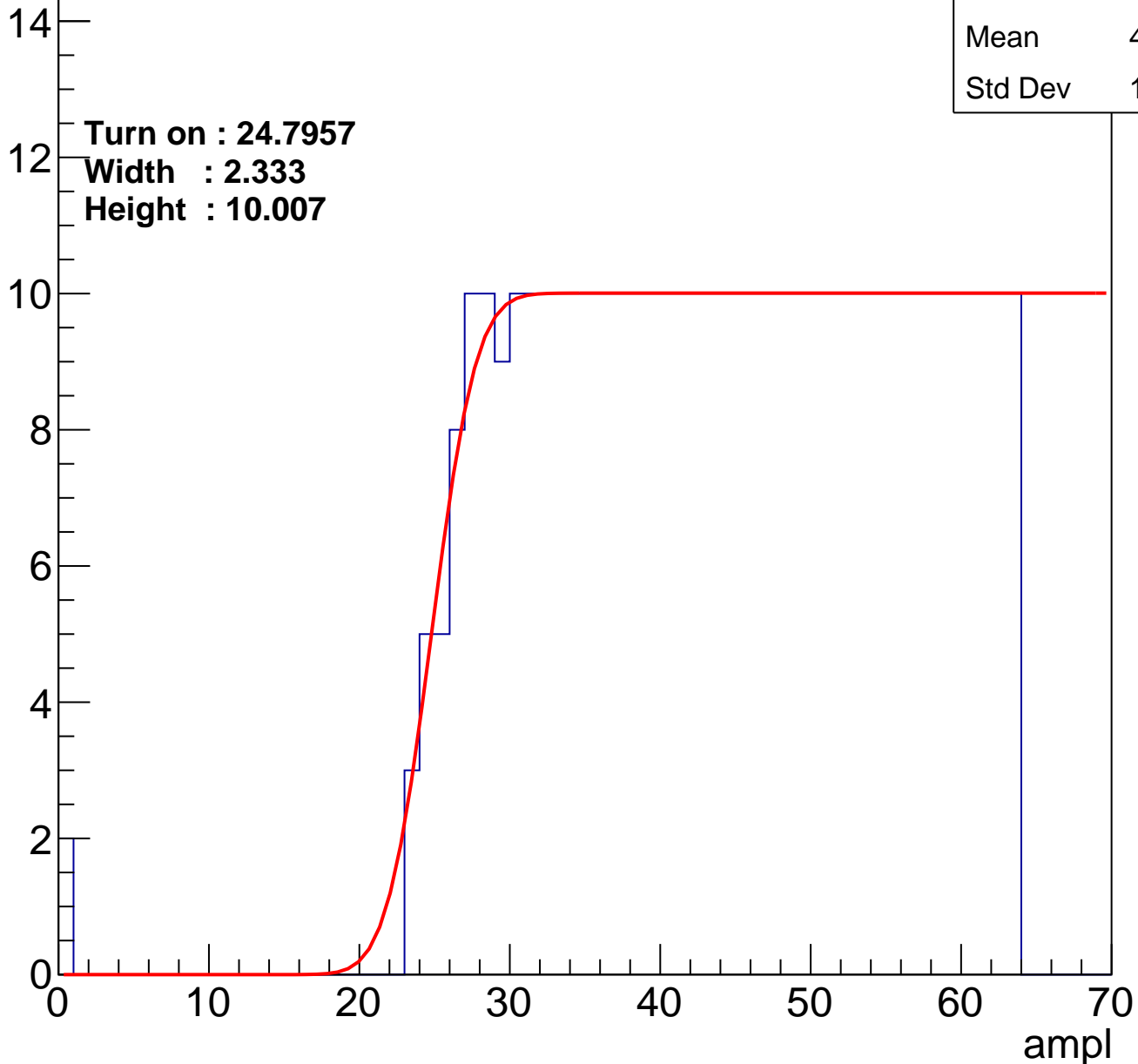
Entries	392
Mean	43.73
Std Dev	11.72

Turn on : 24.7957

Width : 2.333

Height : 10.007

Entry



B1L002S, U24-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.57
Std Dev	11.71

Turn on : 24.7544

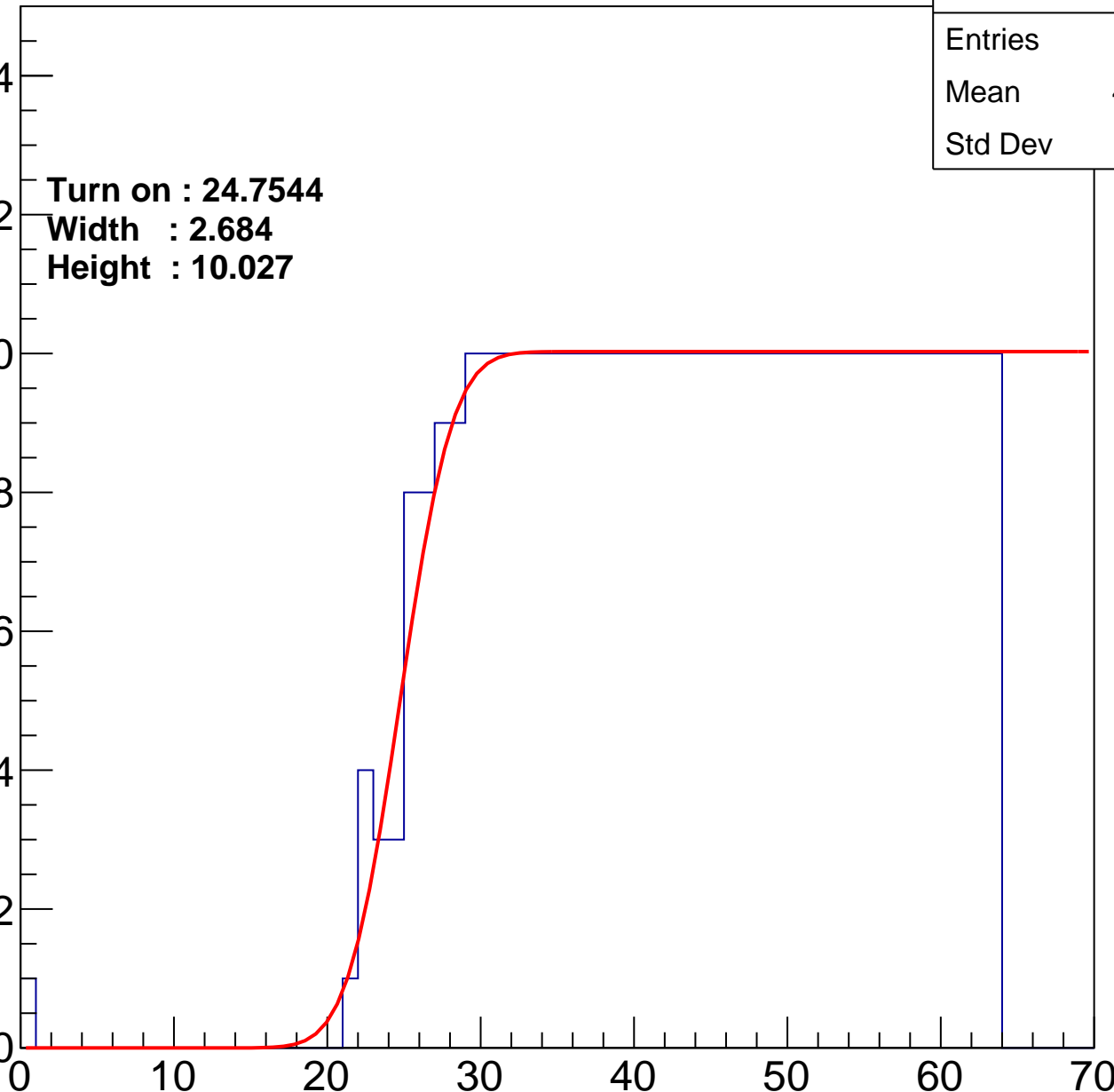
Width : 2.684

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.02
Std Dev	11.58

Turn on : 25.6654

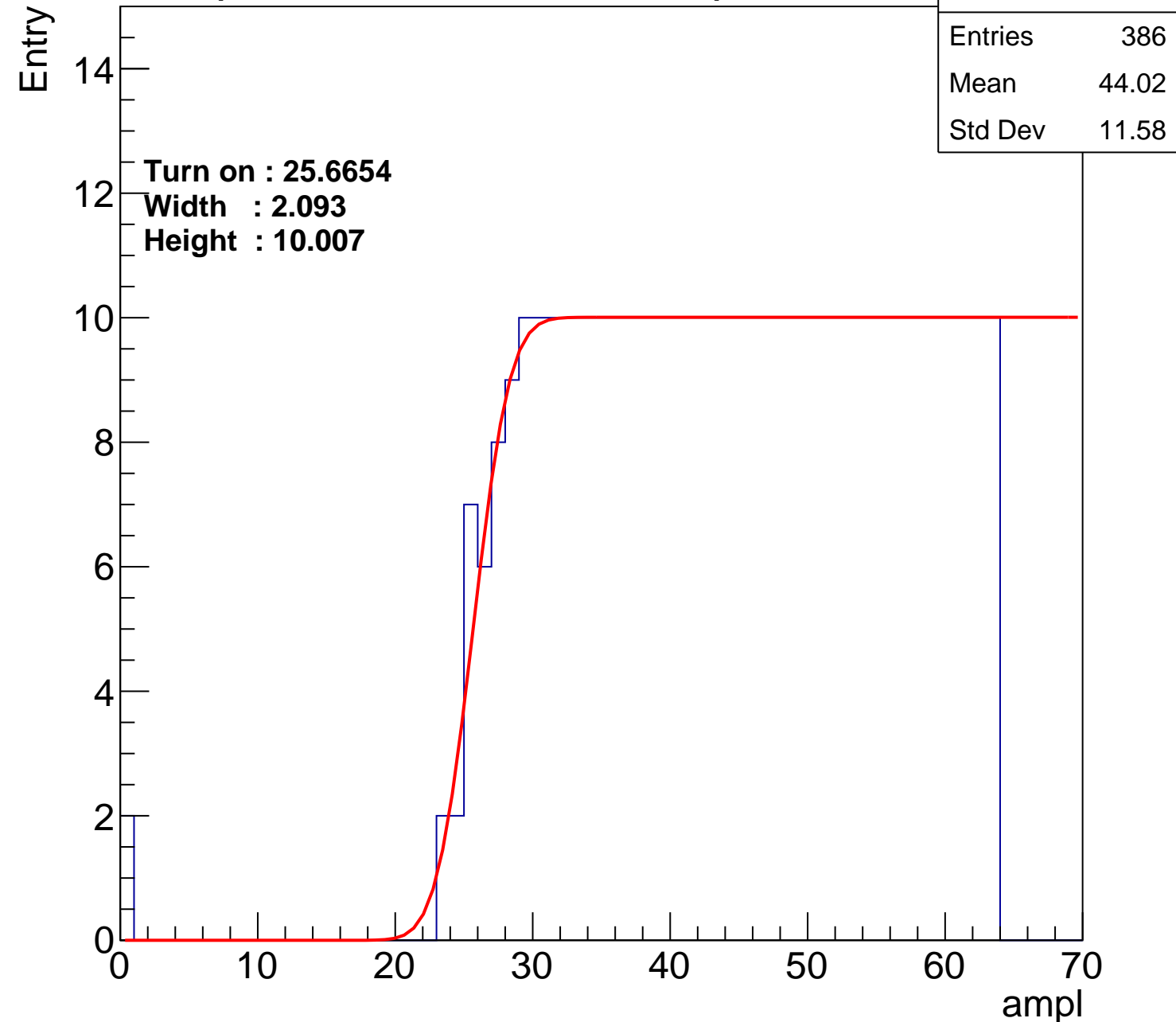
Width : 2.093

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch105

calib_packv5_042523_0143.root, FC#10, port B3

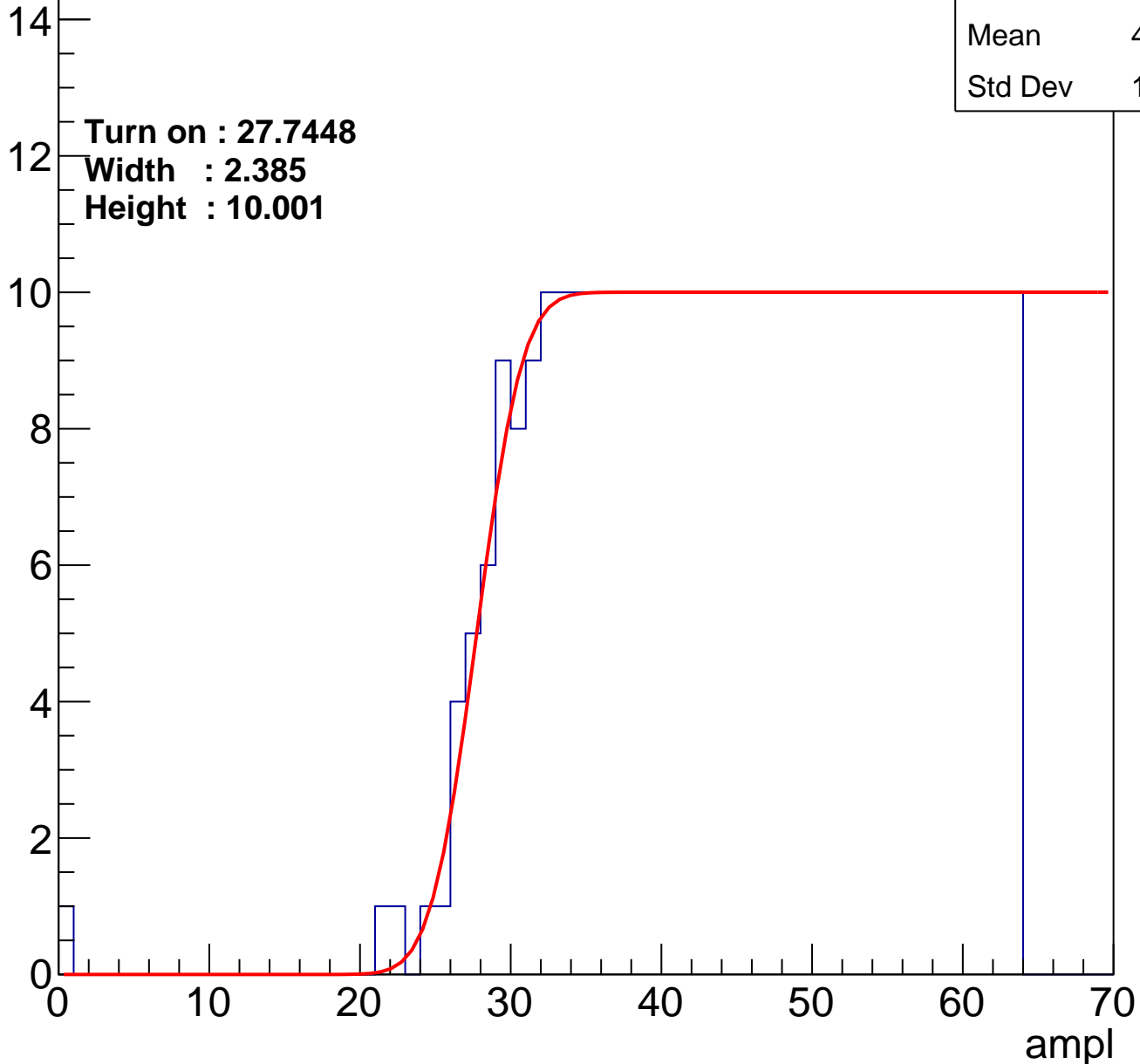
Entries	366
Mean	45.02
Std Dev	10.95

Turn on : 27.7448

Width : 2.385

Height : 10.001

Entry



B1L002S, U24-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.04
Std Dev	11.49

Turn on : 25.7769

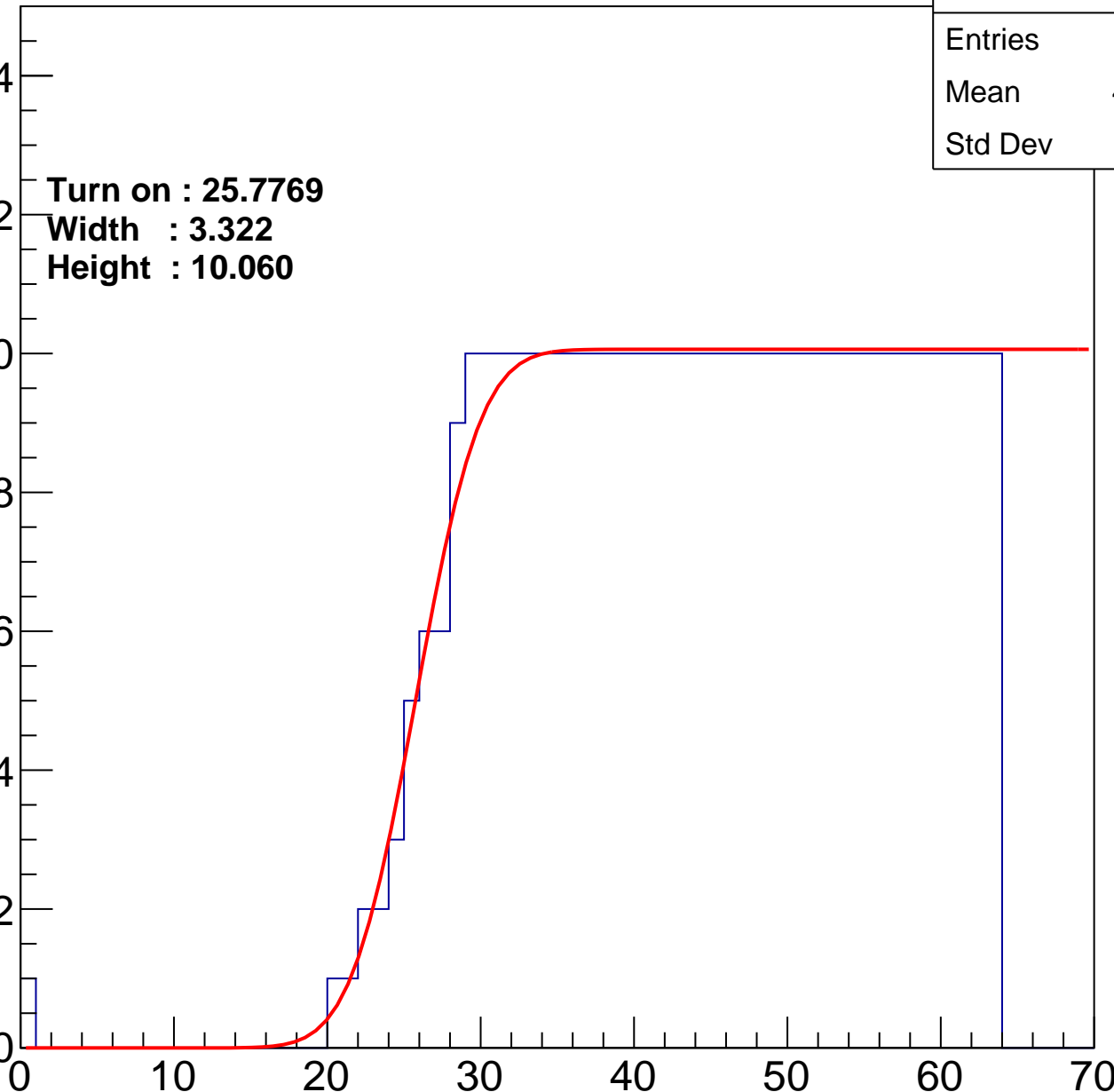
Width : 3.322

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch107

calib_packv5_042523_0143.root, FC#10, port B3

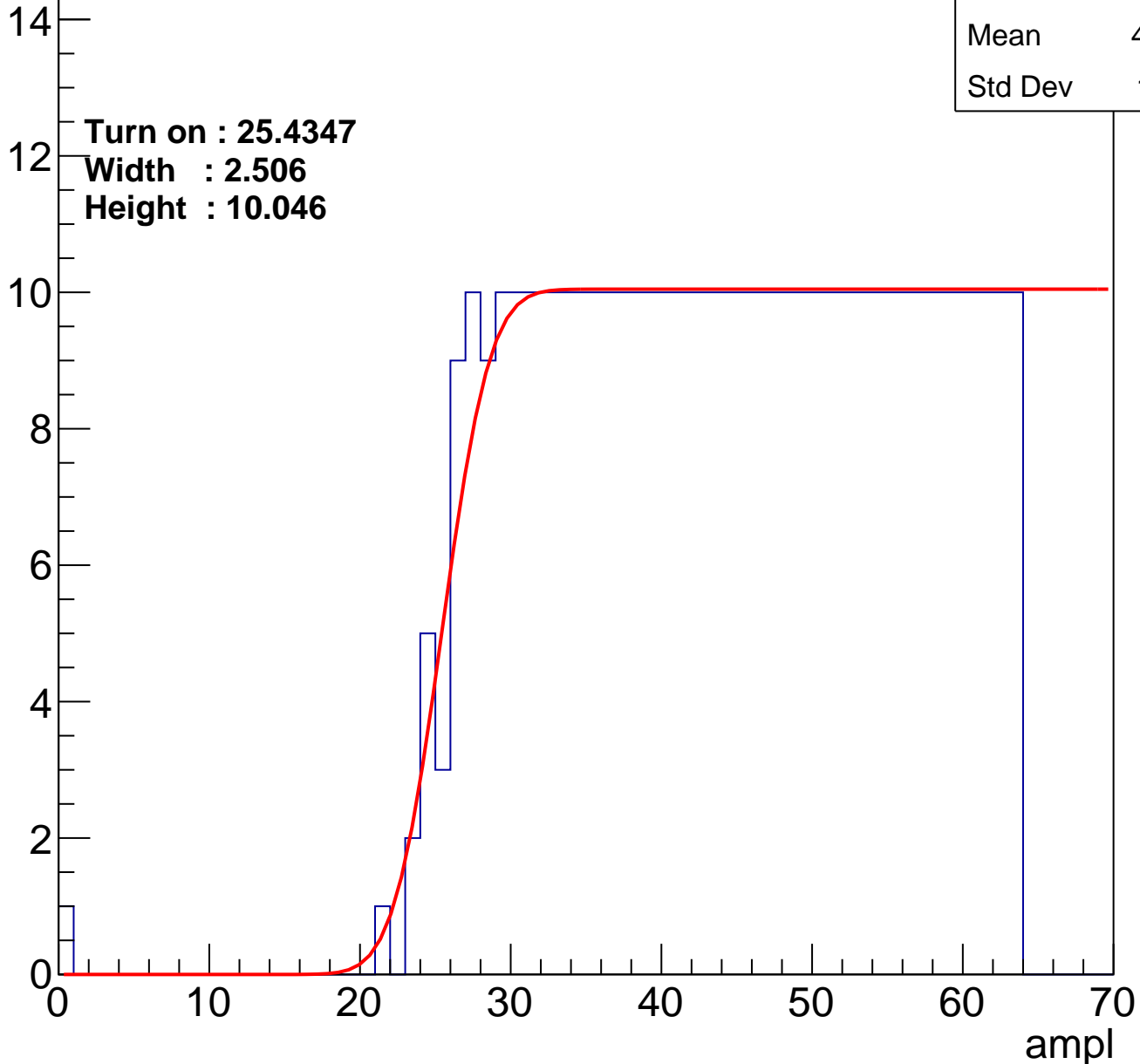
Entries	390
Mean	43.89
Std Dev	11.51

Turn on : 25.4347

Width : 2.506

Height : 10.046

Entry



B1L002S, U24-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.17
Std Dev	11.96

Turn on : 24.0431

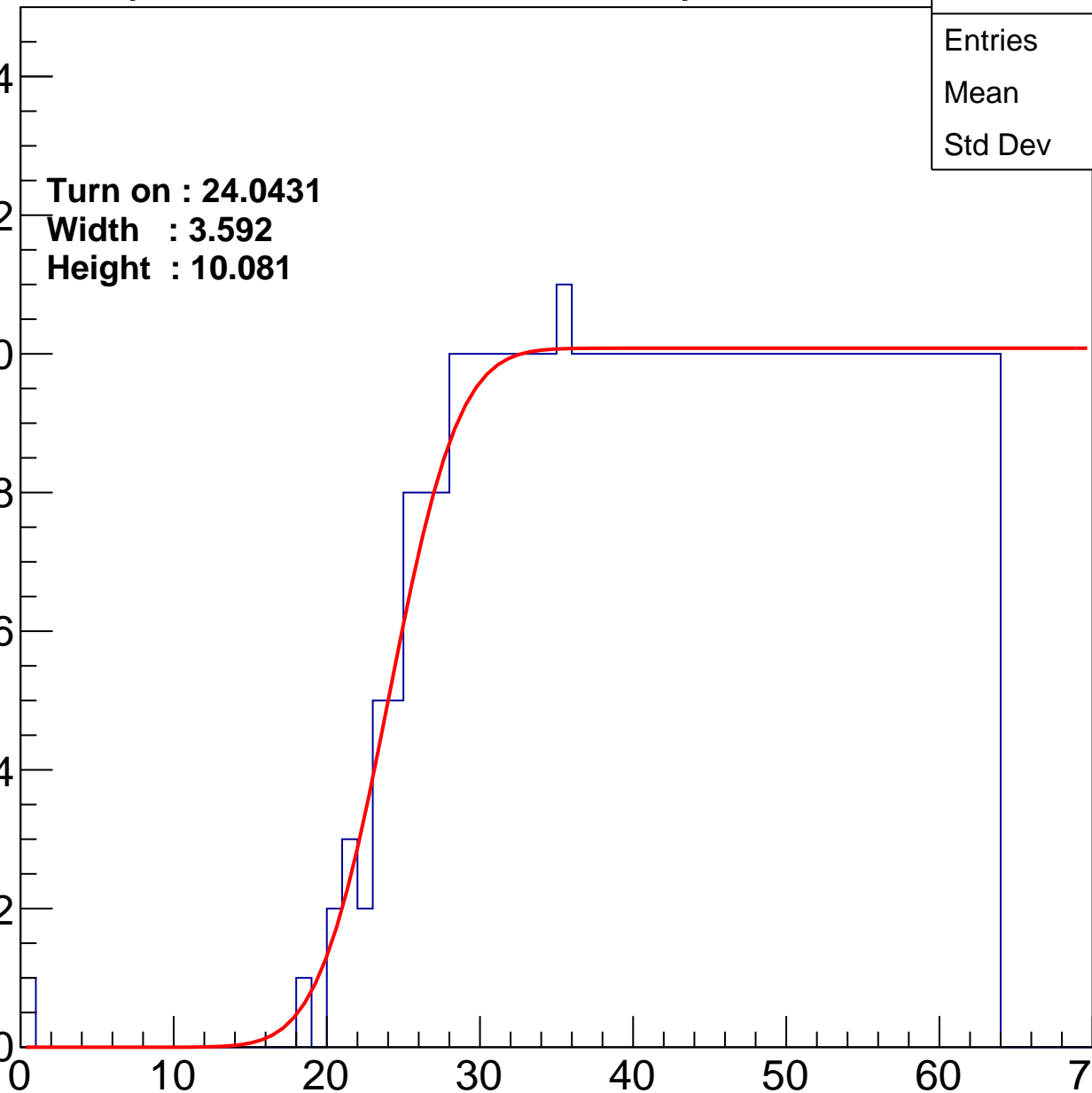
Width : 3.592

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch109

calib_packv5_042523_0143.root, FC#10, port B3

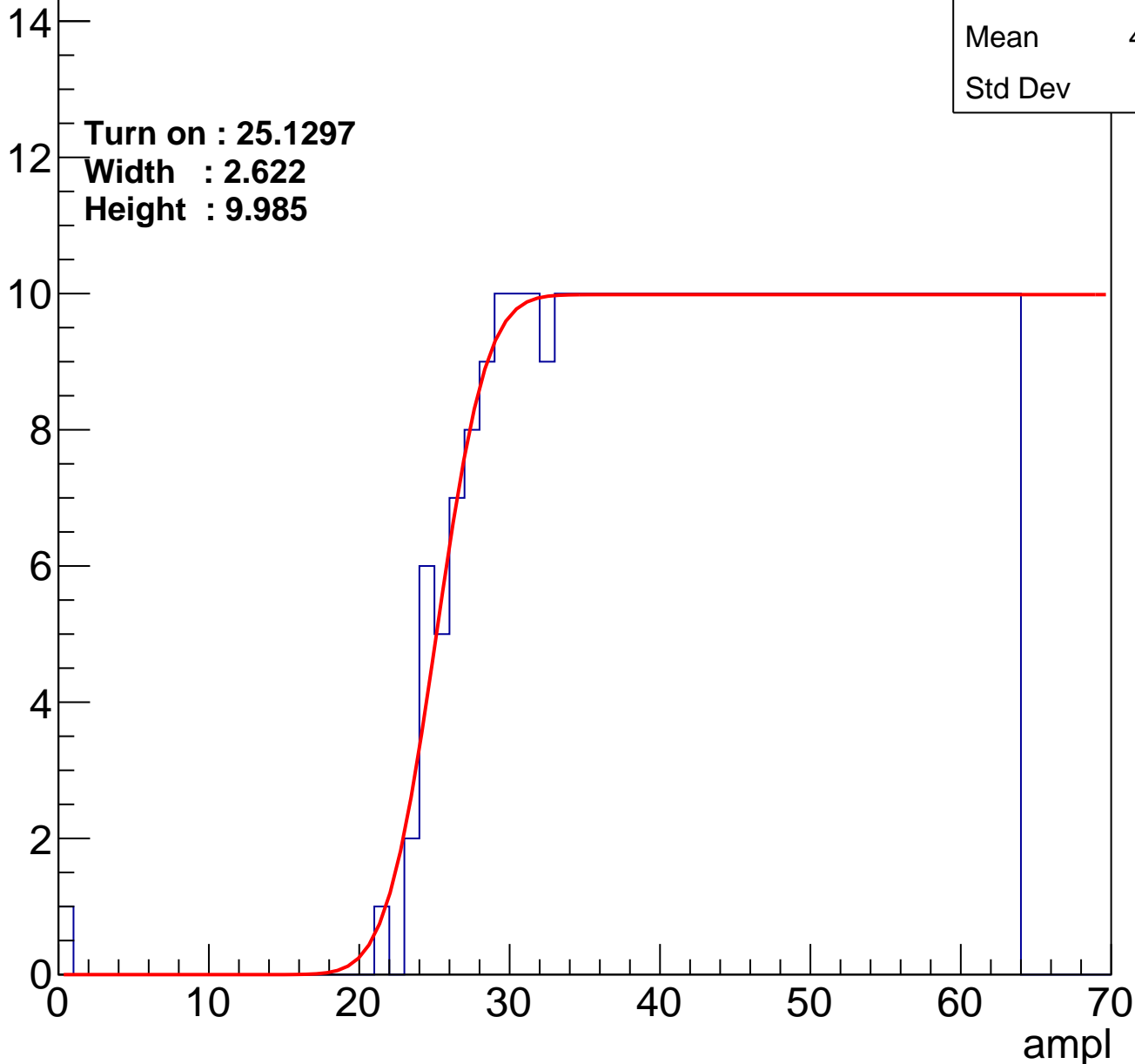
Entries	388
Mean	43.95
Std Dev	11.51

Turn on : 25.1297

Width : 2.622

Height : 9.985

Entry



B1L002S, U24-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.59
Std Dev	11.8

Turn on : 24.7561

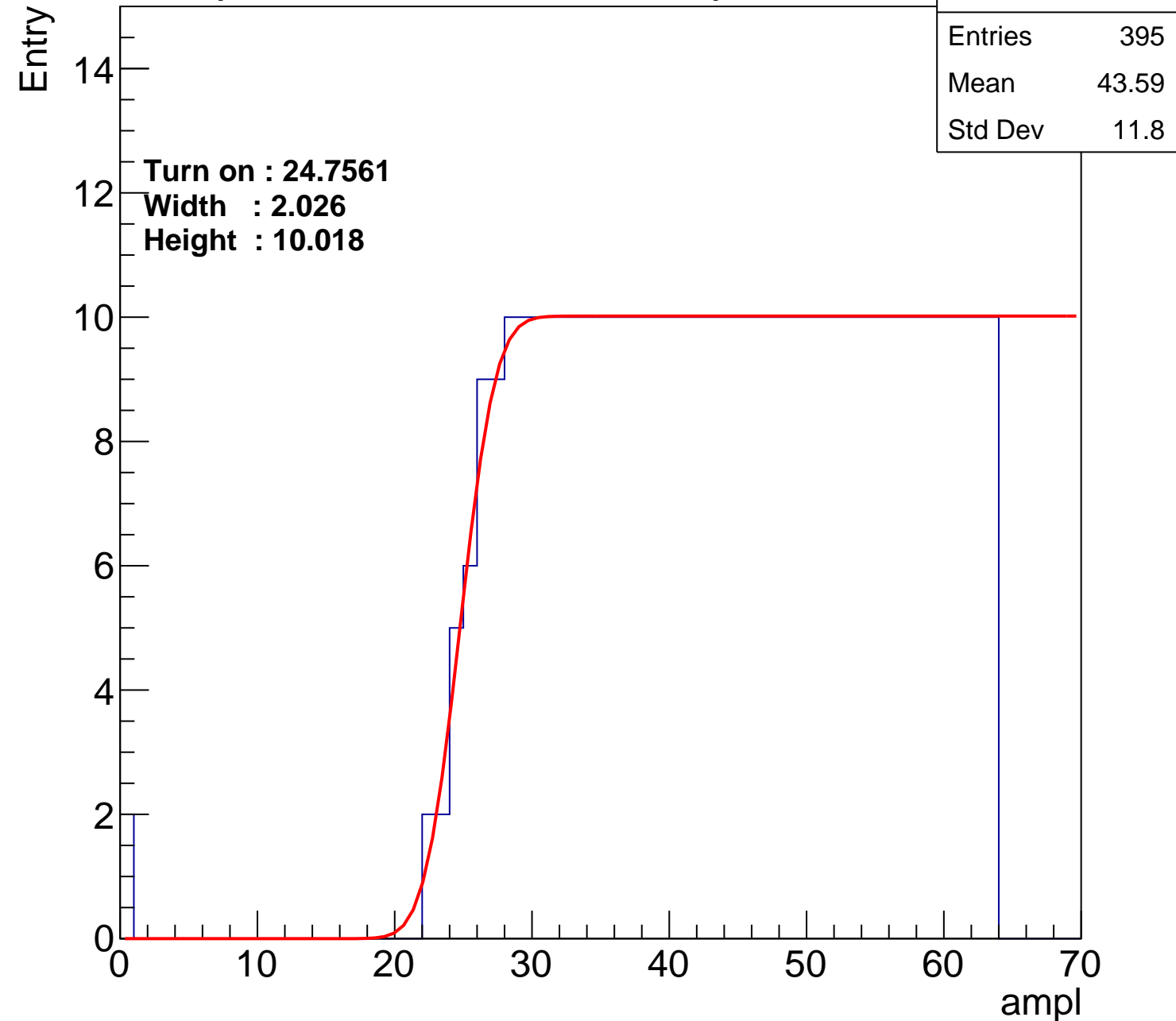
Width : 2.026

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	358
Mean	45.38
Std Dev	10.8

Turn on : 28.7209

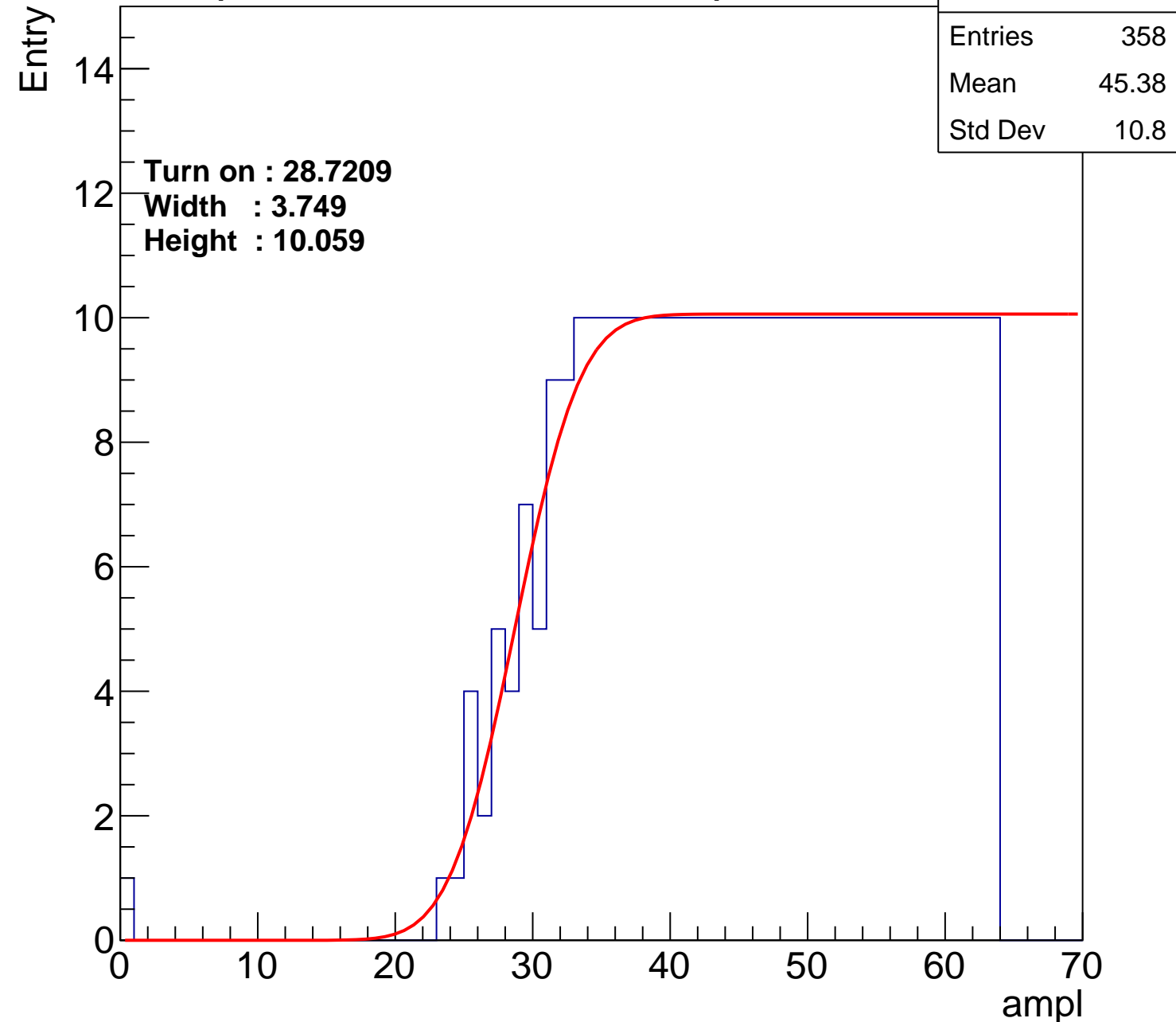
Width : 3.749

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
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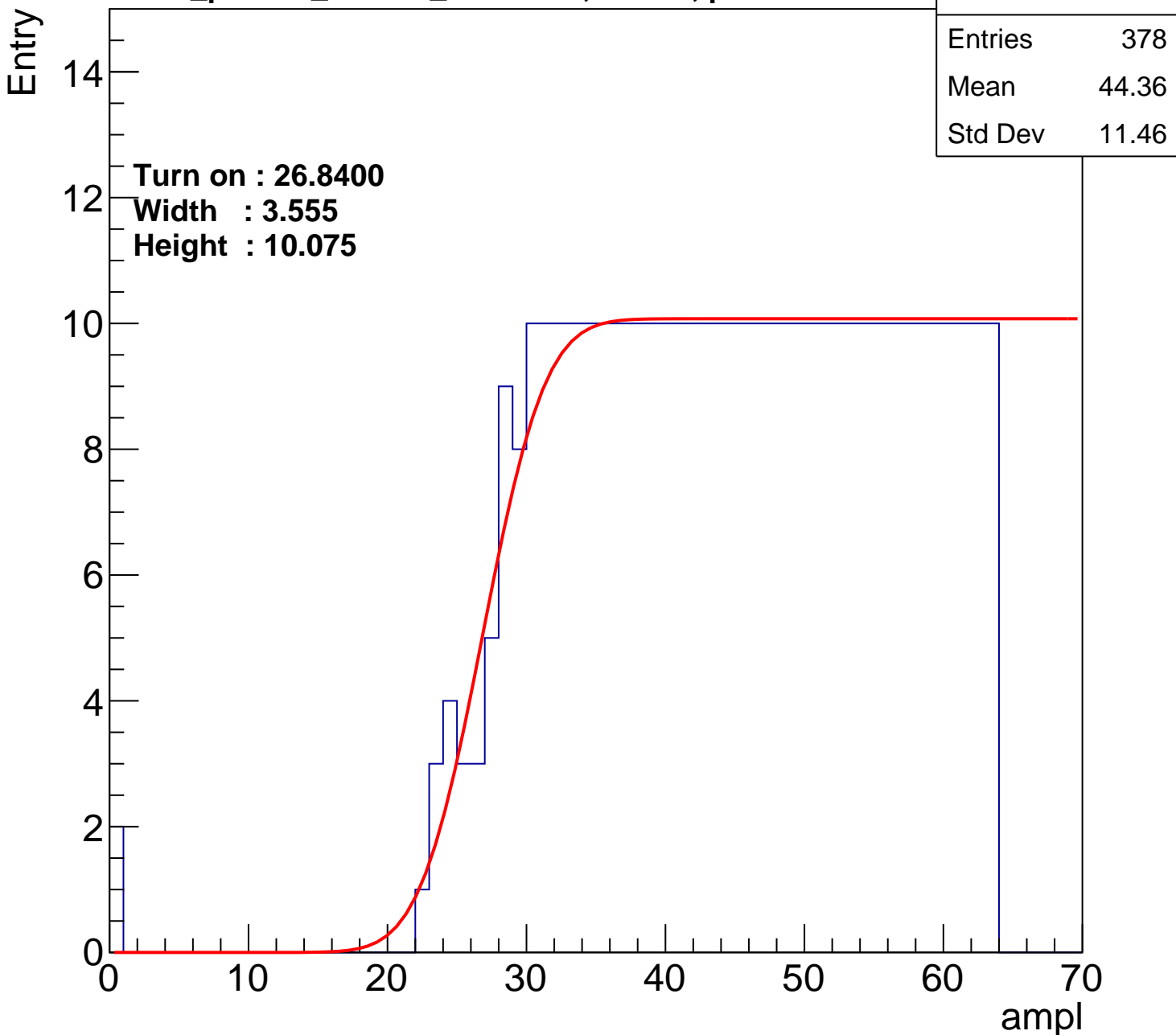
Mean	44.36
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Std Dev	11.46
---------	-------

Turn on : 26.8400

Width : 3.555

Height : 10.075



B1L002S, U24-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.78
Std Dev	11.7

Turn on : 25.2956

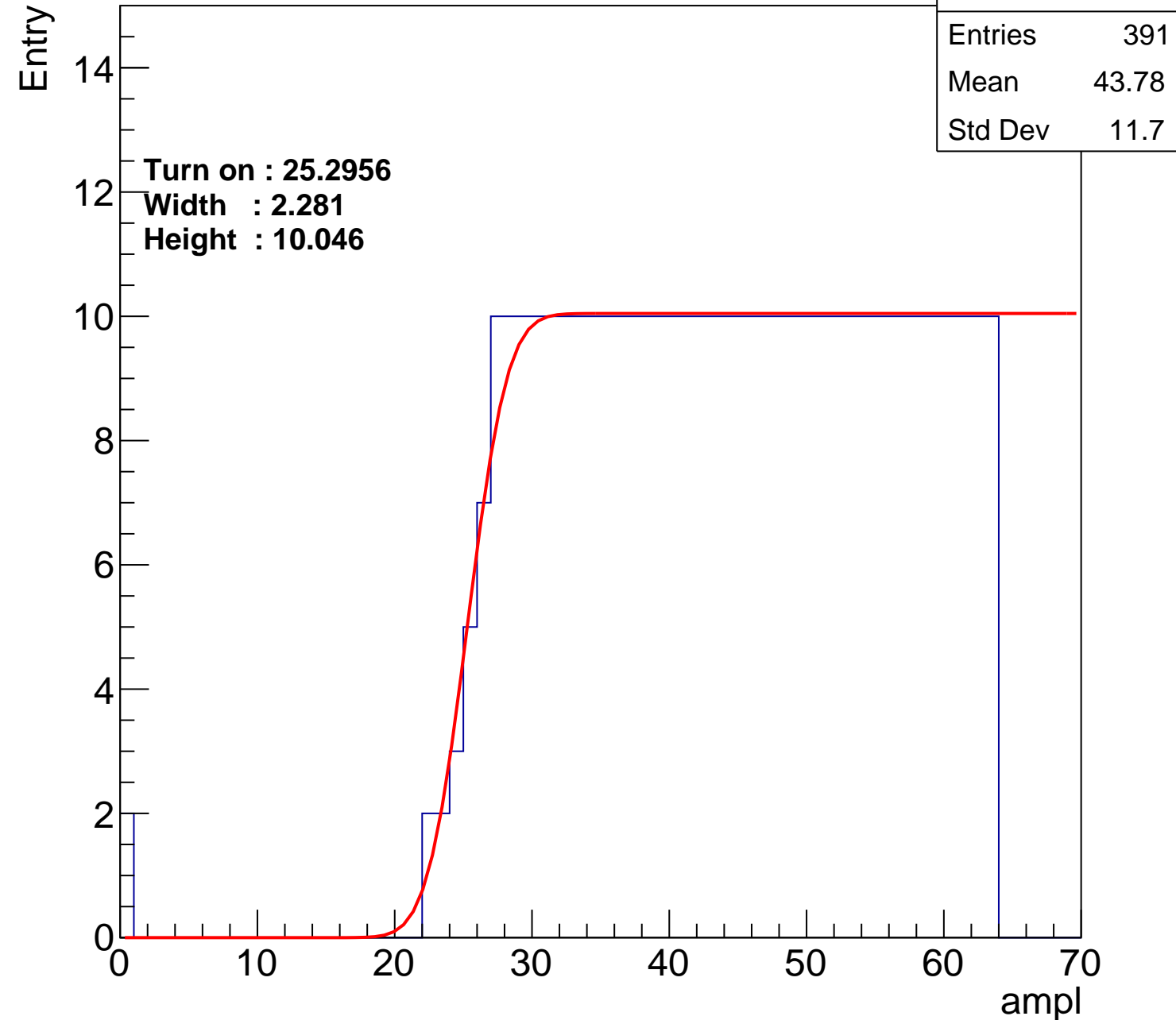
Width : 2.281

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.37
Std Dev	11.82

Turn on : 24.4777

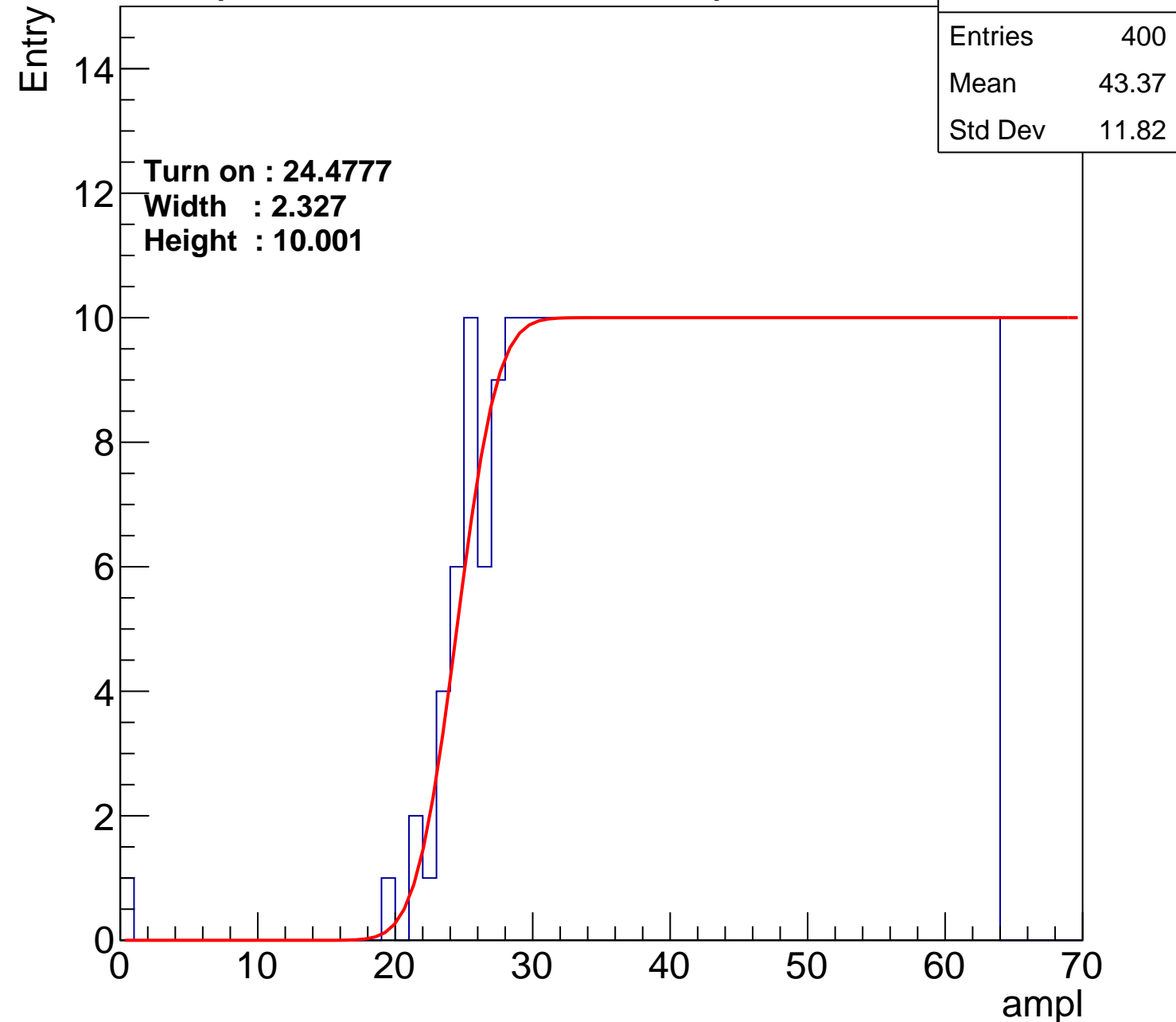
Width : 2.327

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch115

calib_packv5_042523_0143.root, FC#10, port B3

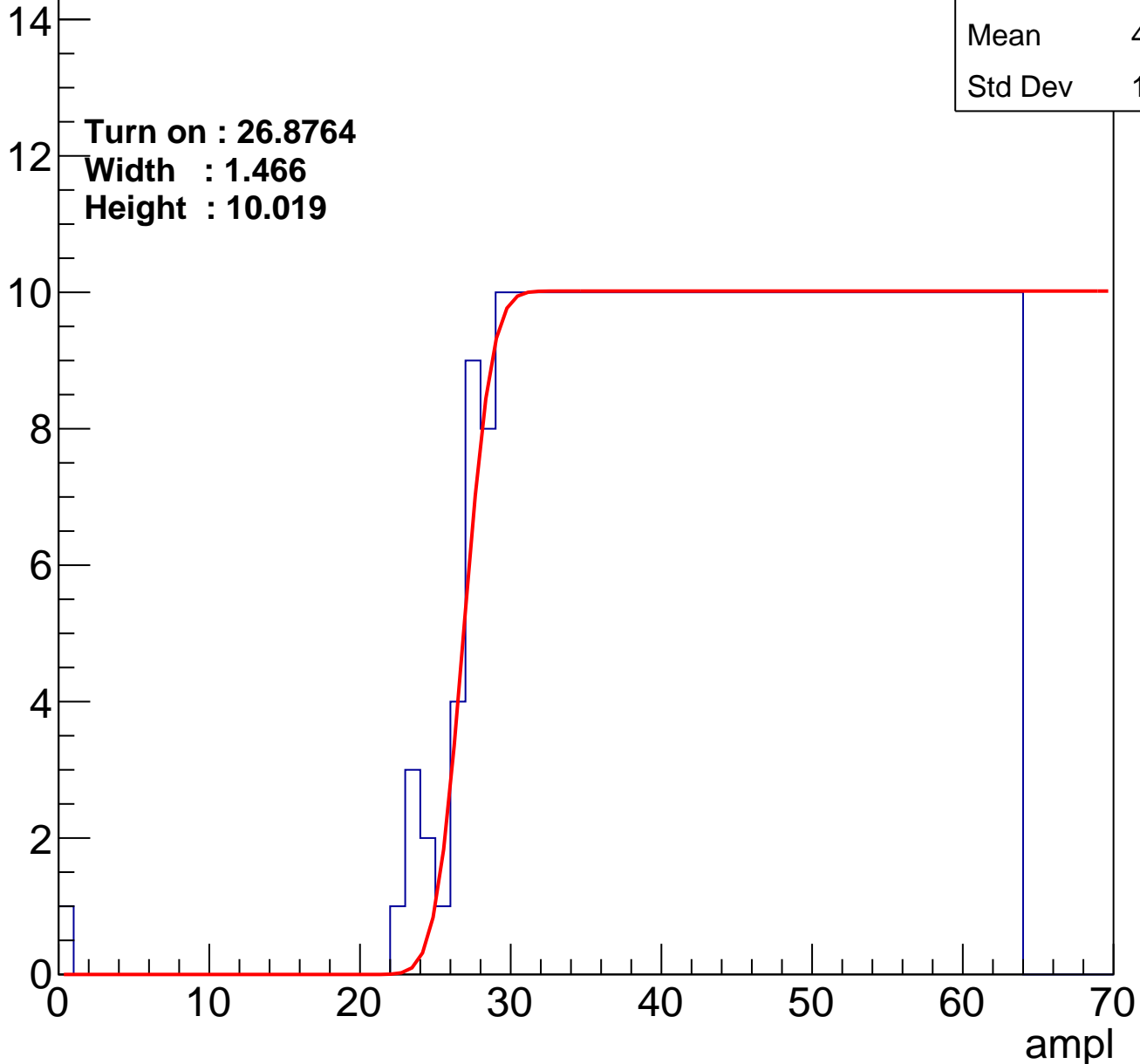
Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.8764

Width : 1.466

Height : 10.019

Entry



B1L002S, U24-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.72
Std Dev	11.91

Turn on : 25.2739

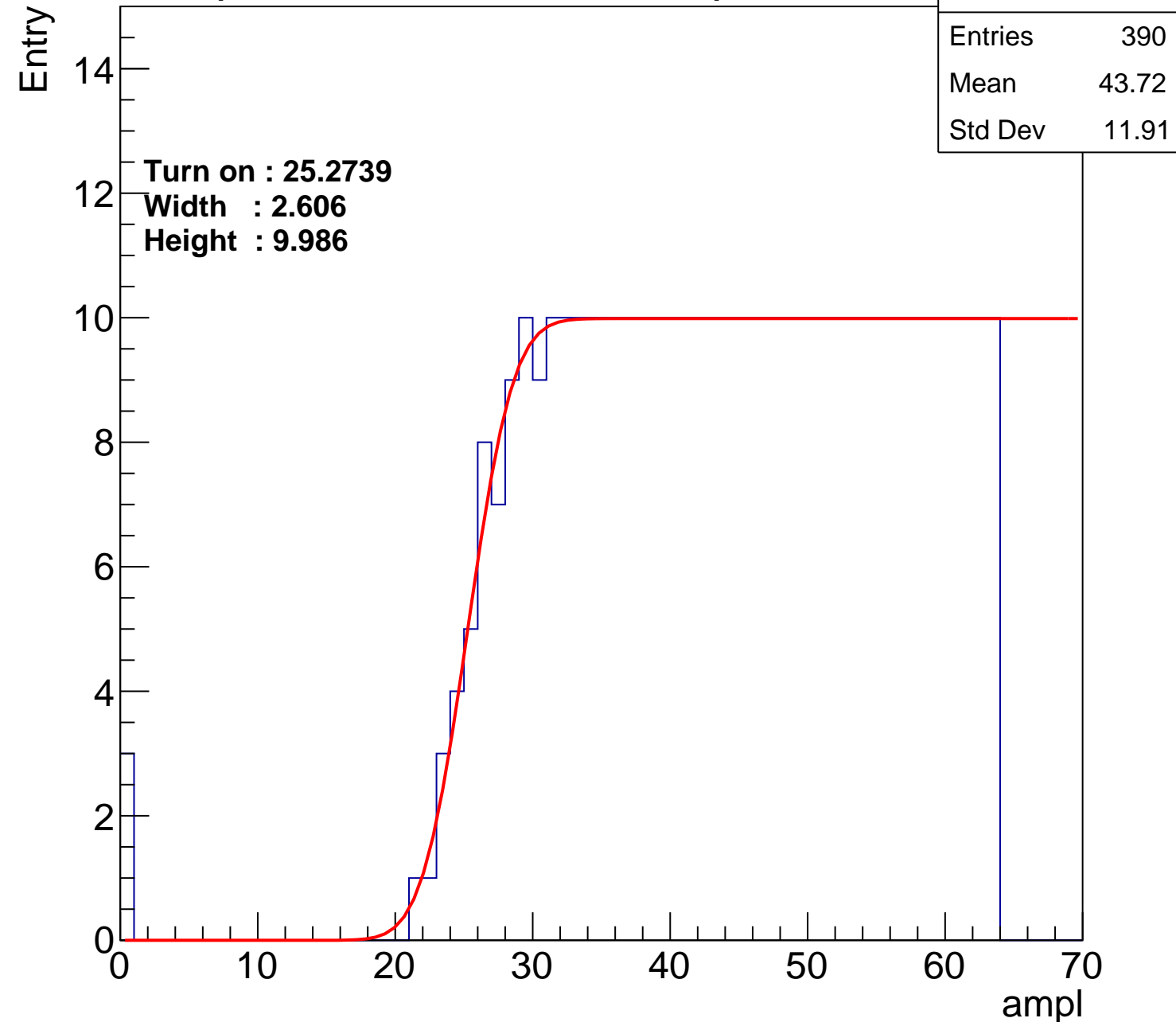
Width : 2.606

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 26.4904

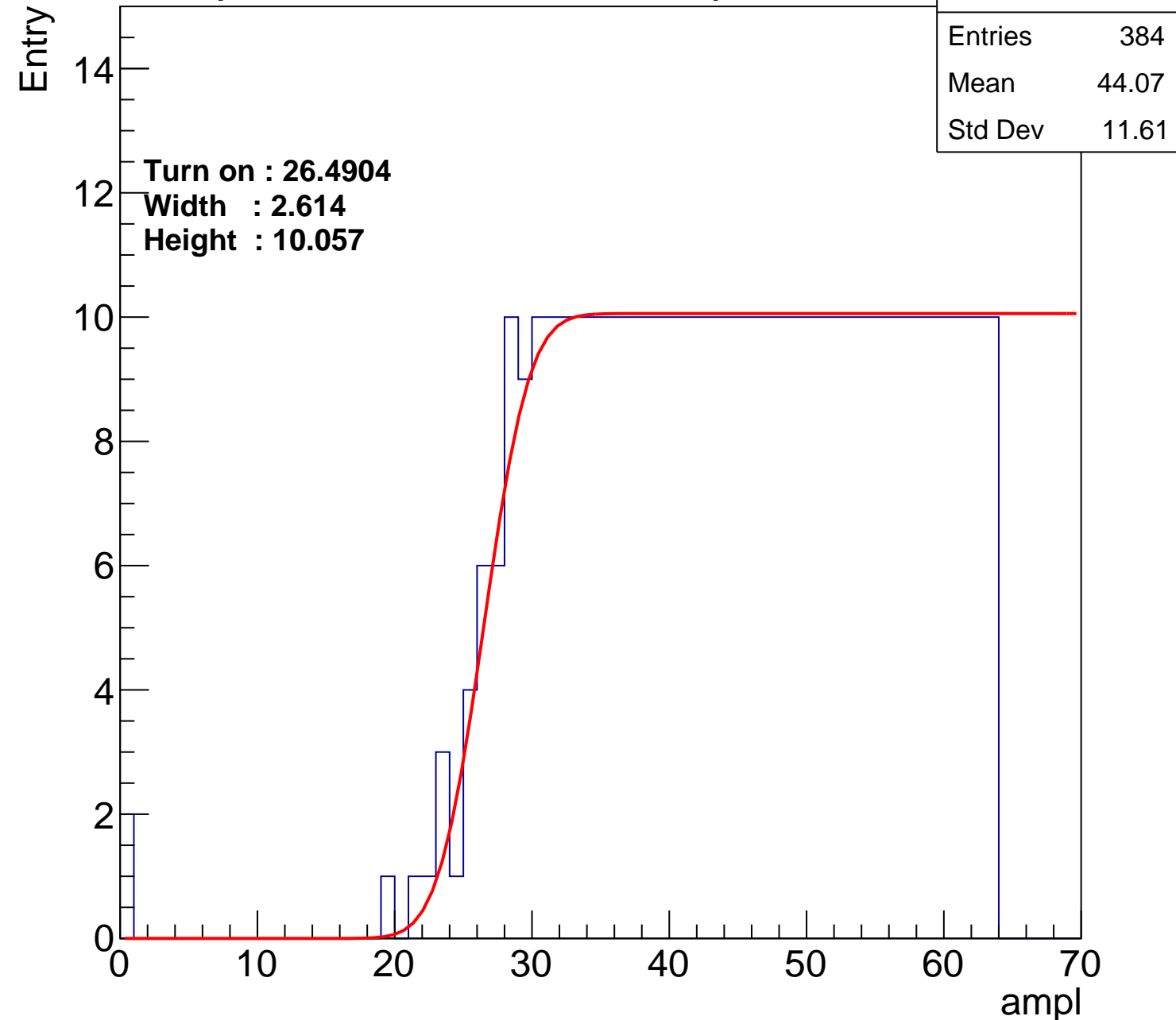
Width : 2.614

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch118

calib_packv5_042523_0143.root, FC#10, port B3

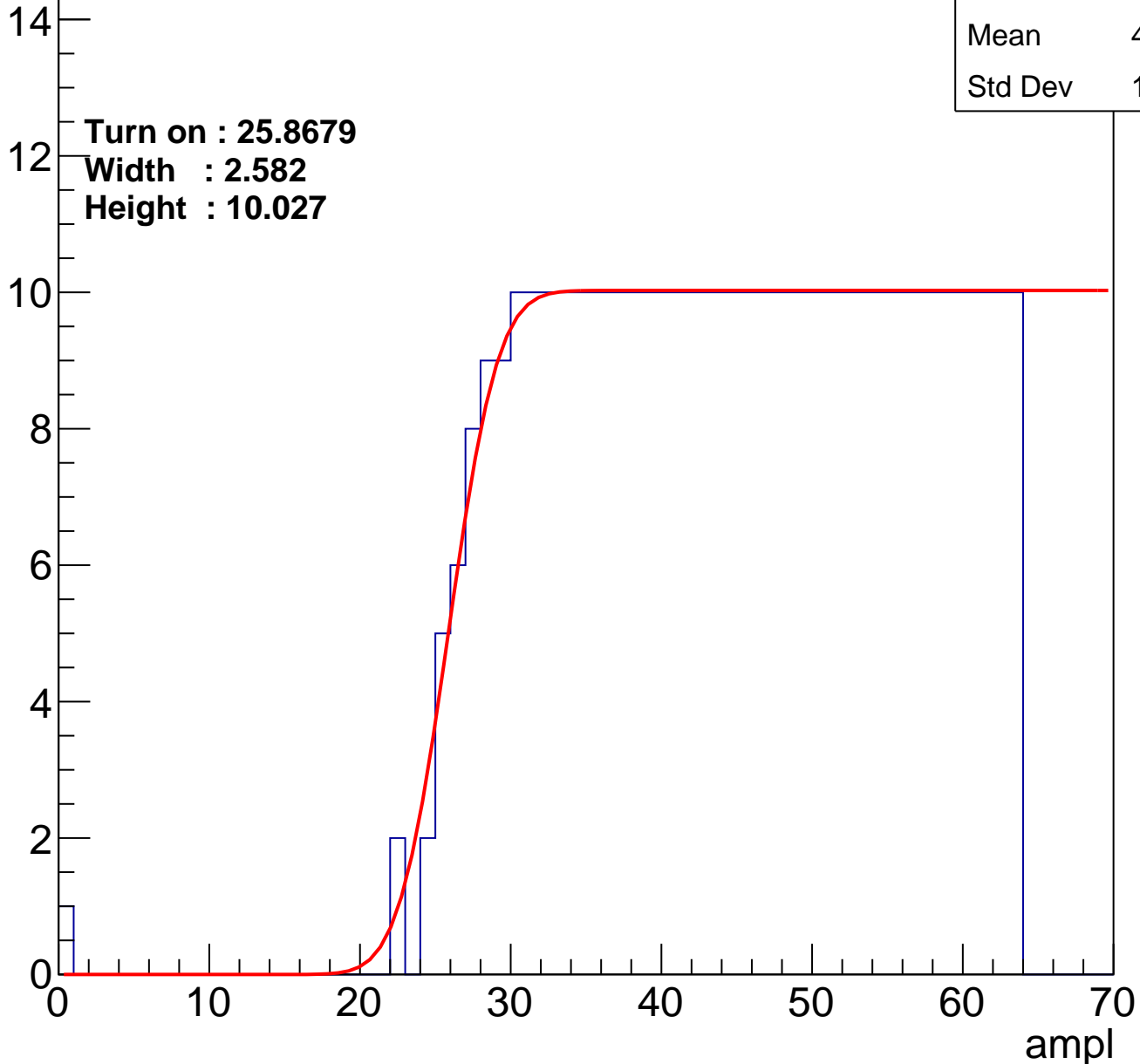
Entries	382
Mean	44.27
Std Dev	11.32

Turn on : 25.8679

Width : 2.582

Height : 10.027

Entry



B1L002S, U24-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 24.8017

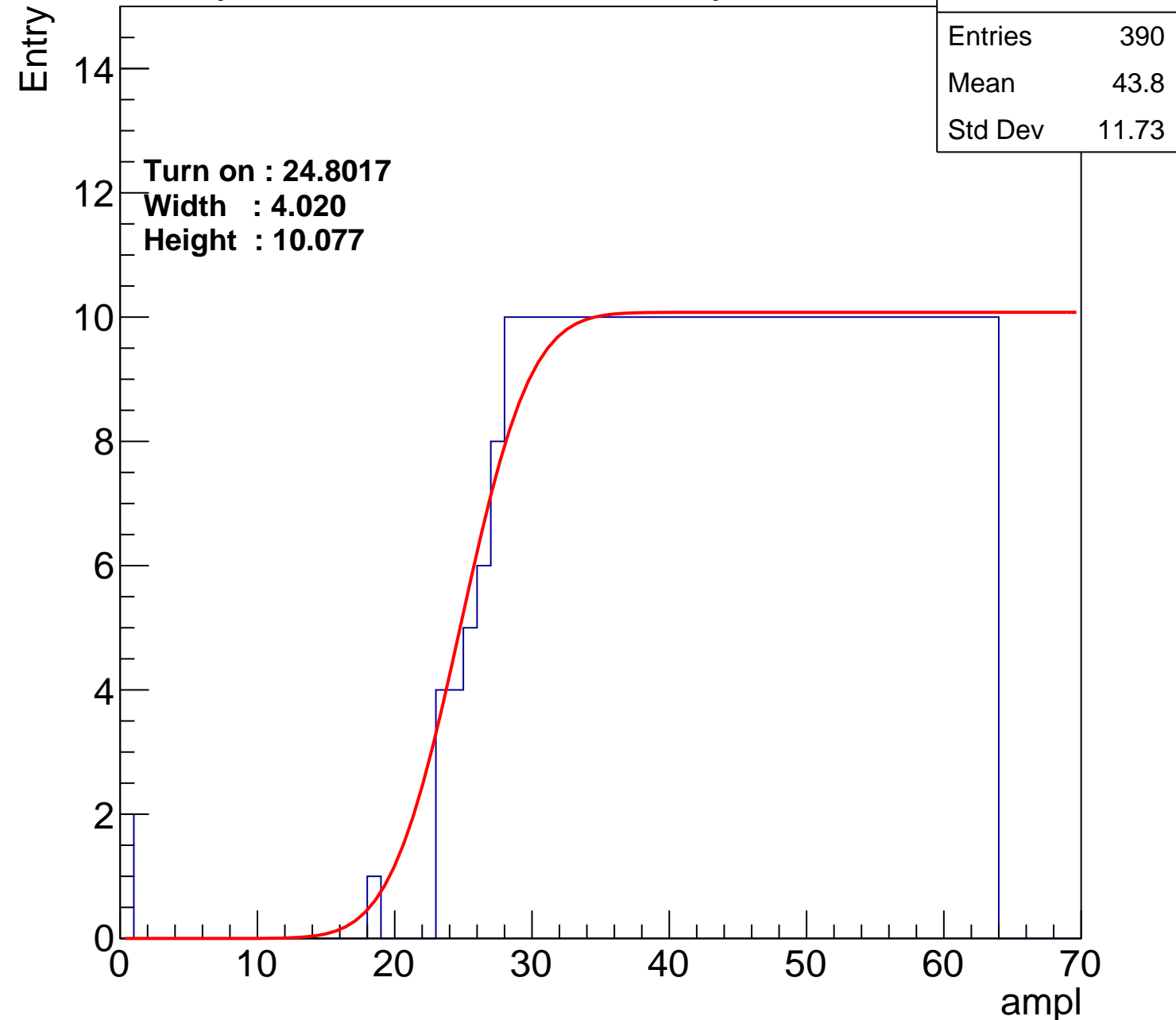
Width : 4.020

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch120

calib_packv5_042523_0143.root, FC#10, port B3

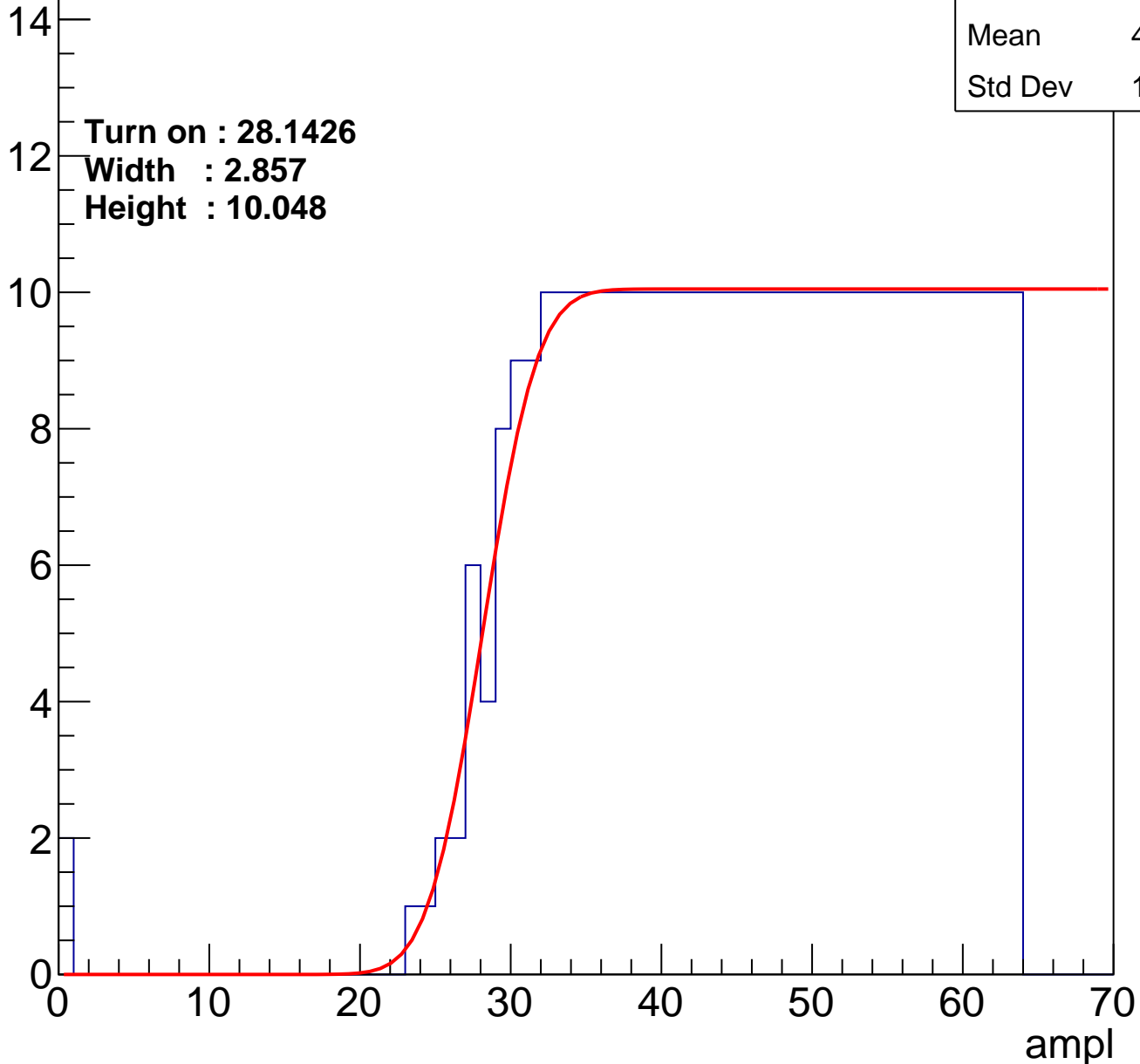
Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 28.1426

Width : 2.857

Height : 10.048

Entry



B1L002S, U24-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.07
Std Dev	11.43

Turn on : 25.5648

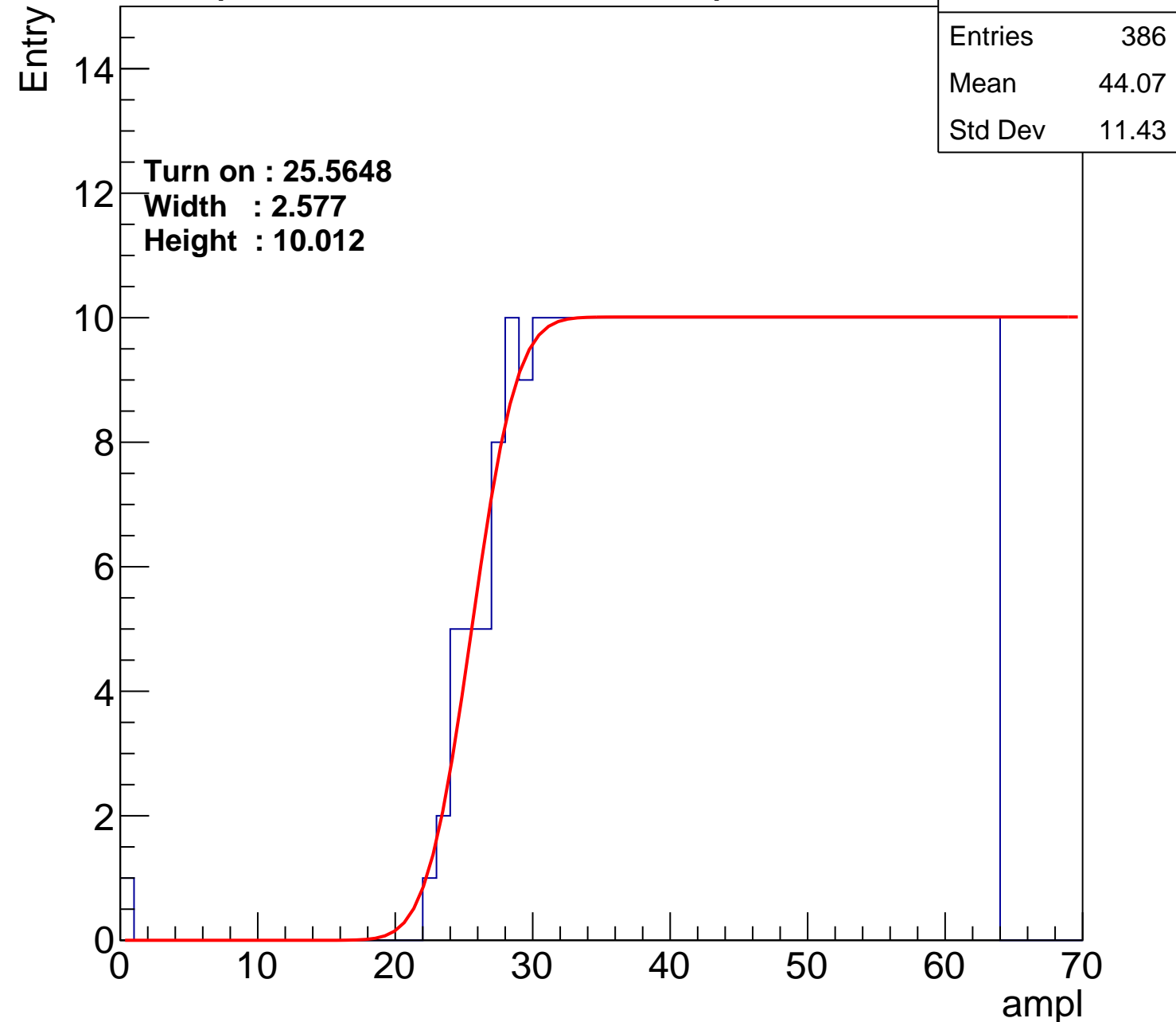
Width : 2.577

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.5
Std Dev	11.77

Turn on : 23.9703

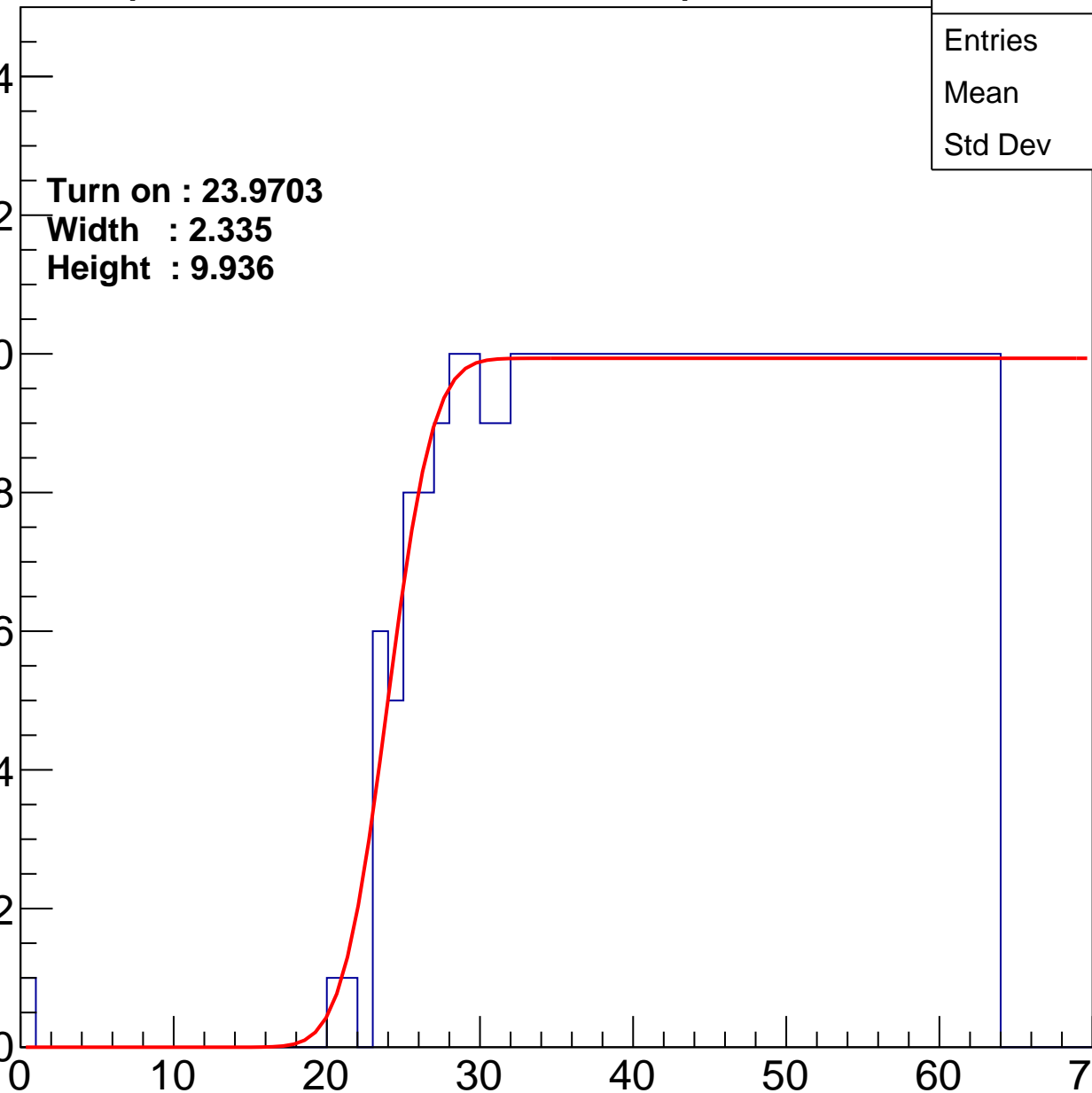
Width : 2.335

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.66

Turn on : 26.3377

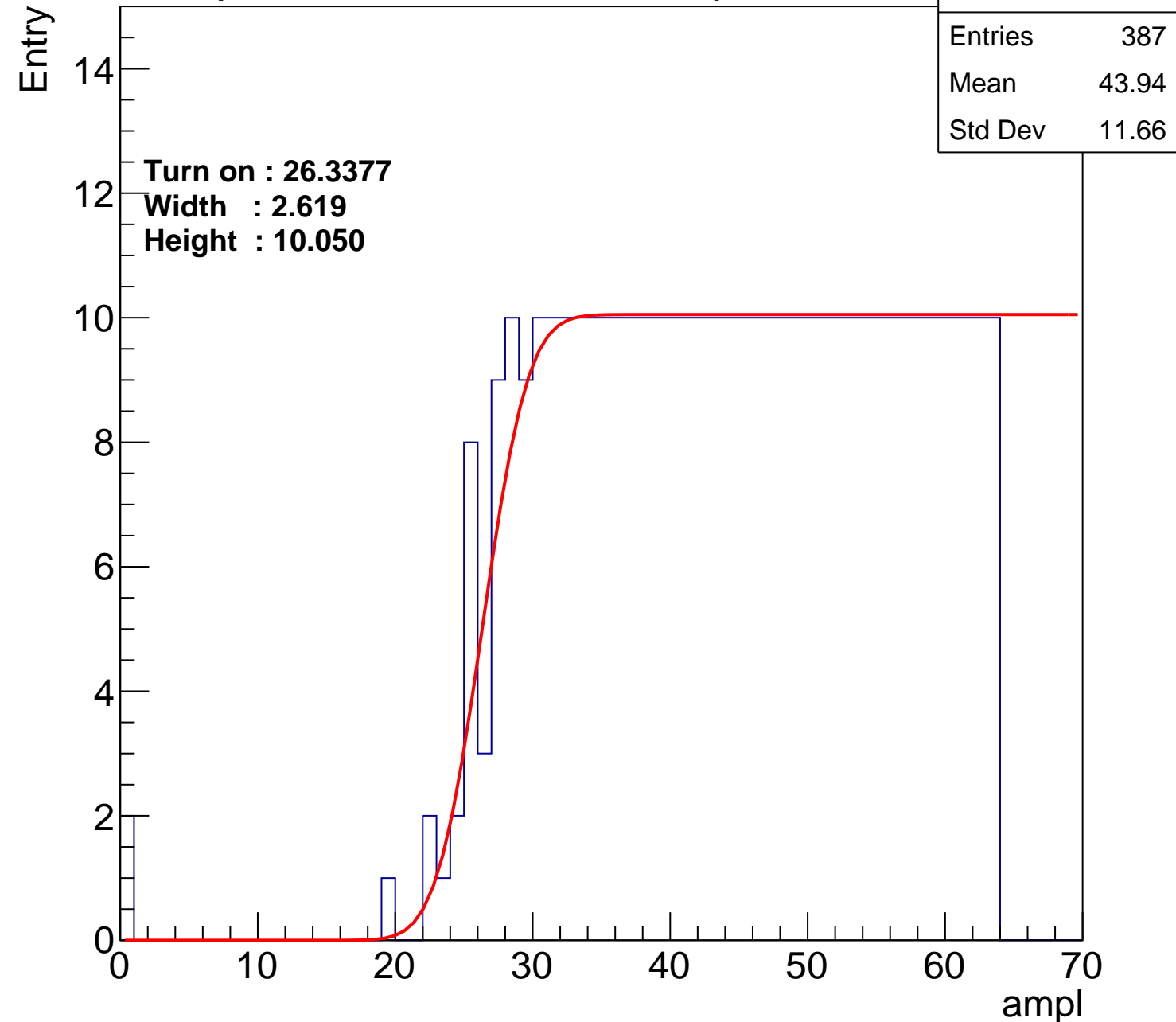
Width : 2.619

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.57
Std Dev	12.47

Turn on : 22.7654

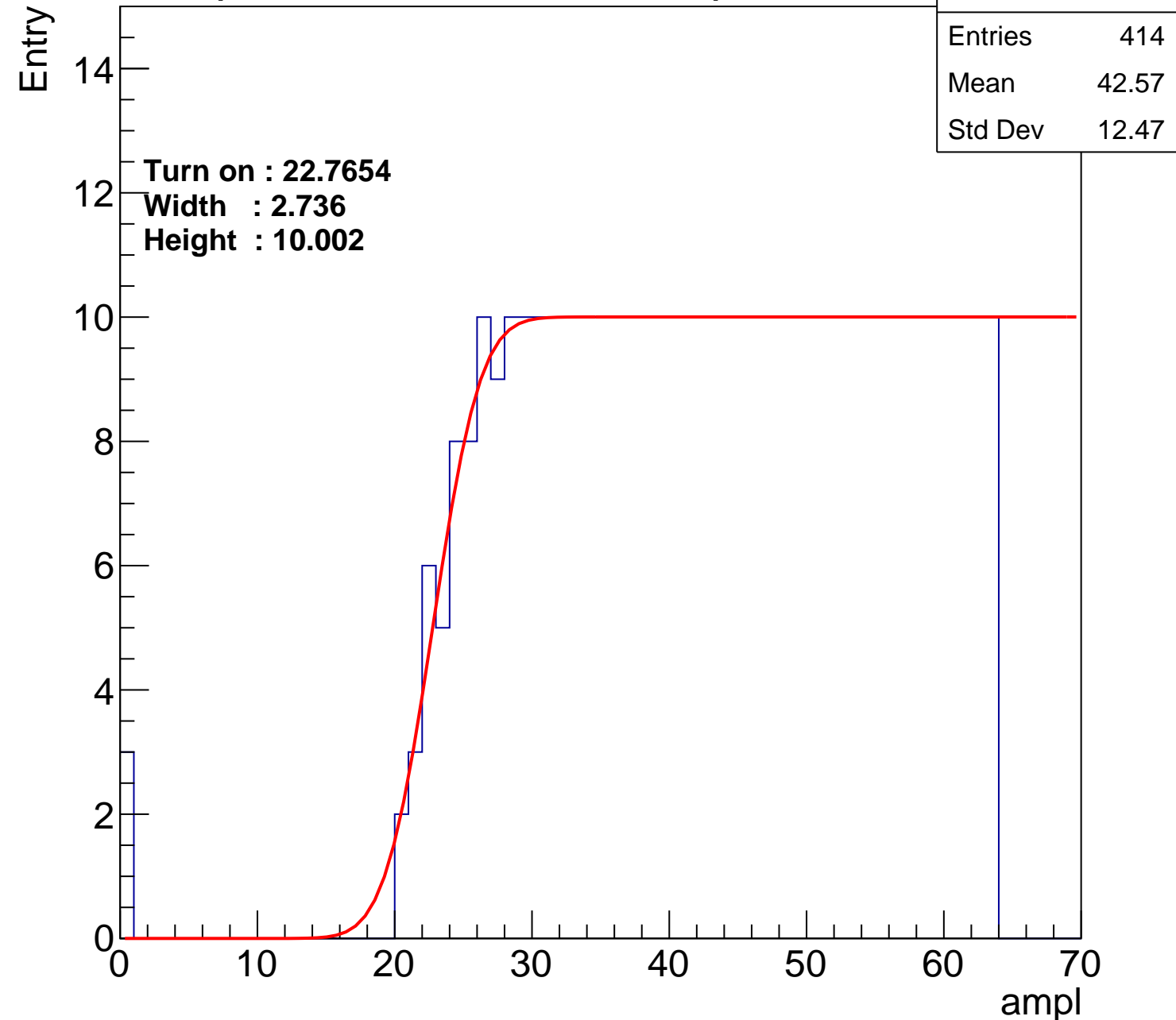
Width : 2.736

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch125

calib_packv5_042523_0143.root, FC#10, port B3

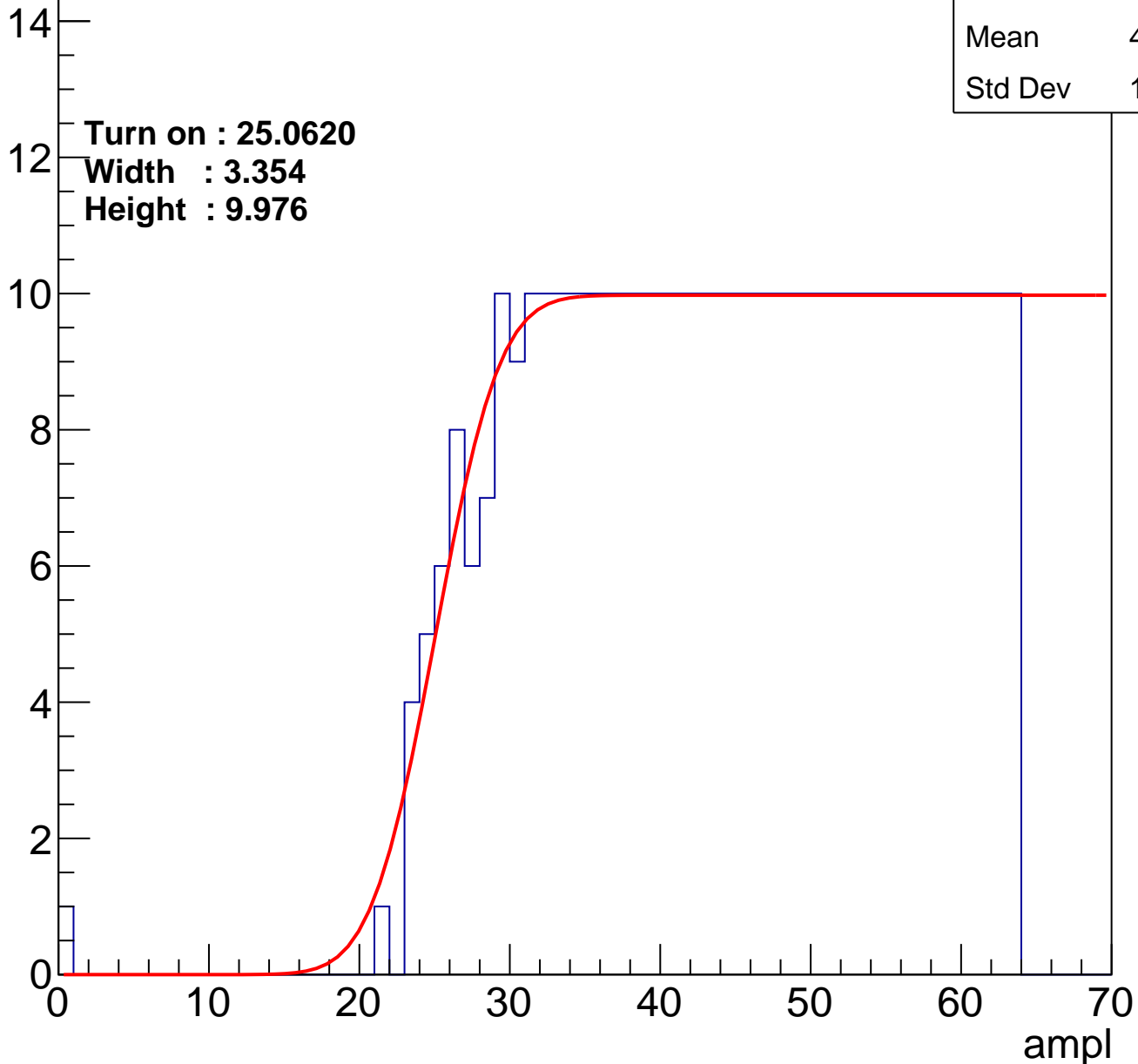
Entries	387
Mean	43.98
Std Dev	11.53

Turn on : 25.0620

Width : 3.354

Height : 9.976

Entry



B1L002S, U24-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.4126

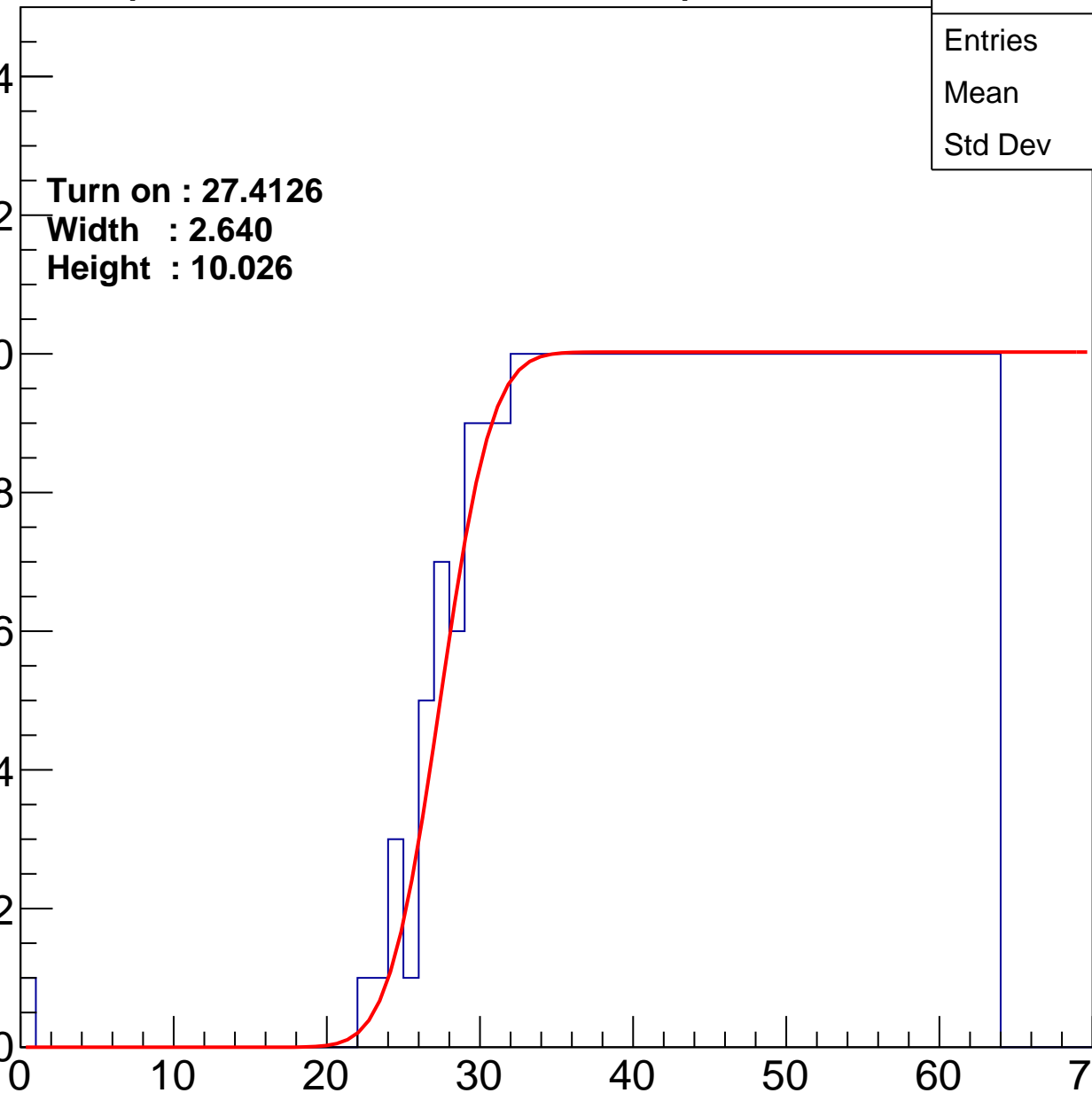
Width : 2.640

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.77
Std Dev	11.1

Turn on : 26.9741

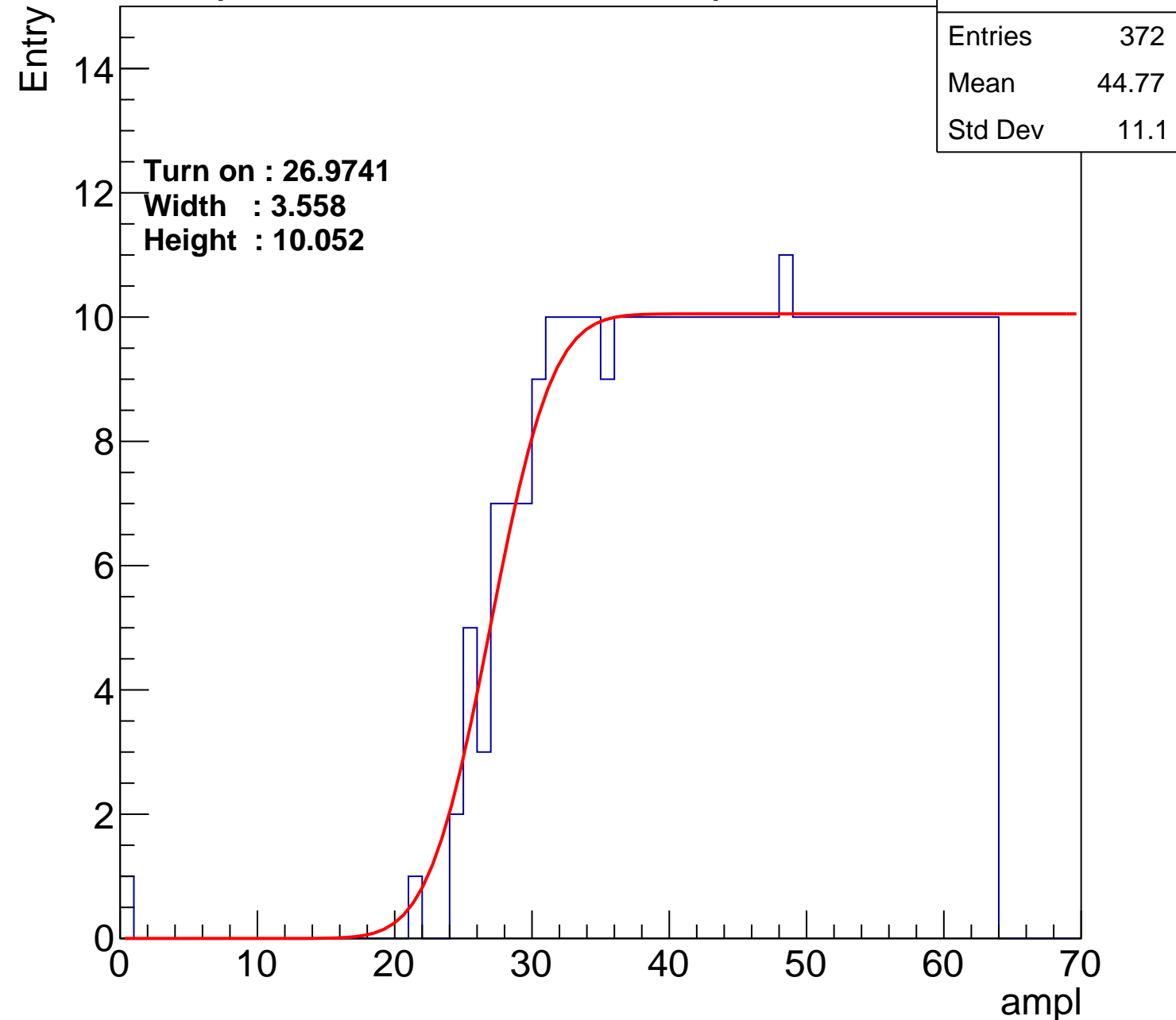
Width : 3.558

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U24-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.77
Std Dev	11.1

Turn on : 26.9741

Width : 3.558

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

