



# B1L000S, U21-ch0, adc0

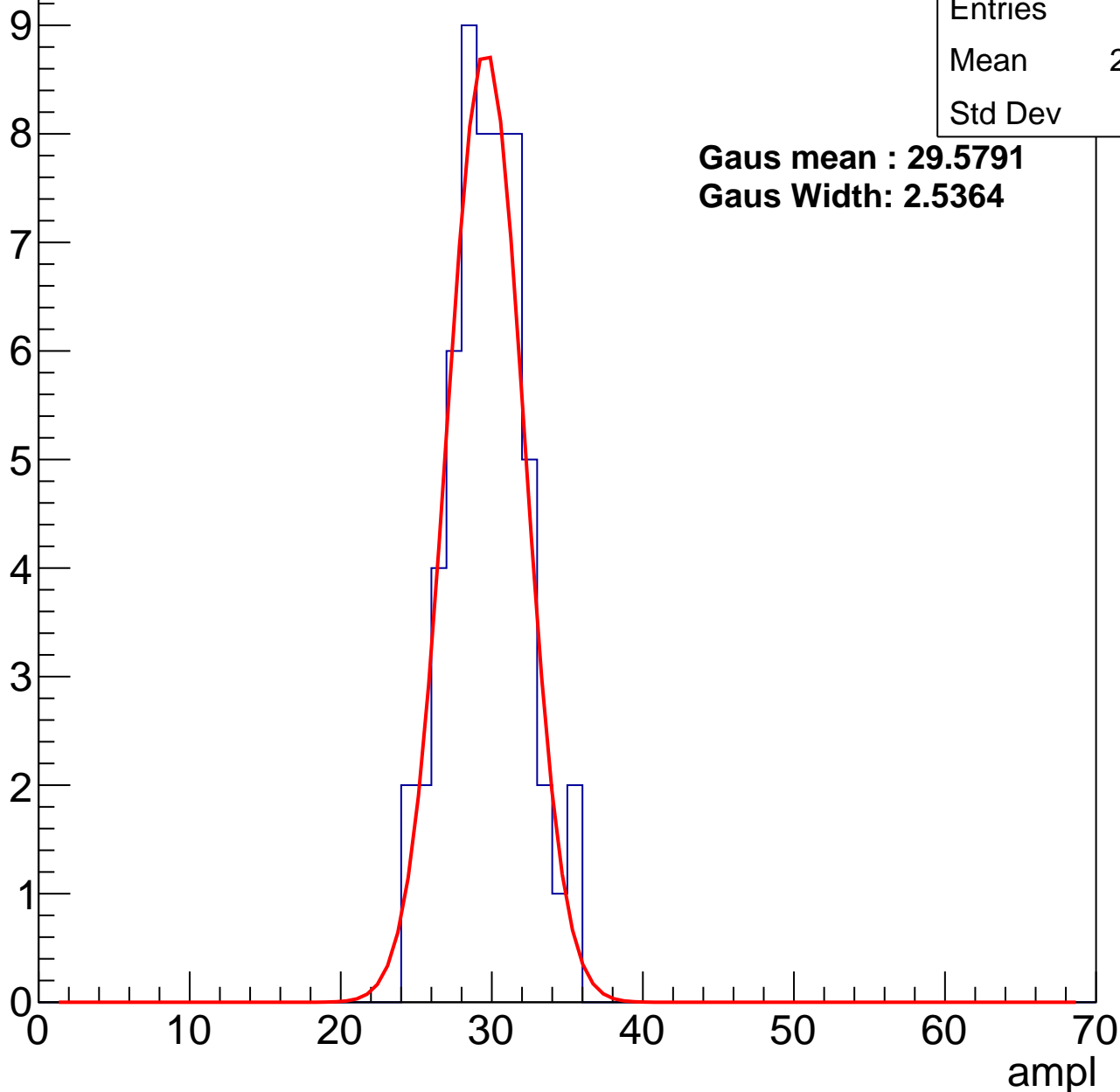
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	29.23
Std Dev	2.52

**Gaus mean : 29.5791**

**Gaus Width: 2.5364**



# B1L000S, U21-ch0, adc1

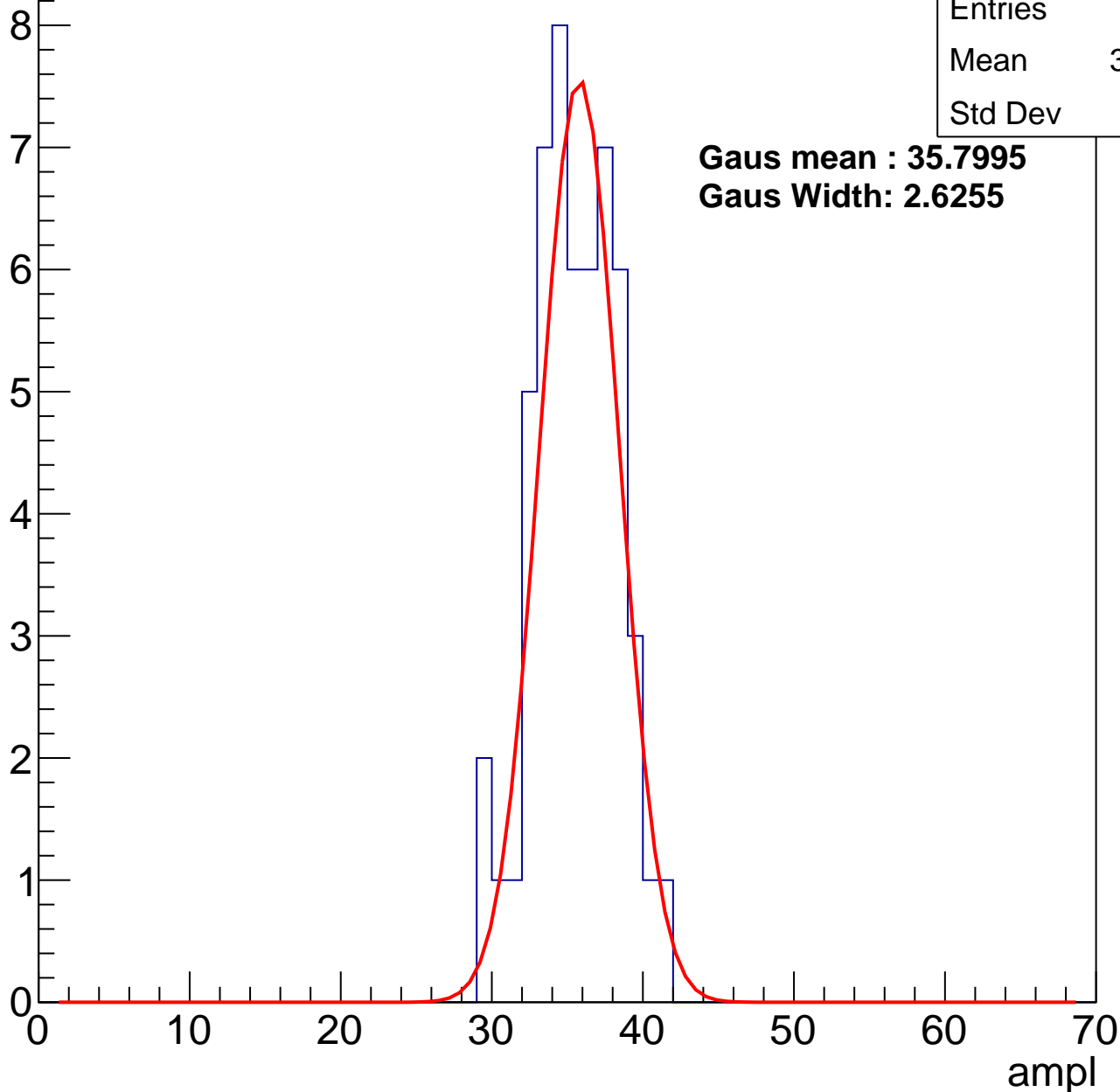
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	35.06
Std Dev	2.69

**Gaus mean : 35.7995**

**Gaus Width: 2.6255**



# B1L000S, U21-ch0, adc2

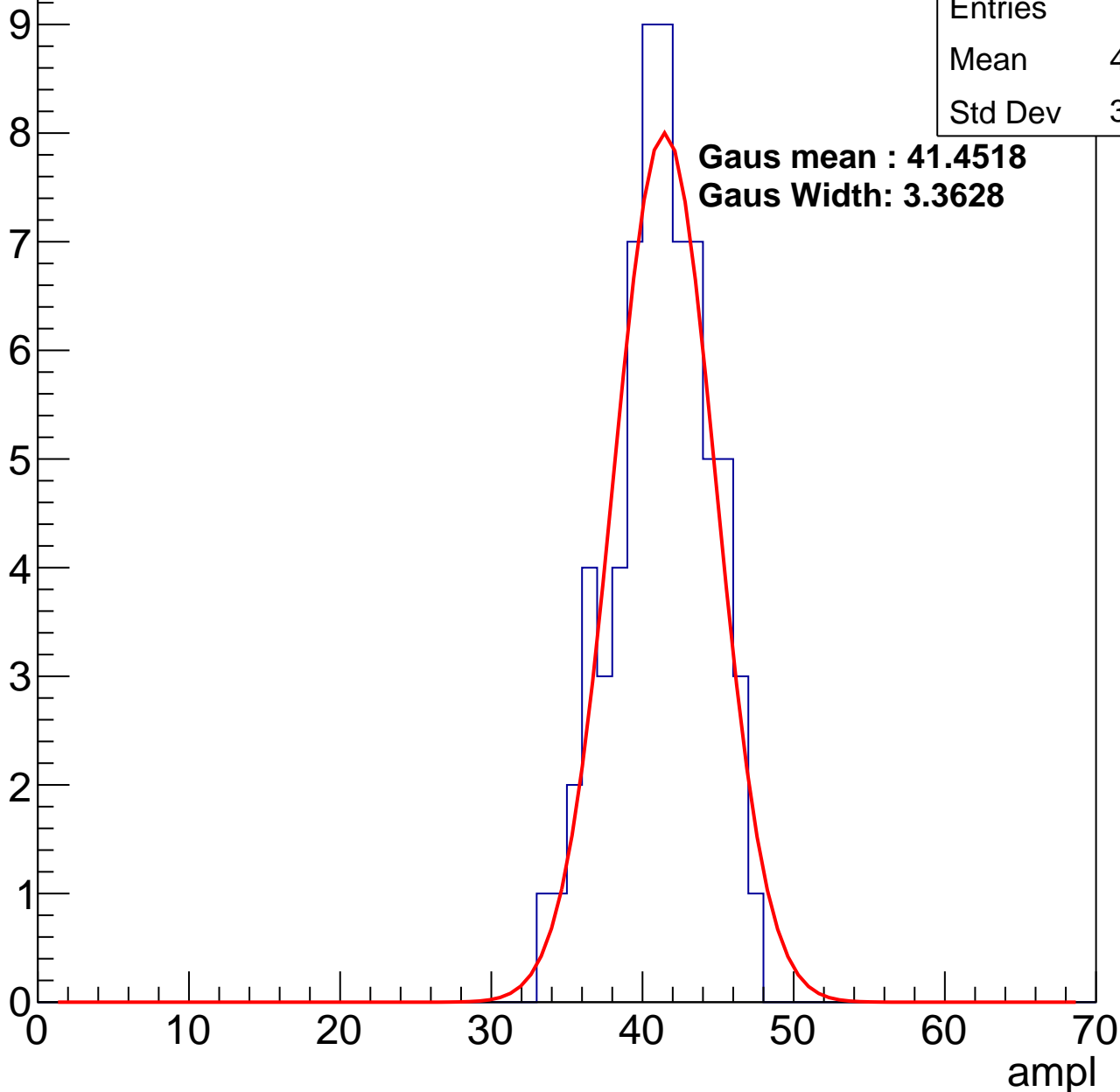
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	40.75
Std Dev	3.145

**Gaus mean : 41.4518**

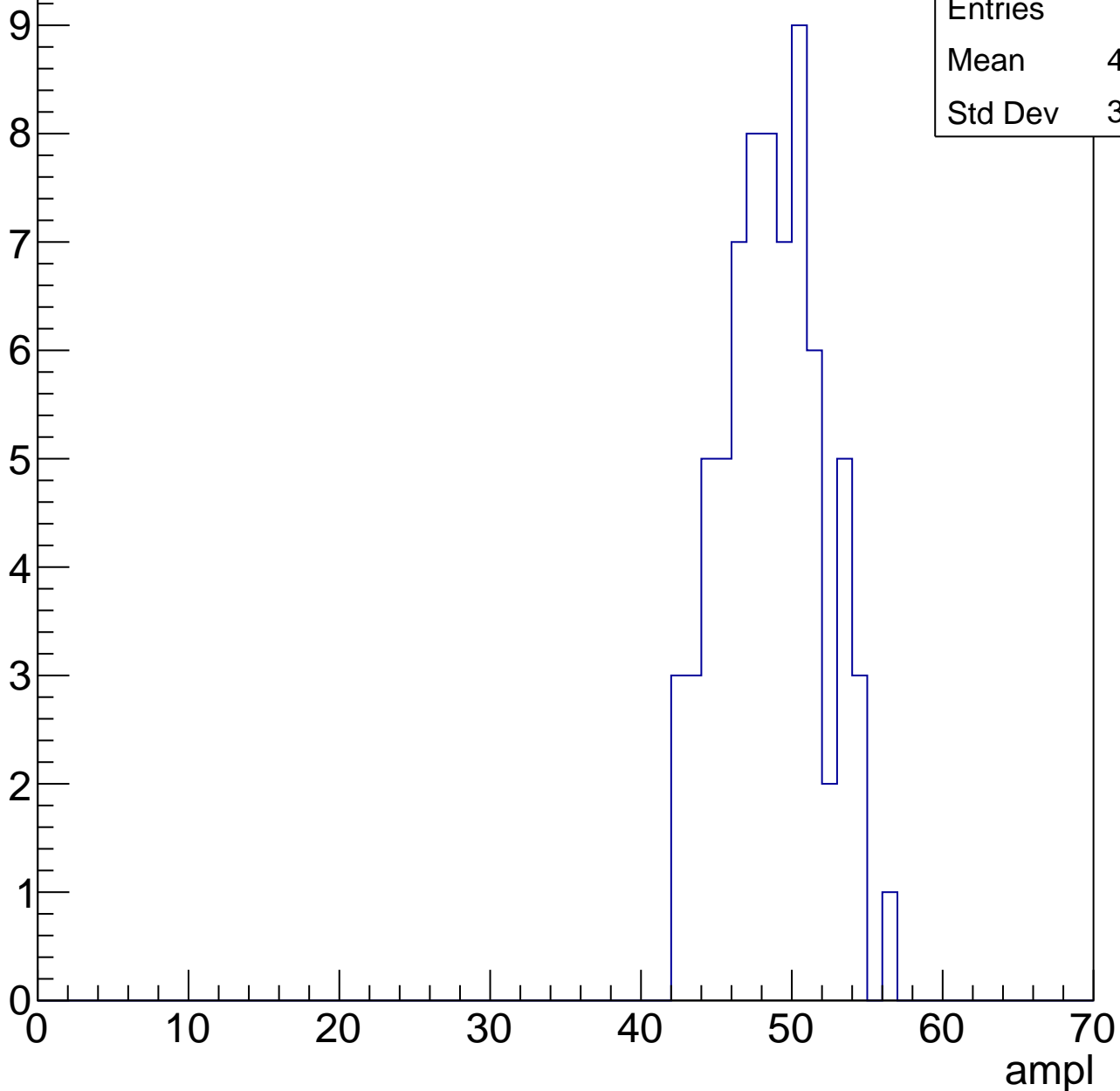
**Gaus Width: 3.3628**



# B1L000S, U21-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

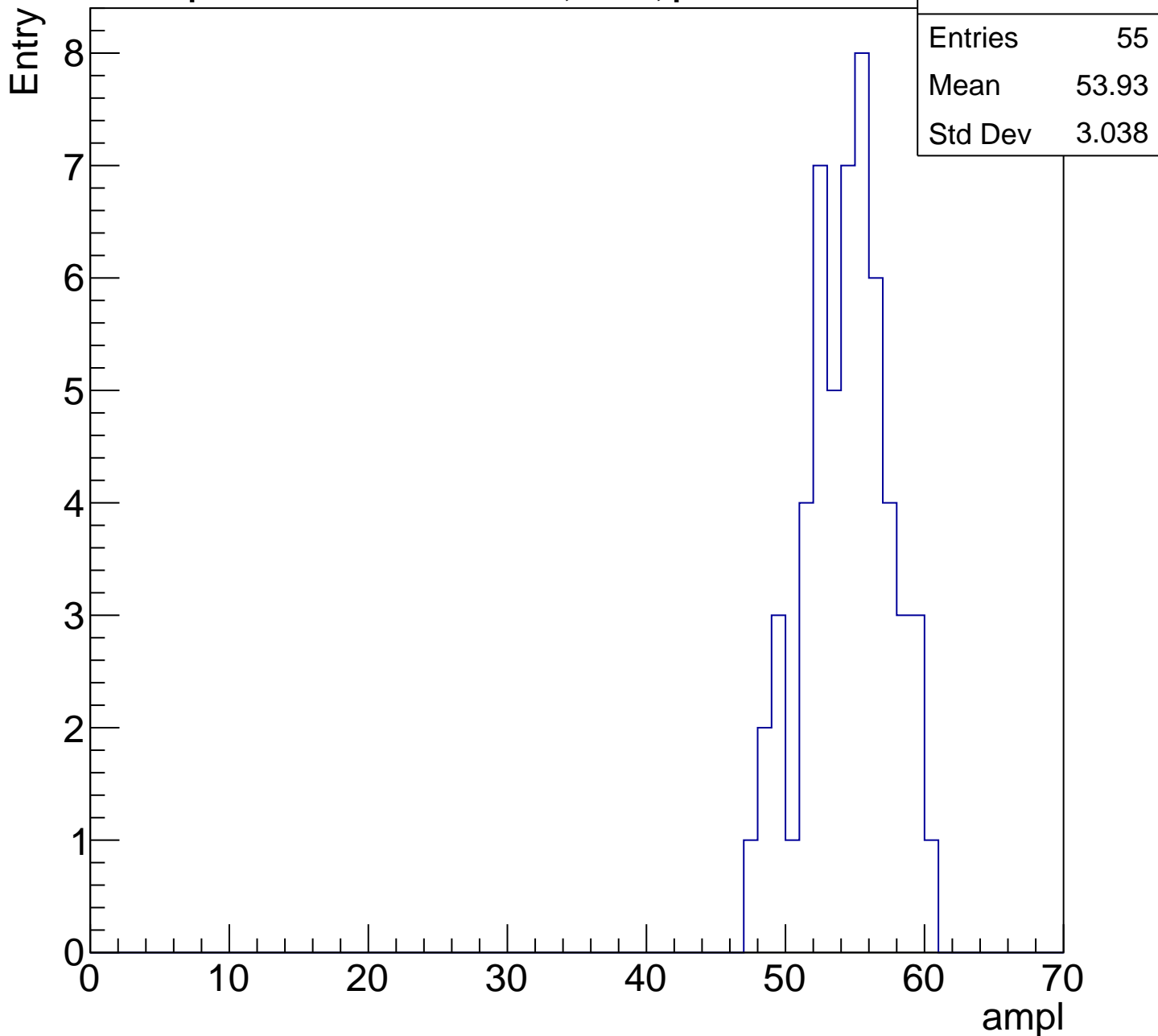
Entry



Entries	72
Mean	48.17
Std Dev	3.266

# B1L000S, U21-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	59.34
Std Dev	2.43

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

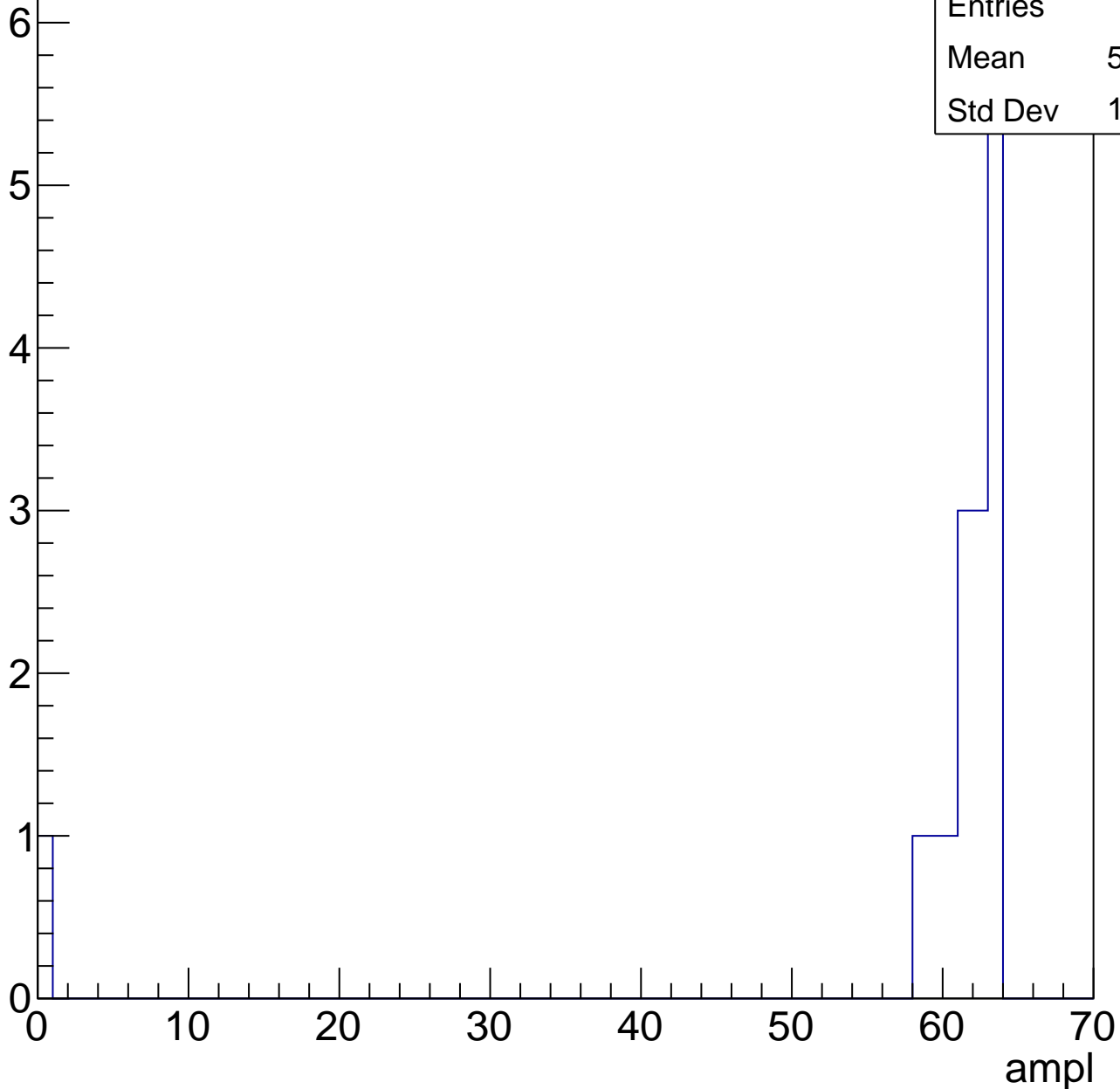
8

# B1L000S, U21-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.75
Std Dev	14.99





# B1L000S, U21-ch0, adc7

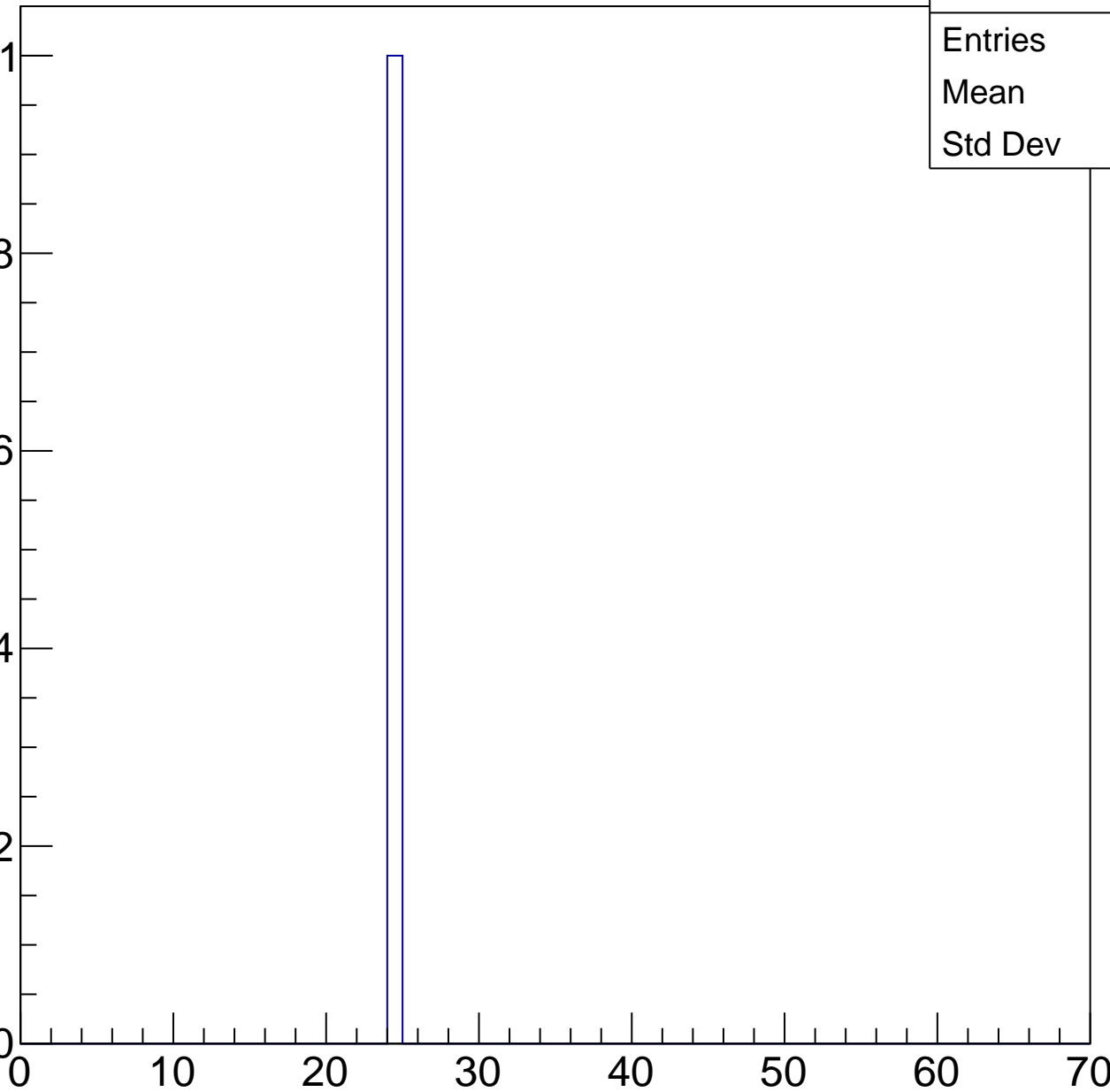
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl



# B1L000S, U21-ch1, adc0

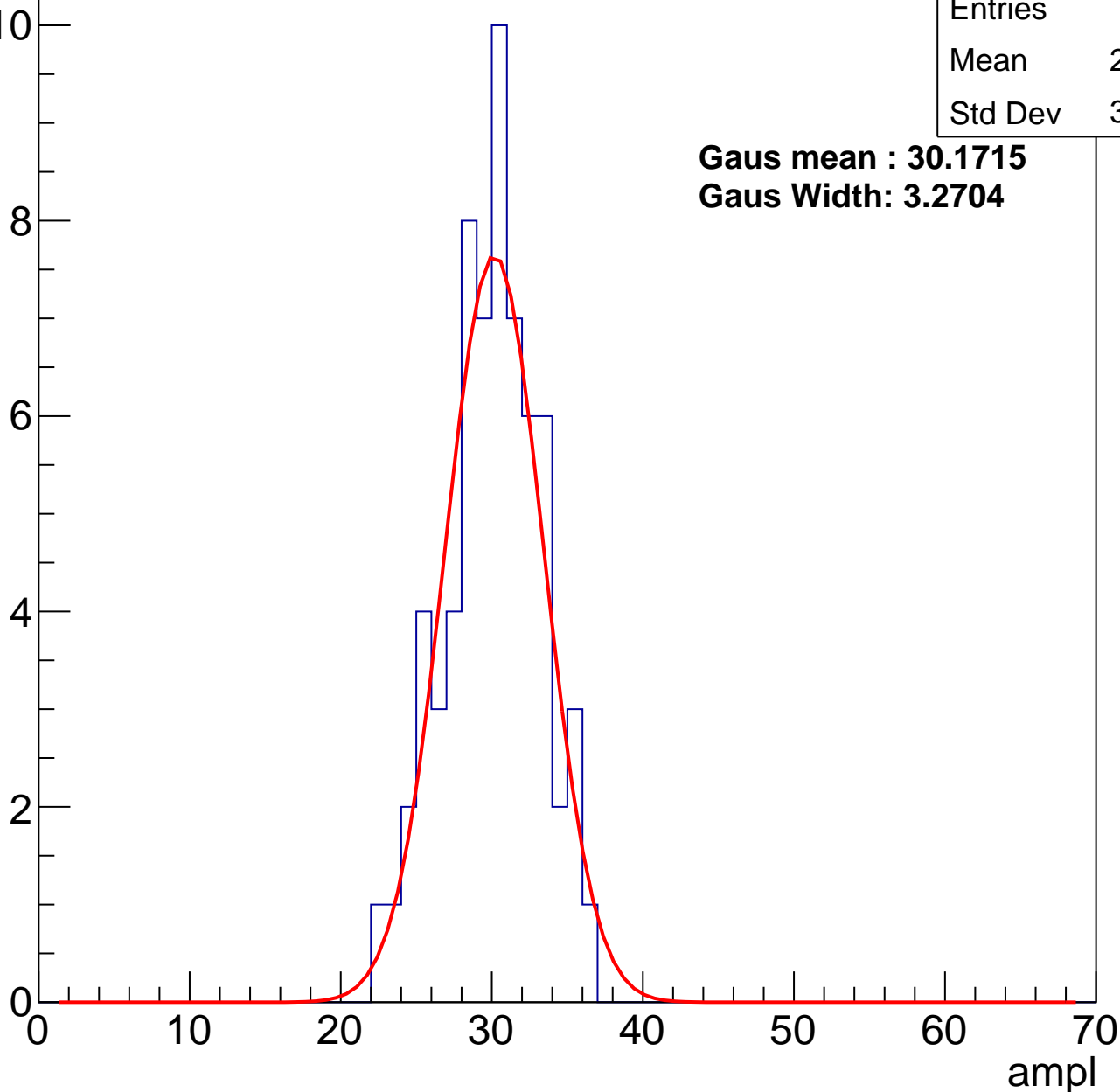
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	29.57
Std Dev	3.098

**Gaus mean : 30.1715**

**Gaus Width: 3.2704**



# B1L000S, U21-ch1, adc1

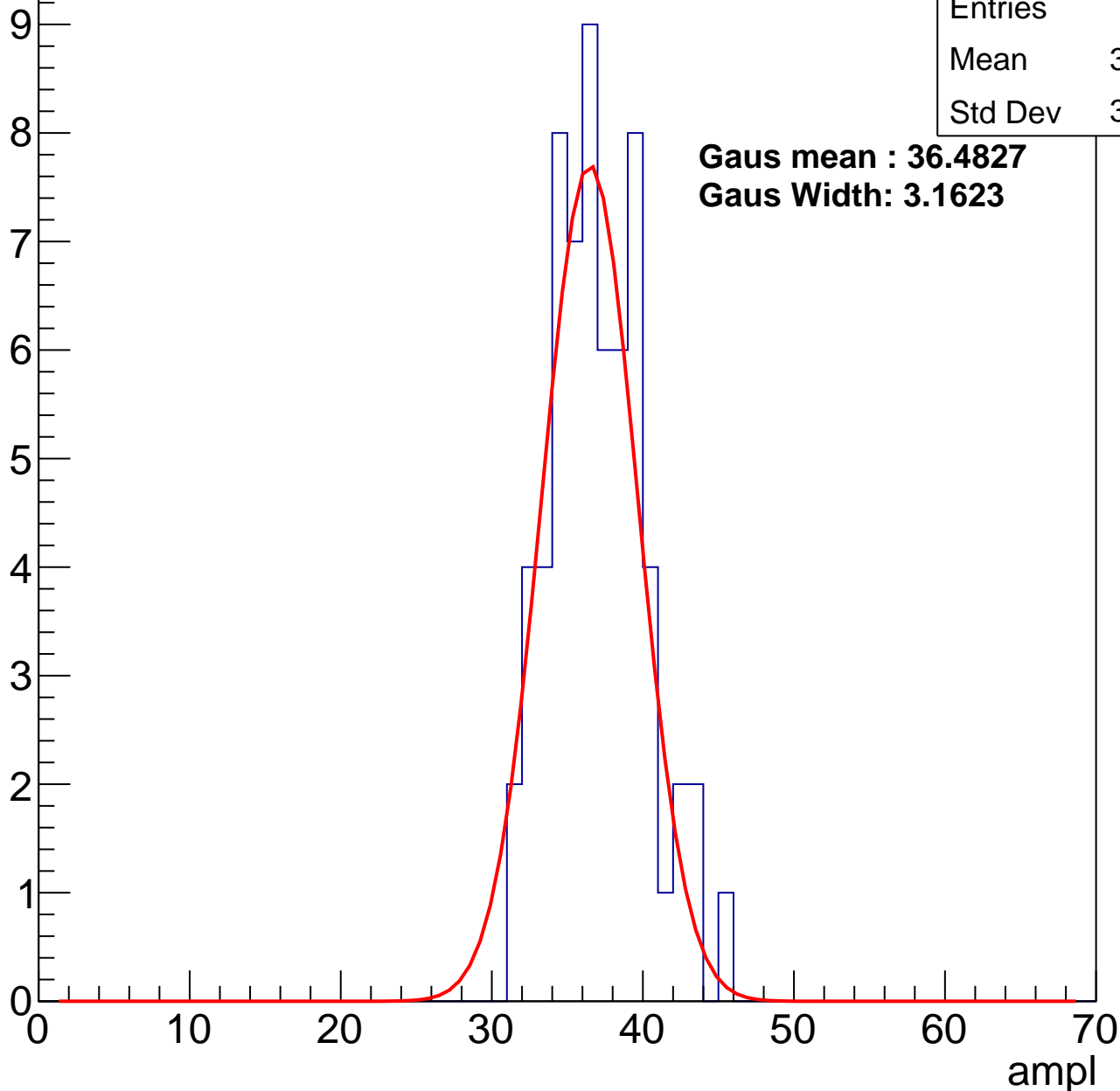
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	36.58
Std Dev	3.086

**Gaus mean : 36.4827**

**Gaus Width: 3.1623**



# B1L000S, U21-ch1, adc2

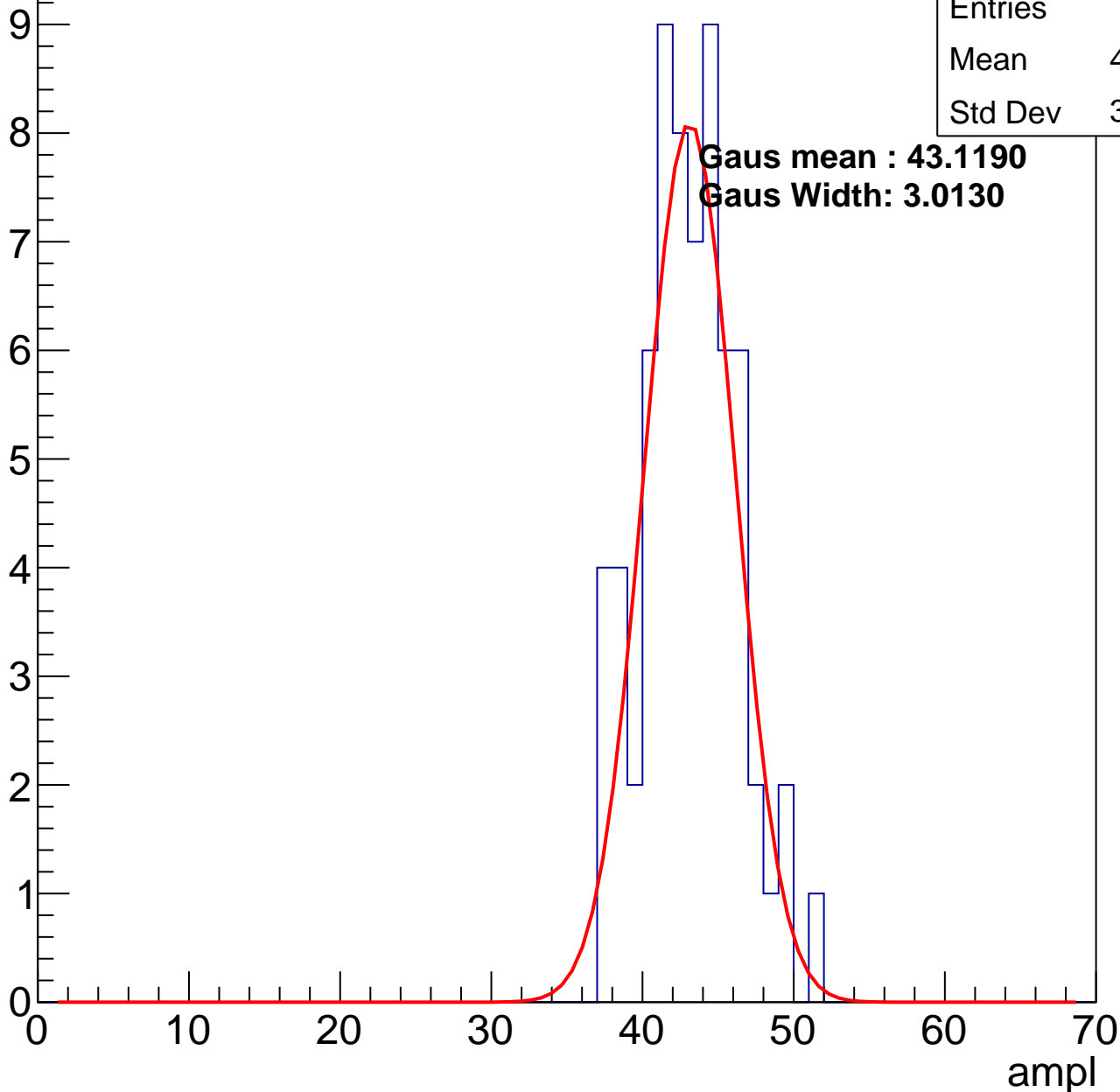
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.64
Std Dev	3.104

**Gaus mean : 43.1190**

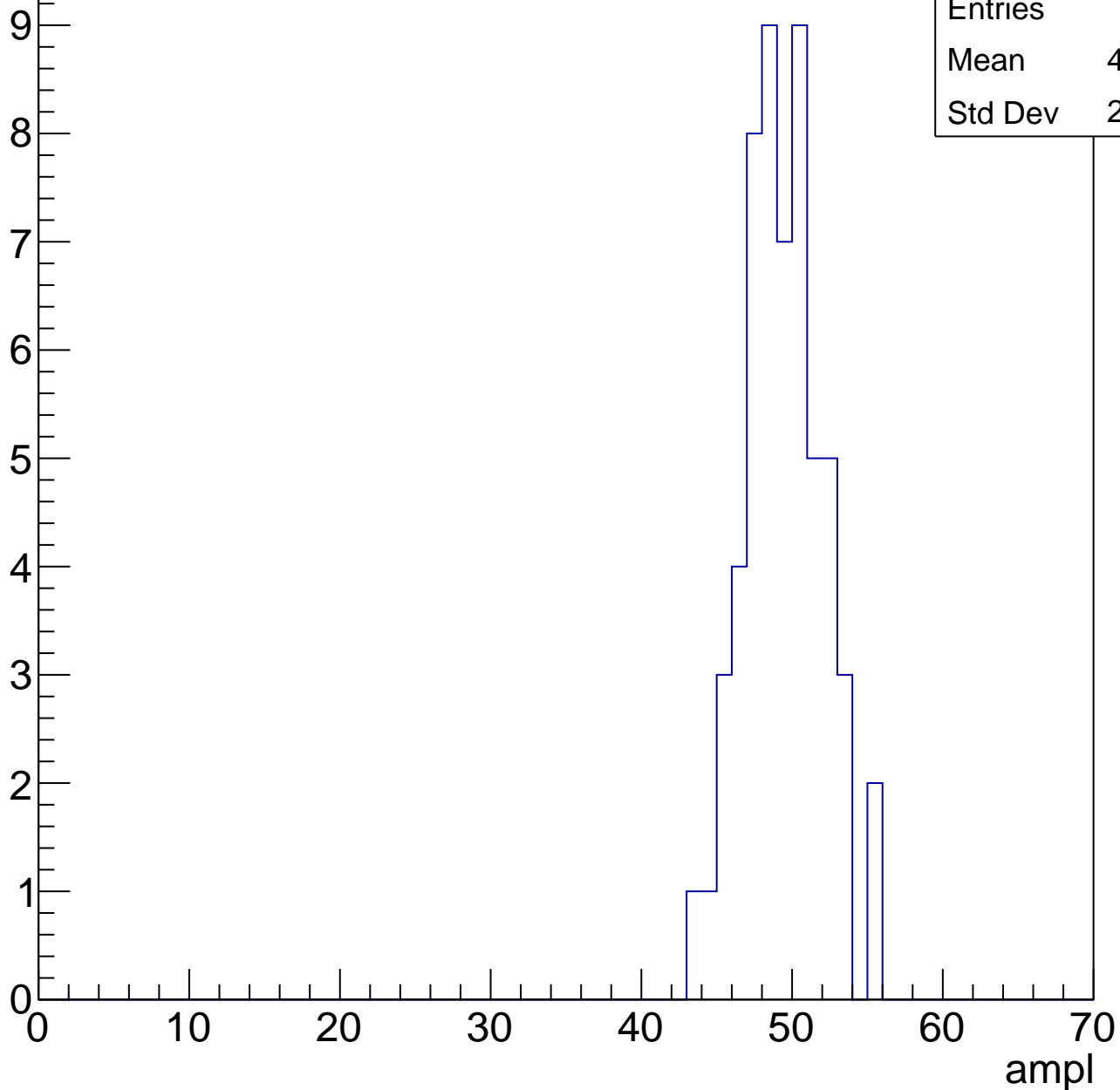
**Gaus Width: 3.0130**



# B1L000S, U21-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



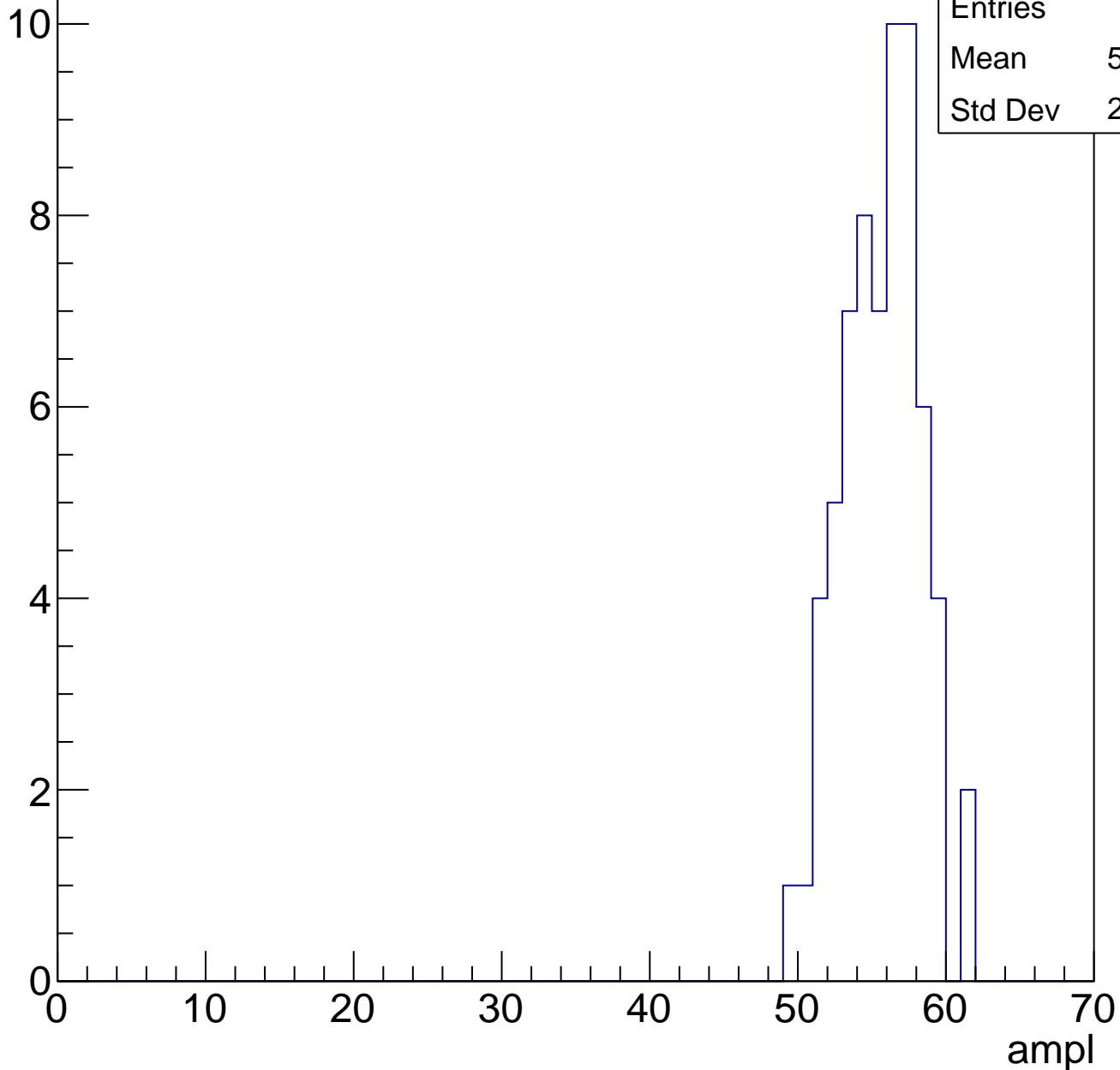
Entries	57
Mean	48.96
Std Dev	2.582

# B1L000S, U21-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

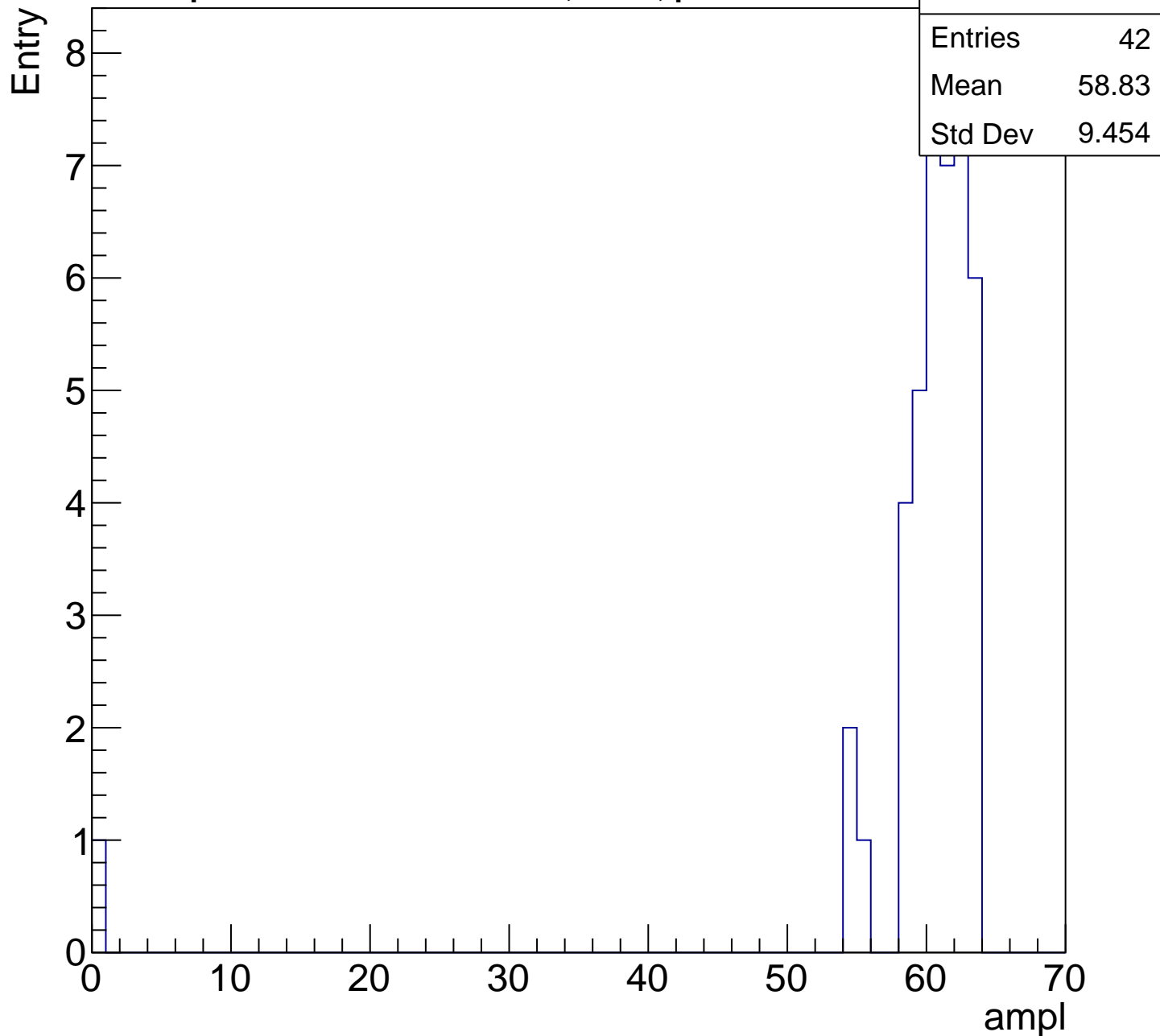
Entries	65
Mean	55.18
Std Dev	2.613

Entry



# B1L000S, U21-ch1, adc5

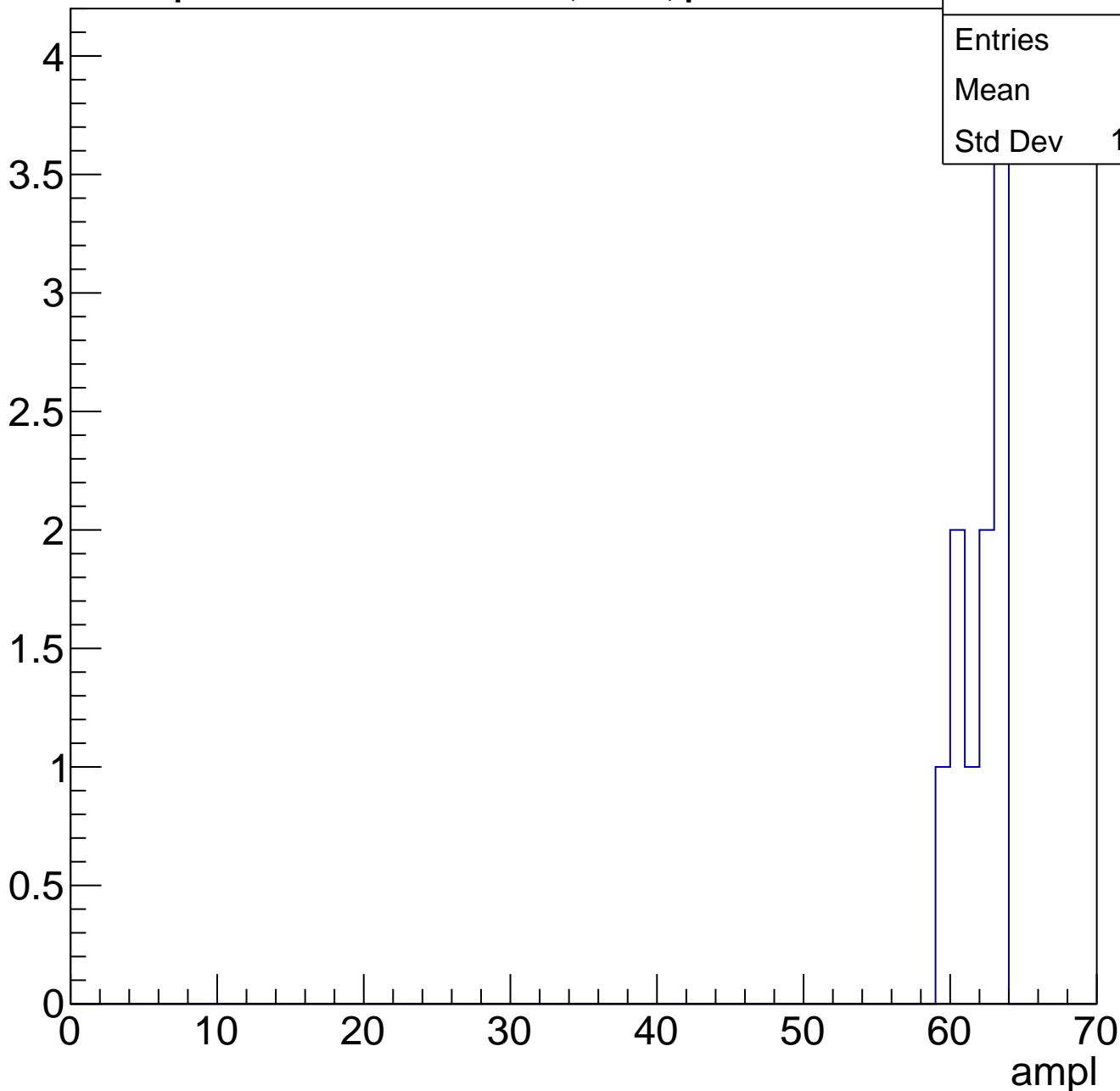
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	10
Mean	61.6
Std Dev	1.428



# B1L000S, U21-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch2, adc0

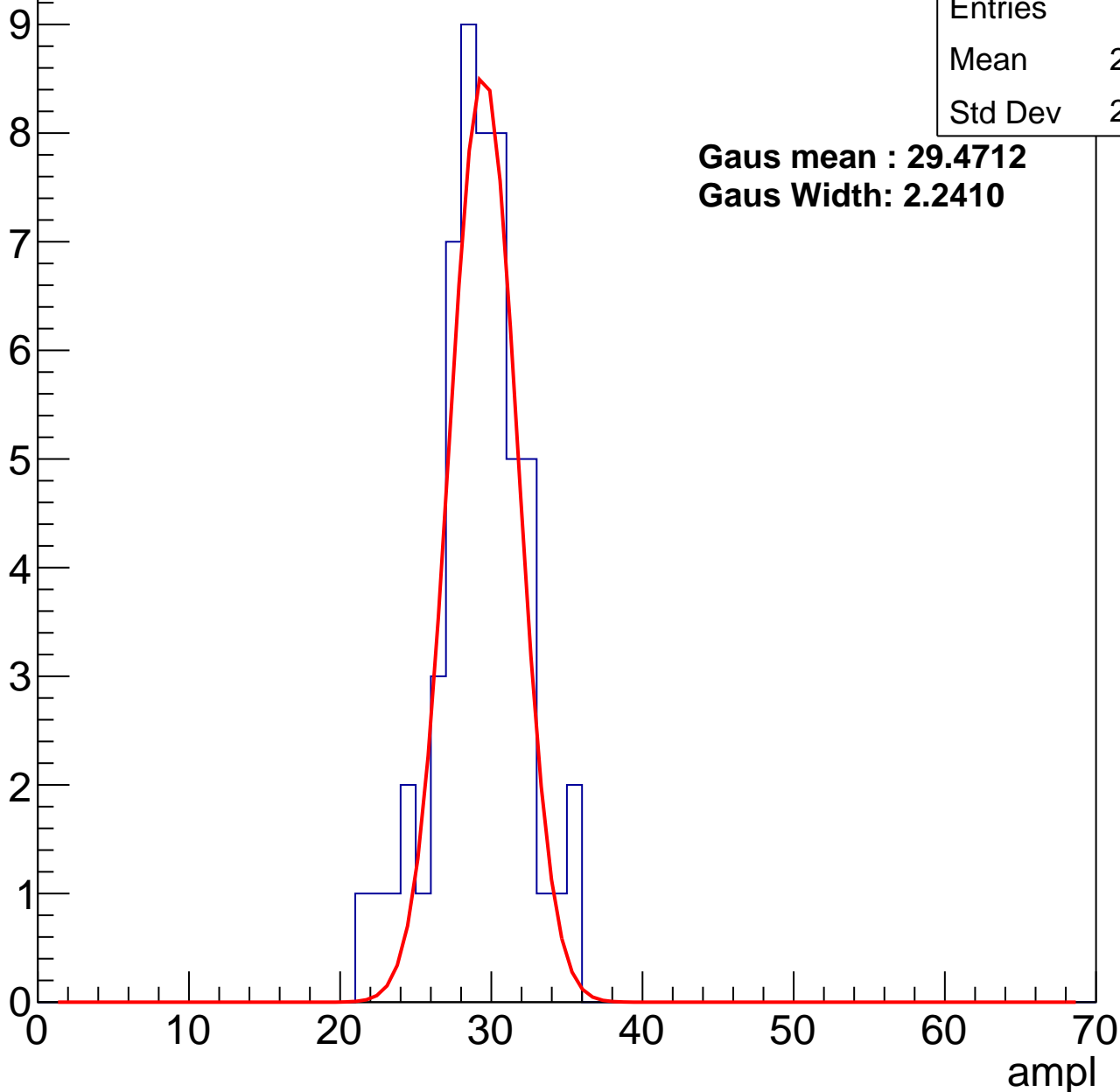
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	28.76
Std Dev	2.898

**Gaus mean : 29.4712**

**Gaus Width: 2.2410**



# B1L000S, U21-ch2, adc1

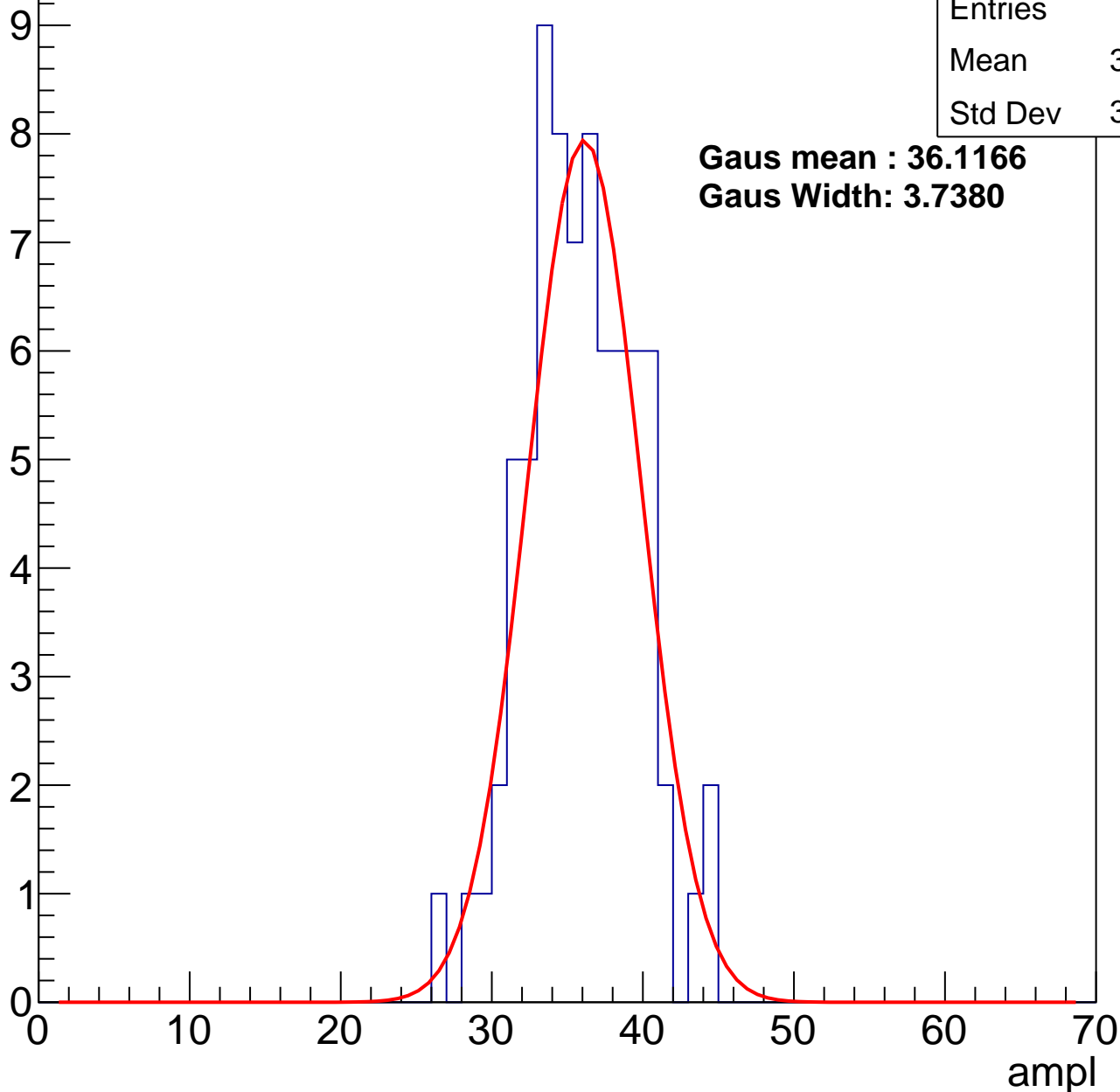
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	35.49
Std Dev	3.622

**Gaus mean : 36.1166**

**Gaus Width: 3.7380**



# B1L000S, U21-ch2, adc2

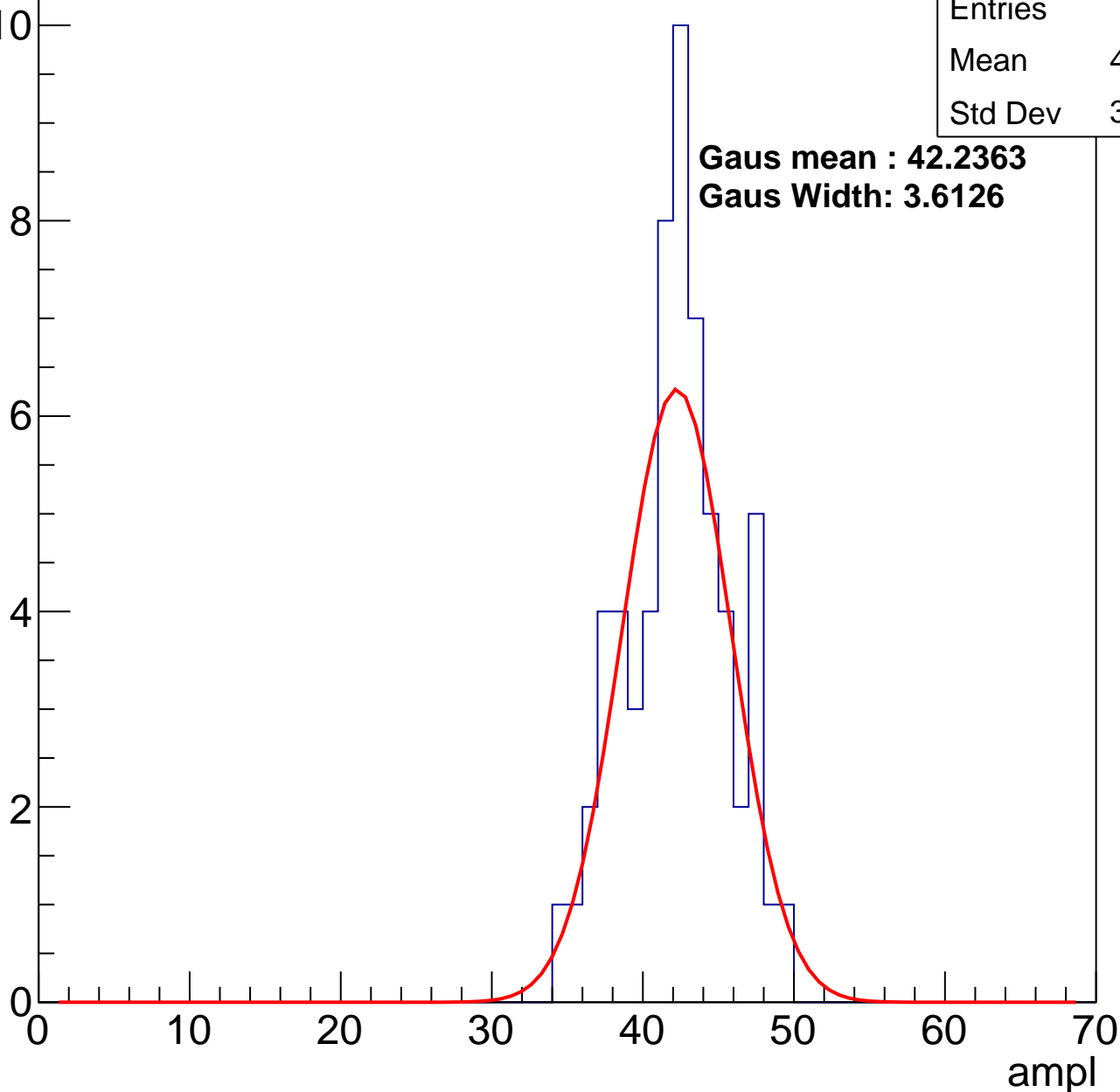
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.79
Std Dev	3.366

**Gaus mean : 42.2363**

**Gaus Width: 3.6126**

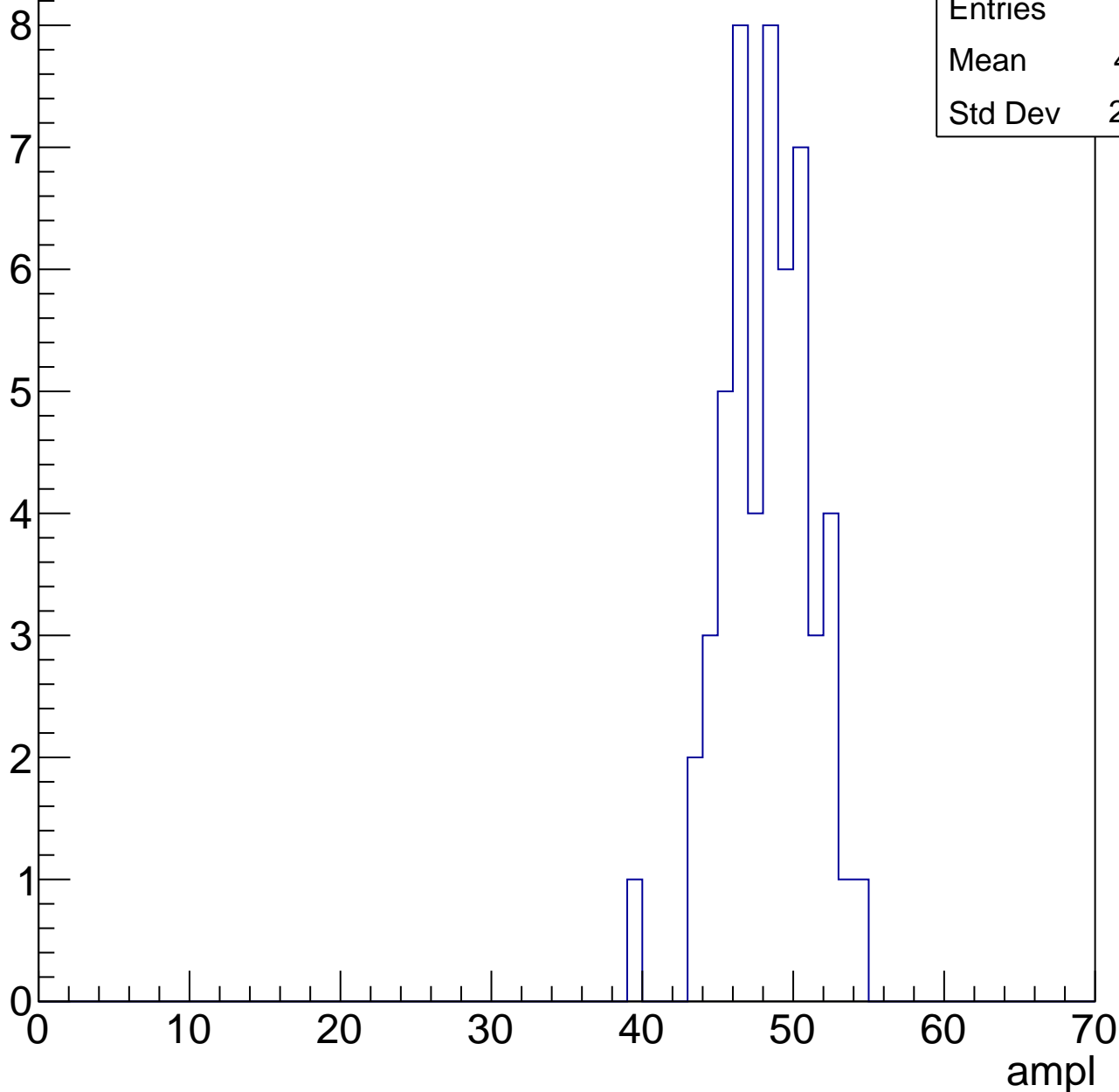


# B1L000S, U21-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	47.81
Std Dev	2.895

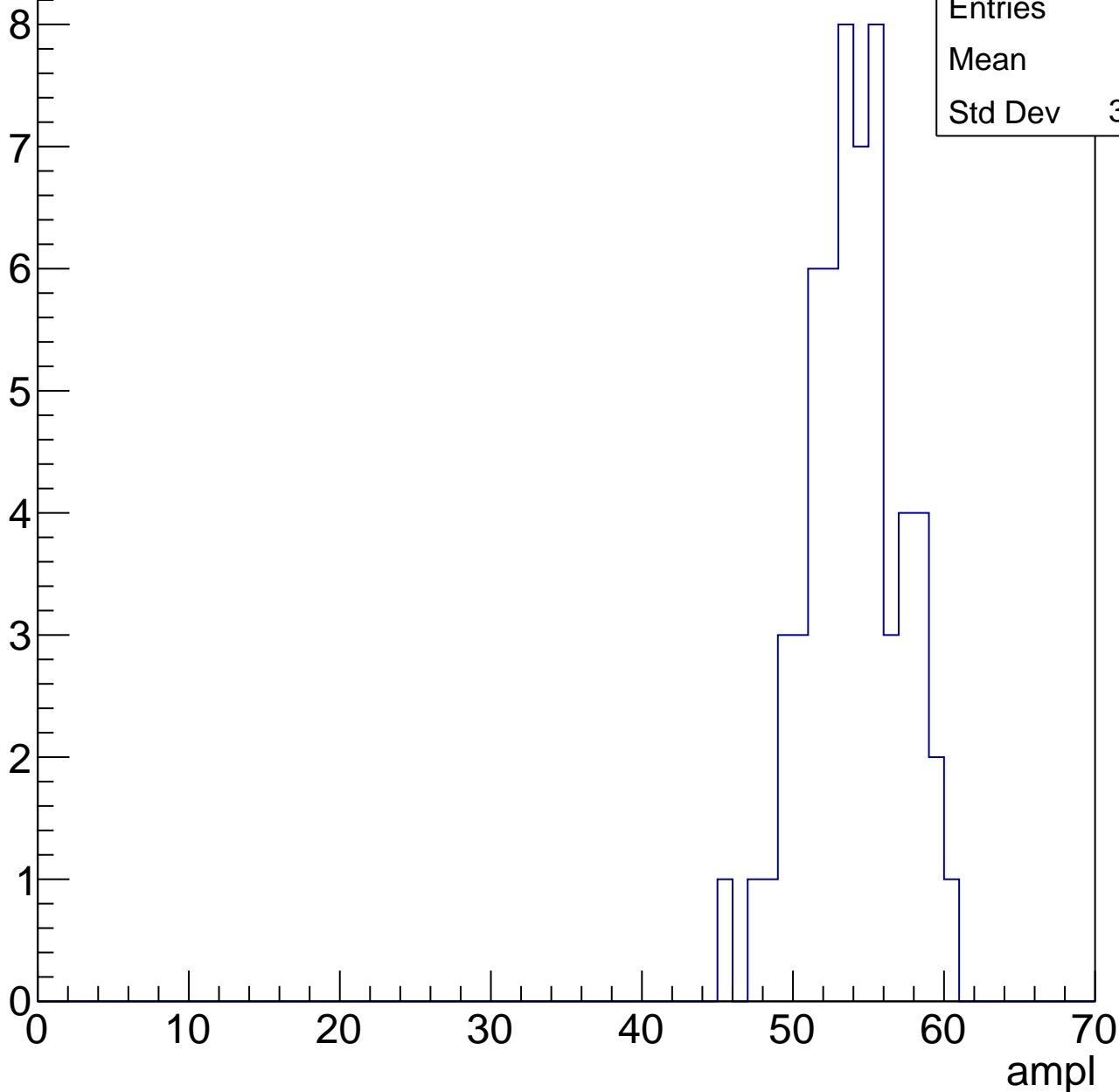


# B1L000S, U21-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	53.5
Std Dev	3.125

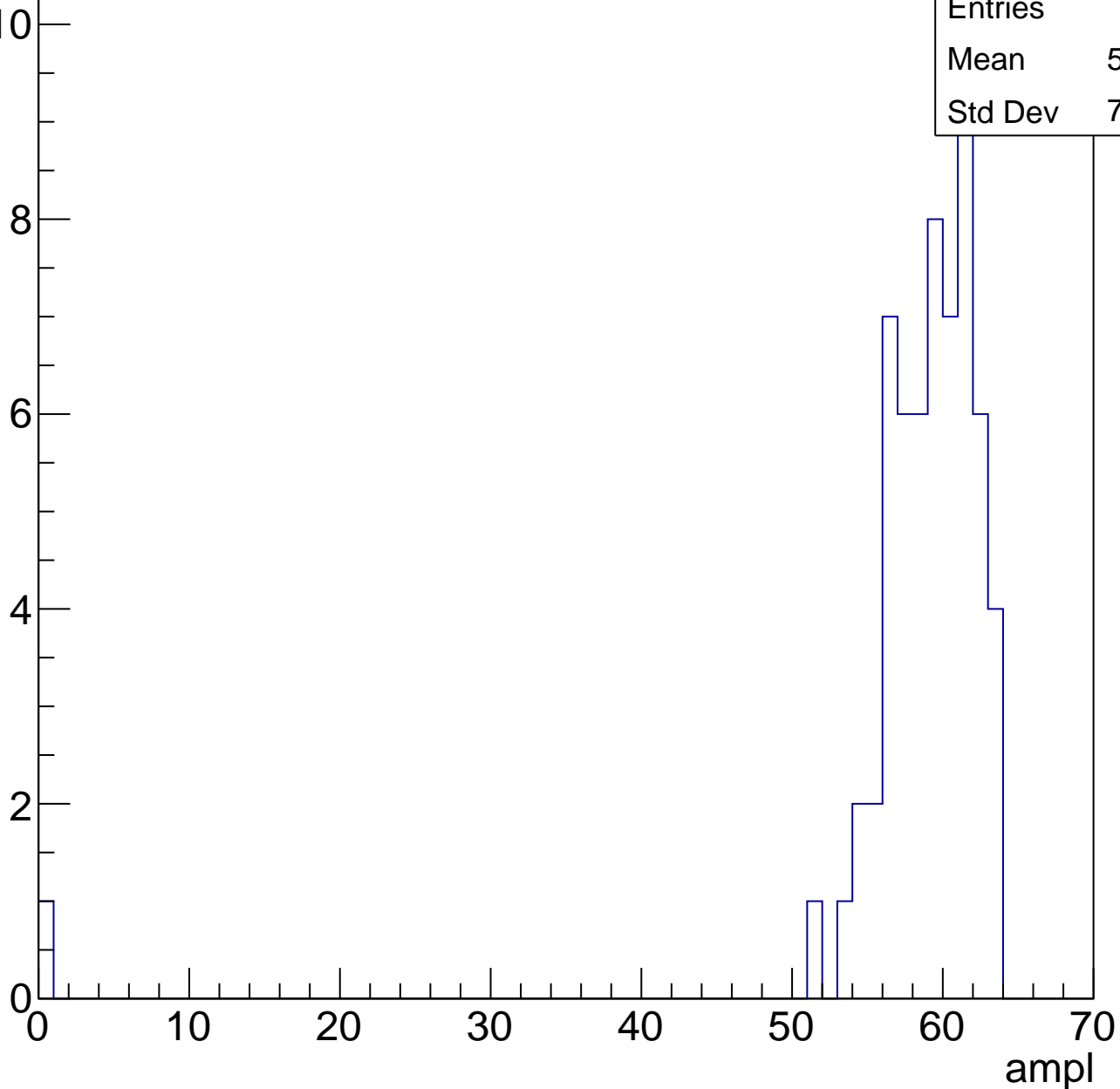


# B1L000S, U21-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	57.87
Std Dev	7.937

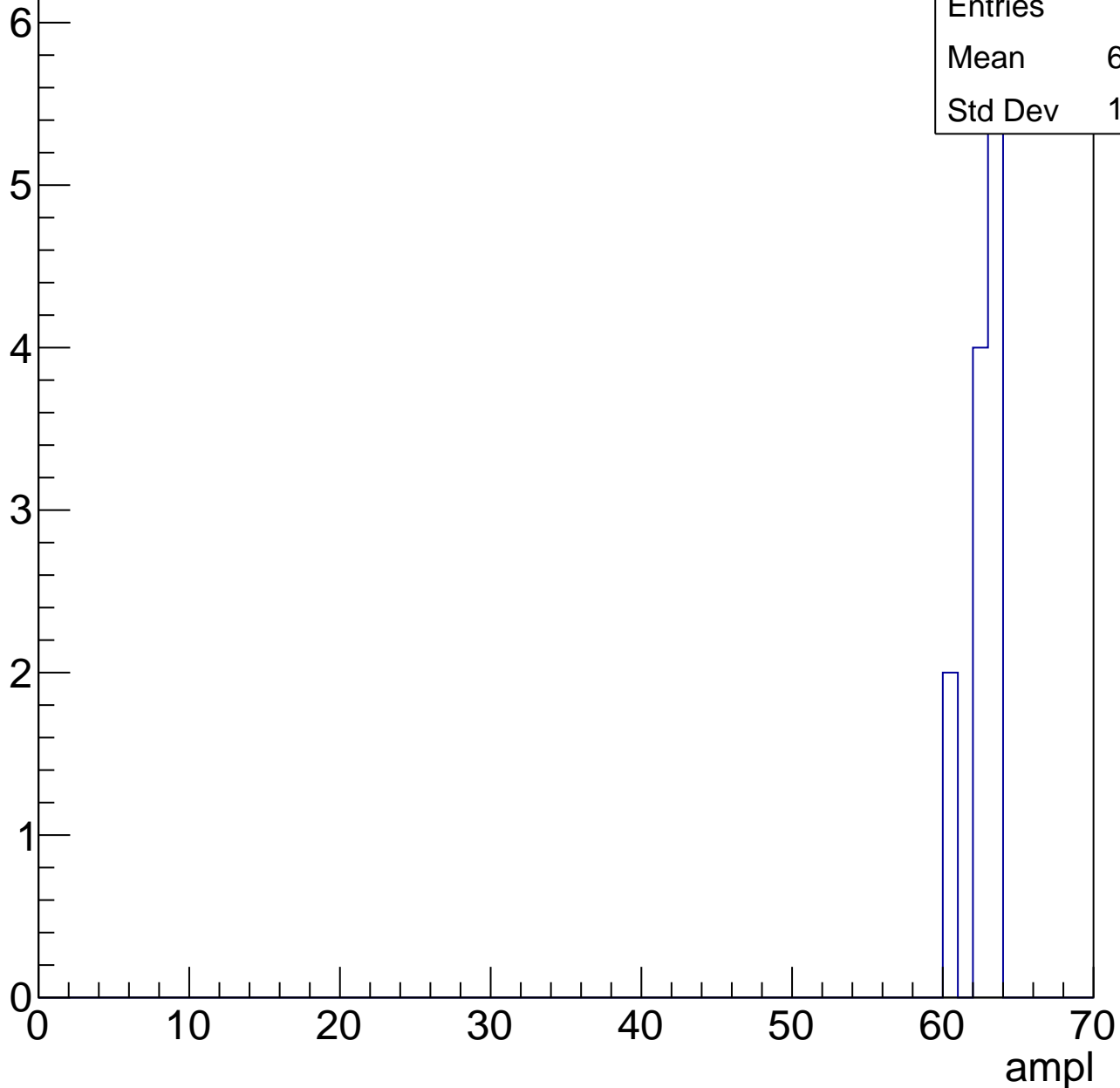


# B1L000S, U21-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	62.17
Std Dev	1.067





# B1L000S, U21-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch3, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	30.55
Std Dev	3.02

**Gaus mean : 30.7810**

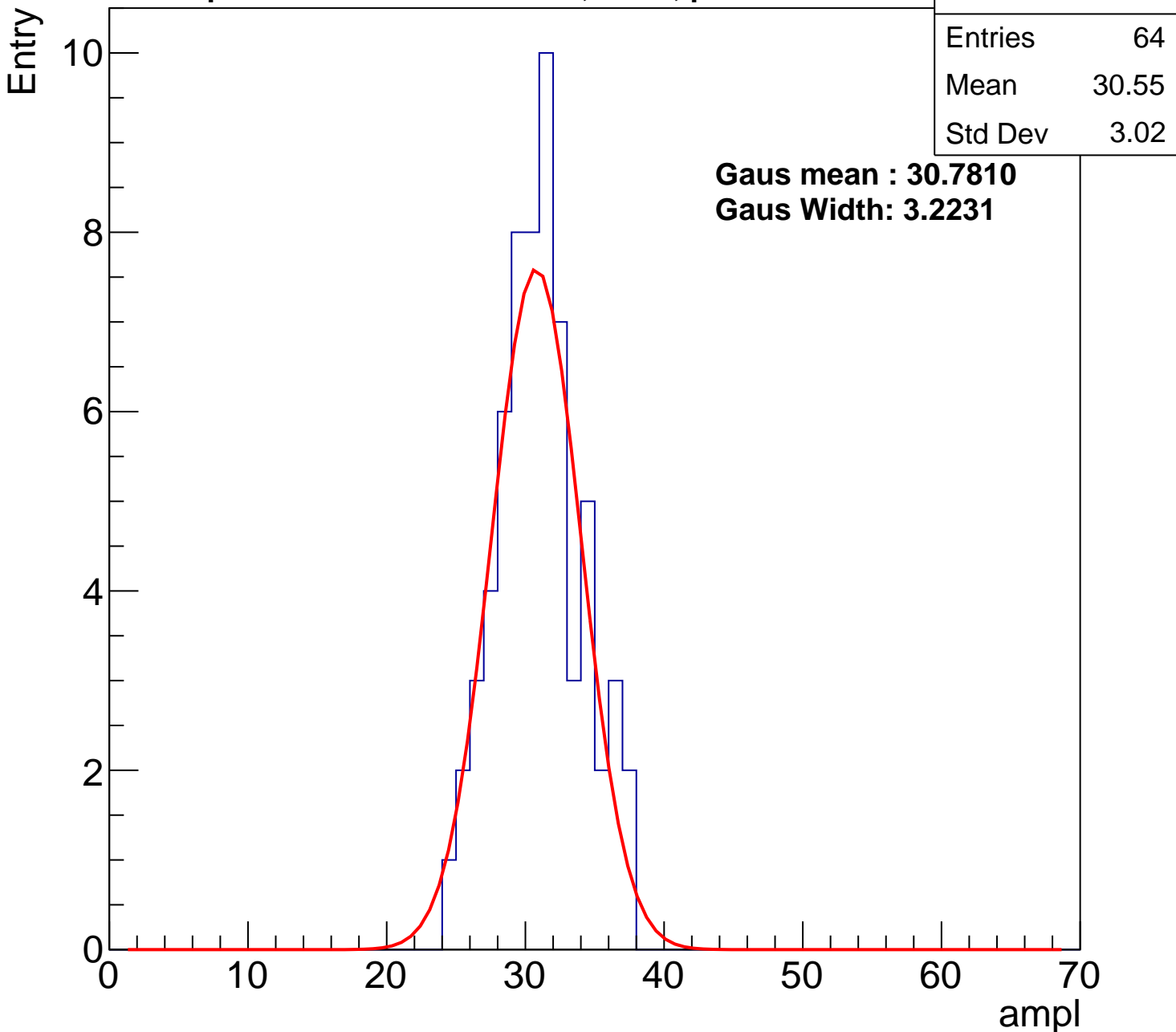
**Gaus Width: 3.2231**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch3, adc1

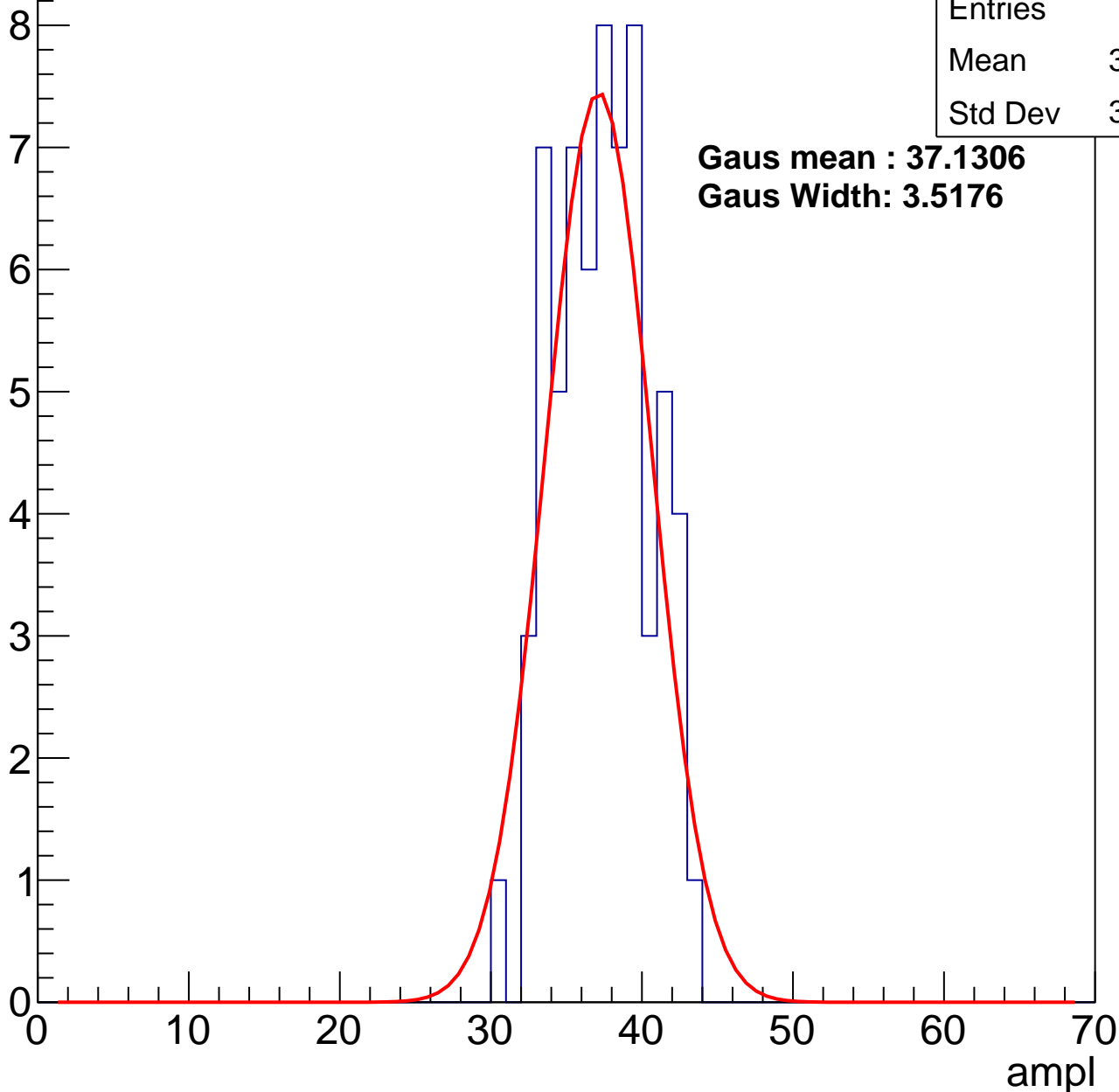
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	36.89
Std Dev	3.029

**Gaus mean : 37.1306**

**Gaus Width: 3.5176**



# B1L000S, U21-ch3, adc2

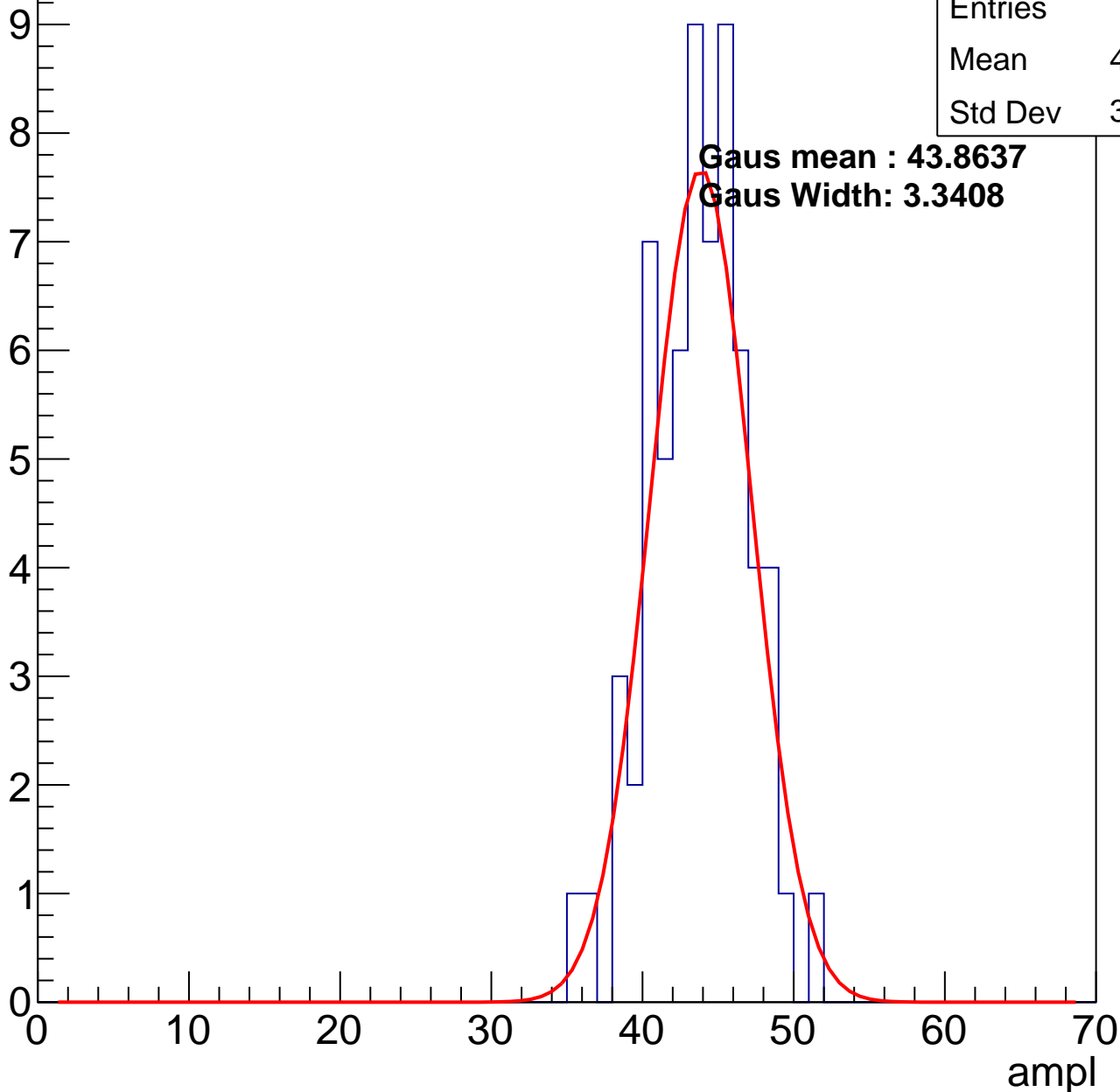
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	43.27
Std Dev	3.174

**Gaus mean : 43.8637**

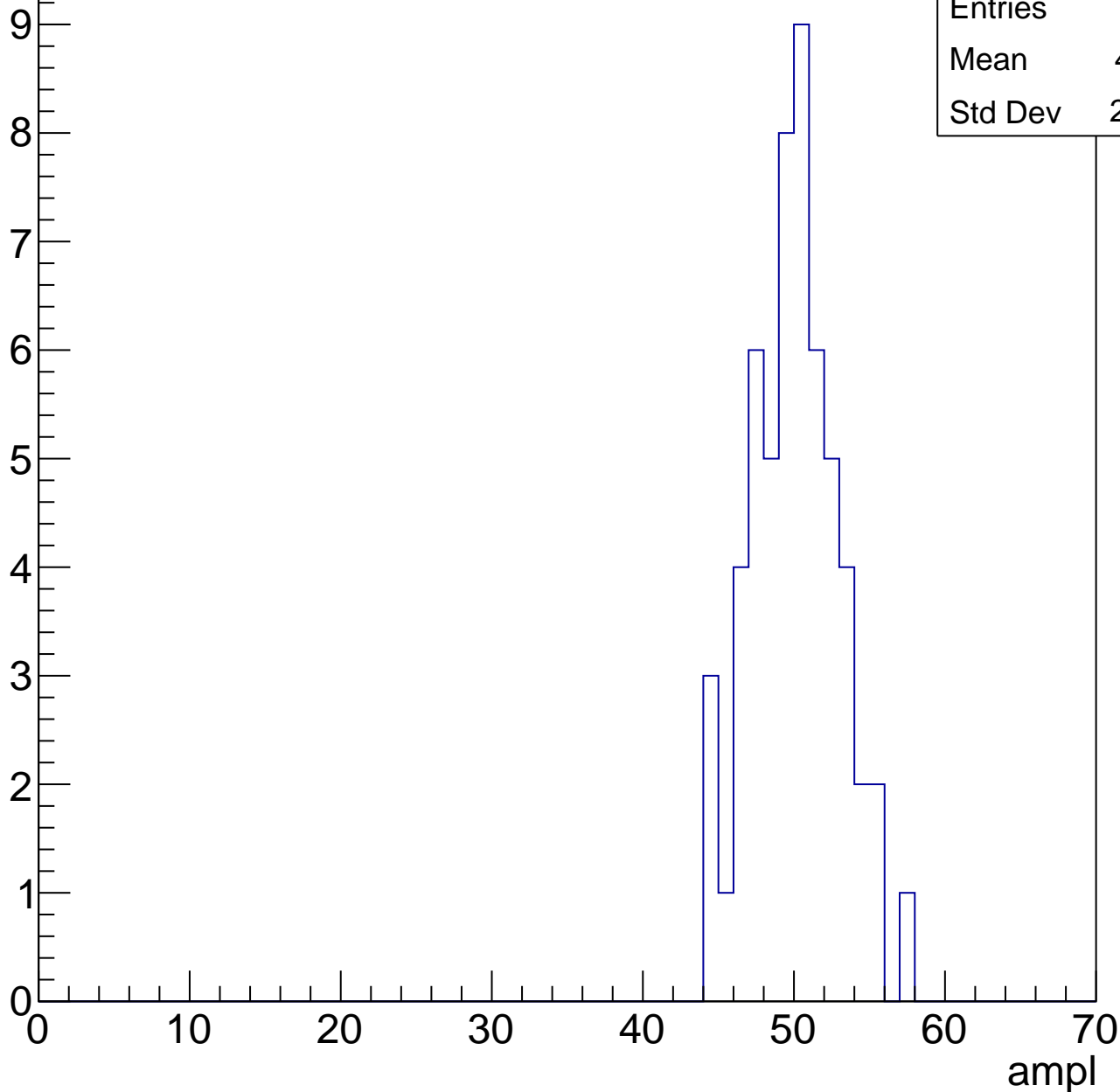
**Gaus Width: 3.3408**



# B1L000S, U21-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

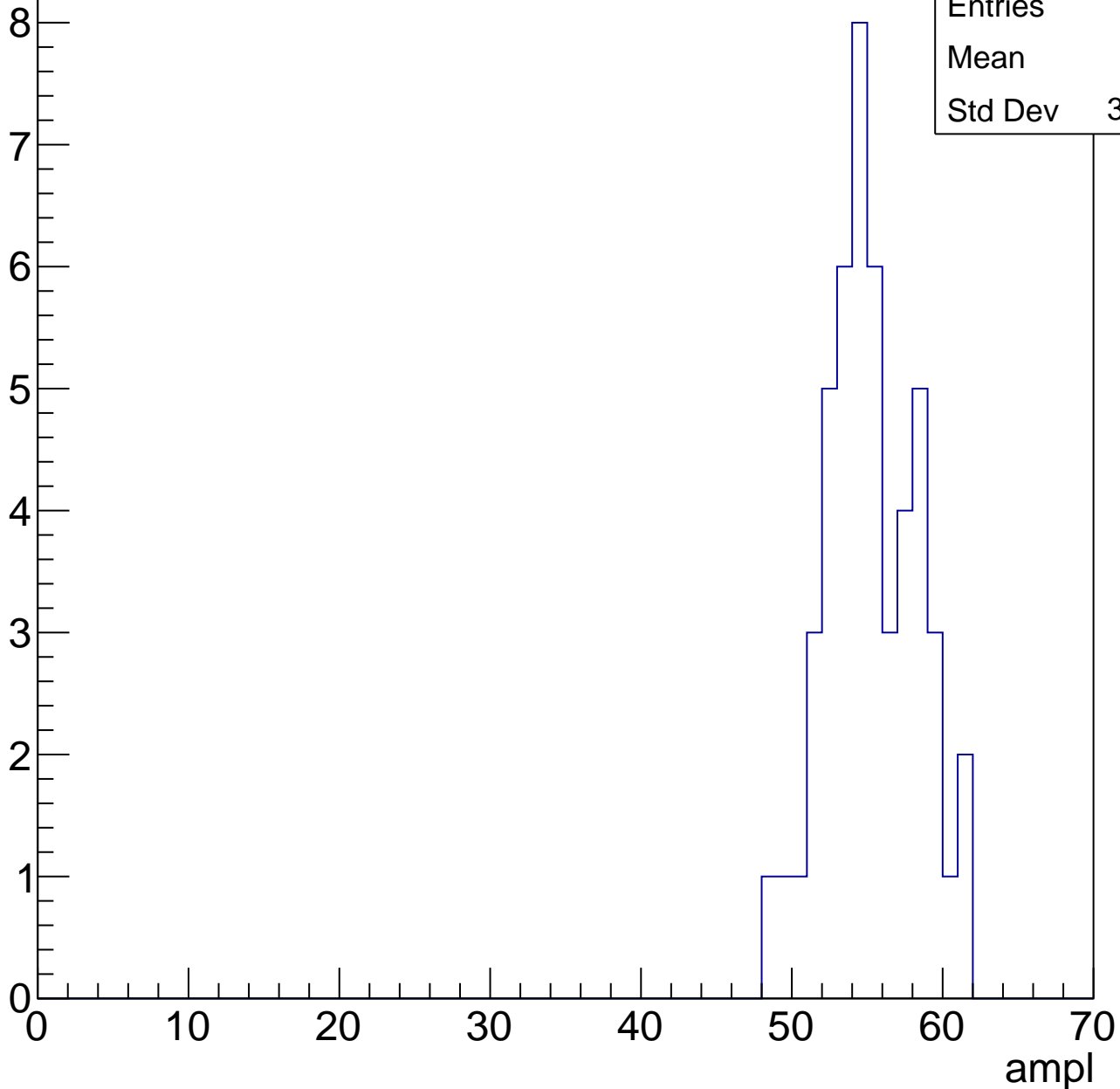


# B1L000S, U21-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

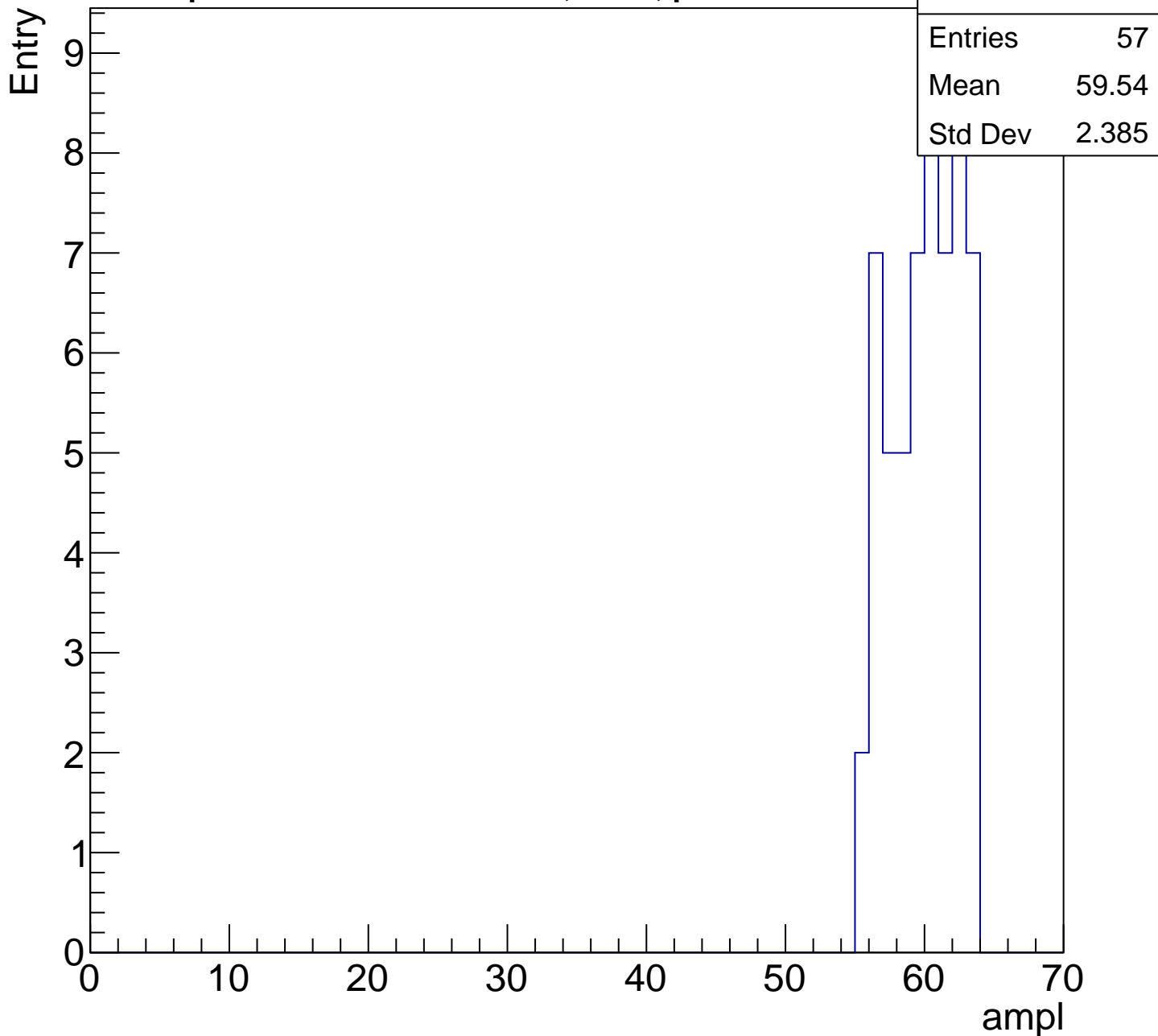
Entry

Entries	49
Mean	54.8
Std Dev	3.003



# B1L000S, U21-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch4, adc0

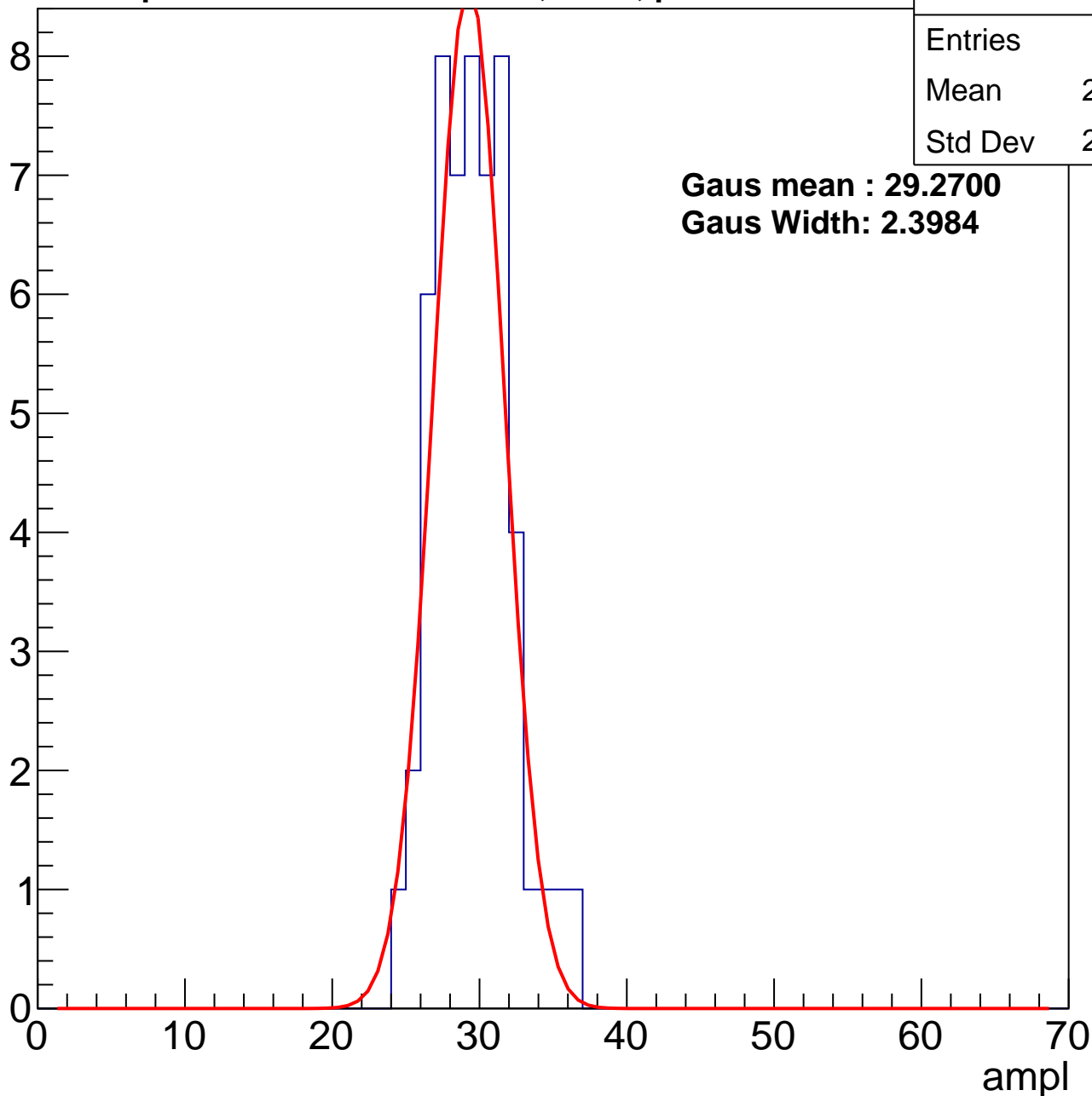
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	29.05
Std Dev	2.526

**Gaus mean : 29.2700**

**Gaus Width: 2.3984**



# B1L000S, U21-ch4, adc1

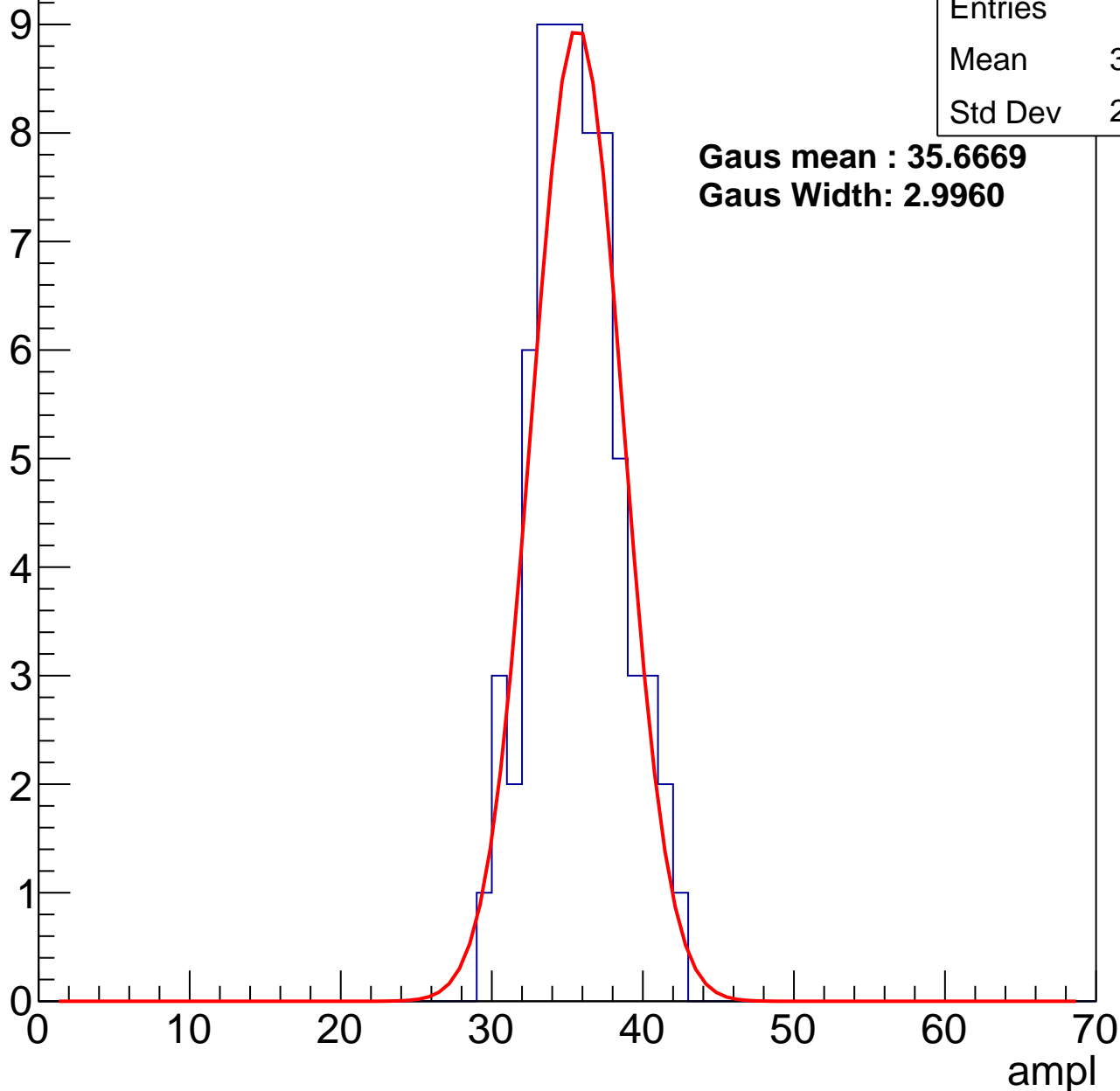
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.16
Std Dev	2.872

**Gaus mean : 35.6669**

**Gaus Width: 2.9960**



# B1L000S, U21-ch4, adc2

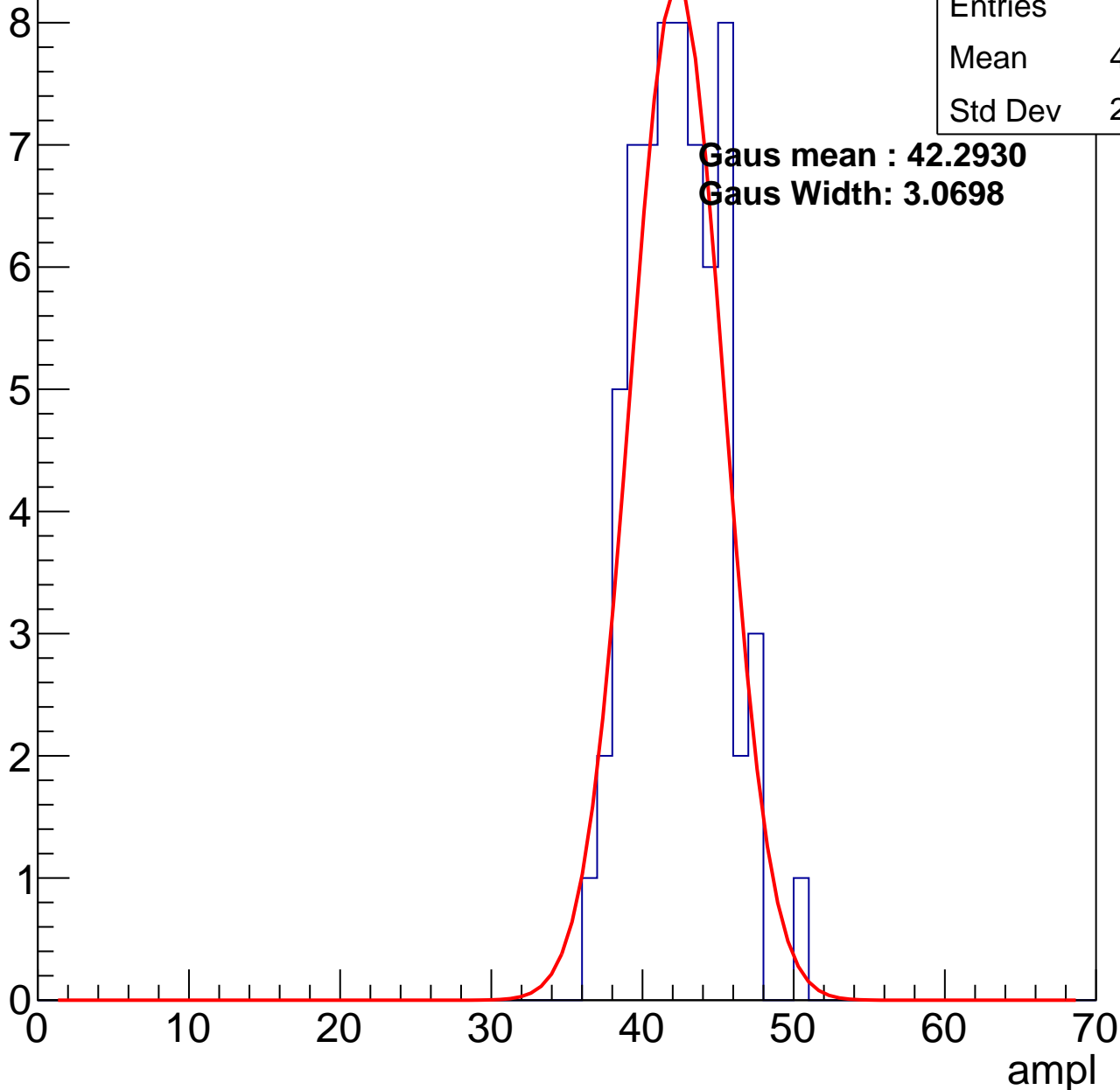
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.92
Std Dev	2.879

**Gaus mean : 42.2930**

**Gaus Width: 3.0698**



# B1L000S, U21-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

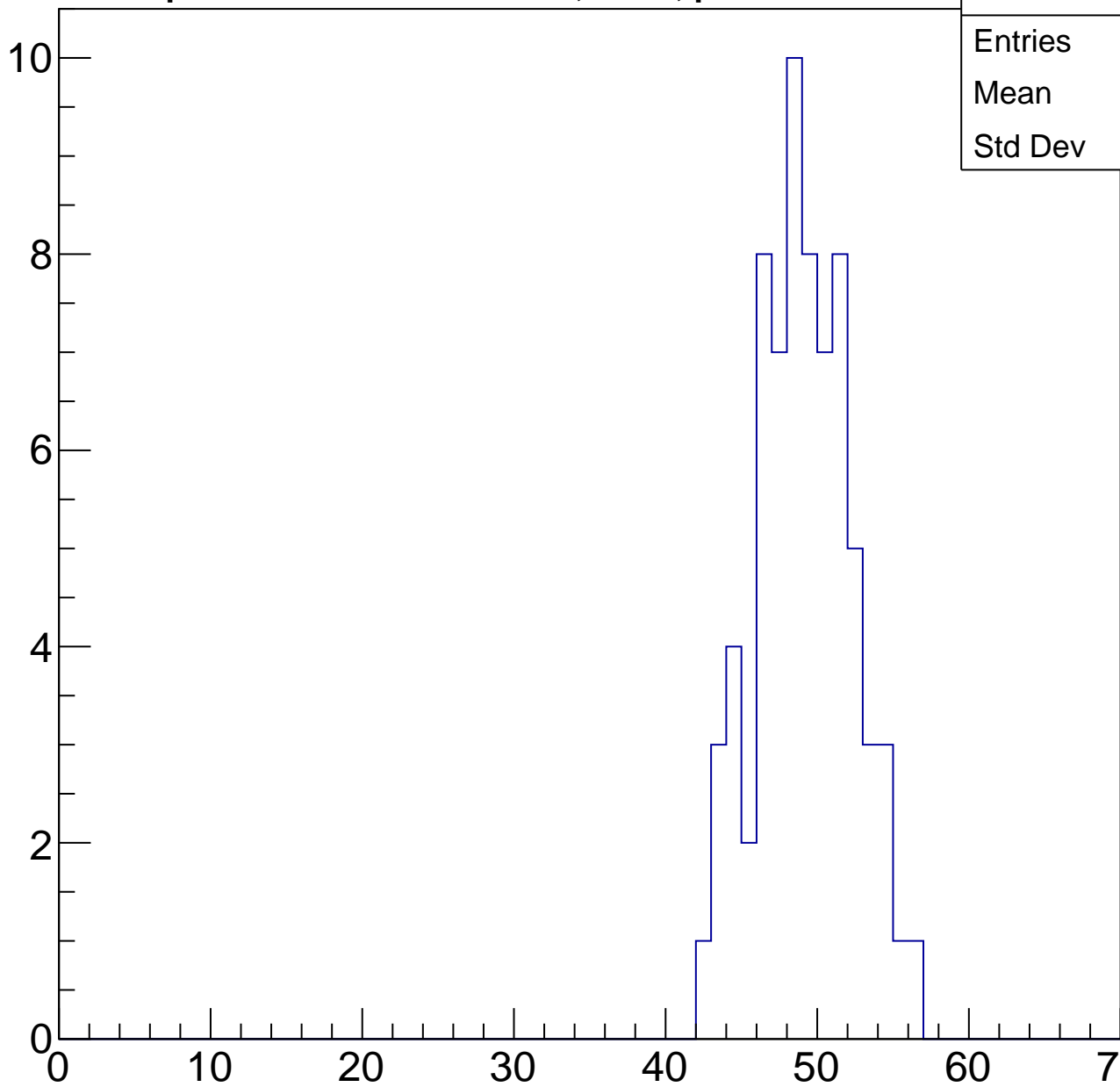
Entries	71
Mean	48.68
Std Dev	3.103

Entry

10  
8  
6  
4  
2  
0

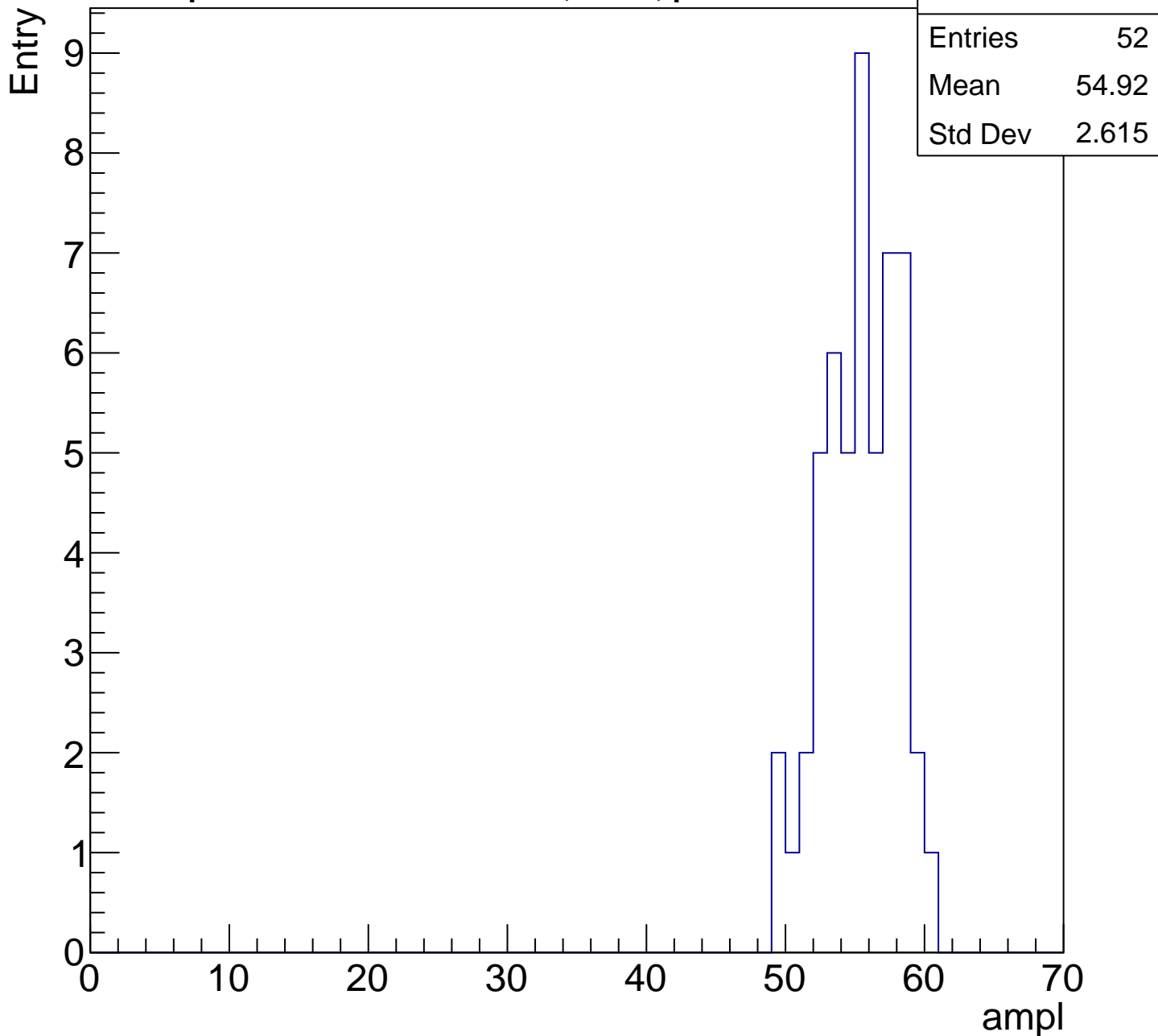
0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch4, adc4

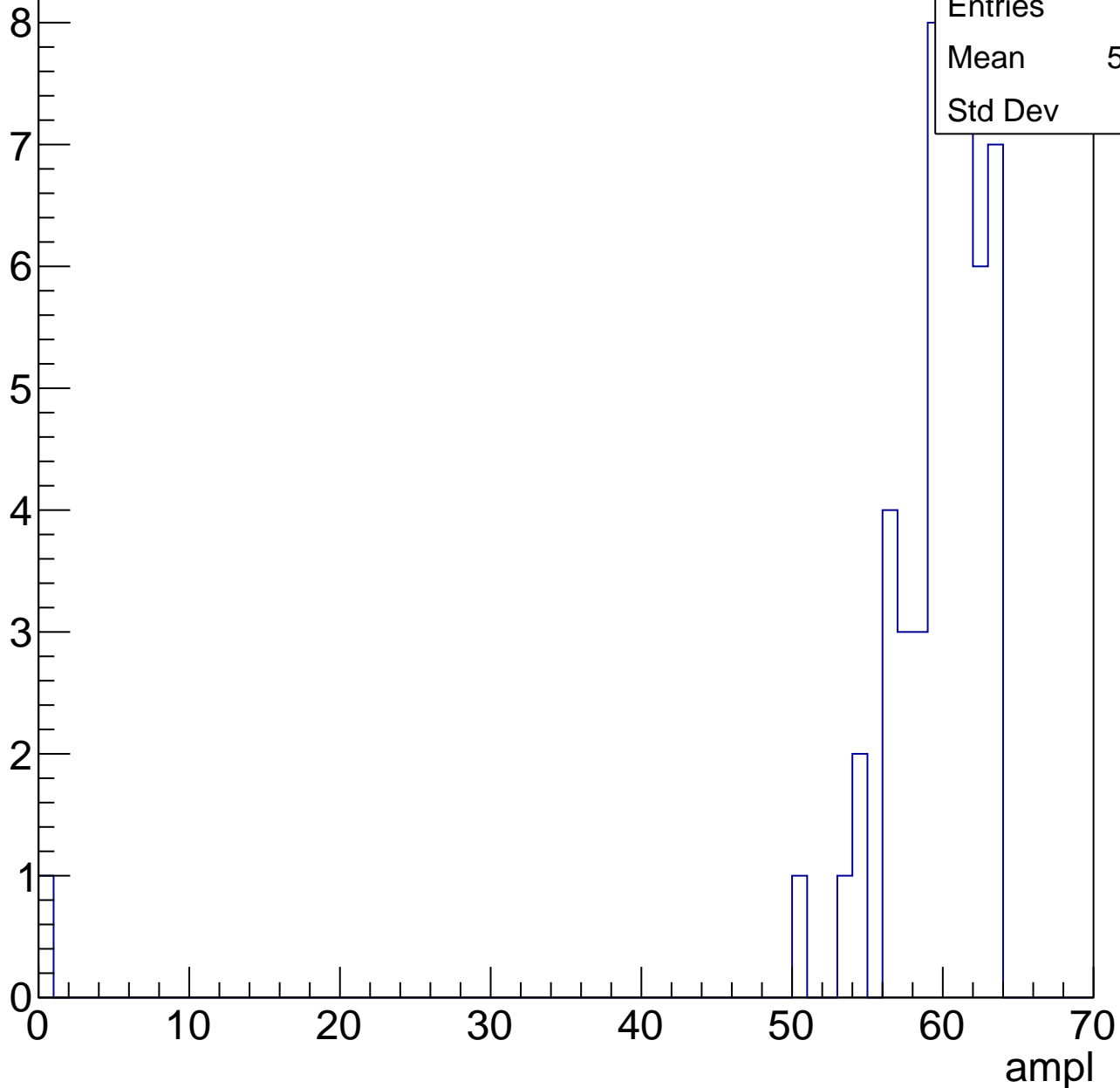
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

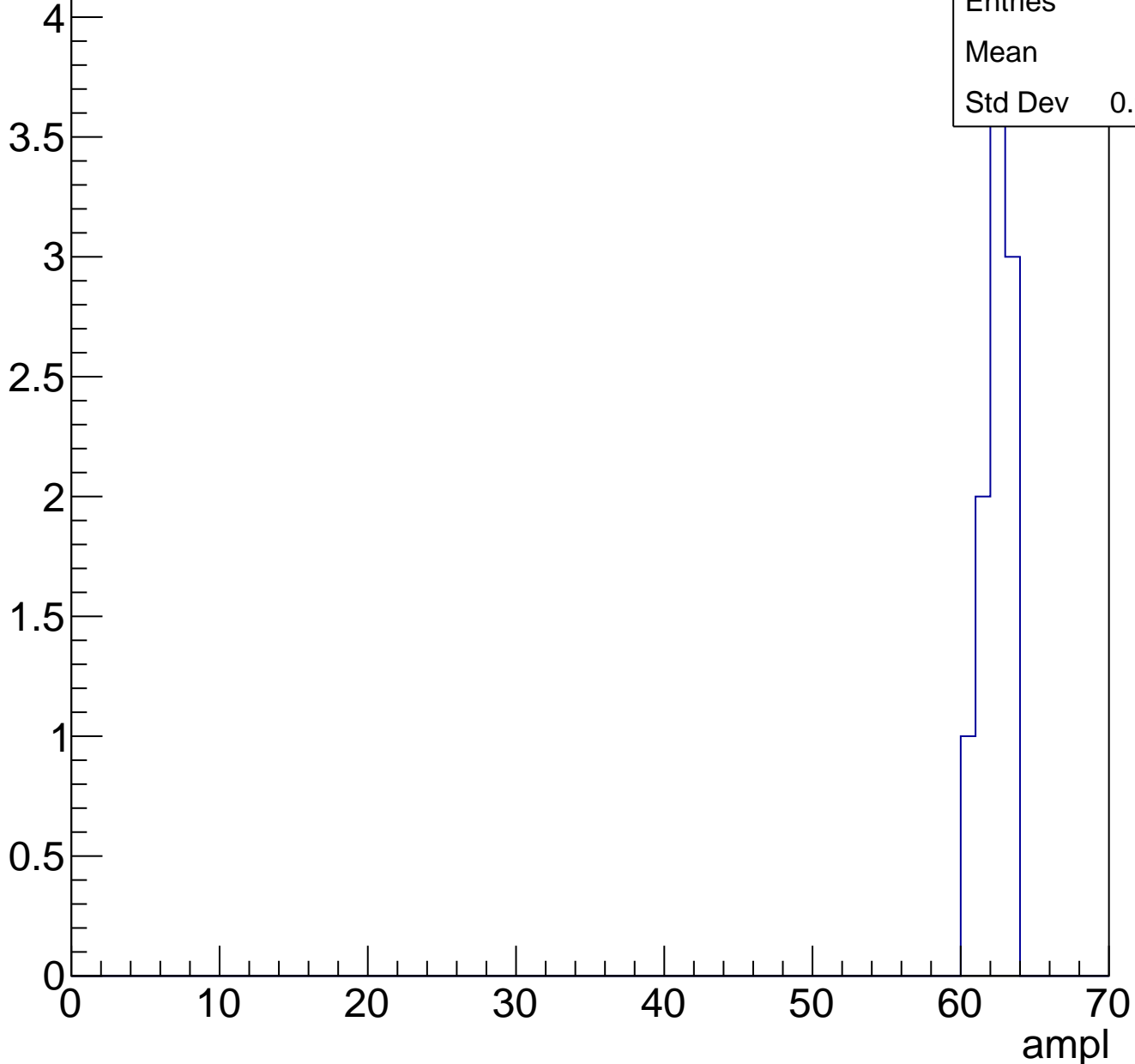
Entry



# B1L000S, U21-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	10
Mean	61.9
Std Dev	0.9434



# B1L000S, U21-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch5, adc0

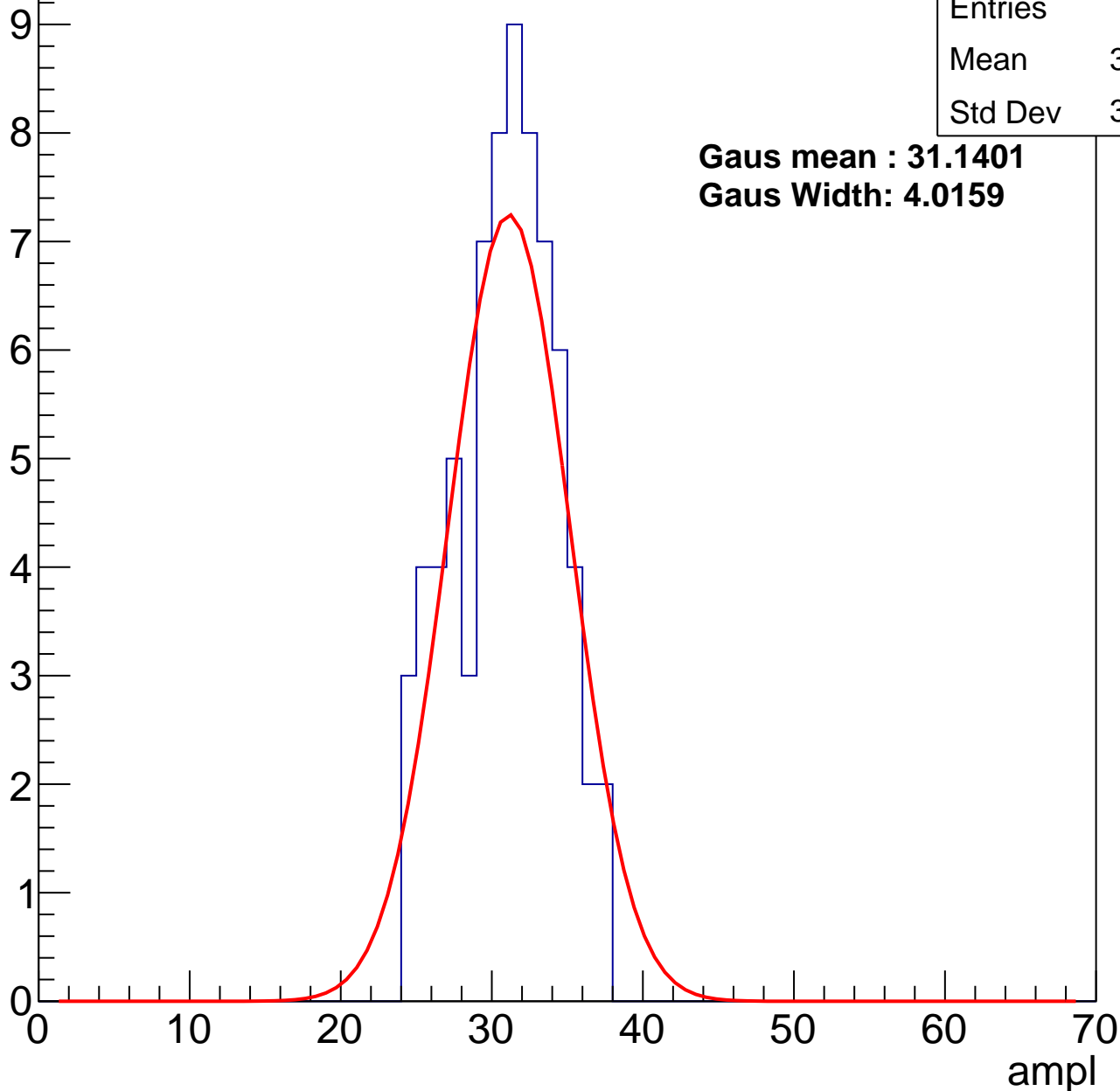
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	30.47
Std Dev	3.312

**Gaus mean : 31.1401**

**Gaus Width: 4.0159**



# B1L000S, U21-ch5, adc1

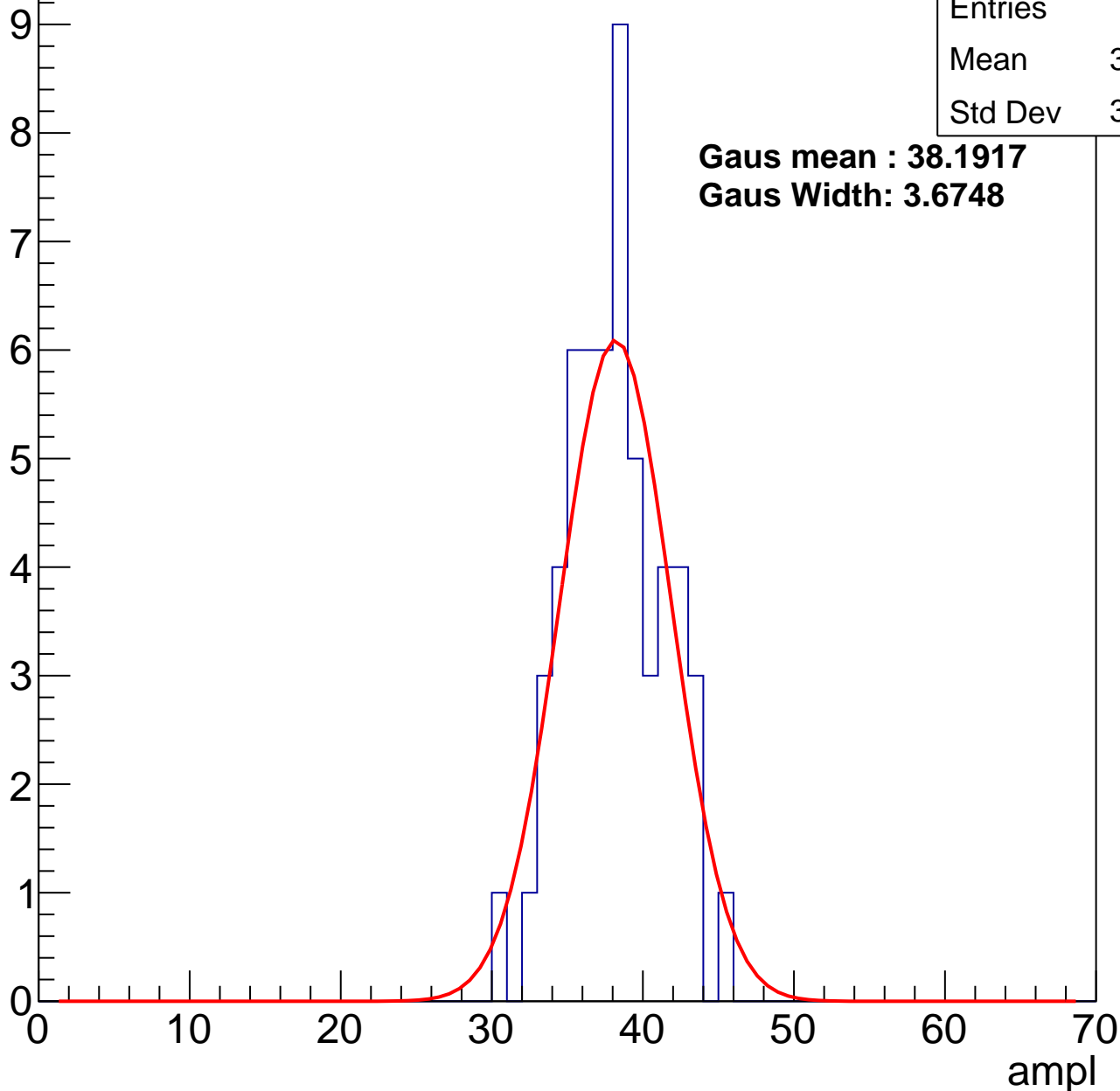
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	37.64
Std Dev	3.153

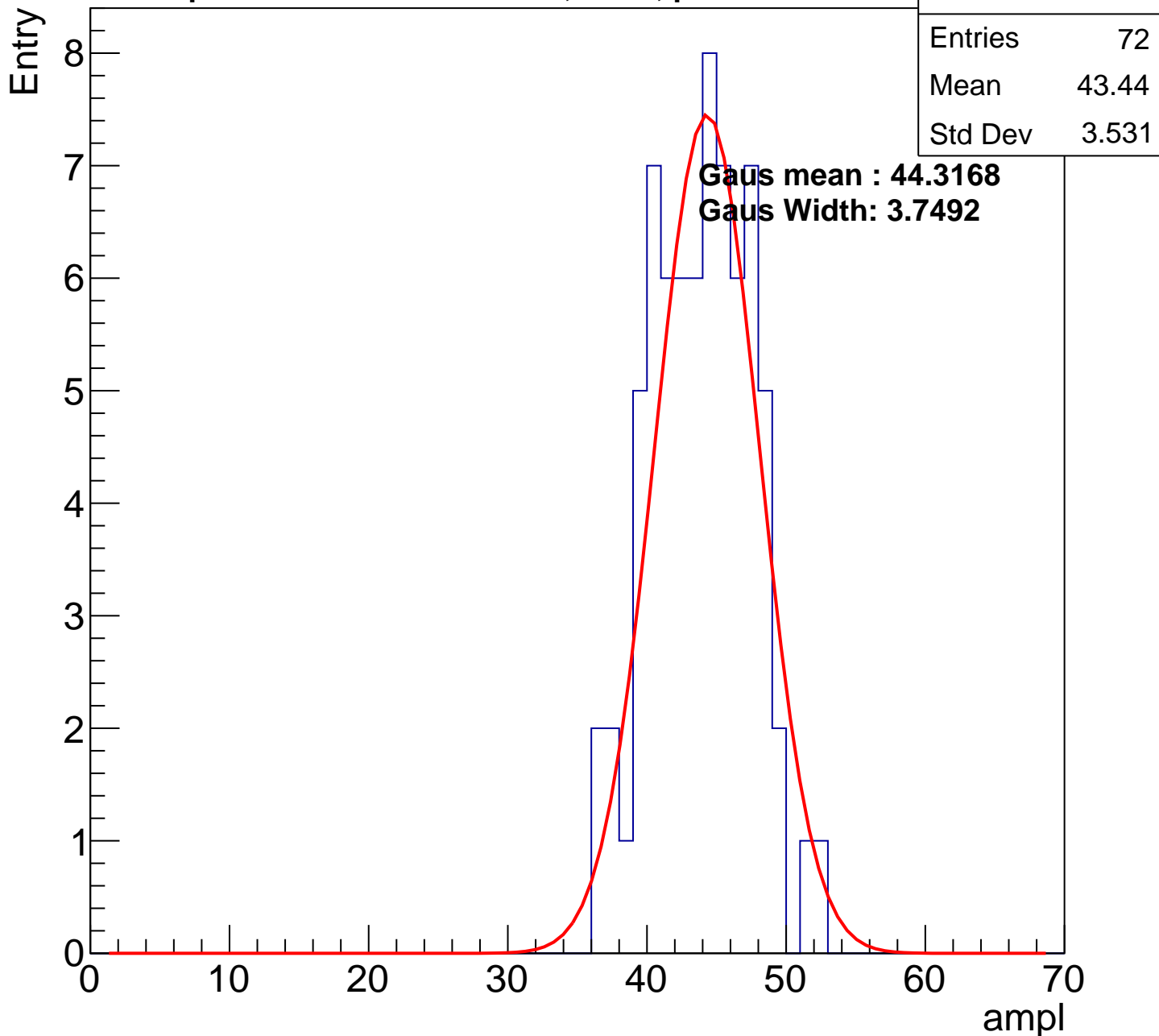
**Gaus mean : 38.1917**

**Gaus Width: 3.6748**



# B1L000S, U21-ch5, adc2

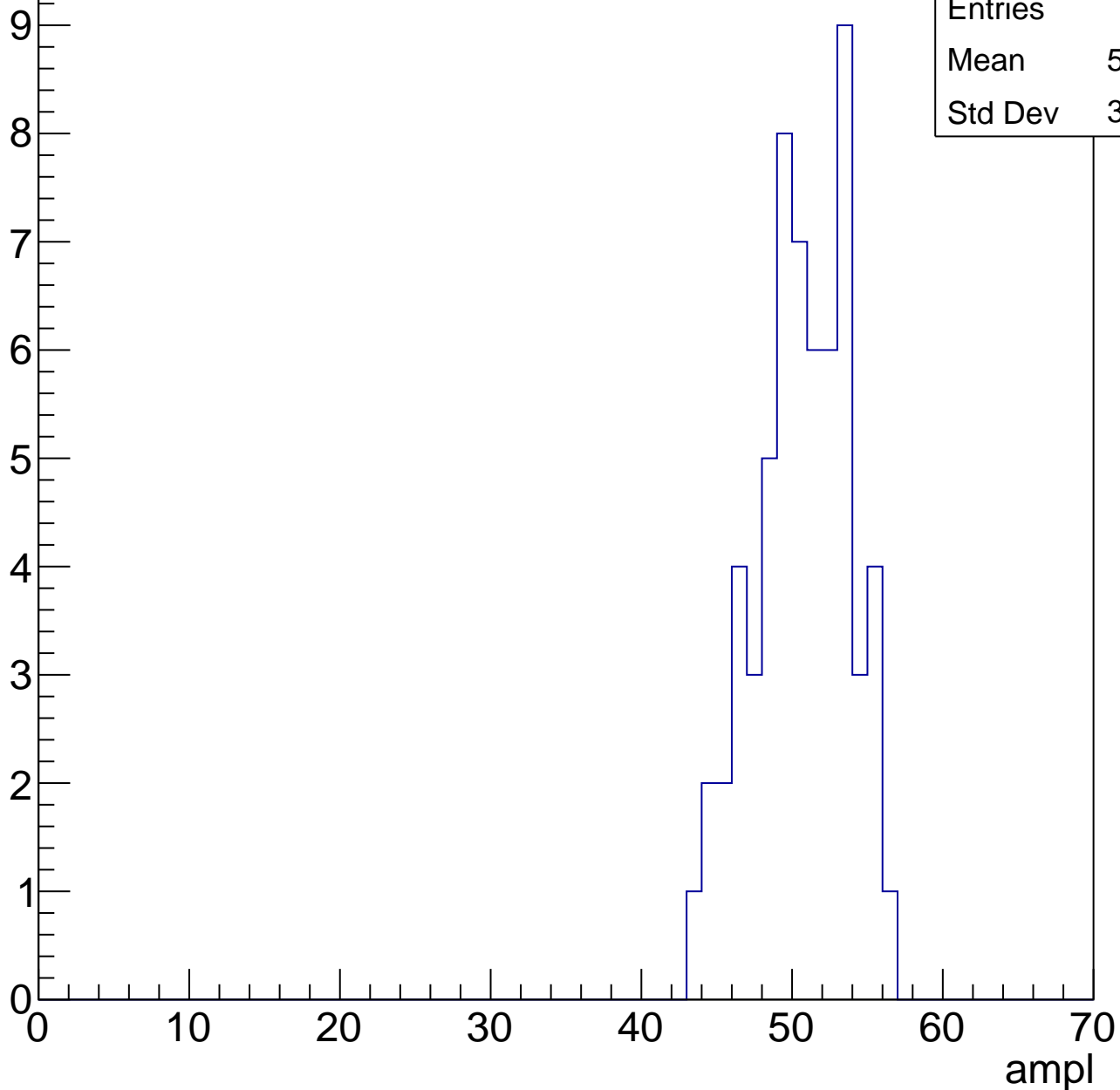
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch5, adc3

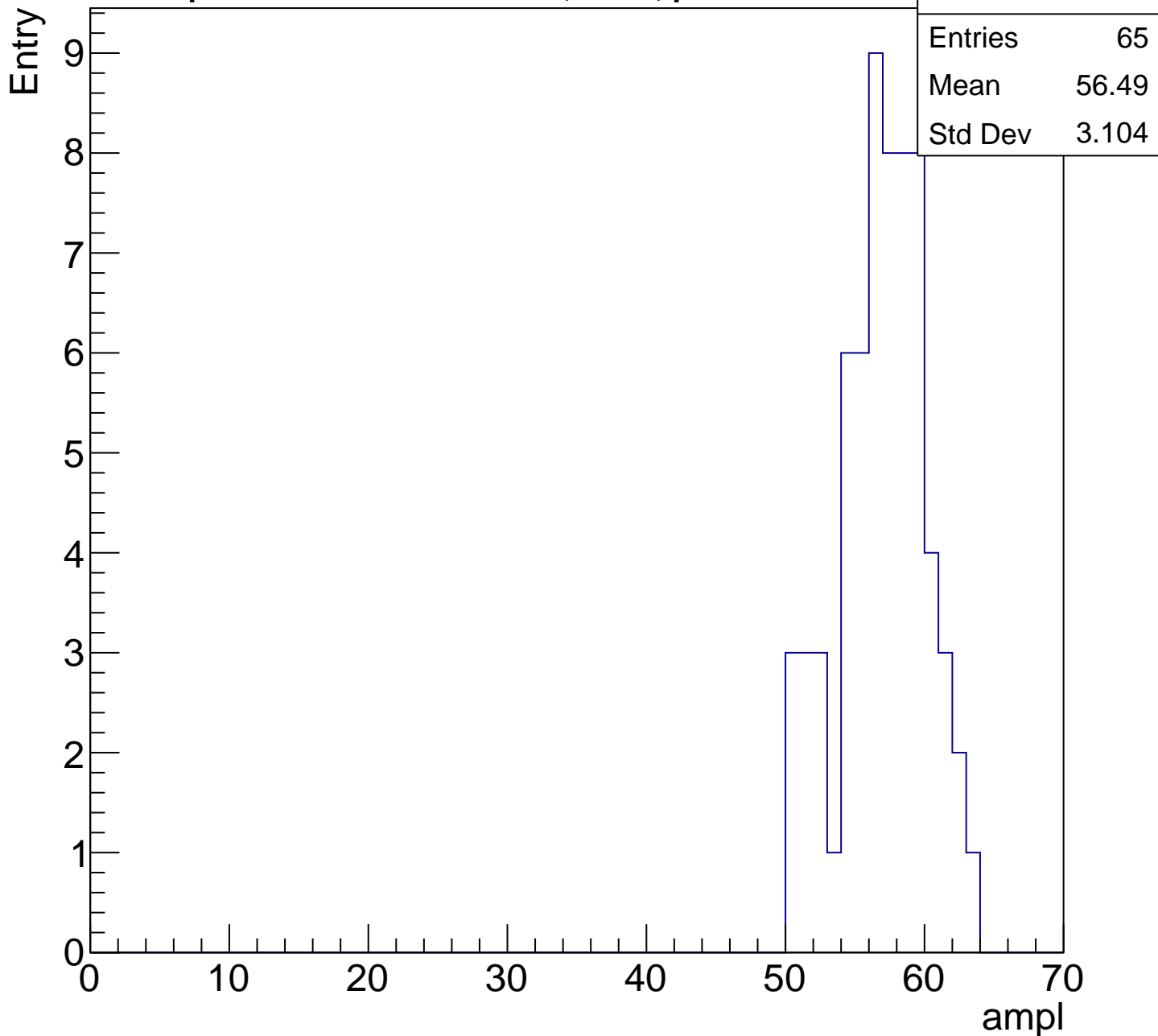
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch5, adc5

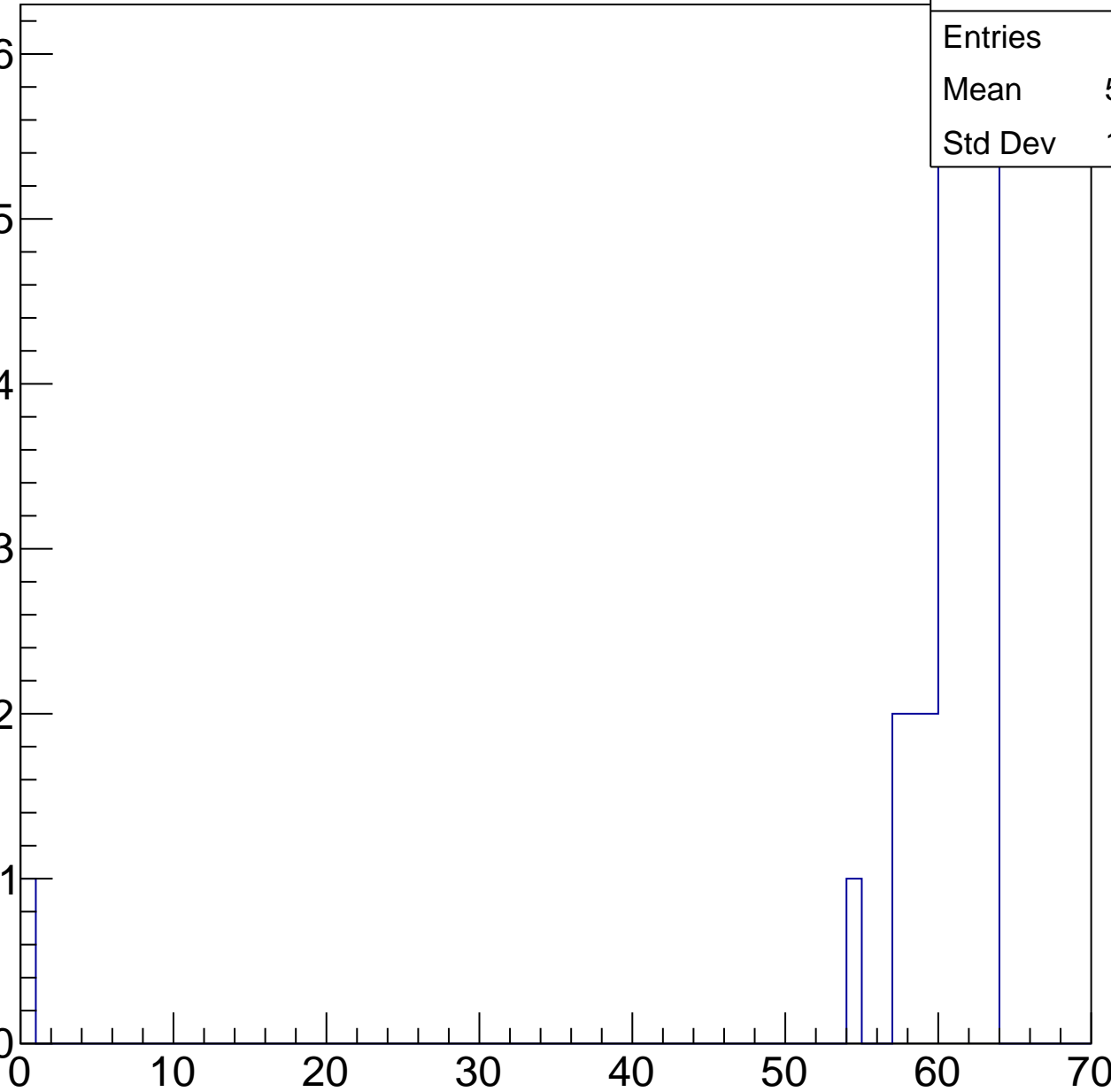
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

6  
5  
4  
3  
2  
1  
0

Entries	32
Mean	58.69
Std Dev	10.74

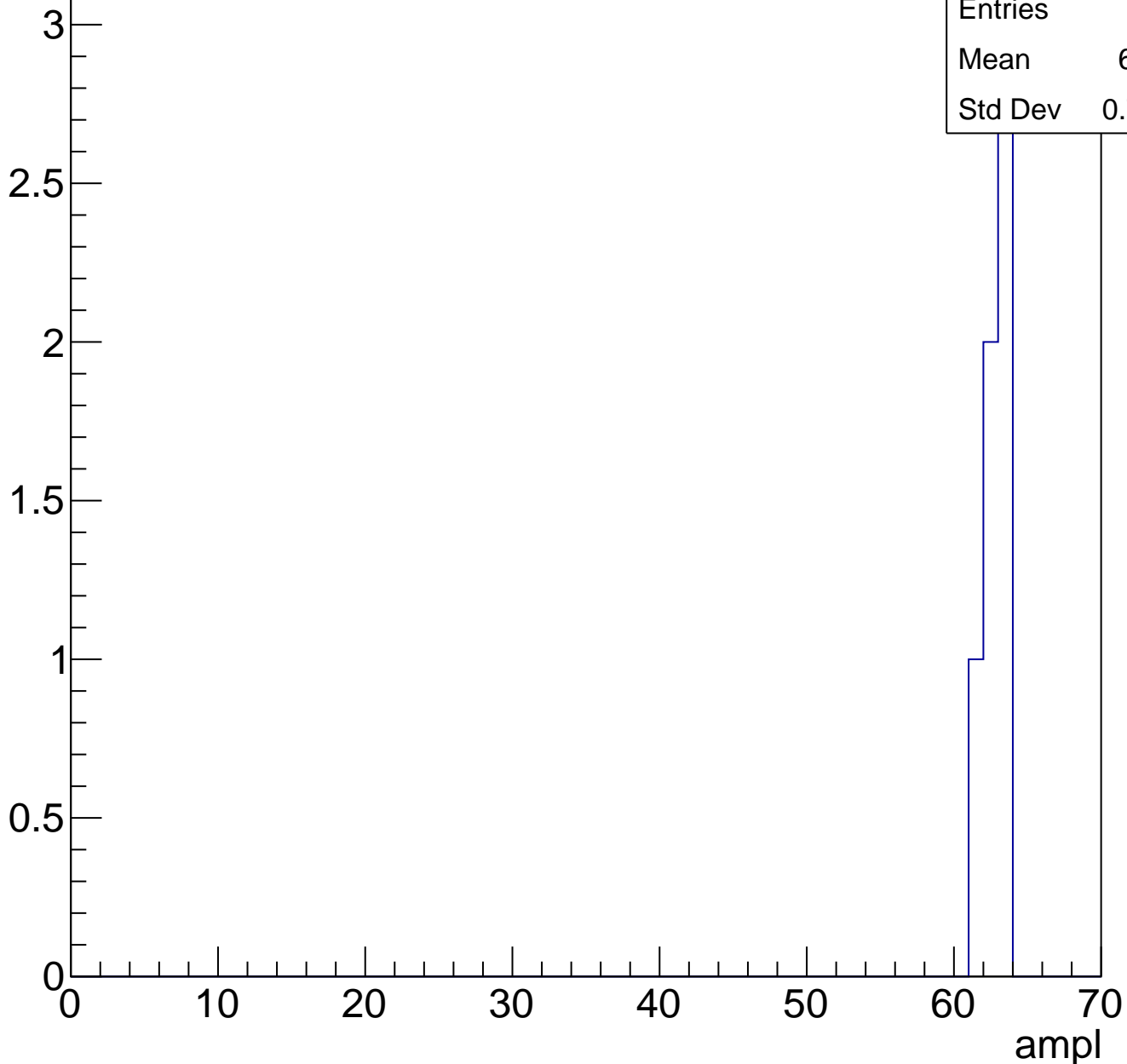
ampl



# B1L000S, U21-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B1L000S, U21-ch6, adc0

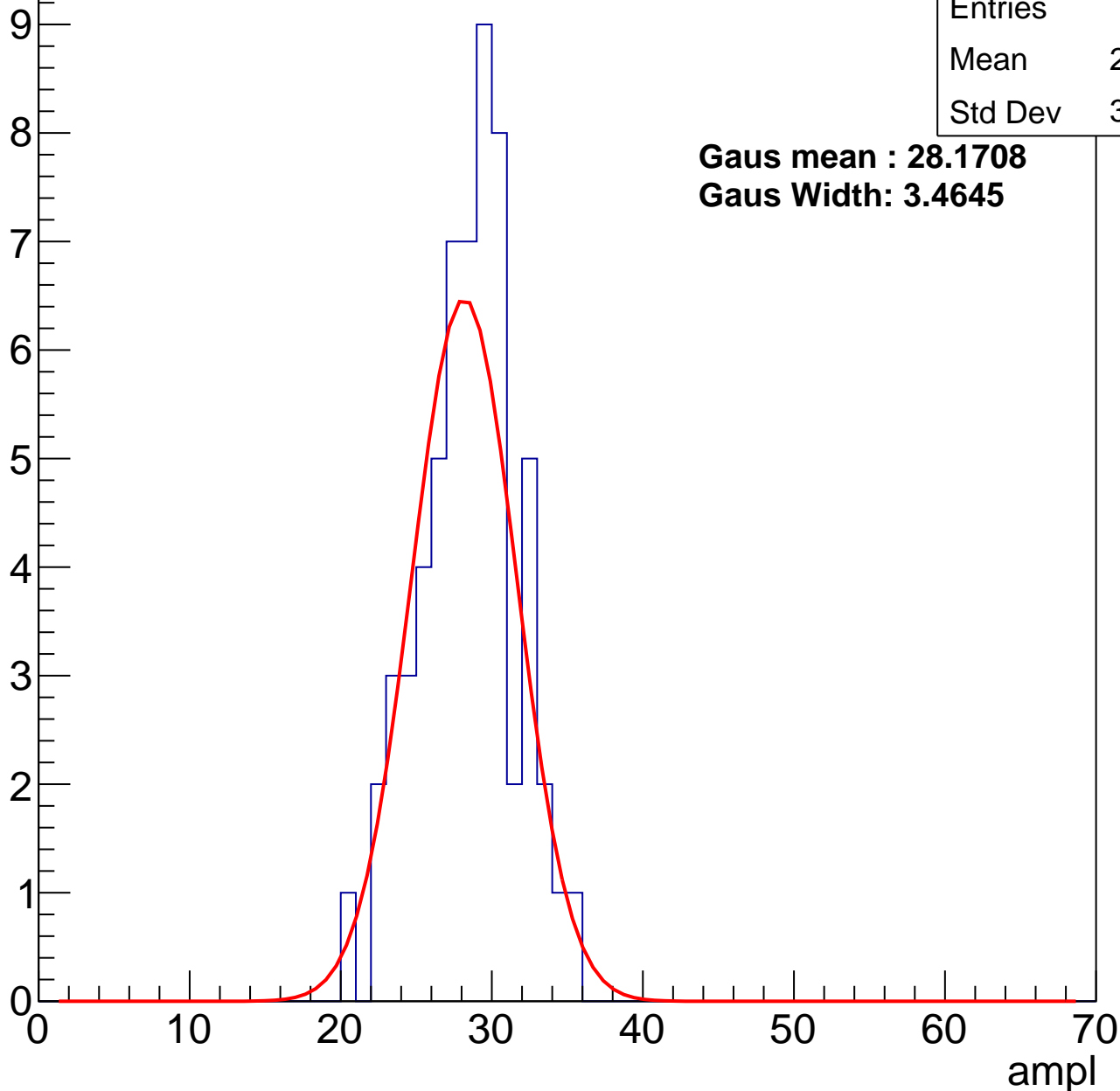
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.97
Std Dev	3.152

**Gaus mean : 28.1708**

**Gaus Width: 3.4645**



# B1L000S, U21-ch6, adc1

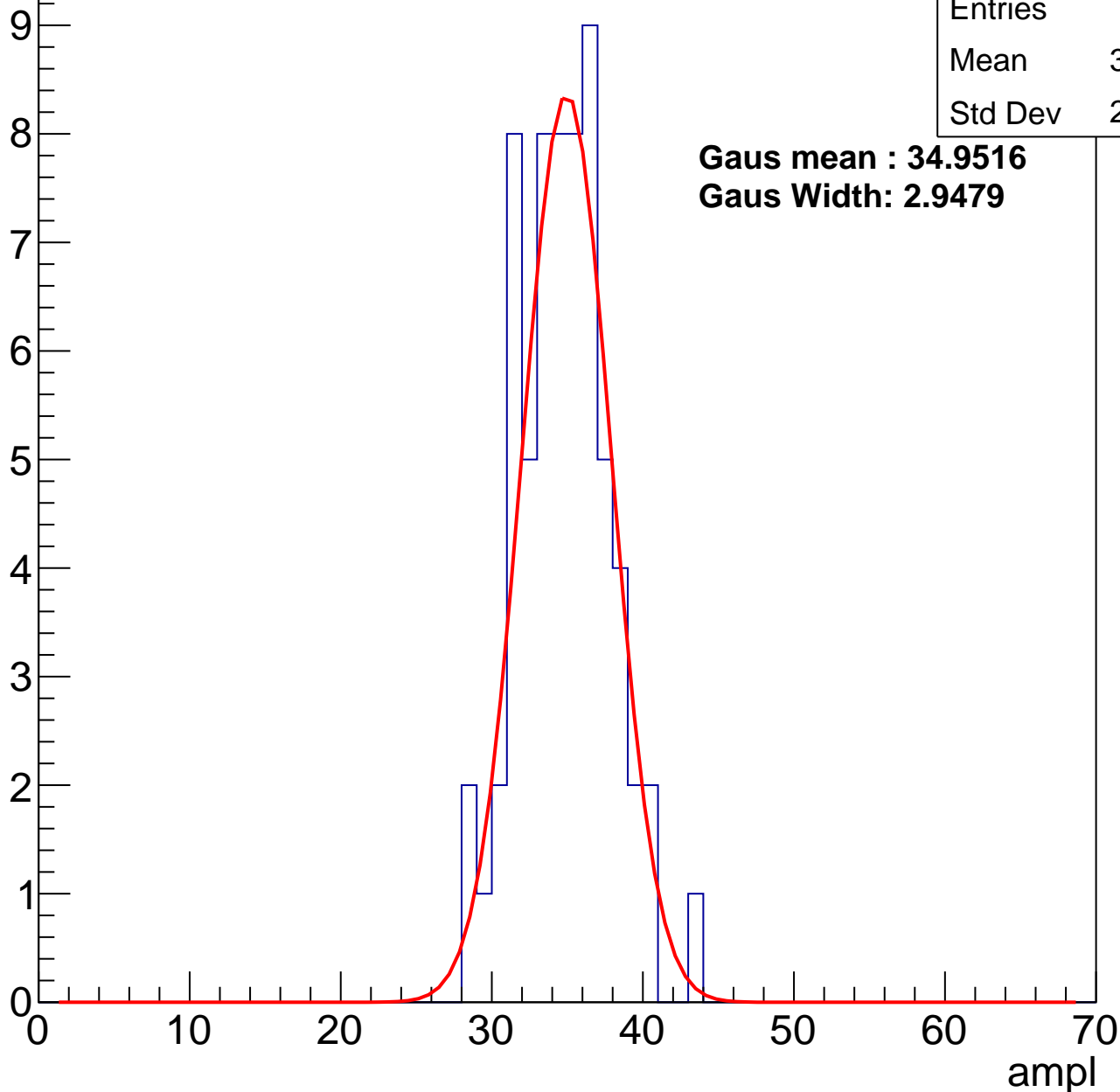
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	34.32
Std Dev	2.983

**Gaus mean : 34.9516**

**Gaus Width: 2.9479**



# B1L000S, U21-ch6, adc2

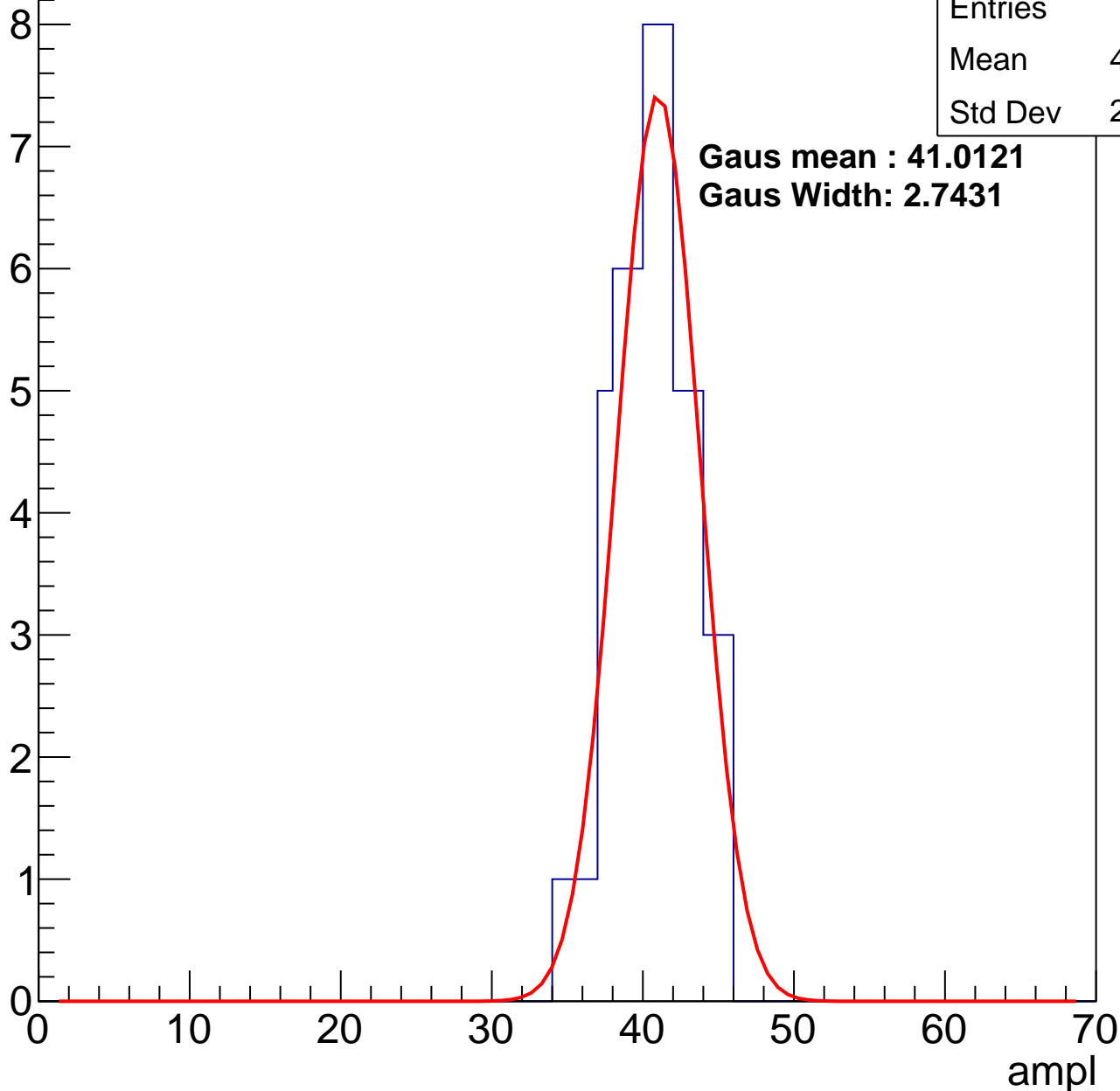
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	40.23
Std Dev	2.577

**Gaus mean : 41.0121**

**Gaus Width: 2.7431**



# B1L000S, U21-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	46.07
Std Dev	2.906

Entry

10

8

6

4

2

0

0

10

20

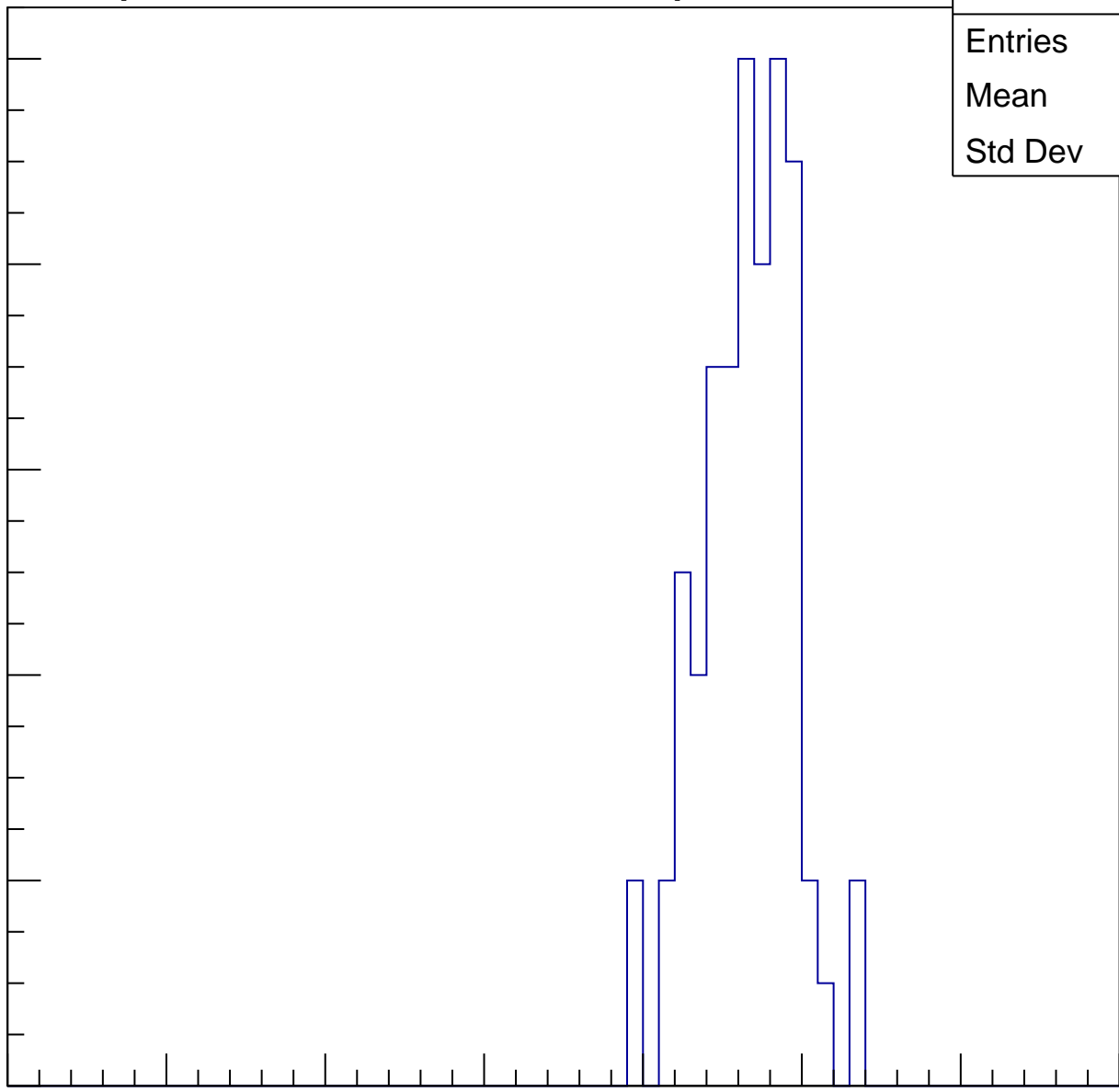
30

40

50

60

ampl

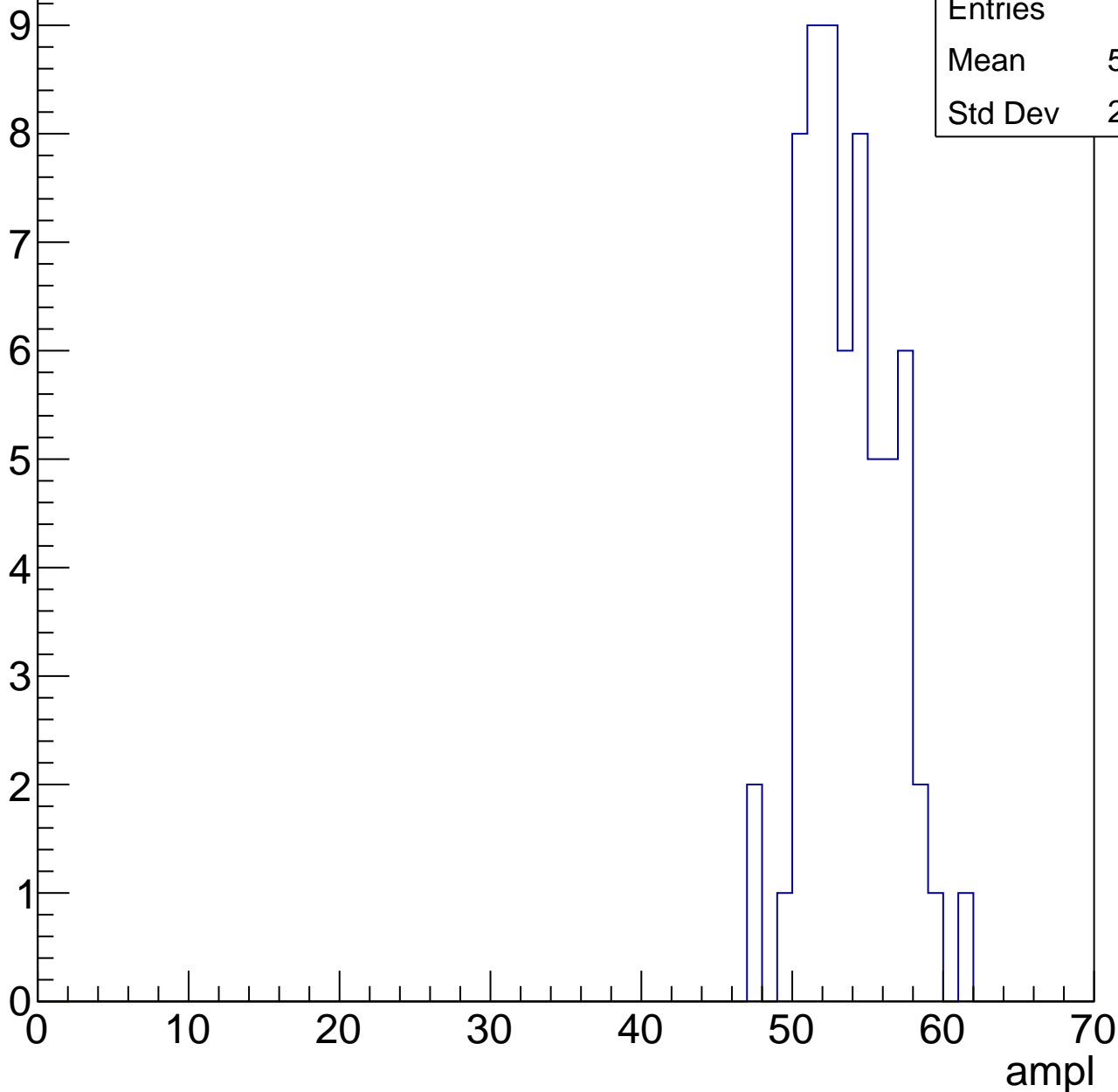


# B1L000S, U21-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

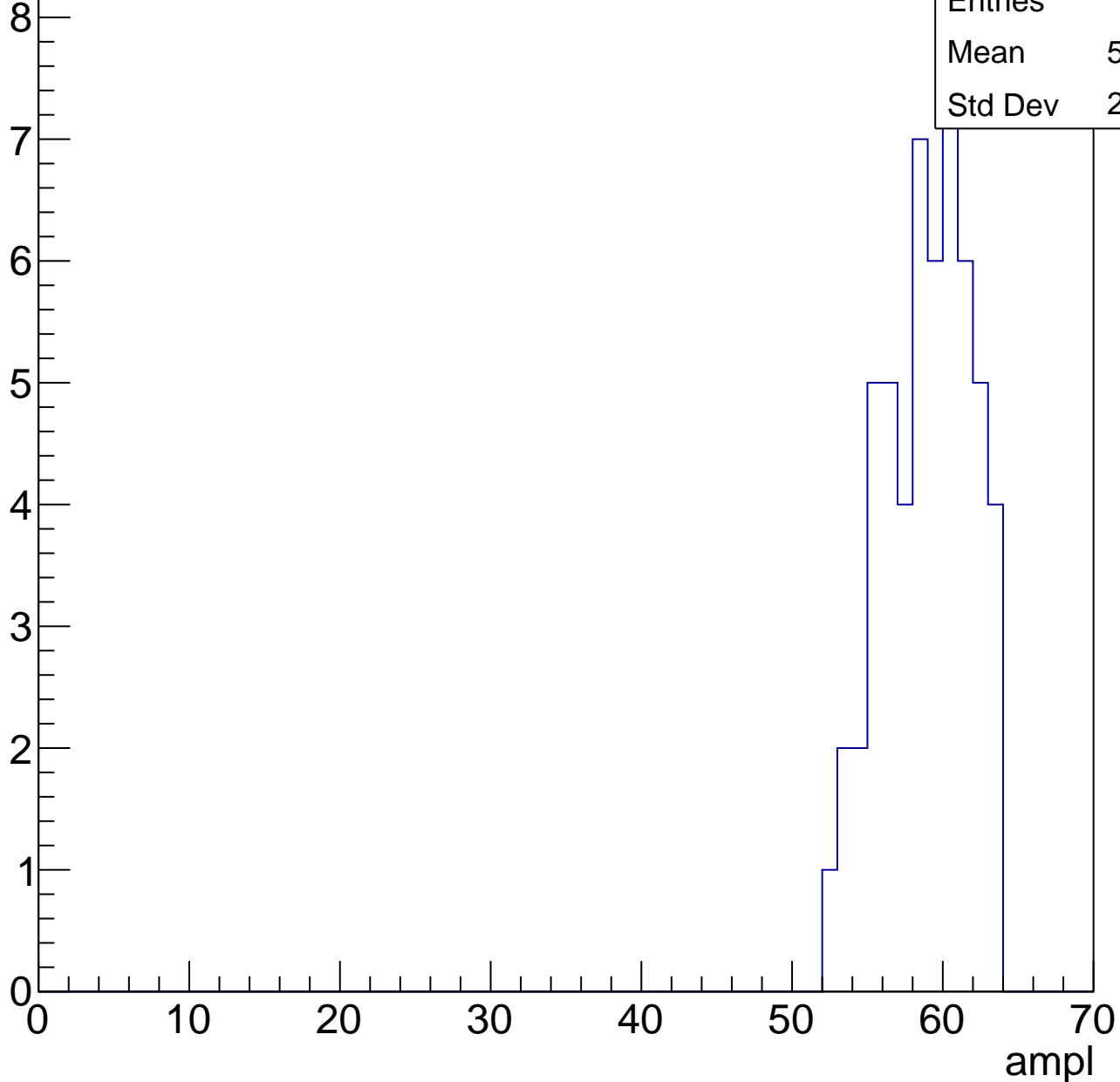
Entries	63
Mean	53.22
Std Dev	2.875



# B1L000S, U21-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



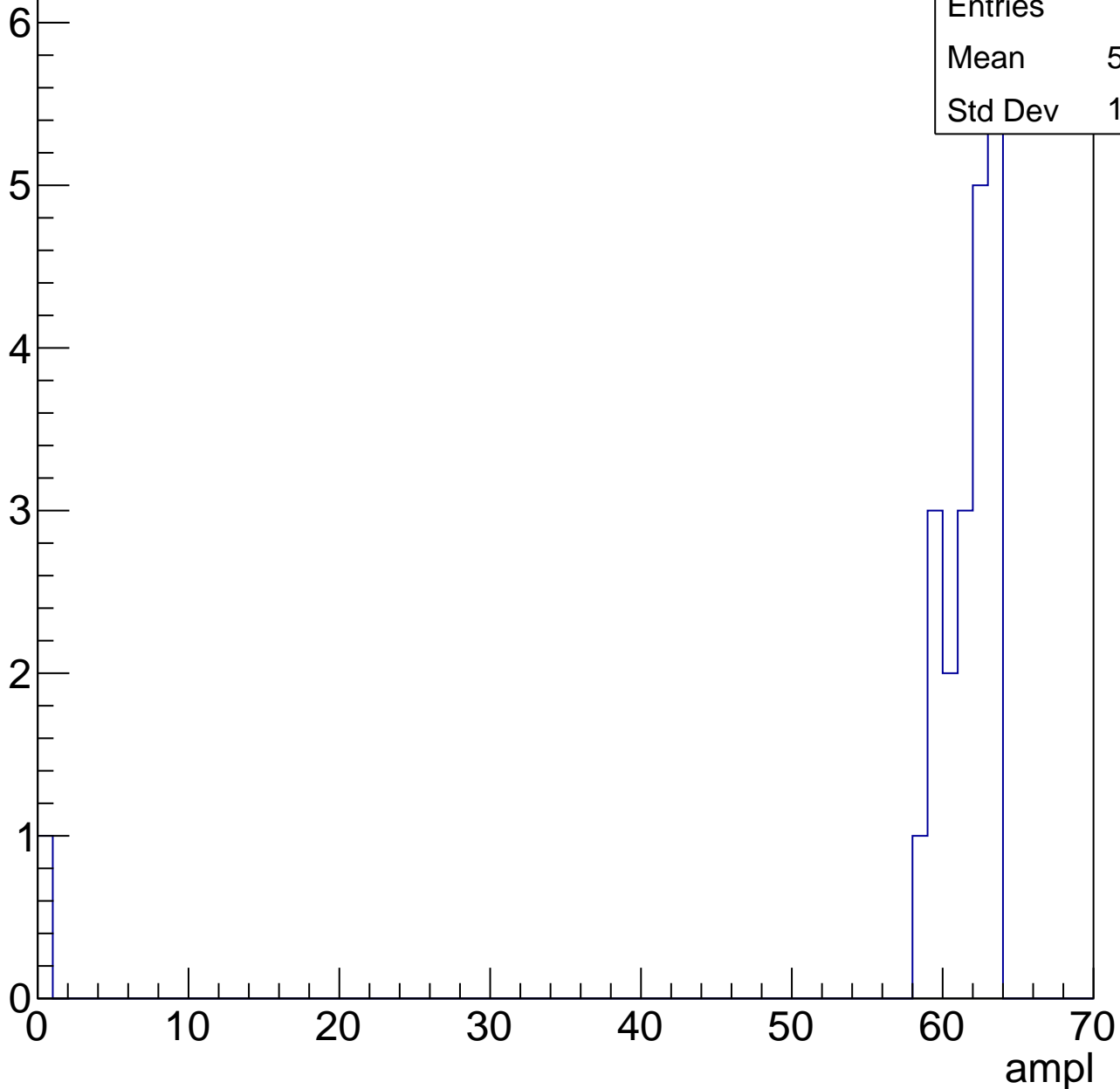
Entries	55
Mean	58.49
Std Dev	2.847

# B1L000S, U21-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.38
Std Dev	13.15





# B1L000S, U21-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch7, adc0

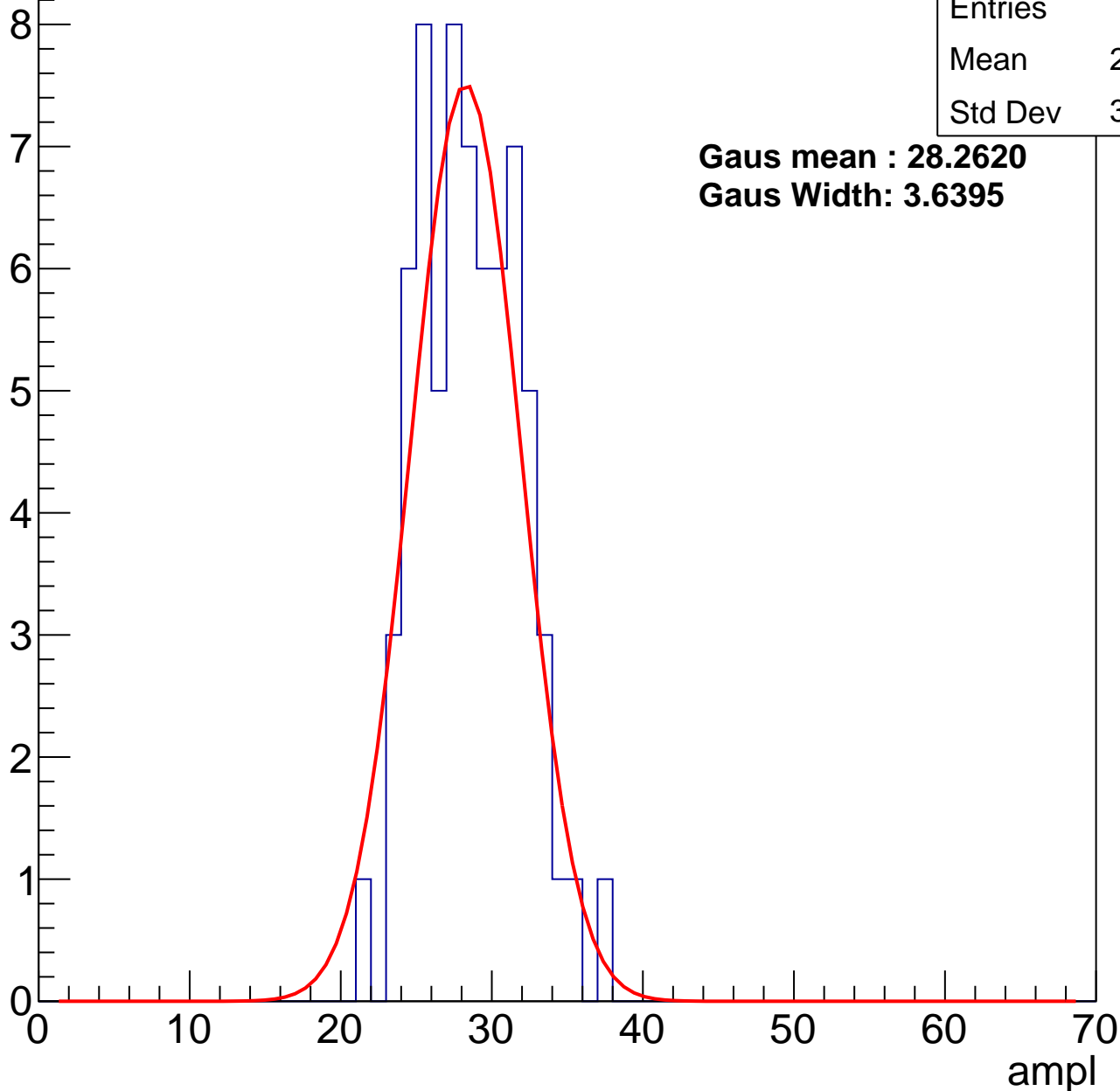
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.12
Std Dev	3.283

**Gaus mean : 28.2620**

**Gaus Width: 3.6395**



# B1L000S, U21-ch7, adc1

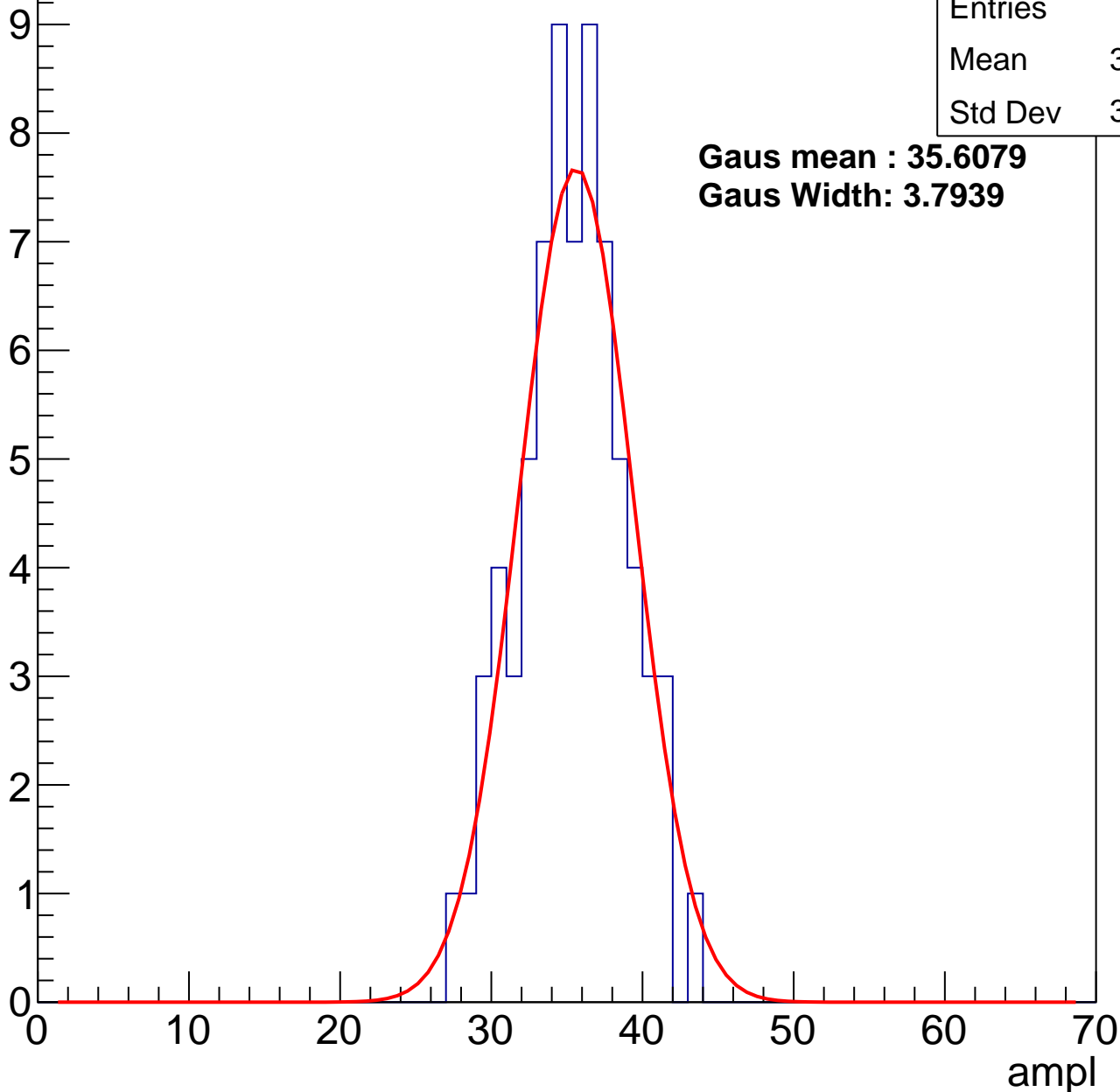
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	34.89
Std Dev	3.422

**Gaus mean : 35.6079**

**Gaus Width: 3.7939**



# B1L000S, U21-ch7, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.22
Std Dev	2.903

7

6

5

4

3

2

1

0

**Gaus mean : 41.7064**

**Gaus Width: 2.9374**

0

10

20

30

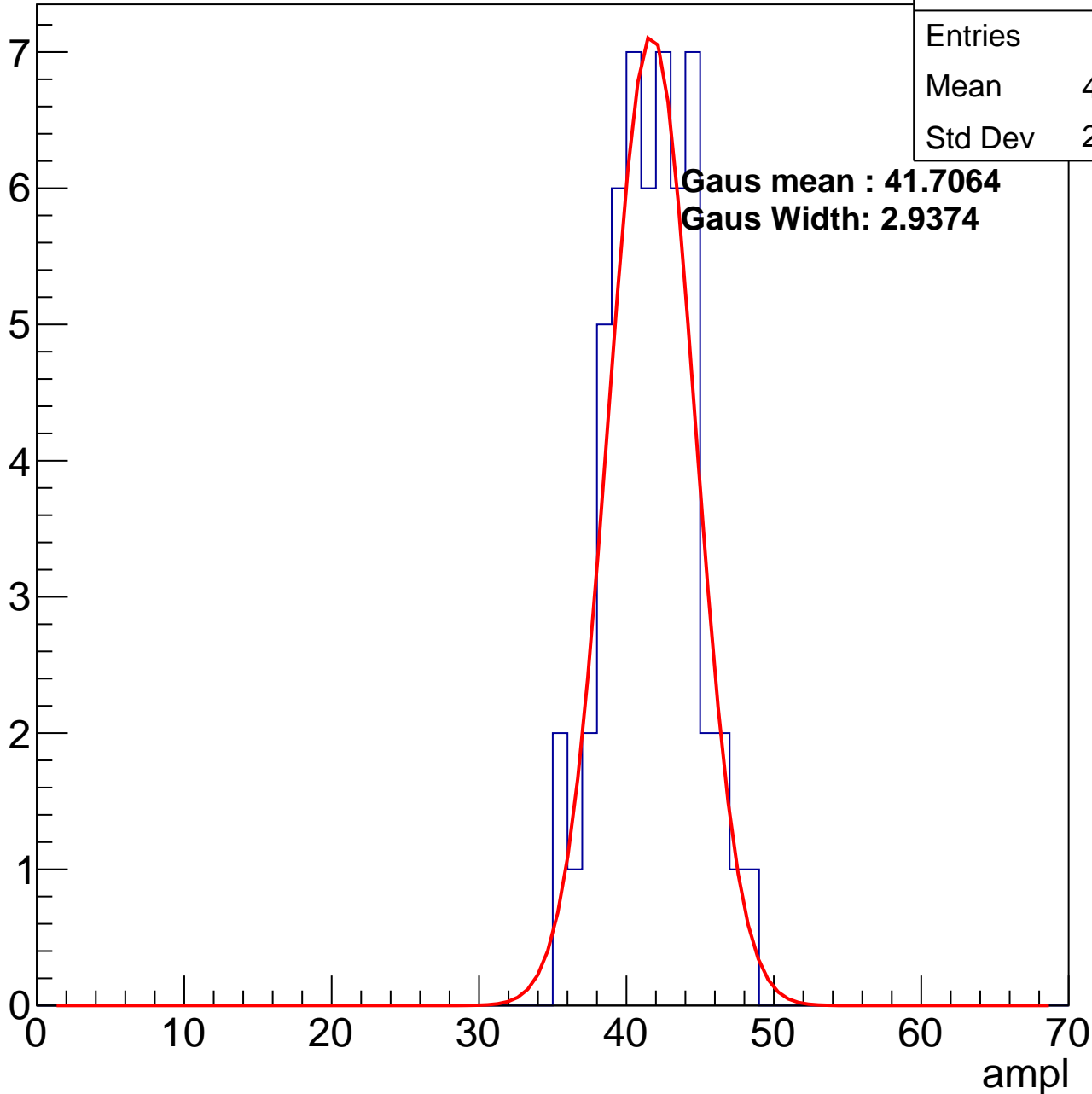
40

50

60

70

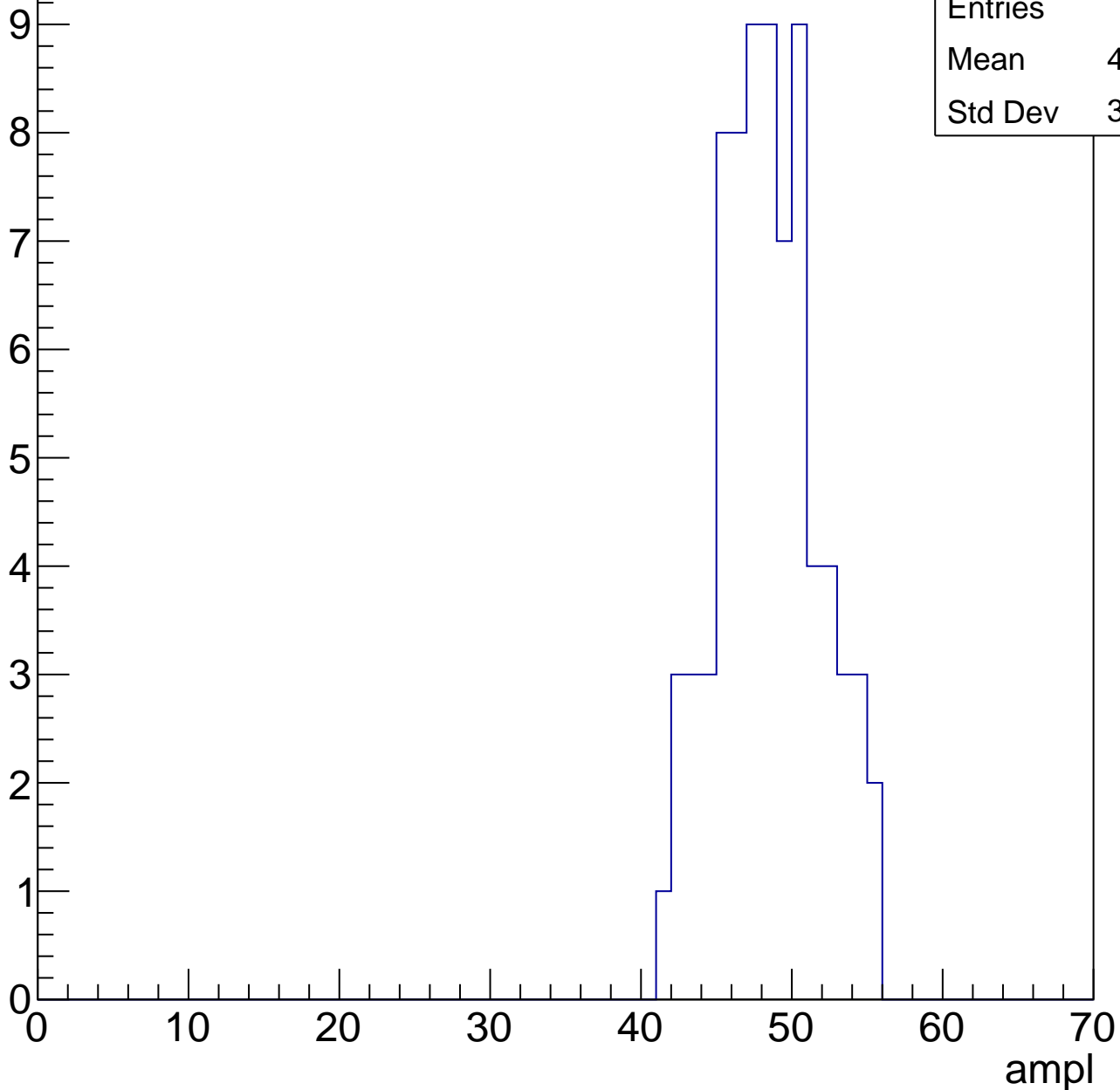
ampl



# B1L000S, U21-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

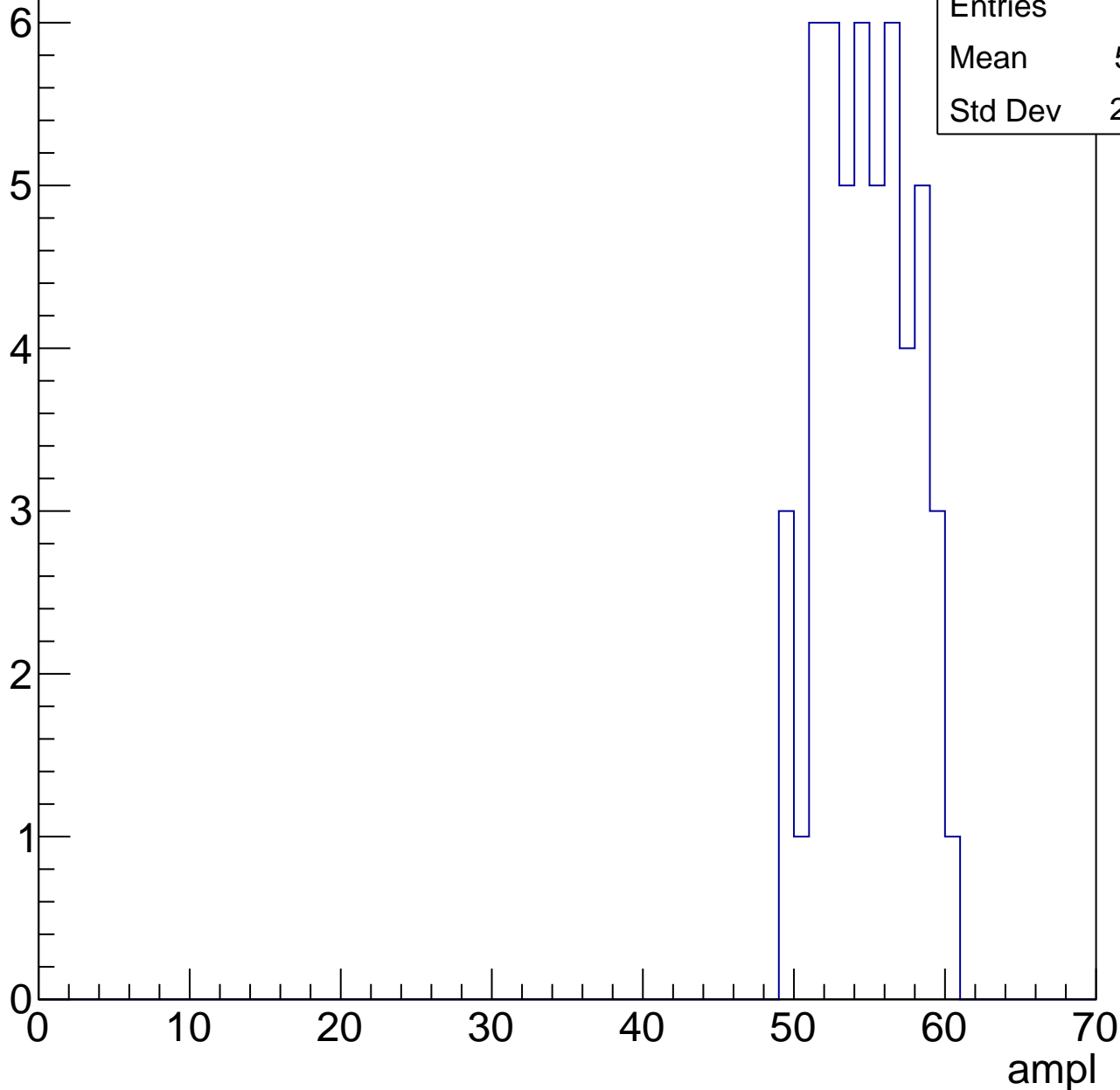
Entry



# B1L000S, U21-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



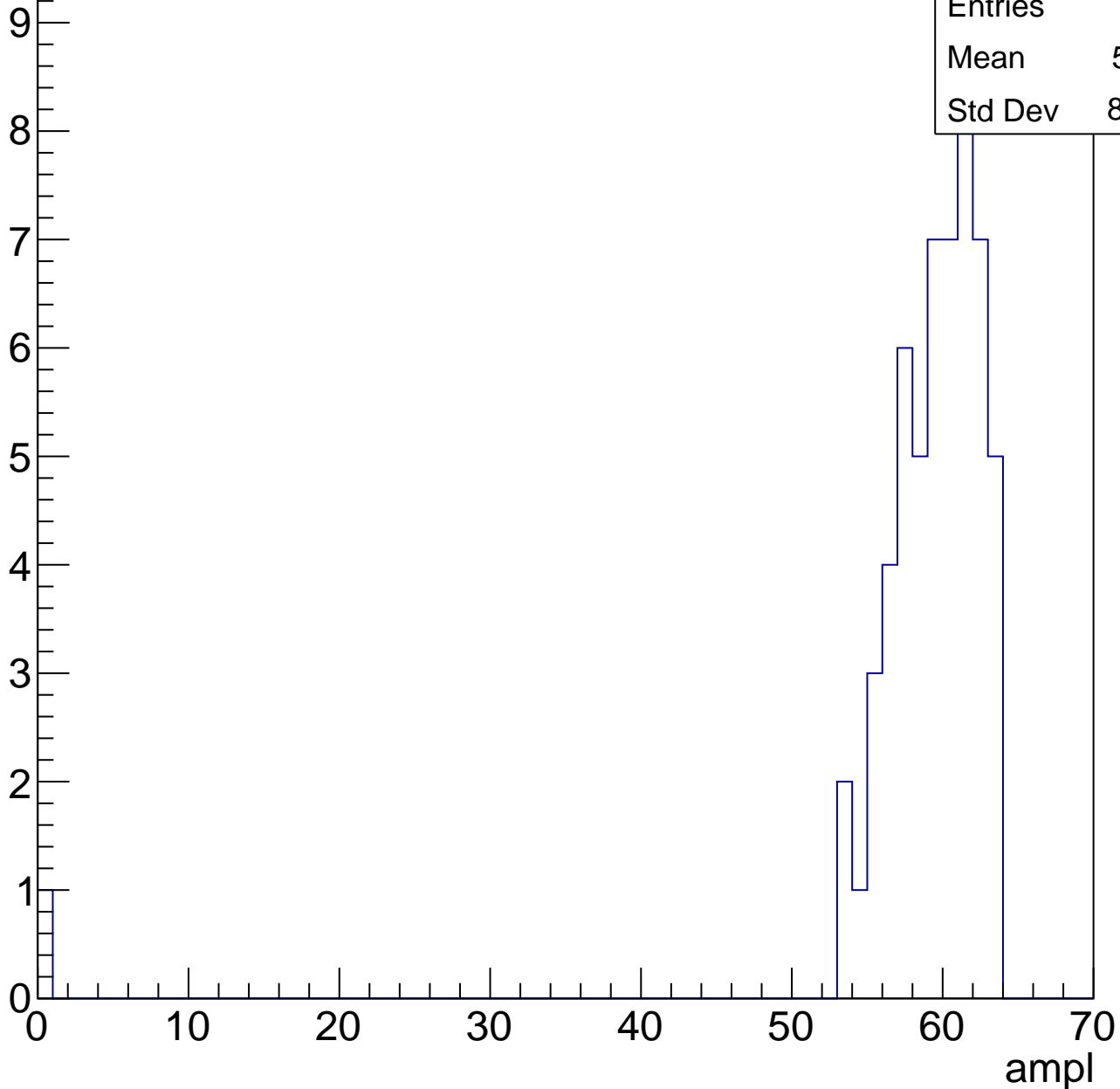
Entries	51
Mean	54.31
Std Dev	2.887

# B1L000S, U21-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	58.11
Std Dev	8.199

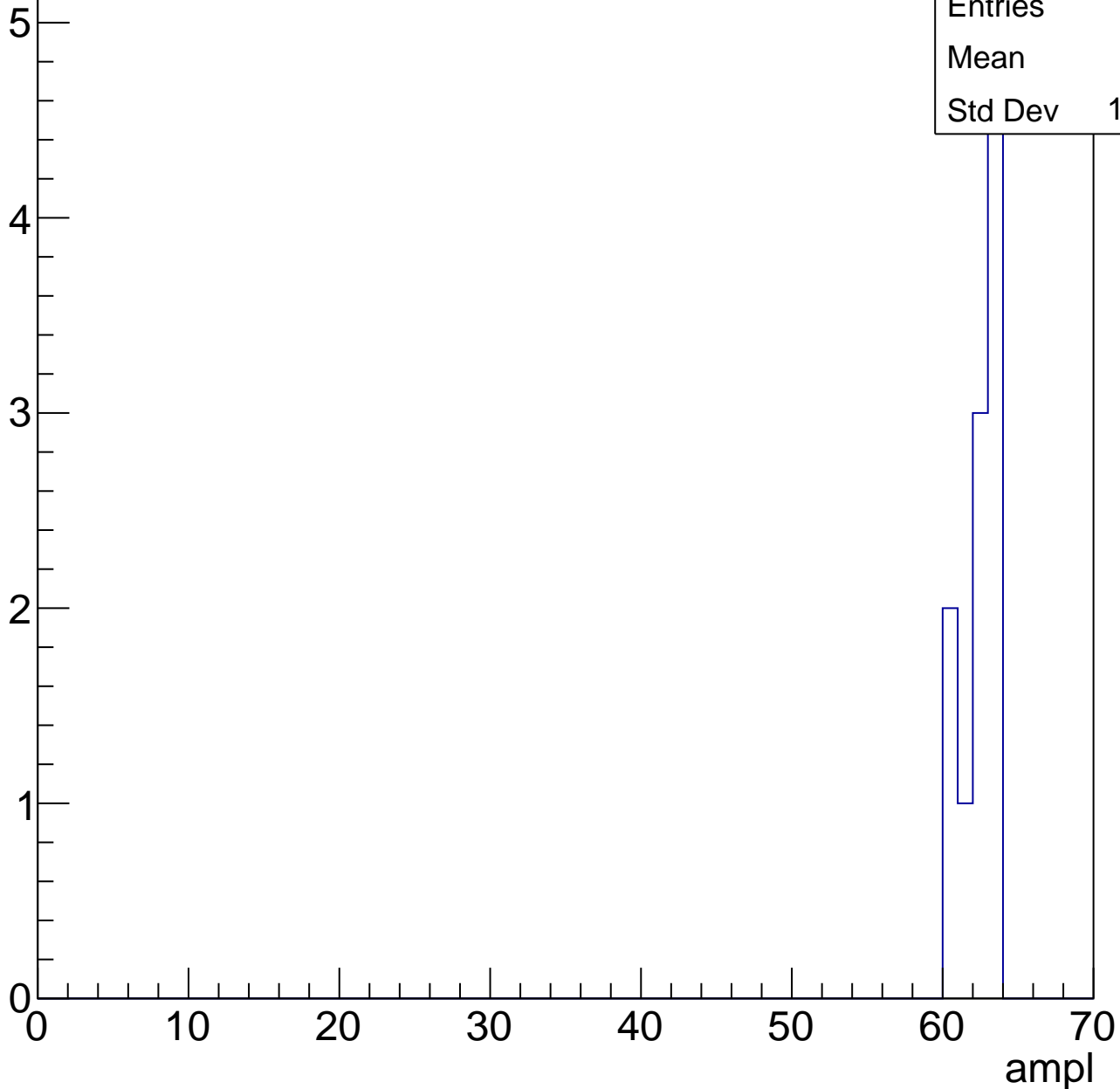


# B1L000S, U21-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	62
Std Dev	1.128





# B1L000S, U21-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L000S, U21-ch8, adc0

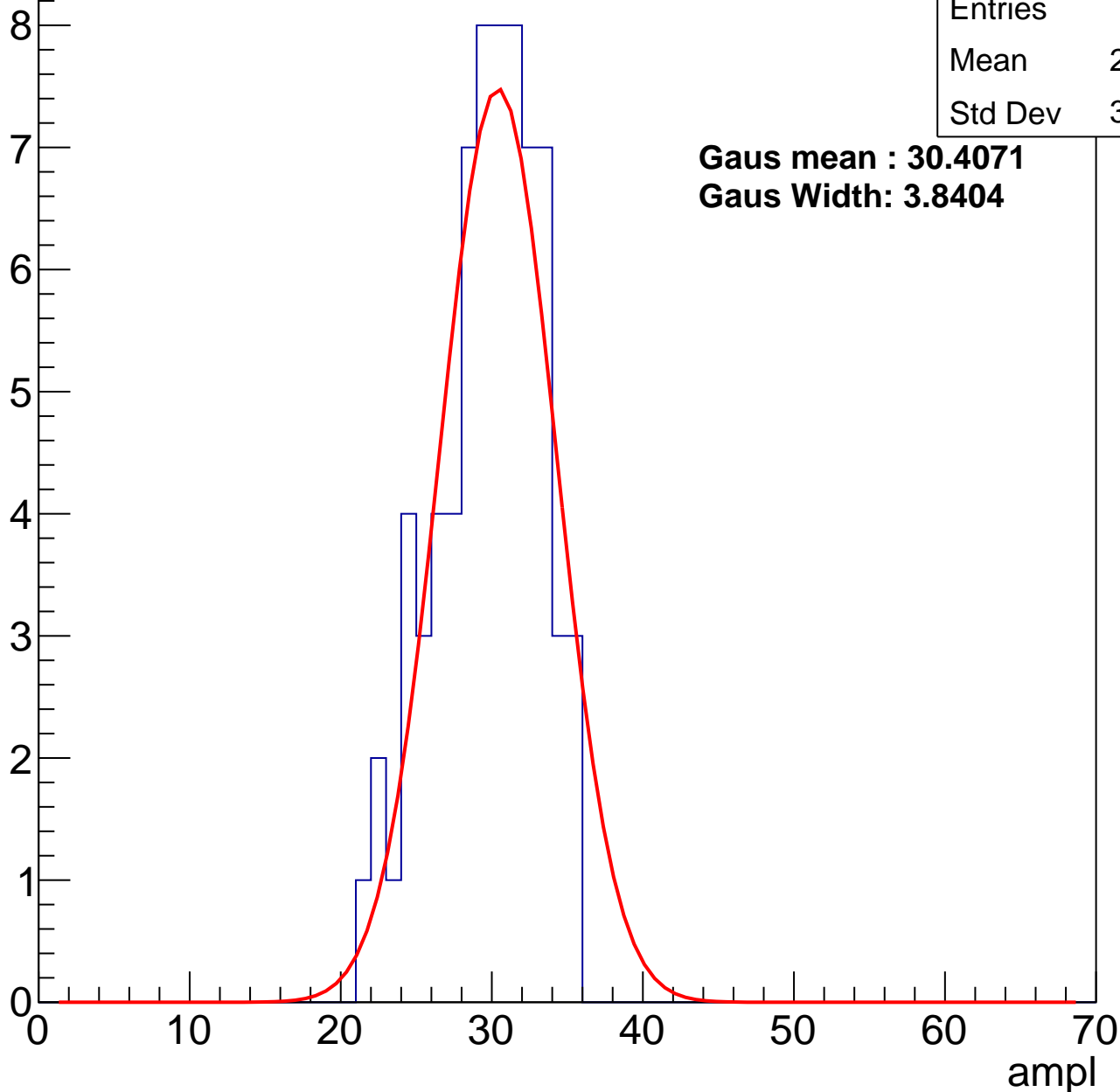
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	29.27
Std Dev	3.376

**Gaus mean : 30.4071**

**Gaus Width: 3.8404**



# B1L000S, U21-ch8, adc1

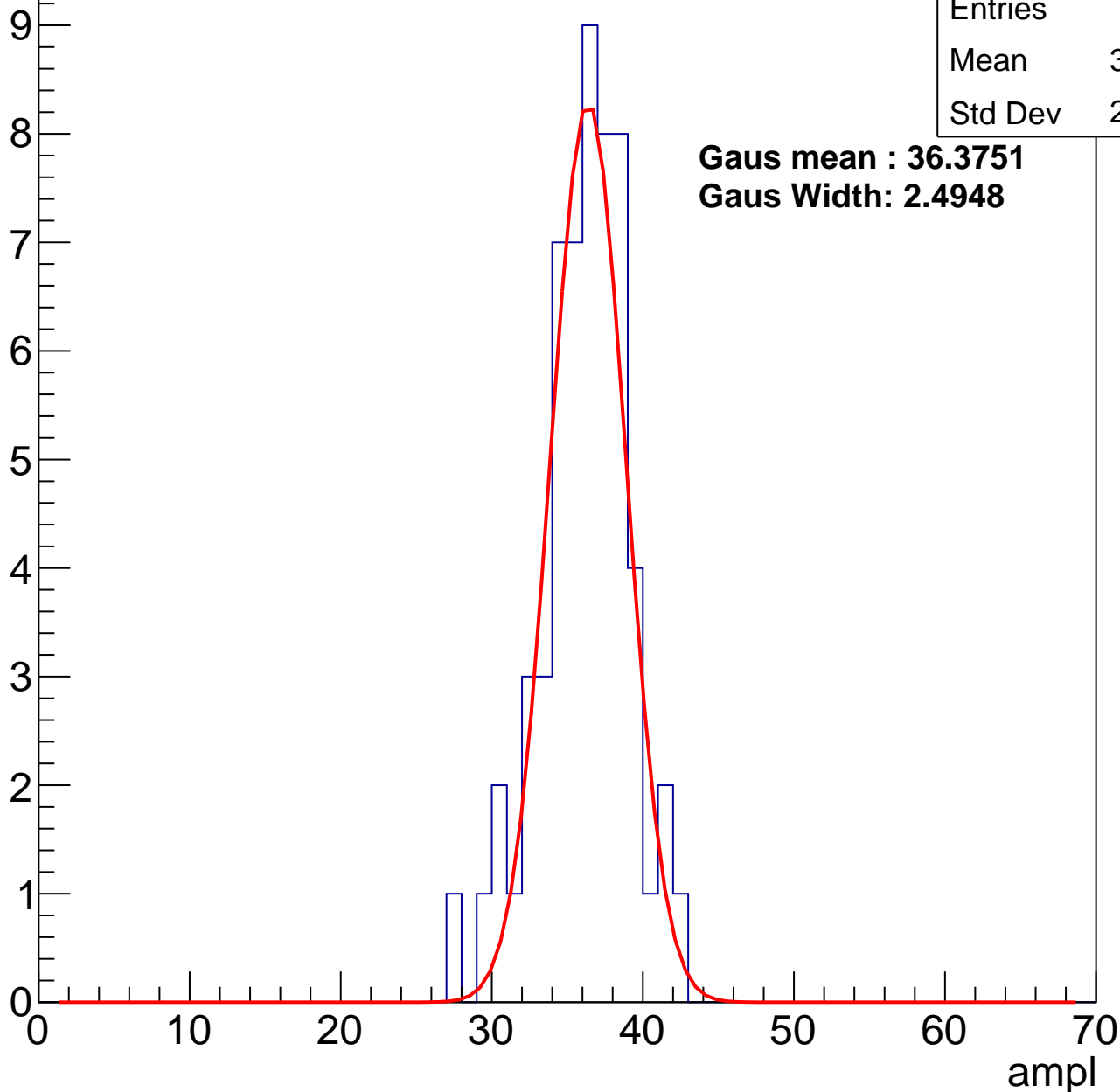
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.67
Std Dev	2.962

**Gaus mean : 36.3751**

**Gaus Width: 2.4948**



# B1L000S, U21-ch8, adc2

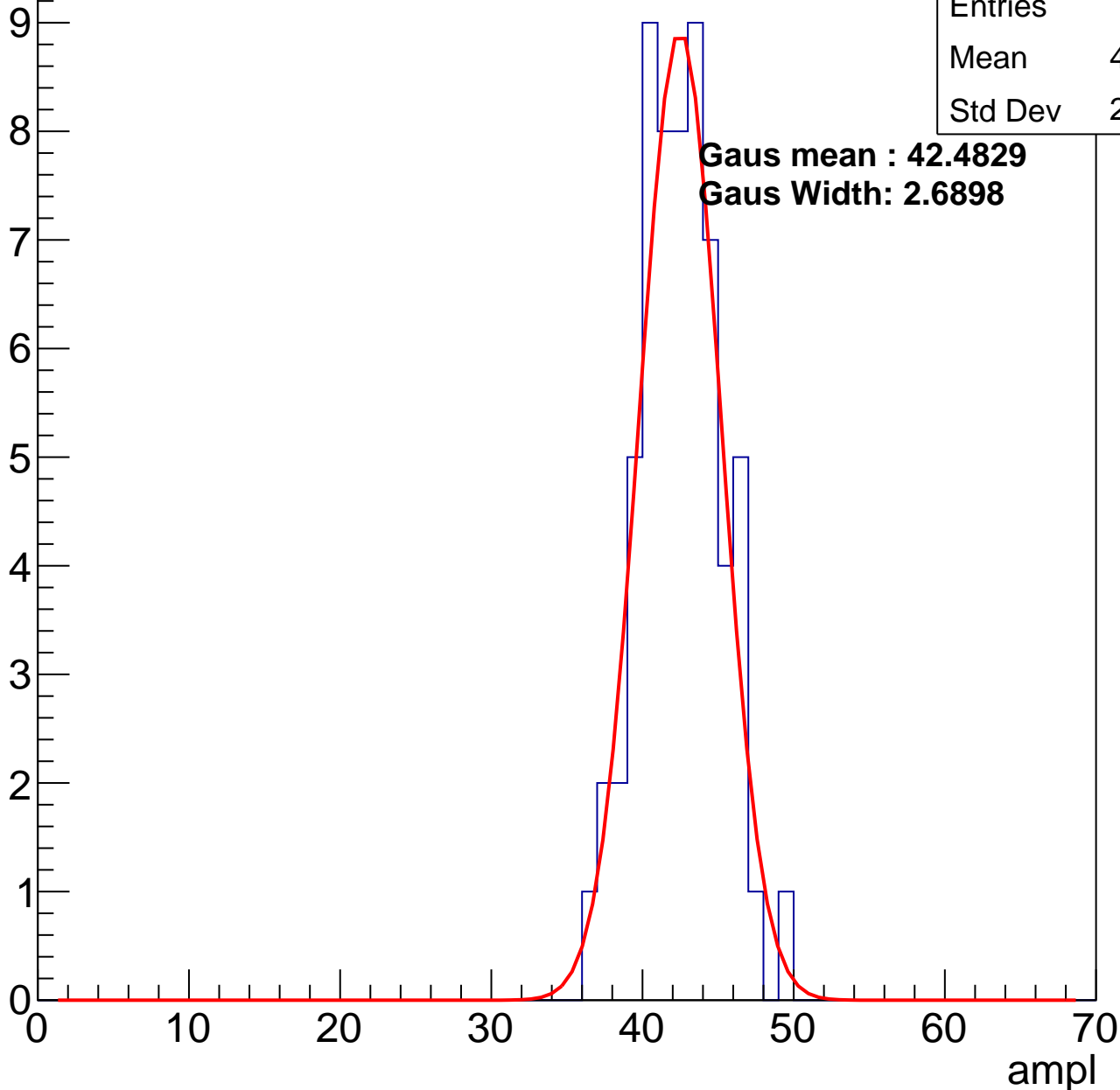
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.03
Std Dev	2.646

**Gaus mean : 42.4829**

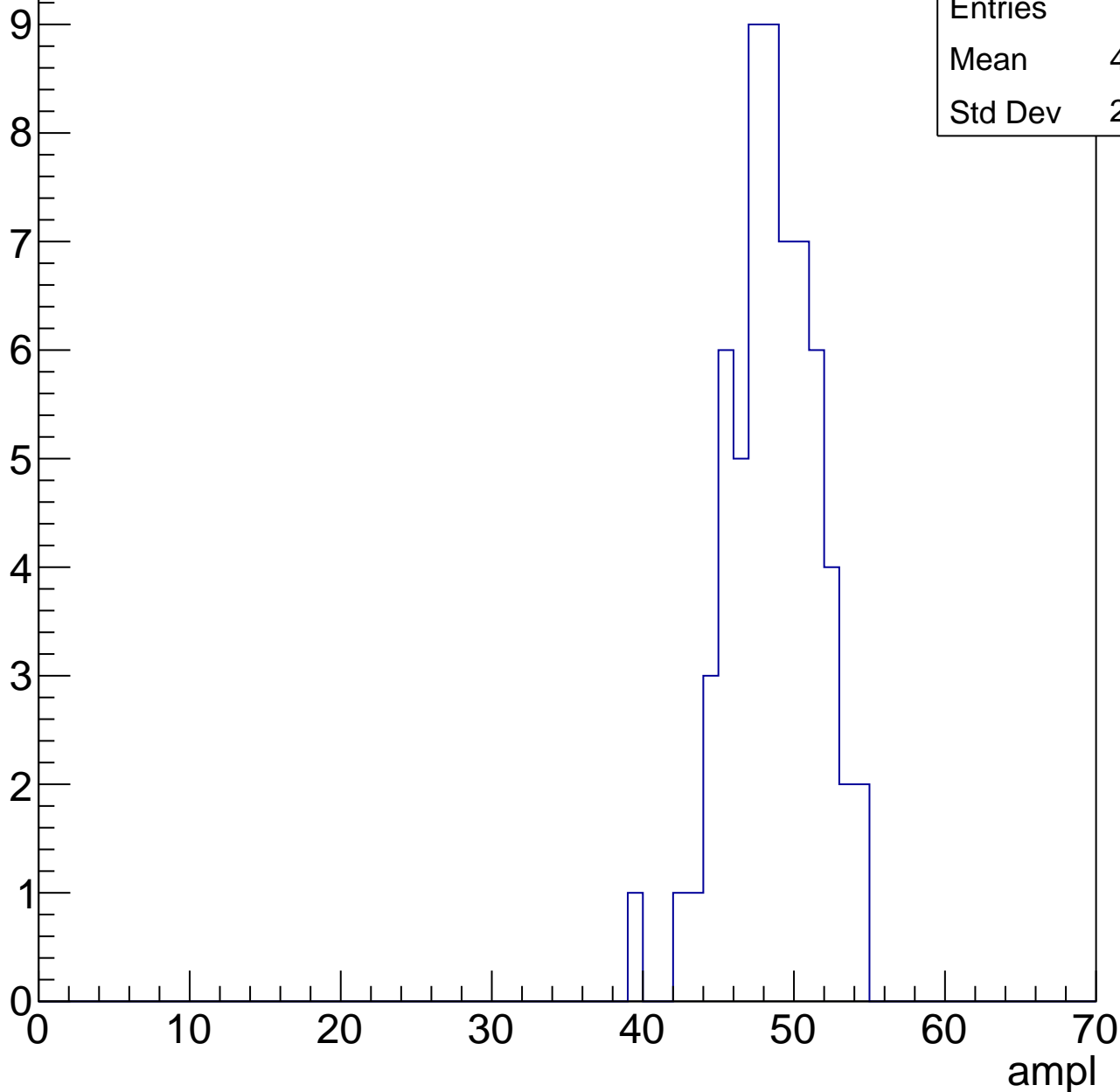
**Gaus Width: 2.6898**



# B1L000S, U21-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

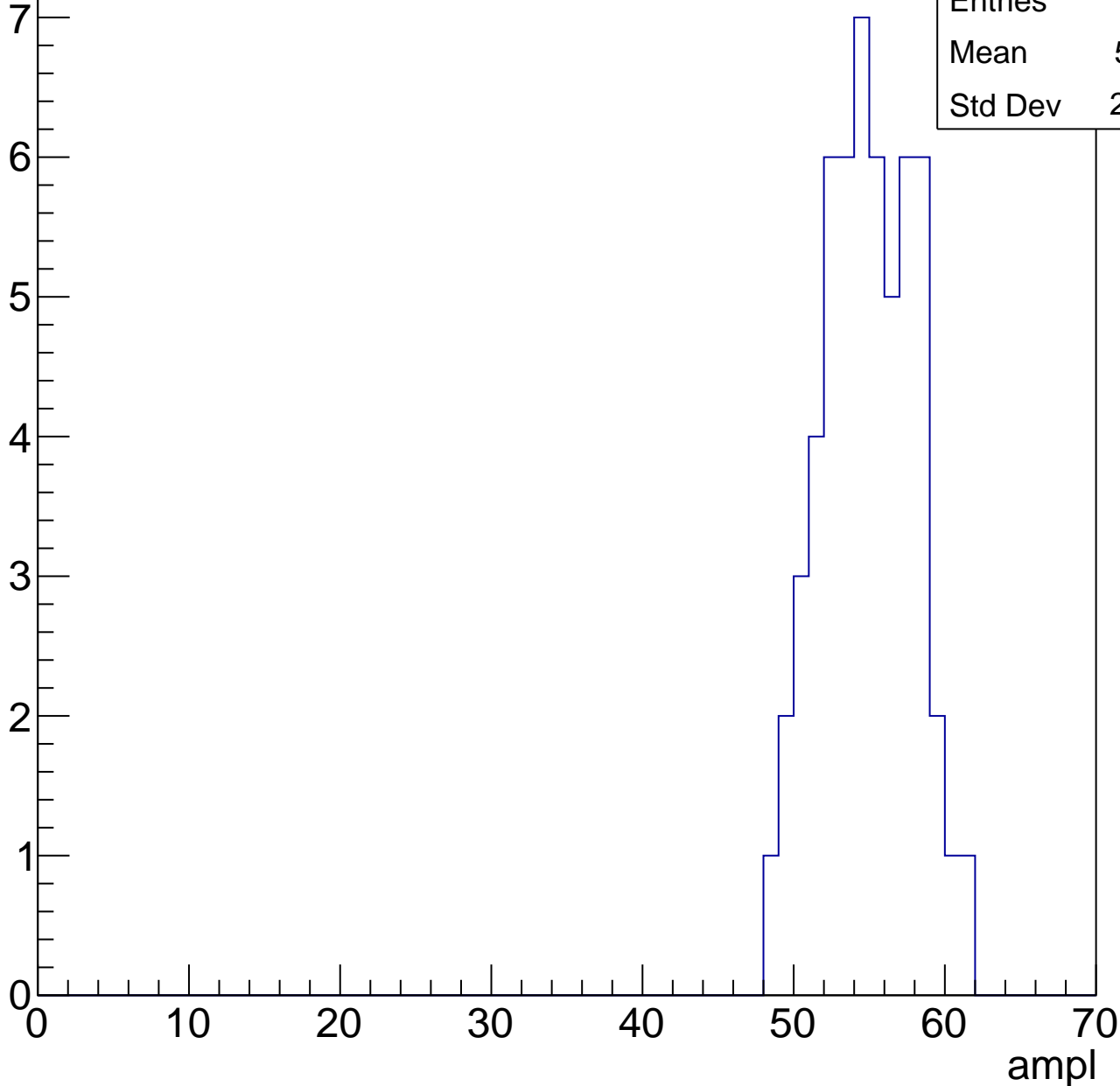


# B1L000S, U21-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	54.41
Std Dev	2.993

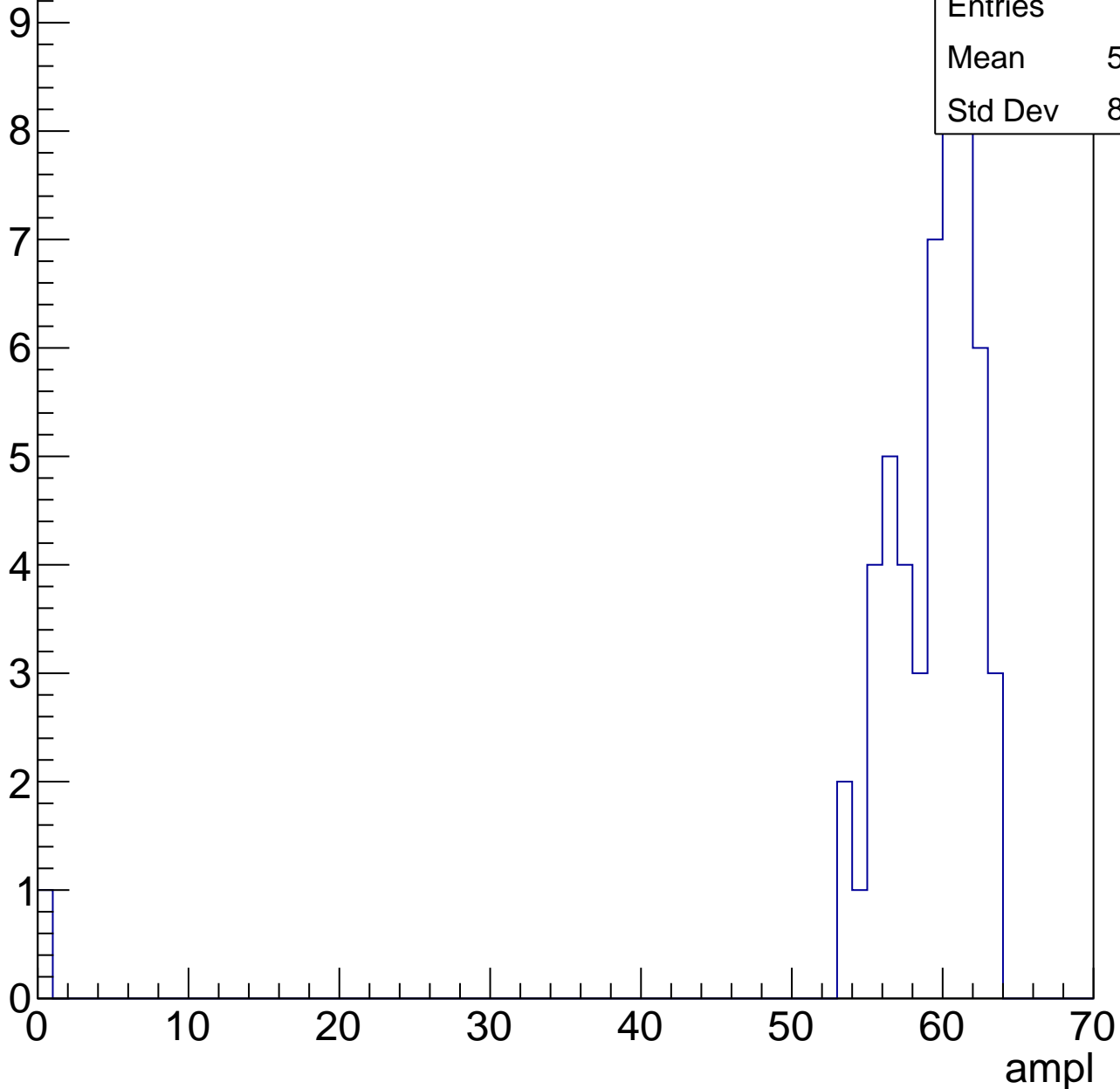


# B1L000S, U21-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	57.83
Std Dev	8.445

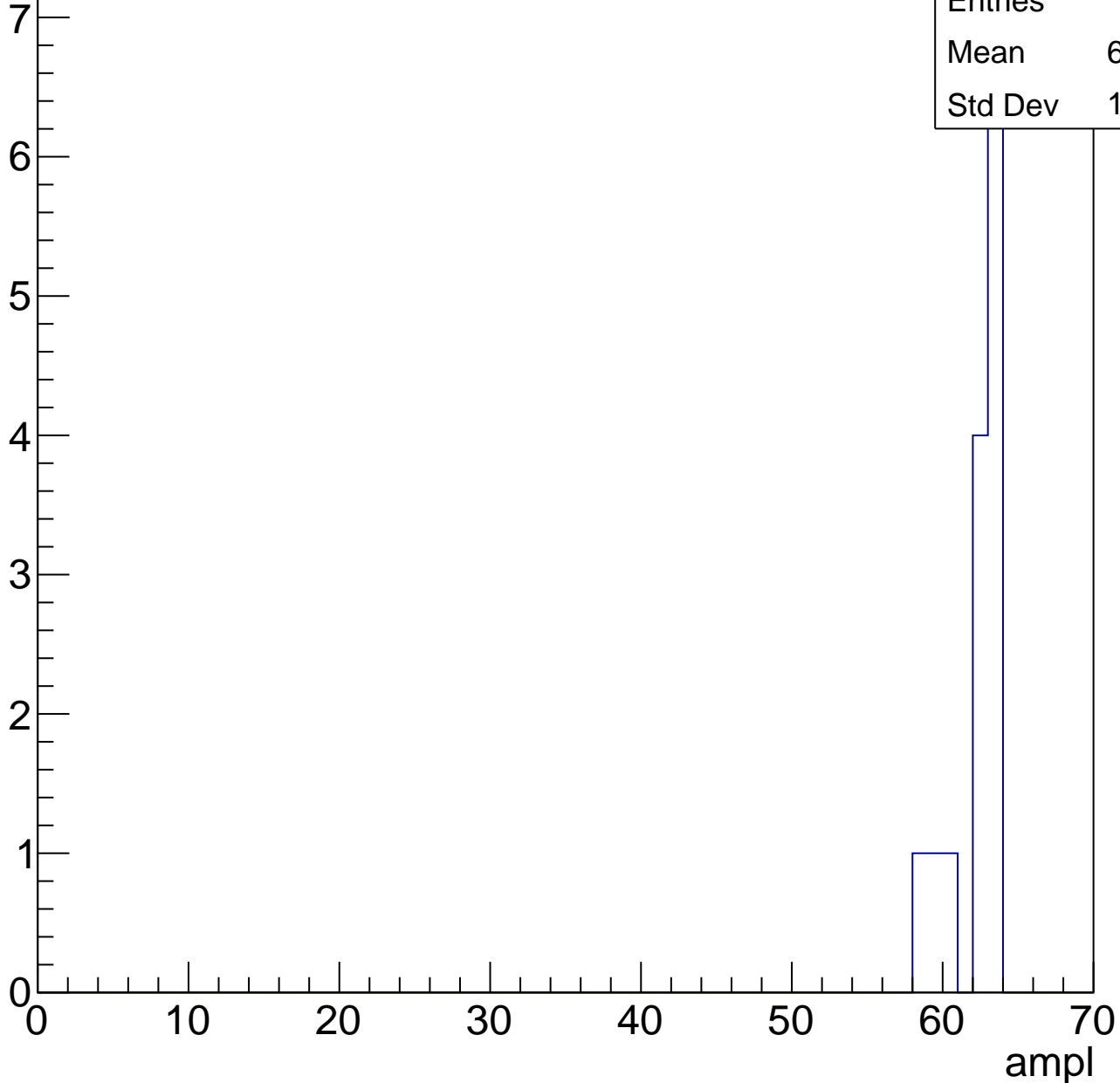


# B1L000S, U21-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.86
Std Dev	1.597





# B1L000S, U21-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch9, adc0

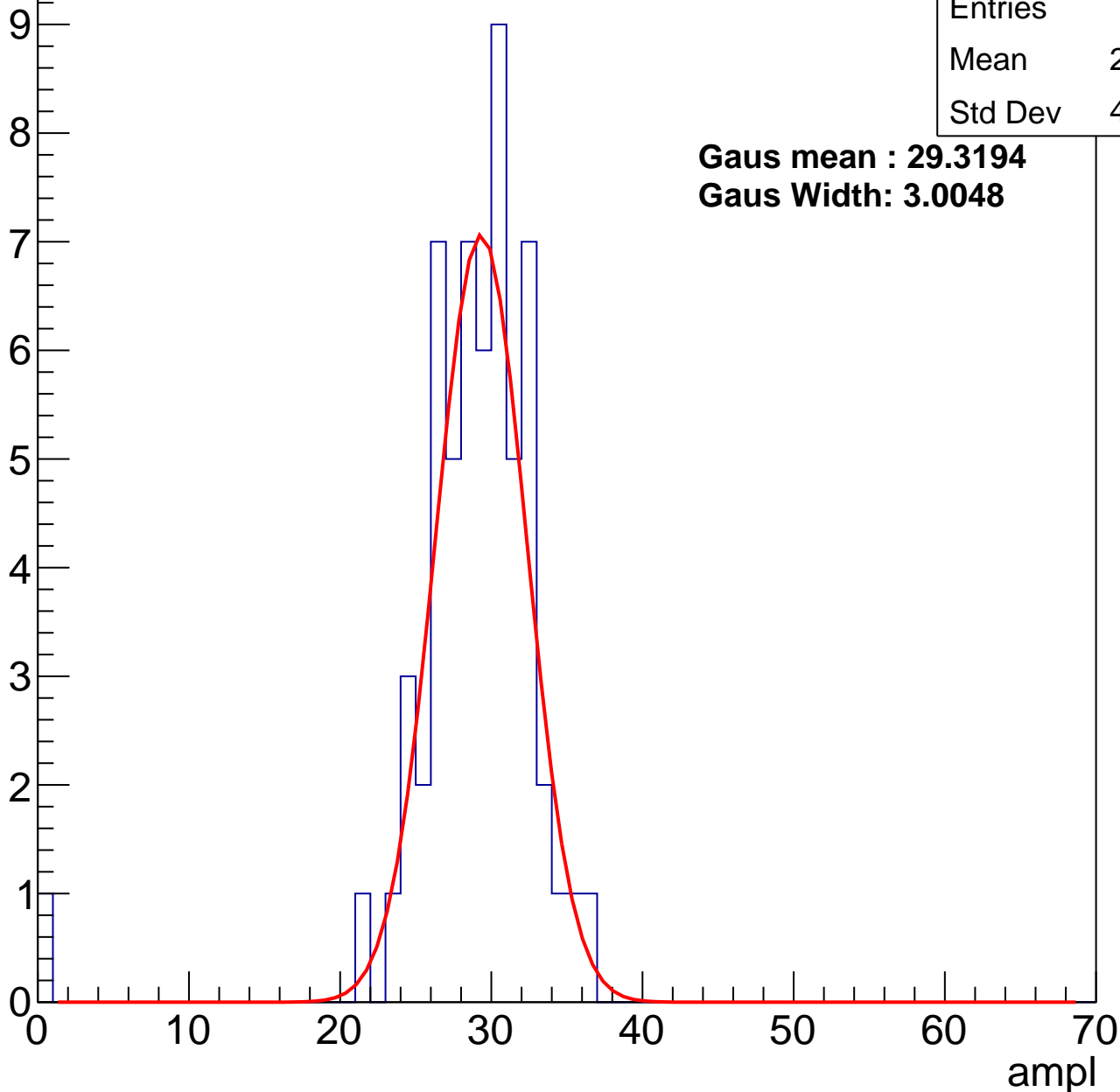
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	28.36
Std Dev	4.779

**Gaus mean : 29.3194**

**Gaus Width: 3.0048**



# B1L000S, U21-ch9, adc1

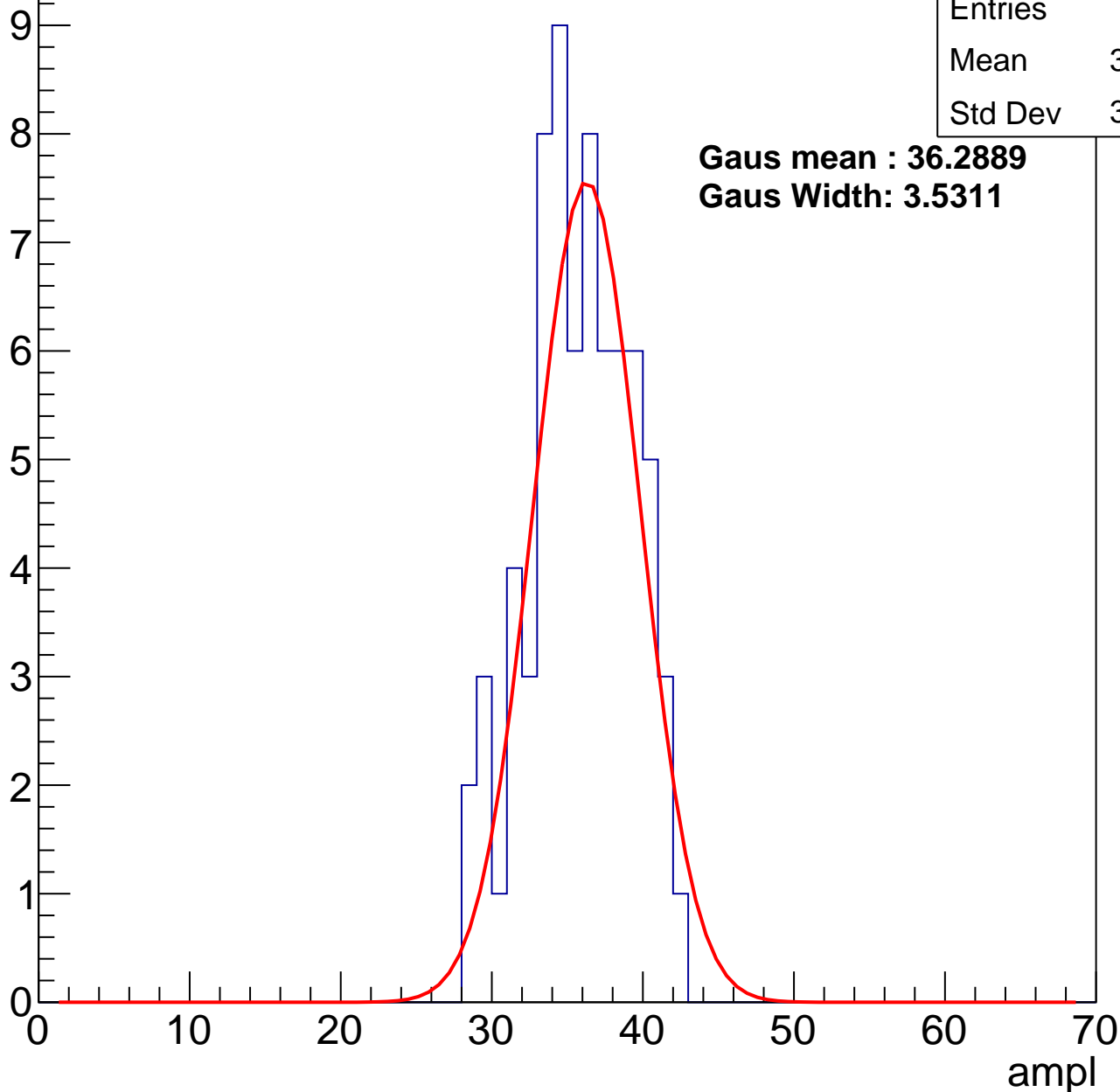
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.35
Std Dev	3.395

**Gaus mean : 36.2889**

**Gaus Width: 3.5311**



# B1L000S, U21-ch9, adc2

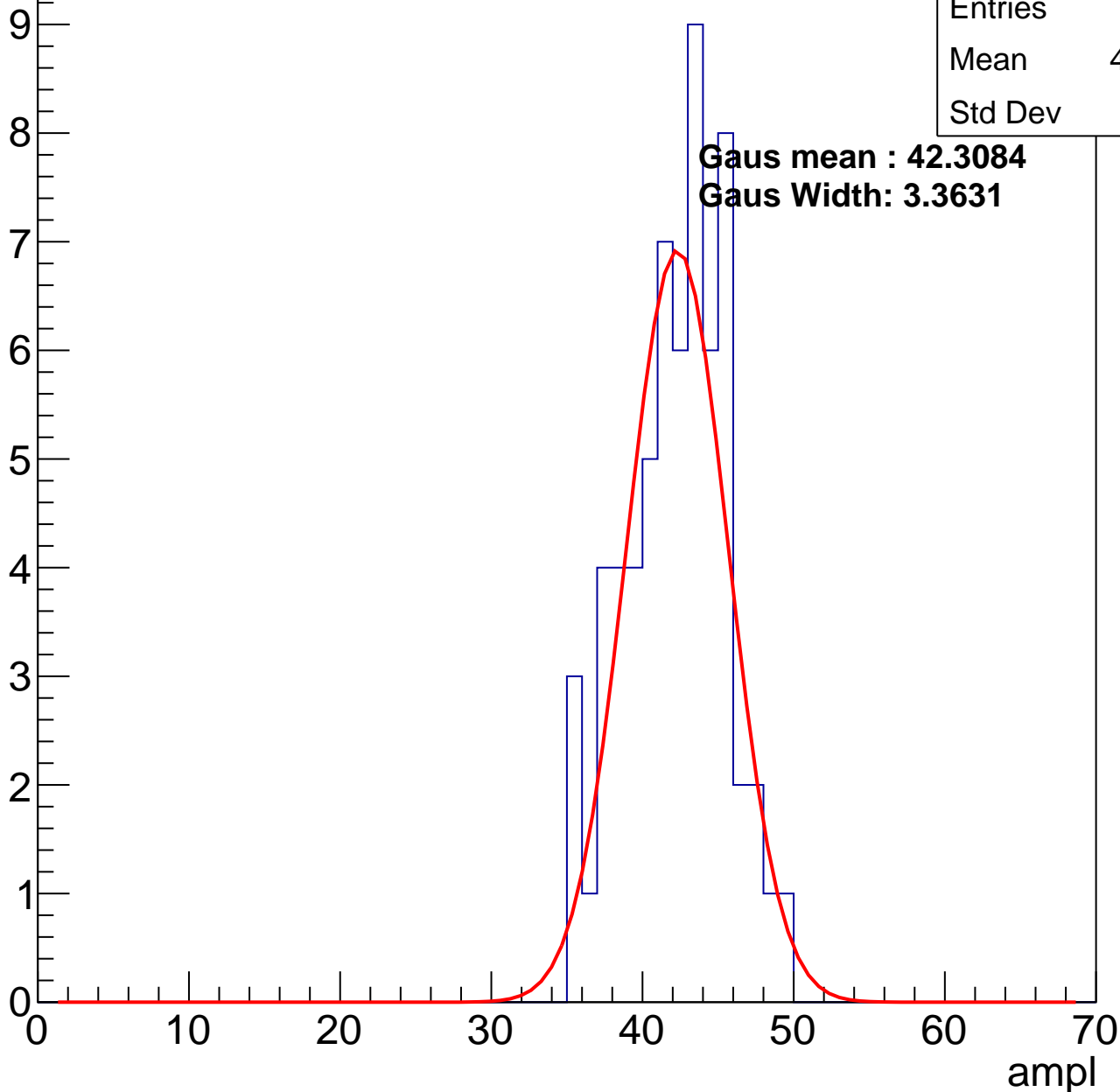
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.75
Std Dev	3.28

**Gaus mean : 42.3084**

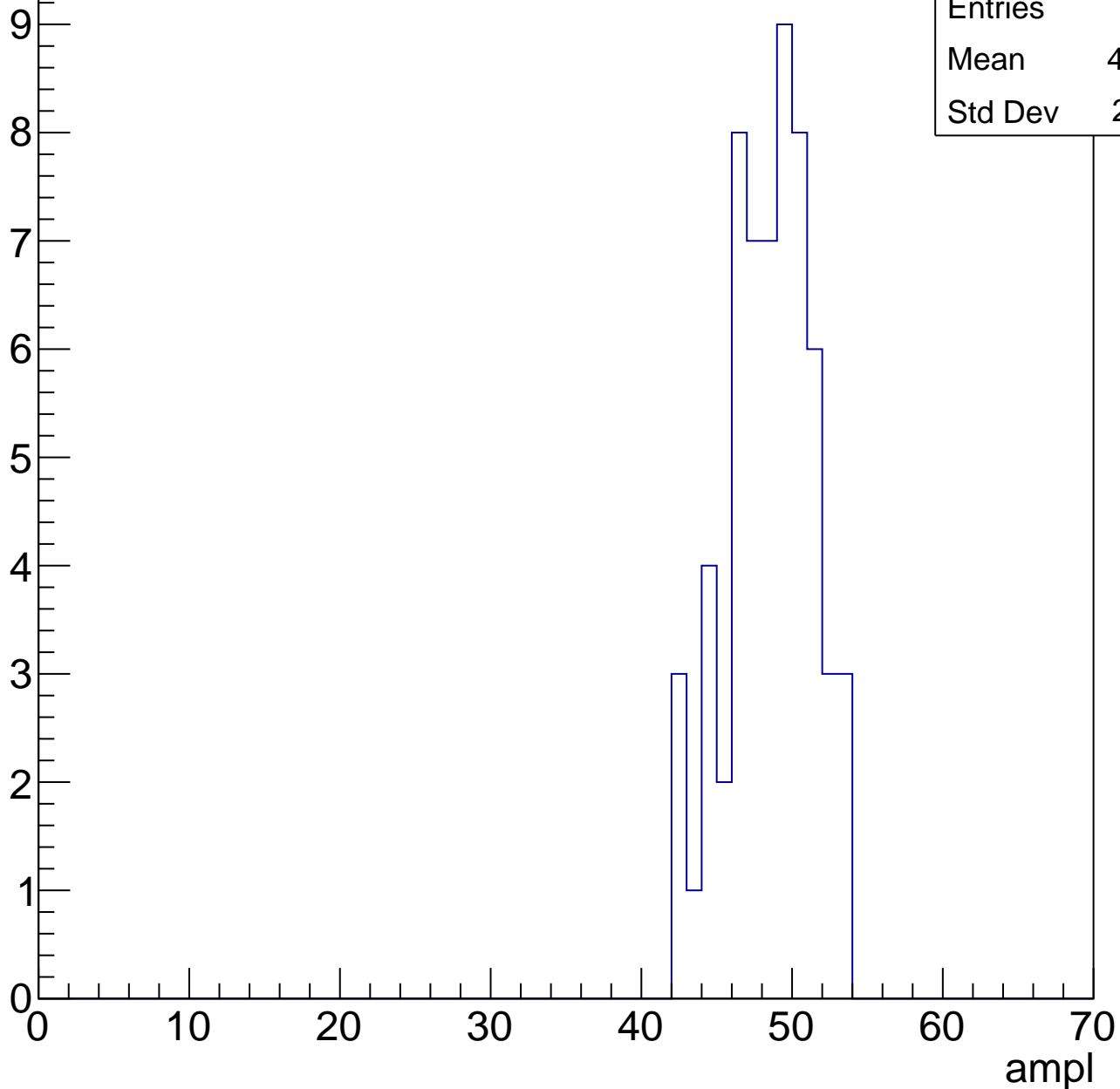
**Gaus Width: 3.3631**



# B1L000S, U21-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

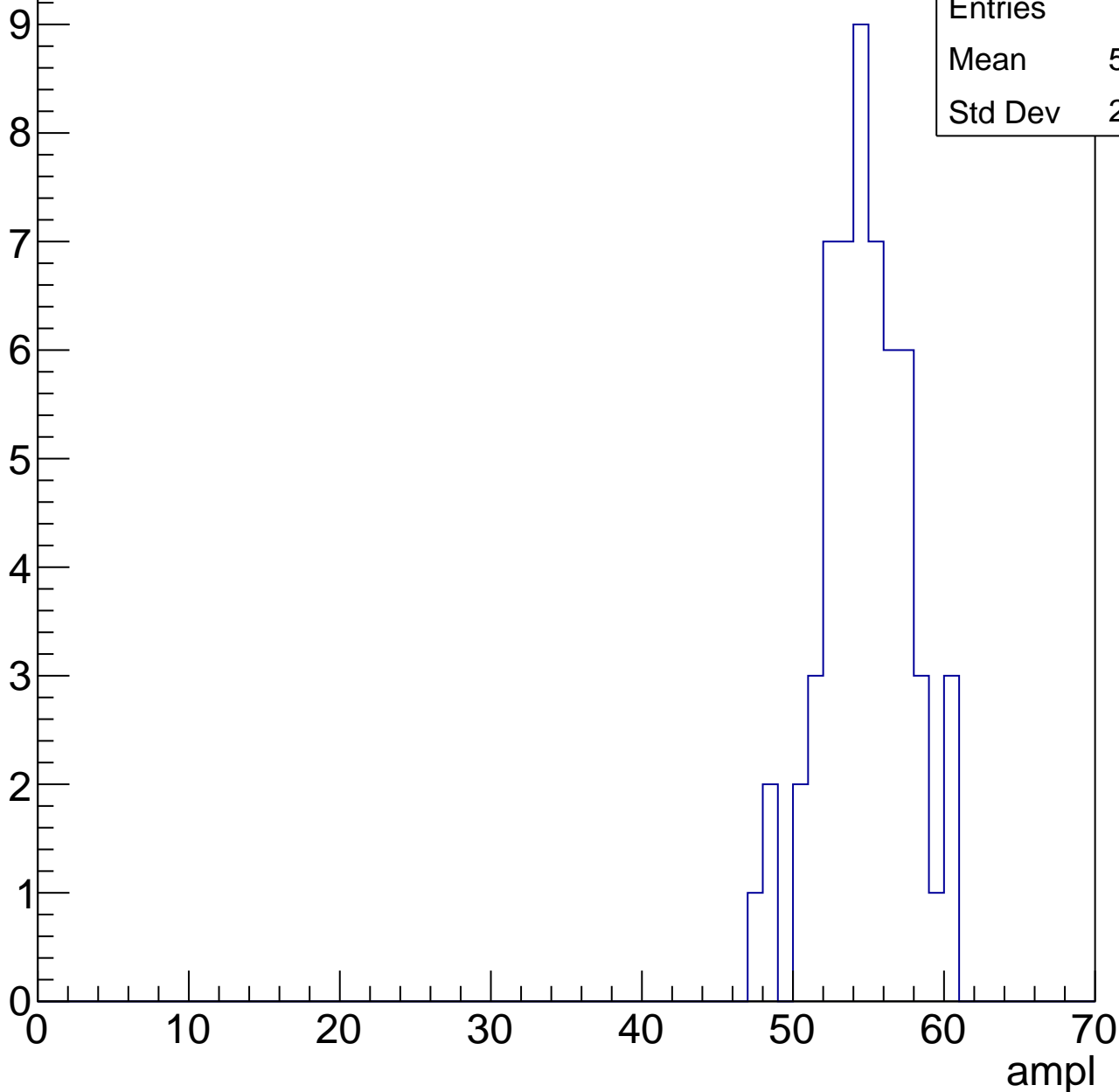


# B1L000S, U21-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

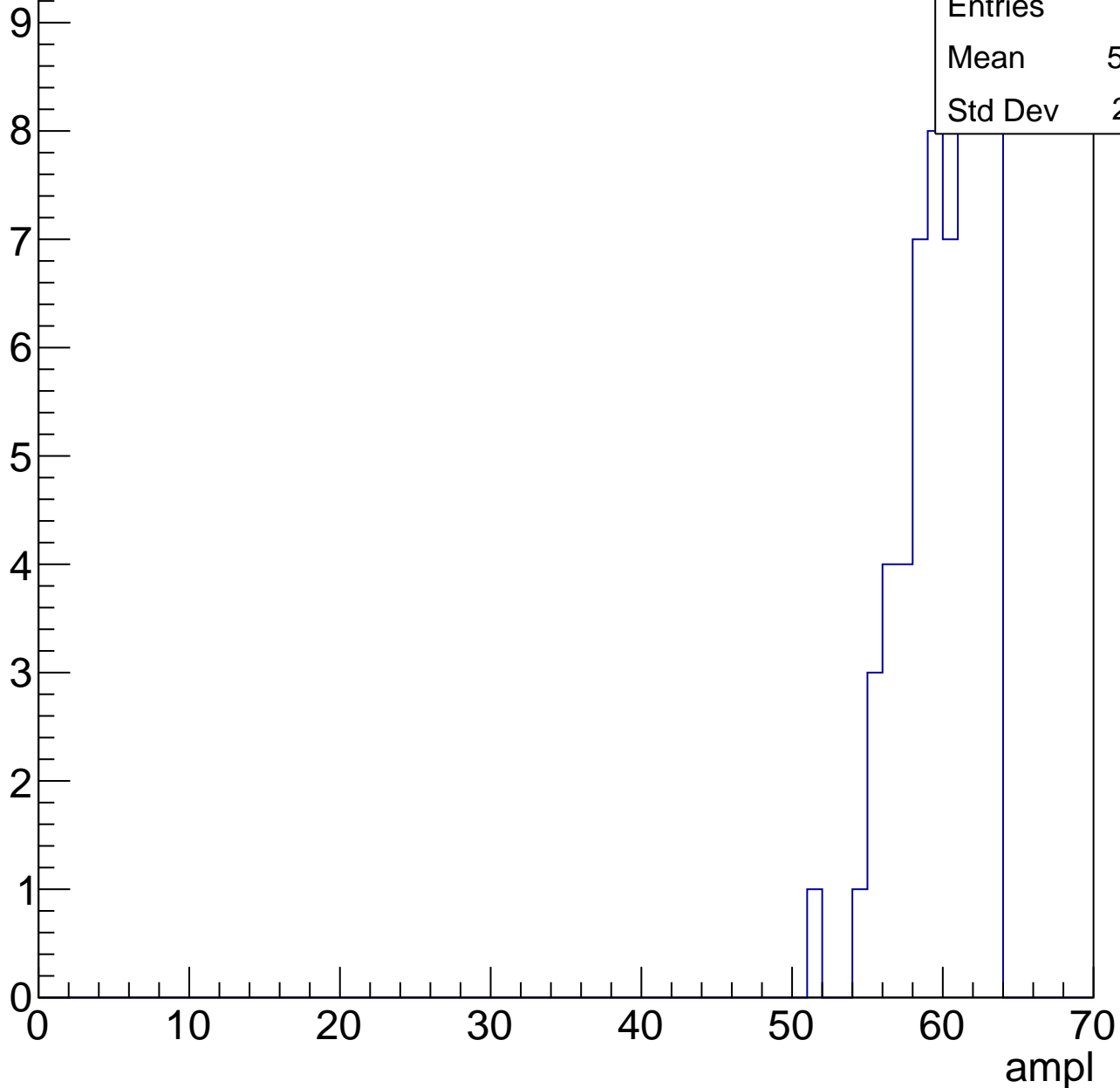
Entries	57
Mean	54.26
Std Dev	2.893



# B1L000S, U21-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

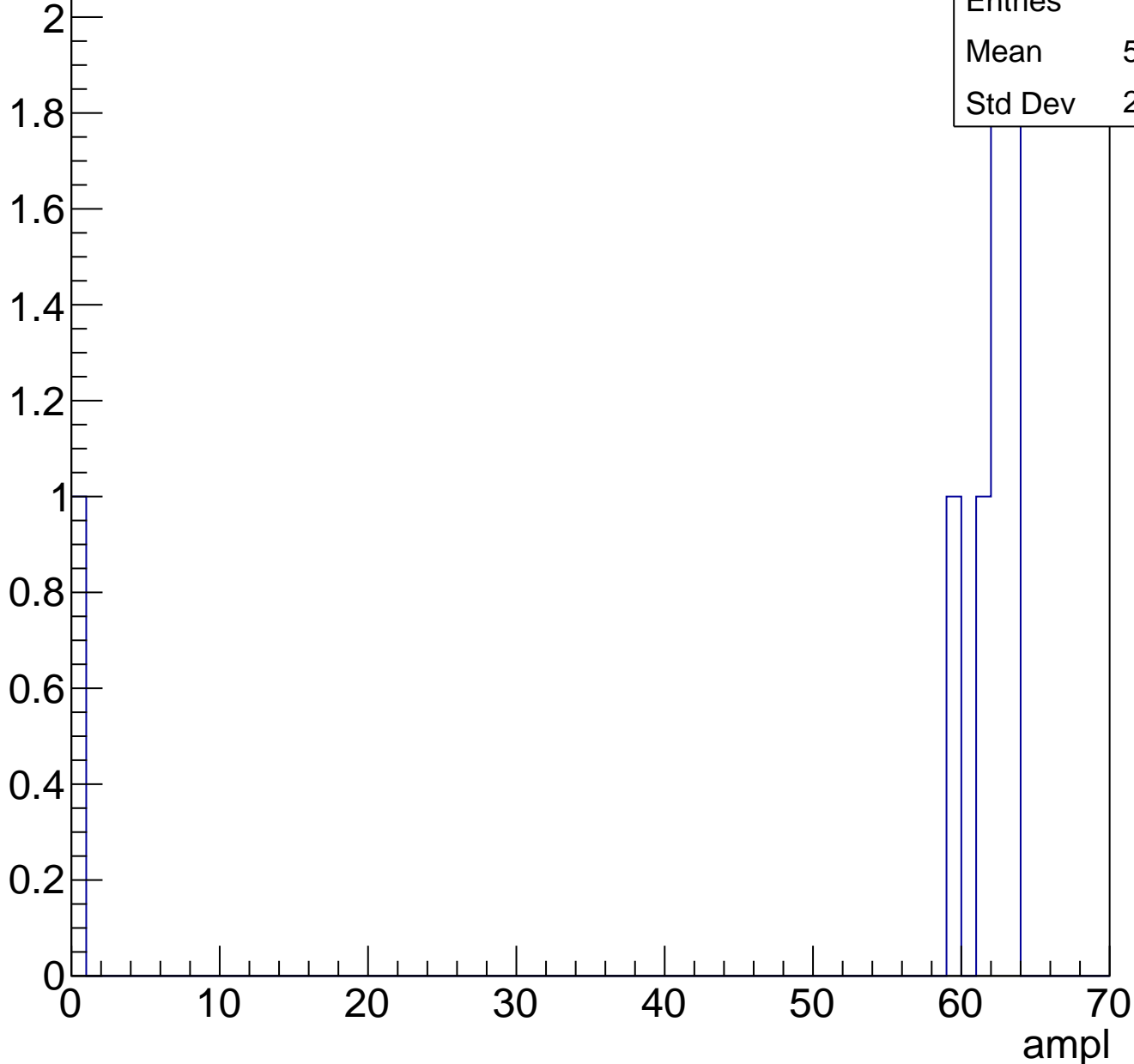
Entry



# B1L000S, U21-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch10, adc0

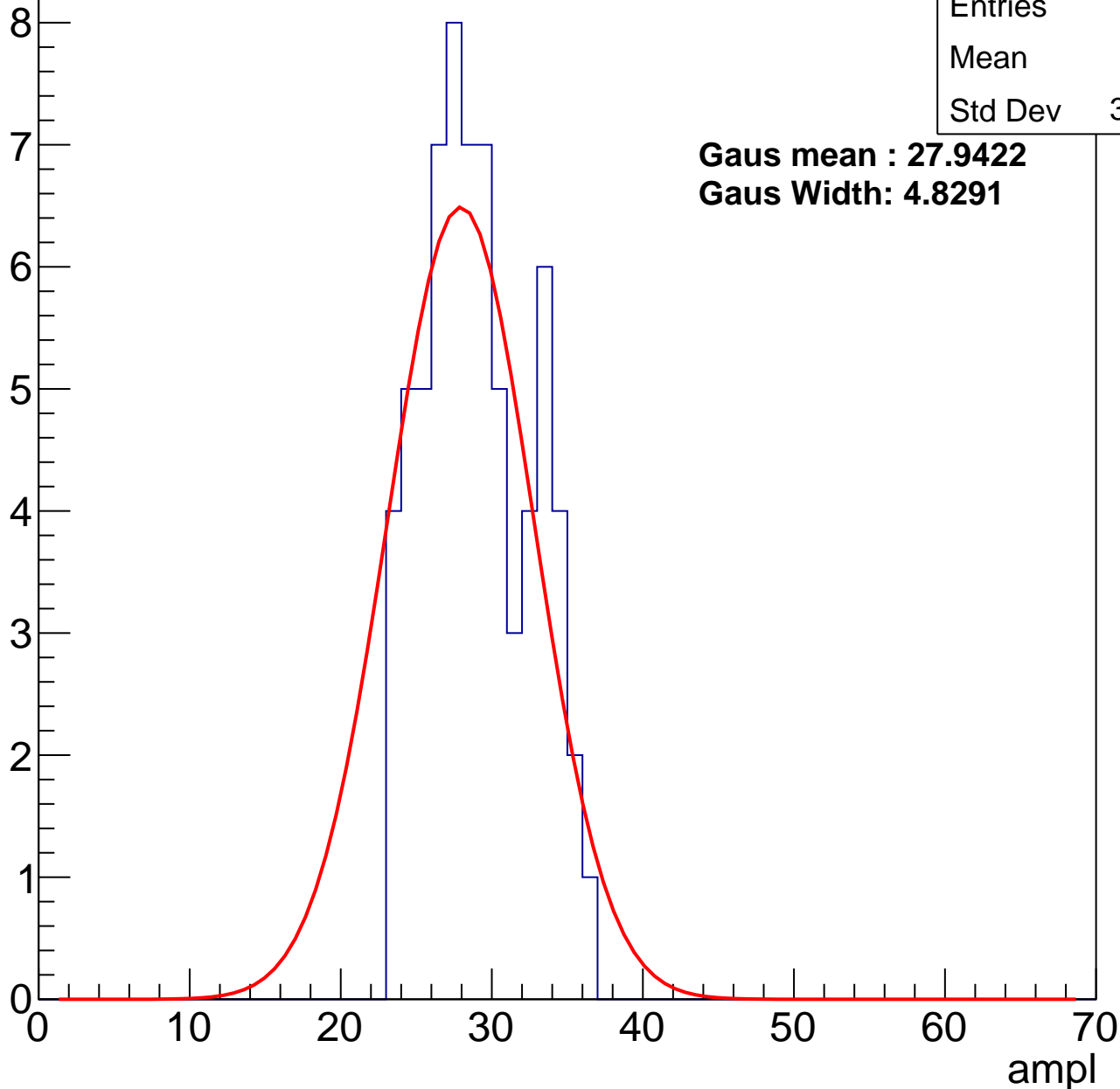
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.6
Std Dev	3.452

**Gaus mean : 27.9422**

**Gaus Width: 4.8291**



# B1L000S, U21-ch10, adc1

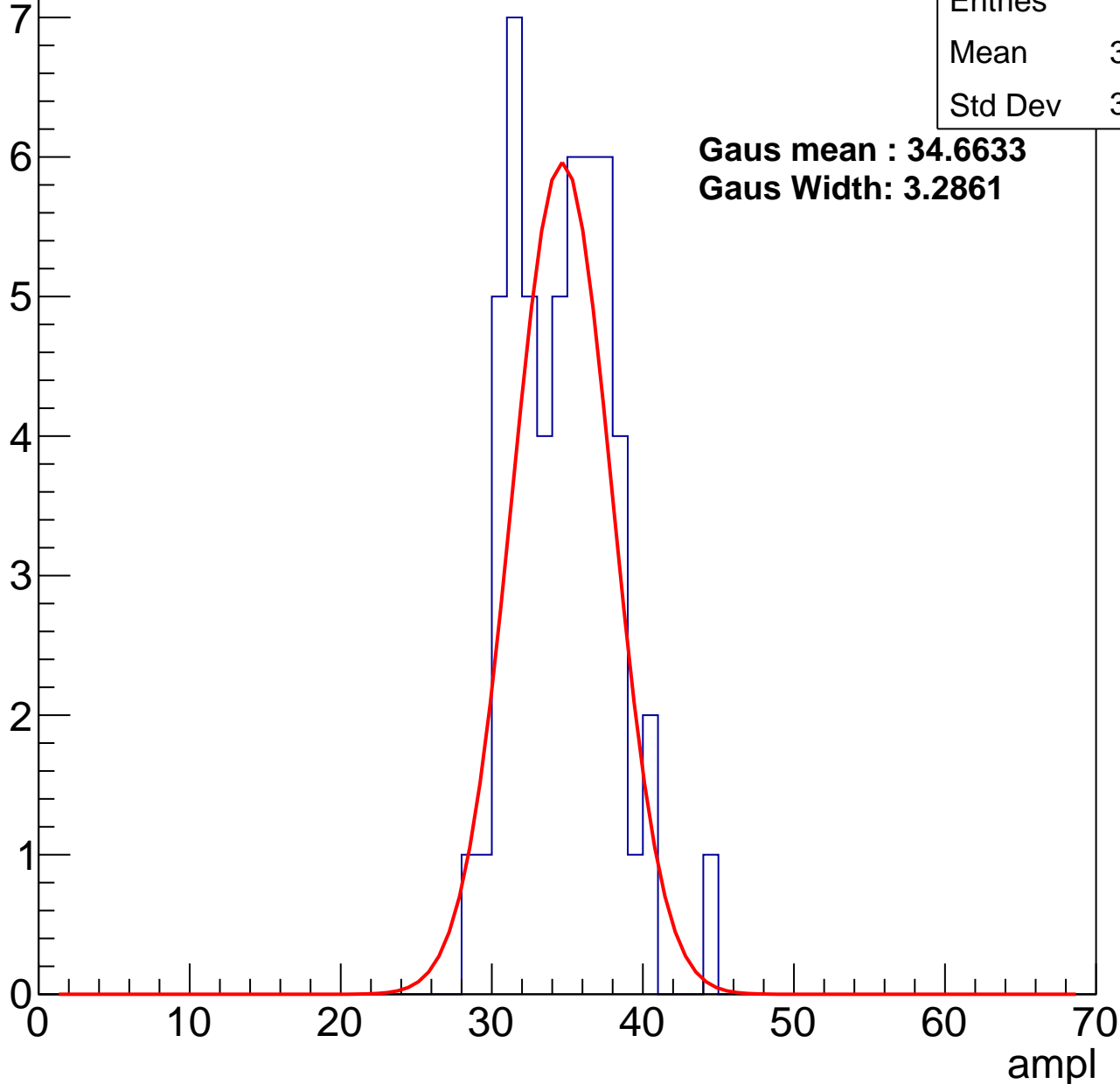
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	34.24
Std Dev	3.249

**Gaus mean : 34.6633**

**Gaus Width: 3.2861**



# B1L000S, U21-ch10, adc2

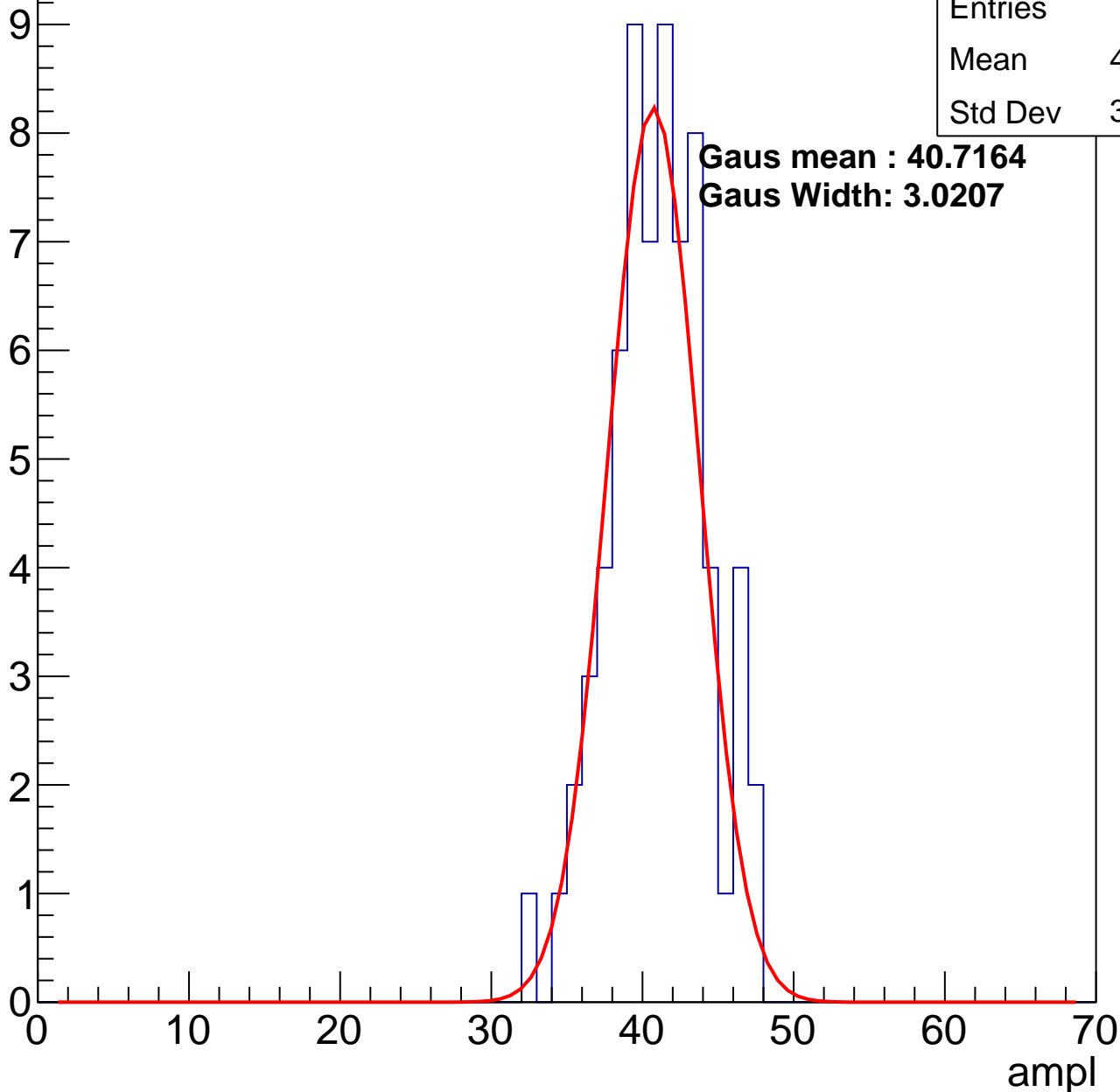
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	40.54
Std Dev	3.178

**Gaus mean : 40.7164**

**Gaus Width: 3.0207**

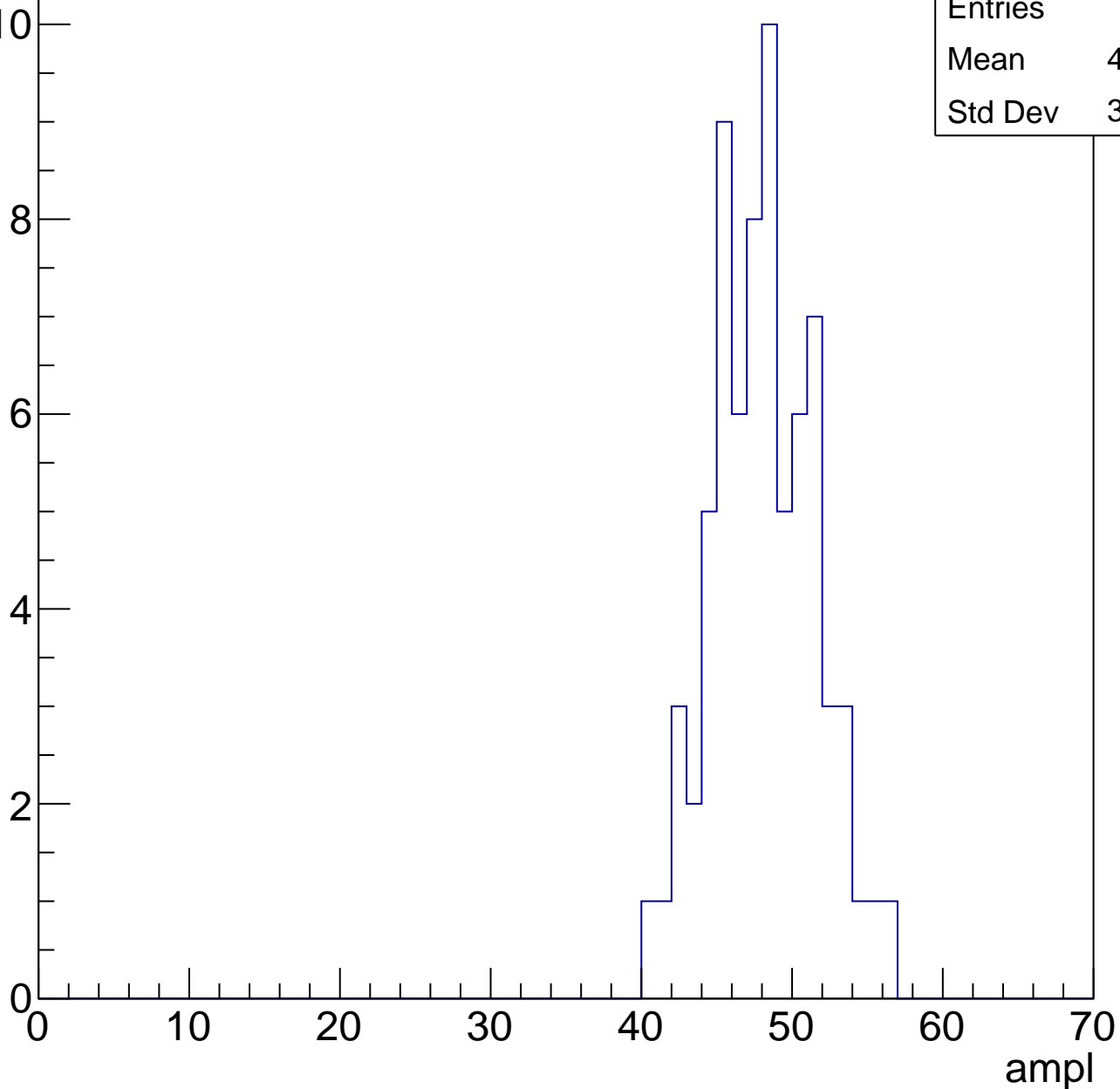


# B1L000S, U21-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	47.67
Std Dev	3.375

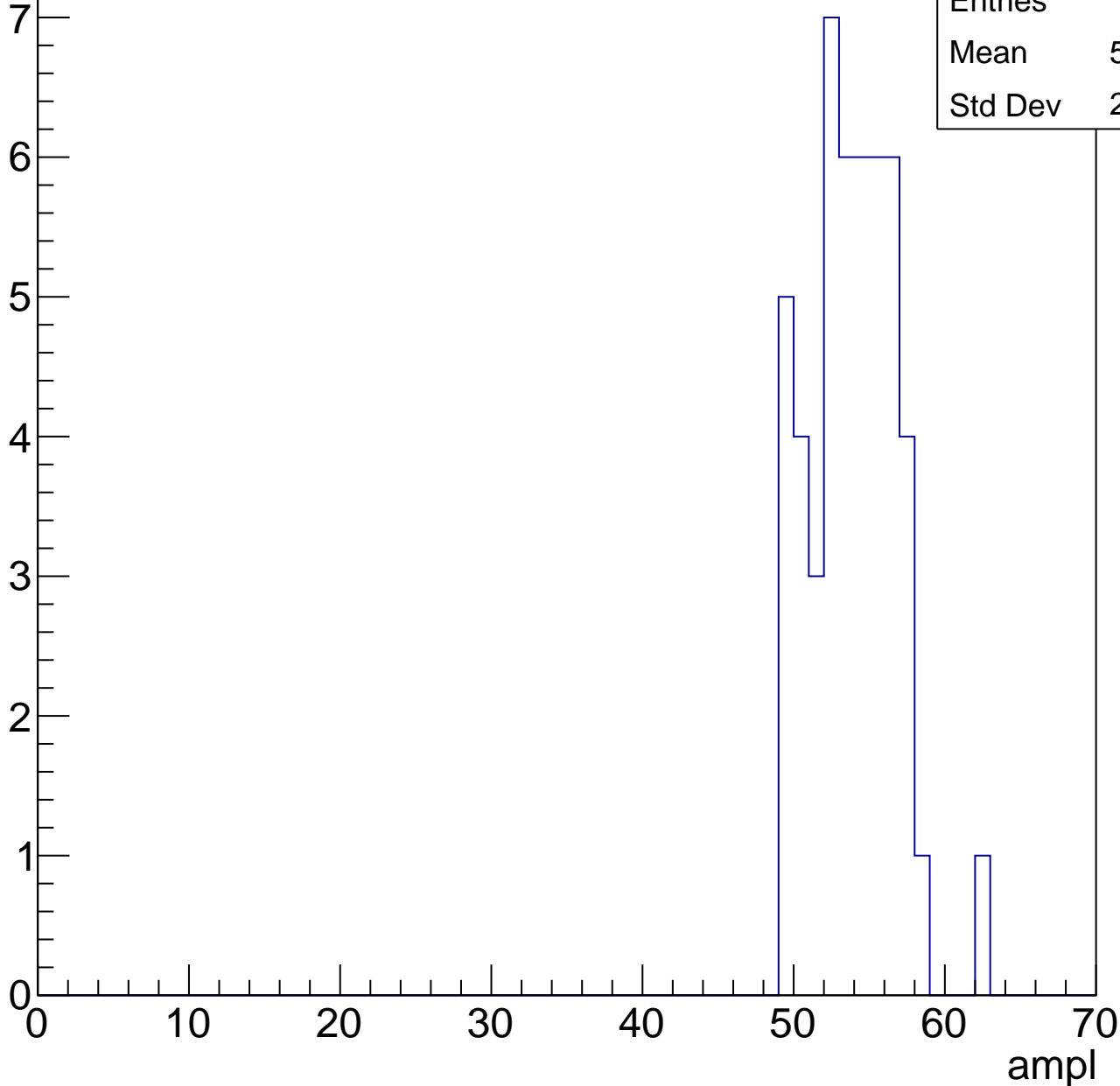


# B1L000S, U21-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

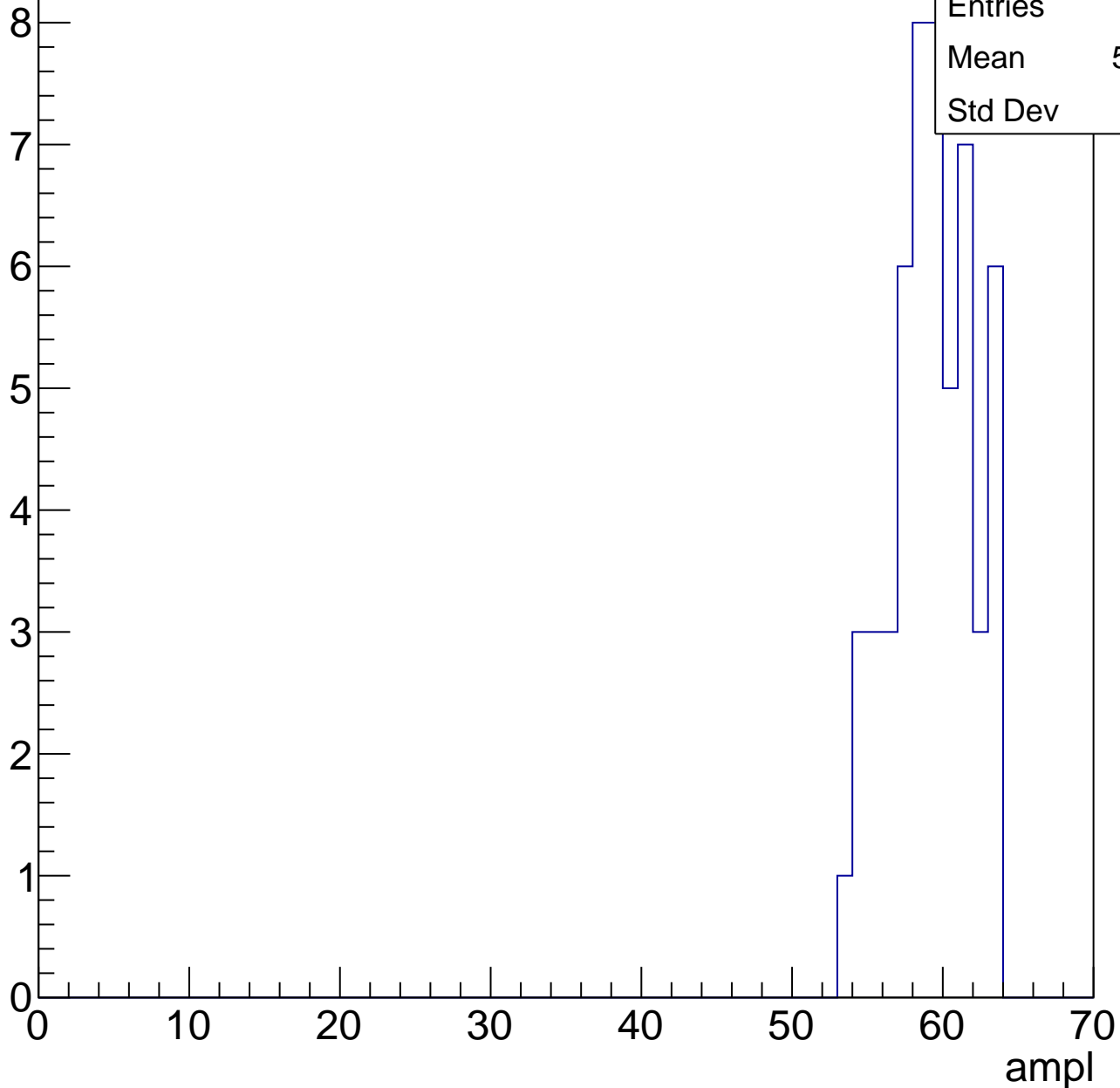
Entries	49
Mean	53.43
Std Dev	2.785



# B1L000S, U21-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

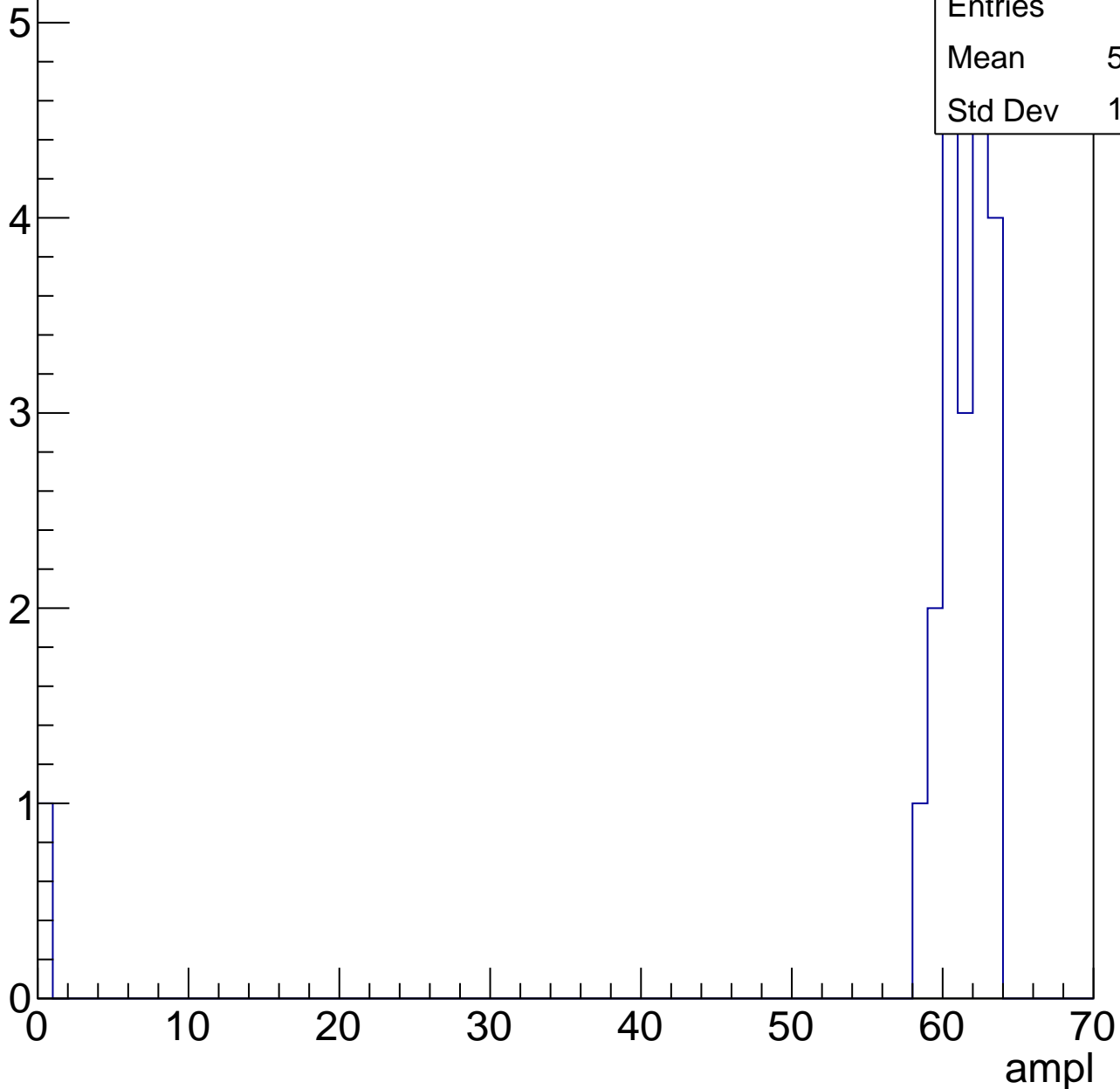


# B1L000S, U21-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.14
Std Dev	13.08

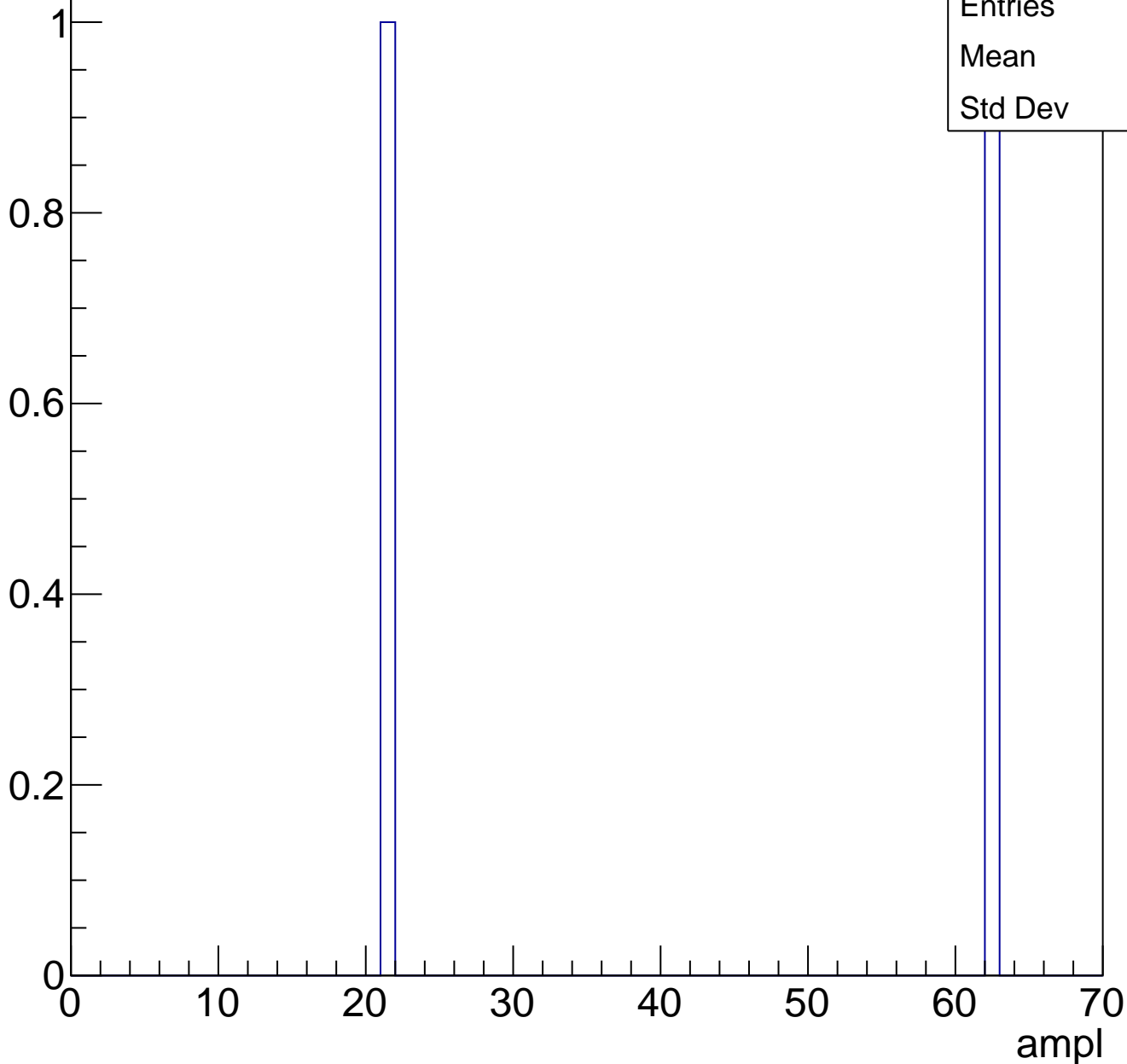




# B1L000S, U21-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch11, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	28.37
Std Dev	2.856

**Gaus mean : 28.6642**

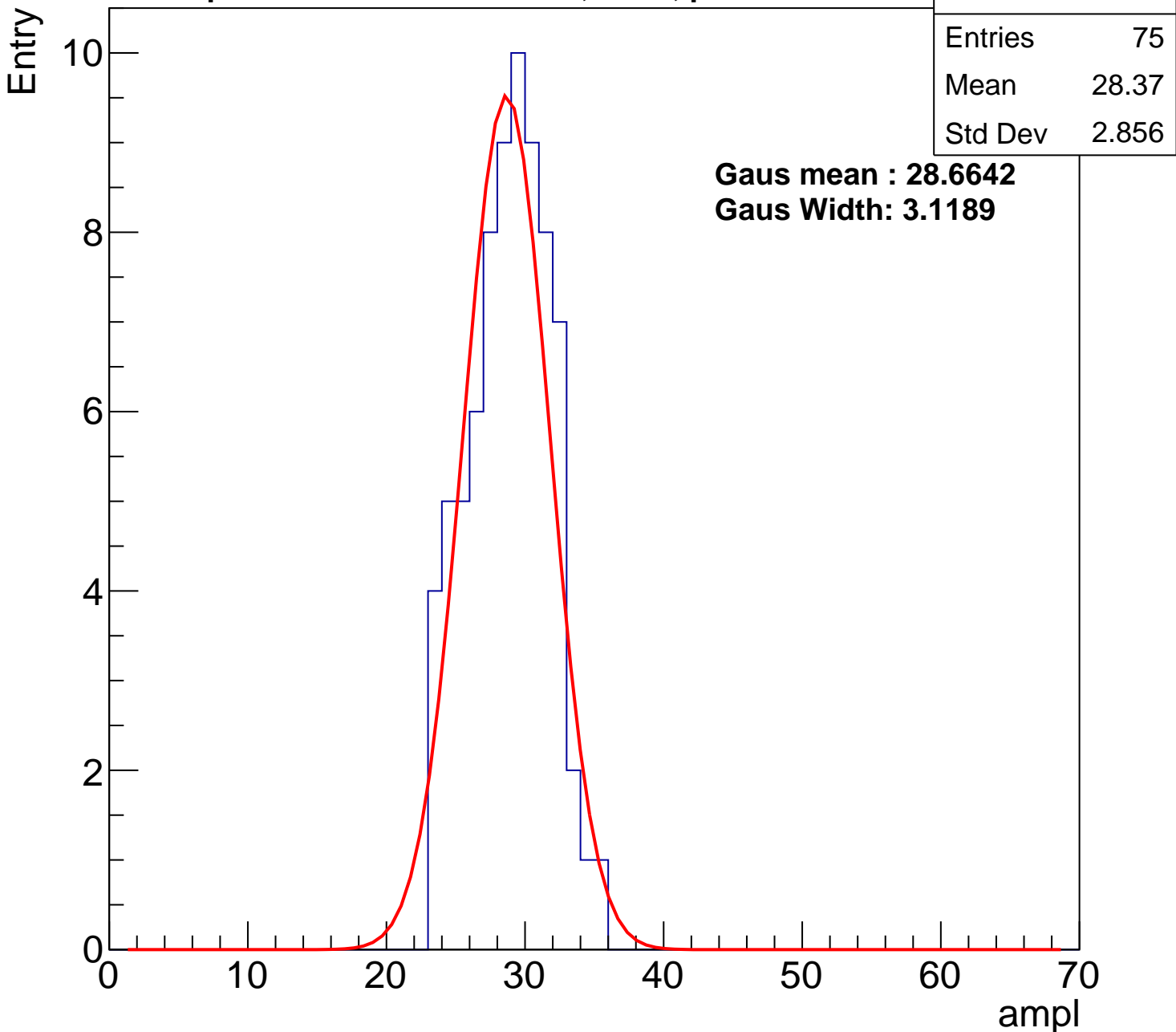
**Gaus Width: 3.1189**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch11, adc1

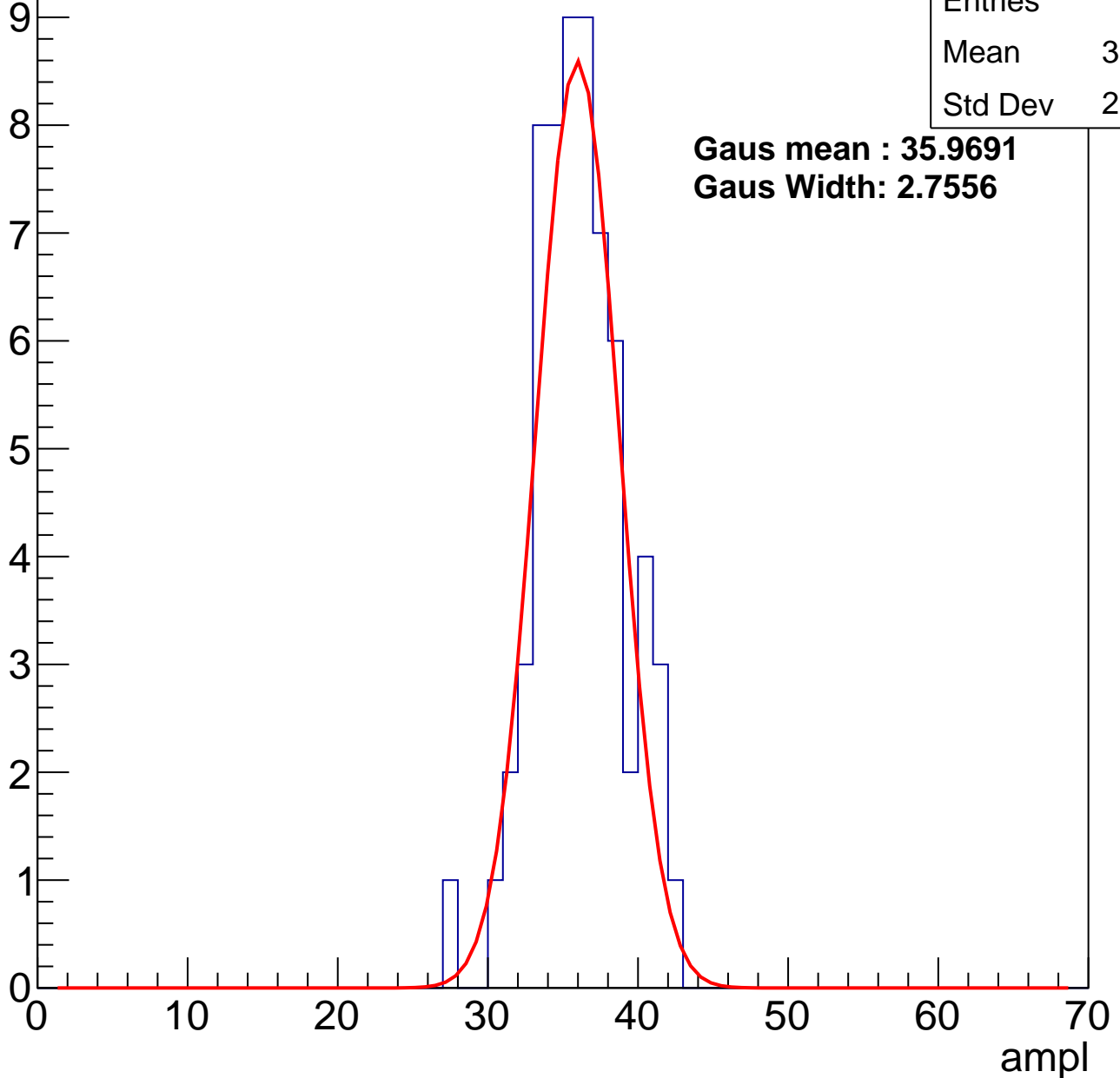
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.62
Std Dev	2.913

**Gaus mean : 35.9691**

**Gaus Width: 2.7556**



# B1L000S, U21-ch11, adc2

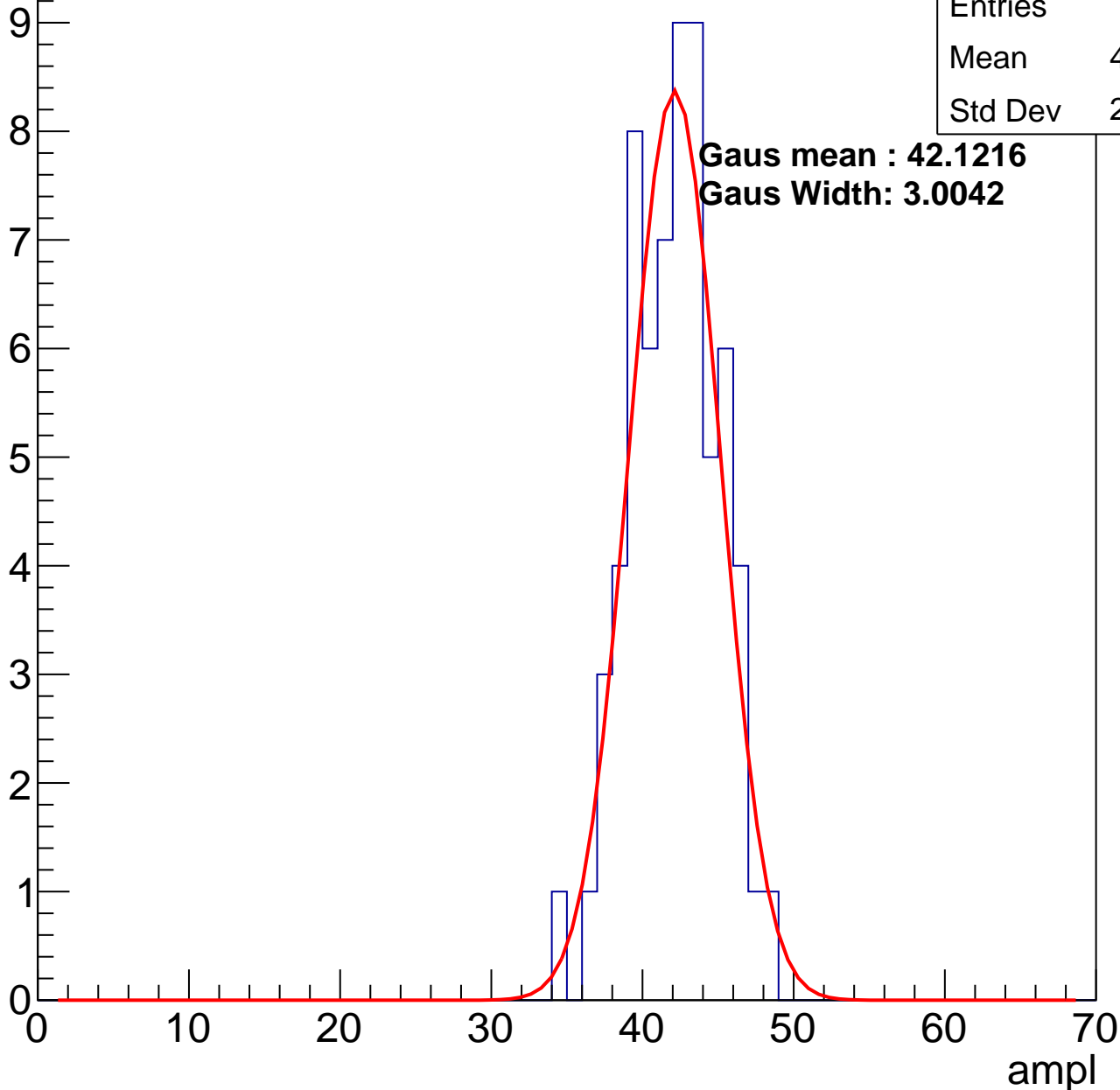
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.63
Std Dev	2.885

**Gaus mean : 42.1216**

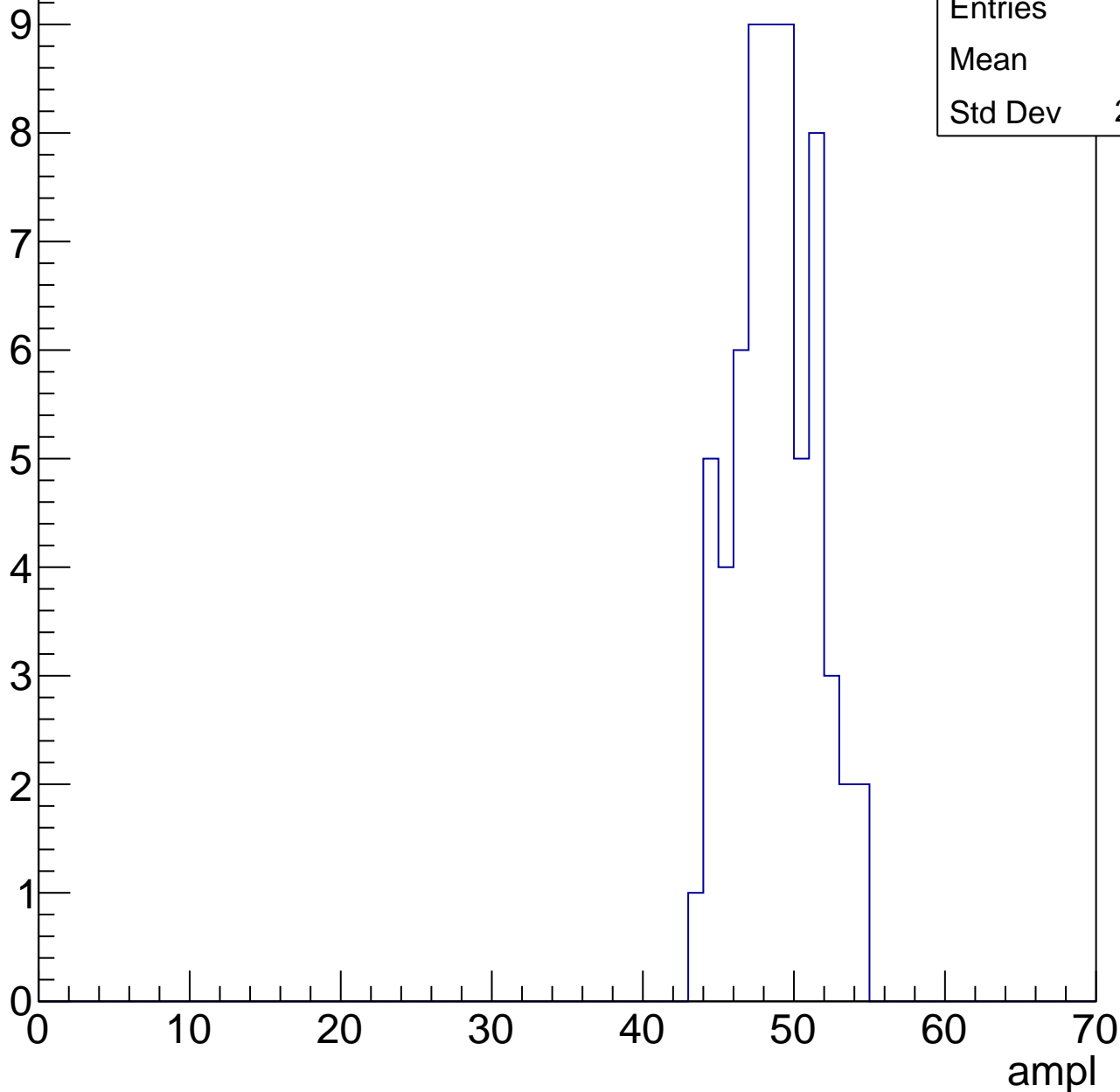
**Gaus Width: 3.0042**



# B1L000S, U21-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

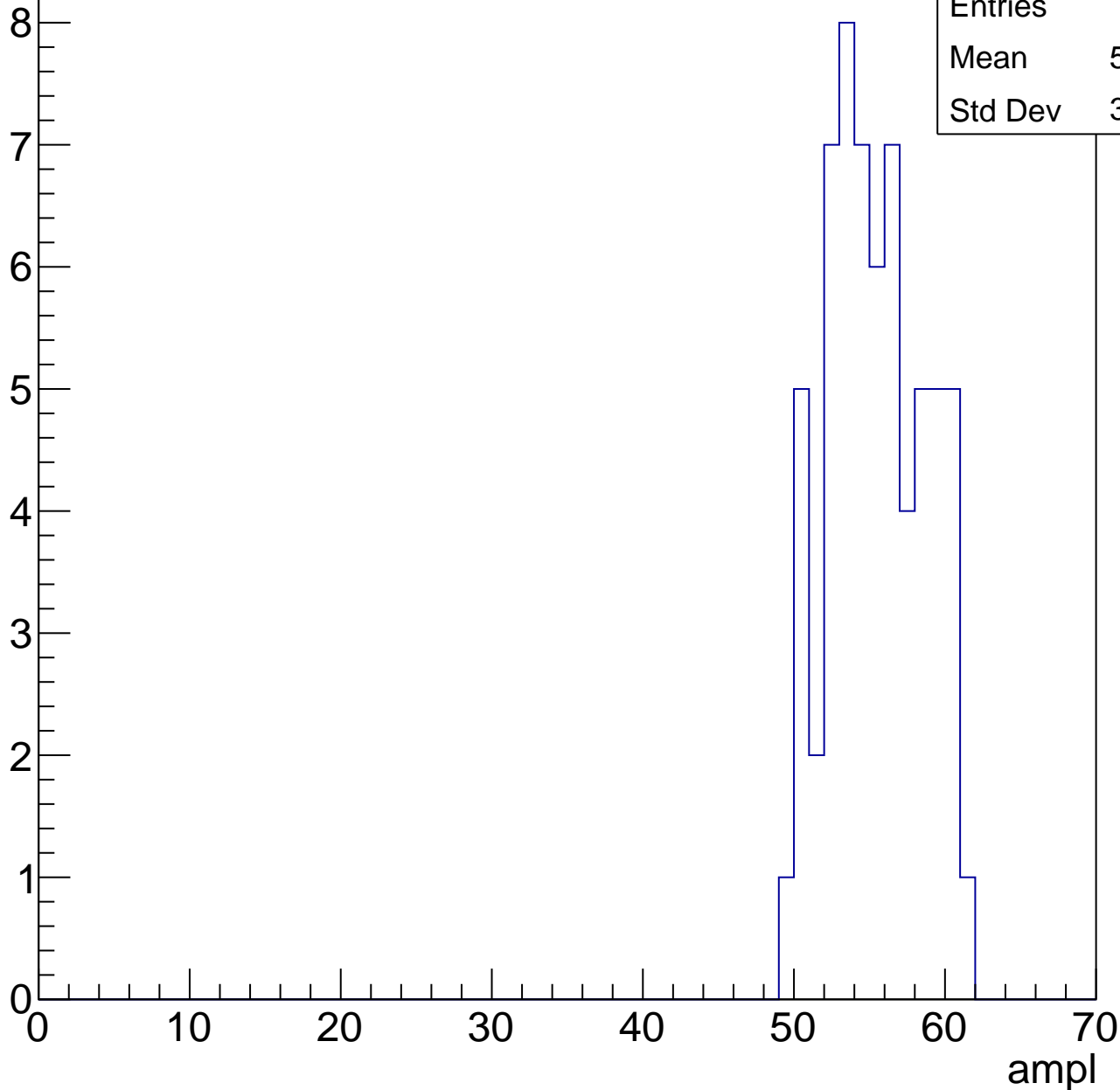


# B1L000S, U21-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

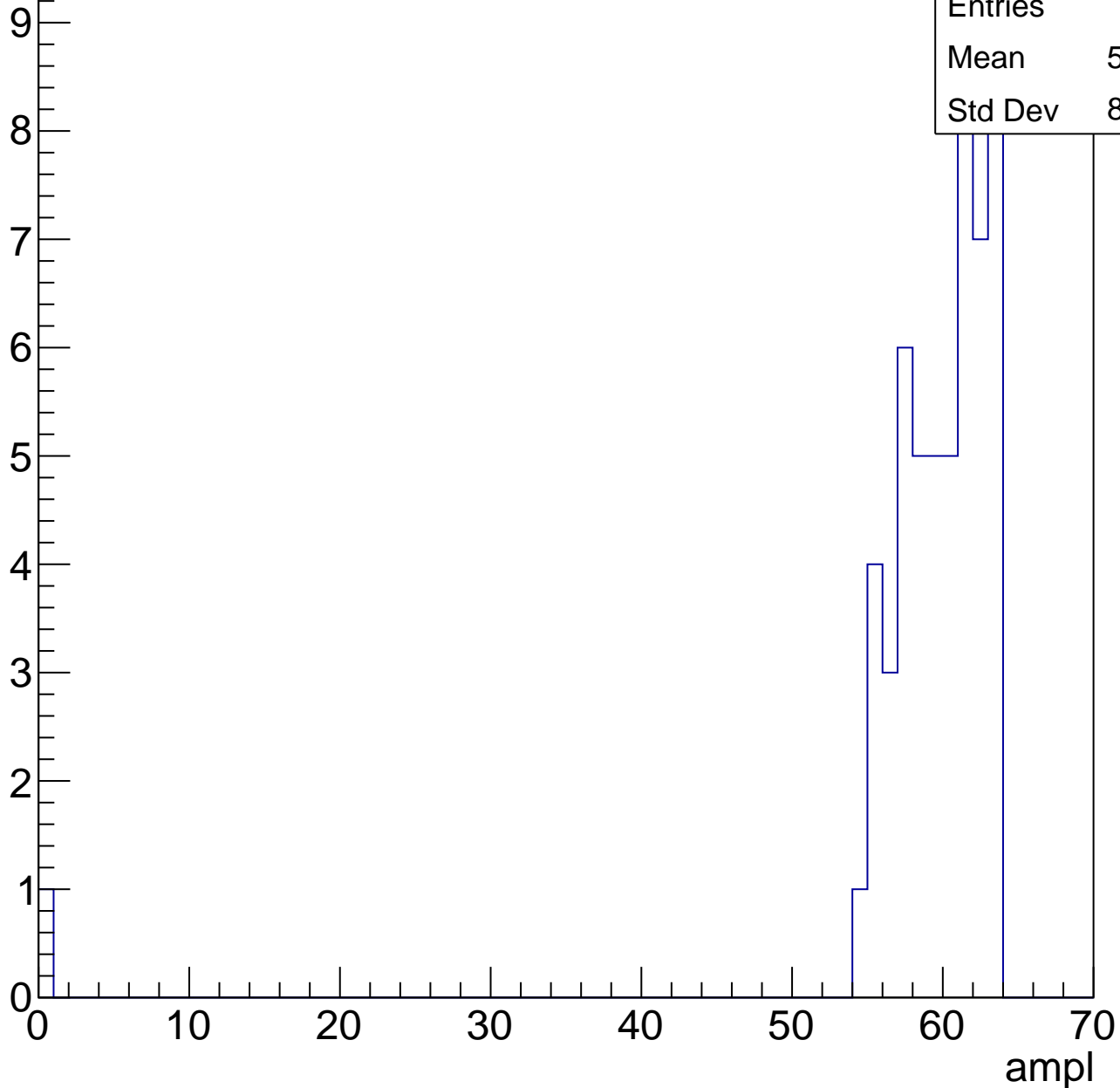
Entries	63
Mean	54.97
Std Dev	3.096



# B1L000S, U21-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	54
Mean	58.44
Std Dev	8.434

# B1L000S, U21-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.4

Std Dev

0.4899



# B1L000S, U21-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	29.42
Std Dev	5.623

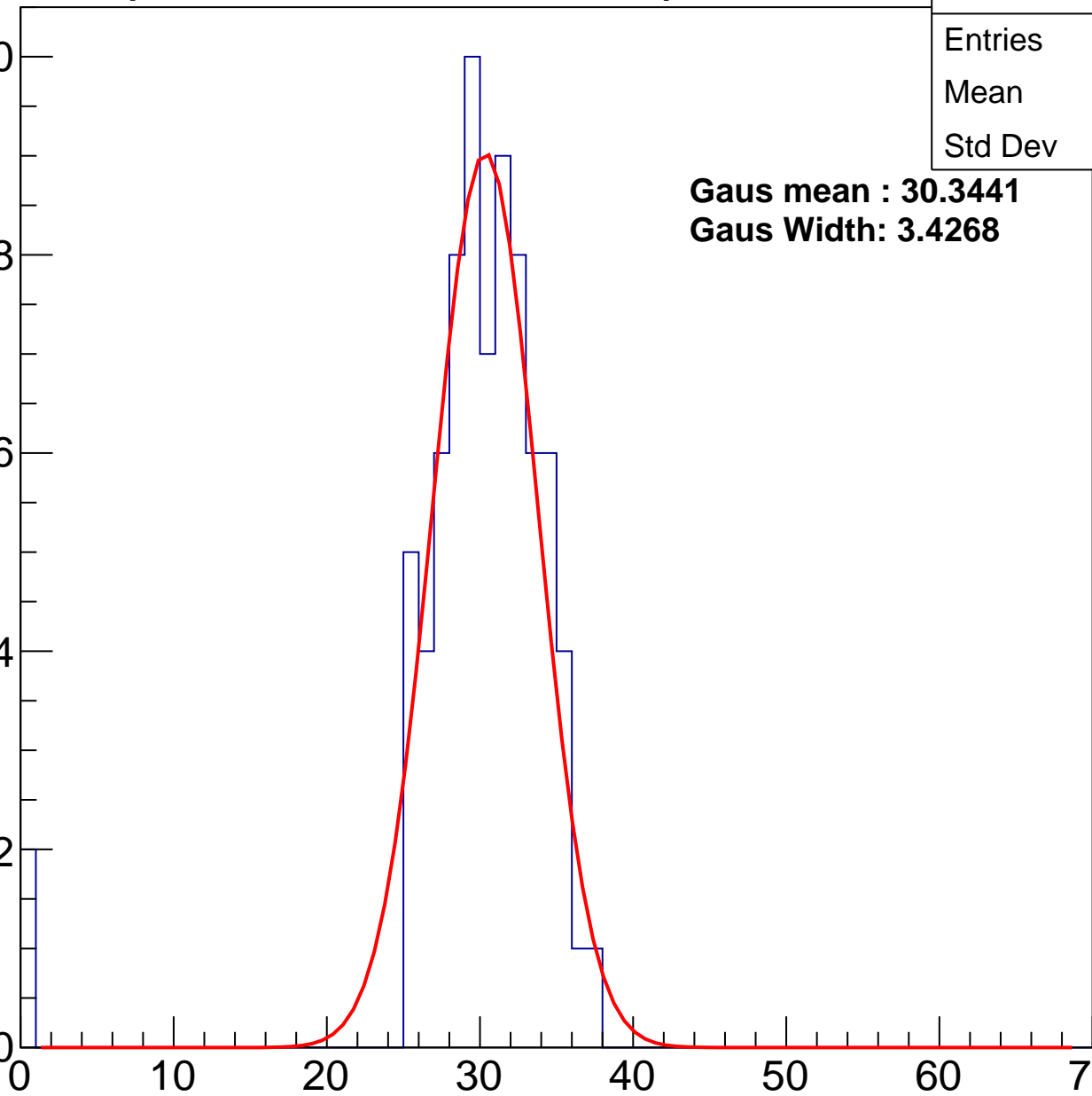
**Gaus mean : 30.3441**

**Gaus Width: 3.4268**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L000S, U21-ch12, adc1

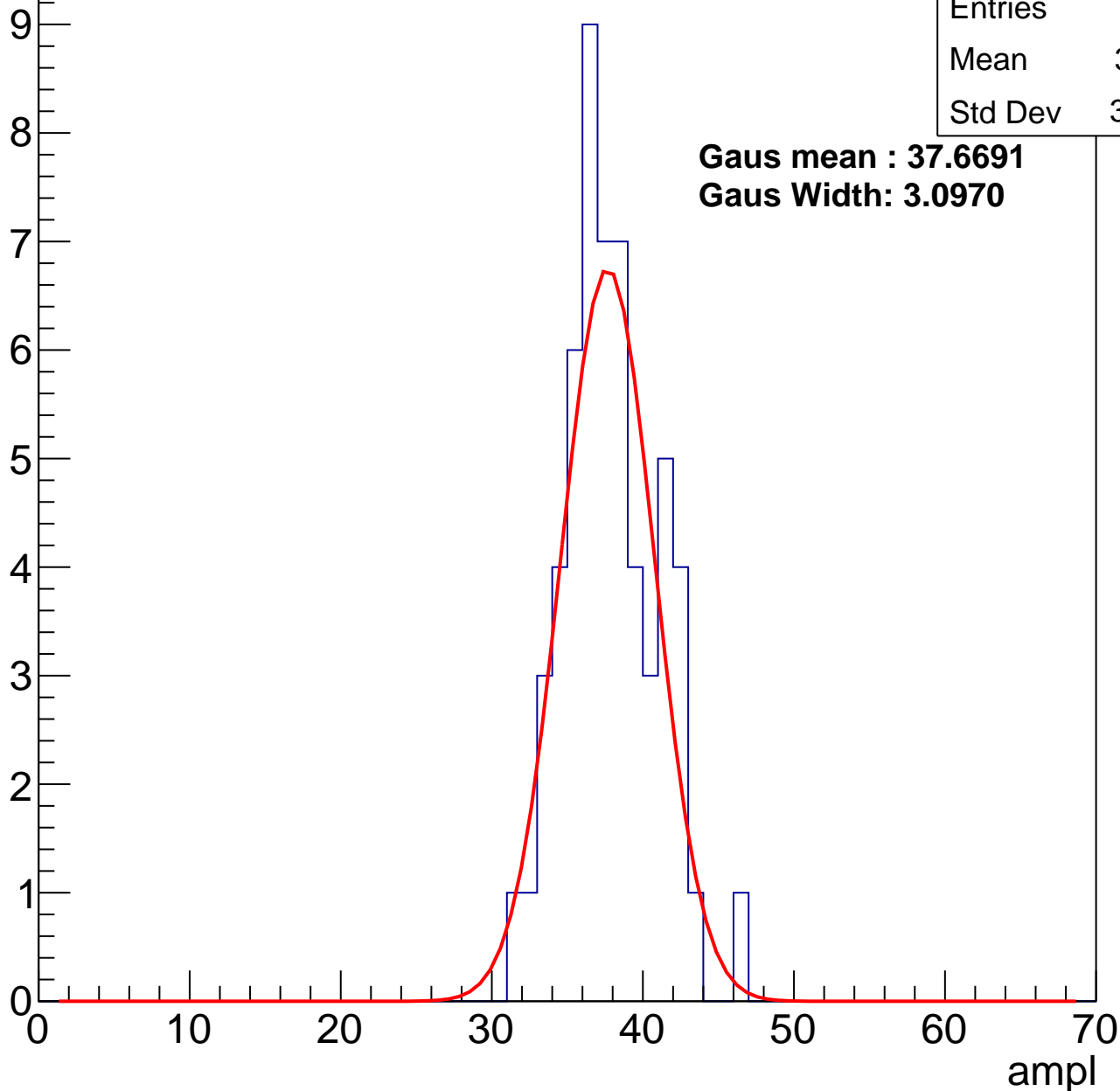
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	37.41
Std Dev	3.034

**Gaus mean : 37.6691**

**Gaus Width: 3.0970**



# B1L000S, U21-ch12, adc2

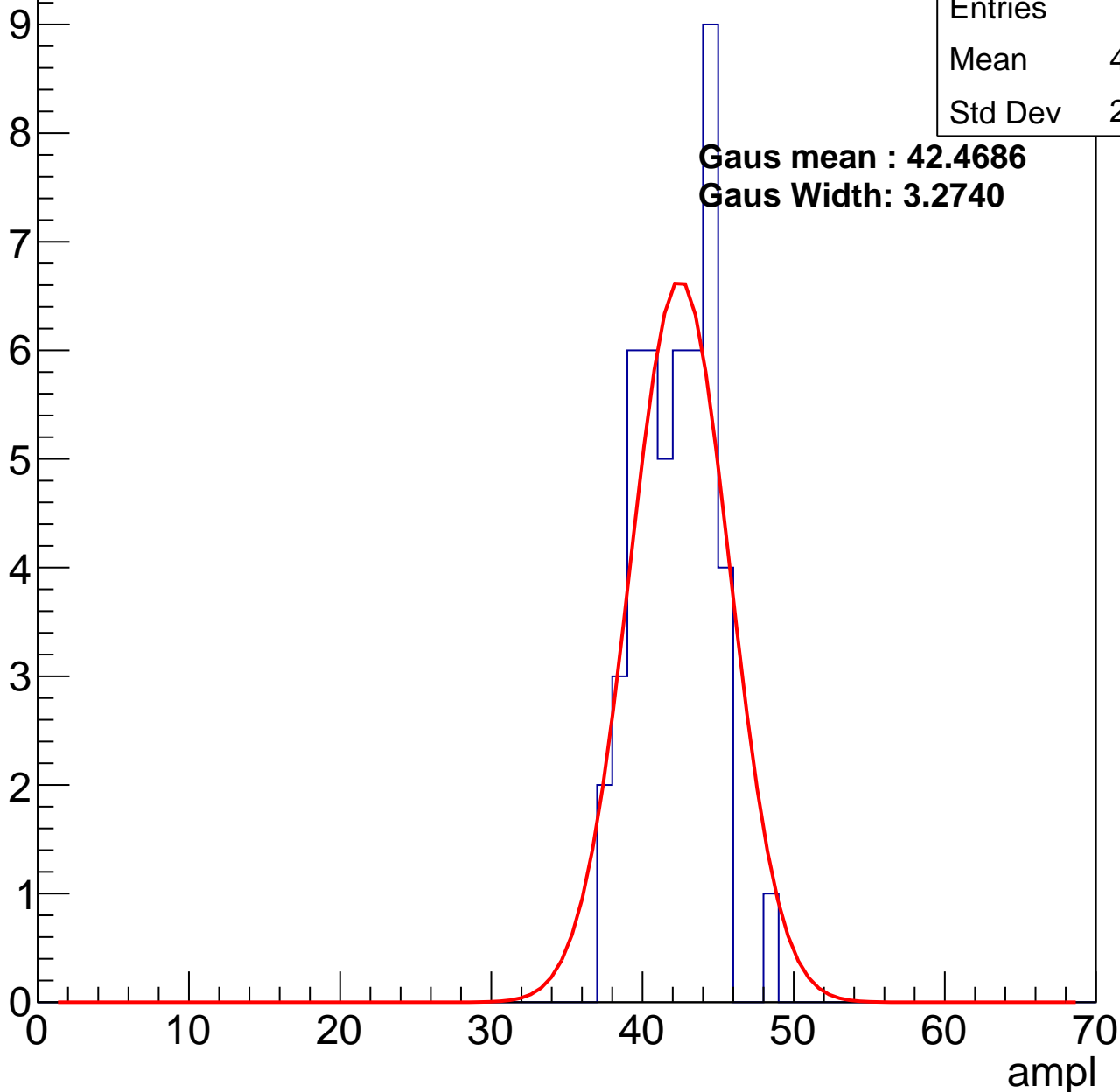
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	41.69
Std Dev	2.459

**Gaus mean : 42.4686**

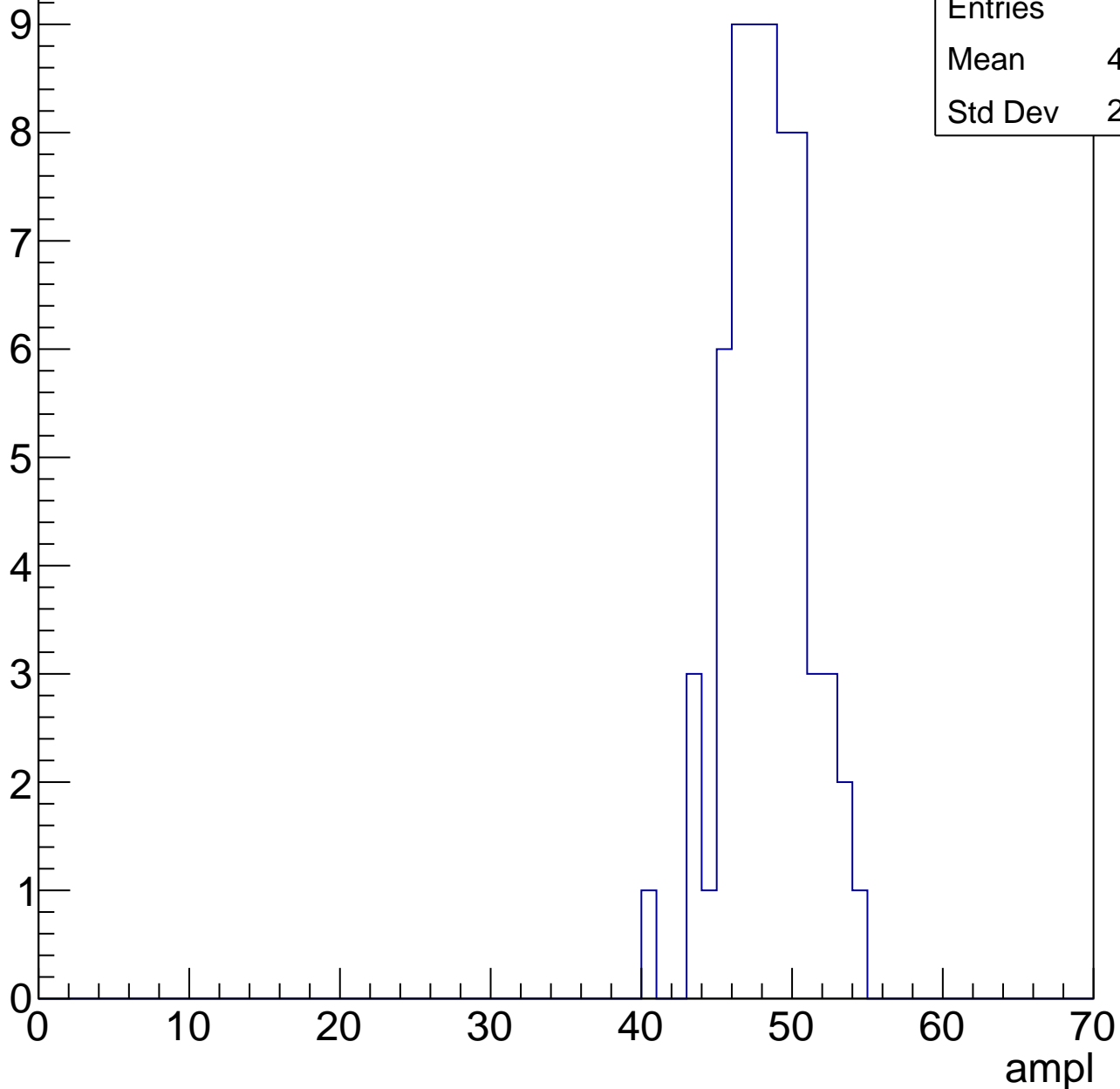
**Gaus Width: 3.2740**



# B1L000S, U21-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

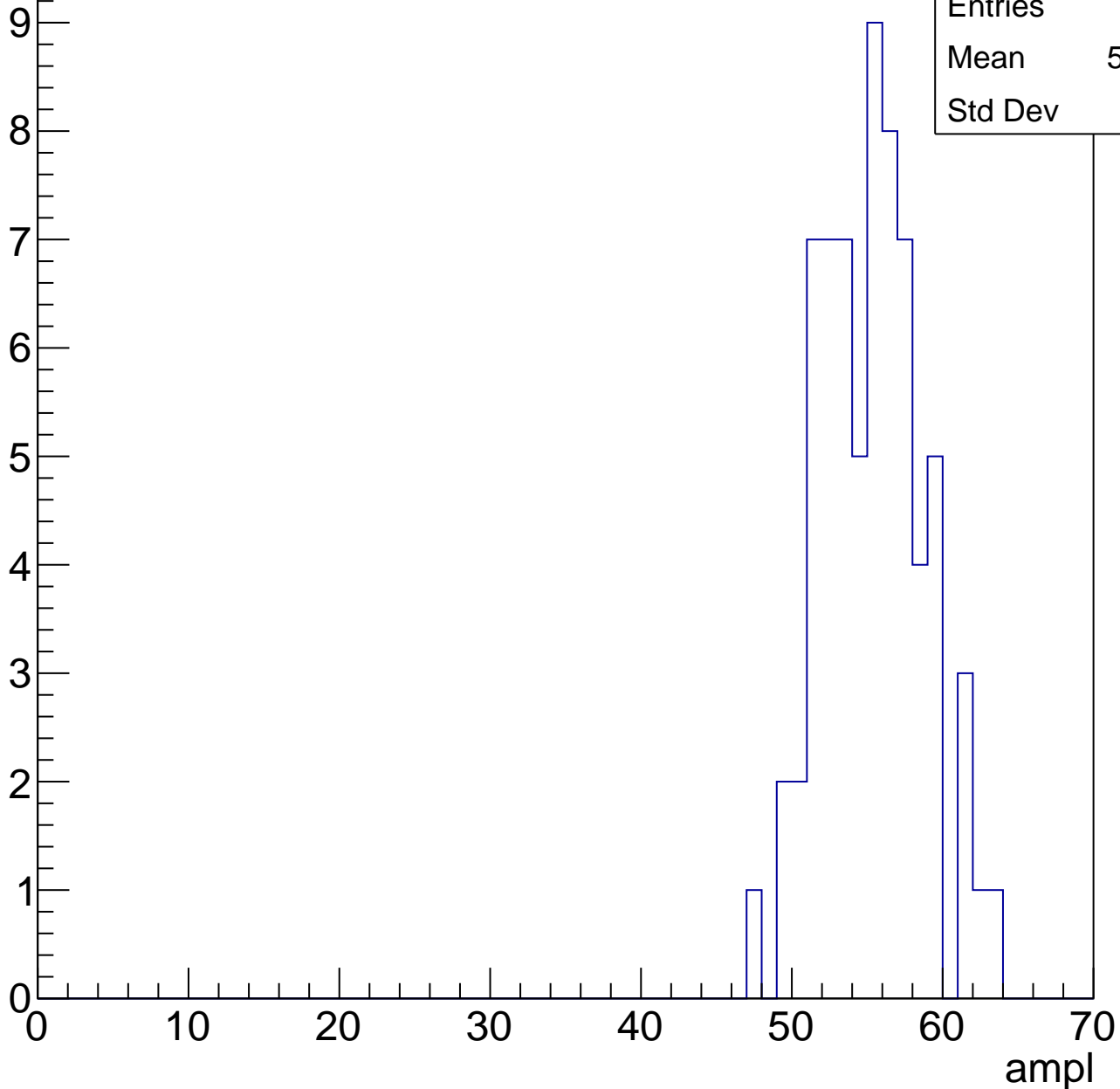


# B1L000S, U21-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	54.84
Std Dev	3.33



# B1L000S, U21-ch12, adc5

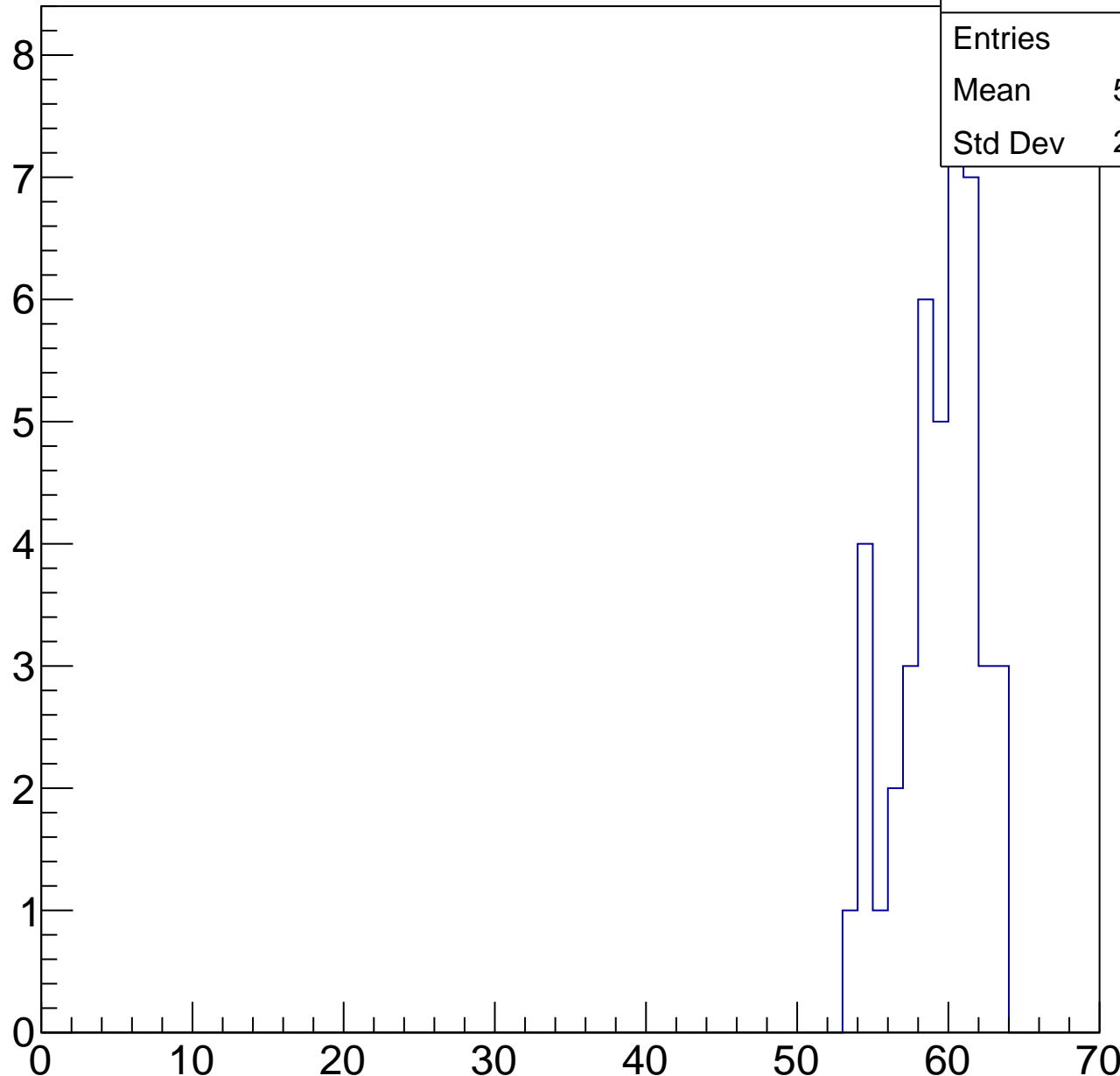
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	58.88
Std Dev	2.634

ampl

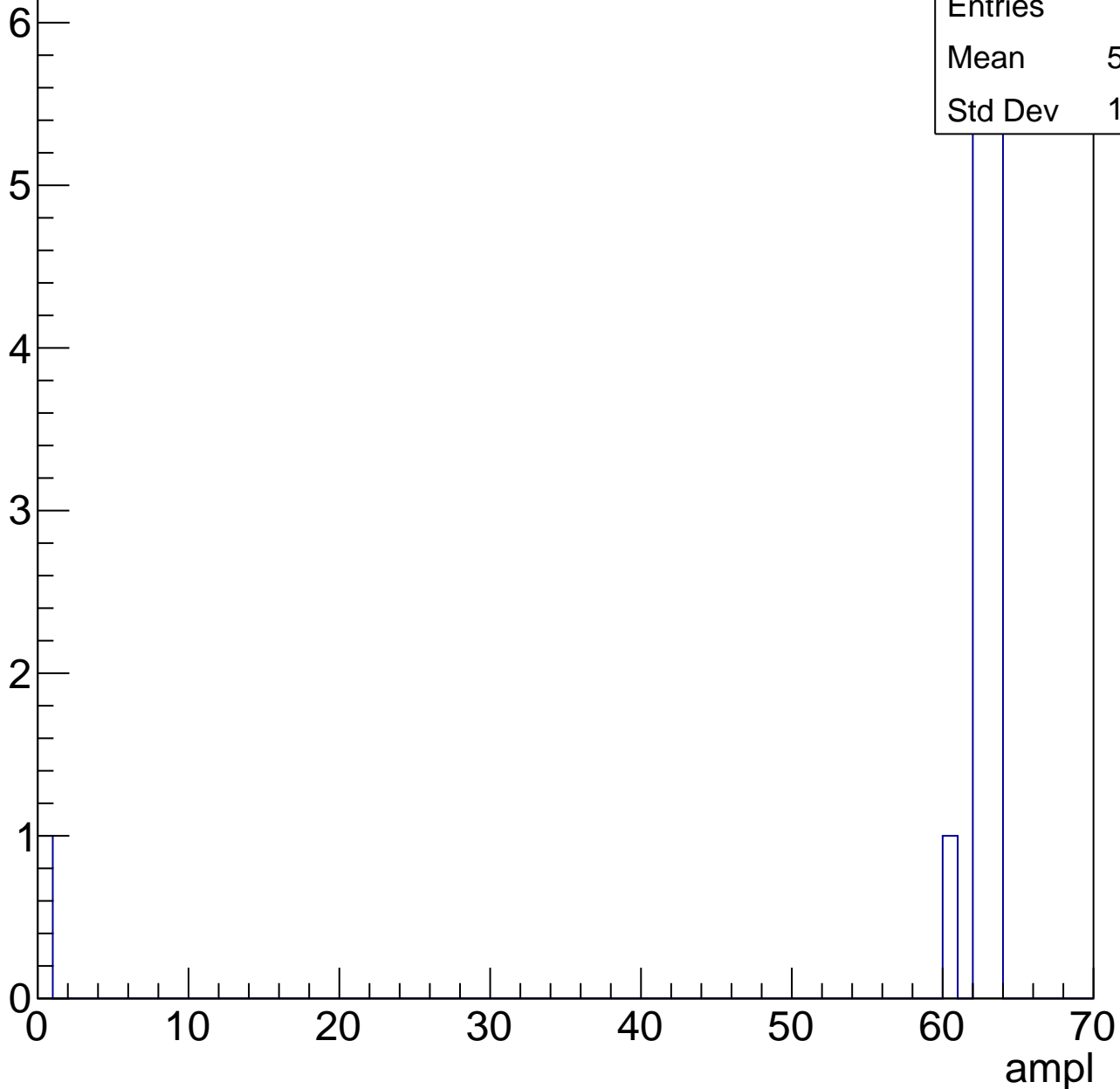


# B1L000S, U21-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.86
Std Dev	16.07





# B1L000S, U21-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	60
Std Dev	0

# B1L000S, U21-ch13, adc0

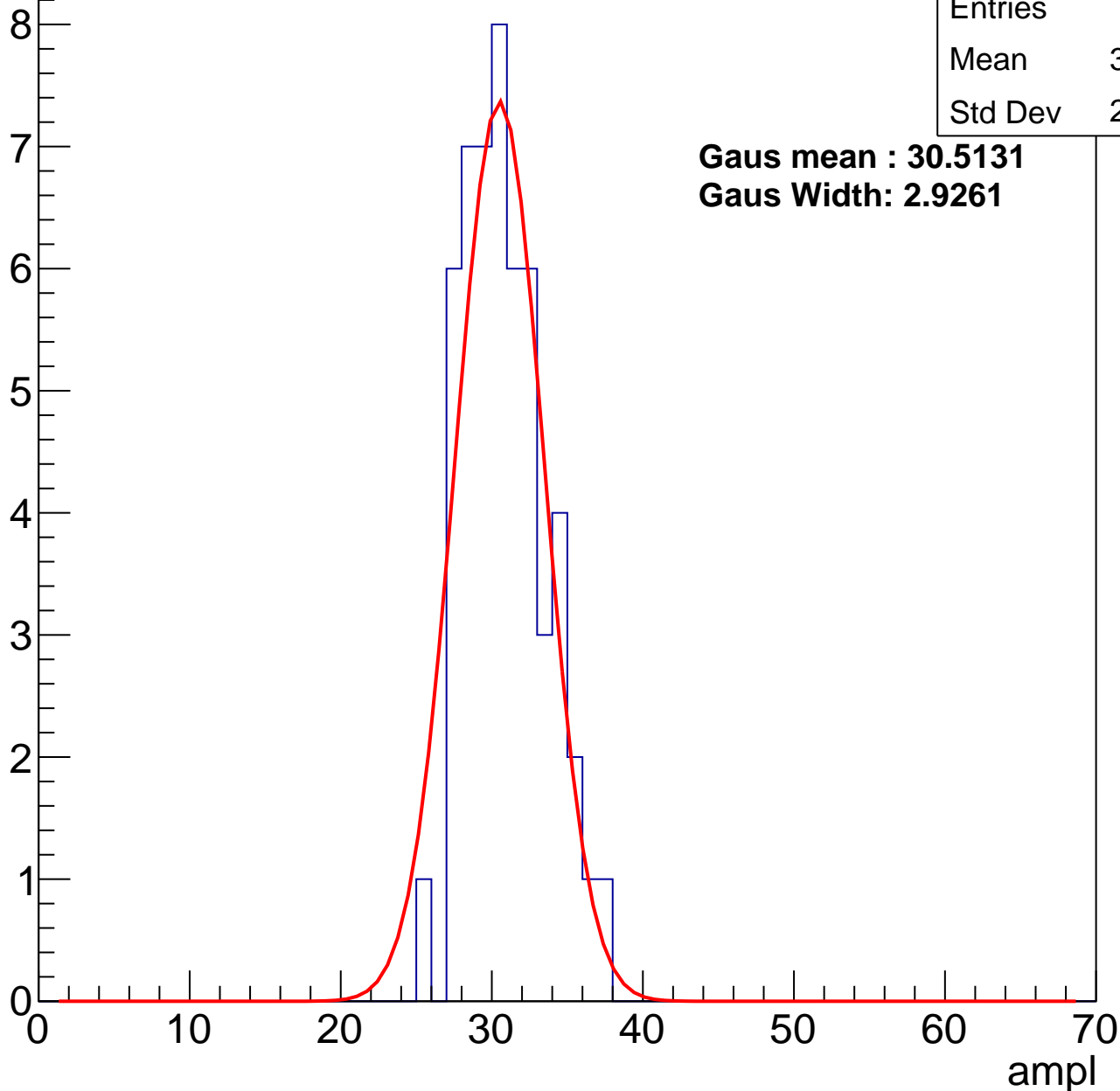
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	30.42
Std Dev	2.634

**Gaus mean : 30.5131**

**Gaus Width: 2.9261**



# B1L000S, U21-ch13, adc1

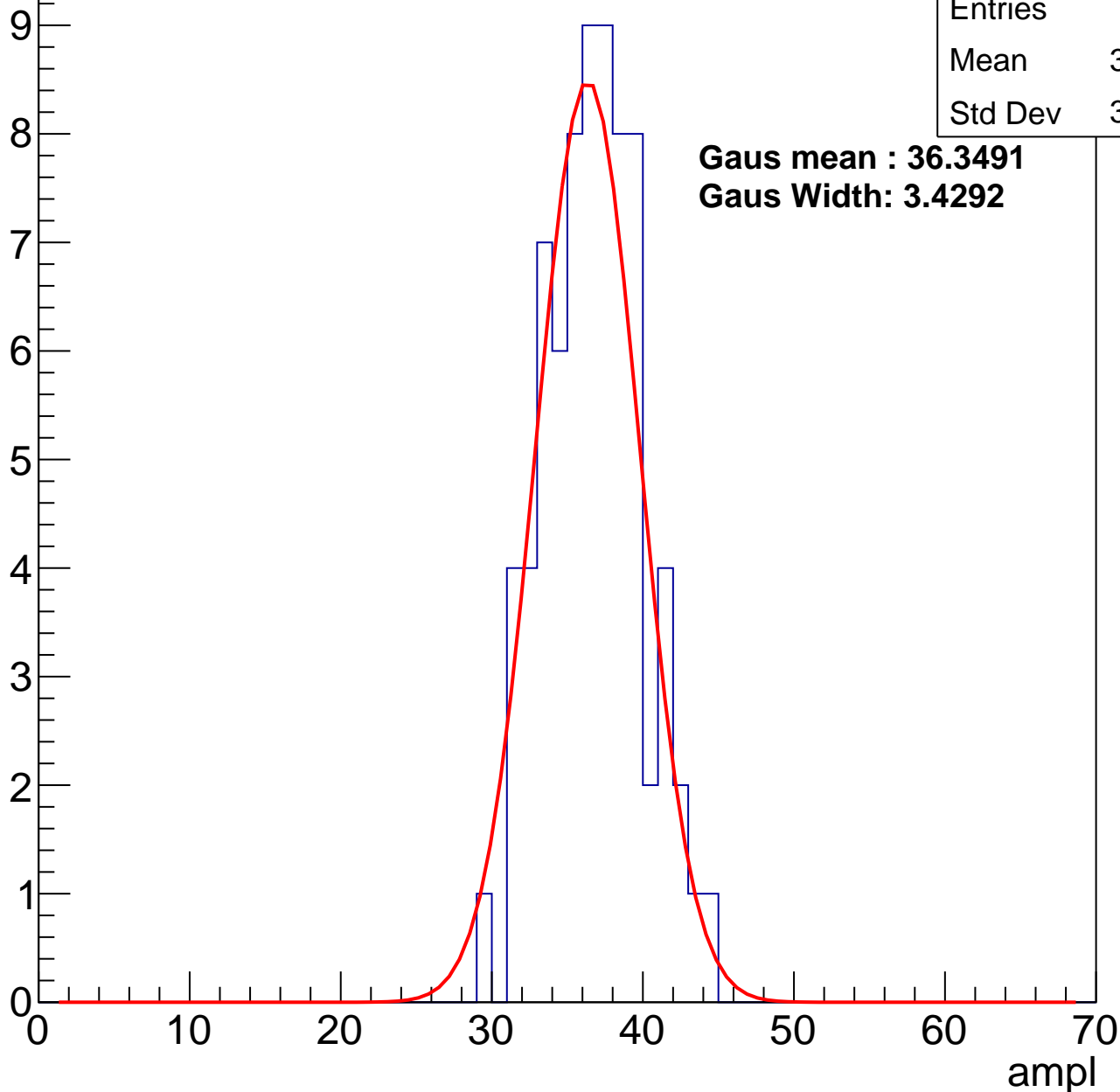
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	36.27
Std Dev	3.146

**Gaus mean : 36.3491**

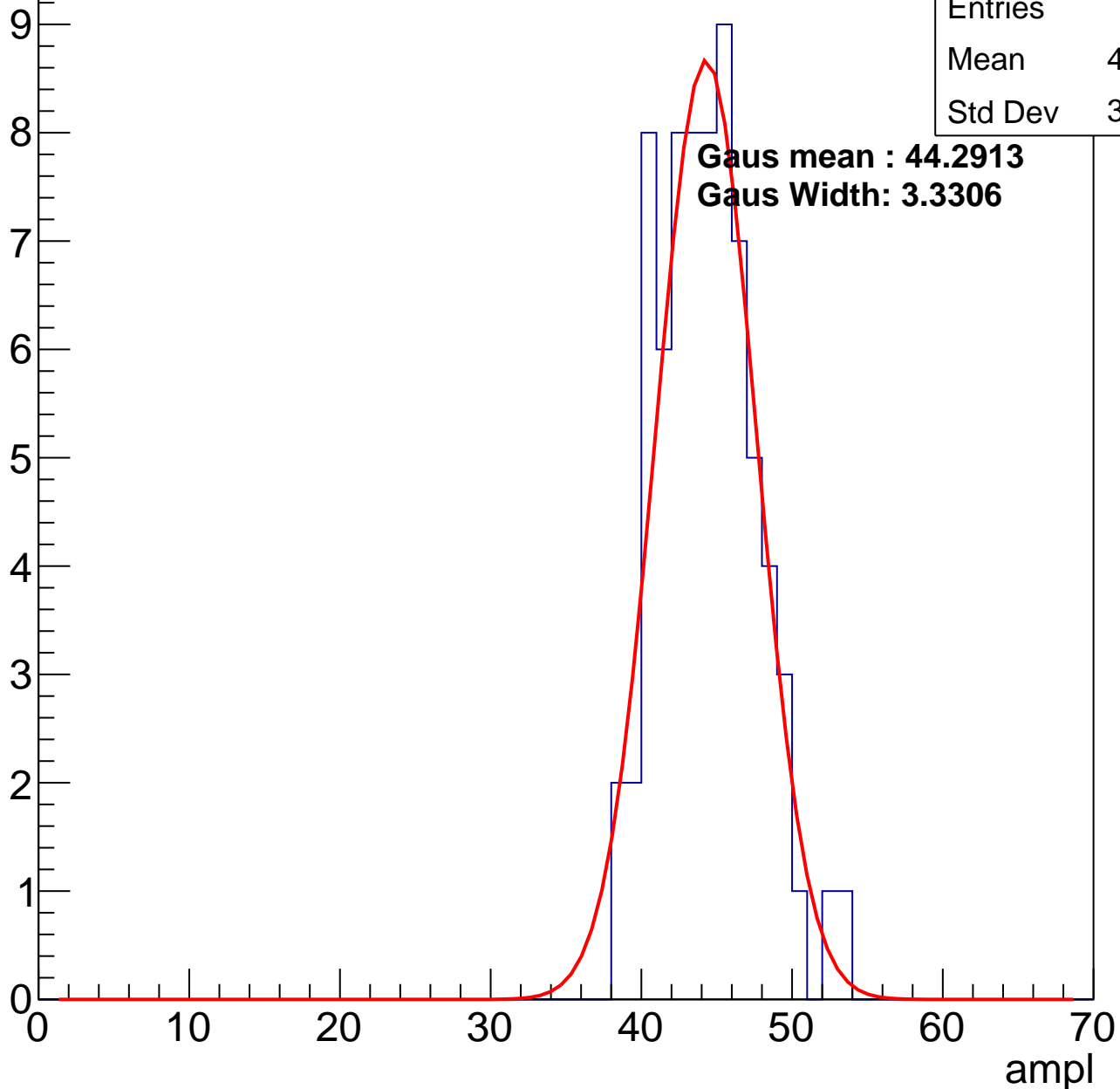
**Gaus Width: 3.4292**



# B1L000S, U21-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



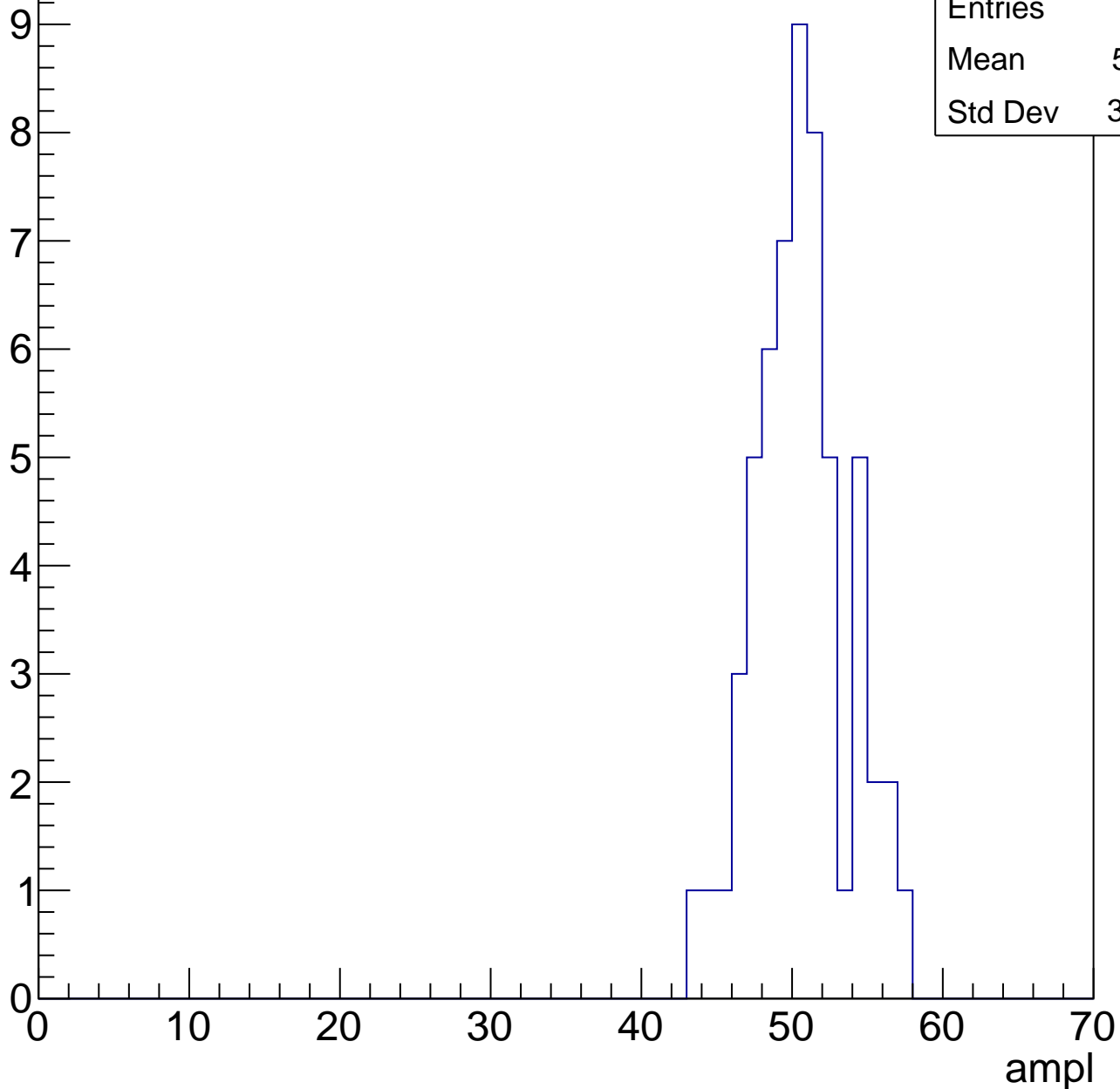
Entries	73
Mean	43.95
Std Dev	3.196

**Gaus mean : 44.2913**  
**Gaus Width: 3.3306**

# B1L000S, U21-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



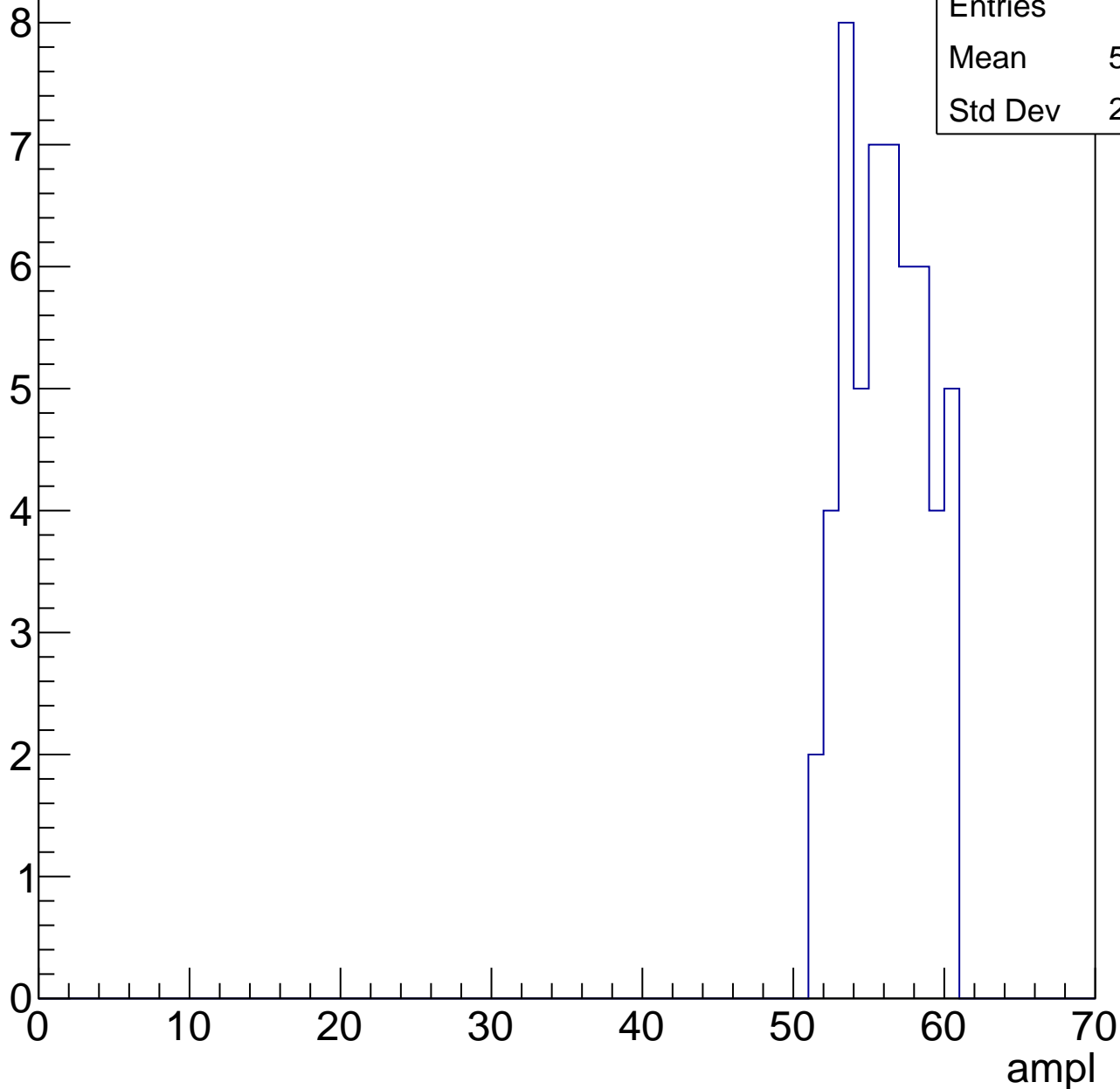
Entries	57
Mean	50.11
Std Dev	3.024

# B1L000S, U21-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	55.69
Std Dev	2.559

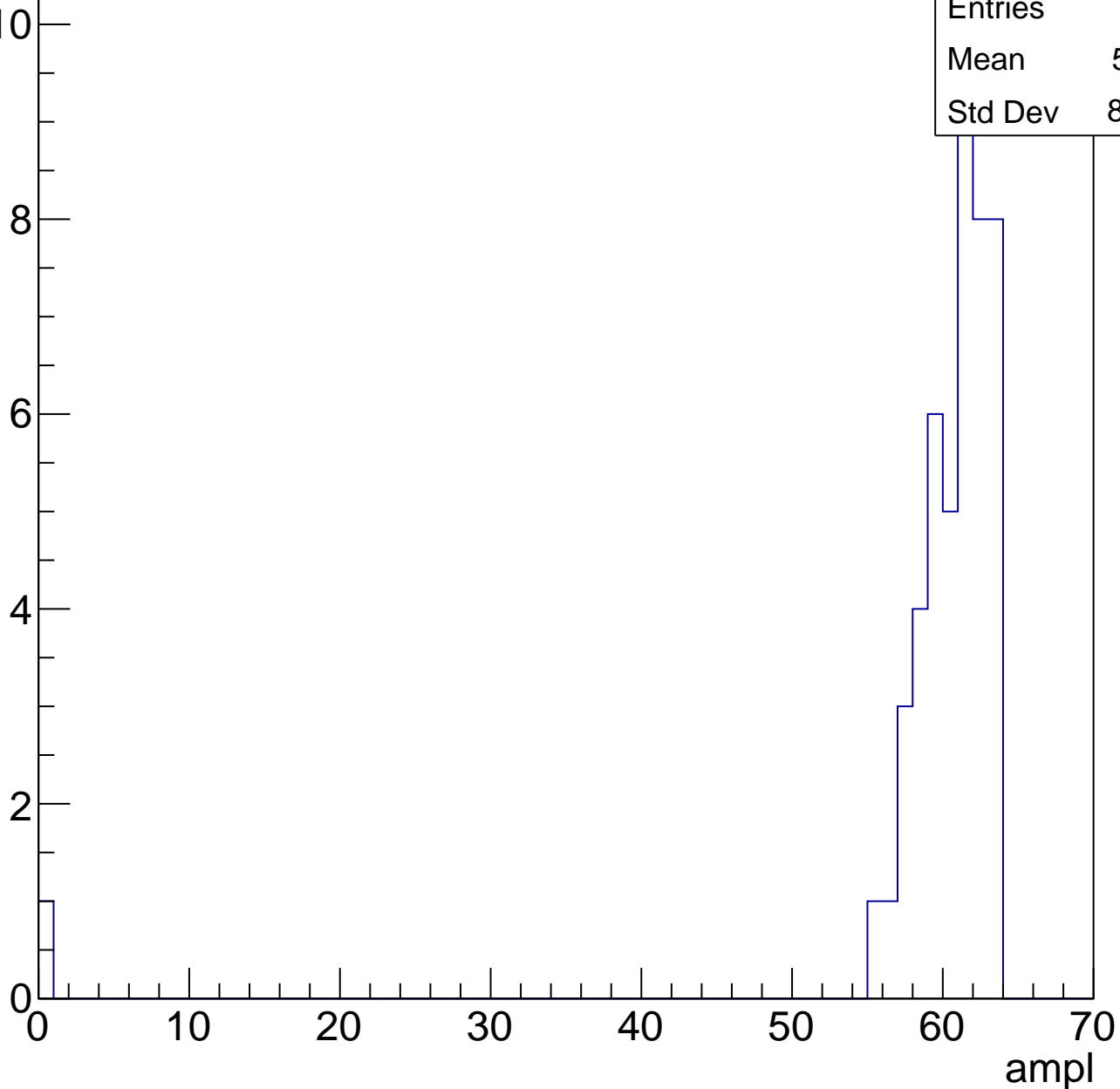


# B1L000S, U21-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	59.11
Std Dev	8.952



# B1L000S, U21-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch14, adc0

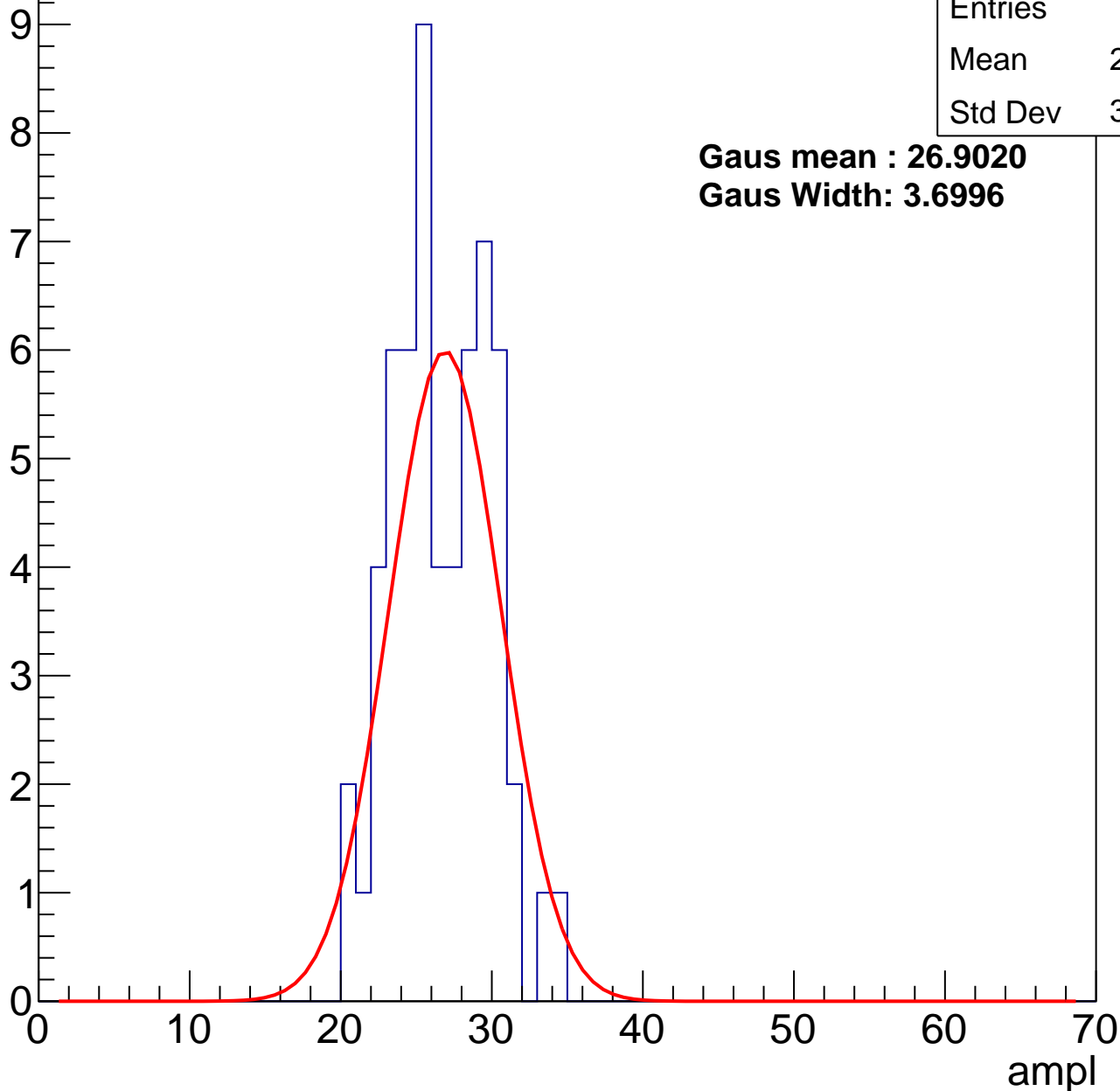
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	26.24
Std Dev	3.175

**Gaus mean : 26.9020**

**Gaus Width: 3.6996**



# B1L000S, U21-ch14, adc1

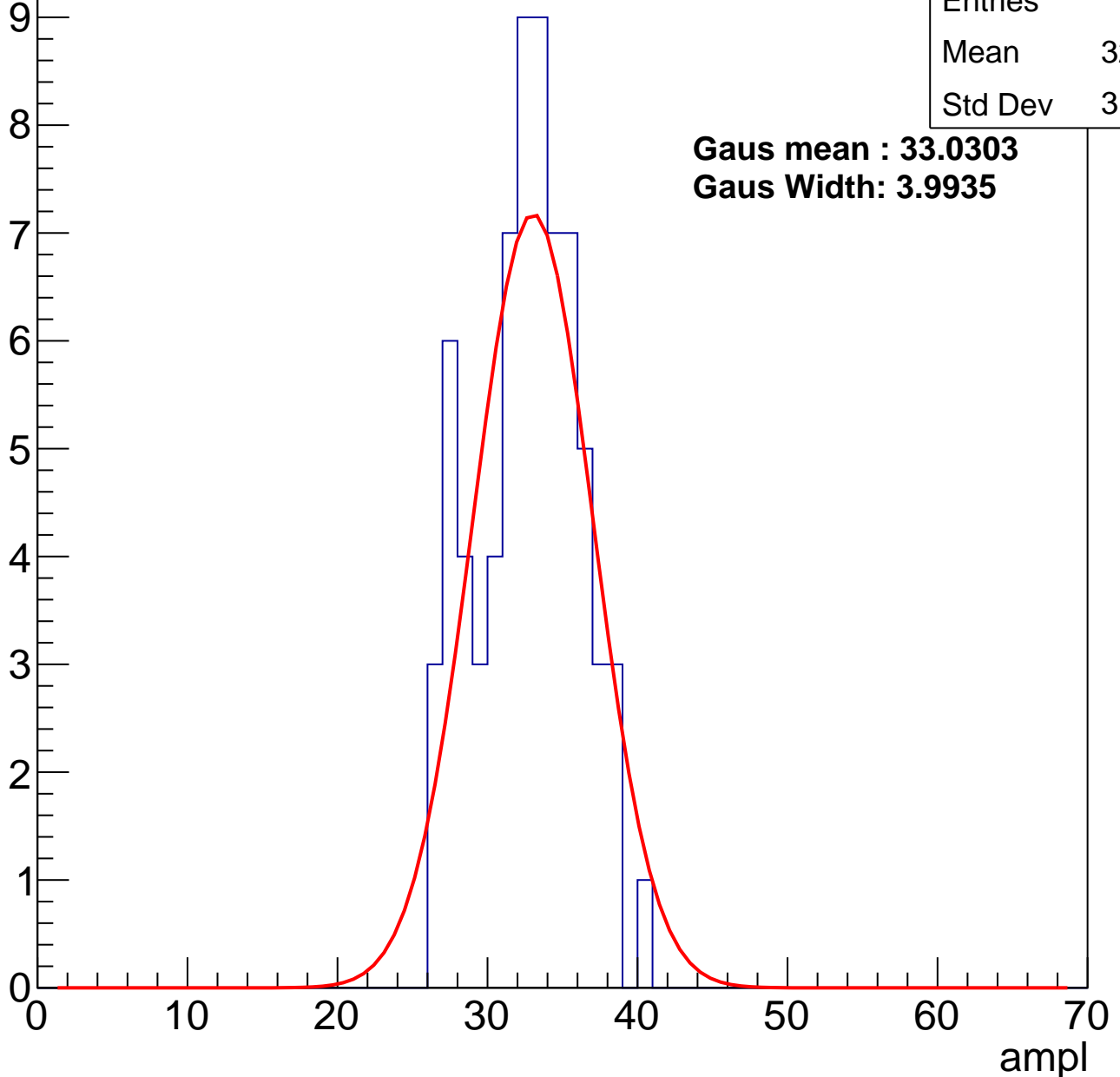
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	32.24
Std Dev	3.346

**Gaus mean : 33.0303**

**Gaus Width: 3.9935**



# B1L000S, U21-ch14, adc2

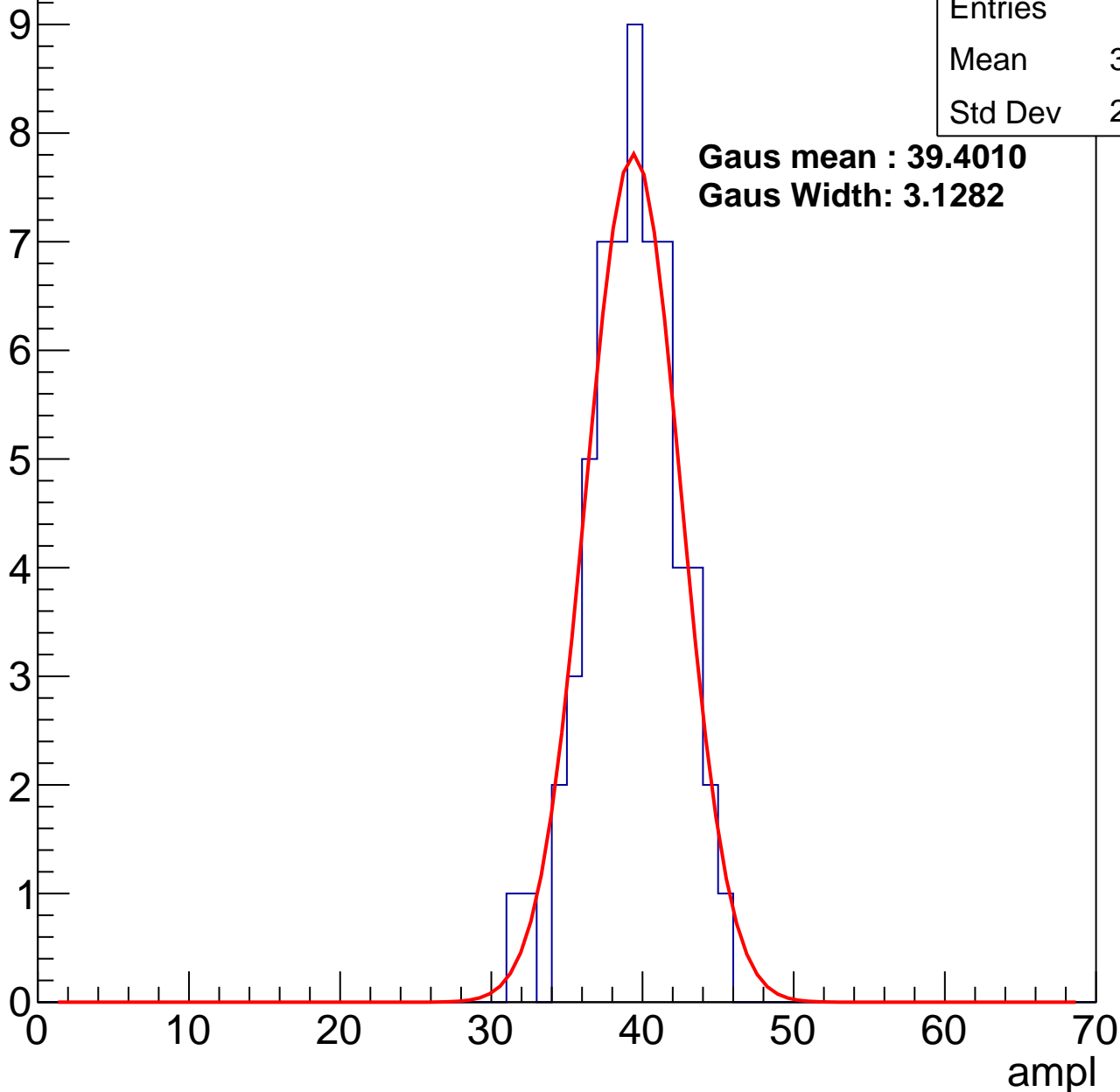
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	38.87
Std Dev	2.918

**Gaus mean : 39.4010**

**Gaus Width: 3.1282**

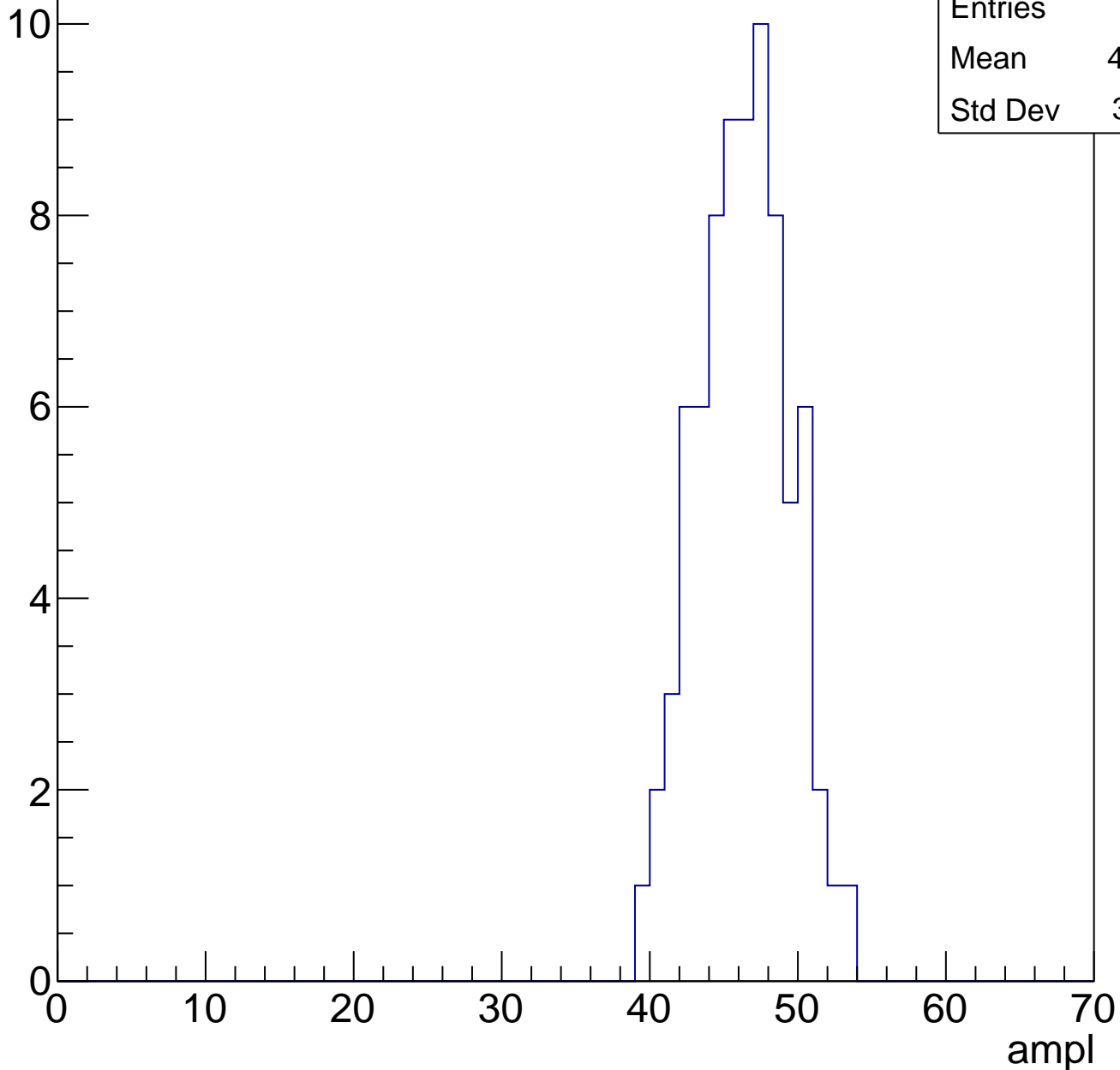


# B1L000S, U21-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	45.83
Std Dev	3.021

Entry

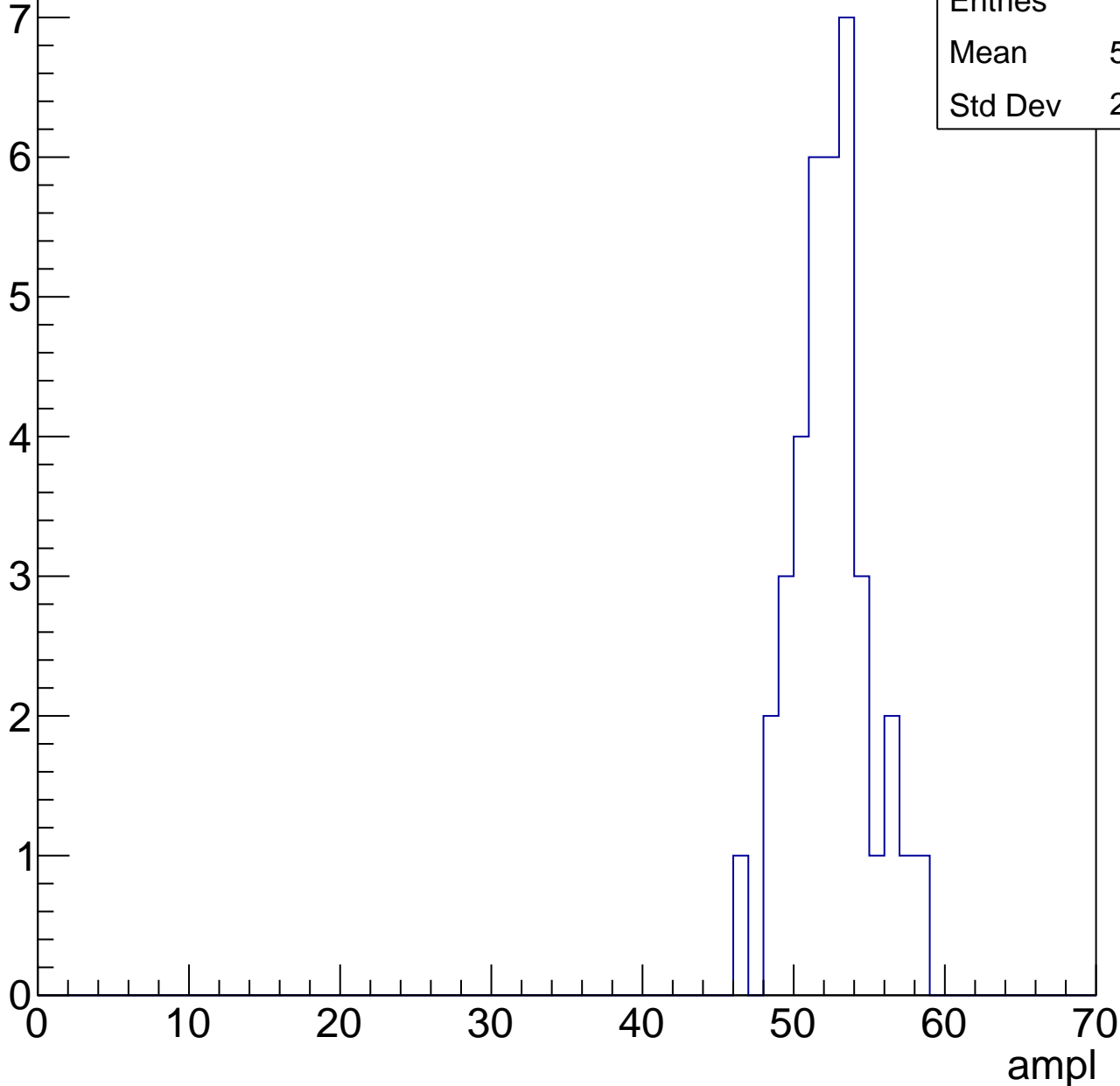


# B1L000S, U21-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	37
Mean	51.95
Std Dev	2.536

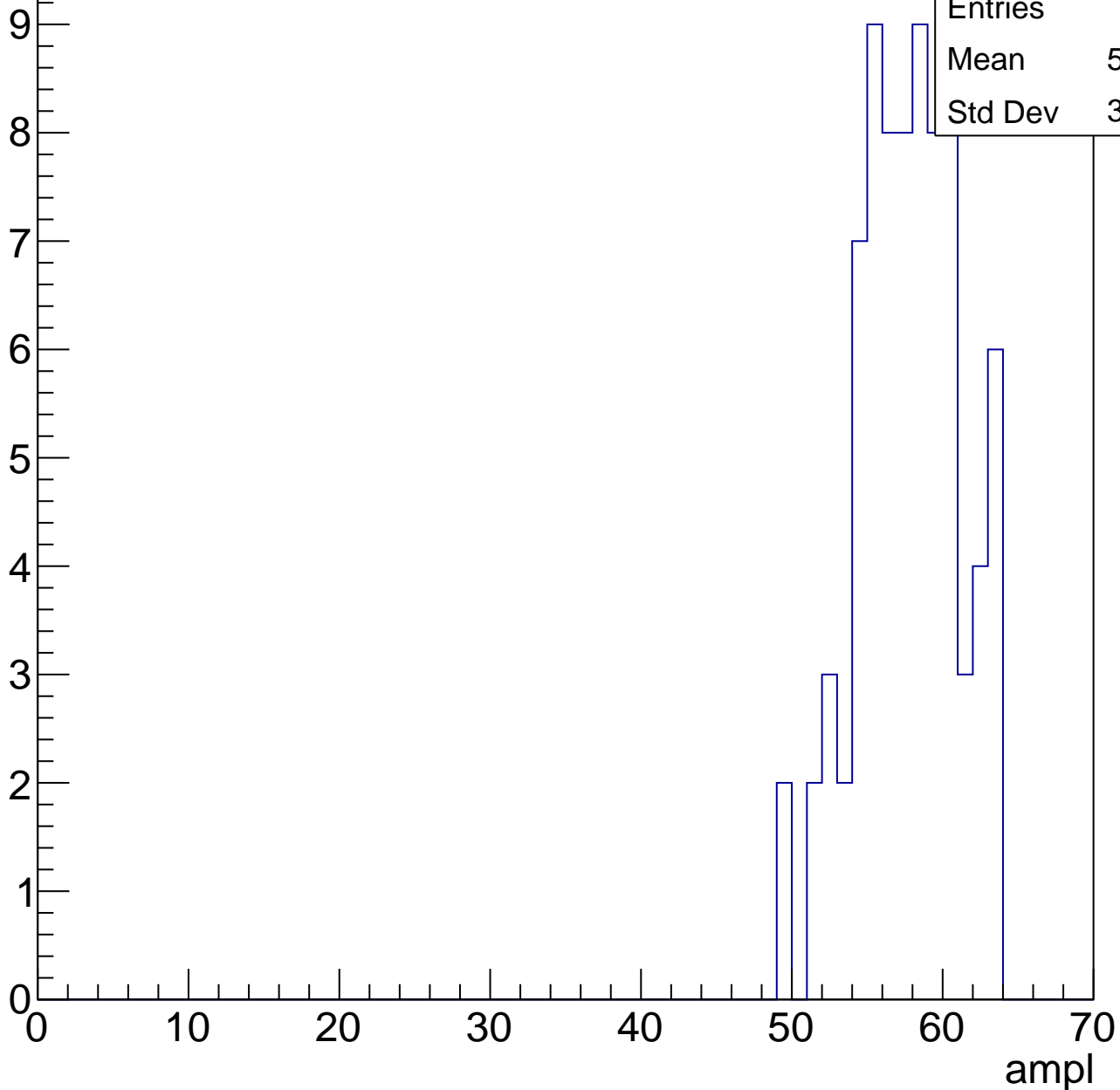


# B1L000S, U21-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	57.27
Std Dev	3.343

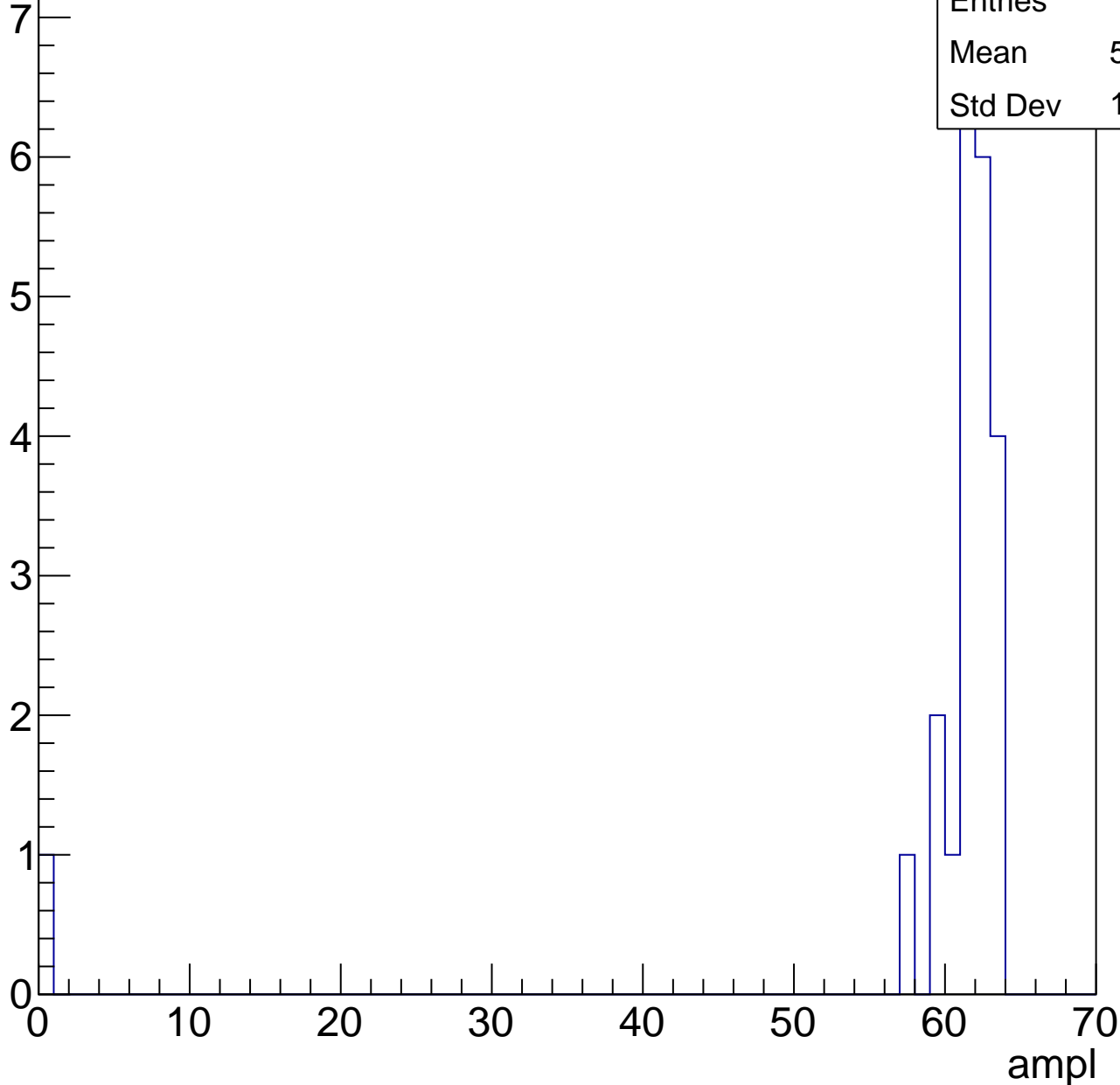


# B1L000S, U21-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.45
Std Dev	12.84





# B1L000S, U21-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U21-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	29.18
Std Dev	5.974

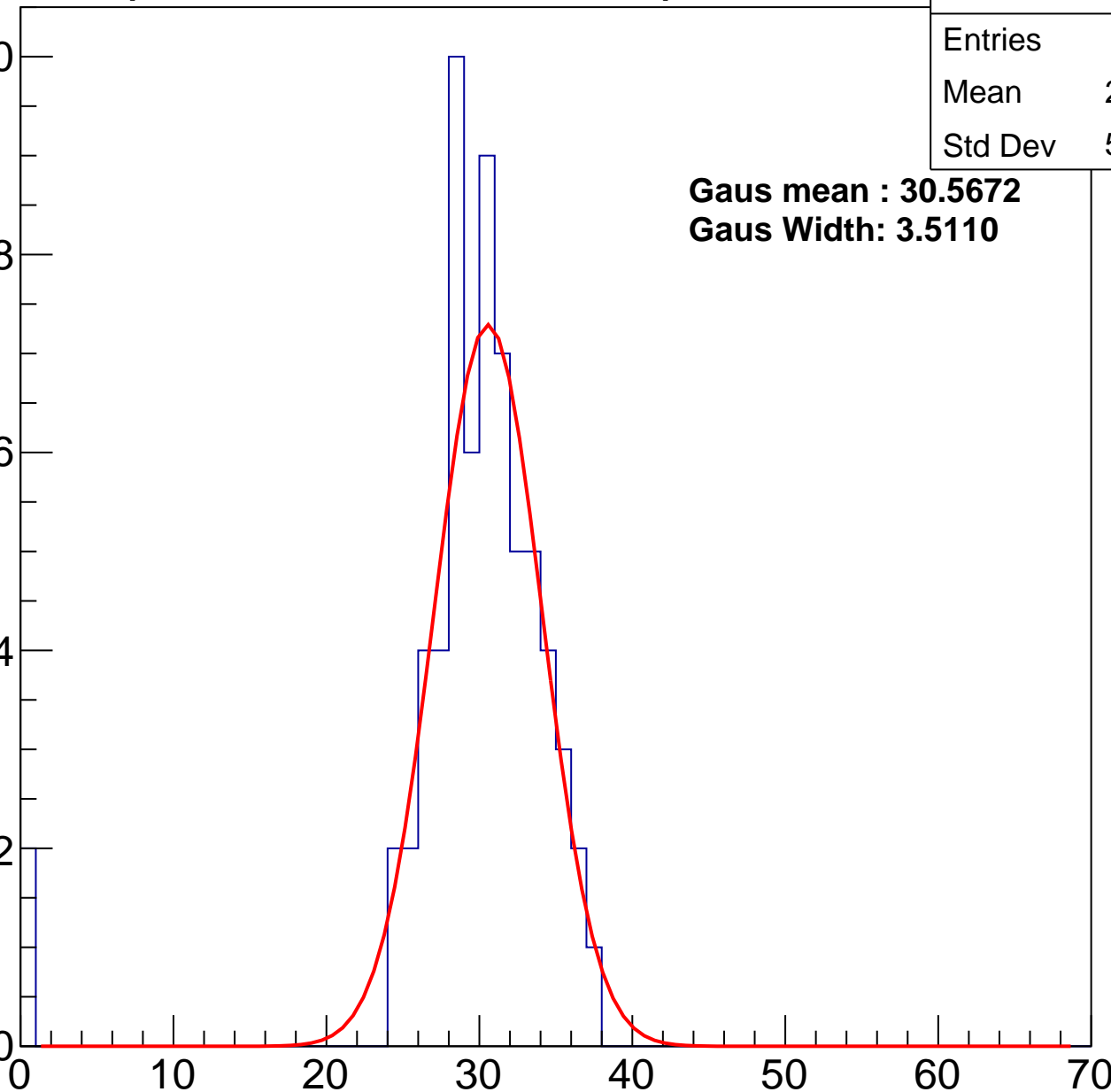
**Gaus mean : 30.5672**

**Gaus Width: 3.5110**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L000S, U21-ch15, adc1

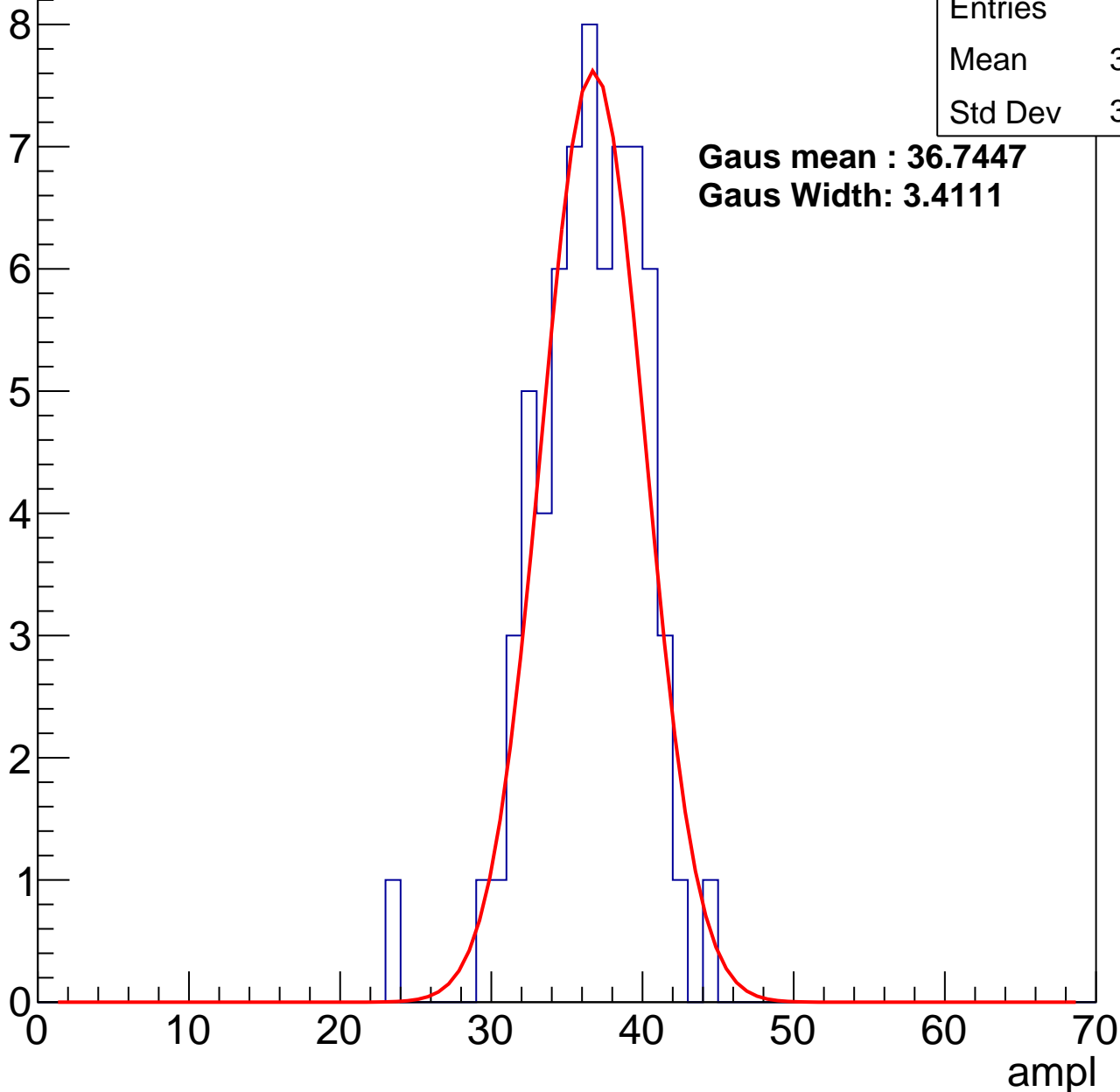
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	36.03
Std Dev	3.549

**Gaus mean : 36.7447**

**Gaus Width: 3.4111**



# B1L000S, U21-ch15, adc2

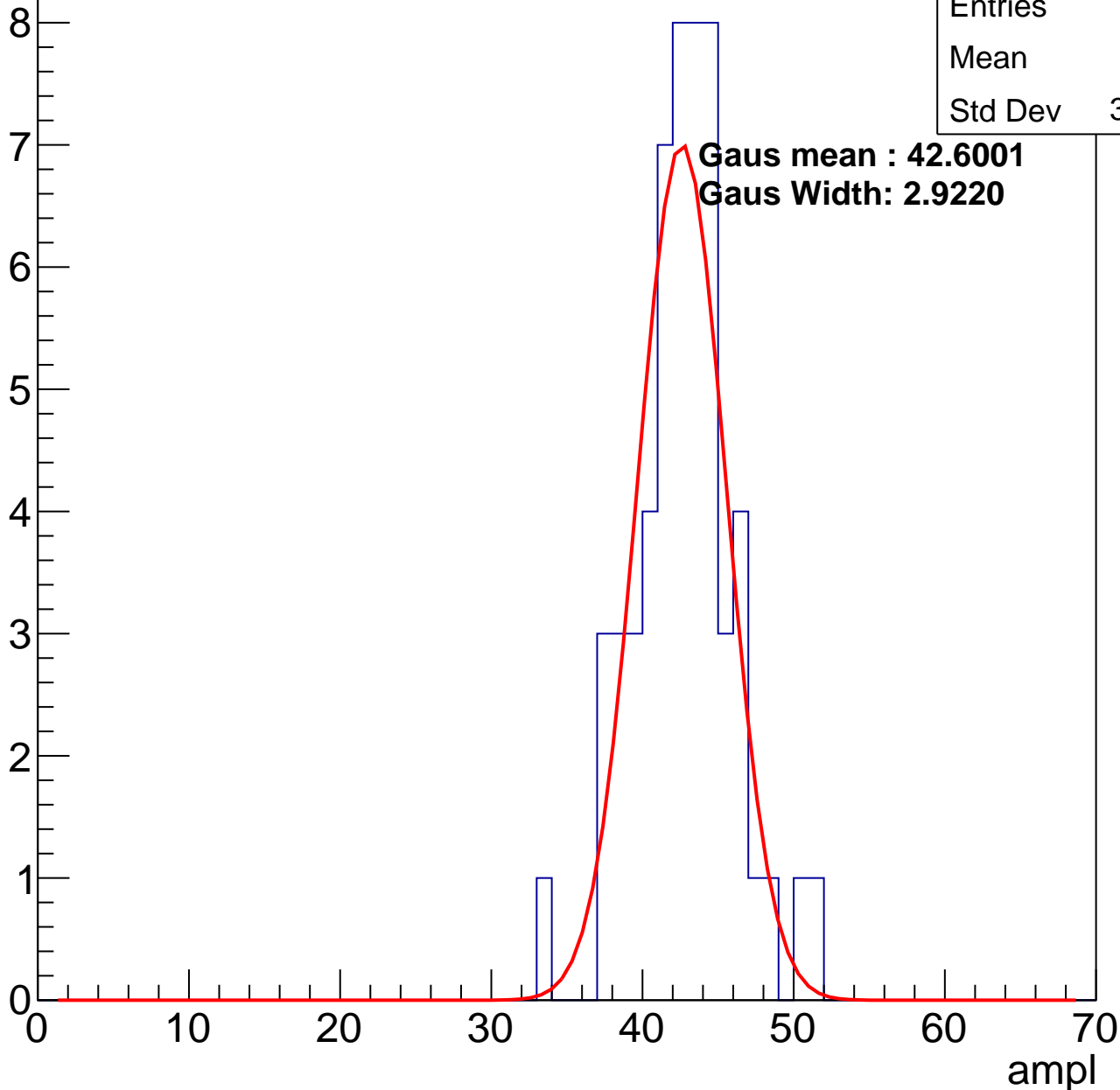
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.3
Std Dev	3.234

**Gaus mean : 42.6001**

**Gaus Width: 2.9220**

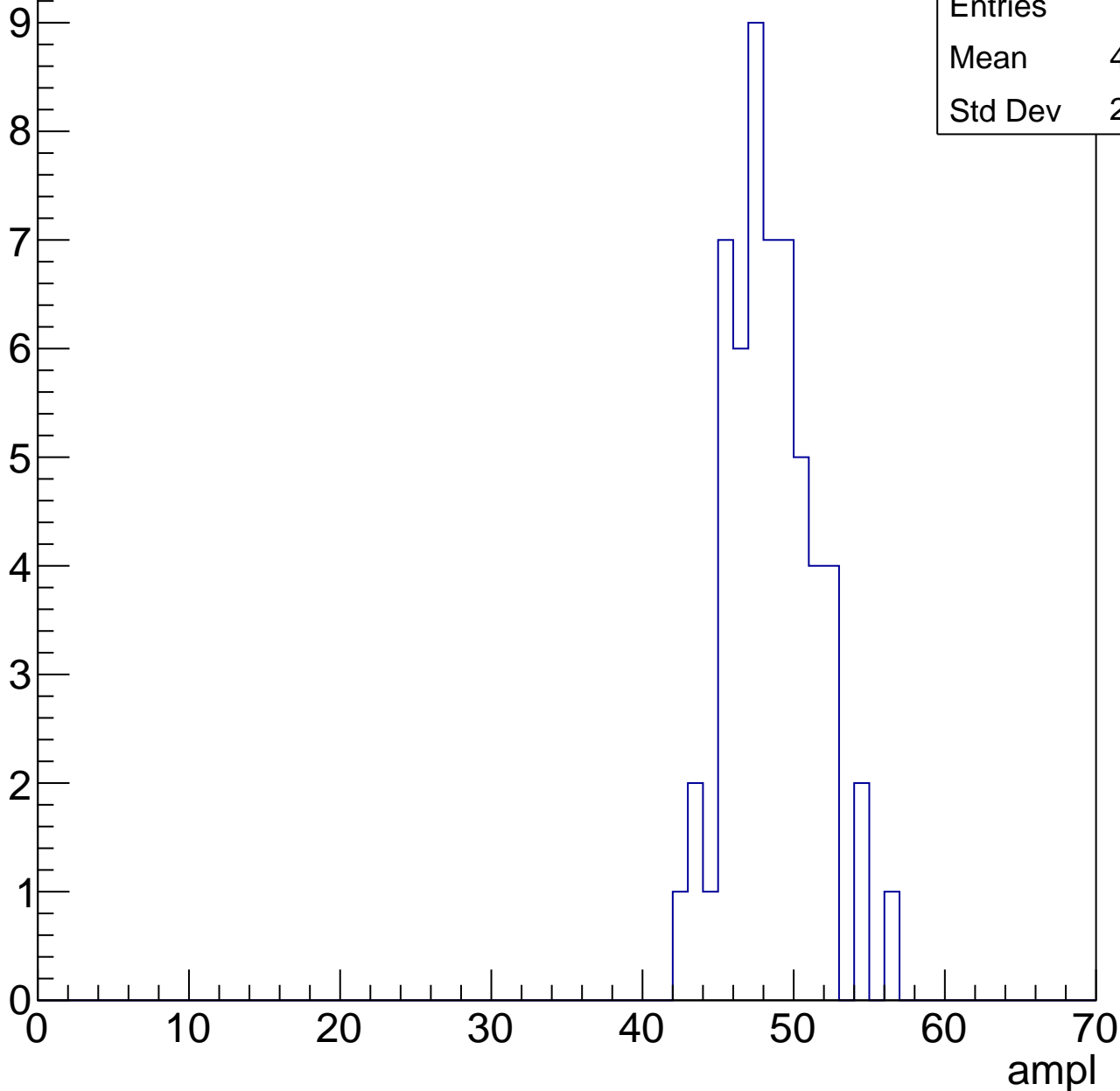


# B1L000S, U21-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	48.05
Std Dev	2.869



# B1L000S, U21-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

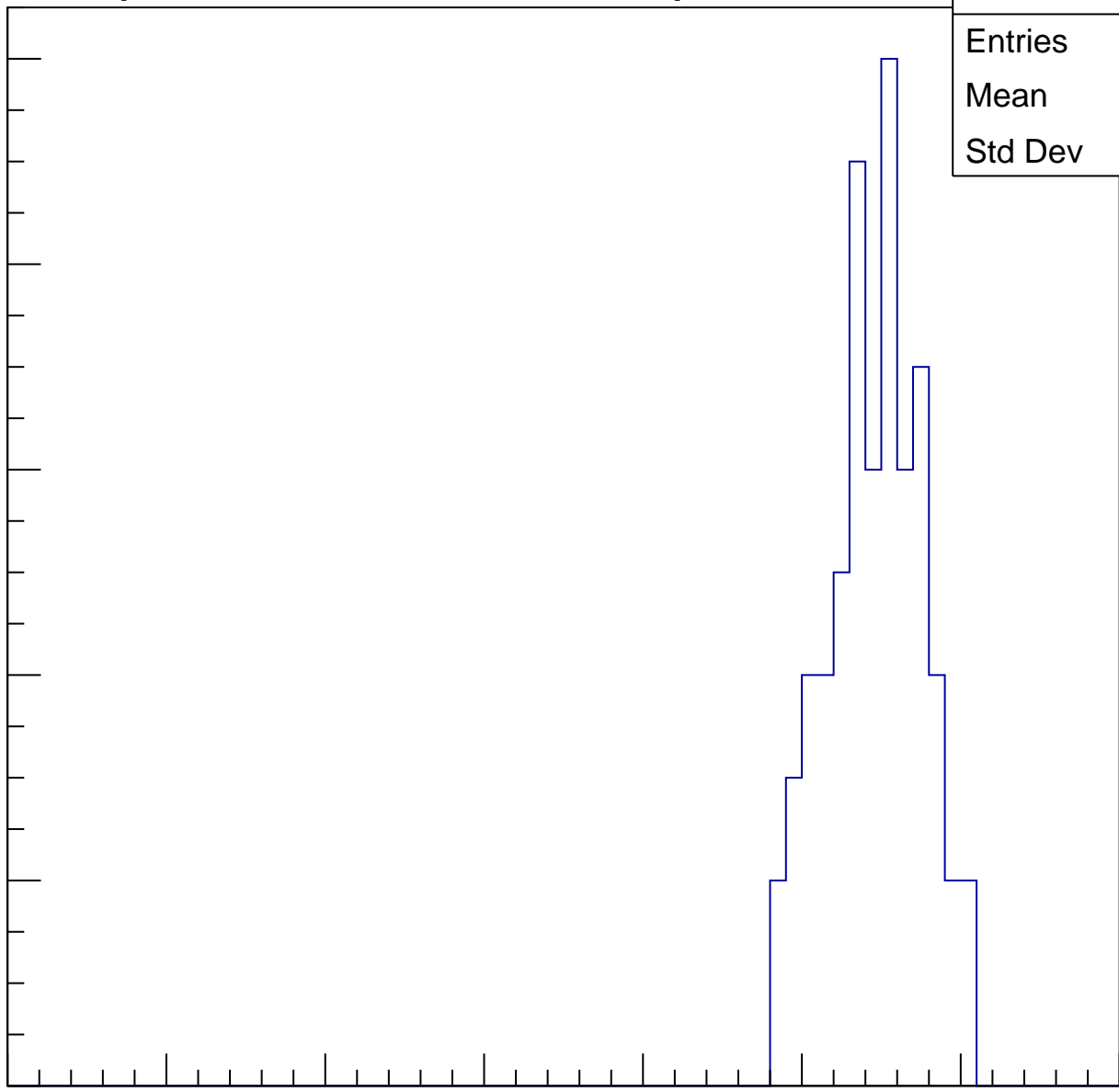
Entries	64
Mean	54.11
Std Dev	2.953

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

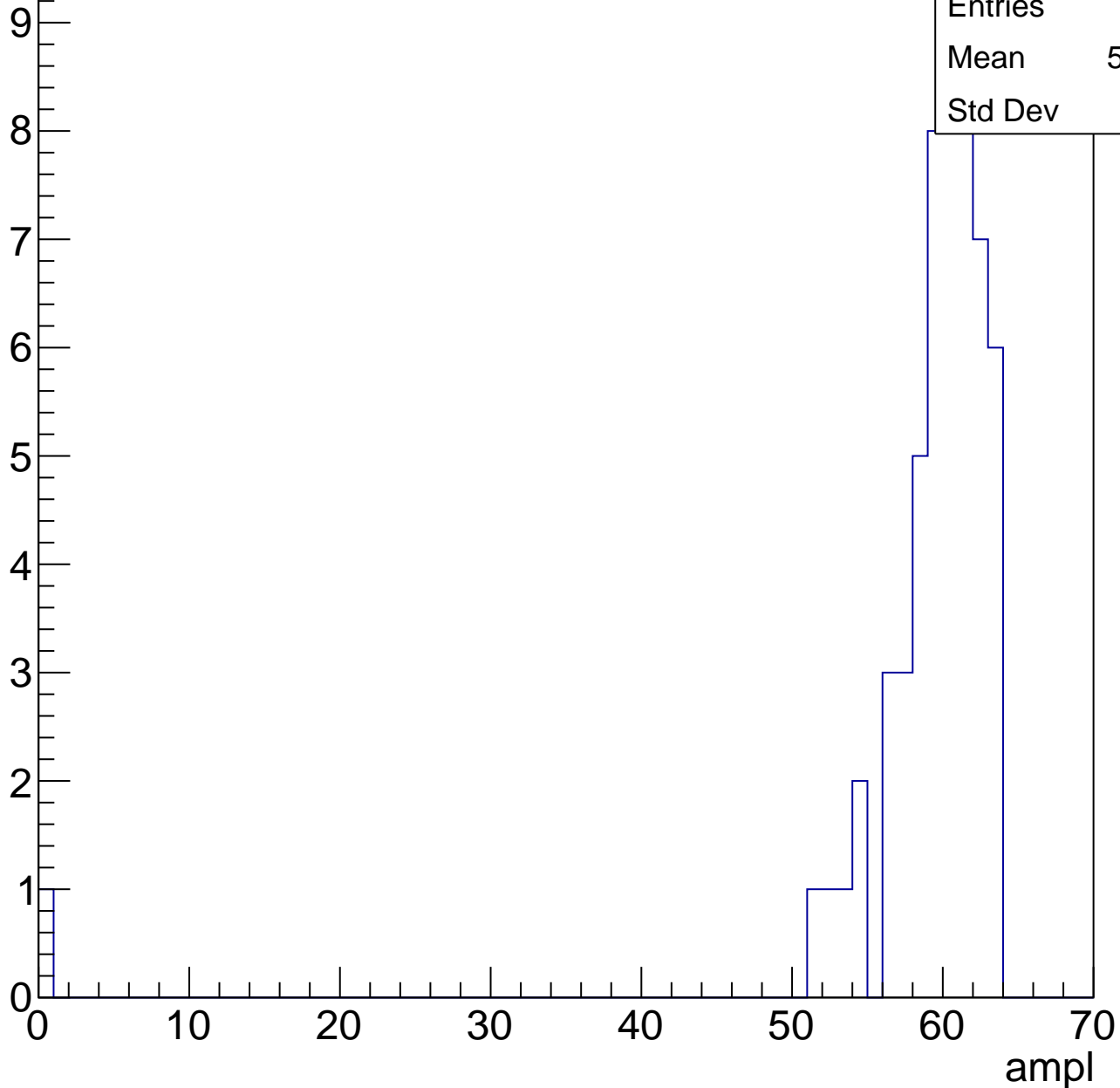
ampl



# B1L000S, U21-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

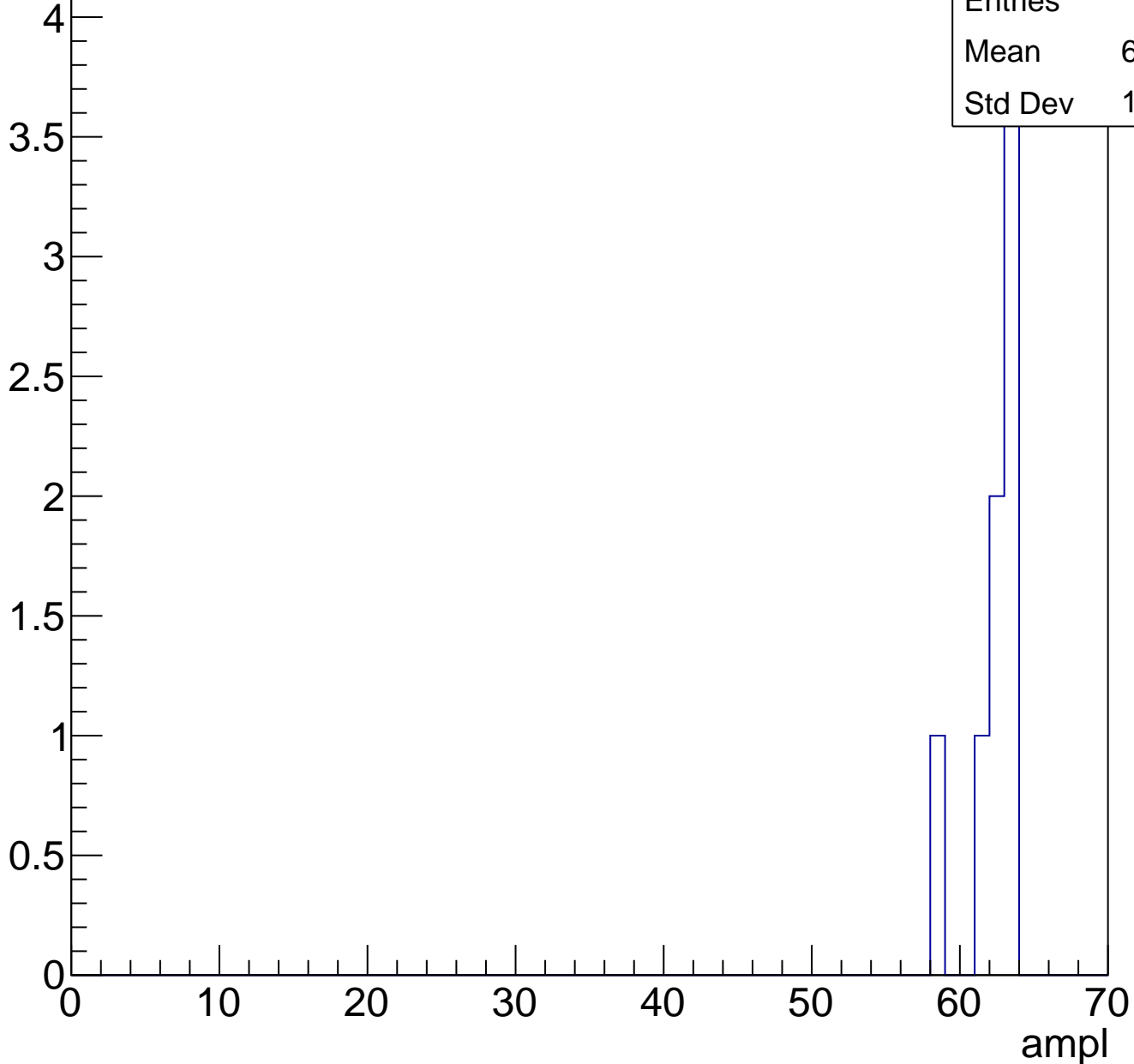
Entry



# B1L000S, U21-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

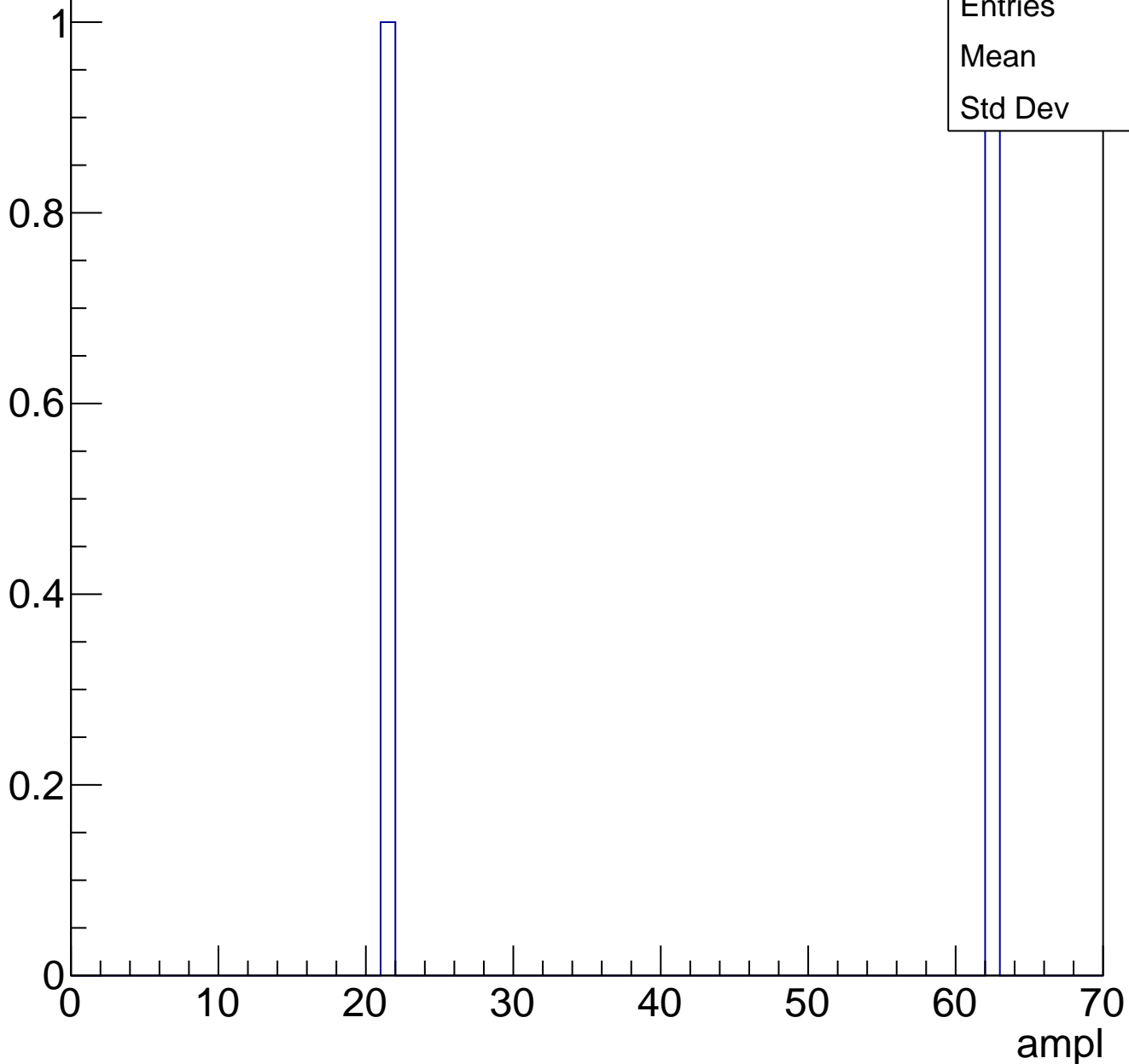




# B1L000S, U21-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch16, adc0

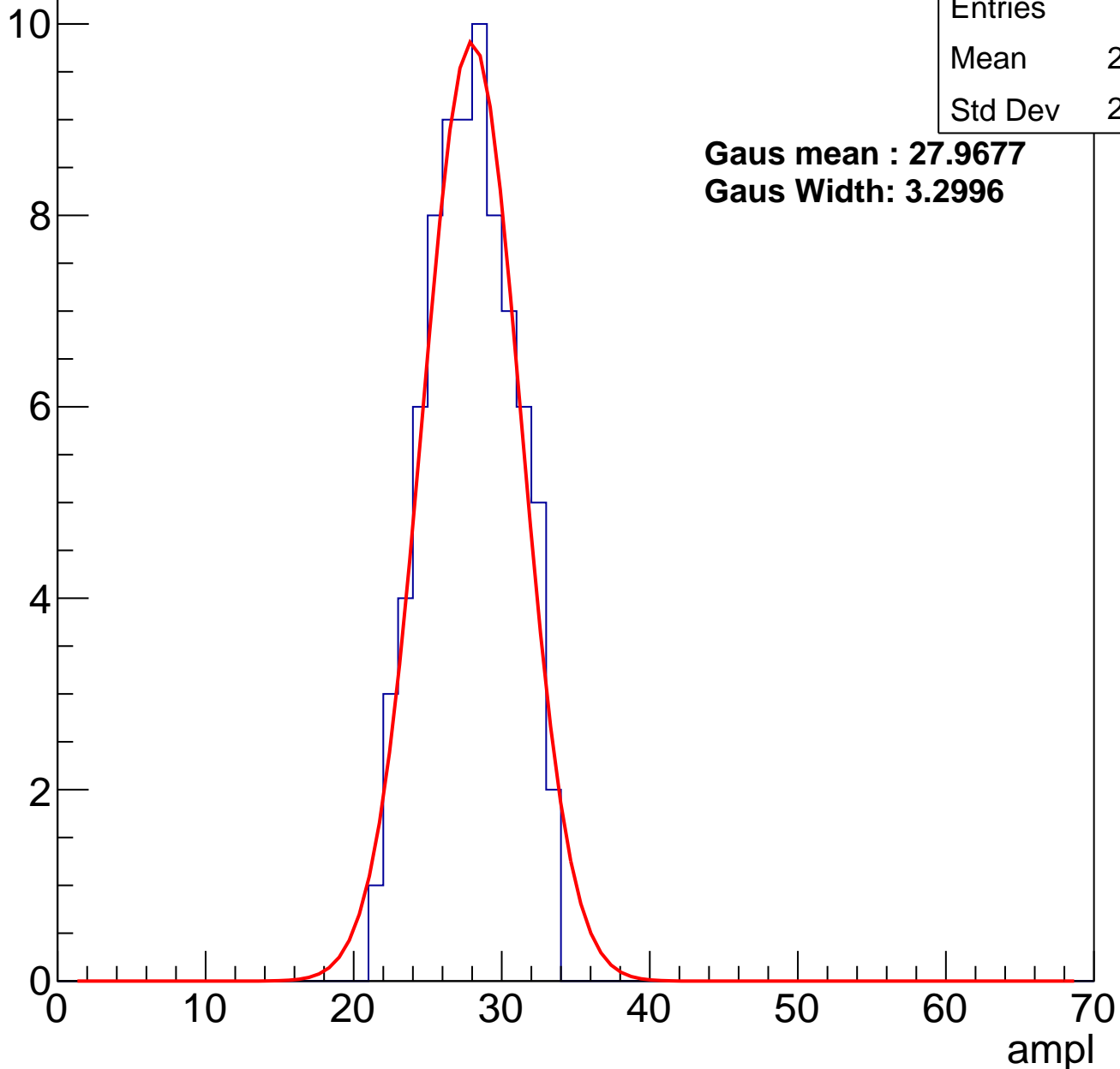
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	27.36
Std Dev	2.904

**Gaus mean : 27.9677**

**Gaus Width: 3.2996**

Entry

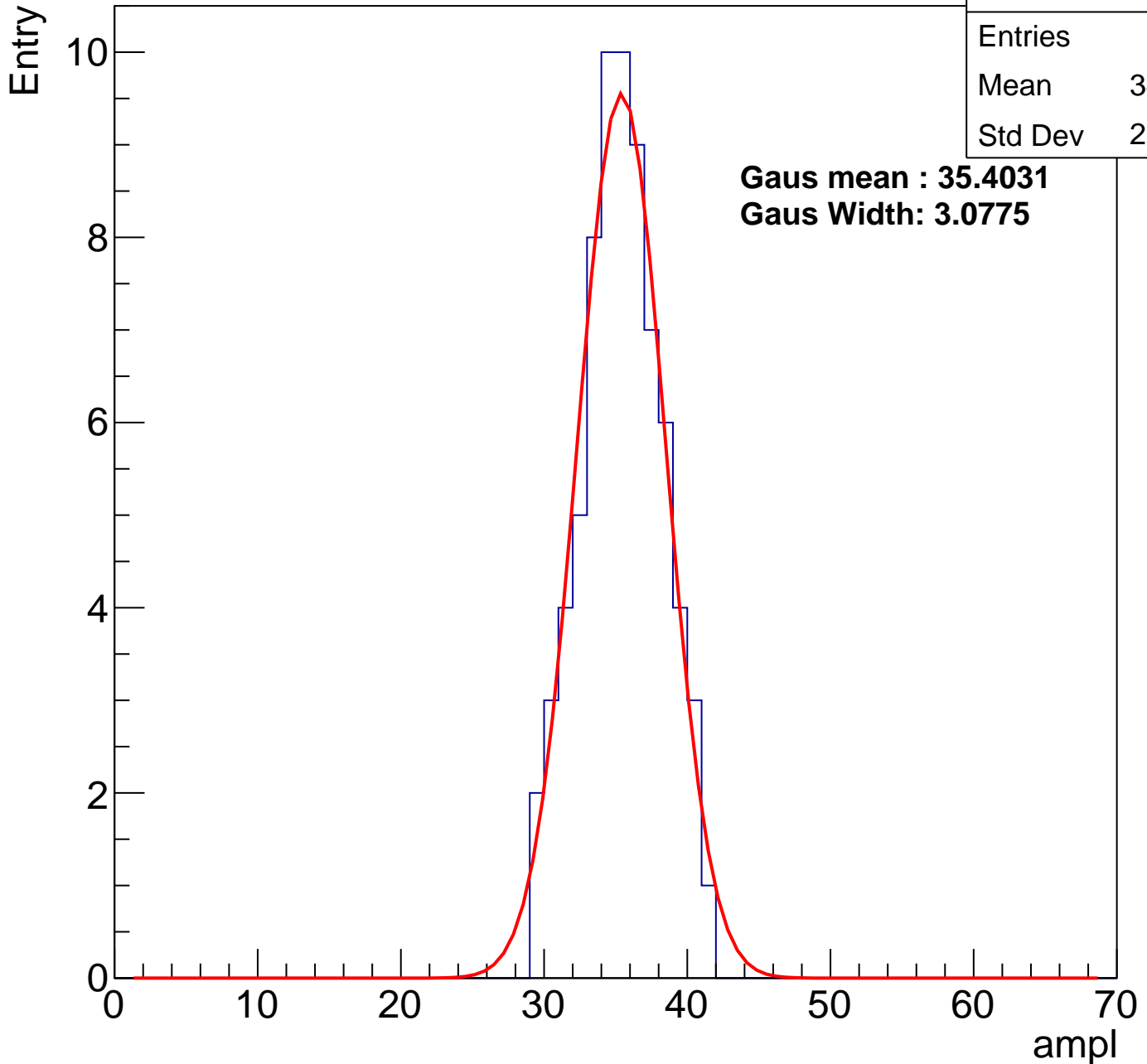


# B1L000S, U21-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	34.92
Std Dev	2.798

**Gaus mean : 35.4031**  
**Gaus Width: 3.0775**



# B1L000S, U21-ch16, adc2

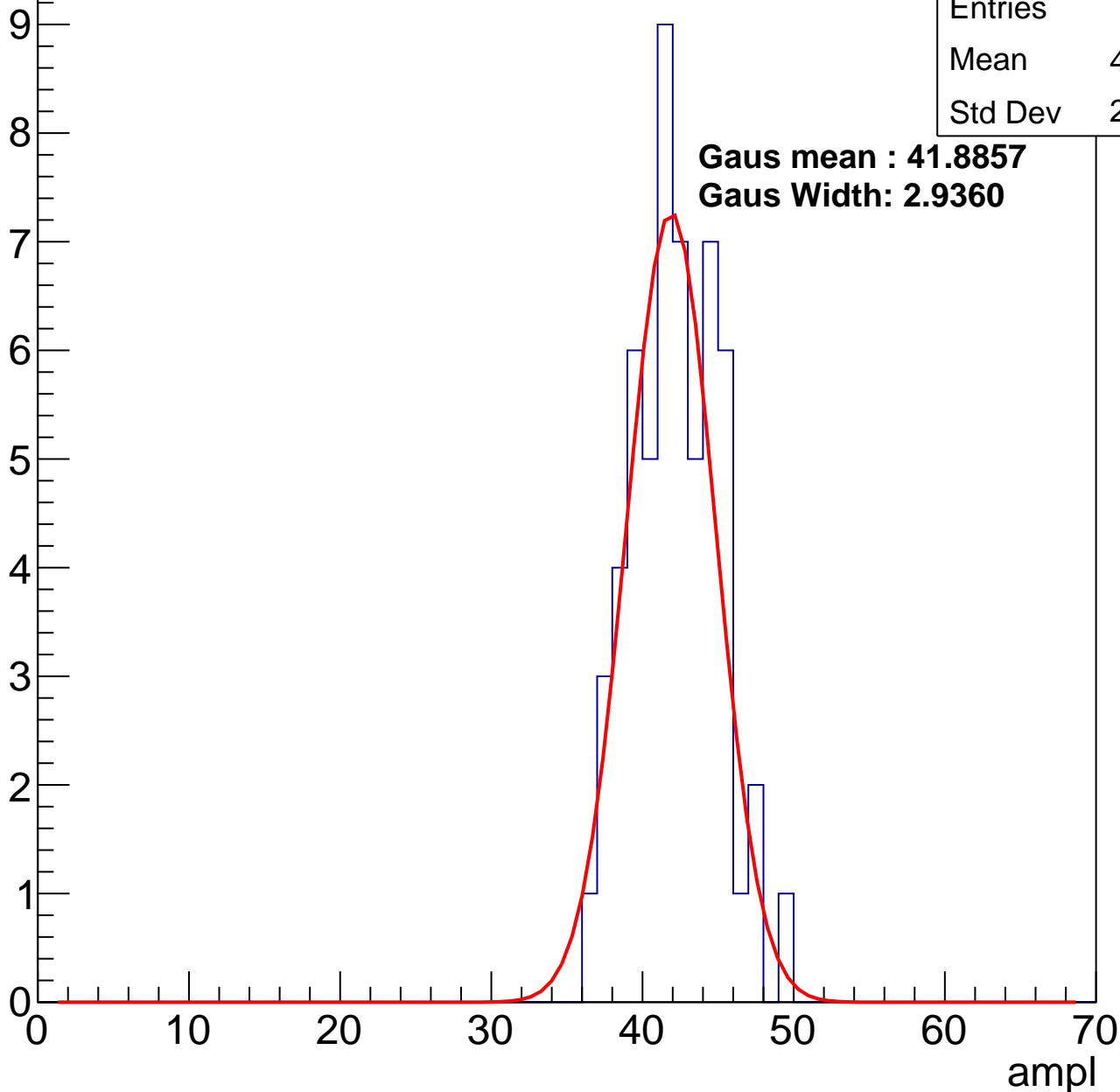
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.72
Std Dev	2.827

**Gaus mean : 41.8857**

**Gaus Width: 2.9360**

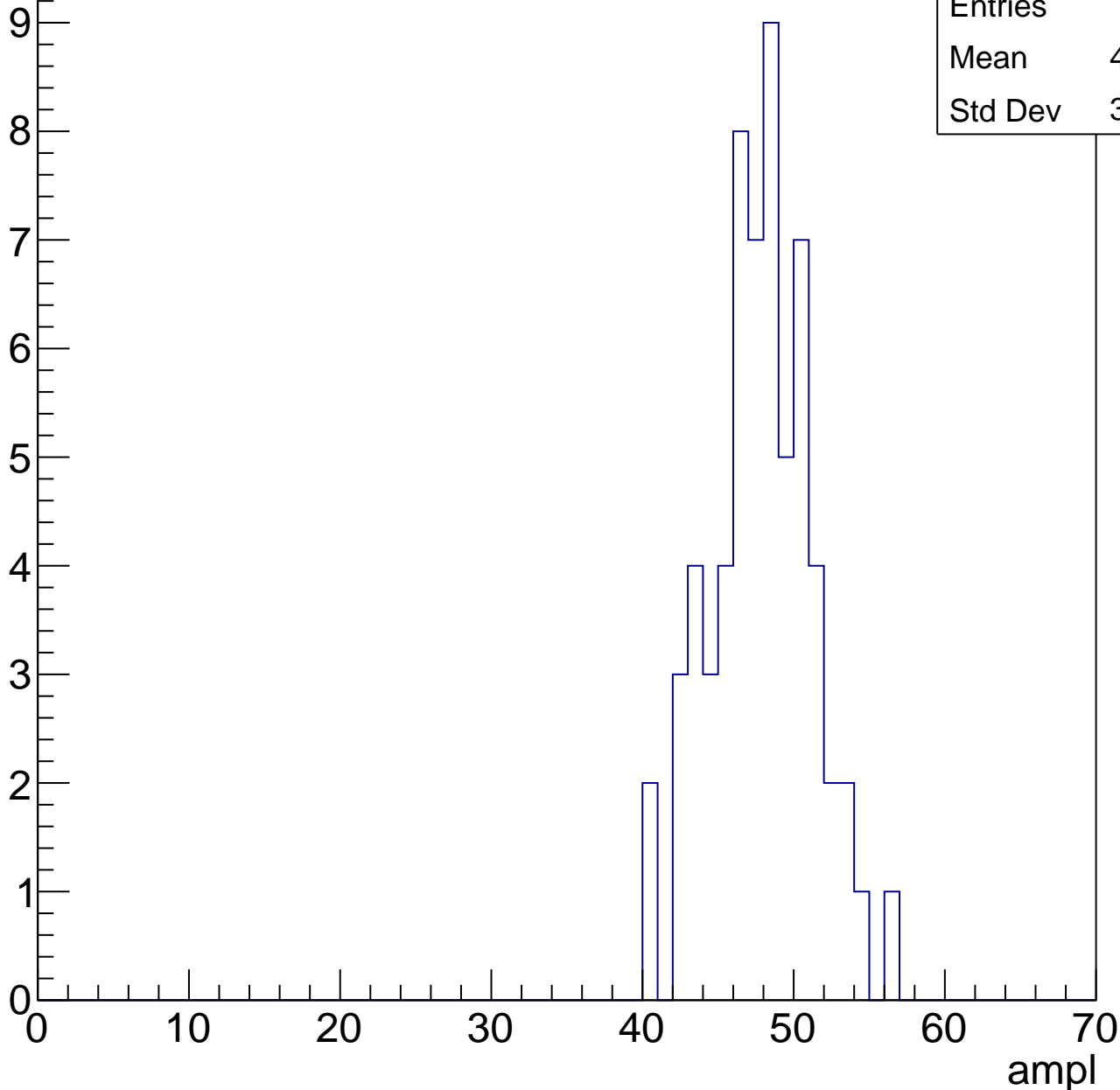


# B1L000S, U21-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	47.39
Std Dev	3.328

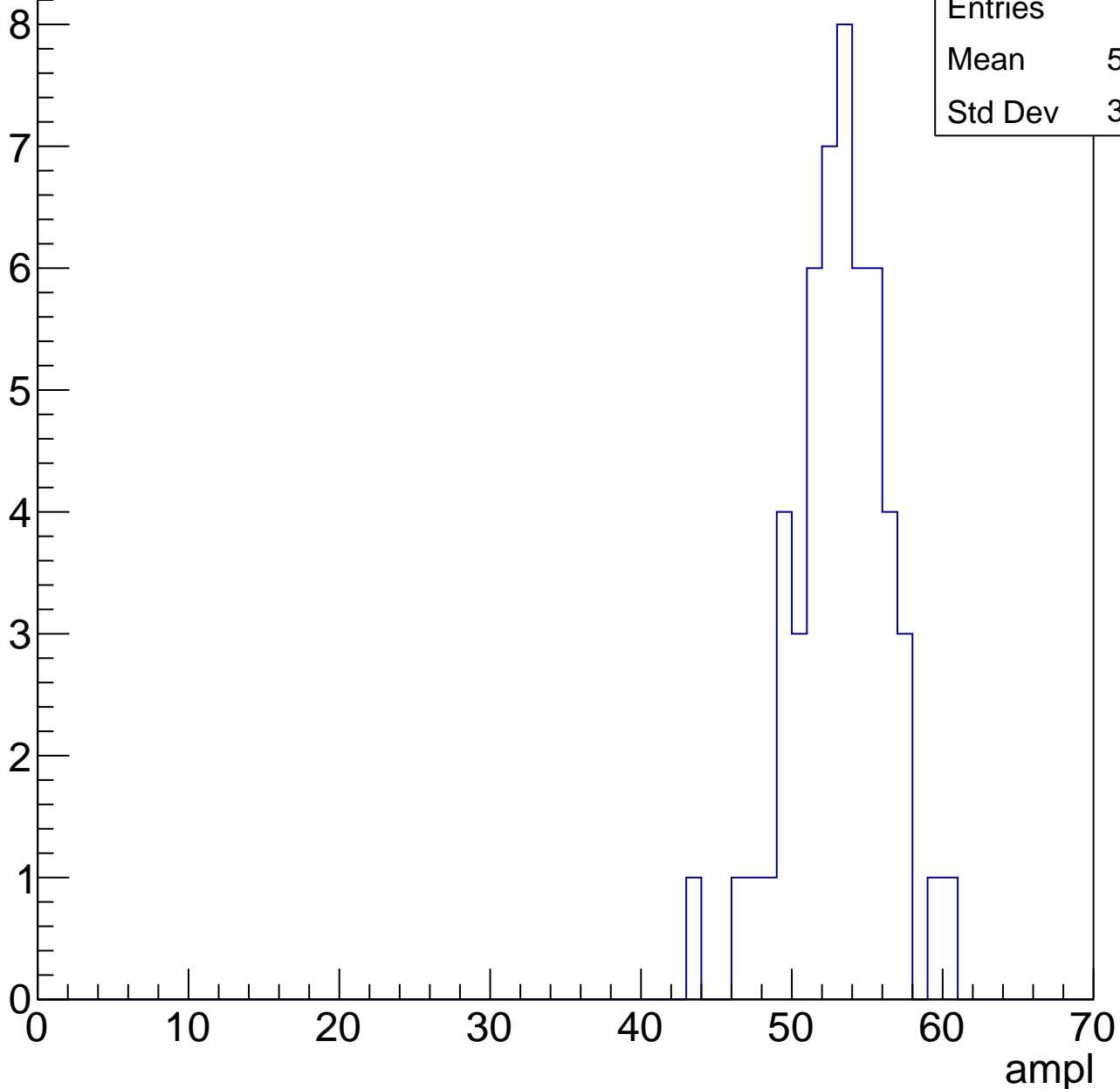


# B1L000S, U21-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

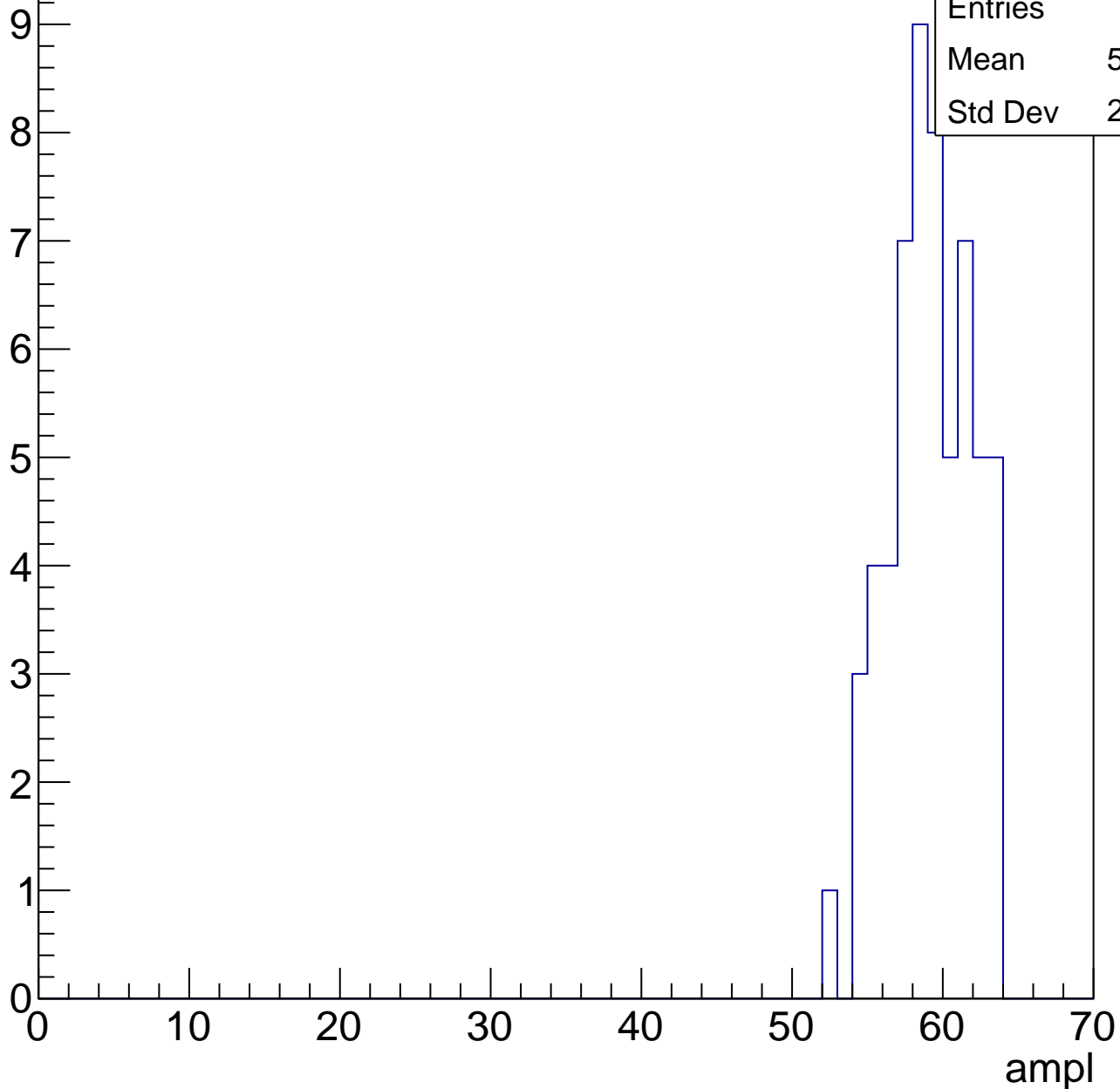
Entries	53
Mean	52.68
Std Dev	3.149



# B1L000S, U21-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



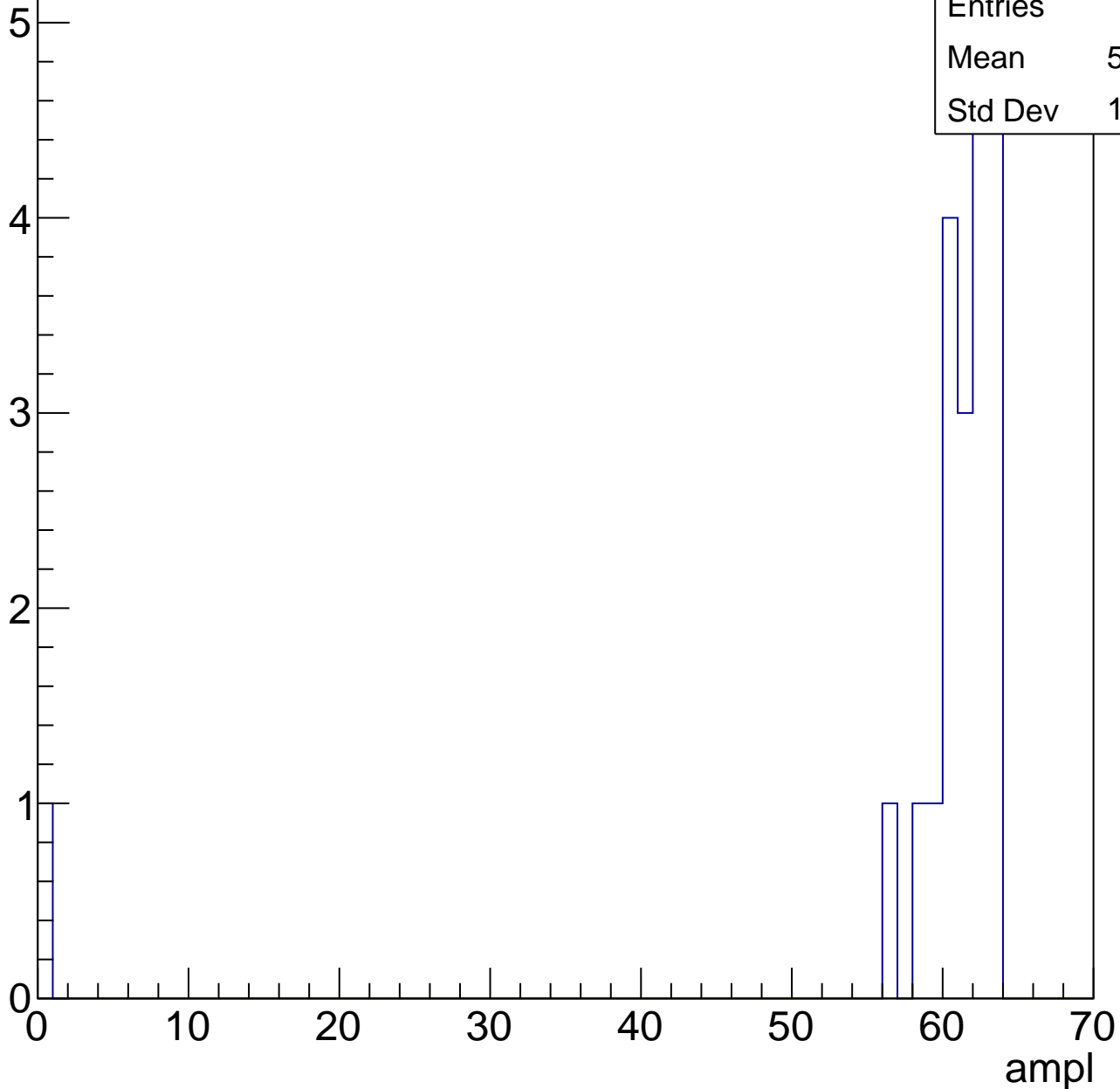
Entries	58
Mean	58.67
Std Dev	2.668

# B1L000S, U21-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.14
Std Dev	13.12





# B1L000S, U21-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch17, adc0

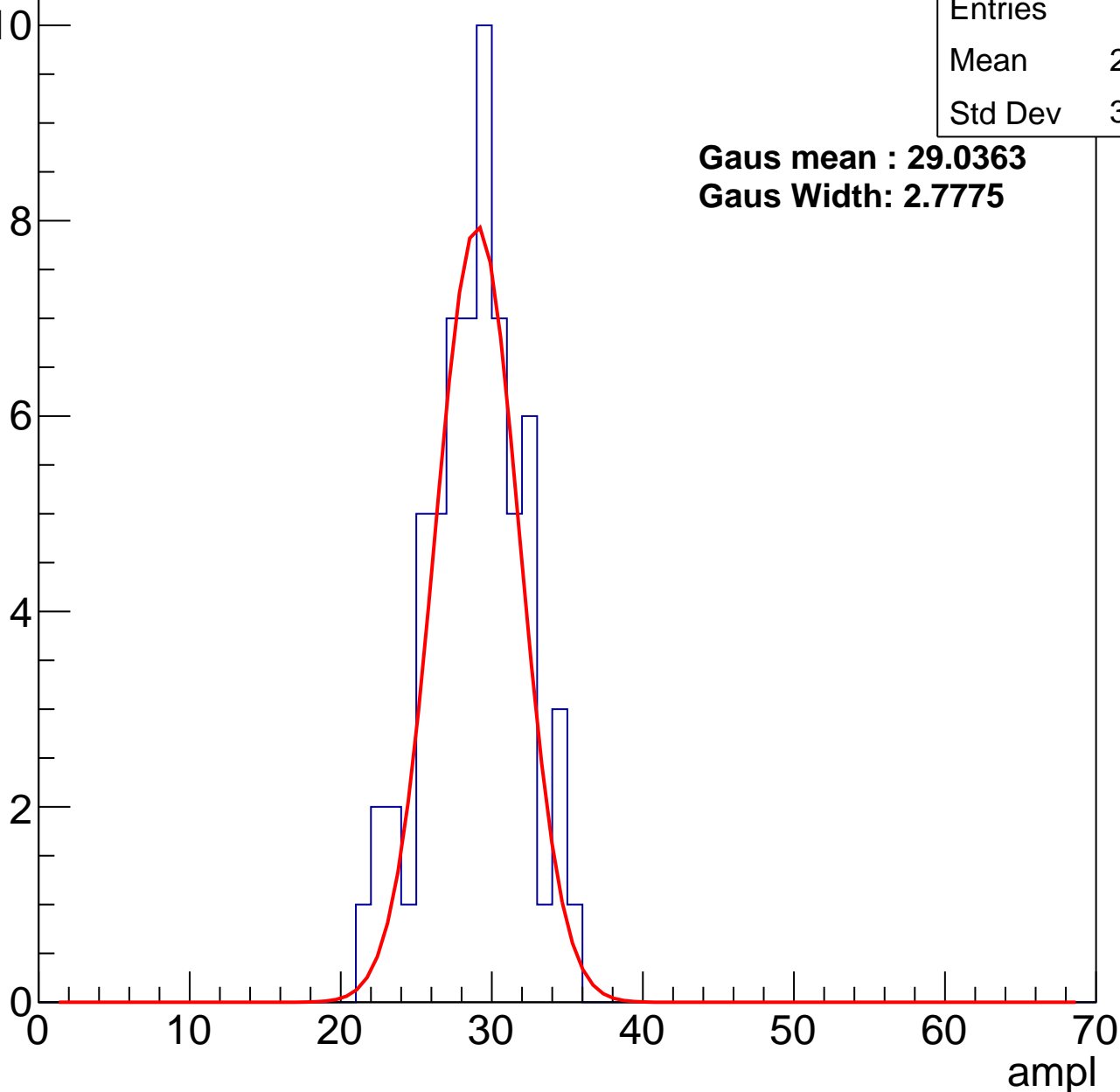
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	28.44
Std Dev	3.105

**Gaus mean : 29.0363**

**Gaus Width: 2.7775**



# B1L000S, U21-ch17, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	35.46
Std Dev	3.118

**Gaus mean : 35.7869**

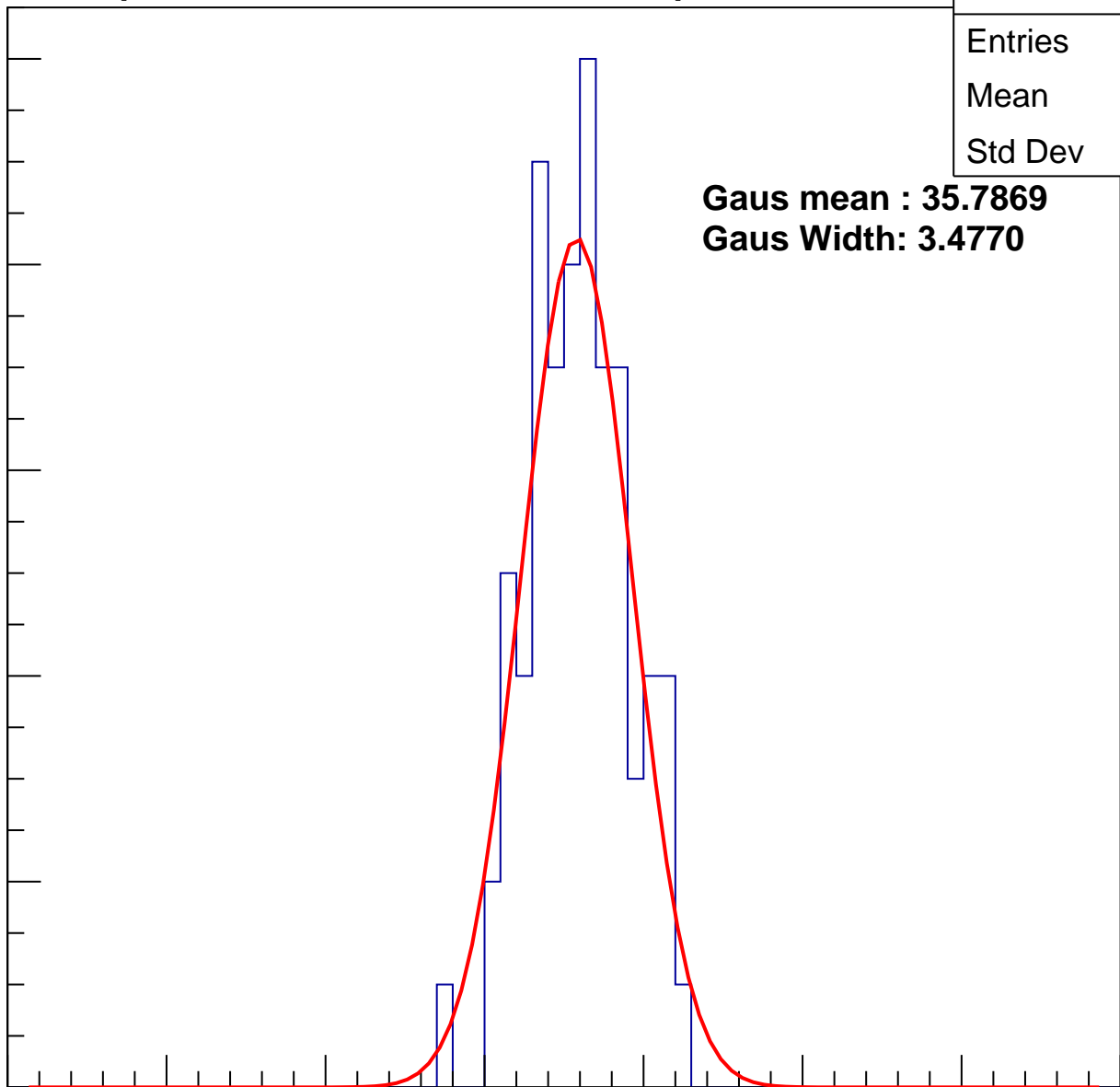
**Gaus Width: 3.4770**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



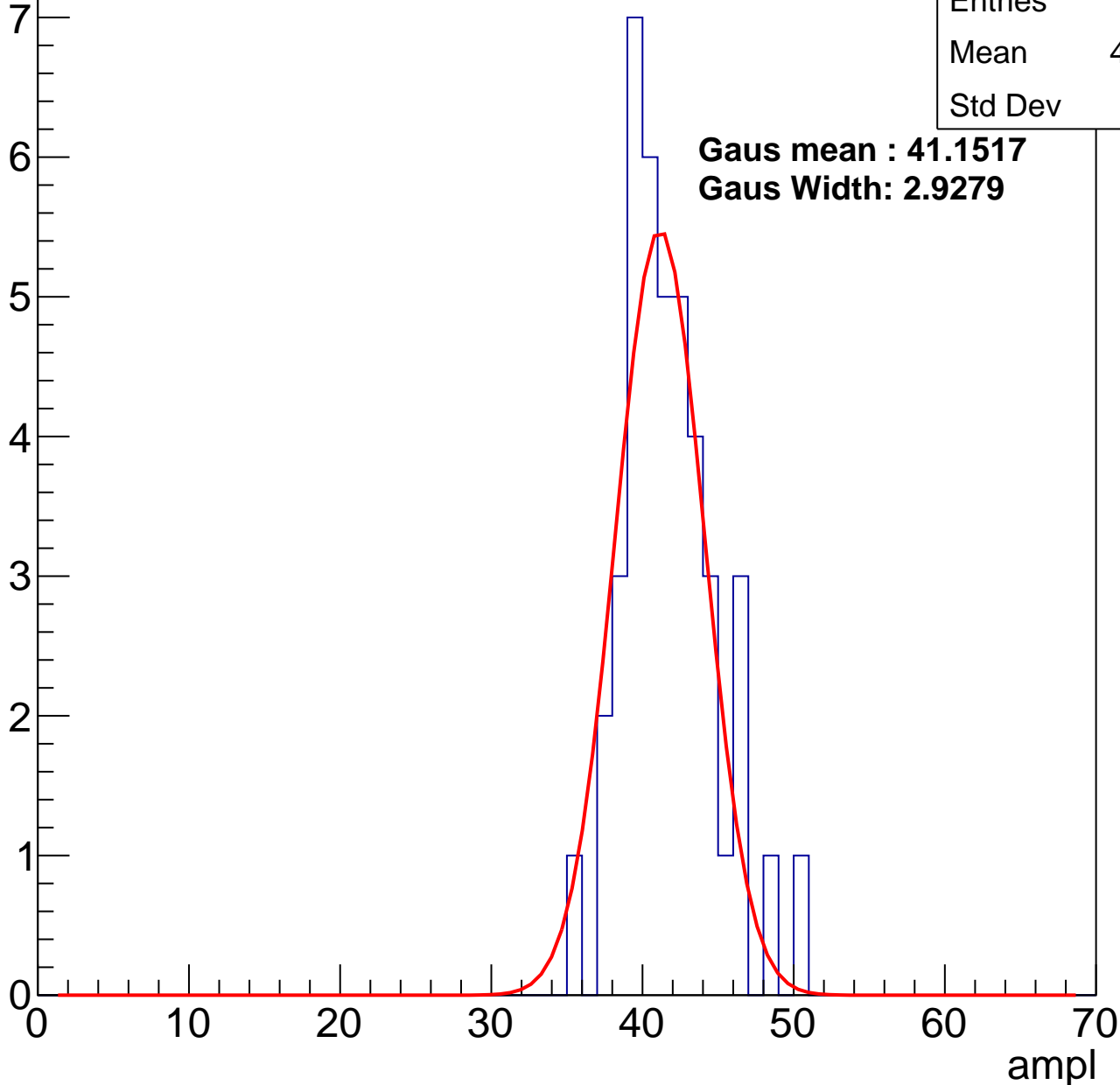
# B1L000S, U21-ch17, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	41.33
Std Dev	3.06

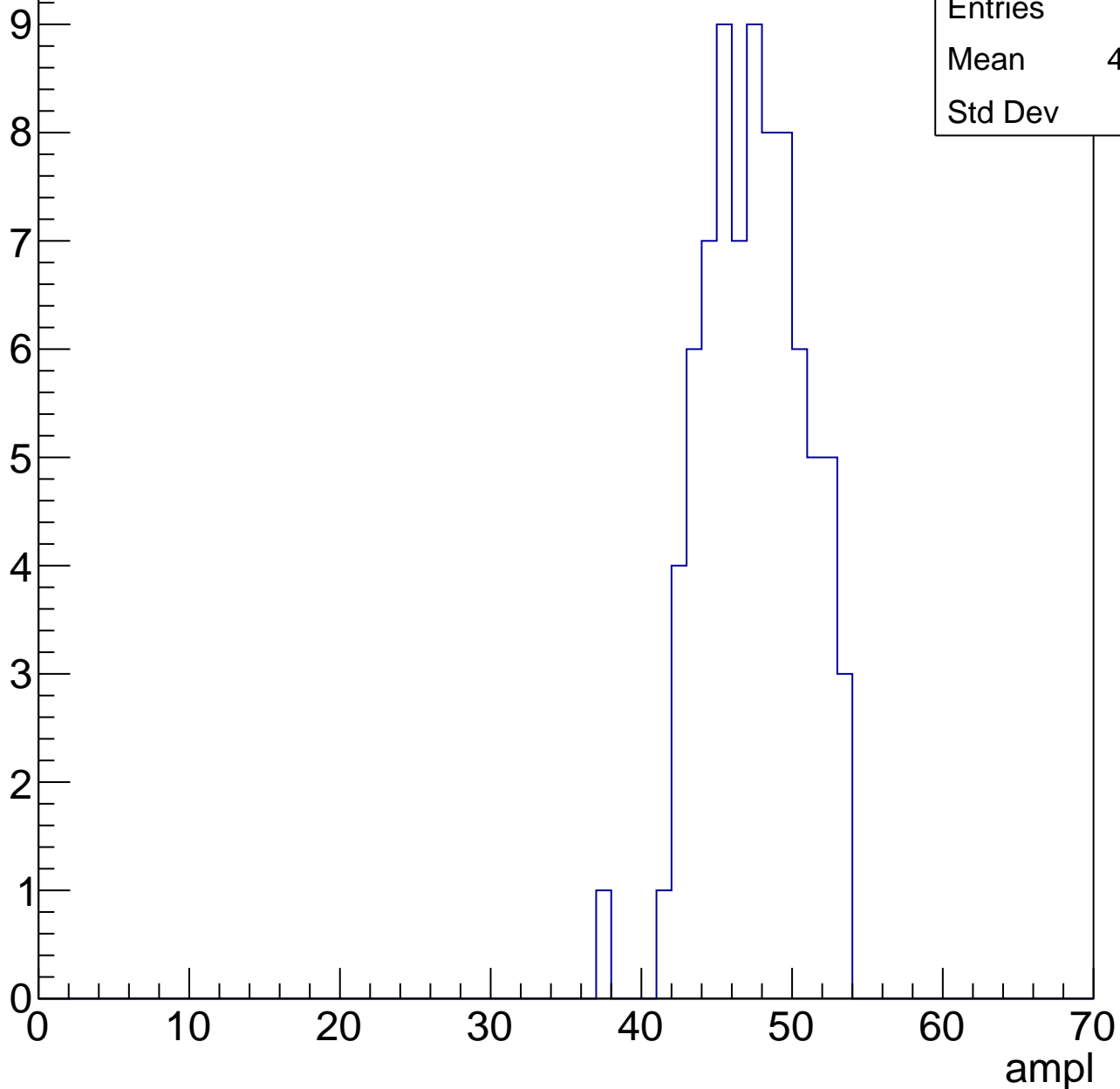
**Gaus mean : 41.1517**  
**Gaus Width: 2.9279**



# B1L000S, U21-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



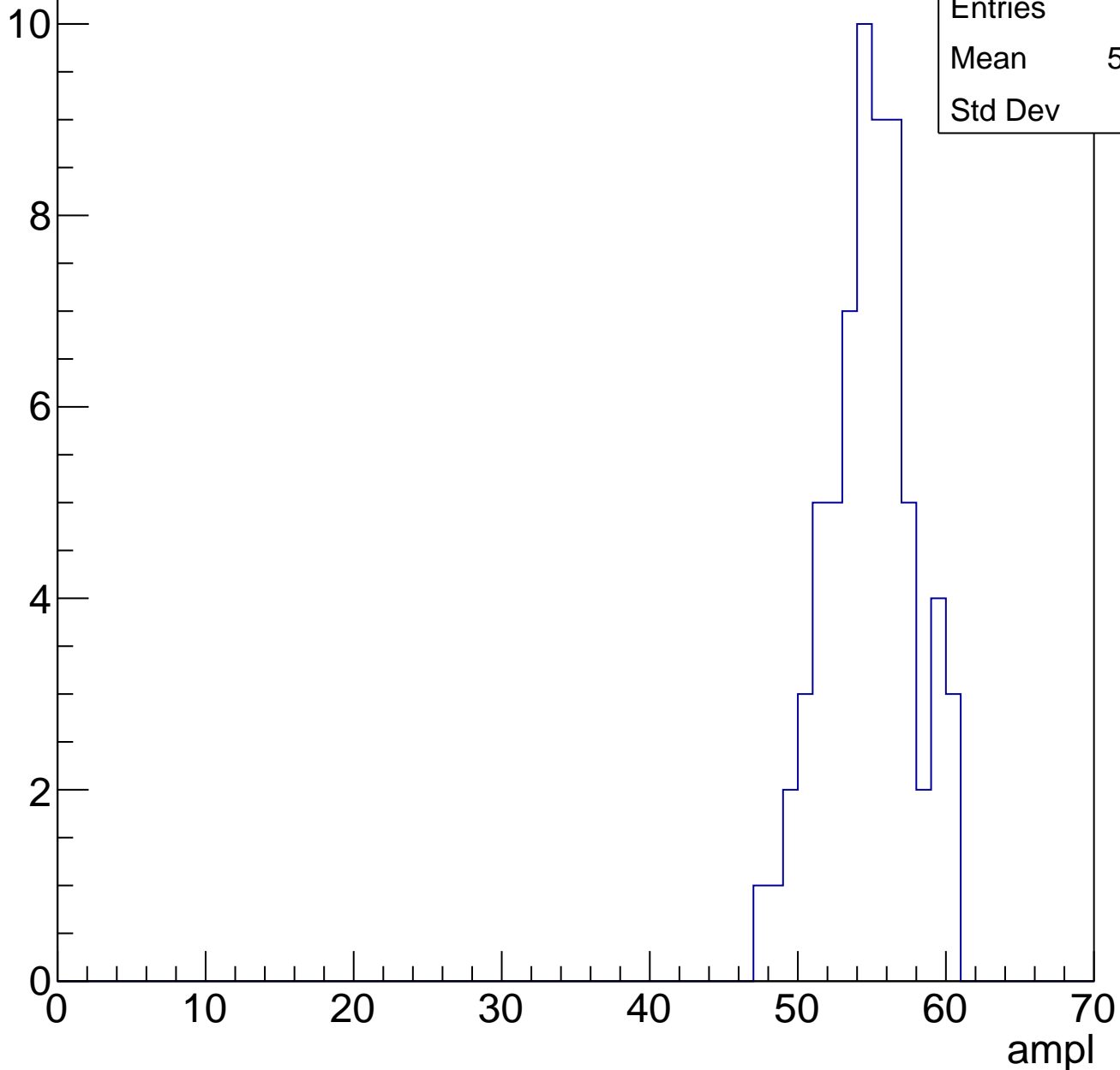
Entries	79
Mean	46.99
Std Dev	3.27

# B1L000S, U21-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	54.32
Std Dev	2.96

Entry



# B1L000S, U21-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	58.89
Std Dev	8.429

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

# B1L000S, U21-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch18, adc0

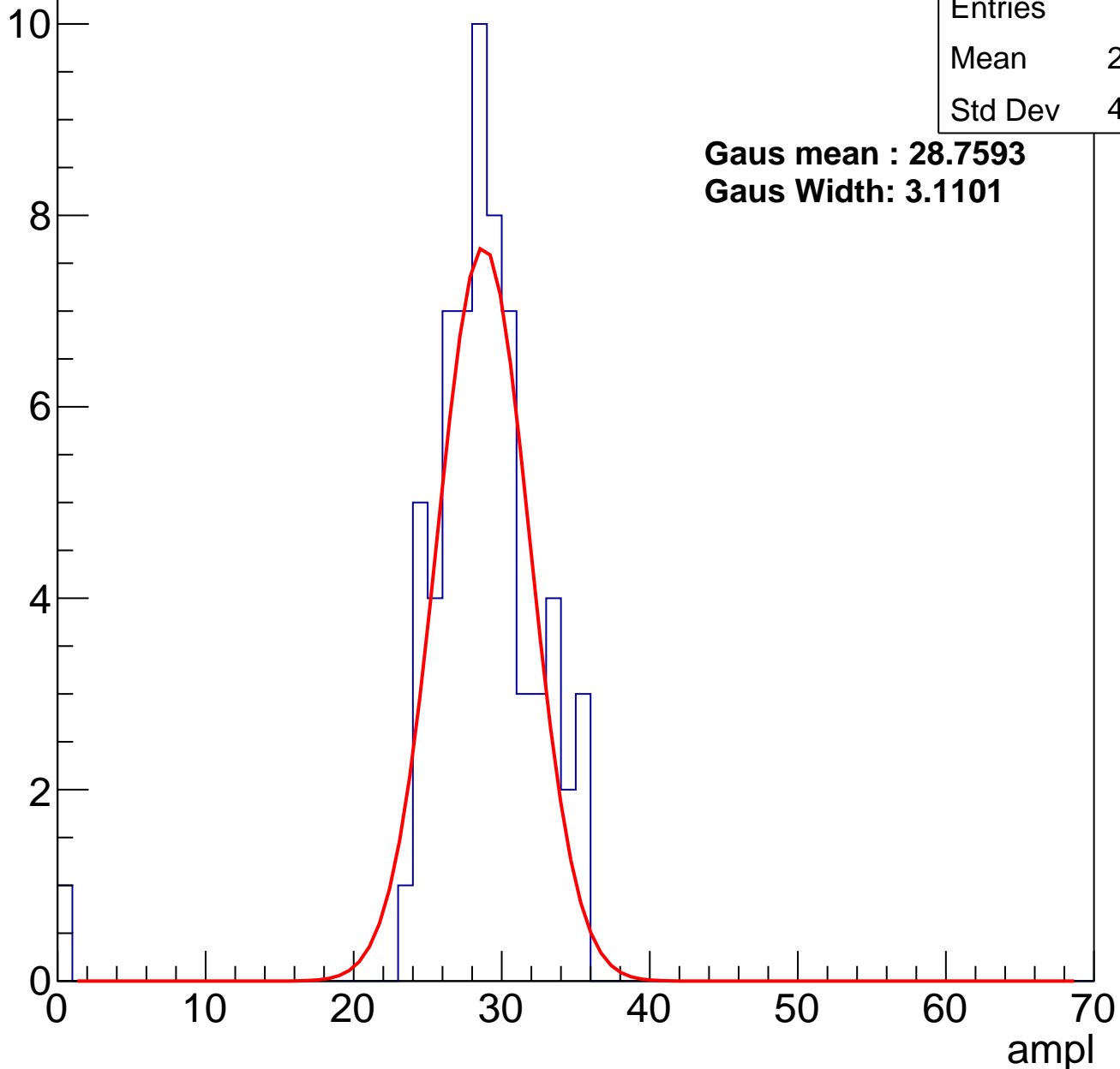
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	28.15
Std Dev	4.622

**Gaus mean : 28.7593**

**Gaus Width: 3.1101**

Entry



# B1L000S, U21-ch18, adc1

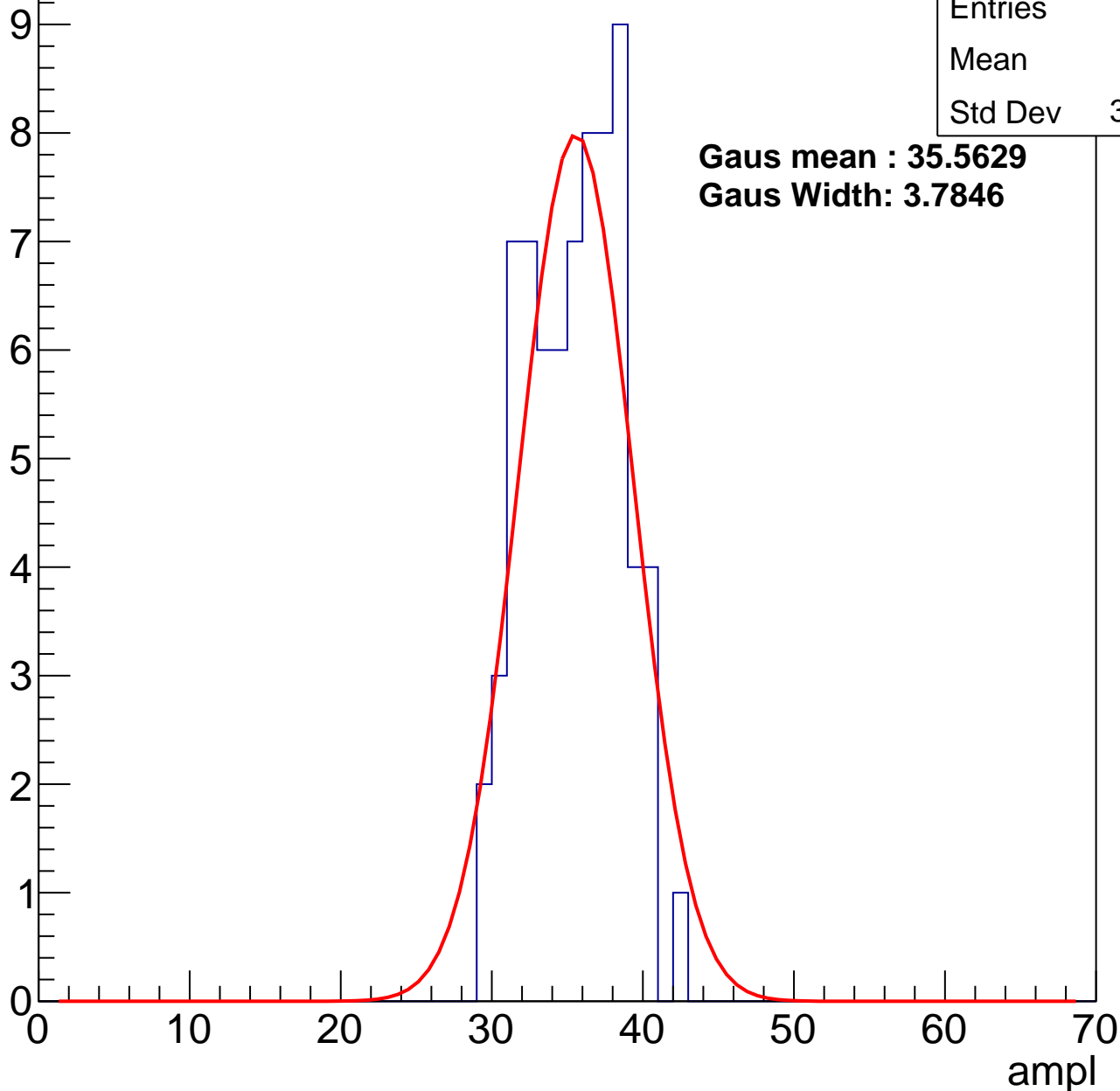
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	35
Std Dev	3.087

**Gaus mean : 35.5629**

**Gaus Width: 3.7846**



# B1L000S, U21-ch18, adc2

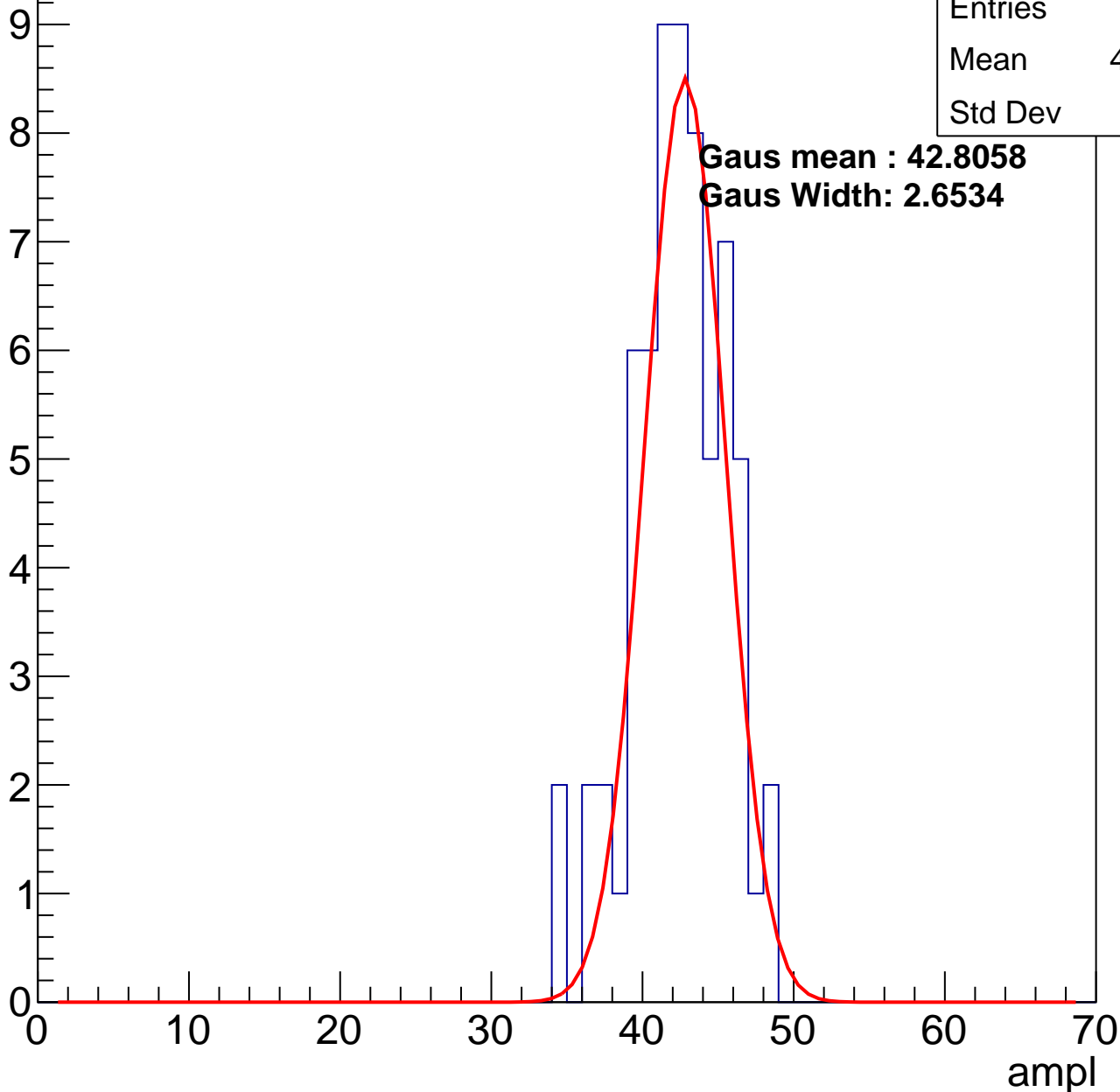
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.92
Std Dev	3.09

**Gaus mean : 42.8058**

**Gaus Width: 2.6534**

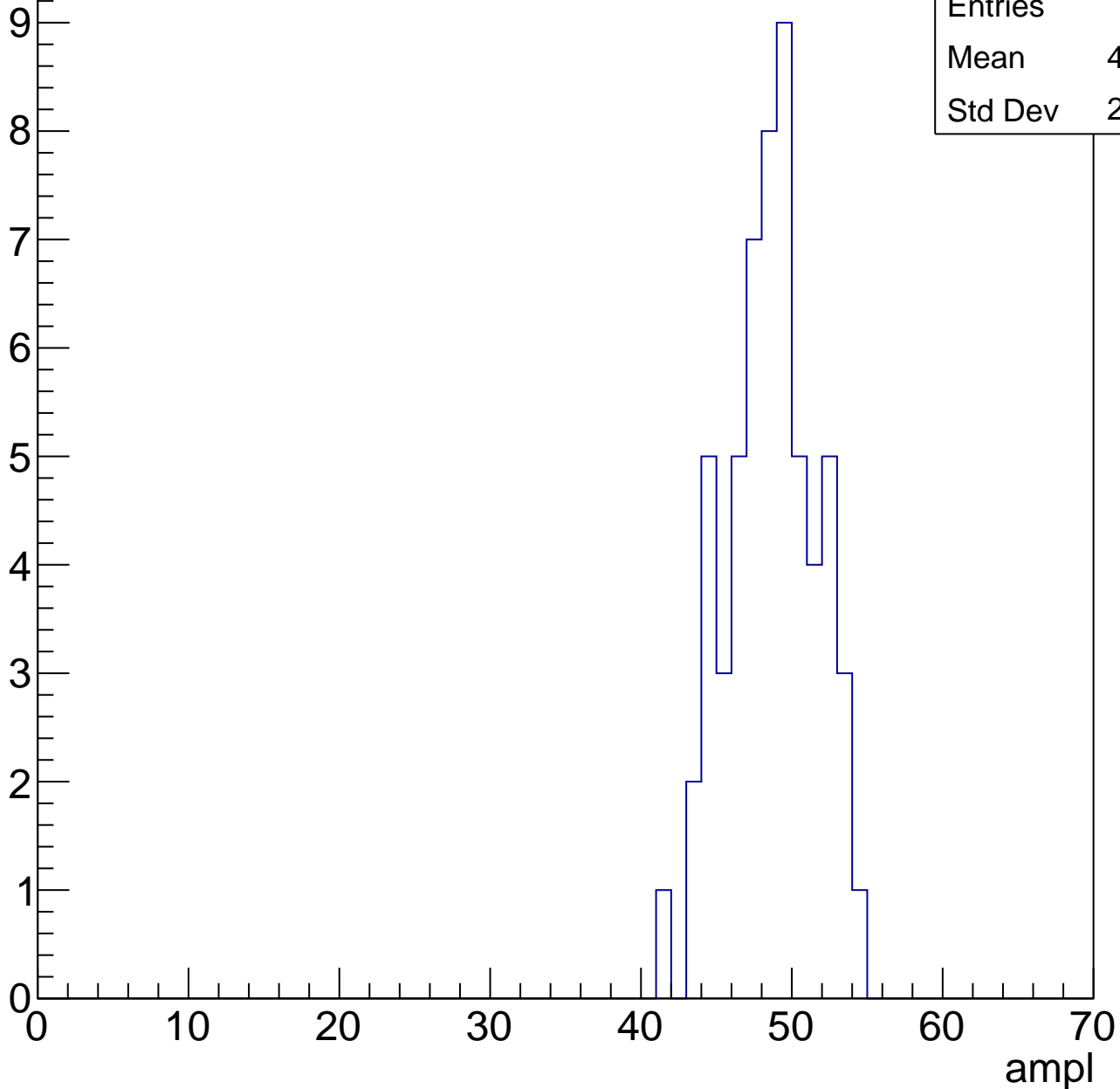


# B1L000S, U21-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	48.16
Std Dev	2.899

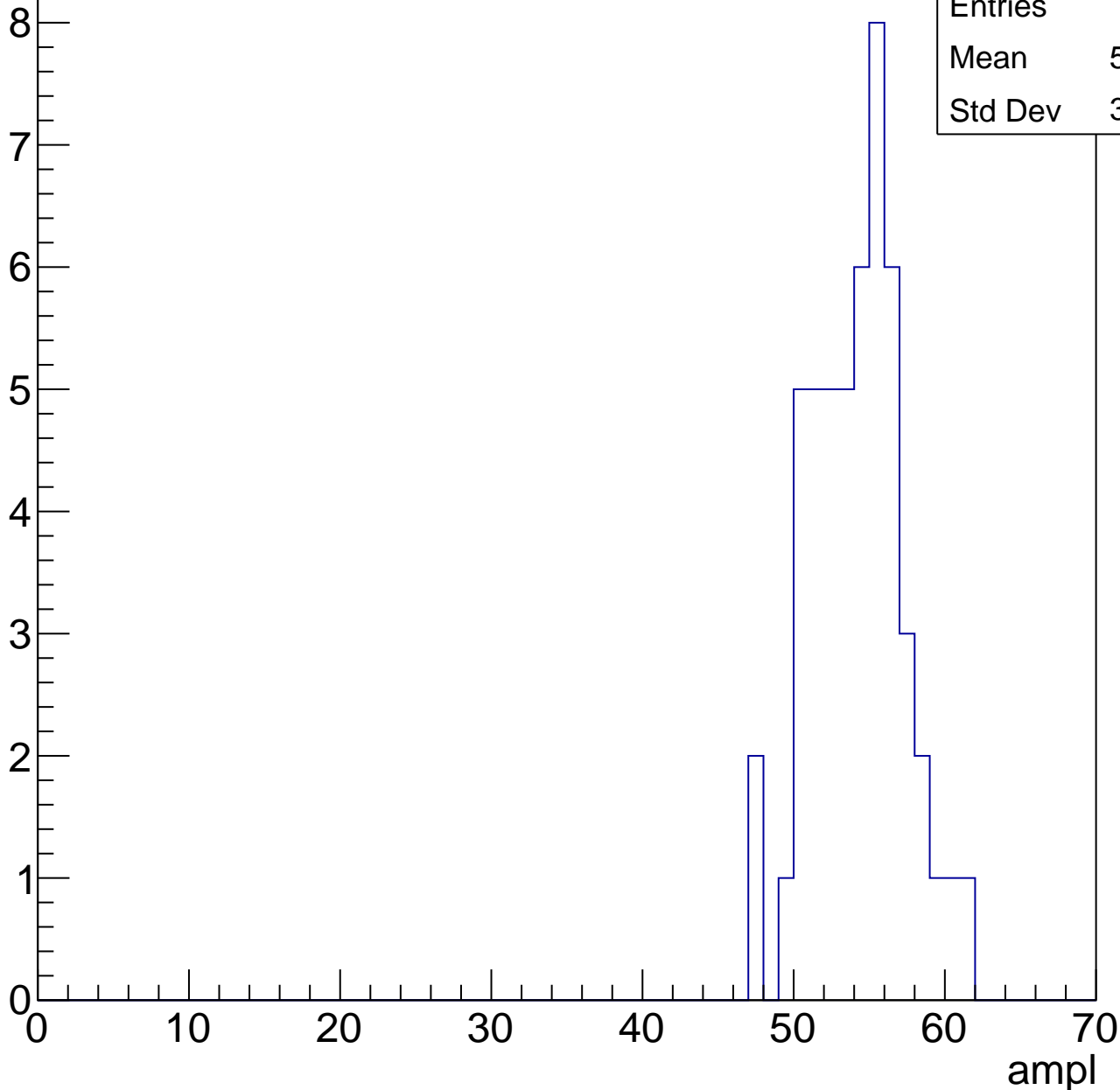


# B1L000S, U21-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	53.73
Std Dev	3.036

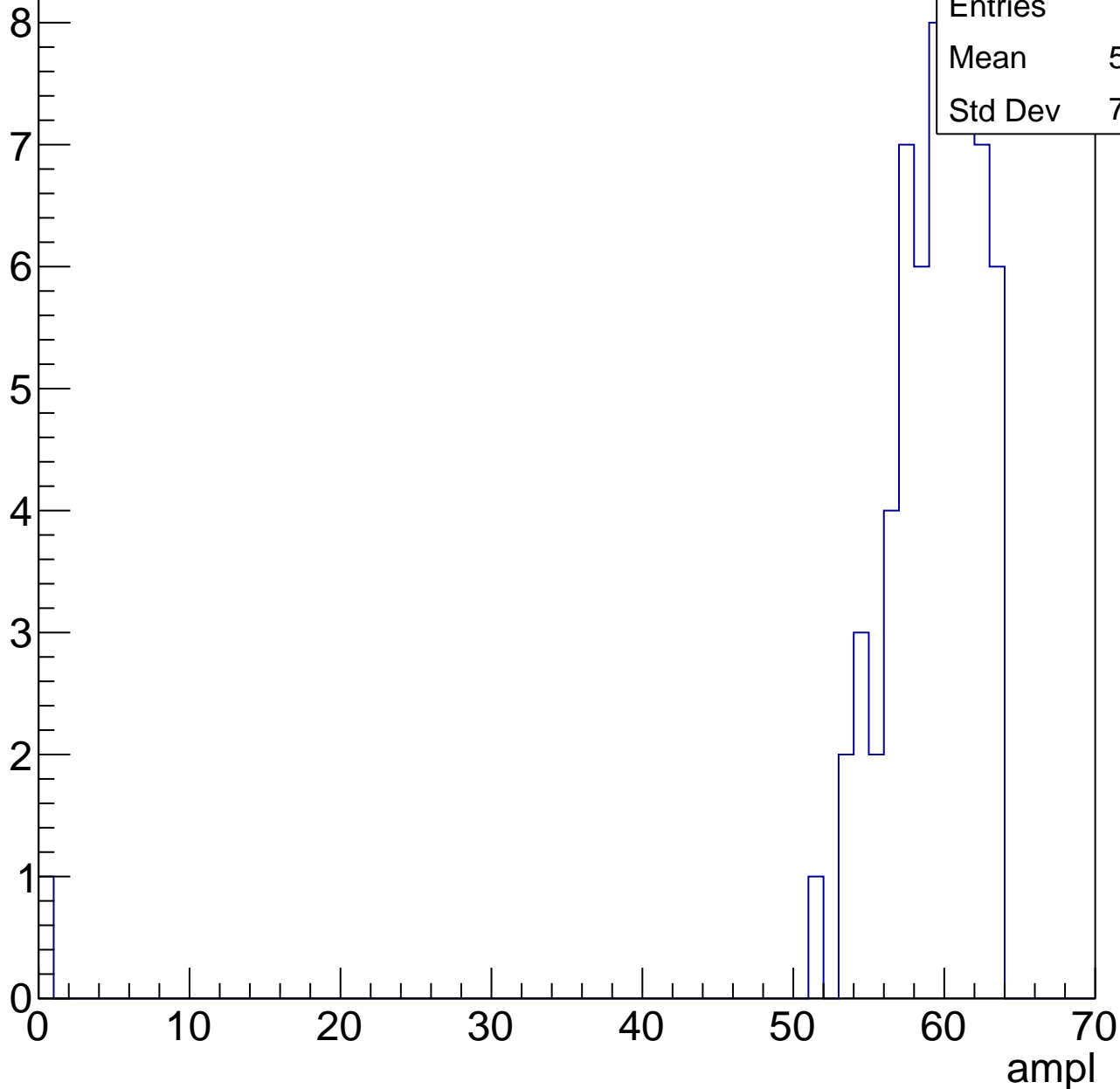


# B1L000S, U21-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

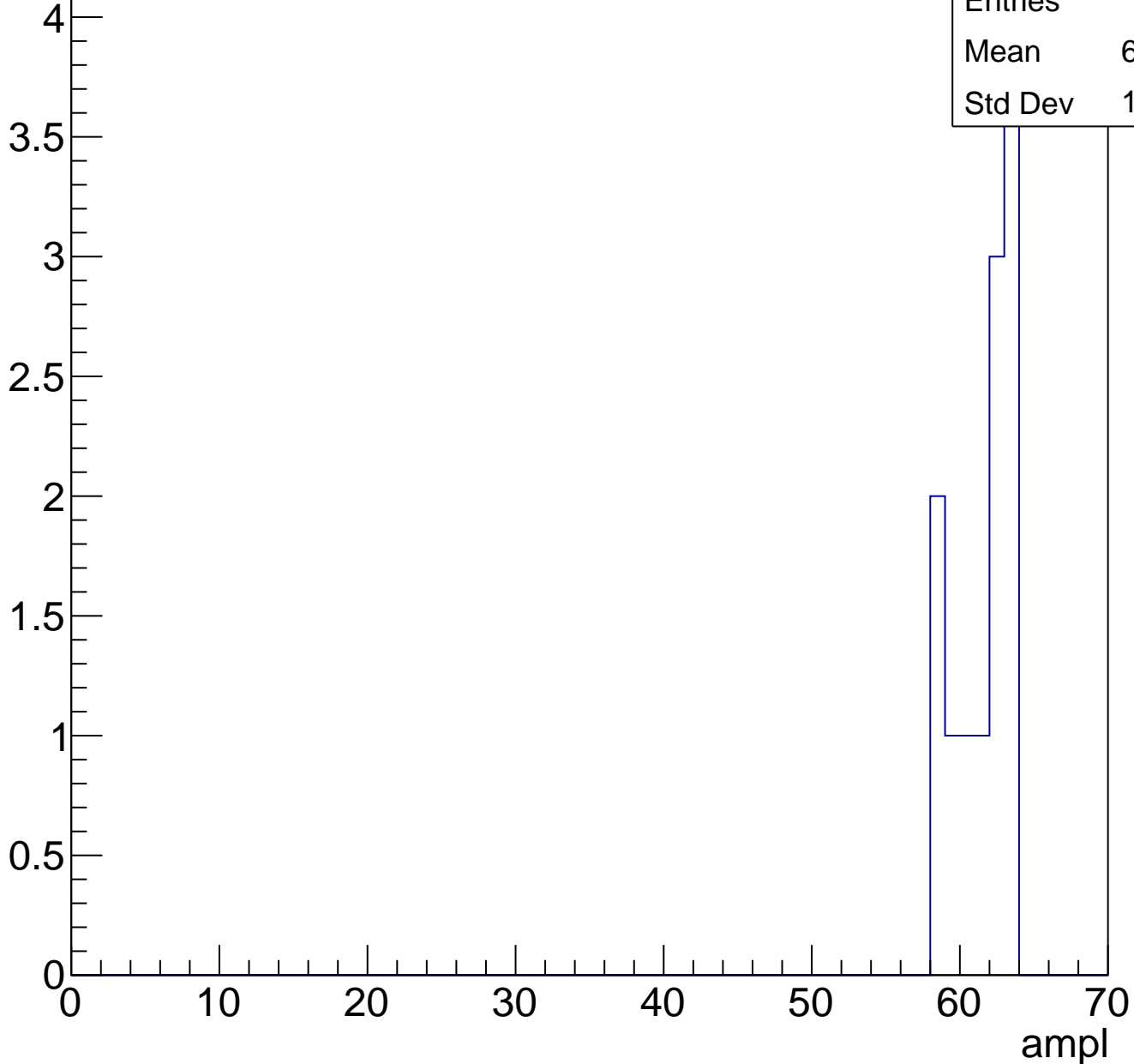
Entries	63
Mean	57.97
Std Dev	7.894



# B1L000S, U21-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

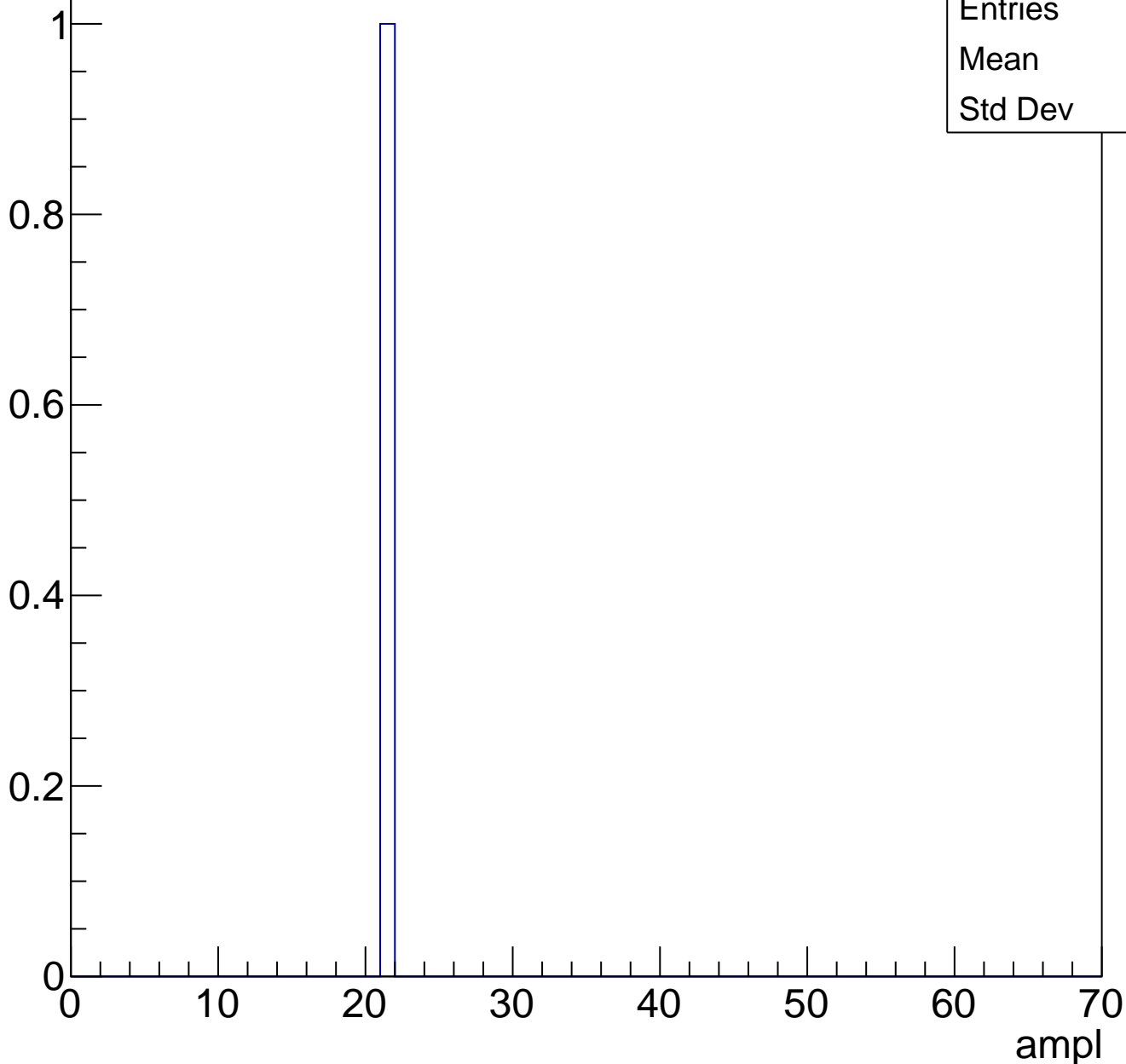




# B1L000S, U21-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch19, adc0

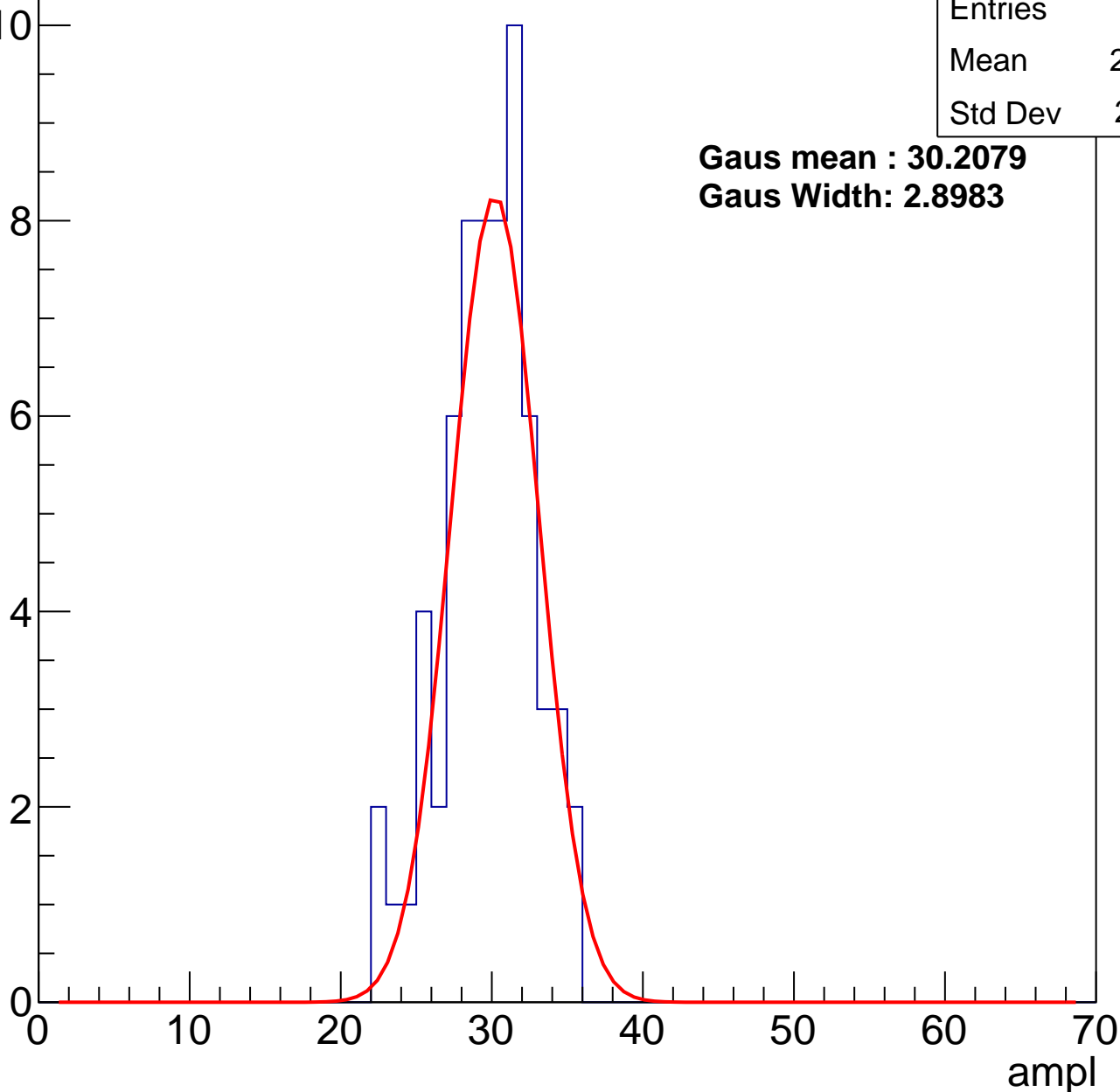
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.28
Std Dev	2.971

**Gaus mean : 30.2079**

**Gaus Width: 2.8983**



# B1L000S, U21-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	35.85
Std Dev	2.435

**Gaus mean : 36.3576**

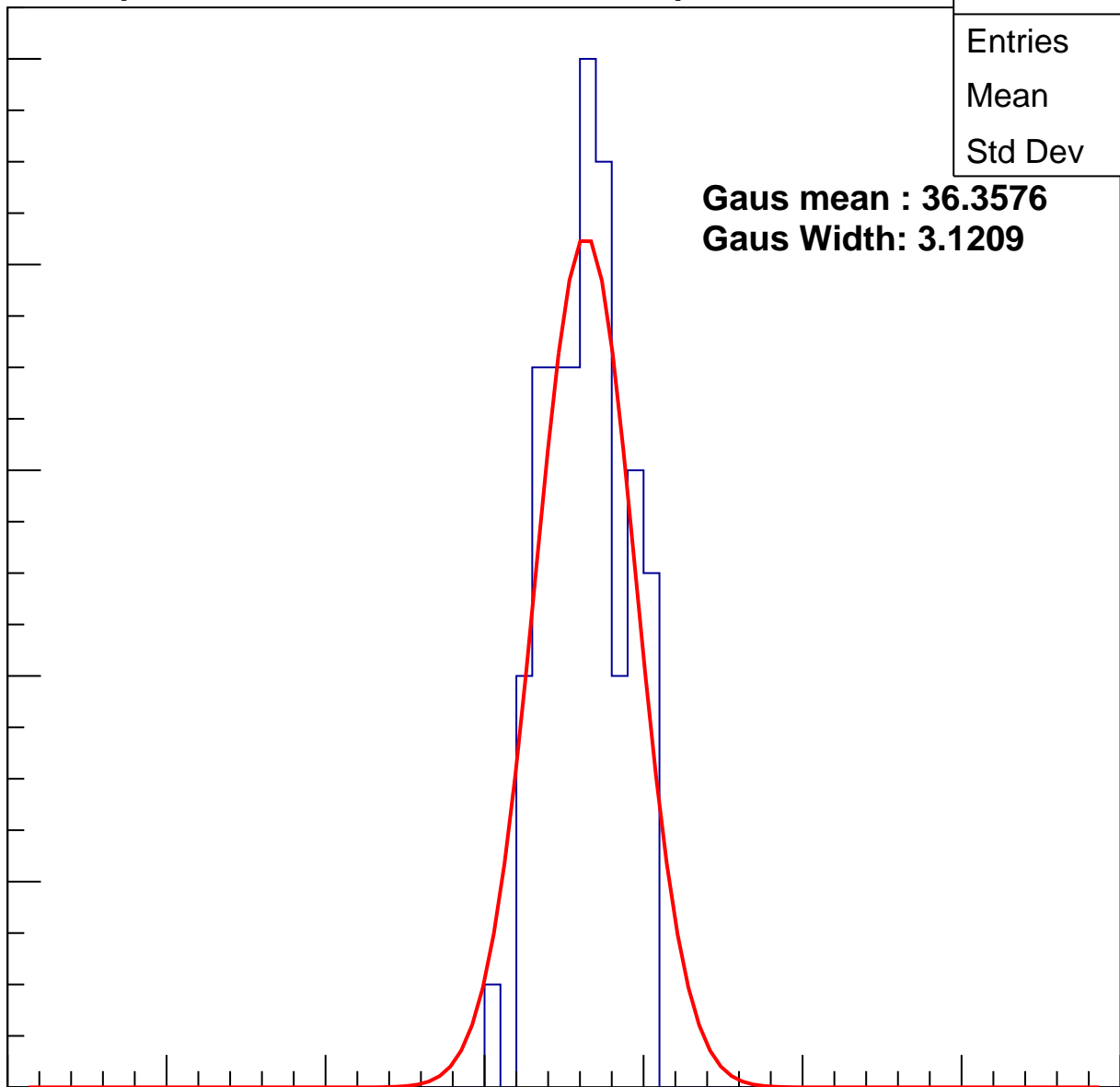
**Gaus Width: 3.1209**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch19, adc2

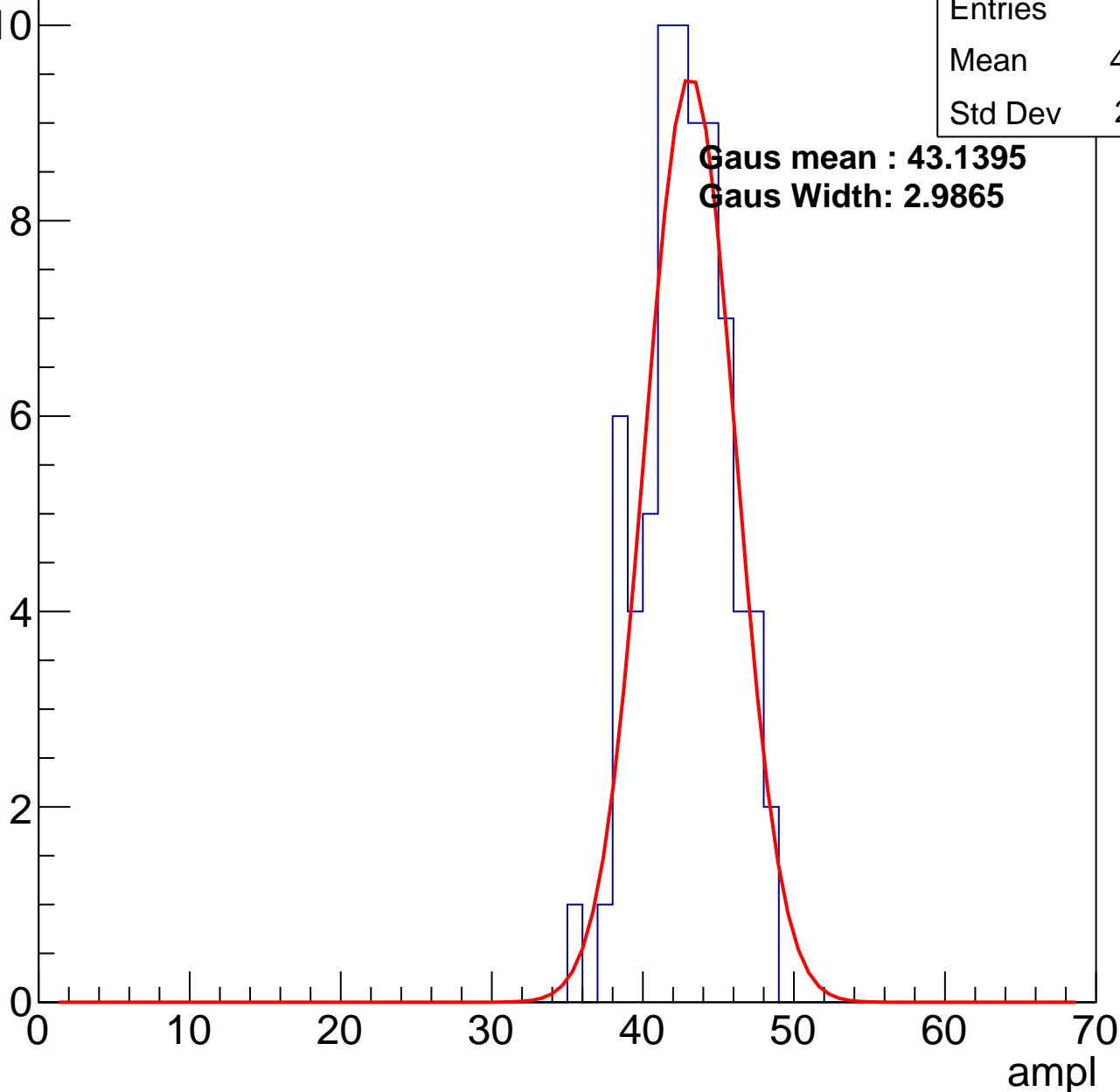
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	42.39
Std Dev	2.811

**Gaus mean : 43.1395**

**Gaus Width: 2.9865**



# B1L000S, U21-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	49.86
Std Dev	3.107

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

1

3

6

7

10

9

6

2

3

1

2

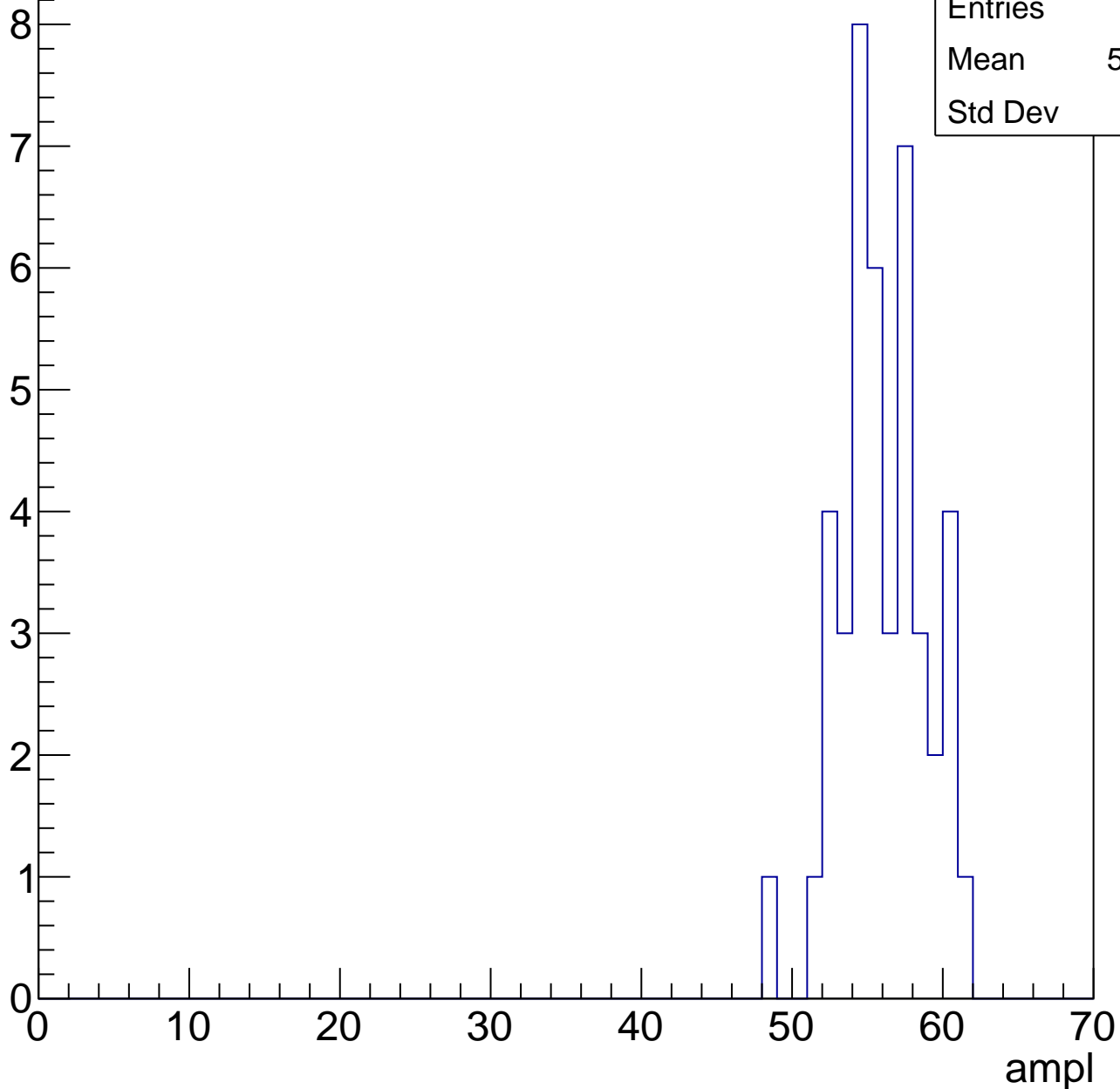
0

# B1L000S, U21-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

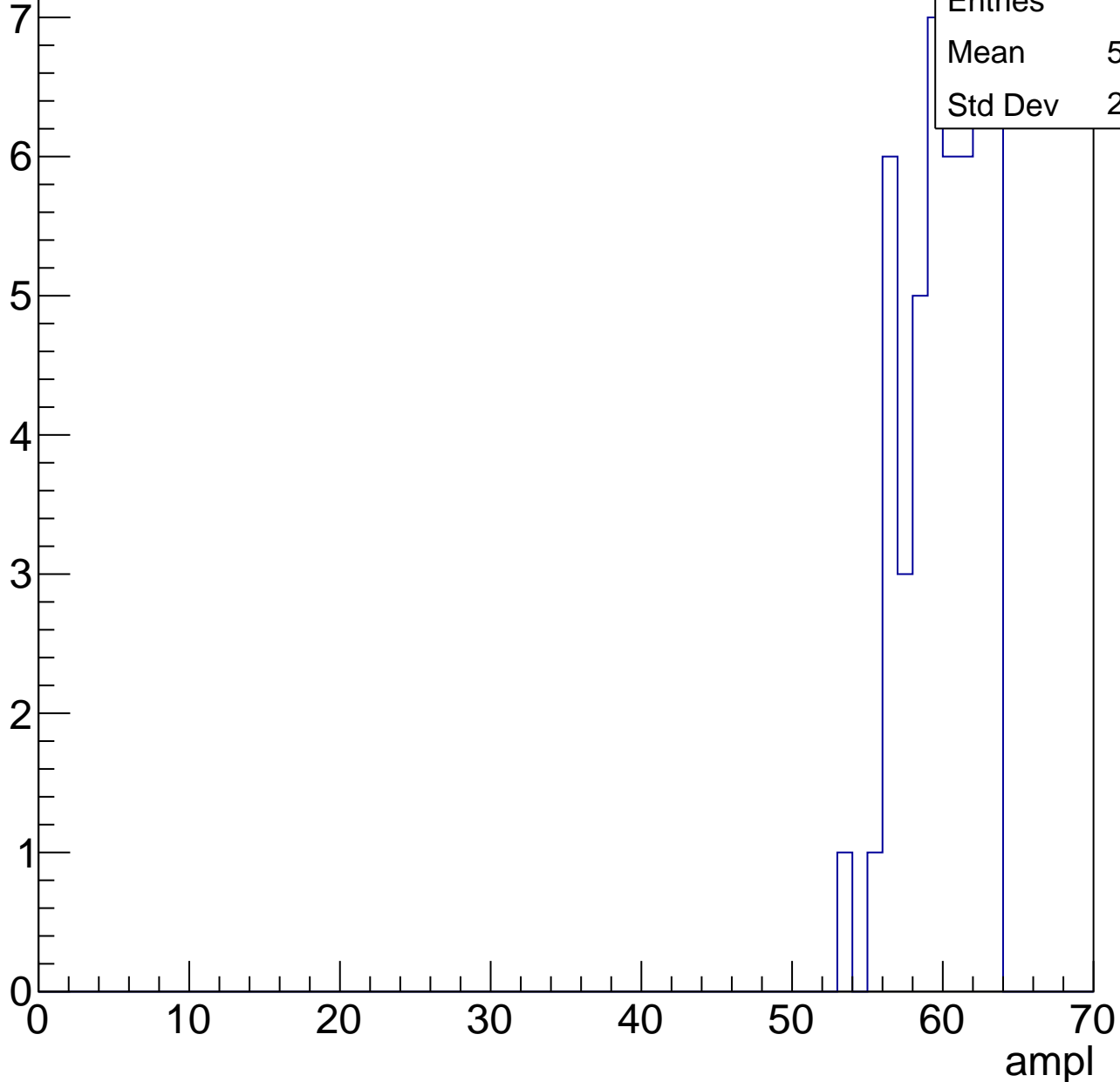
Entries	43
Mean	55.53
Std Dev	2.79



# B1L000S, U21-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

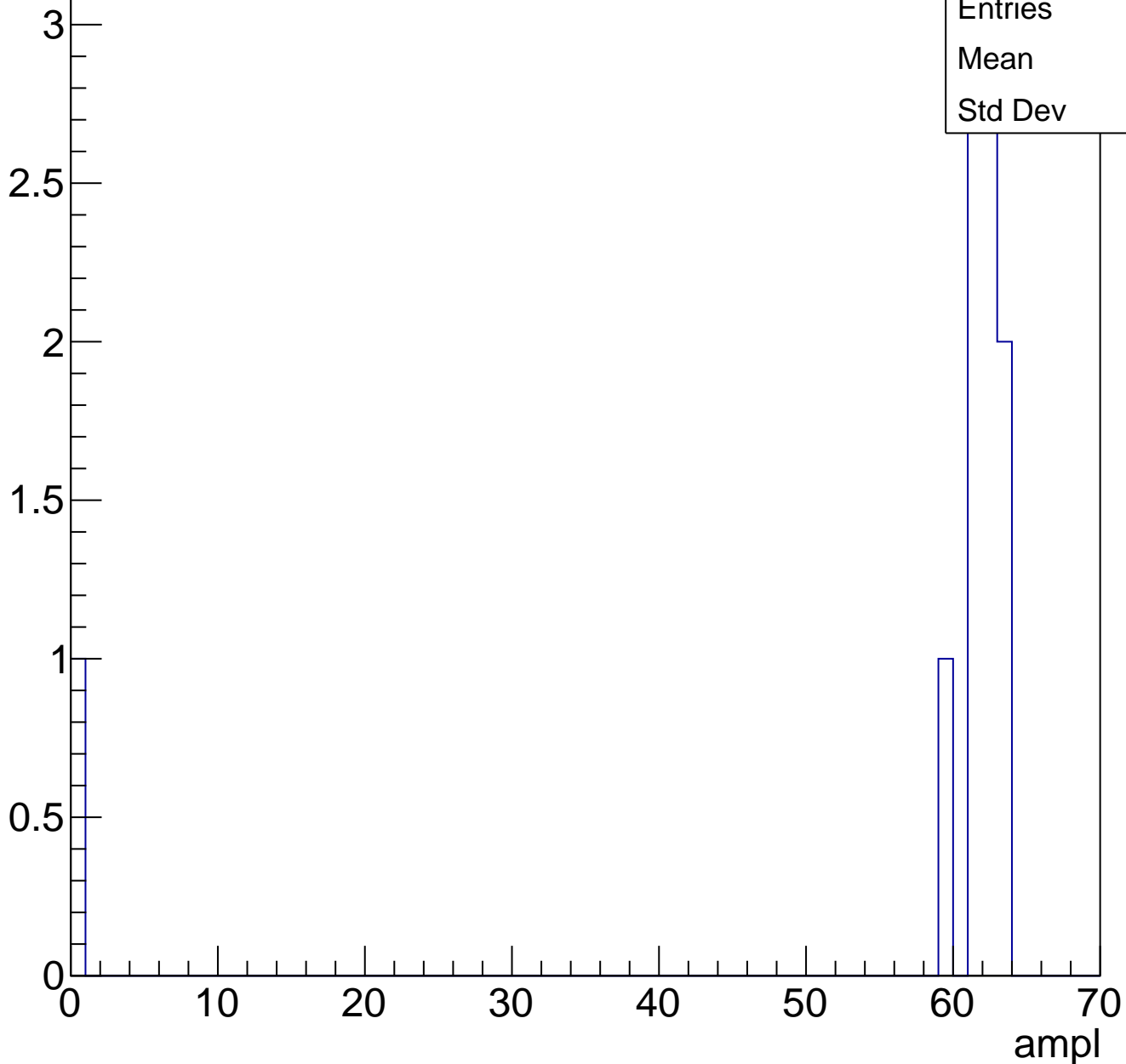
Entry



# B1L000S, U21-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L000S, U21-ch20, adc0

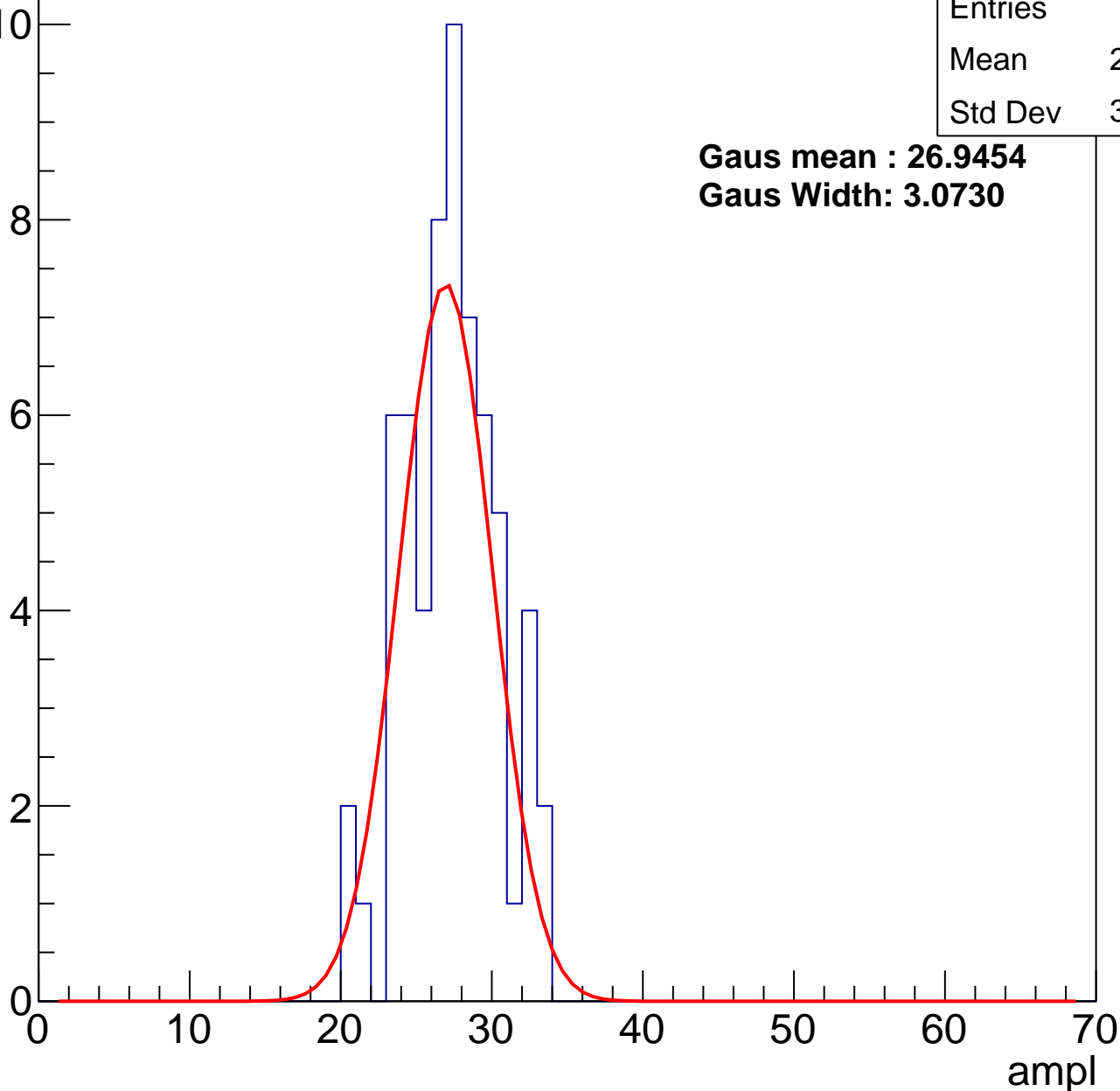
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	26.87
Std Dev	3.035

**Gaus mean : 26.9454**

**Gaus Width: 3.0730**

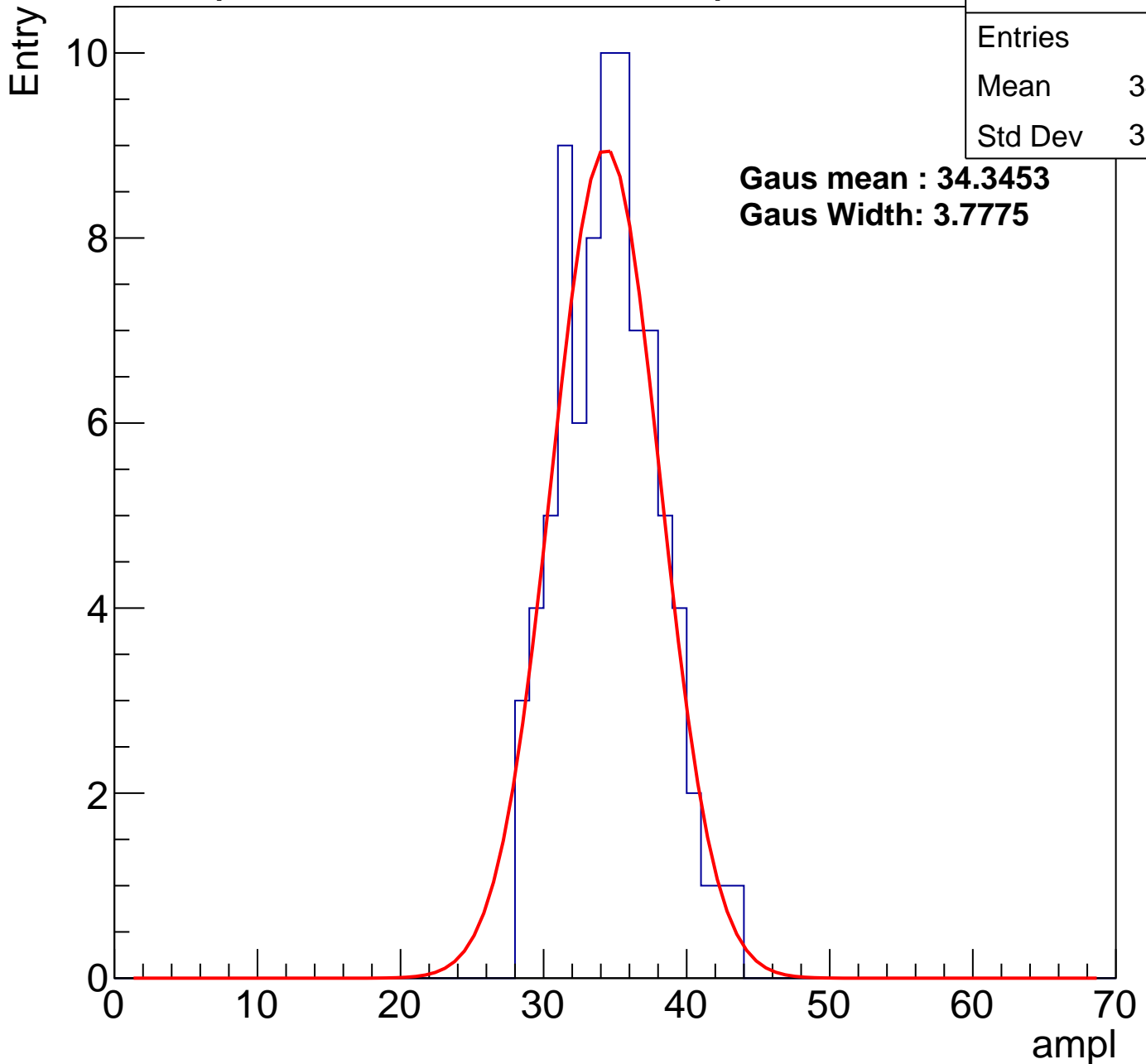


# B1L000S, U21-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	34.19
Std Dev	3.374

**Gaus mean : 34.3453**  
**Gaus Width: 3.7775**



# B1L000S, U21-ch20, adc2

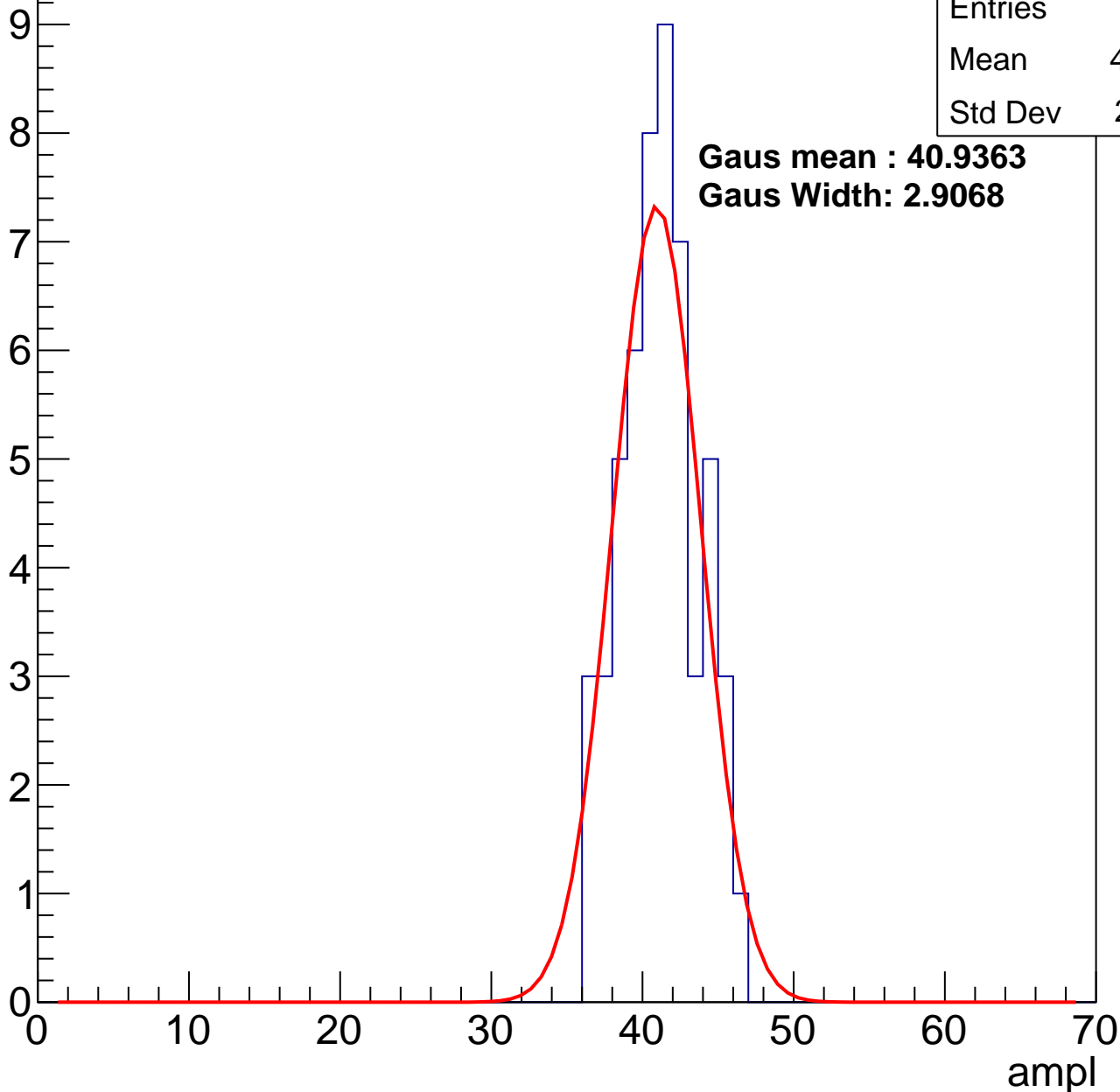
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	40.68
Std Dev	2.501

**Gaus mean : 40.9363**

**Gaus Width: 2.9068**

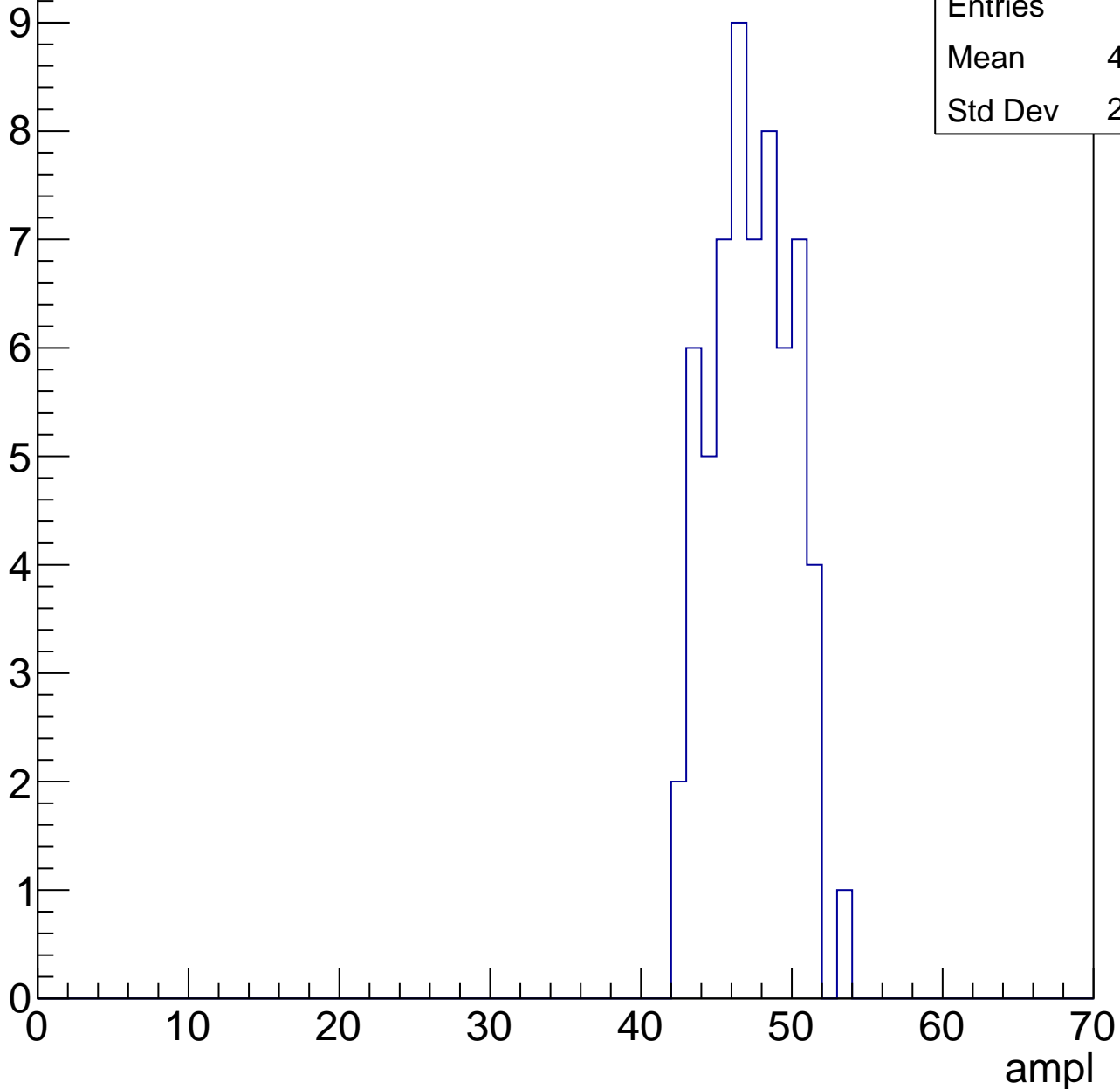


# B1L000S, U21-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

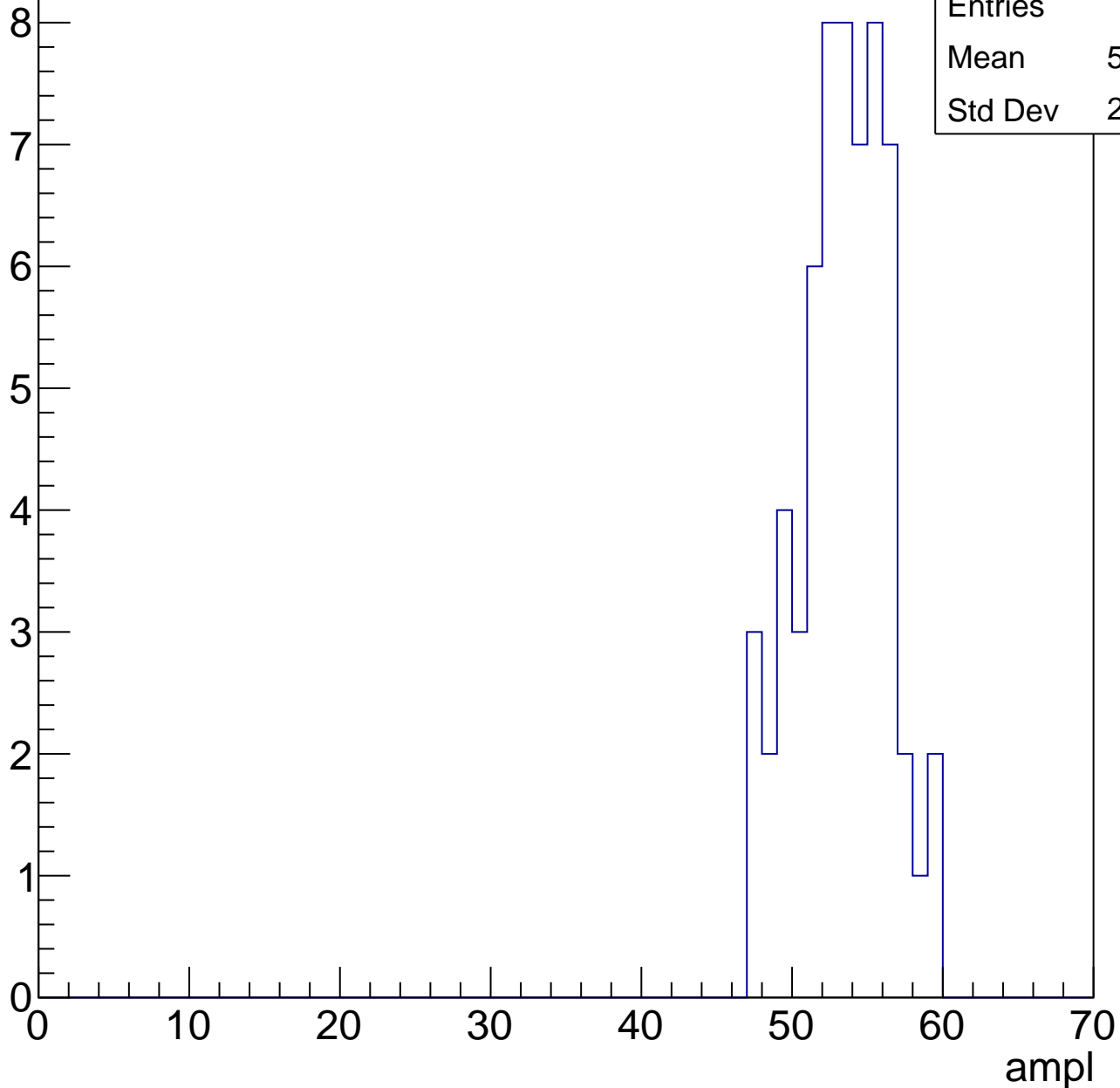
Entries	62
Mean	46.85
Std Dev	2.608



# B1L000S, U21-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

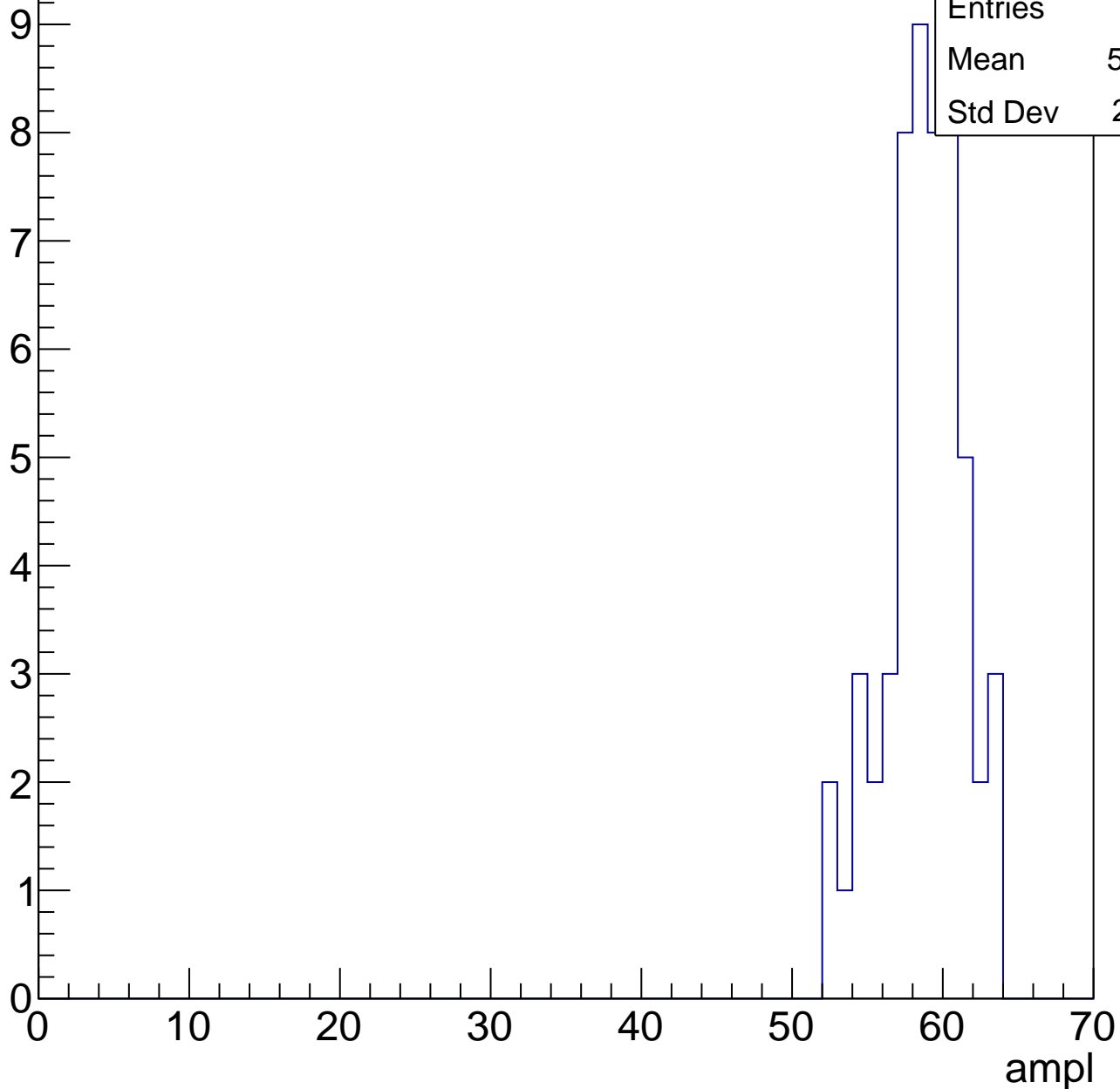


Entries	61
Mean	52.93
Std Dev	2.896

# B1L000S, U21-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

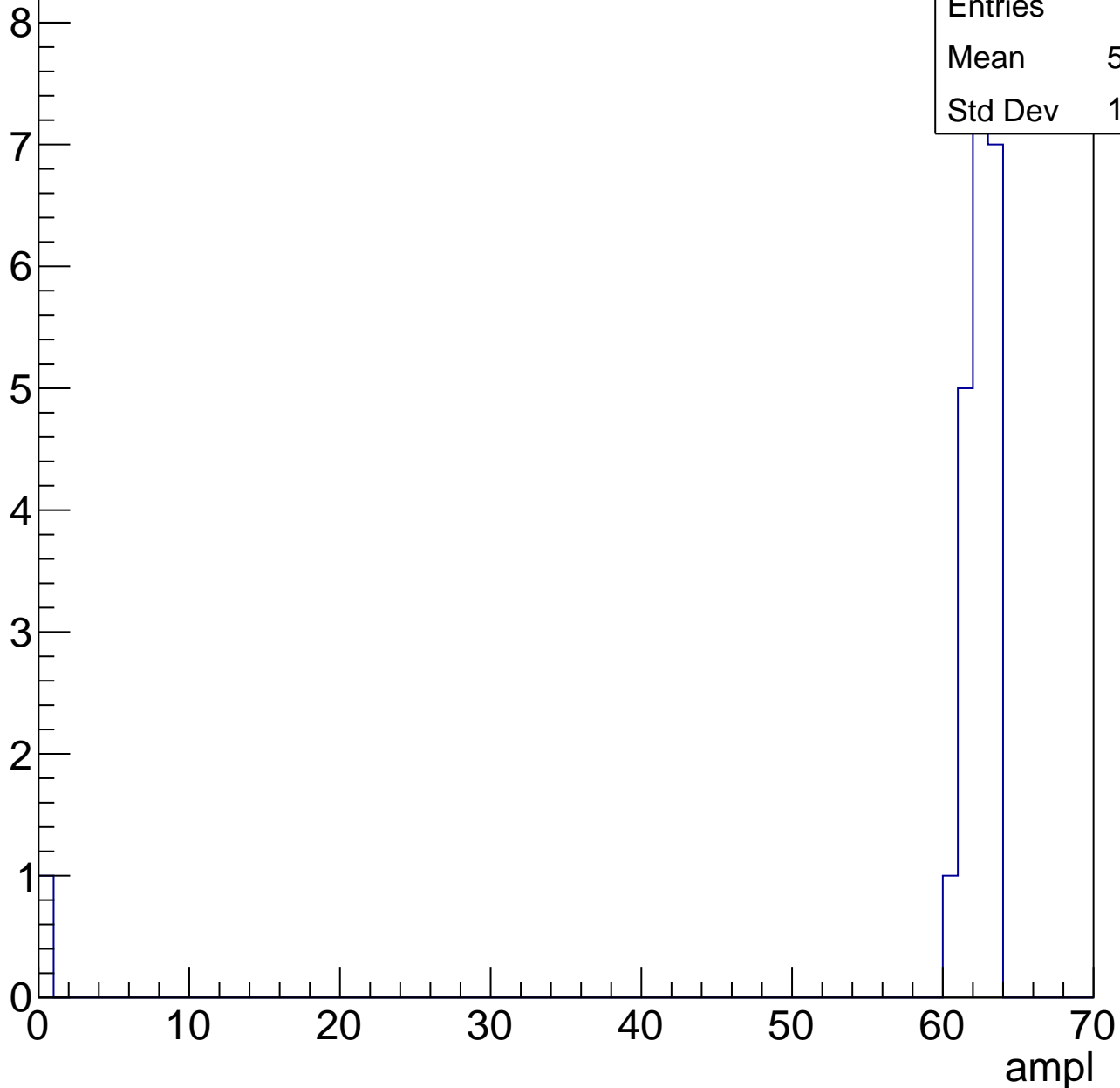


Entries	55
Mean	58.27
Std Dev	2.611

# B1L000S, U21-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch21, adc0

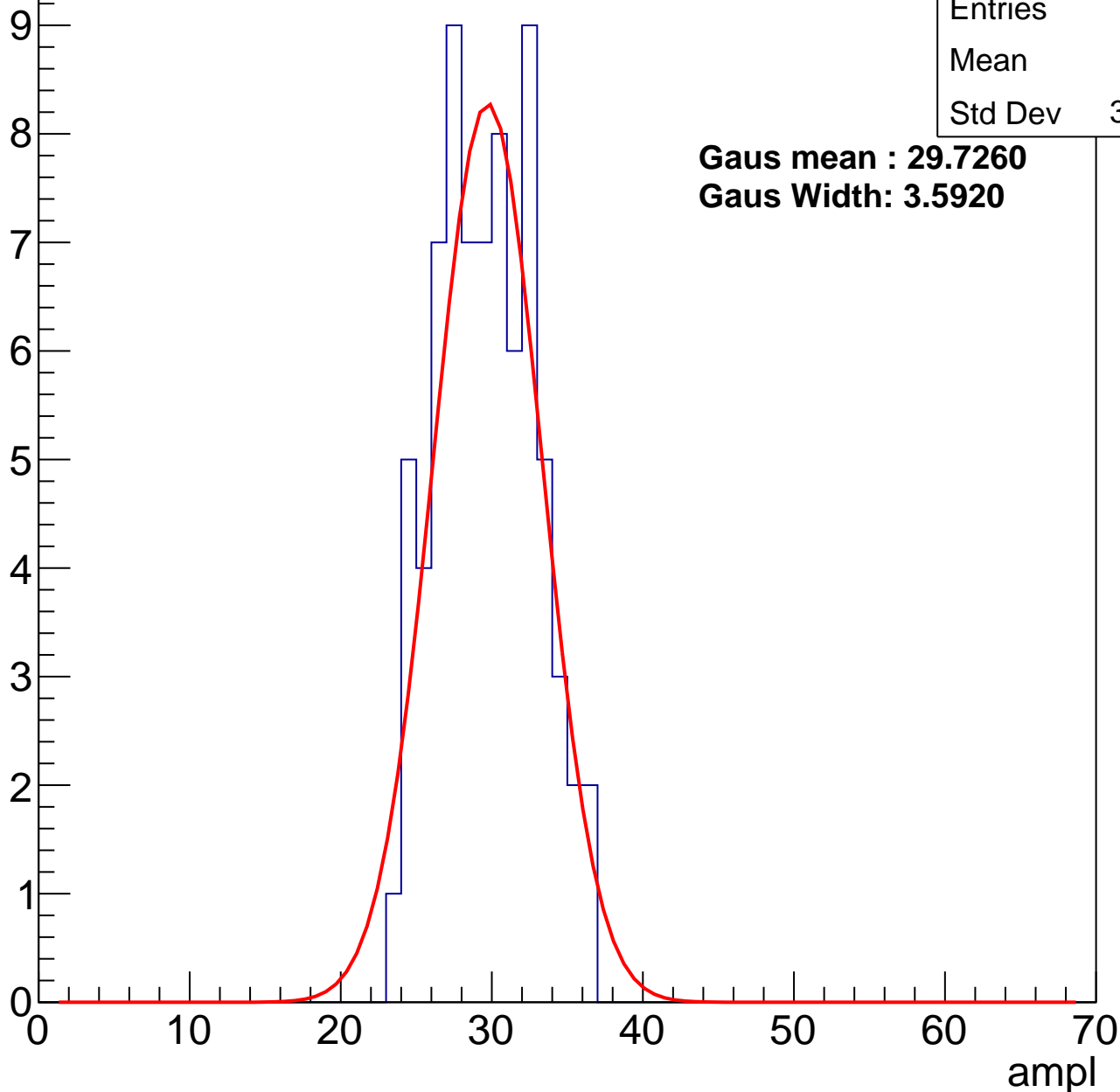
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	29.2
Std Dev	3.196

**Gaus mean : 29.7260**

**Gaus Width: 3.5920**



# B1L000S, U21-ch21, adc1

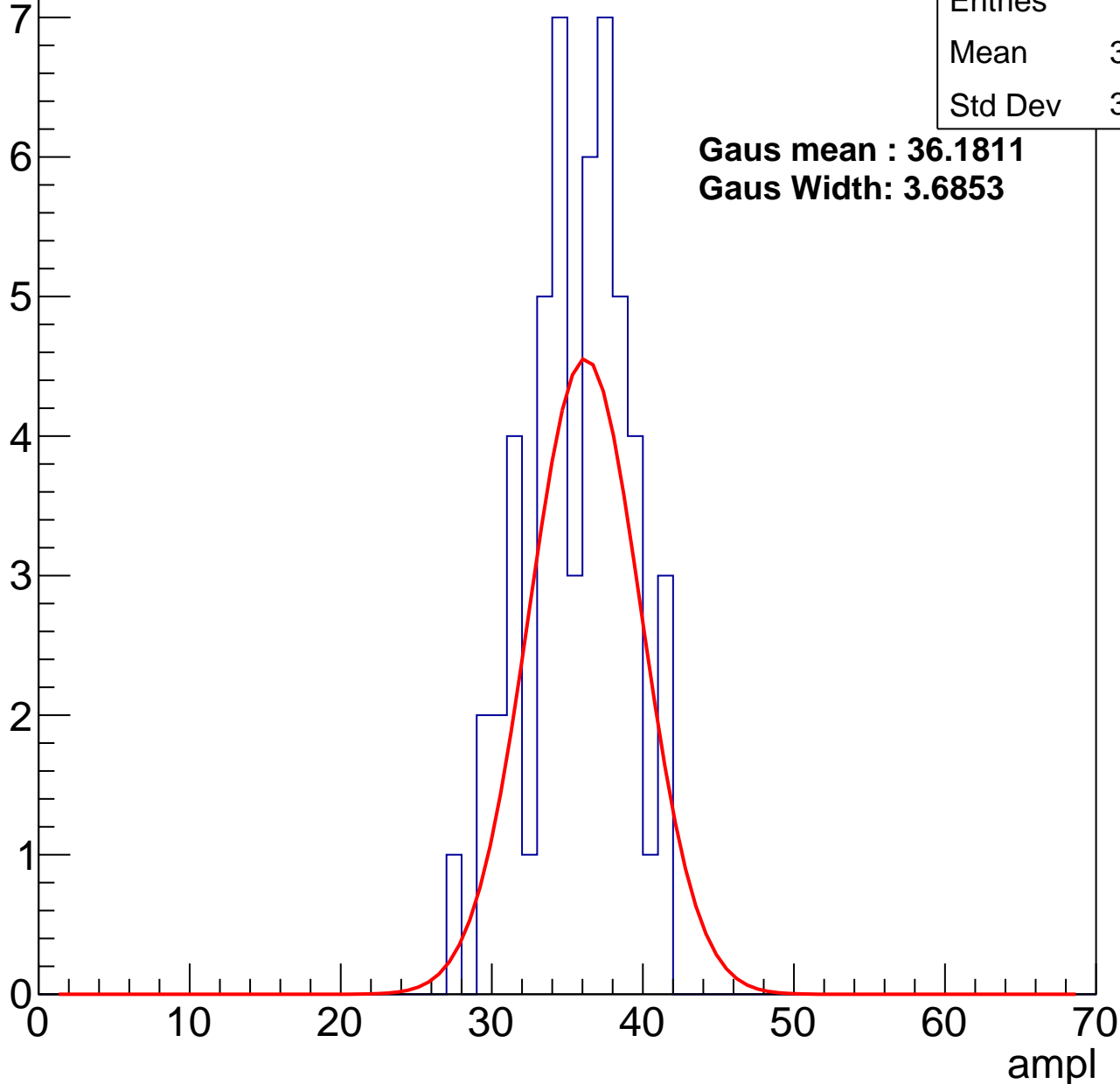
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	35.16
Std Dev	3.316

**Gaus mean : 36.1811**

**Gaus Width: 3.6853**



# B1L000S, U21-ch21, adc2

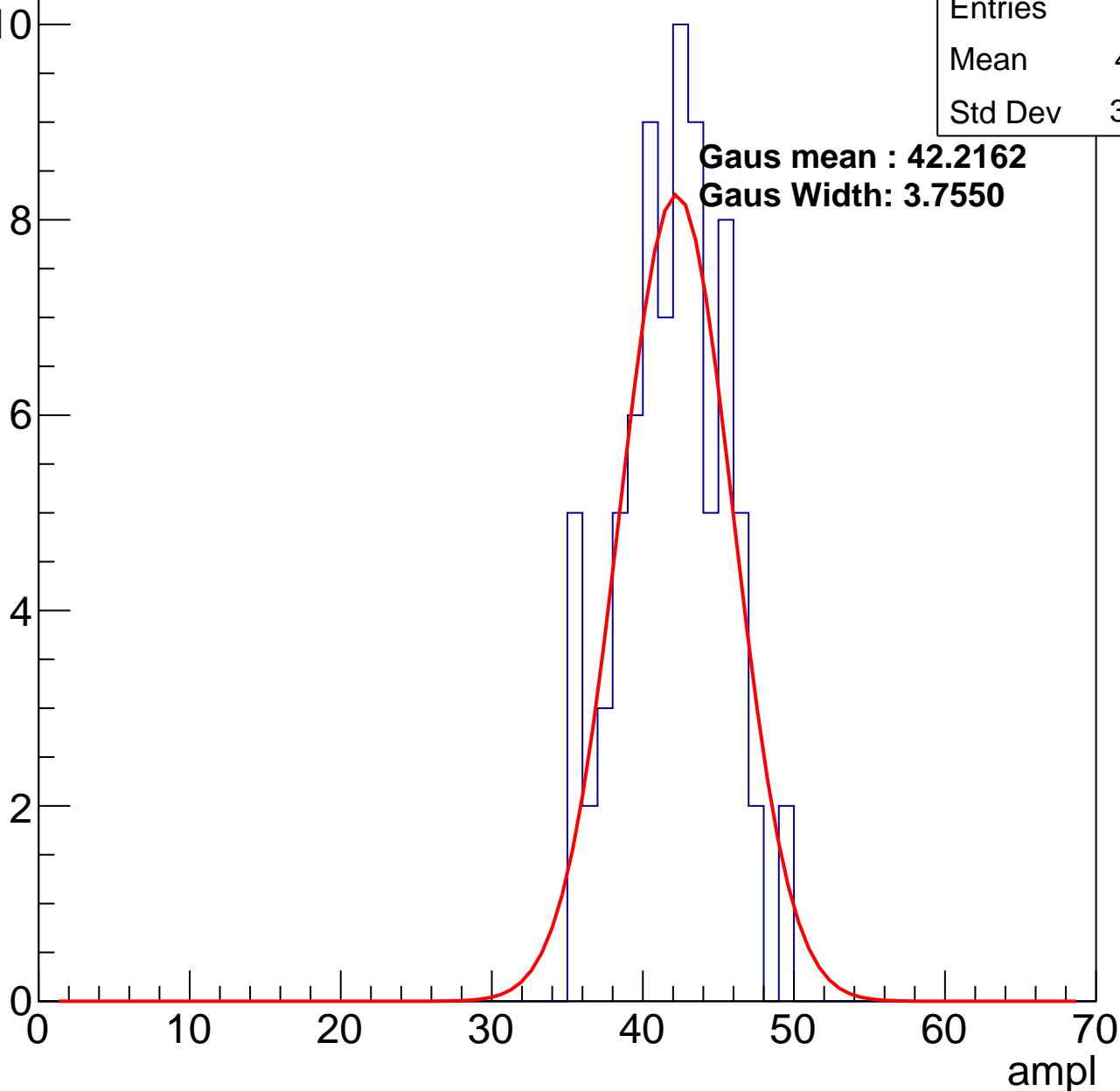
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	41.51
Std Dev	3.358

**Gaus mean : 42.2162**

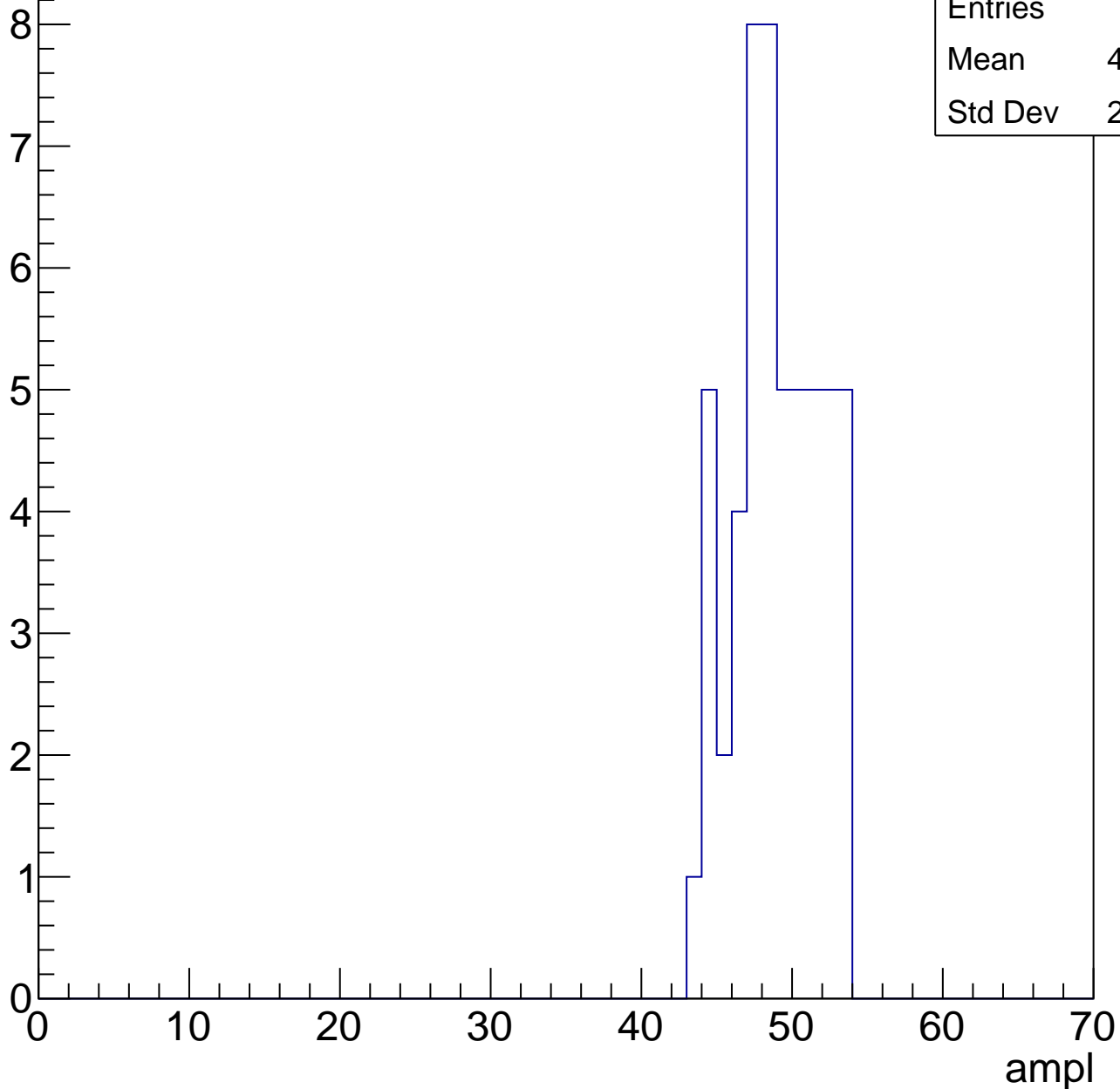
**Gaus Width: 3.7550**



# B1L000S, U21-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



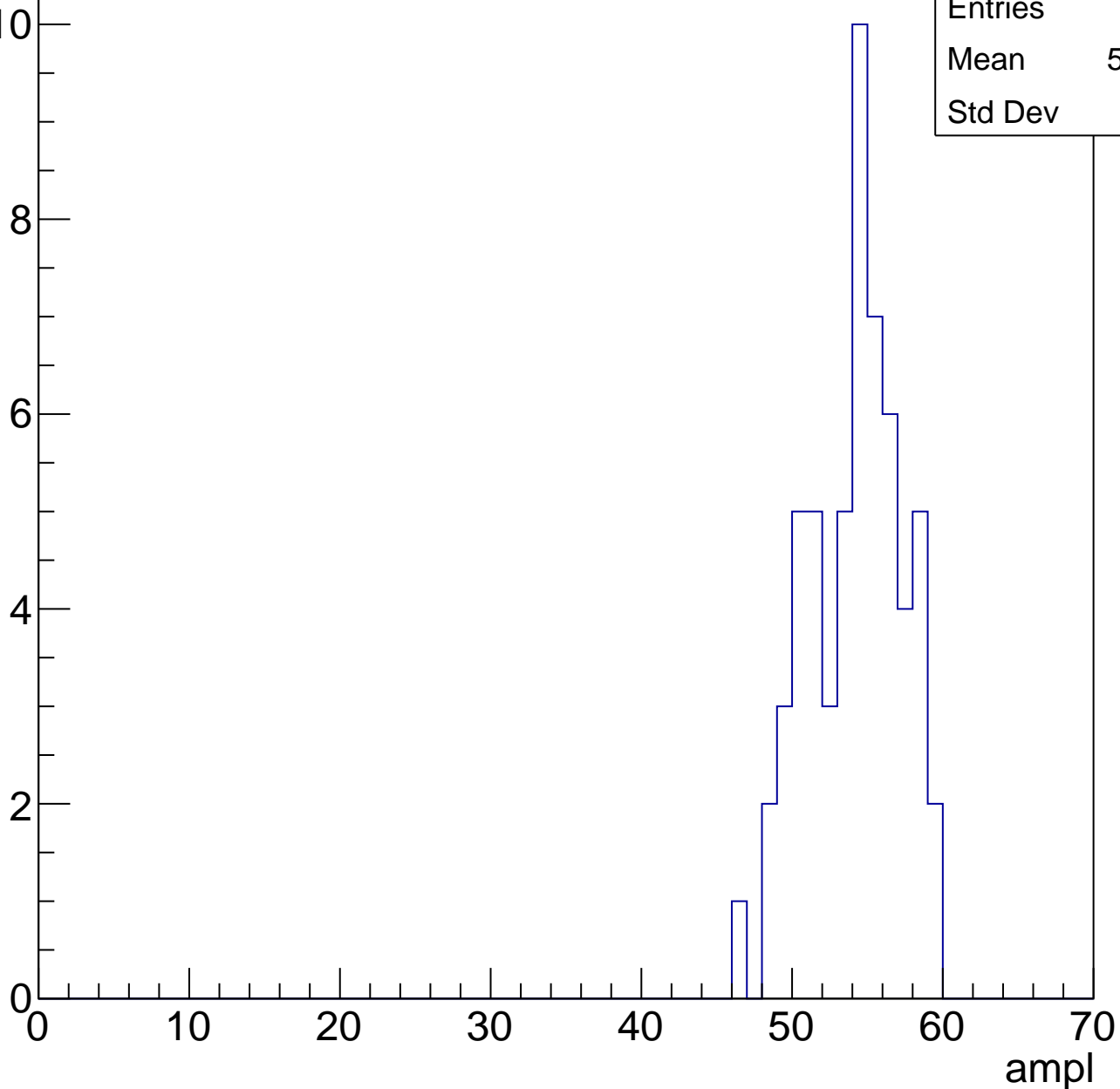
Entries	53
Mean	48.53
Std Dev	2.772

# B1L000S, U21-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	53.66
Std Dev	3.06

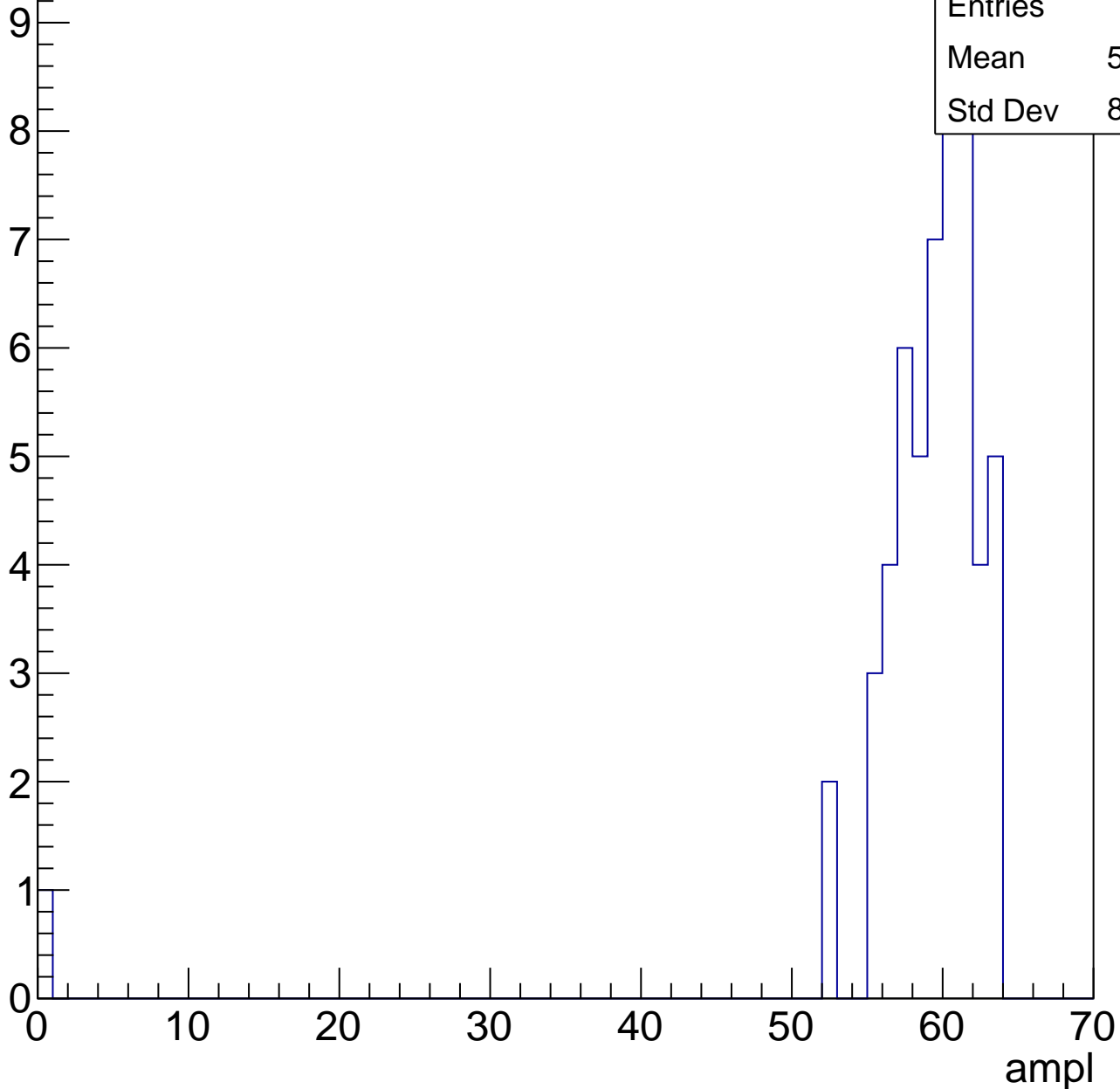


# B1L000S, U21-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	57.94
Std Dev	8.376

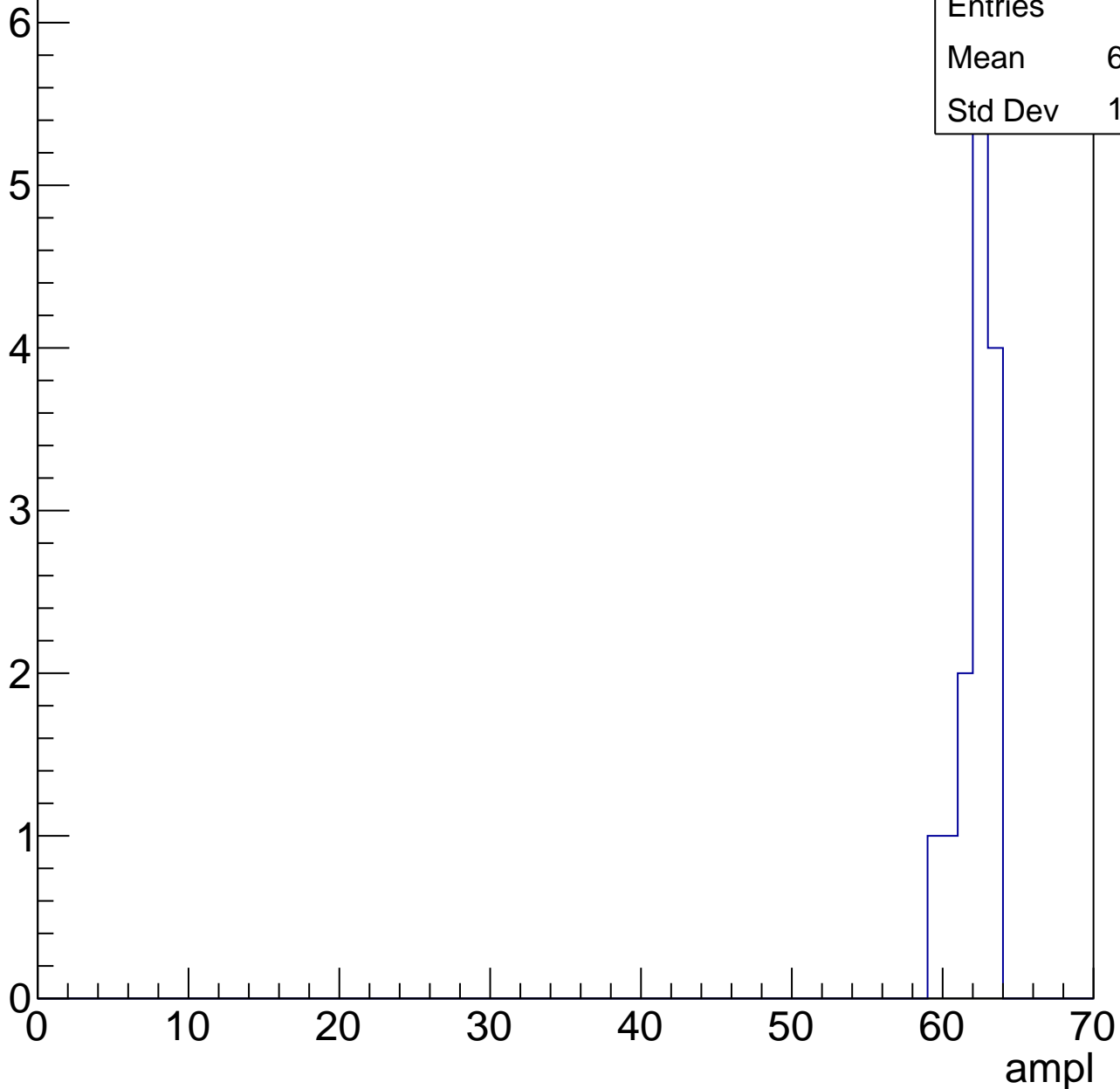


# B1L000S, U21-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.79
Std Dev	1.145





# B1L000S, U21-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch22, adc0

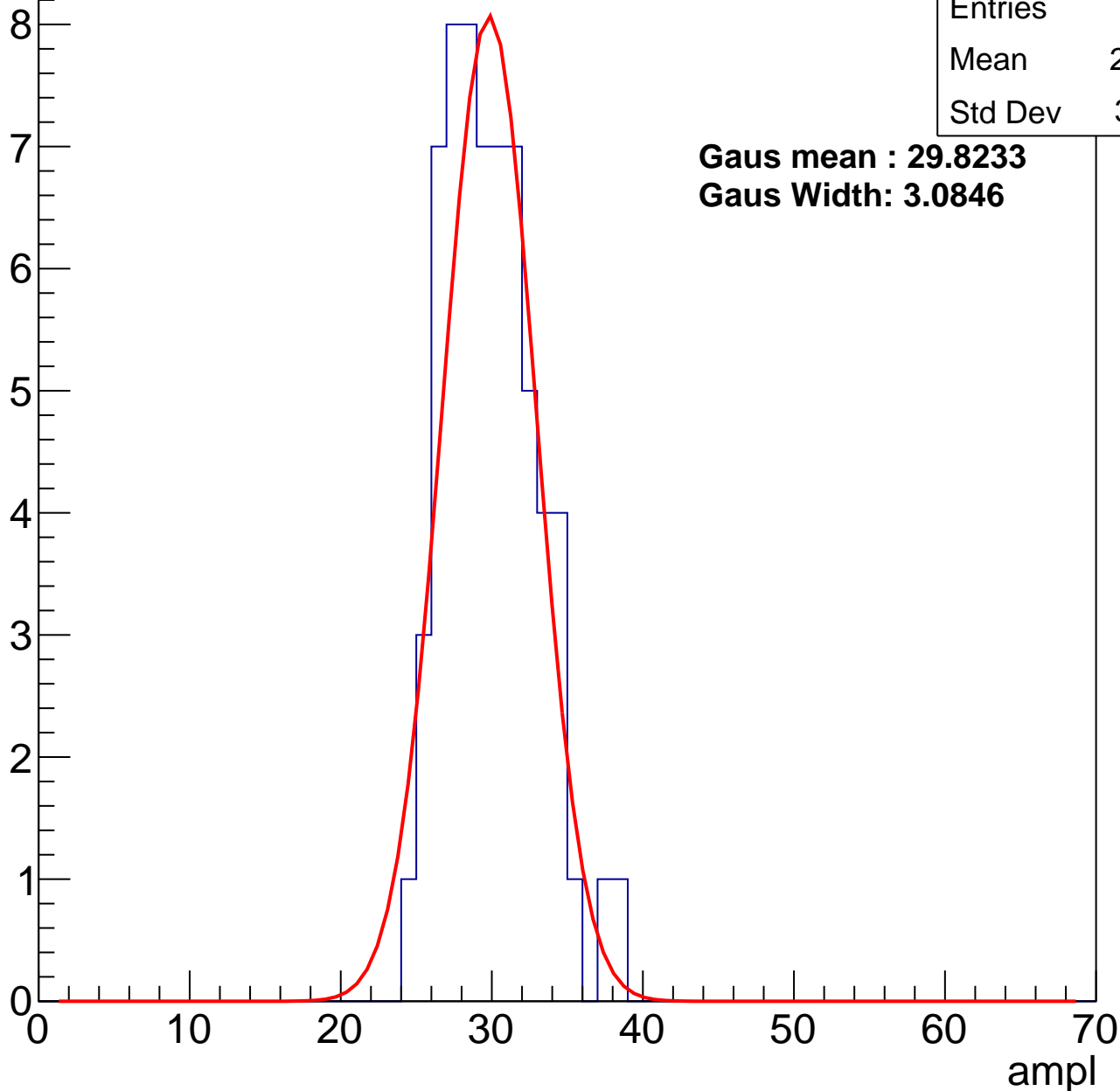
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.52
Std Dev	3.021

**Gaus mean : 29.8233**

**Gaus Width: 3.0846**



# B1L000S, U21-ch22, adc1

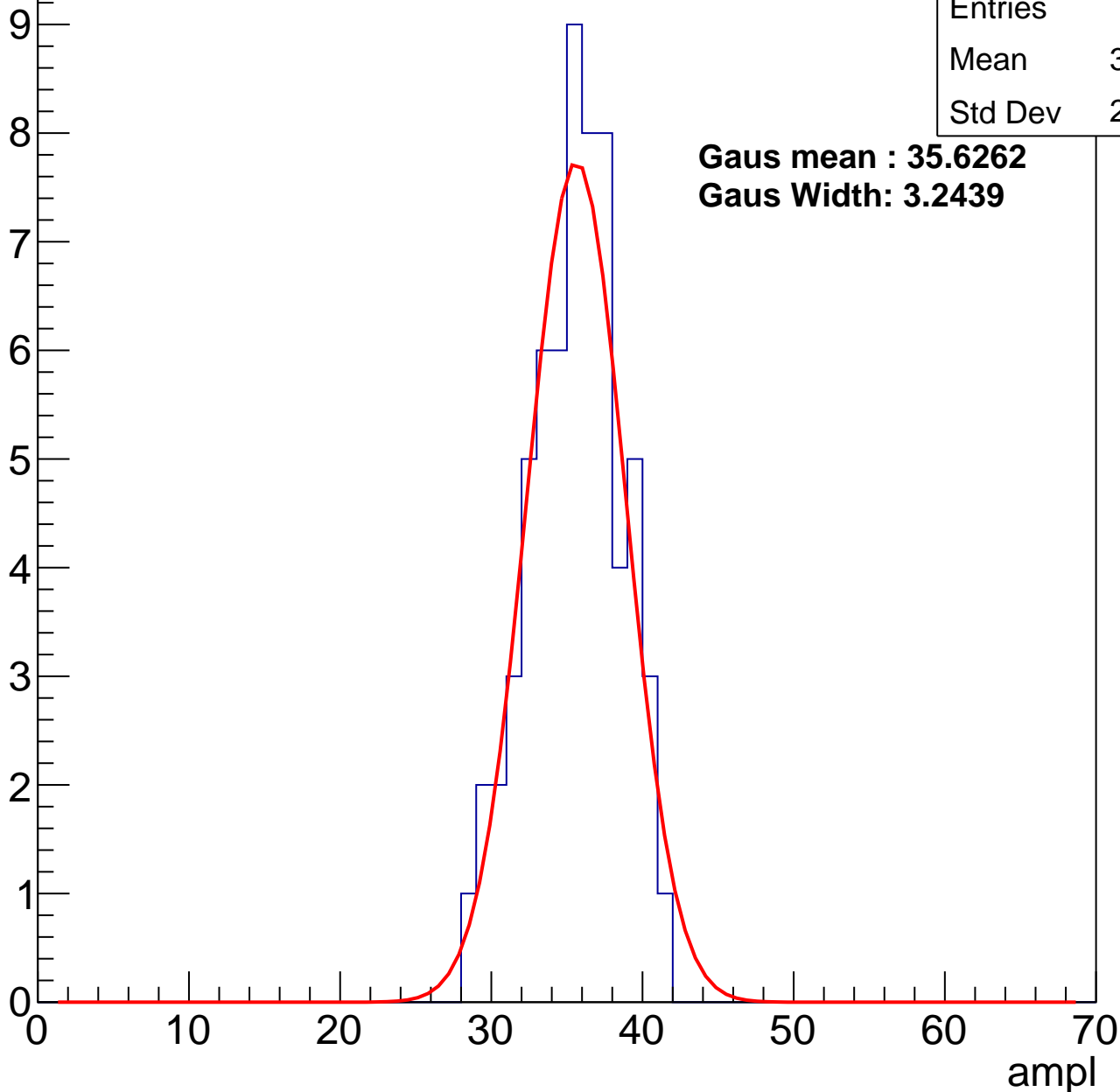
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.05
Std Dev	2.984

**Gaus mean : 35.6262**

**Gaus Width: 3.2439**



# B1L000S, U21-ch22, adc2

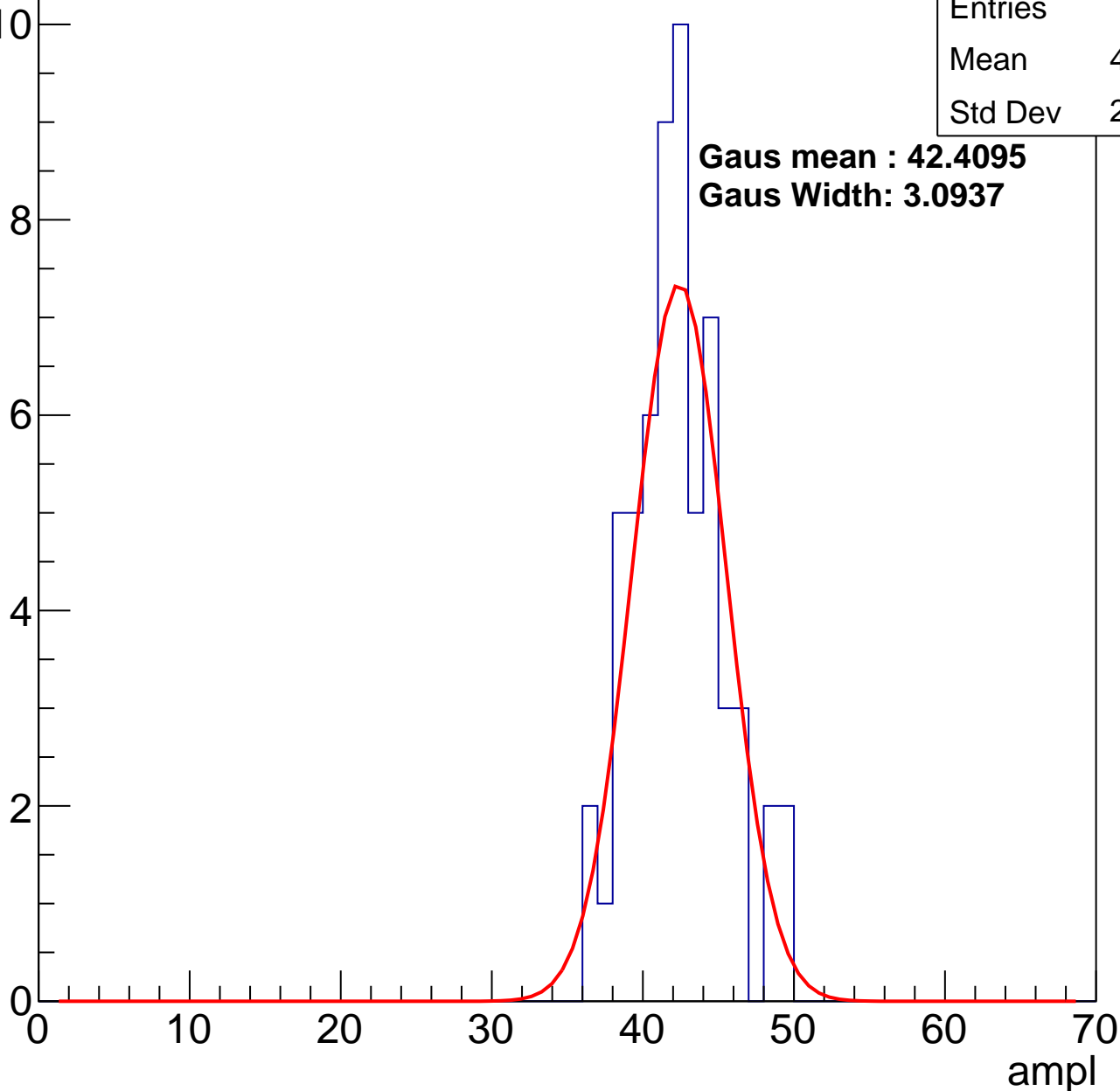
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.88
Std Dev	2.978

**Gaus mean : 42.4095**

**Gaus Width: 3.0937**



# B1L000S, U21-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

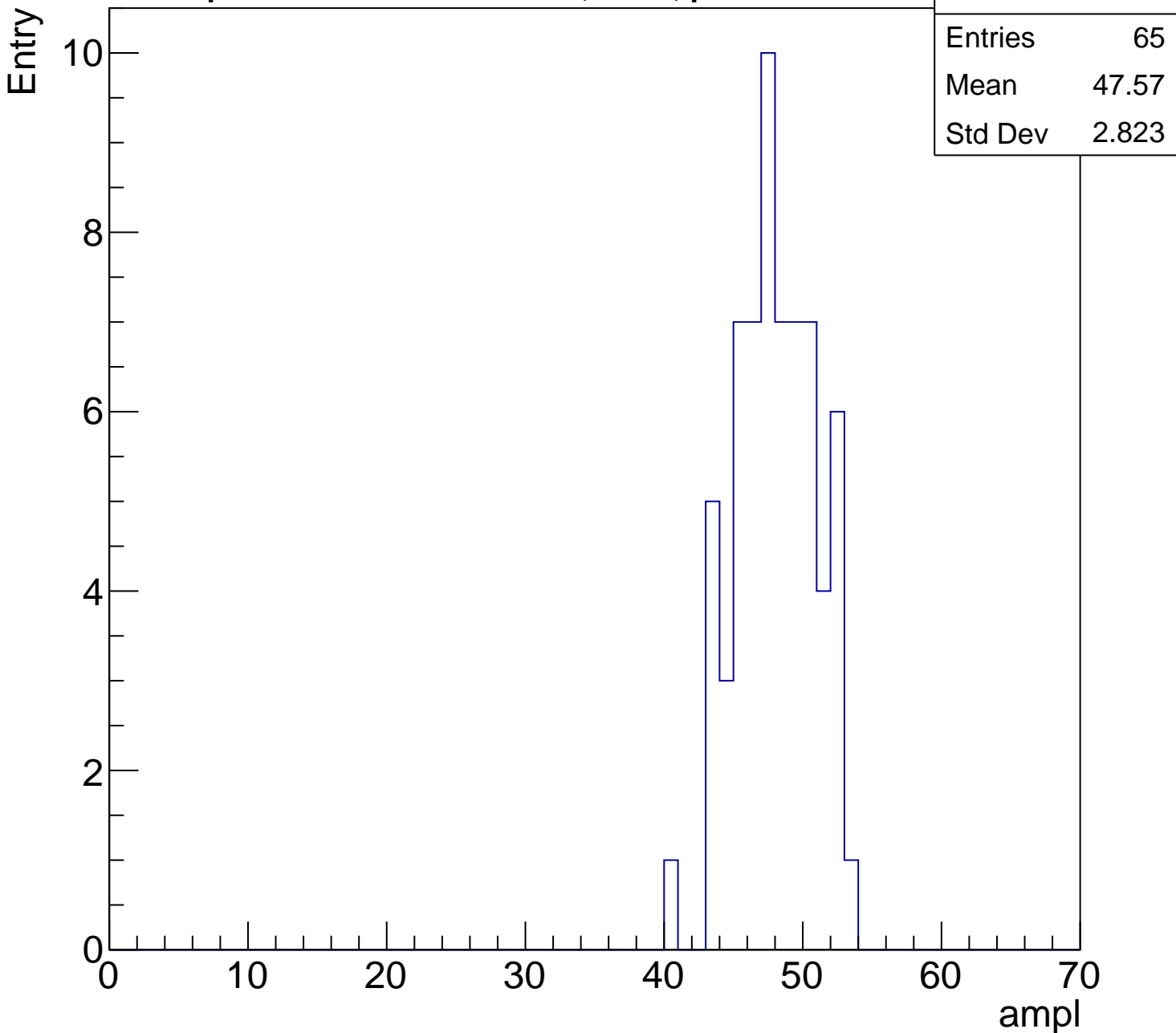
Entries	65
Mean	47.57
Std Dev	2.823

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

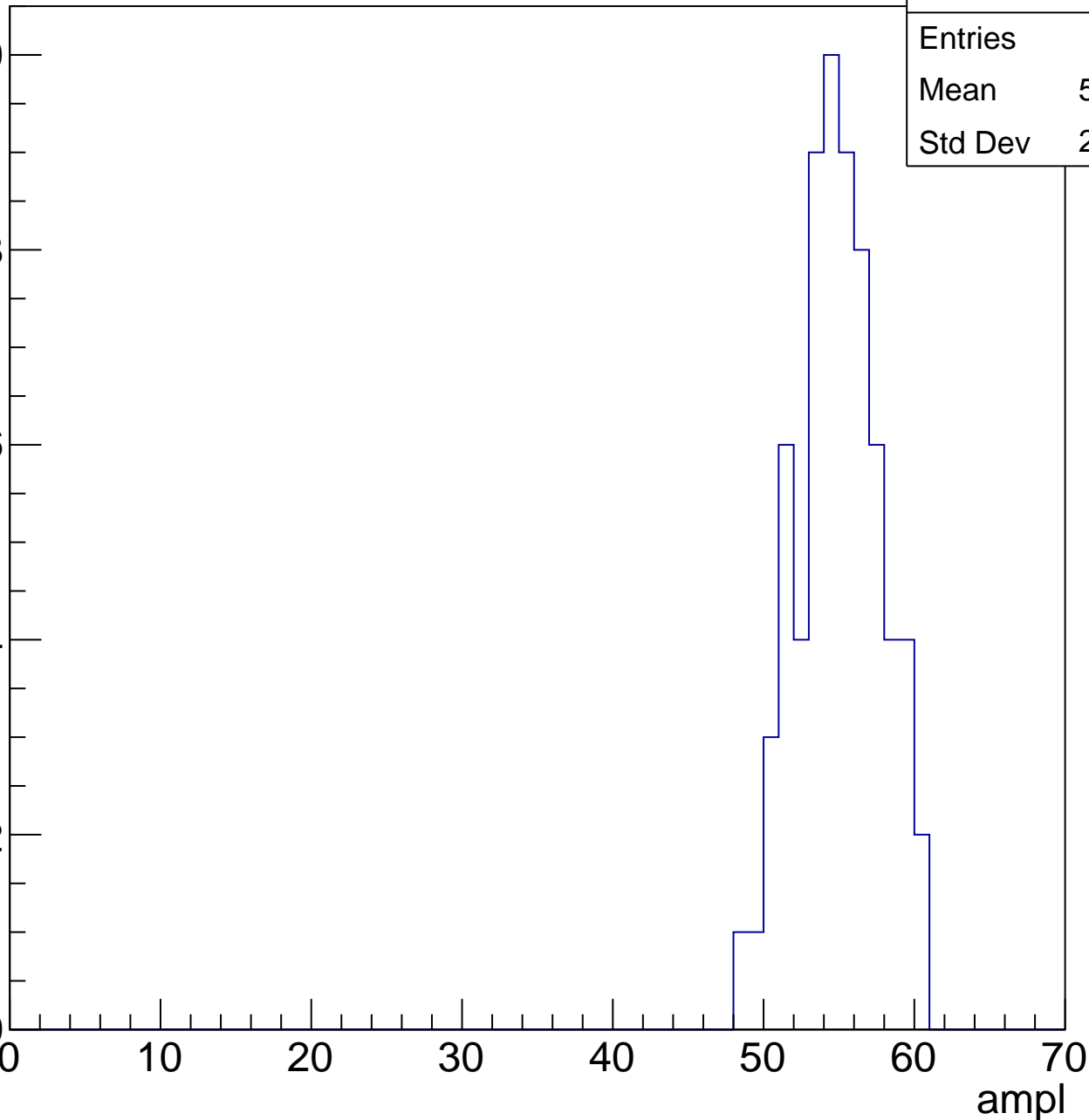
40

50

60

ampl

Entries	67
Mean	54.49
Std Dev	2.739

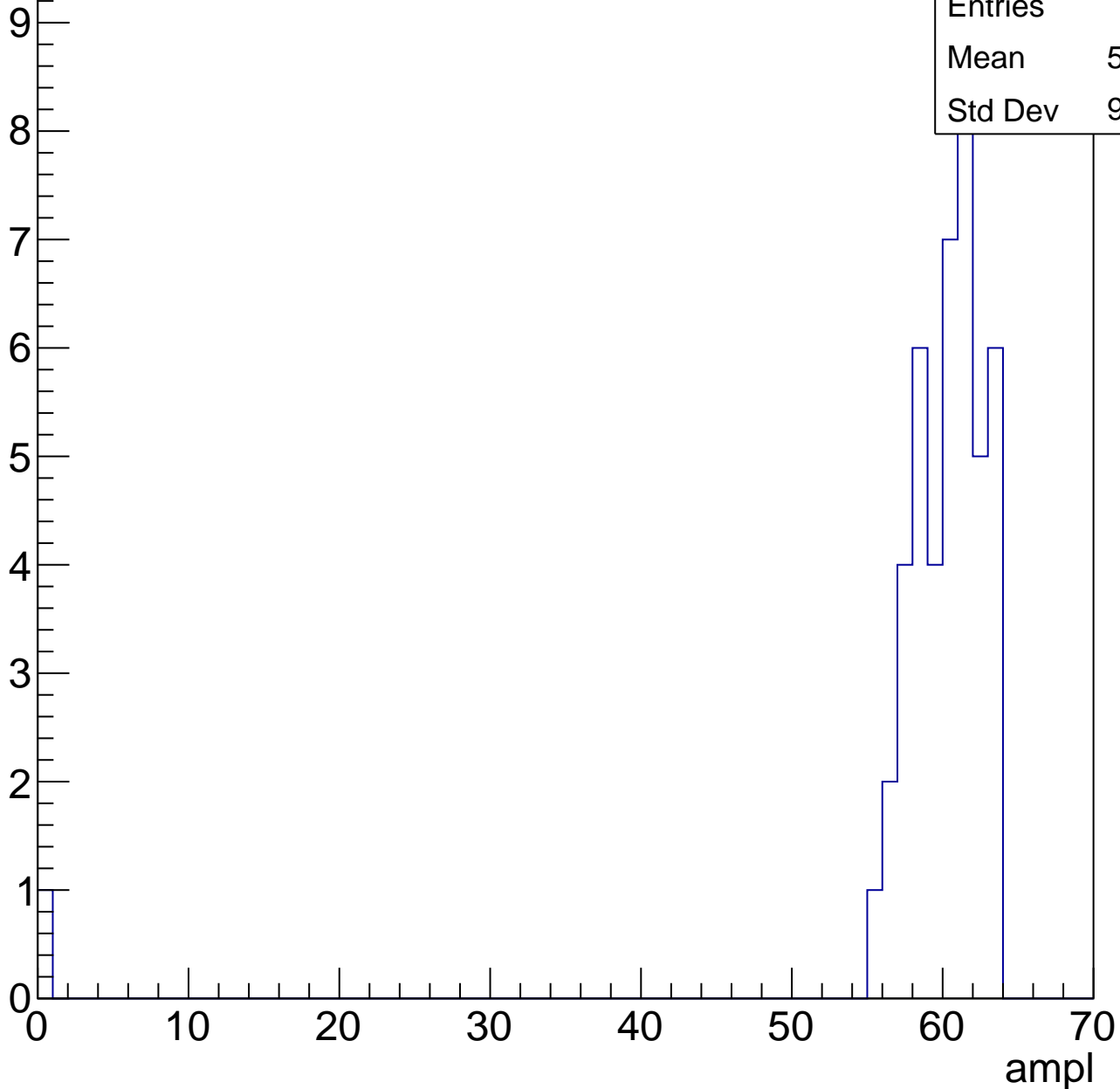


# B1L000S, U21-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	58.58
Std Dev	9.084

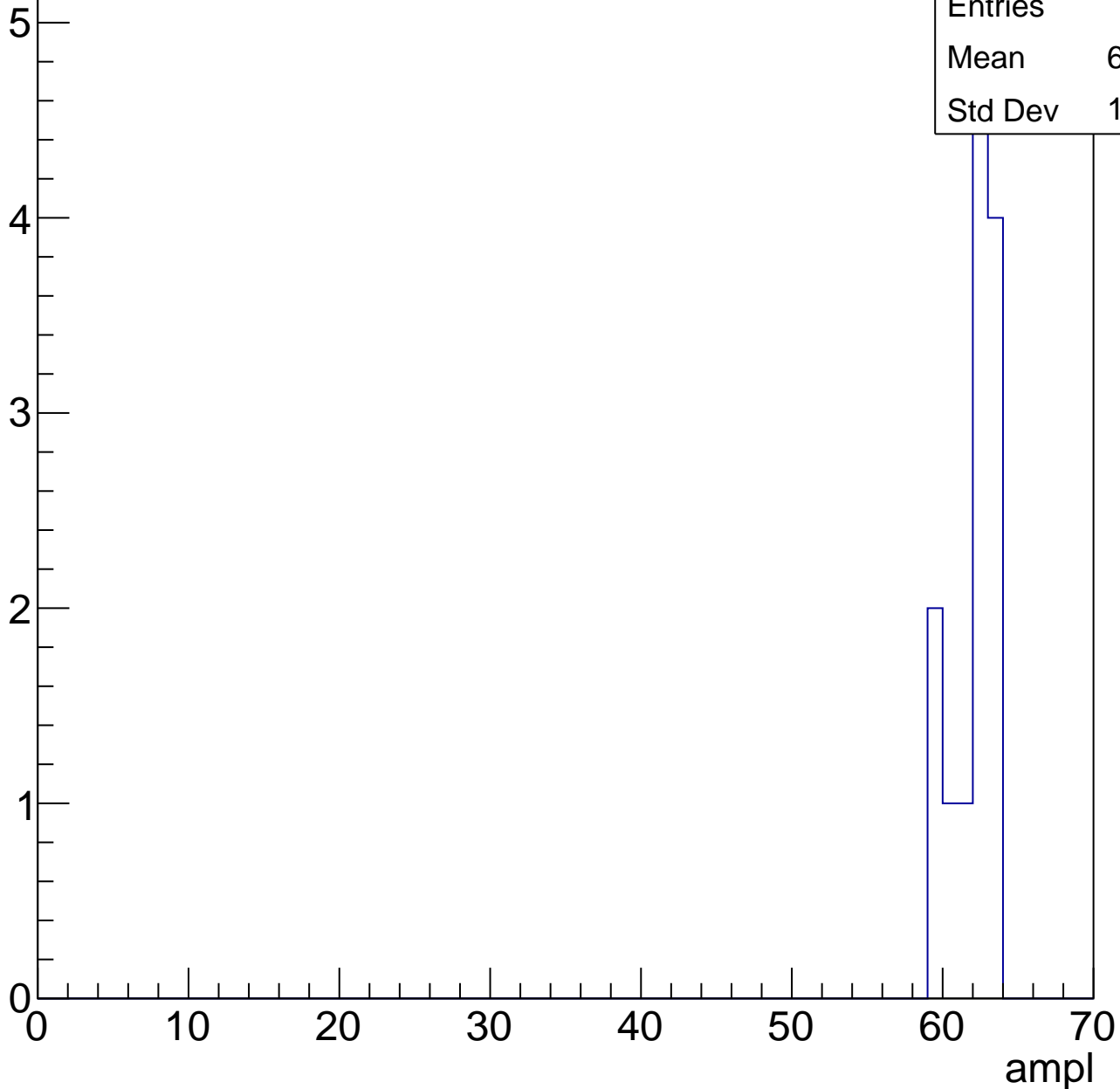


# B1L000S, U21-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	61.62
Std Dev	1.389





# B1L000S, U21-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	8
Std Dev	11.31

# B1L000S, U21-ch23, adc0

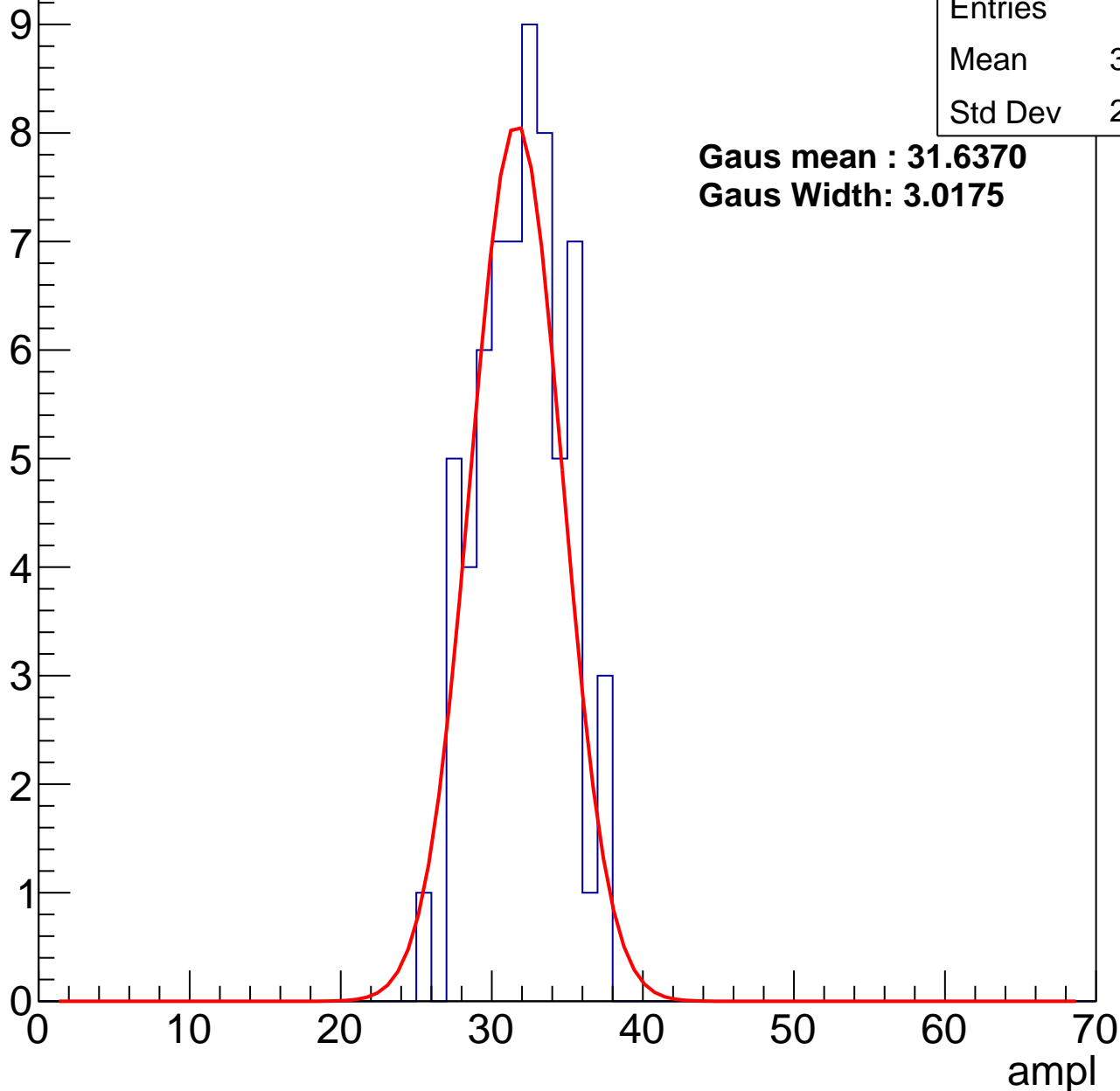
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	31.54
Std Dev	2.805

**Gaus mean : 31.6370**

**Gaus Width: 3.0175**



# B1L000S, U21-ch23, adc1

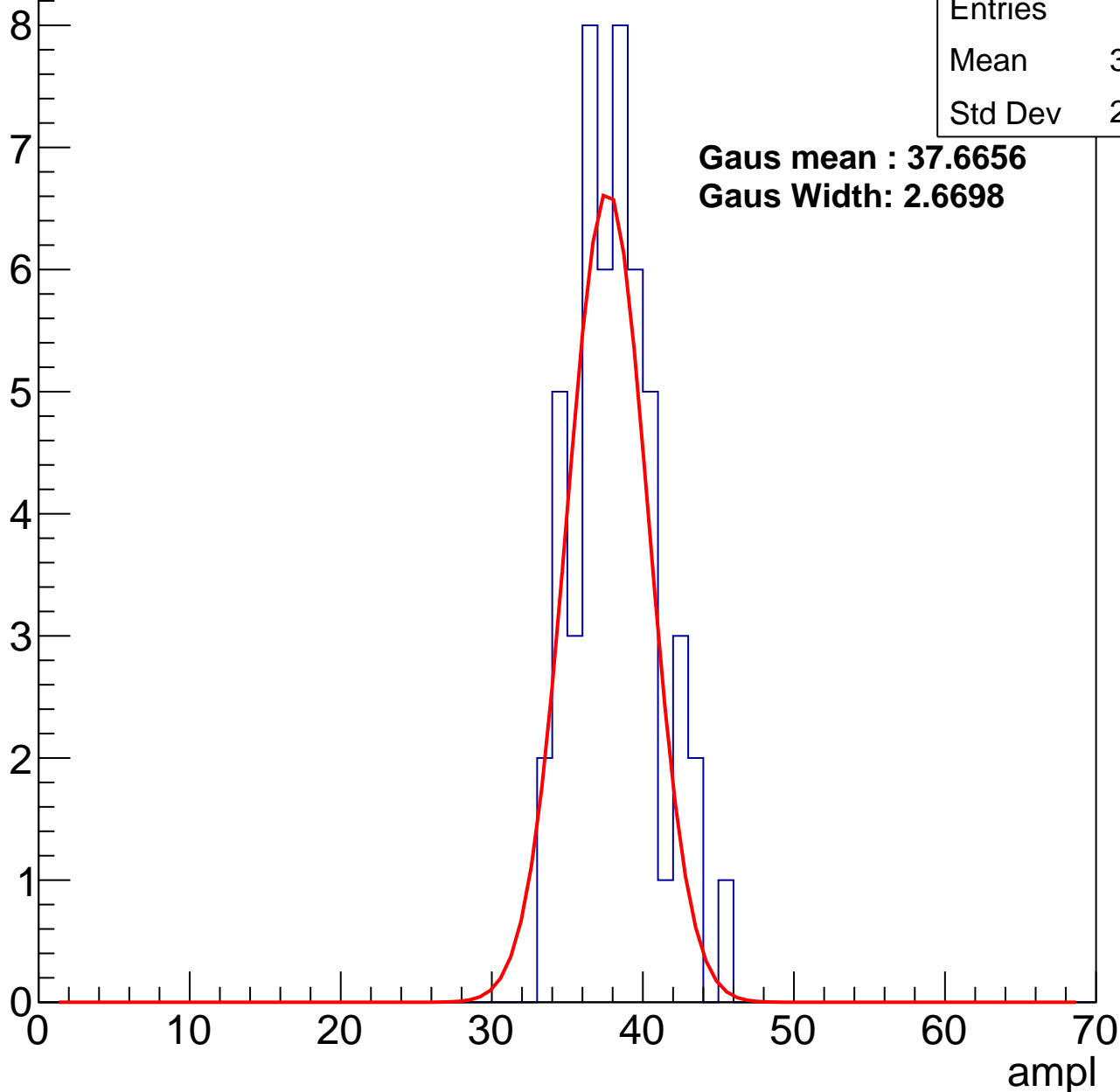
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	37.74
Std Dev	2.734

**Gaus mean : 37.6656**

**Gaus Width: 2.6698**



# B1L000S, U21-ch23, adc2

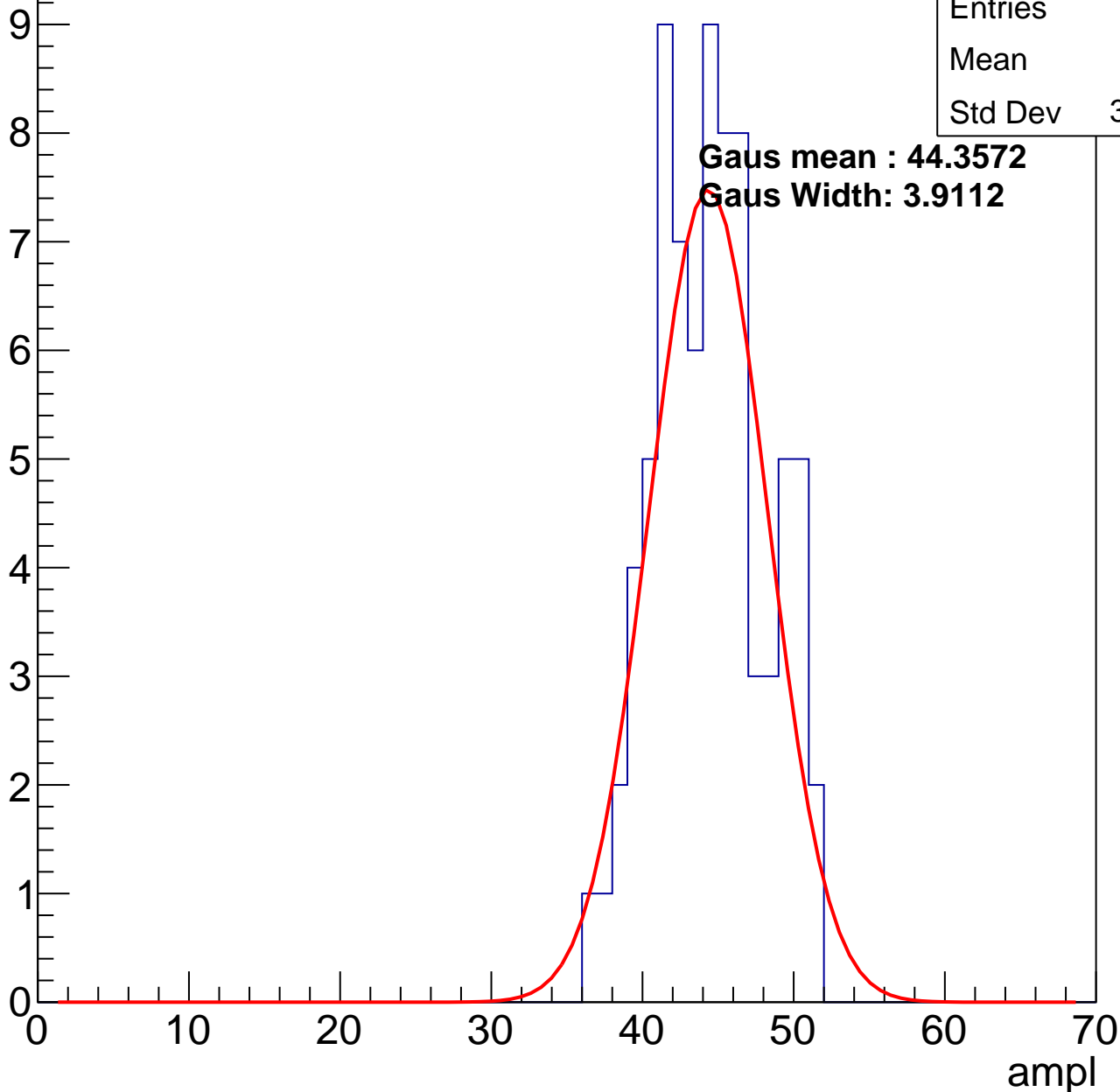
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	44
Std Dev	3.577

**Gaus mean : 44.3572**

**Gaus Width: 3.9112**

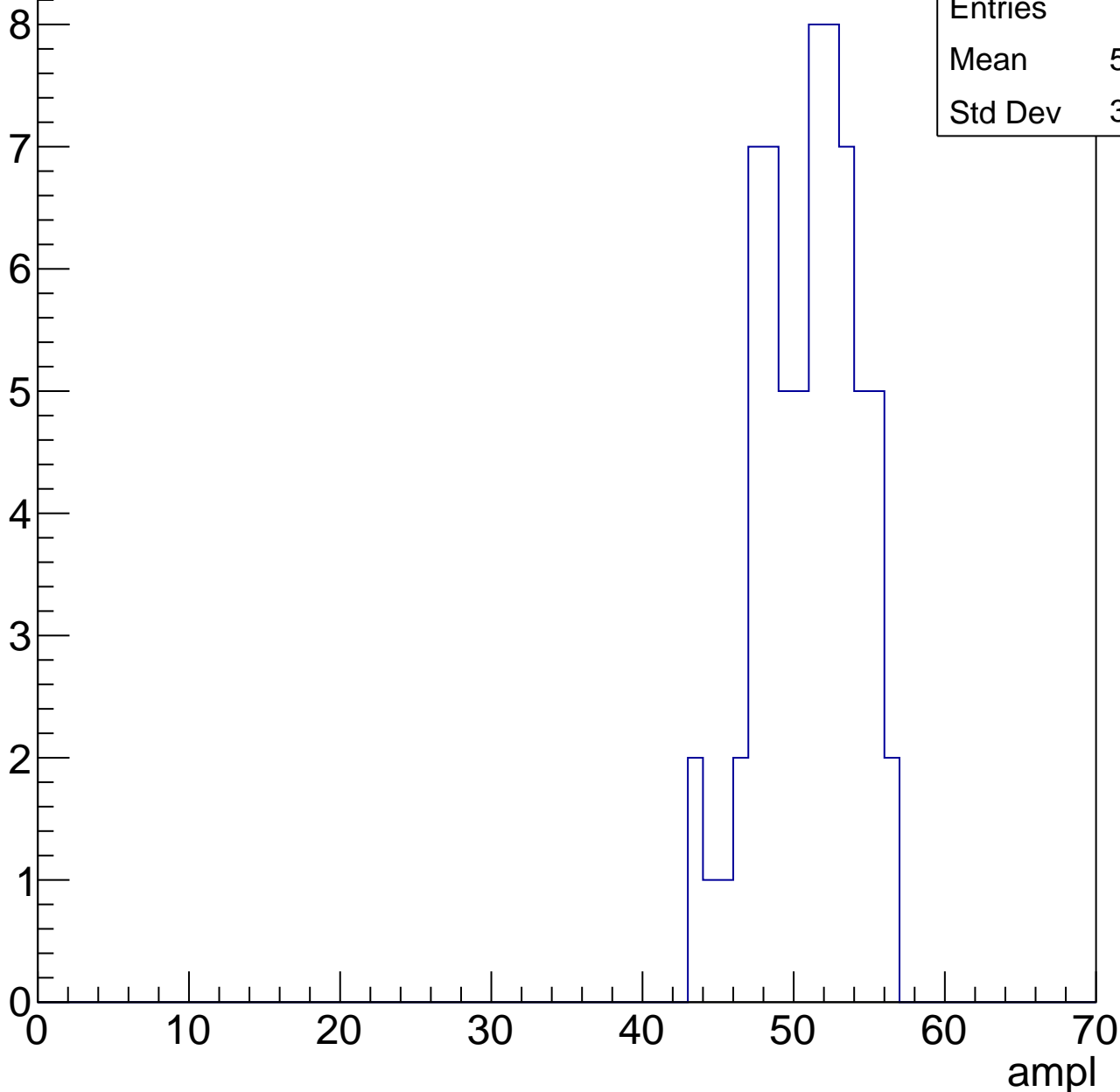


# B1L000S, U21-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

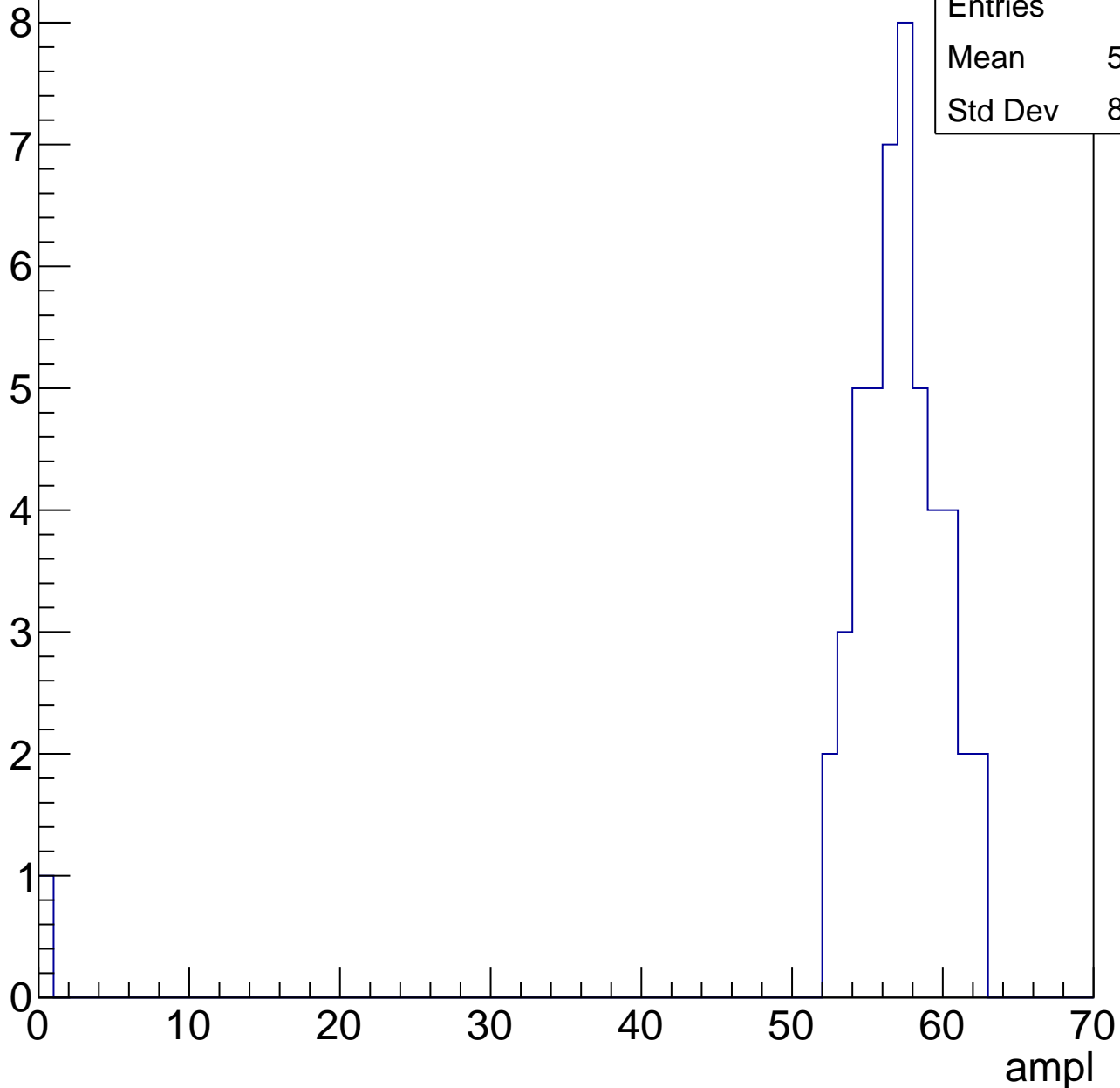
Entries	65
Mean	50.45
Std Dev	3.172



# B1L000S, U21-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	48
Mean	55.58
Std Dev	8.492

# B1L000S, U21-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

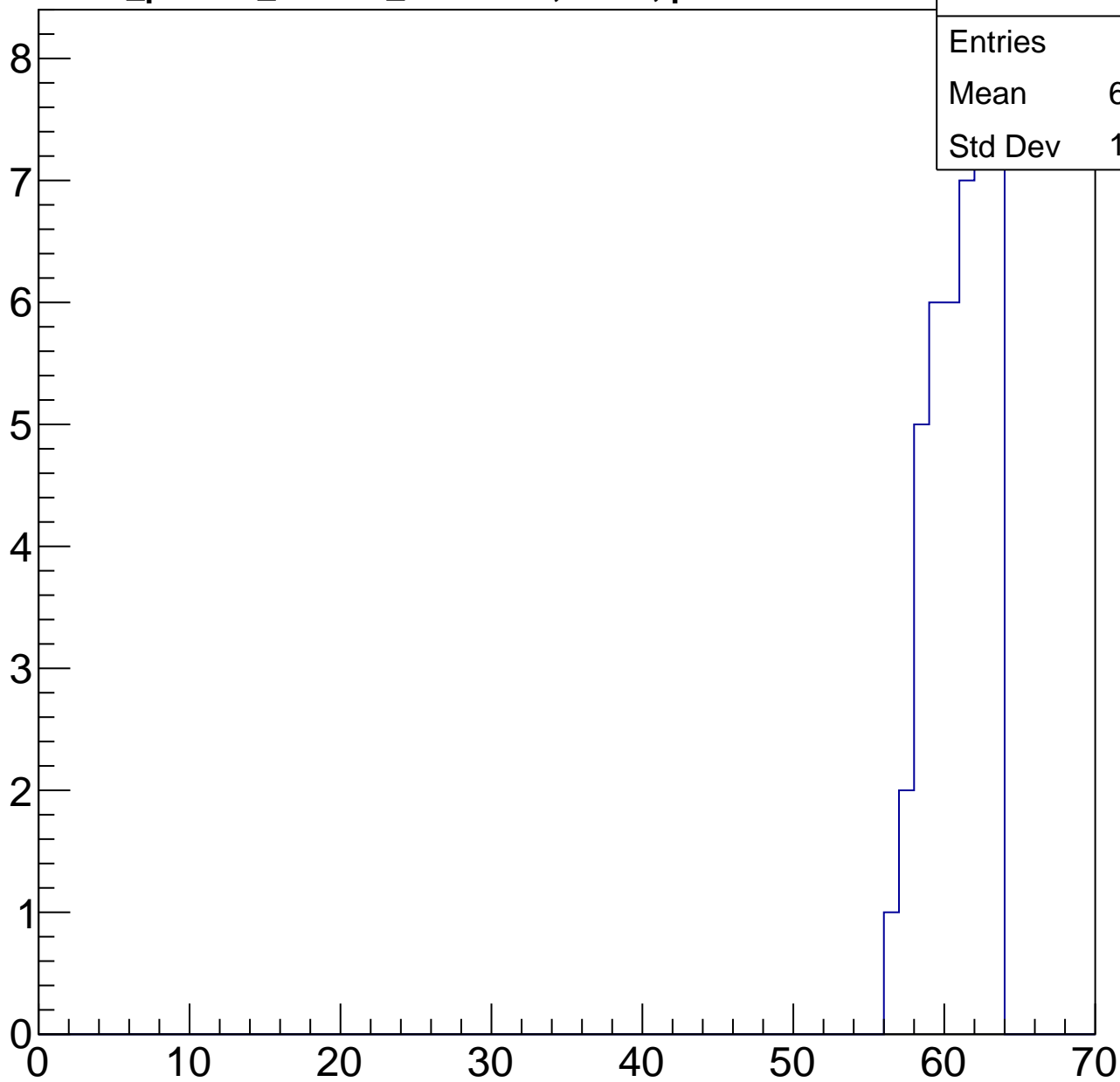
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	60.49
Std Dev	1.933

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

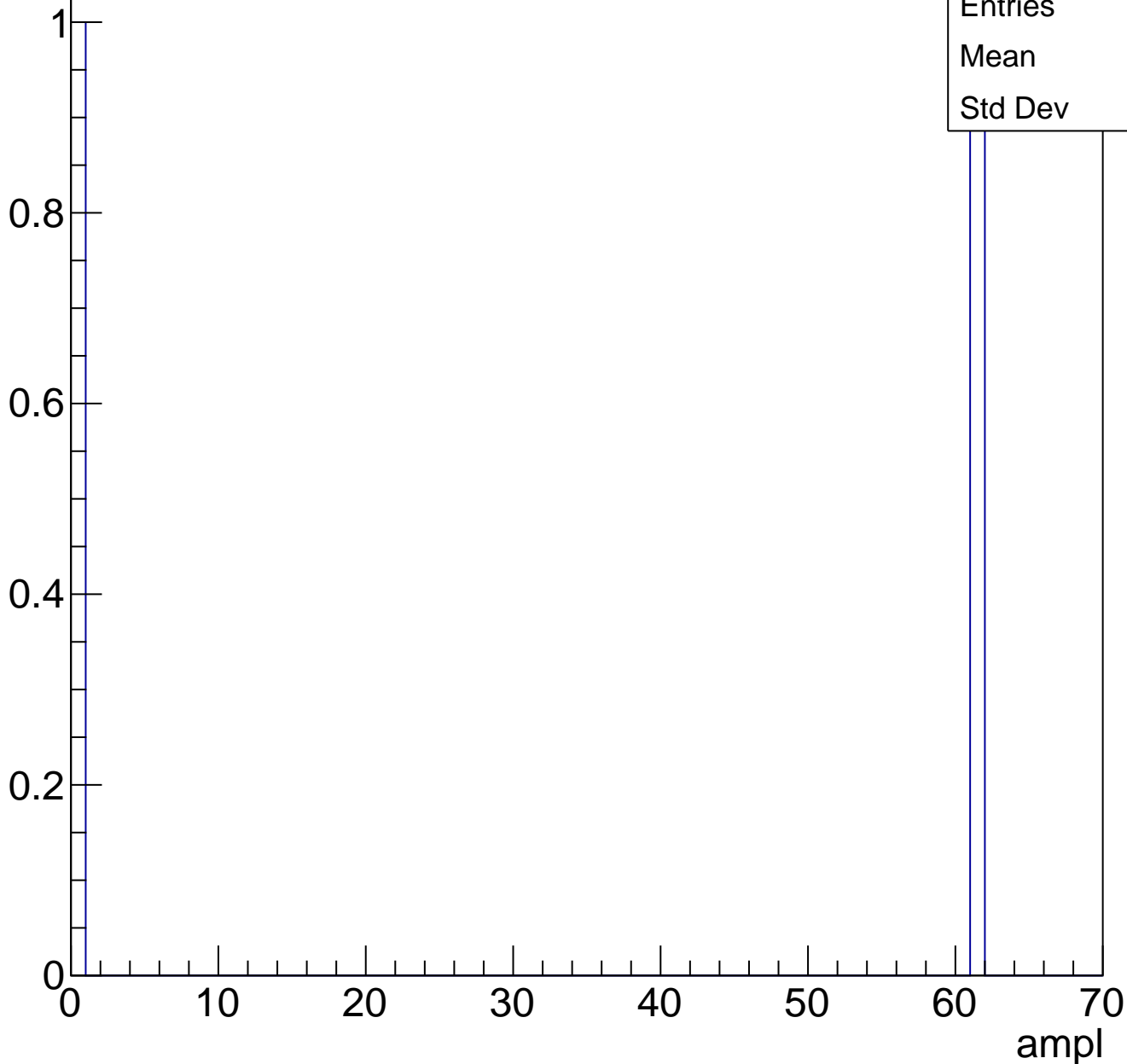




# B1L000S, U21-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch24, adc0

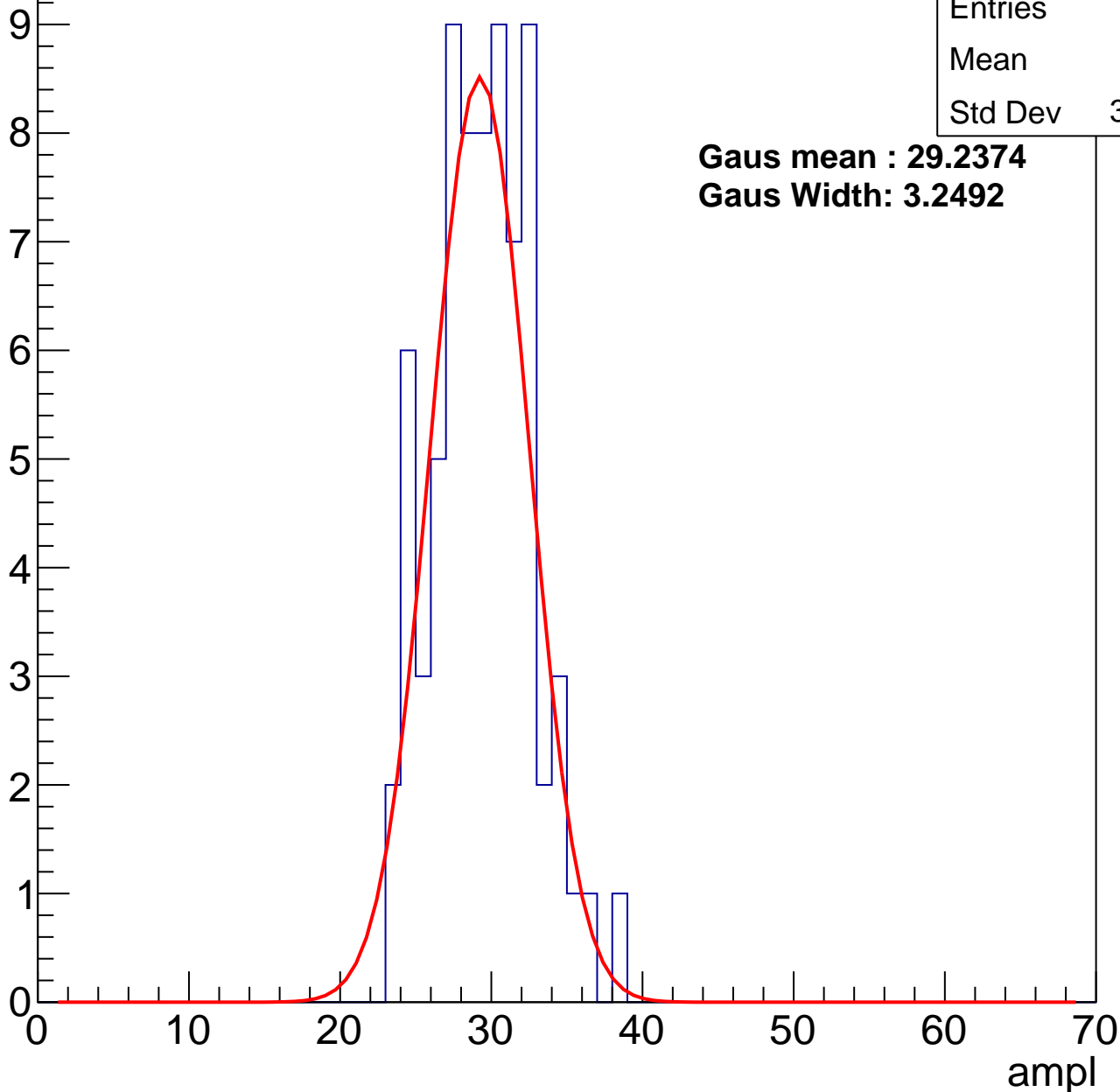
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	29
Std Dev	3.184

**Gaus mean : 29.2374**

**Gaus Width: 3.2492**



# B1L000S, U21-ch24, adc1

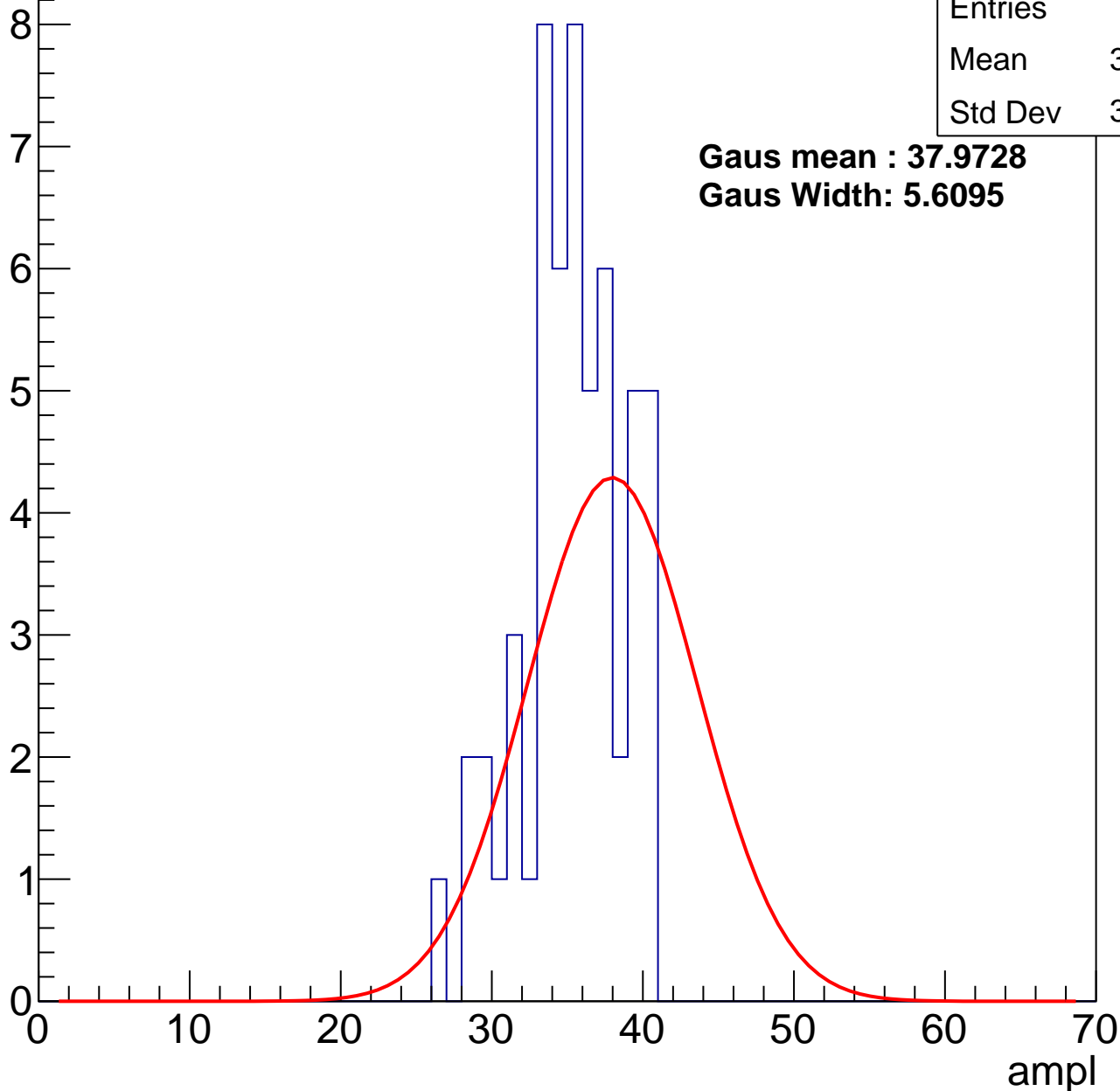
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34.84
Std Dev	3.362

**Gaus mean : 37.9728**

**Gaus Width: 5.6095**



# B1L000S, U21-ch24, adc2

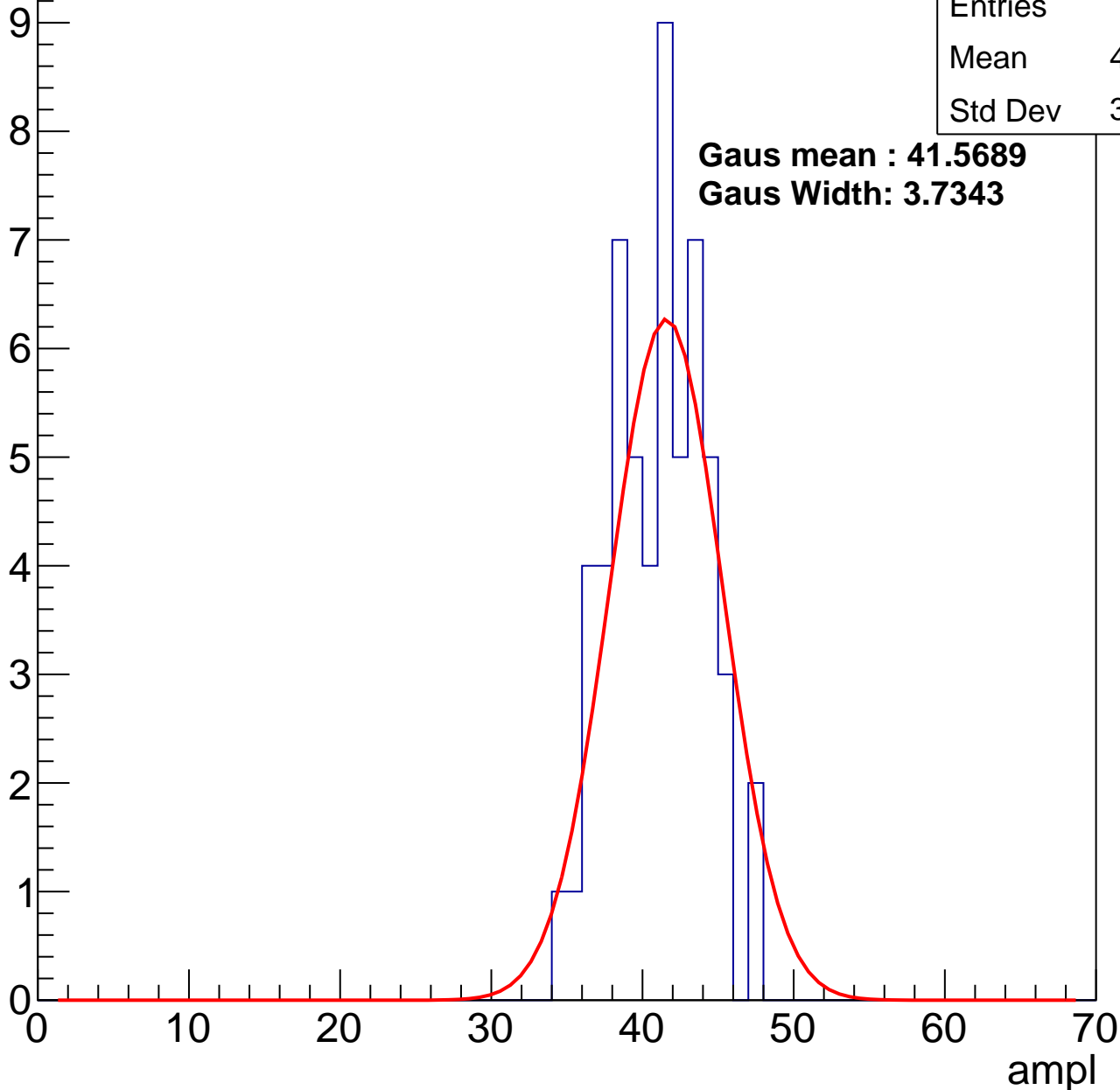
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	40.54
Std Dev	3.027

**Gaus mean : 41.5689**

**Gaus Width: 3.7343**

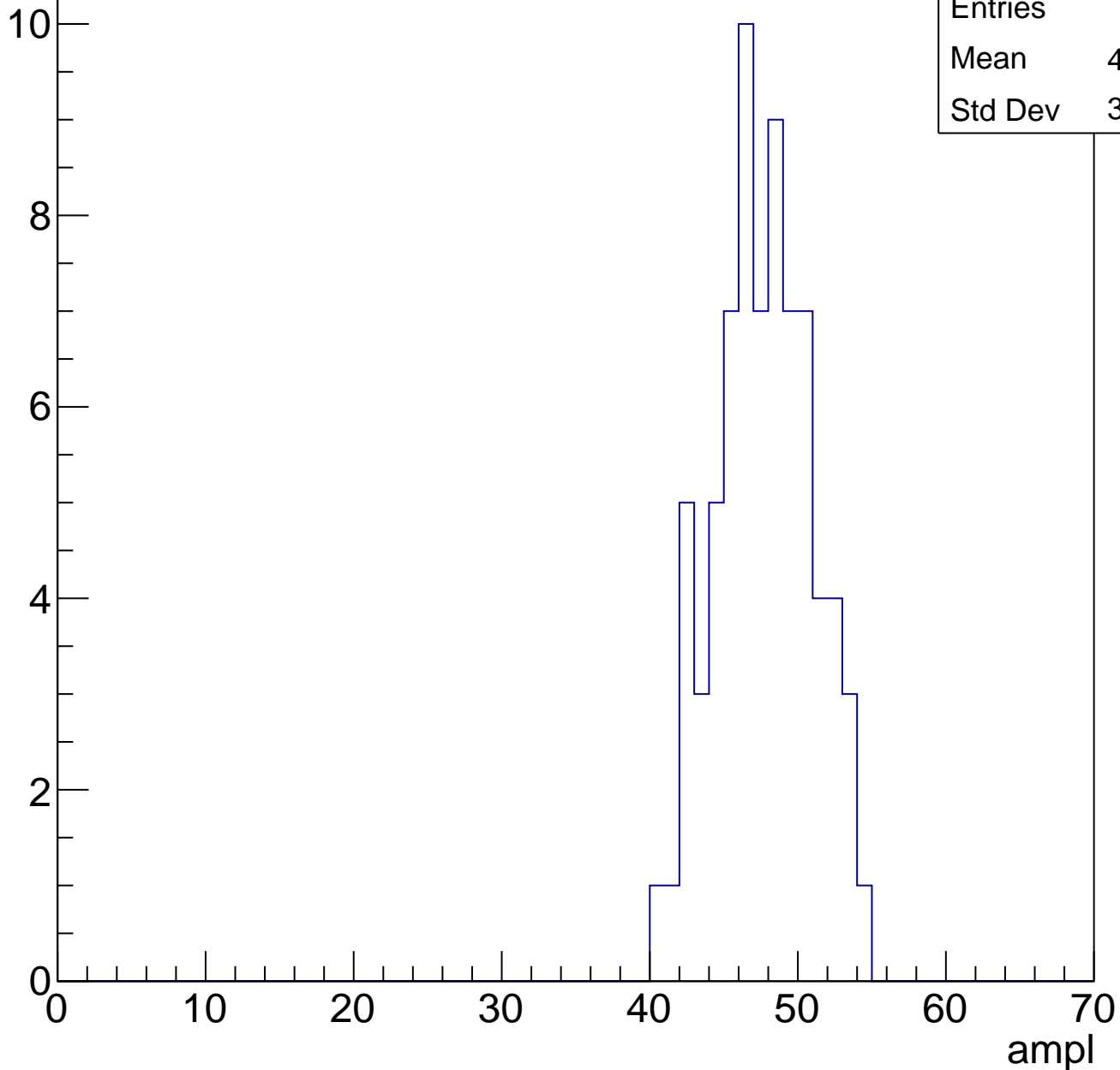


# B1L000S, U21-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	47.22
Std Dev	3.202

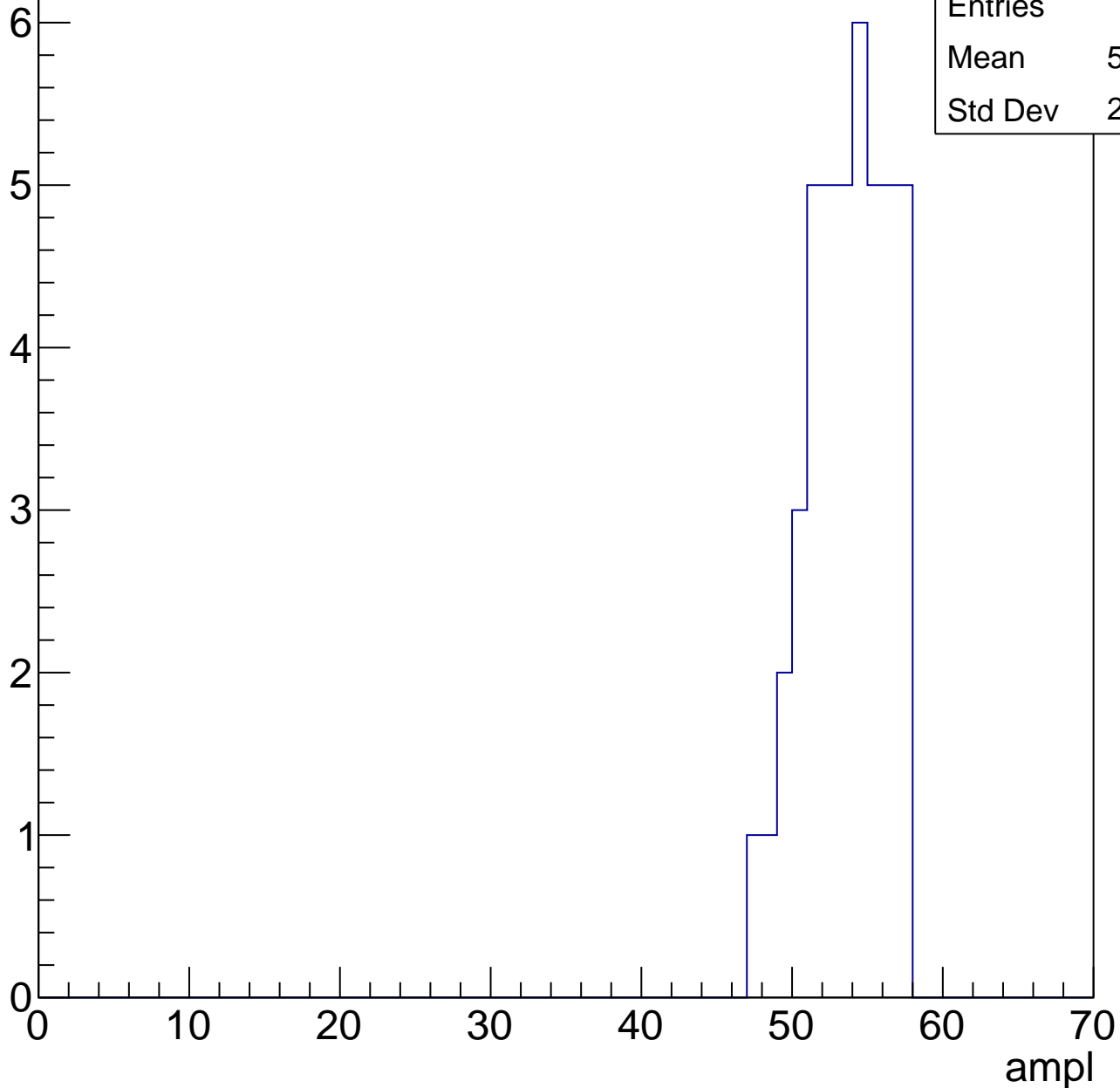
Entry



# B1L000S, U21-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



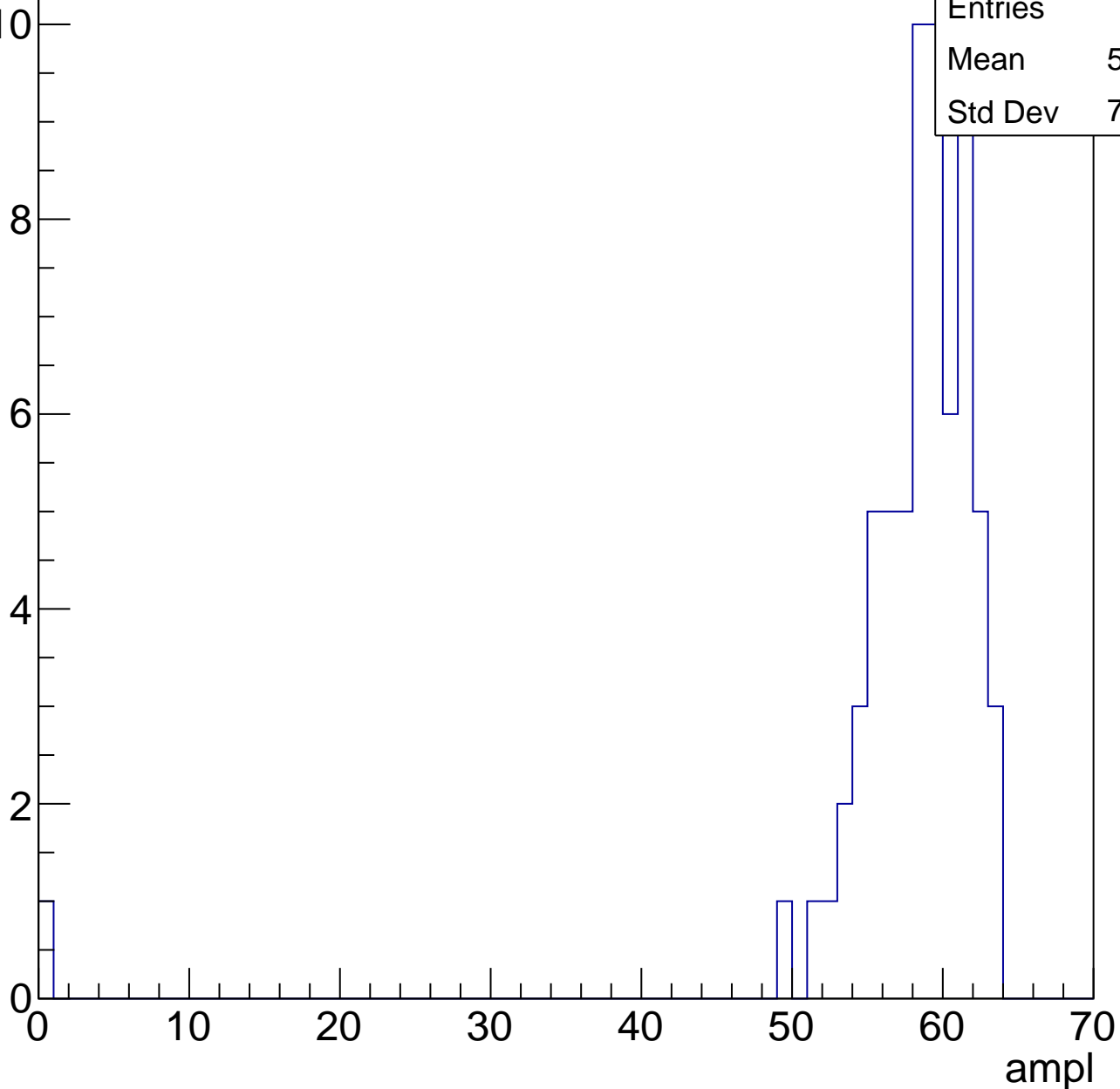
Entries	43
Mean	53.19
Std Dev	2.617

# B1L000S, U21-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	57.28
Std Dev	7.662

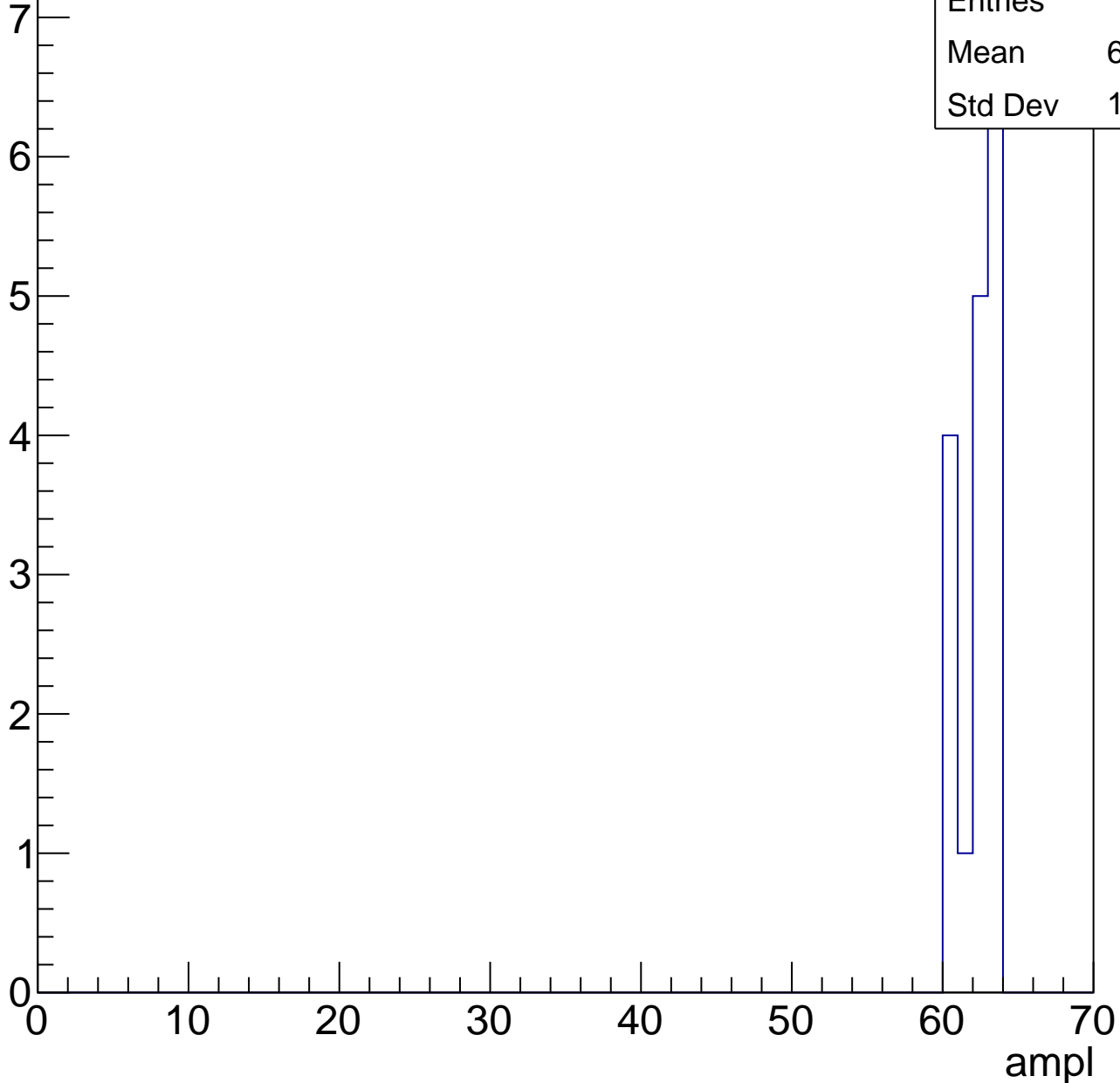


# B1L000S, U21-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	61.88
Std Dev	1.182





# B1L000S, U21-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U21-ch25, adc0

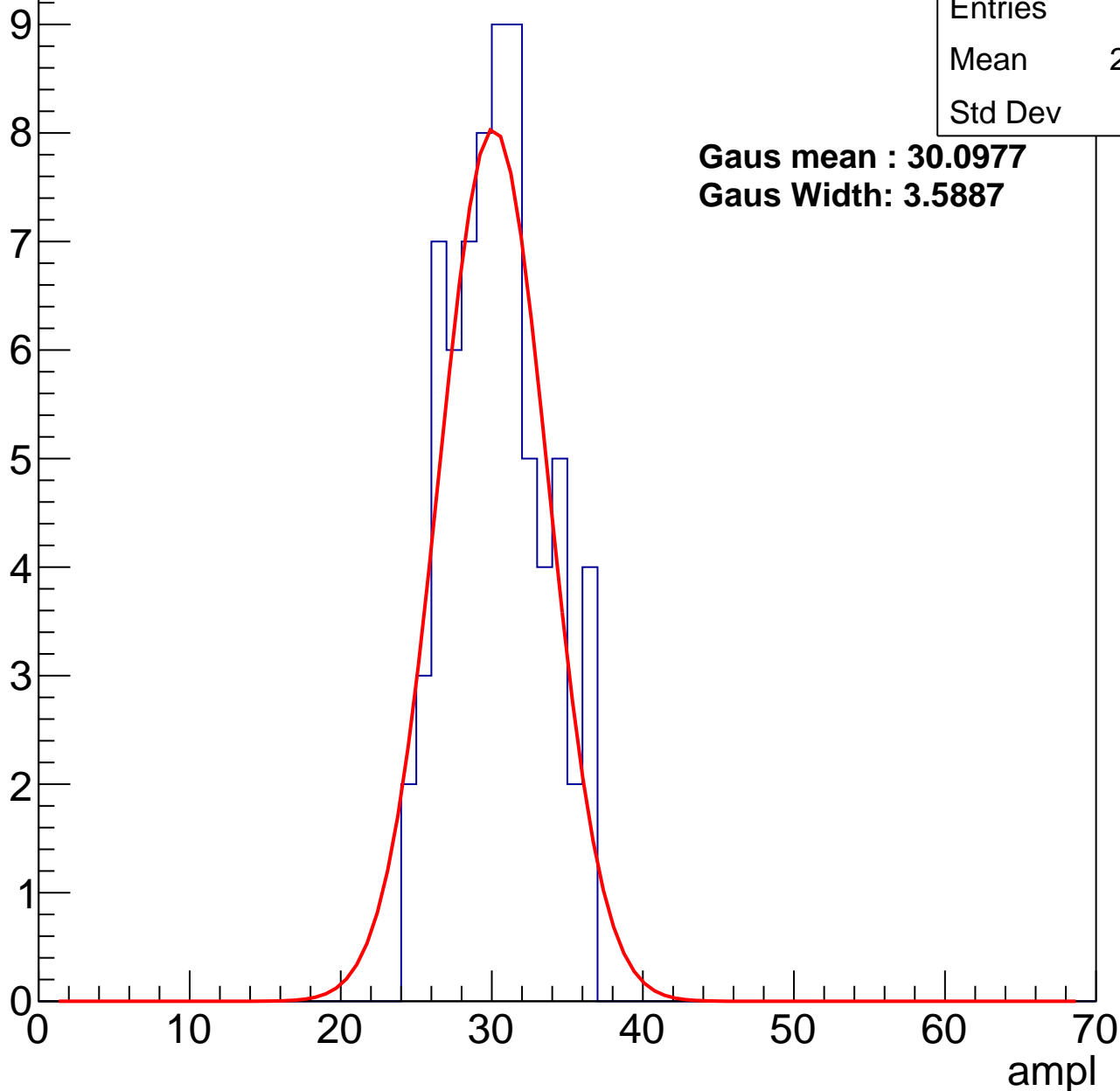
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	29.86
Std Dev	3.11

**Gaus mean : 30.0977**

**Gaus Width: 3.5887**



# B1L000S, U21-ch25, adc1

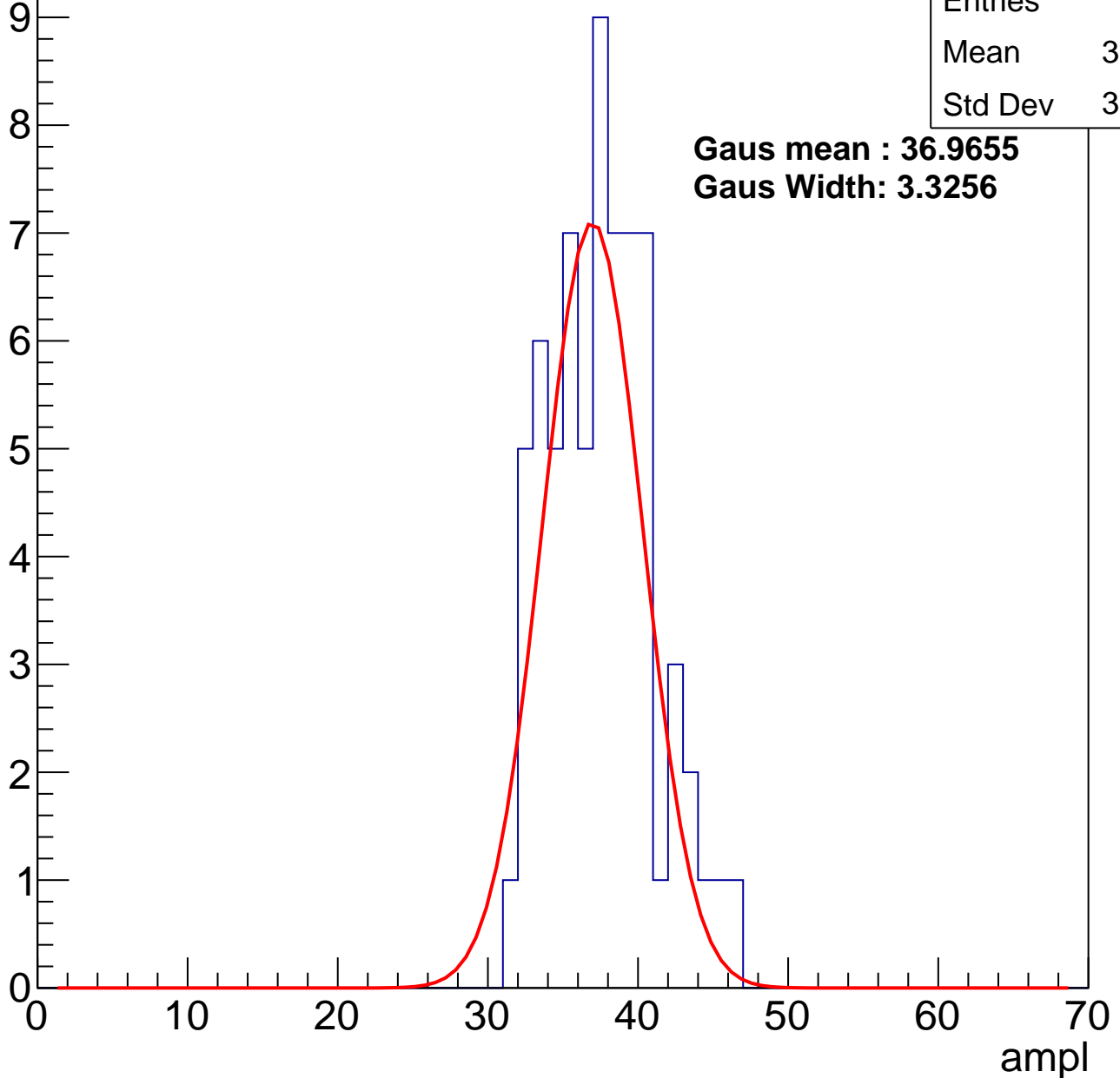
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	37.12
Std Dev	3.406

**Gaus mean : 36.9655**

**Gaus Width: 3.3256**



# B1L000S, U21-ch25, adc2

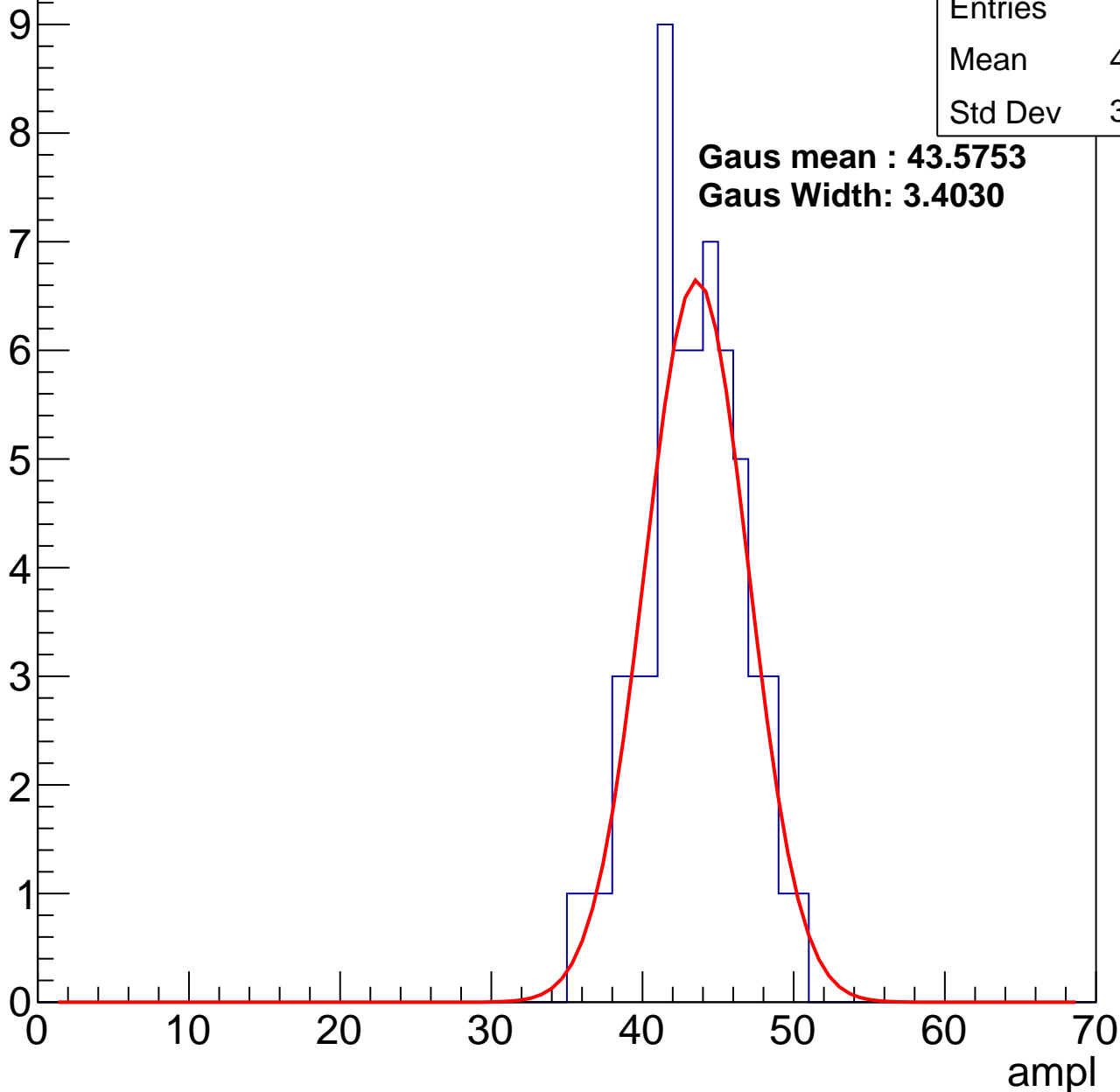
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.88
Std Dev	3.253

**Gaus mean : 43.5753**

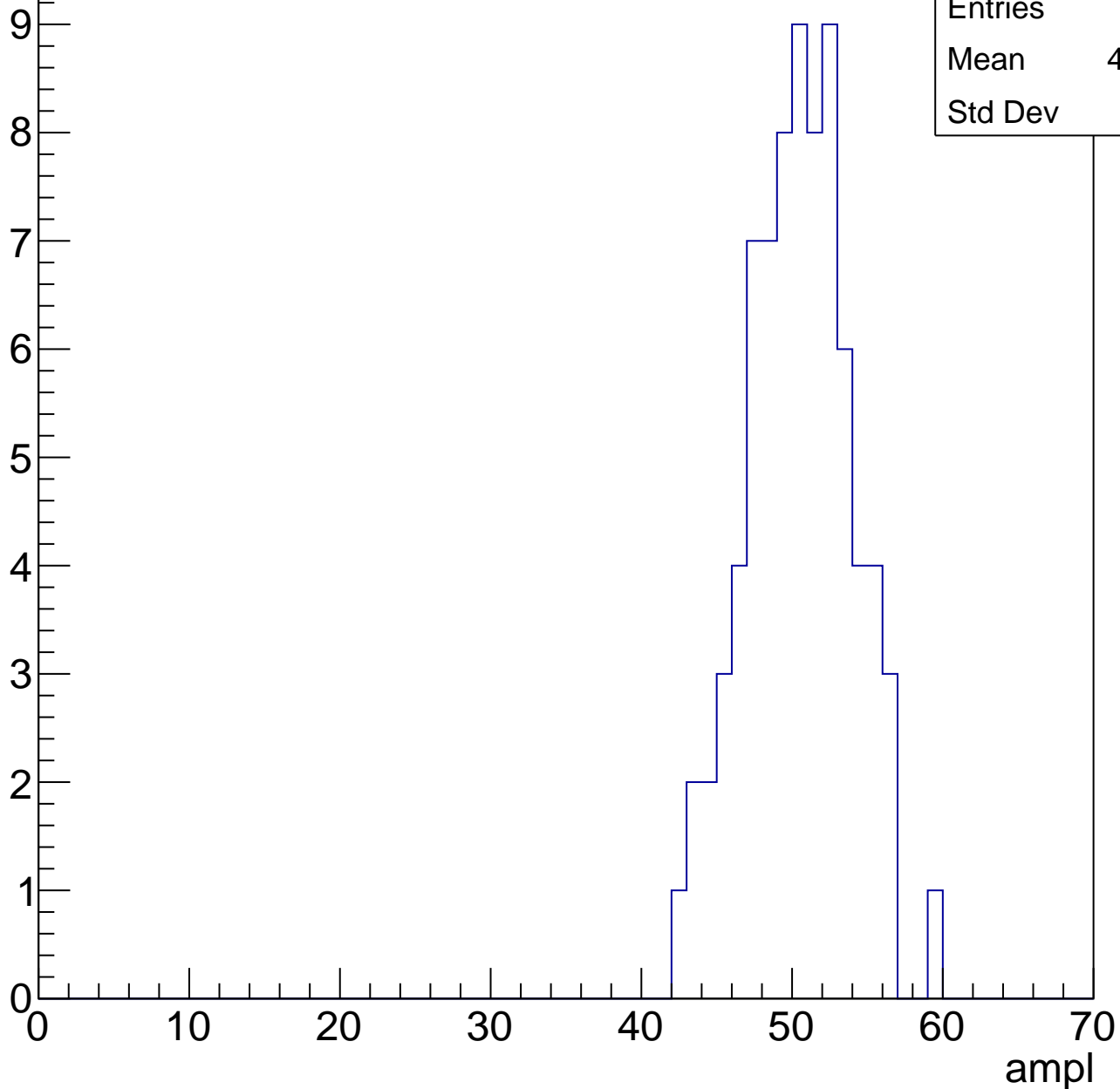
**Gaus Width: 3.4030**



# B1L000S, U21-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

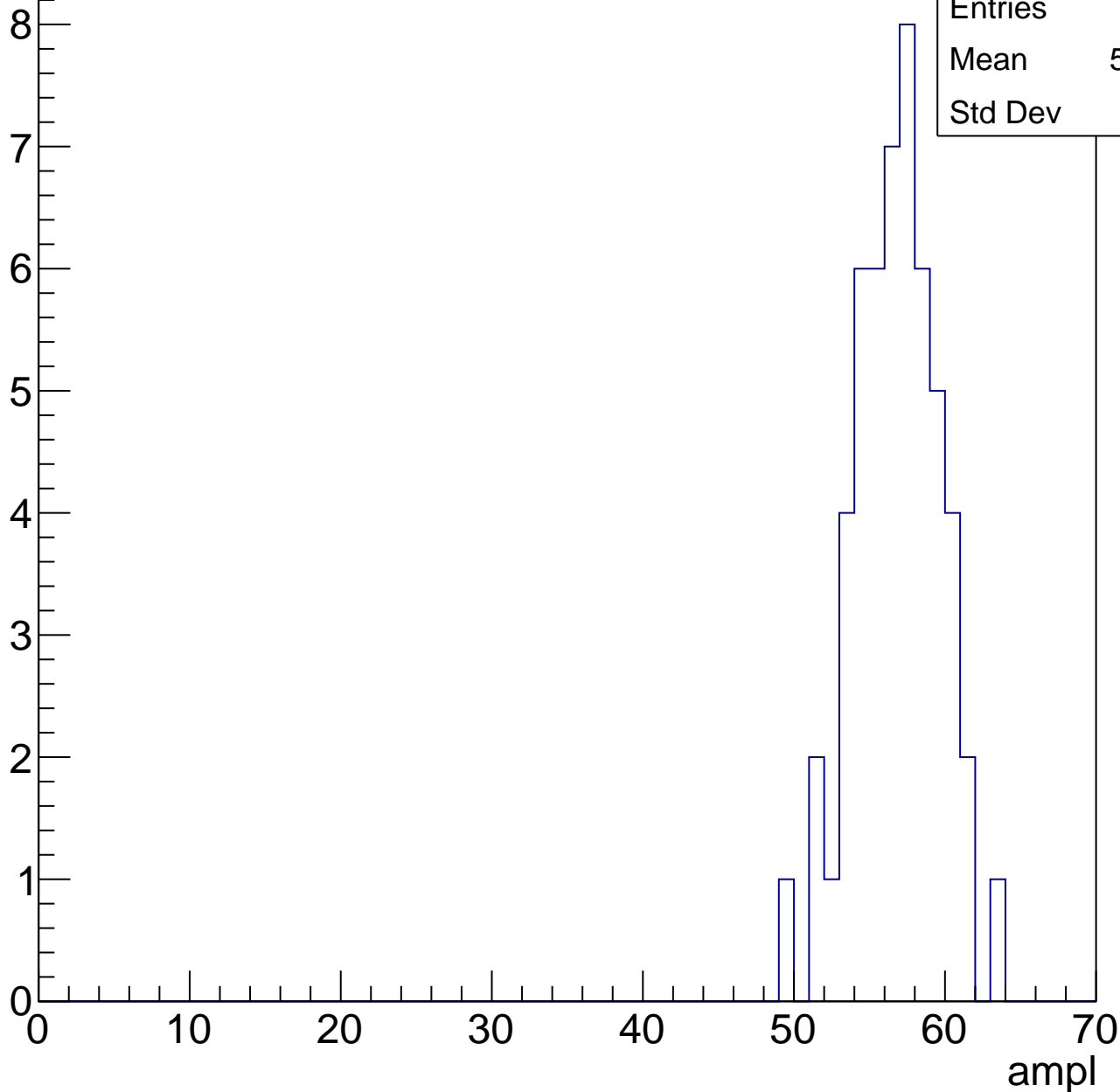


# B1L000S, U21-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	56.32
Std Dev	2.8

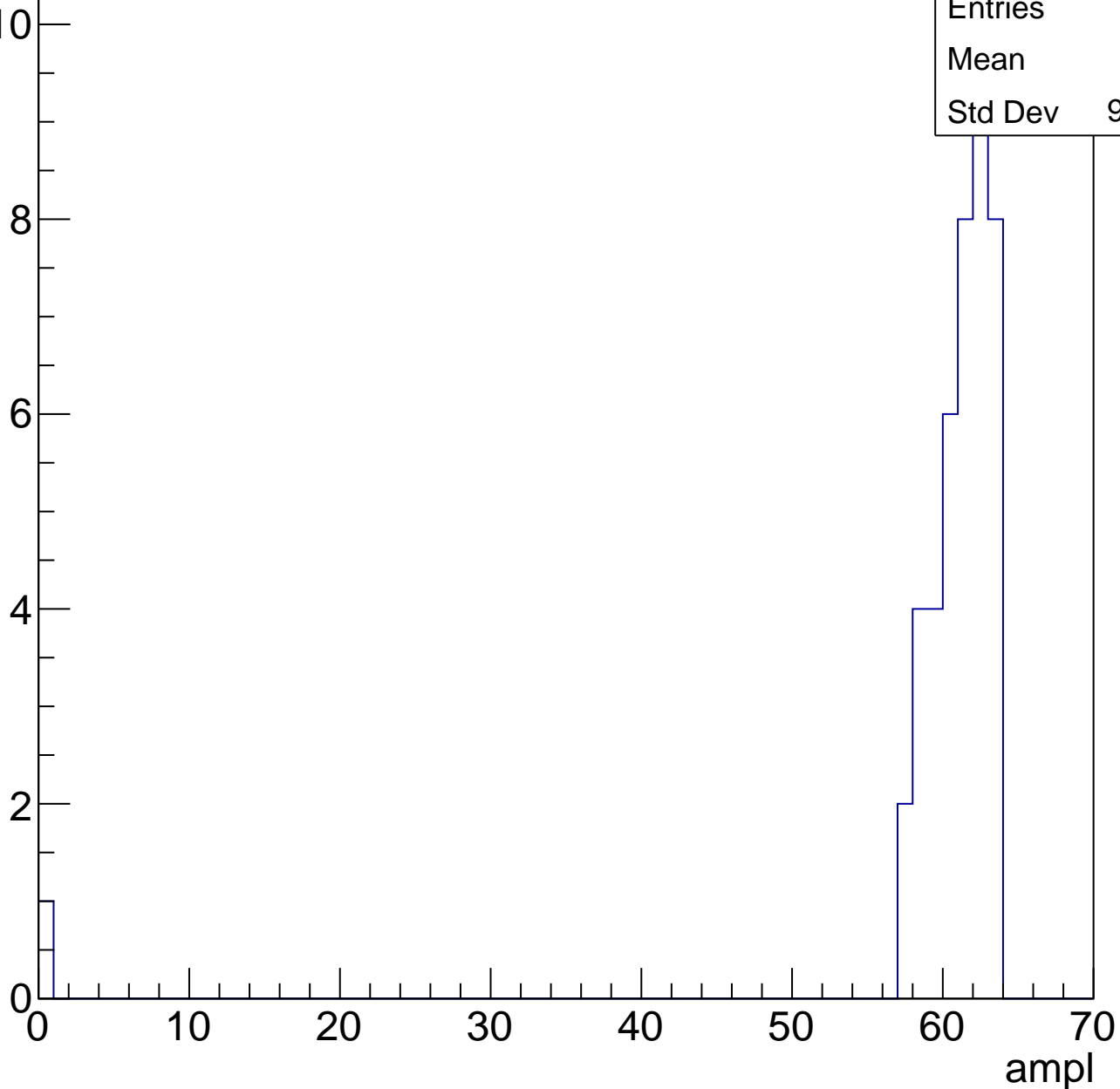


# B1L000S, U21-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	59.4
Std Dev	9.329



# B1L000S, U21-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0



# B1L000S, U21-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U21-ch26, adc0

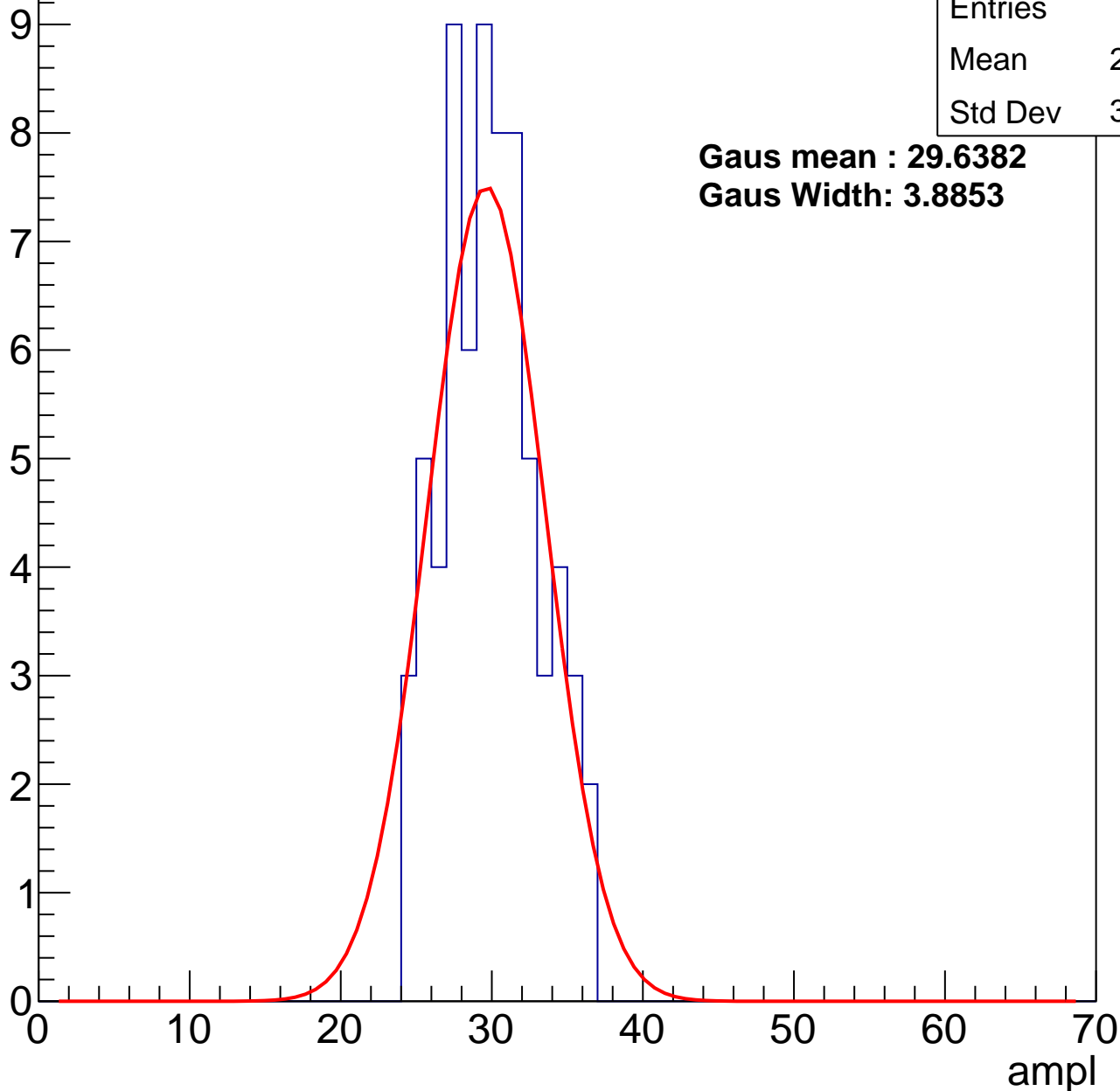
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	29.46
Std Dev	3.086

**Gaus mean : 29.6382**

**Gaus Width: 3.8853**



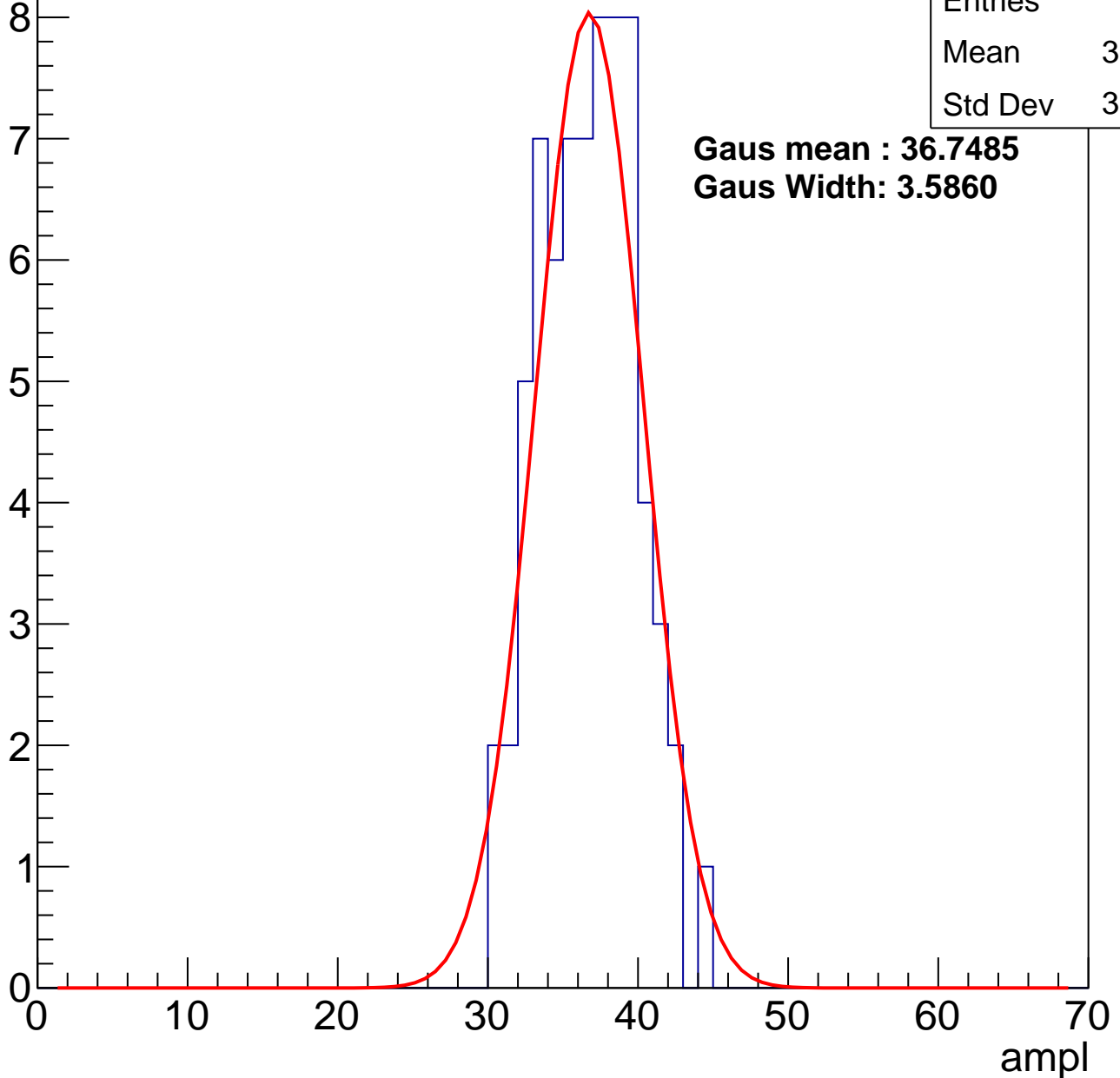
# B1L000S, U21-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.24
Std Dev	3.114

**Gaus mean : 36.7485**  
**Gaus Width: 3.5860**



# B1L000S, U21-ch26, adc2

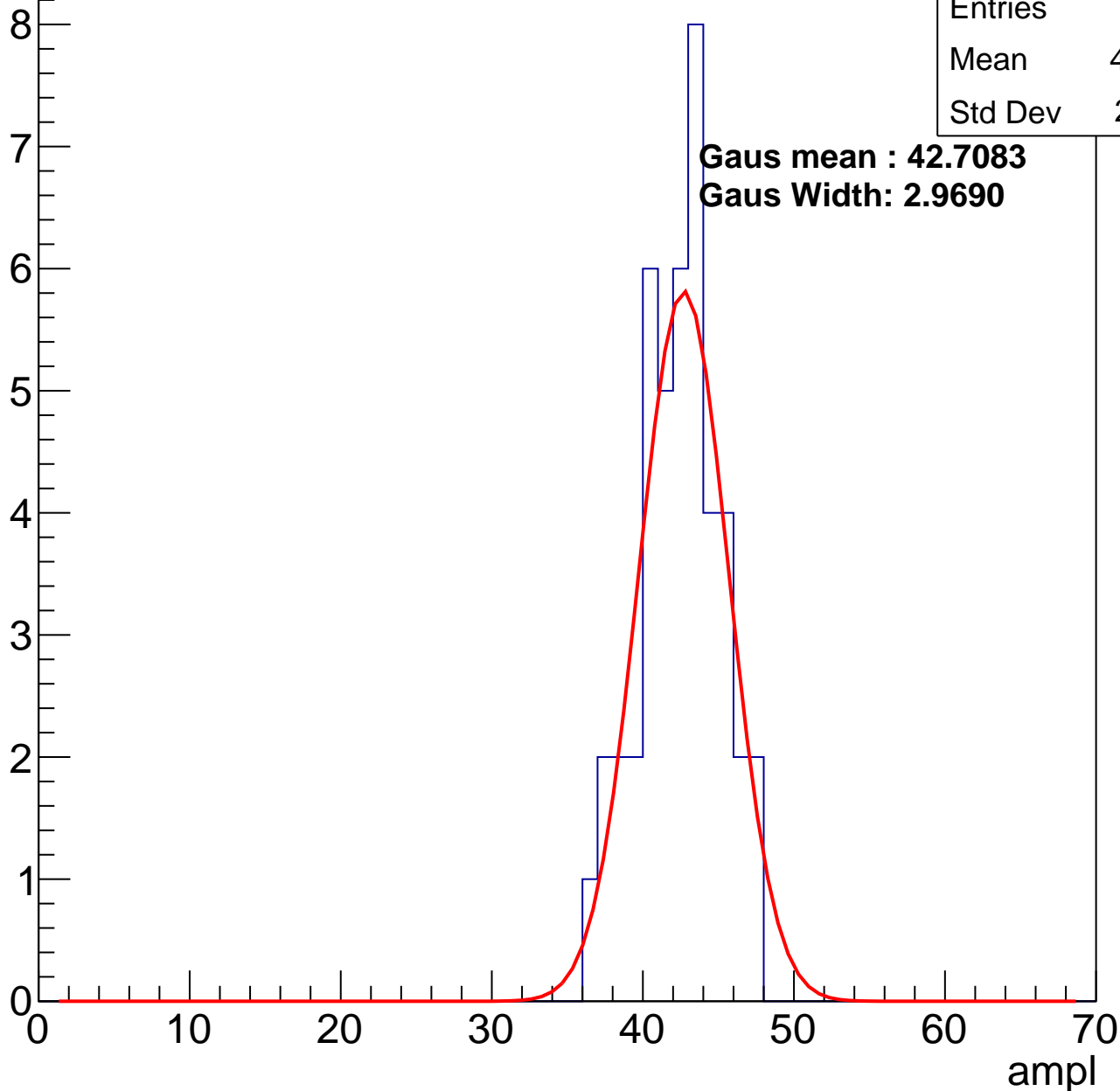
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	41.98
Std Dev	2.641

**Gaus mean : 42.7083**

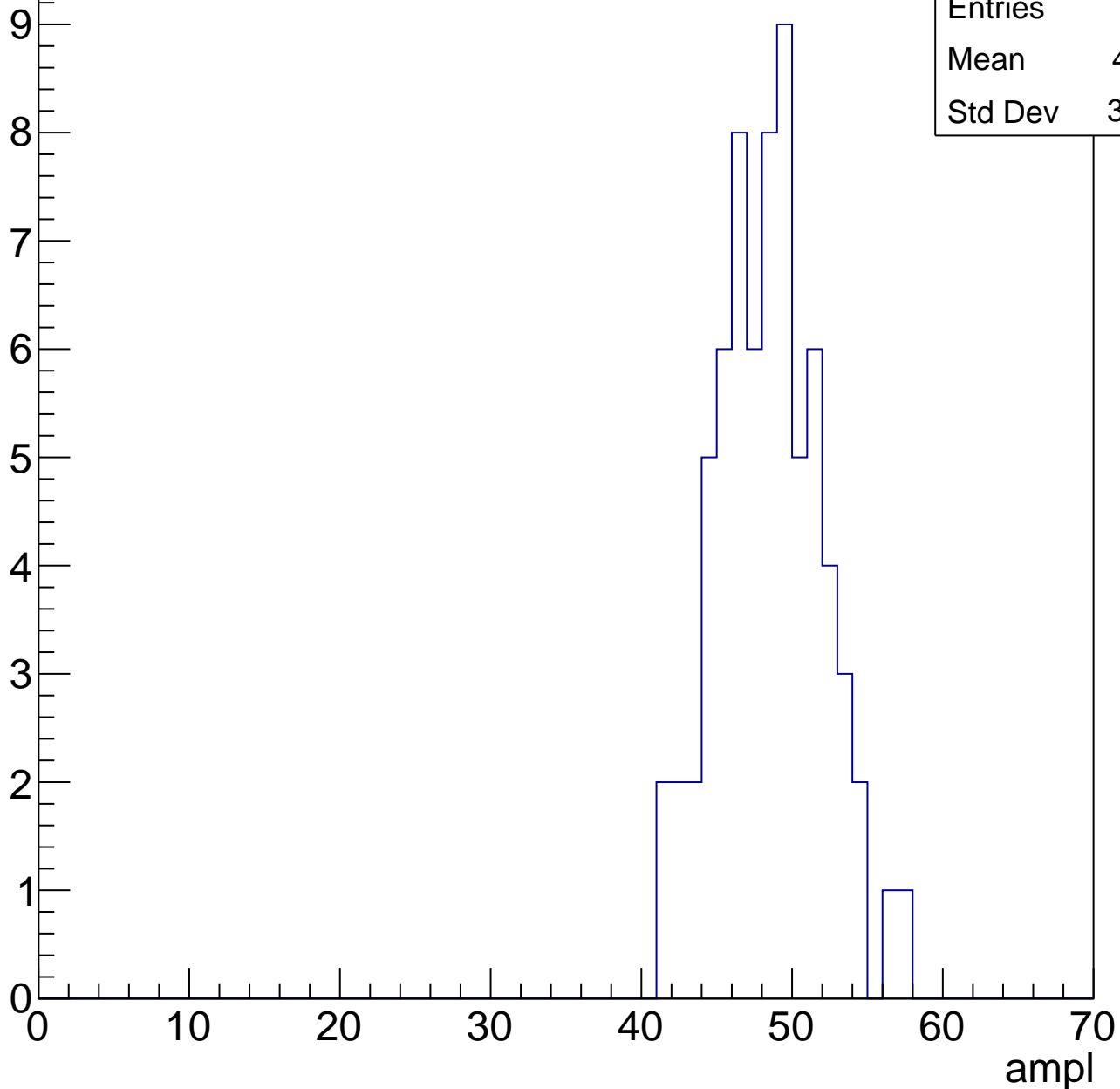
**Gaus Width: 2.9690**



# B1L000S, U21-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



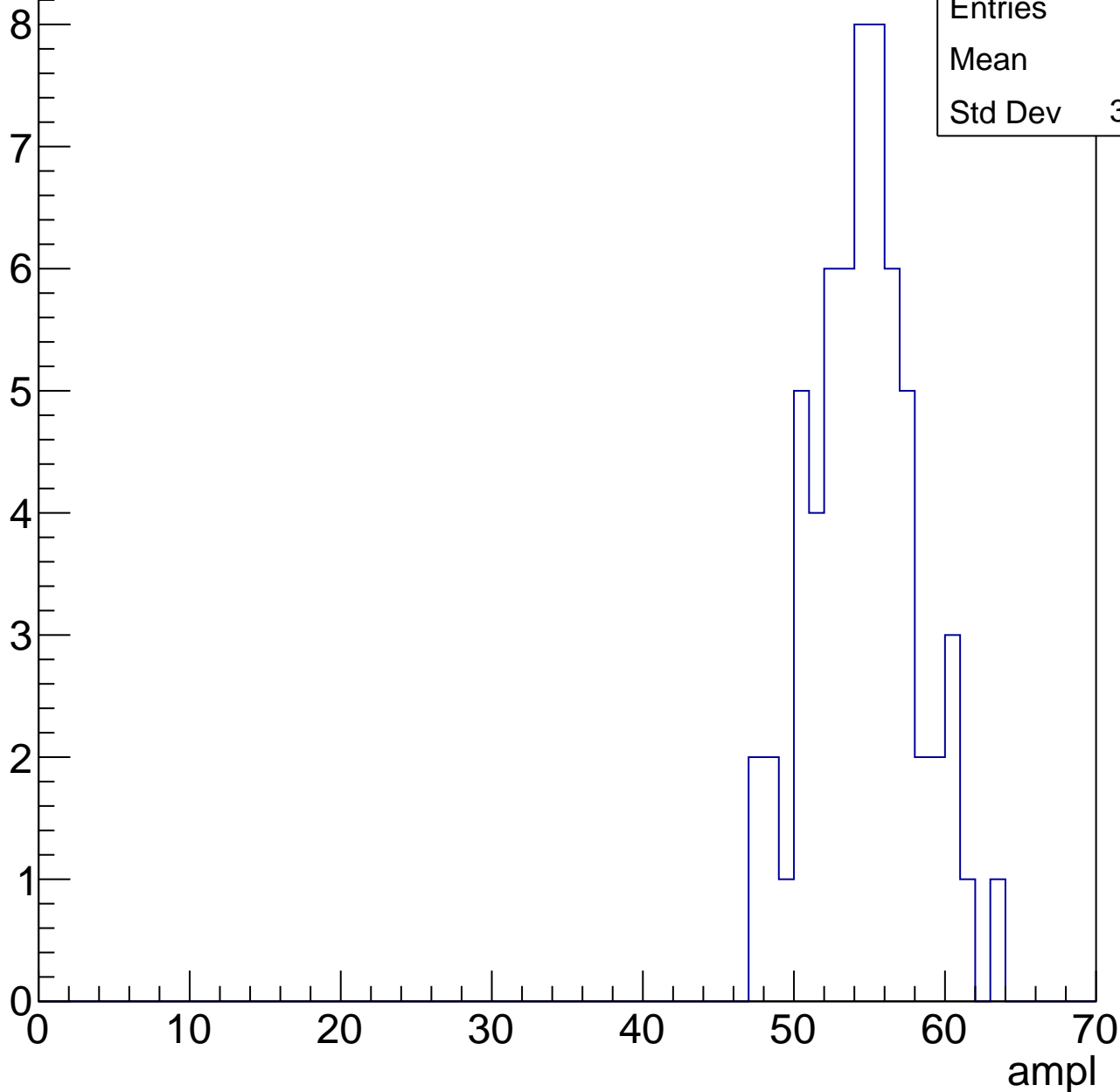
Entries	70
Mean	48.01
Std Dev	3.445

# B1L000S, U21-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

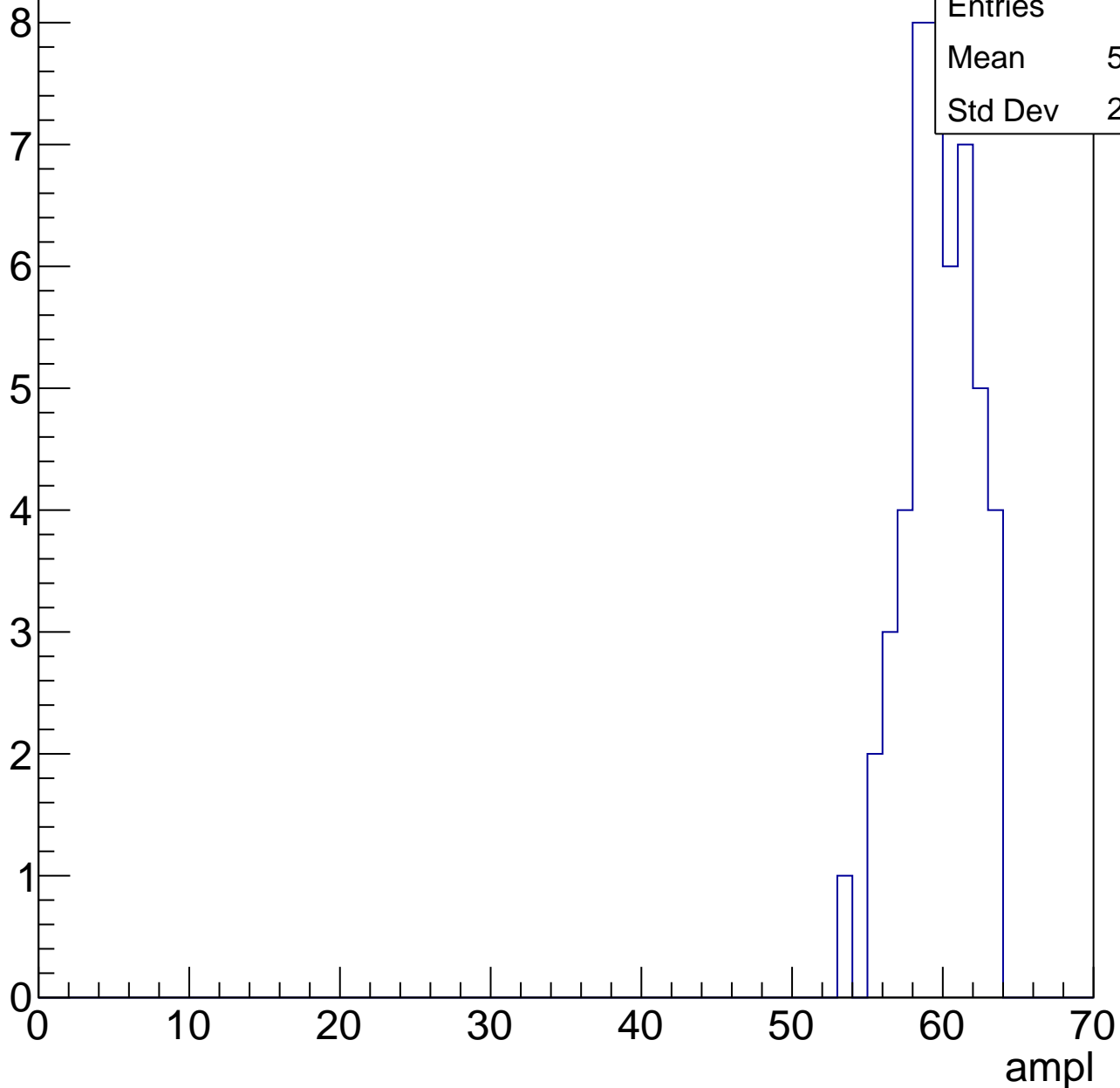
Entries	62
Mean	54.1
Std Dev	3.449



# B1L000S, U21-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

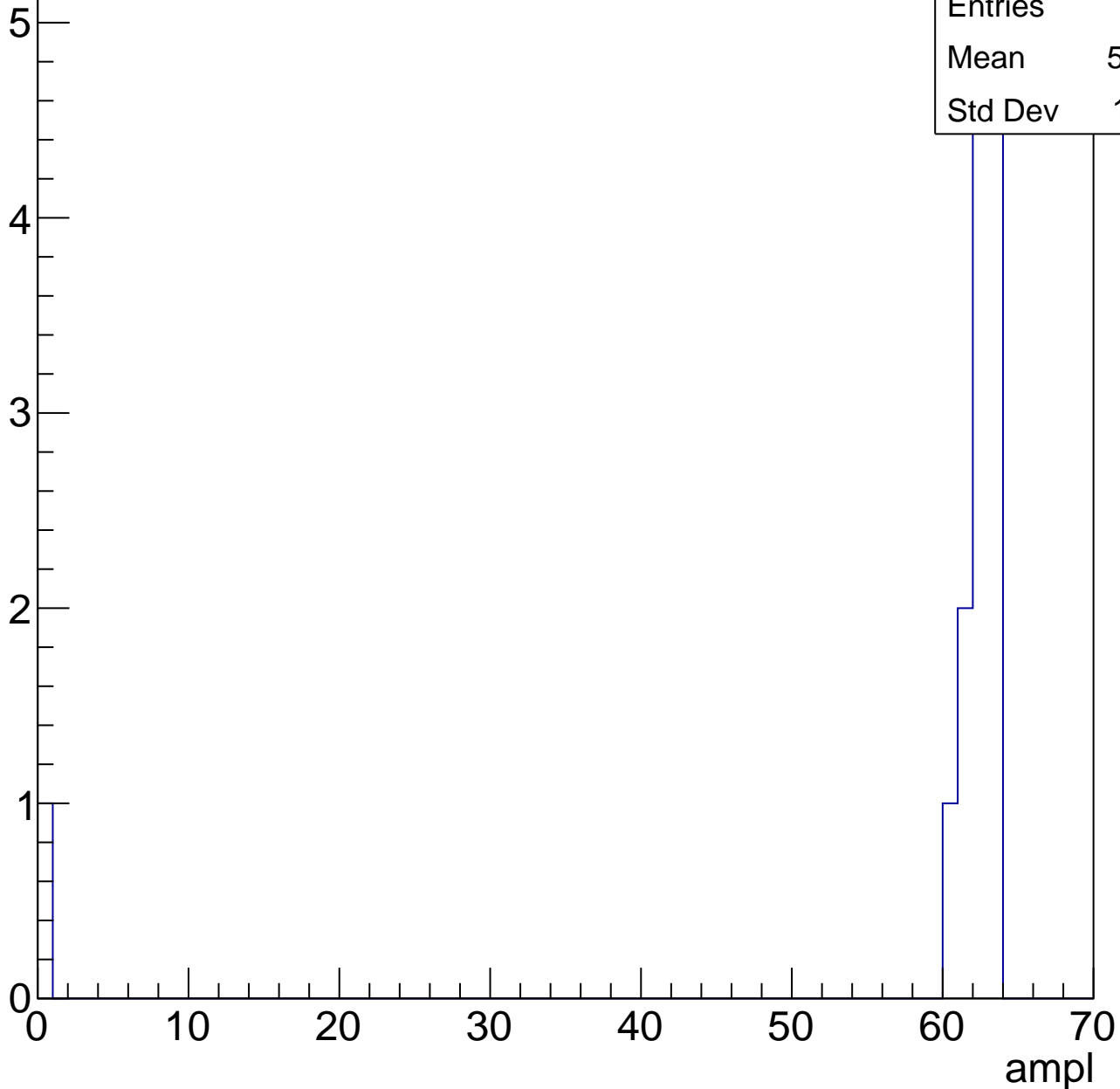


# B1L000S, U21-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.64
Std Dev	16.01





# B1L000S, U21-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch27, adc0

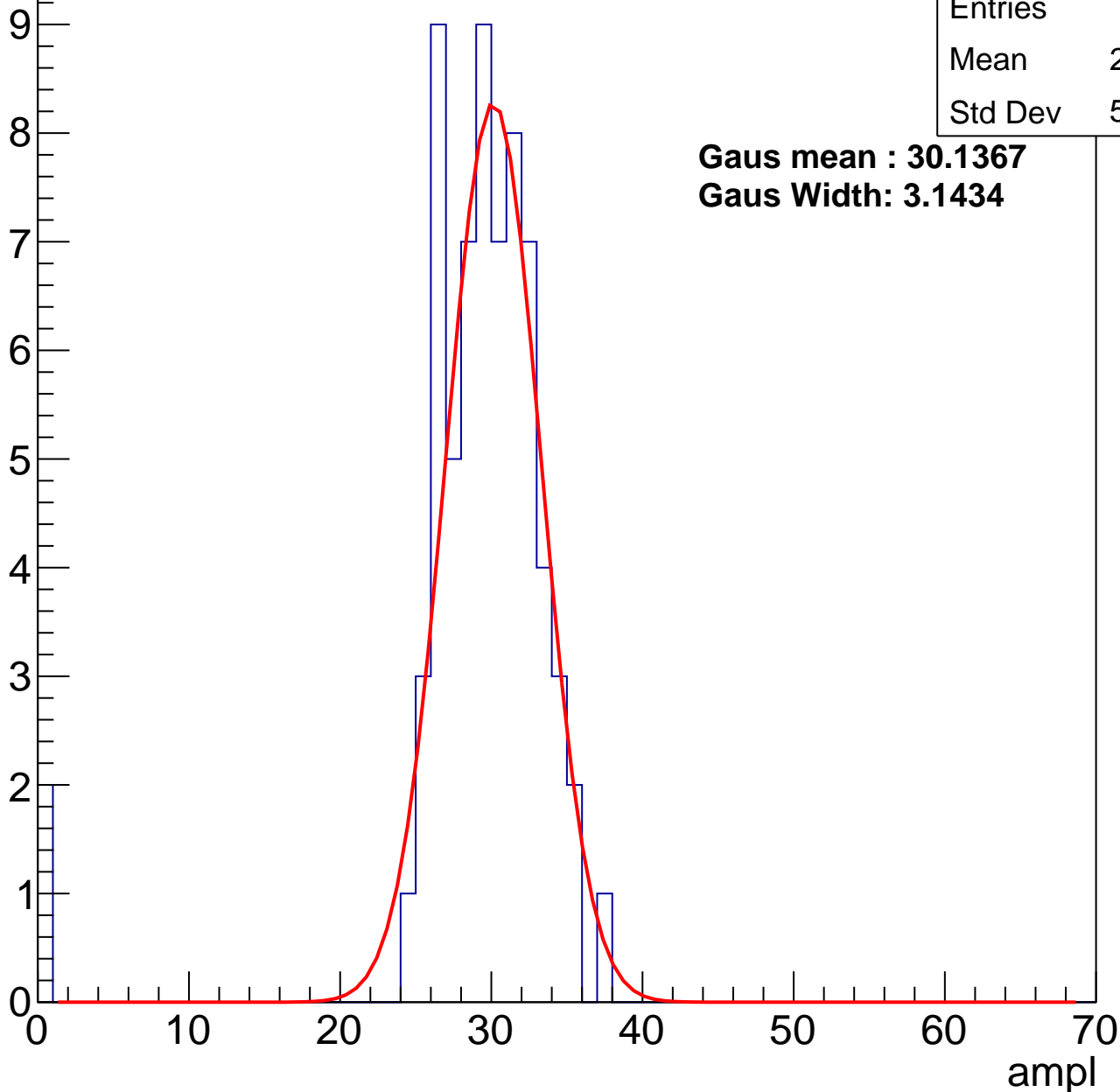
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.65
Std Dev	5.729

**Gaus mean : 30.1367**

**Gaus Width: 3.1434**



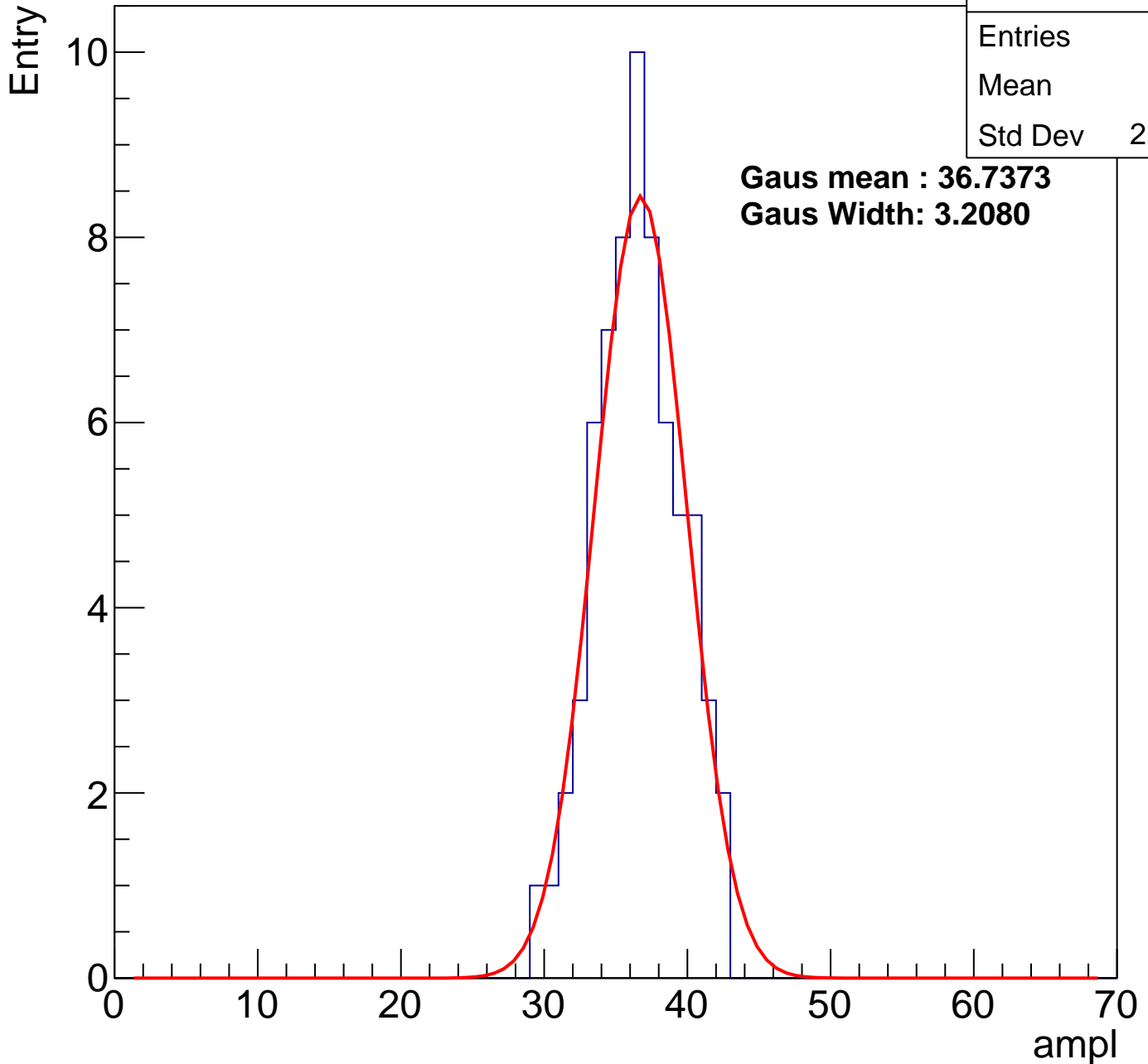
# B1L000S, U21-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	36.1
Std Dev	2.933

**Gaus mean : 36.7373**

**Gaus Width: 3.2080**



# B1L000S, U21-ch27, adc2

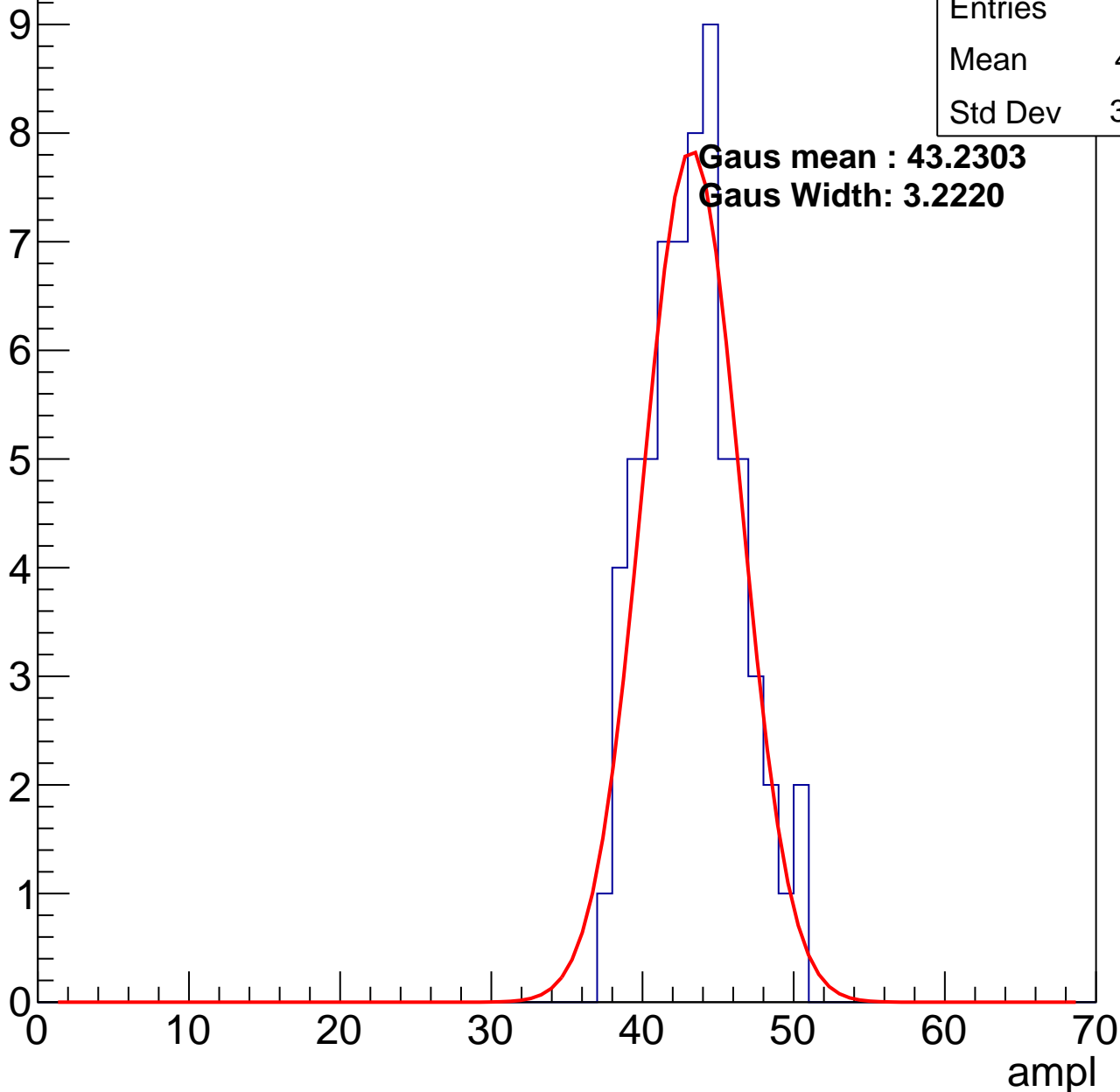
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	42.91
Std Dev	3.066

**Gaus mean : 43.2303**

**Gaus Width: 3.2220**

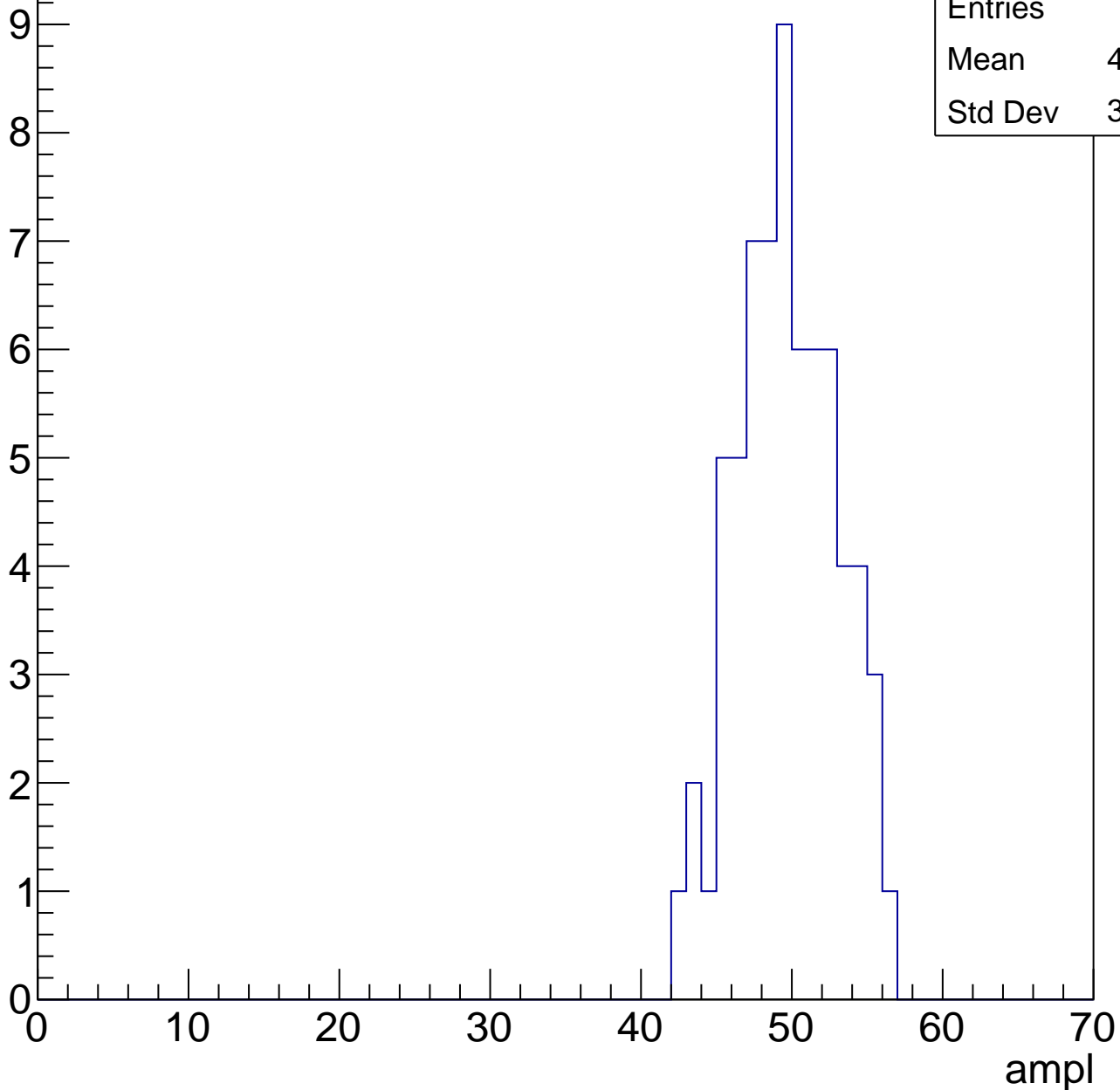


# B1L000S, U21-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	49.25
Std Dev	3.248

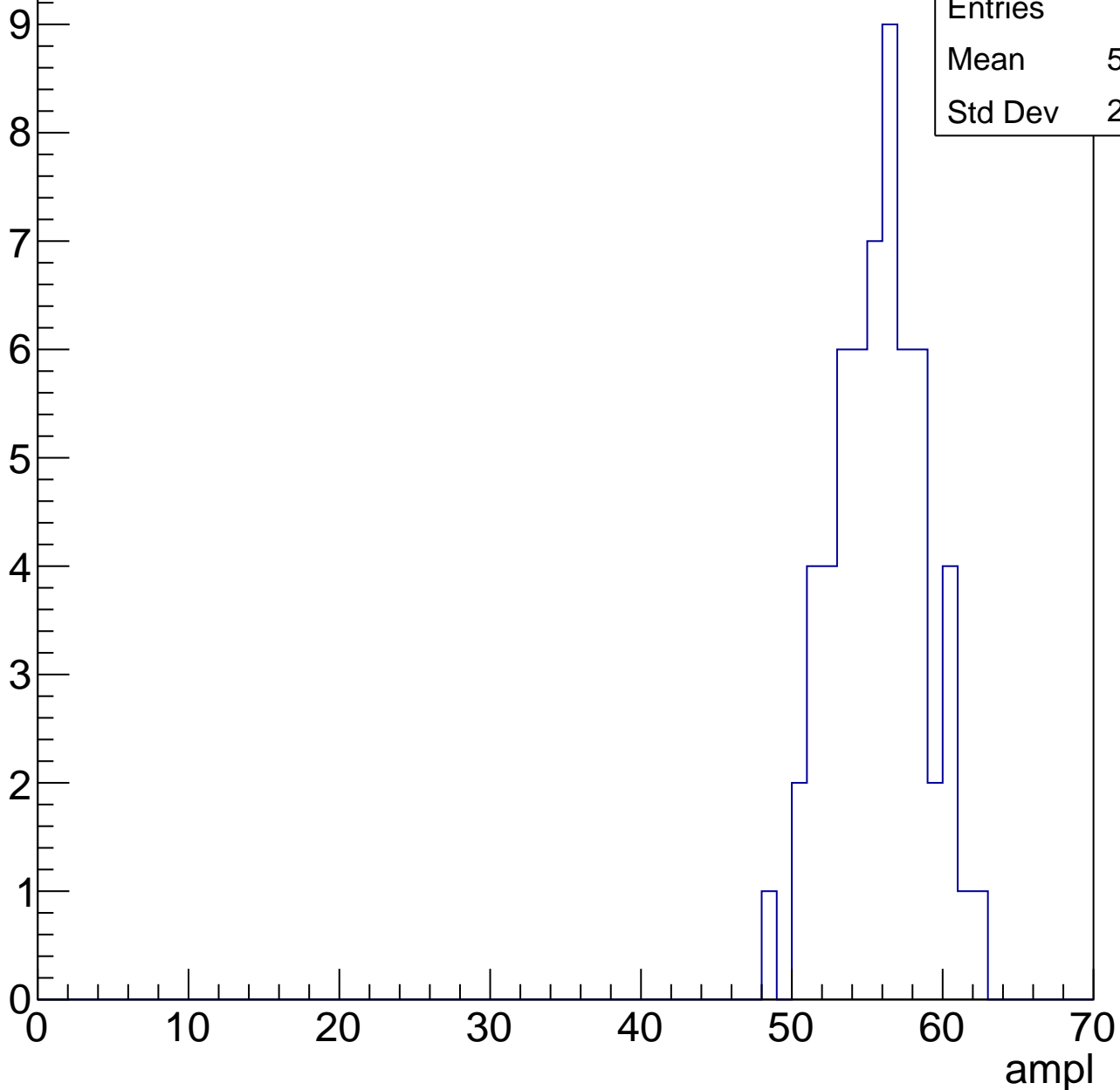


# B1L000S, U21-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

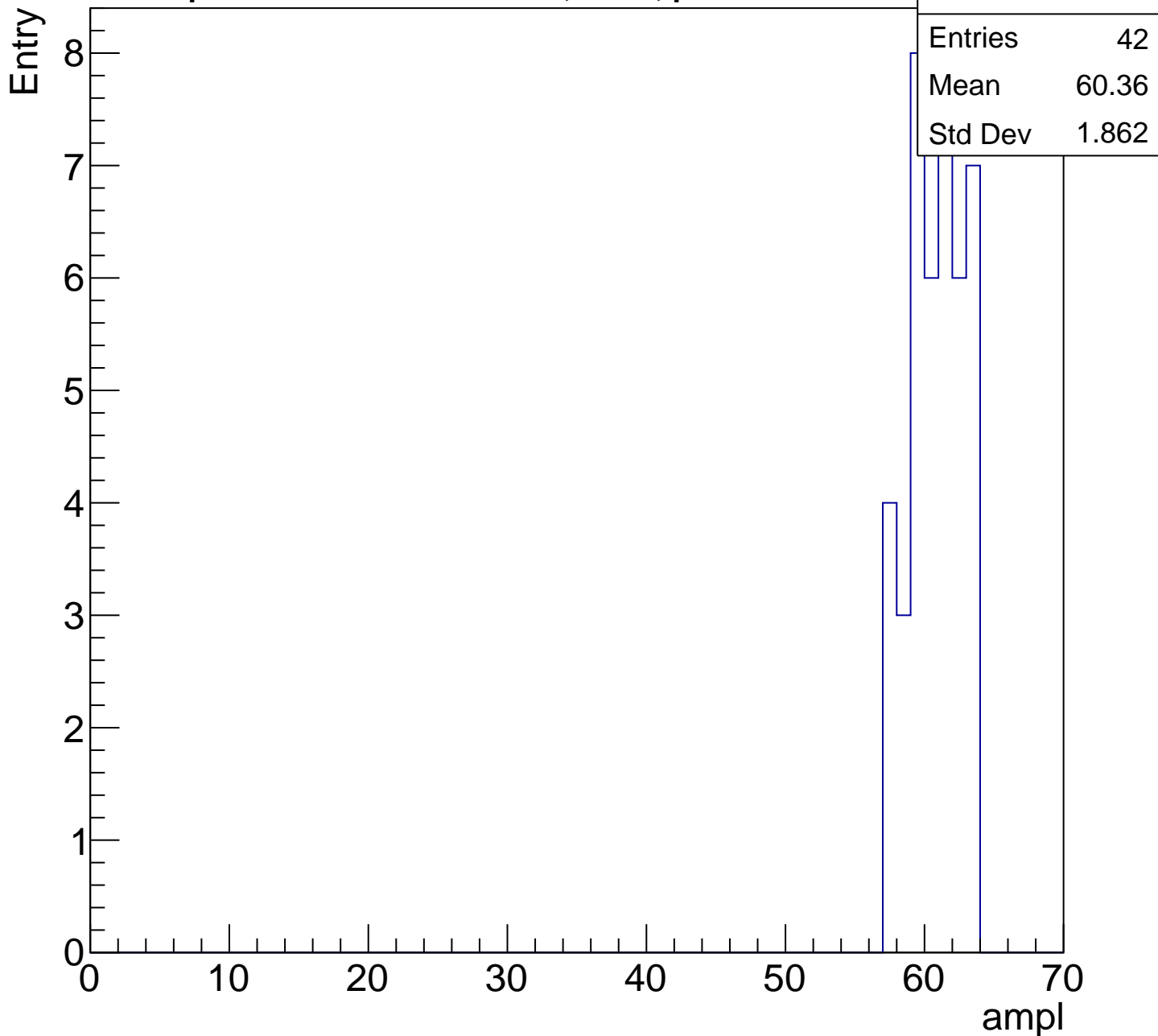
Entry

Entries	59
Mean	55.29
Std Dev	2.992



# B1L000S, U21-ch27, adc5

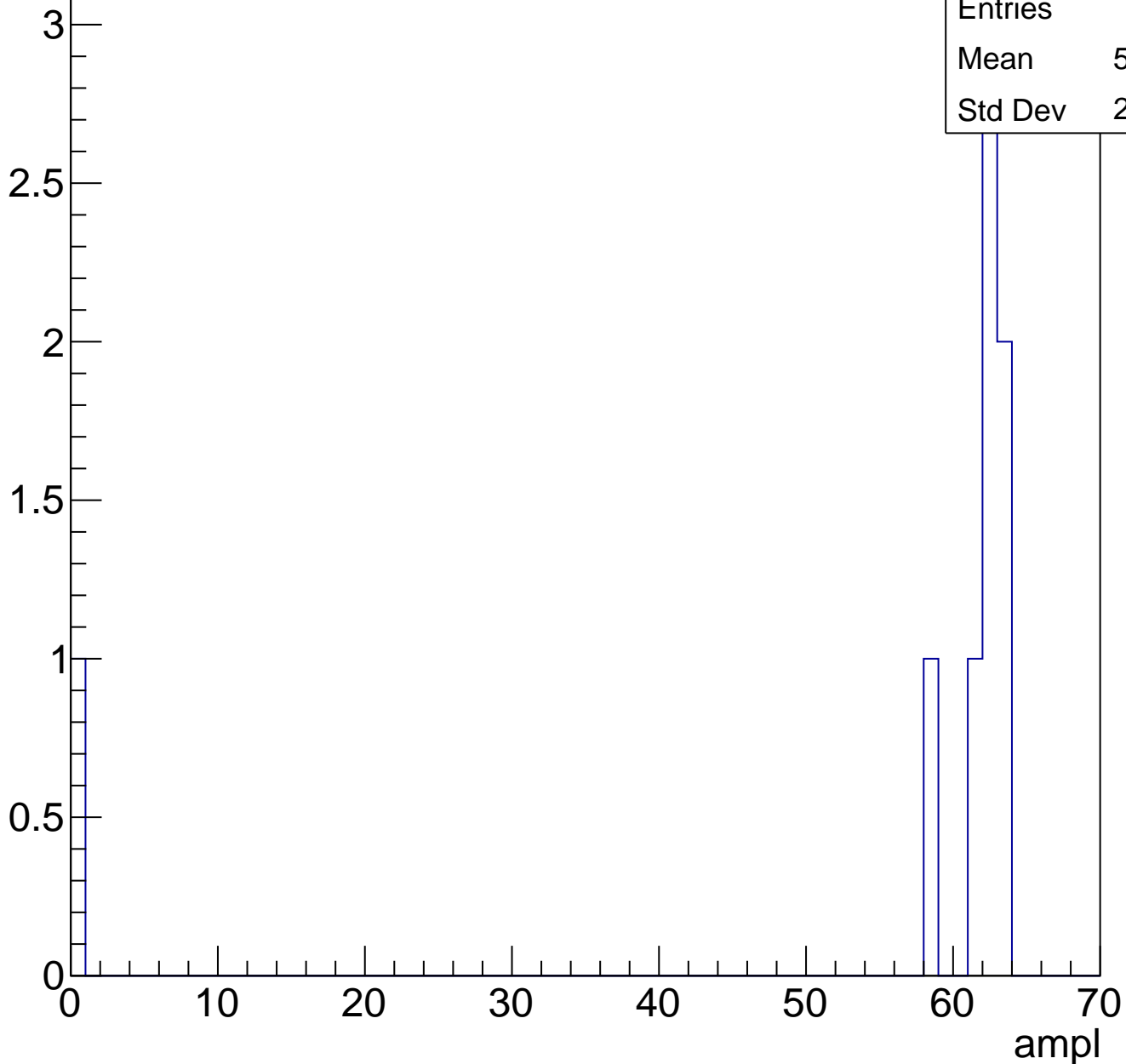
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch28, adc0

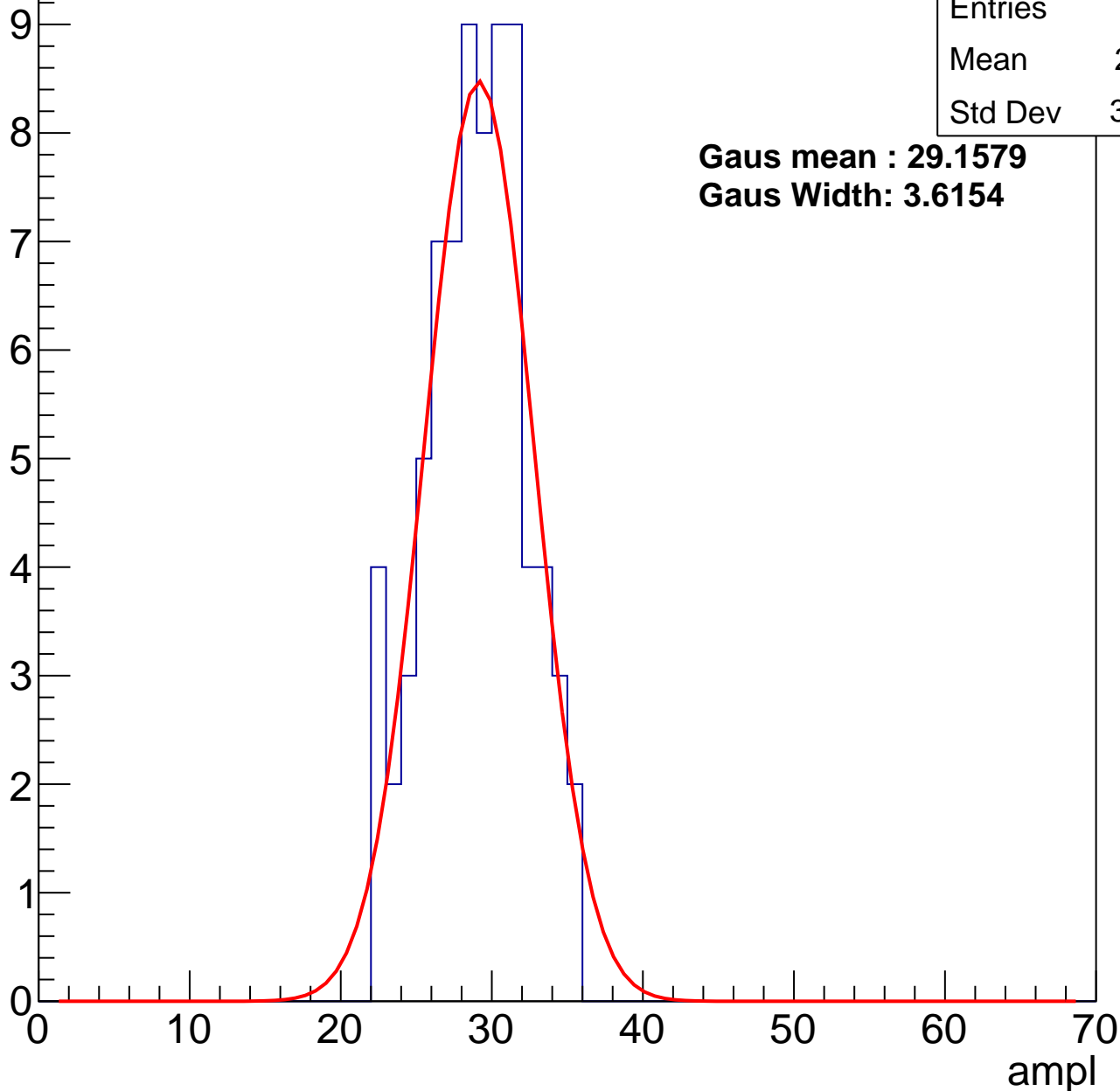
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	28.51
Std Dev	3.238

**Gaus mean : 29.1579**

**Gaus Width: 3.6154**



# B1L000S, U21-ch28, adc1

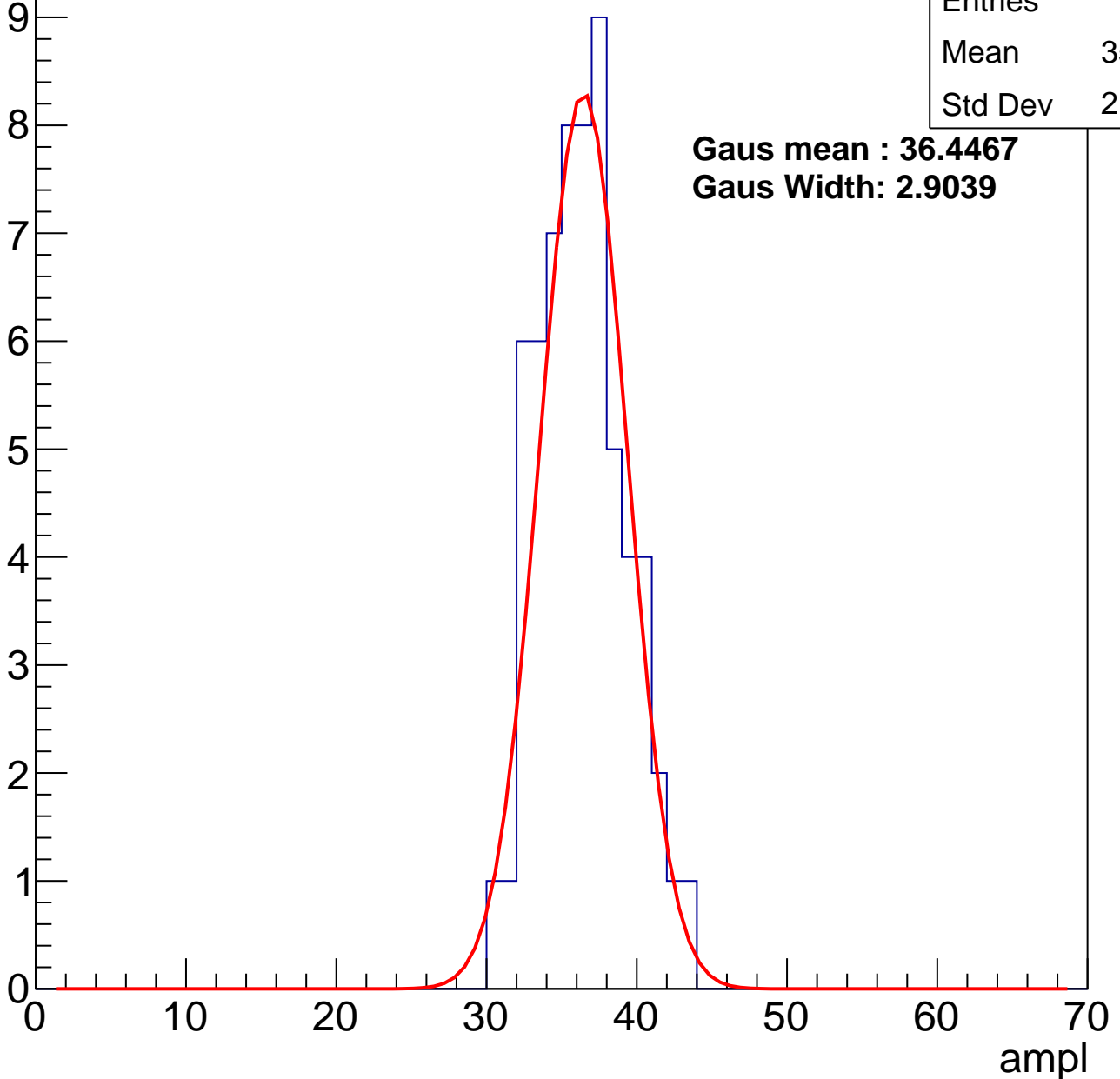
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.92
Std Dev	2.847

**Gaus mean : 36.4467**

**Gaus Width: 2.9039**



# B1L000S, U21-ch28, adc2

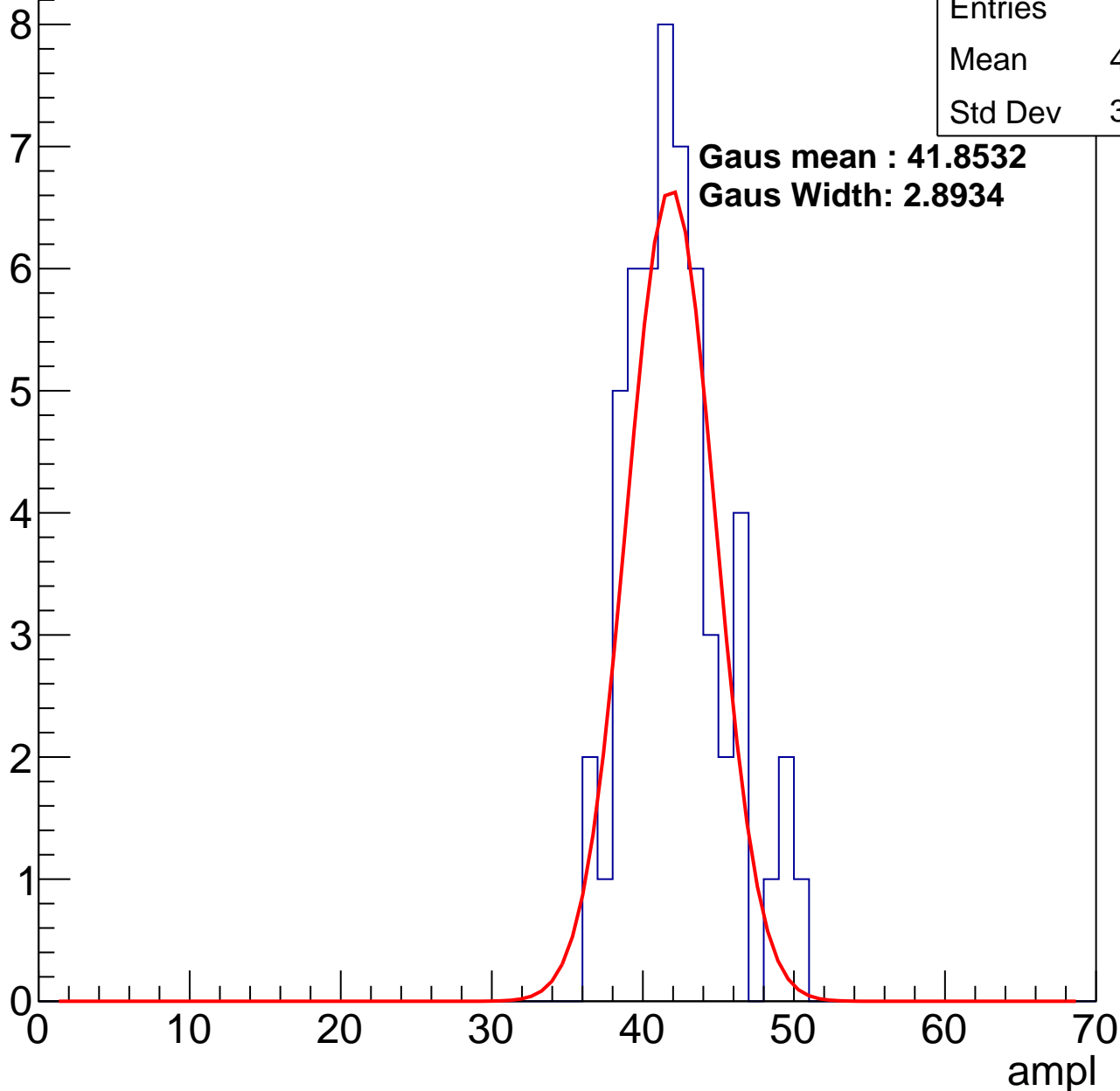
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.76
Std Dev	3.214

**Gaus mean : 41.8532**

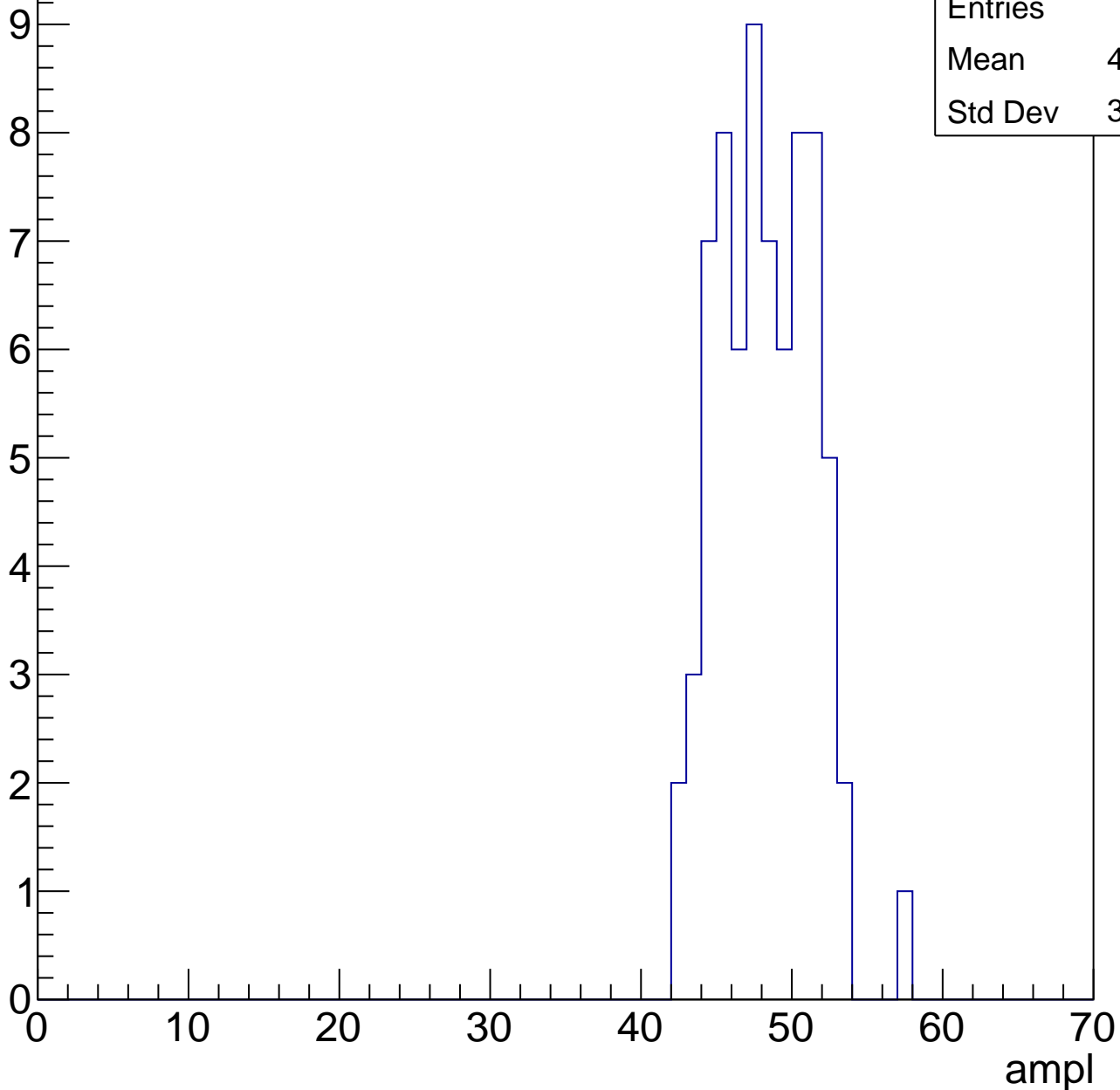
**Gaus Width: 2.8934**



# B1L000S, U21-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

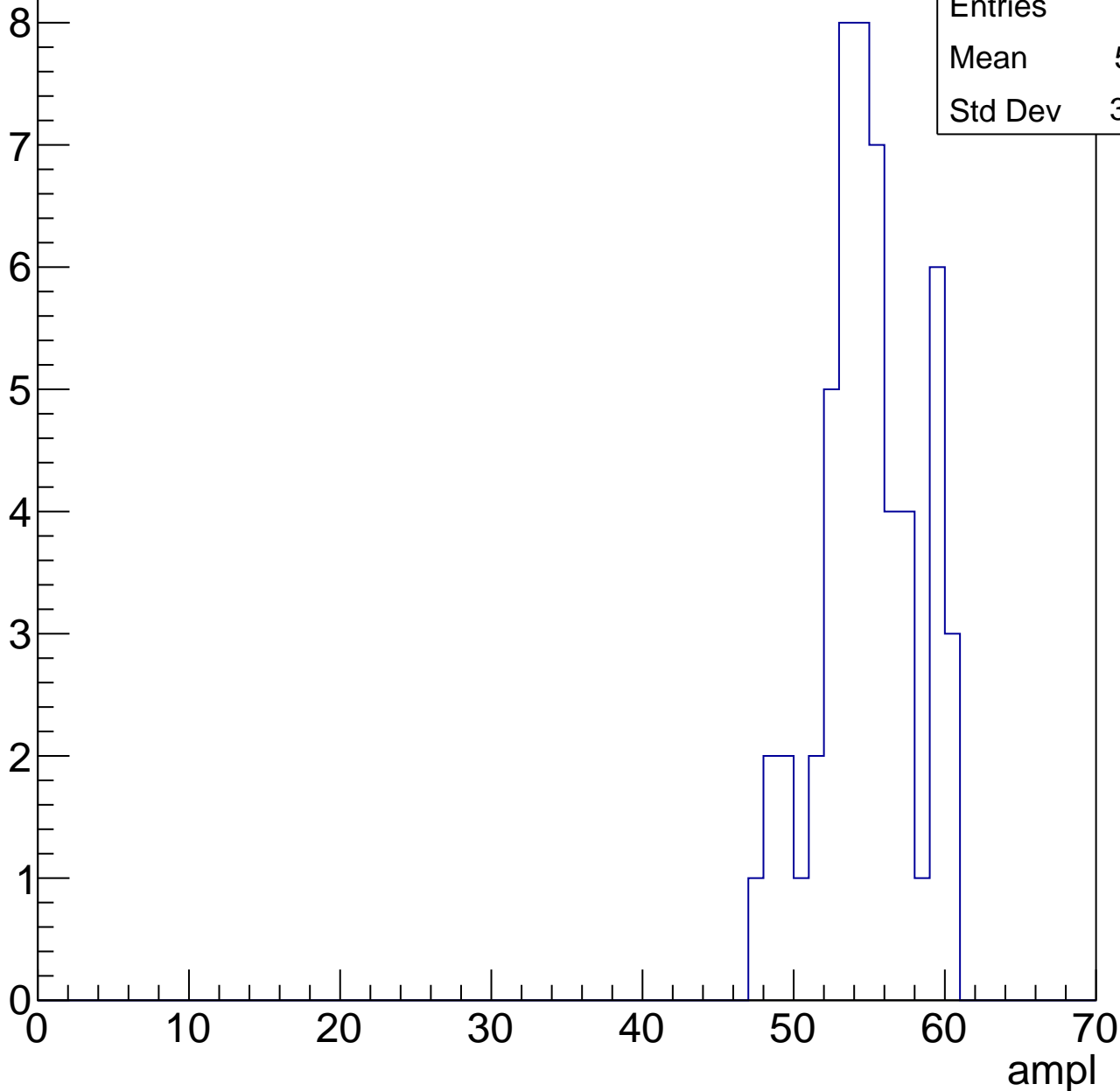


# B1L000S, U21-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

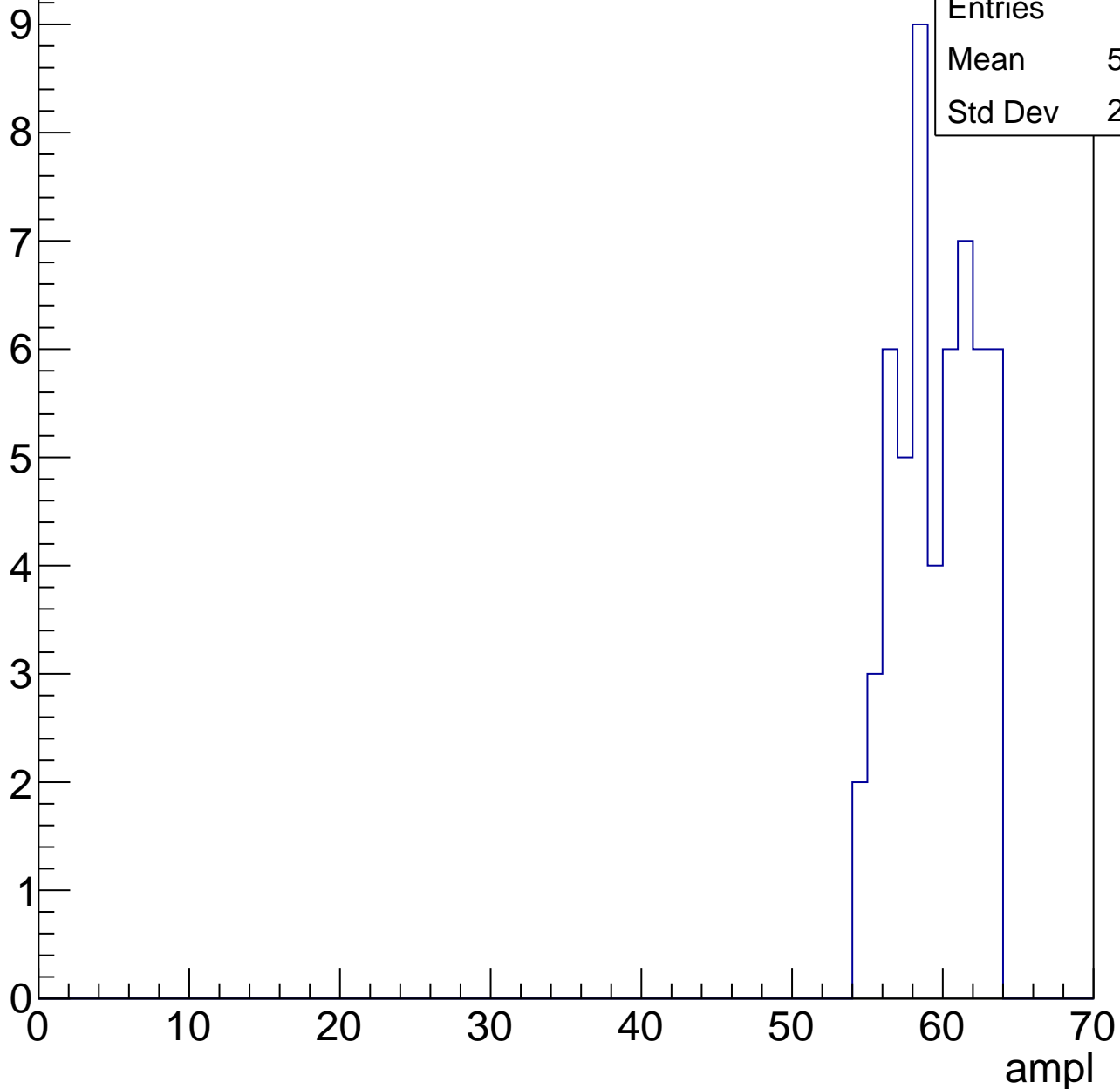
Entries	54
Mean	54.41
Std Dev	3.212



# B1L000S, U21-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

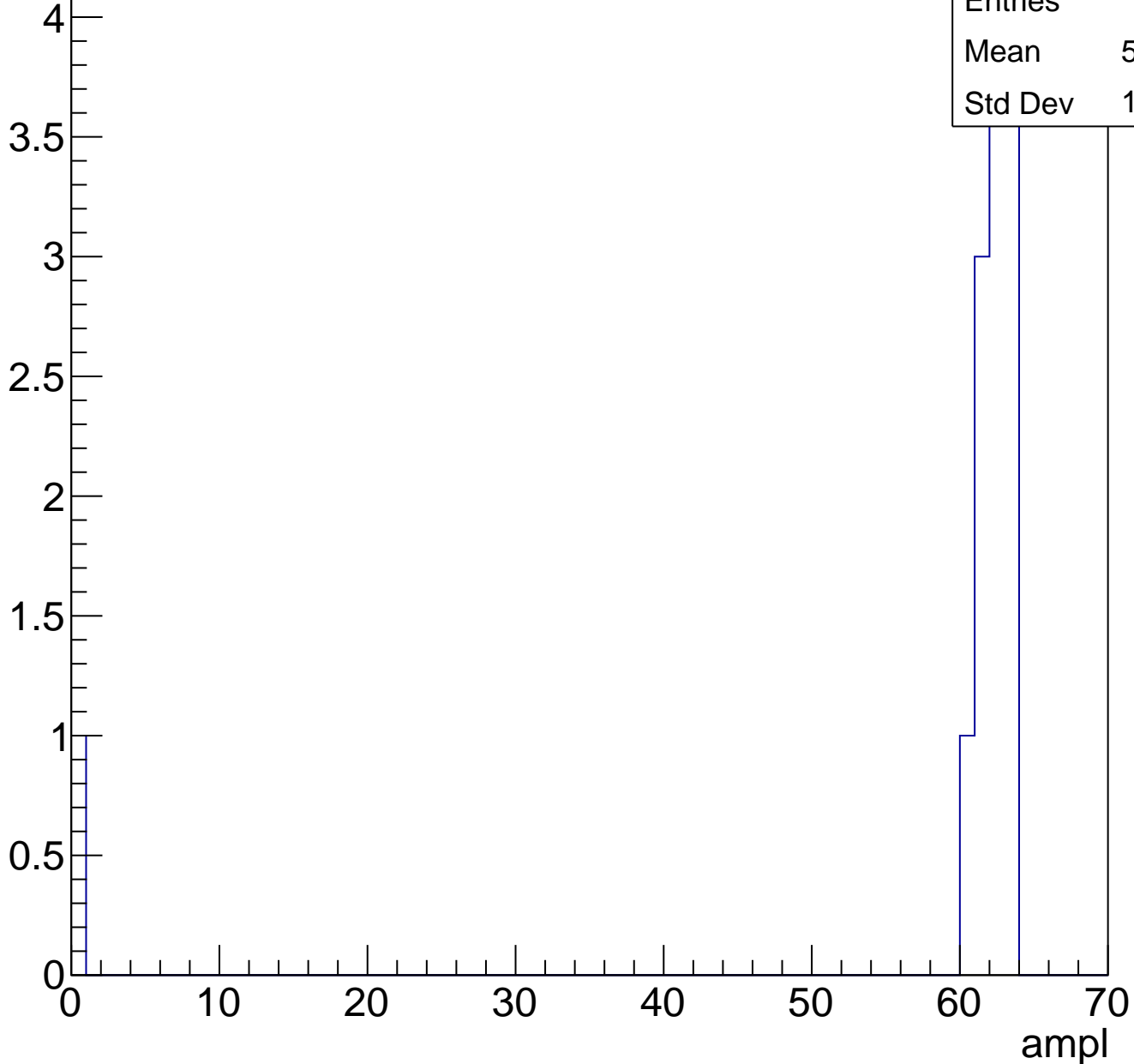
Entry



# B1L000S, U21-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch29, adc0

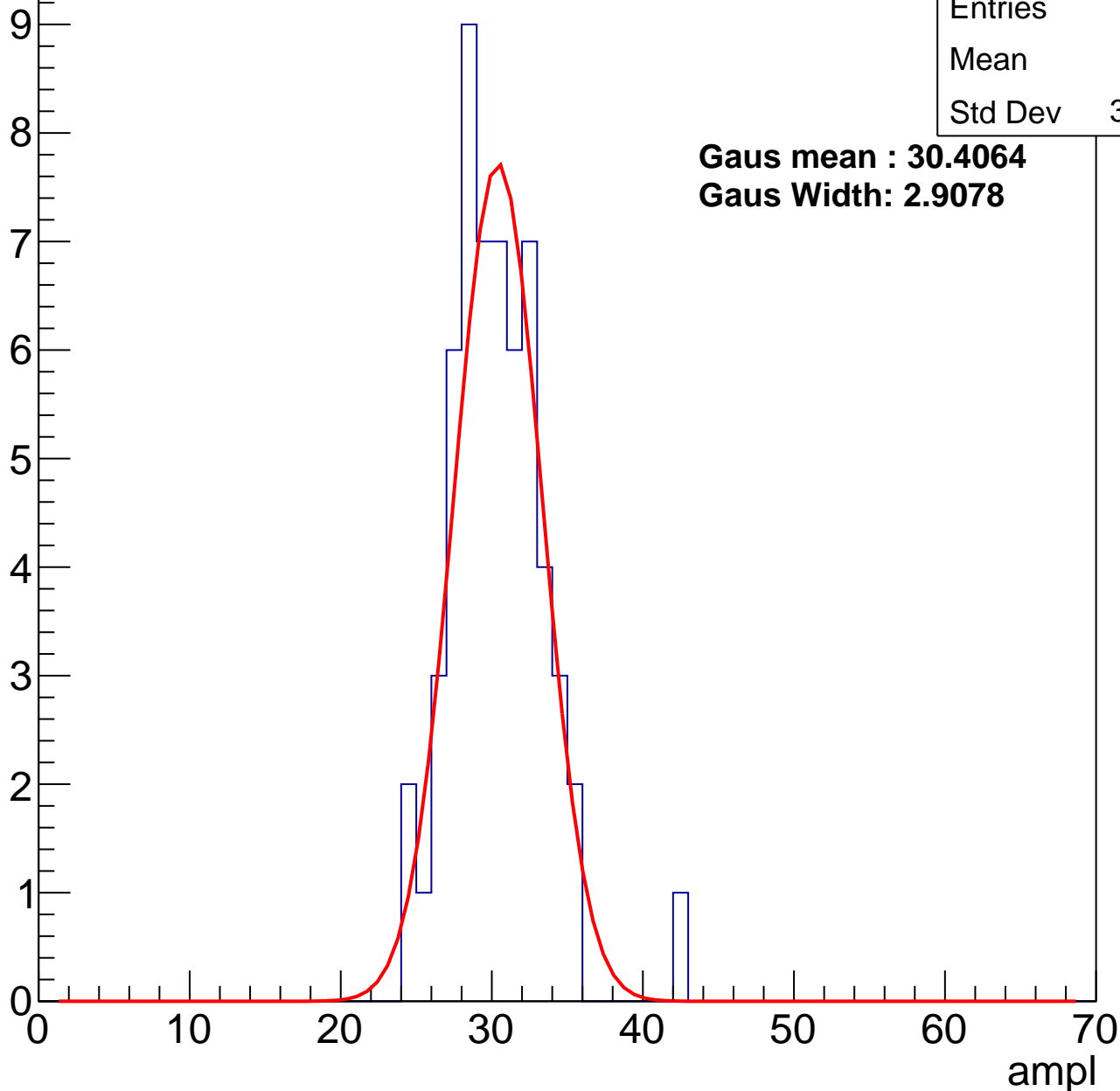
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	29.9
Std Dev	3.089

**Gaus mean : 30.4064**

**Gaus Width: 2.9078**



# B1L000S, U21-ch29, adc1

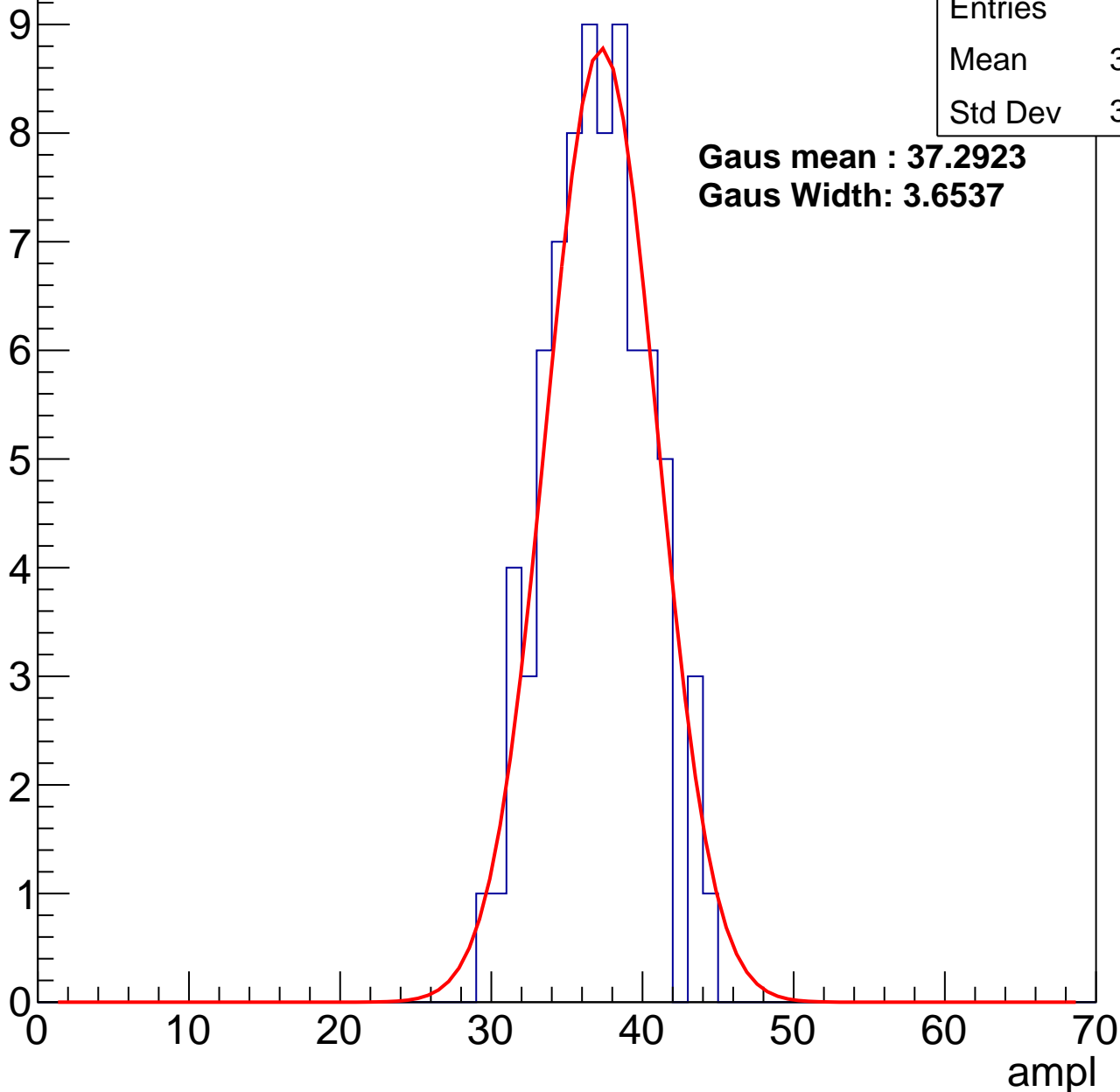
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	36.48
Std Dev	3.293

**Gaus mean : 37.2923**

**Gaus Width: 3.6537**



# B1L000S, U21-ch29, adc2

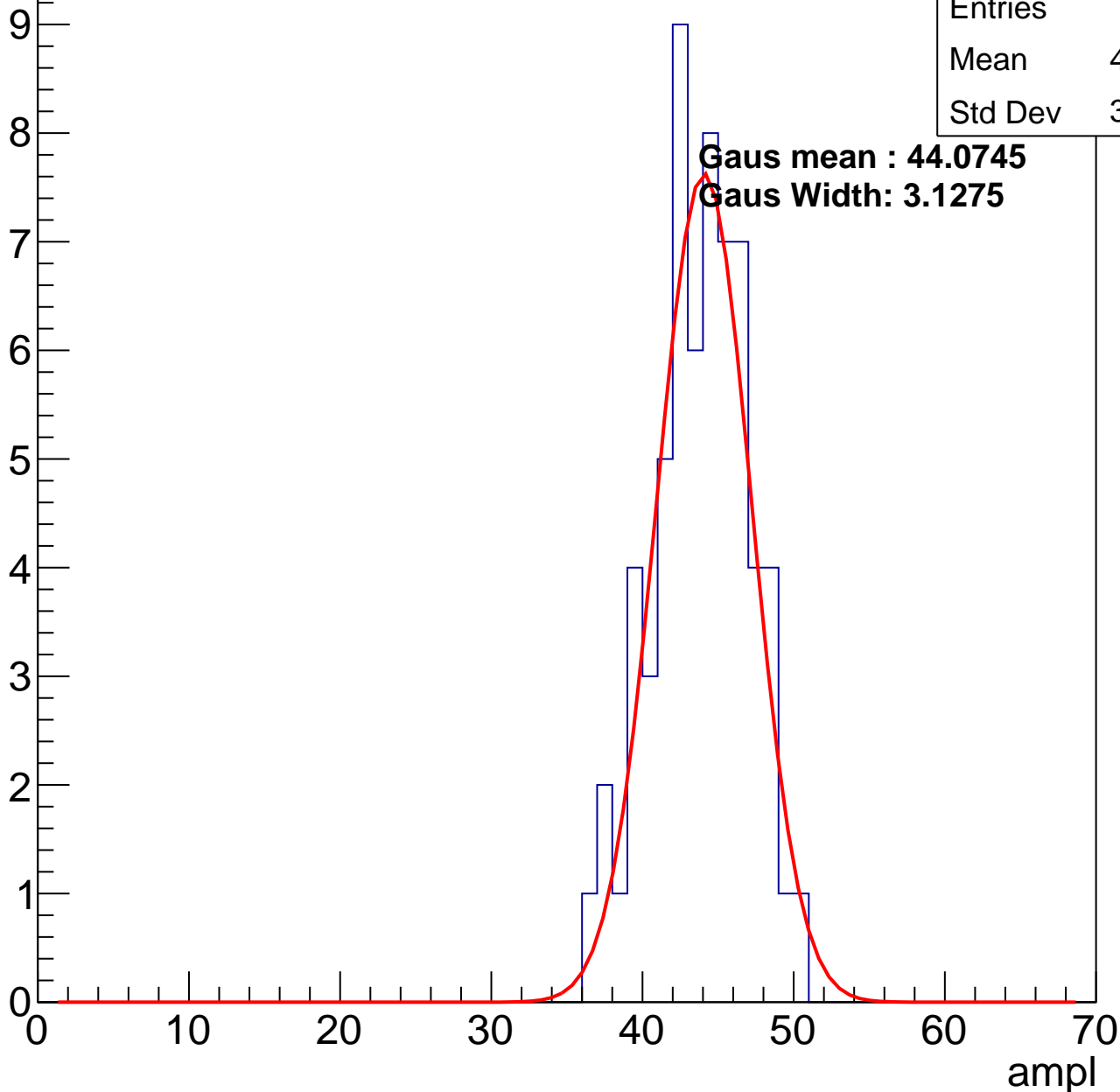
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	43.38
Std Dev	3.099

**Gaus mean : 44.0745**

**Gaus Width: 3.1275**

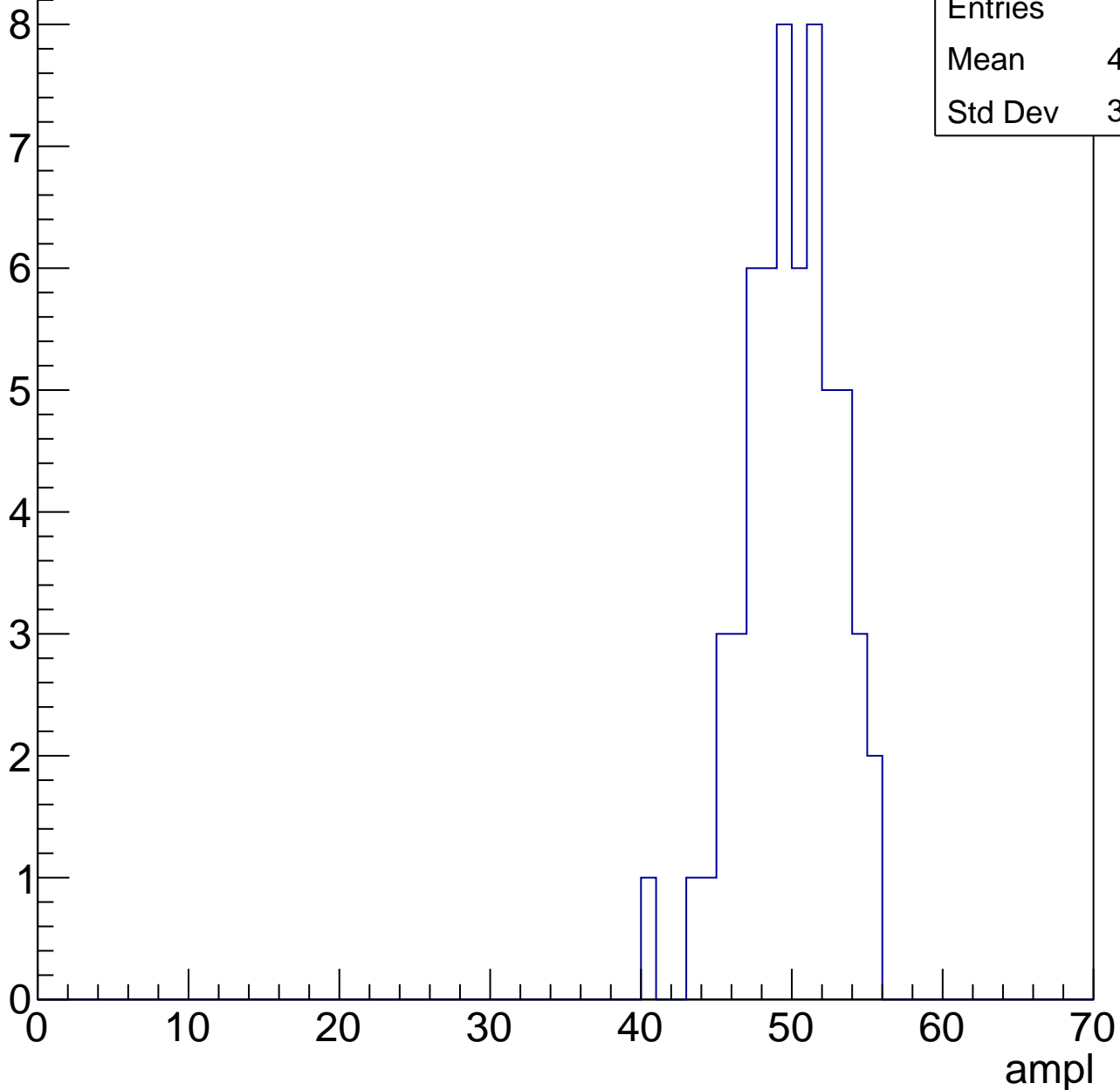


# B1L000S, U21-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

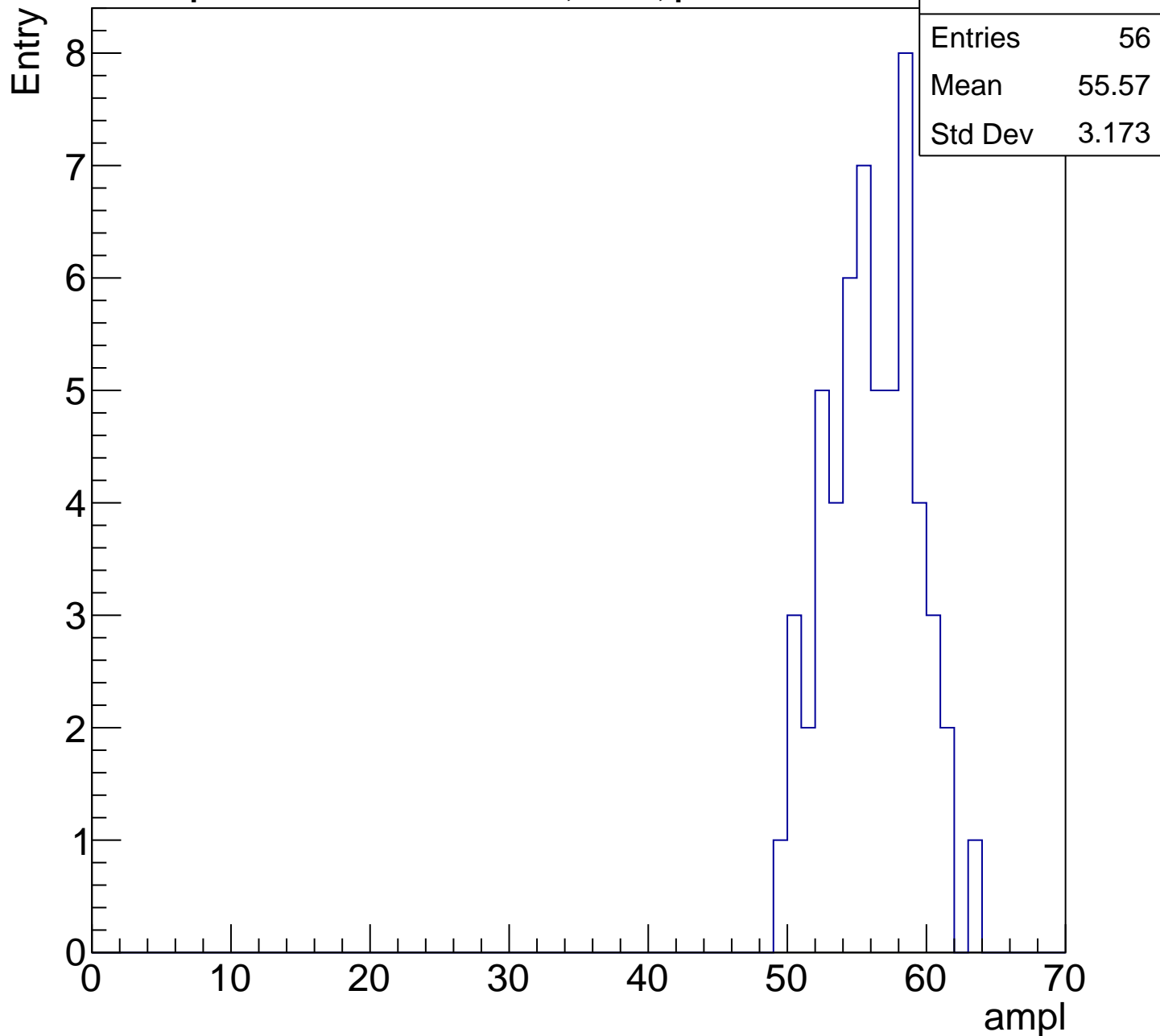
Entry

Entries	58
Mean	49.43
Std Dev	3.069



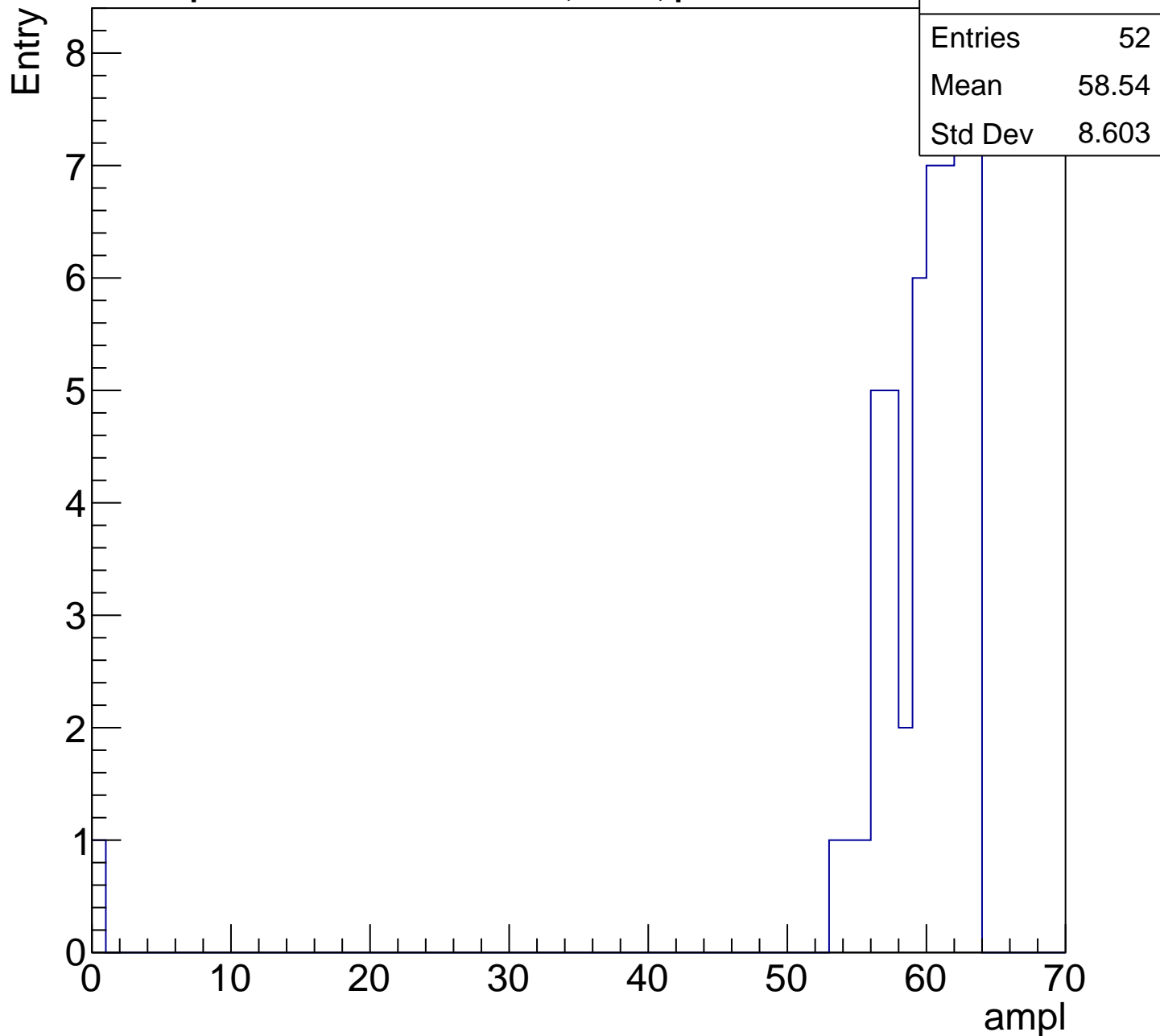
# B1L000S, U21-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

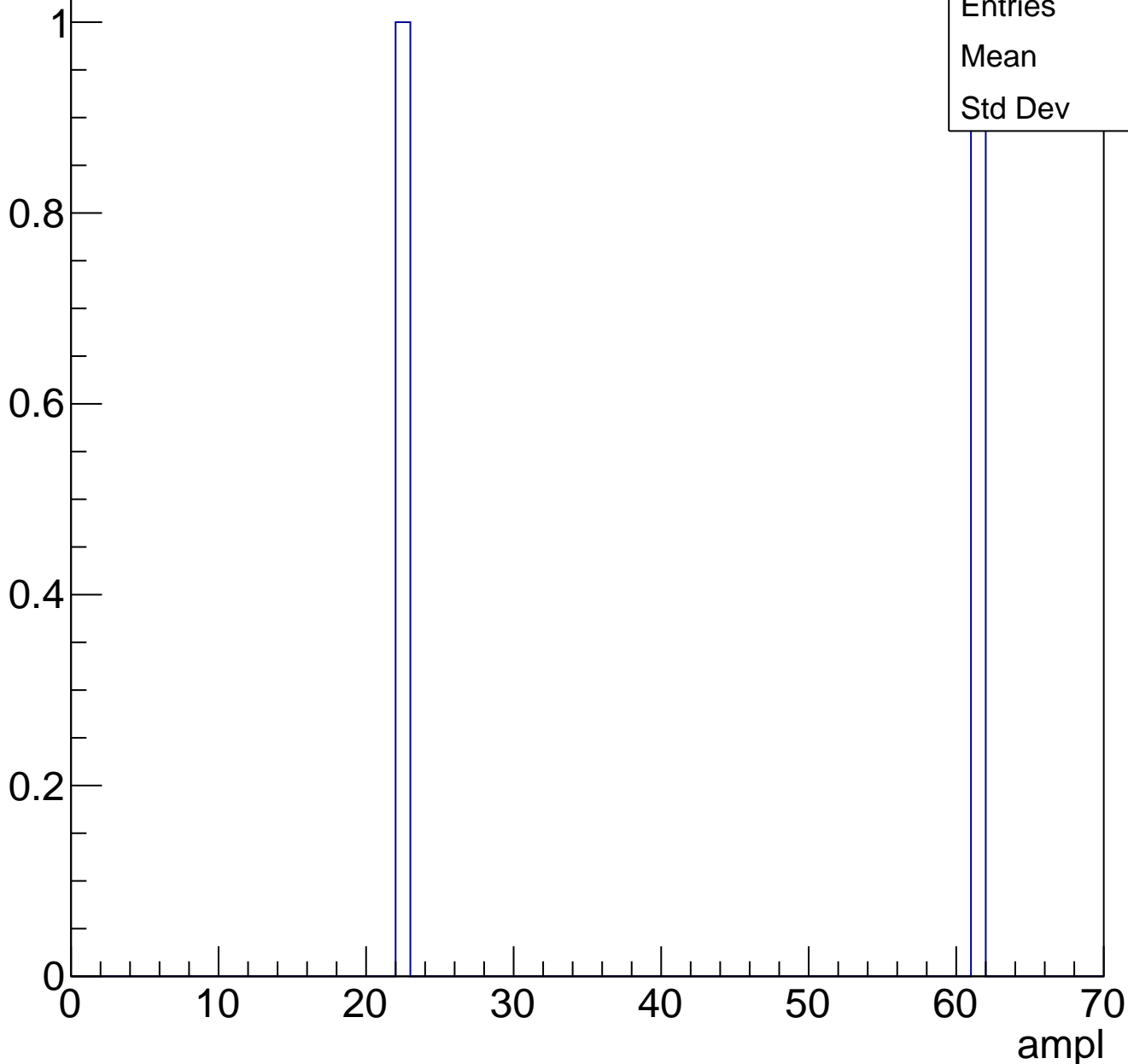
0 10 20 30 40 50 60 70



# B1L000S, U21-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch30, adc0

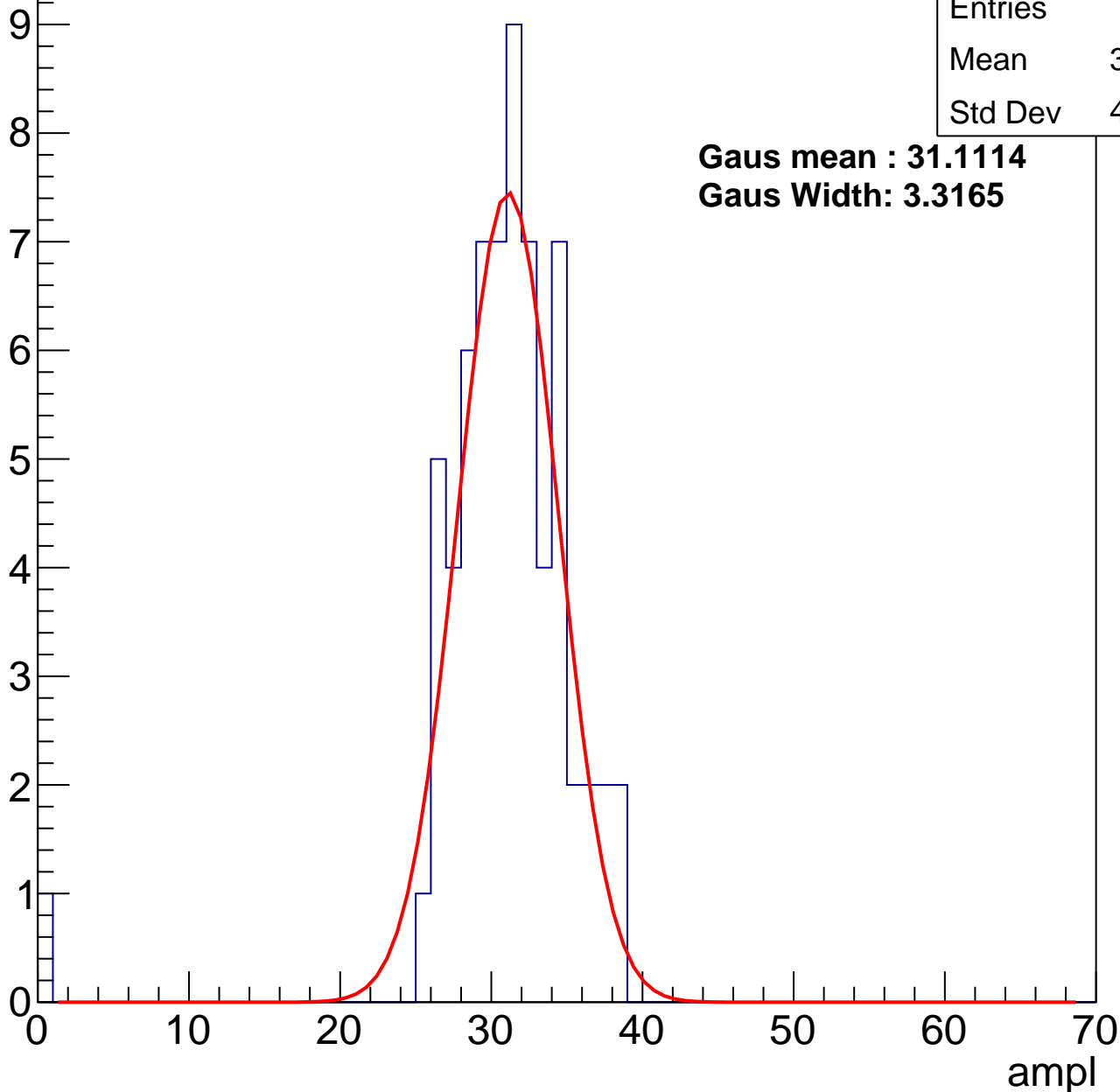
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	30.44
Std Dev	4.912

**Gaus mean : 31.1114**

**Gaus Width: 3.3165**



# B1L000S, U21-ch30, adc1

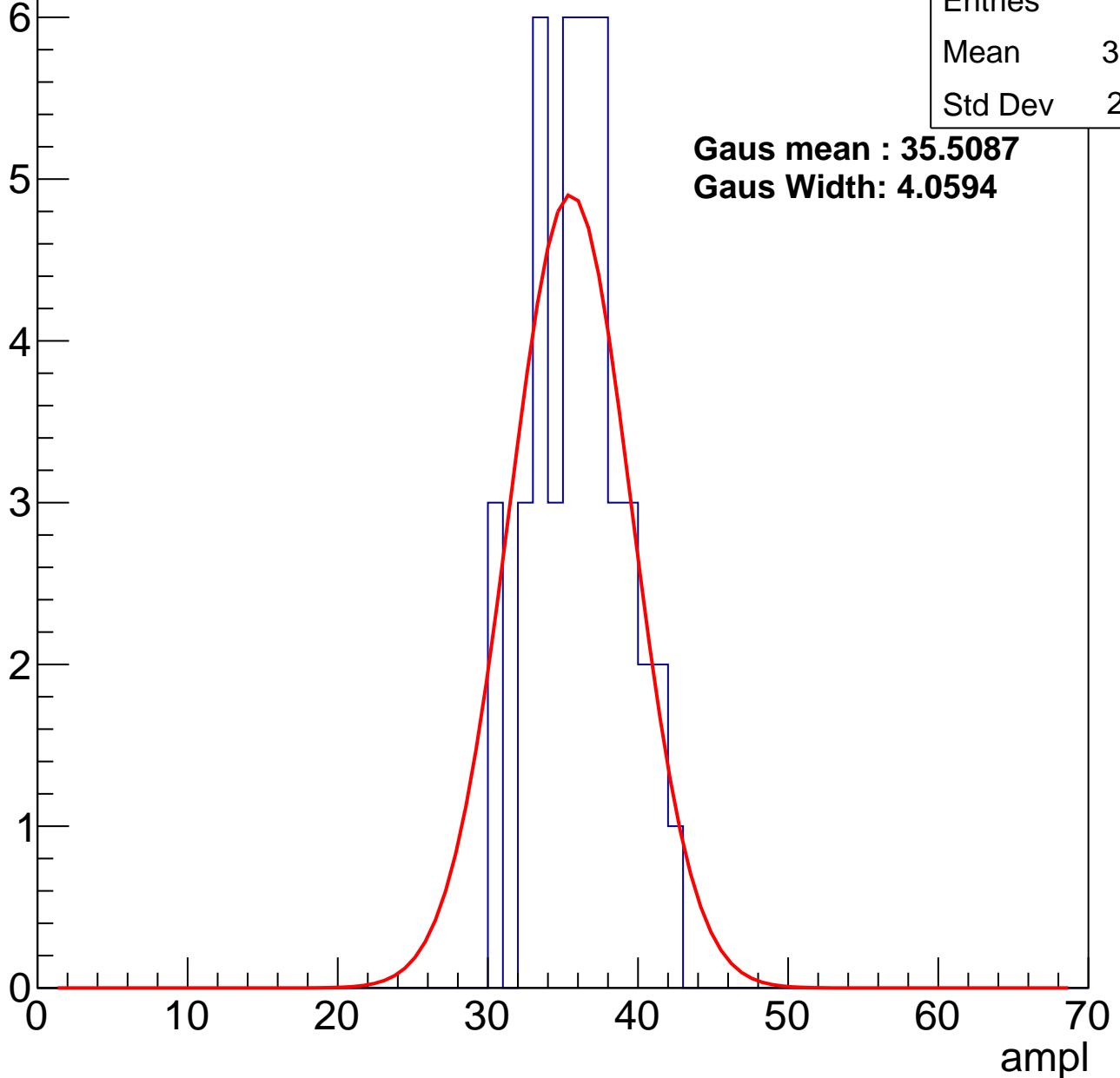
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	35.66
Std Dev	2.961

**Gaus mean : 35.5087**

**Gaus Width: 4.0594**



# B1L000S, U21-ch30, adc2

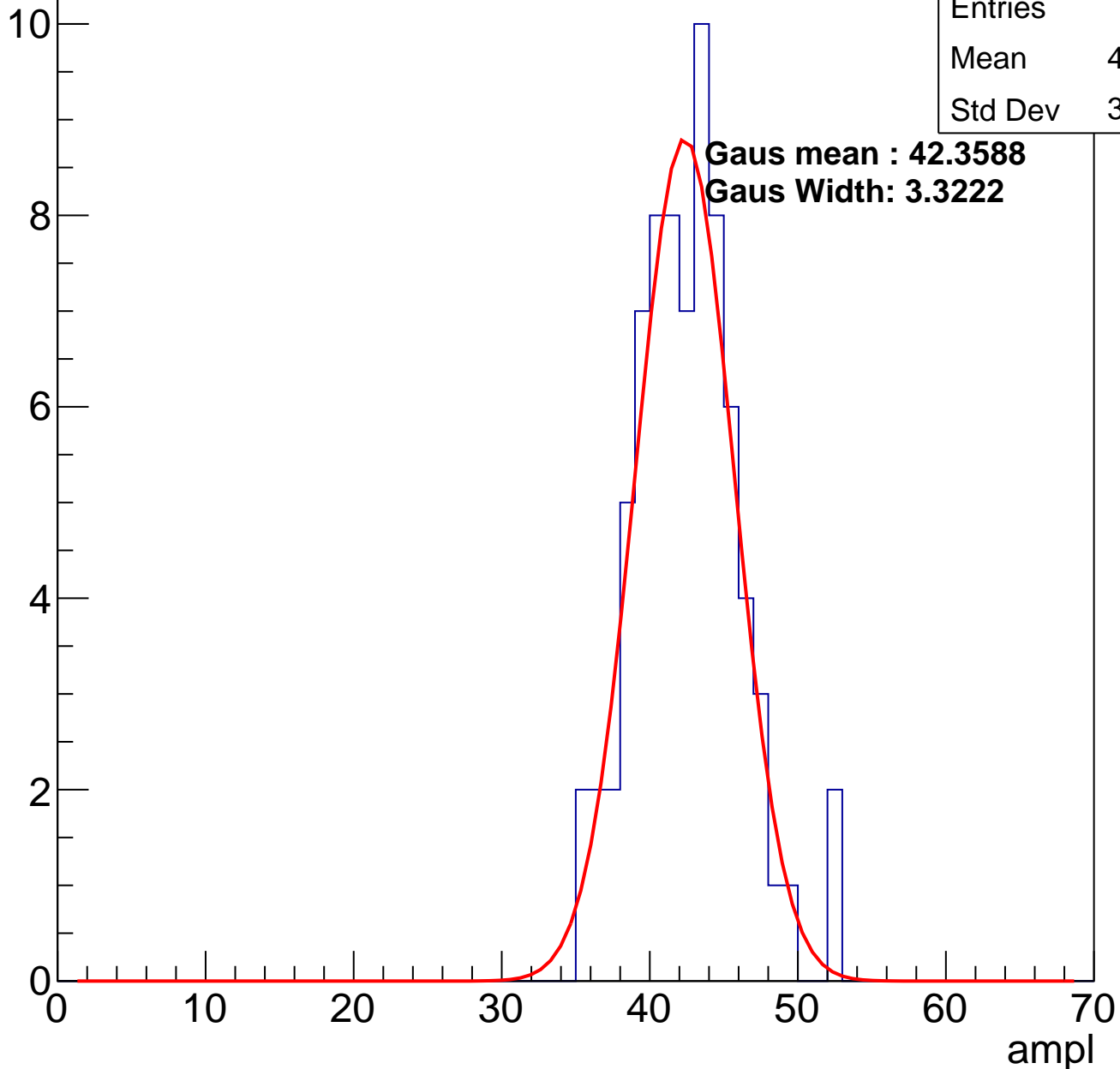
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	42.09
Std Dev	3.484

**Gaus mean : 42.3588**

**Gaus Width: 3.3222**

Entry

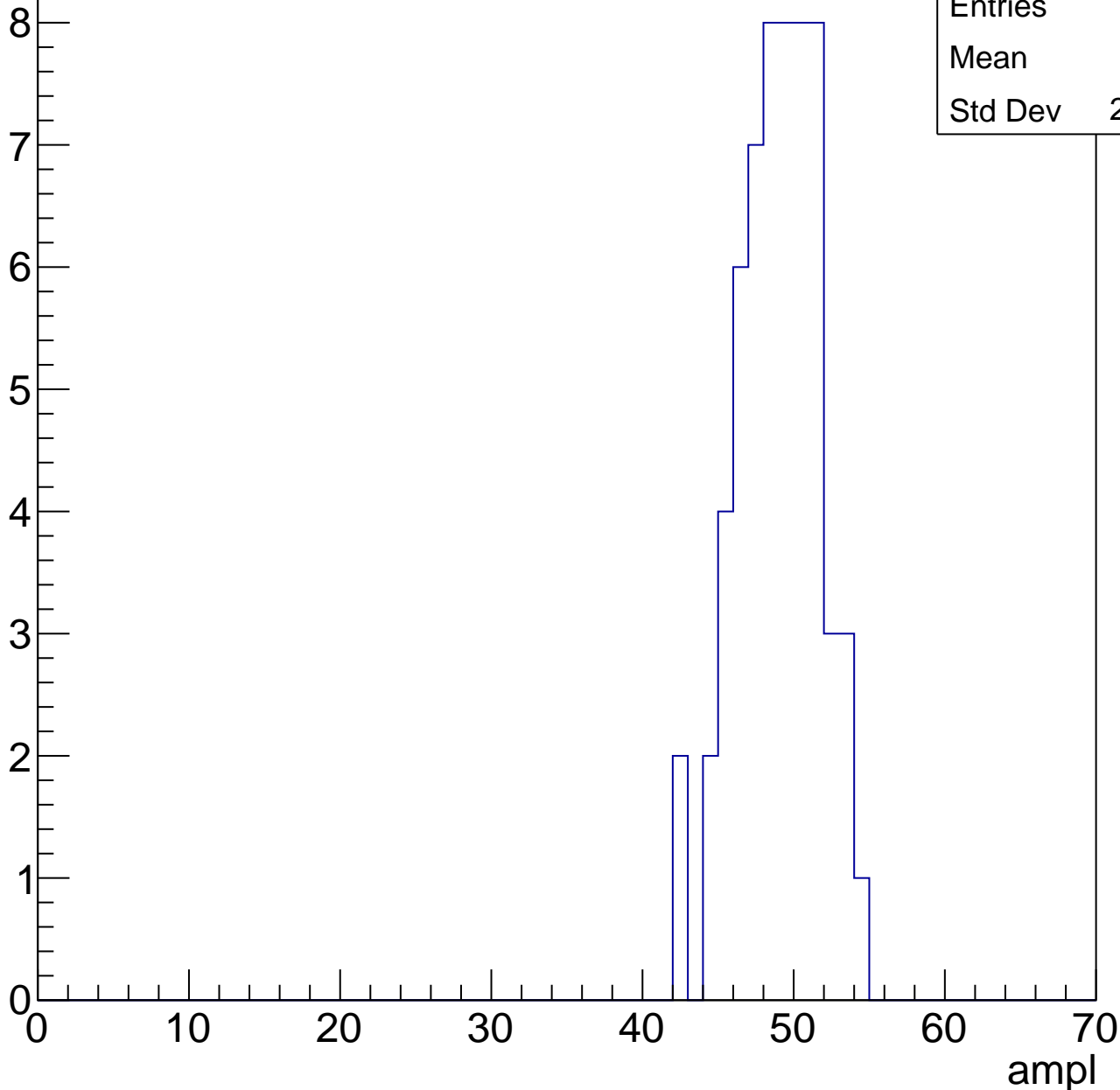


# B1L000S, U21-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	48.5
Std Dev	2.668

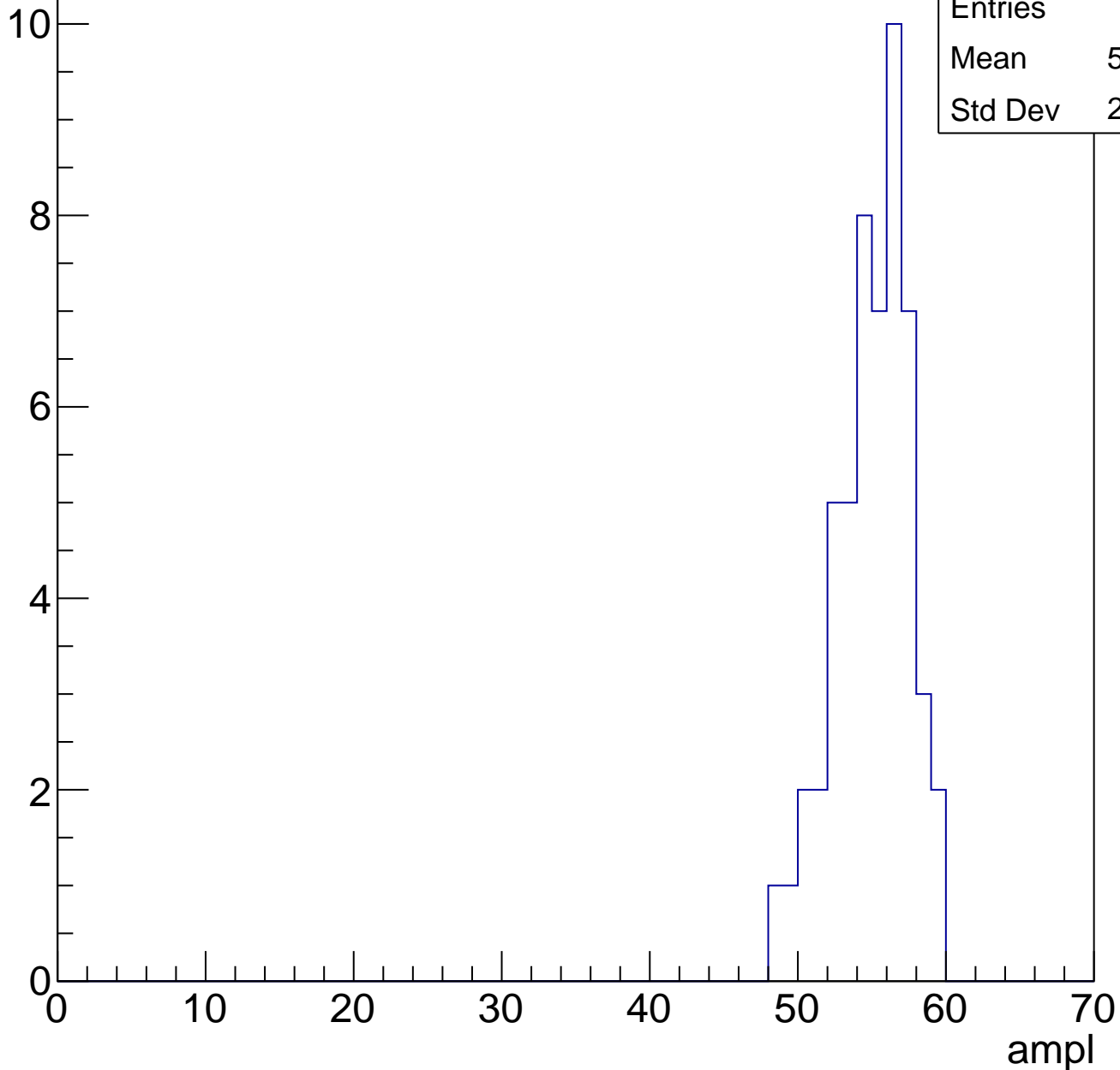


# B1L000S, U21-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	53
Mean	54.57
Std Dev	2.484

Entry

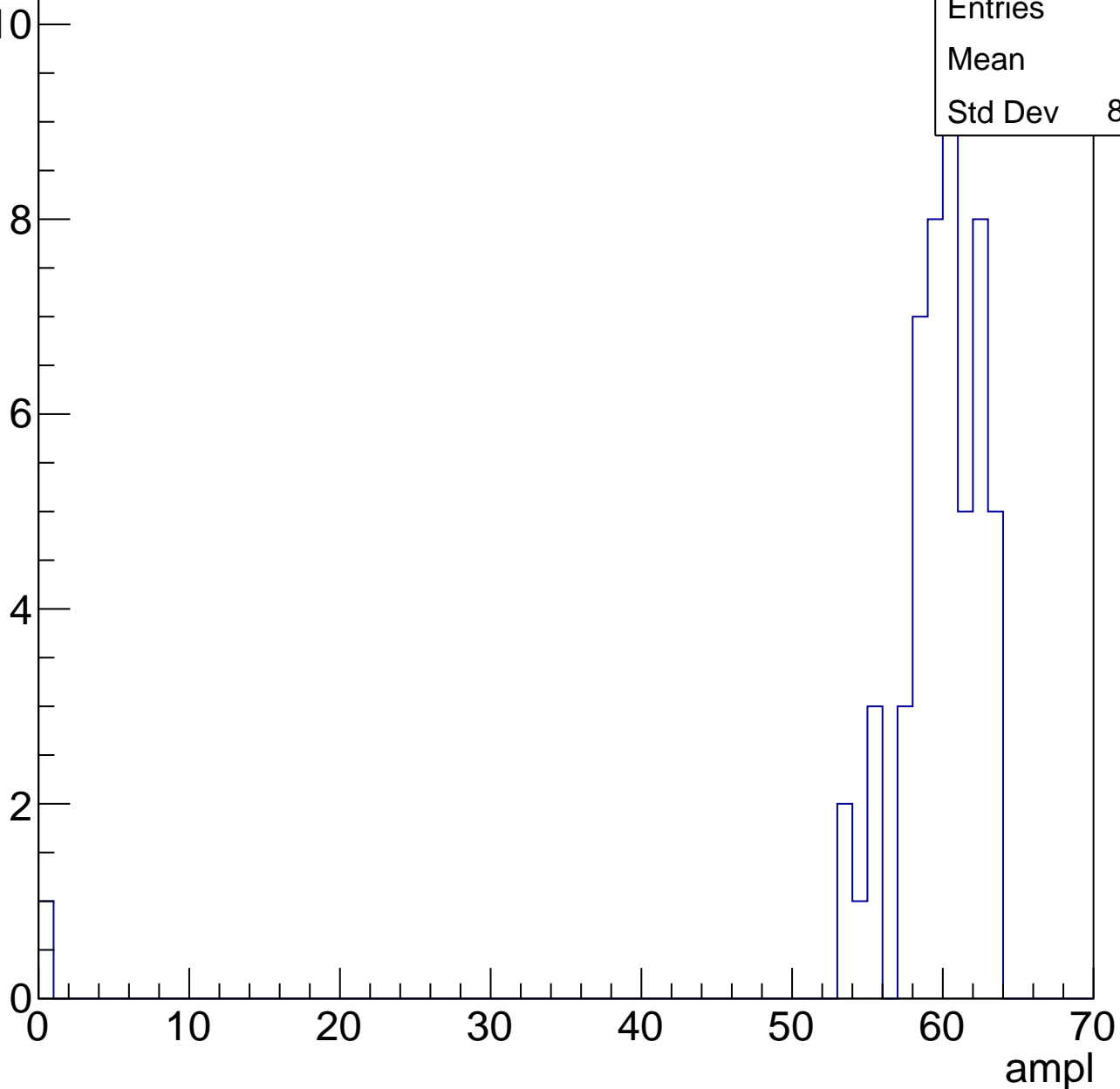


# B1L000S, U21-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	58.3
Std Dev	8.469

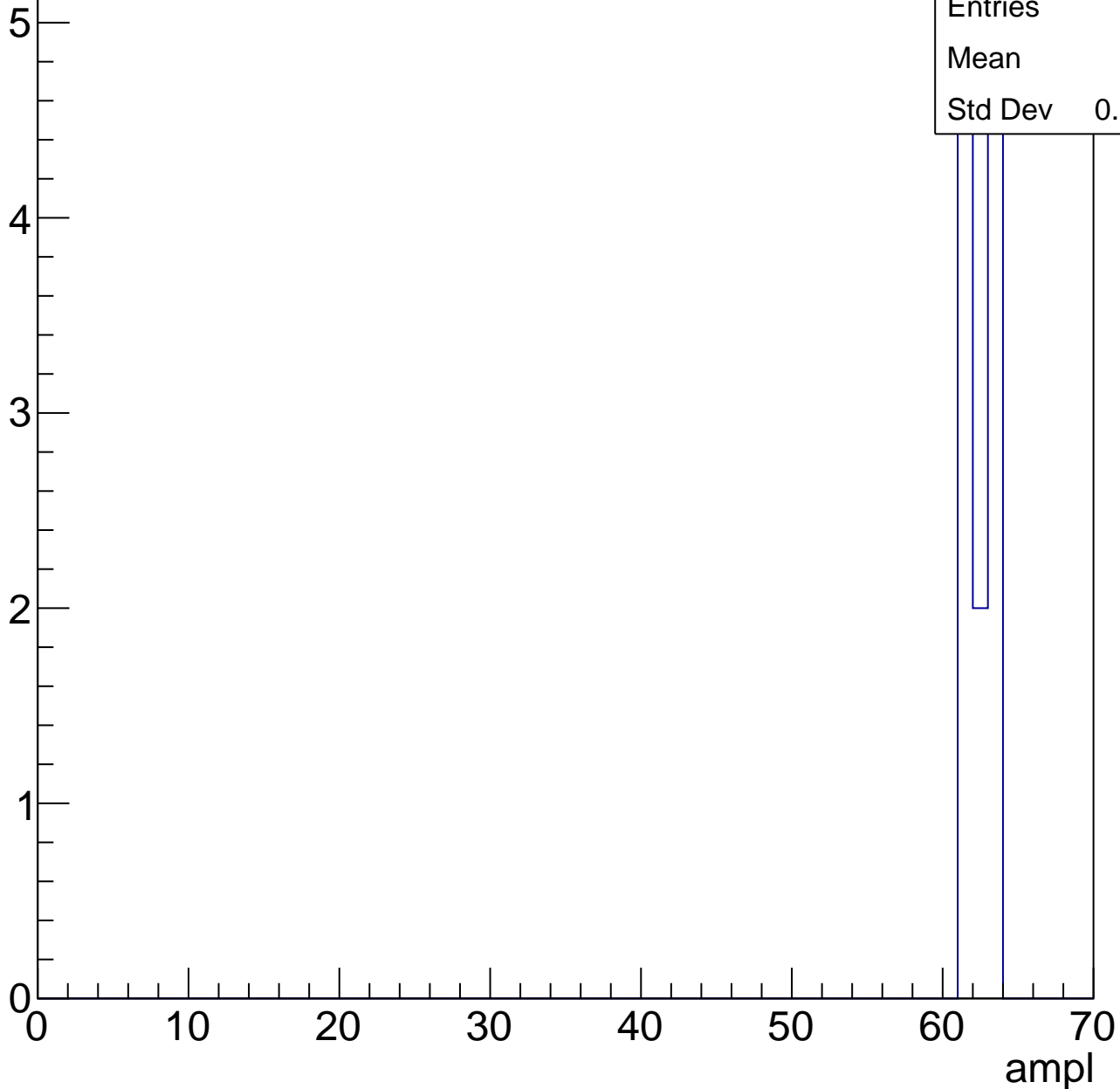


# B1L000S, U21-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	62
Std Dev	0.9129





# B1L000S, U21-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch31, adc0

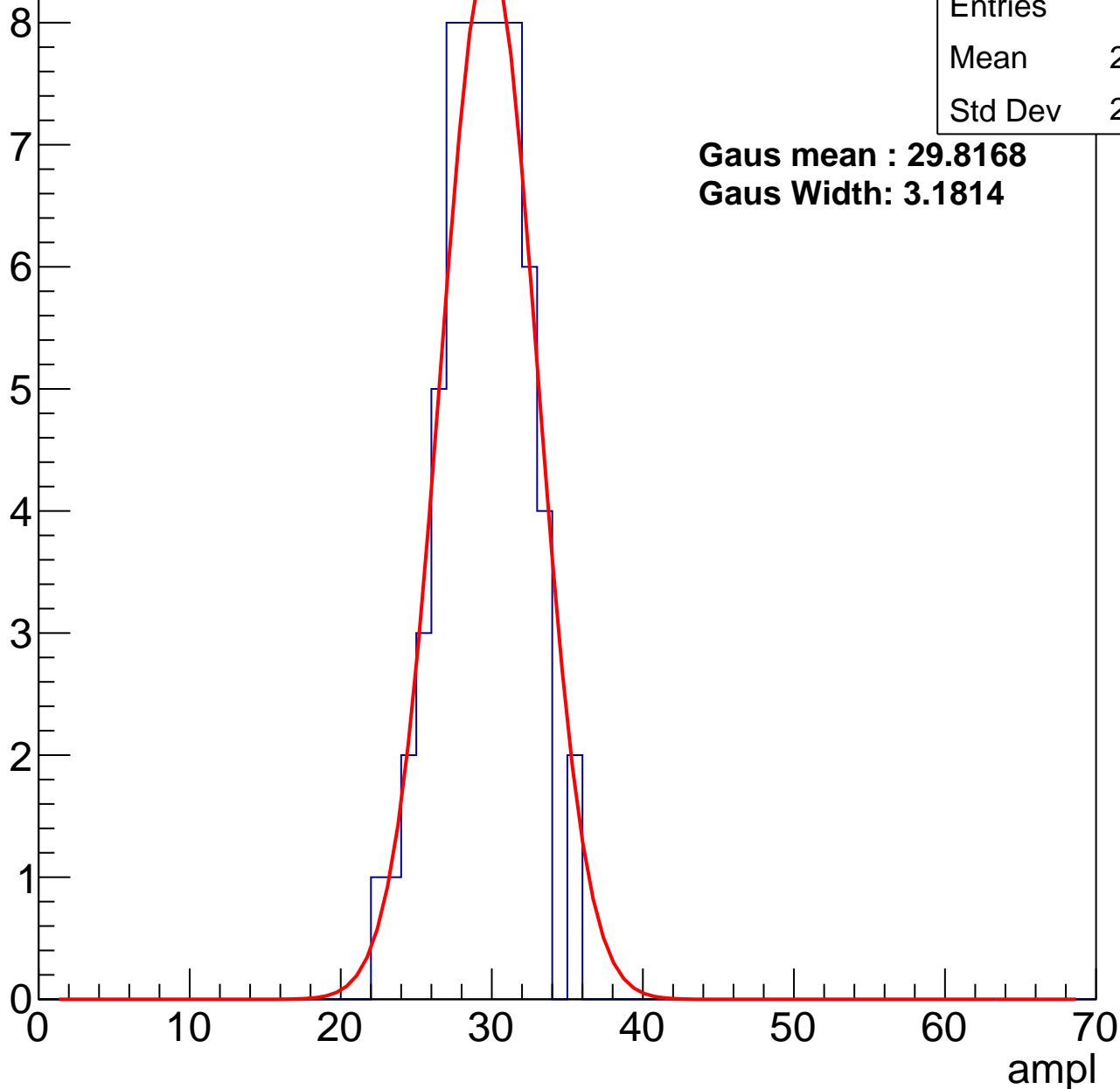
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.94
Std Dev	2.789

**Gaus mean : 29.8168**

**Gaus Width: 3.1814**



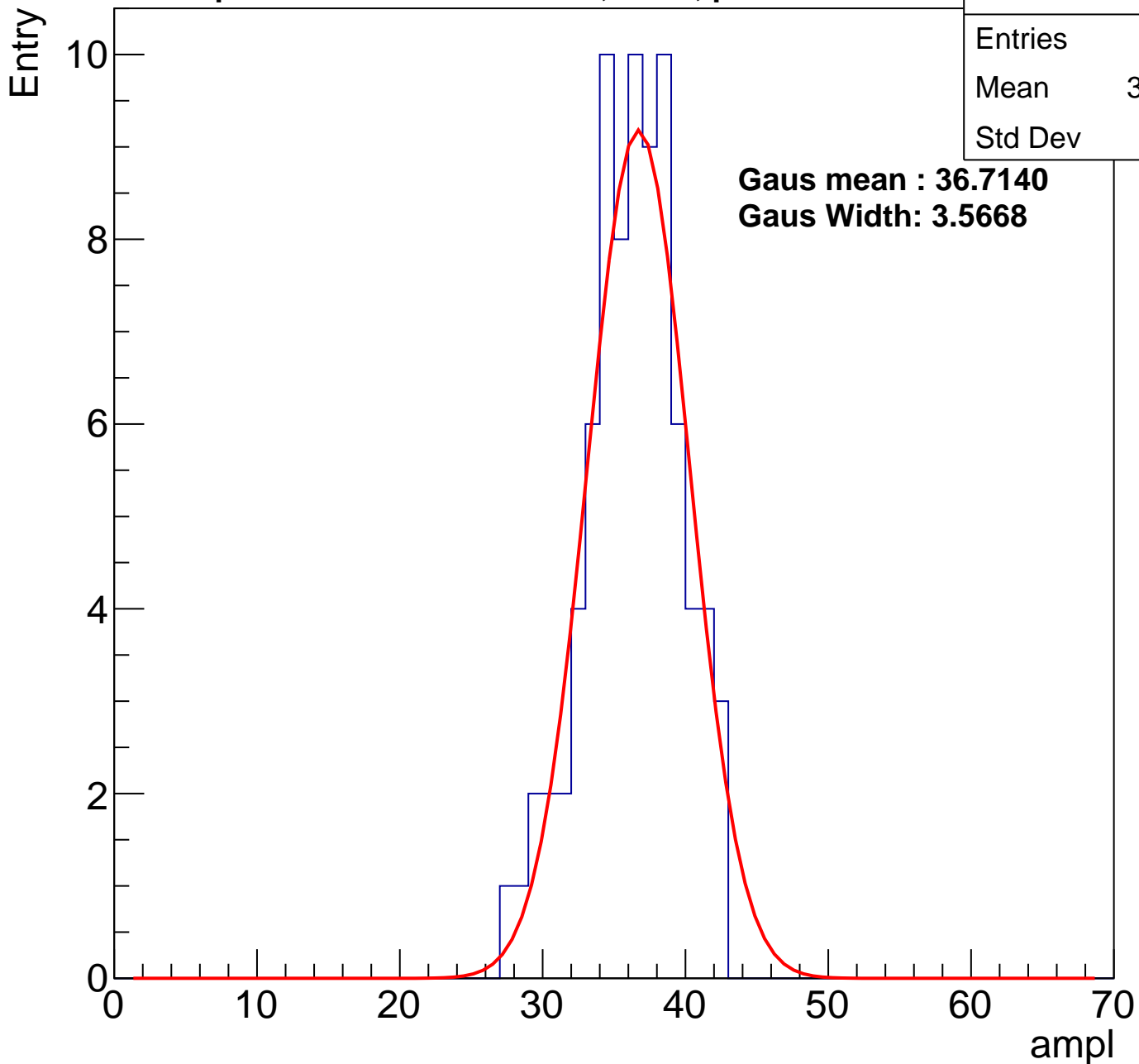
# B1L000S, U21-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	82
Mean	35.83
Std Dev	3.32

**Gaus mean : 36.7140**

**Gaus Width: 3.5668**



# B1L000S, U21-ch31, adc2

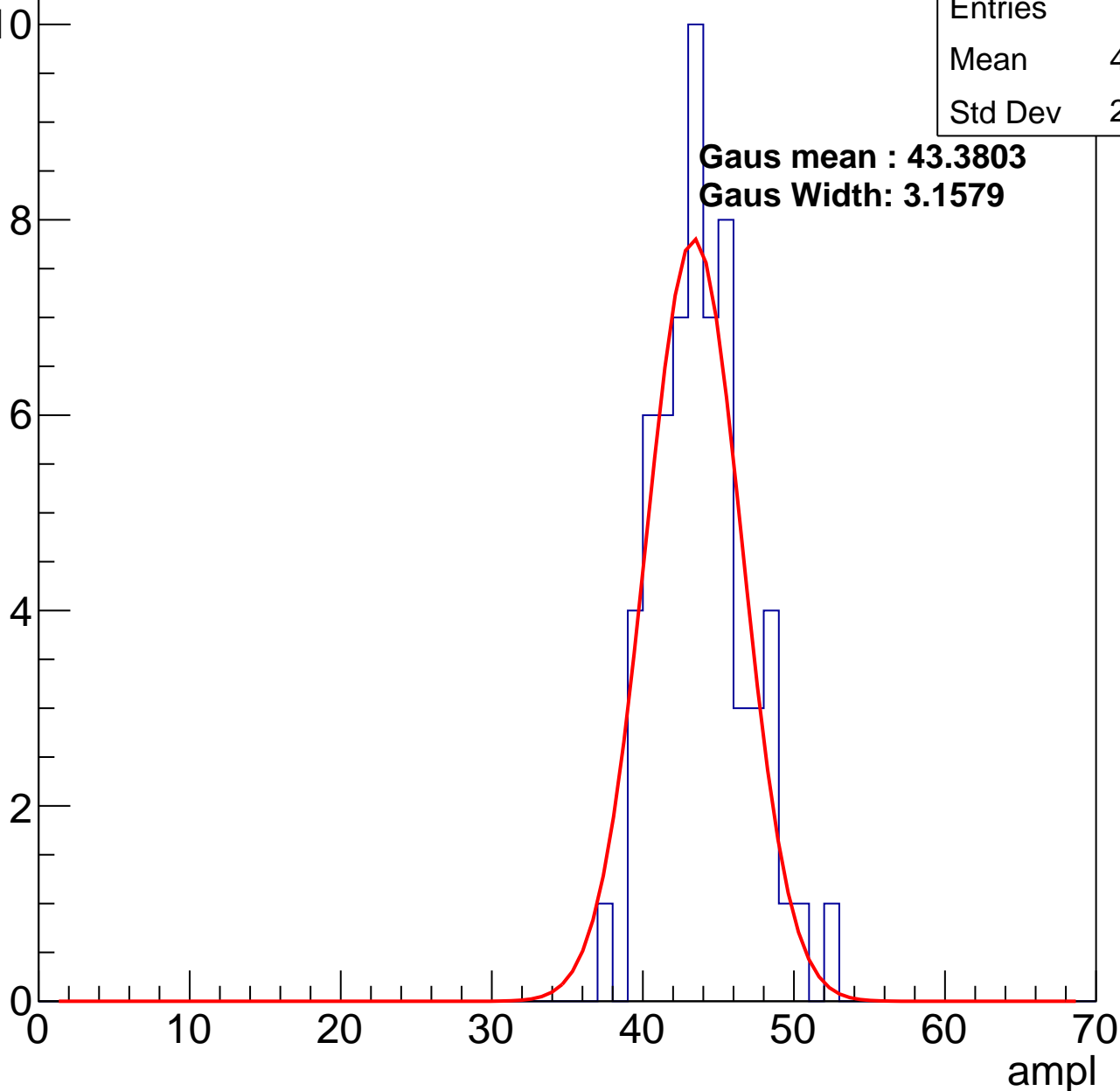
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	43.44
Std Dev	2.987

**Gaus mean : 43.3803**

**Gaus Width: 3.1579**

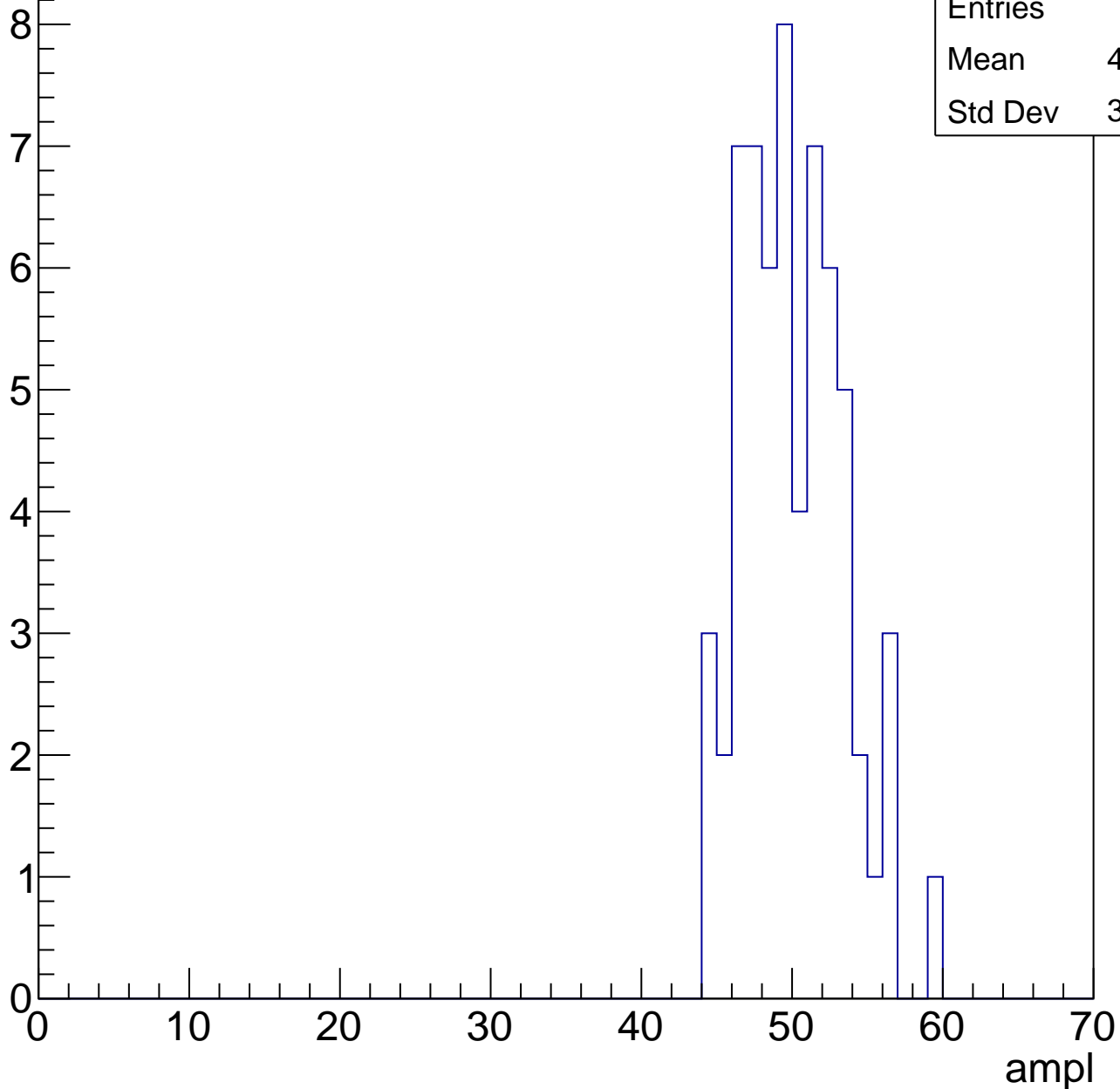


# B1L000S, U21-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	49.63
Std Dev	3.308

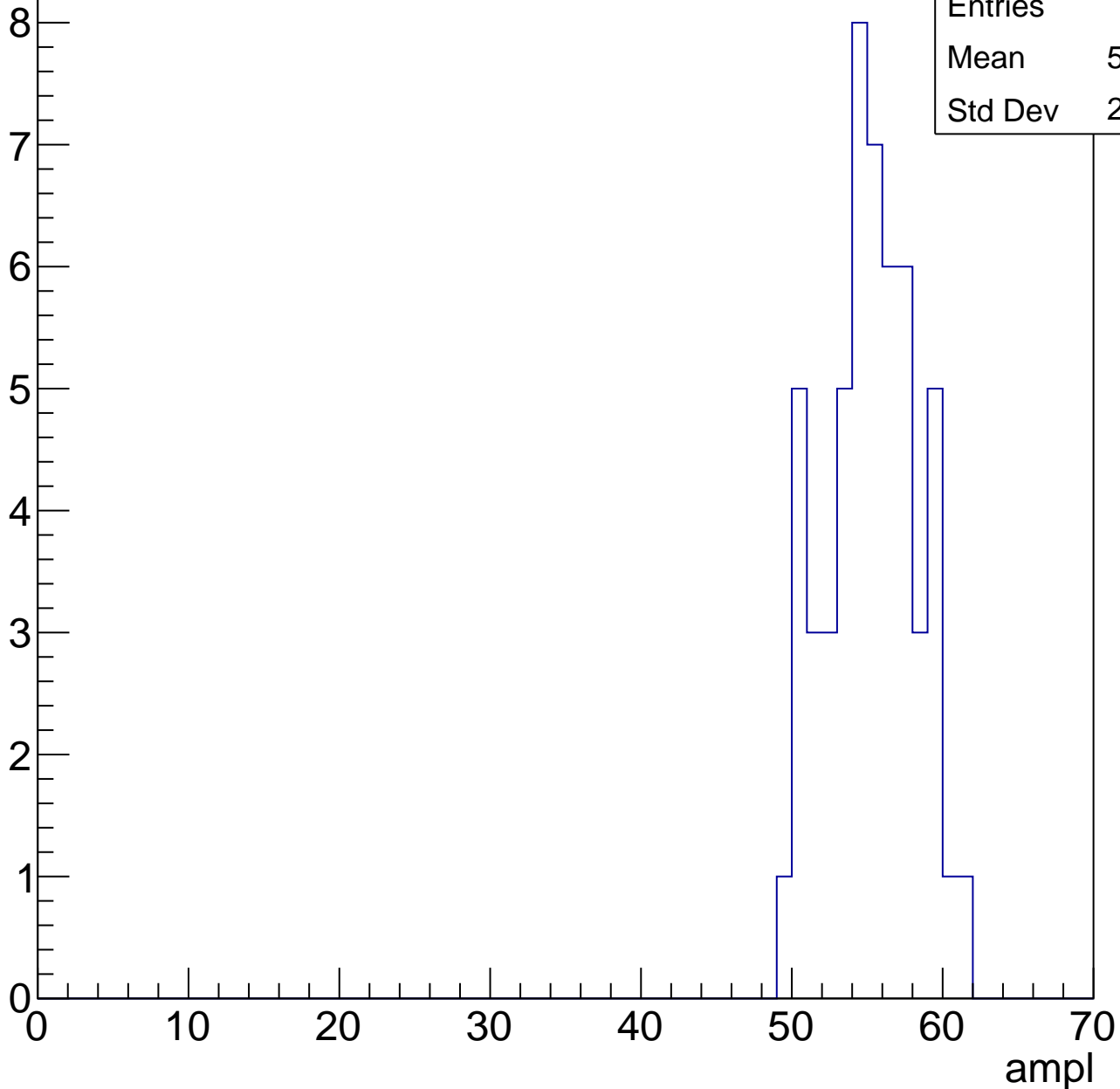


# B1L000S, U21-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	54.78
Std Dev	2.917



# B1L000S, U21-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	58.36
Std Dev	9.16

0

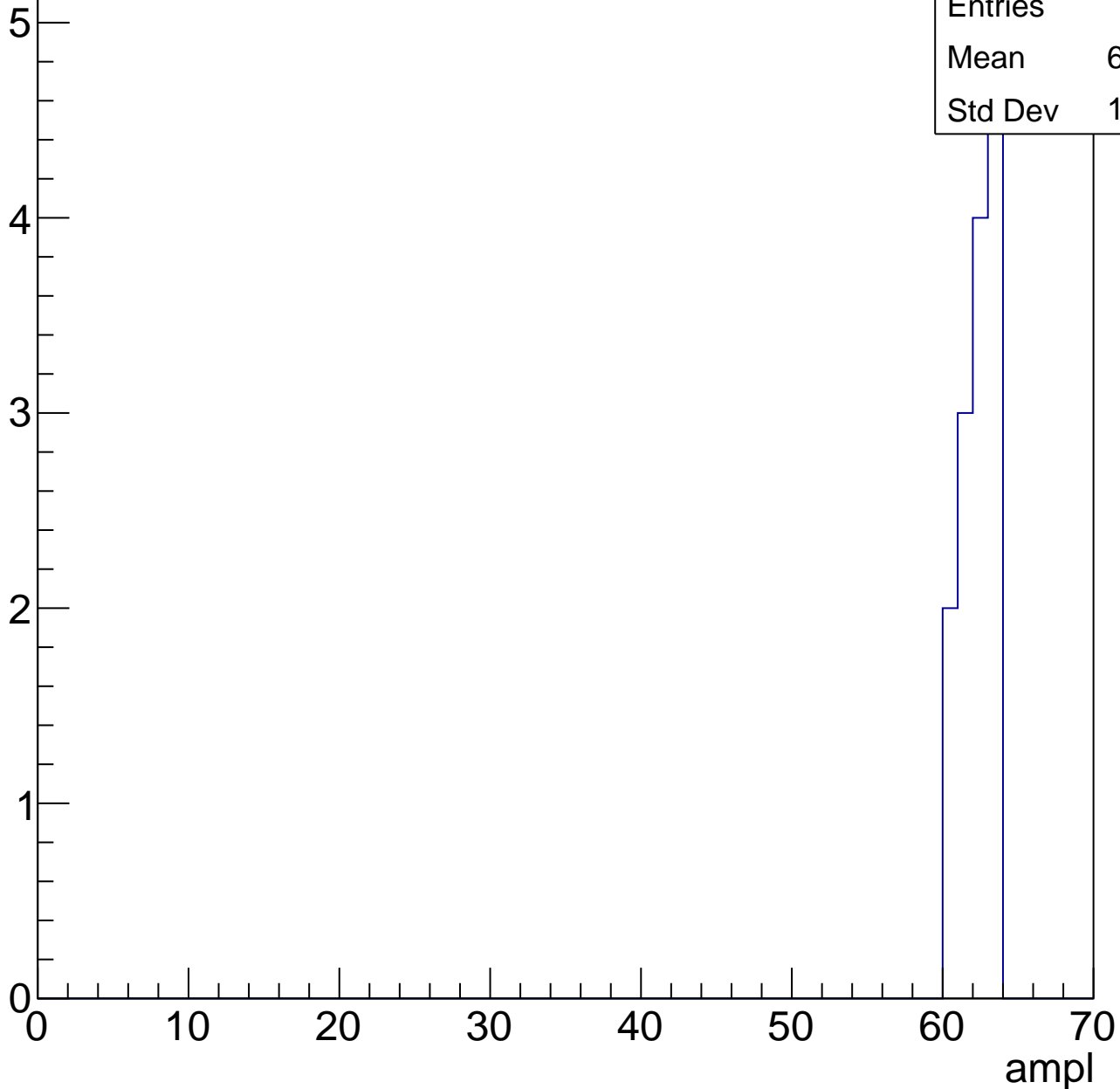
ampl

# B1L000S, U21-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.86
Std Dev	1.059

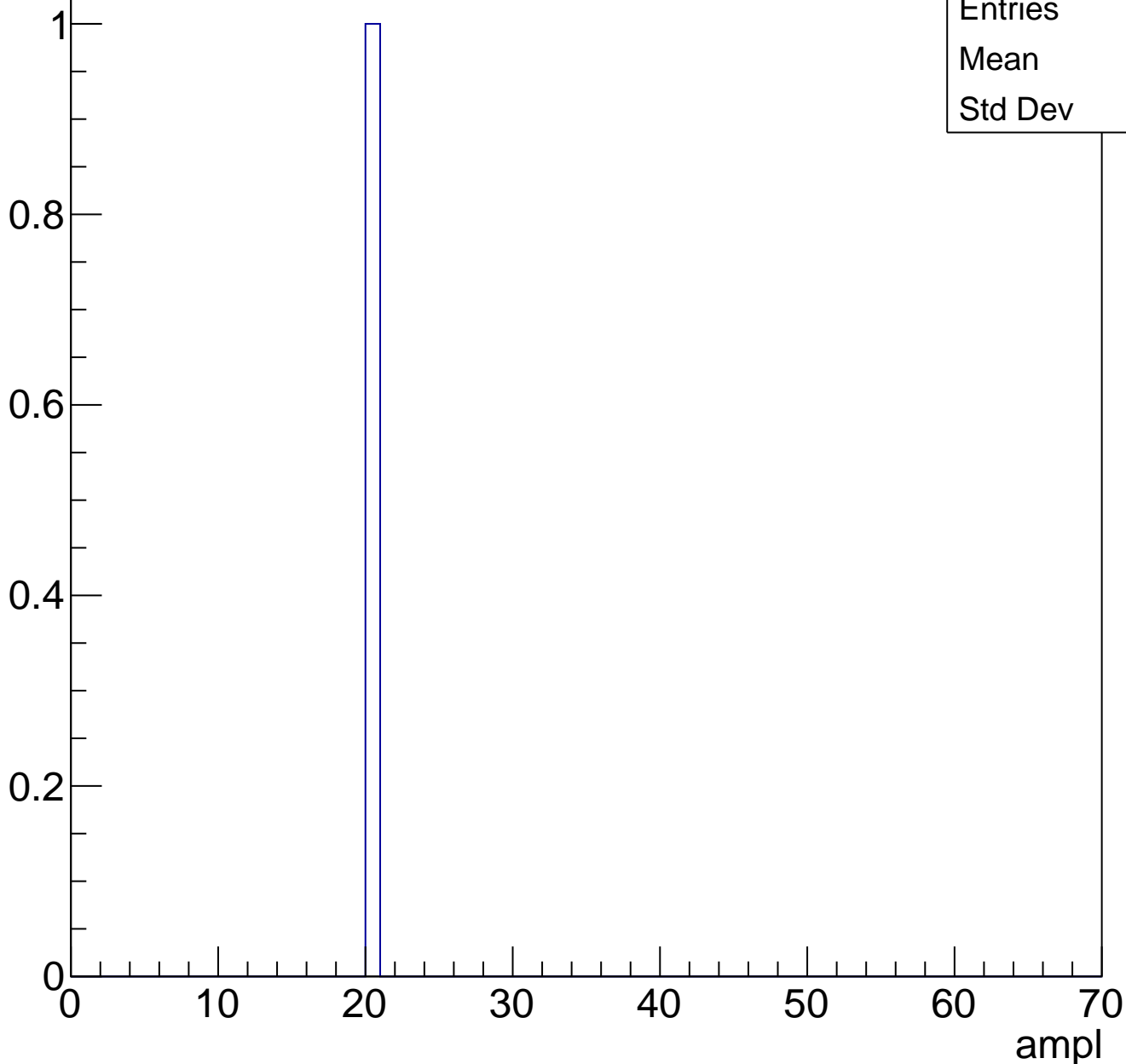




# B1L000S, U21-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U21-ch32, adc0

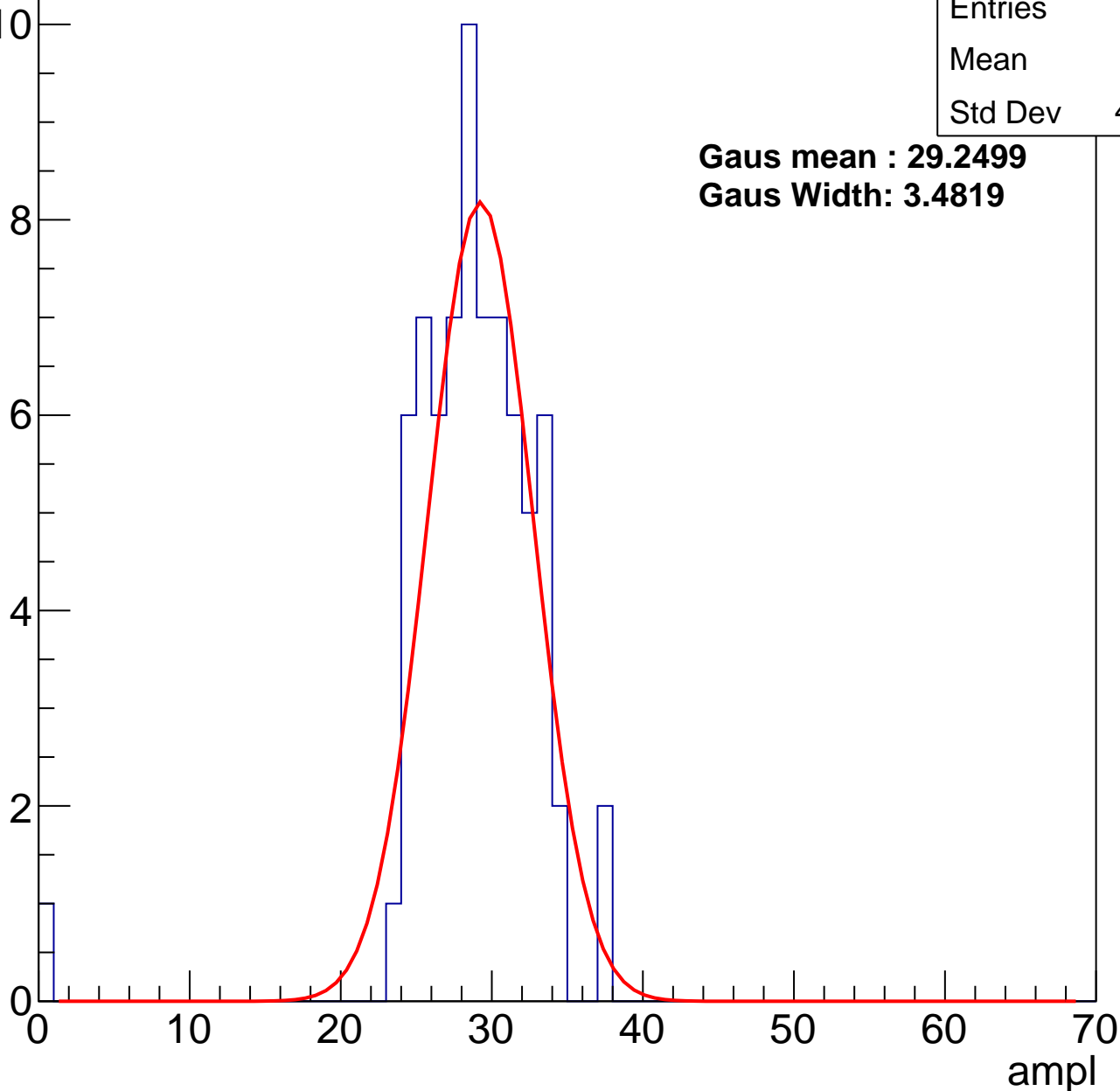
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.3
Std Dev	4.601

**Gaus mean : 29.2499**

**Gaus Width: 3.4819**



# B1L000S, U21-ch32, adc1

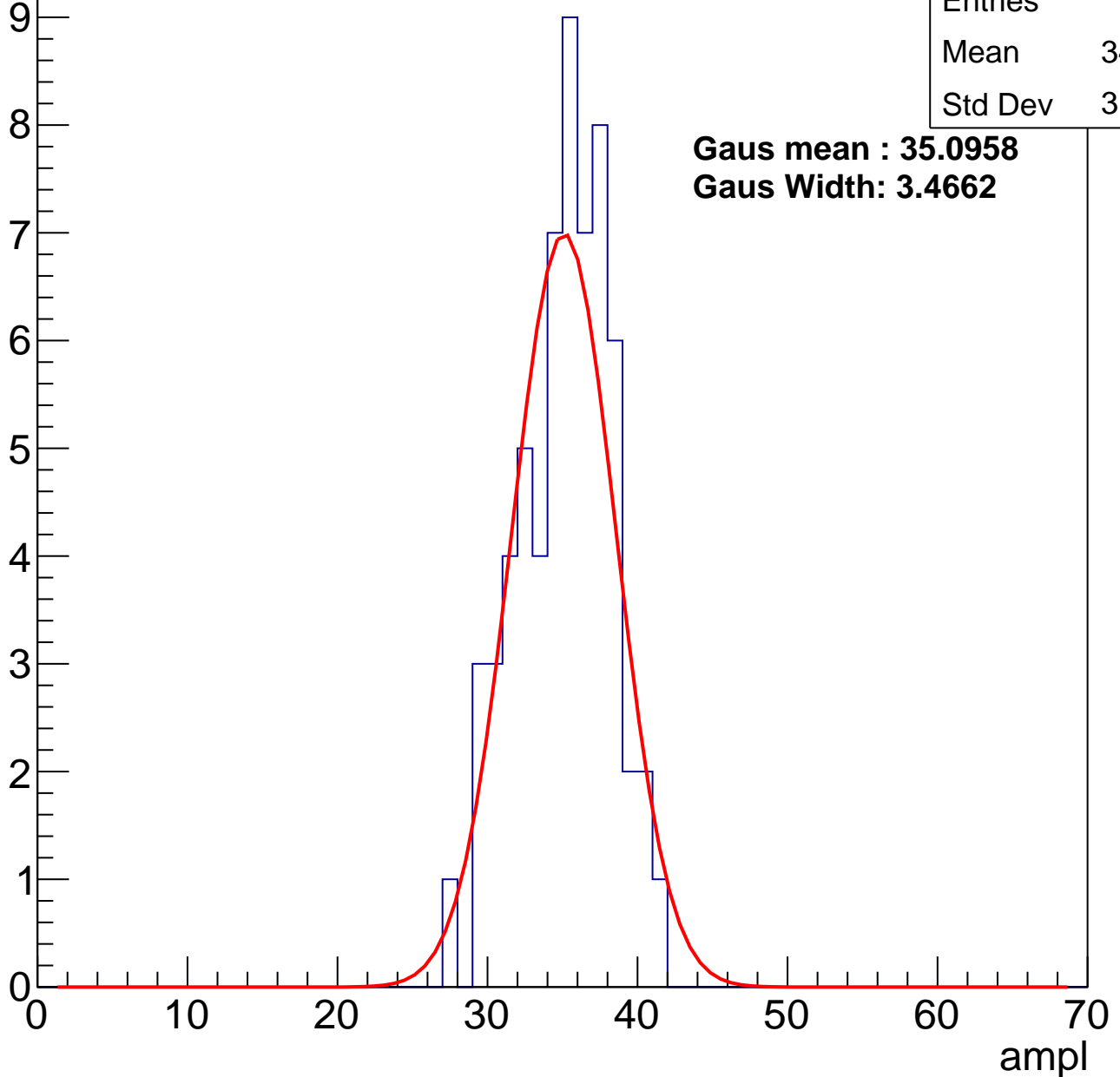
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	34.65
Std Dev	3.064

**Gaus mean : 35.0958**

**Gaus Width: 3.4662**



# B1L000S, U21-ch32, adc2

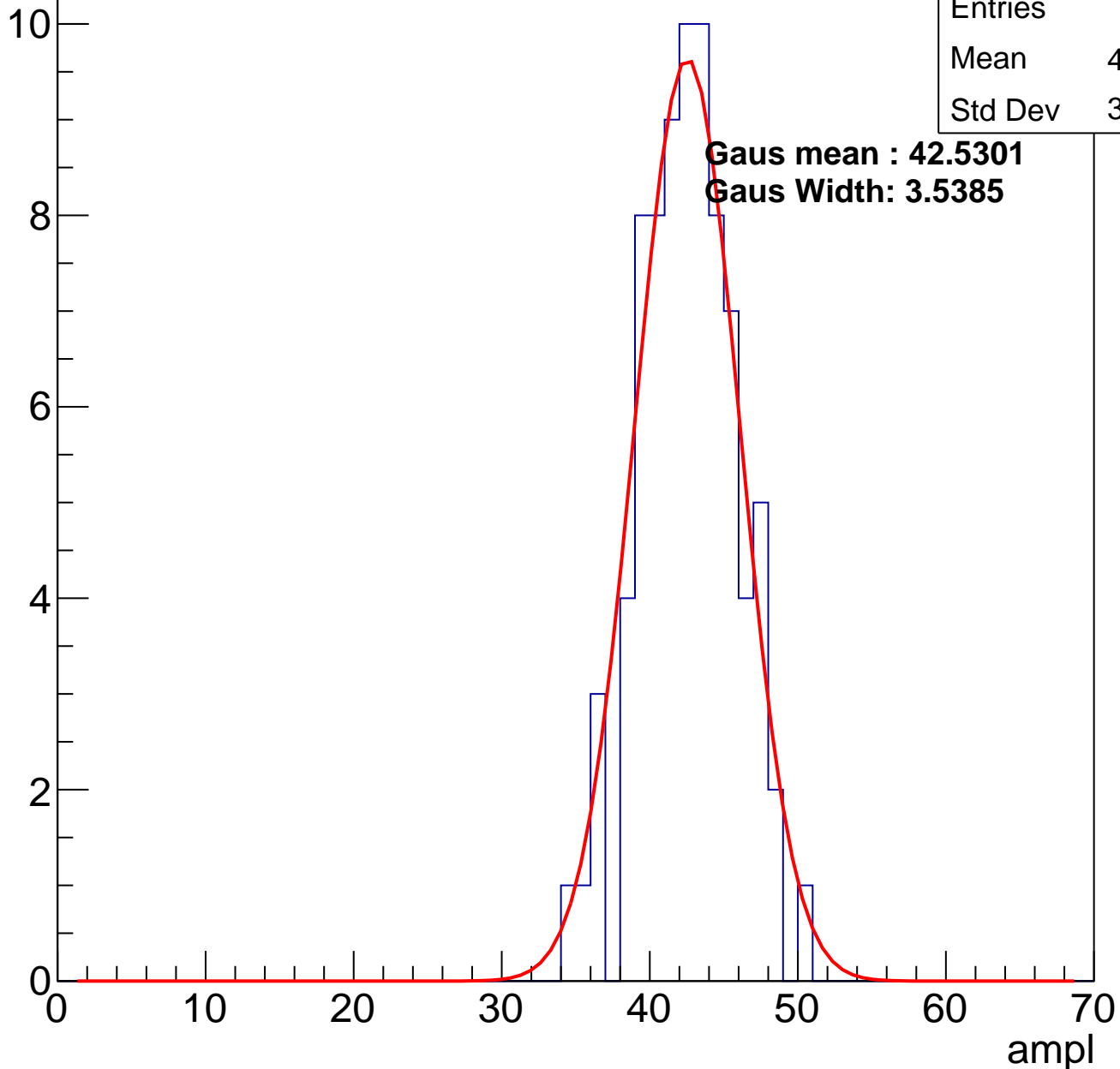
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	42.12
Std Dev	3.195

**Gaus mean : 42.5301**

**Gaus Width: 3.5385**

Entry

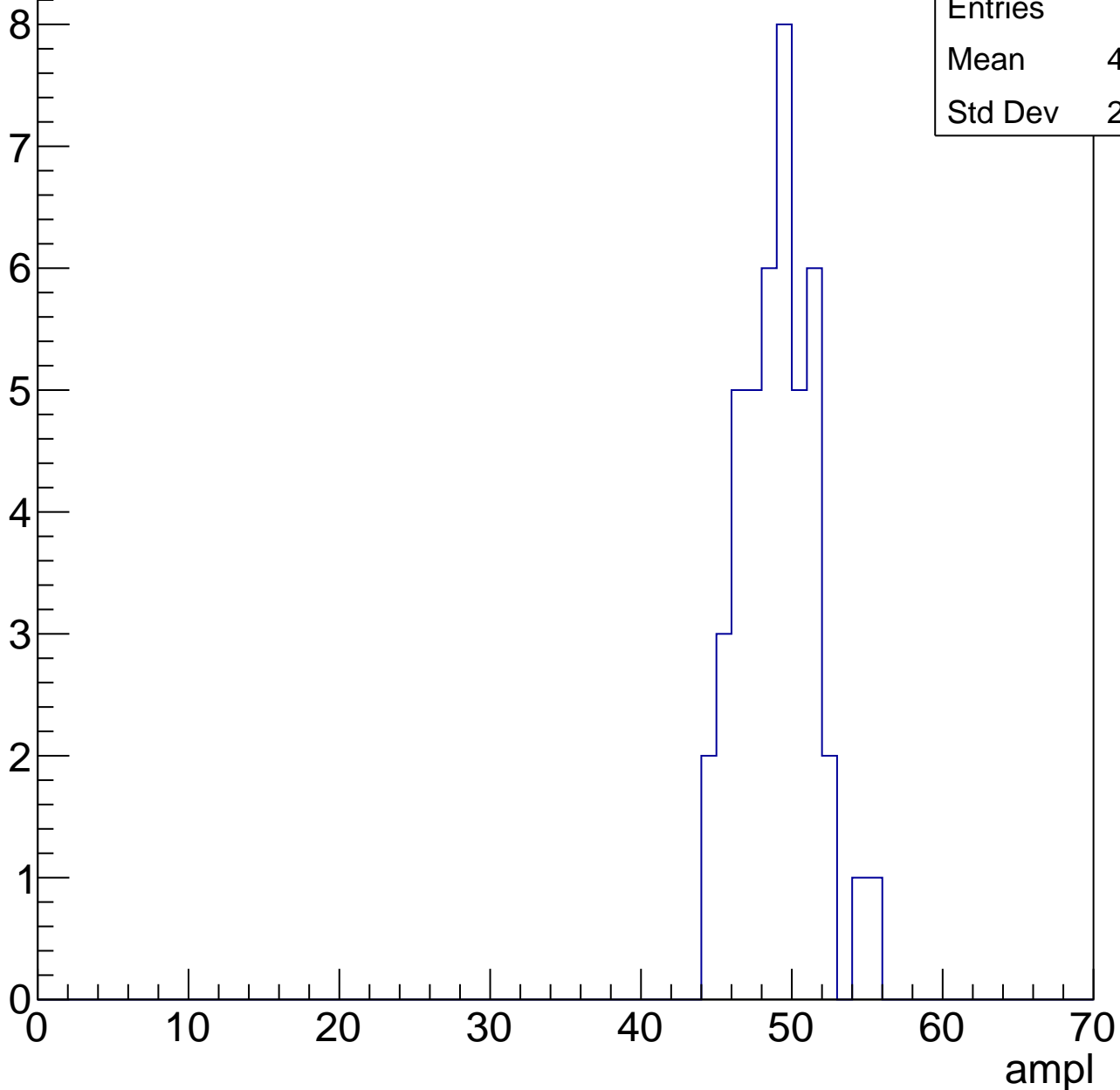


# B1L000S, U21-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	48.57
Std Dev	2.472

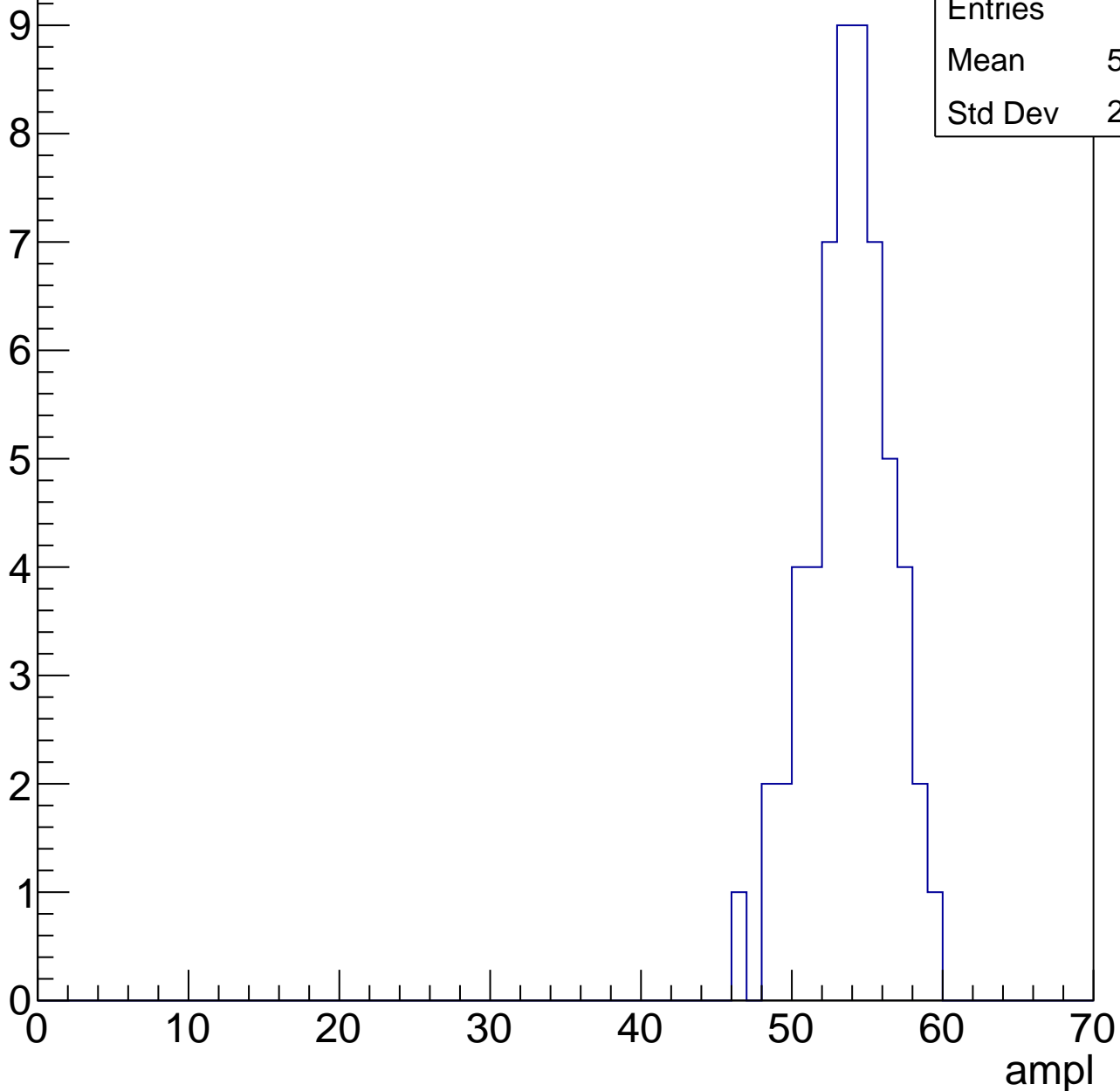


# B1L000S, U21-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	53.32
Std Dev	2.703

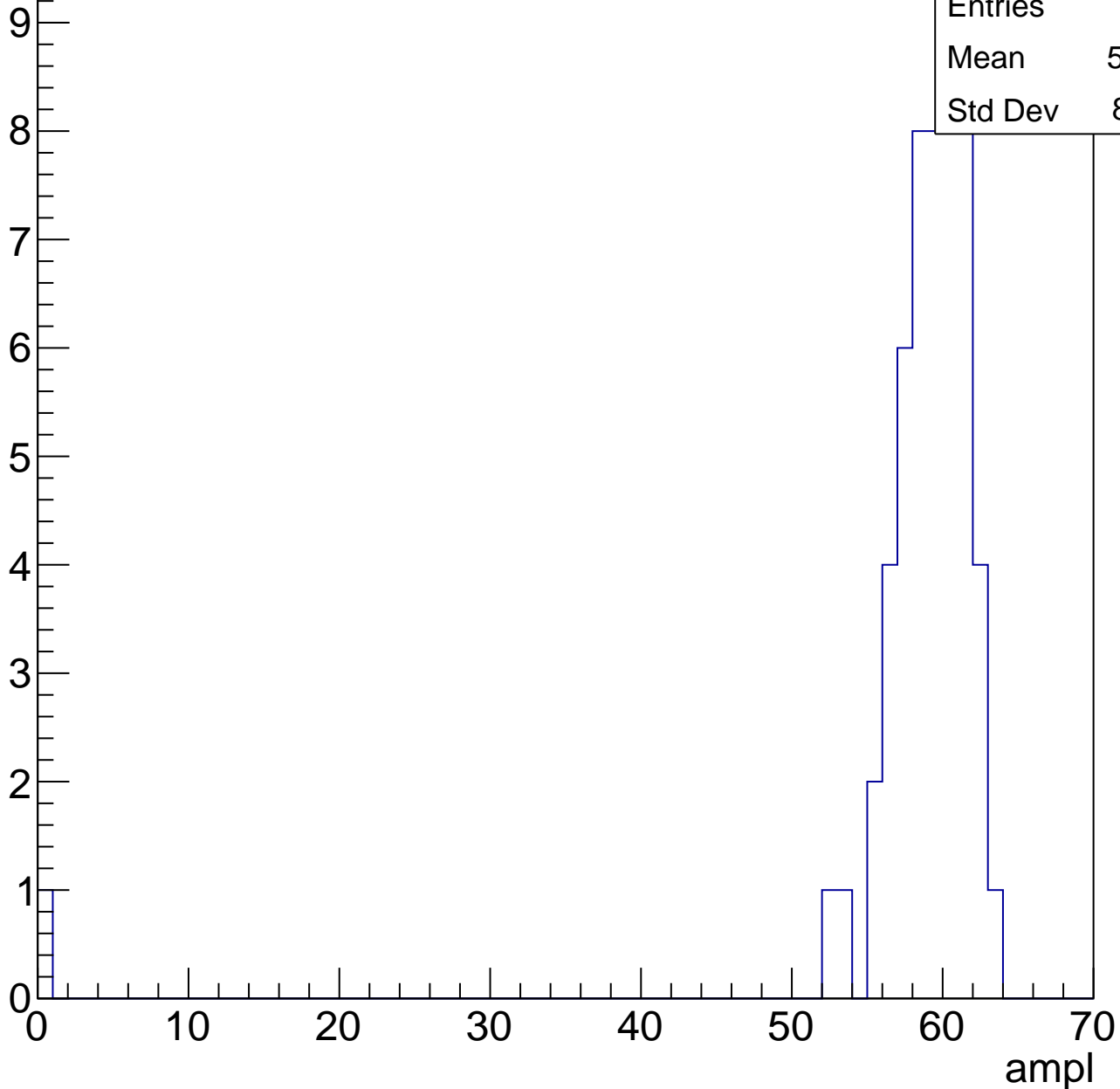


# B1L000S, U21-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	57.68
Std Dev	8.321

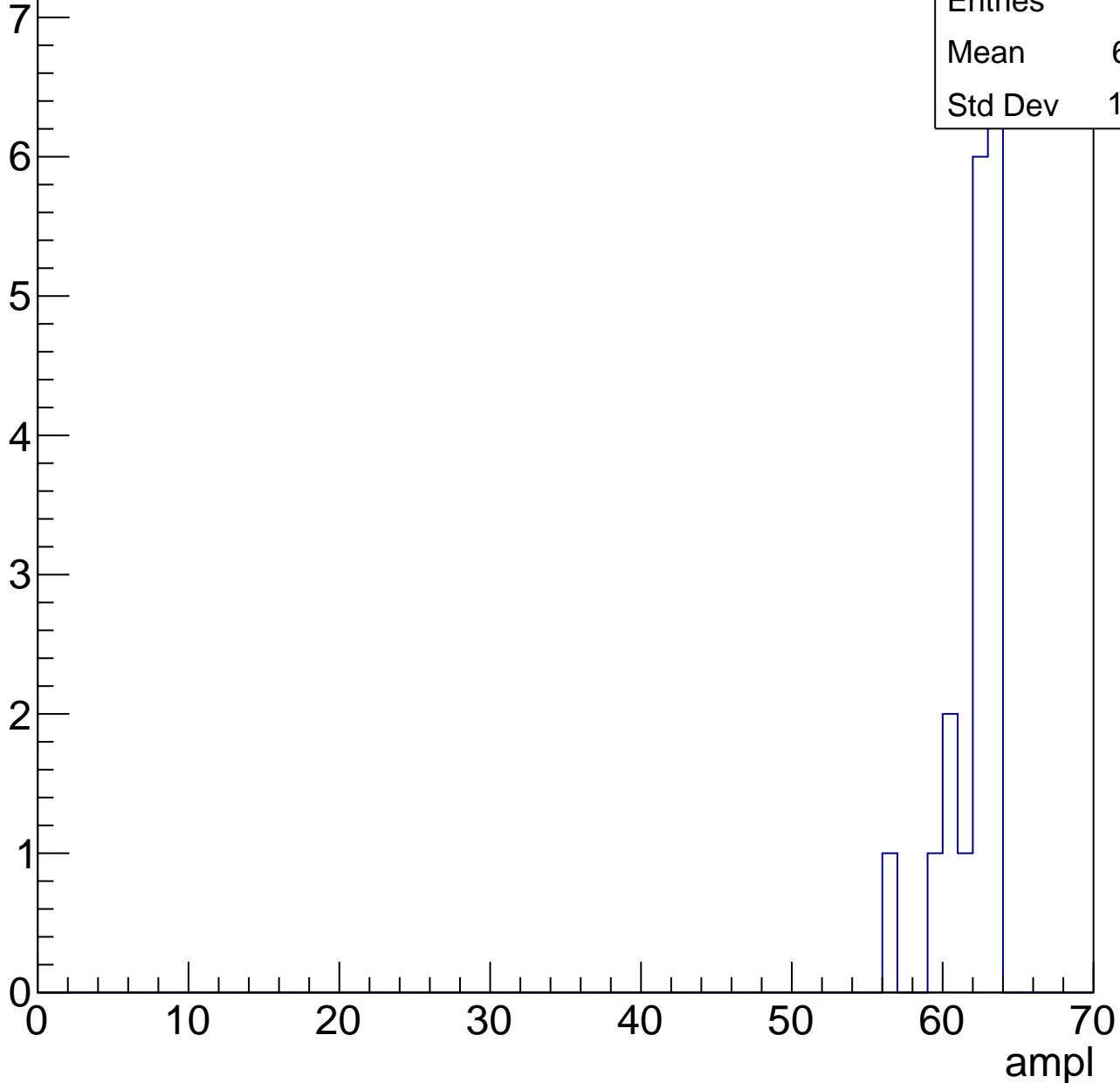


# B1L000S, U21-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.61
Std Dev	1.799





# B1L000S, U21-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch33, adc0

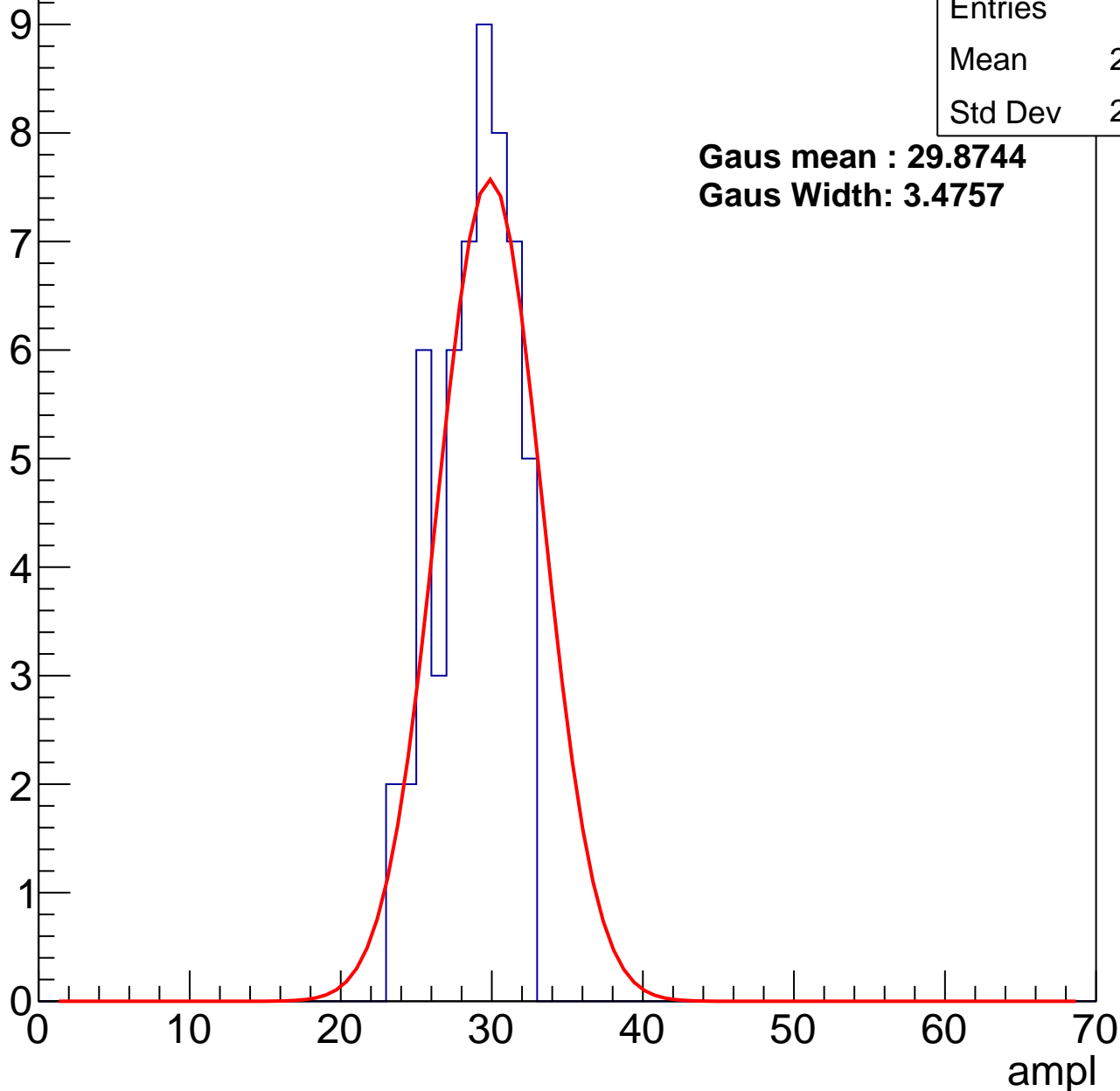
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	28.33
Std Dev	2.457

**Gaus mean : 29.8744**

**Gaus Width: 3.4757**



# B1L000S, U21-ch33, adc1

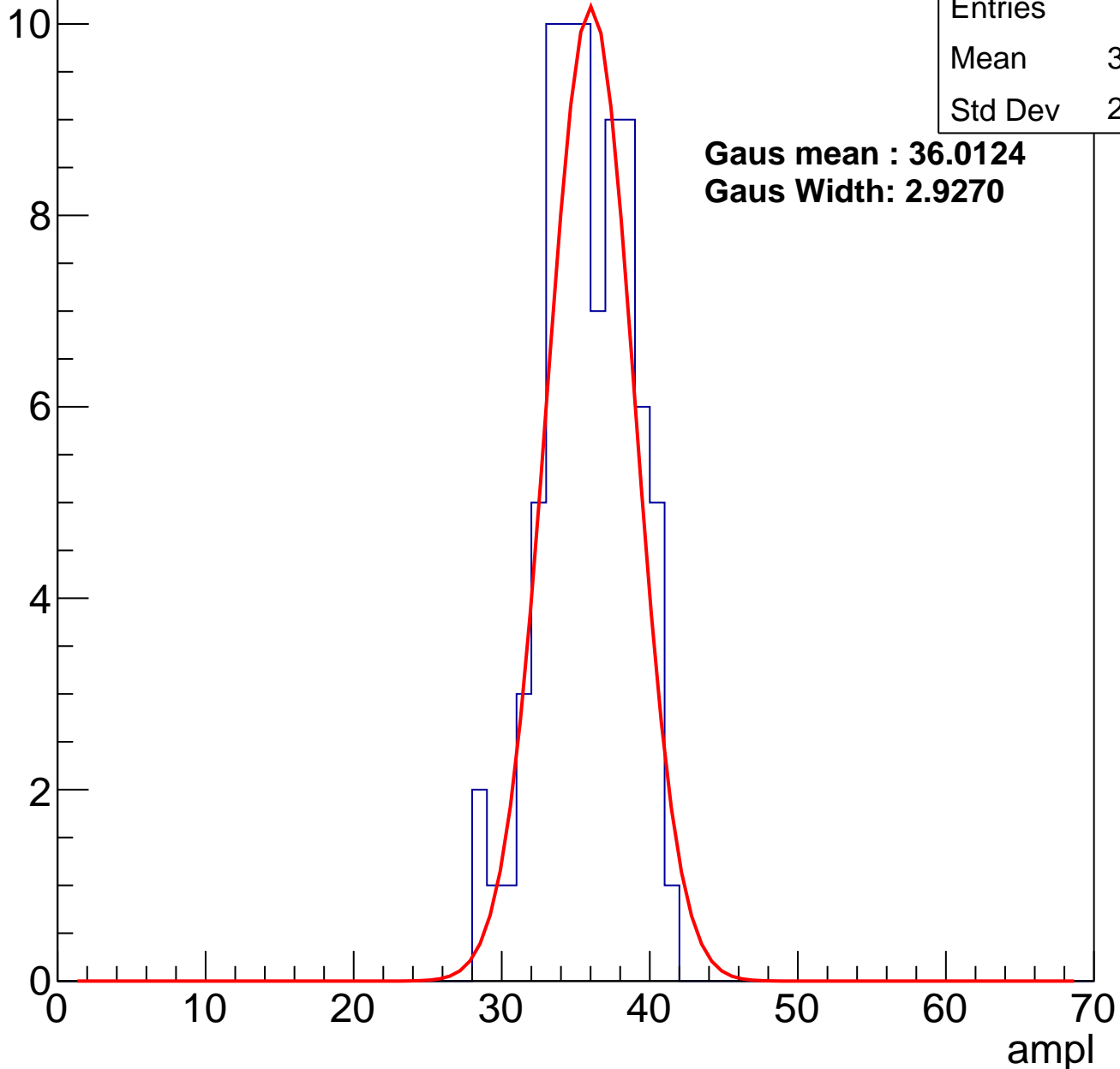
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	35.32
Std Dev	2.923

**Gaus mean : 36.0124**

**Gaus Width: 2.9270**

Entry



# B1L000S, U21-ch33, adc2

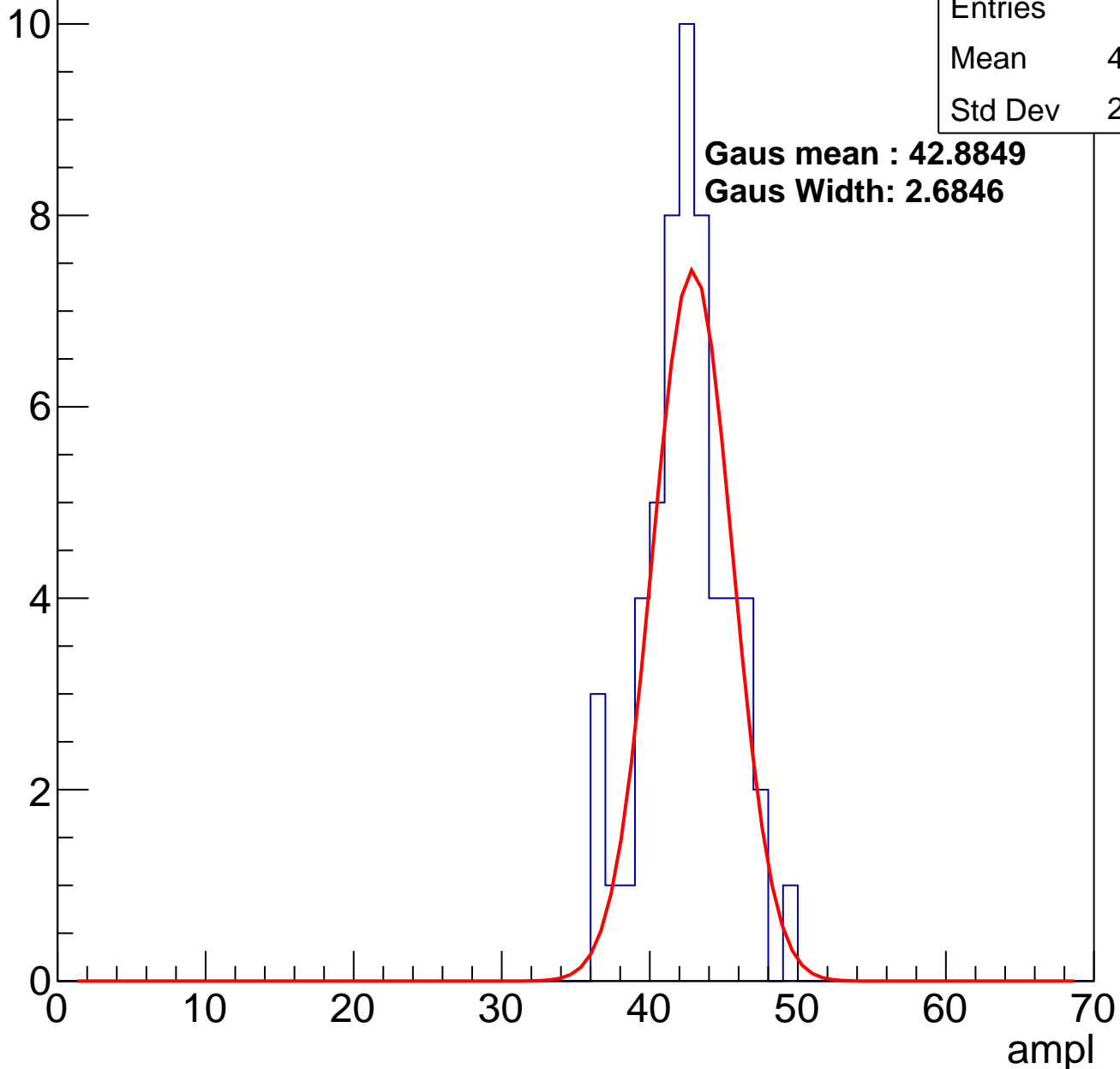
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	55
Mean	42.07
Std Dev	2.815

**Gaus mean : 42.8849**

**Gaus Width: 2.6846**

Entry



# B1L000S, U21-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

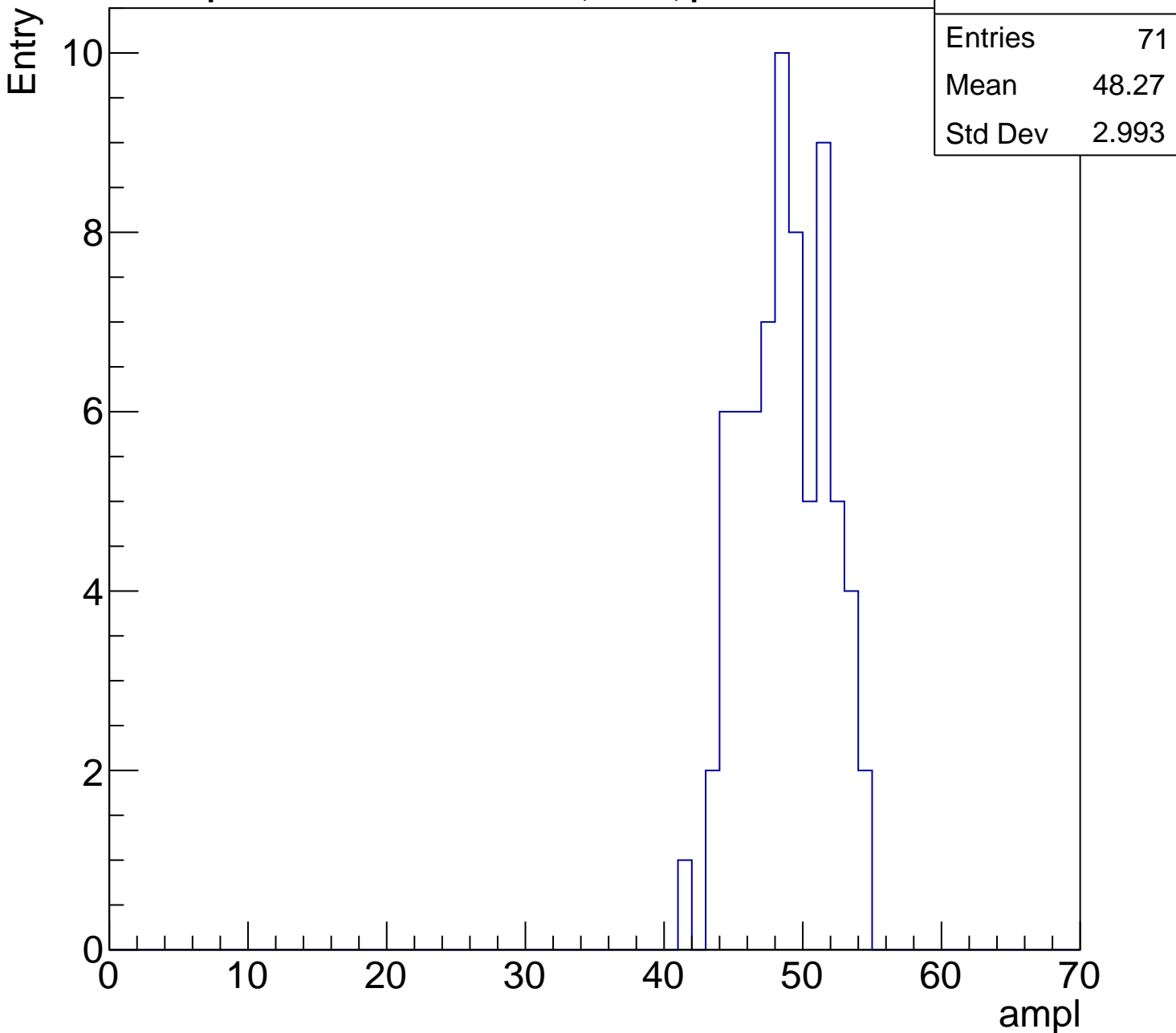
Entries	71
Mean	48.27
Std Dev	2.993

Entry

10  
8  
6  
4  
2  
0

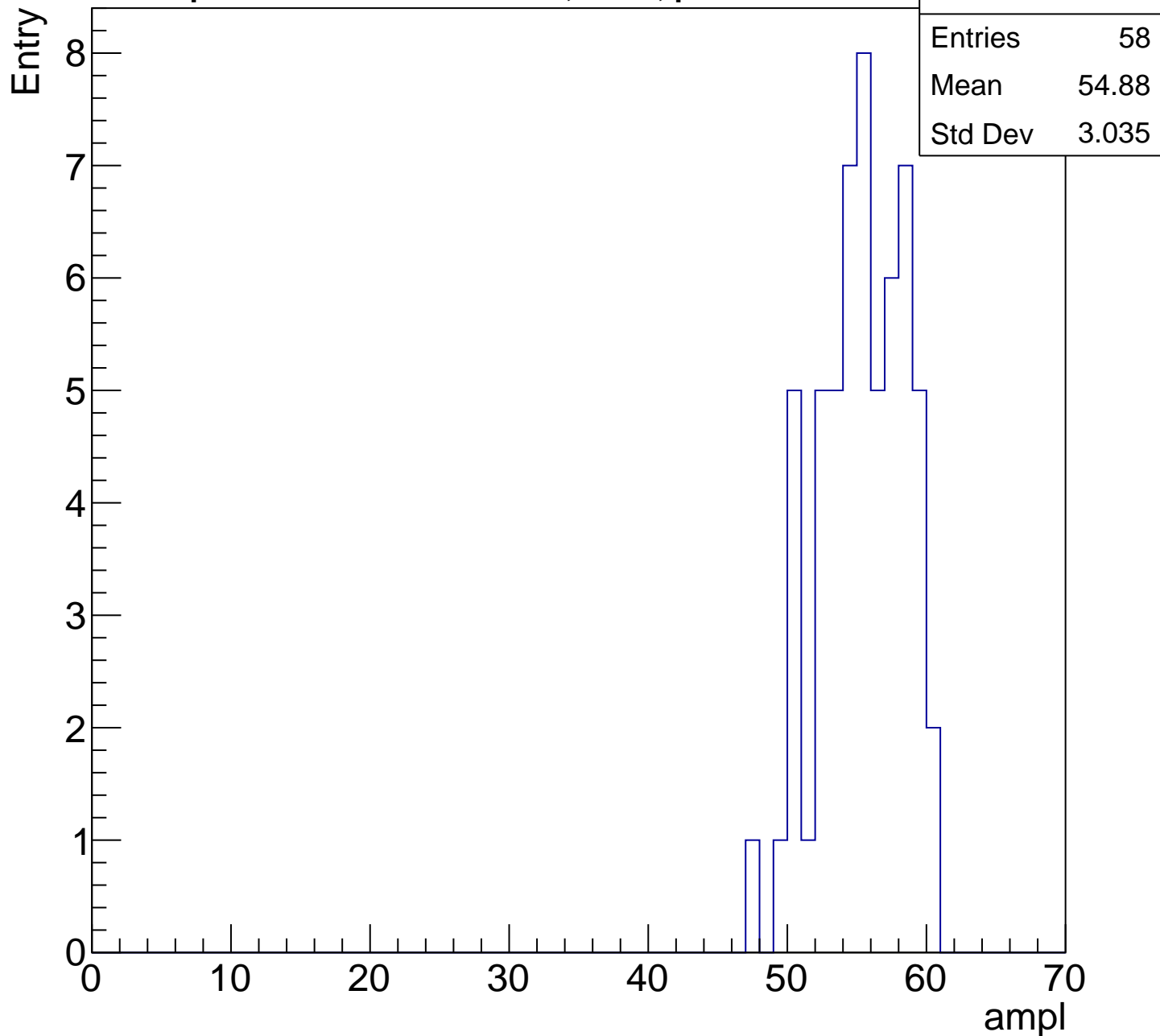
0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch33, adc4

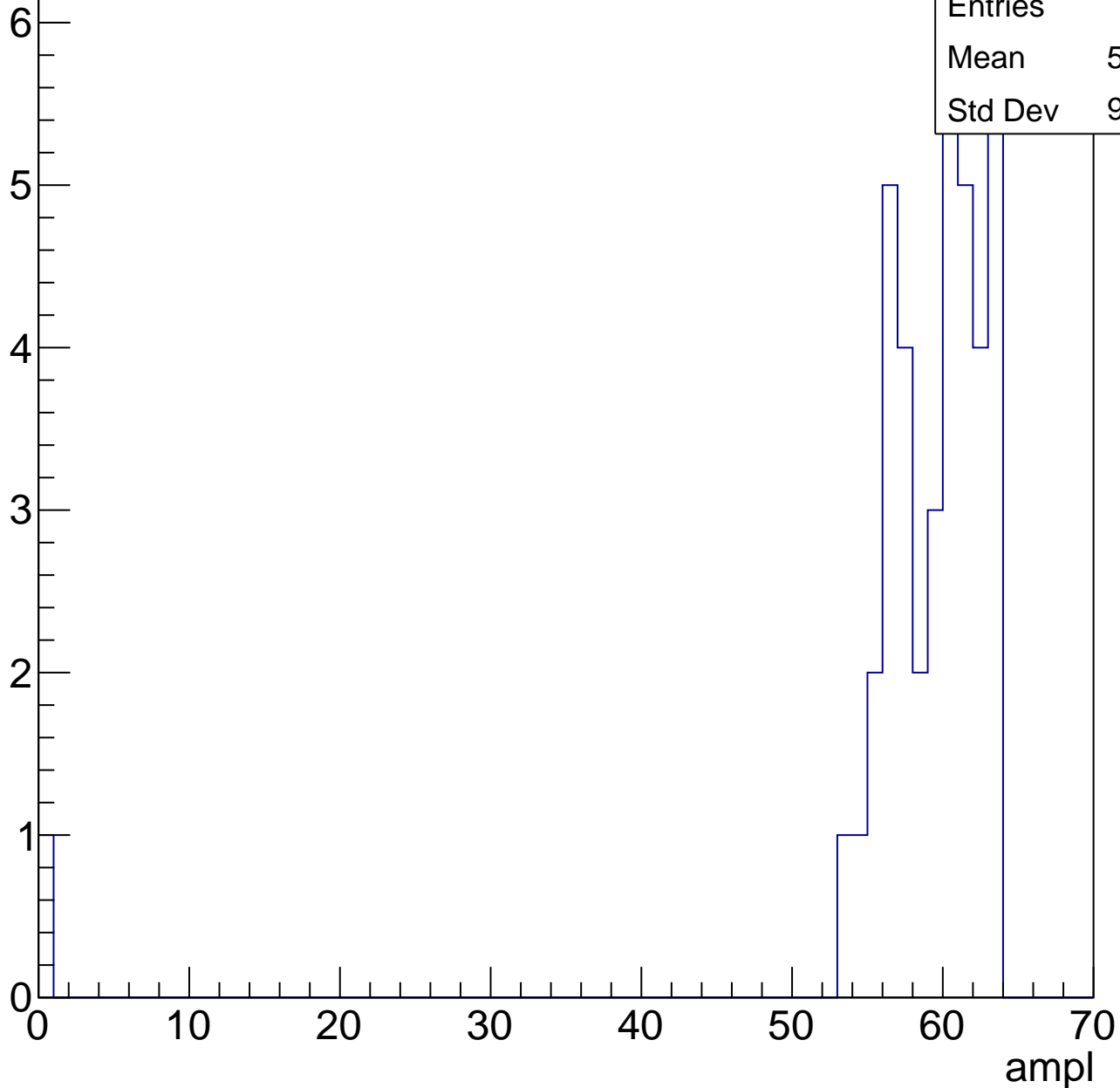
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

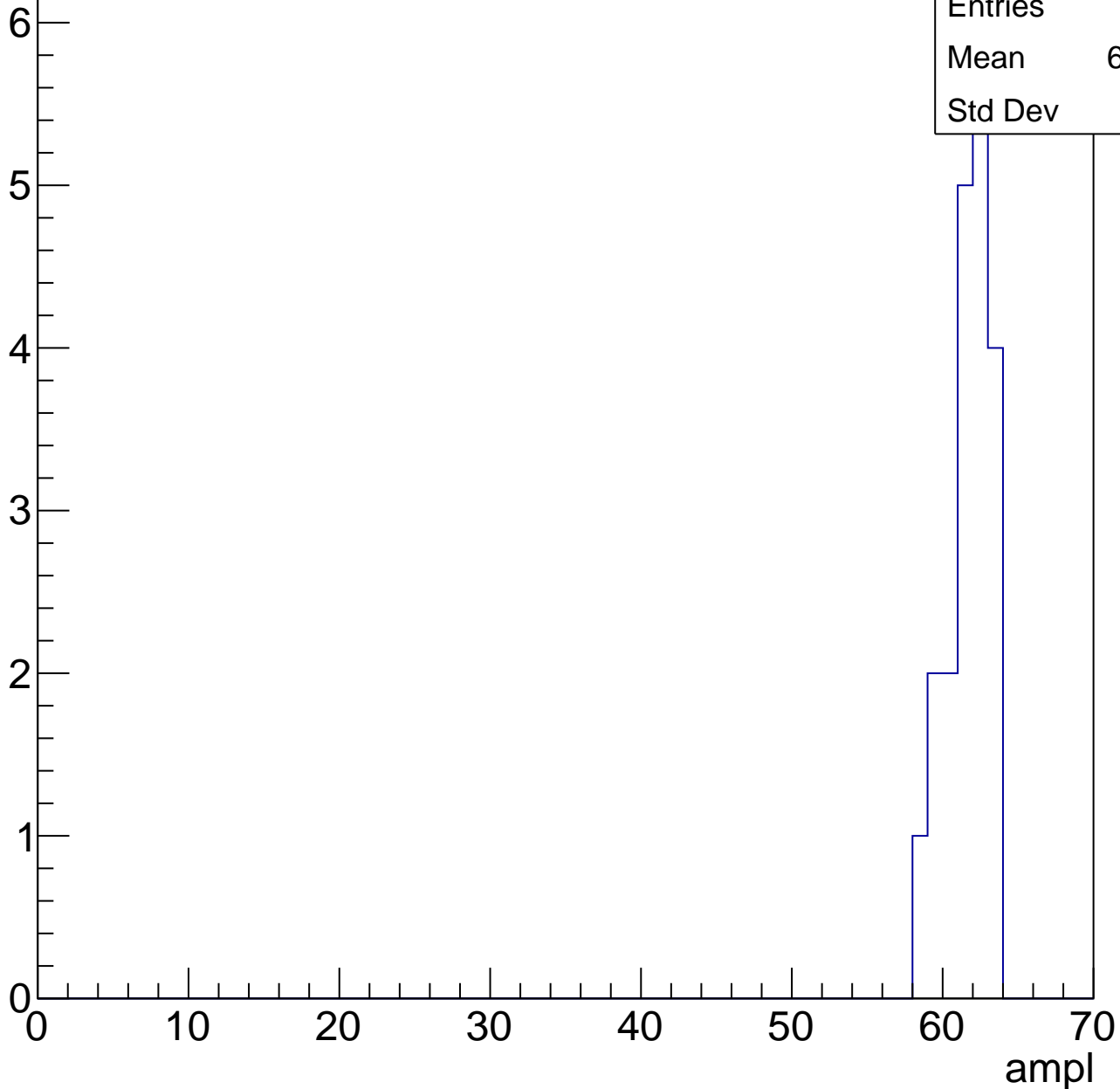


# B1L000S, U21-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	61.25
Std Dev	1.41





# B1L000S, U21-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch34, adc0

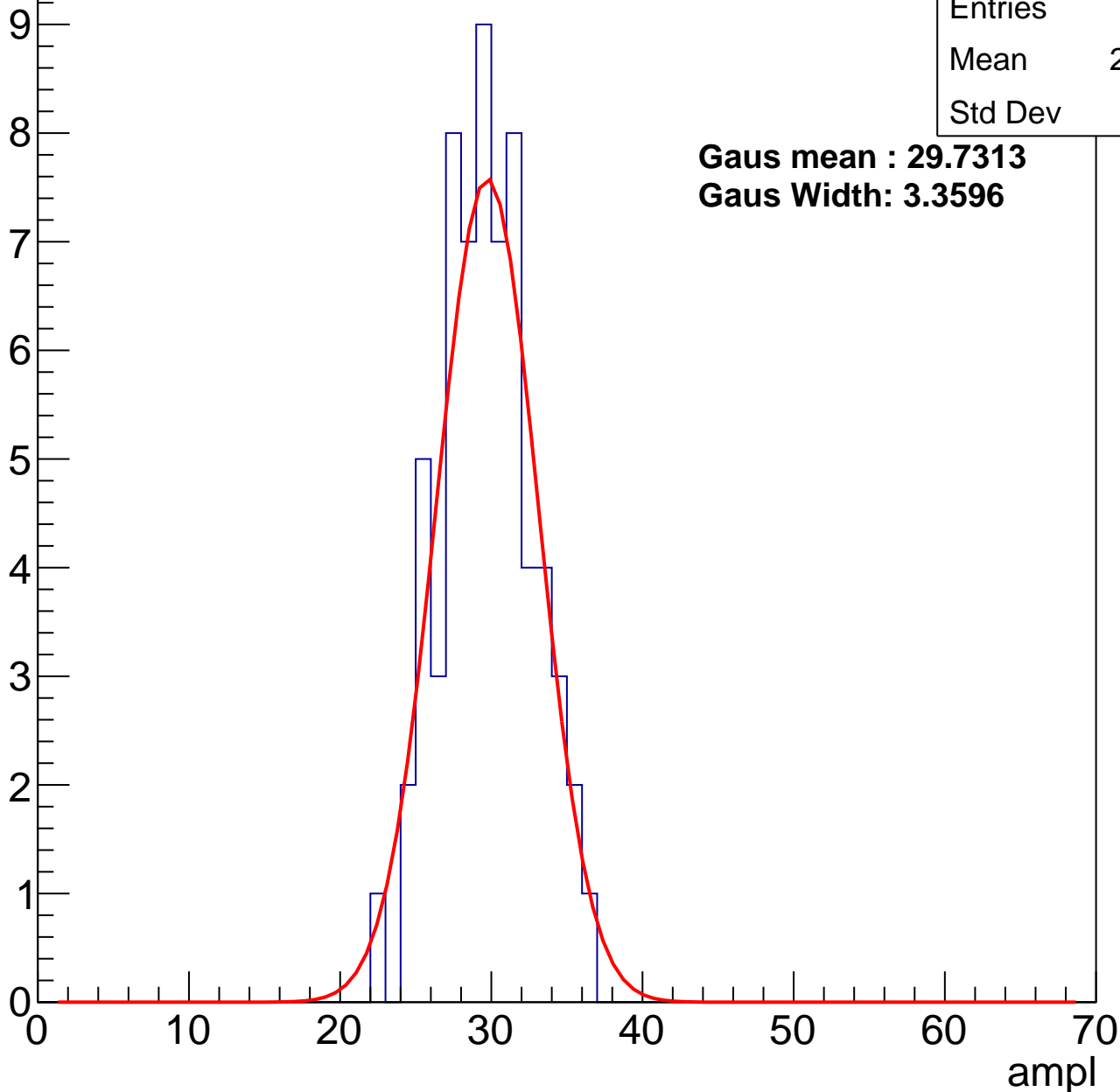
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.25
Std Dev	3

**Gaus mean : 29.7313**

**Gaus Width: 3.3596**



# B1L000S, U21-ch34, adc1

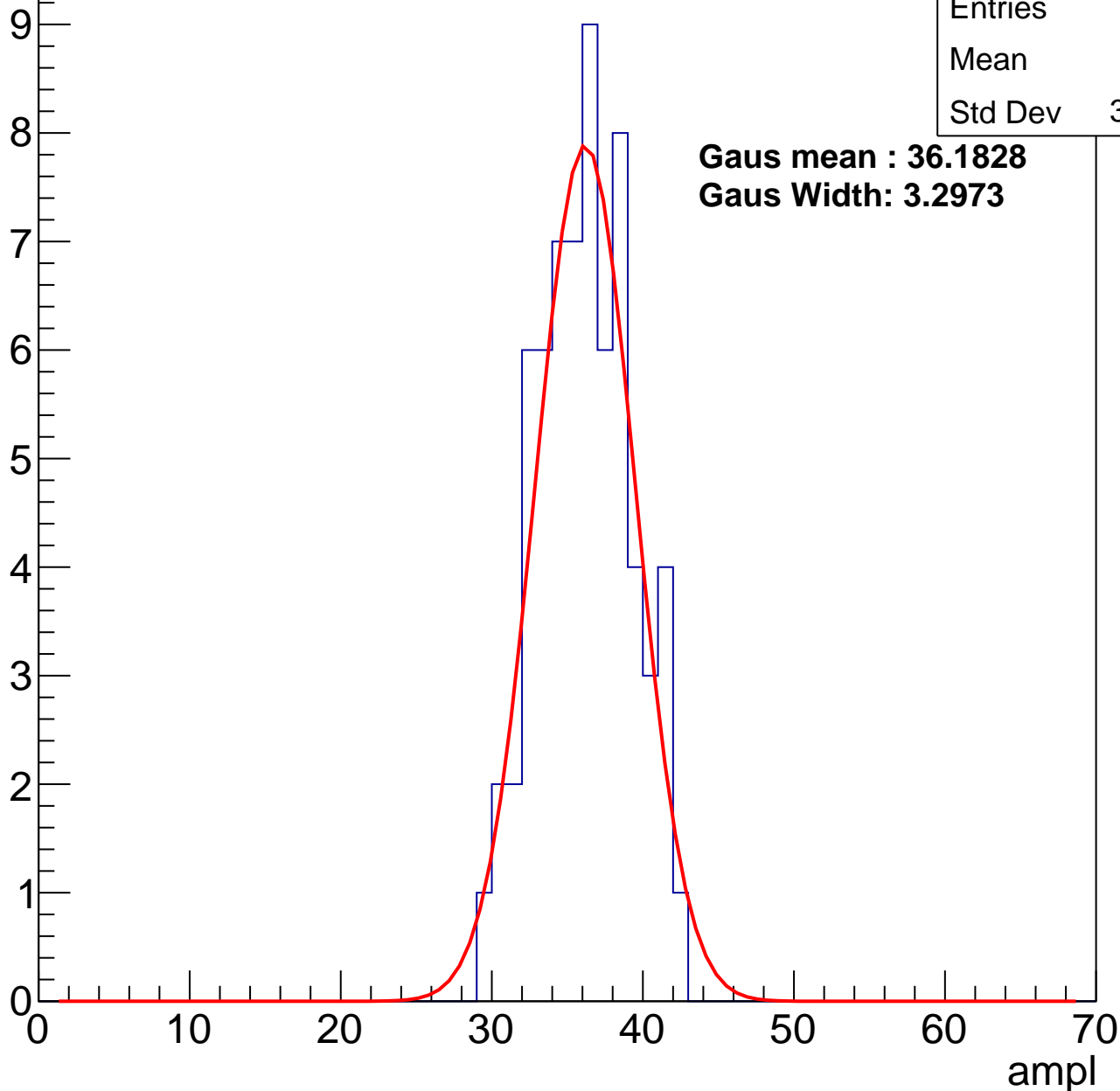
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	35.7
Std Dev	3.035

**Gaus mean : 36.1828**

**Gaus Width: 3.2973**



# B1L000S, U21-ch34, adc2

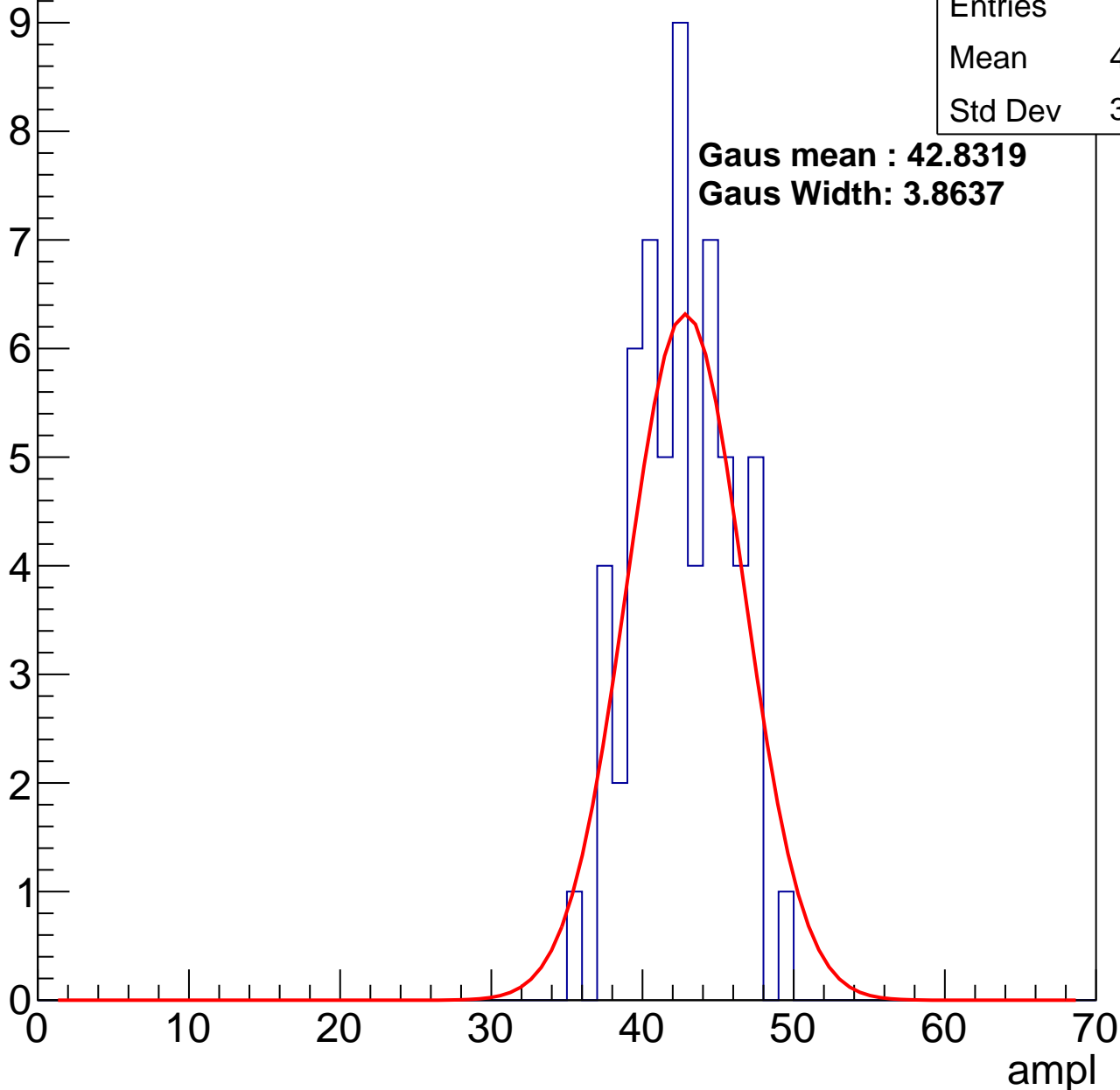
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.15
Std Dev	3.114

**Gaus mean : 42.8319**

**Gaus Width: 3.8637**

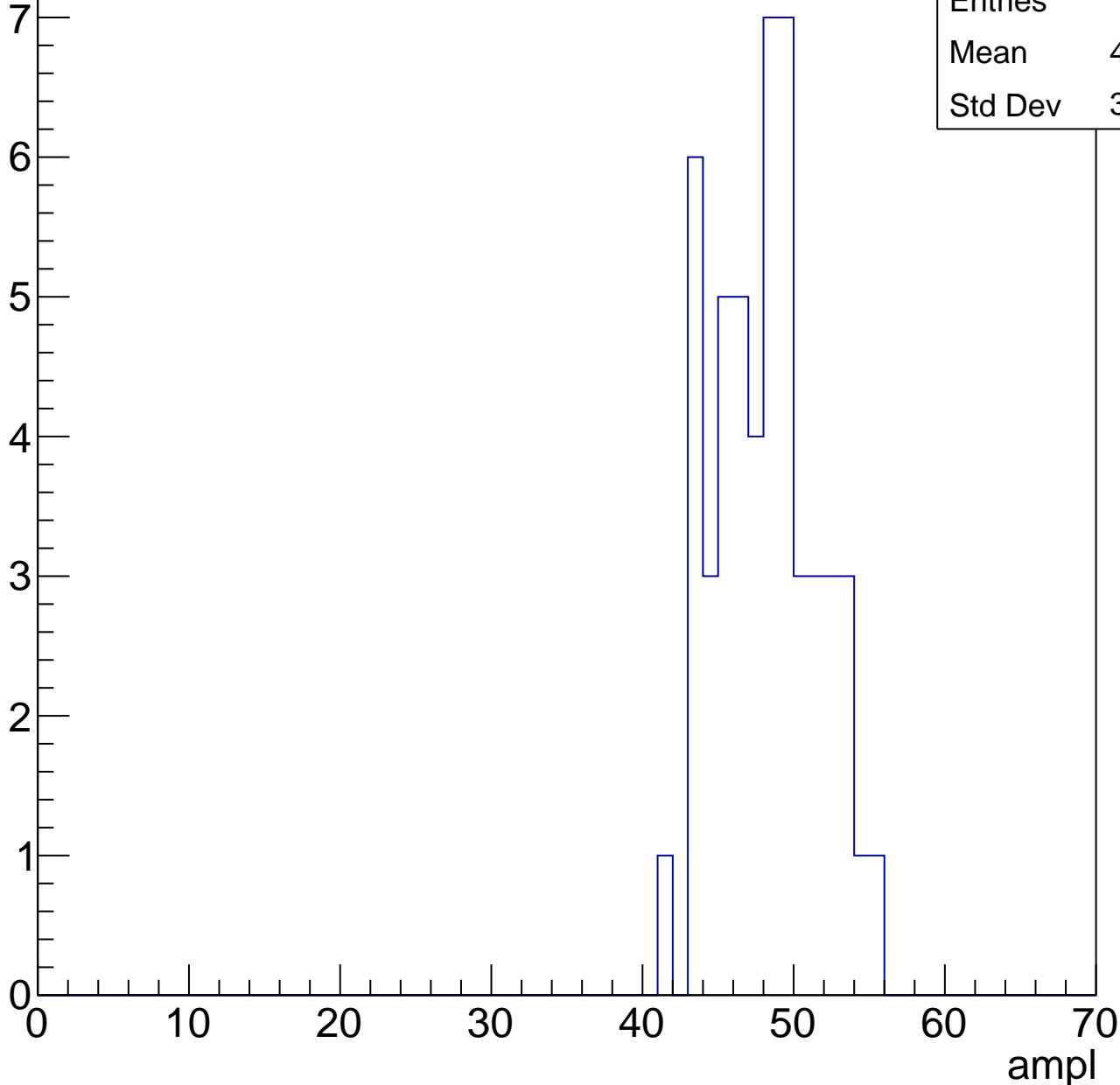


# B1L000S, U21-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	47.69
Std Dev	3.296

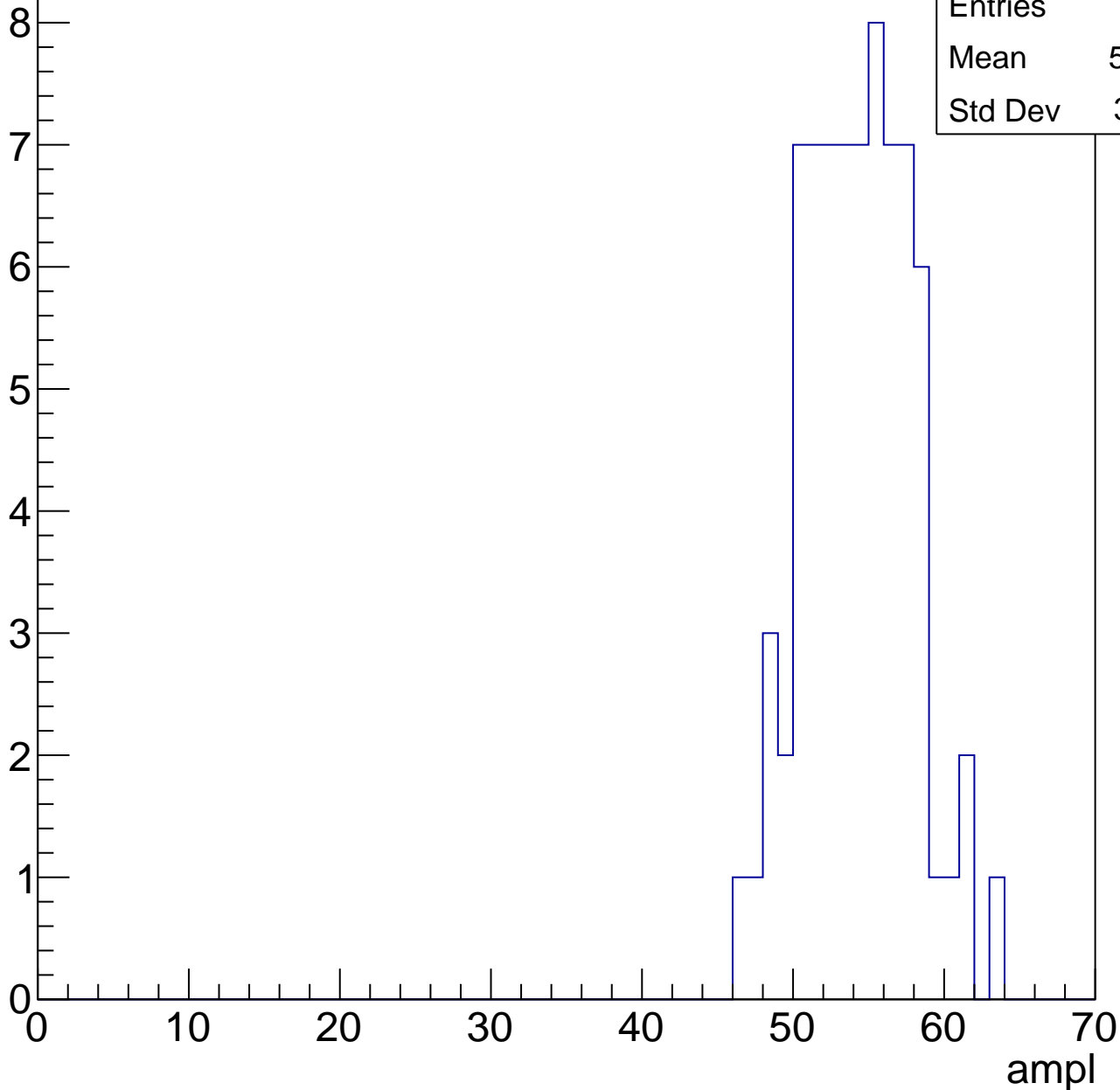


# B1L000S, U21-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

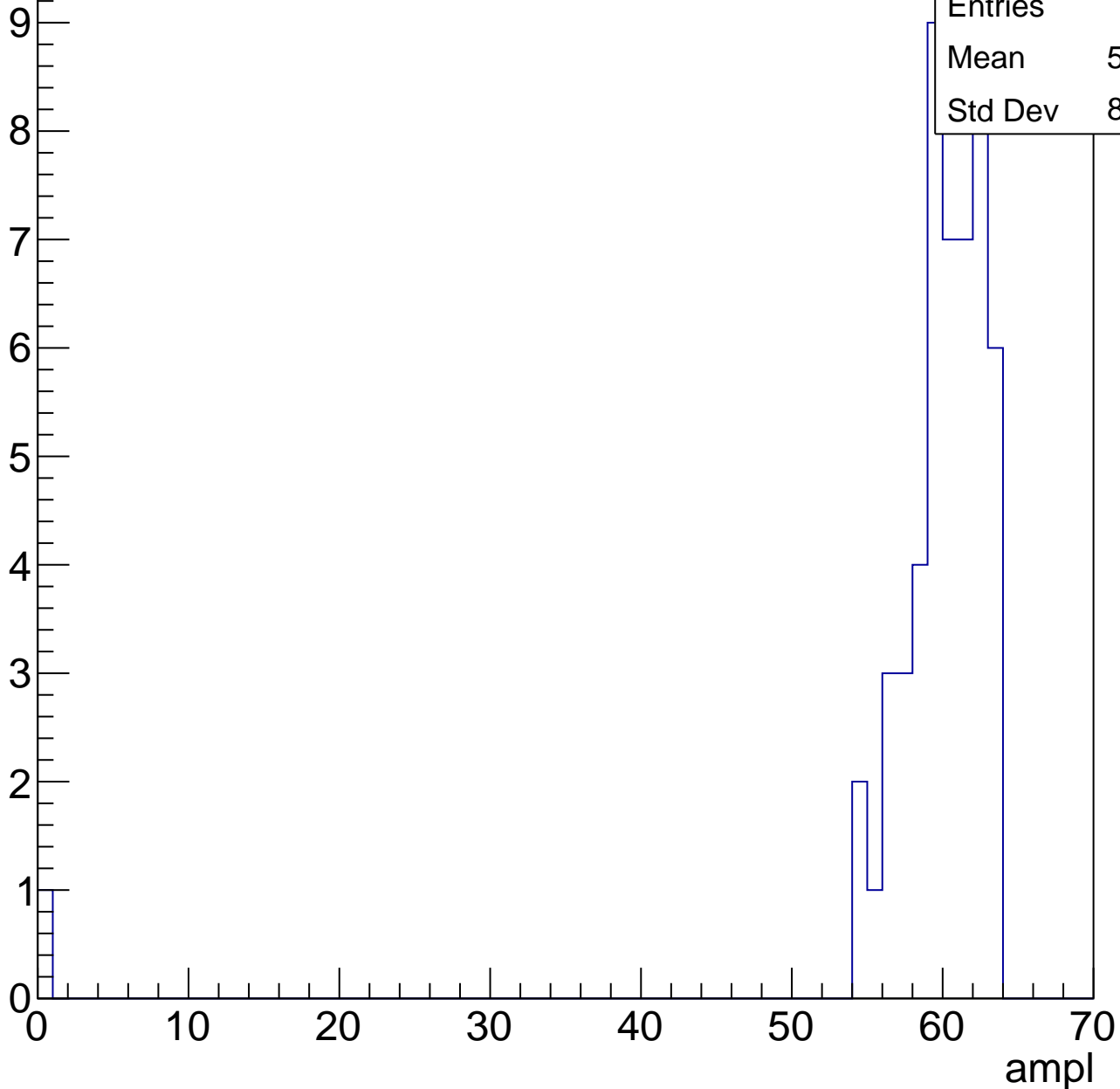
Entries	75
Mean	53.84
Std Dev	3.491



# B1L000S, U21-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

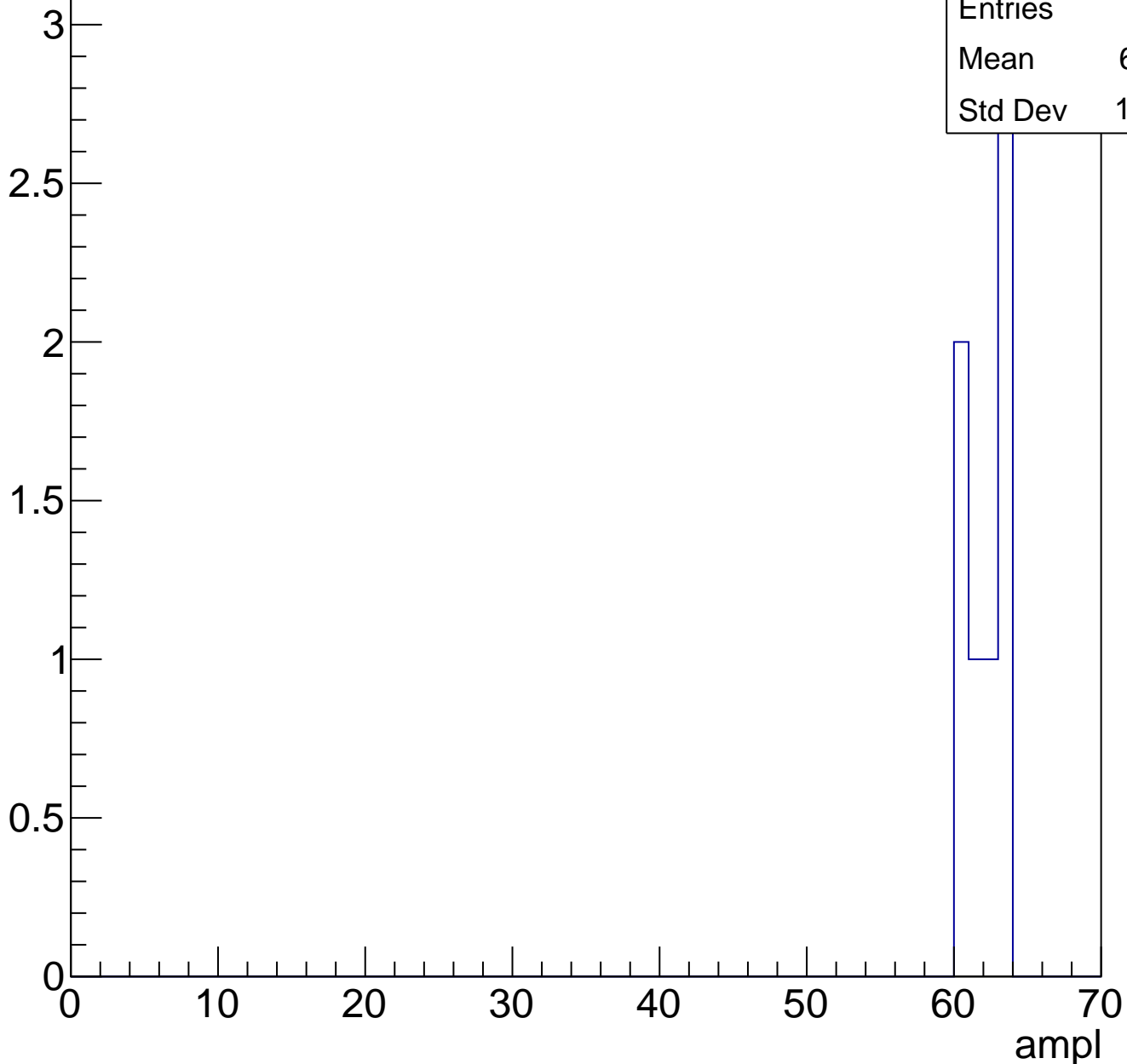
Entry



# B1L000S, U21-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch35, adc0

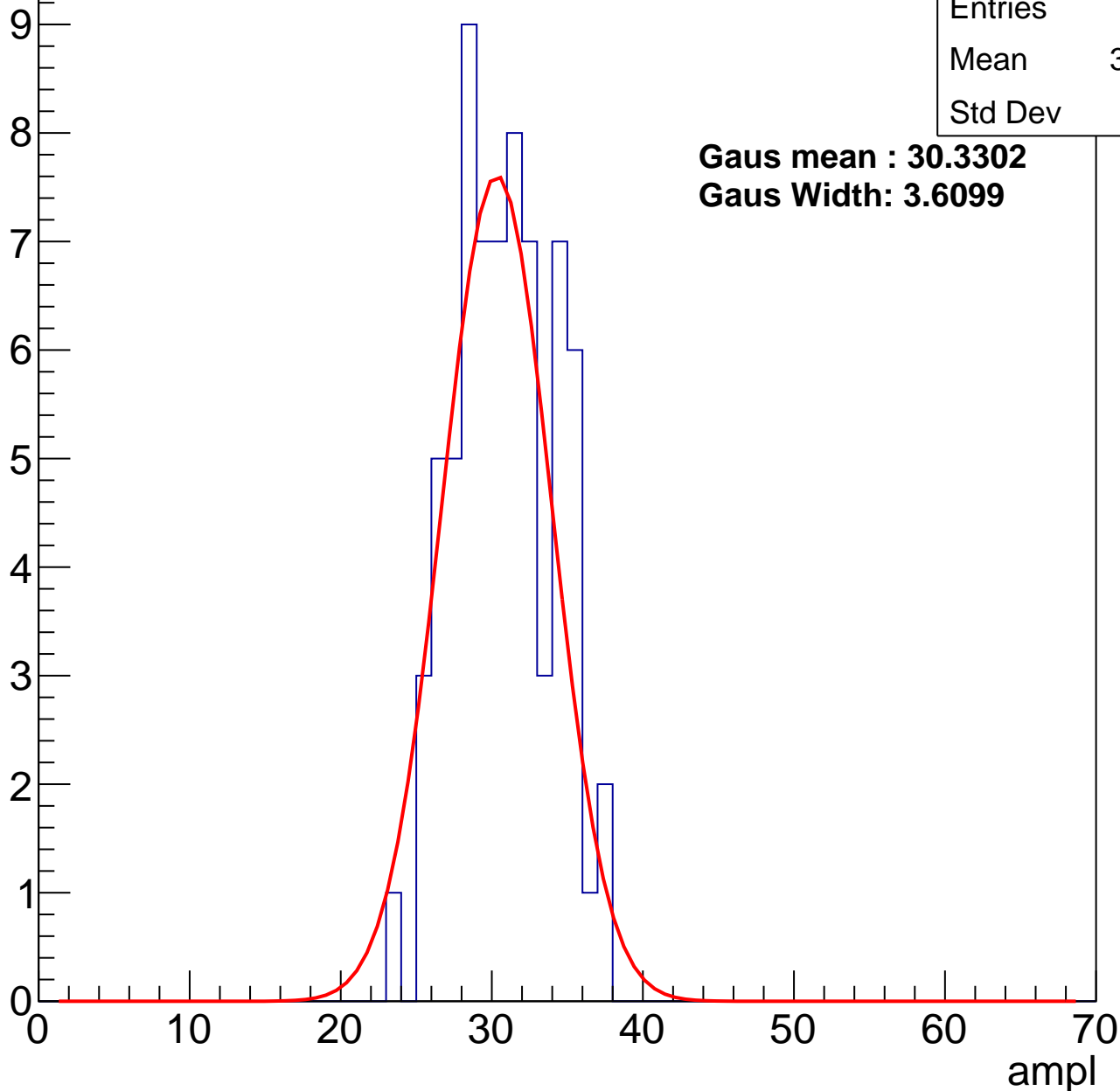
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	30.38
Std Dev	3.23

**Gaus mean : 30.3302**

**Gaus Width: 3.6099**



# B1L000S, U21-ch35, adc1

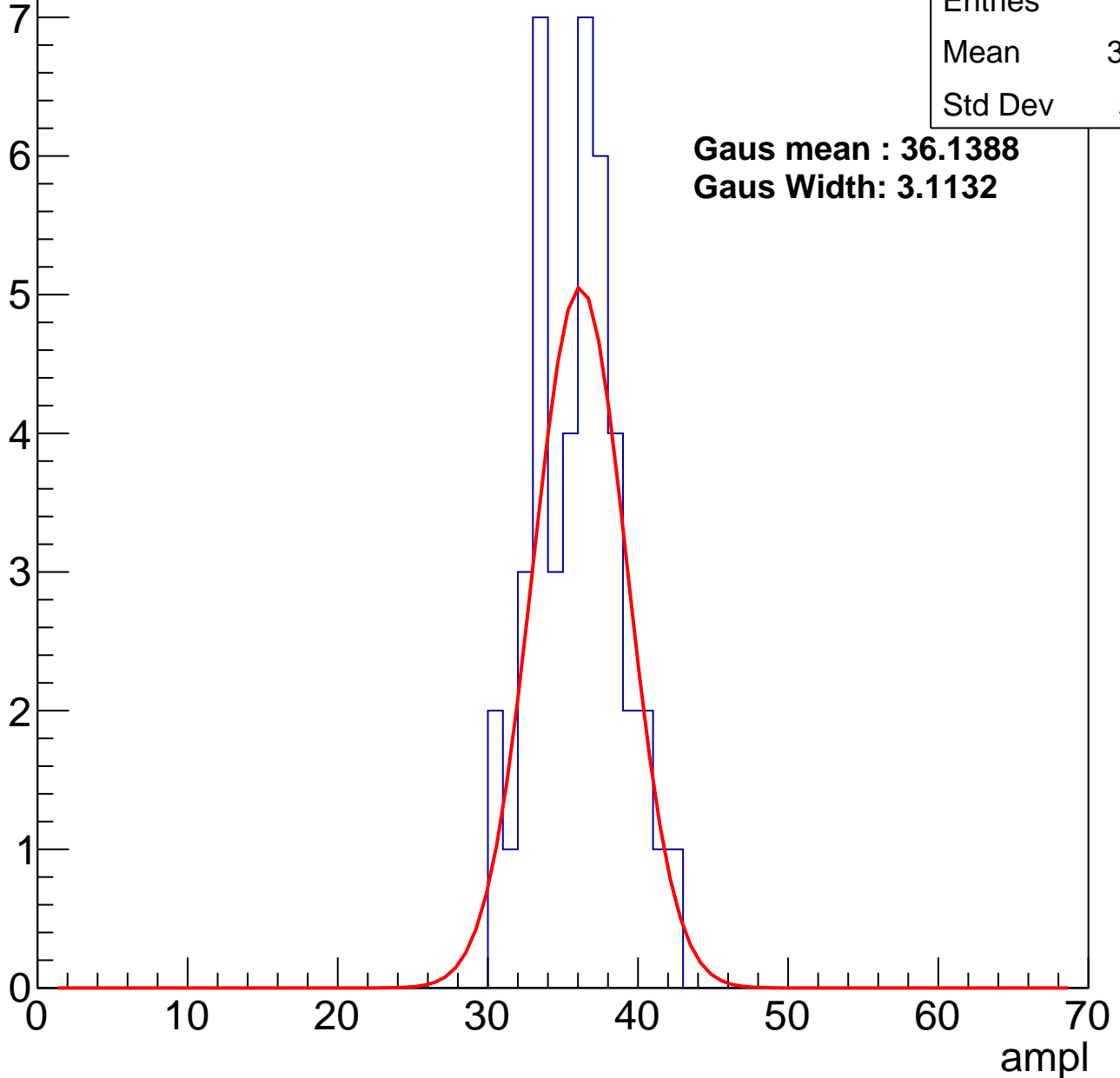
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	35.51
Std Dev	2.84

**Gaus mean : 36.1388**

**Gaus Width: 3.1132**



# B1L000S, U21-ch35, adc2

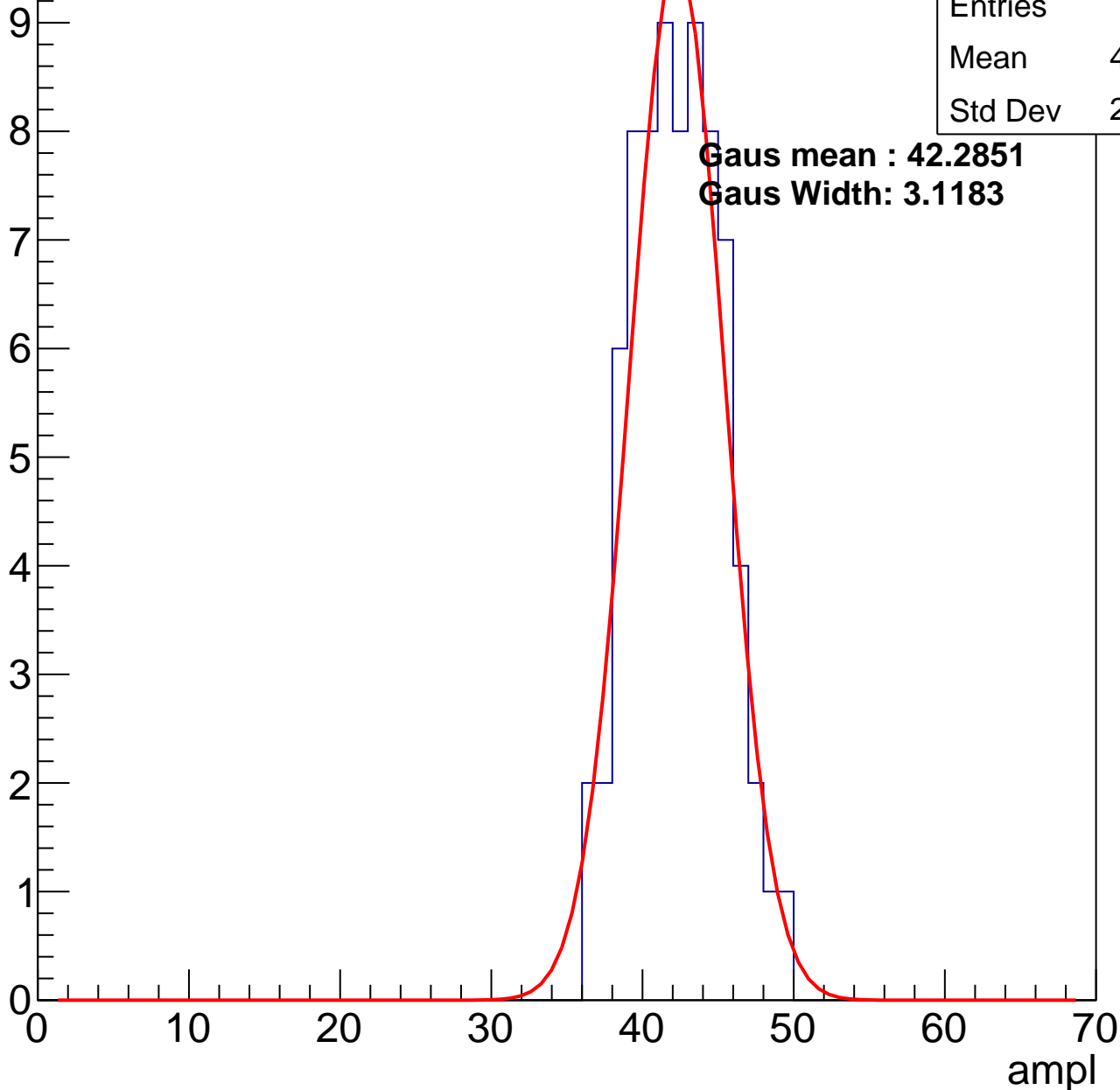
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	41.87
Std Dev	2.904

**Gaus mean : 42.2851**

**Gaus Width: 3.1183**



# B1L000S, U21-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	49.21
Std Dev	2.995

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

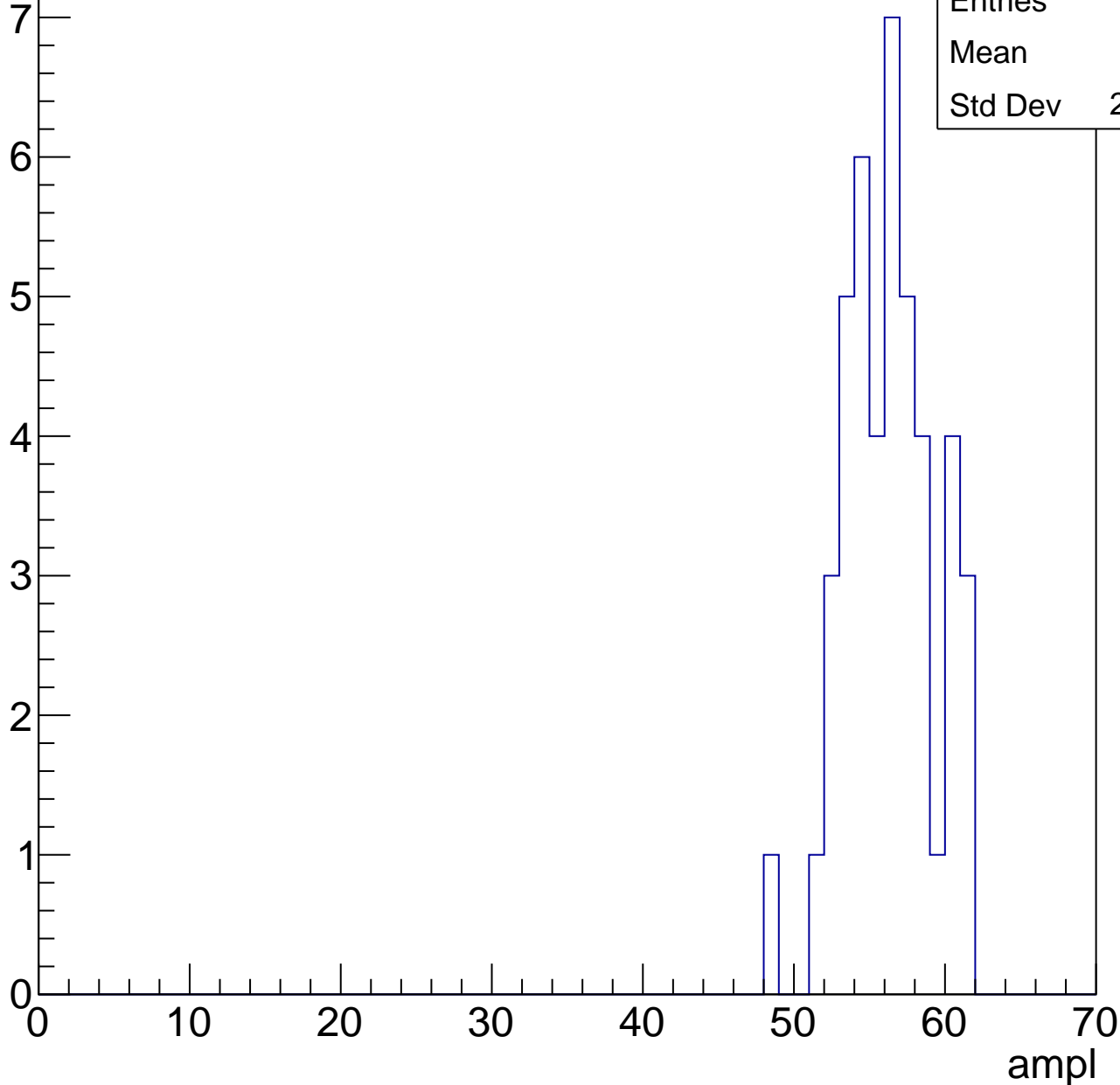
10

# B1L000S, U21-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

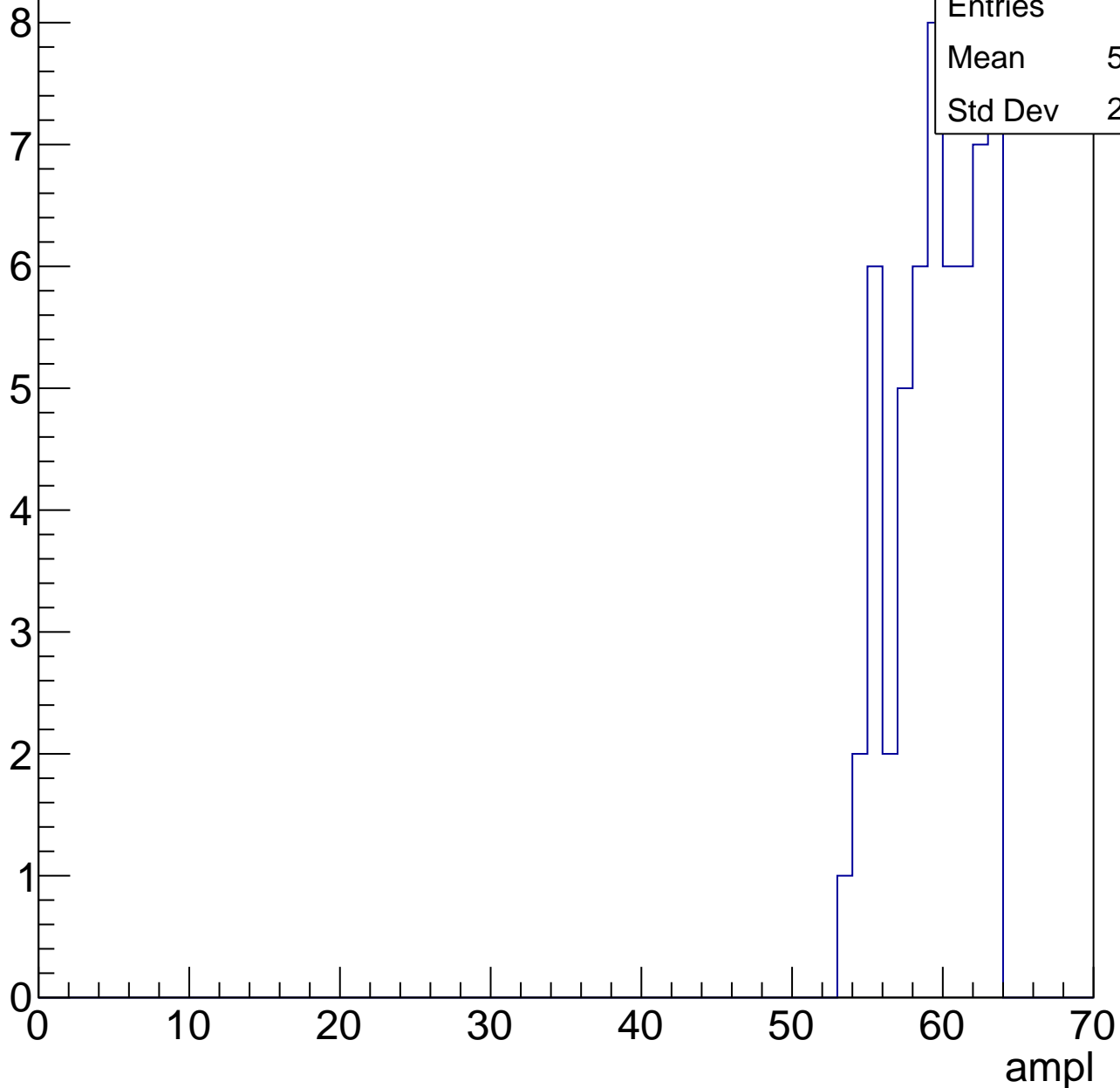
Entries	44
Mean	55.8
Std Dev	2.928



# B1L000S, U21-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

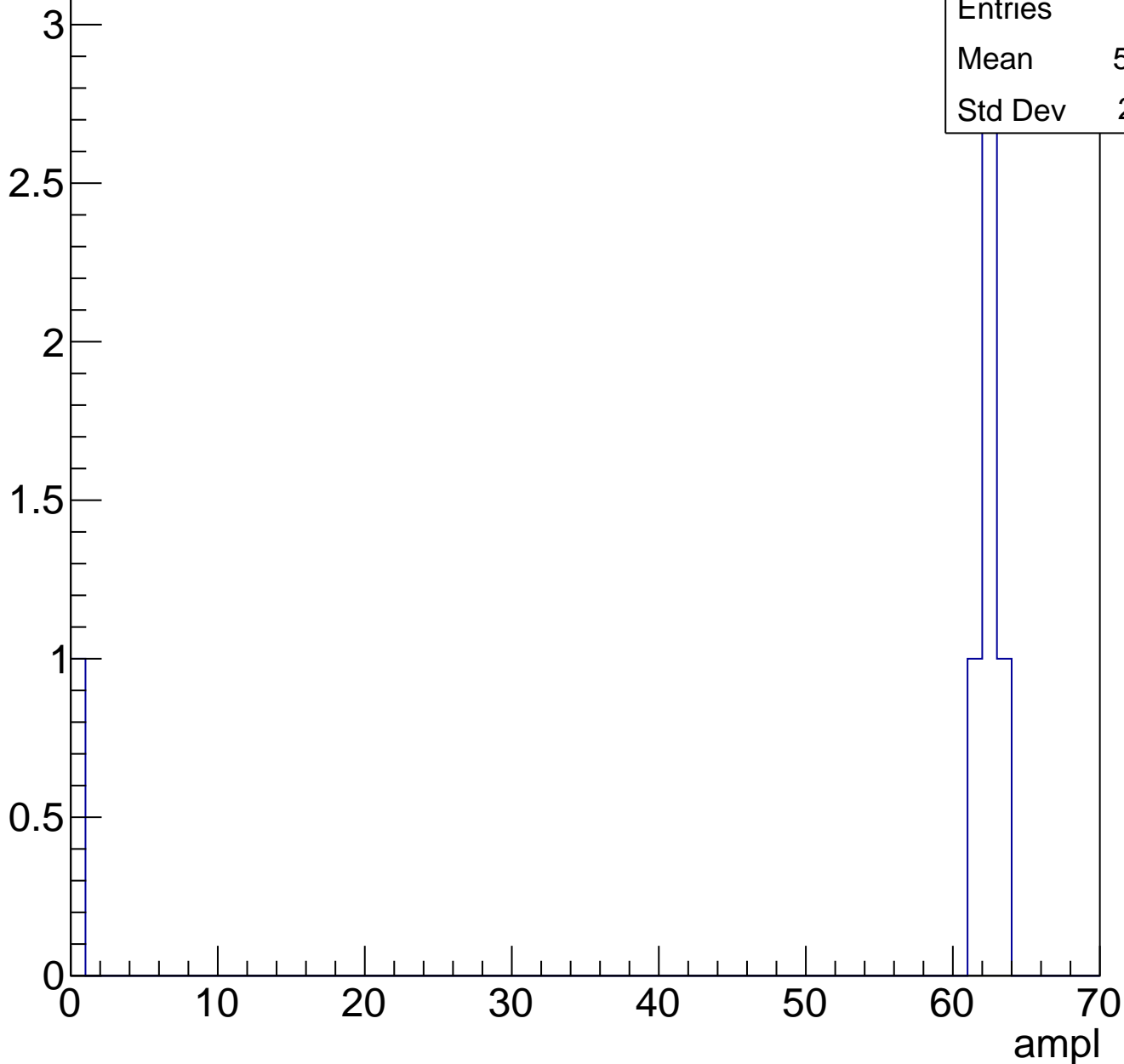


Entries	57
Mean	59.16
Std Dev	2.796

# B1L000S, U21-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch35, adc7

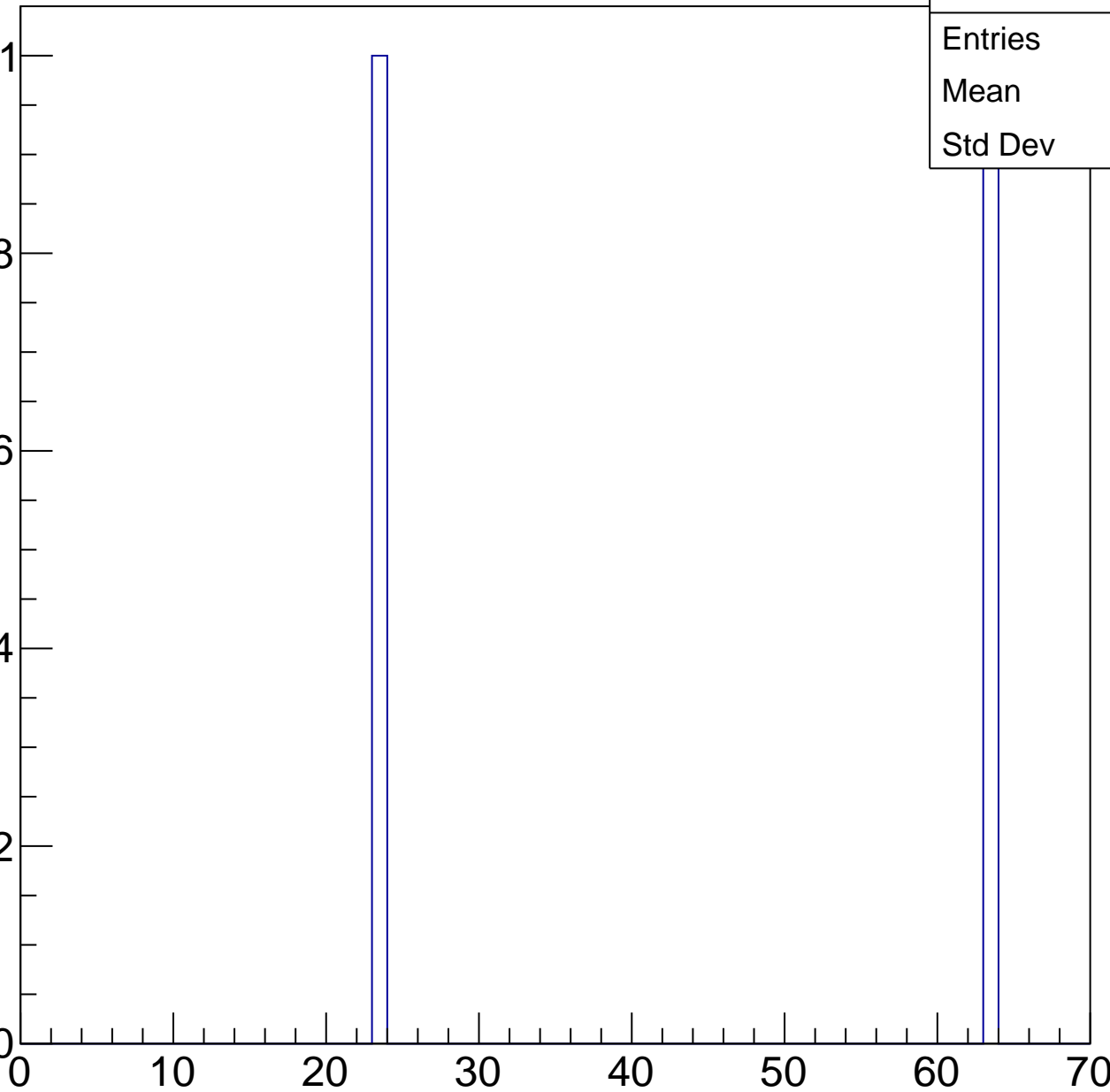
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	43
Std Dev	20

ampl



# B1L000S, U21-ch36, adc0

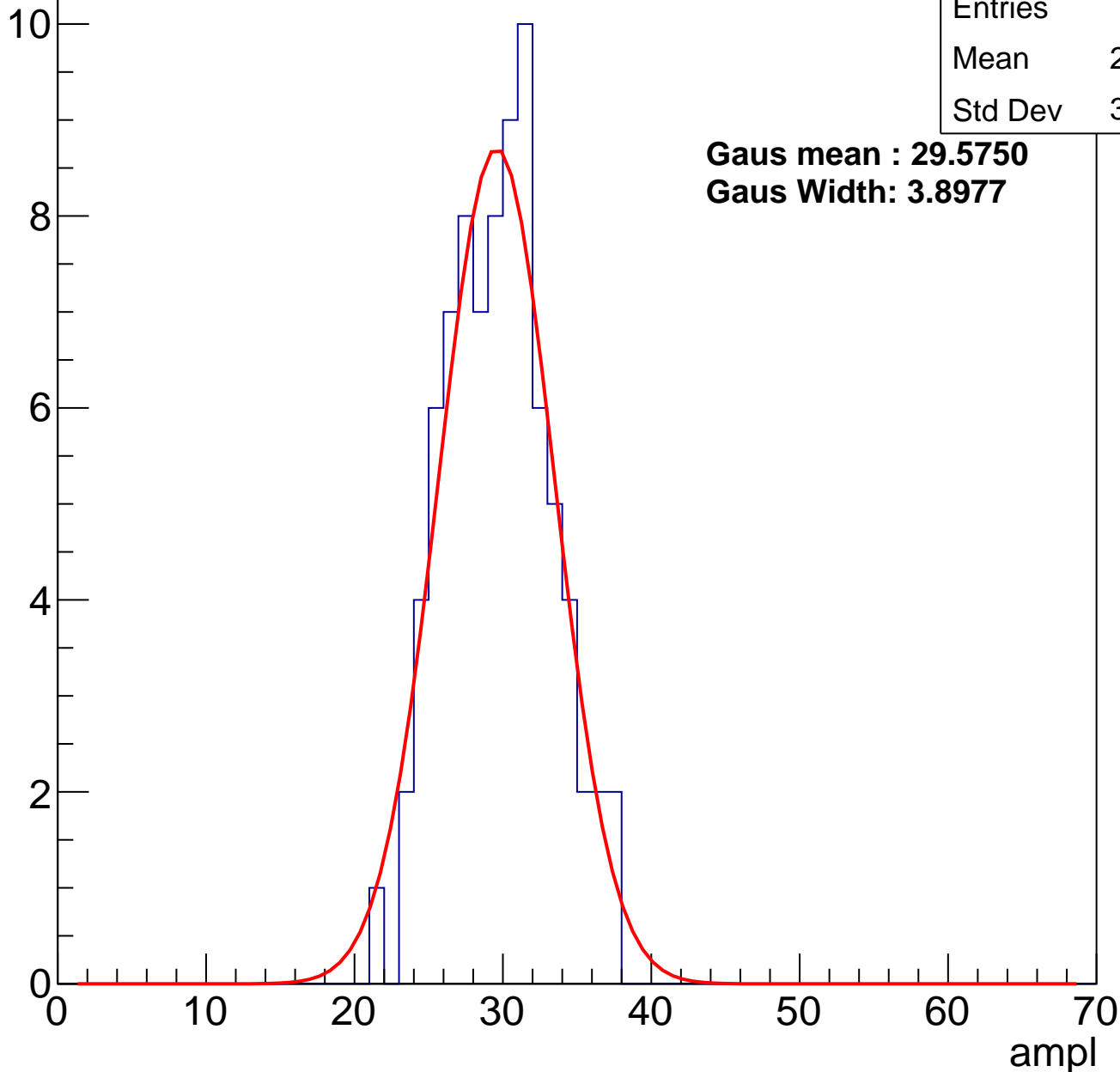
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	29.25
Std Dev	3.488

**Gaus mean : 29.5750**

**Gaus Width: 3.8977**

Entry



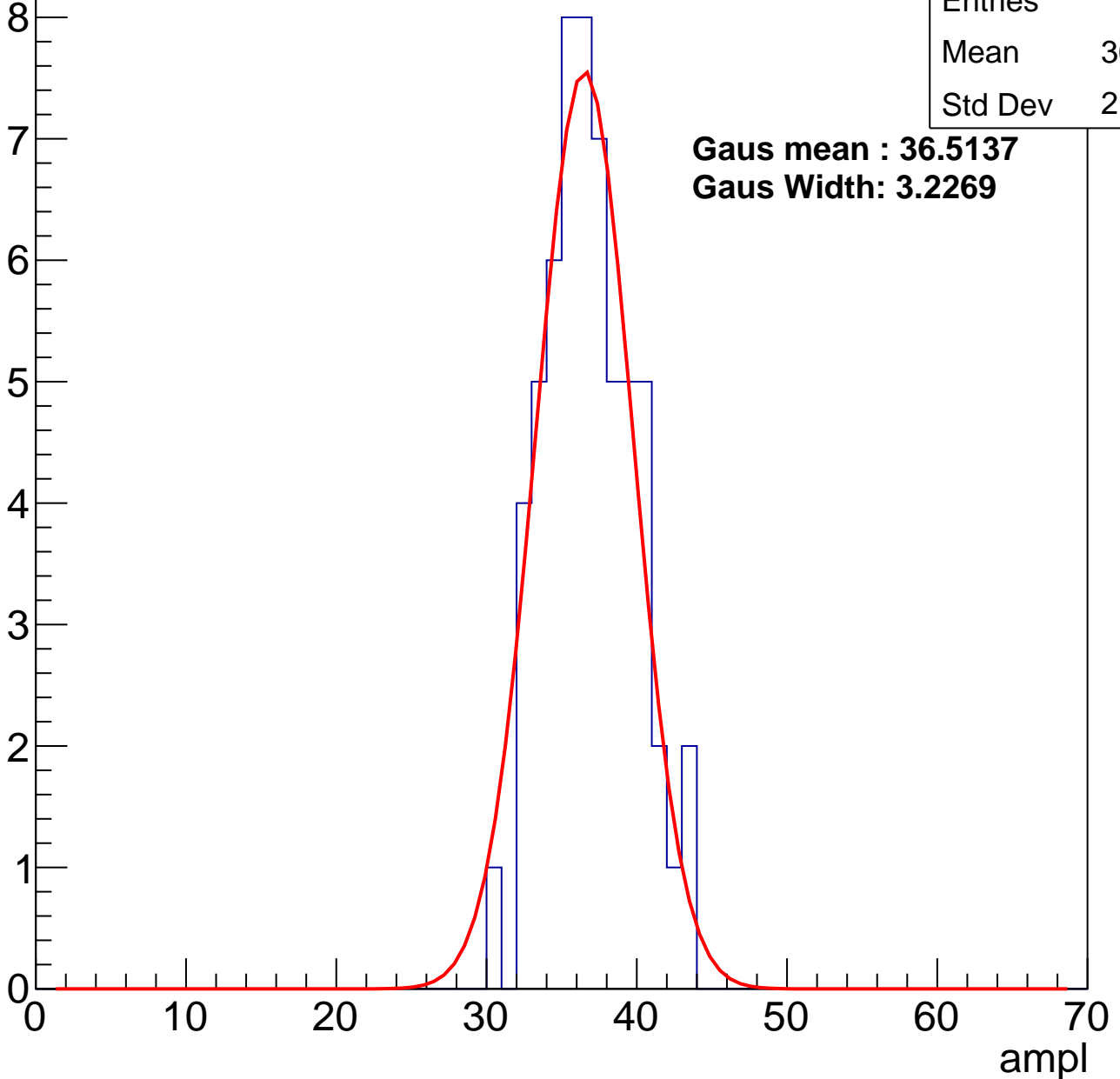
# B1L000S, U21-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	36.42
Std Dev	2.918

**Gaus mean : 36.5137**  
**Gaus Width: 3.2269**



# B1L000S, U21-ch36, adc2

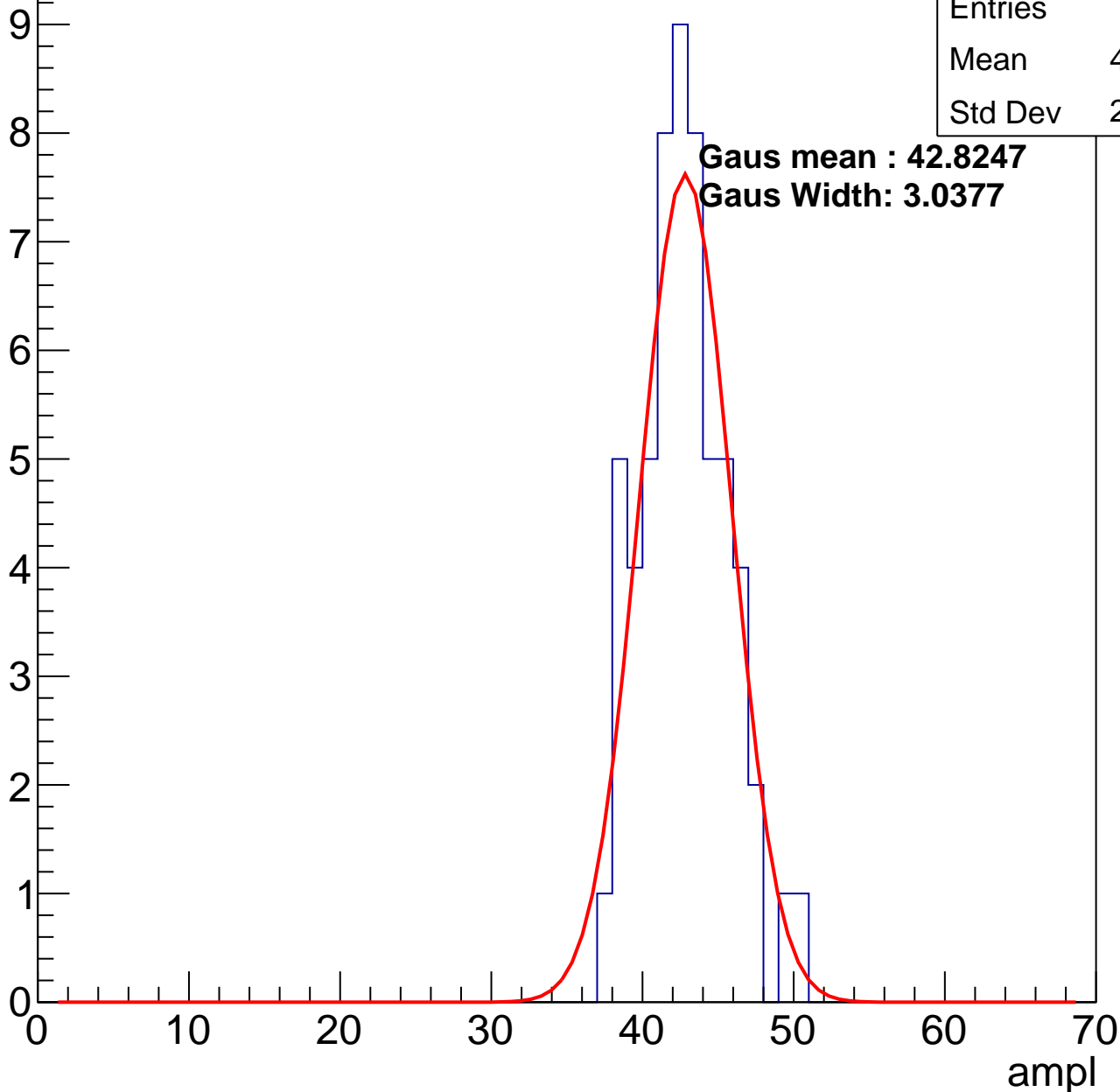
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	42.33
Std Dev	2.825

**Gaus mean : 42.8247**

**Gaus Width: 3.0377**

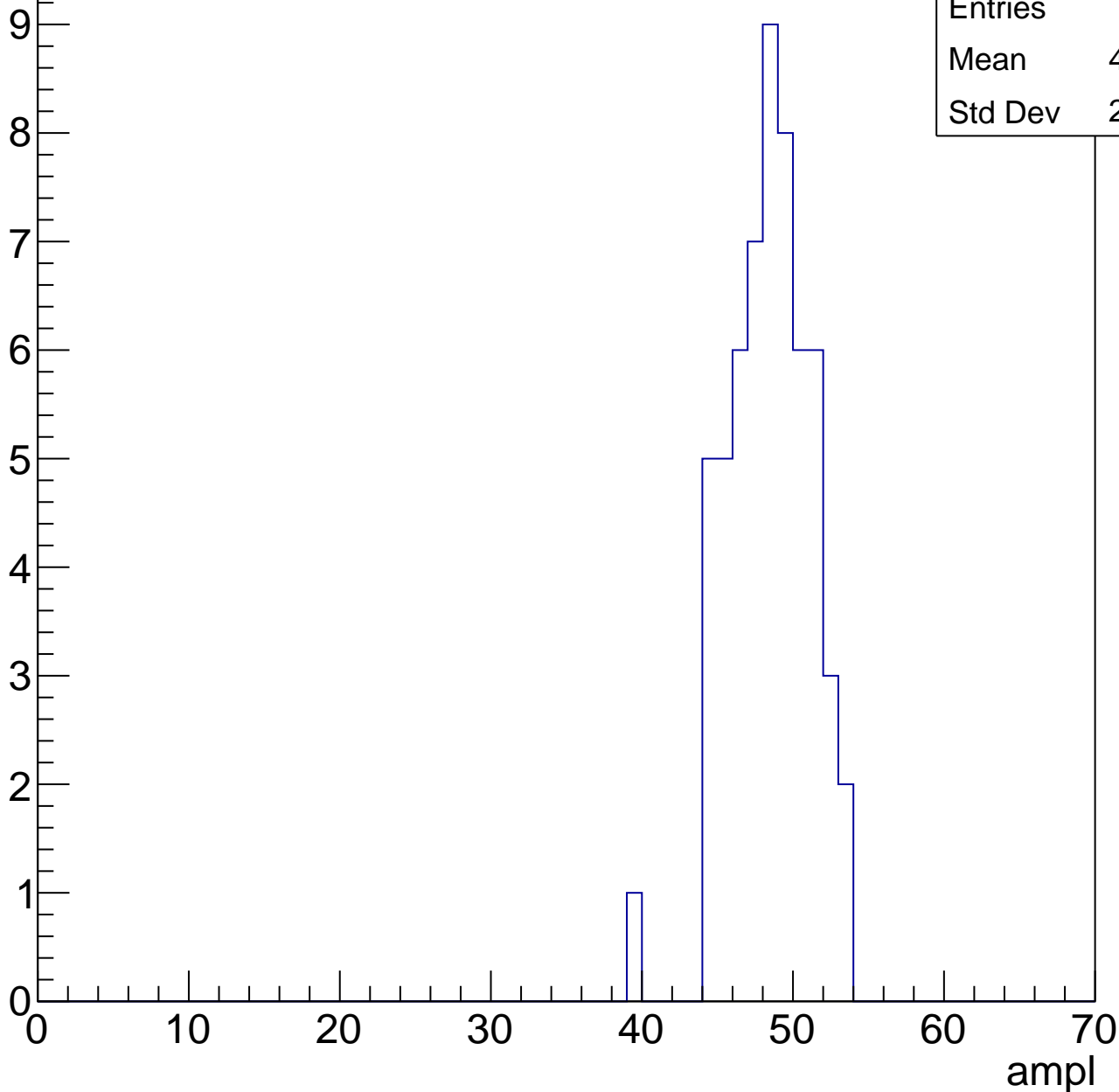


# B1L000S, U21-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

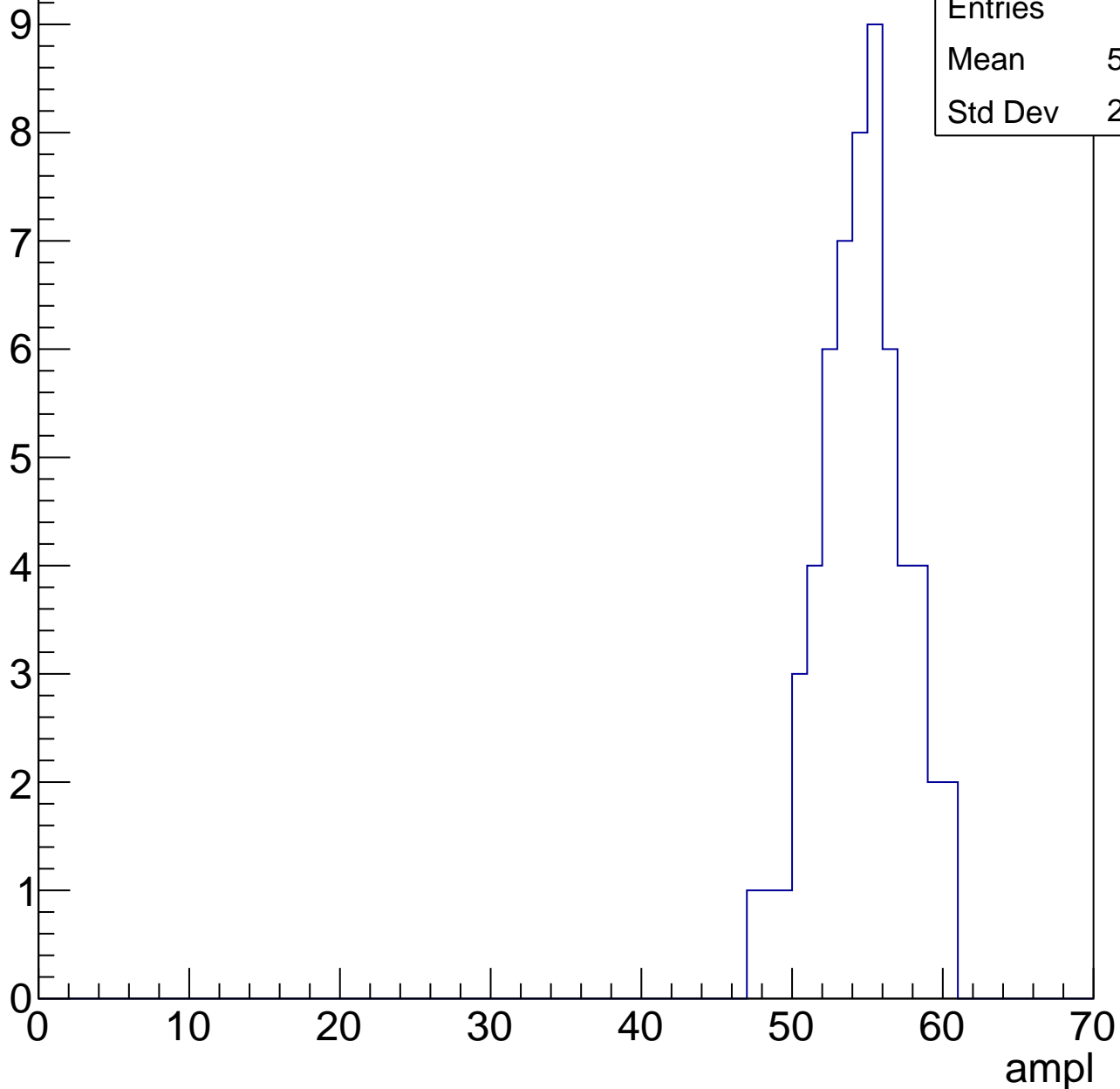
Entries	58
Mean	47.95
Std Dev	2.694



# B1L000S, U21-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



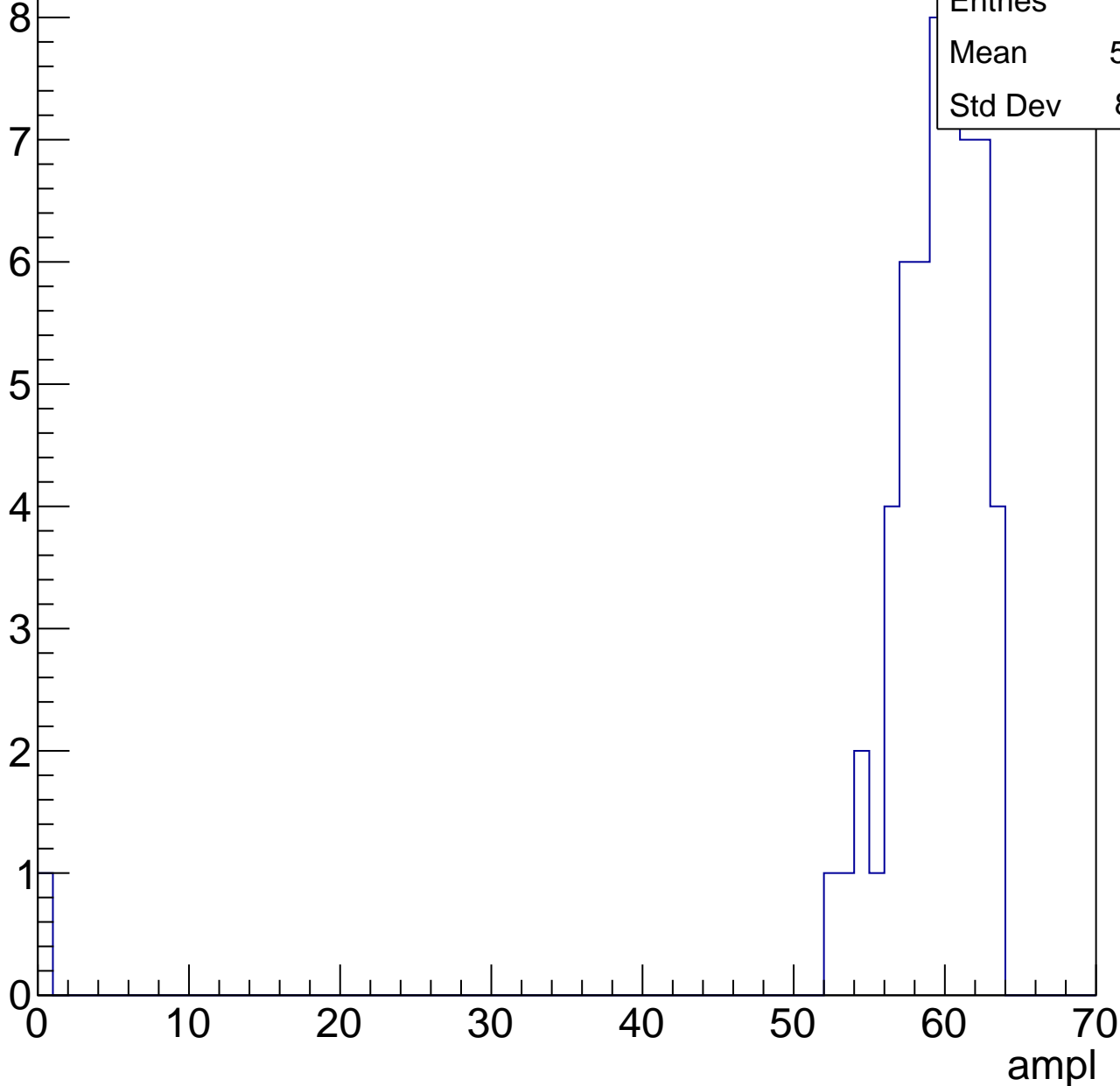
Entries	58
Mean	54.17
Std Dev	2.872

# B1L000S, U21-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	57.98
Std Dev	8.241

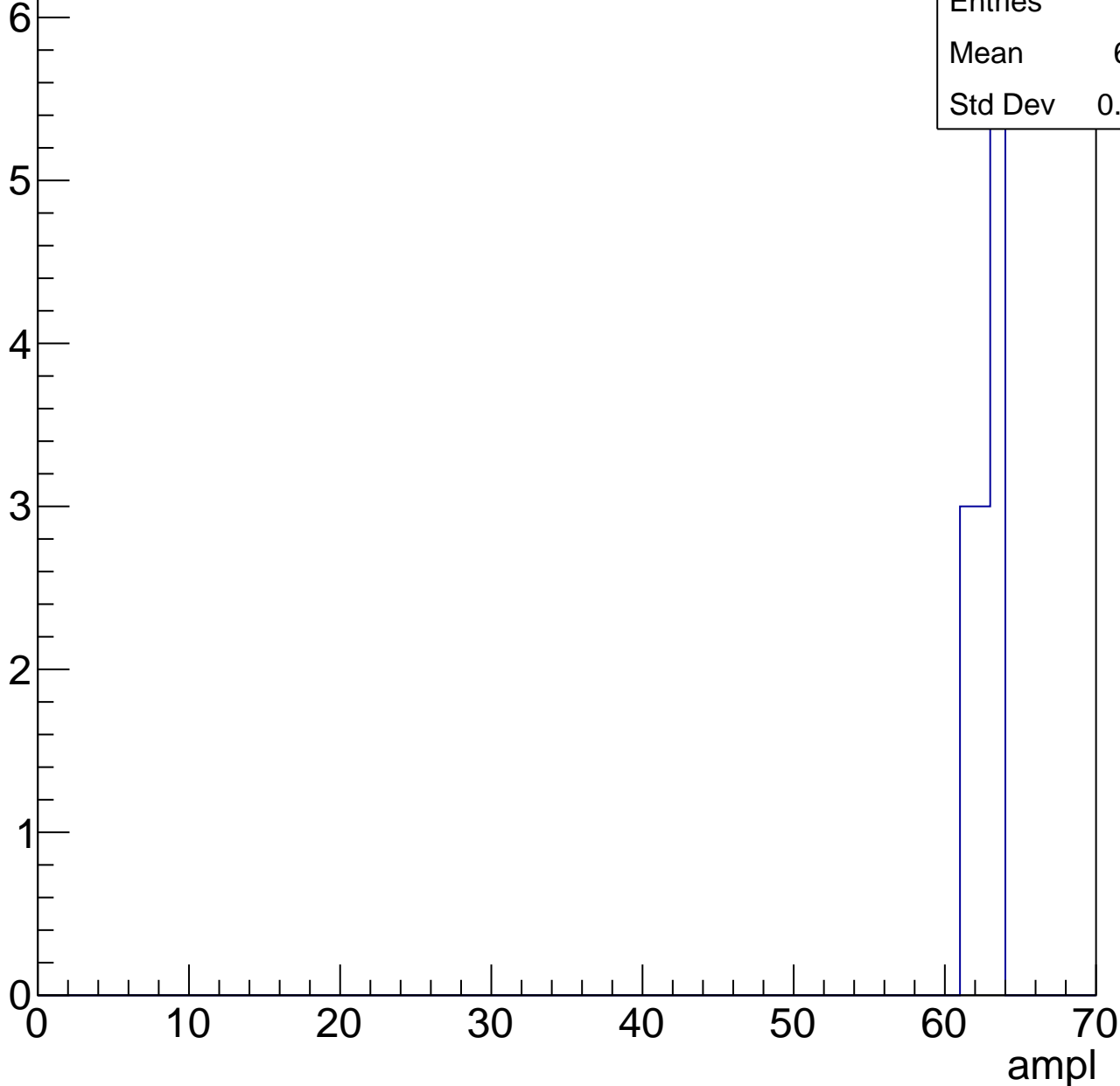


# B1L000S, U21-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	62.25
Std Dev	0.8292





# B1L000S, U21-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B1L000S, U21-ch37, adc0

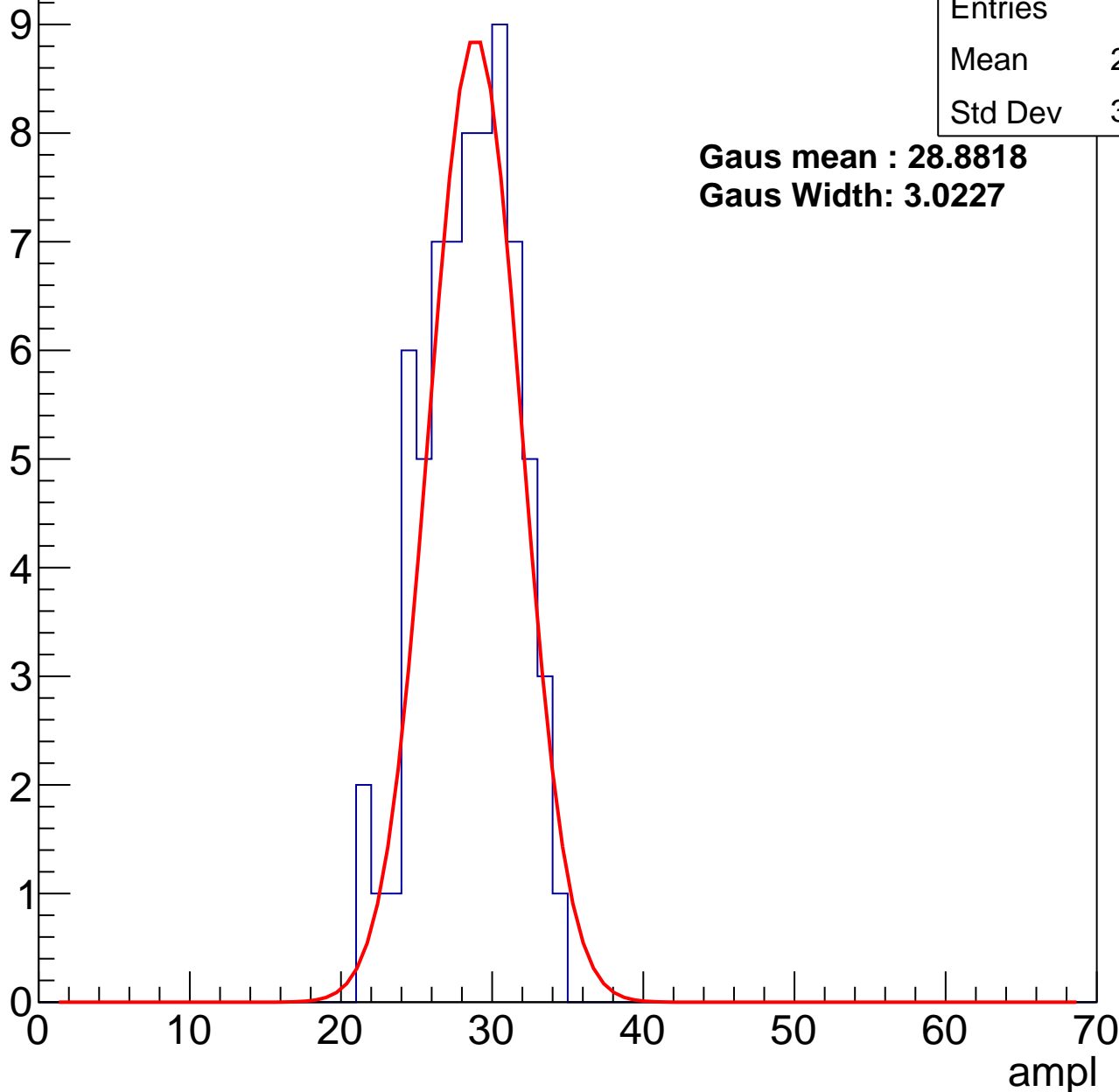
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	28.04
Std Dev	3.007

**Gaus mean : 28.8818**

**Gaus Width: 3.0227**



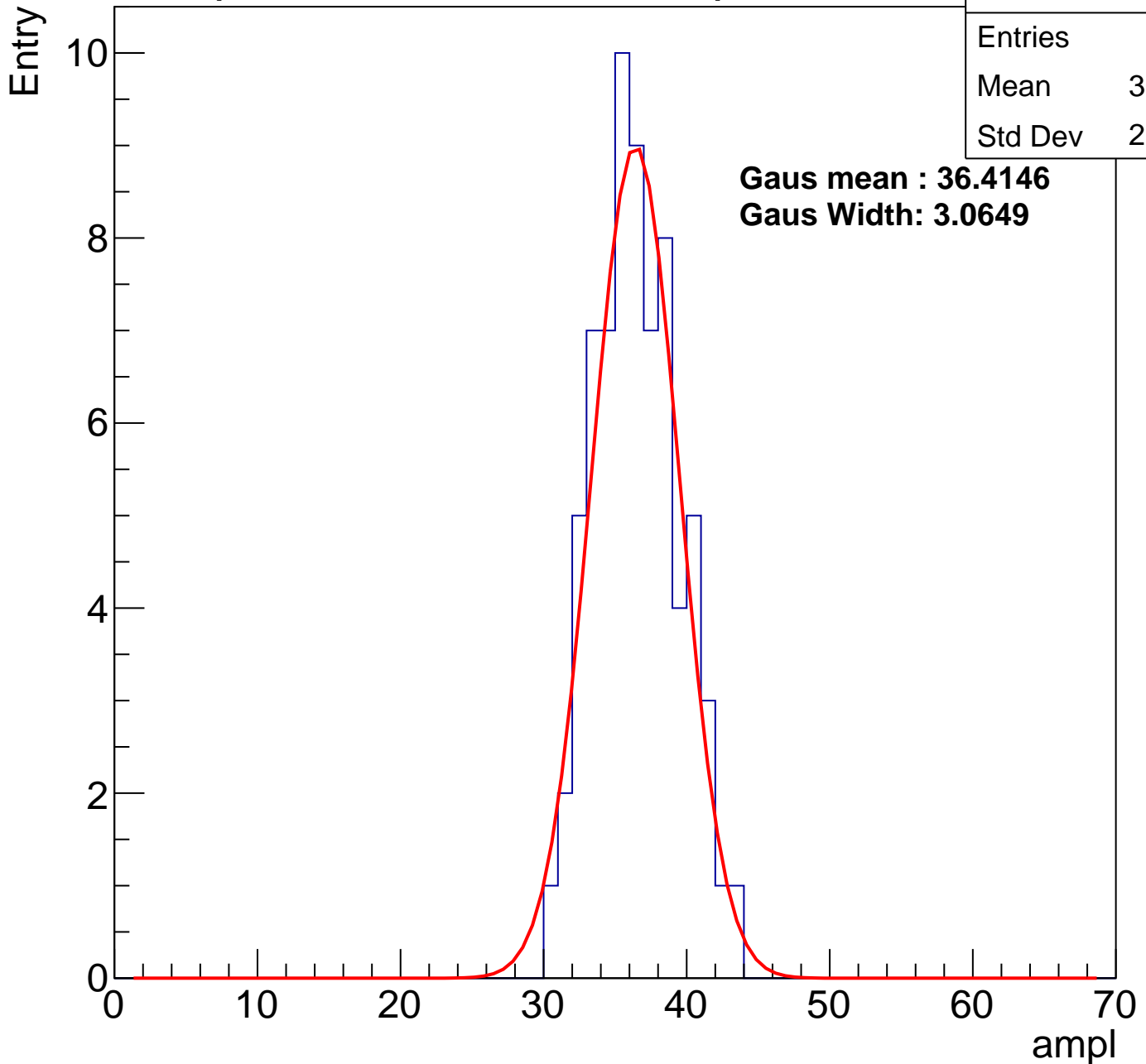
# B1L000S, U21-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	36.03
Std Dev	2.883

**Gaus mean : 36.4146**

**Gaus Width: 3.0649**



# B1L000S, U21-ch37, adc2

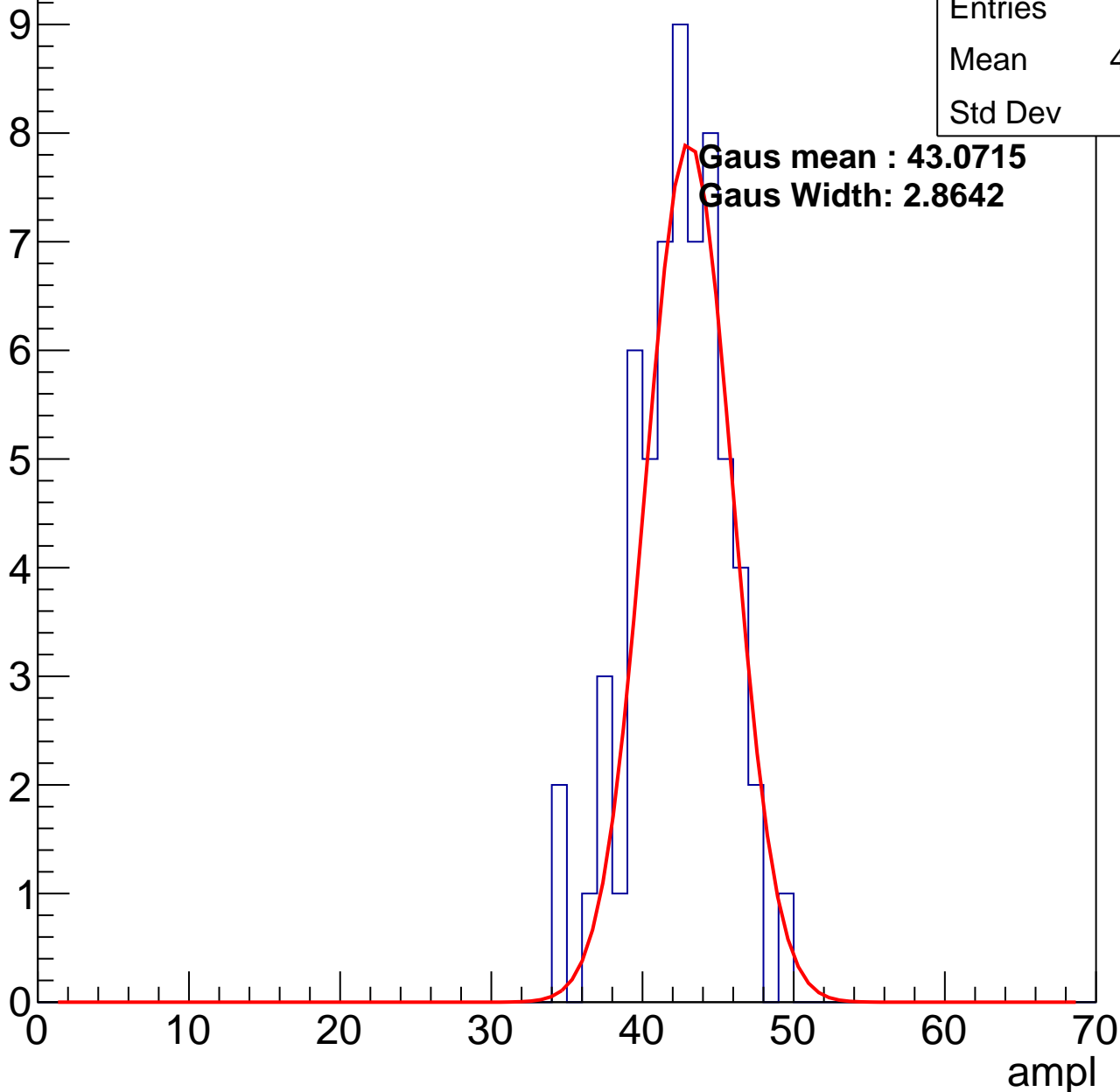
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.92
Std Dev	3.09

**Gaus mean : 43.0715**

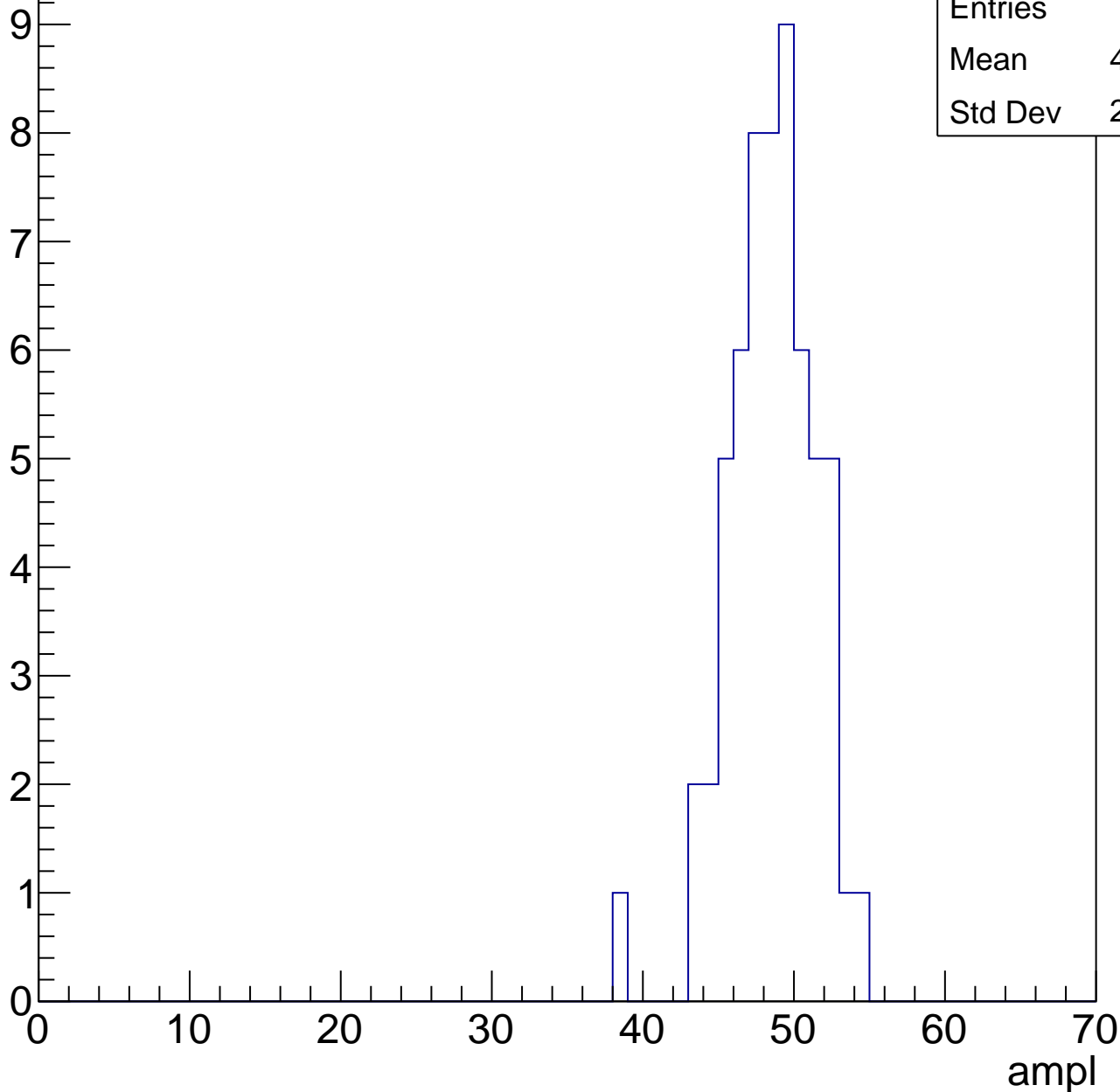
**Gaus Width: 2.8642**



# B1L000S, U21-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



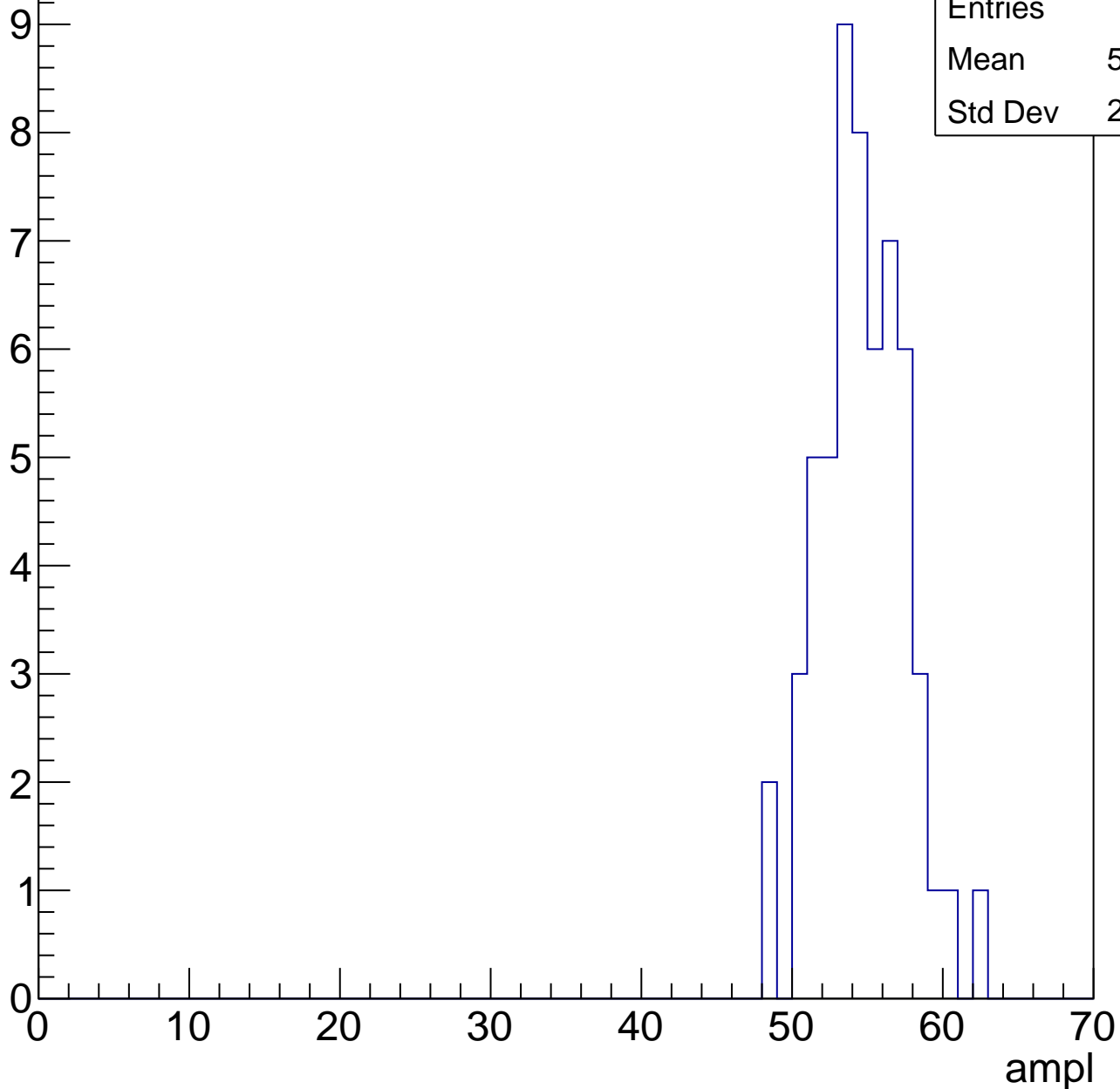
Entries	59
Mean	48.07
Std Dev	2.846

# B1L000S, U21-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	54.19
Std Dev	2.819

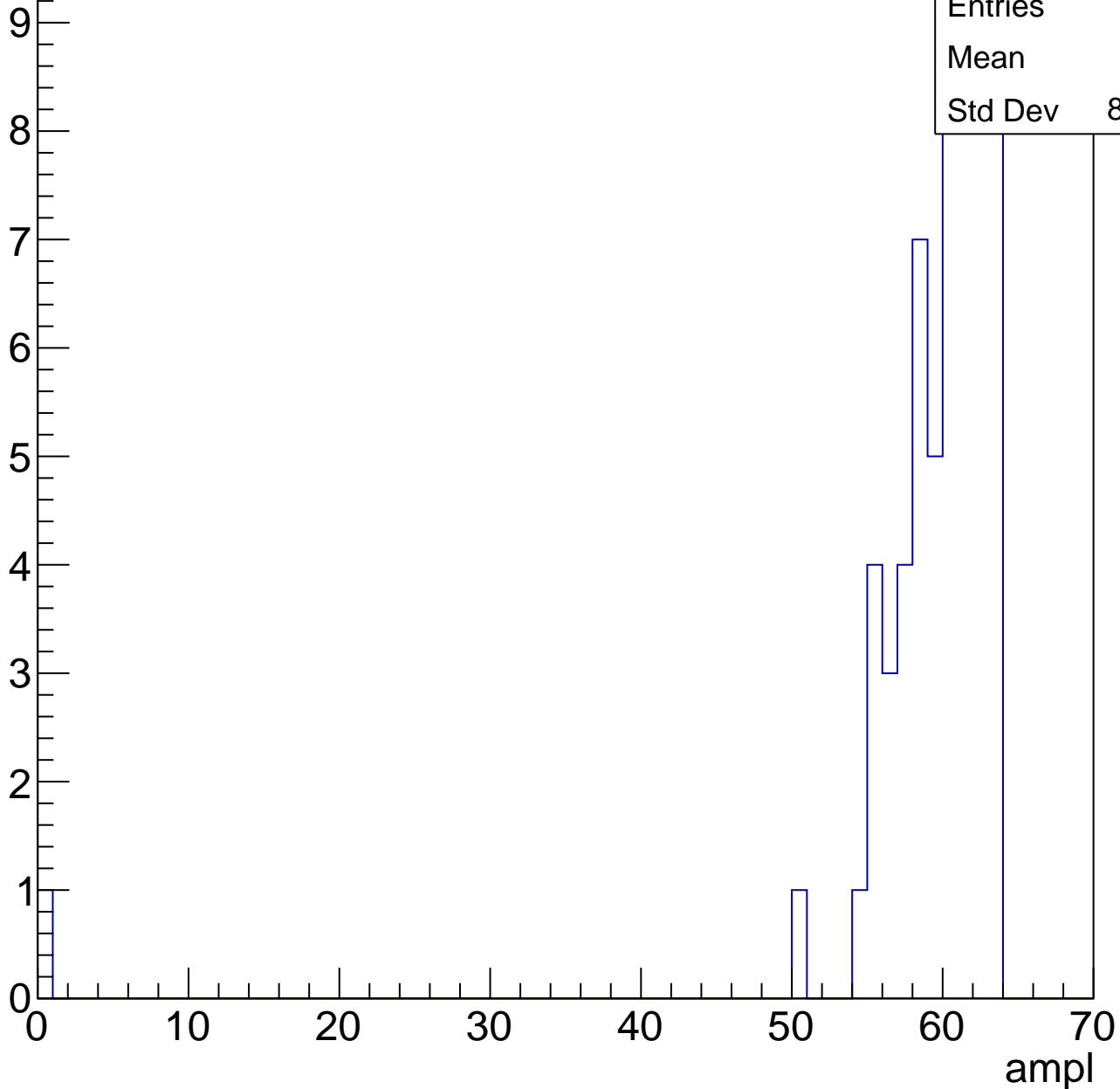


# B1L000S, U21-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	58.5
Std Dev	8.094



# B1L000S, U21-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

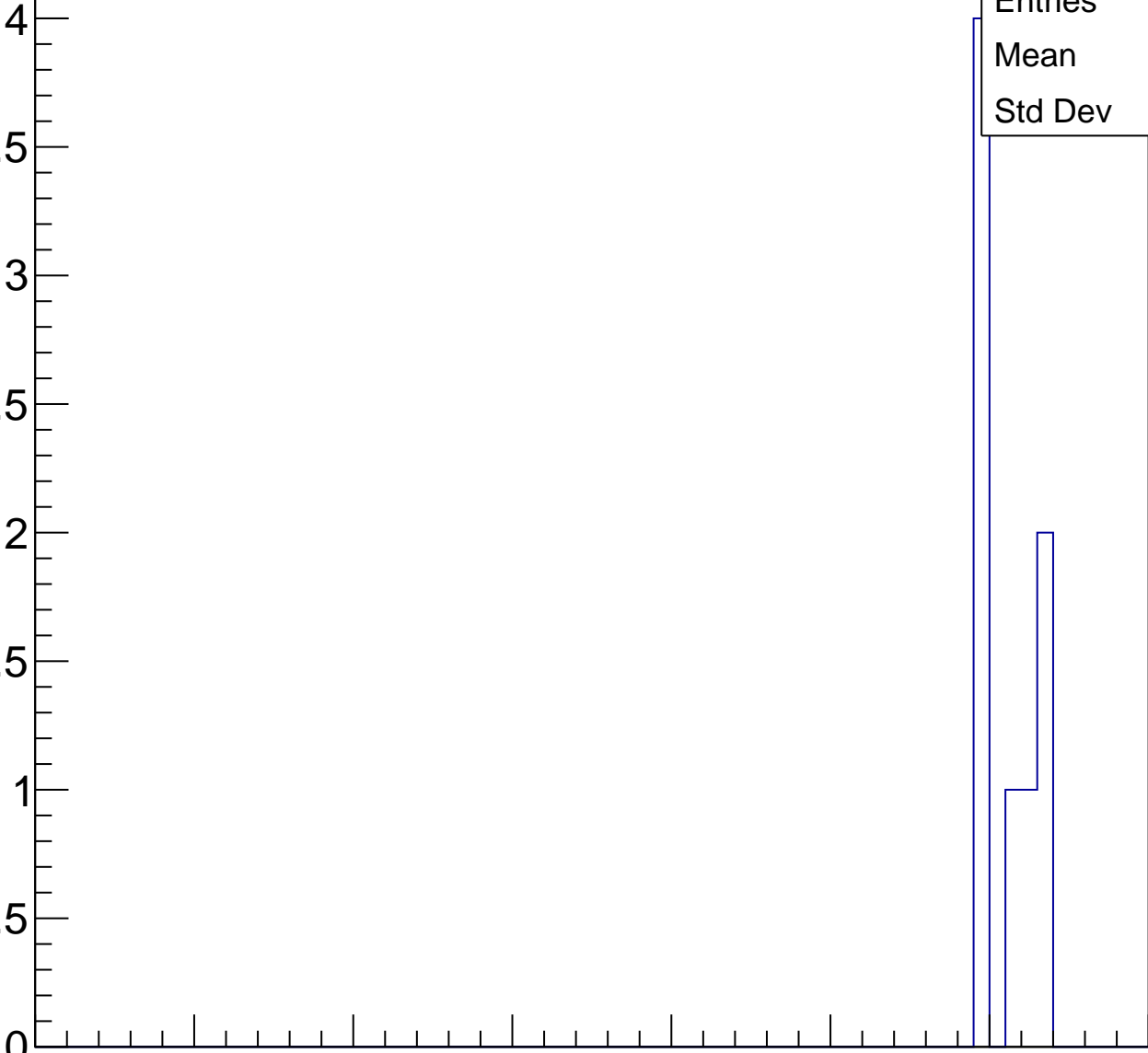
Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	8
Mean	60.62
Std Dev	1.728

ampl

0 10 20 30 40 50 60 70





# B1L000S, U21-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch38, adc0

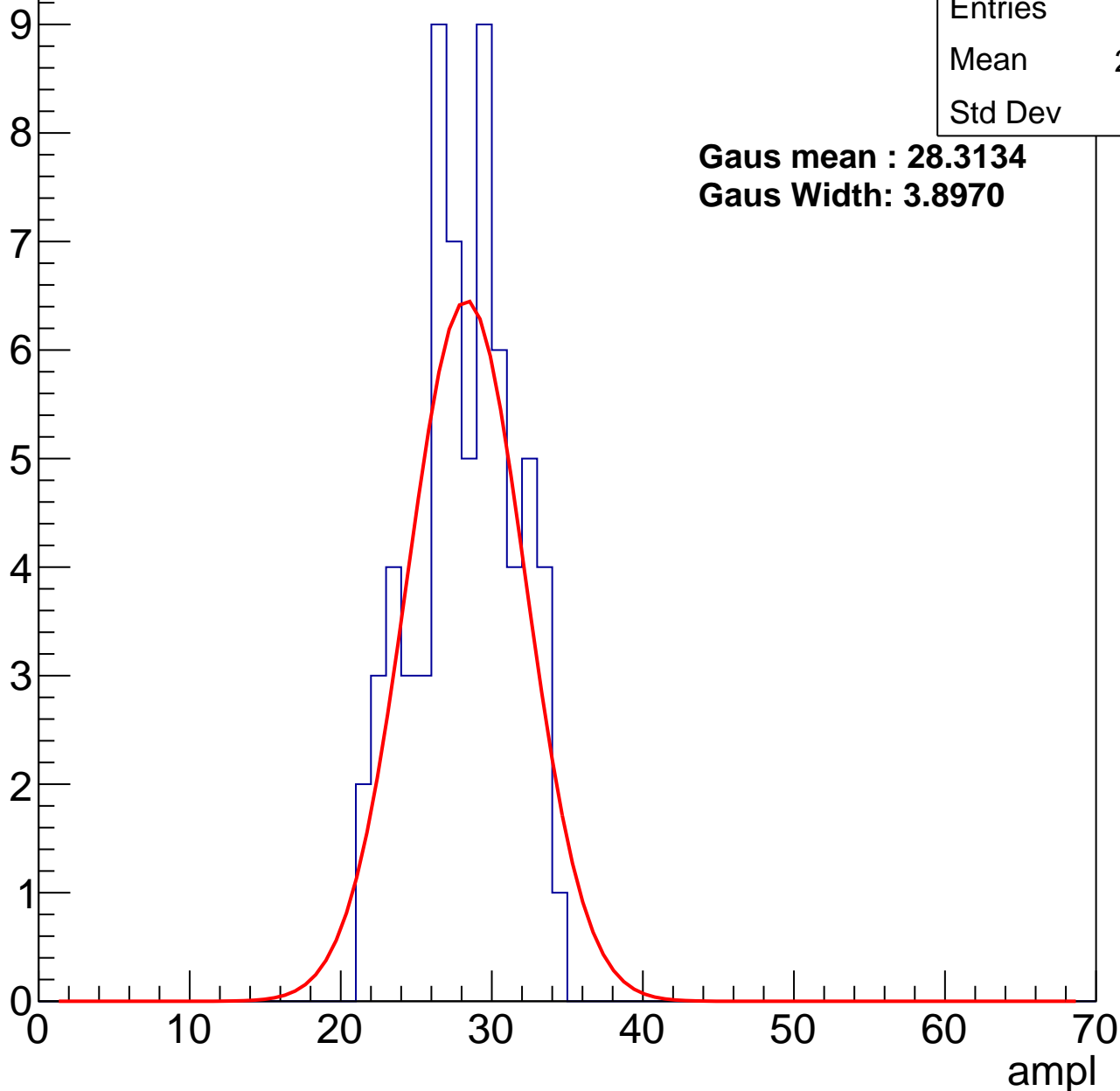
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	27.71
Std Dev	3.29

**Gaus mean : 28.3134**

**Gaus Width: 3.8970**



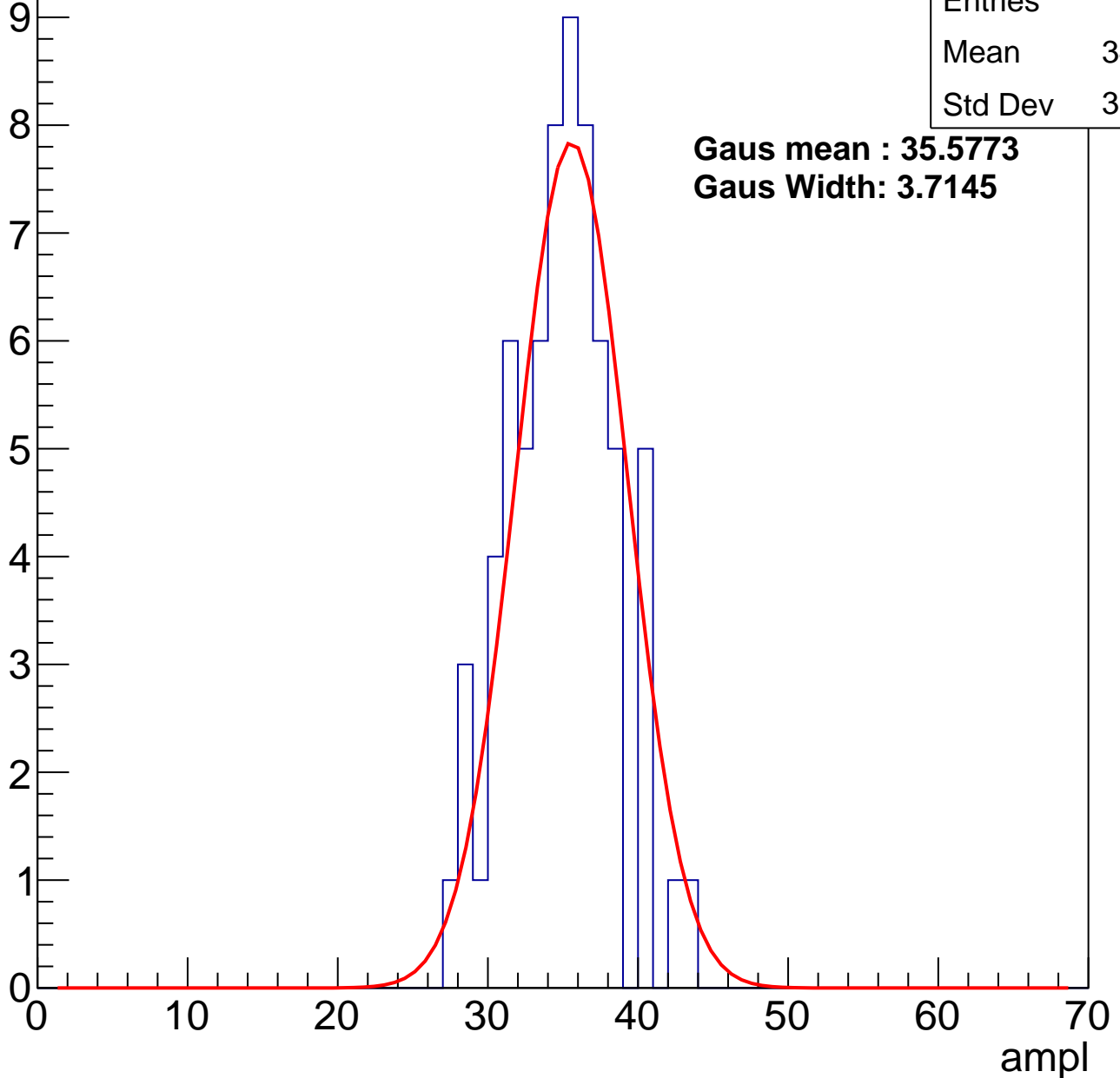
# B1L000S, U21-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.43
Std Dev	3.432

**Gaus mean : 35.5773**  
**Gaus Width: 3.7145**



# B1L000S, U21-ch38, adc2

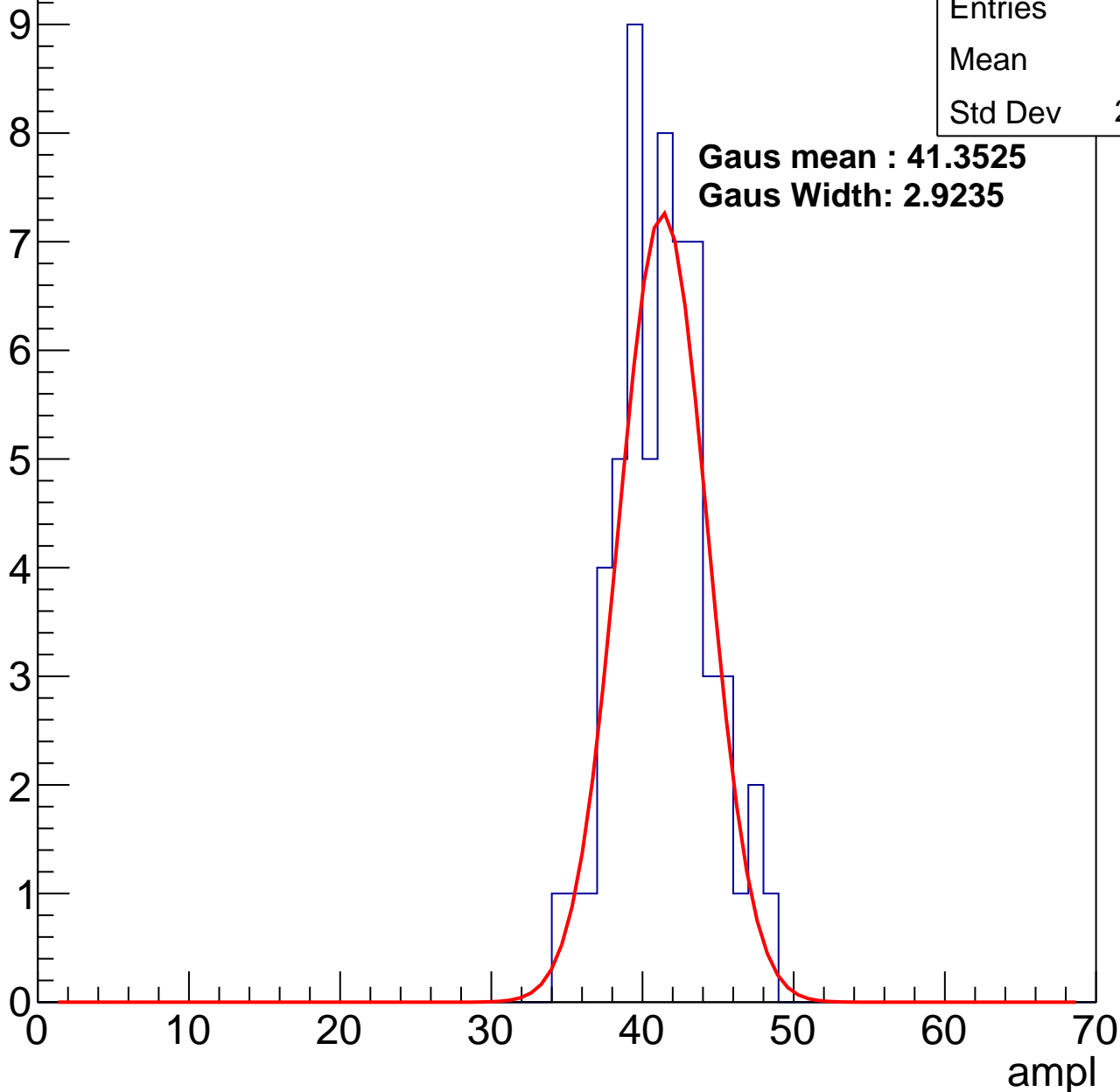
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	40.9
Std Dev	2.981

**Gaus mean : 41.3525**

**Gaus Width: 2.9235**

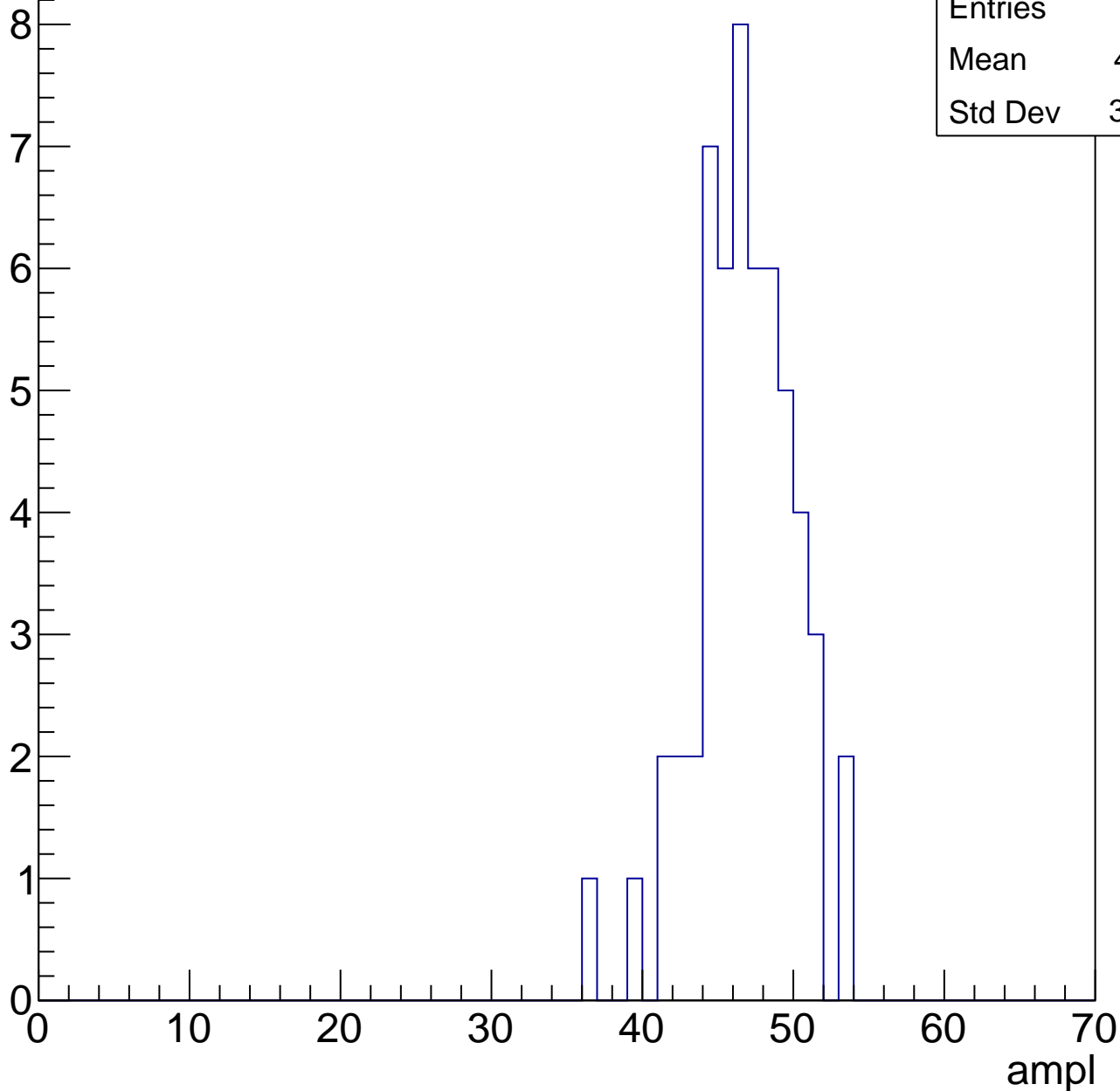


# B1L000S, U21-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	46.31
Std Dev	3.275

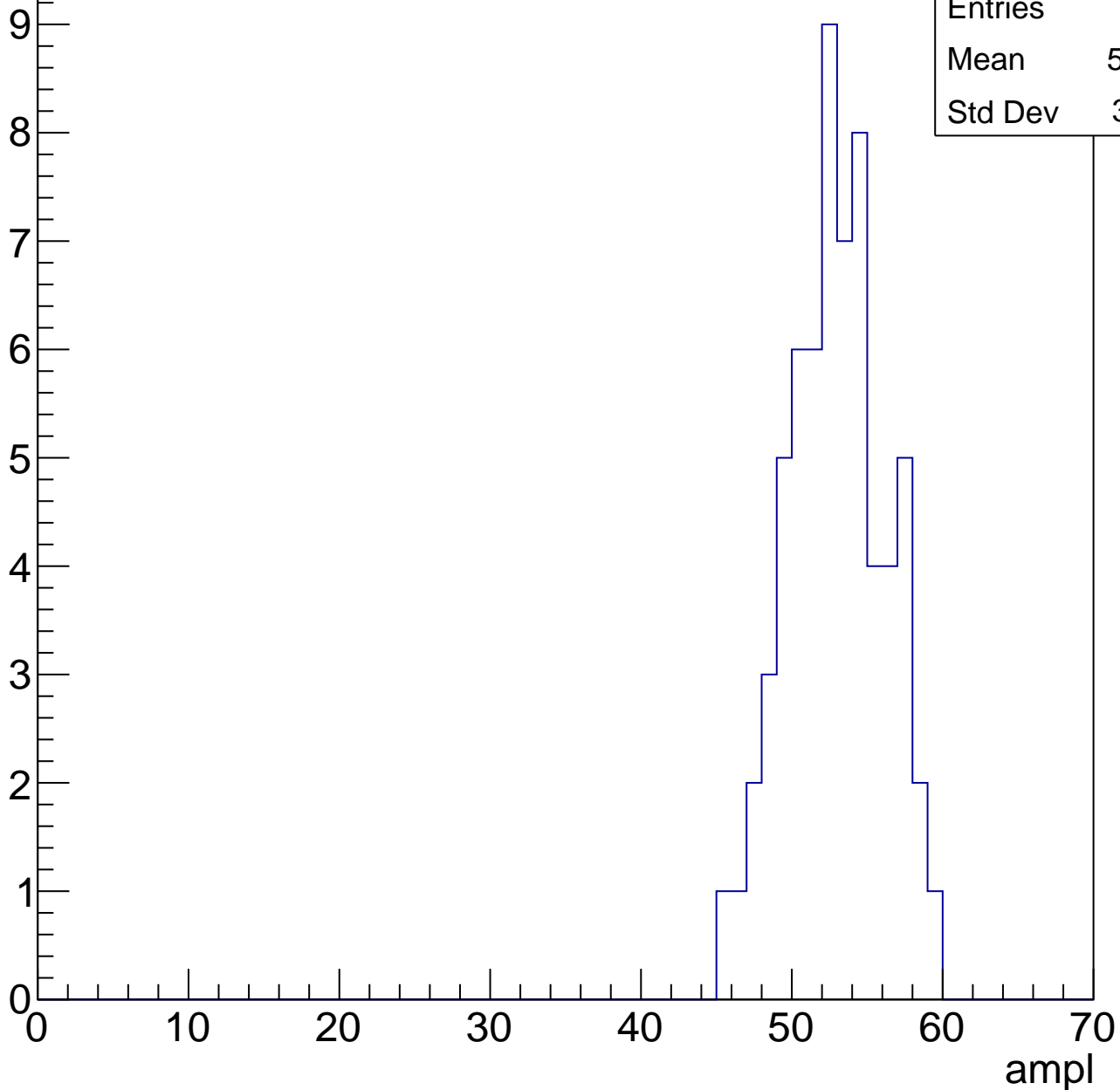


# B1L000S, U21-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

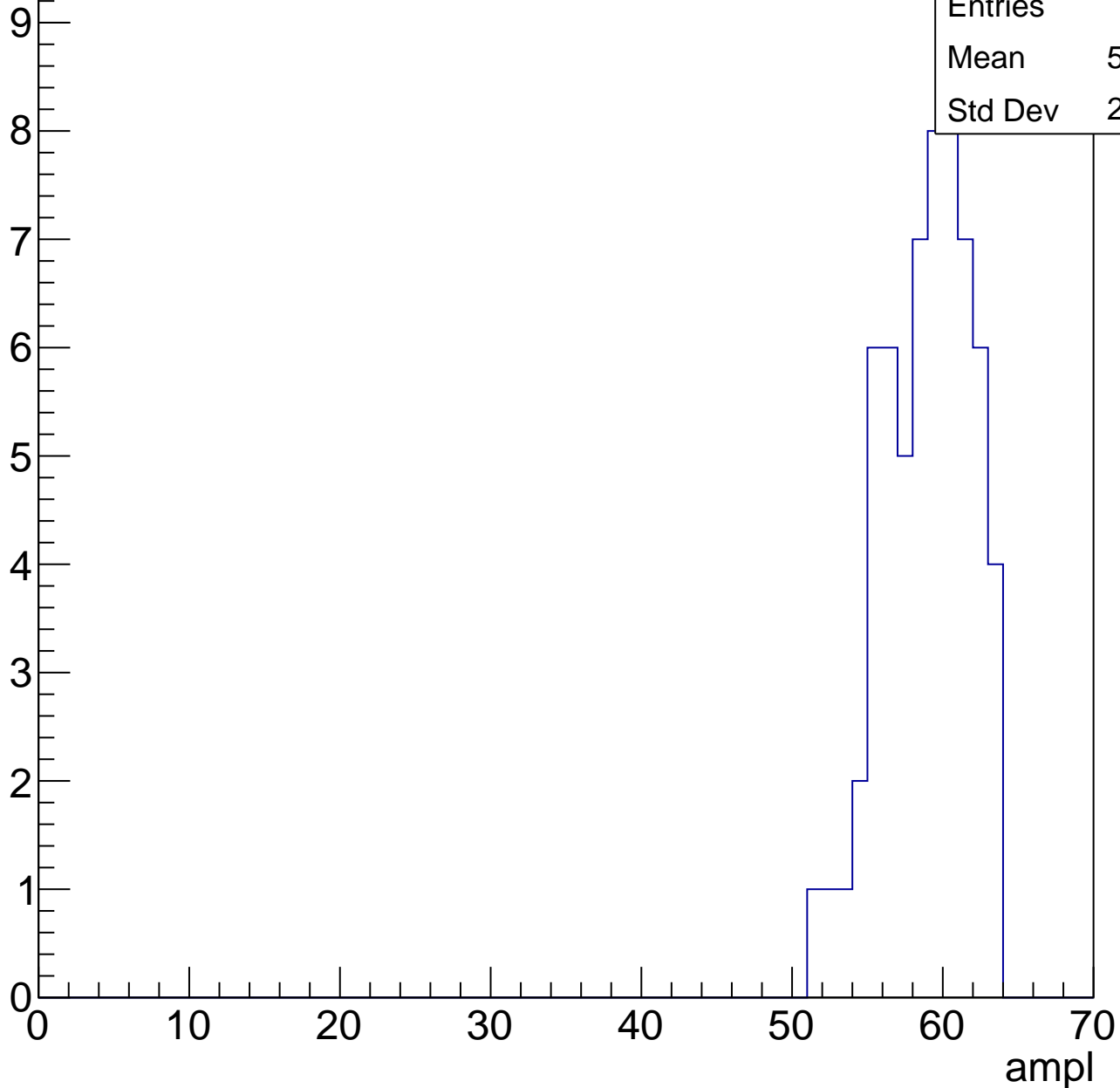
Entries	64
Mean	52.42
Std Dev	3.141



# B1L000S, U21-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

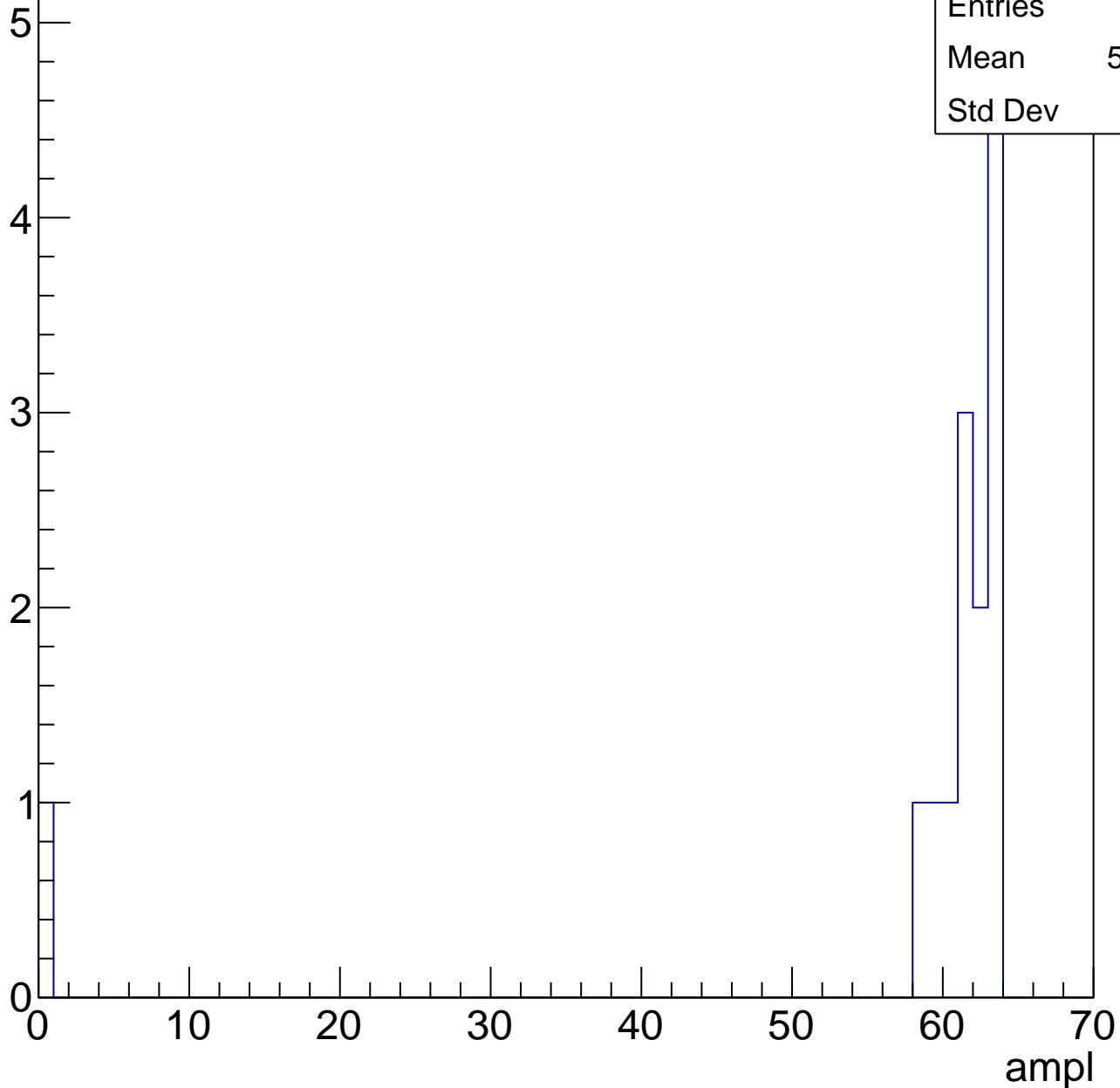


# B1L000S, U21-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.07
Std Dev	15.9

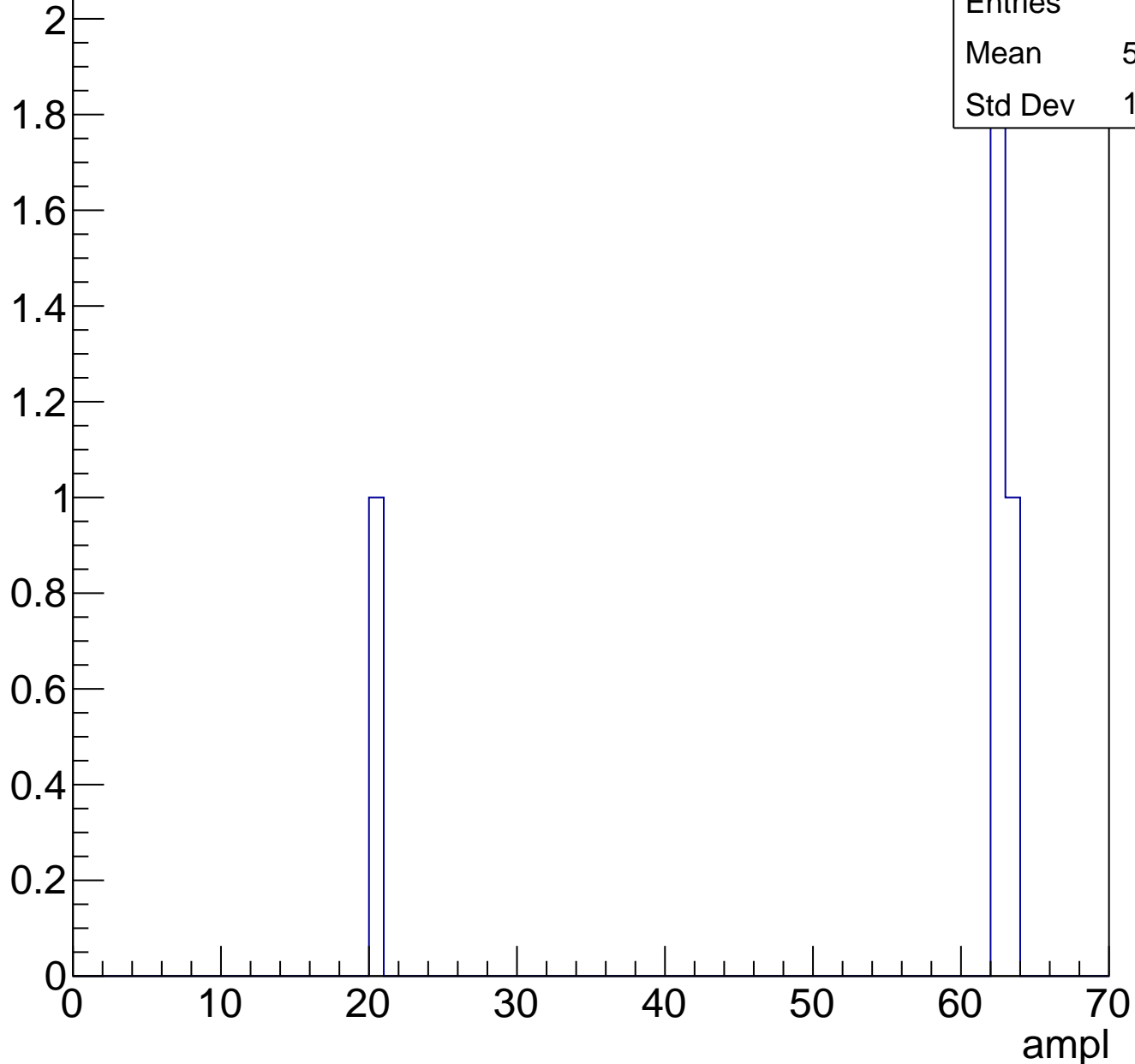




# B1L000S, U21-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	51.75
Std Dev	18.34

# B1L000S, U21-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	31.43
Std Dev	2.805

**Gaus mean : 31.8549**

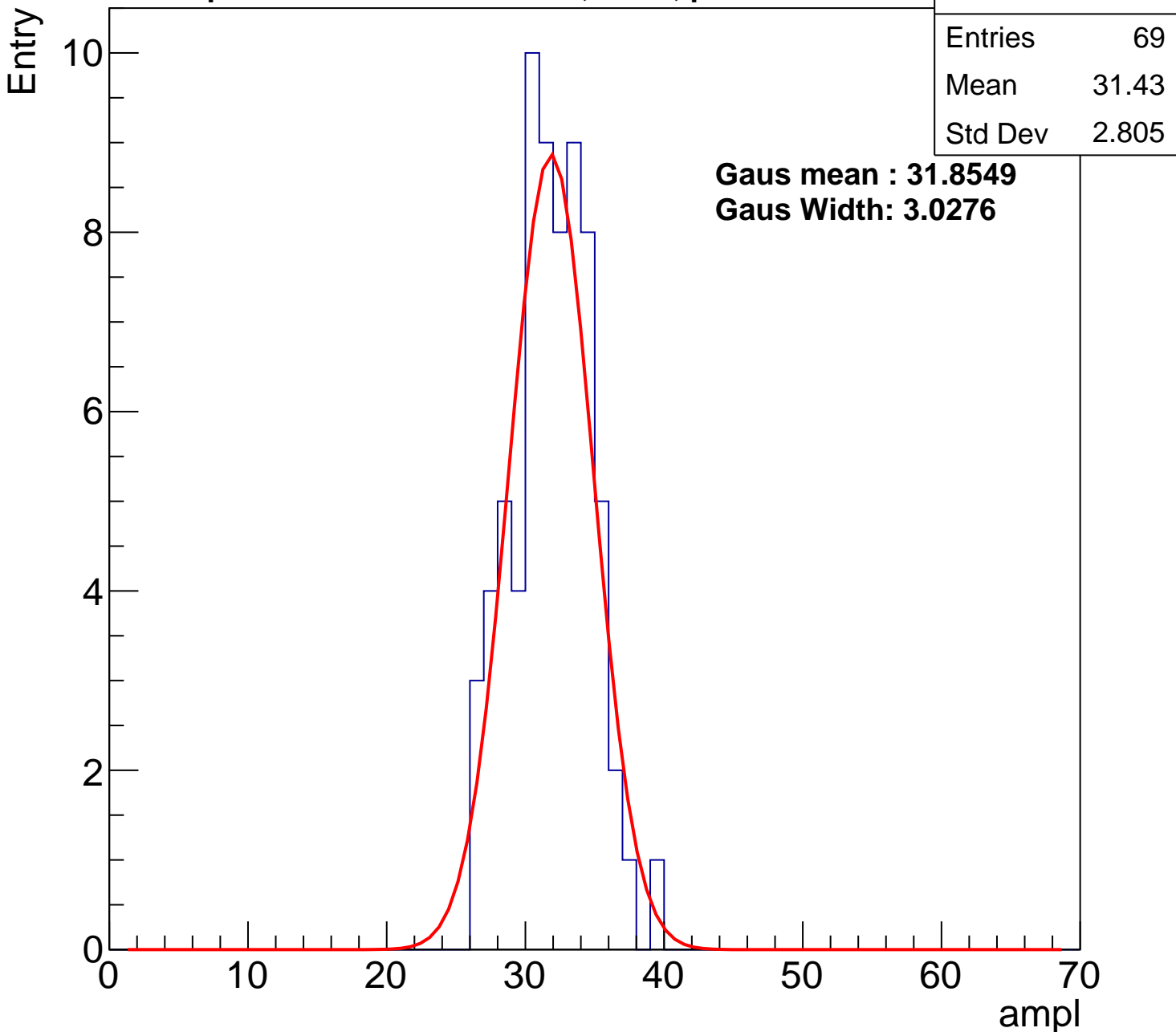
**Gaus Width: 3.0276**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



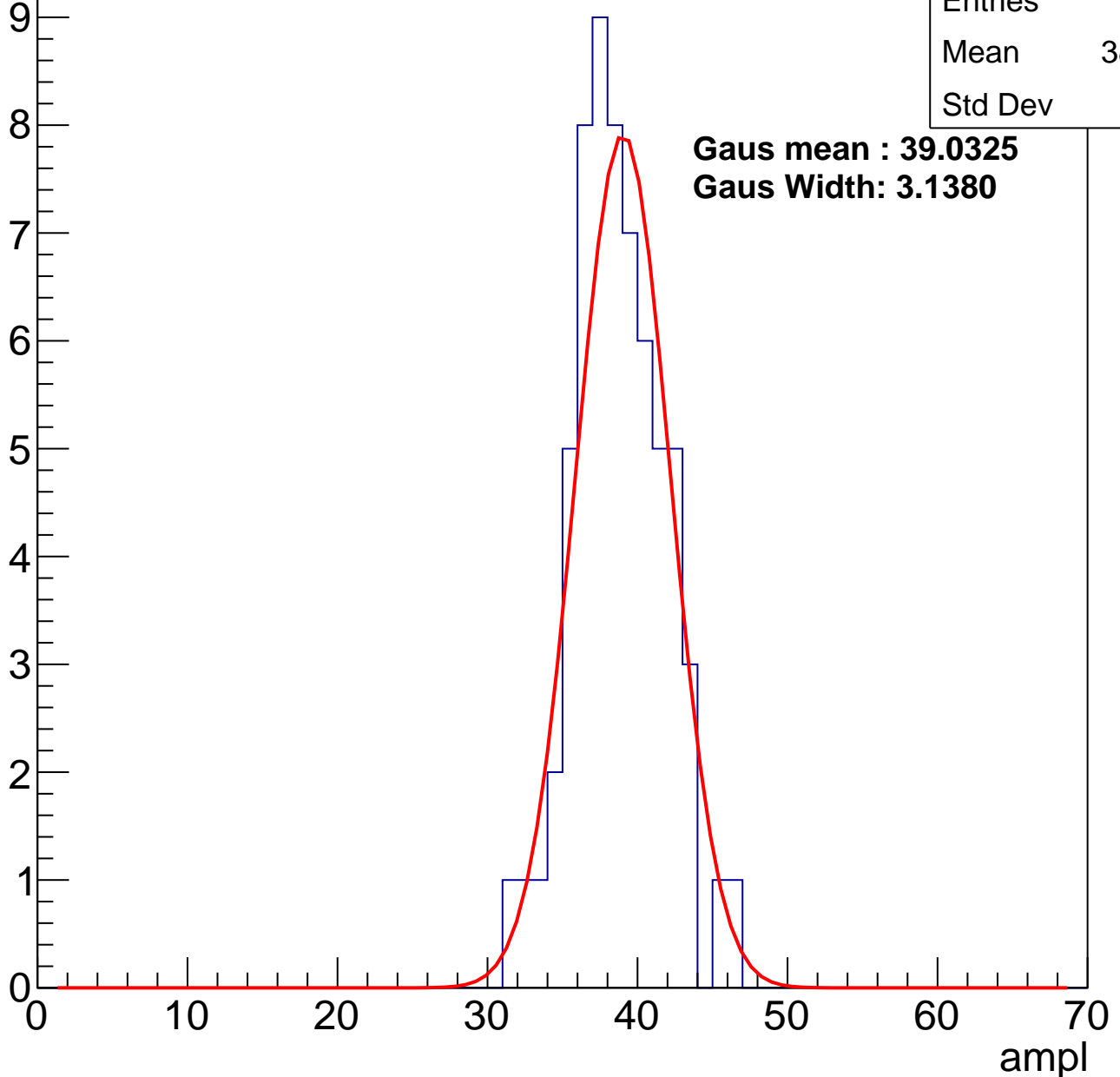
# B1L000S, U21-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	38.29
Std Dev	3

**Gaus mean : 39.0325**  
**Gaus Width: 3.1380**



# B1L000S, U21-ch39, adc2

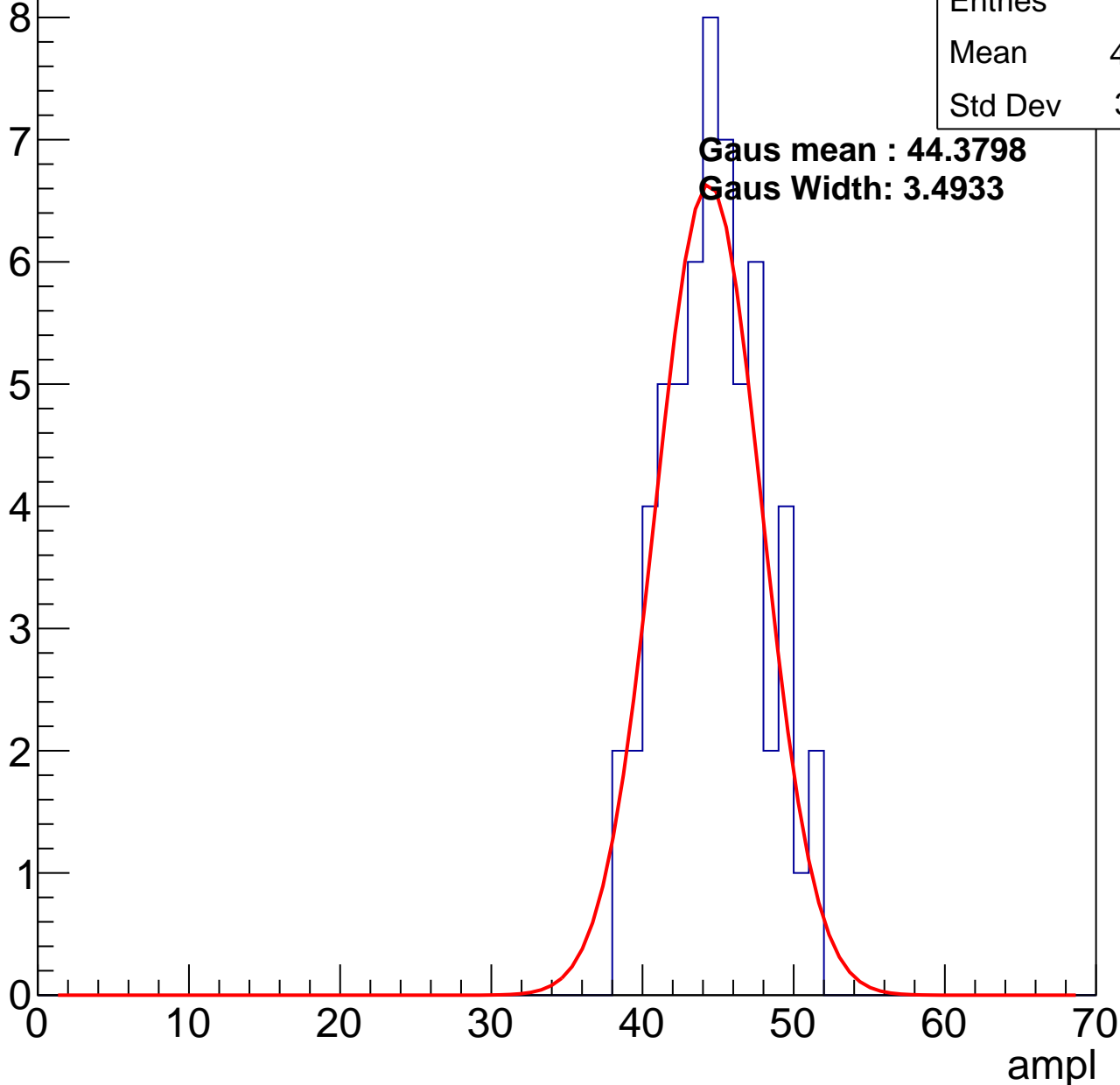
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	44.24
Std Dev	3.191

**Gaus mean : 44.3798**

**Gaus Width: 3.4933**



# B1L000S, U21-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

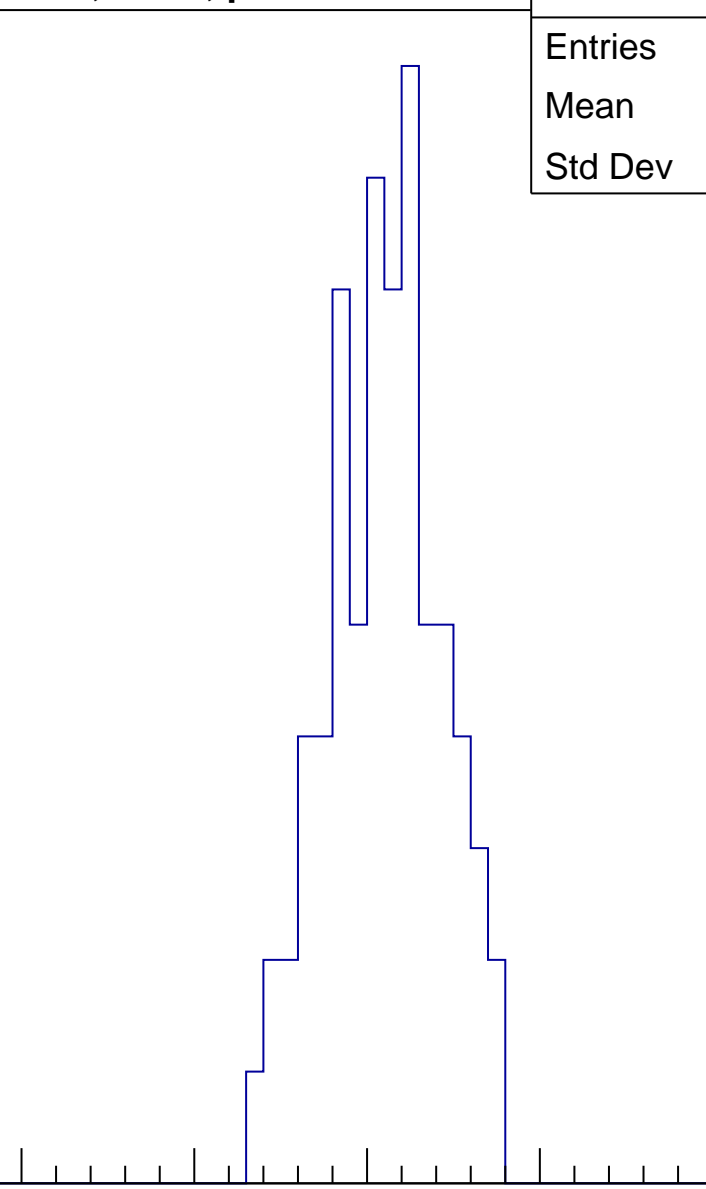
Entries	72
Mean	50.51
Std Dev	3.266

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

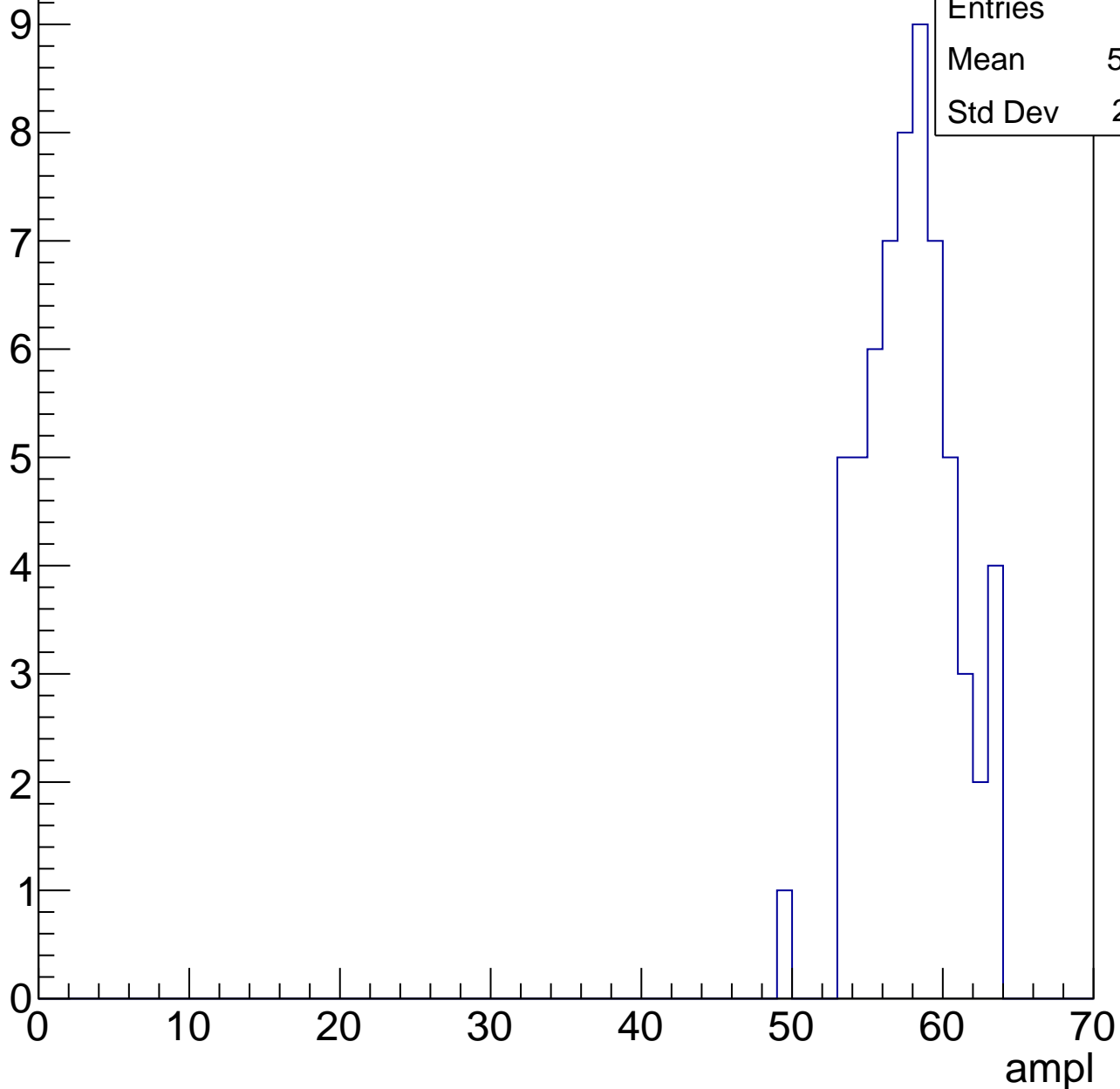
ampl



# B1L000S, U21-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



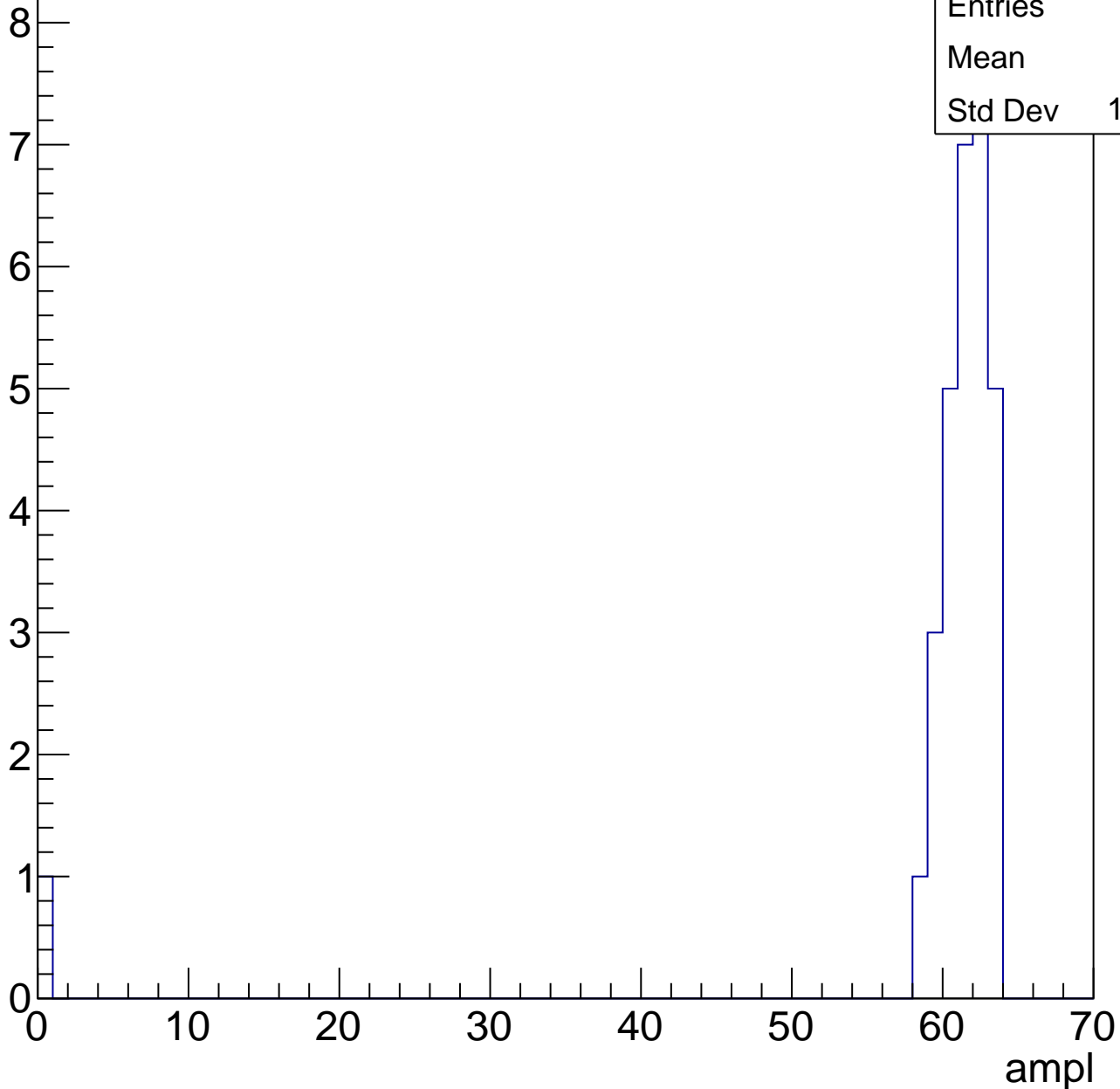
Entries	62
Mean	57.35
Std Dev	2.941

# B1L000S, U21-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	59.1
Std Dev	11.06



# B1L000S, U21-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



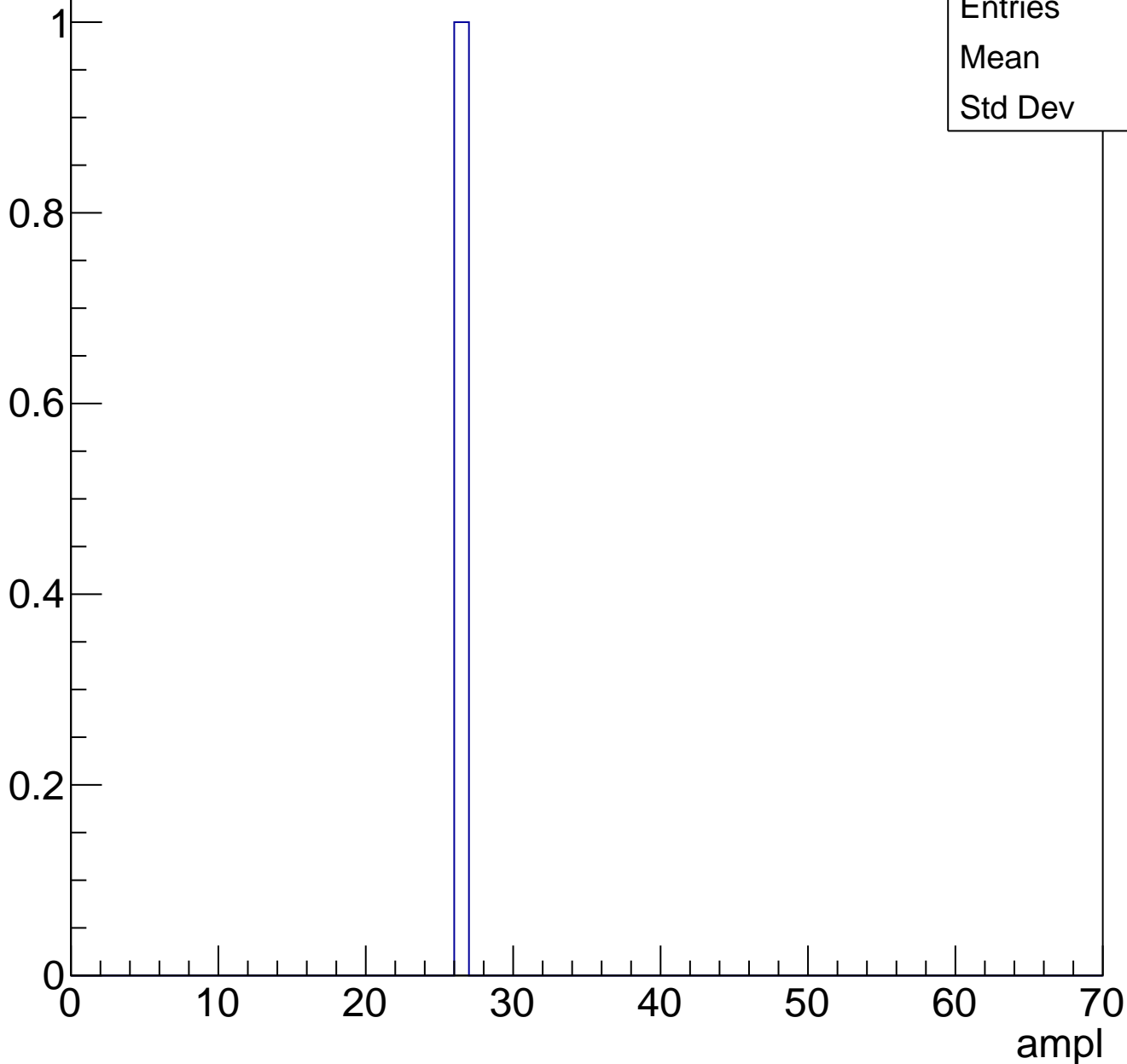
Entries	1
Mean	63
Std Dev	0



# B1L000S, U21-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch40, adc0

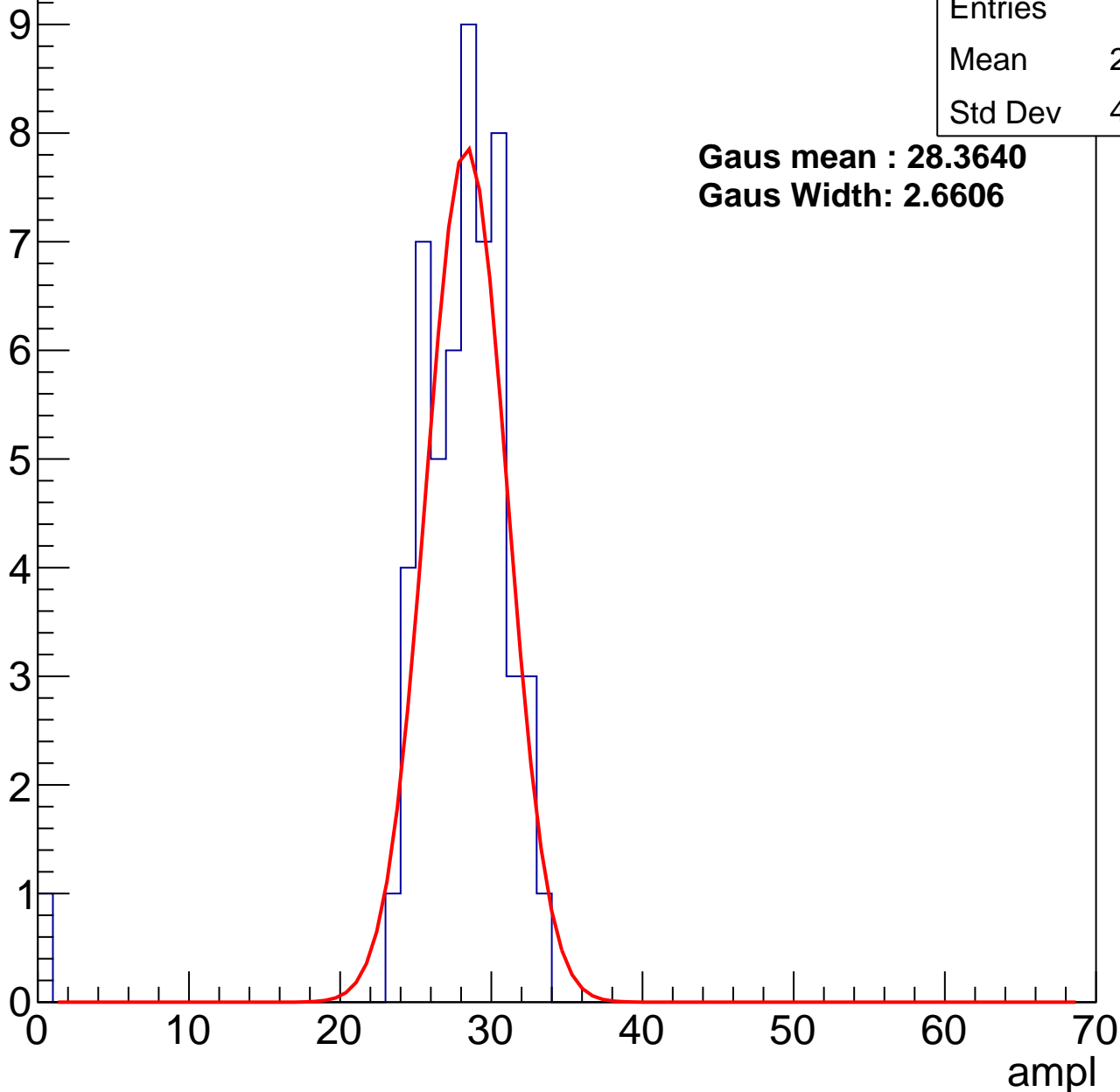
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	27.33
Std Dev	4.423

**Gaus mean : 28.3640**

**Gaus Width: 2.6606**



# B1L000S, U21-ch40, adc1

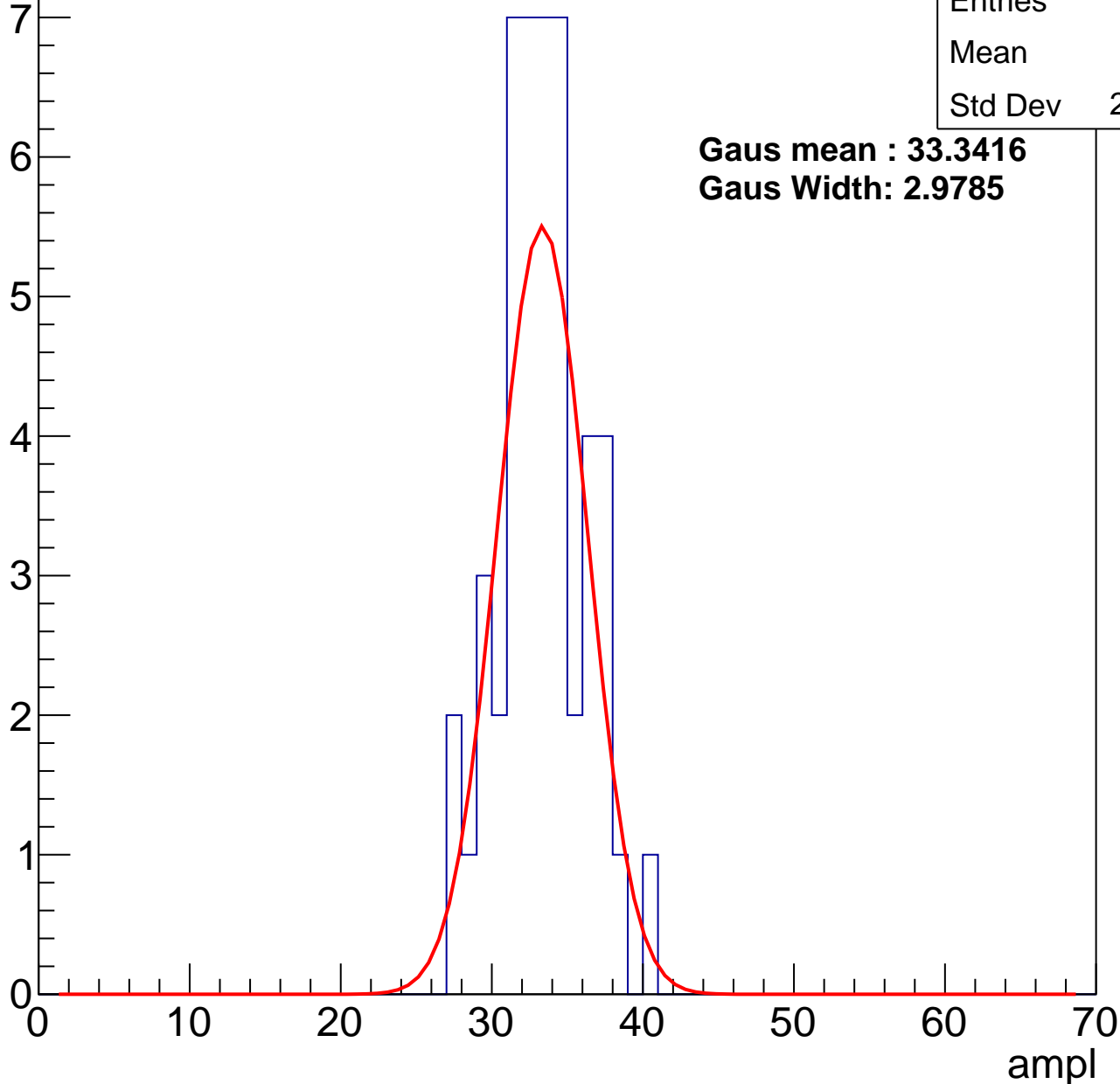
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	32.9
Std Dev	2.838

**Gaus mean : 33.3416**

**Gaus Width: 2.9785**



# B1L000S, U21-ch40, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	39.35
Std Dev	3.282

**Gaus mean : 39.6976**

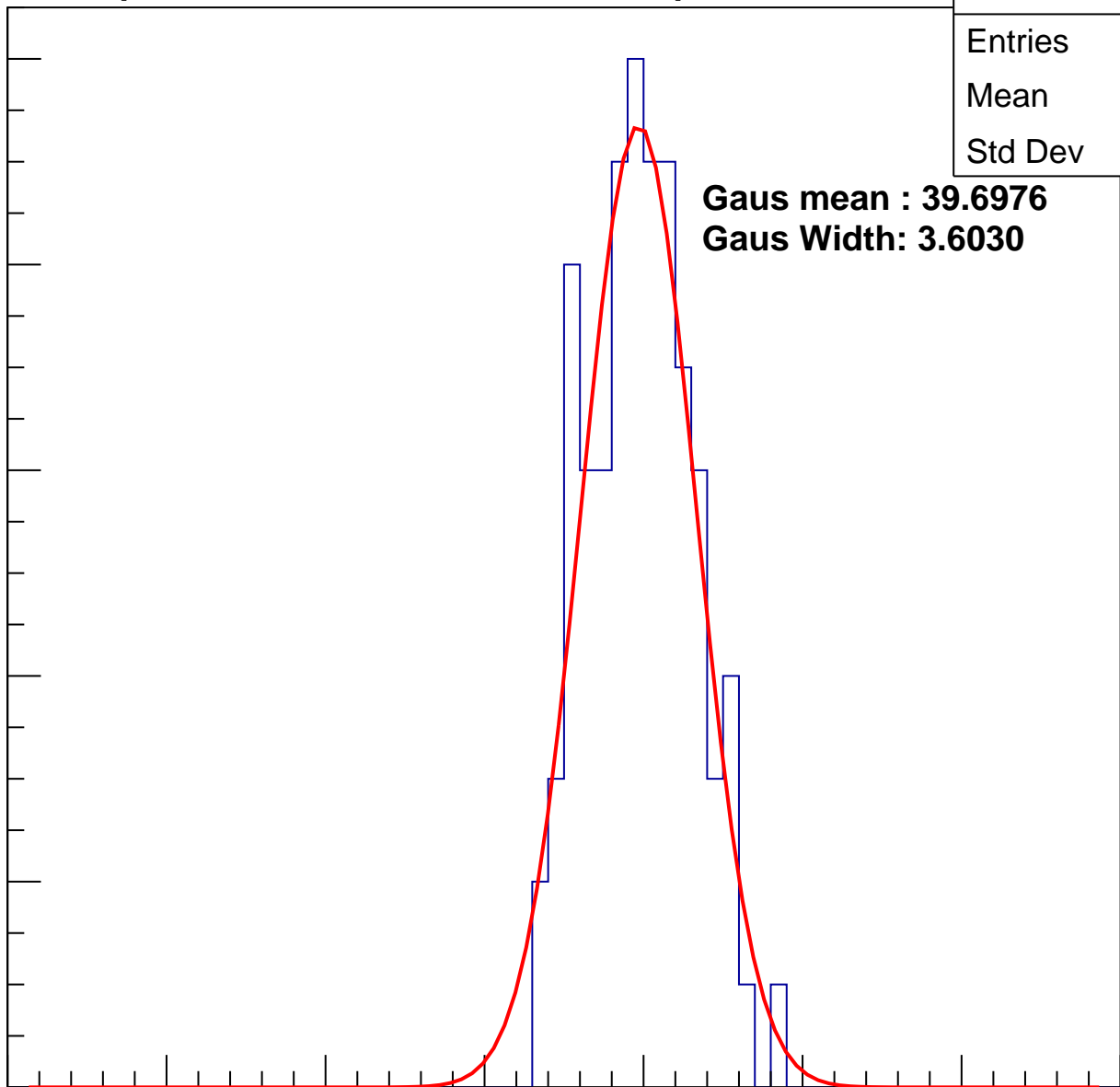
**Gaus Width: 3.6030**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

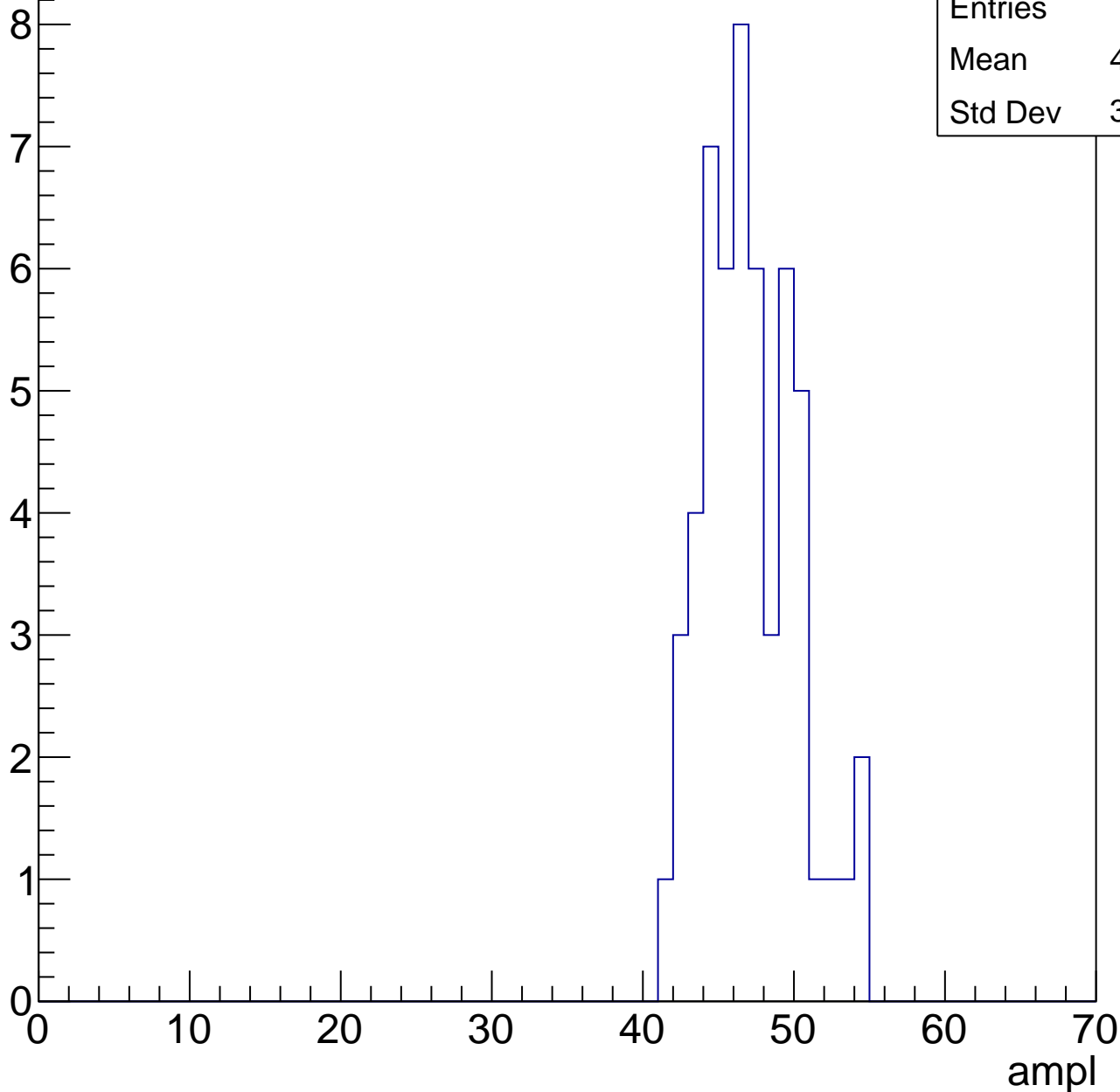


# B1L000S, U21-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	46.65
Std Dev	3.074

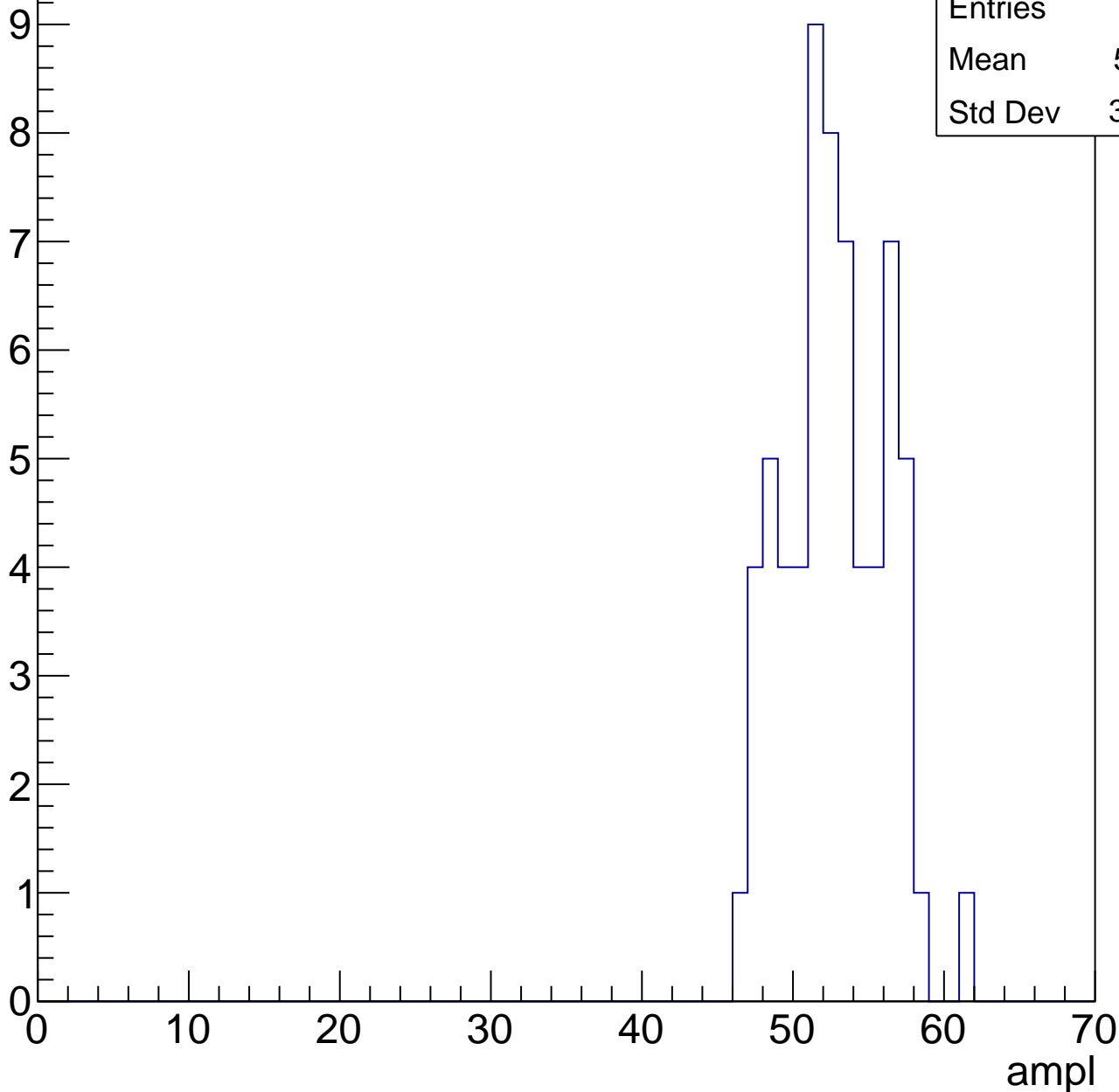


# B1L000S, U21-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

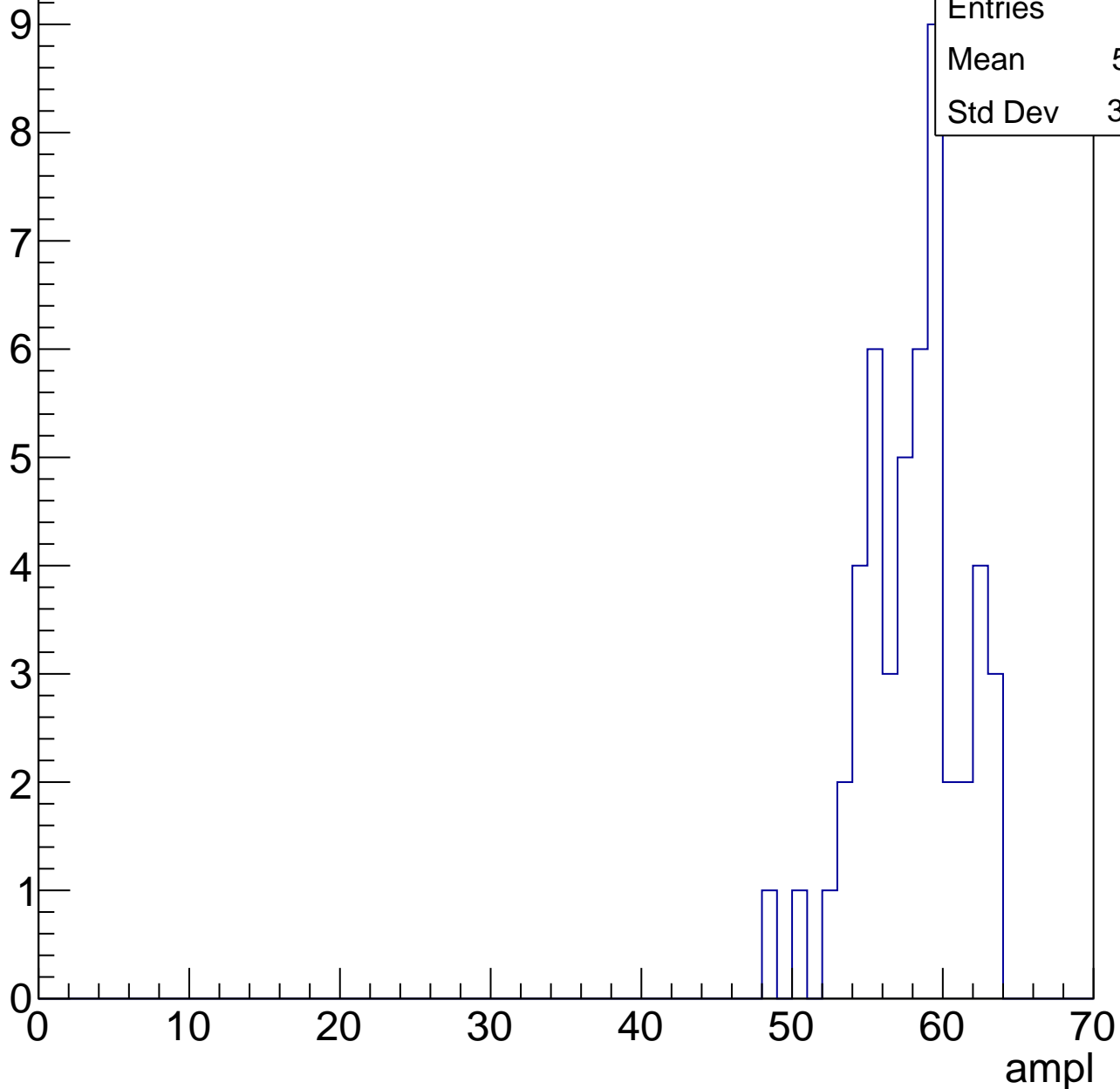
Entries	64
Mean	52.31
Std Dev	3.269



# B1L000S, U21-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

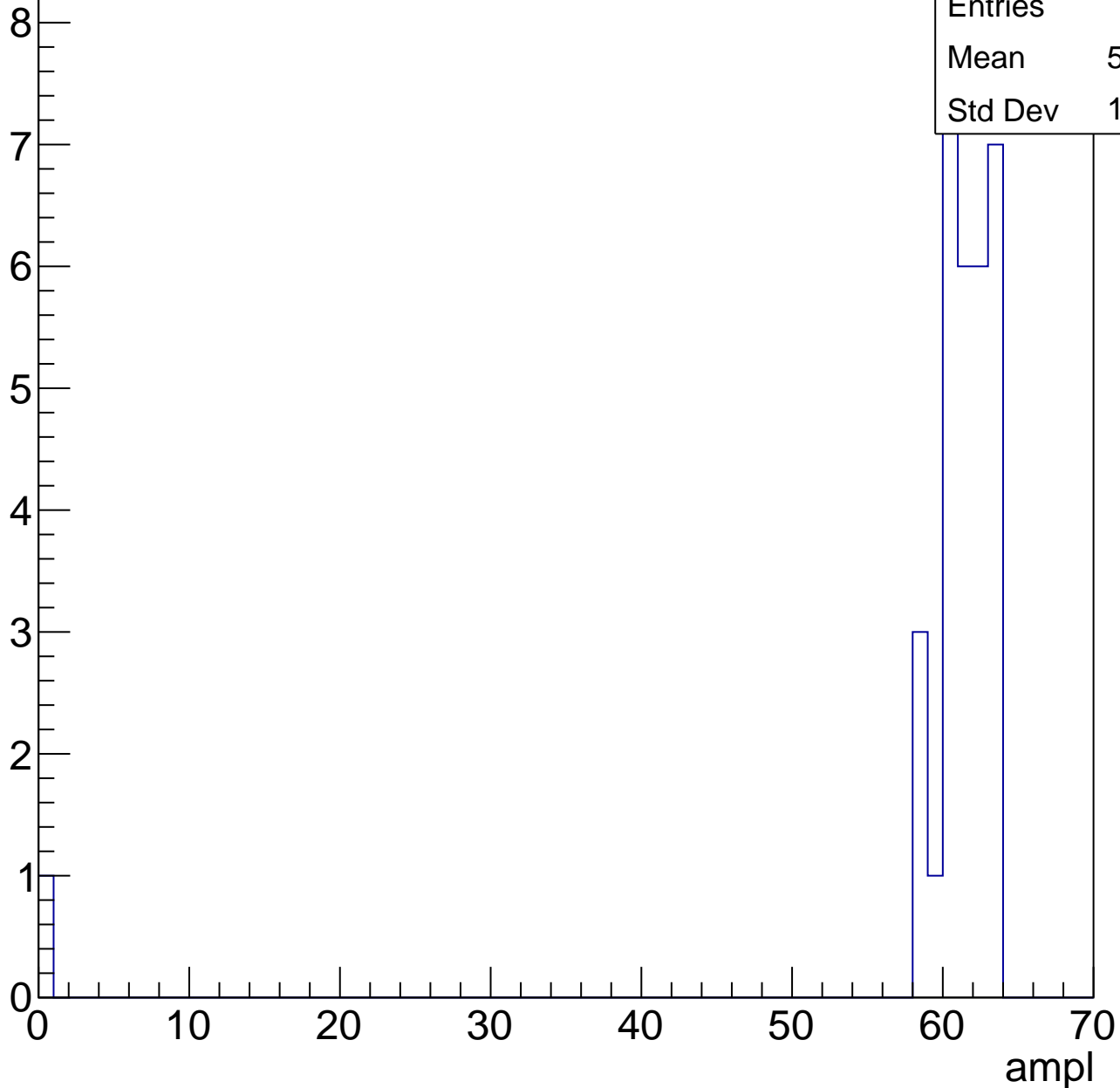
Entry



# B1L000S, U21-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch41, adc0

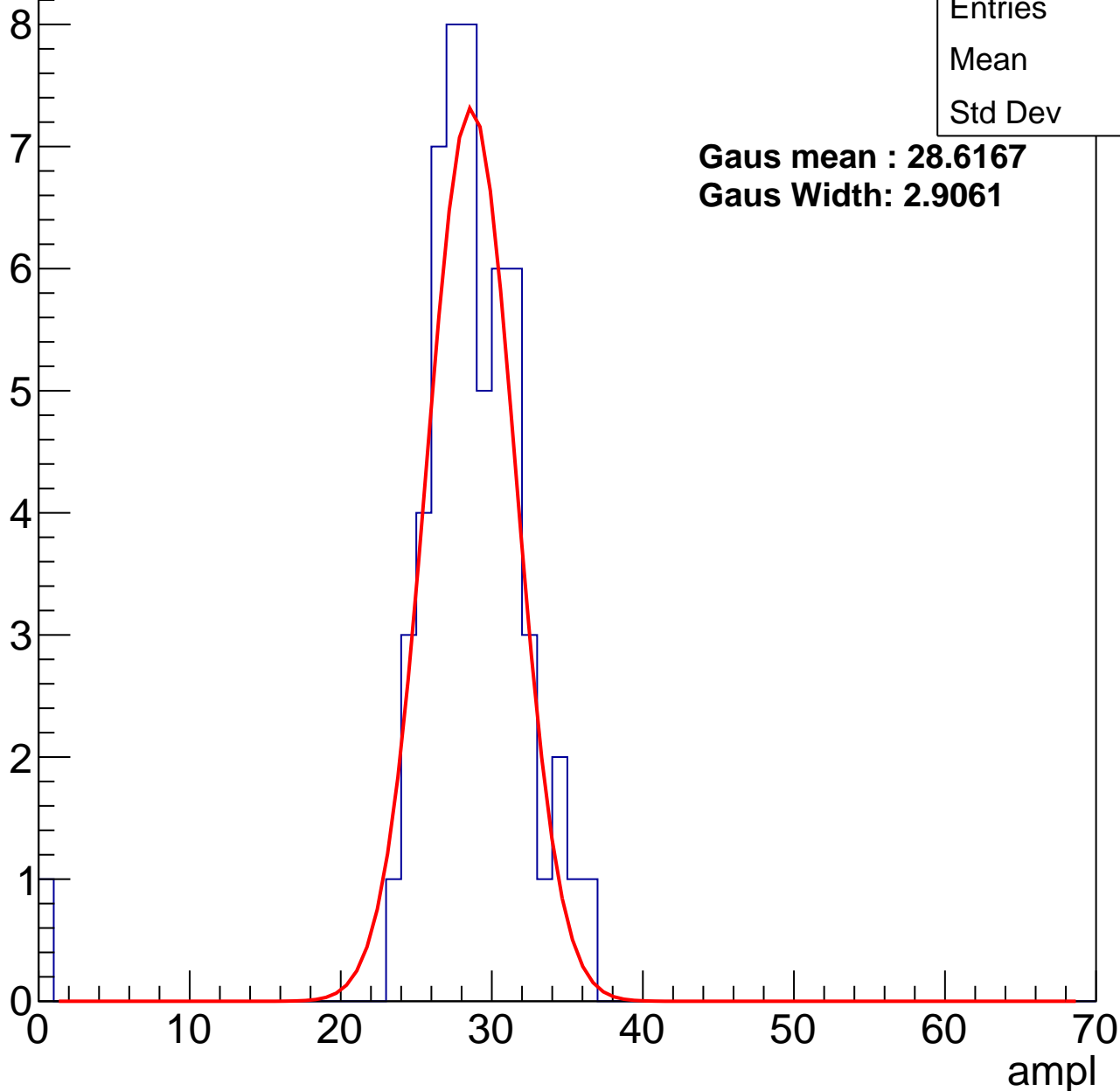
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28
Std Dev	4.72

**Gaus mean : 28.6167**

**Gaus Width: 2.9061**



# B1L000S, U21-ch41, adc1

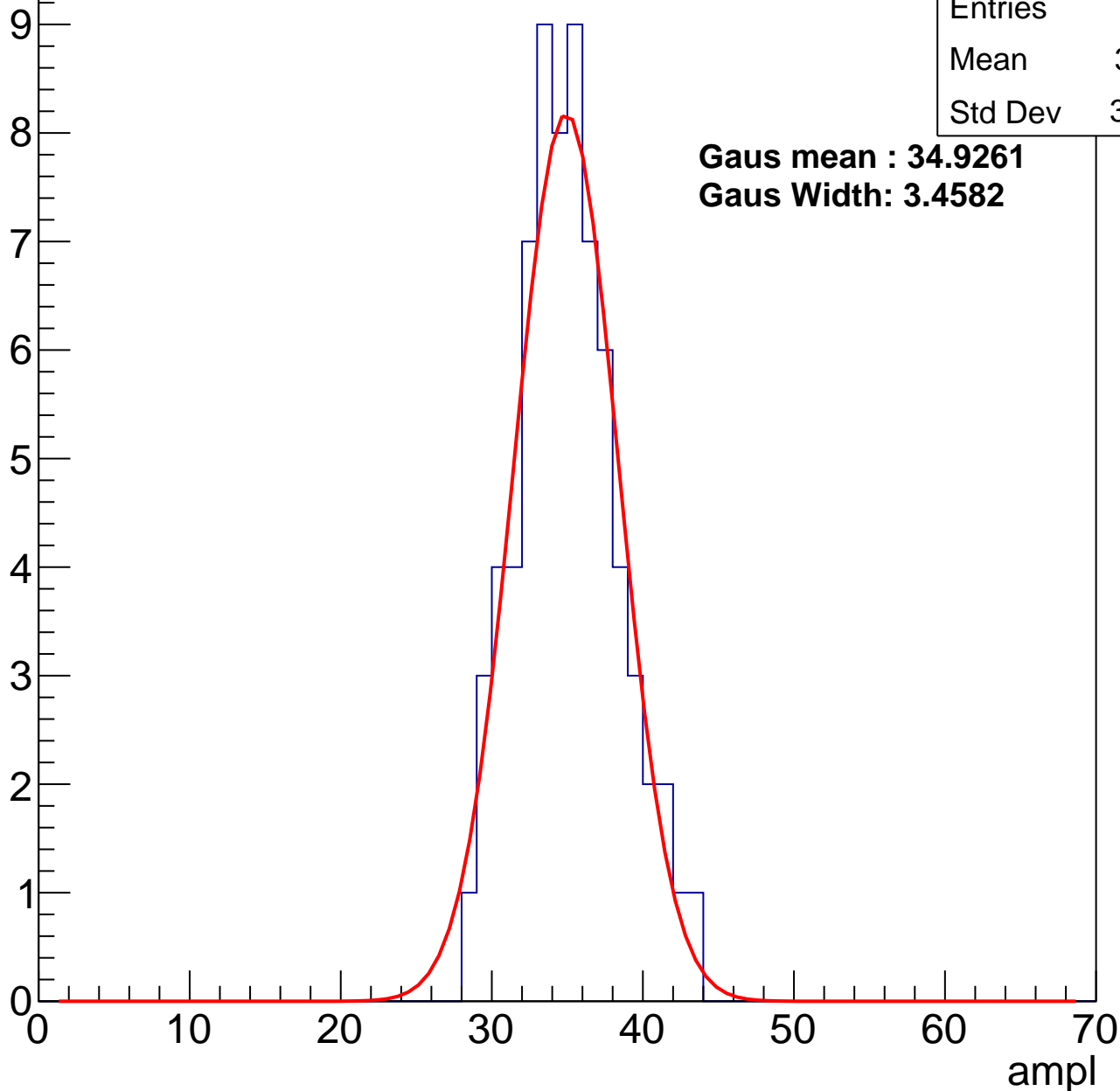
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.61
Std Dev	3.287

**Gaus mean : 34.9261**

**Gaus Width: 3.4582**



# B1L000S, U21-ch41, adc2

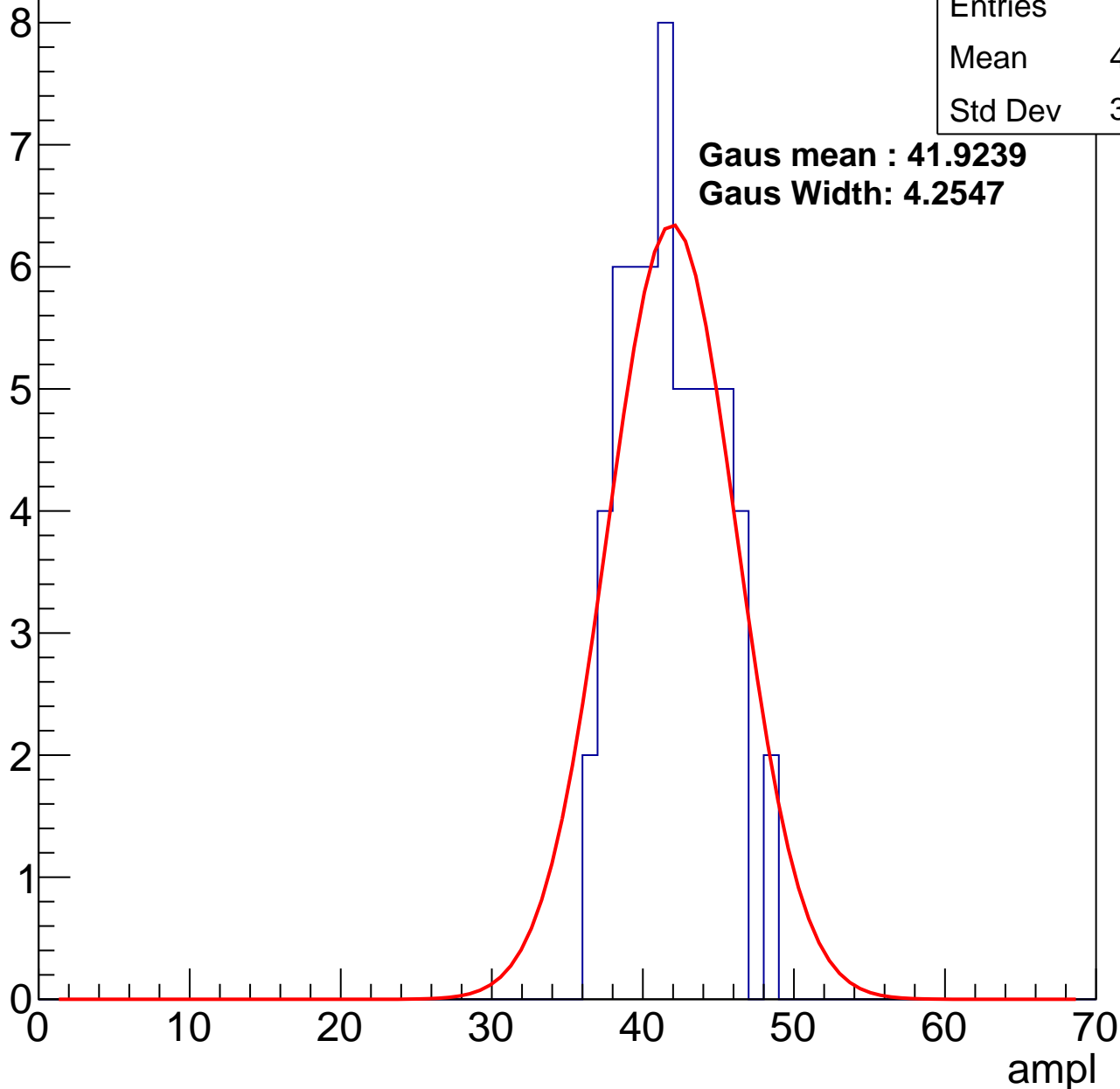
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.38
Std Dev	3.045

**Gaus mean : 41.9239**

**Gaus Width: 4.2547**

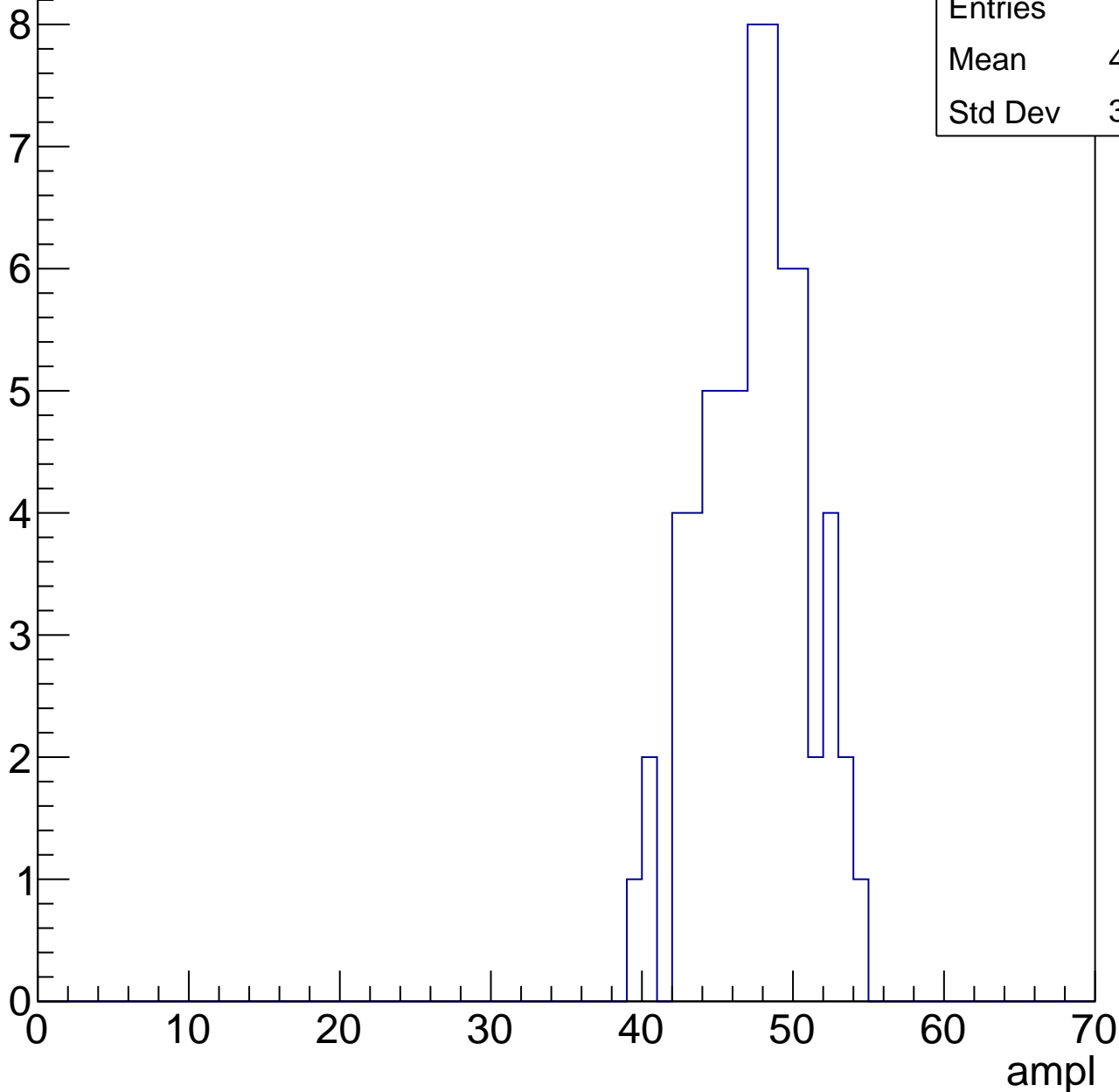


# B1L000S, U21-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	46.95
Std Dev	3.415

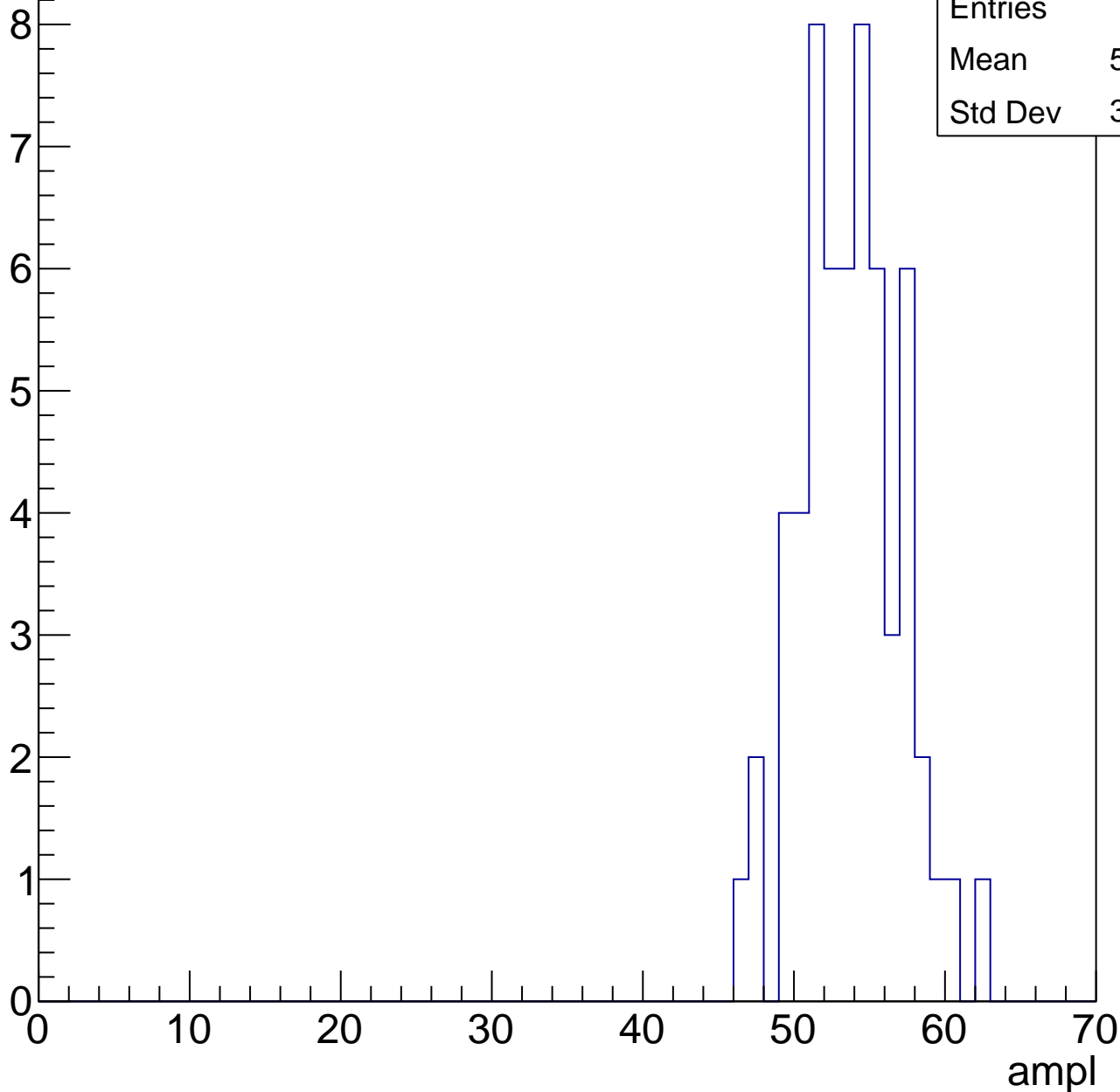


# B1L000S, U21-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	53.27
Std Dev	3.256



# B1L000S, U21-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

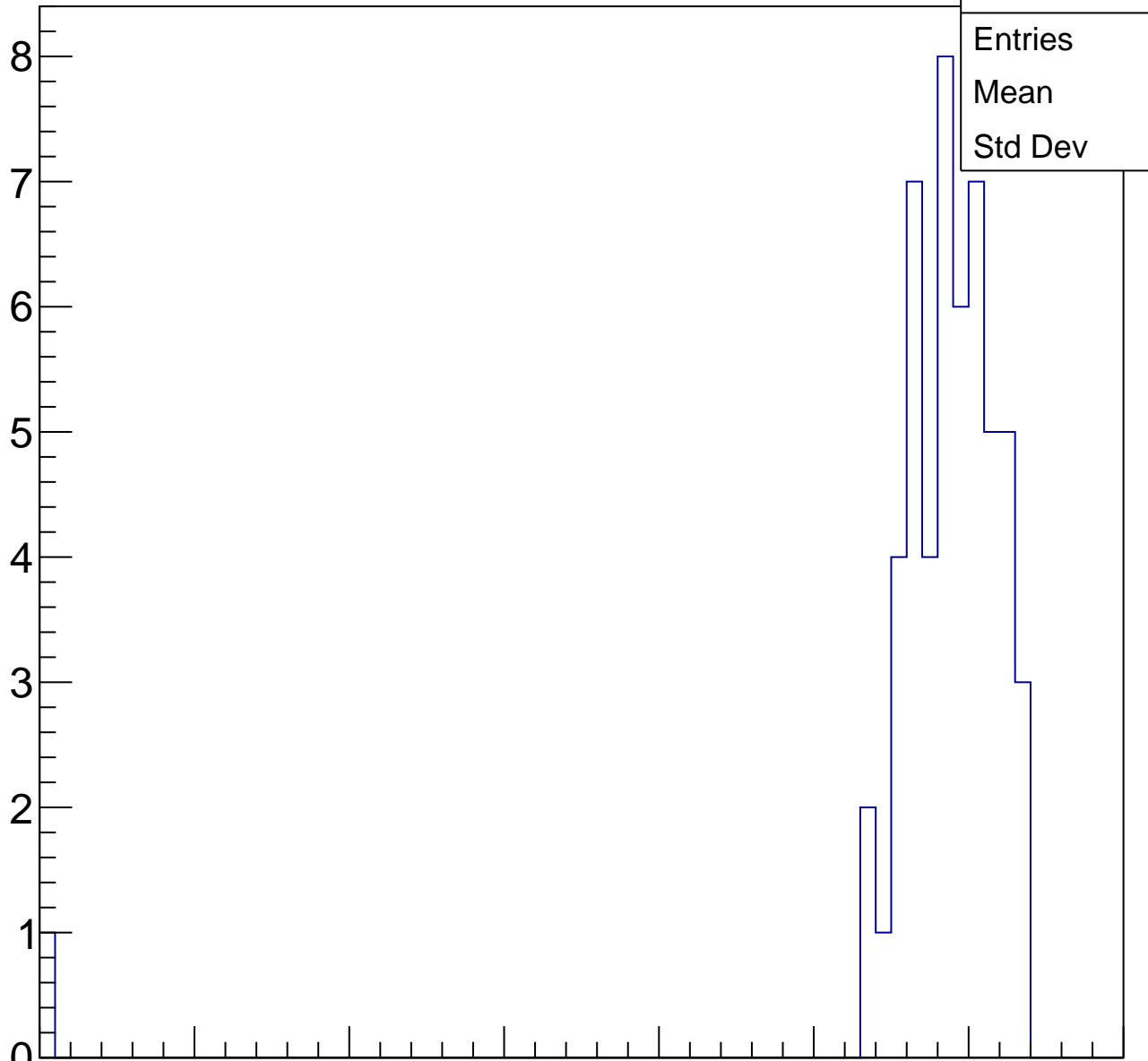
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	57.4
Std Dev	8.37

ampl

0 10 20 30 40 50 60 70

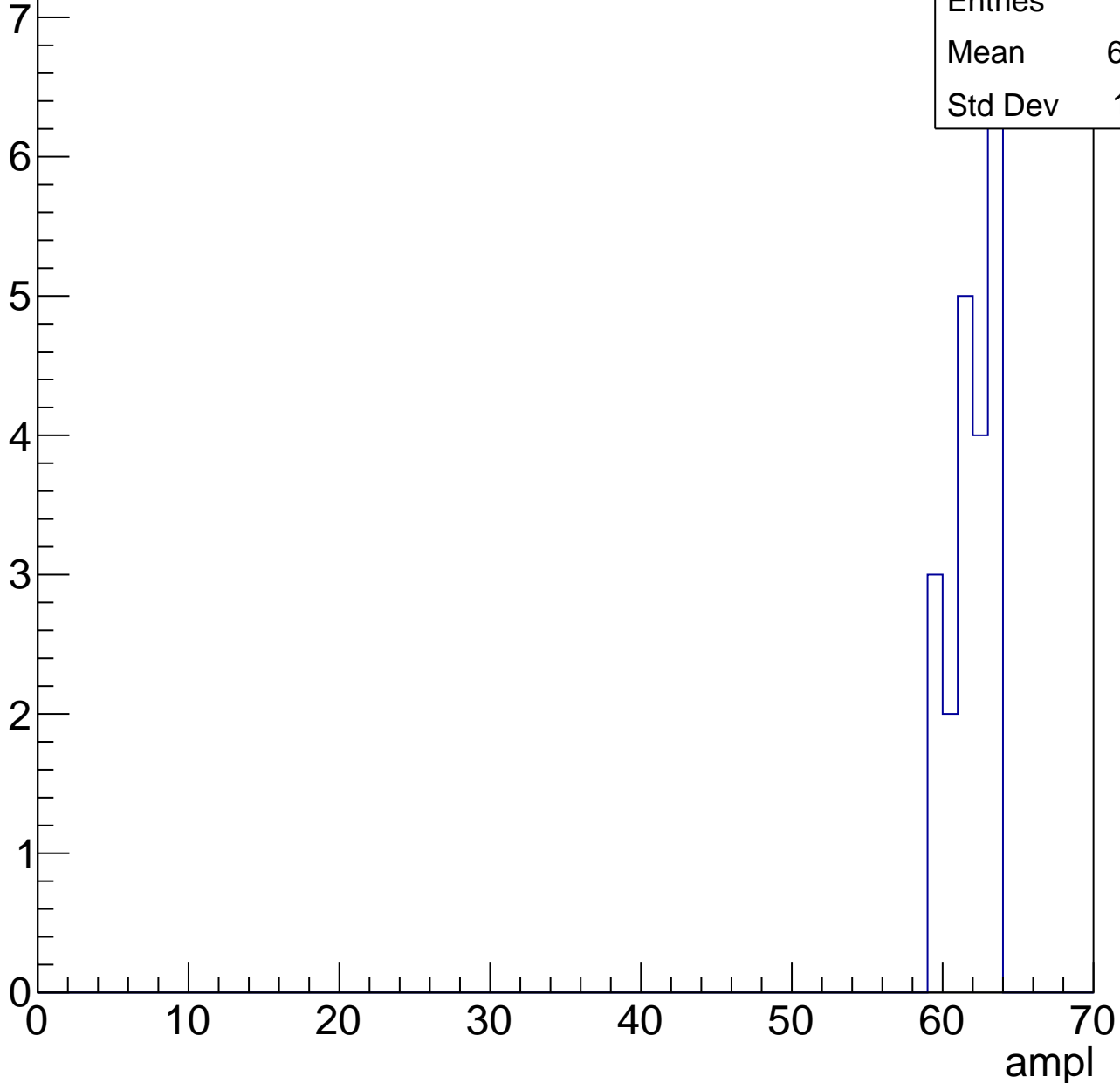


# B1L000S, U21-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	61.48
Std Dev	1.401





# B1L000S, U21-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch42, adc0

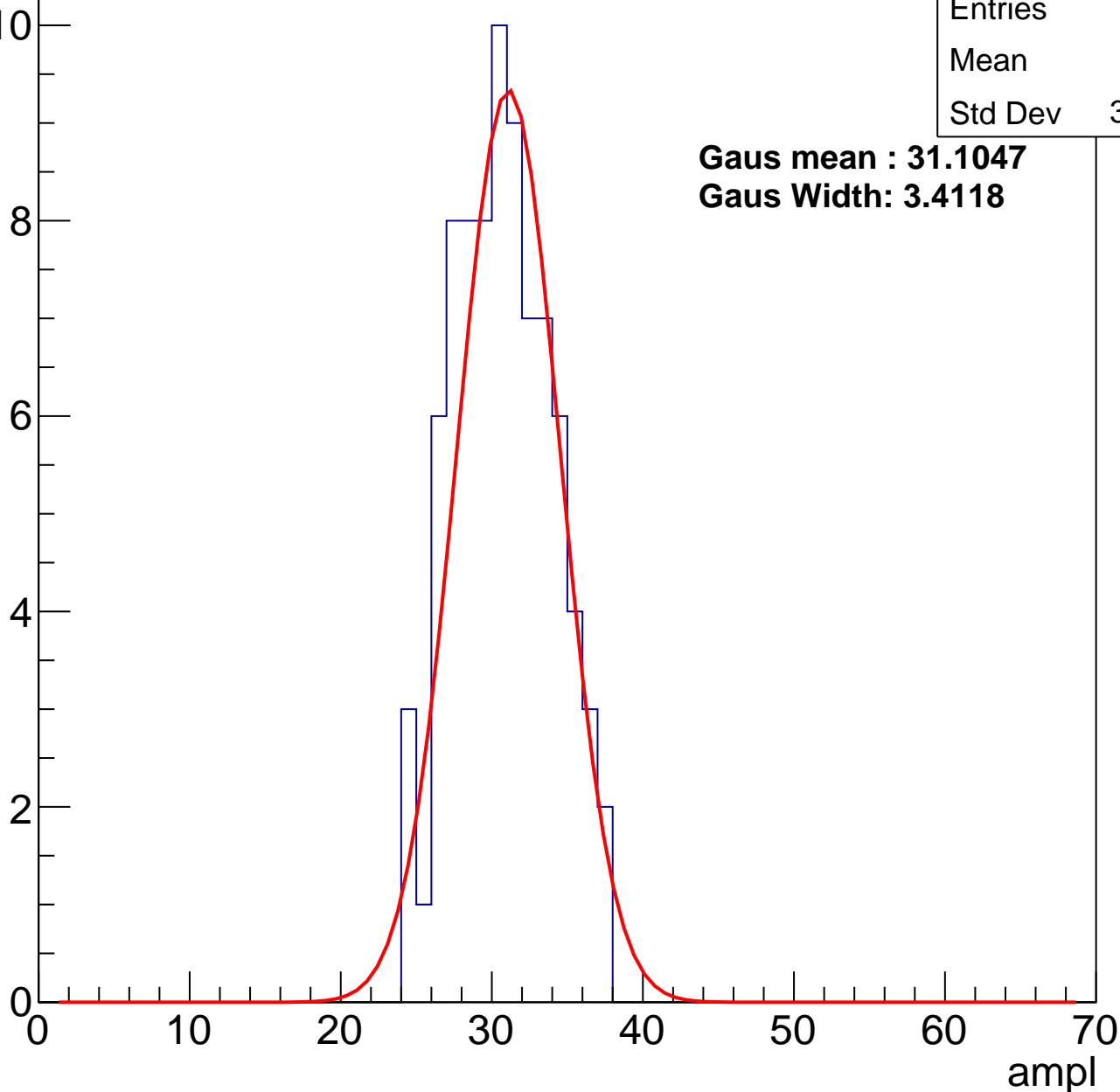
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	30.3
Std Dev	3.192

**Gaus mean : 31.1047**

**Gaus Width: 3.4118**



# B1L000S, U21-ch42, adc1

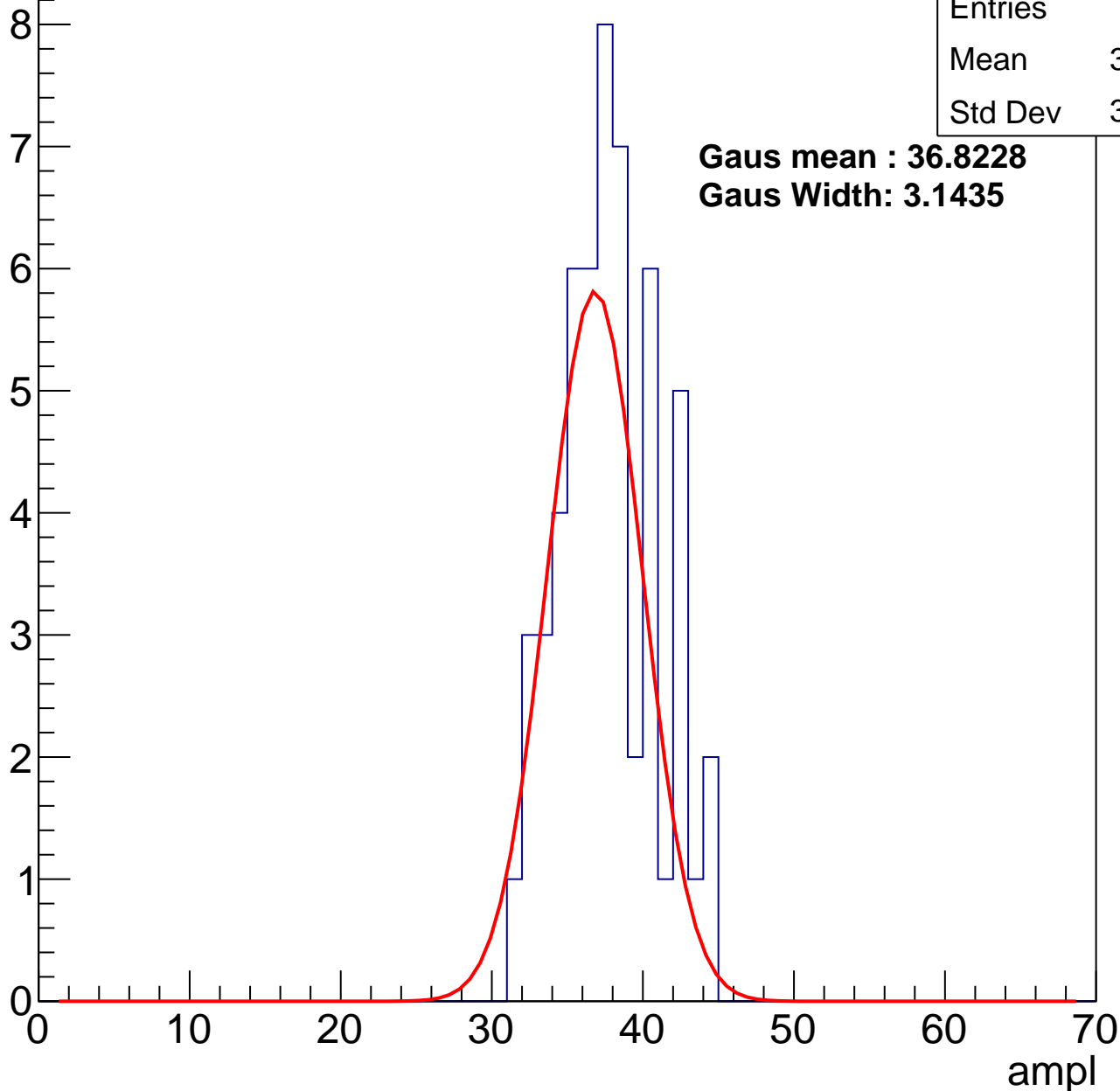
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	37.27
Std Dev	3.205

**Gaus mean : 36.8228**

**Gaus Width: 3.1435**



# B1L000S, U21-ch42, adc2

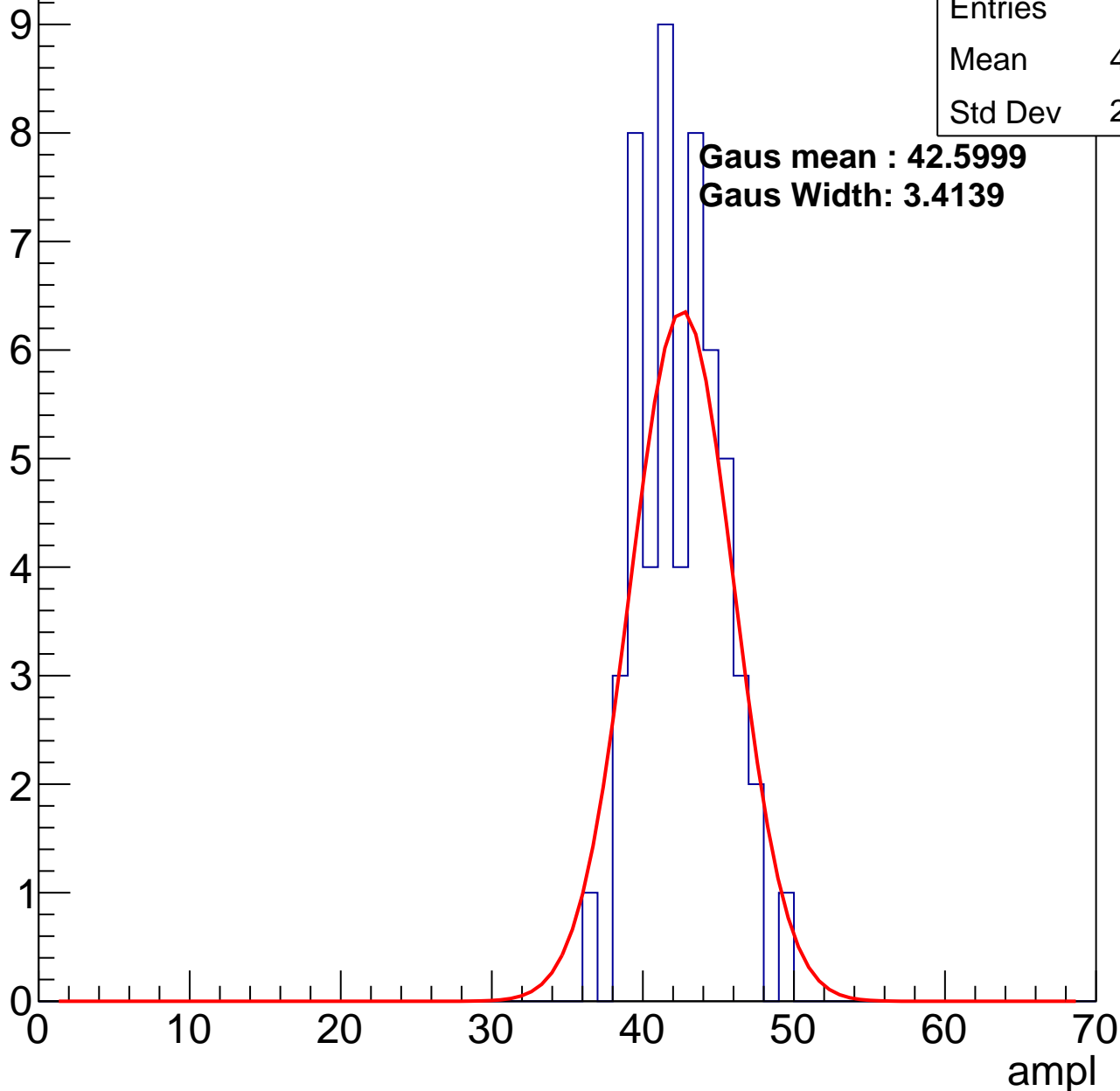
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.09
Std Dev	2.737

**Gaus mean : 42.5999**

**Gaus Width: 3.4139**



# B1L000S, U21-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

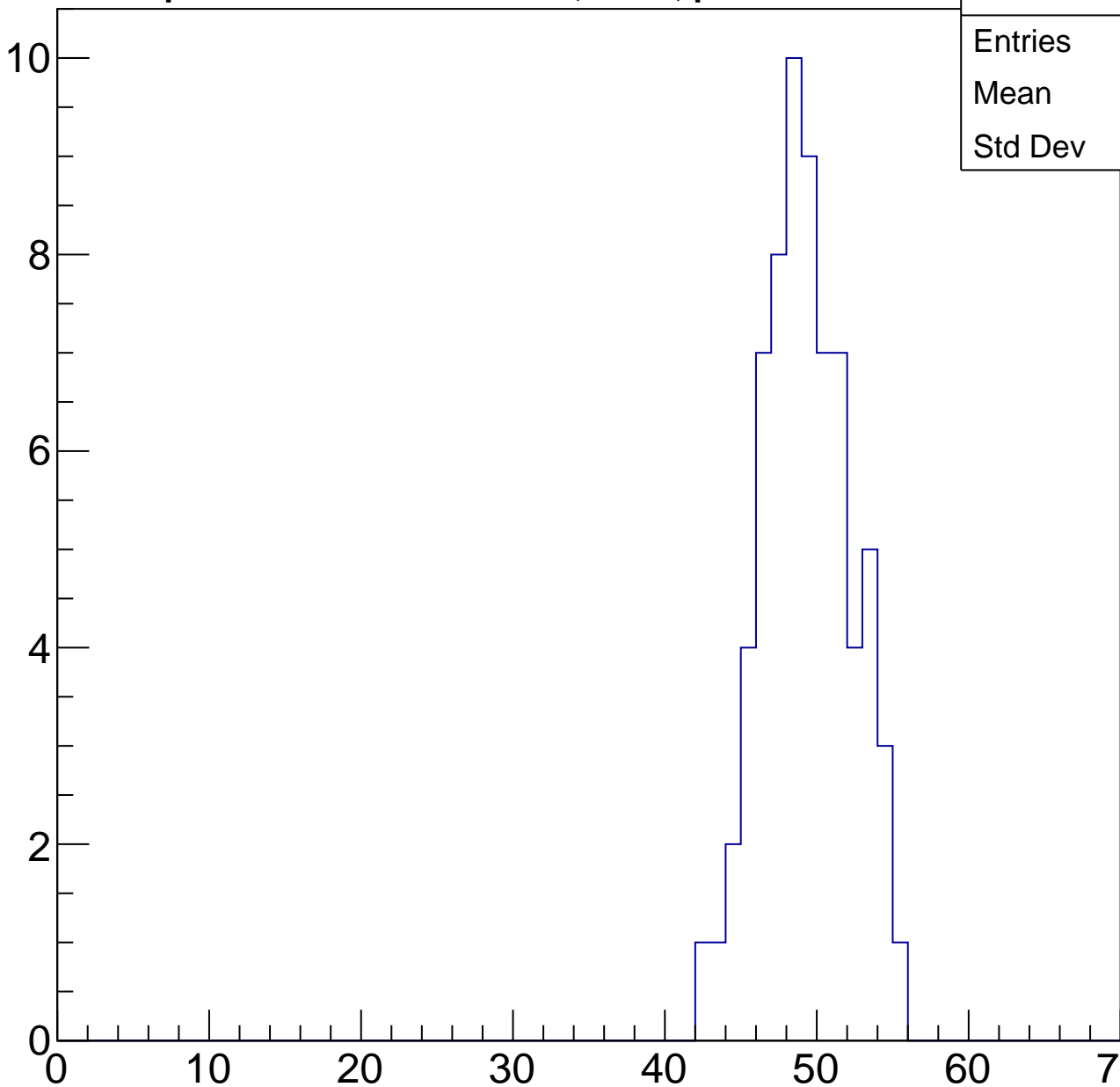
Entries	69
Mean	48.83
Std Dev	2.859

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

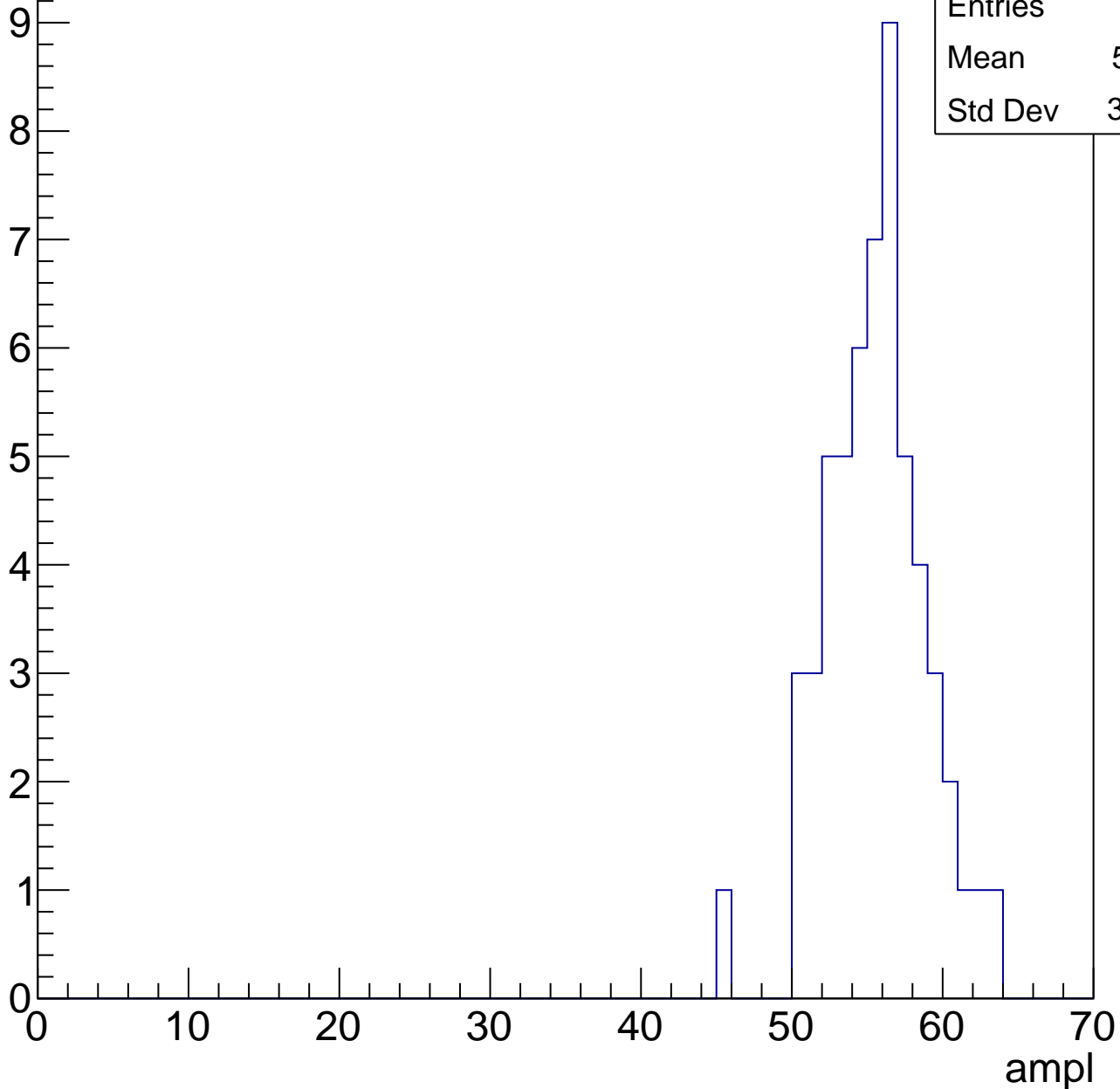


# B1L000S, U21-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	55.11
Std Dev	3.288

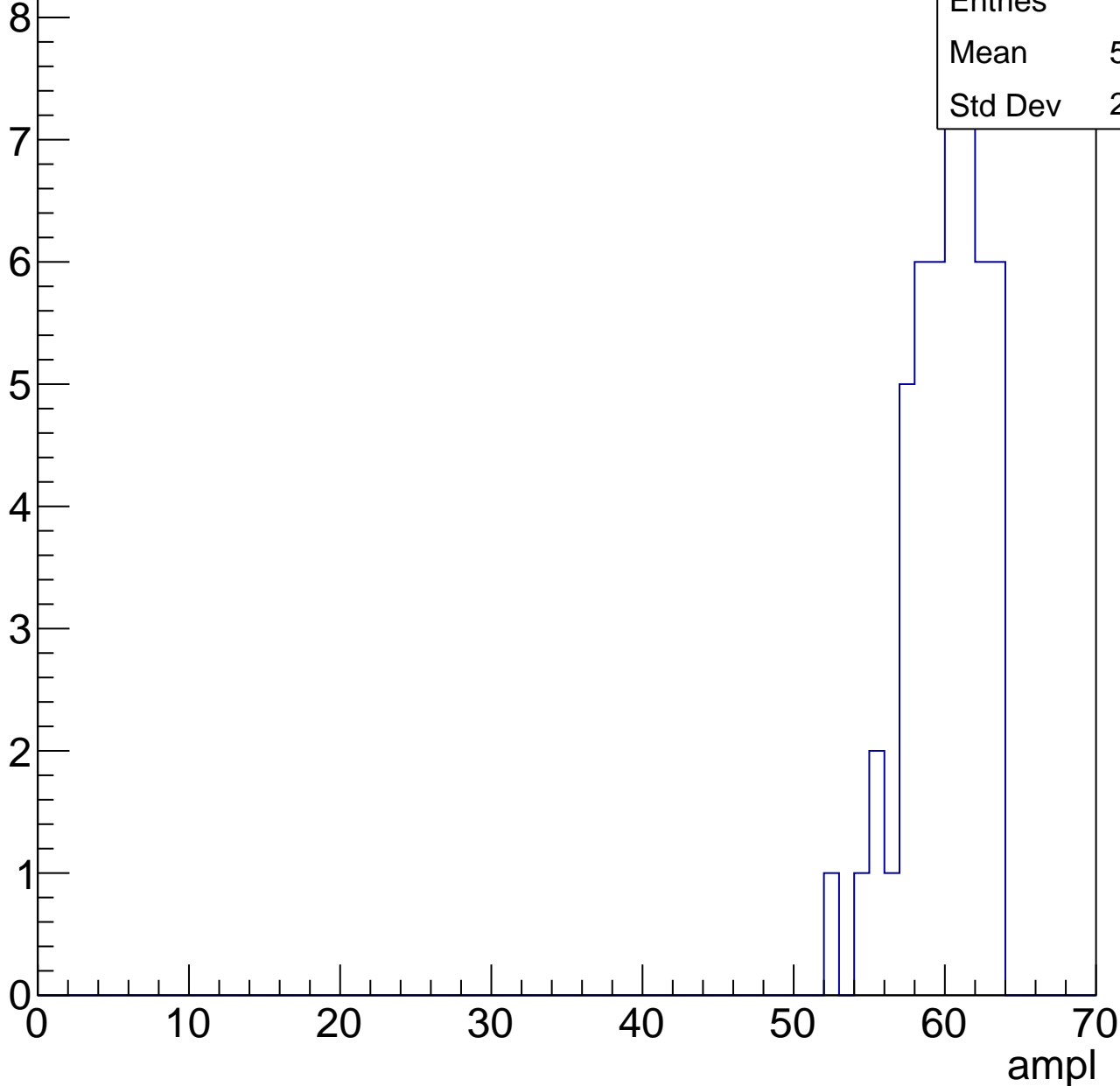


# B1L000S, U21-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

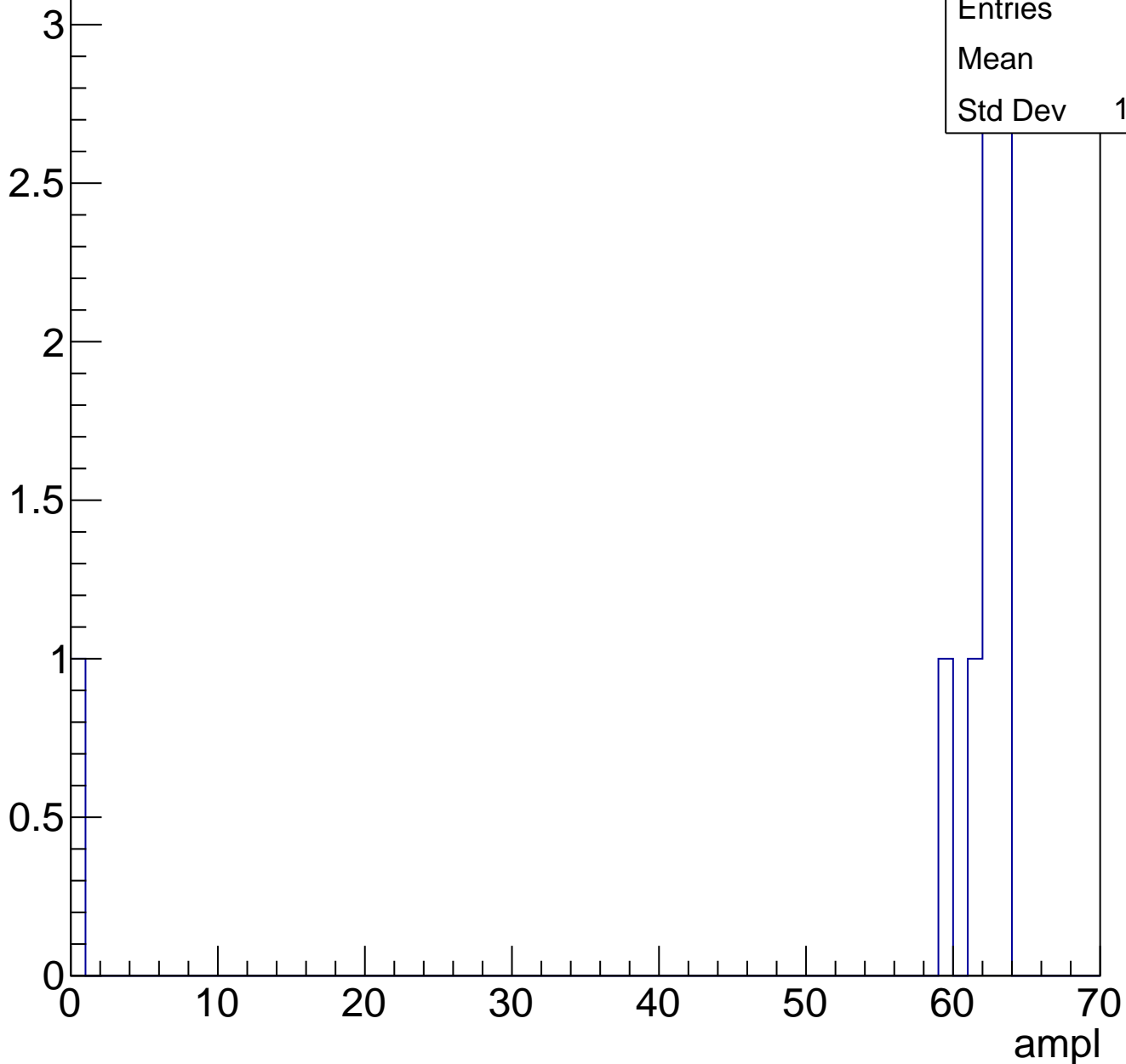
Entries	50
Mean	59.54
Std Dev	2.516



# B1L000S, U21-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch43, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	31.46
Std Dev	3.249

**Gaus mean : 31.3550**

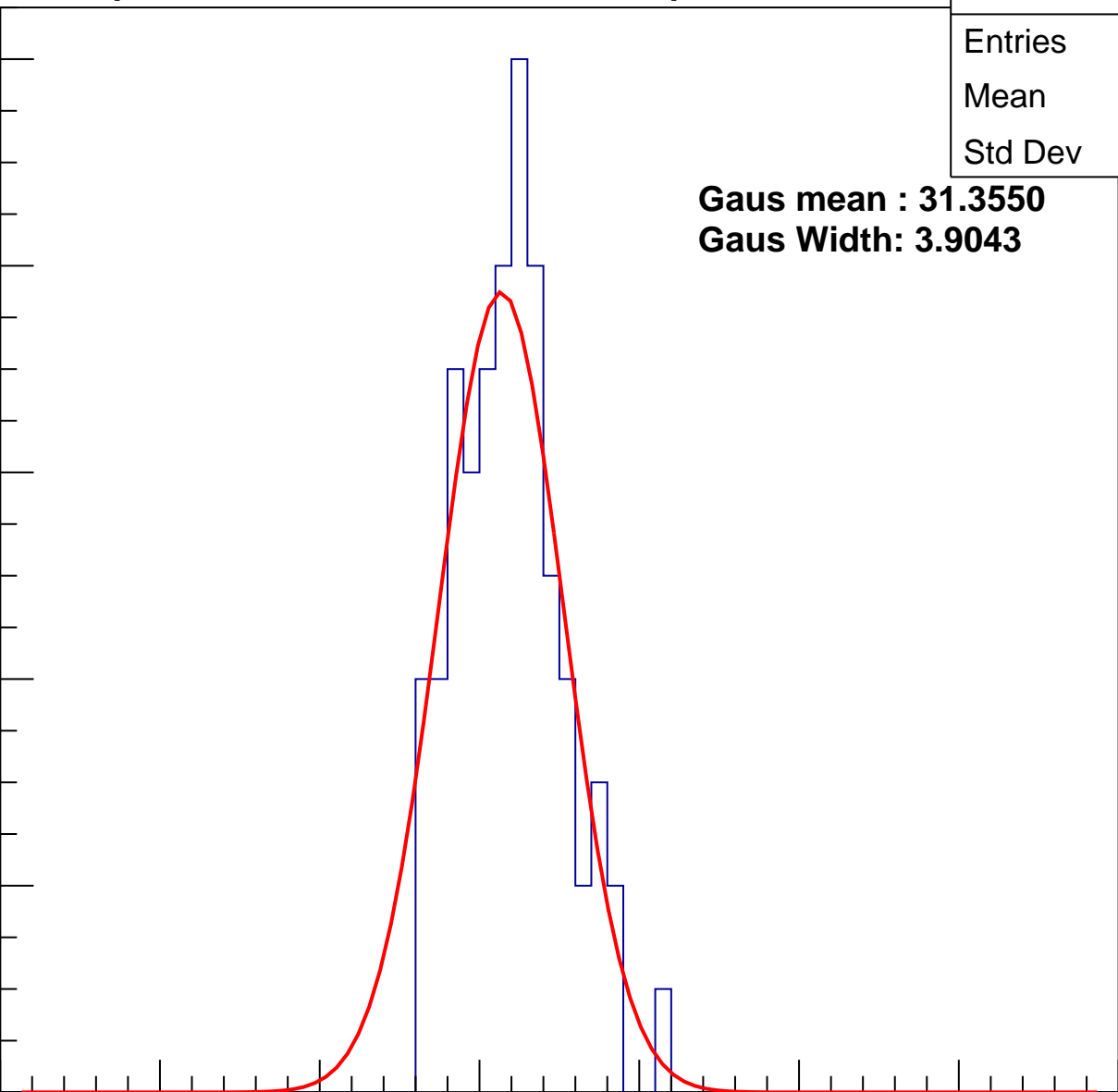
**Gaus Width: 3.9043**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	38.32
Std Dev	3.041

**Gaus mean : 38.7089**

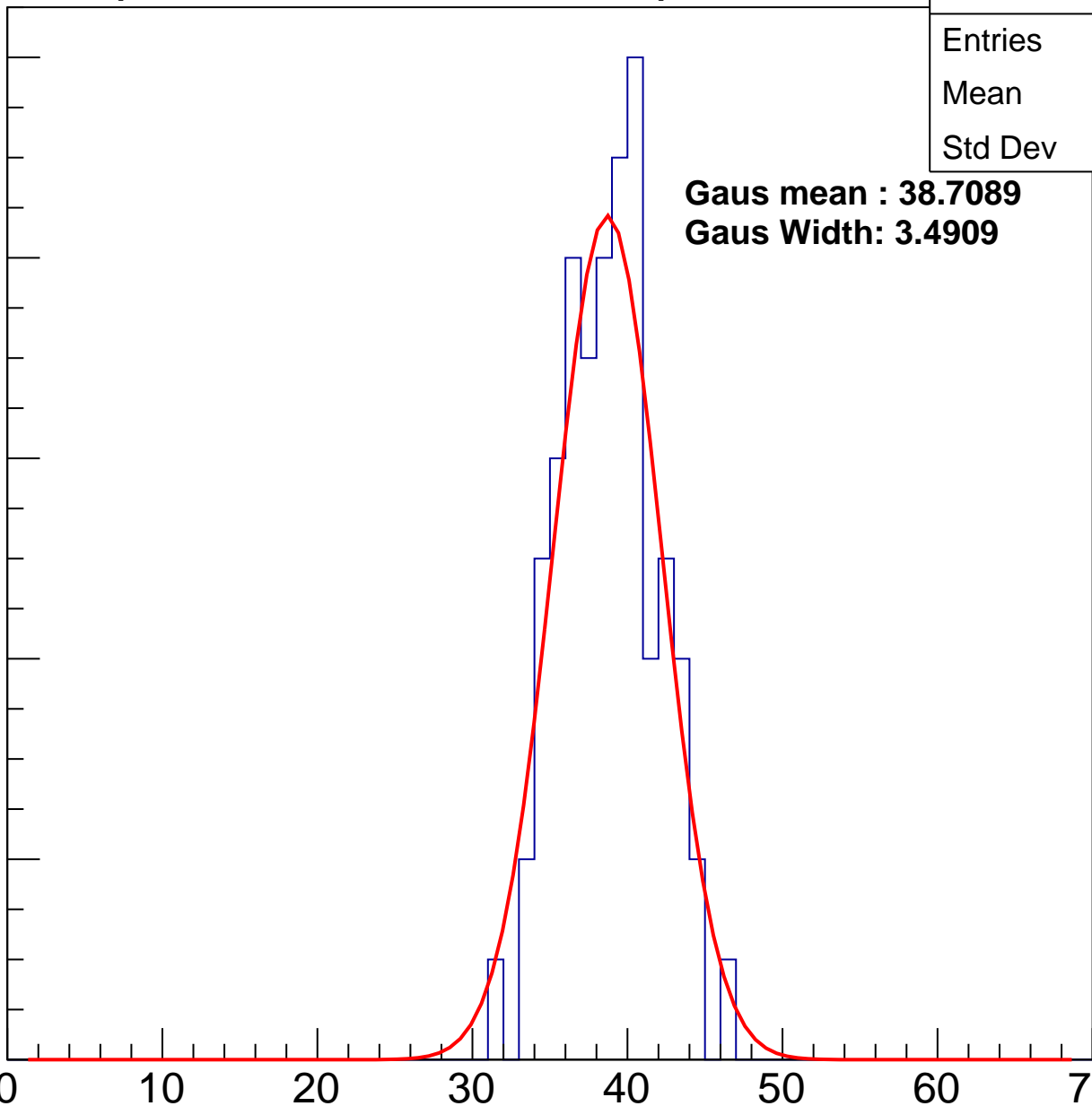
**Gaus Width: 3.4909**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

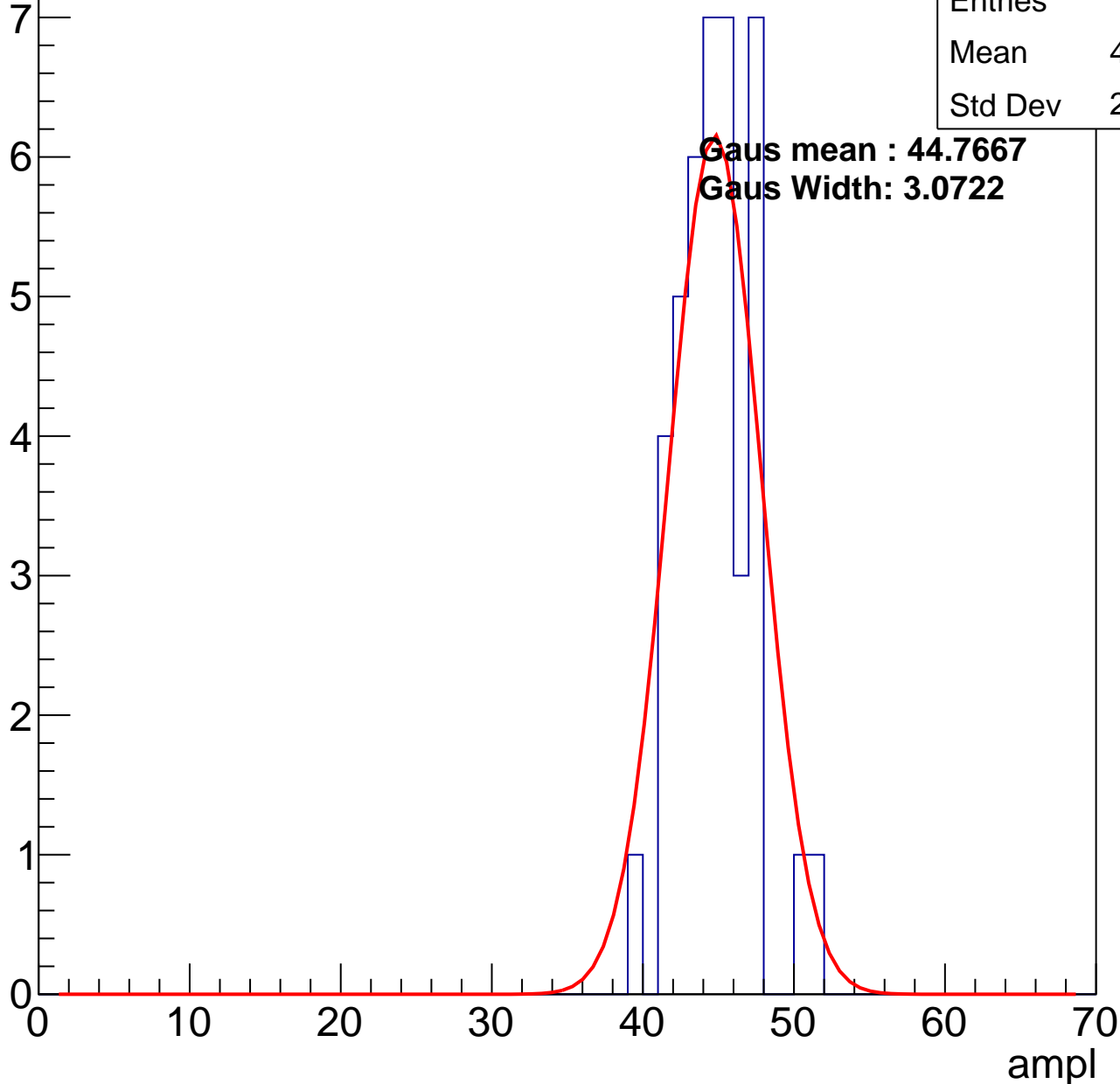
ampl



# B1L000S, U21-ch43, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

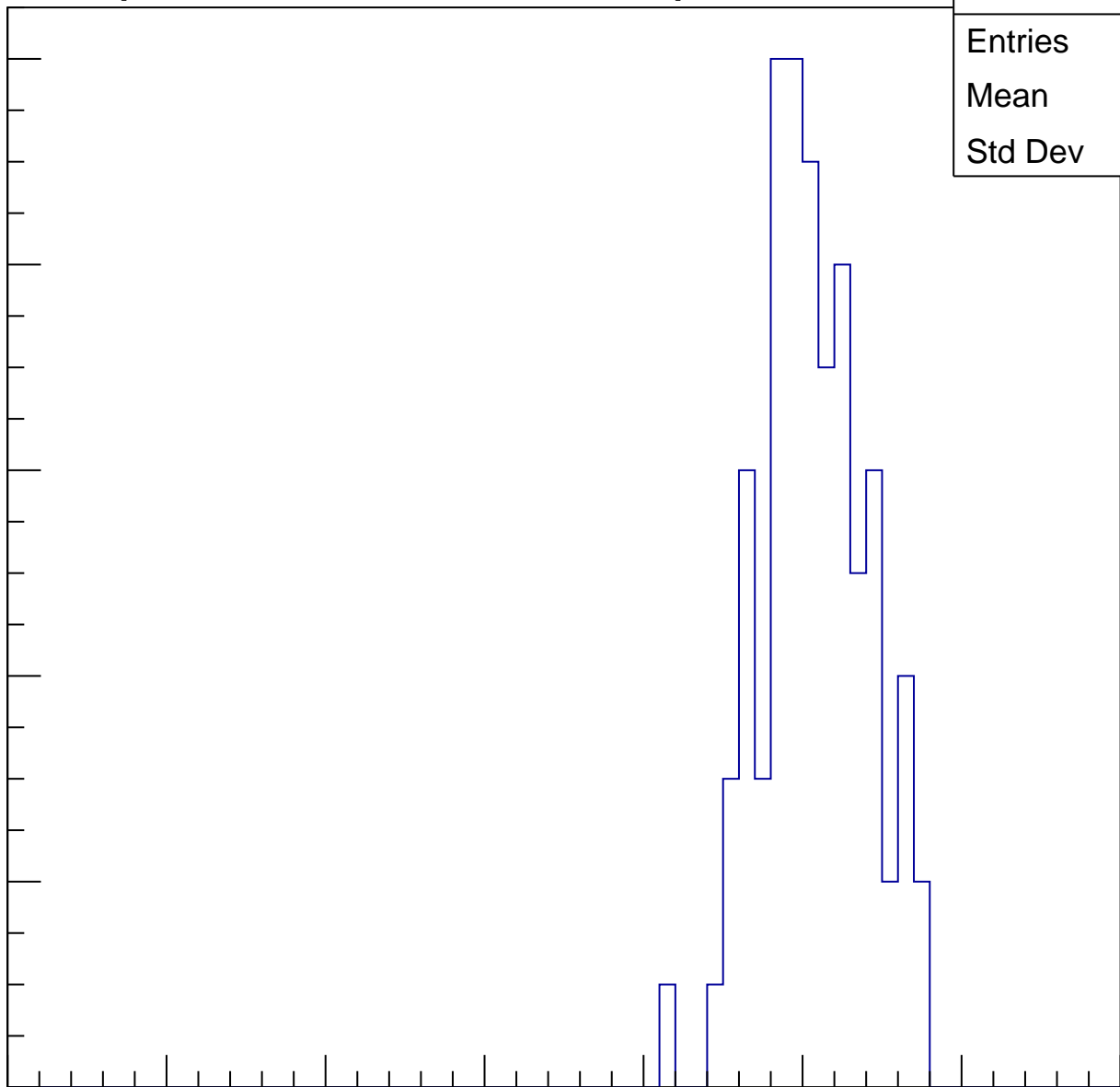
Entries	77
Mean	50.22
Std Dev	3.29

Entry

10  
8  
6  
4  
2  
0

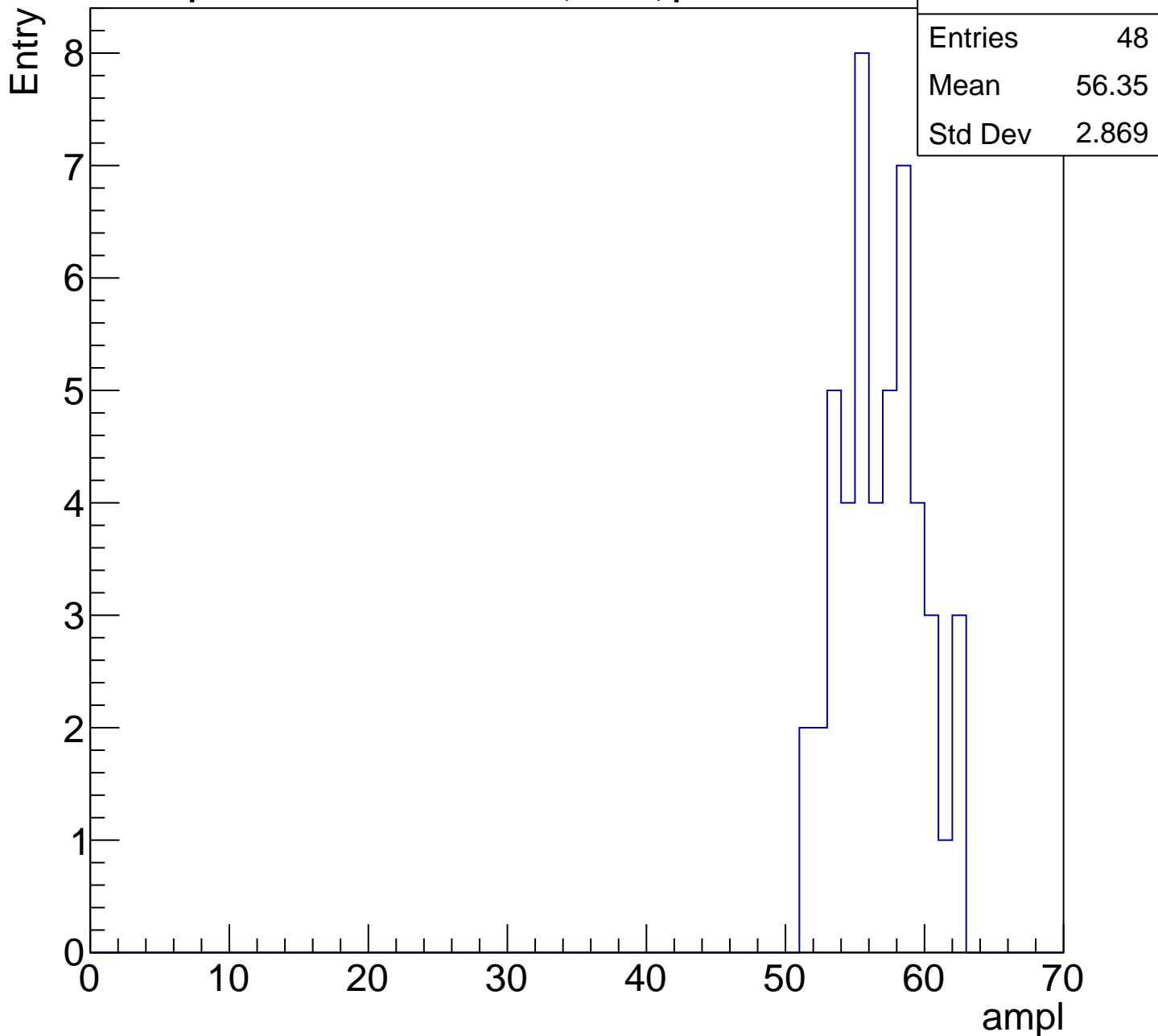
0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch43, adc4

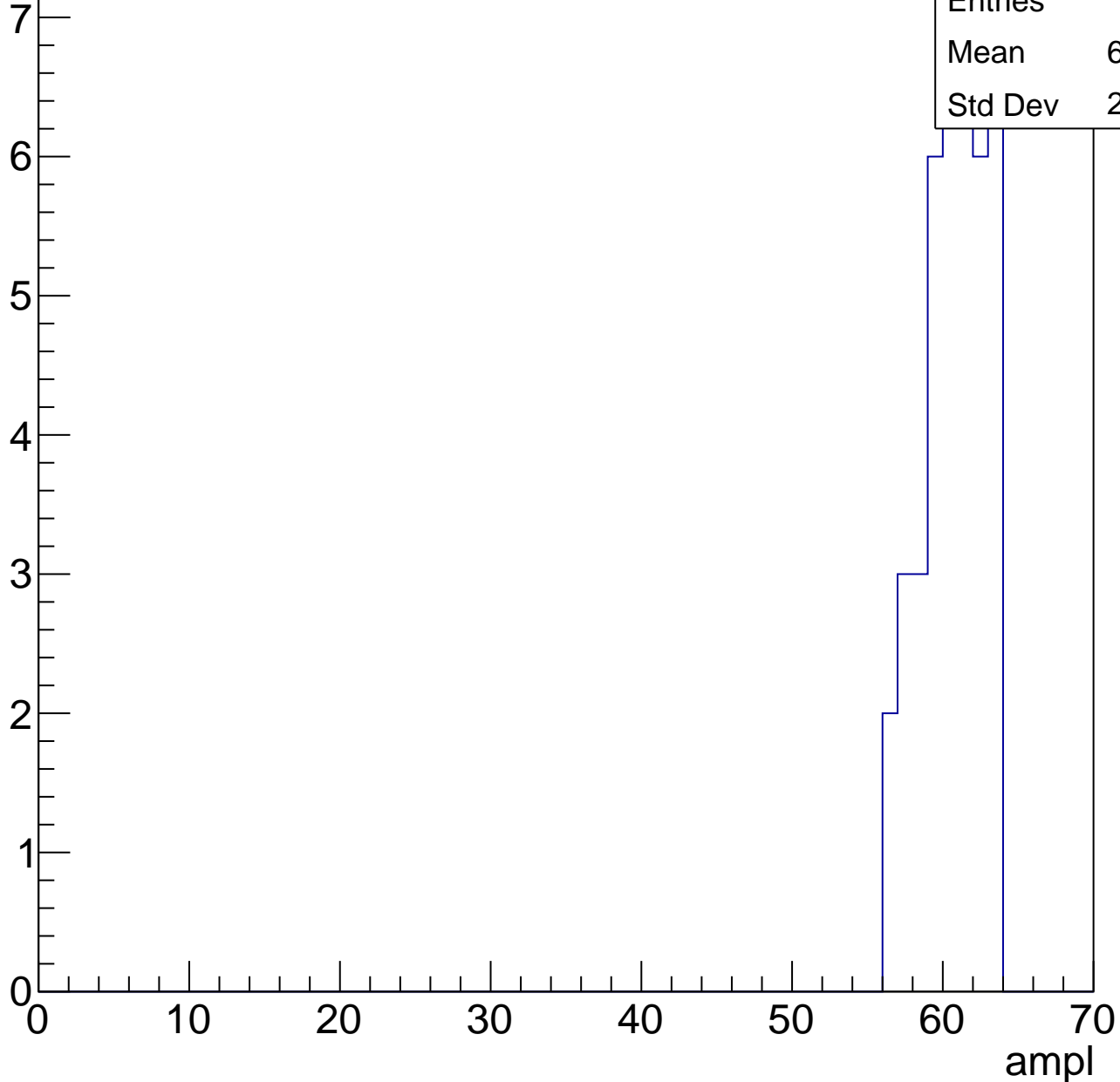
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

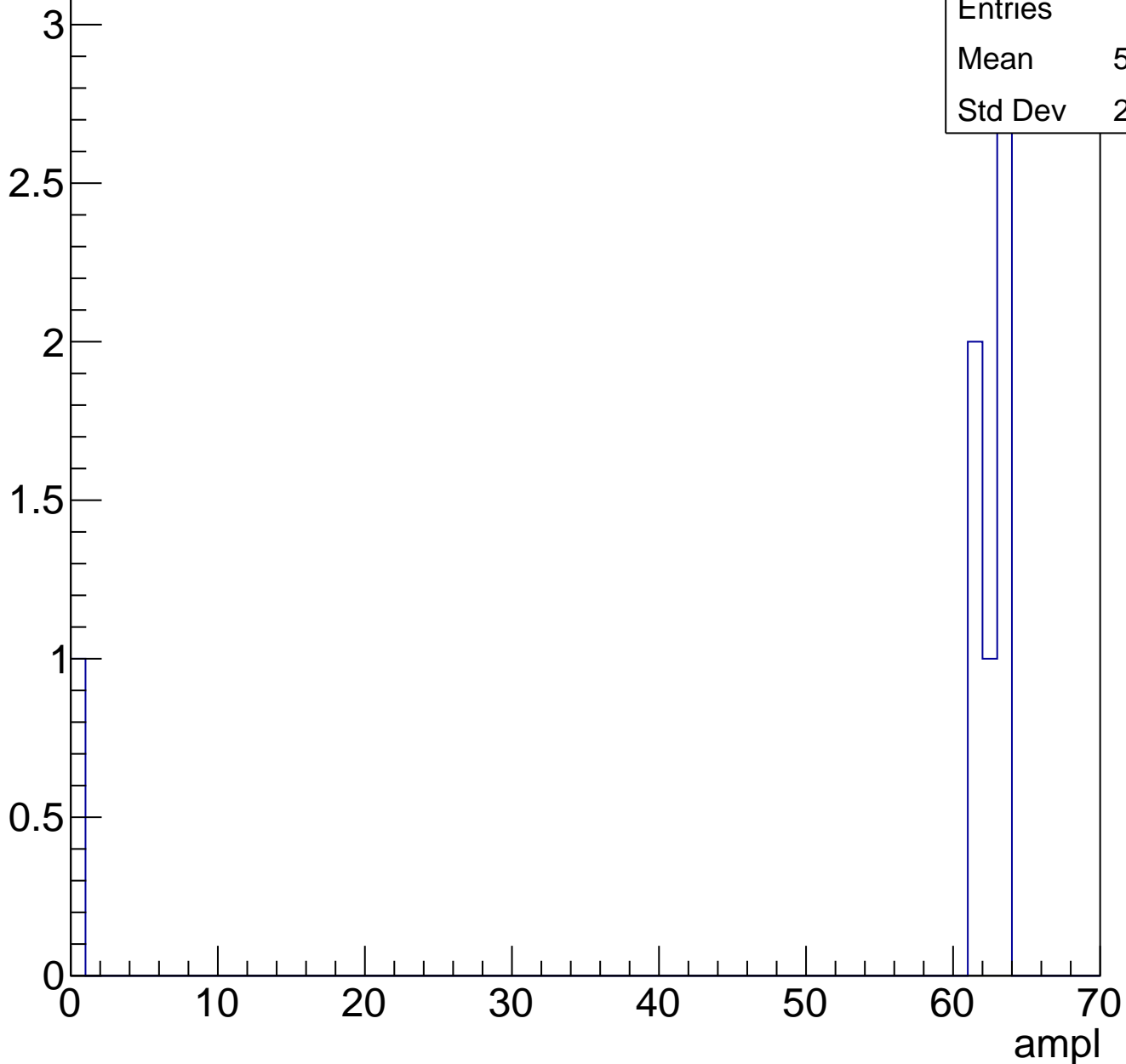


Entries	41
Mean	60.27
Std Dev	2.025

# B1L000S, U21-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

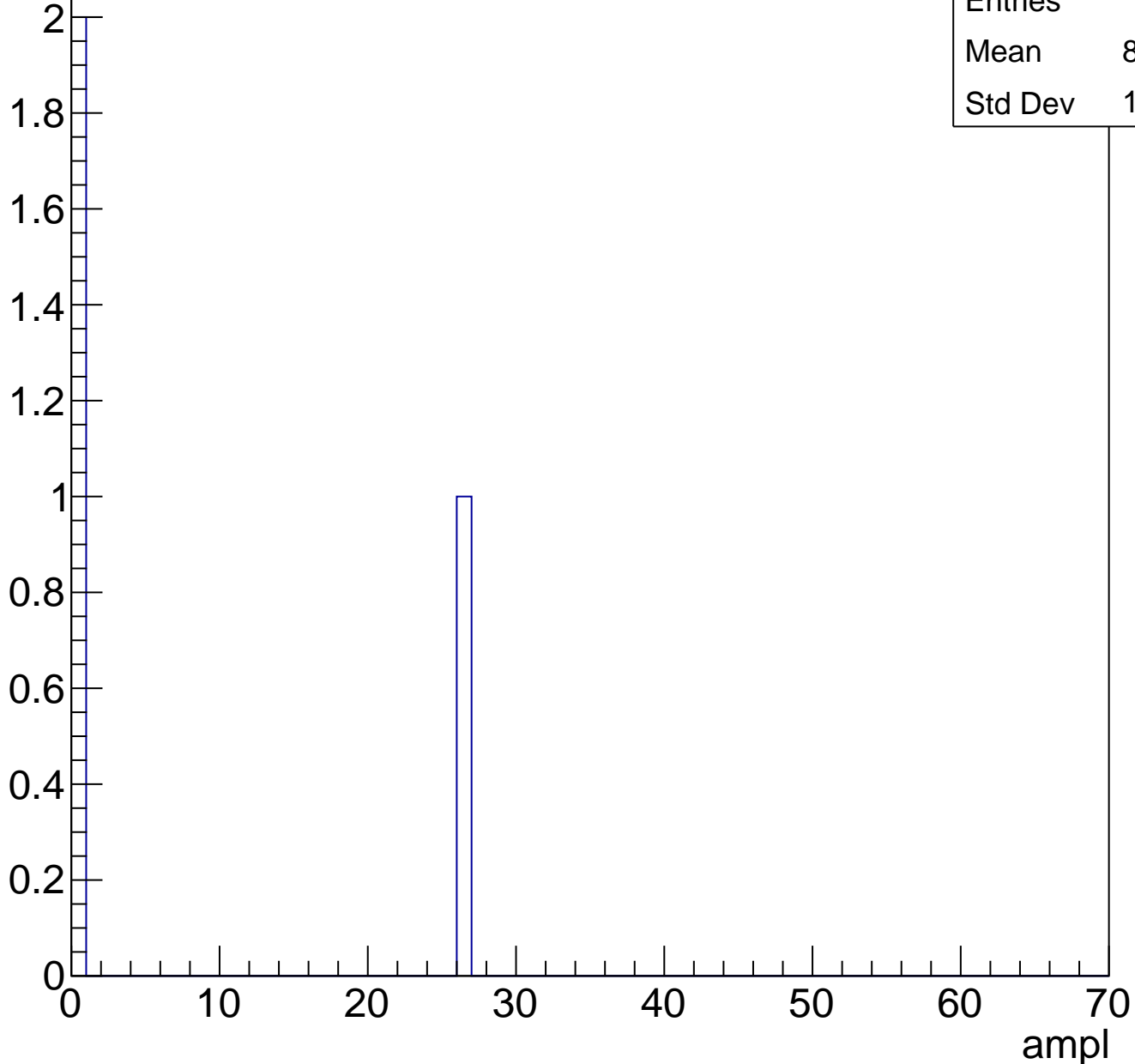




# B1L000S, U21-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	8.667
Std Dev	12.26

# B1L000S, U21-ch44, adc0

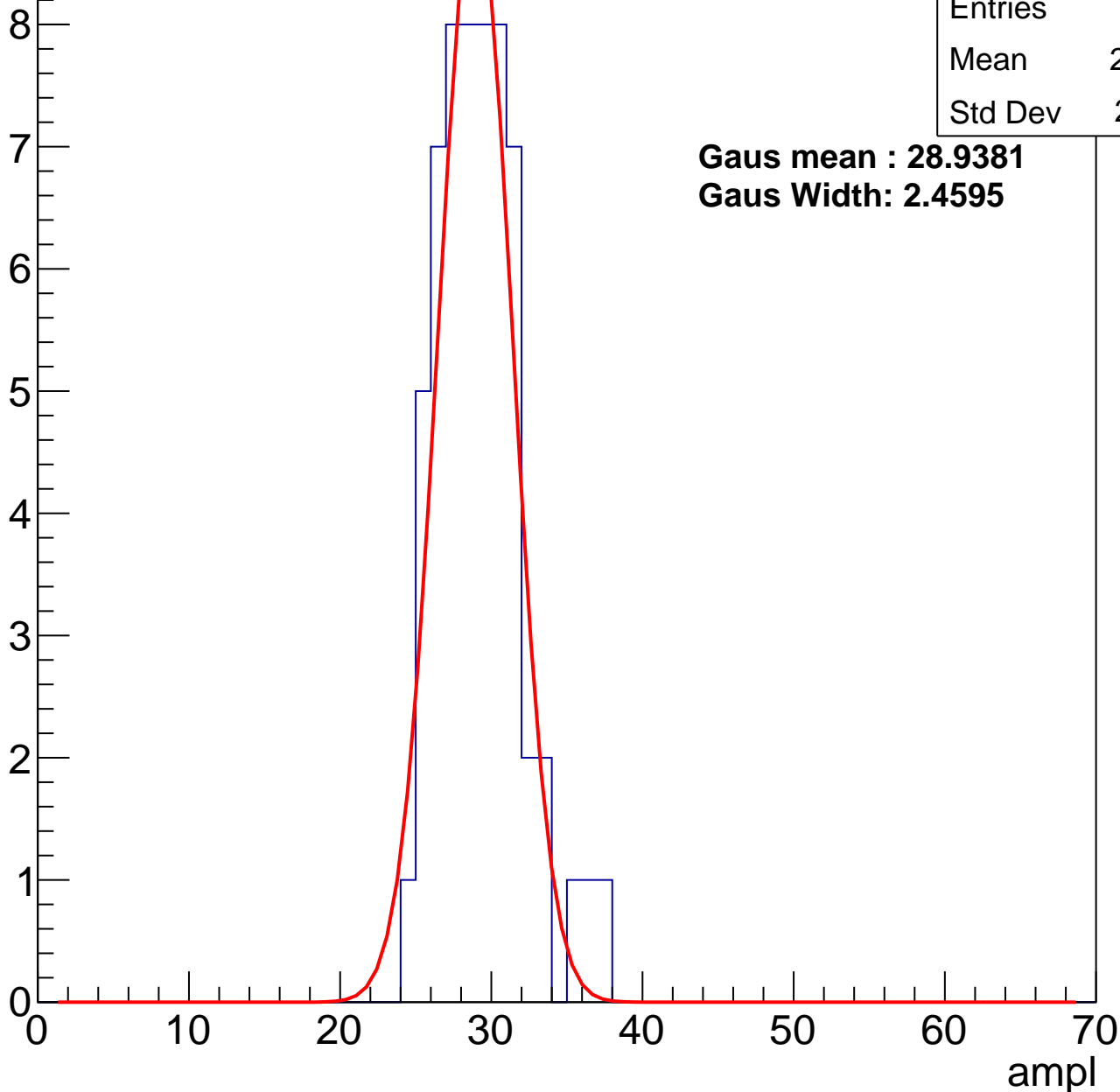
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	28.78
Std Dev	2.731

**Gaus mean : 28.9381**

**Gaus Width: 2.4595**



# B1L000S, U21-ch44, adc1

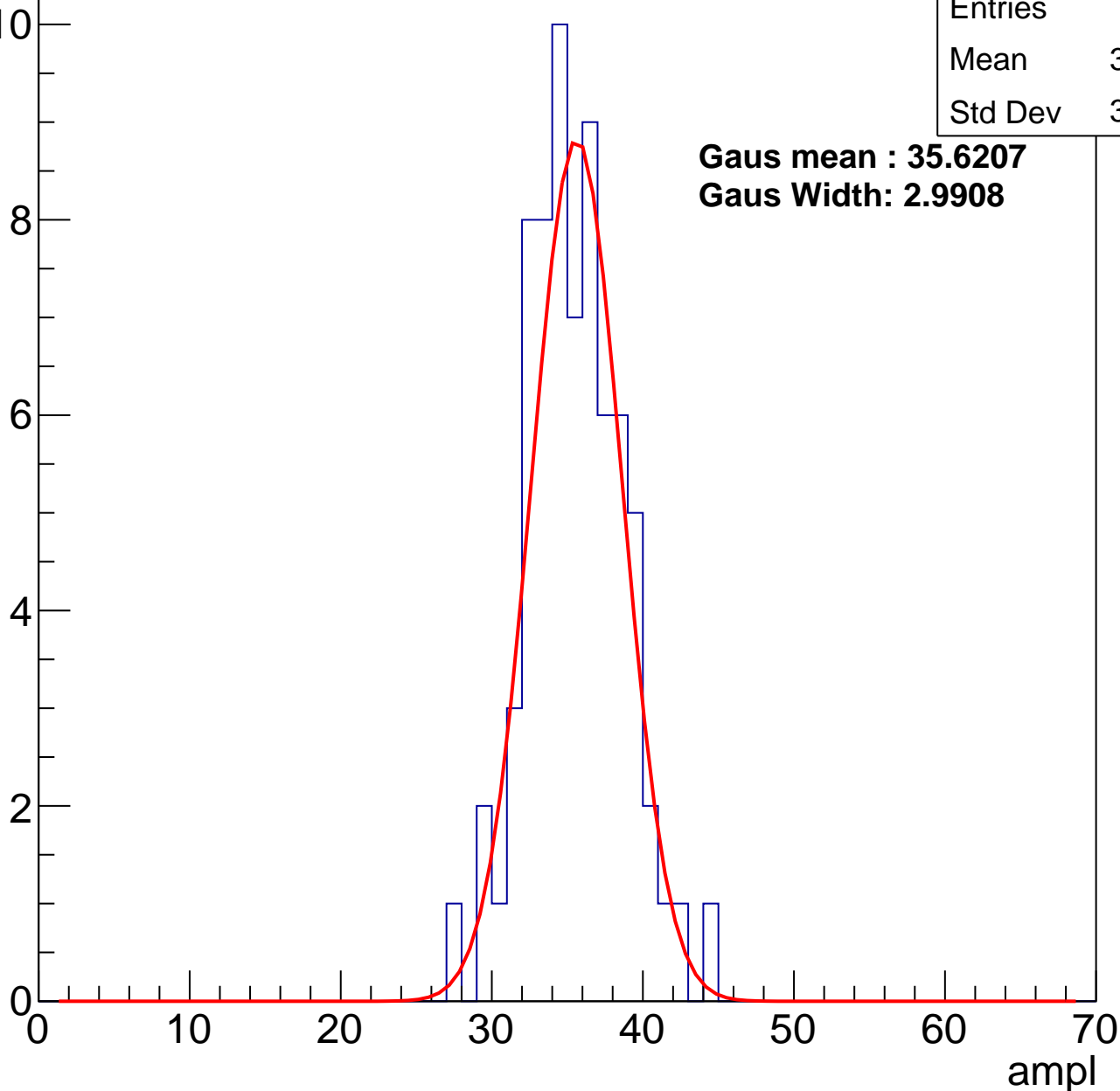
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.06
Std Dev	3.153

**Gaus mean : 35.6207**

**Gaus Width: 2.9908**



# B1L000S, U21-ch44, adc2

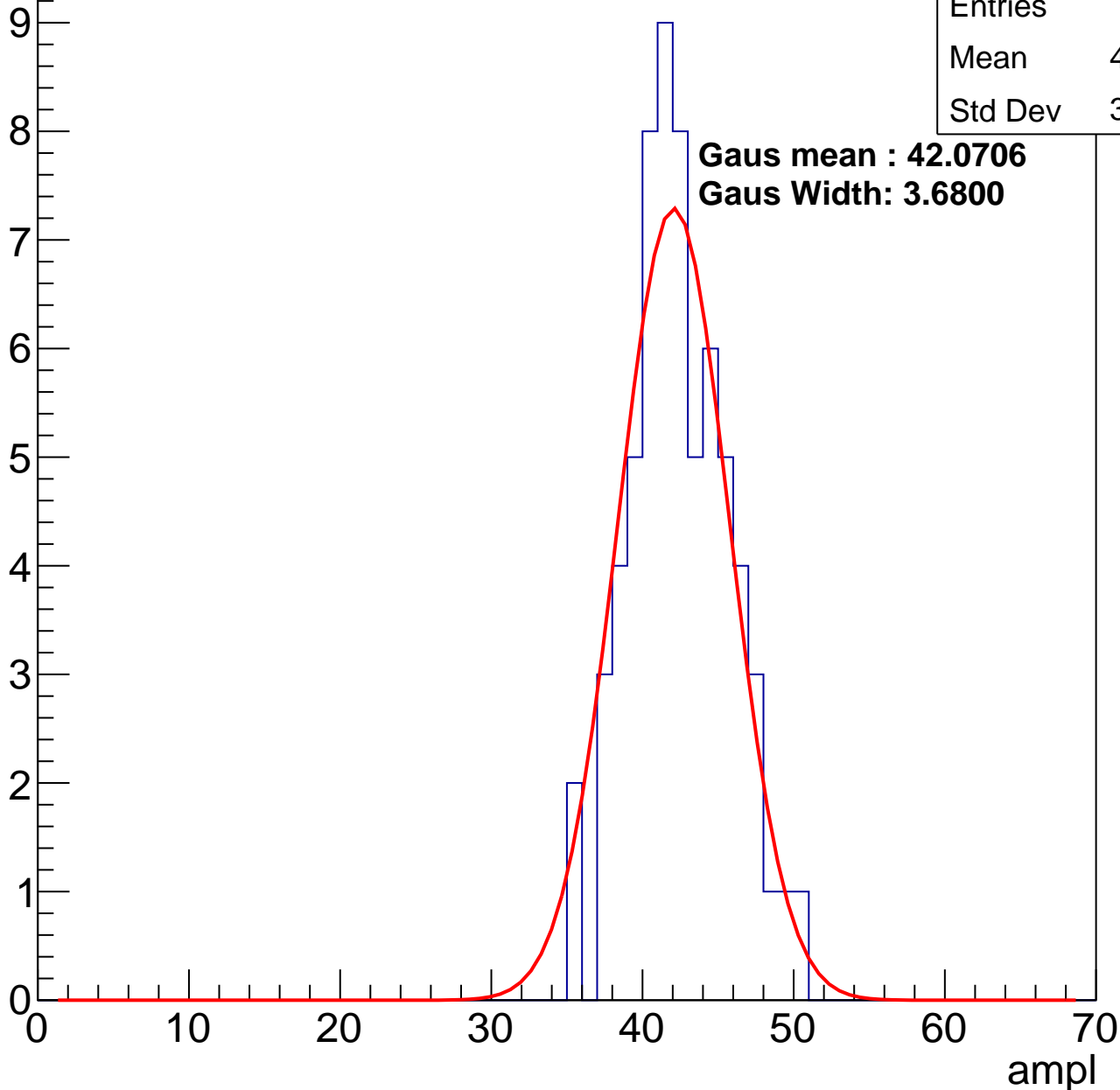
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.98
Std Dev	3.246

**Gaus mean : 42.0706**

**Gaus Width: 3.6800**

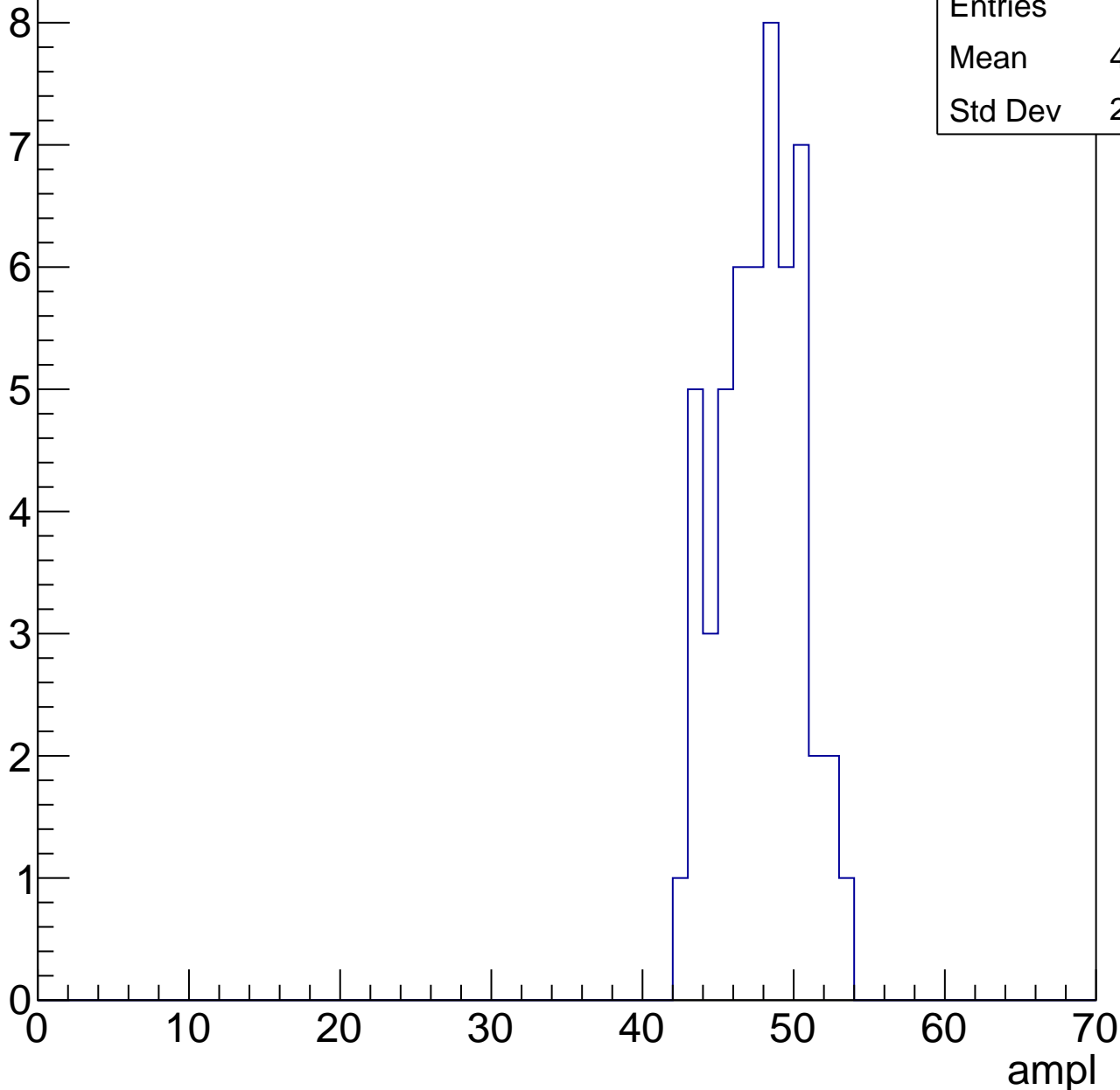


# B1L000S, U21-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

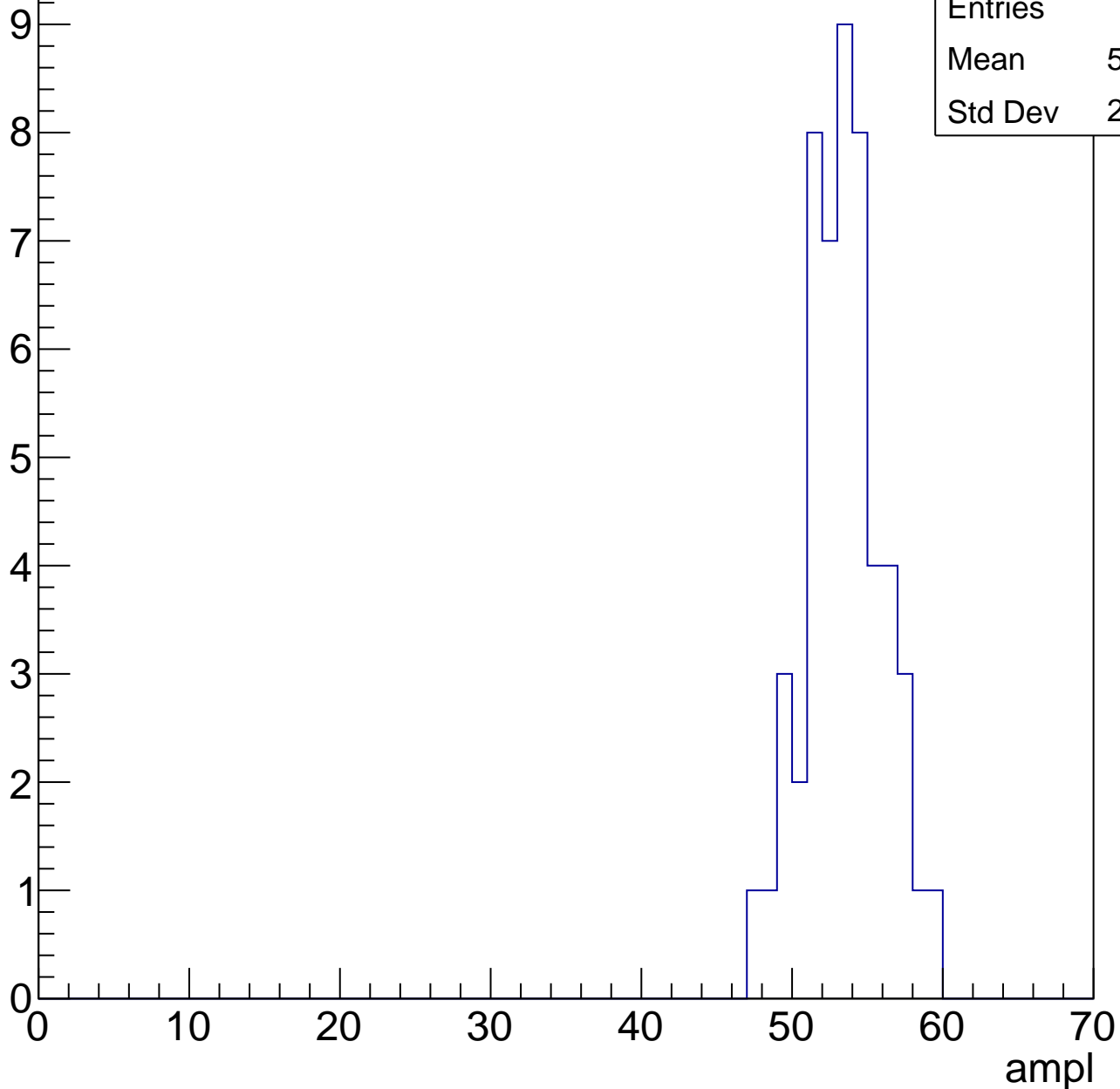
Entries	52
Mean	47.29
Std Dev	2.655



# B1L000S, U21-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

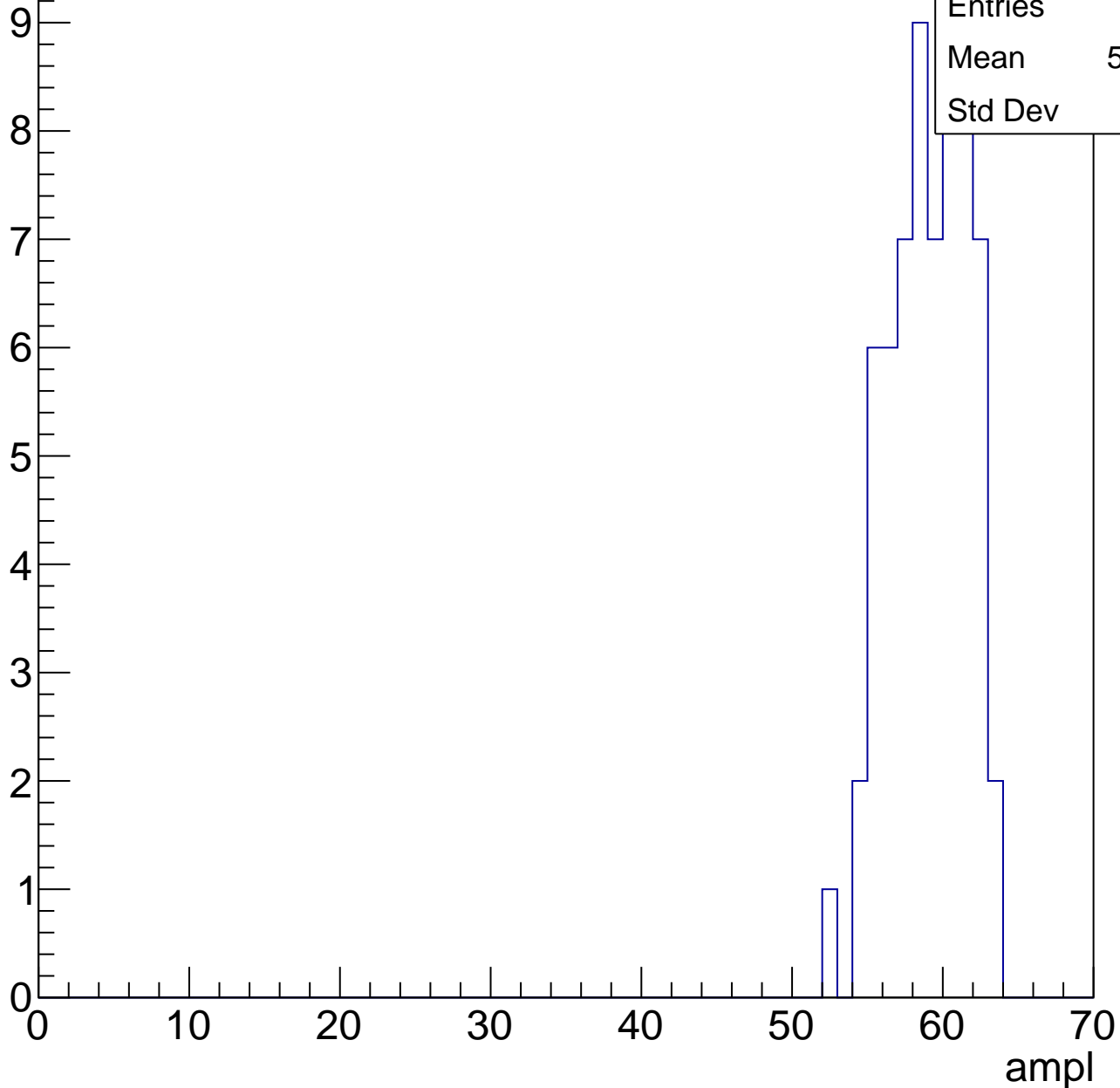


Entries	52
Mean	52.98
Std Dev	2.538

# B1L000S, U21-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

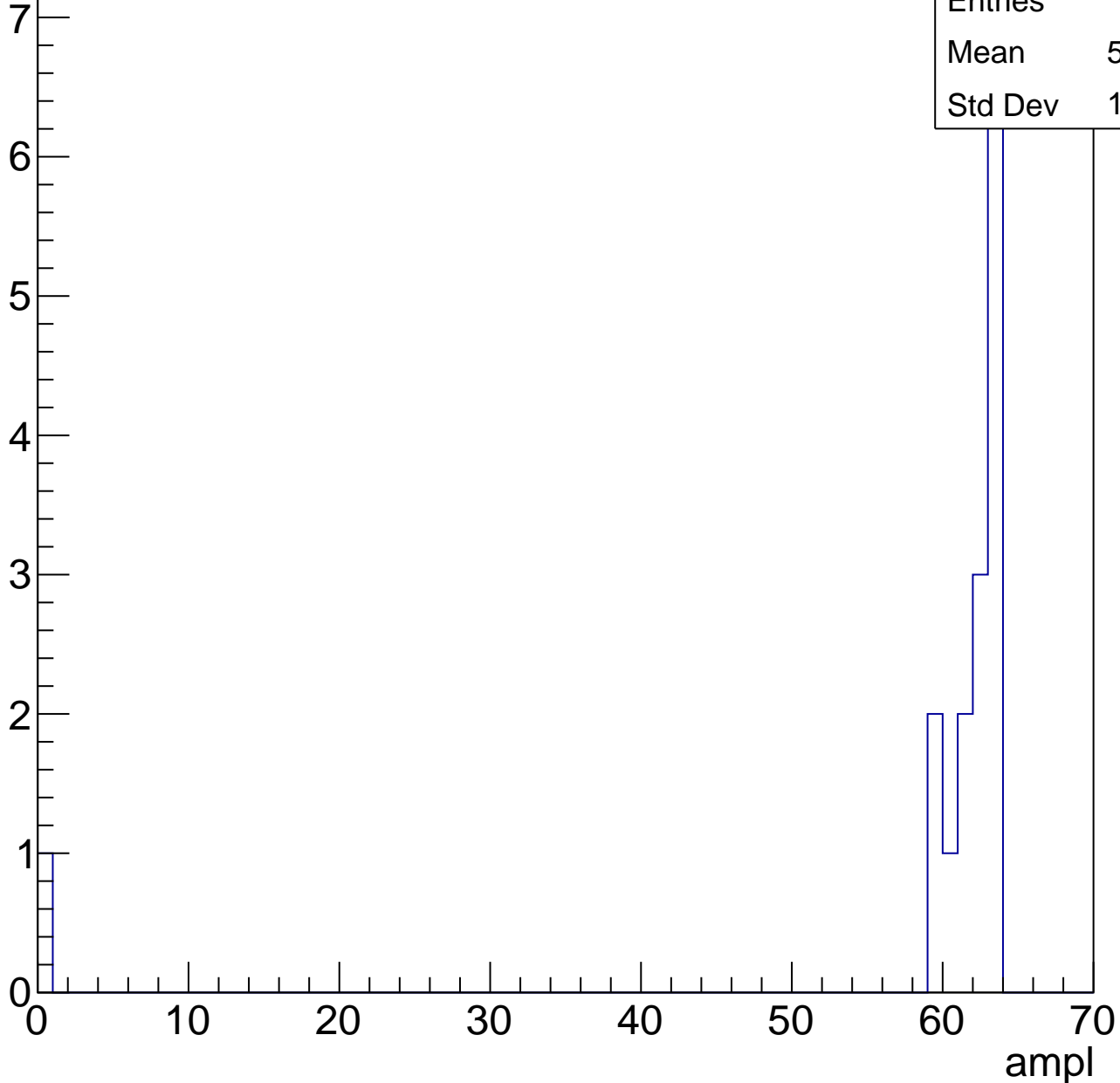


# B1L000S, U21-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.94
Std Dev	15.02

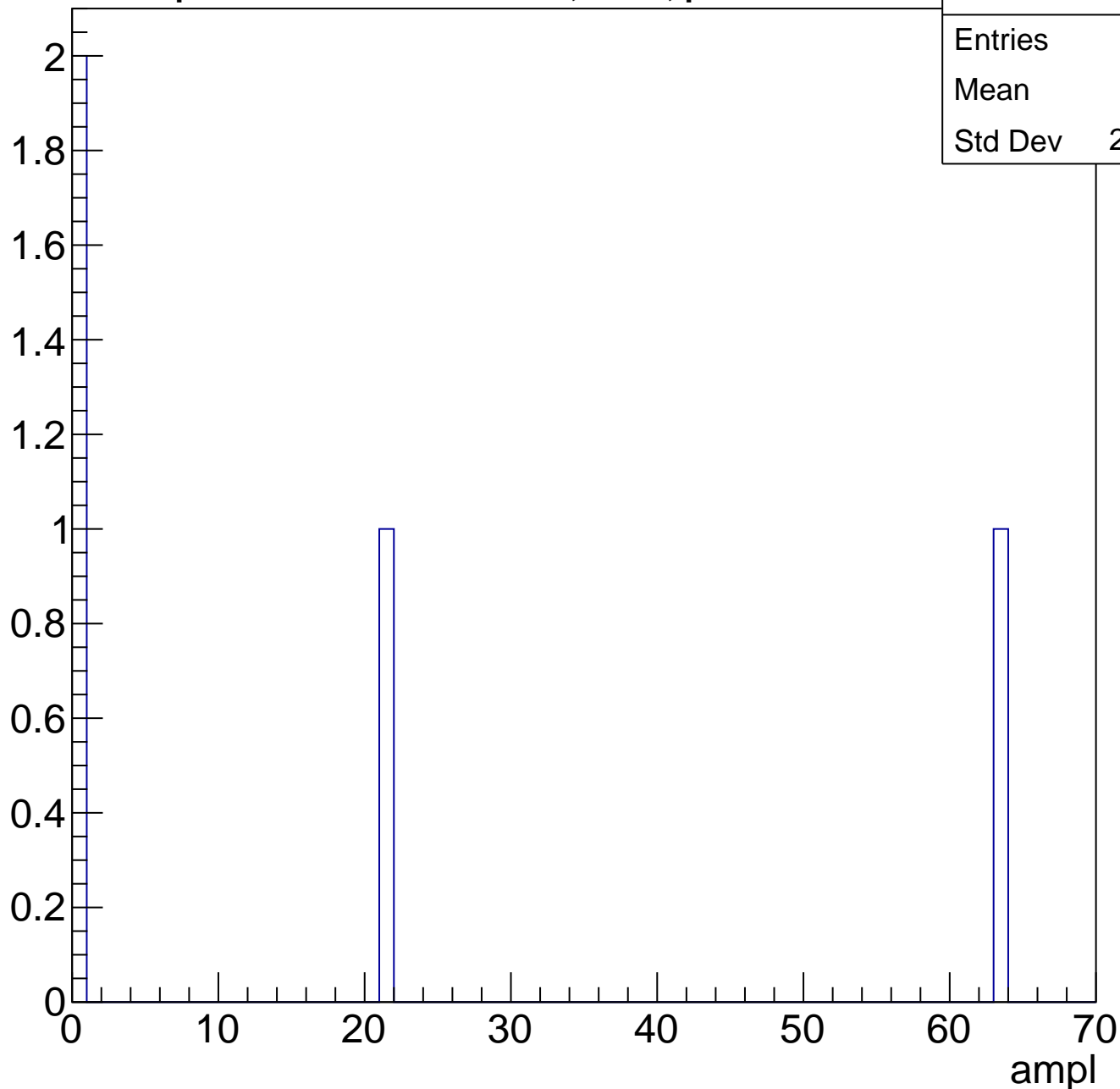




# B1L000S, U21-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	21
Std Dev	25.72

# B1L000S, U21-ch45, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	27.51
Std Dev	4.374

**Gaus mean : 28.0558**

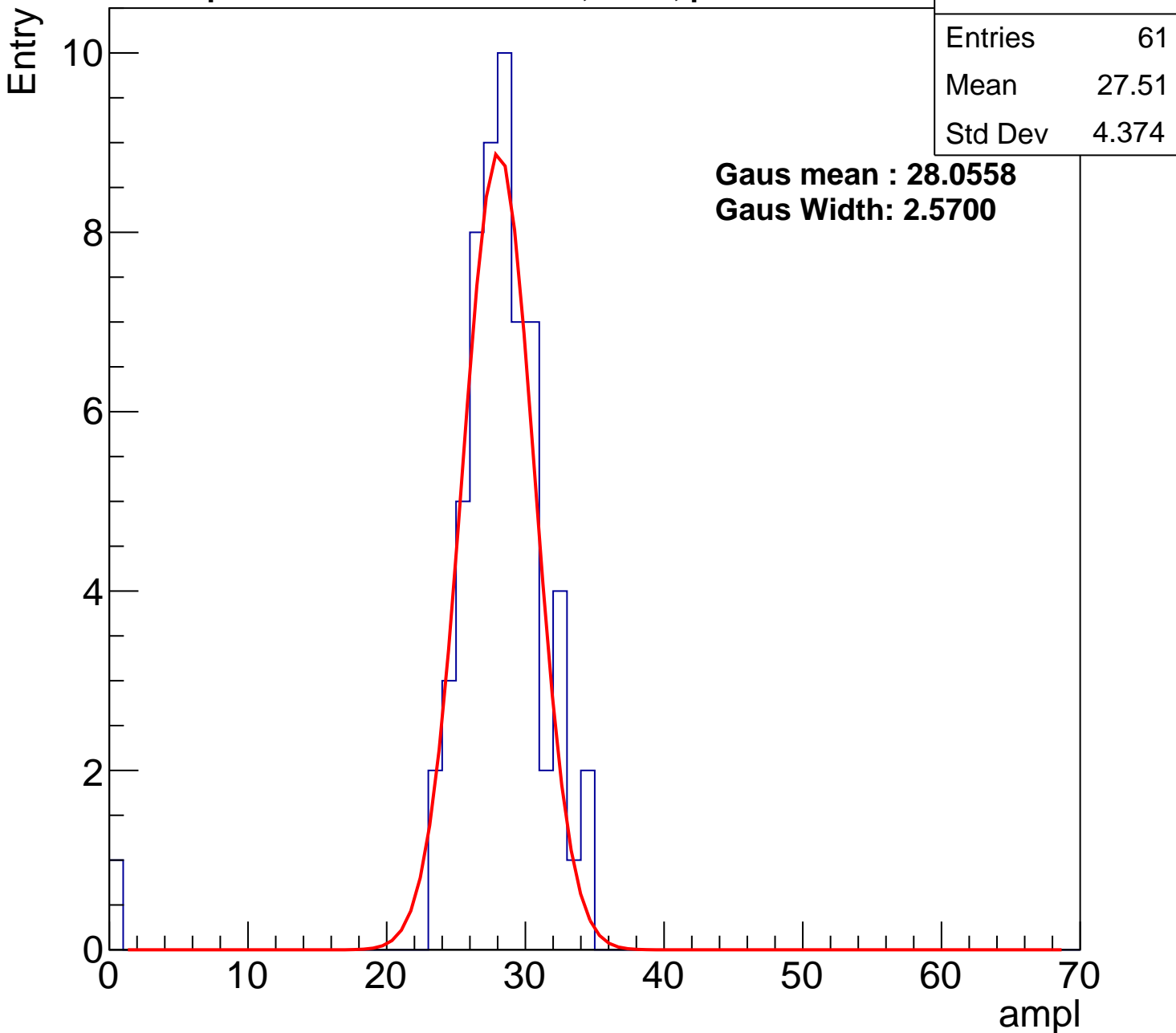
**Gaus Width: 2.5700**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch45, adc1

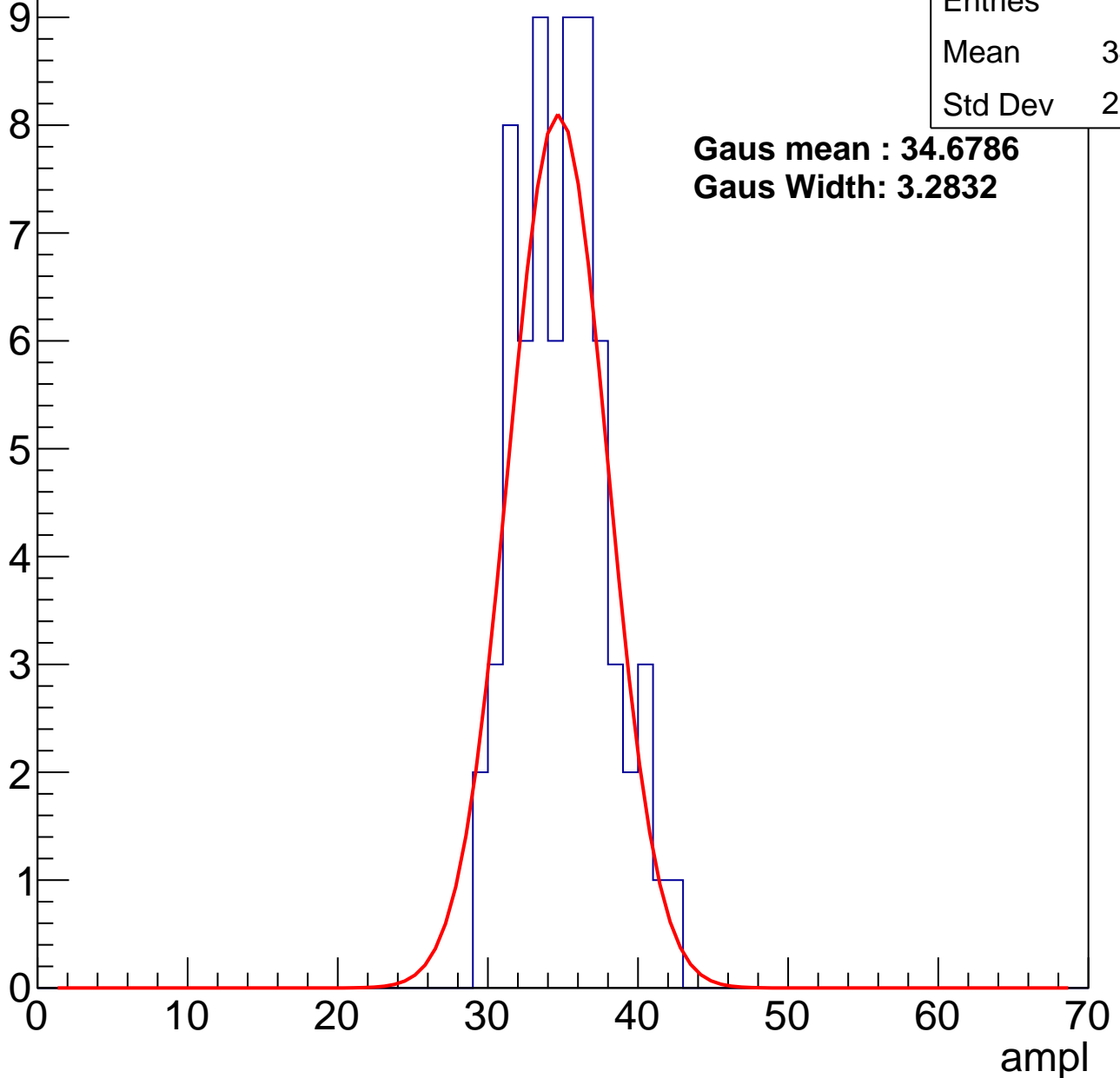
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	34.49
Std Dev	2.983

**Gaus mean : 34.6786**

**Gaus Width: 3.2832**



# B1L000S, U21-ch45, adc2

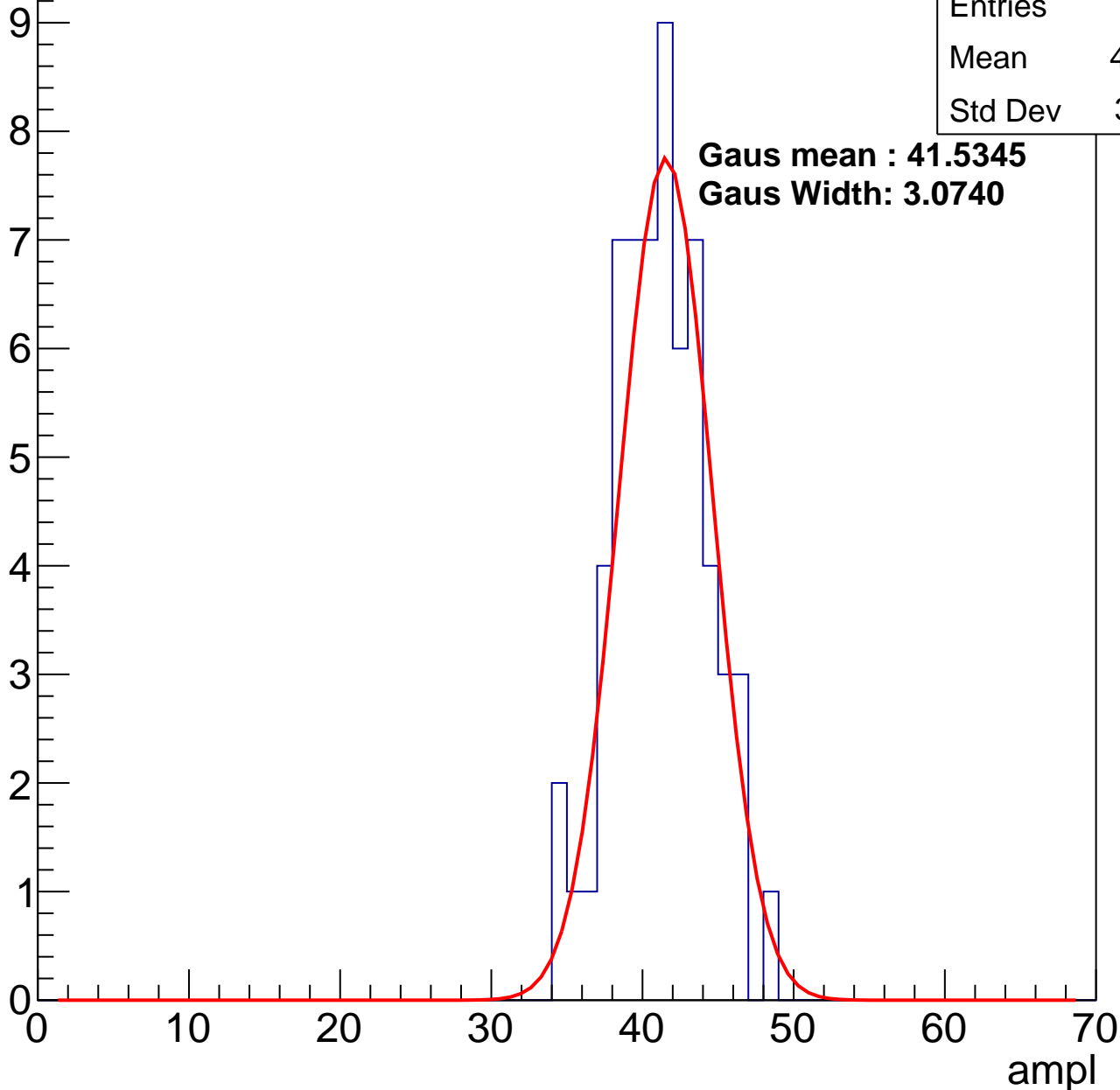
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.73
Std Dev	3.001

**Gaus mean : 41.5345**

**Gaus Width: 3.0740**

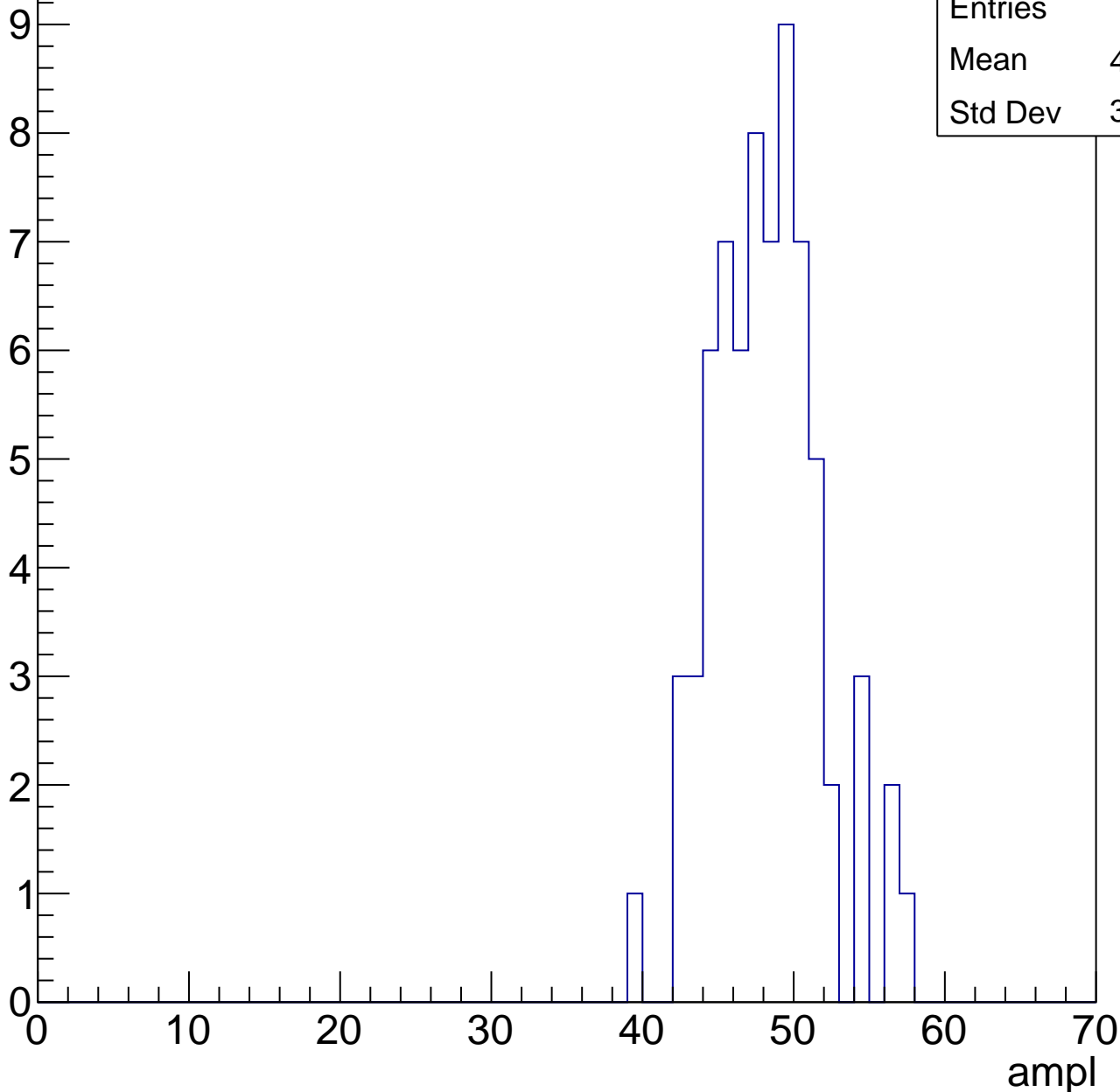


# B1L000S, U21-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	47.74
Std Dev	3.548



# B1L000S, U21-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

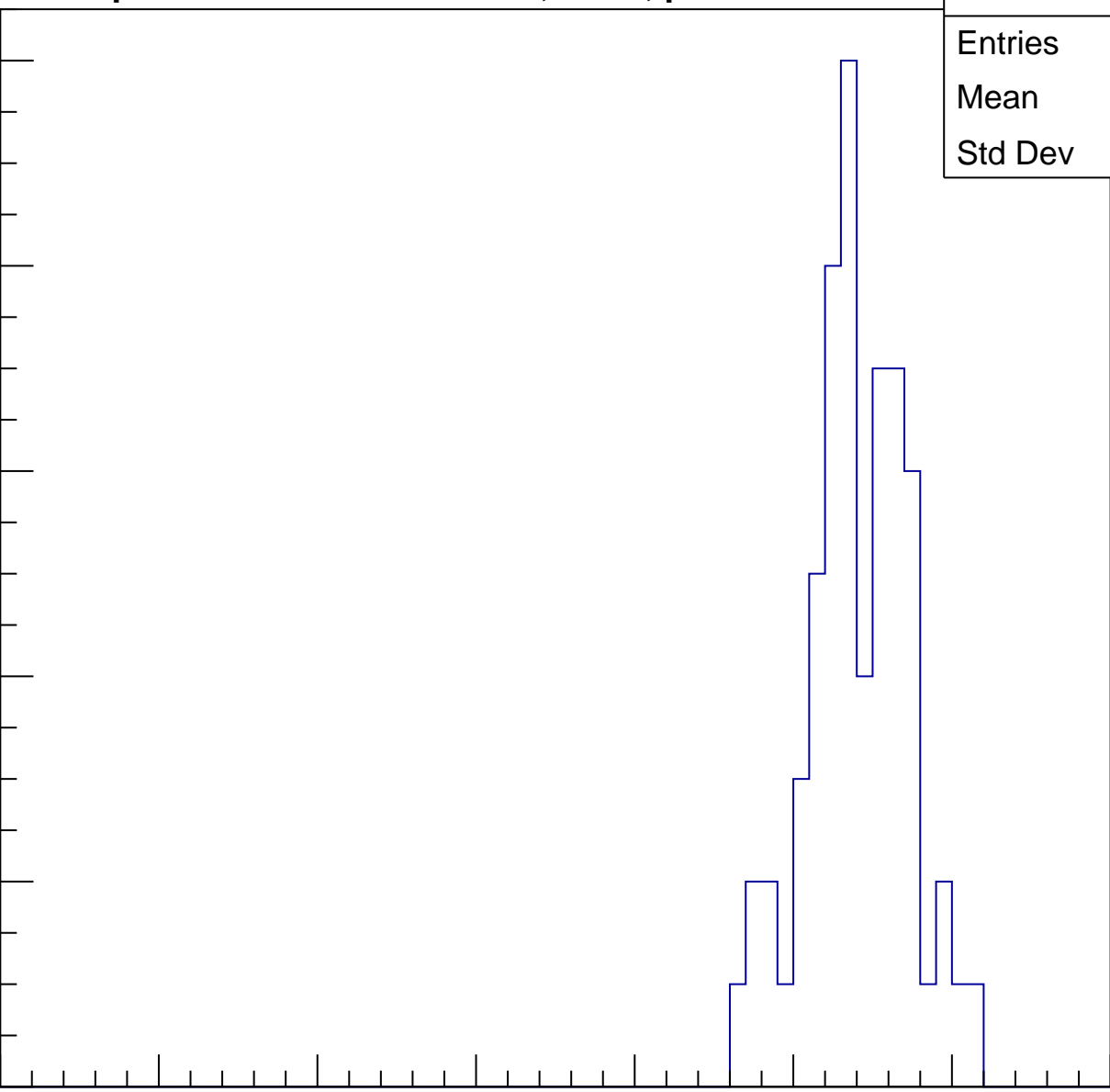
Entries	61
Mean	53.57
Std Dev	3.196

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch45, adc5

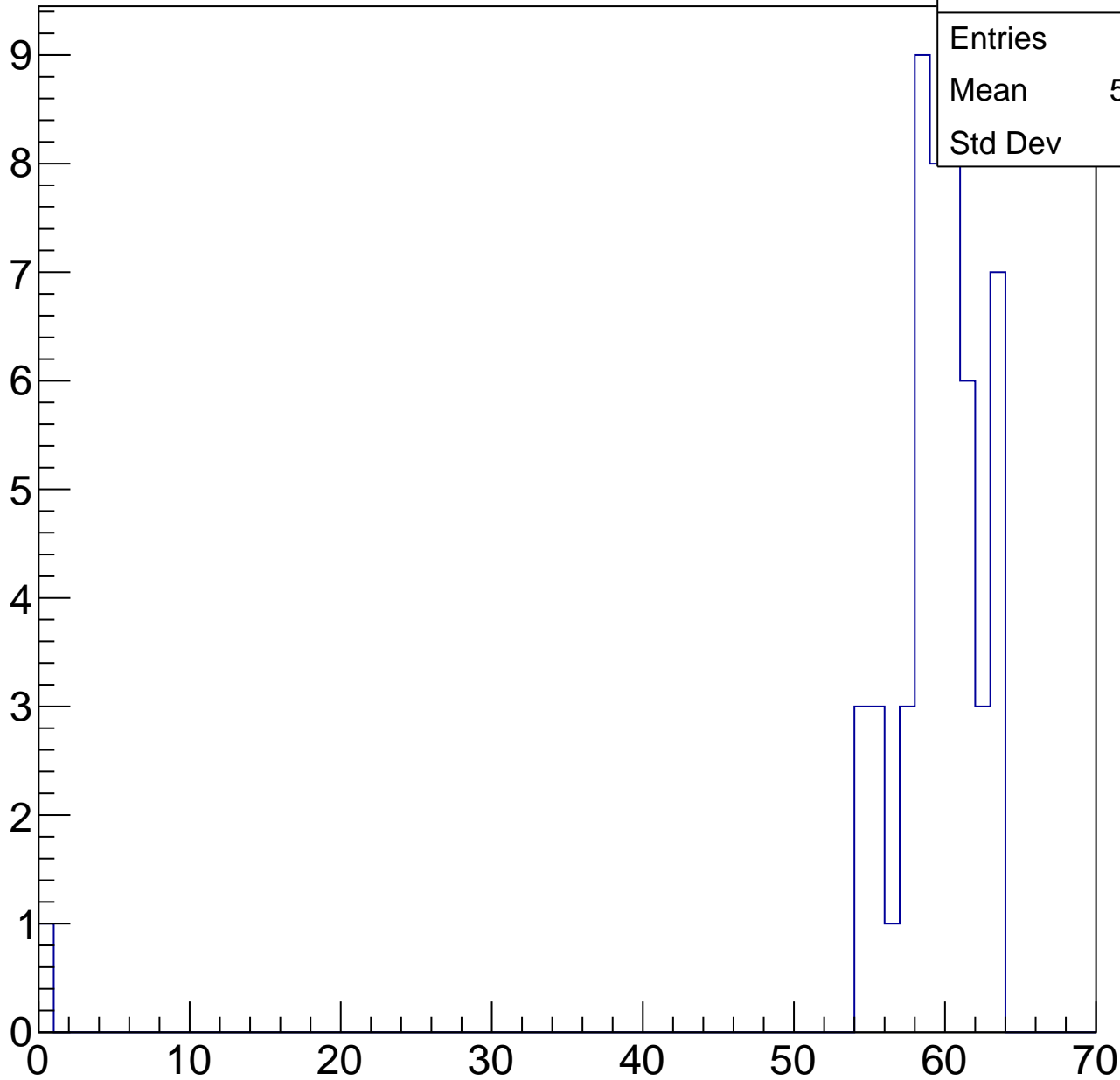
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	58.13
Std Dev	8.43

ampl

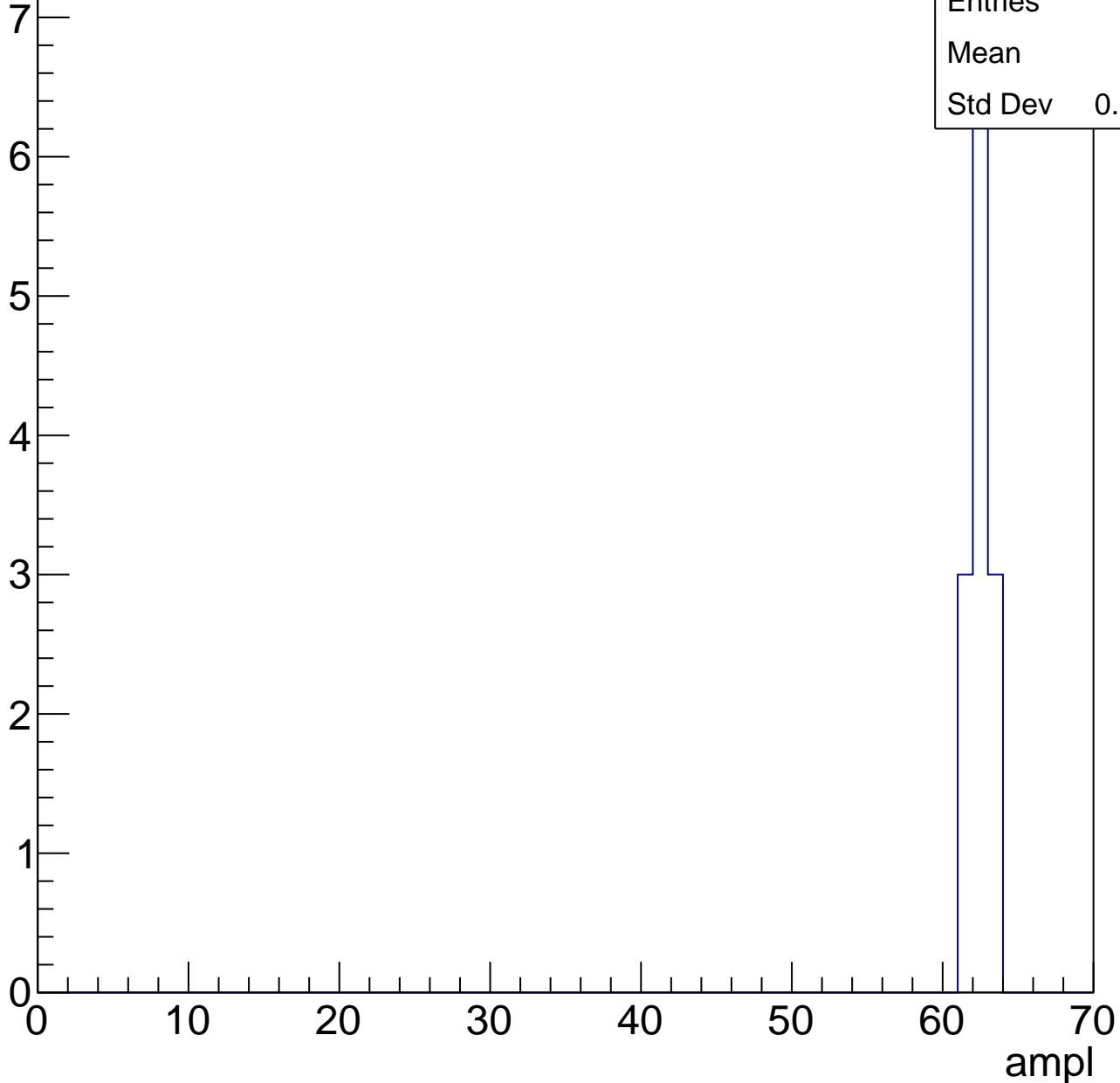


# B1L000S, U21-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	62
Std Dev	0.6794





# B1L000S, U21-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch46, adc0

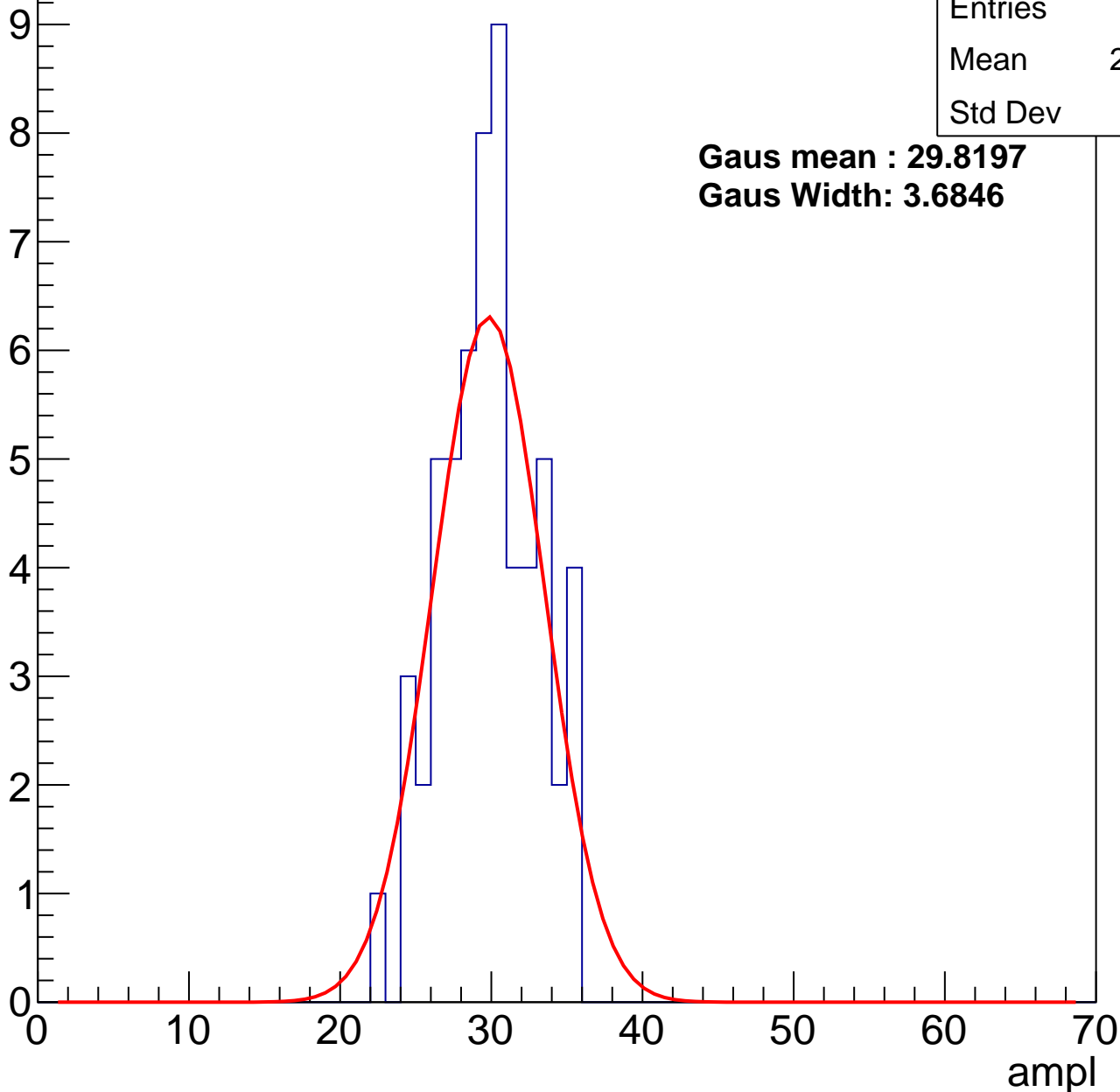
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	29.38
Std Dev	3.09

**Gaus mean : 29.8197**

**Gaus Width: 3.6846**



# B1L000S, U21-ch46, adc1

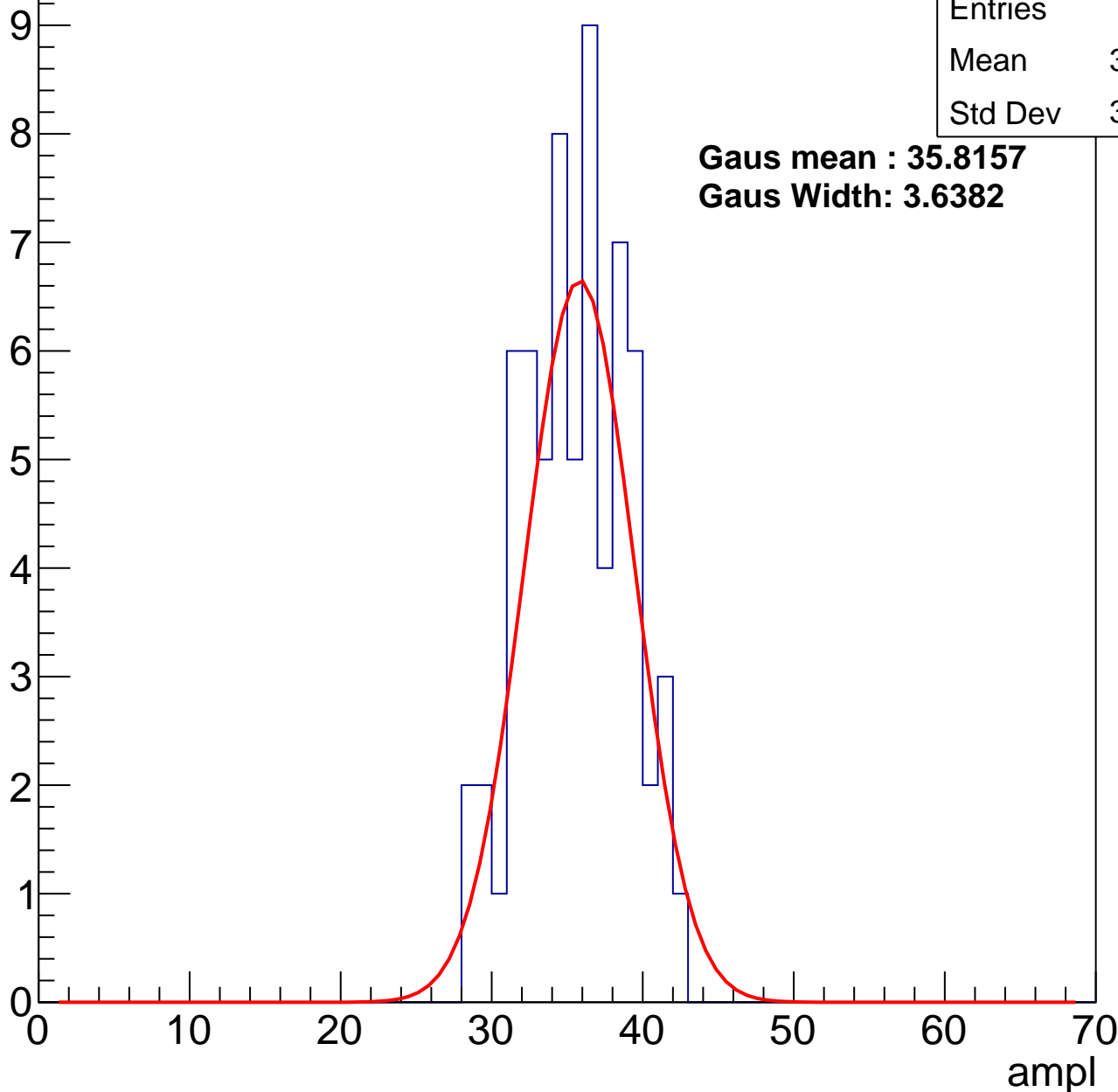
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.09
Std Dev	3.376

**Gaus mean : 35.8157**

**Gaus Width: 3.6382**



# B1L000S, U21-ch46, adc2

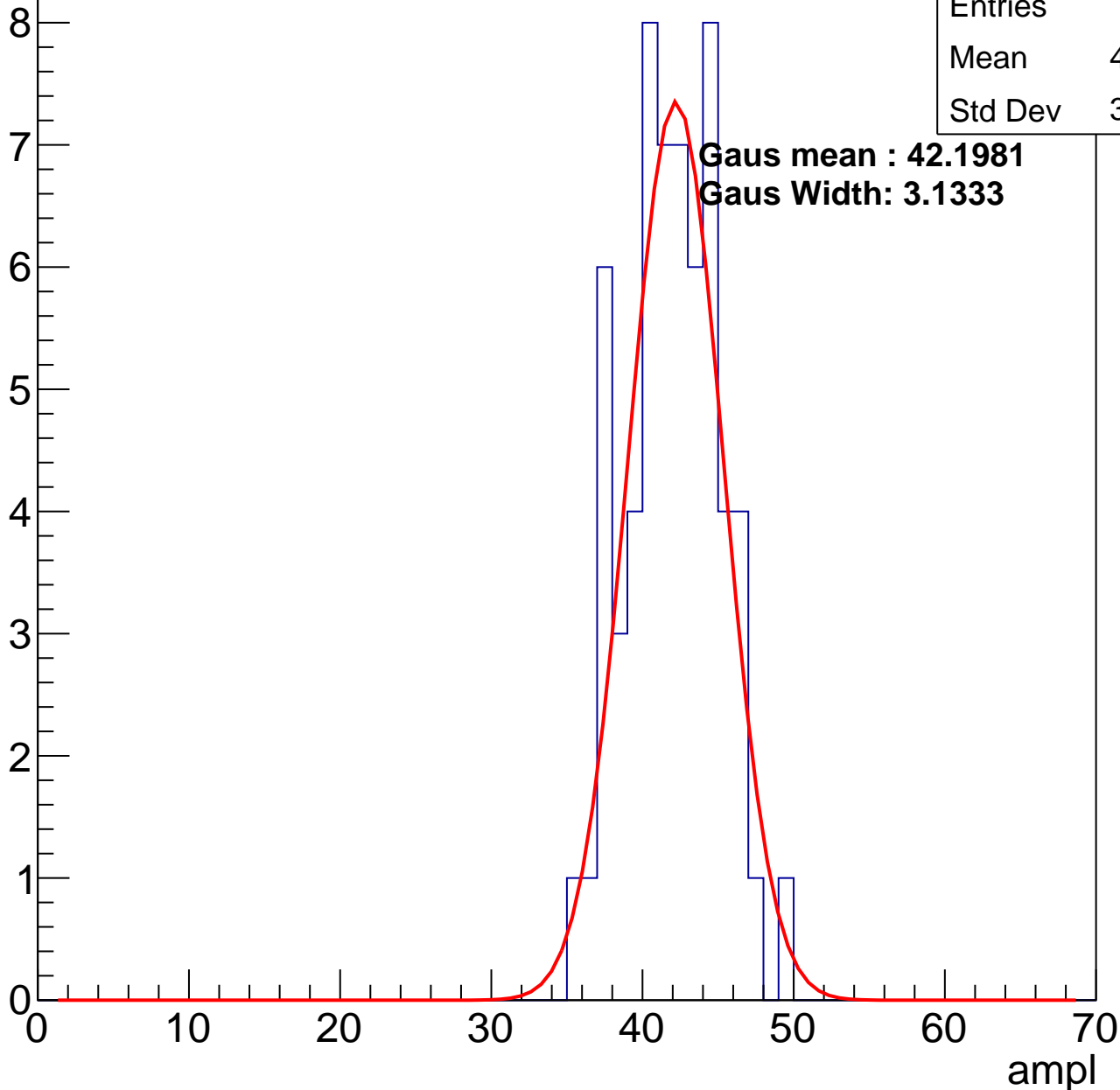
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.54
Std Dev	3.022

**Gaus mean : 42.1981**

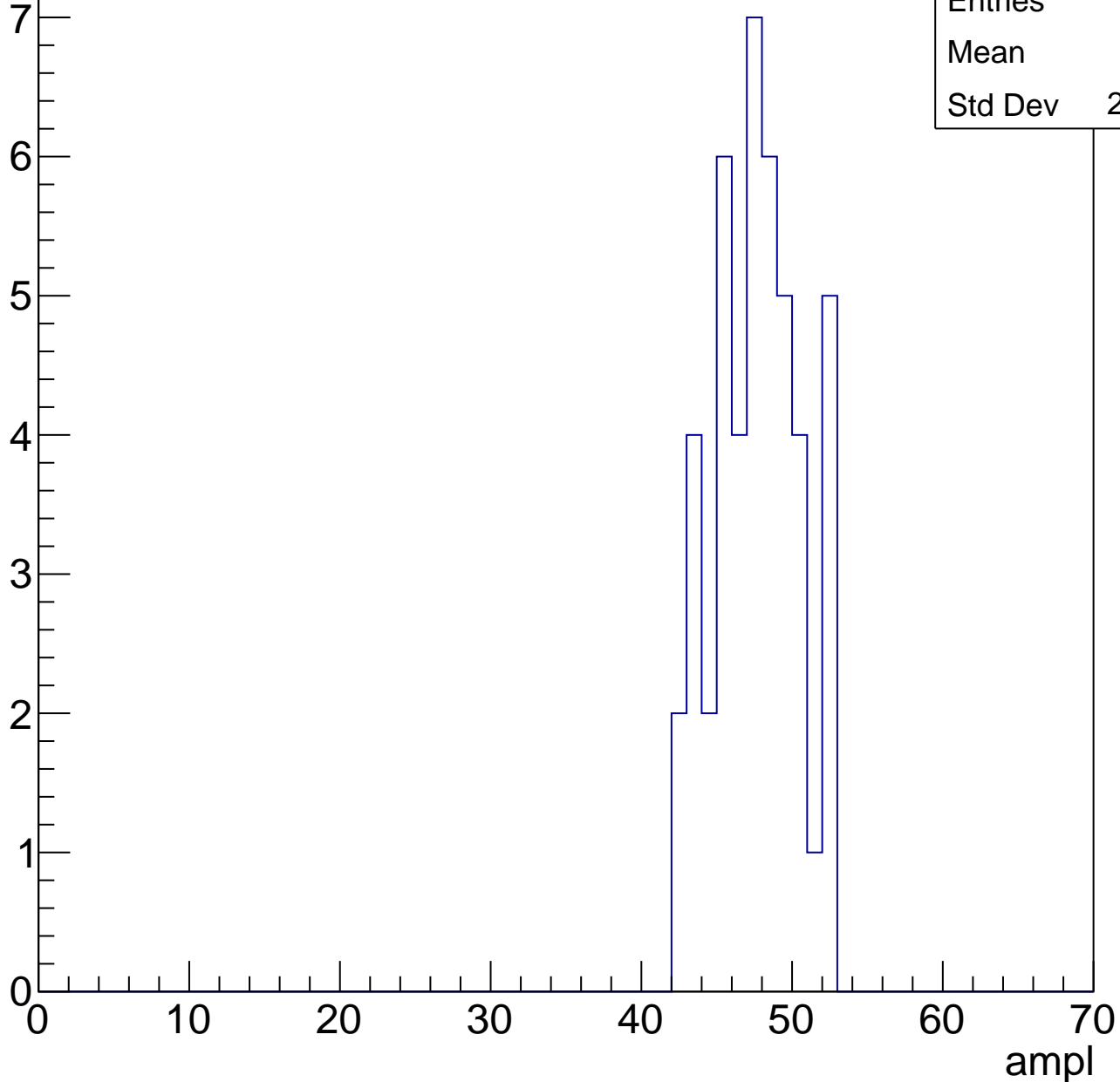
**Gaus Width: 3.1333**



# B1L000S, U21-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

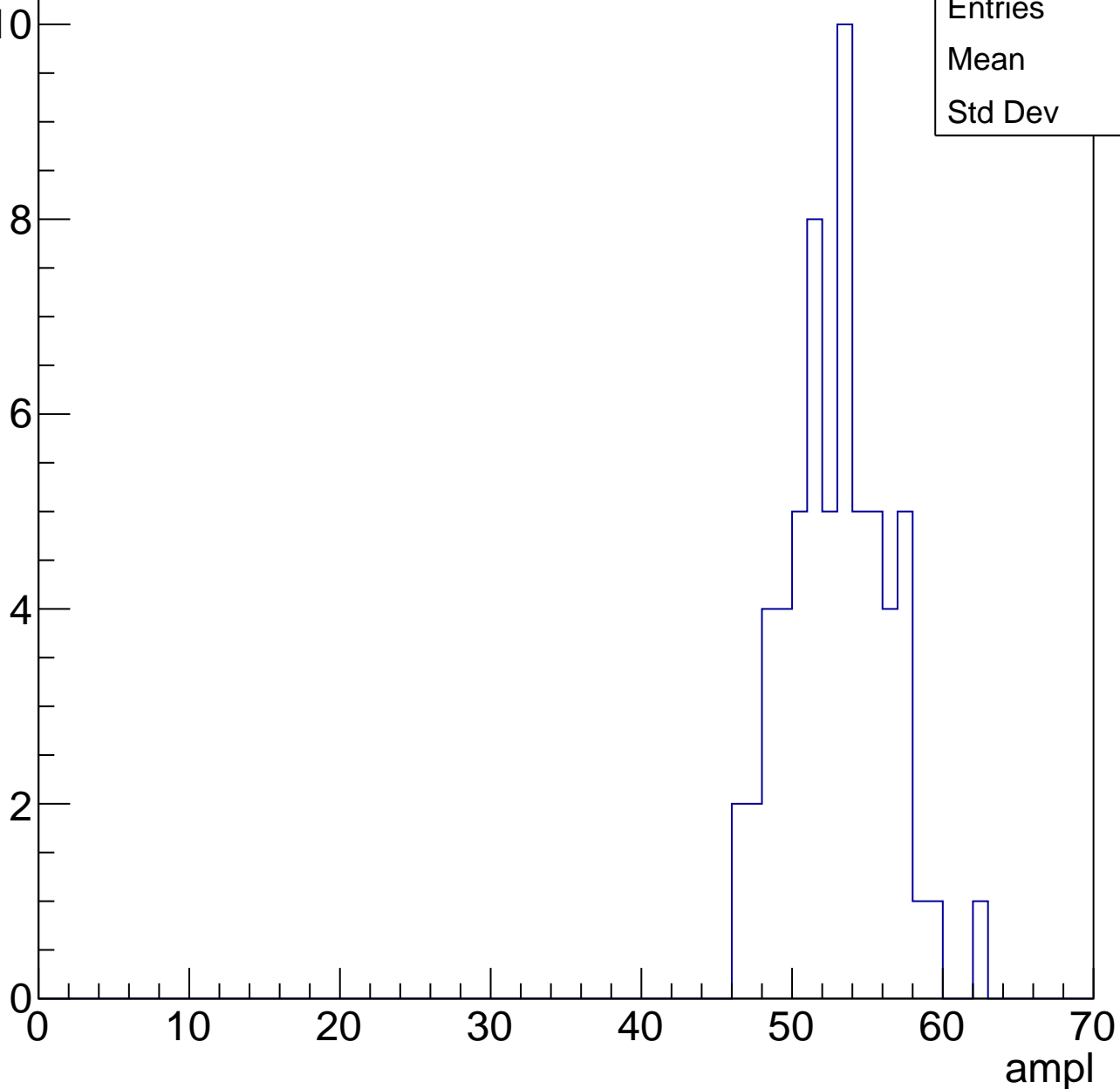


# B1L000S, U21-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	52.5
Std Dev	3.32

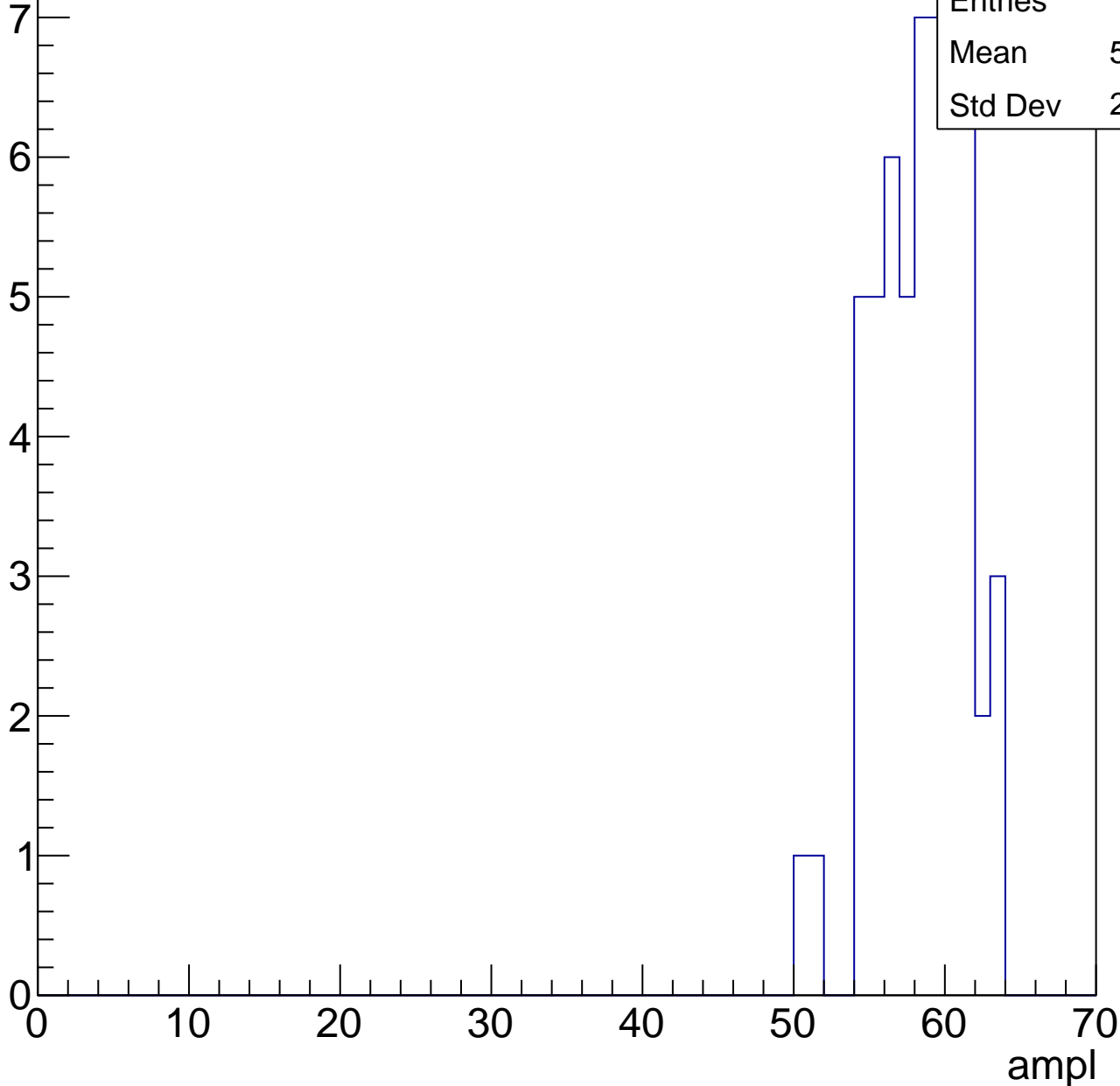


# B1L000S, U21-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

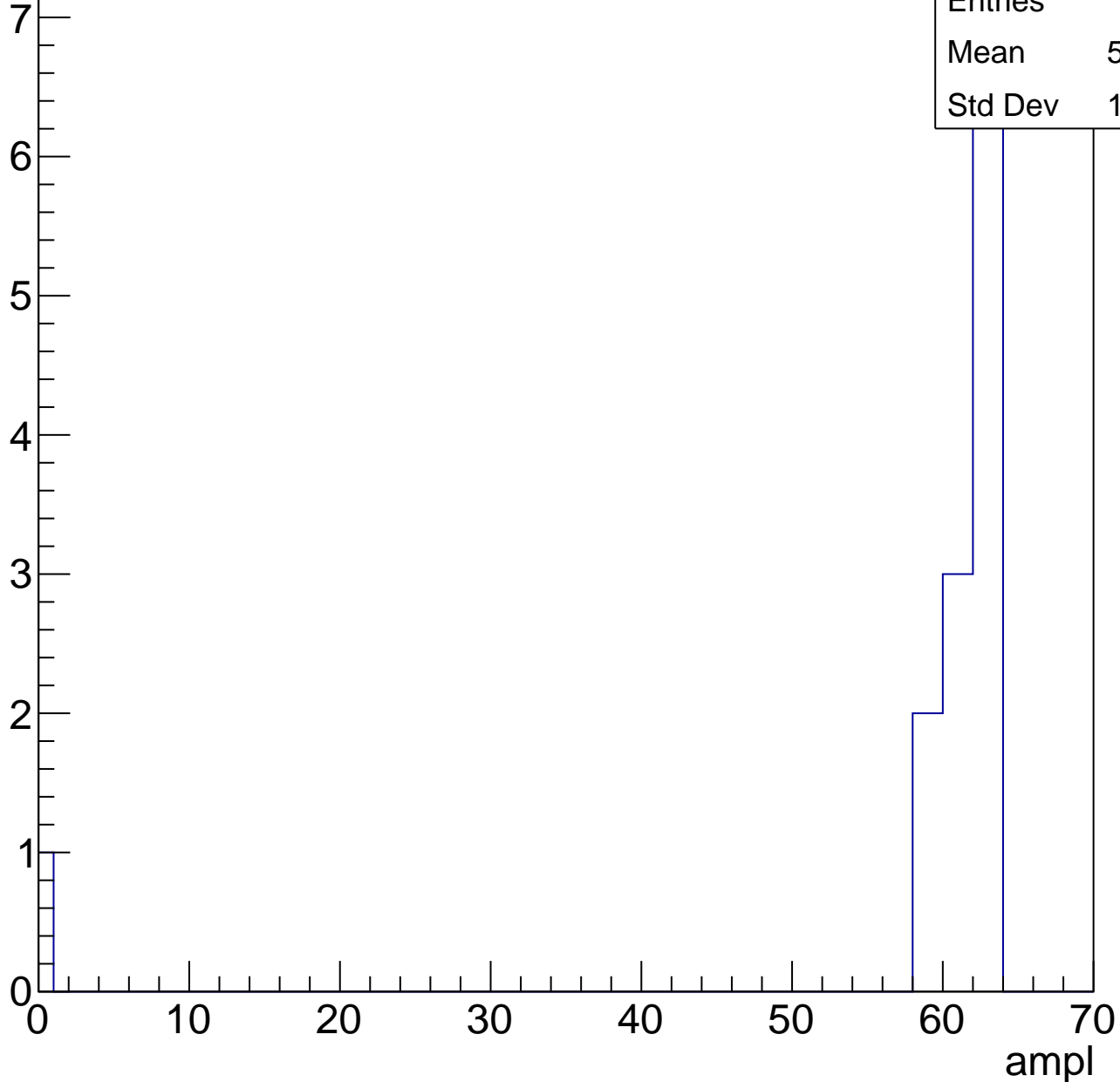
Entries	56
Mean	57.96
Std Dev	2.903



# B1L000S, U21-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch47, adc0

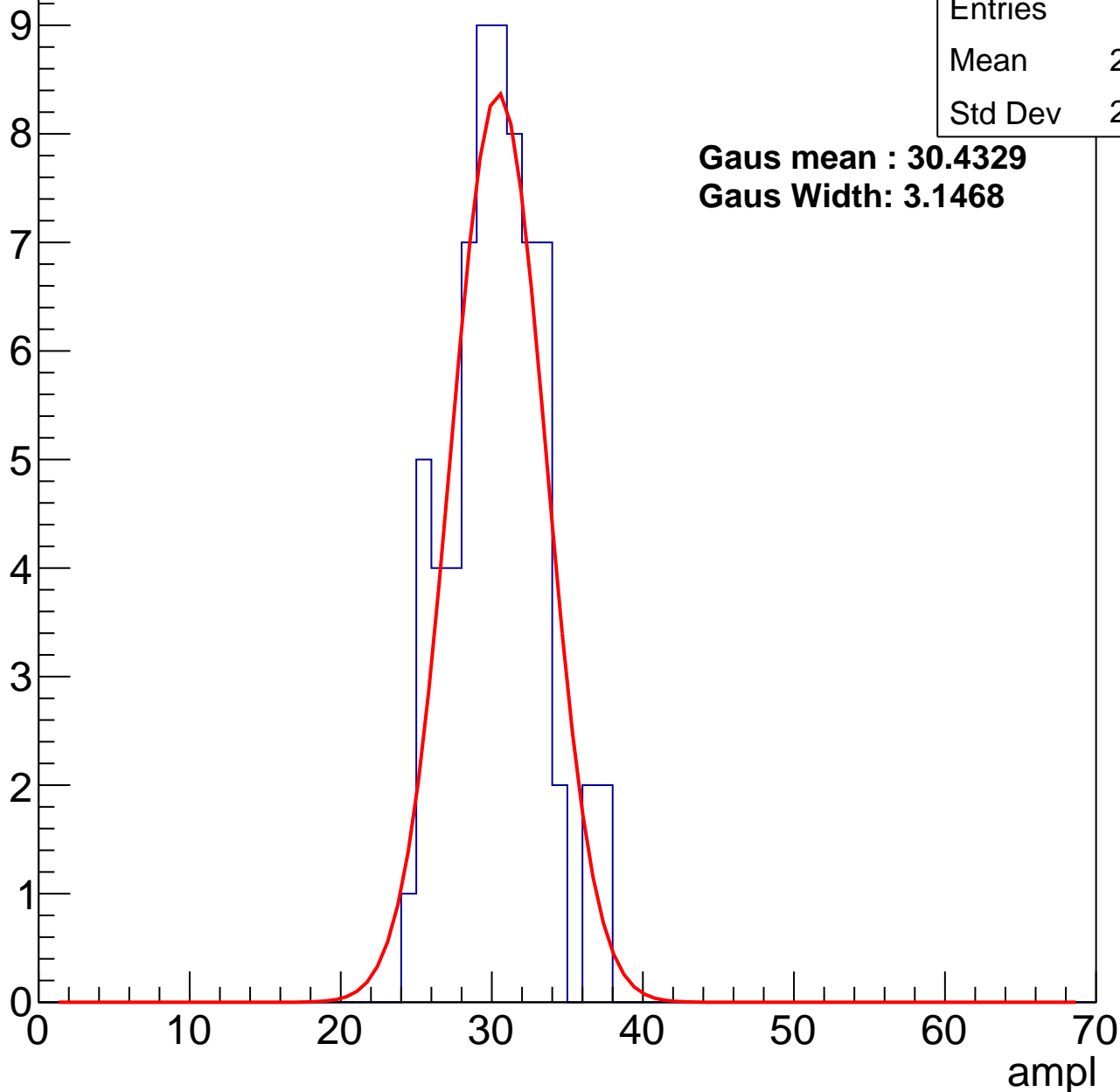
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.93
Std Dev	2.989

**Gaus mean : 30.4329**

**Gaus Width: 3.1468**



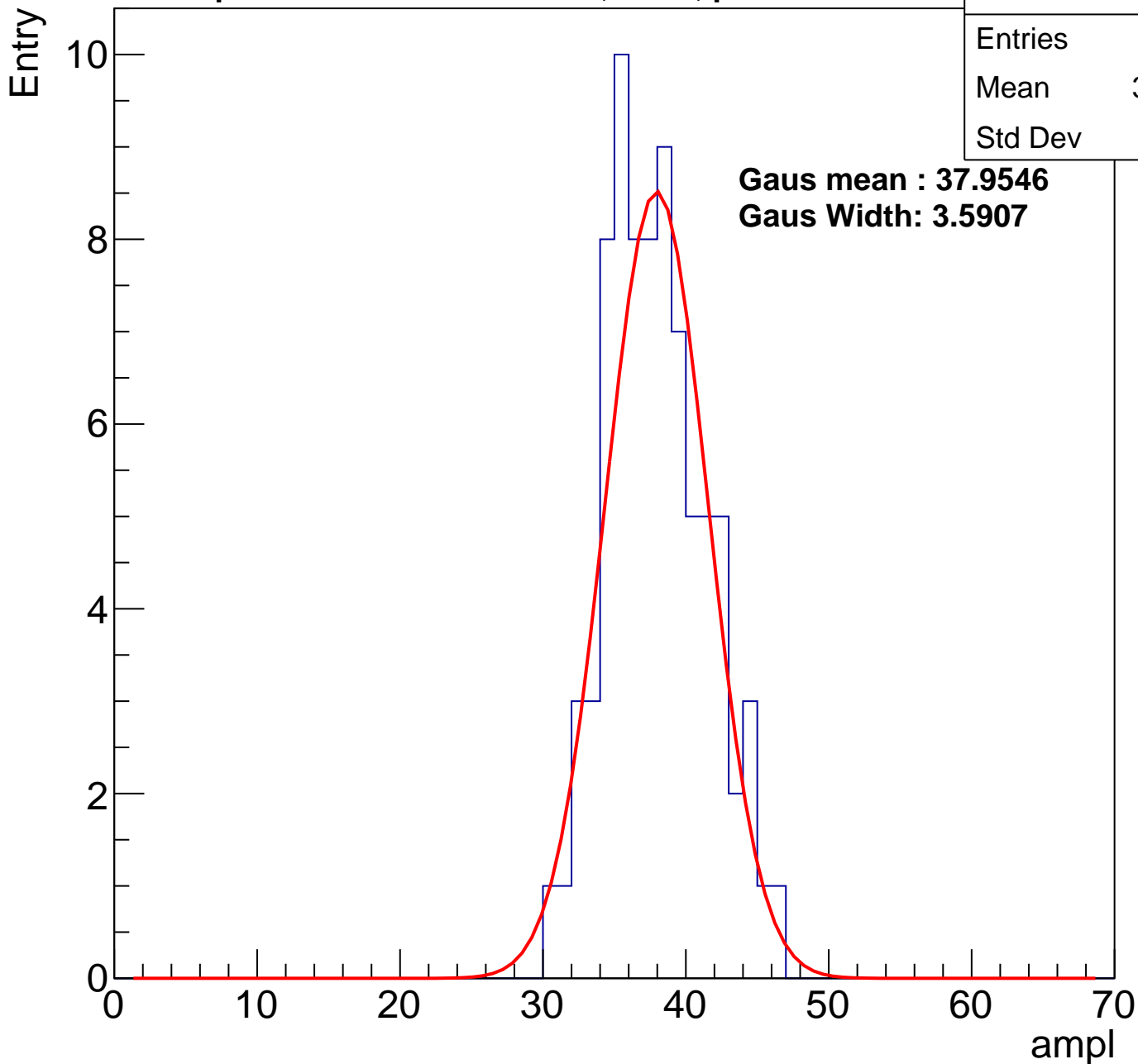
# B1L000S, U21-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	37.51
Std Dev	3.46

**Gaus mean : 37.9546**

**Gaus Width: 3.5907**

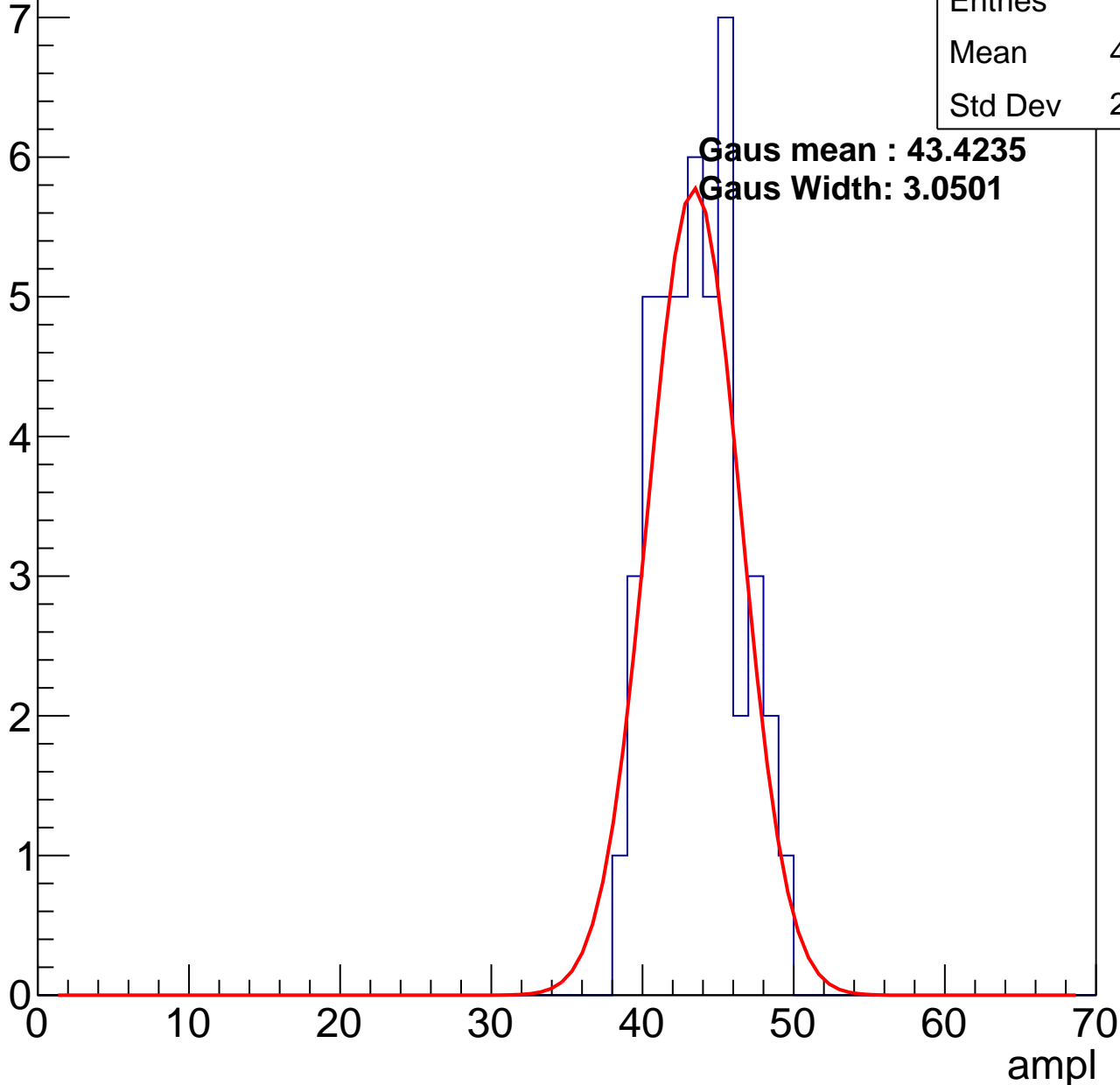


# B1L000S, U21-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

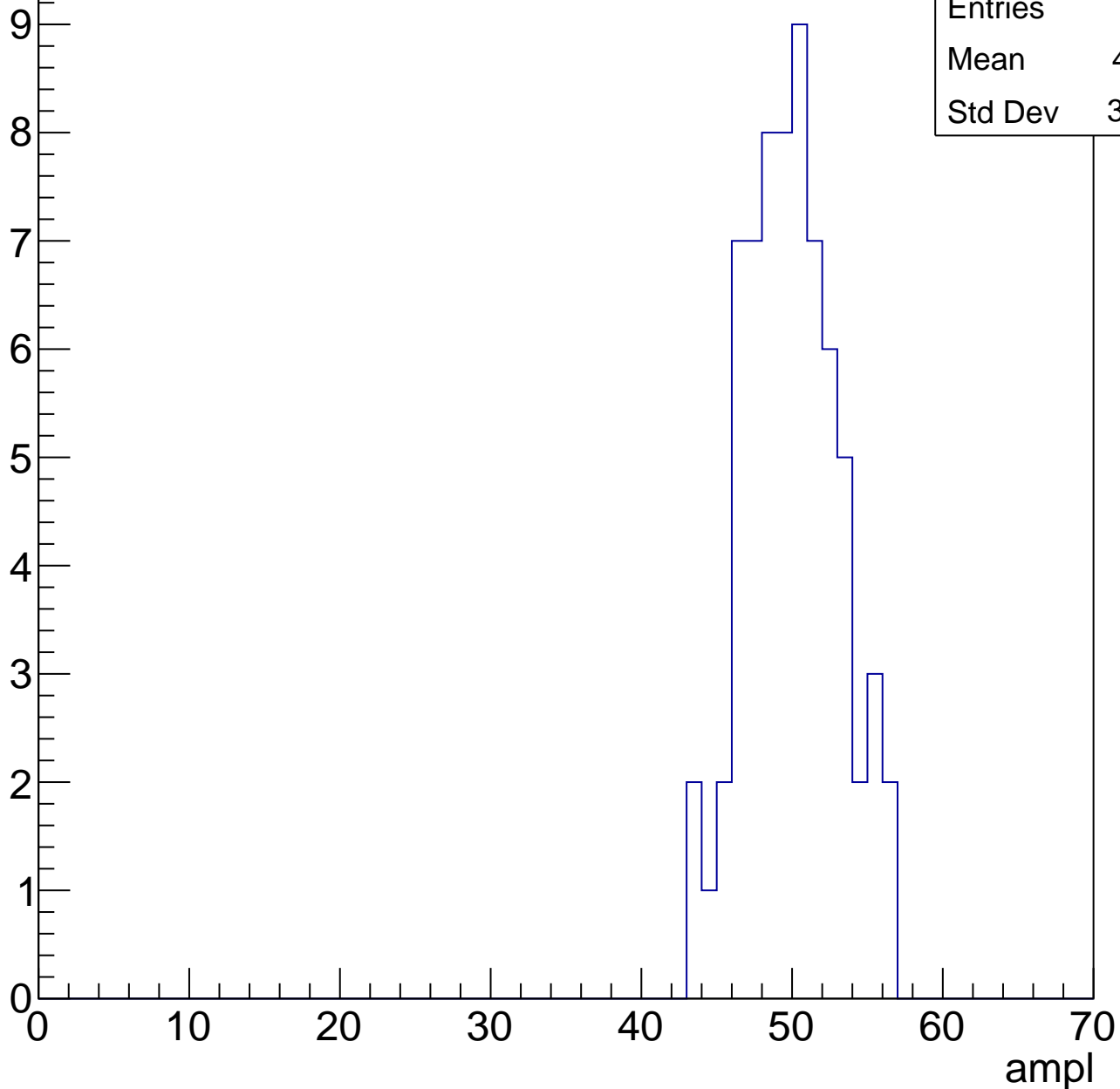
Entries	45
Mean	43.13
Std Dev	2.696



# B1L000S, U21-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

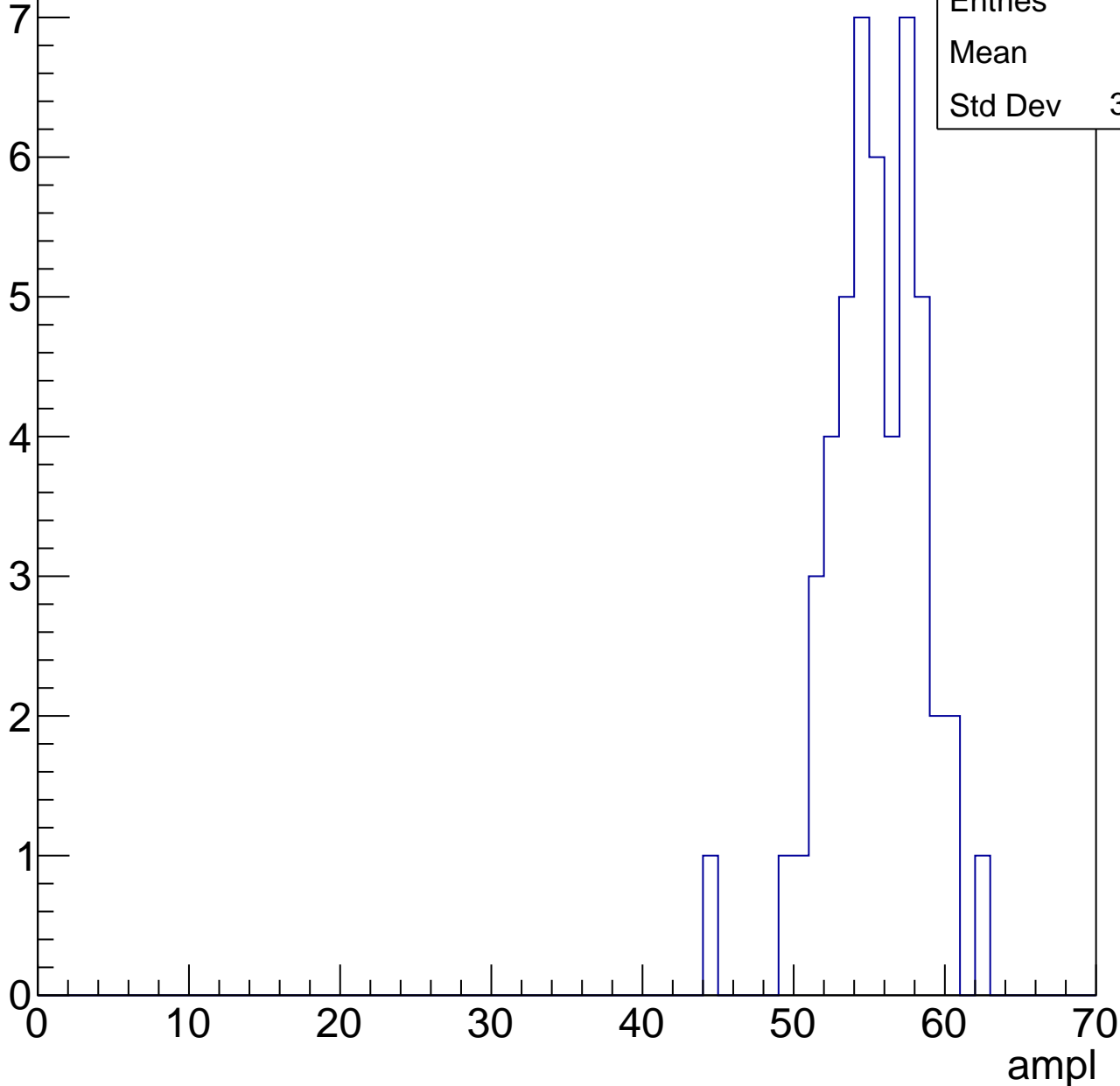


# B1L000S, U21-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	54.9
Std Dev	3.196

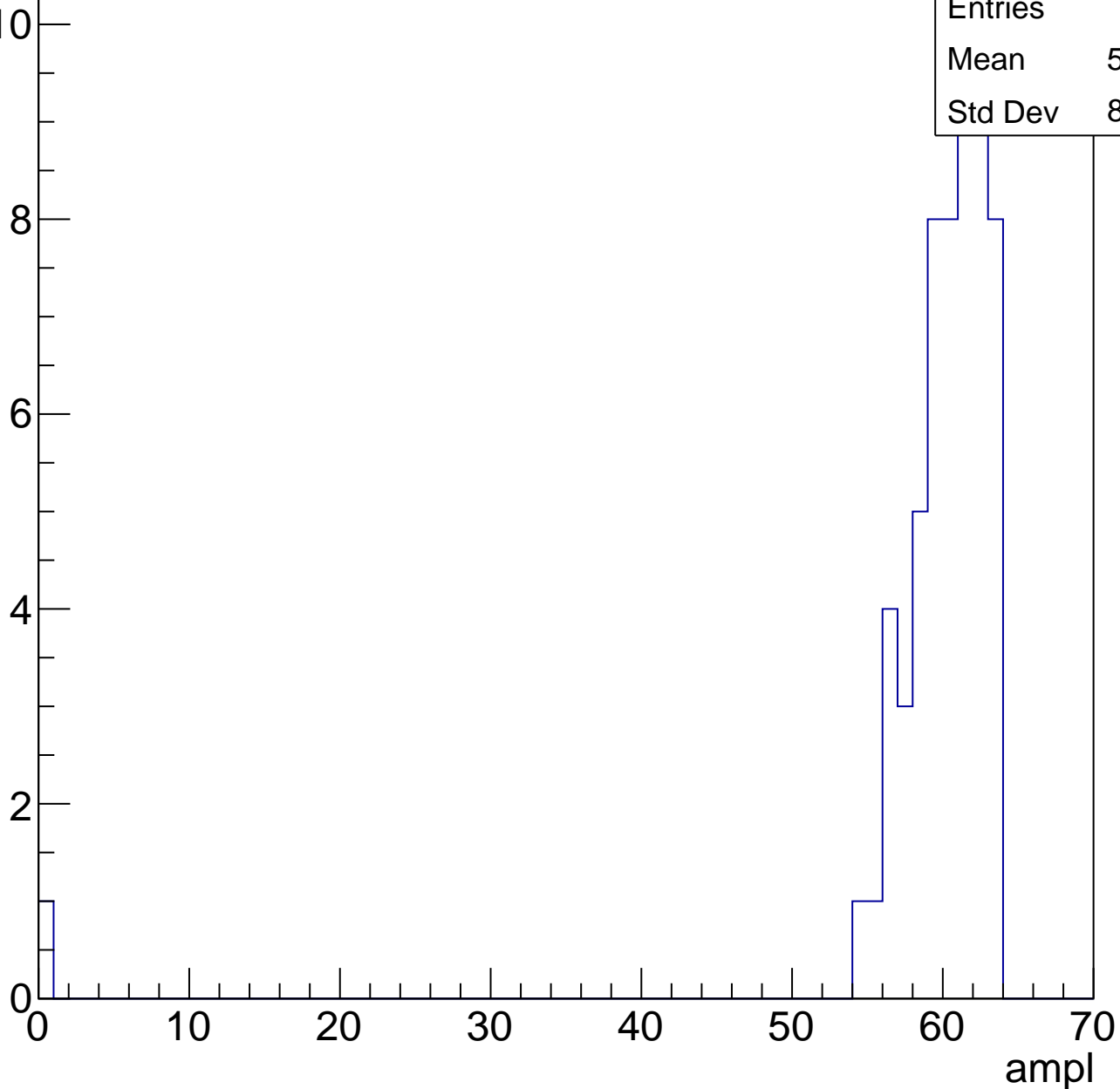


# B1L000S, U21-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	58.93
Std Dev	8.128



# B1L000S, U21-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch48, adc0

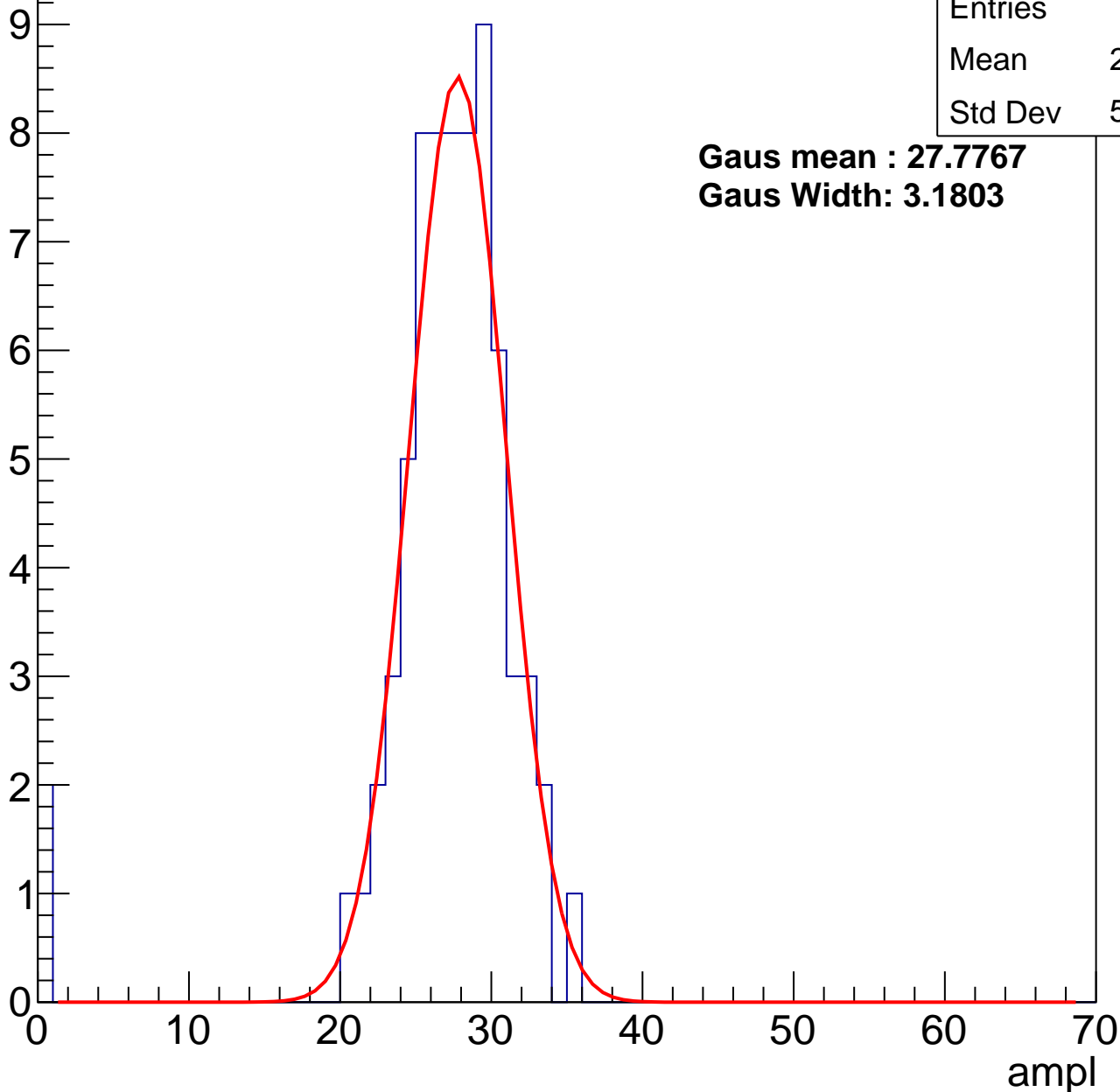
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	26.47
Std Dev	5.429

**Gaus mean : 27.7767**

**Gaus Width: 3.1803**



# B1L000S, U21-ch48, adc1

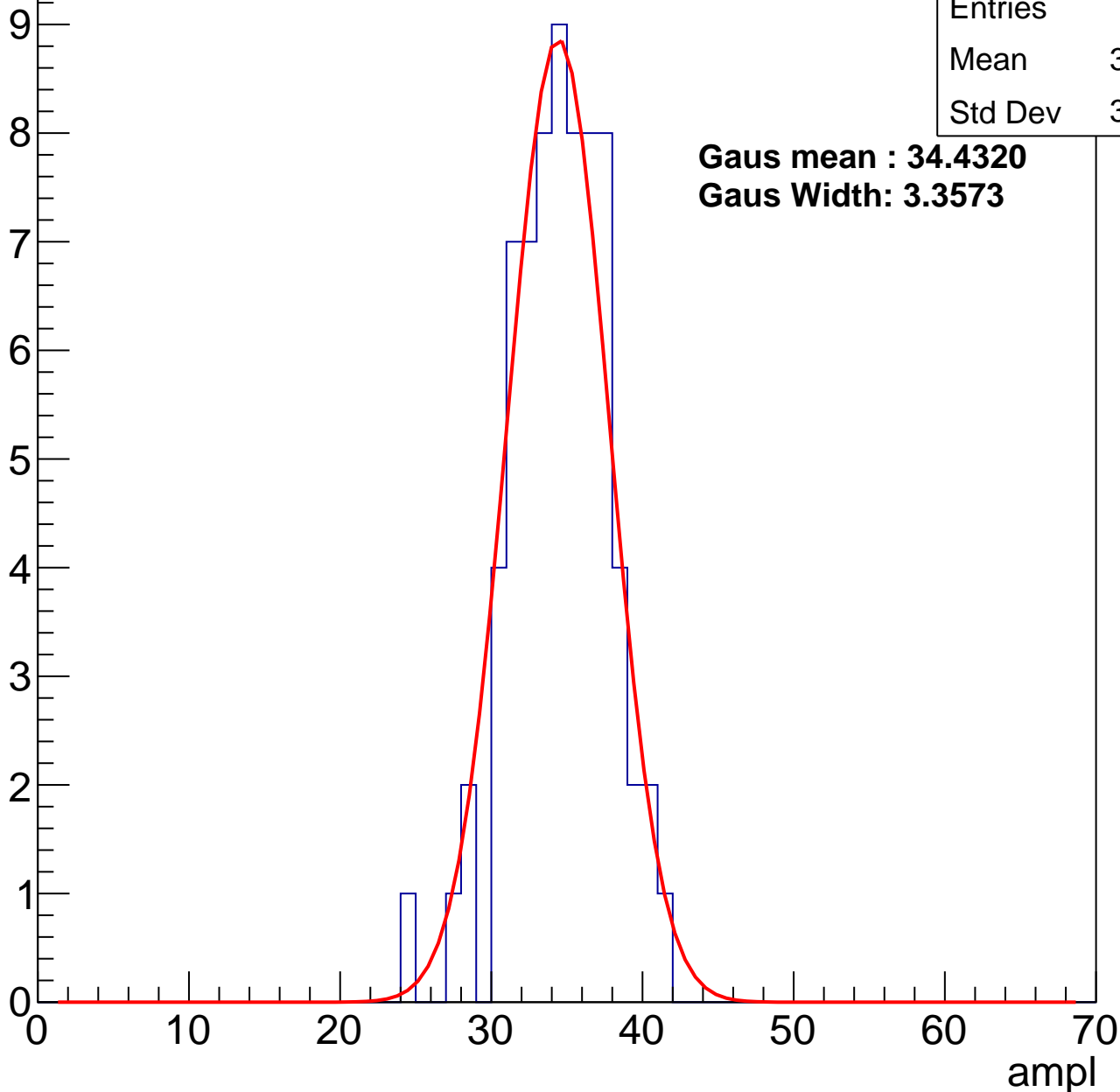
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	34.07
Std Dev	3.186

**Gaus mean : 34.4320**

**Gaus Width: 3.3573**



# B1L000S, U21-ch48, adc2

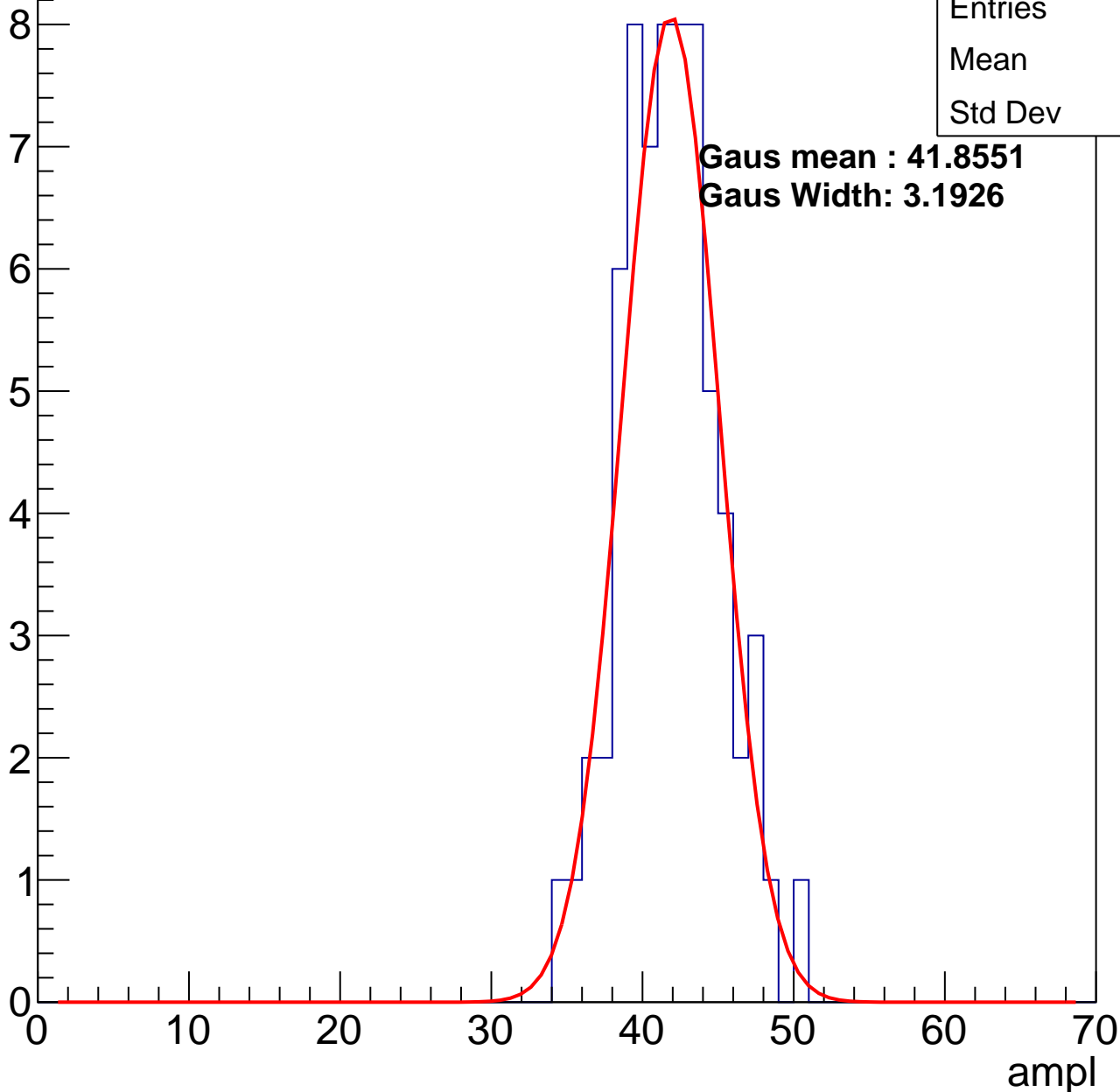
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	41.4
Std Dev	3.2

**Gaus mean : 41.8551**

**Gaus Width: 3.1926**

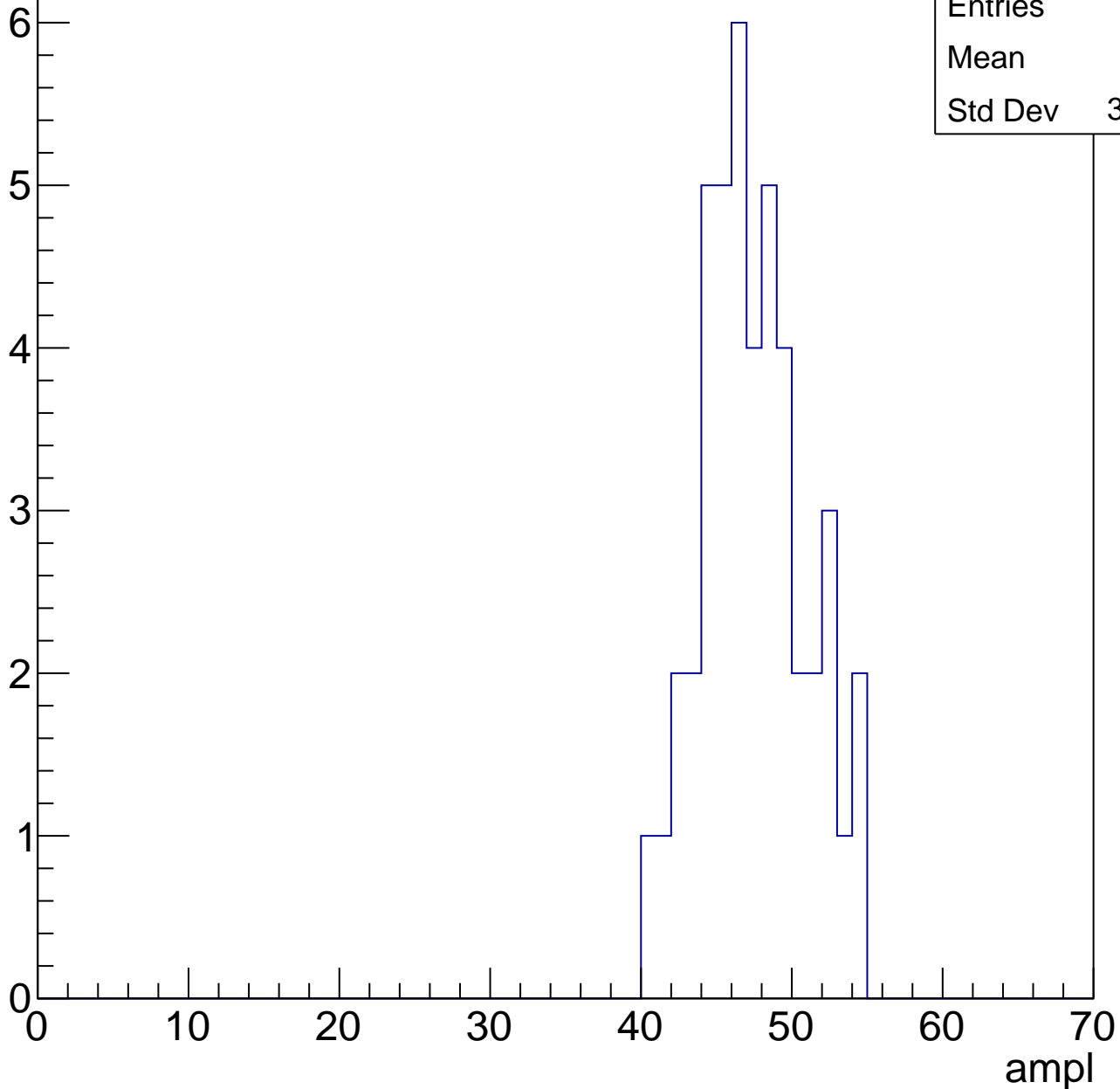


# B1L000S, U21-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	47
Std Dev	3.393

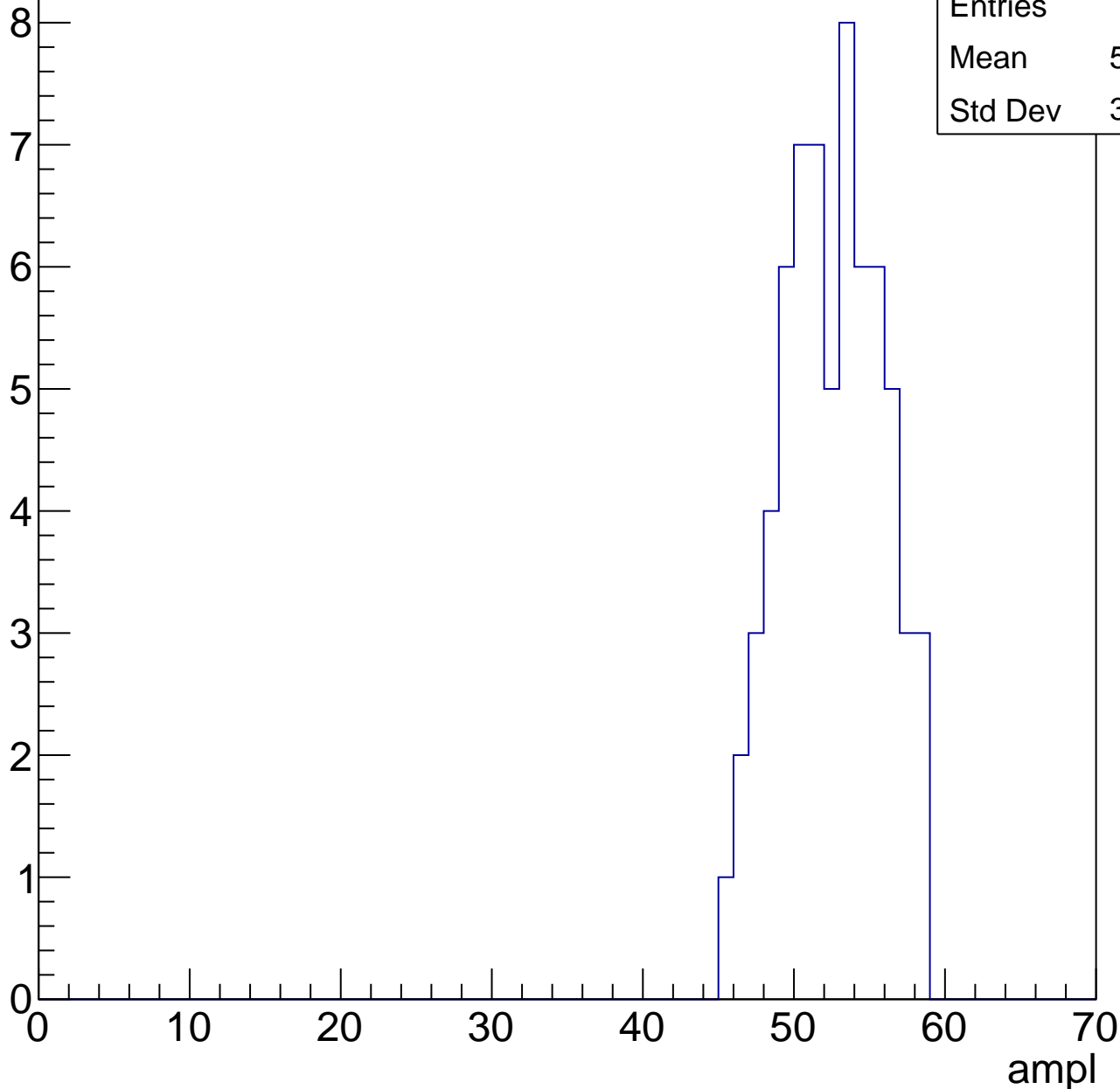


# B1L000S, U21-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

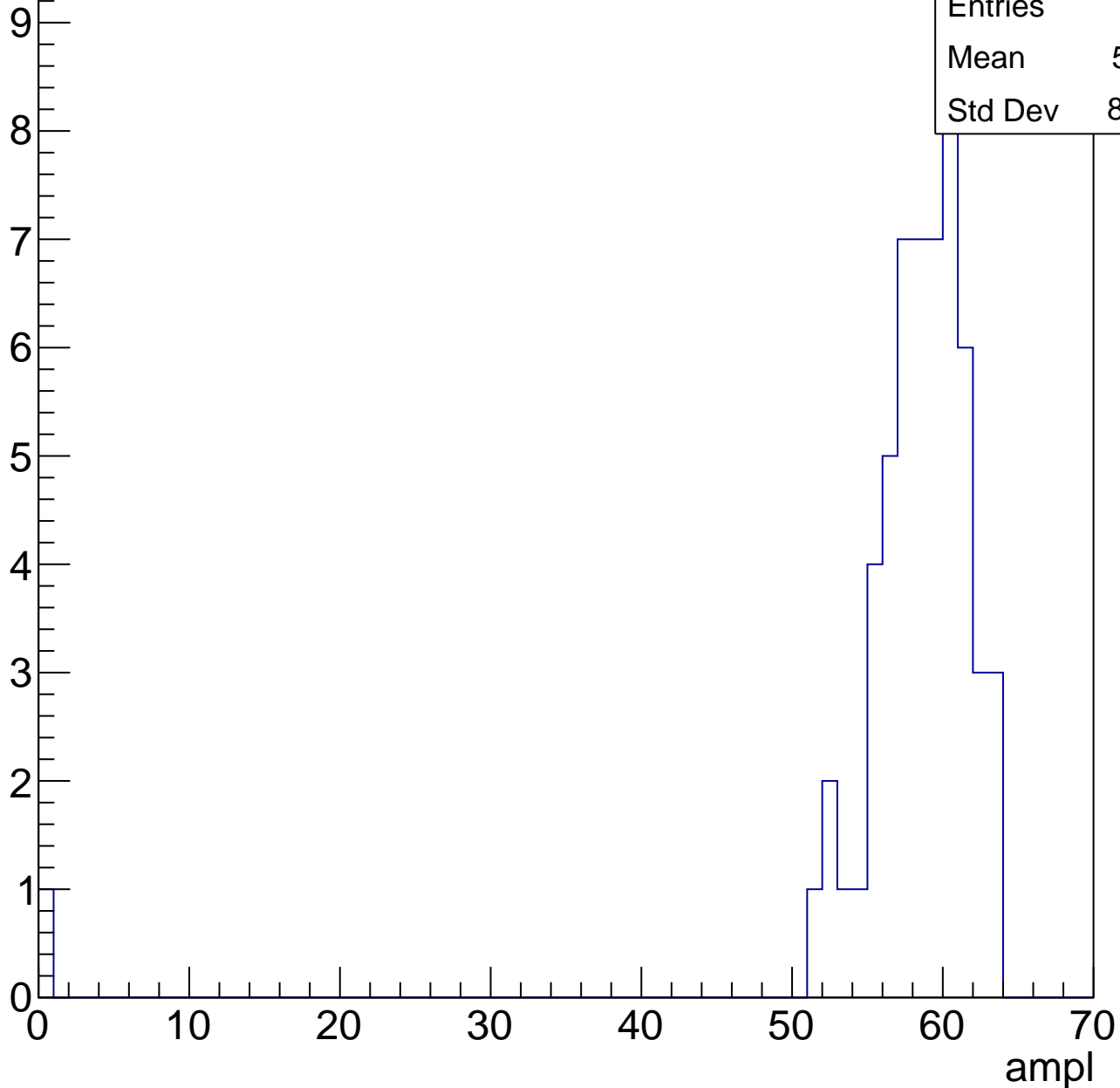
Entries	66
Mean	52.03
Std Dev	3.252



# B1L000S, U21-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

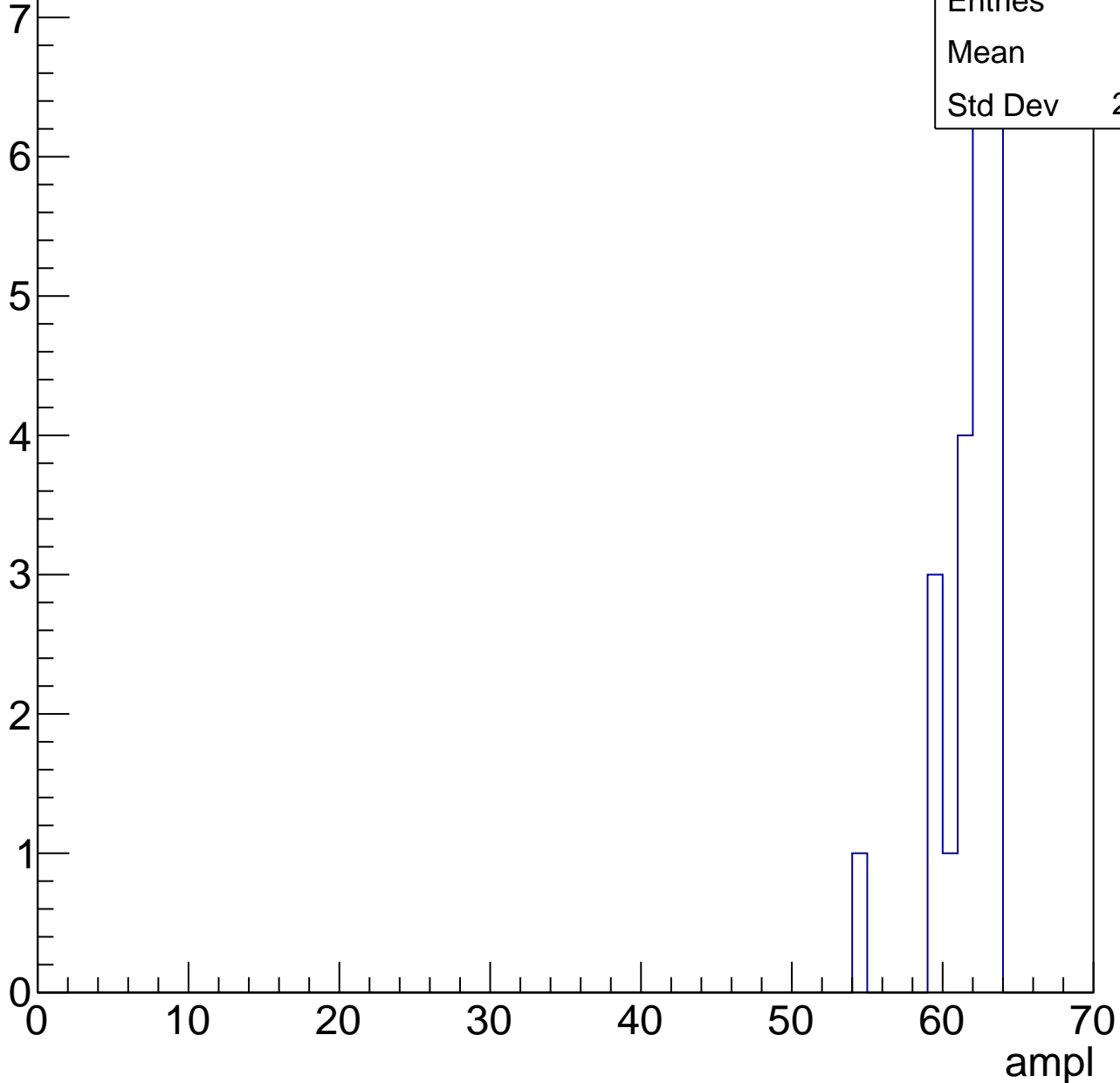


# B1L000S, U21-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	61.3
Std Dev	2.031





# B1L000S, U21-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch49, adc0

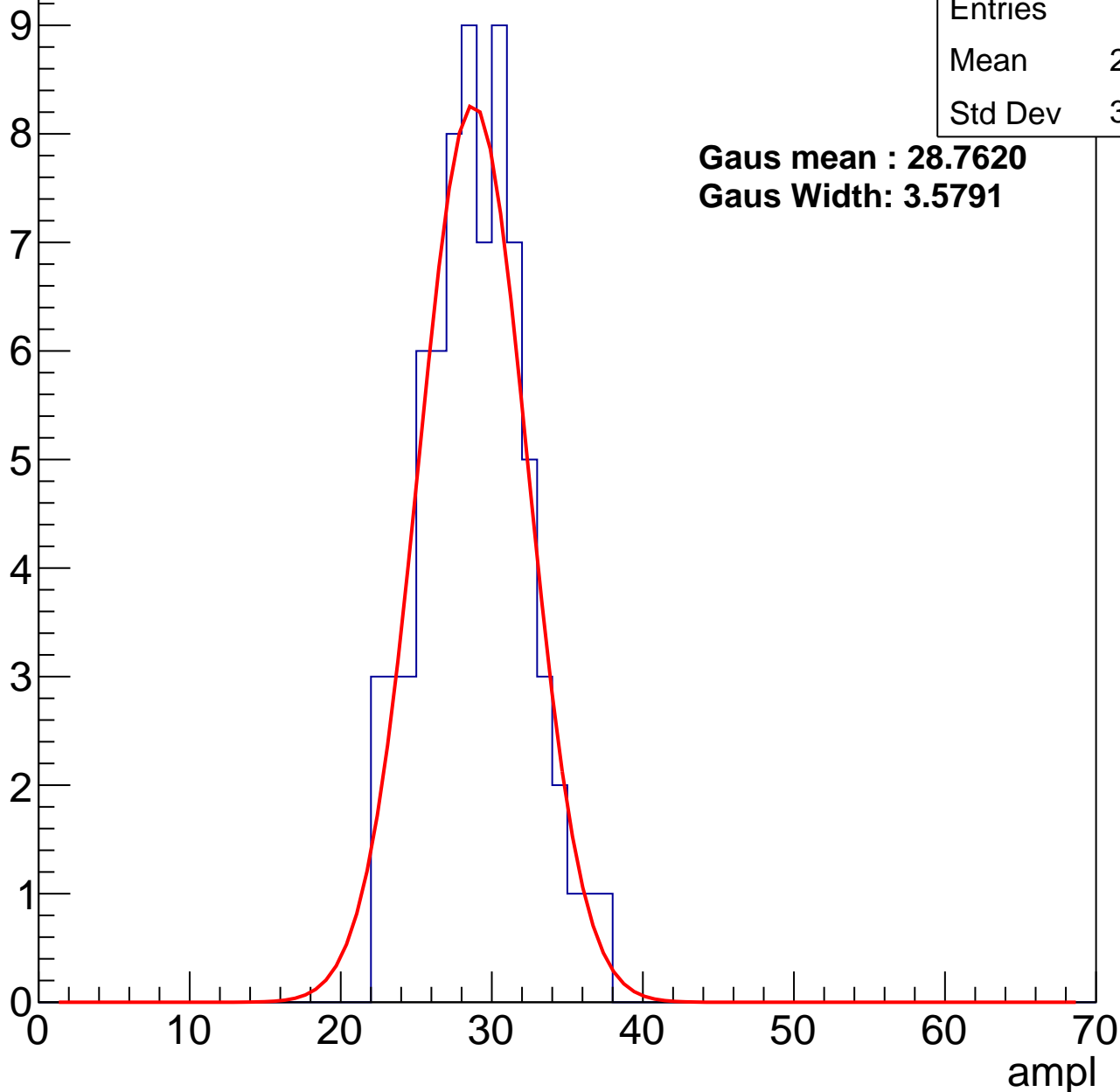
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	28.46
Std Dev	3.346

**Gaus mean : 28.7620**

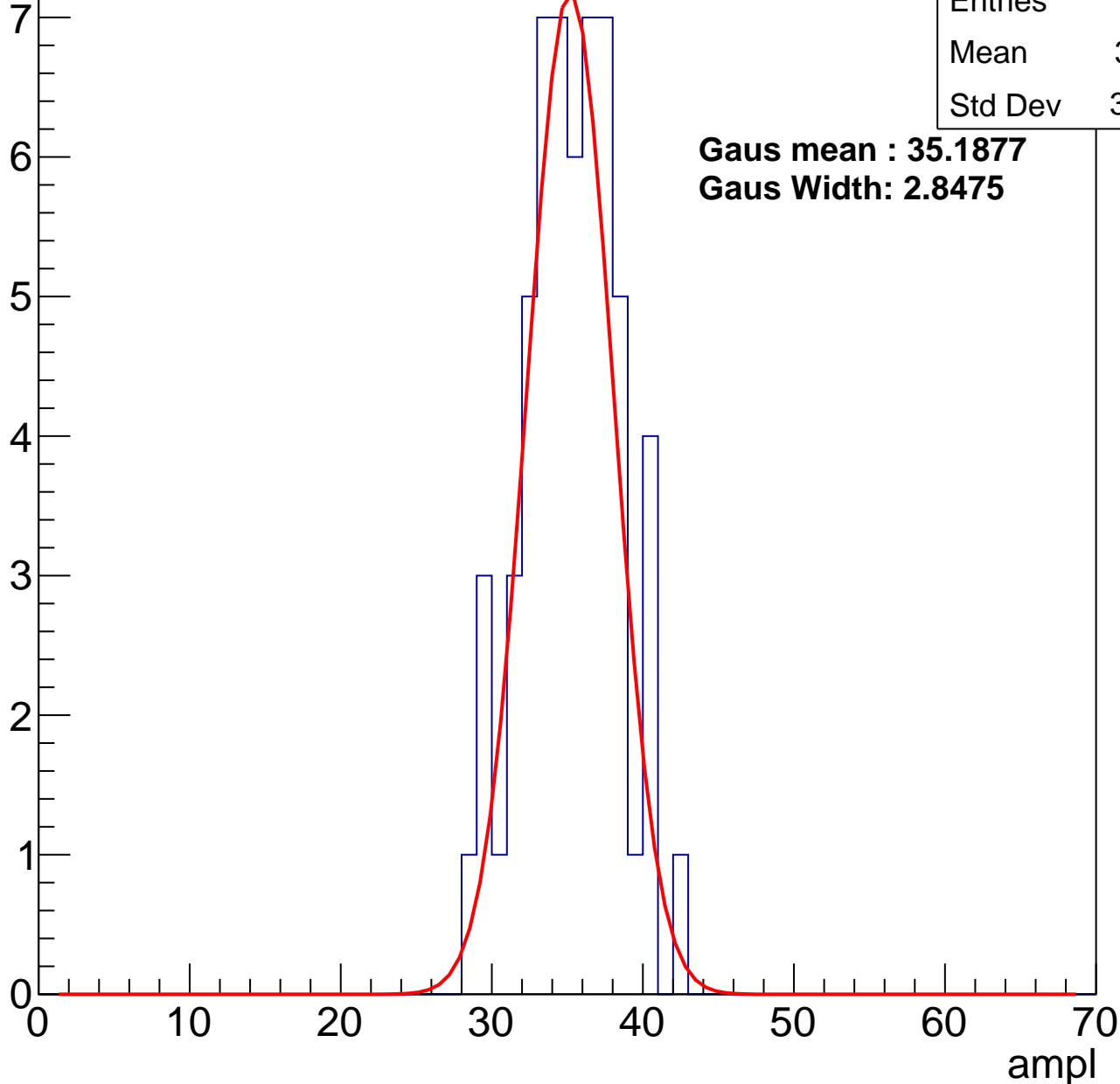
**Gaus Width: 3.5791**



# B1L000S, U21-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch49, adc2

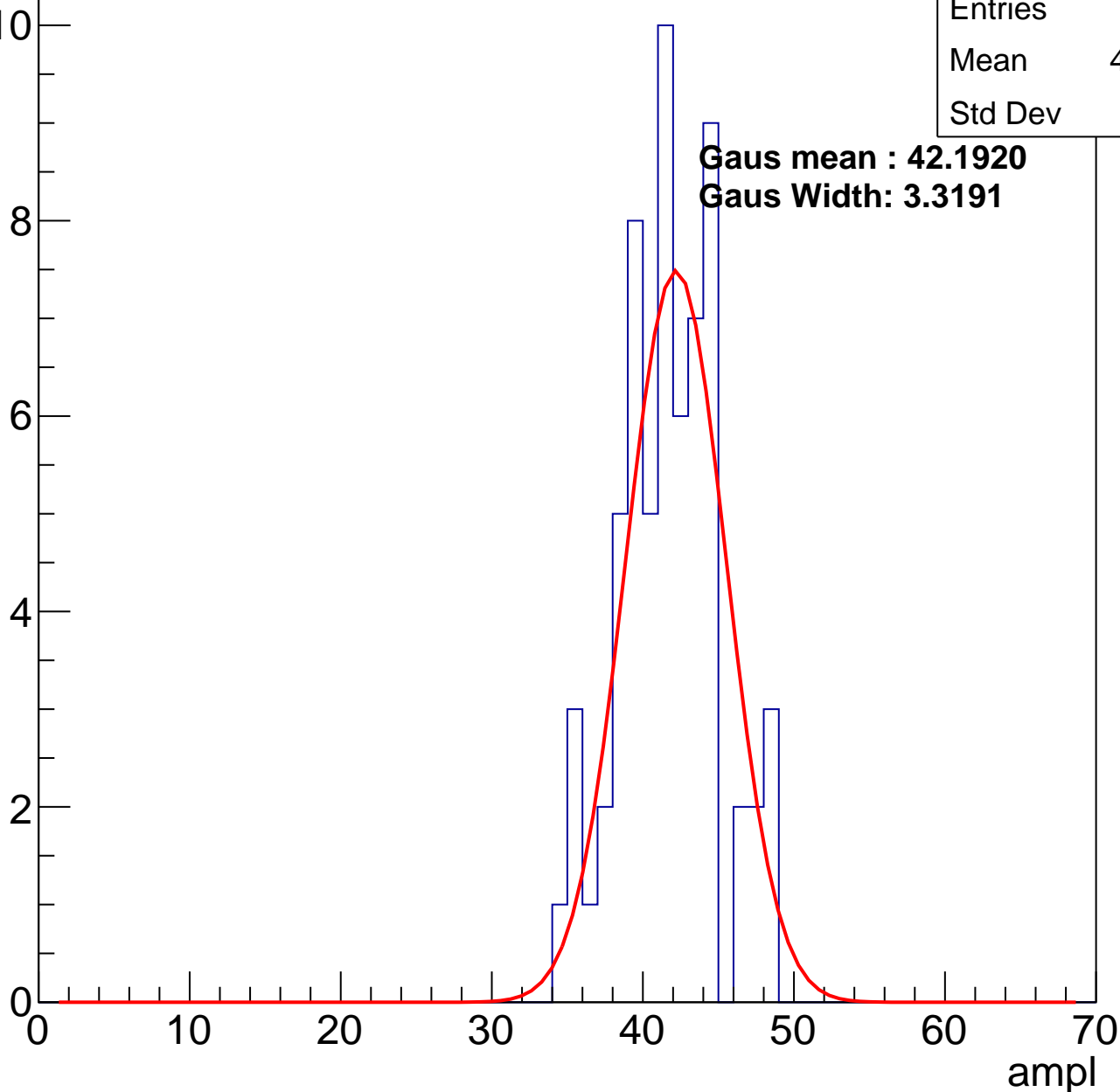
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.25
Std Dev	3.25

**Gaus mean : 42.1920**

**Gaus Width: 3.3191**

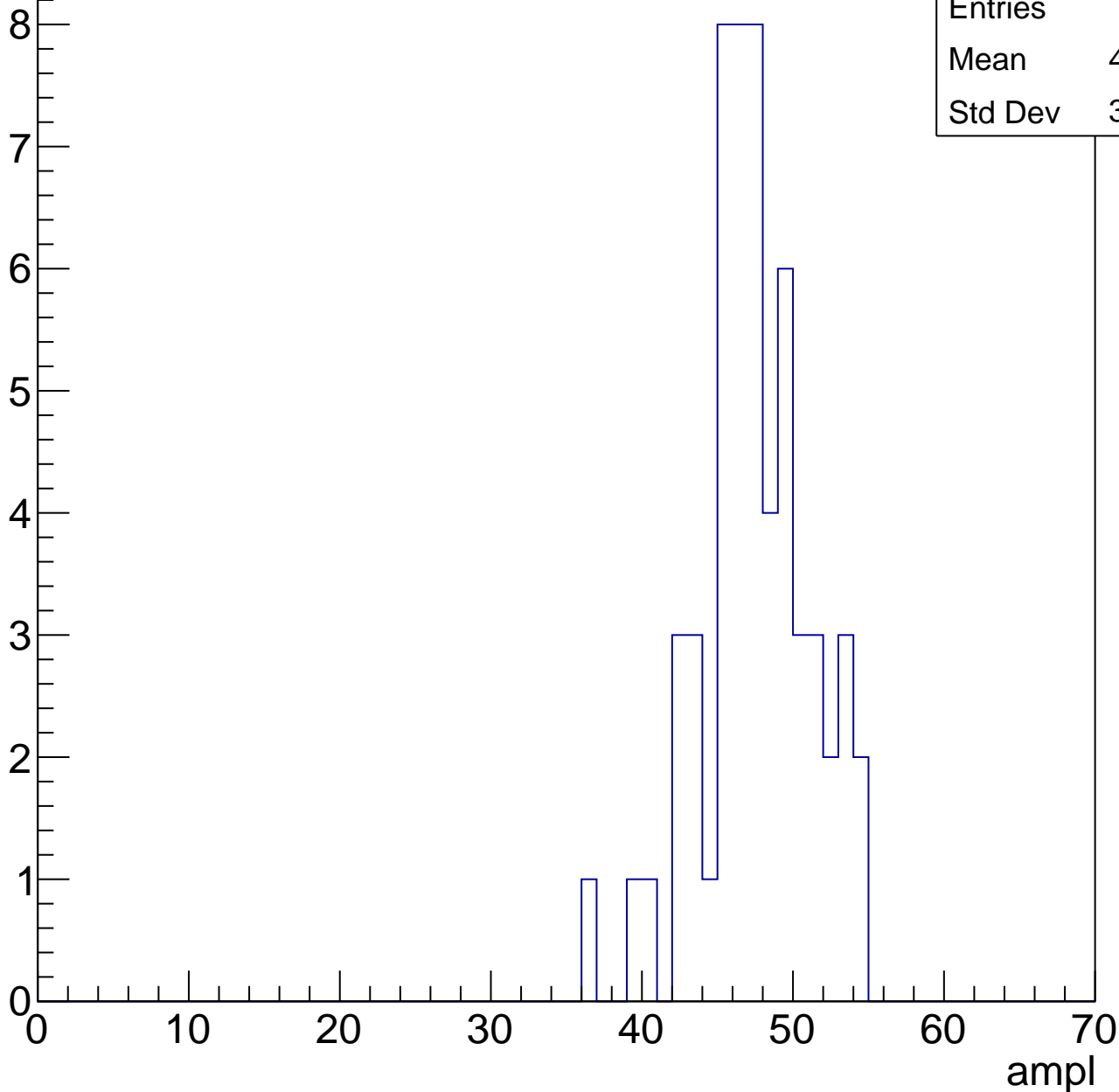


# B1L000S, U21-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	46.98
Std Dev	3.663

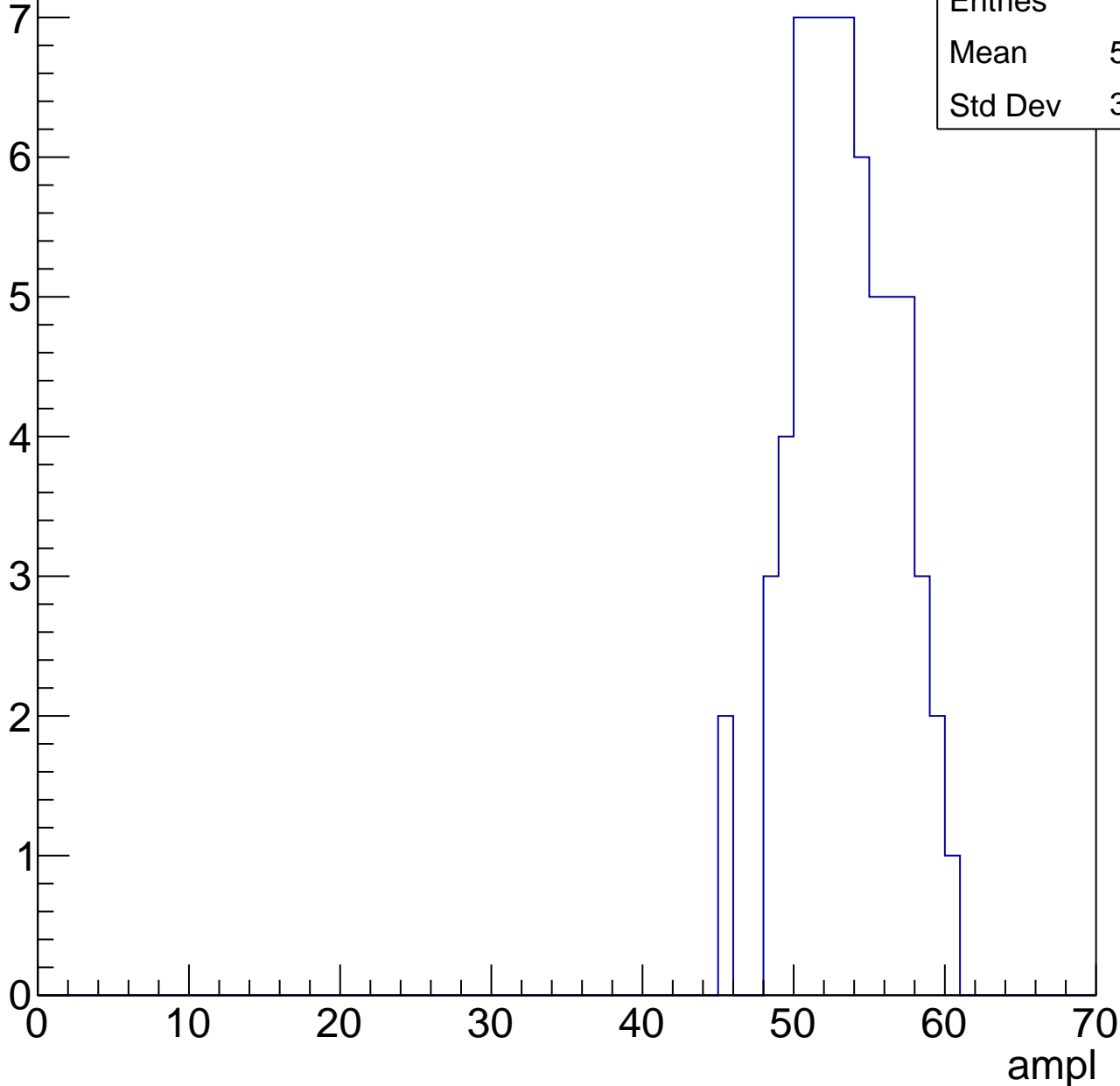


# B1L000S, U21-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	52.94
Std Dev	3.335



# B1L000S, U21-ch49, adc5

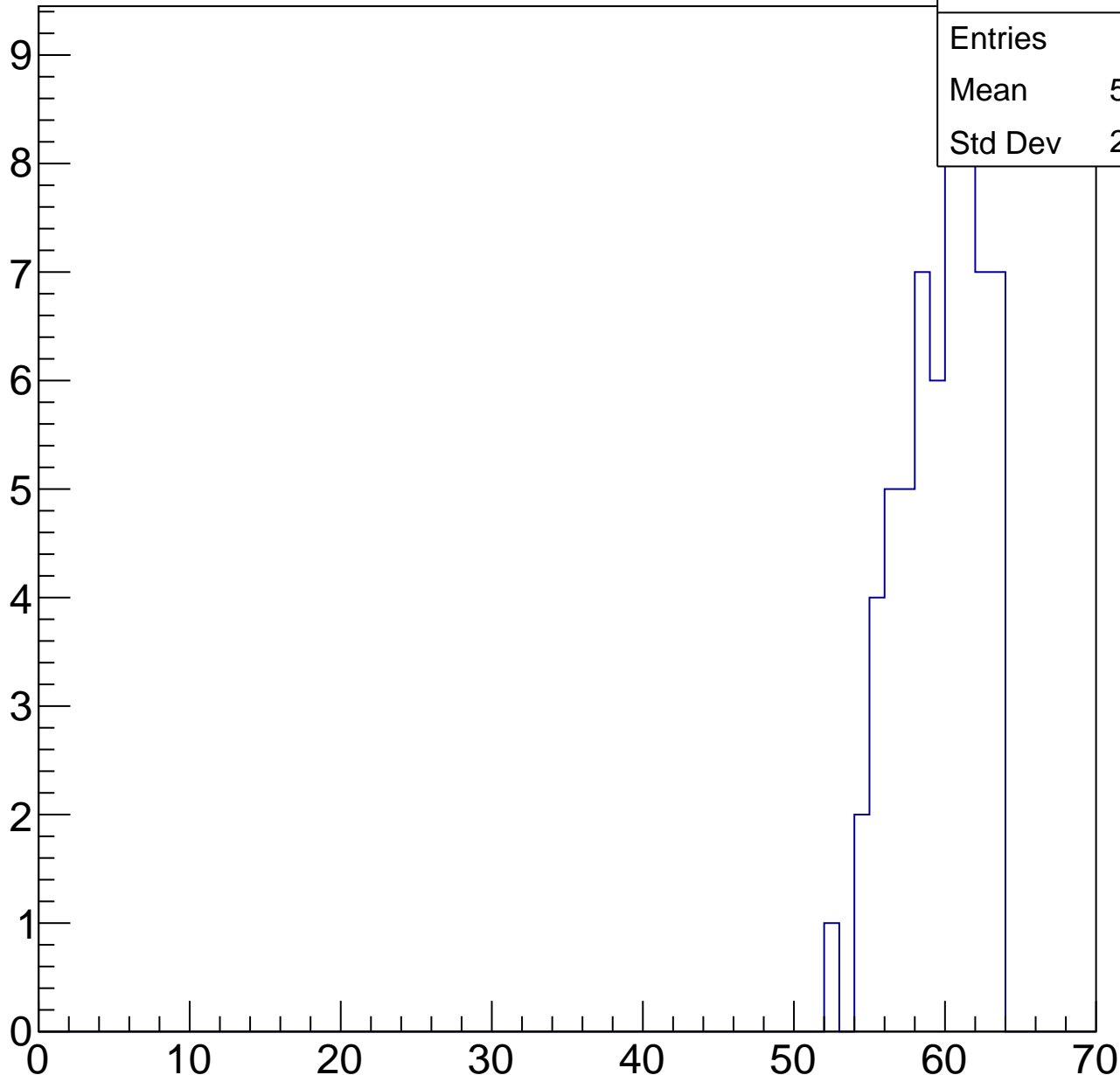
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	59.16
Std Dev	2.723

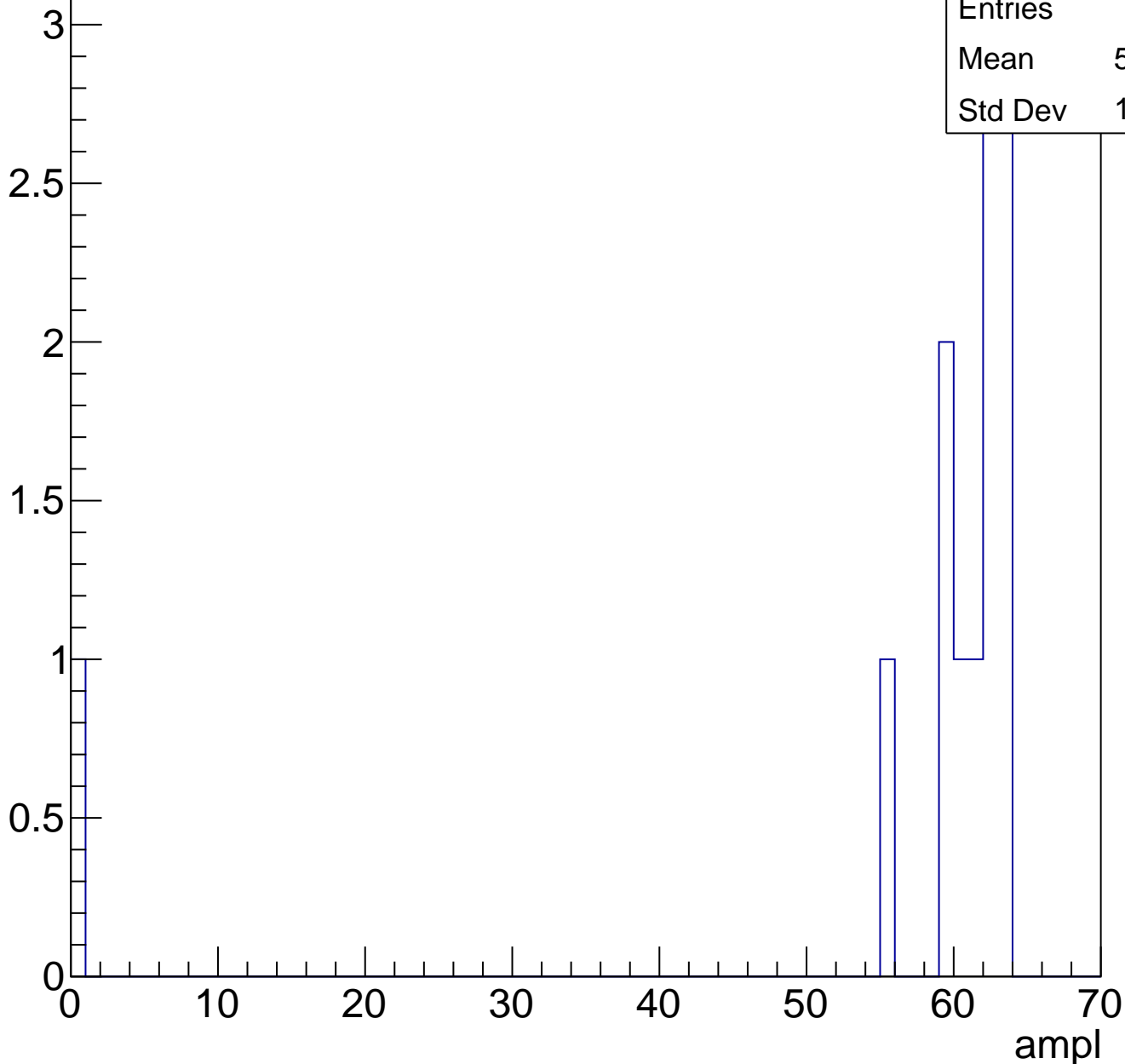
ampl



# B1L000S, U21-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch50, adc0

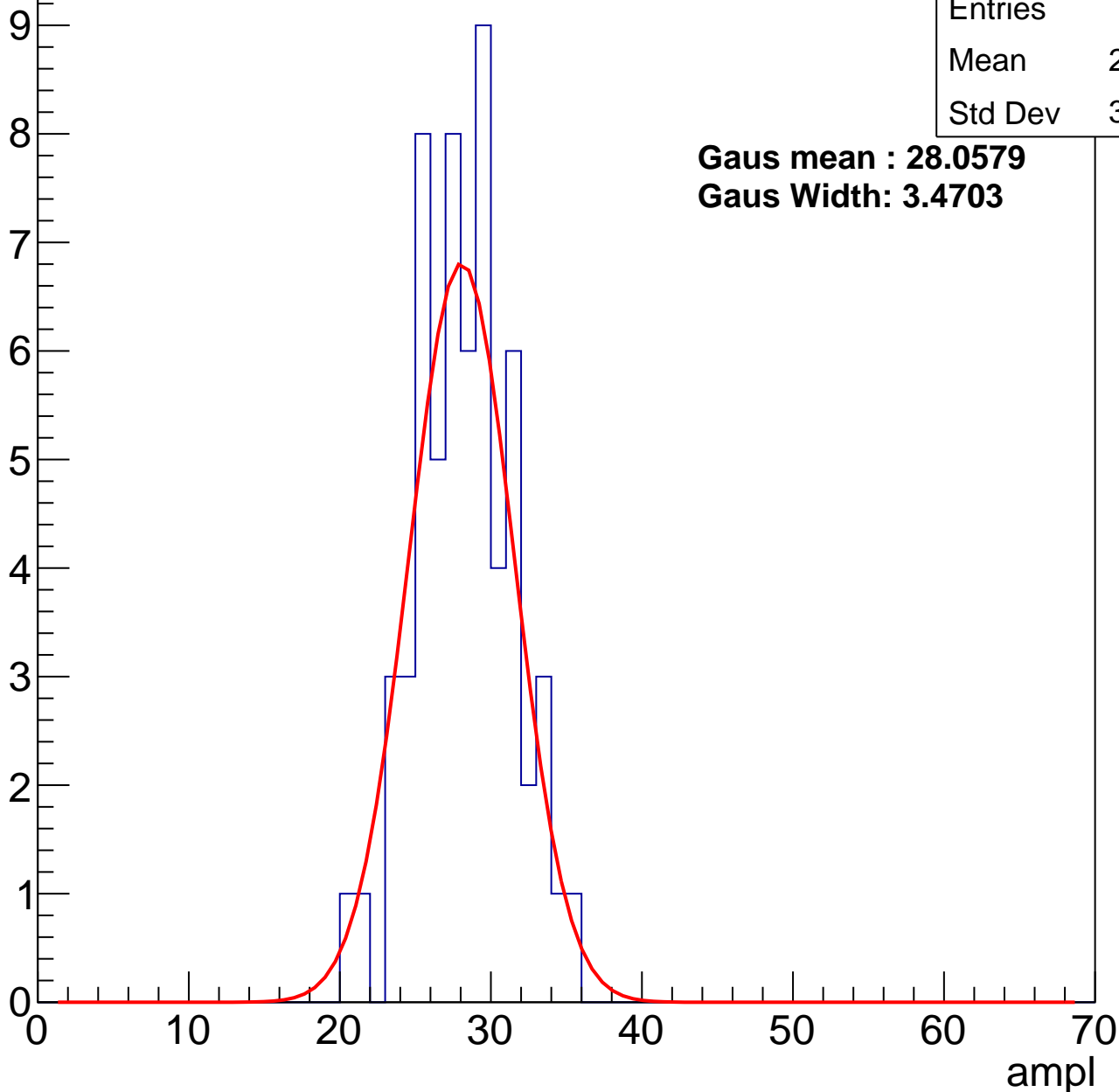
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	27.79
Std Dev	3.147

**Gaus mean : 28.0579**

**Gaus Width: 3.4703**



# B1L000S, U21-ch50, adc1

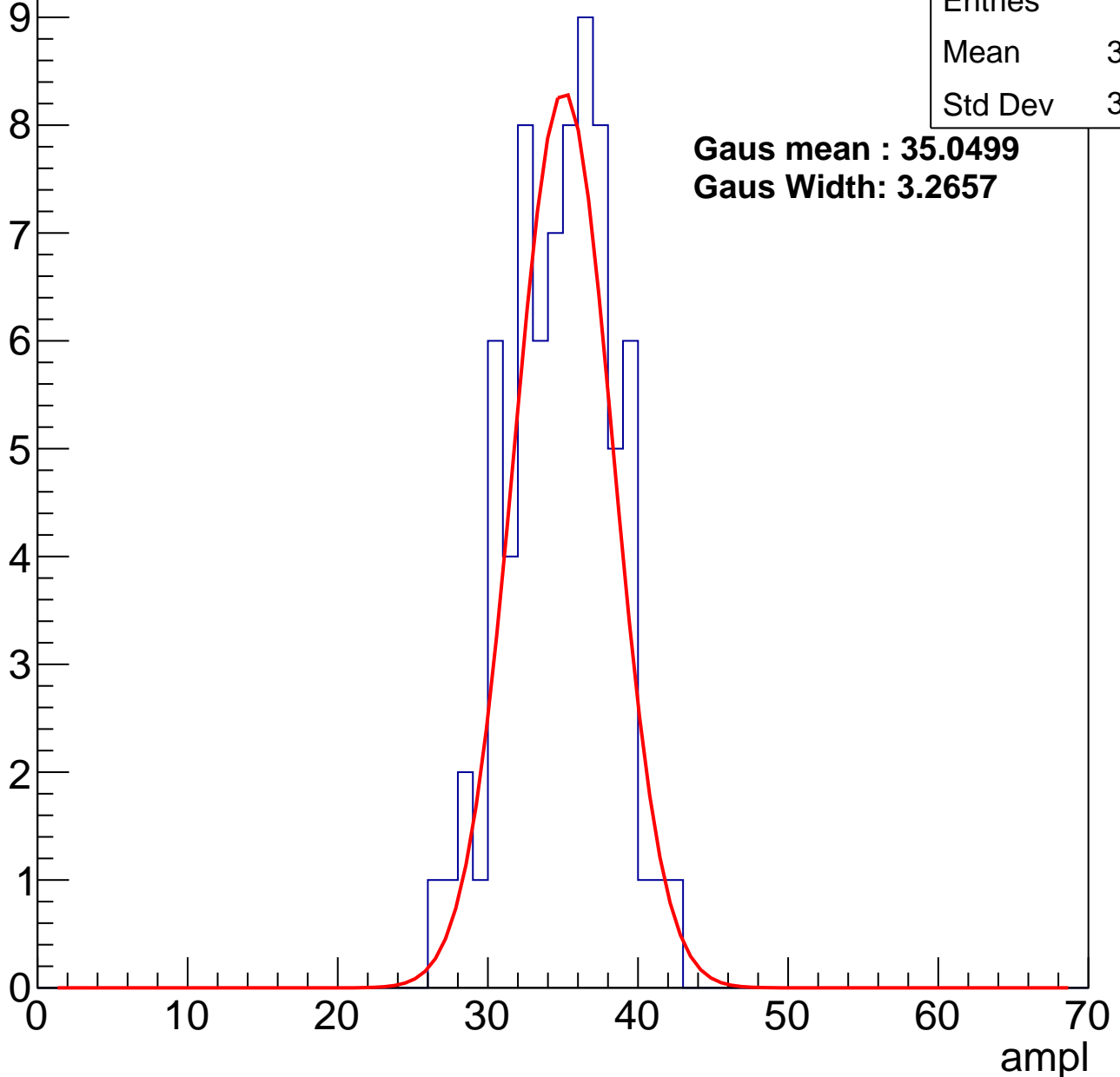
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	34.41
Std Dev	3.391

**Gaus mean : 35.0499**

**Gaus Width: 3.2657**



# B1L000S, U21-ch50, adc2

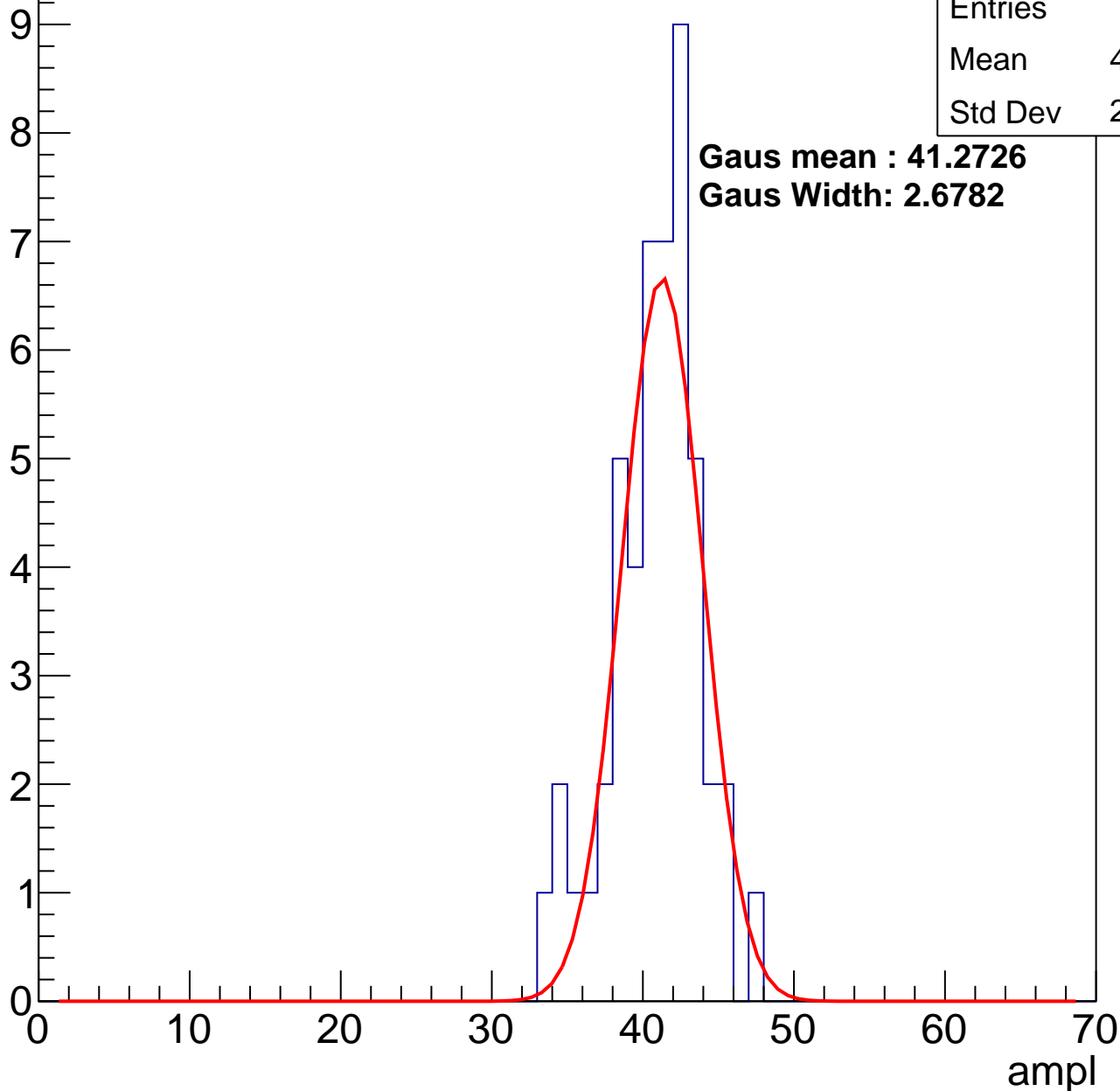
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	40.35
Std Dev	2.918

**Gaus mean : 41.2726**

**Gaus Width: 2.6782**

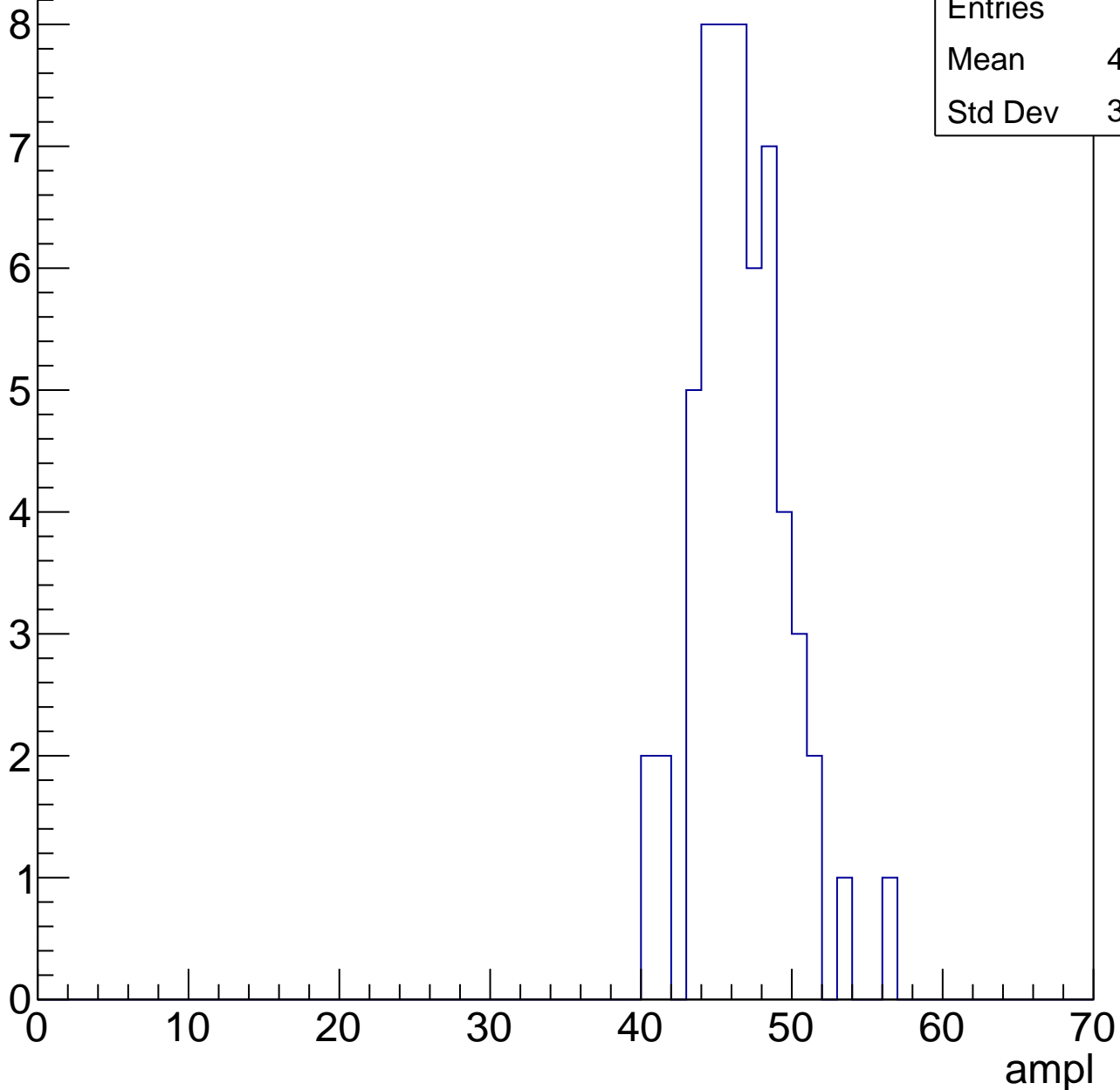


# B1L000S, U21-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	46.18
Std Dev	3.027



# B1L000S, U21-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

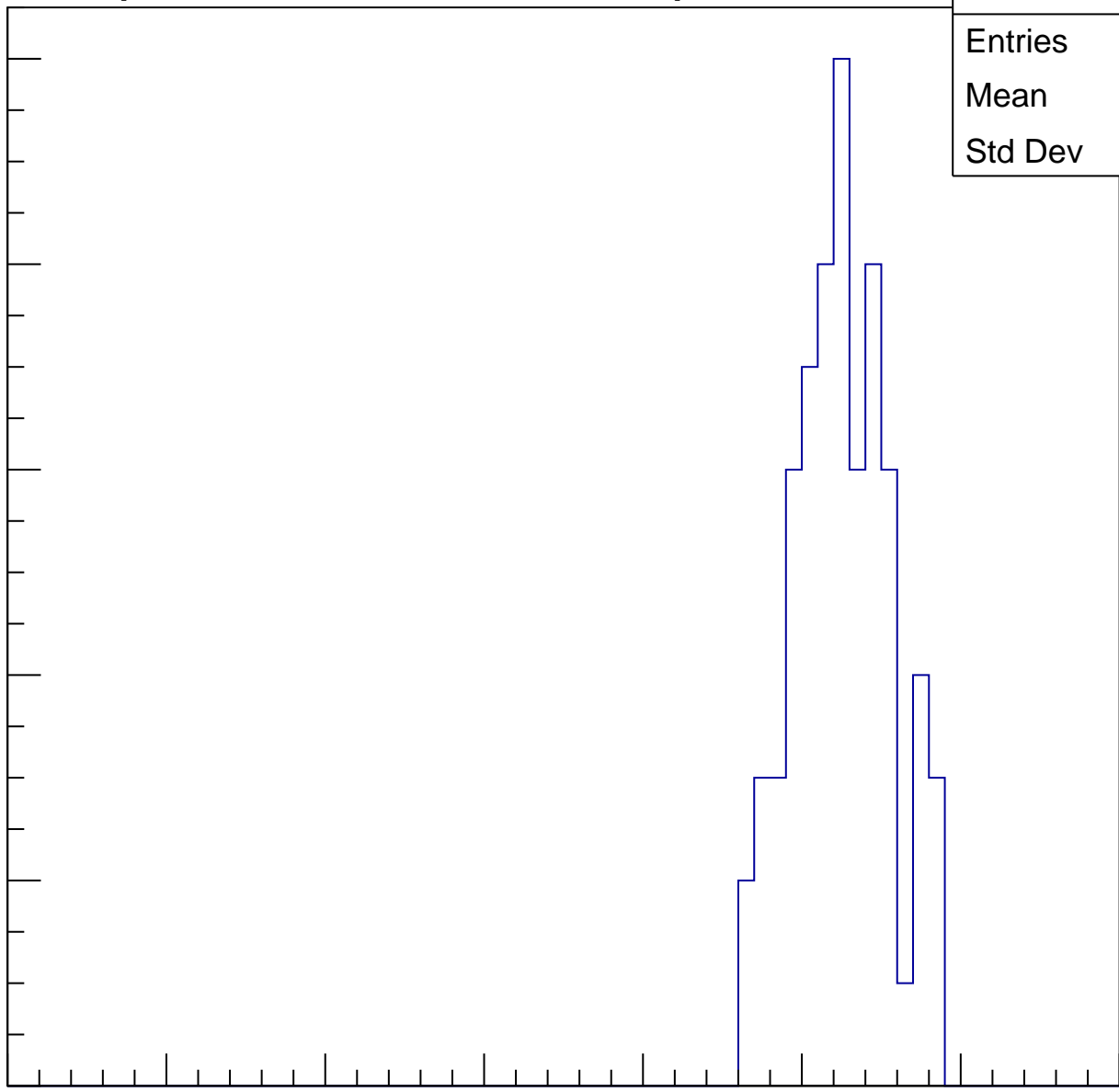
Entries	67
Mean	52.04
Std Dev	2.995

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

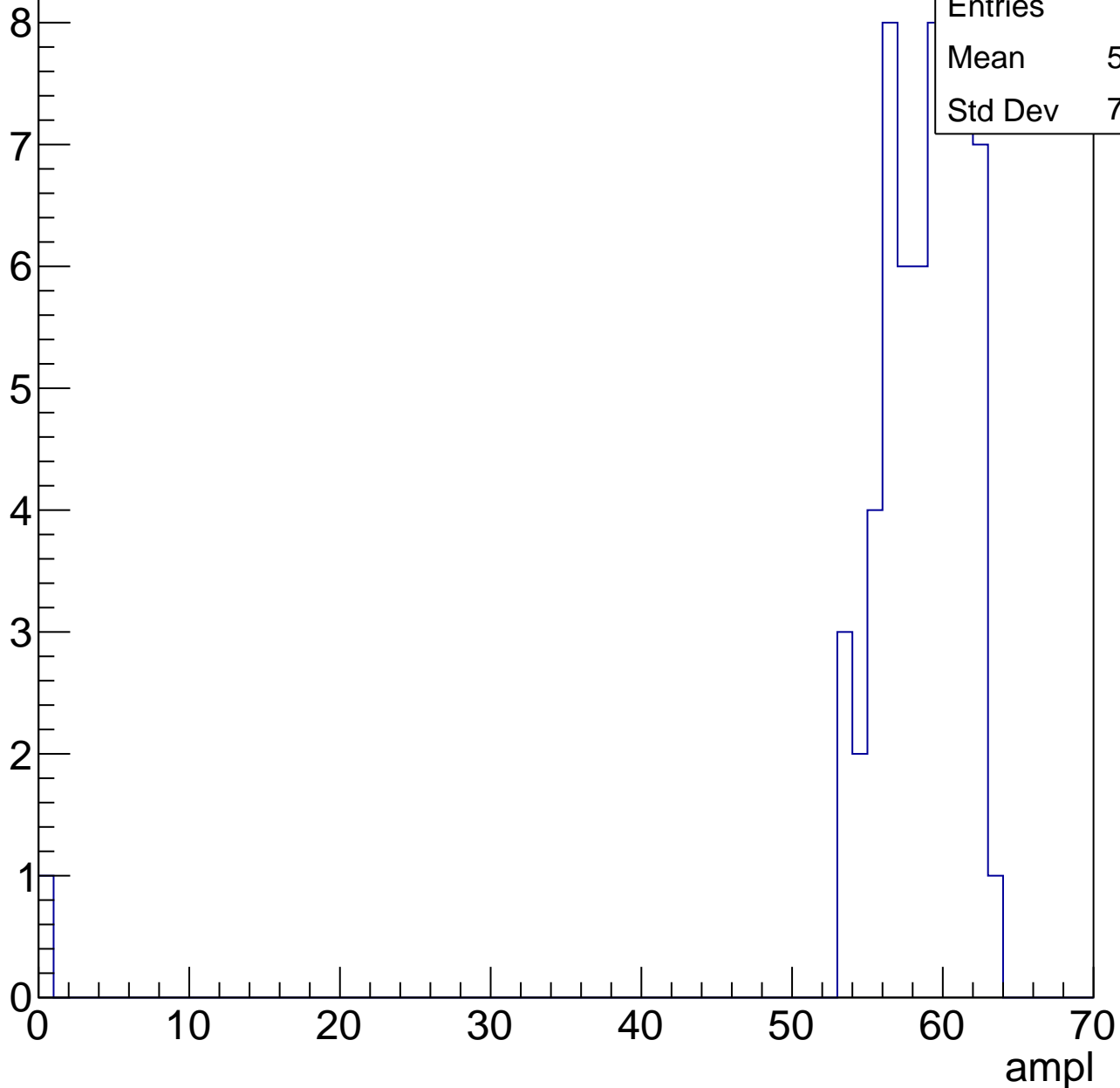
ampl



# B1L000S, U21-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

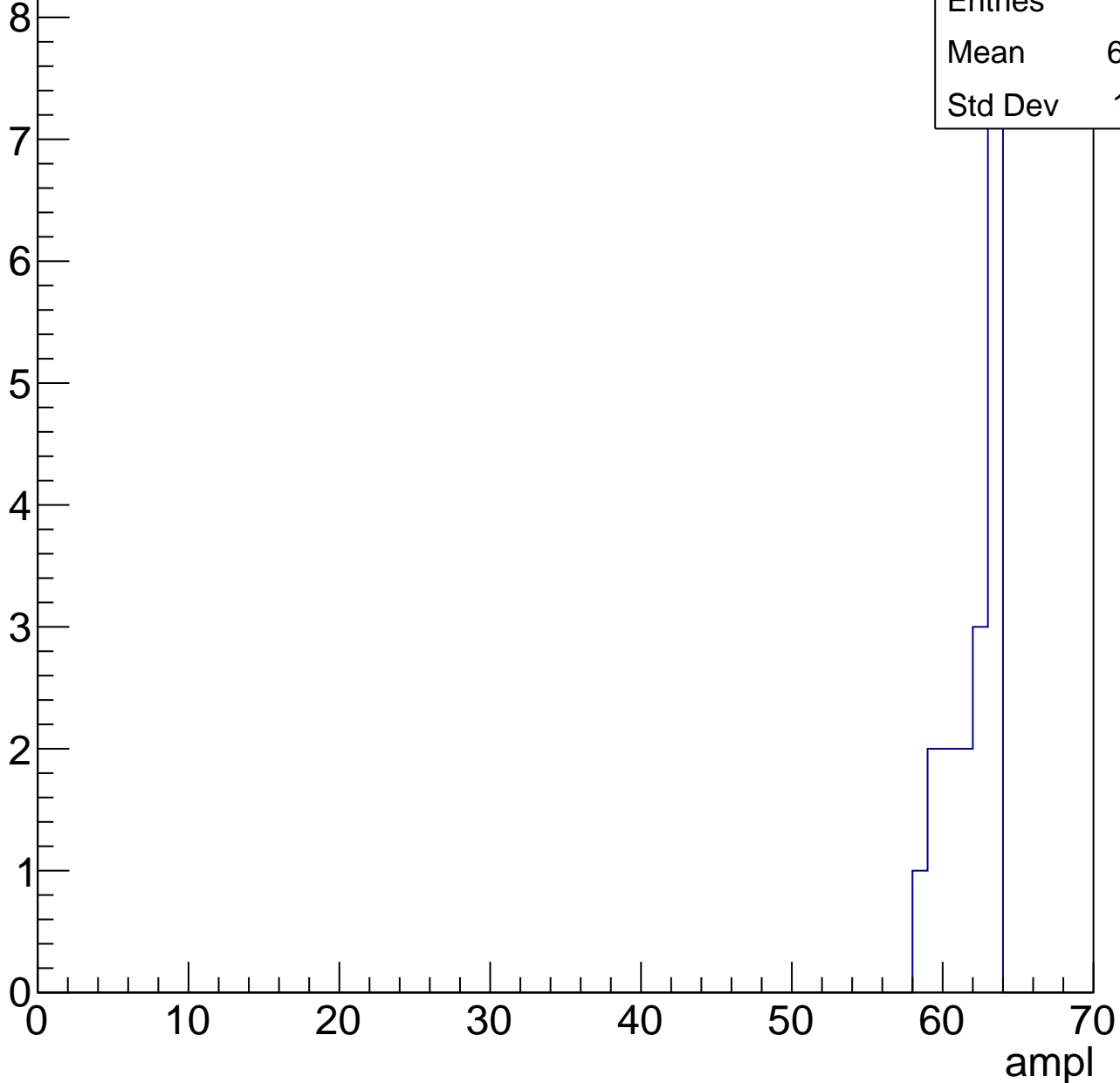


# B1L000S, U21-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.56
Std Dev	1.641





# B1L000S, U21-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch51, adc0

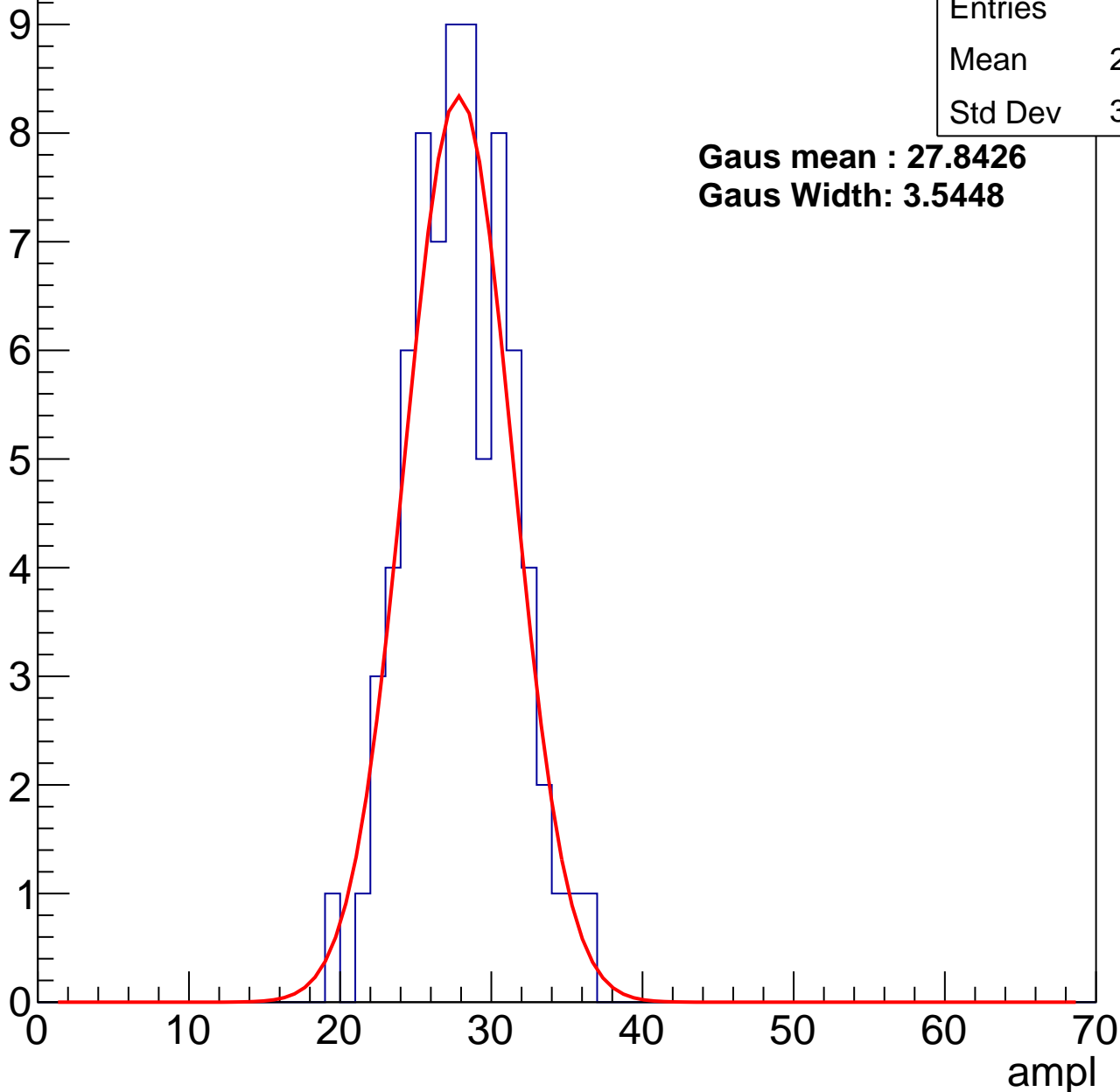
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	27.49
Std Dev	3.397

**Gaus mean : 27.8426**

**Gaus Width: 3.5448**



# B1L000S, U21-ch51, adc1

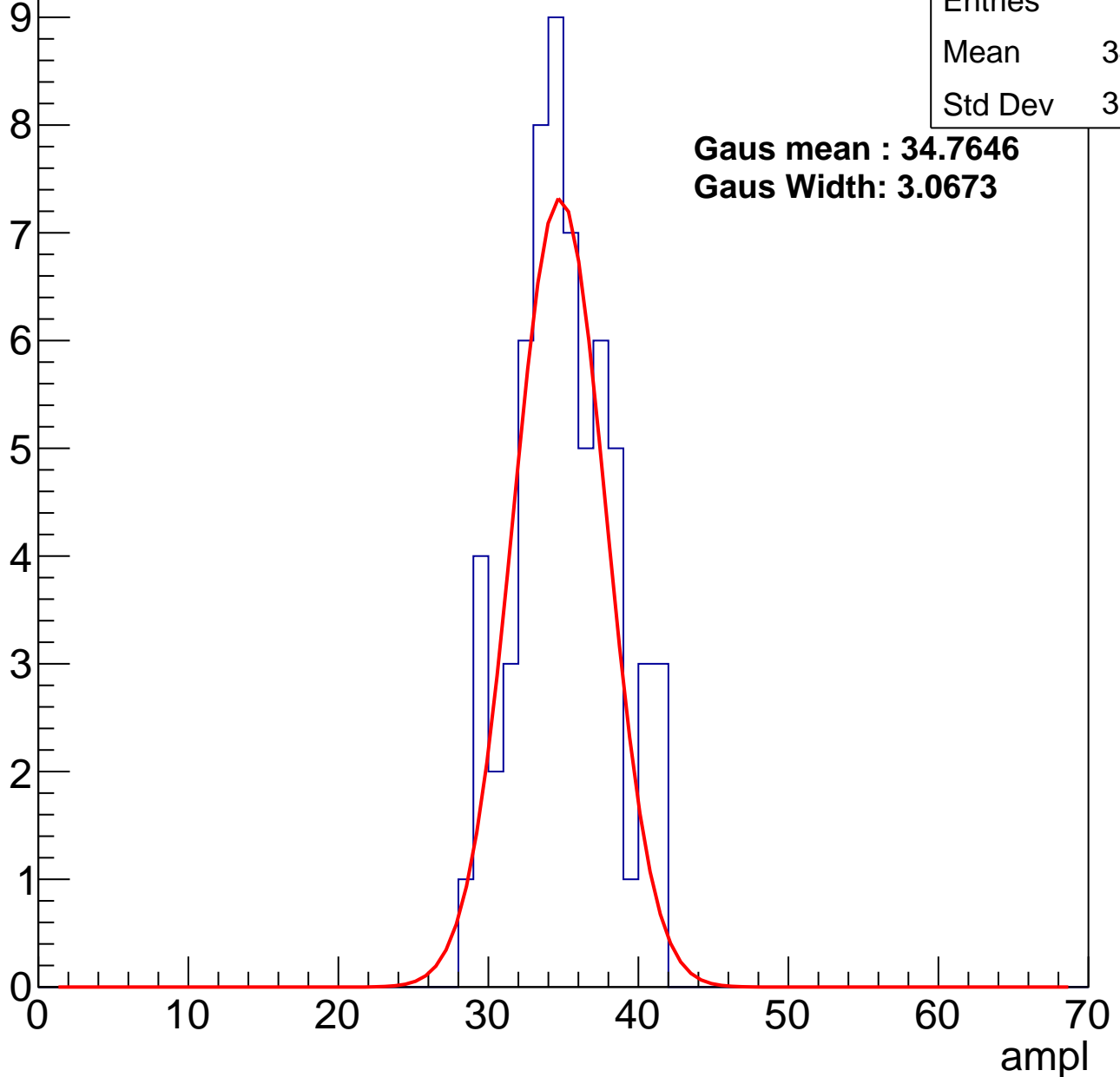
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	34.57
Std Dev	3.206

**Gaus mean : 34.7646**

**Gaus Width: 3.0673**



# B1L000S, U21-ch51, adc2

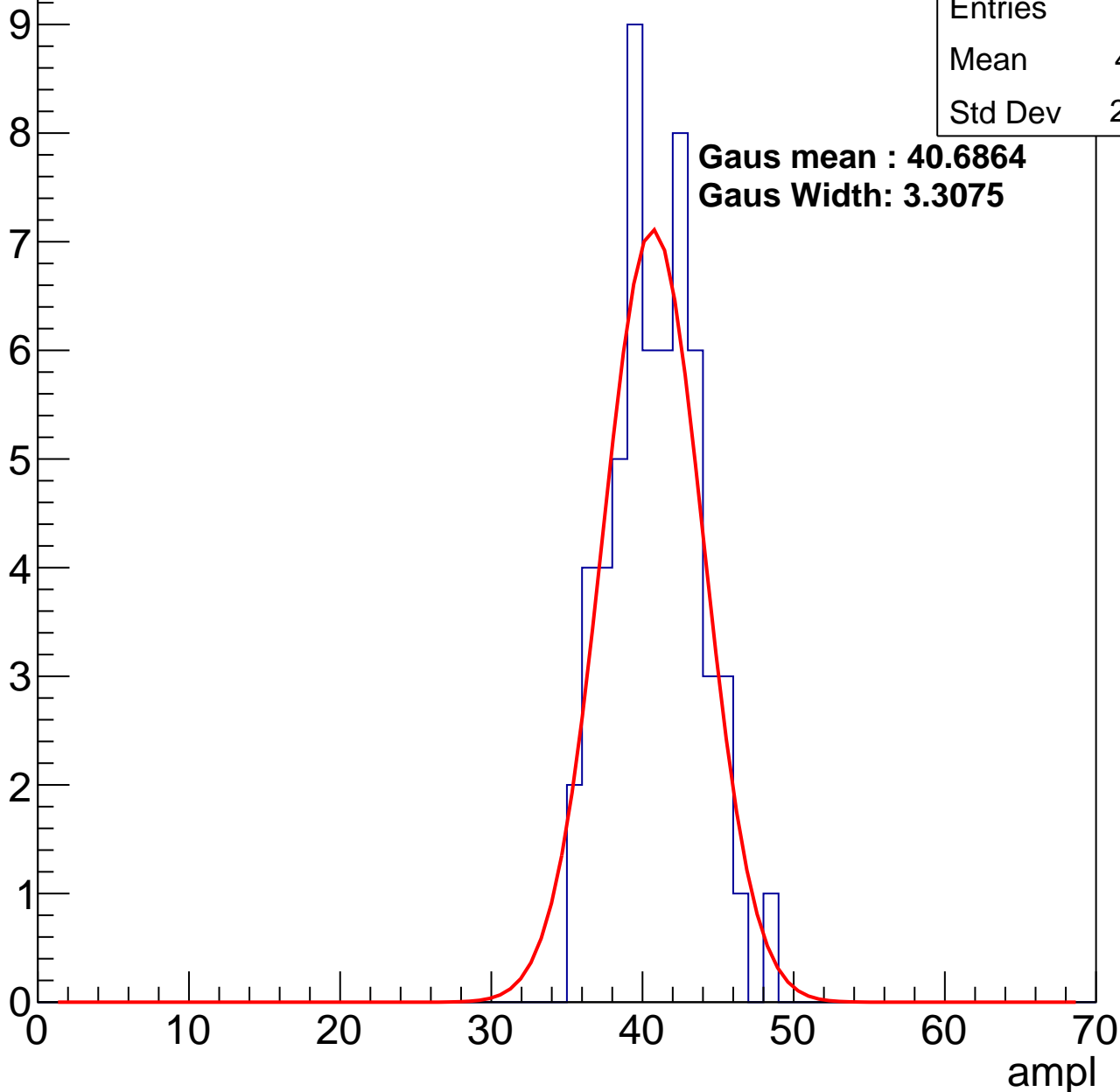
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	40.41
Std Dev	2.889

**Gaus mean : 40.6864**

**Gaus Width: 3.3075**

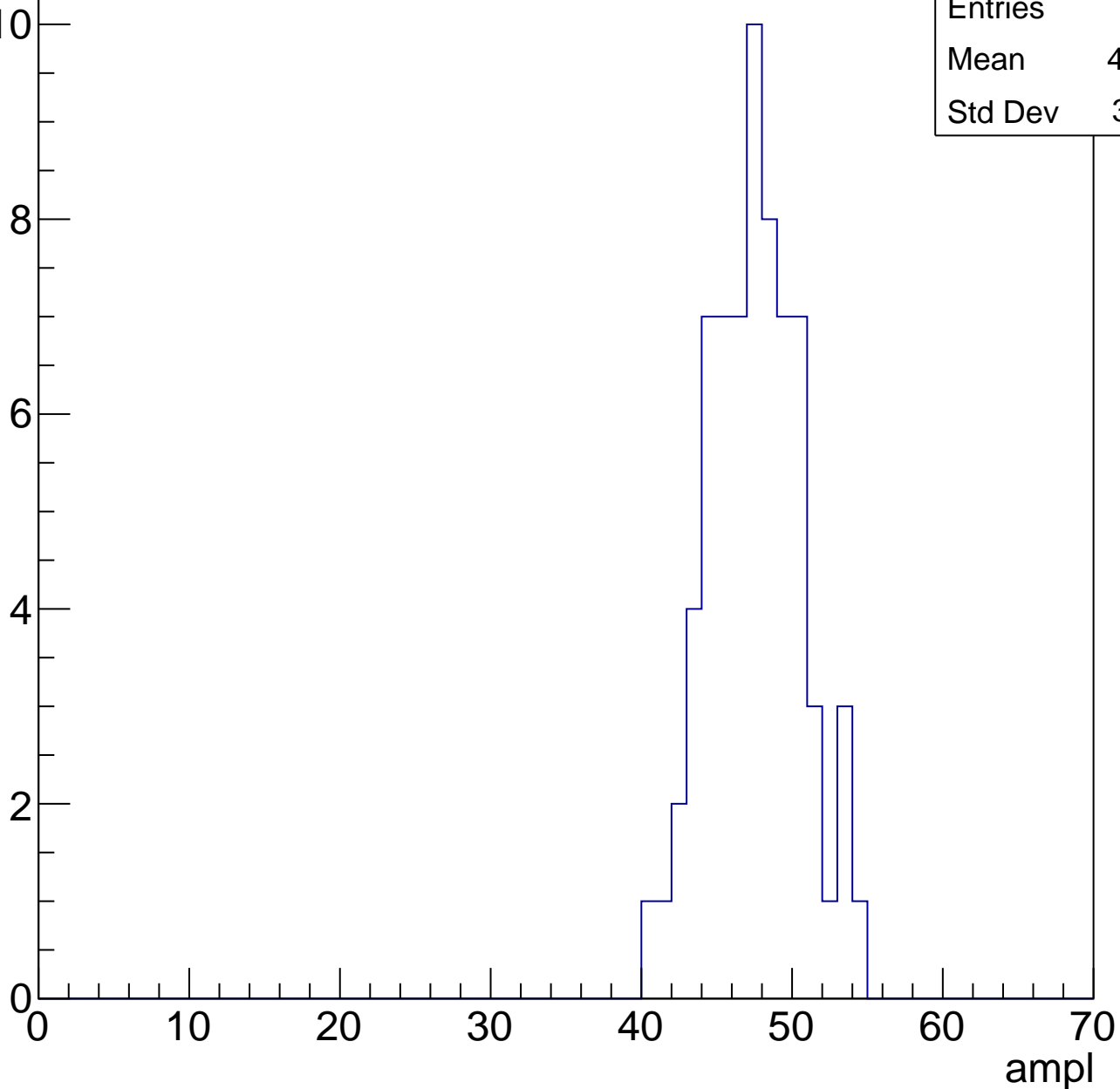


# B1L000S, U21-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	47.06
Std Dev	3.011

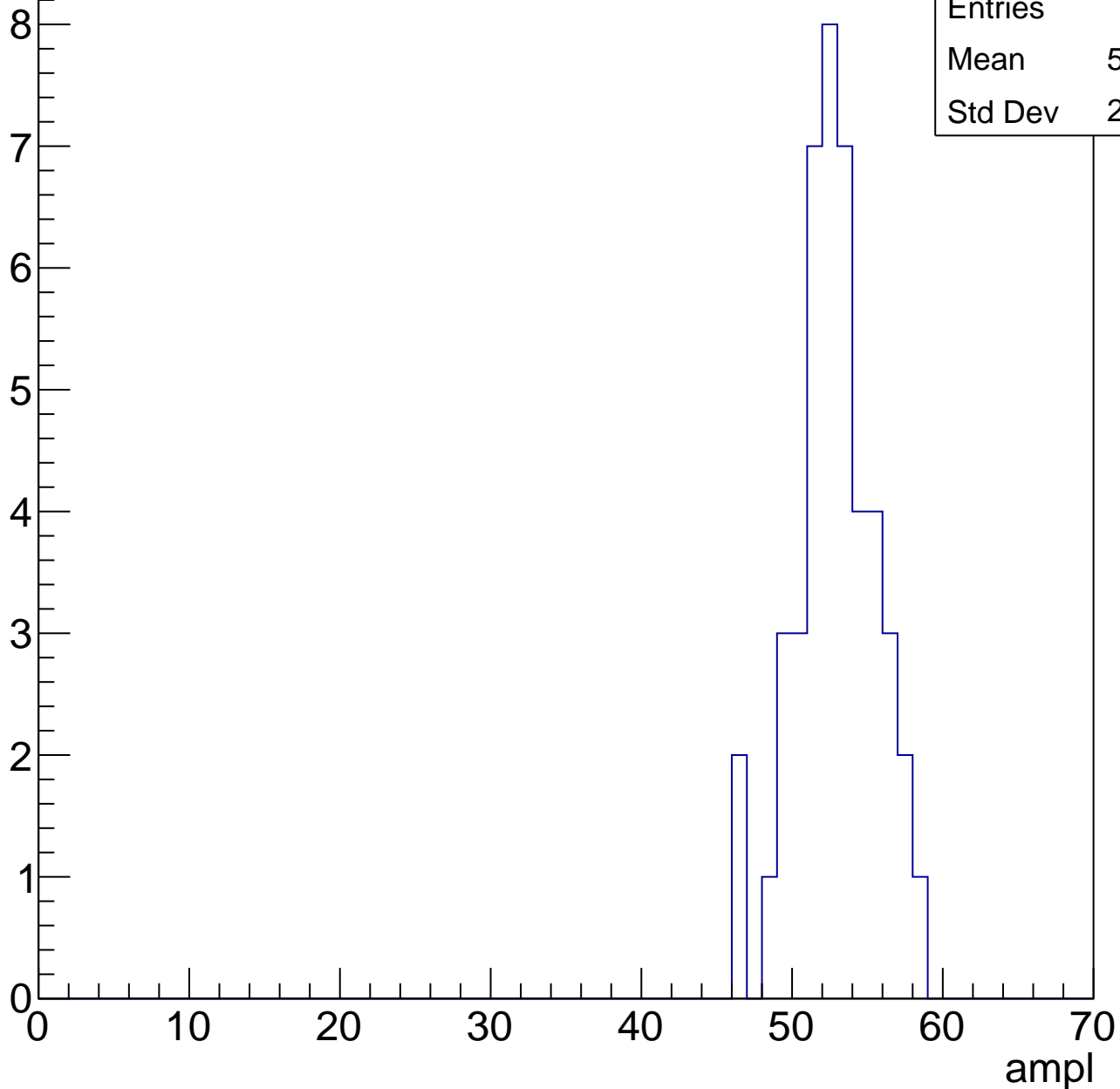


# B1L000S, U21-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	52.38
Std Dev	2.669

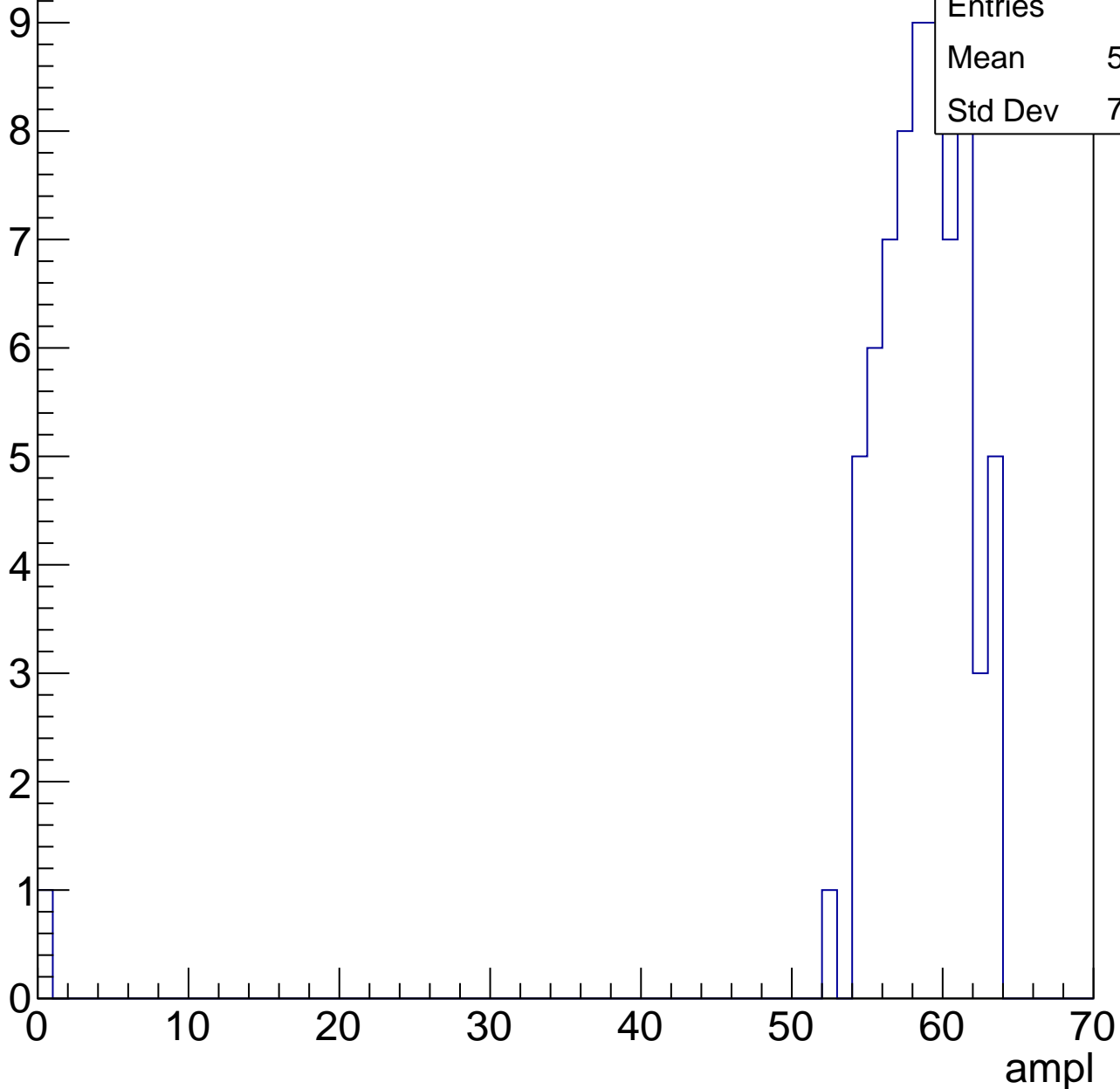


# B1L000S, U21-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	57.42
Std Dev	7.449

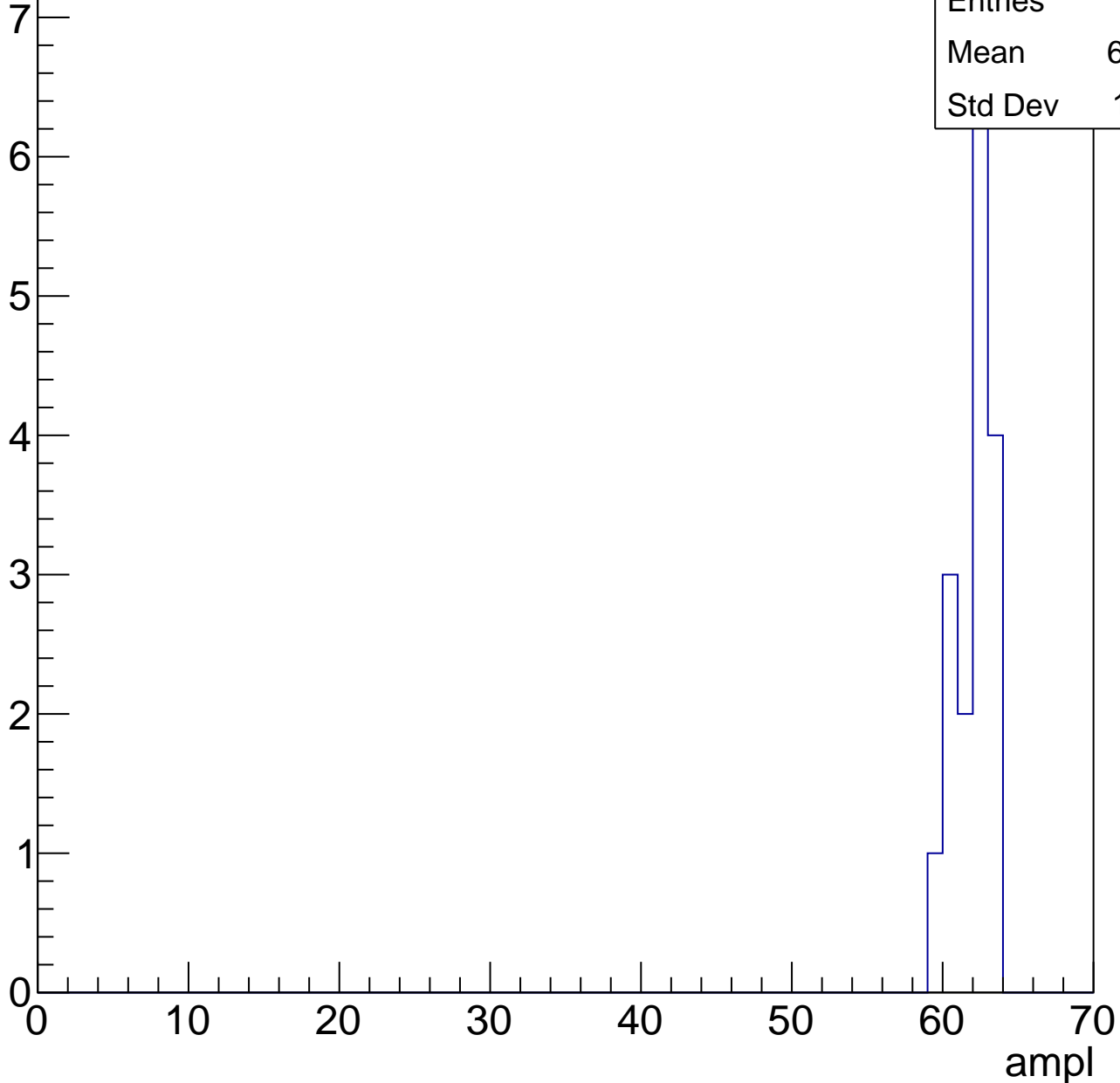


# B1L000S, U21-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	61.59
Std Dev	1.191





# B1L000S, U21-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch52, adc0

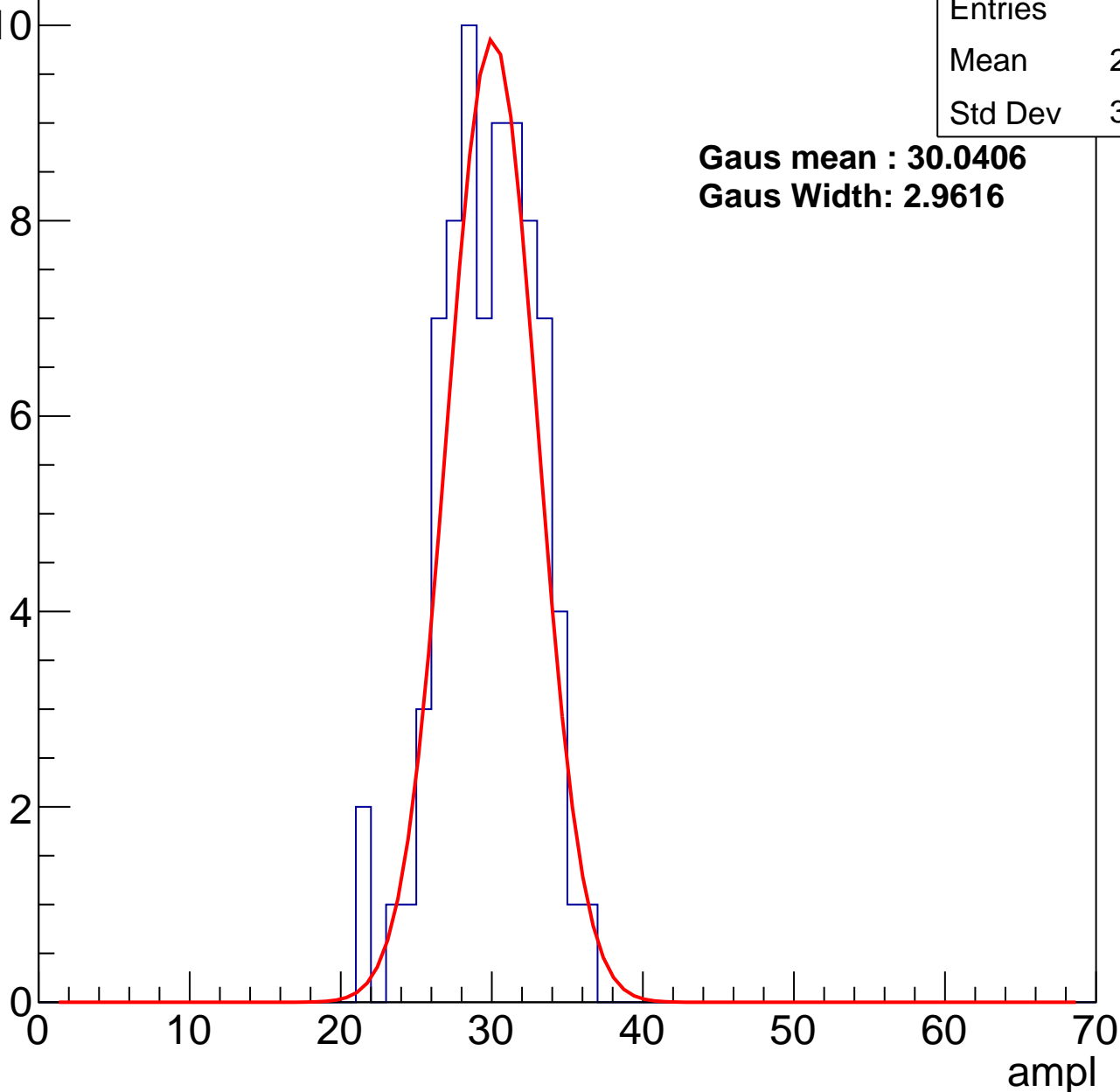
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	29.33
Std Dev	3.095

**Gaus mean : 30.0406**

**Gaus Width: 2.9616**



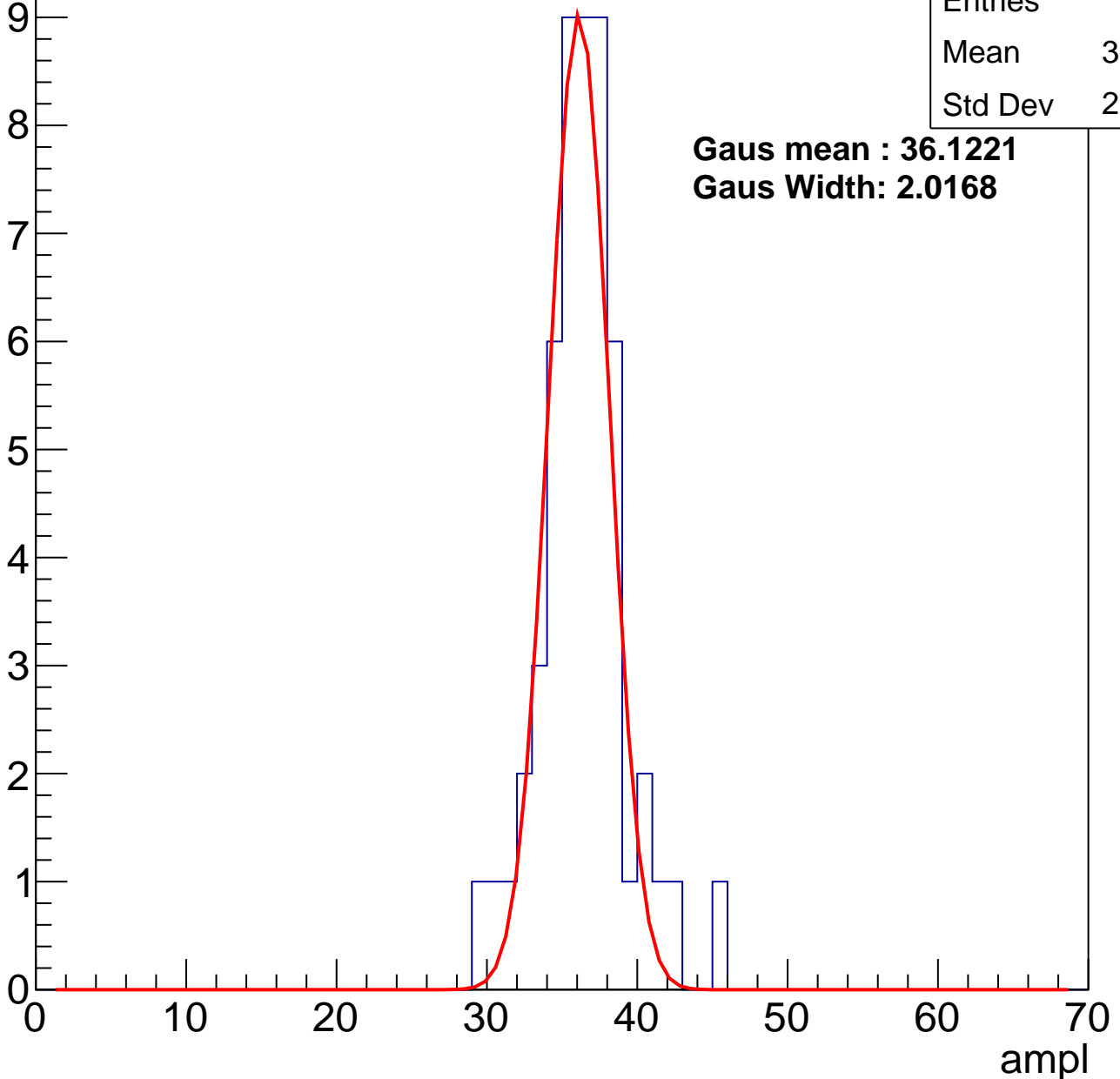
# B1L000S, U21-ch52, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	35.92
Std Dev	2.807

**Gaus mean : 36.1221**  
**Gaus Width: 2.0168**



# B1L000S, U21-ch52, adc2

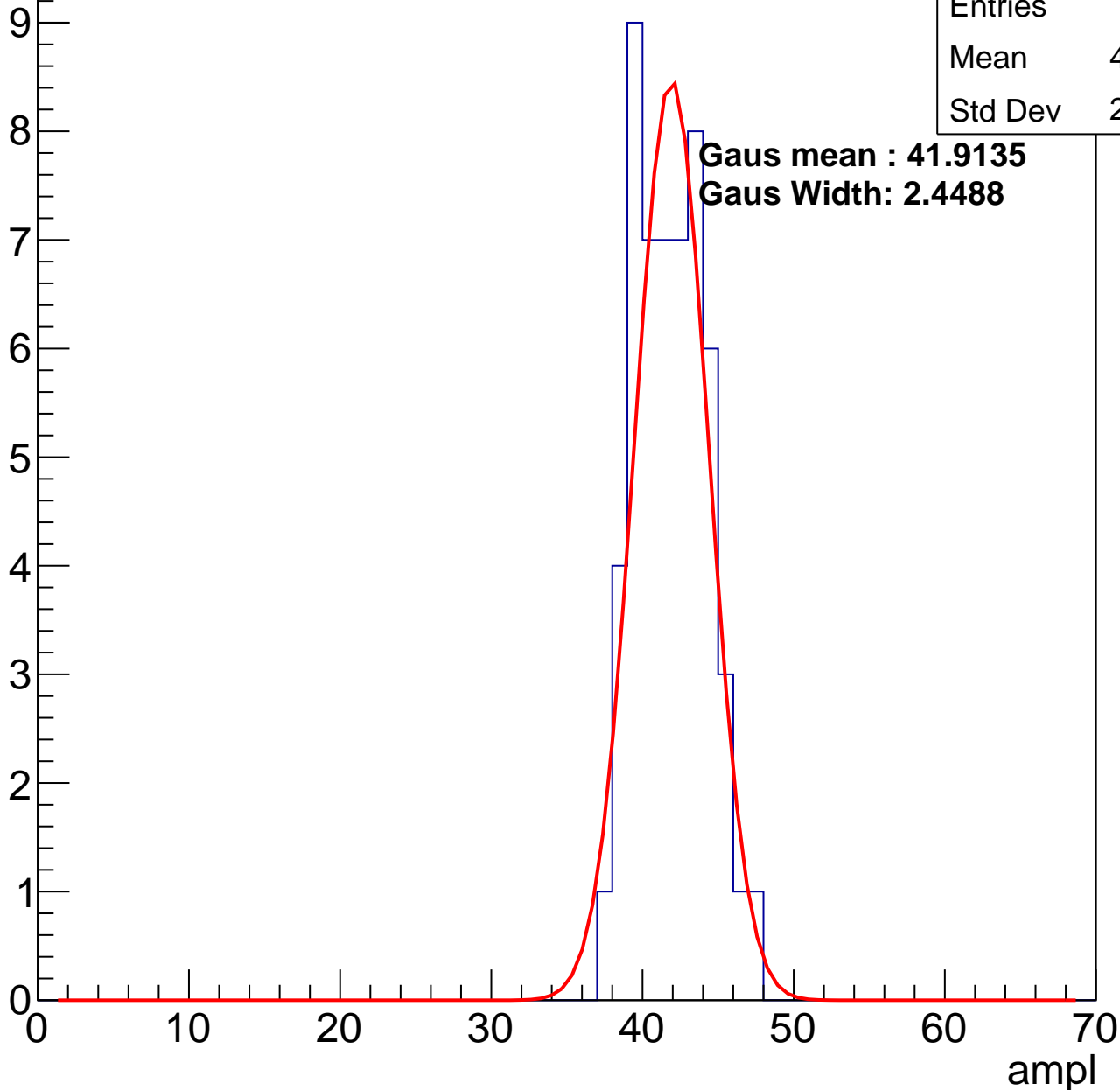
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.43
Std Dev	2.306

**Gaus mean : 41.9135**

**Gaus Width: 2.4488**



# B1L000S, U21-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

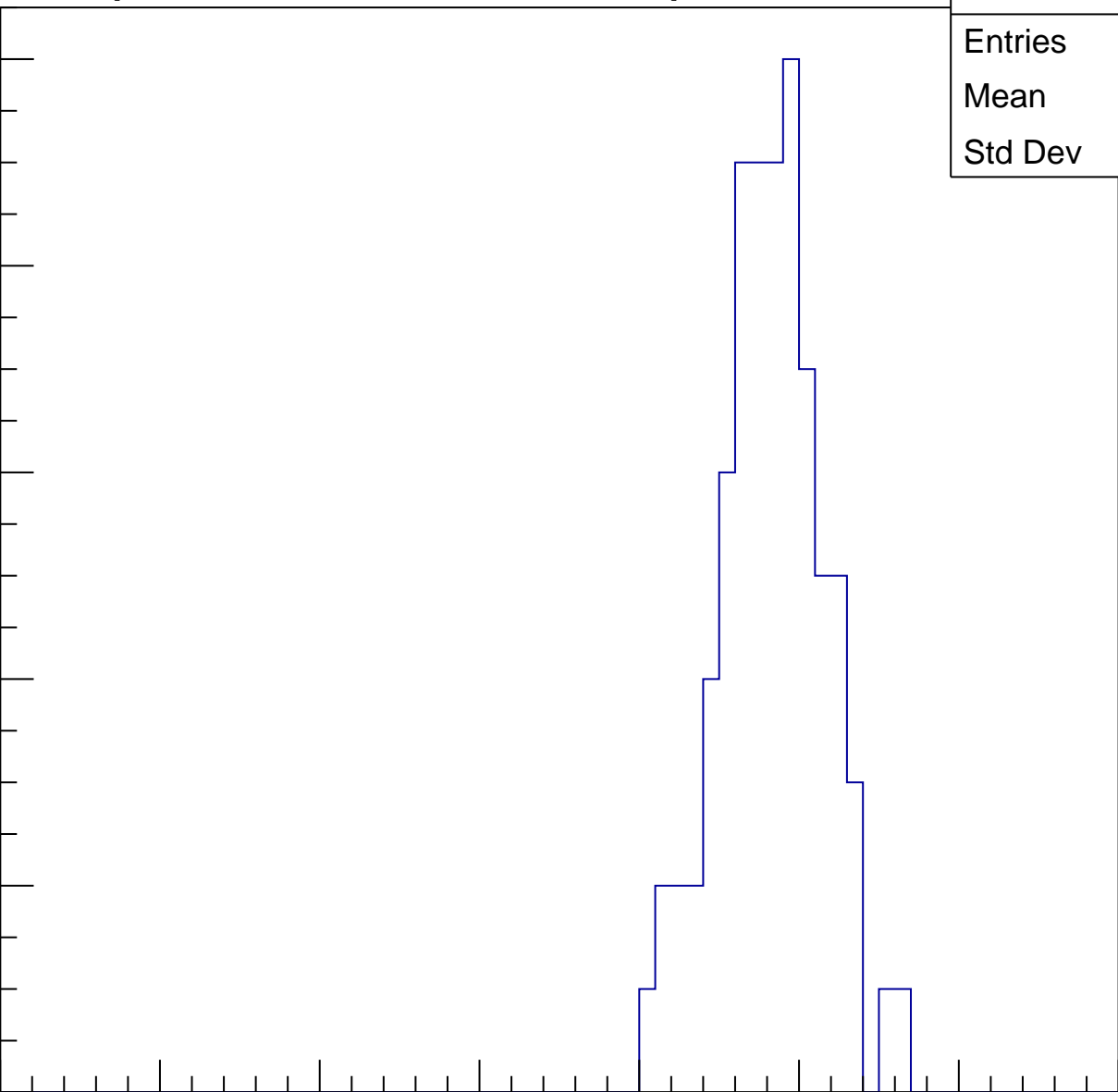
Entries	76
Mean	47.79
Std Dev	3.234

Entry

10  
8  
6  
4  
2  
0

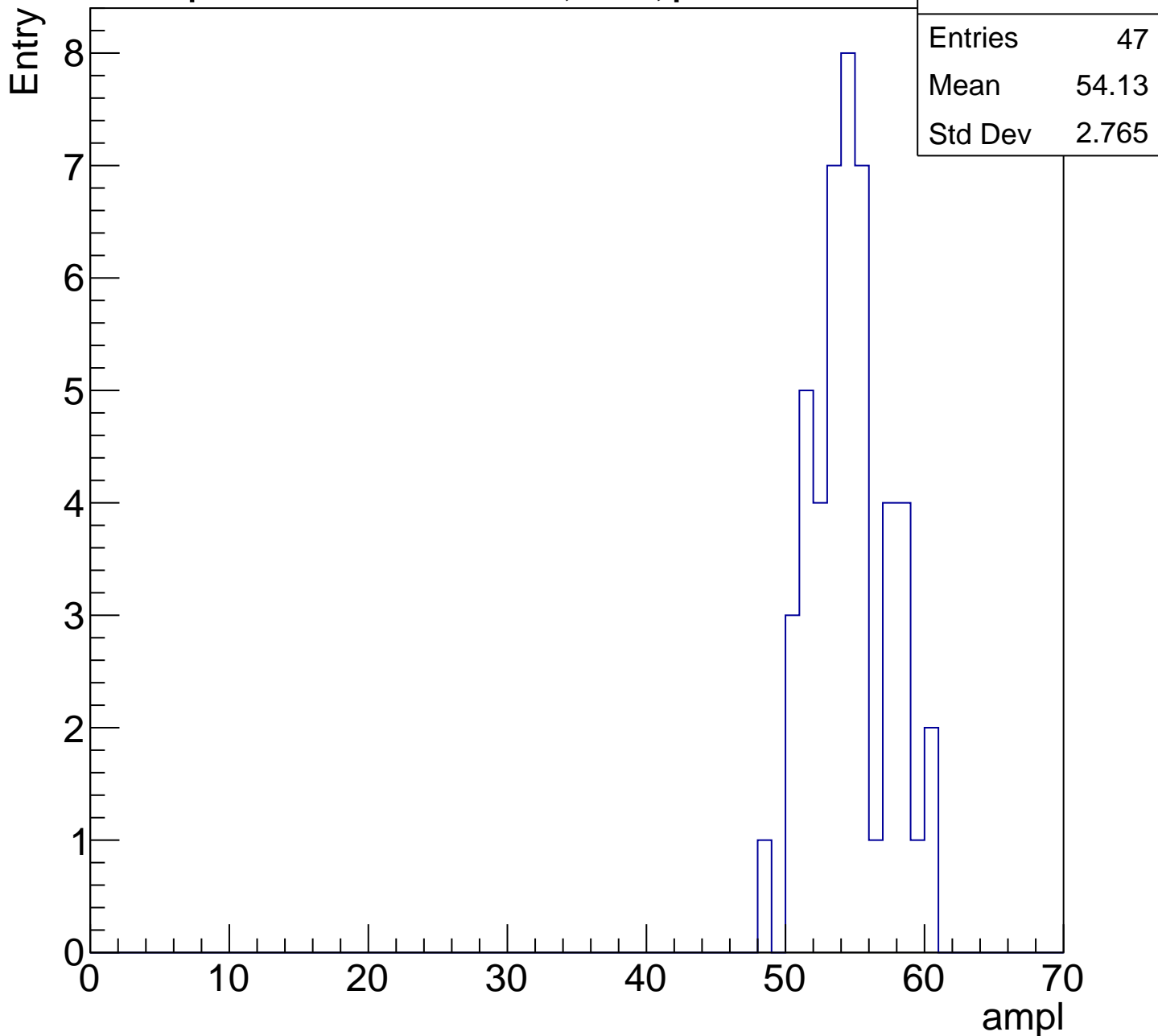
0 10 20 30 40 50 60 70

ampl



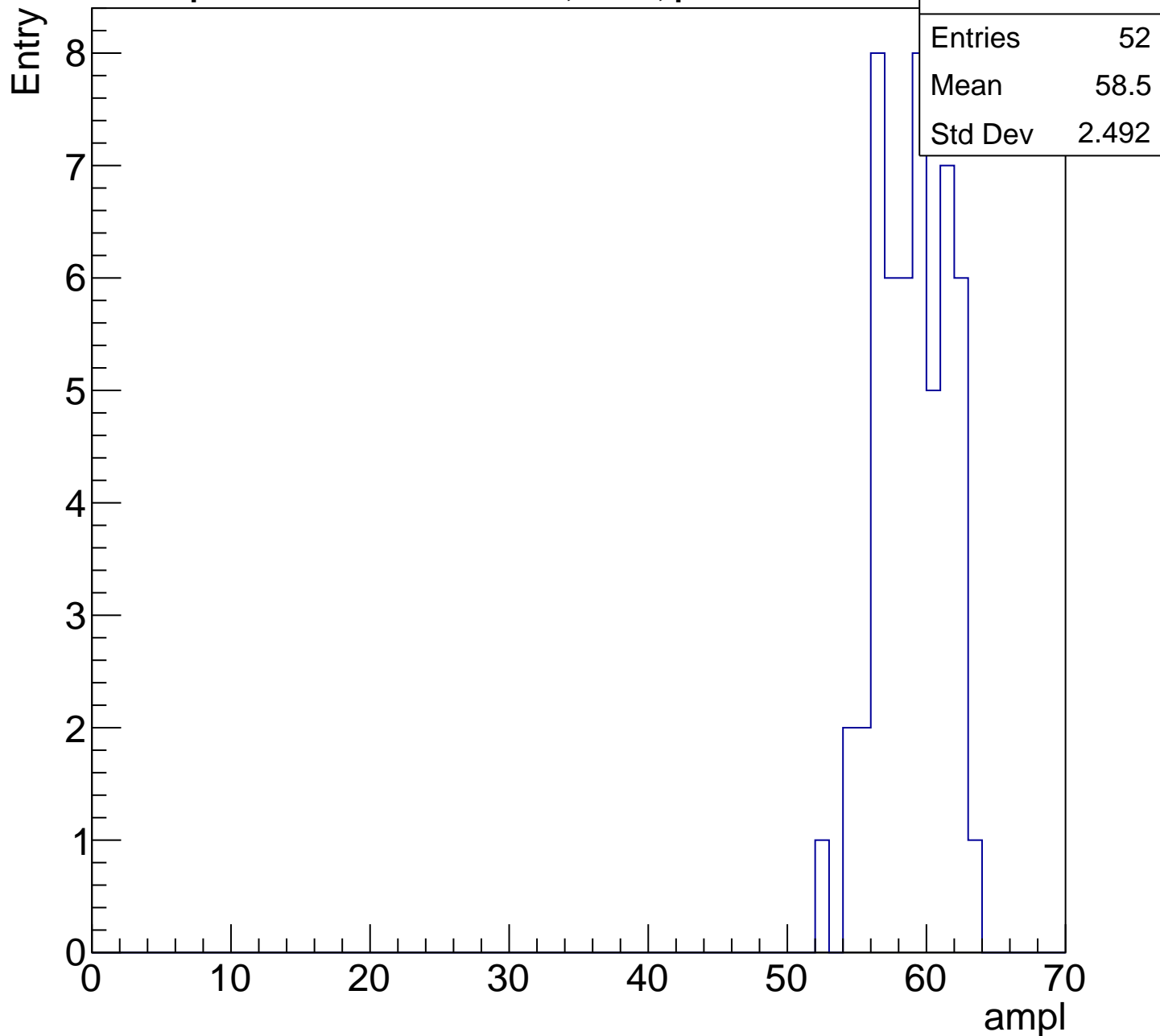
# B1L000S, U21-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch52, adc5

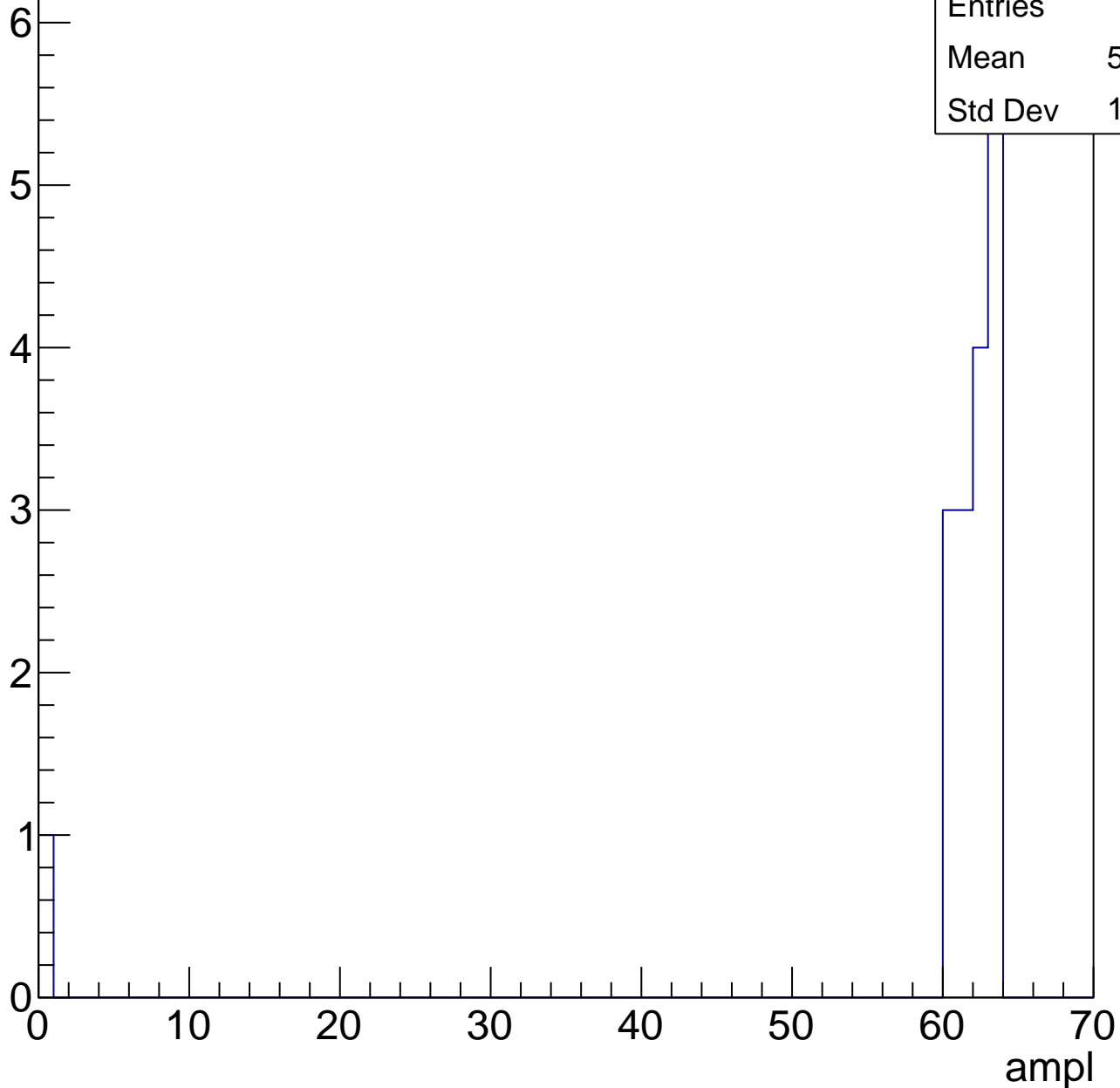
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

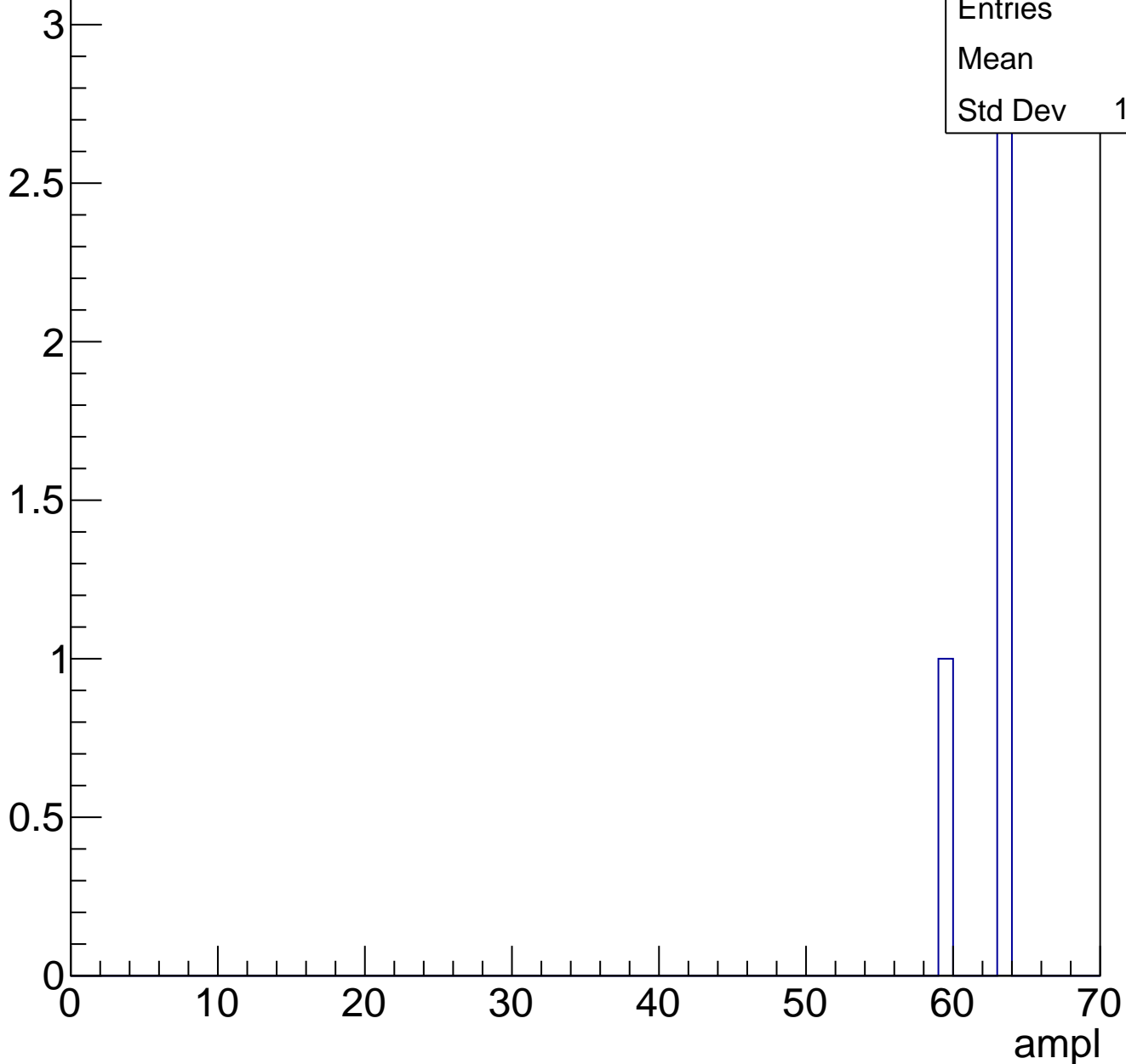




# B1L000S, U21-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch53, adc0

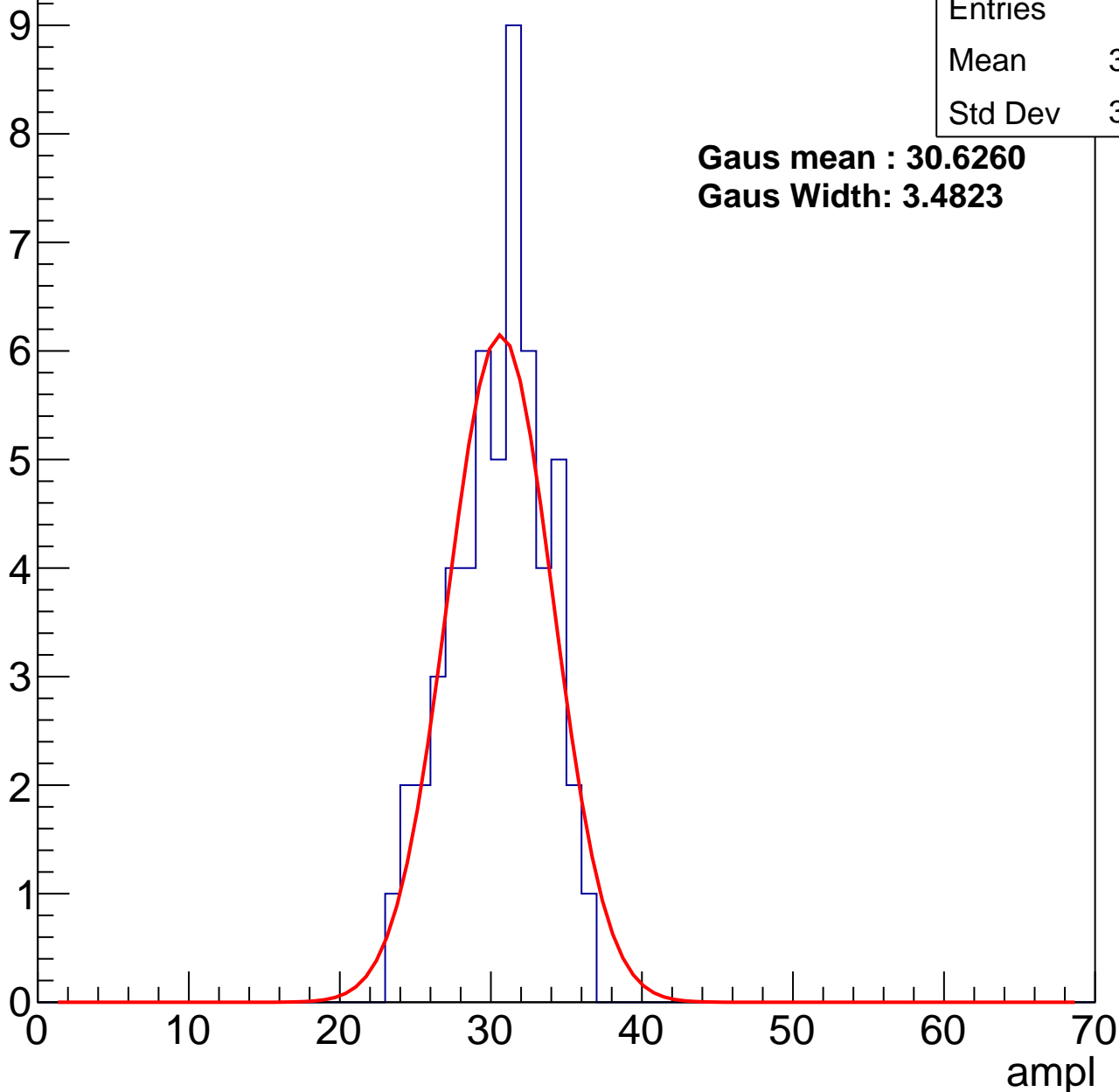
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	30.04
Std Dev	3.079

**Gaus mean : 30.6260**

**Gaus Width: 3.4823**



# B1L000S, U21-ch53, adc1

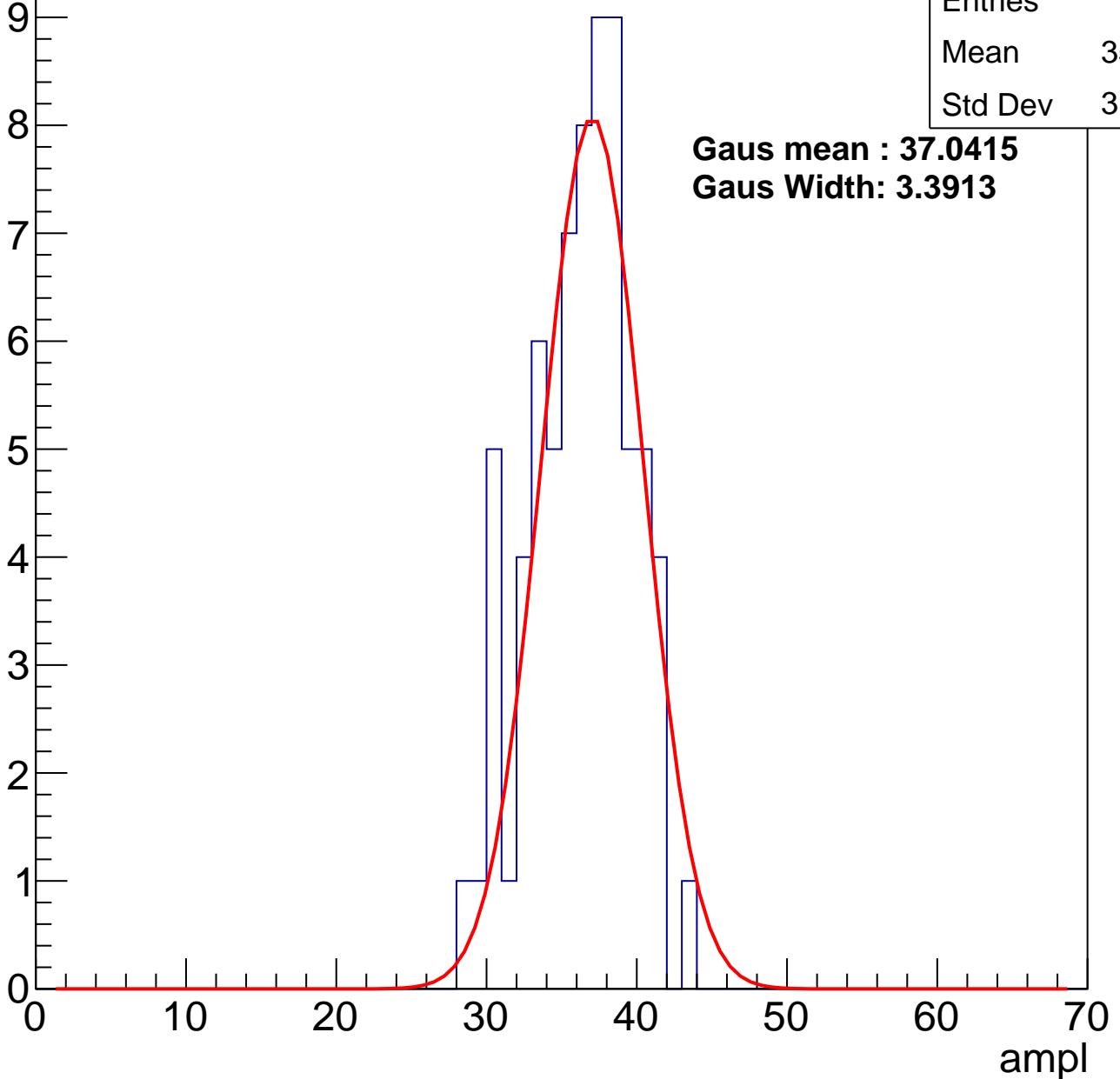
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	35.83
Std Dev	3.314

**Gaus mean : 37.0415**

**Gaus Width: 3.3913**



# B1L000S, U21-ch53, adc2

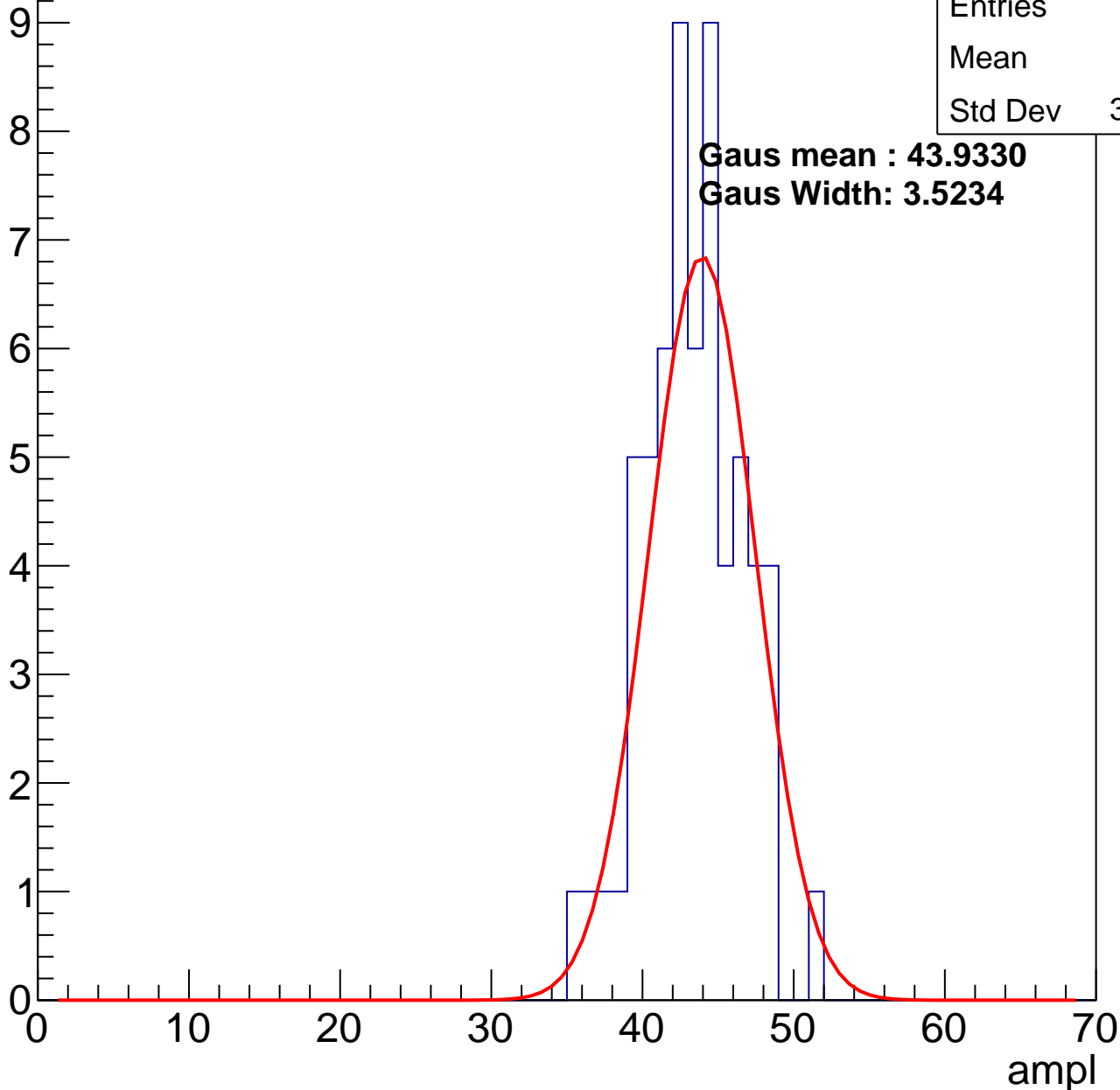
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.9
Std Dev	3.186

**Gaus mean : 43.9330**

**Gaus Width: 3.5234**

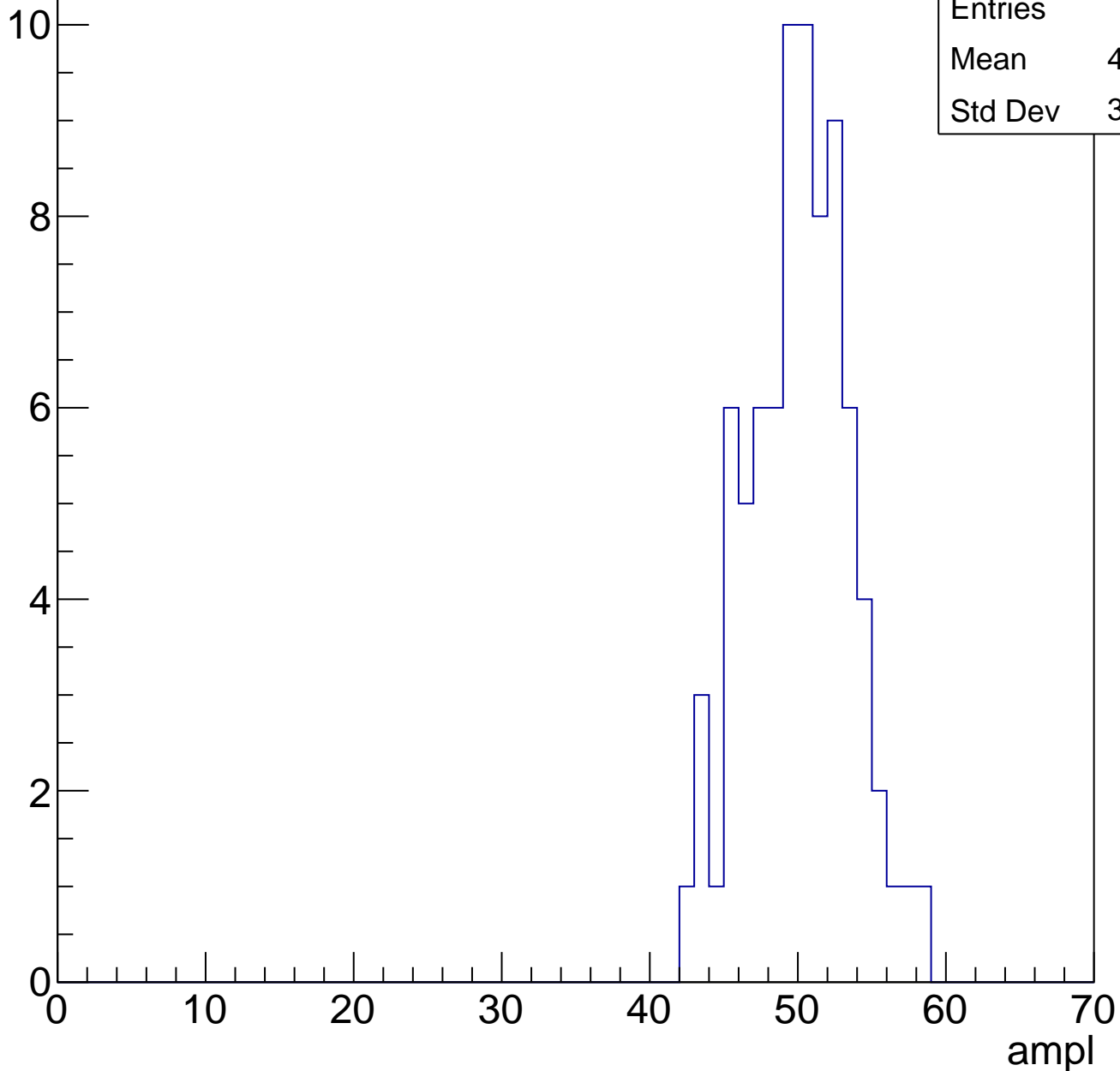


# B1L000S, U21-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	49.58
Std Dev	3.376

Entry

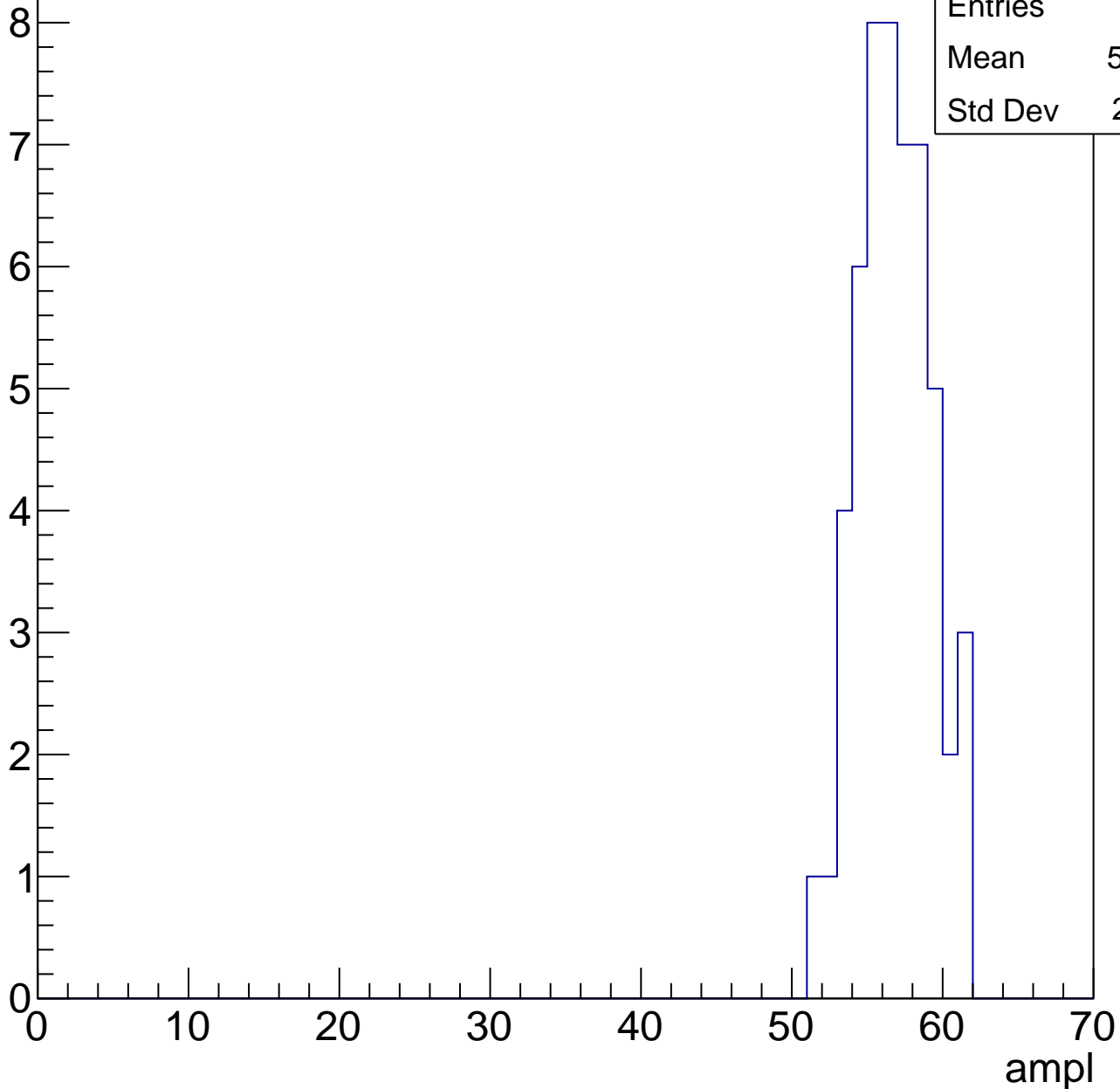


# B1L000S, U21-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	56.35
Std Dev	2.361



# B1L000S, U21-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	59.24
Std Dev	9.553

ampl

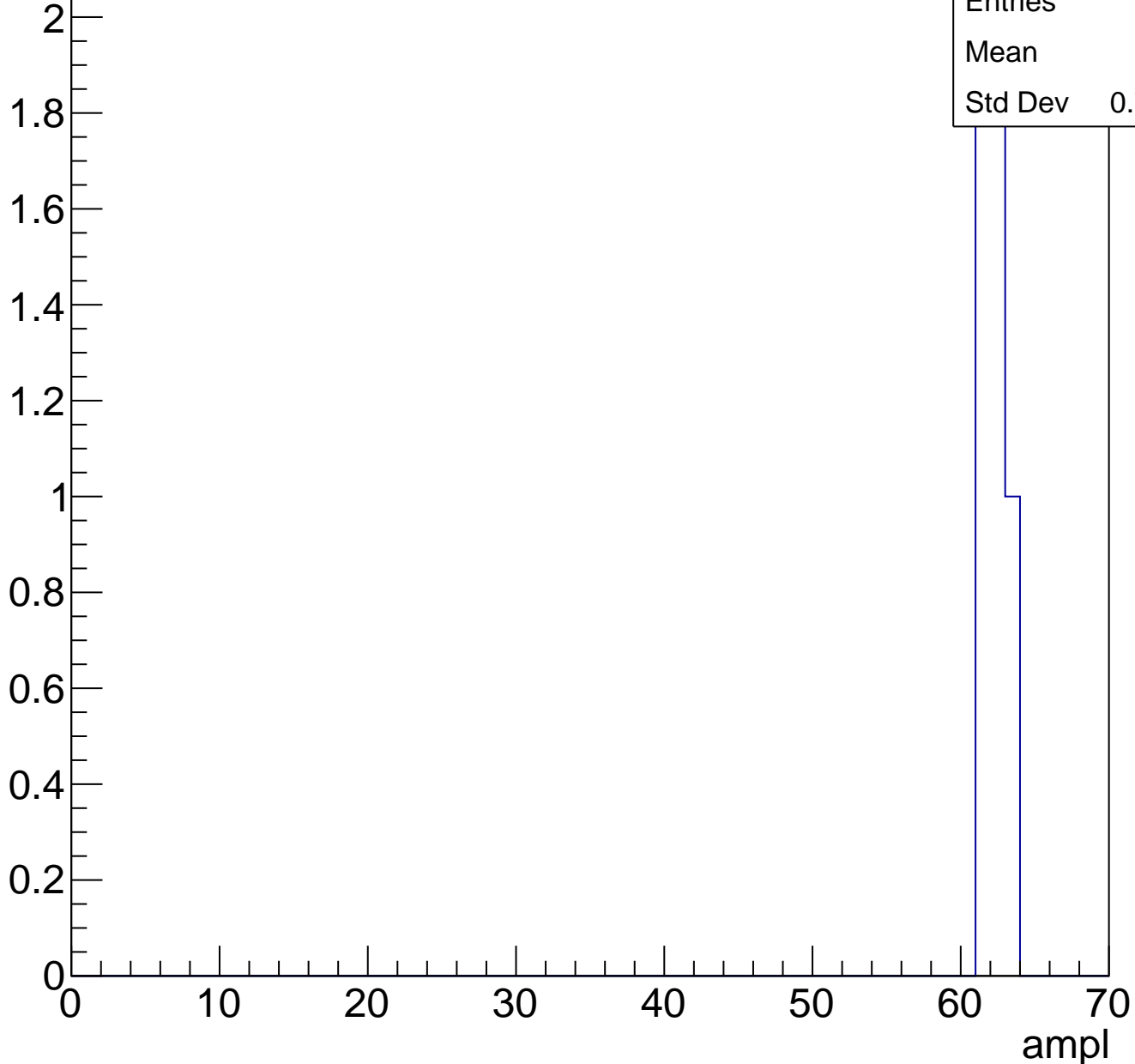
0 10 20 30 40 50 60 70



# B1L000S, U21-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch54, adc0

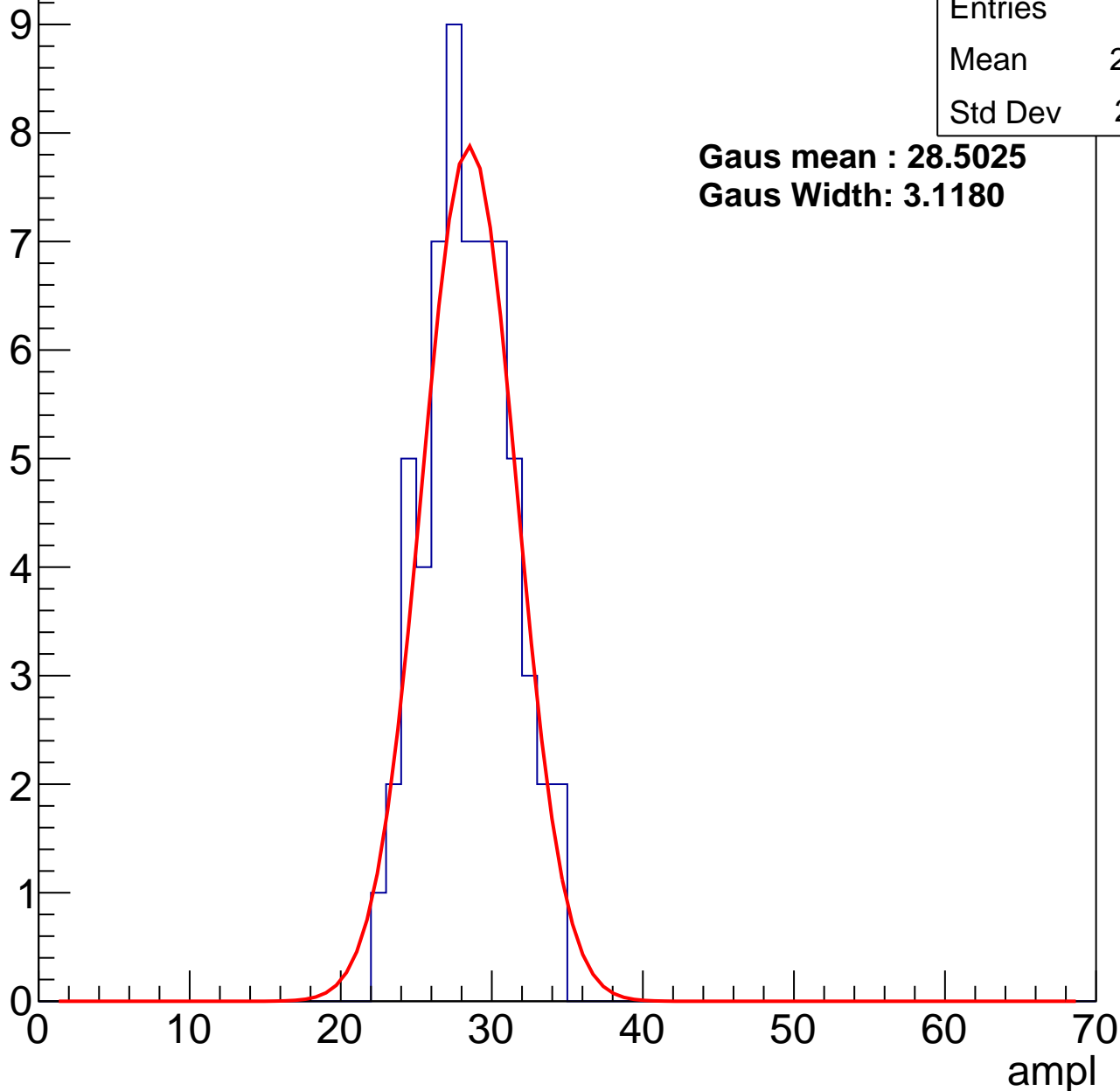
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	27.98
Std Dev	2.831

**Gaus mean : 28.5025**

**Gaus Width: 3.1180**



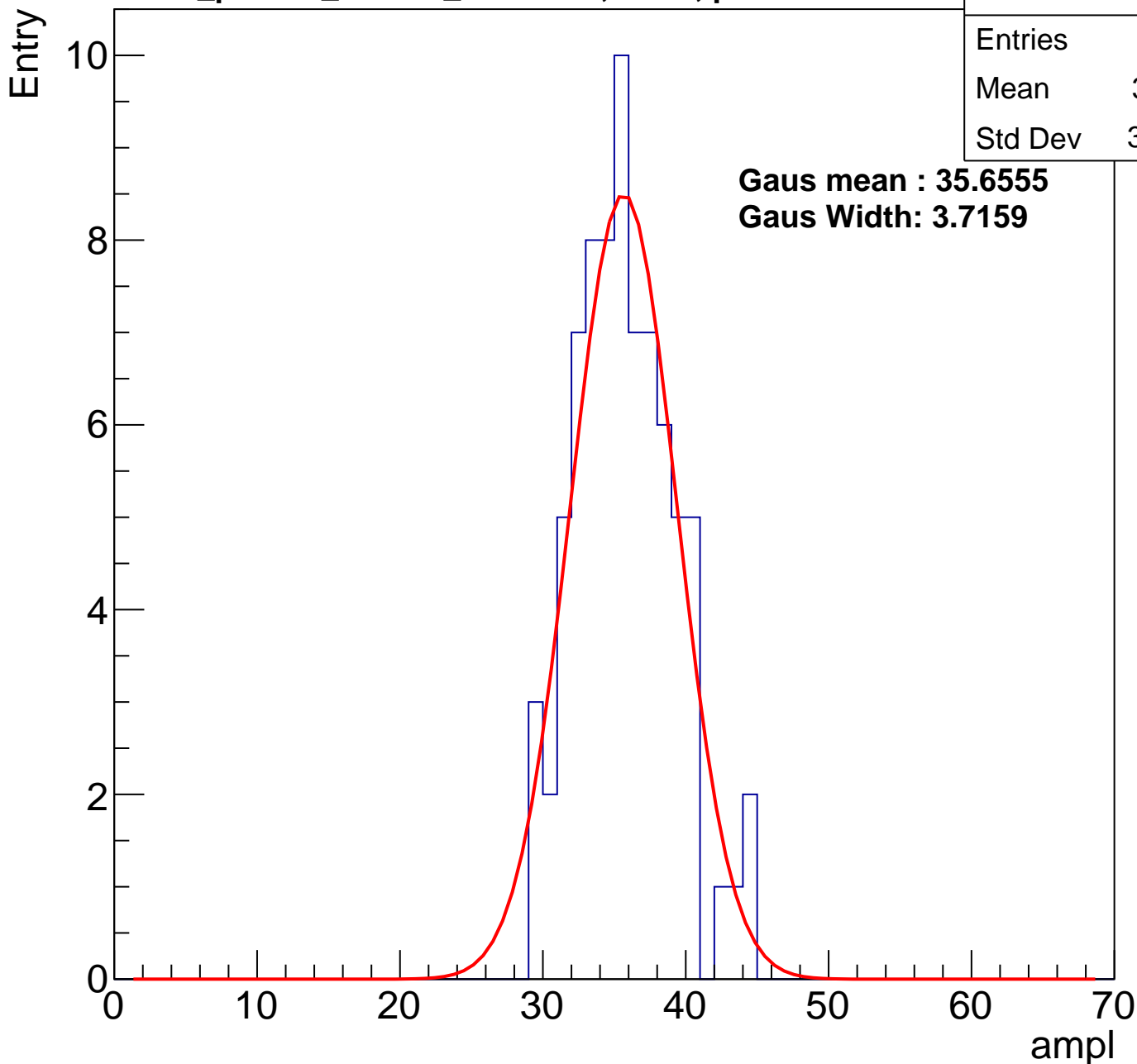
# B1L000S, U21-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	35.31
Std Dev	3.424

**Gaus mean : 35.6555**

**Gaus Width: 3.7159**



# B1L000S, U21-ch54, adc2

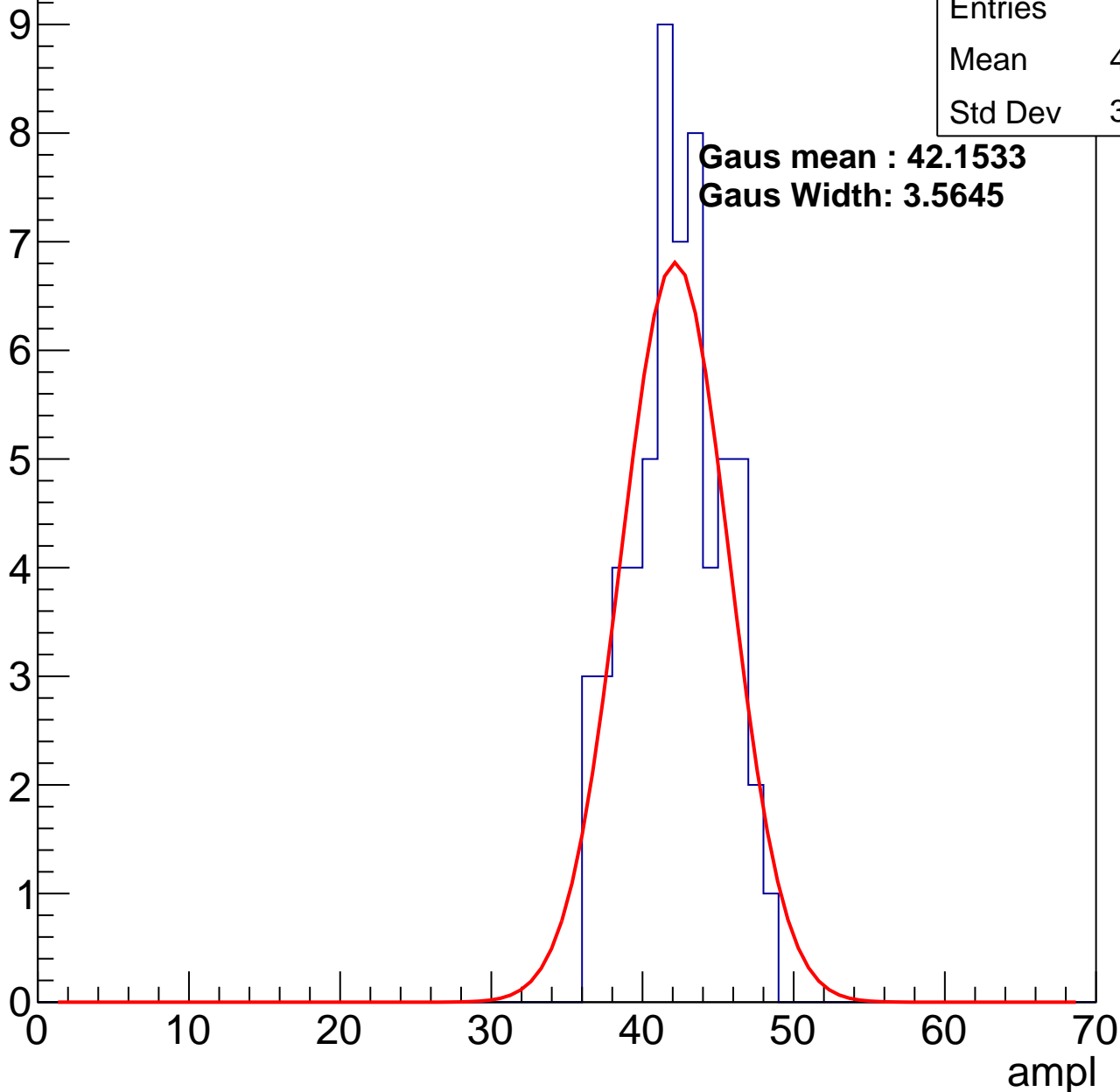
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.78
Std Dev	3.012

**Gaus mean : 42.1533**

**Gaus Width: 3.5645**

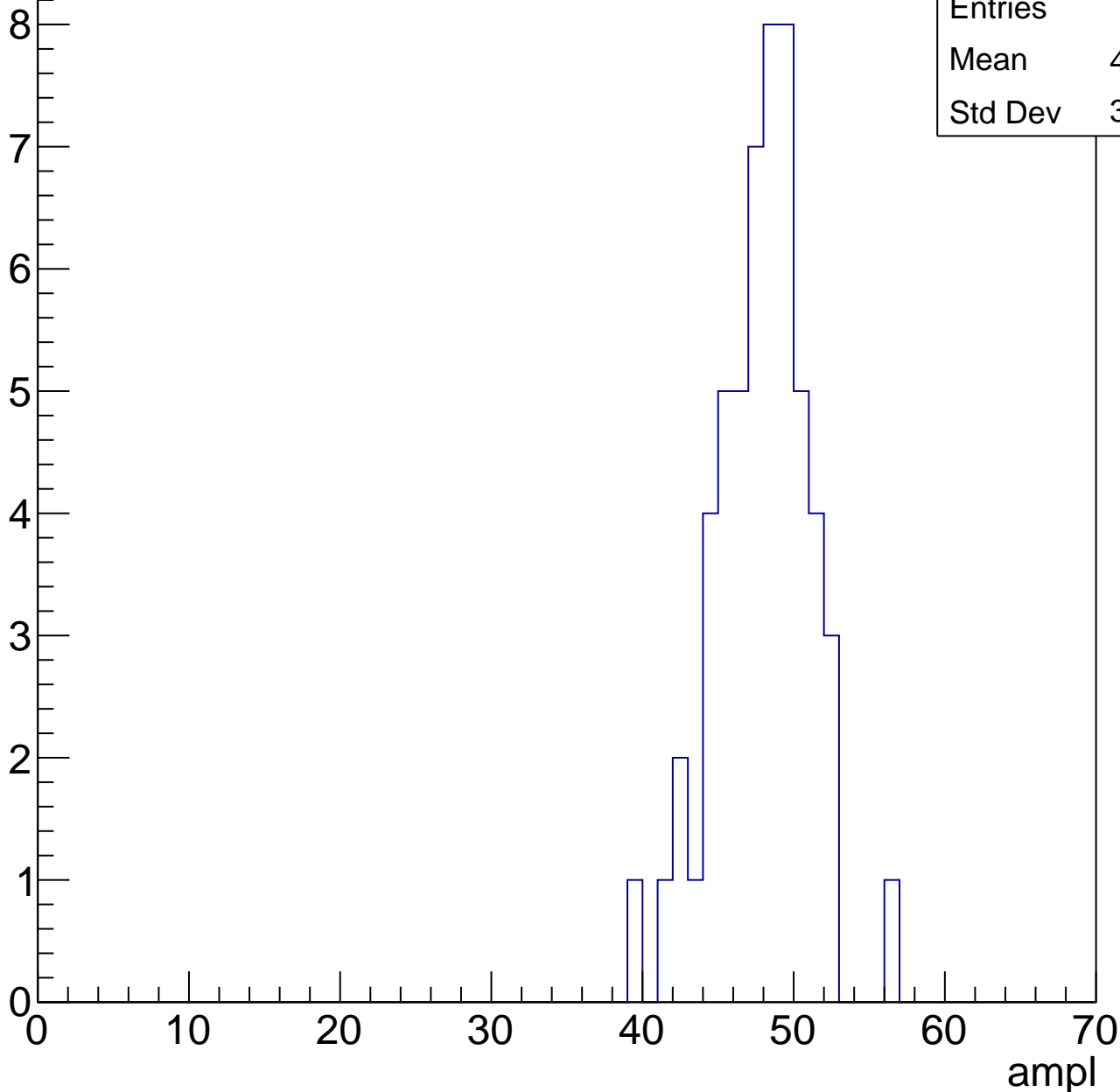


# B1L000S, U21-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	47.44
Std Dev	3.079

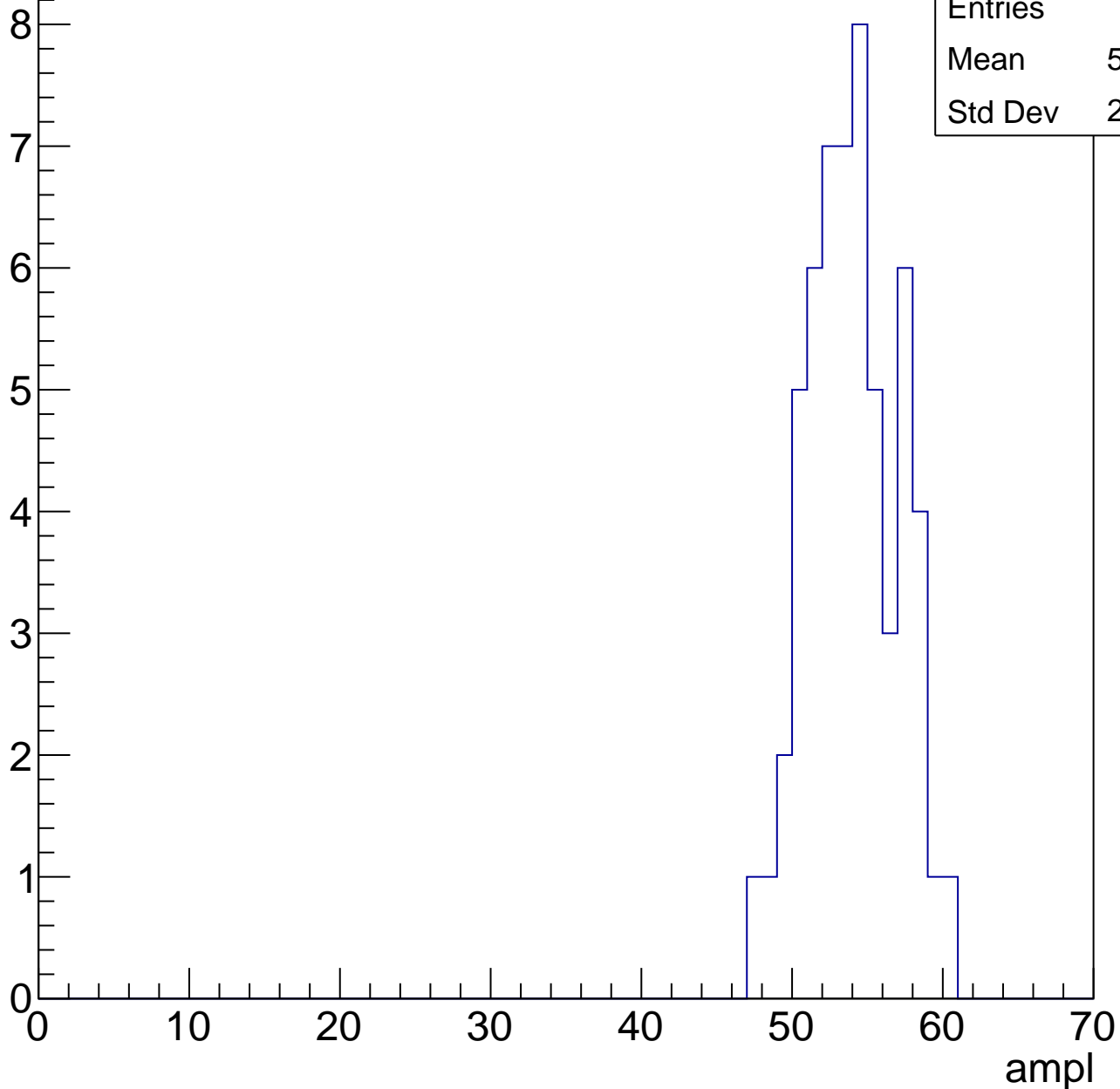


# B1L000S, U21-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	53.54
Std Dev	2.926

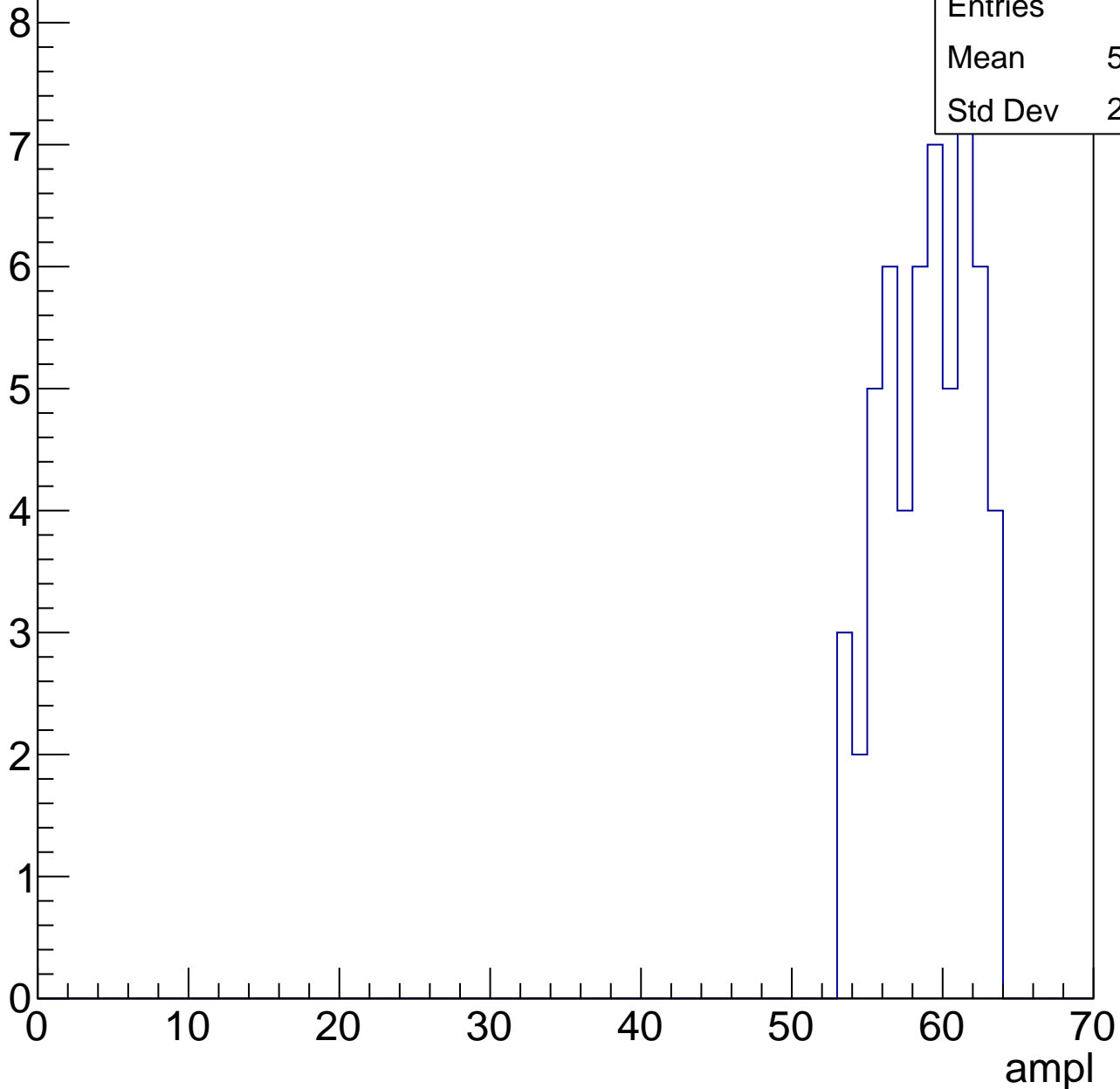


# B1L000S, U21-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	58.55
Std Dev	2.859

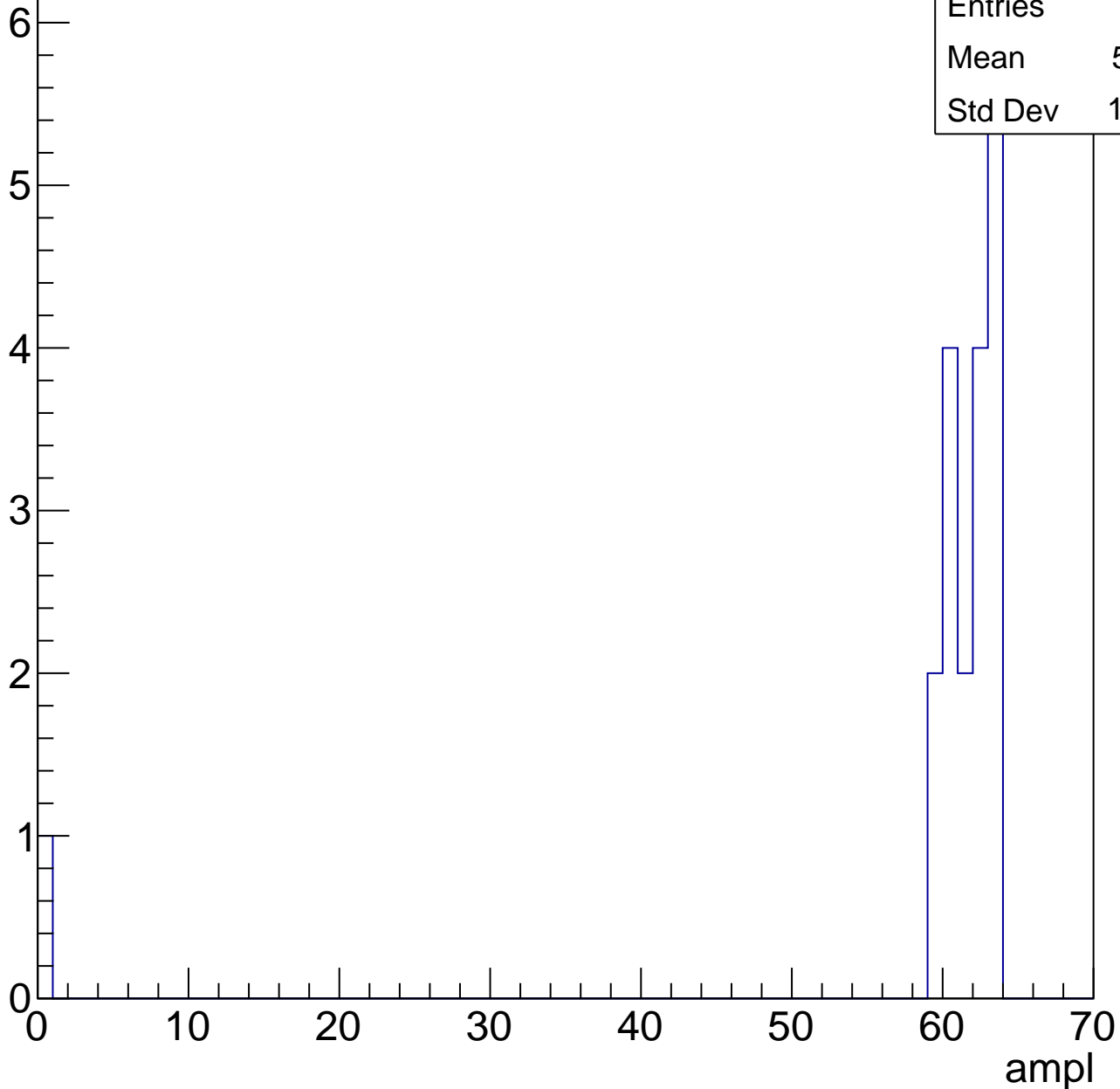


# B1L000S, U21-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.21
Std Dev	13.79

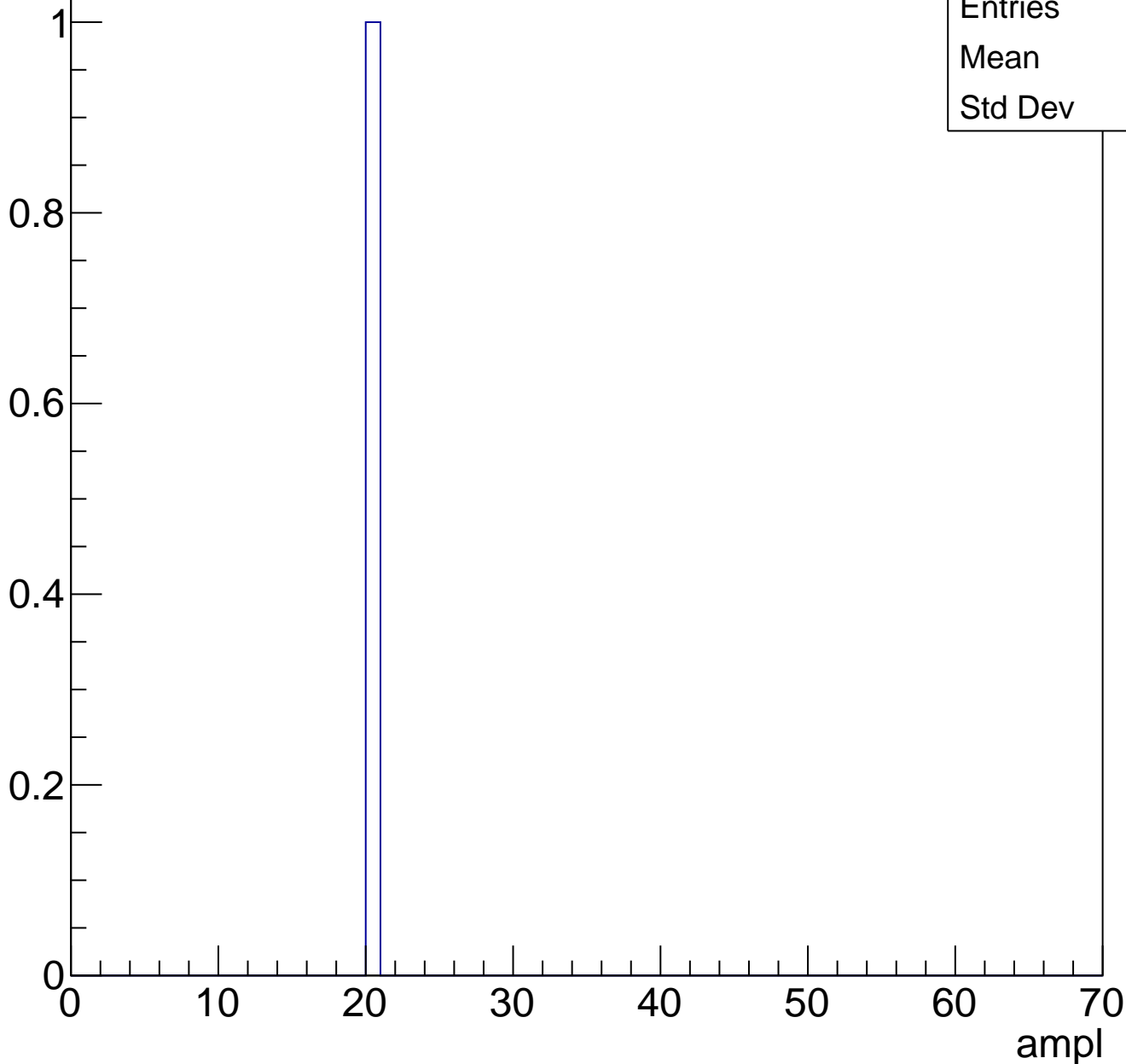




# B1L000S, U21-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch55, adc0

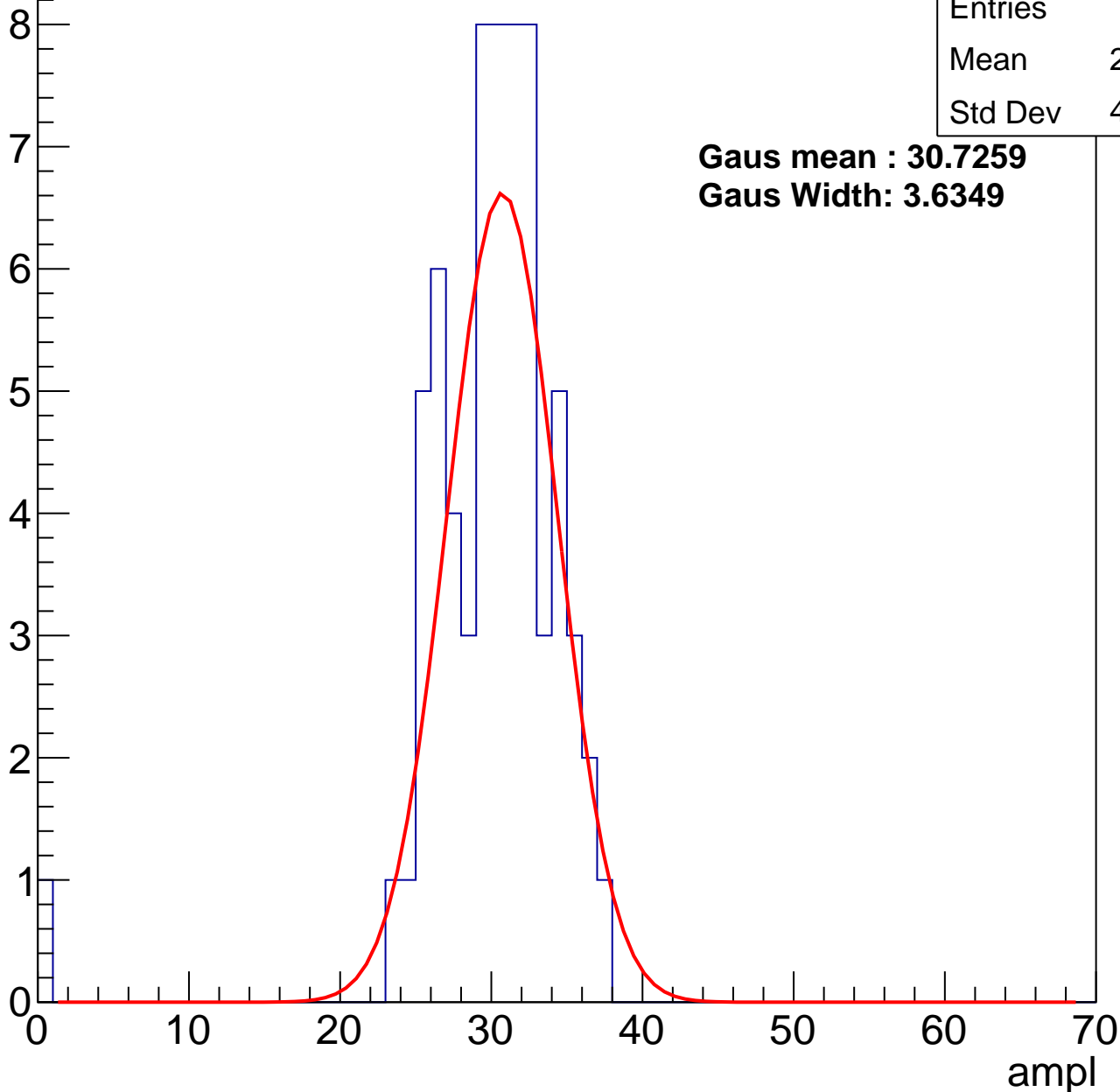
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.54
Std Dev	4.872

**Gaus mean : 30.7259**

**Gaus Width: 3.6349**



# B1L000S, U21-ch55, adc1

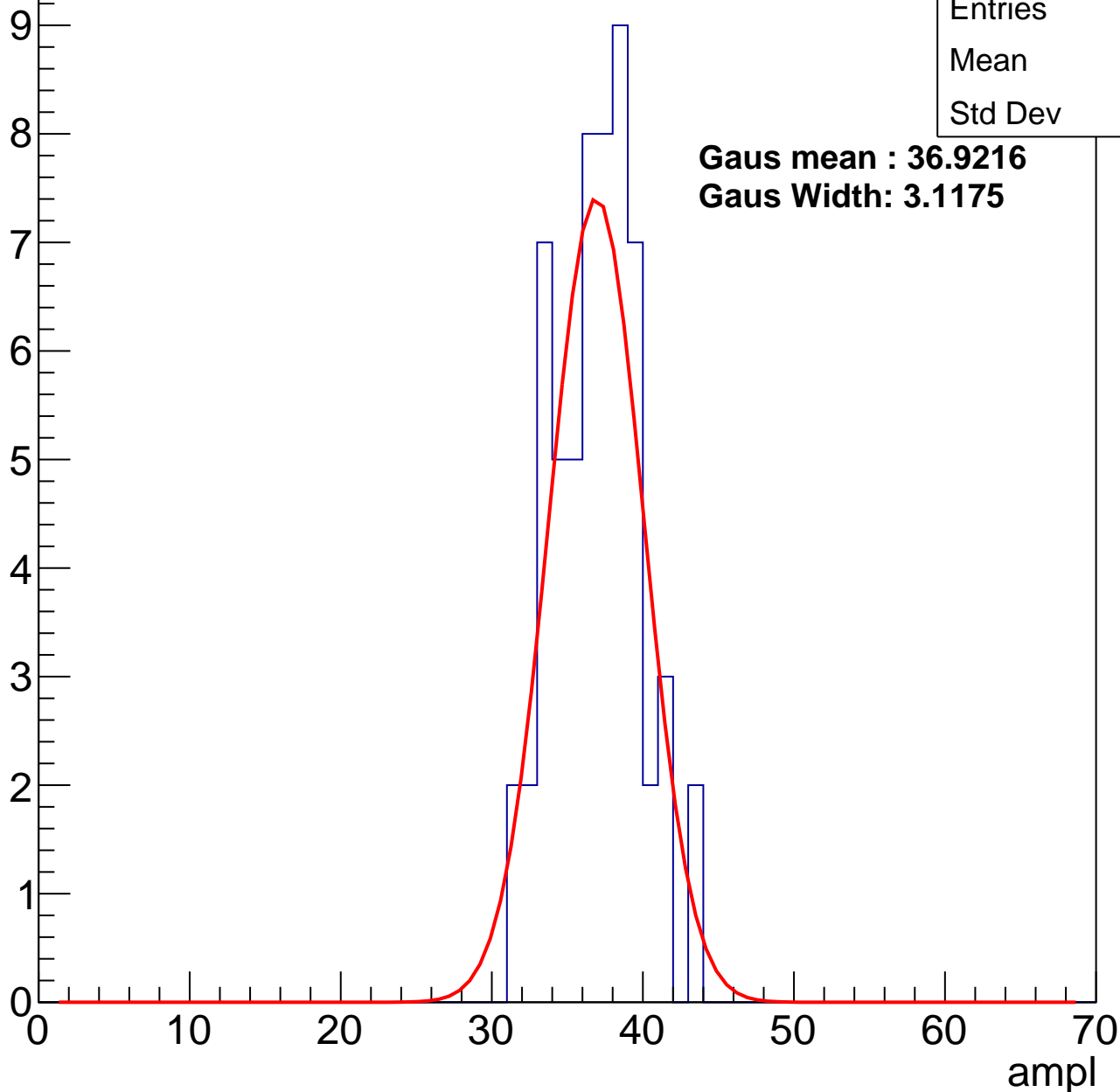
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	36.5
Std Dev	2.79

**Gaus mean : 36.9216**

**Gaus Width: 3.1175**



# B1L000S, U21-ch55, adc2

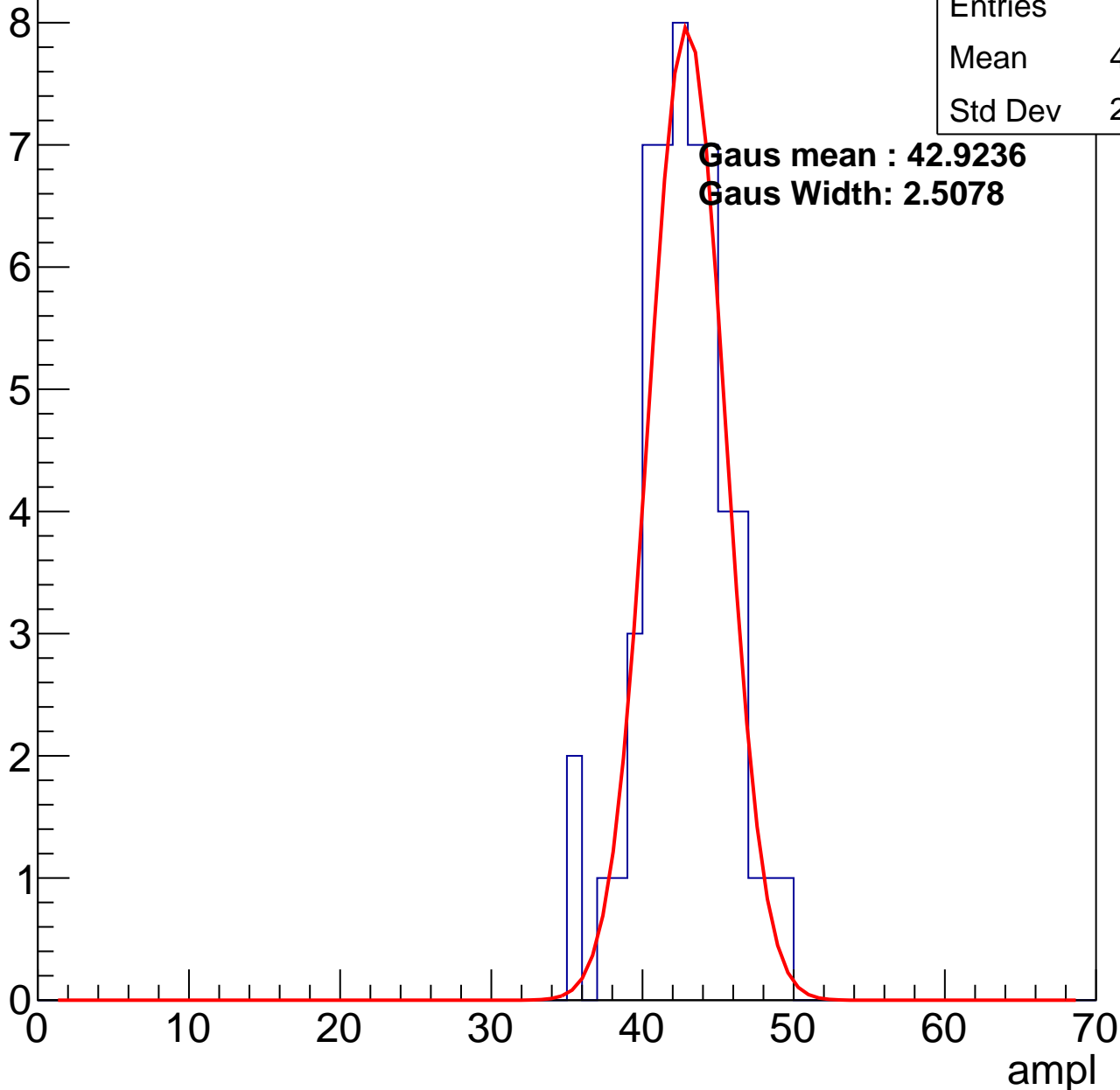
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.26
Std Dev	2.862

**Gaus mean : 42.9236**

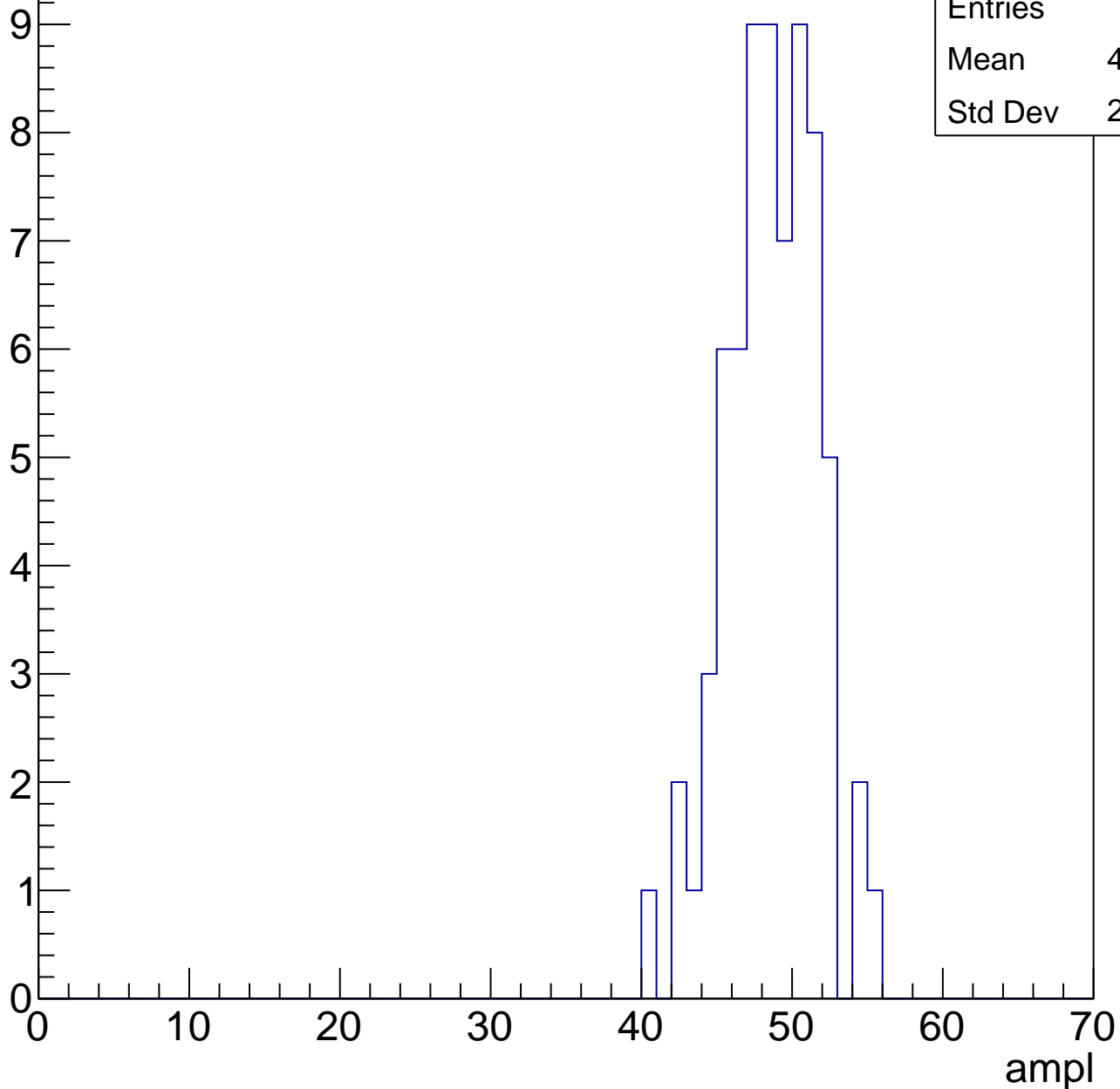
**Gaus Width: 2.5078**



# B1L000S, U21-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

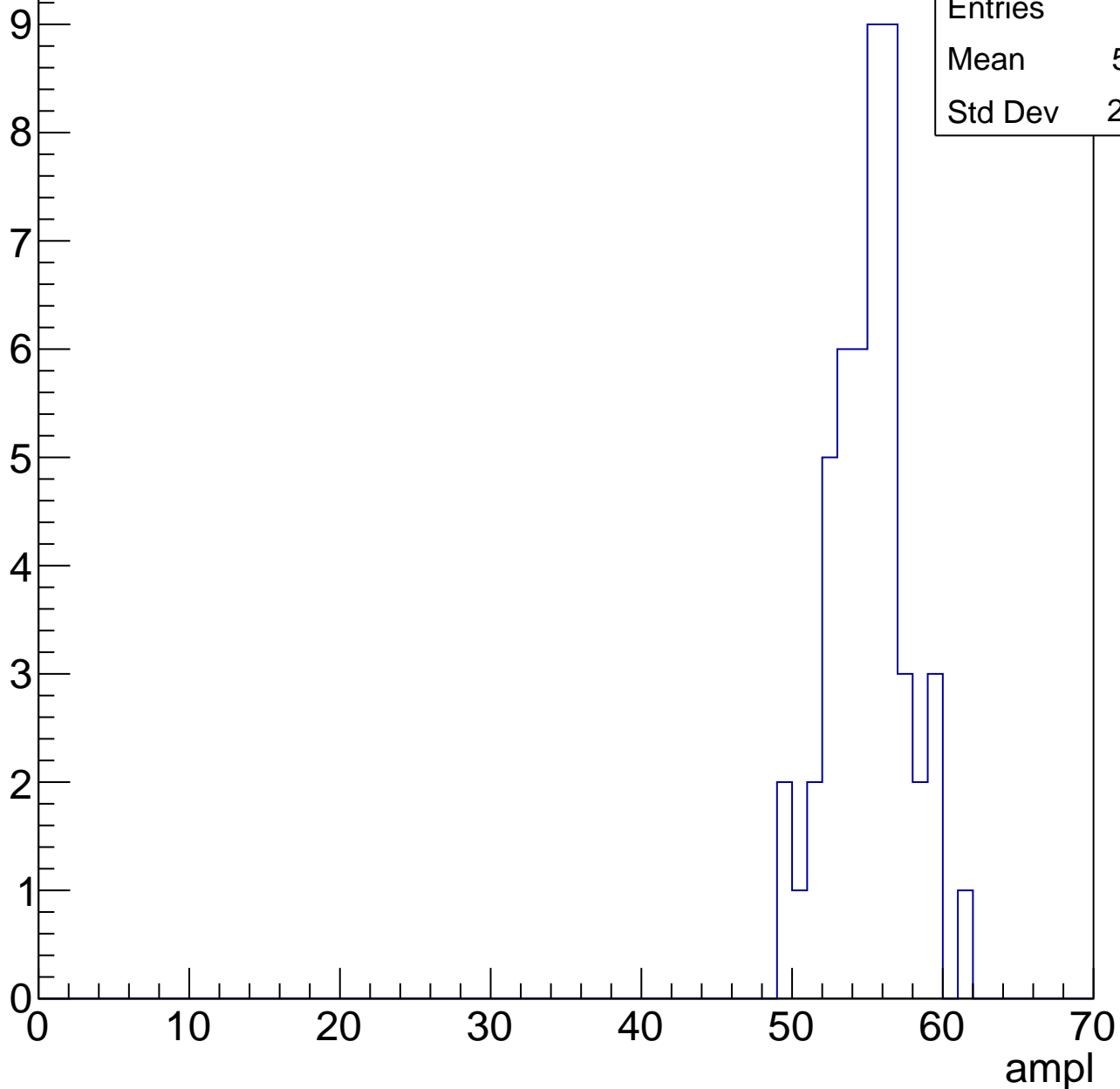
Entry



# B1L000S, U21-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

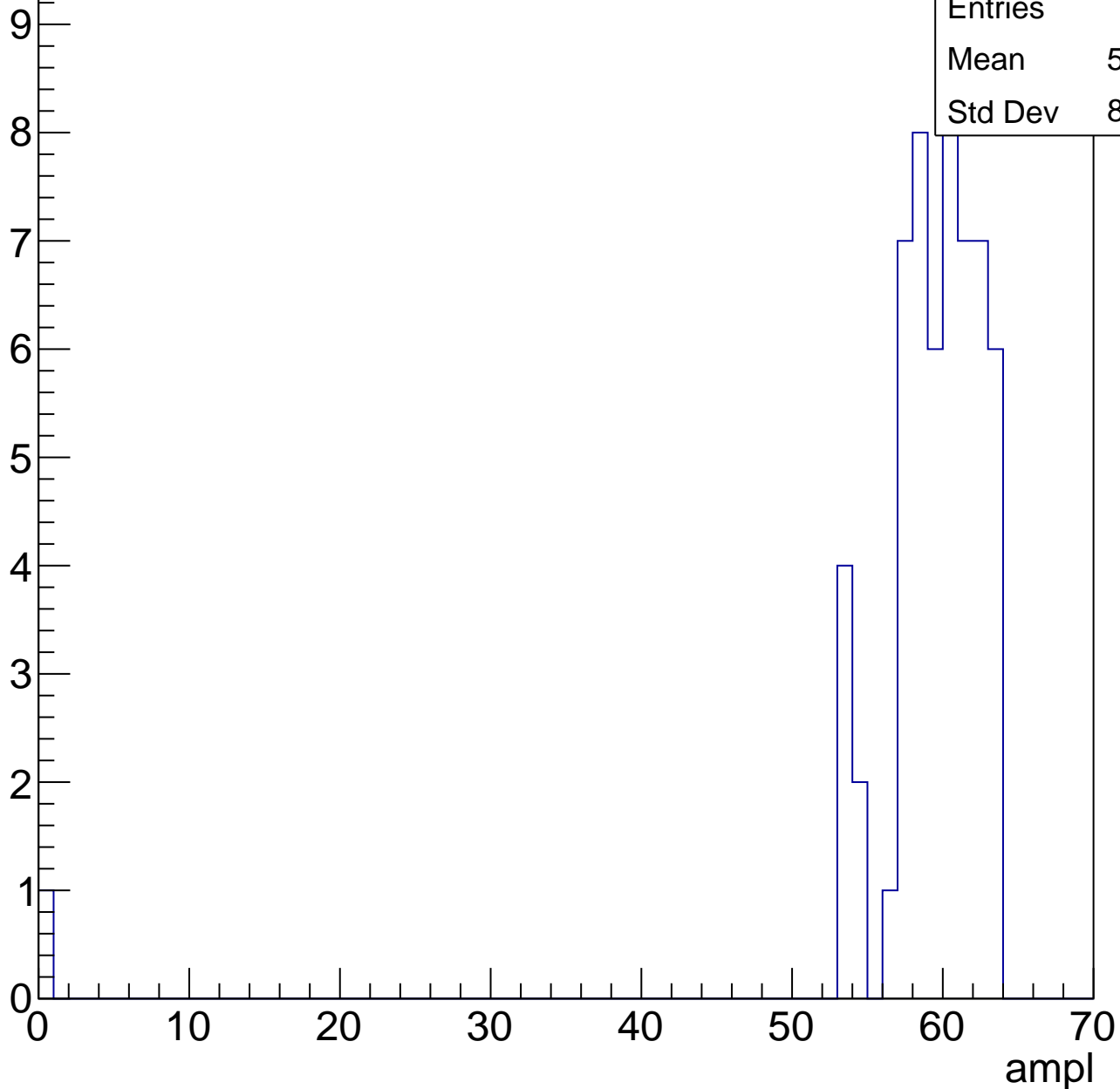


Entries	49
Mean	54.61
Std Dev	2.554

# B1L000S, U21-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

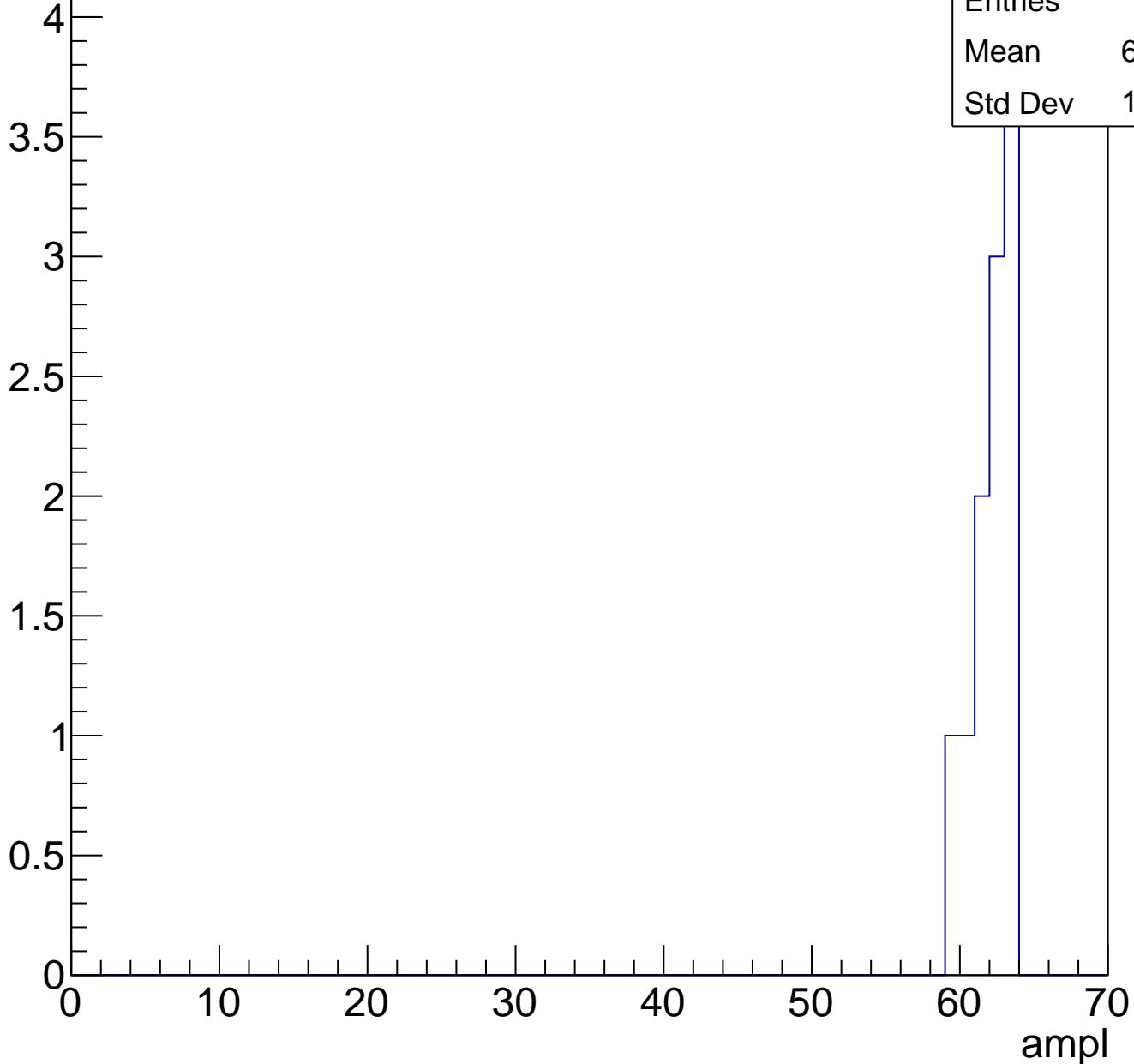
Entry



# B1L000S, U21-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch56, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	82
Mean	30.16
Std Dev	3.369

**Gaus mean : 30.5274**

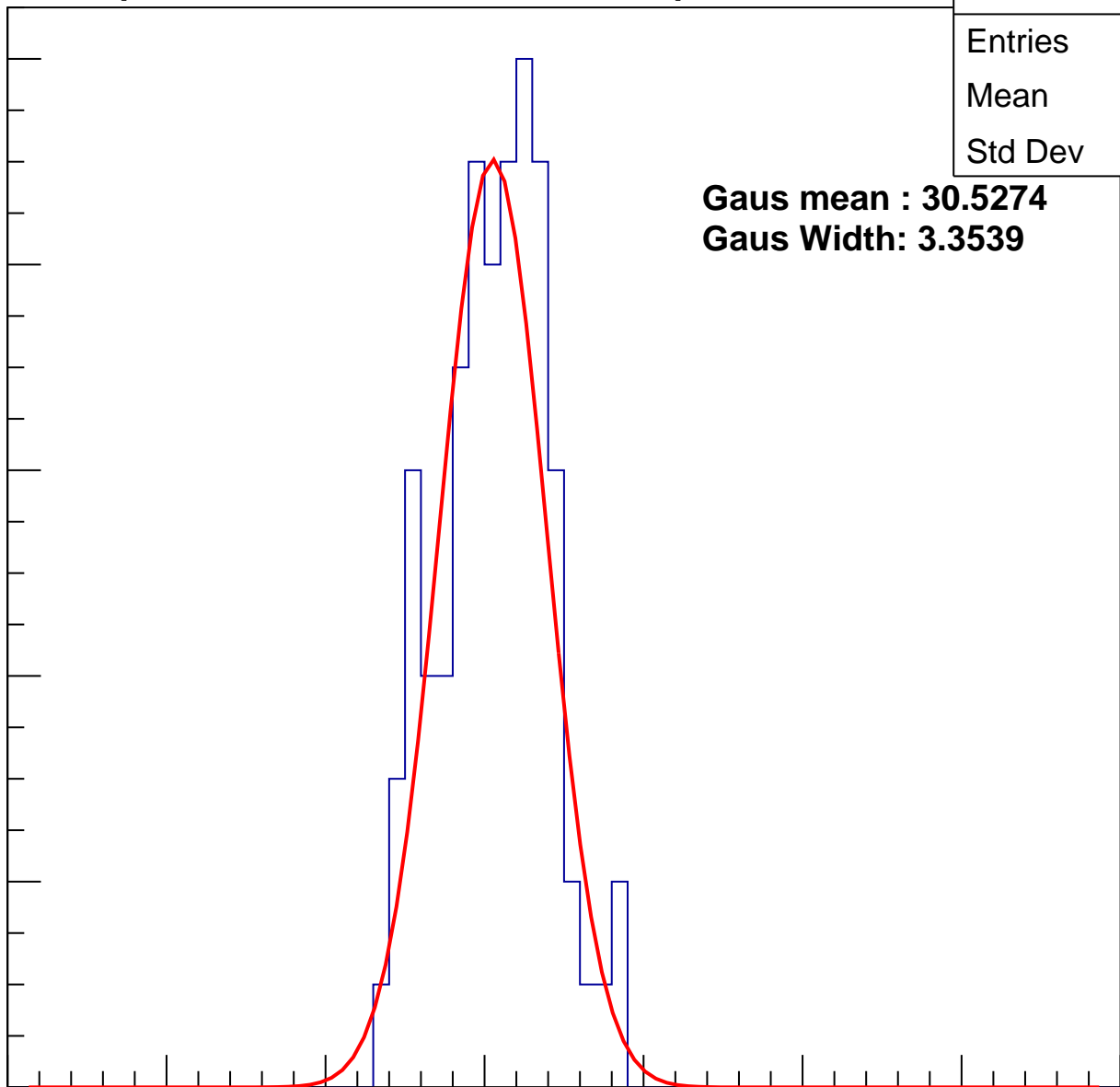
**Gaus Width: 3.3539**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch56, adc1

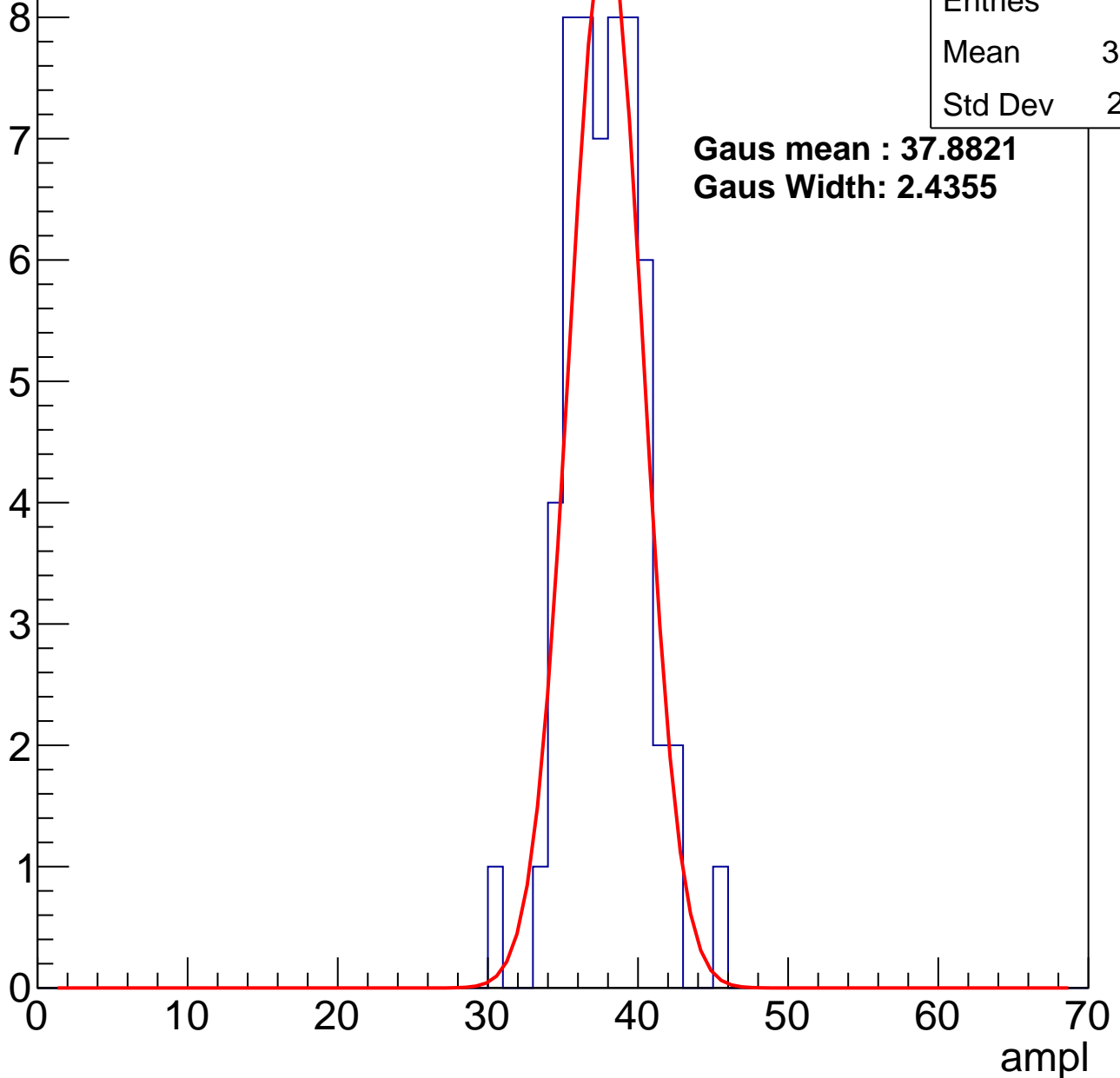
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	37.38
Std Dev	2.581

**Gaus mean : 37.8821**

**Gaus Width: 2.4355**



# B1L000S, U21-ch56, adc2

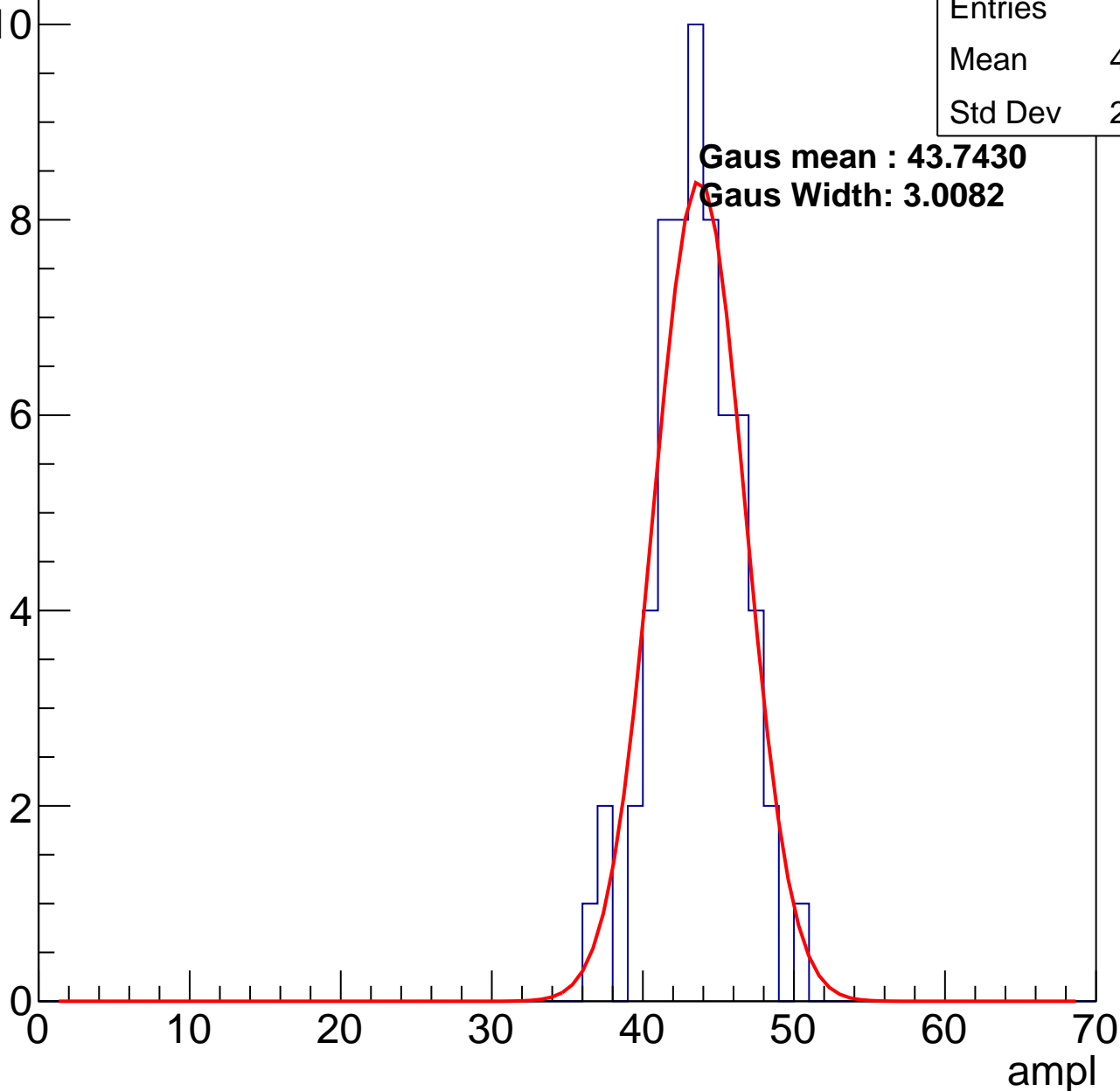
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	43.13
Std Dev	2.774

**Gaus mean : 43.7430**

**Gaus Width: 3.0082**



# B1L000S, U21-ch56, adc3

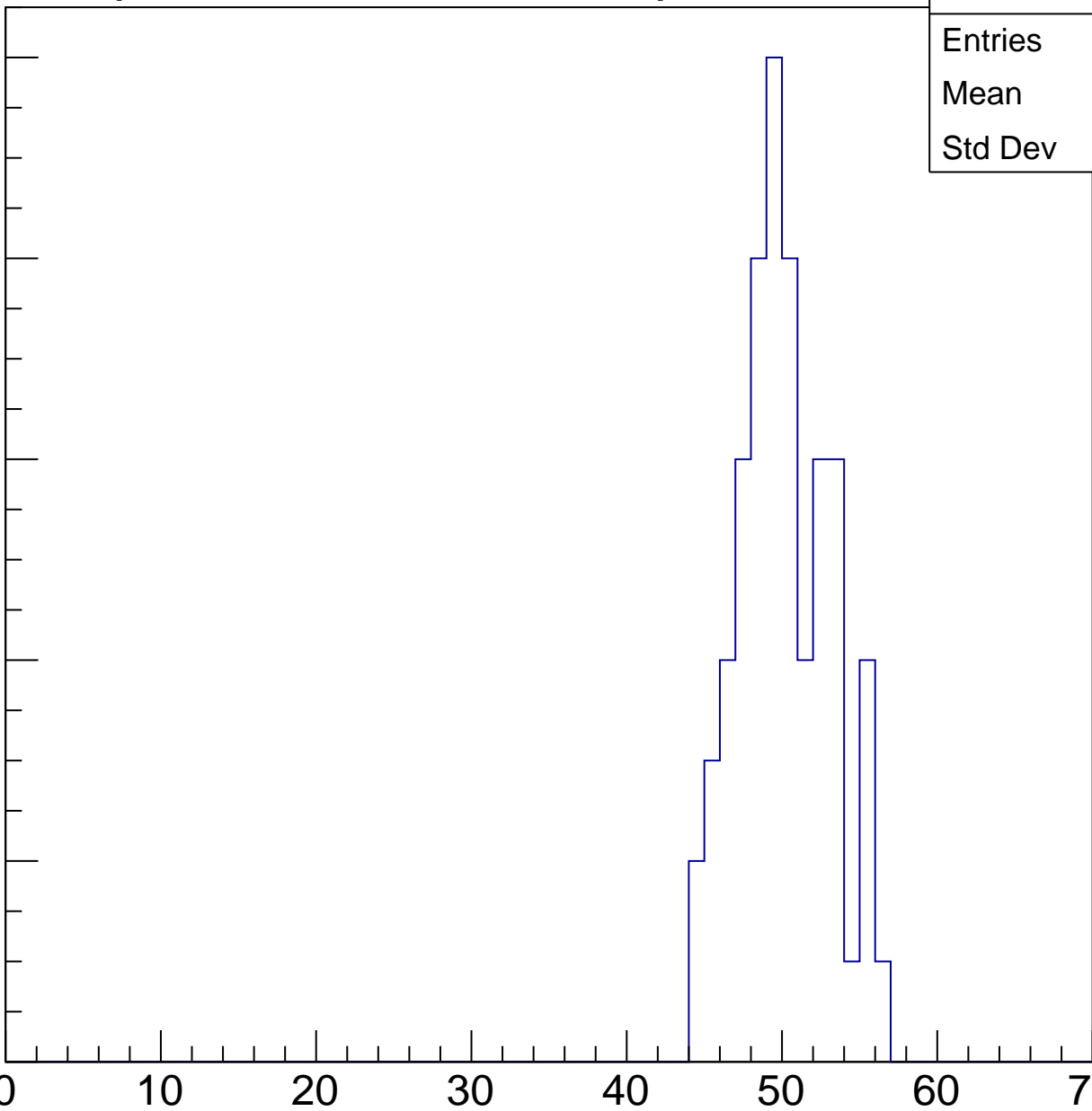
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10  
8  
6  
4  
2  
0

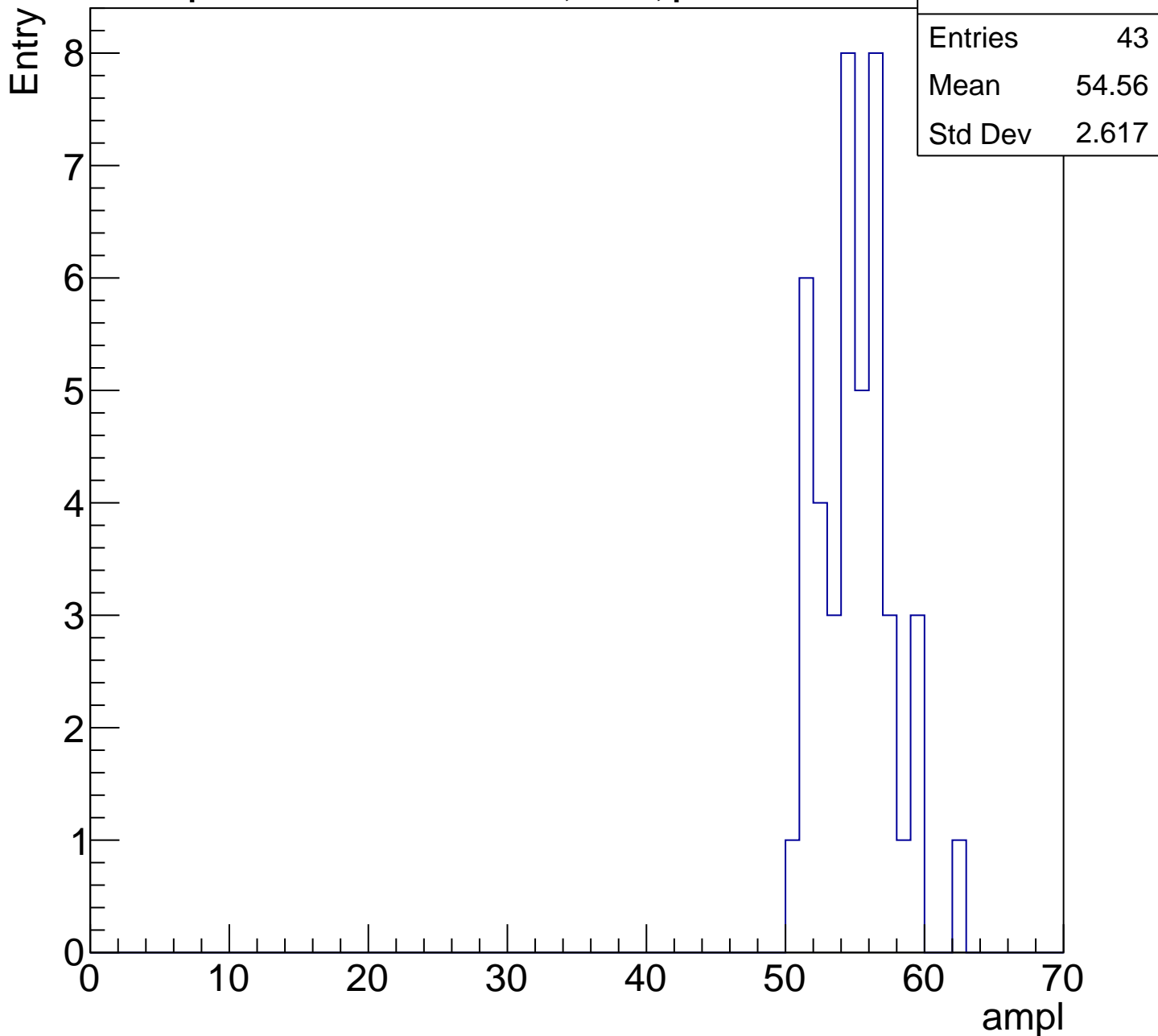
Entries	63
Mean	49.63
Std Dev	2.908

ampl



# B1L000S, U21-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 56

Mean 59.43

Std Dev 2.29

8

6

4

2

0

0

10

20

30

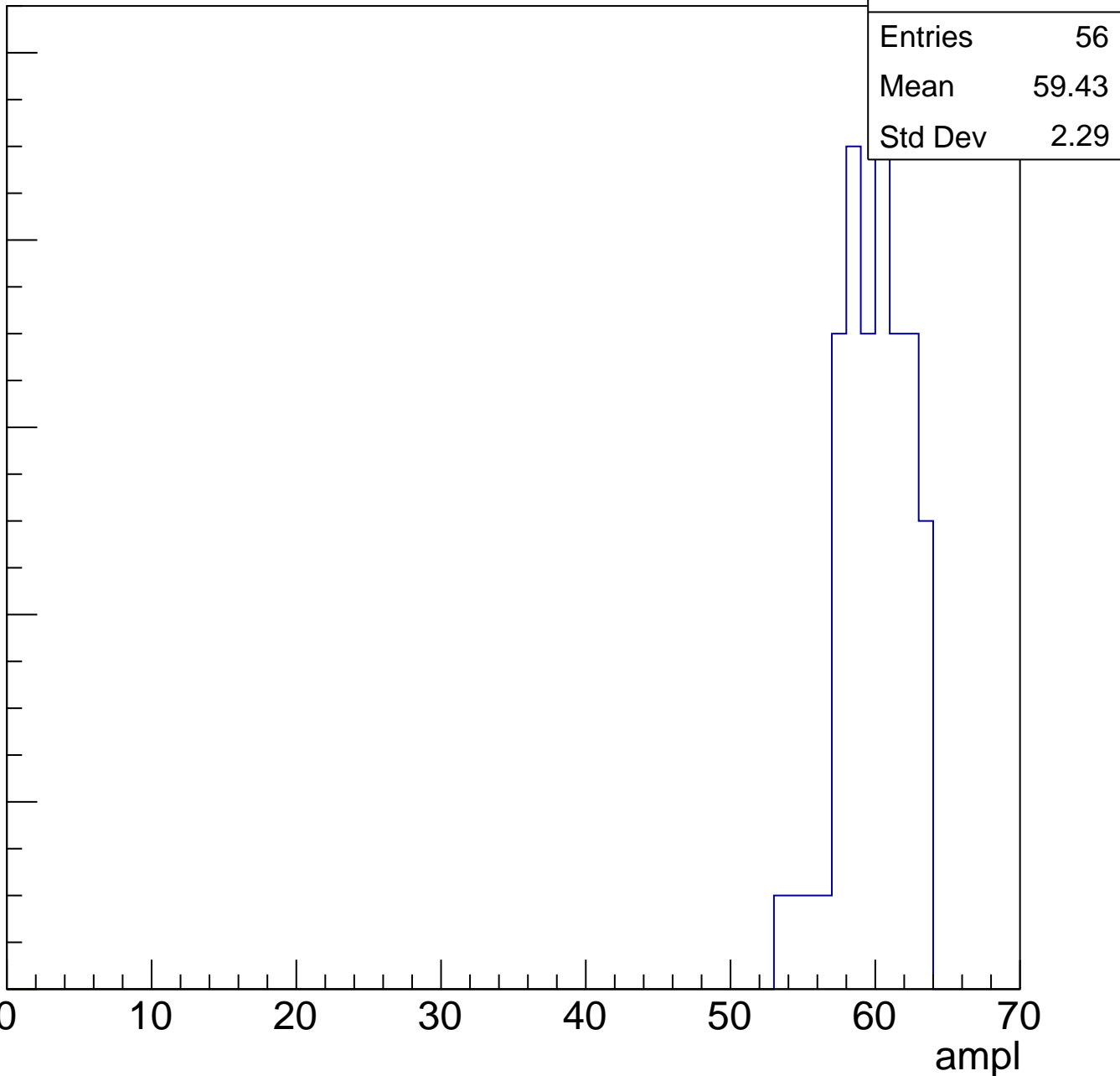
40

50

60

70

ampl

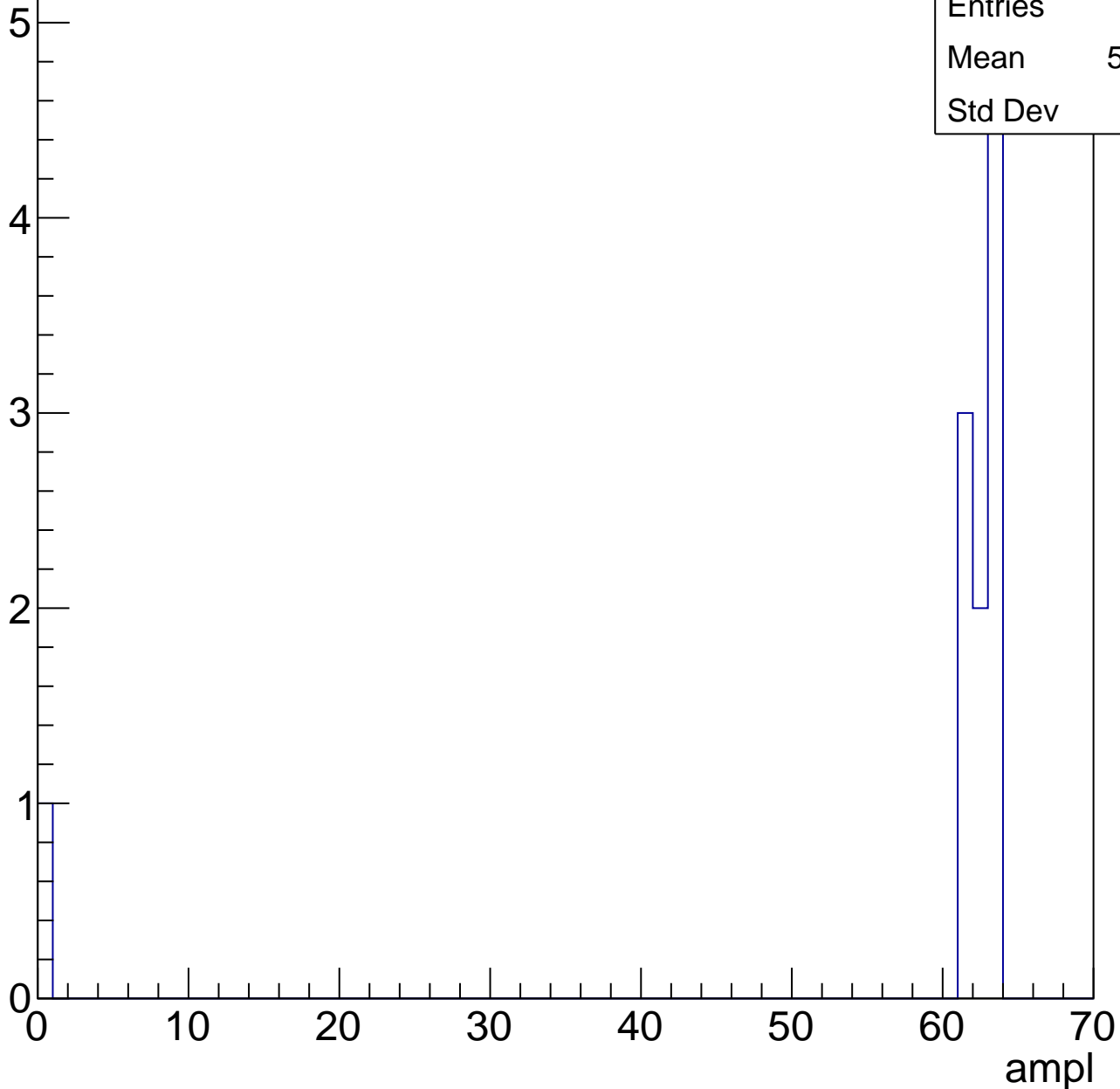


# B1L000S, U21-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	56.55
Std Dev	17.9





# B1L000S, U21-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch57, adc0

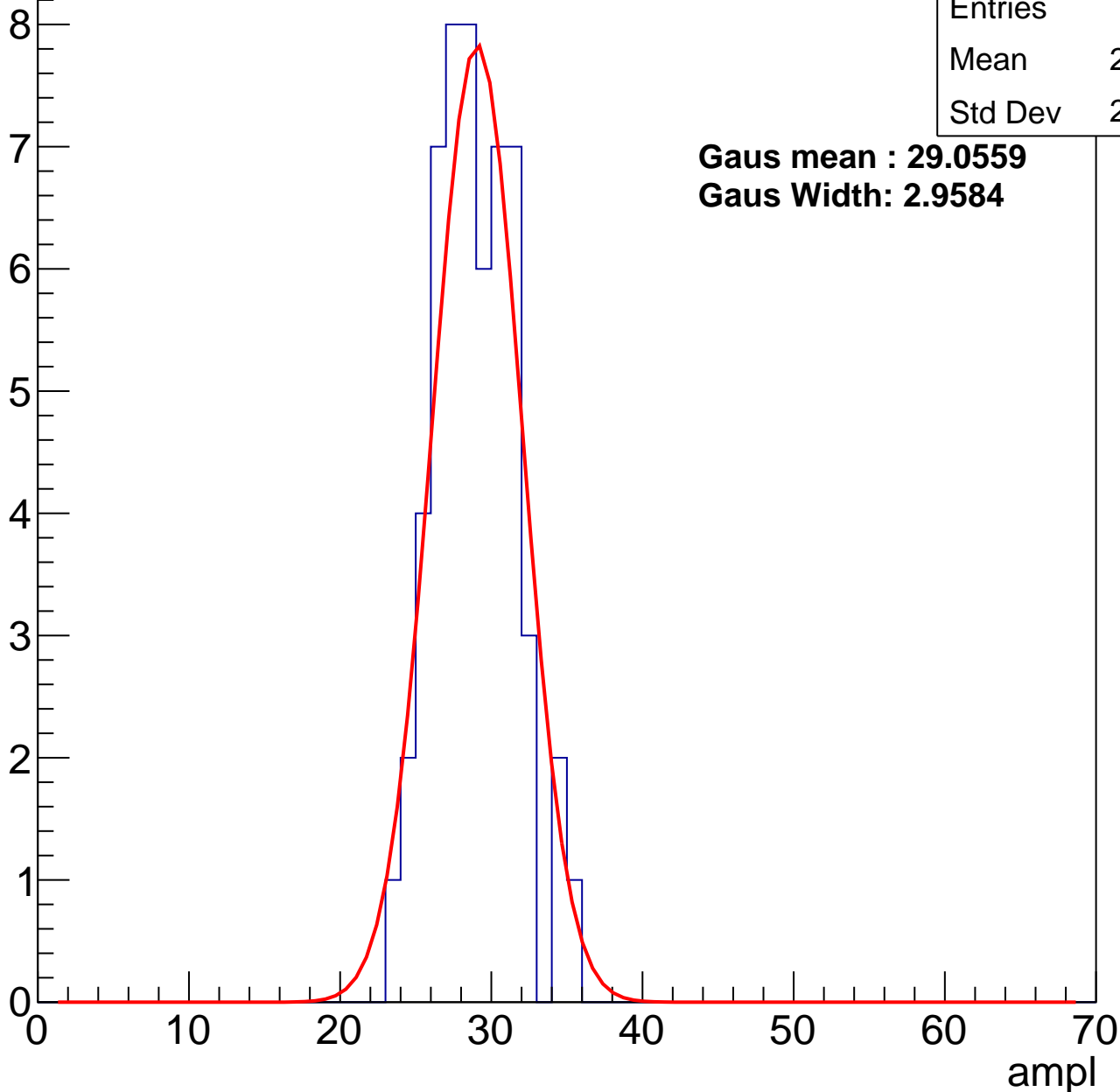
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	28.45
Std Dev	2.618

**Gaus mean : 29.0559**

**Gaus Width: 2.9584**



# B1L000S, U21-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	35.23
Std Dev	3.343

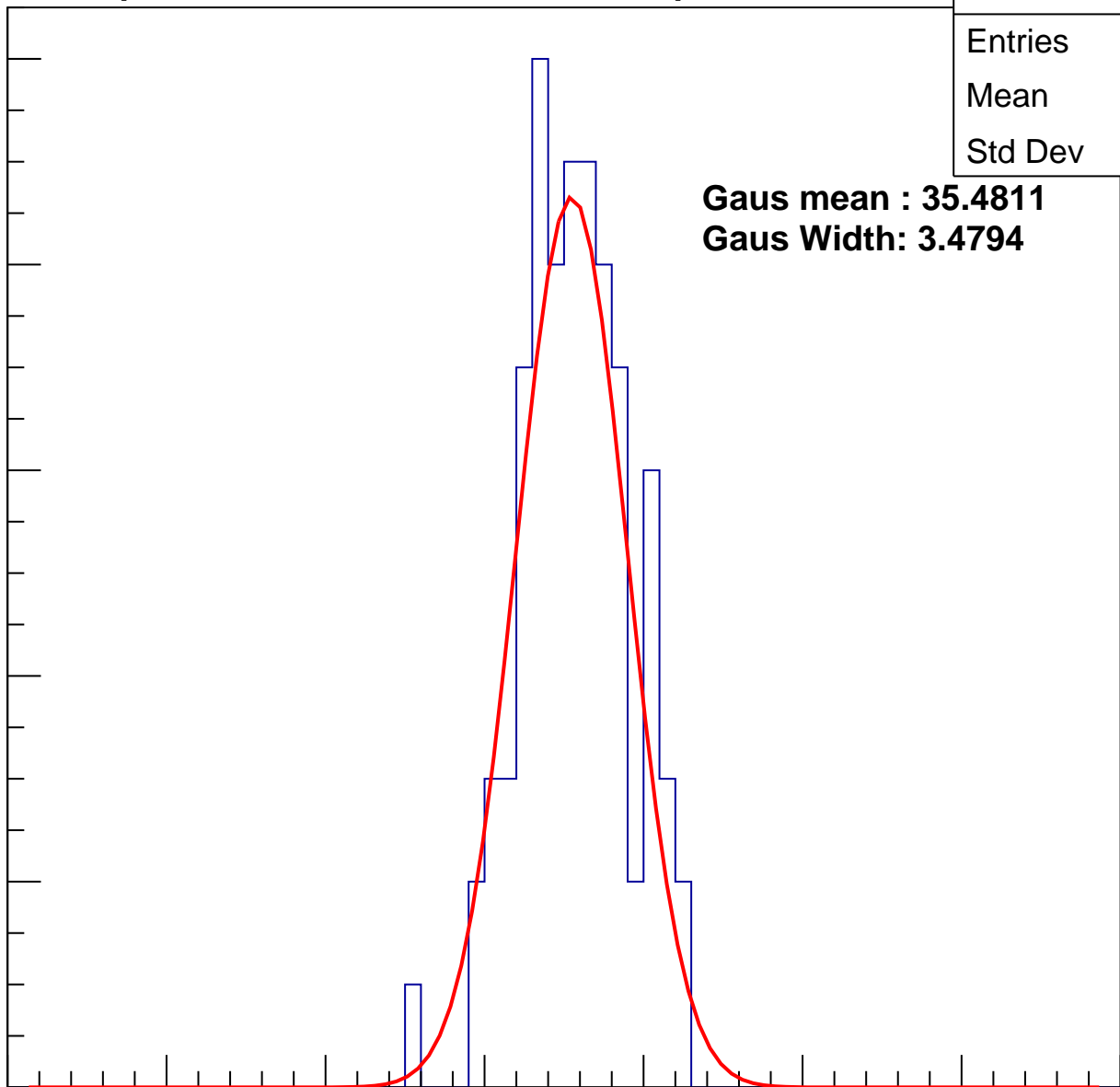
**Gaus mean : 35.4811**  
**Gaus Width: 3.4794**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch57, adc2

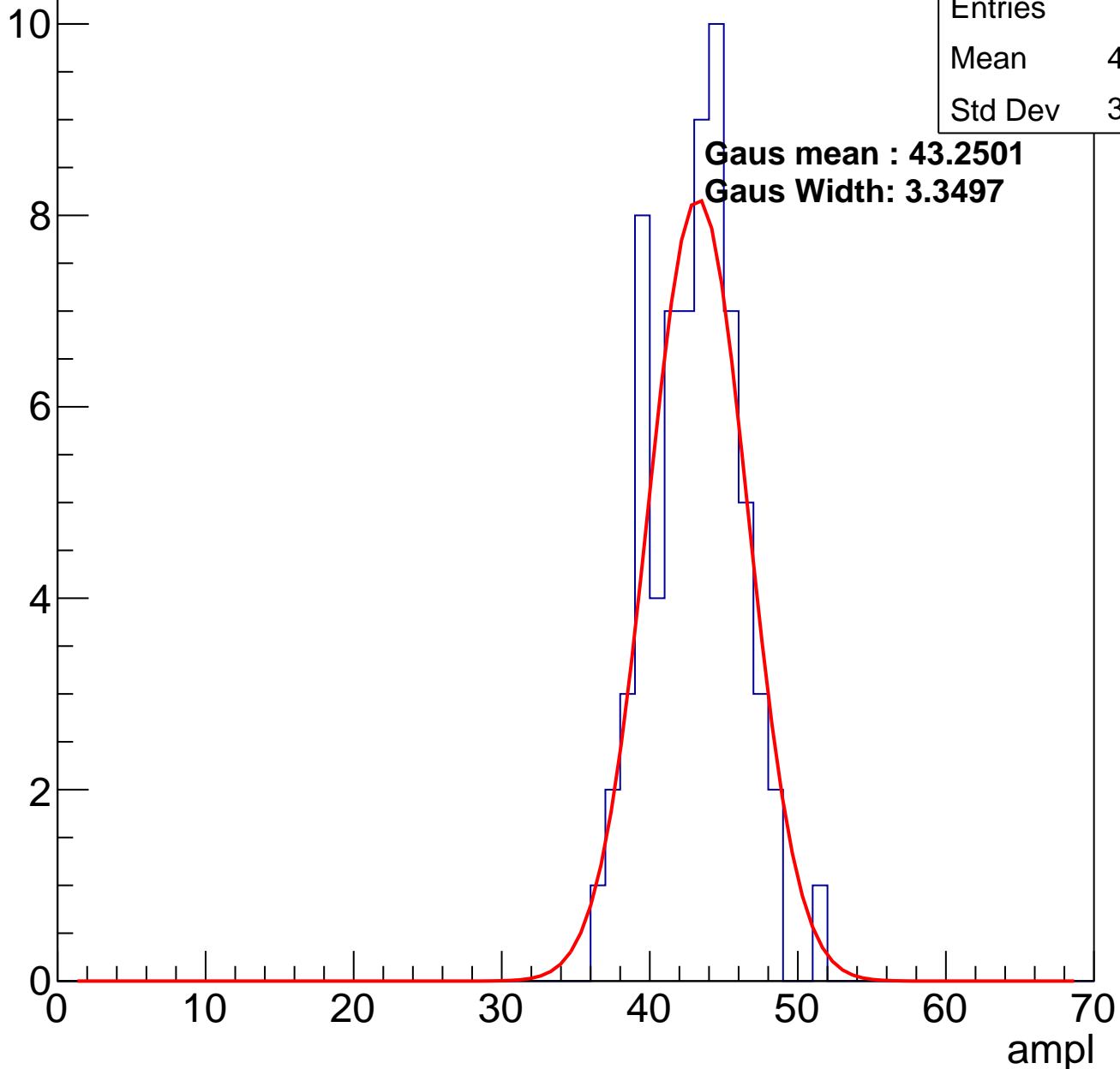
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	42.57
Std Dev	3.014

**Gaus mean : 43.2501**

**Gaus Width: 3.3497**

Entry

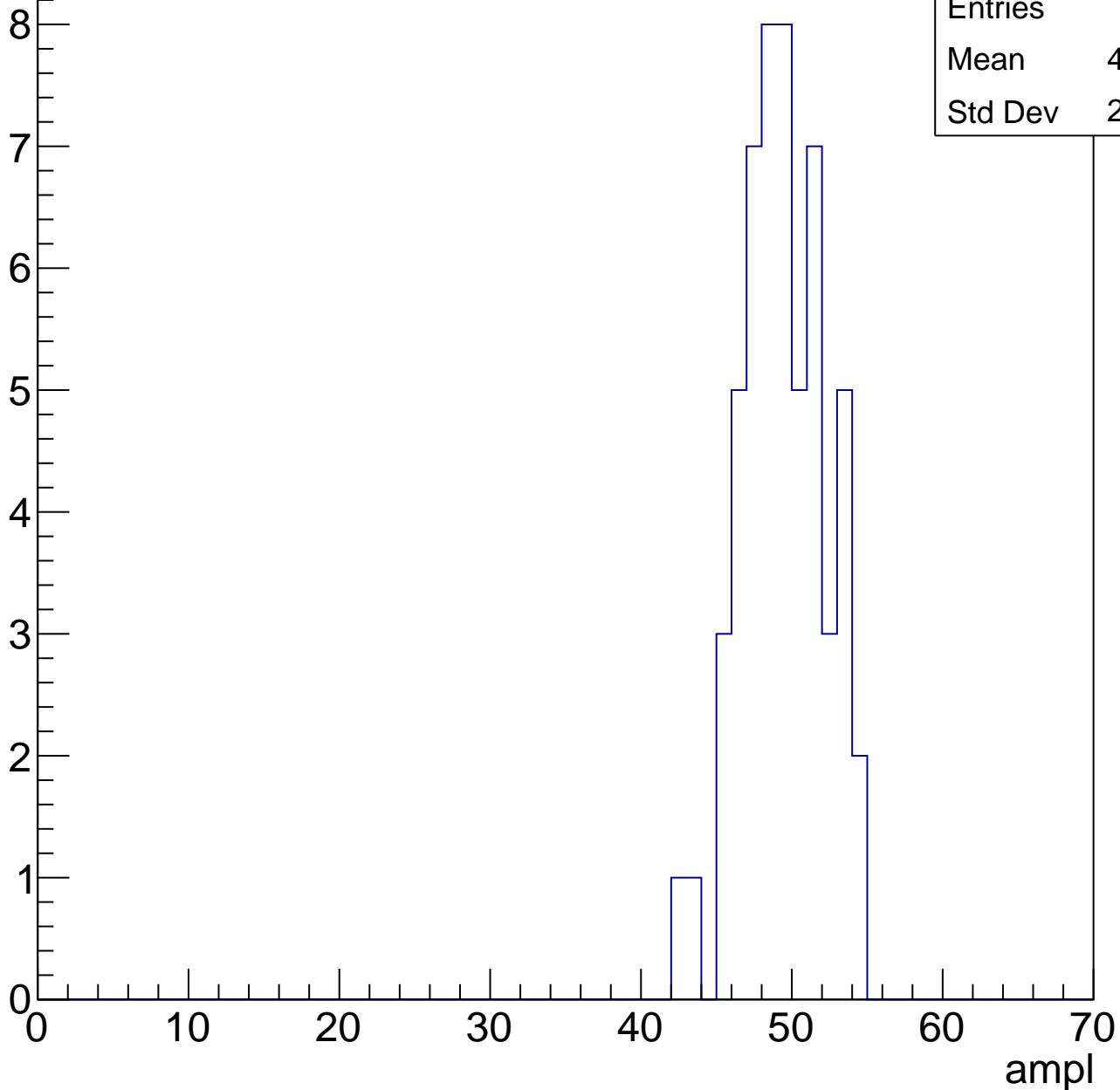


# B1L000S, U21-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

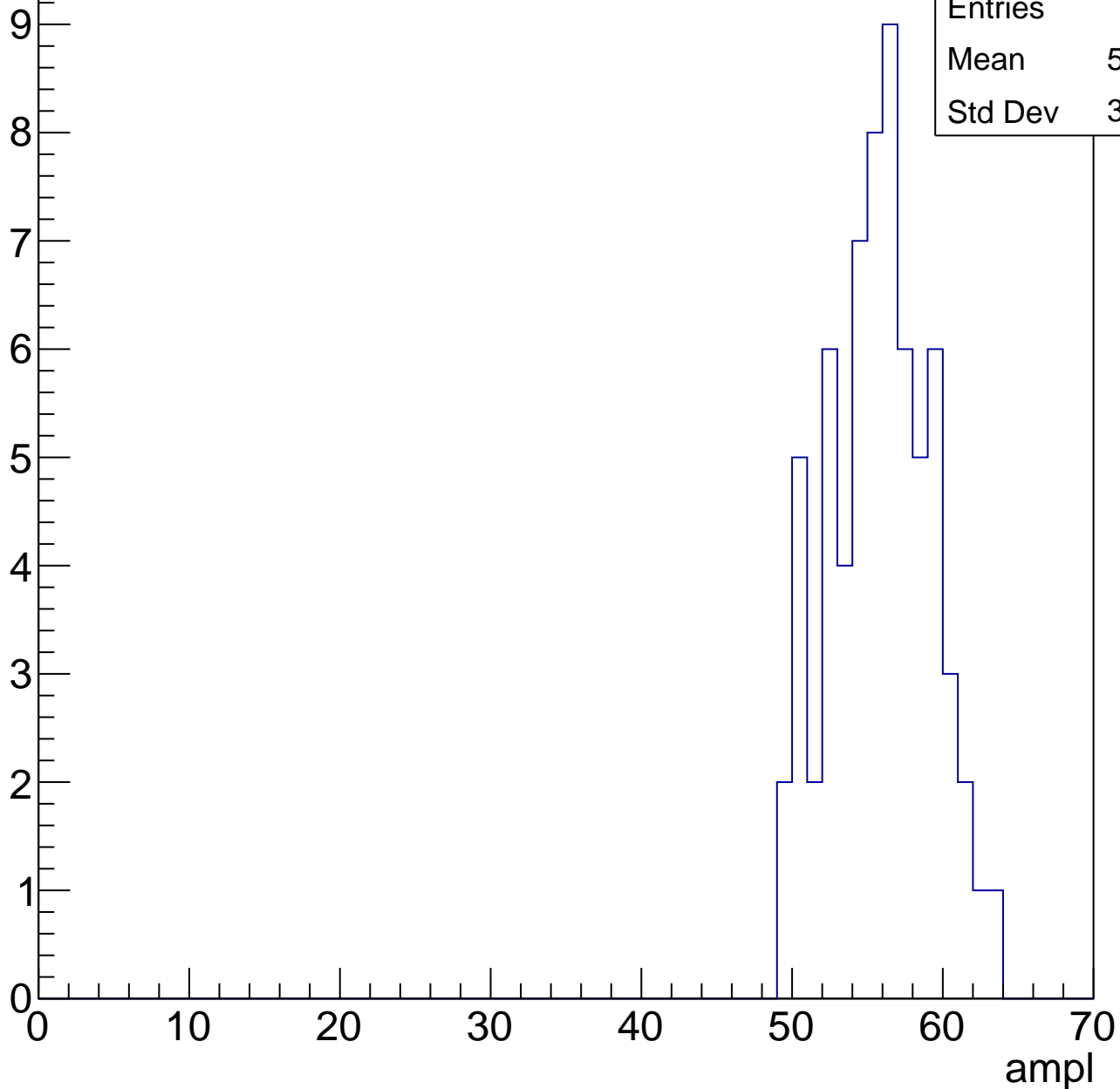
Entries	55
Mean	48.93
Std Dev	2.709



# B1L000S, U21-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

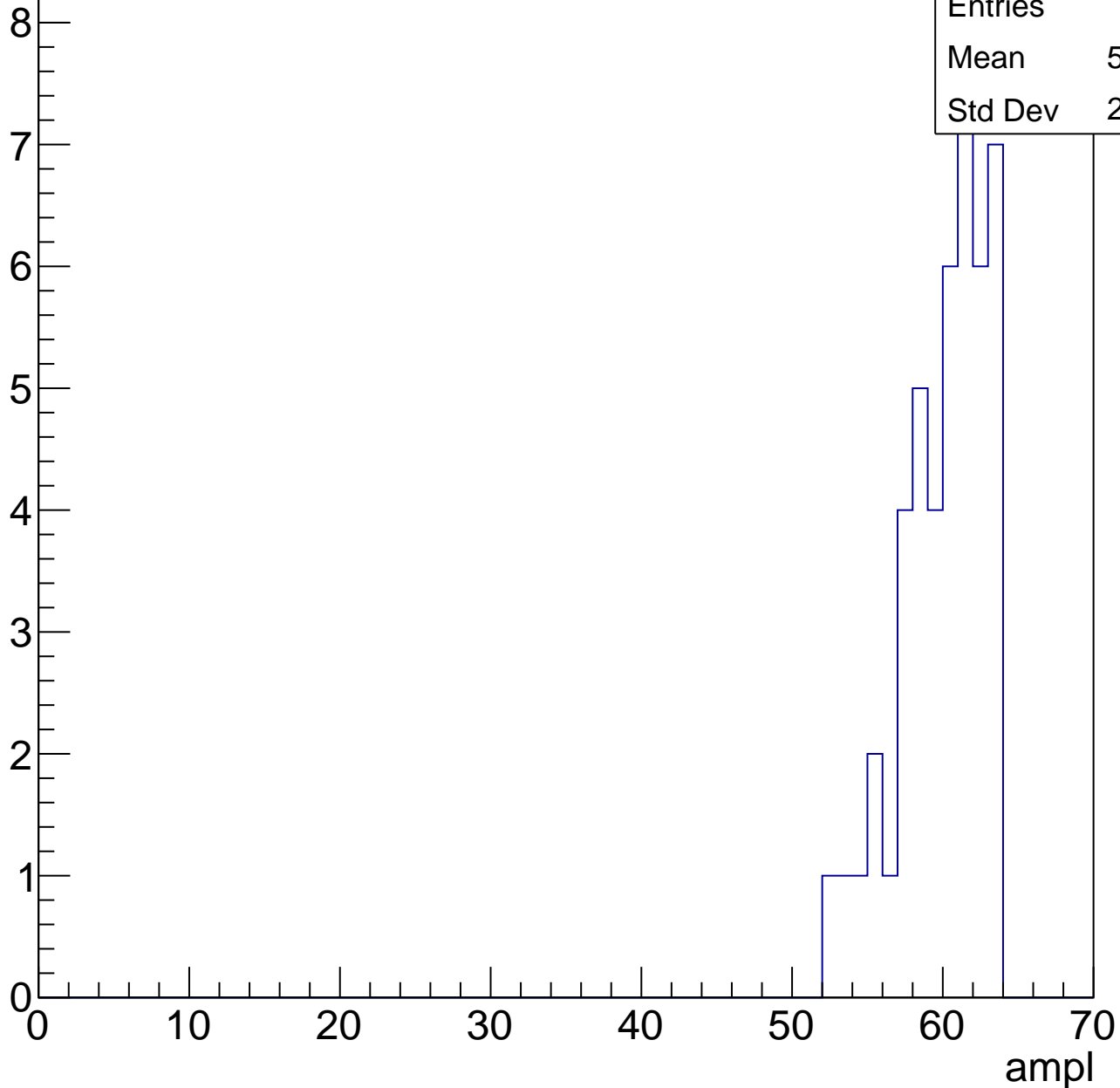
Entry



# B1L000S, U21-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

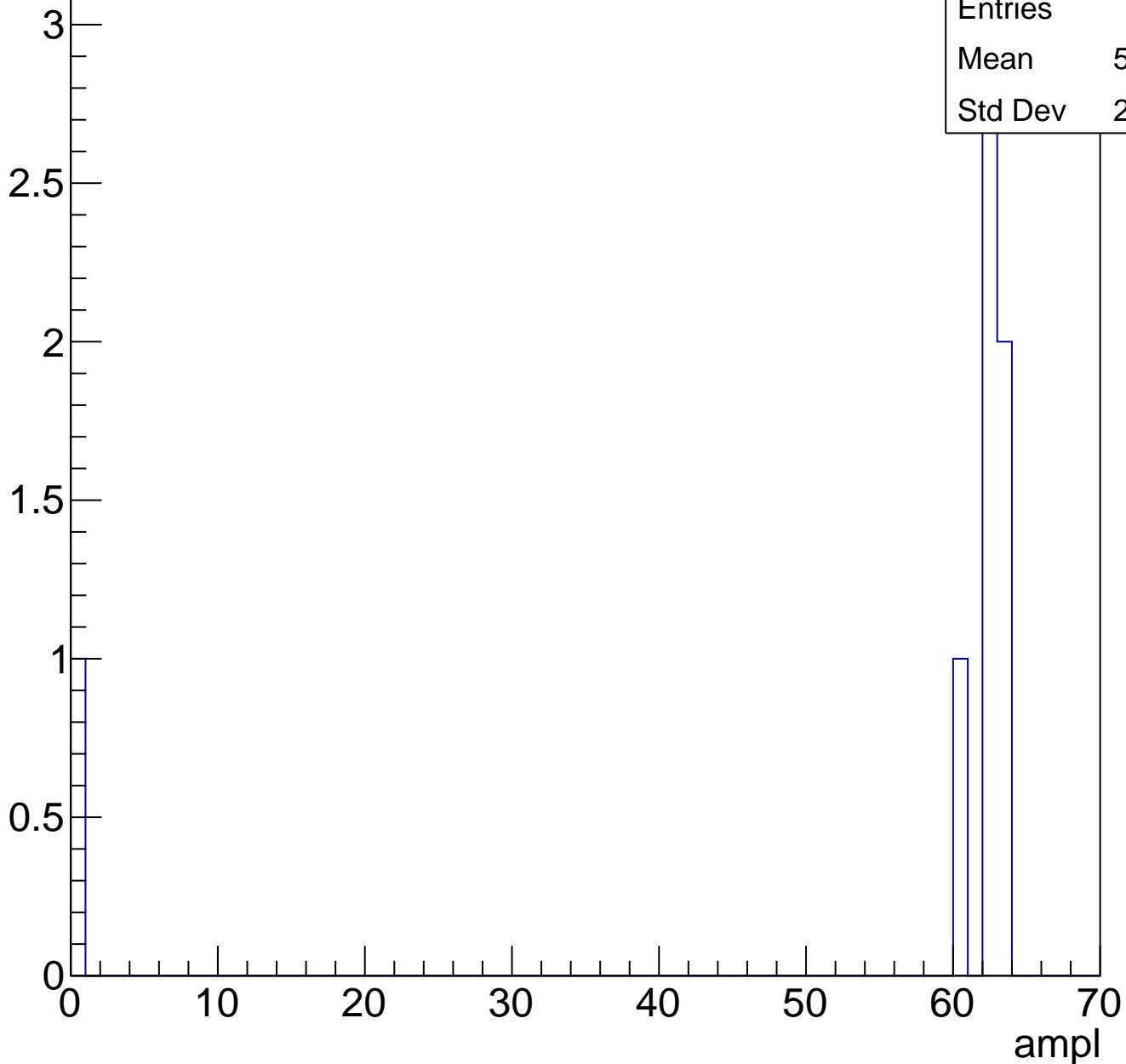
Entry



# B1L000S, U21-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B1L000S, U21-ch58, adc0

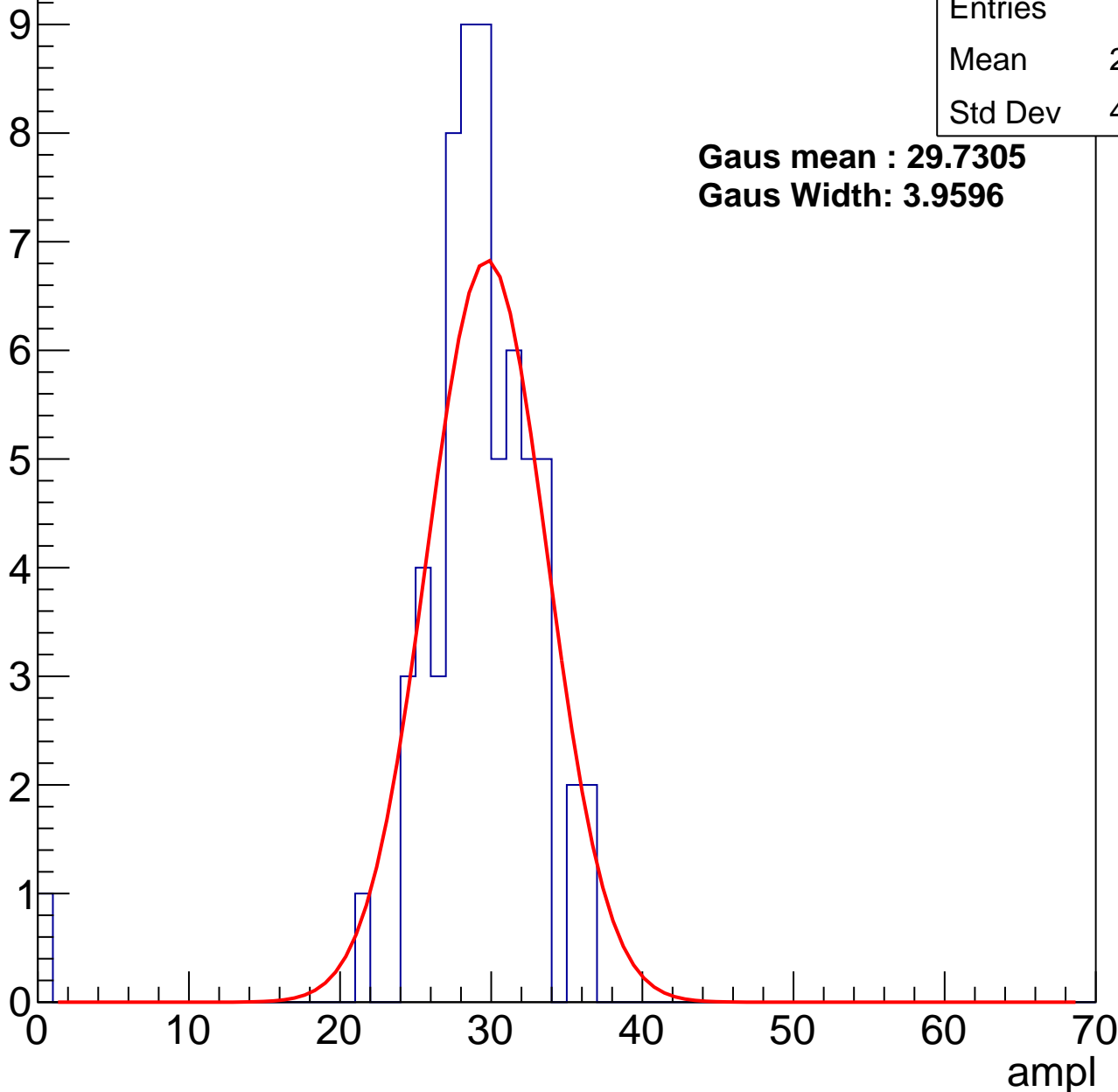
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	28.62
Std Dev	4.759

**Gaus mean : 29.7305**

**Gaus Width: 3.9596**



# B1L000S, U21-ch58, adc1

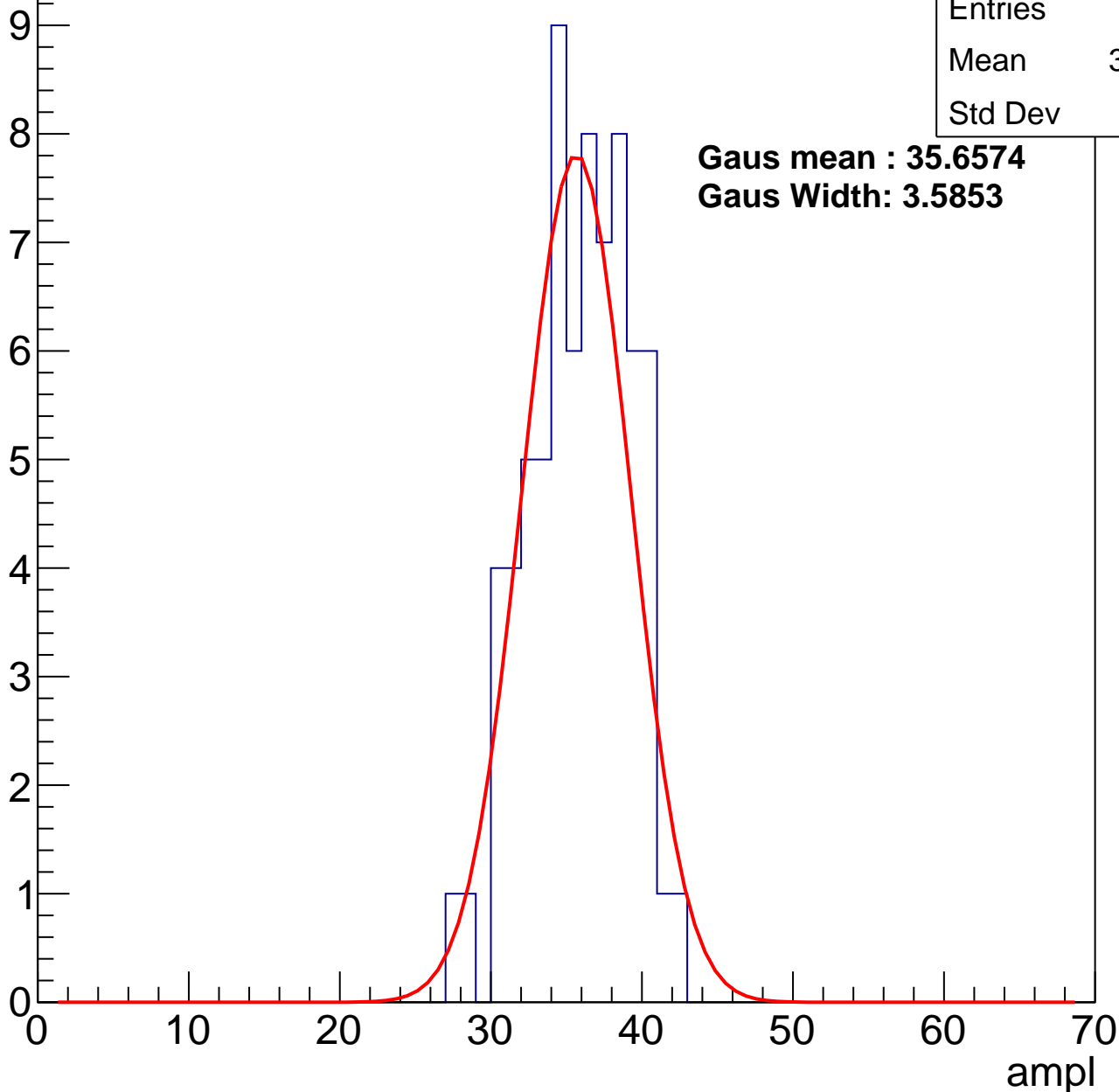
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	35.39
Std Dev	3.29

**Gaus mean : 35.6574**

**Gaus Width: 3.5853**



# B1L000S, U21-ch58, adc2

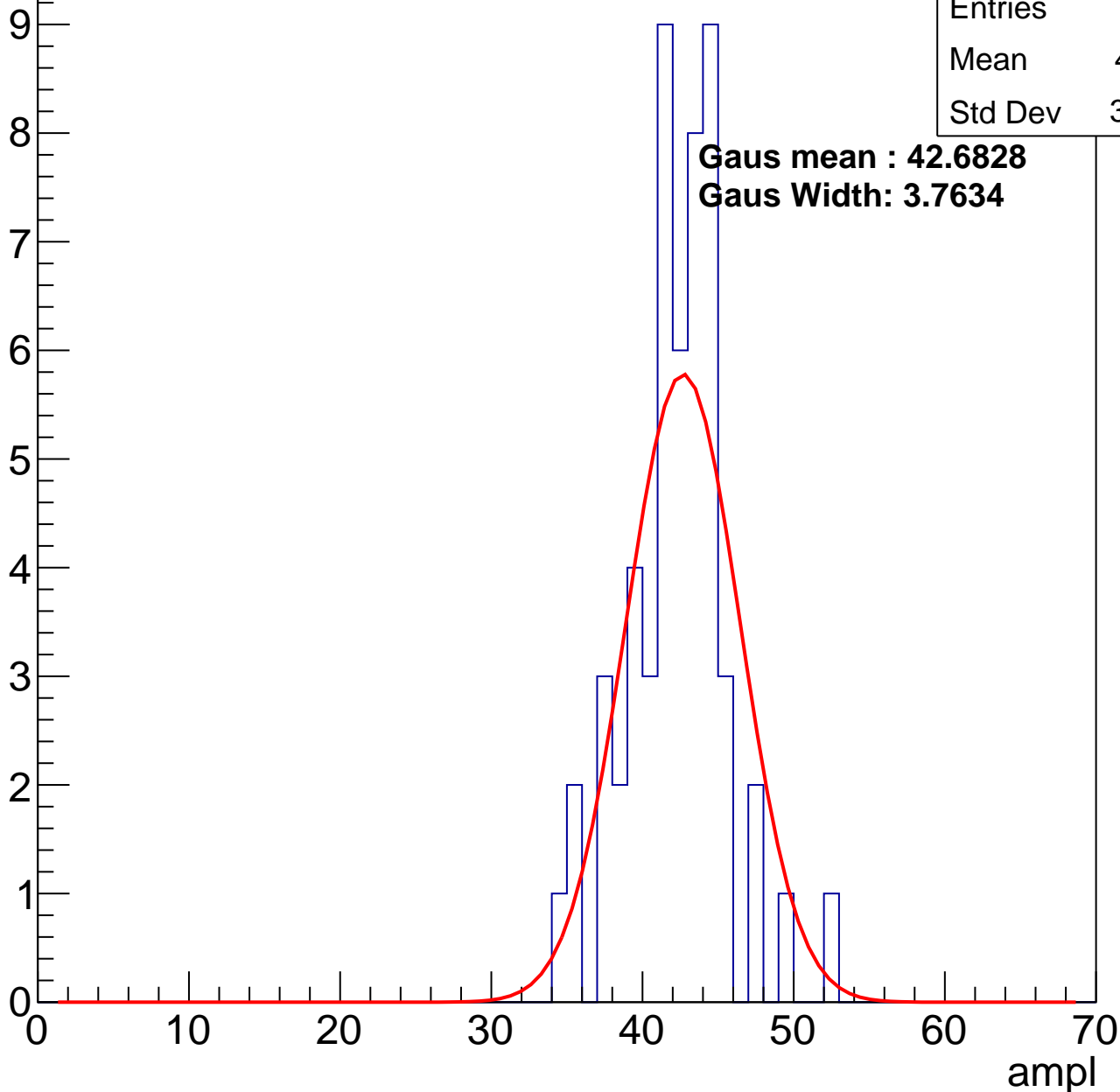
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.81
Std Dev	3.317

**Gaus mean : 42.6828**

**Gaus Width: 3.7634**



# B1L000S, U21-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

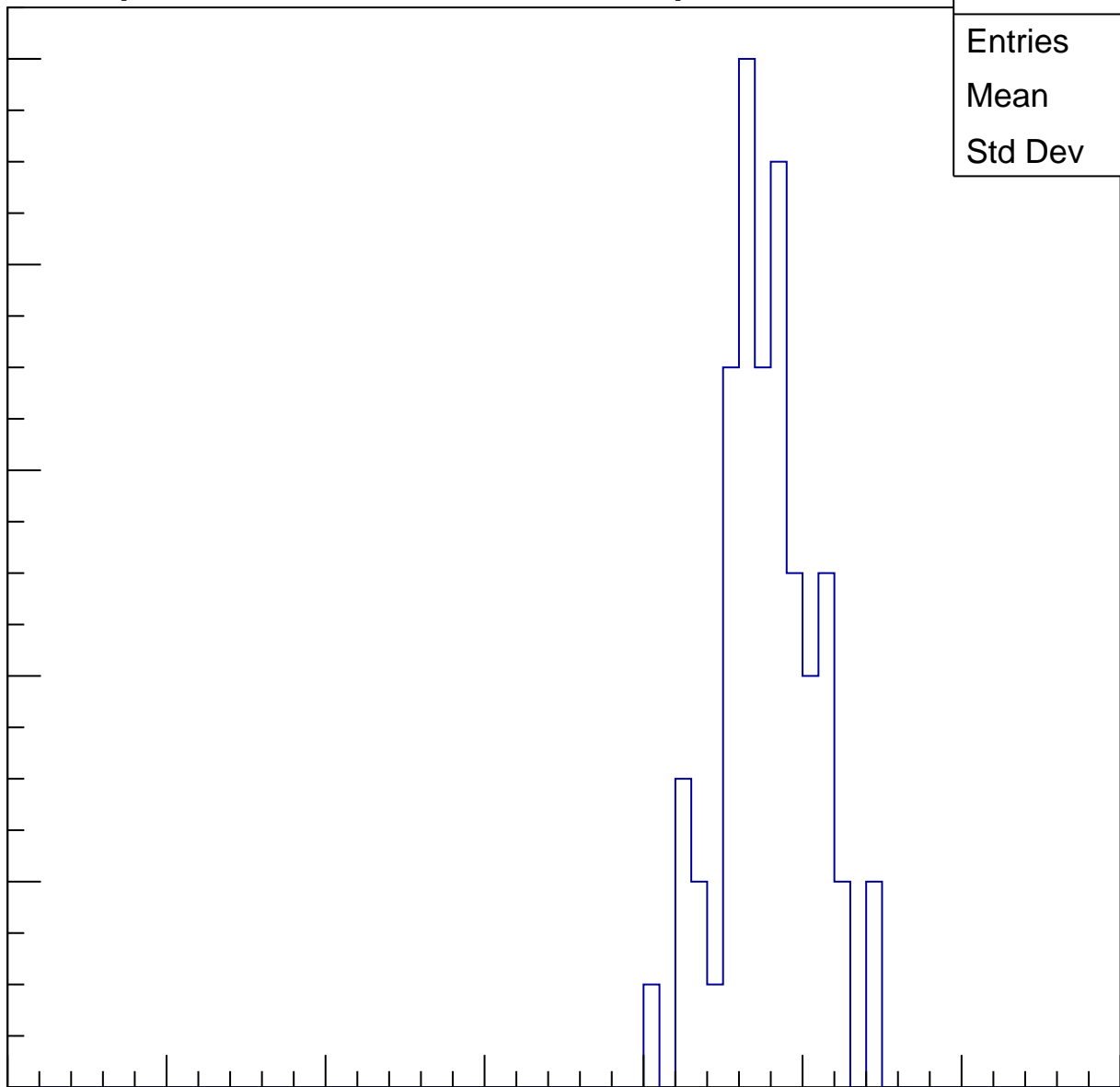
Entries	58
Mean	47.31
Std Dev	2.908

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	52.89
Std Dev	2.67

Entry

10

8

6

4

2

0

0

10

20

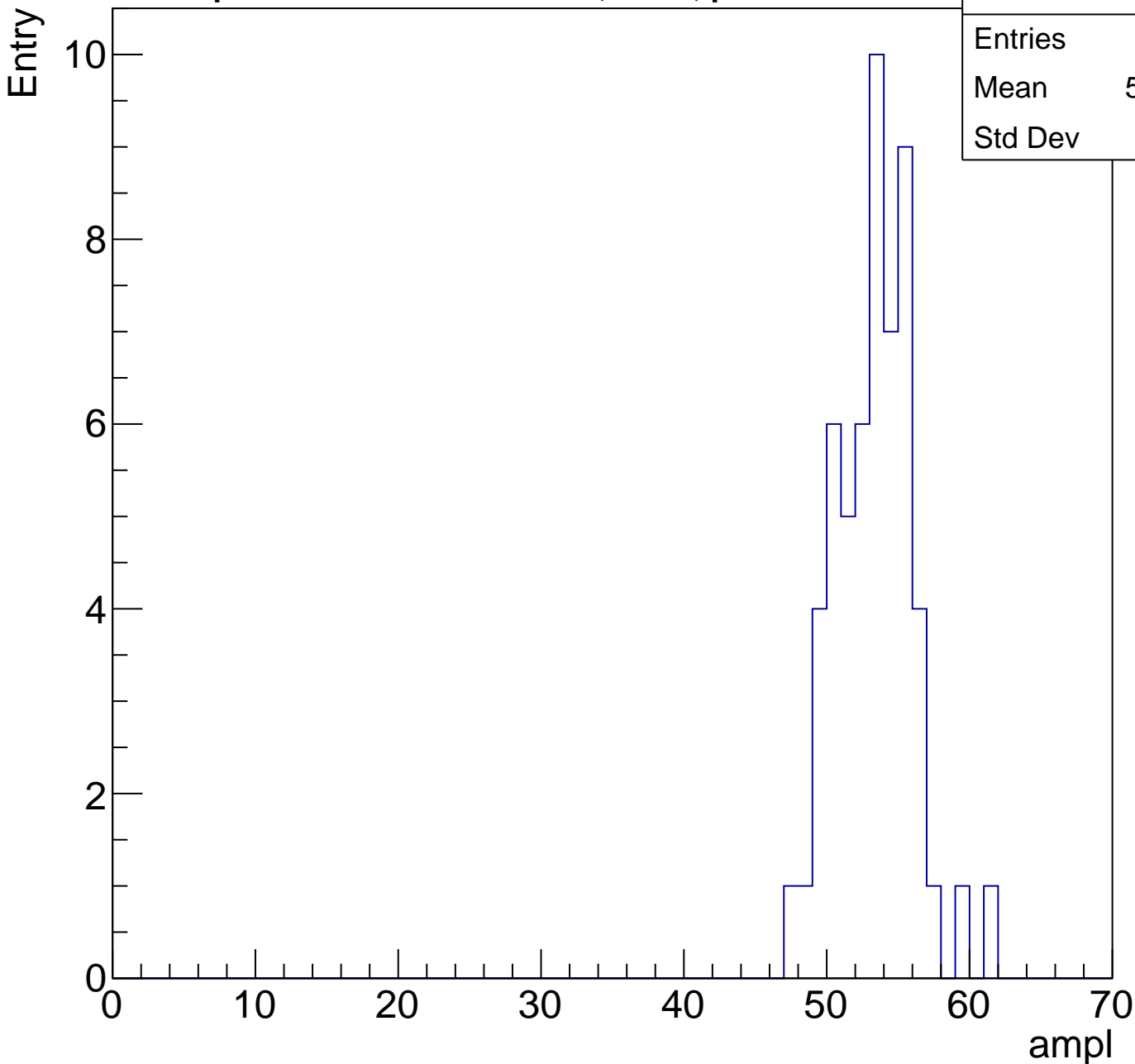
30

40

50

60

ampl



# B1L000S, U21-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

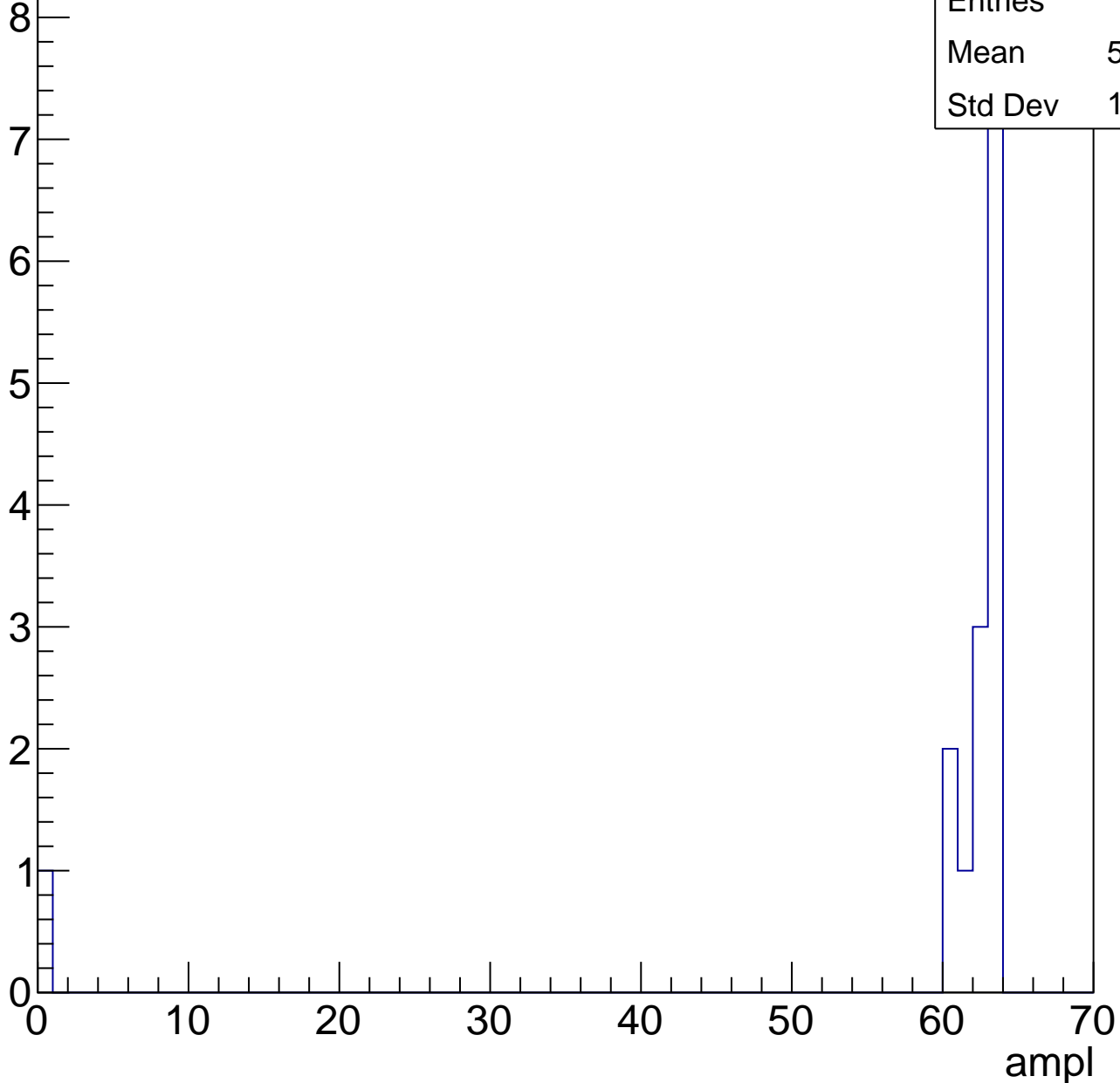
Entries	61
Mean	58.82
Std Dev	2.287

# B1L000S, U21-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	58.07
Std Dev	15.55





# B1L000S, U21-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	52
Mean	27.37
Std Dev	4.678

**Gaus mean : 28.8723**

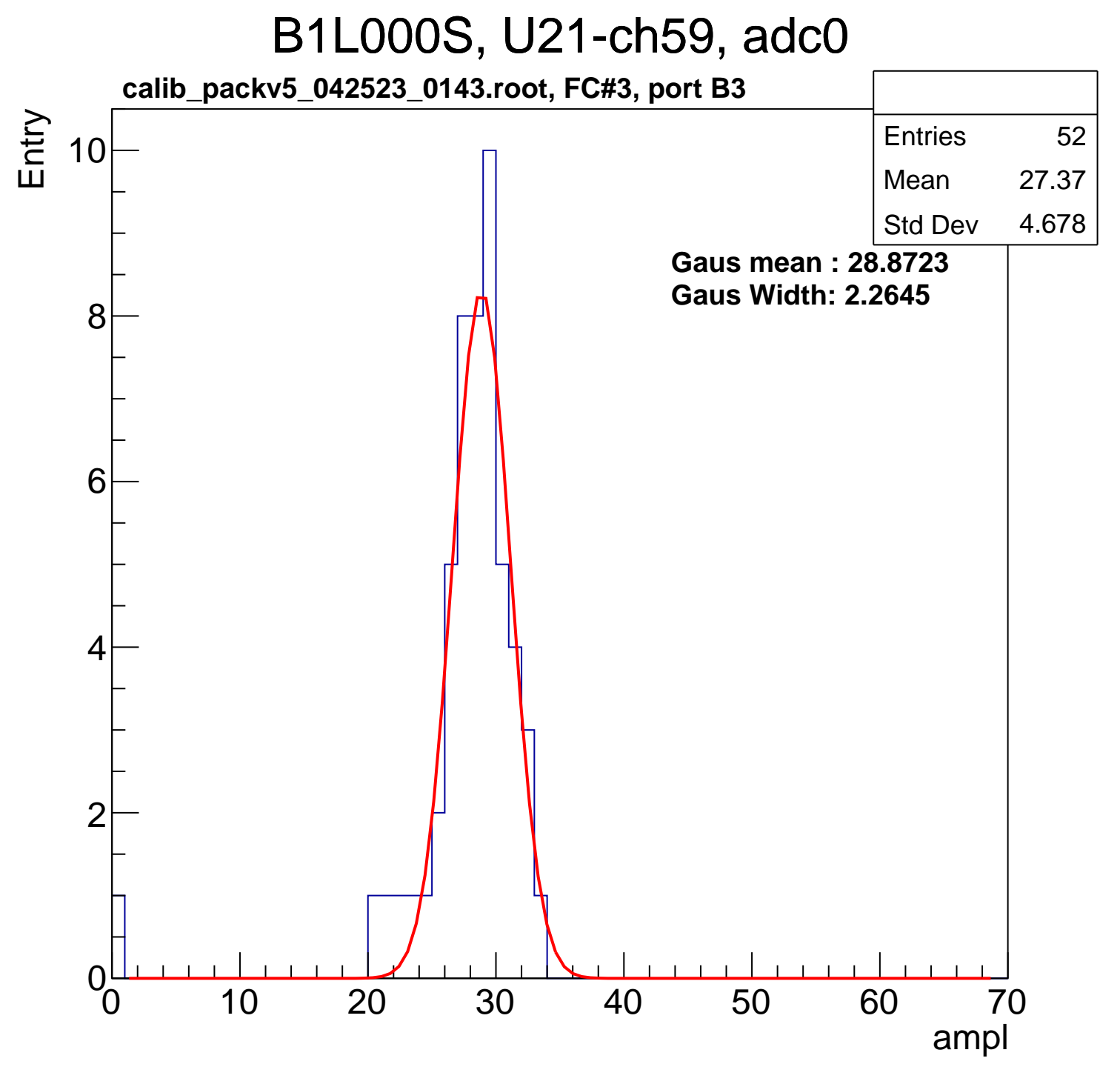
**Gaus Width: 2.2645**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch59, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	34.87
Std Dev	3.176

**Gaus mean : 35.0770**

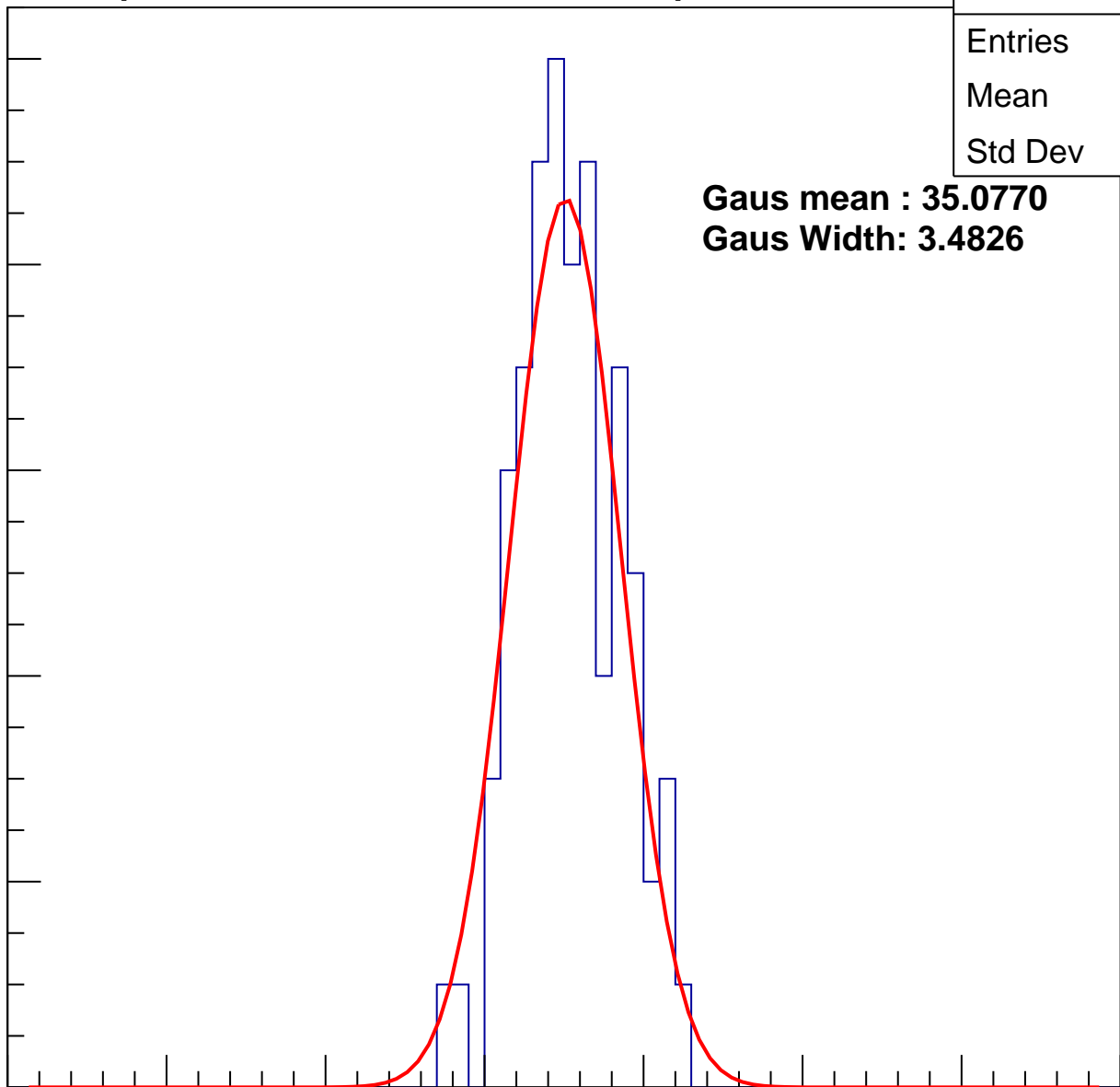
**Gaus Width: 3.4826**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch59, adc2

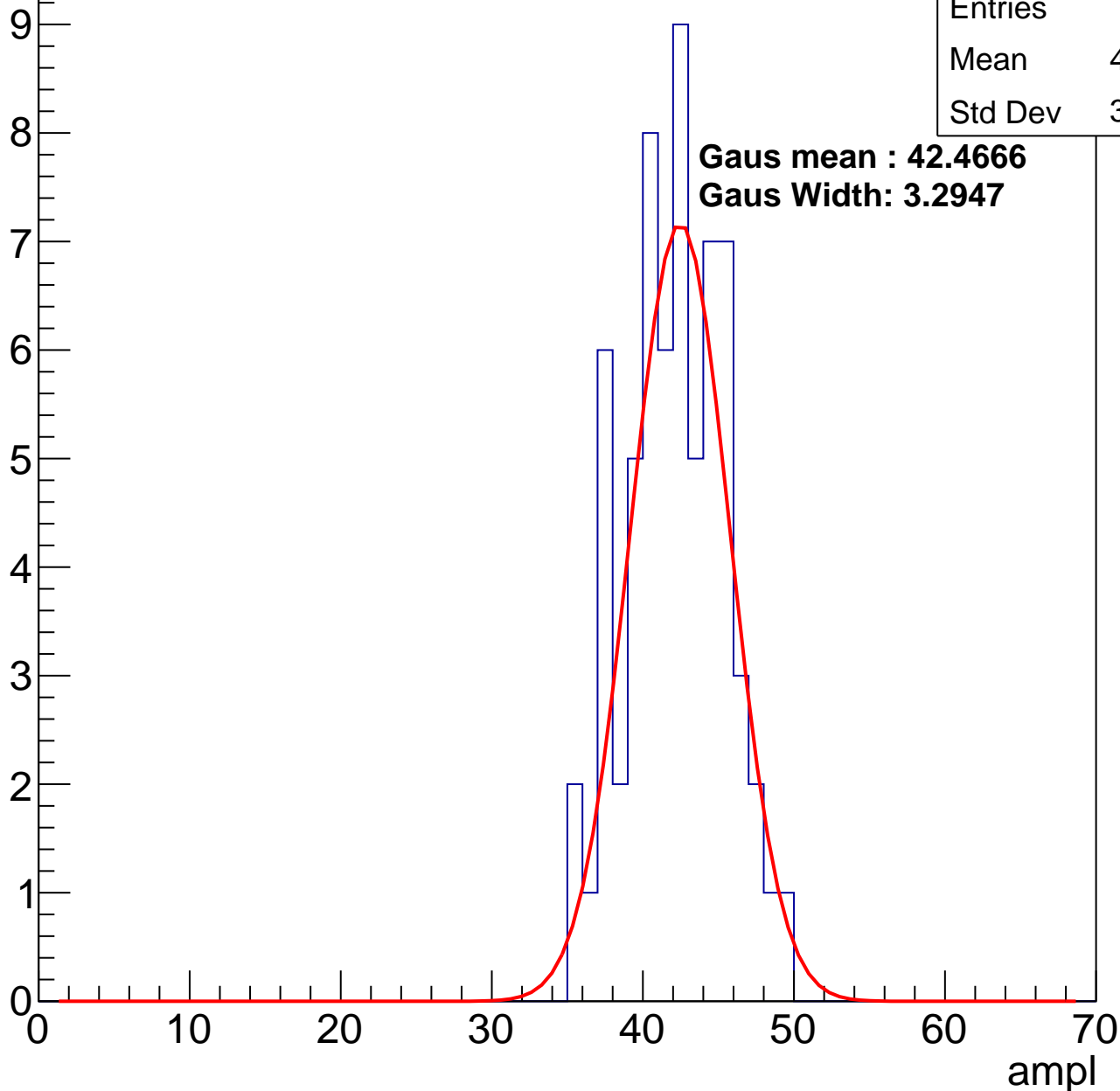
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.69
Std Dev	3.215

**Gaus mean : 42.4666**

**Gaus Width: 3.2947**

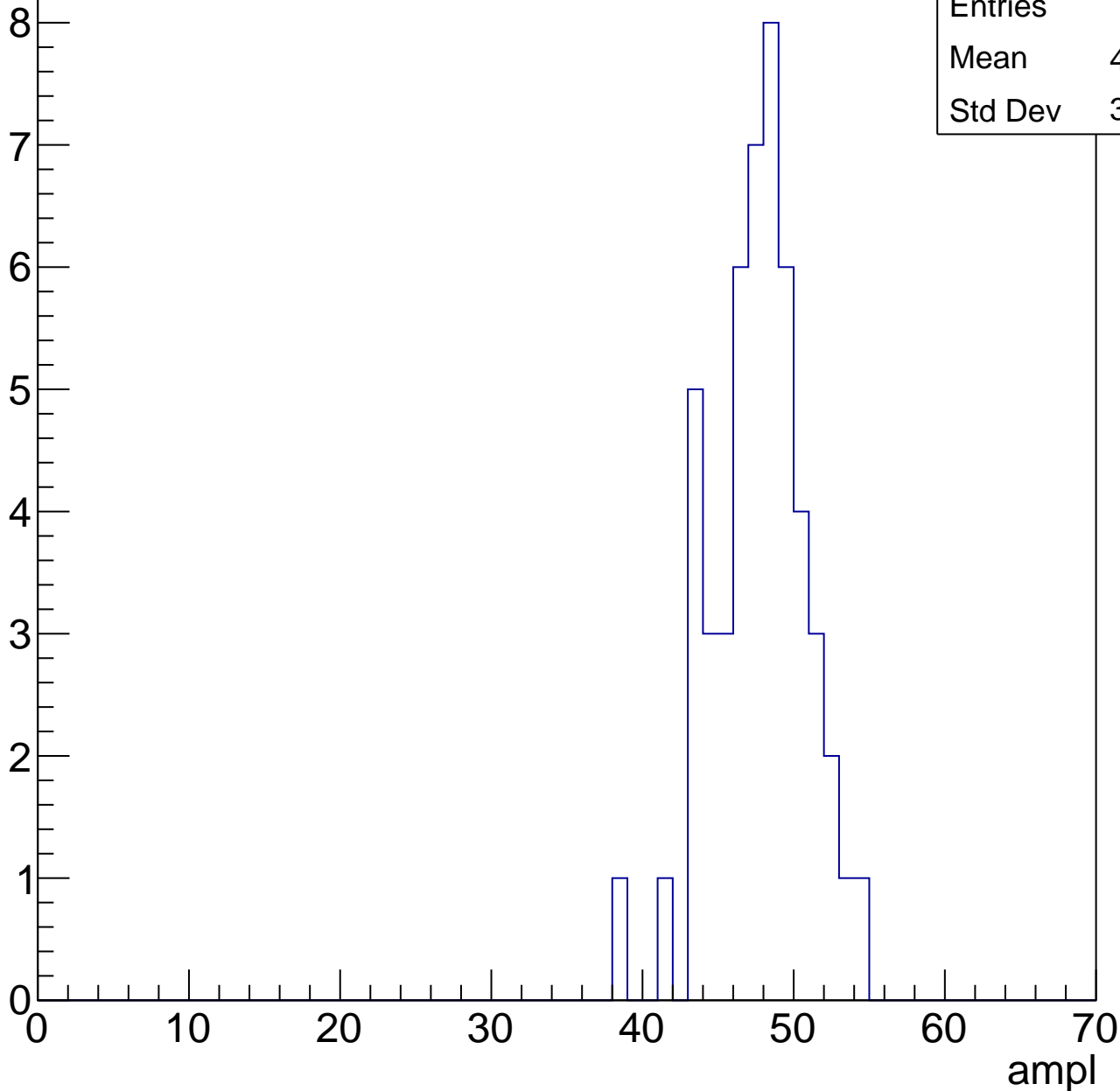


# B1L000S, U21-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

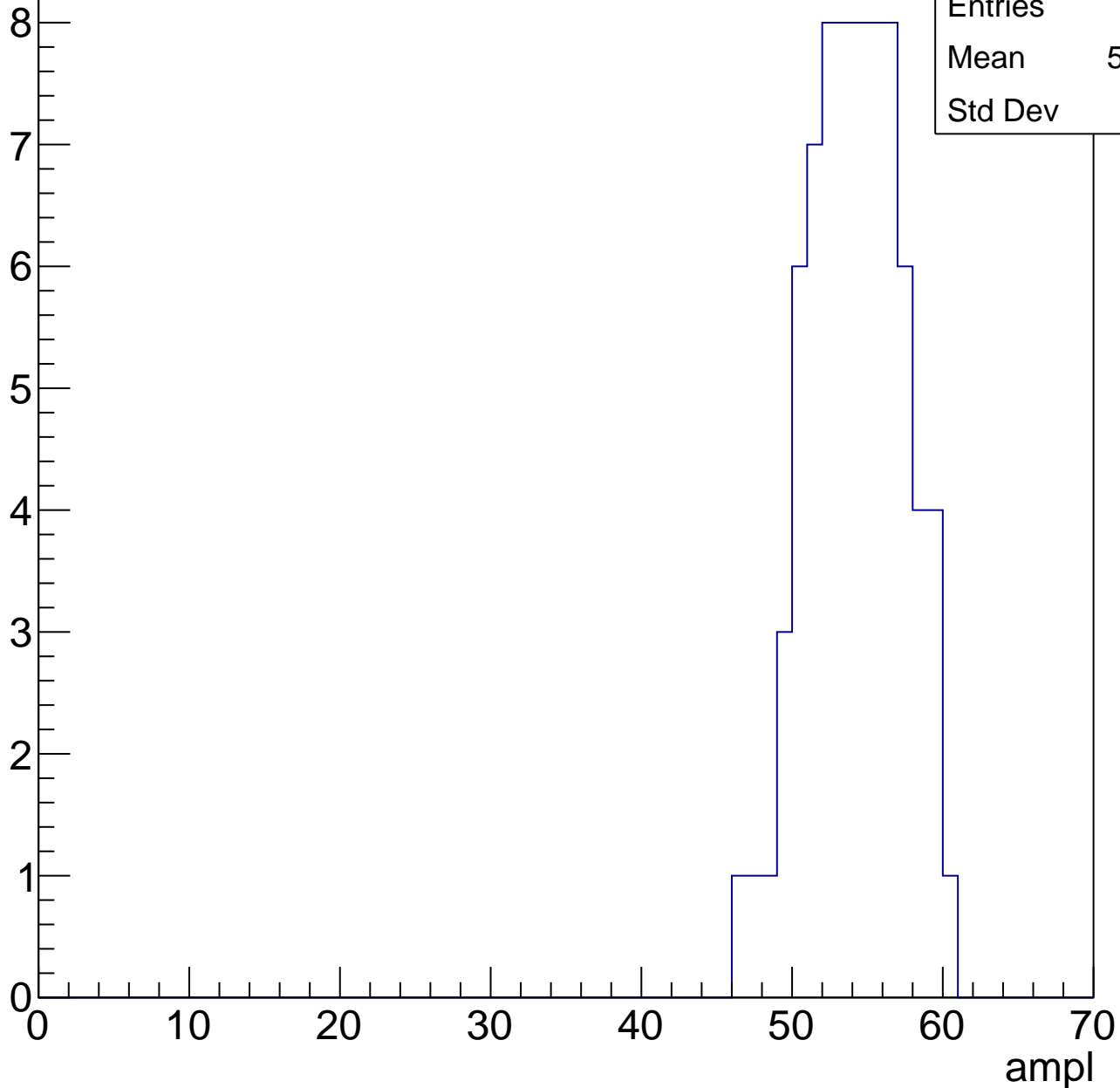
Entries	51
Mean	47.22
Std Dev	3.102



# B1L000S, U21-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

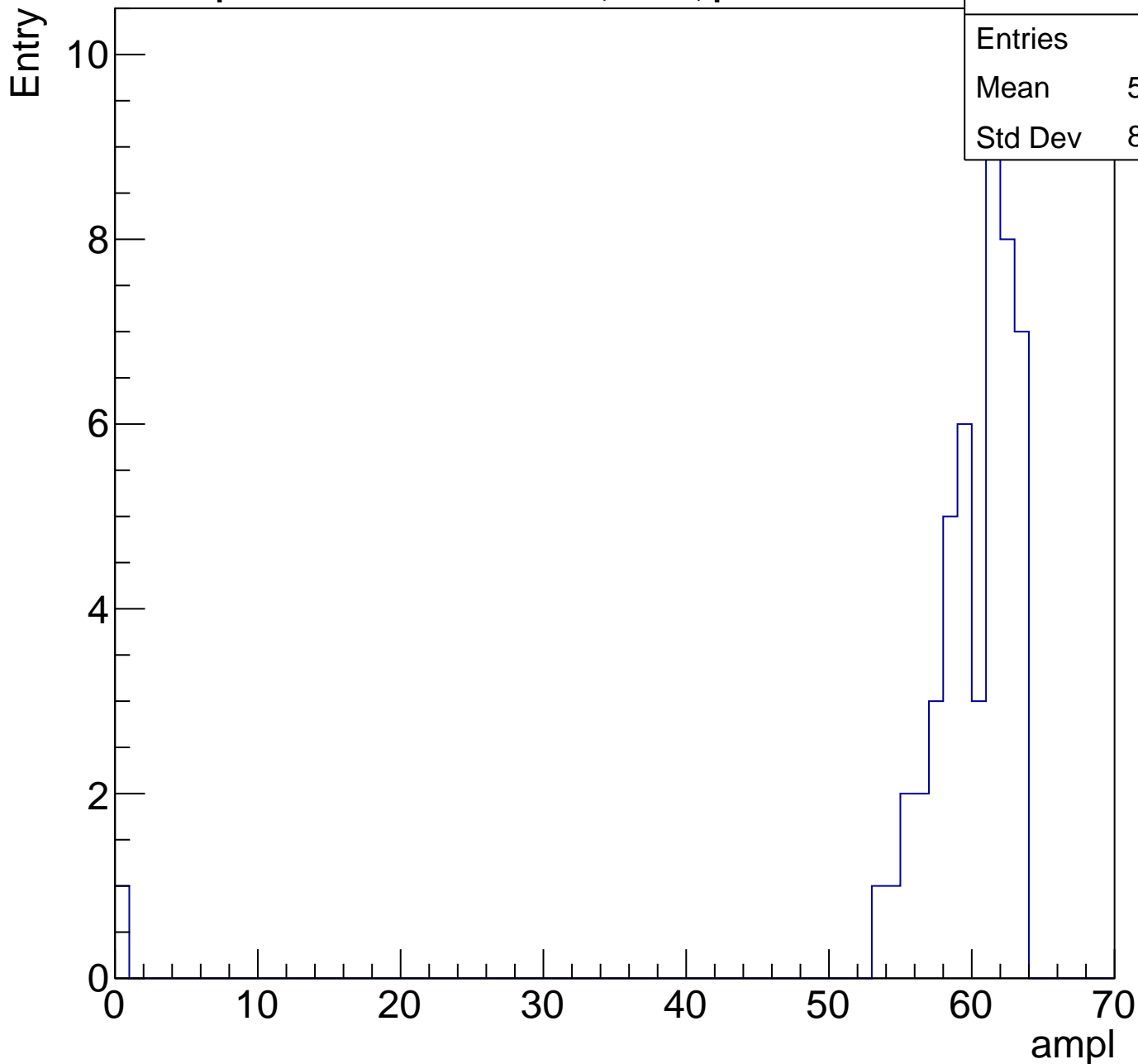


Entries	74
Mean	53.72
Std Dev	3.1

# B1L000S, U21-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

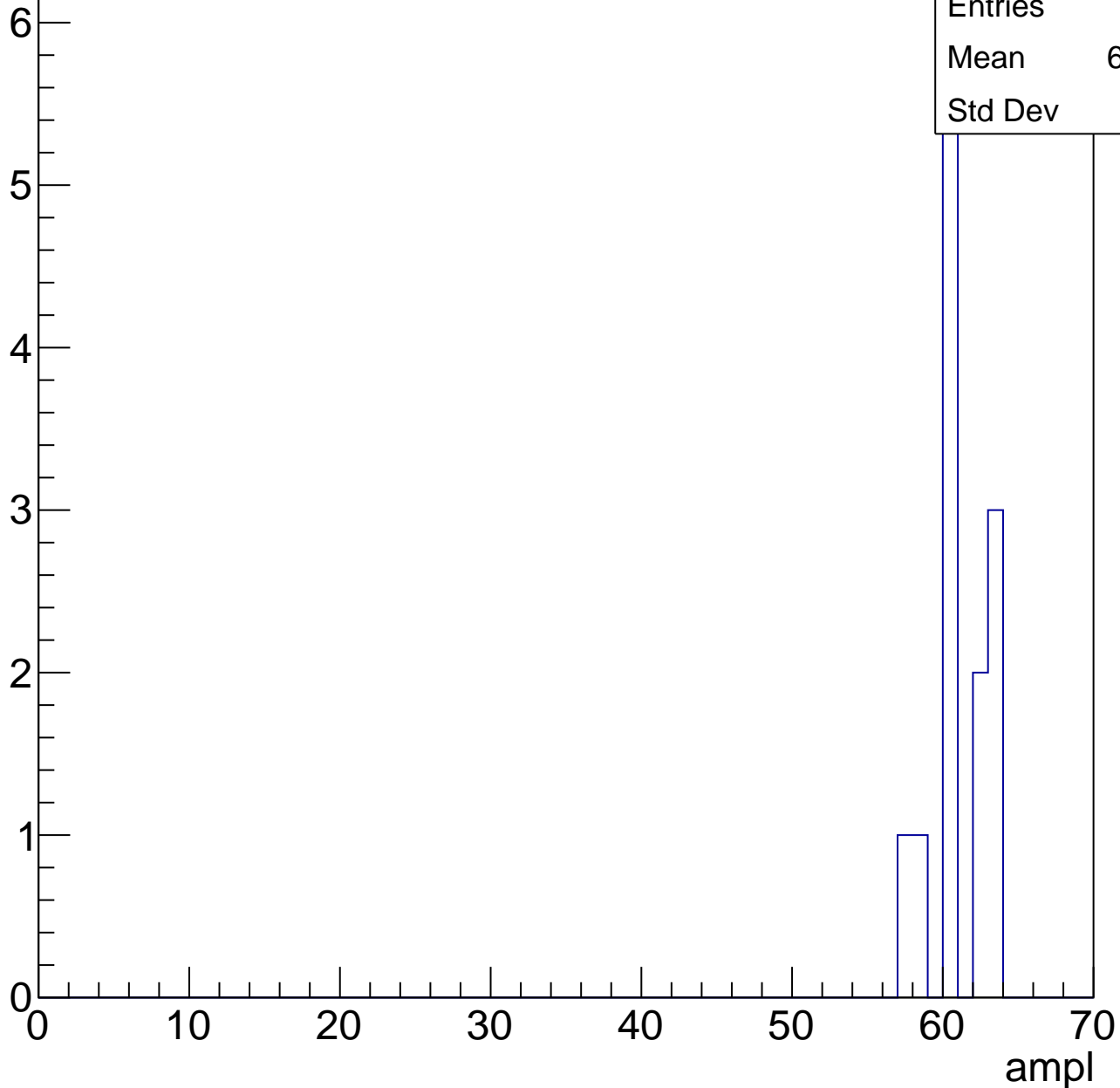
Entries	49
Mean	58.59
Std Dev	8.839



# B1L000S, U21-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch60, adc0

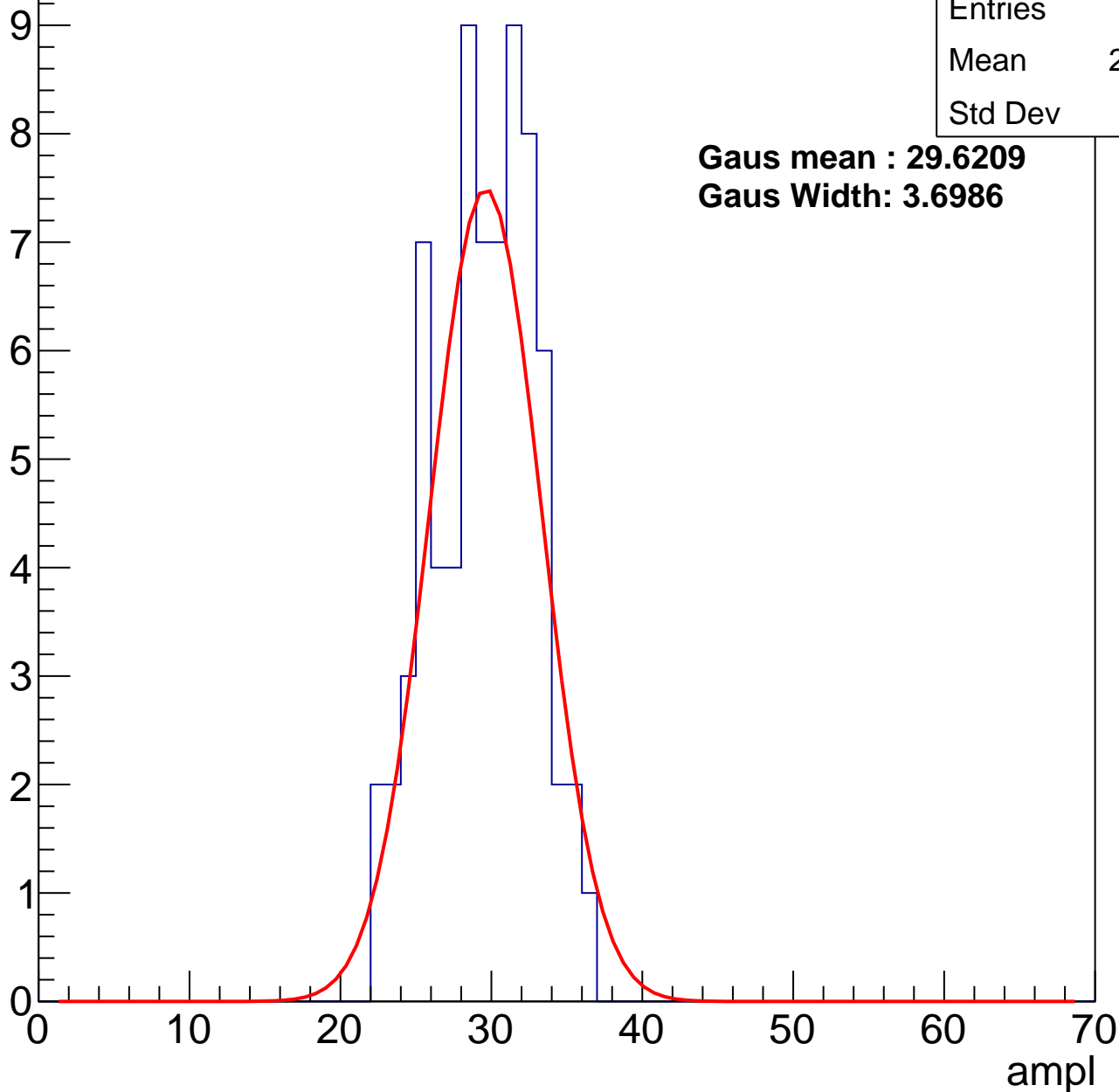
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	29.05
Std Dev	3.31

**Gaus mean : 29.6209**

**Gaus Width: 3.6986**



# B1L000S, U21-ch60, adc1

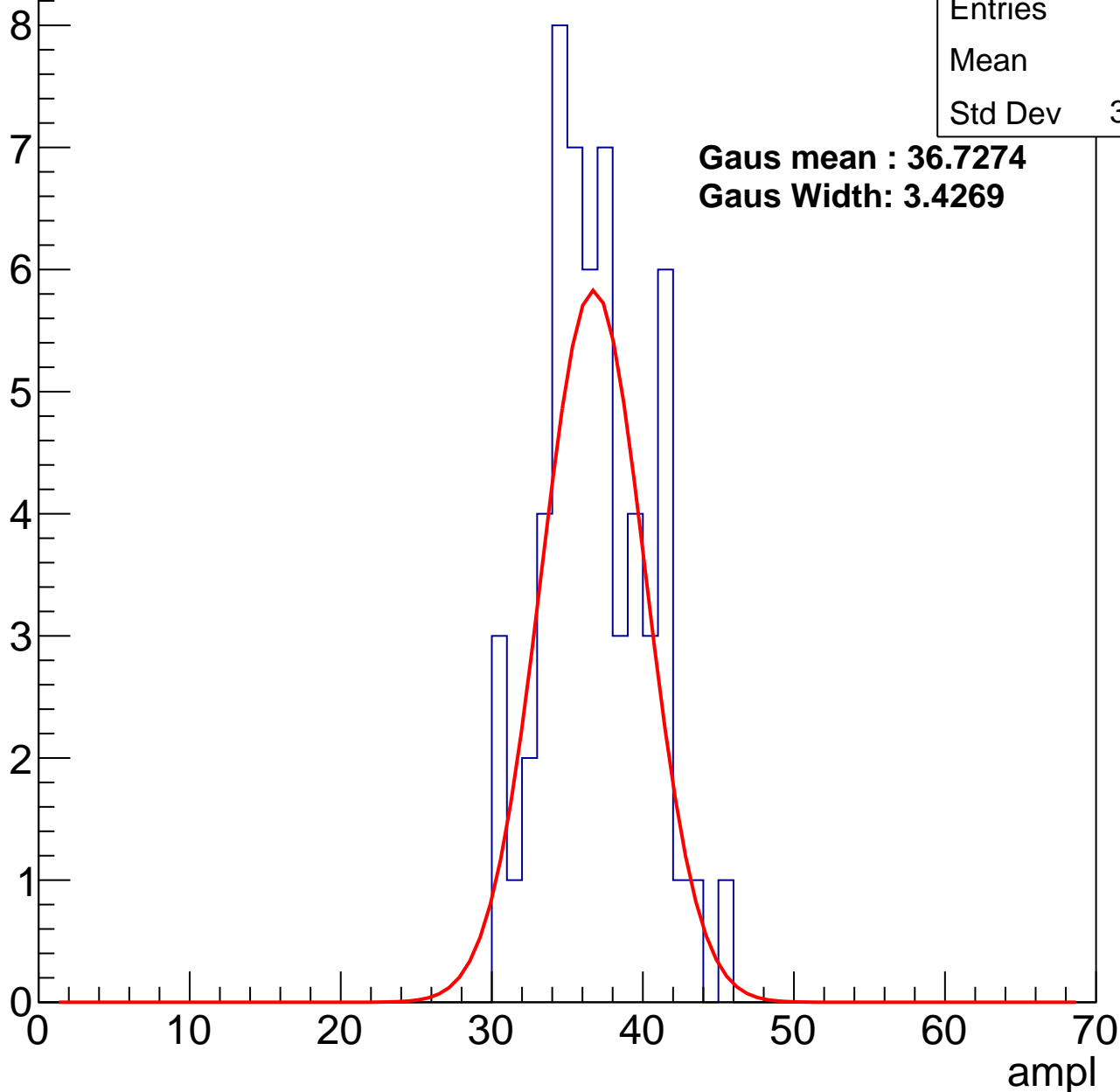
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	36.4
Std Dev	3.376

**Gaus mean : 36.7274**

**Gaus Width: 3.4269**



# B1L000S, U21-ch60, adc2

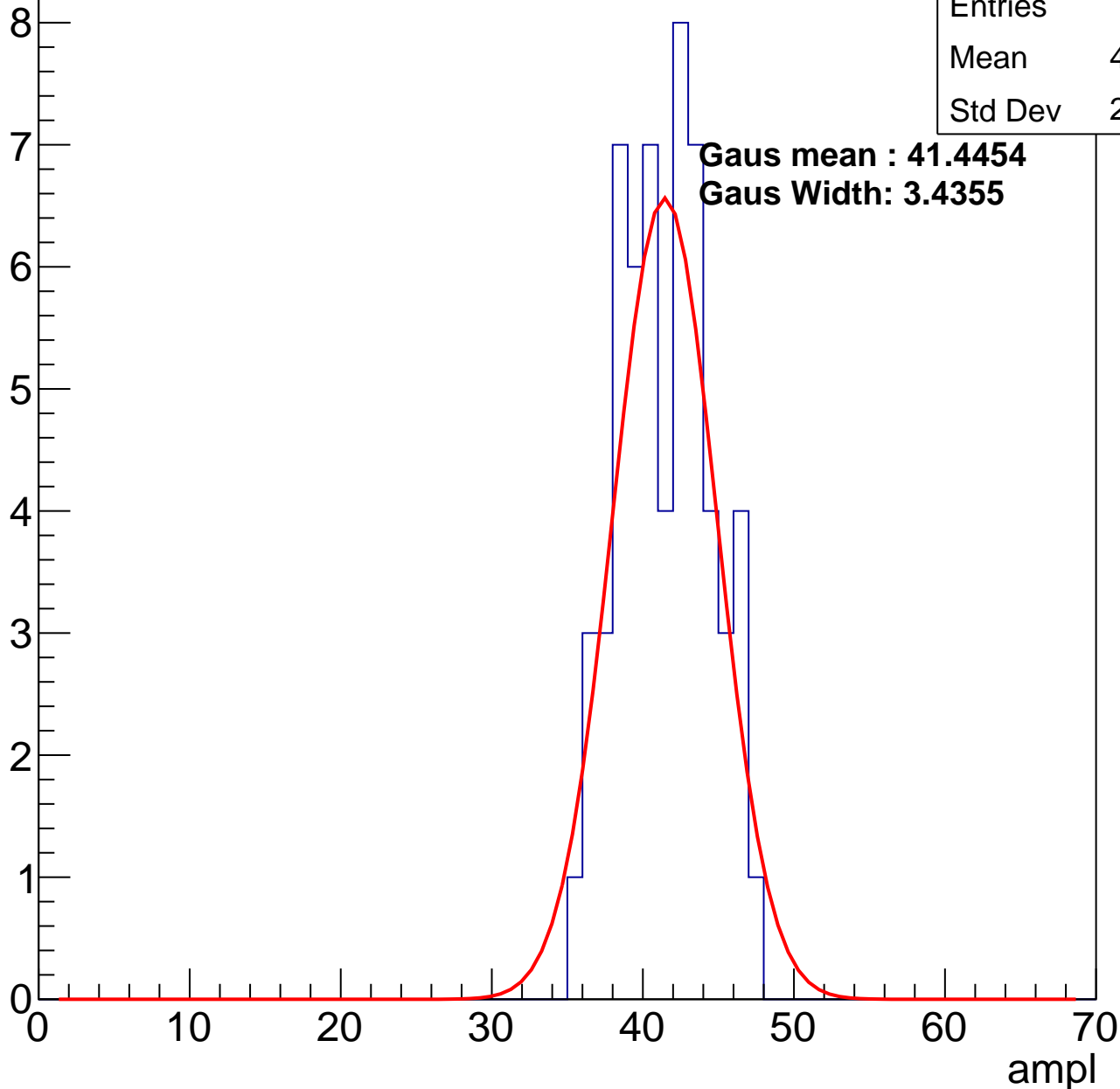
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	40.98
Std Dev	2.962

**Gaus mean : 41.4454**

**Gaus Width: 3.4355**

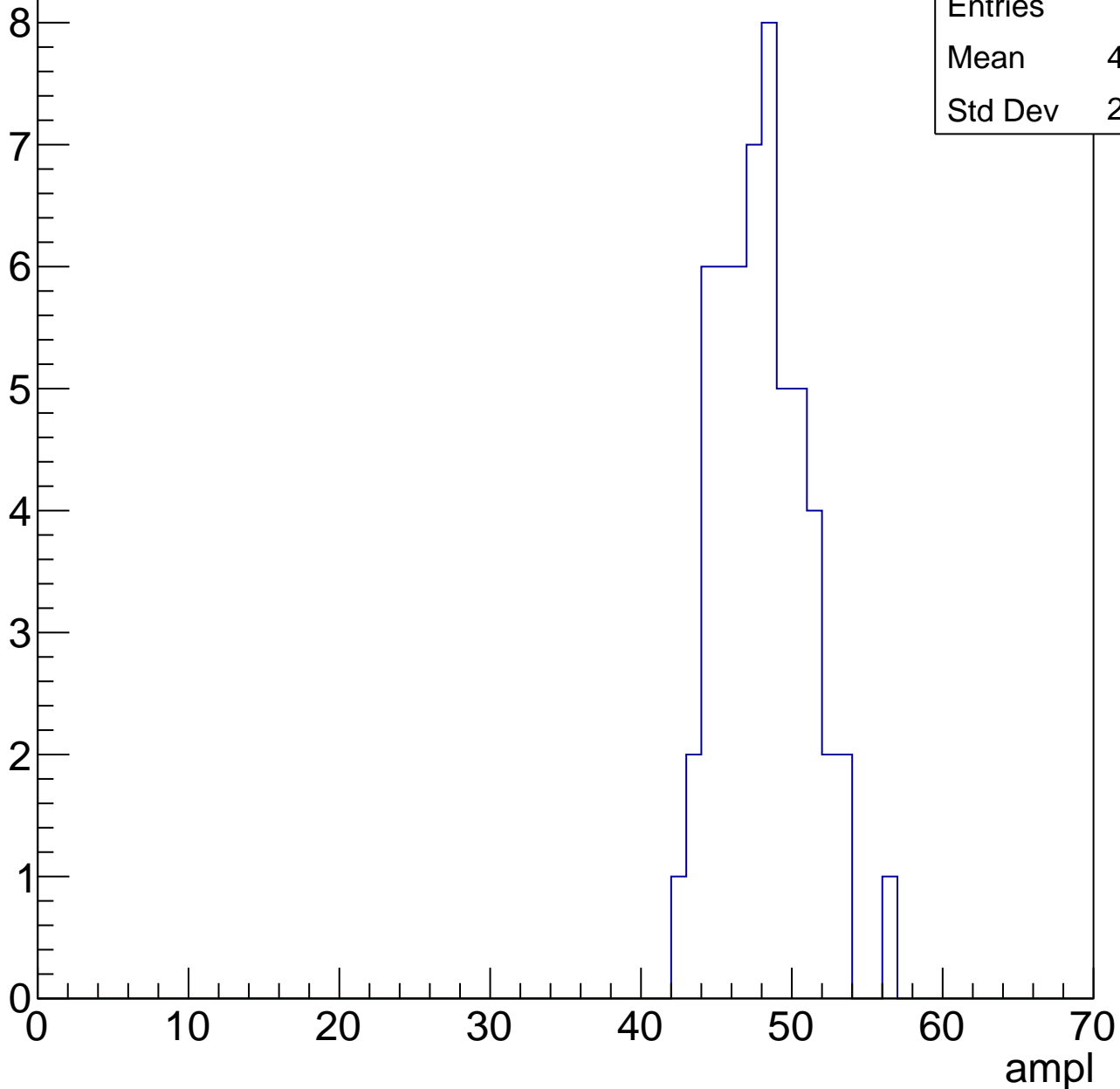


# B1L000S, U21-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

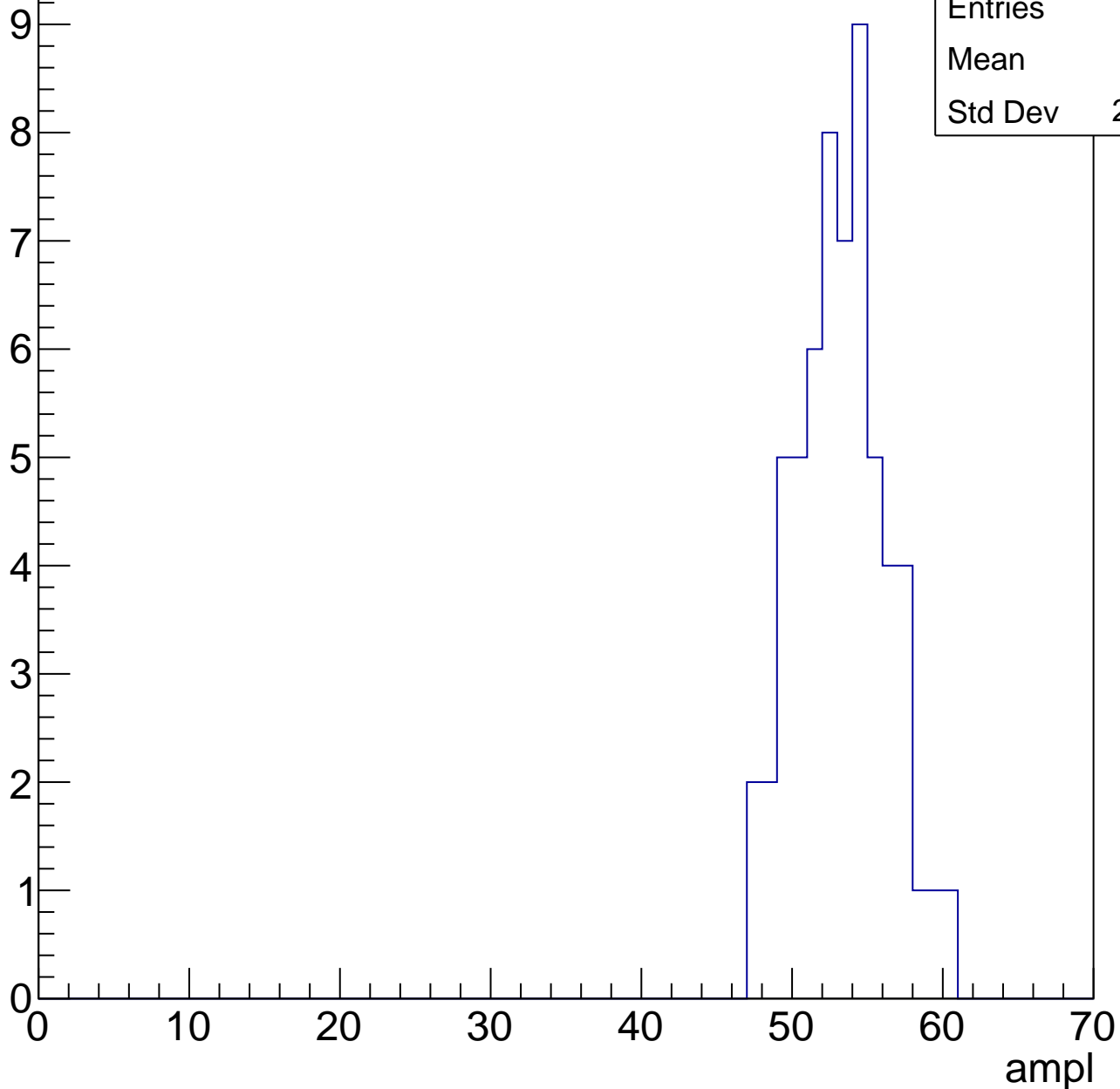
Entries	55
Mean	47.56
Std Dev	2.903



# B1L000S, U21-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

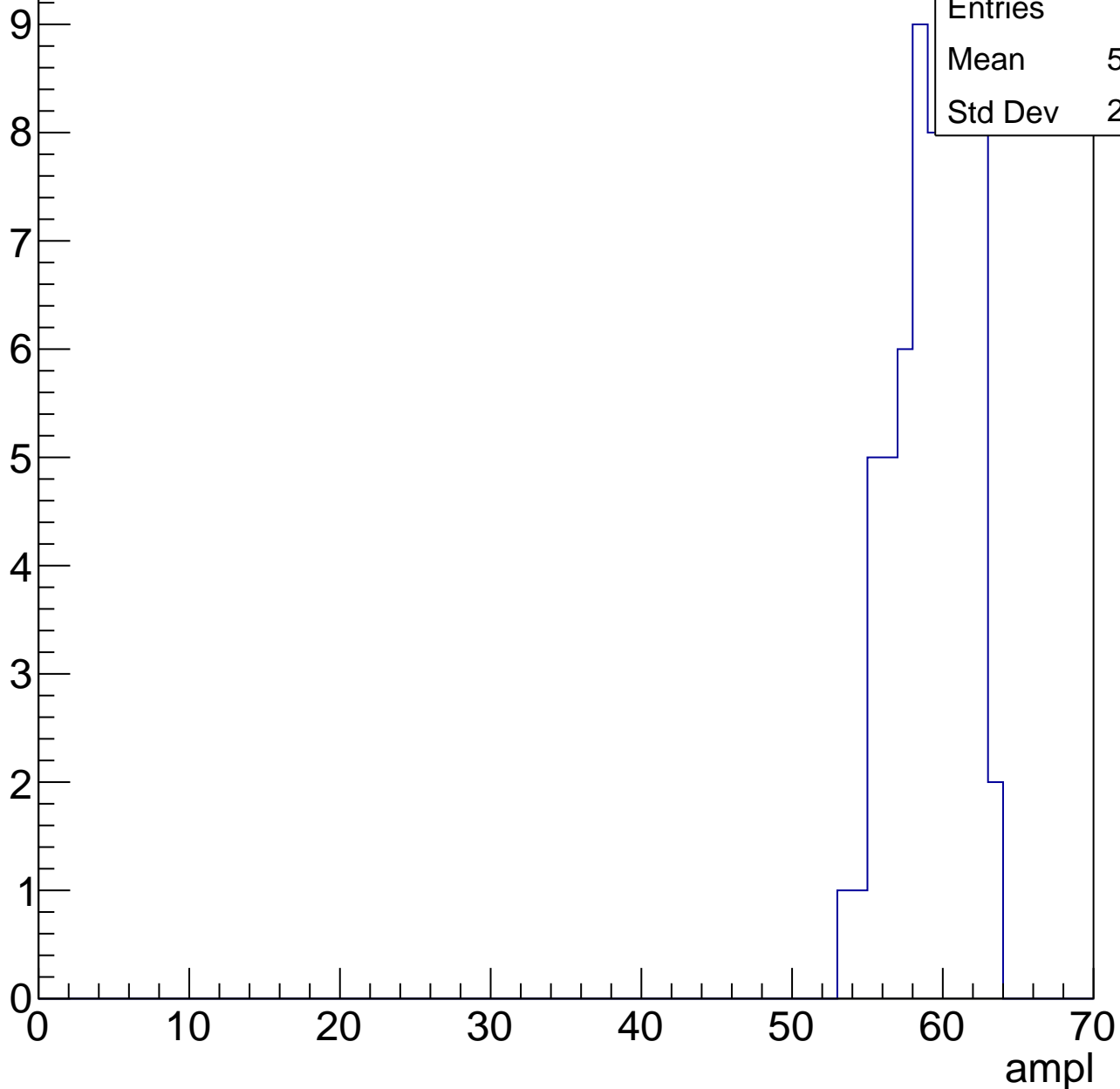


Entries	60
Mean	52.8
Std Dev	2.931

# B1L000S, U21-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

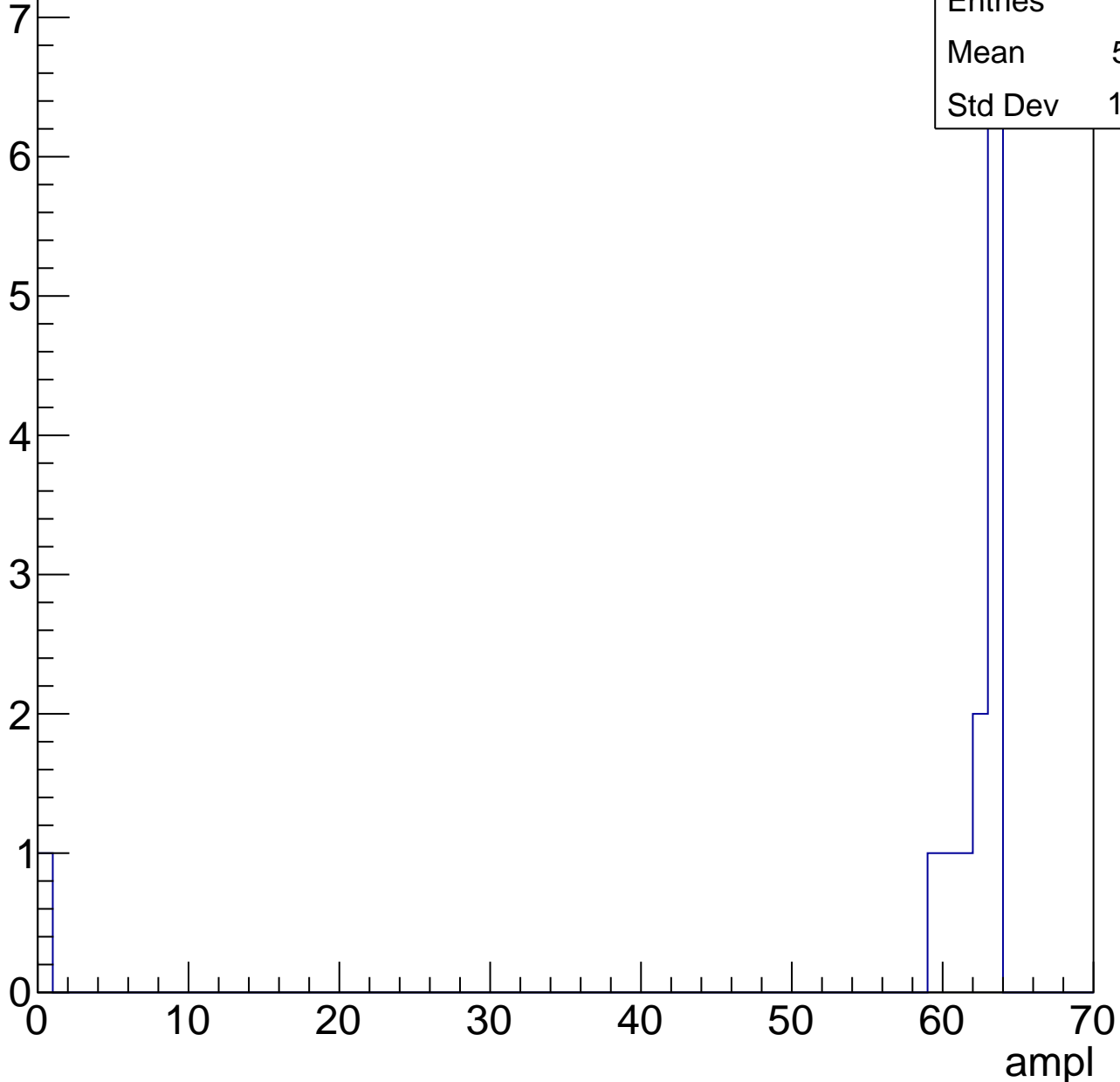


# B1L000S, U21-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	57.31
Std Dev	16.59





# B1L000S, U21-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U21-ch61, adc0

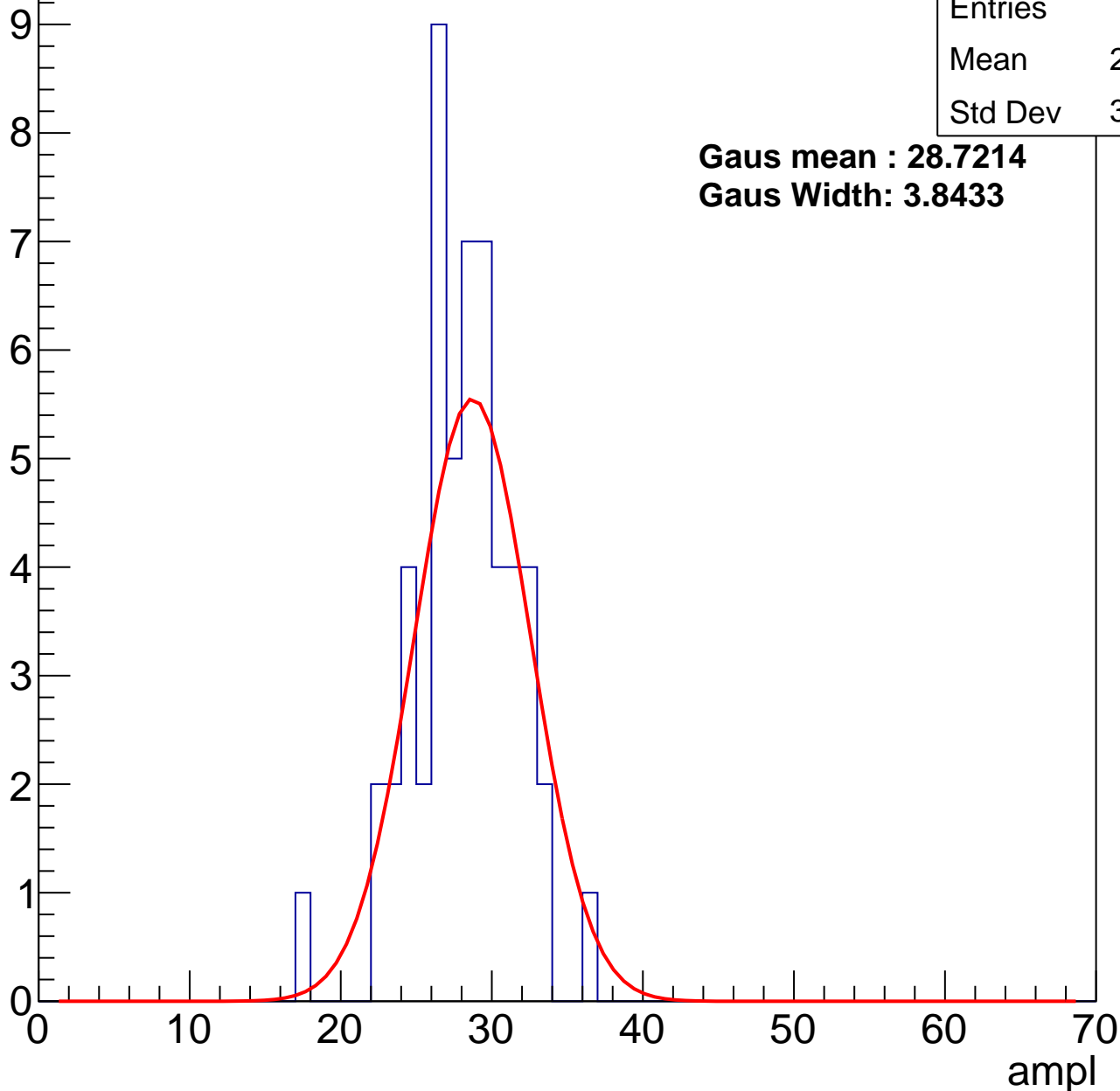
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	27.69
Std Dev	3.327

**Gaus mean : 28.7214**

**Gaus Width: 3.8433**



# B1L000S, U21-ch61, adc1

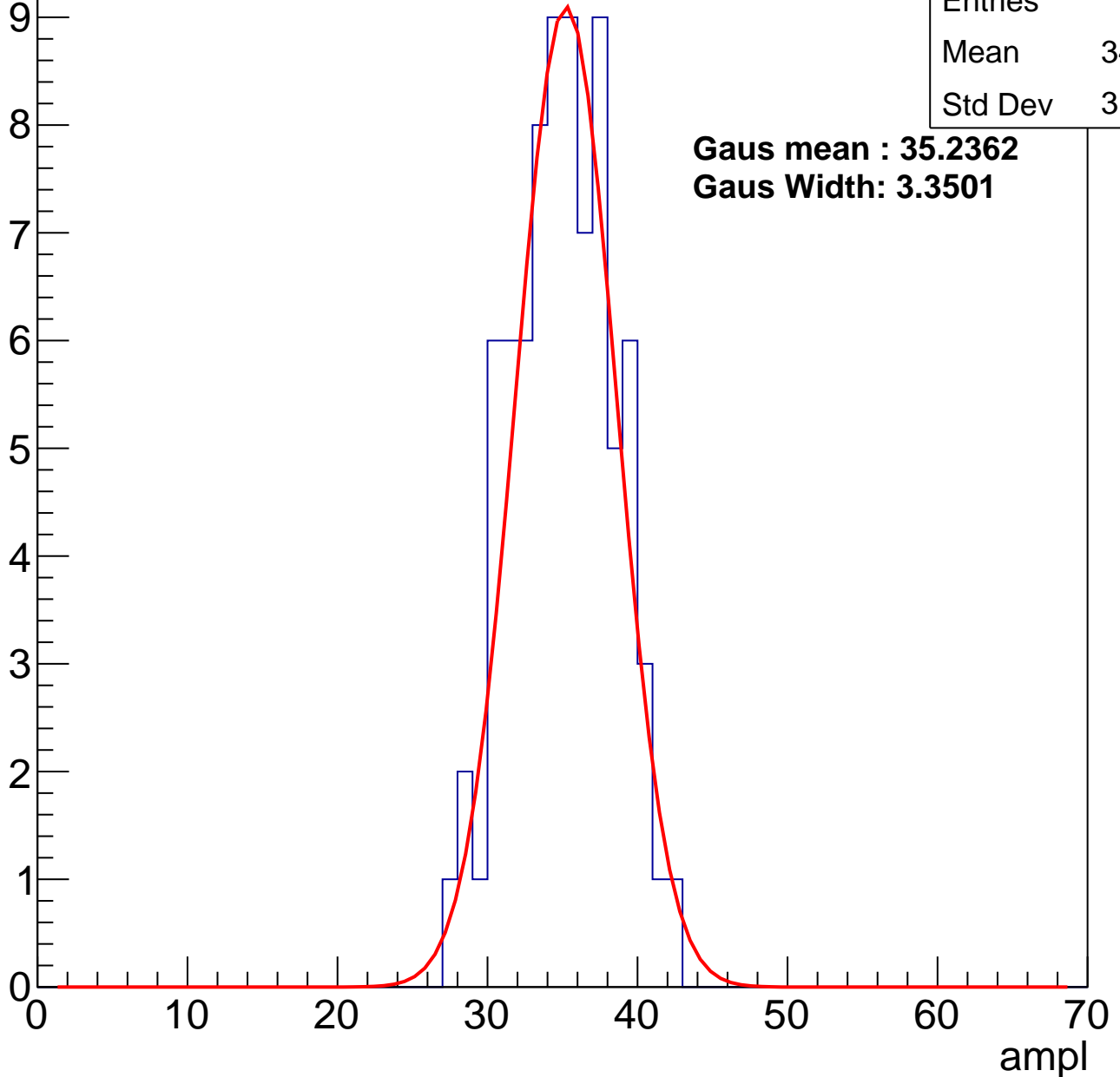
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	34.59
Std Dev	3.297

**Gaus mean : 35.2362**

**Gaus Width: 3.3501**



# B1L000S, U21-ch61, adc2

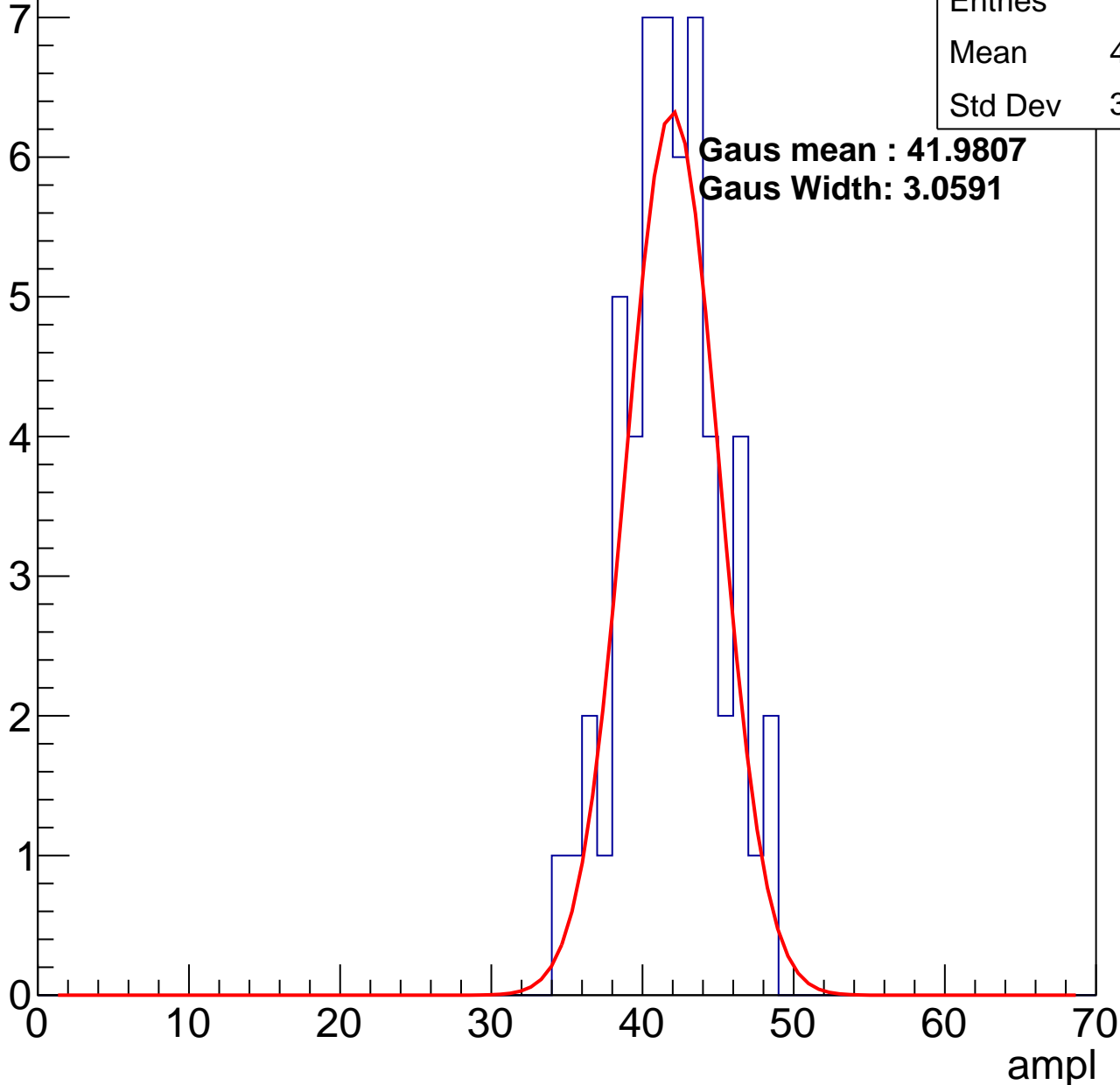
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.43
Std Dev	3.177

**Gaus mean : 41.9807**

**Gaus Width: 3.0591**

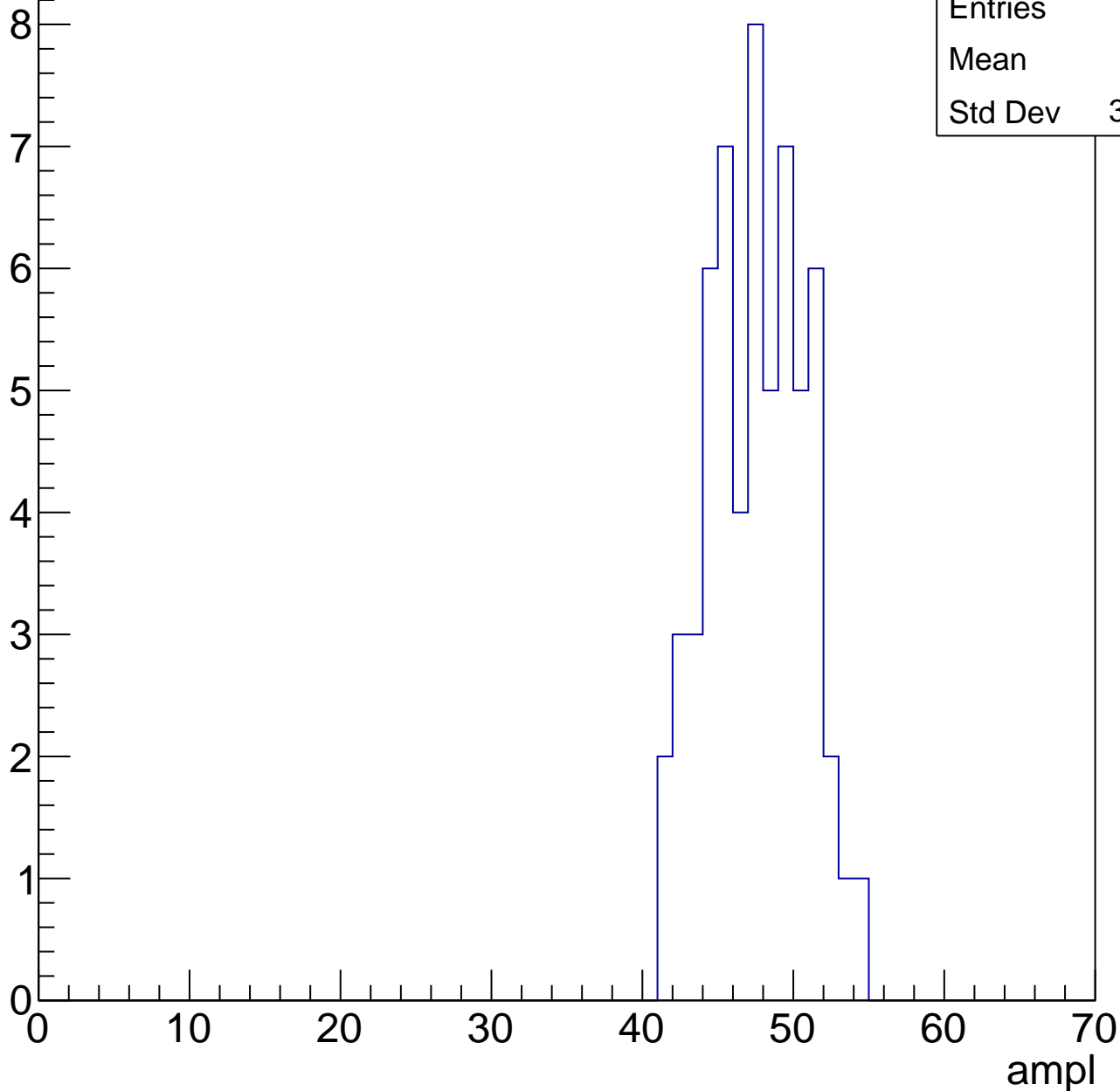


# B1L000S, U21-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	47.1
Std Dev	3.134

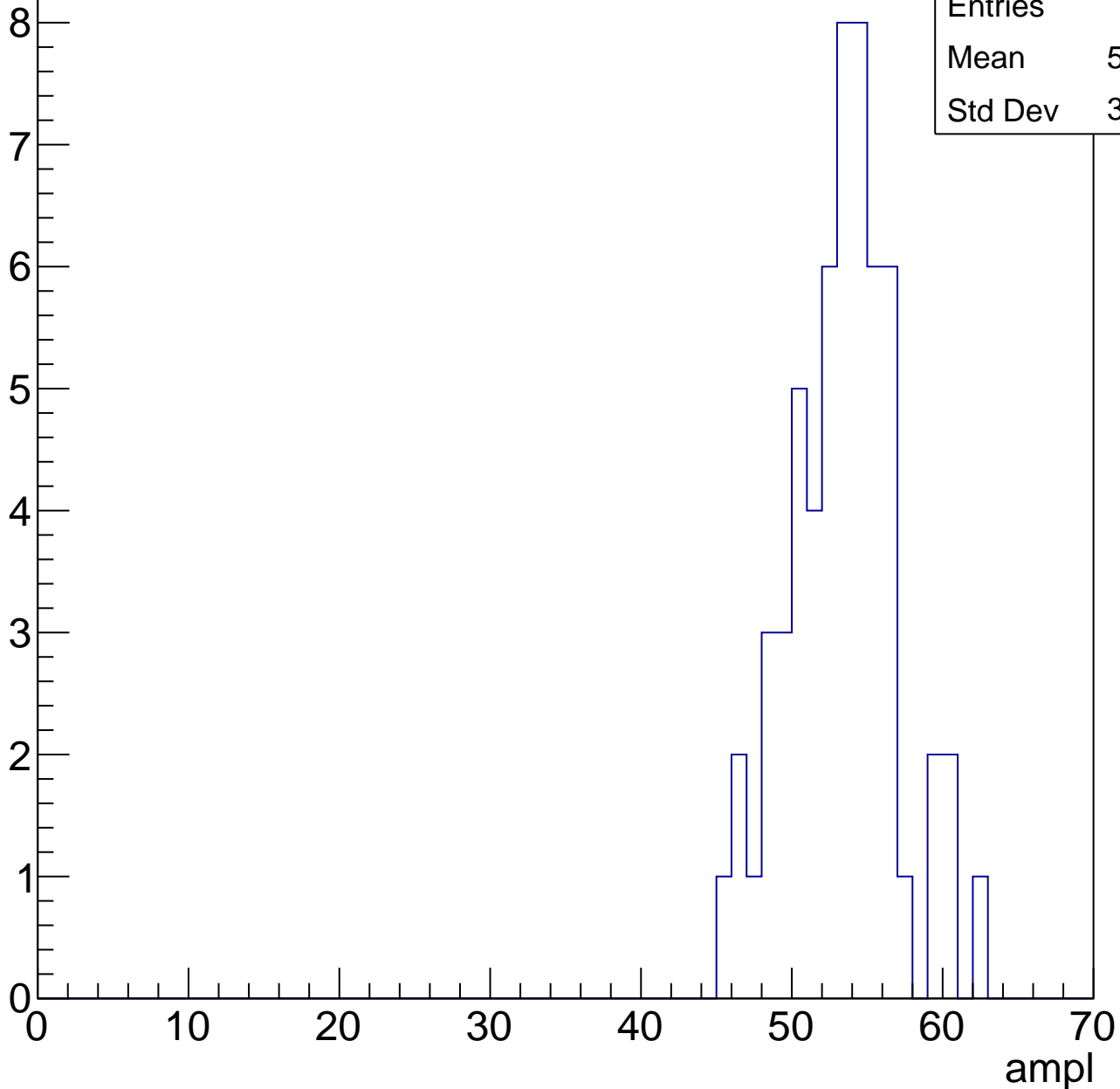


# B1L000S, U21-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

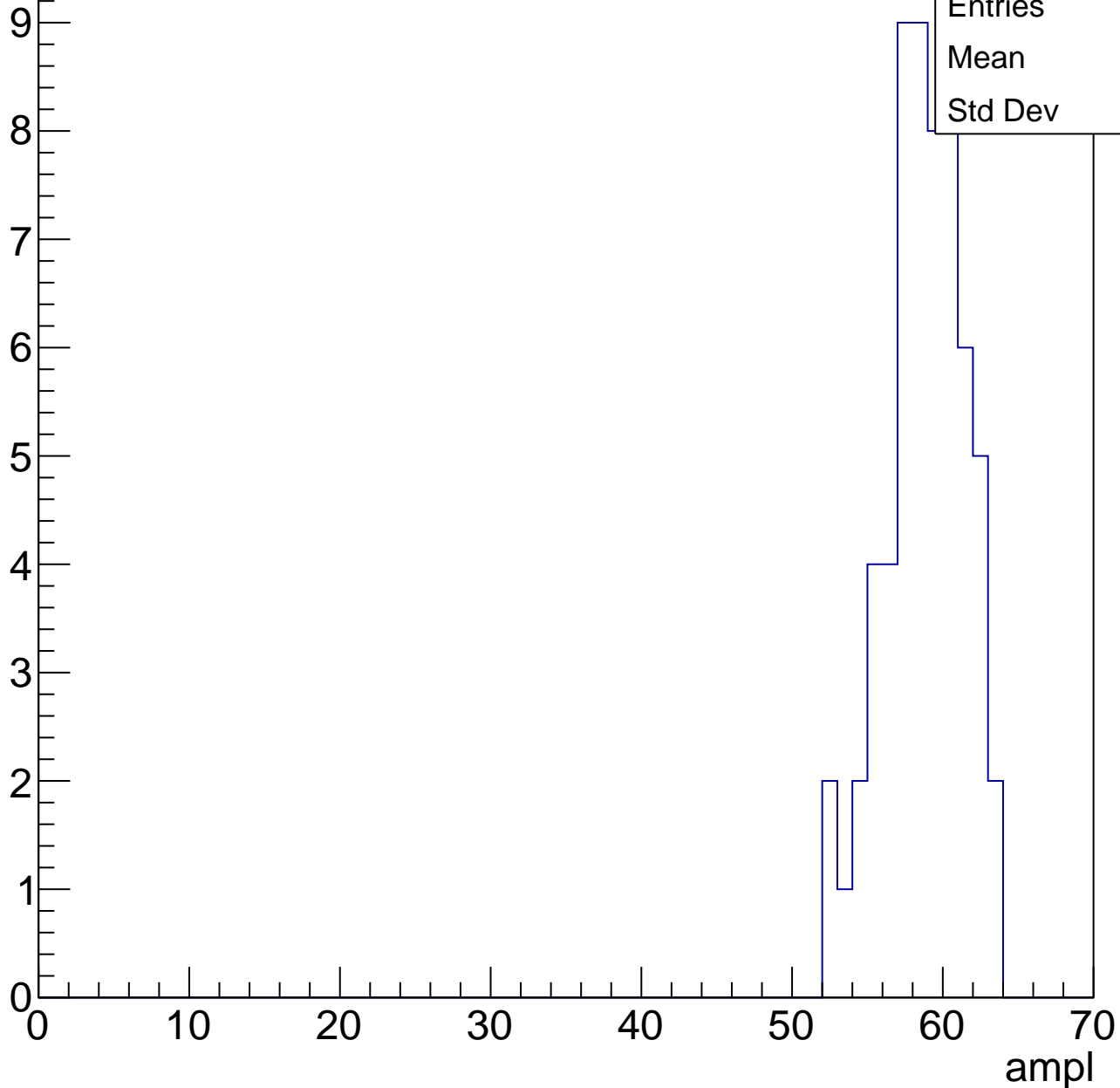
Entries	59
Mean	52.88
Std Dev	3.542



# B1L000S, U21-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



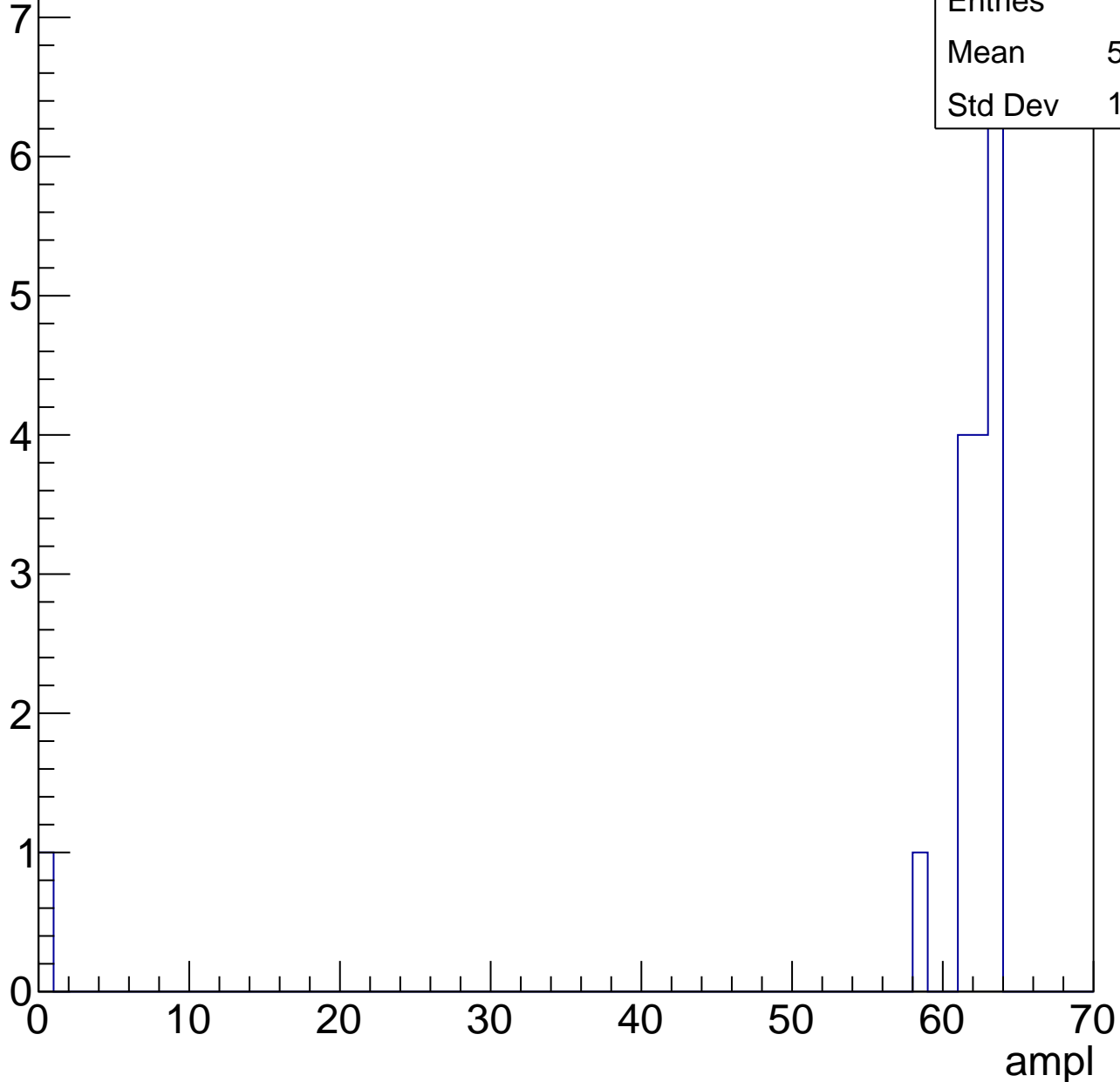
Entries	60
Mean	58.3
Std Dev	2.61

# B1L000S, U21-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.29
Std Dev	14.63





# B1L000S, U21-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch62, adc0

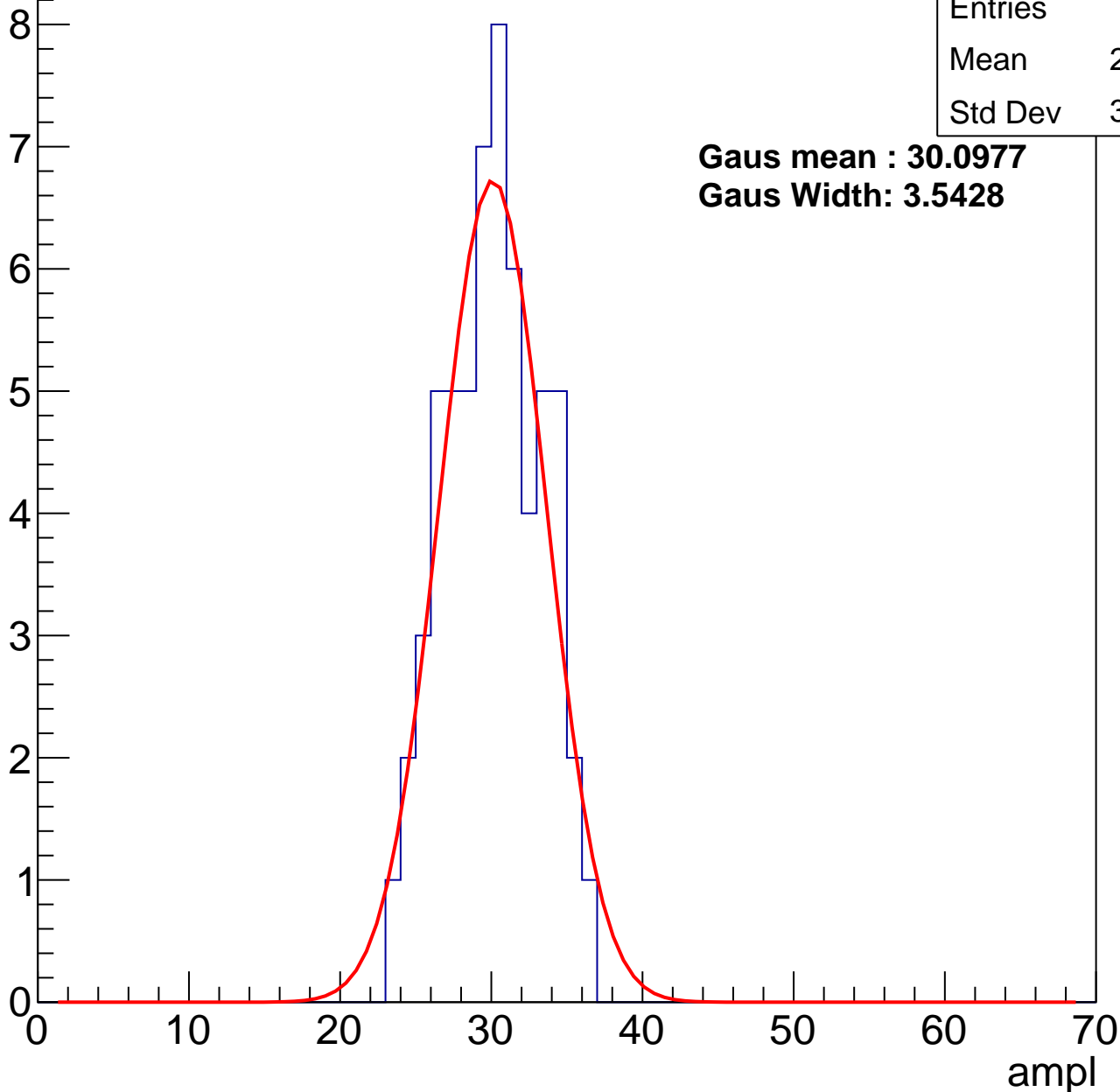
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	29.64
Std Dev	3.118

**Gaus mean : 30.0977**

**Gaus Width: 3.5428**



# B1L000S, U21-ch62, adc1

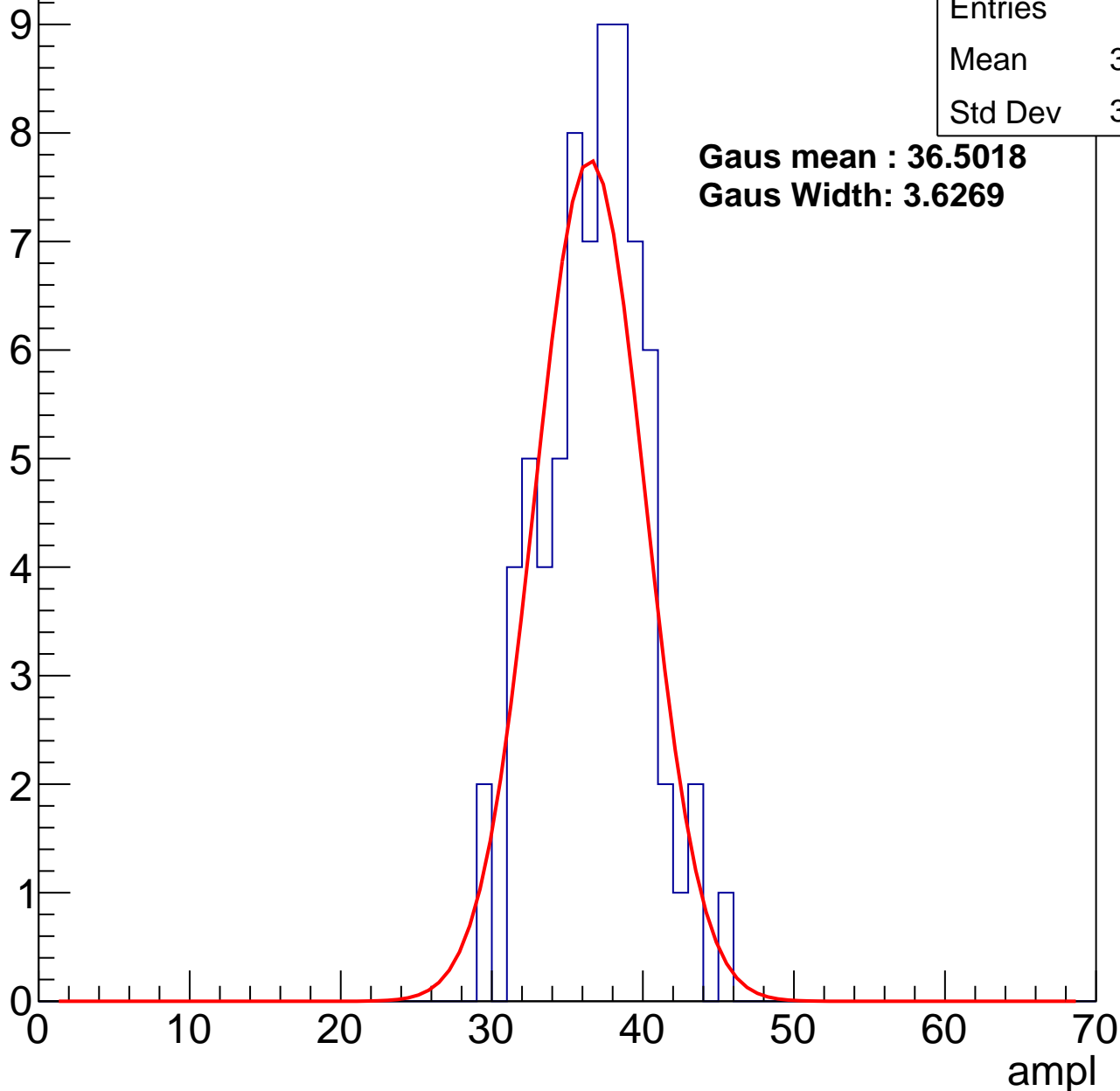
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	36.38
Std Dev	3.335

**Gaus mean : 36.5018**

**Gaus Width: 3.6269**



# B1L000S, U21-ch62, adc2

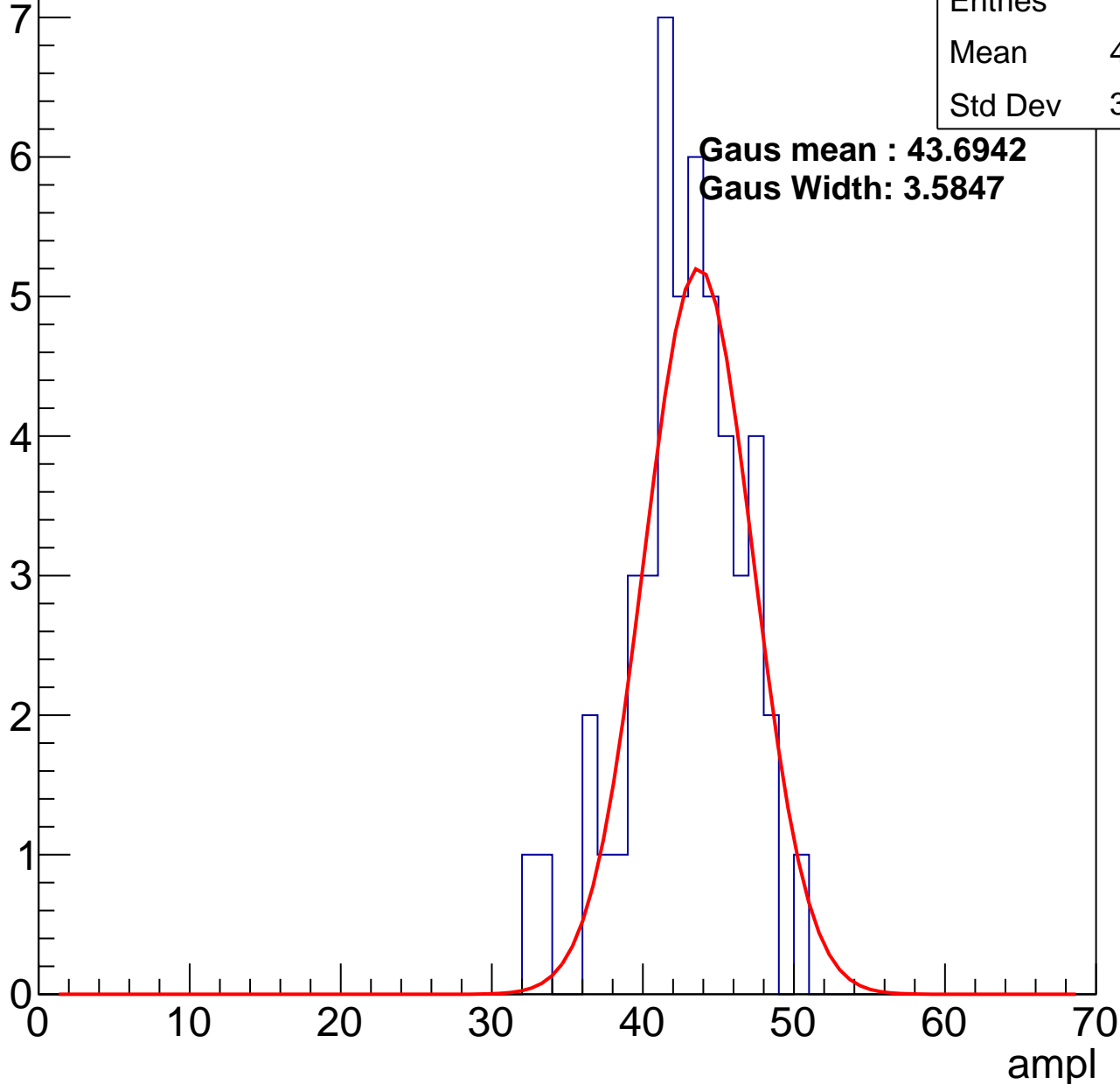
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	42.37
Std Dev	3.724

**Gaus mean : 43.6942**

**Gaus Width: 3.5847**

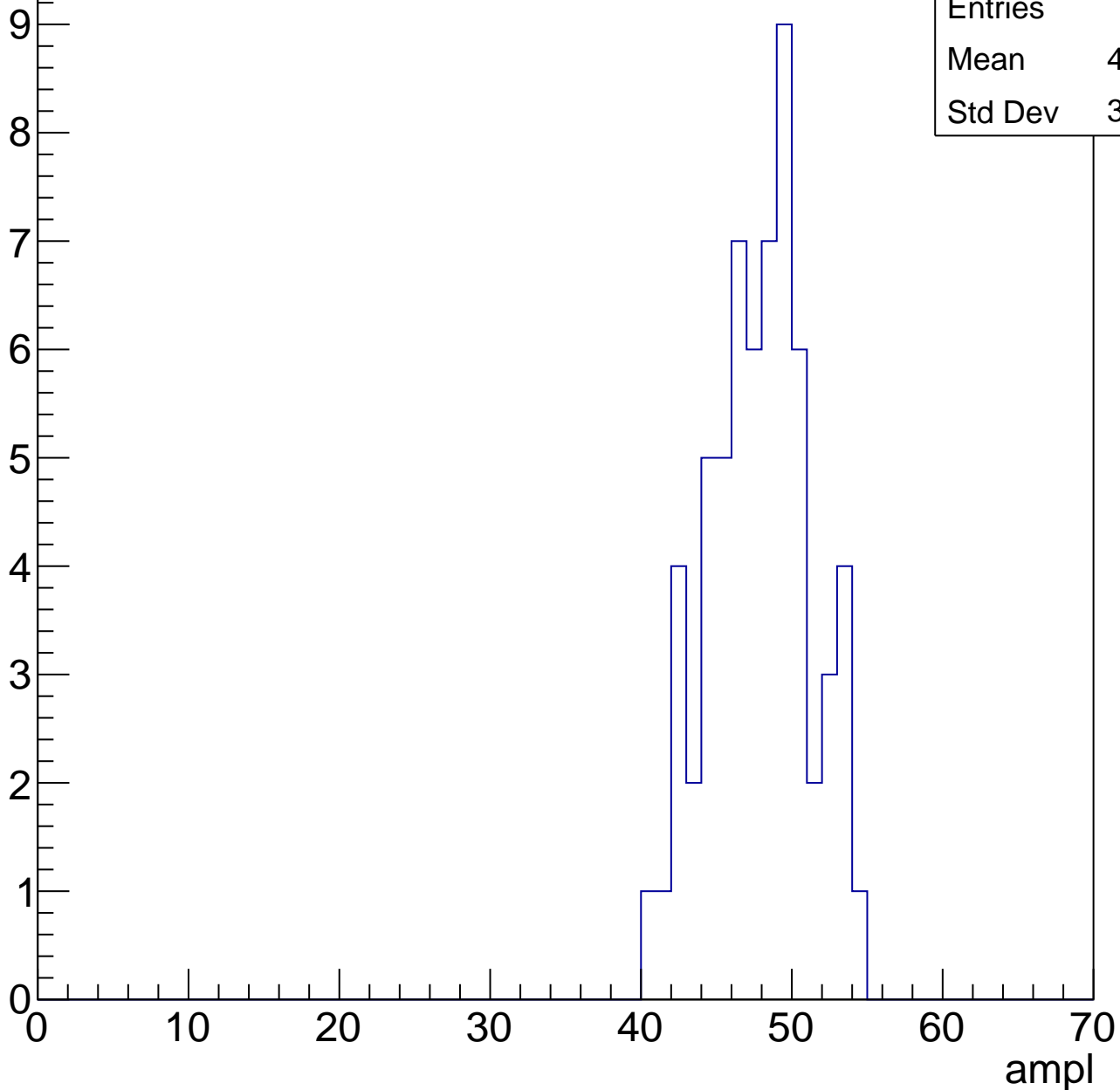


# B1L000S, U21-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

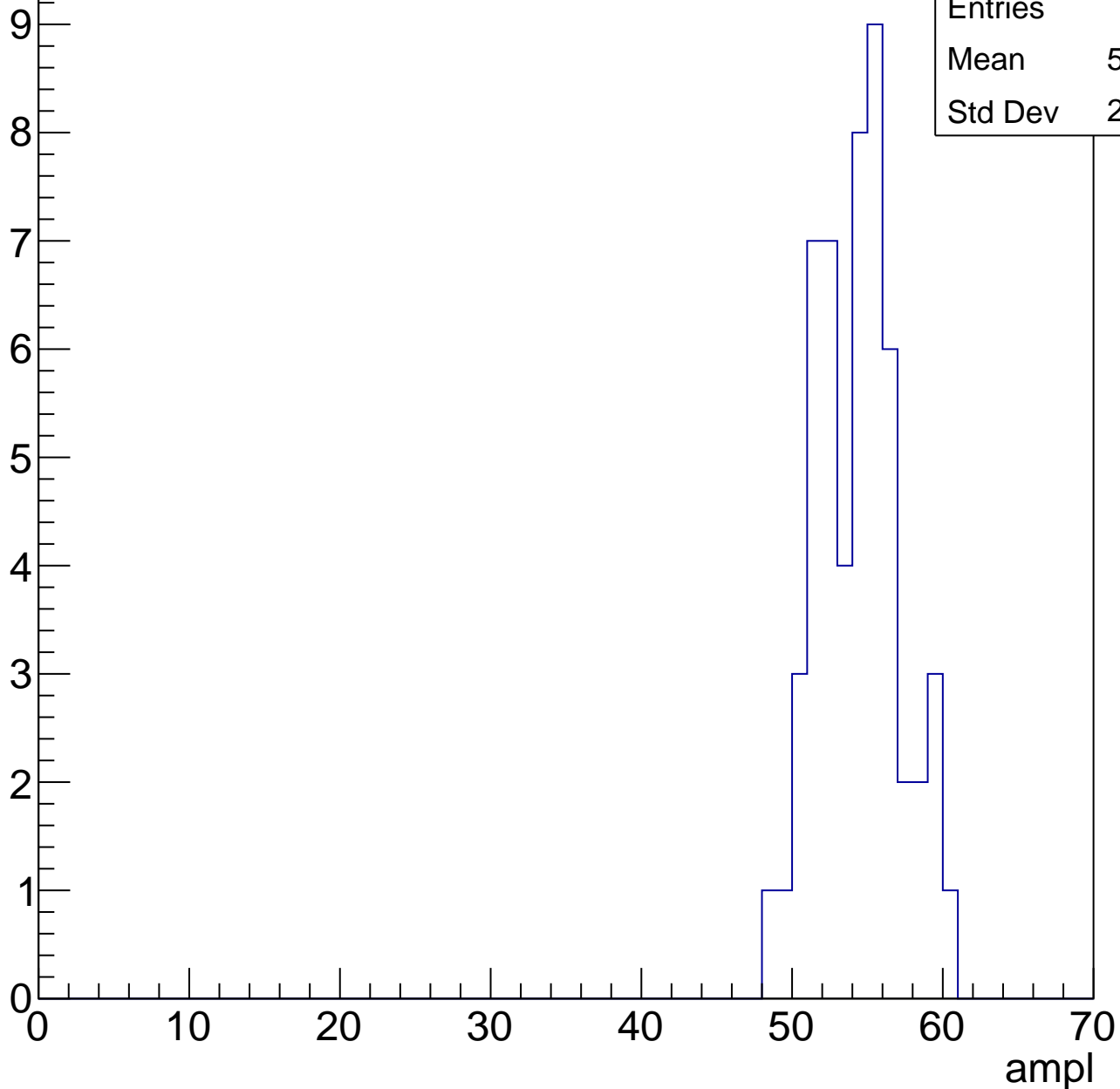
Entries	63
Mean	47.38
Std Dev	3.278



# B1L000S, U21-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



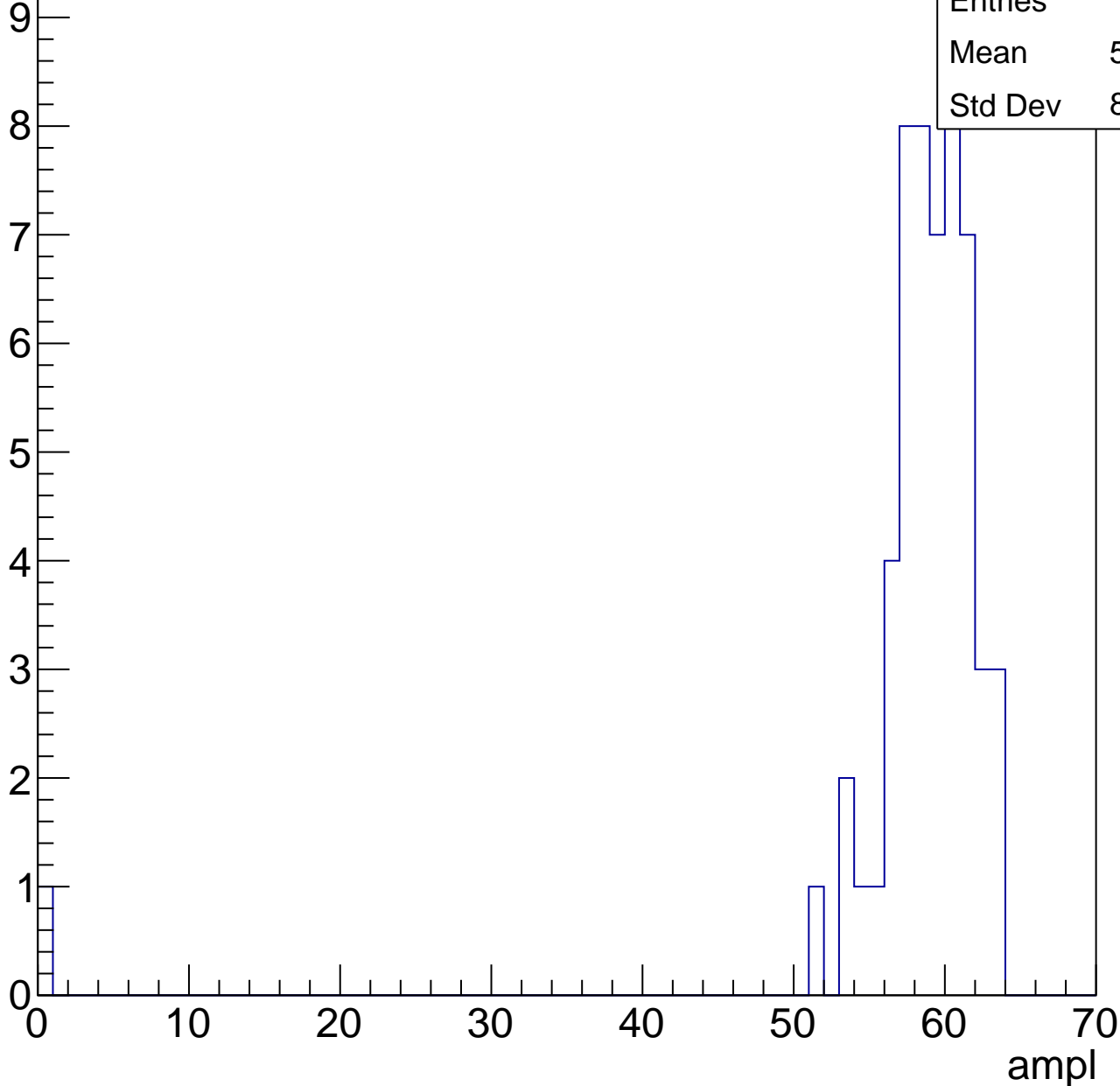
Entries	54
Mean	53.89
Std Dev	2.713

# B1L000S, U21-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	57.55
Std Dev	8.235

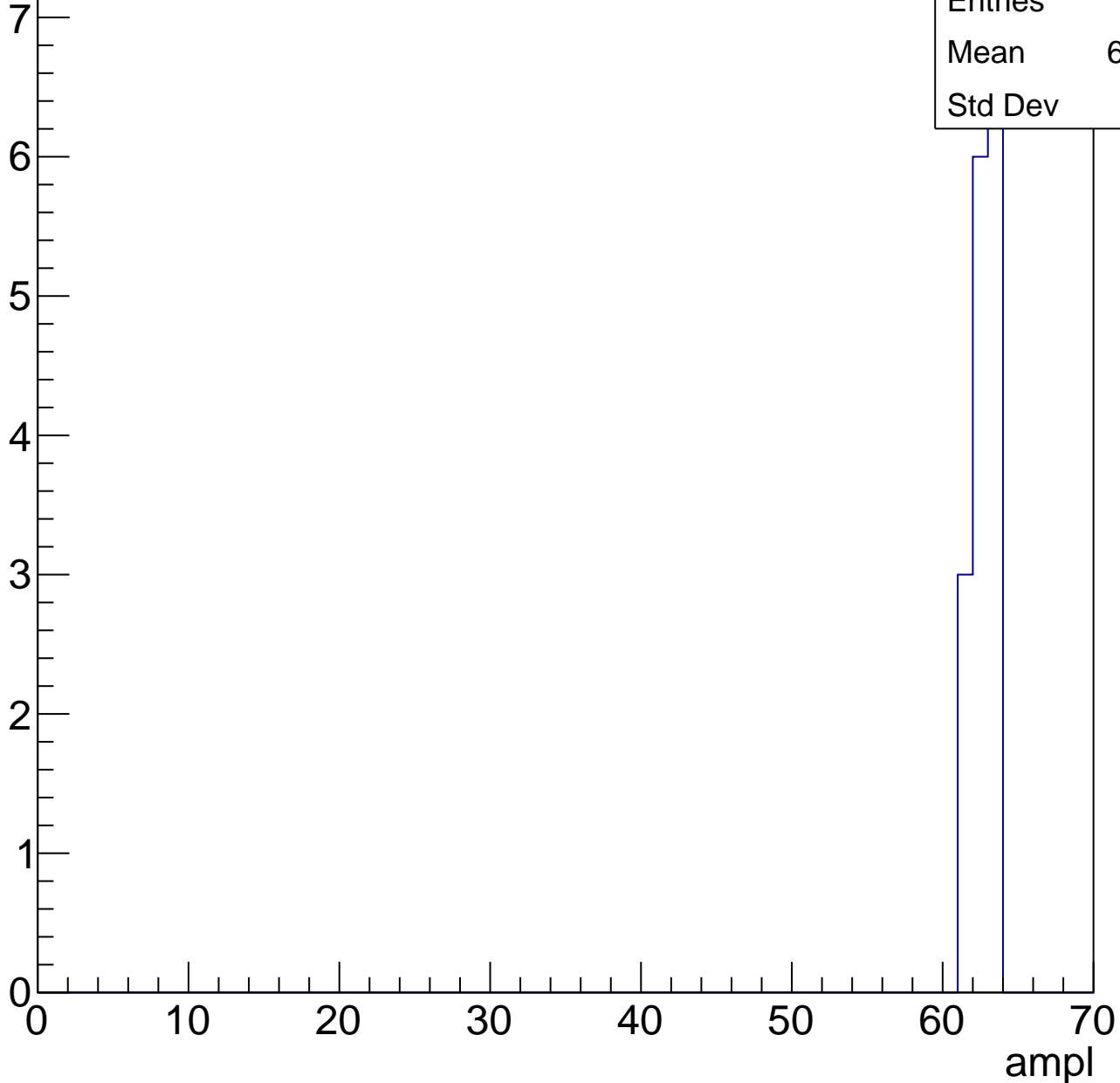


# B1L000S, U21-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	62.25
Std Dev	0.75

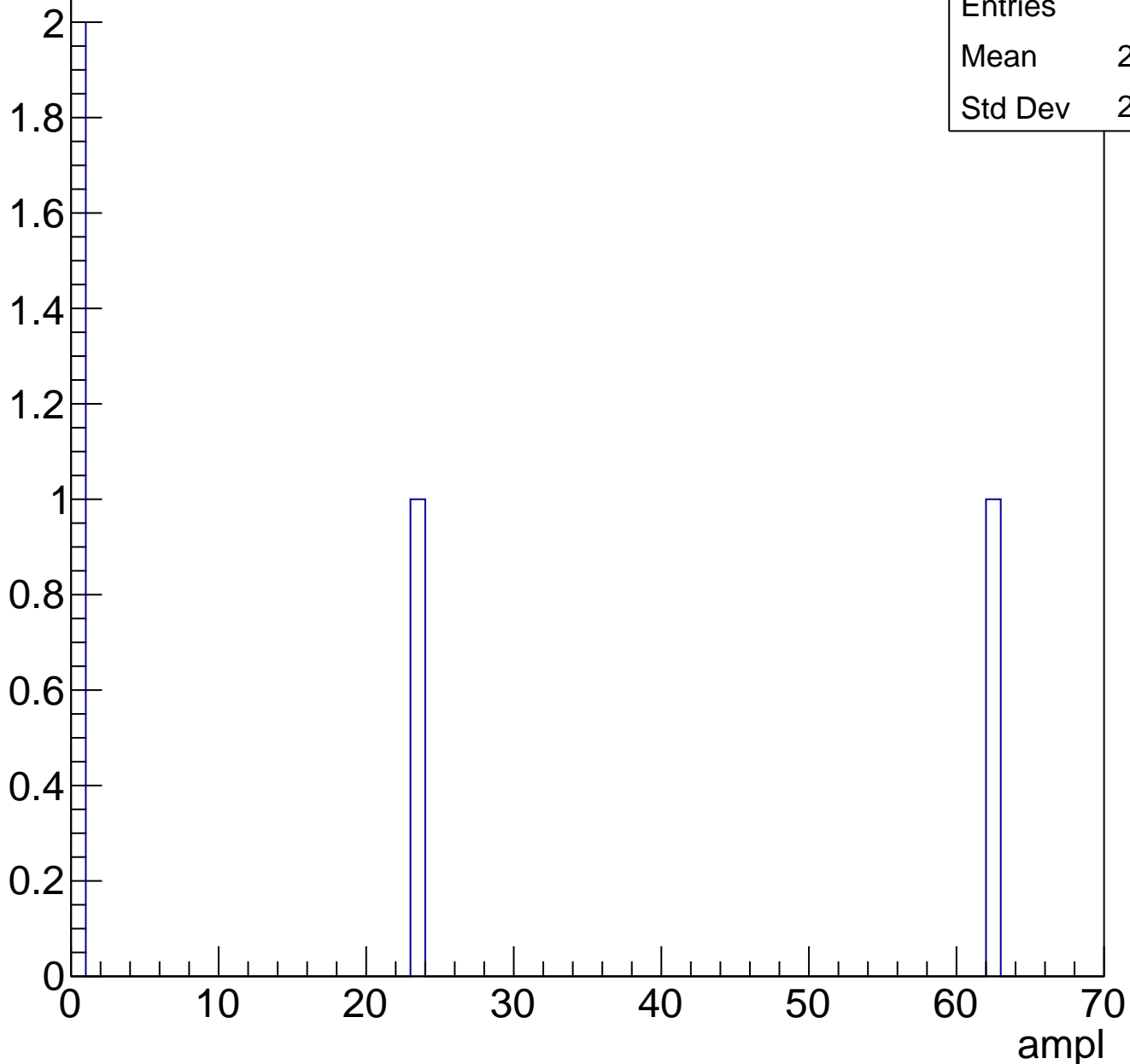




# B1L000S, U21-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	21.25
Std Dev	25.33

# B1L000S, U21-ch63, adc0

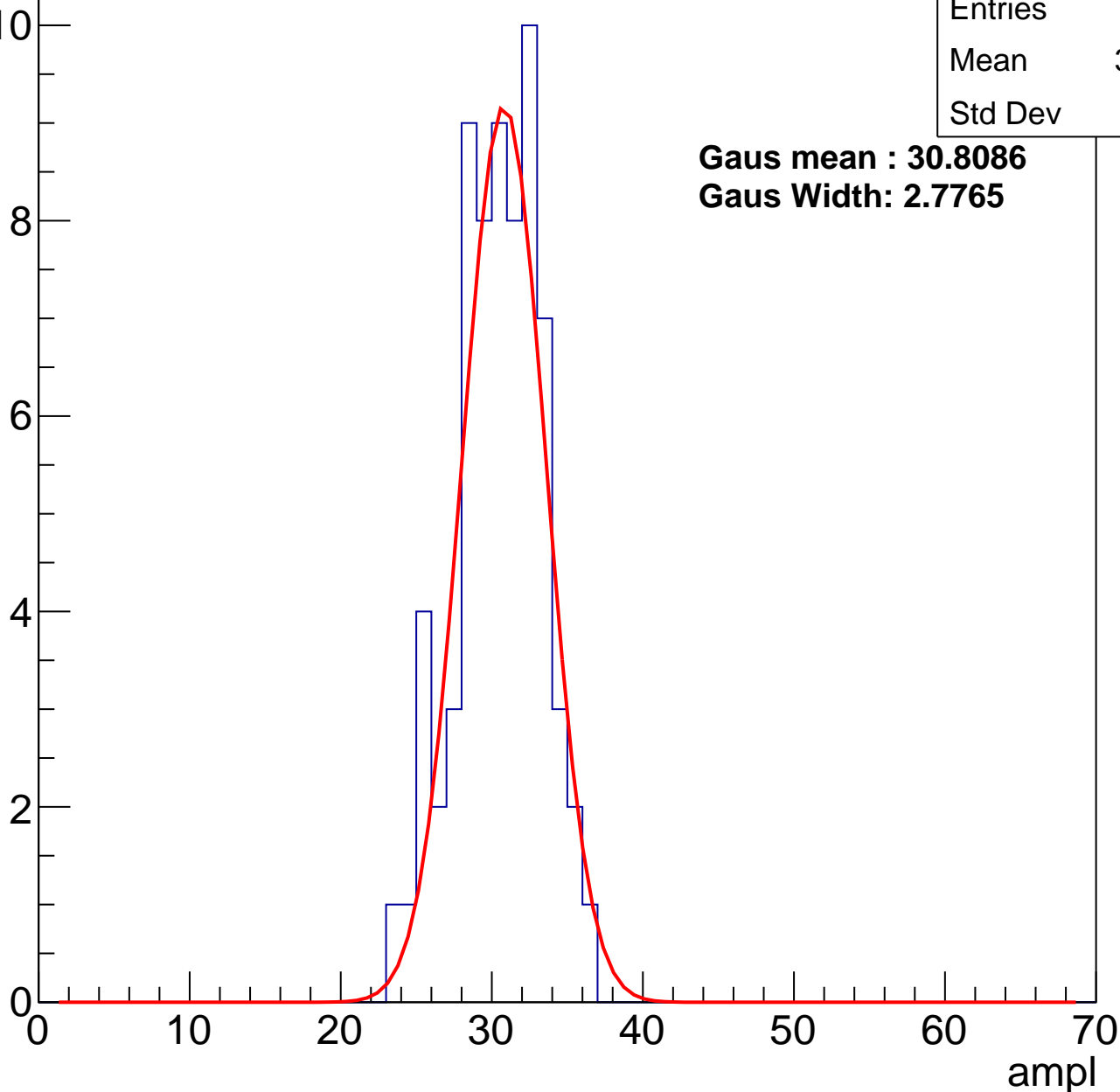
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	30.01
Std Dev	2.8

**Gaus mean : 30.8086**

**Gaus Width: 2.7765**



# B1L000S, U21-ch63, adc1

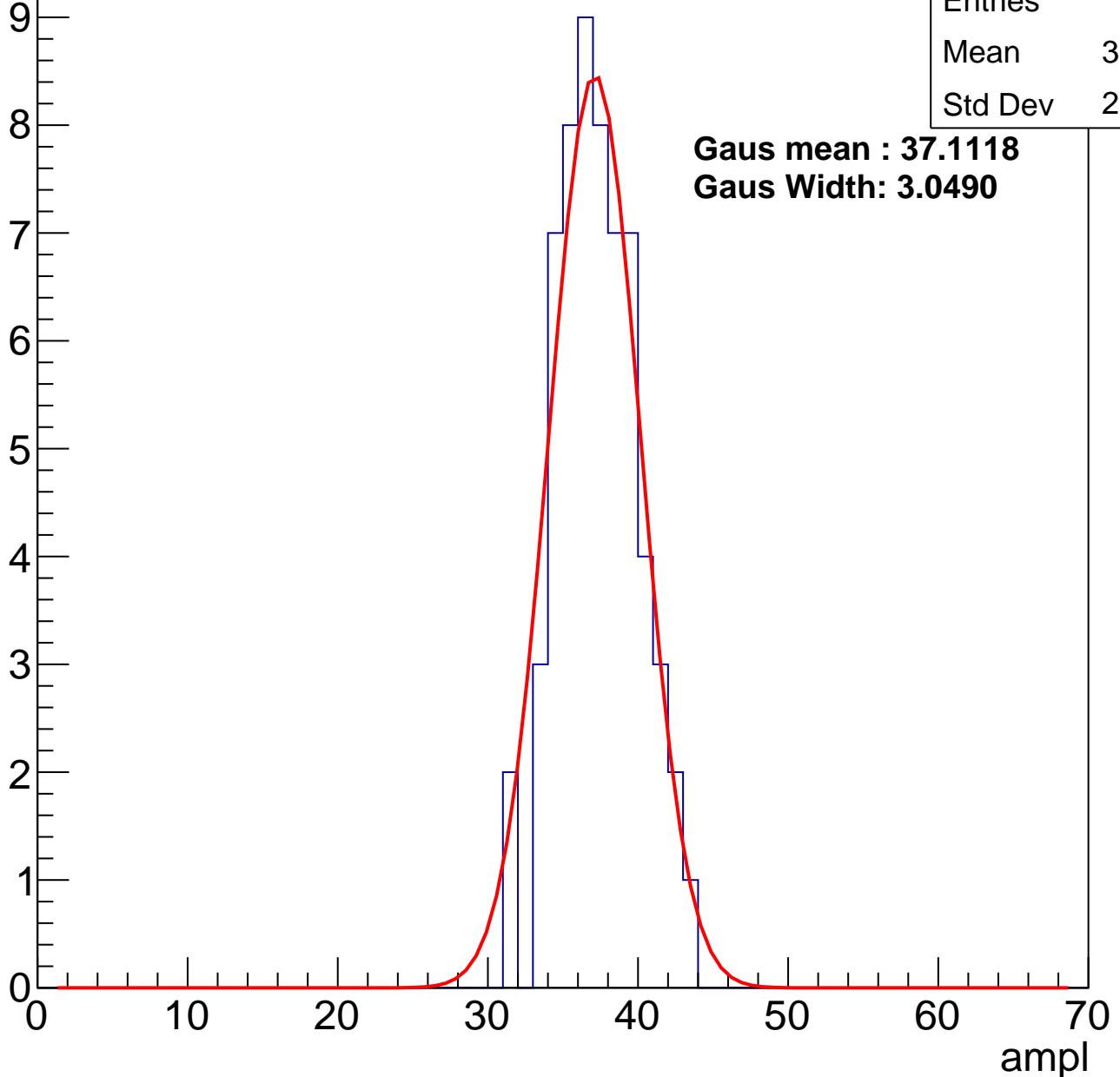
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	36.85
Std Dev	2.648

**Gaus mean : 37.1118**

**Gaus Width: 3.0490**



# B1L000S, U21-ch63, adc2

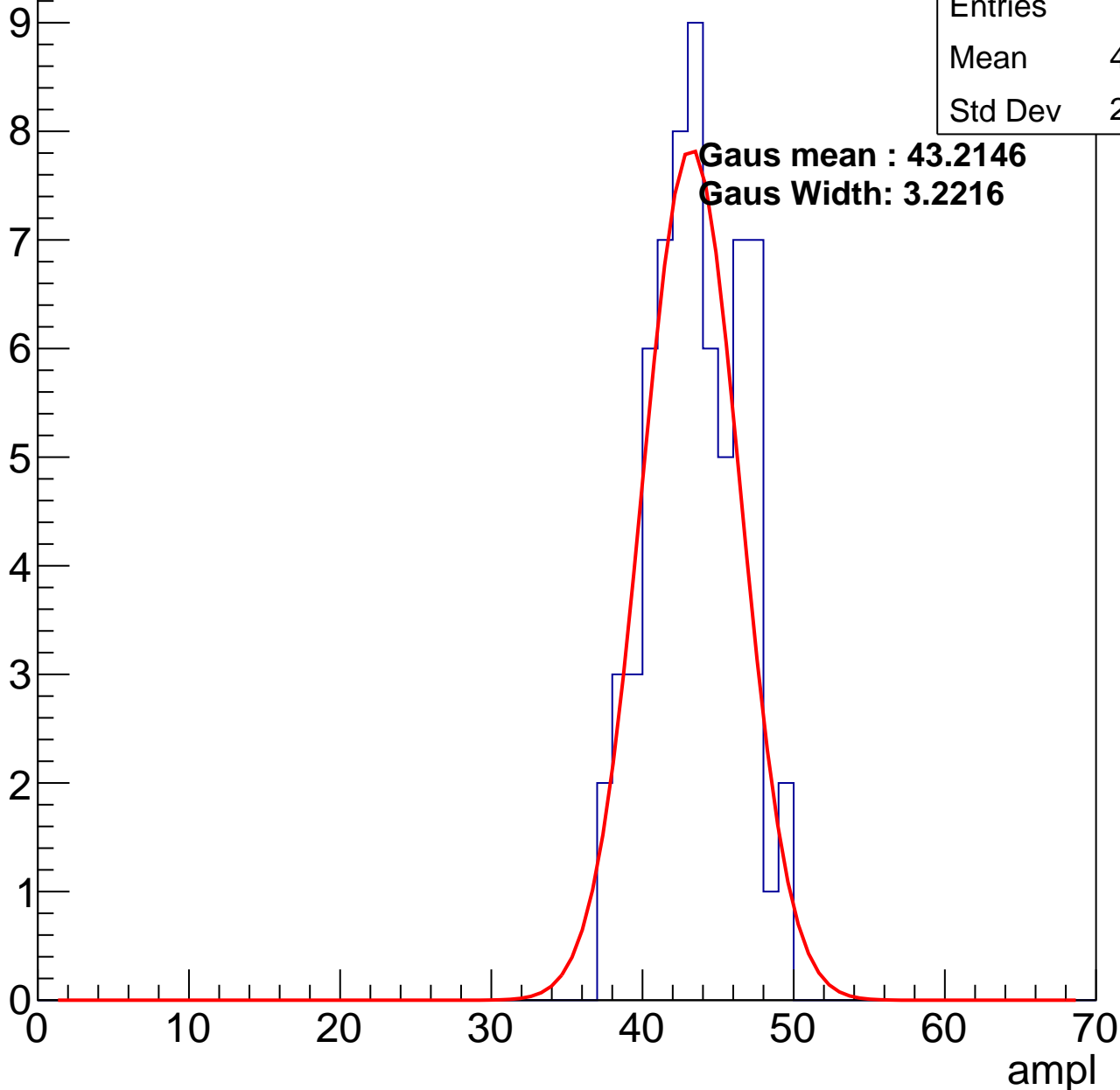
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	43.05
Std Dev	2.972

**Gaus mean : 43.2146**

**Gaus Width: 3.2216**

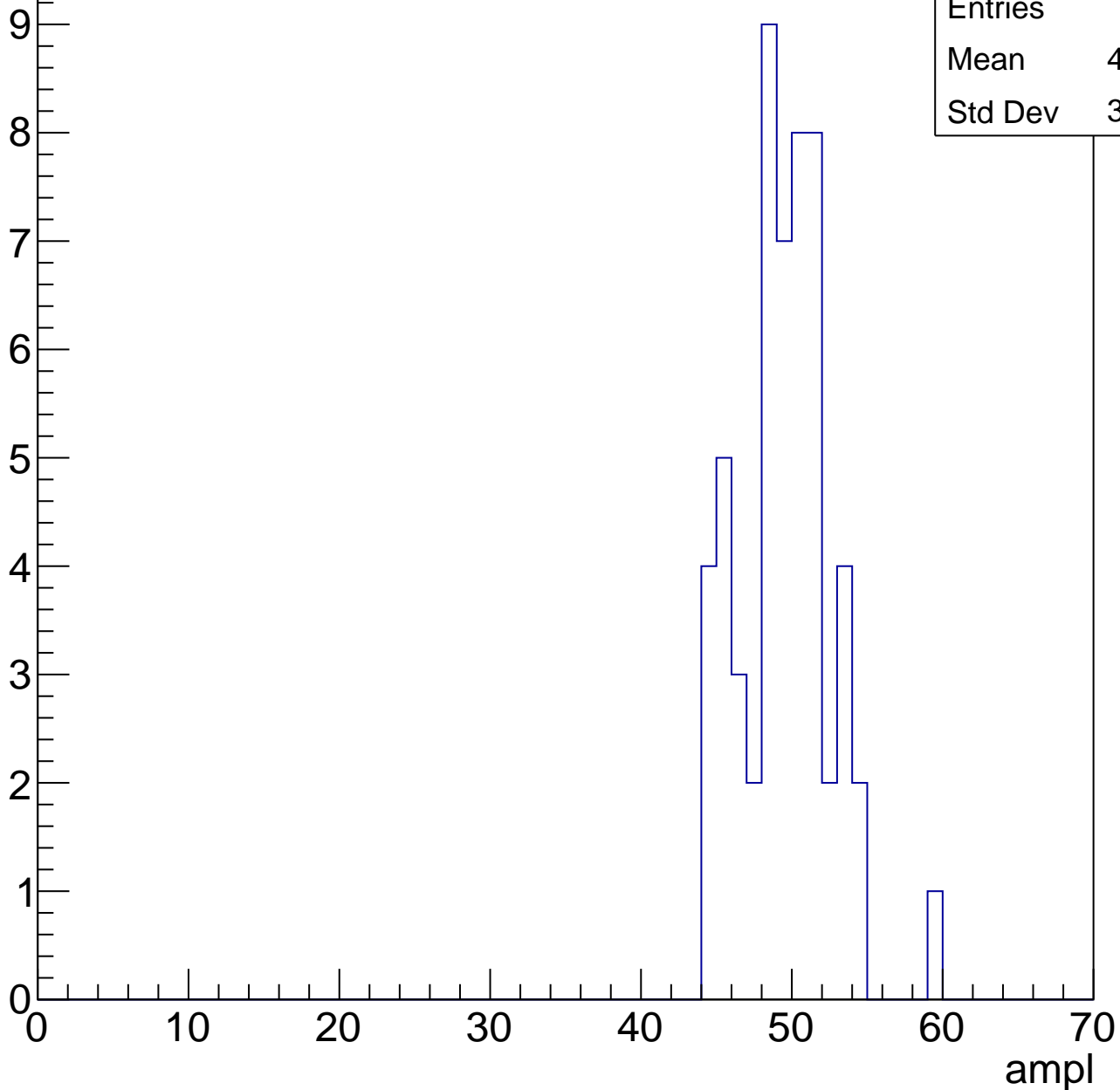


# B1L000S, U21-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	49.07
Std Dev	3.002

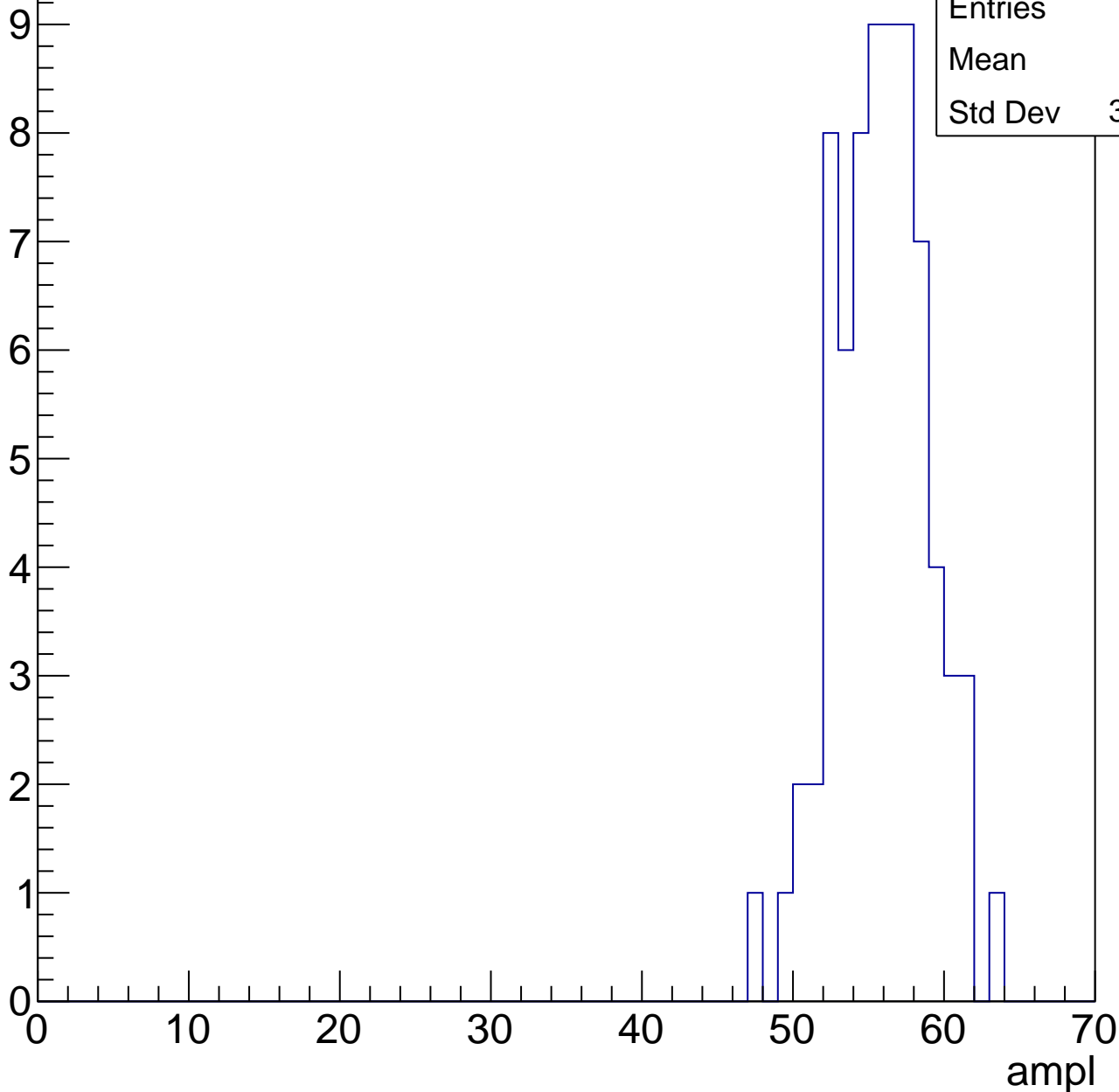


# B1L000S, U21-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	55.4
Std Dev	3.082

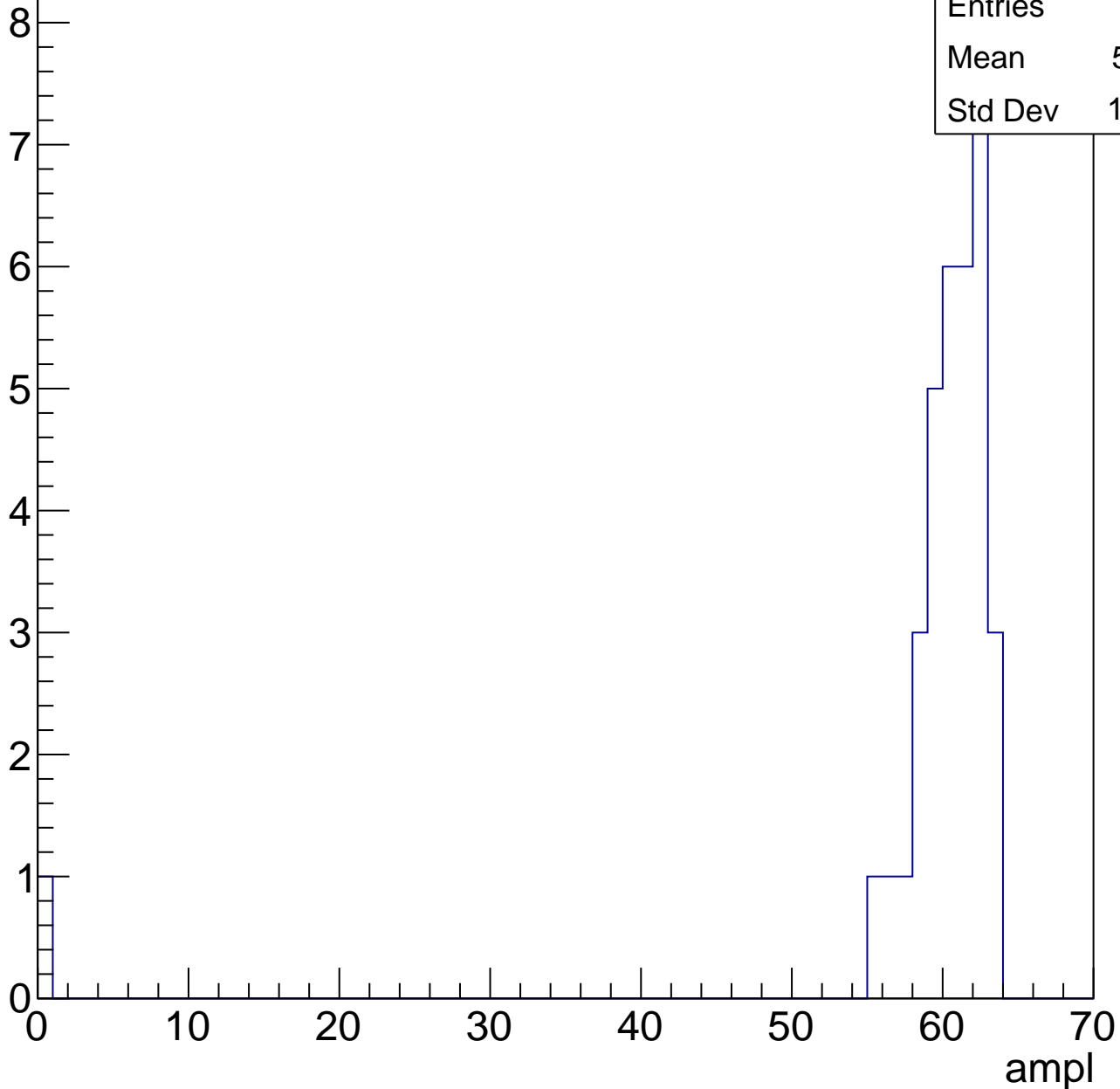


# B1L000S, U21-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

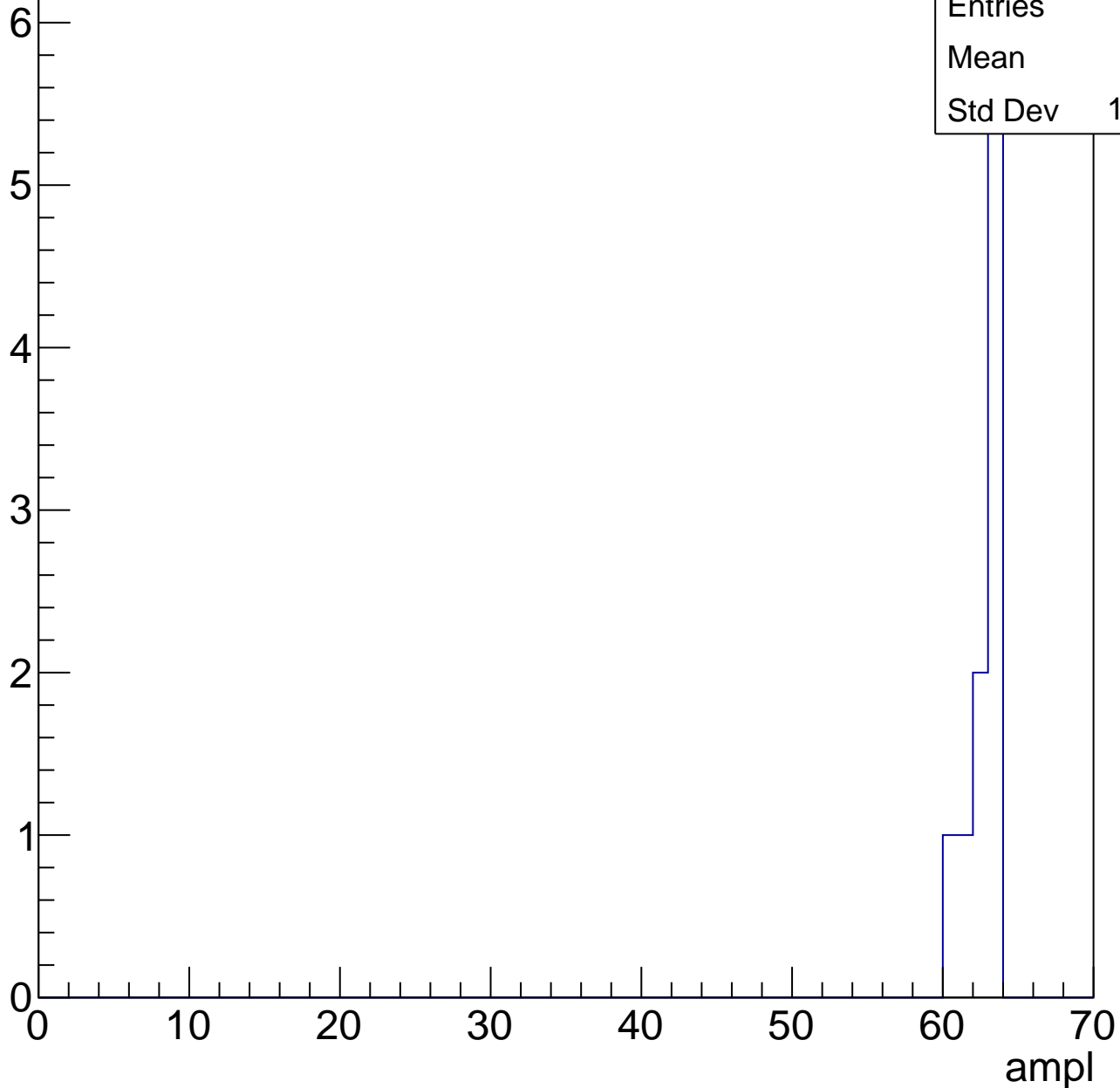
Entries	35
Mean	58.51
Std Dev	10.22



# B1L000S, U21-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch64, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	28.85
Std Dev	3.205

**Gaus mean : 29.0427**

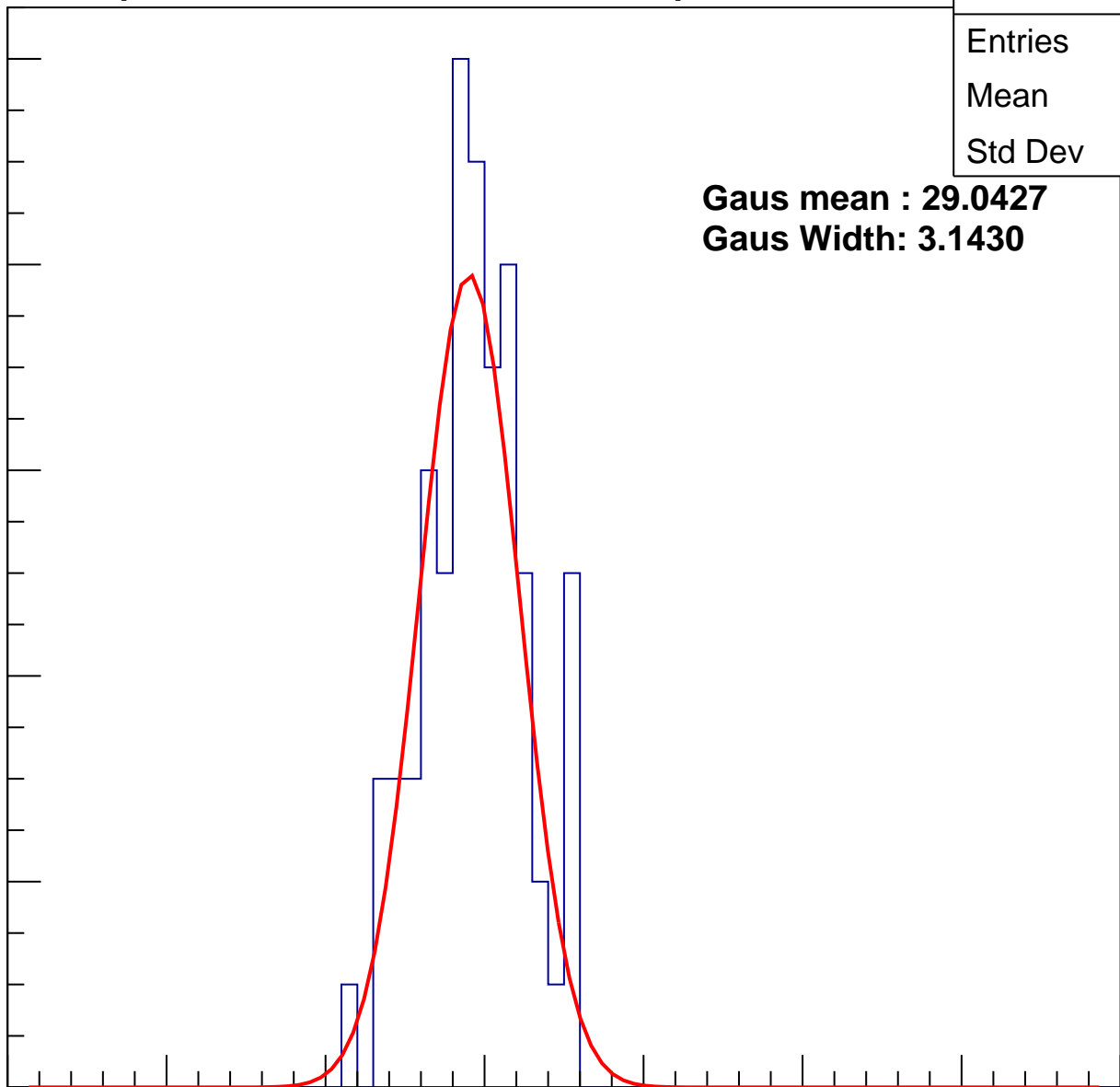
**Gaus Width: 3.1430**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch64, adc1

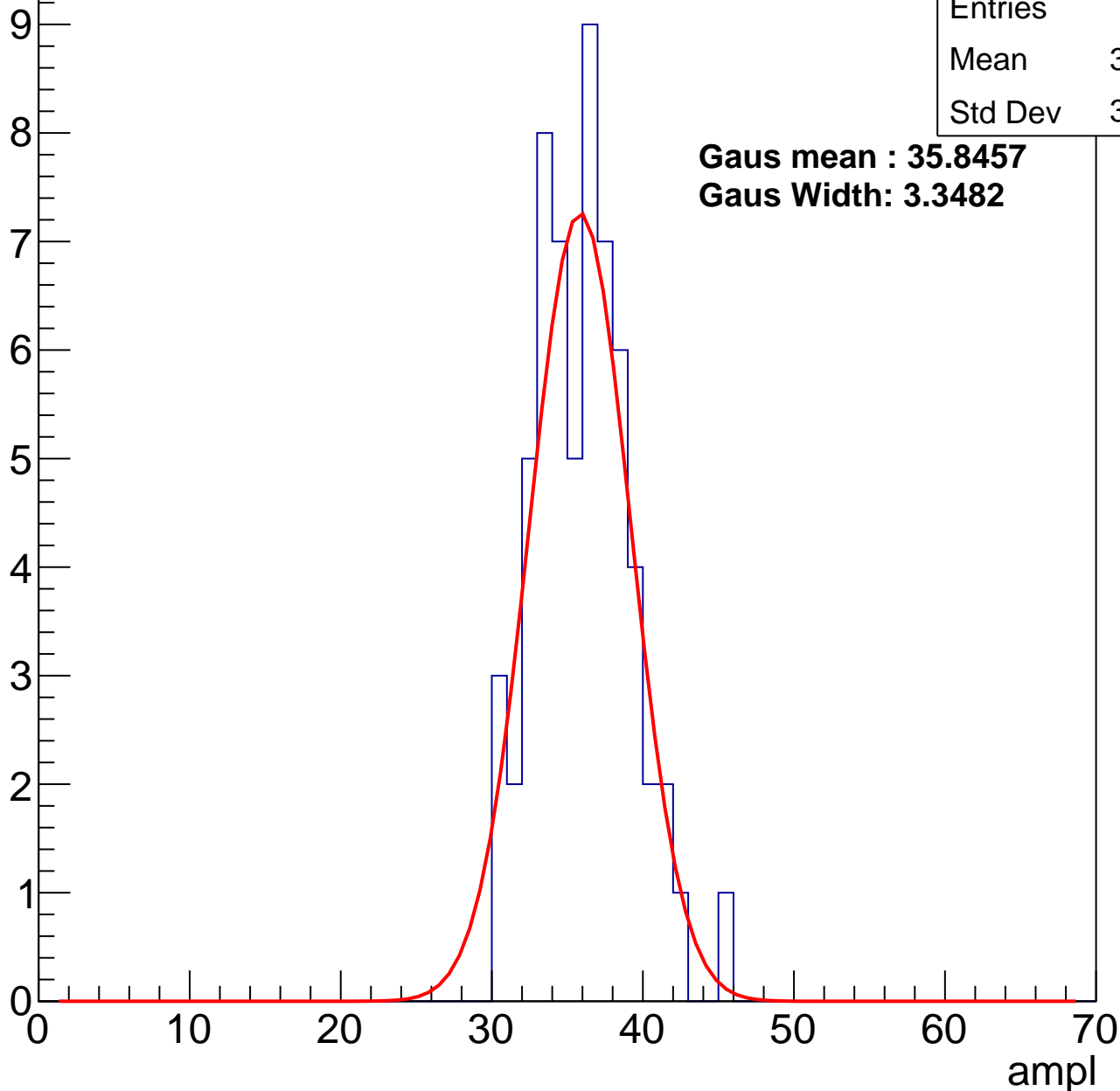
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.56
Std Dev	3.104

**Gaus mean : 35.8457**

**Gaus Width: 3.3482**



# B1L000S, U21-ch64, adc2

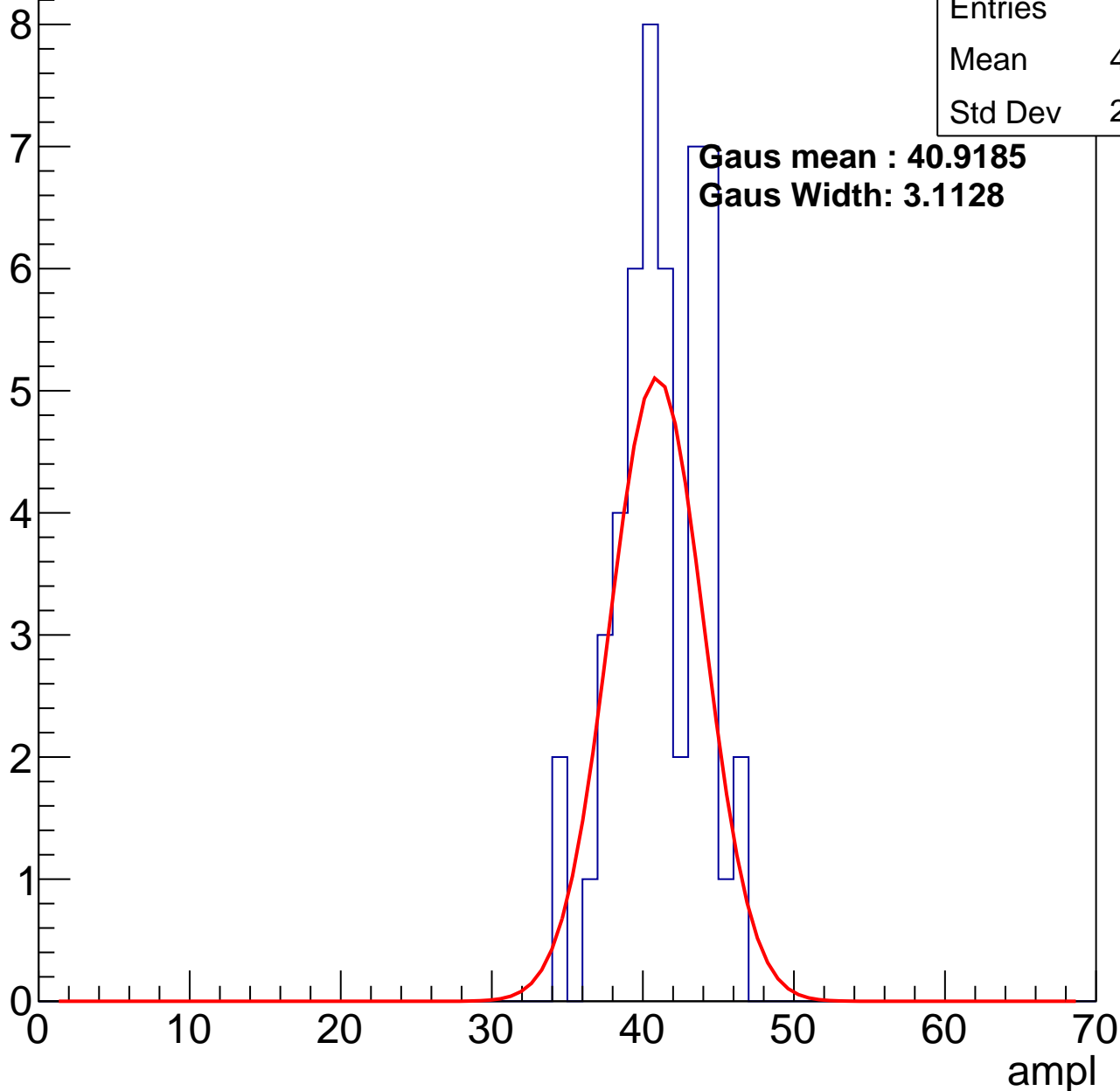
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	40.76
Std Dev	2.839

**Gaus mean : 40.9185**

**Gaus Width: 3.1128**



# B1L000S, U21-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	47.13
Std Dev	3.134

Entry

10

8

6

4

2

0

0

10

20

30

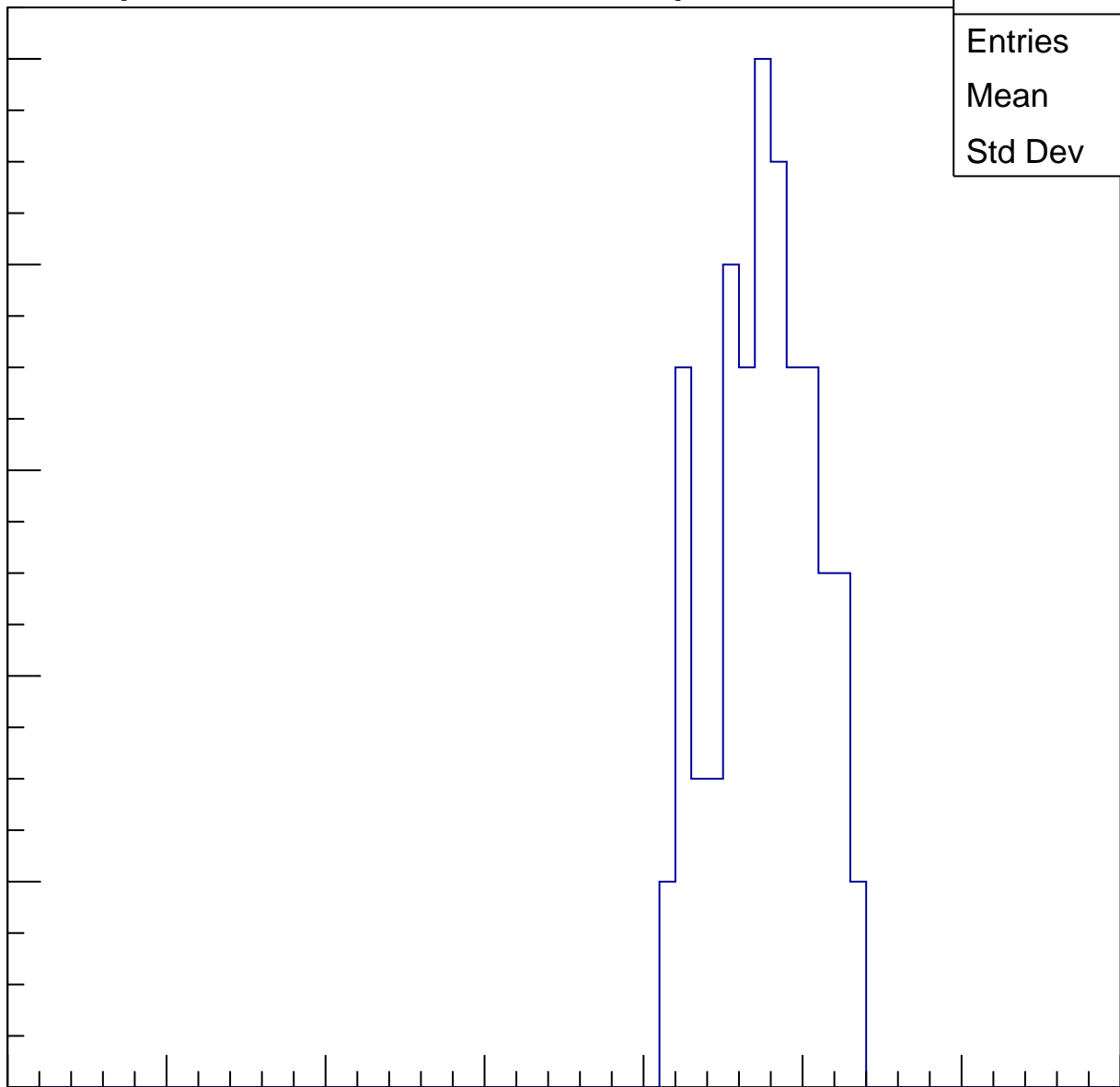
40

50

60

70

ampl

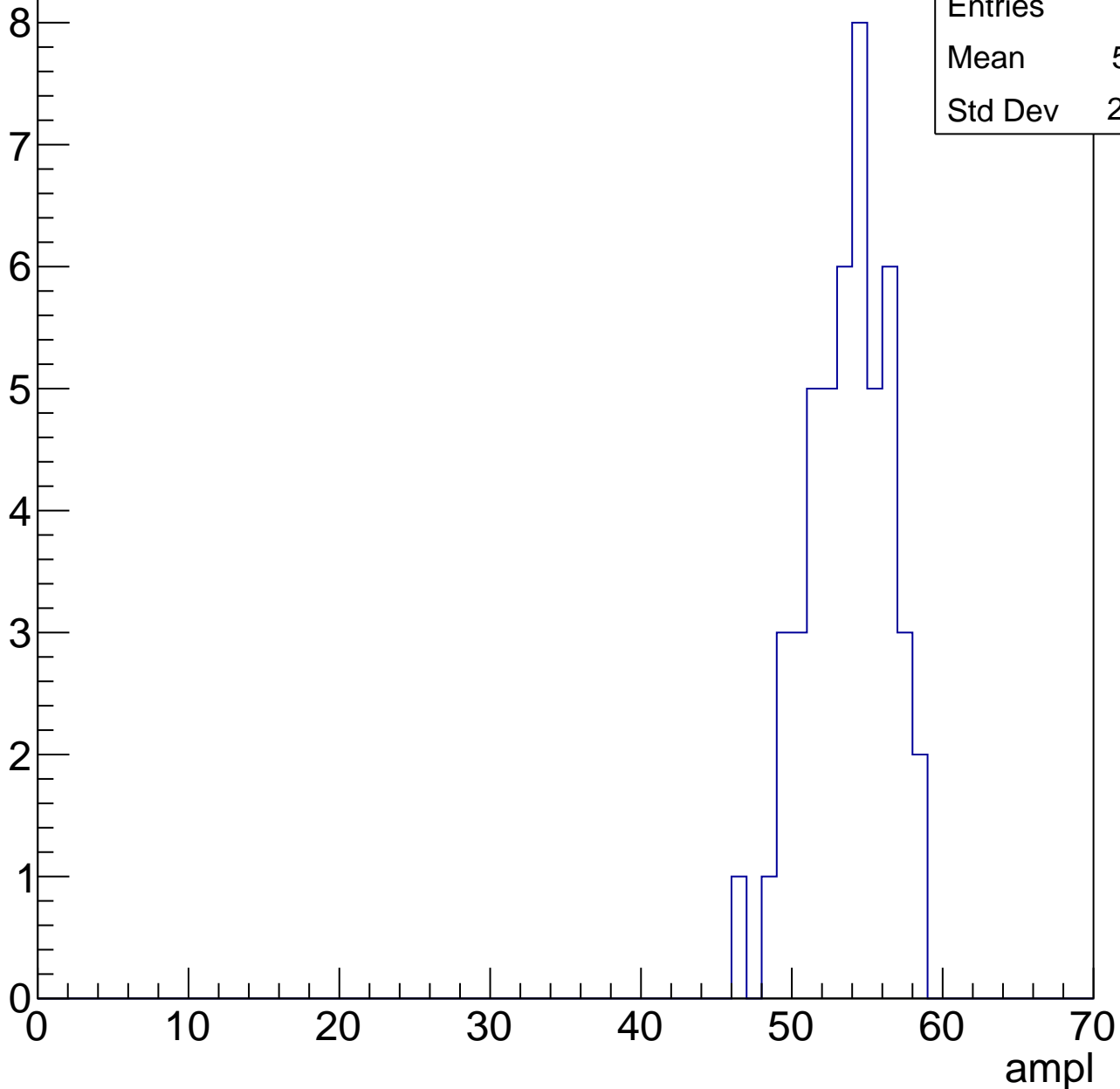


# B1L000S, U21-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	53.21
Std Dev	2.708



# B1L000S, U21-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 63

Mean 58.76

Std Dev 2.641

8

6

4

2

0

0

10

20

30

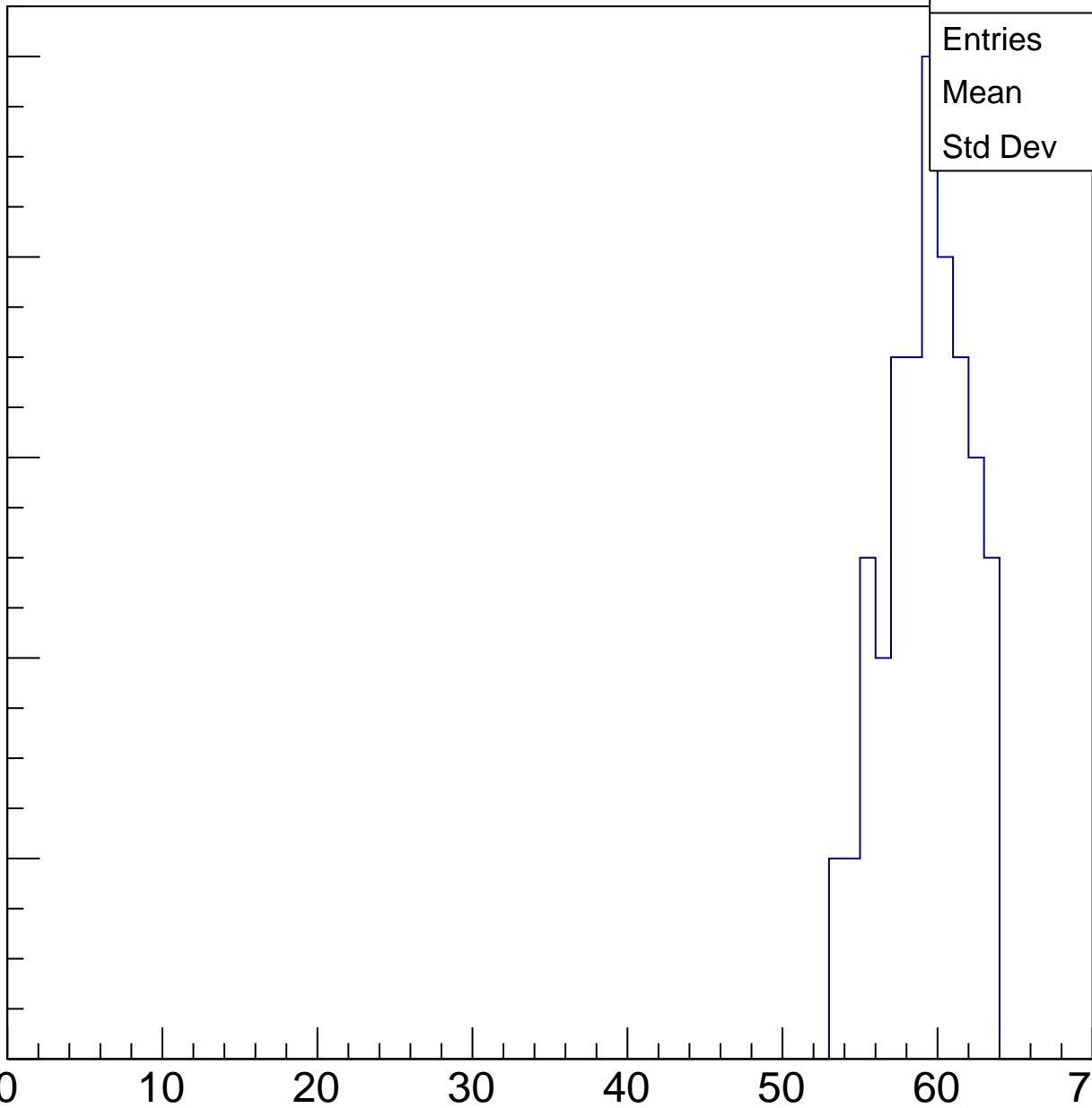
40

50

60

70

ampl

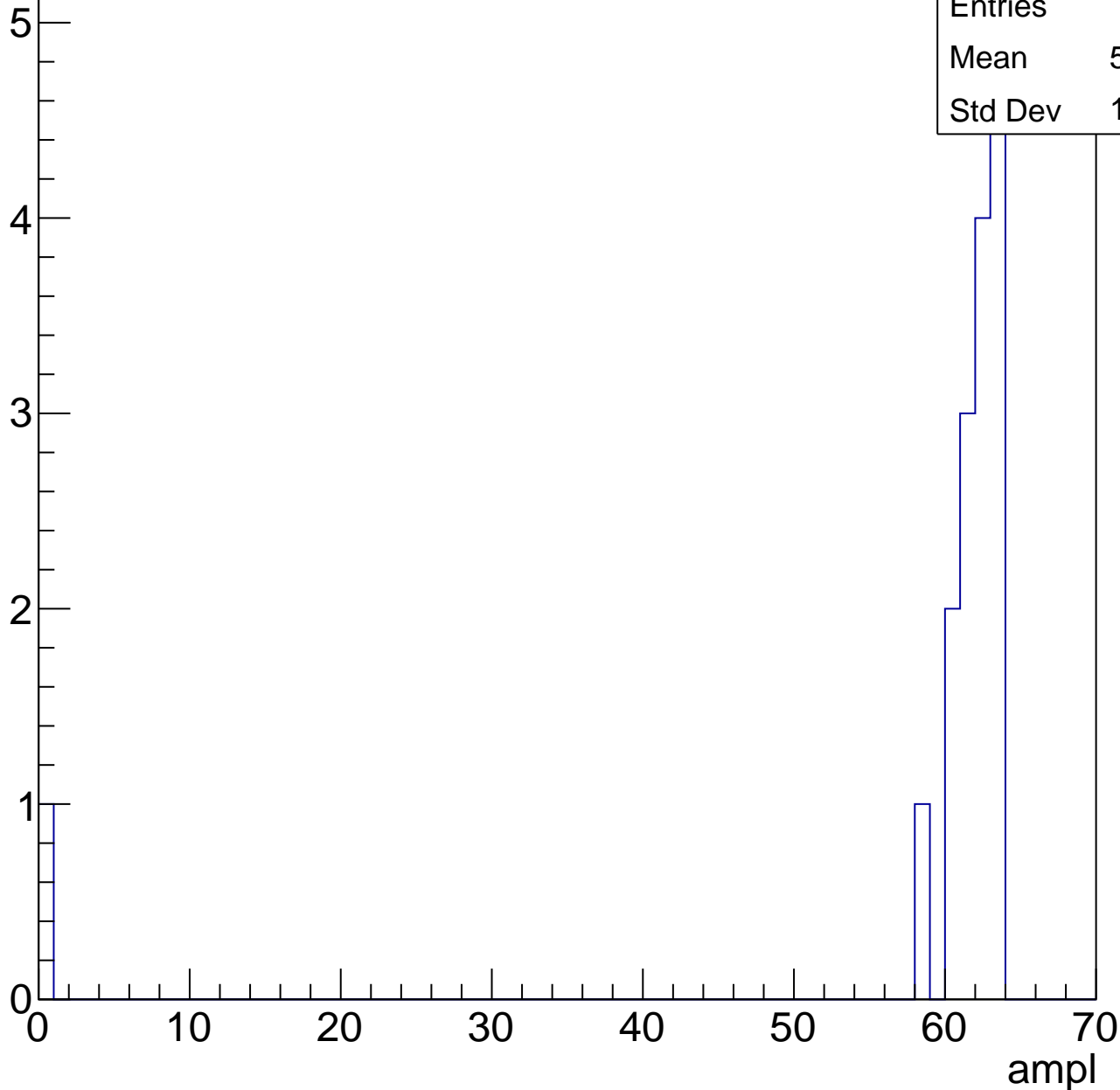


# B1L000S, U21-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.75
Std Dev	14.97





# B1L000S, U21-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L000S, U21-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	27.41
Std Dev	2.816

**Gaus mean : 27.9988**

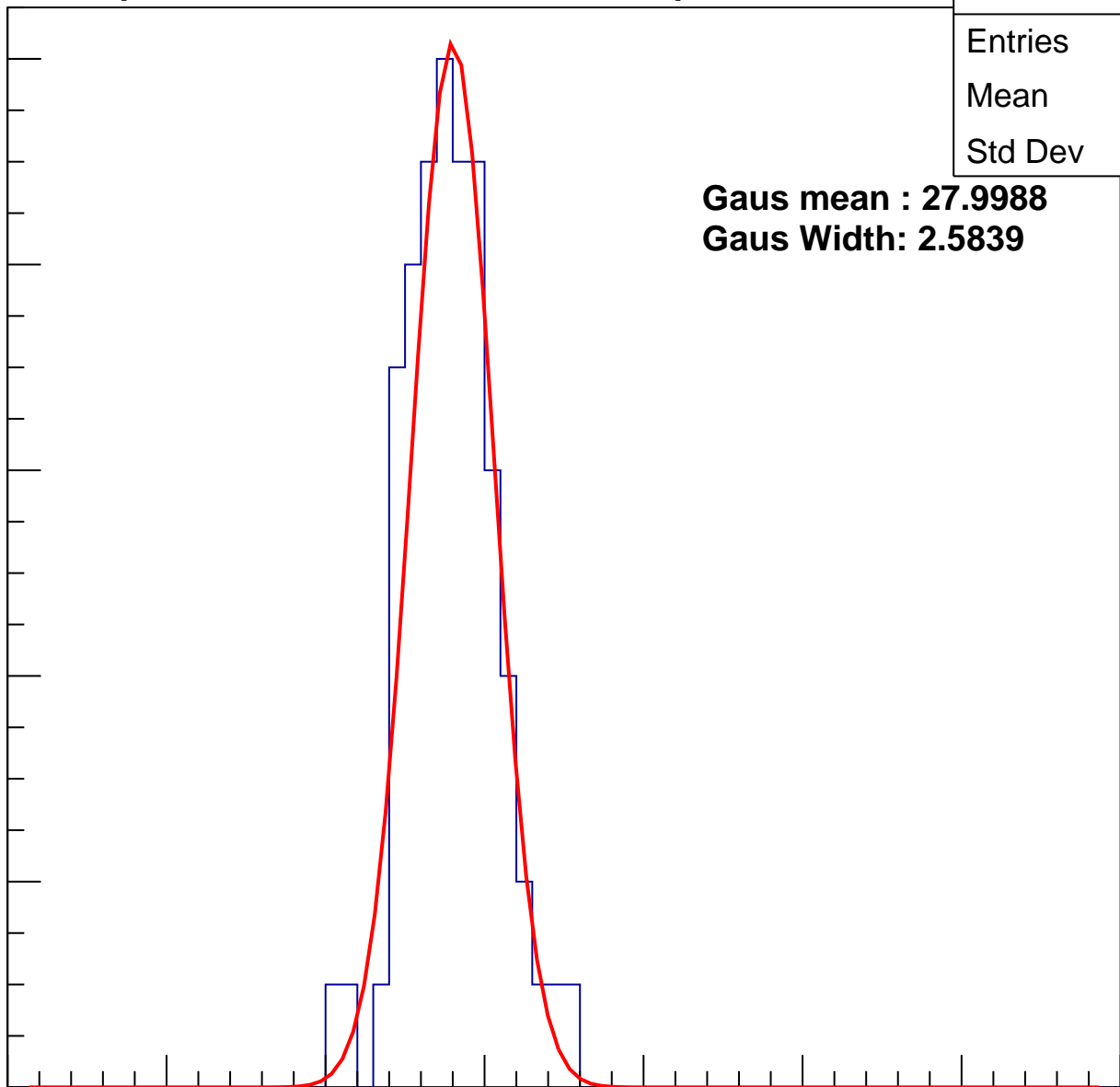
**Gaus Width: 2.5839**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch65, adc1

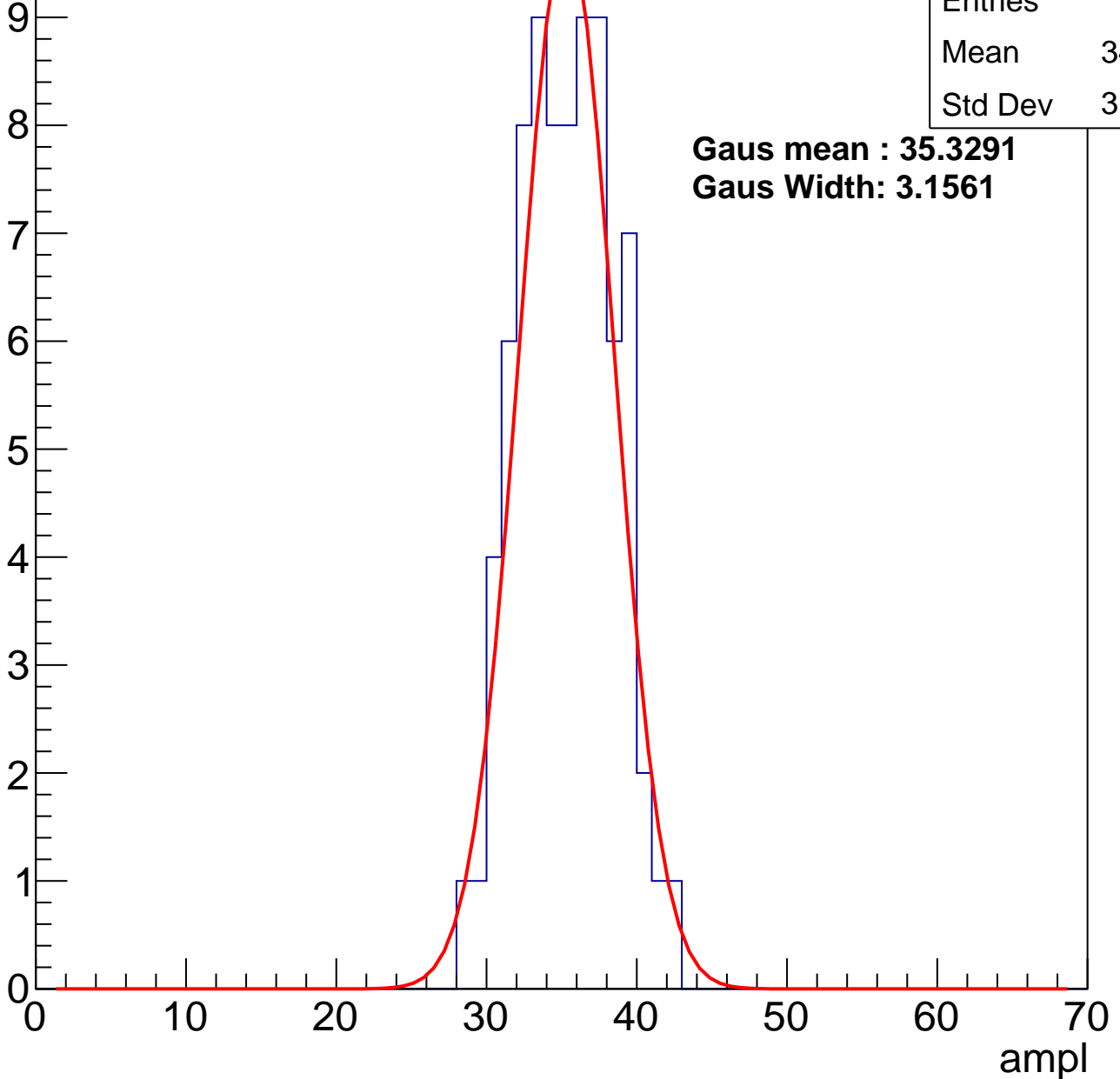
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	34.86
Std Dev	3.044

**Gaus mean : 35.3291**

**Gaus Width: 3.1561**



# B1L000S, U21-ch65, adc2

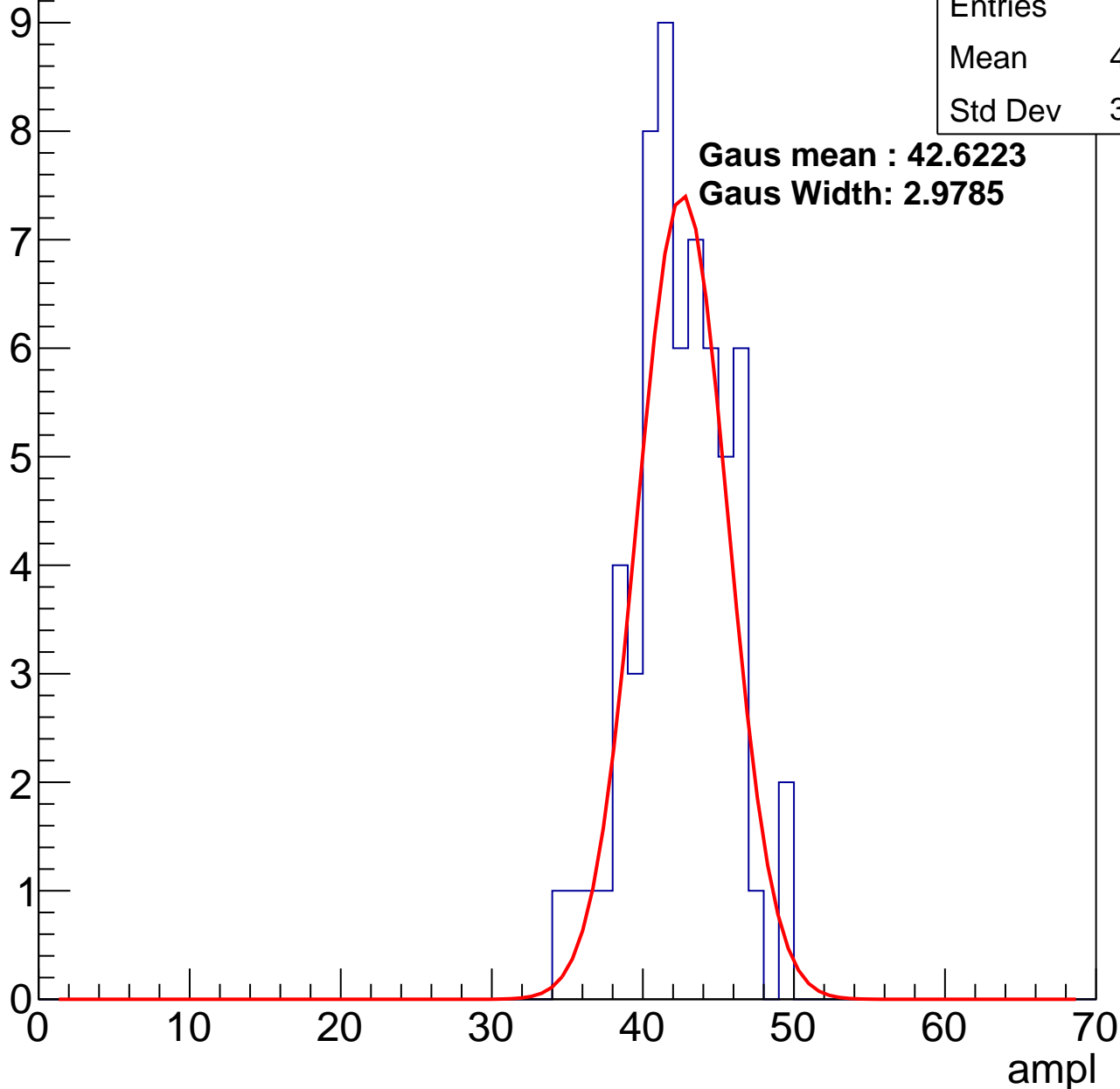
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.02
Std Dev	3.139

**Gaus mean : 42.6223**

**Gaus Width: 2.9785**



# B1L000S, U21-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

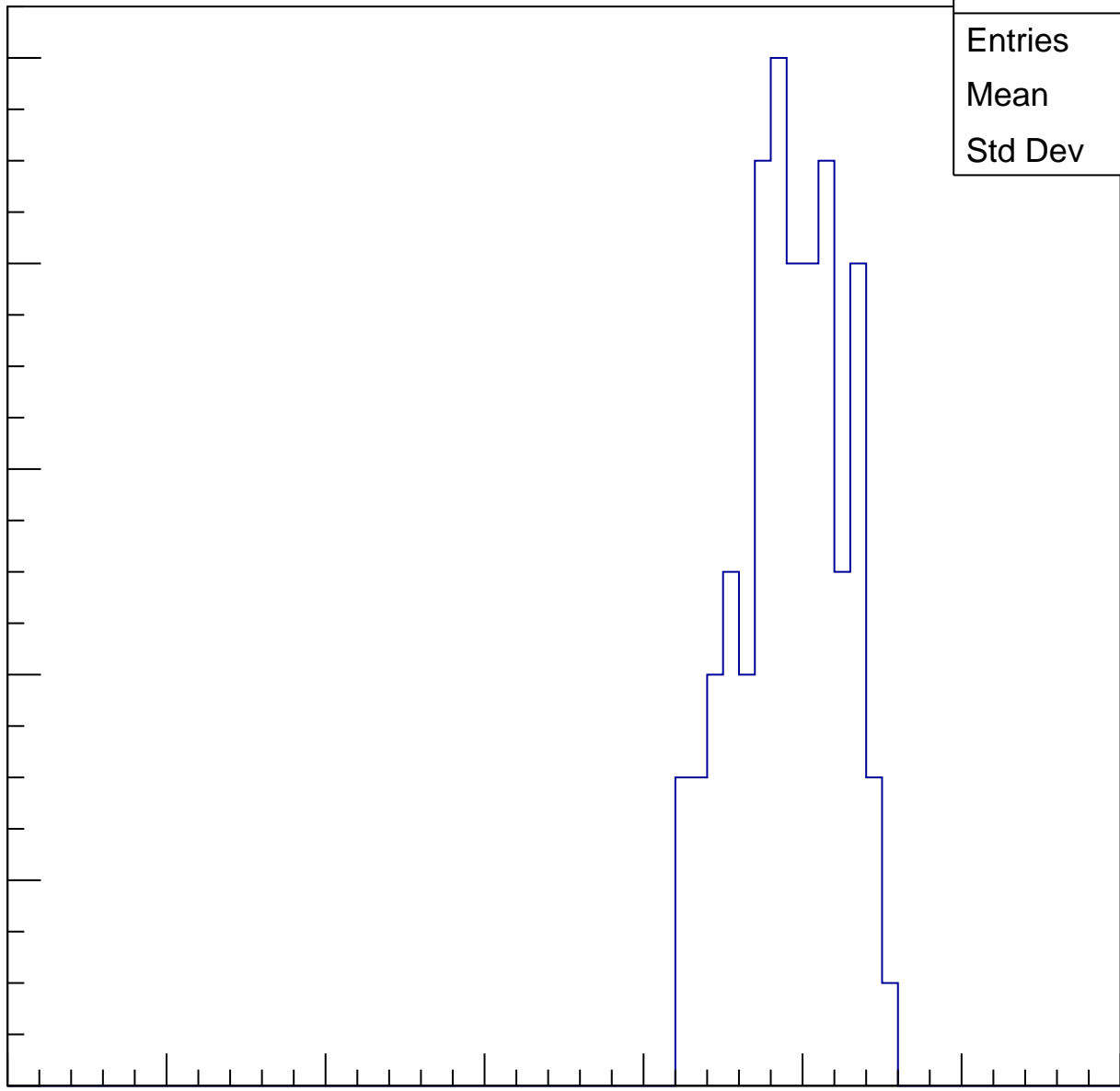
Entries	80
Mean	48.69
Std Dev	3.235

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

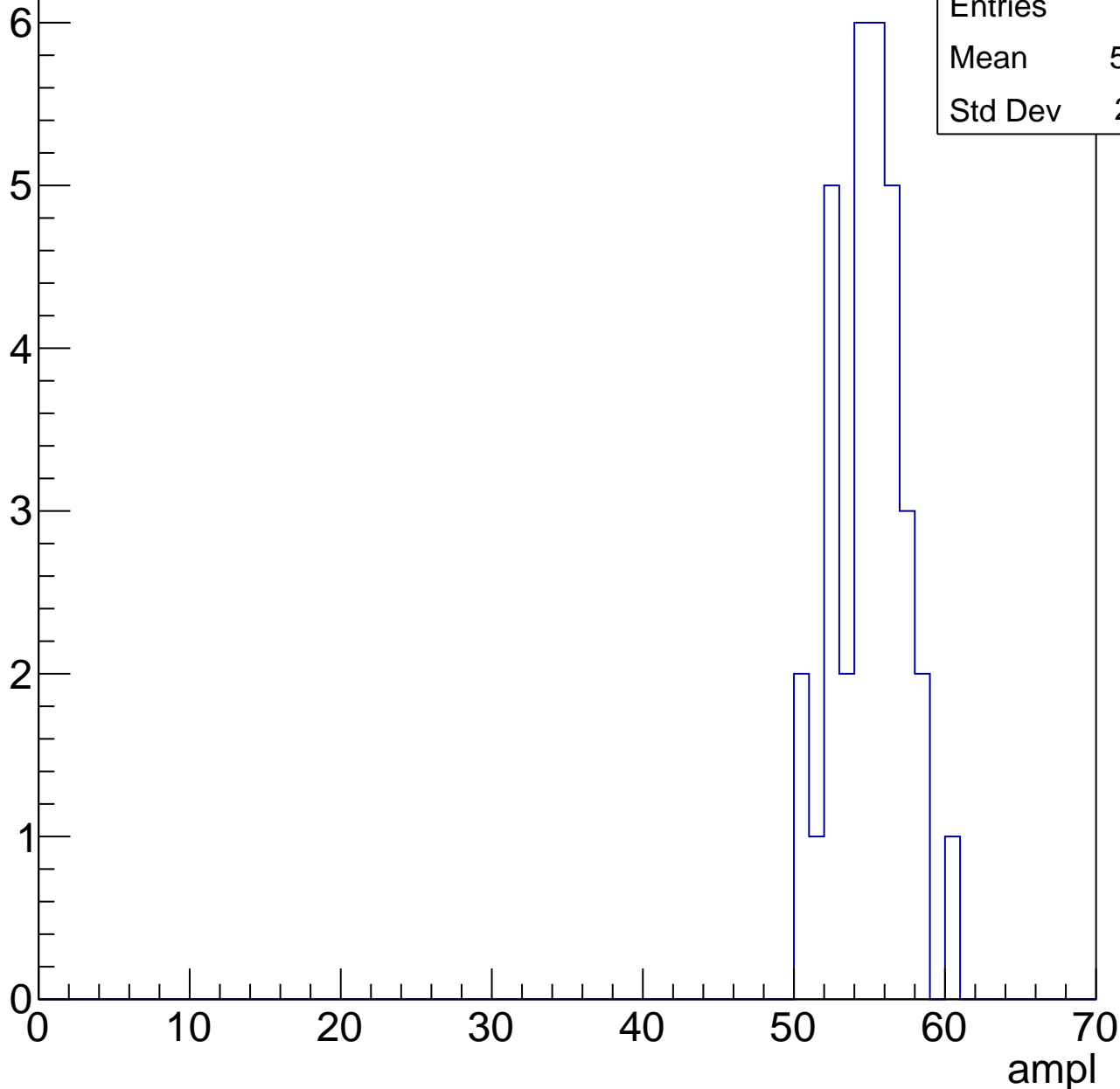
ampl



# B1L000S, U21-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	33
Mean	54.48
Std Dev	2.311

# B1L000S, U21-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

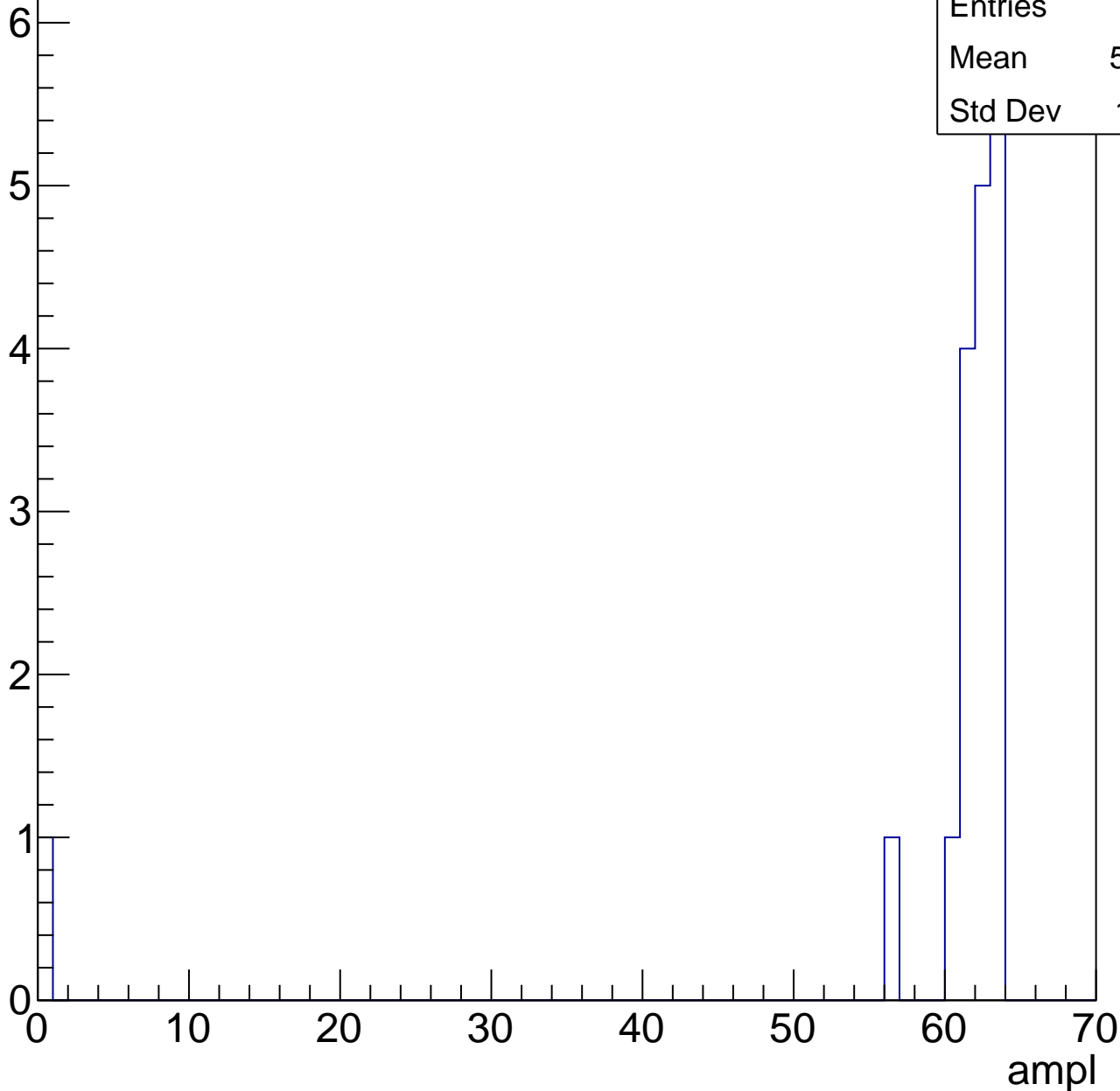
Entries	56
Mean	59
Std Dev	2.26

# B1L000S, U21-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.22
Std Dev	14.21

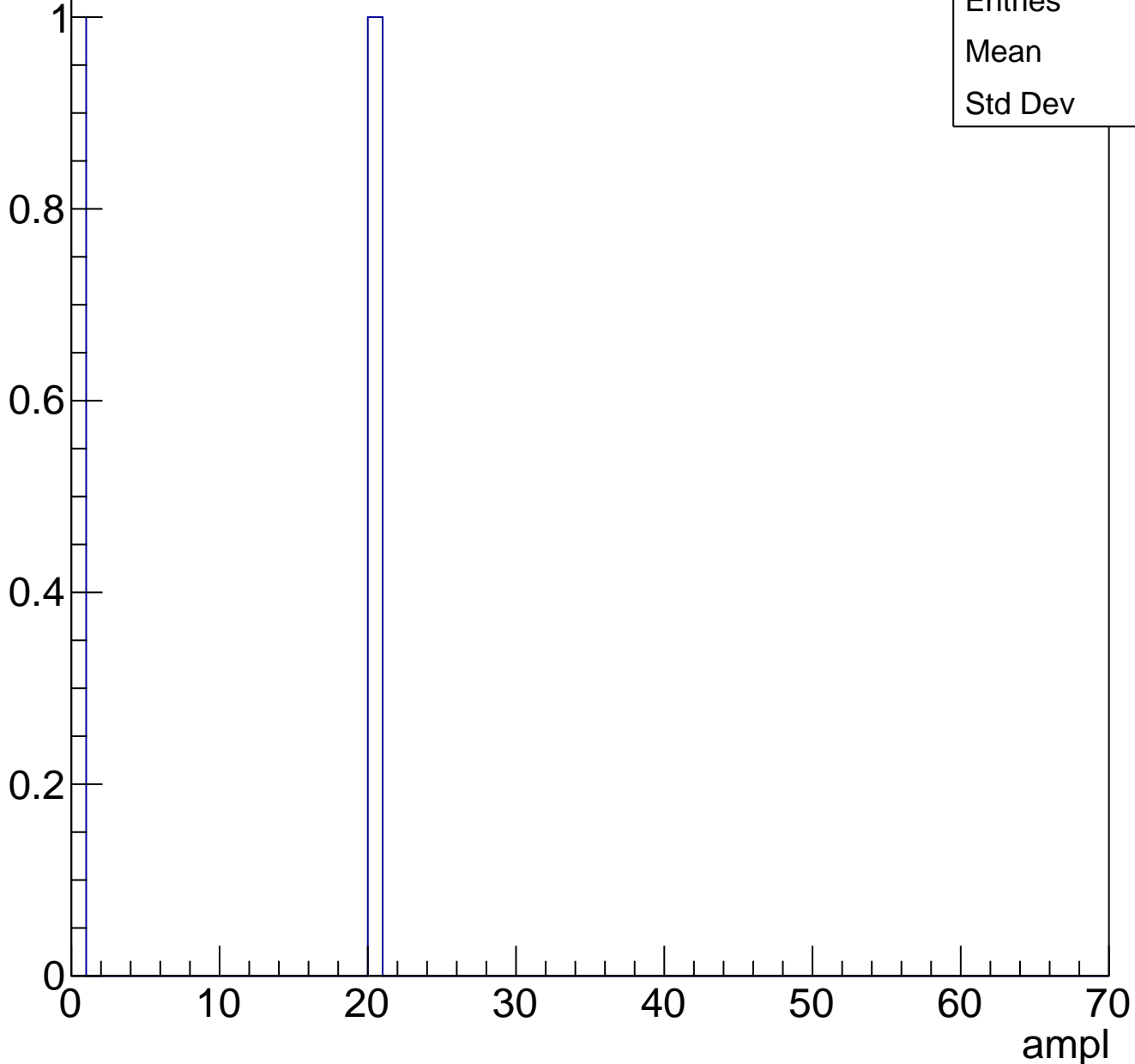




# B1L000S, U21-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L000S, U21-ch66, adc0

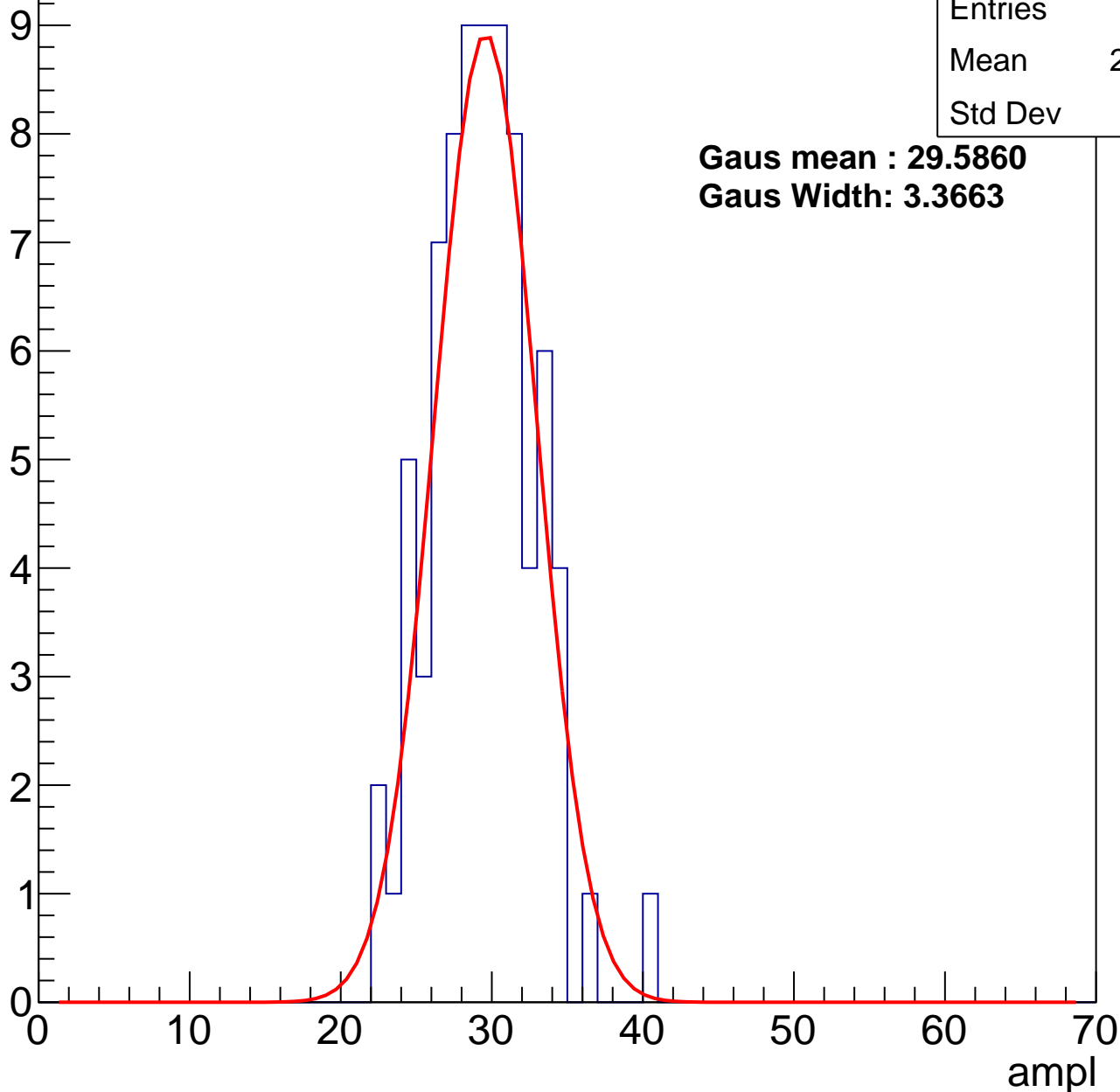
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	28.95
Std Dev	3.33

**Gaus mean : 29.5860**

**Gaus Width: 3.3663**



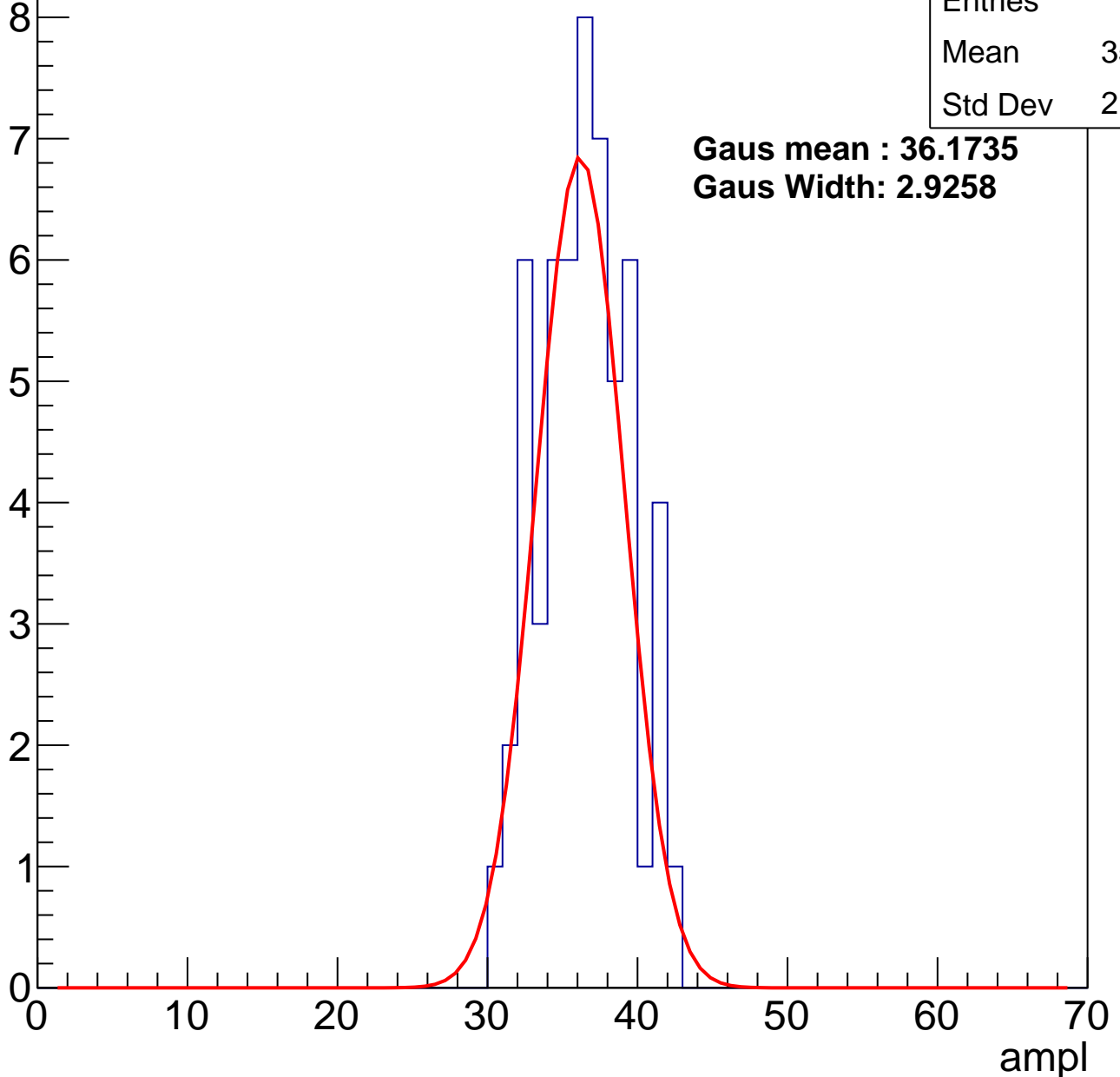
# B1L000S, U21-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.96
Std Dev	2.903

**Gaus mean : 36.1735**  
**Gaus Width: 2.9258**



# B1L000S, U21-ch66, adc2

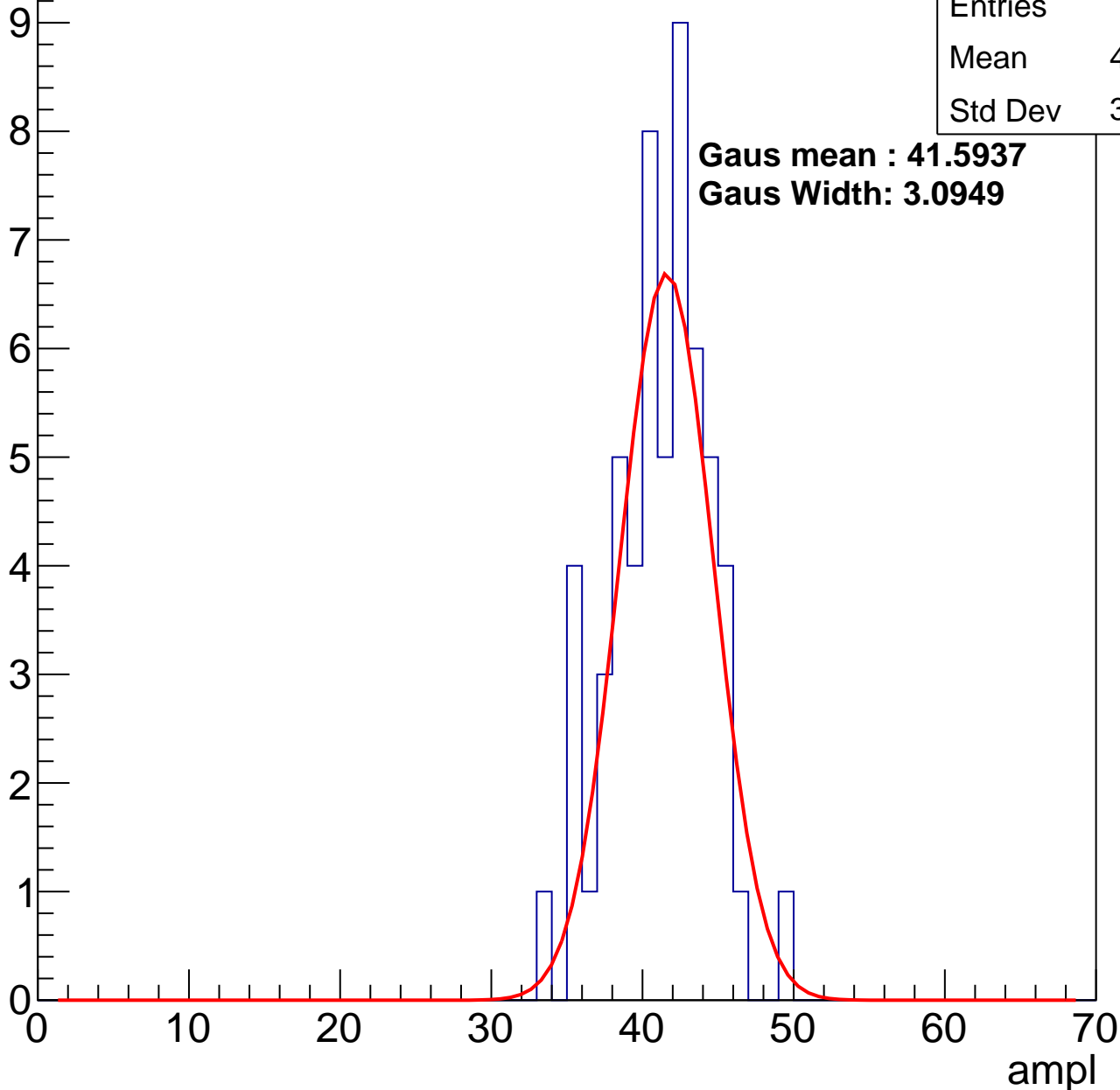
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	40.74
Std Dev	3.182

**Gaus mean : 41.5937**

**Gaus Width: 3.0949**



# B1L000S, U21-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

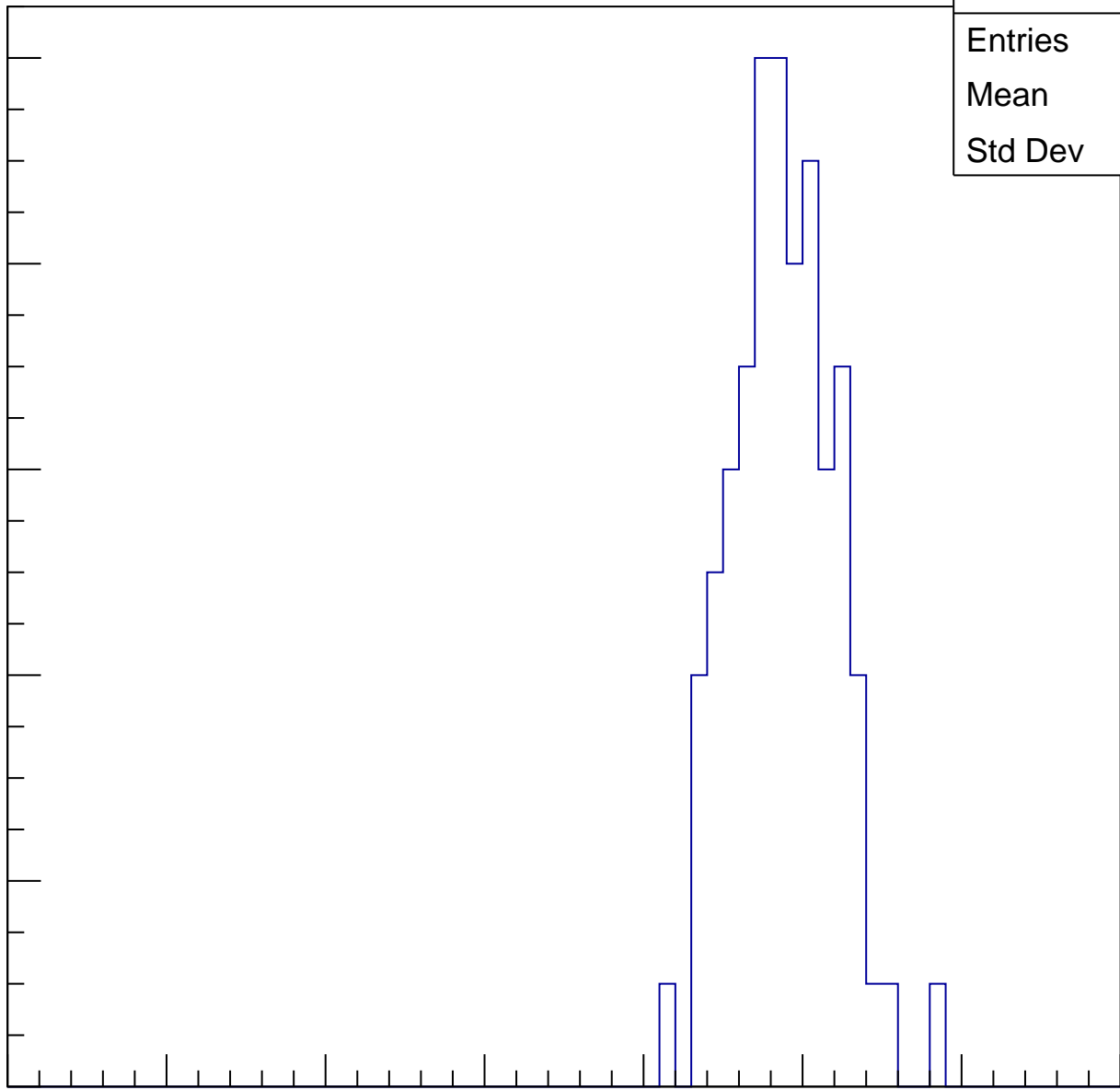
Entries	80
Mean	48.33
Std Dev	3.177

Entry

10  
8  
6  
4  
2  
0

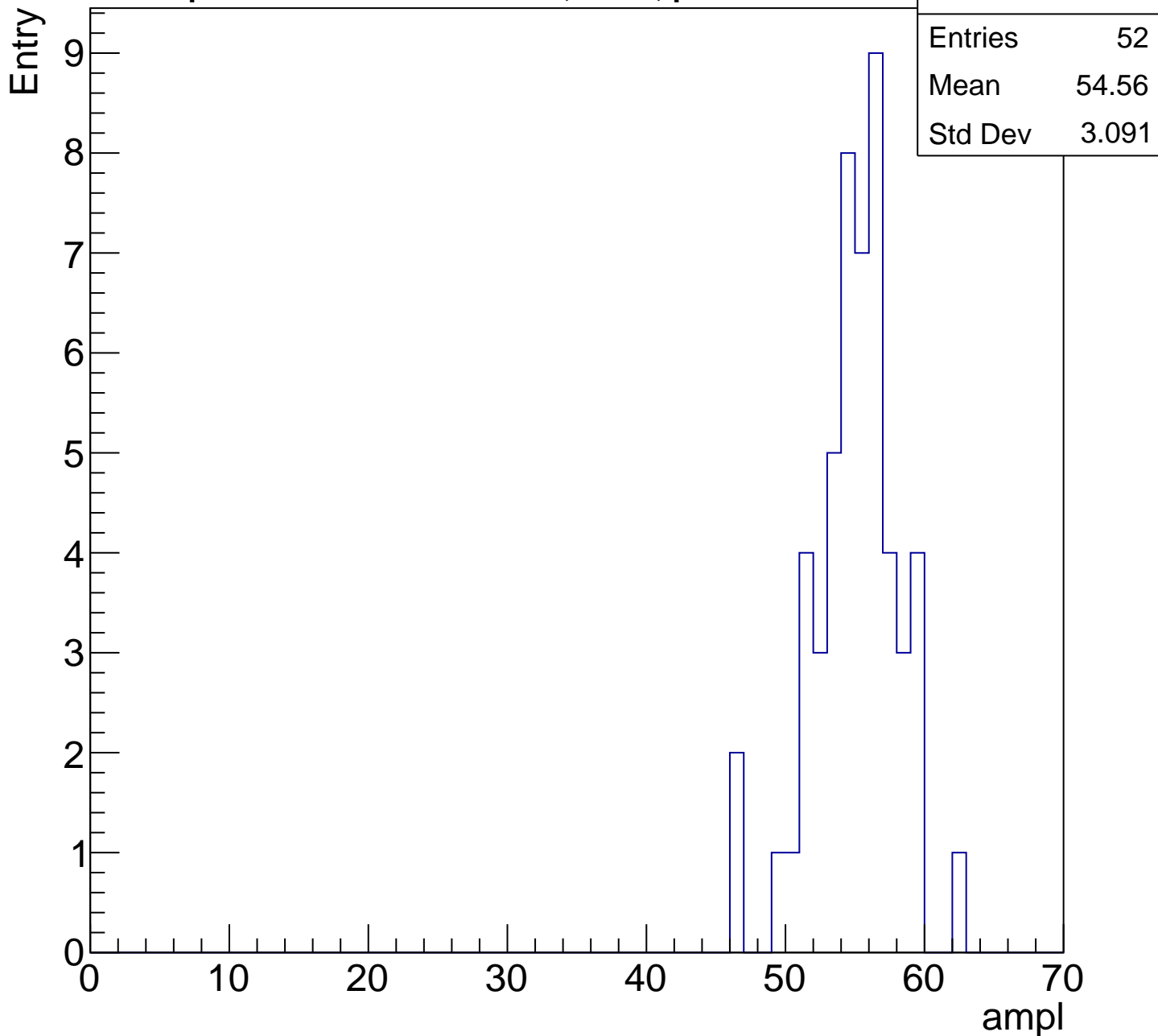
ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch66, adc4

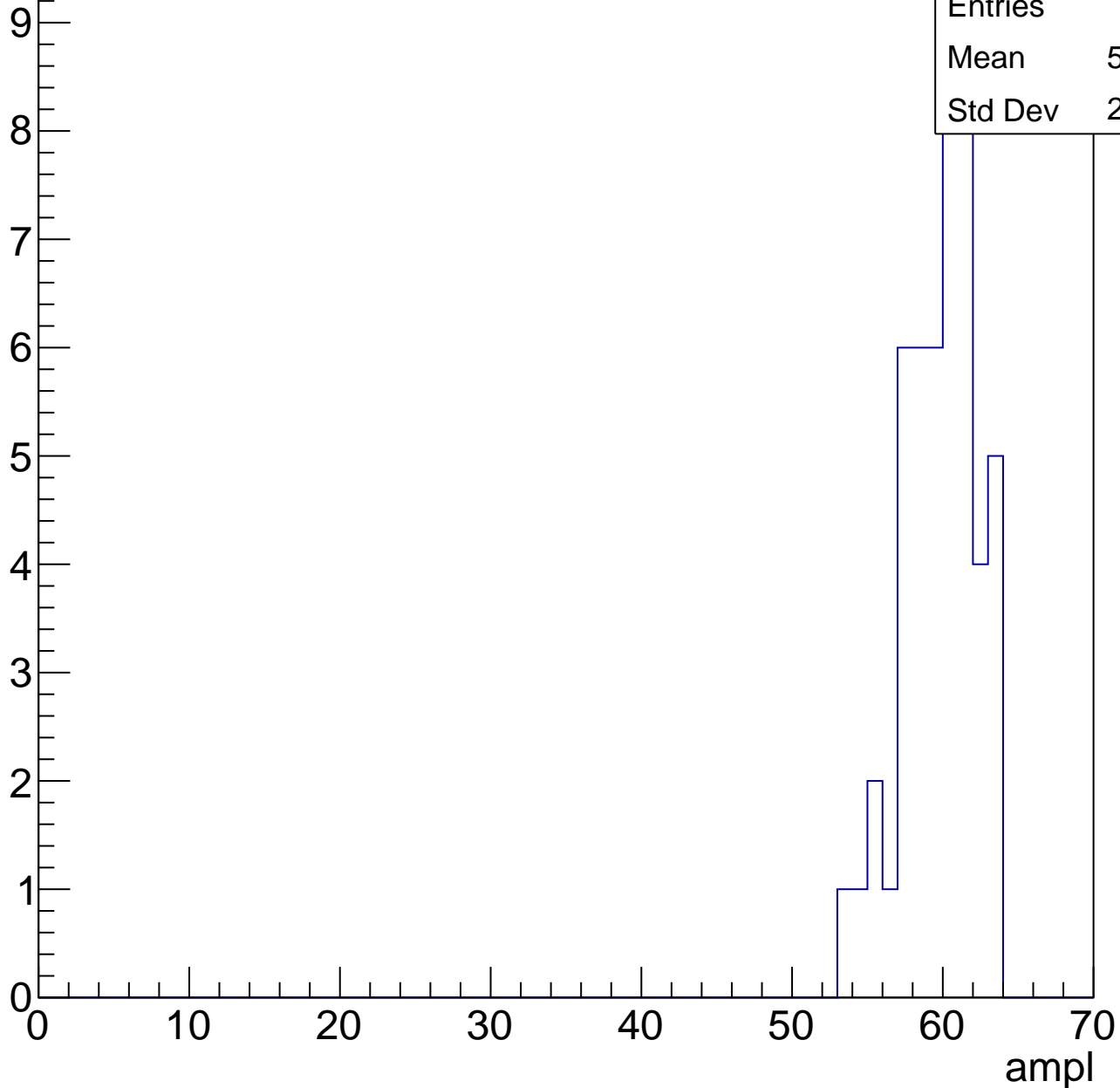
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

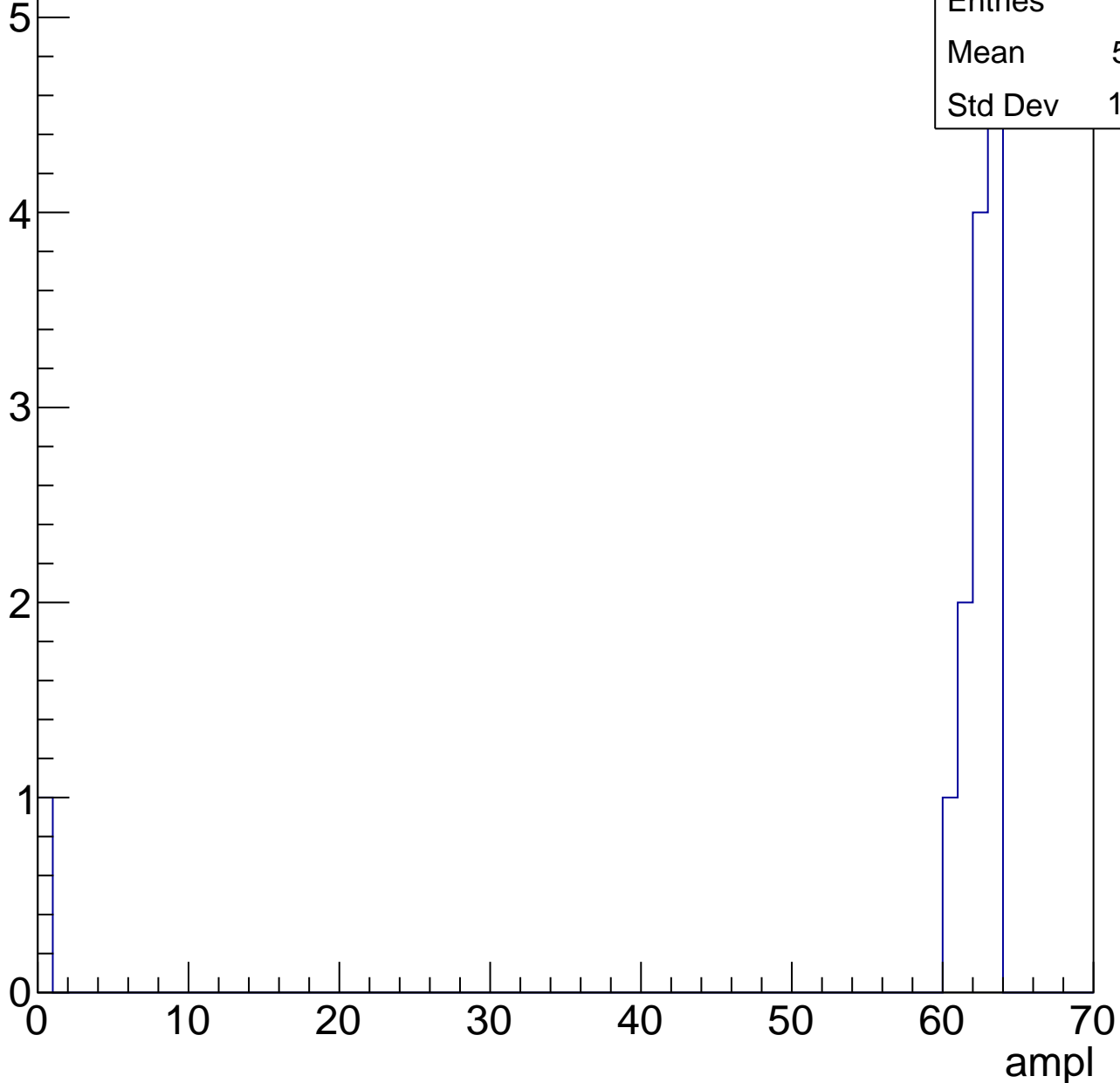


# B1L000S, U21-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	57.31
Std Dev	16.57





# B1L000S, U21-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch67, adc0

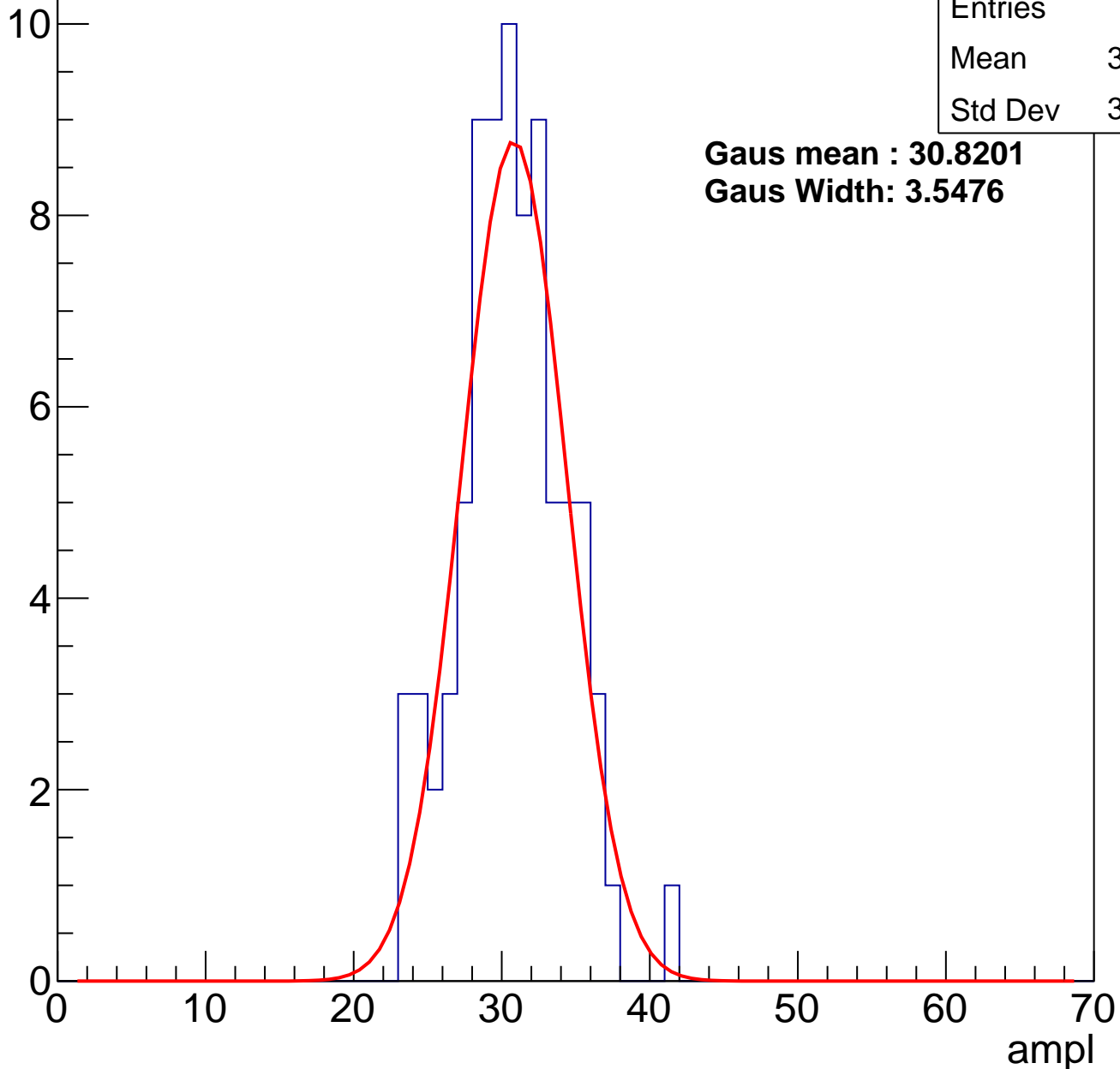
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	81
Mean	30.23
Std Dev	3.532

**Gaus mean : 30.8201**

**Gaus Width: 3.5476**

Entry



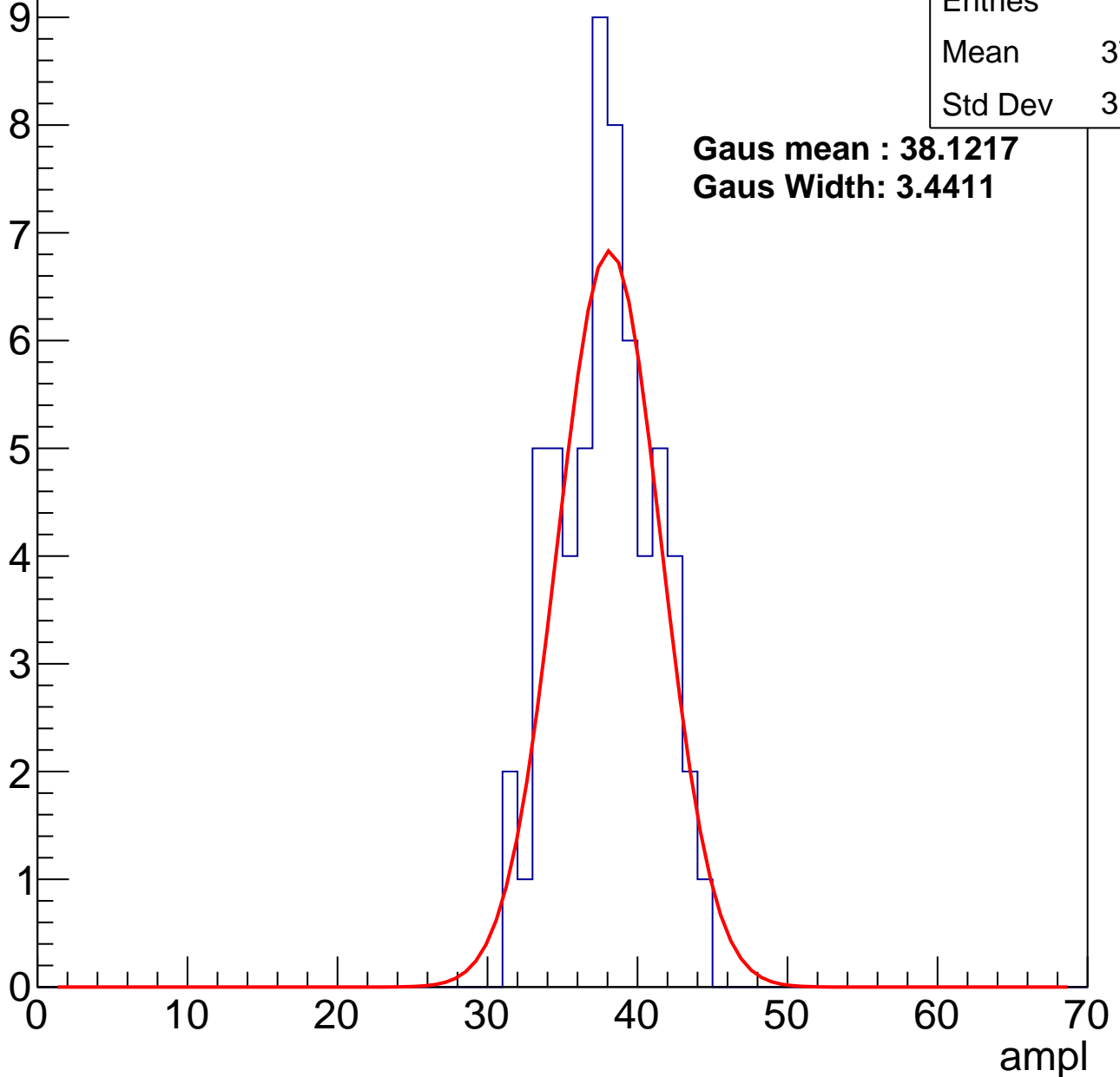
# B1L000S, U21-ch67, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	37.43
Std Dev	3.139

**Gaus mean : 38.1217**  
**Gaus Width: 3.4411**



# B1L000S, U21-ch67, adc2

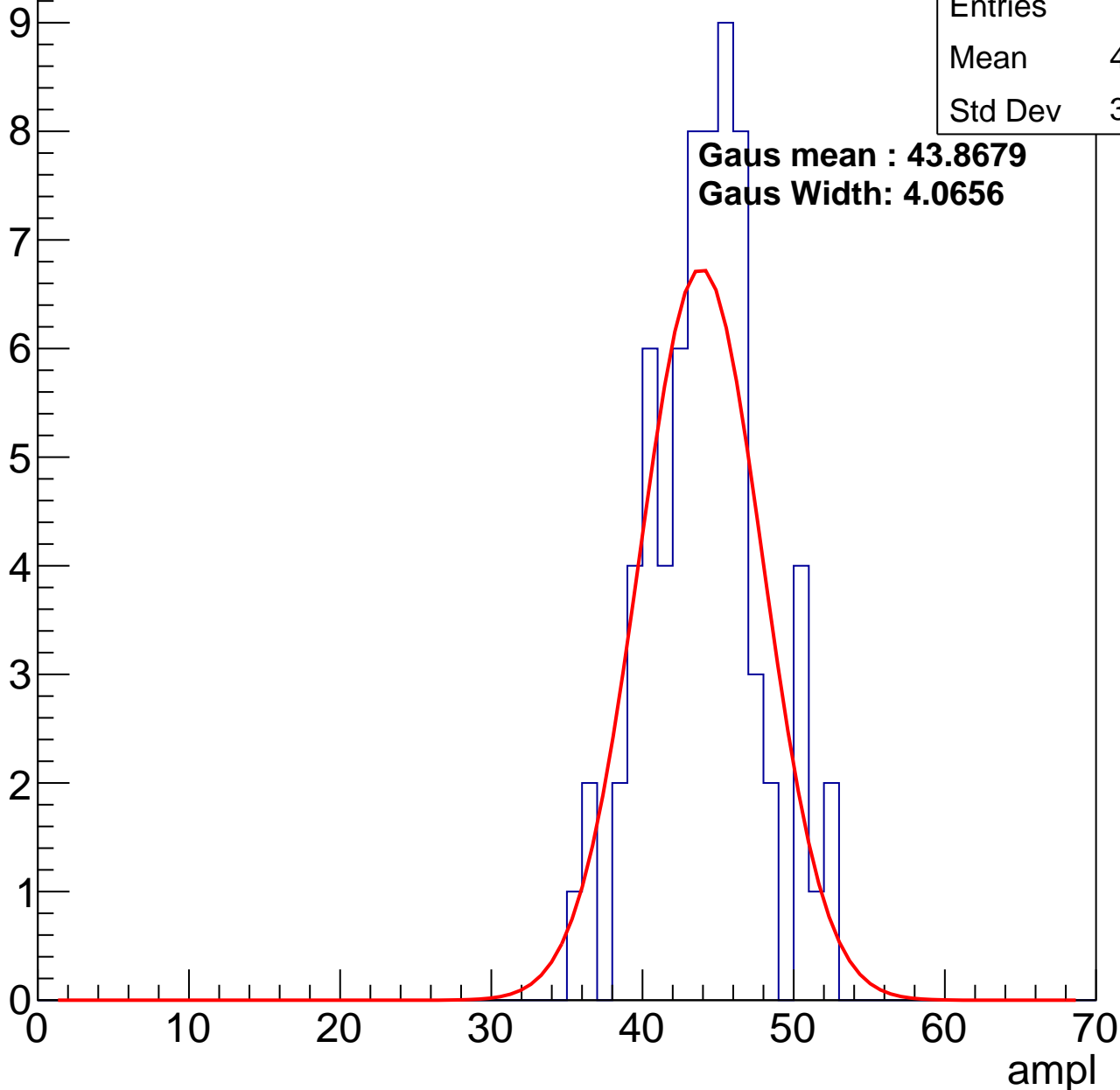
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	43.66
Std Dev	3.699

**Gaus mean : 43.8679**

**Gaus Width: 4.0656**

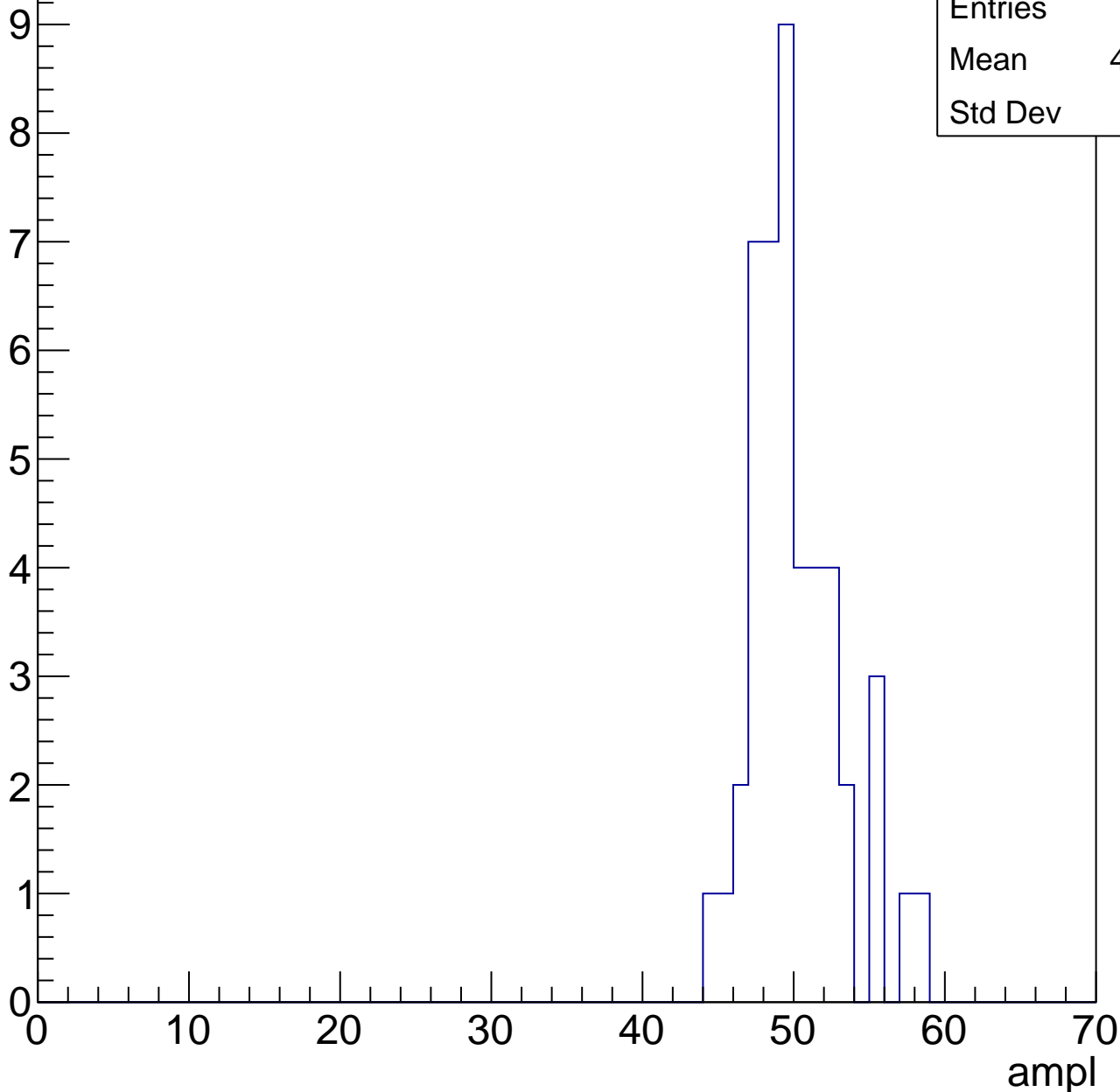


# B1L000S, U21-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

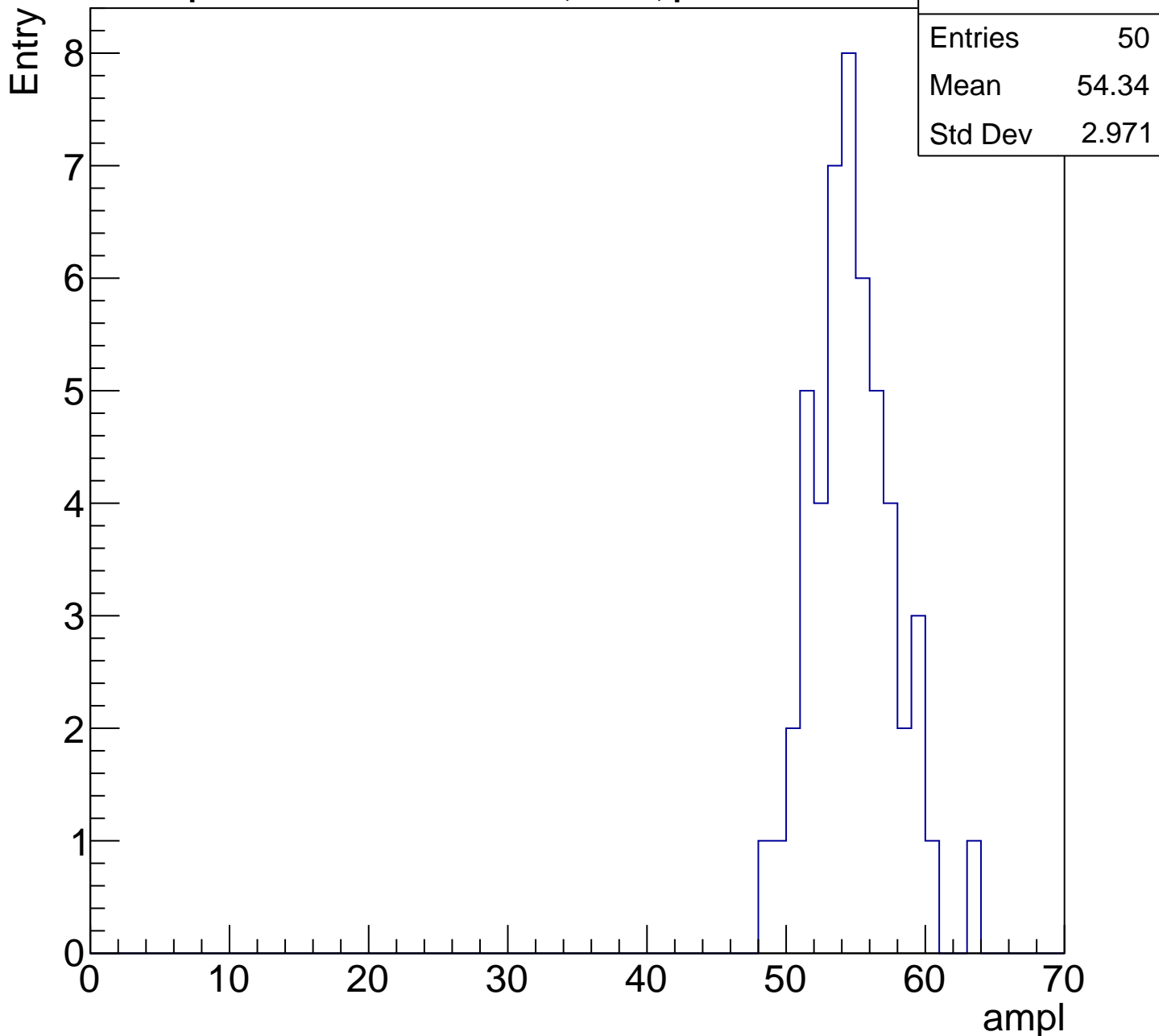
Entry

Entries	46
Mean	49.67
Std Dev	3



# B1L000S, U21-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

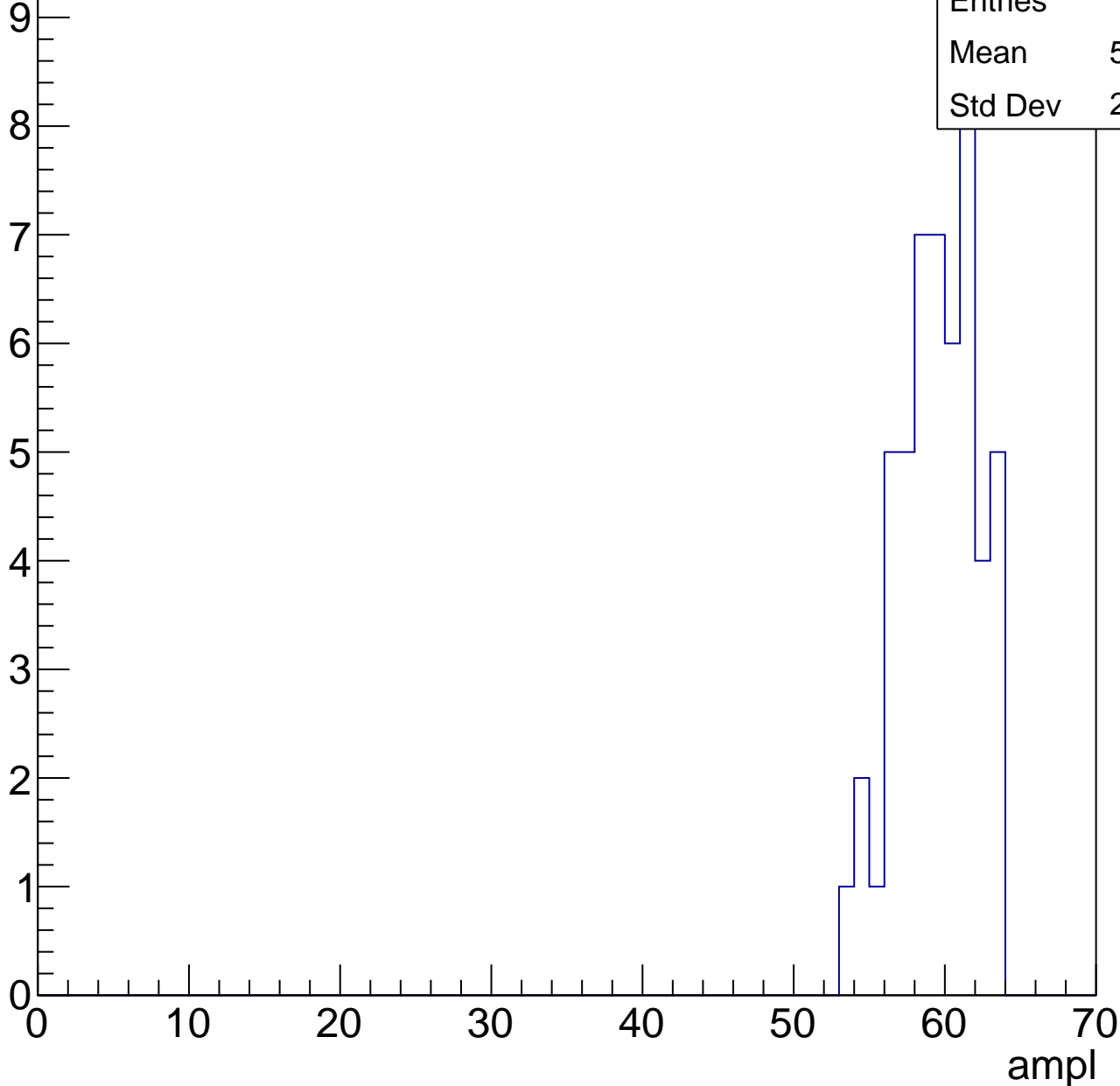


# B1L000S, U21-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	59.08
Std Dev	2.526

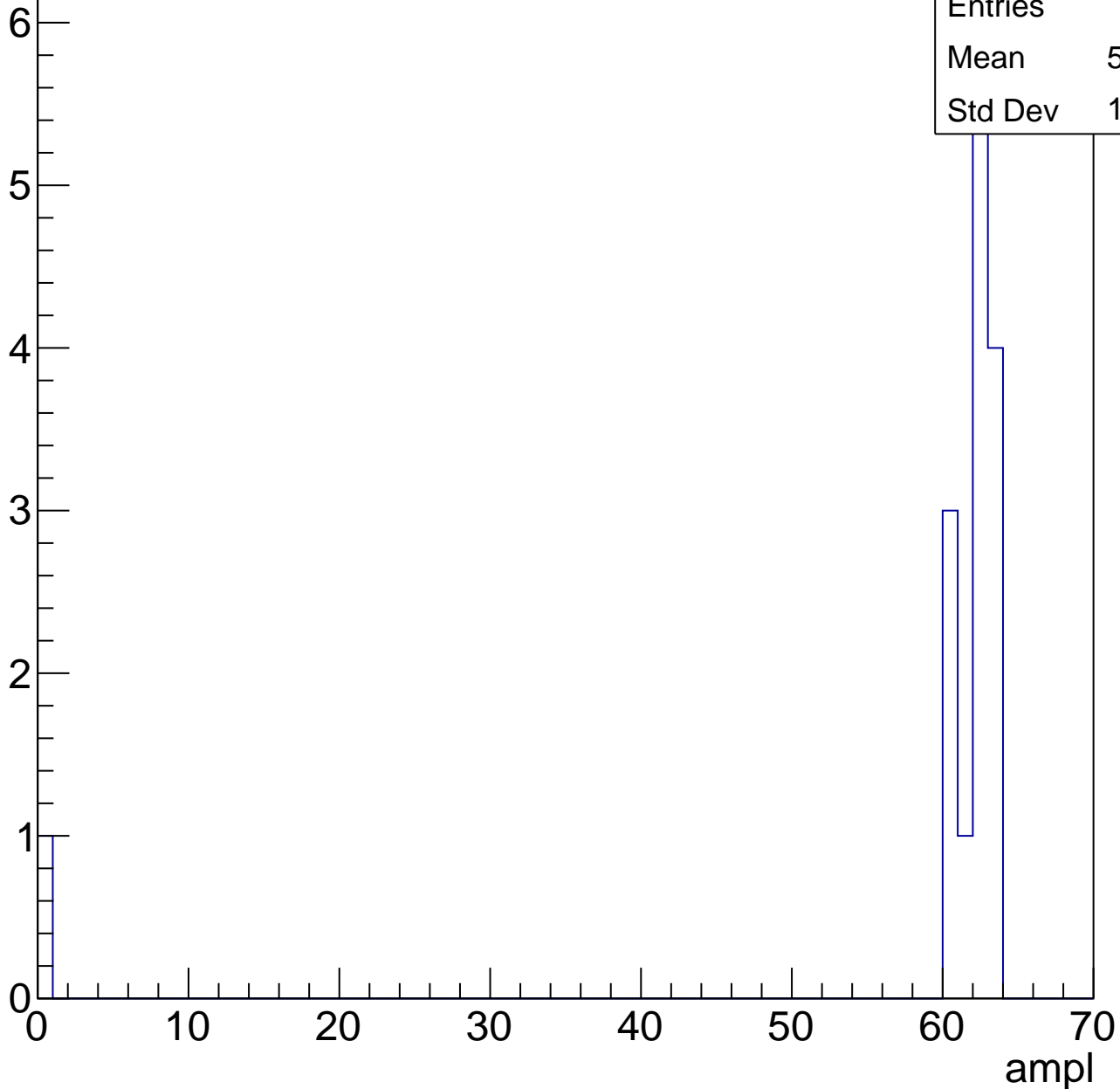


# B1L000S, U21-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.67
Std Dev	15.45

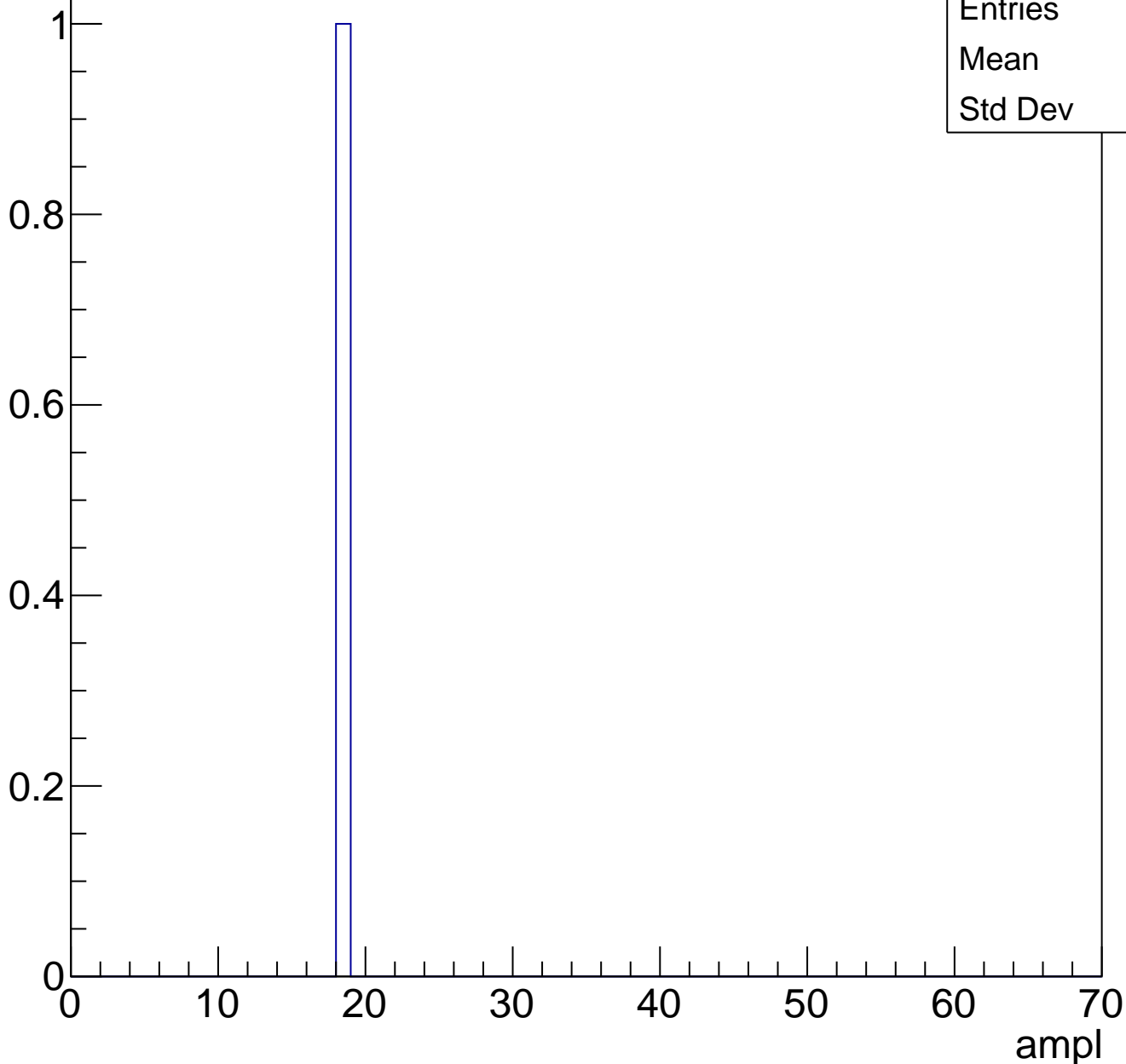




# B1L000S, U21-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch68, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	29.03
Std Dev	3.094

**Gaus mean : 29.0441**

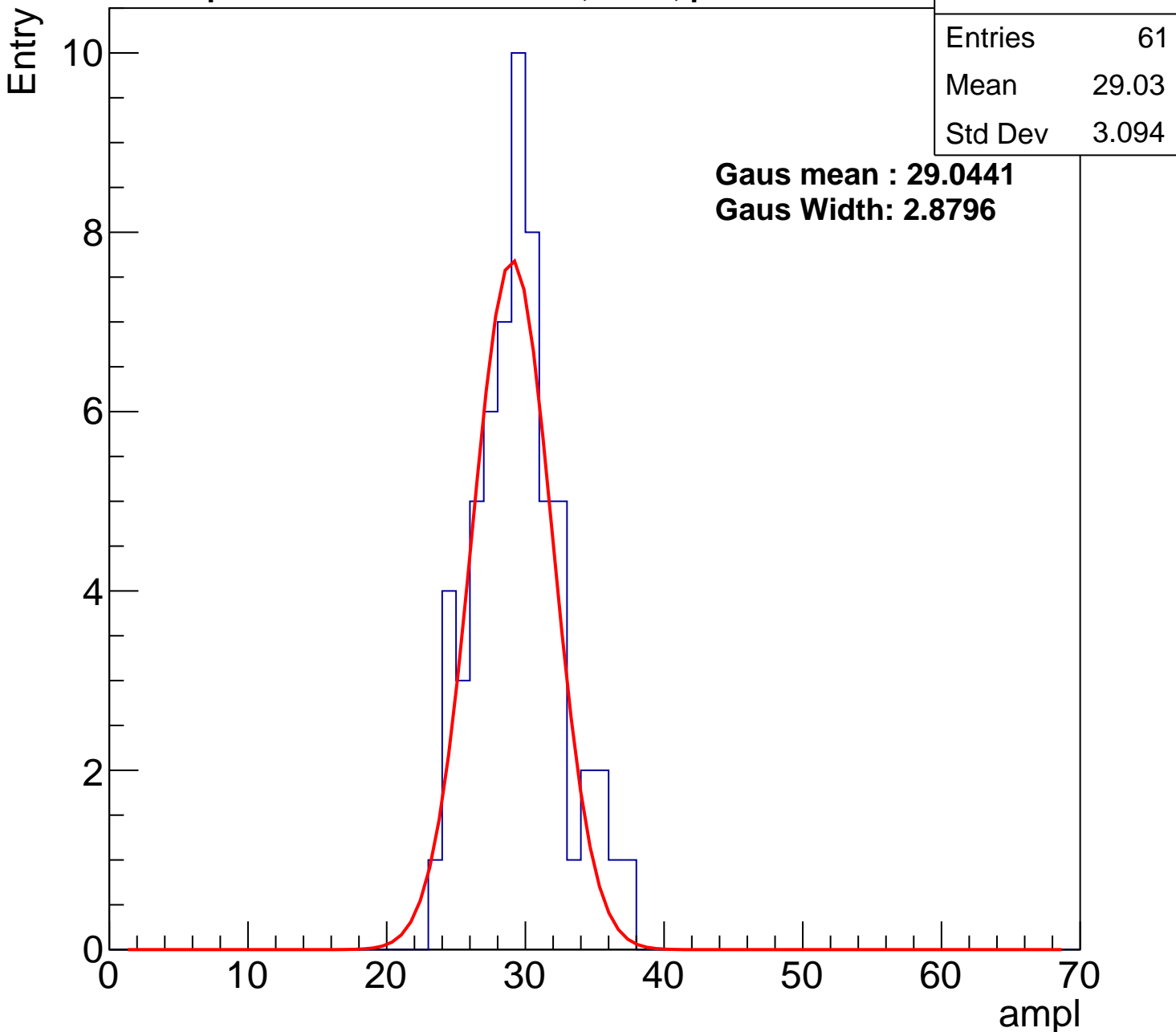
**Gaus Width: 2.8796**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch68, adc1

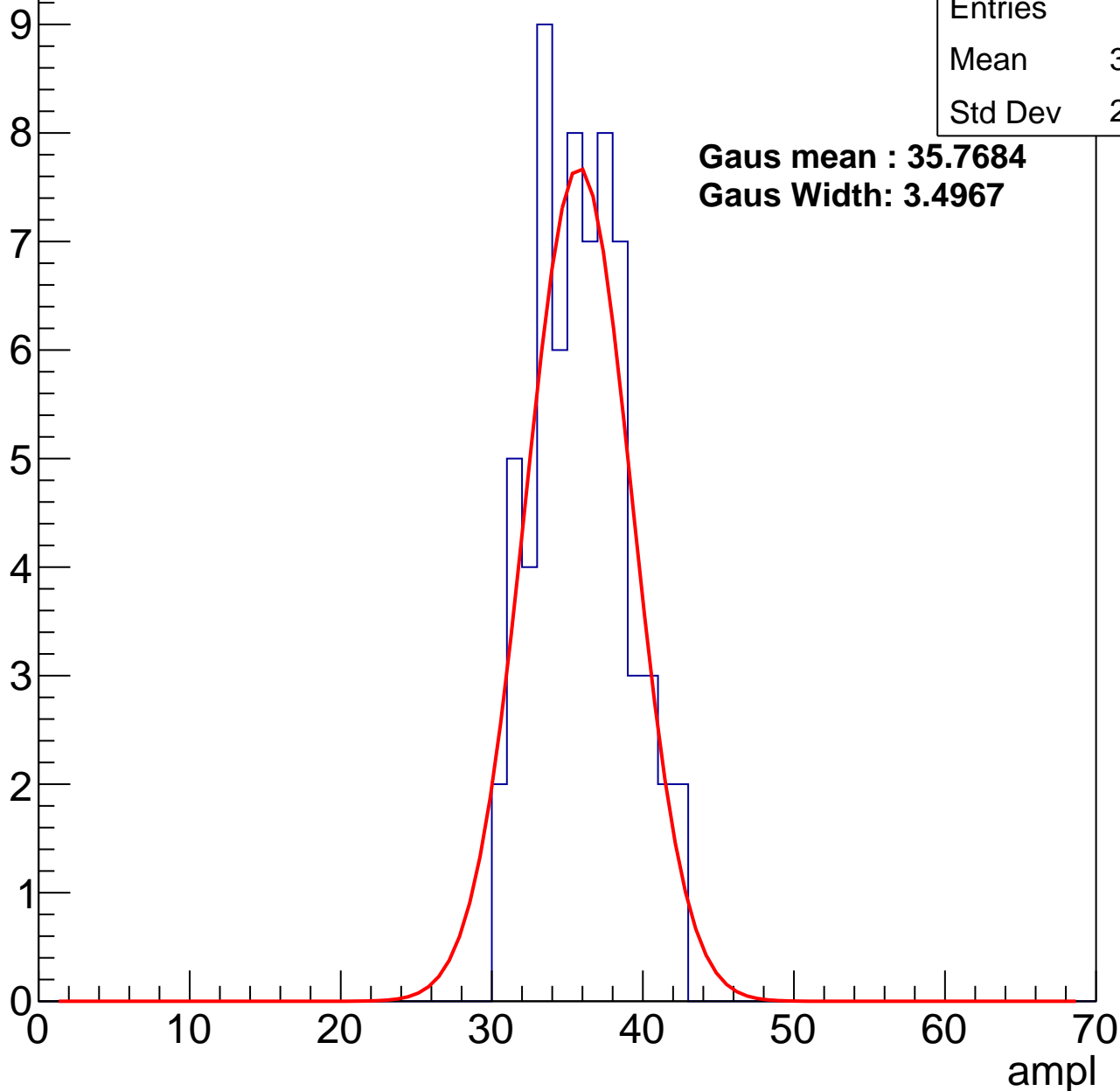
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	35.47
Std Dev	2.986

**Gaus mean : 35.7684**

**Gaus Width: 3.4967**



# B1L000S, U21-ch68, adc2

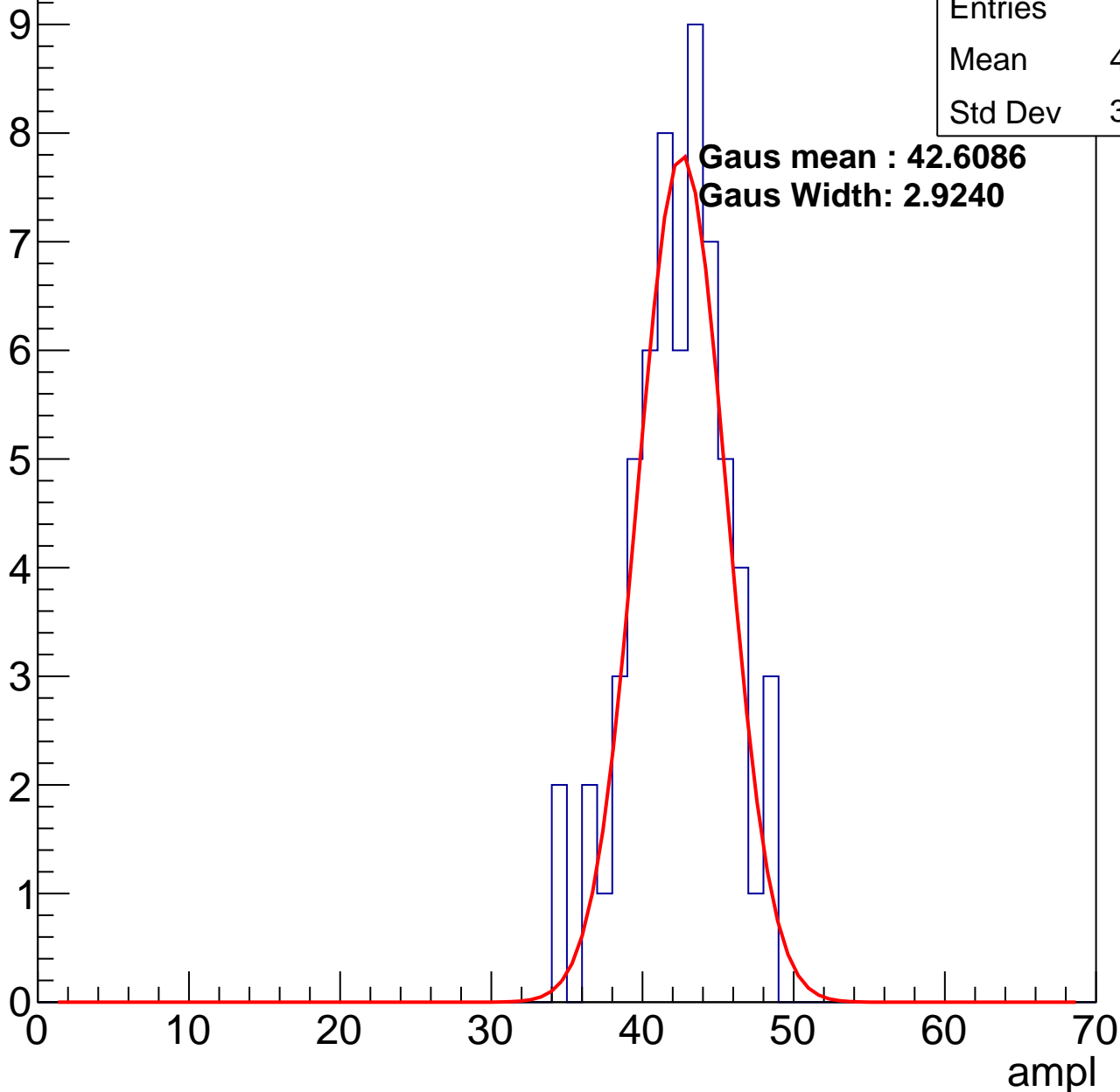
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.95
Std Dev	3.185

**Gaus mean : 42.6086**

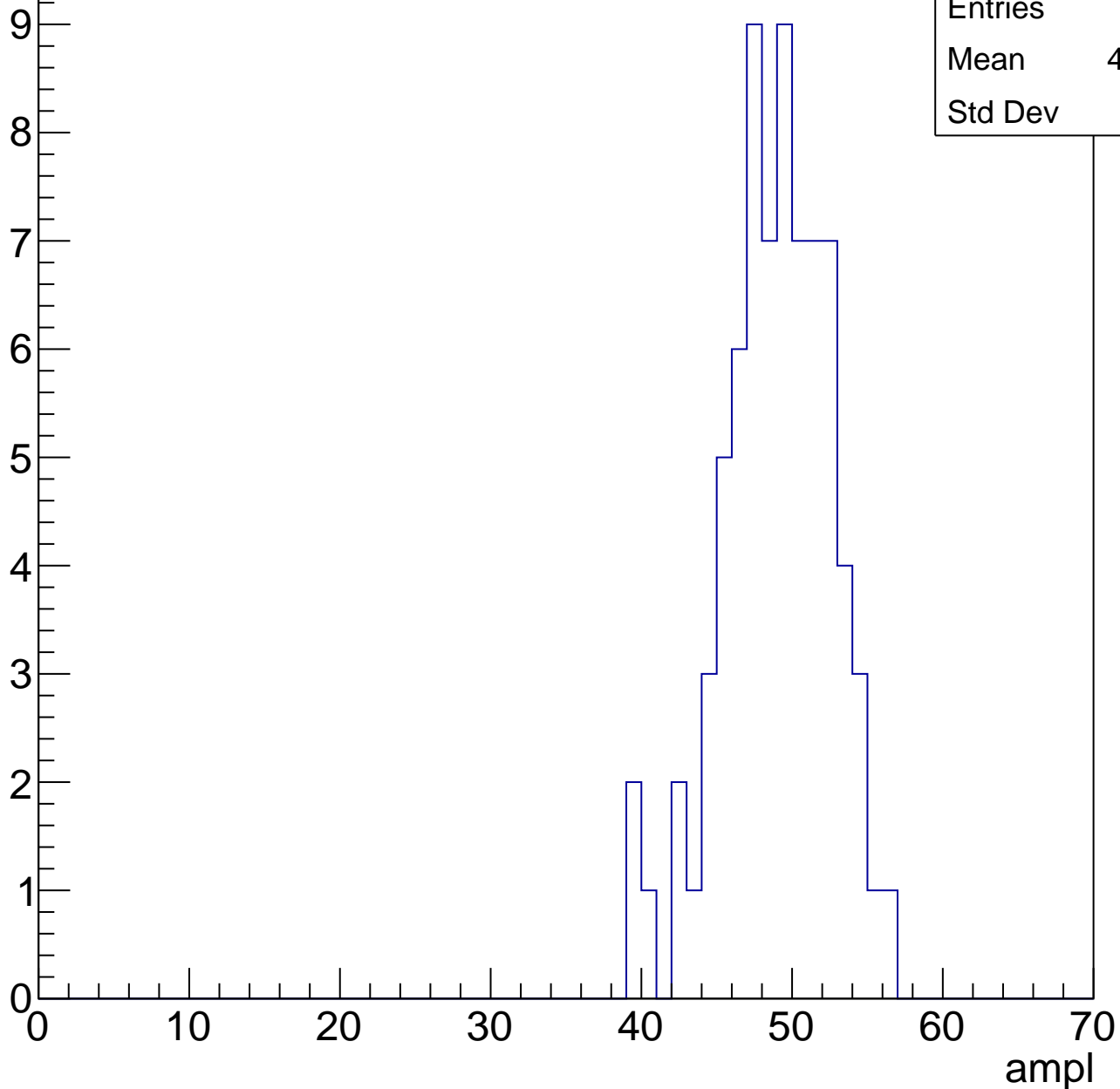
**Gaus Width: 2.9240**



# B1L000S, U21-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



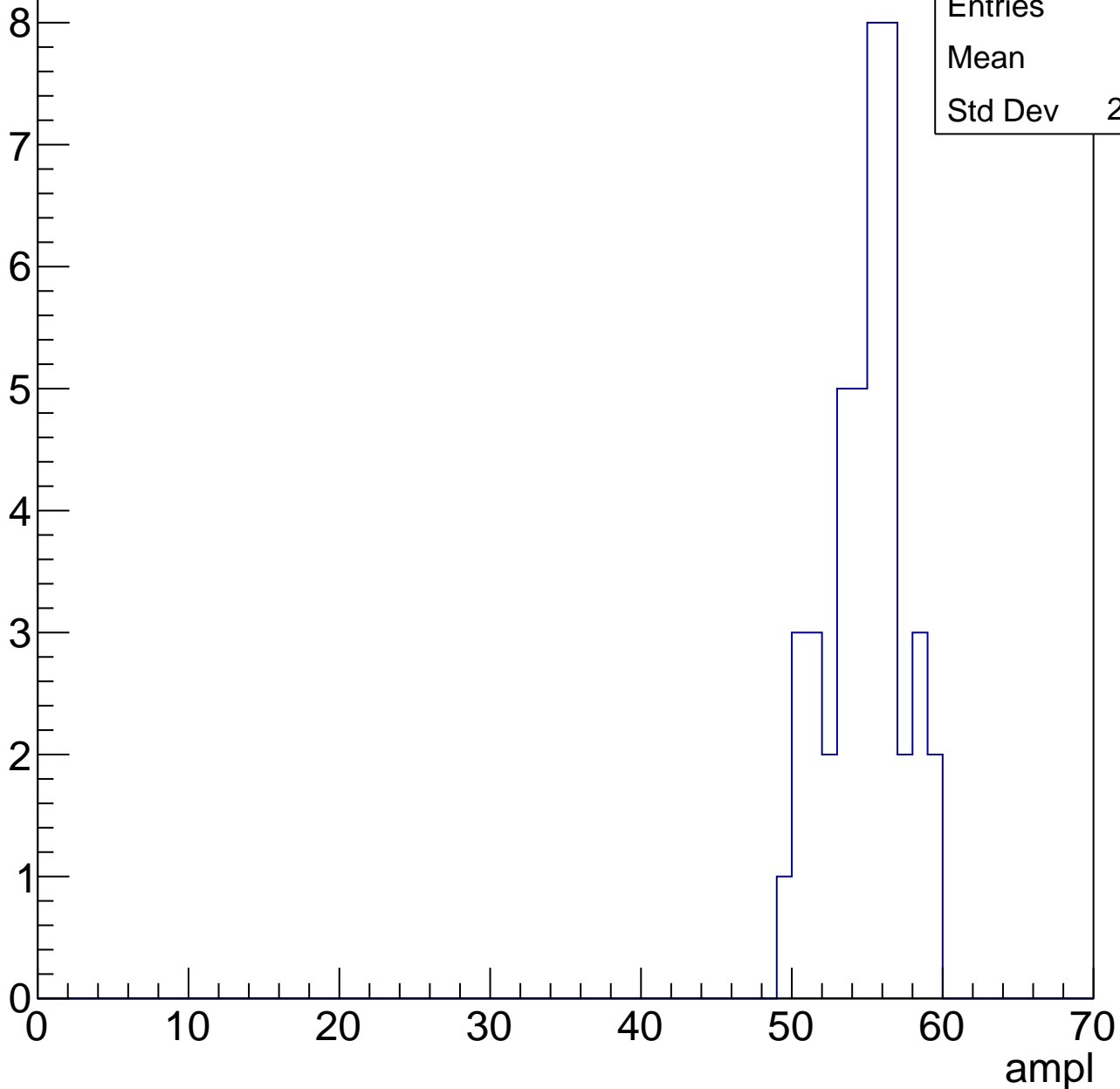
Entries	75
Mean	48.45
Std Dev	3.59

# B1L000S, U21-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	54.4
Std Dev	2.498



# B1L000S, U21-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	58.37
Std Dev	8.027

Entry

10

8

6

4

2

0

0

10

20

30

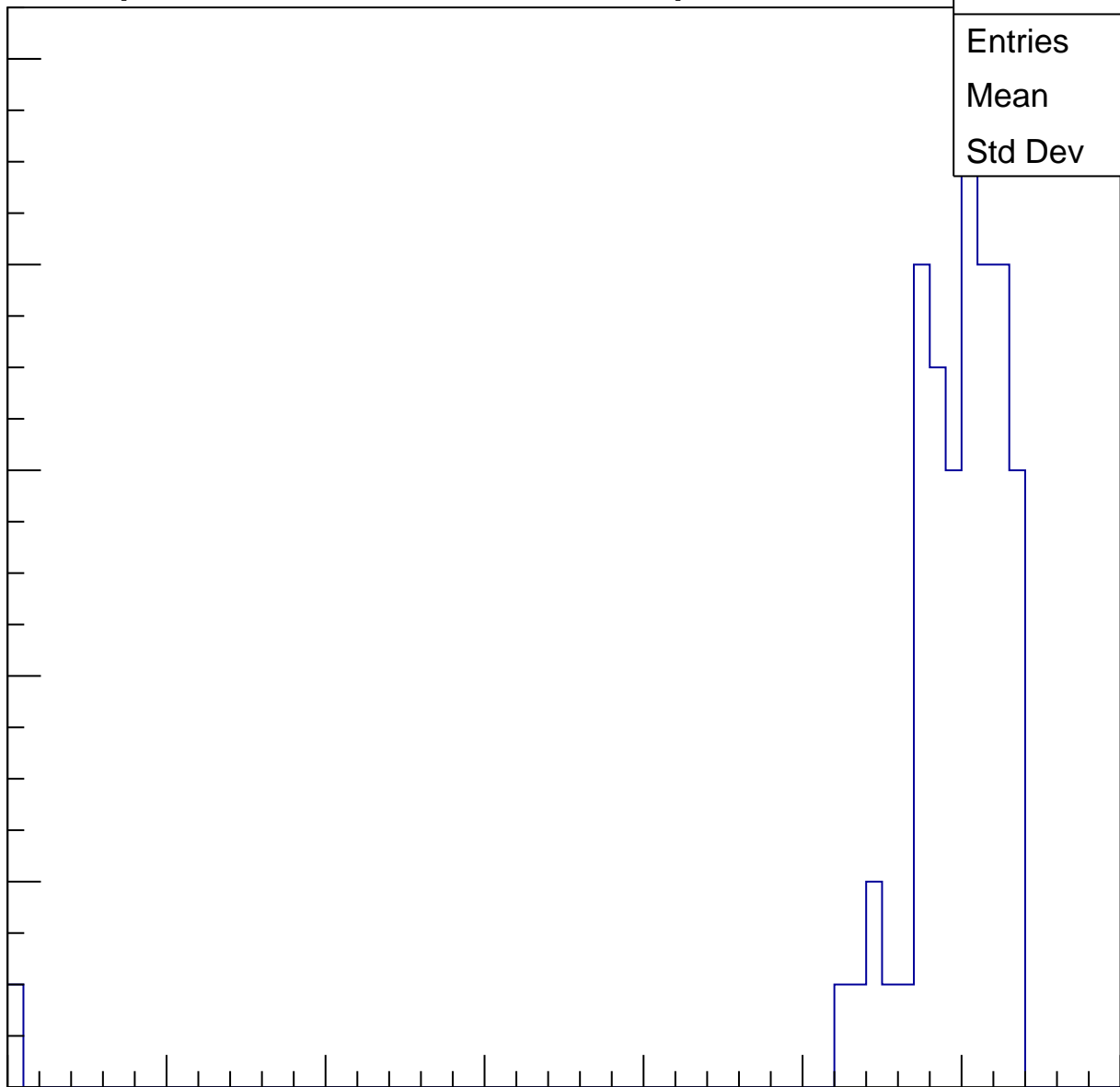
40

50

60

70

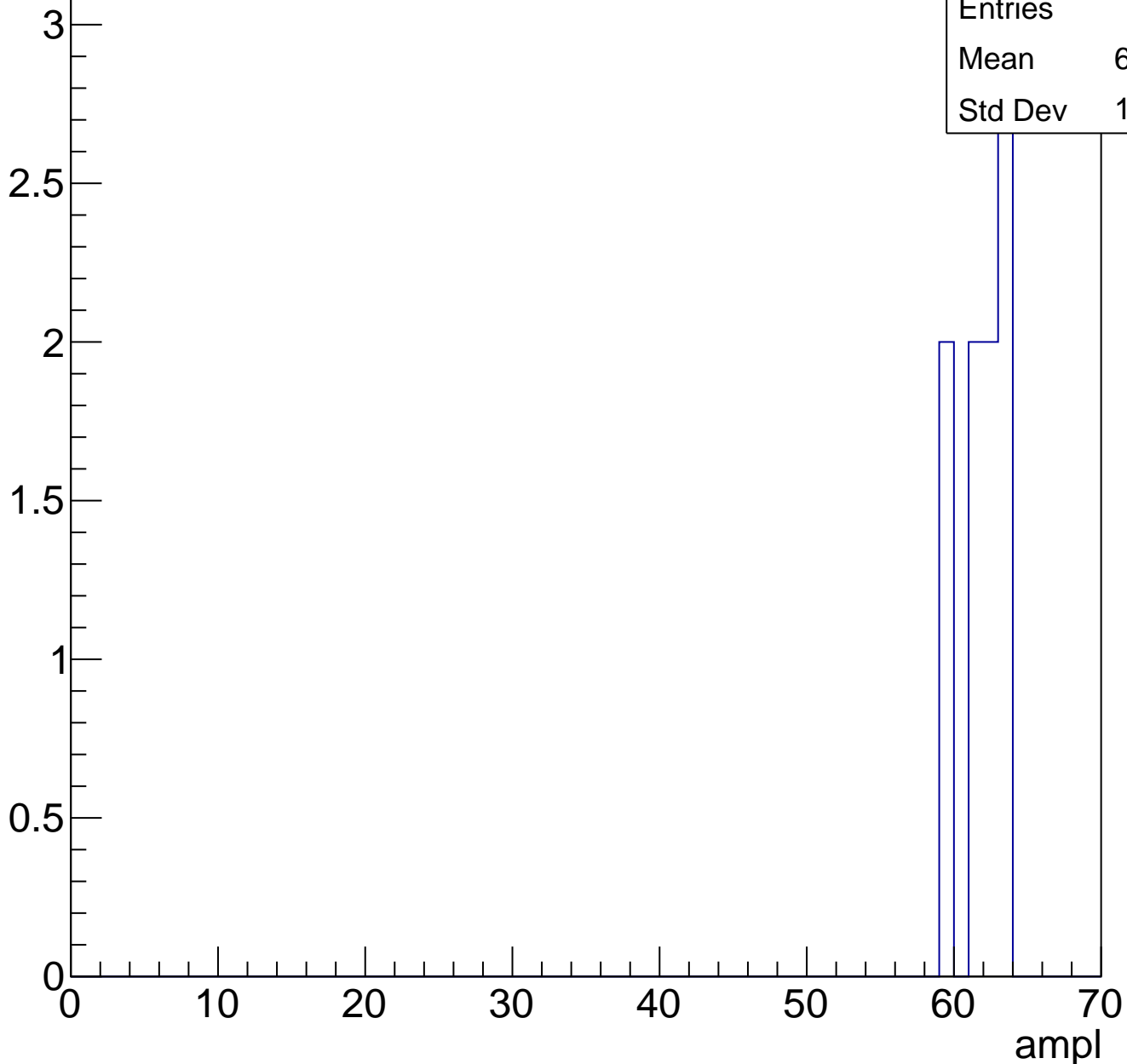
ampl



# B1L000S, U21-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

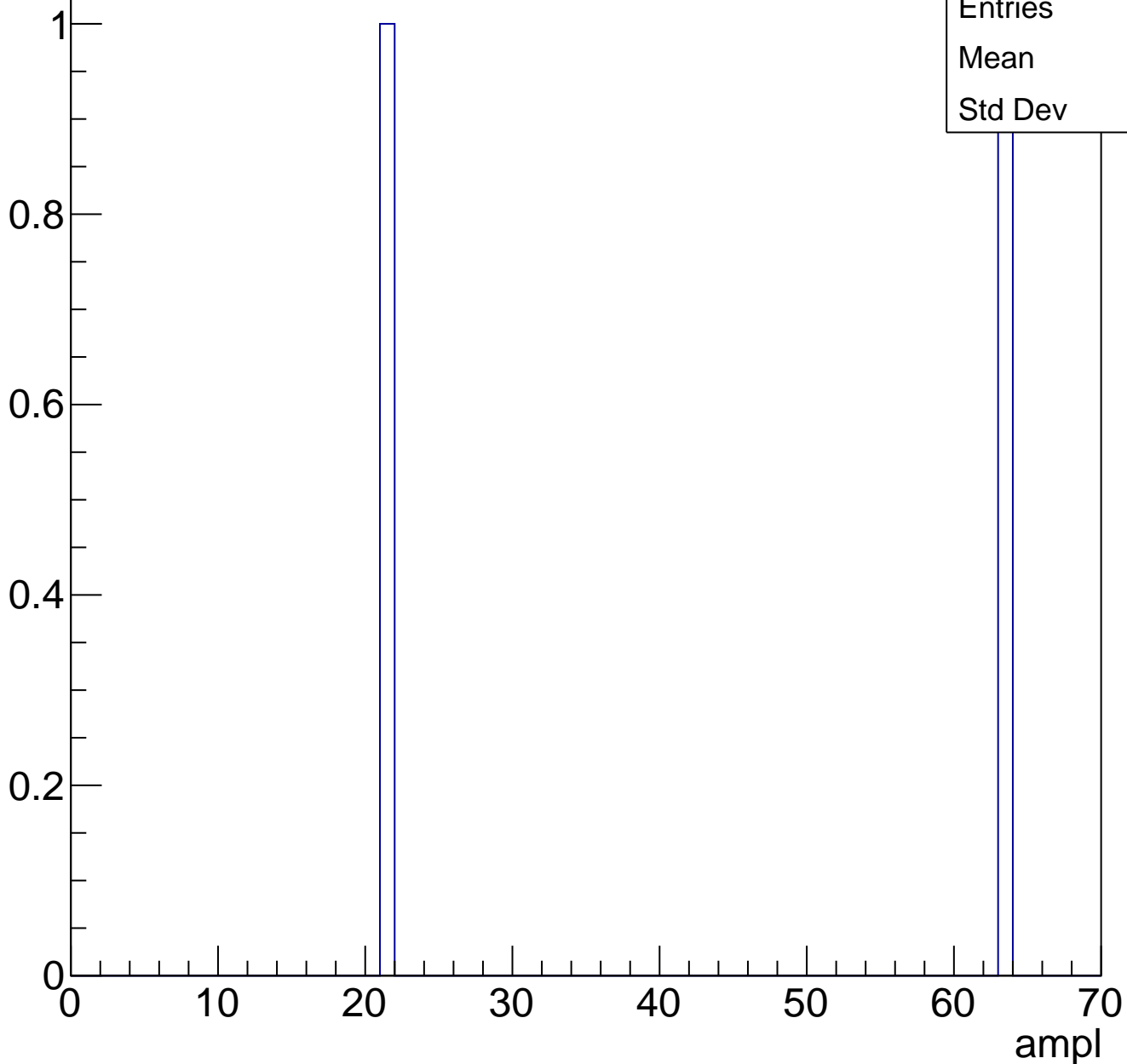




# B1L000S, U21-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch69, adc0

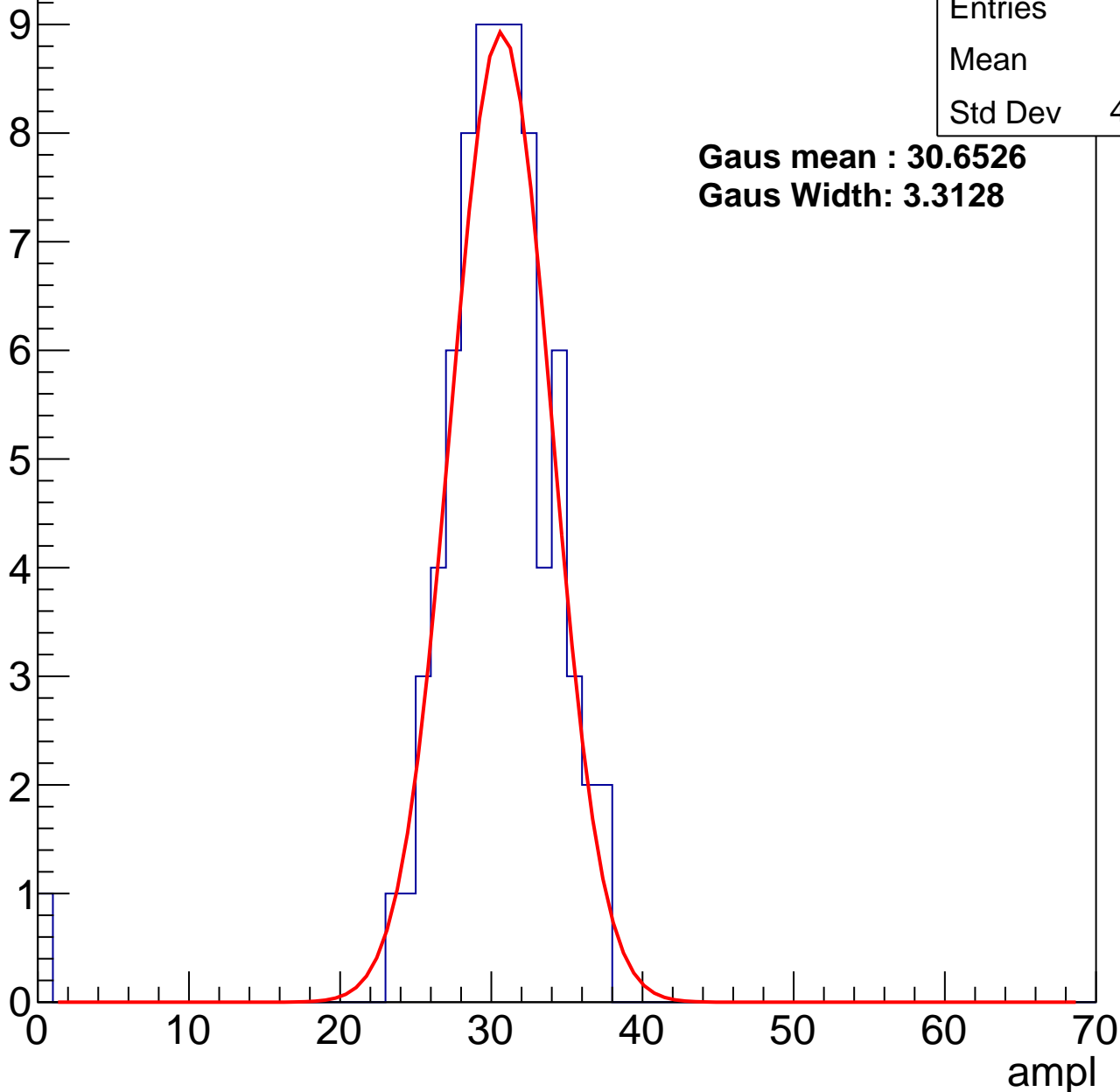
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	29.8
Std Dev	4.637

**Gaus mean : 30.6526**

**Gaus Width: 3.3128**



# B1L000S, U21-ch69, adc1

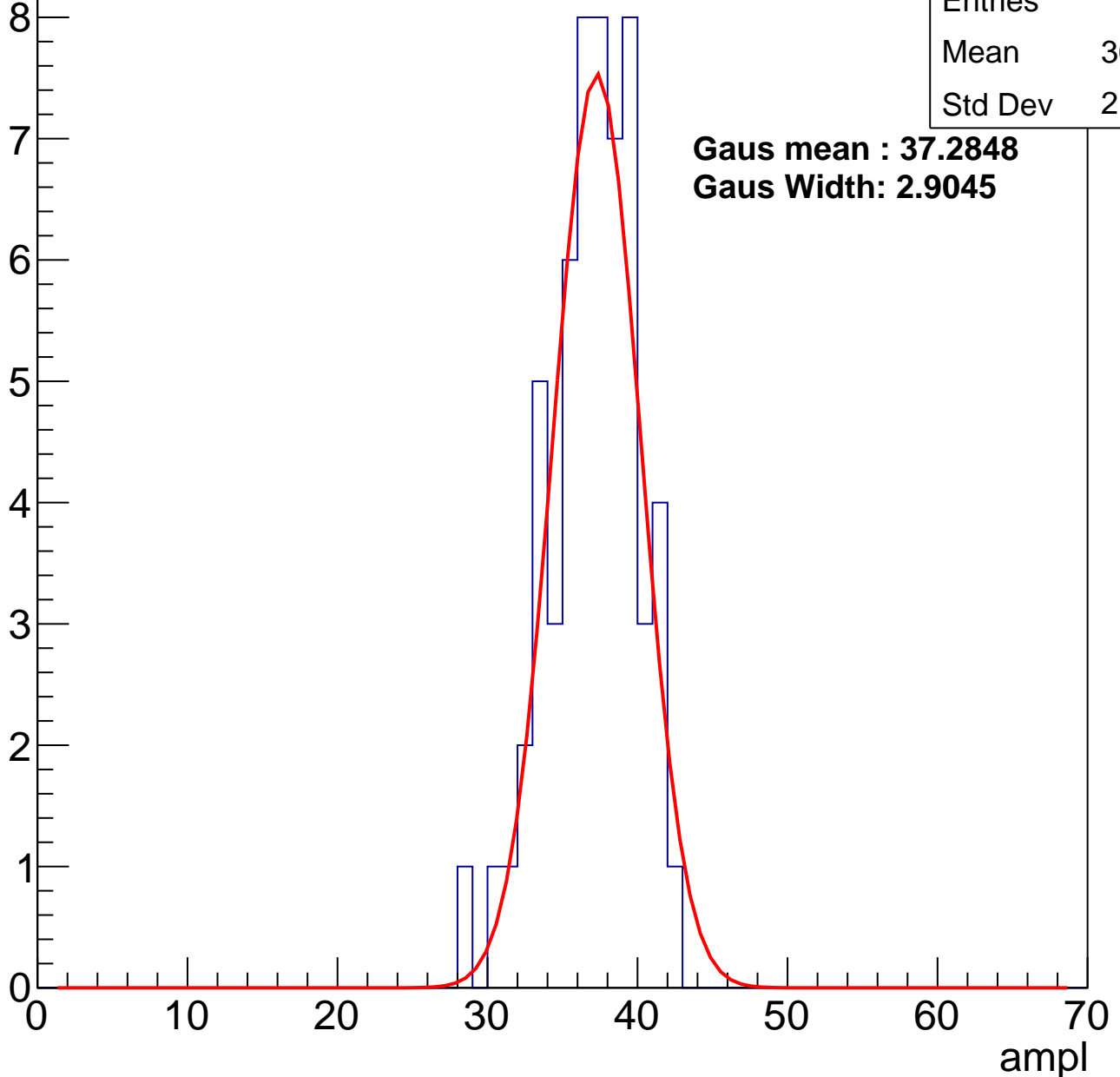
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	36.52
Std Dev	2.926

**Gaus mean : 37.2848**

**Gaus Width: 2.9045**



# B1L000S, U21-ch69, adc2

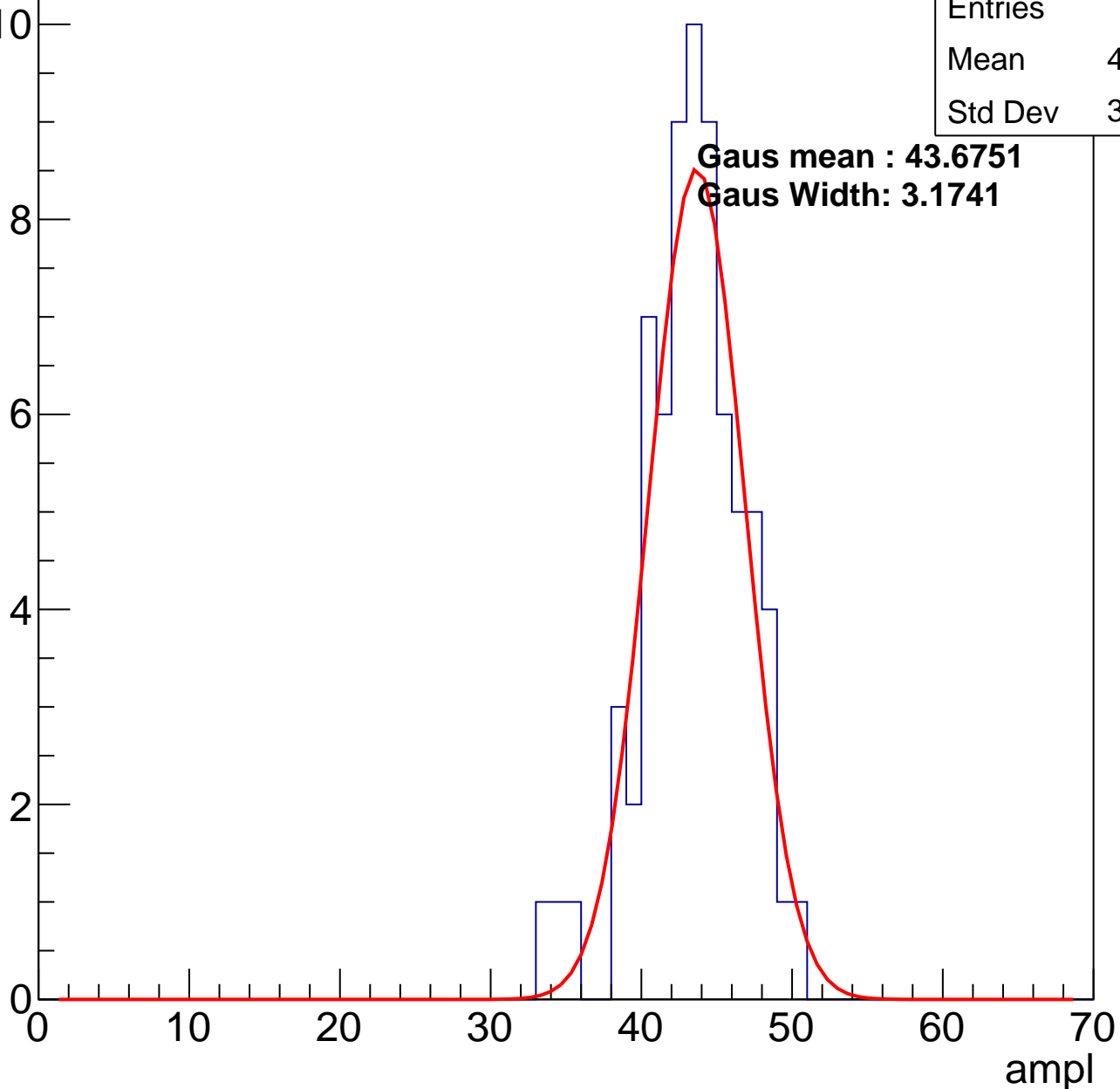
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	42.96
Std Dev	3.338

**Gaus mean : 43.6751**

**Gaus Width: 3.1741**

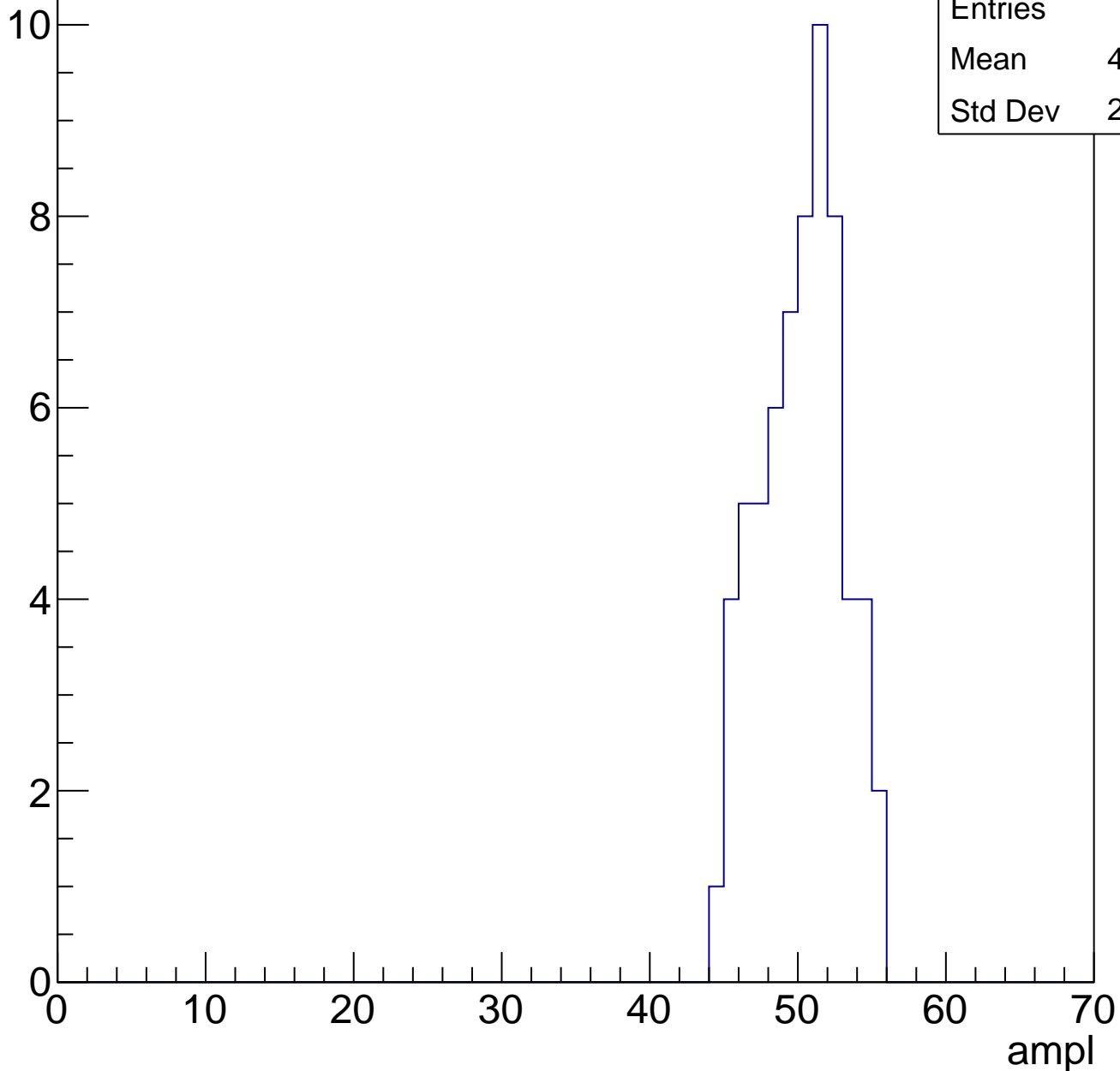


# B1L000S, U21-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	49.75
Std Dev	2.739

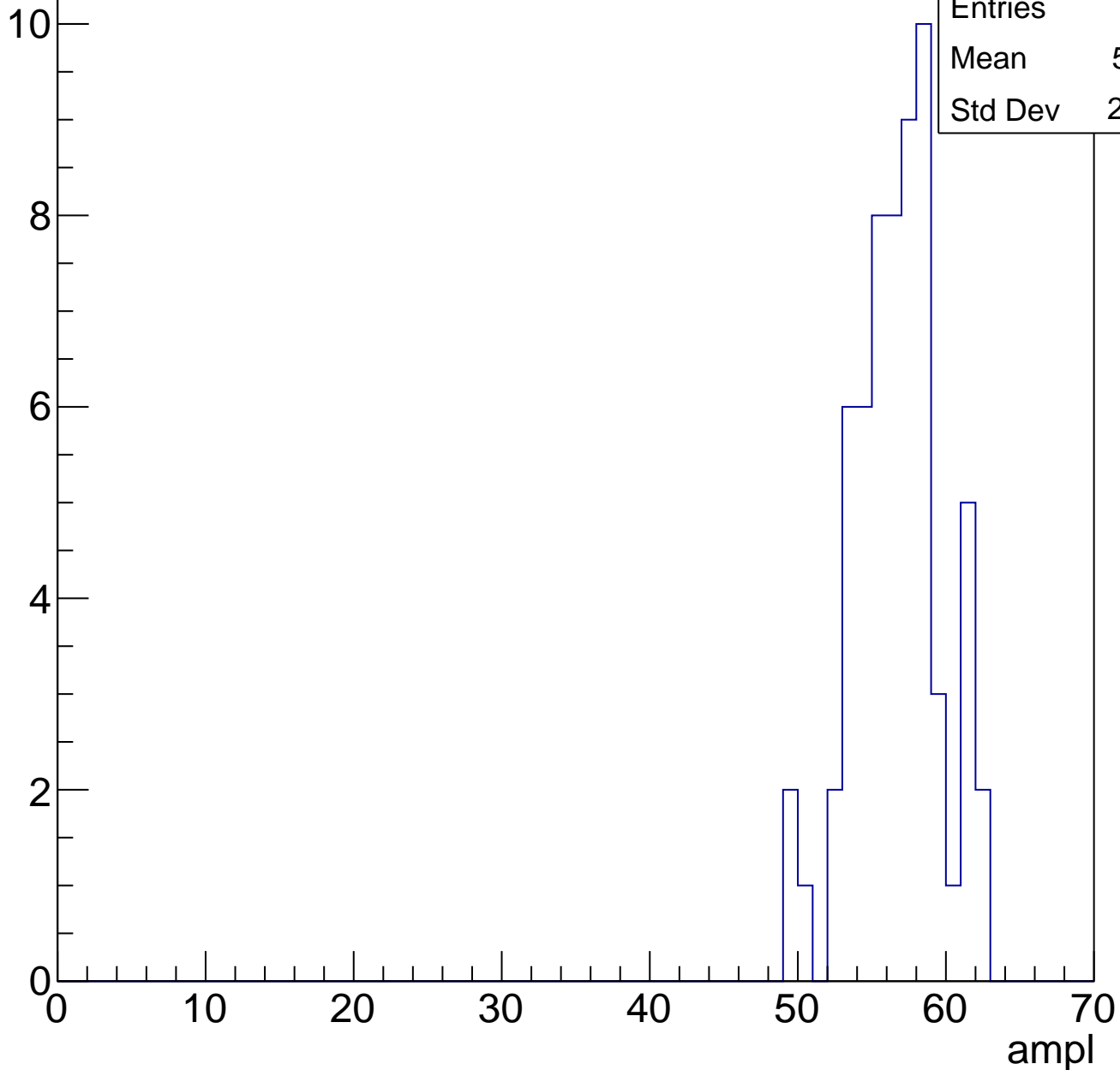


# B1L000S, U21-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	56.21
Std Dev	2.923

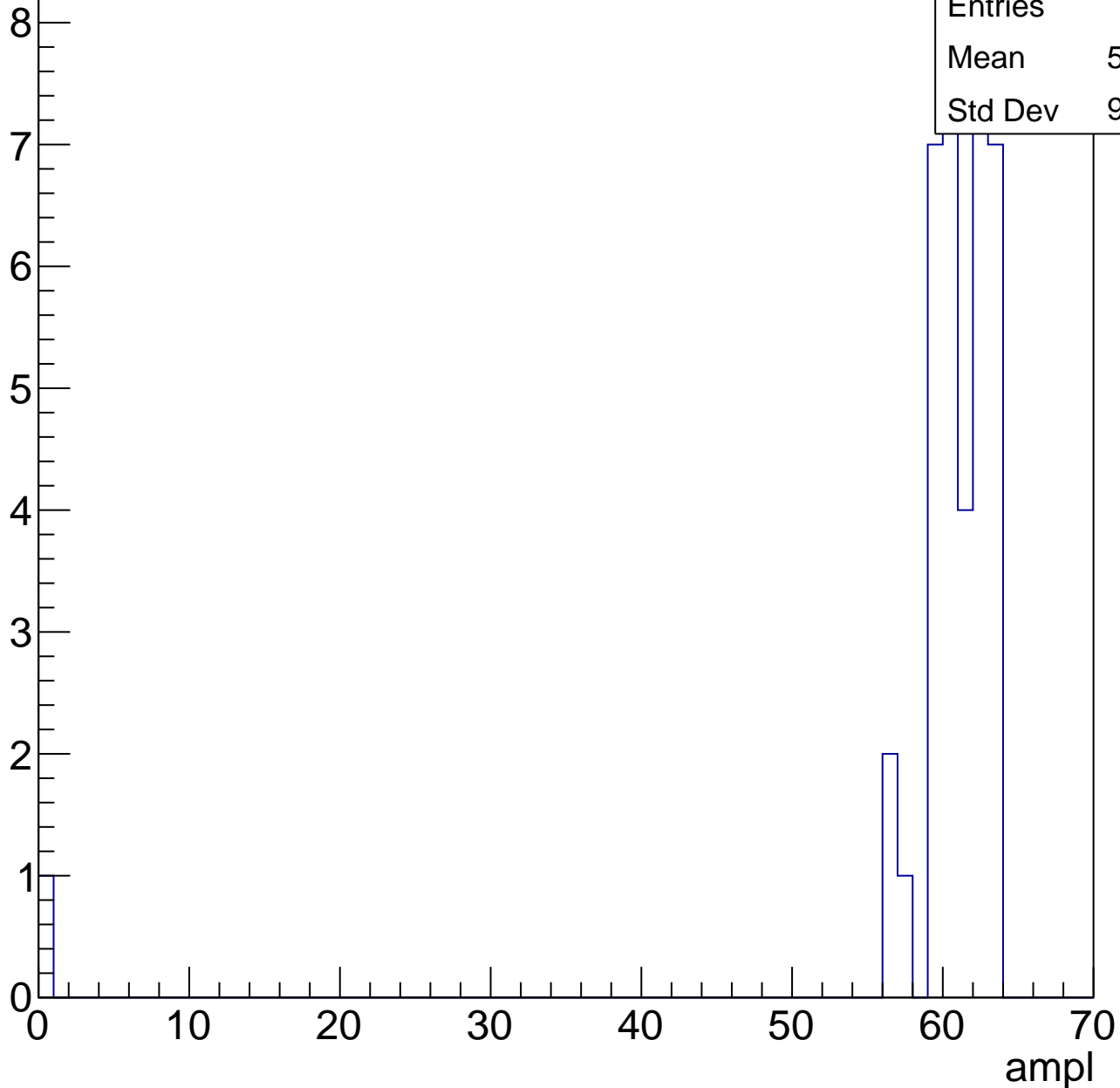
Entry



# B1L000S, U21-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

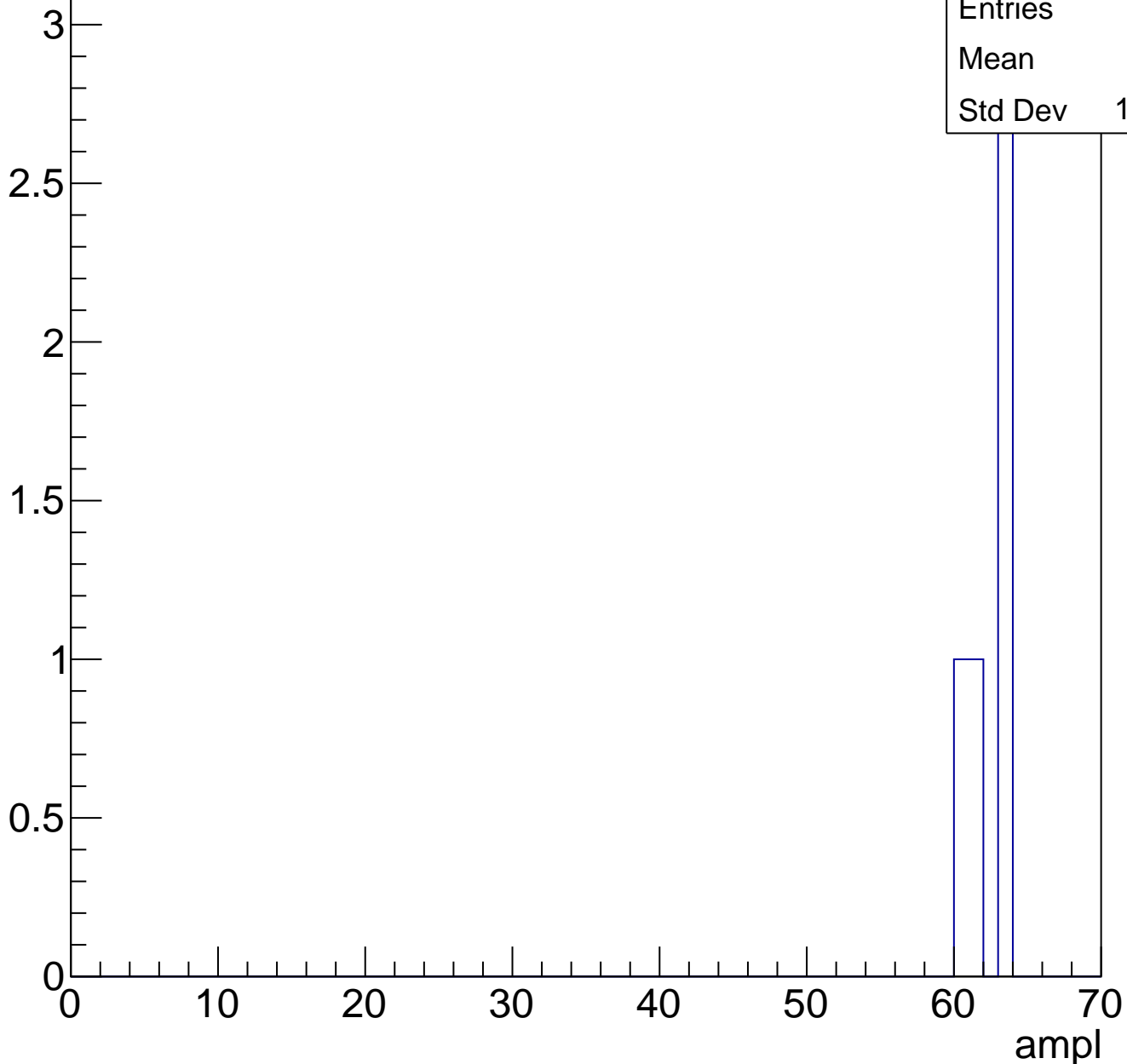
Entry



# B1L000S, U21-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B1L000S, U21-ch70, adc0

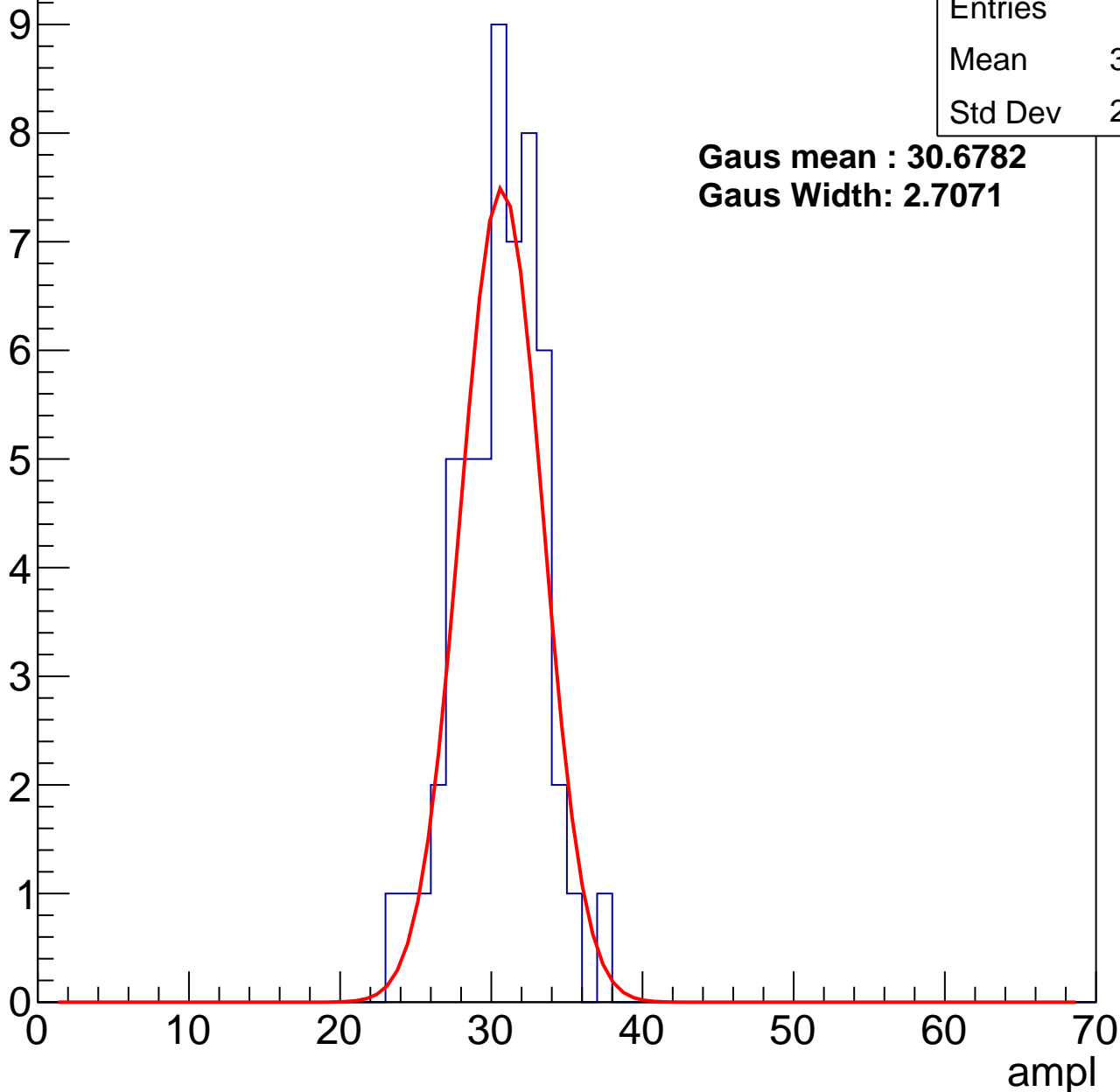
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	30.09
Std Dev	2.757

**Gaus mean : 30.6782**

**Gaus Width: 2.7071**



# B1L000S, U21-ch70, adc1

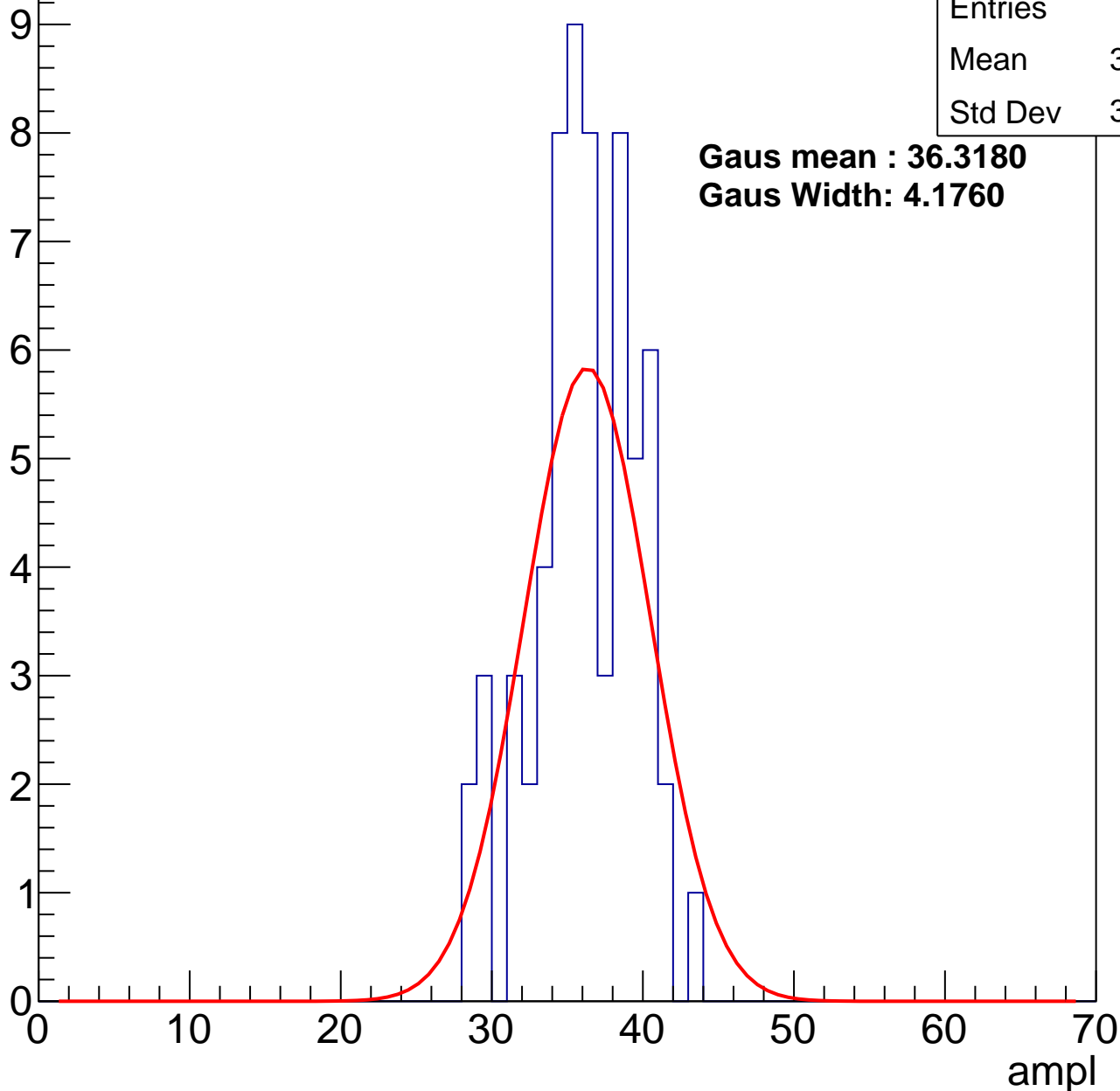
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.66
Std Dev	3.369

**Gaus mean : 36.3180**

**Gaus Width: 4.1760**



# B1L000S, U21-ch70, adc2

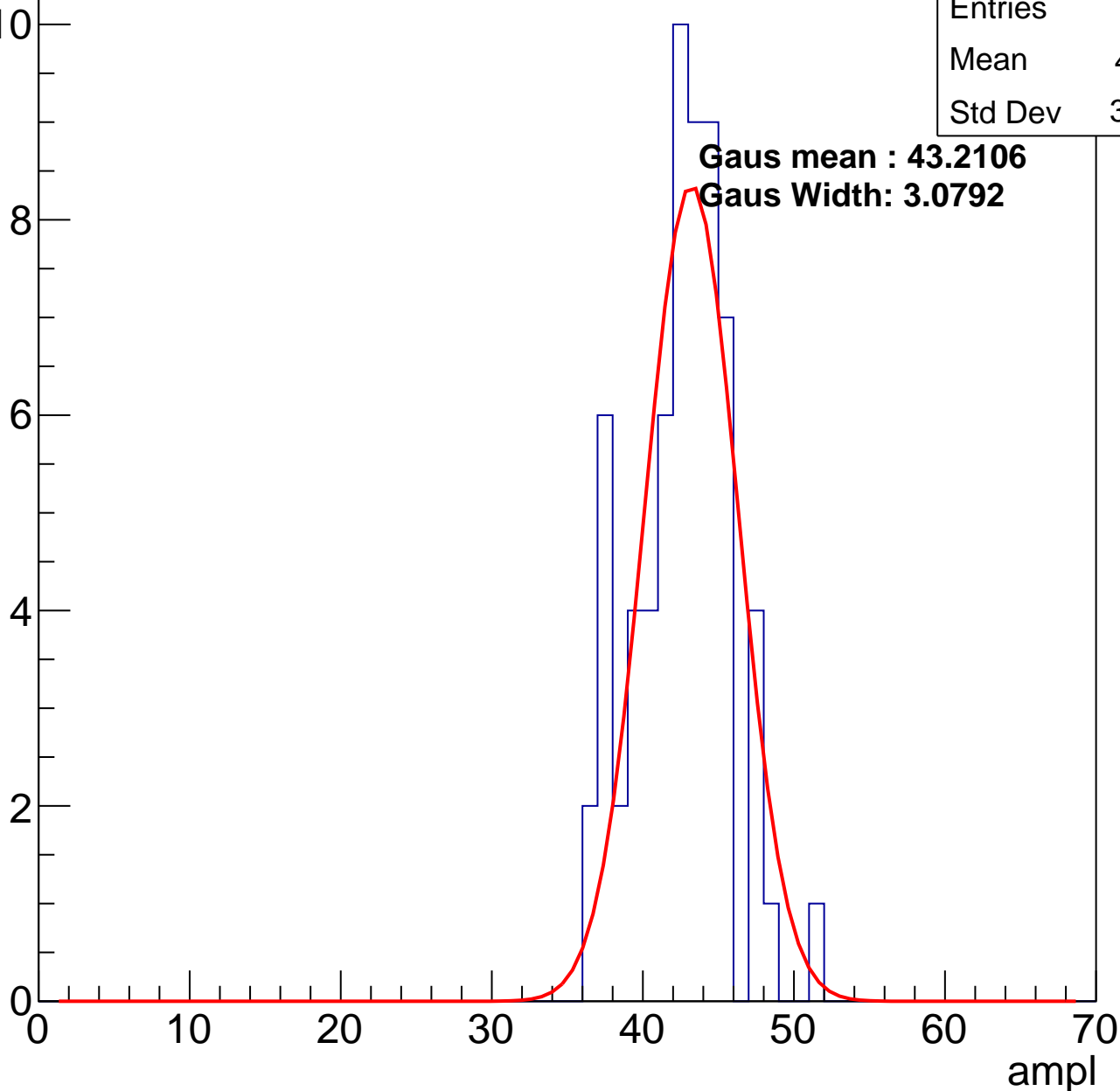
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	42.11
Std Dev	3.129

**Gaus mean : 43.2106**

**Gaus Width: 3.0792**



# B1L000S, U21-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

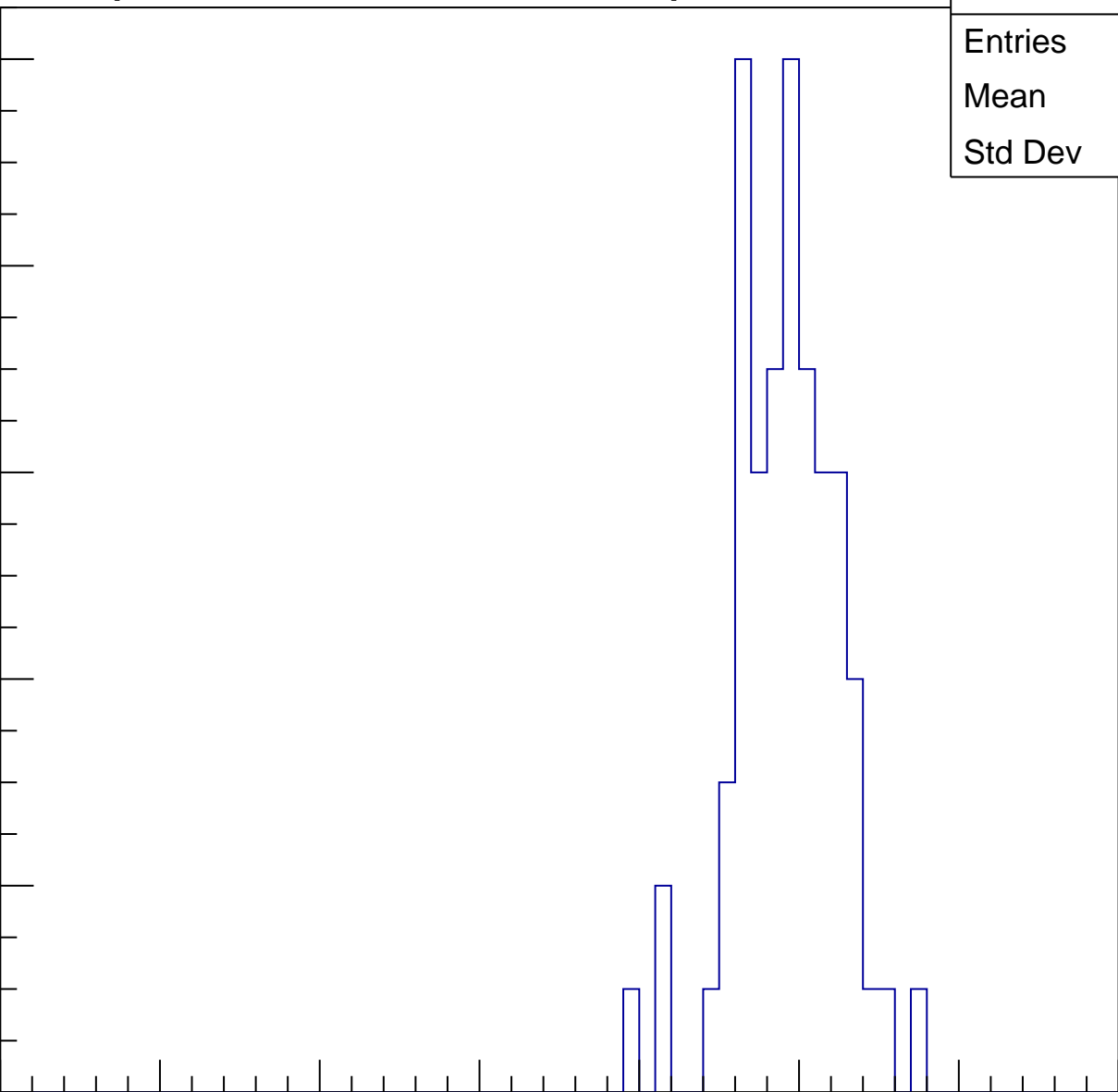
Entries	66
Mean	48.7
Std Dev	3.233

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

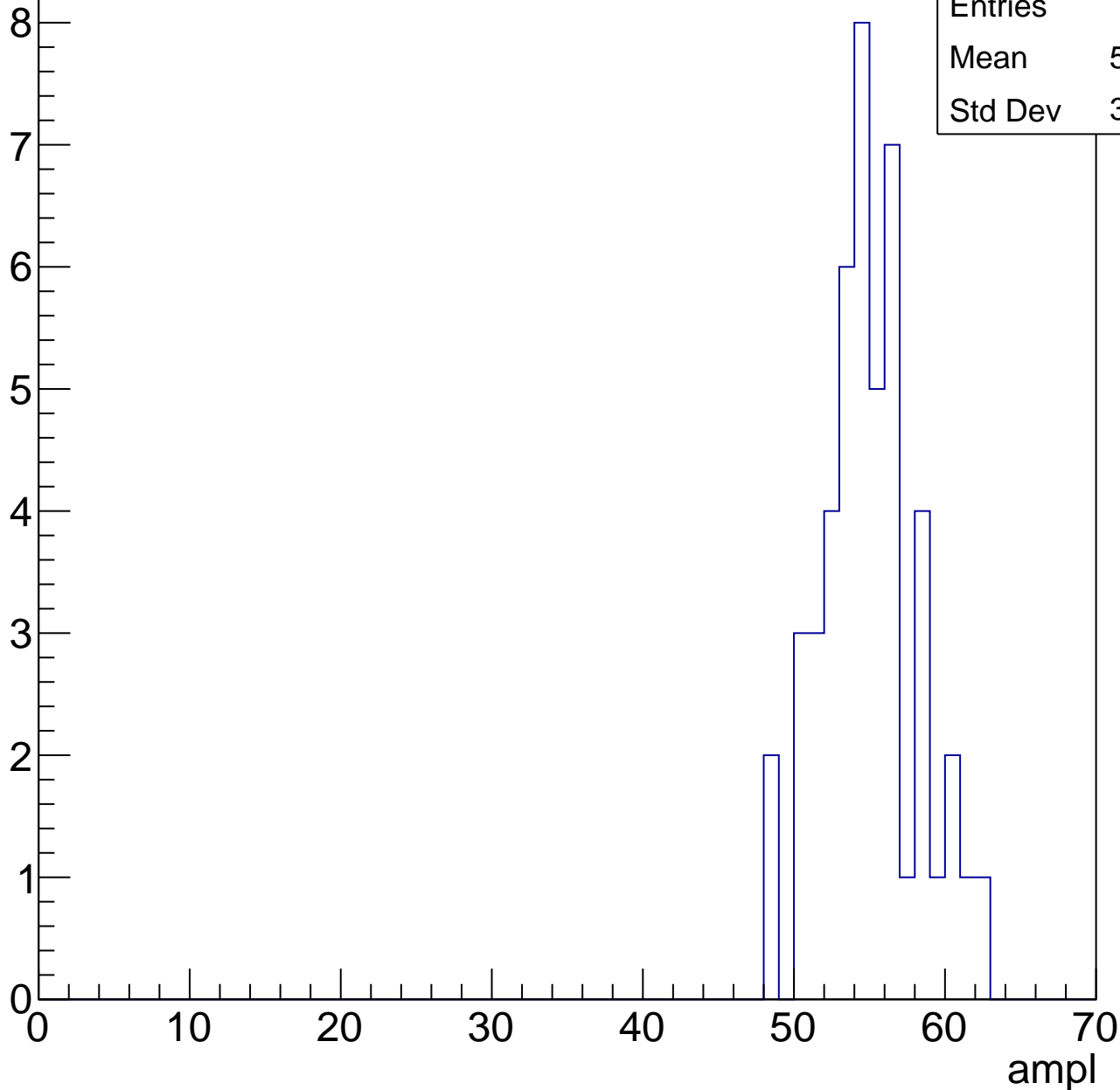


# B1L000S, U21-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	54.48
Std Dev	3.142

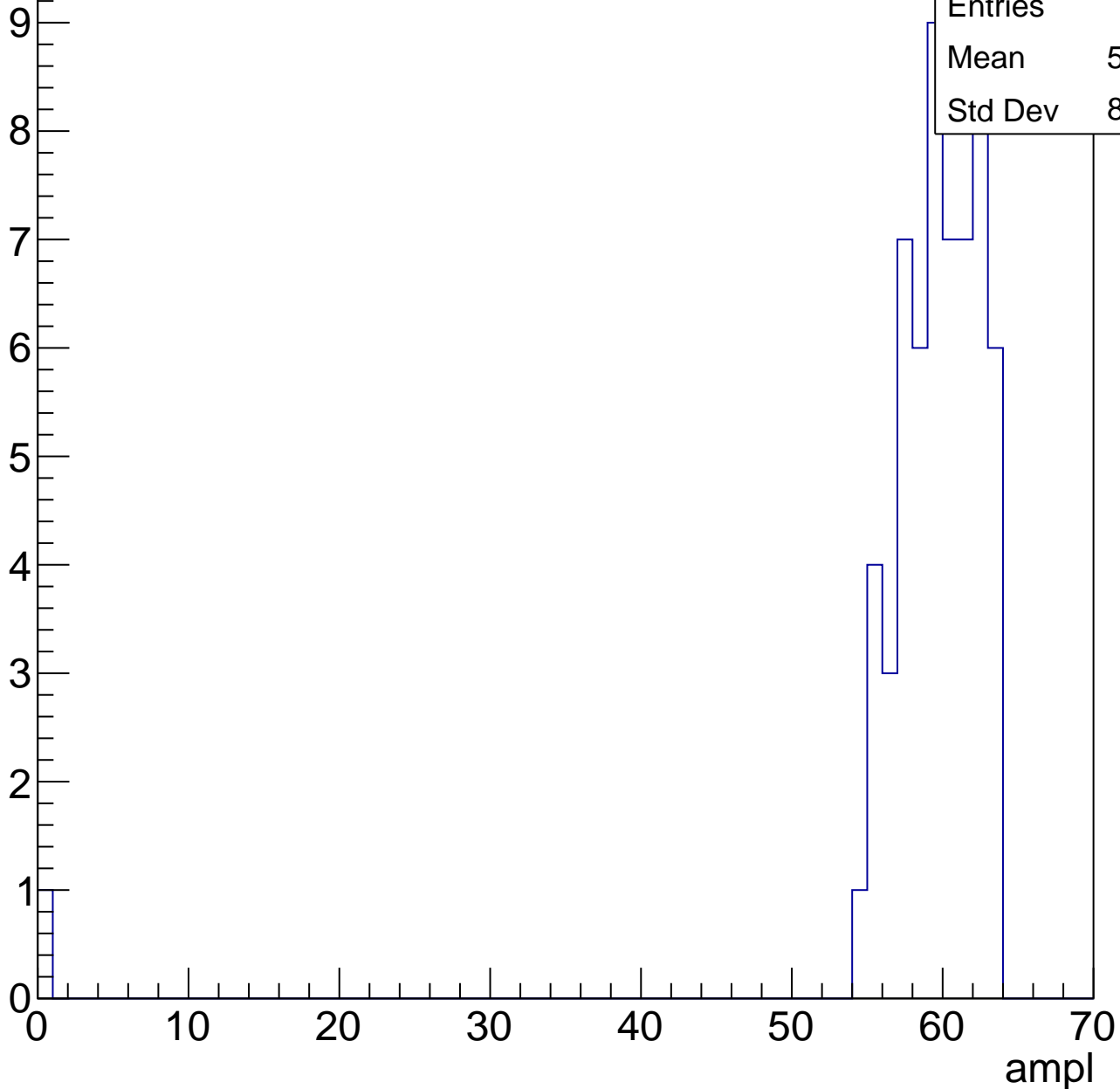


# B1L000S, U21-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

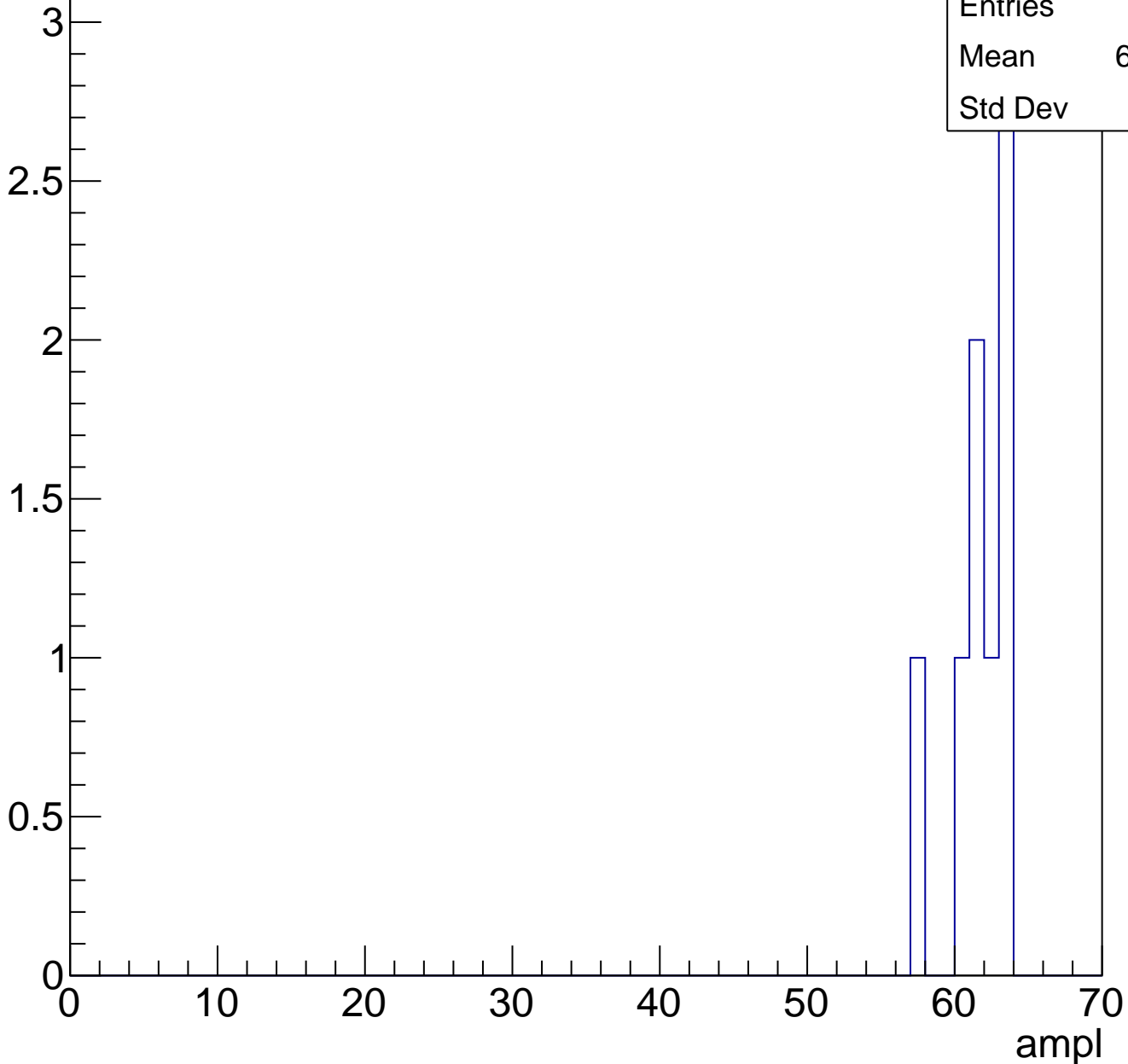
Entries	59
Mean	58.32
Std Dev	8.033



# B1L000S, U21-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

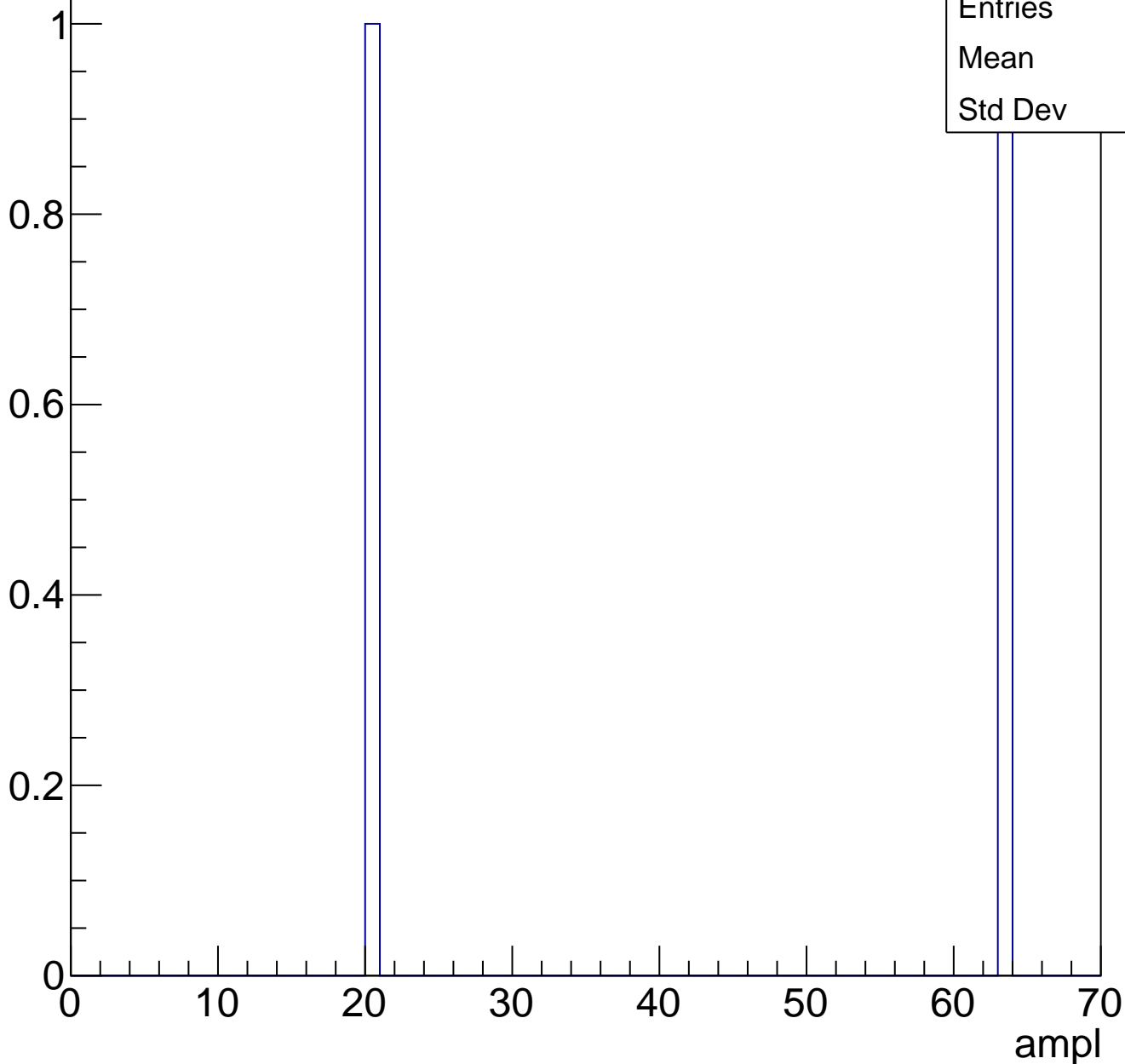




# B1L000S, U21-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch71, adc0

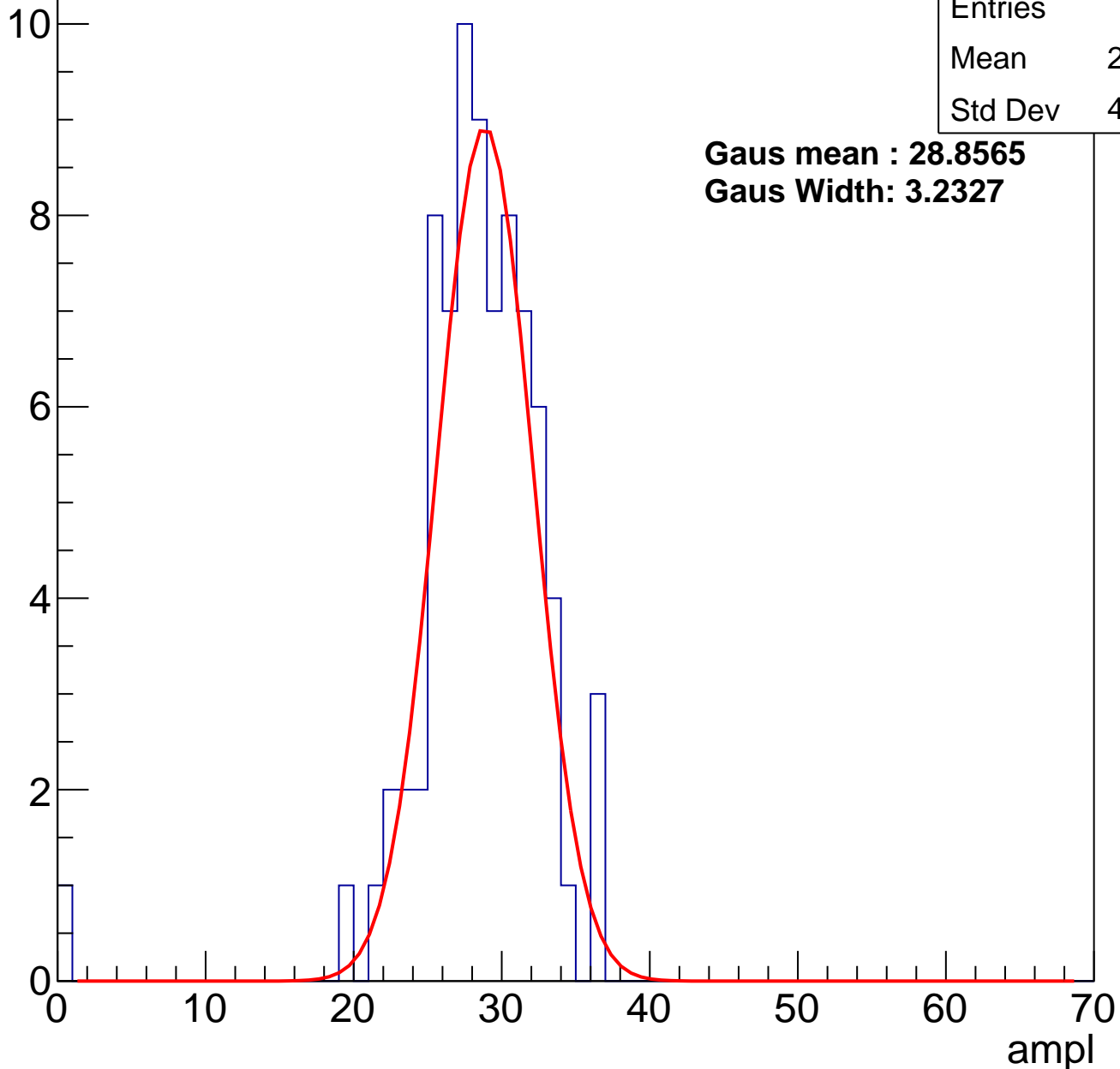
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	27.95
Std Dev	4.644

**Gaus mean : 28.8565**

**Gaus Width: 3.2327**

Entry



# B1L000S, U21-ch71, adc1

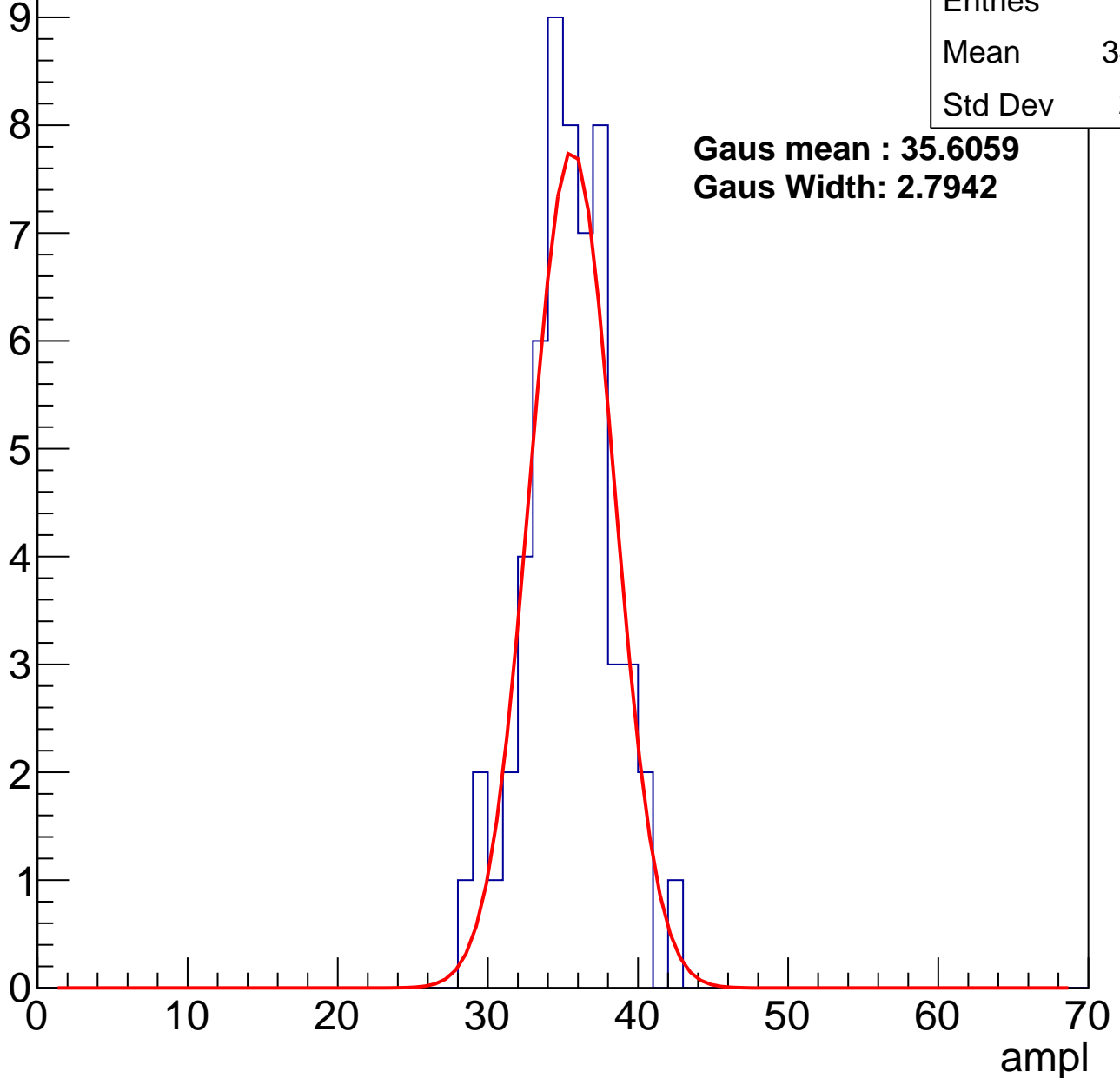
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	34.93
Std Dev	2.84

**Gaus mean : 35.6059**

**Gaus Width: 2.7942**



# B1L000S, U21-ch71, adc2

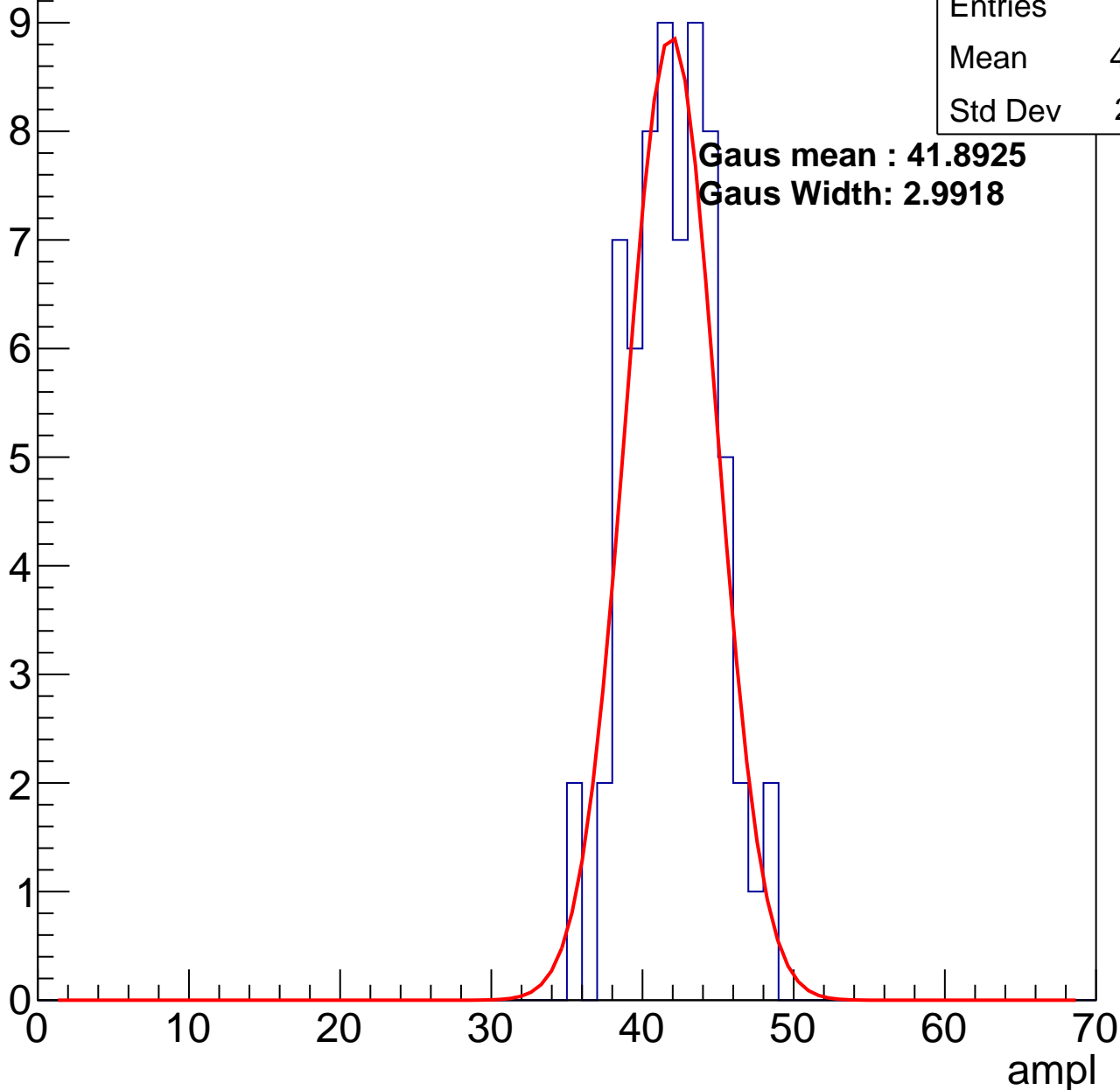
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	41.56
Std Dev	2.861

**Gaus mean : 41.8925**

**Gaus Width: 2.9918**

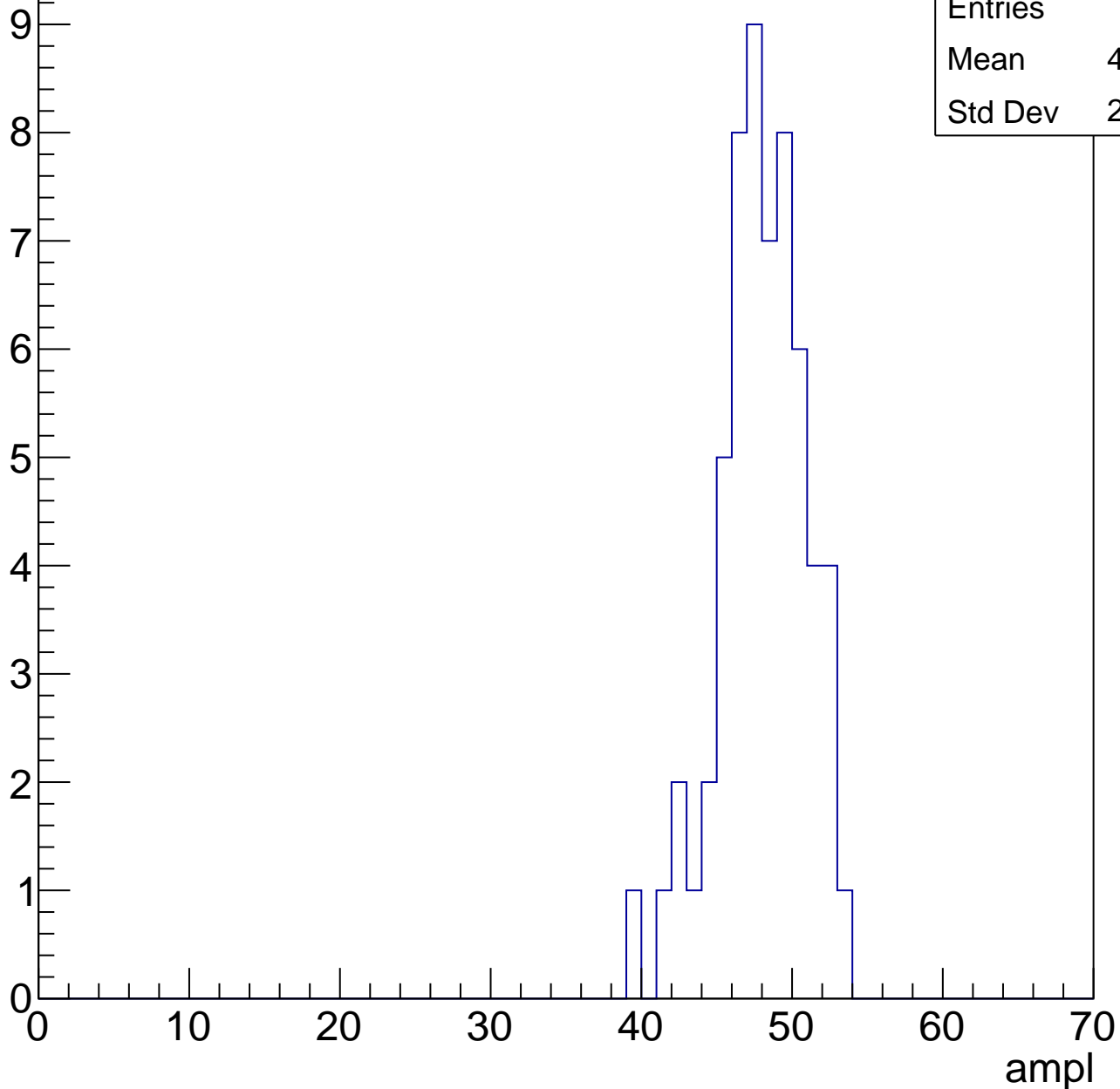


# B1L000S, U21-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

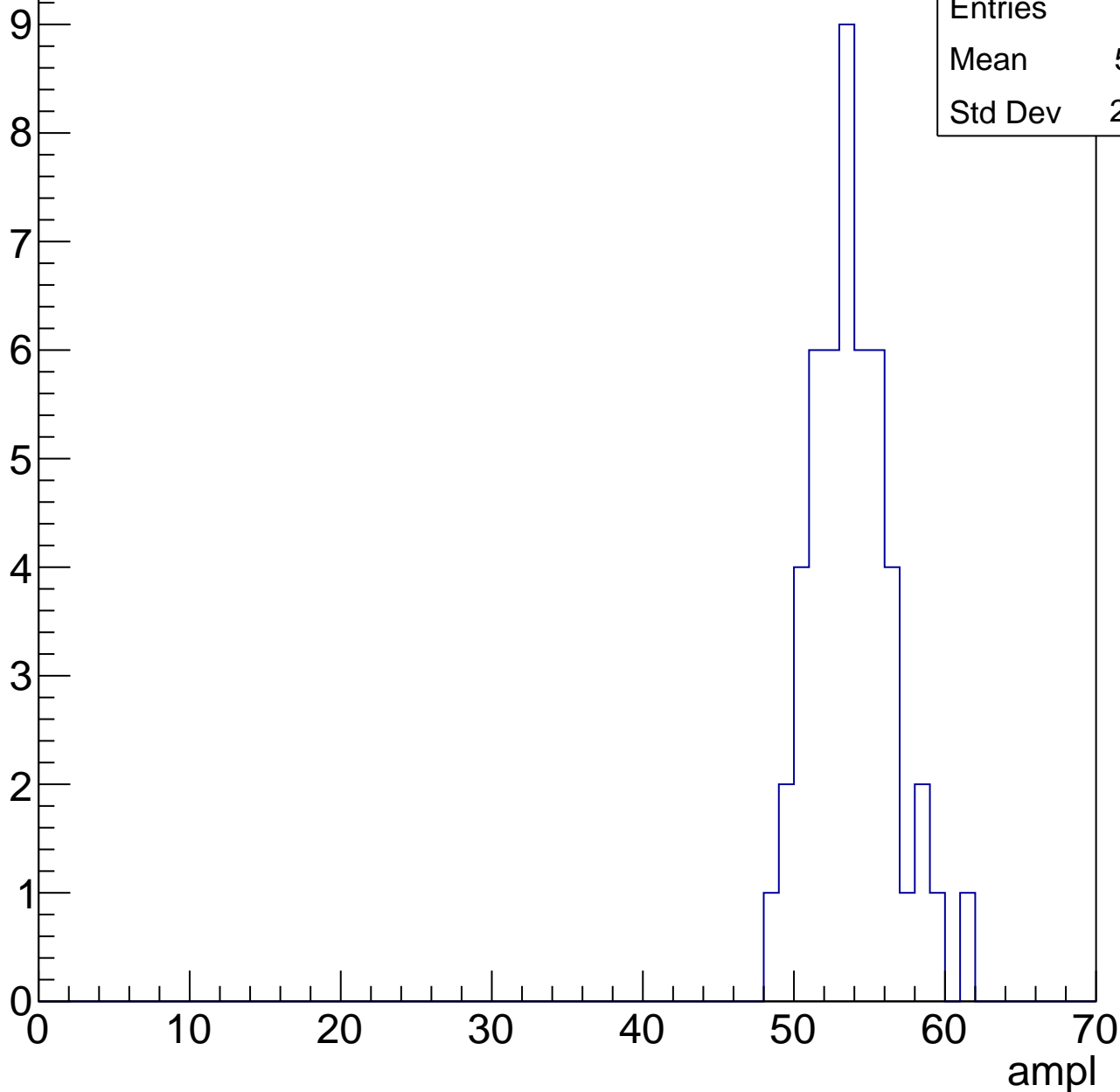
Entries	59
Mean	47.53
Std Dev	2.878



# B1L000S, U21-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

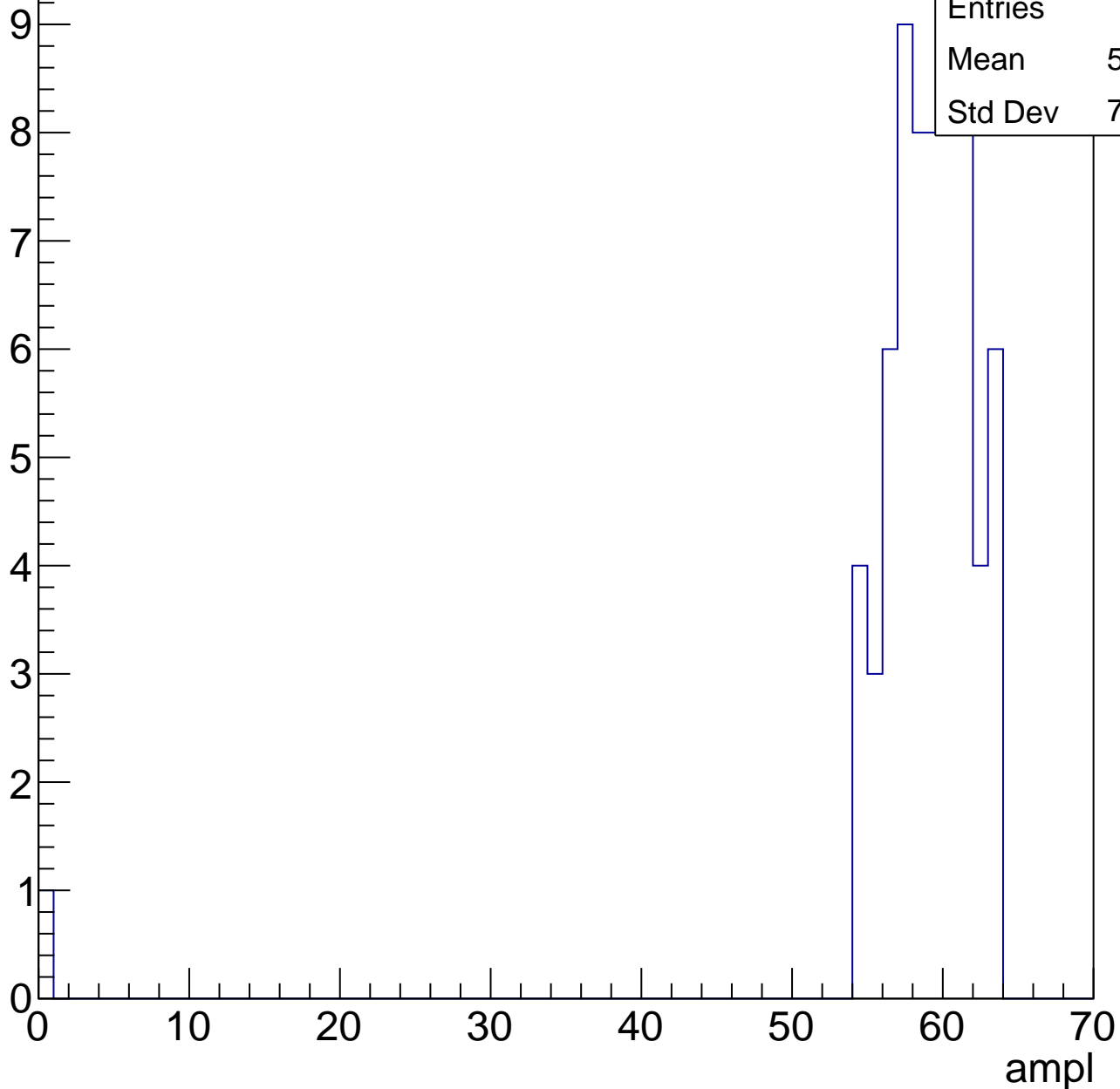


Entries	49
Mean	53.31
Std Dev	2.674

# B1L000S, U21-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

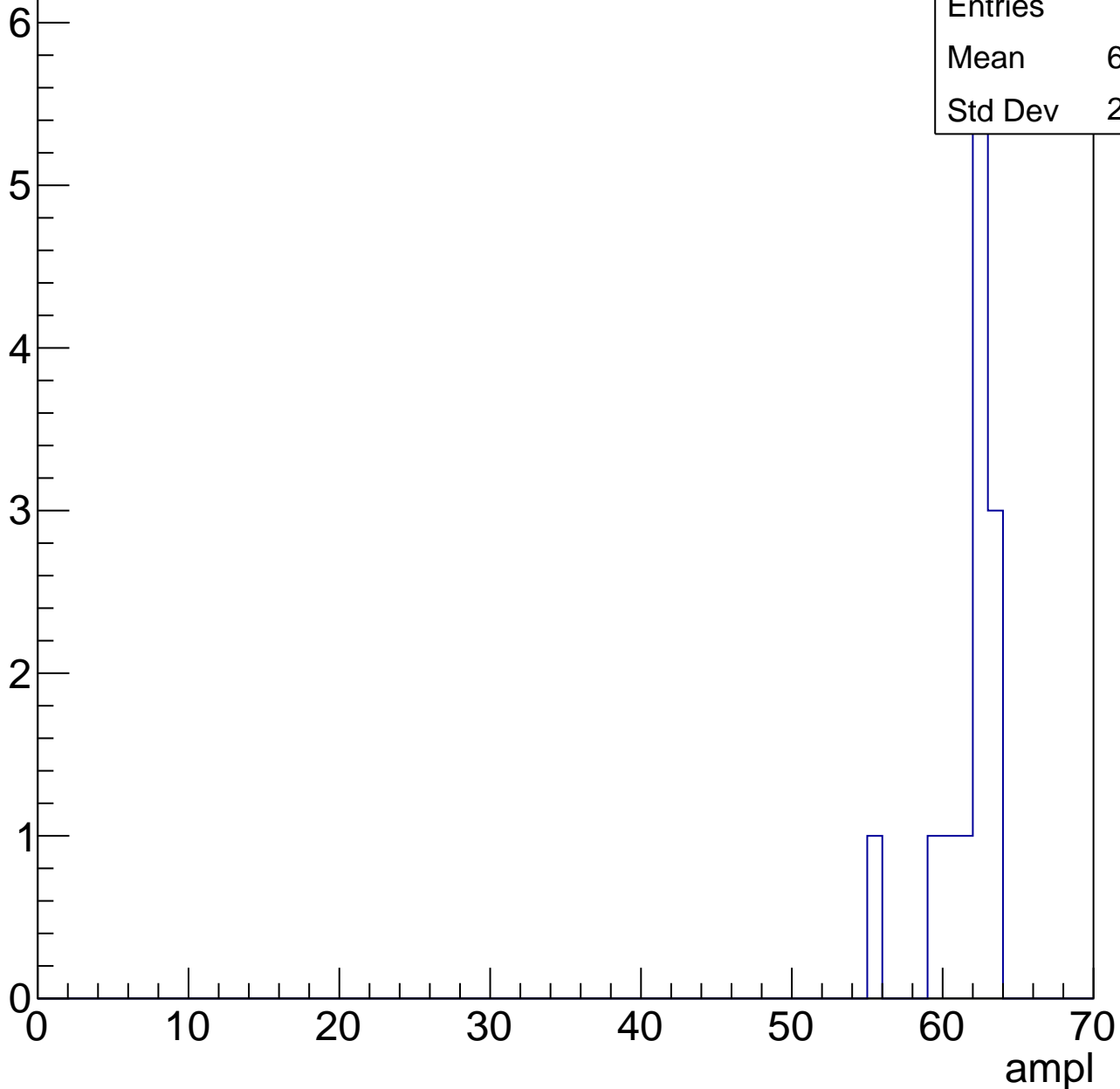


# B1L000S, U21-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	61.23
Std Dev	2.118





# B1L000S, U21-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch72, adc0

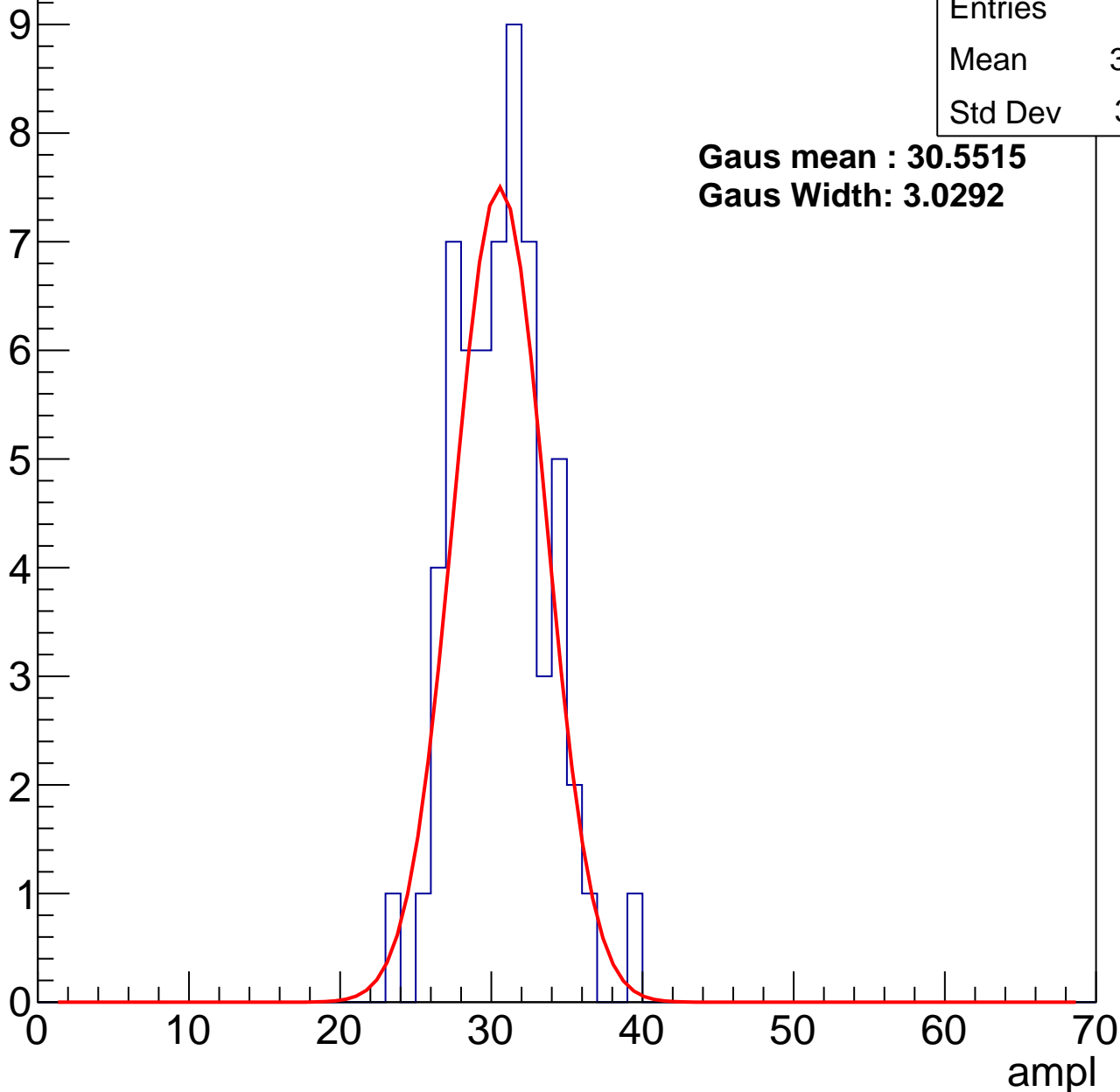
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	30.17
Std Dev	3.001

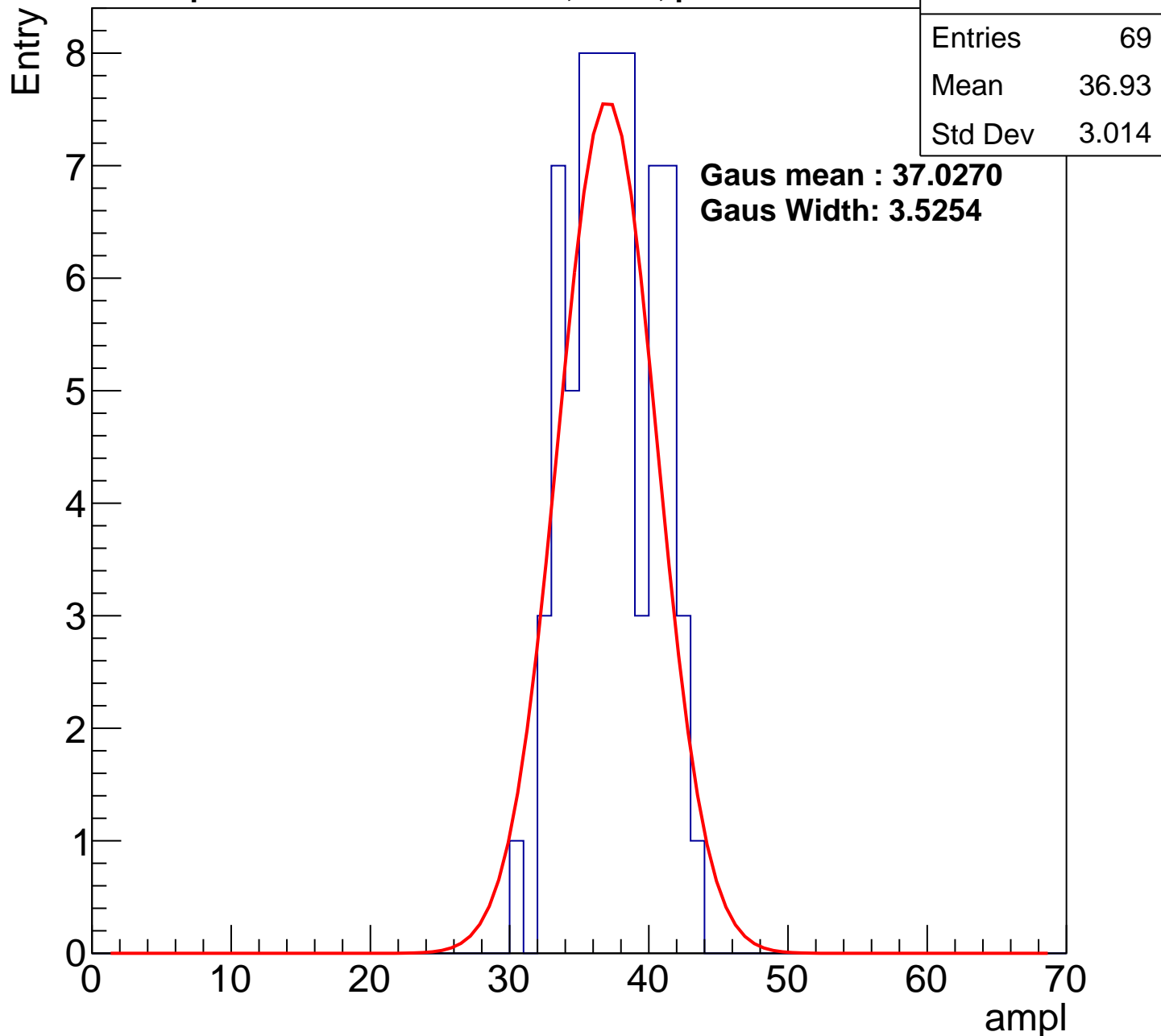
**Gaus mean : 30.5515**

**Gaus Width: 3.0292**



# B1L000S, U21-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch72, adc2

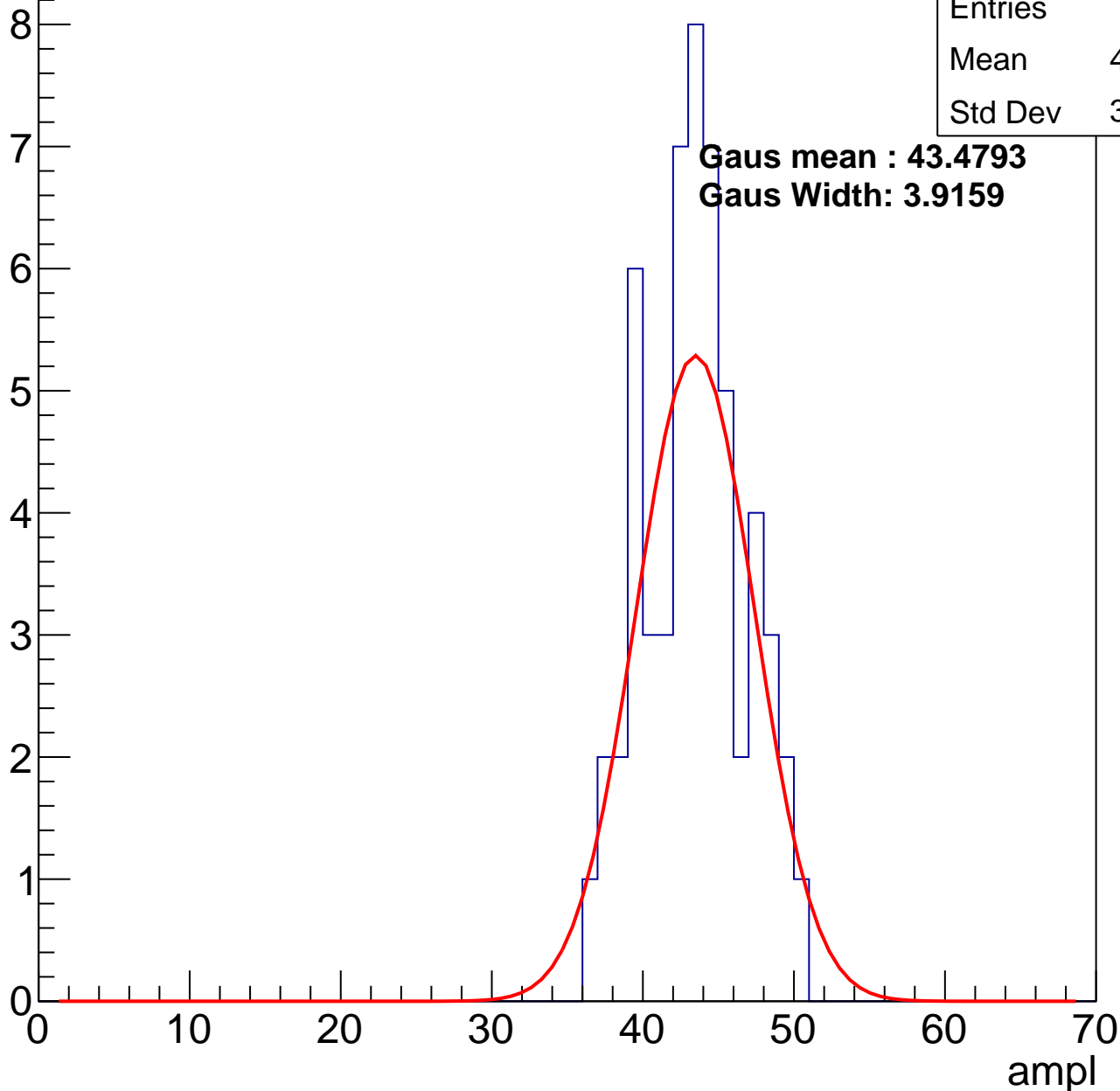
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	42.96
Std Dev	3.322

**Gaus mean : 43.4793**

**Gaus Width: 3.9159**

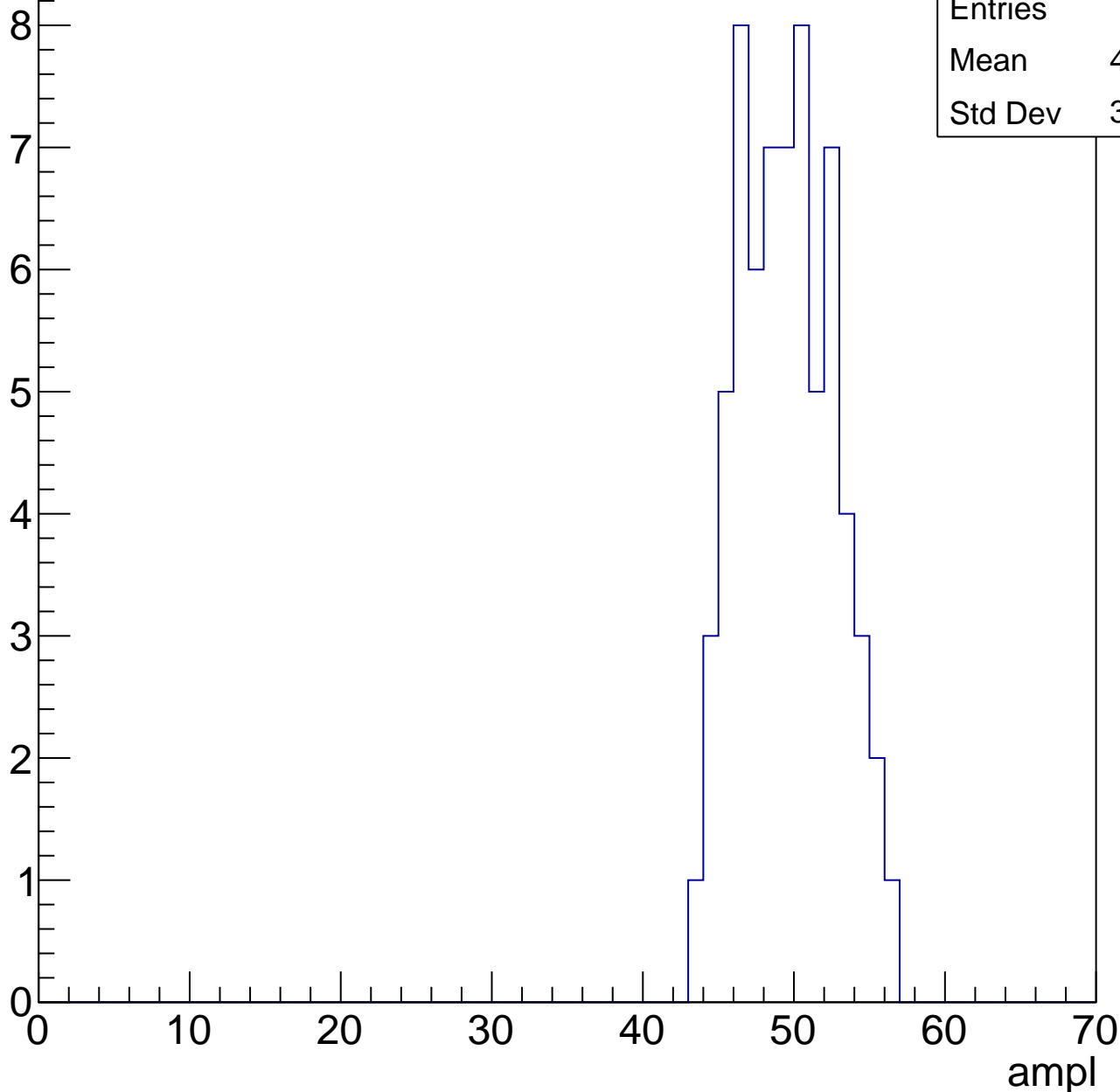


# B1L000S, U21-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	49.07
Std Dev	3.102

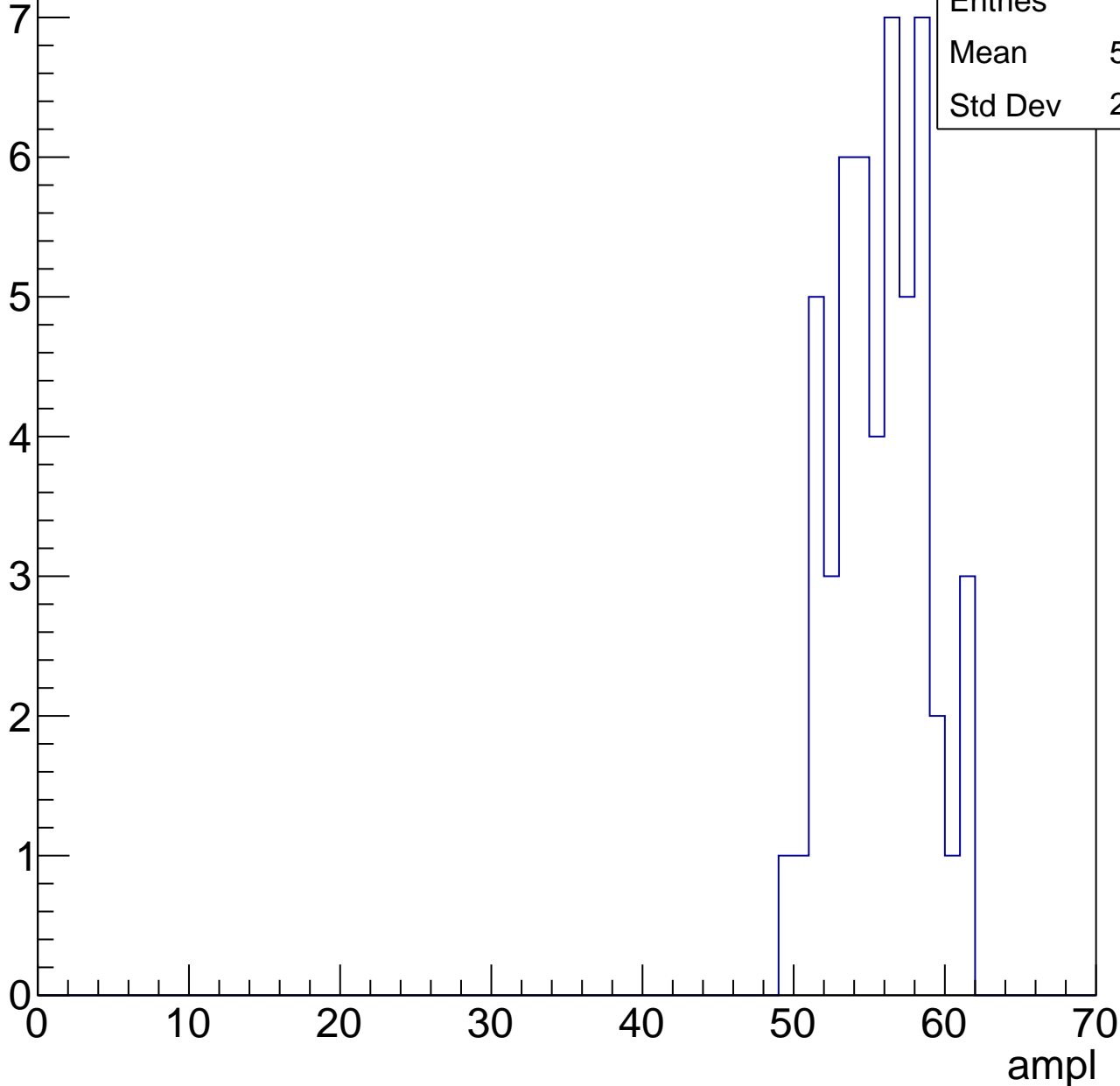


# B1L000S, U21-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

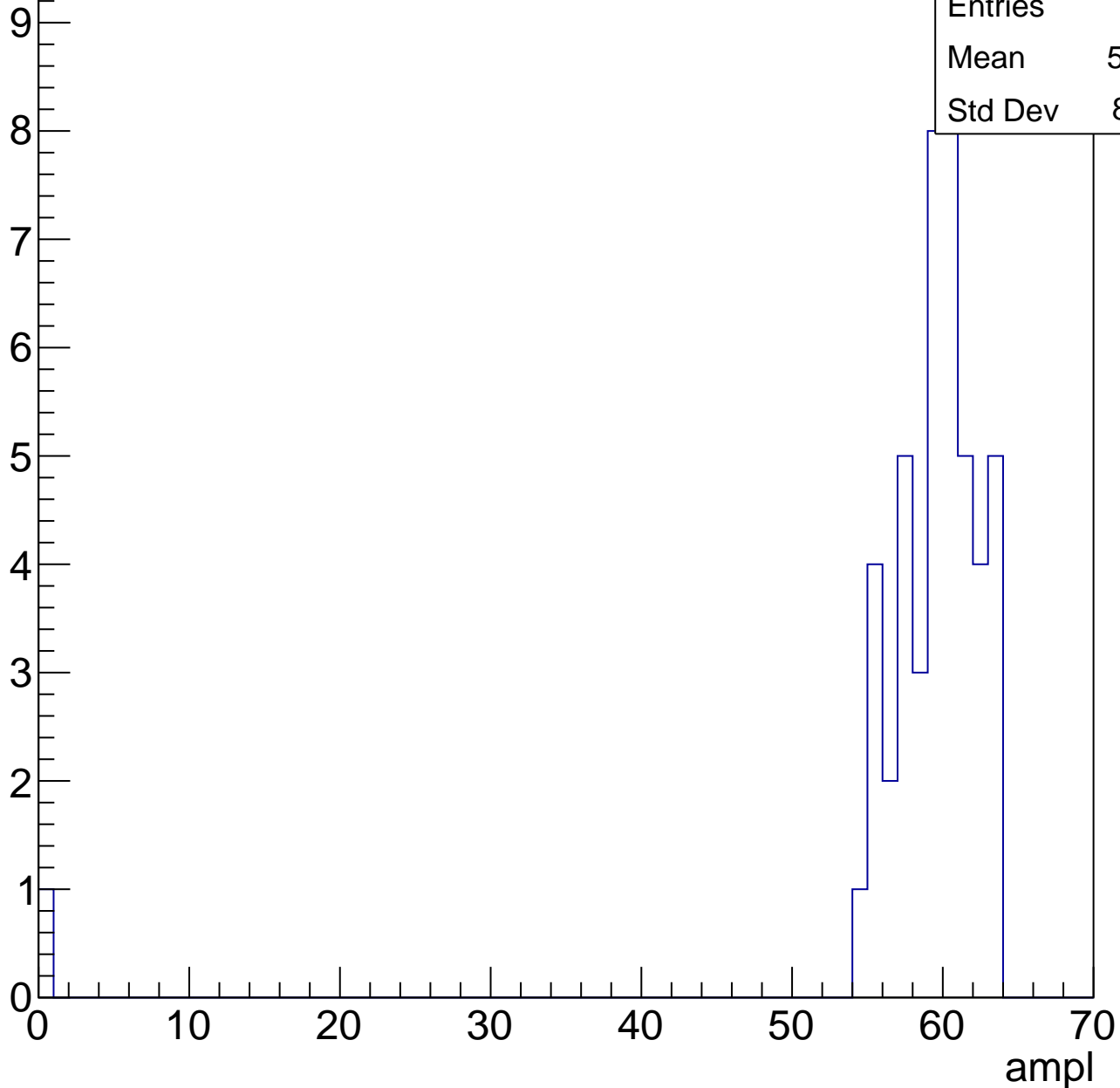
Entries	51
Mean	55.22
Std Dev	2.973



# B1L000S, U21-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



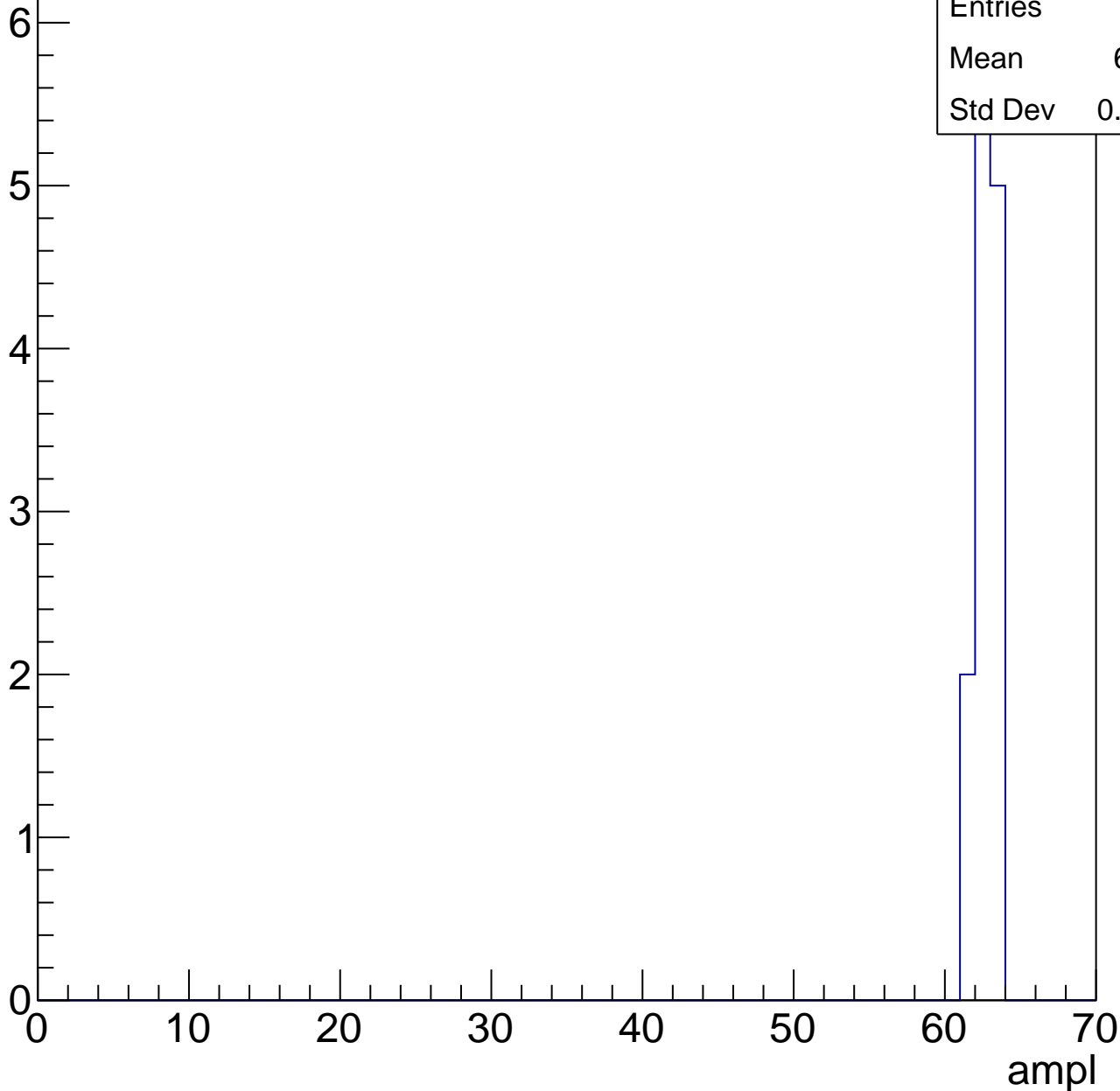
Entries	47
Mean	57.98
Std Dev	8.881

# B1L000S, U21-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	62.23
Std Dev	0.6966





# B1L000S, U21-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch73, adc0

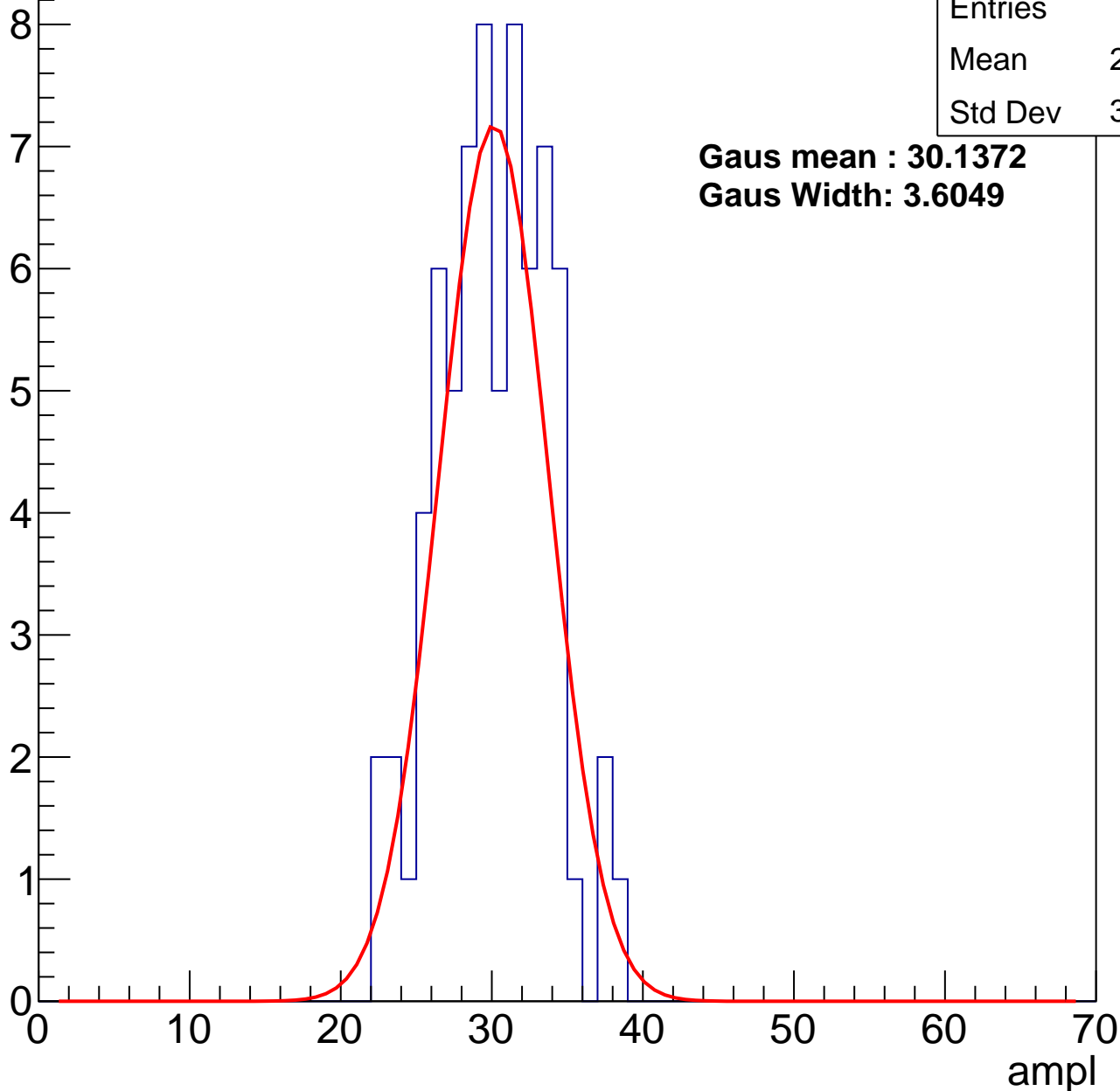
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	29.65
Std Dev	3.577

**Gaus mean : 30.1372**

**Gaus Width: 3.6049**



# B1L000S, U21-ch73, adc1

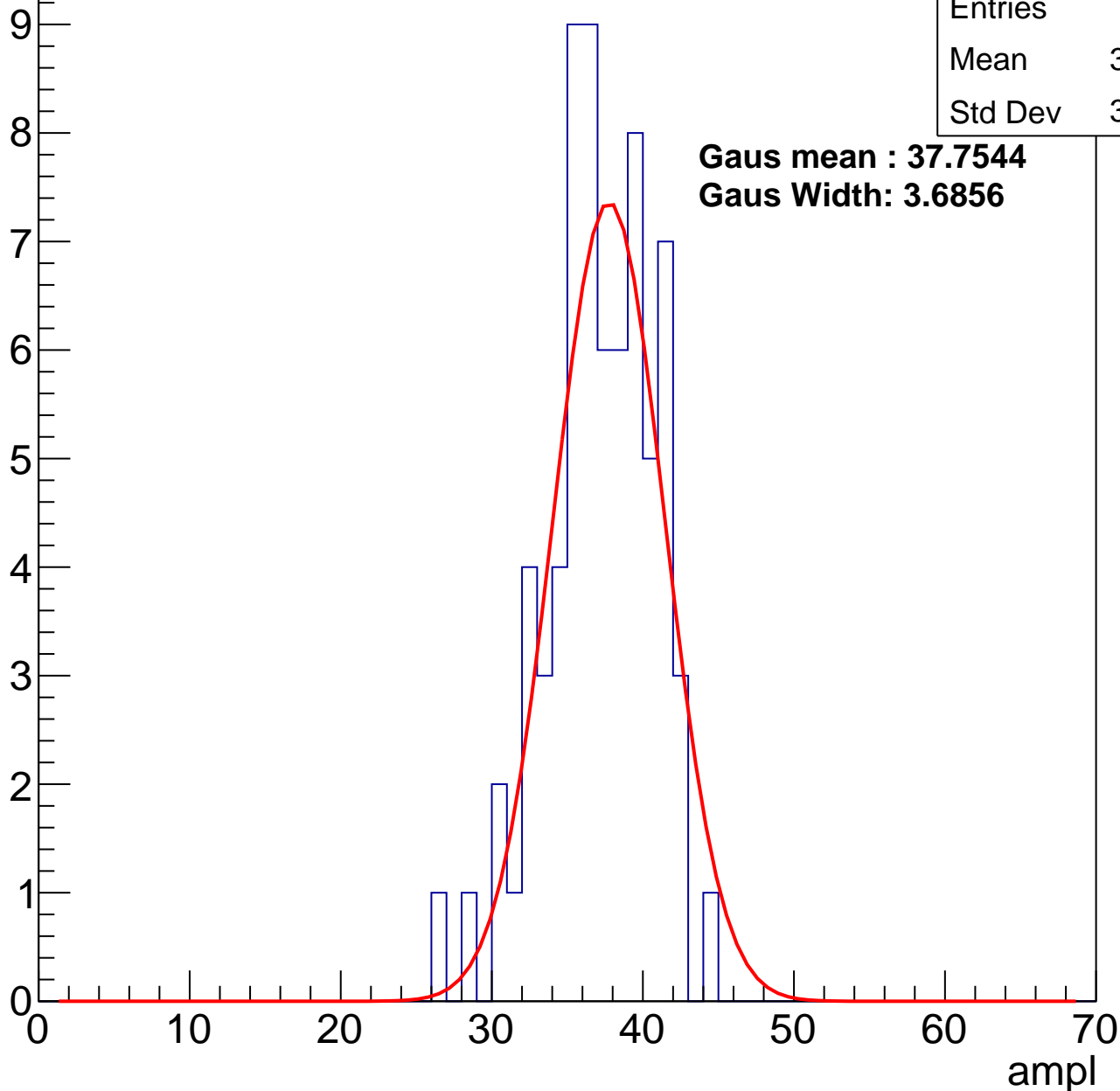
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.66
Std Dev	3.537

**Gaus mean : 37.7544**

**Gaus Width: 3.6856**



# B1L000S, U21-ch73, adc2

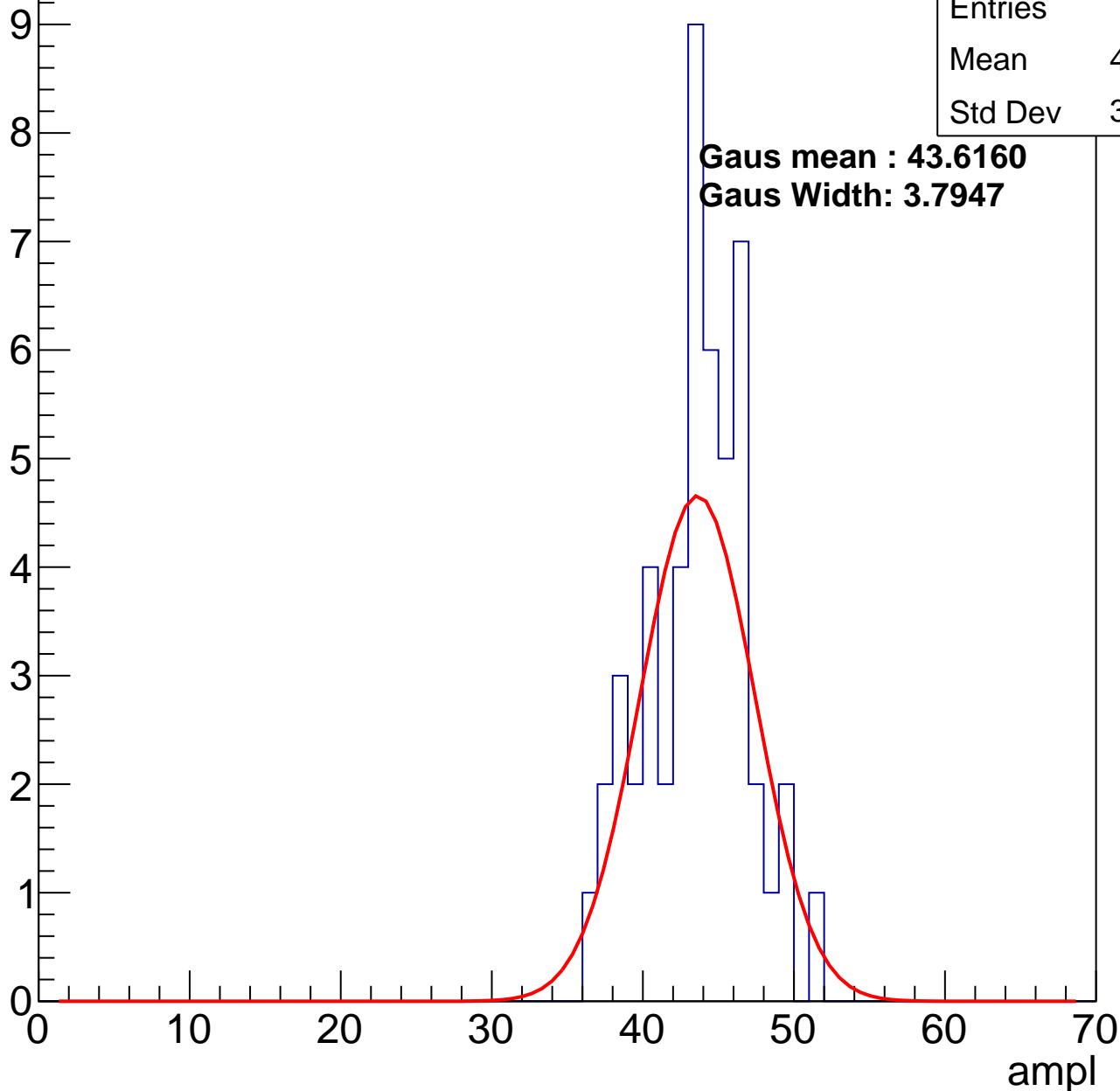
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	43.16
Std Dev	3.304

**Gaus mean : 43.6160**

**Gaus Width: 3.7947**

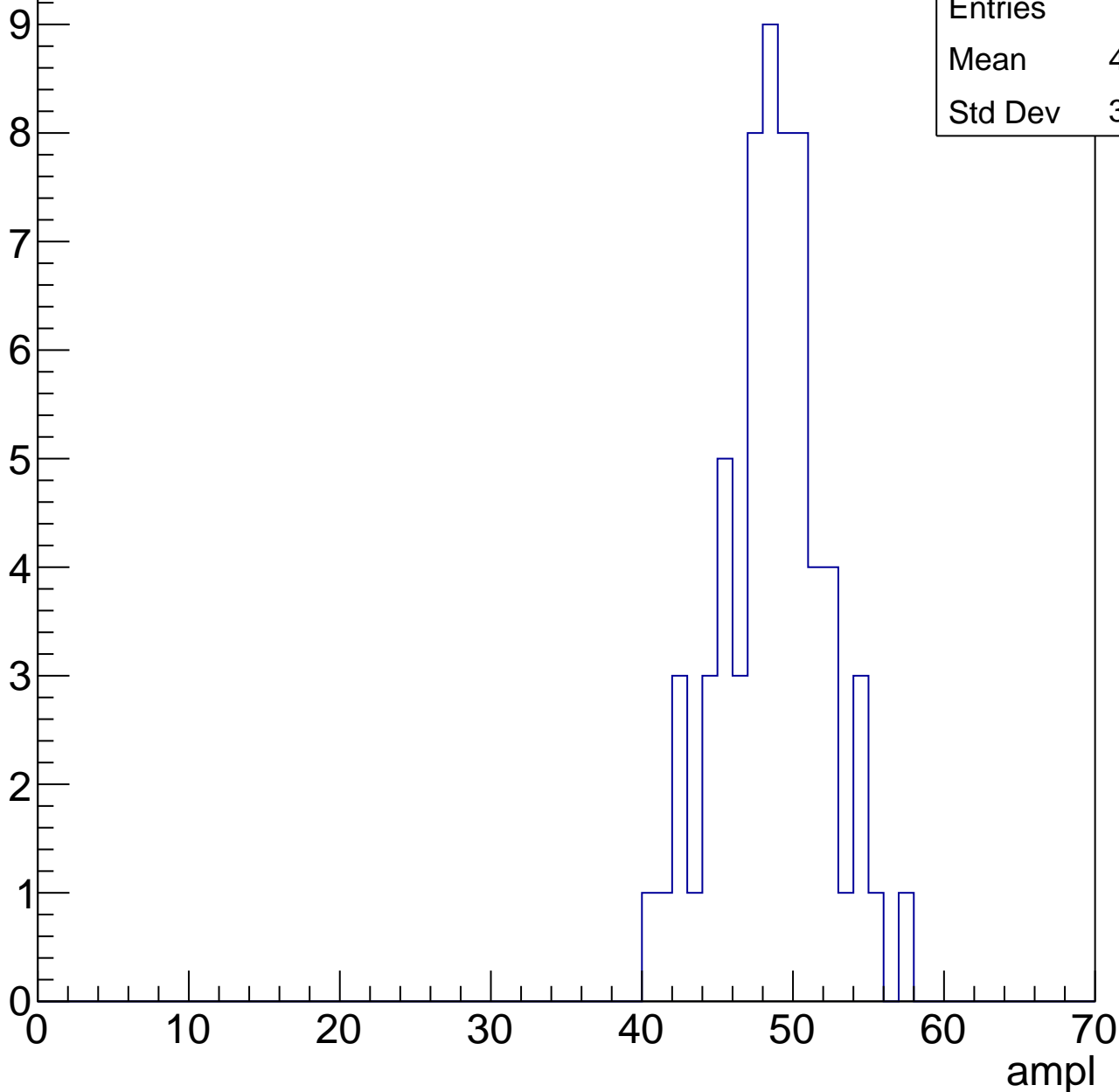


# B1L000S, U21-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

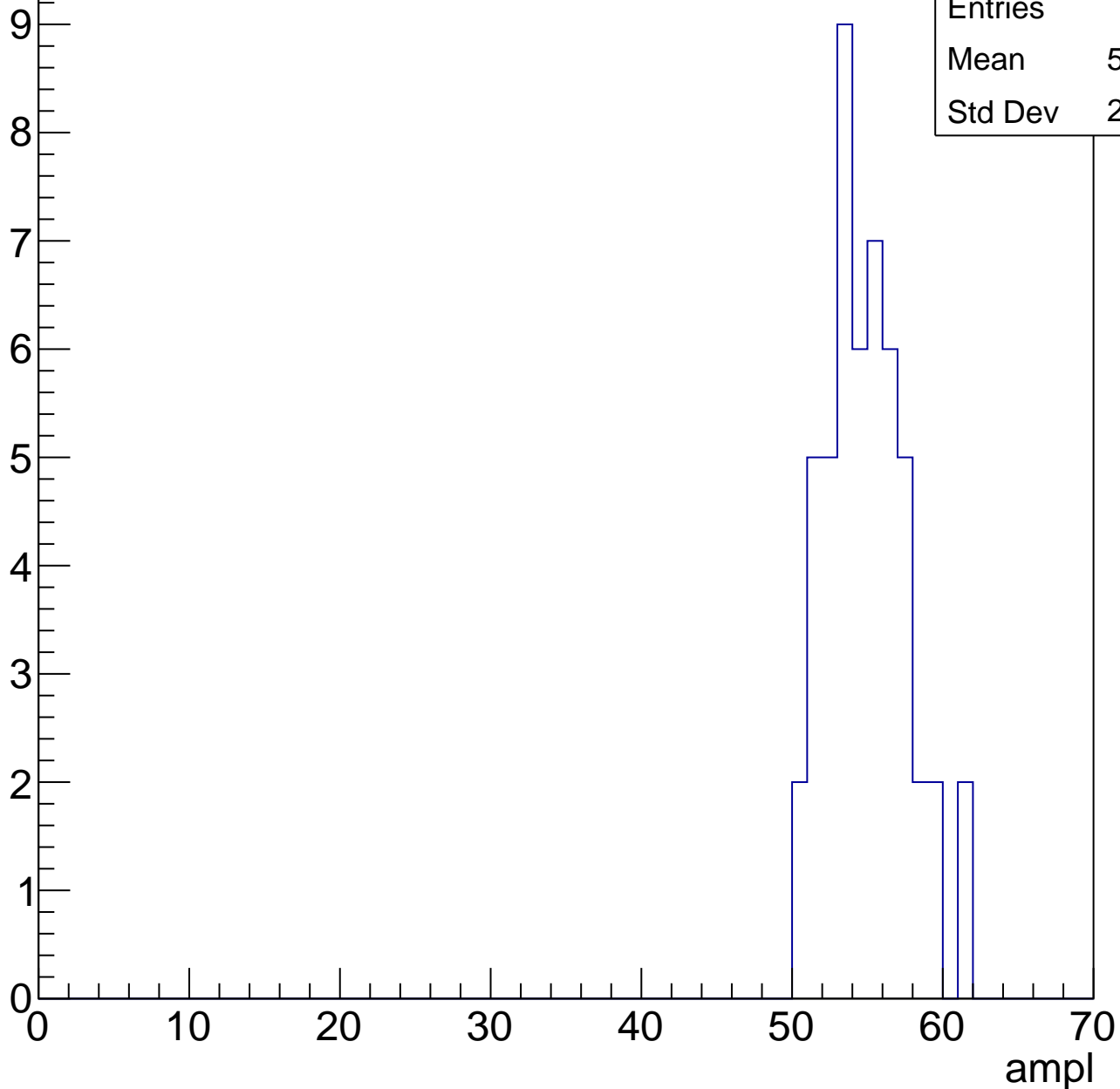
Entries	64
Mean	48.19
Std Dev	3.445



# B1L000S, U21-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

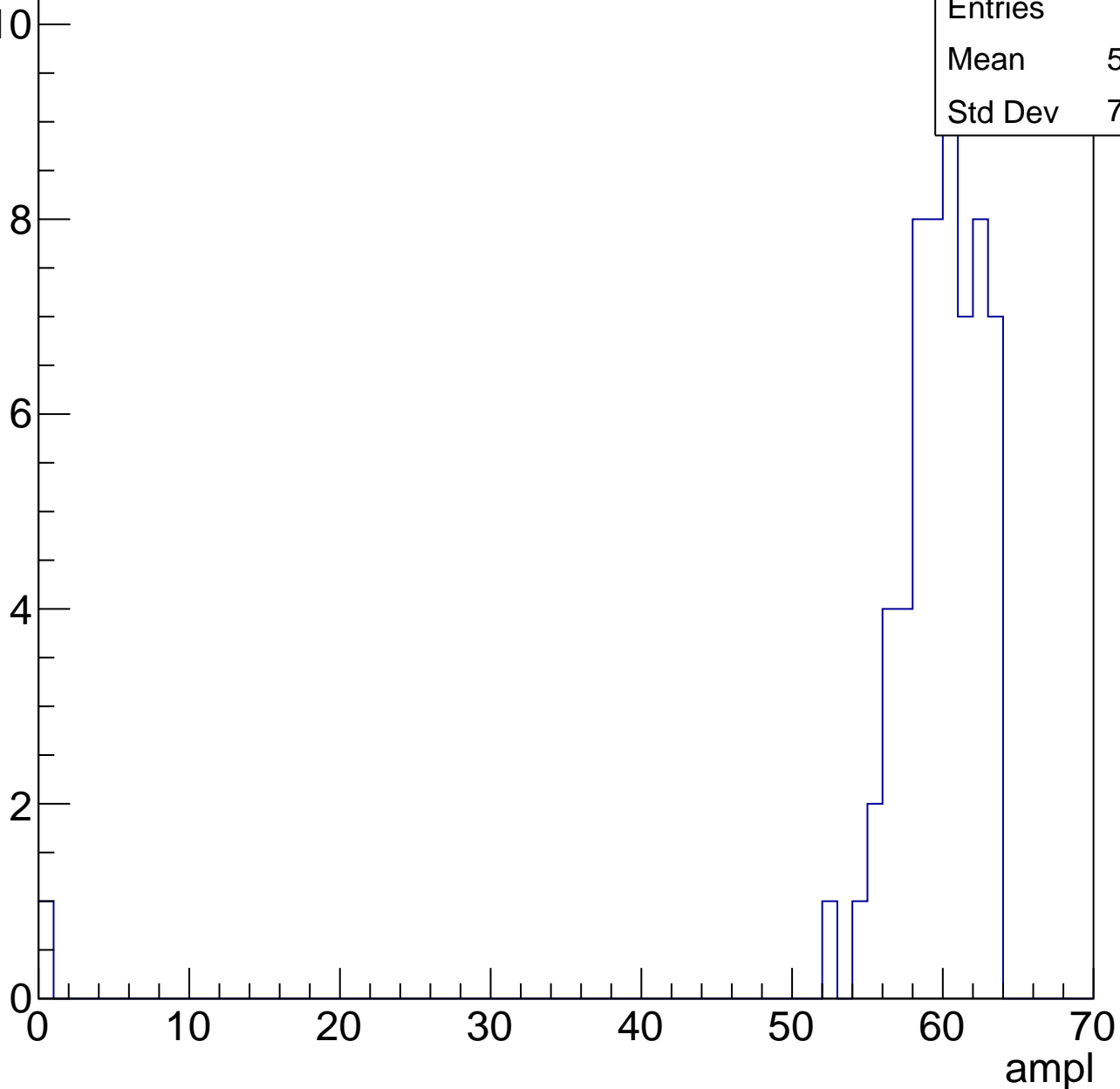


# B1L000S, U21-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	58.49
Std Dev	7.948



# B1L000S, U21-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

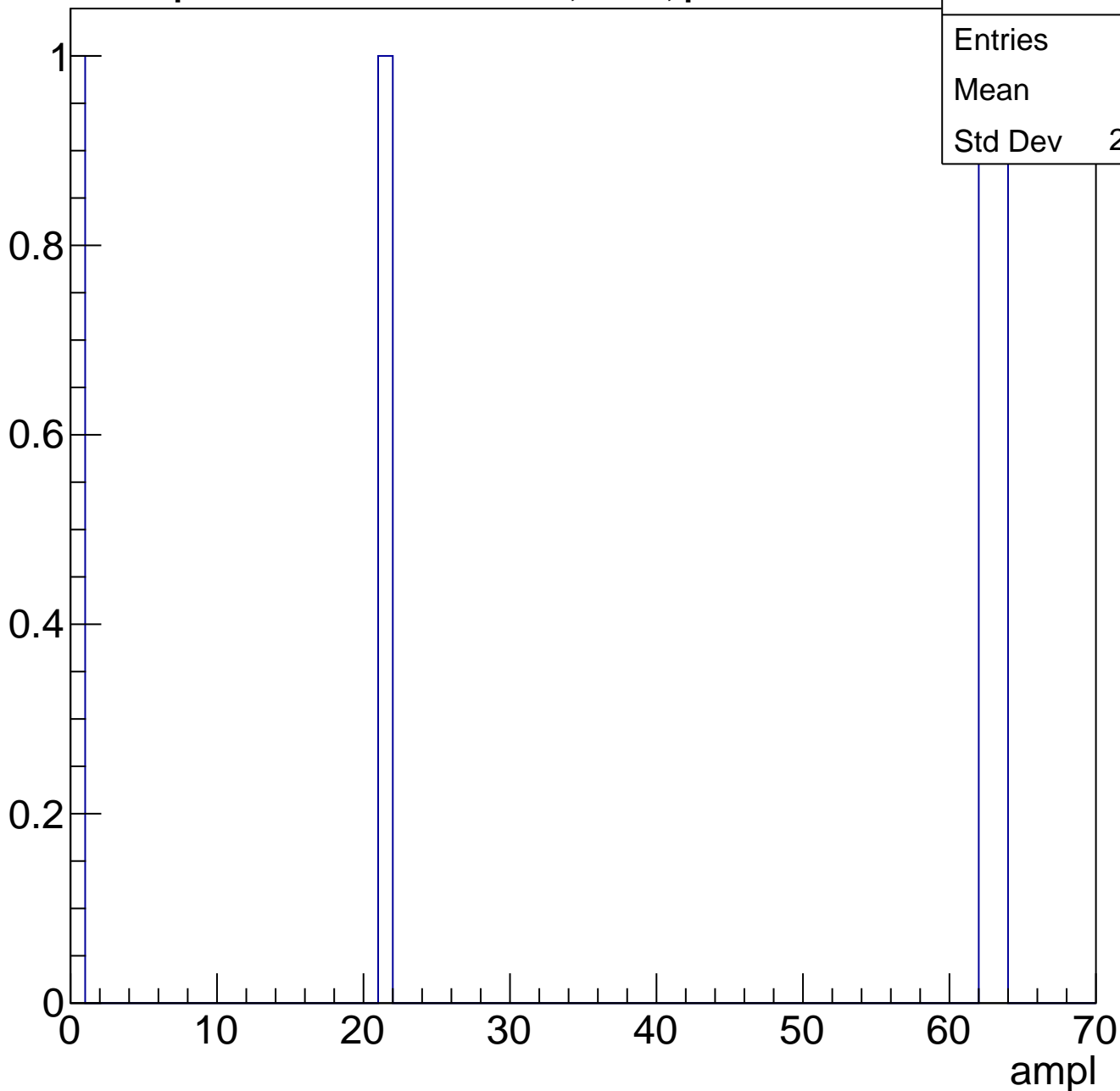
0 10 20 30 40 50 60 70



# B1L000S, U21-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch74, adc0

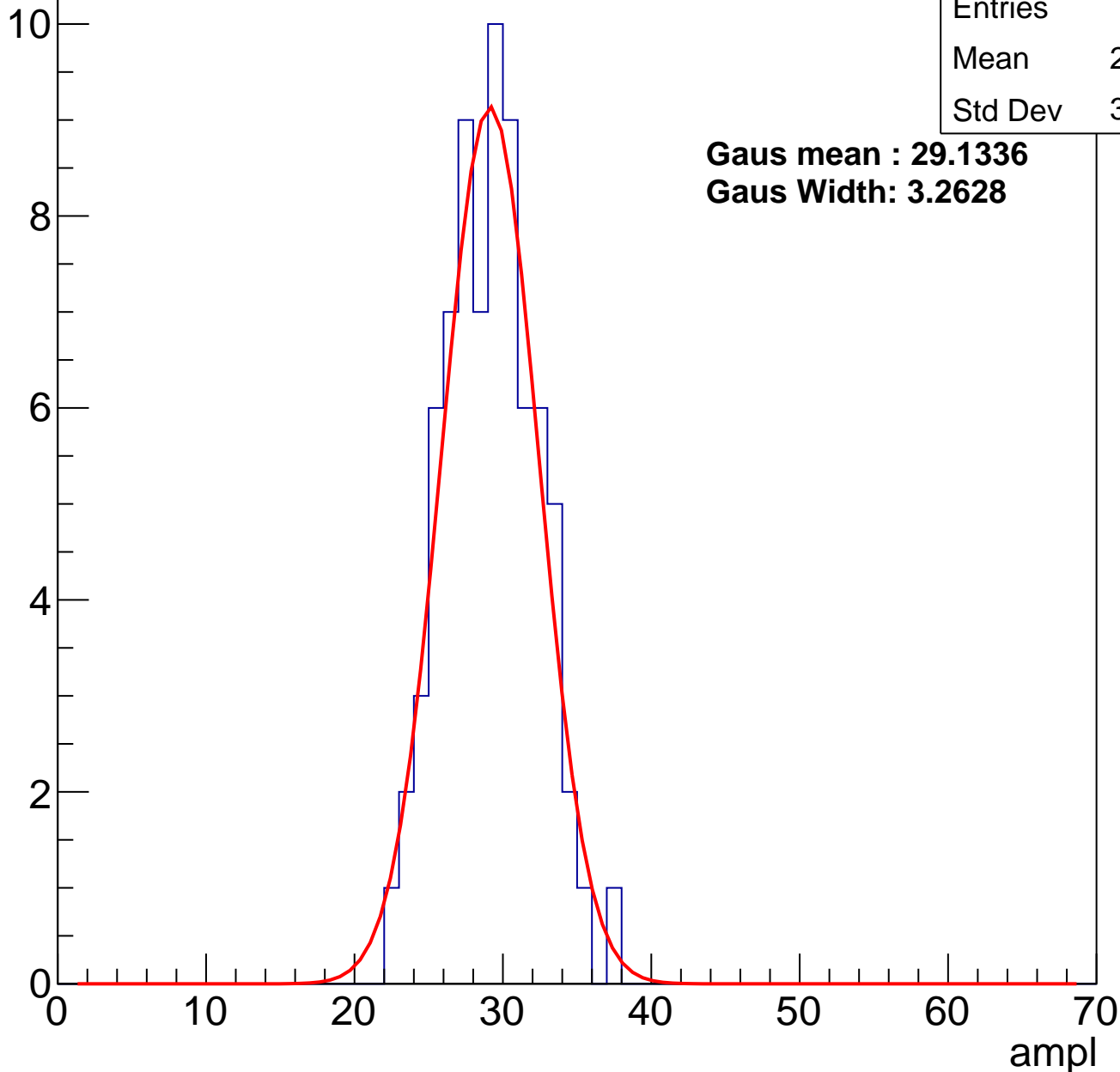
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	28.72
Std Dev	3.075

**Gaus mean : 29.1336**

**Gaus Width: 3.2628**

Entry



# B1L000S, U21-ch74, adc1

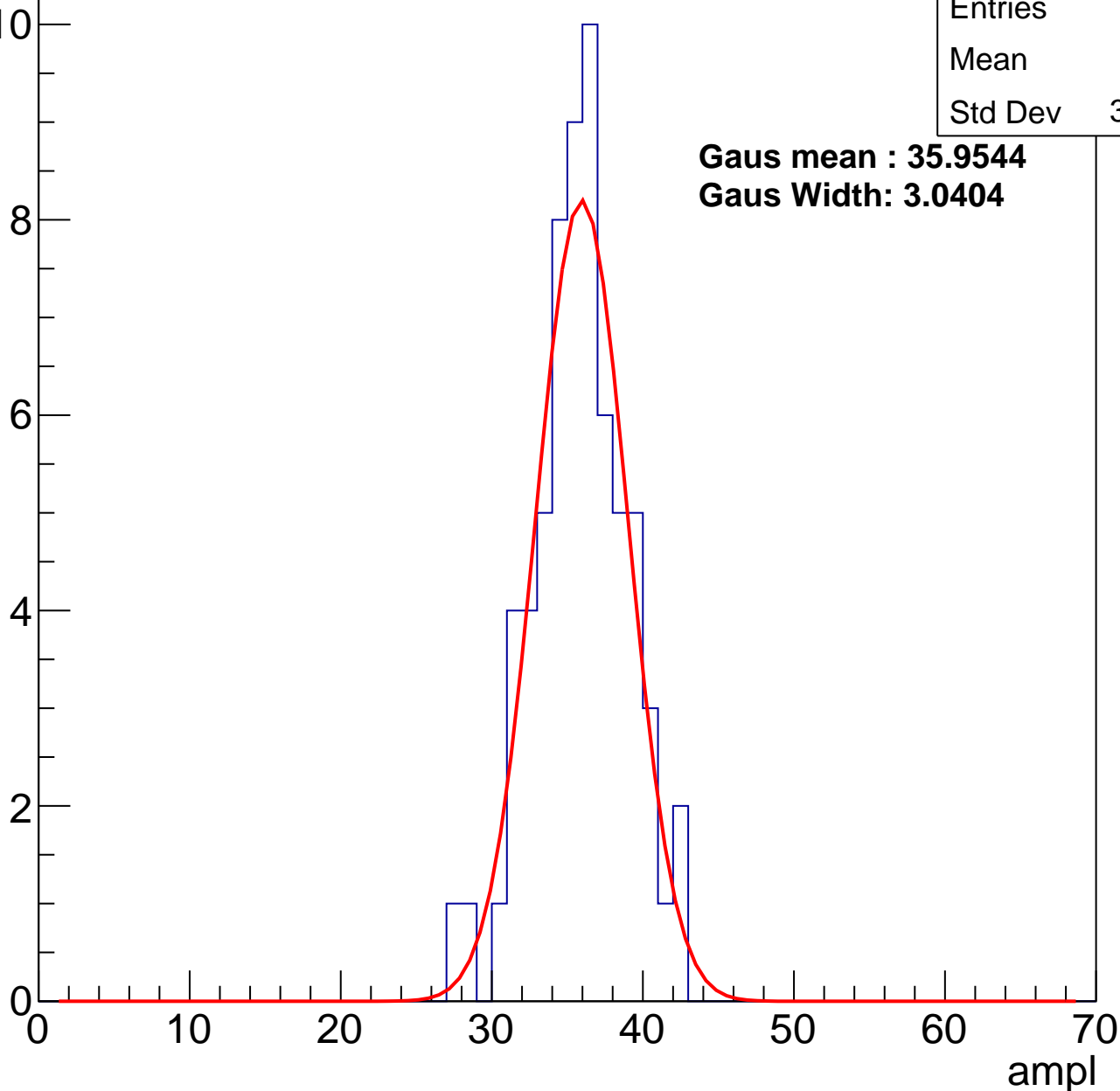
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.4
Std Dev	3.097

**Gaus mean : 35.9544**

**Gaus Width: 3.0404**



# B1L000S, U21-ch74, adc2

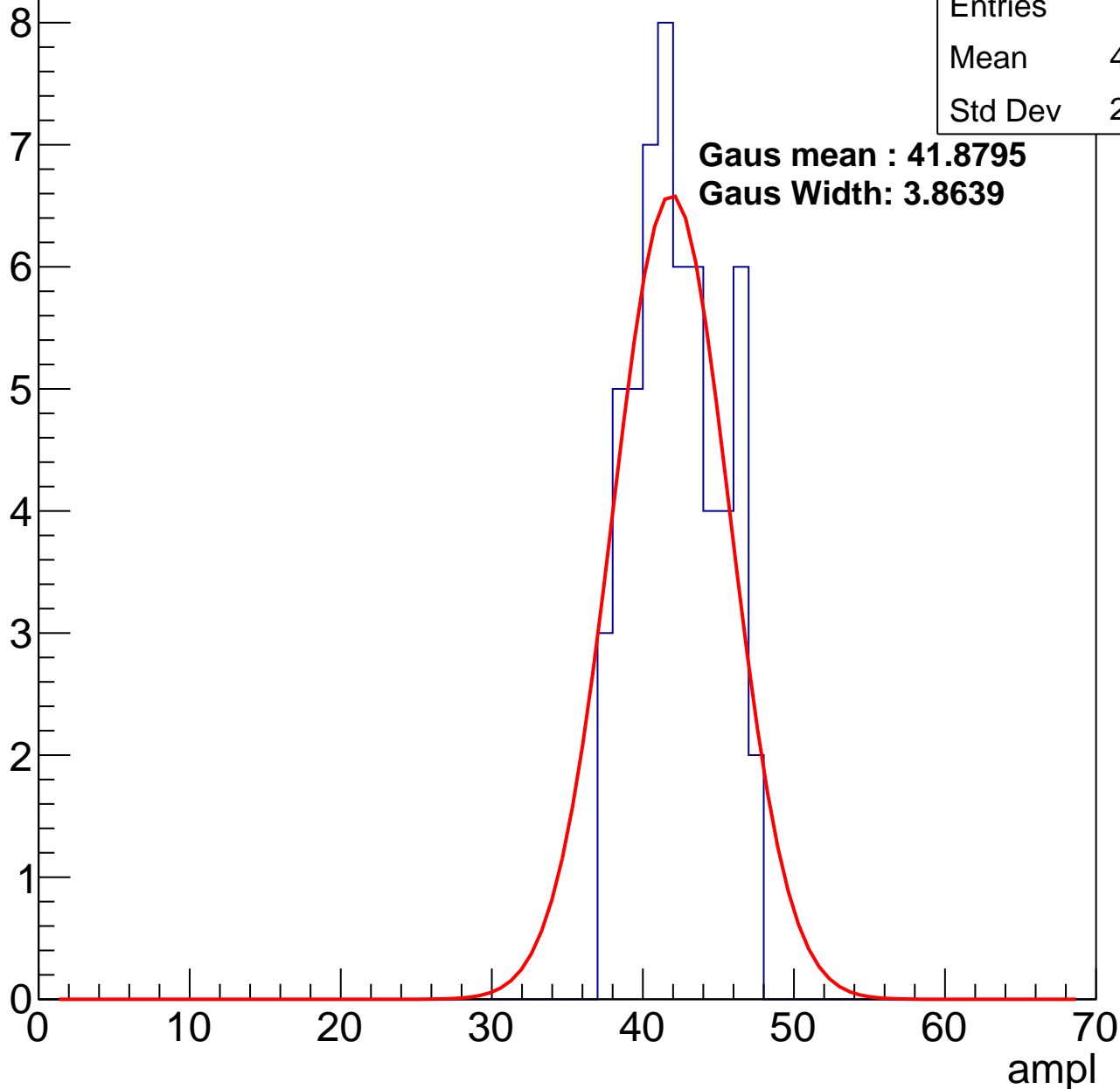
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.79
Std Dev	2.795

**Gaus mean : 41.8795**

**Gaus Width: 3.8639**

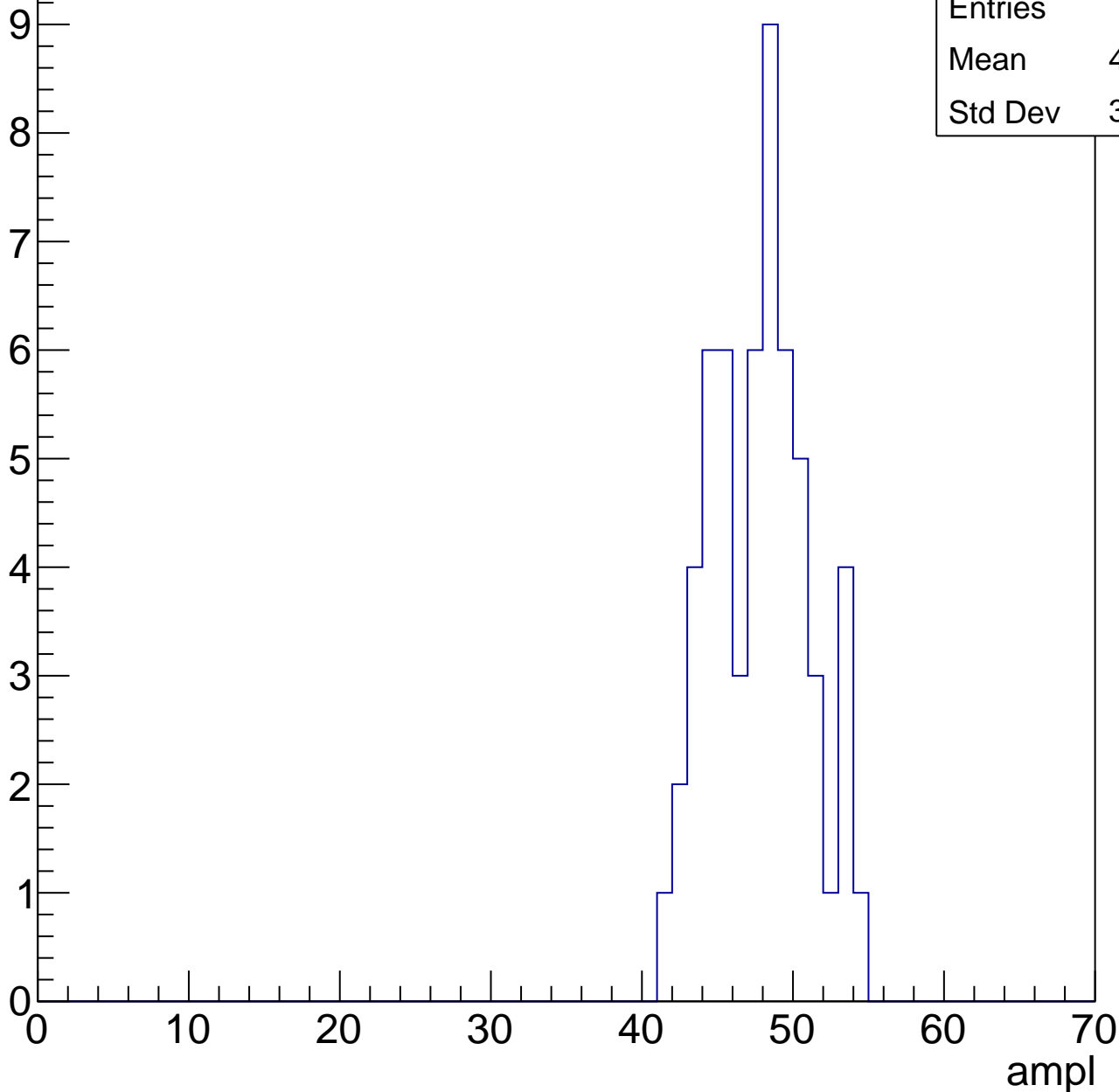


# B1L000S, U21-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	47.33
Std Dev	3.159

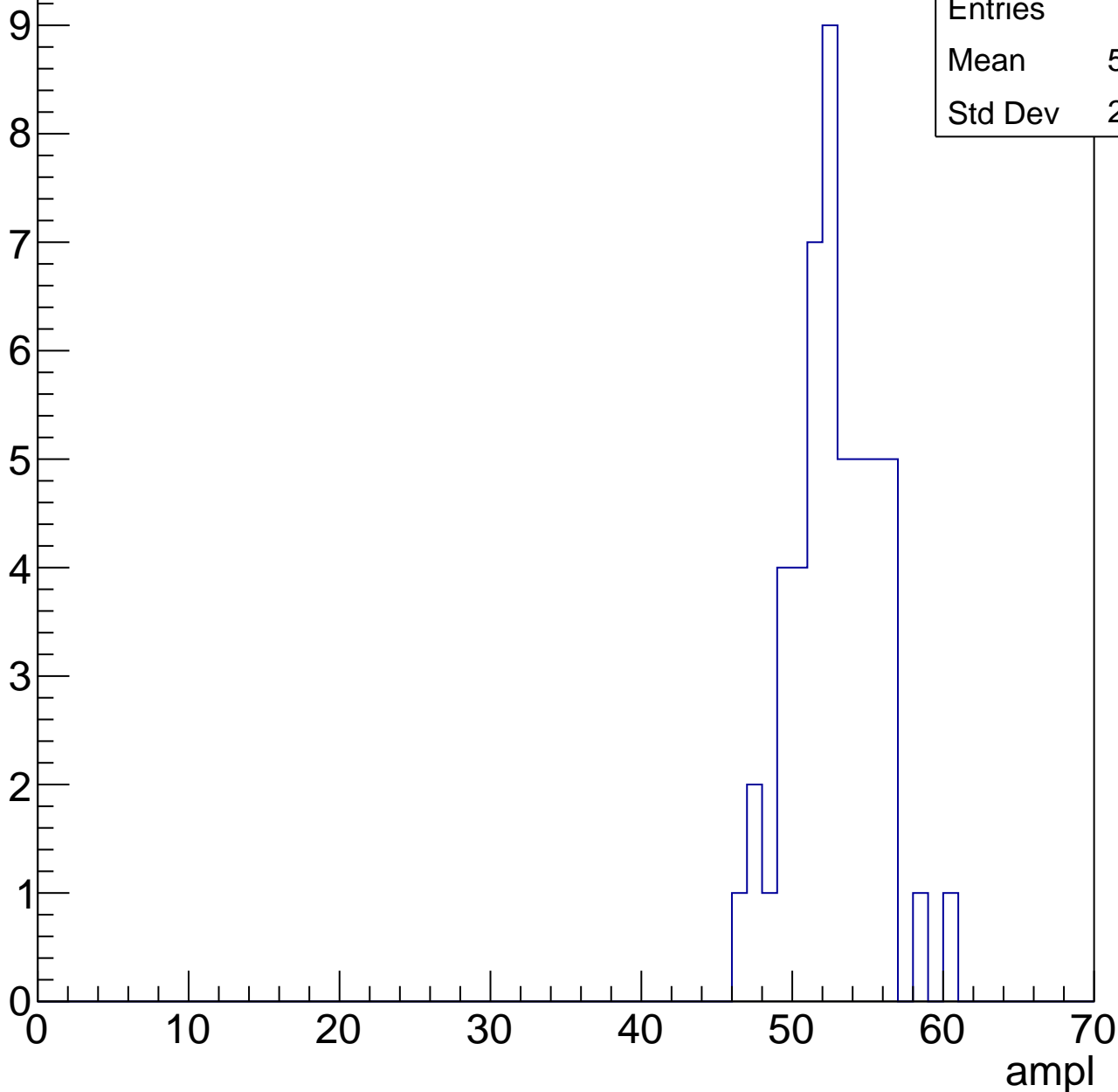


# B1L000S, U21-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	52.34
Std Dev	2.847



# B1L000S, U21-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	58.23
Std Dev	2.739

Entry

10

8

6

4

2

0

0

10

20

30

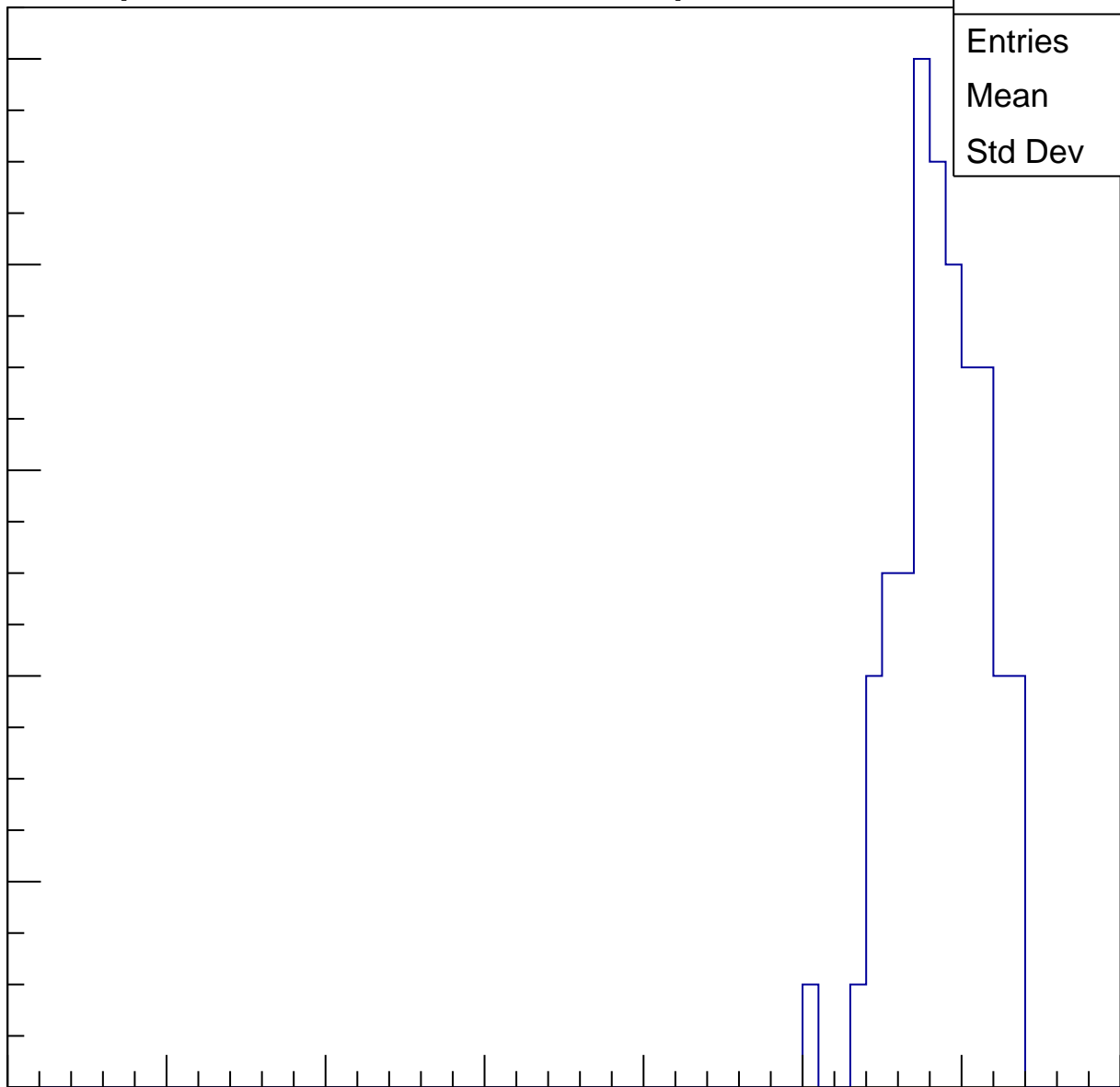
40

50

60

70

ampl

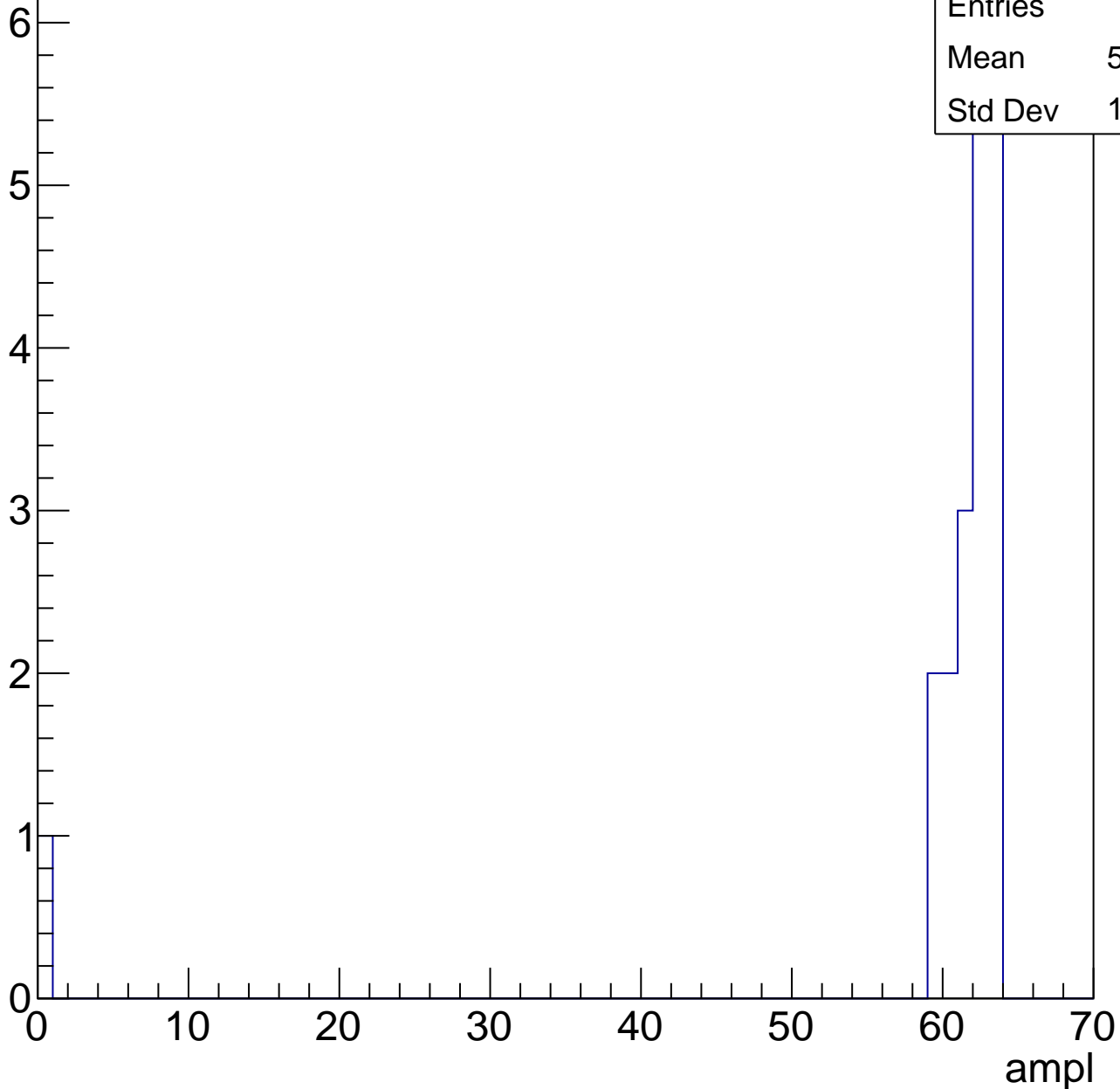


# B1L000S, U21-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.55
Std Dev	13.49





# B1L000S, U21-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U21-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	62
Mean	29.53
Std Dev	4.686

**Gaus mean : 30.7907**

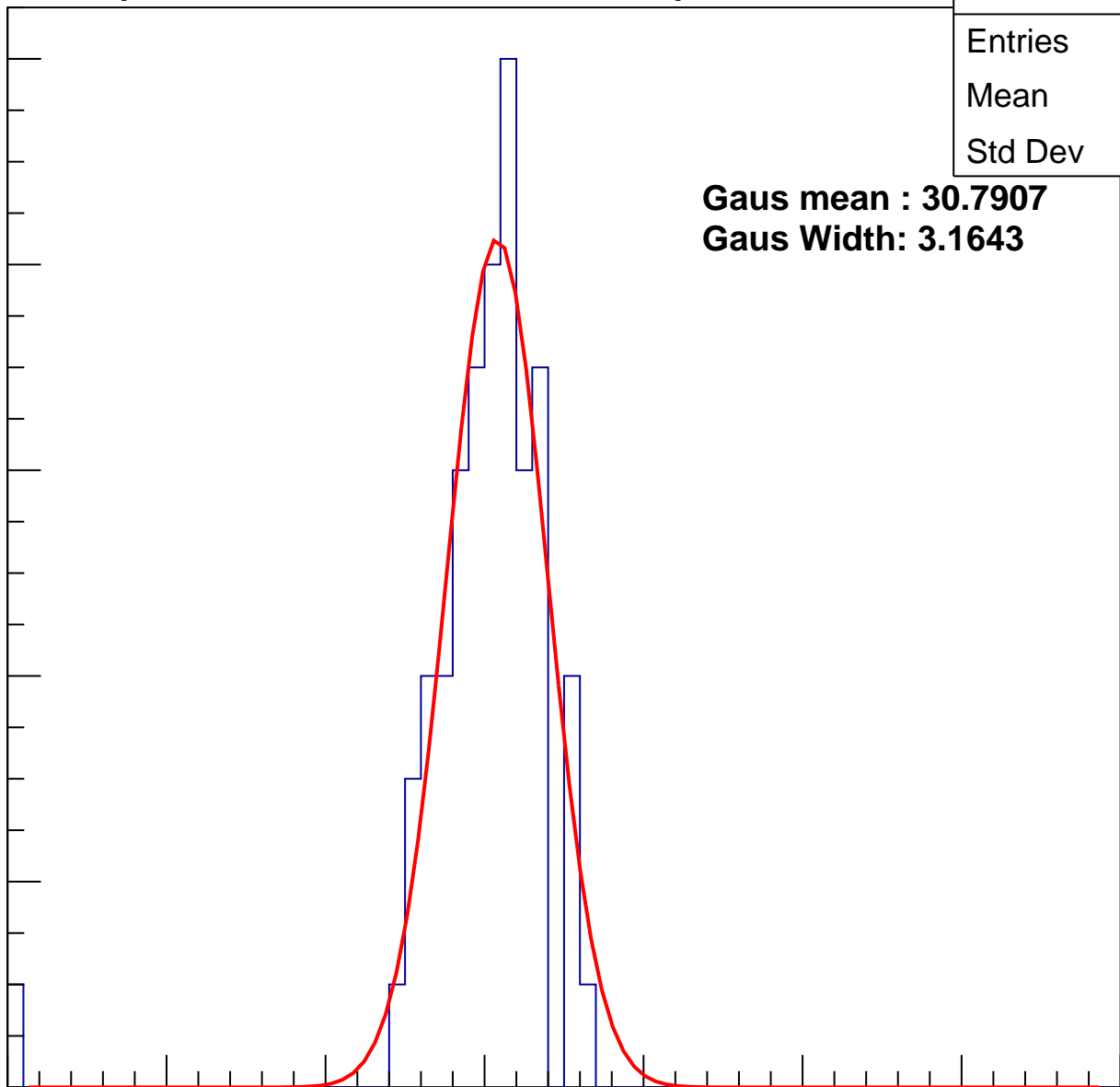
**Gaus Width: 3.1643**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	36.28
Std Dev	3.463

**Gaus mean : 37.0048**

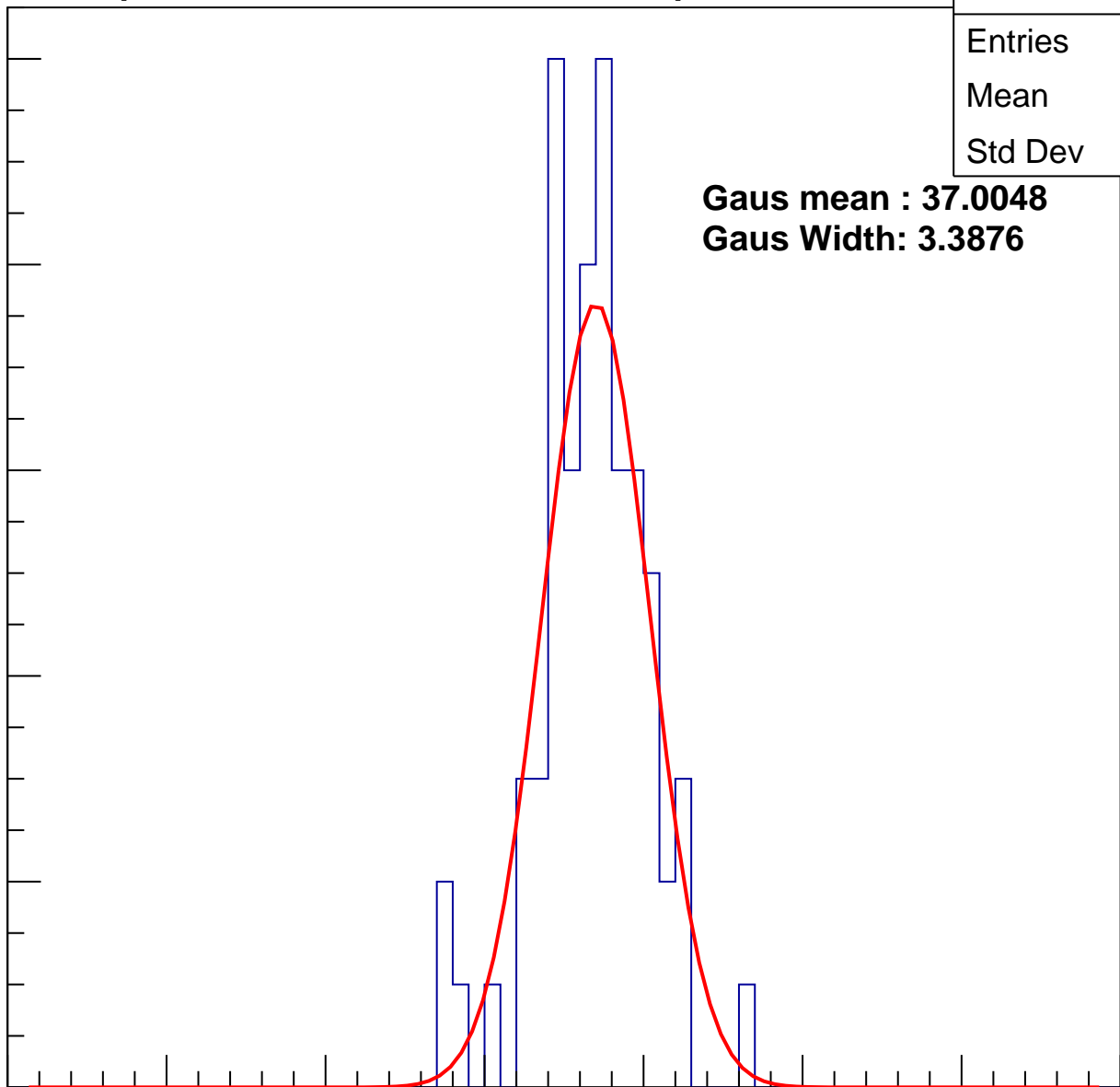
**Gaus Width: 3.3876**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch75, adc2

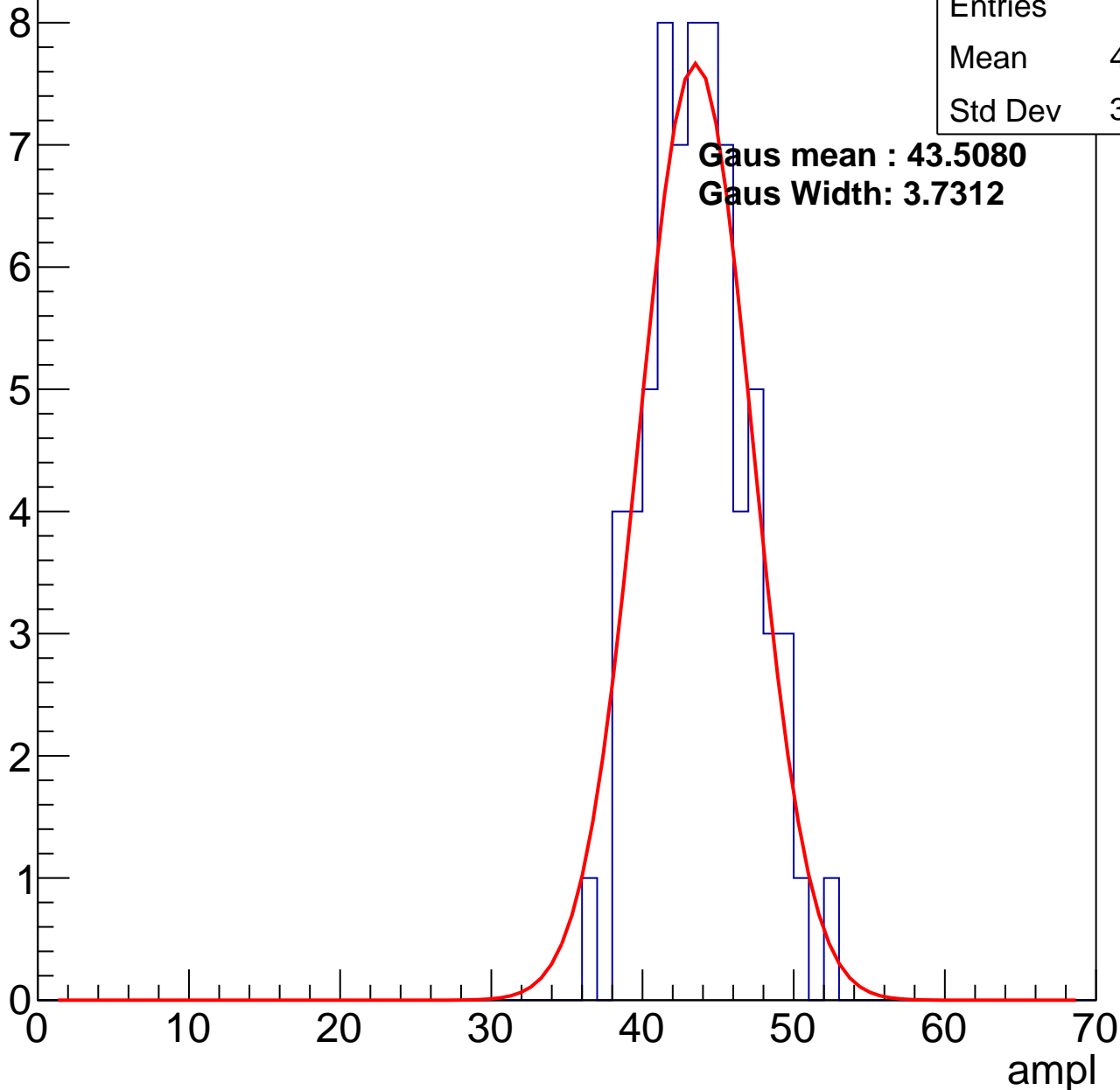
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	43.32
Std Dev	3.308

**Gaus mean : 43.5080**

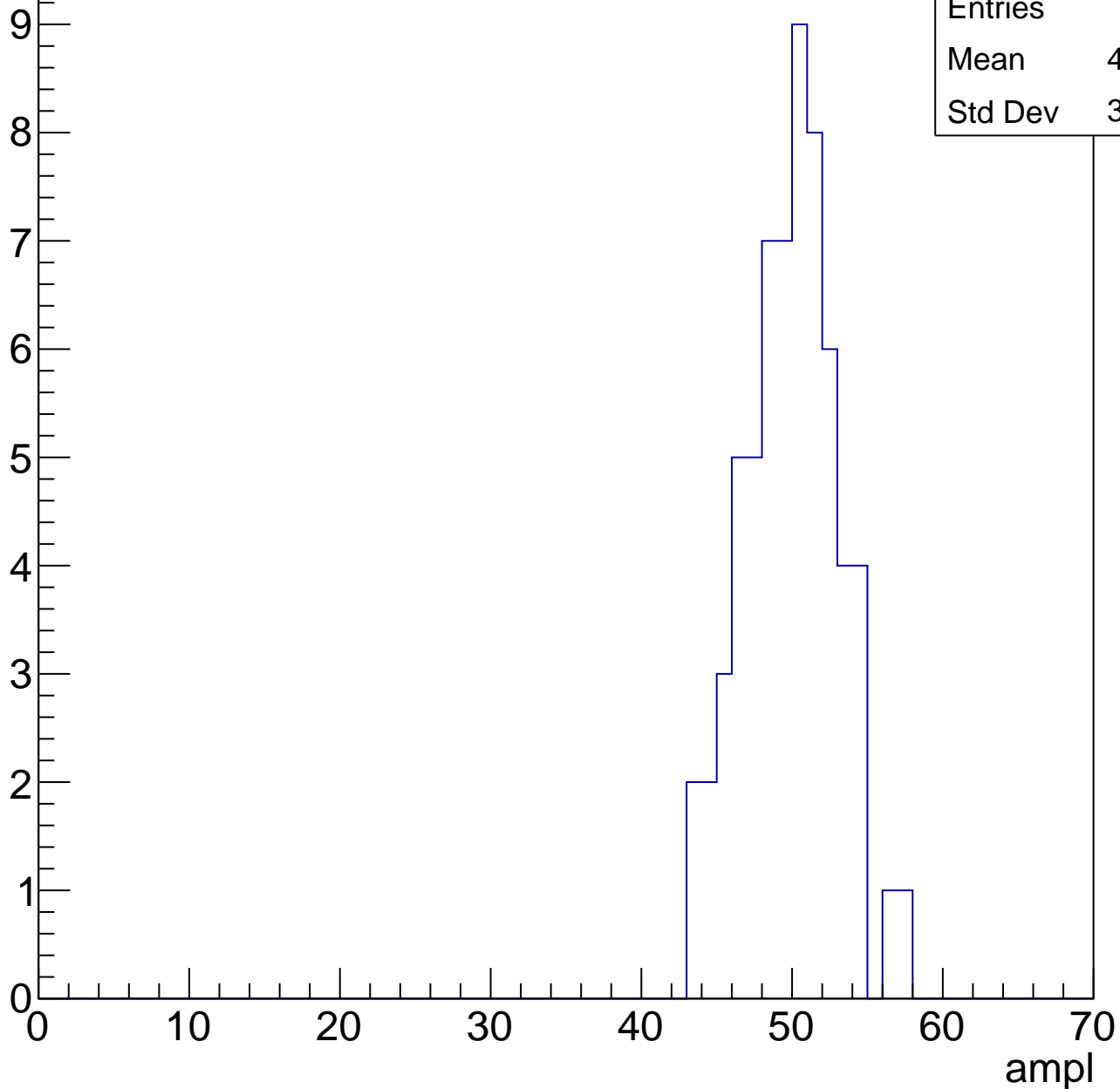
**Gaus Width: 3.7312**



# B1L000S, U21-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

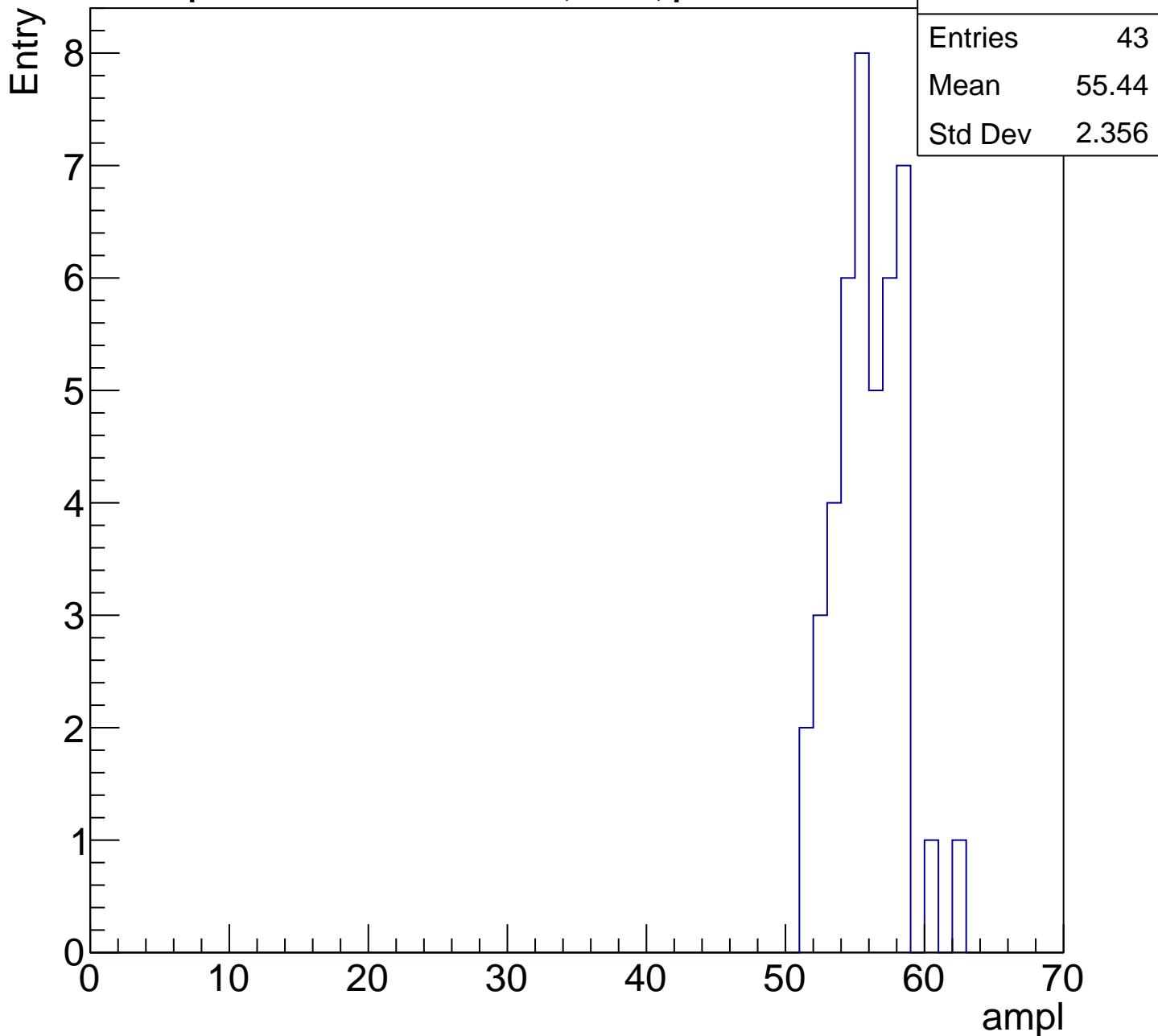
Entry



Entries	64
Mean	49.44
Std Dev	3.066

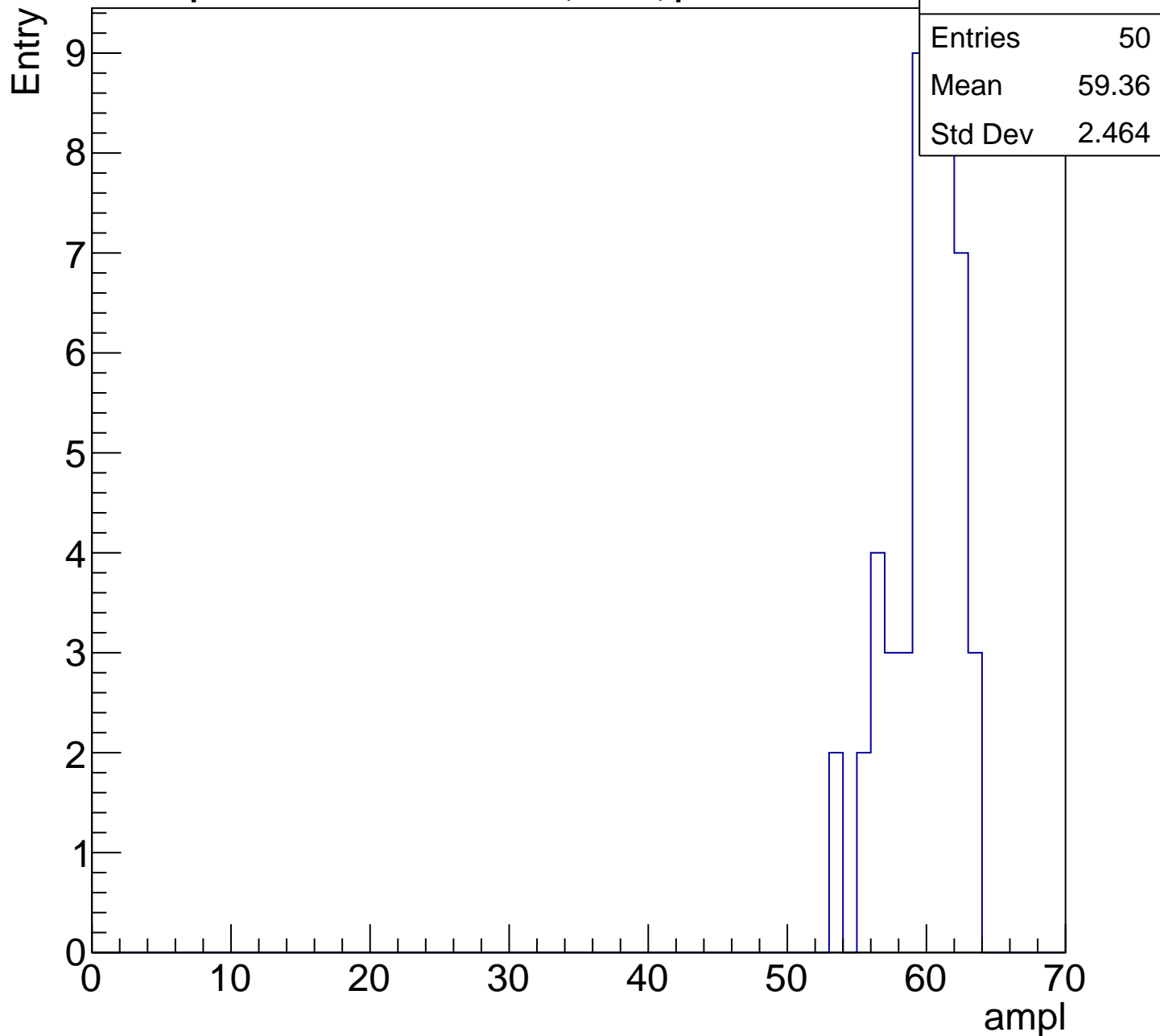
# B1L000S, U21-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

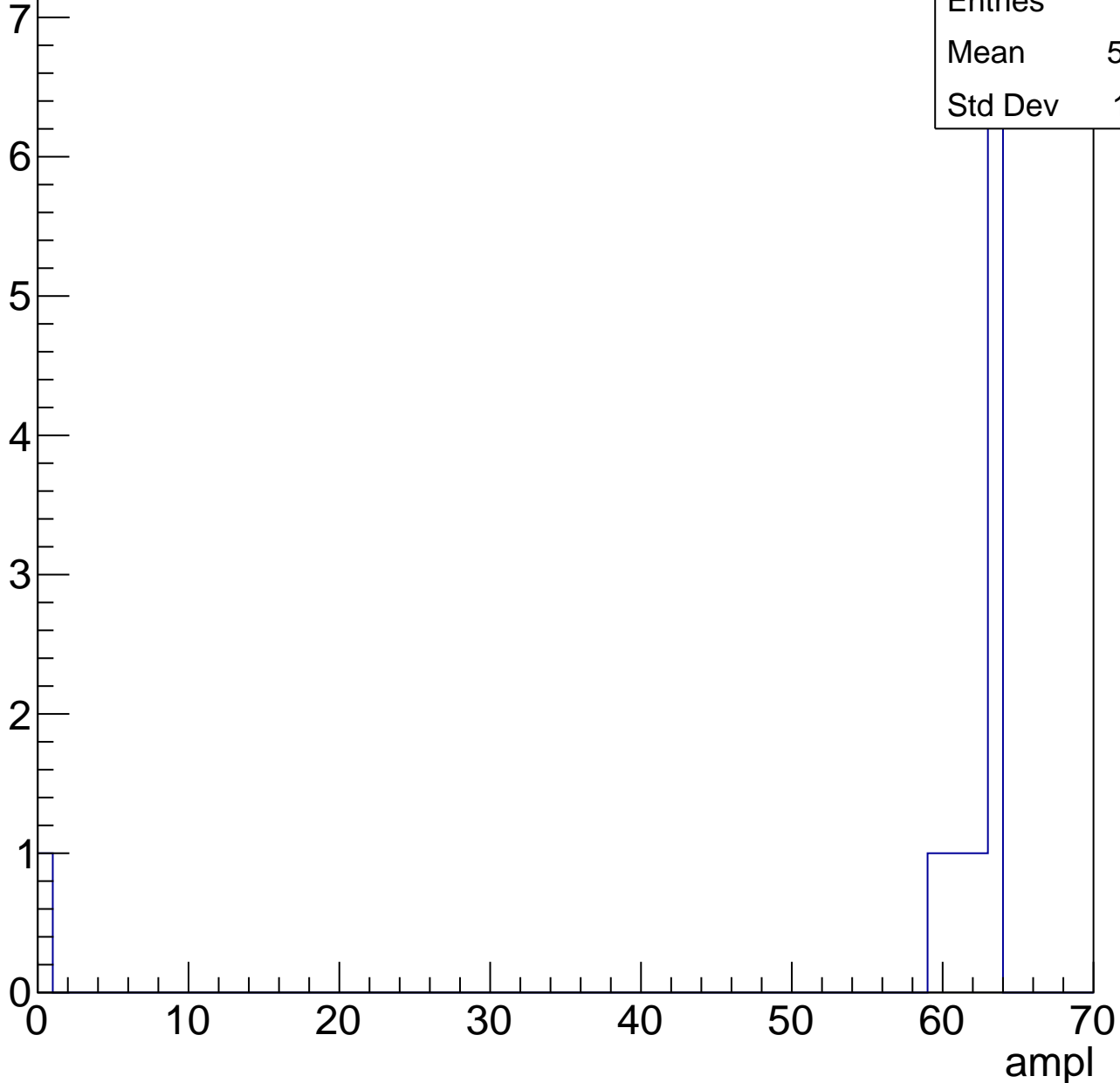


# B1L000S, U21-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	56.92
Std Dev	17.21





# B1L000S, U21-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch76, adc0

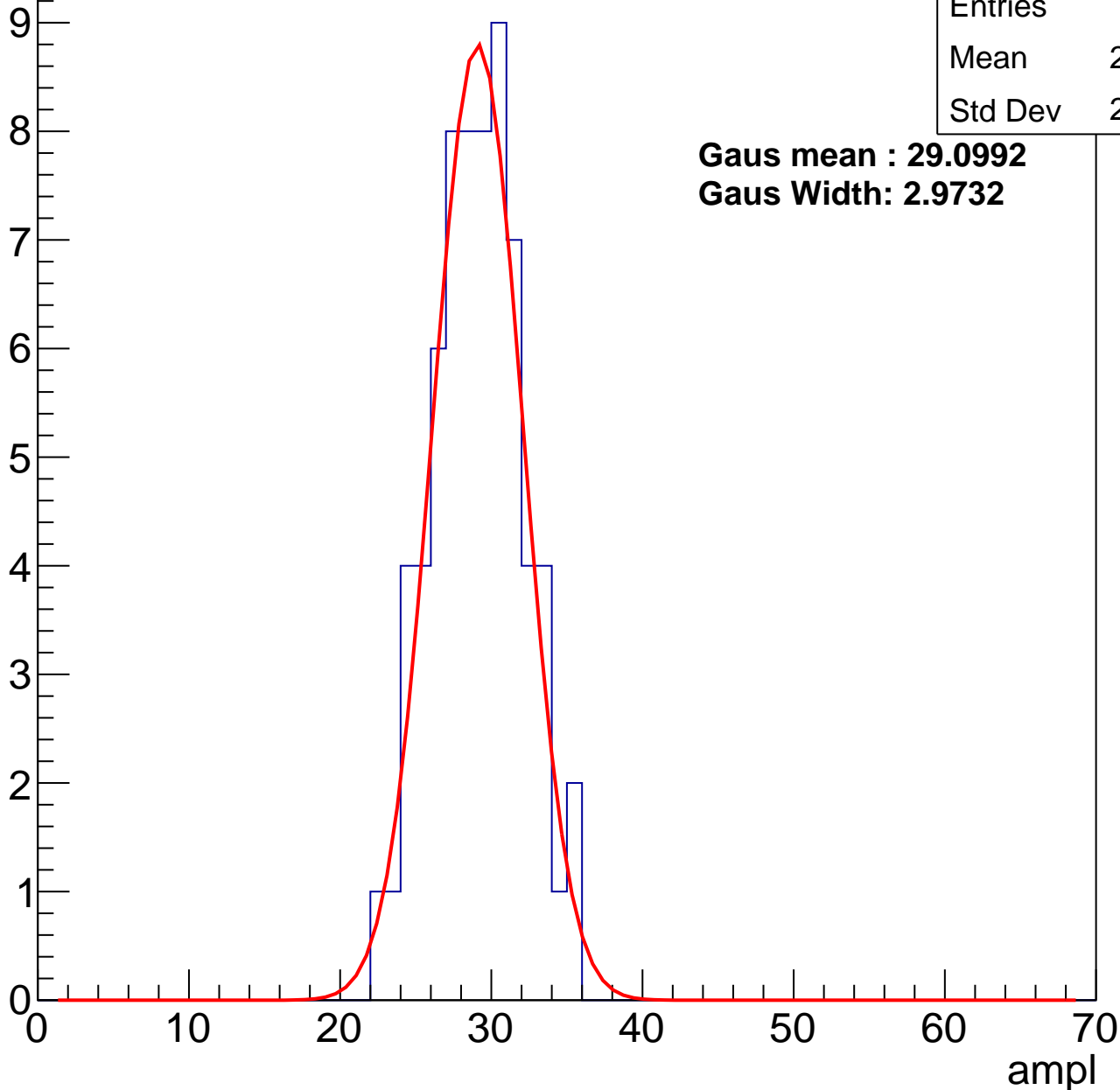
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.66
Std Dev	2.914

**Gaus mean : 29.0992**

**Gaus Width: 2.9732**



# B1L000S, U21-ch76, adc1

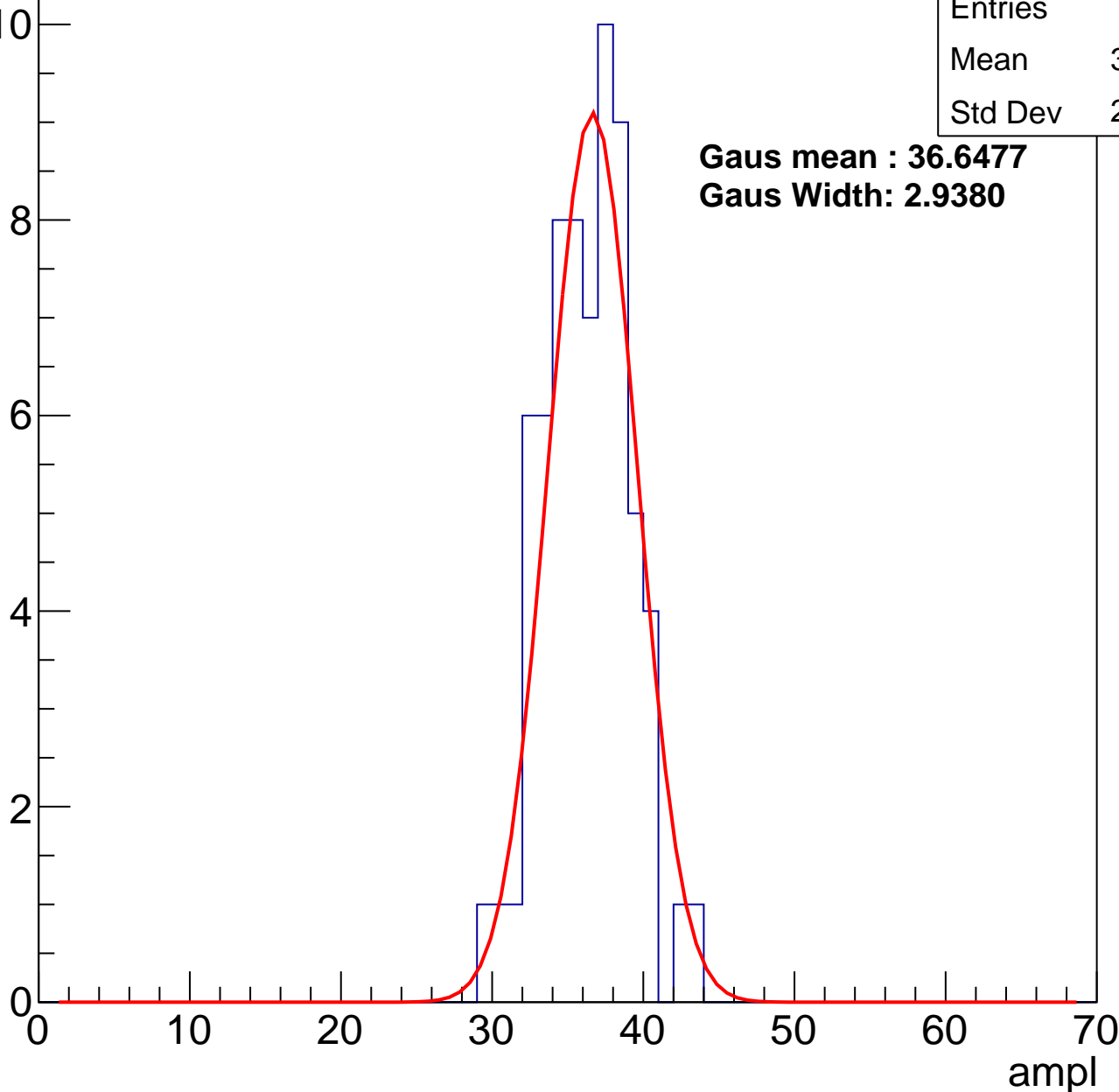
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.82
Std Dev	2.812

**Gaus mean : 36.6477**

**Gaus Width: 2.9380**



# B1L000S, U21-ch76, adc2

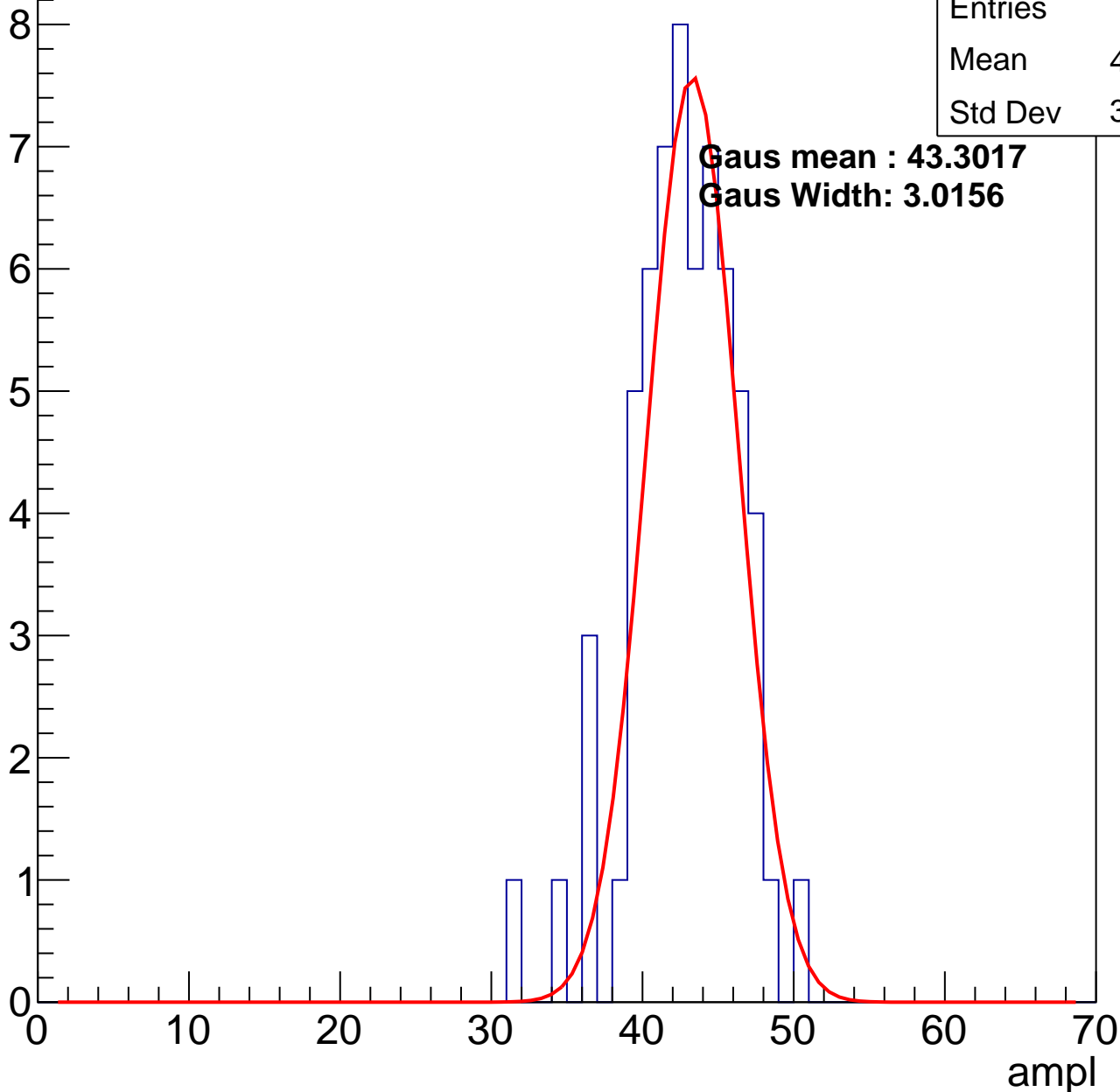
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.27
Std Dev	3.474

**Gaus mean : 43.3017**

**Gaus Width: 3.0156**

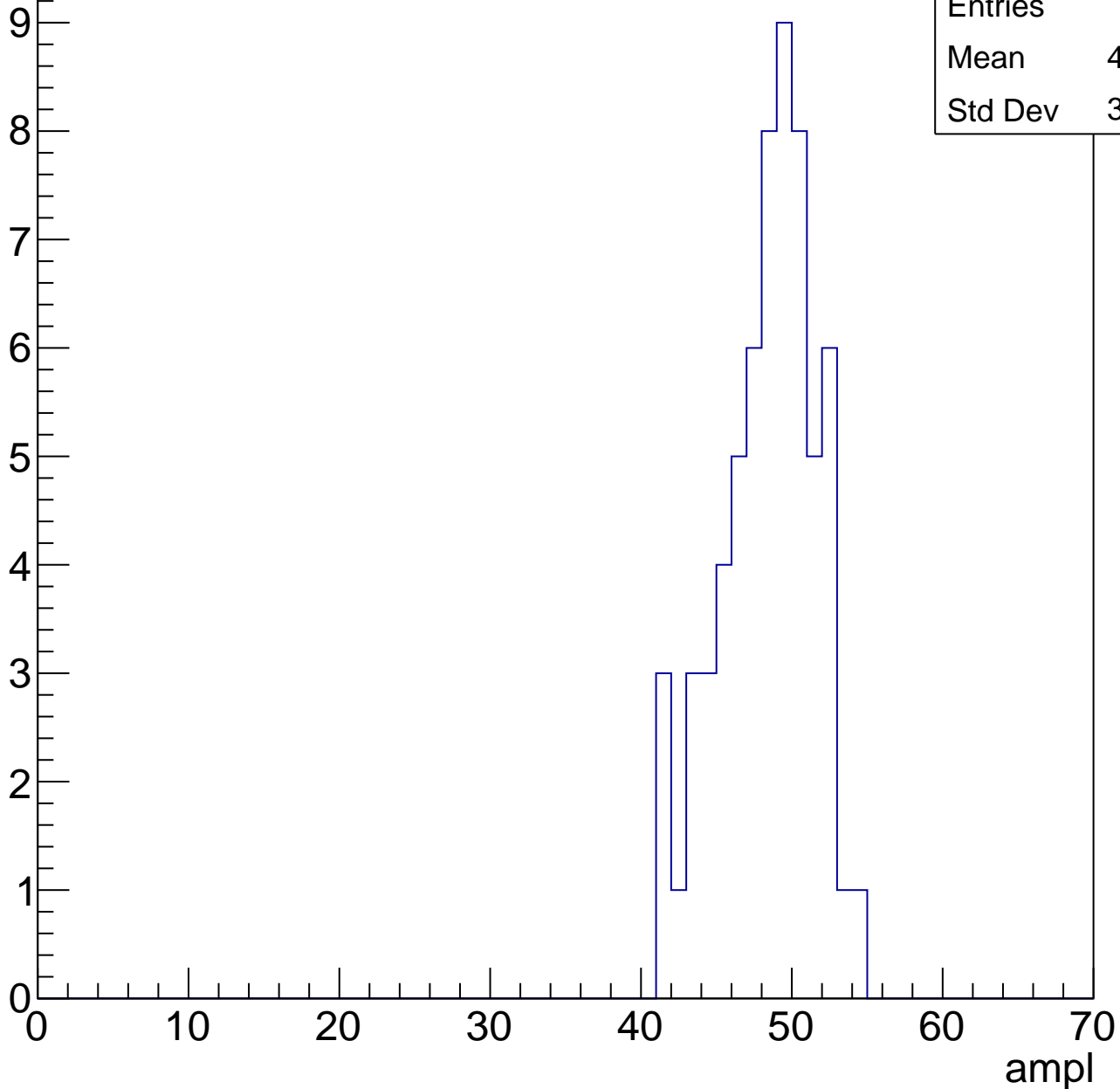


# B1L000S, U21-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	47.89
Std Dev	3.112

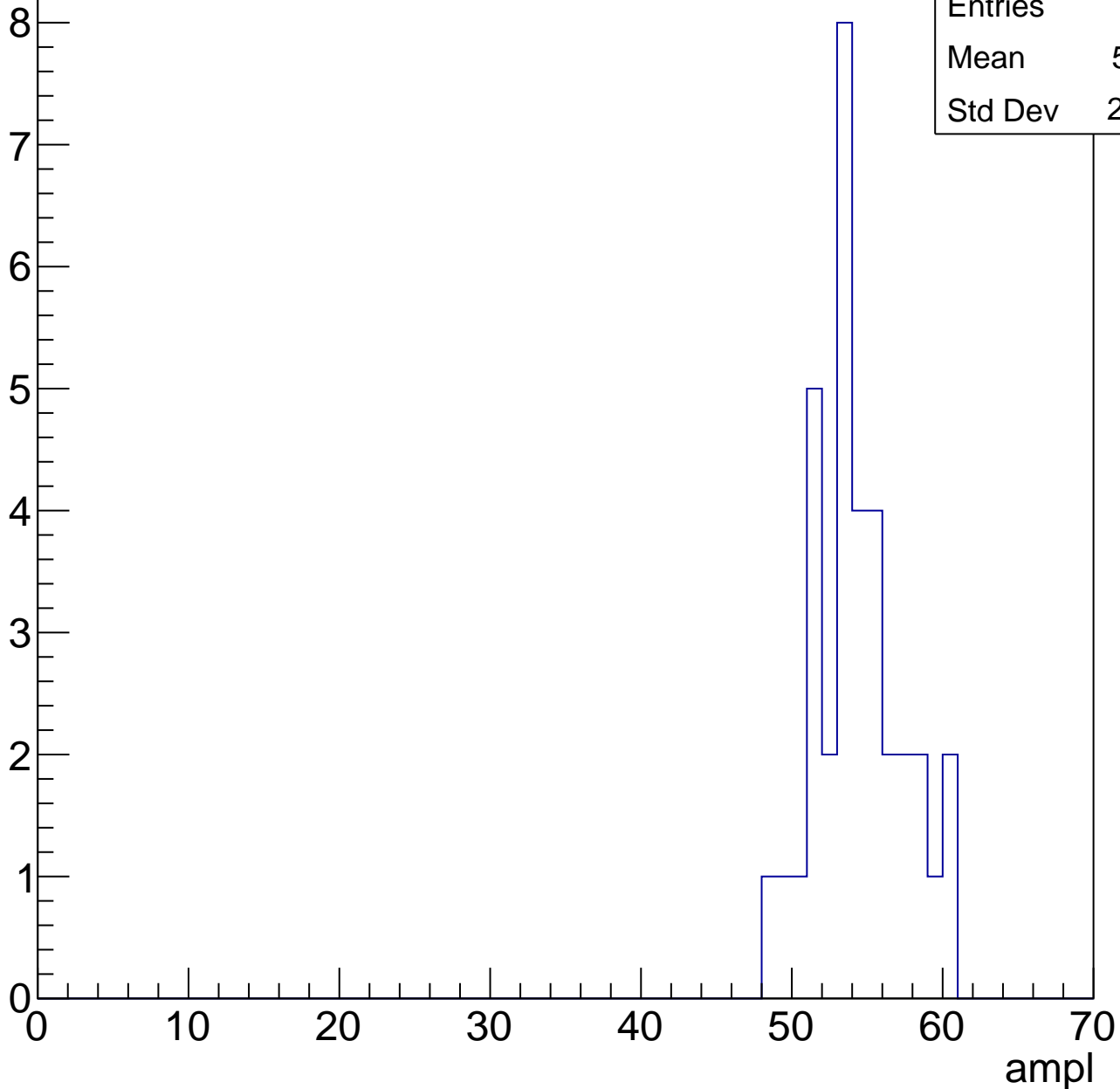


# B1L000S, U21-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	35
Mean	53.91
Std Dev	2.912

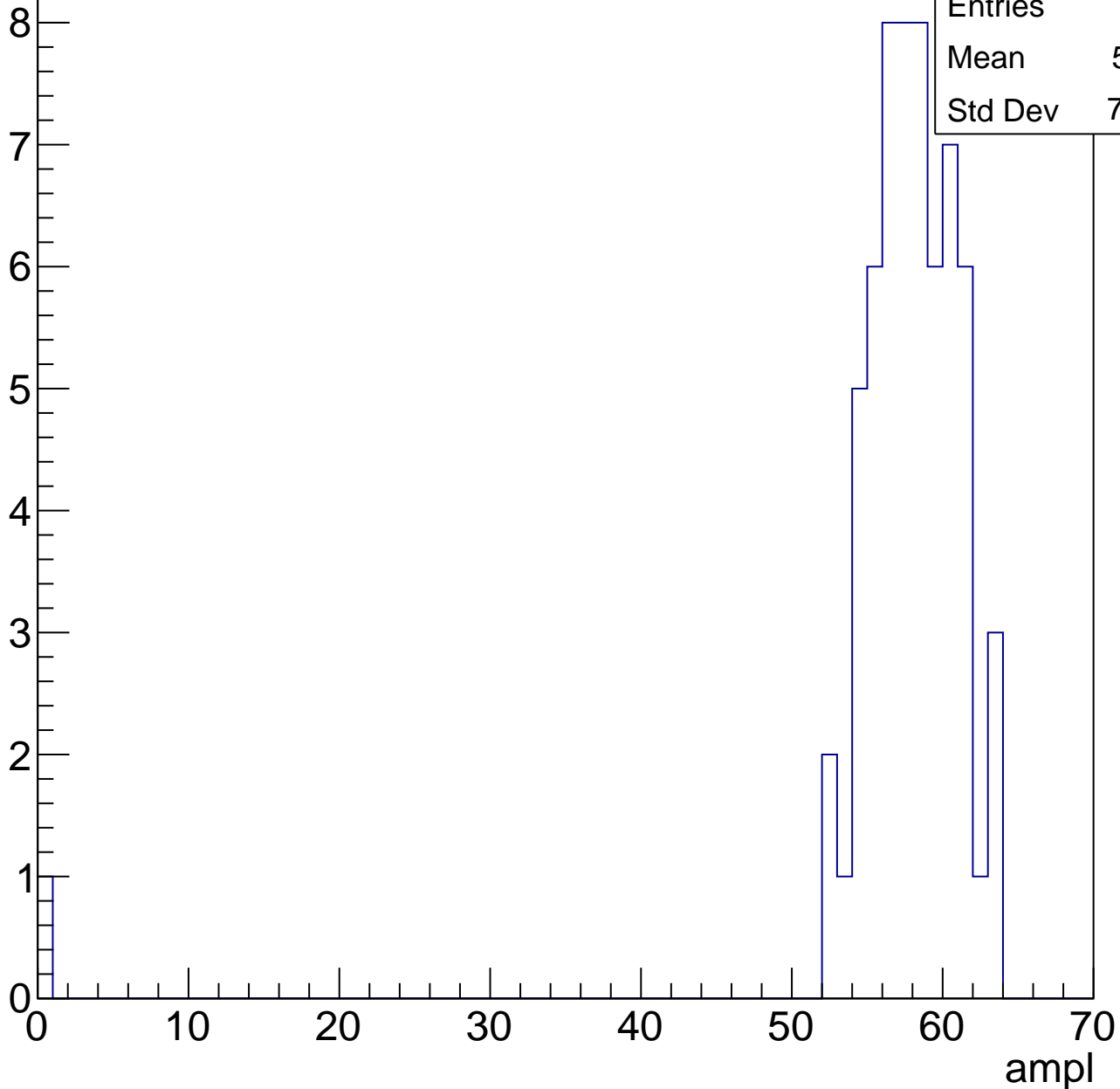


# B1L000S, U21-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	56.71
Std Dev	7.736

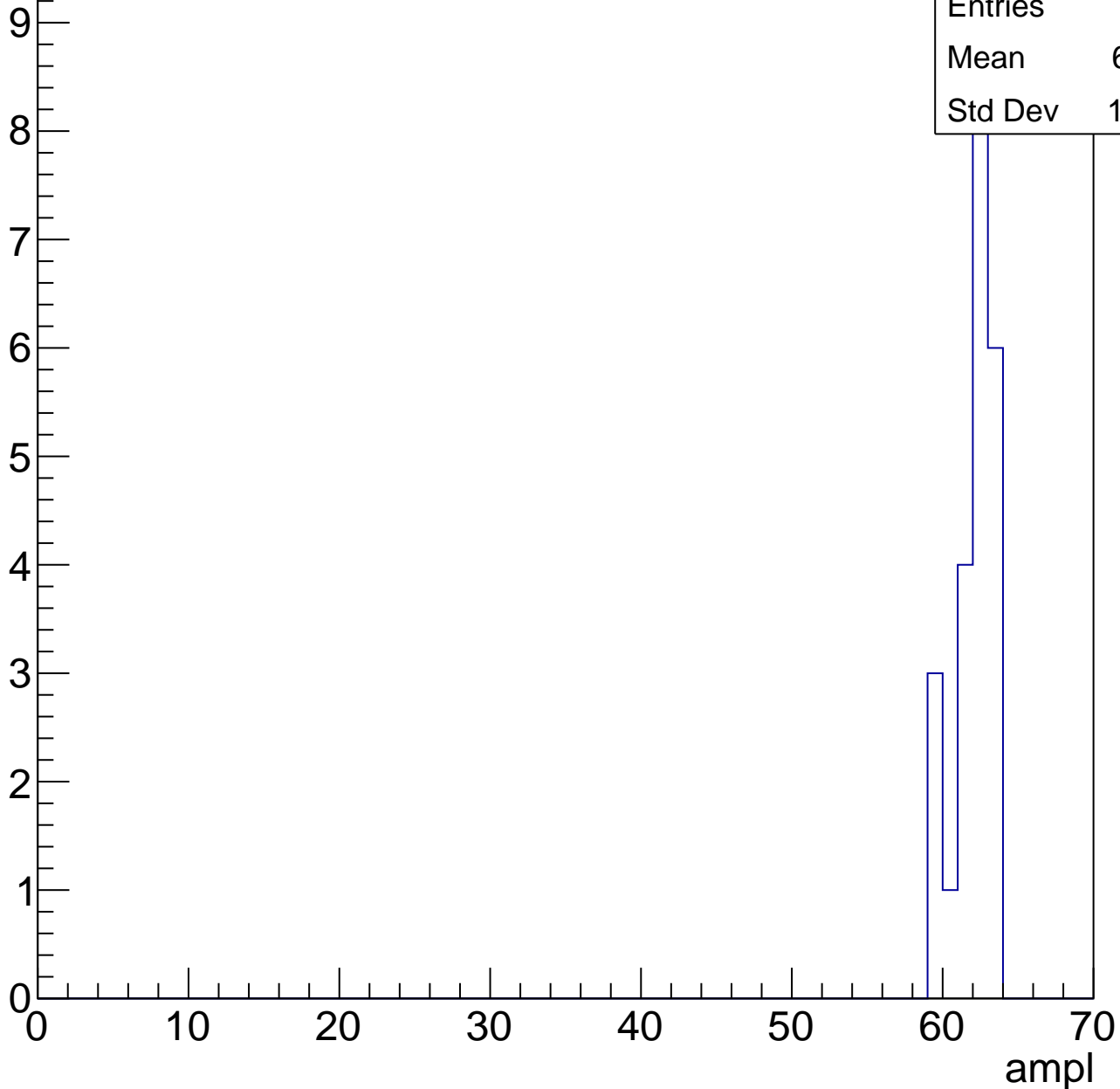


# B1L000S, U21-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	61.61
Std Dev	1.277

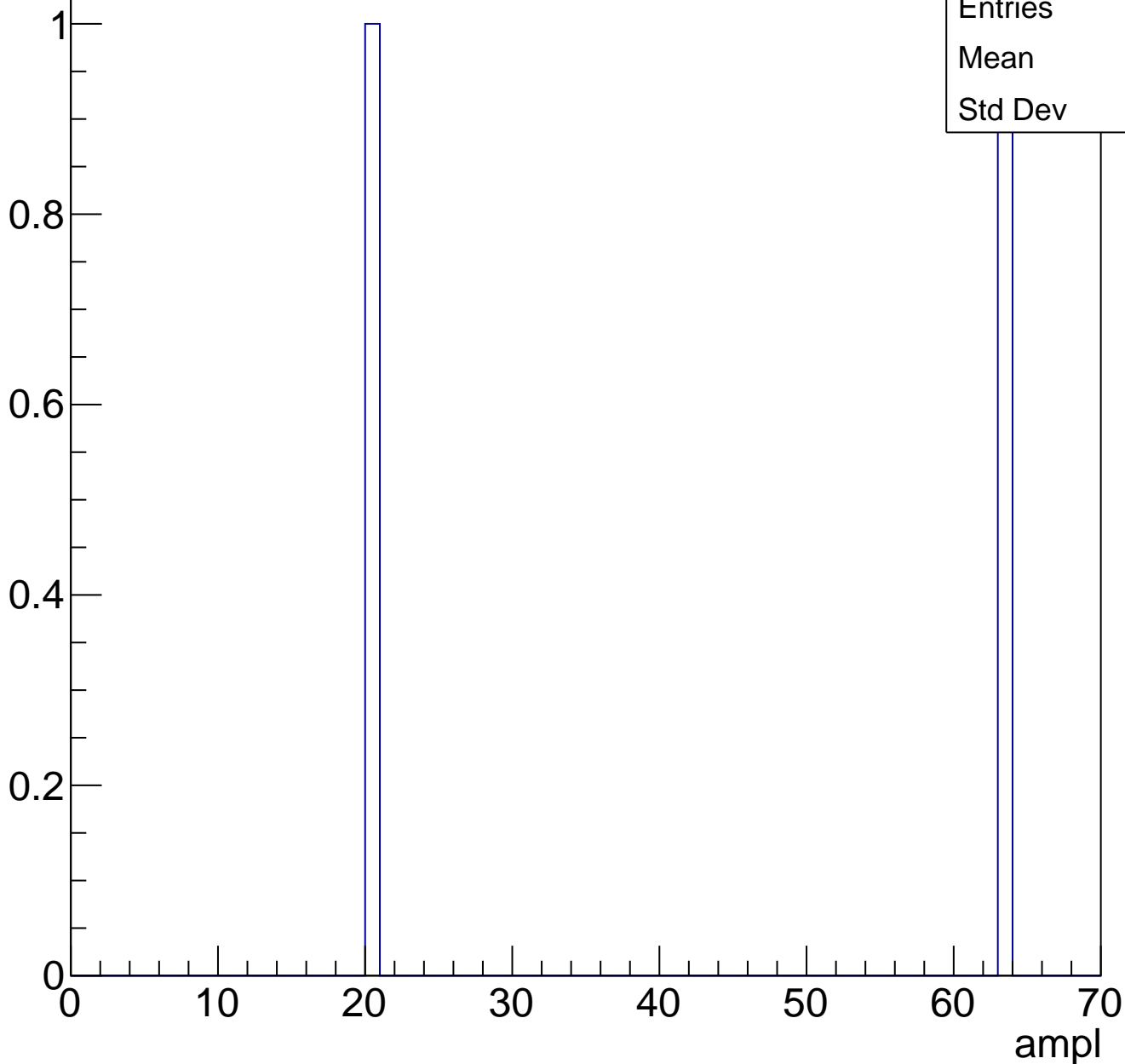




# B1L000S, U21-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch77, adc0

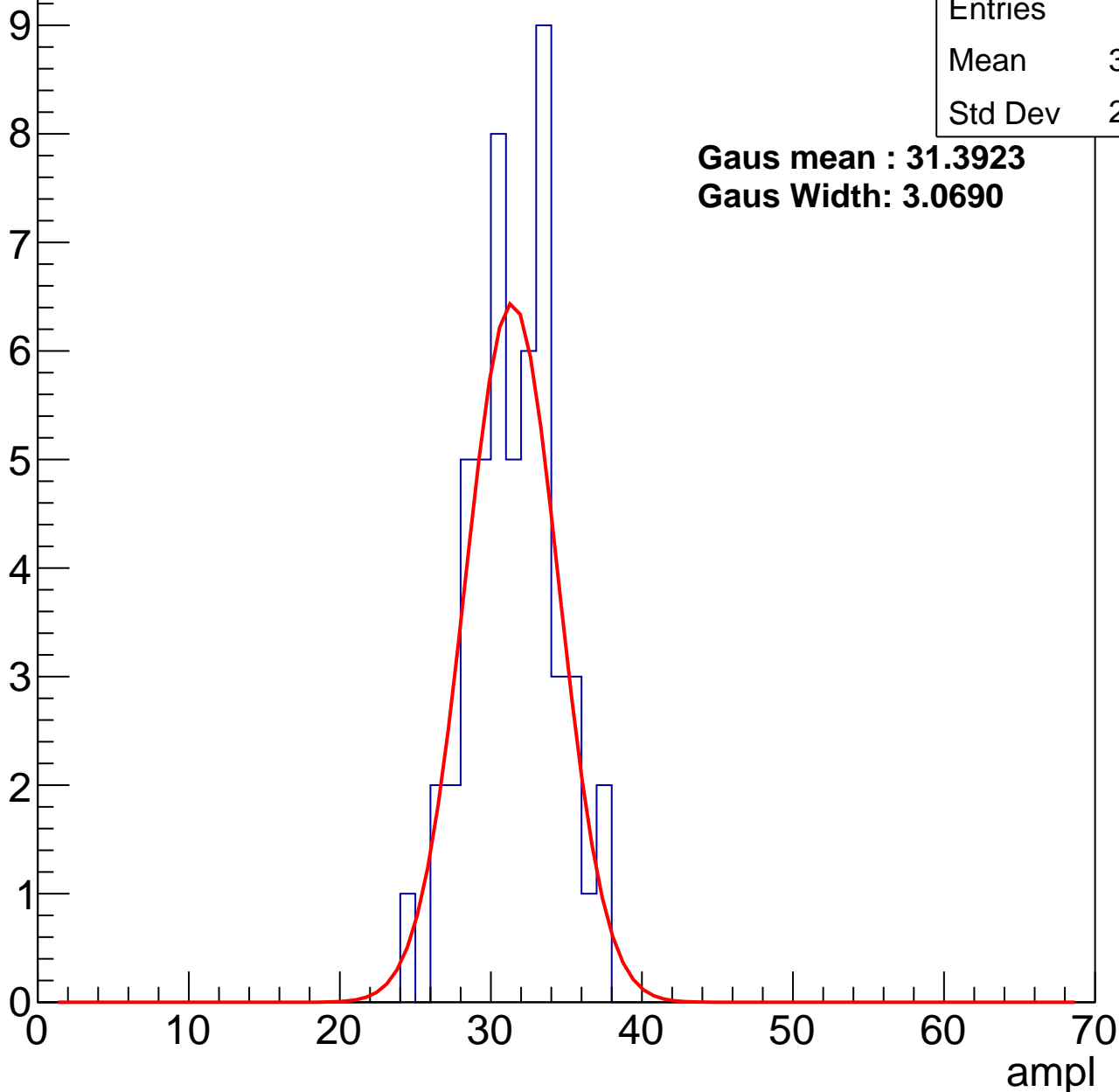
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	31.08
Std Dev	2.834

**Gaus mean : 31.3923**

**Gaus Width: 3.0690**



# B1L000S, U21-ch77, adc1

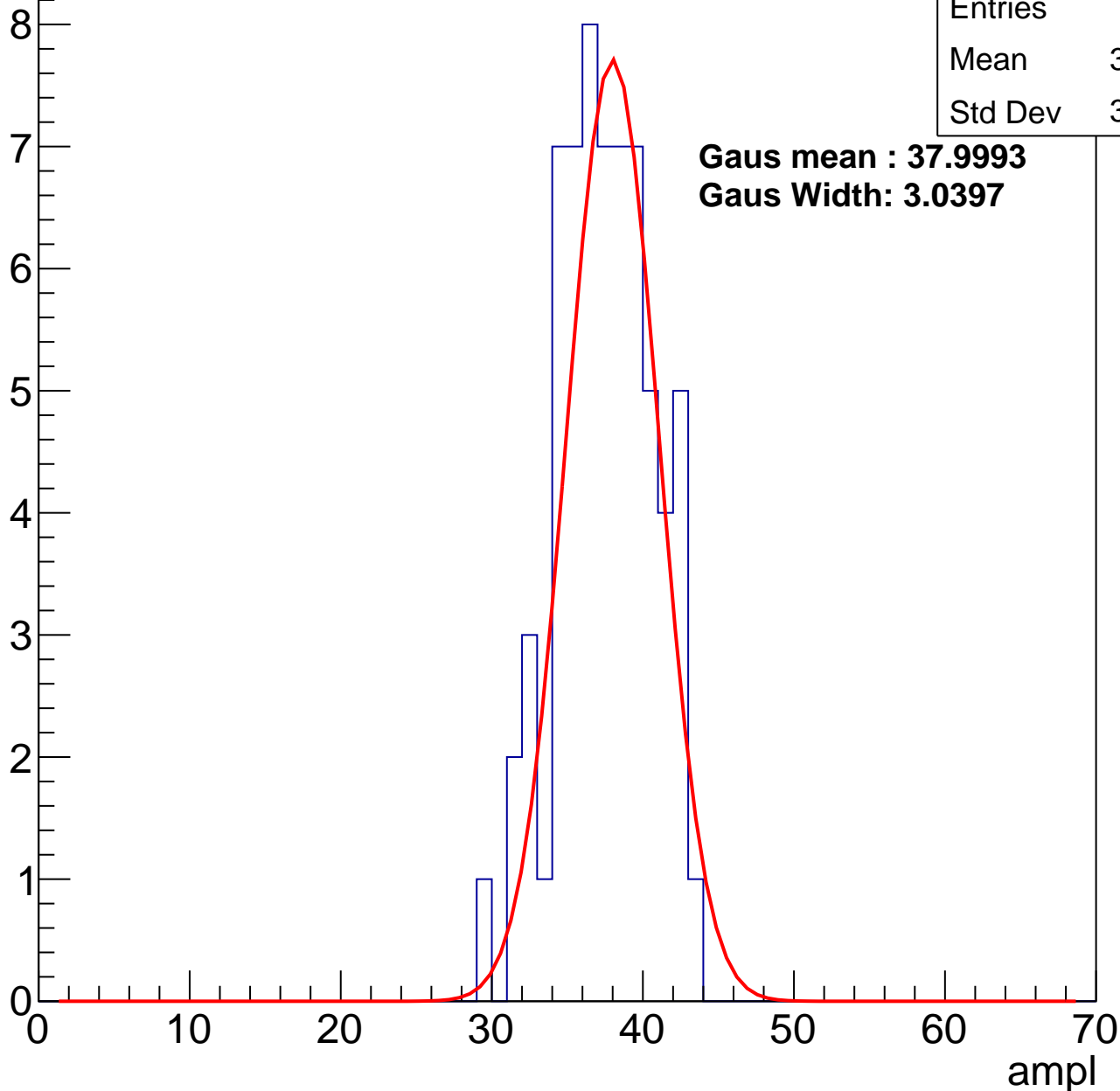
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	37.02
Std Dev	3.116

**Gaus mean : 37.9993**

**Gaus Width: 3.0397**



# B1L000S, U21-ch77, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	43.4
Std Dev	3.091

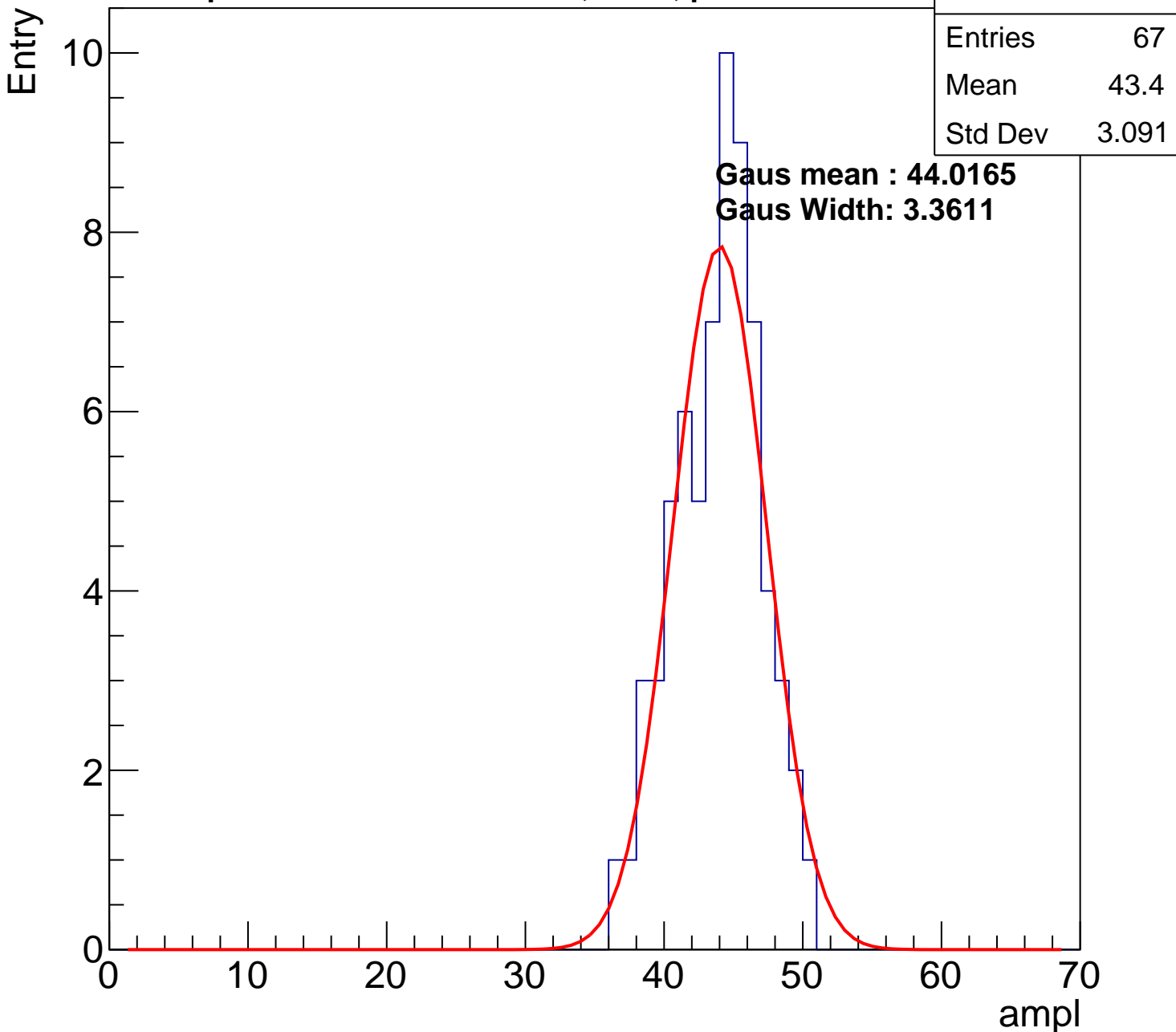
**Gaus mean : 44.0165**

**Gaus Width: 3.3611**

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

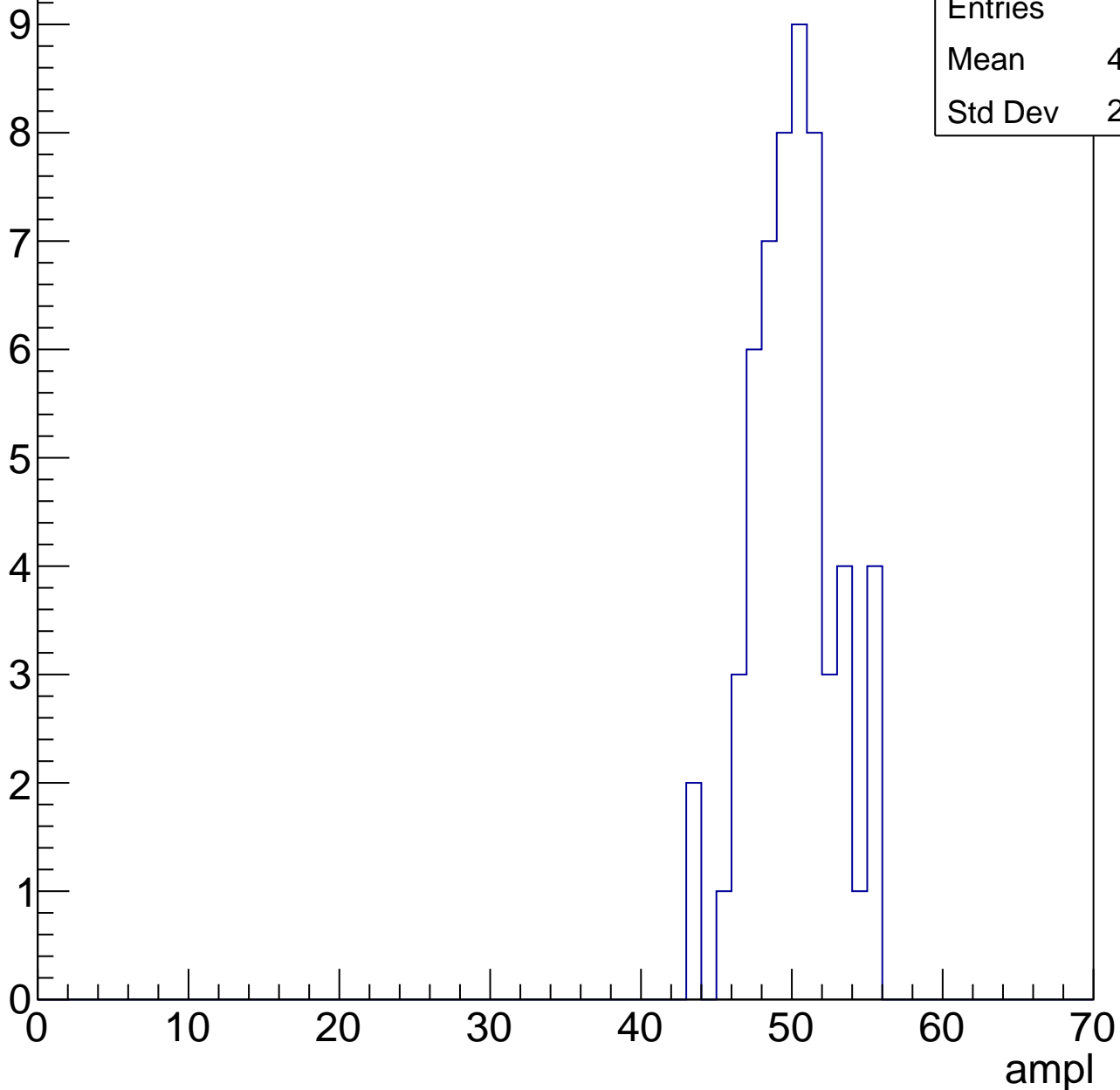


# B1L000S, U21-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	49.62
Std Dev	2.755



# B1L000S, U21-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

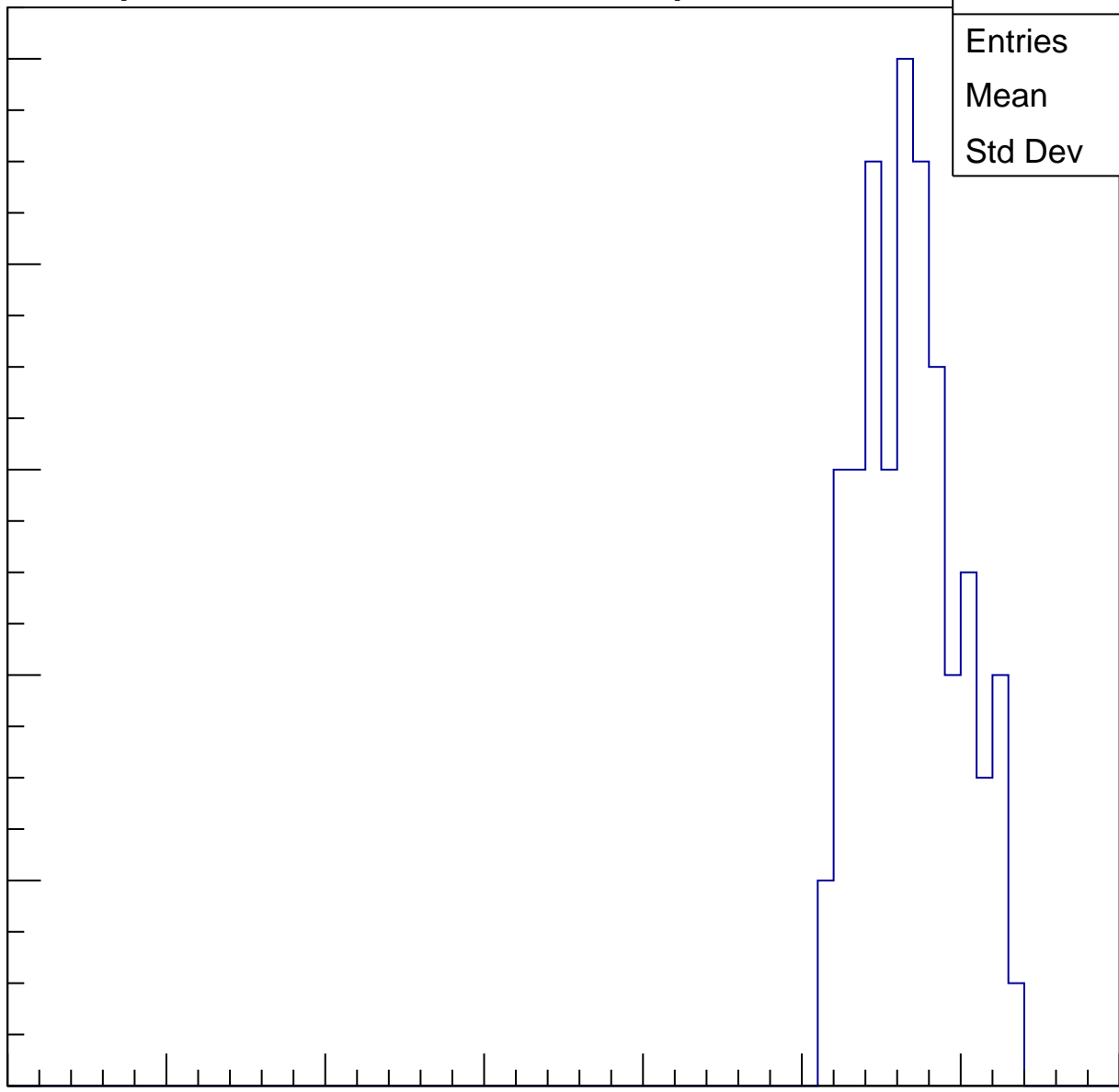
Entries	72
Mean	56.35
Std Dev	3.015

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

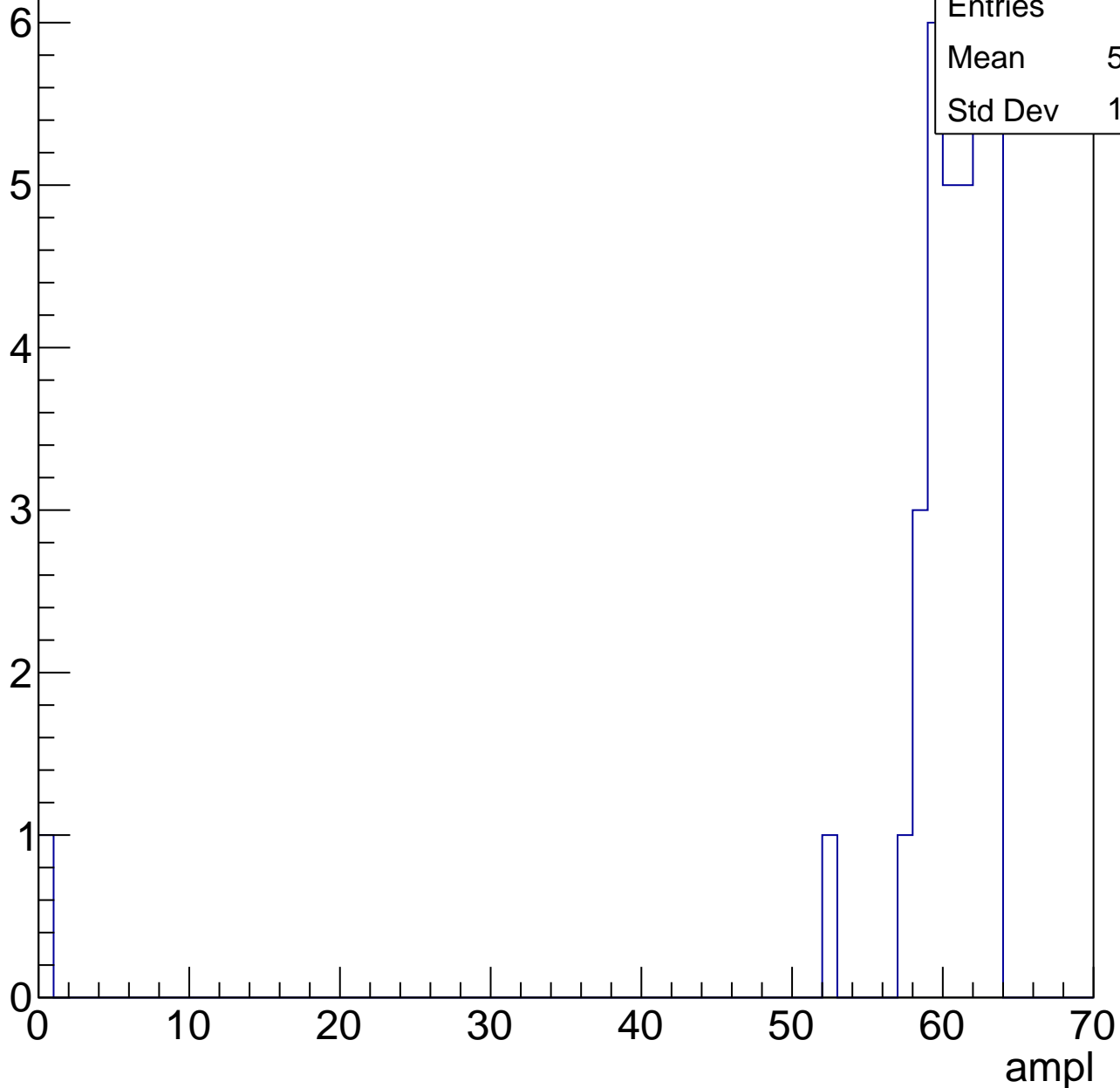
ampl



# B1L000S, U21-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch78, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	57
Mean	29.7
Std Dev	4.91

**Gaus mean : 30.1137**

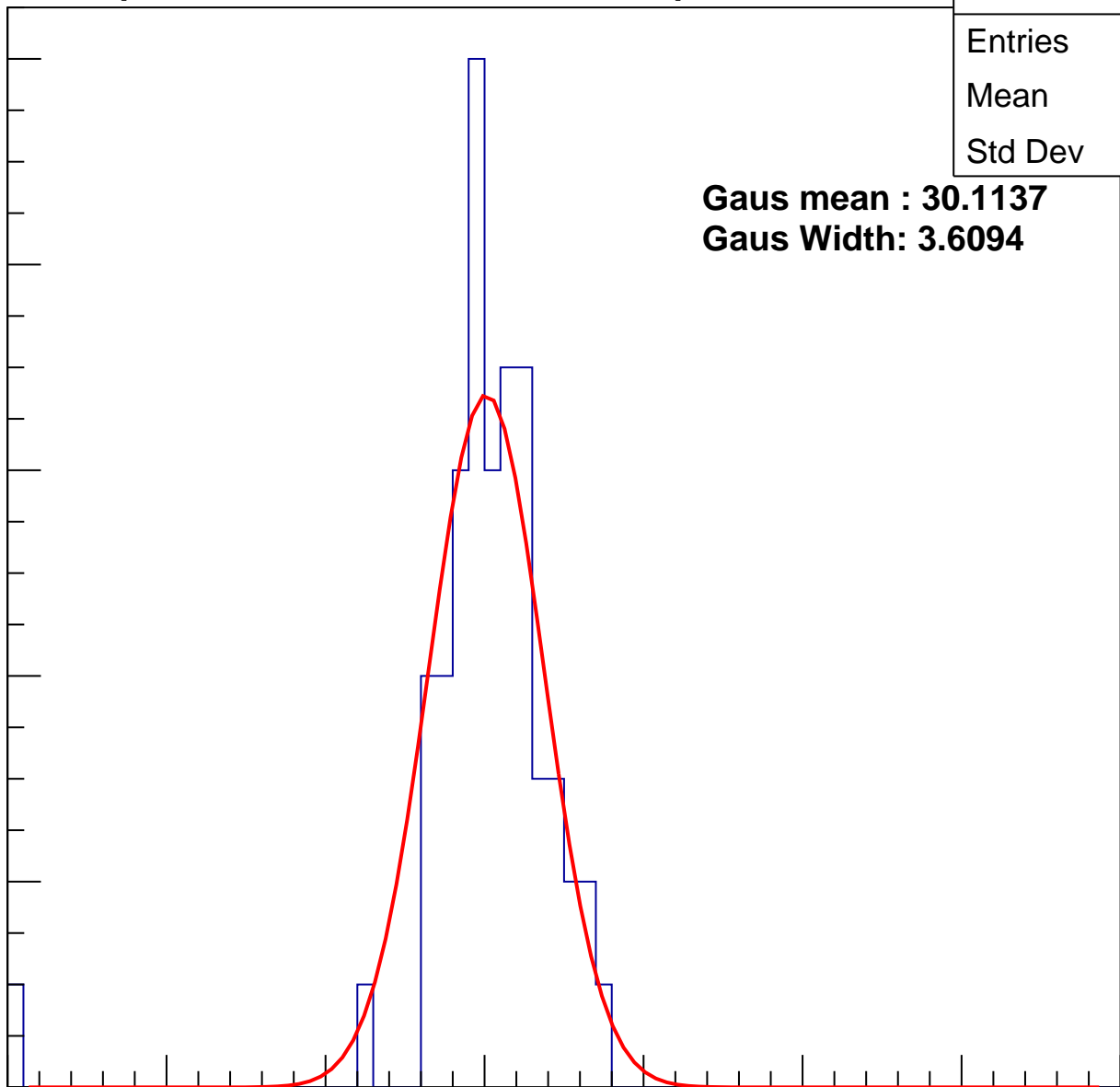
**Gaus Width: 3.6094**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



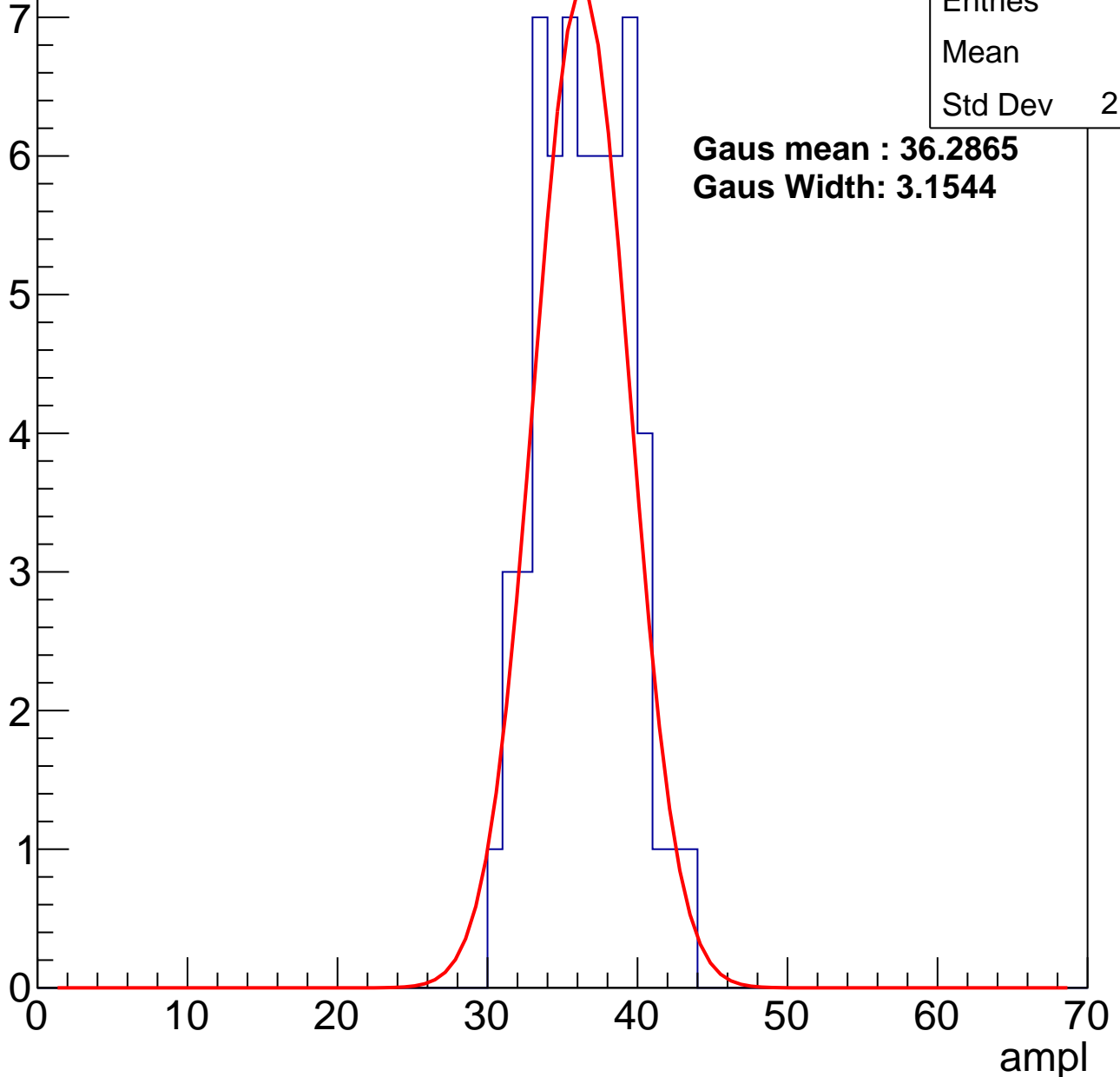
# B1L000S, U21-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	36
Std Dev	2.969

**Gaus mean : 36.2865**  
**Gaus Width: 3.1544**



# B1L000S, U21-ch78, adc2

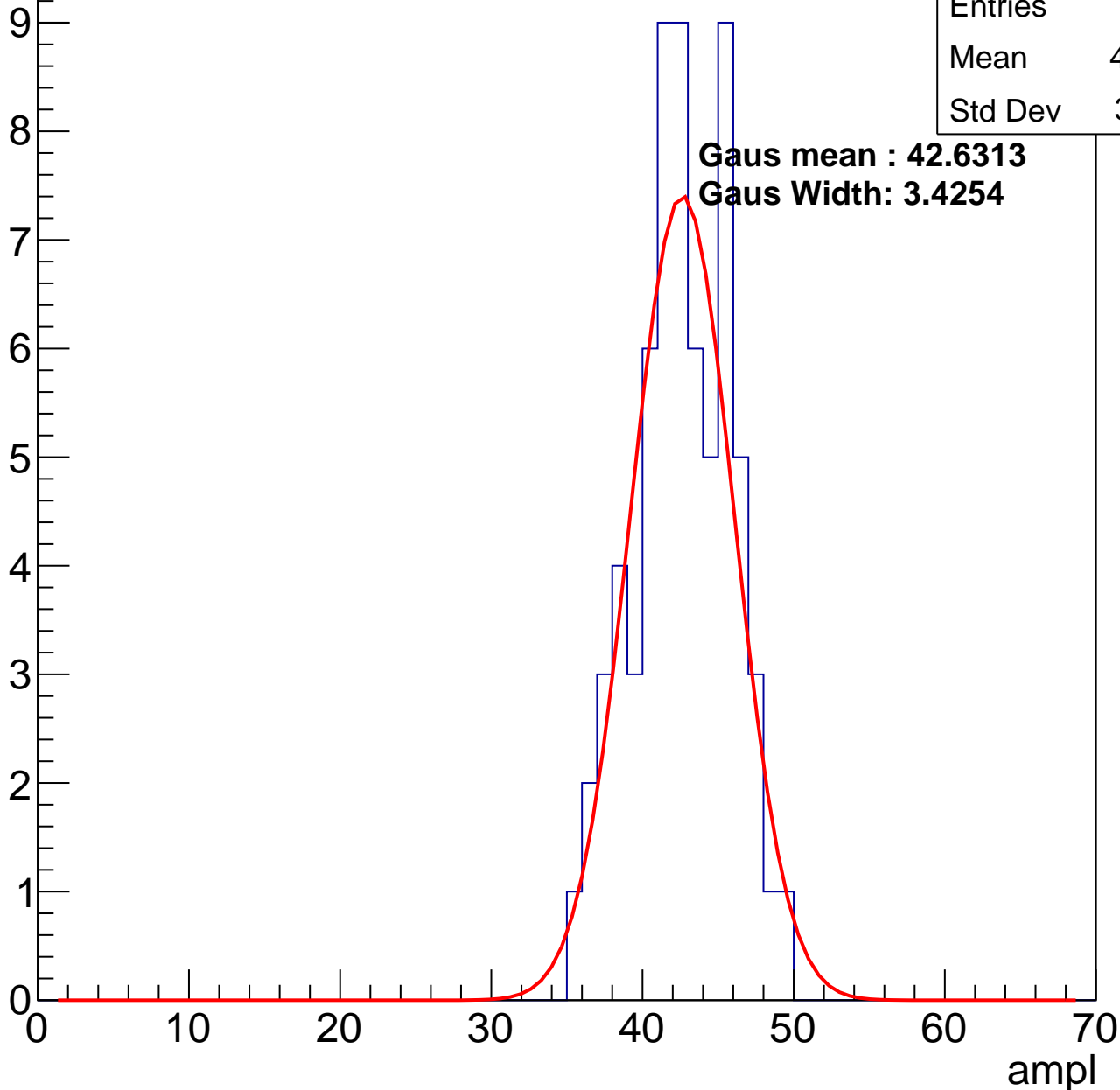
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.16
Std Dev	3.151

**Gaus mean : 42.6313**

**Gaus Width: 3.4254**

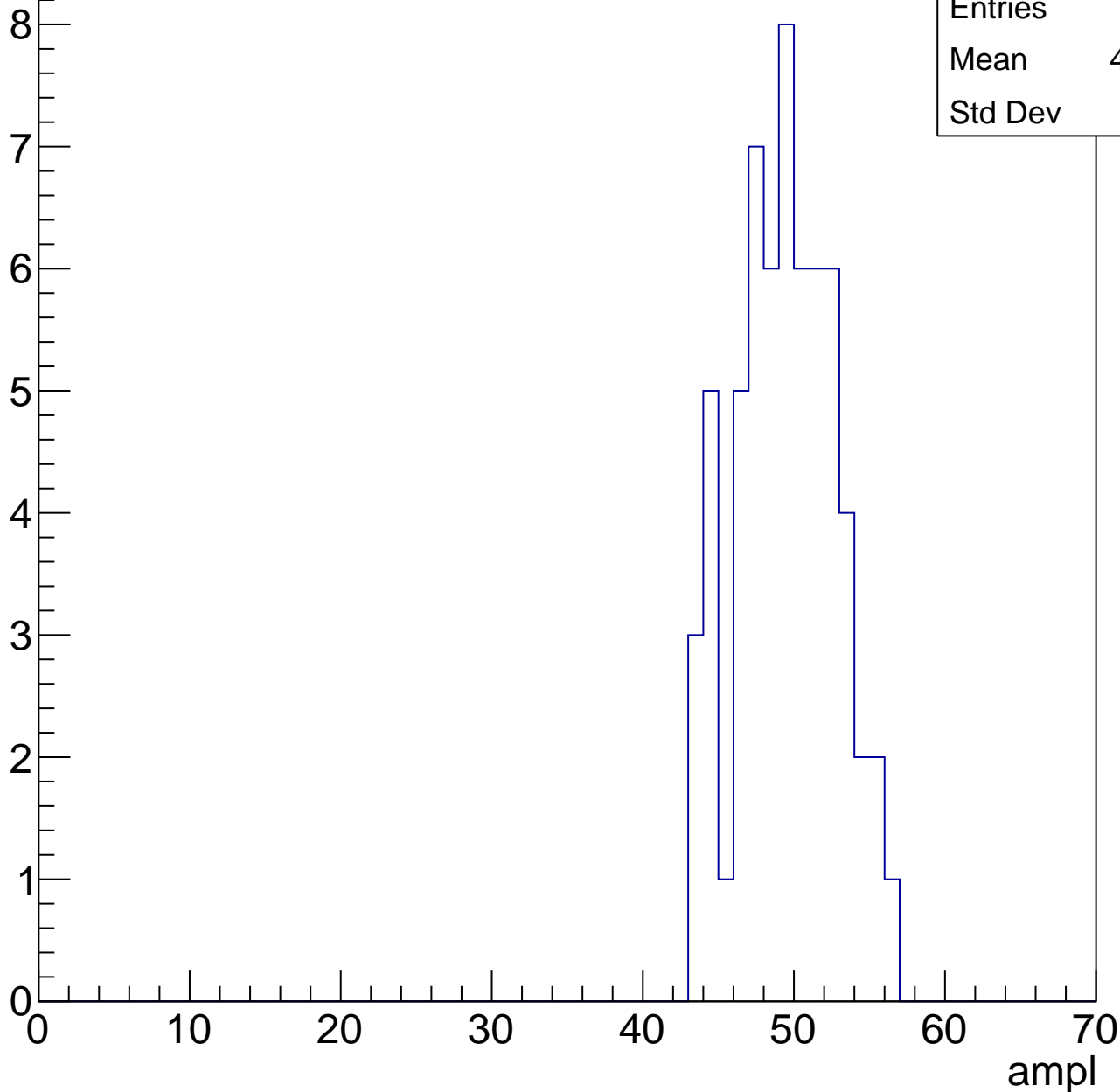


# B1L000S, U21-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	48.98
Std Dev	3.23

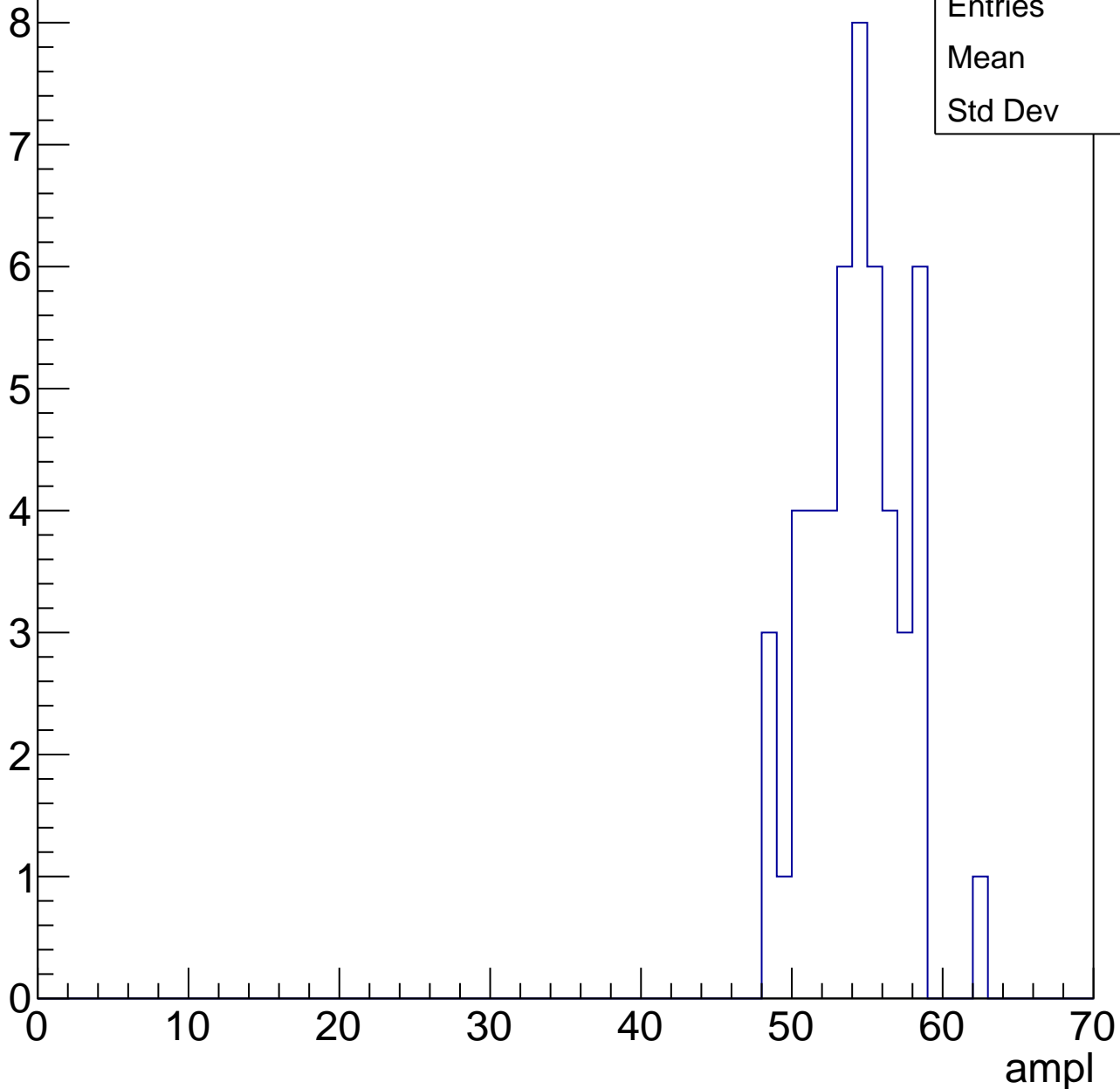


# B1L000S, U21-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	53.8
Std Dev	3.04



# B1L000S, U21-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	59.46
Std Dev	2.268

Entry

10

8

6

4

2

0

0

10

20

30

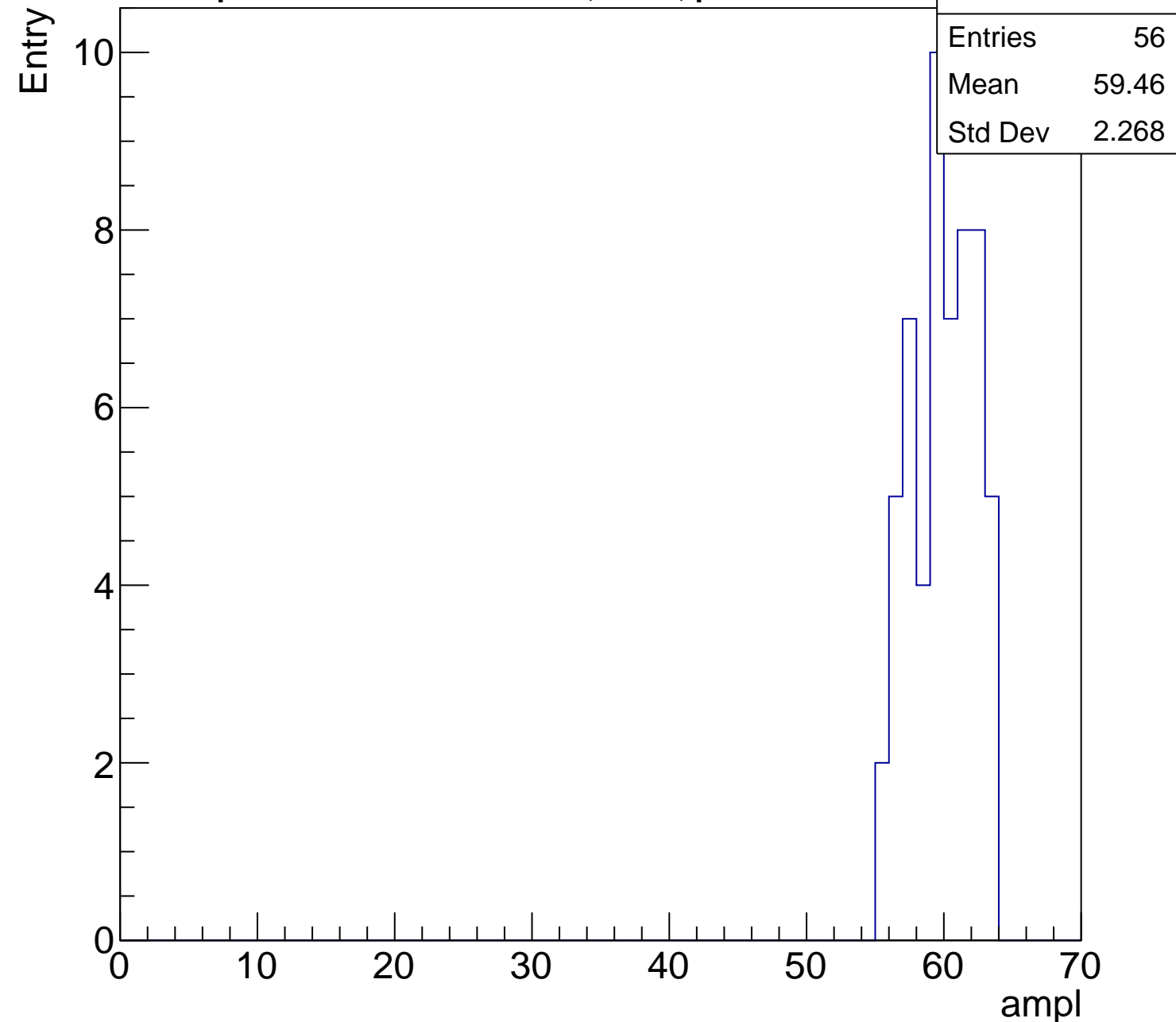
40

50

60

ampl

70

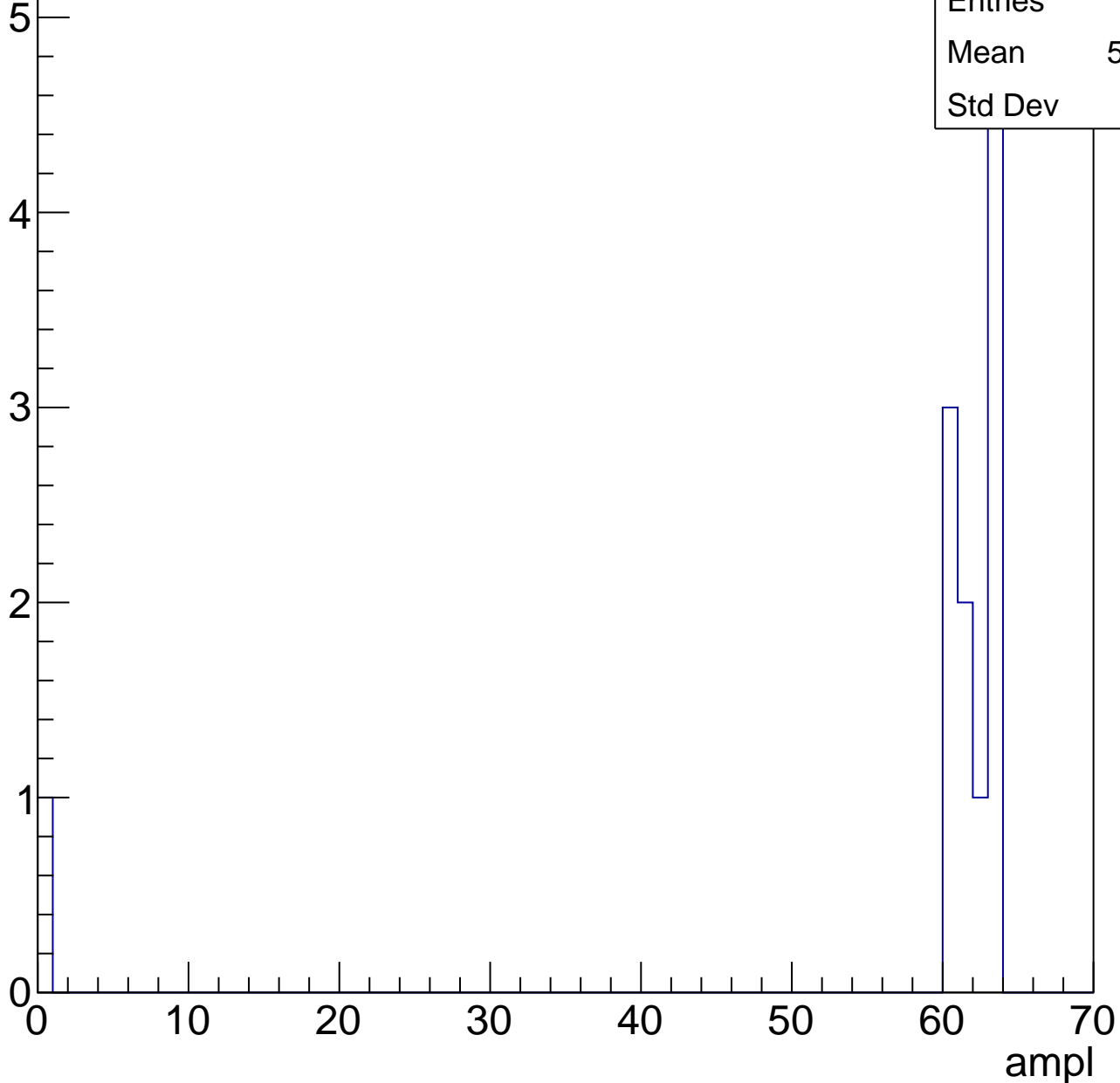


# B1L000S, U21-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	56.58
Std Dev	17.1





# B1L000S, U21-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch79, adc0

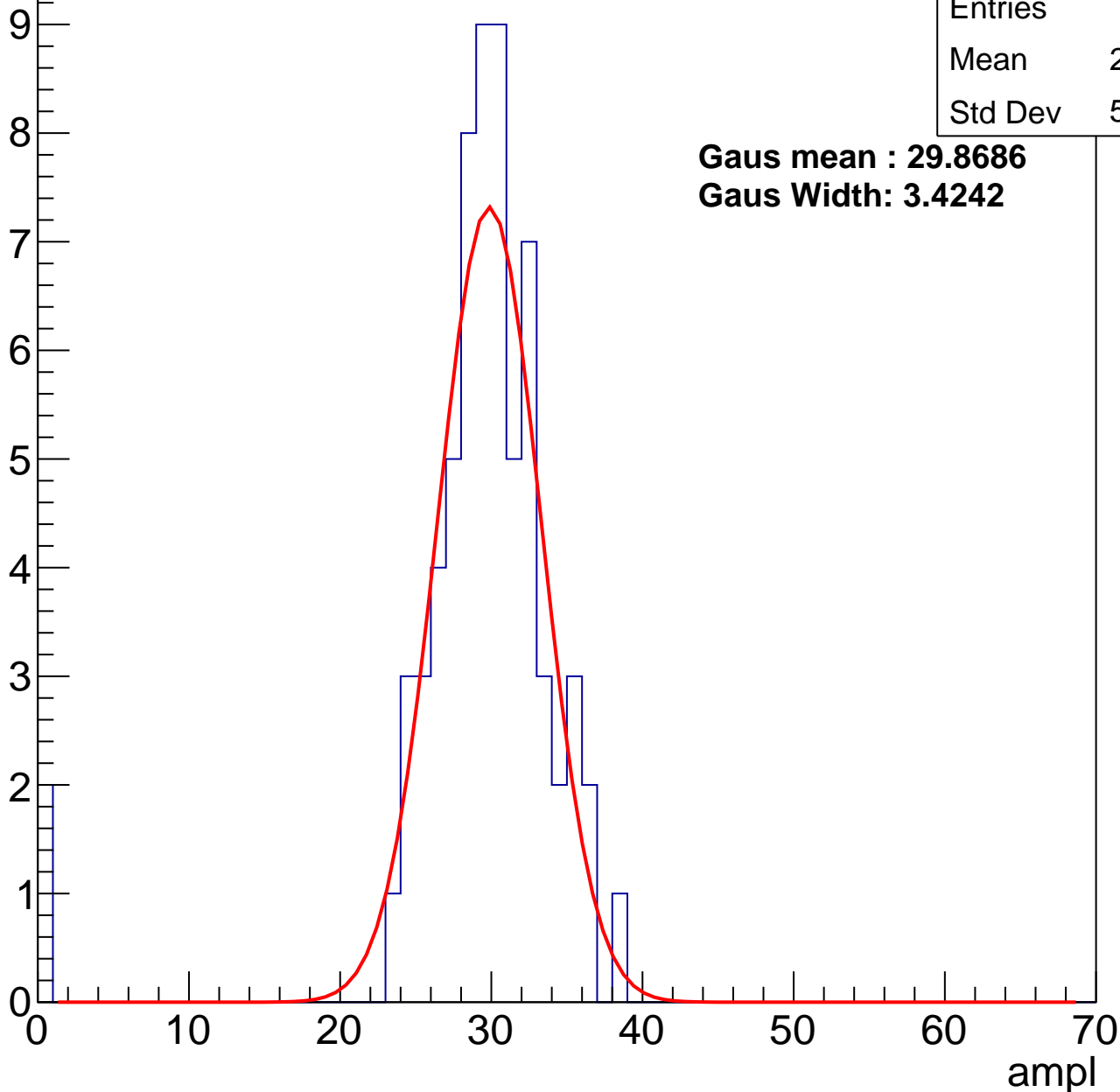
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.73
Std Dev	5.957

**Gaus mean : 29.8686**

**Gaus Width: 3.4242**



# B1L000S, U21-ch79, adc1

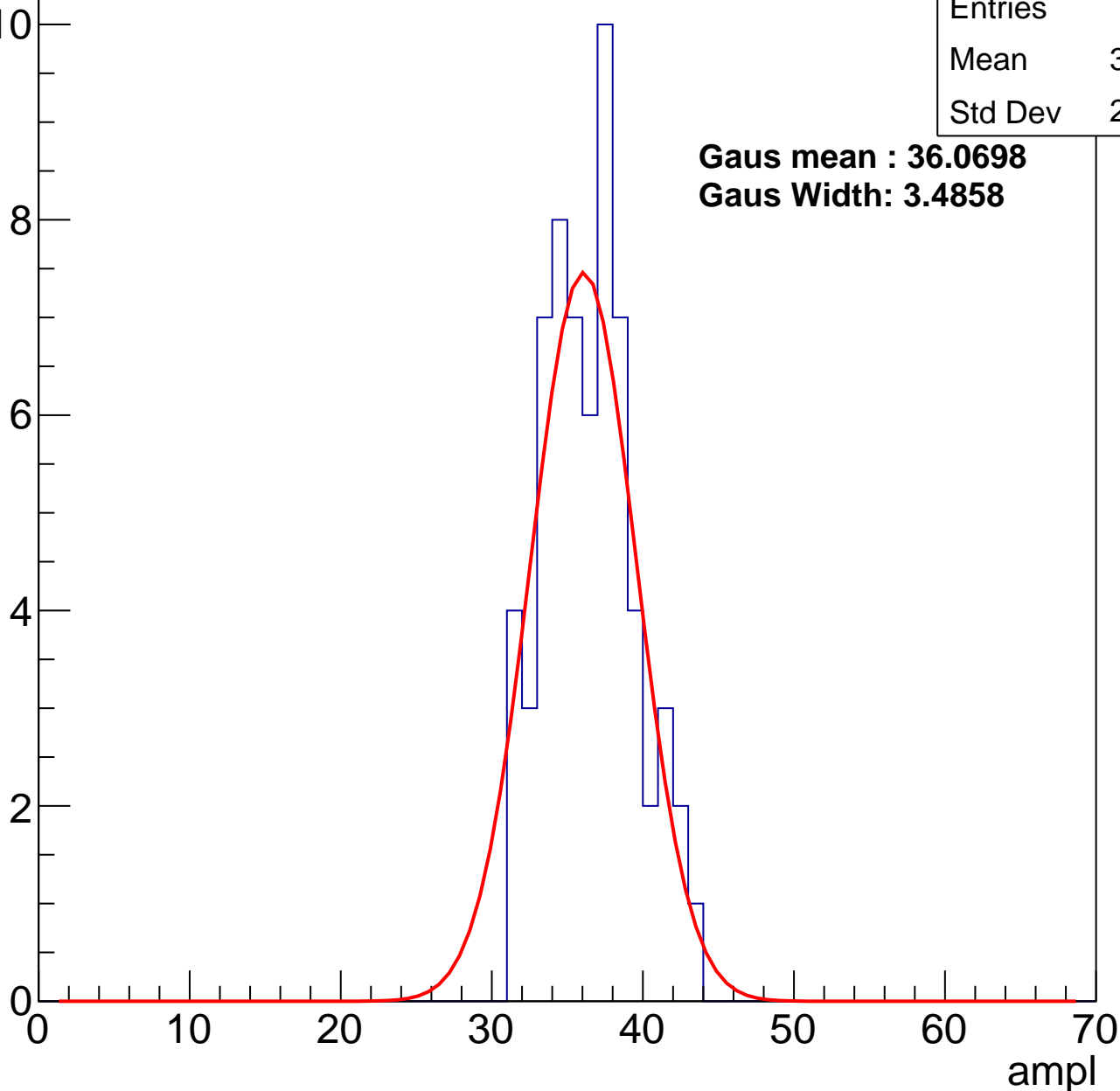
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	36.03
Std Dev	2.937

**Gaus mean : 36.0698**

**Gaus Width: 3.4858**



# B1L000S, U21-ch79, adc2

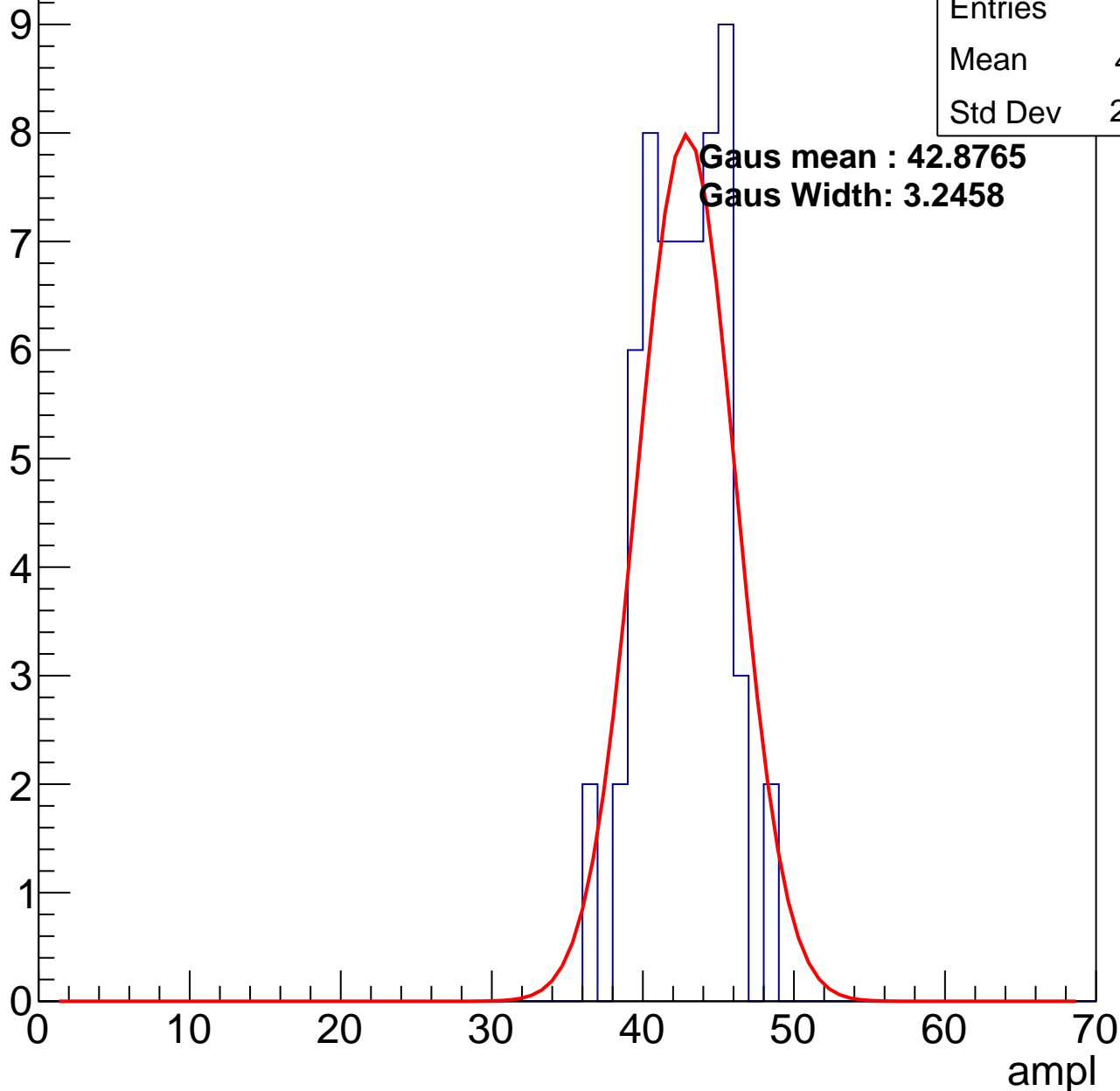
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.21
Std Dev	2.668

**Gaus mean : 42.8765**

**Gaus Width: 3.2458**



# B1L000S, U21-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	82
Mean	49.7
Std Dev	3.29

Entry

10

8

6

4

2

0

0

10

20

30

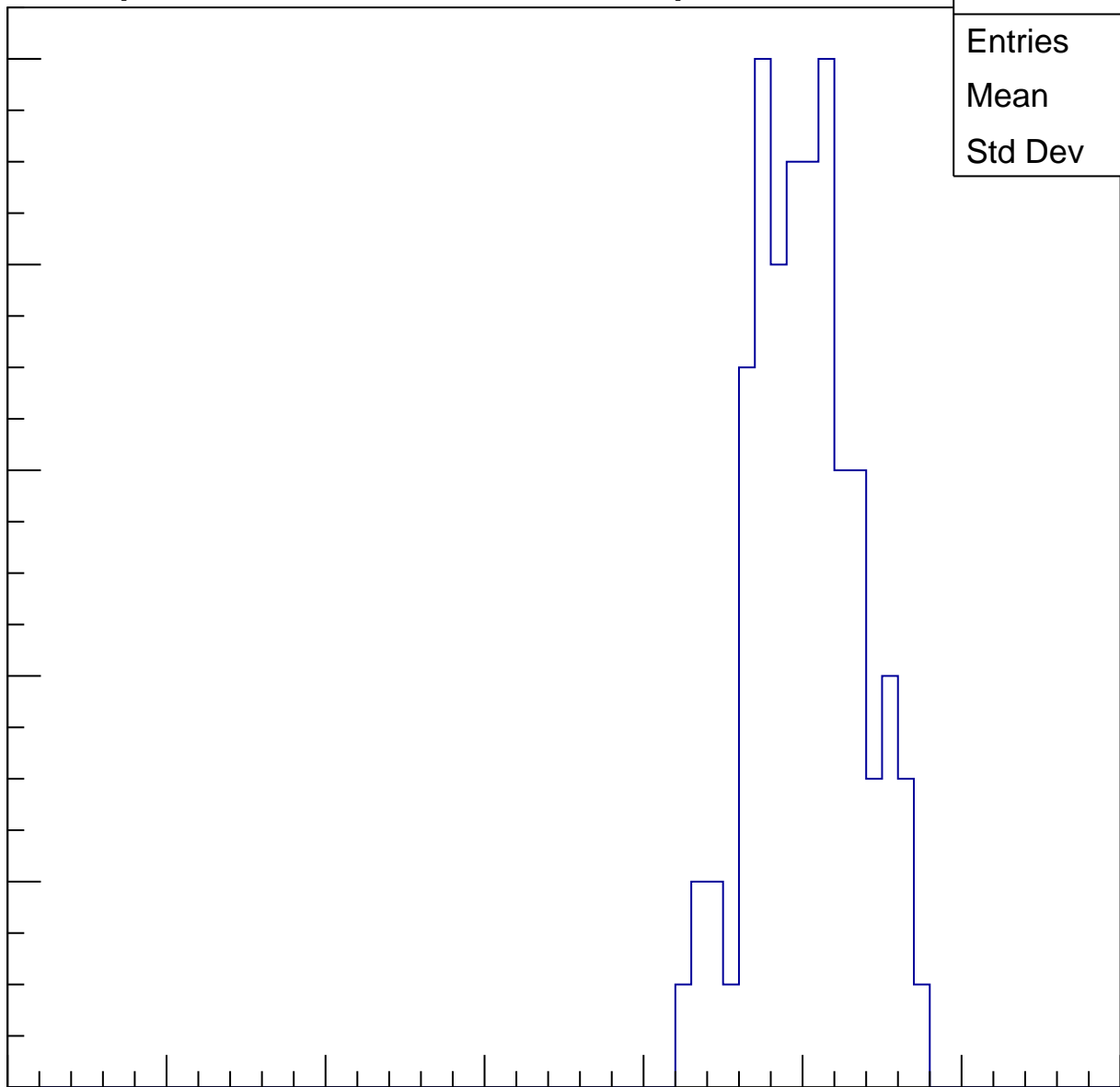
40

50

60

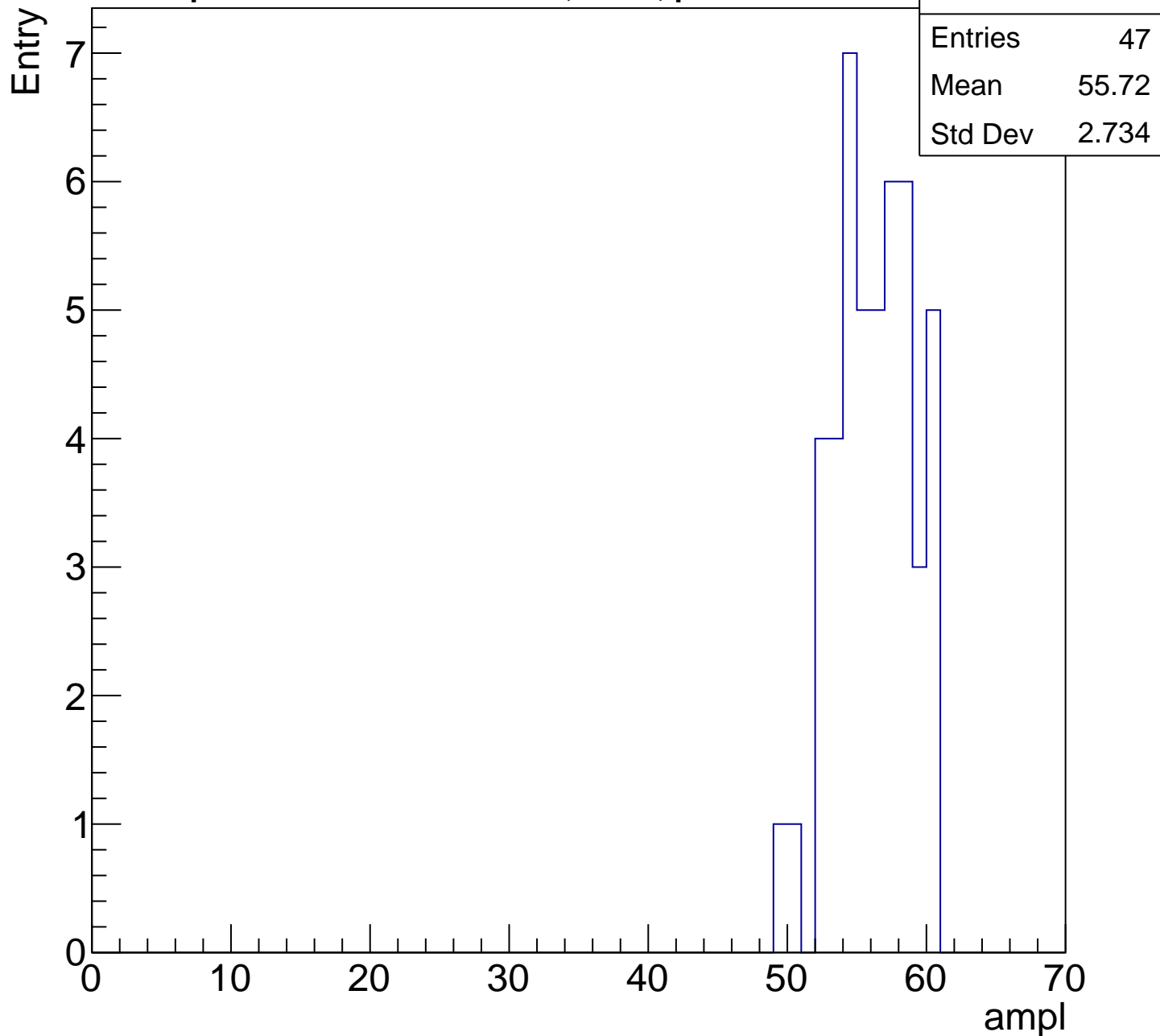
70

ampl



# B1L000S, U21-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

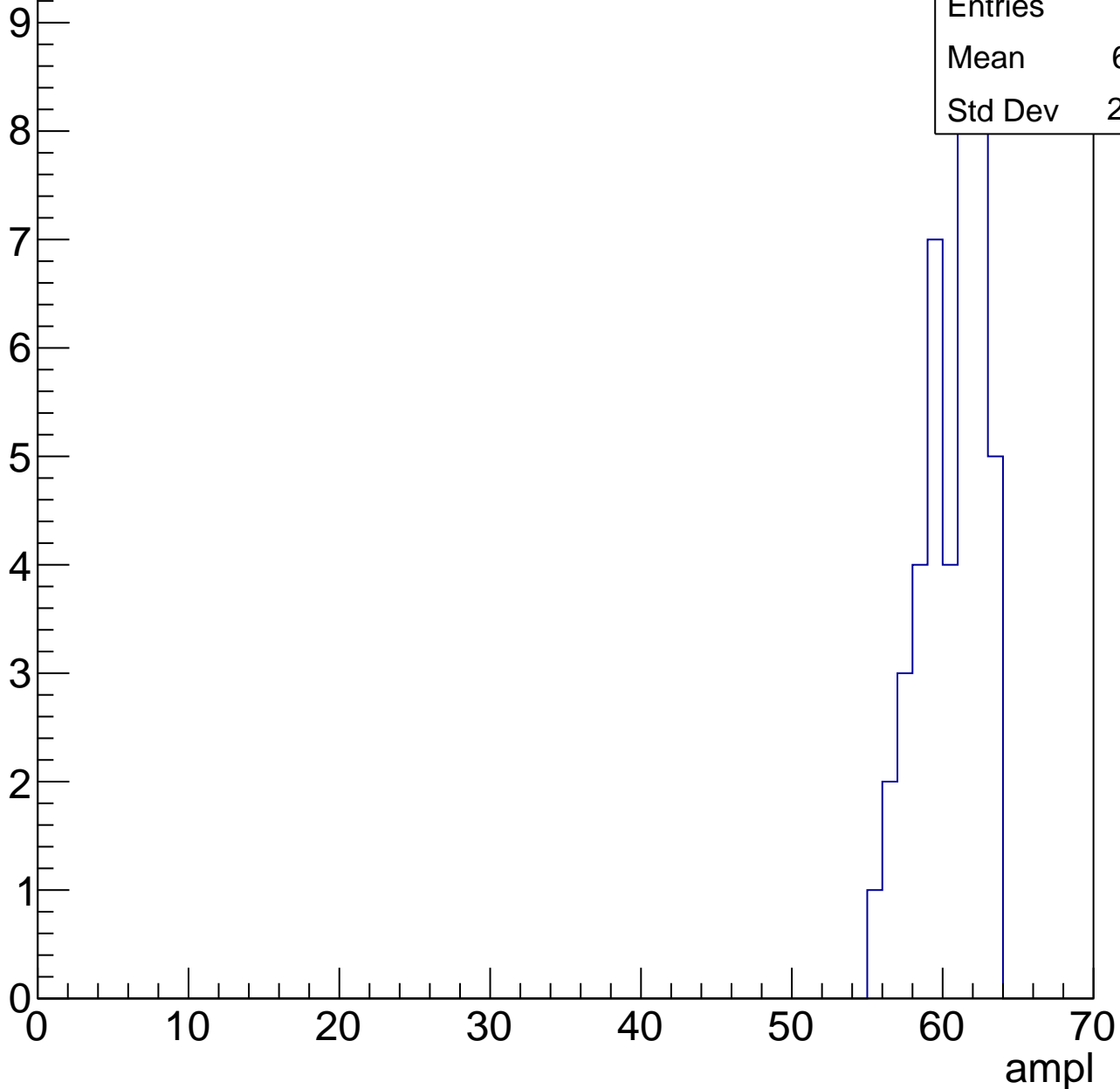


# B1L000S, U21-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	60.11
Std Dev	2.113

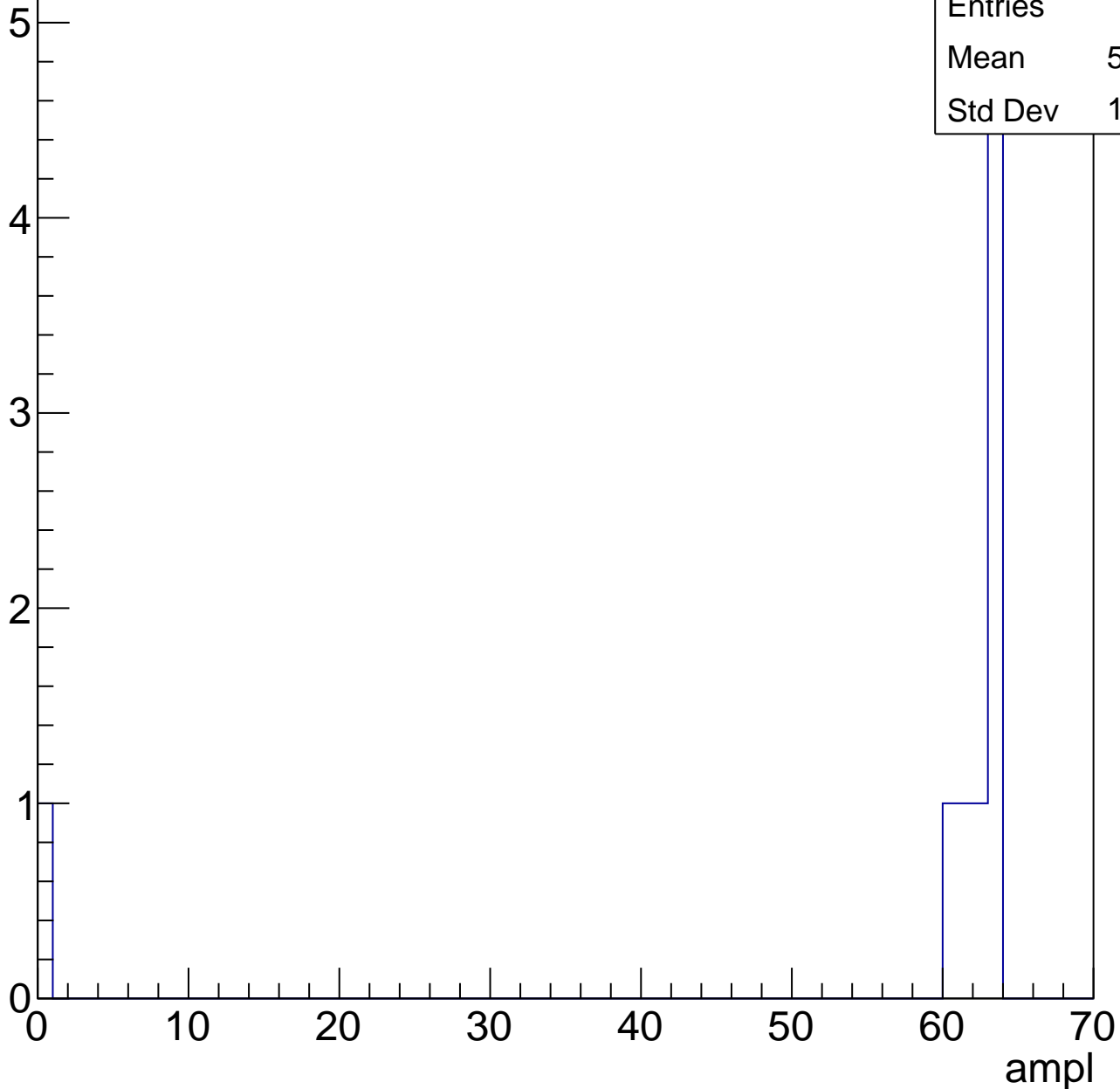


# B1L000S, U21-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	9
Mean	55.33
Std Dev	19.59





# B1L000S, U21-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch80, adc0

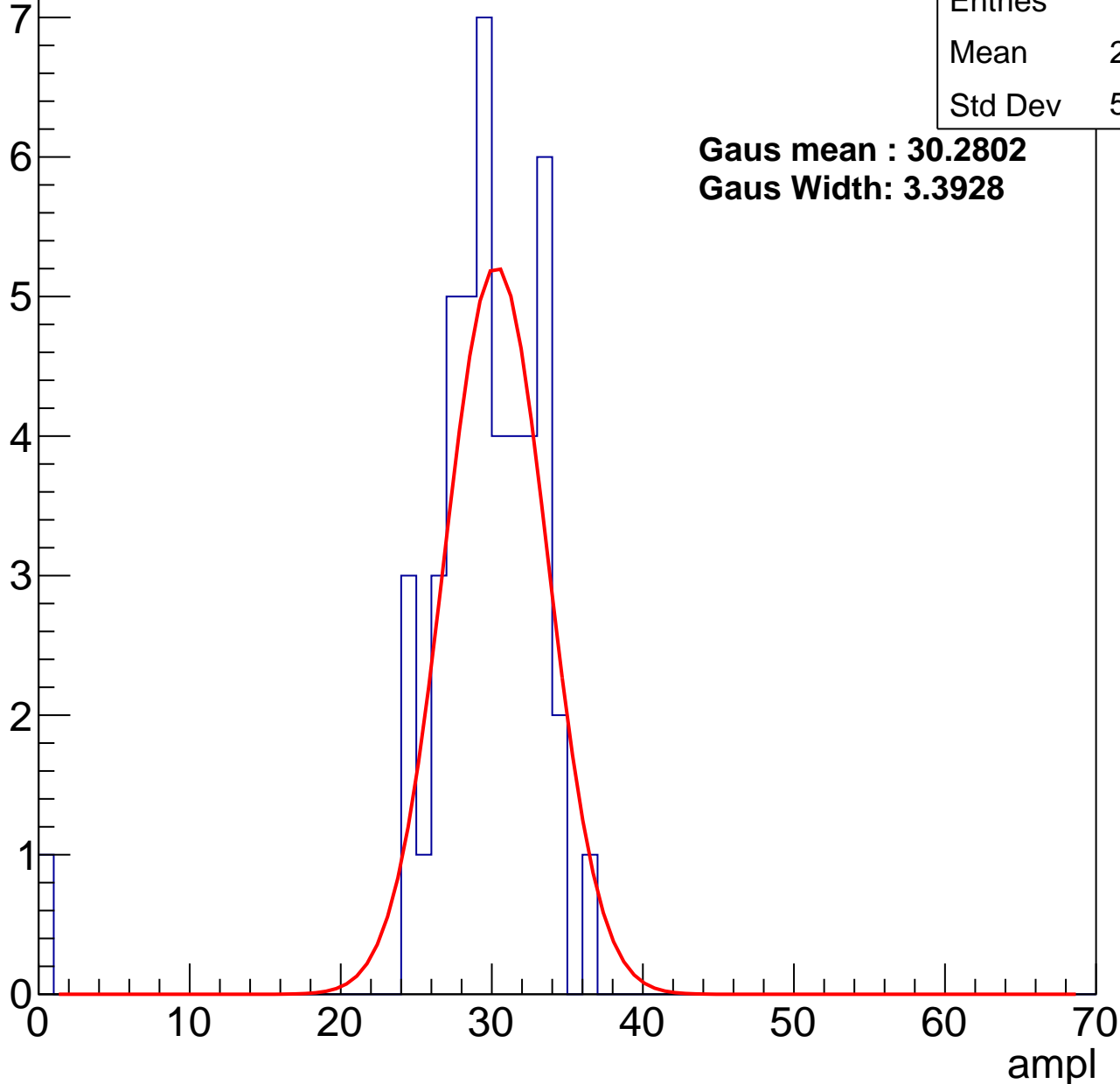
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	28.85
Std Dev	5.179

**Gaus mean : 30.2802**

**Gaus Width: 3.3928**



# B1L000S, U21-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	35.65
Std Dev	3.578

**Gaus mean : 35.9174**

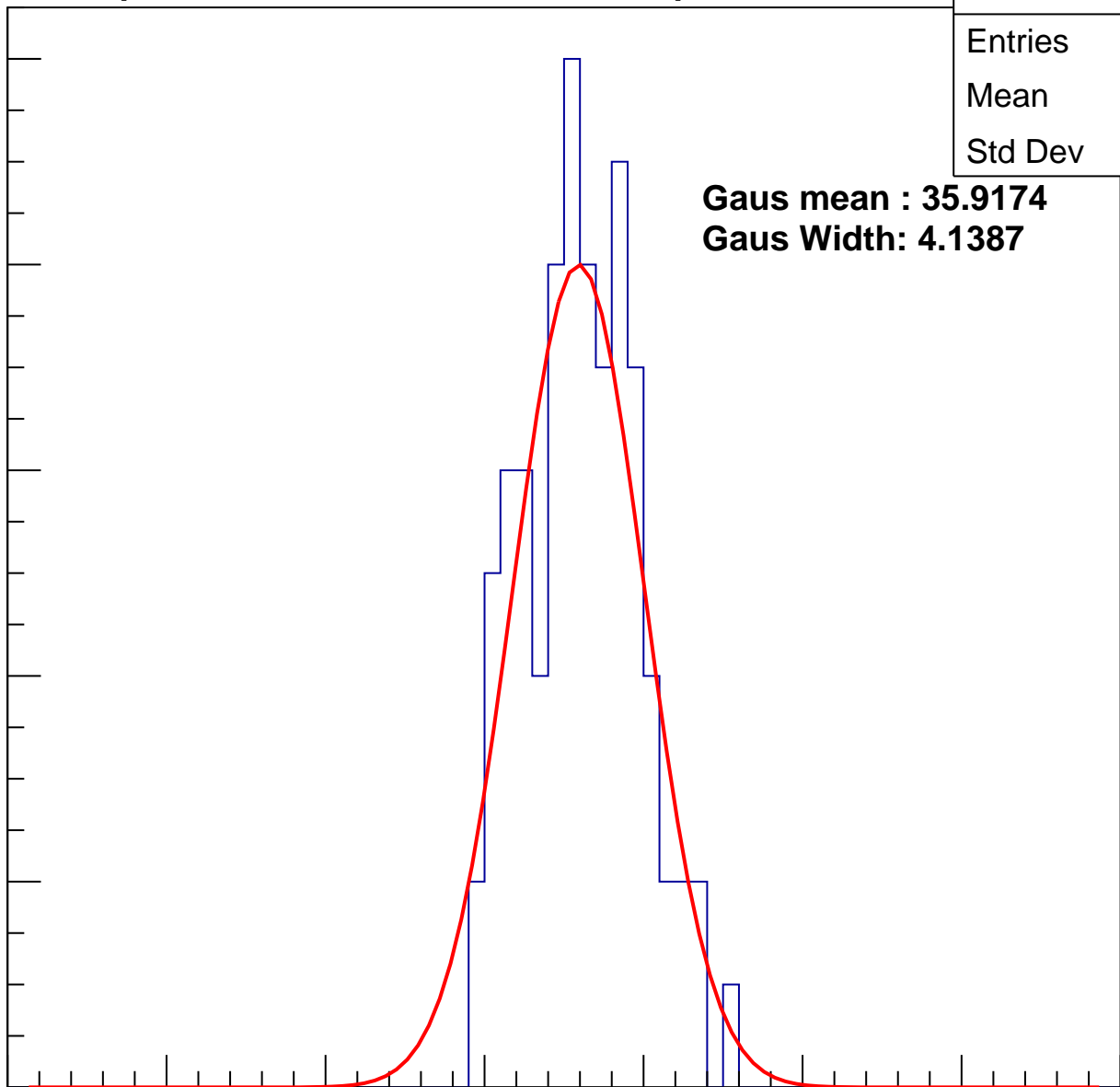
**Gaus Width: 4.1387**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch80, adc2

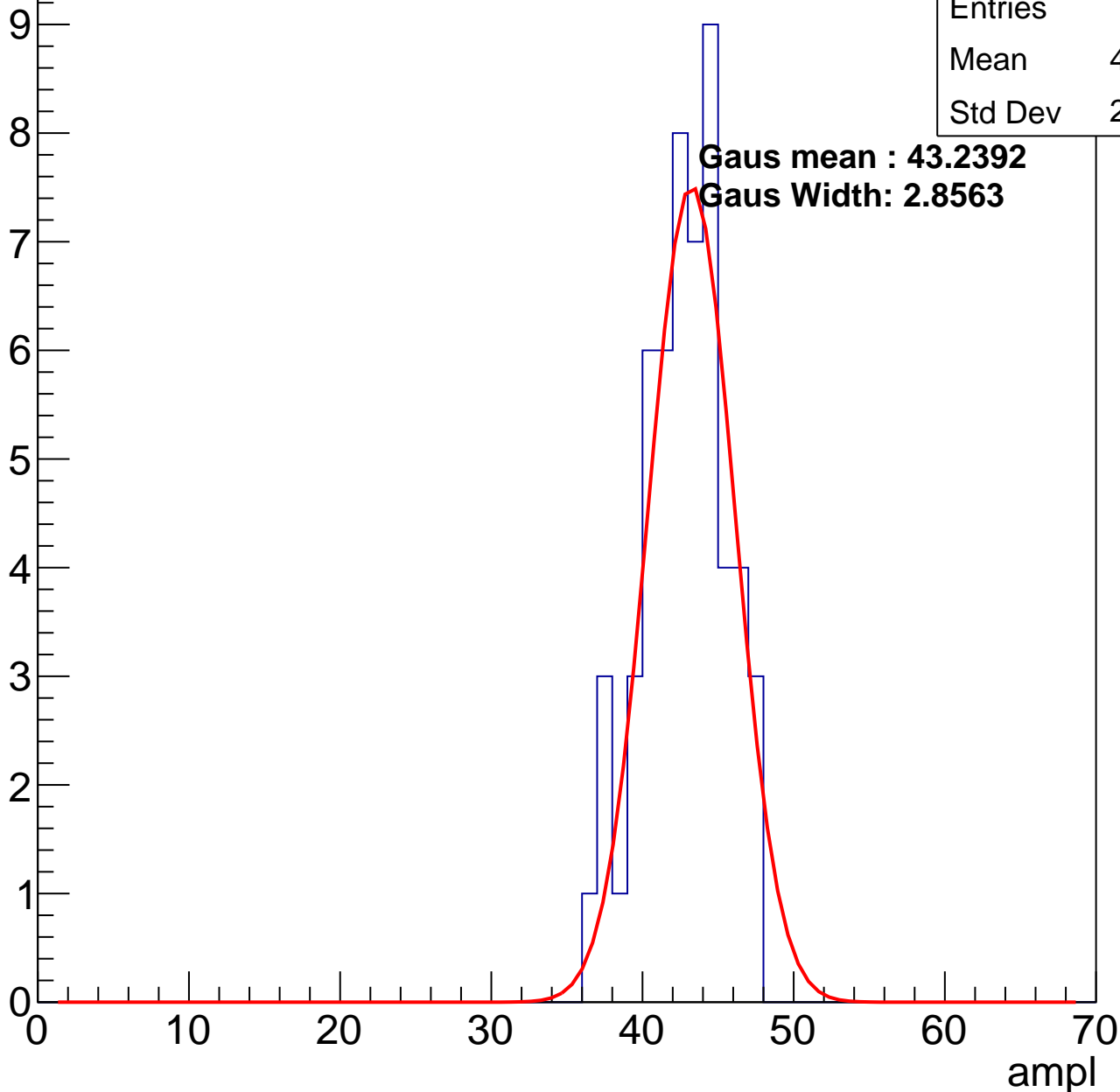
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	42.29
Std Dev	2.688

**Gaus mean : 43.2392**

**Gaus Width: 2.8563**

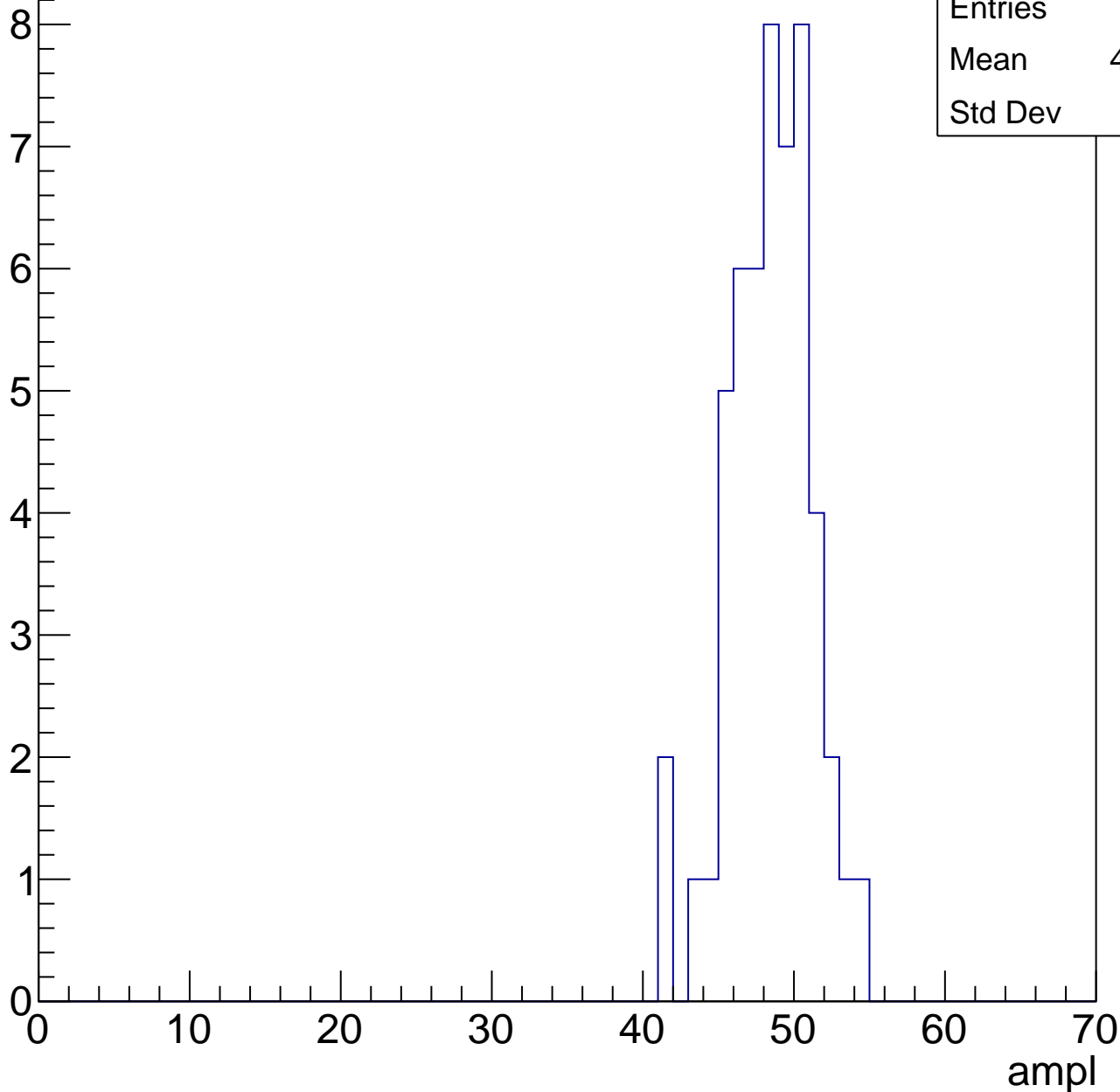


# B1L000S, U21-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	47.96
Std Dev	2.71

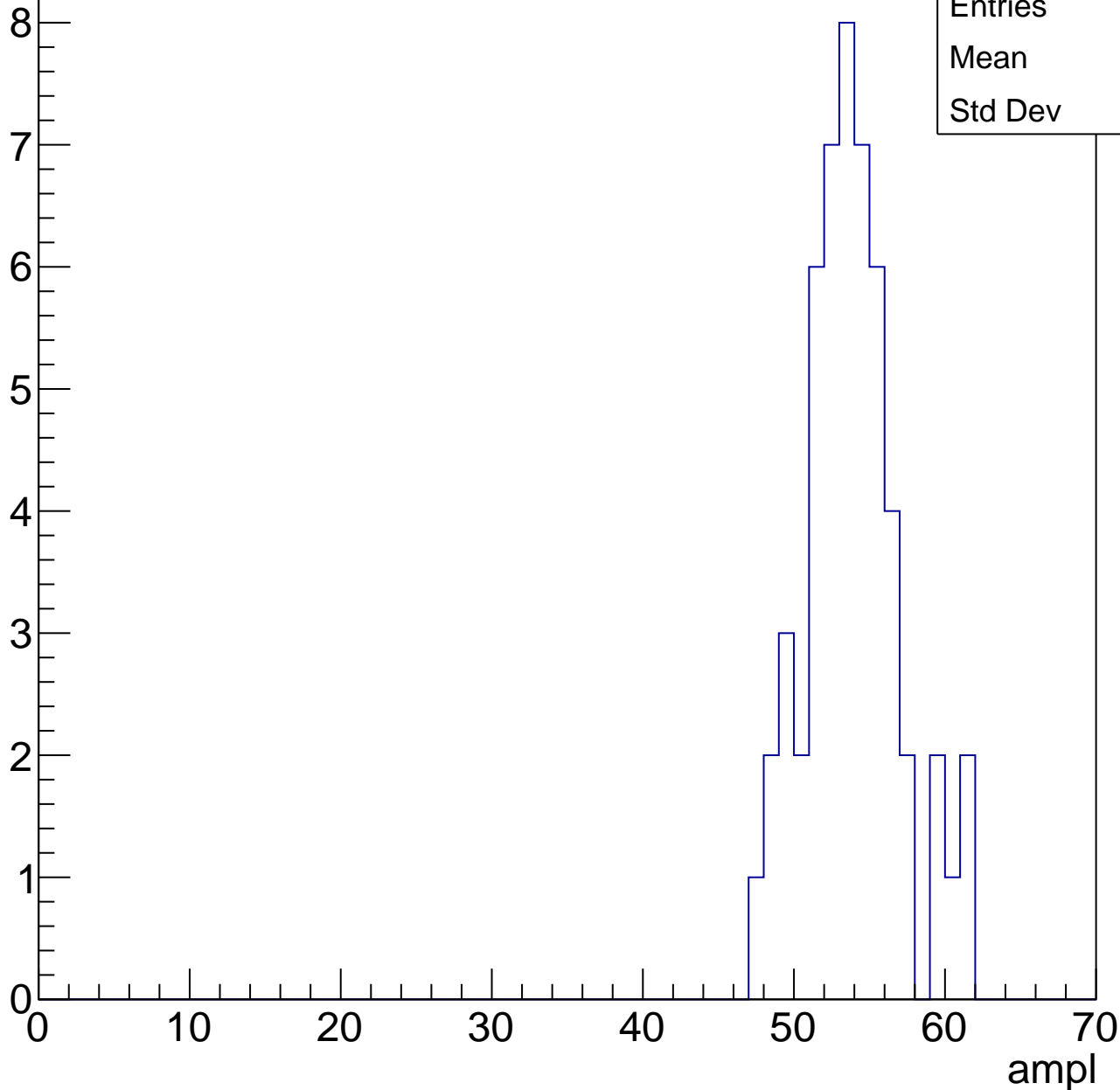


# B1L000S, U21-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	53.4
Std Dev	3.14



# B1L000S, U21-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

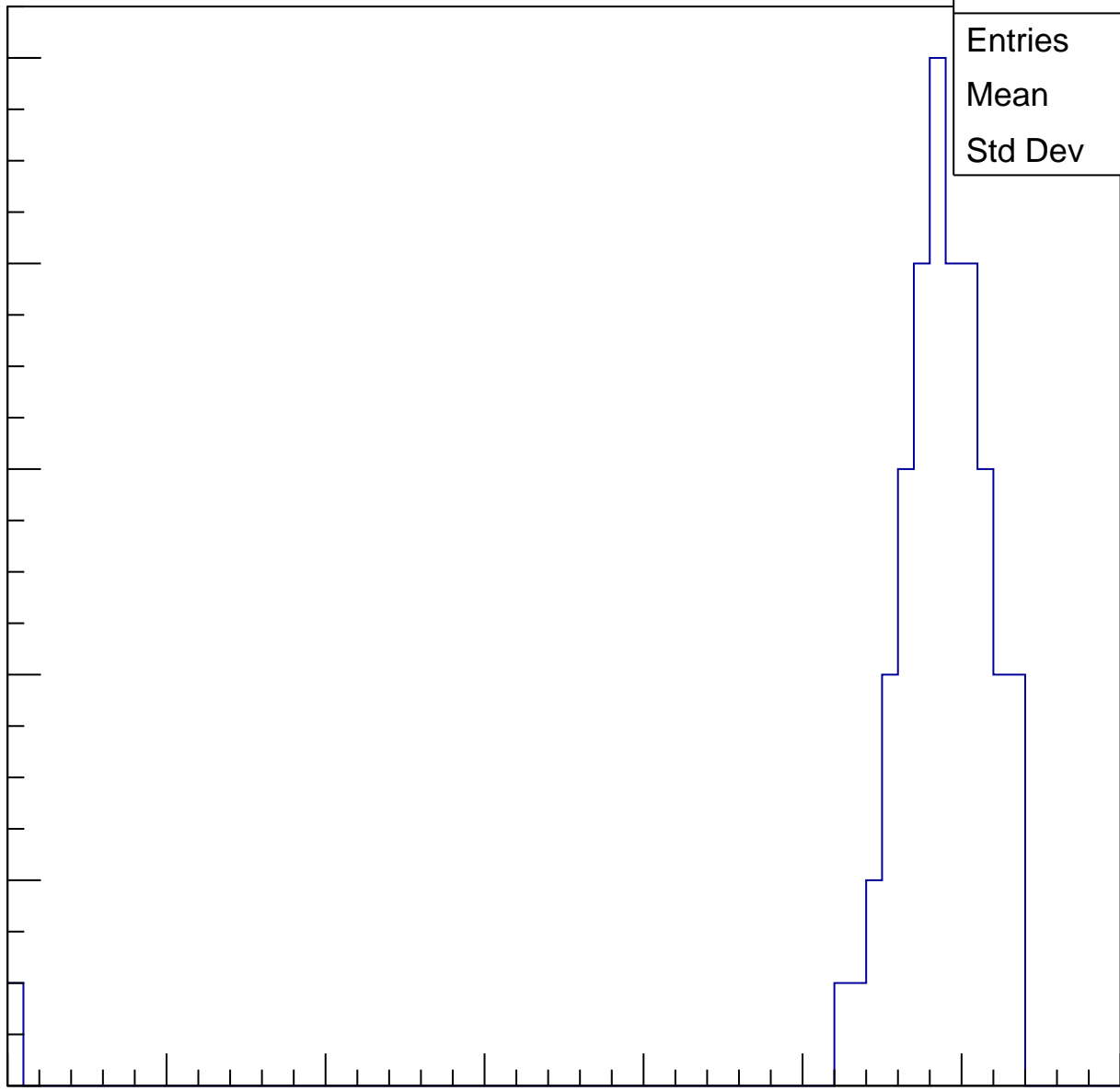
50

60

70

ampl

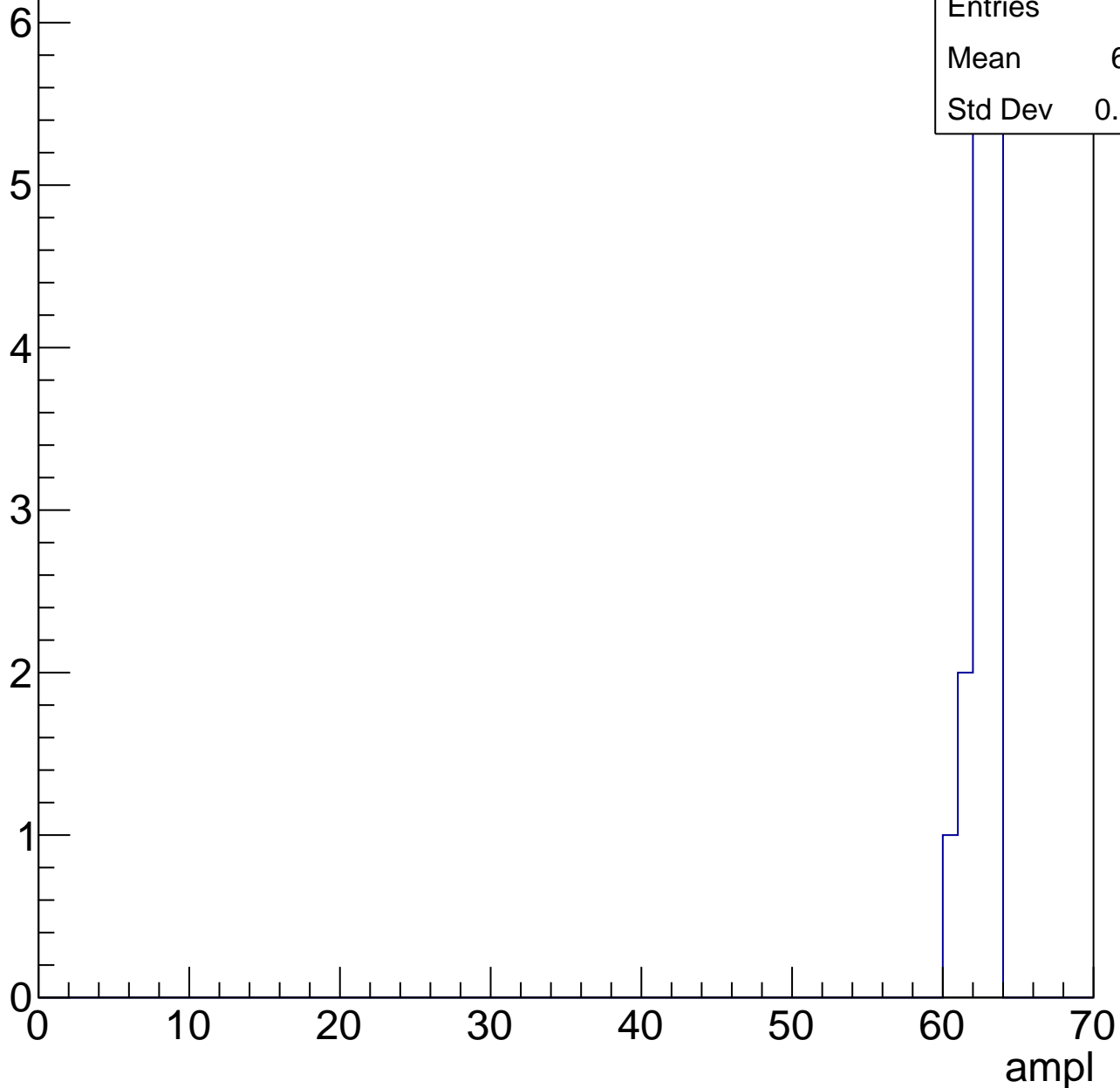
Entries	63
Mean	57.51
Std Dev	7.733



# B1L000S, U21-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

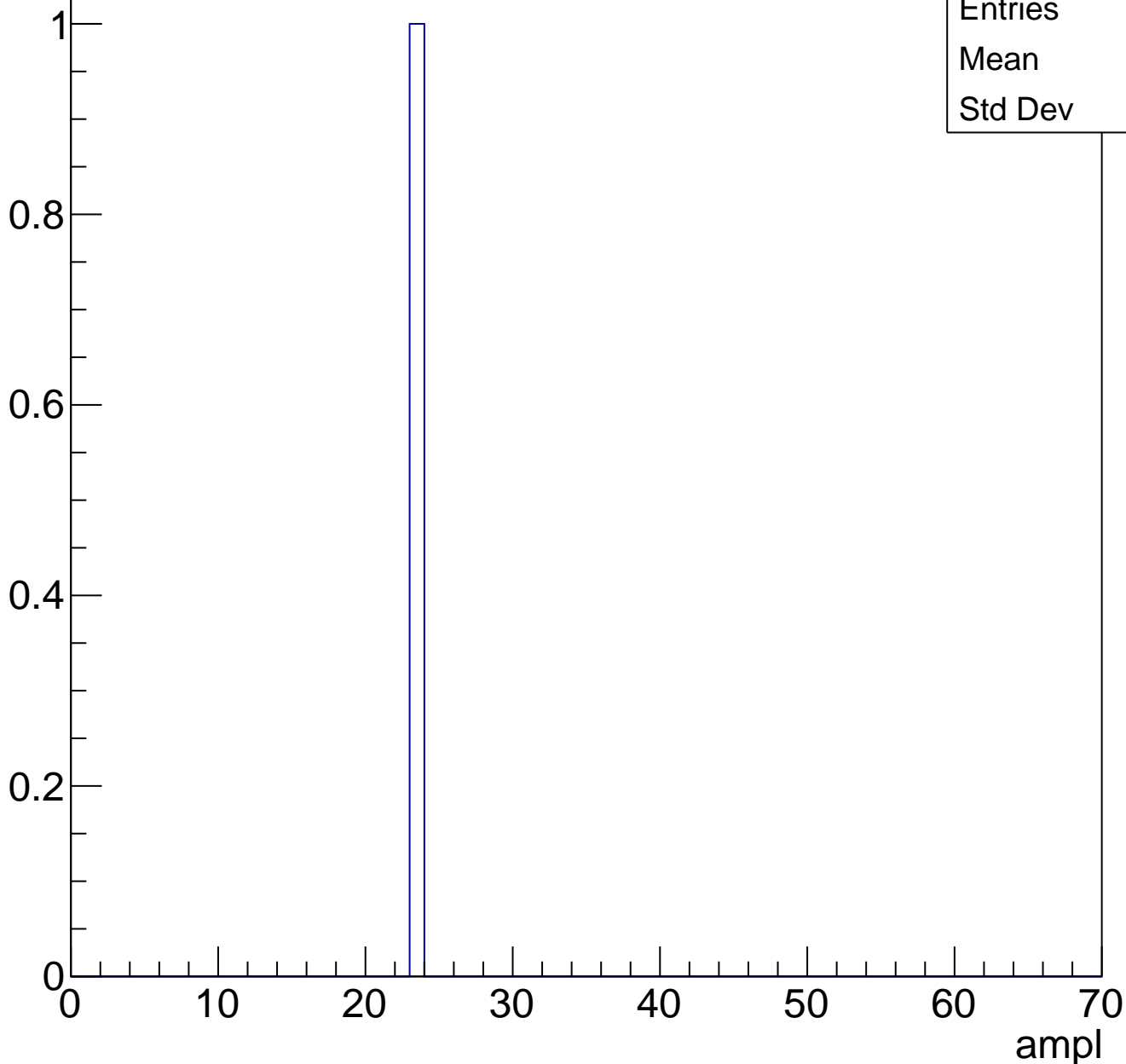




# B1L000S, U21-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch81, adc0

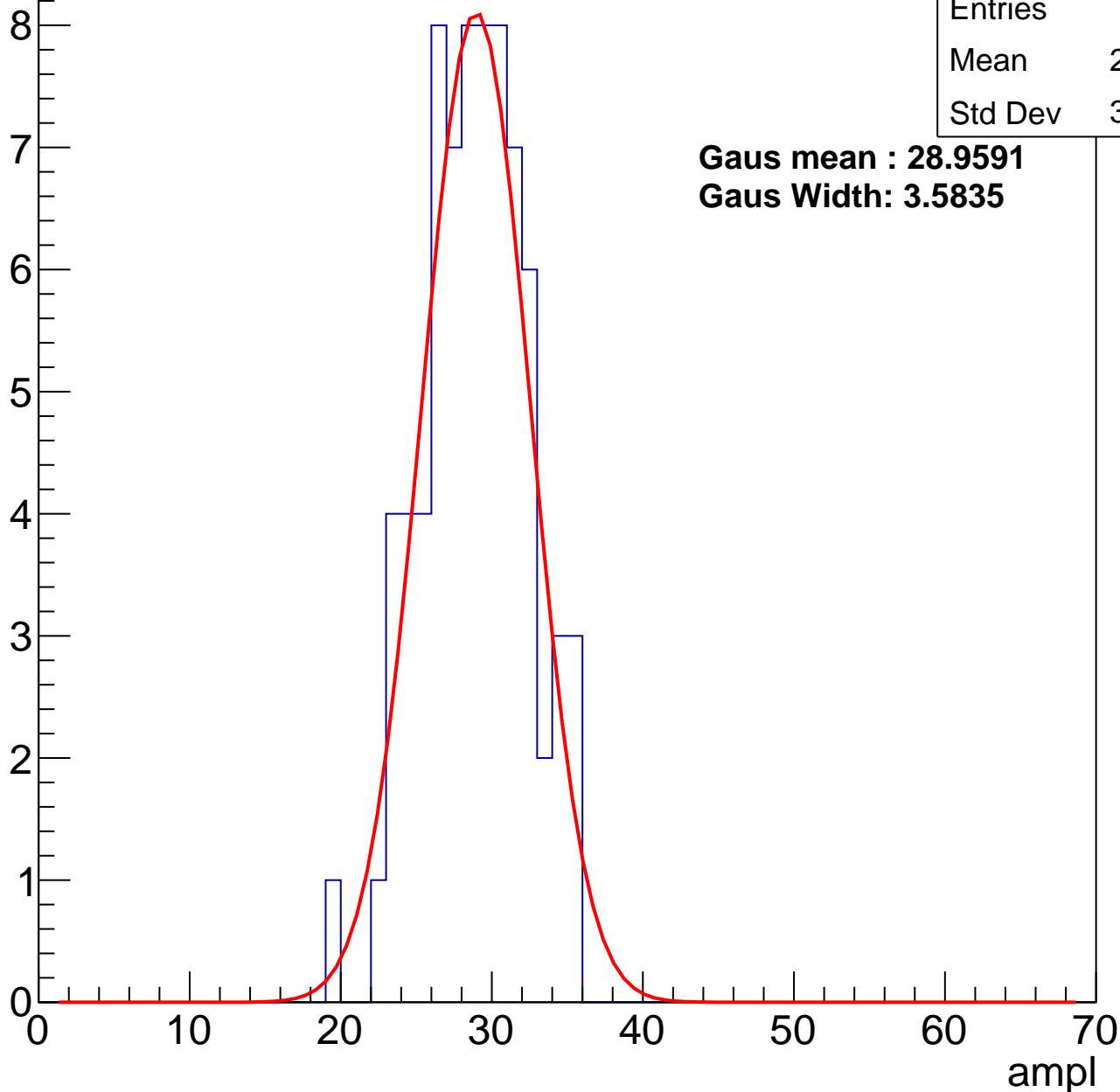
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	28.43
Std Dev	3.382

**Gaus mean : 28.9591**

**Gaus Width: 3.5835**



# B1L000S, U21-ch81, adc1

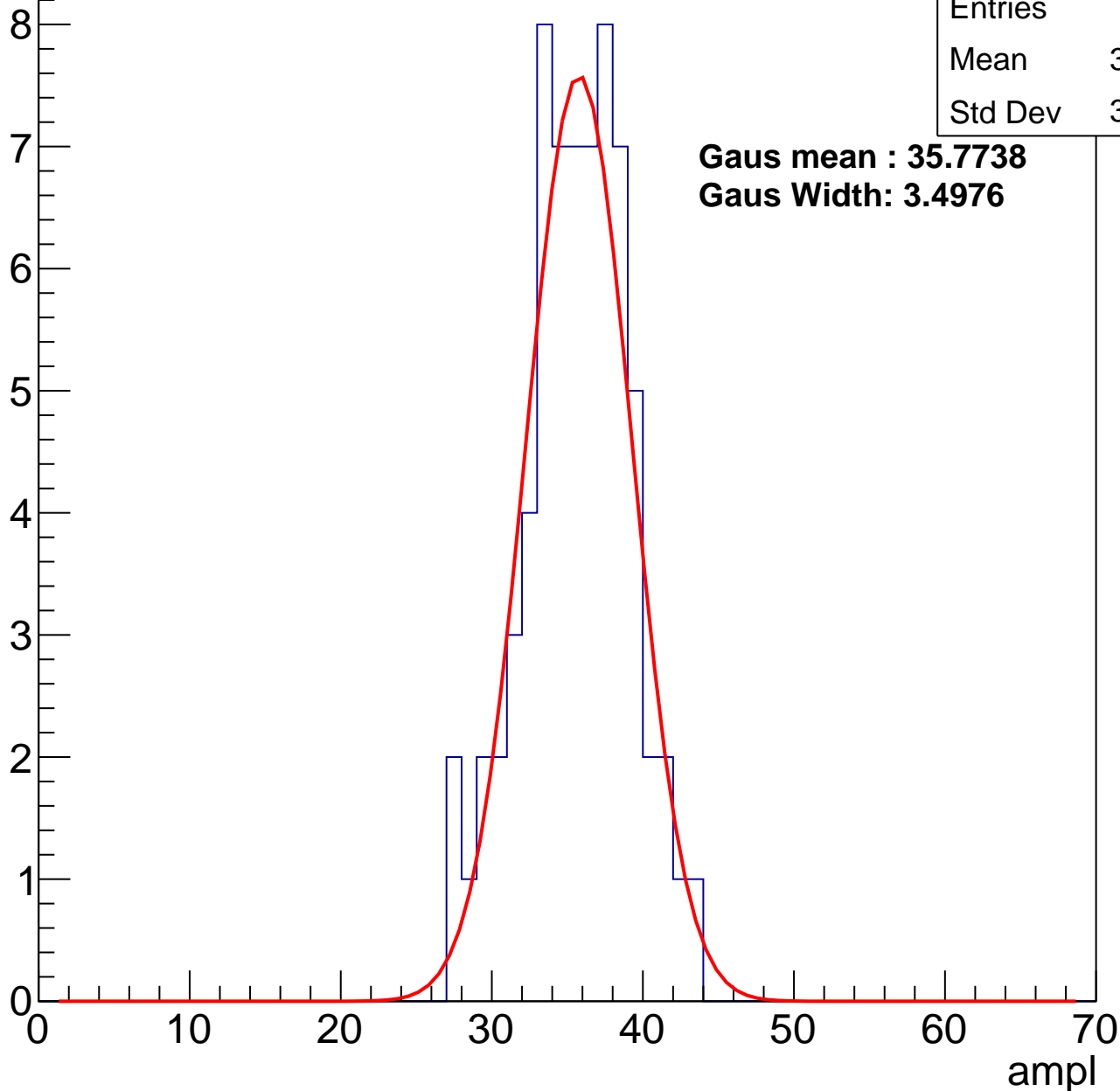
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.13
Std Dev	3.485

**Gaus mean : 35.7738**

**Gaus Width: 3.4976**



# B1L000S, U21-ch81, adc2

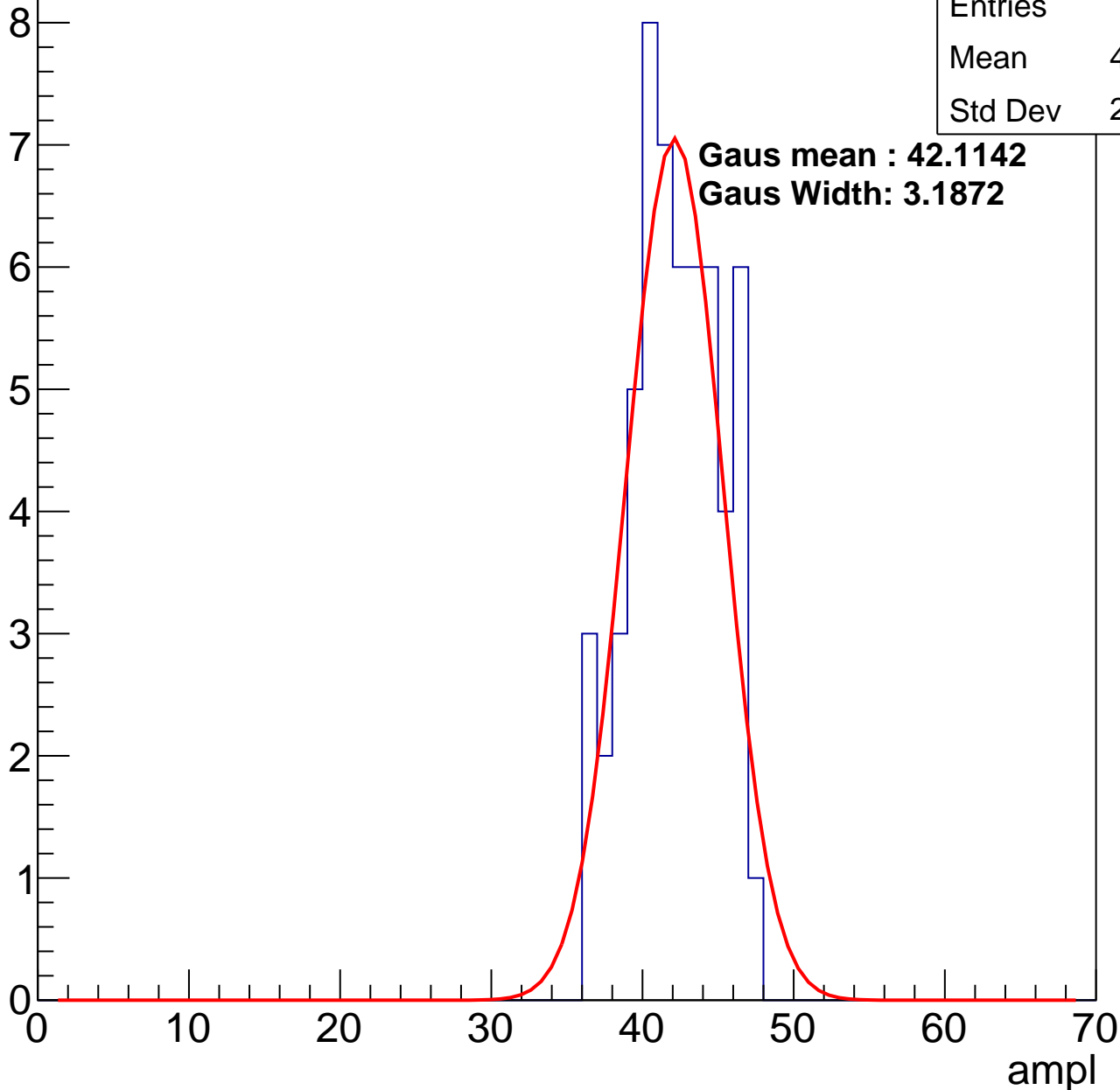
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.67
Std Dev	2.874

**Gaus mean : 42.1142**

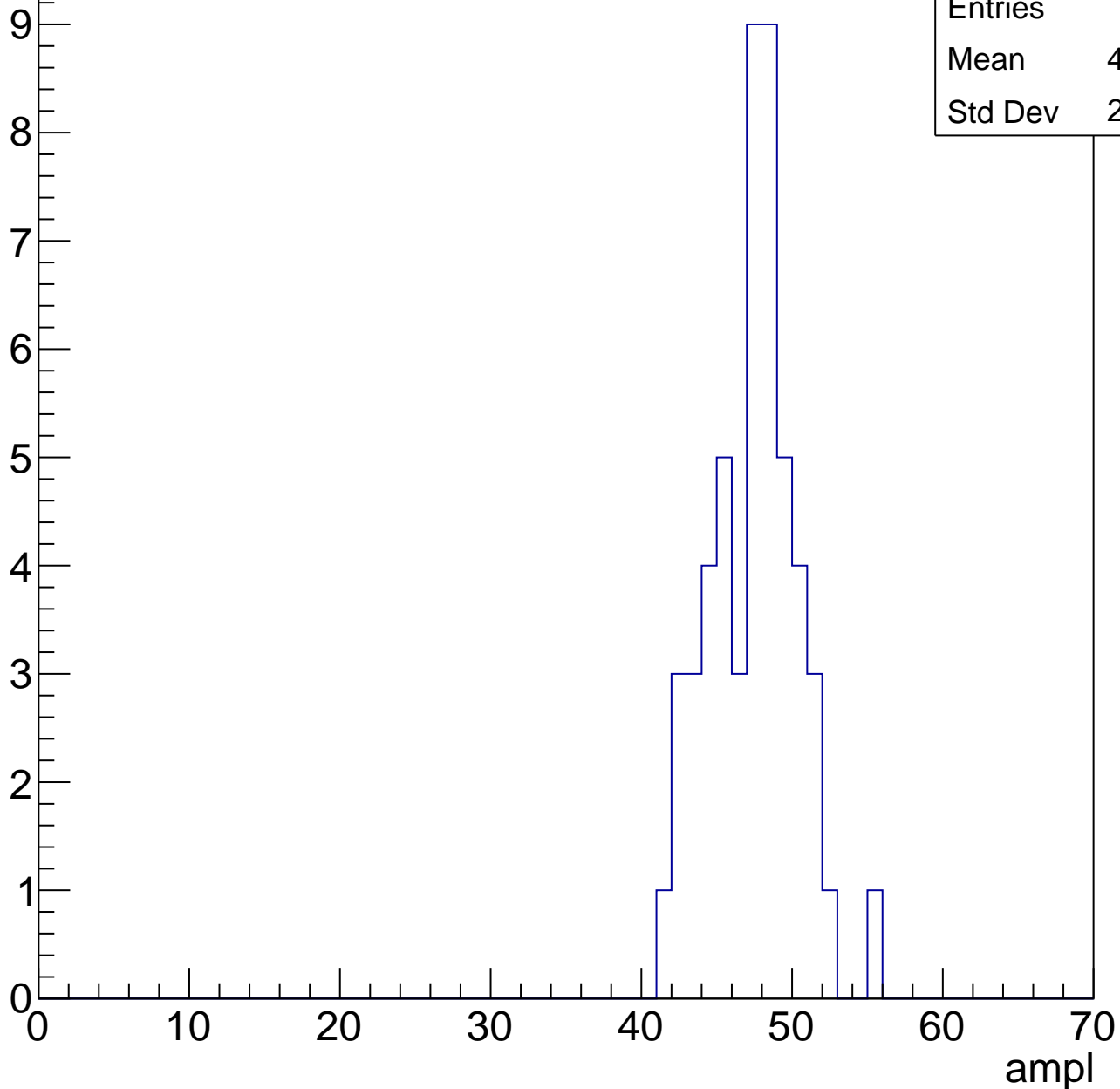
**Gaus Width: 3.1872**



# B1L000S, U21-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

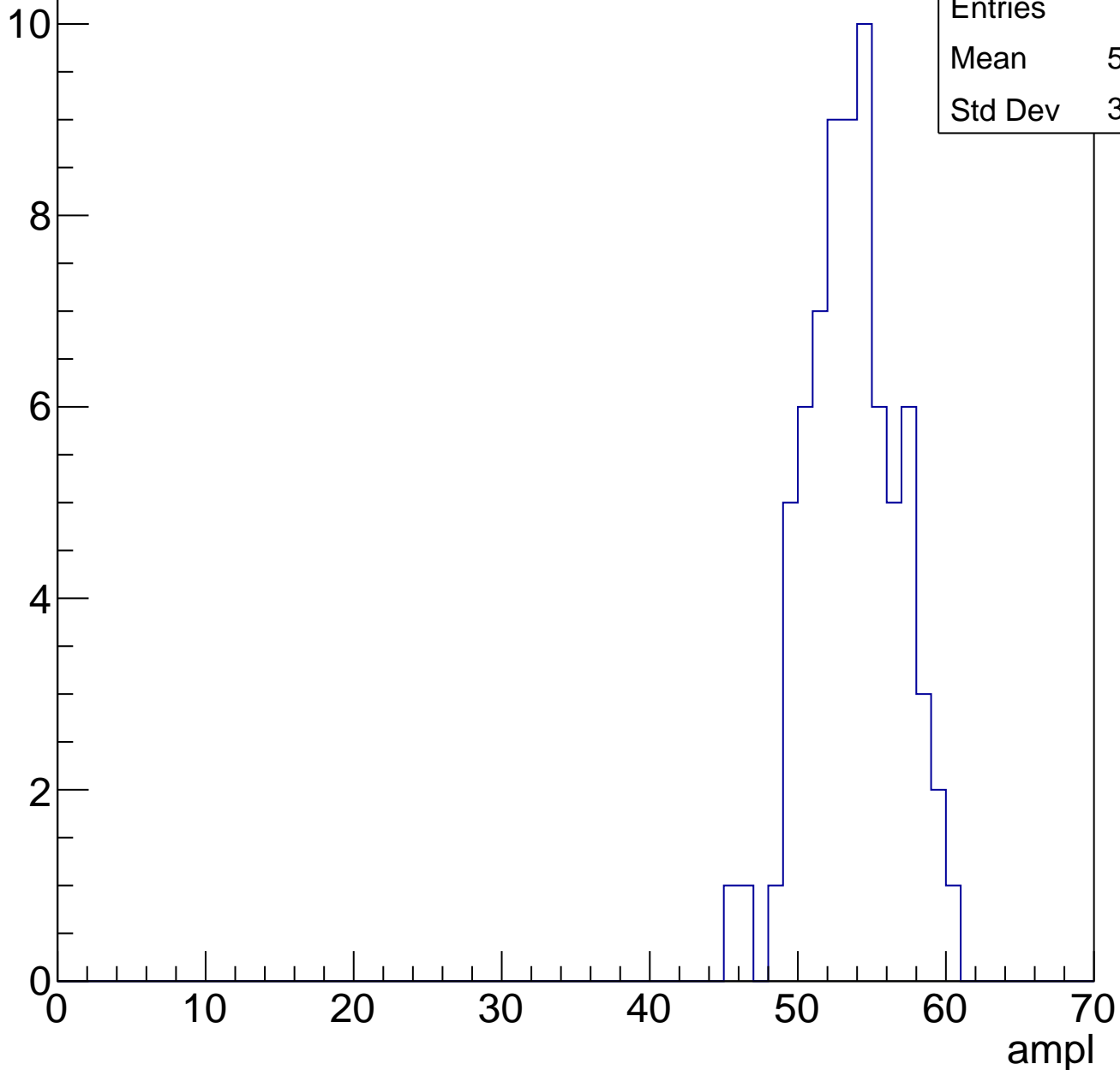


# B1L000S, U21-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	53.19
Std Dev	3.067

Entry

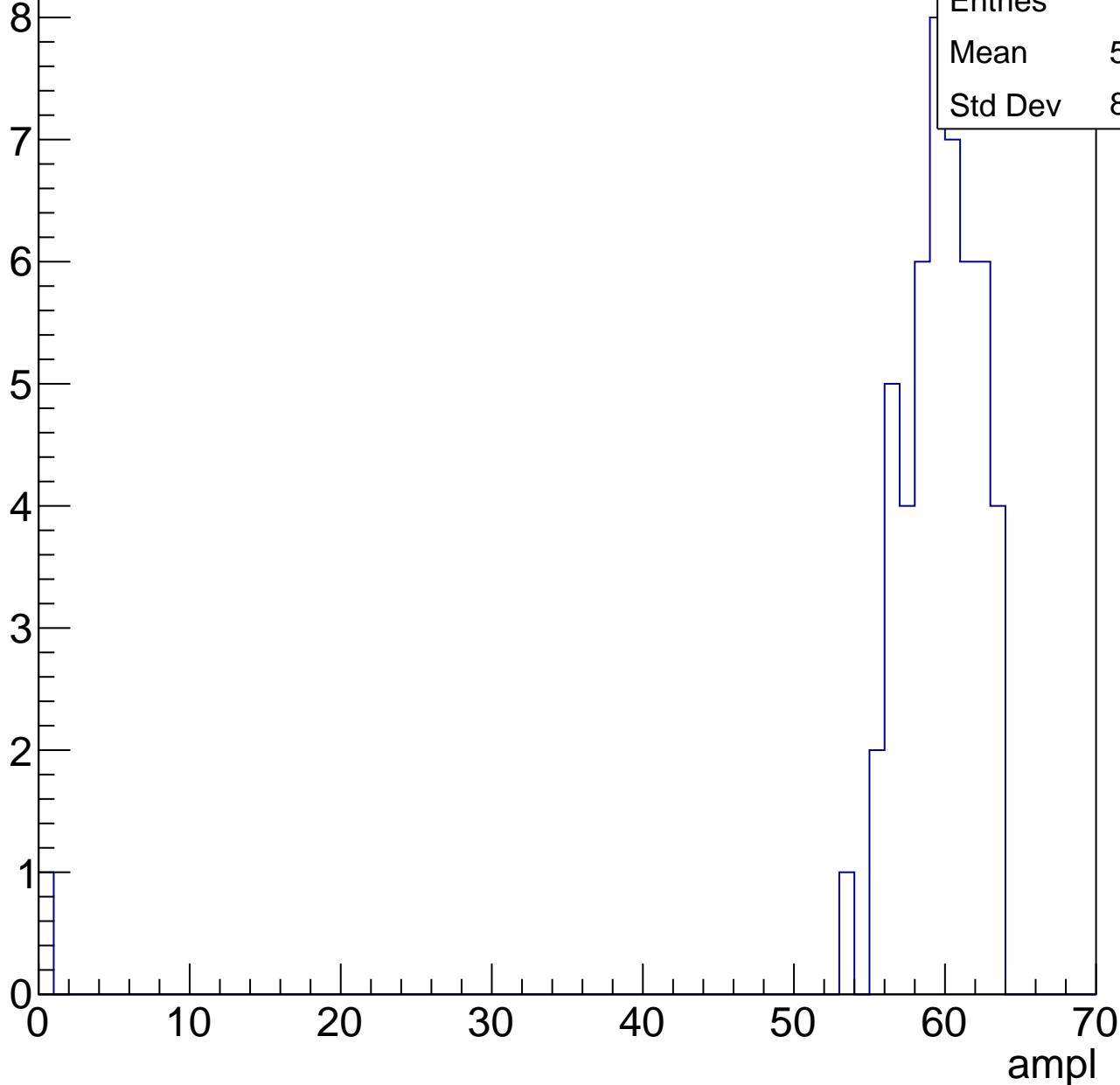


# B1L000S, U21-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.02
Std Dev	8.622

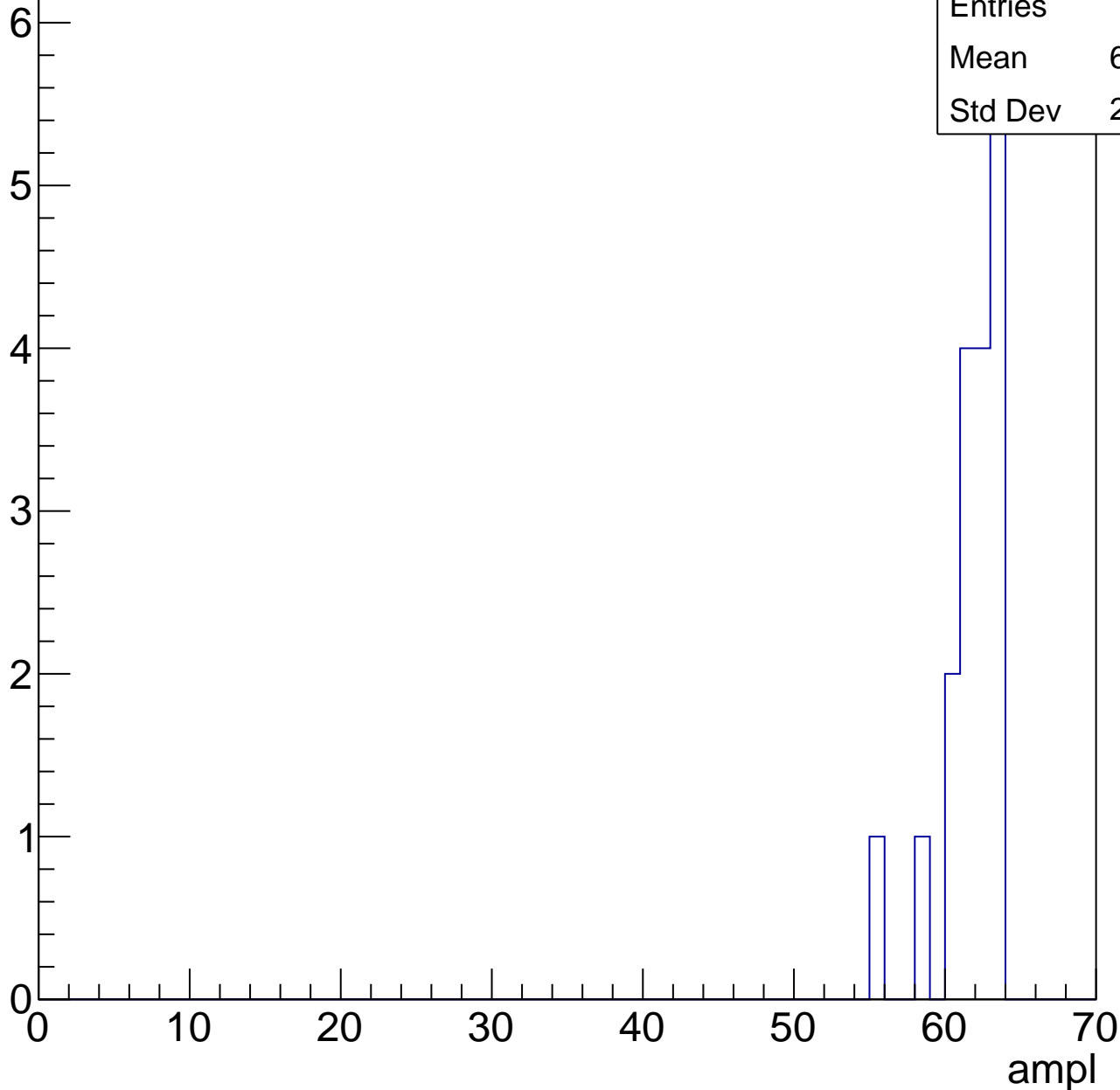


# B1L000S, U21-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.28
Std Dev	2.022





# B1L000S, U21-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	28.57
Std Dev	4.595

**Gaus mean : 29.5379**

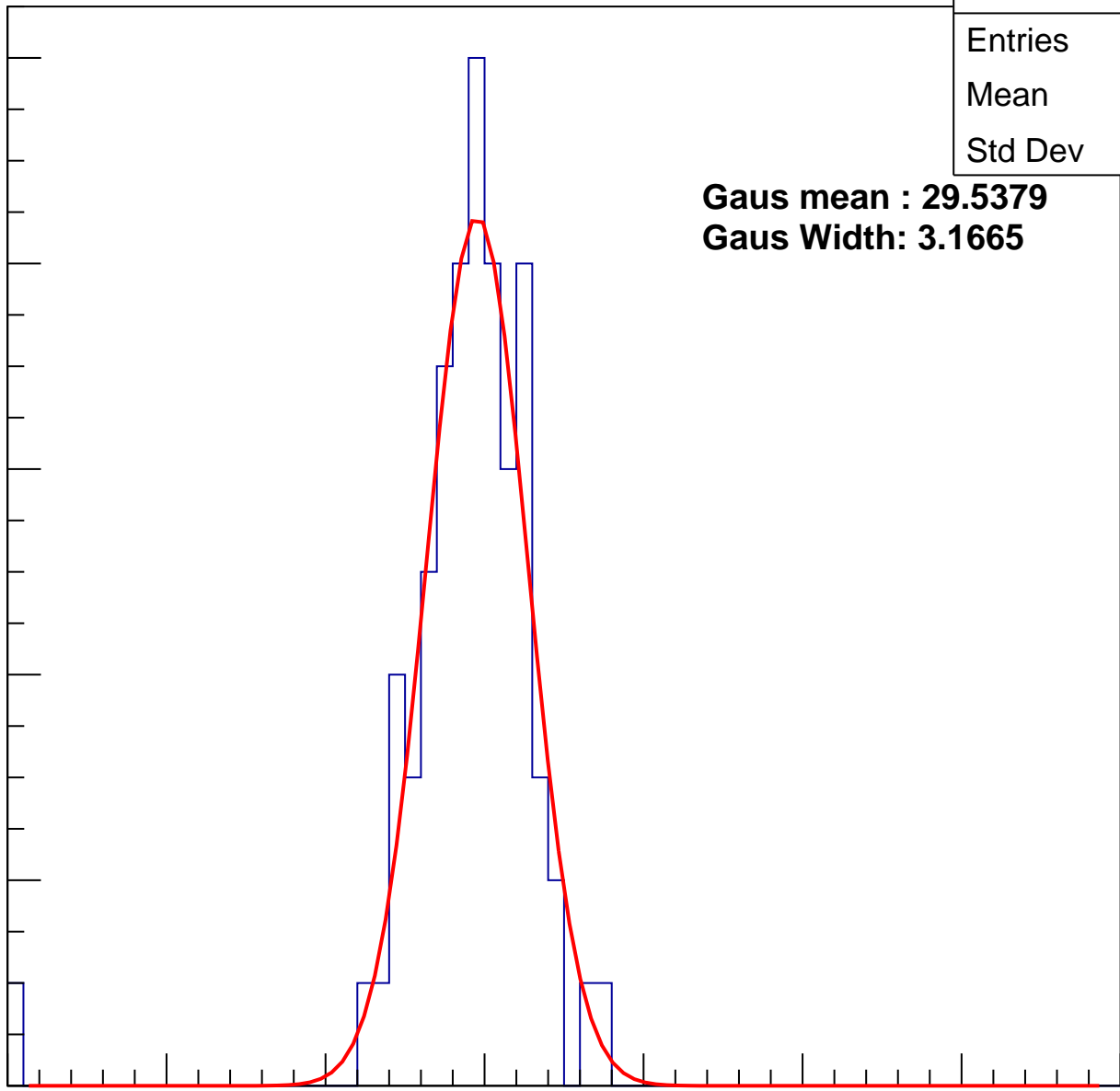
**Gaus Width: 3.1665**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch82, adc1

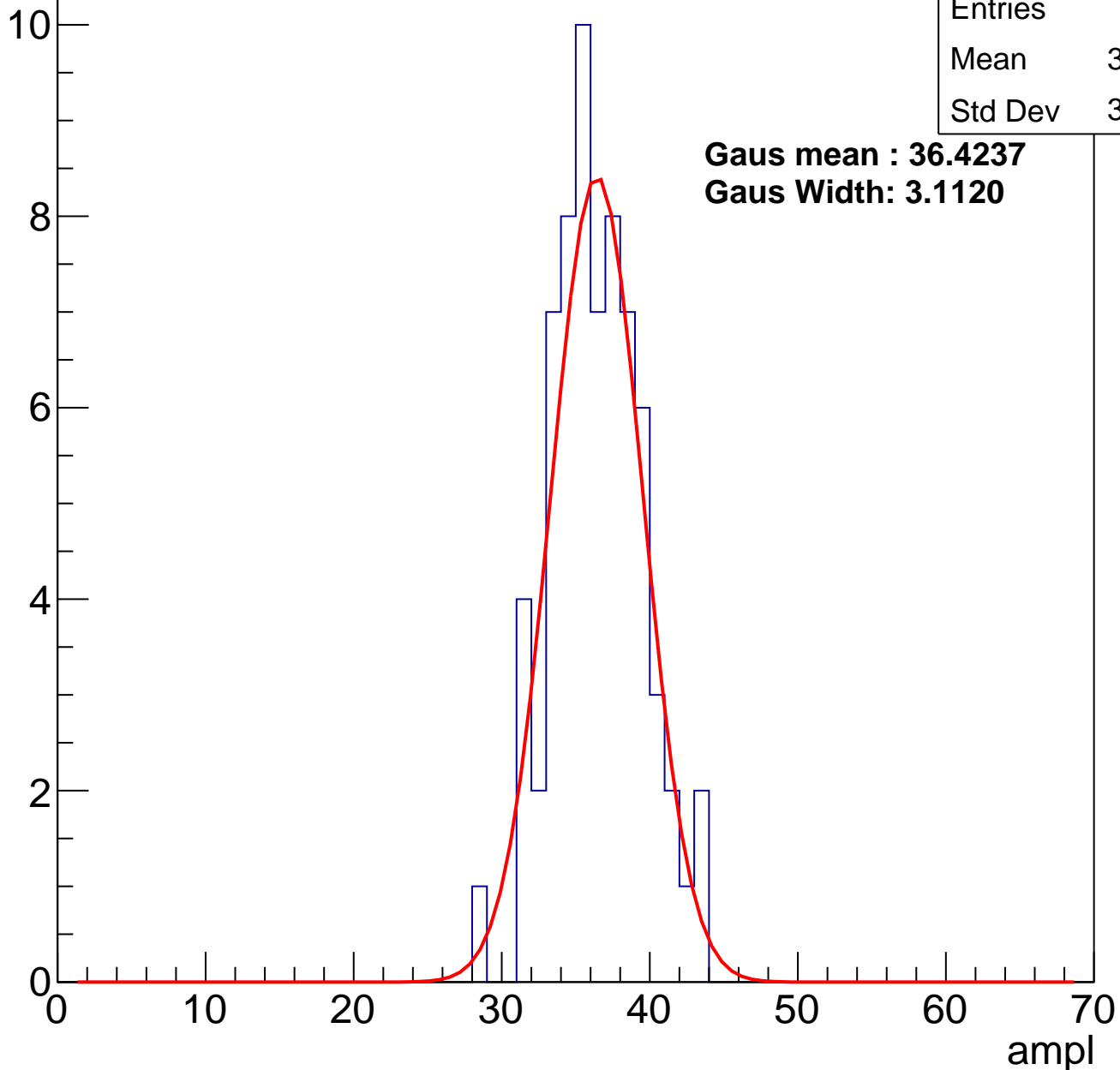
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	35.99
Std Dev	3.027

**Gaus mean : 36.4237**

**Gaus Width: 3.1120**

Entry



# B1L000S, U21-ch82, adc2

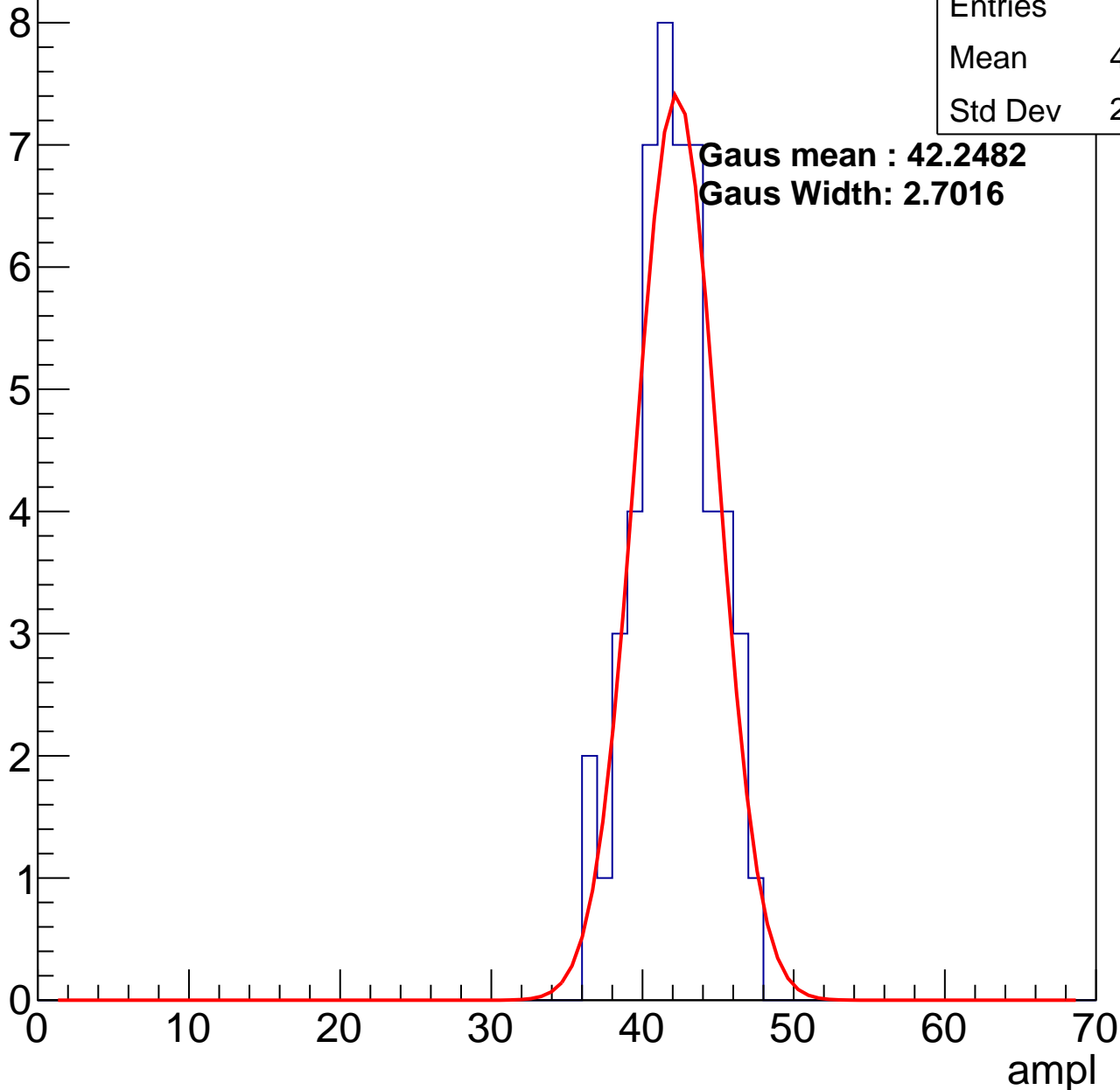
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	41.63
Std Dev	2.589

**Gaus mean : 42.2482**

**Gaus Width: 2.7016**

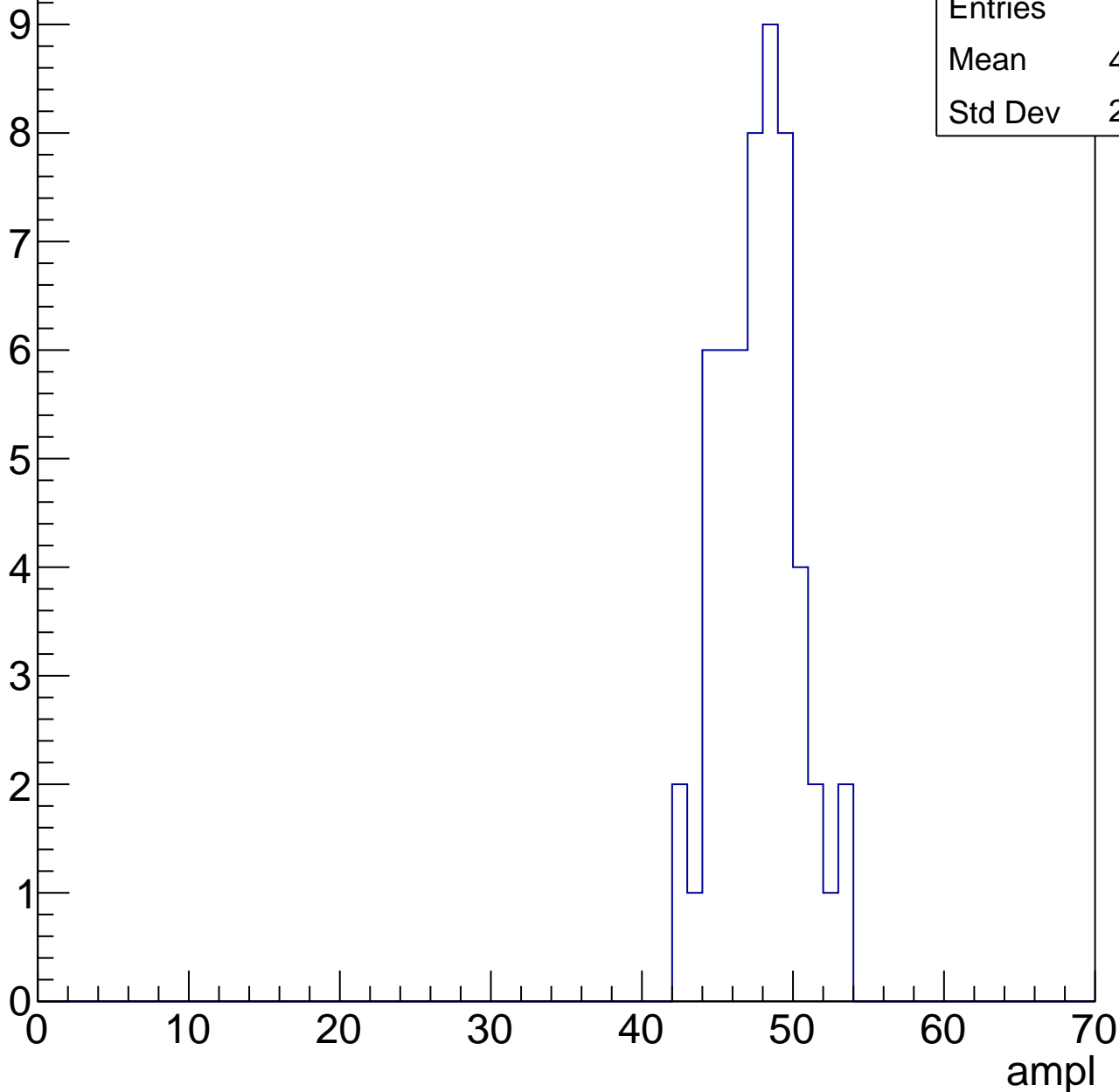


# B1L000S, U21-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	47.22
Std Dev	2.535

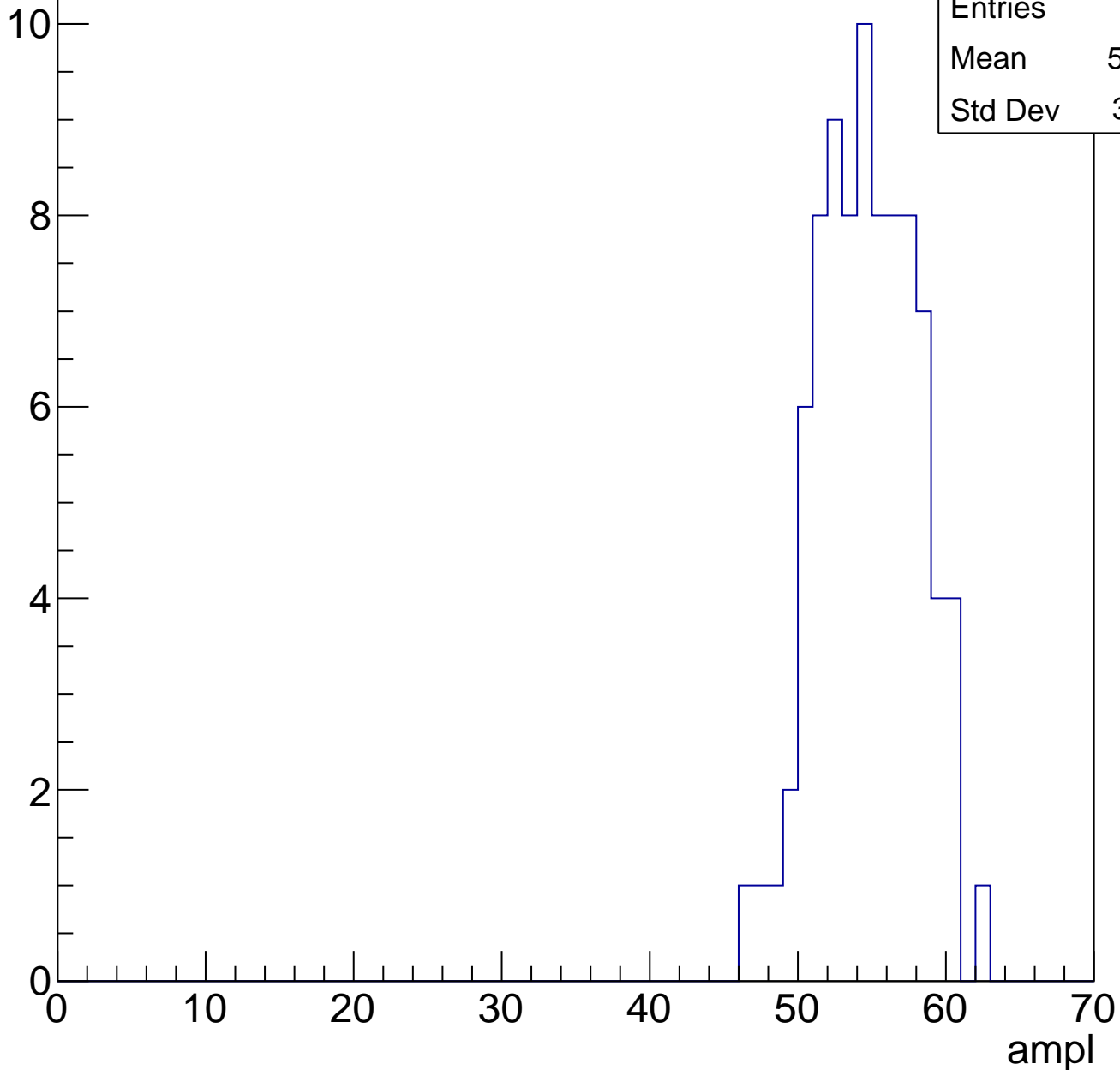


# B1L000S, U21-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	86
Mean	54.27
Std Dev	3.301

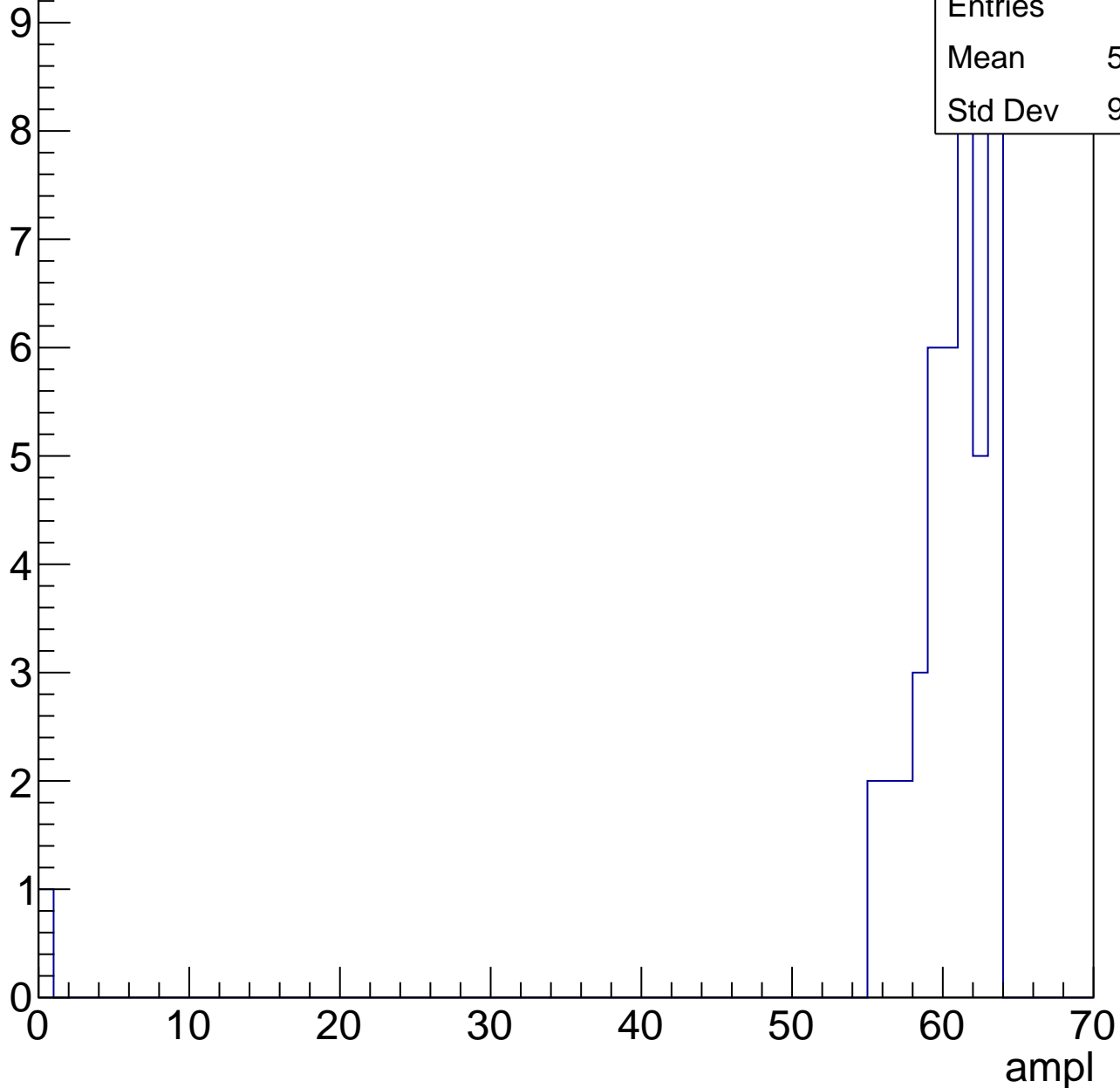
Entry



# B1L000S, U21-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

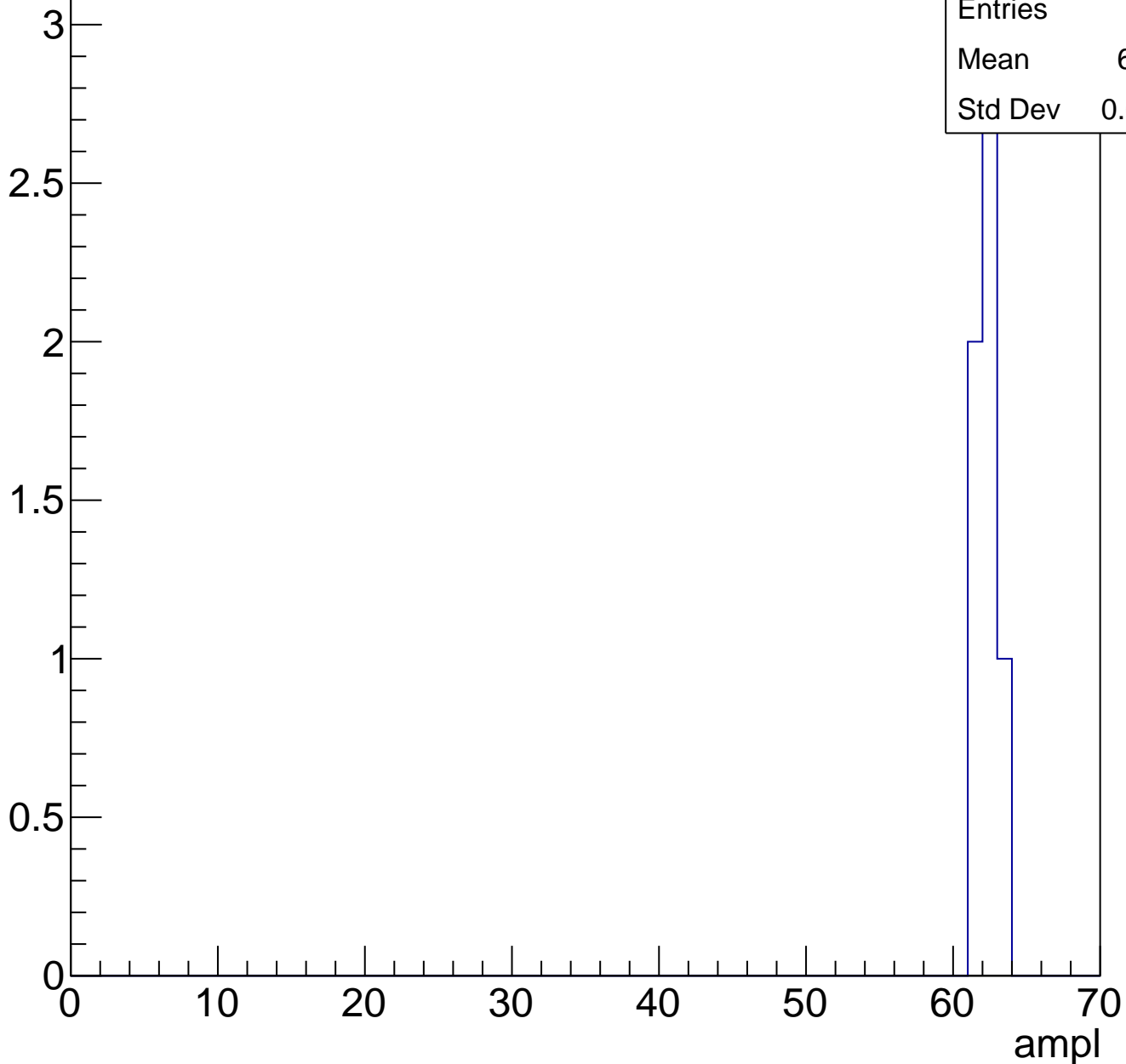


Entries	44
Mean	58.84
Std Dev	9.254

# B1L000S, U21-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch83, adc0

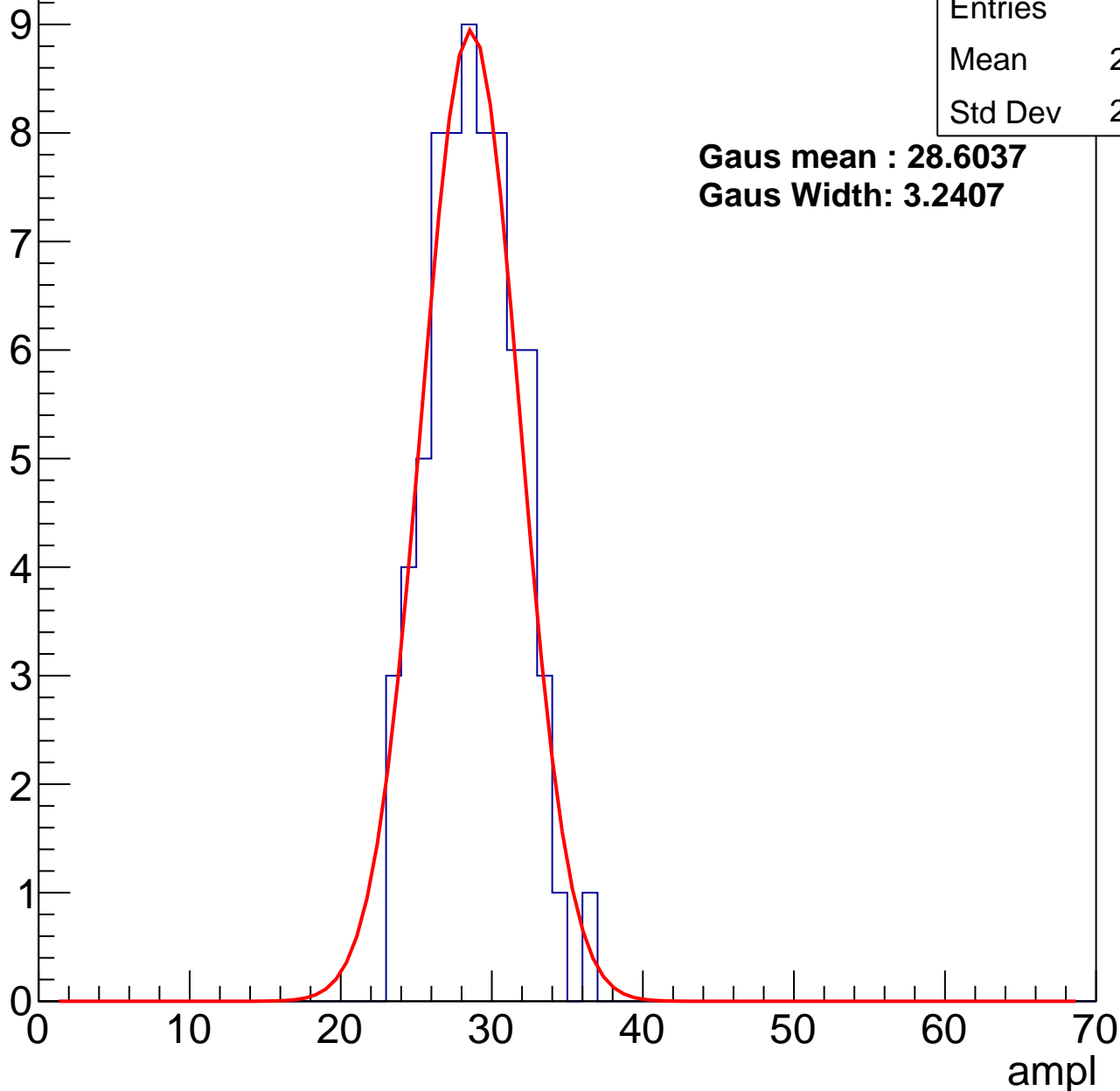
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	28.36
Std Dev	2.879

**Gaus mean : 28.6037**

**Gaus Width: 3.2407**



# B1L000S, U21-ch83, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	35.66
Std Dev	3.148

**Gaus mean : 36.1964**

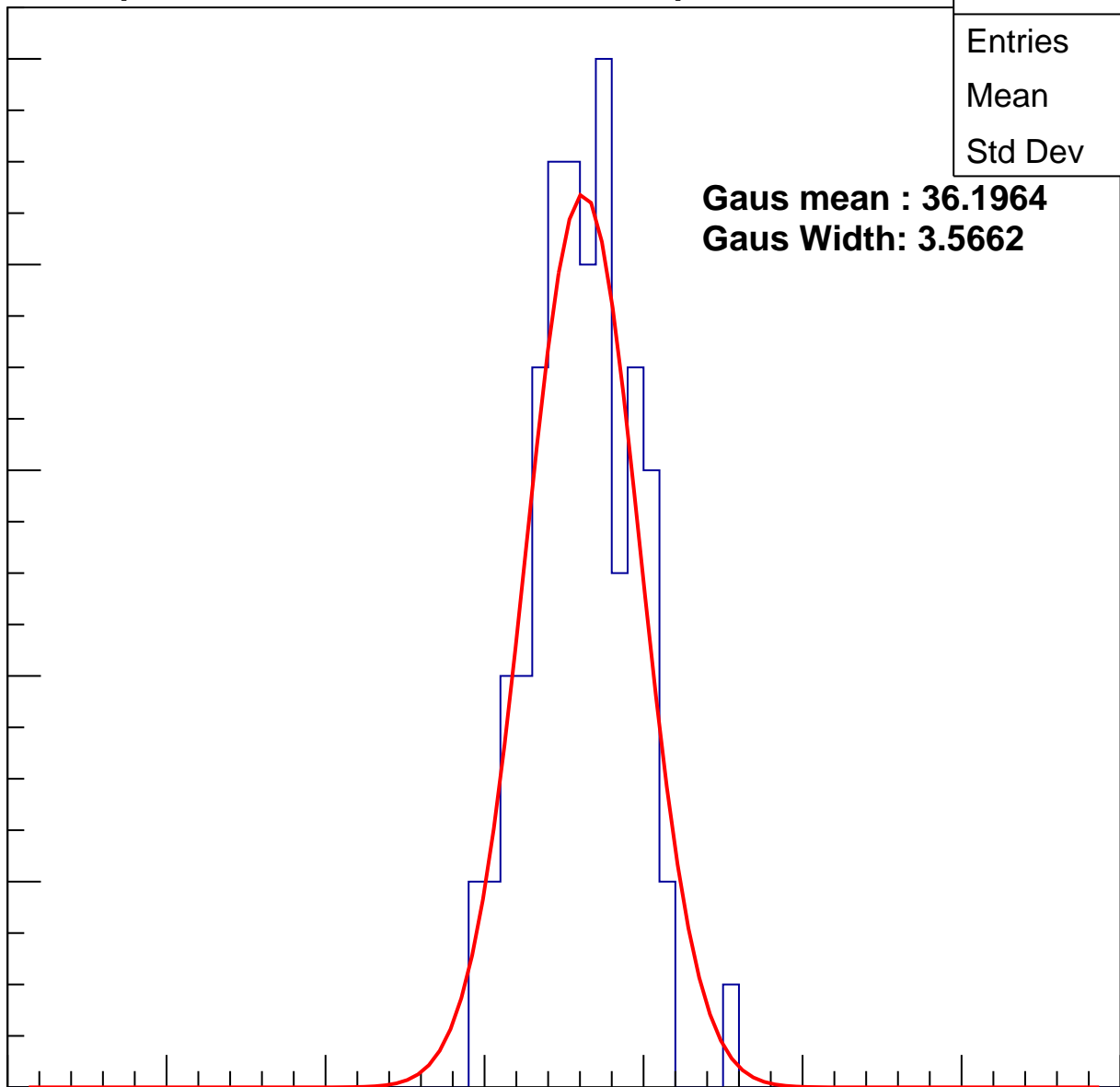
**Gaus Width: 3.5662**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch83, adc2

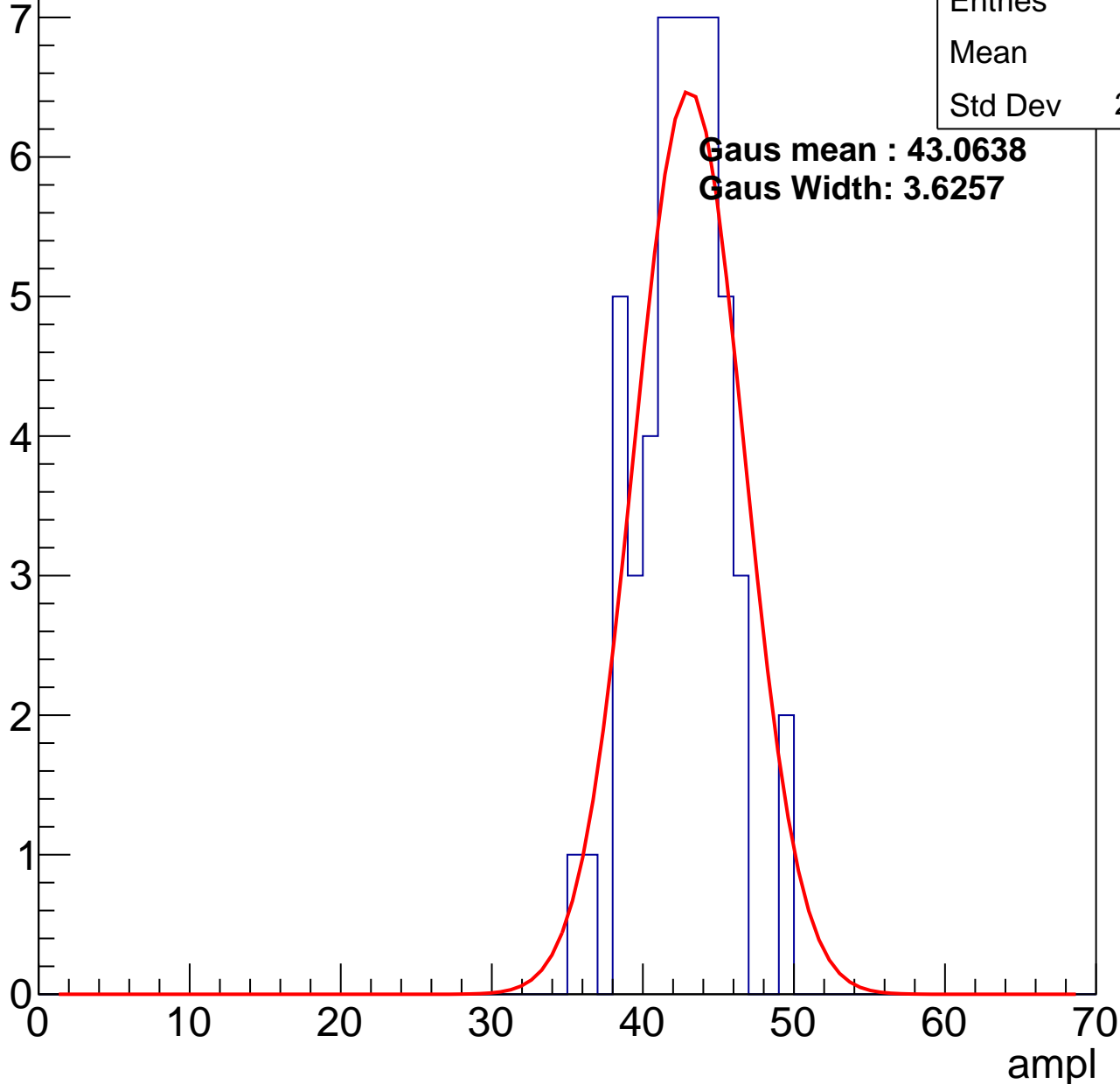
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	42.1
Std Dev	2.911

**Gaus mean : 43.0638**

**Gaus Width: 3.6257**



# B1L000S, U21-ch83, adc3

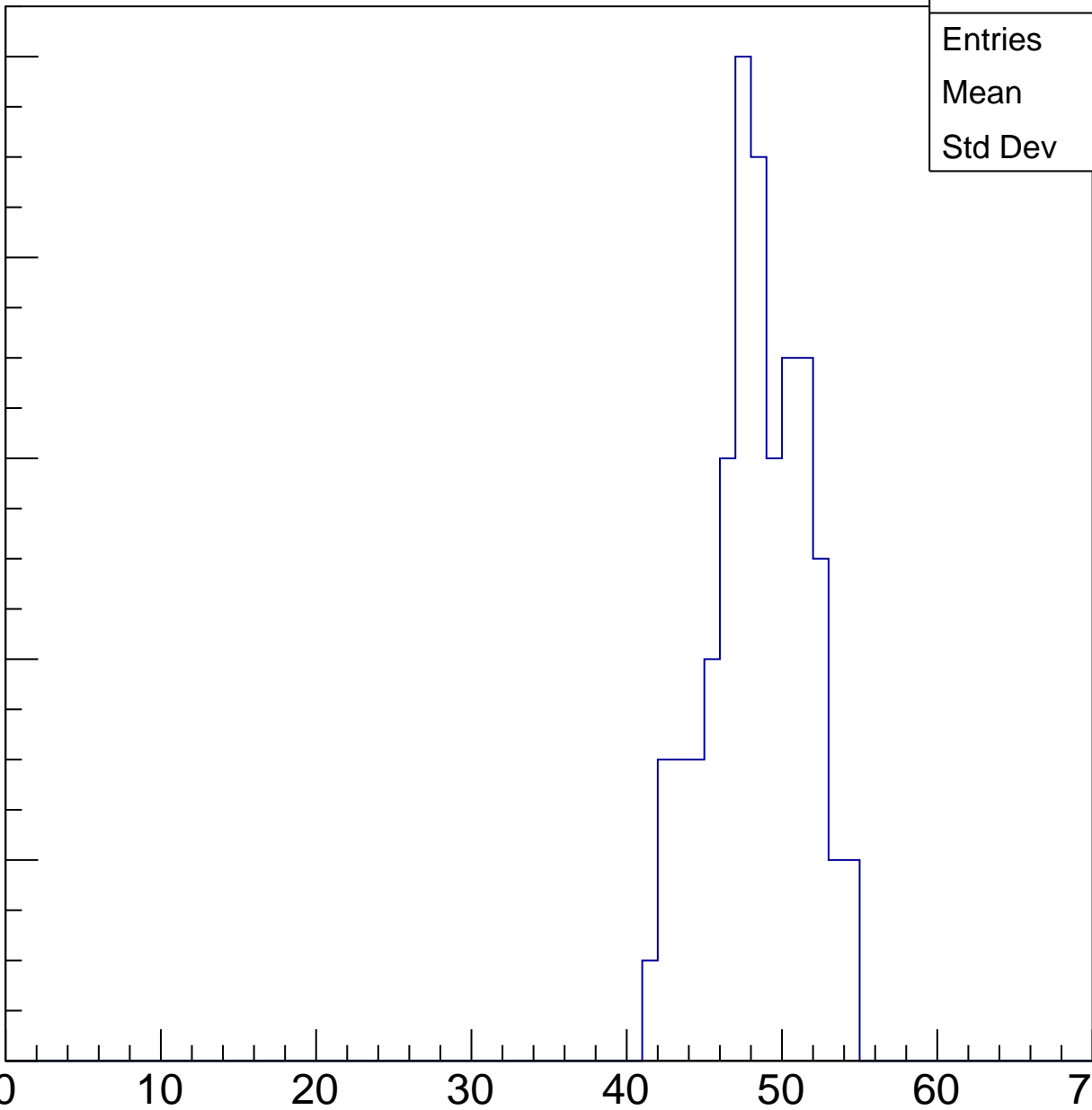
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10  
8  
6  
4  
2  
0

Entries	68
Mean	47.96
Std Dev	3.089

ampl

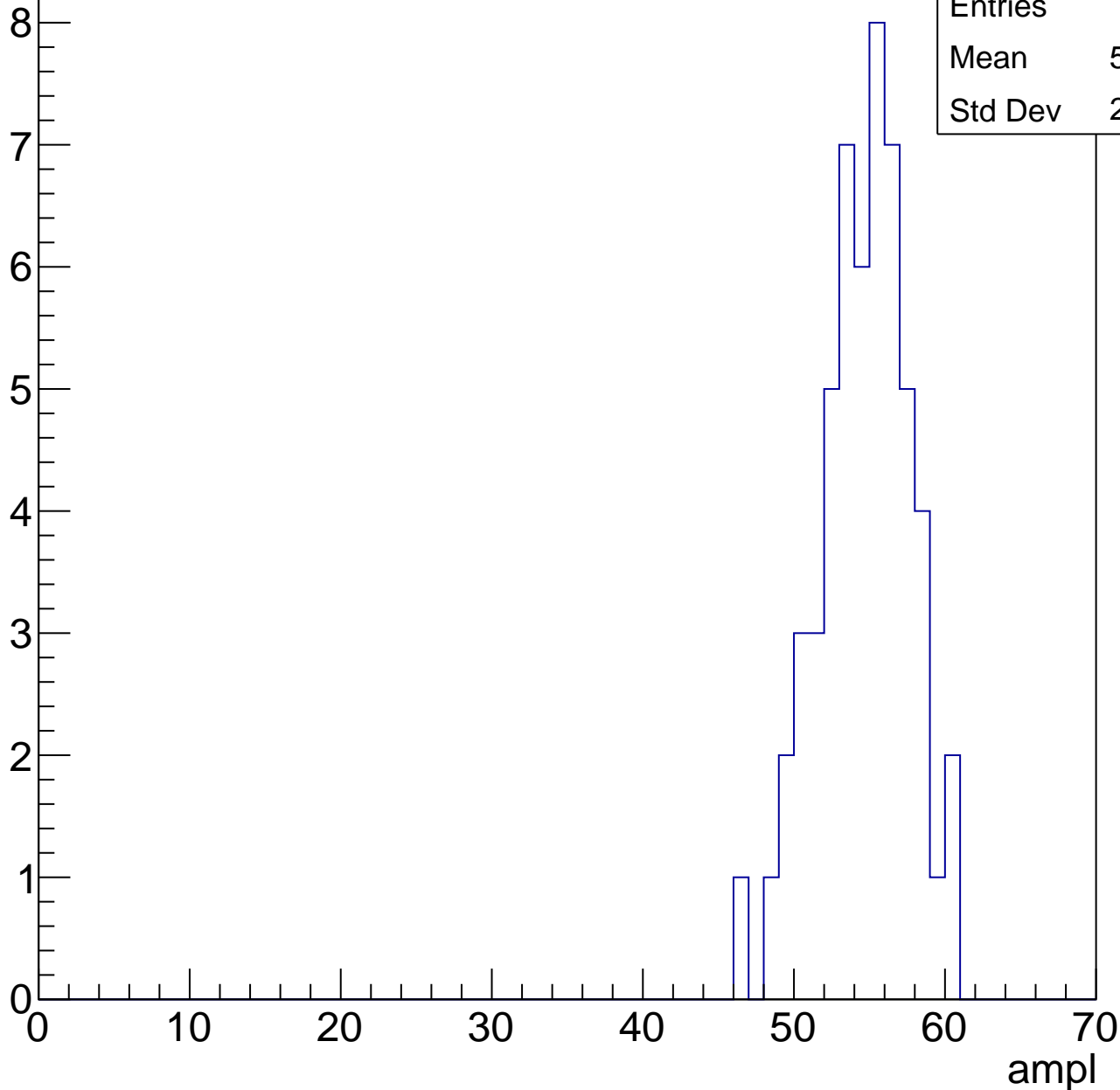


# B1L000S, U21-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

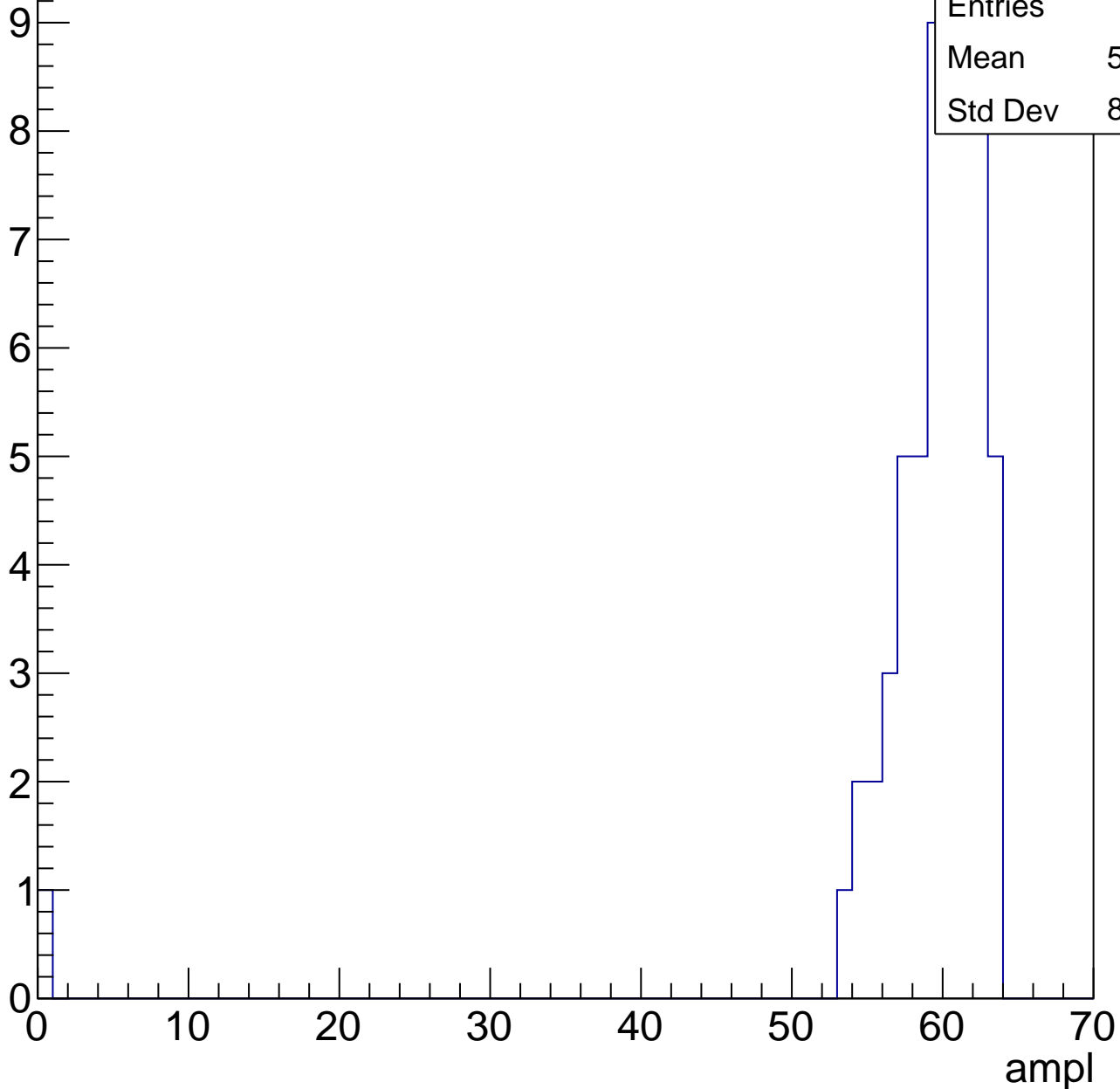
Entries	55
Mean	54.15
Std Dev	2.993



# B1L000S, U21-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

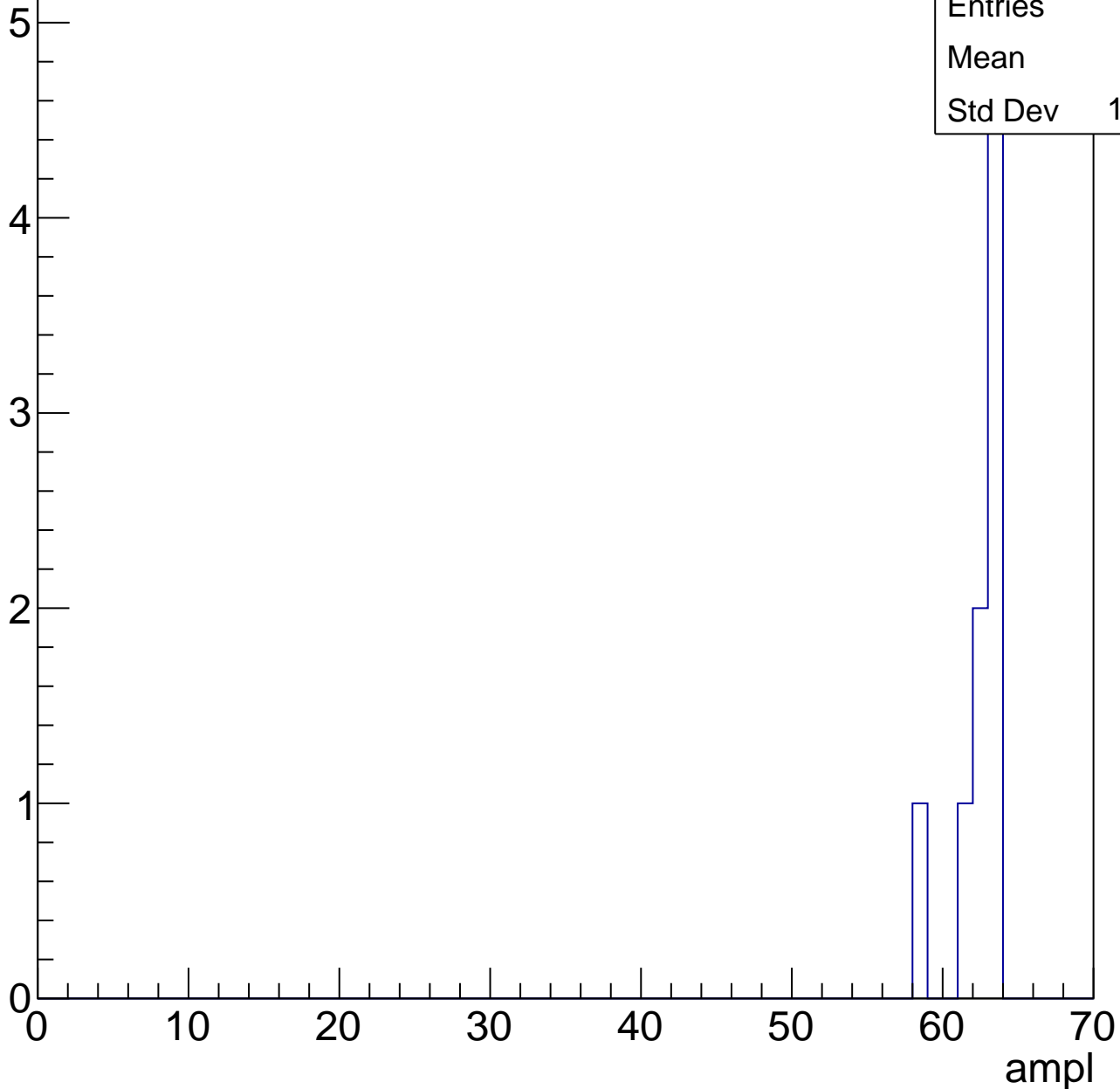


# B1L000S, U21-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	9
Mean	62
Std Dev	1.563





# B1L000S, U21-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B1L000S, U21-ch84, adc0

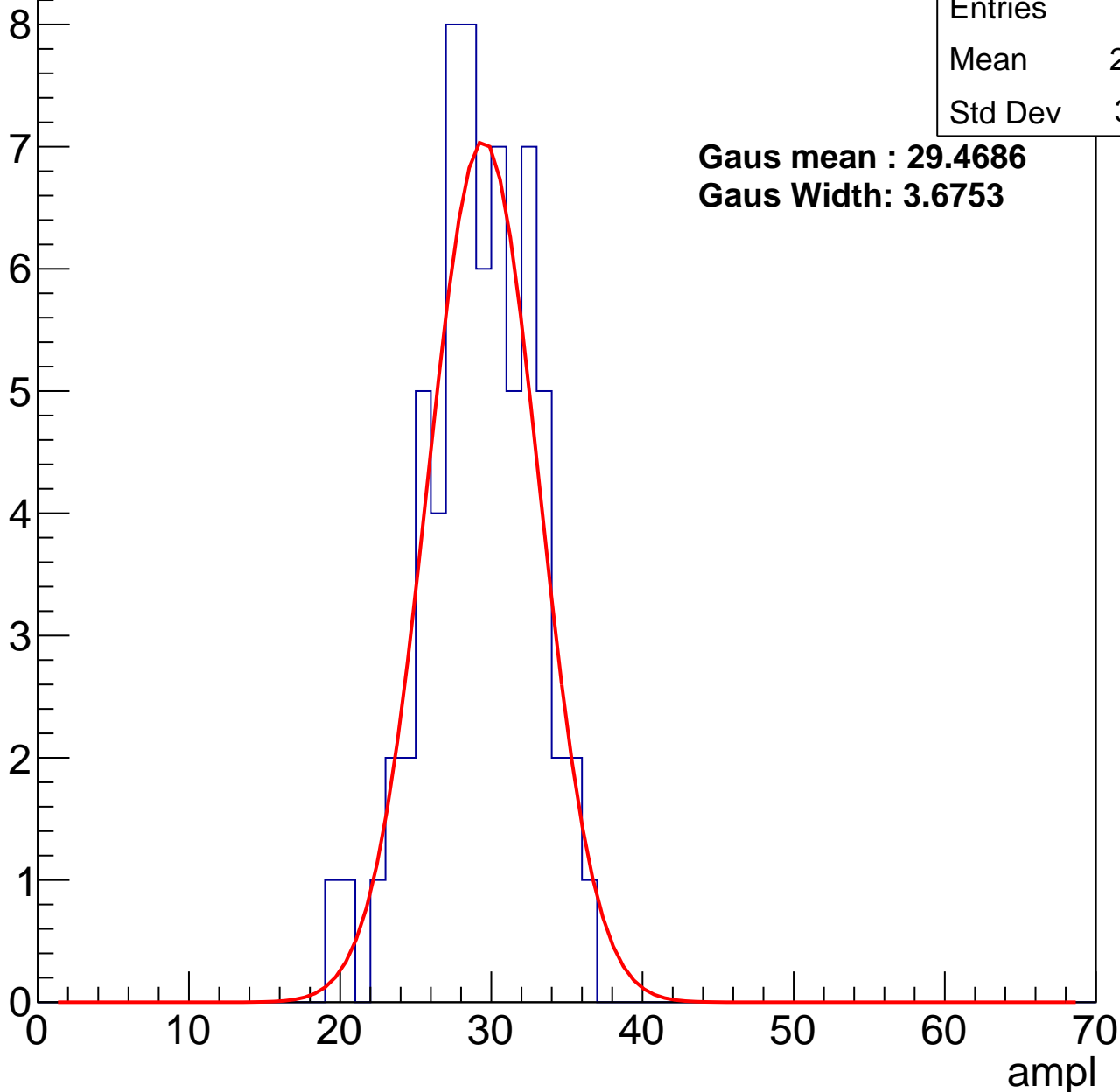
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.75
Std Dev	3.551

**Gaus mean : 29.4686**

**Gaus Width: 3.6753**



# B1L000S, U21-ch84, adc1

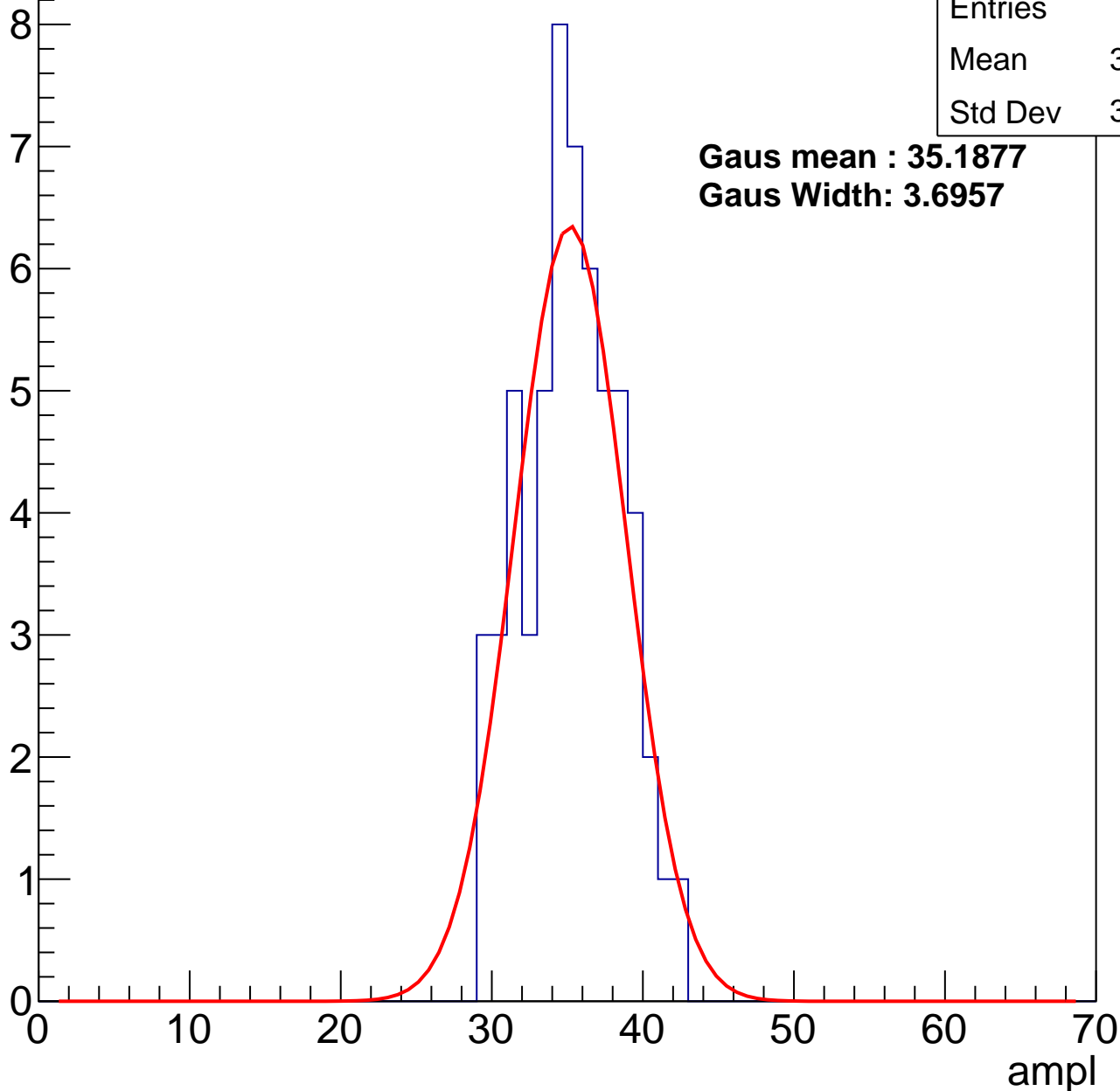
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.83
Std Dev	3.179

**Gaus mean : 35.1877**

**Gaus Width: 3.6957**



# B1L000S, U21-ch84, adc2

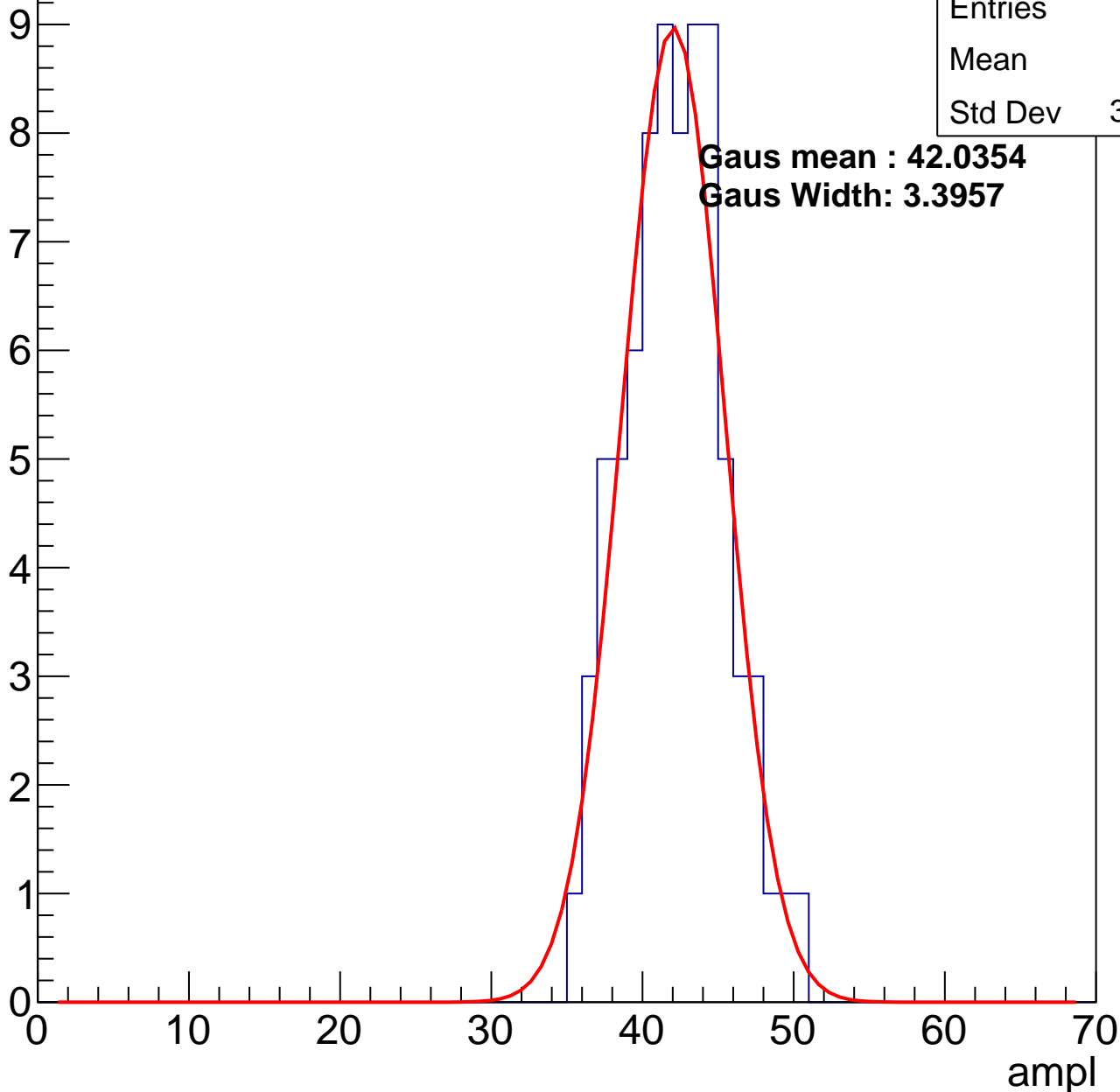
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	41.7
Std Dev	3.248

**Gaus mean : 42.0354**

**Gaus Width: 3.3957**

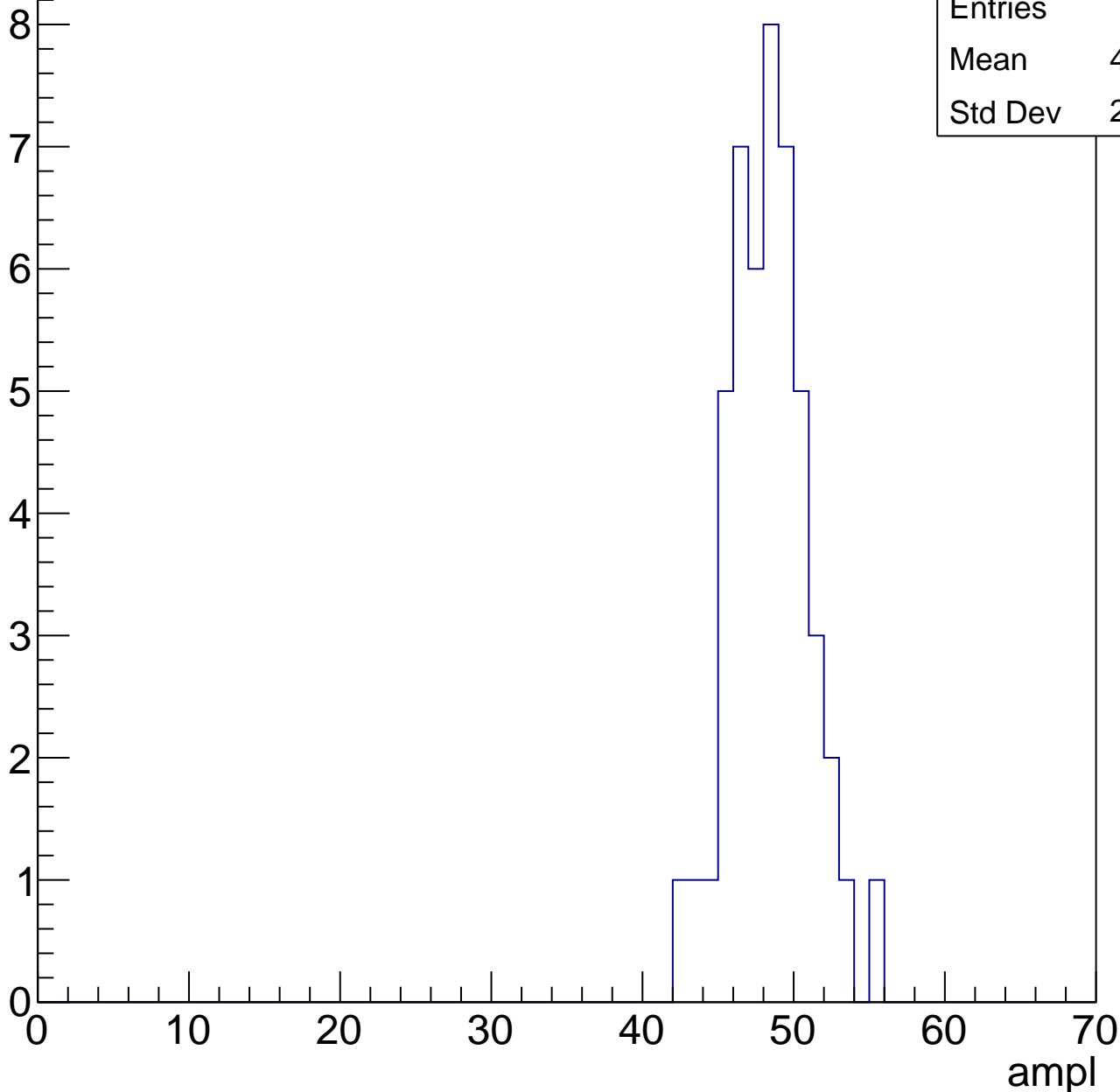


# B1L000S, U21-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

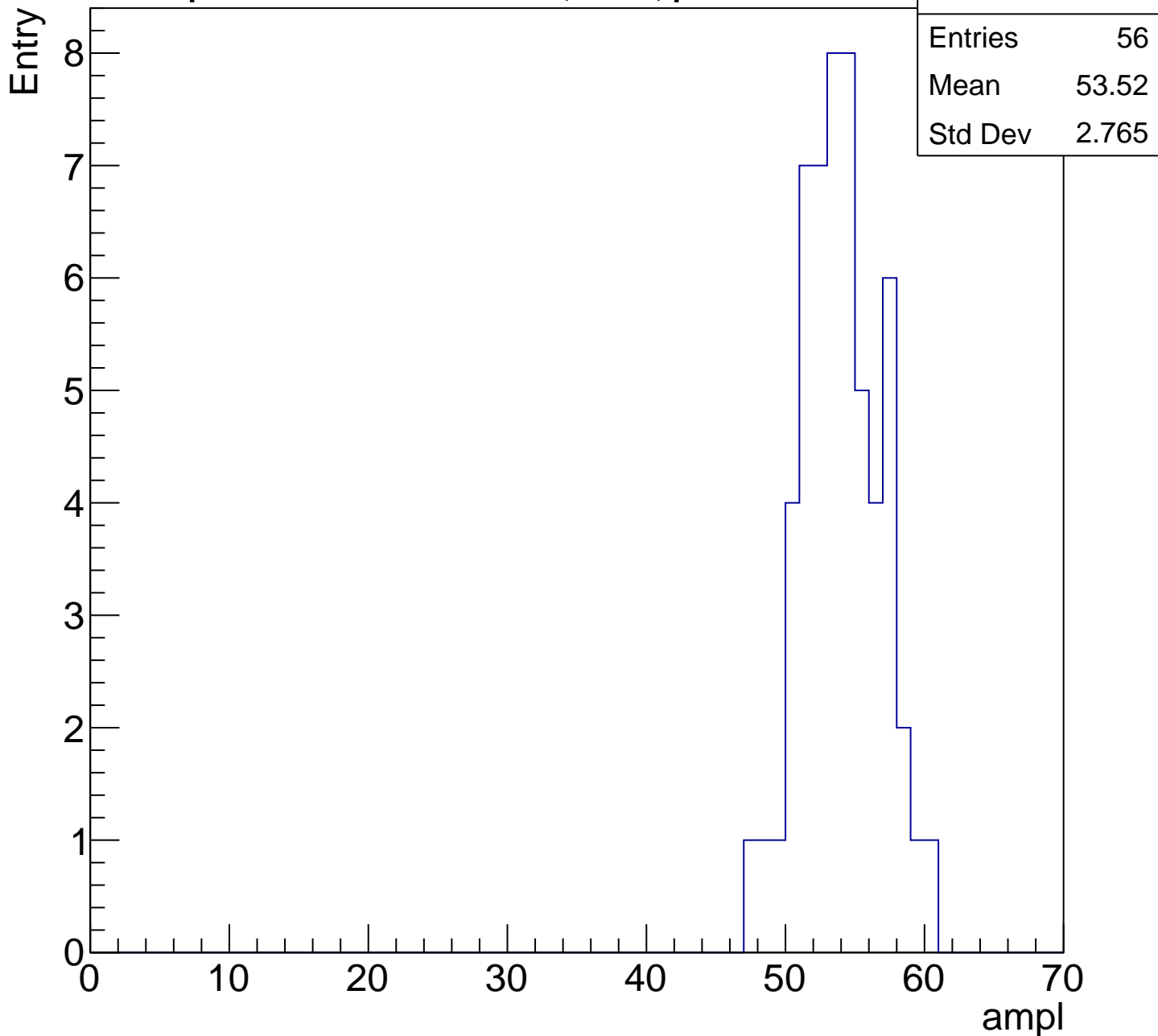
Entry

Entries	48
Mean	47.92
Std Dev	2.564



# B1L000S, U21-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U21-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries

52

Mean

58.37

Std Dev

3.019

0

1

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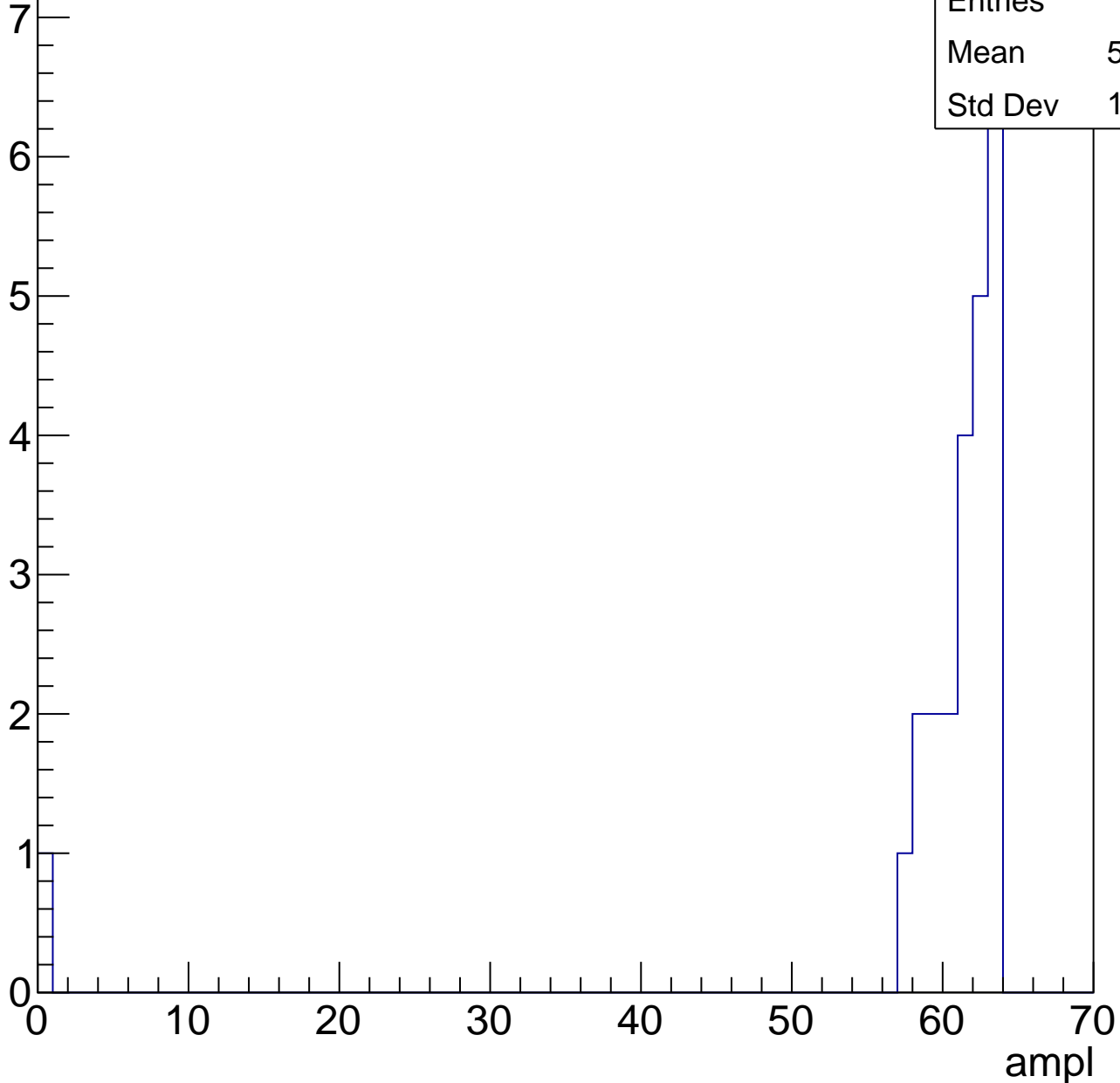
320

# B1L000S, U21-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.58
Std Dev	12.35





# B1L000S, U21-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U21-ch85, adc0

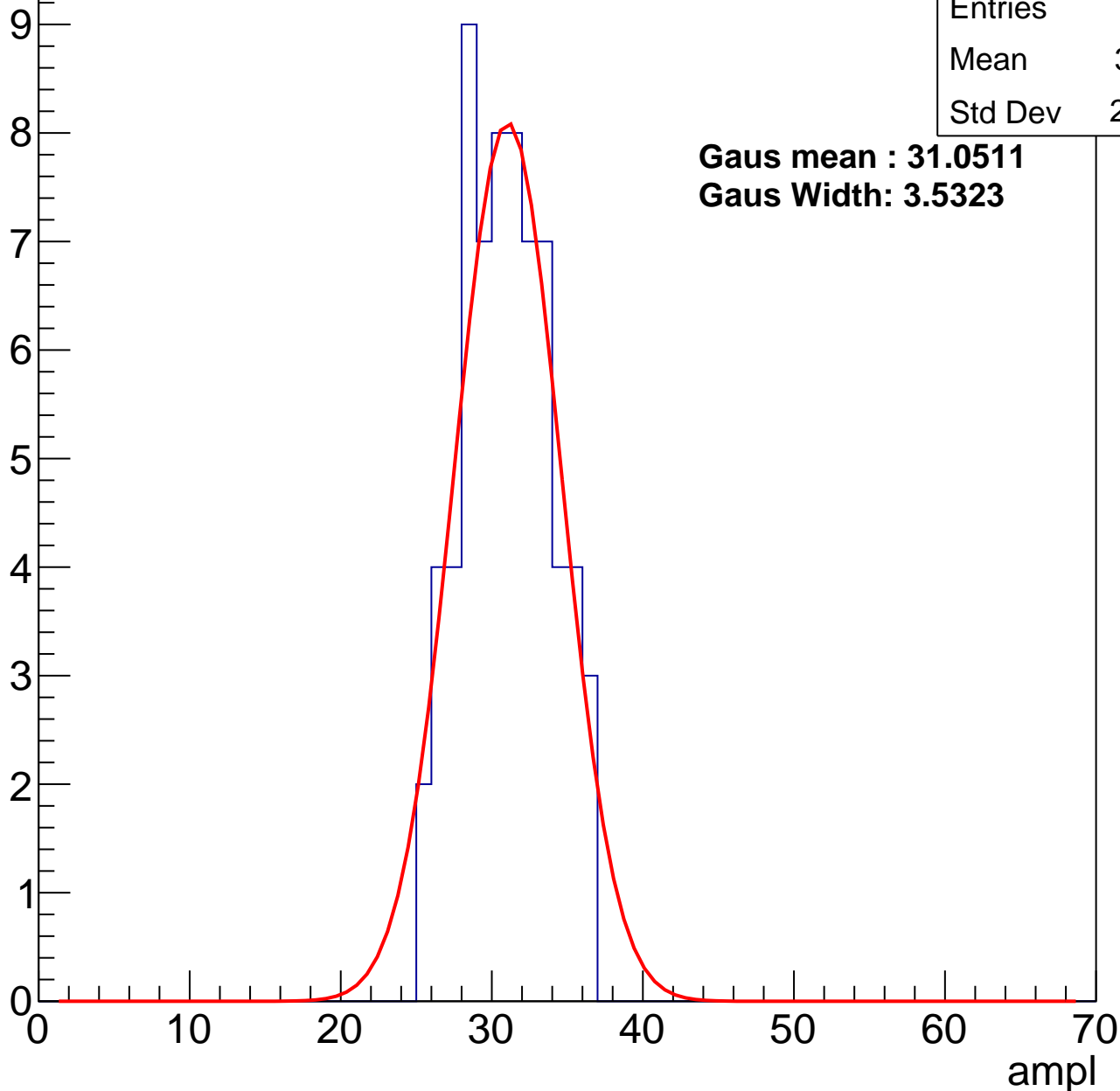
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	30.51
Std Dev	2.857

**Gaus mean : 31.0511**

**Gaus Width: 3.5323**



# B1L000S, U21-ch85, adc1

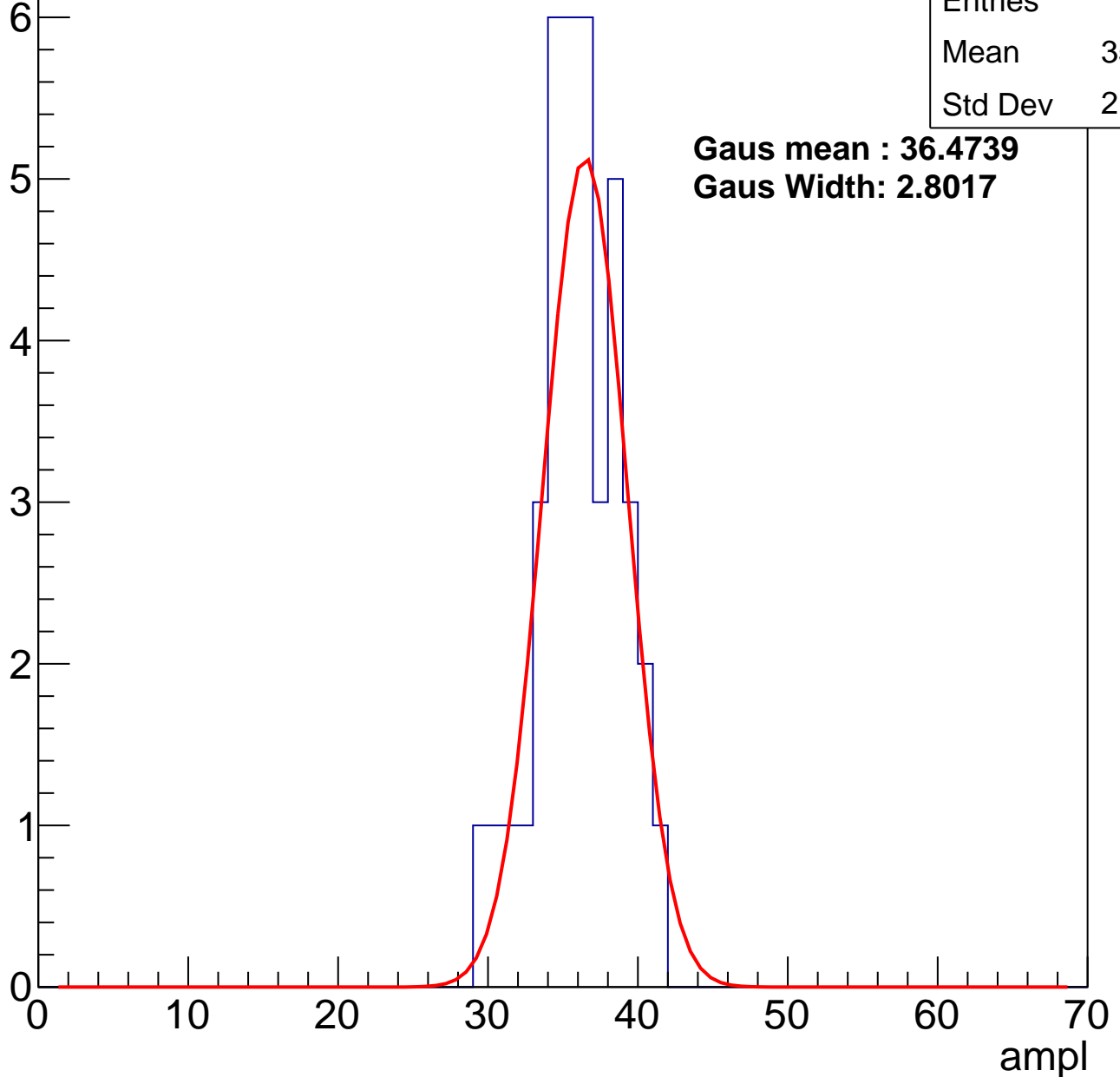
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	35.64
Std Dev	2.703

**Gaus mean : 36.4739**

**Gaus Width: 2.8017**



# B1L000S, U21-ch85, adc2

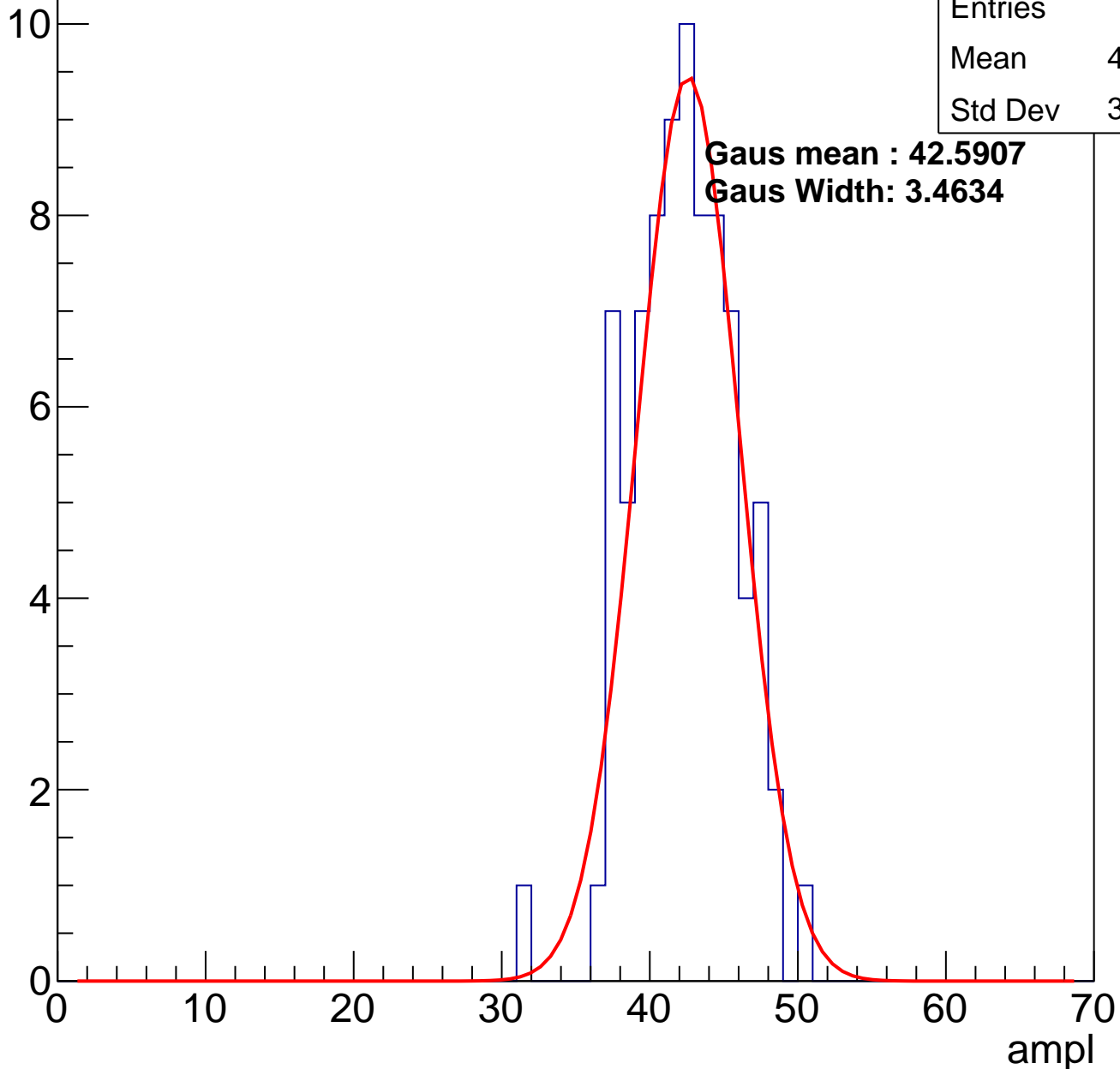
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	41.86
Std Dev	3.369

**Gaus mean : 42.5907**

**Gaus Width: 3.4634**

Entry



# B1L000S, U21-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

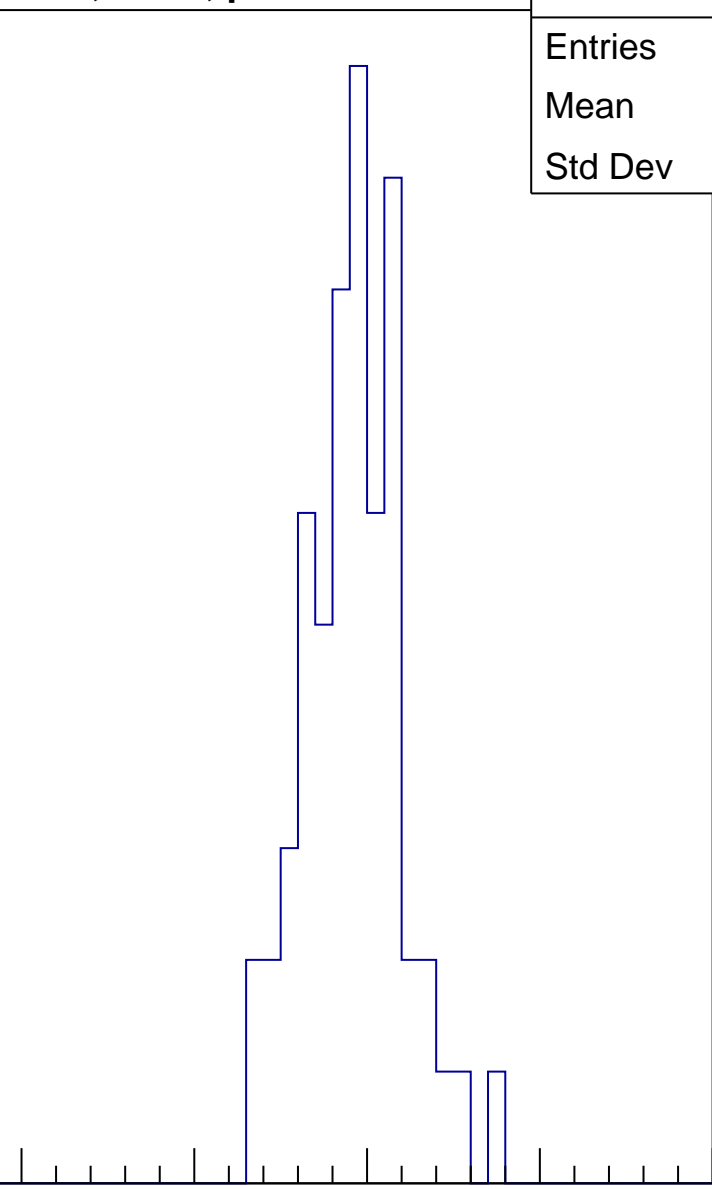
Entries	58
Mean	48.78
Std Dev	2.835

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

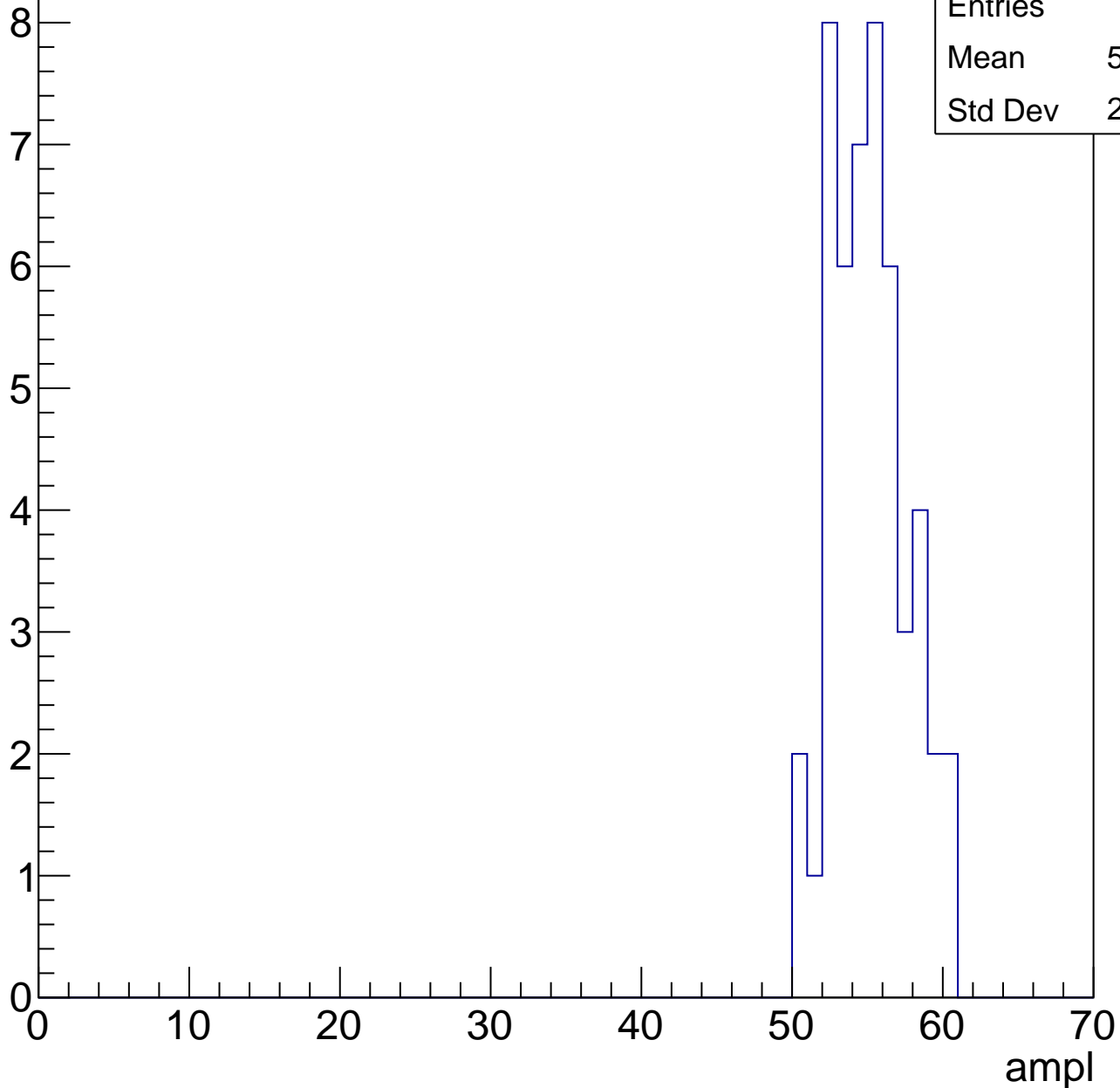


# B1L000S, U21-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

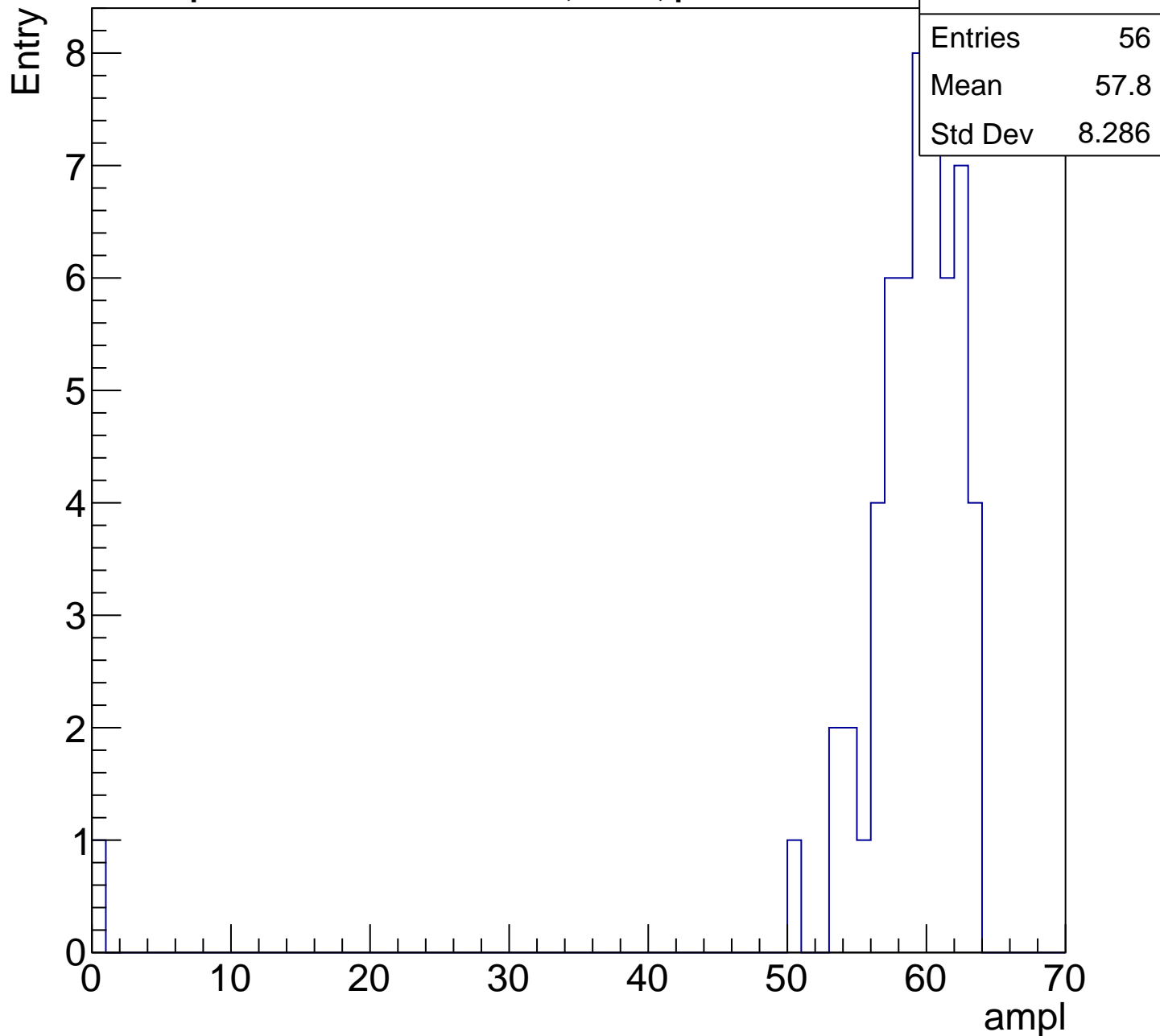
Entry

Entries	49
Mean	54.69
Std Dev	2.476



# B1L000S, U21-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

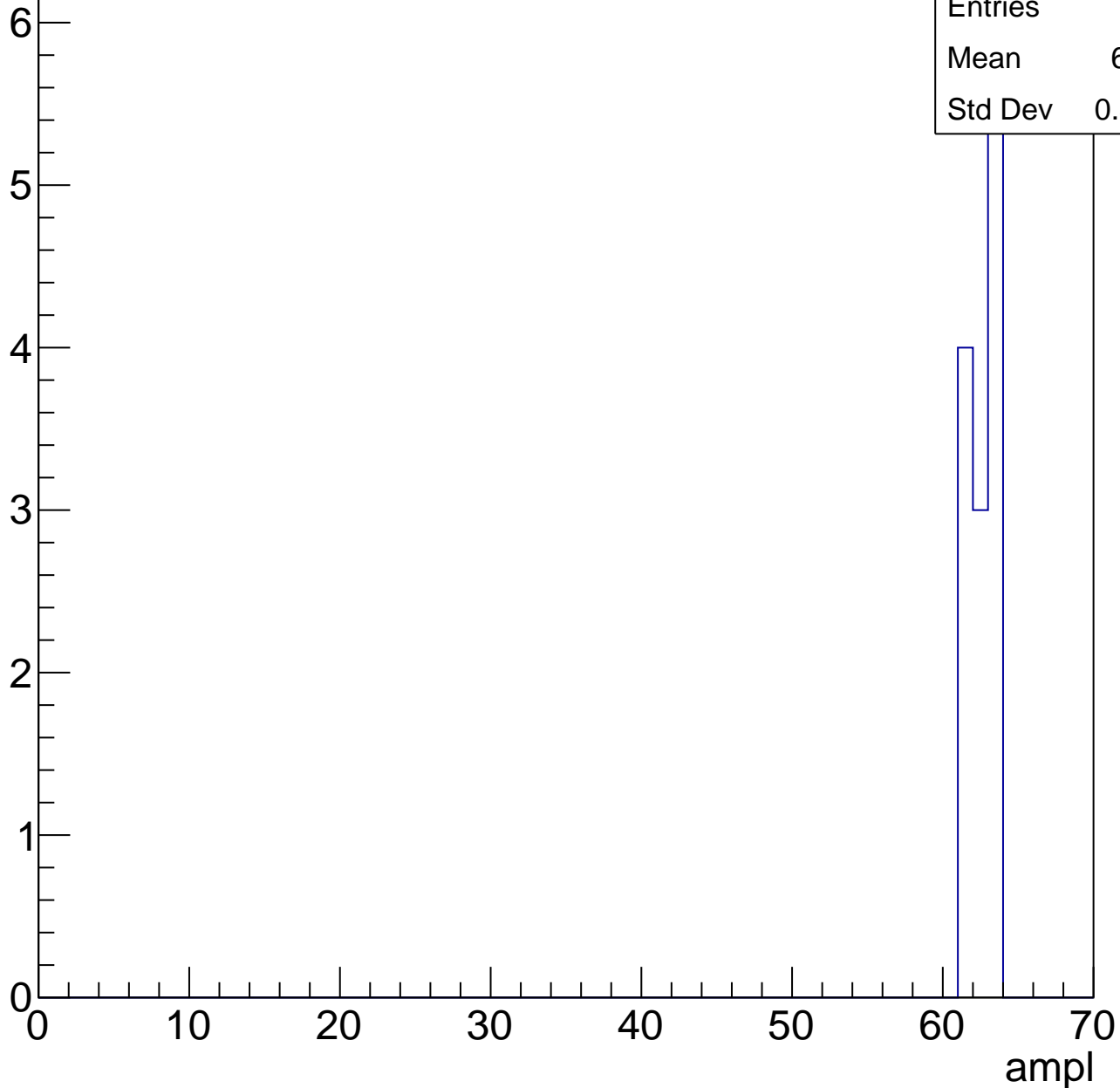


# B1L000S, U21-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	62.15
Std Dev	0.8635

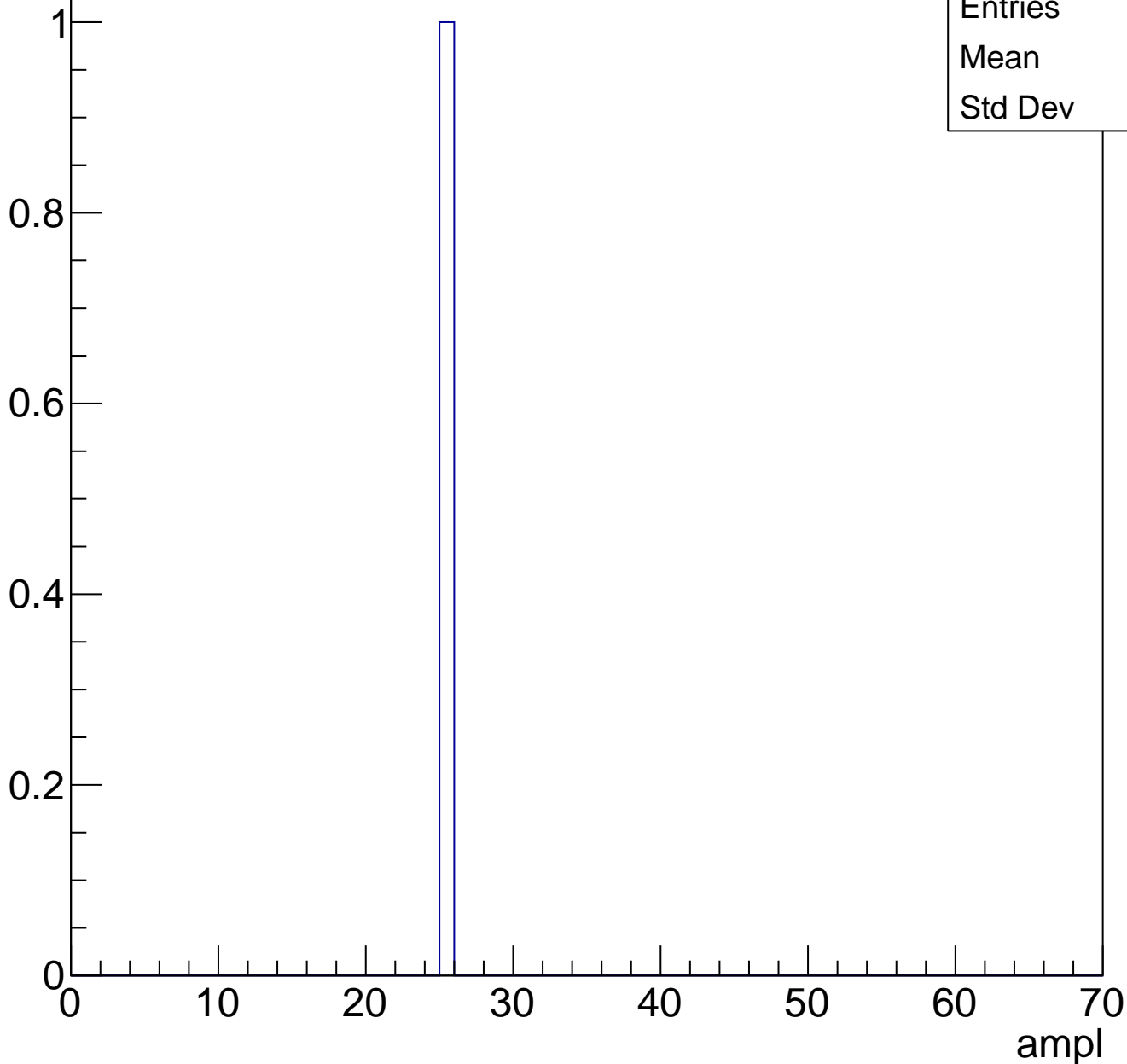




# B1L000S, U21-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	25
Std Dev	0

# B1L000S, U21-ch86, adc0

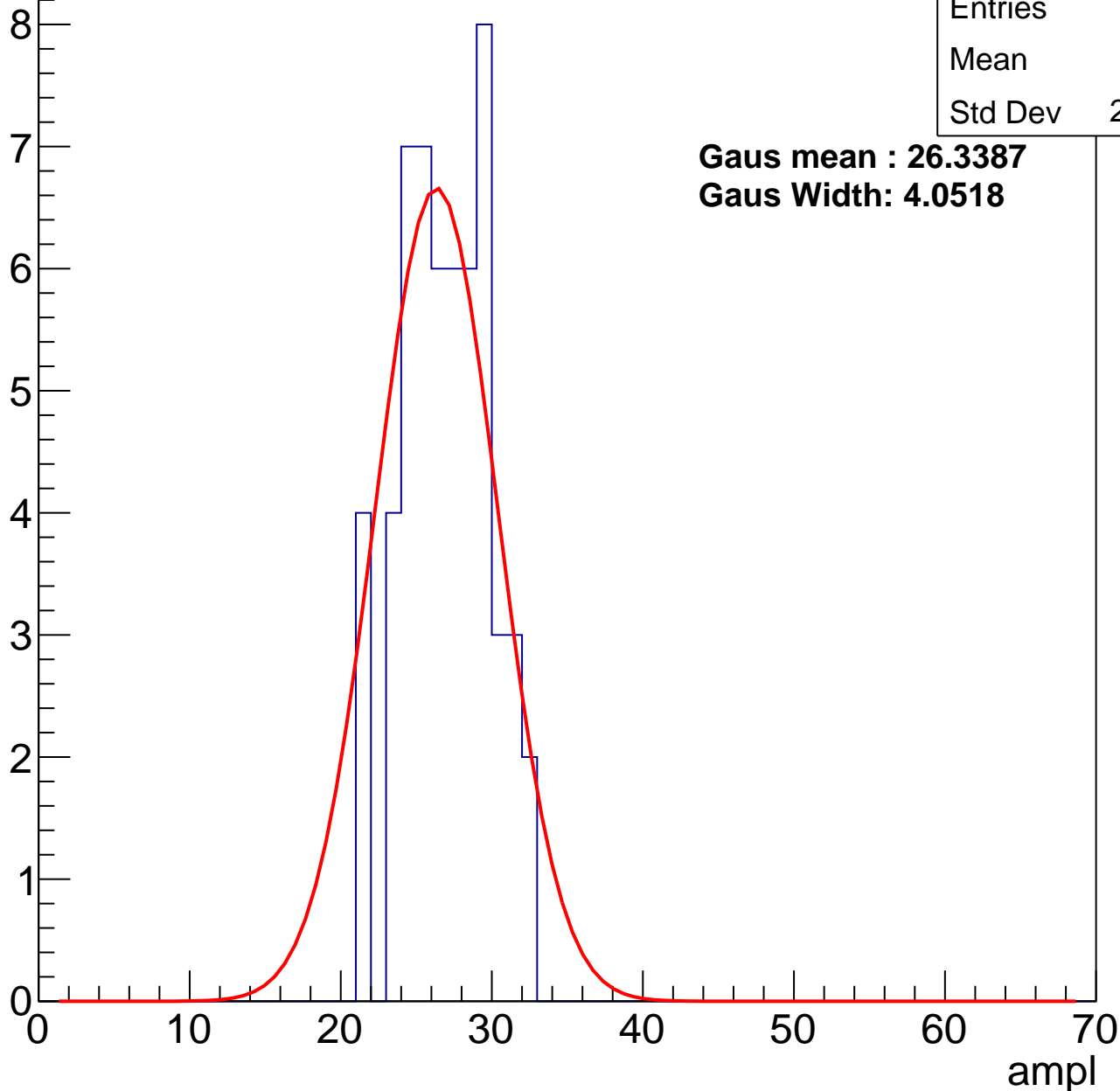
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	26.5
Std Dev	2.847

**Gaus mean : 26.3387**

**Gaus Width: 4.0518**



# B1L000S, U21-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	33.4
Std Dev	3.312

**Gaus mean : 34.2634**

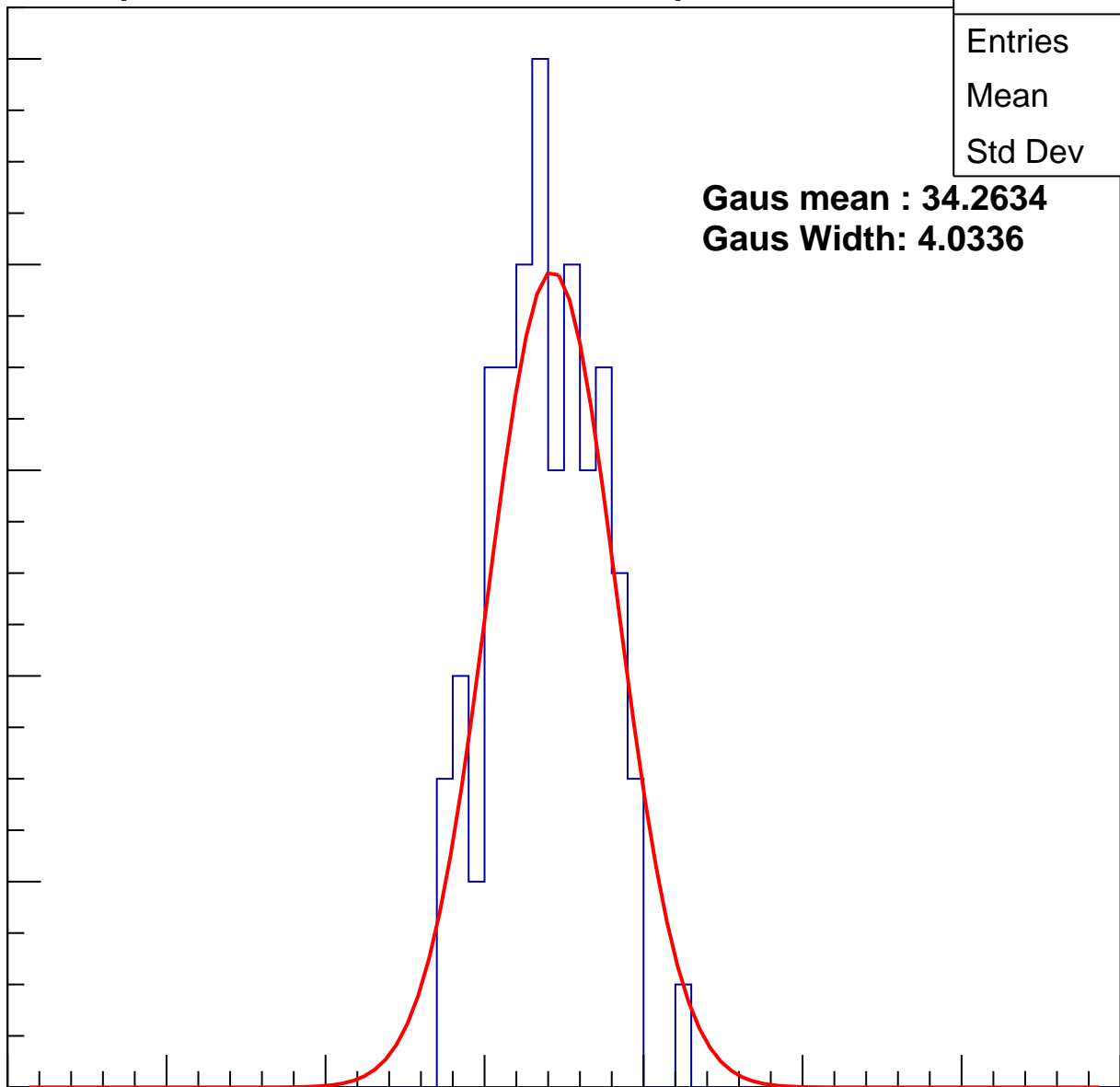
**Gaus Width: 4.0336**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch86, adc2

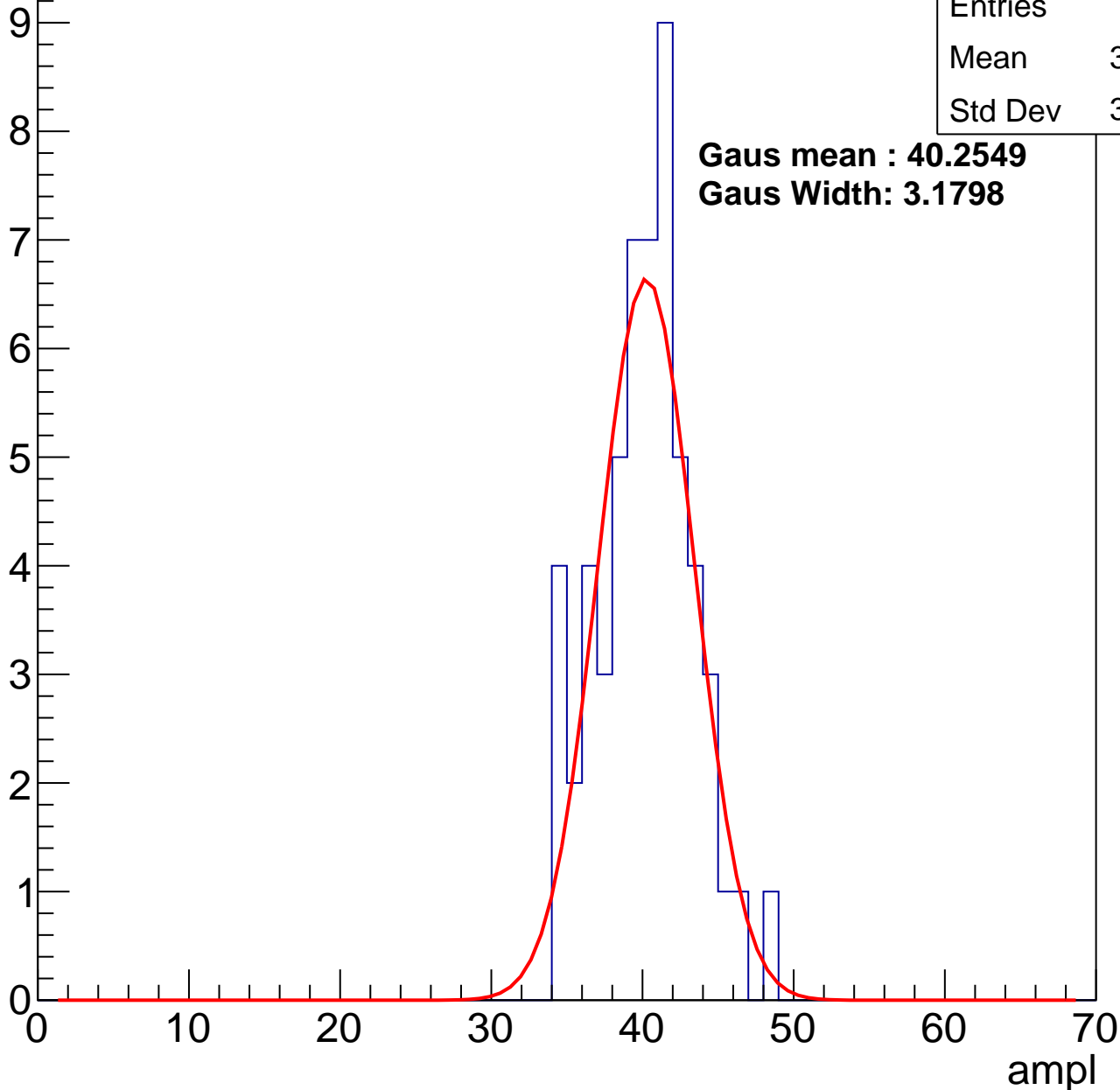
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	39.75
Std Dev	3.124

**Gaus mean : 40.2549**

**Gaus Width: 3.1798**

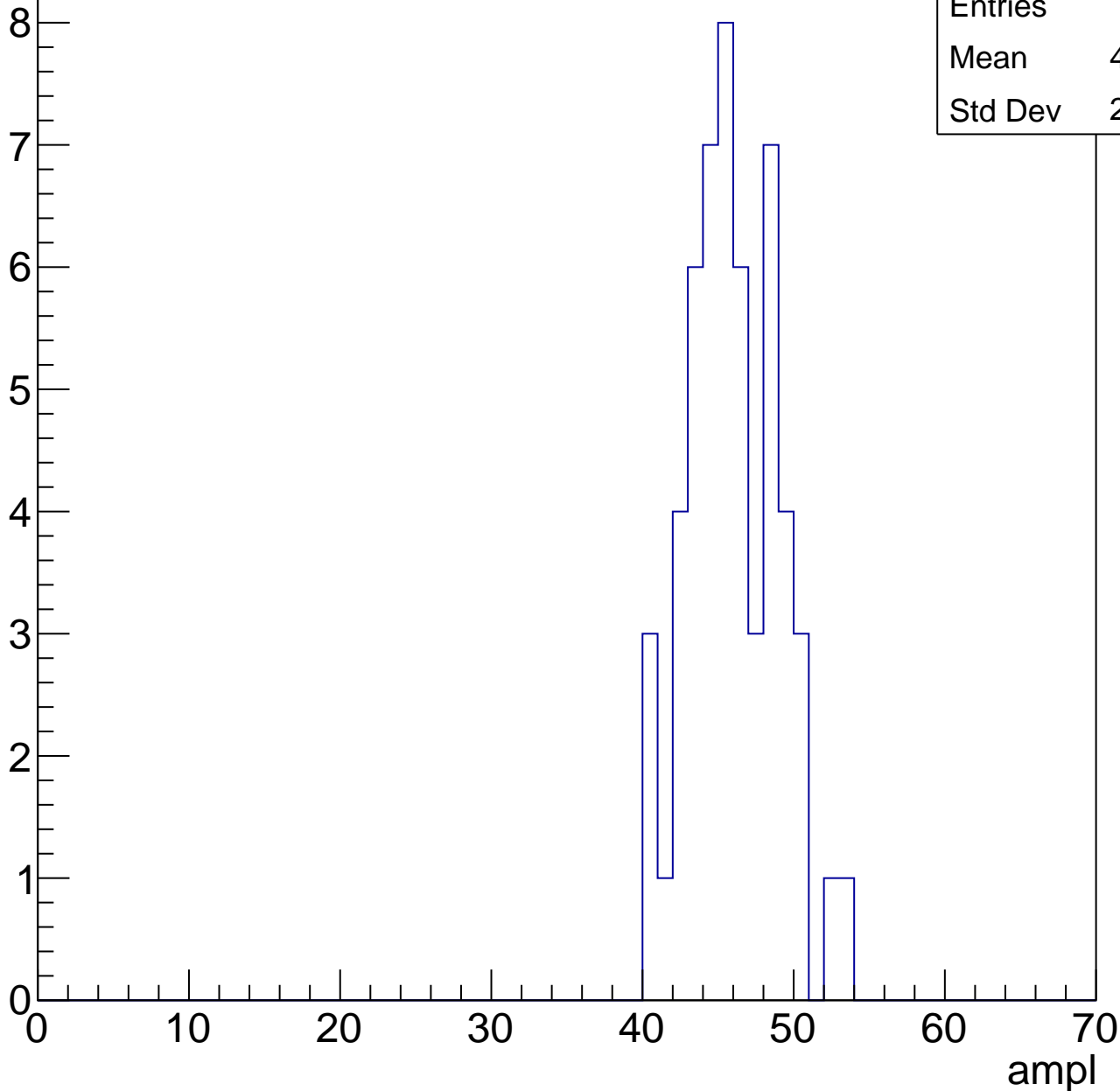


# B1L000S, U21-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	45.54
Std Dev	2.967

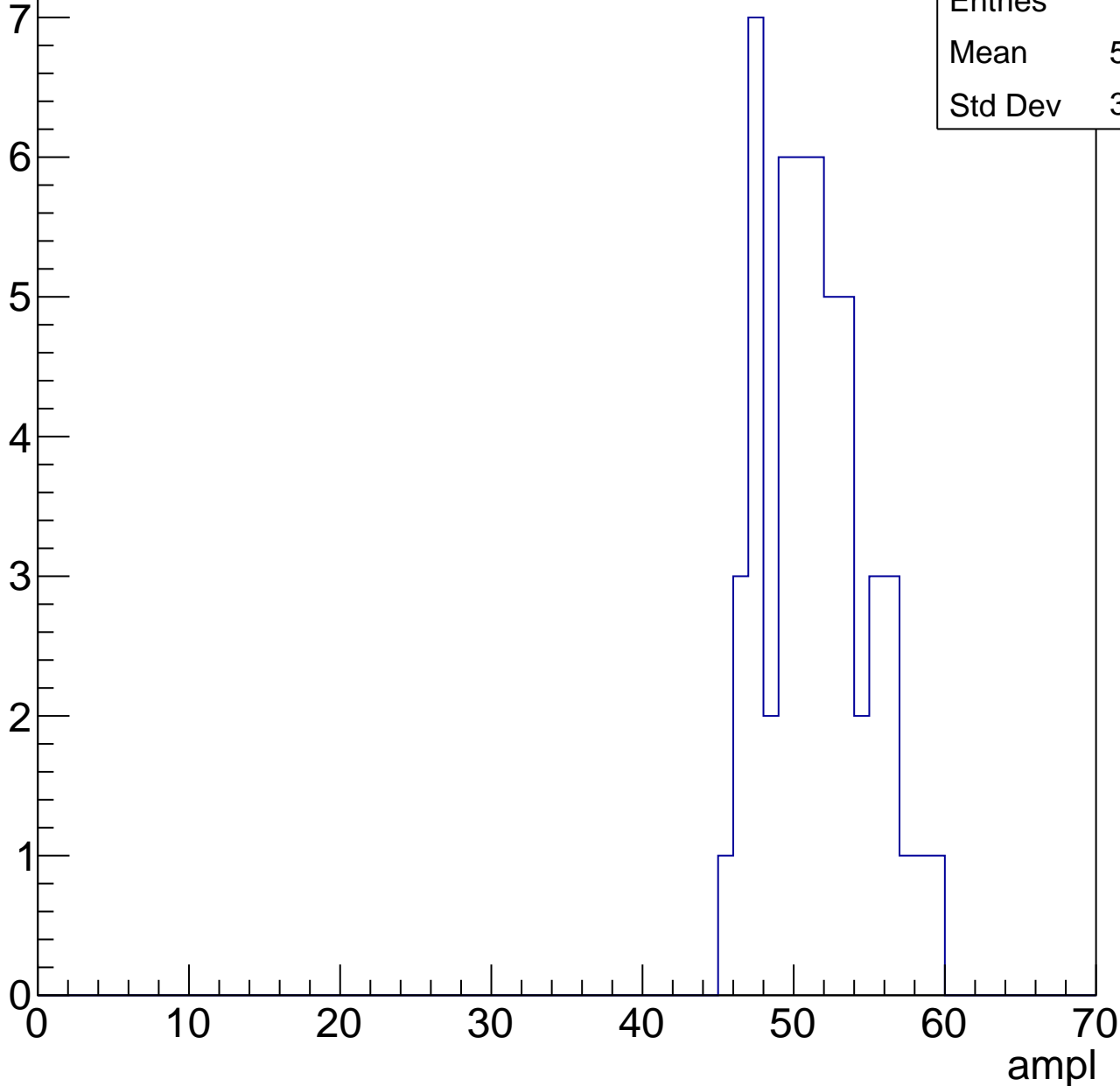


# B1L000S, U21-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

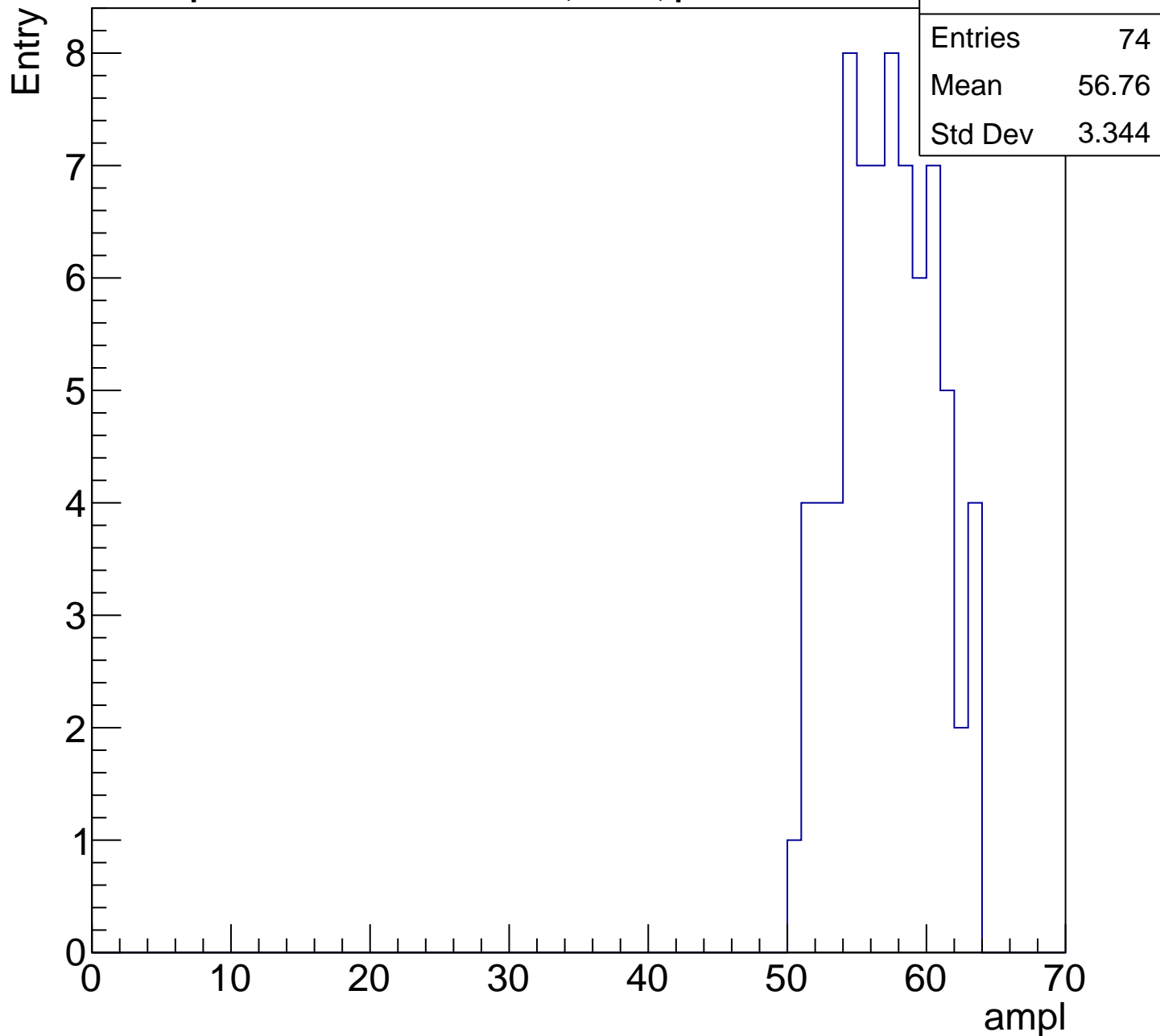
Entry

Entries	52
Mean	50.92
Std Dev	3.356



# B1L000S, U21-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

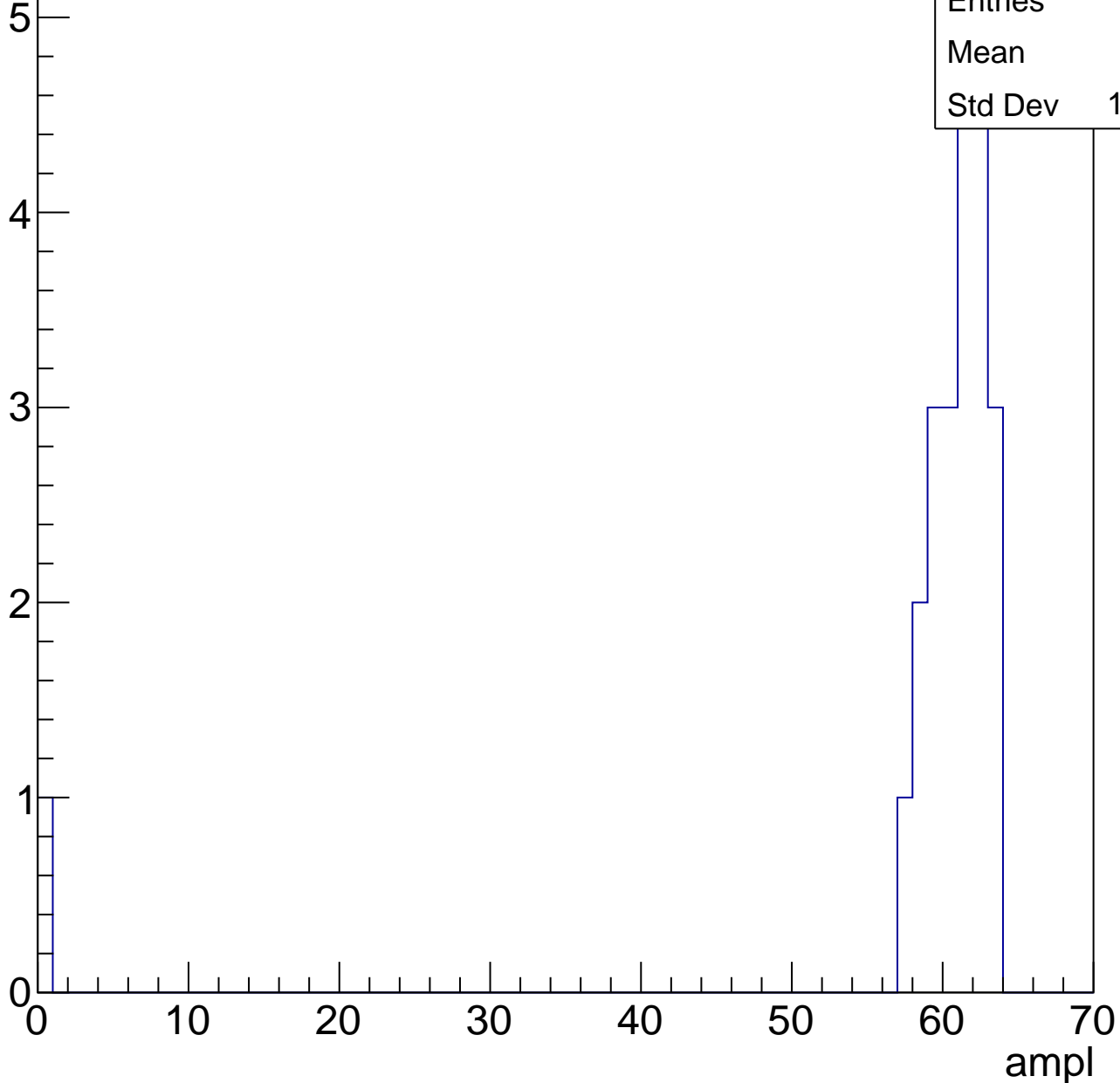


# B1L000S, U21-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	58
Std Dev	12.48

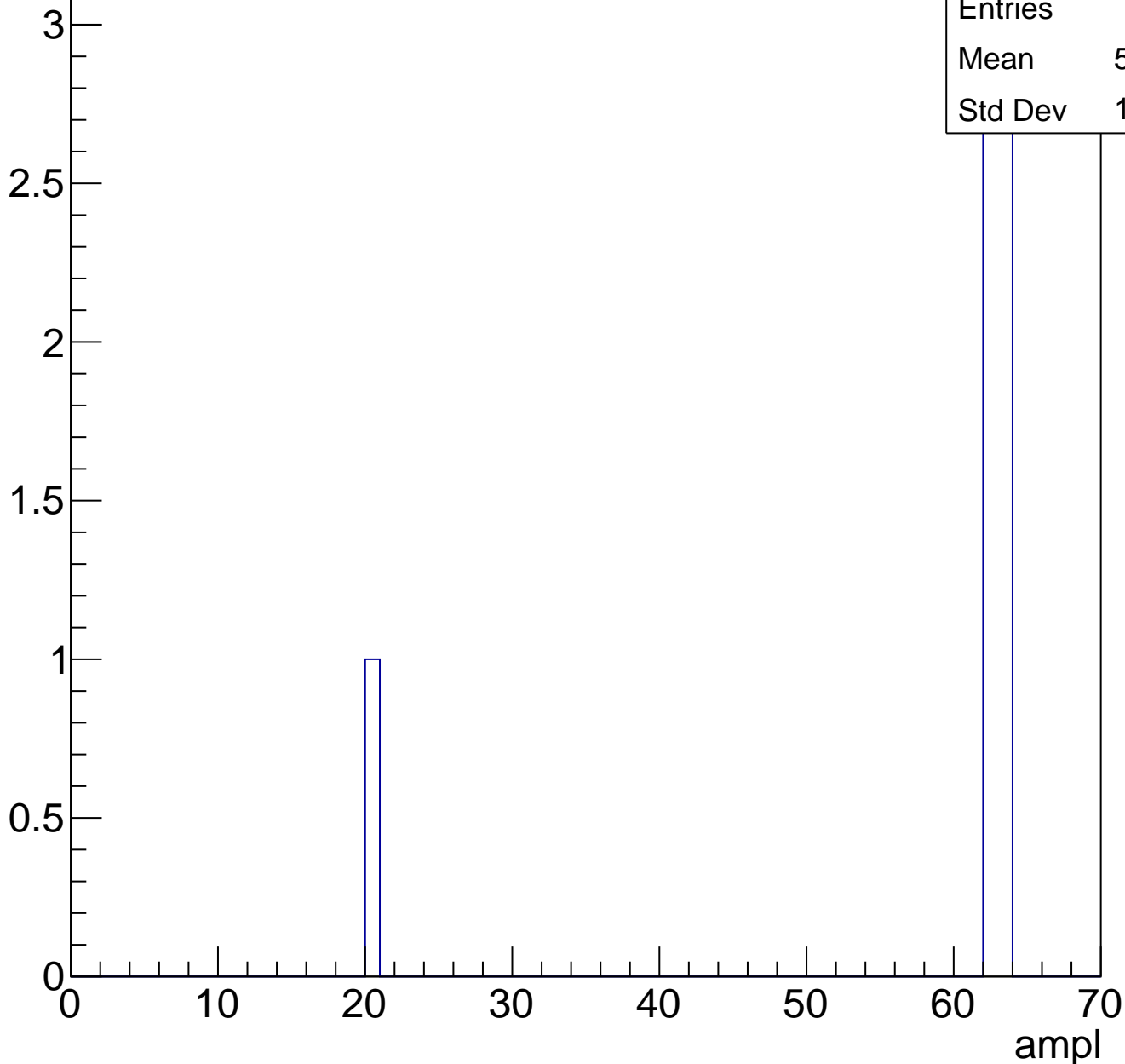




# B1L000S, U21-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch87, adc0

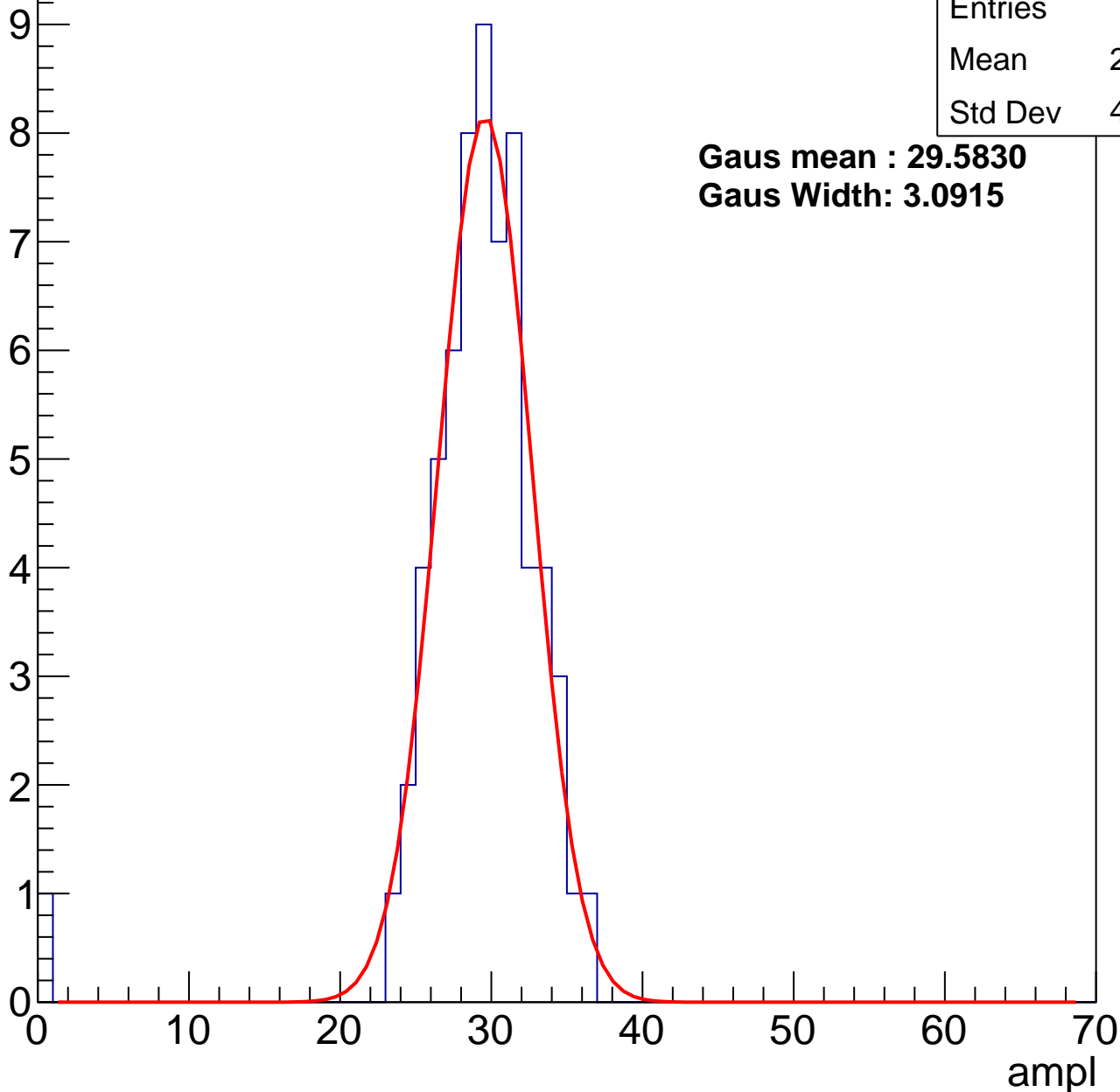
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.73
Std Dev	4.614

**Gaus mean : 29.5830**

**Gaus Width: 3.0915**



# B1L000S, U21-ch87, adc1

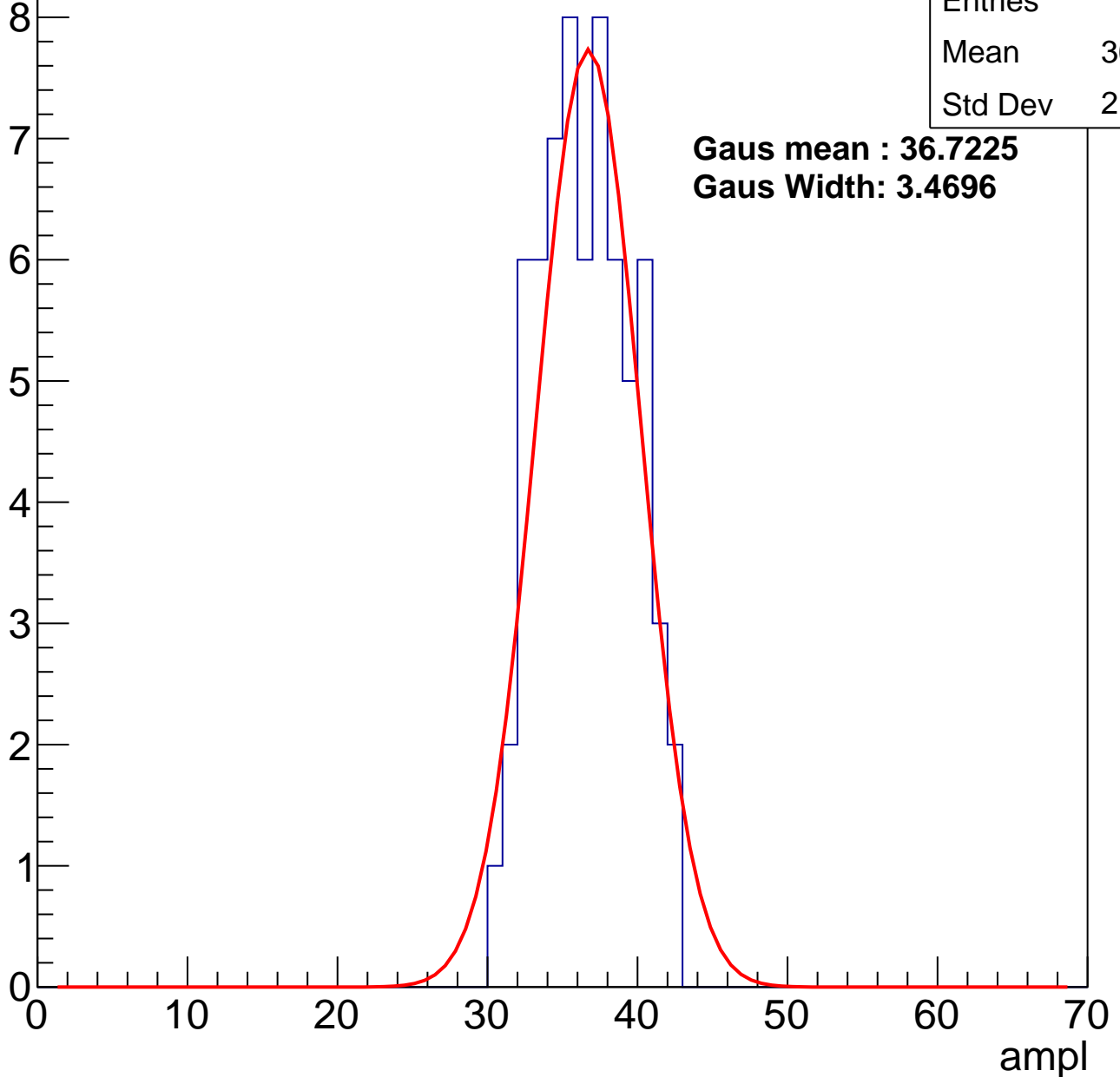
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	36.09
Std Dev	2.994

**Gaus mean : 36.7225**

**Gaus Width: 3.4696**



# B1L000S, U21-ch87, adc2

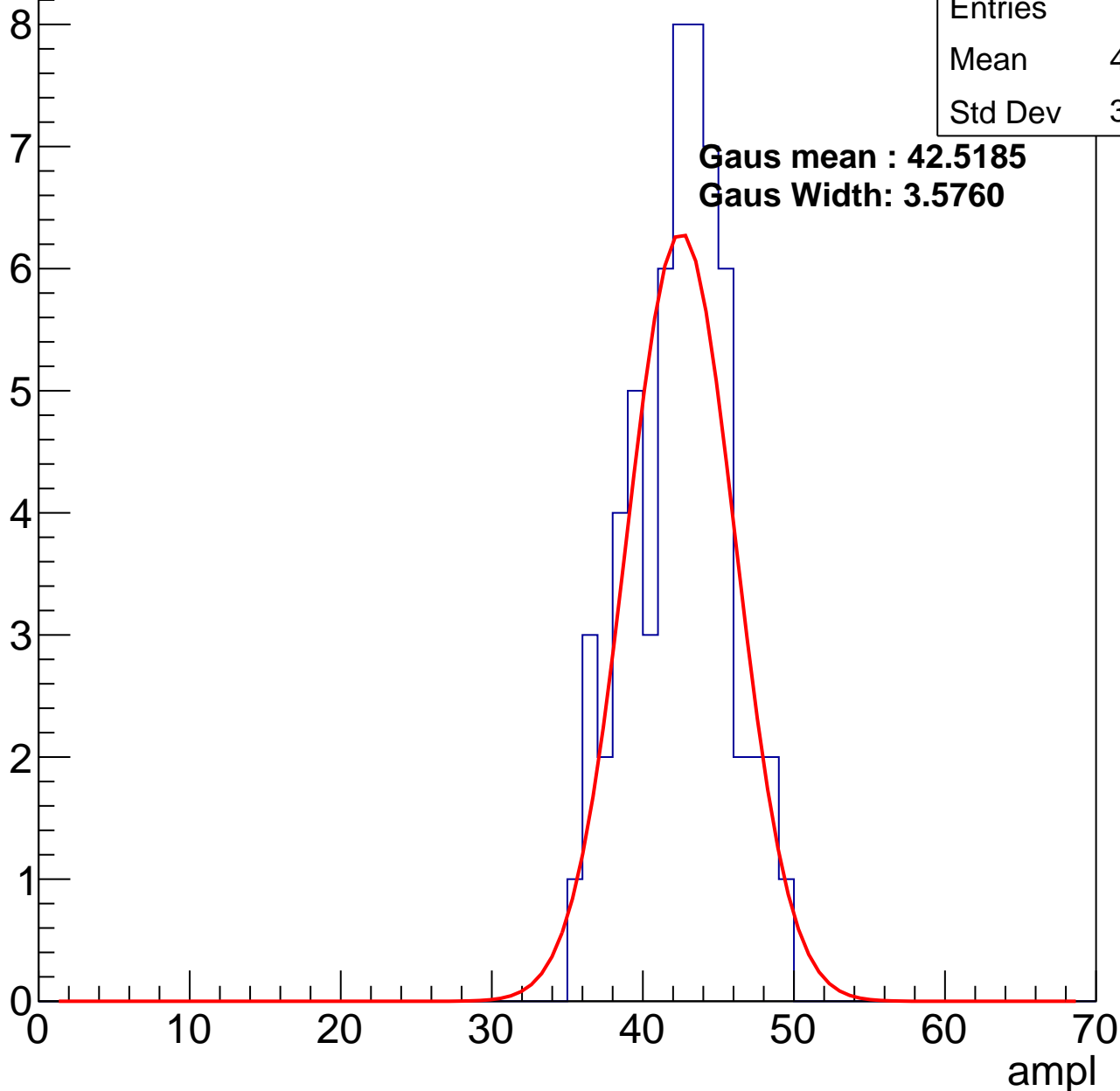
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.98
Std Dev	3.233

**Gaus mean : 42.5185**

**Gaus Width: 3.5760**

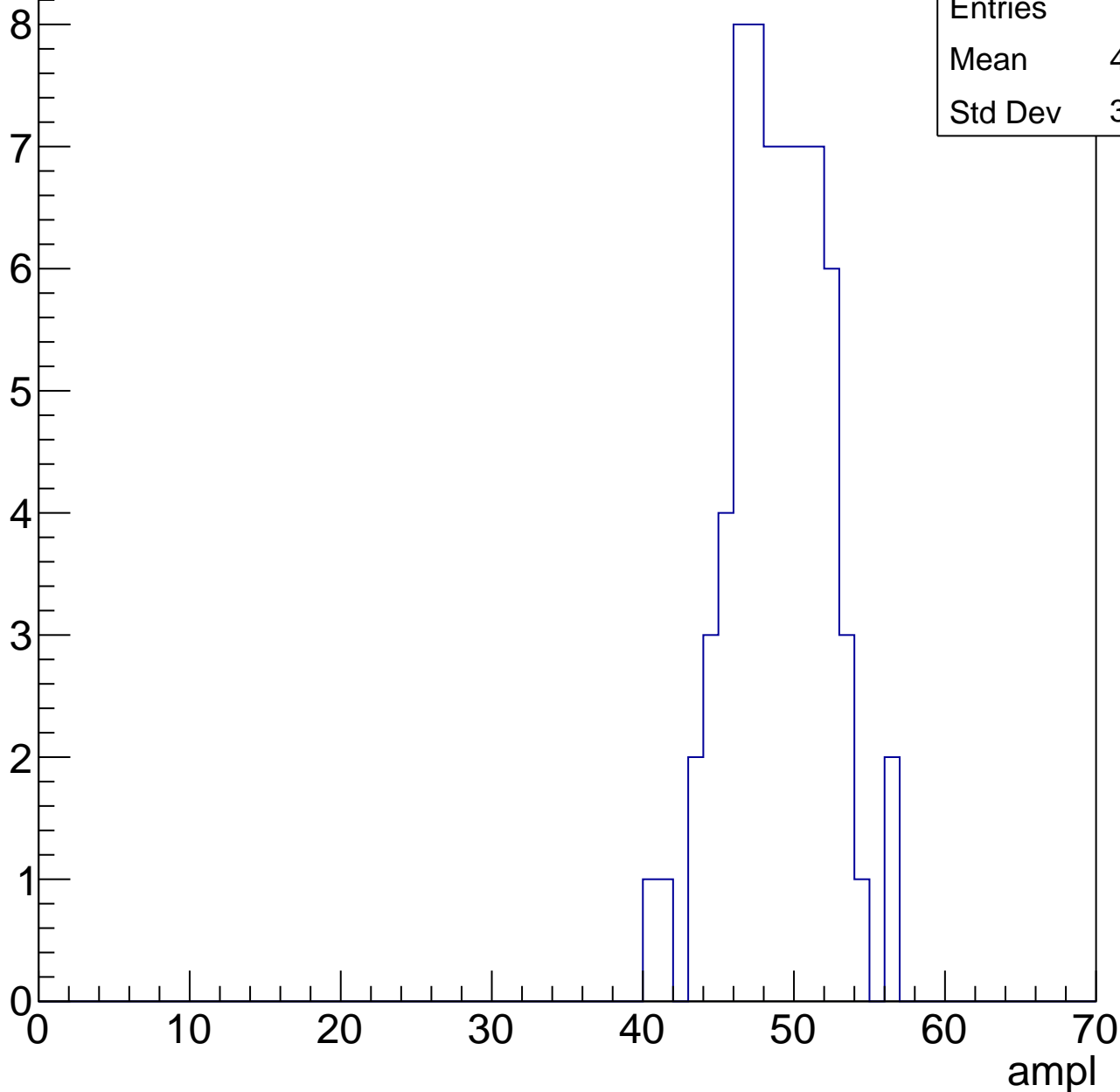


# B1L000S, U21-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	48.45
Std Dev	3.247

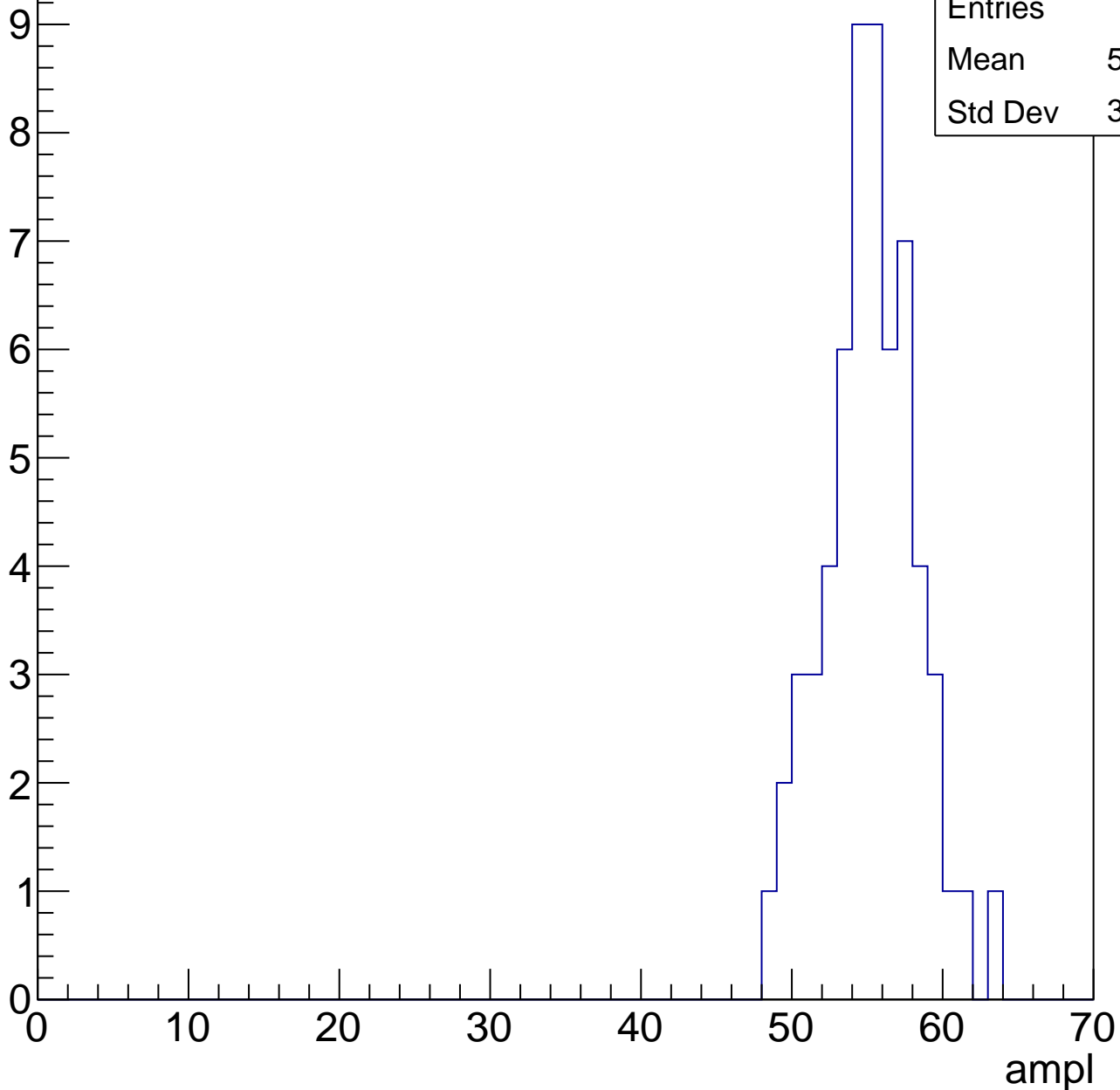


# B1L000S, U21-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.73
Std Dev	3.032



# B1L000S, U21-ch87, adc5

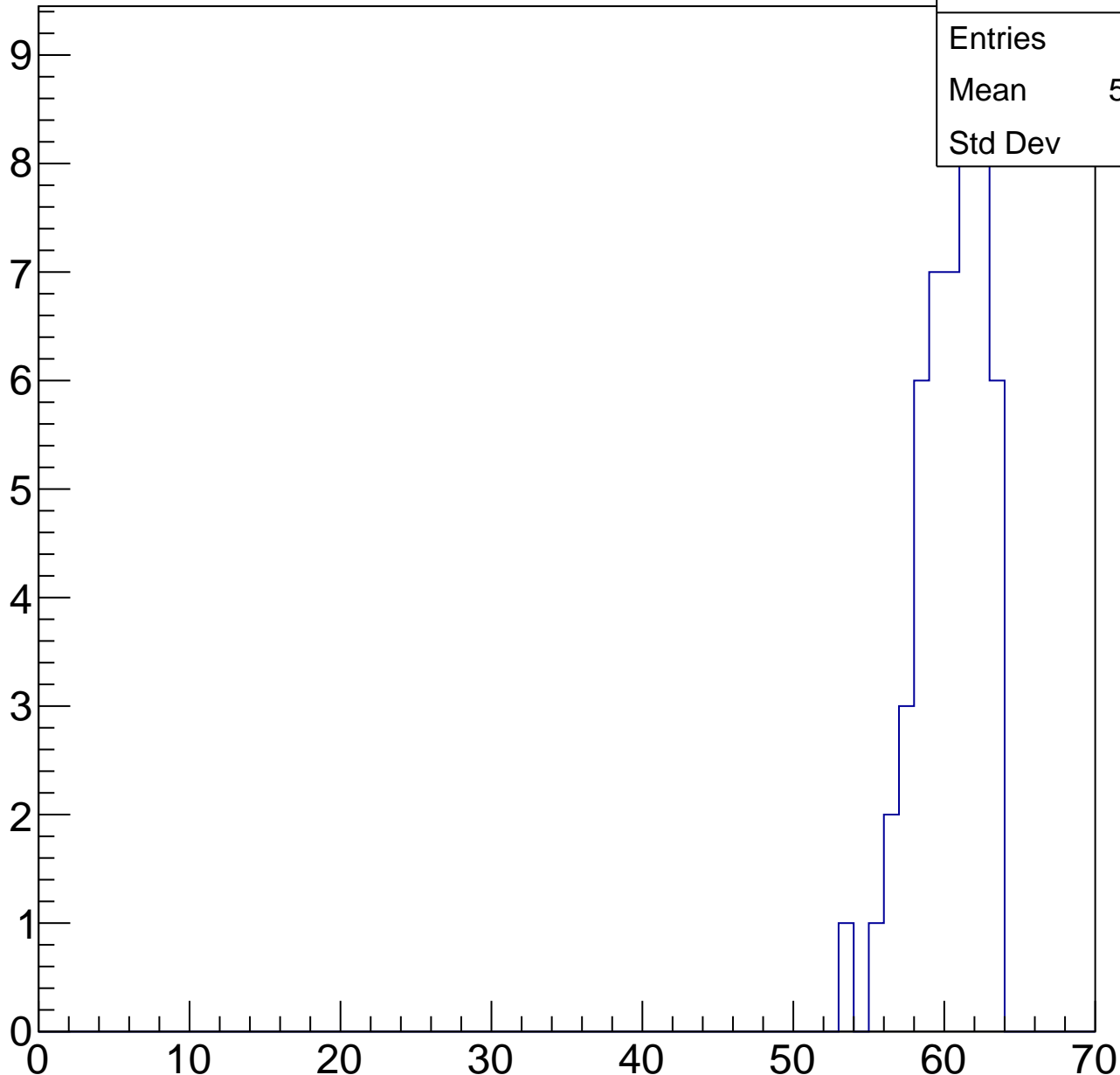
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.94
Std Dev	2.27

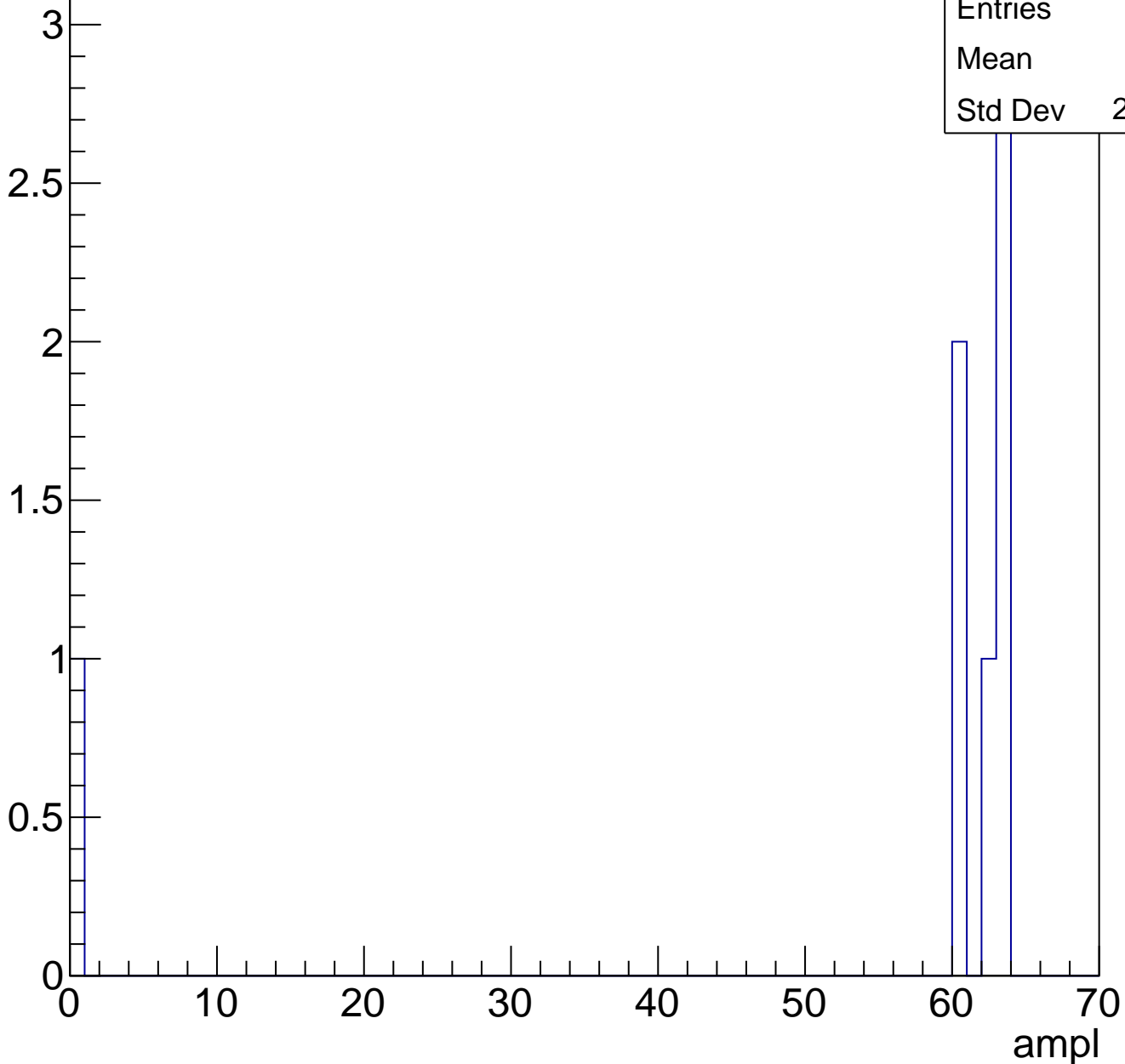
ampl



# B1L000S, U21-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L000S, U21-ch88, adc0

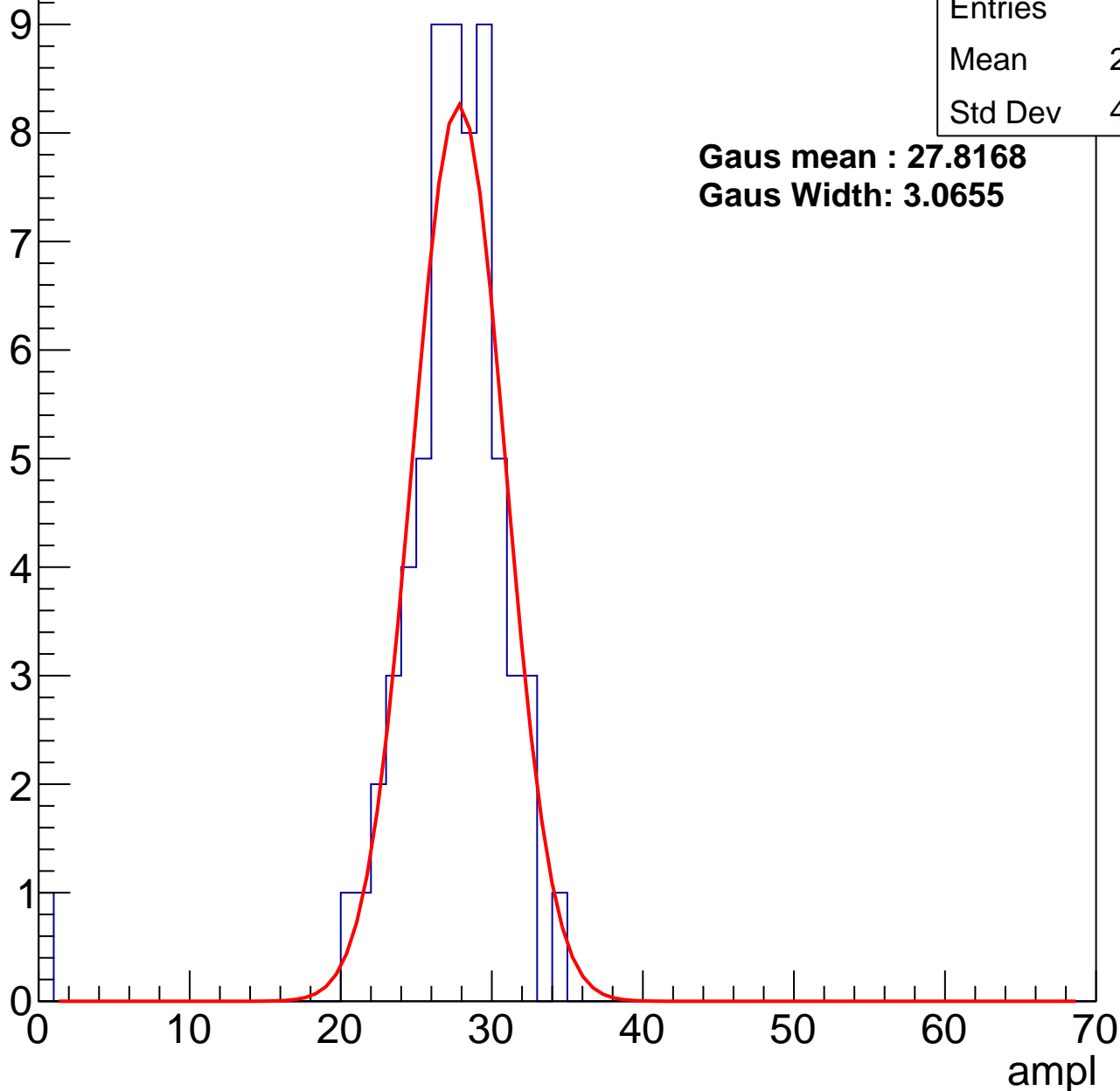
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	26.72
Std Dev	4.389

**Gaus mean : 27.8168**

**Gaus Width: 3.0655**



# B1L000S, U21-ch88, adc1

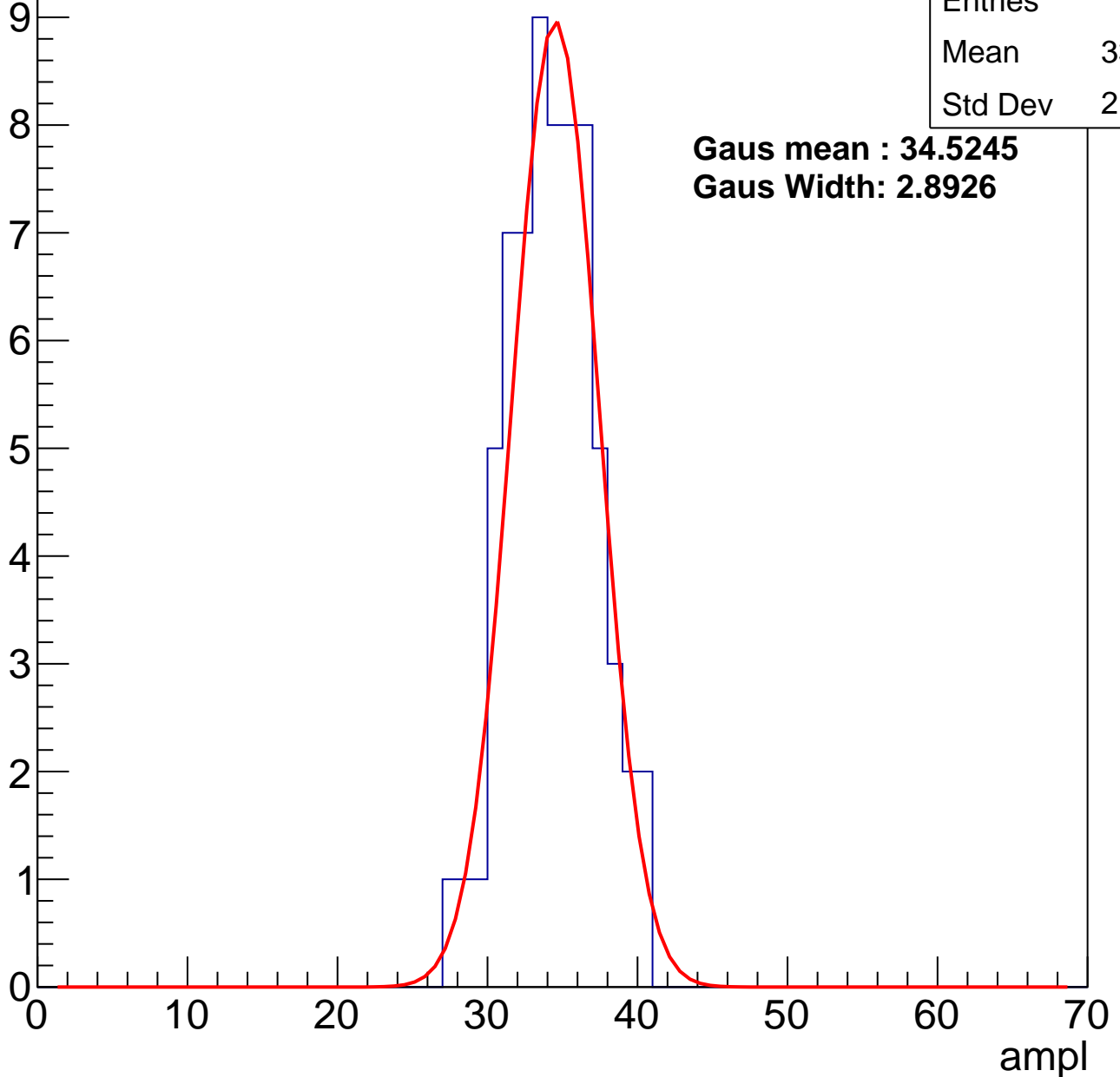
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	33.87
Std Dev	2.849

**Gaus mean : 34.5245**

**Gaus Width: 2.8926**



# B1L000S, U21-ch88, adc2

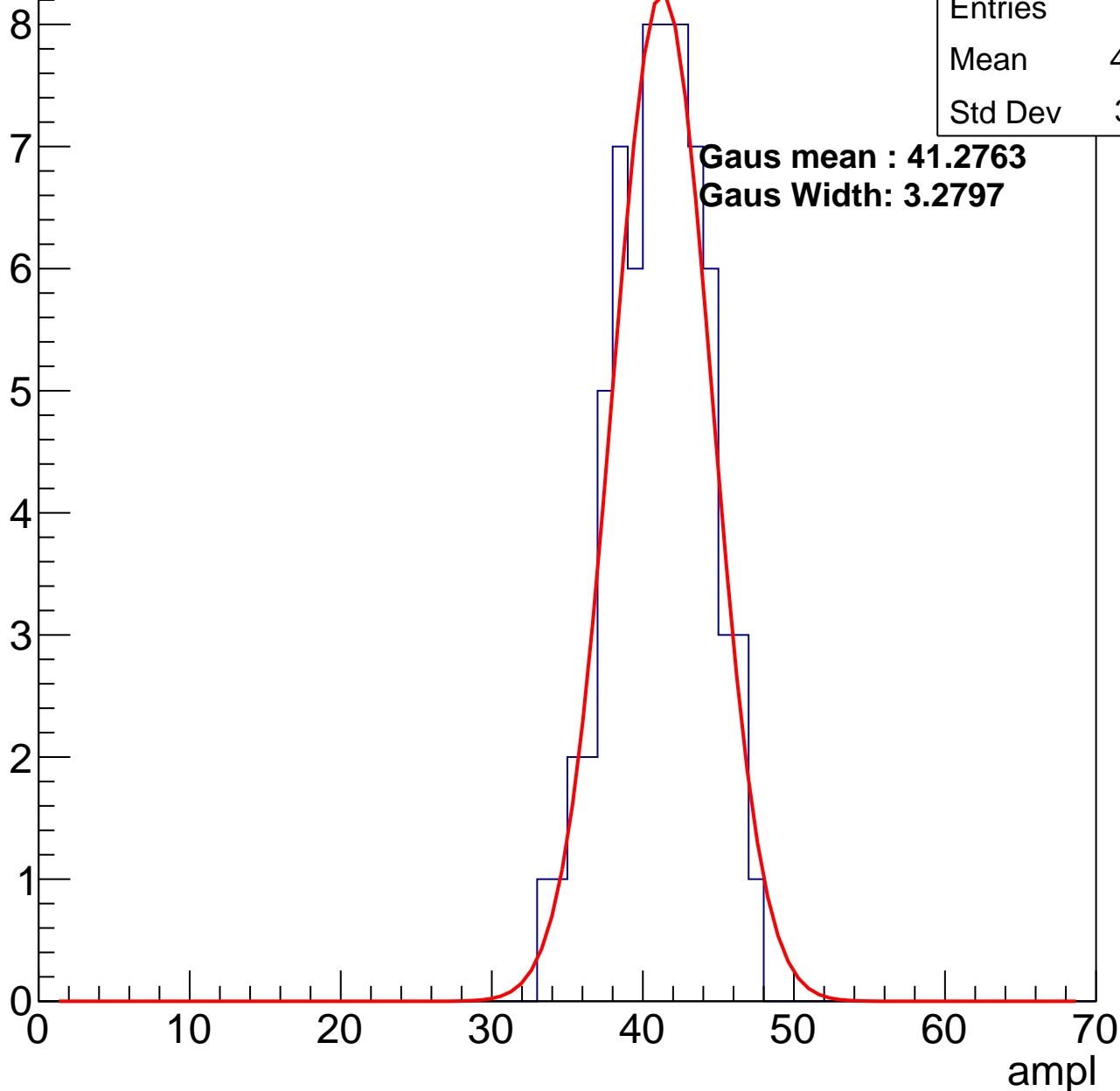
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	40.63
Std Dev	3.091

**Gaus mean : 41.2763**

**Gaus Width: 3.2797**

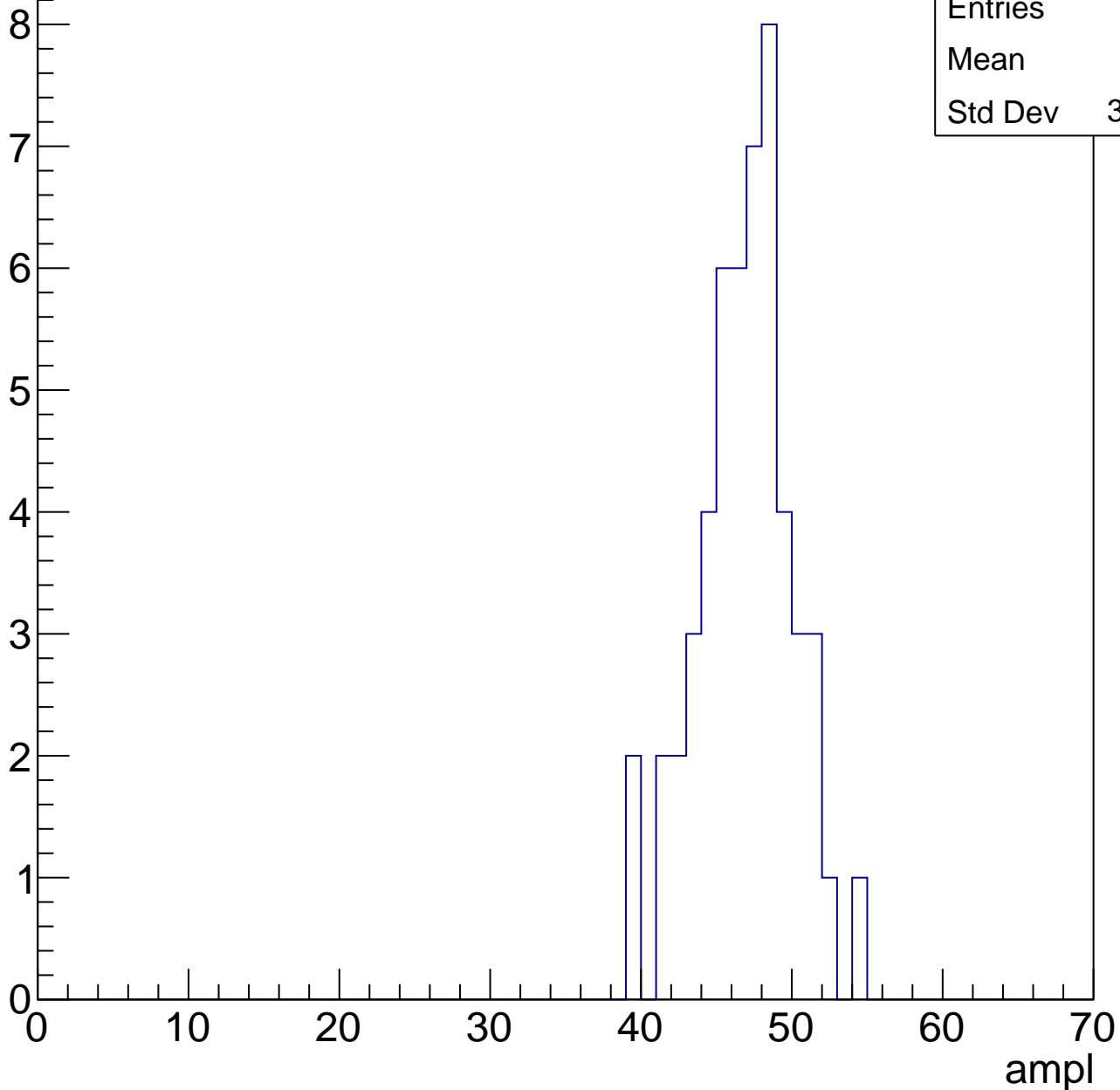


# B1L000S, U21-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	46.4
Std Dev	3.158

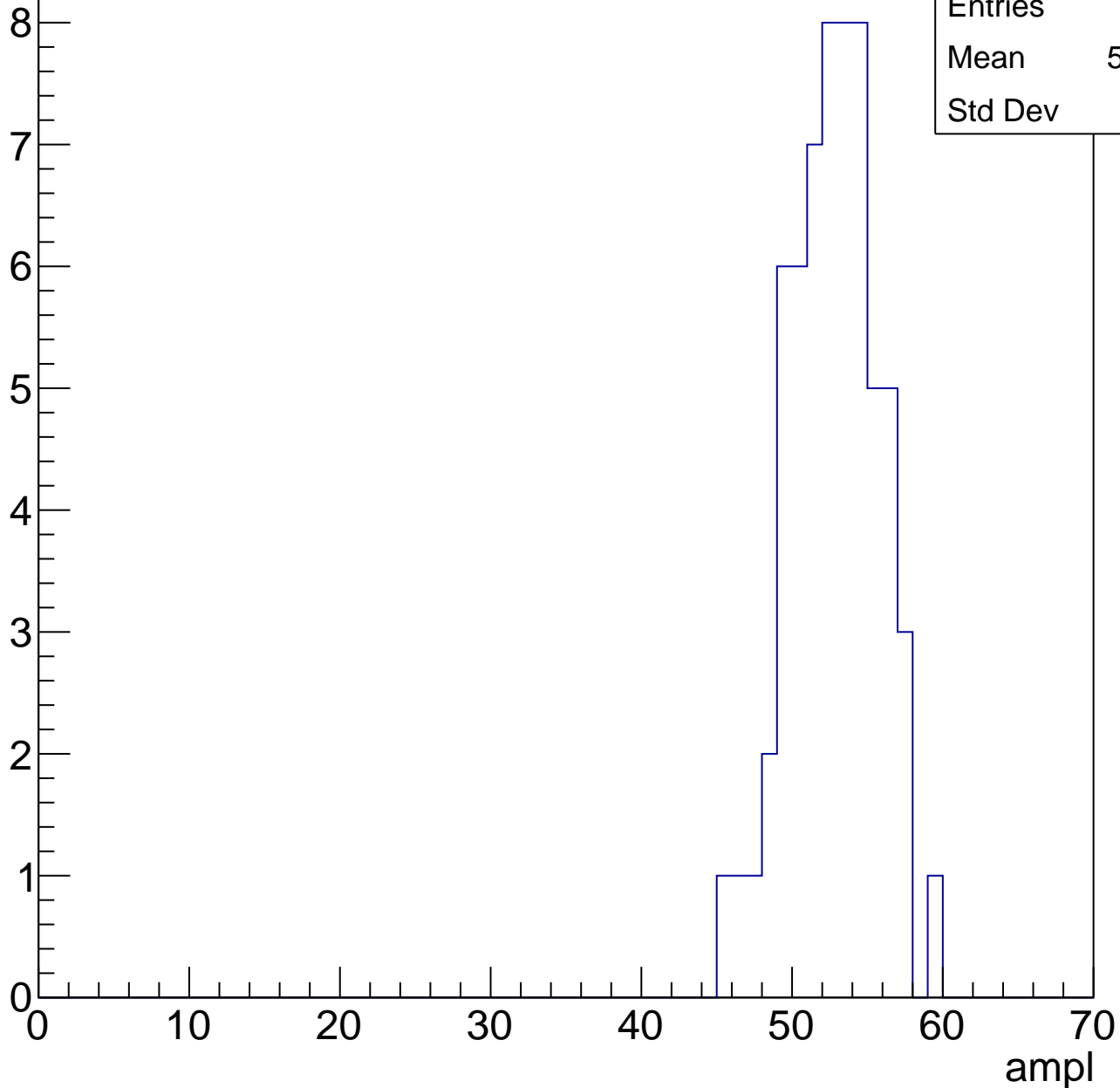


# B1L000S, U21-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	52.29
Std Dev	2.87

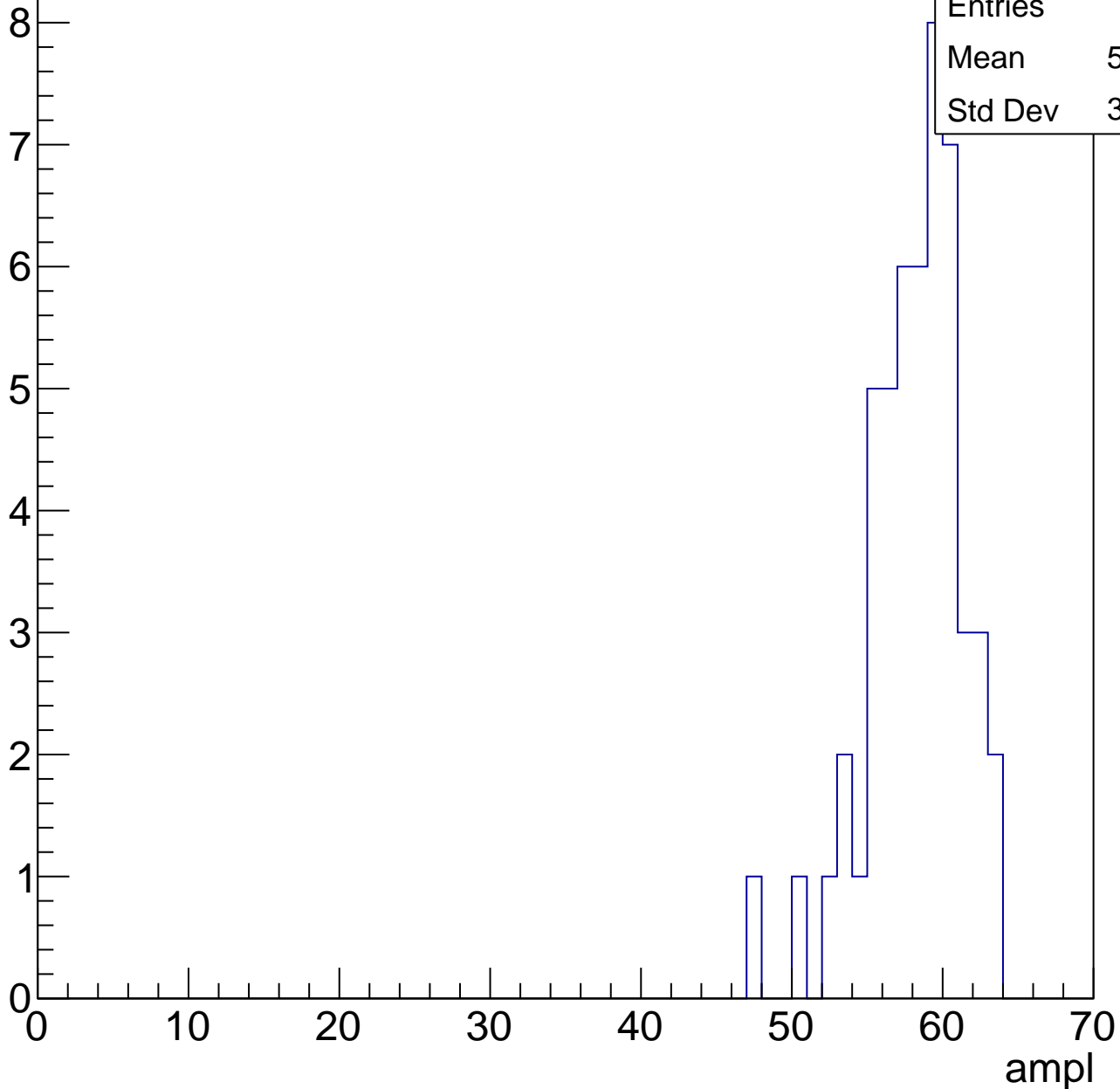


# B1L000S, U21-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	57.67
Std Dev	3.173

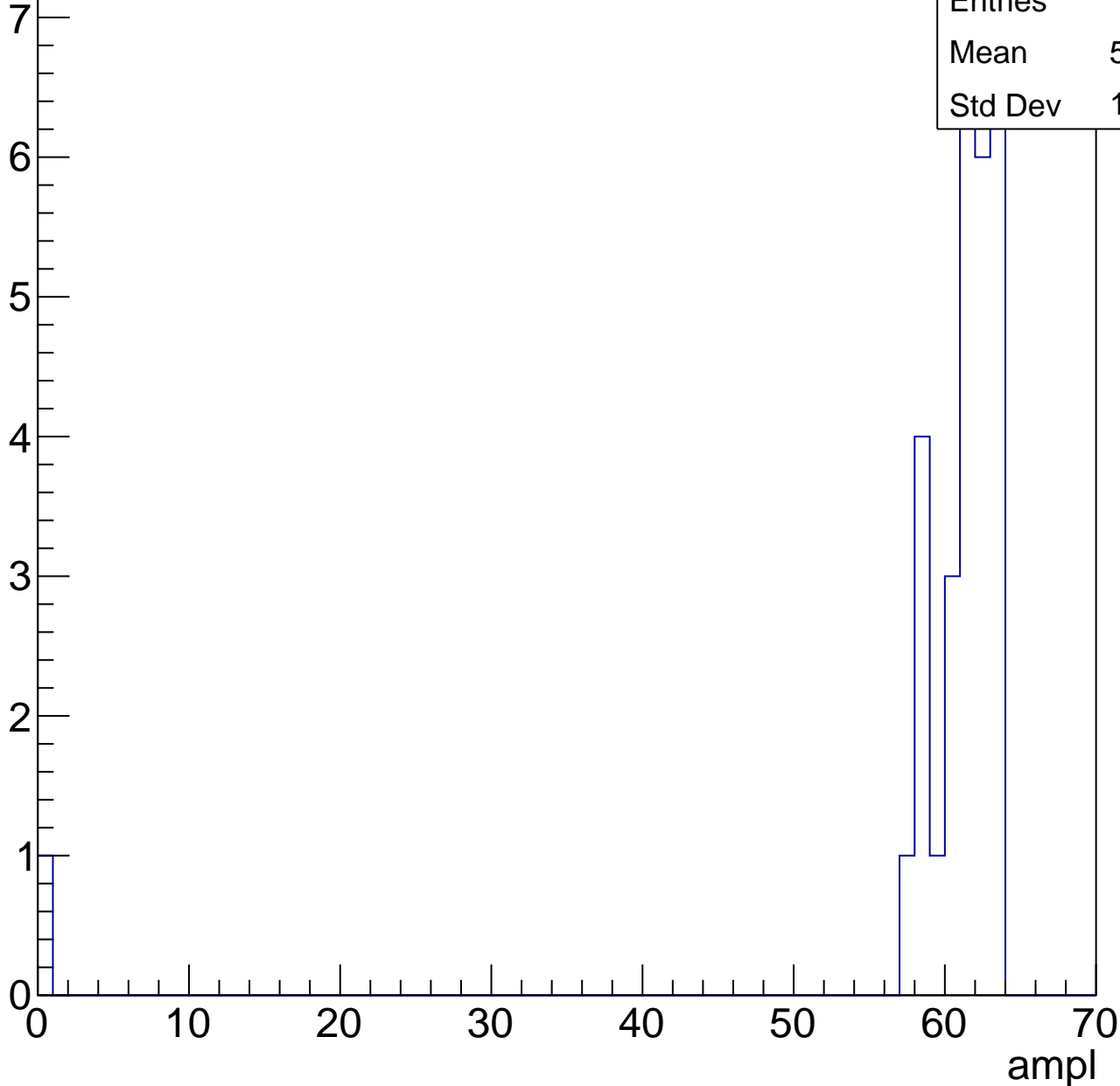


# B1L000S, U21-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	58.93
Std Dev	11.08

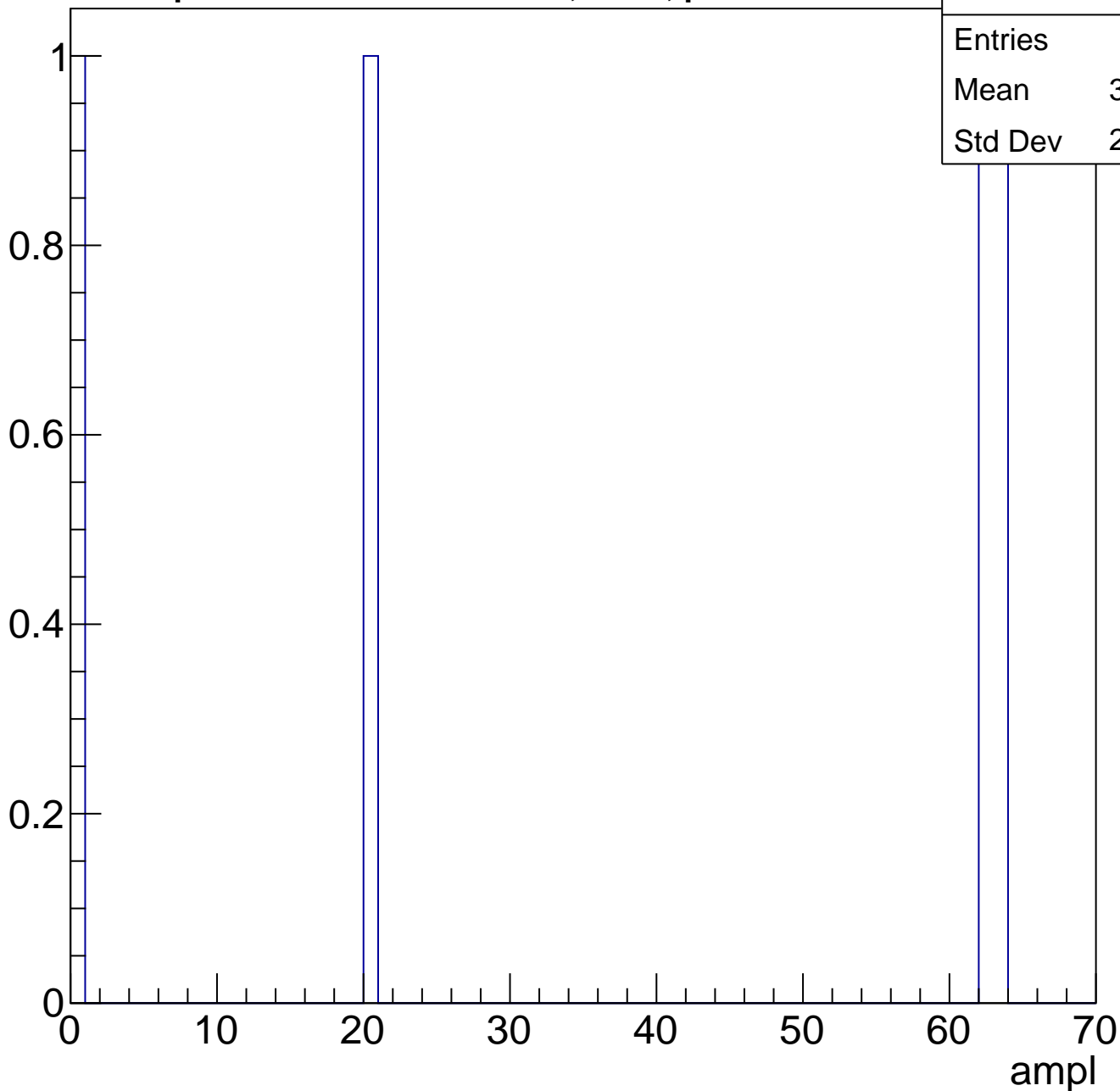




# B1L000S, U21-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch89, adc0

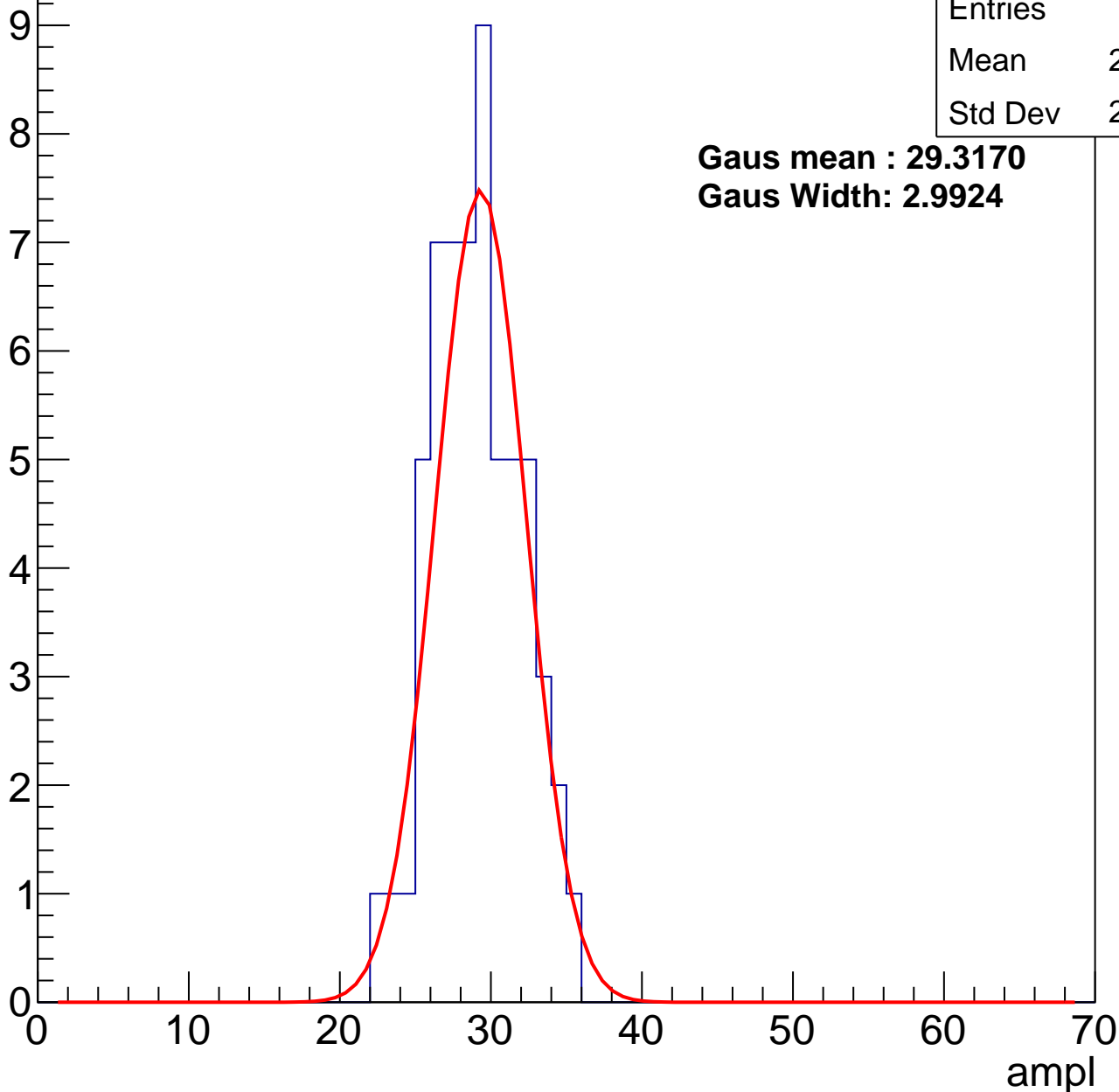
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	28.63
Std Dev	2.864

**Gaus mean : 29.3170**

**Gaus Width: 2.9924**



# B1L000S, U21-ch89, adc1

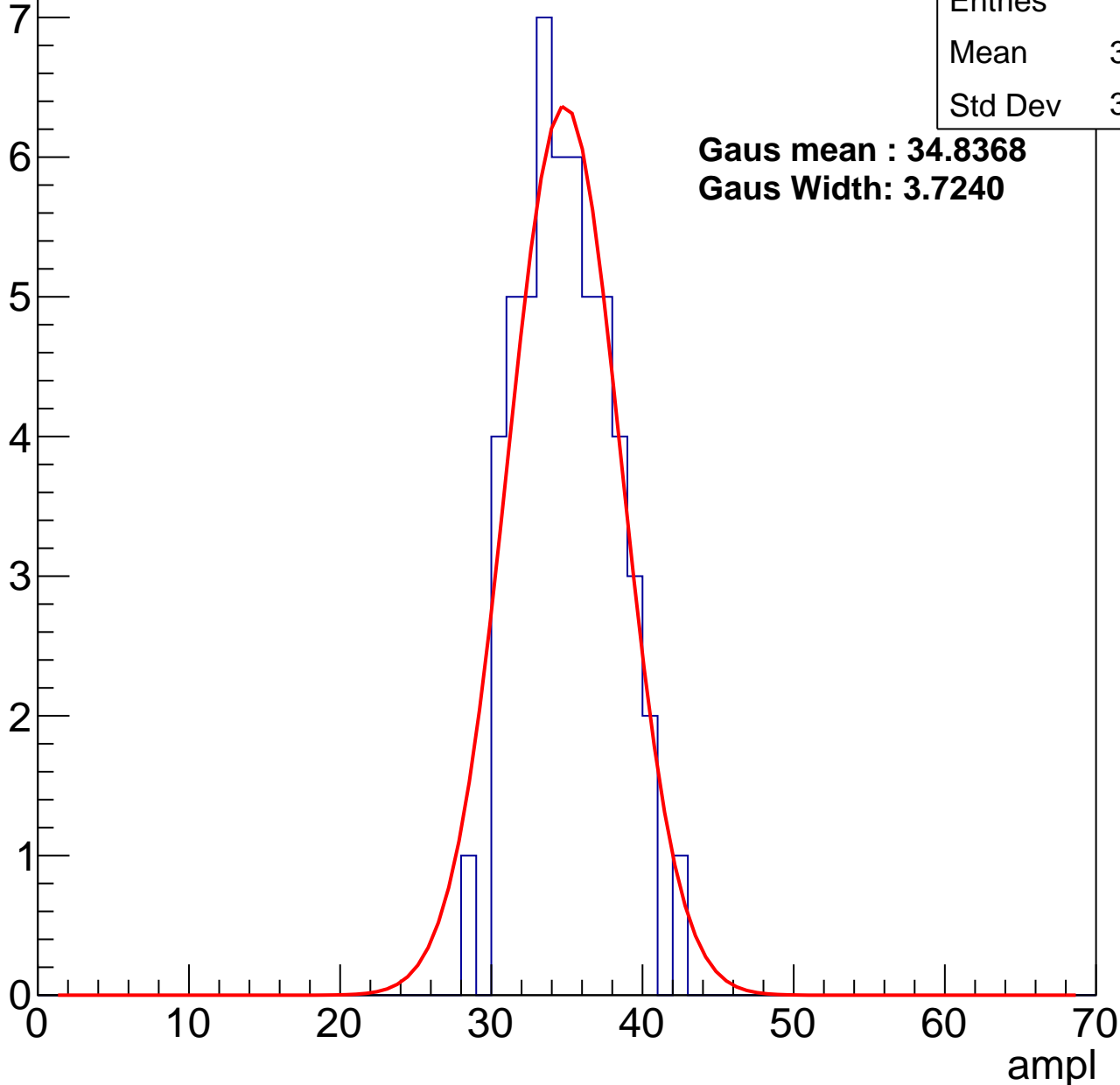
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	34.52
Std Dev	3.053

**Gaus mean : 34.8368**

**Gaus Width: 3.7240**



# B1L000S, U21-ch89, adc2

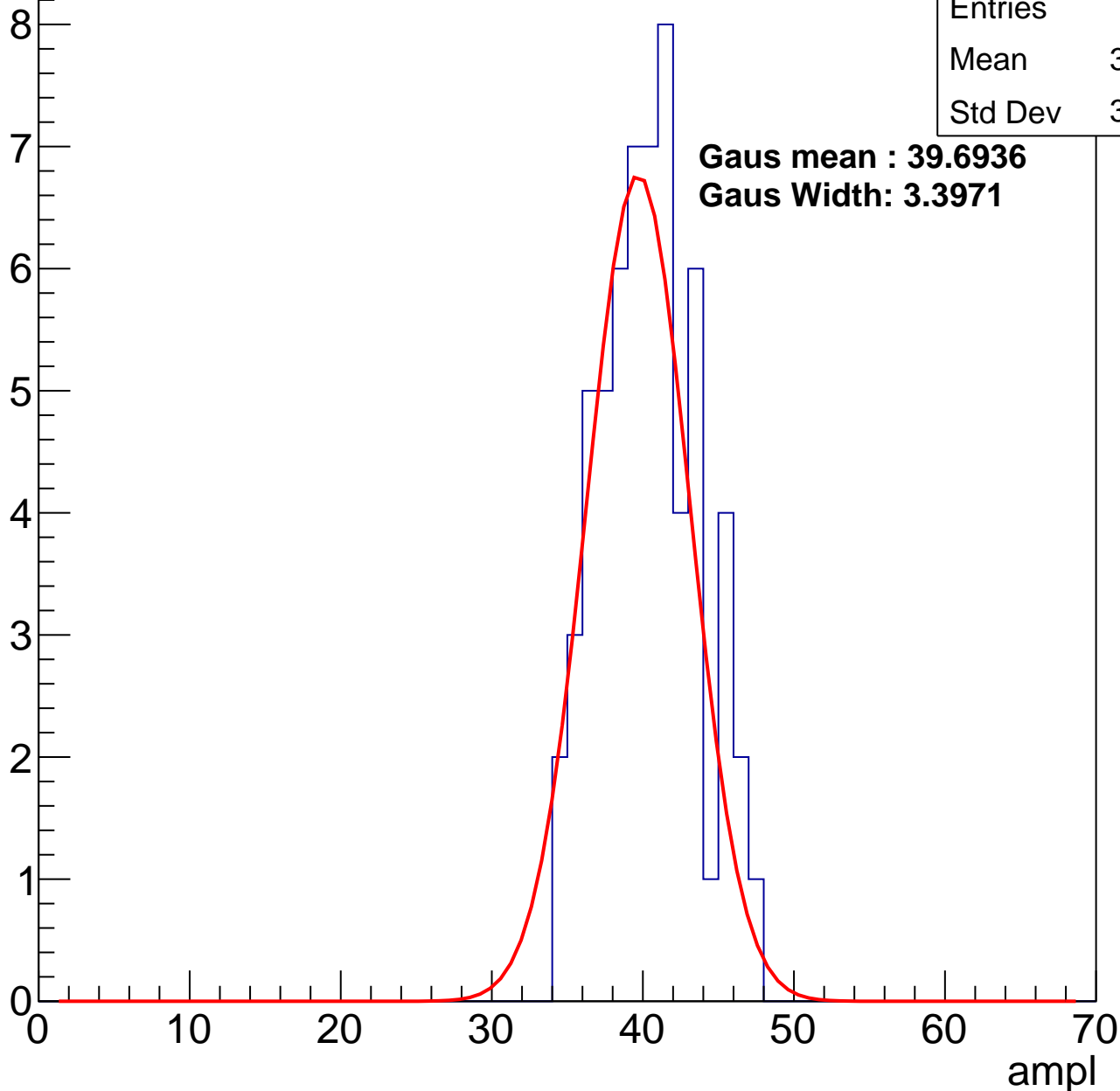
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	39.93
Std Dev	3.182

**Gaus mean : 39.6936**

**Gaus Width: 3.3971**

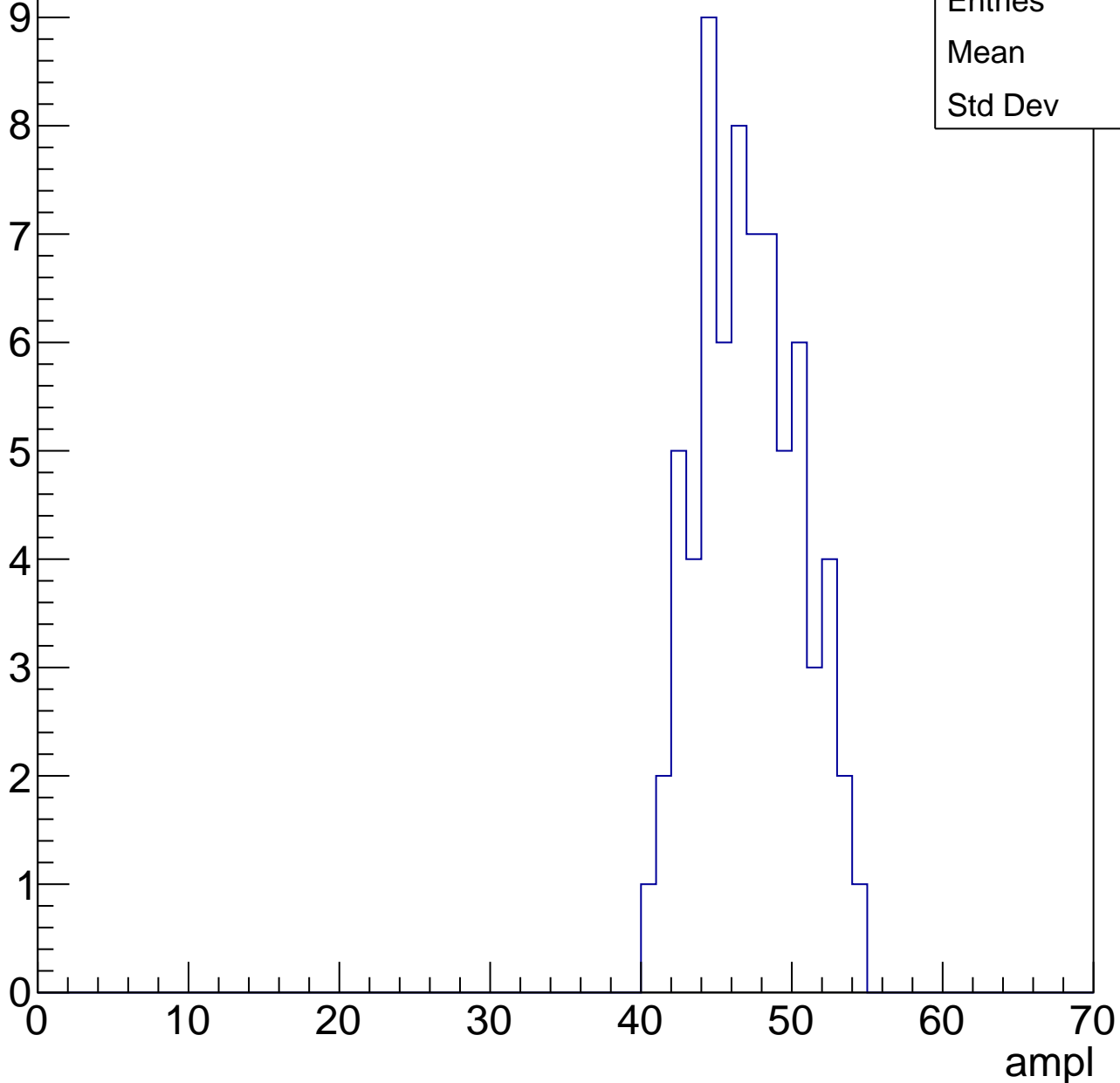


# B1L000S, U21-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

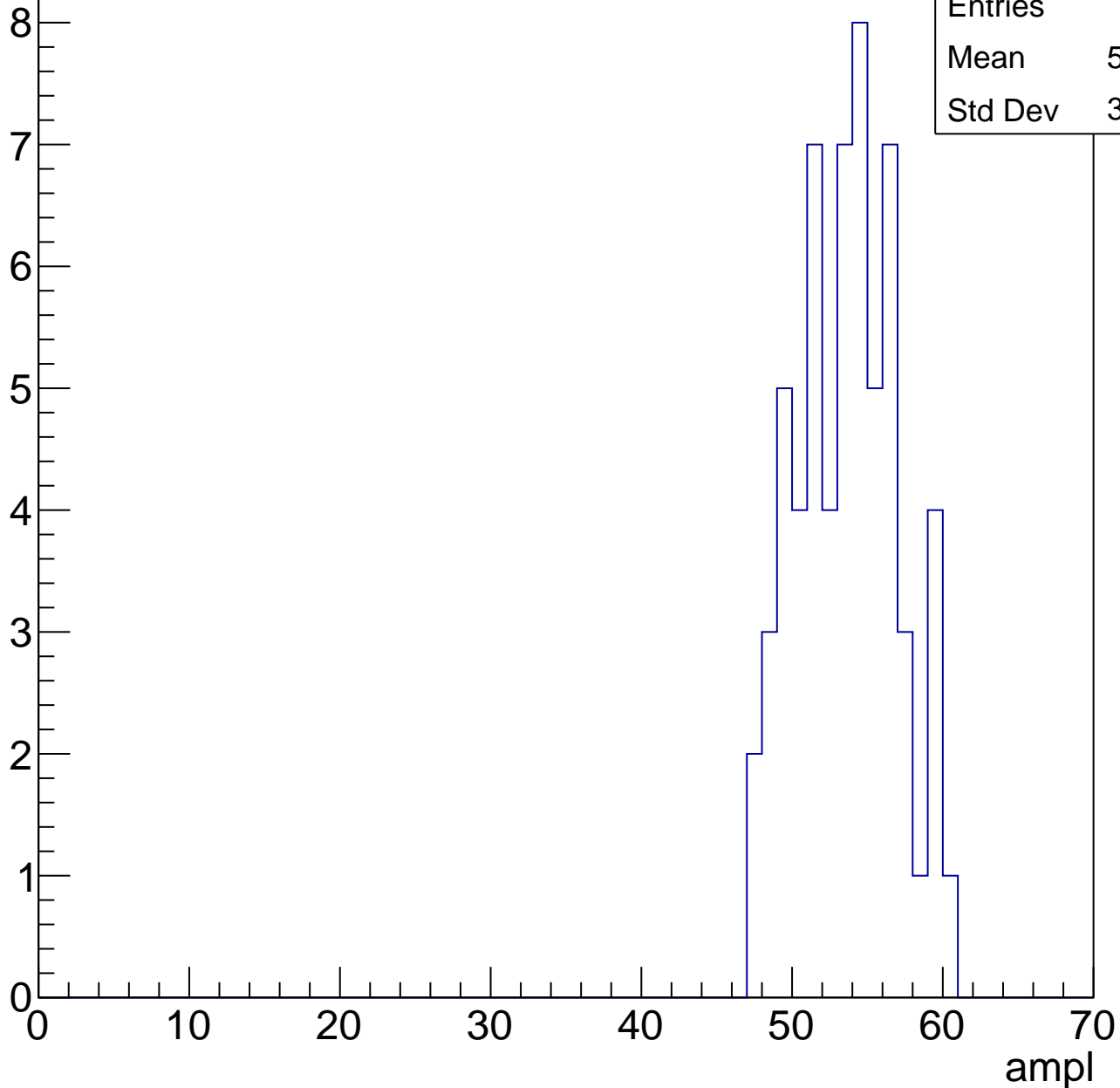
Entries	70
Mean	46.7
Std Dev	3.31



# B1L000S, U21-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

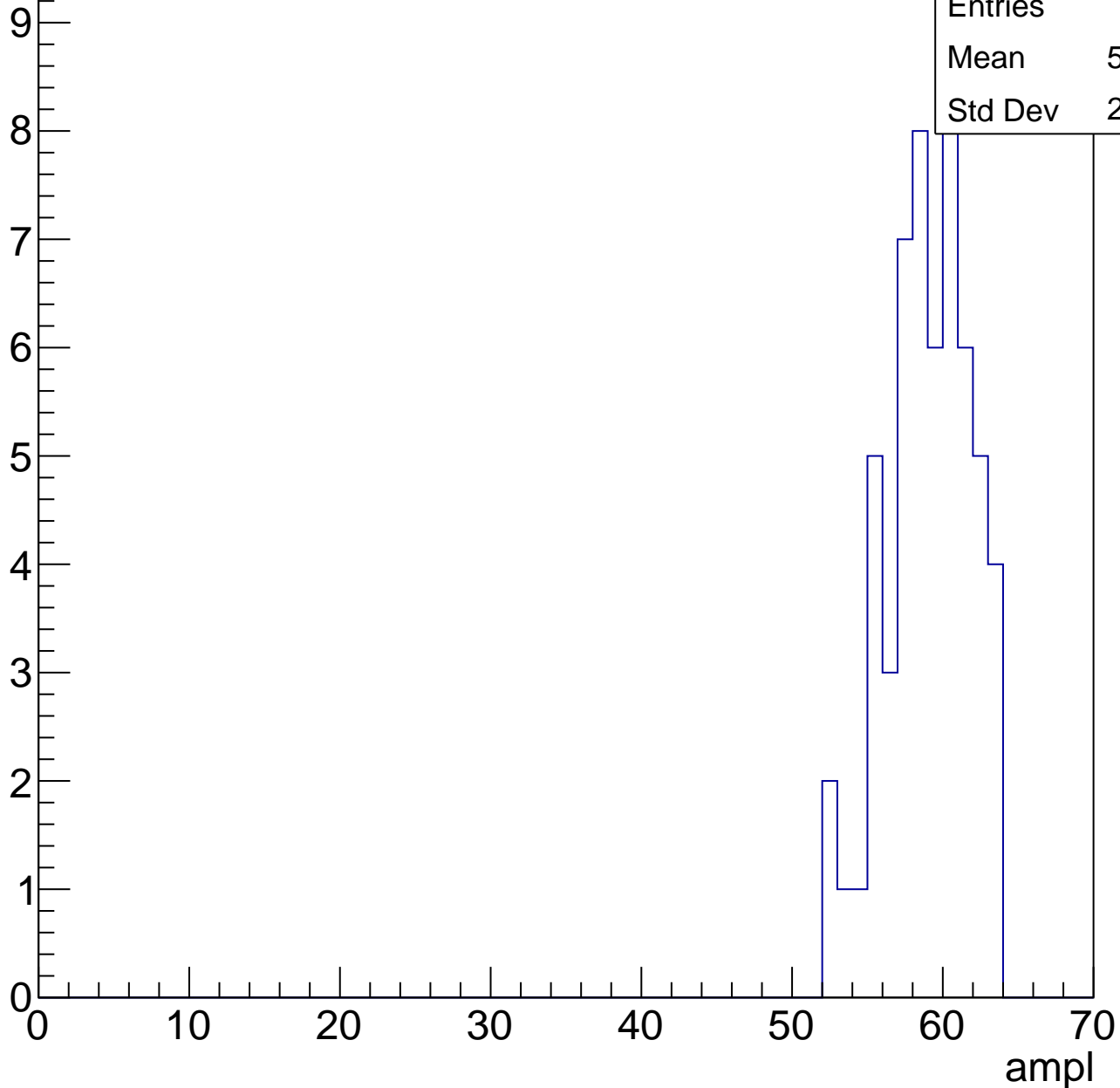


Entries	61
Mean	53.16
Std Dev	3.265

# B1L000S, U21-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

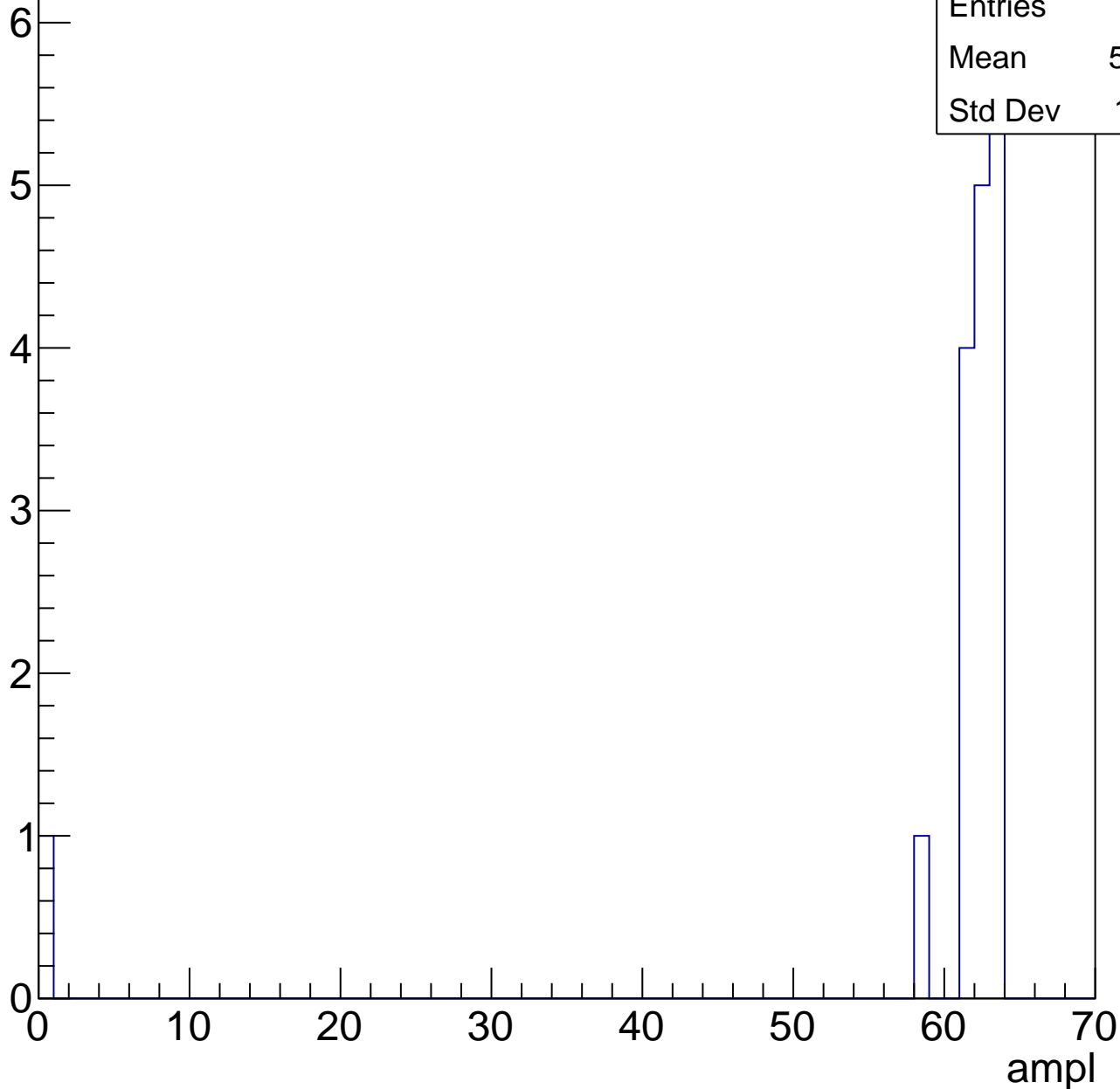


# B1L000S, U21-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.24
Std Dev	14.61





# B1L000S, U21-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch90, adc0

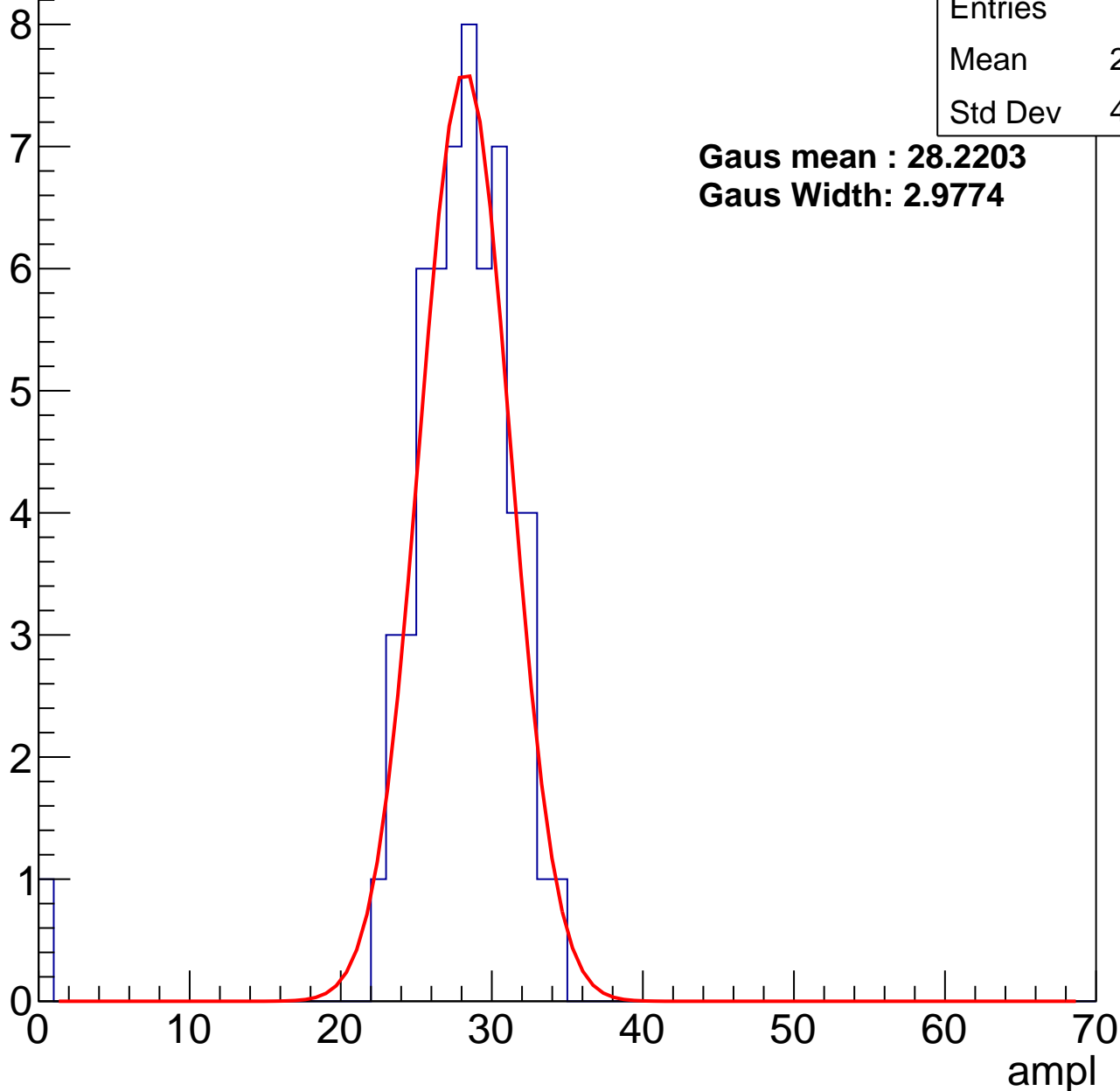
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	27.33
Std Dev	4.542

**Gaus mean : 28.2203**

**Gaus Width: 2.9774**



# B1L000S, U21-ch90, adc1

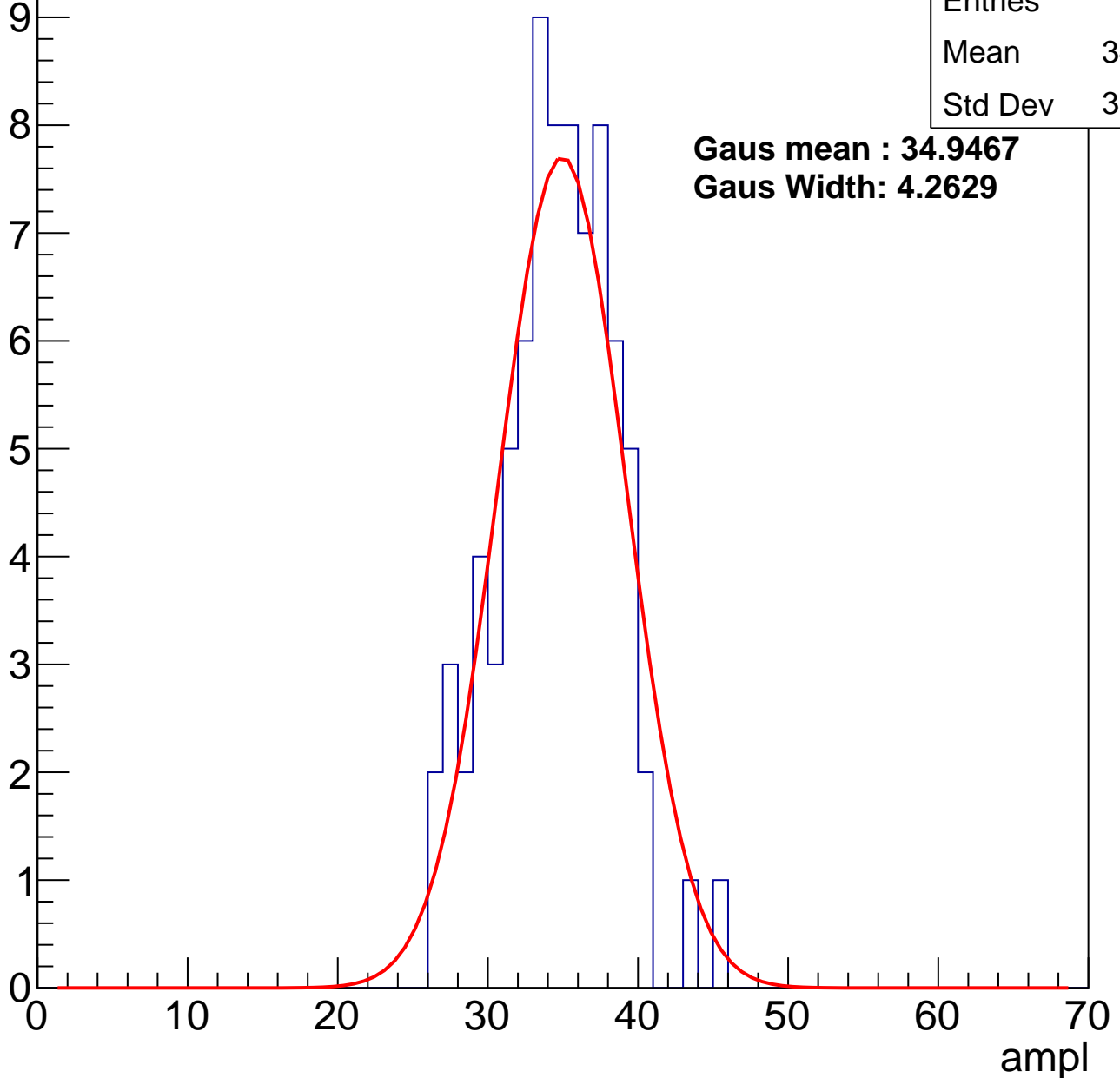
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	34.12
Std Dev	3.832

**Gaus mean : 34.9467**

**Gaus Width: 4.2629**



# B1L000S, U21-ch90, adc2

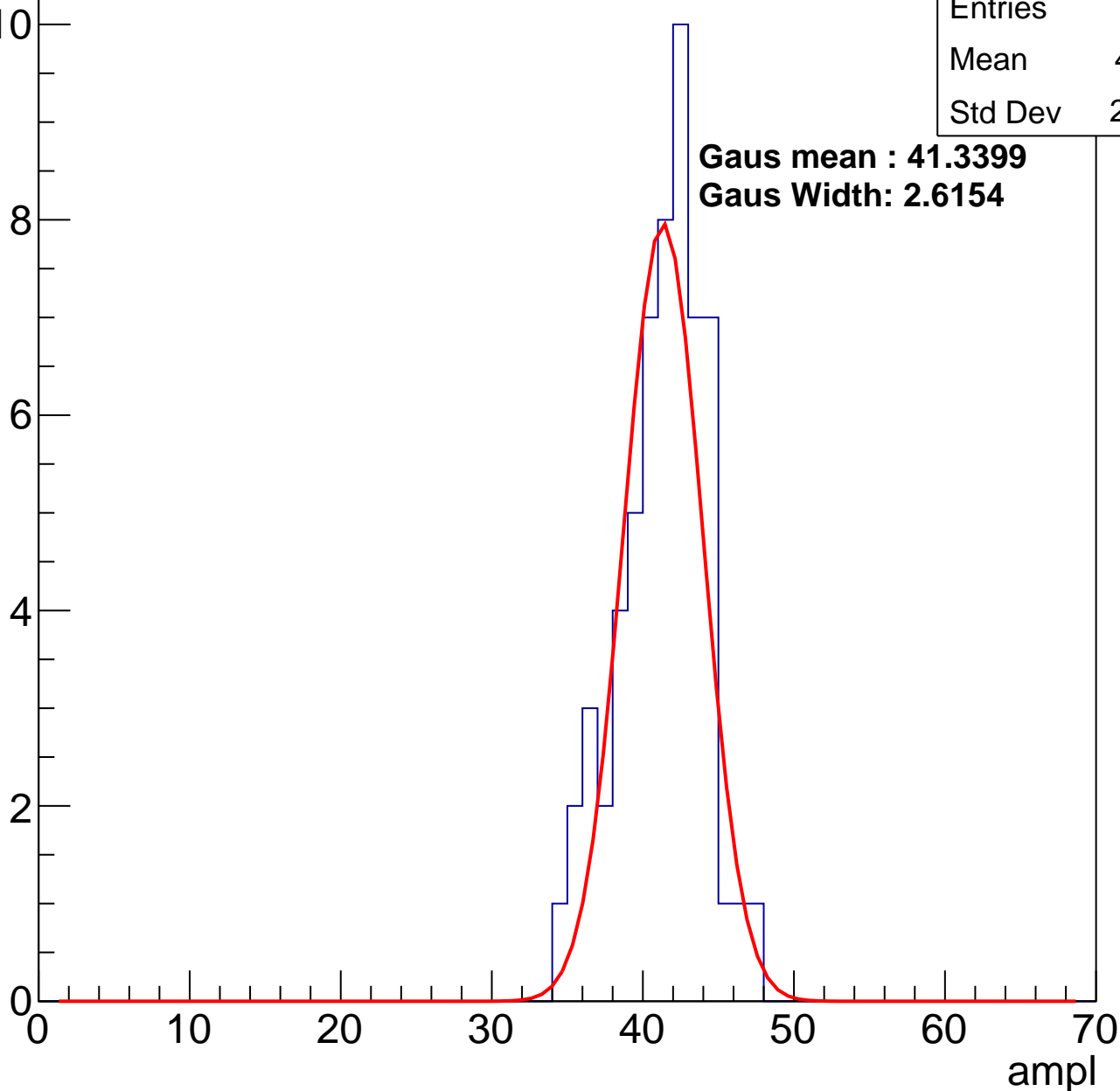
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	40.81
Std Dev	2.813

**Gaus mean : 41.3399**

**Gaus Width: 2.6154**

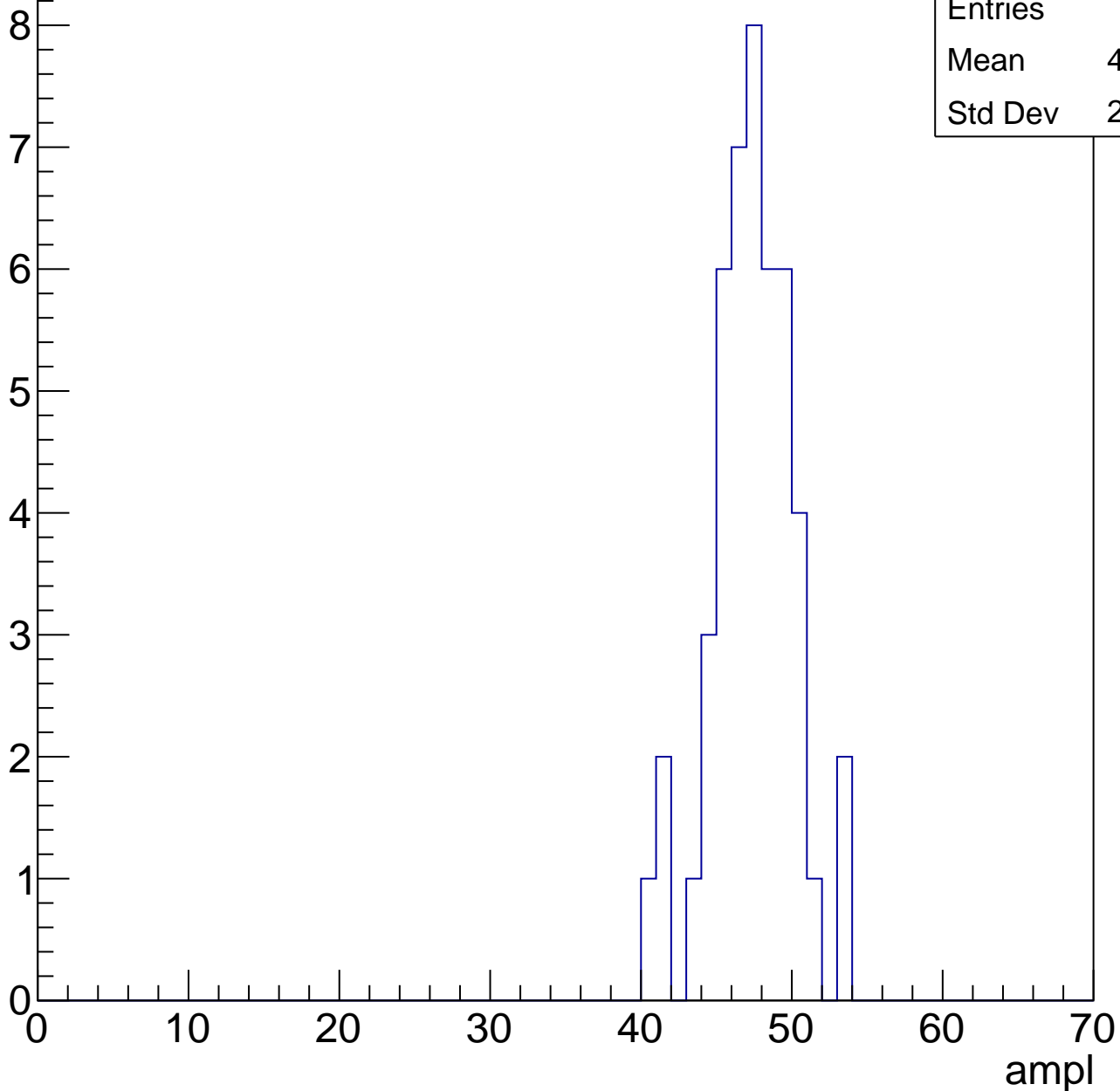


# B1L000S, U21-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

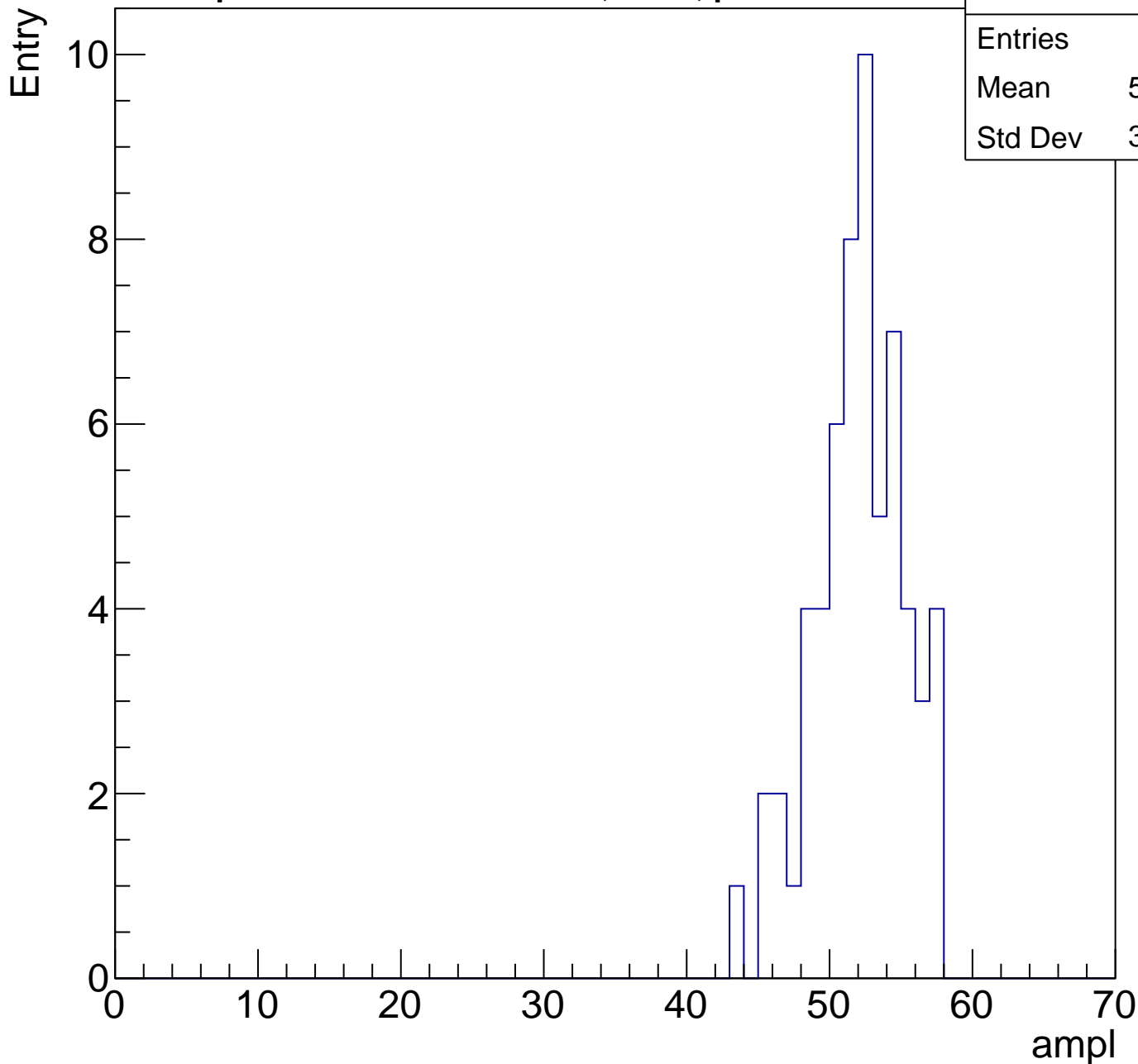
Entries	47
Mean	46.89
Std Dev	2.723



# B1L000S, U21-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	51.59
Std Dev	3.175

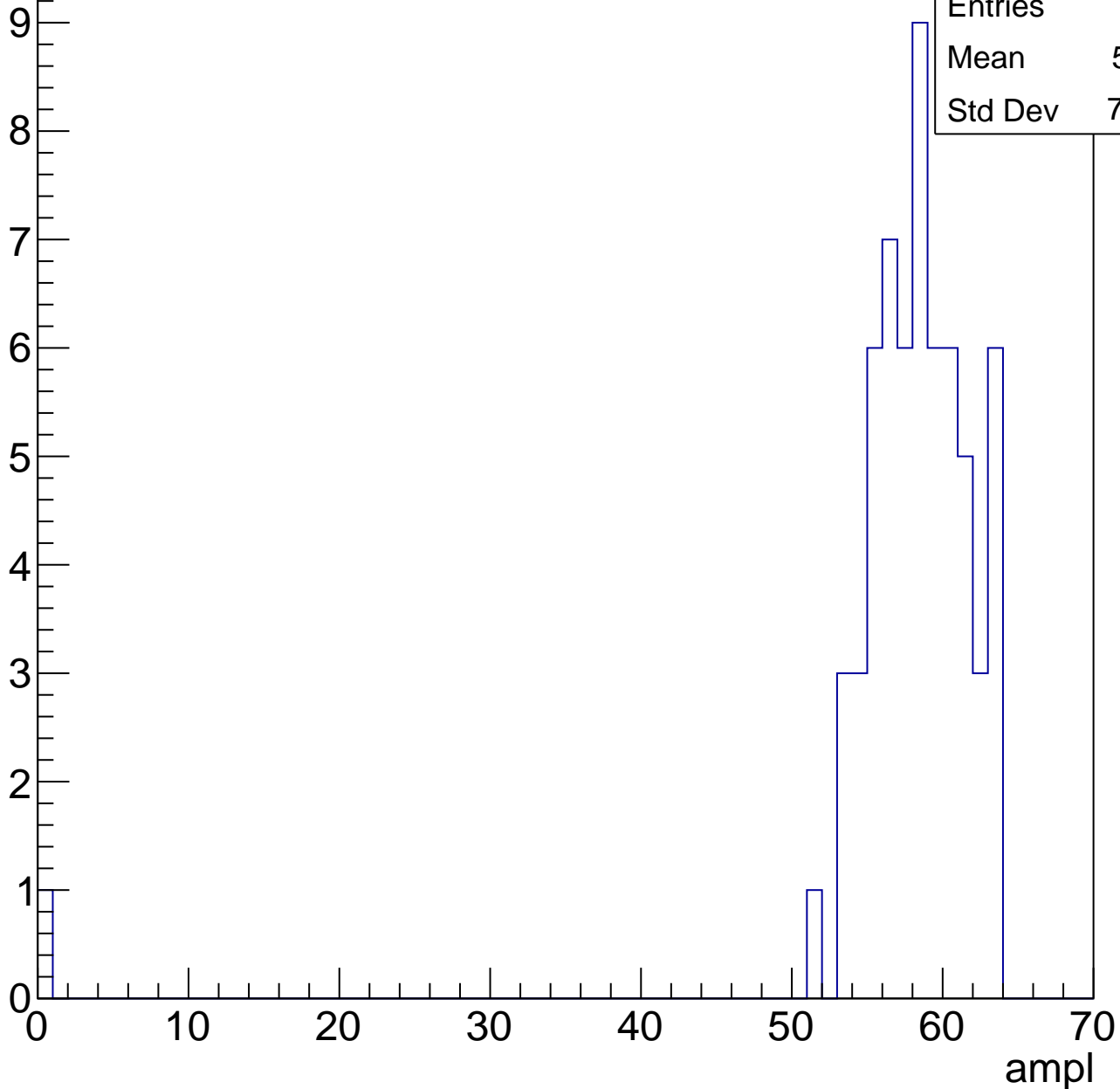


# B1L000S, U21-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	57.11
Std Dev	7.878

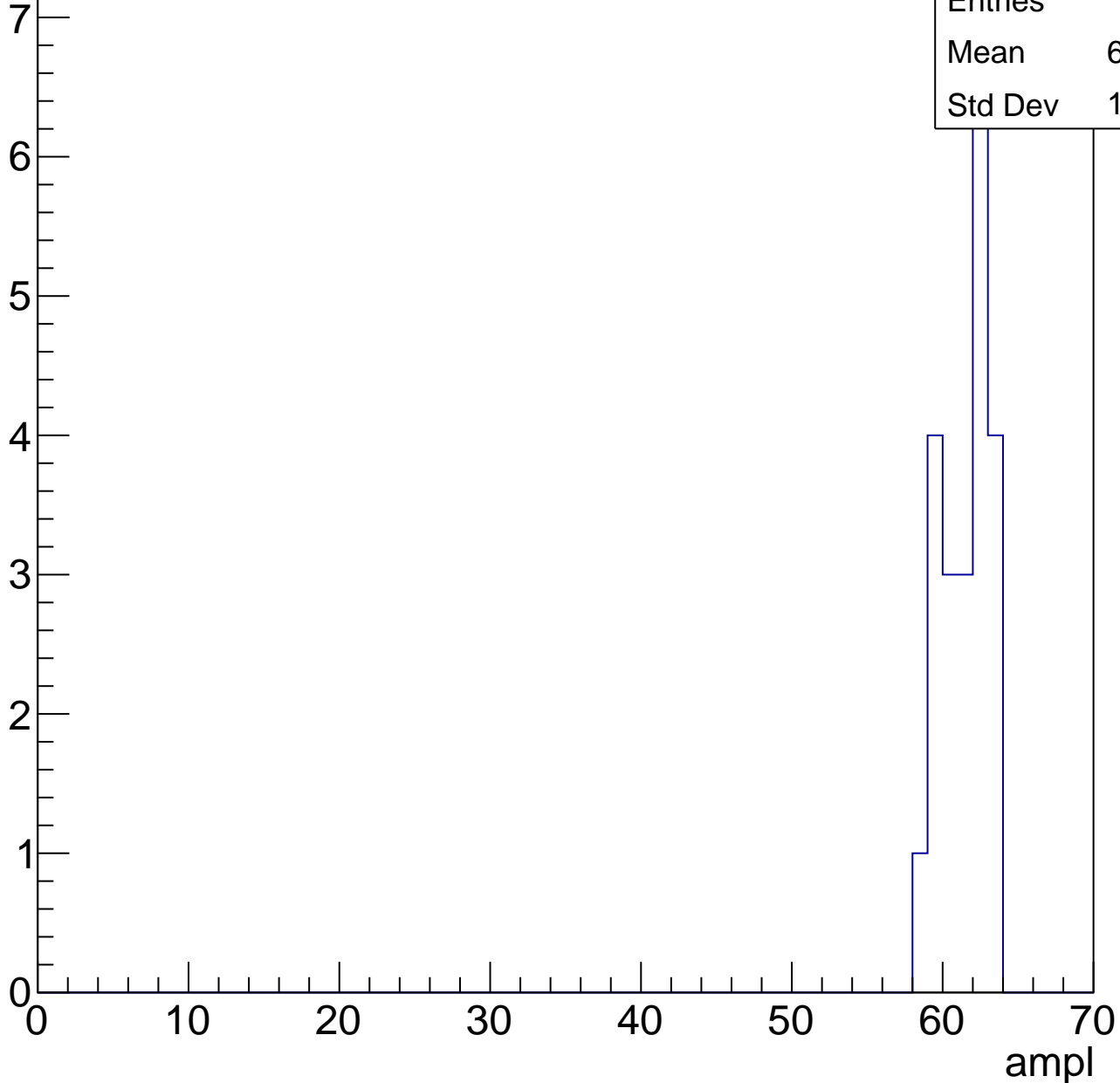


# B1L000S, U21-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	61.05
Std Dev	1.522

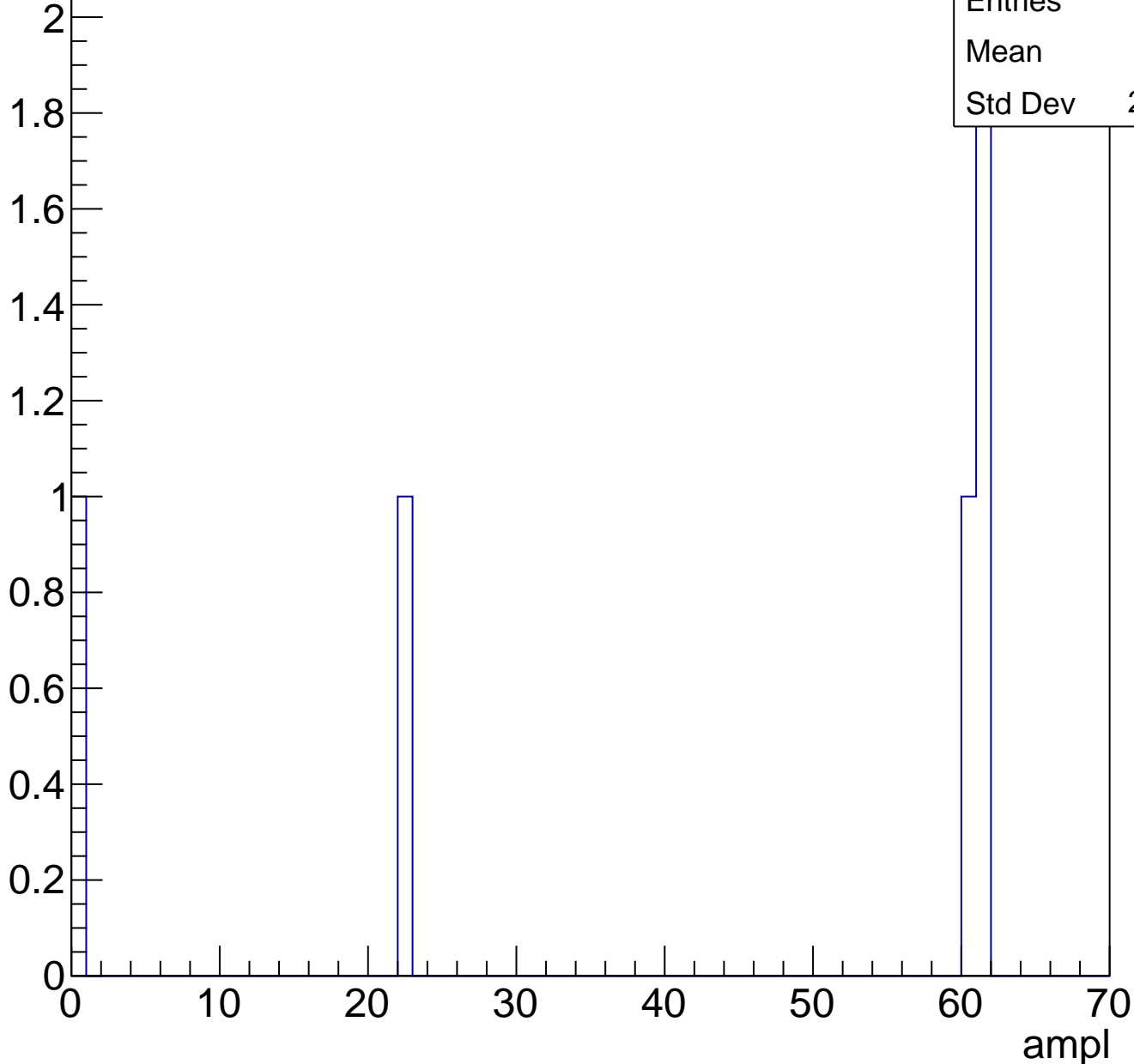




# B1L000S, U21-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	40.8
Std Dev	25.31

# B1L000S, U21-ch91, adc0

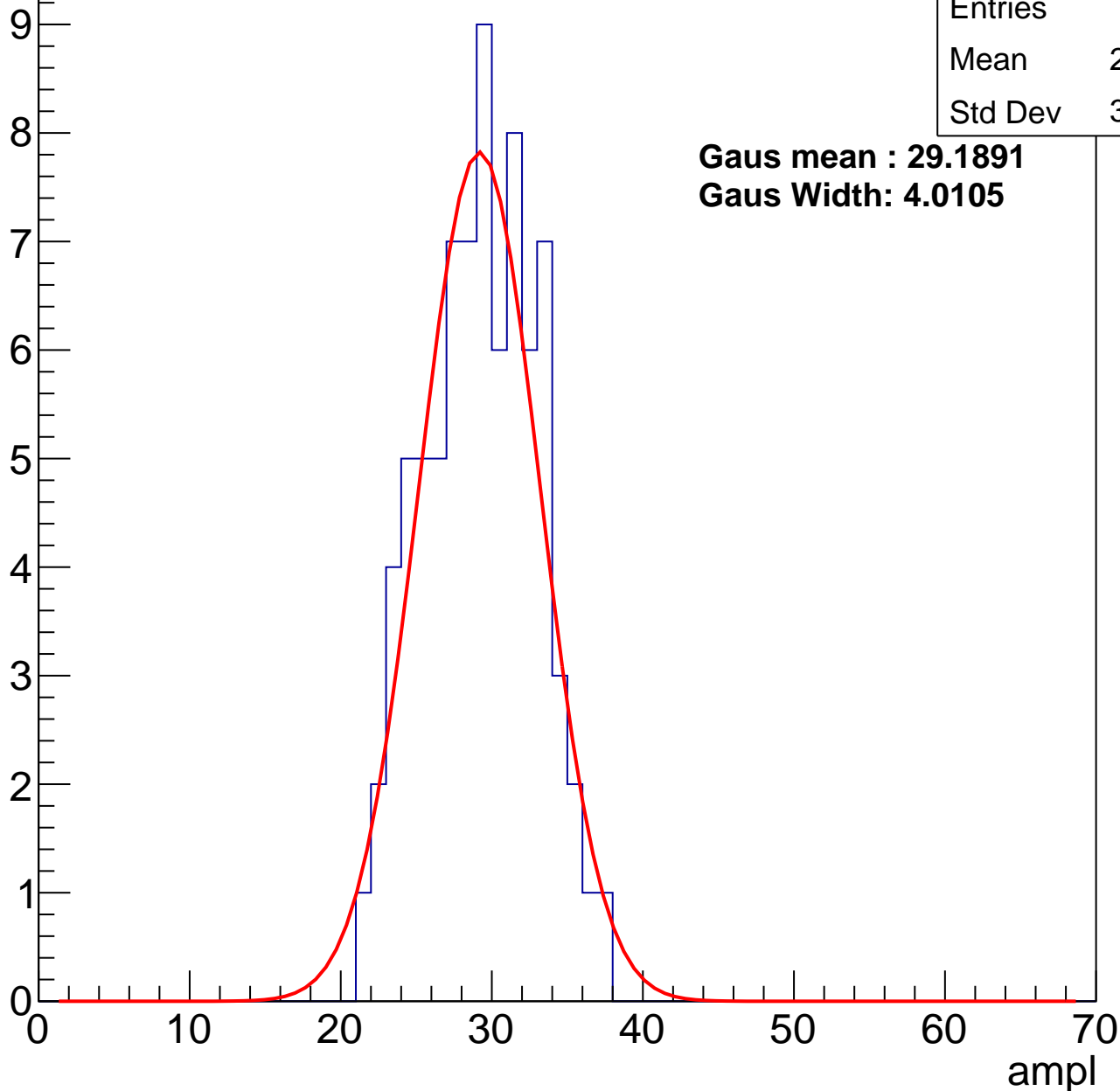
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	28.78
Std Dev	3.648

**Gaus mean : 29.1891**

**Gaus Width: 4.0105**



# B1L000S, U21-ch91, adc1

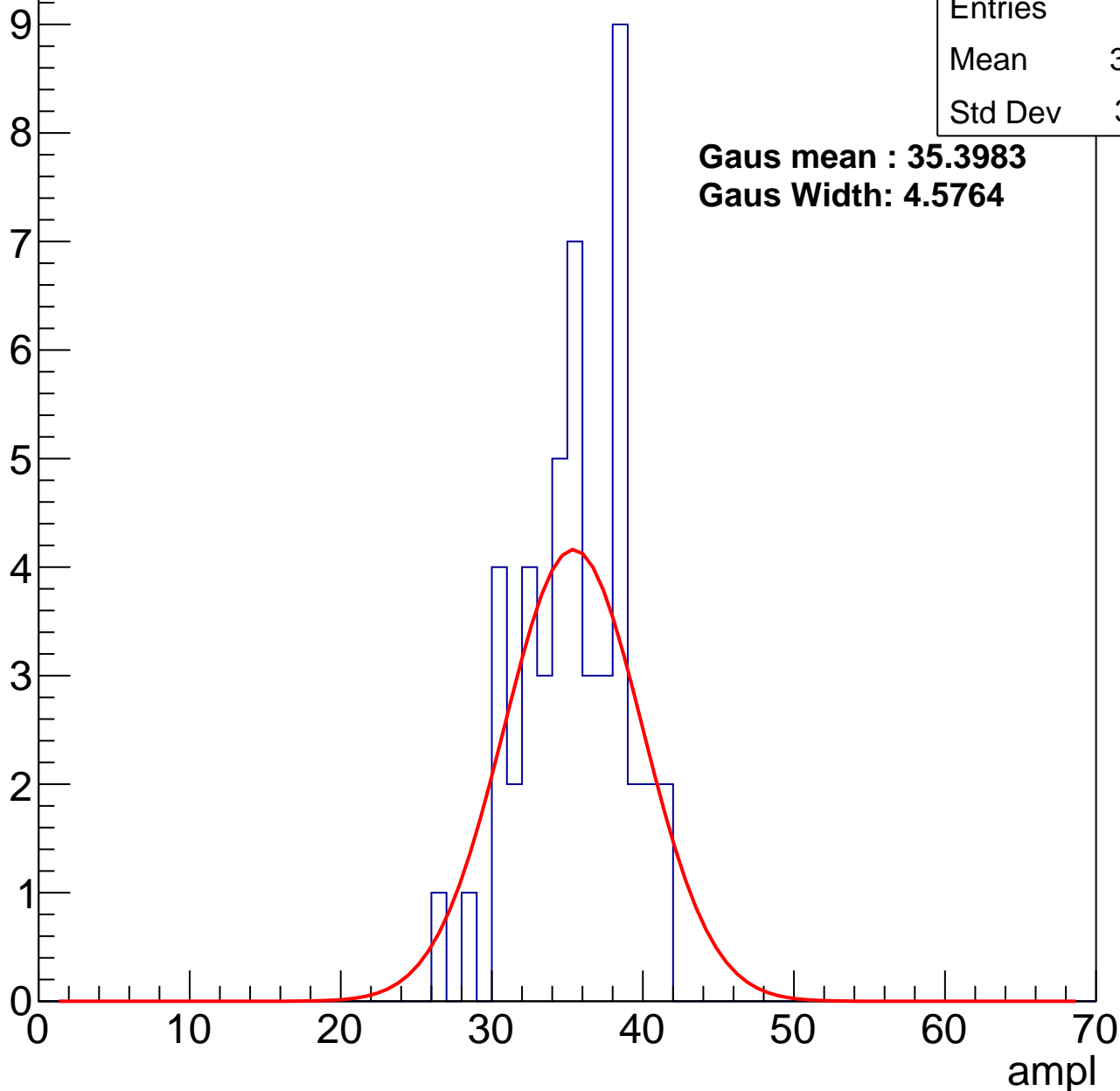
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	34.98
Std Dev	3.431

**Gaus mean : 35.3983**

**Gaus Width: 4.5764**



# B1L000S, U21-ch91, adc2

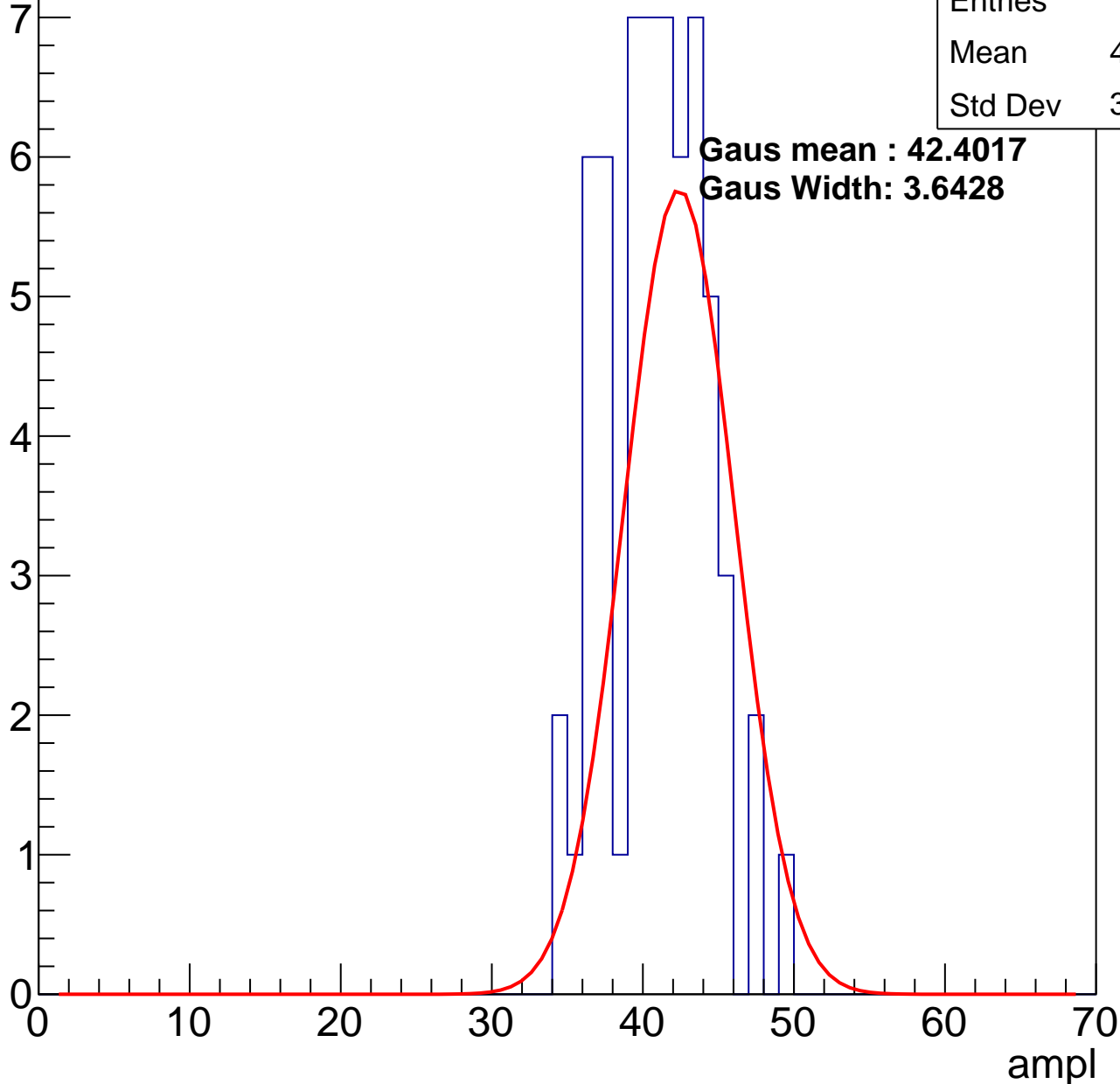
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	40.49
Std Dev	3.317

**Gaus mean : 42.4017**

**Gaus Width: 3.6428**

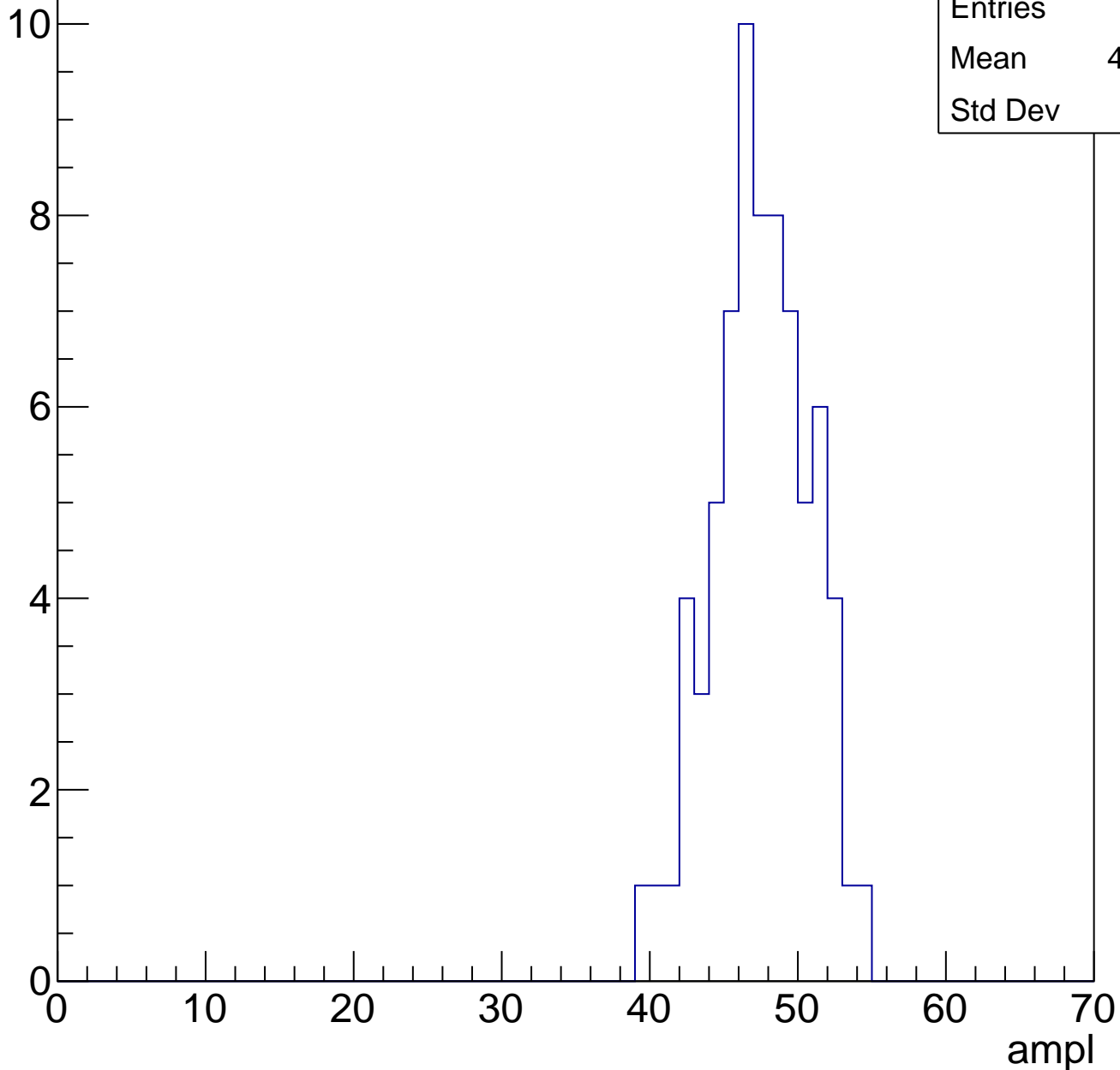


# B1L000S, U21-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	47.03
Std Dev	3.21

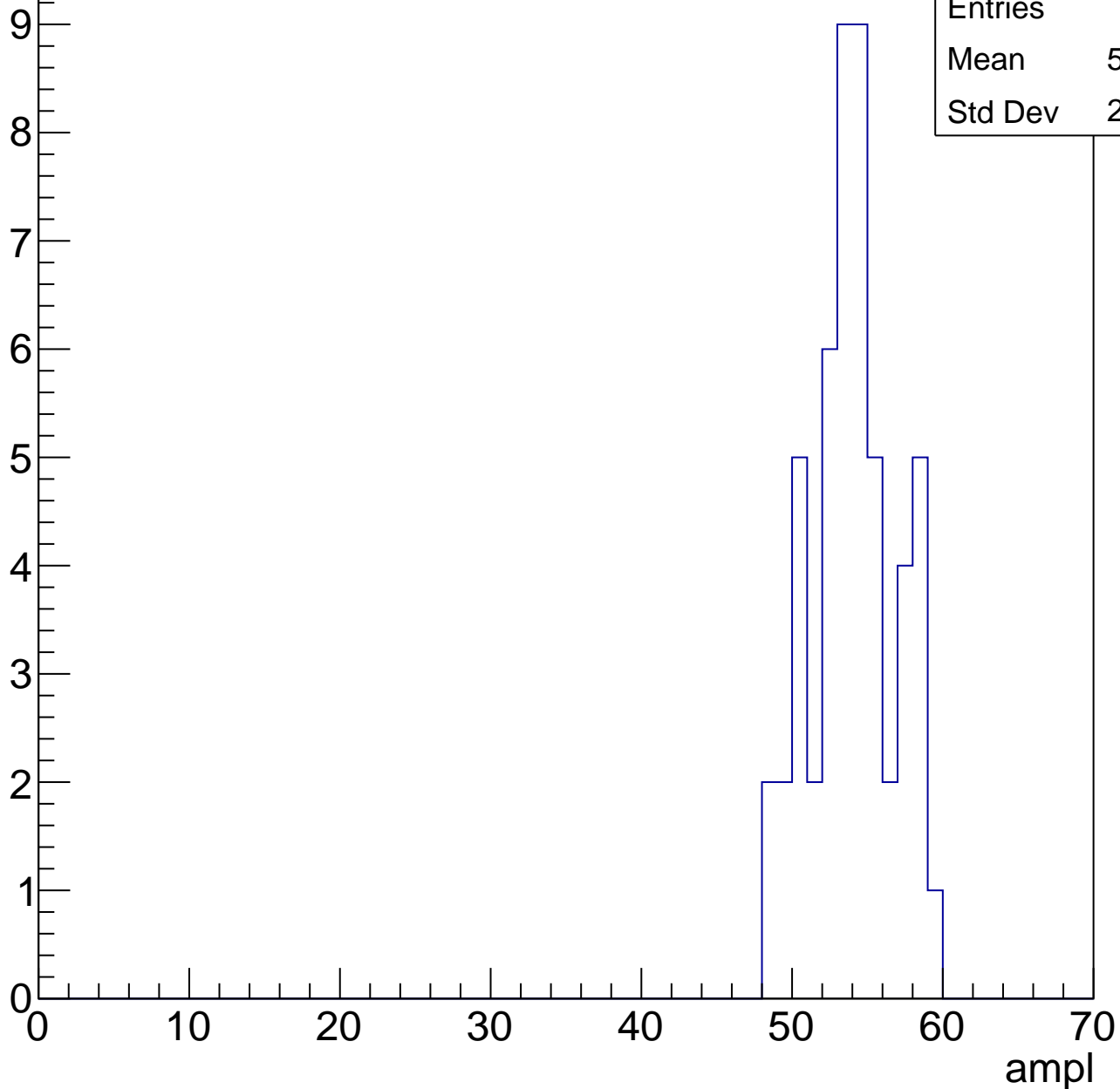
Entry



# B1L000S, U21-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

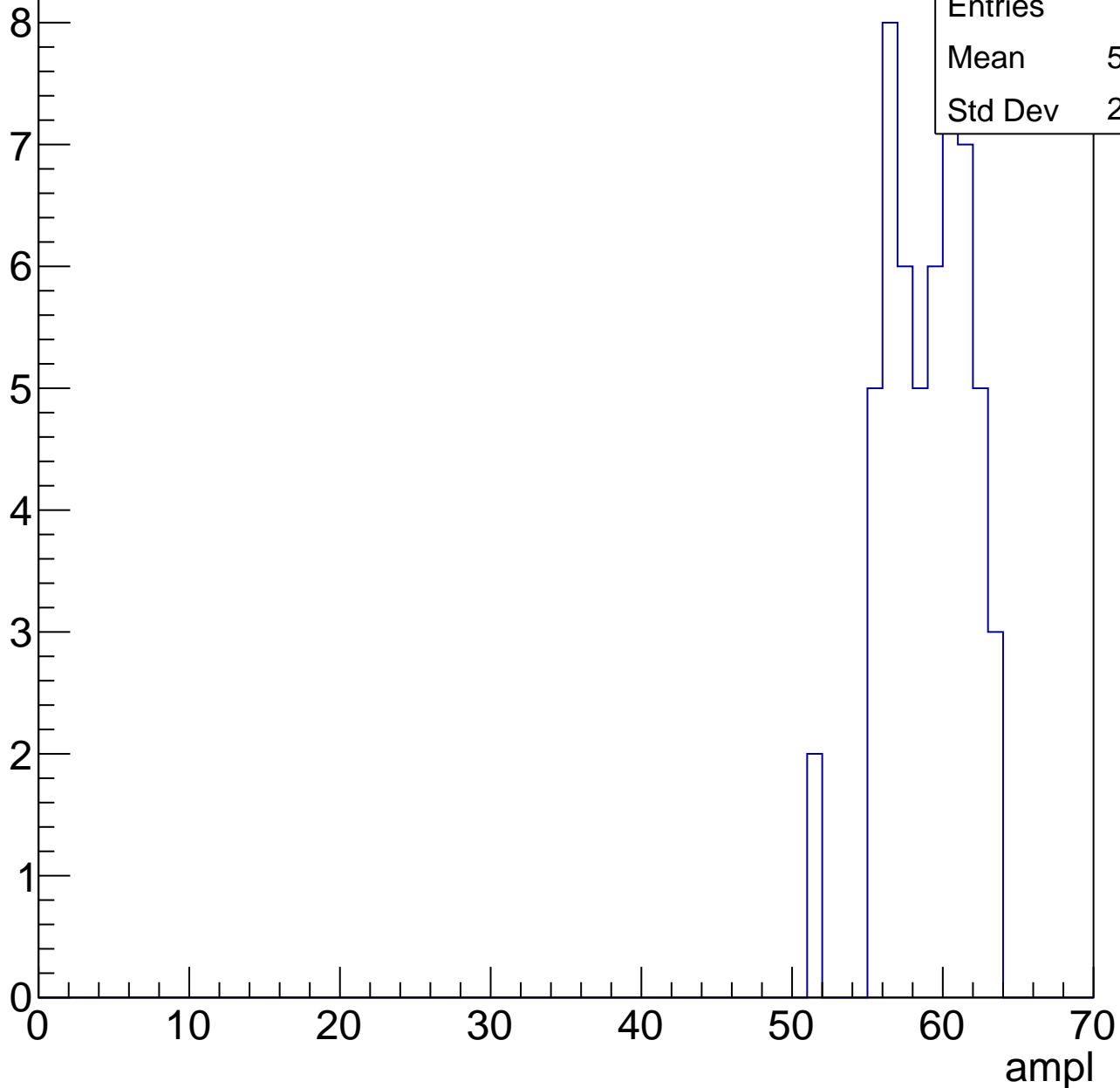


Entries	52
Mean	53.56
Std Dev	2.762

# B1L000S, U21-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

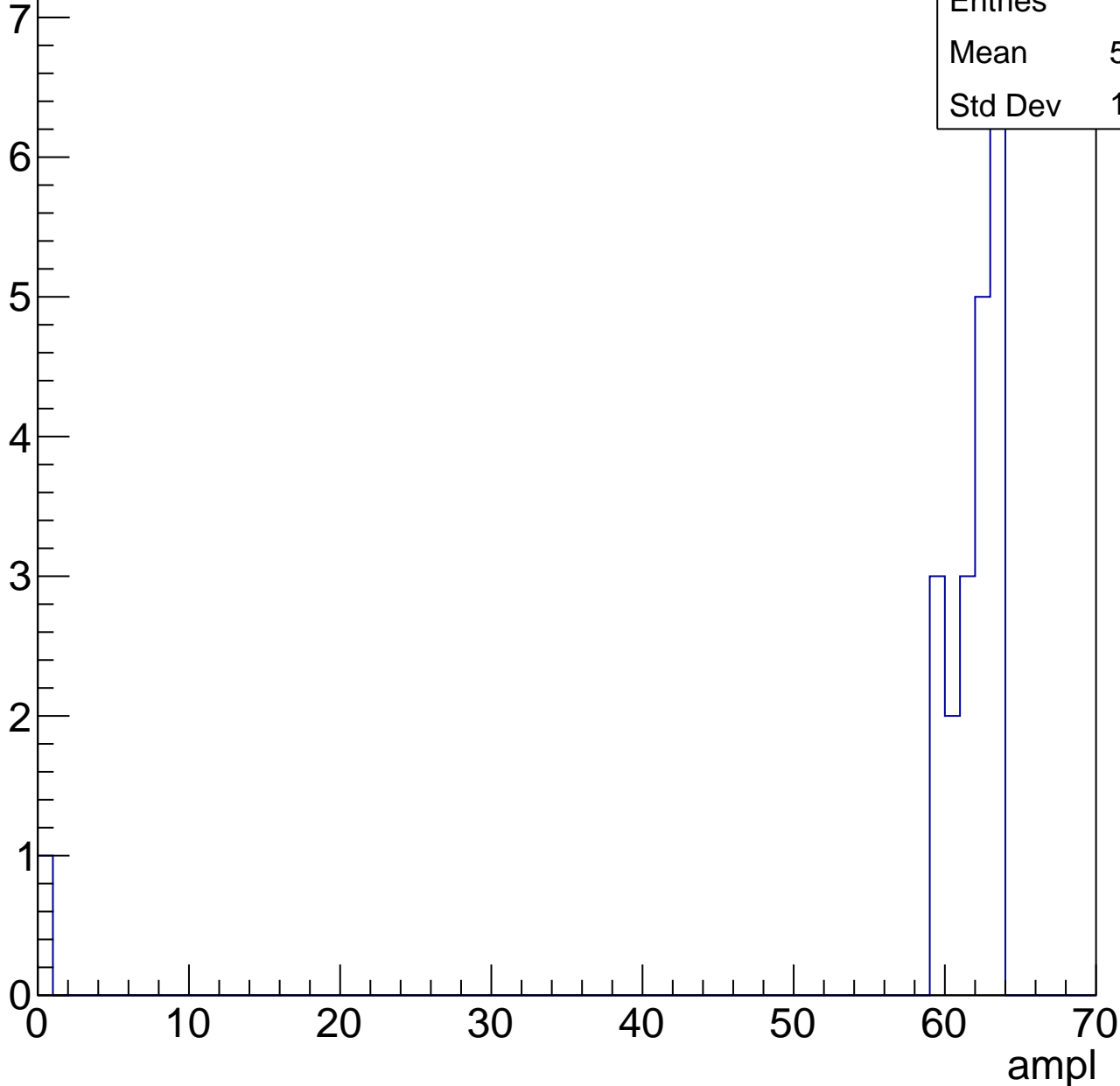


# B1L000S, U21-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.62
Std Dev	13.18





# B1L000S, U21-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U21-ch92, adc0

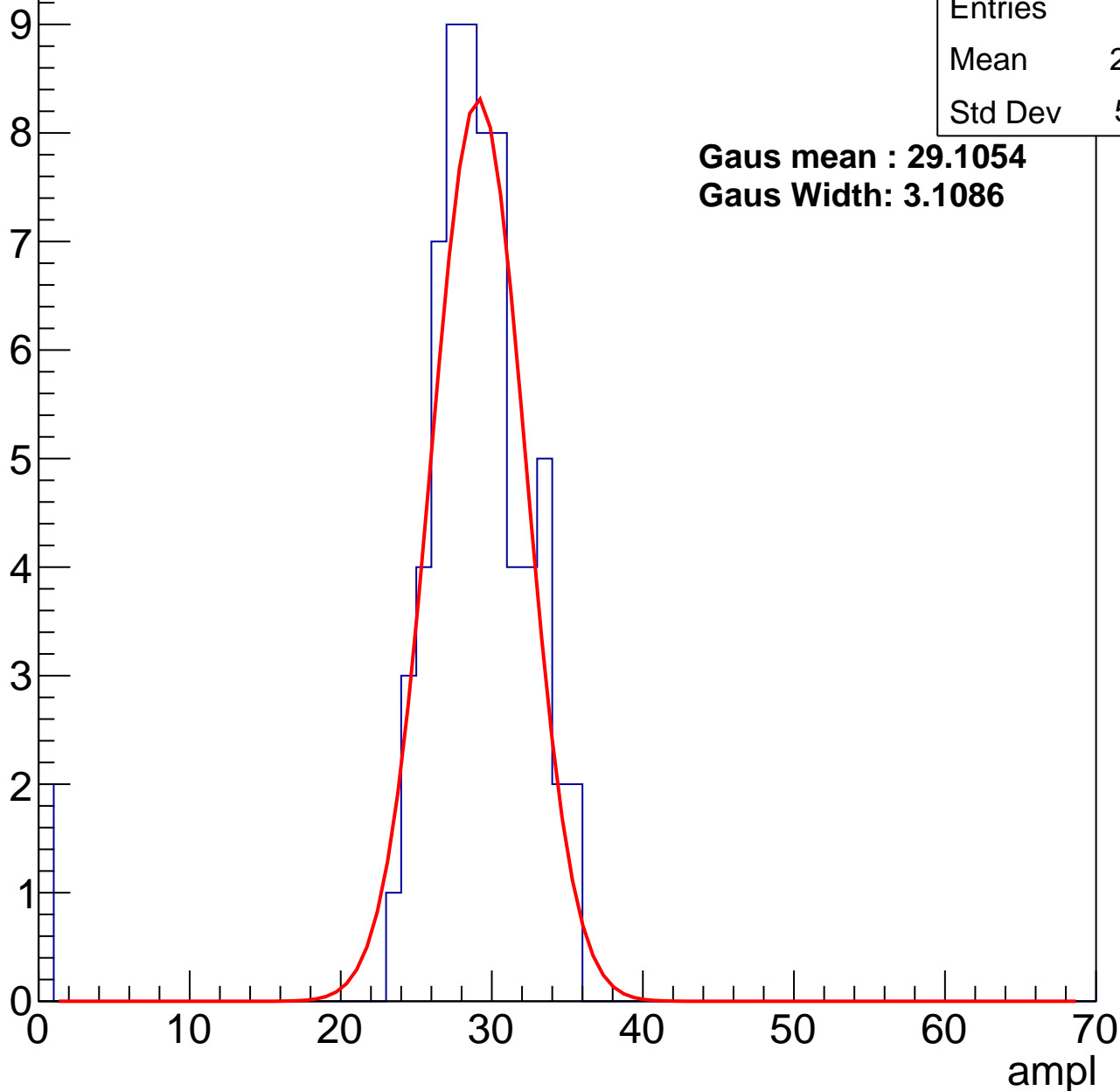
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	27.93
Std Dev	5.621

**Gaus mean : 29.1054**

**Gaus Width: 3.1086**



# B1L000S, U21-ch92, adc1

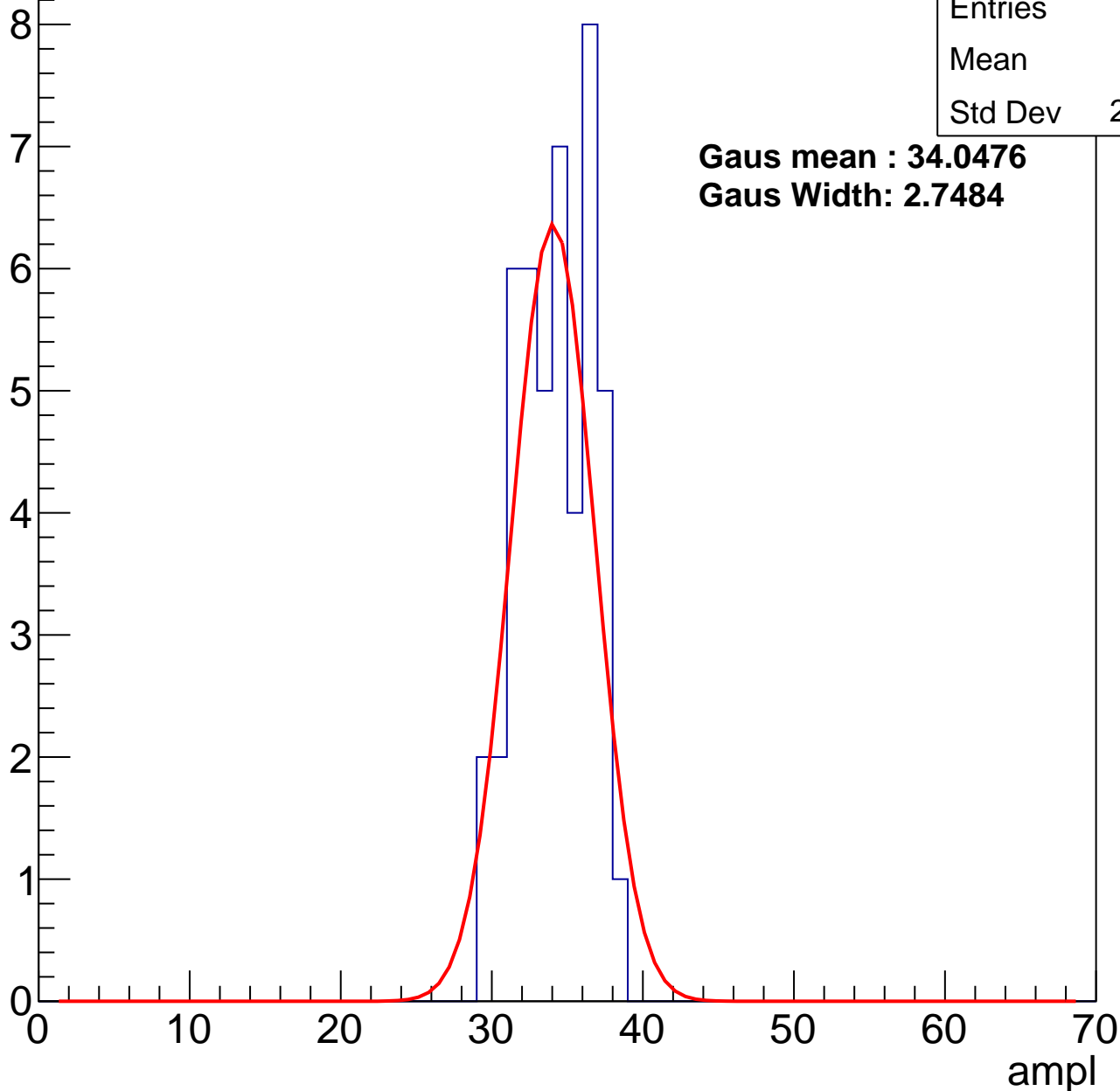
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	33.7
Std Dev	2.367

**Gaus mean : 34.0476**

**Gaus Width: 2.7484**



# B1L000S, U21-ch92, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	40.22
Std Dev	3.029

**Gaus mean : 40.7996**

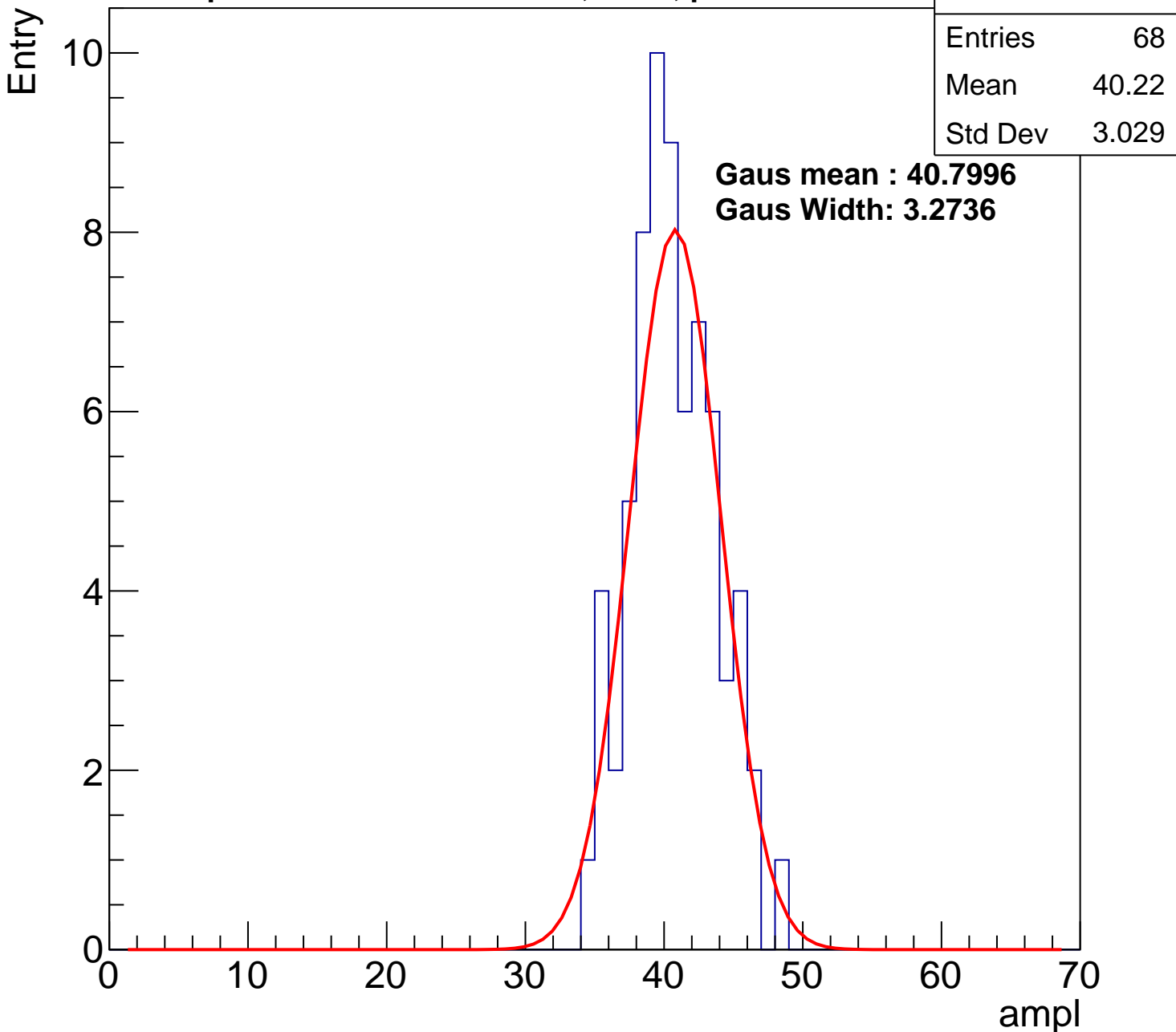
**Gaus Width: 3.2736**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

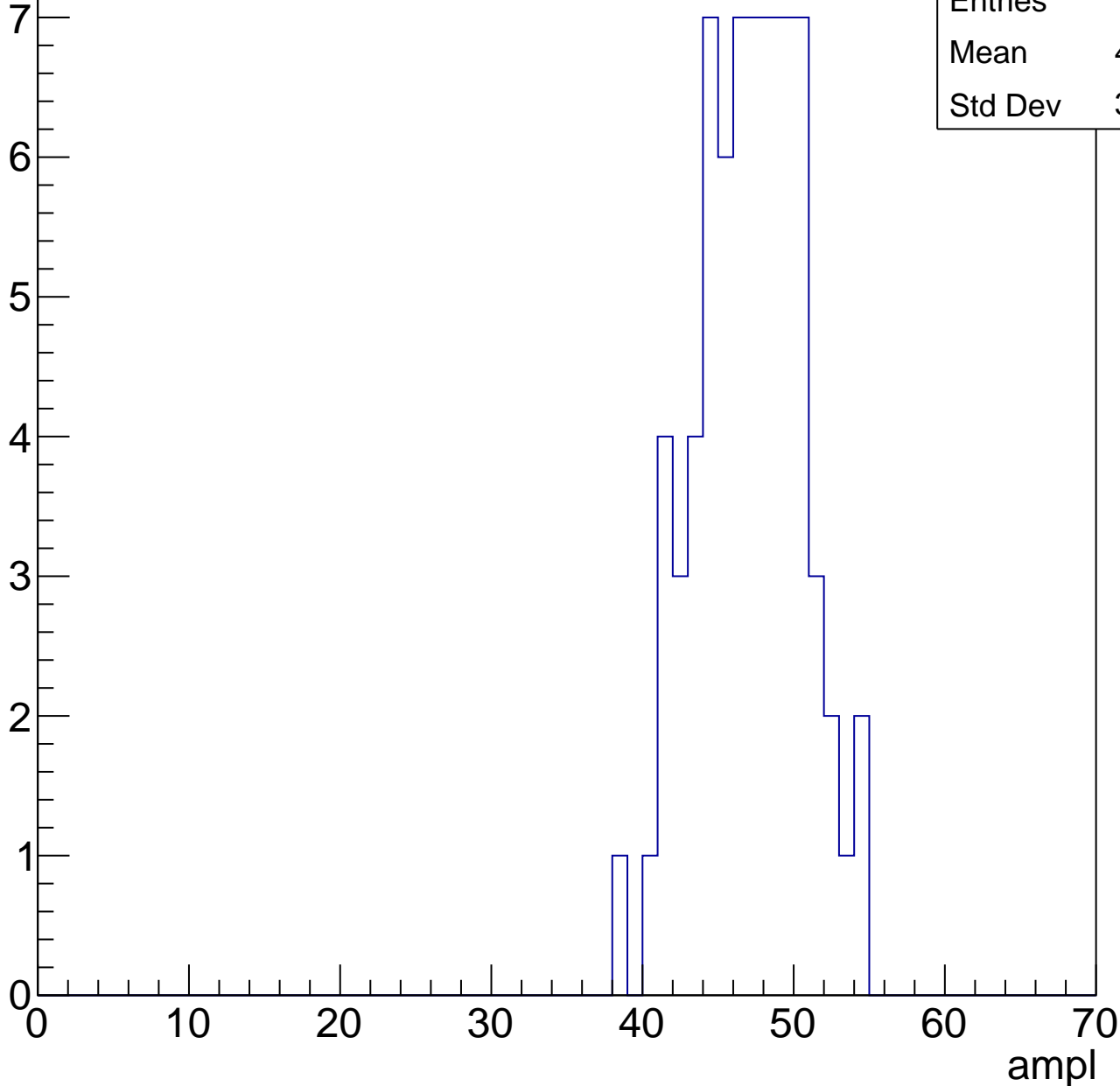


# B1L000S, U21-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

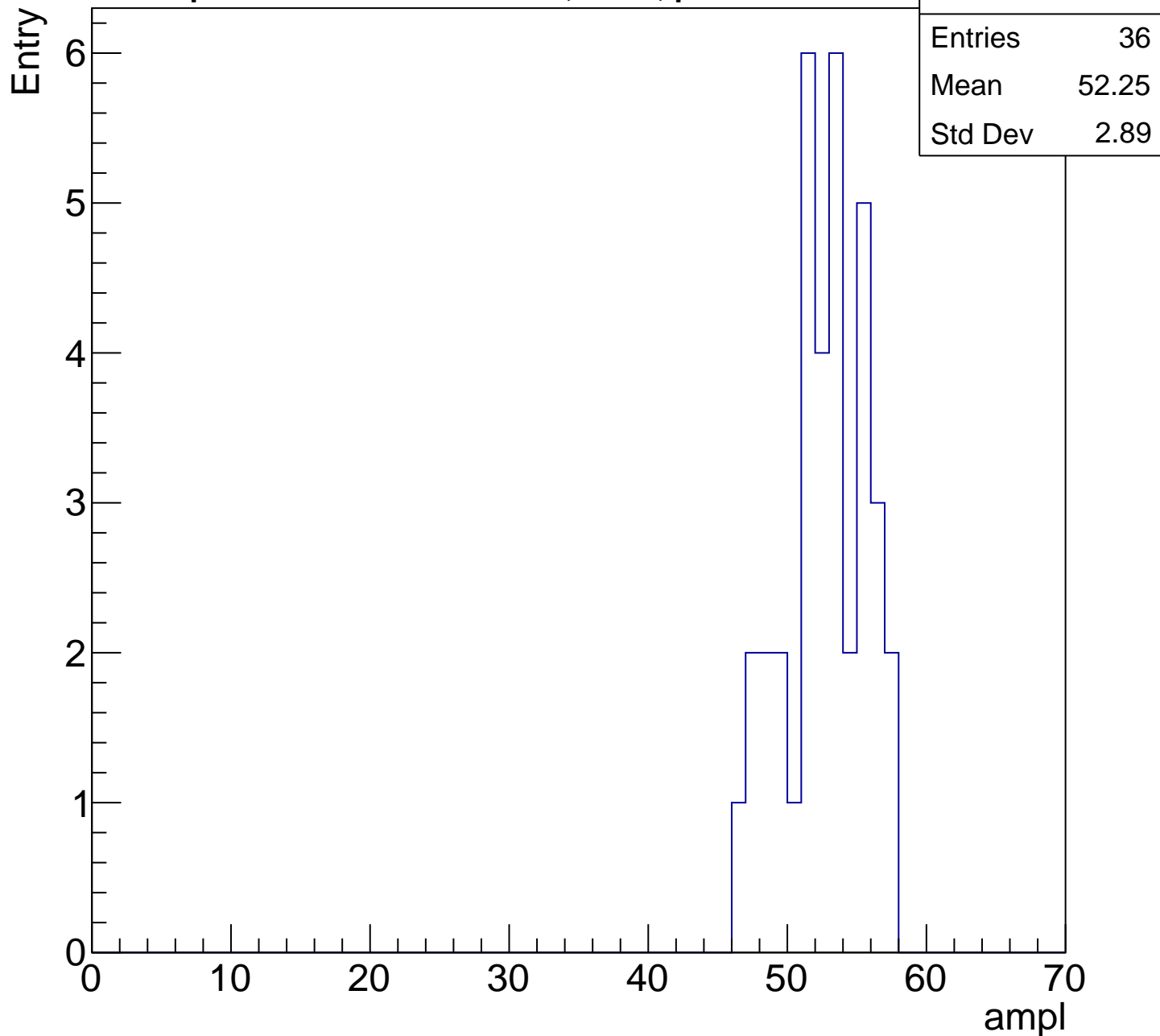
Entry

Entries	69
Mean	46.61
Std Dev	3.461



# B1L000S, U21-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

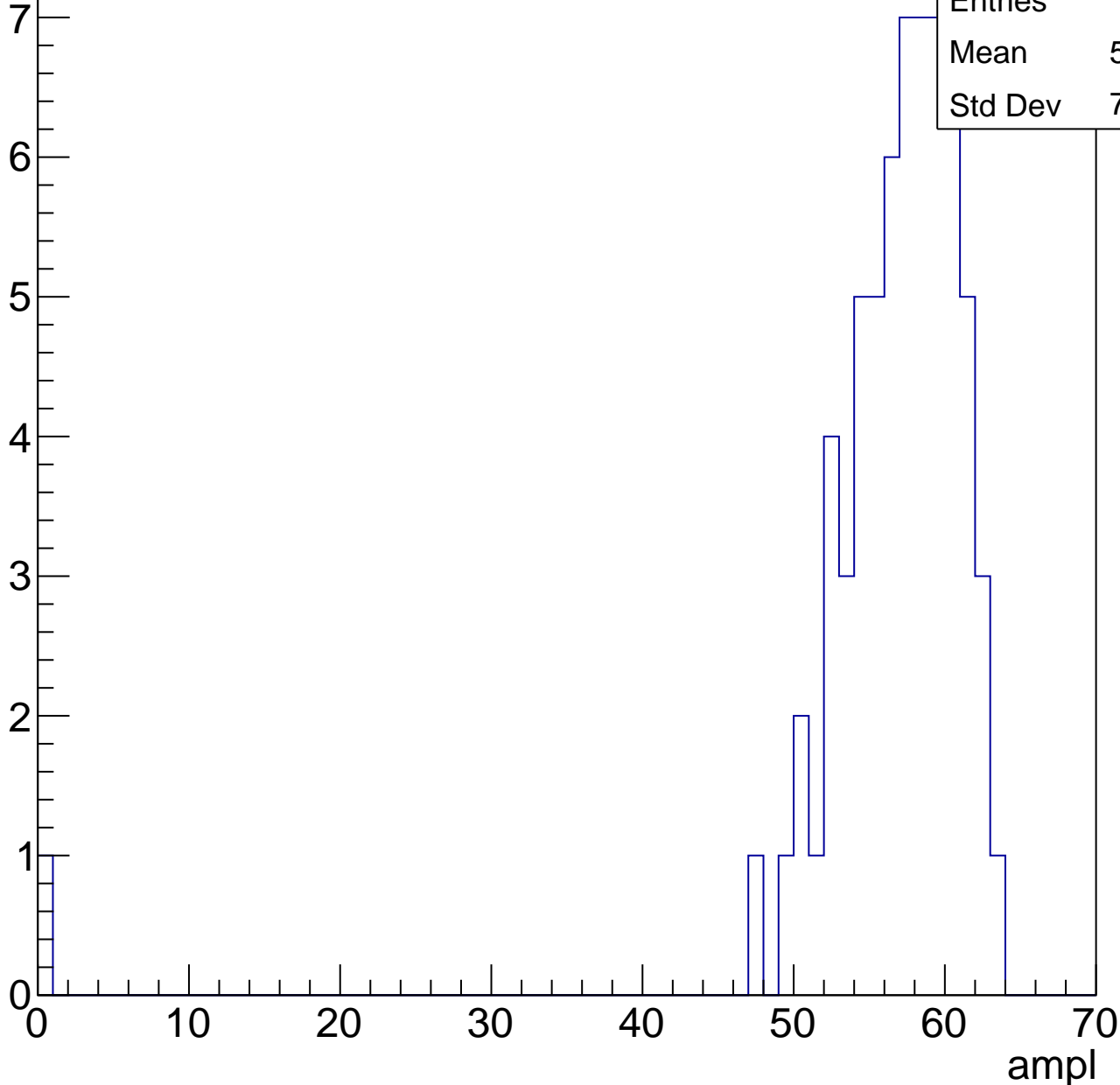


# B1L000S, U21-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	55.86
Std Dev	7.756



# B1L000S, U21-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	31
Mean	60.58
Std Dev	2.283

0

1

2

3

4

5

6

7

8

9

10

11

12

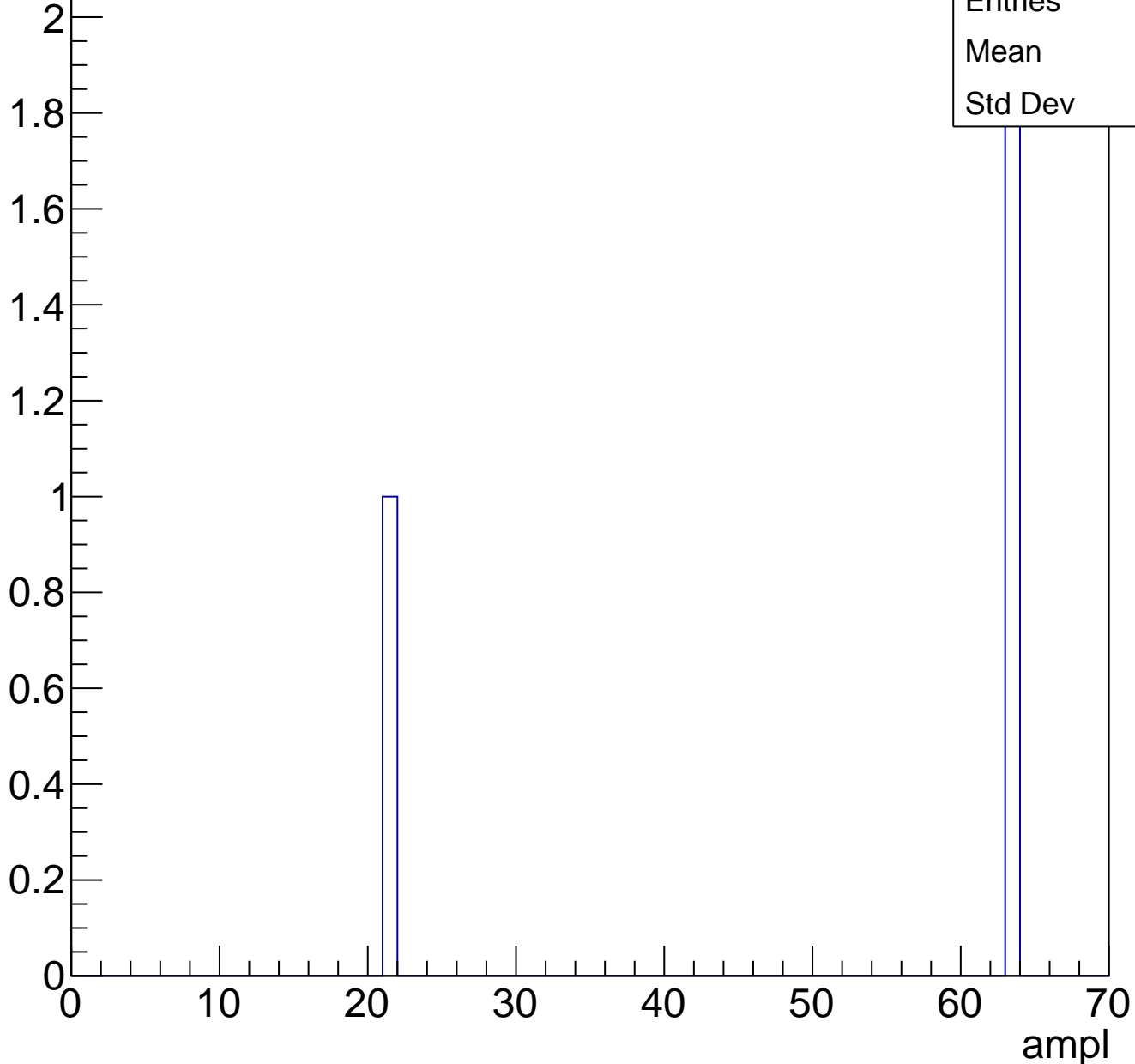
13



# B1L000S, U21-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch93, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	27.98
Std Dev	3.165

**Gaus mean : 27.9999**

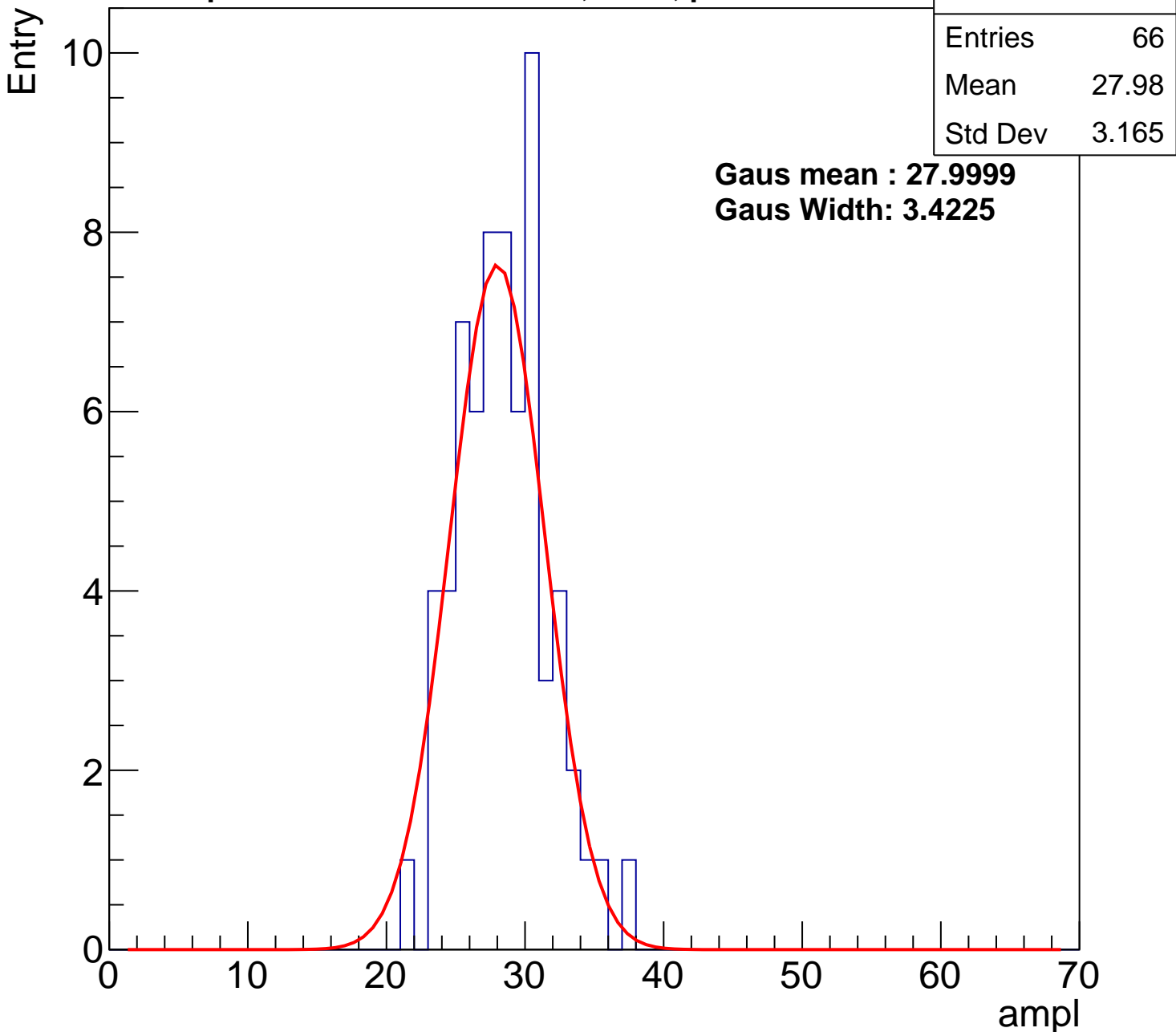
**Gaus Width: 3.4225**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch93, adc1

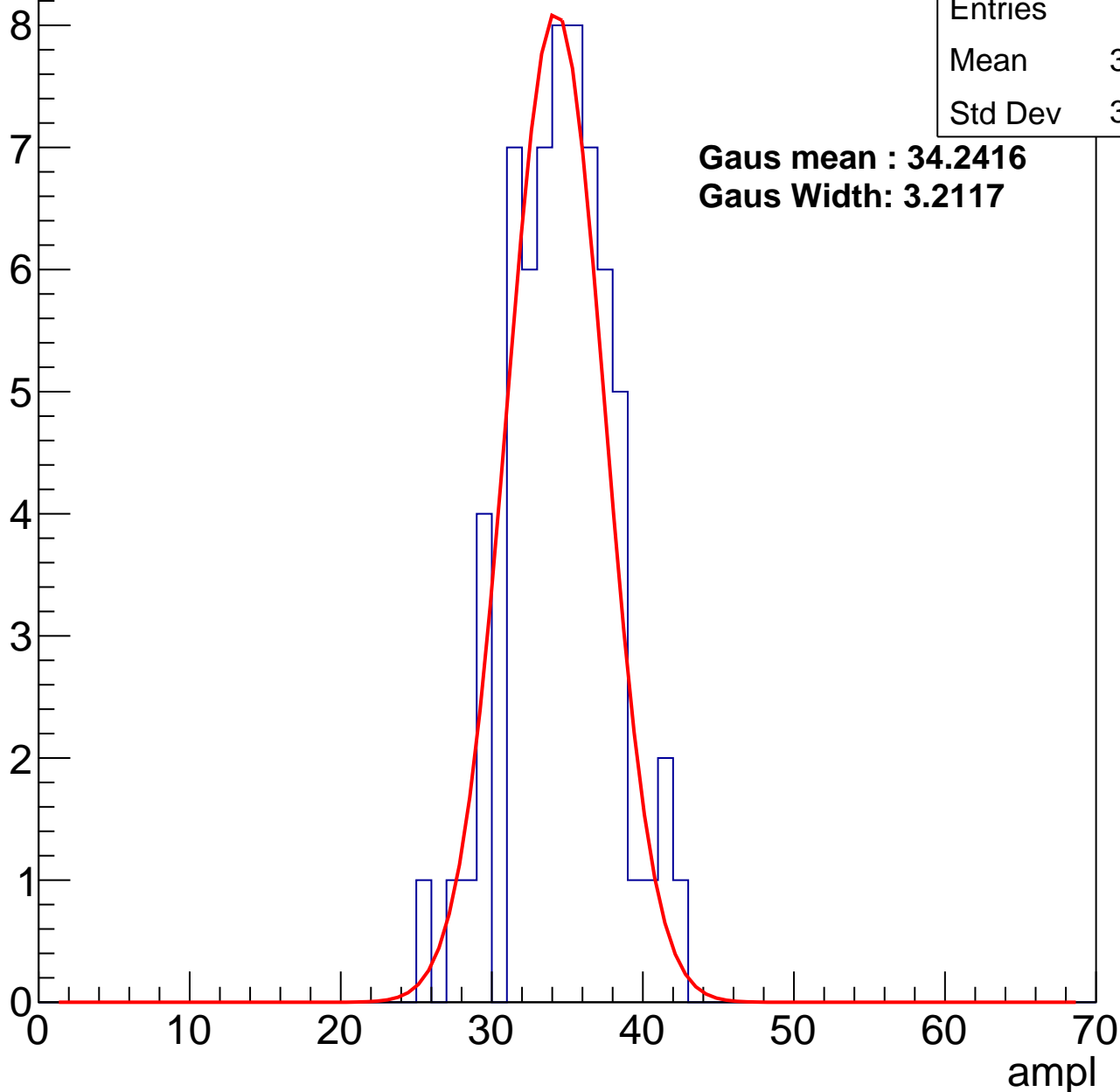
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.17
Std Dev	3.374

**Gaus mean : 34.2416**

**Gaus Width: 3.2117**



# B1L000S, U21-ch93, adc2

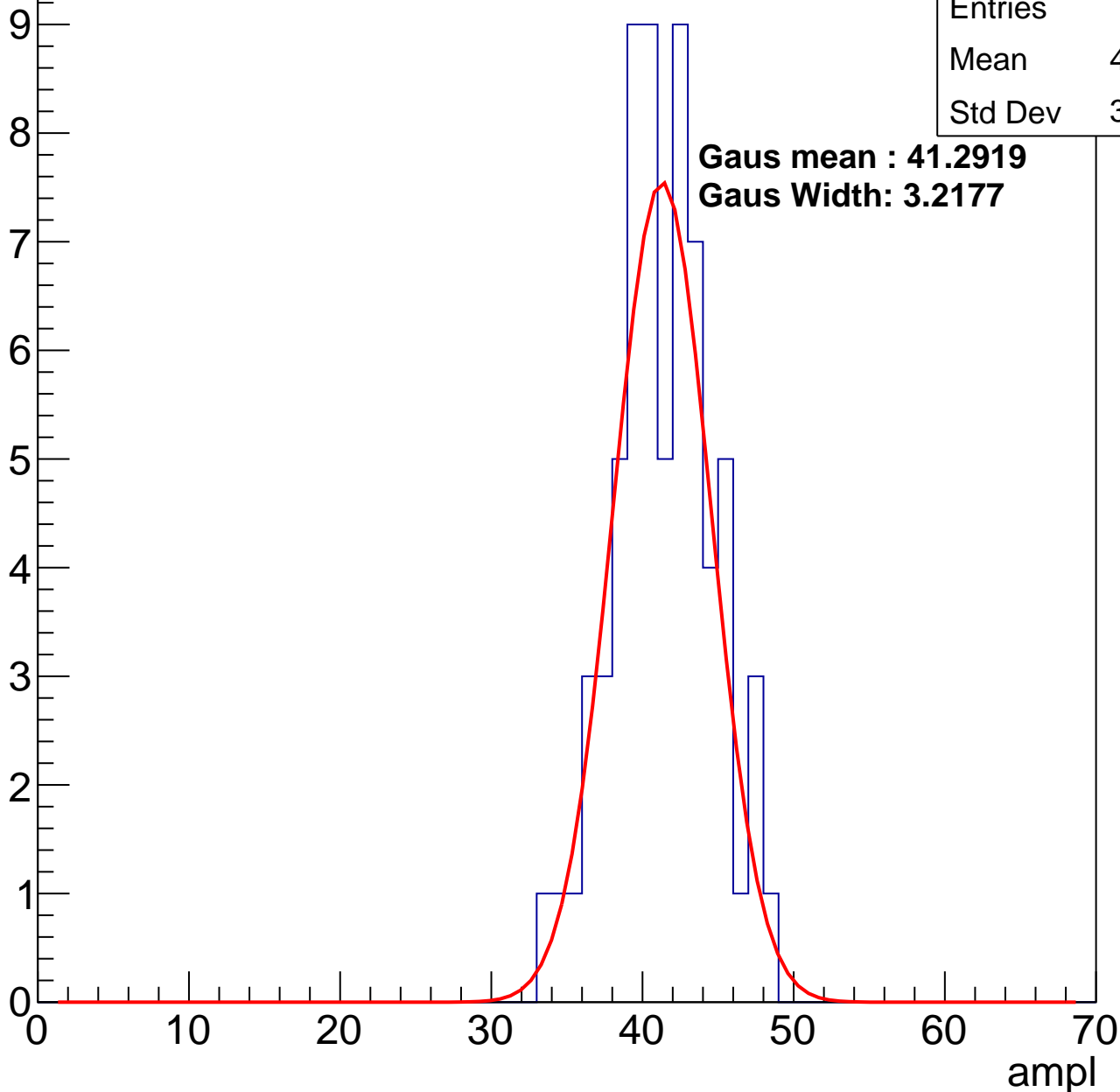
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	40.93
Std Dev	3.225

**Gaus mean : 41.2919**

**Gaus Width: 3.2177**

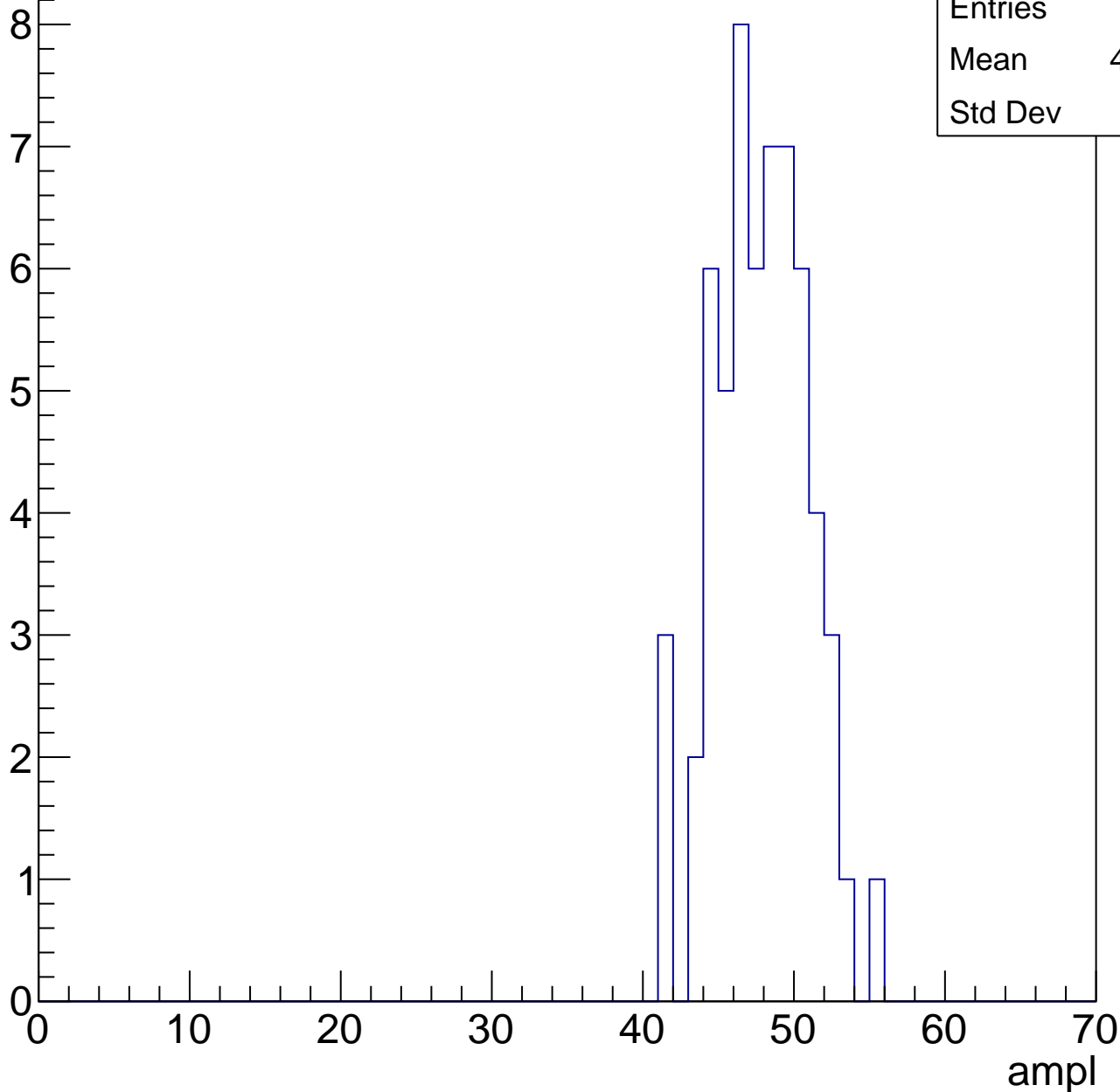


# B1L000S, U21-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.37
Std Dev	3.03

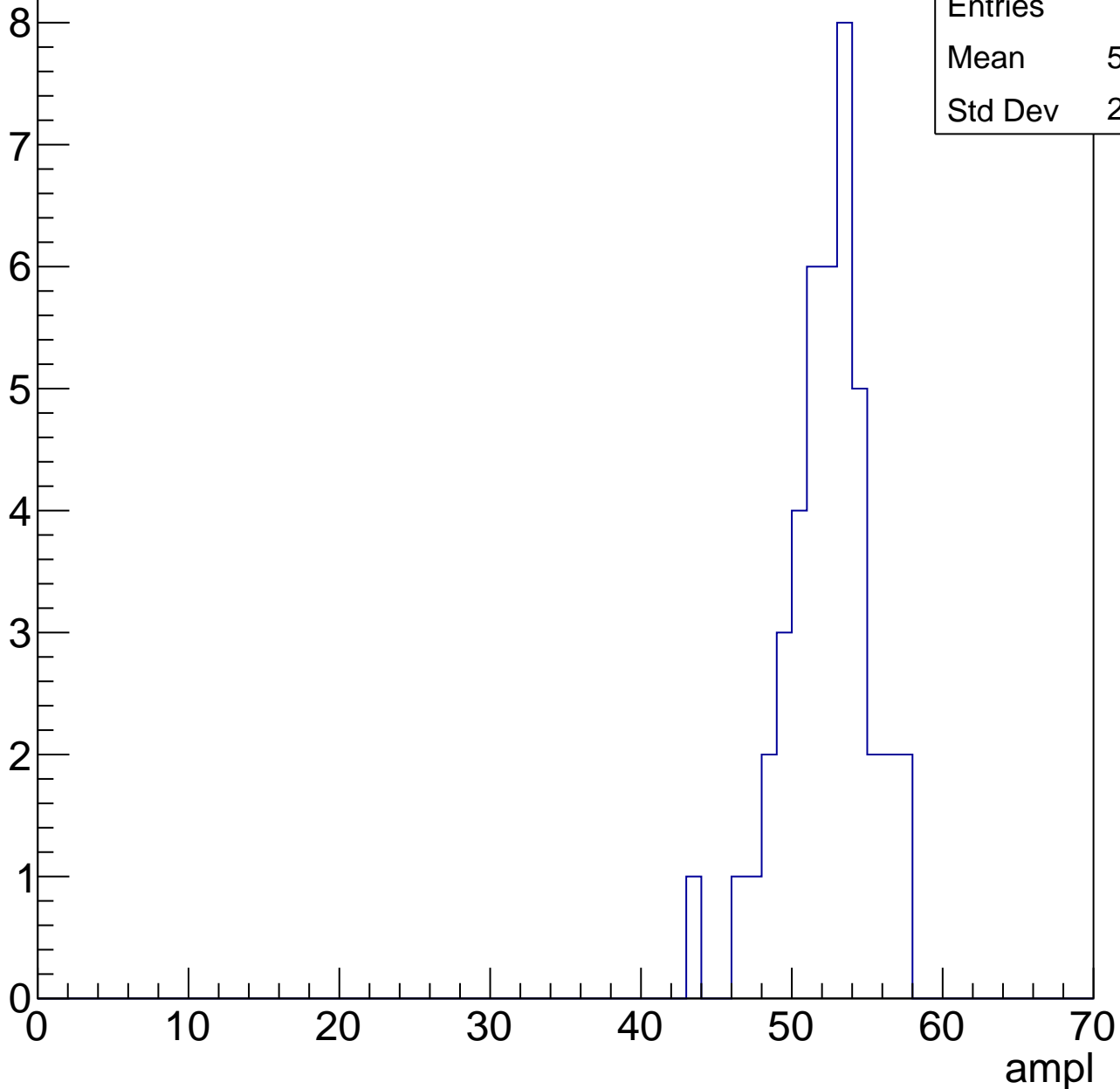


# B1L000S, U21-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

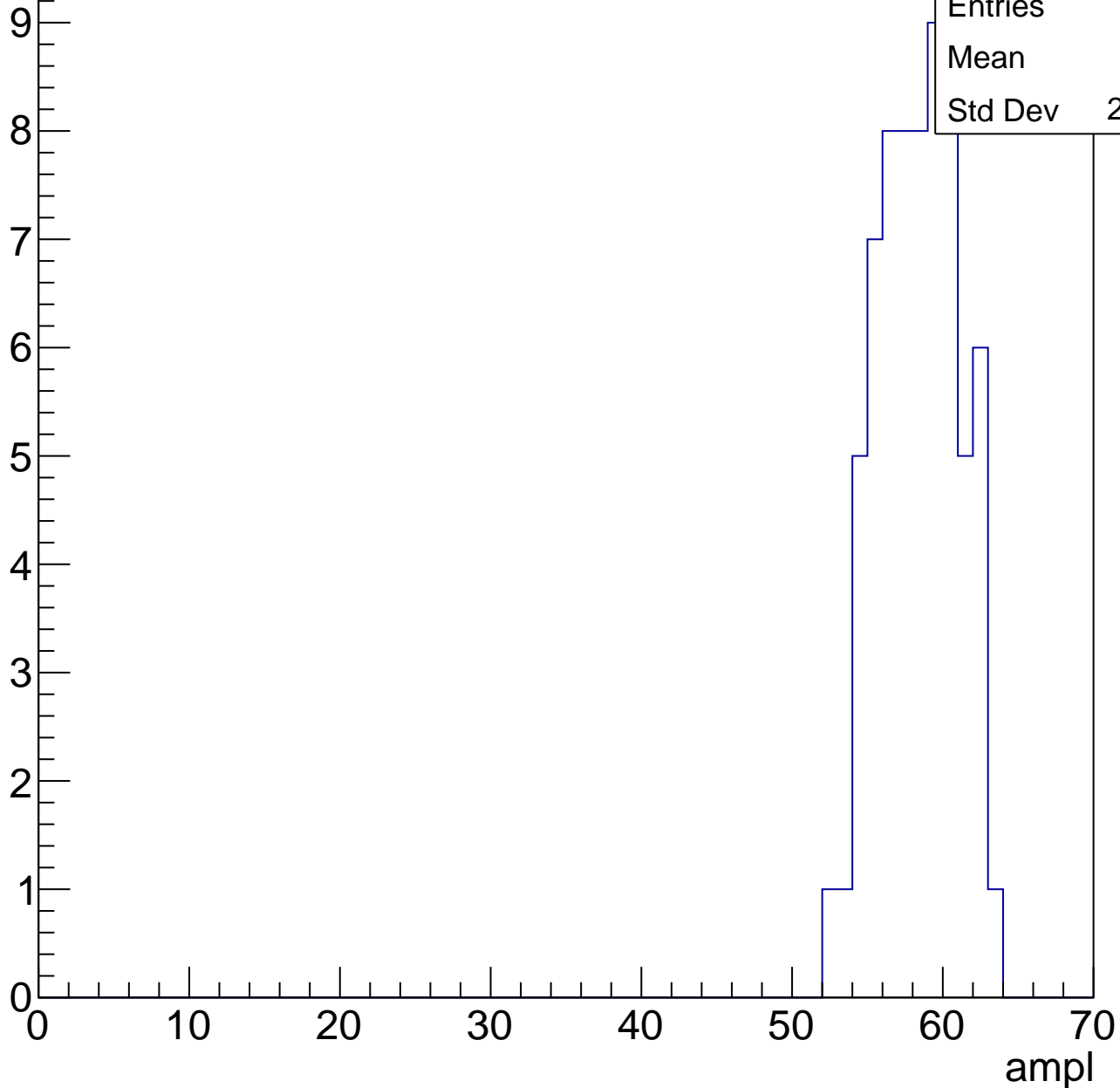
Entries	43
Mean	51.79
Std Dev	2.849



# B1L000S, U21-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

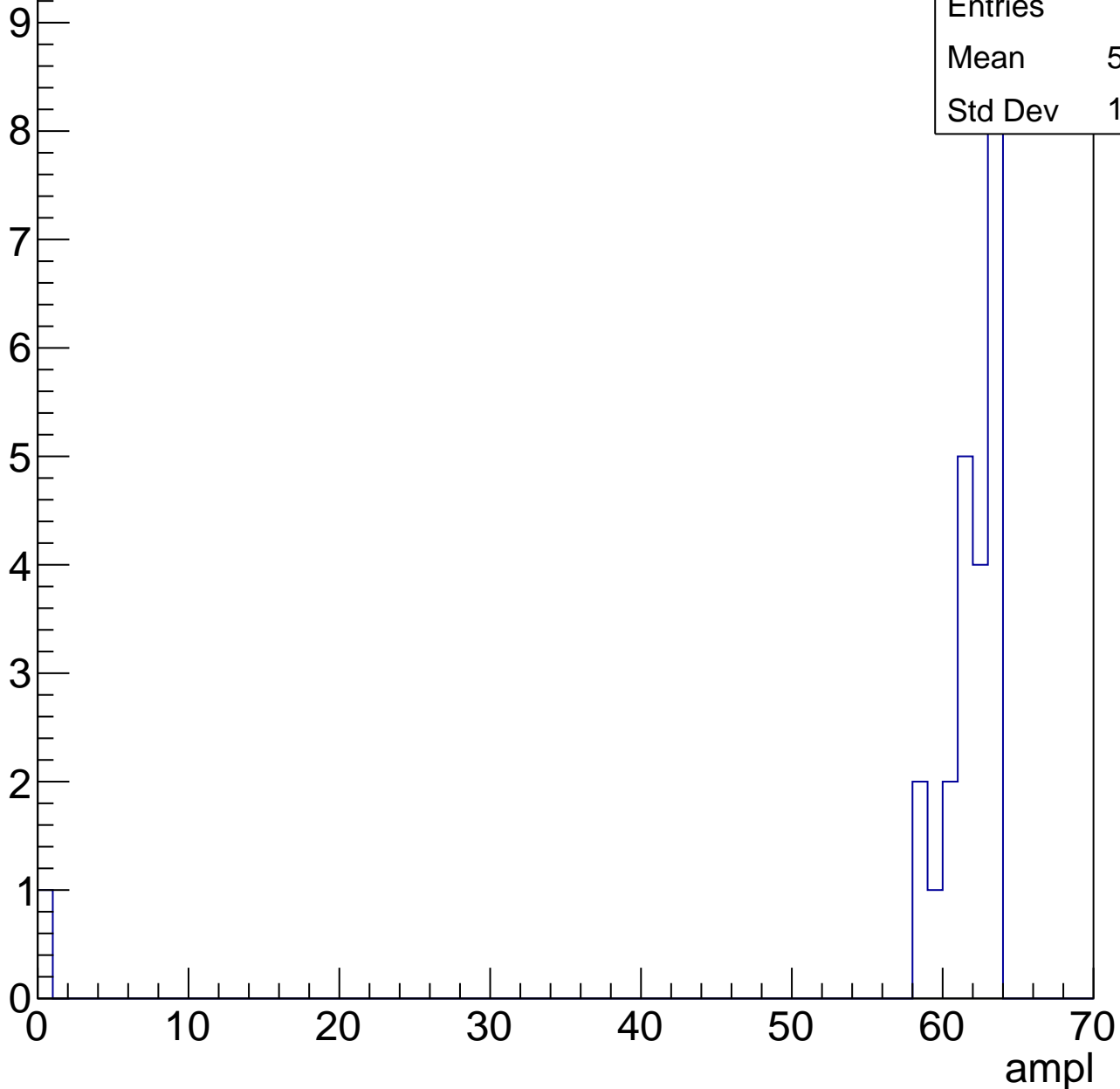


# B1L000S, U21-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.96
Std Dev	12.39





# B1L000S, U21-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch94, adc0

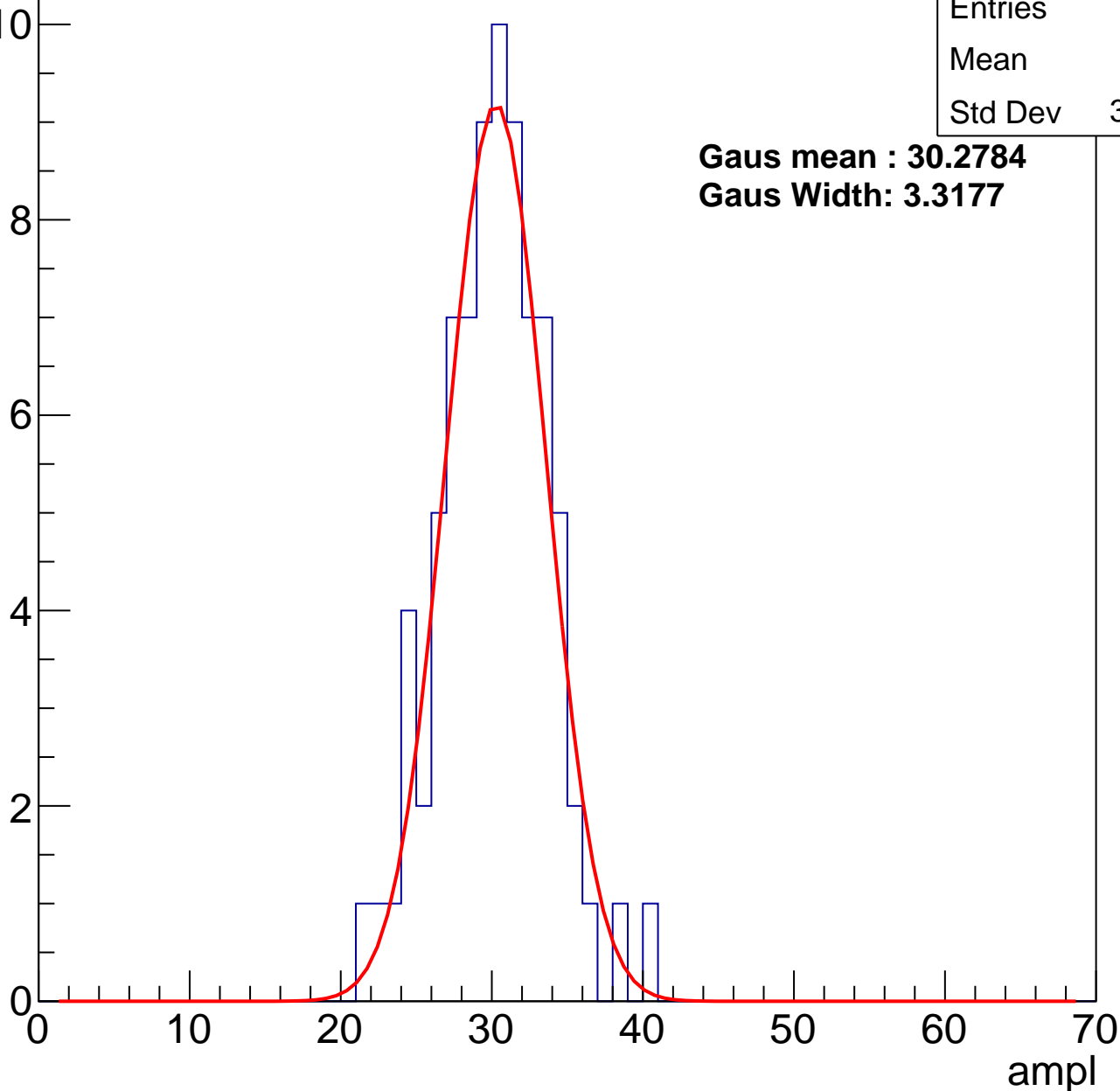
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	29.7
Std Dev	3.519

**Gaus mean : 30.2784**

**Gaus Width: 3.3177**



# B1L000S, U21-ch94, adc1

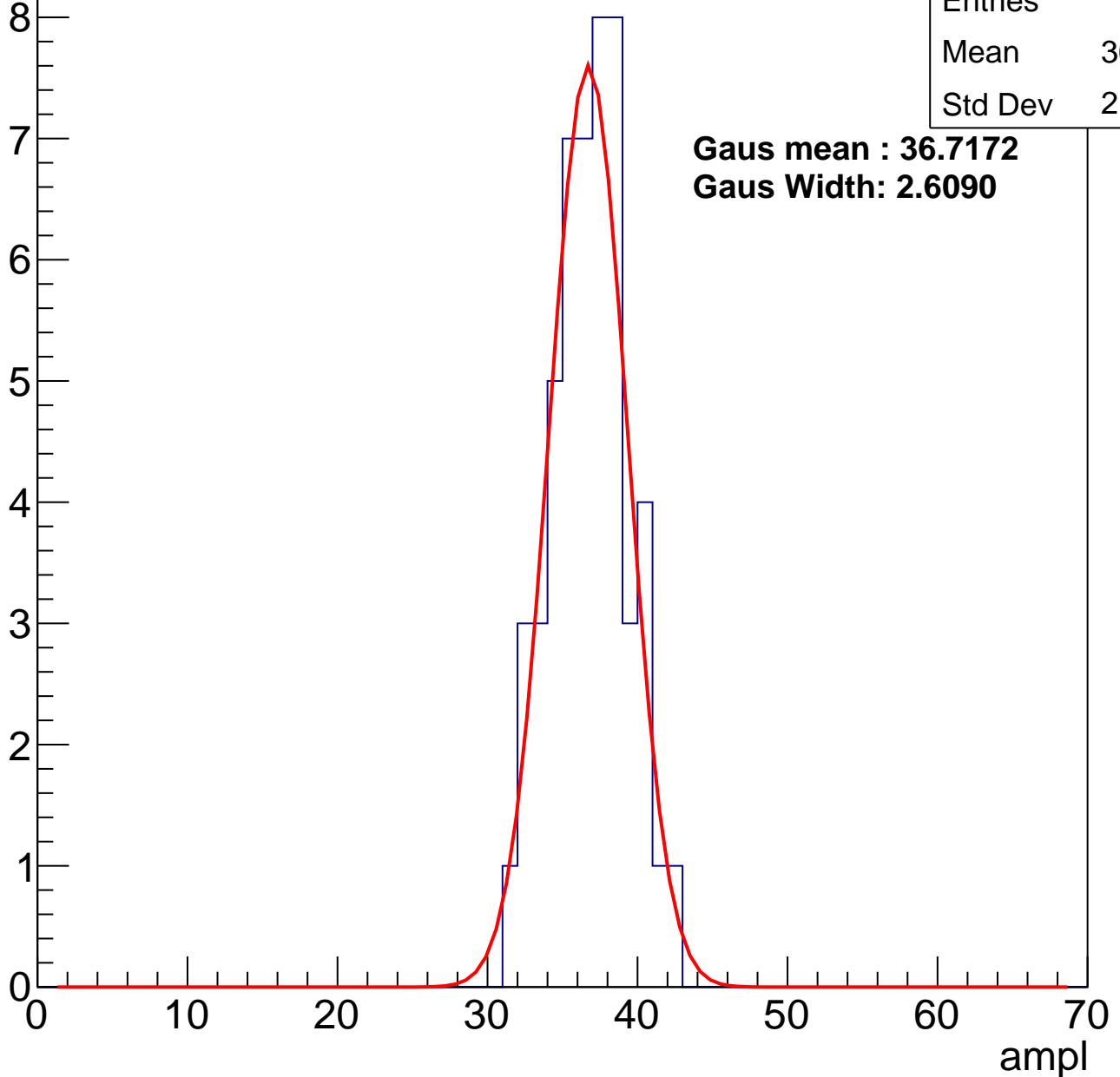
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	36.33
Std Dev	2.479

**Gaus mean : 36.7172**

**Gaus Width: 2.6090**



# B1L000S, U21-ch94, adc2

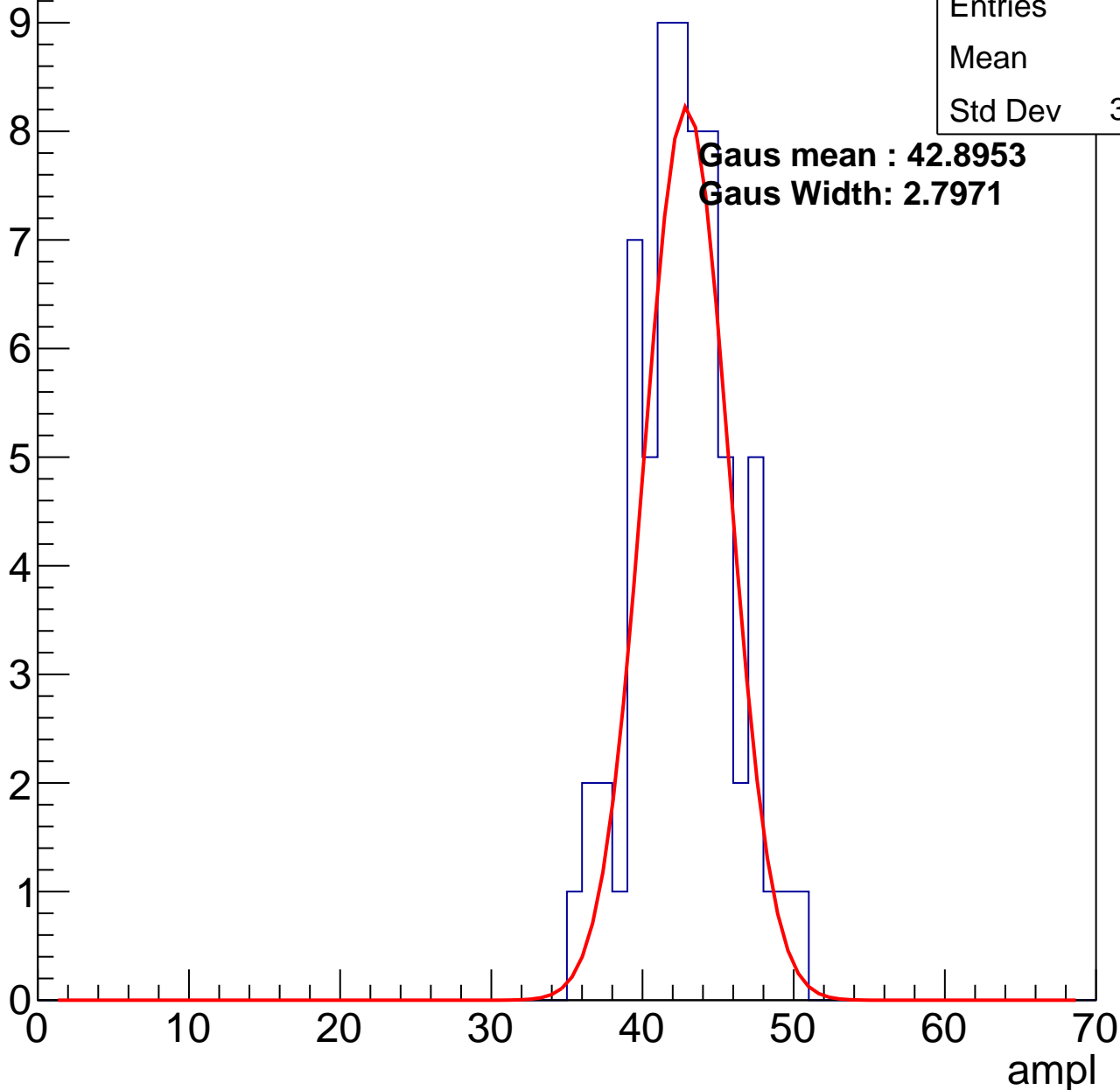
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.3
Std Dev	3.148

**Gaus mean : 42.8953**

**Gaus Width: 2.7971**

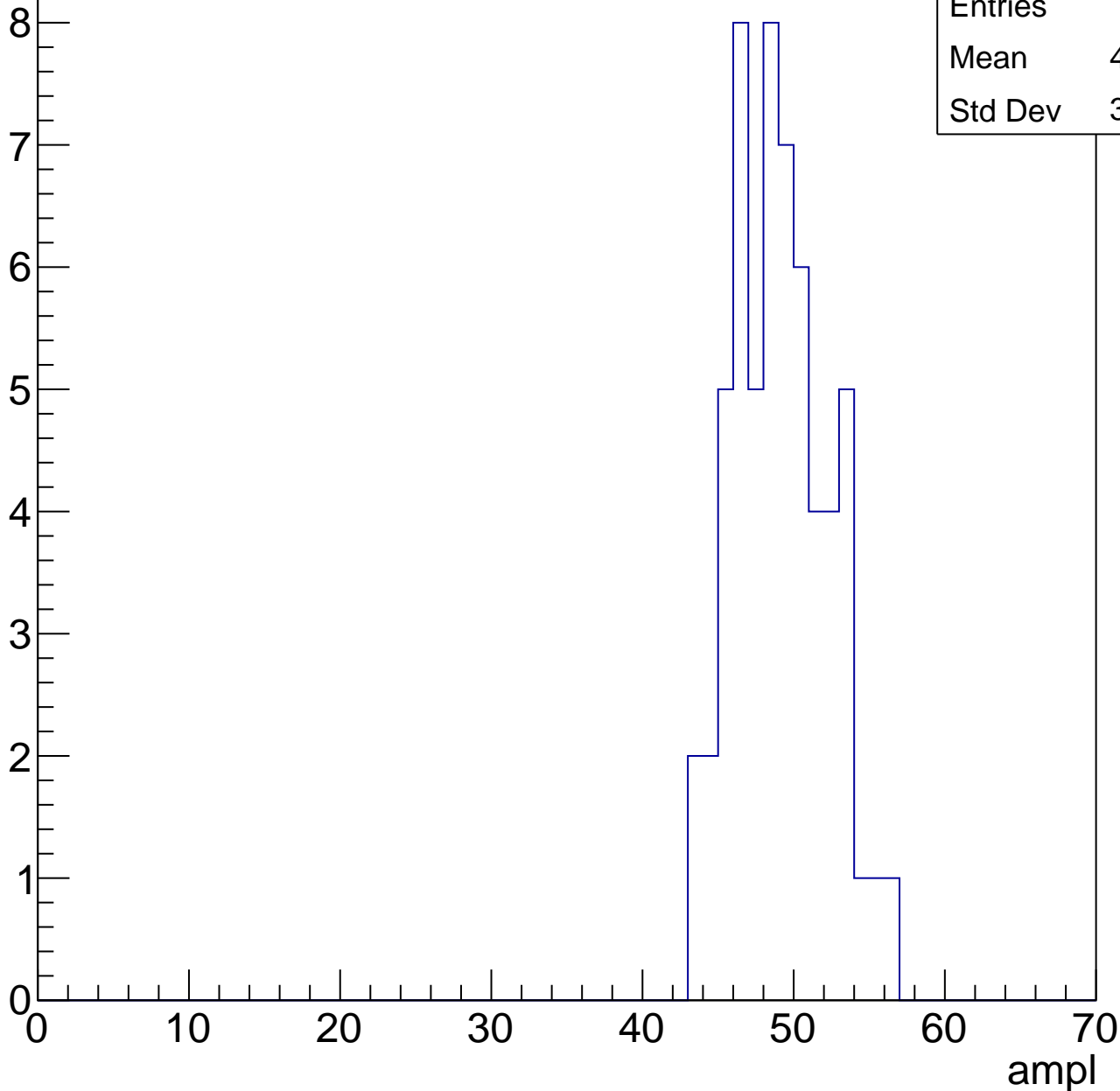


# B1L000S, U21-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

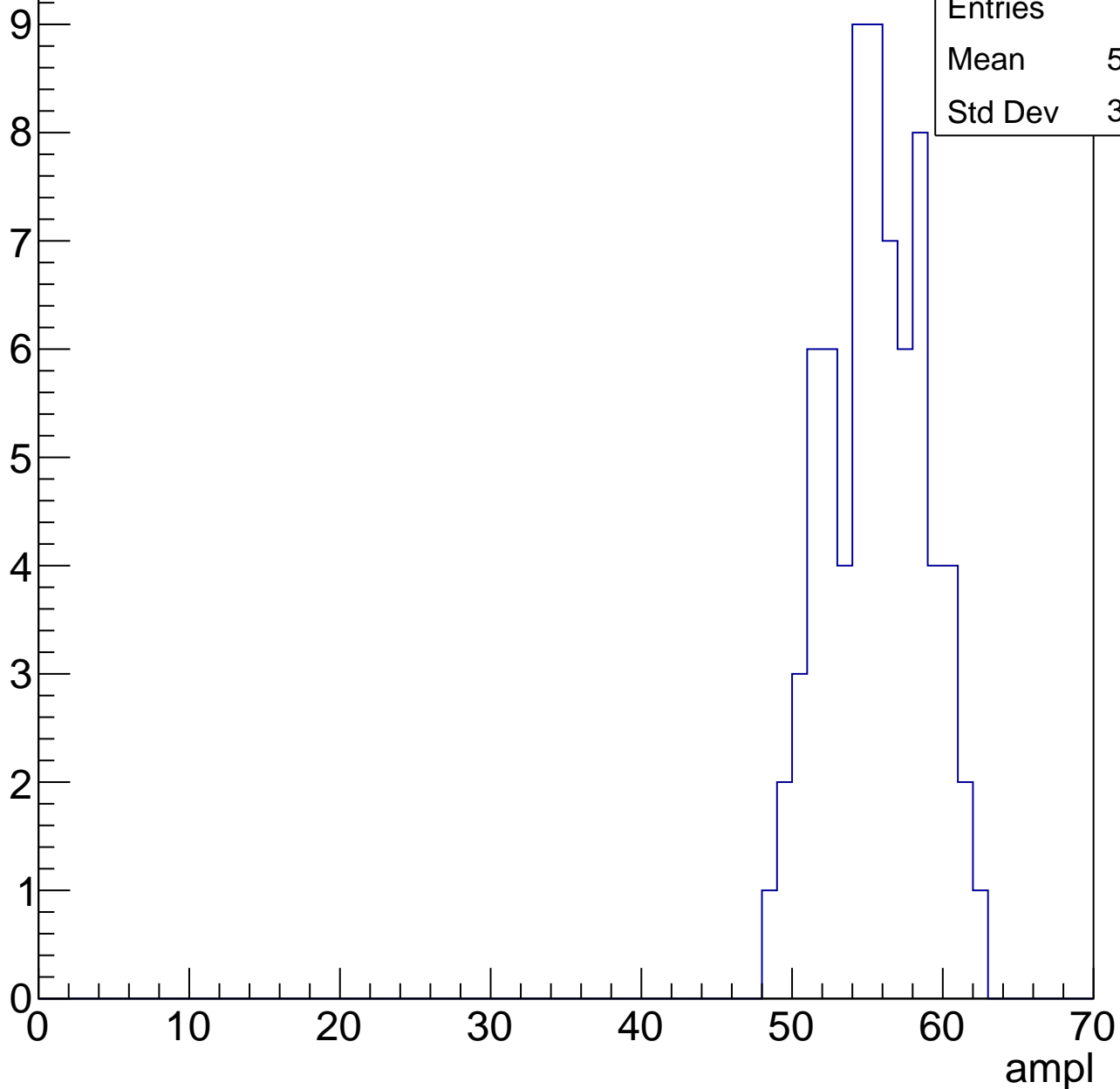
Entries	59
Mean	48.66
Std Dev	3.034



# B1L000S, U21-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

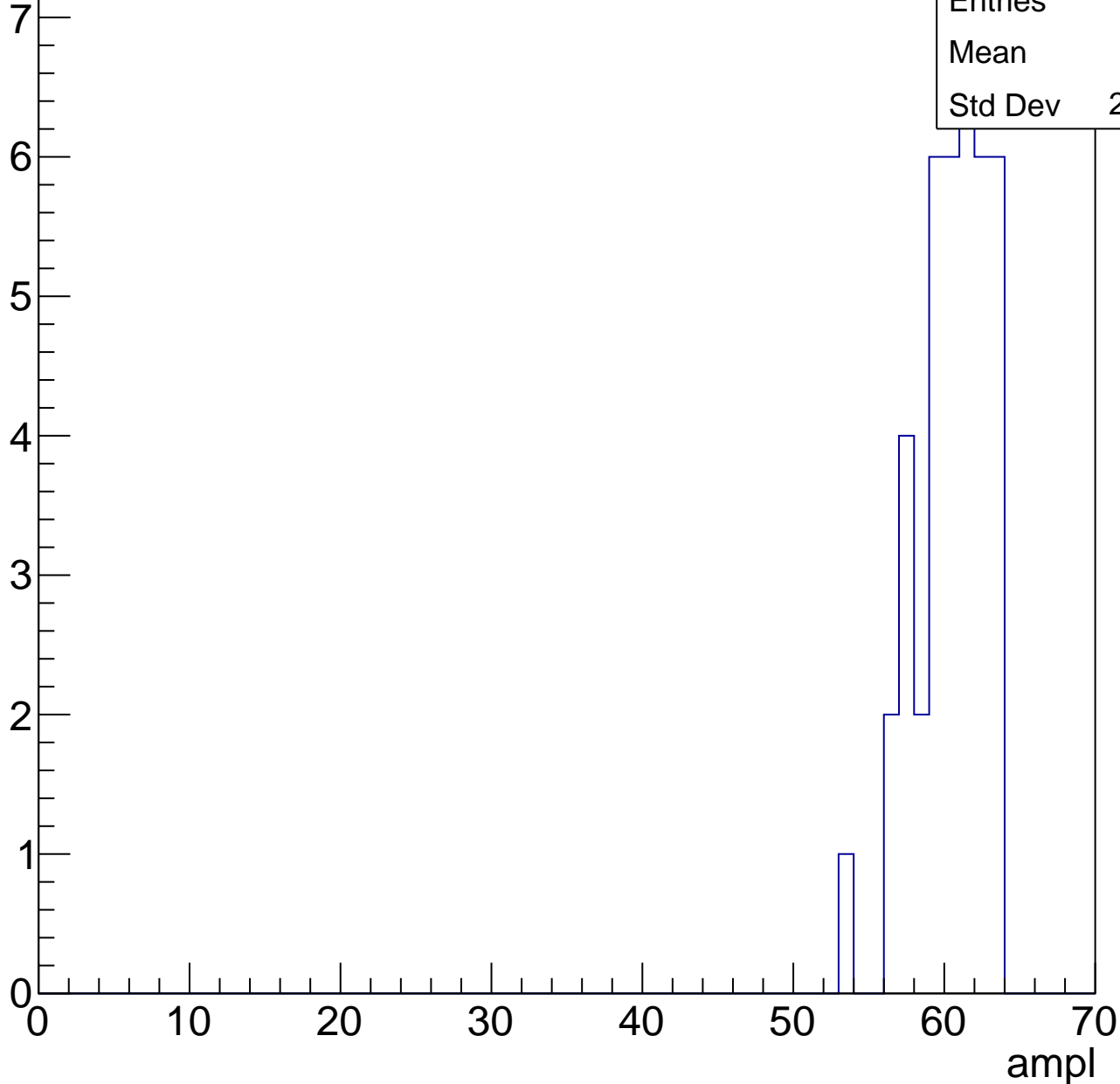


# B1L000S, U21-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

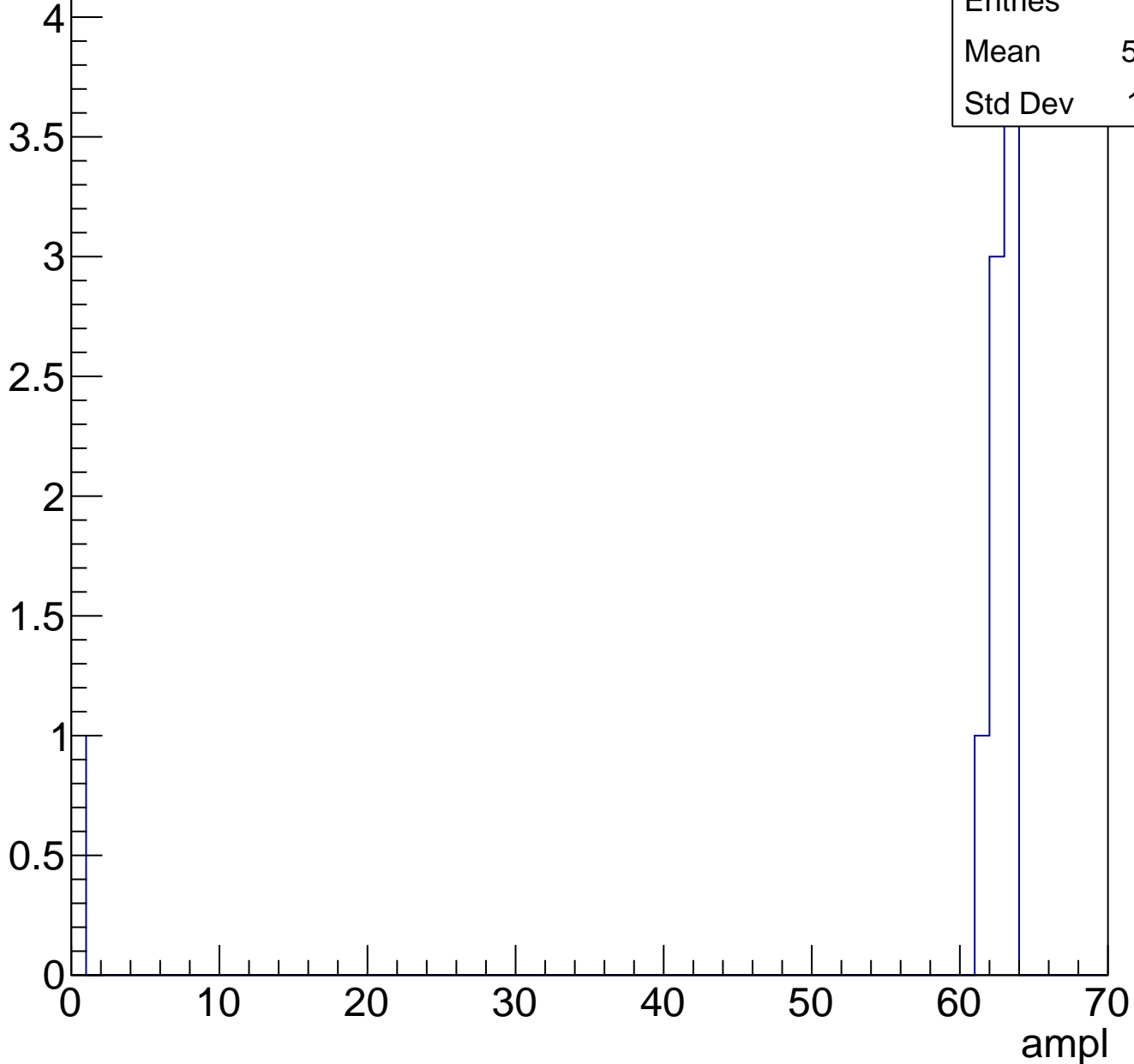
Entries	40
Mean	60
Std Dev	2.324



# B1L000S, U21-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U21-ch95, adc0

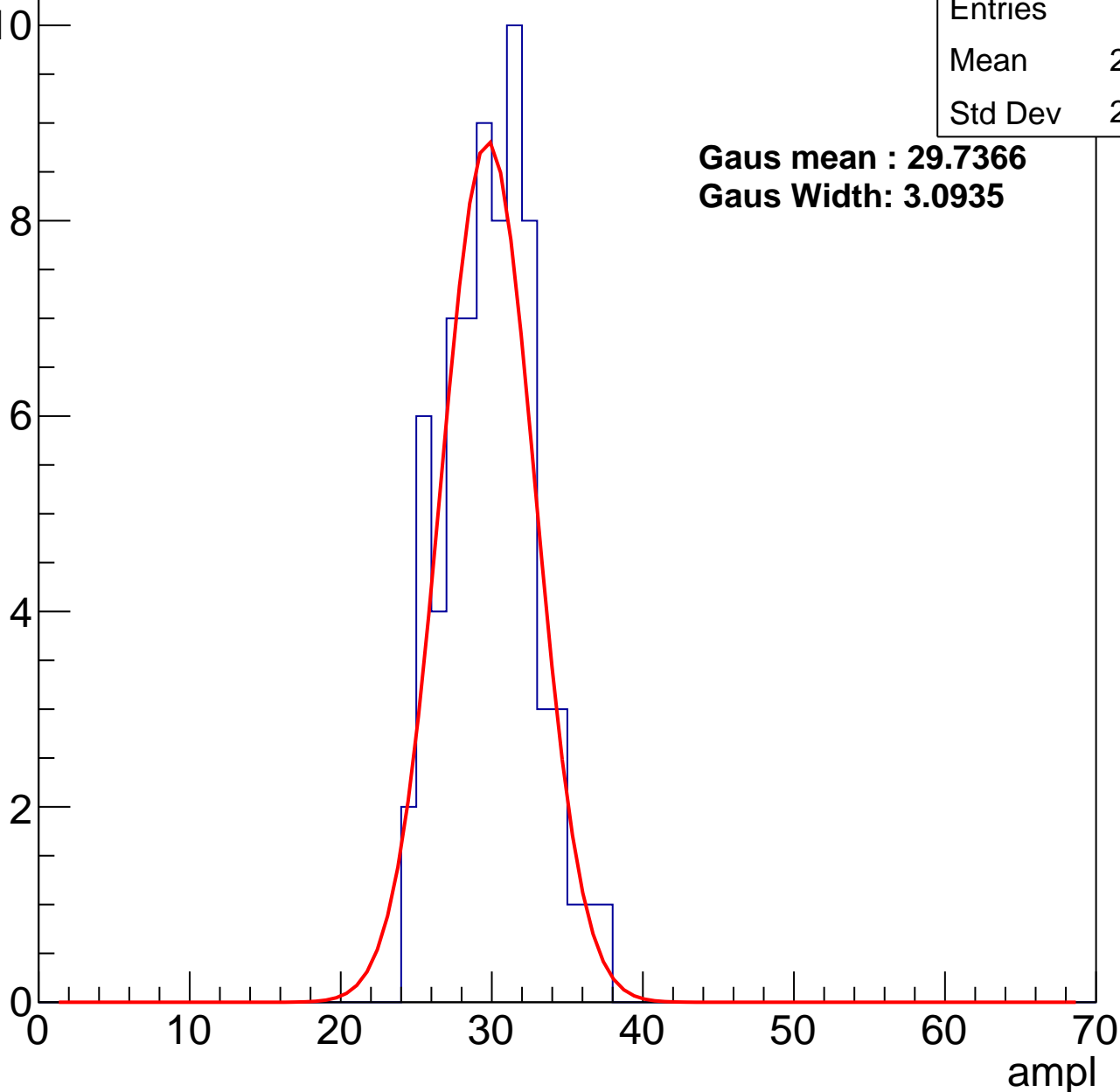
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	29.47
Std Dev	2.912

**Gaus mean : 29.7366**

**Gaus Width: 3.0935**



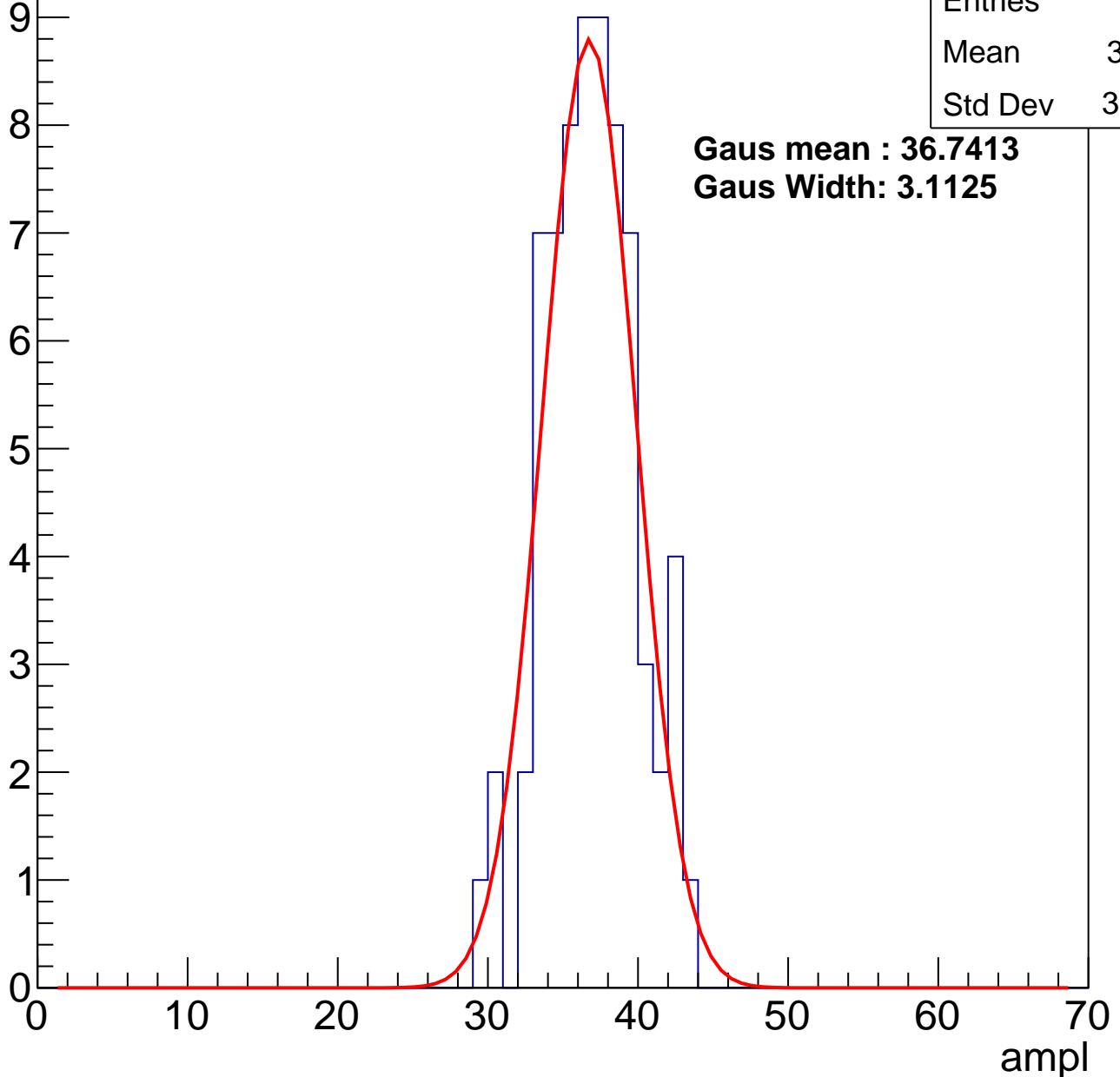
# B1L000S, U21-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.41
Std Dev	3.012

**Gaus mean : 36.7413**  
**Gaus Width: 3.1125**



# B1L000S, U21-ch95, adc2

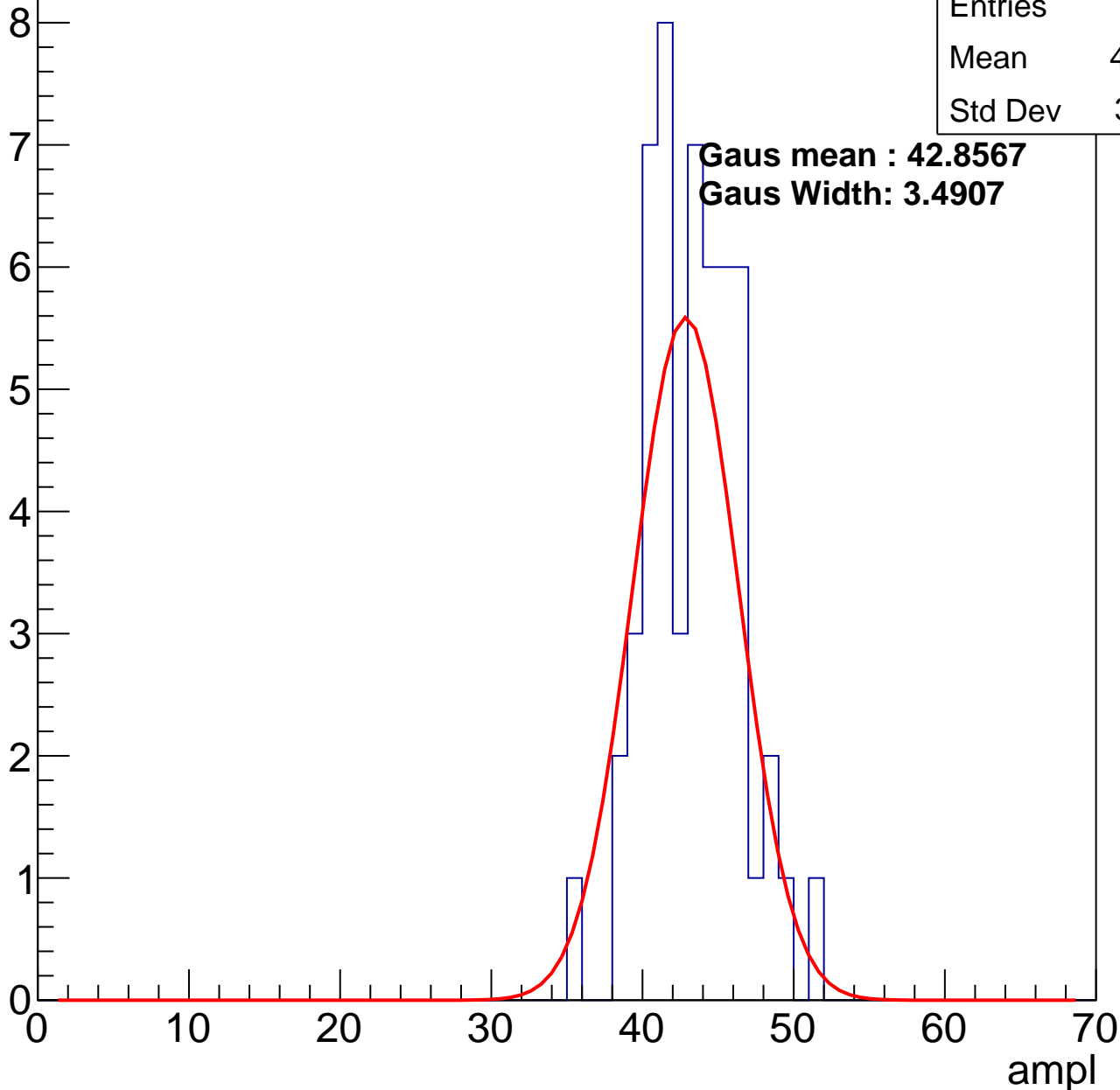
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.89
Std Dev	3.071

**Gaus mean : 42.8567**

**Gaus Width: 3.4907**

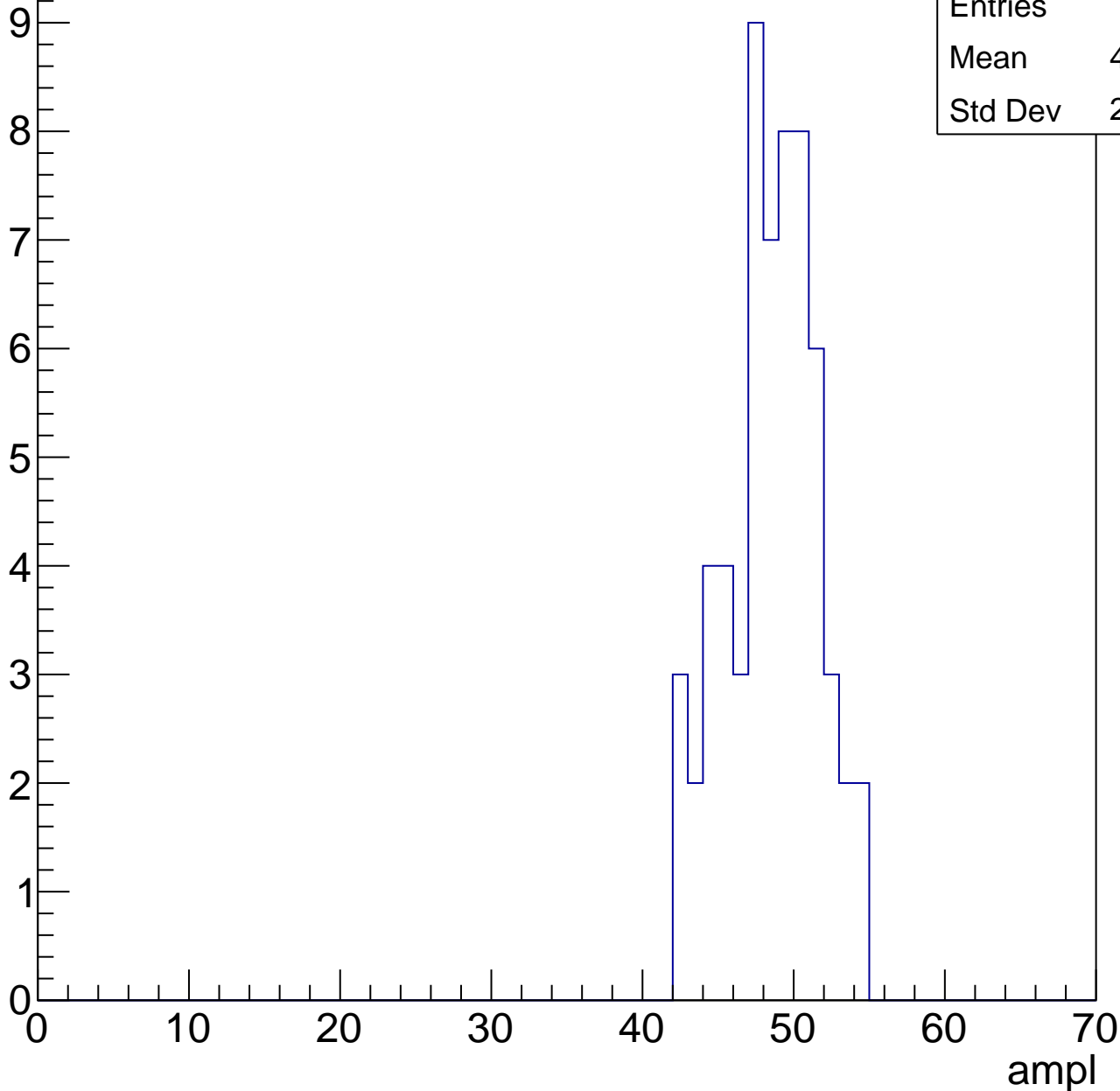


# B1L000S, U21-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	48.08
Std Dev	2.982

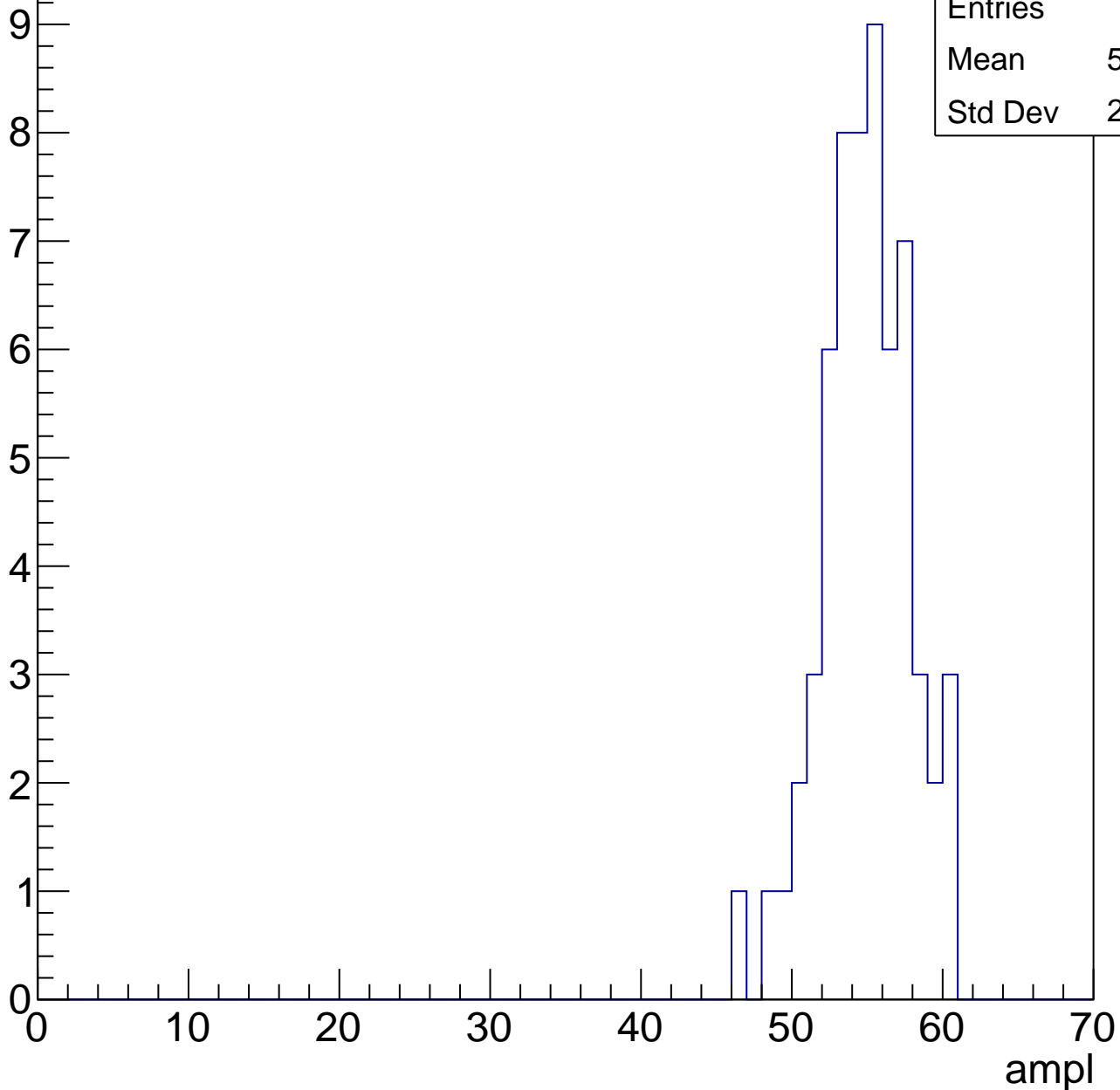


# B1L000S, U21-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.43
Std Dev	2.906



# B1L000S, U21-ch95, adc5

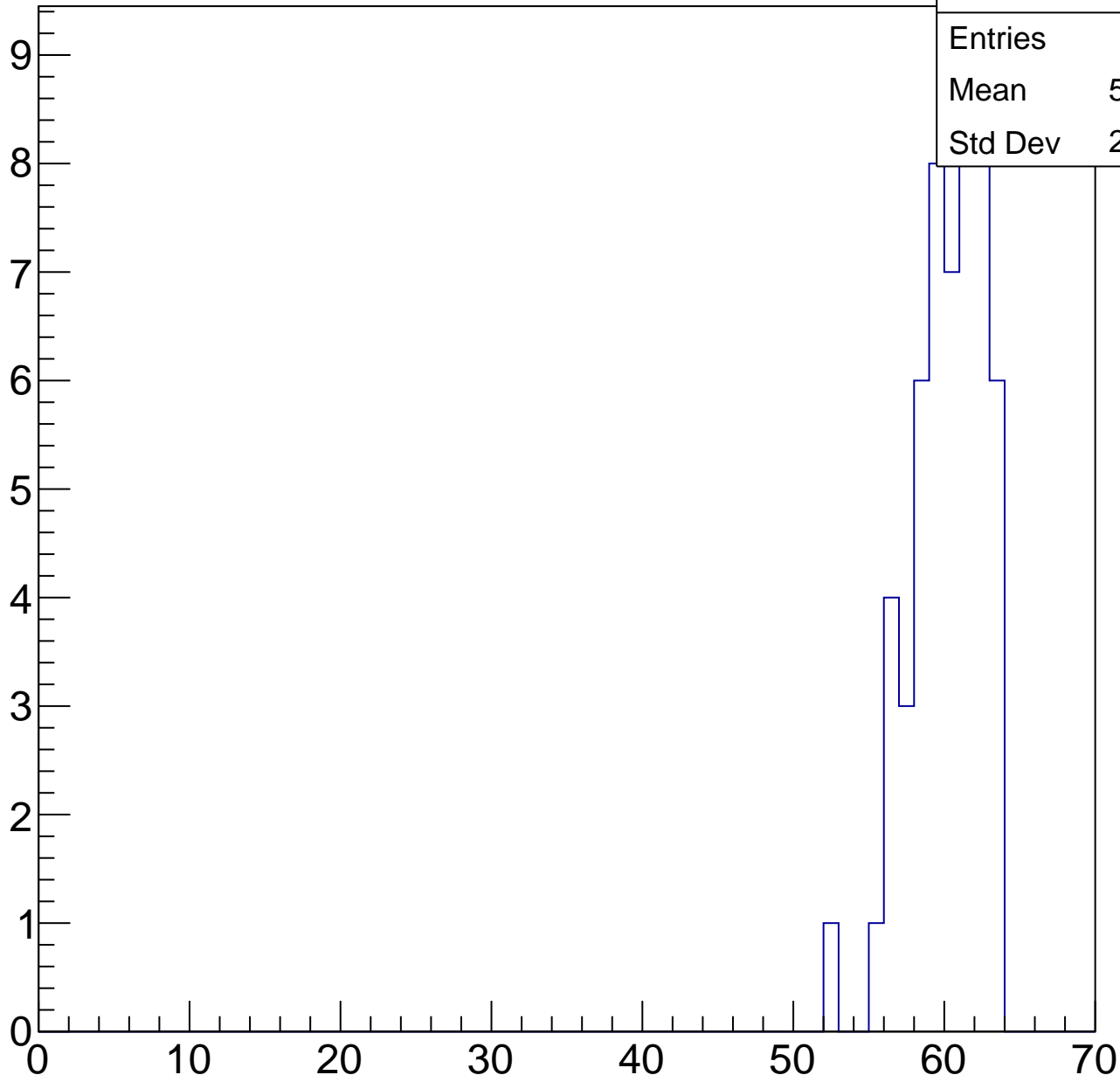
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	59.74
Std Dev	2.404

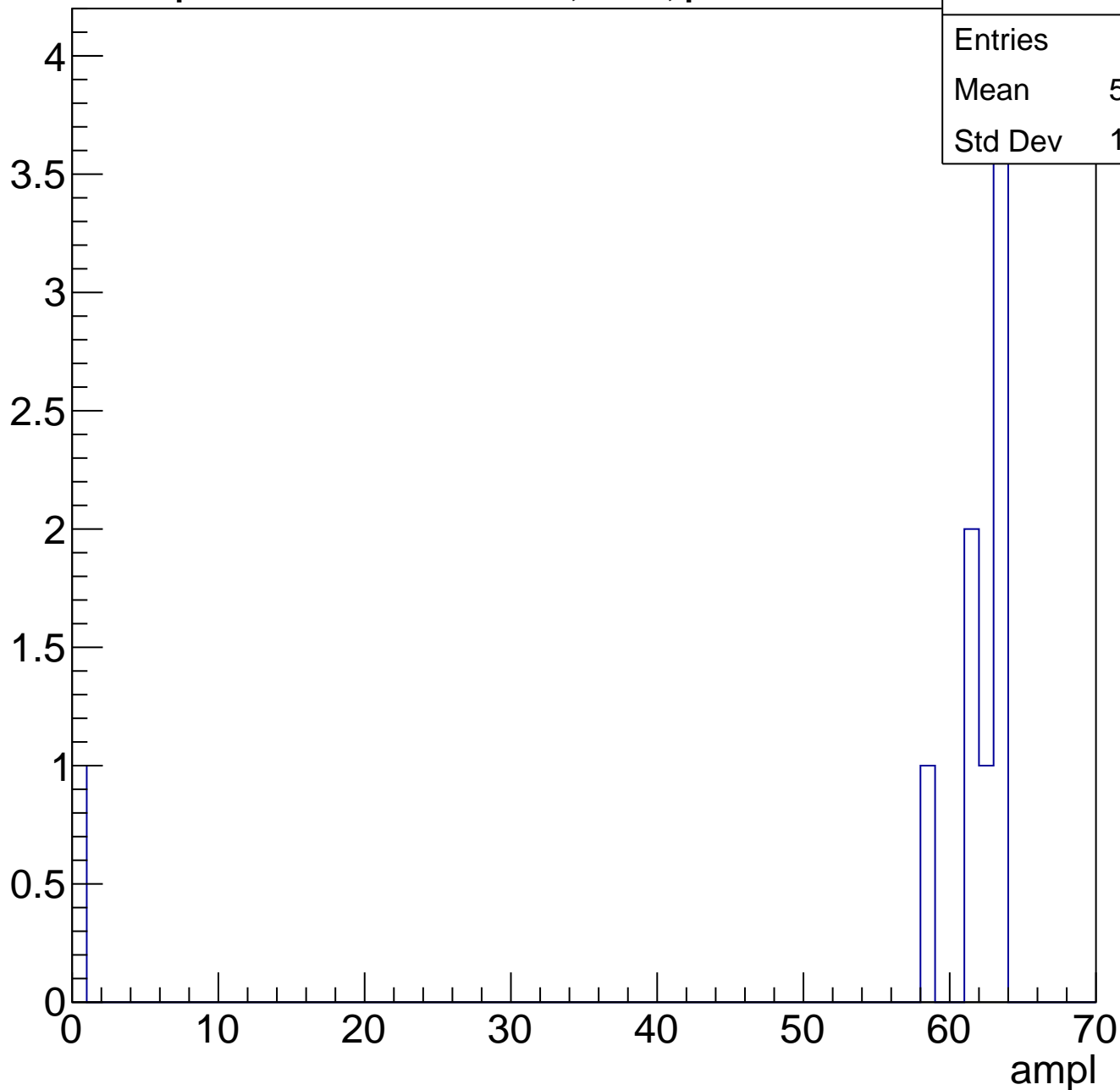
ampl



# B1L000S, U21-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch96, adc0

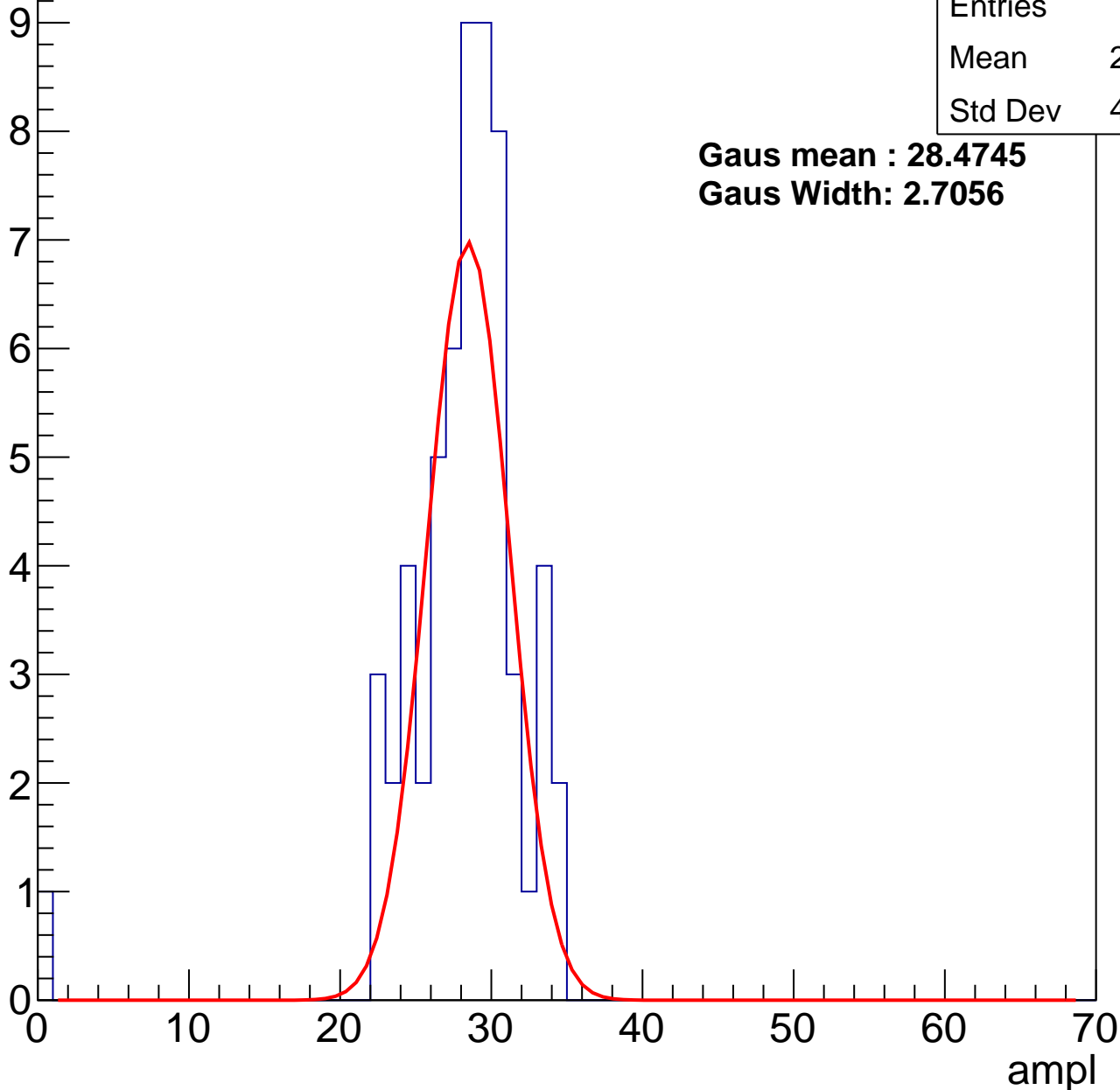
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.59
Std Dev	4.687

**Gaus mean : 28.4745**

**Gaus Width: 2.7056**



# B1L000S, U21-ch96, adc1

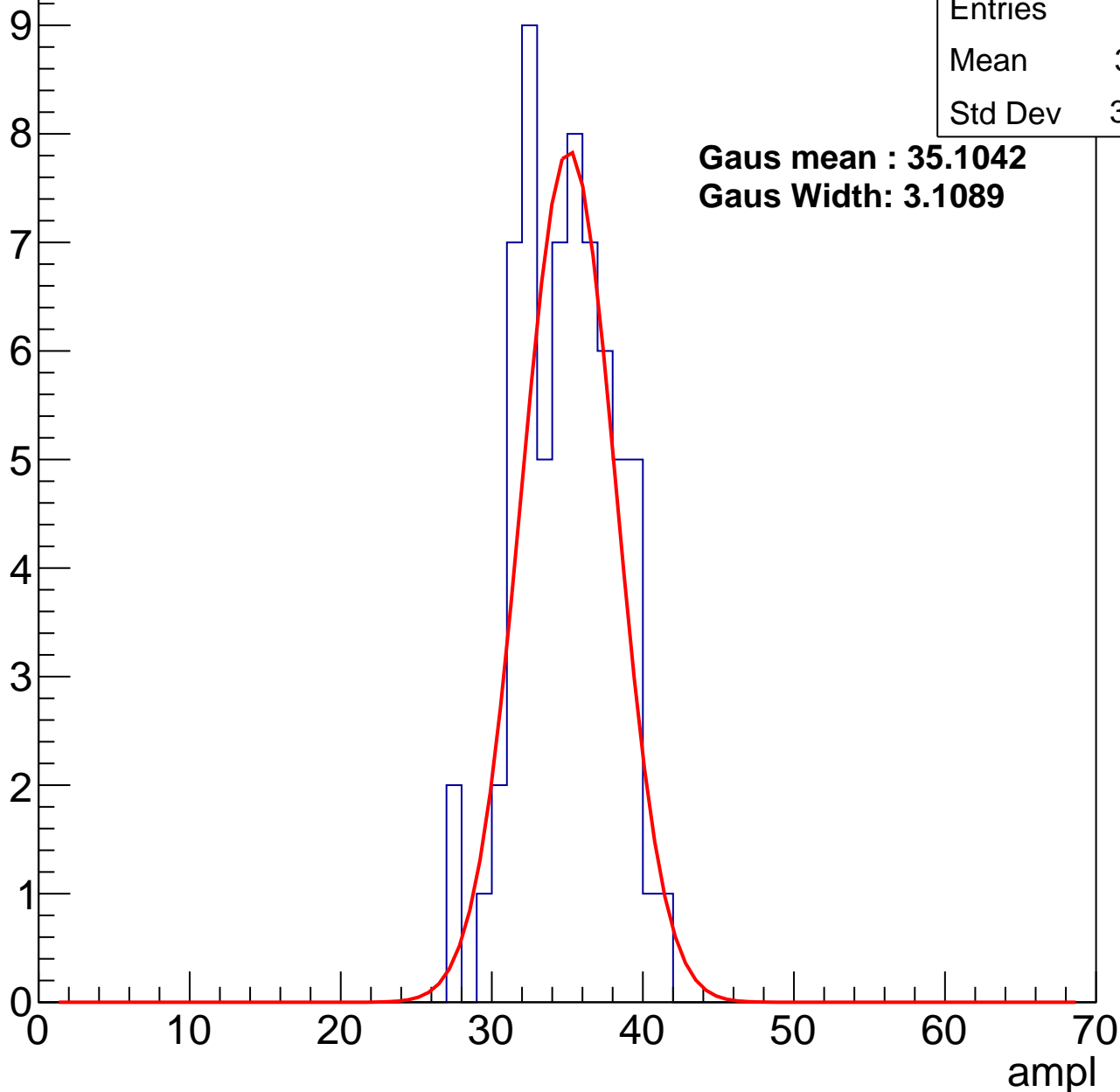
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.41
Std Dev	3.085

**Gaus mean : 35.1042**

**Gaus Width: 3.1089**



# B1L000S, U21-ch96, adc2

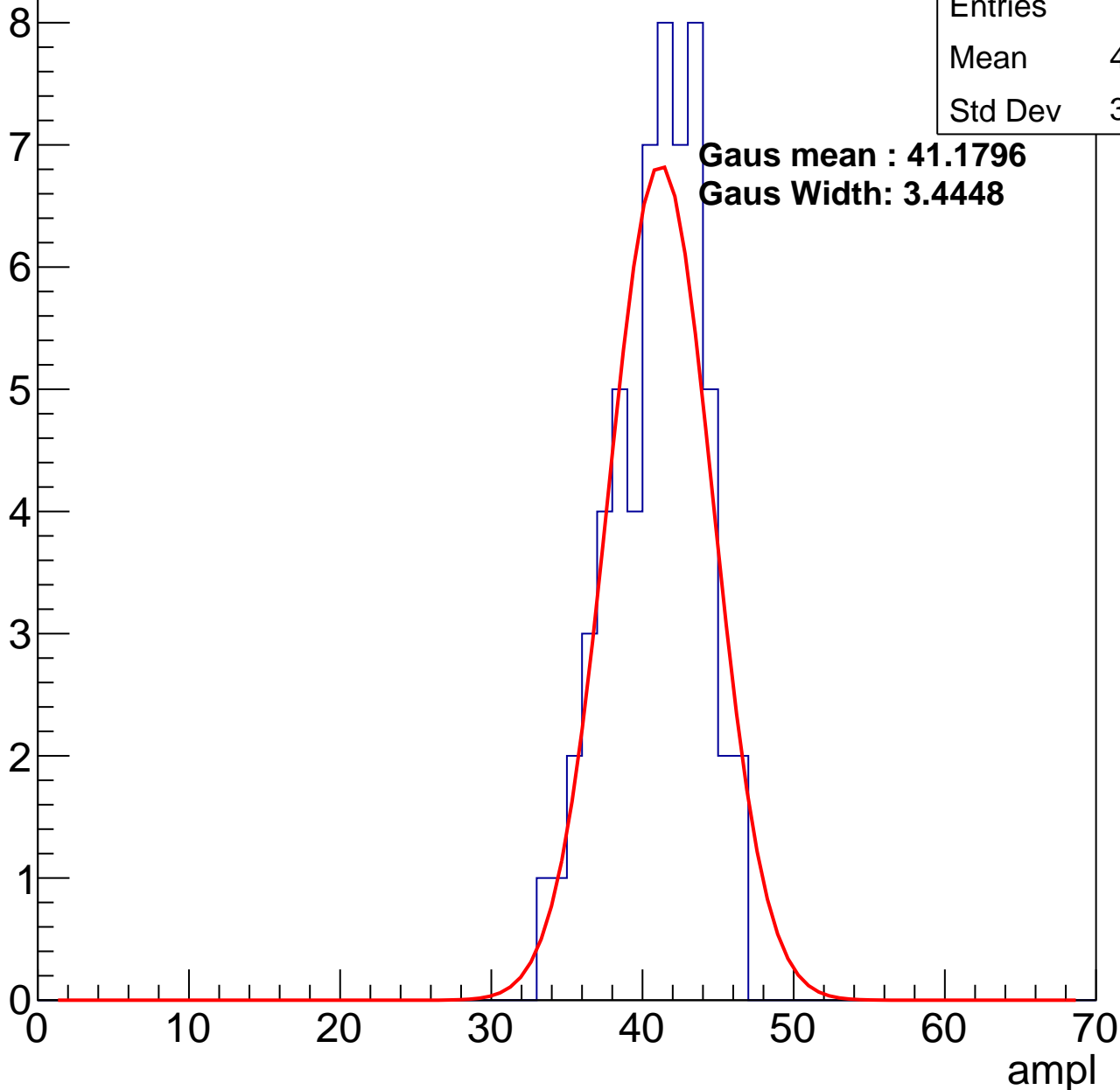
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	40.46
Std Dev	3.027

**Gaus mean : 41.1796**

**Gaus Width: 3.4448**



# B1L000S, U21-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	46.97
Std Dev	3.139

Entry

10

8

6

4

2

0

0

10

20

30

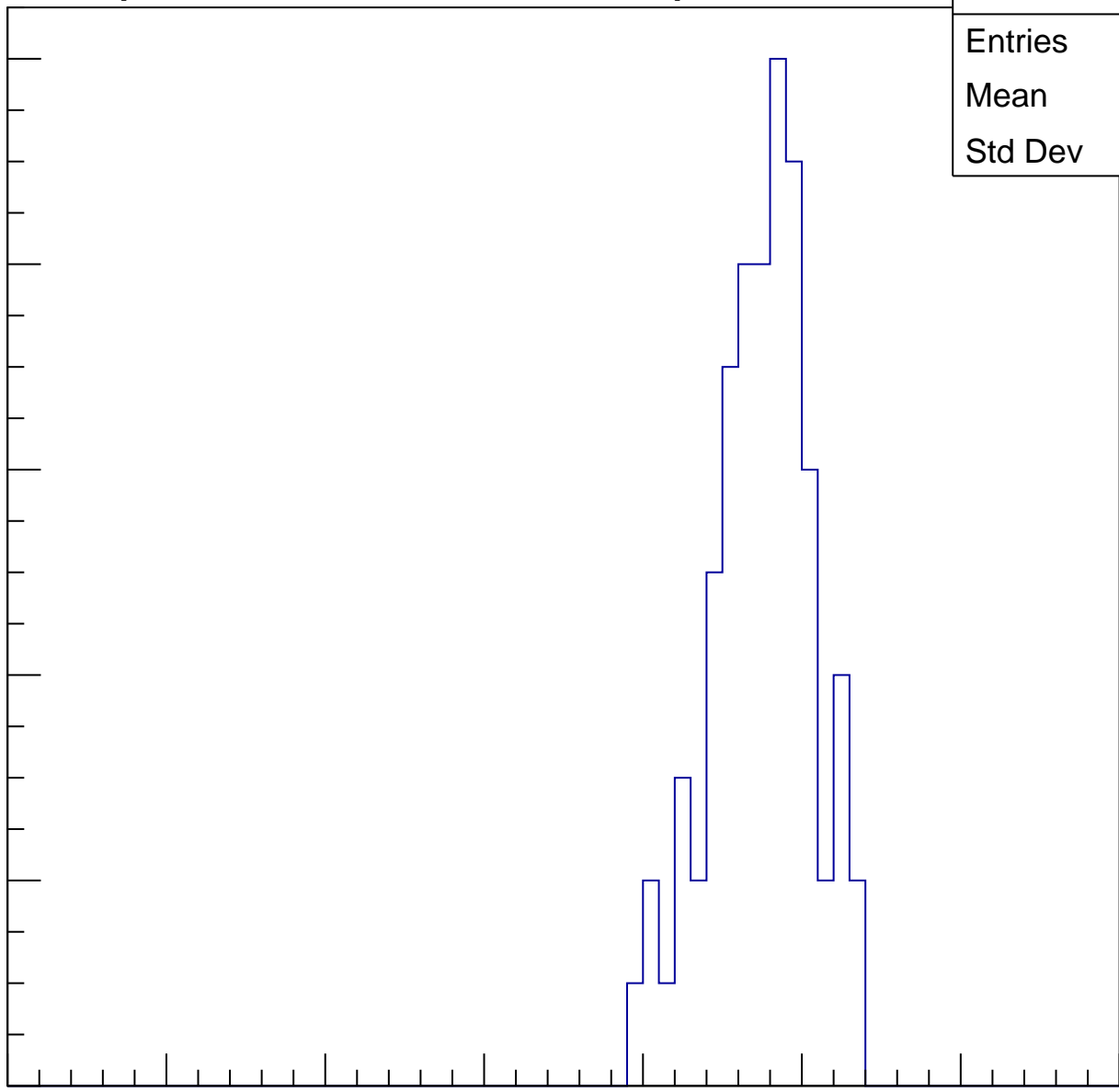
40

50

60

70

ampl

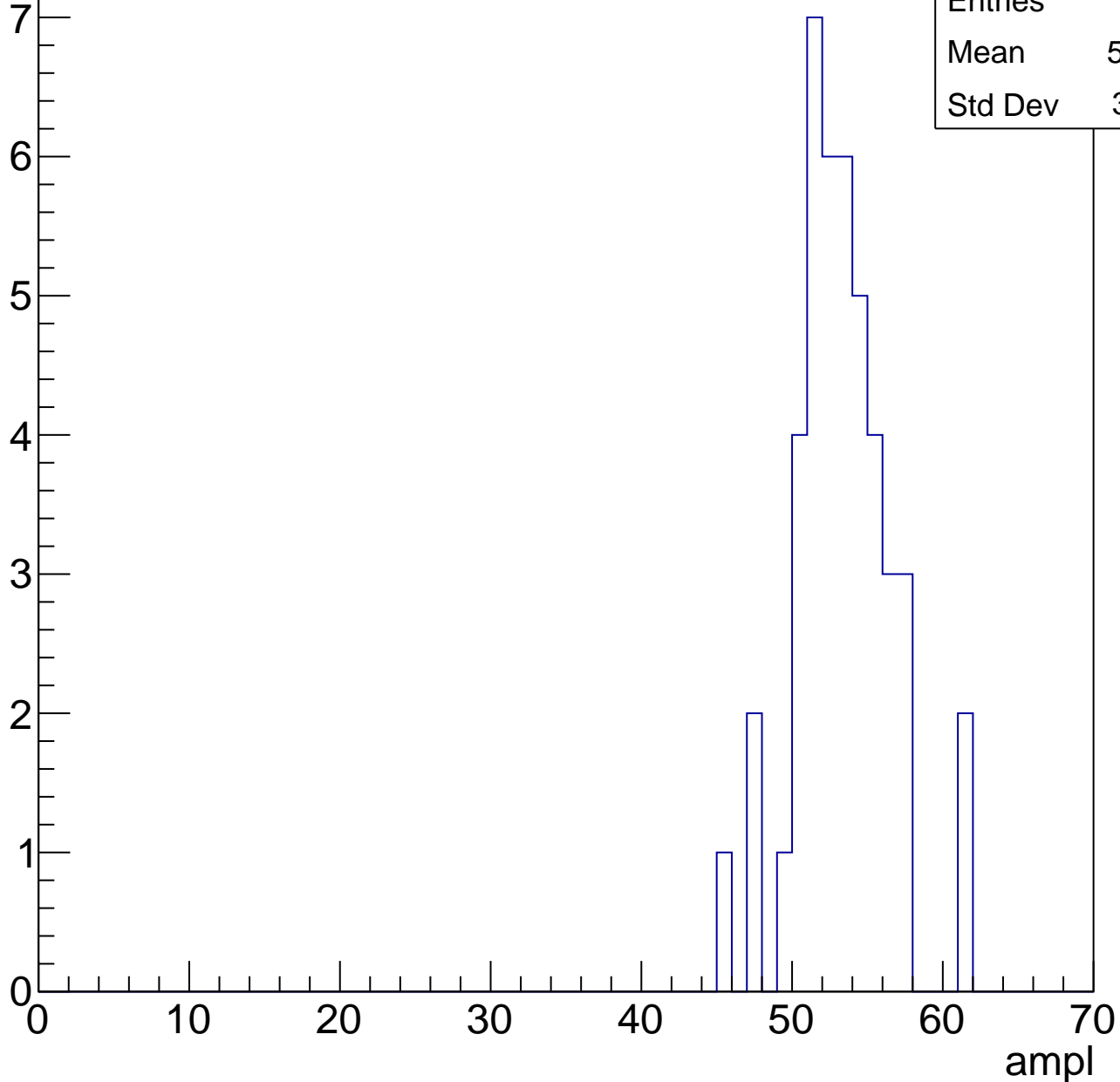


# B1L000S, U21-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	52.86
Std Dev	3.181

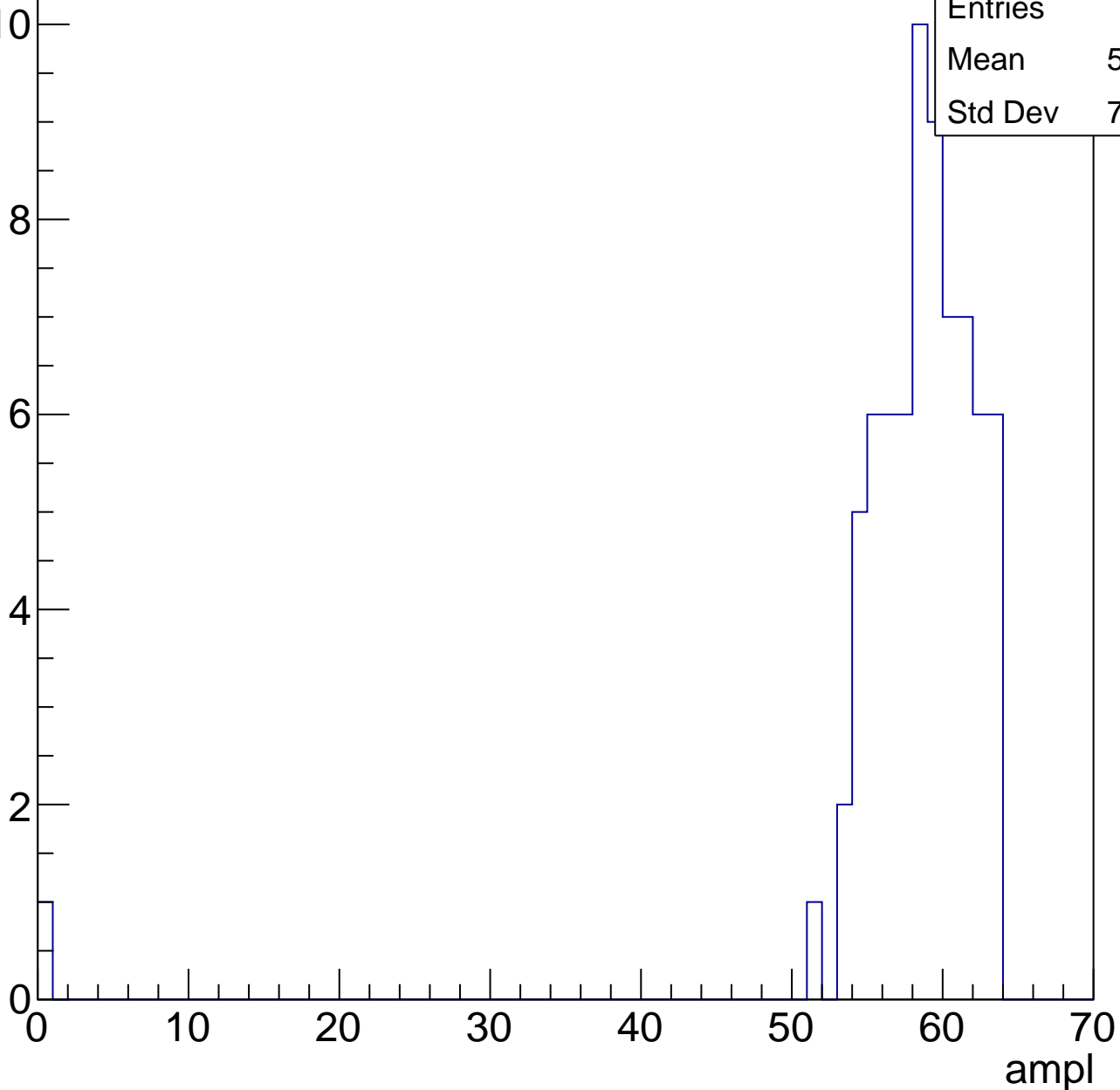


# B1L000S, U21-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

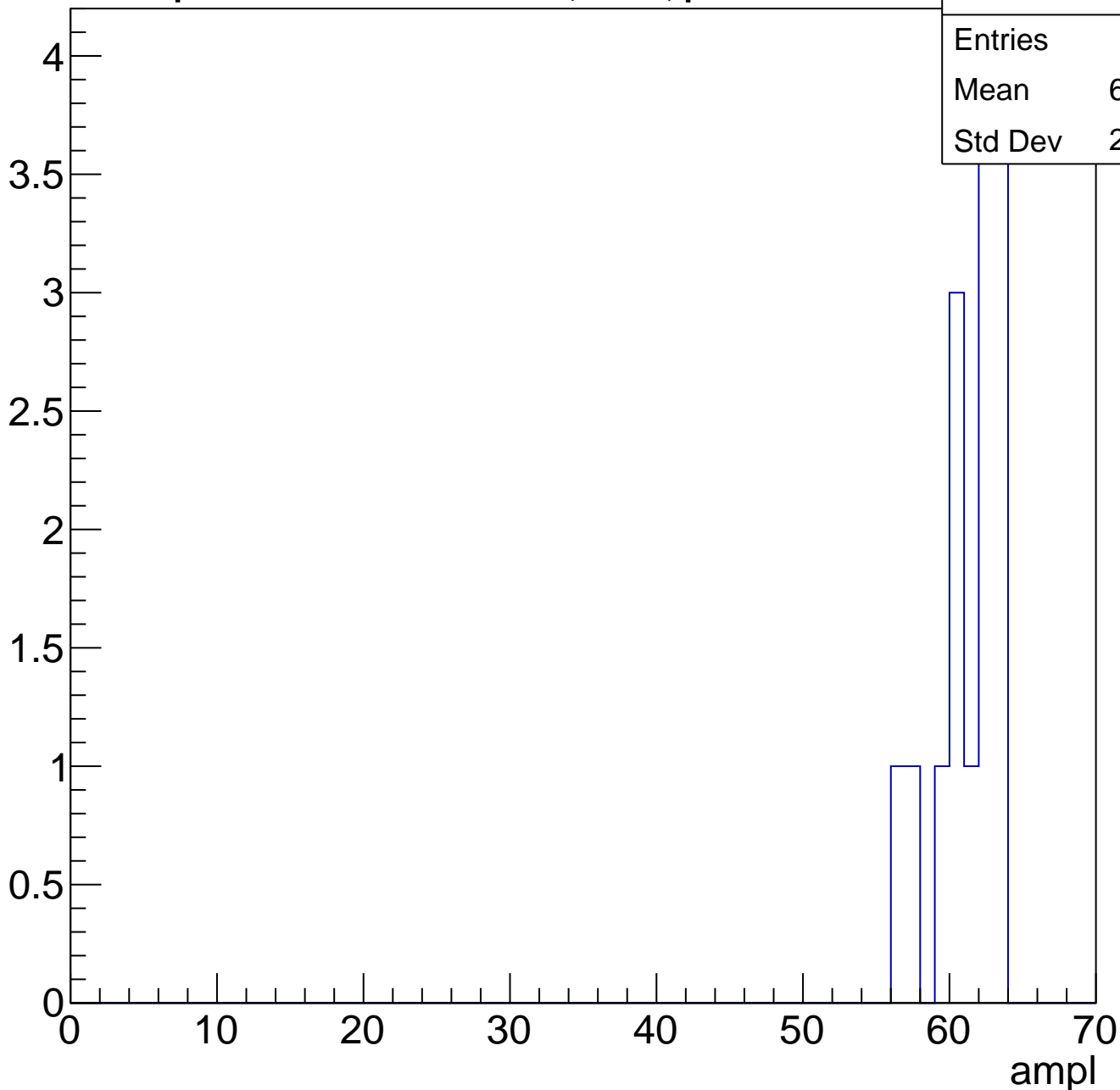
Entries	72
Mean	57.54
Std Dev	7.414



# B1L000S, U21-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

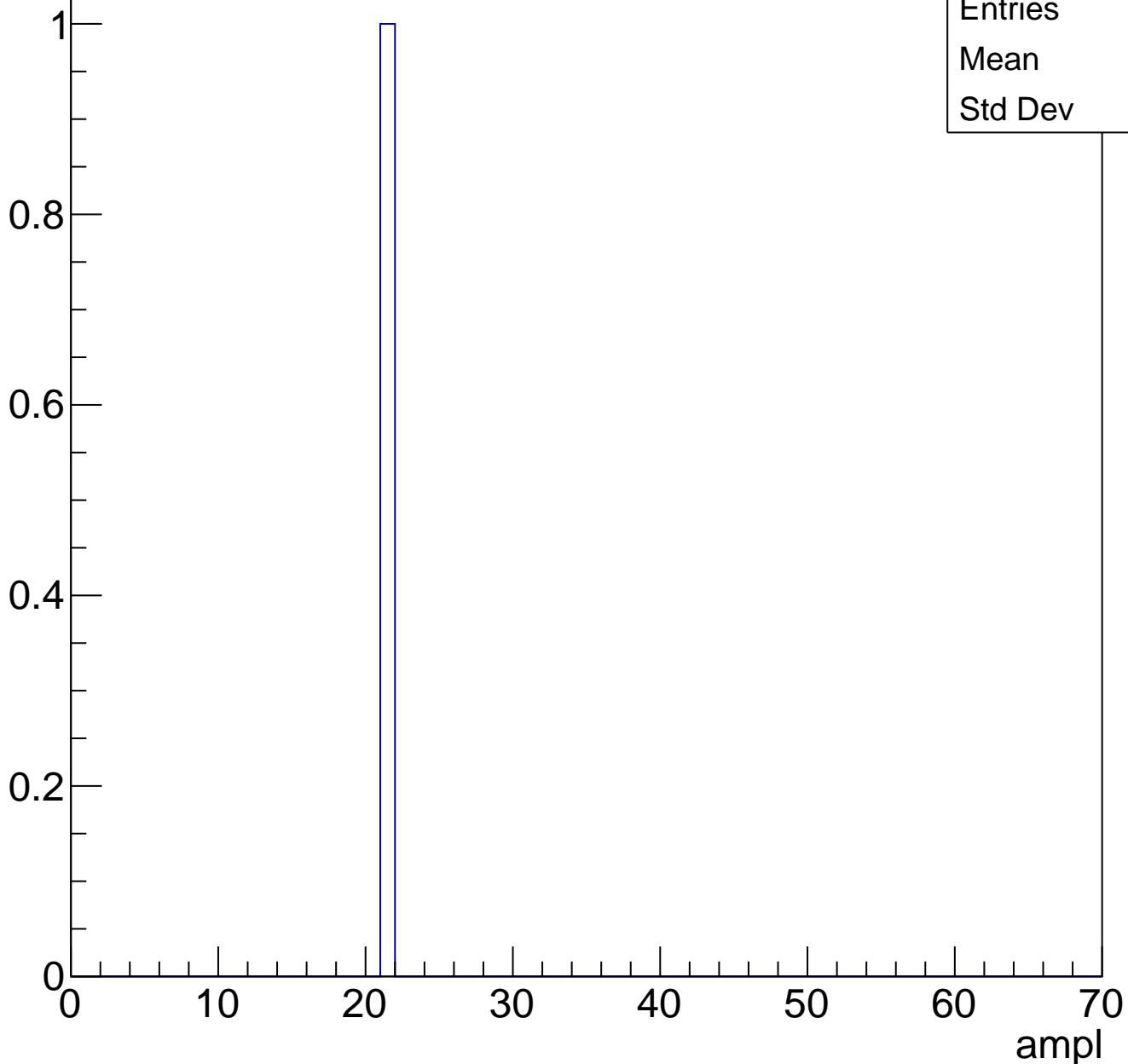




# B1L000S, U21-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	21
Std Dev	0

# B1L000S, U21-ch97, adc0

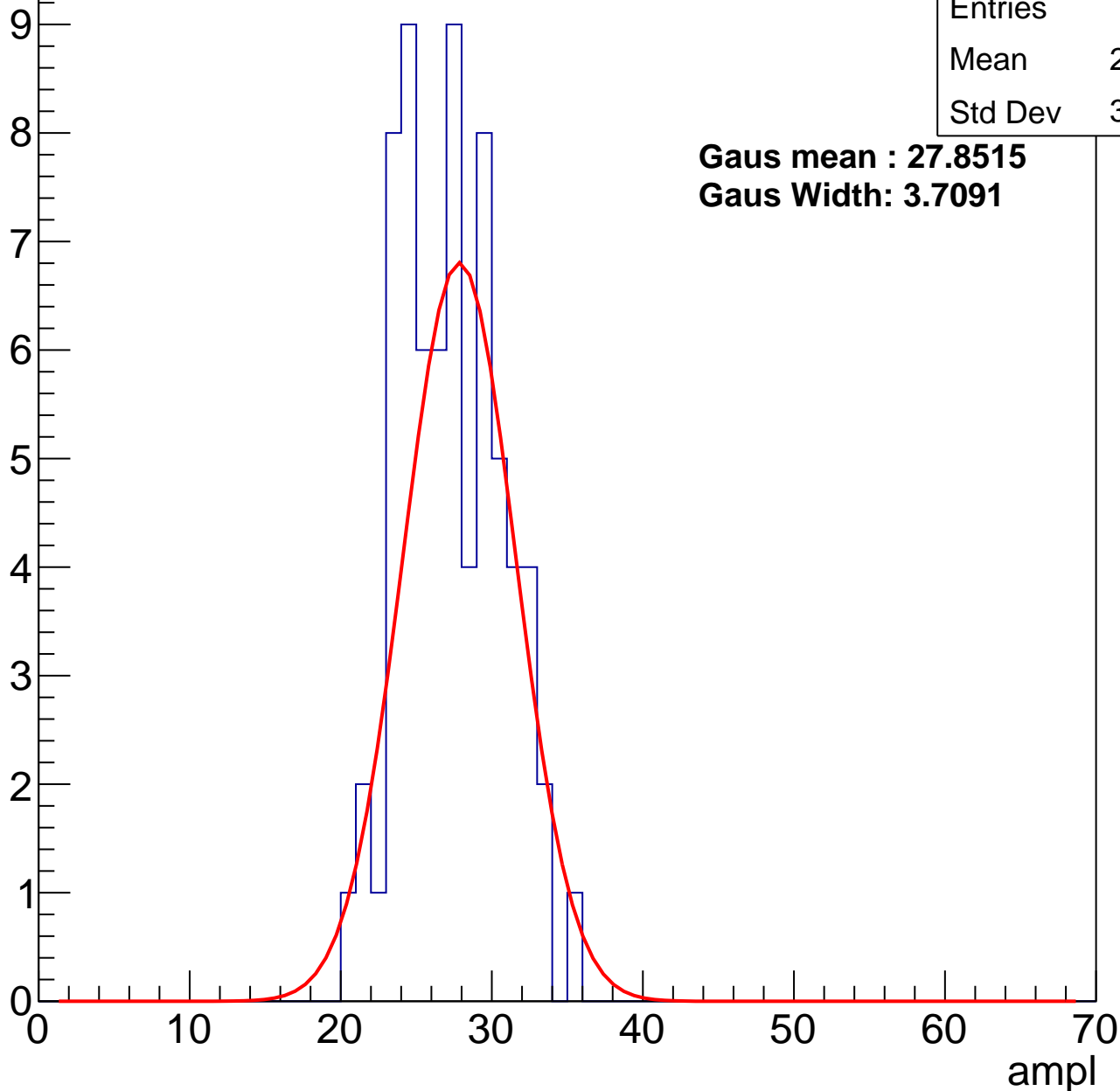
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	26.86
Std Dev	3.305

**Gaus mean : 27.8515**

**Gaus Width: 3.7091**



# B1L000S, U21-ch97, adc1

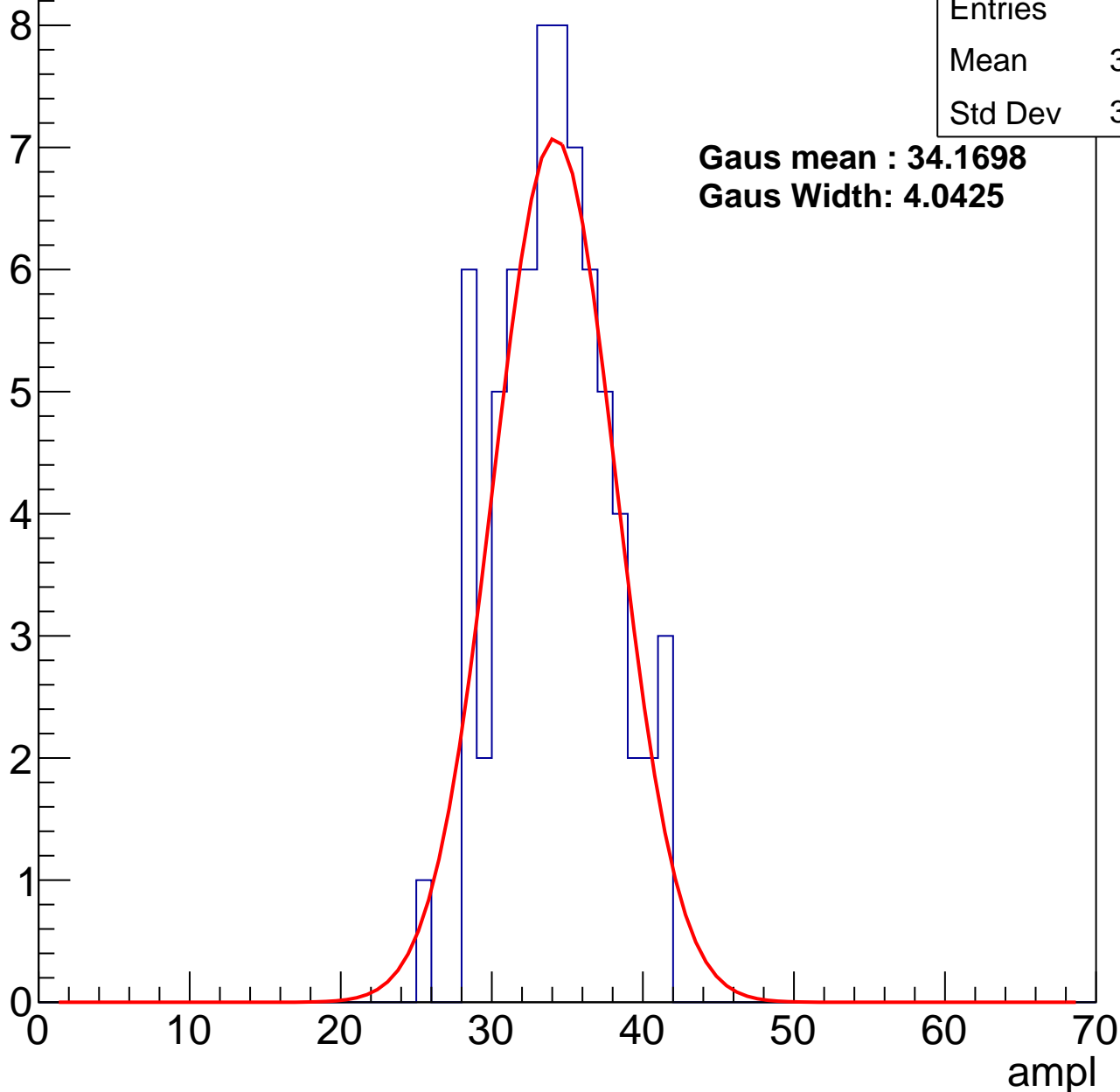
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	33.72
Std Dev	3.593

**Gaus mean : 34.1698**

**Gaus Width: 4.0425**



# B1L000S, U21-ch97, adc2

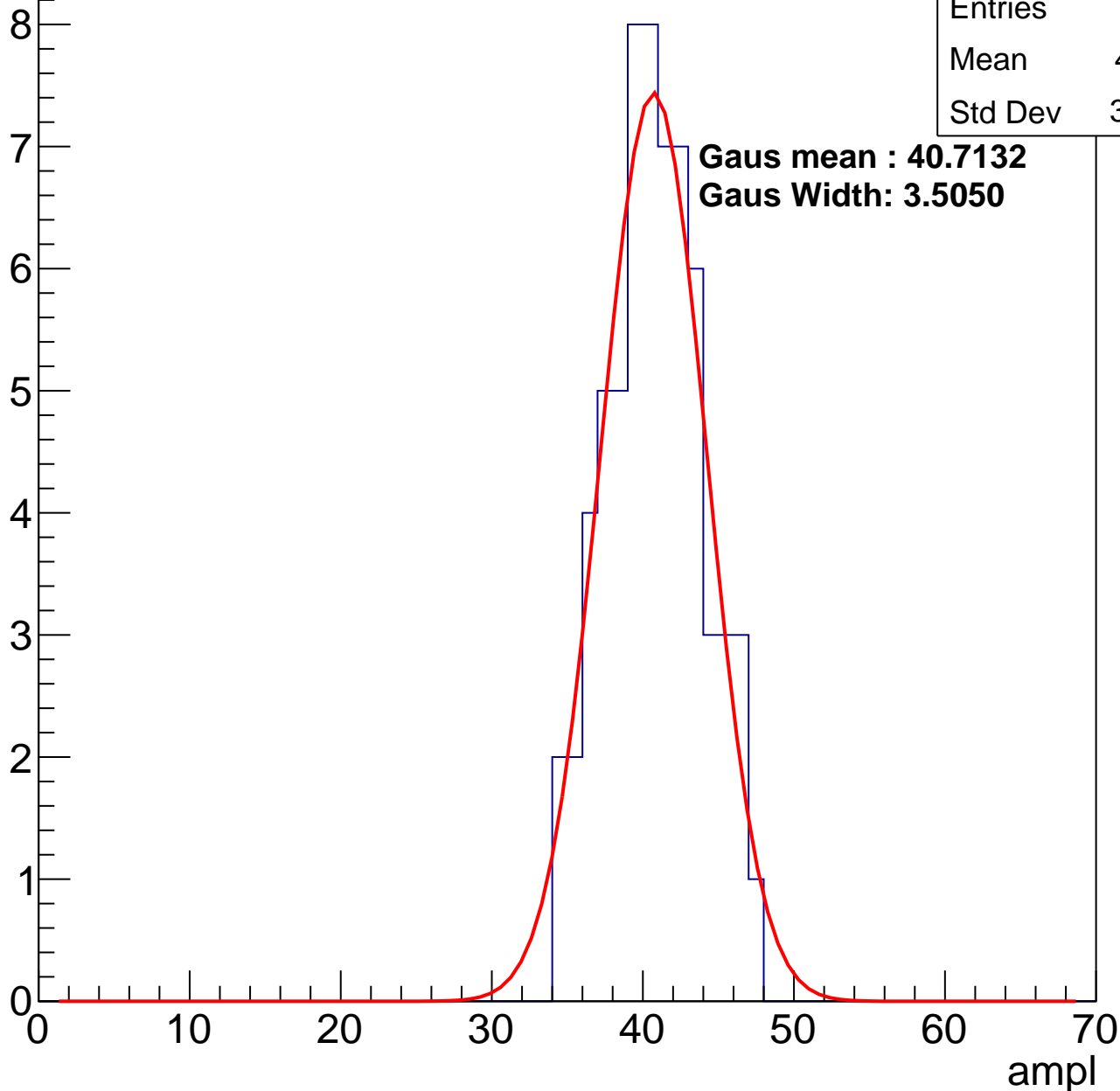
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	40.31
Std Dev	3.117

**Gaus mean : 40.7132**

**Gaus Width: 3.5050**

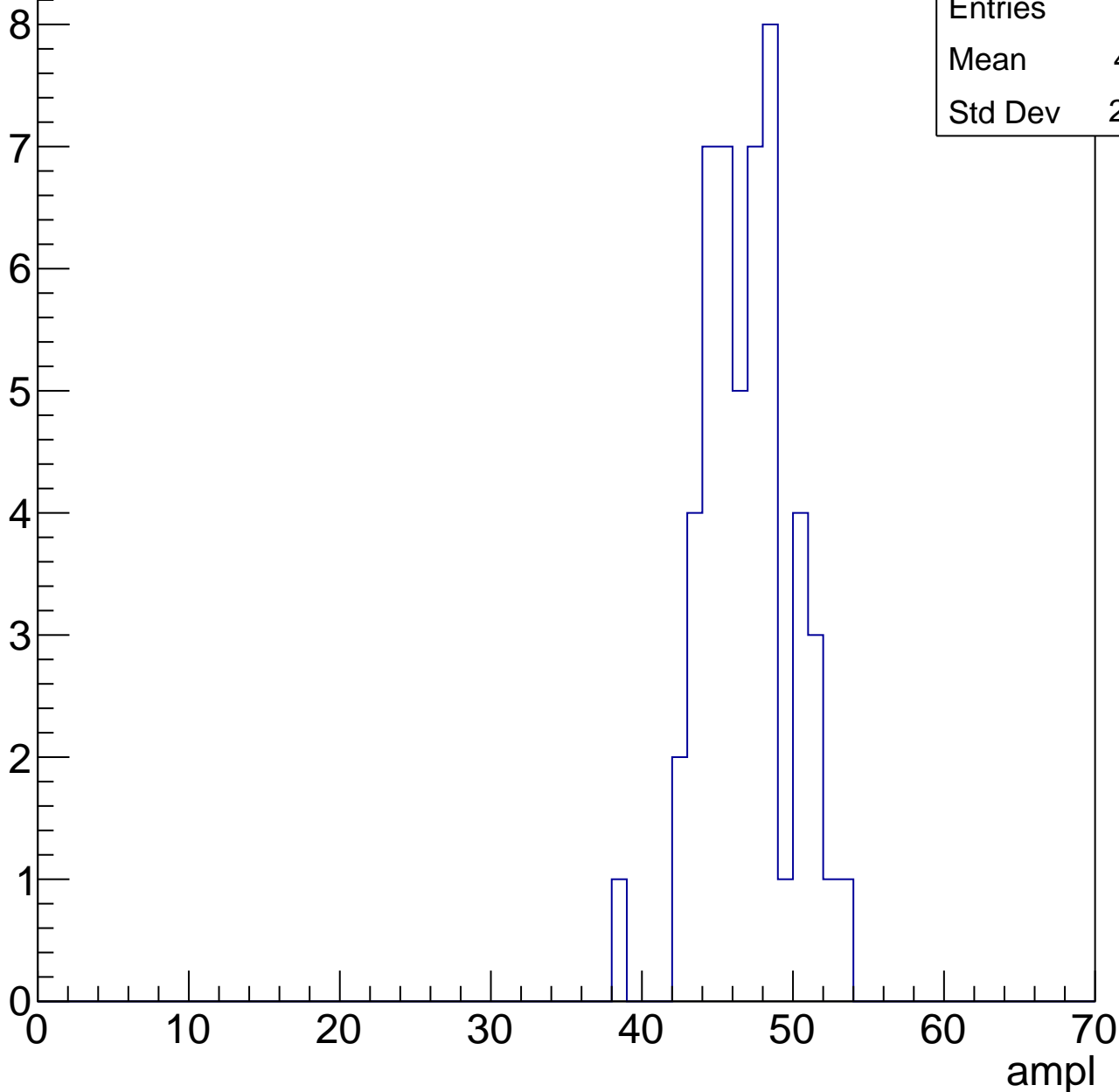


# B1L000S, U21-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	46.41
Std Dev	2.905

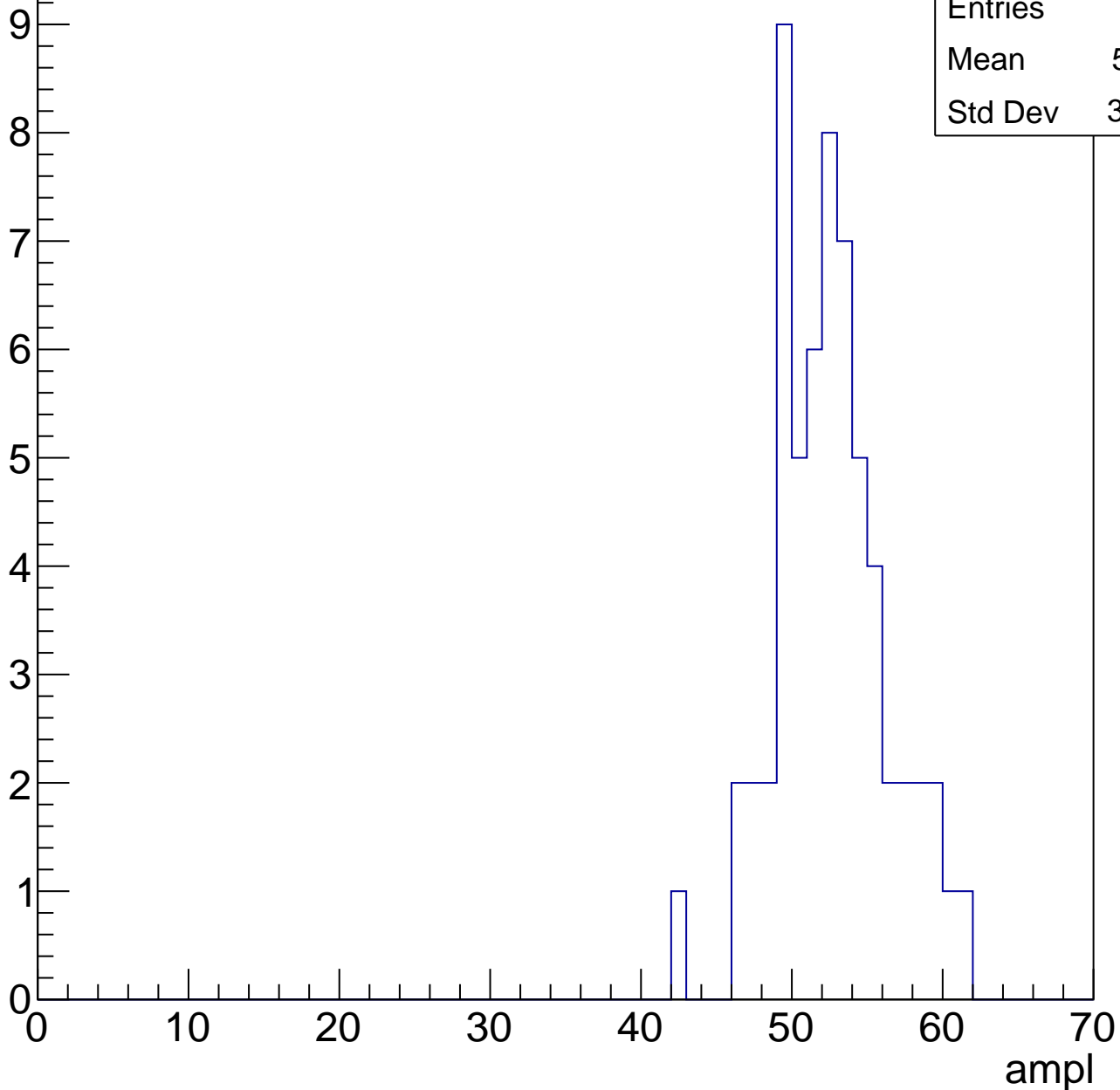


# B1L000S, U21-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	52.11
Std Dev	3.667

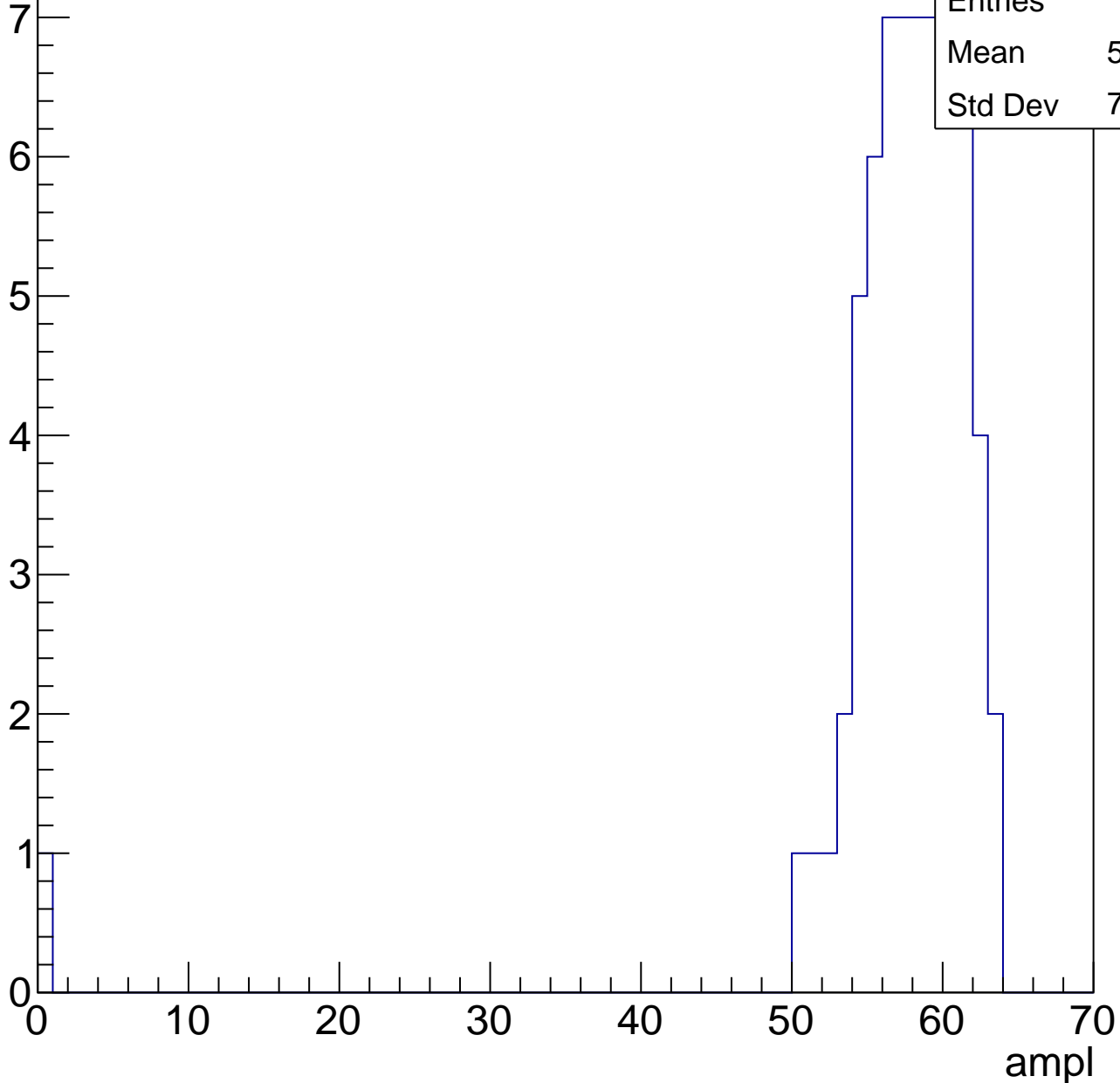


# B1L000S, U21-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	56.77
Std Dev	7.692

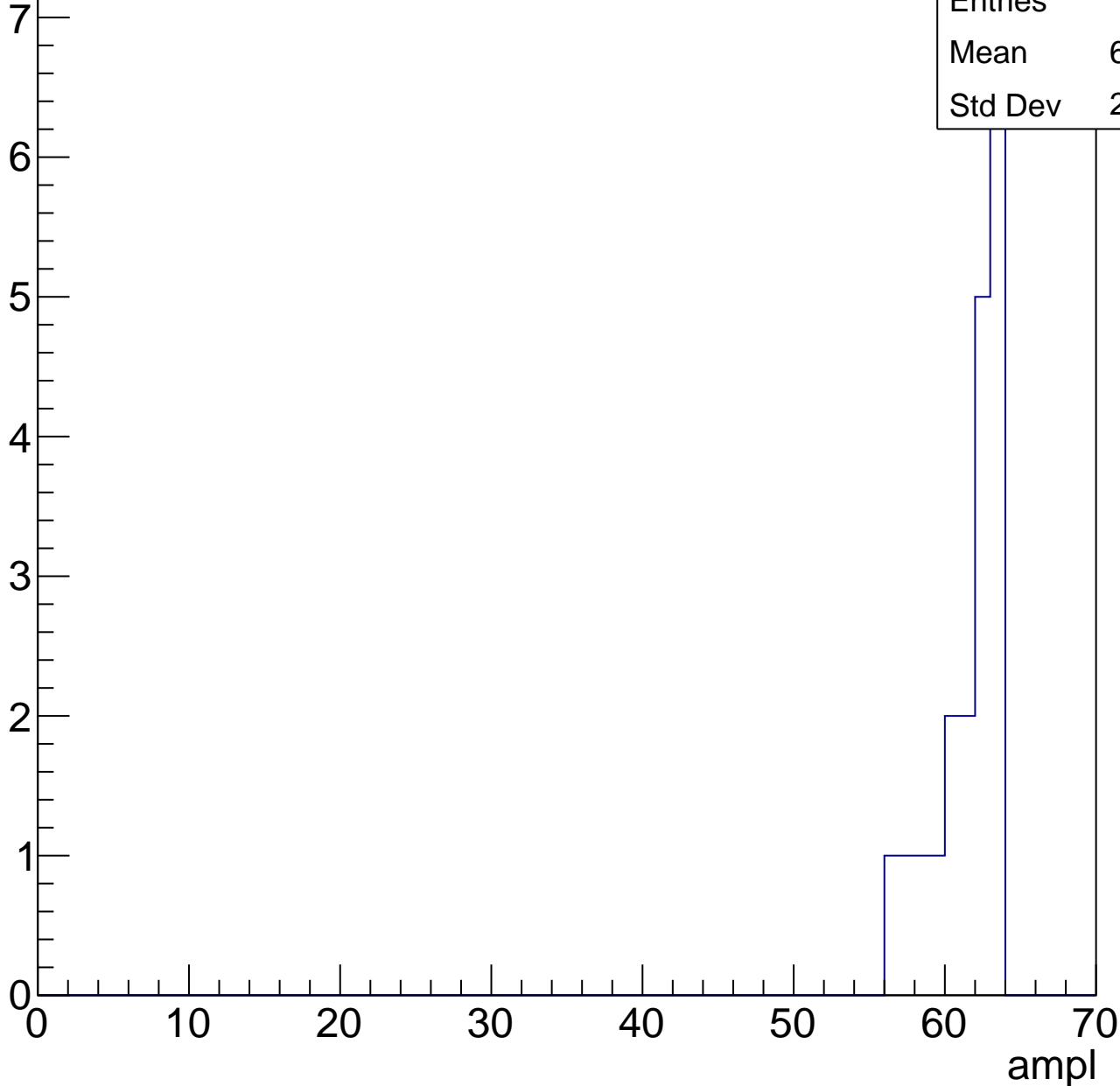


# B1L000S, U21-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	61.15
Std Dev	2.104





# B1L000S, U21-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	31.25
Std Dev	31.25

# B1L000S, U21-ch98, adc0

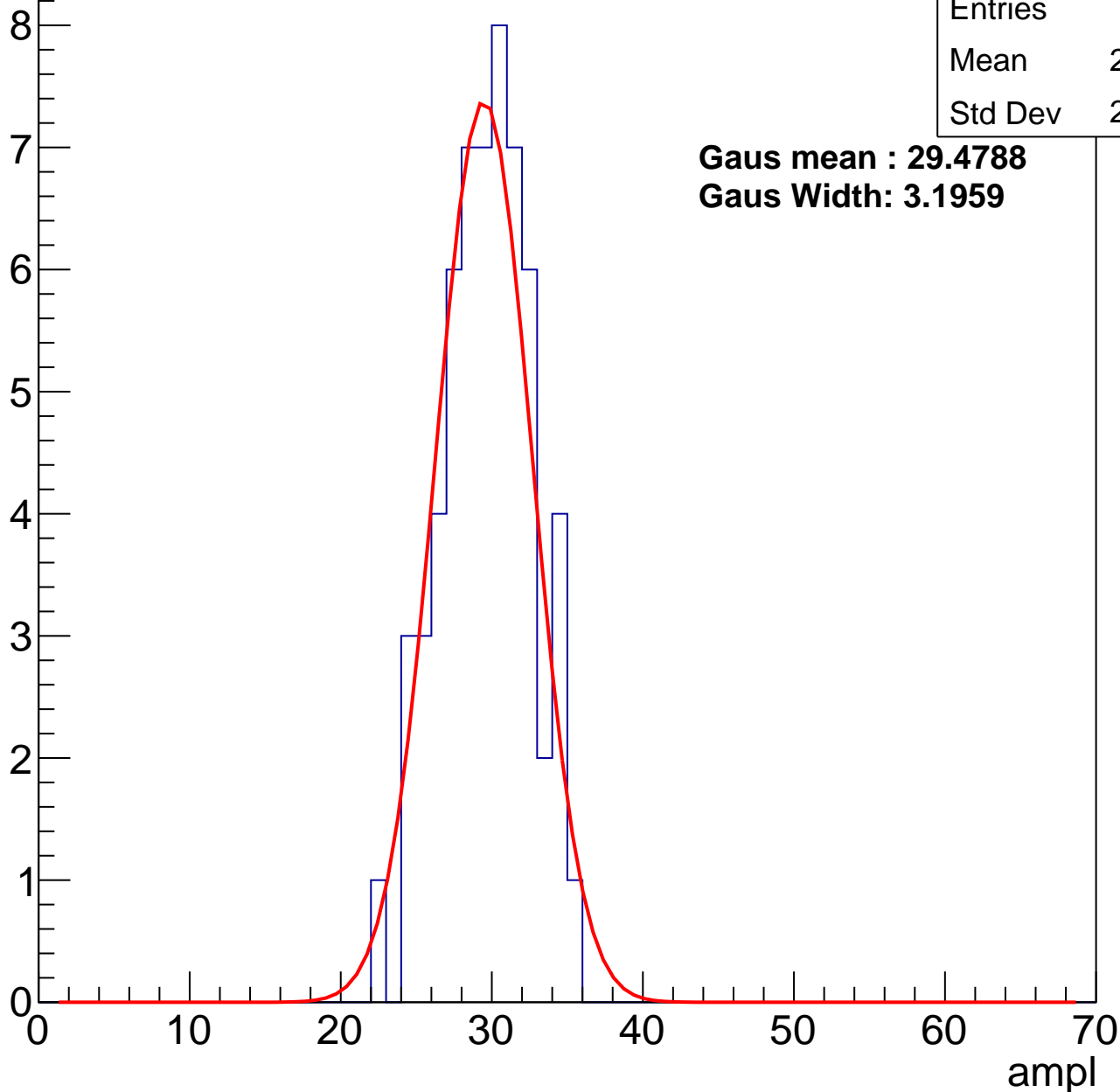
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	29.15
Std Dev	2.898

**Gaus mean : 29.4788**

**Gaus Width: 3.1959**



# B1L000S, U21-ch98, adc1

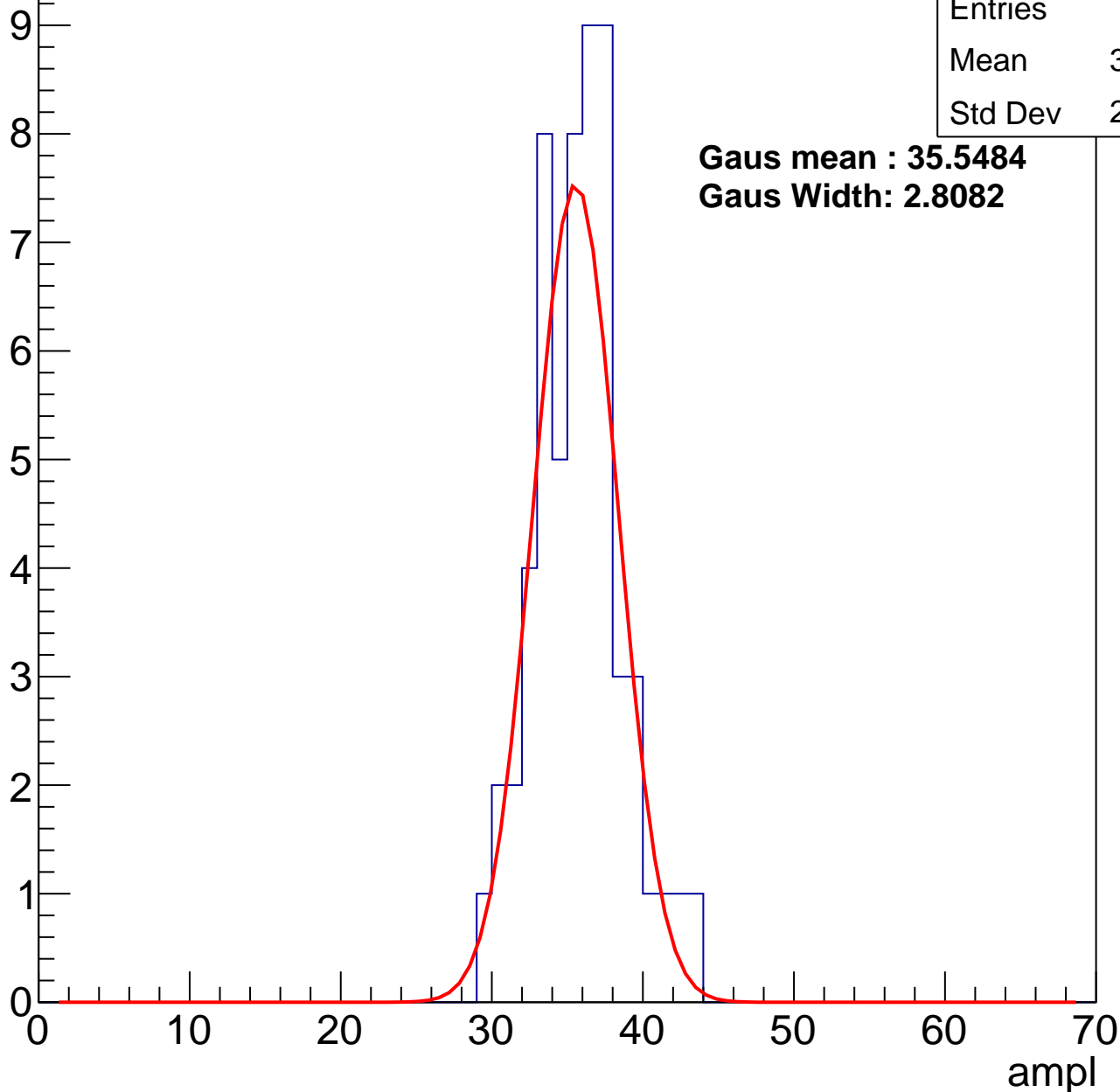
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.29
Std Dev	2.877

**Gaus mean : 35.5484**

**Gaus Width: 2.8082**



# B1L000S, U21-ch98, adc2

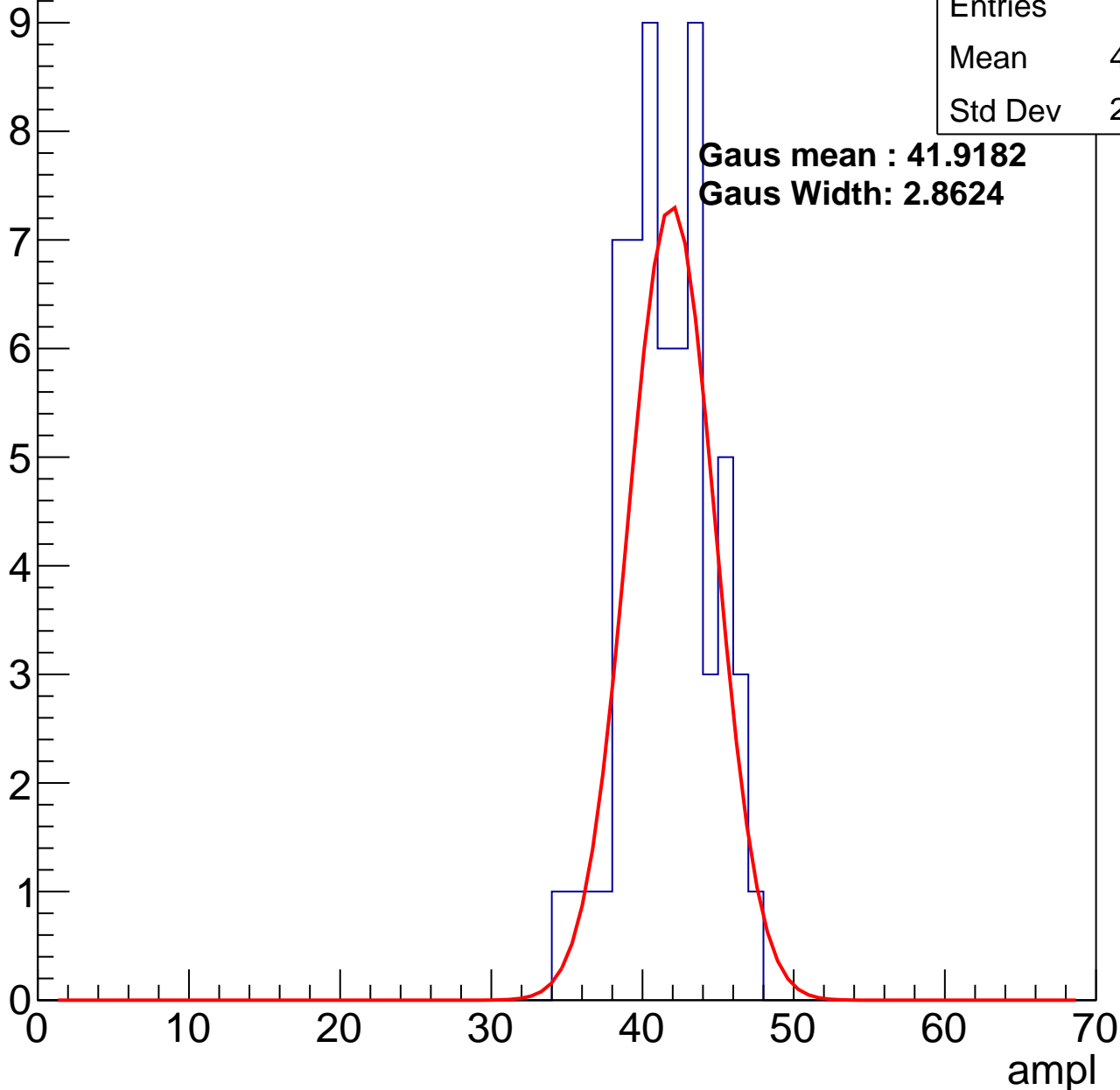
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	41.13
Std Dev	2.843

**Gaus mean : 41.9182**

**Gaus Width: 2.8624**



# B1L000S, U21-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	48
Std Dev	3.553

Entry

10

8

6

4

2

0

0

10

20

30

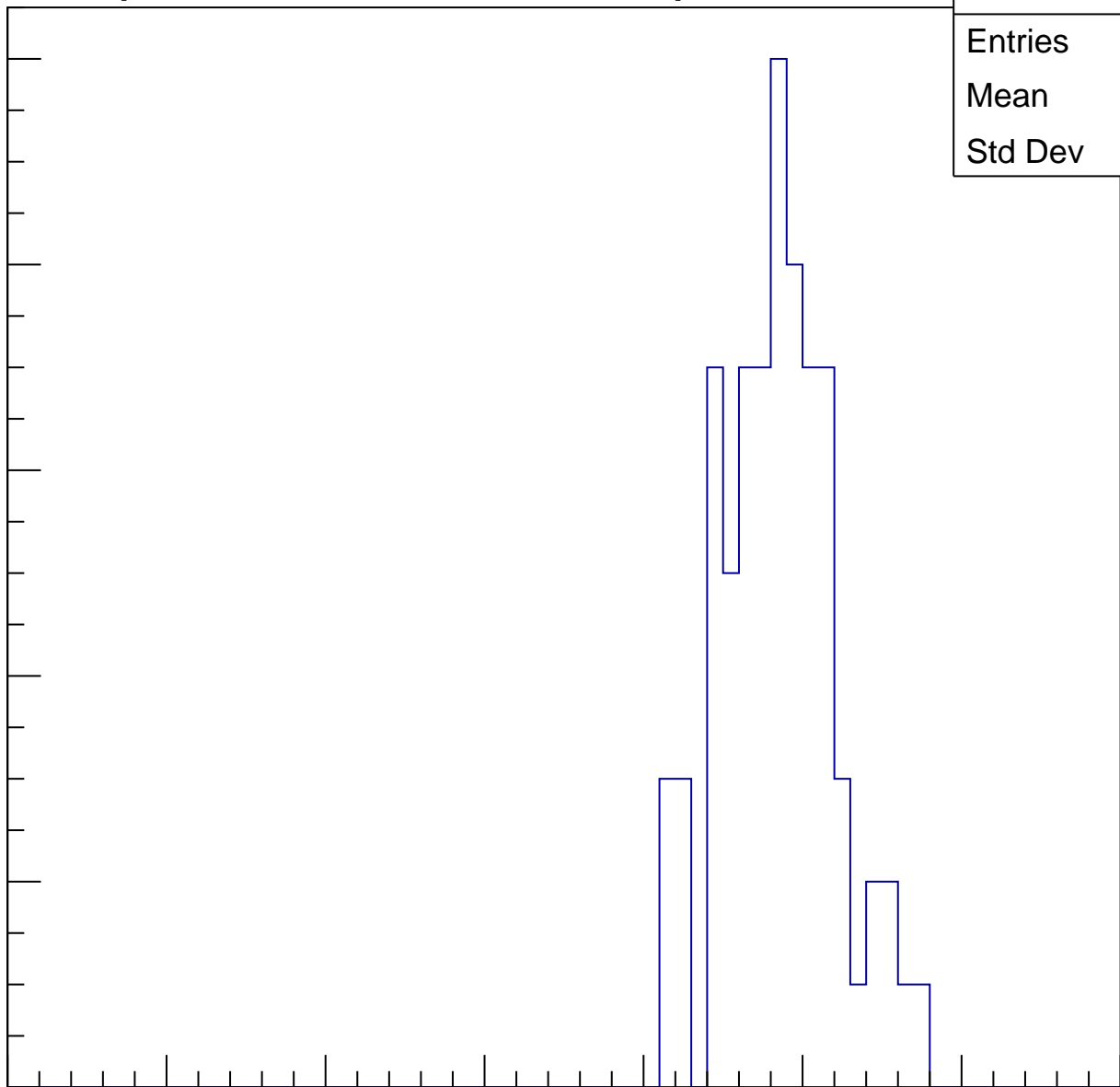
40

50

60

70

ampl

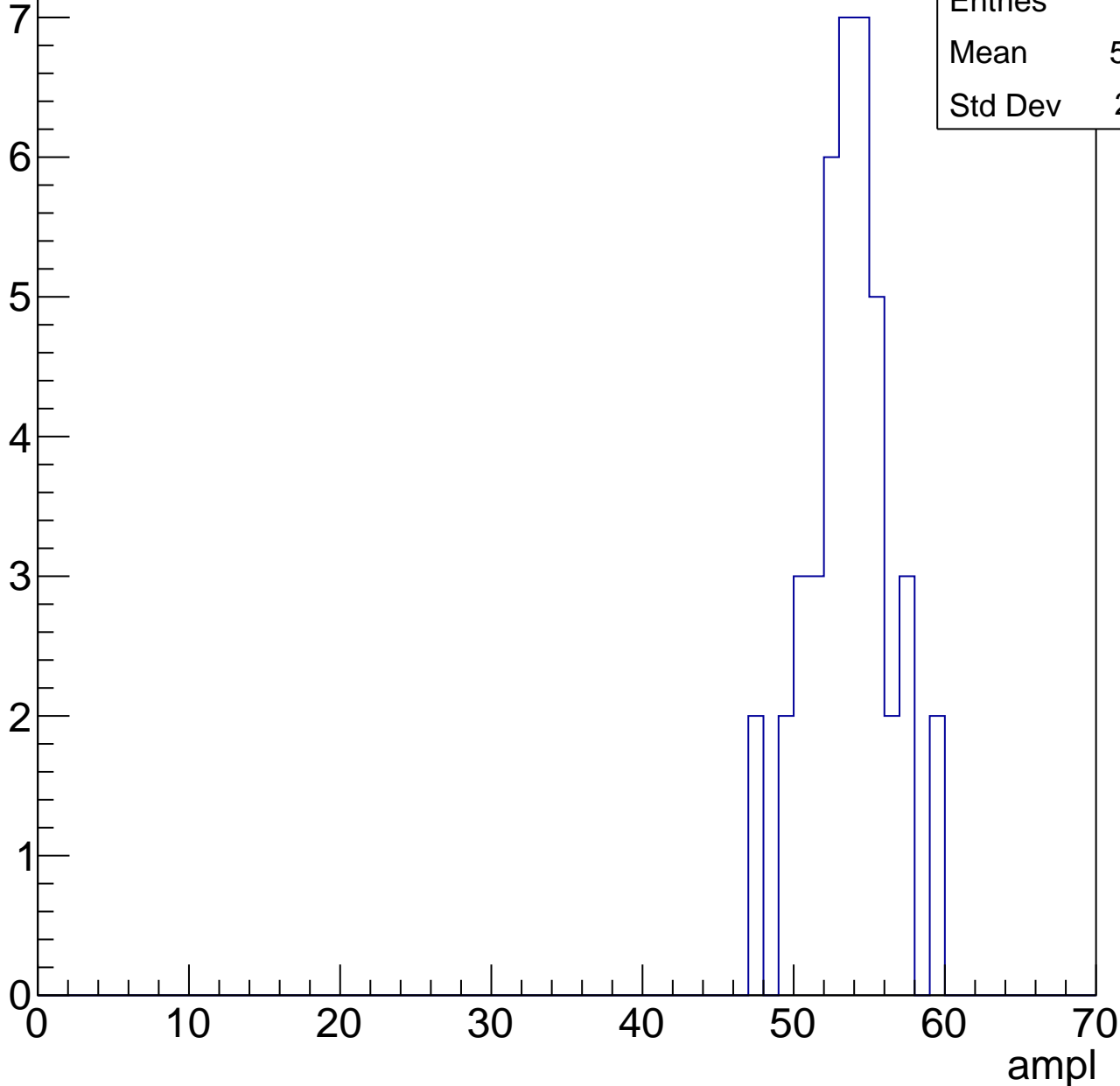


# B1L000S, U21-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	53.14
Std Dev	2.731

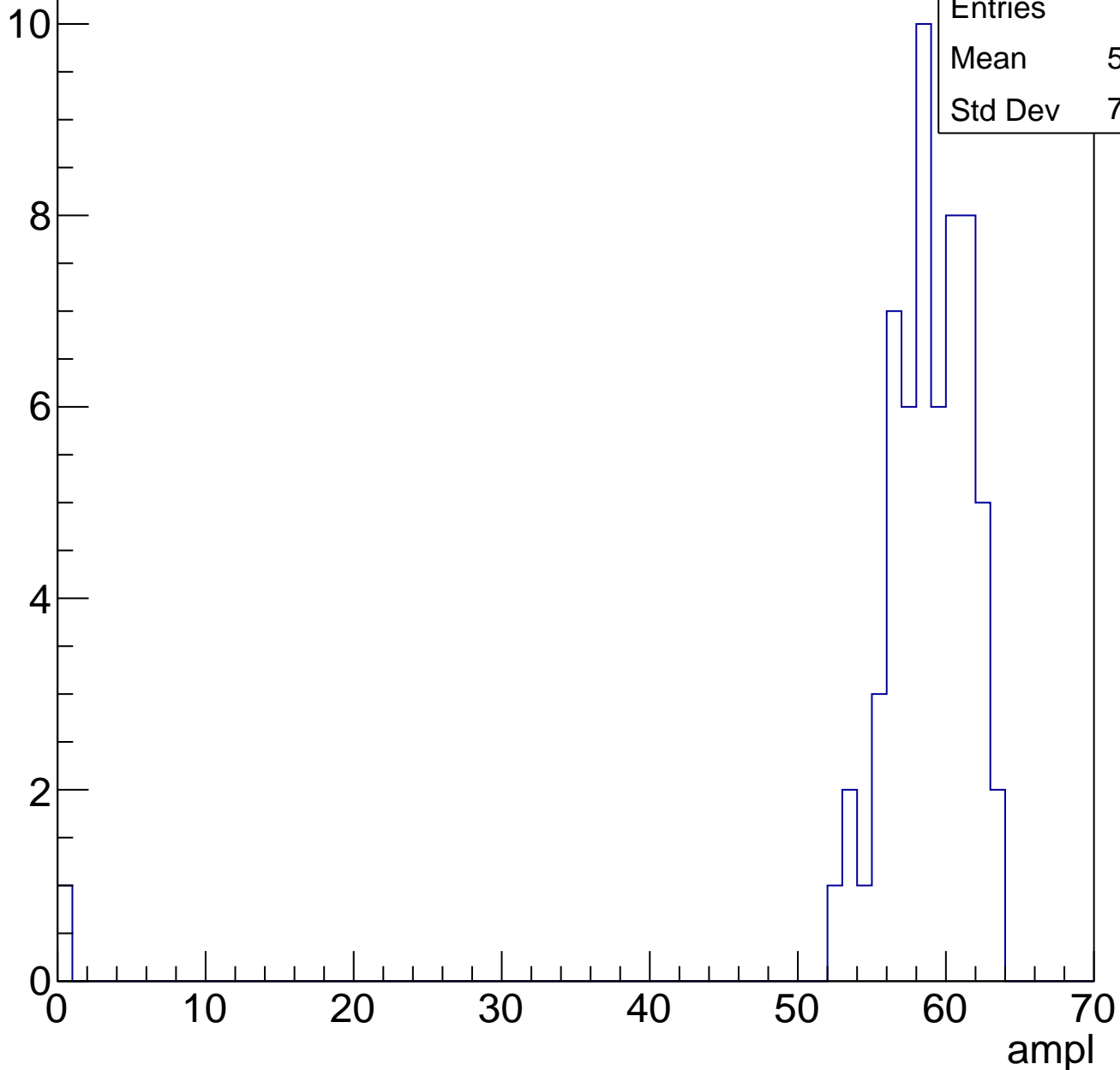


# B1L000S, U21-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	57.48
Std Dev	7.907

Entry

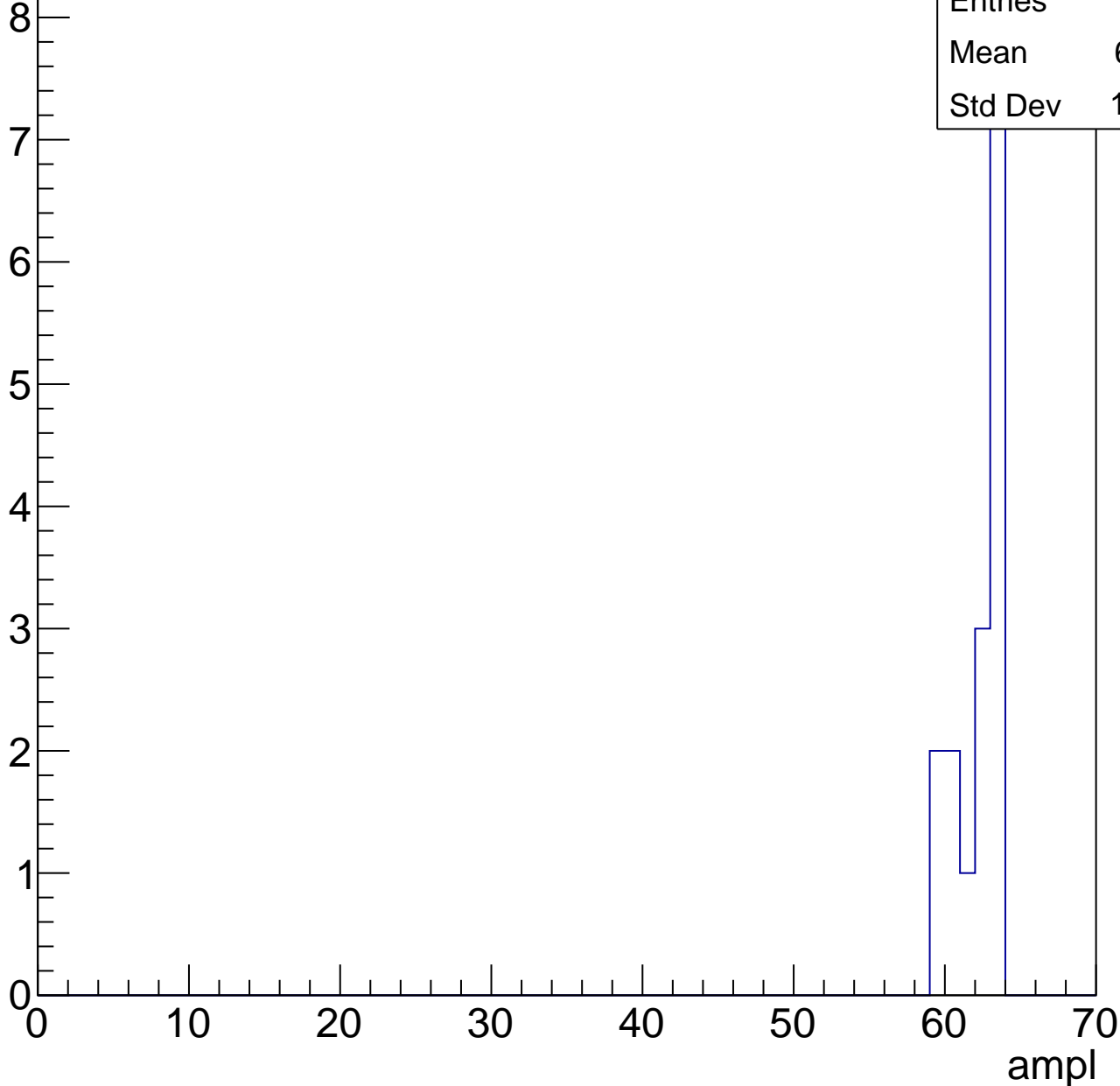


# B1L000S, U21-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.81
Std Dev	1.467

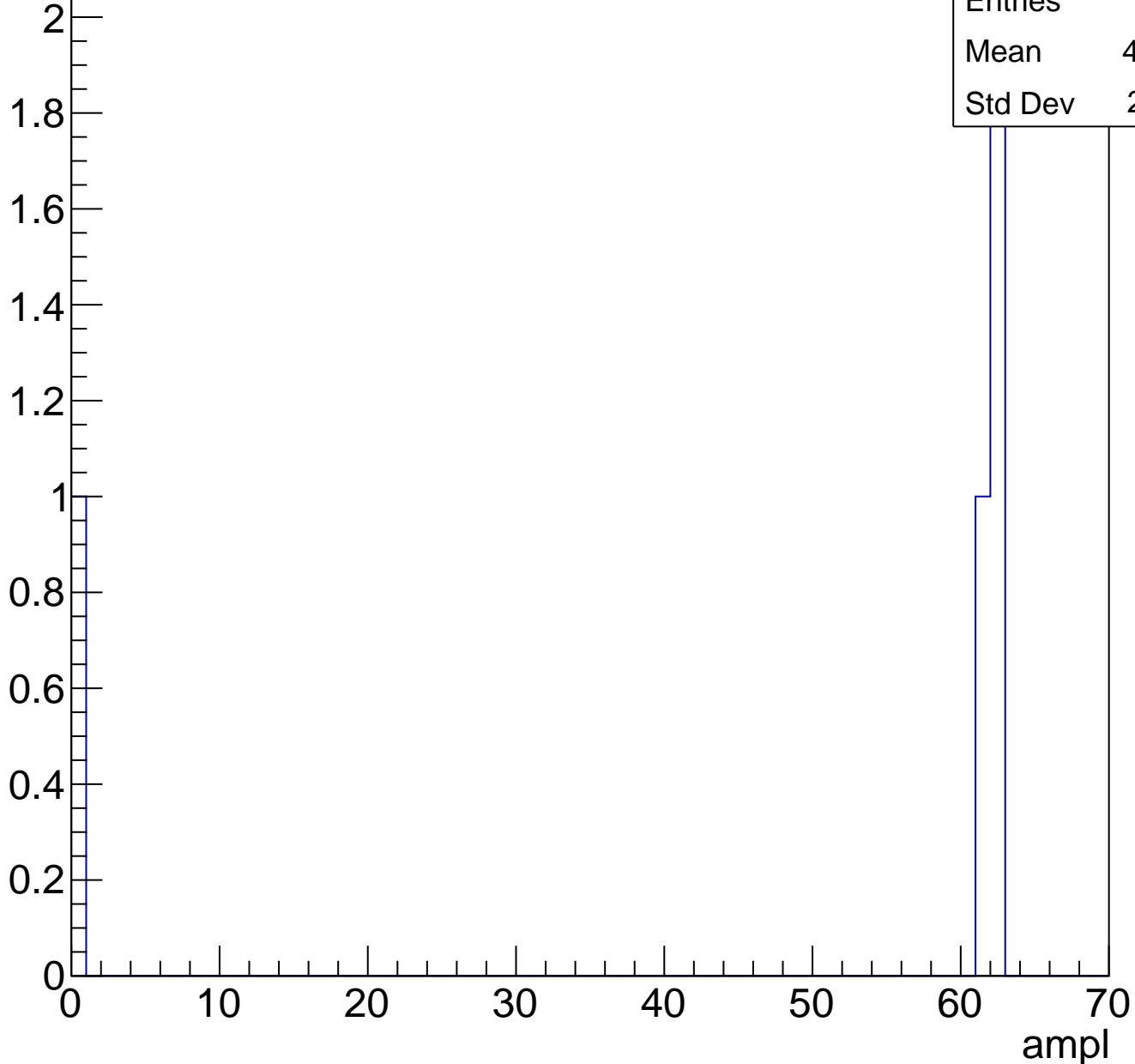




# B1L000S, U21-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch99, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	27.66
Std Dev	4.454

**Gaus mean : 28.6905**

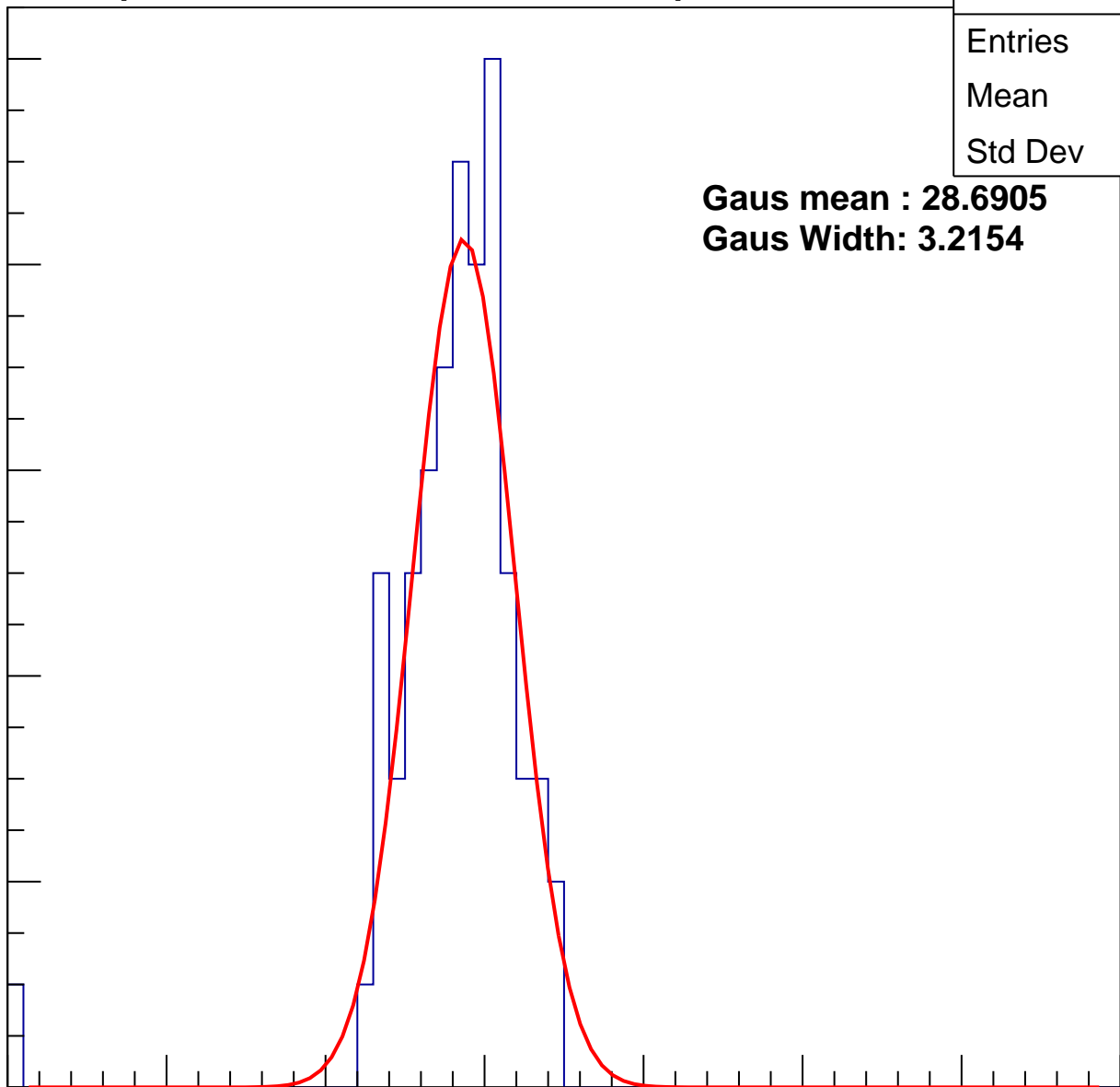
**Gaus Width: 3.2154**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	35.61
Std Dev	3.342

**Gaus mean : 35.9595**

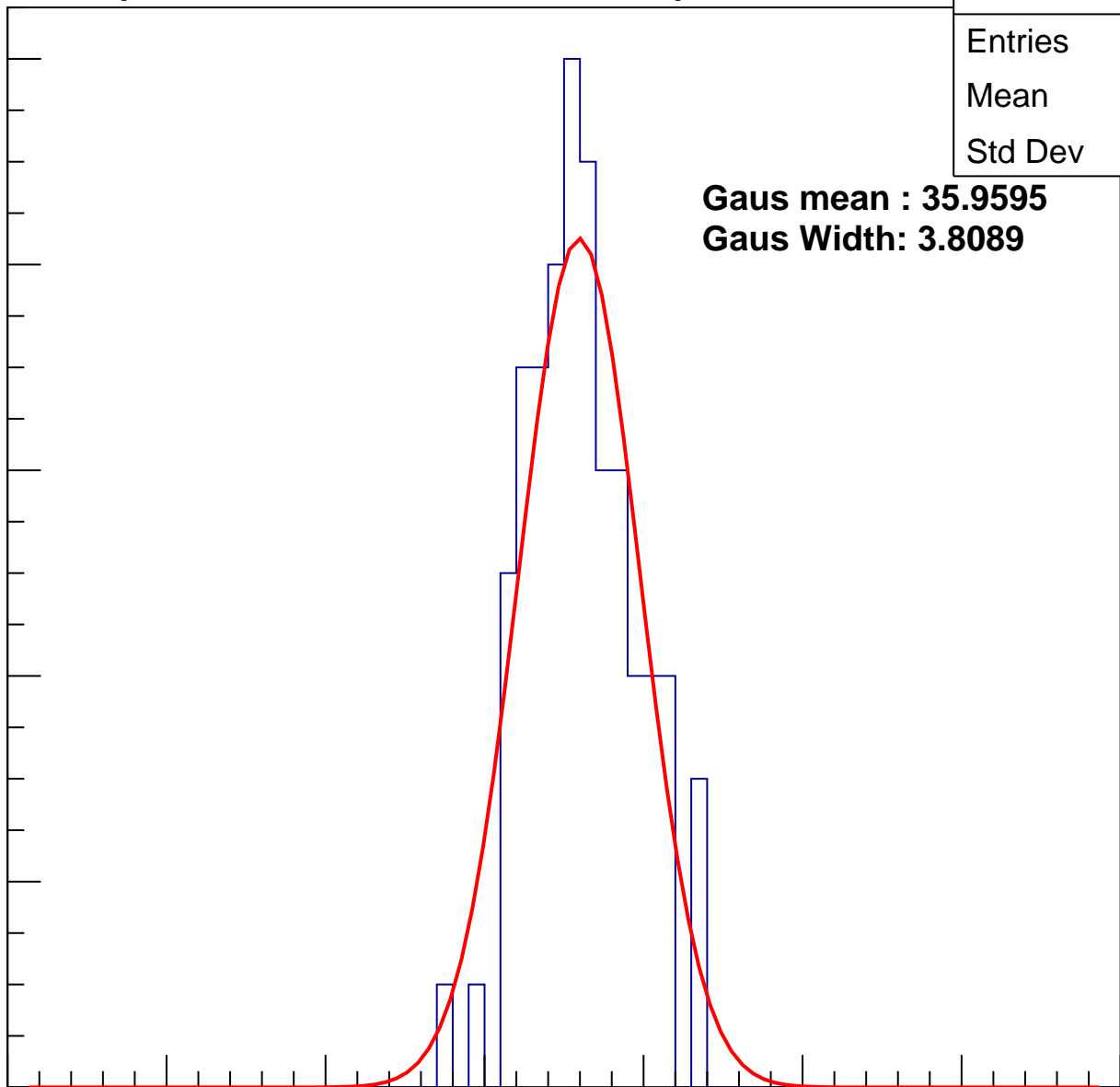
**Gaus Width: 3.8089**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch99, adc2

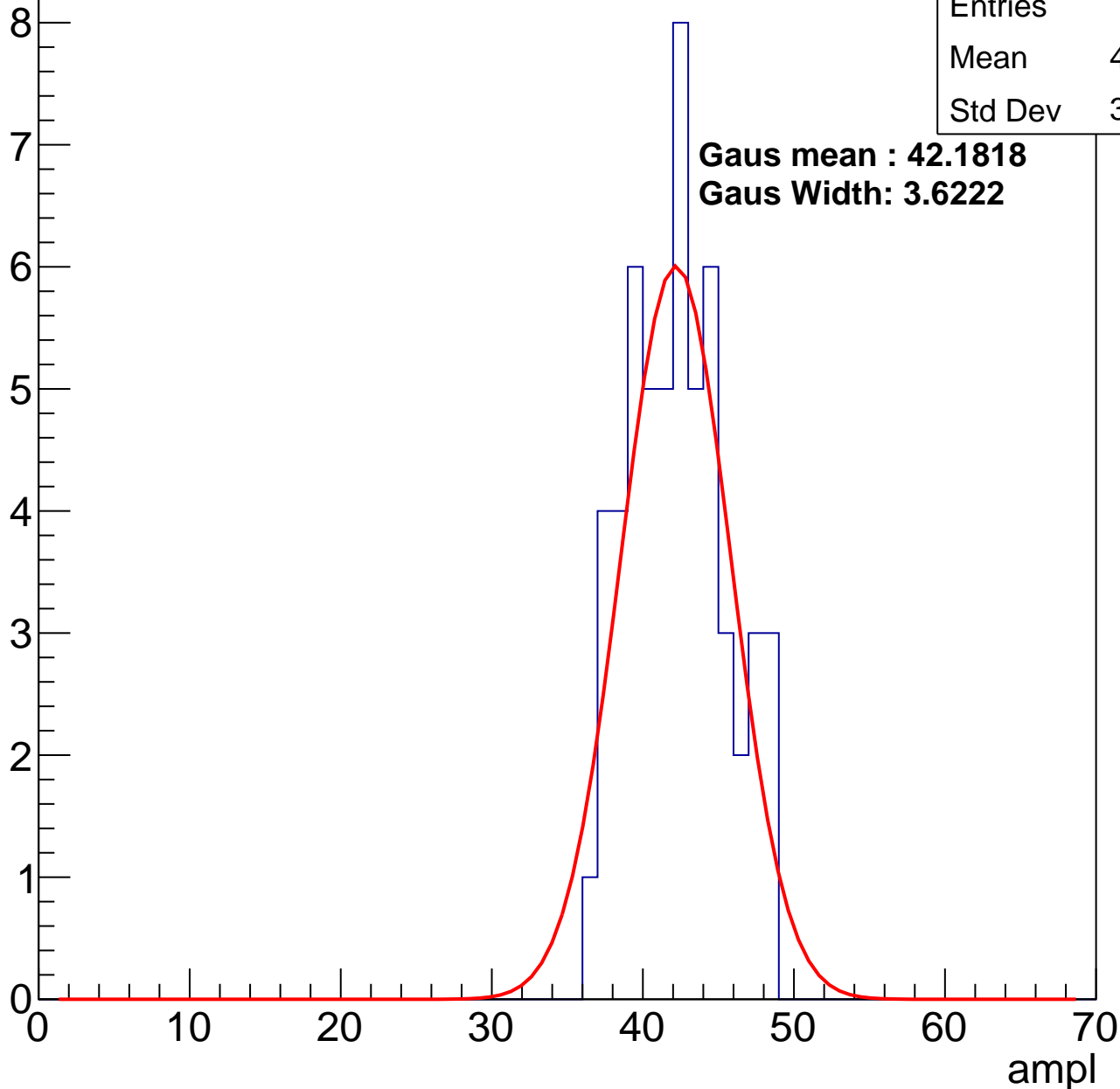
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.85
Std Dev	3.159

**Gaus mean : 42.1818**

**Gaus Width: 3.6222**



# B1L000S, U21-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

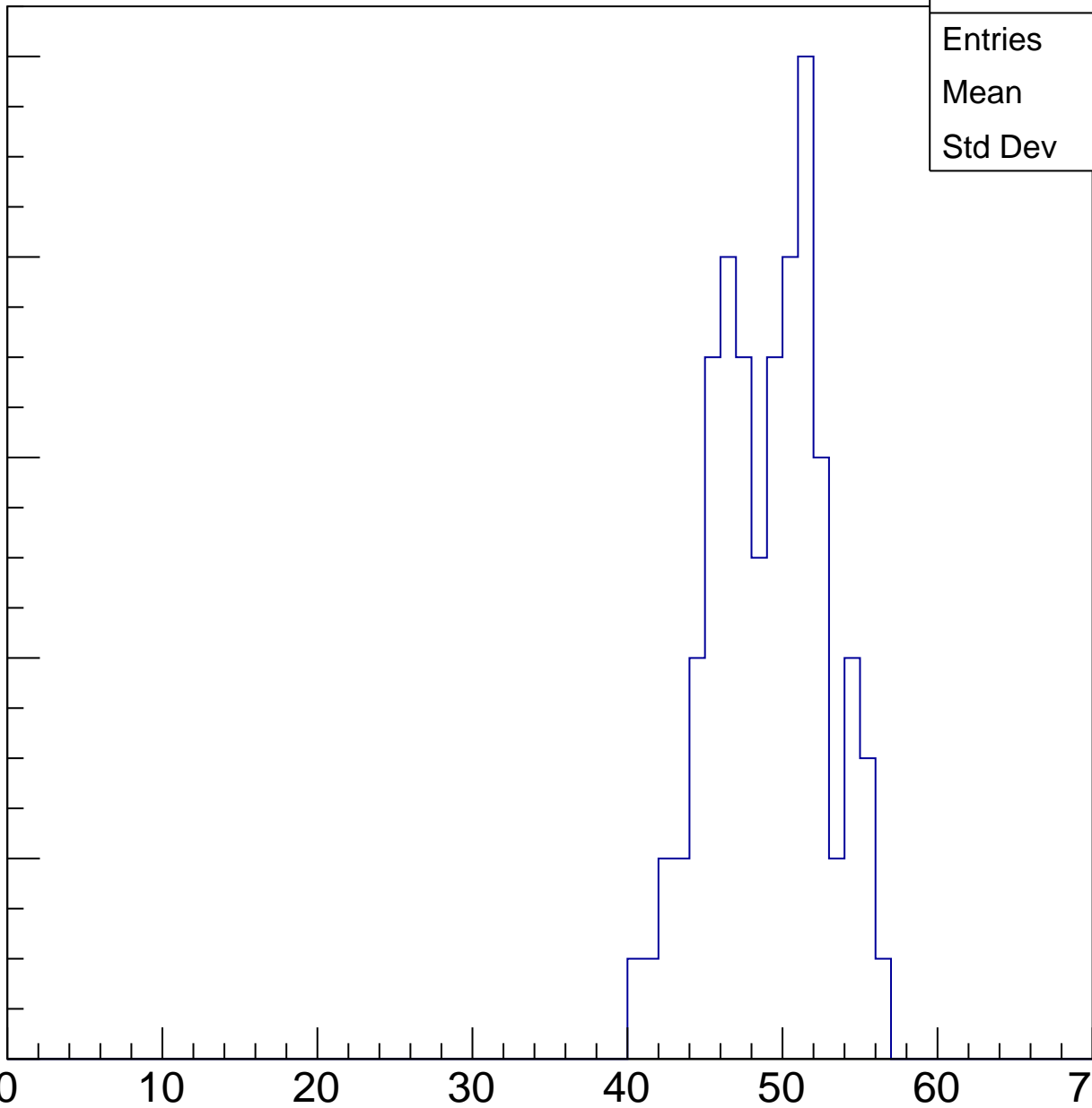
Entries	78
Mean	48.55
Std Dev	3.594

Entry

10  
8  
6  
4  
2  
0

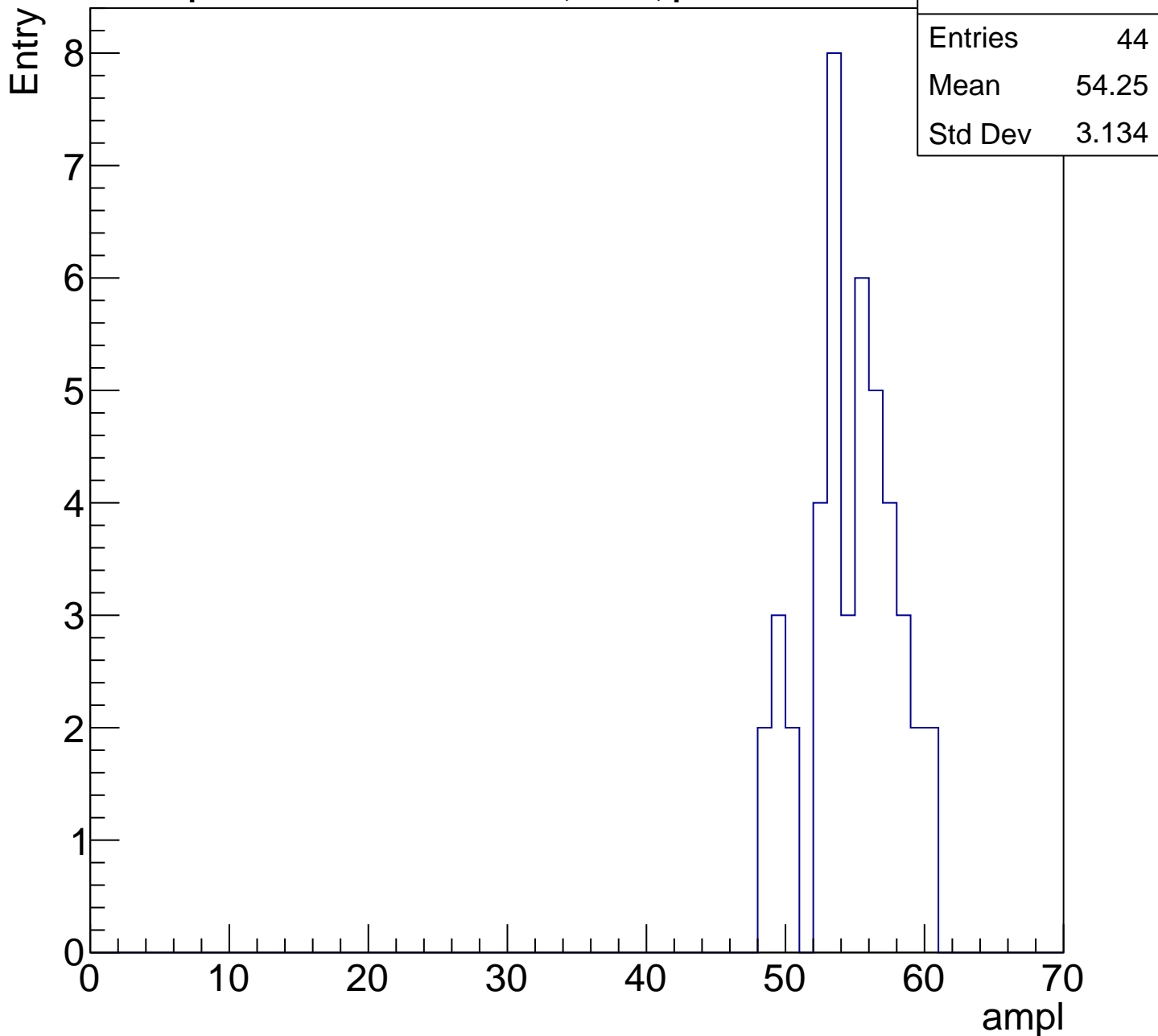
0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

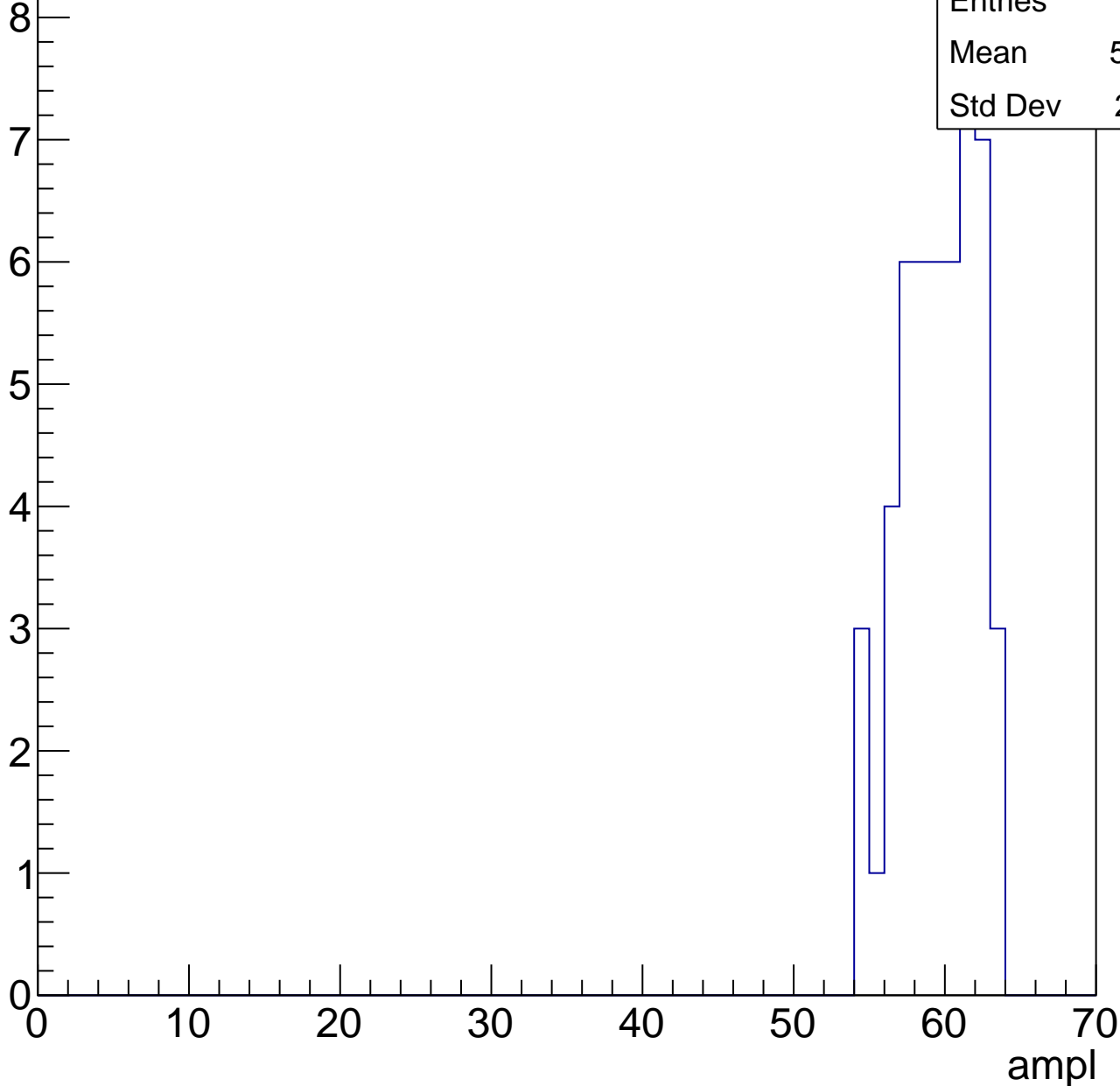


# B1L000S, U21-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	59.12
Std Dev	2.471

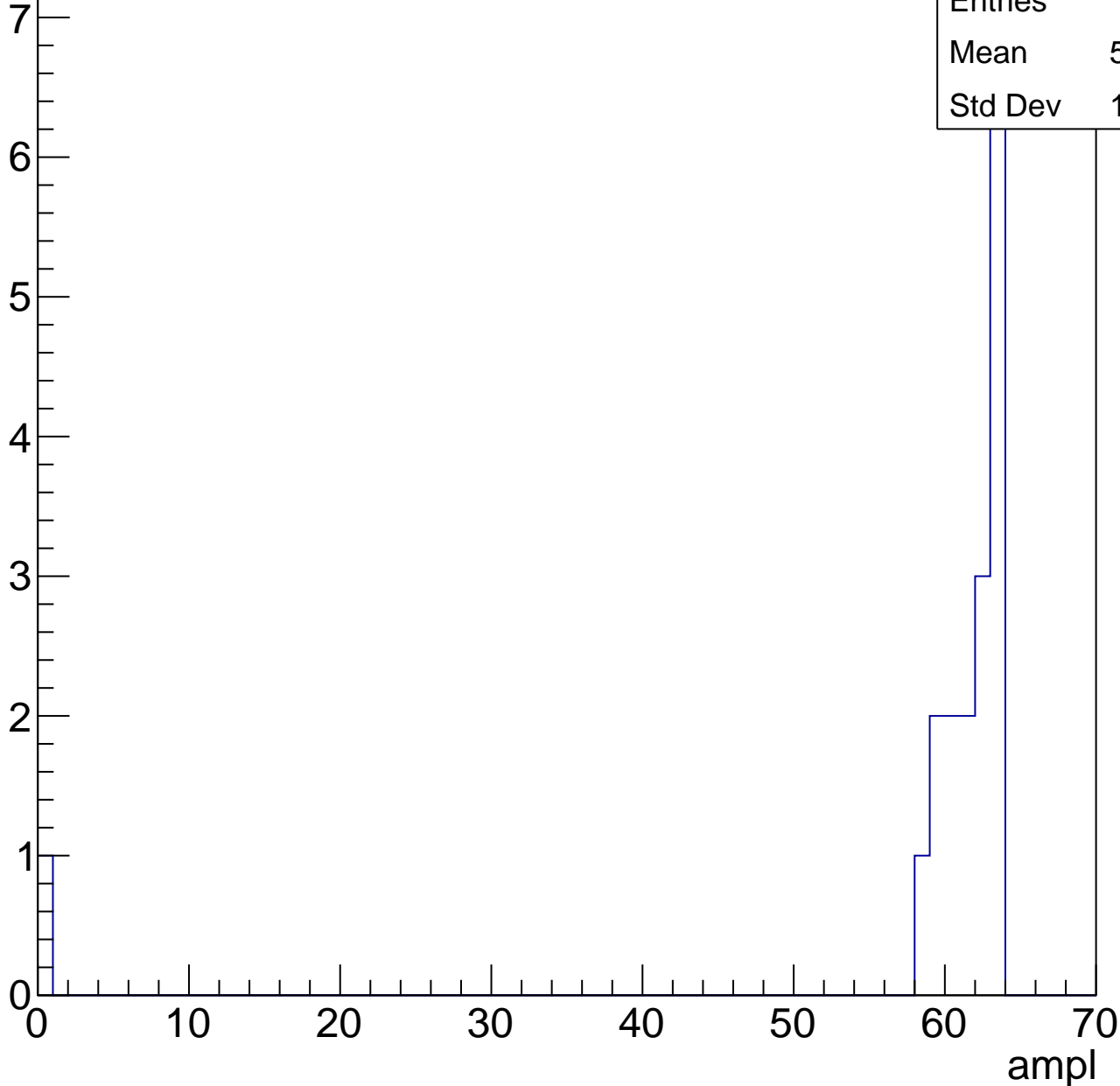


# B1L000S, U21-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.06
Std Dev	14.17





# B1L000S, U21-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch100, adc0

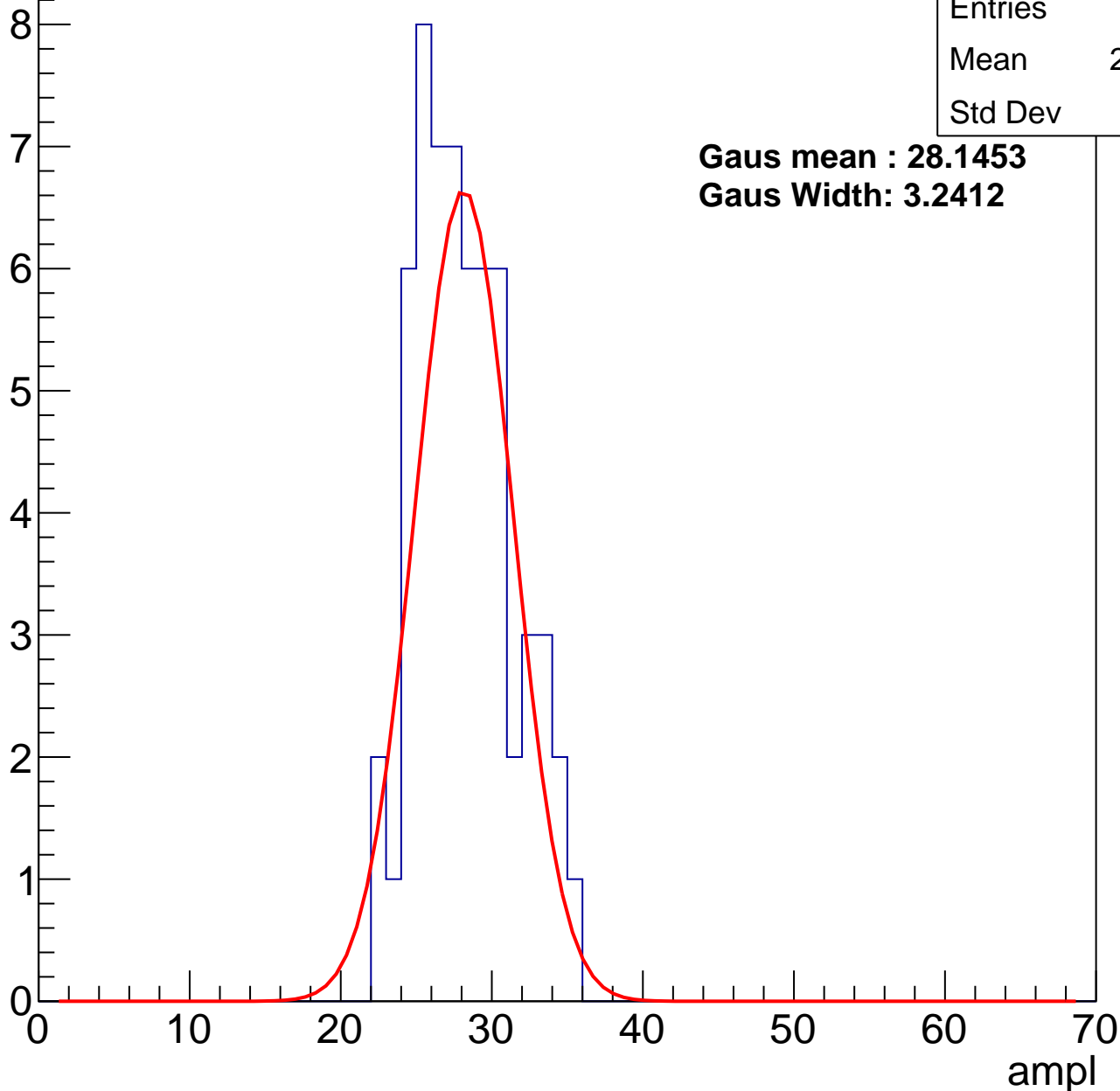
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.73
Std Dev	3.13

**Gaus mean : 28.1453**

**Gaus Width: 3.2412**



# B1L000S, U21-ch100, adc1

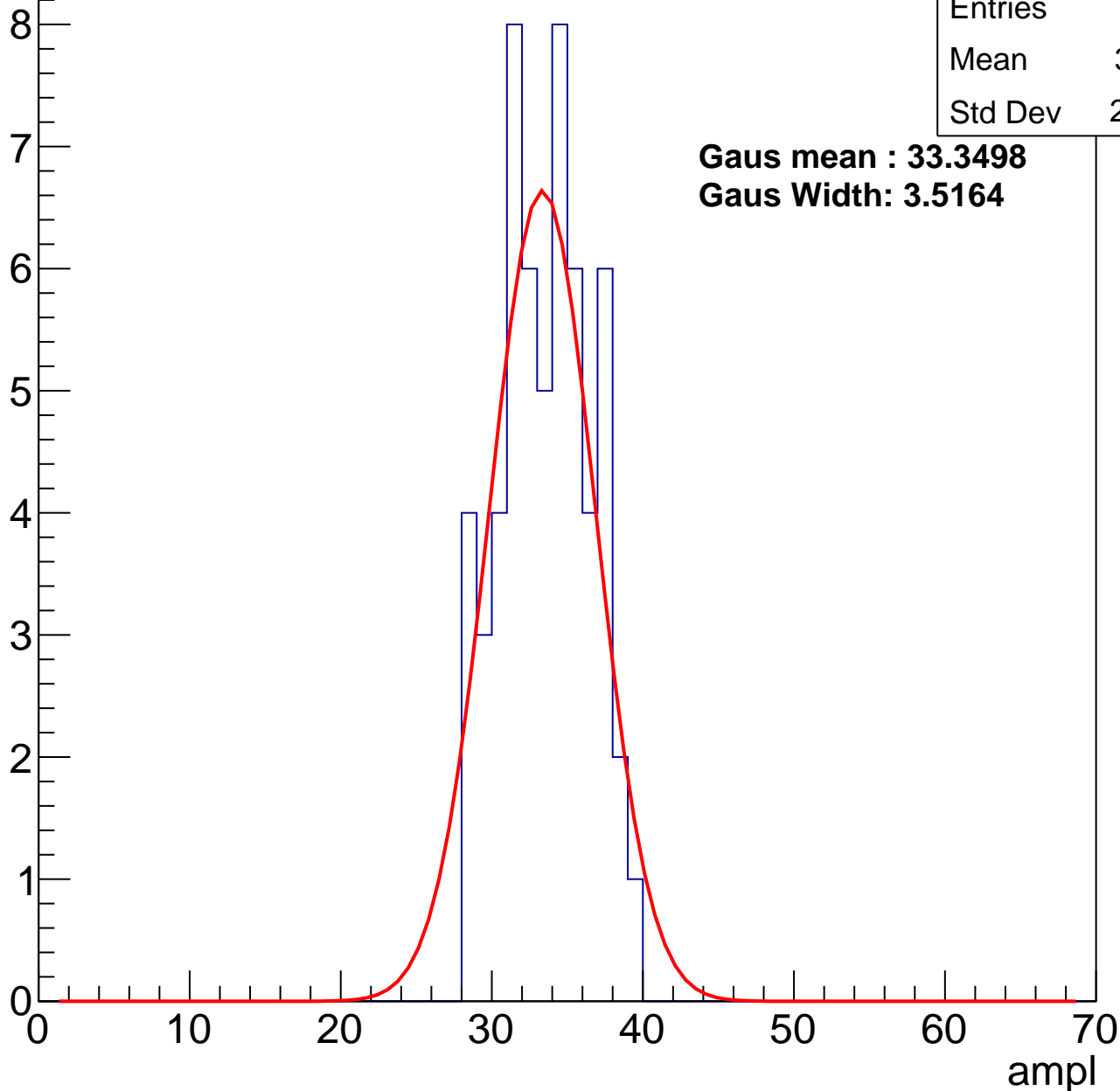
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	33.11
Std Dev	2.876

**Gaus mean : 33.3498**

**Gaus Width: 3.5164**



# B1L000S, U21-ch100, adc2

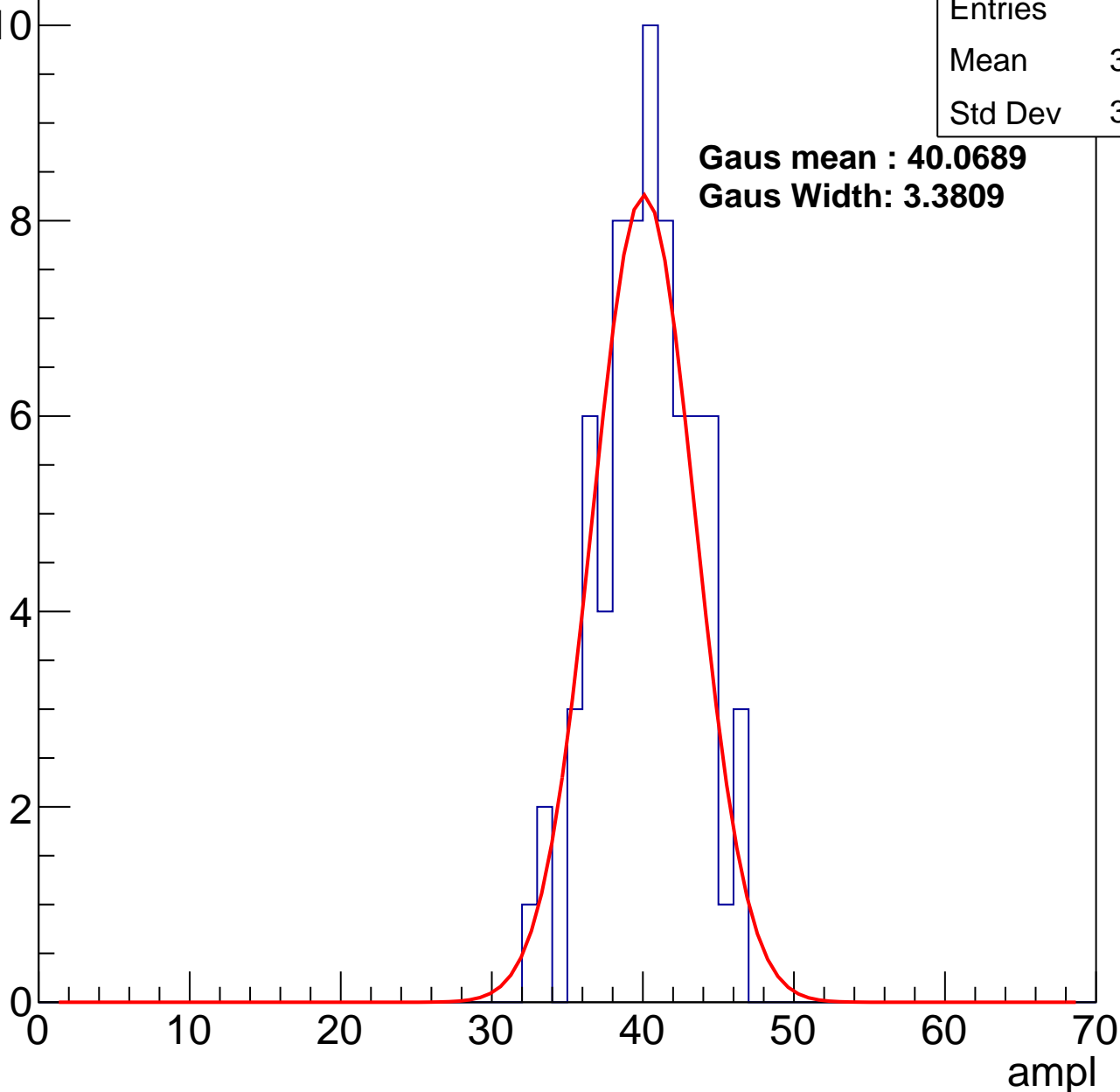
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	39.83
Std Dev	3.167

**Gaus mean : 40.0689**

**Gaus Width: 3.3809**

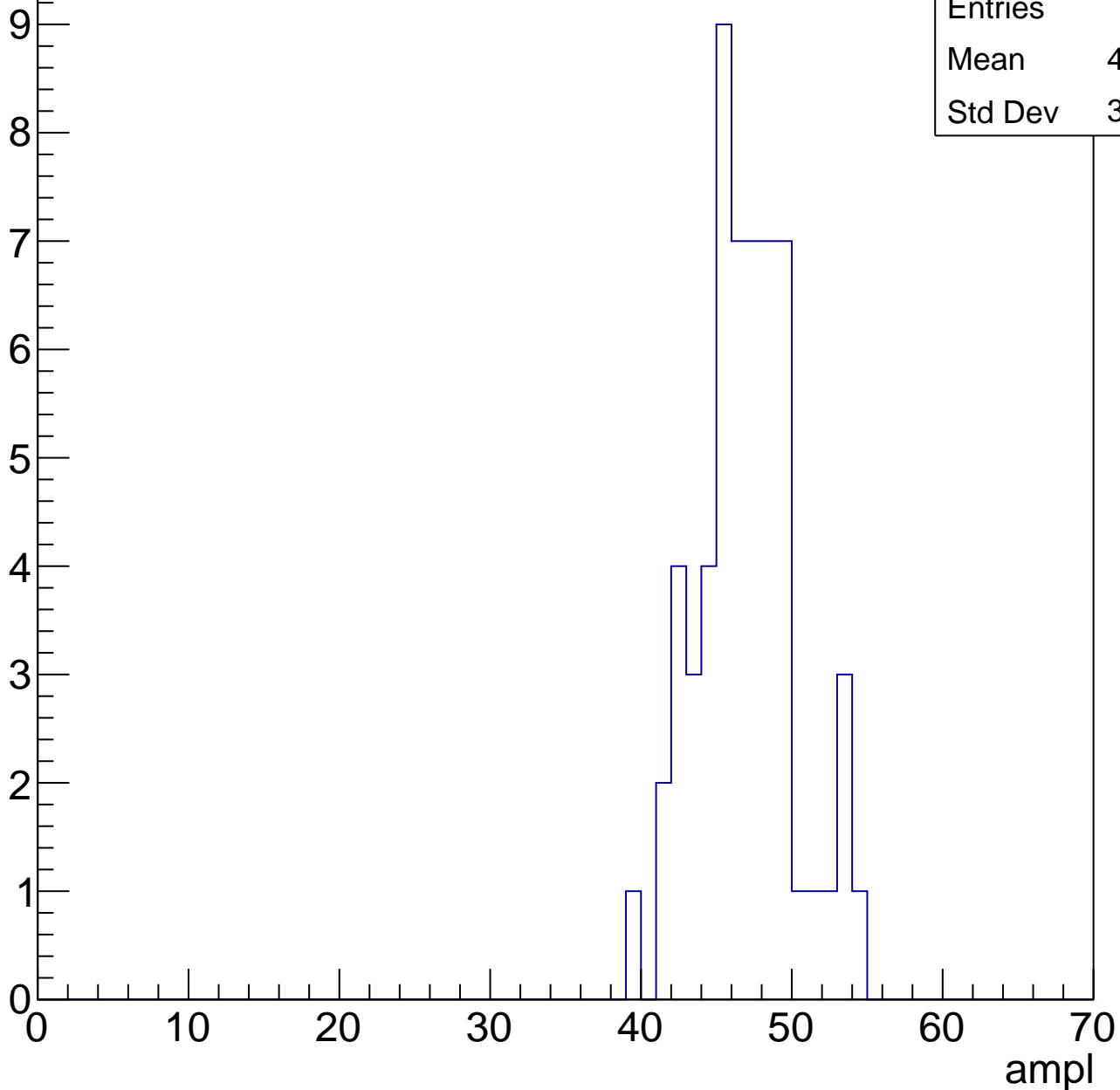


# B1L000S, U21-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	46.47
Std Dev	3.185

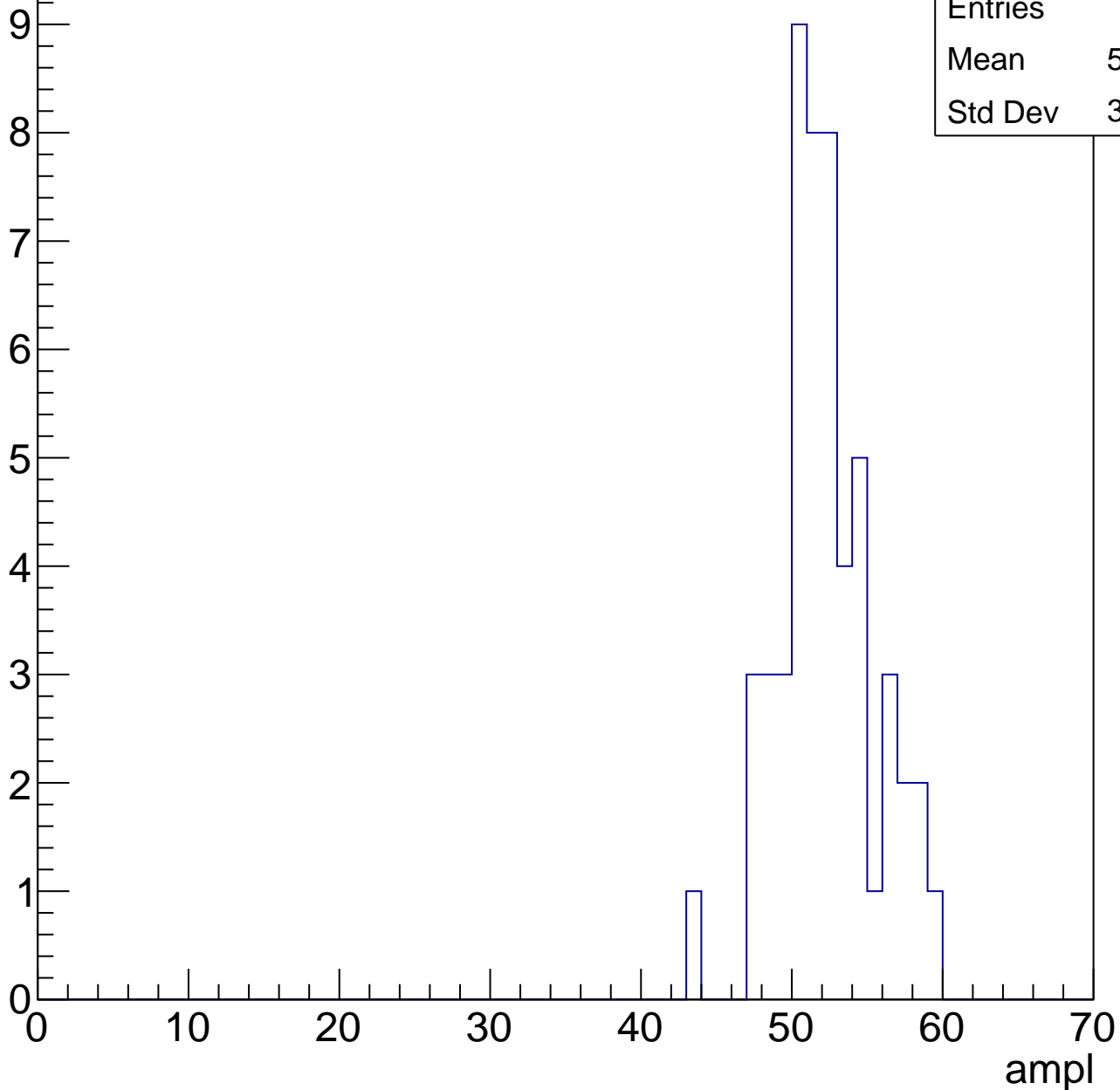


# B1L000S, U21-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

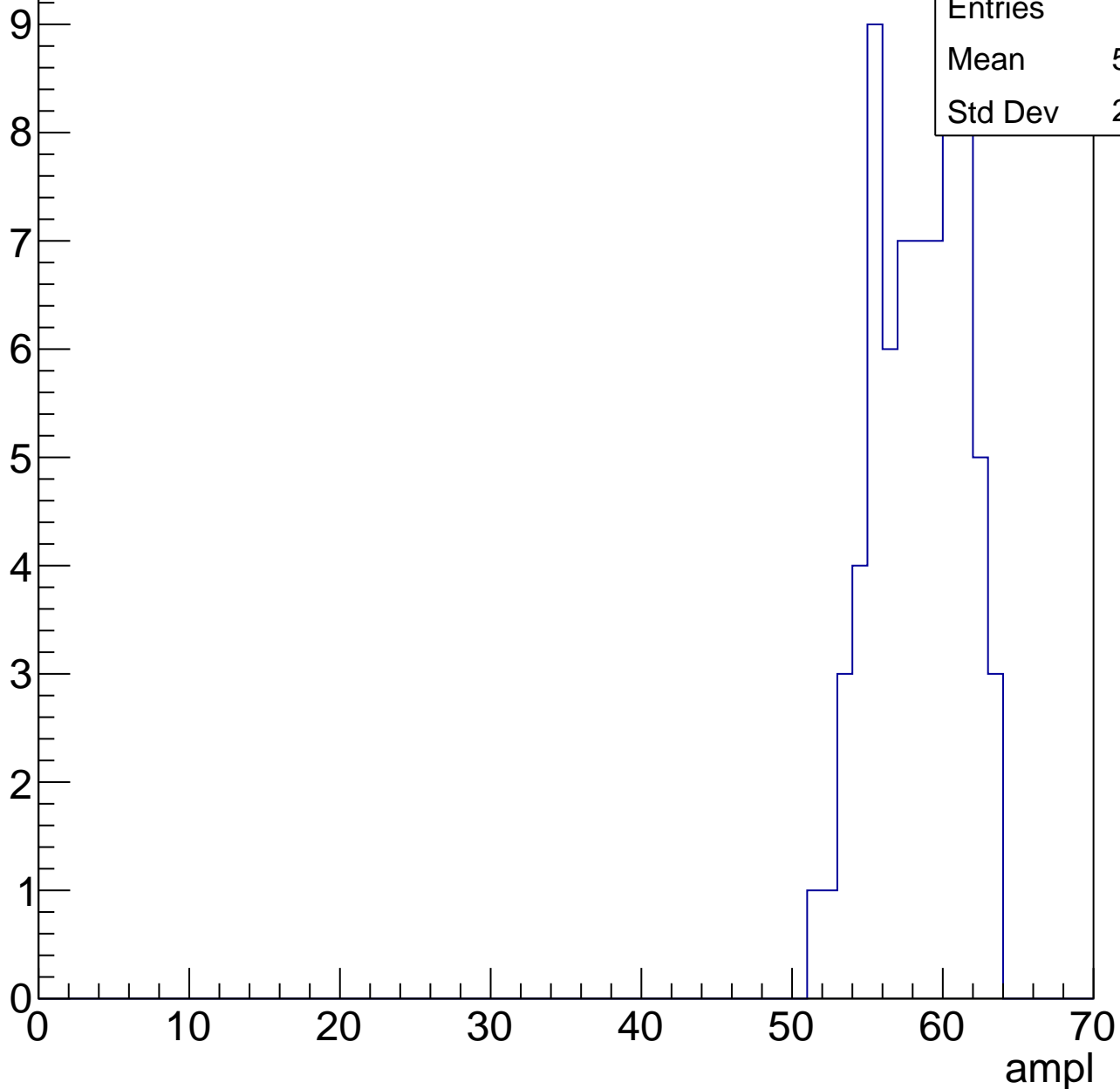
Entries	53
Mean	51.75
Std Dev	3.144



# B1L000S, U21-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

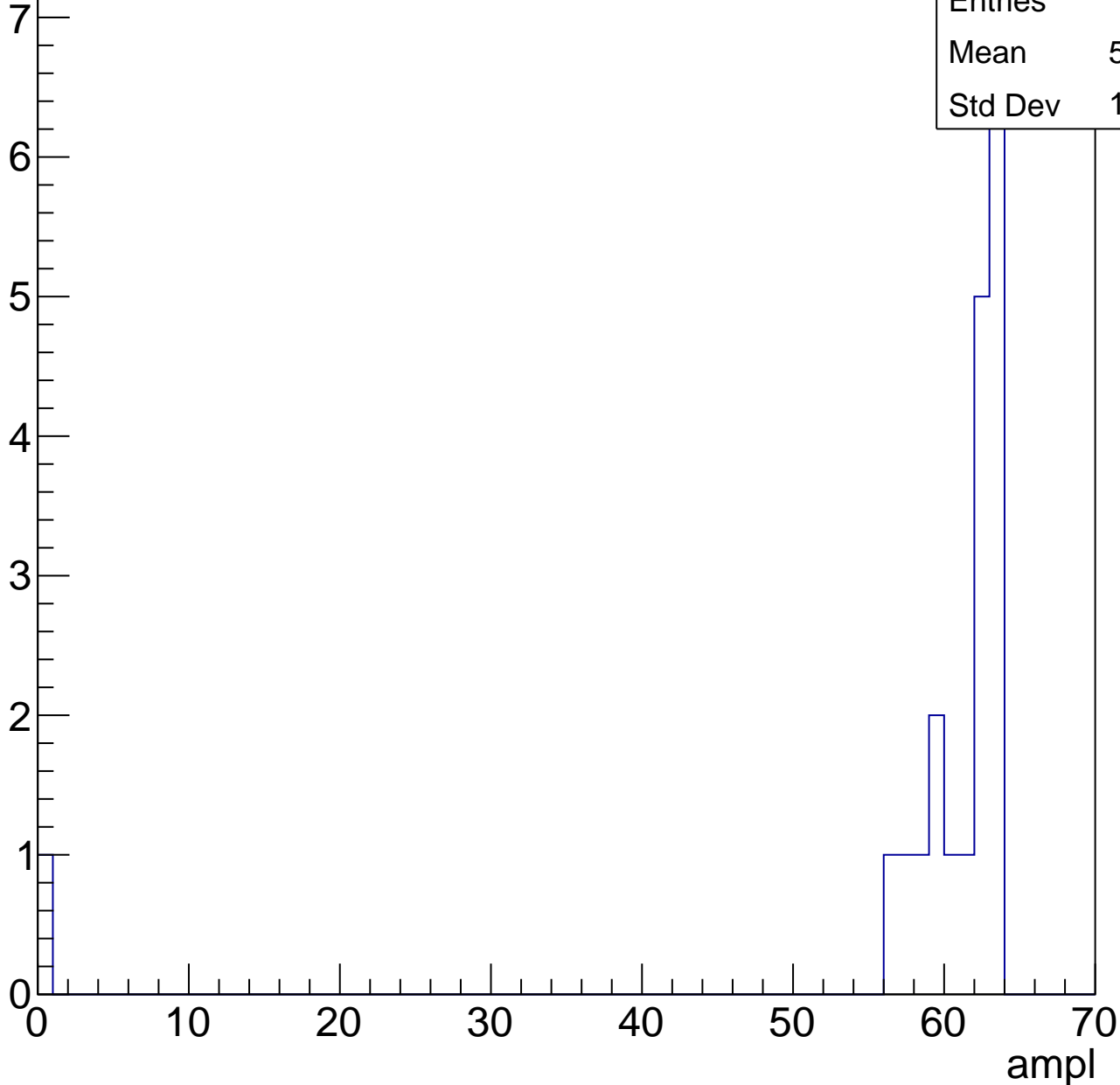


# B1L000S, U21-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.05
Std Dev	13.49

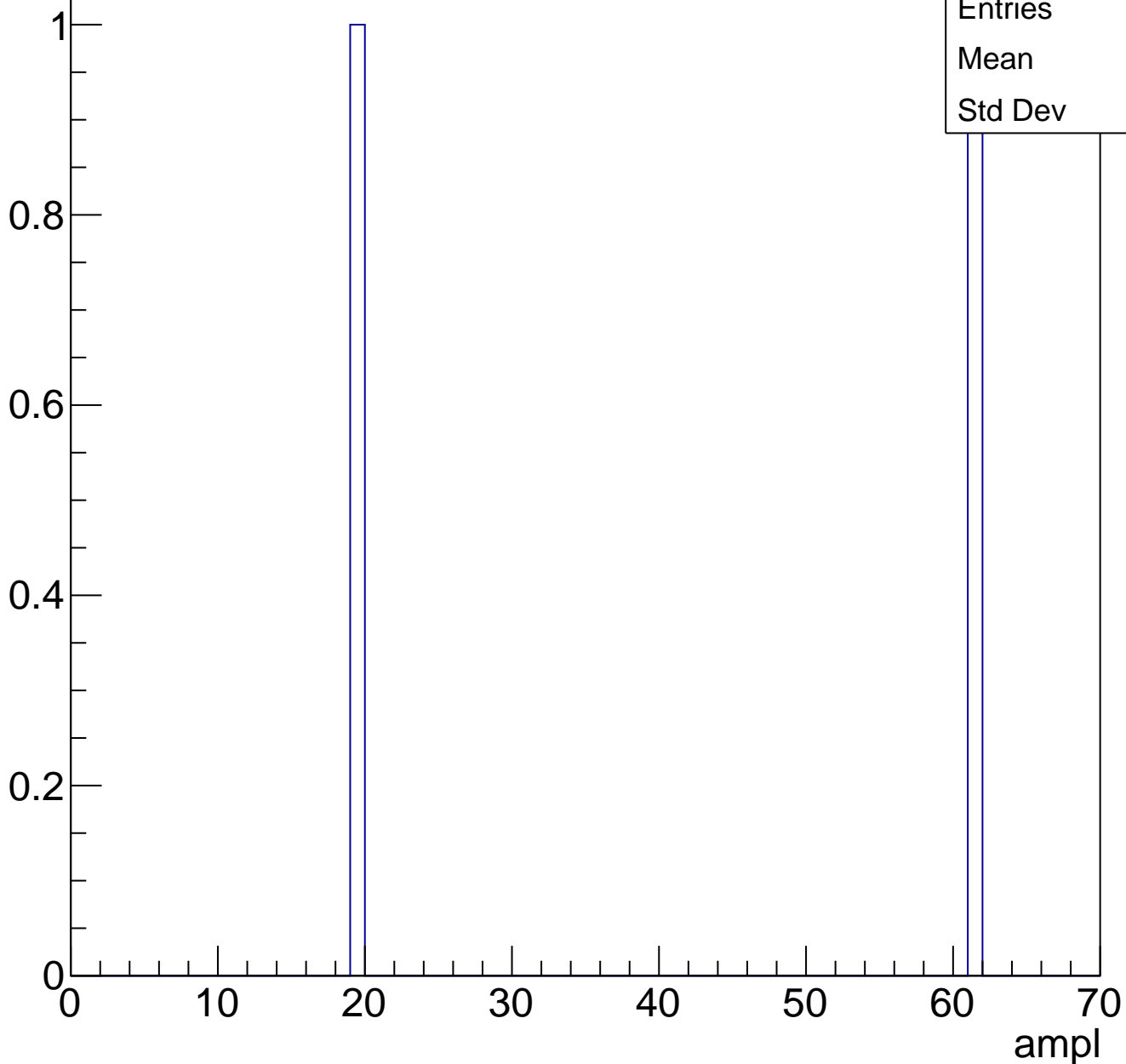




# B1L000S, U21-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch101, adc0

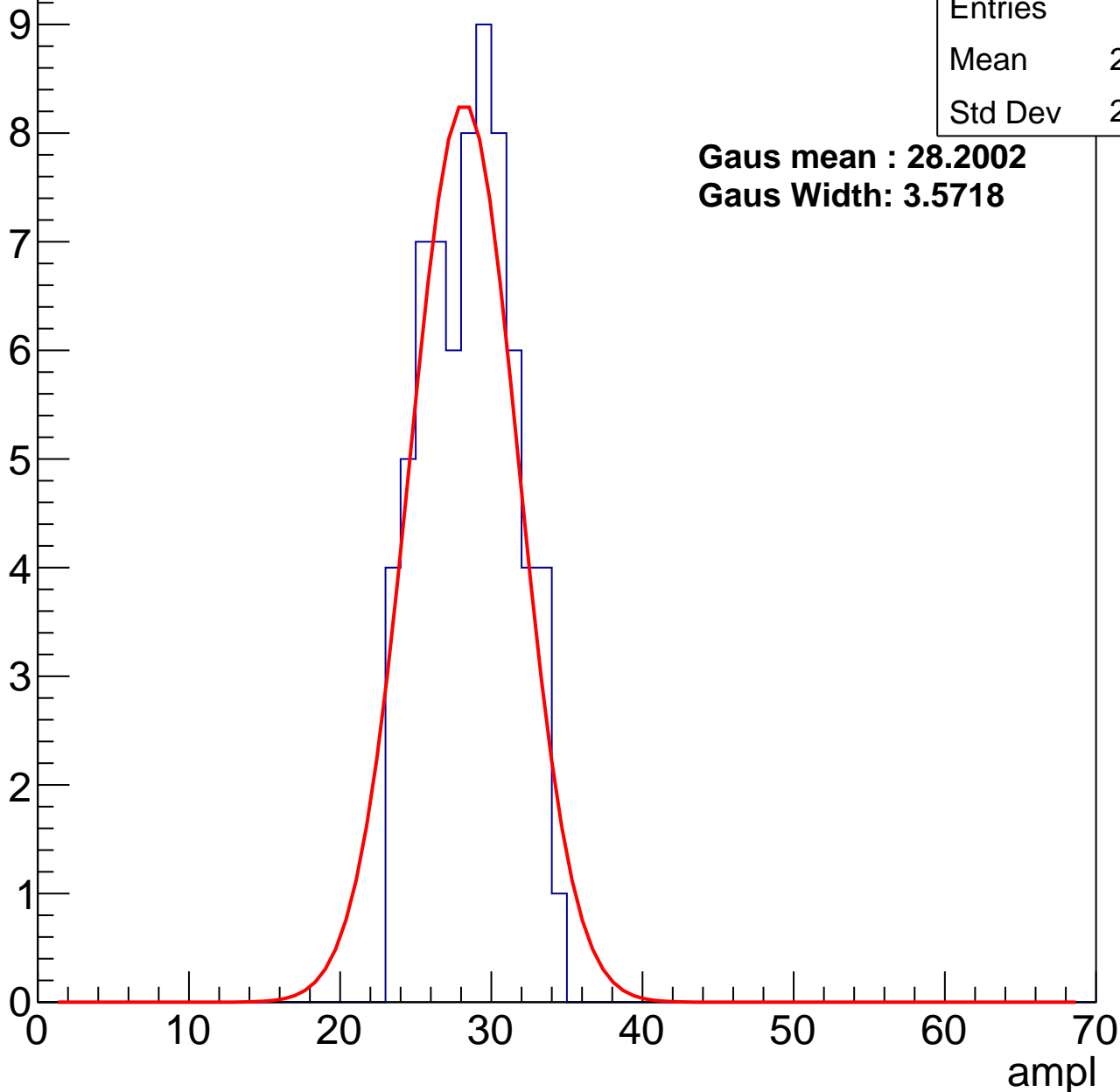
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.06
Std Dev	2.879

**Gaus mean : 28.2002**

**Gaus Width: 3.5718**



# B1L000S, U21-ch101, adc1

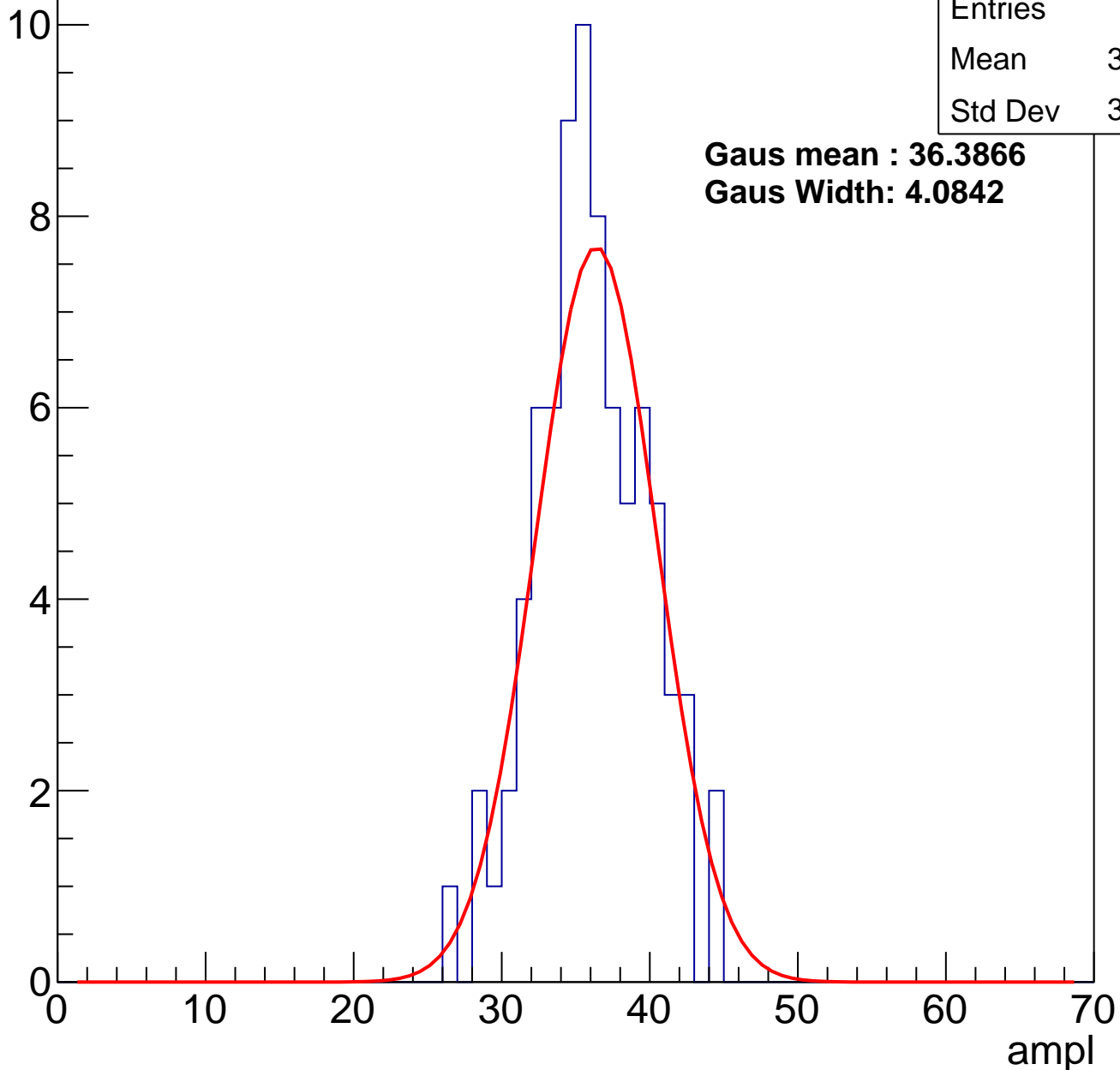
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	35.59
Std Dev	3.737

**Gaus mean : 36.3866**

**Gaus Width: 4.0842**

Entry



# B1L000S, U21-ch101, adc2

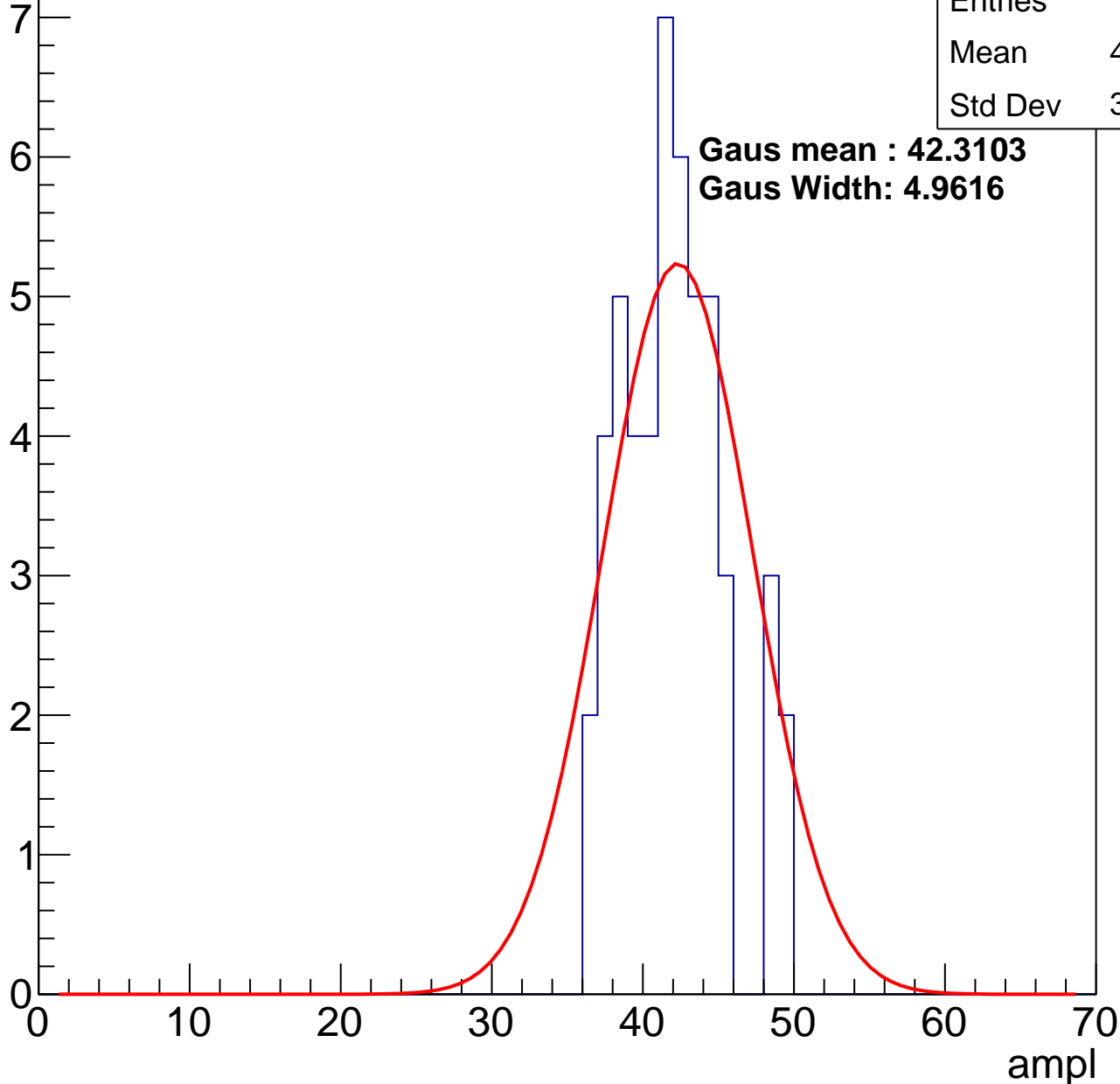
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	41.54
Std Dev	3.342

**Gaus mean : 42.3103**

**Gaus Width: 4.9616**

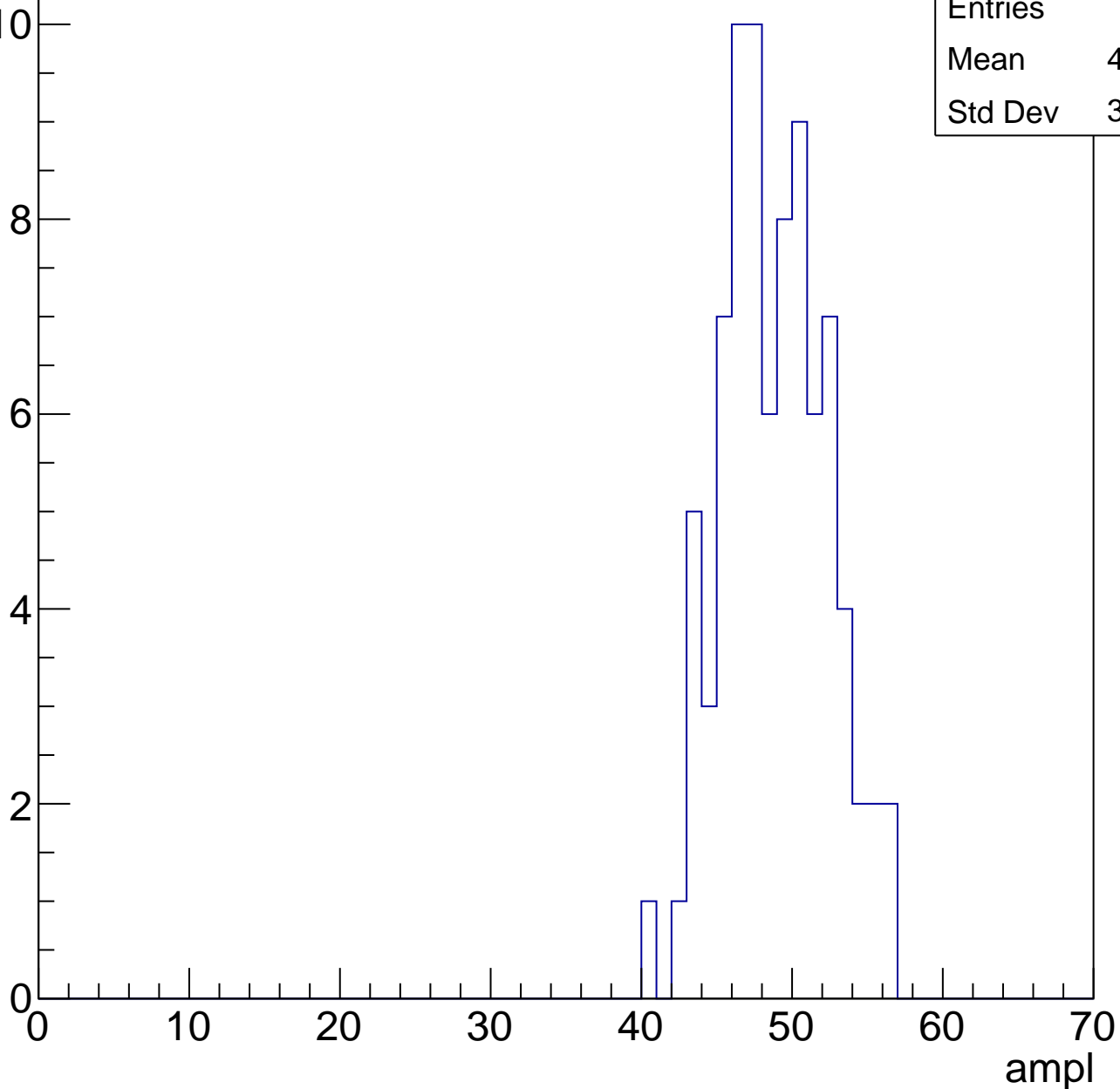


# B1L000S, U21-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	83
Mean	48.39
Std Dev	3.439

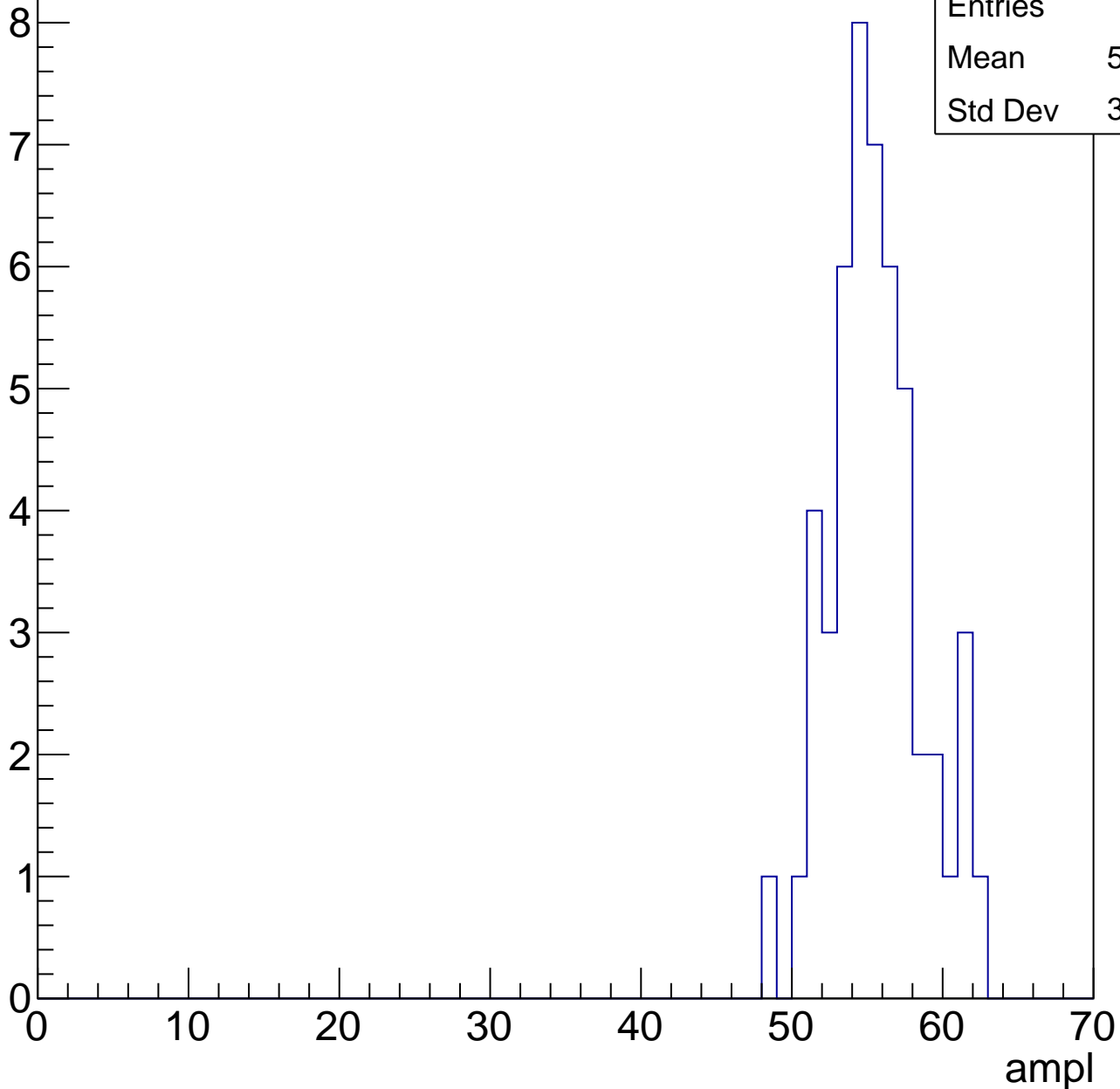


# B1L000S, U21-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

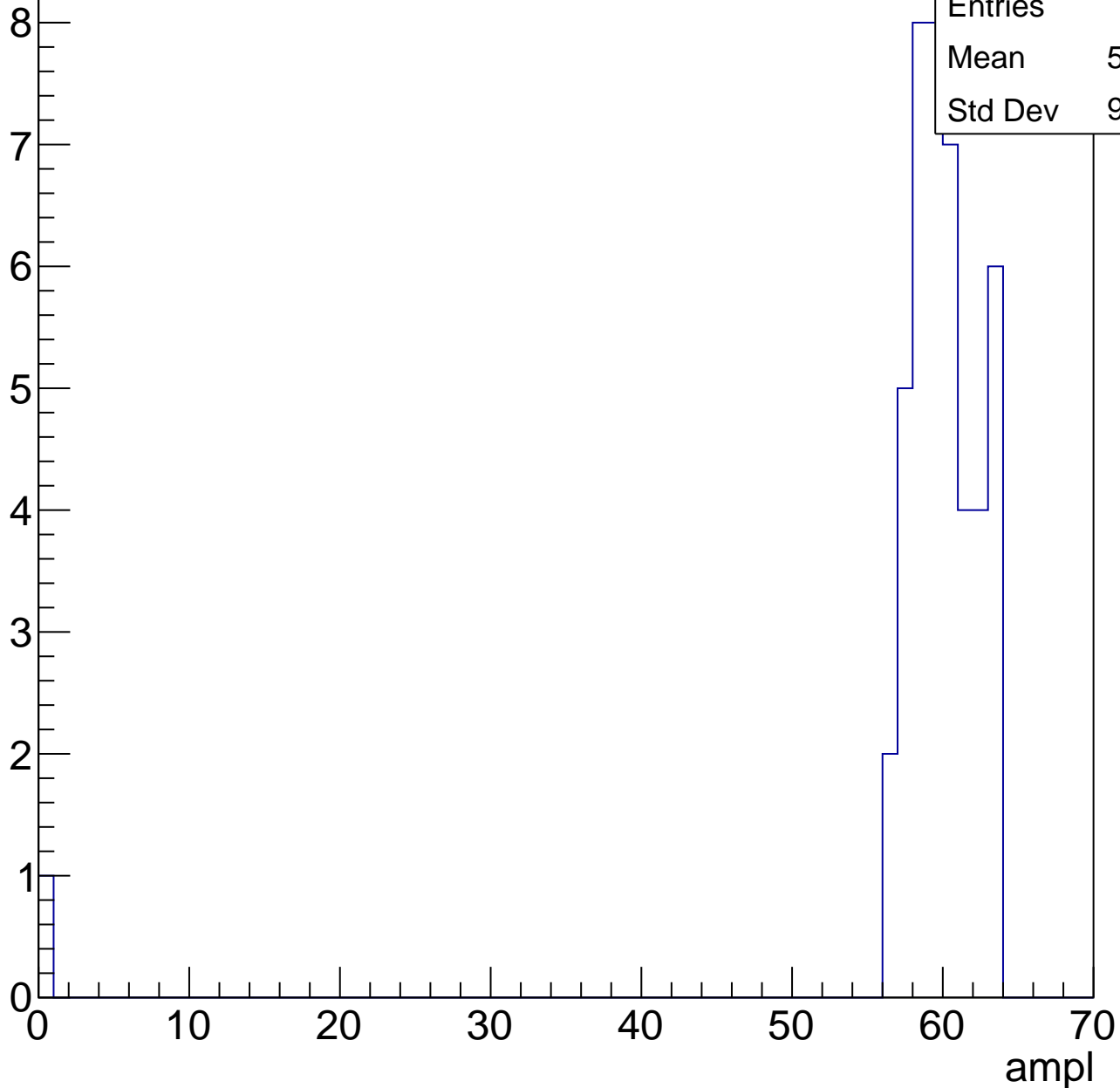
Entries	50
Mean	55.06
Std Dev	3.016



# B1L000S, U21-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

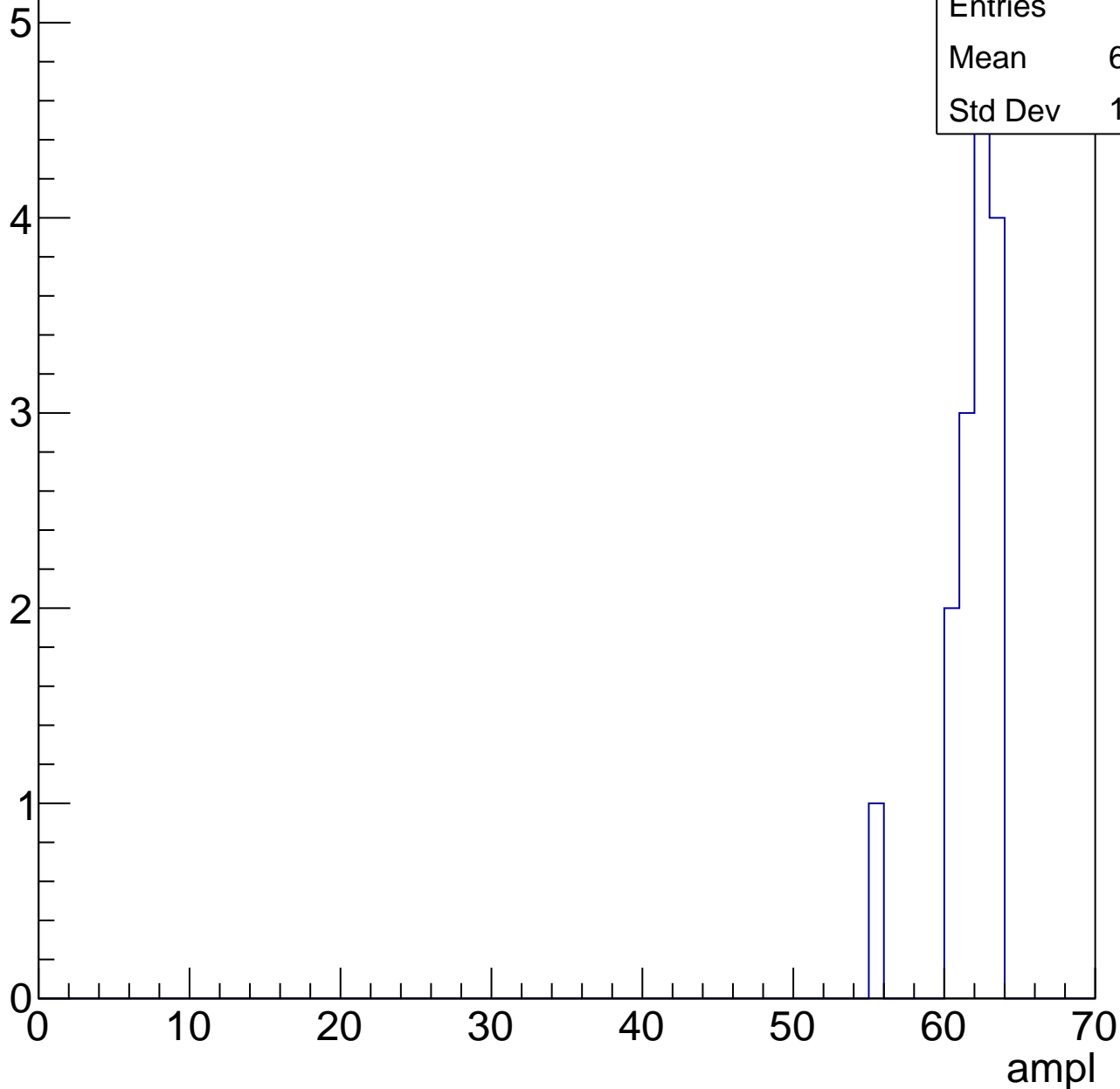


# B1L000S, U21-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.33
Std Dev	1.955





# B1L000S, U21-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U21-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	25.66
Std Dev	4.695

**Gaus mean : 26.6568**

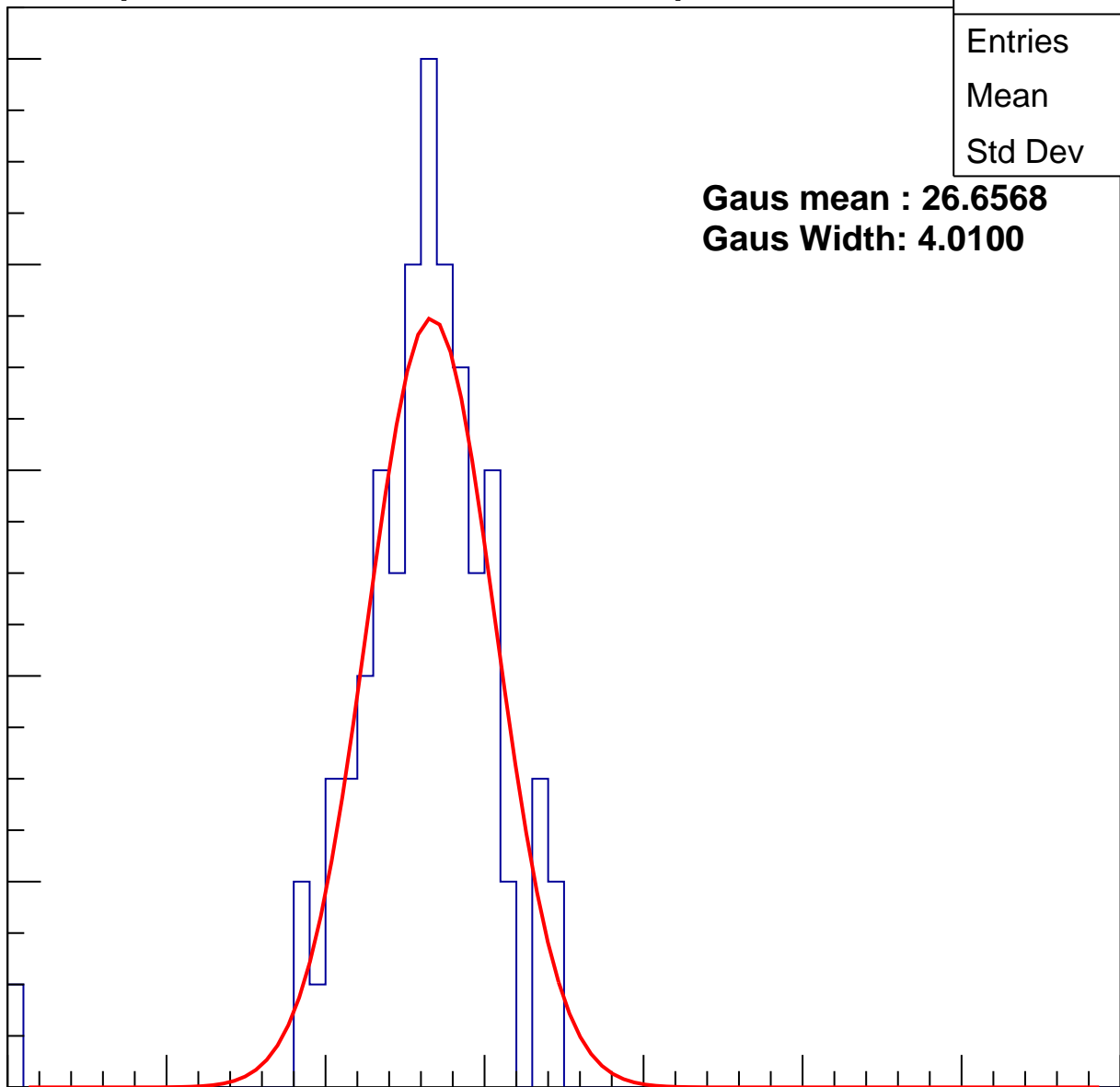
**Gaus Width: 4.0100**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch102, adc1

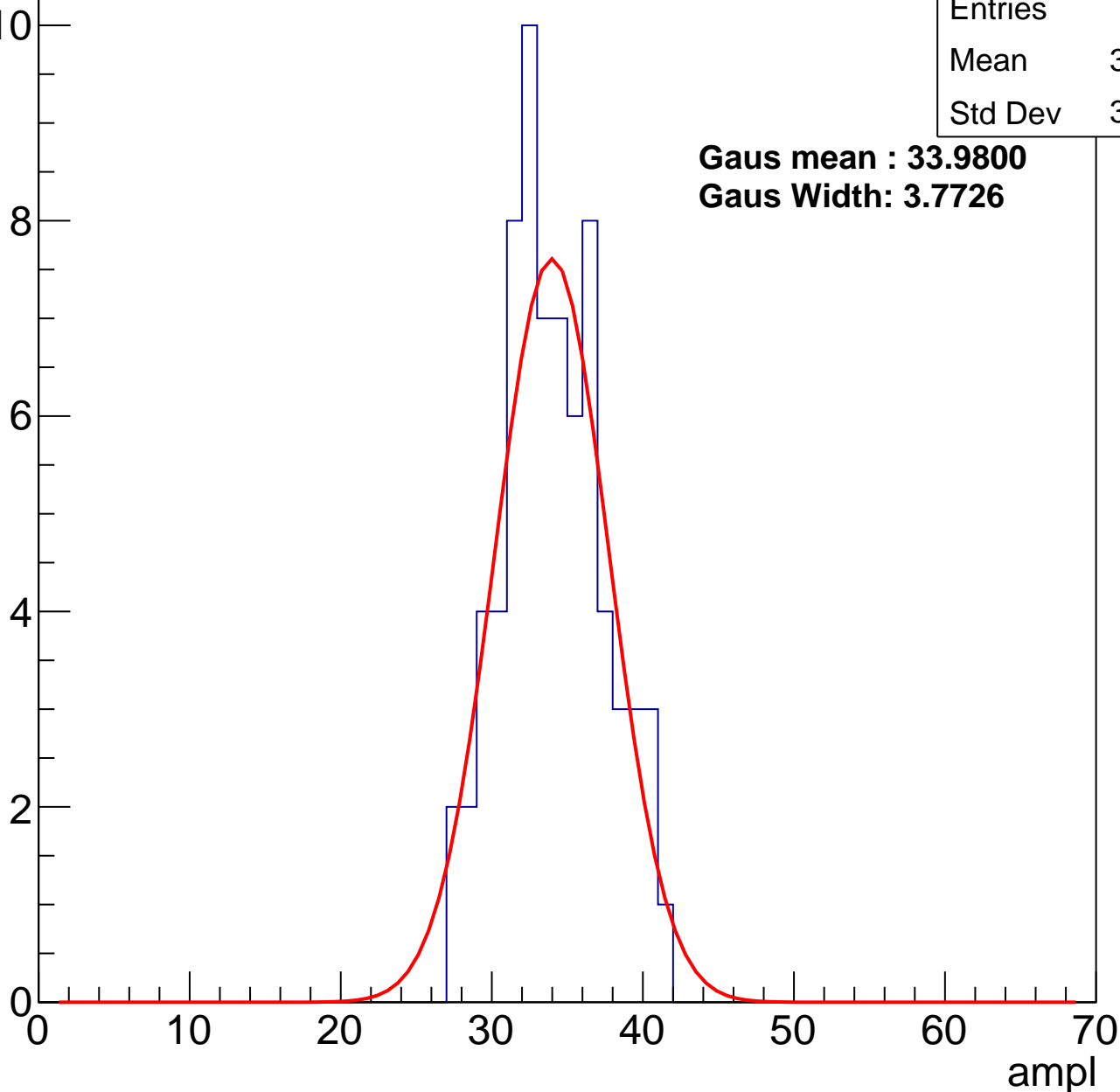
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	33.62
Std Dev	3.327

**Gaus mean : 33.9800**

**Gaus Width: 3.7726**



# B1L000S, U21-ch102, adc2

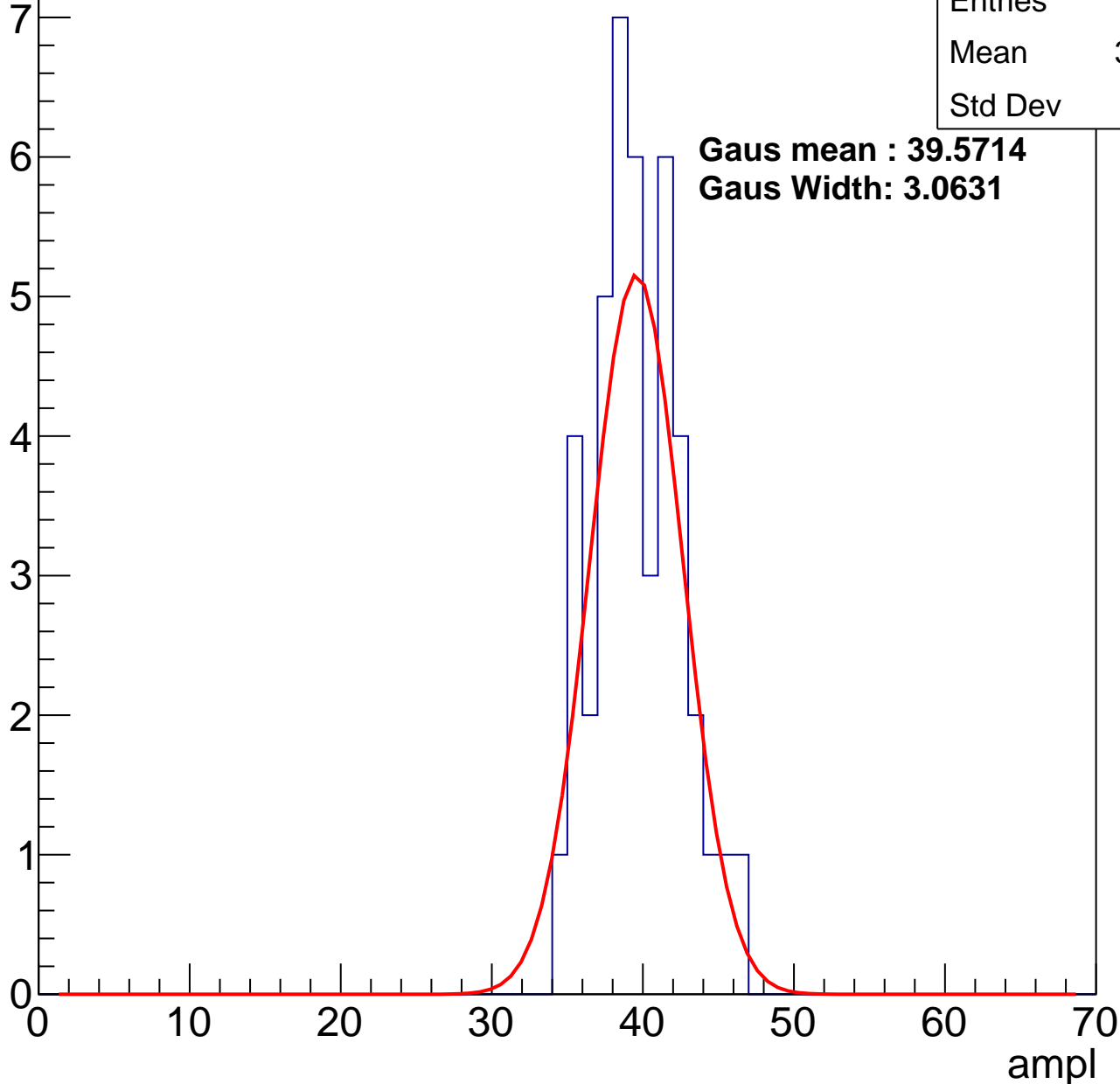
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	39.21
Std Dev	2.8

**Gaus mean : 39.5714**

**Gaus Width: 3.0631**

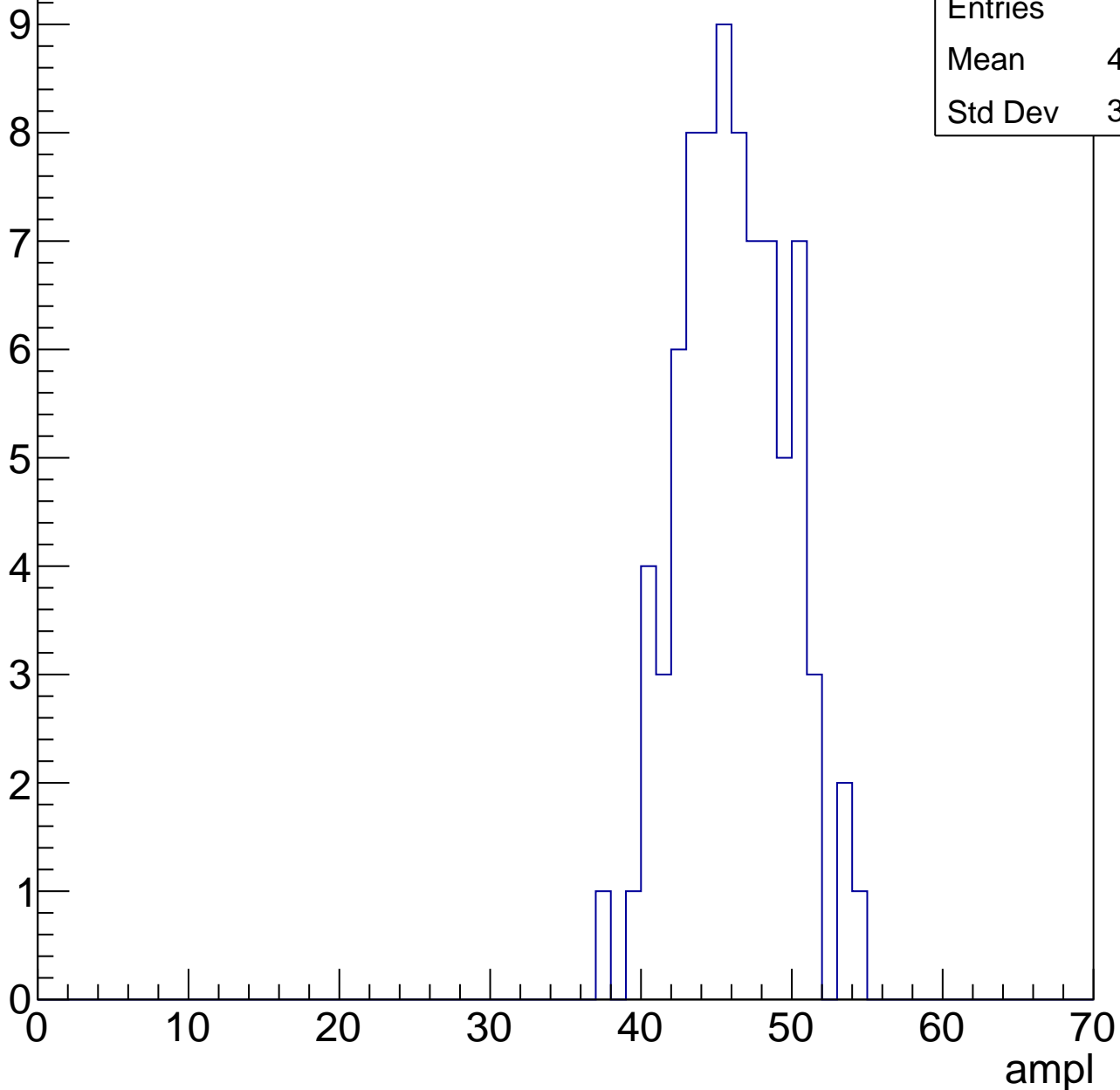


# B1L000S, U21-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	45.66
Std Dev	3.496

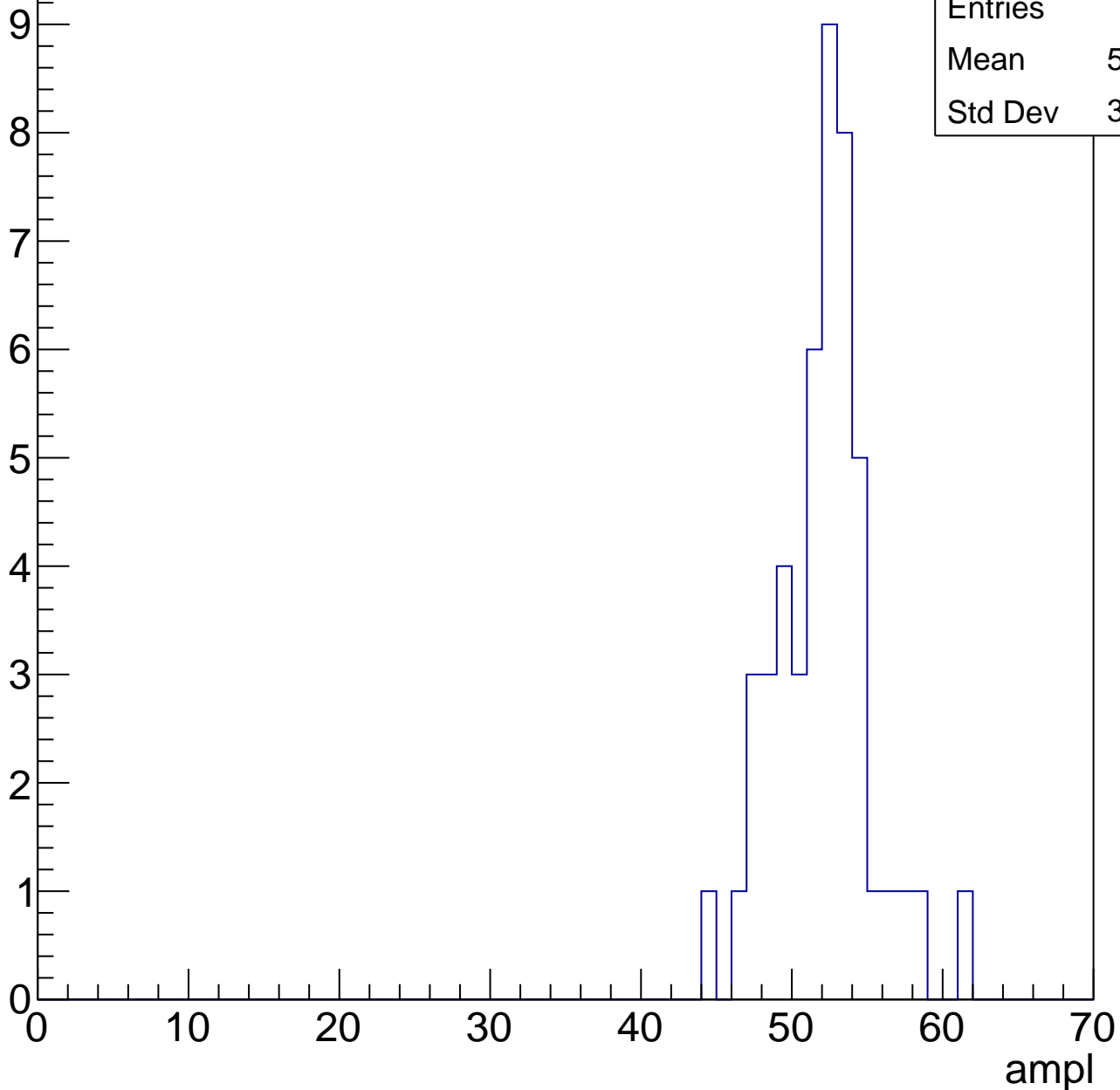


# B1L000S, U21-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

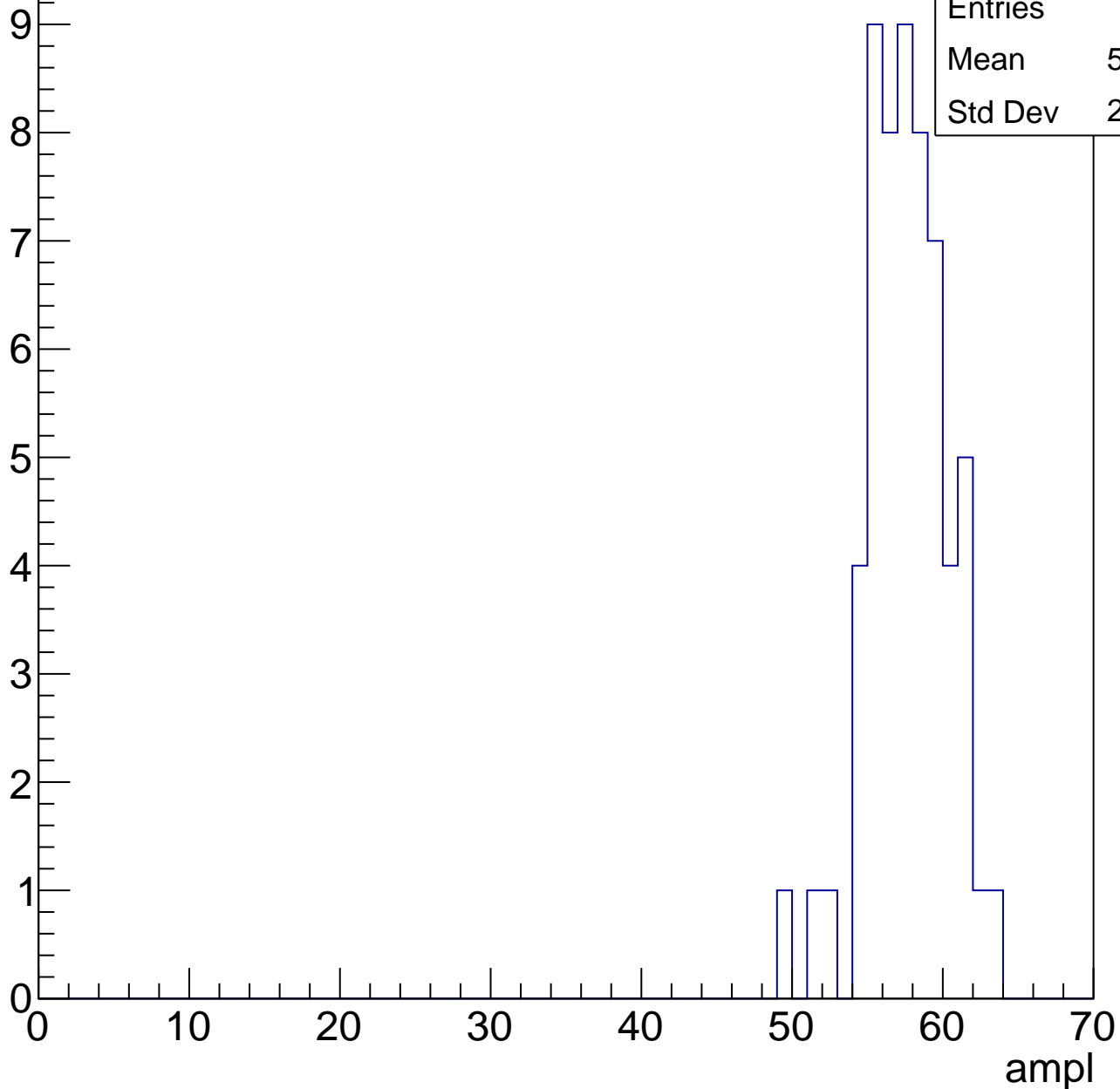
Entries	48
Mean	51.58
Std Dev	3.108



# B1L000S, U21-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

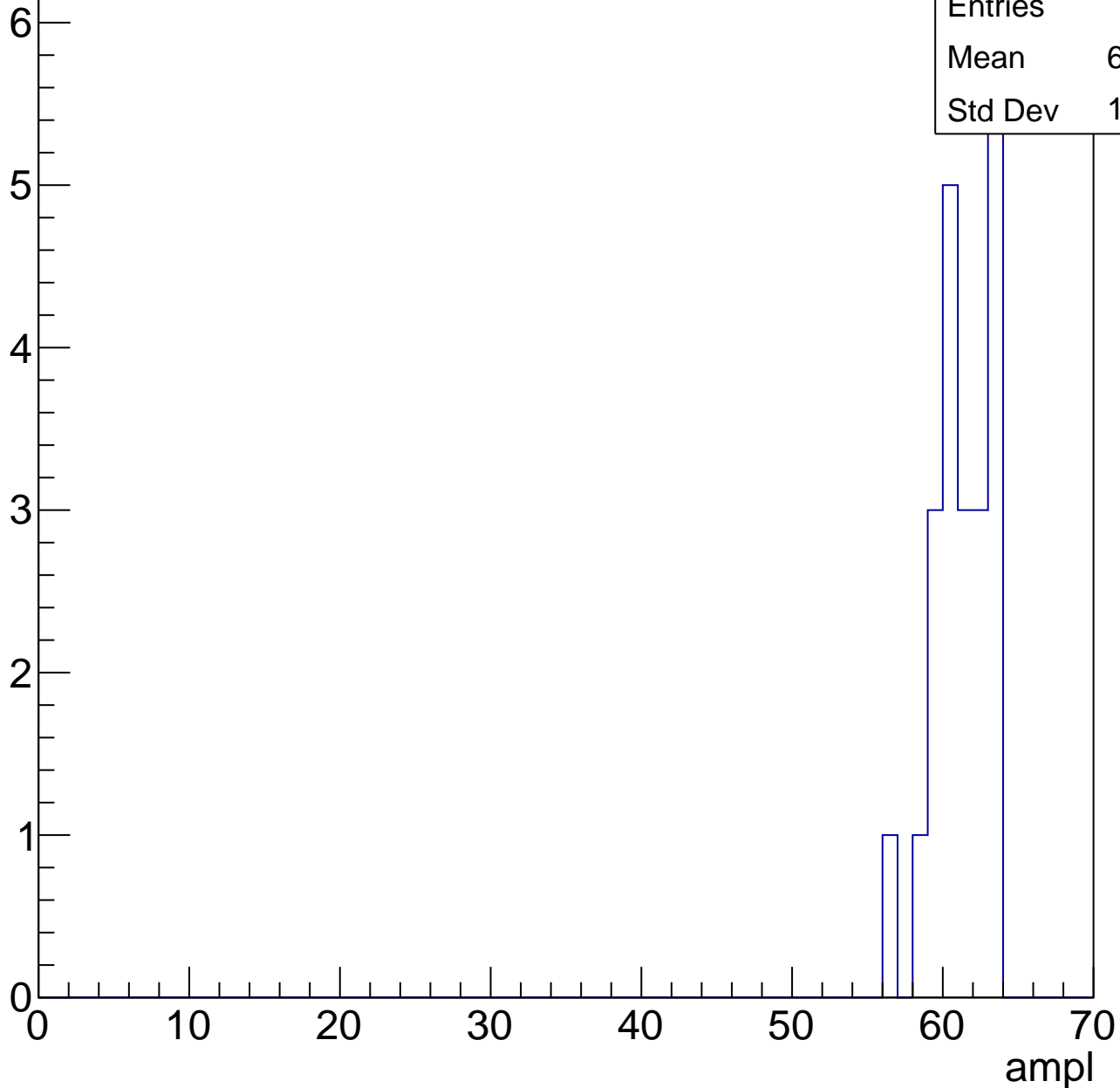


# B1L000S, U21-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	60.82
Std Dev	1.874



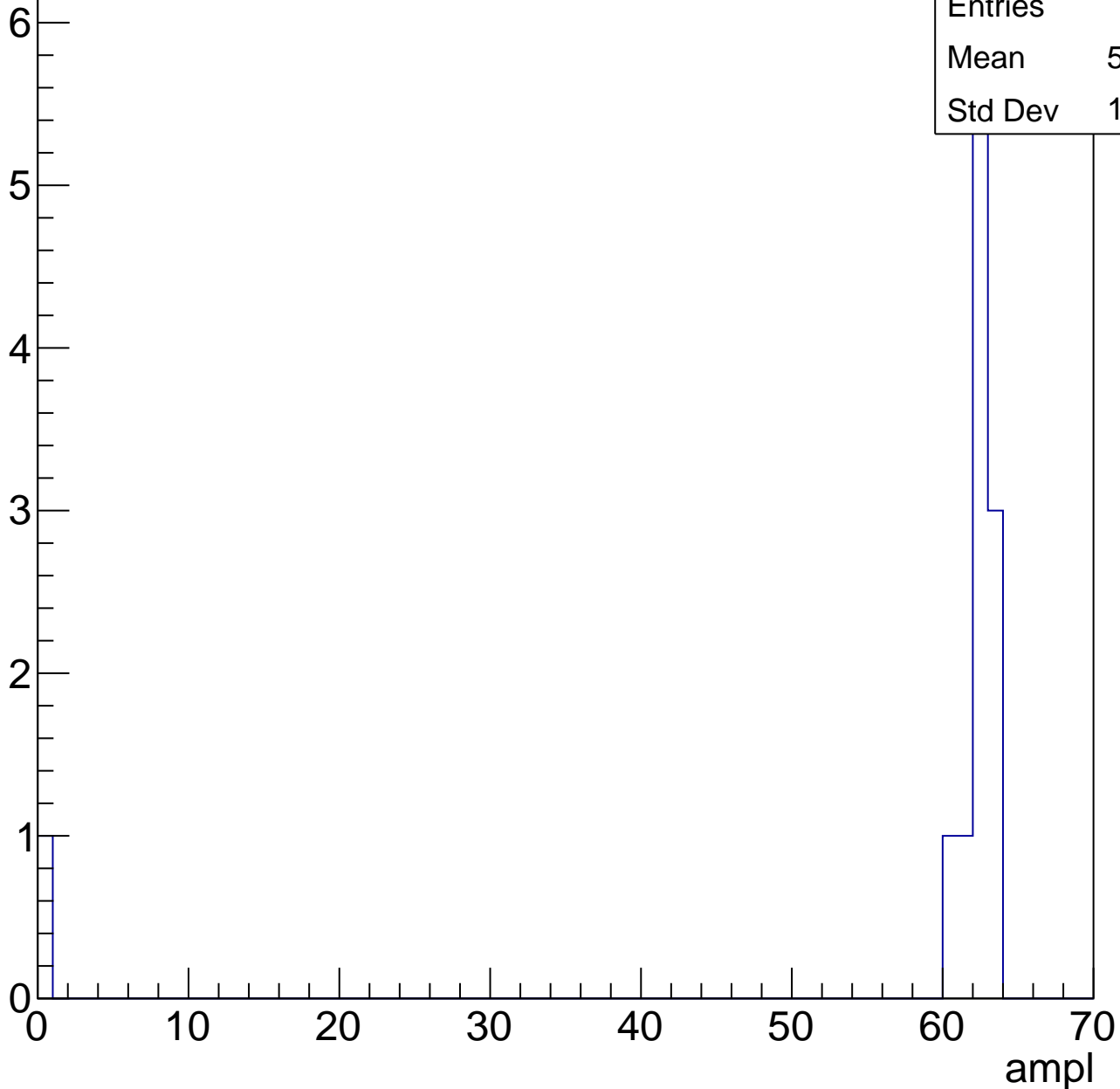


# B1L000S, U21-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	56.83
Std Dev	17.16



# B1L000S, U21-ch103, adc0

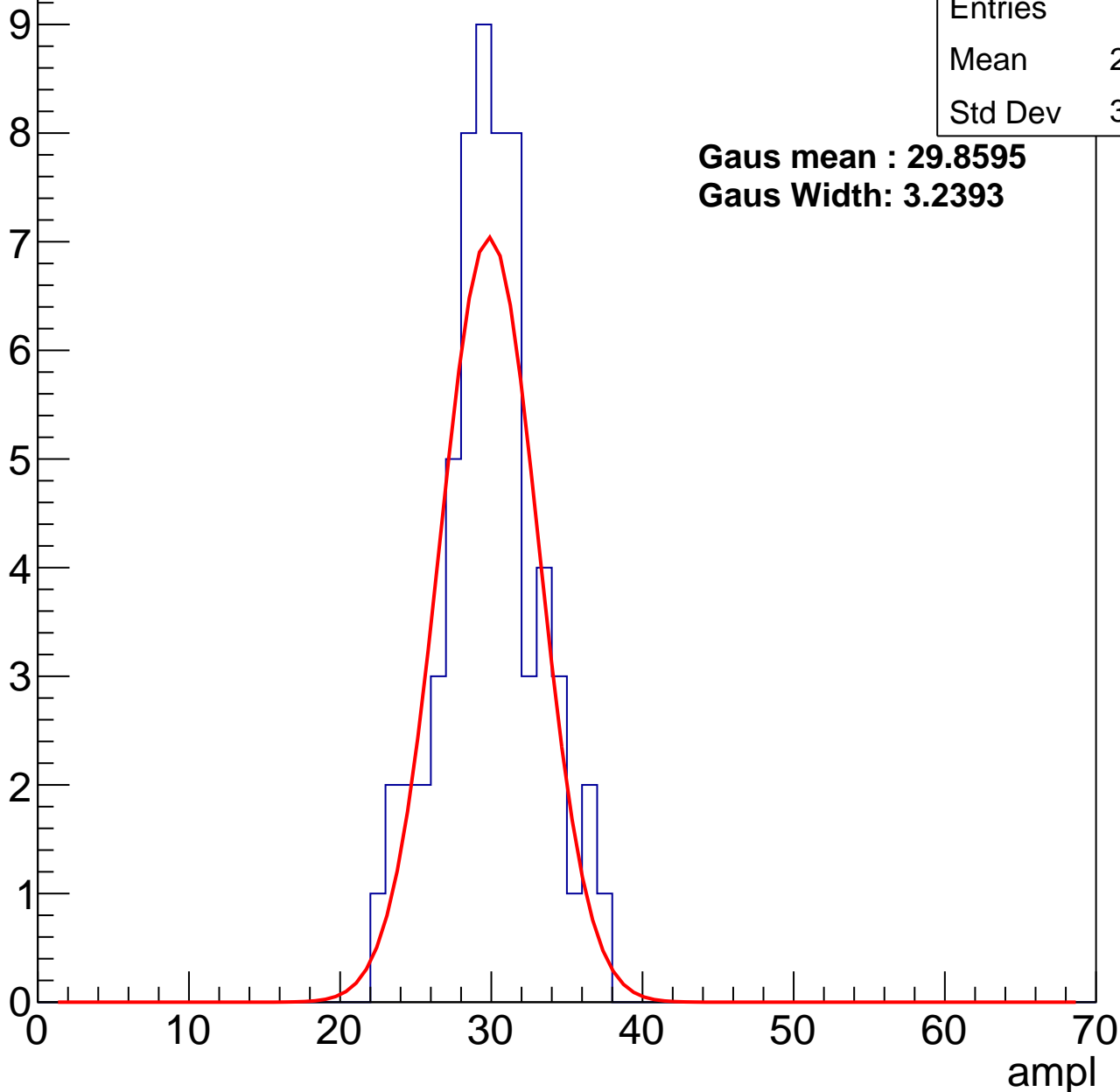
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	29.45
Std Dev	3.236

**Gaus mean : 29.8595**

**Gaus Width: 3.2393**



# B1L000S, U21-ch103, adc1

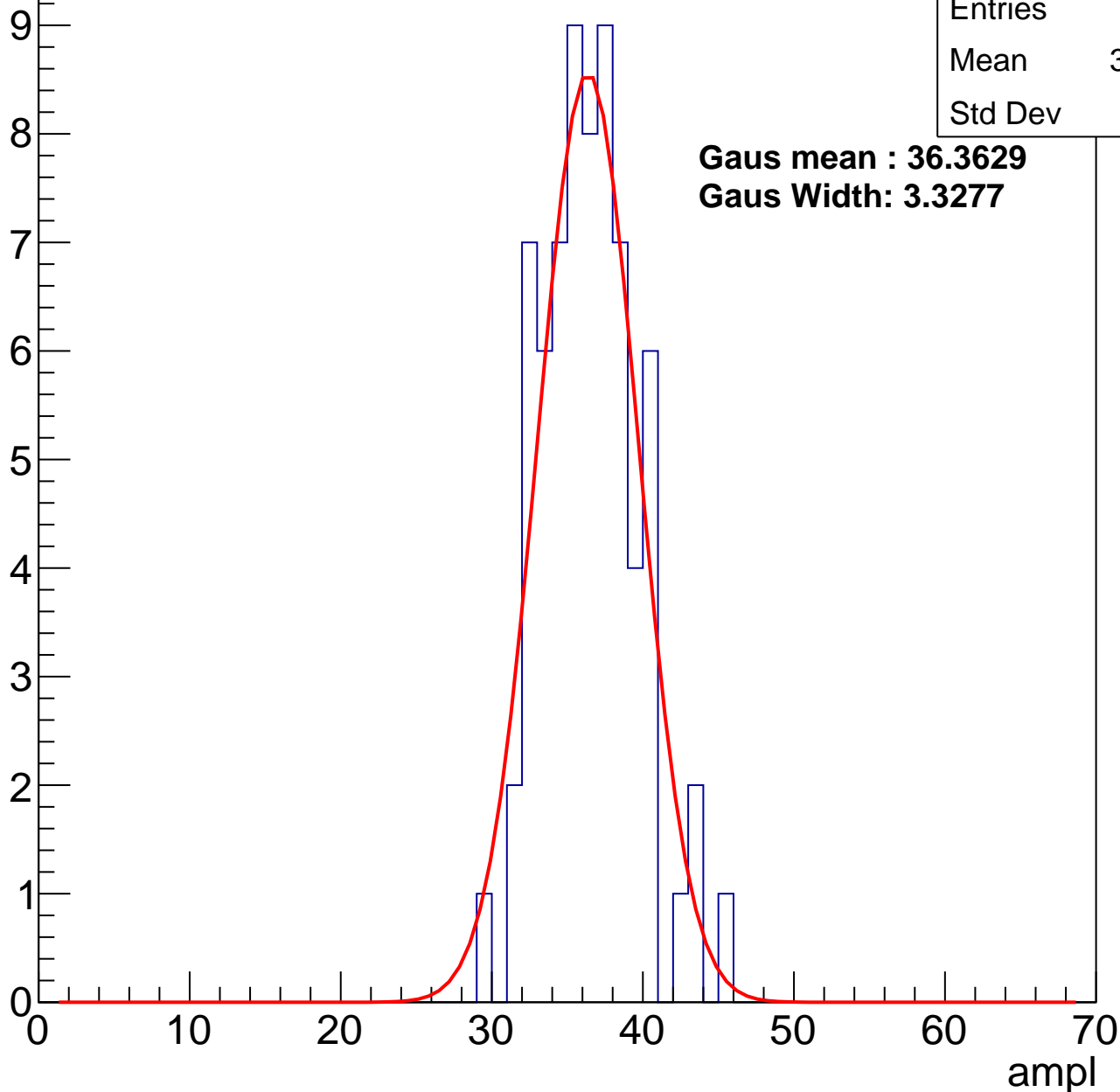
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.03
Std Dev	3.13

**Gaus mean : 36.3629**

**Gaus Width: 3.3277**



# B1L000S, U21-ch103, adc2

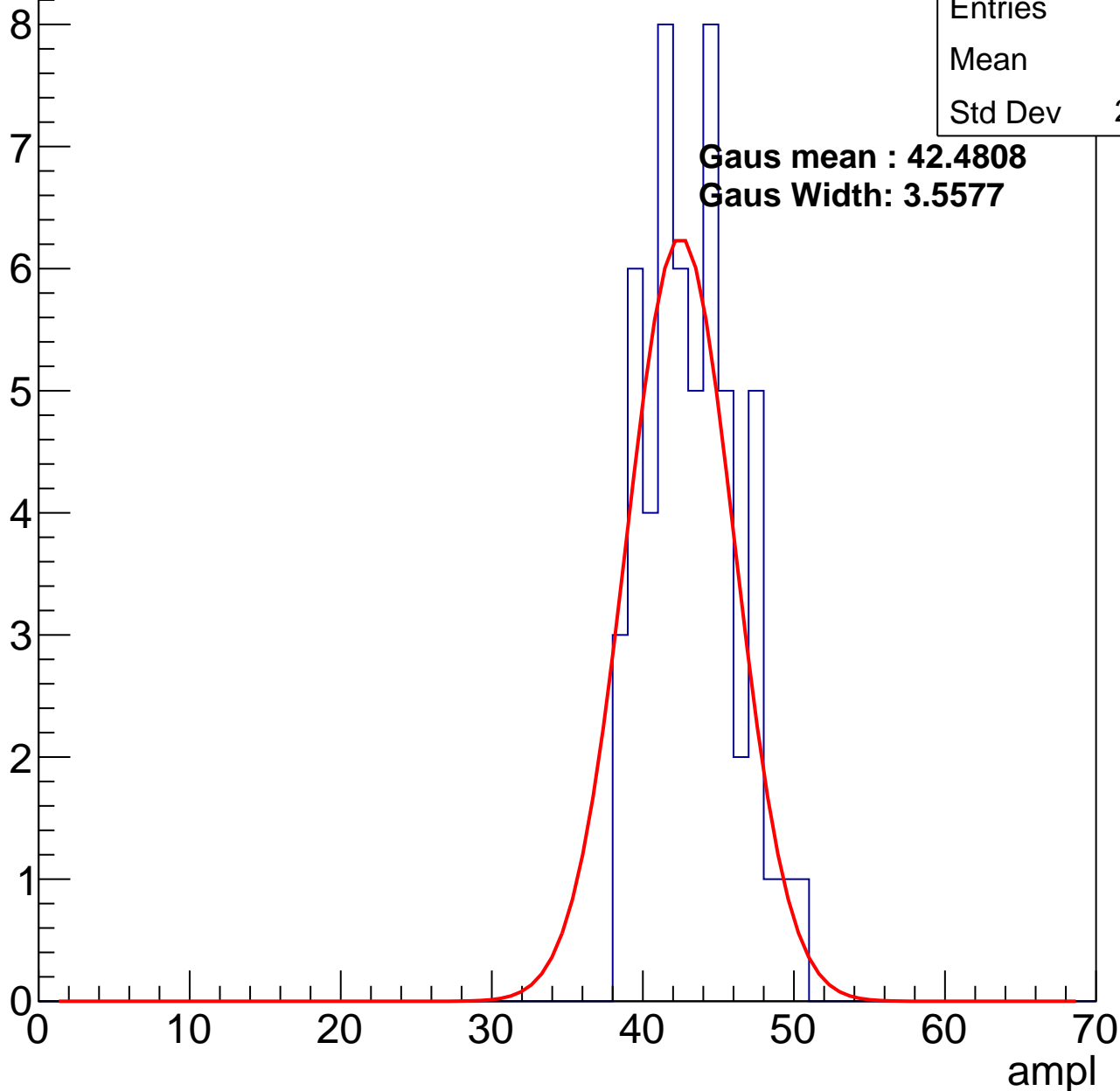
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	42.8
Std Dev	2.951

**Gaus mean : 42.4808**

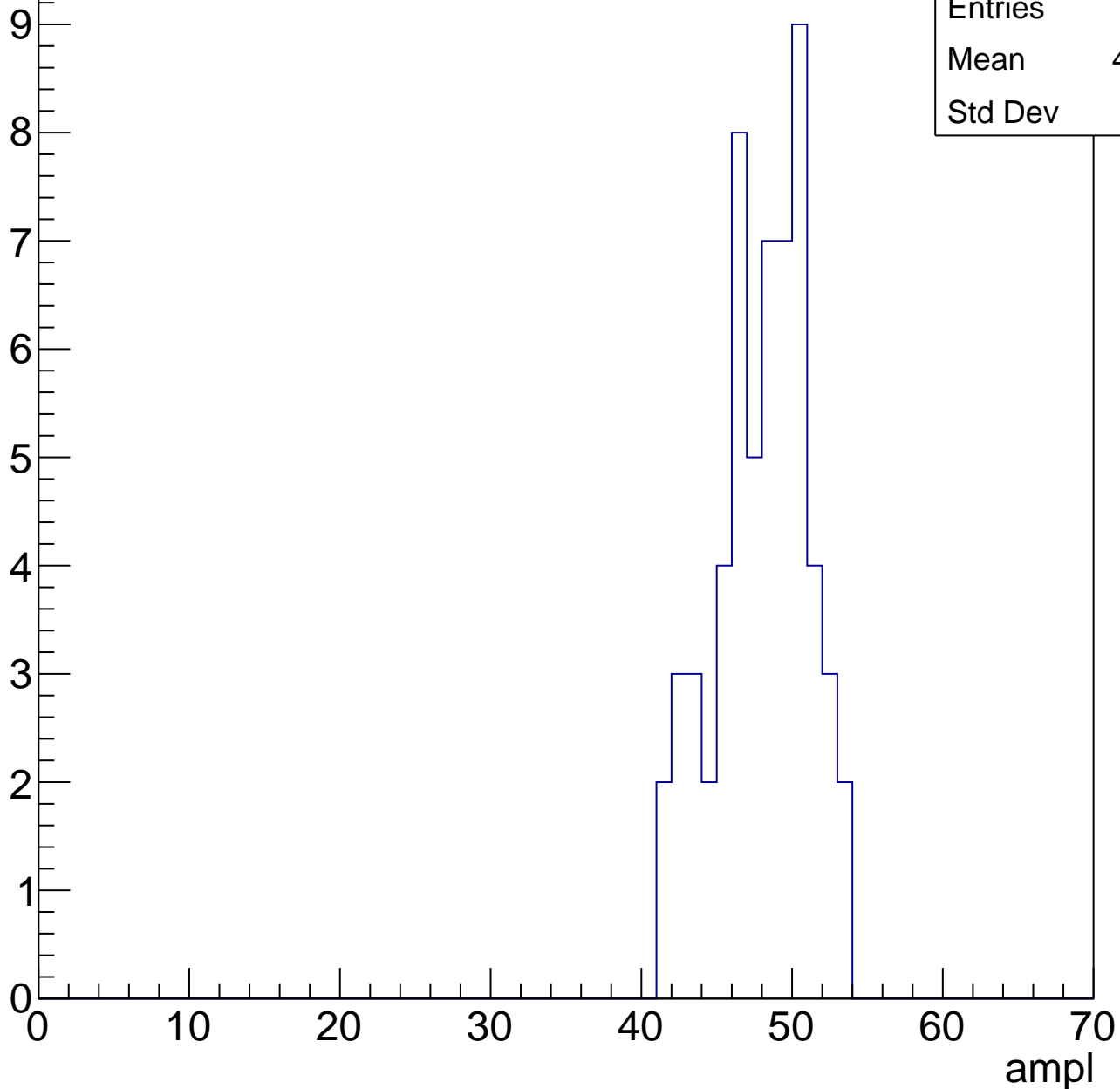
**Gaus Width: 3.5577**



# B1L000S, U21-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	59
Mean	47.51
Std Dev	3.05

# B1L000S, U21-ch103, adc4

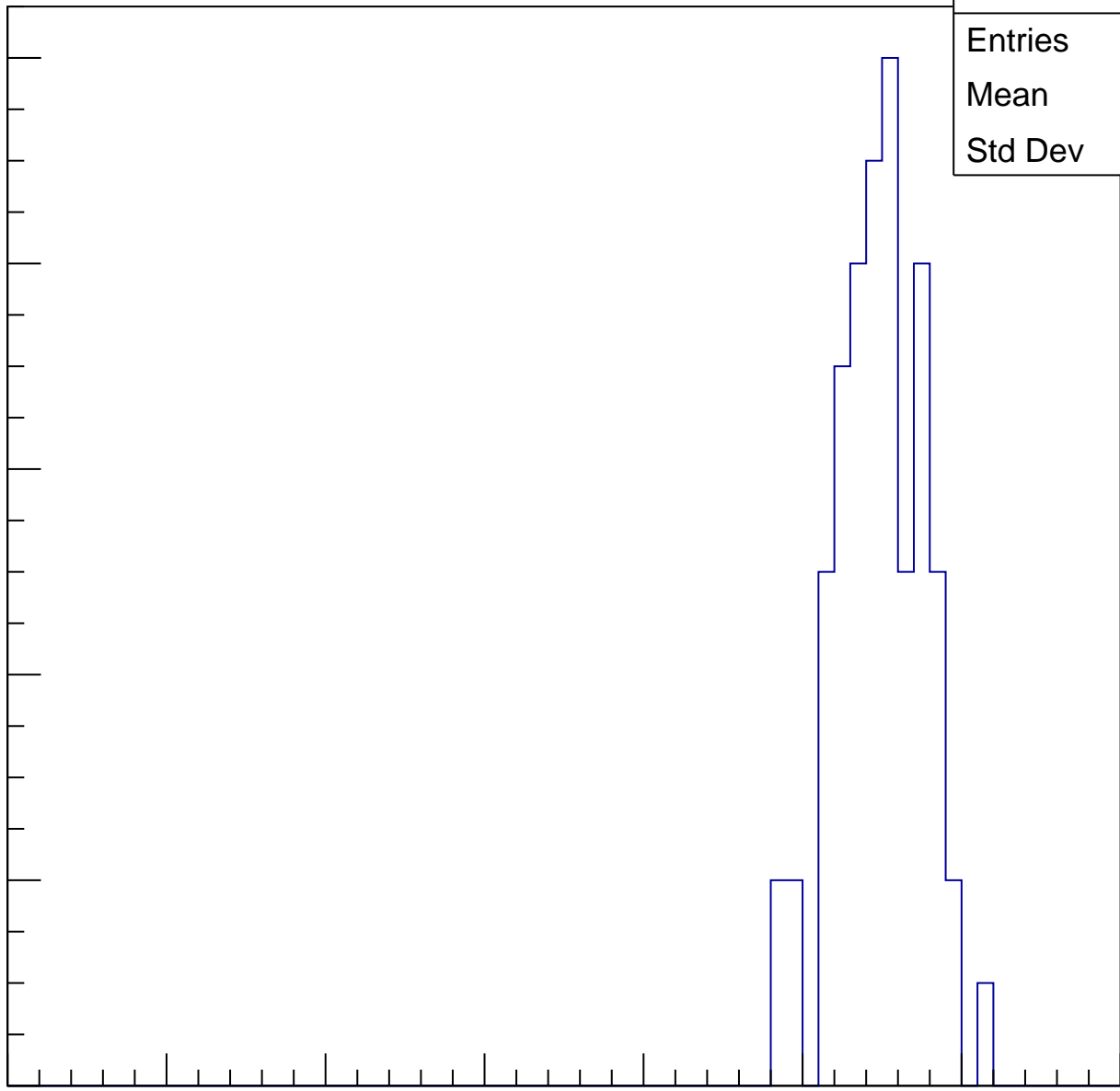
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	54.34
Std Dev	2.728

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70  
ampl



# B1L000S, U21-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

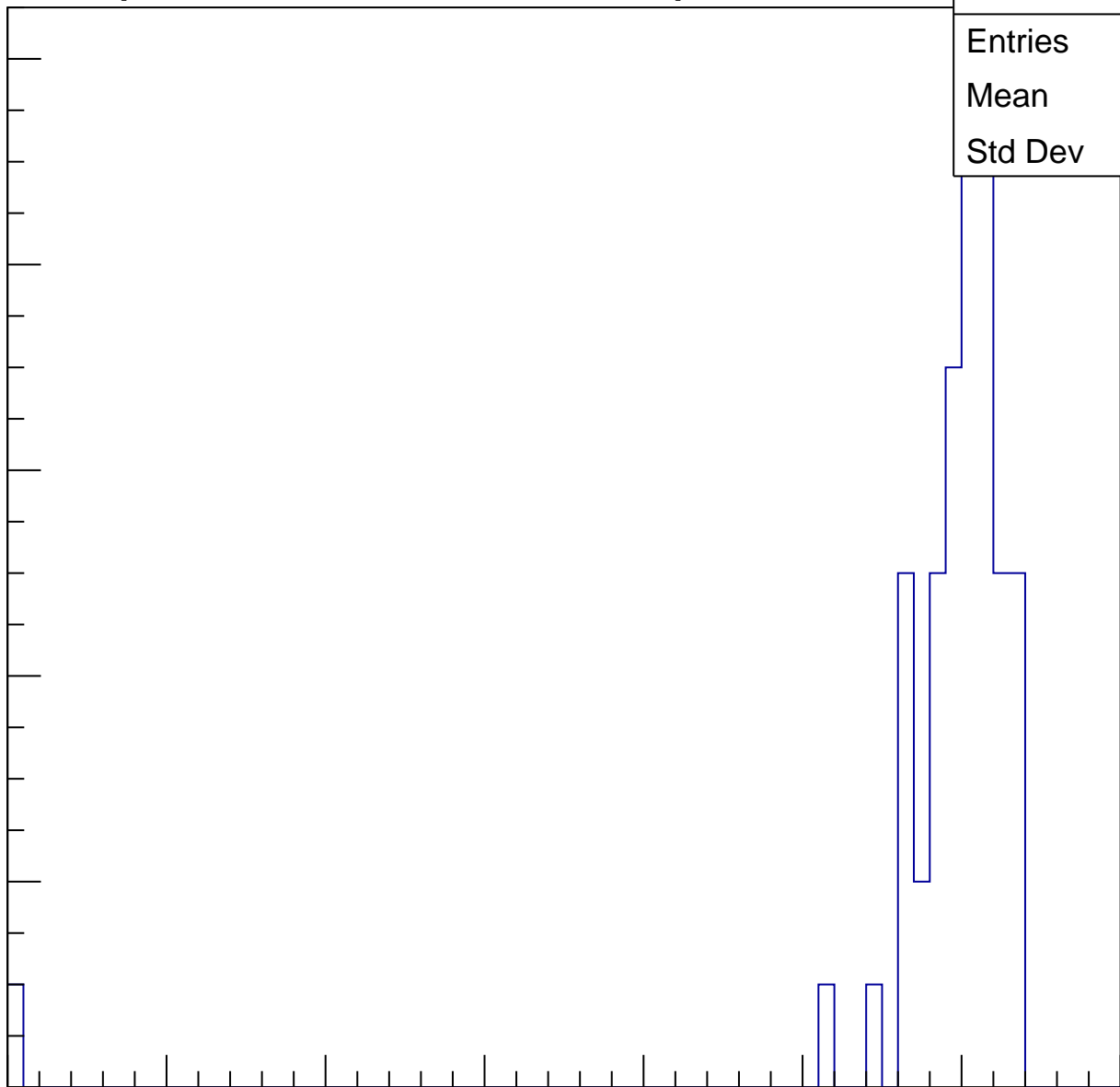
Entries	51
Mean	58.35
Std Dev	8.606

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

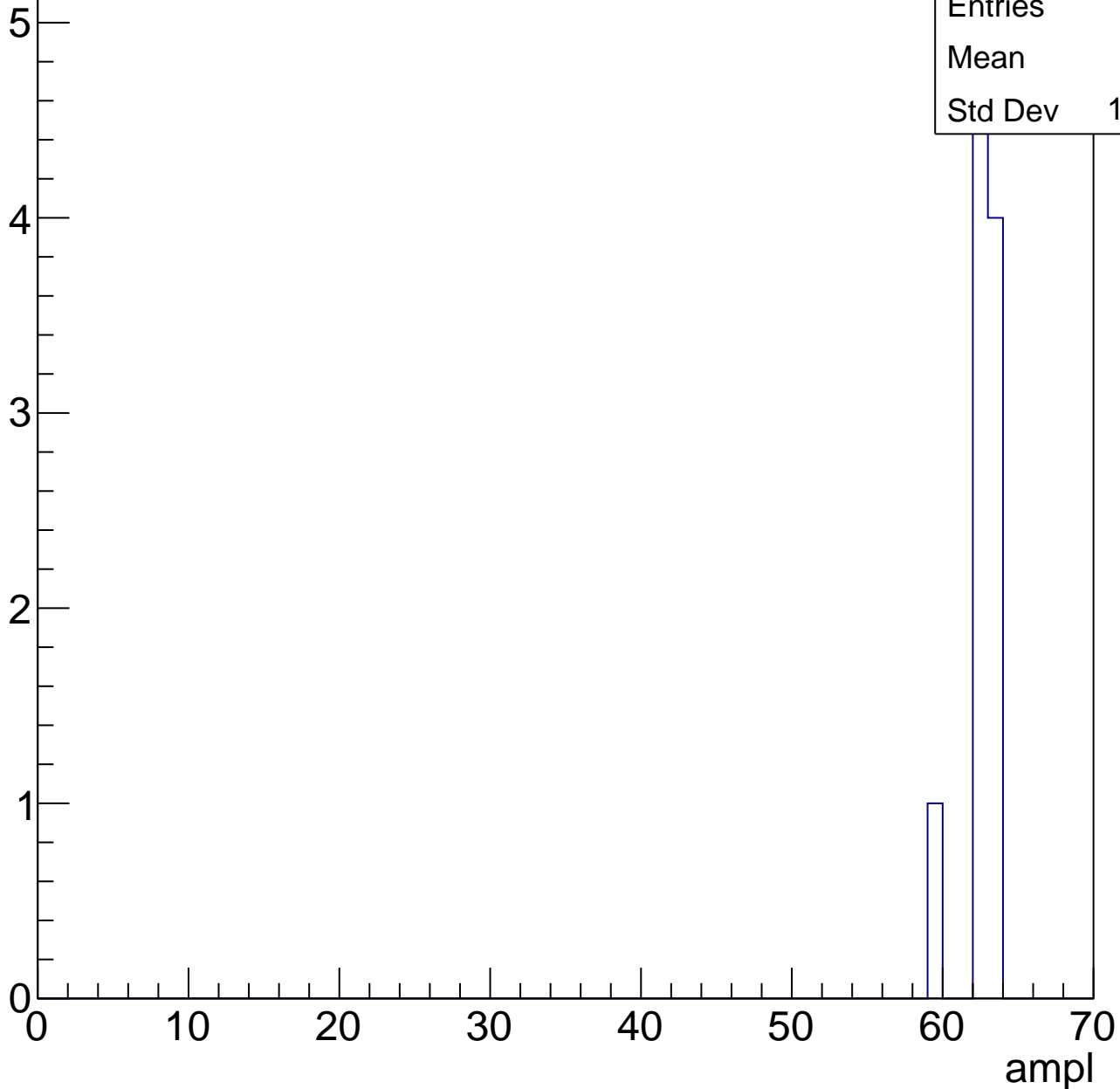


# B1L000S, U21-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	10
Mean	62.1
Std Dev	1.136

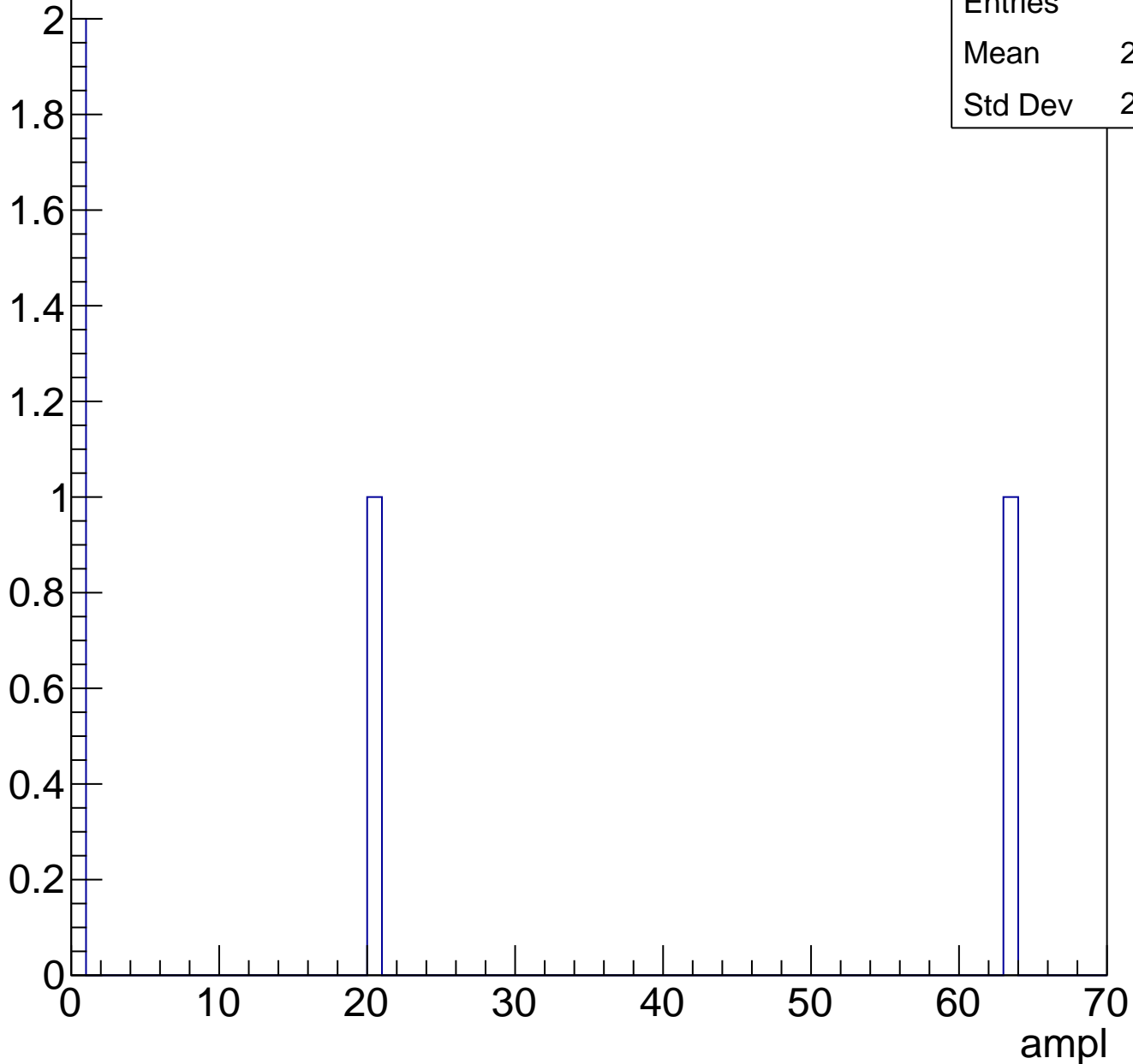




# B1L000S, U21-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	20.75
Std Dev	25.72

# B1L000S, U21-ch104, adc0

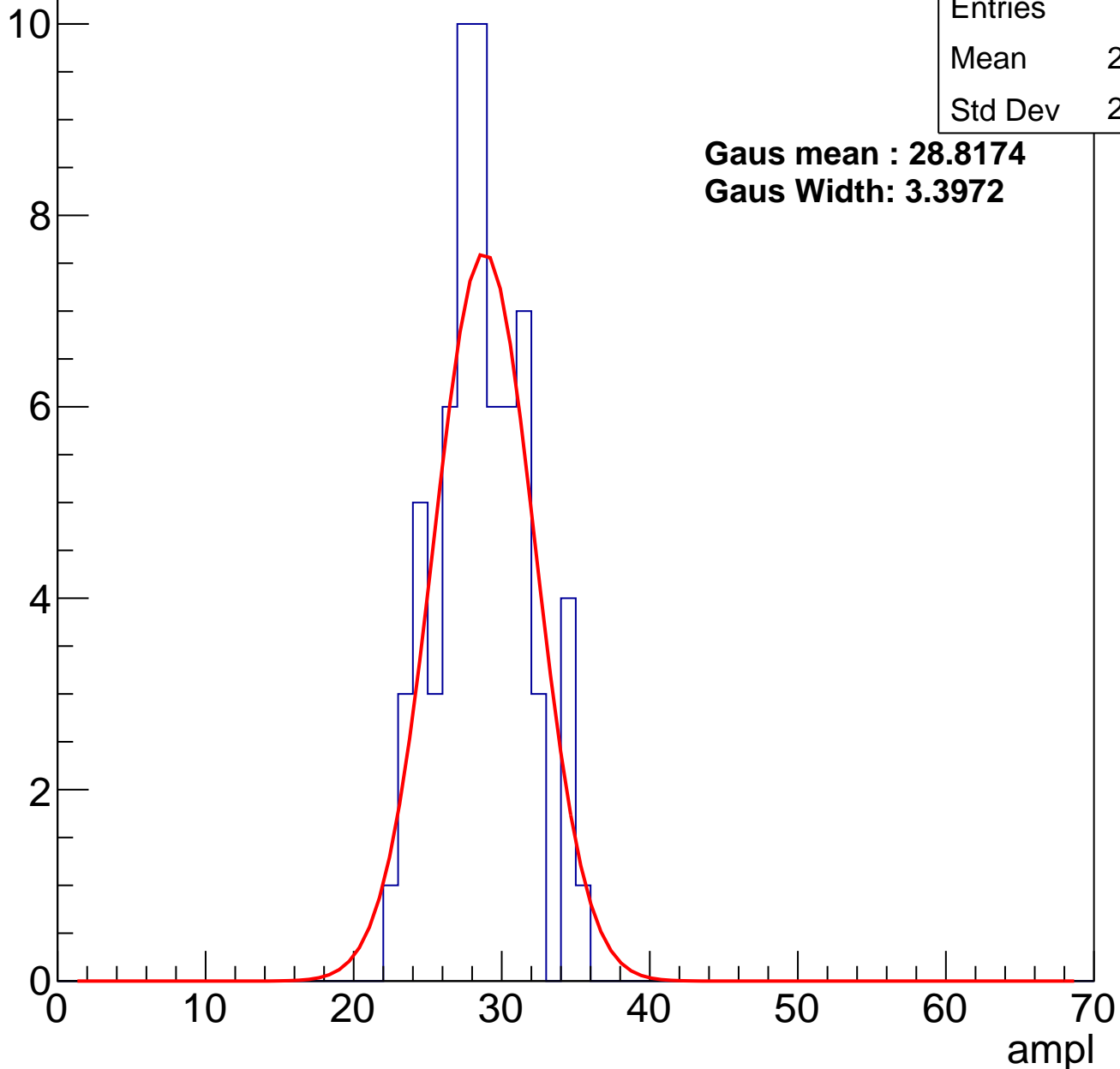
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	28.15
Std Dev	2.999

**Gaus mean : 28.8174**

**Gaus Width: 3.3972**

Entry



# B1L000S, U21-ch104, adc1

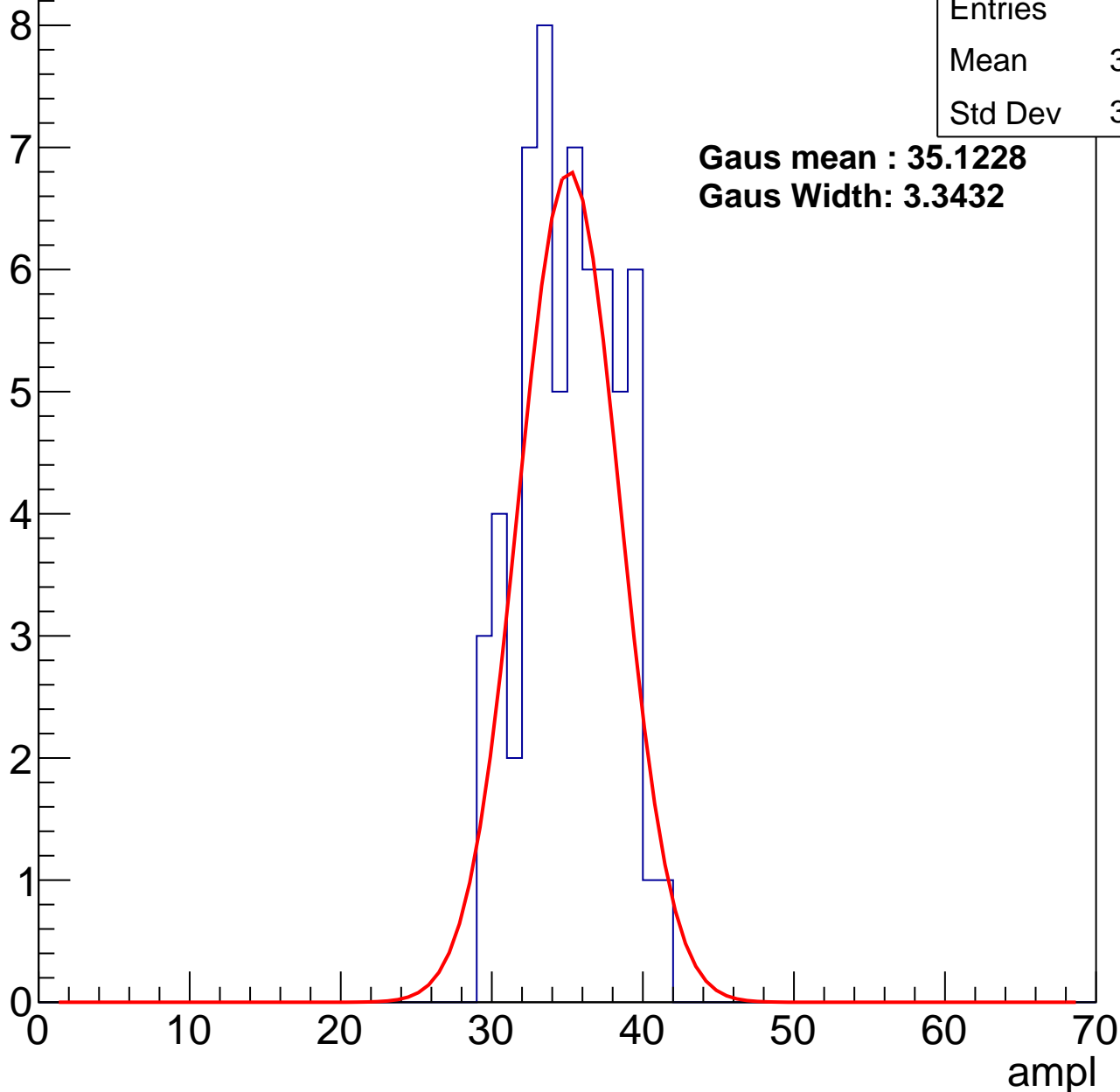
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	34.67
Std Dev	3.045

**Gaus mean : 35.1228**

**Gaus Width: 3.3432**



# B1L000S, U21-ch104, adc2

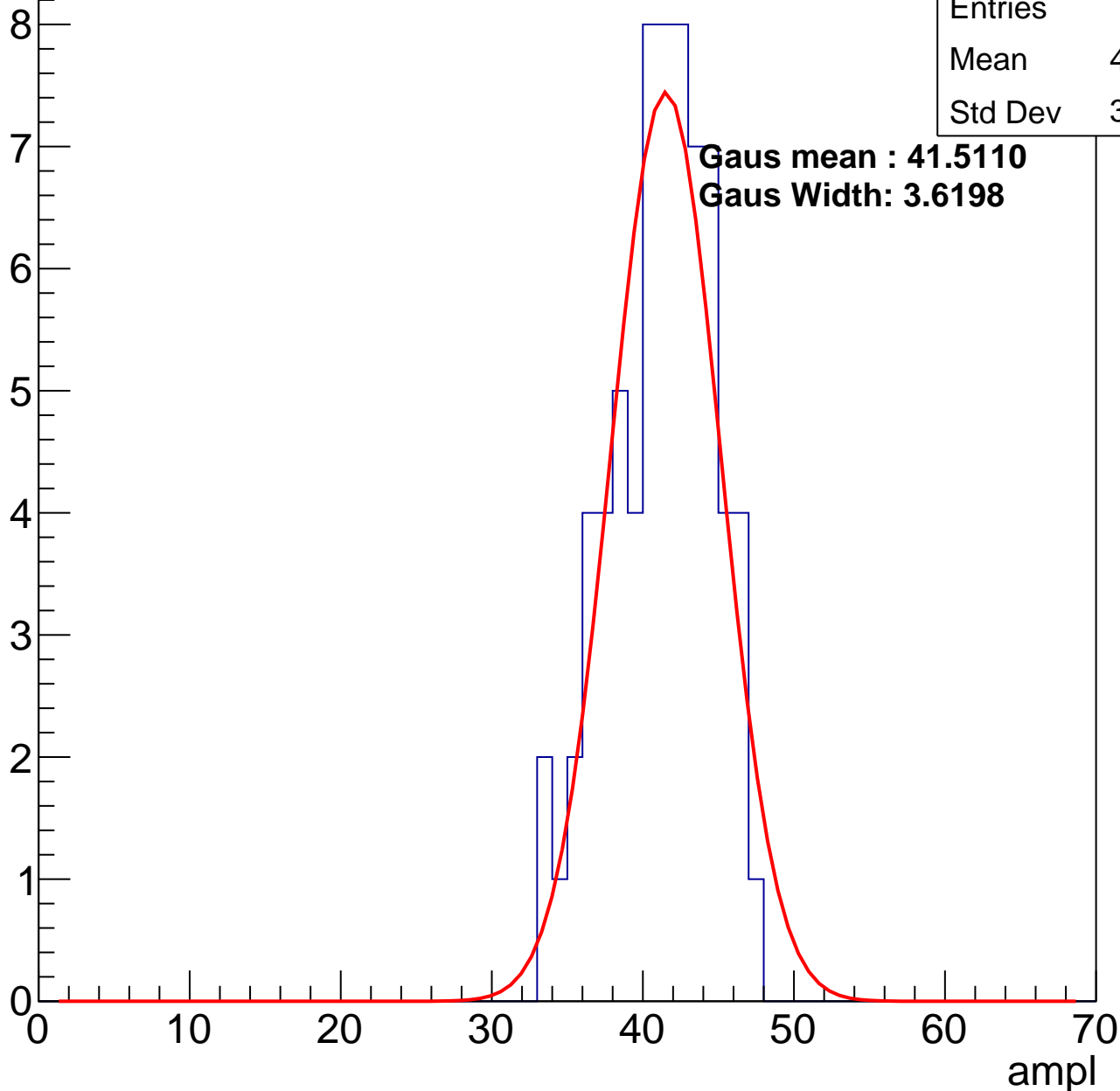
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	40.75
Std Dev	3.355

**Gaus mean : 41.5110**

**Gaus Width: 3.6198**

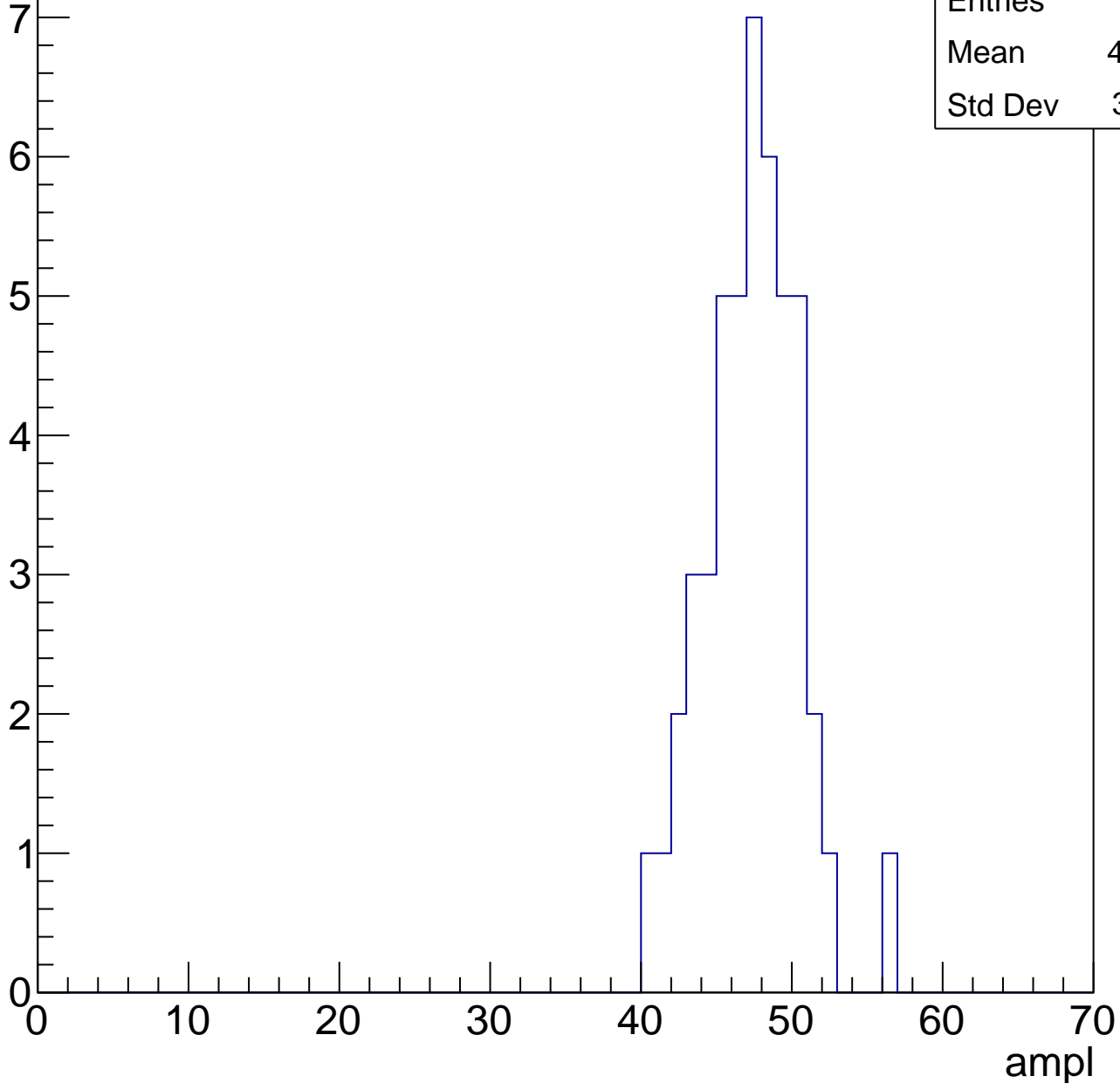


# B1L000S, U21-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	46.87
Std Dev	3.071

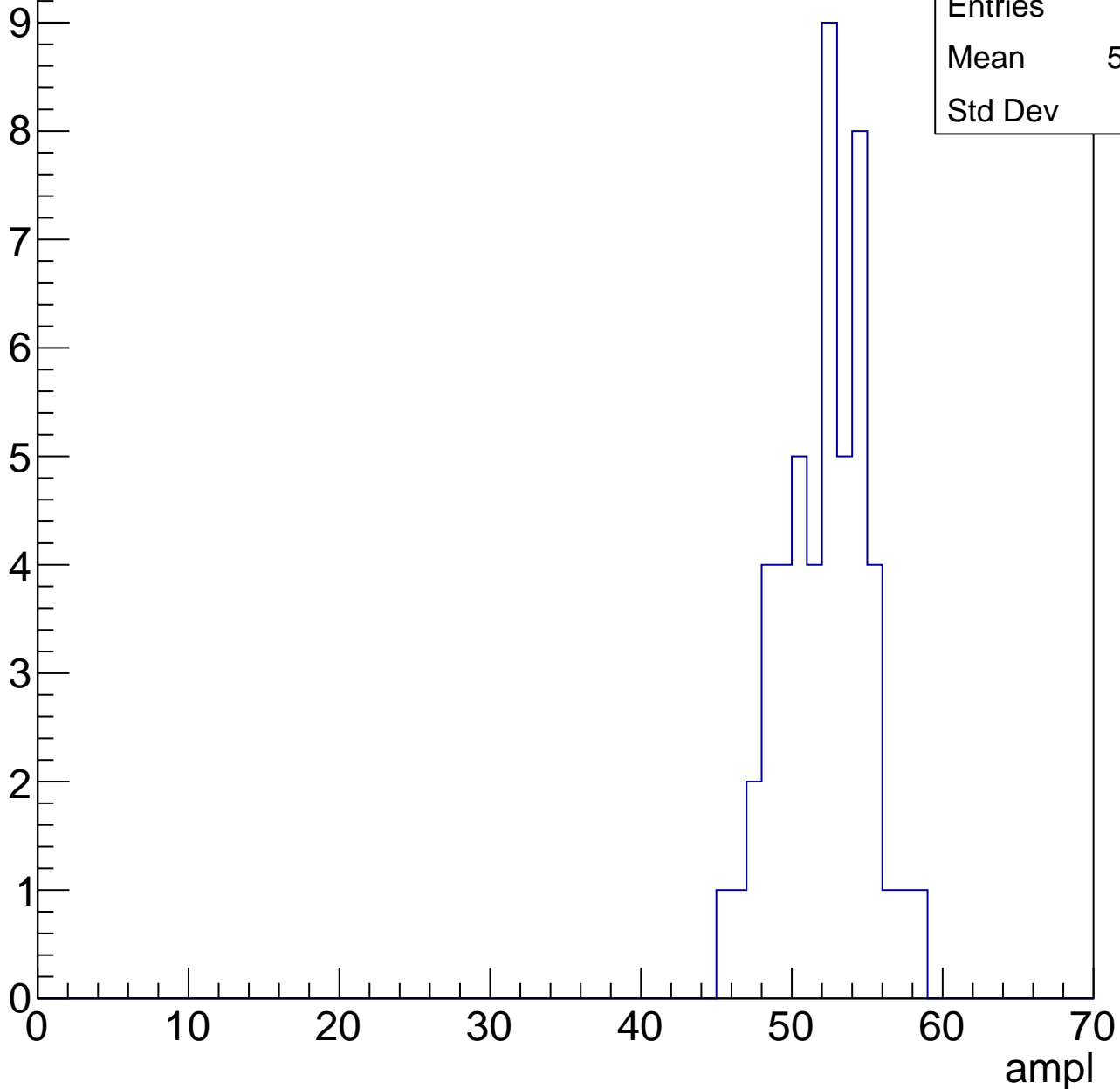


# B1L000S, U21-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	51.66
Std Dev	2.84

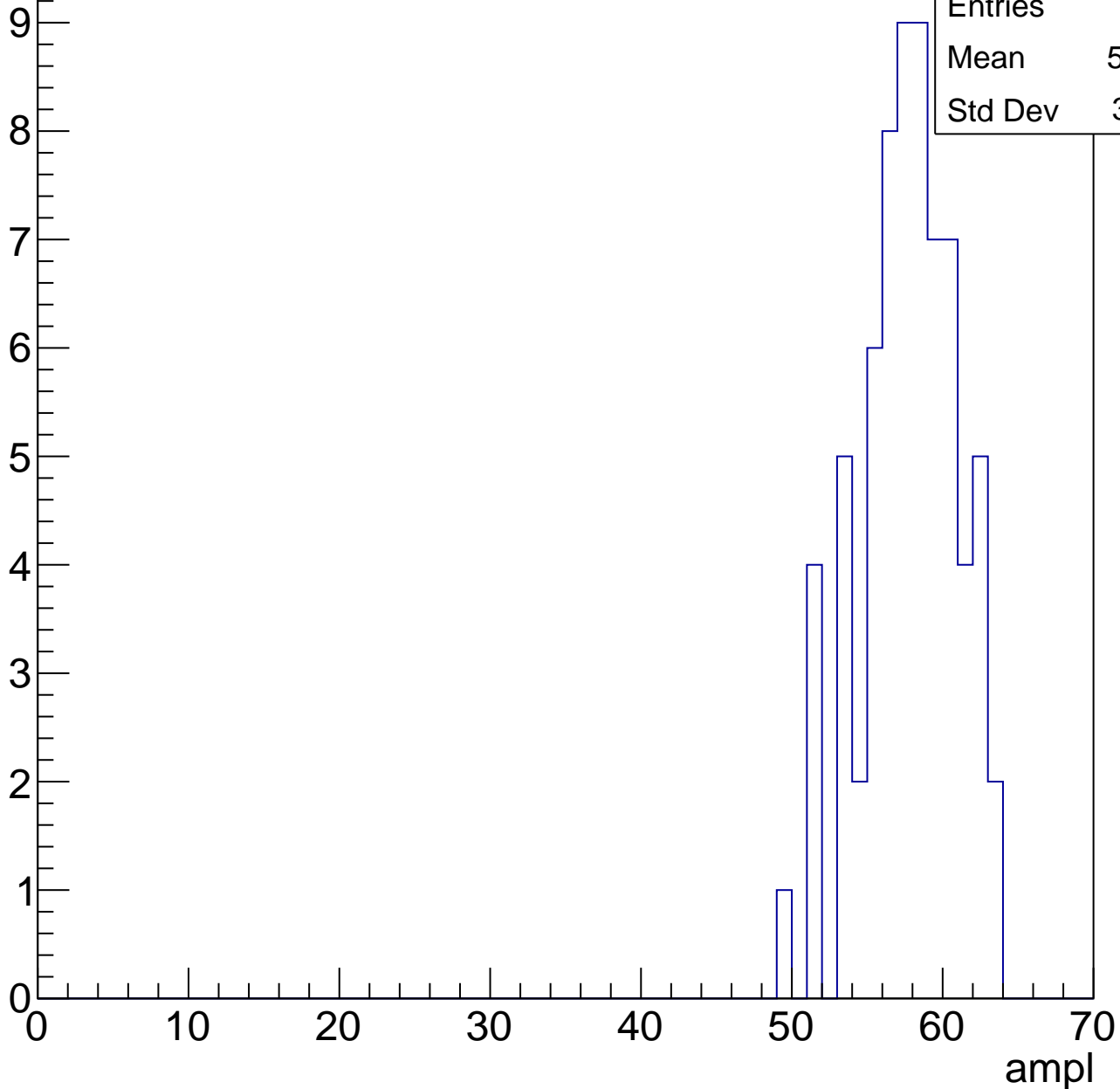


# B1L000S, U21-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	57.28
Std Dev	3.171

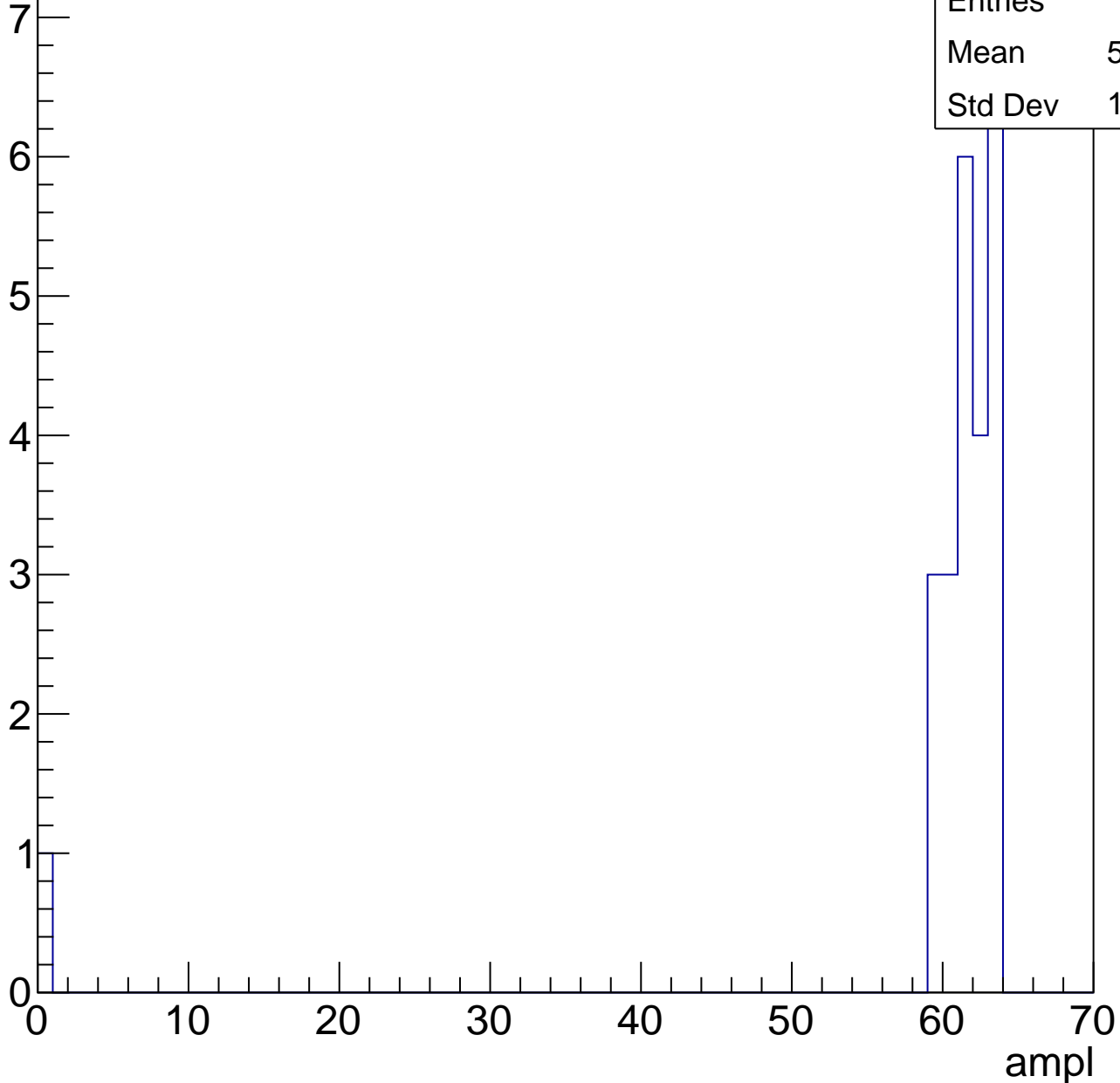


# B1L000S, U21-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.83
Std Dev	12.34

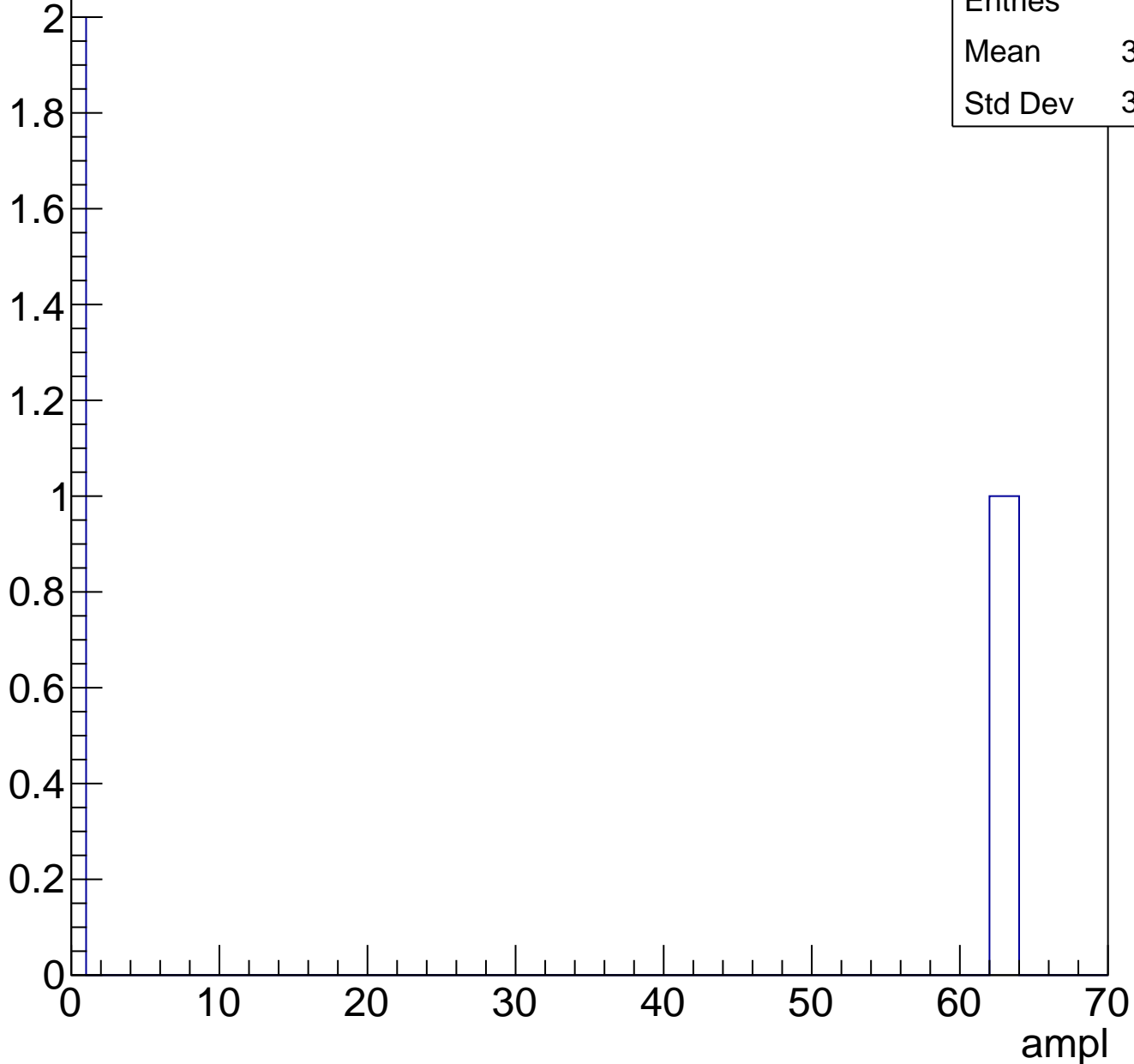




# B1L000S, U21-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch105, adc0

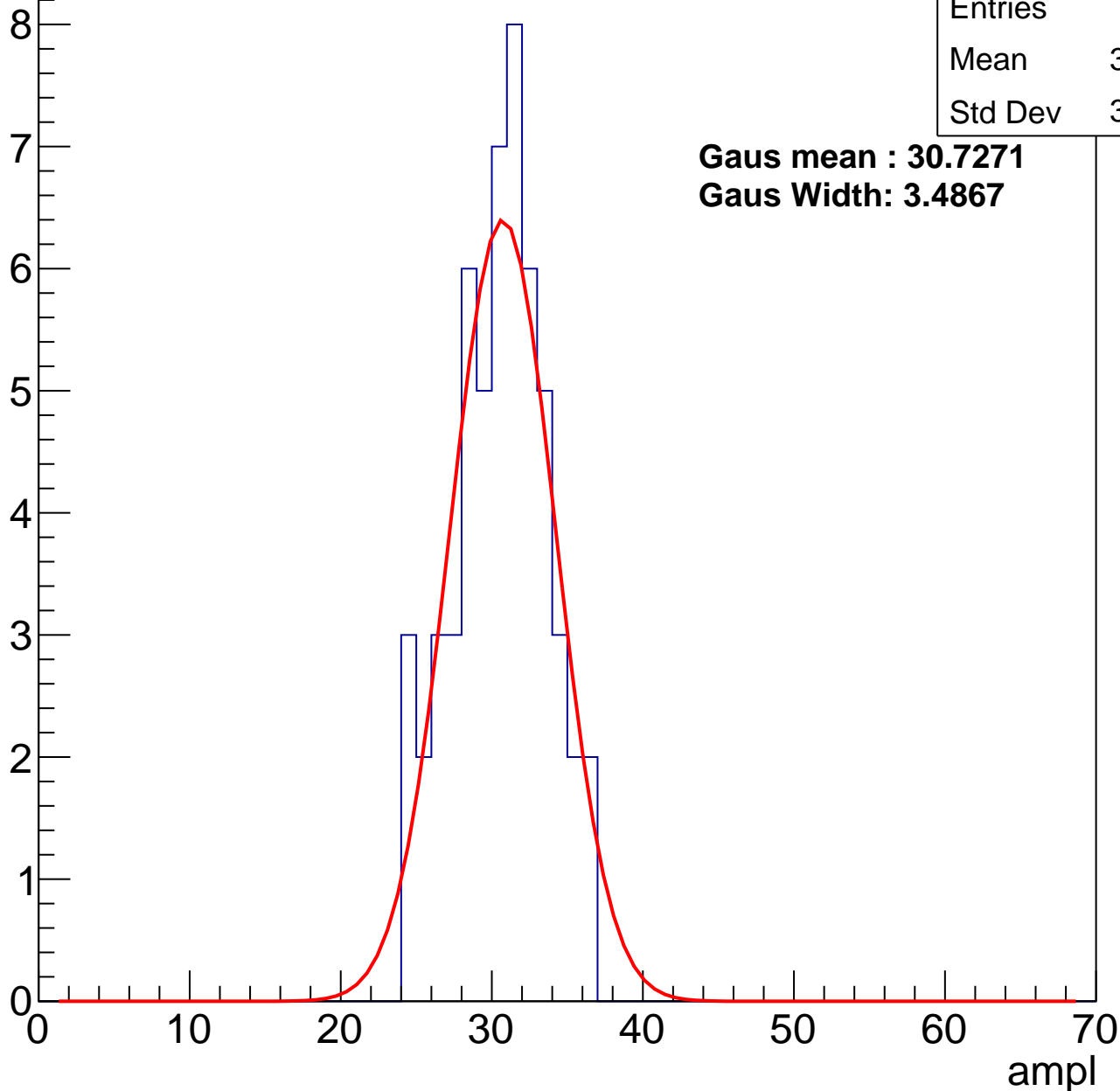
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	30.05
Std Dev	3.042

**Gaus mean : 30.7271**

**Gaus Width: 3.4867**



# B1L000S, U21-ch105, adc1

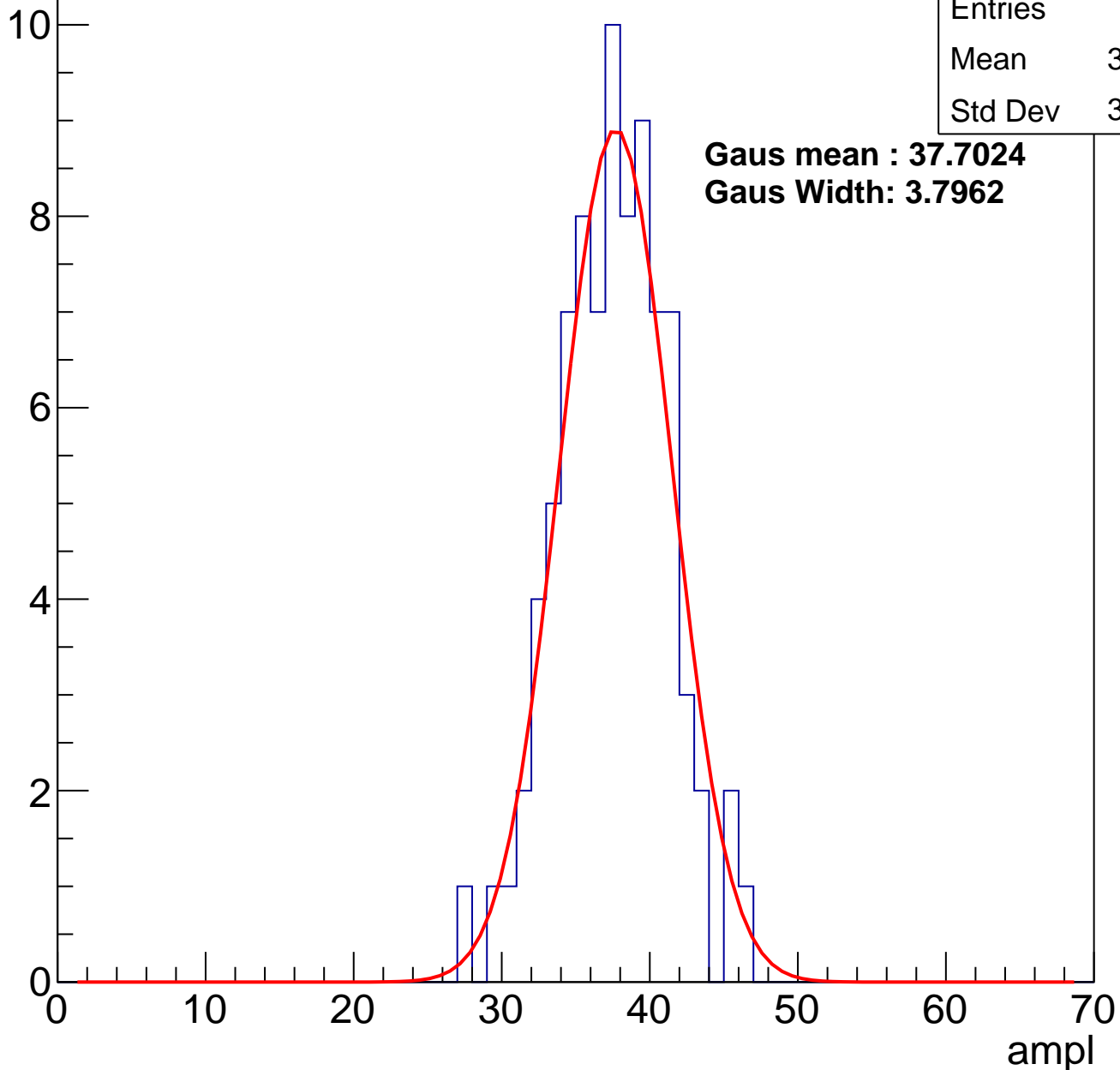
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	85
Mean	37.07
Std Dev	3.665

**Gaus mean : 37.7024**

**Gaus Width: 3.7962**

Entry



# B1L000S, U21-ch105, adc2

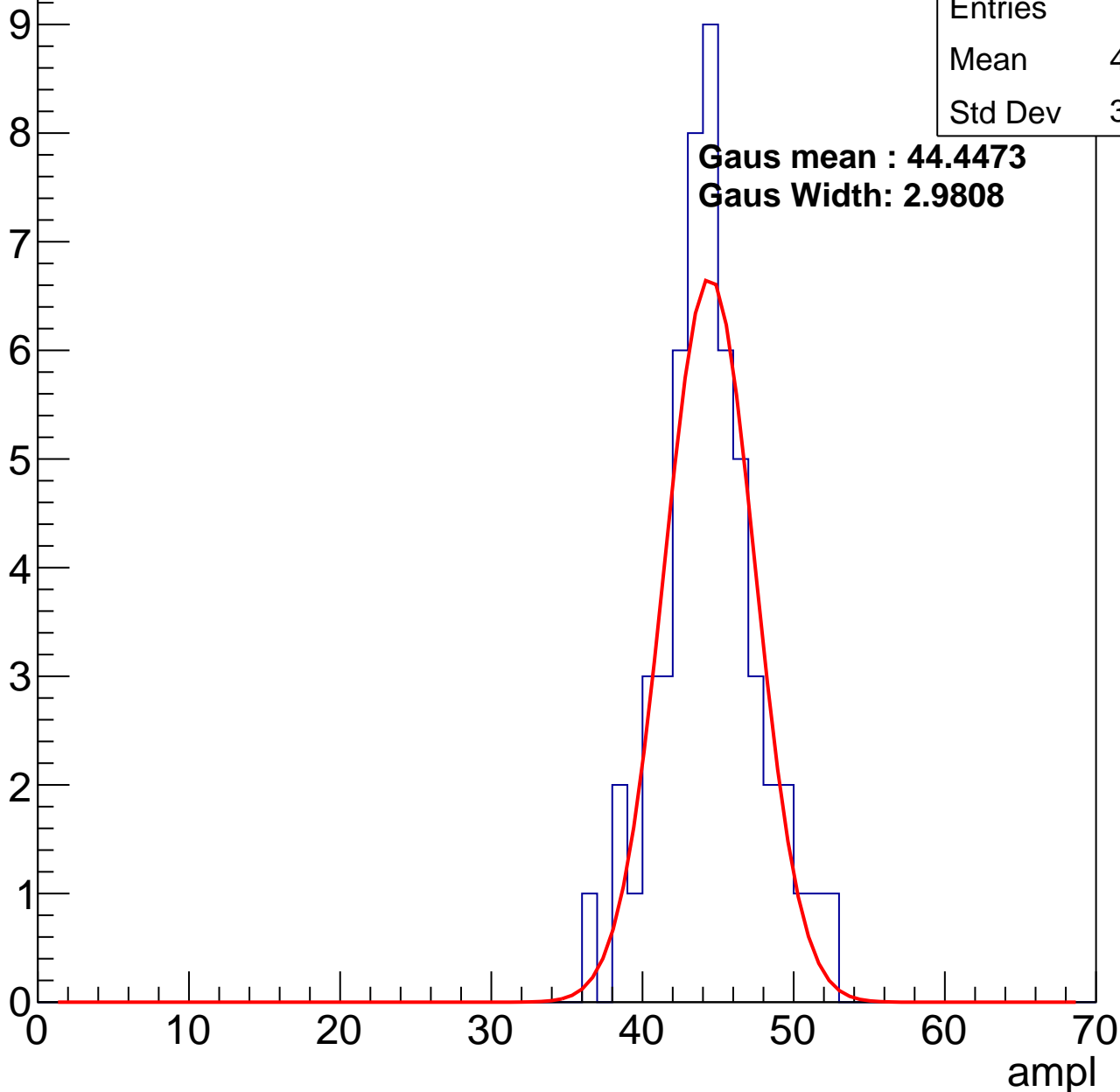
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	43.96
Std Dev	3.197

**Gaus mean : 44.4473**

**Gaus Width: 2.9808**

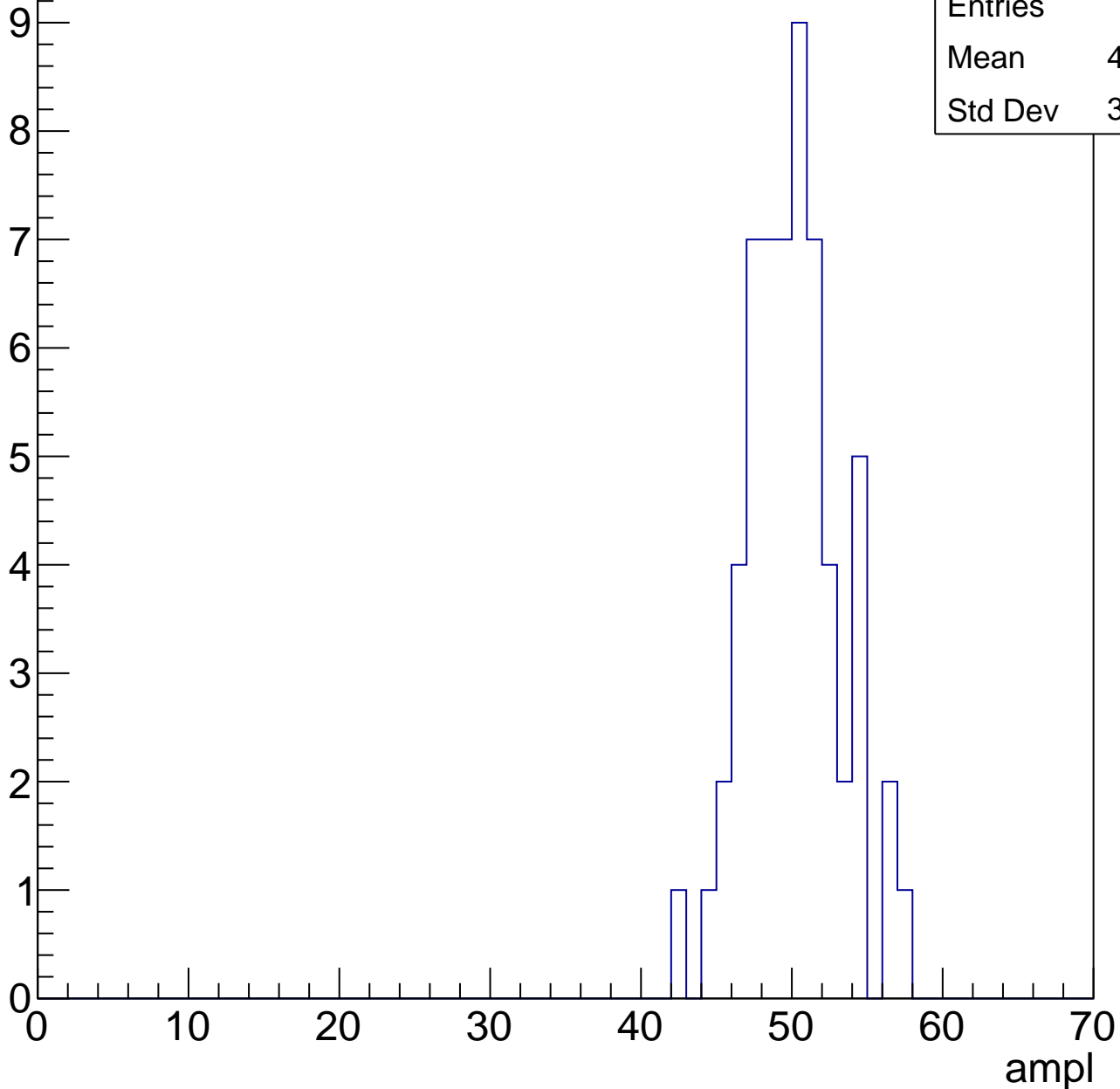


# B1L000S, U21-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	49.63
Std Dev	3.042

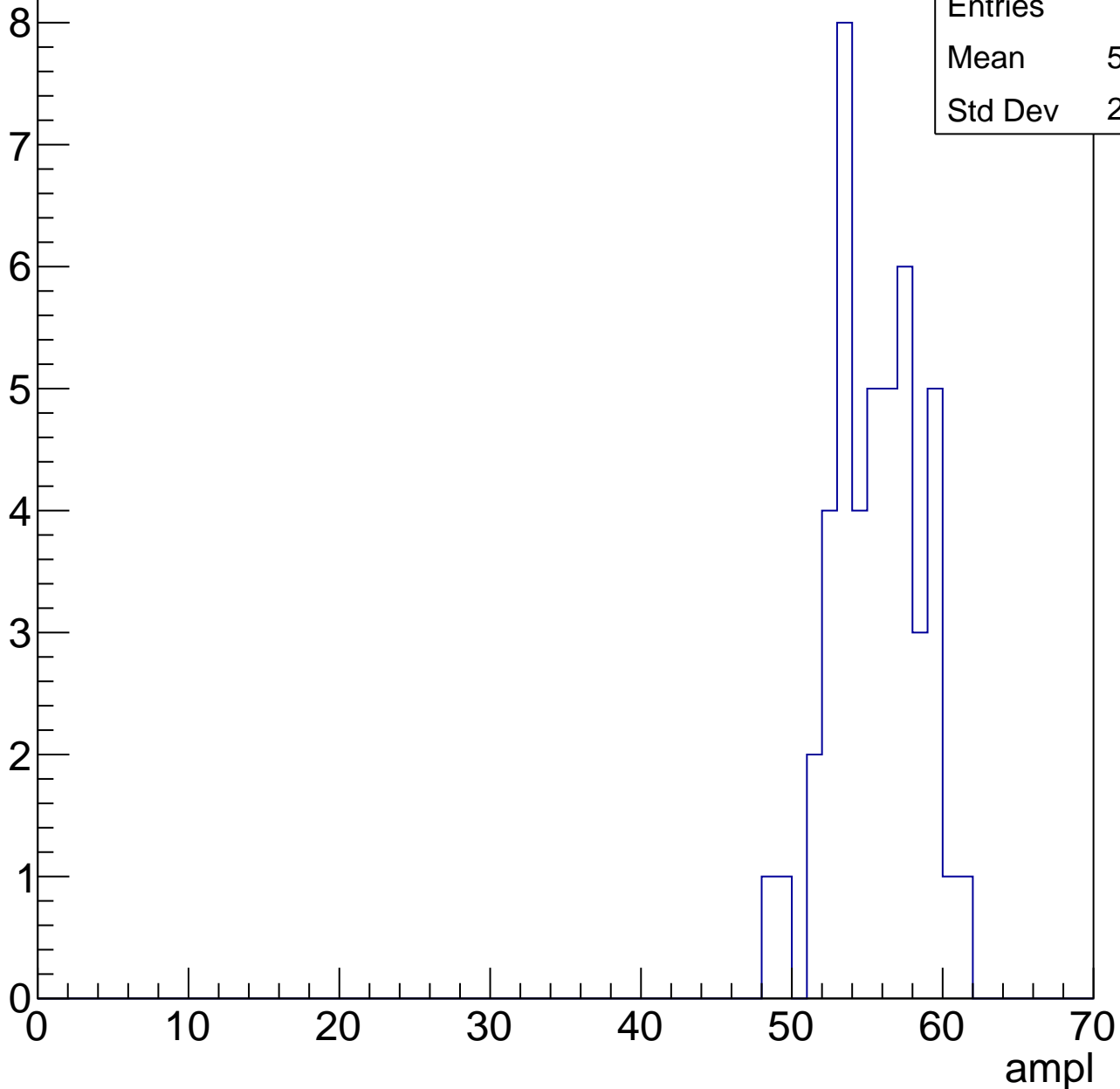


# B1L000S, U21-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	55.09
Std Dev	2.895

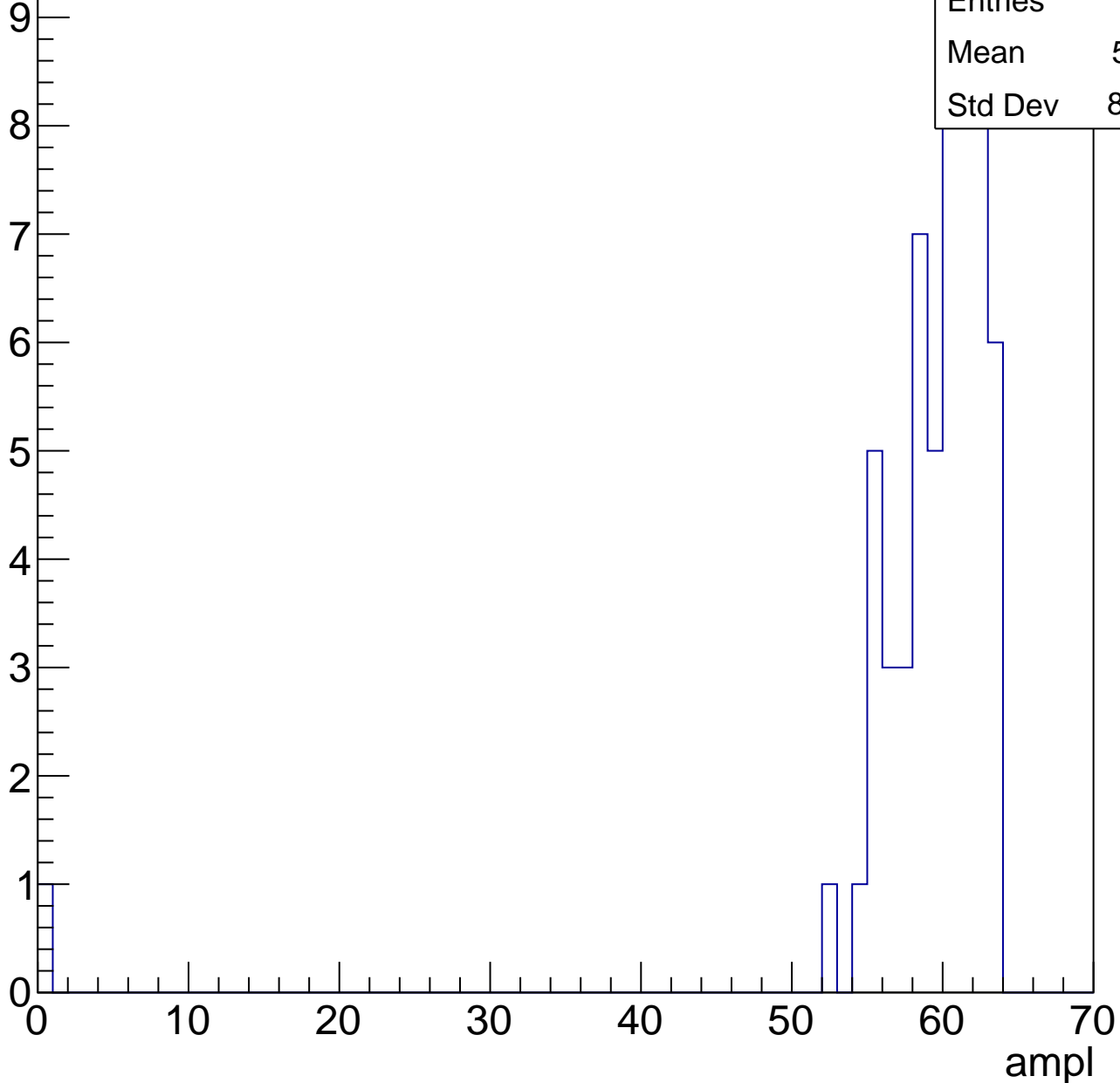


# B1L000S, U21-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	58.41
Std Dev	8.112



# B1L000S, U21-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



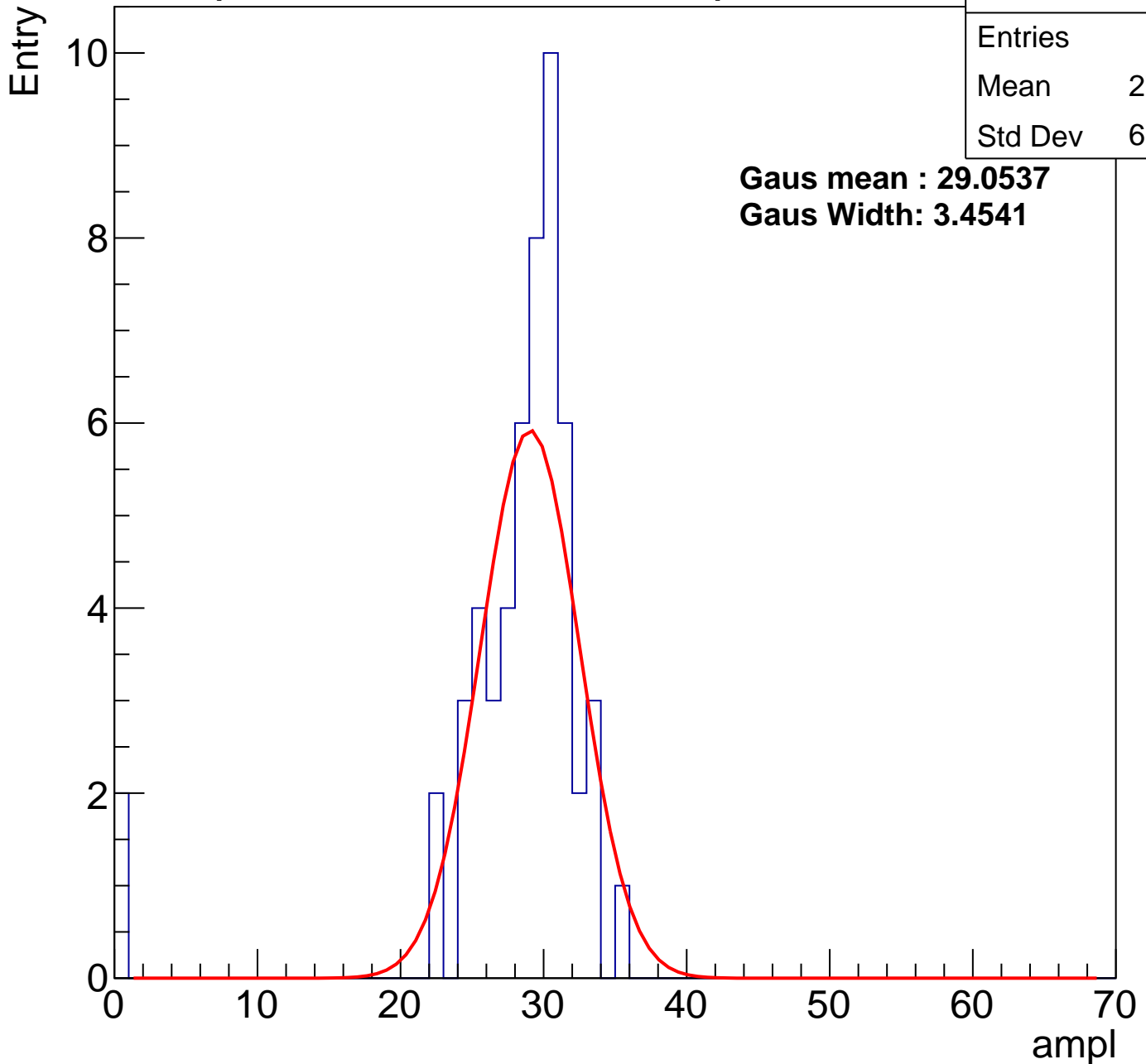
# B1L000S, U21-ch106, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	54
Mean	27.52
Std Dev	6.064

**Gaus mean : 29.0537**

**Gaus Width: 3.4541**



# B1L000S, U21-ch106, adc1

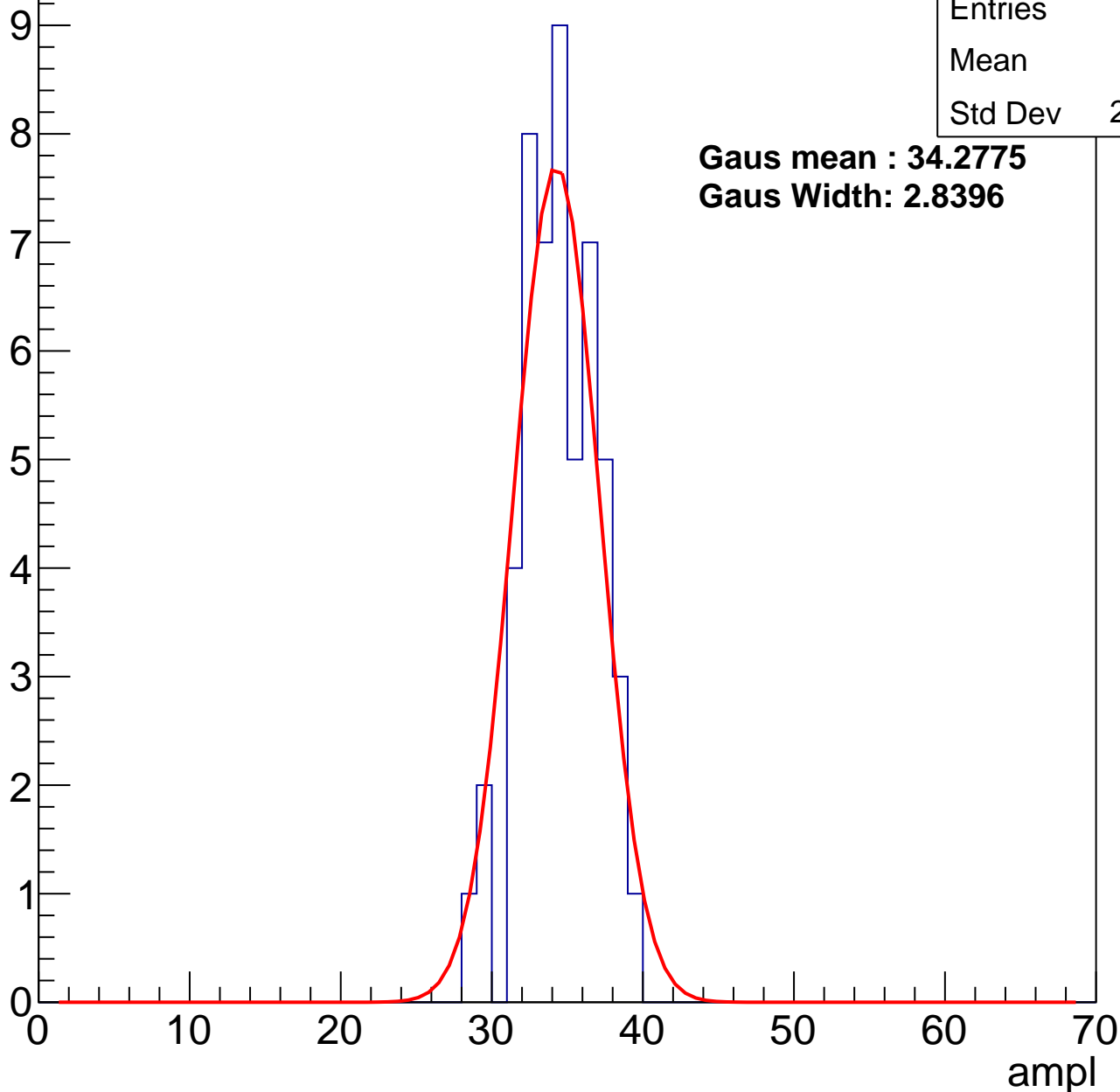
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	34
Std Dev	2.449

**Gaus mean : 34.2775**

**Gaus Width: 2.8396**



# B1L000S, U21-ch106, adc2

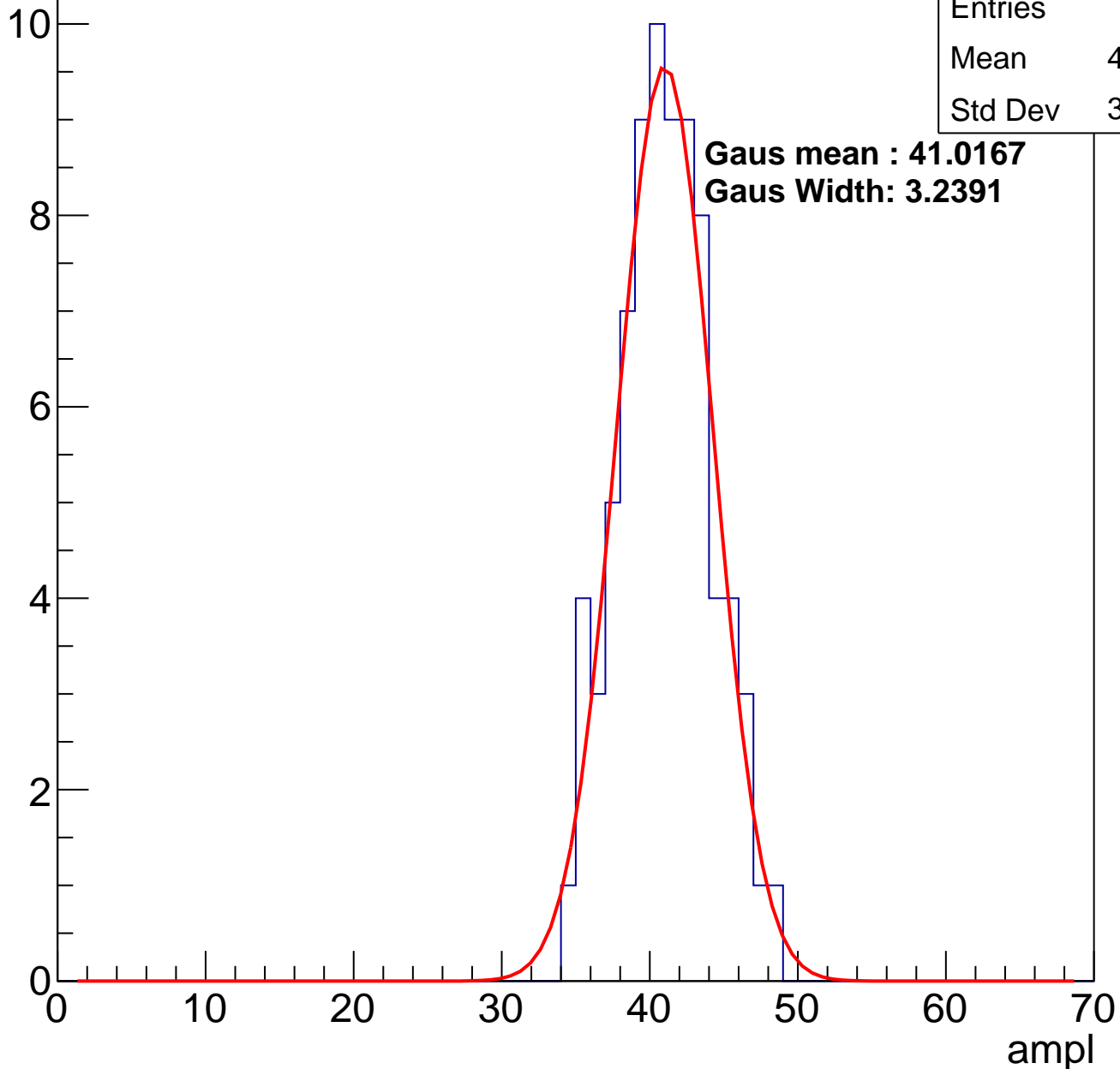
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	40.56
Std Dev	3.087

**Gaus mean : 41.0167**

**Gaus Width: 3.2391**

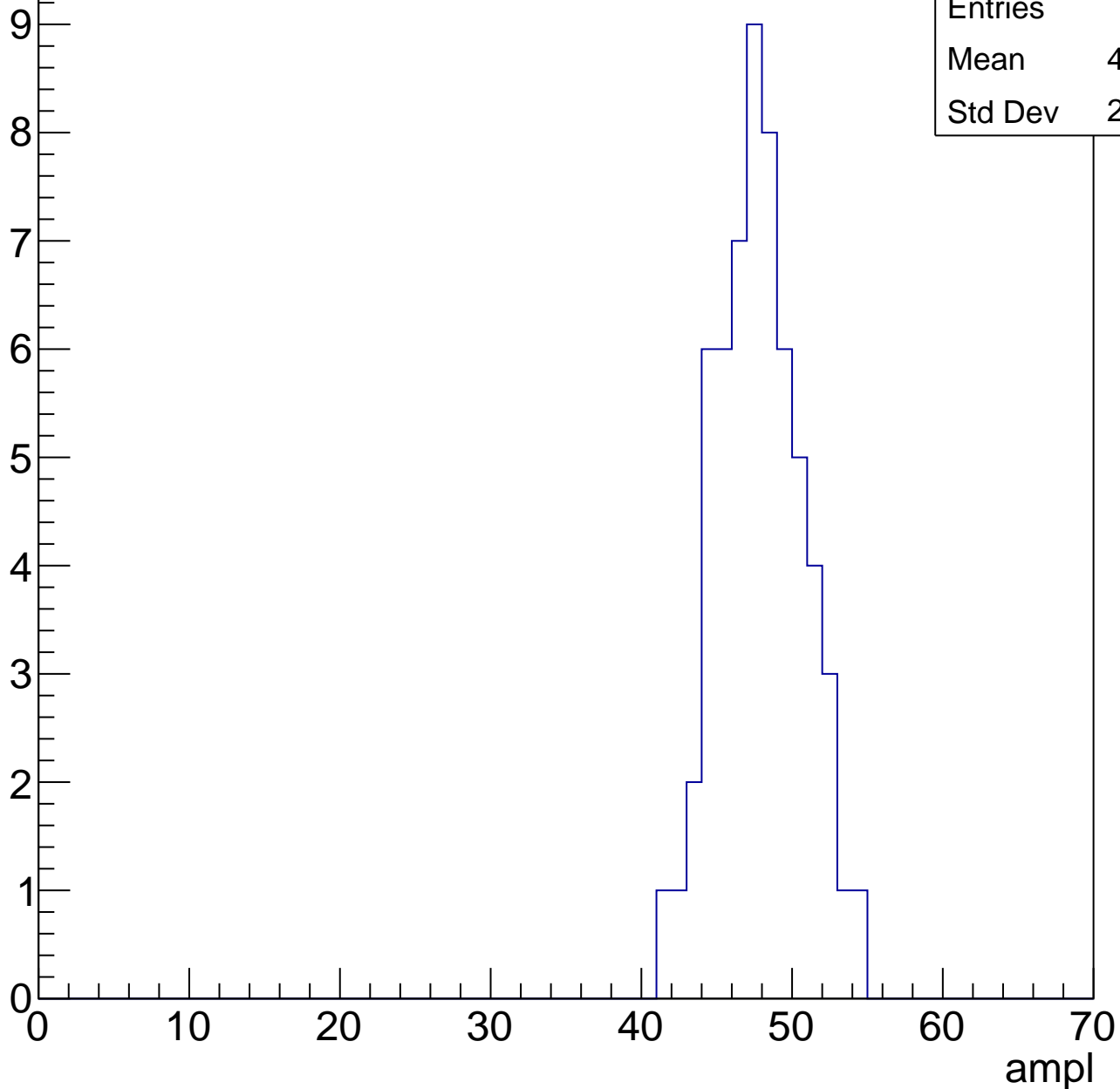
Entry



# B1L000S, U21-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



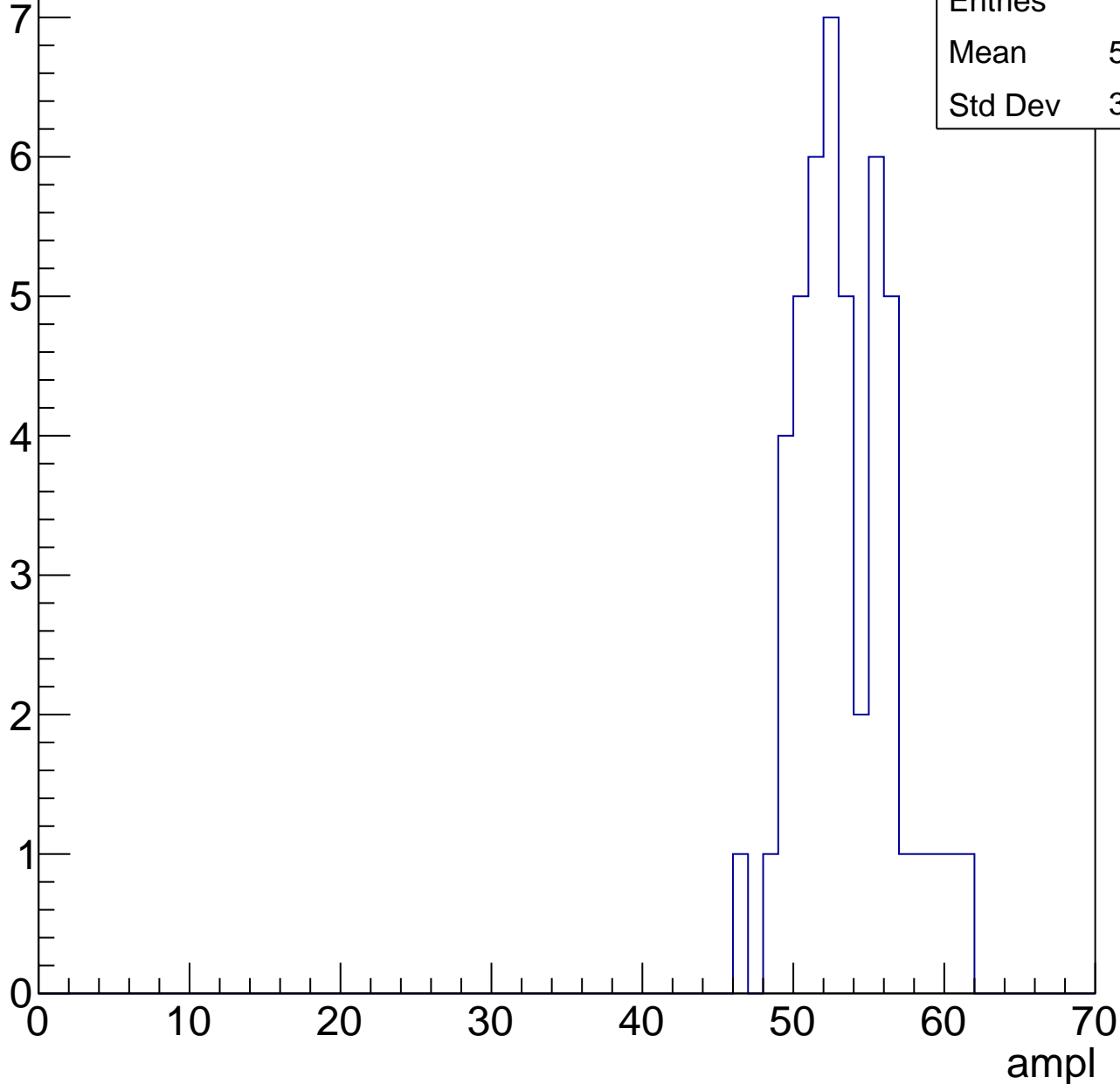
Entries	60
Mean	47.38
Std Dev	2.799

# B1L000S, U21-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	52.94
Std Dev	3.178

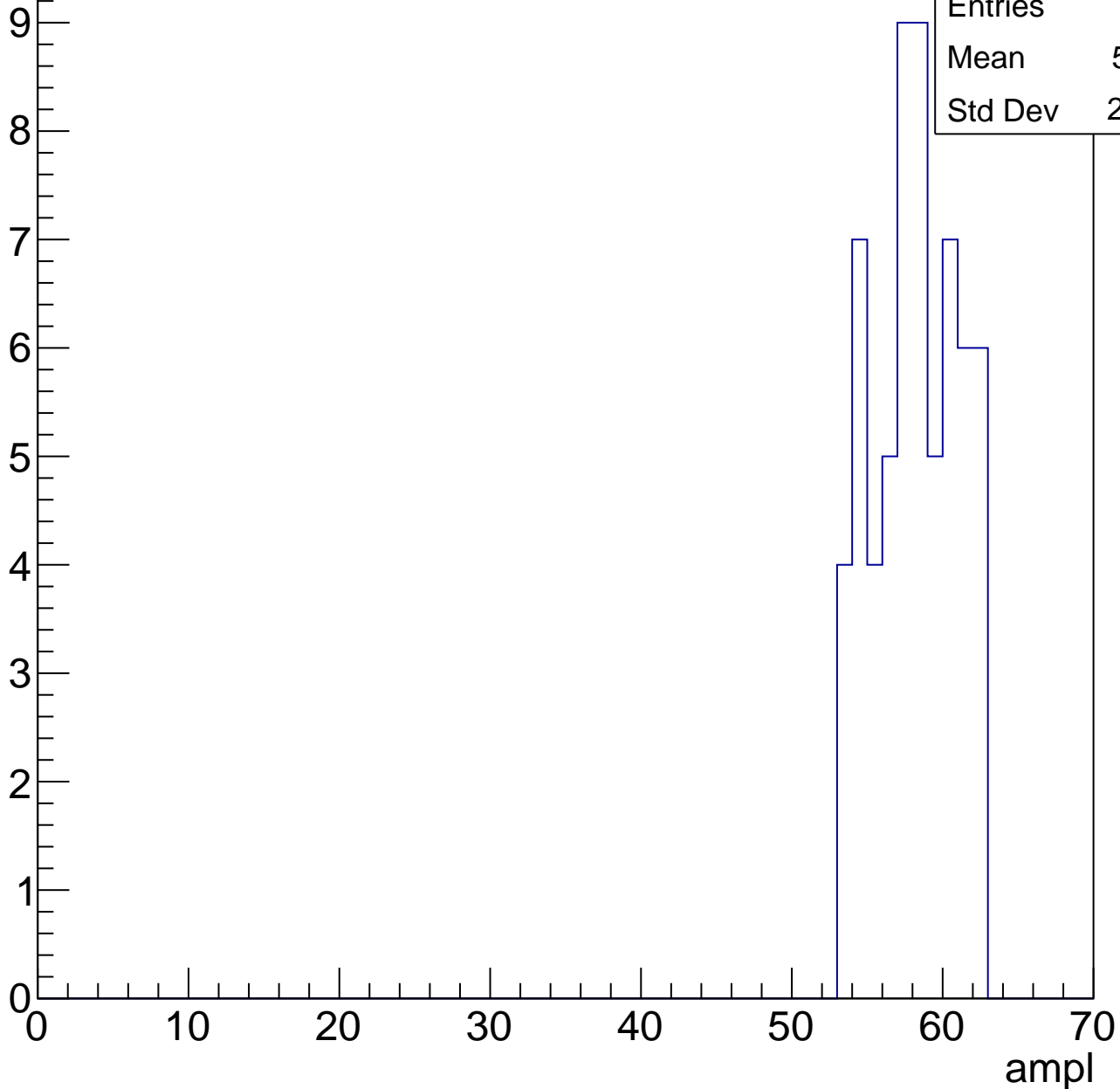


# B1L000S, U21-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	57.71
Std Dev	2.708

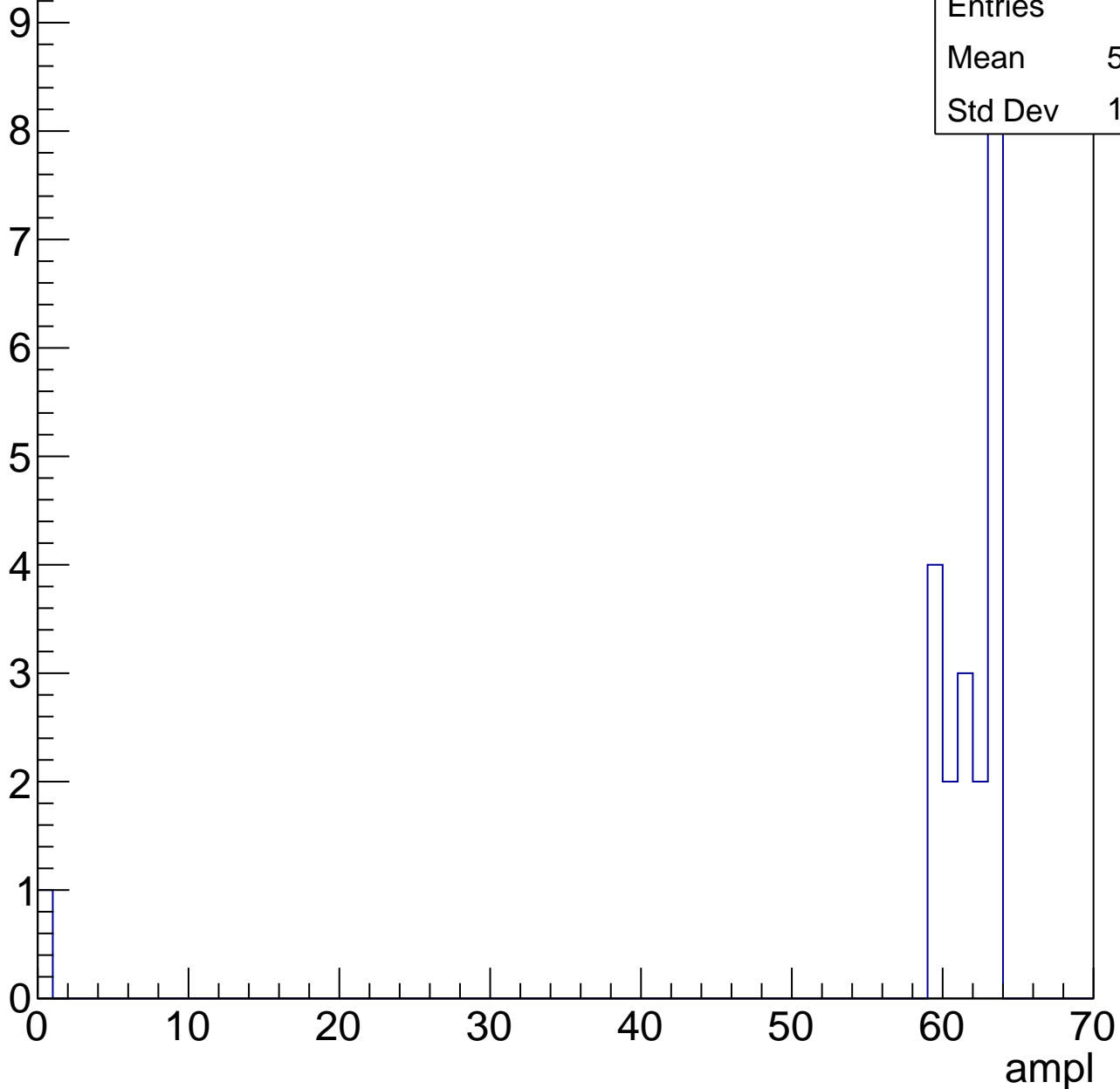


# B1L000S, U21-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.57
Std Dev	13.19

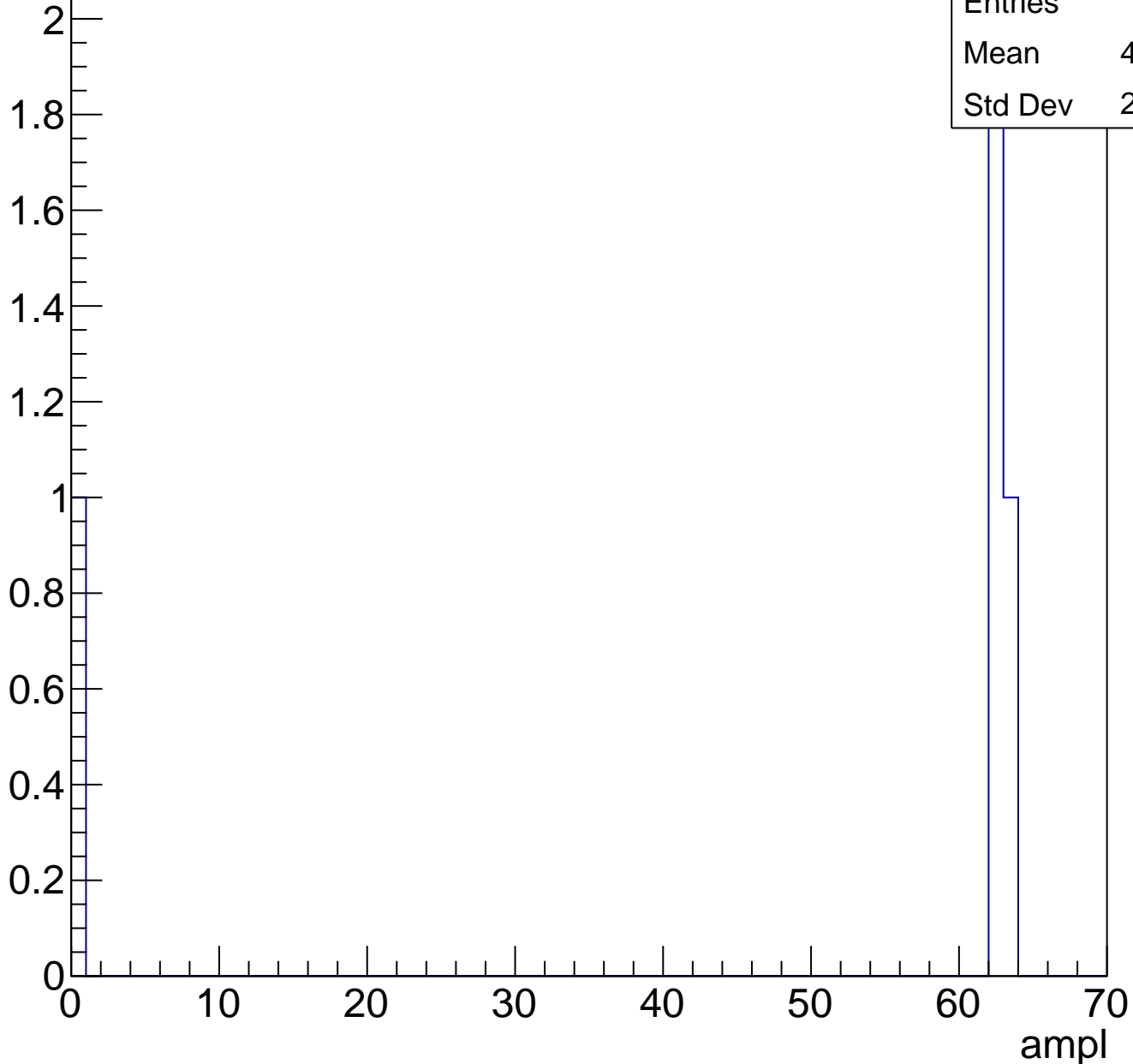




# B1L000S, U21-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch107, adc0

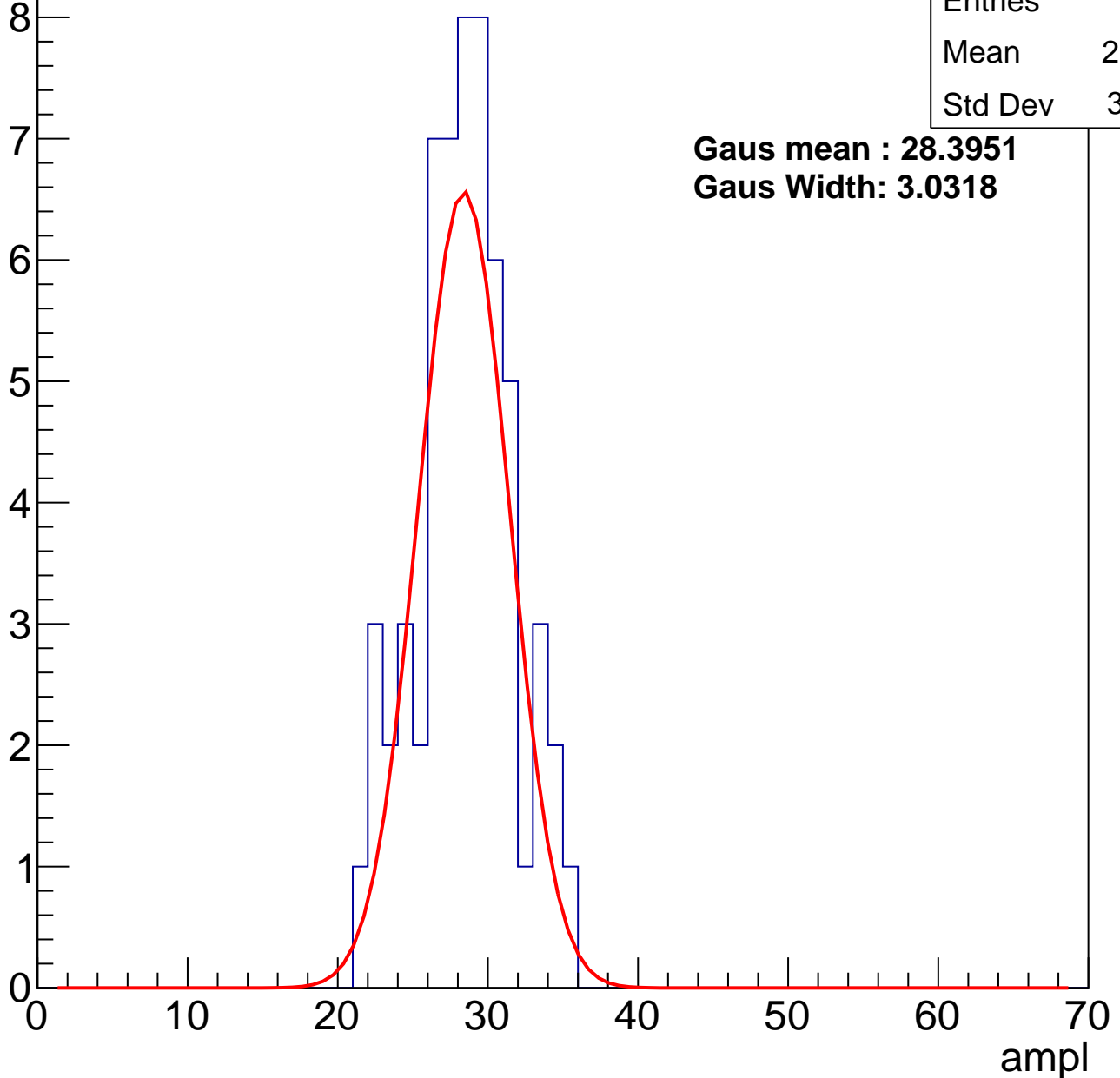
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.98
Std Dev	3.181

**Gaus mean : 28.3951**

**Gaus Width: 3.0318**



# B1L000S, U21-ch107, adc1

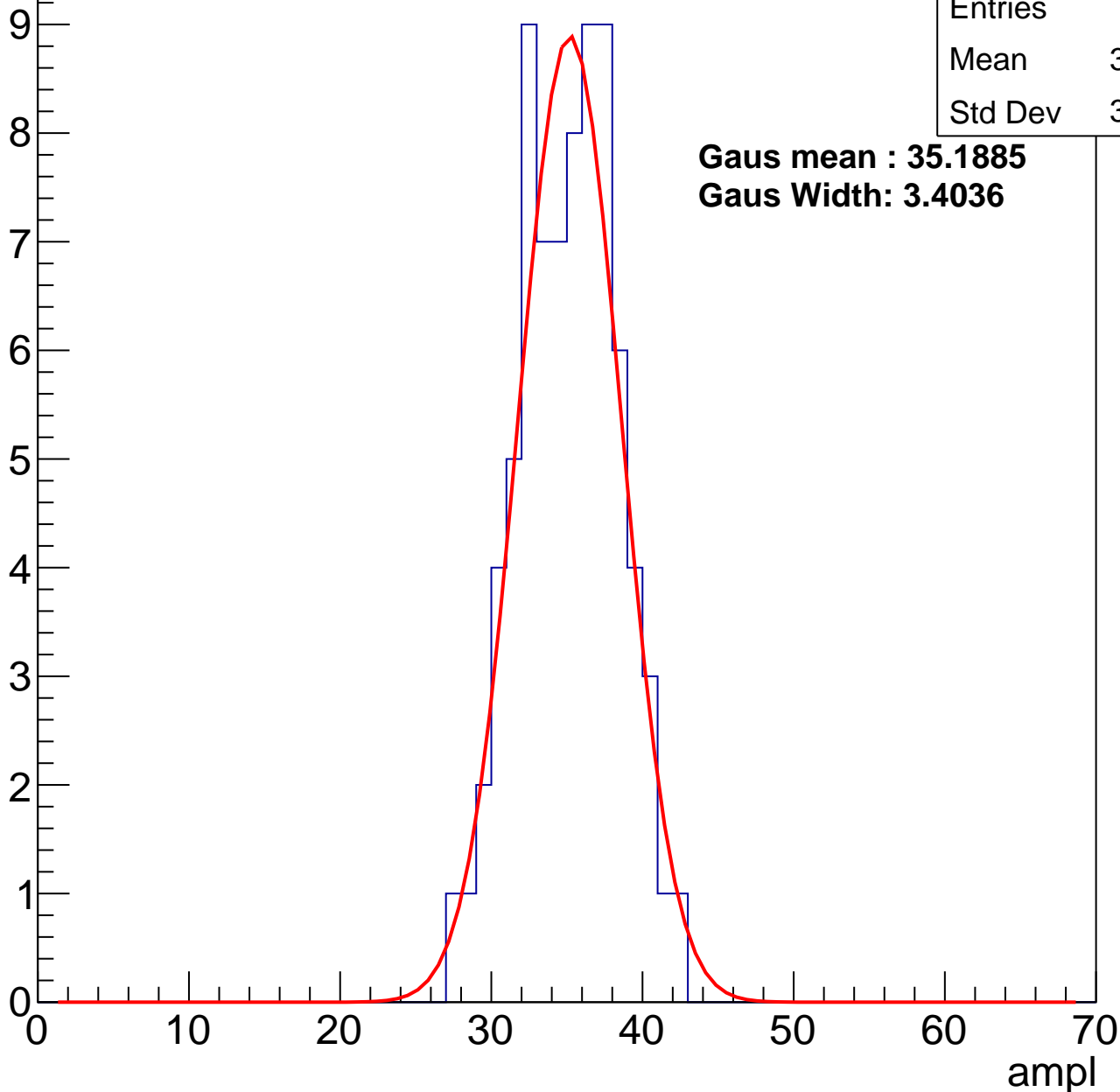
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	34.66
Std Dev	3.214

**Gaus mean : 35.1885**

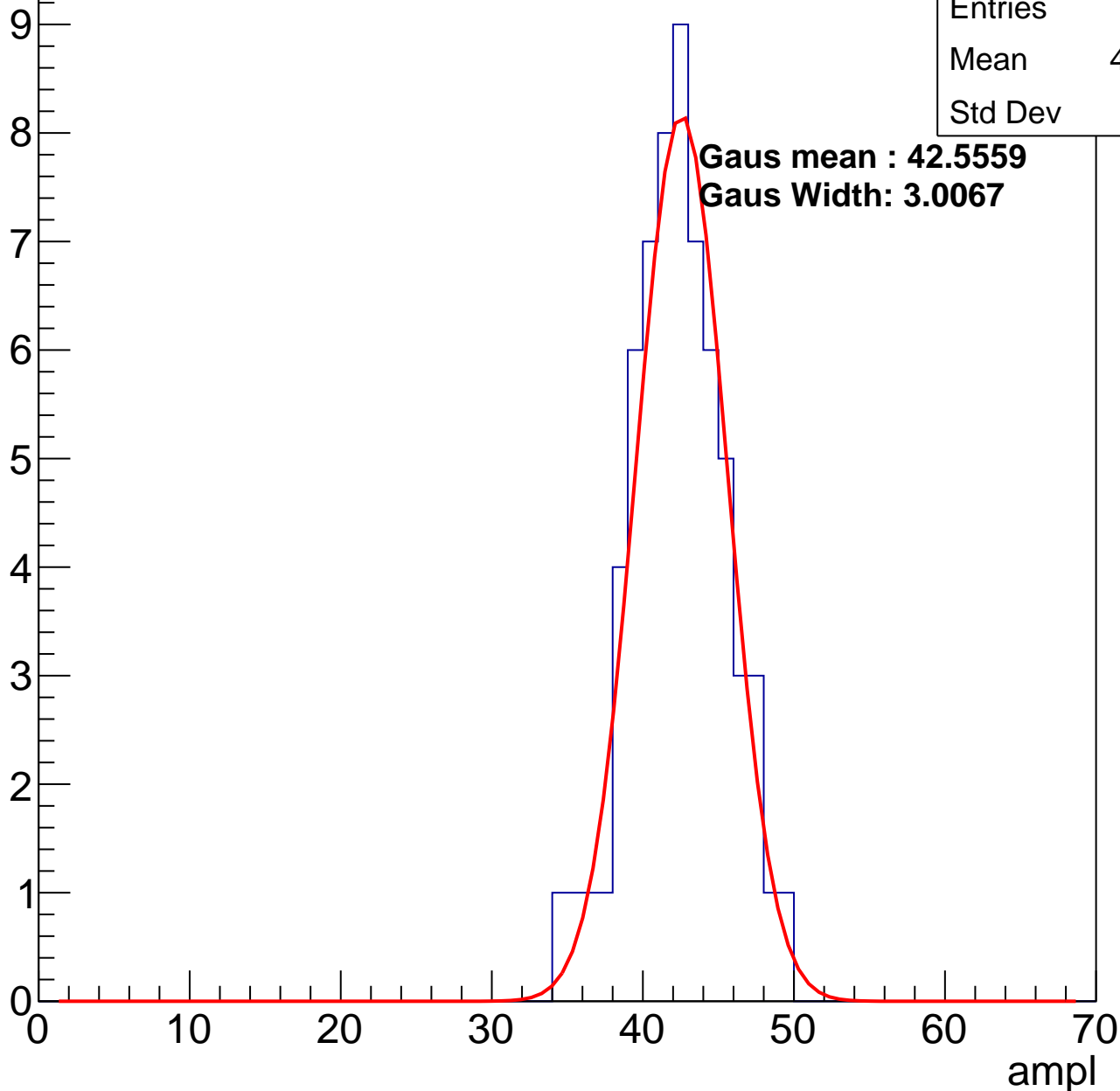
**Gaus Width: 3.4036**



# B1L000S, U21-ch107, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

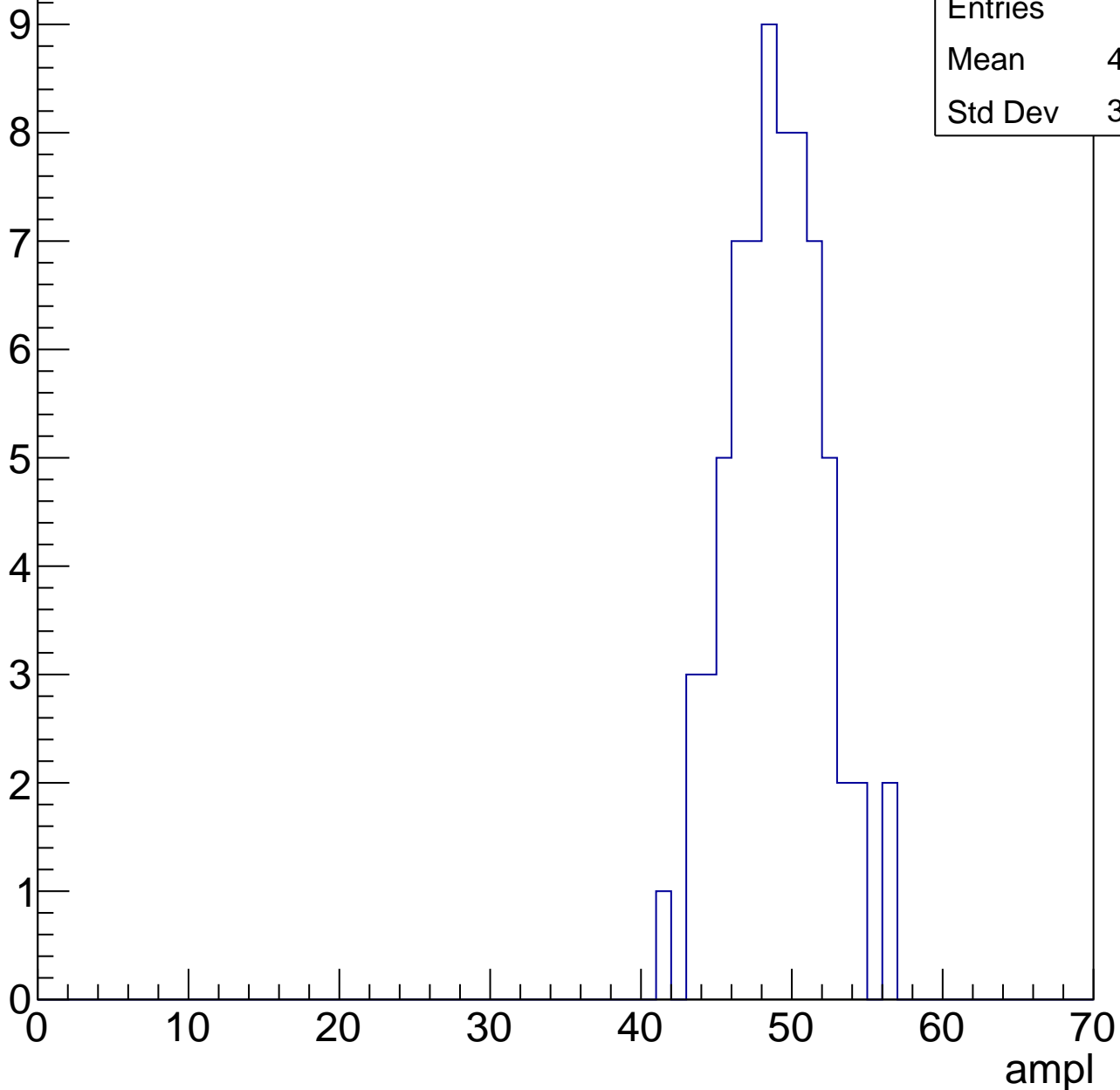


# B1L000S, U21-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	48.48
Std Dev	3.114

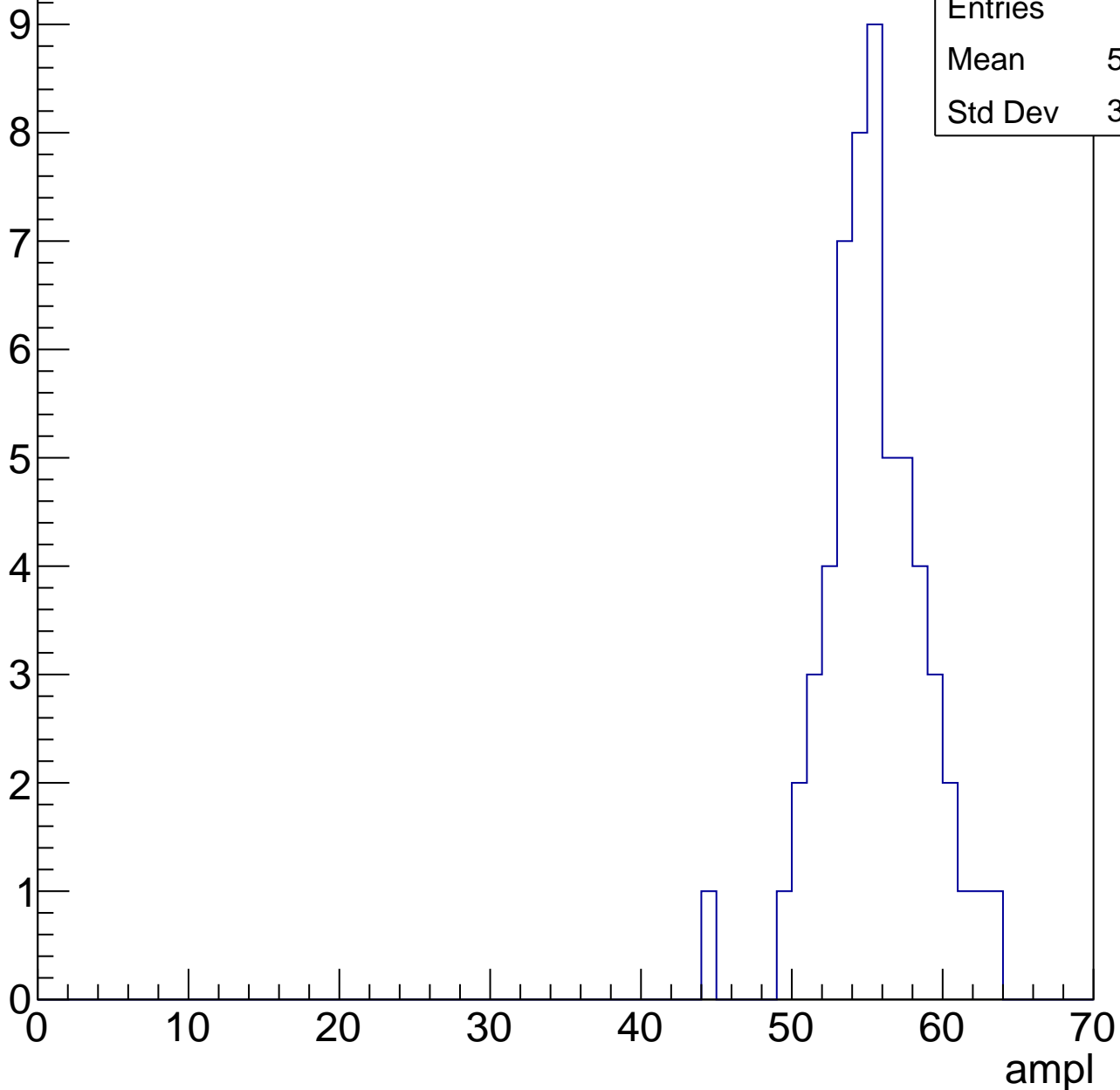


# B1L000S, U21-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	54.95
Std Dev	3.337

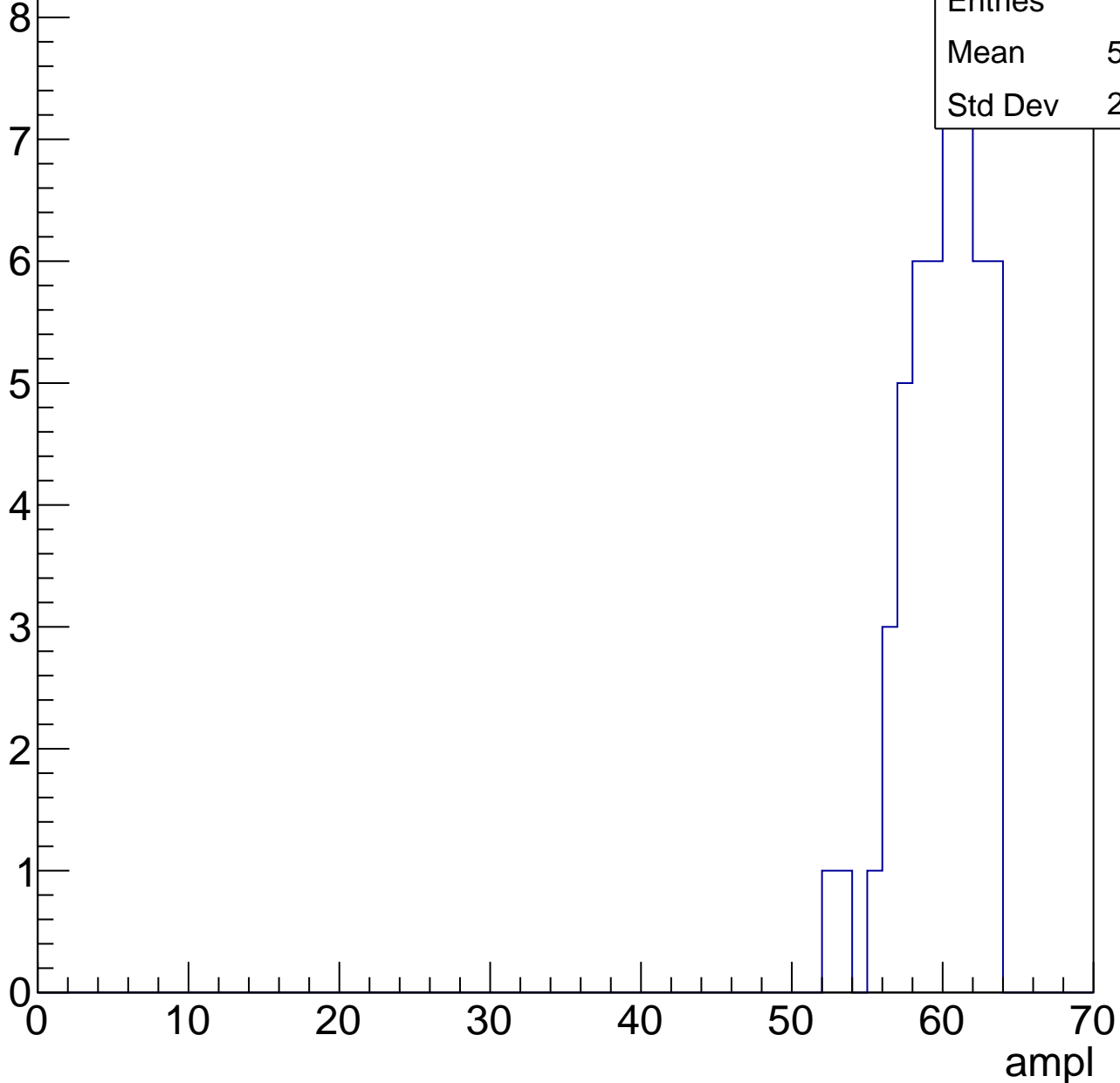


# B1L000S, U21-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

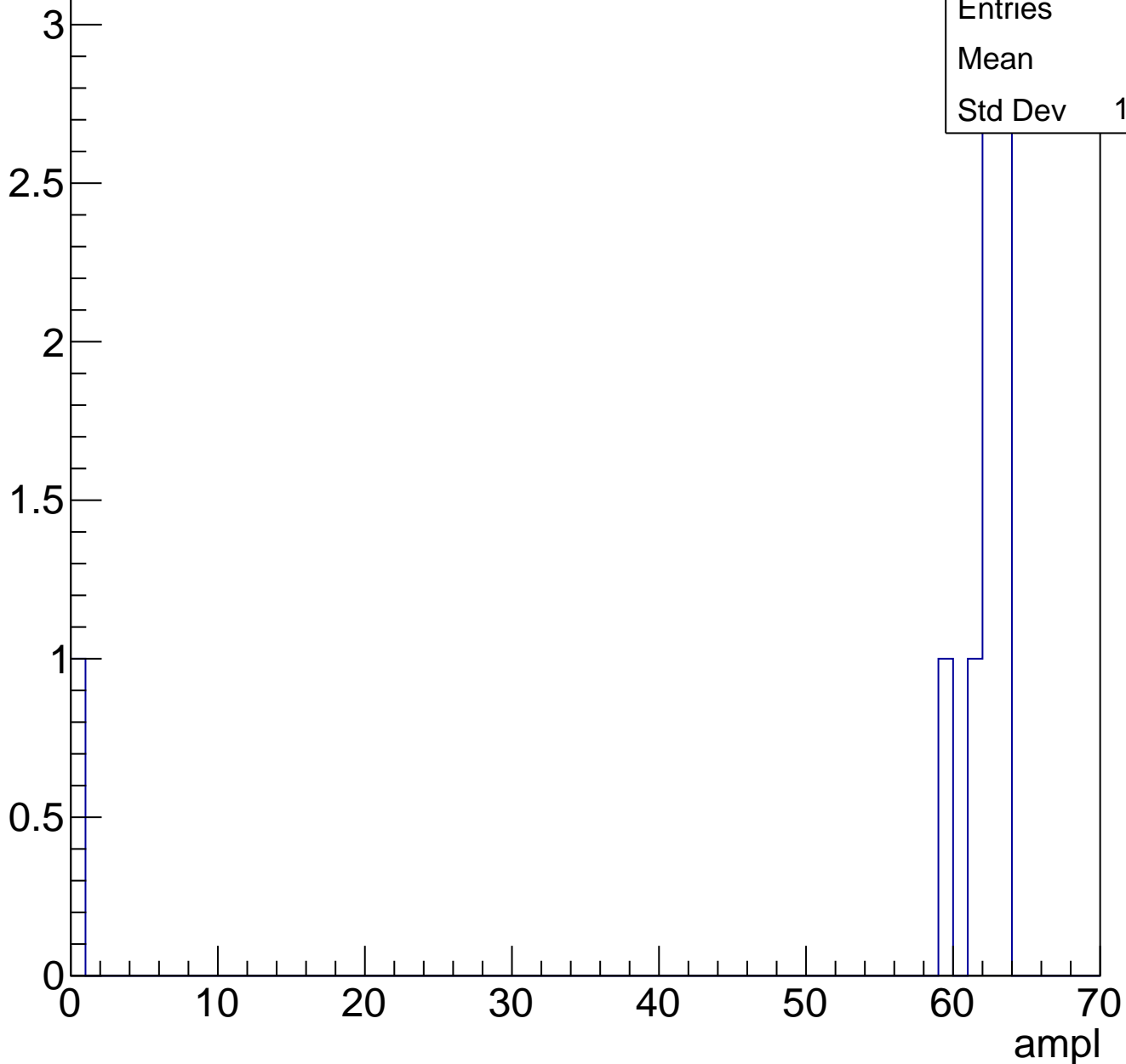
Entries	51
Mean	59.47
Std Dev	2.554



# B1L000S, U21-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

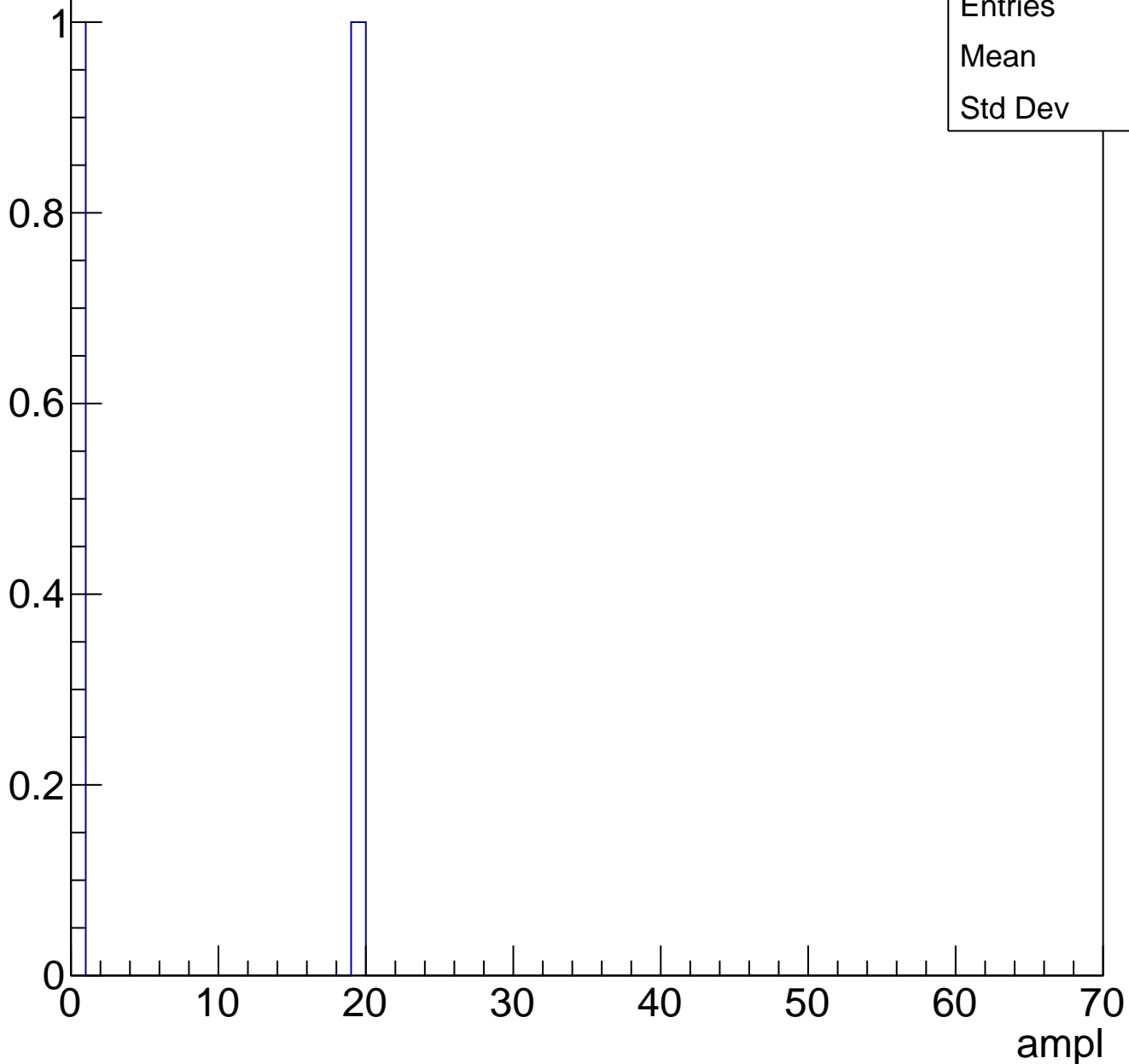




# B1L000S, U21-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B1L000S, U21-ch108, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	29.21
Std Dev	3.101

**Gaus mean : 29.8629**

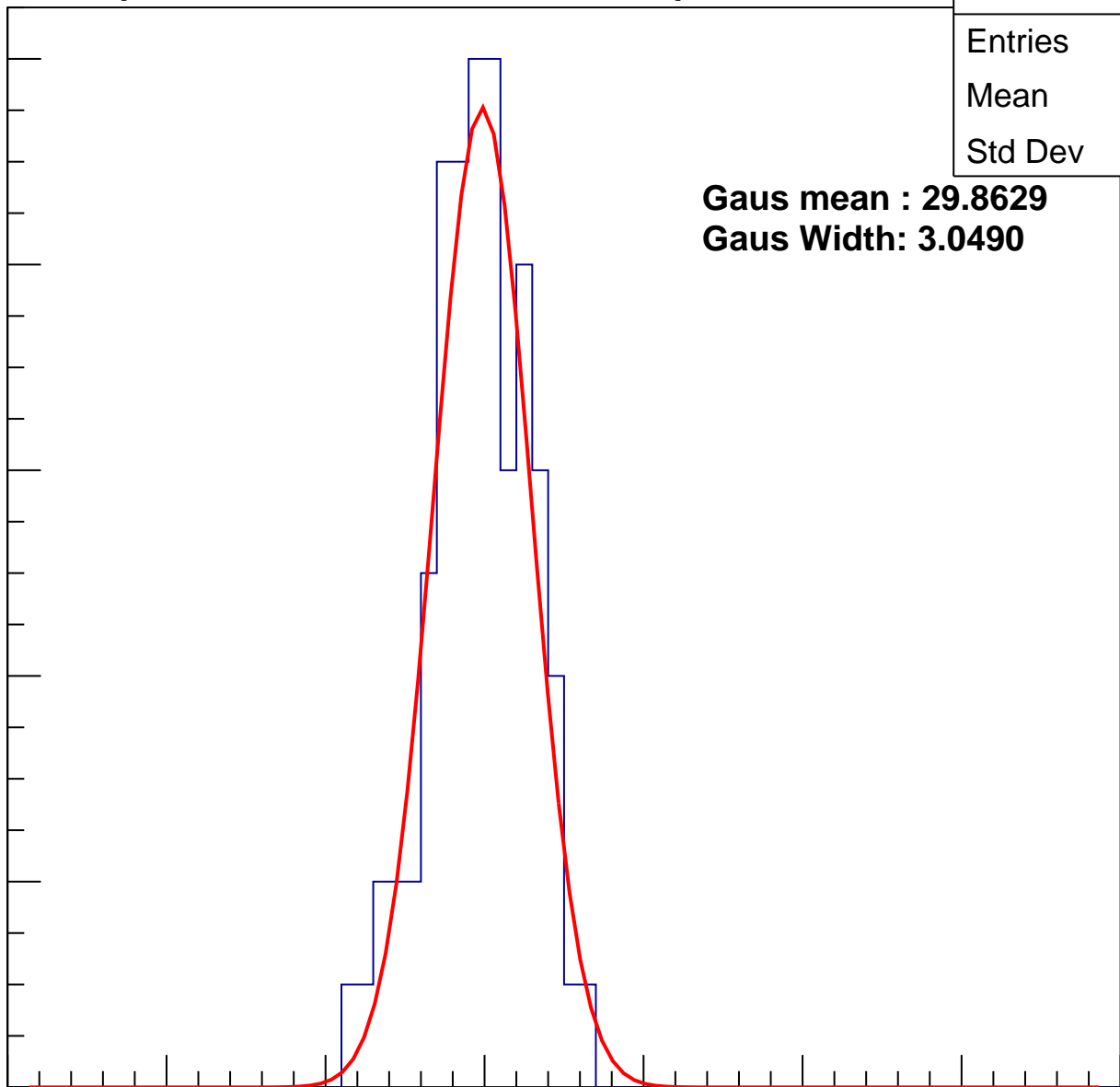
**Gaus Width: 3.0490**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch108, adc1

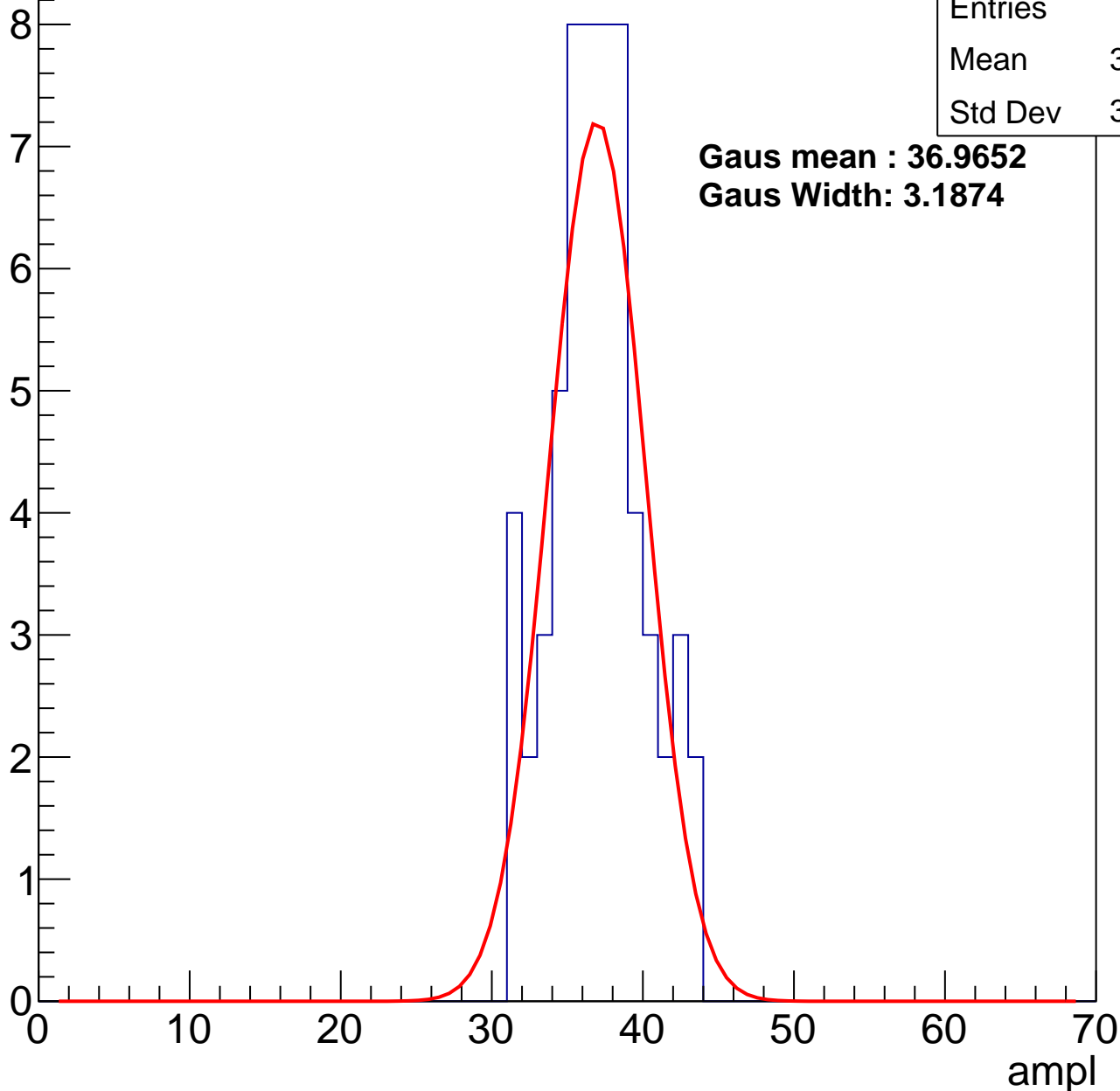
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	36.58
Std Dev	3.018

**Gaus mean : 36.9652**

**Gaus Width: 3.1874**



# B1L000S, U21-ch108, adc2

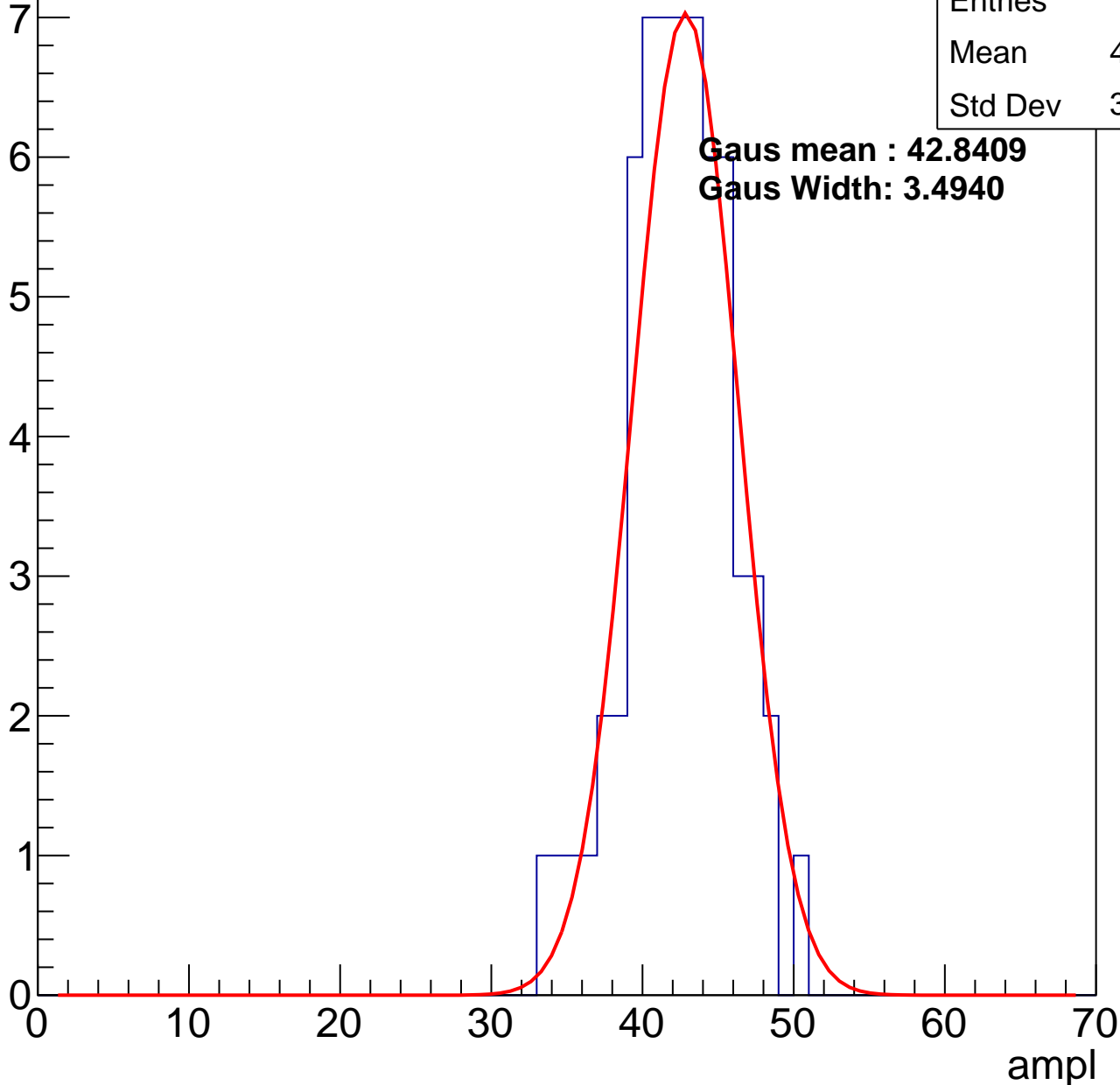
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.95
Std Dev	3.438

**Gaus mean : 42.8409**

**Gaus Width: 3.4940**

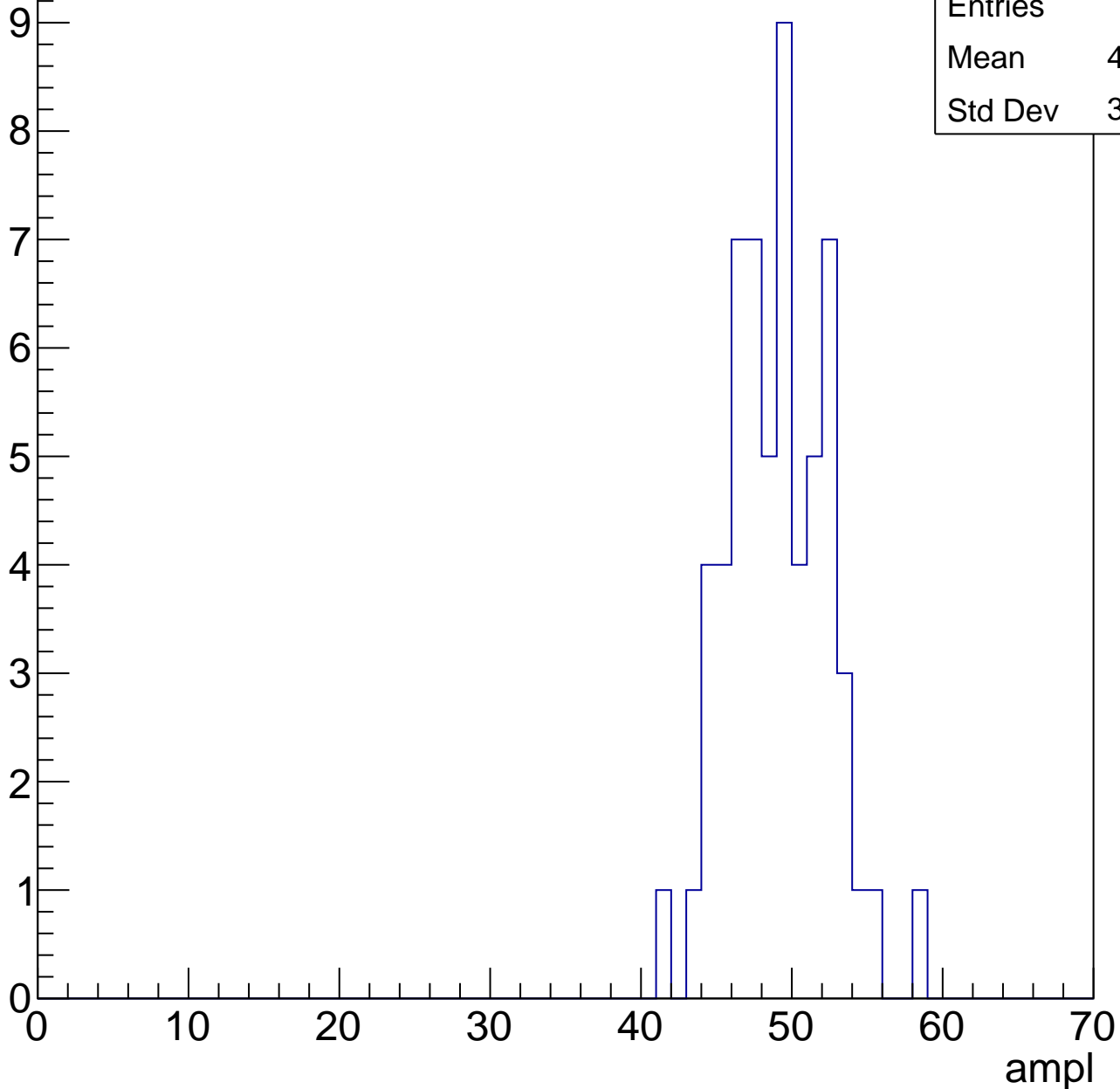


# B1L000S, U21-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

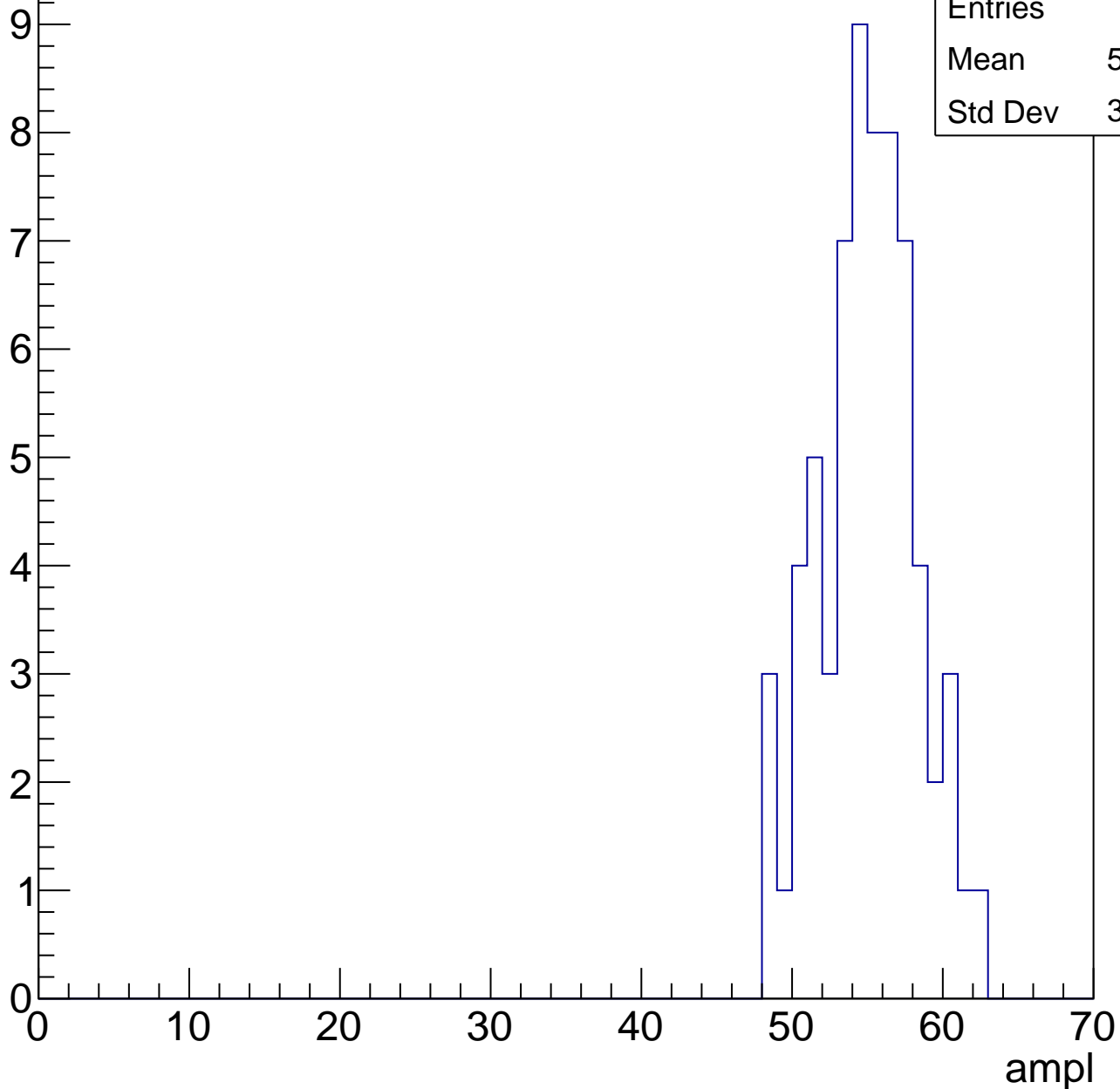
Entries	60
Mean	48.62
Std Dev	3.236



# B1L000S, U21-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

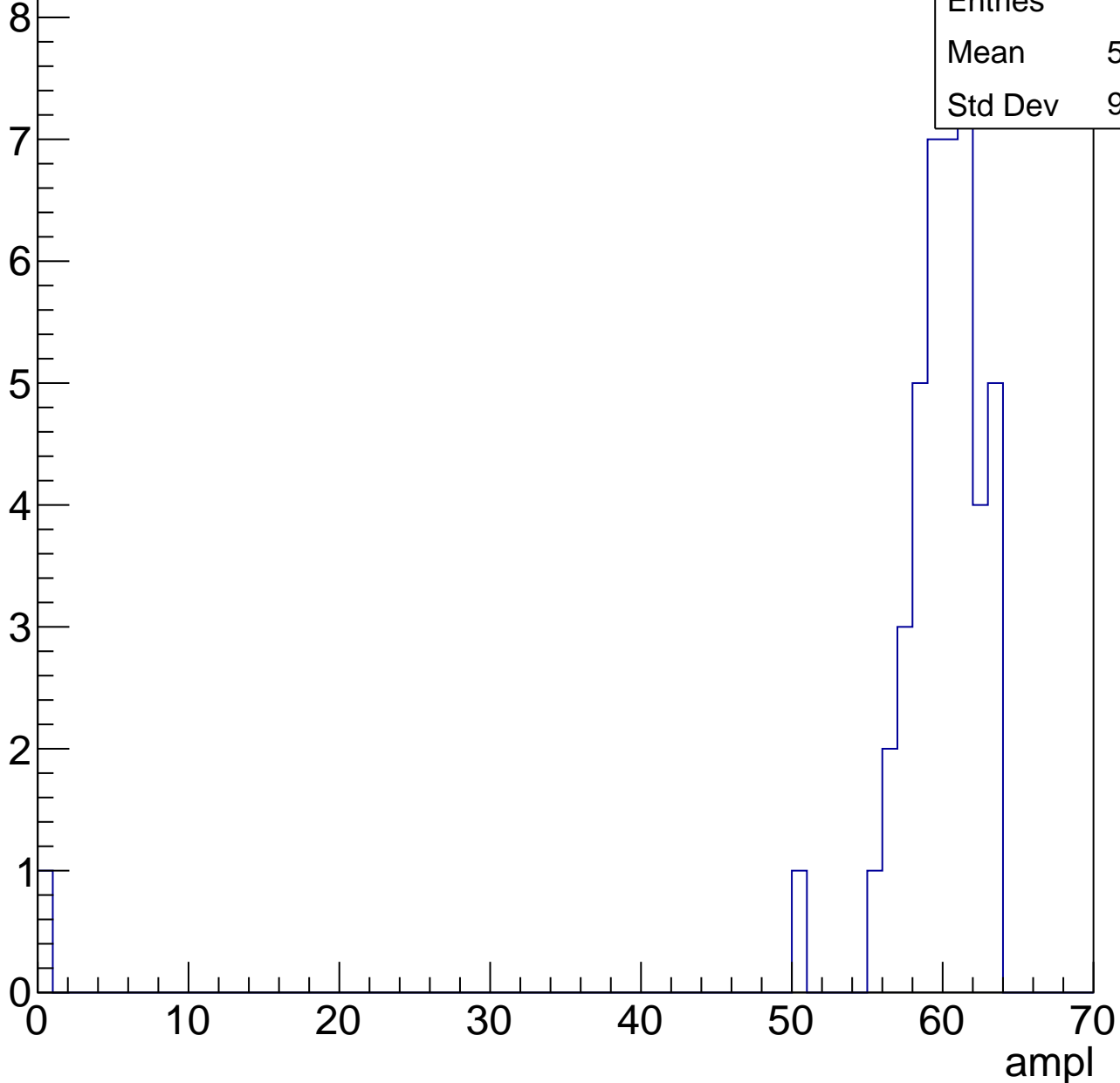


# B1L000S, U21-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	58.23
Std Dev	9.222

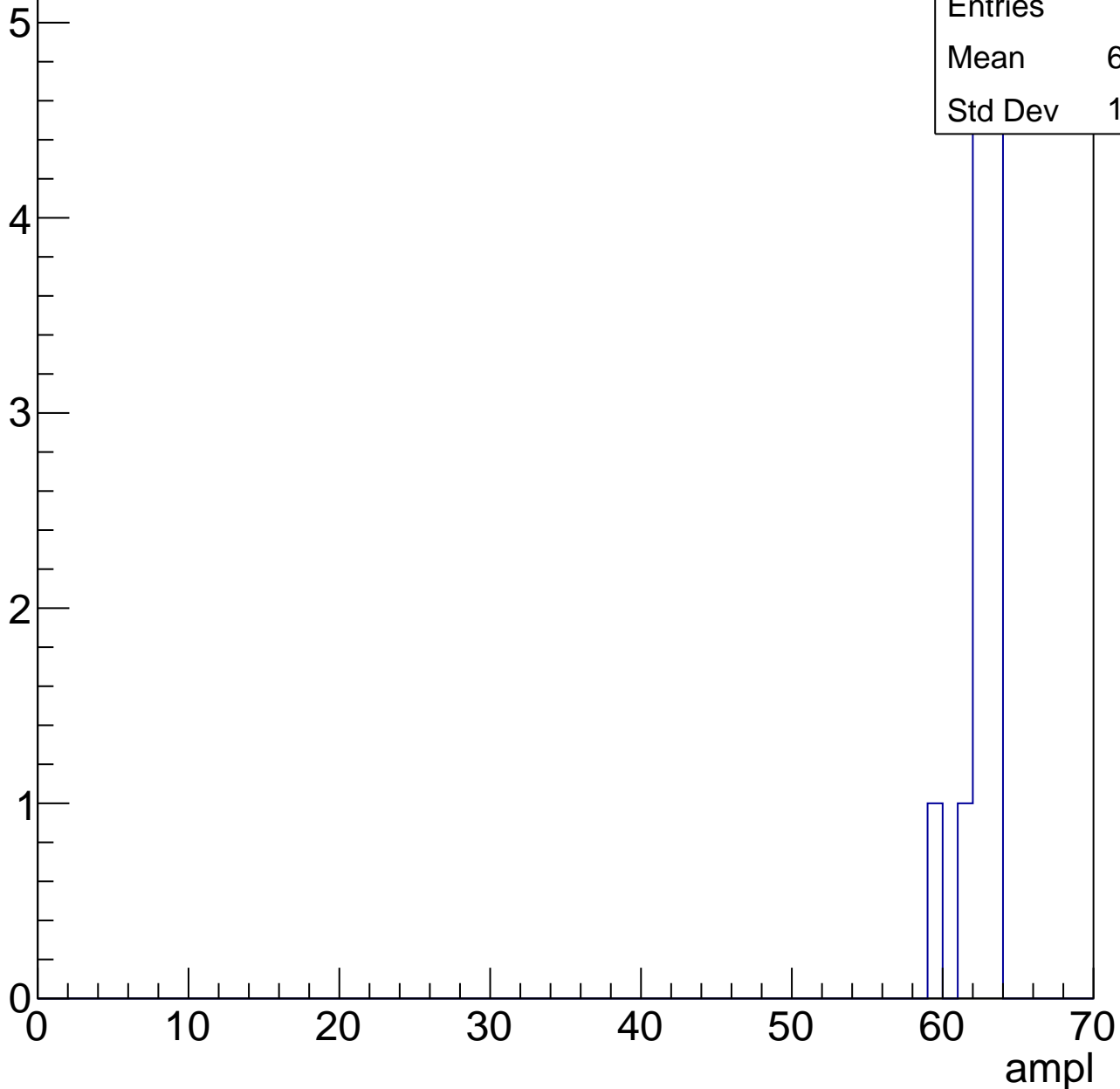


# B1L000S, U21-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	62.08
Std Dev	1.115





# B1L000S, U21-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch109, adc0

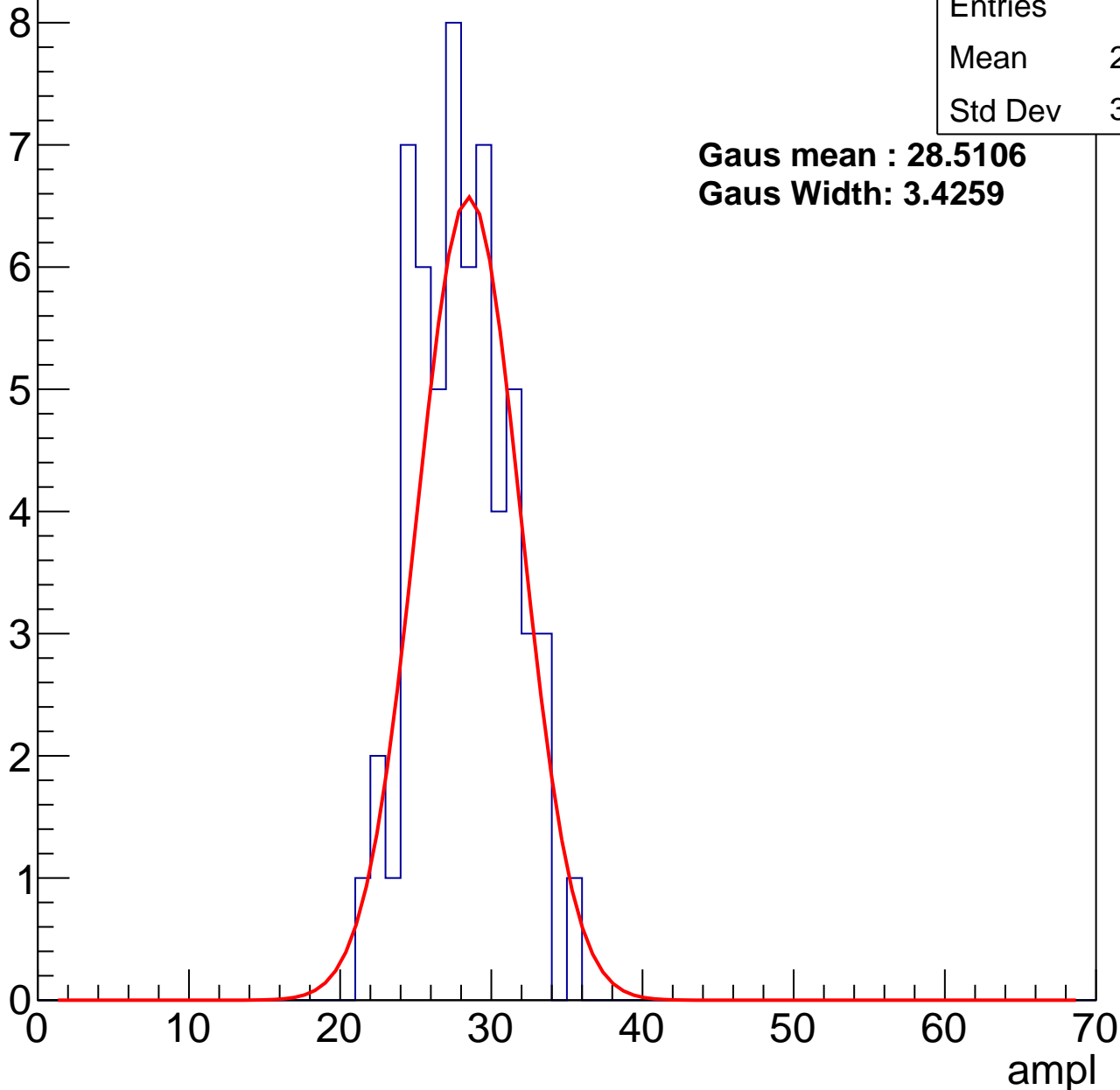
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.59
Std Dev	3.109

**Gaus mean : 28.5106**

**Gaus Width: 3.4259**



# B1L000S, U21-ch109, adc1

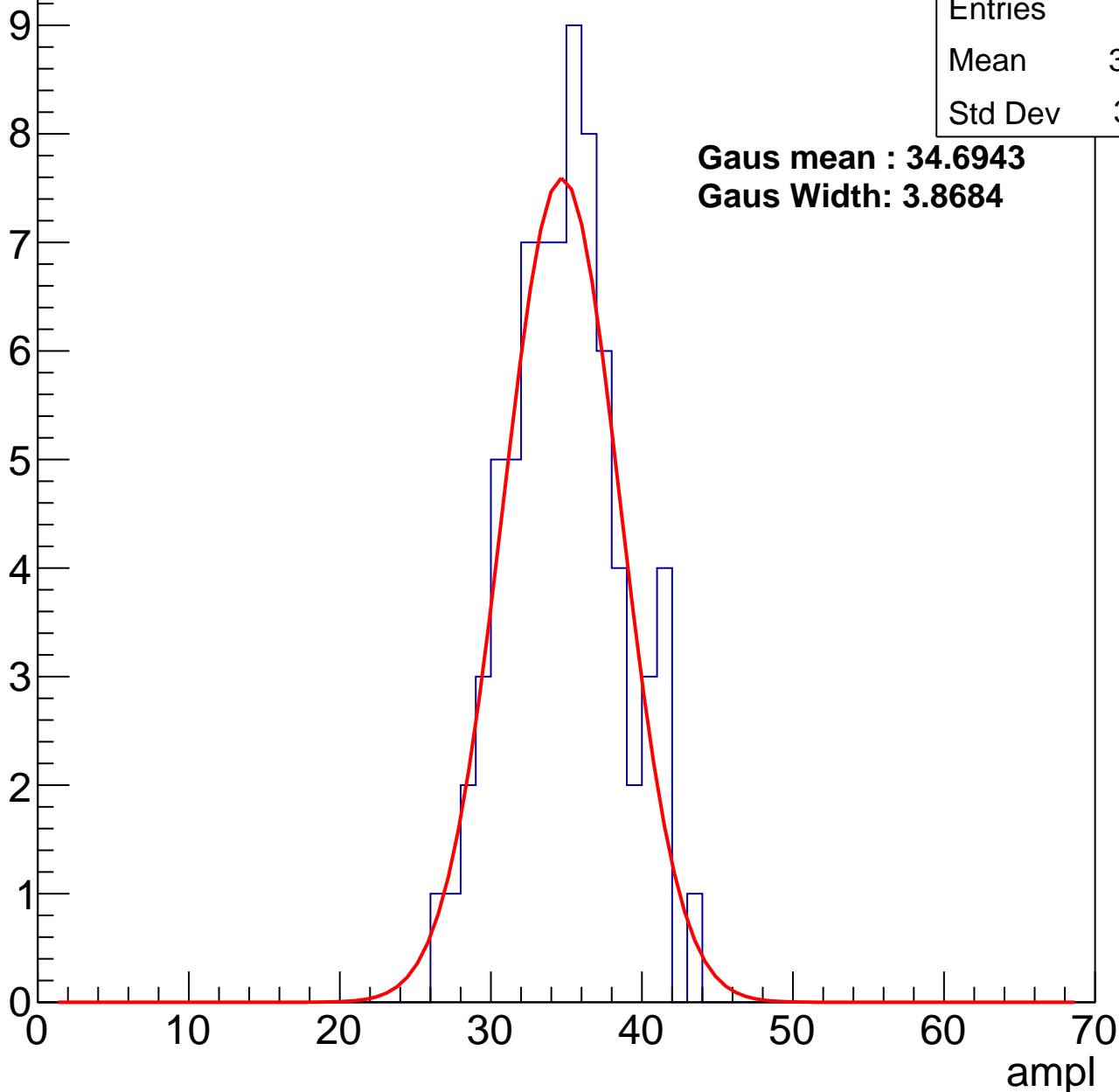
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	34.35
Std Dev	3.661

**Gaus mean : 34.6943**

**Gaus Width: 3.8684**



# B1L000S, U21-ch109, adc2

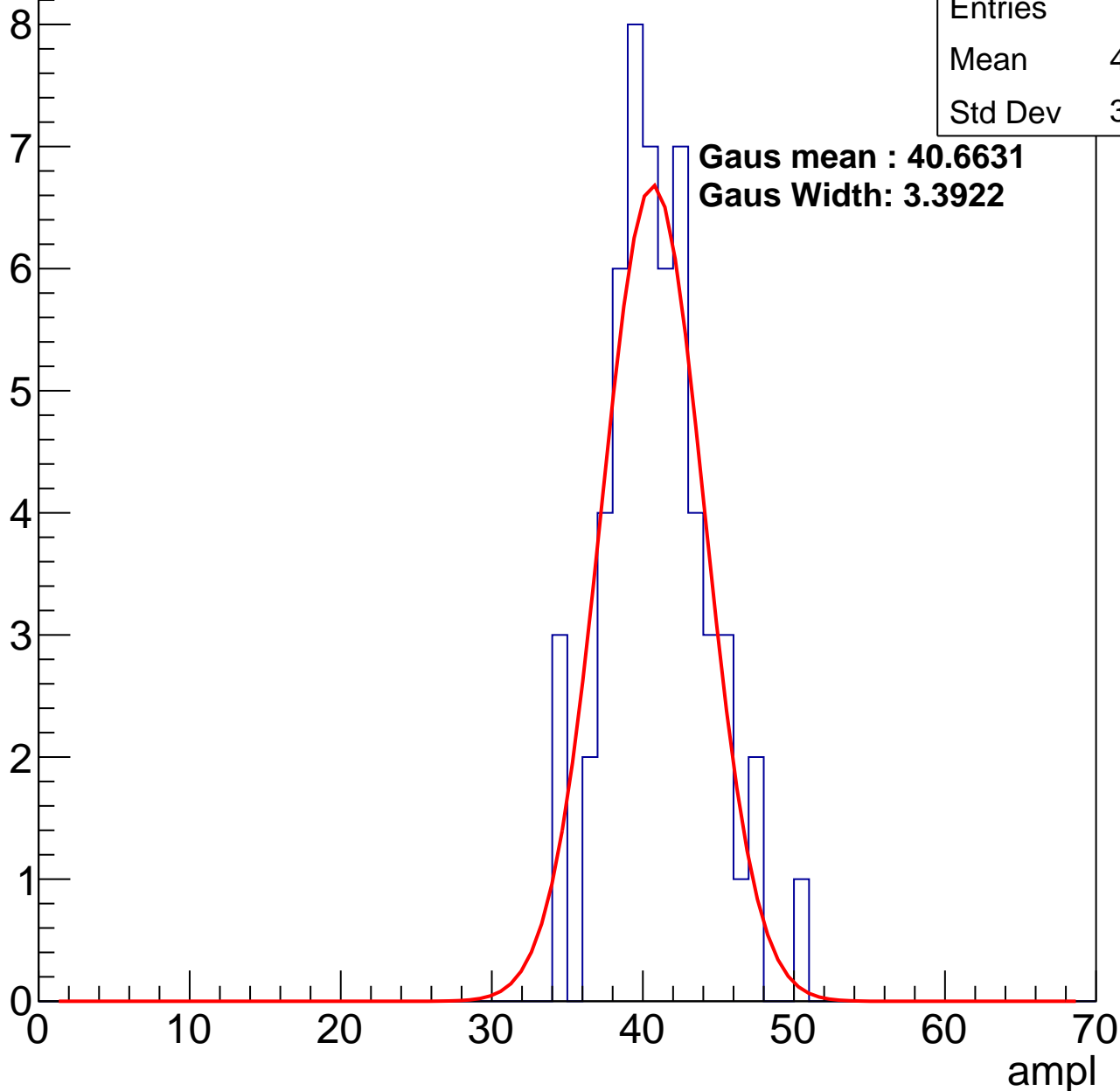
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	40.54
Std Dev	3.293

**Gaus mean : 40.6631**

**Gaus Width: 3.3922**

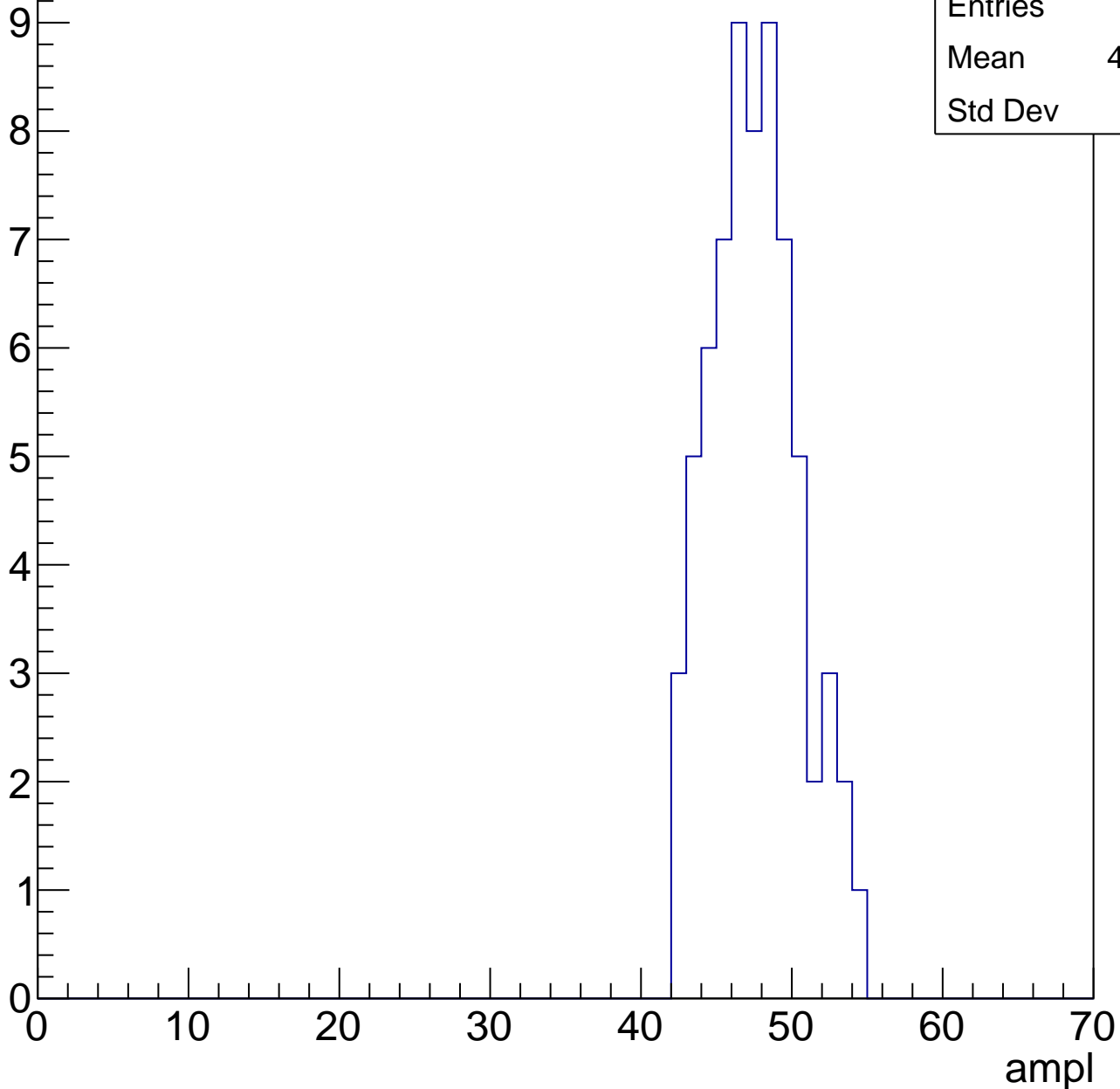


# B1L000S, U21-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	47.06
Std Dev	2.88

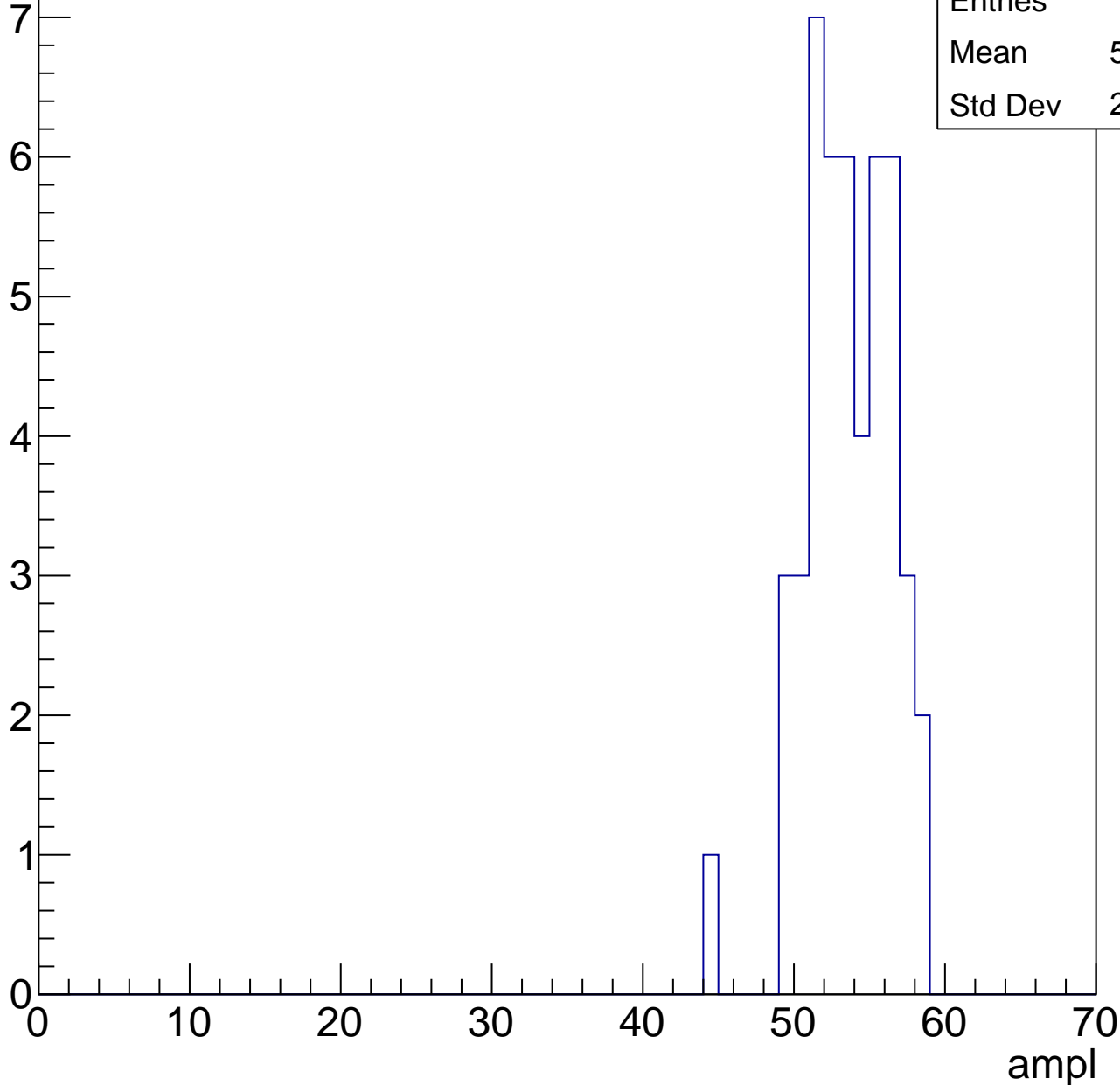


# B1L000S, U21-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	53.13
Std Dev	2.803



# B1L000S, U21-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	56.8
Std Dev	7.738

10

8

6

4

2

0

0

10

20

30

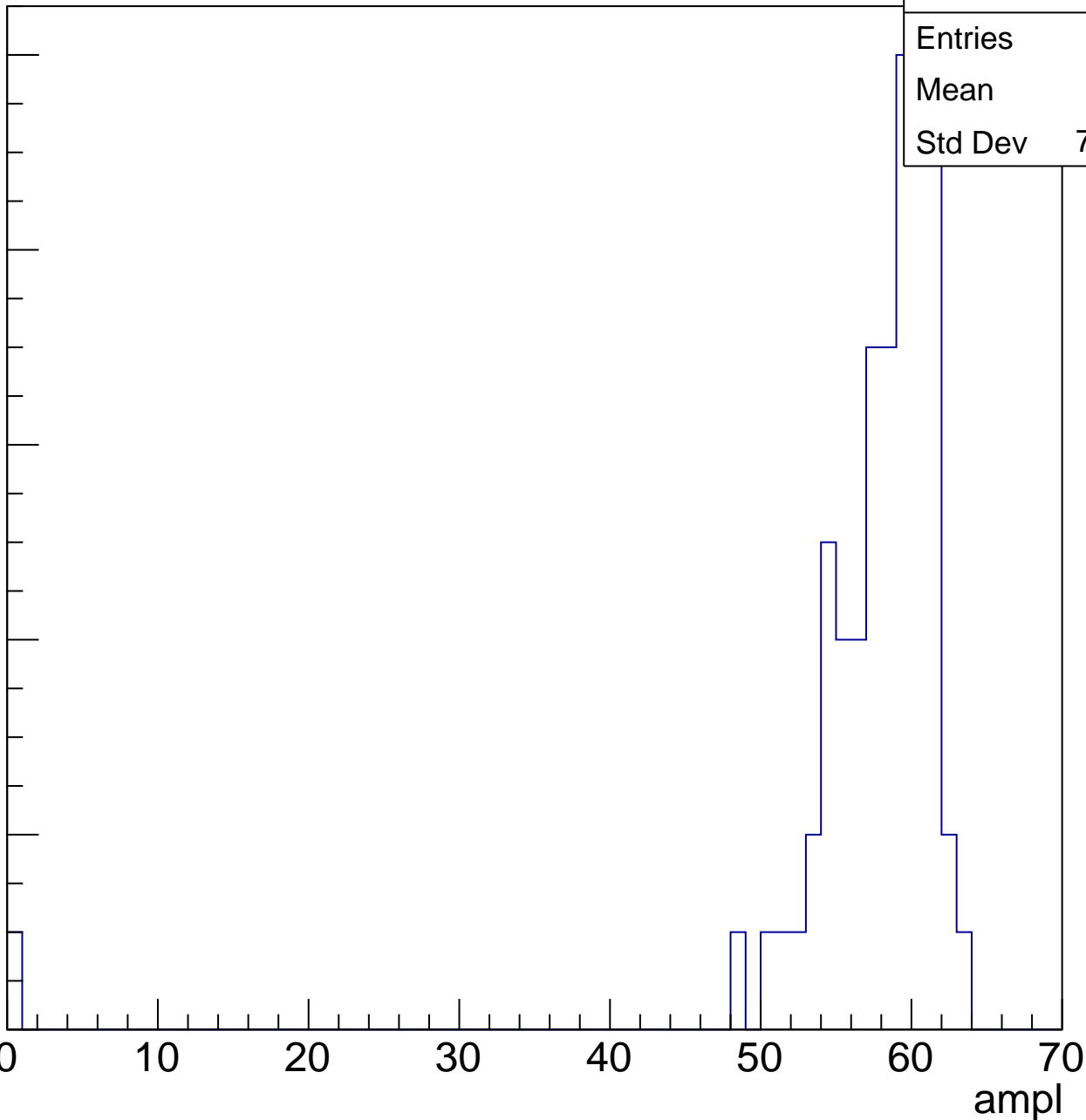
40

50

60

70

ampl

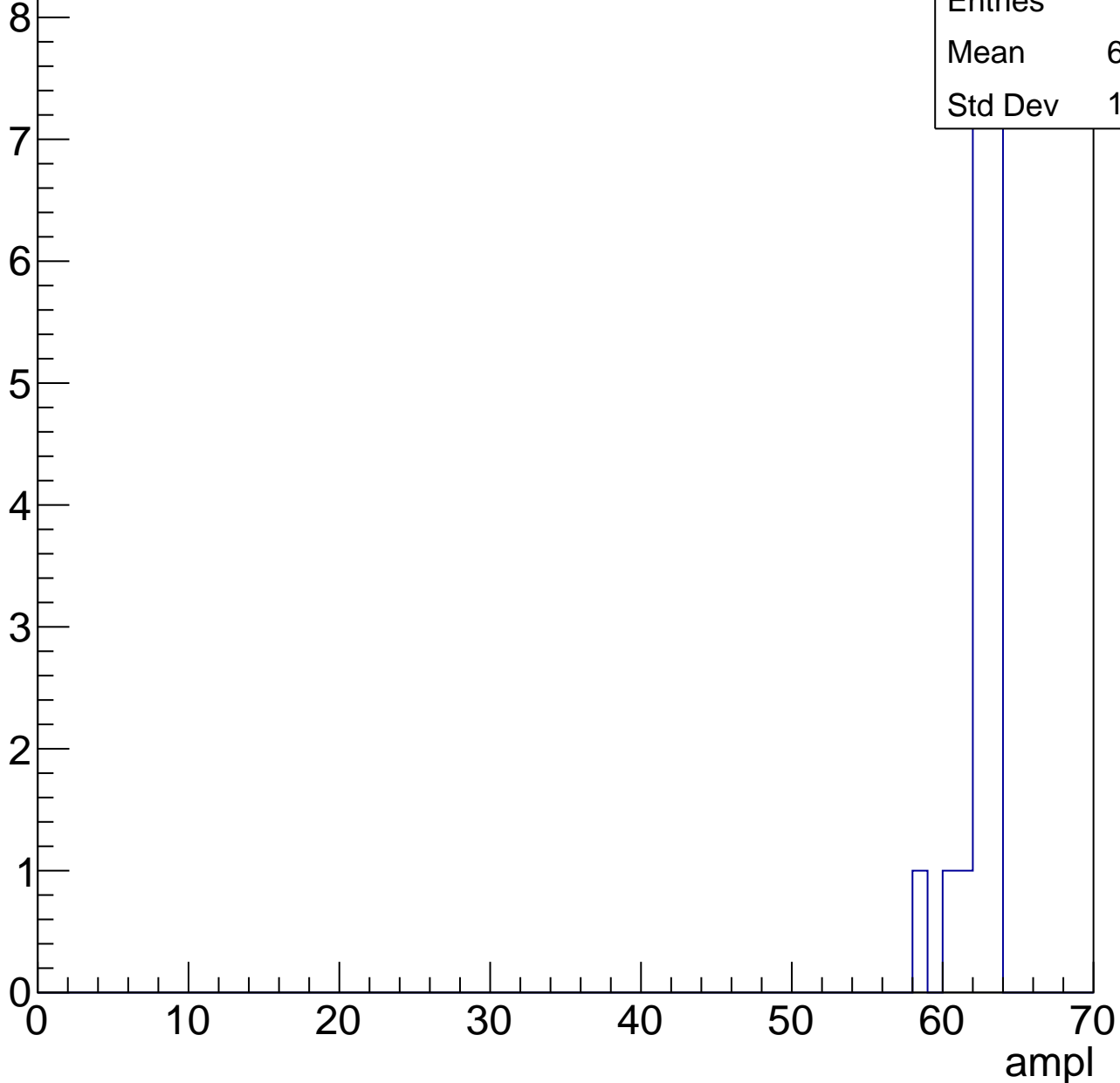


# B1L000S, U21-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	62.05
Std Dev	1.234

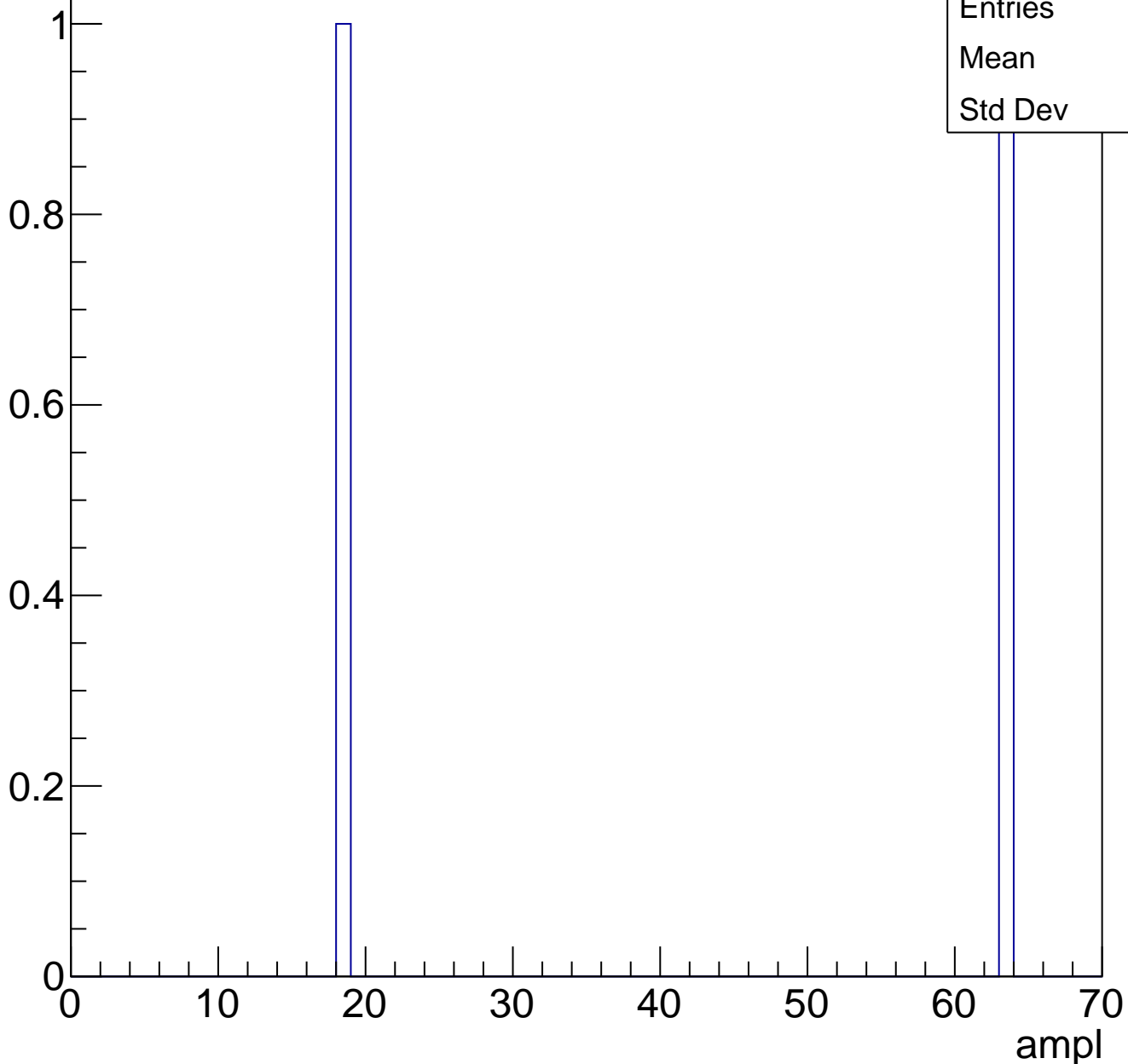




# B1L000S, U21-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	40.5
Std Dev	22.5

# B1L000S, U21-ch110, adc0

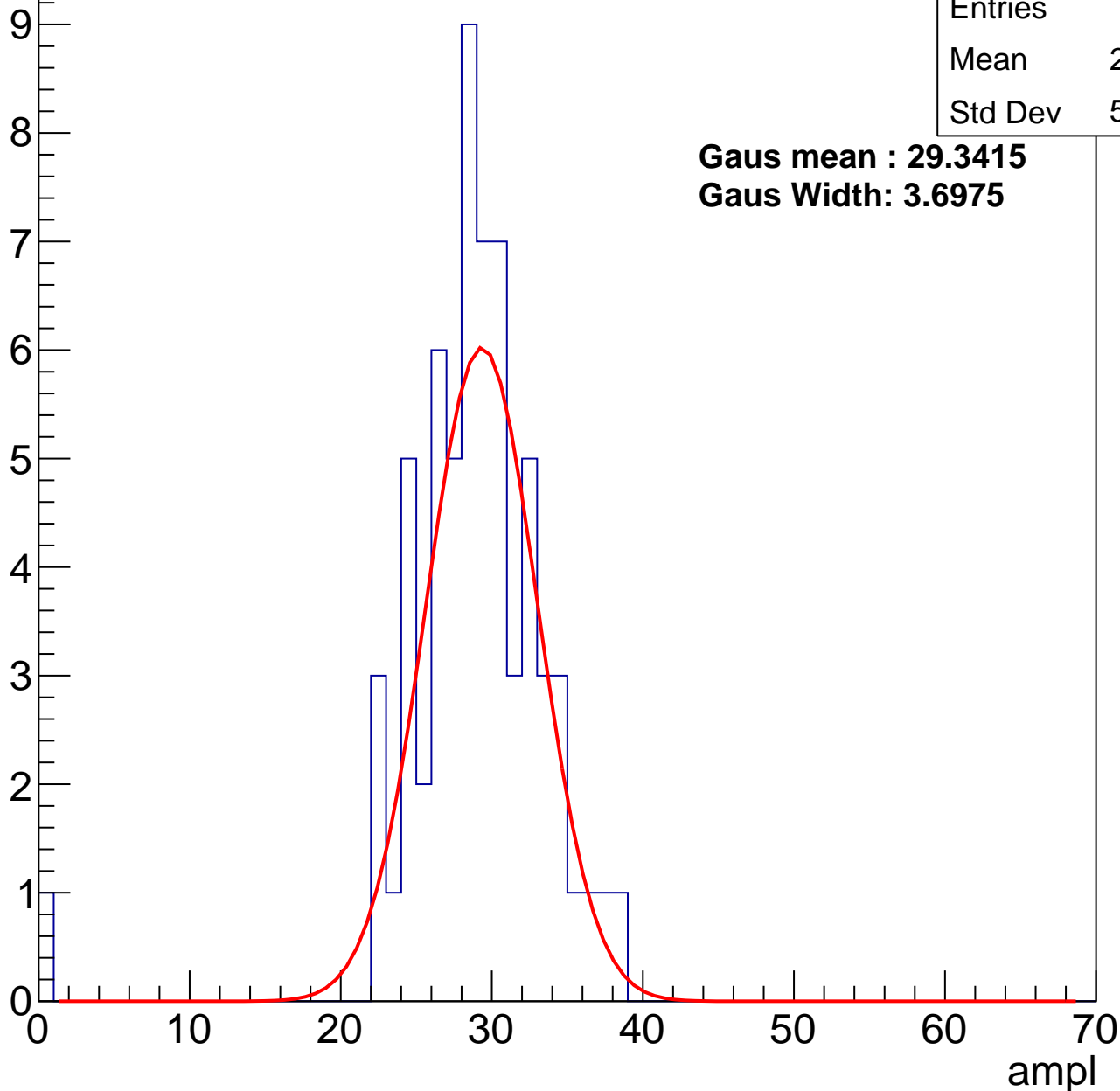
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.36
Std Dev	5.085

**Gaus mean : 29.3415**

**Gaus Width: 3.6975**



# B1L000S, U21-ch110, adc1

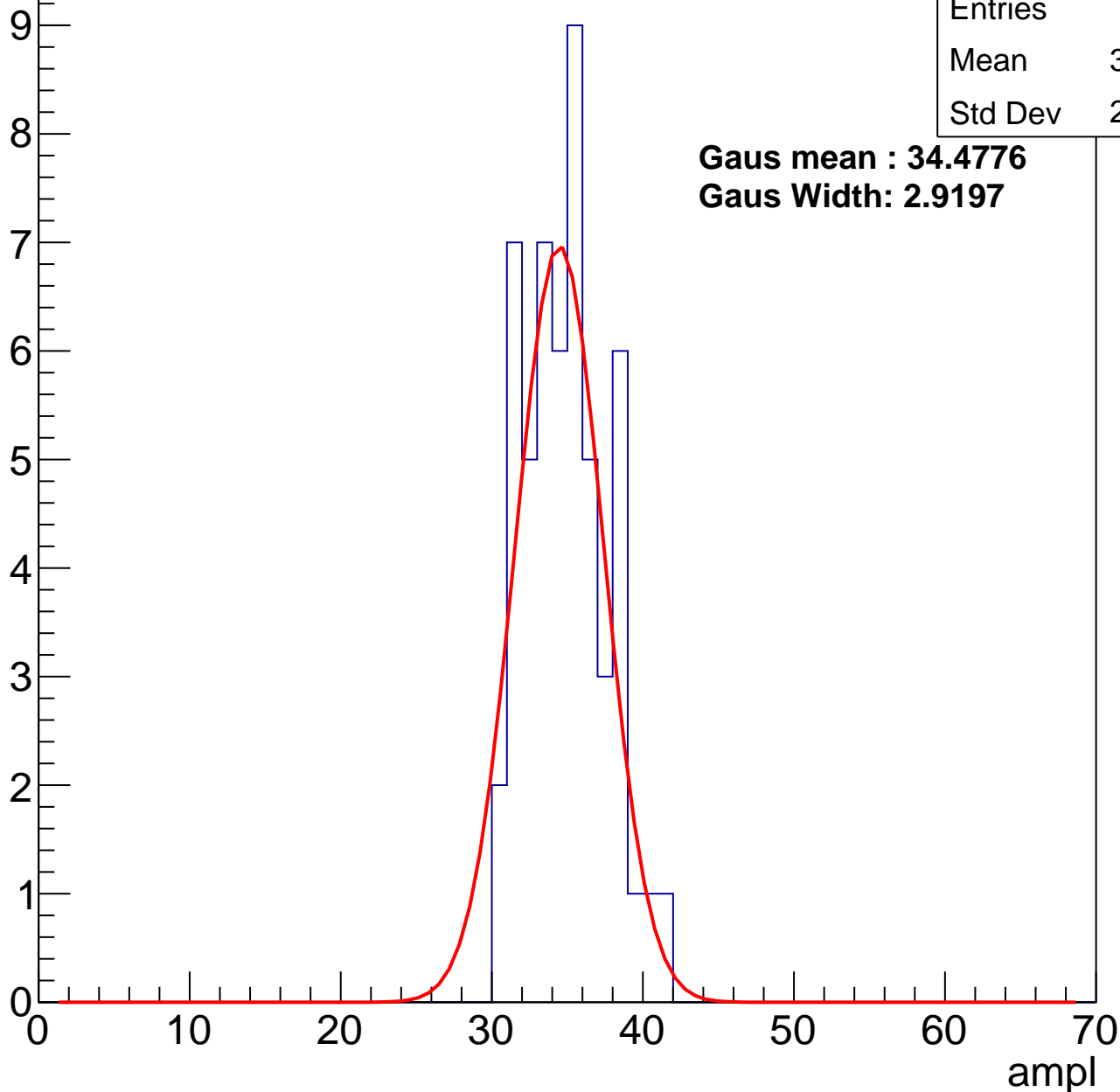
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	34.45
Std Dev	2.653

**Gaus mean : 34.4776**

**Gaus Width: 2.9197**



# B1L000S, U21-ch110, adc2

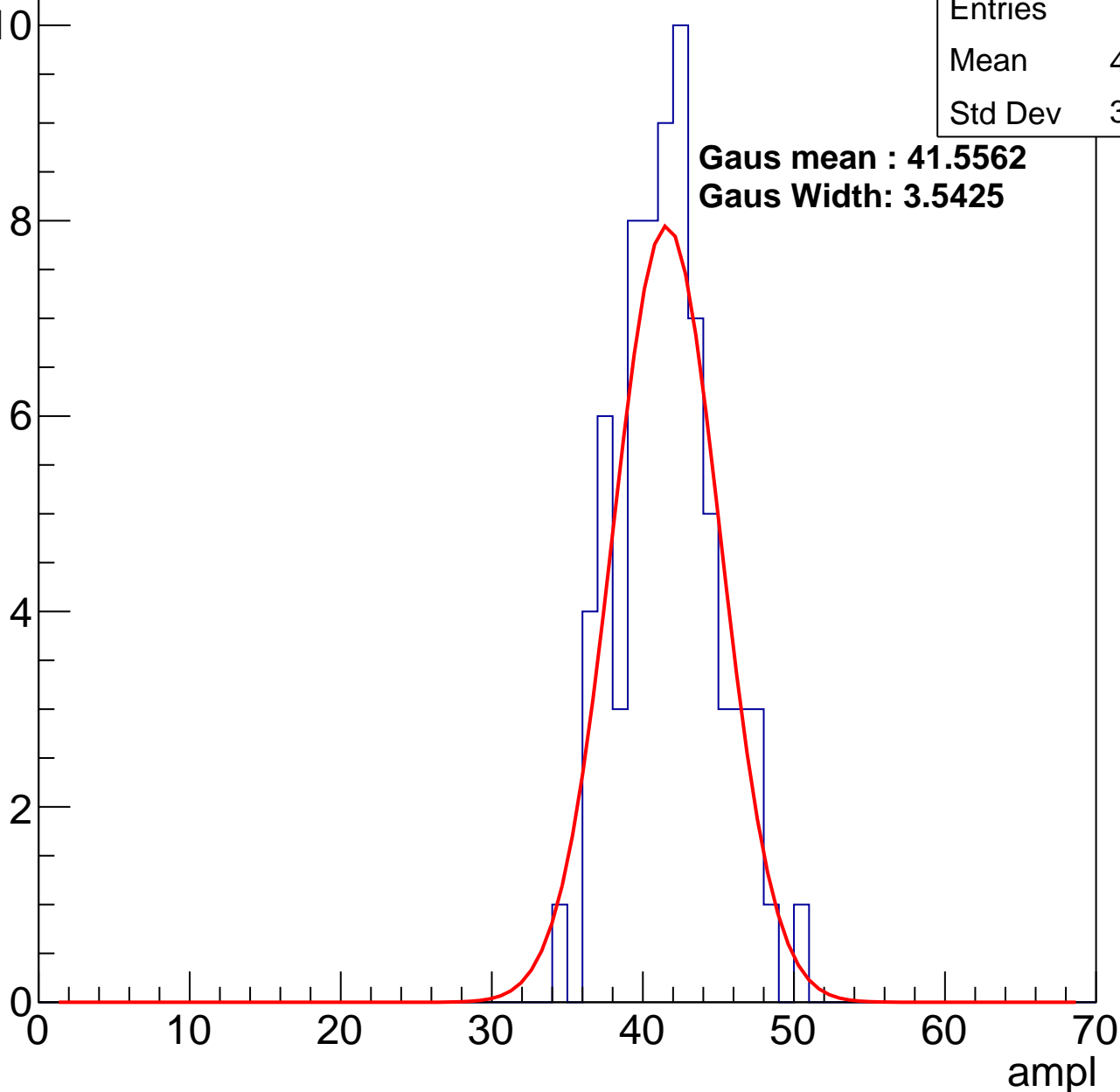
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	41.22
Std Dev	3.233

**Gaus mean : 41.5562**

**Gaus Width: 3.5425**

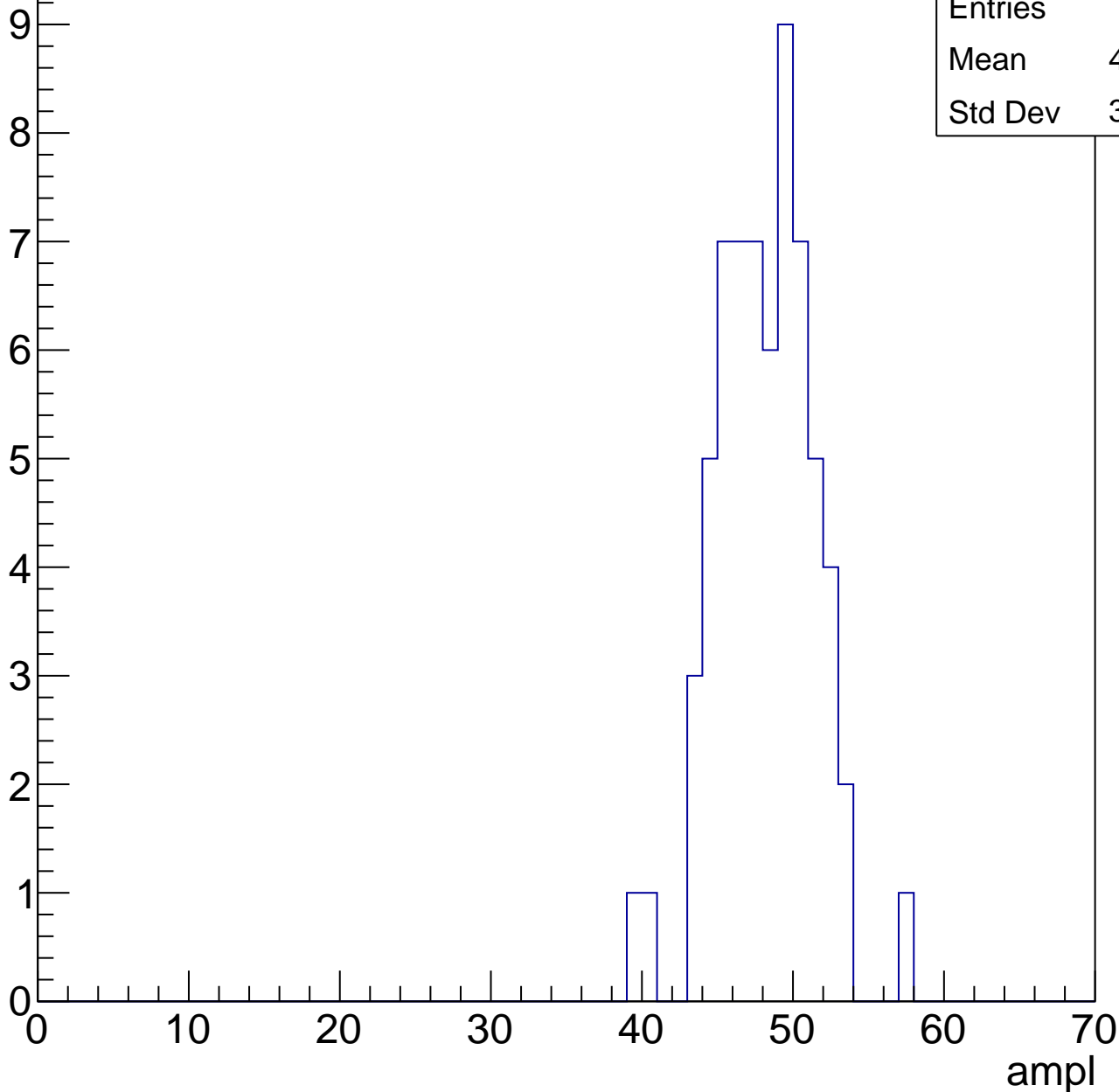


# B1L000S, U21-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	47.68
Std Dev	3.206



# B1L000S, U21-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

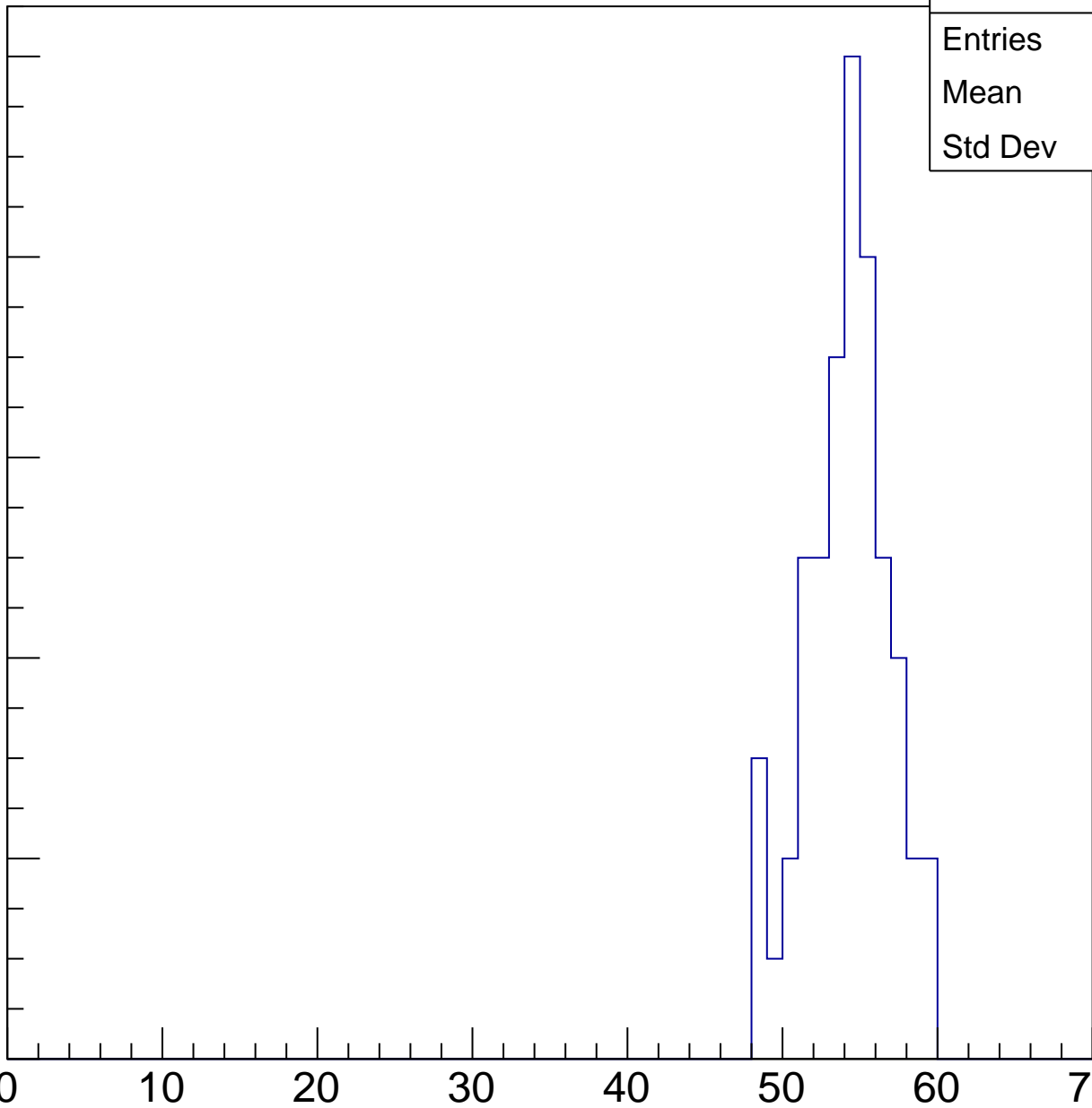
Entries	54
Mean	53.72
Std Dev	2.649

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

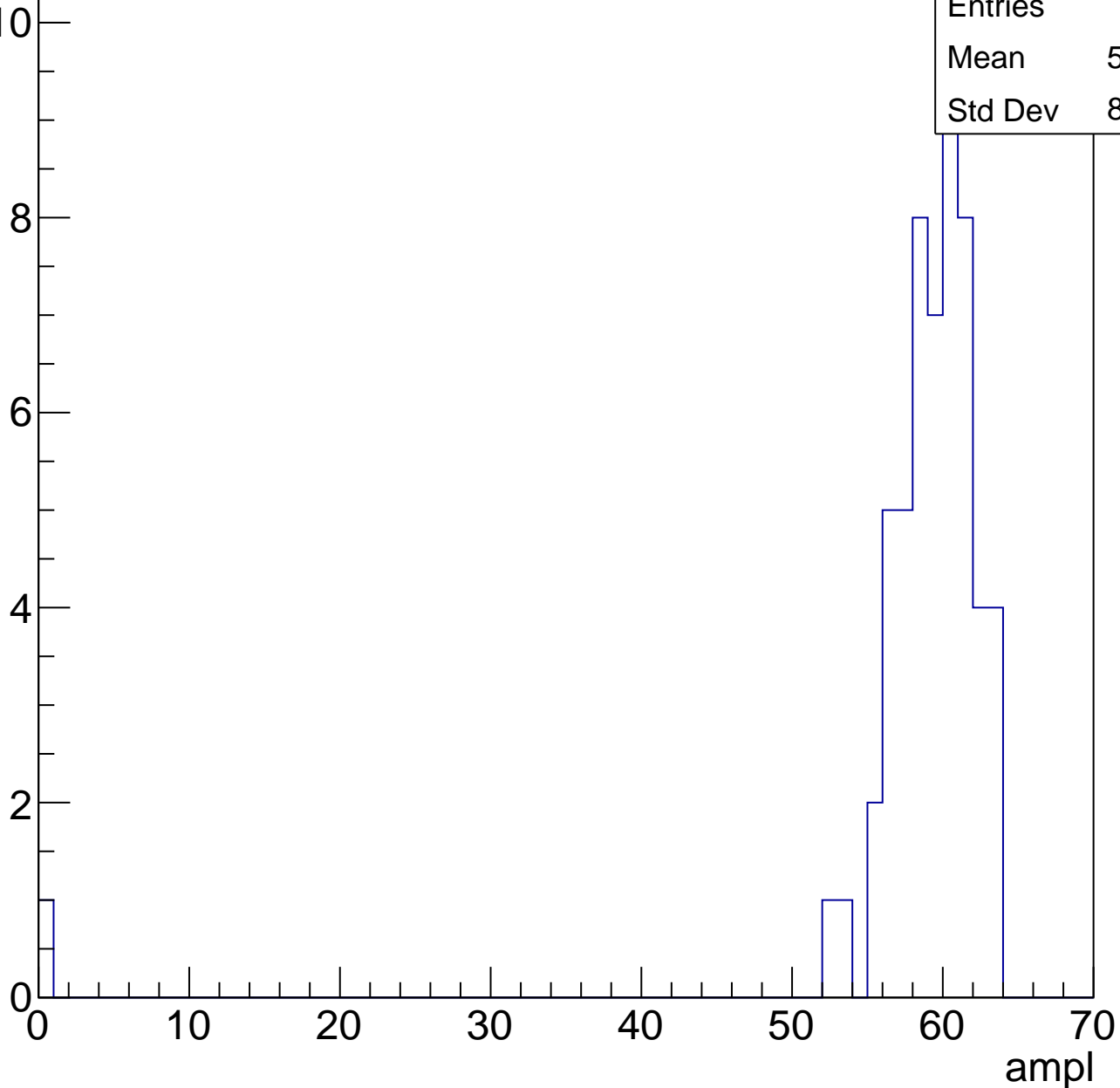


# B1L000S, U21-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	57.95
Std Dev	8.184

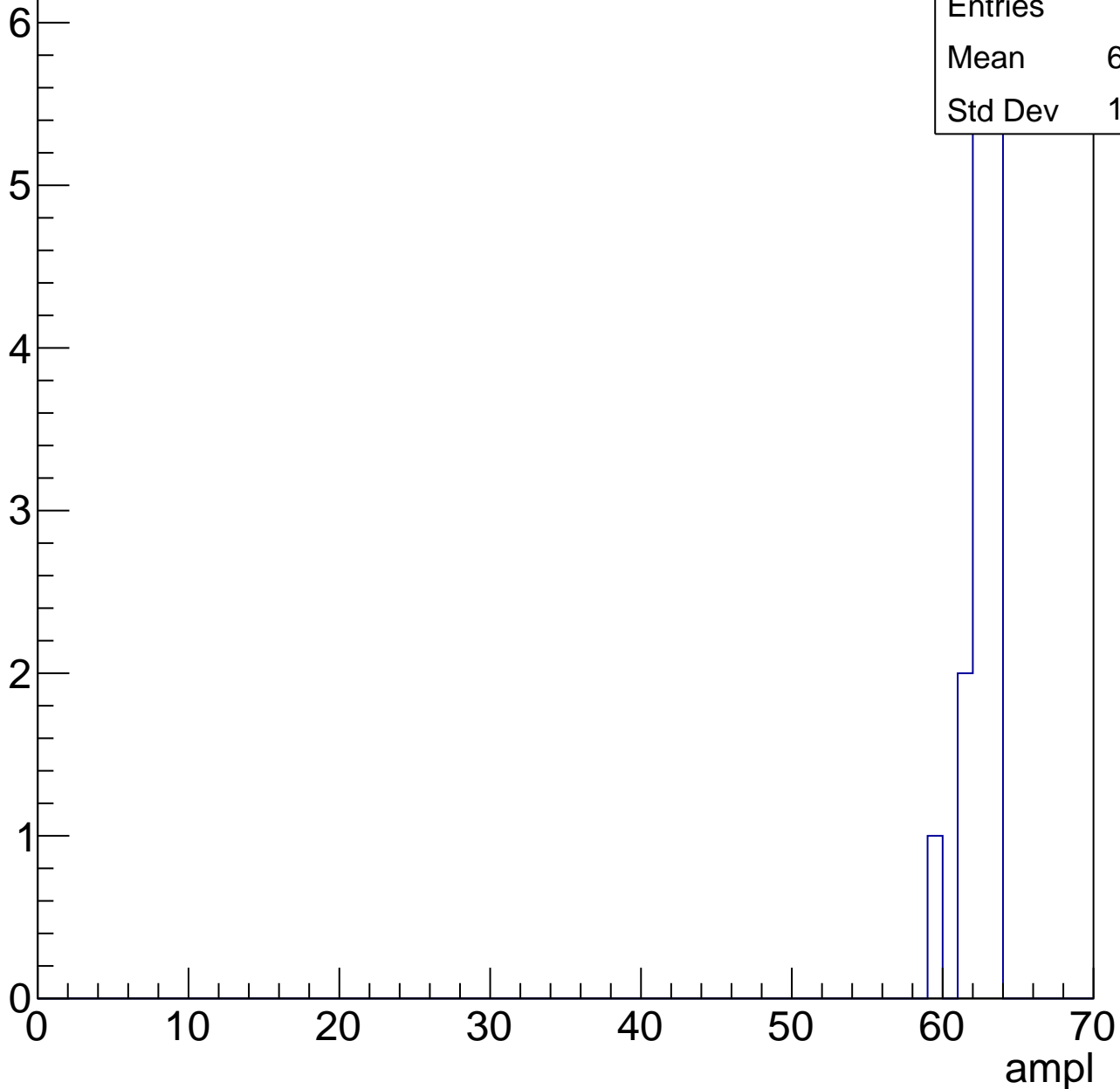


# B1L000S, U21-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	62.07
Std Dev	1.062





# B1L000S, U21-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch111, adc0

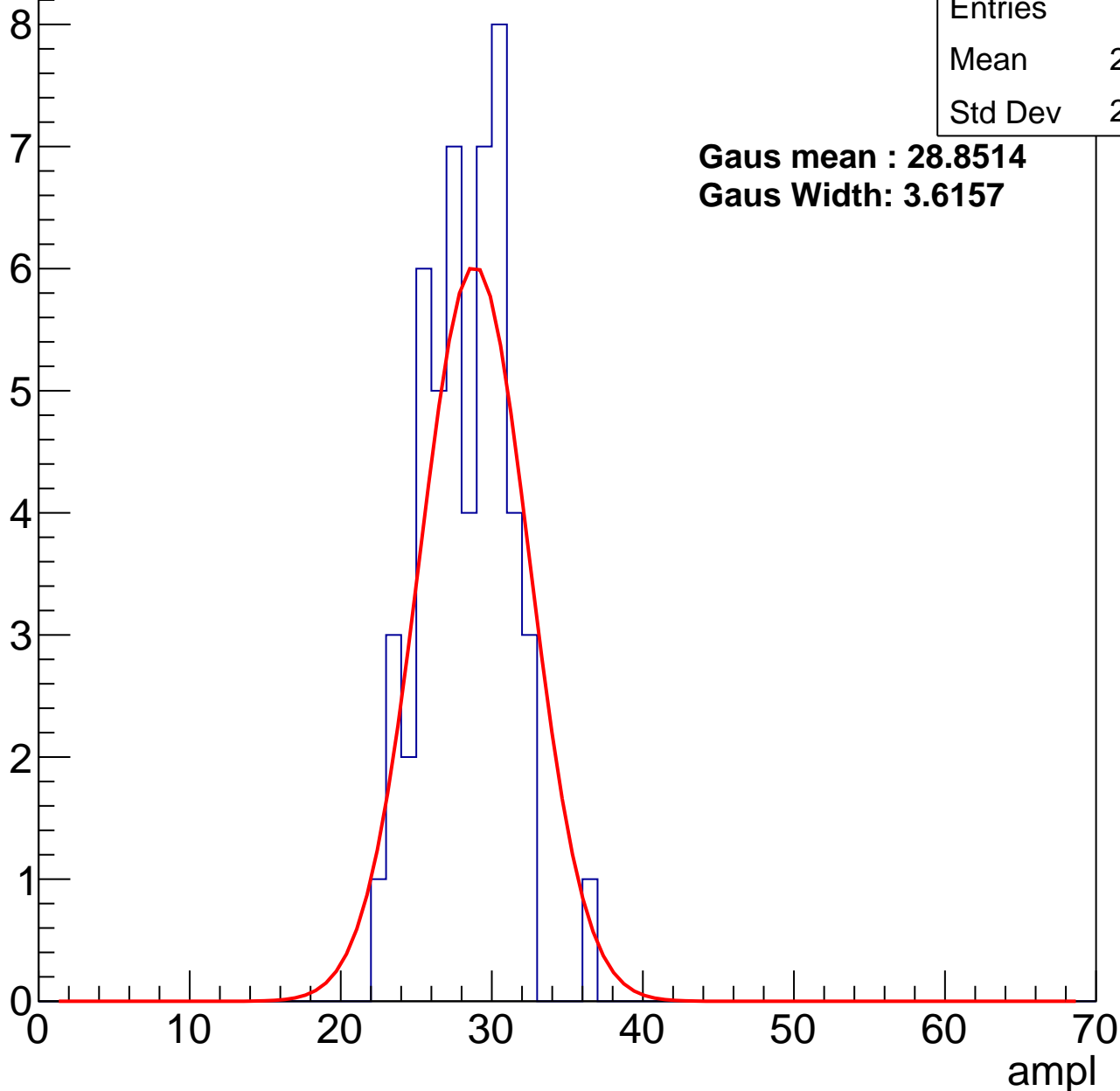
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	27.82
Std Dev	2.833

**Gaus mean : 28.8514**

**Gaus Width: 3.6157**



# B1L000S, U21-ch111, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	34.89
Std Dev	3.609

**Gaus mean : 35.2959**

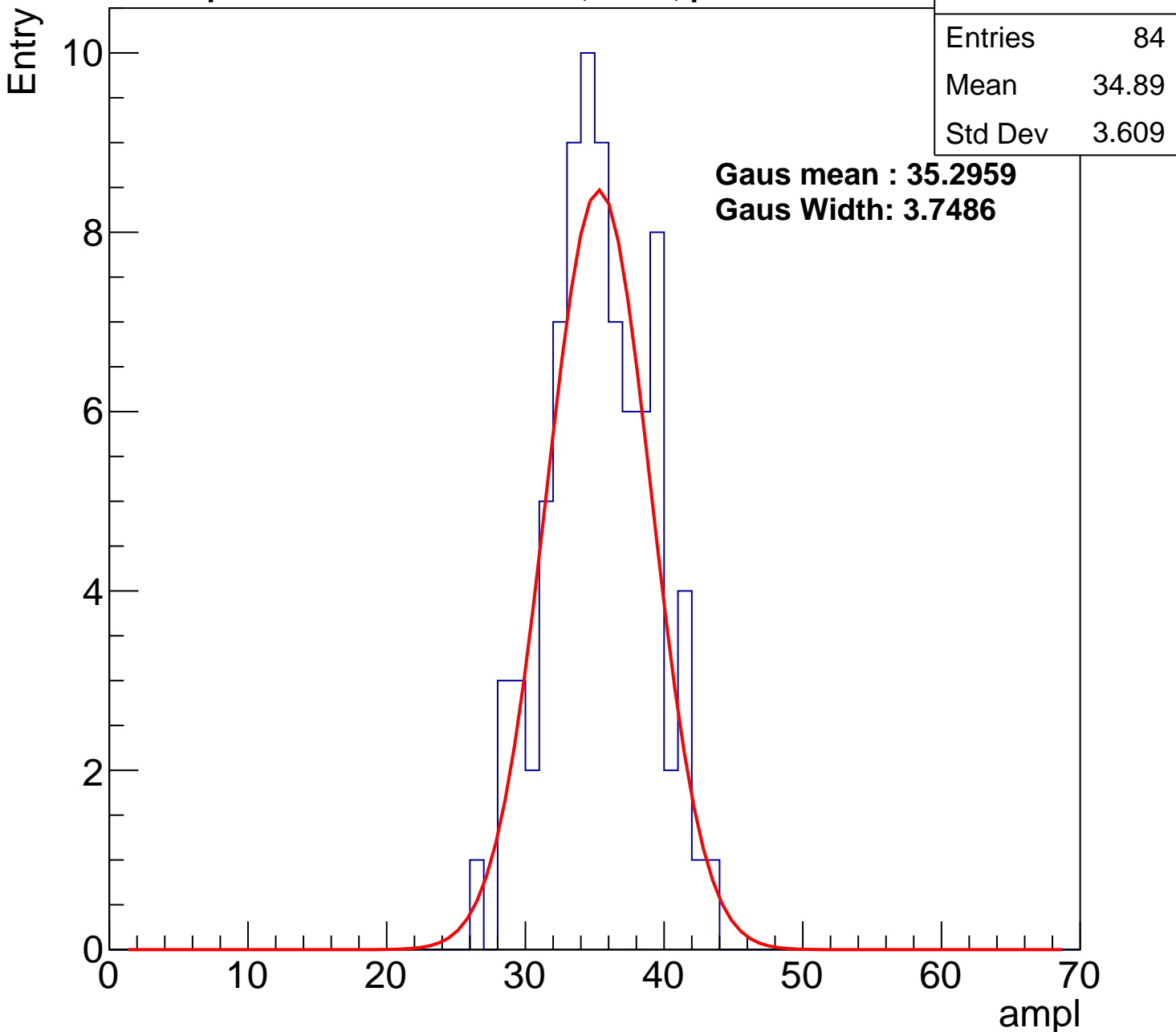
**Gaus Width: 3.7486**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch111, adc2

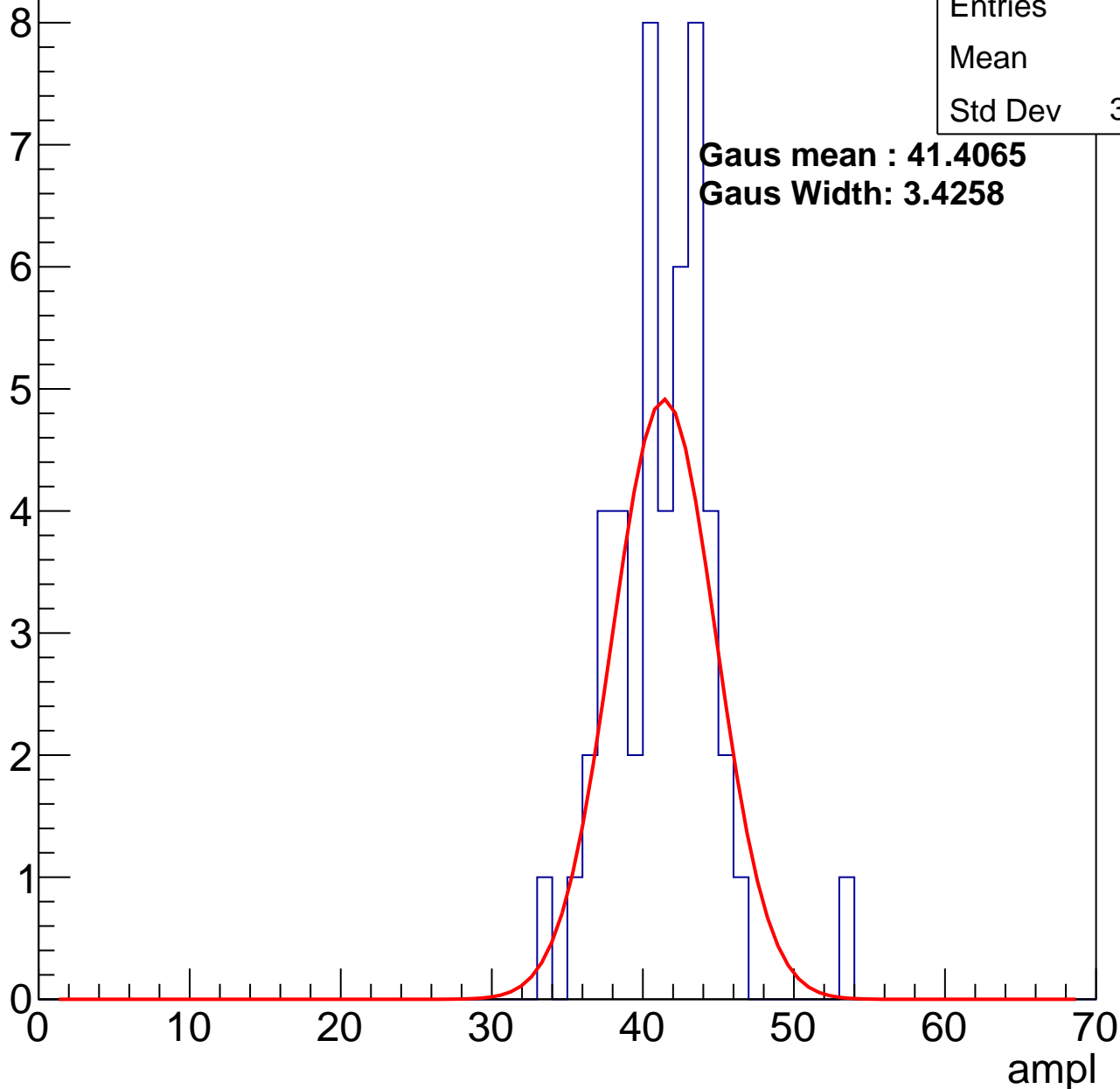
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	40.9
Std Dev	3.356

**Gaus mean : 41.4065**

**Gaus Width: 3.4258**

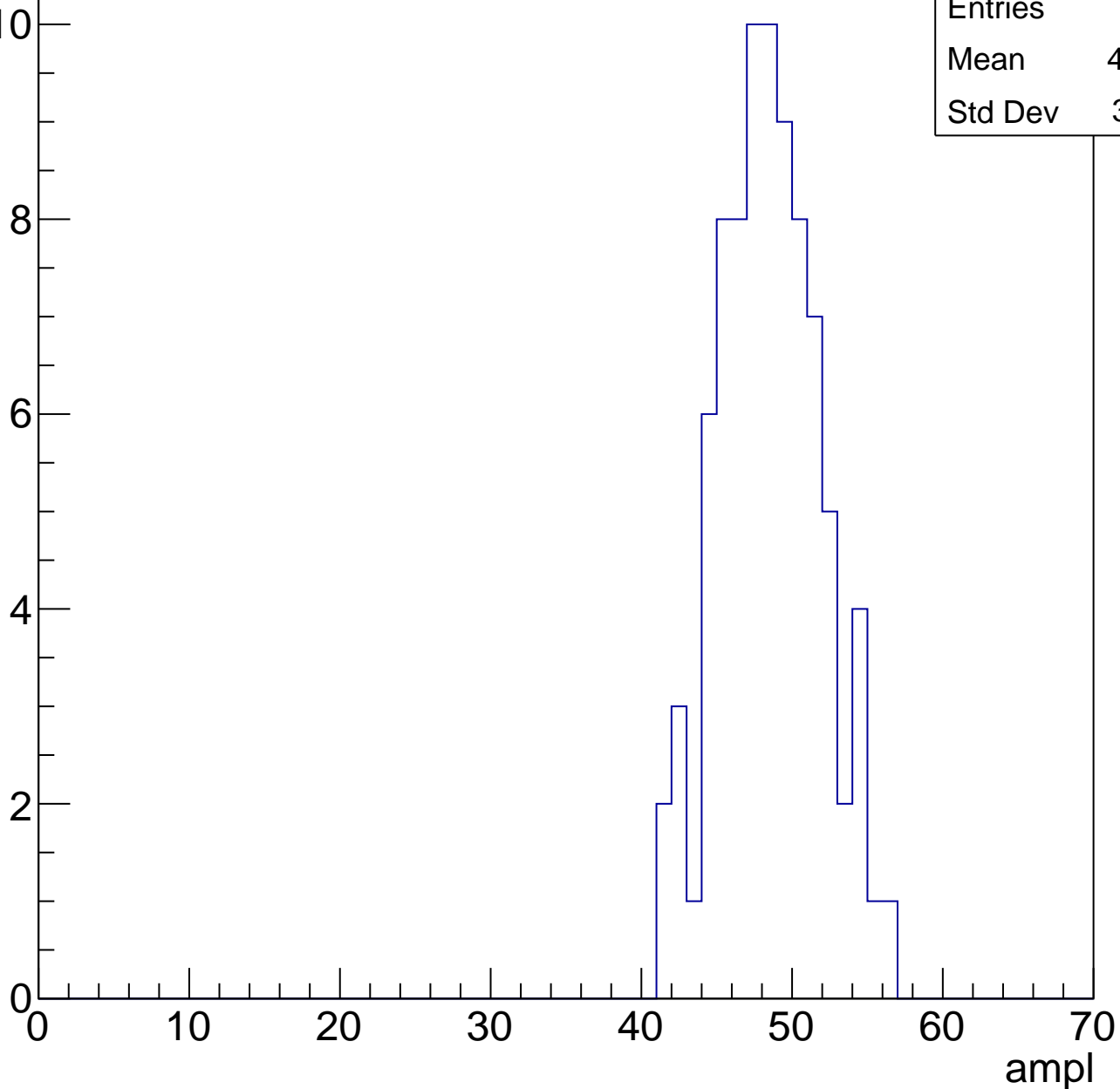


# B1L000S, U21-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	85
Mean	48.05
Std Dev	3.311

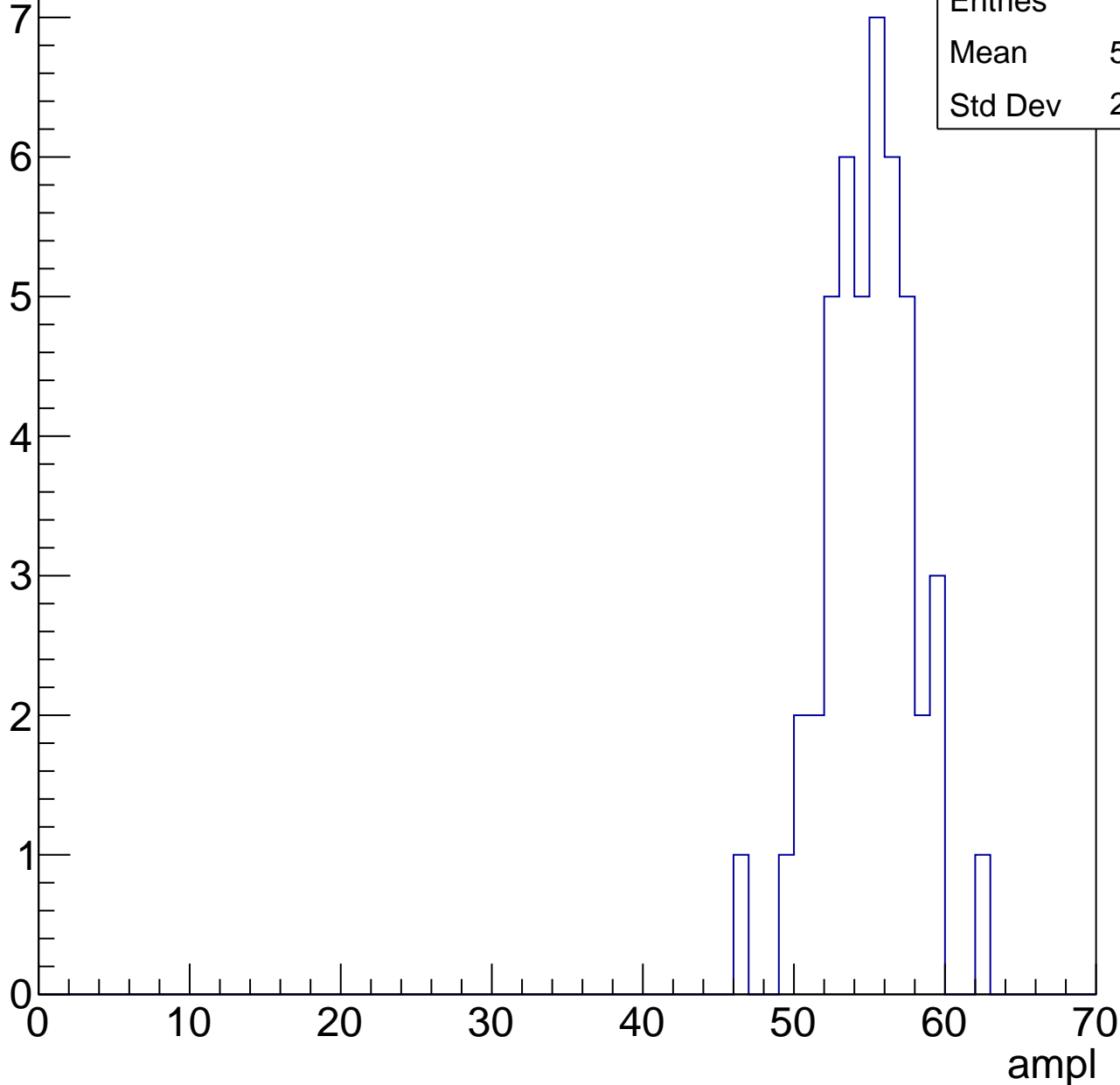


# B1L000S, U21-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	54.48
Std Dev	2.954

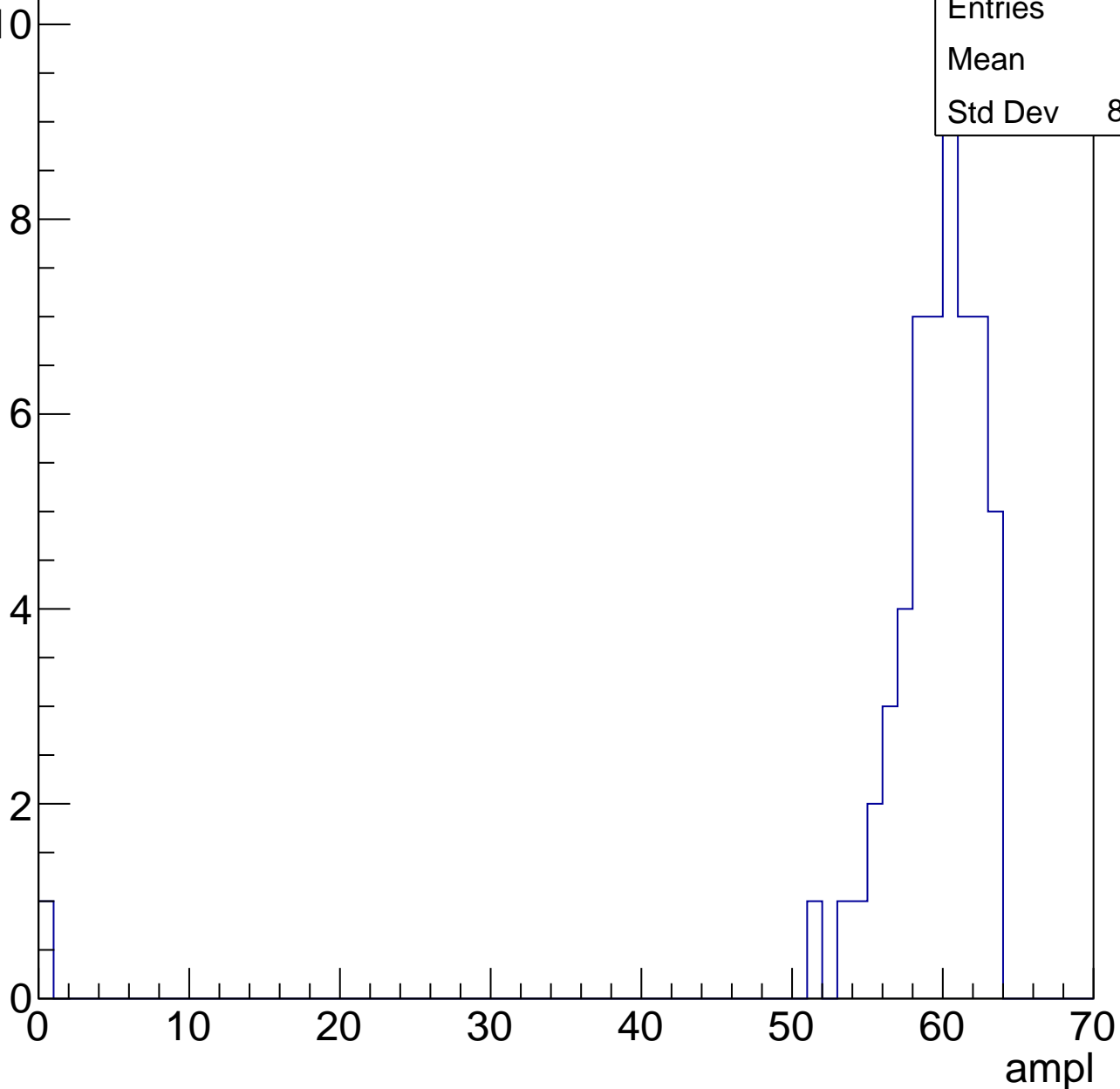


# B1L000S, U21-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	58.2
Std Dev	8.273

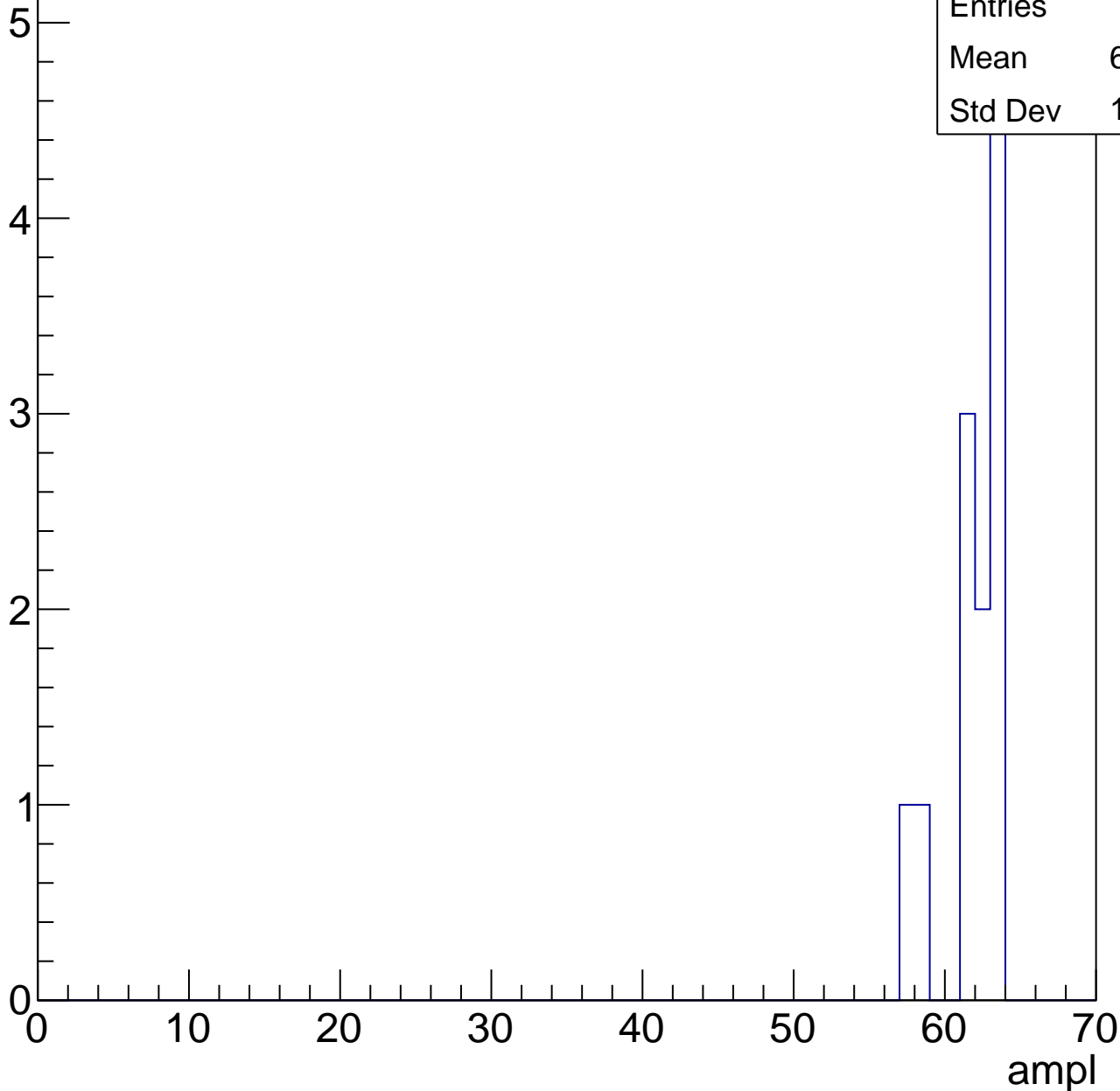


# B1L000S, U21-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	61.42
Std Dev	1.935





# B1L000S, U21-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch112, adc0

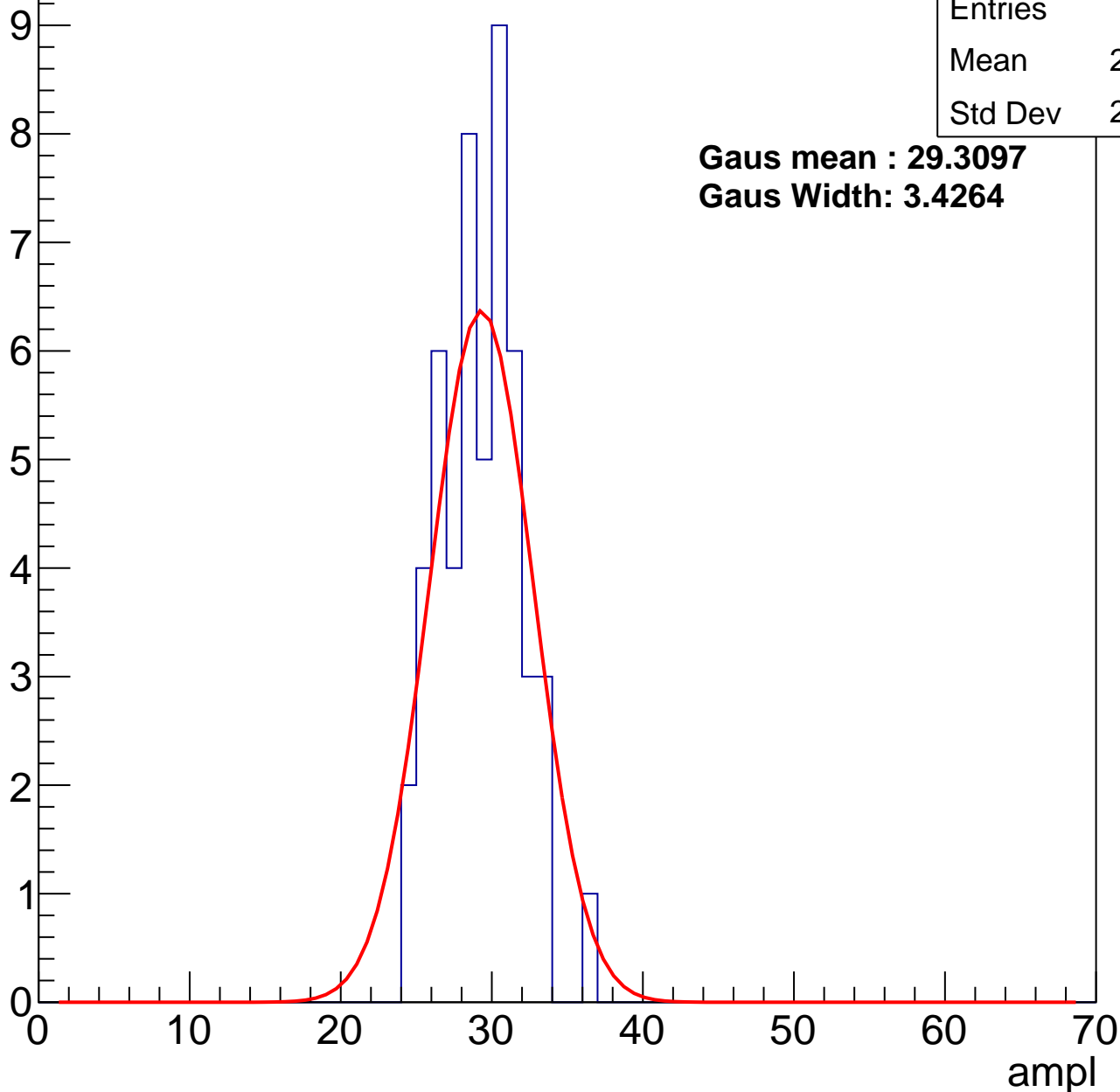
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	28.78
Std Dev	2.607

**Gaus mean : 29.3097**

**Gaus Width: 3.4264**



# B1L000S, U21-ch112, adc1

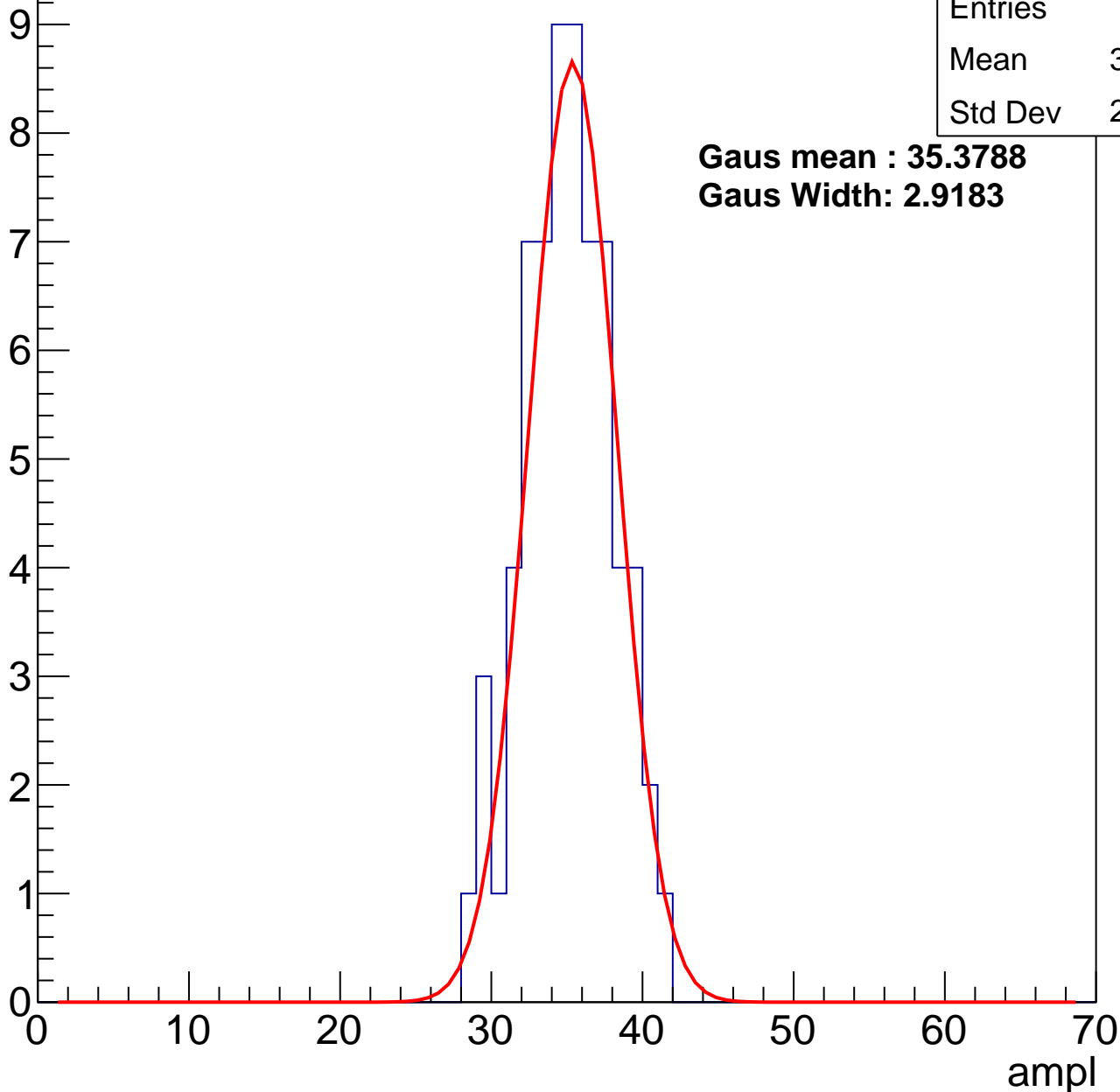
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.62
Std Dev	2.906

**Gaus mean : 35.3788**

**Gaus Width: 2.9183**



# B1L000S, U21-ch112, adc2

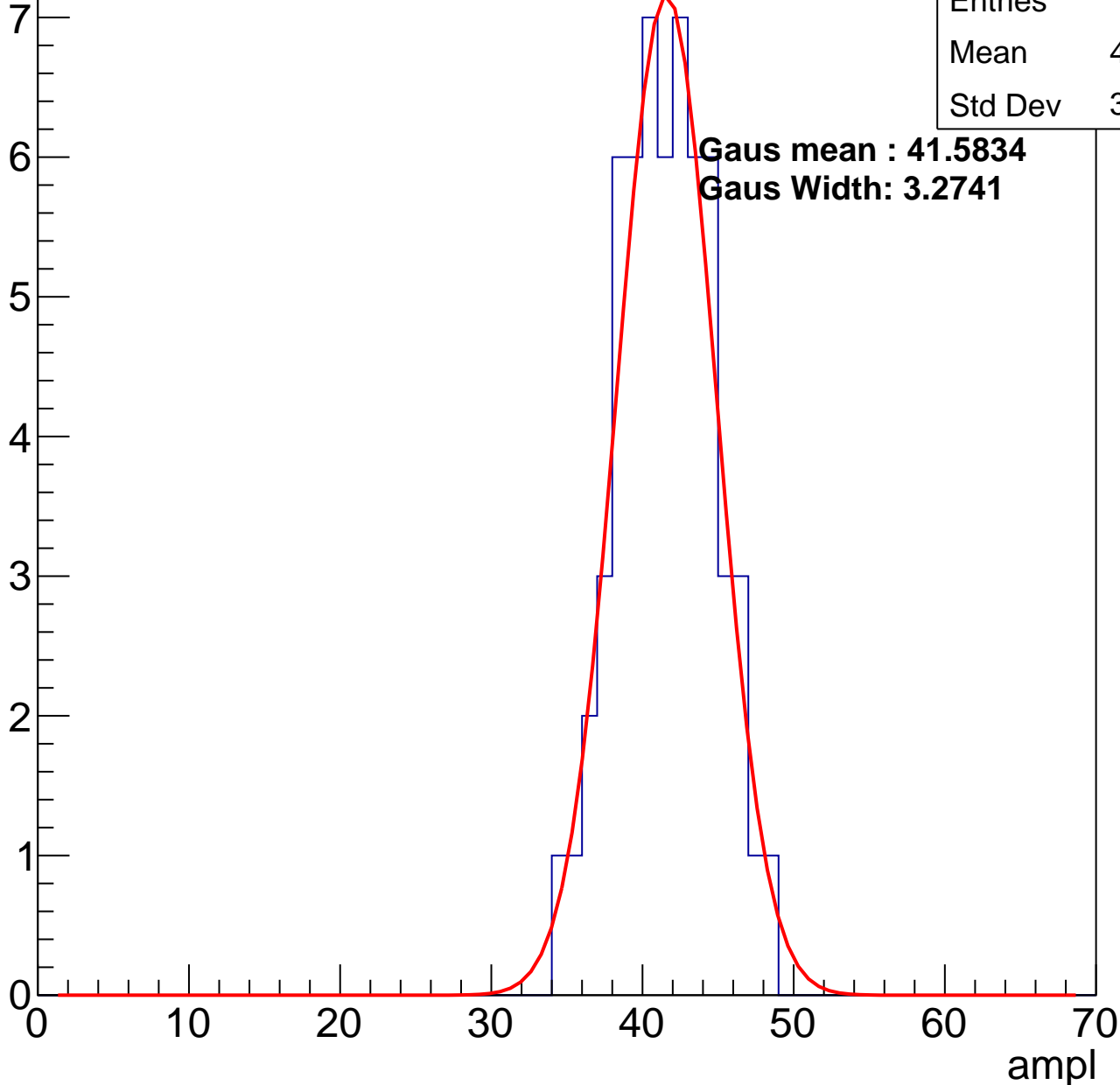
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.08
Std Dev	3.082

**Gaus mean : 41.5834**

**Gaus Width: 3.2741**

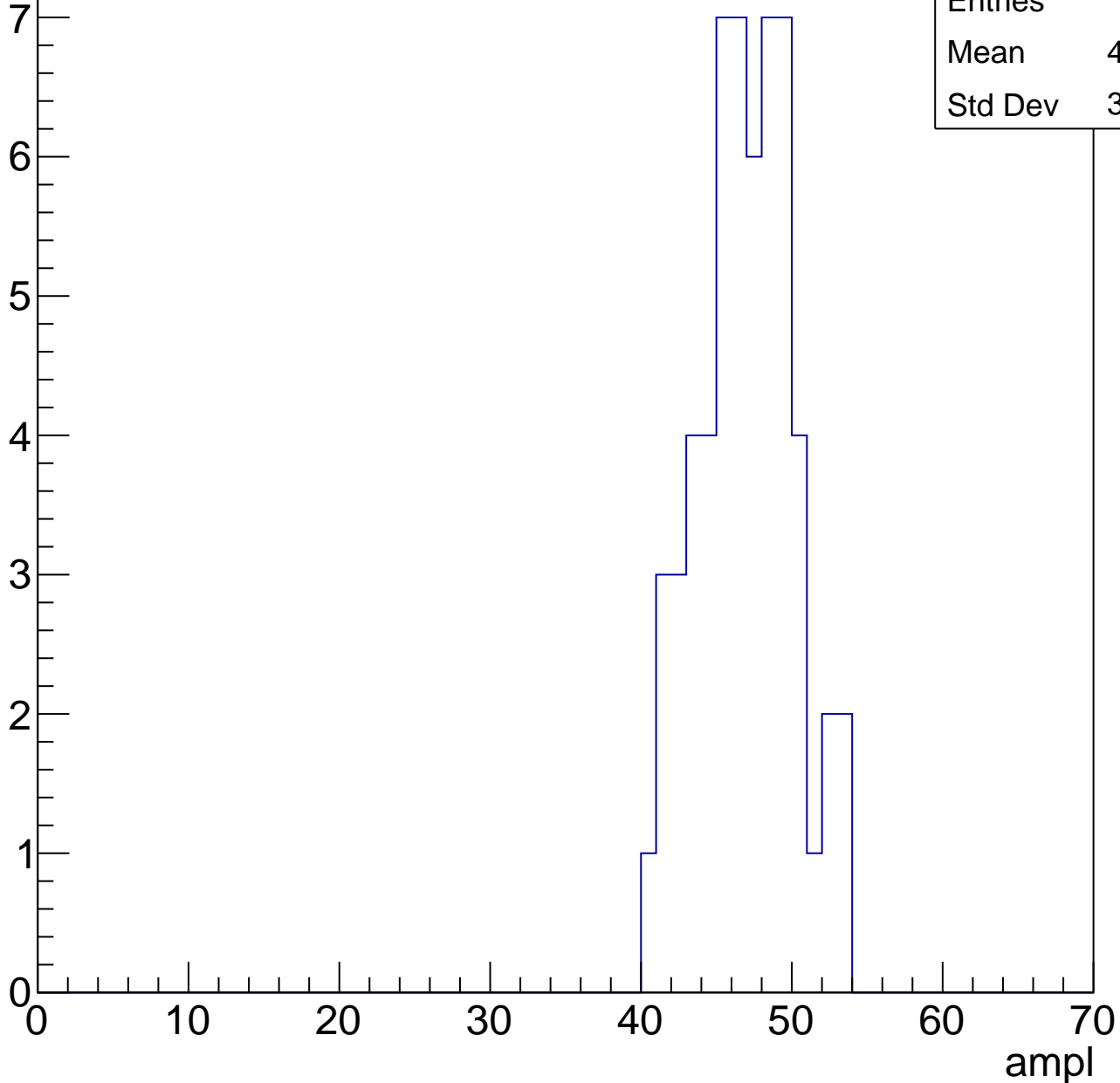


# B1L000S, U21-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	46.48
Std Dev	3.109

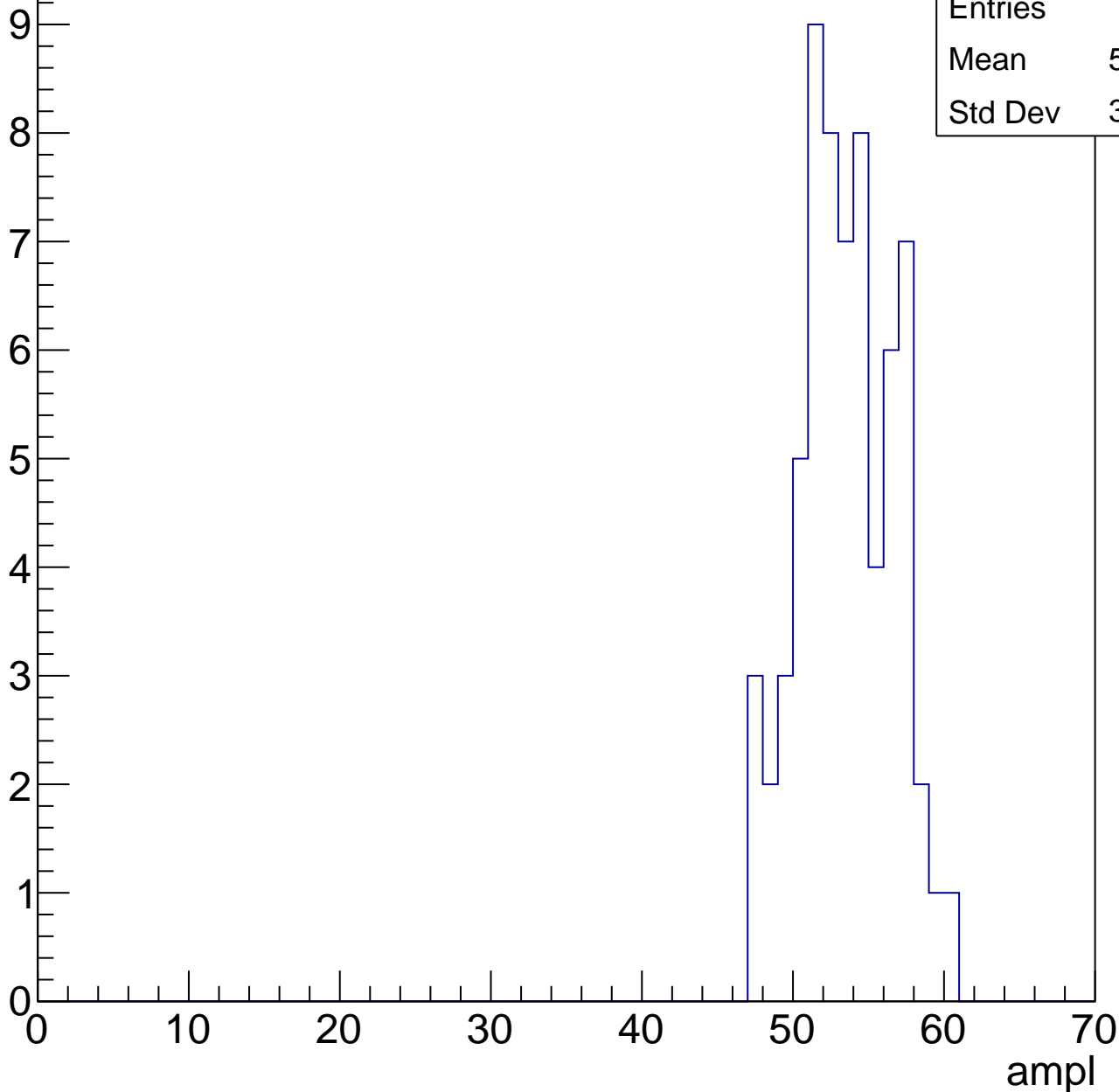


# B1L000S, U21-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	53.06
Std Dev	3.064

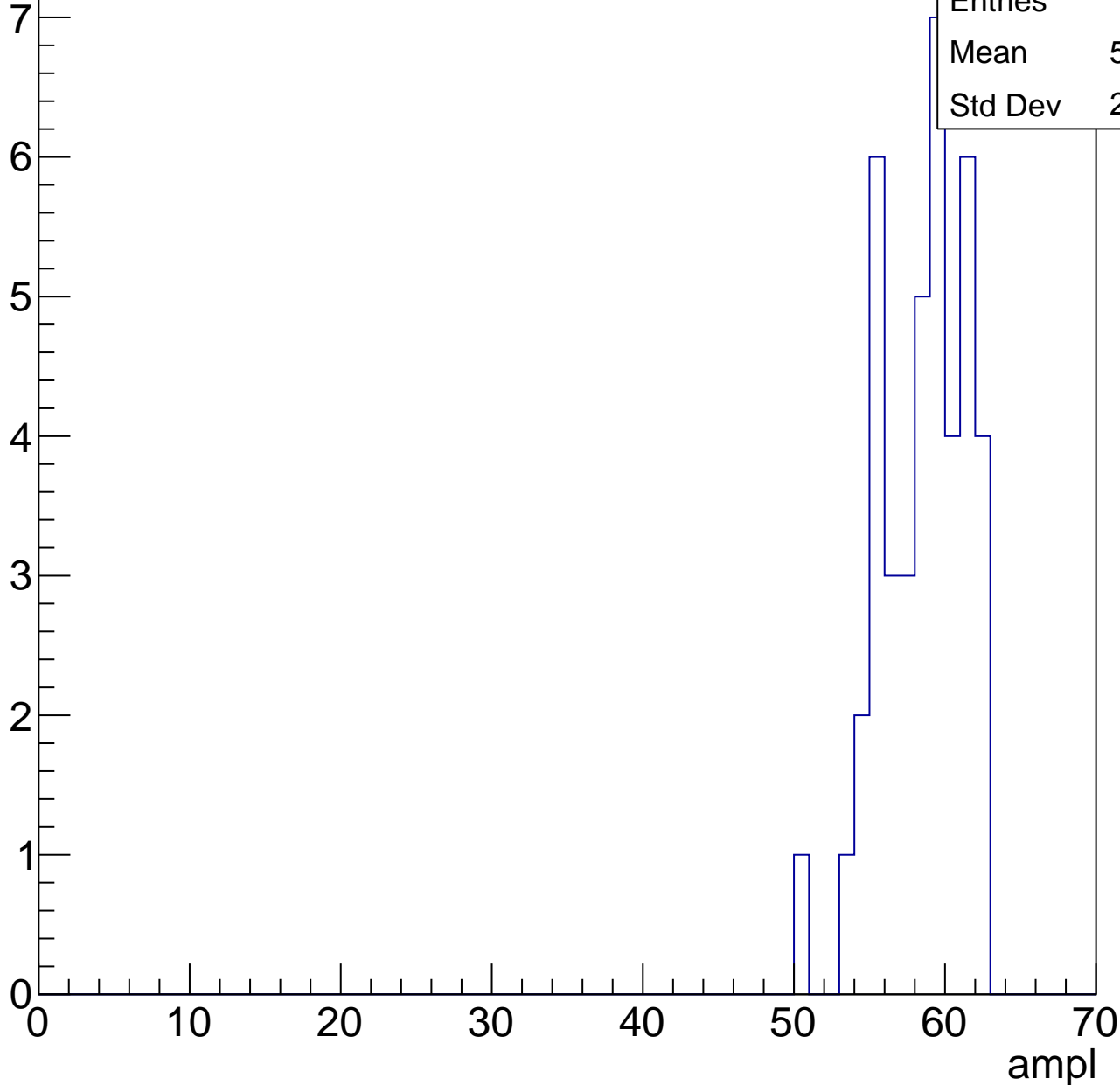


# B1L000S, U21-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	58.02
Std Dev	2.807

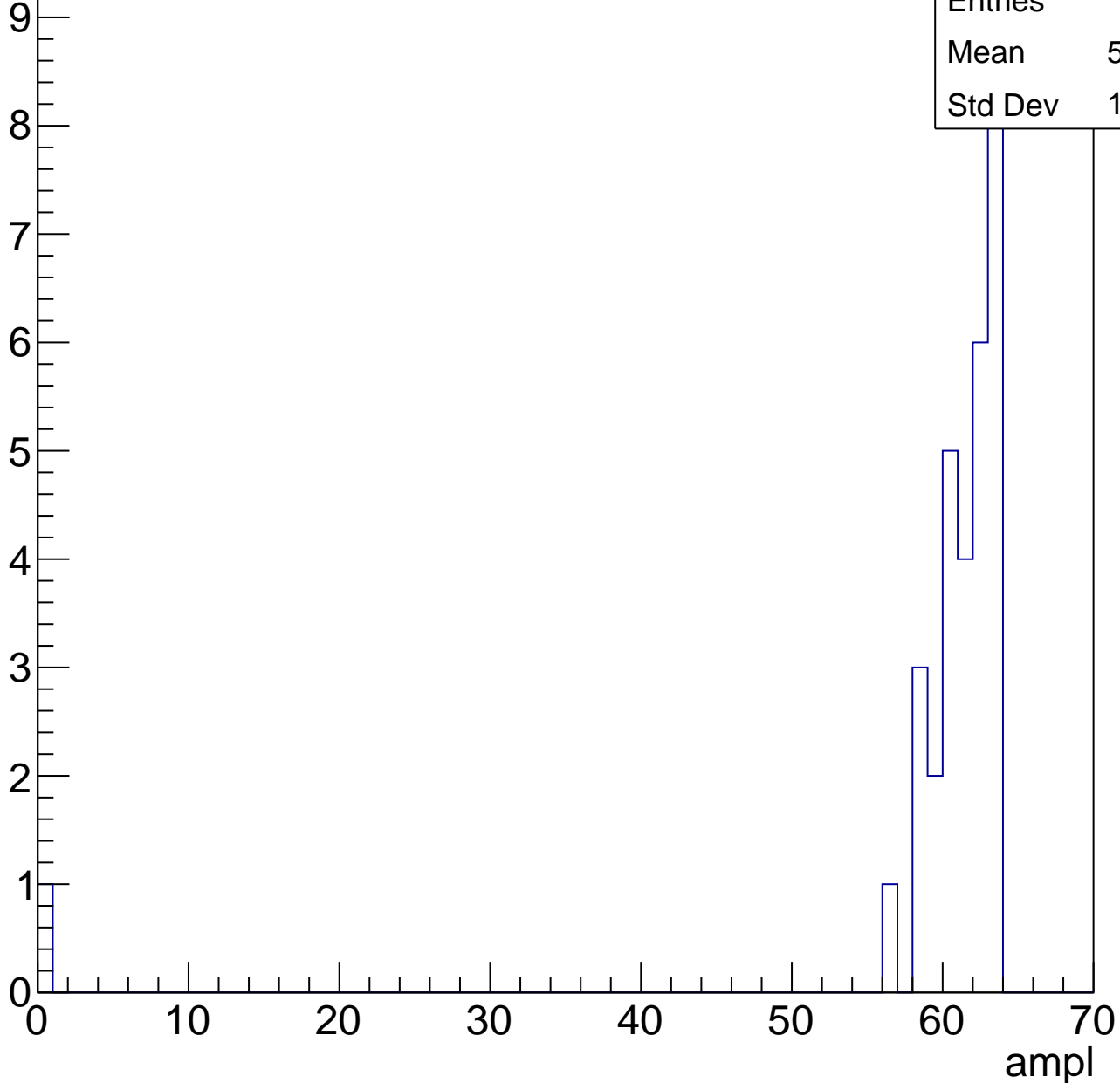


# B1L000S, U21-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	31
Mean	59.06
Std Dev	10.94

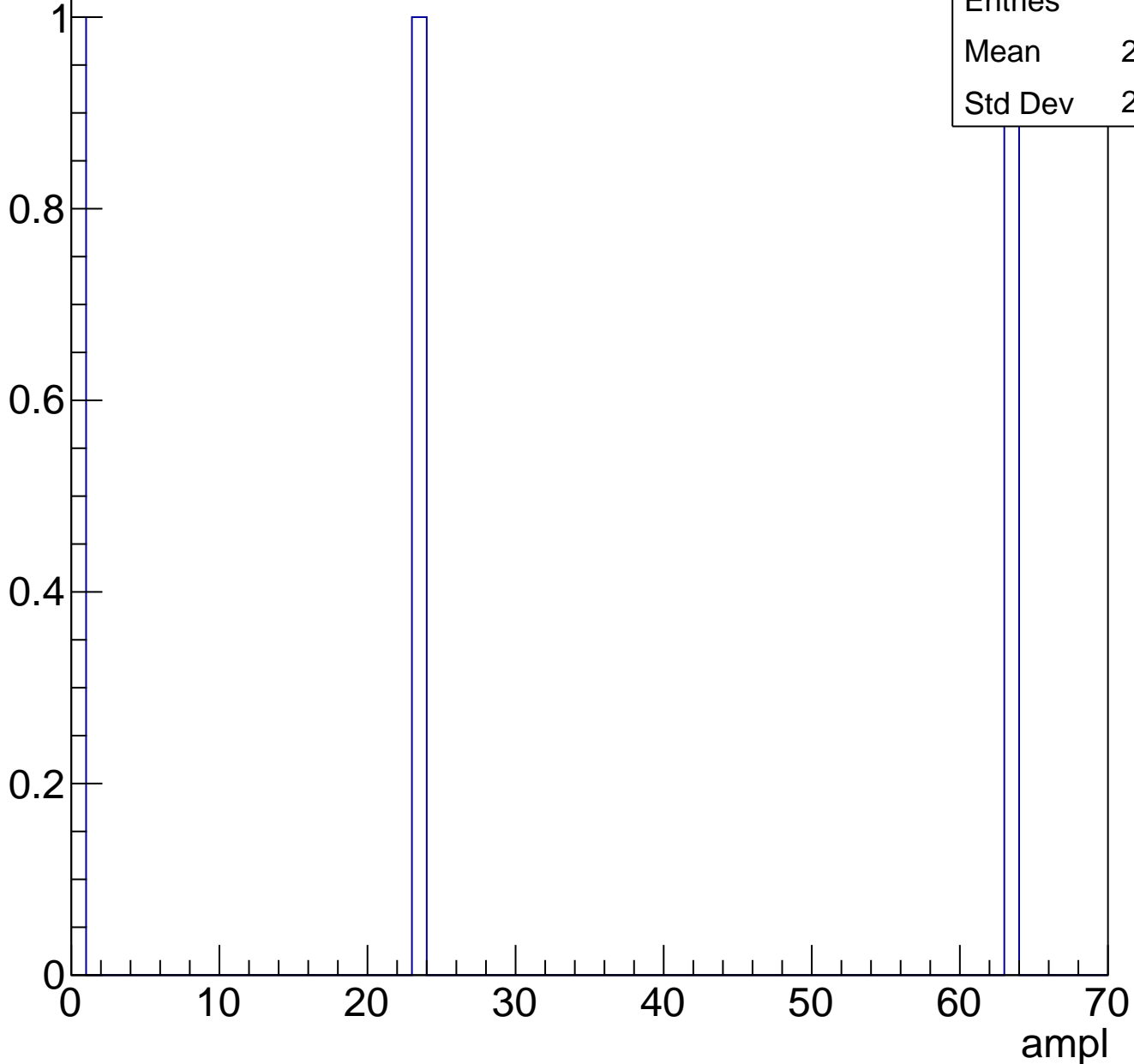




# B1L000S, U21-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.67
Std Dev	26.03

# B1L000S, U21-ch113, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	29.19
Std Dev	4.89

**Gaus mean : 30.3906**

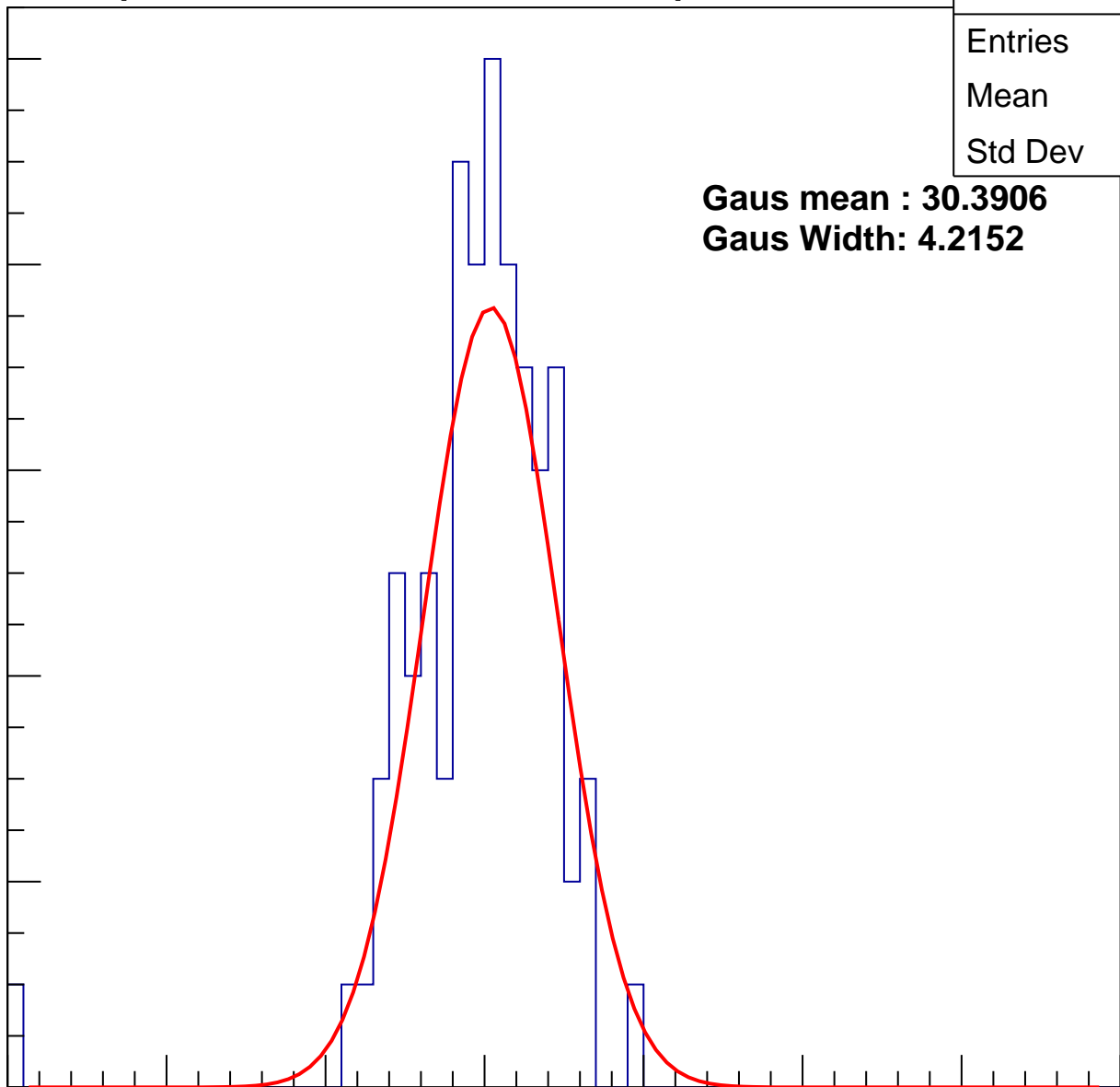
**Gaus Width: 4.2152**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch113, adc1

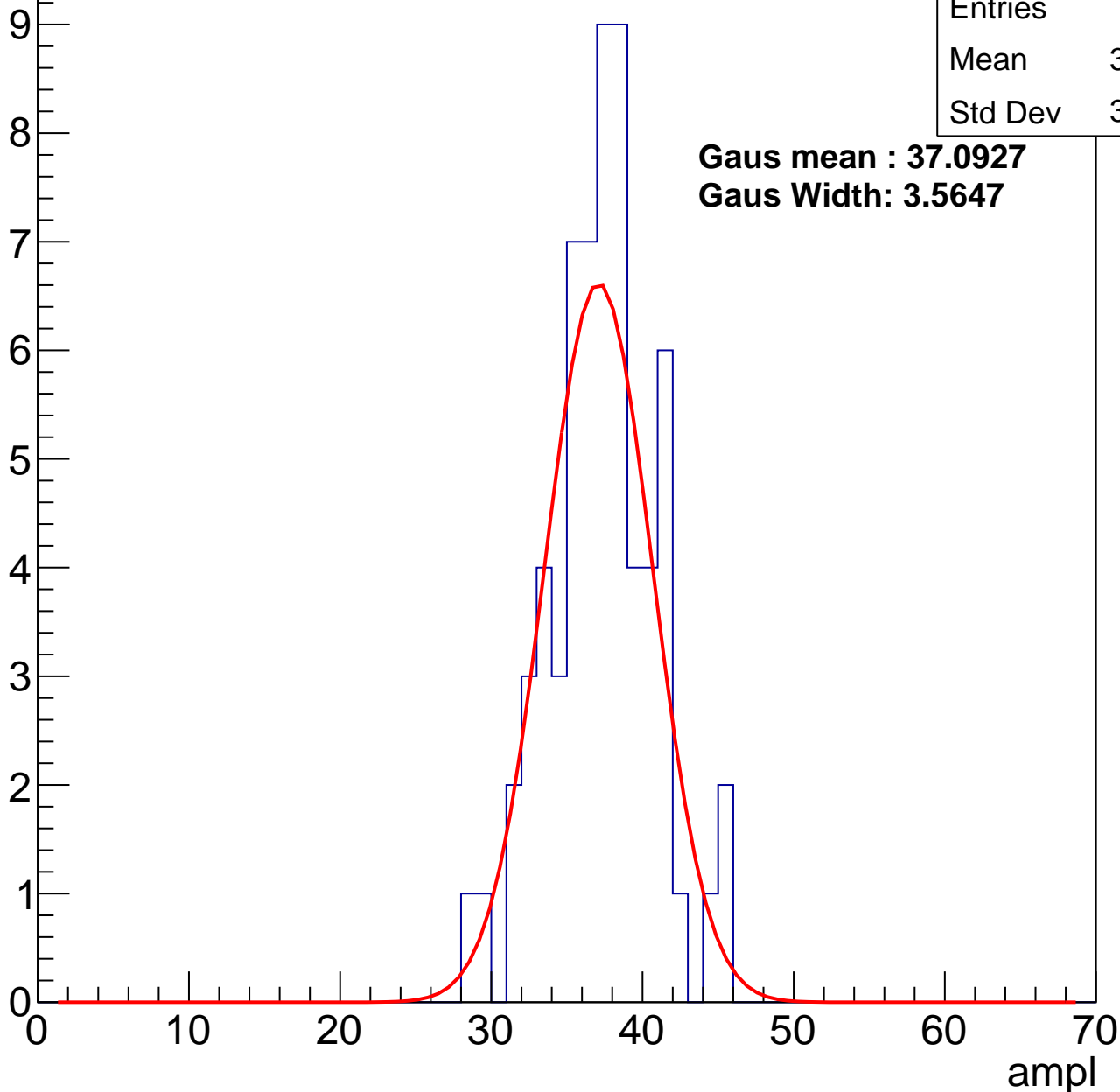
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	36.86
Std Dev	3.482

**Gaus mean : 37.0927**

**Gaus Width: 3.5647**



# B1L000S, U21-ch113, adc2

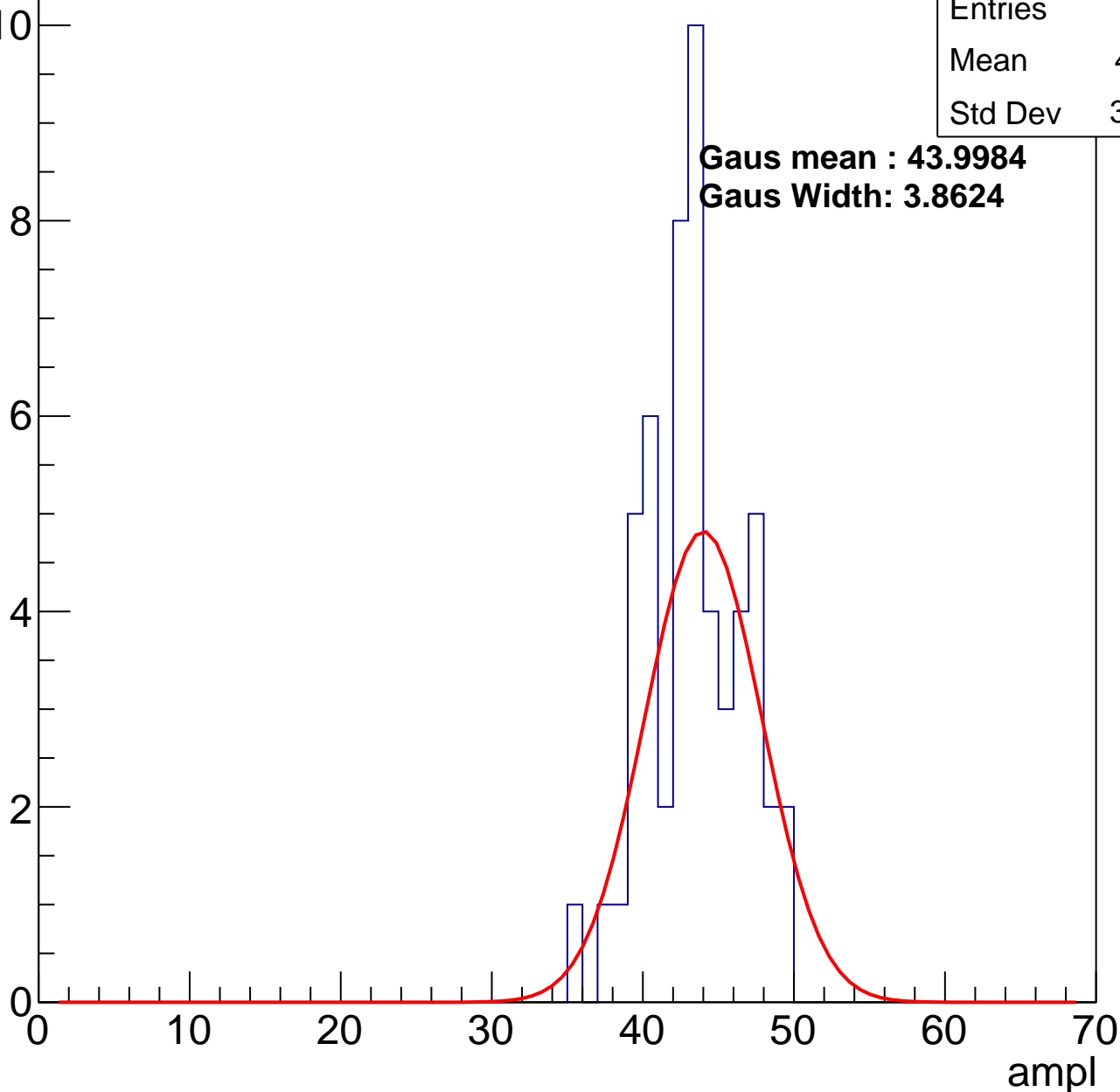
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.91
Std Dev	3.129

**Gaus mean : 43.9984**

**Gaus Width: 3.8624**

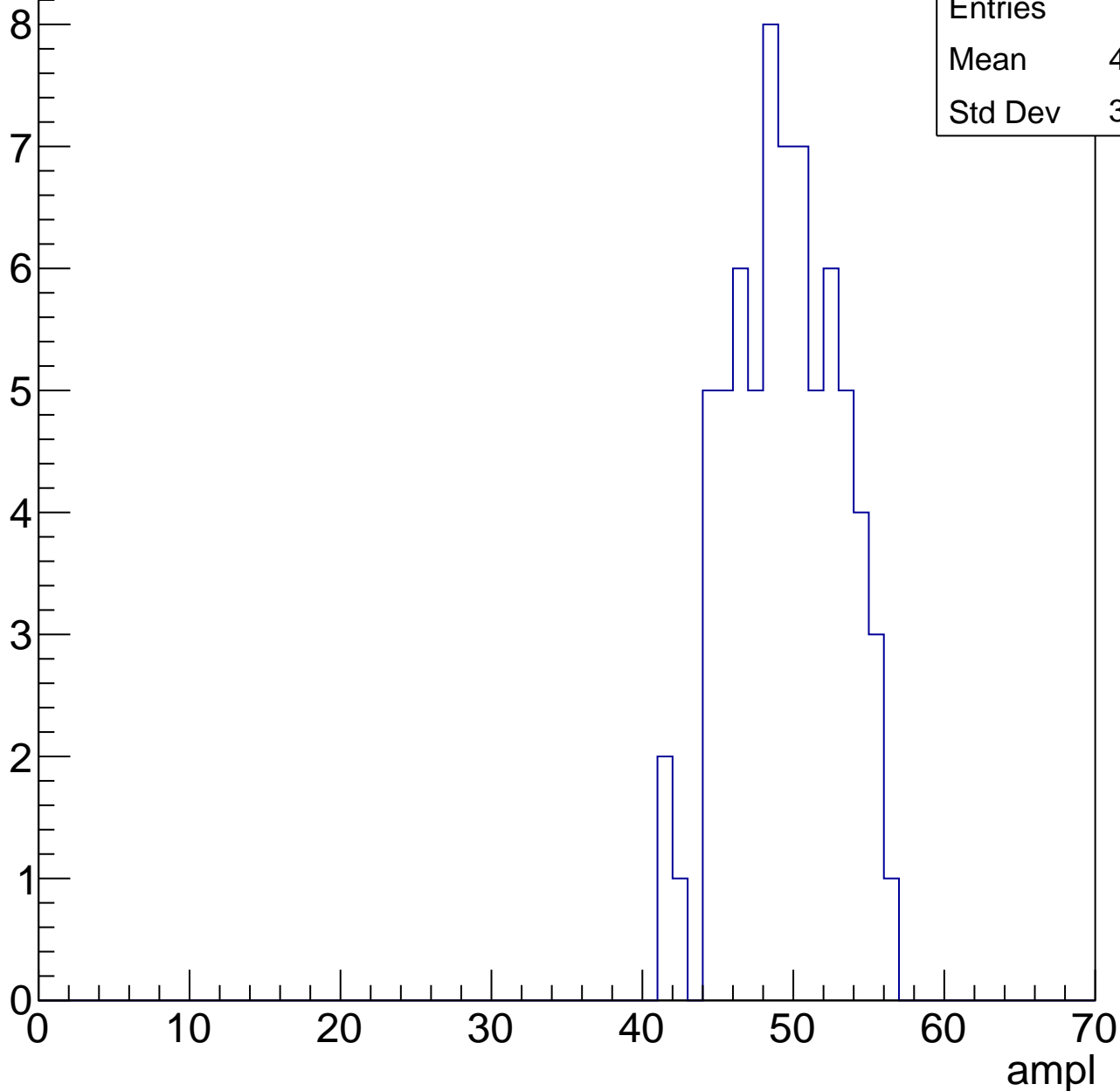


# B1L000S, U21-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	48.94
Std Dev	3.549

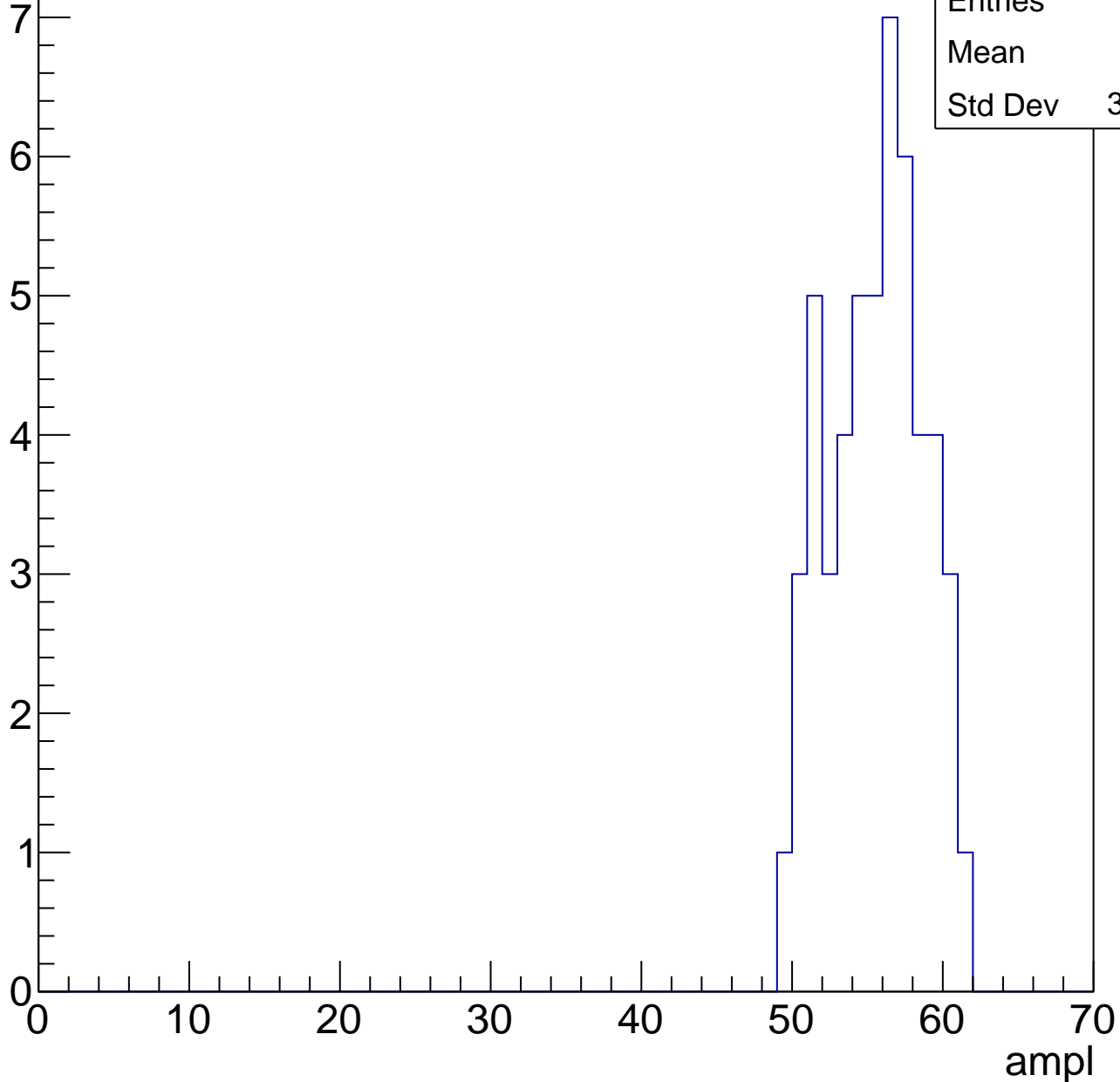


# B1L000S, U21-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

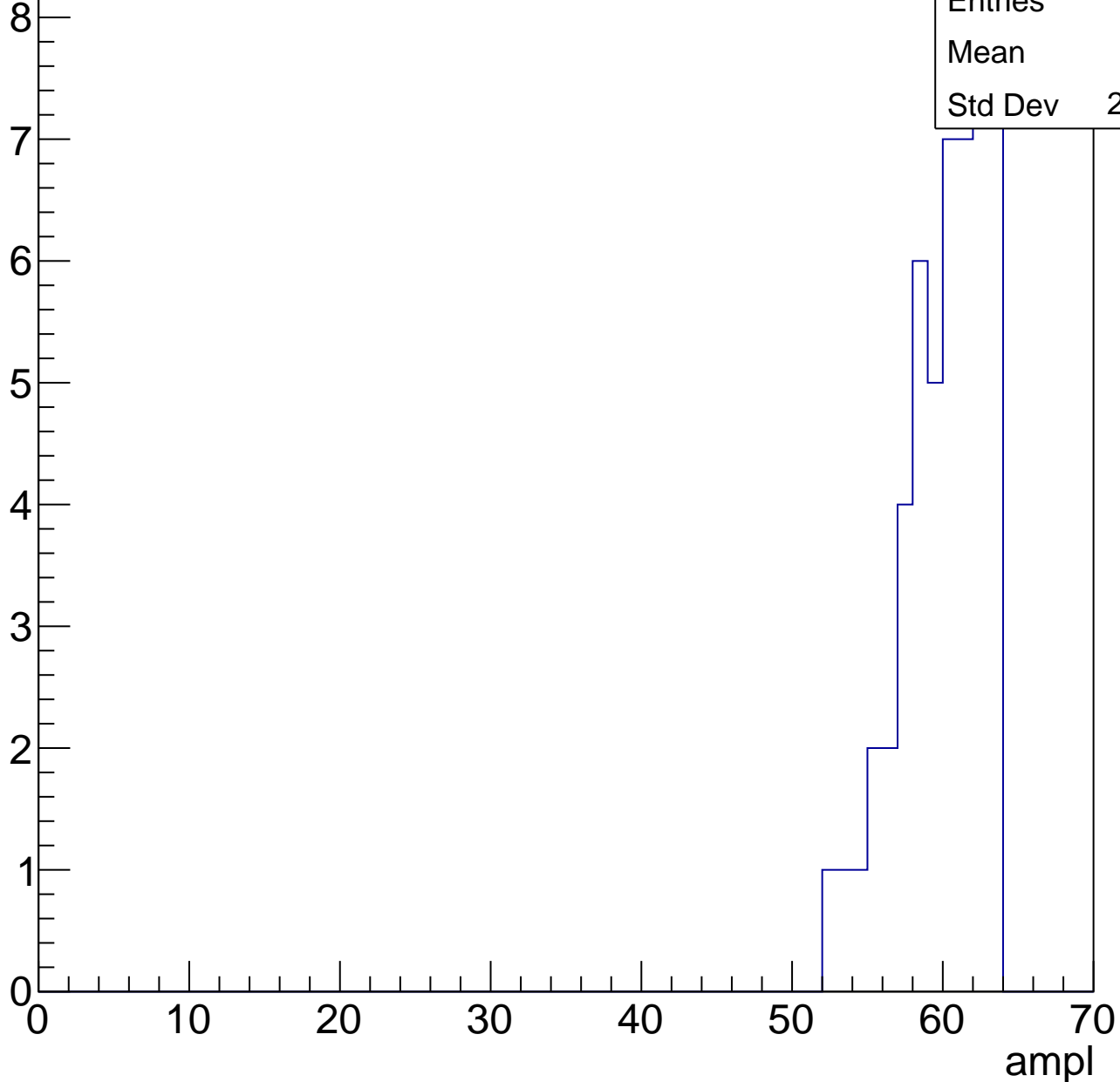
Entries	51
Mean	55.1
Std Dev	3.069



# B1L000S, U21-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

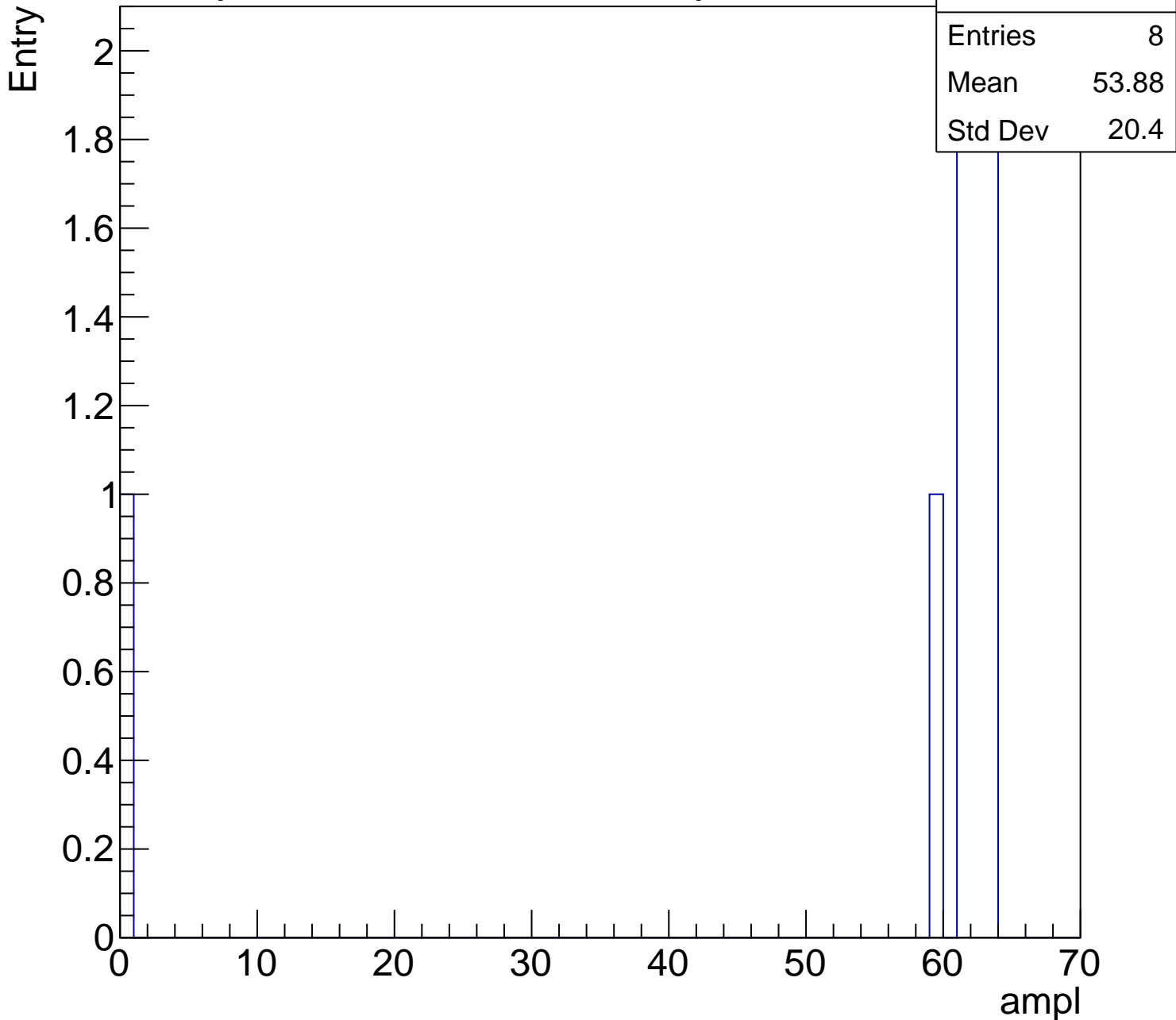
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.88
Std Dev	20.4

0 10 20 30 40 50 60 70

ampl





# B1L000S, U21-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch114, adc0

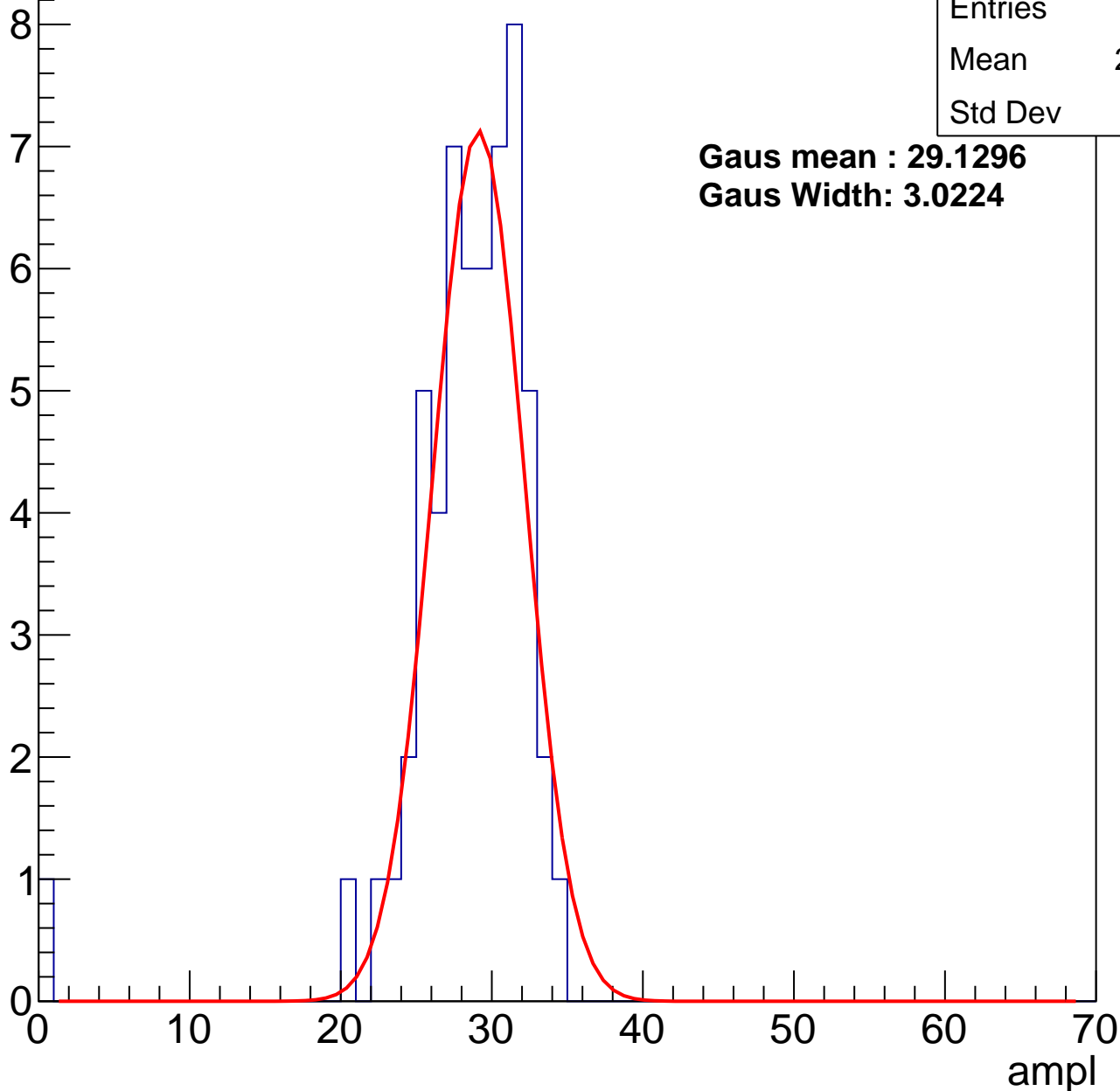
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	27.91
Std Dev	4.74

**Gaus mean : 29.1296**

**Gaus Width: 3.0224**



# B1L000S, U21-ch114, adc1

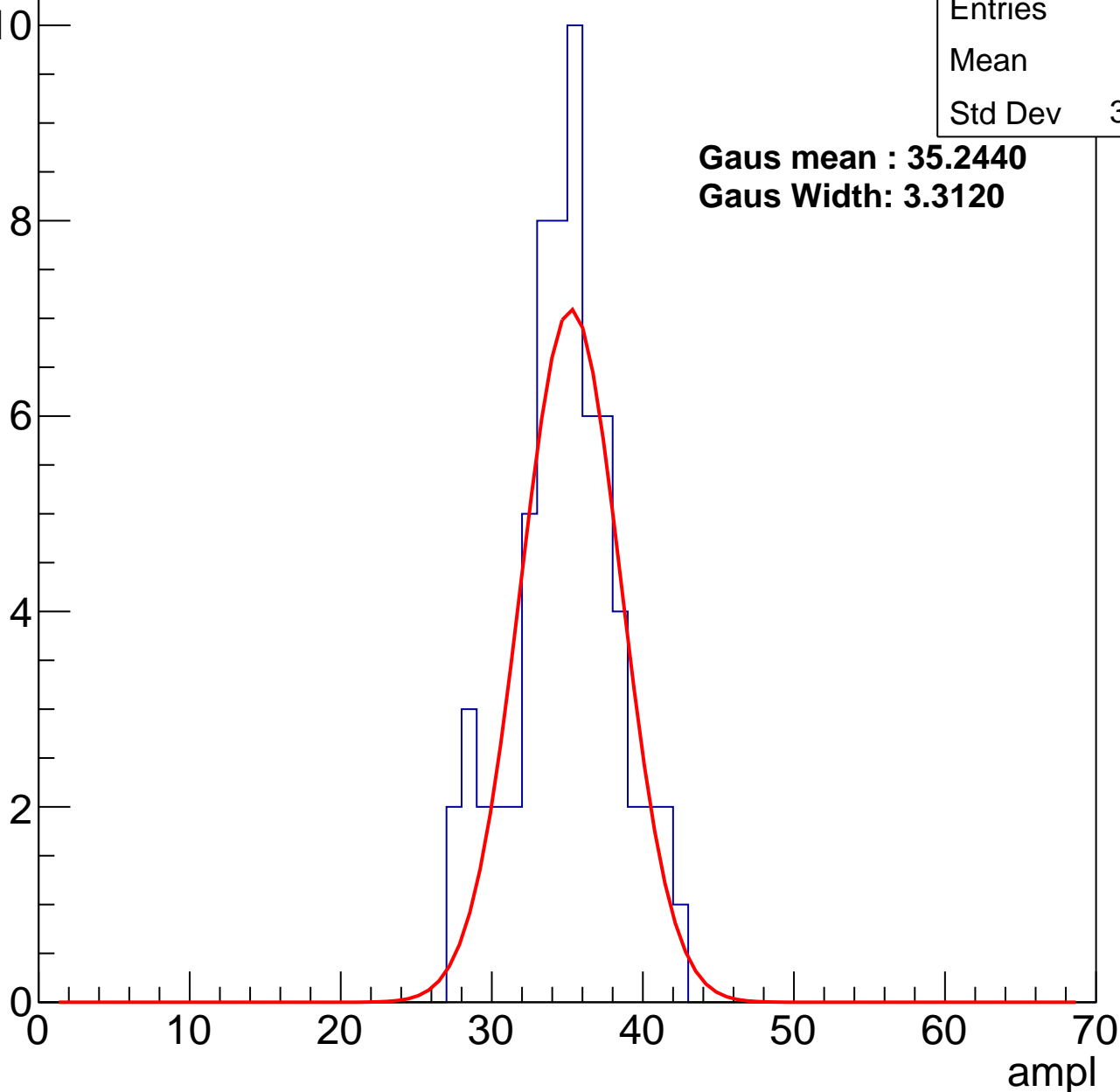
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	34.4
Std Dev	3.419

**Gaus mean : 35.2440**

**Gaus Width: 3.3120**



# B1L000S, U21-ch114, adc2

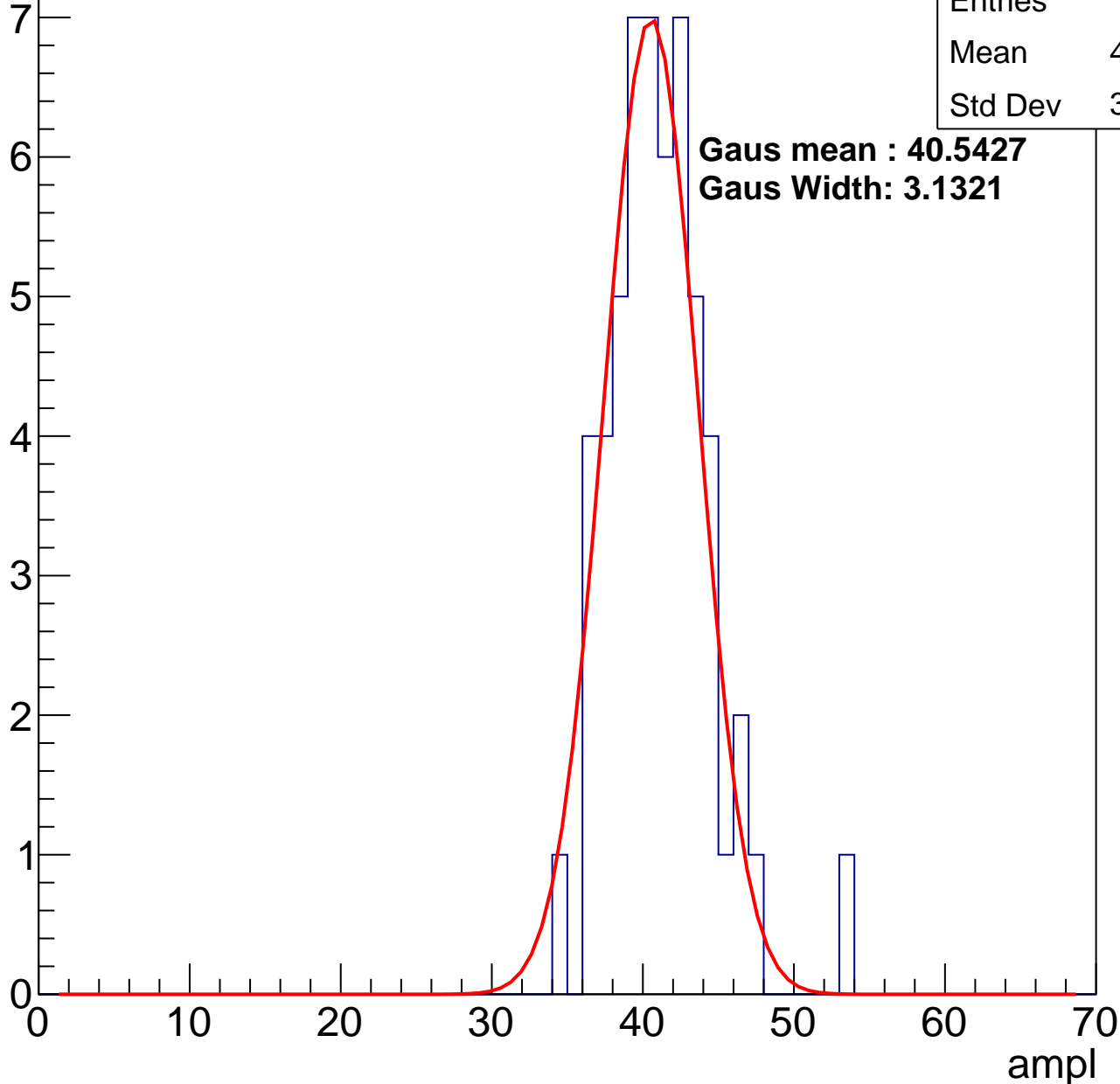
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	40.67
Std Dev	3.292

**Gaus mean : 40.5427**

**Gaus Width: 3.1321**

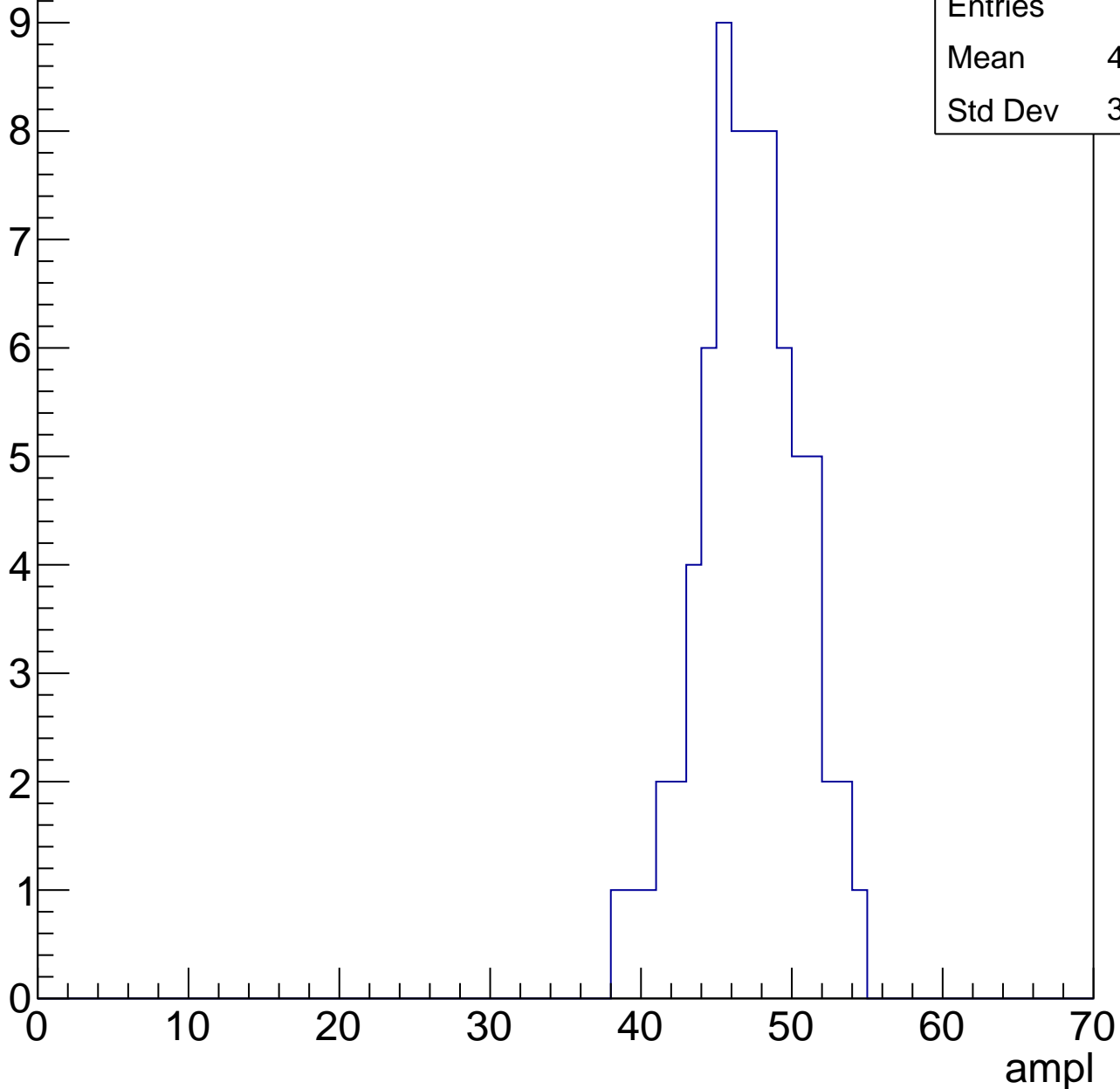


# B1L000S, U21-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	46.69
Std Dev	3.355

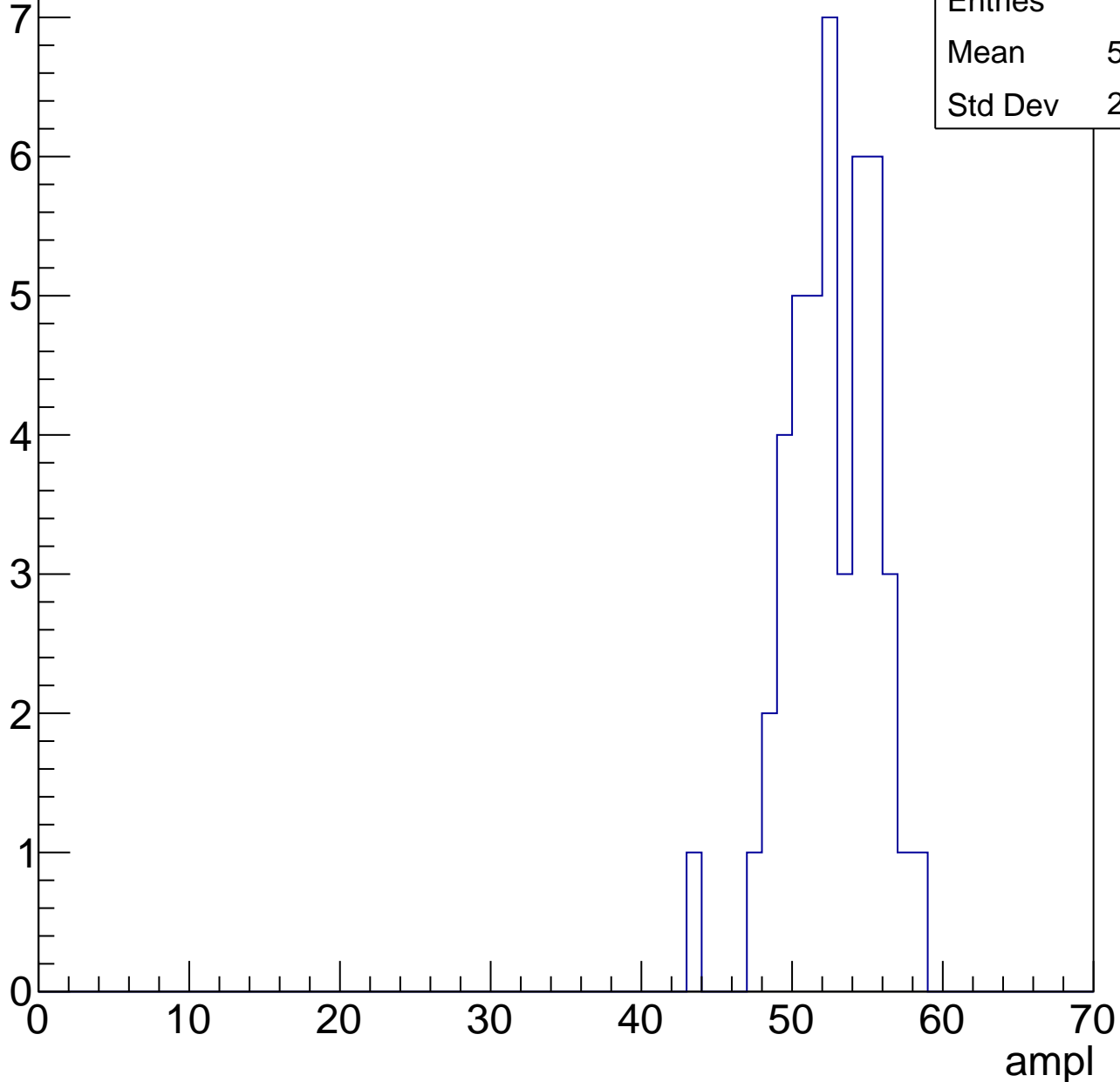


# B1L000S, U21-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

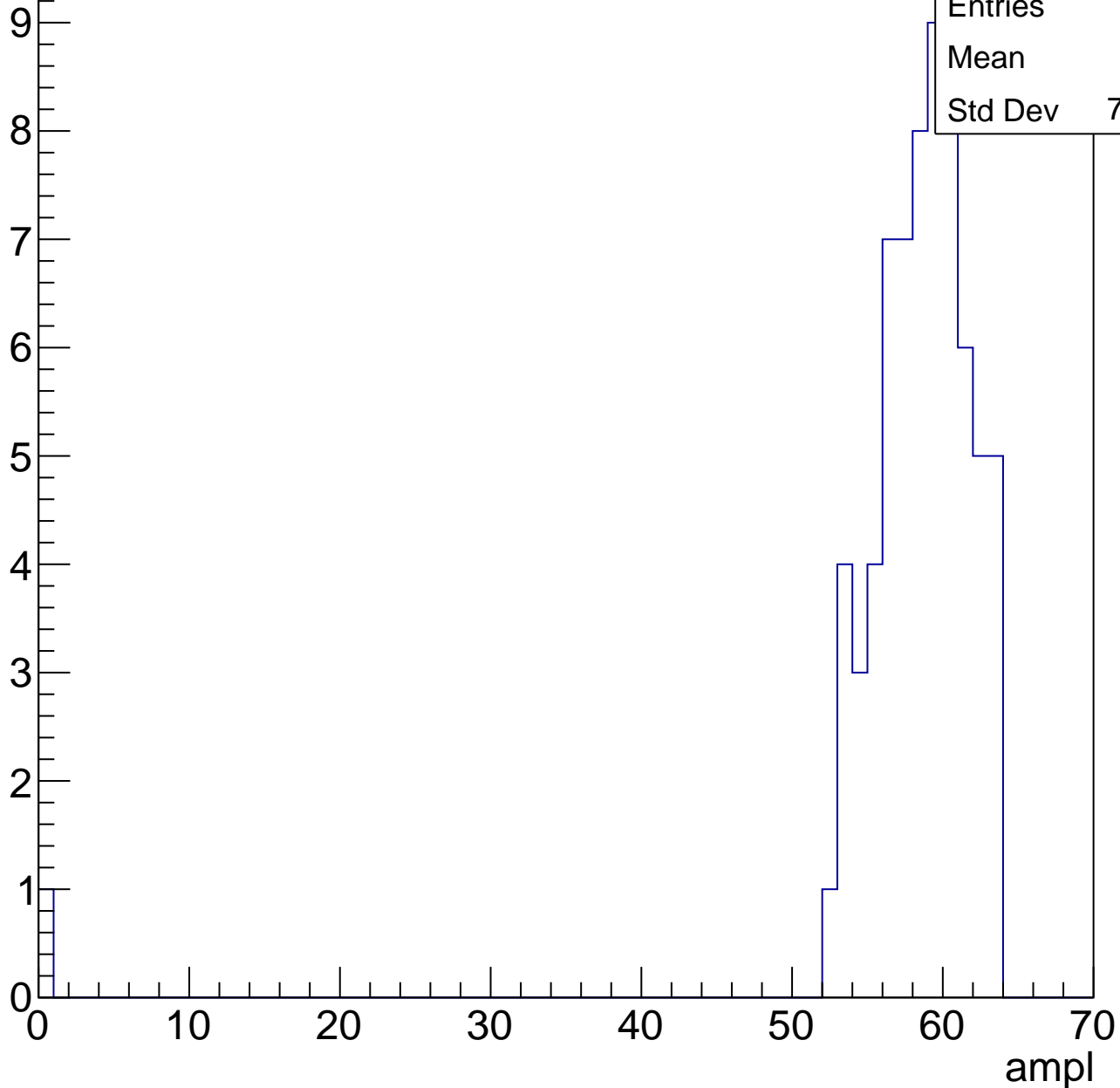
Entries	45
Mean	52.16
Std Dev	2.936



# B1L000S, U21-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

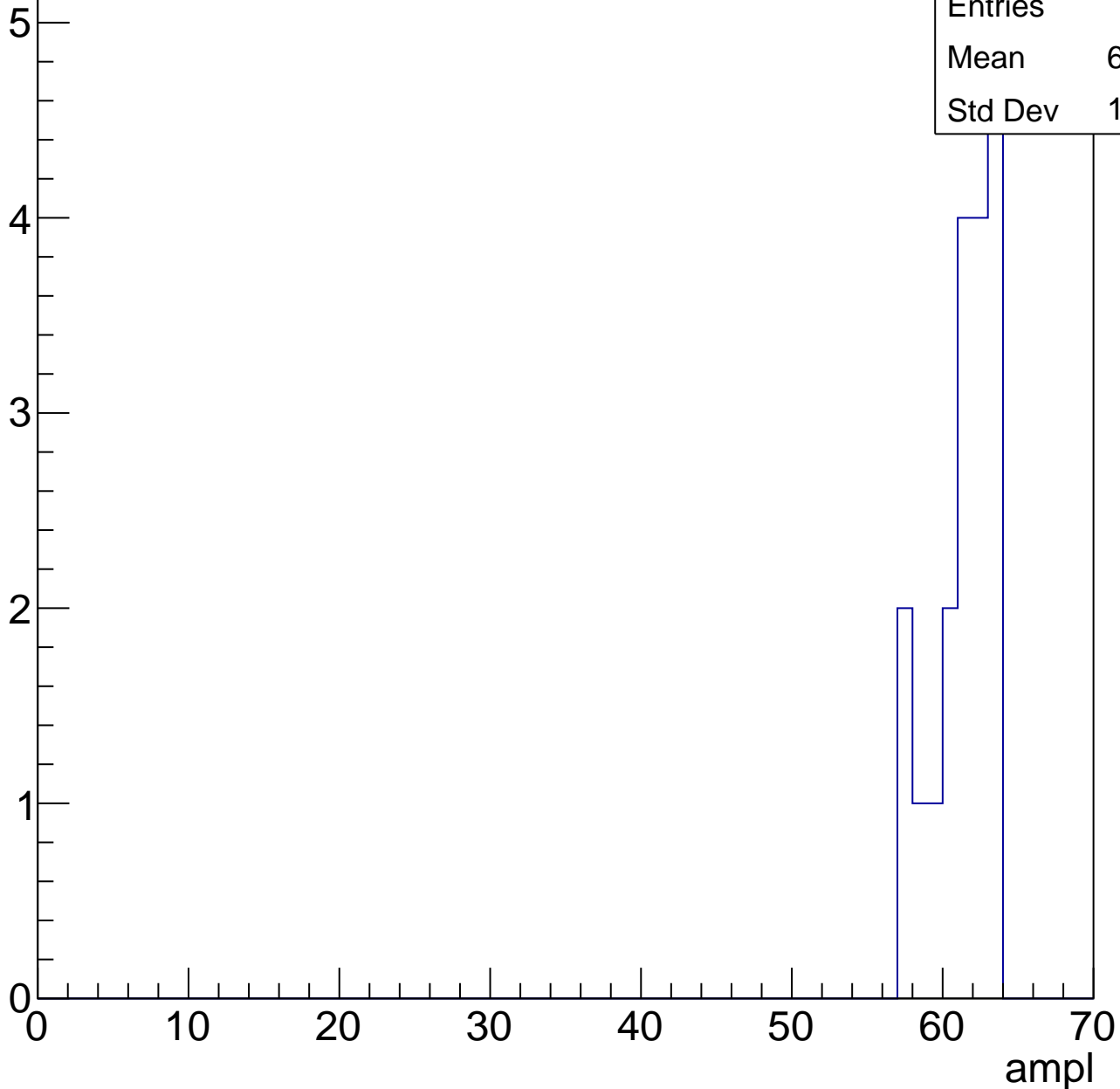


# B1L000S, U21-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	60.95
Std Dev	1.932

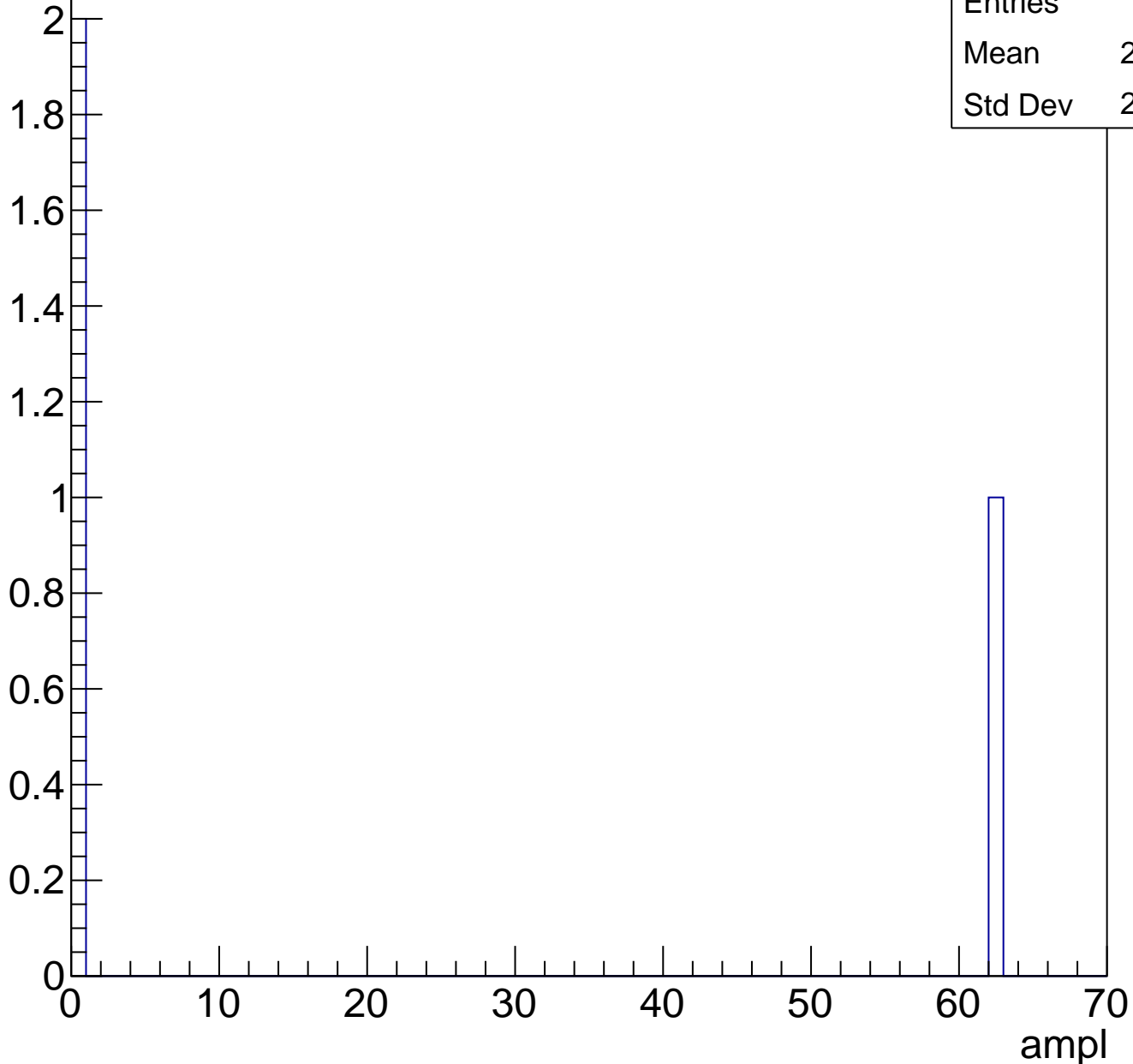




# B1L000S, U21-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B1L000S, U21-ch115, adc0

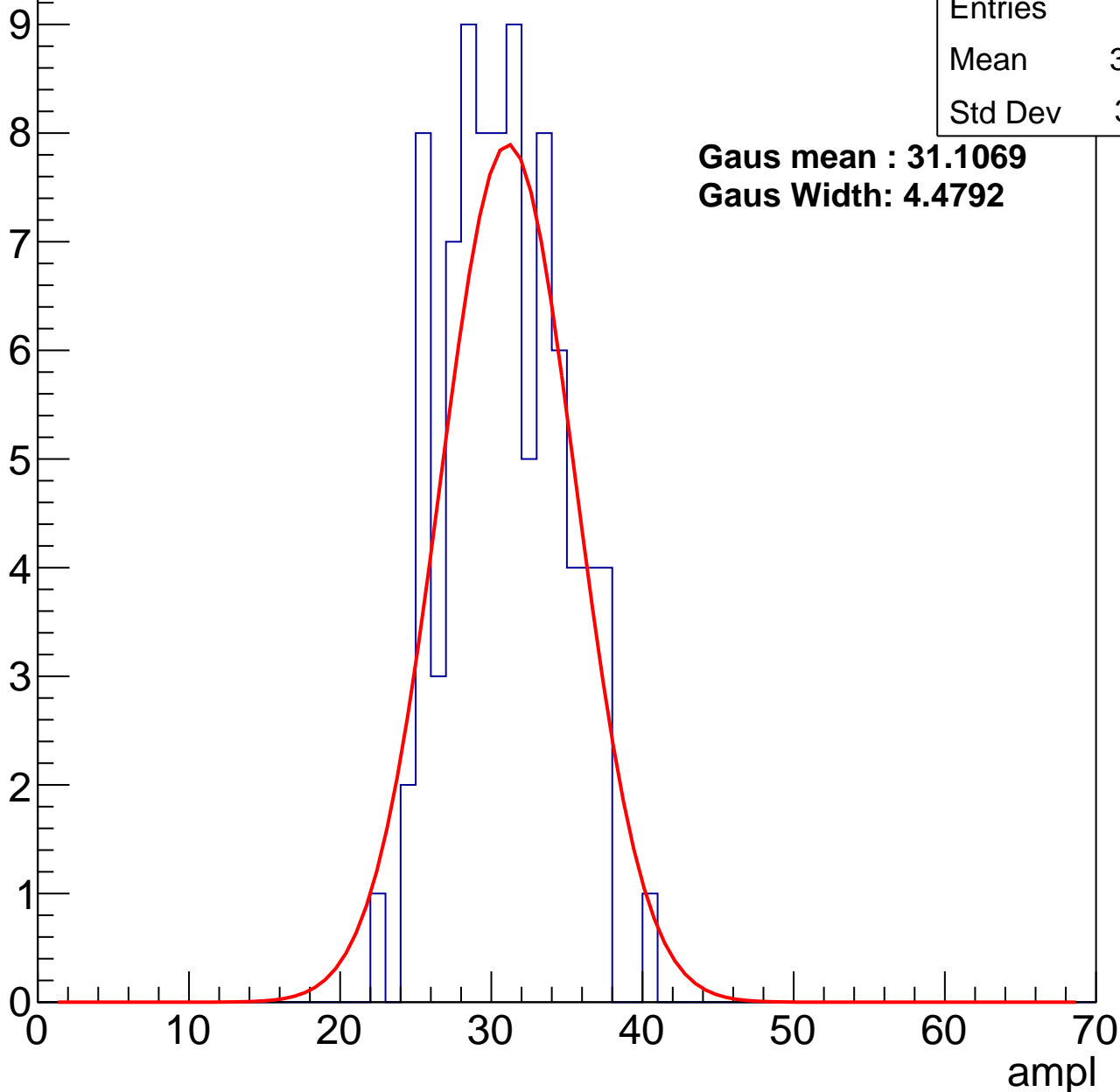
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	87
Mean	30.34
Std Dev	3.741

**Gaus mean : 31.1069**

**Gaus Width: 4.4792**



# B1L000S, U21-ch115, adc1

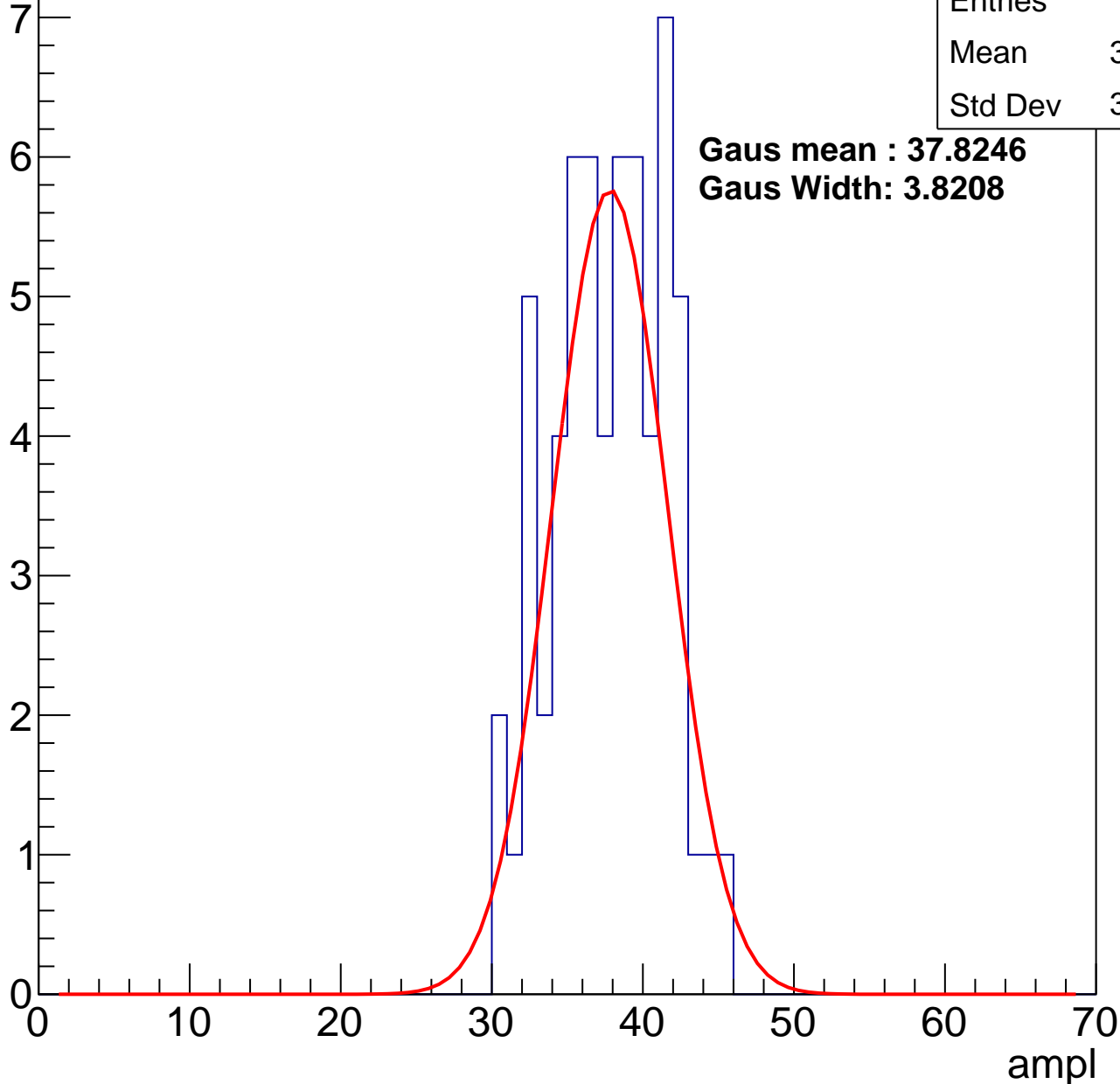
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	37.34
Std Dev	3.625

**Gaus mean : 37.8246**

**Gaus Width: 3.8208**



# B1L000S, U21-ch115, adc2

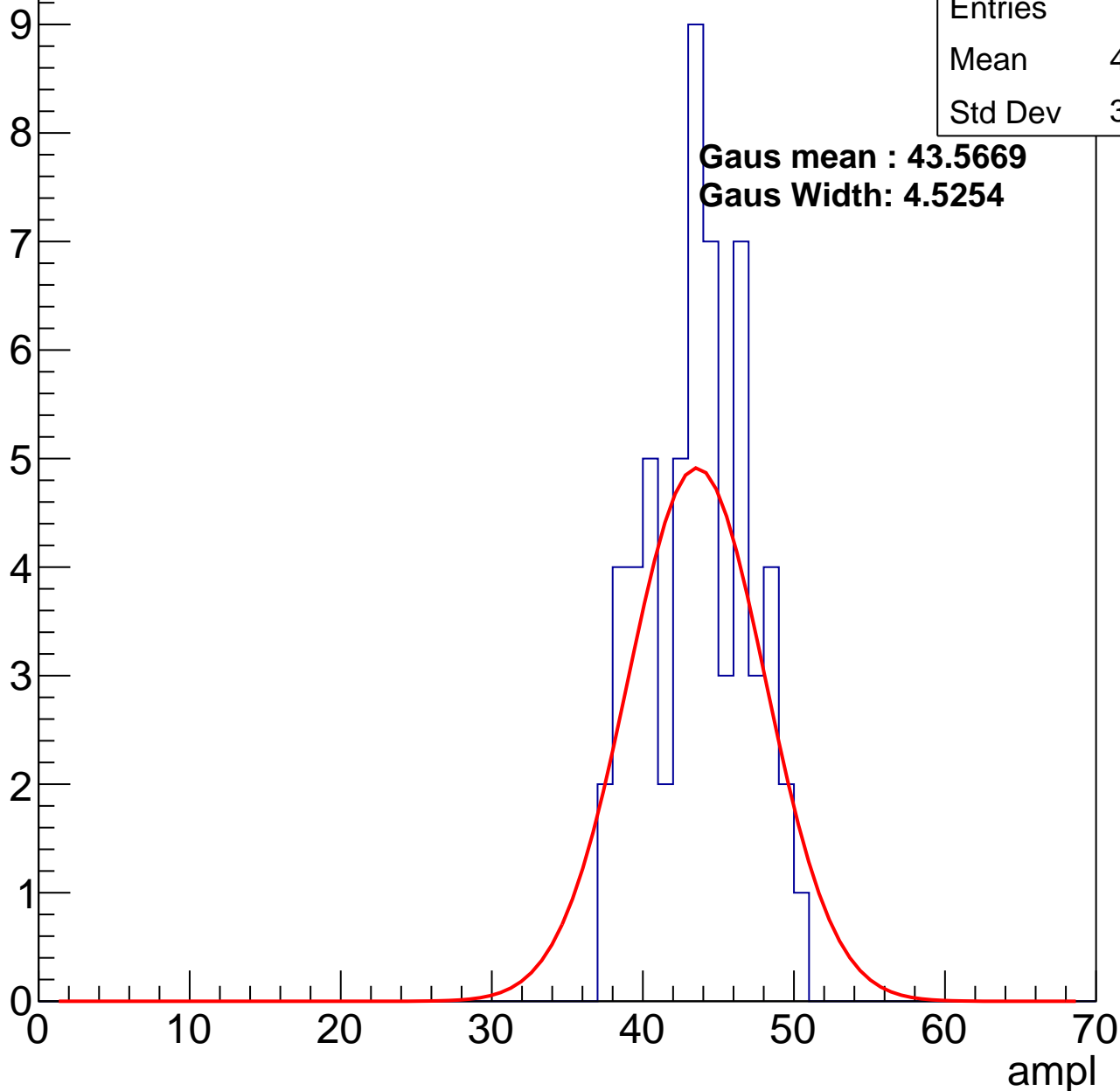
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	43.22
Std Dev	3.327

**Gaus mean : 43.5669**

**Gaus Width: 4.5254**

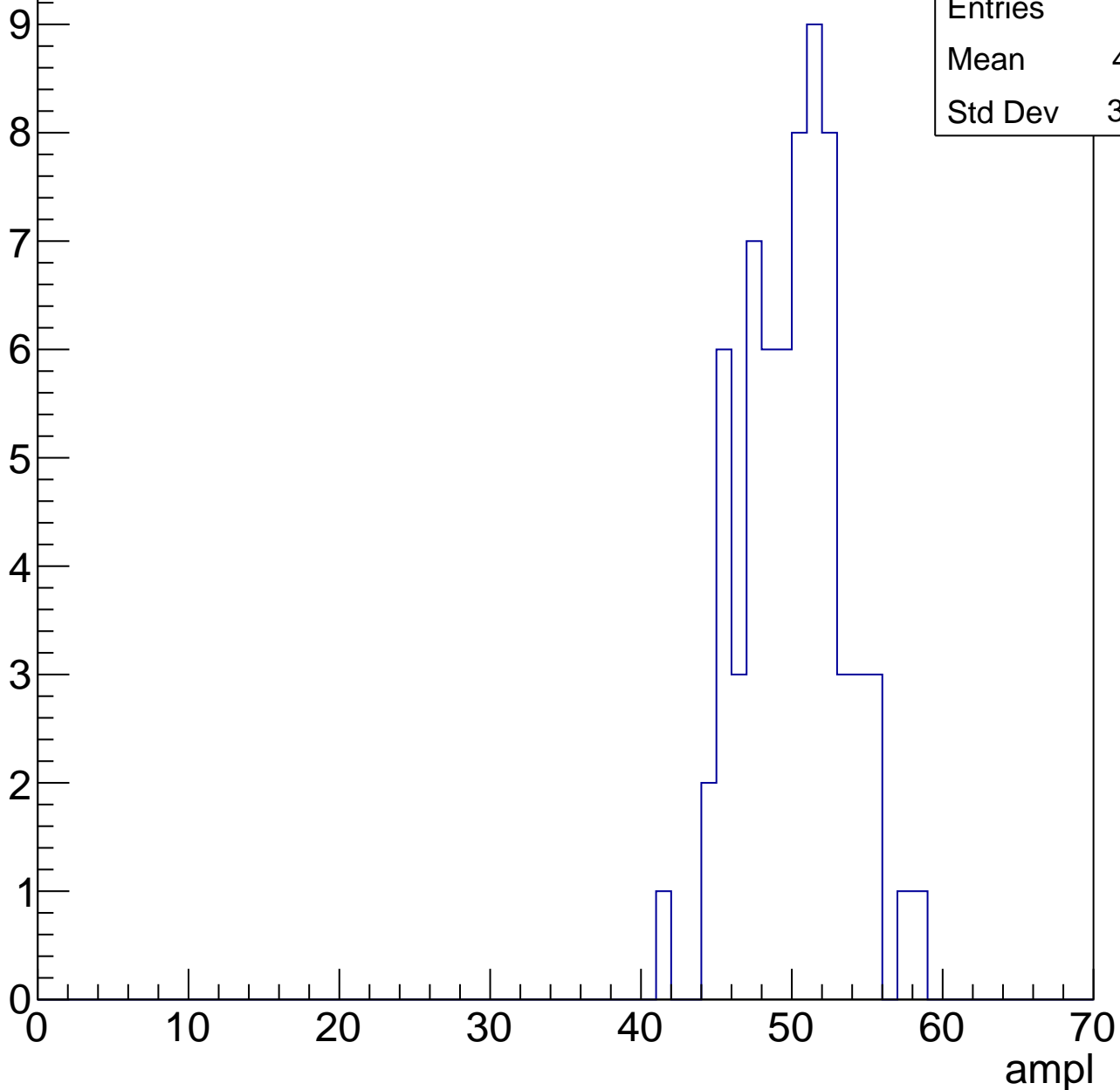


# B1L000S, U21-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	49.61
Std Dev	3.319

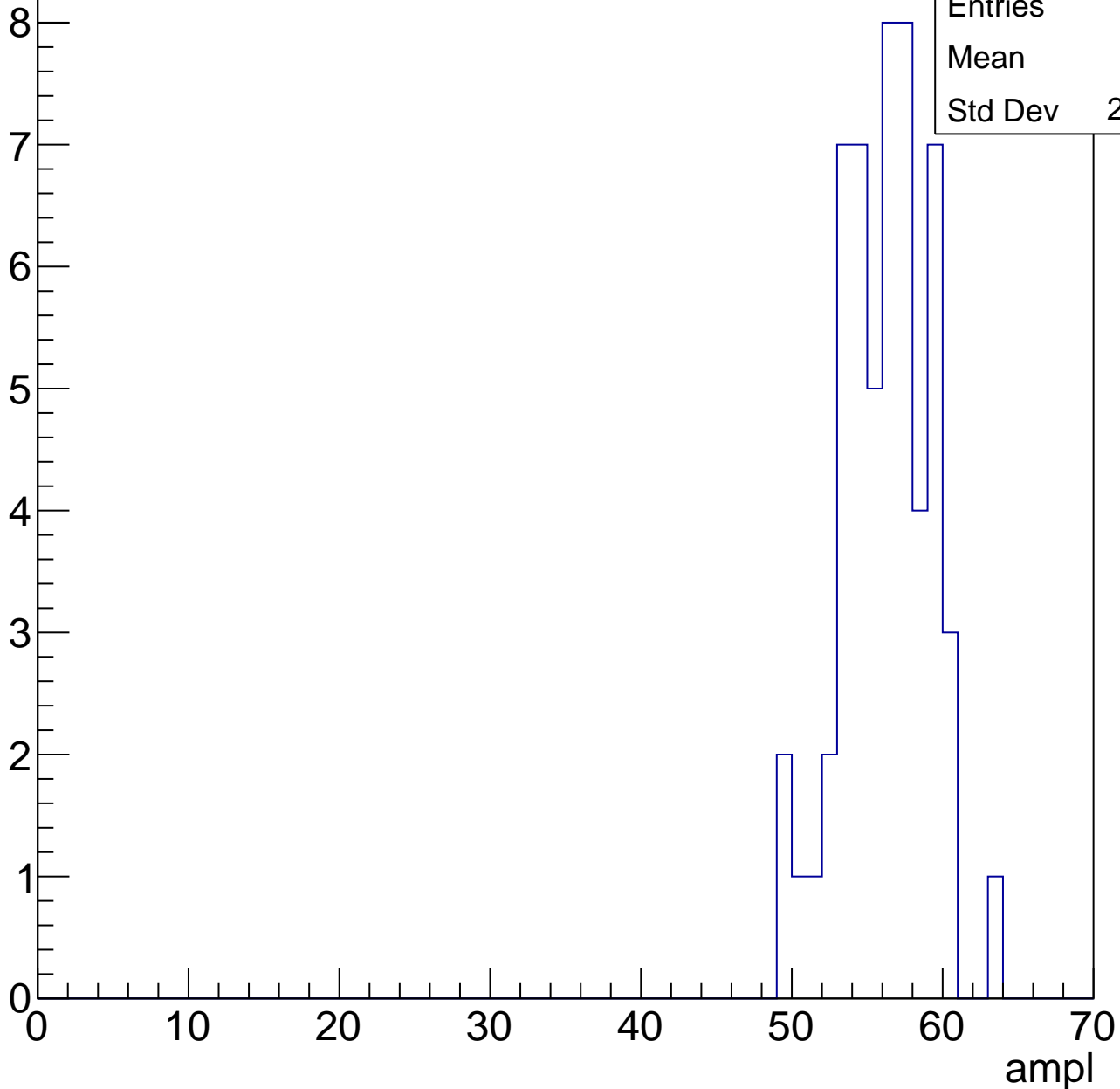


# B1L000S, U21-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	55.7
Std Dev	2.884

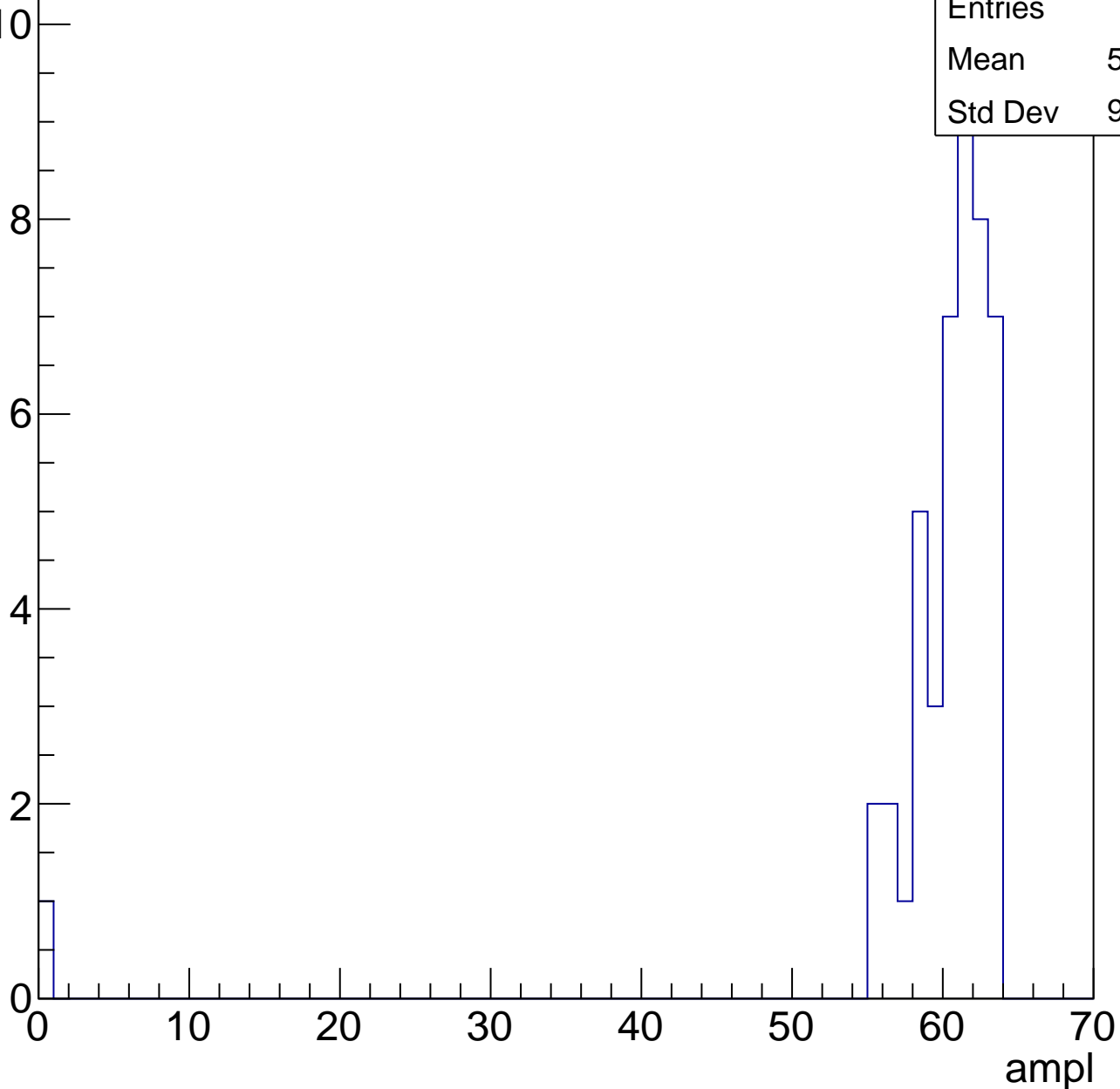


# B1L000S, U21-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	58.98
Std Dev	9.054



# B1L000S, U21-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U21-ch116, adc0

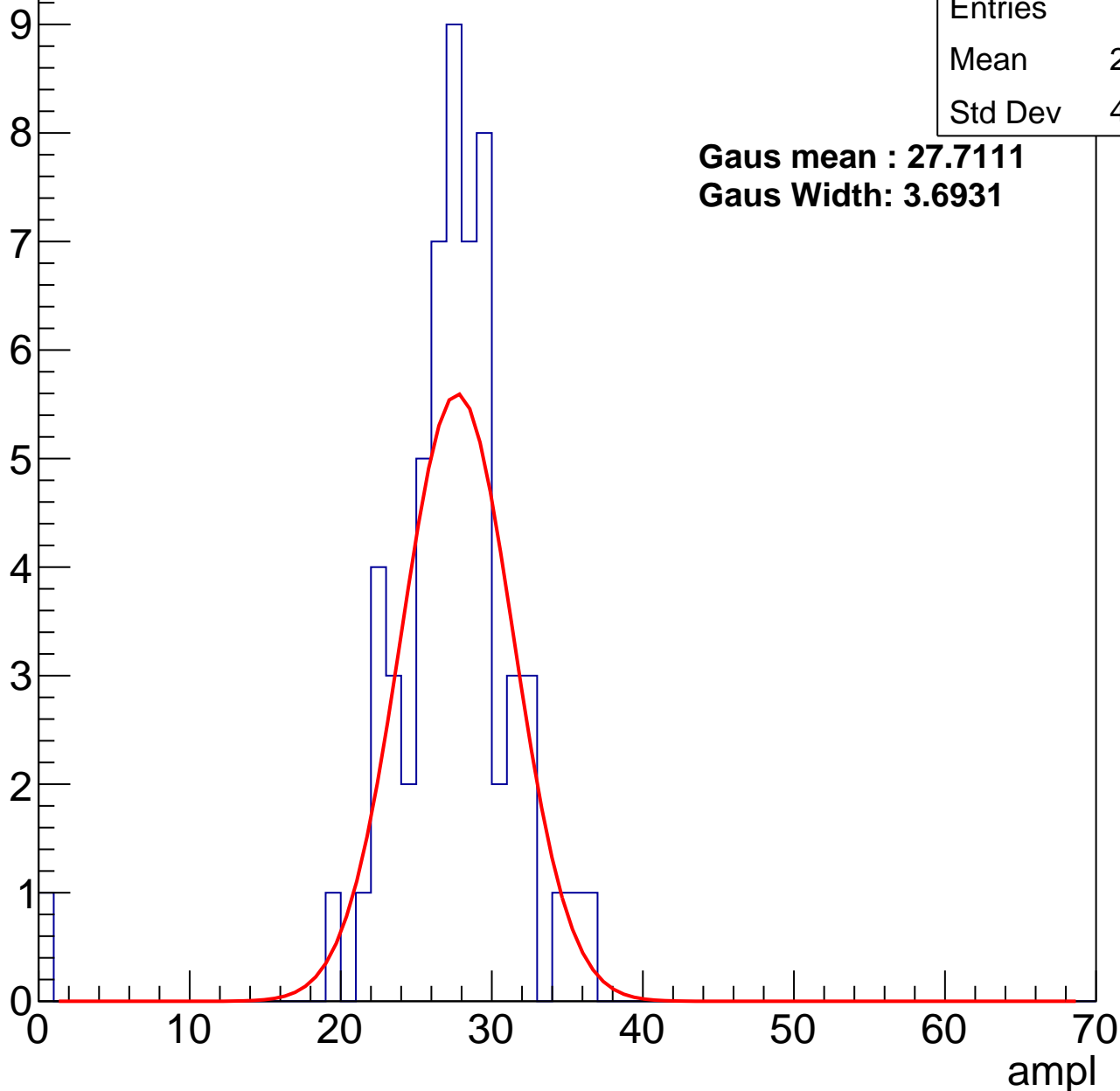
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	26.73
Std Dev	4.857

**Gaus mean : 27.7111**

**Gaus Width: 3.6931**



# B1L000S, U21-ch116, adc1

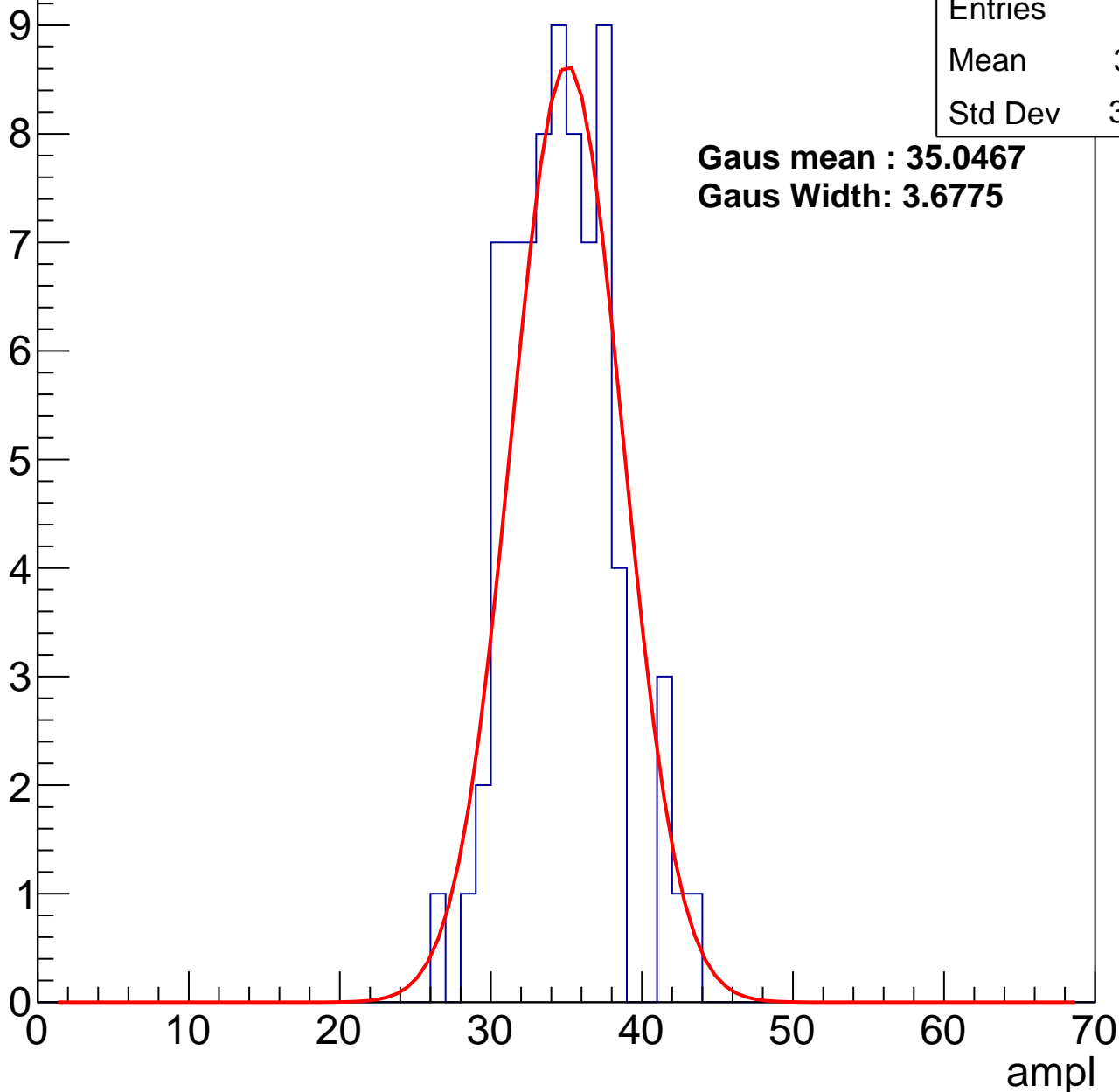
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	34.11
Std Dev	3.333

**Gaus mean : 35.0467**

**Gaus Width: 3.6775**



# B1L000S, U21-ch116, adc2

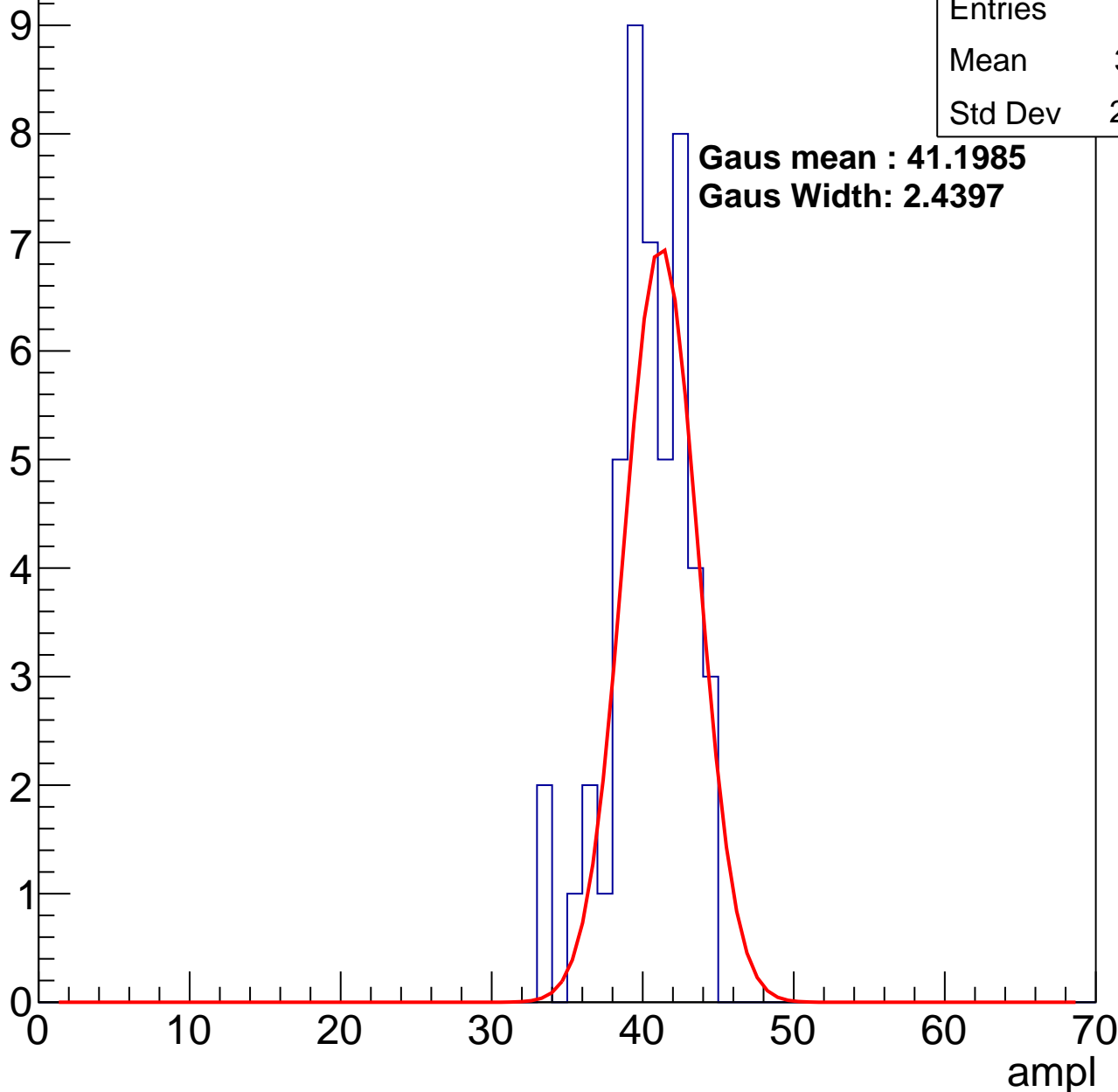
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	39.91
Std Dev	2.583

**Gaus mean : 41.1985**

**Gaus Width: 2.4397**

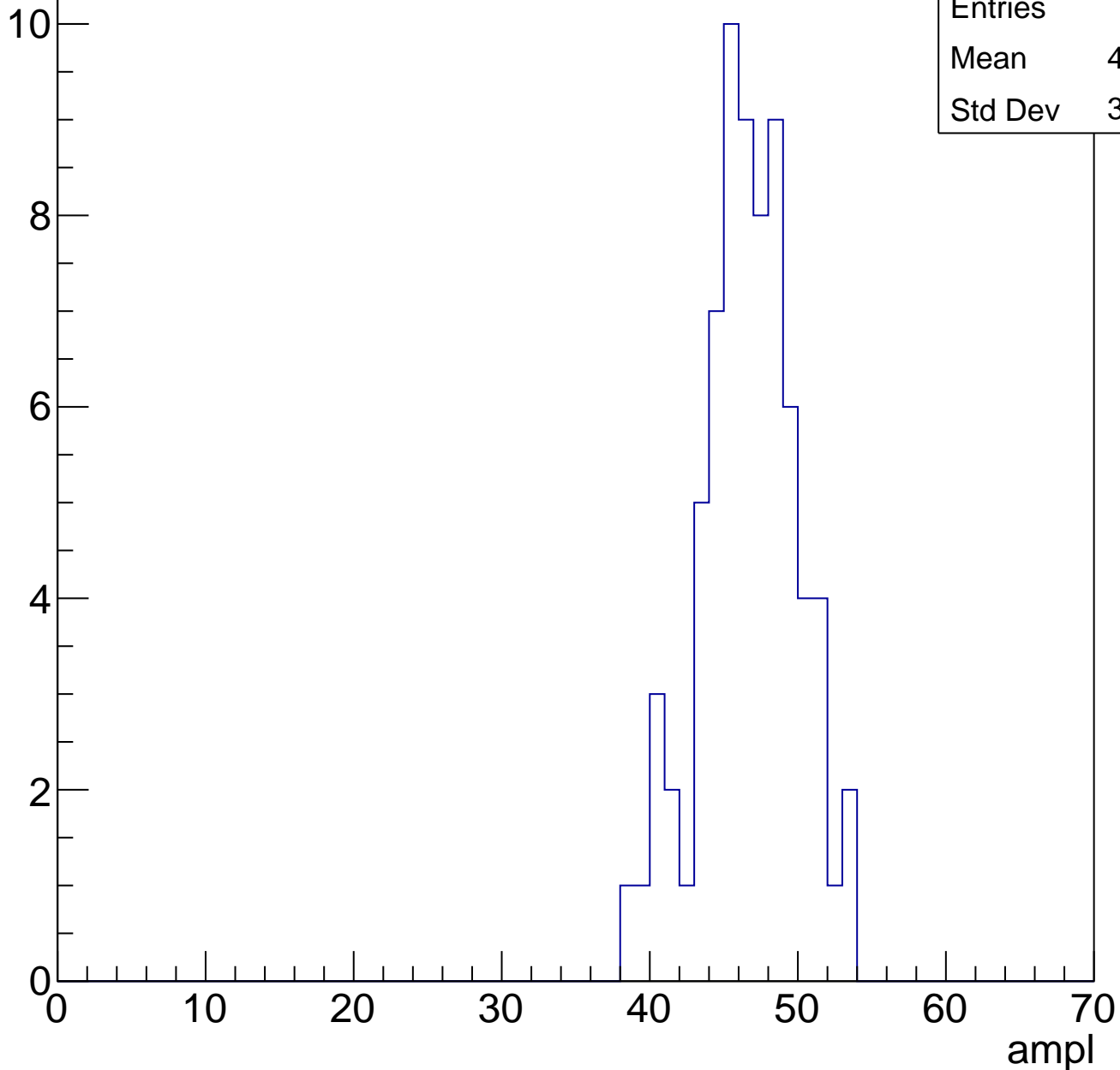


# B1L000S, U21-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	46.19
Std Dev	3.234

Entry

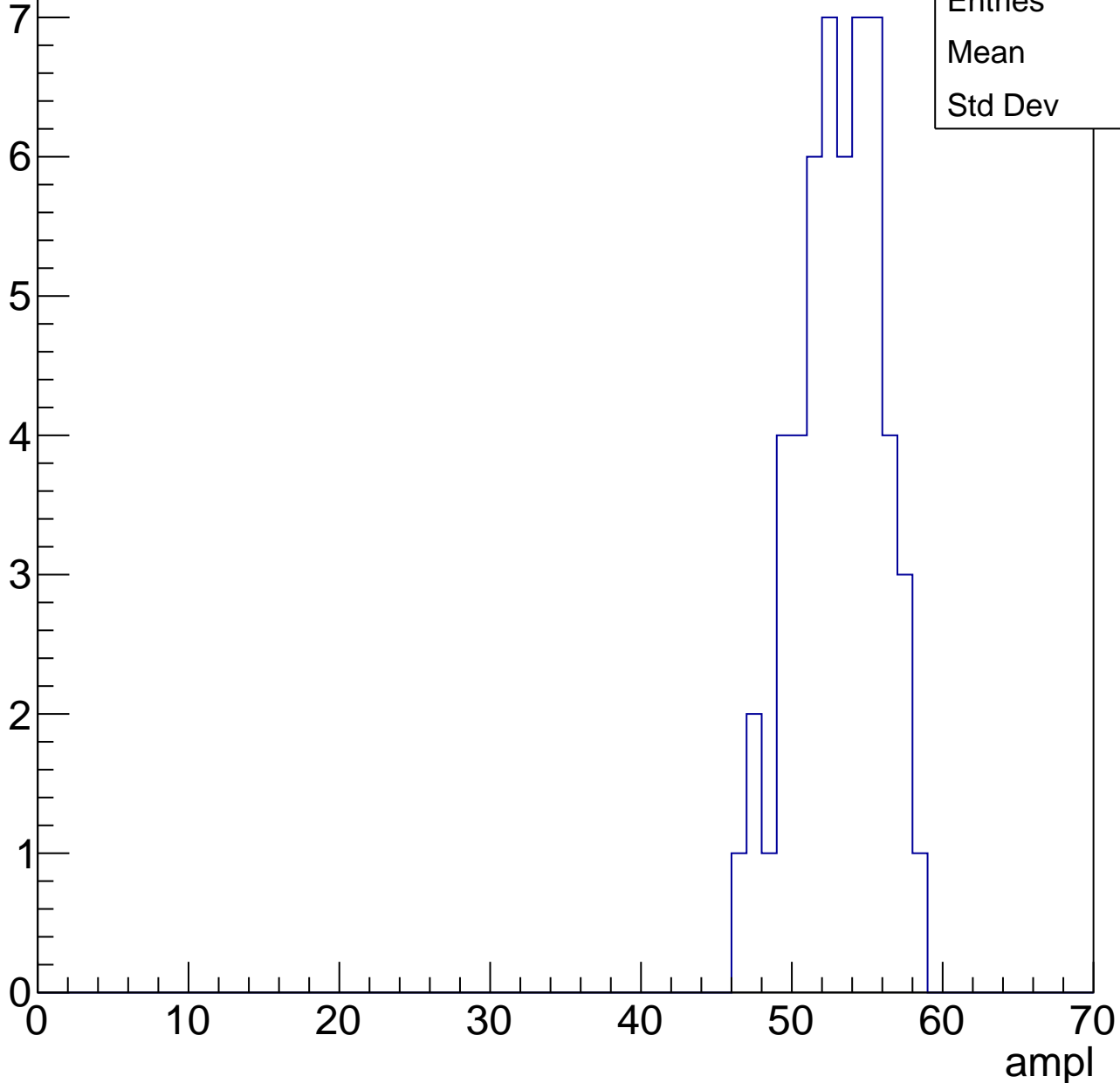


# B1L000S, U21-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	52.6
Std Dev	2.79

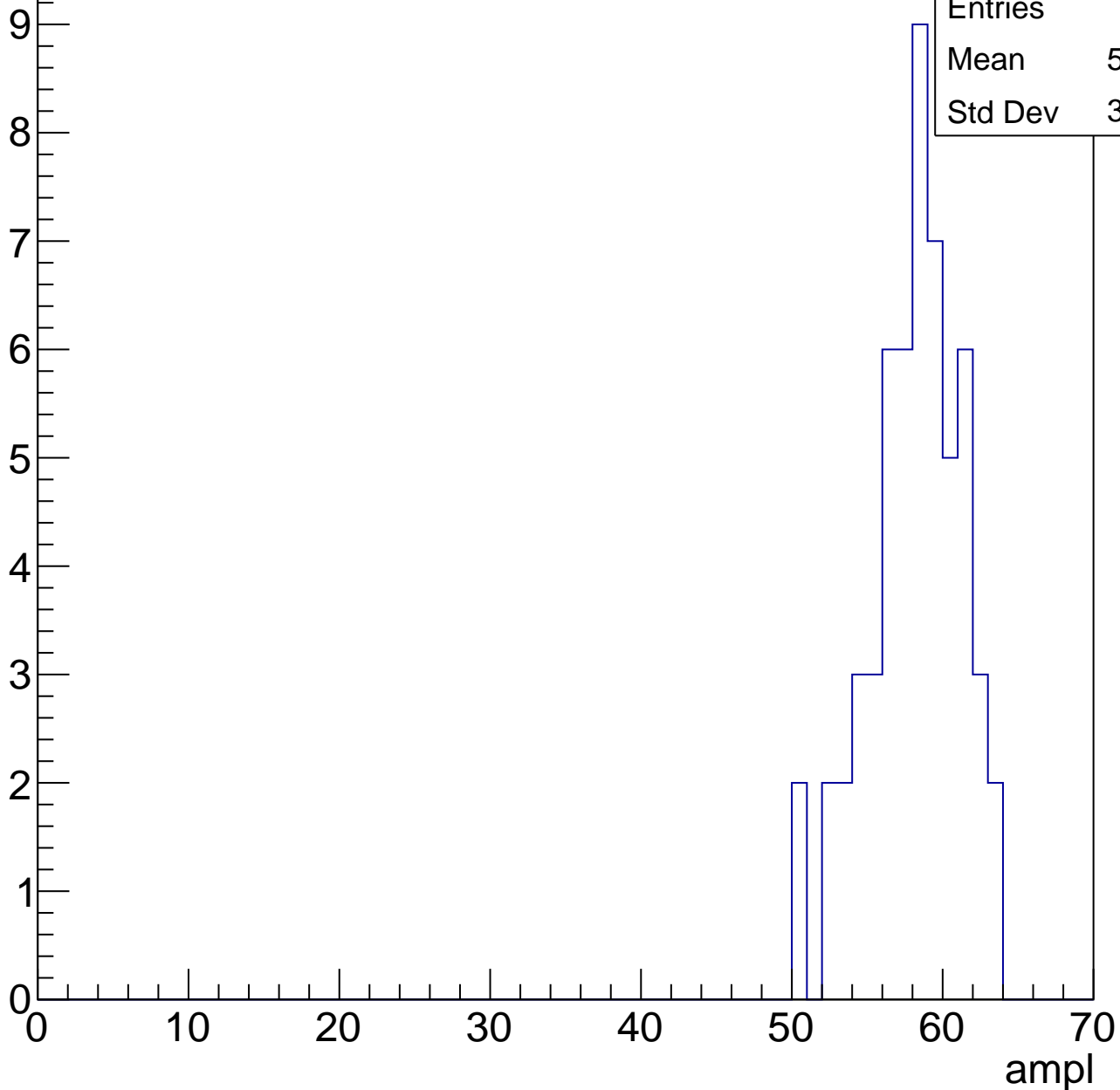


# B1L000S, U21-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	57.64
Std Dev	3.067

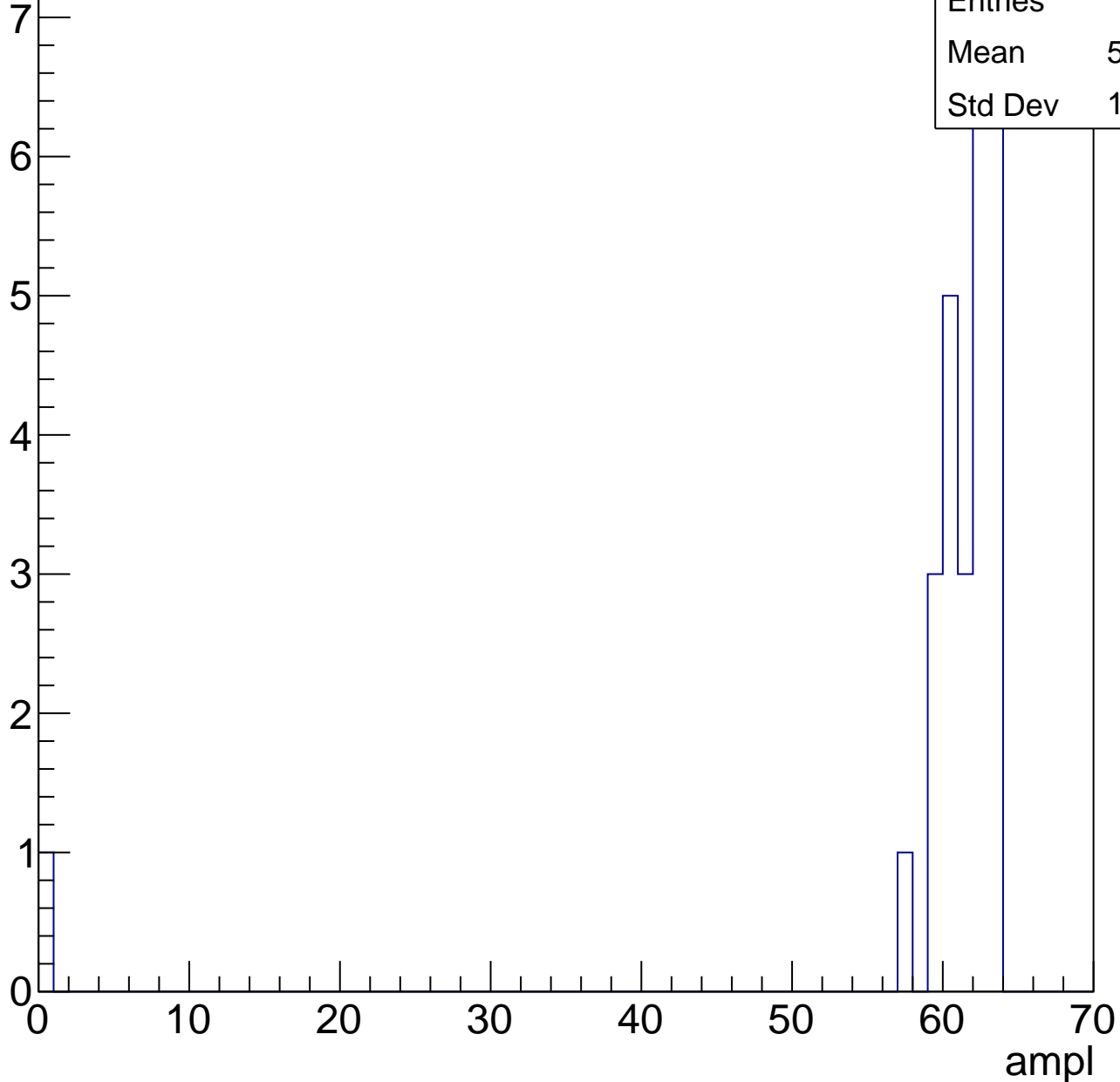


# B1L000S, U21-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	58.96
Std Dev	11.67





# B1L000S, U21-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch117, adc0

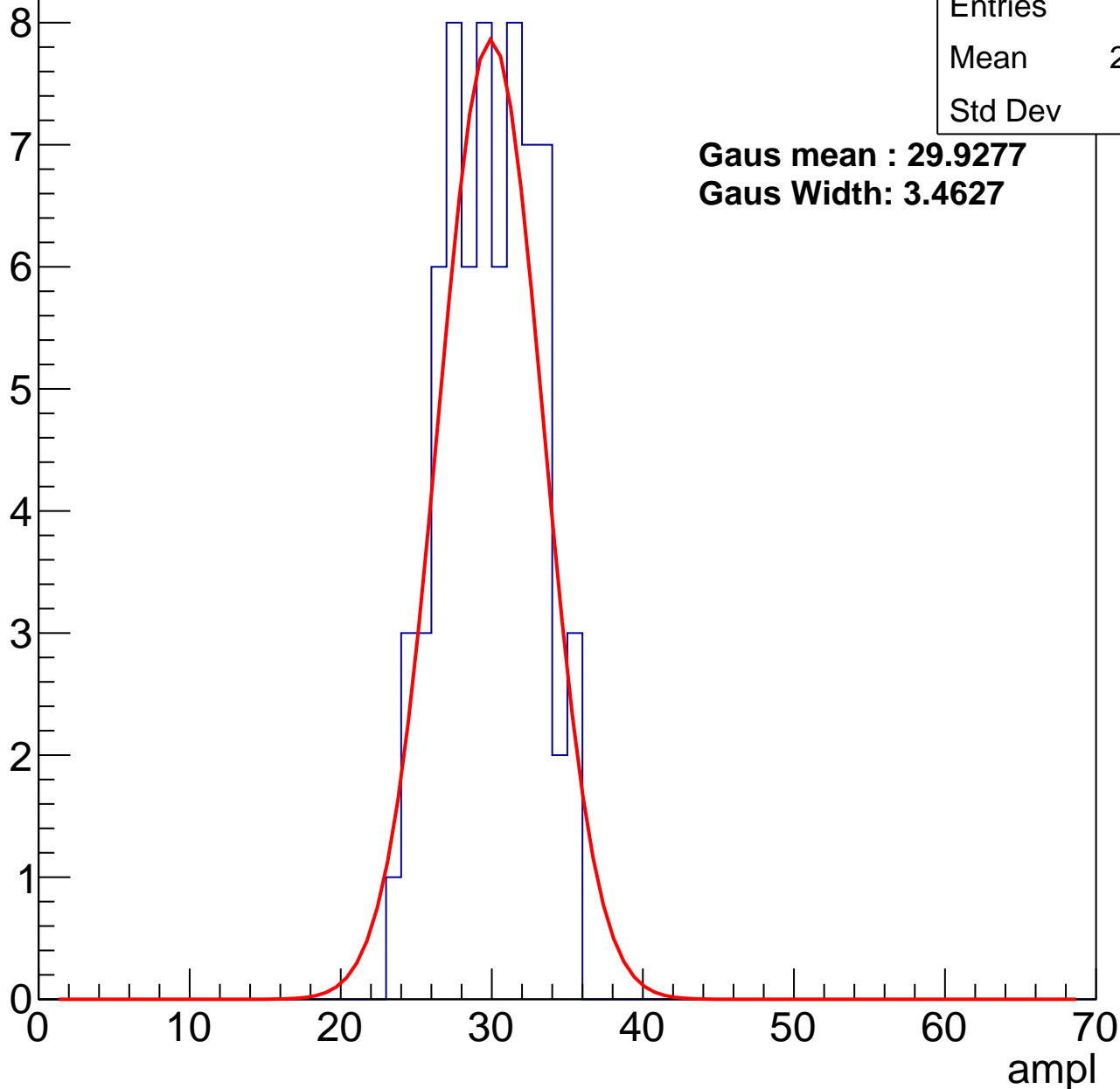
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	29.38
Std Dev	3

**Gaus mean : 29.9277**

**Gaus Width: 3.4627**



# B1L000S, U21-ch117, adc1

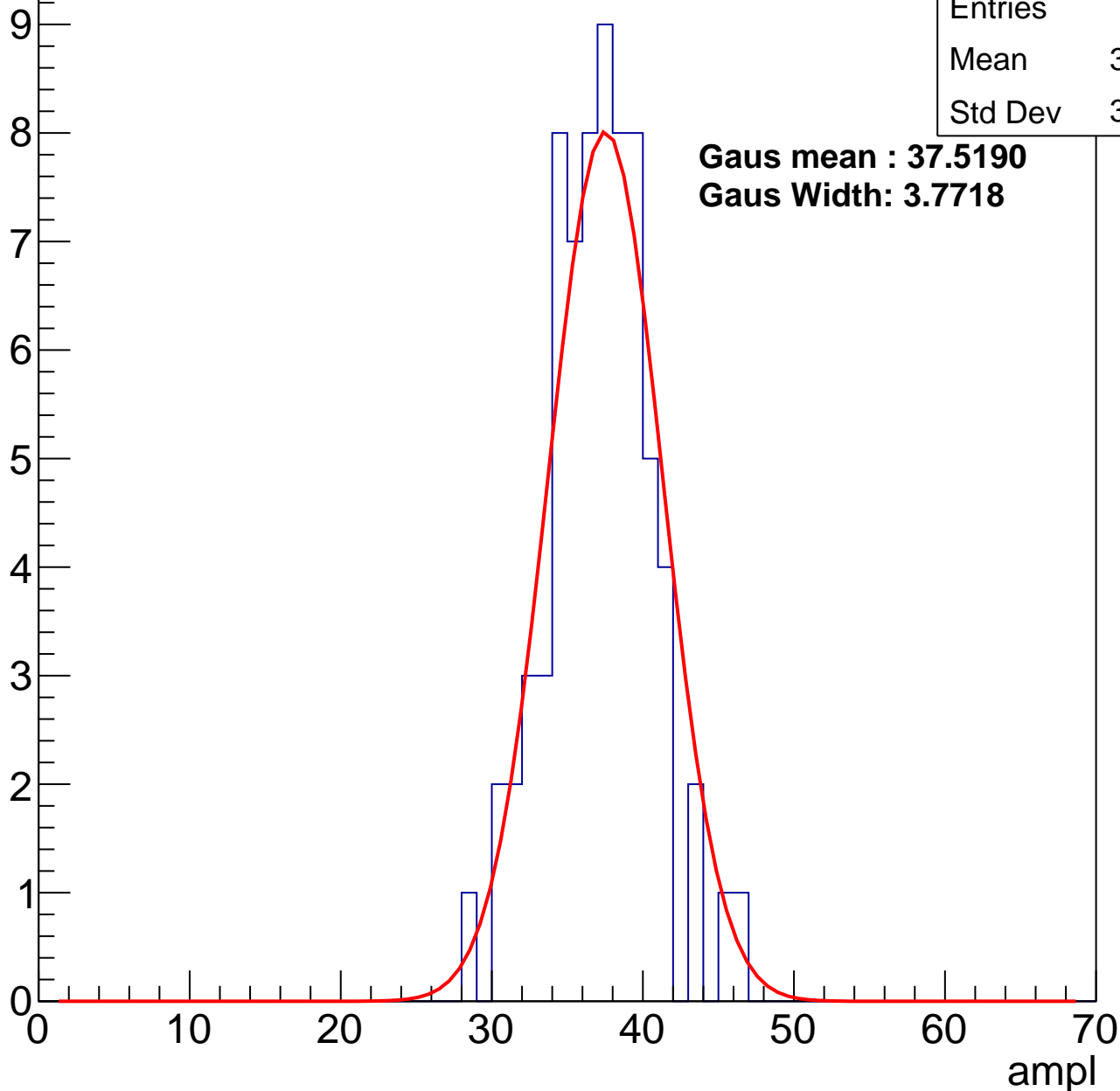
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	36.67
Std Dev	3.416

**Gaus mean : 37.5190**

**Gaus Width: 3.7718**



# B1L000S, U21-ch117, adc2

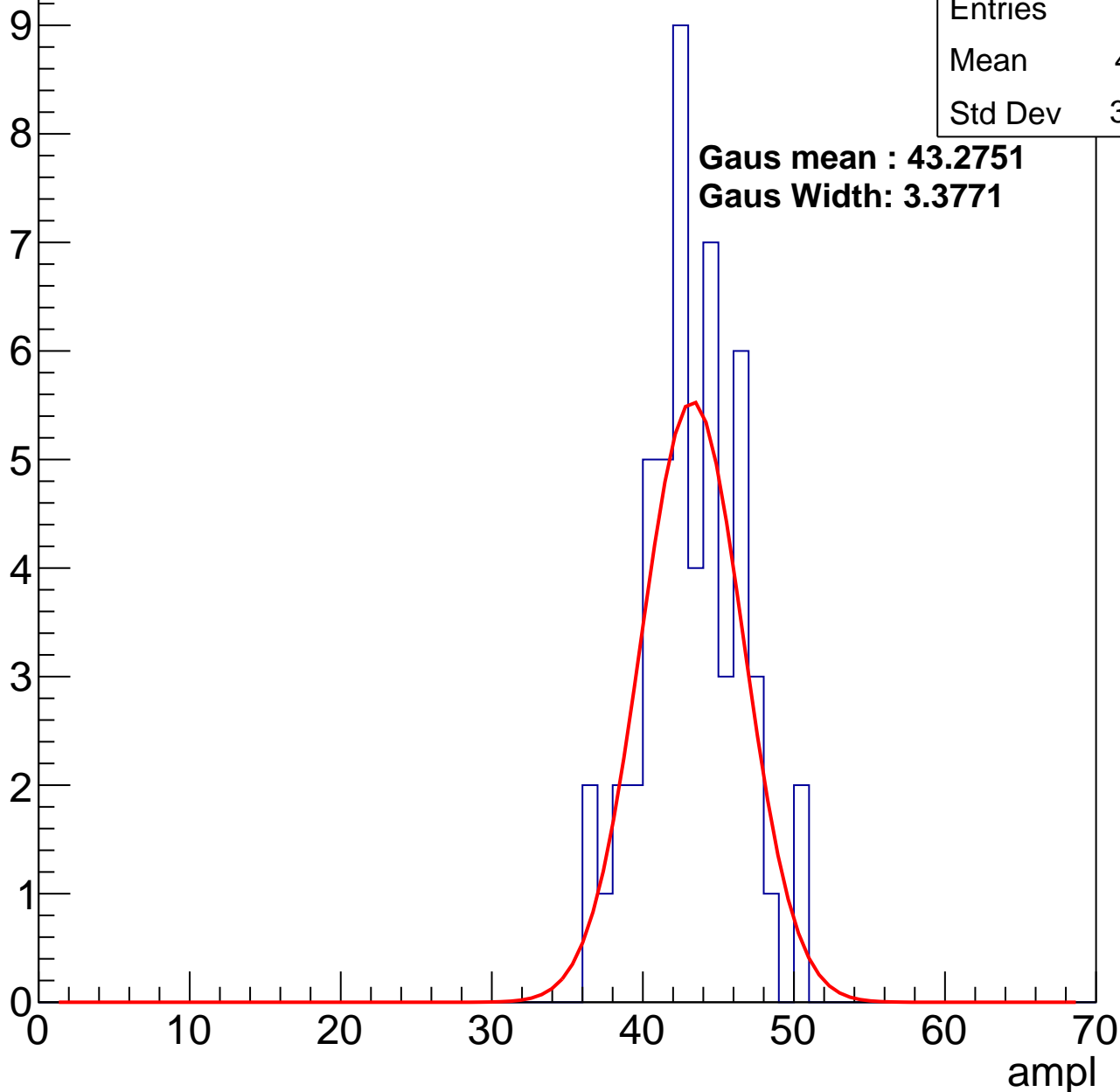
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	42.81
Std Dev	3.199

**Gaus mean : 43.2751**

**Gaus Width: 3.3771**

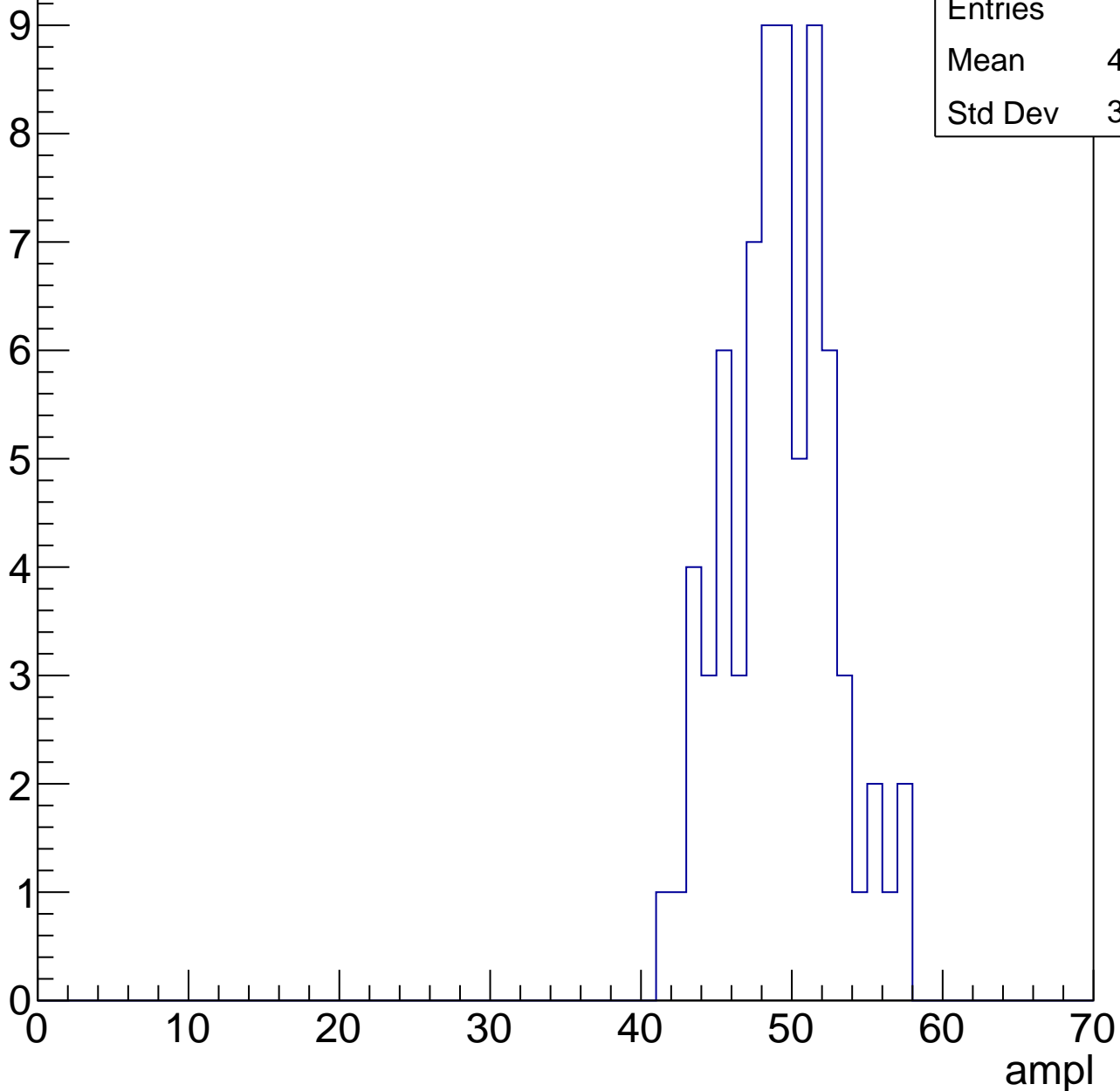


# B1L000S, U21-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	48.76
Std Dev	3.545

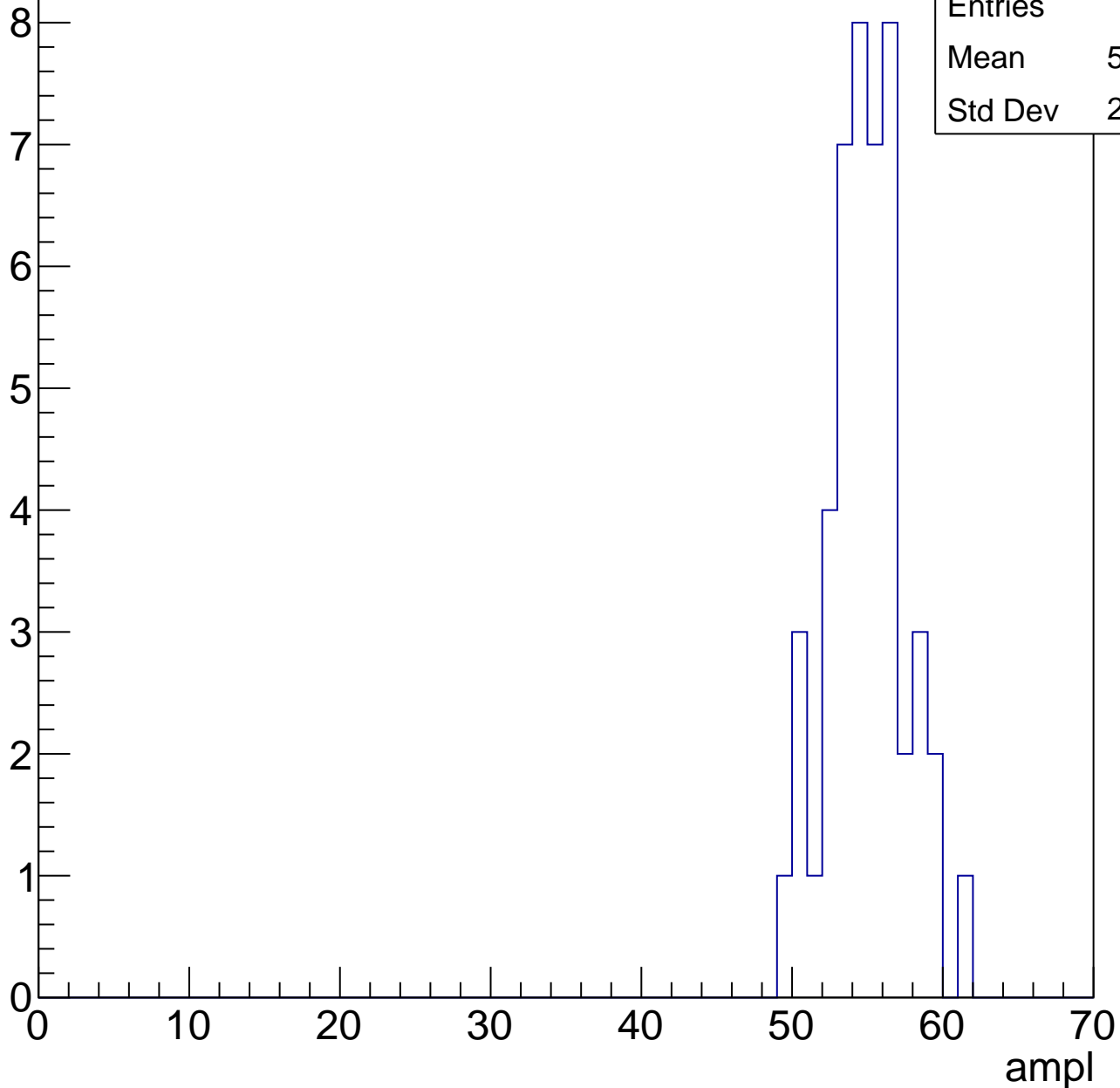


# B1L000S, U21-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	54.49
Std Dev	2.517

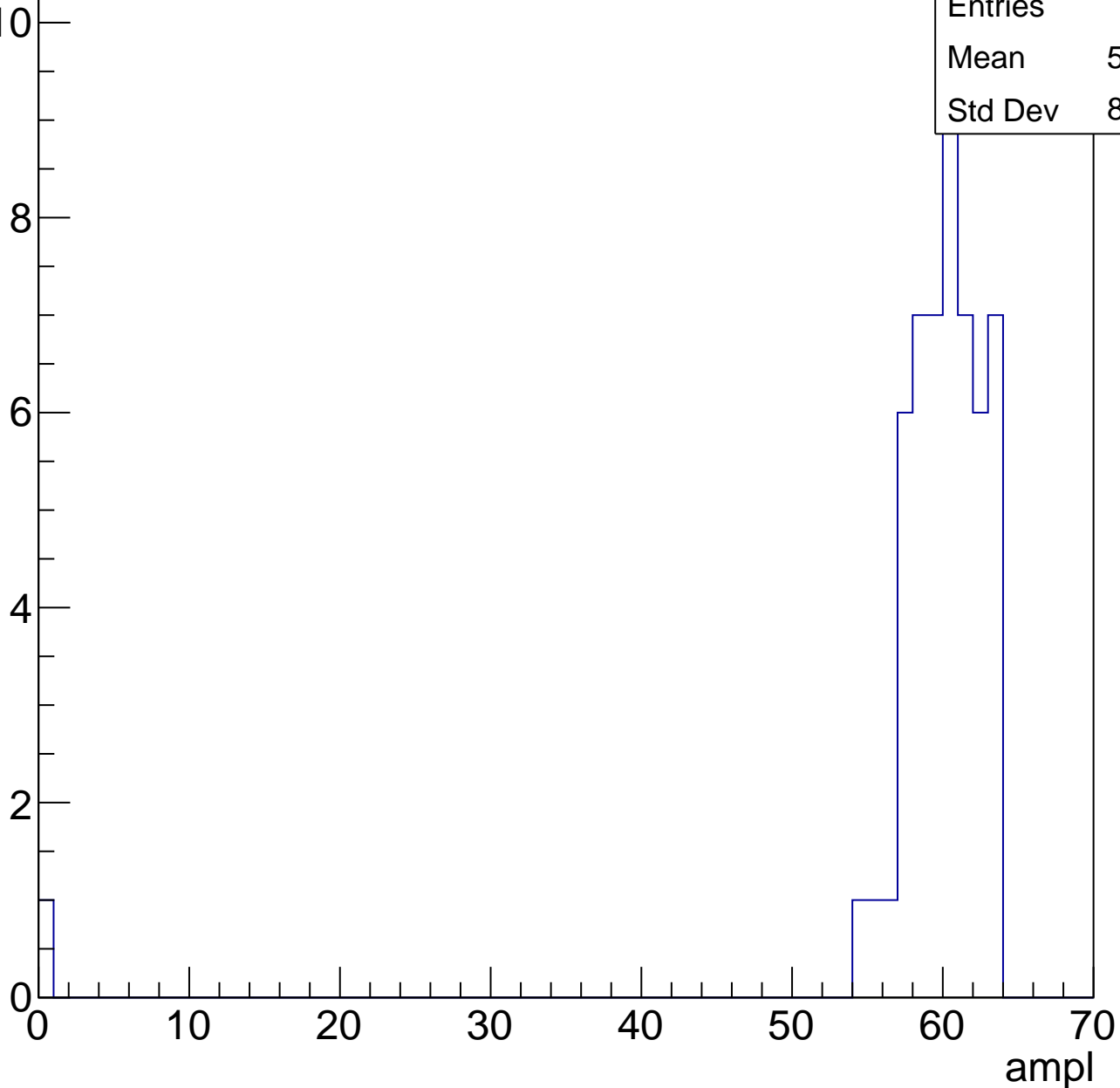


# B1L000S, U21-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

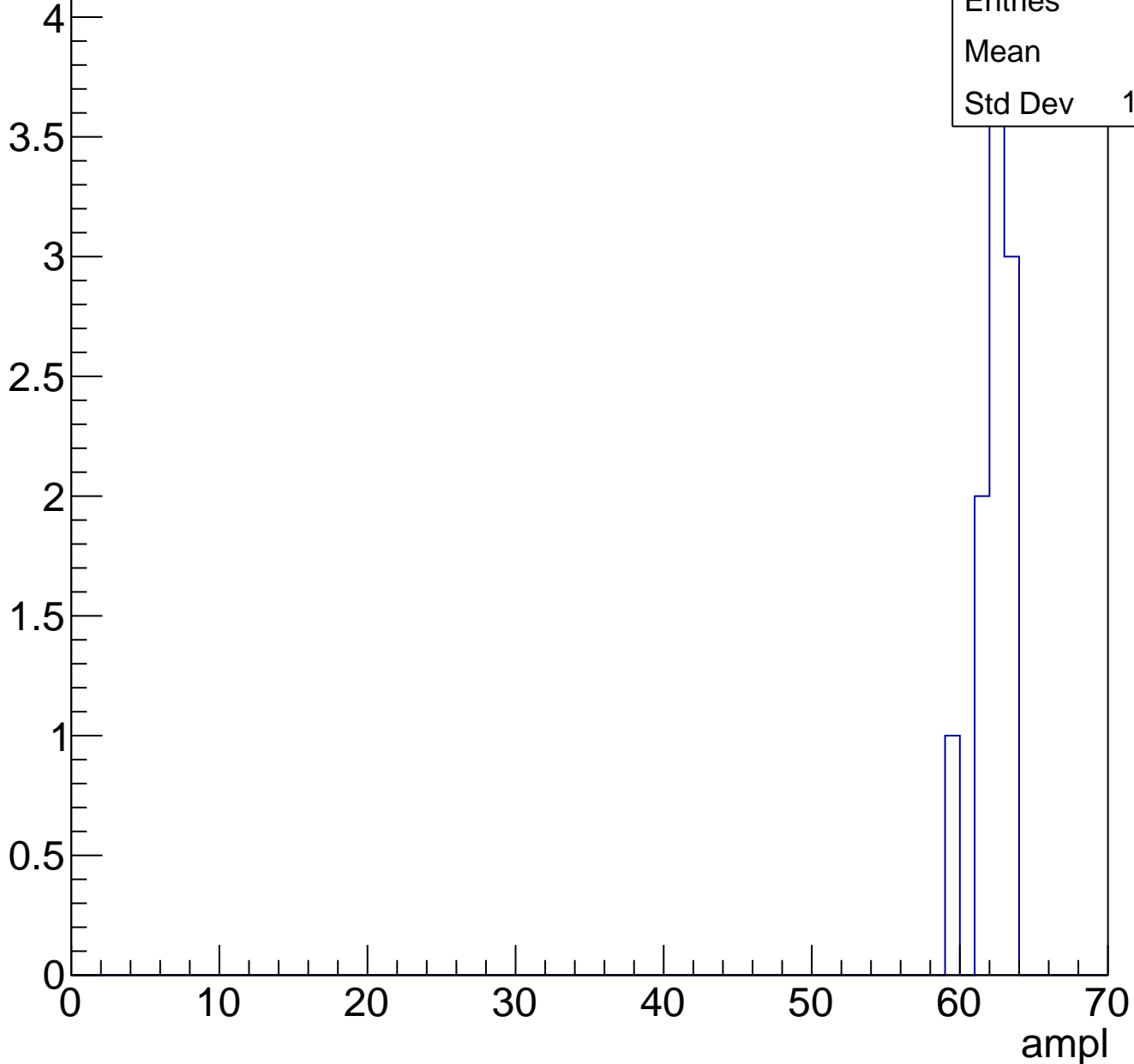
Entries	54
Mean	58.63
Std Dev	8.343



# B1L000S, U21-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	10
Mean	61.8
Std Dev	1.166



# B1L000S, U21-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch118, adc0

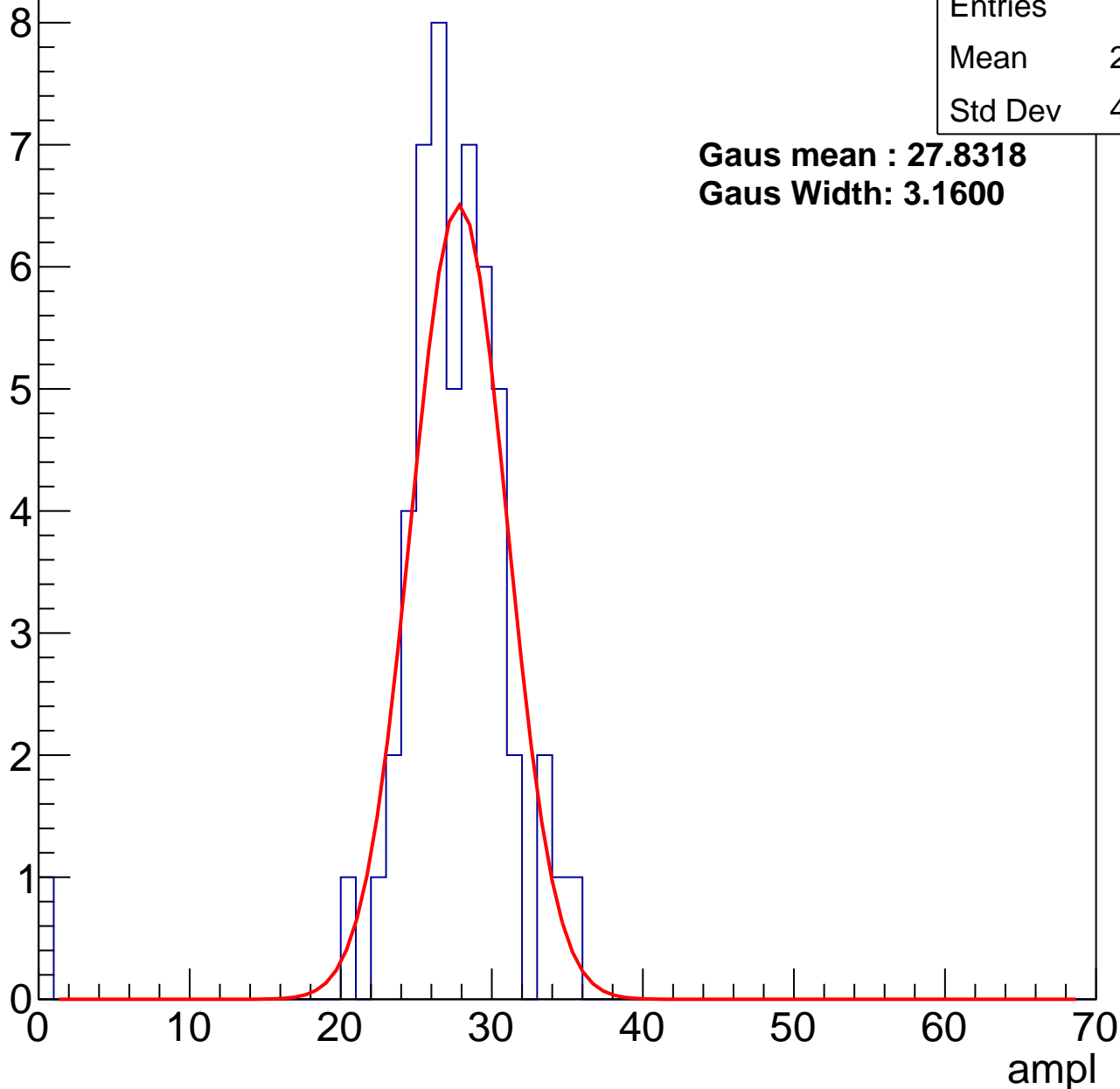
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	26.77
Std Dev	4.753

**Gaus mean : 27.8318**

**Gaus Width: 3.1600**



# B1L000S, U21-ch118, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	86
Mean	34.45
Std Dev	3.588

**Gaus mean : 34.7045**

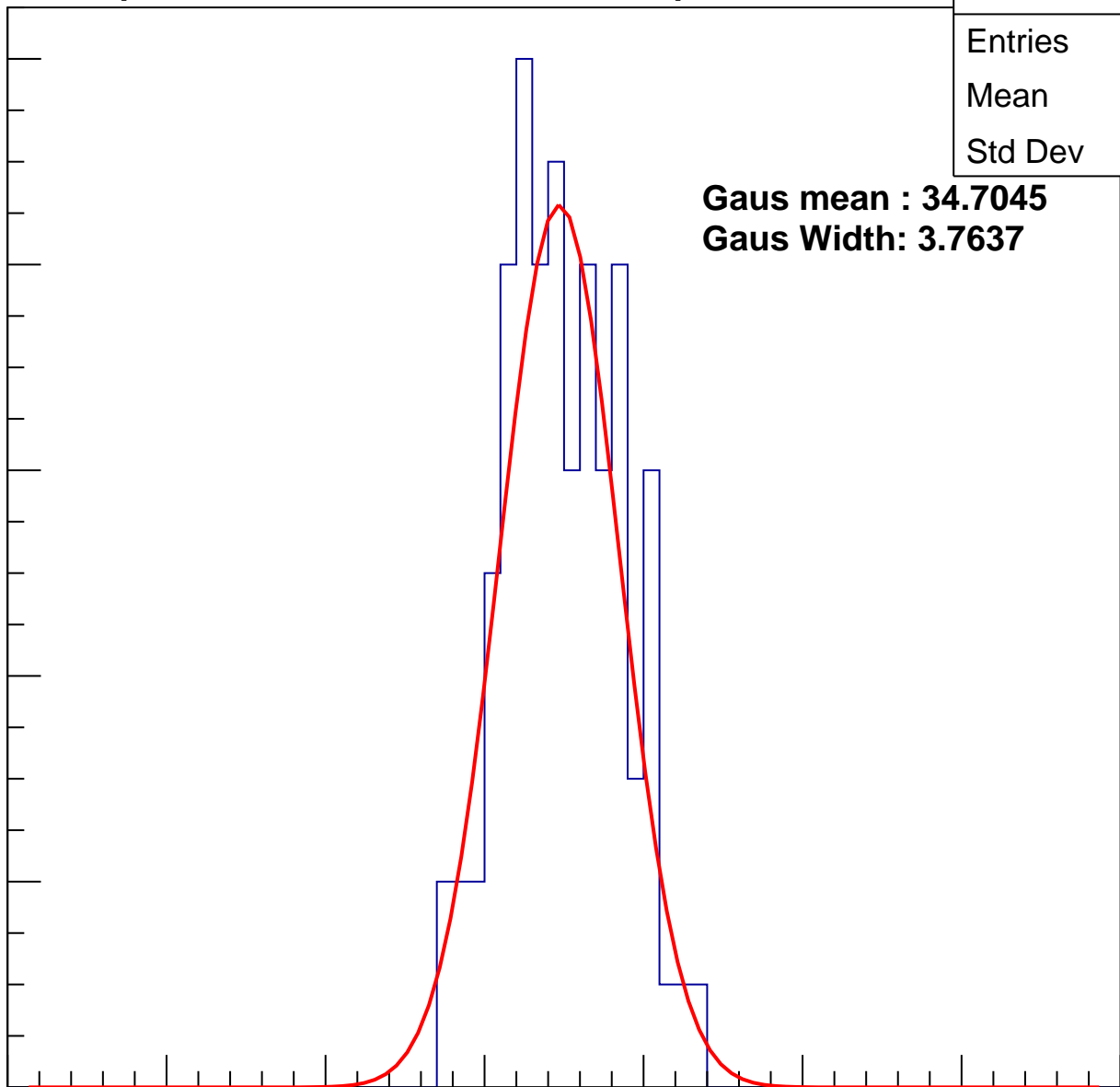
**Gaus Width: 3.7637**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U21-ch118, adc2

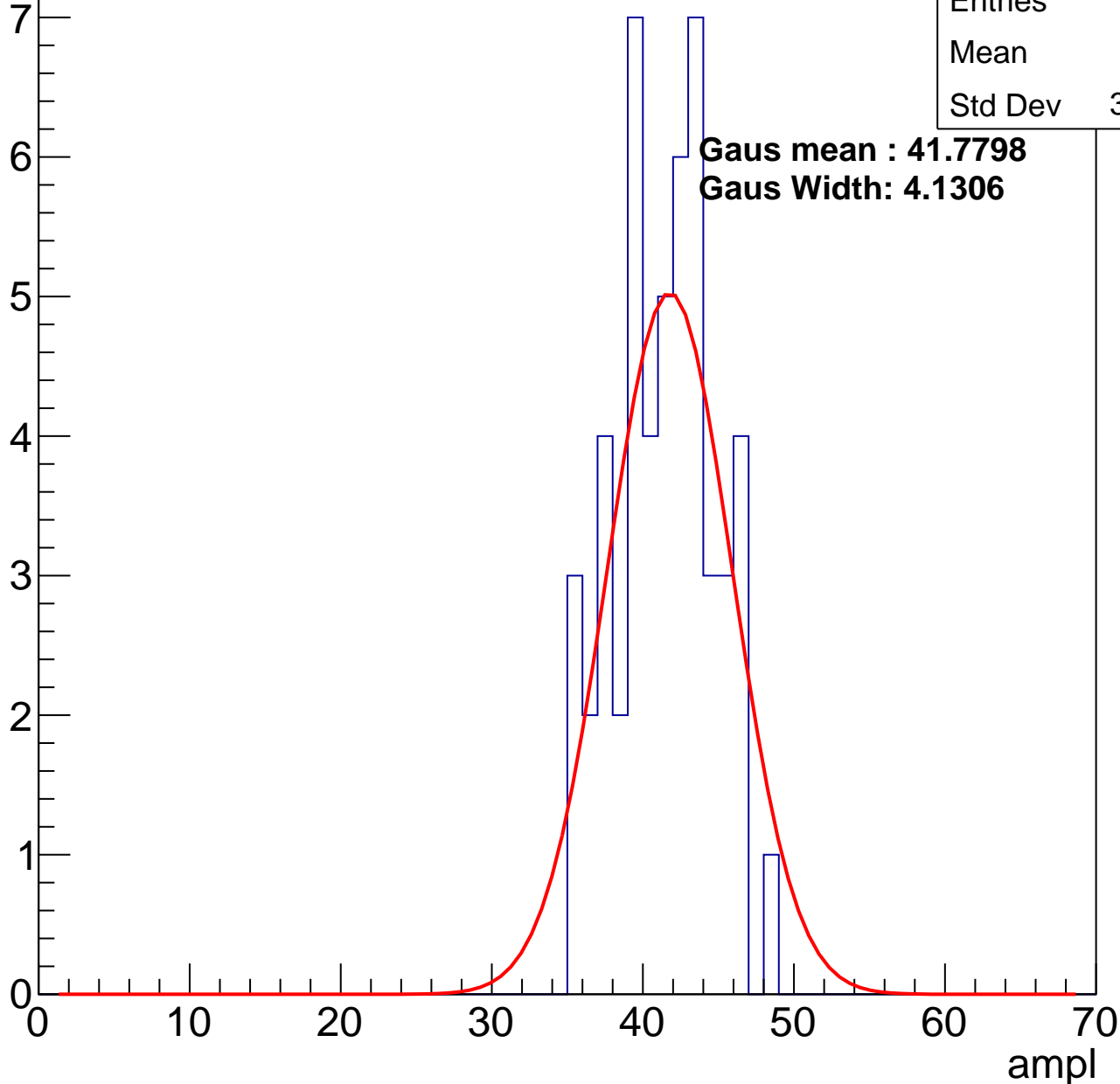
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	41
Std Dev	3.224

**Gaus mean : 41.7798**

**Gaus Width: 4.1306**

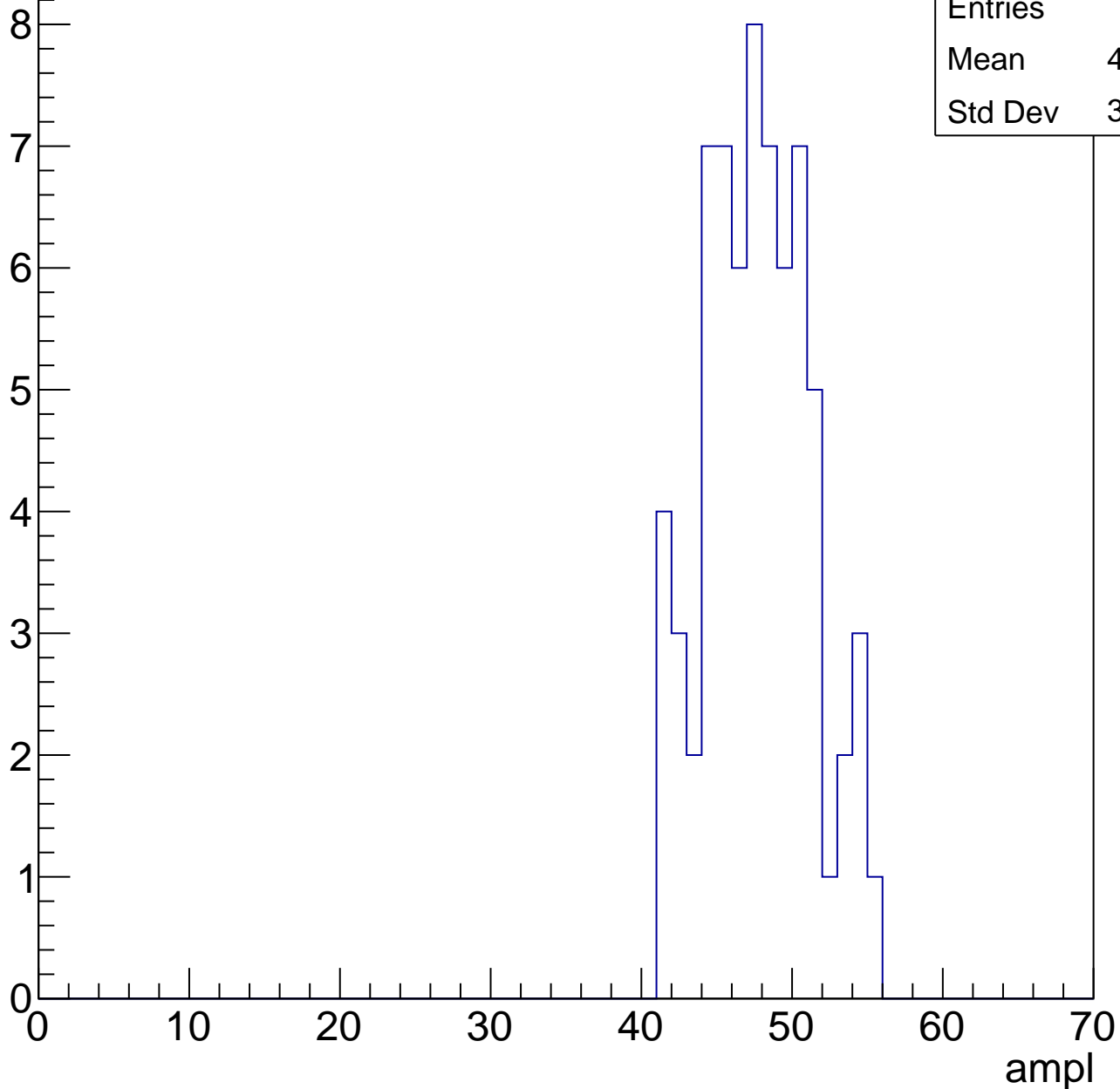


# B1L000S, U21-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	47.26
Std Dev	3.458

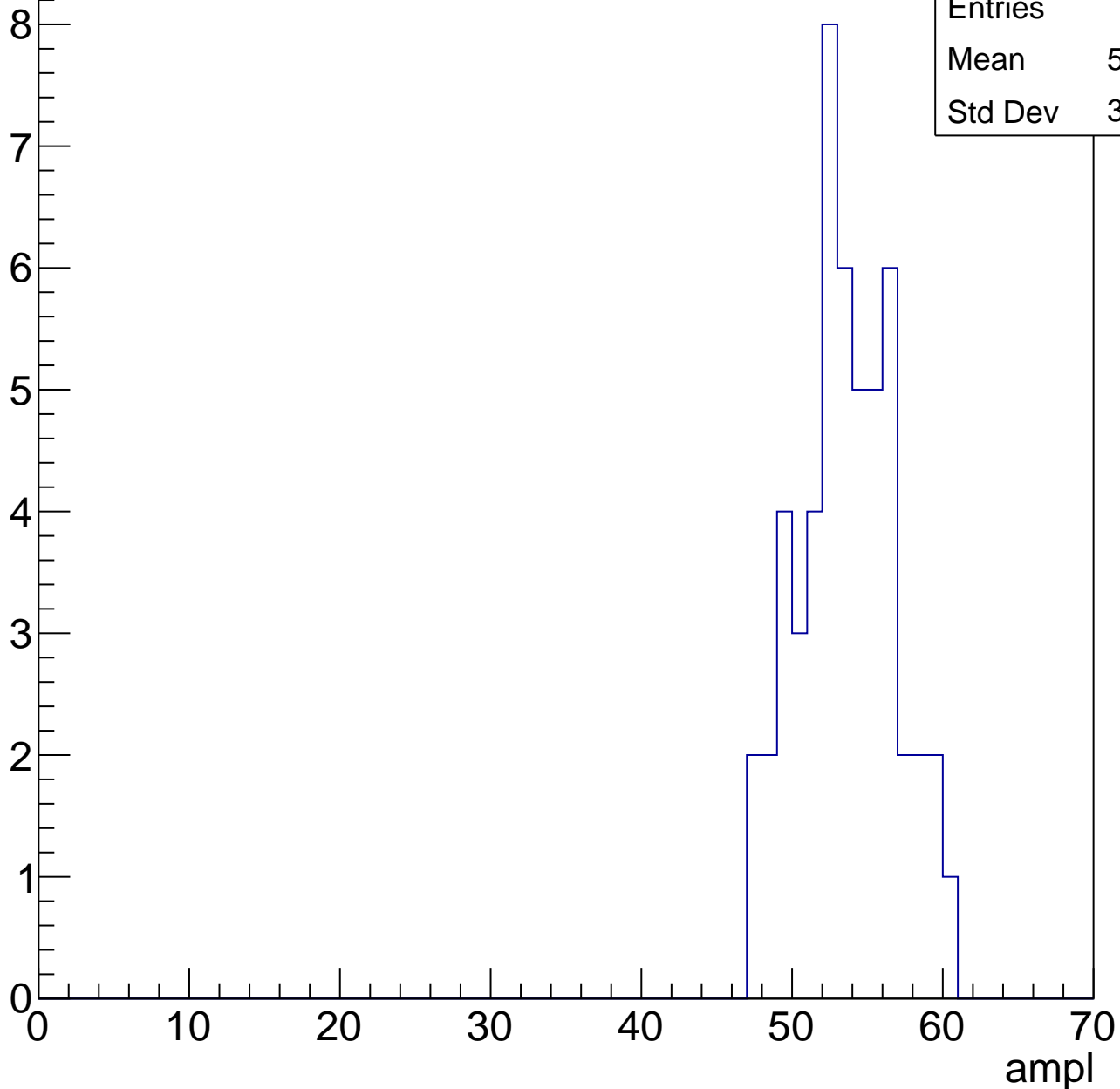


# B1L000S, U21-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	53.13
Std Dev	3.156

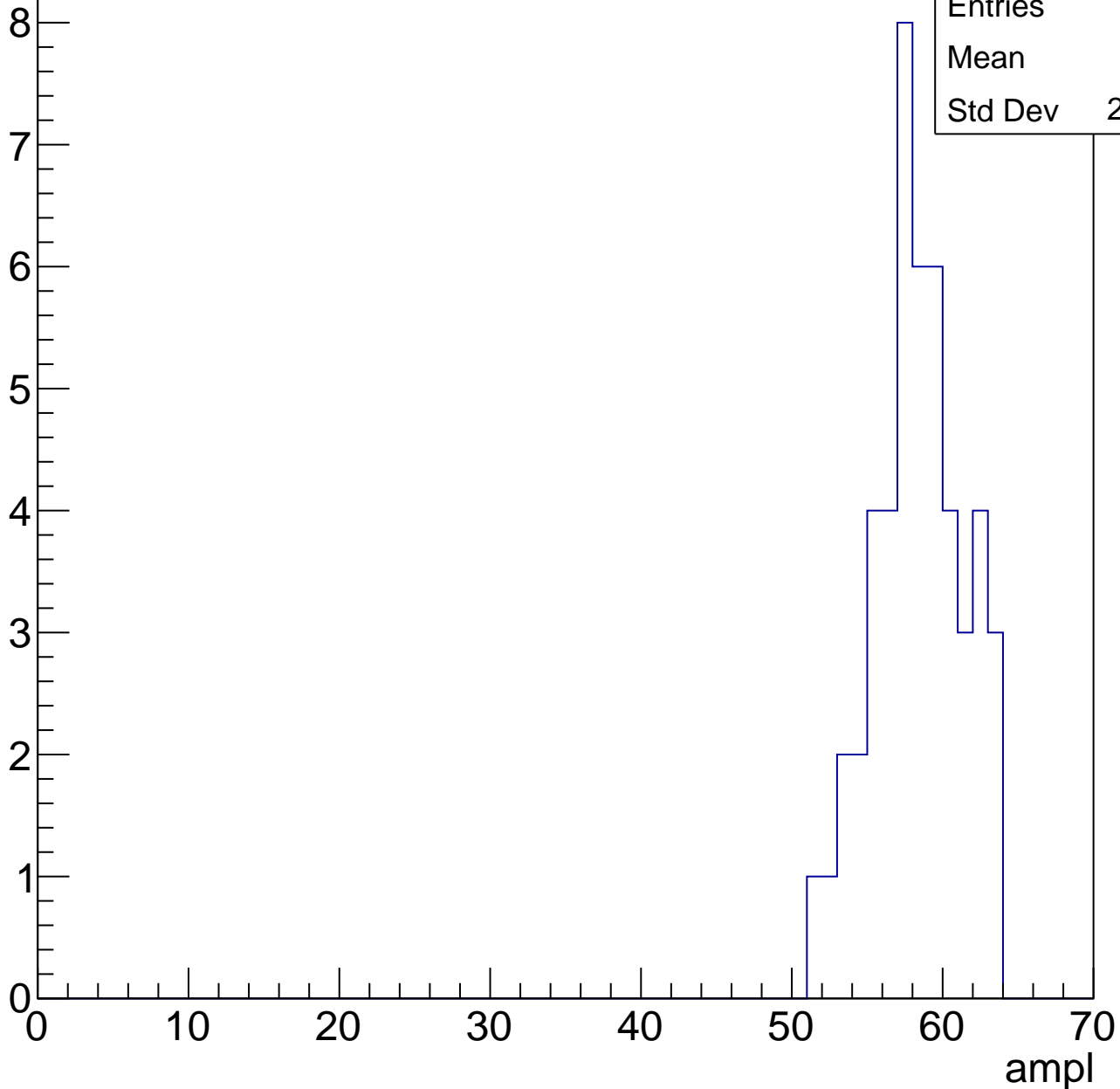


# B1L000S, U21-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	57.9
Std Dev	2.939

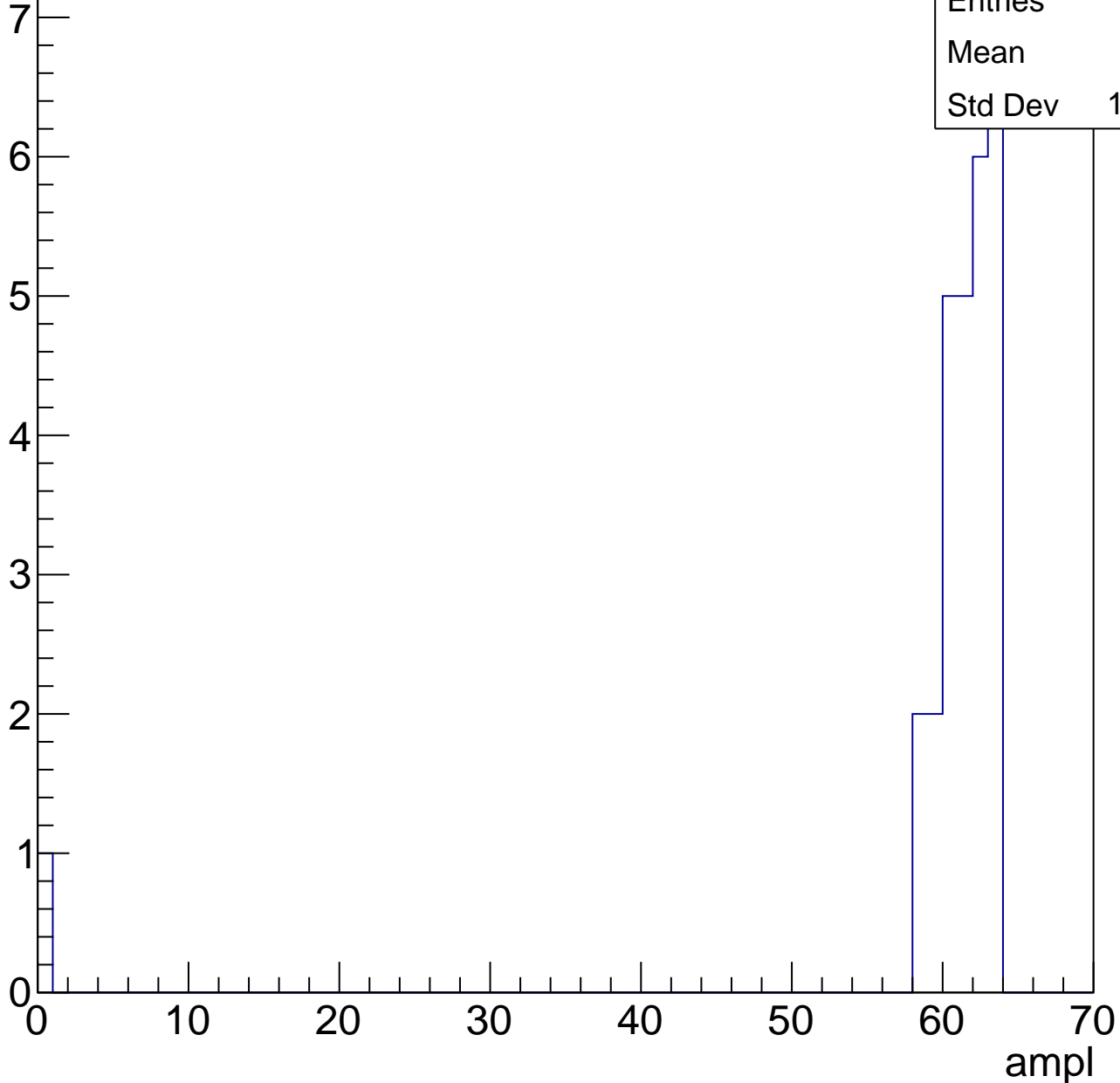


# B1L000S, U21-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	59
Std Dev	11.45

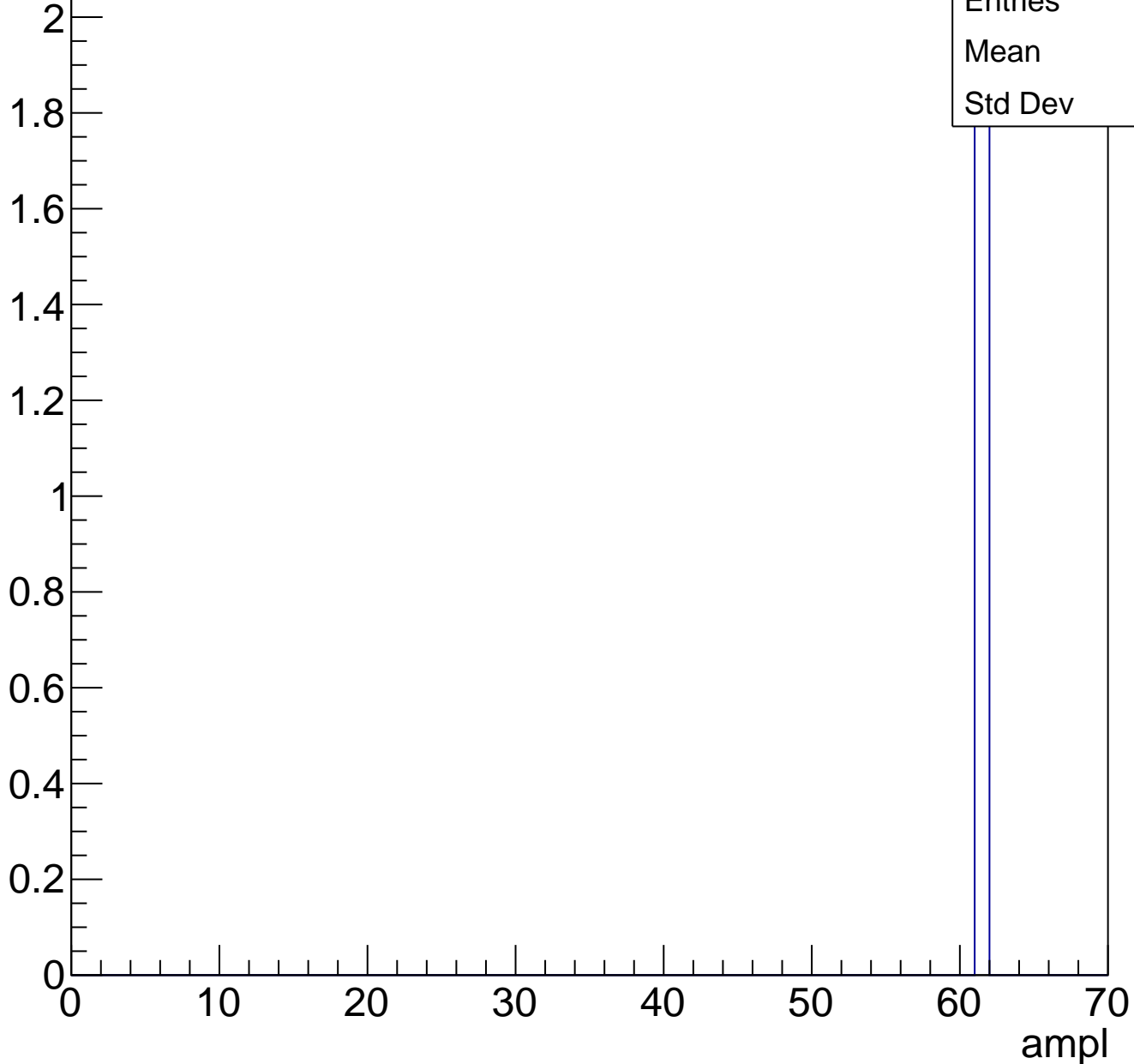




# B1L000S, U21-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	29.82
Std Dev	3.405

**Gaus mean : 29.8779**

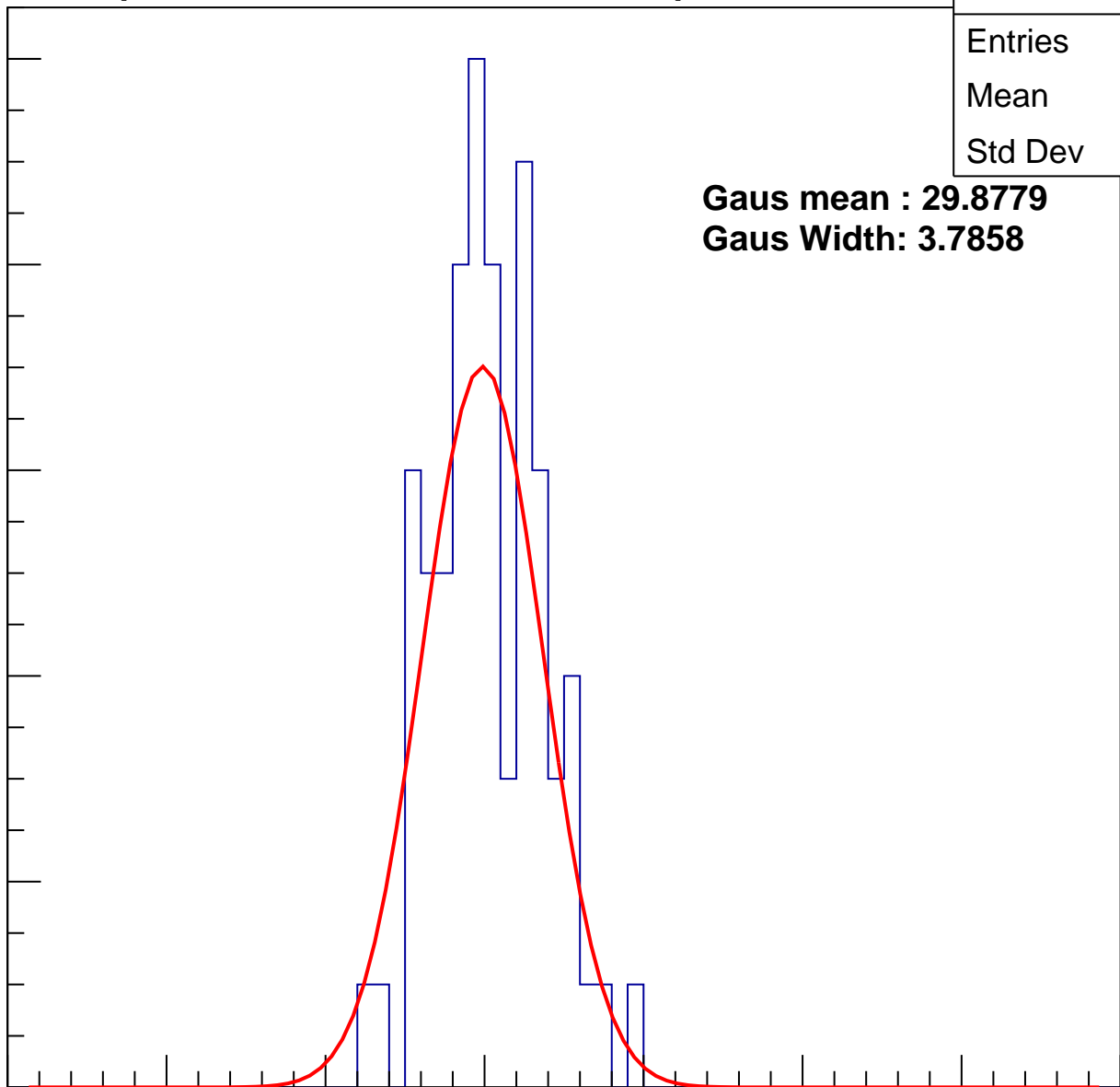
**Gaus Width: 3.7858**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch119, adc1

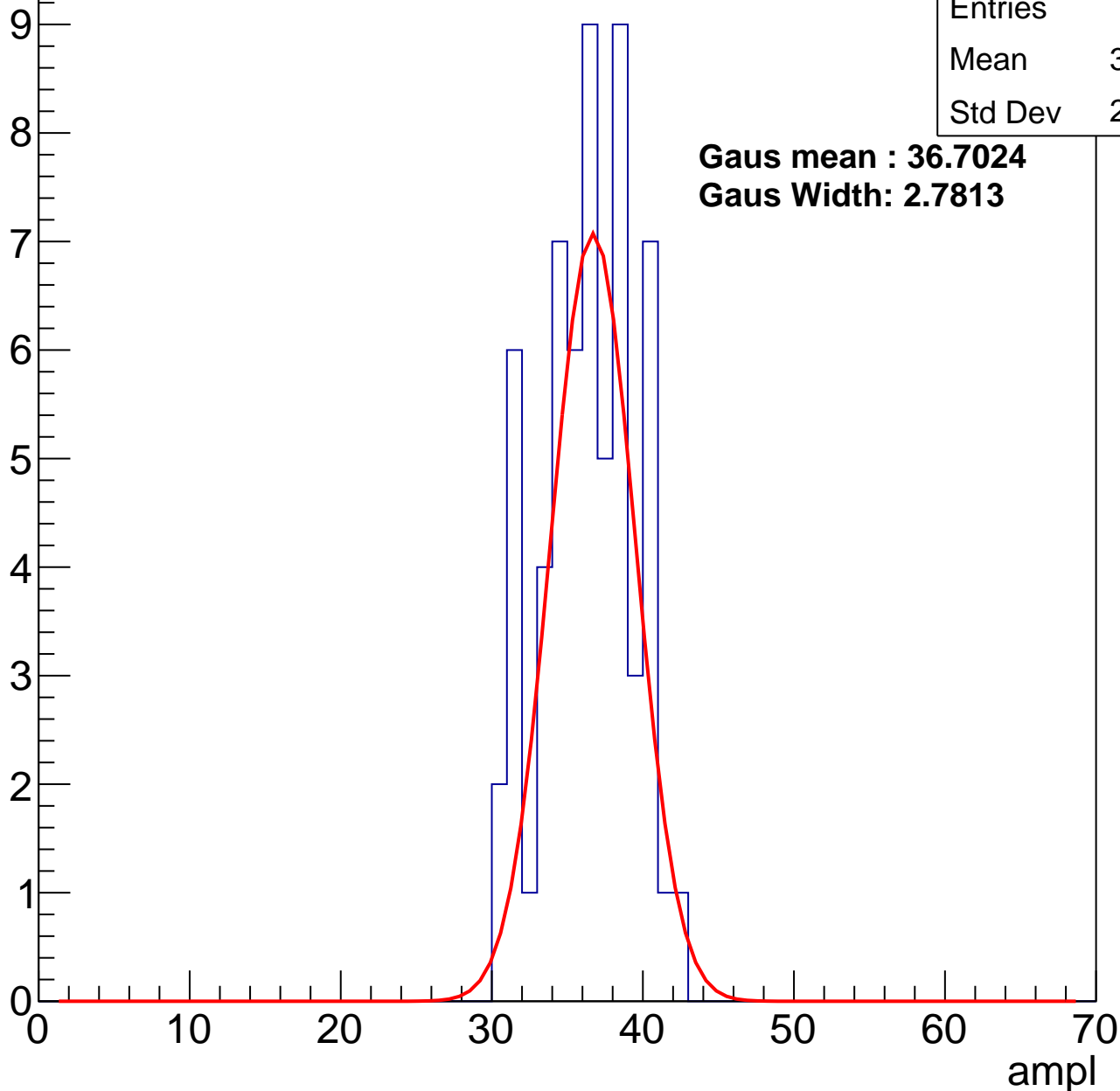
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.89
Std Dev	2.998

**Gaus mean : 36.7024**

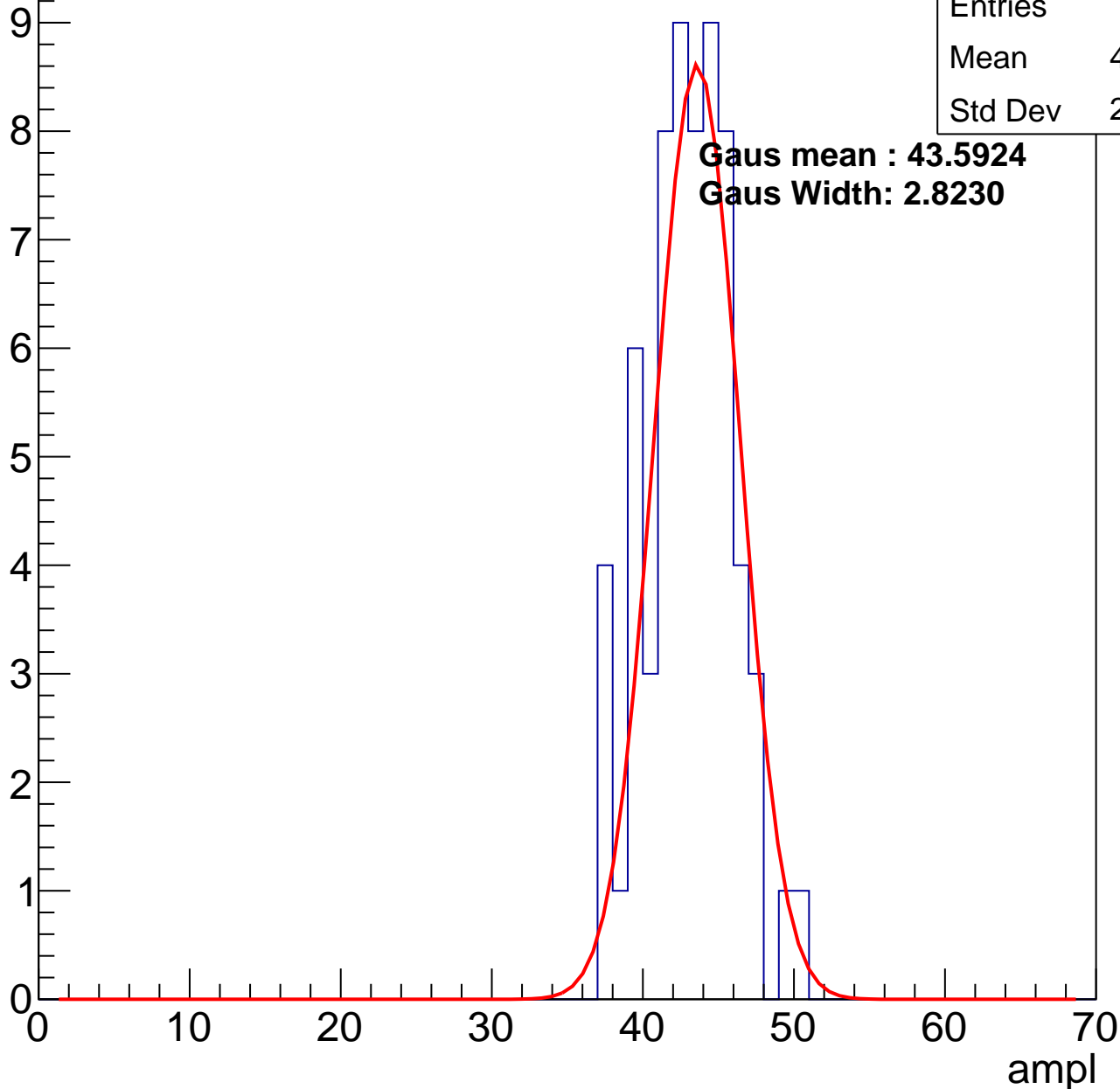
**Gaus Width: 2.7813**



# B1L000S, U21-ch119, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



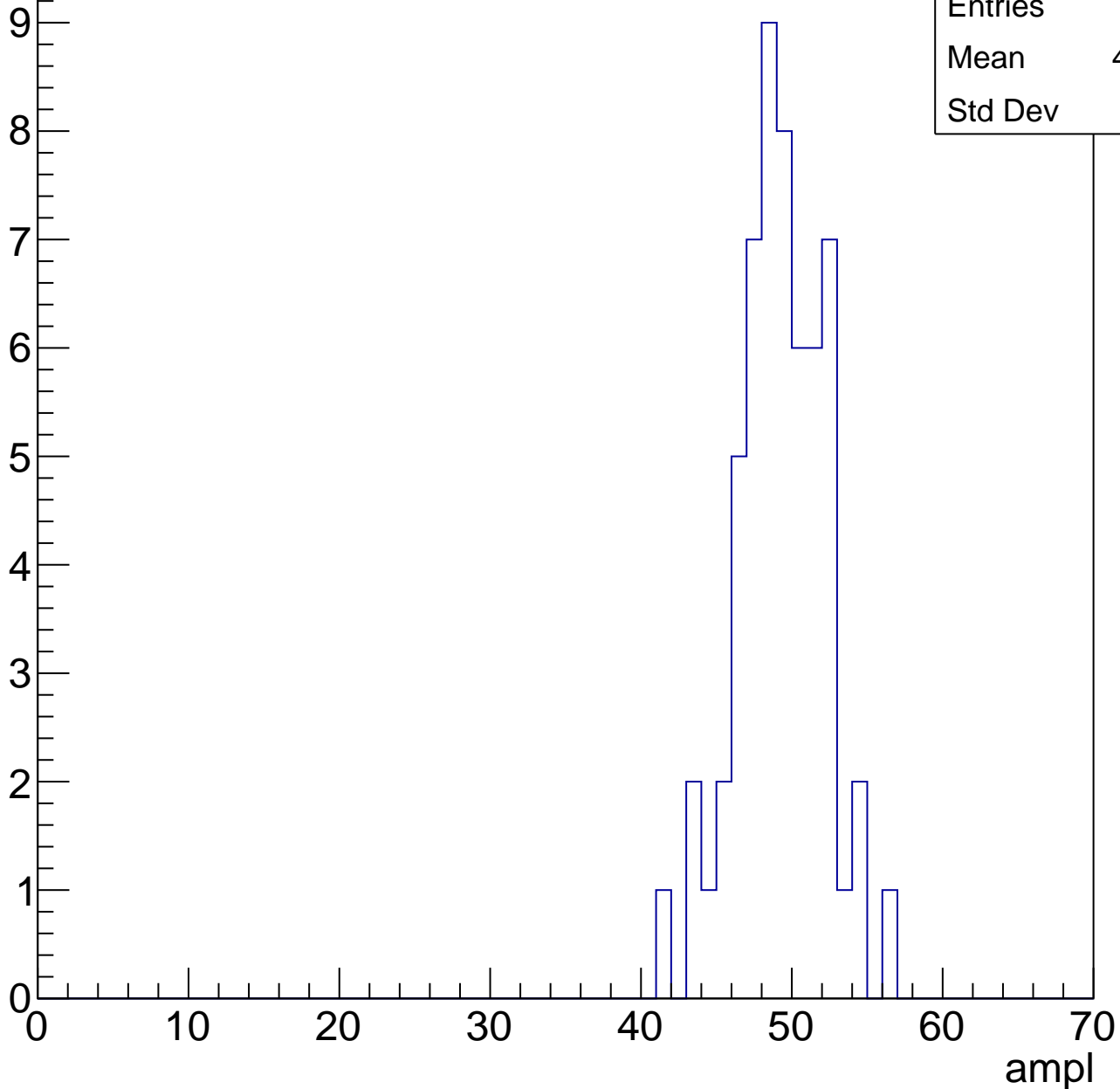
Entries	65
Mean	42.62
Std Dev	2.865

# B1L000S, U21-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	48.81
Std Dev	2.88

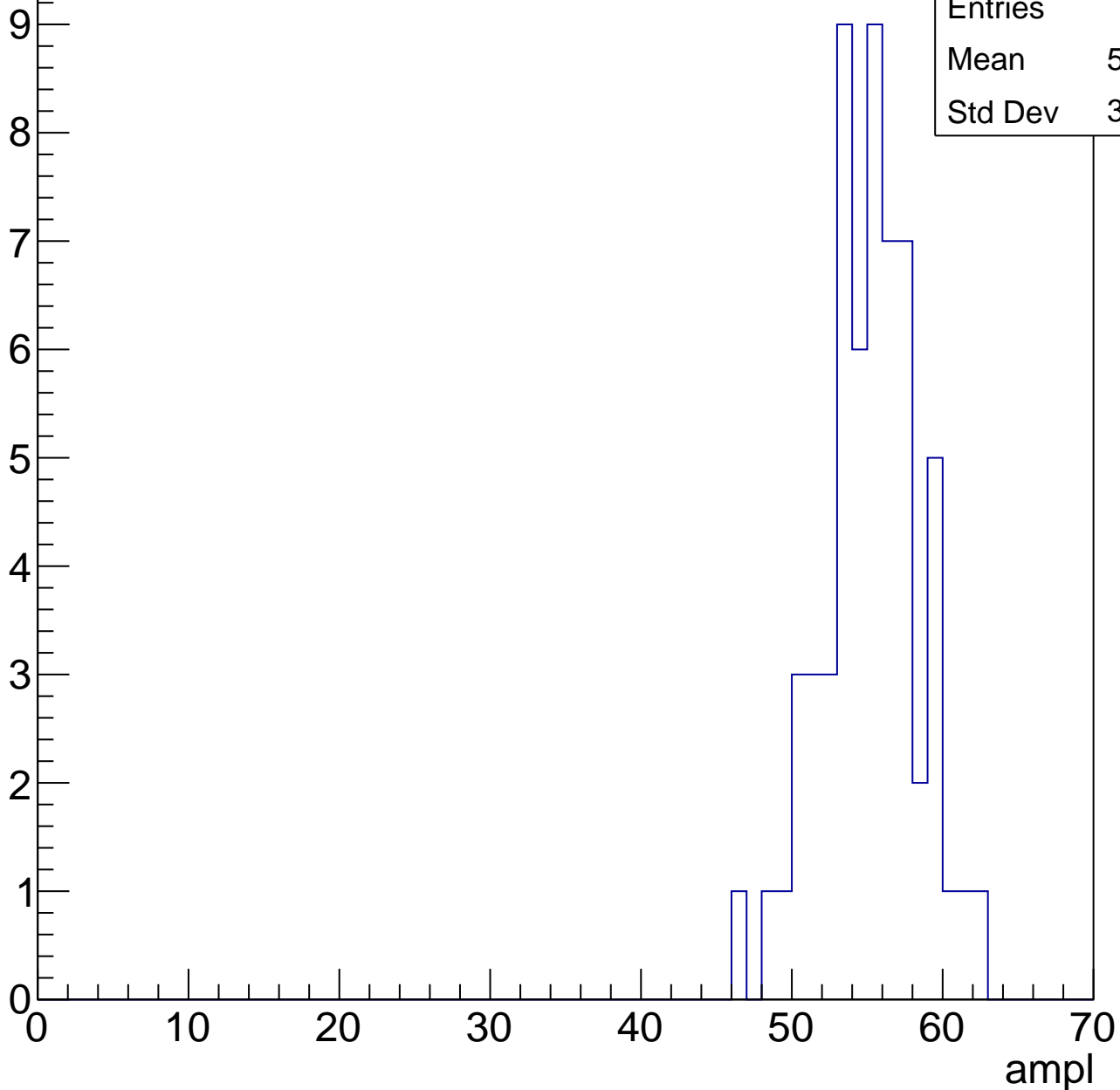


# B1L000S, U21-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.72
Std Dev	3.152

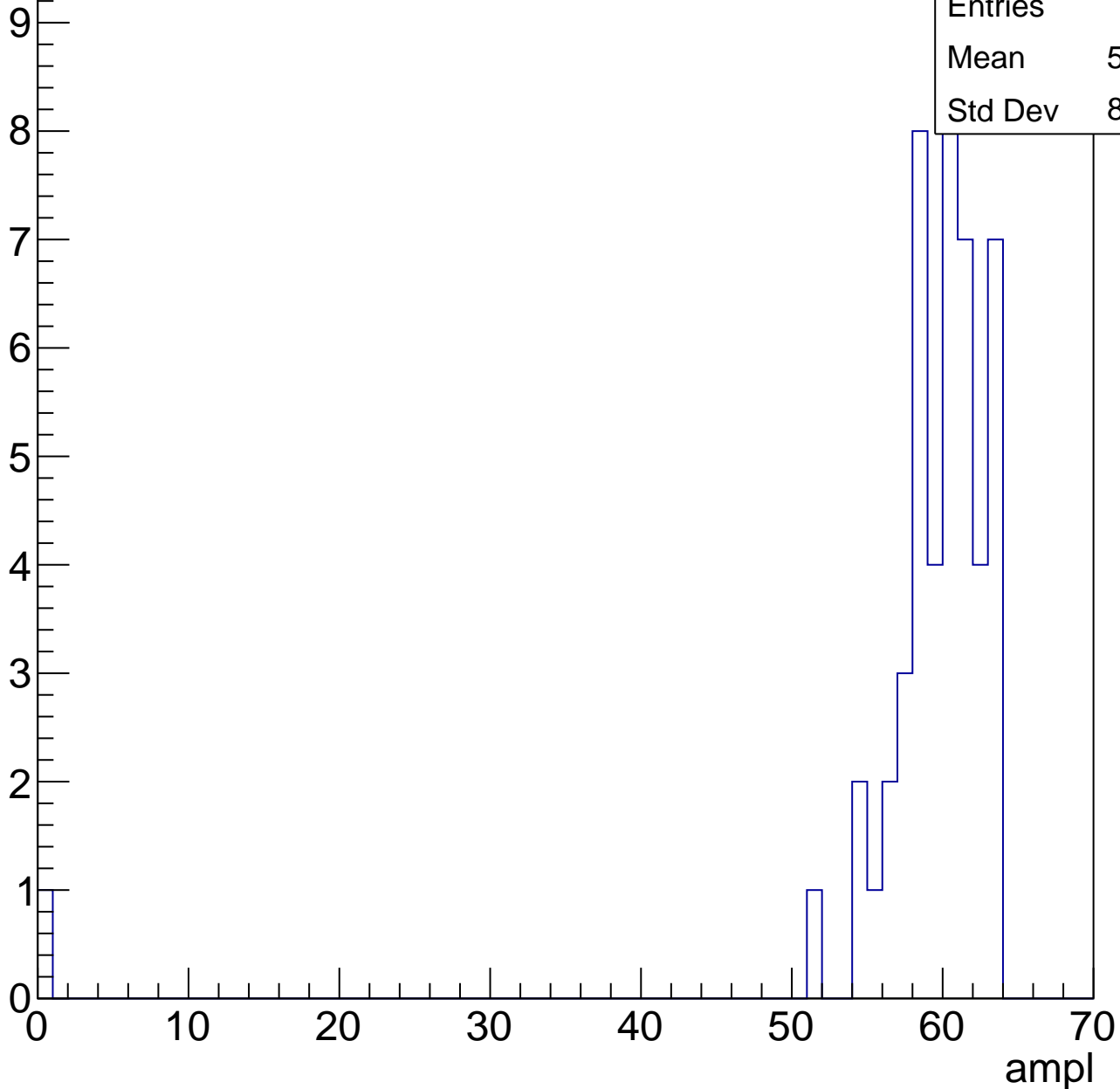


# B1L000S, U21-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	58.22
Std Dev	8.812

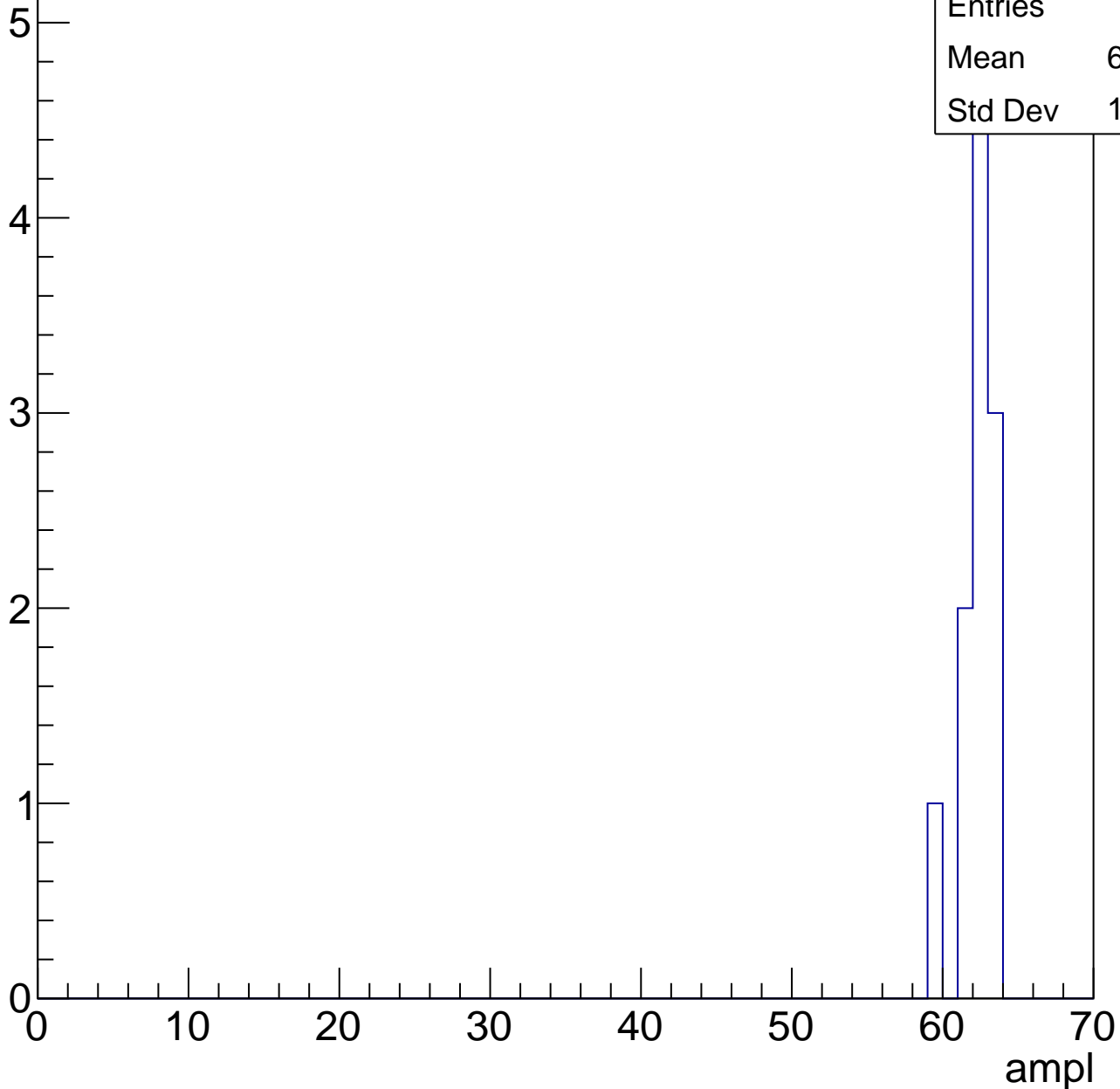


# B1L000S, U21-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	61.82
Std Dev	1.113





# B1L000S, U21-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch120, adc0

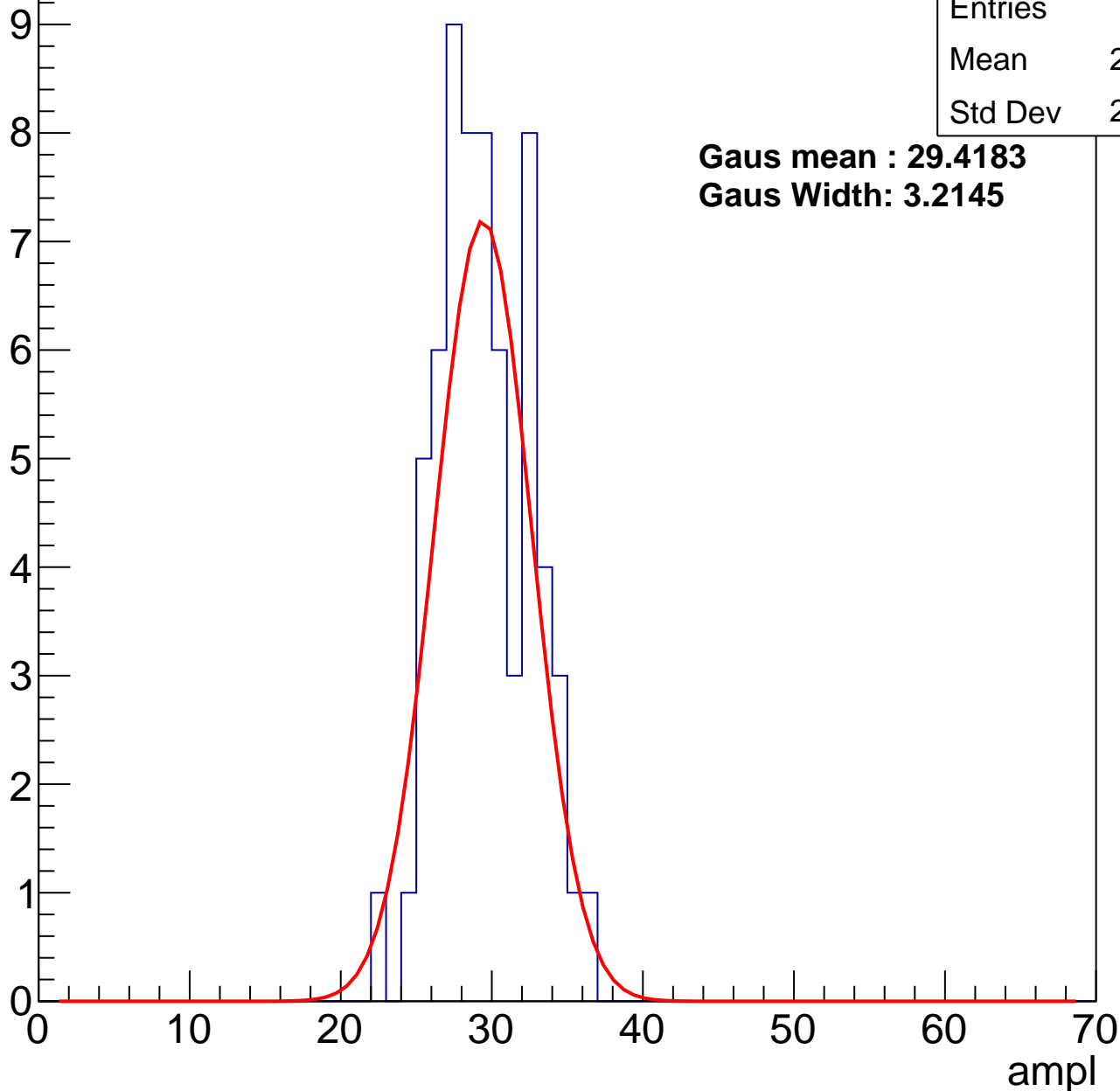
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.06
Std Dev	2.973

**Gaus mean : 29.4183**

**Gaus Width: 3.2145**



# B1L000S, U21-ch120, adc1

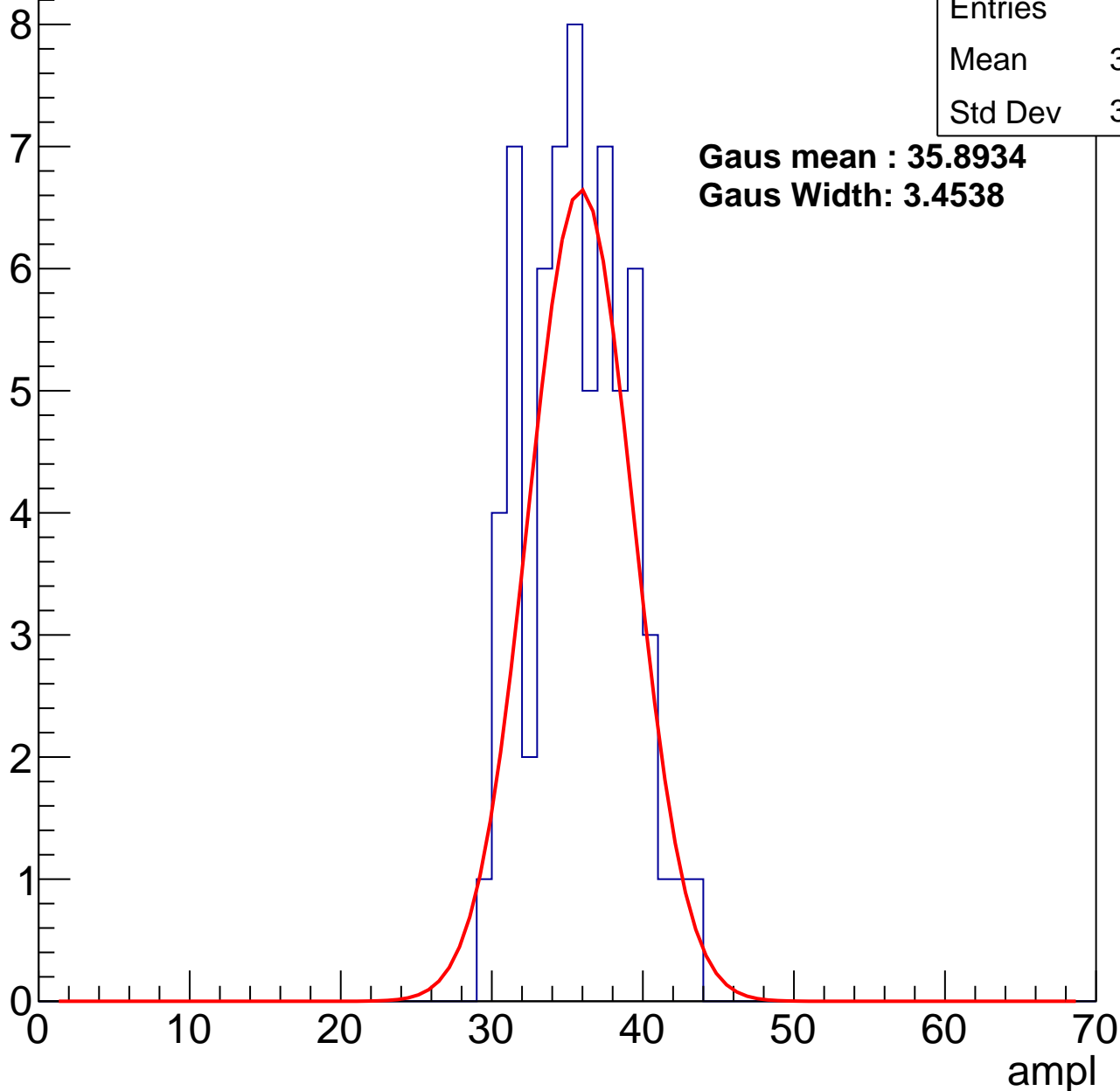
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.23
Std Dev	3.287

**Gaus mean : 35.8934**

**Gaus Width: 3.4538**



# B1L000S, U21-ch120, adc2

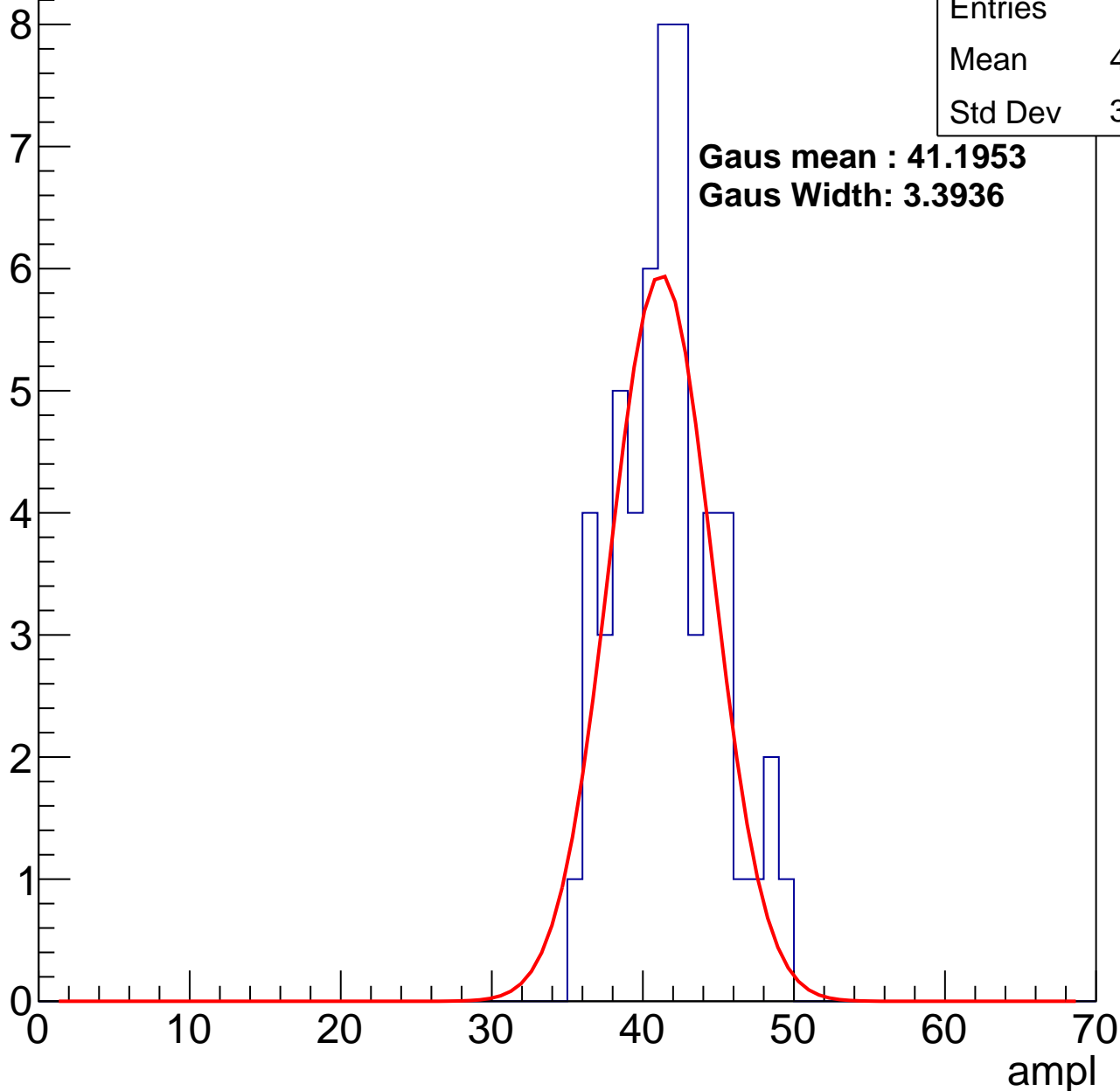
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.15
Std Dev	3.283

**Gaus mean : 41.1953**

**Gaus Width: 3.3936**

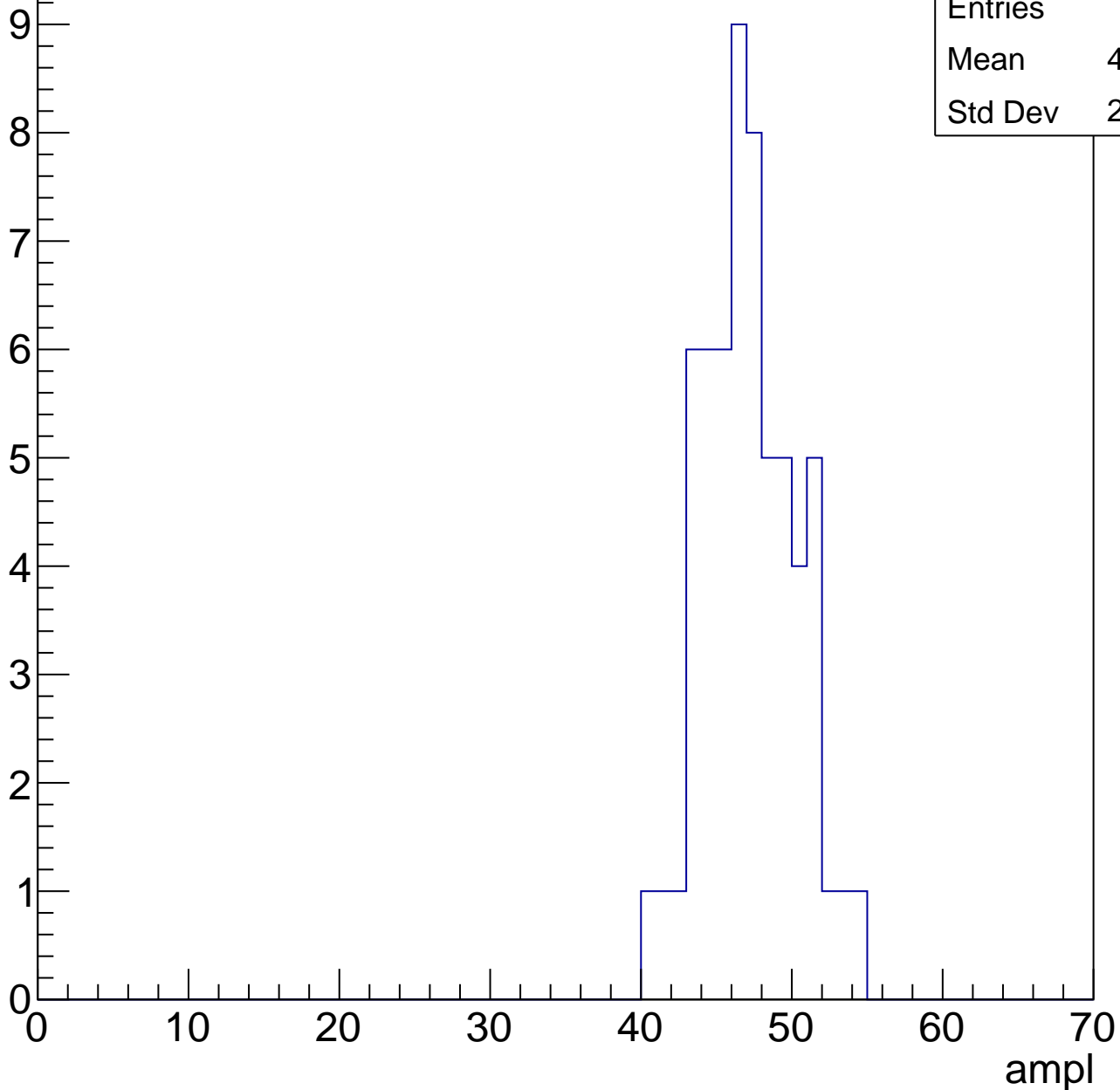


# B1L000S, U21-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	46.73
Std Dev	2.999

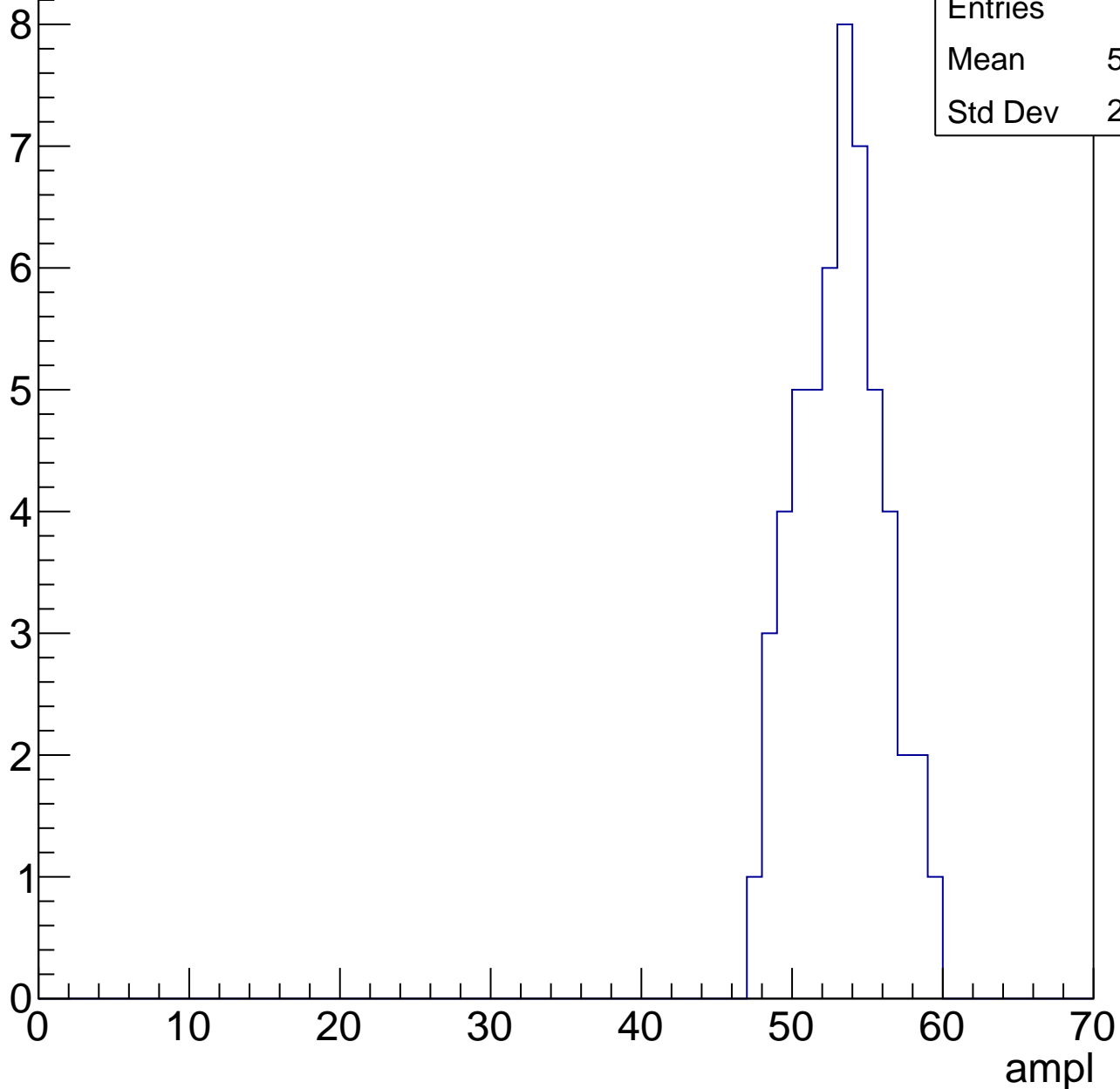


# B1L000S, U21-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	52.72
Std Dev	2.824

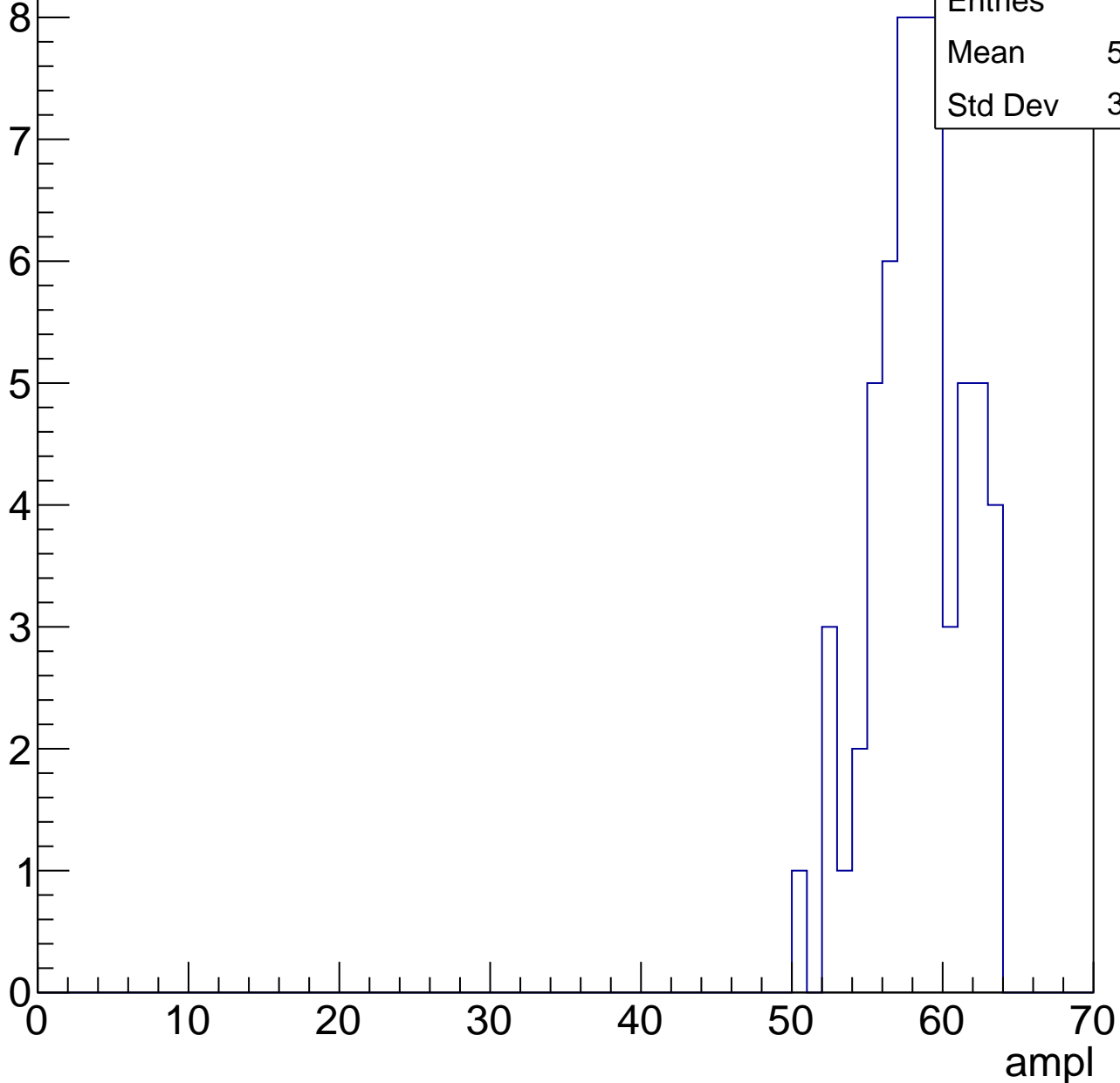


# B1L000S, U21-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.92
Std Dev	3.055

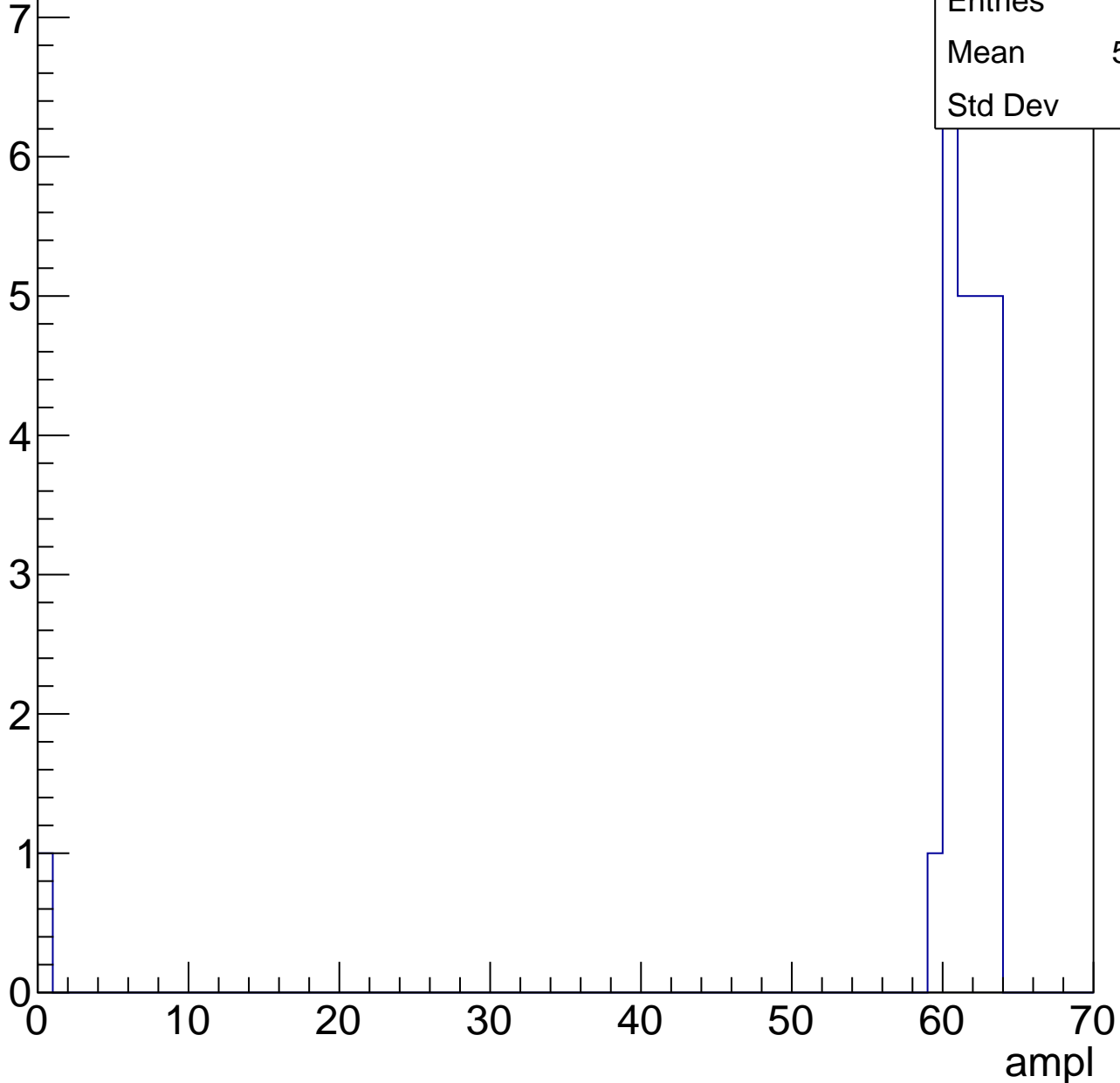


# B1L000S, U21-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.71
Std Dev	12.3

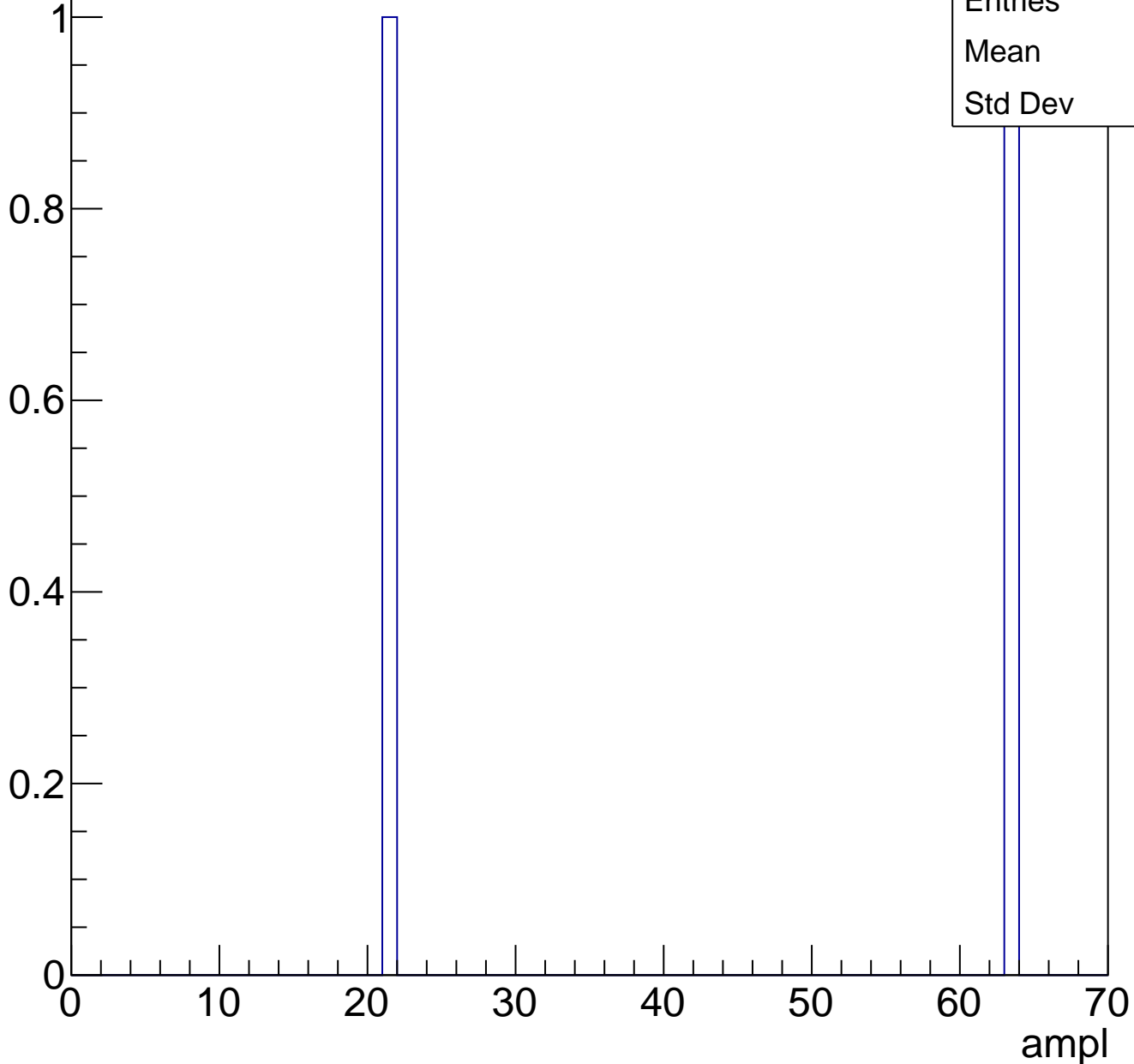




# B1L000S, U21-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch121, adc0

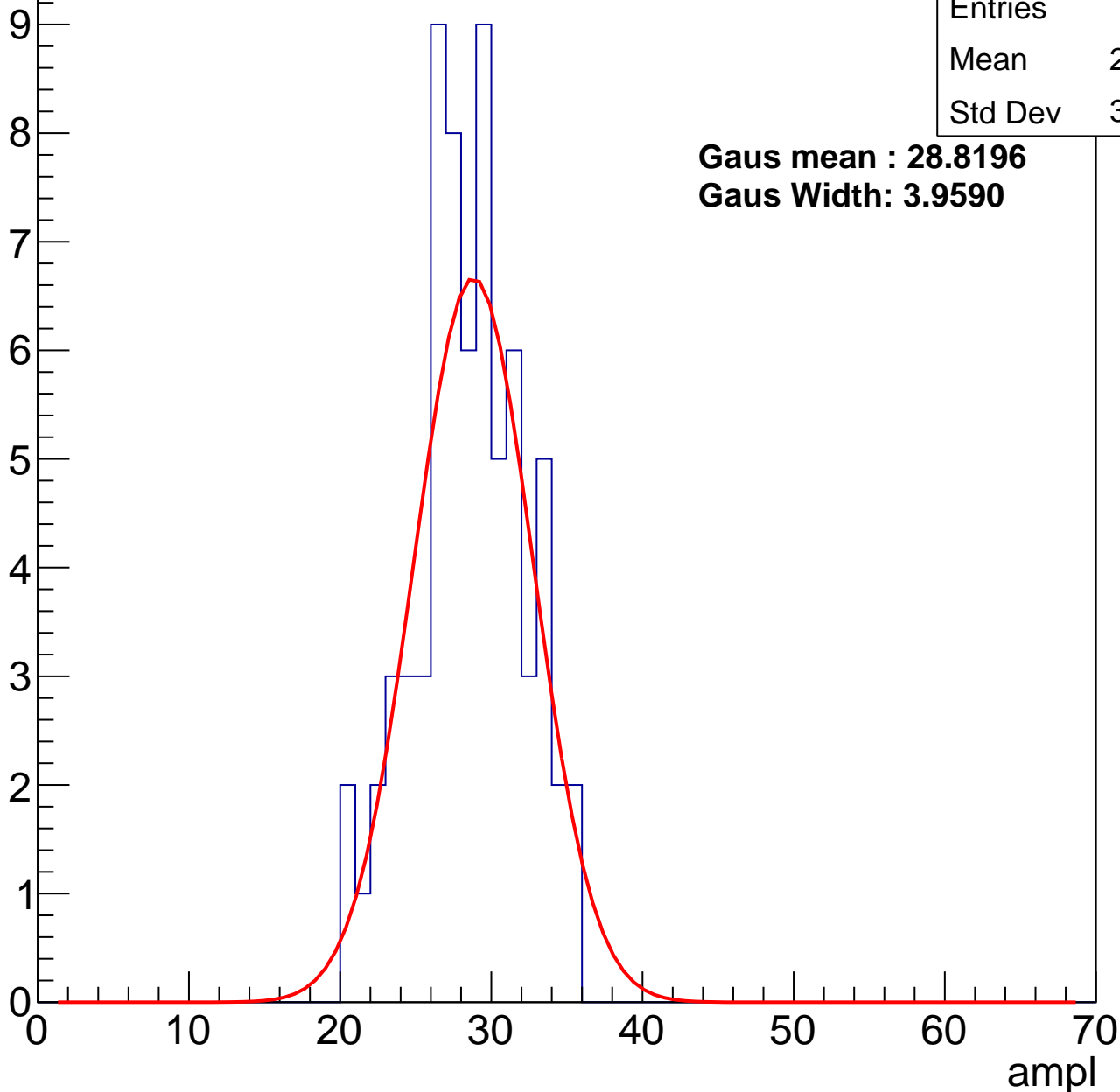
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.04
Std Dev	3.549

**Gaus mean : 28.8196**

**Gaus Width: 3.9590**



# B1L000S, U21-ch121, adc1

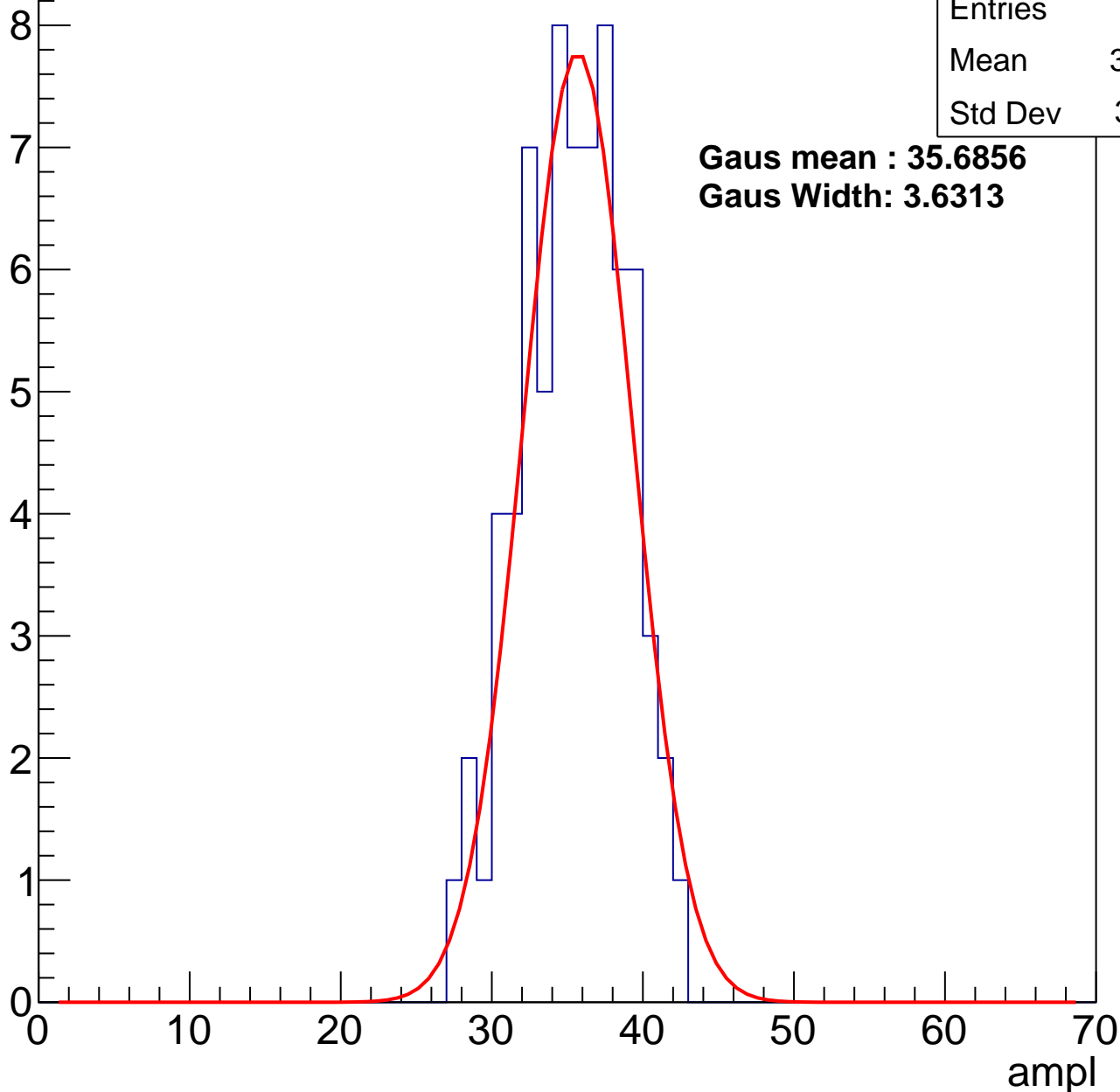
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	34.94
Std Dev	3.411

**Gaus mean : 35.6856**

**Gaus Width: 3.6313**



# B1L000S, U21-ch121, adc2

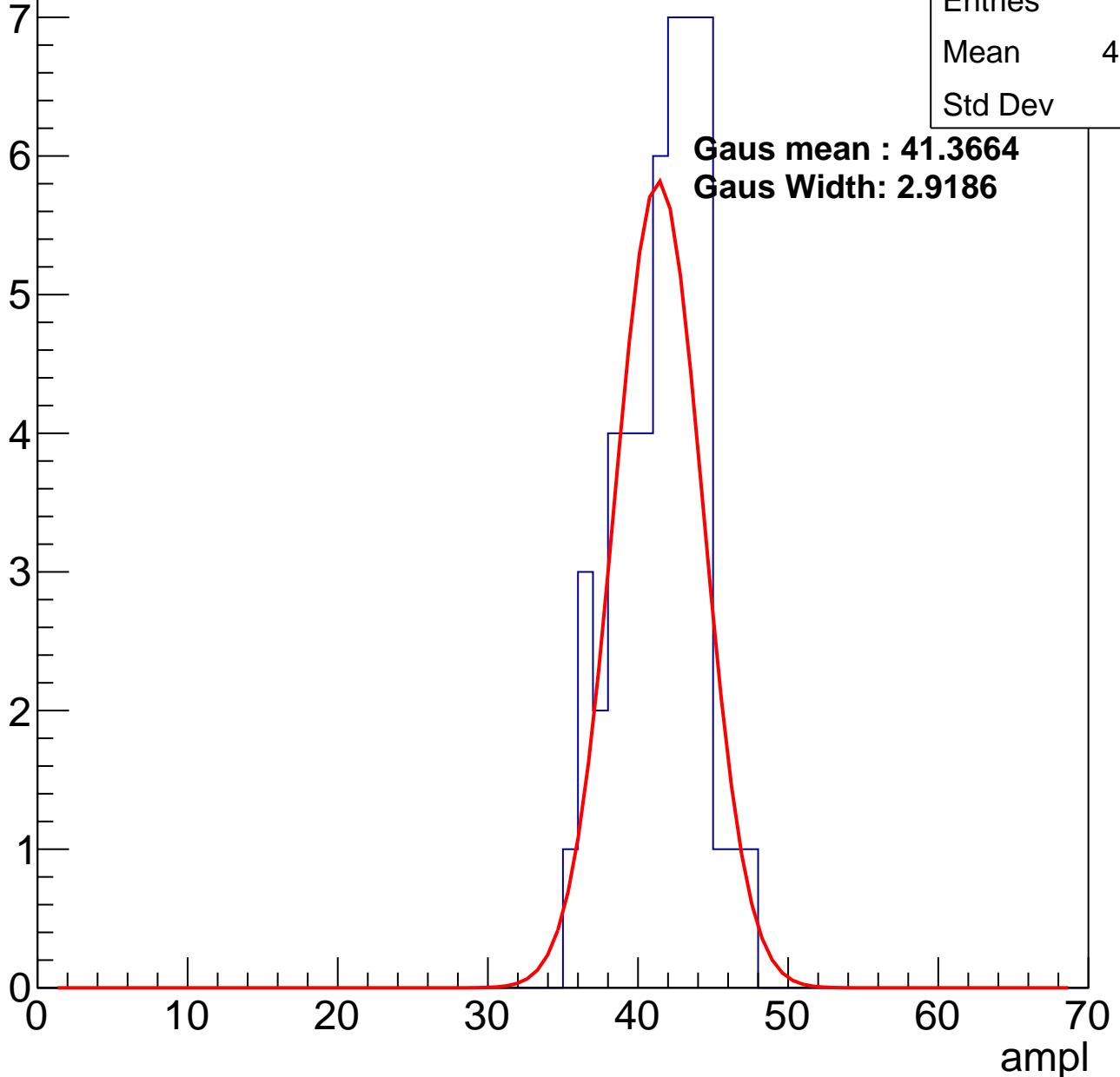
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	41.08
Std Dev	2.79

**Gaus mean : 41.3664**

**Gaus Width: 2.9186**

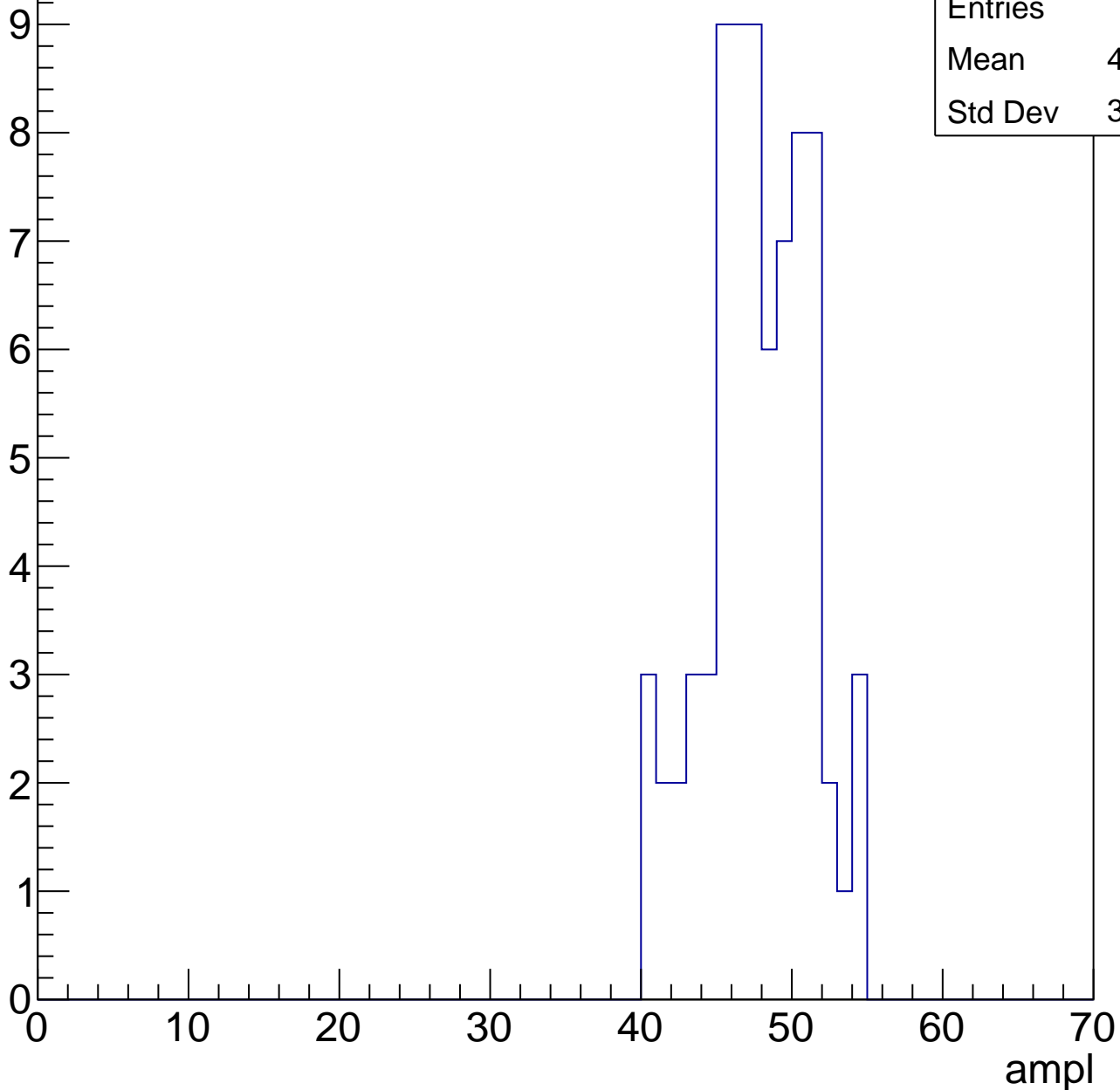


# B1L000S, U21-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	47.29
Std Dev	3.366

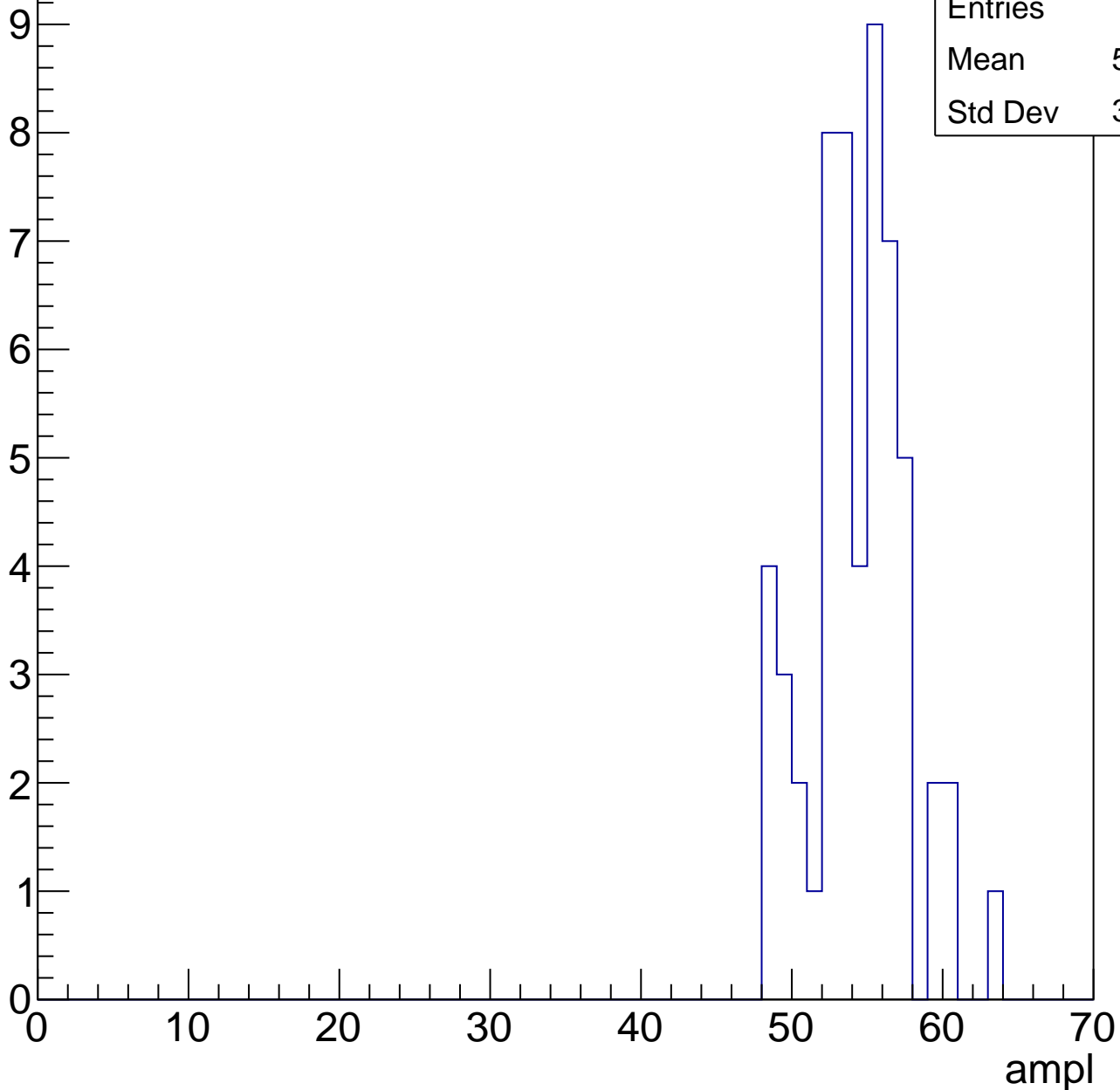


# B1L000S, U21-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	53.91
Std Dev	3.231

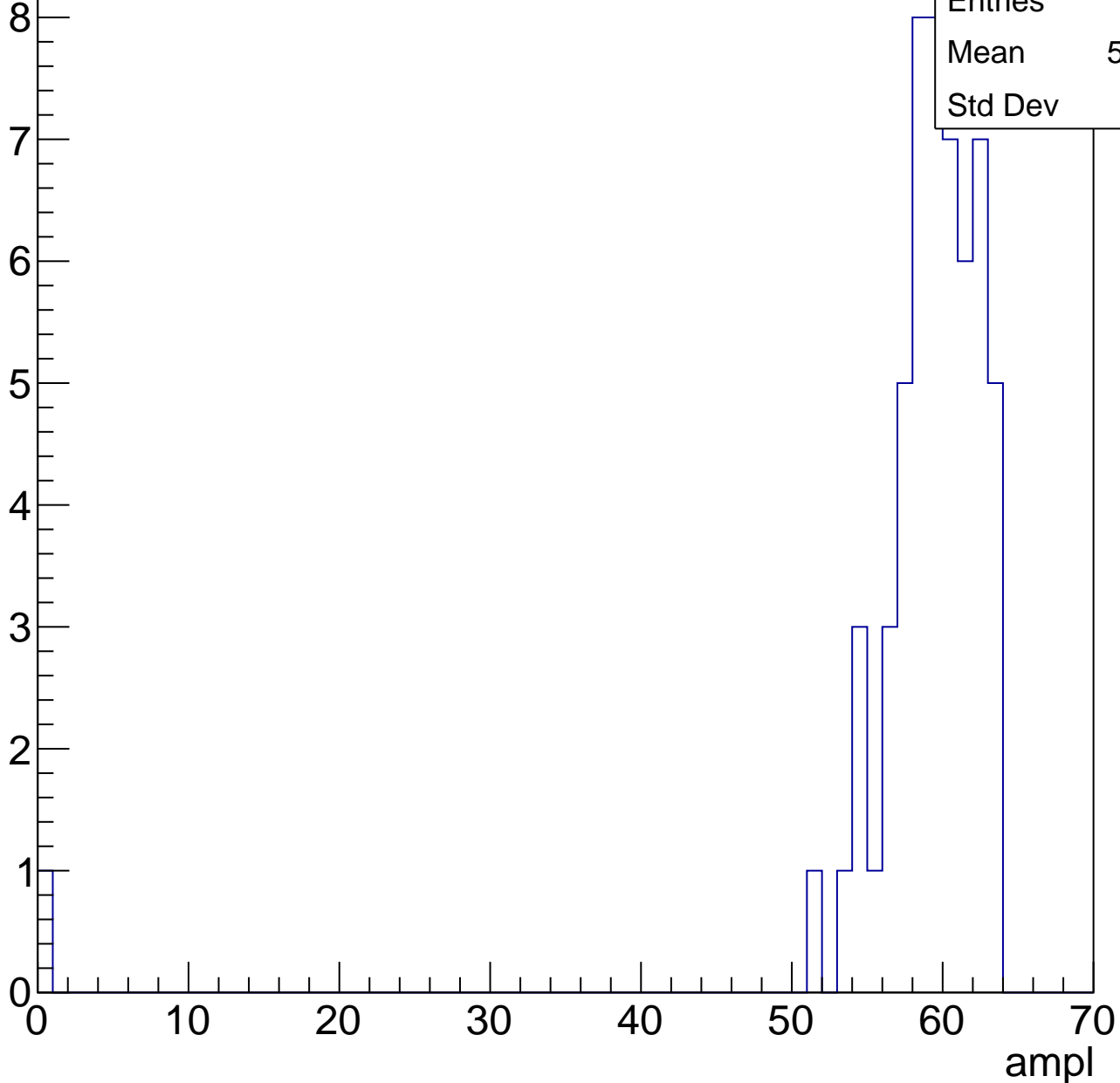


# B1L000S, U21-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

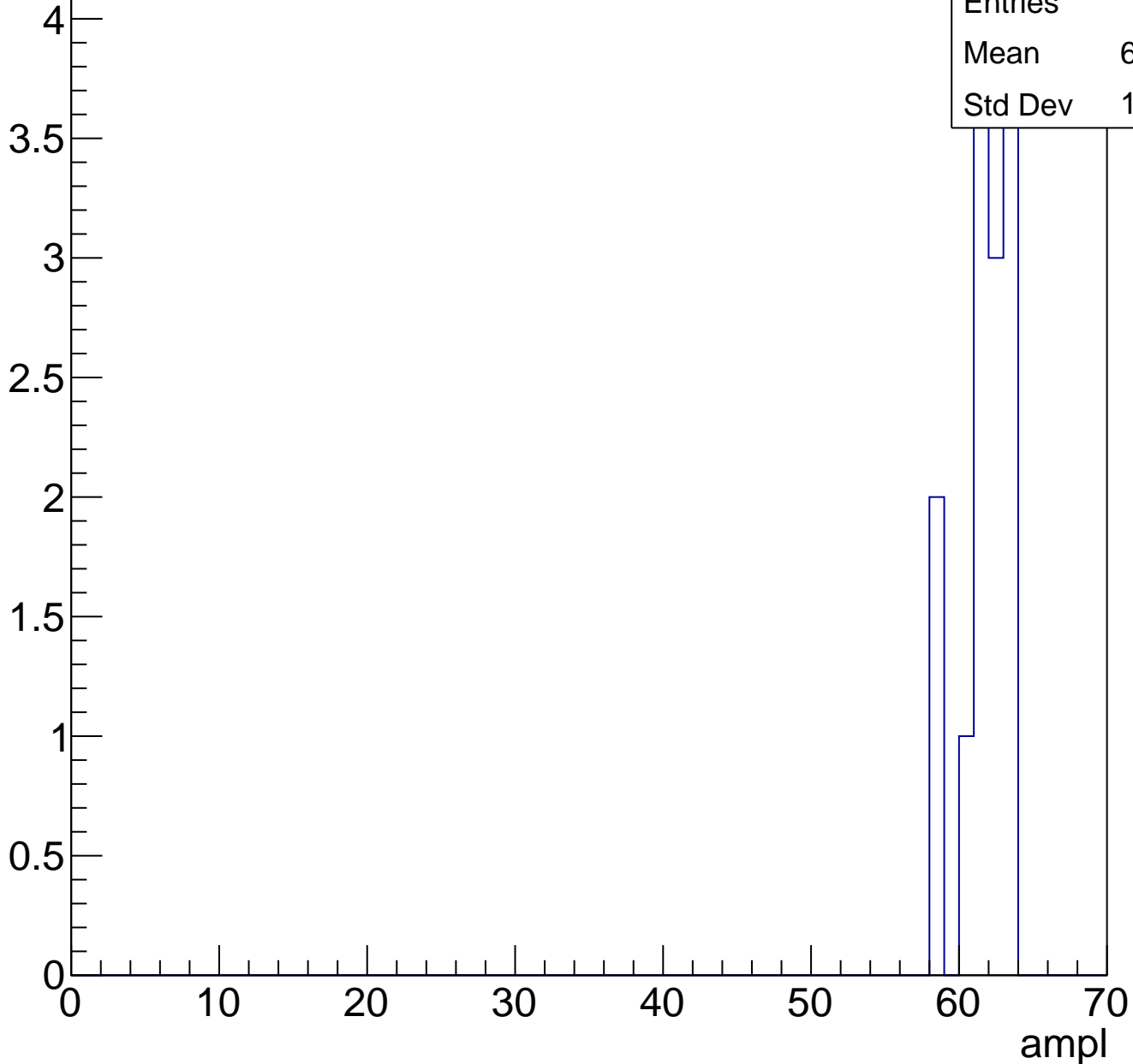
Entries	56
Mean	57.95
Std Dev	8.28



# B1L000S, U21-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U21-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U21-ch122, adc0

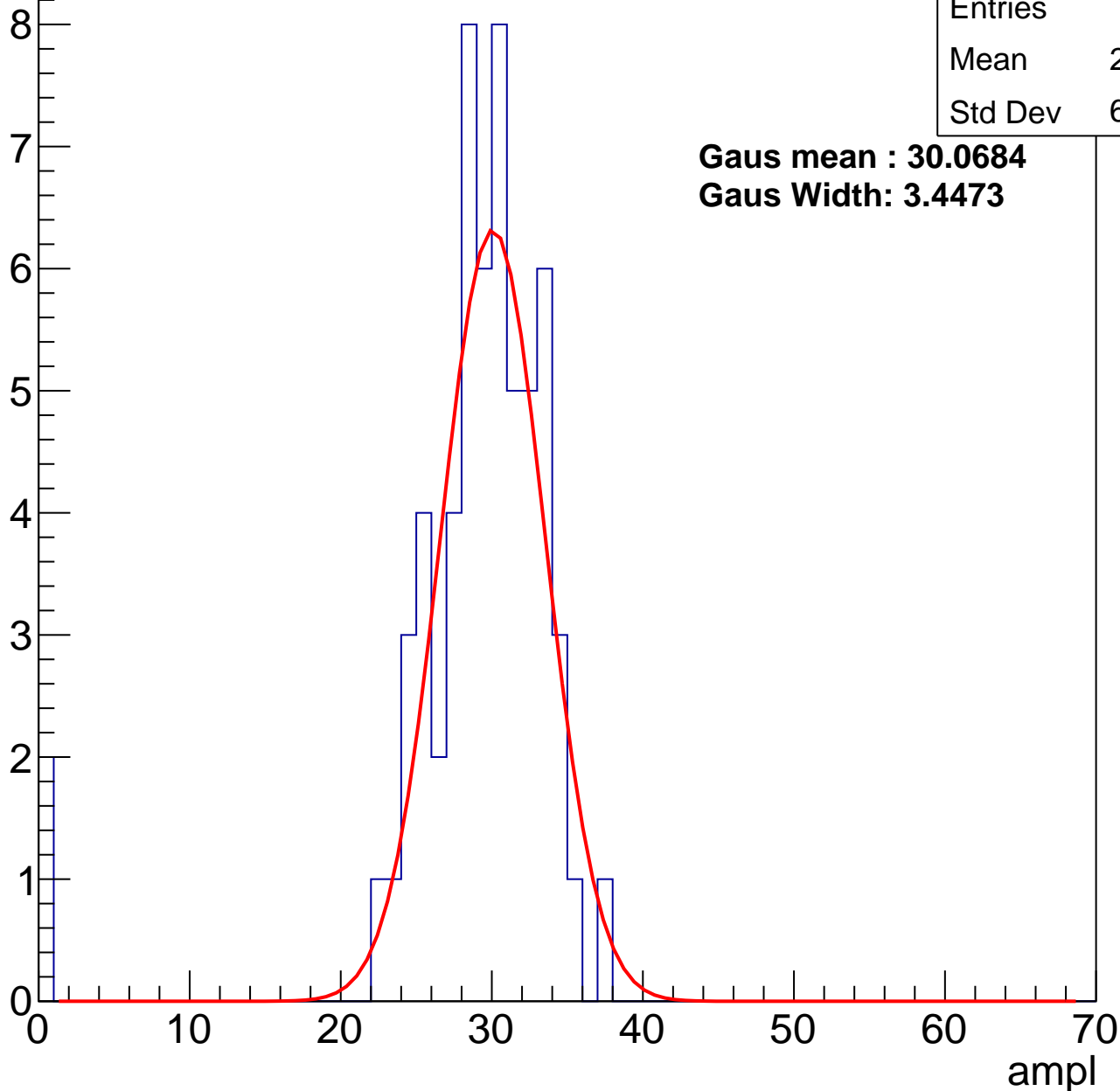
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	28.37
Std Dev	6.148

**Gaus mean : 30.0684**

**Gaus Width: 3.4473**



# B1L000S, U21-ch122, adc1

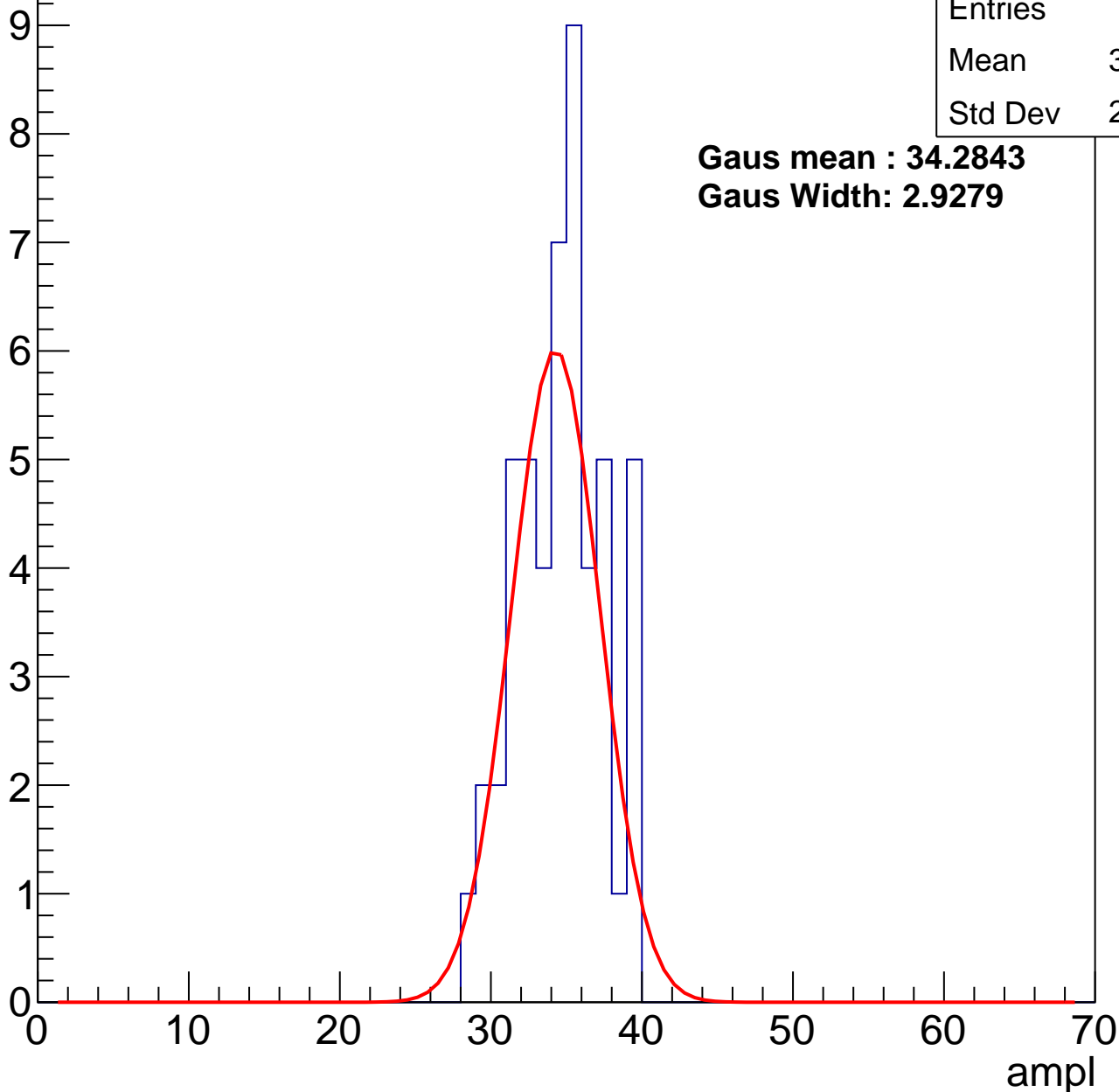
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	34.16
Std Dev	2.817

**Gaus mean : 34.2843**

**Gaus Width: 2.9279**



# B1L000S, U21-ch122, adc2

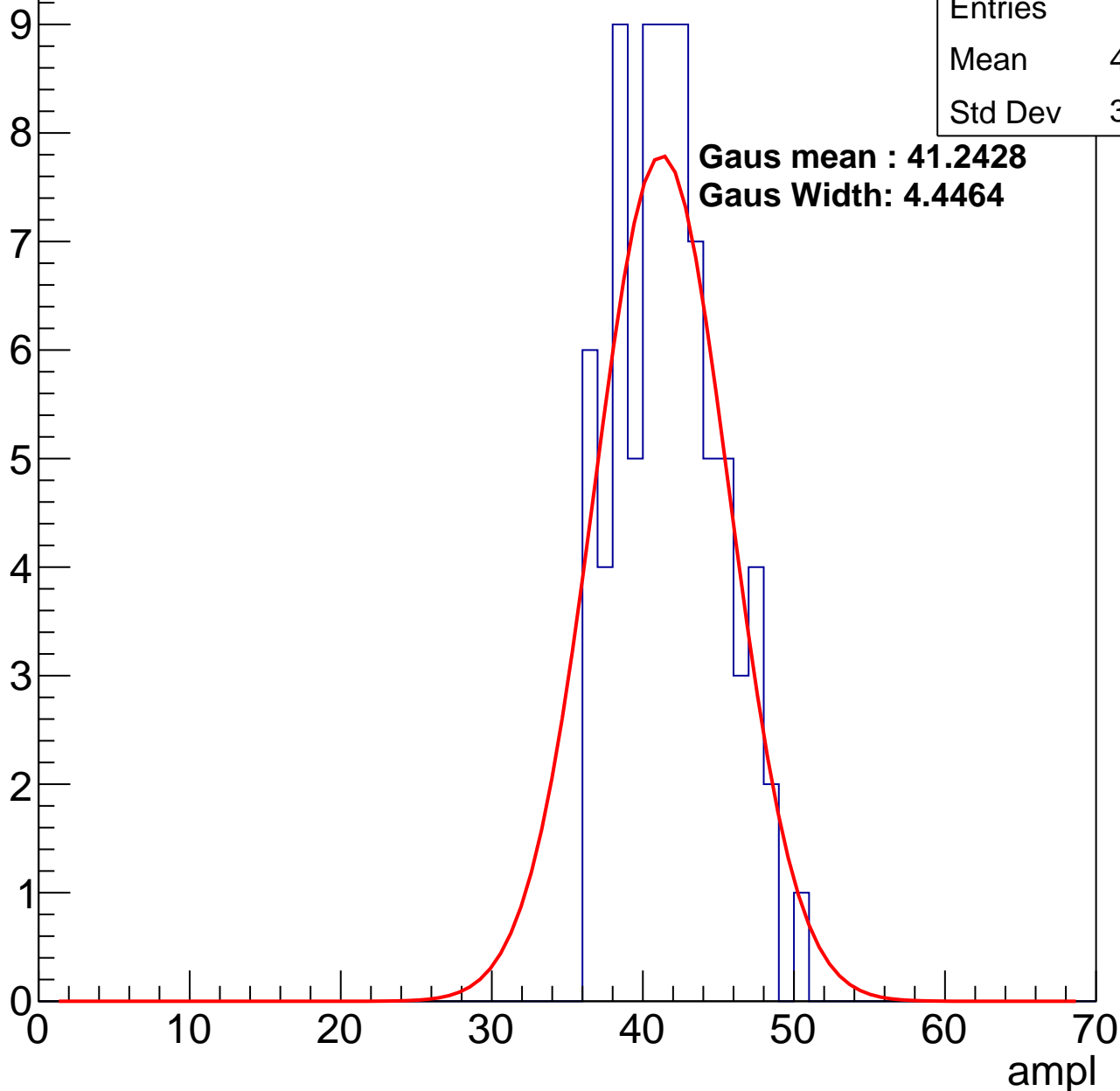
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	41.36
Std Dev	3.347

**Gaus mean : 41.2428**

**Gaus Width: 4.4464**

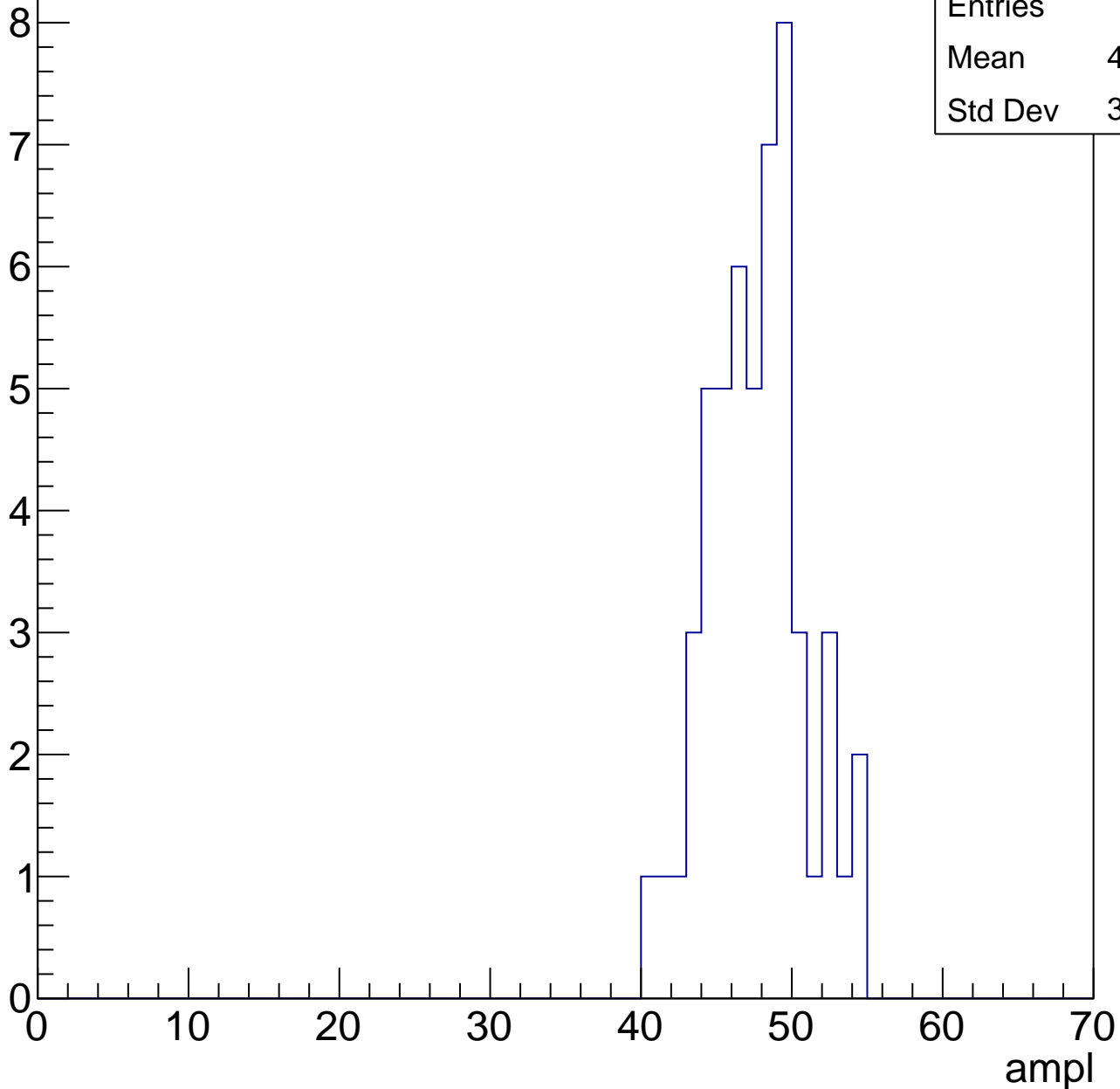


# B1L000S, U21-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

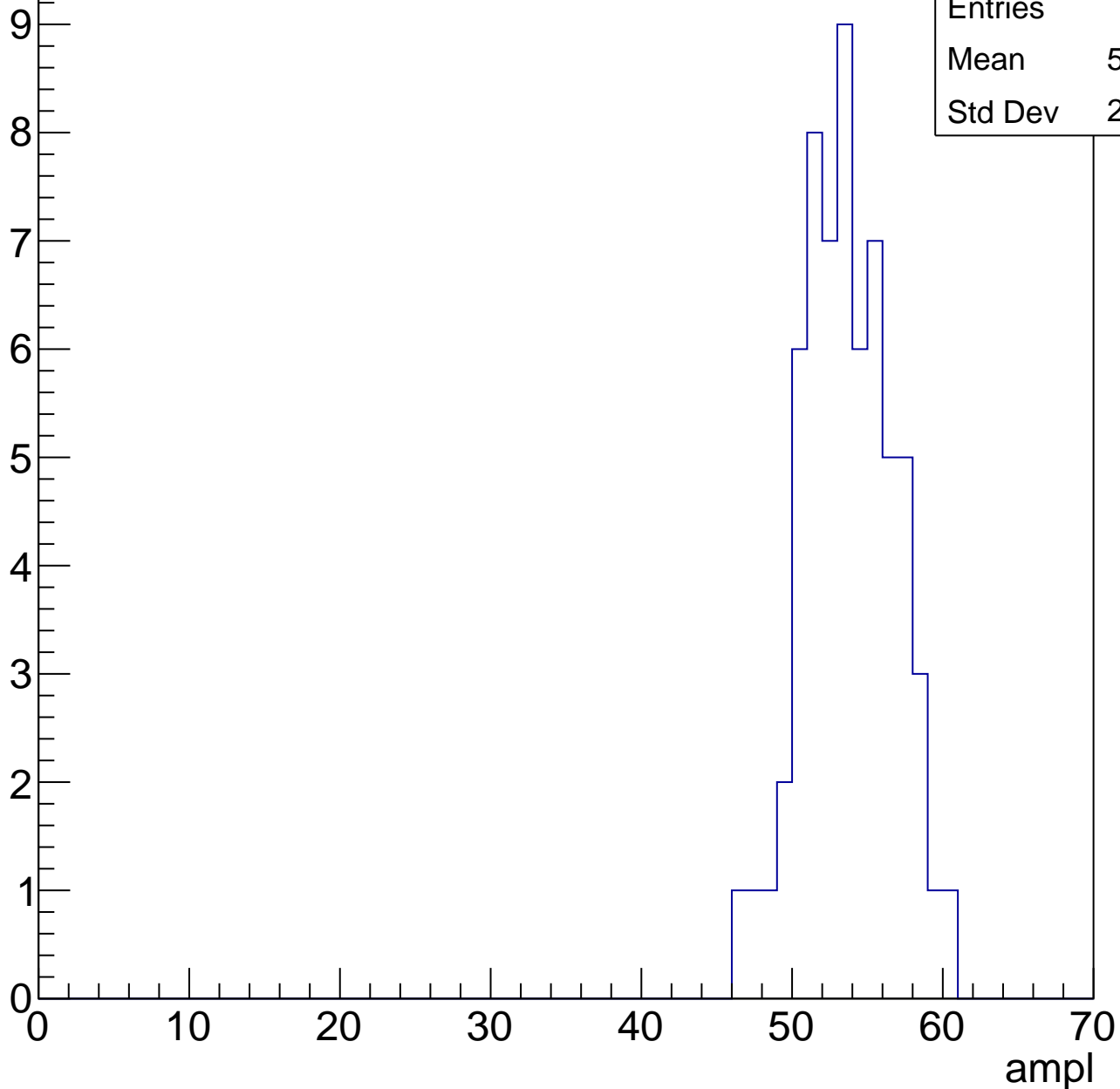
Entries	52
Mean	47.19
Std Dev	3.156



# B1L000S, U21-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

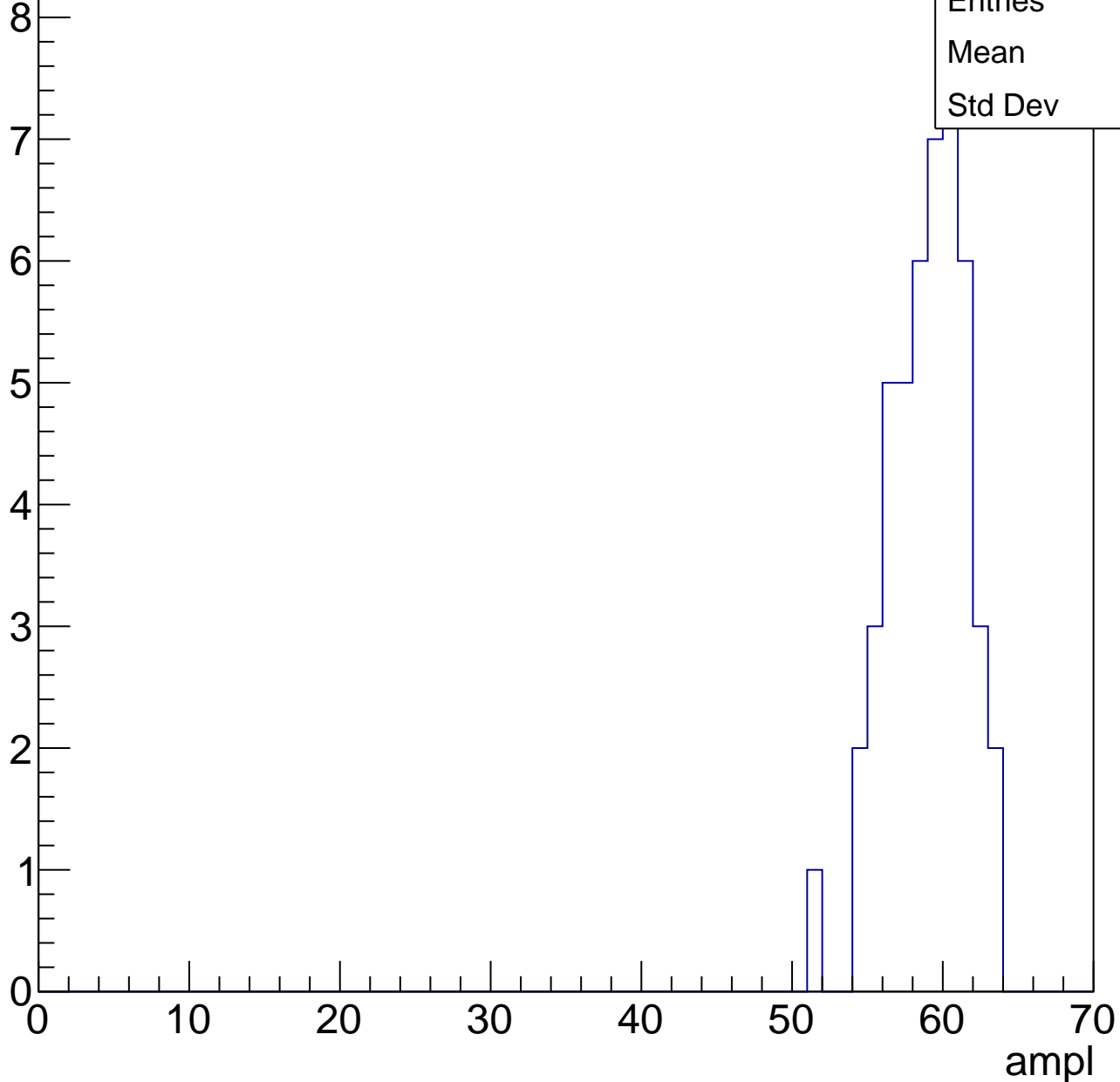


# B1L000S, U21-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	58.5
Std Dev	2.55

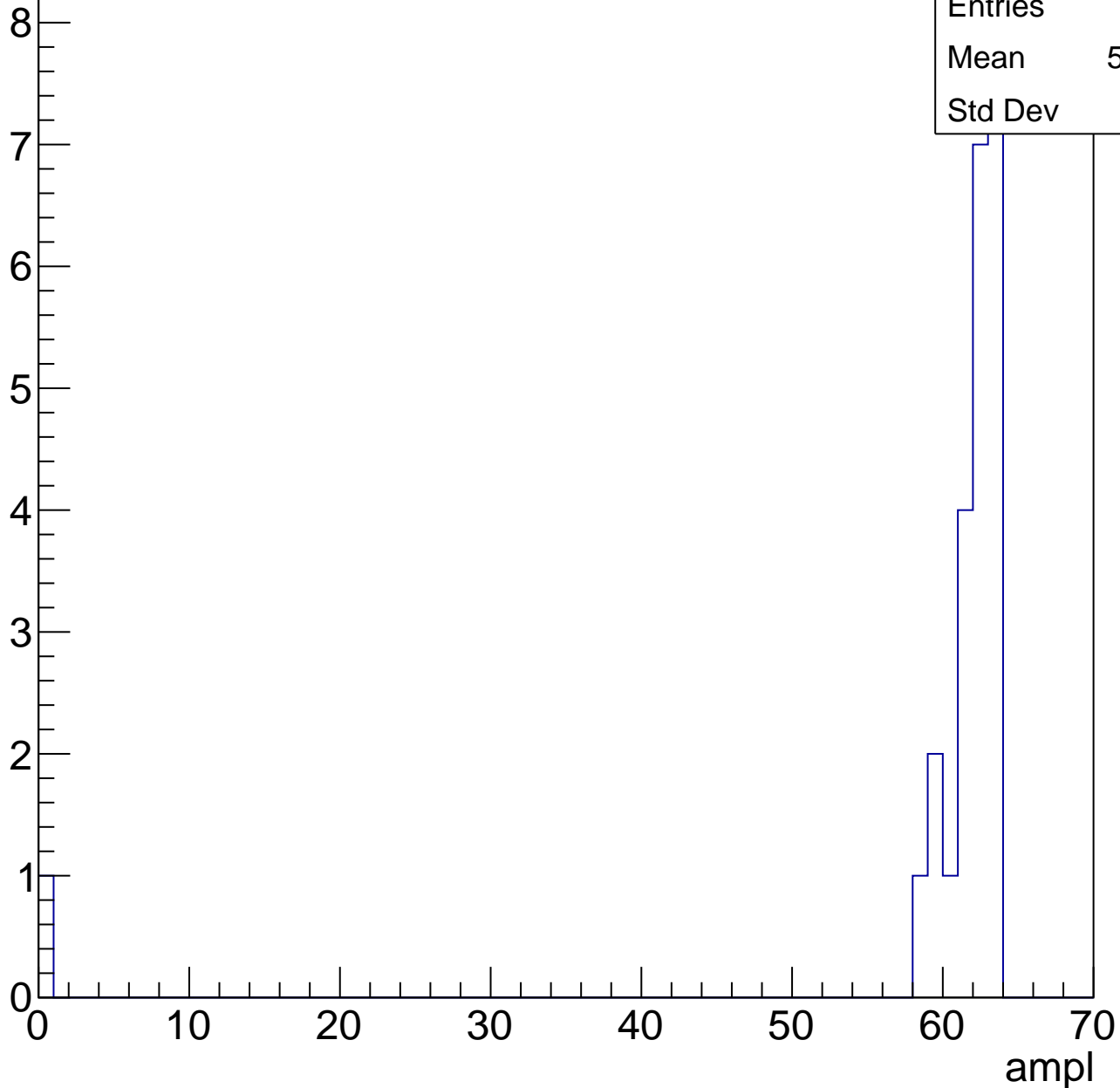


# B1L000S, U21-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	59.08
Std Dev	12.4





# B1L000S, U21-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U21-ch123, adc0

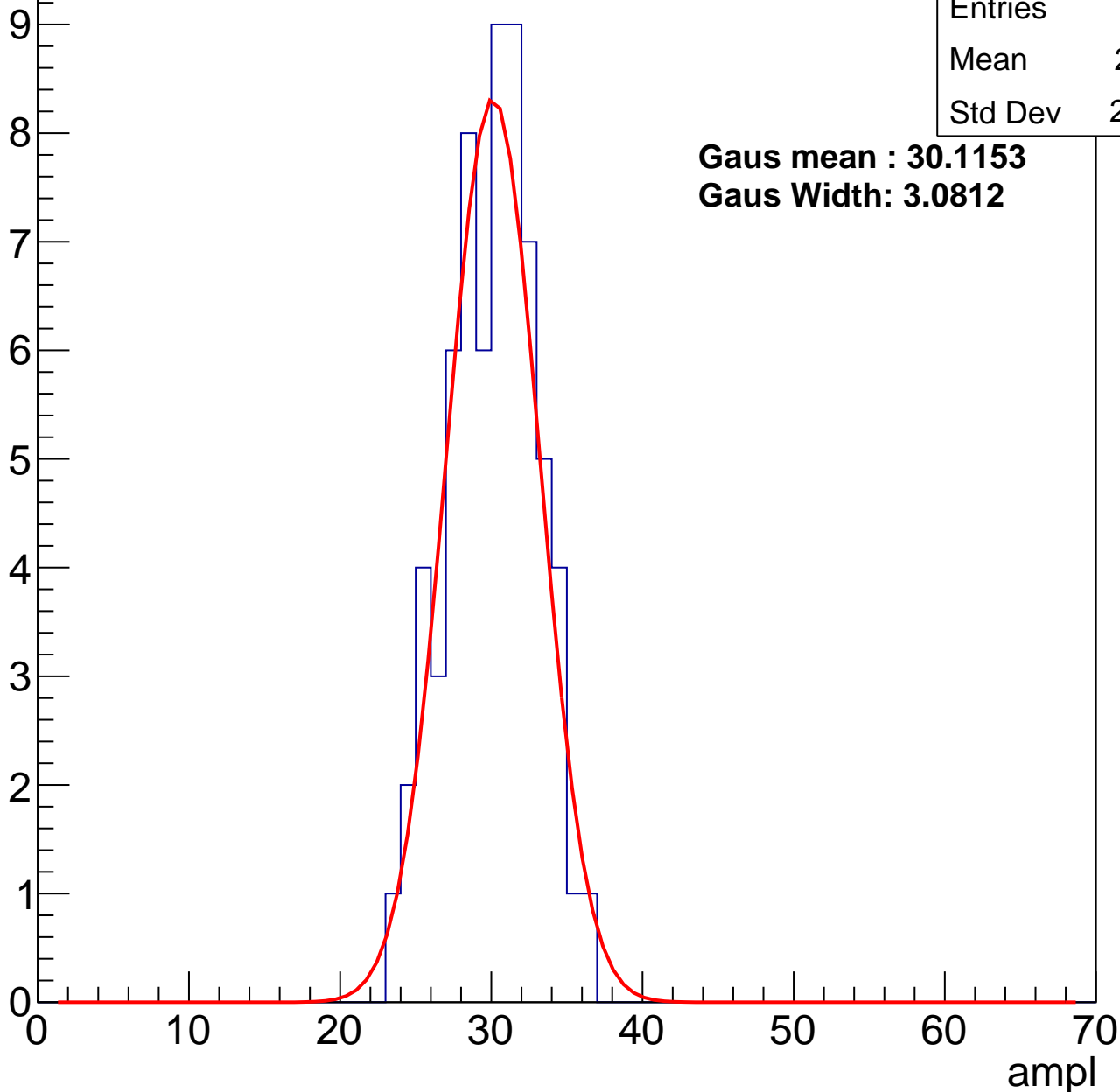
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	29.61
Std Dev	2.907

**Gaus mean : 30.1153**

**Gaus Width: 3.0812**



# B1L000S, U21-ch123, adc1

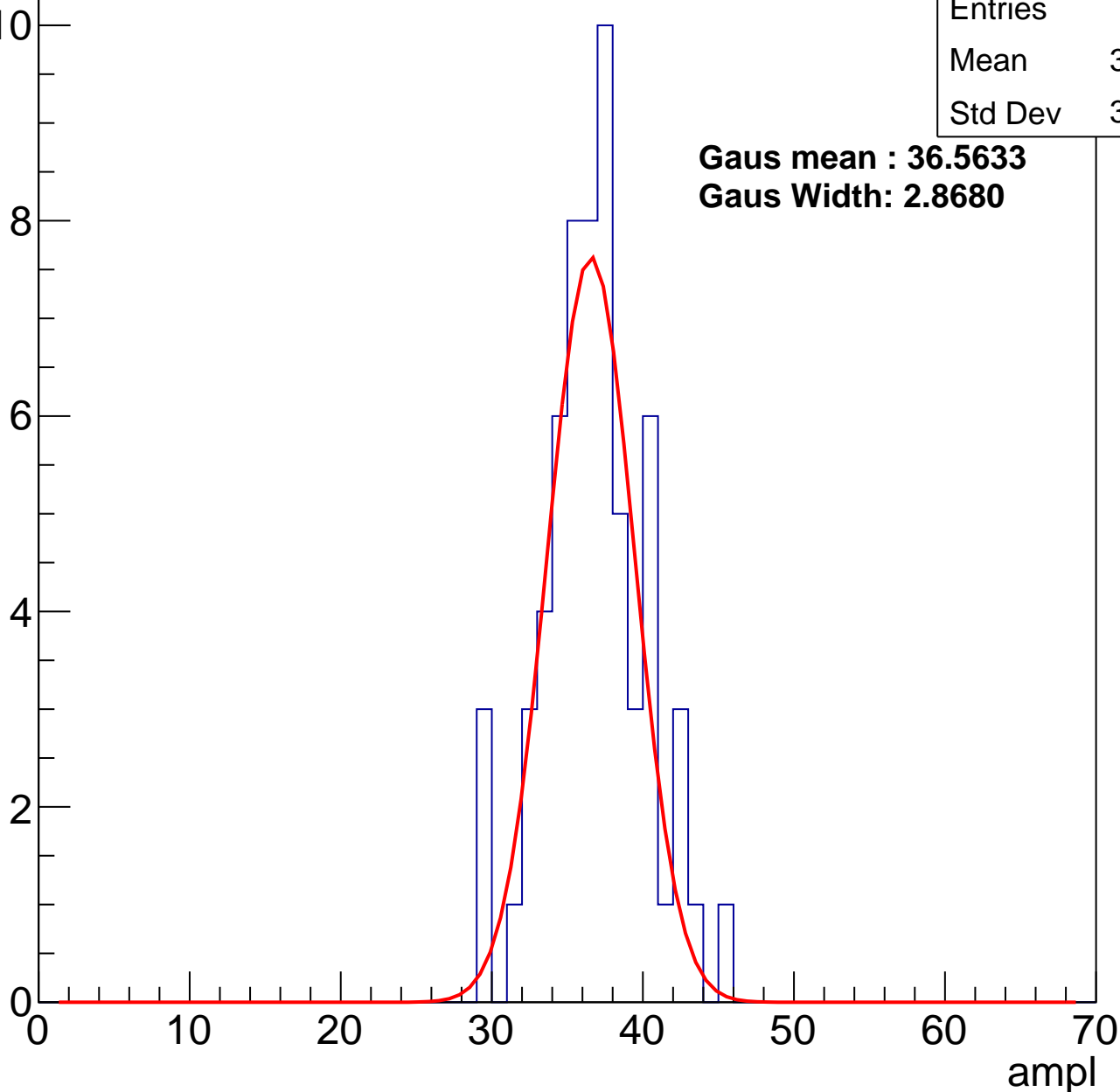
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	36.35
Std Dev	3.325

**Gaus mean : 36.5633**

**Gaus Width: 2.8680**



# B1L000S, U21-ch123, adc2

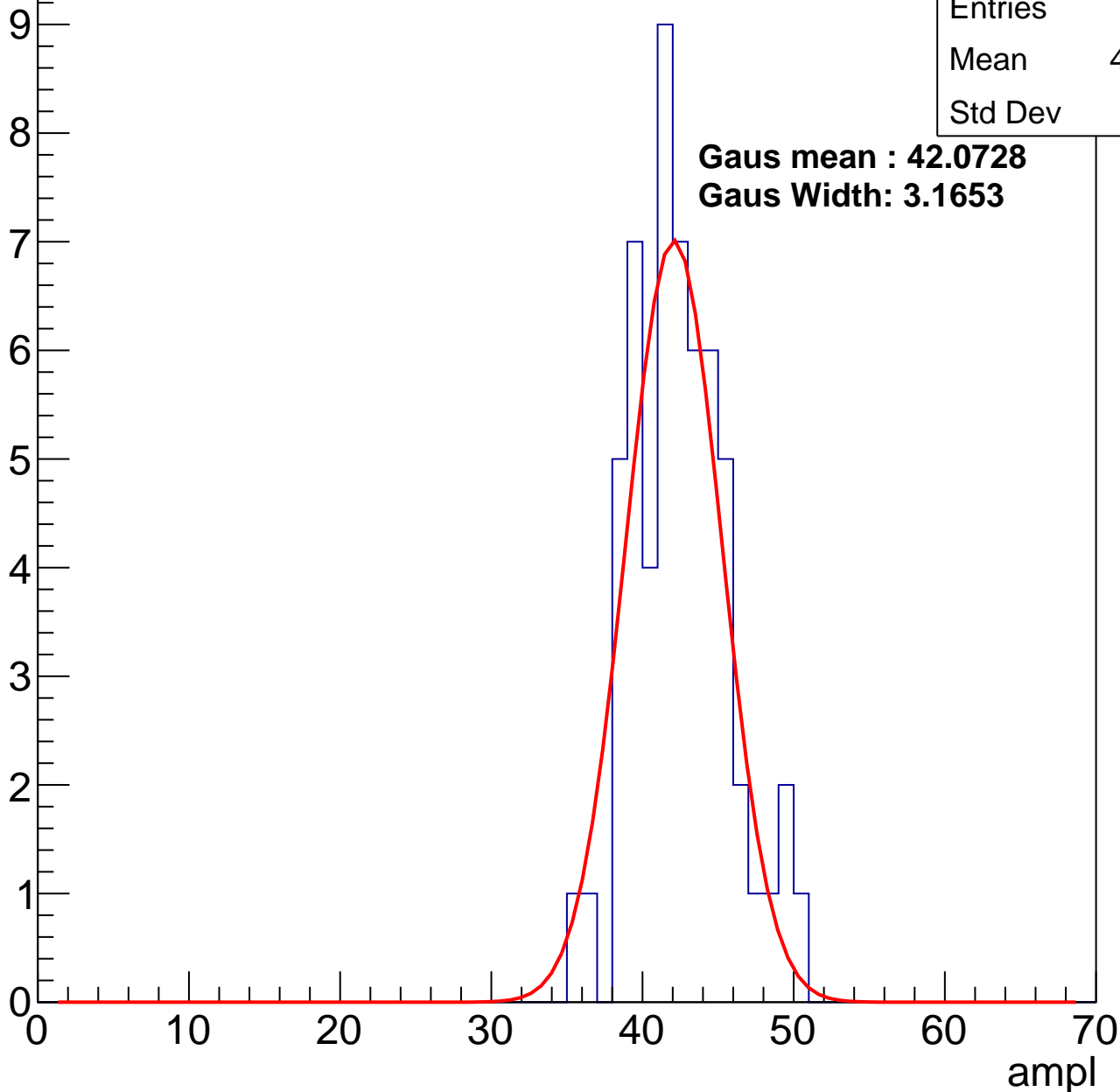
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	42.05
Std Dev	3.17

**Gaus mean : 42.0728**

**Gaus Width: 3.1653**

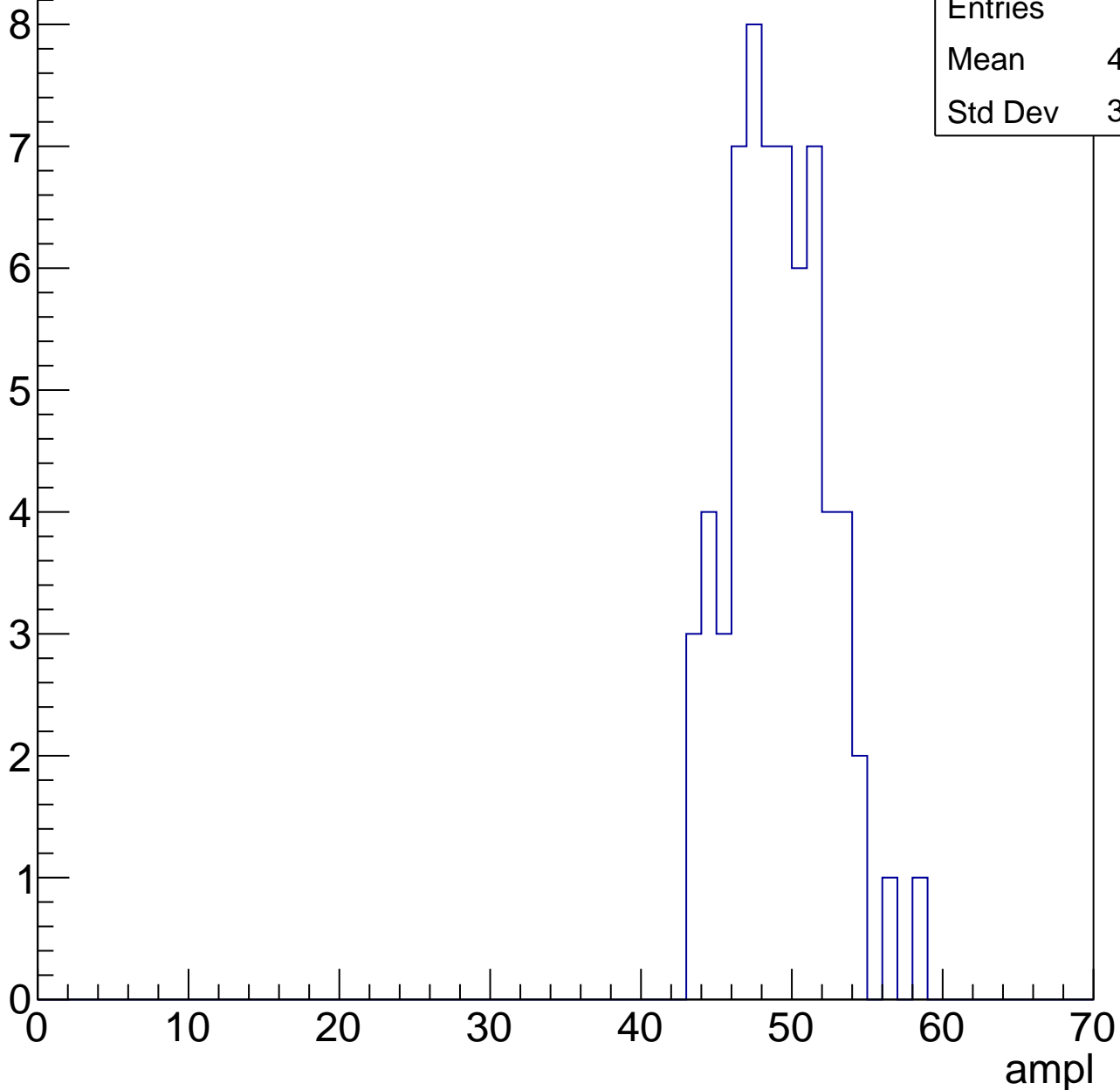


# B1L000S, U21-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.69
Std Dev	3.225

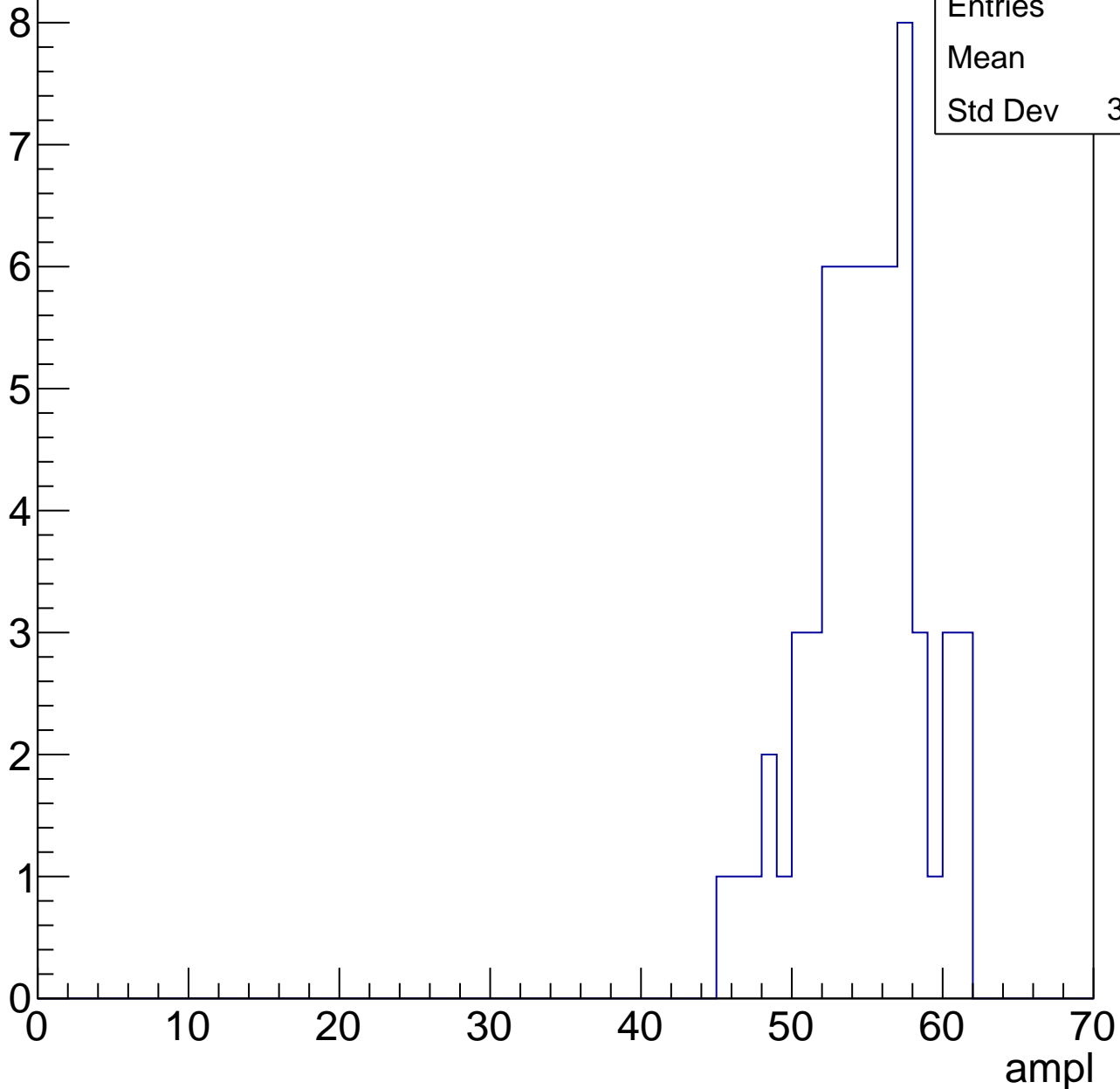


# B1L000S, U21-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

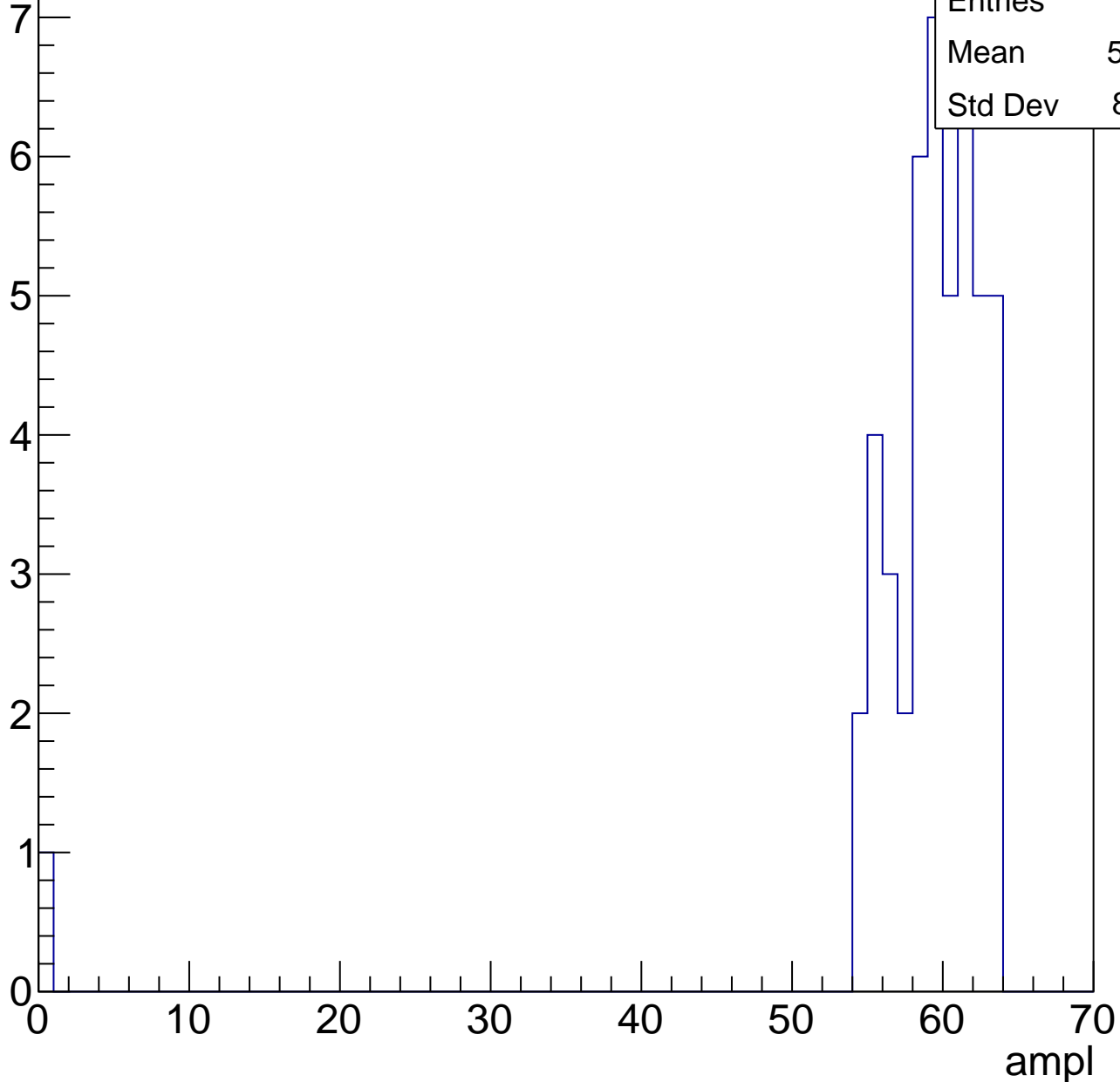
Entries	60
Mean	54.3
Std Dev	3.698



# B1L000S, U21-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

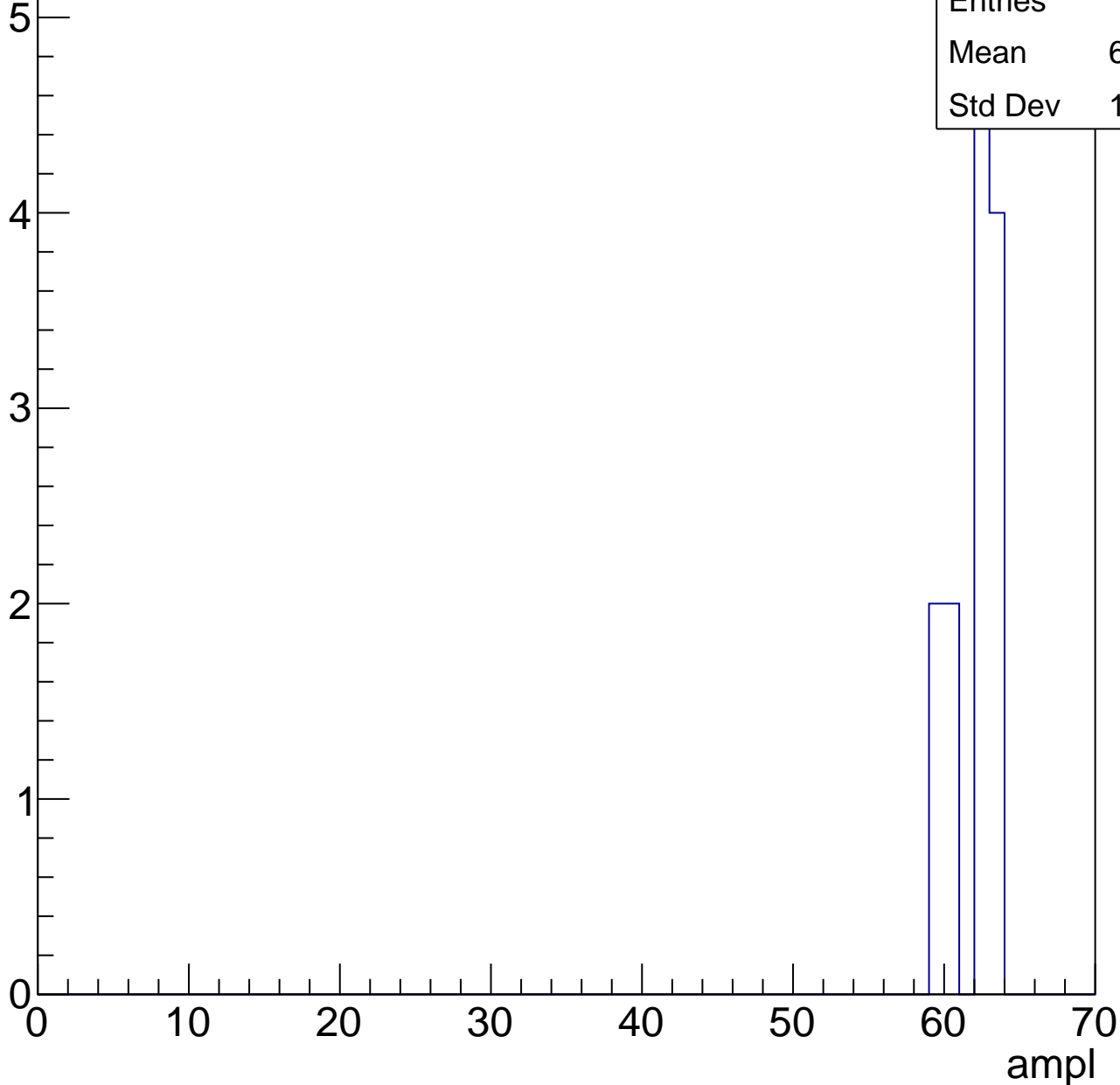


# B1L000S, U21-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	61.54
Std Dev	1.447

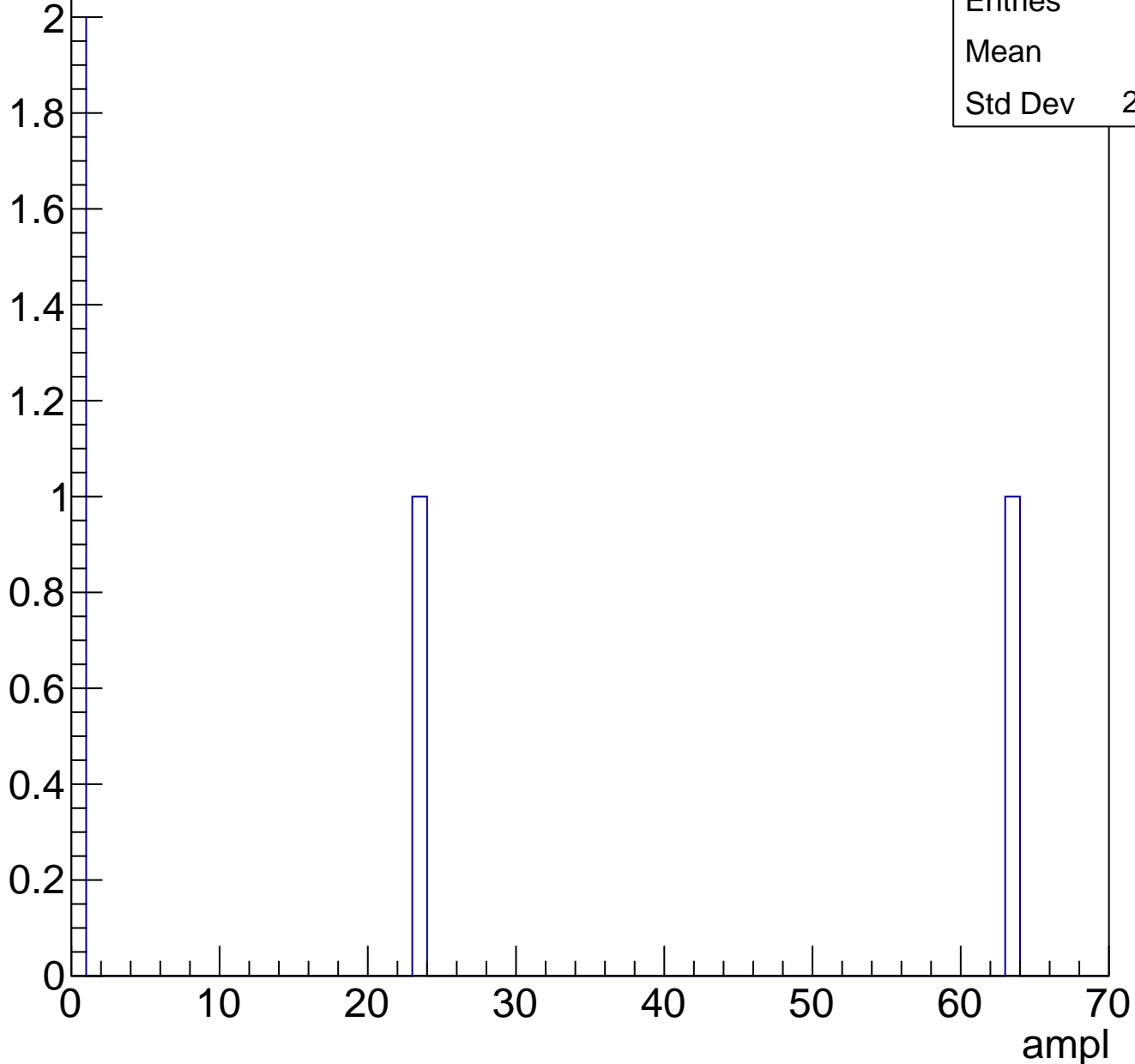




# B1L000S, U21-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	21.5
Std Dev	25.73

# B1L000S, U21-ch124, adc0

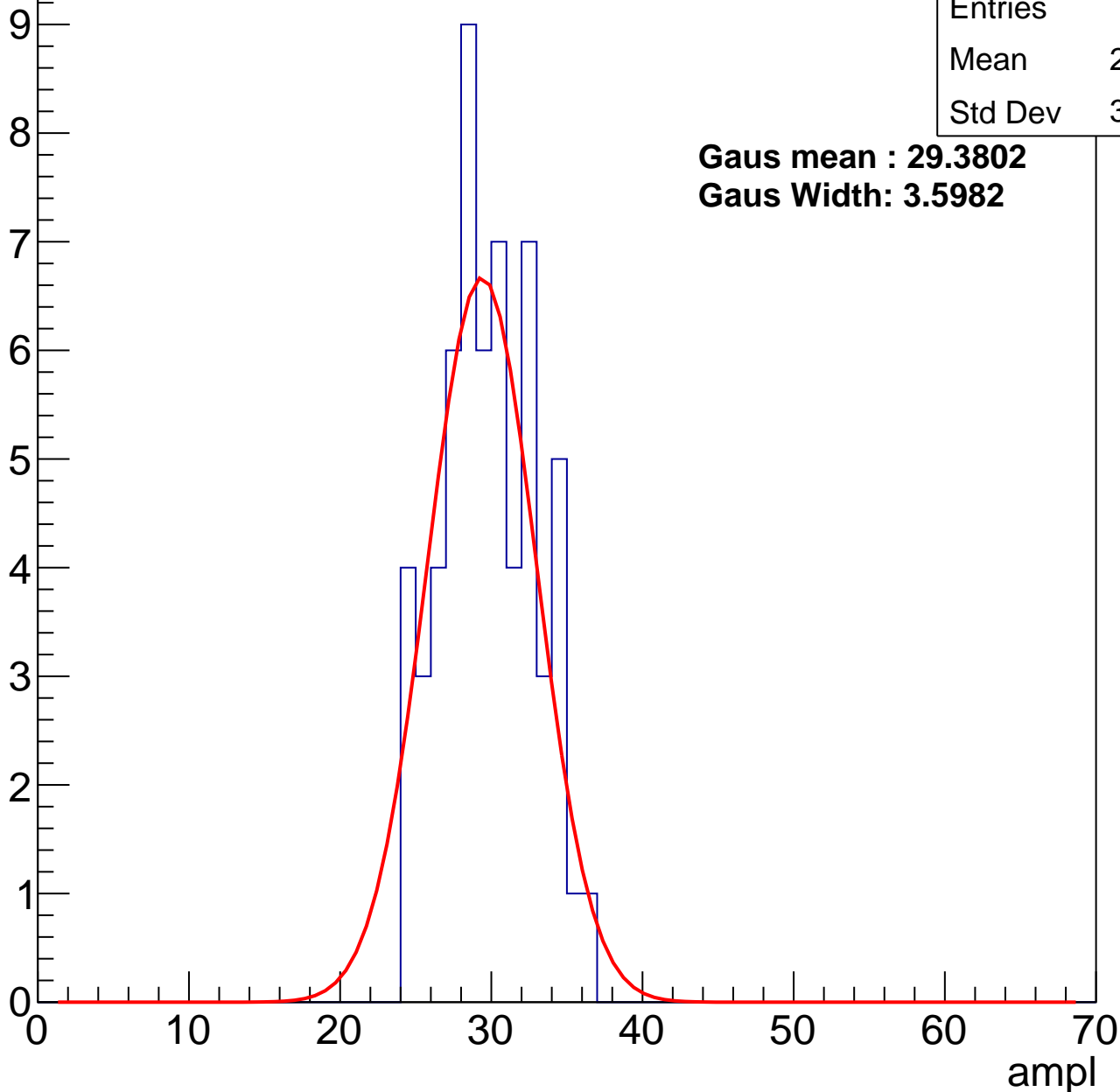
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	29.35
Std Dev	3.038

**Gaus mean : 29.3802**

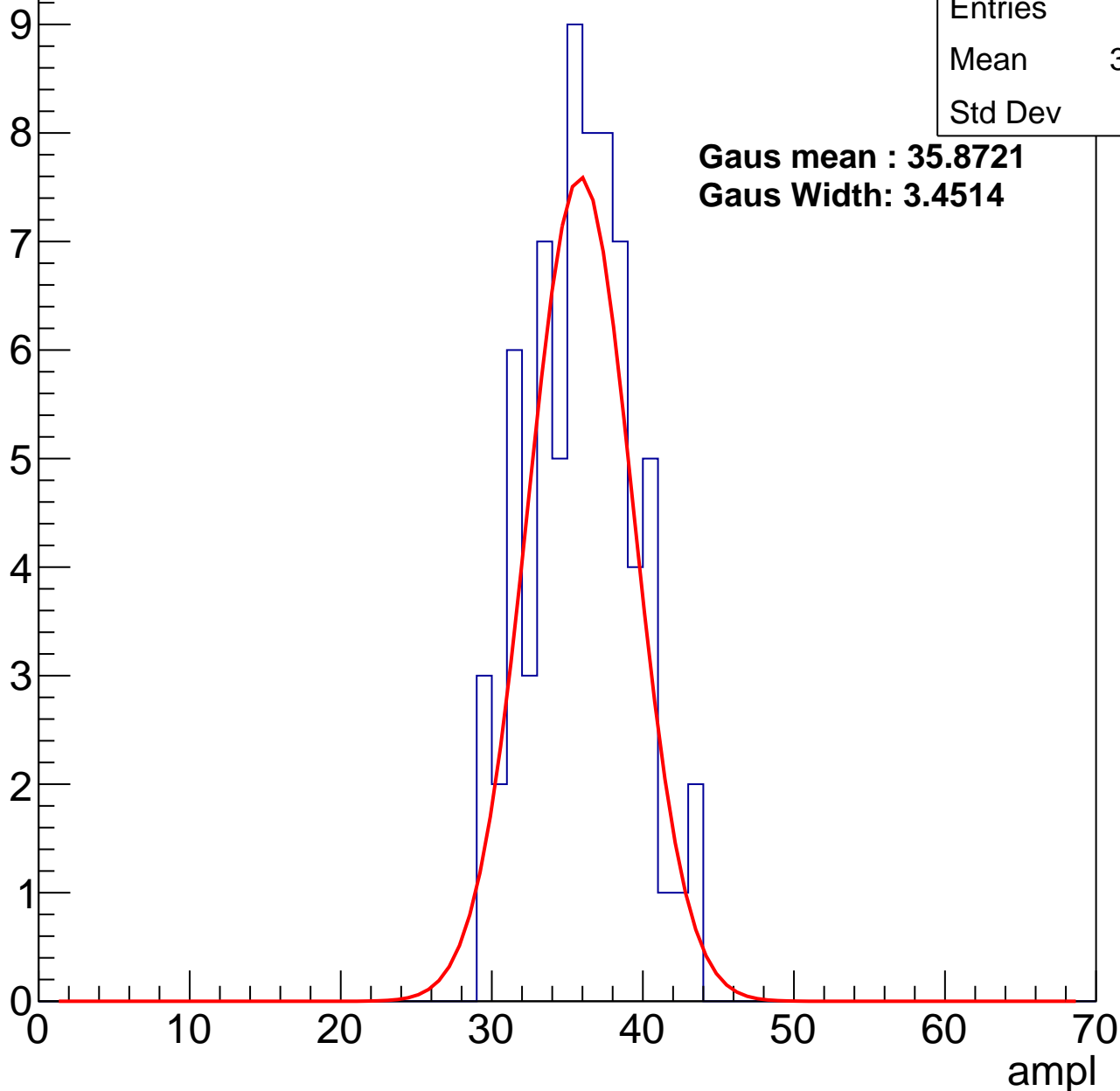
**Gaus Width: 3.5982**



# B1L000S, U21-ch124, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch124, adc2

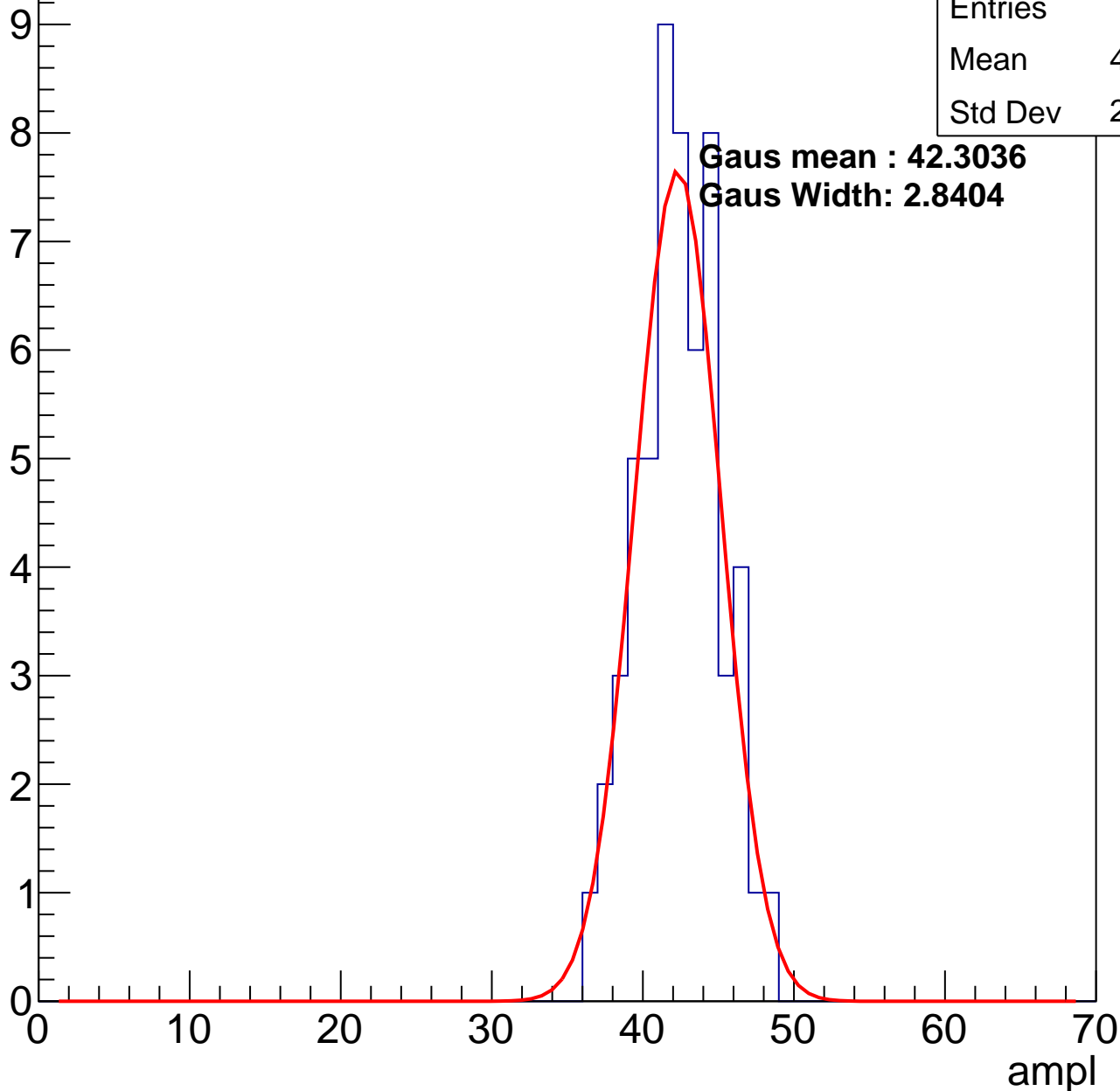
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.93
Std Dev	2.665

**Gaus mean : 42.3036**

**Gaus Width: 2.8404**

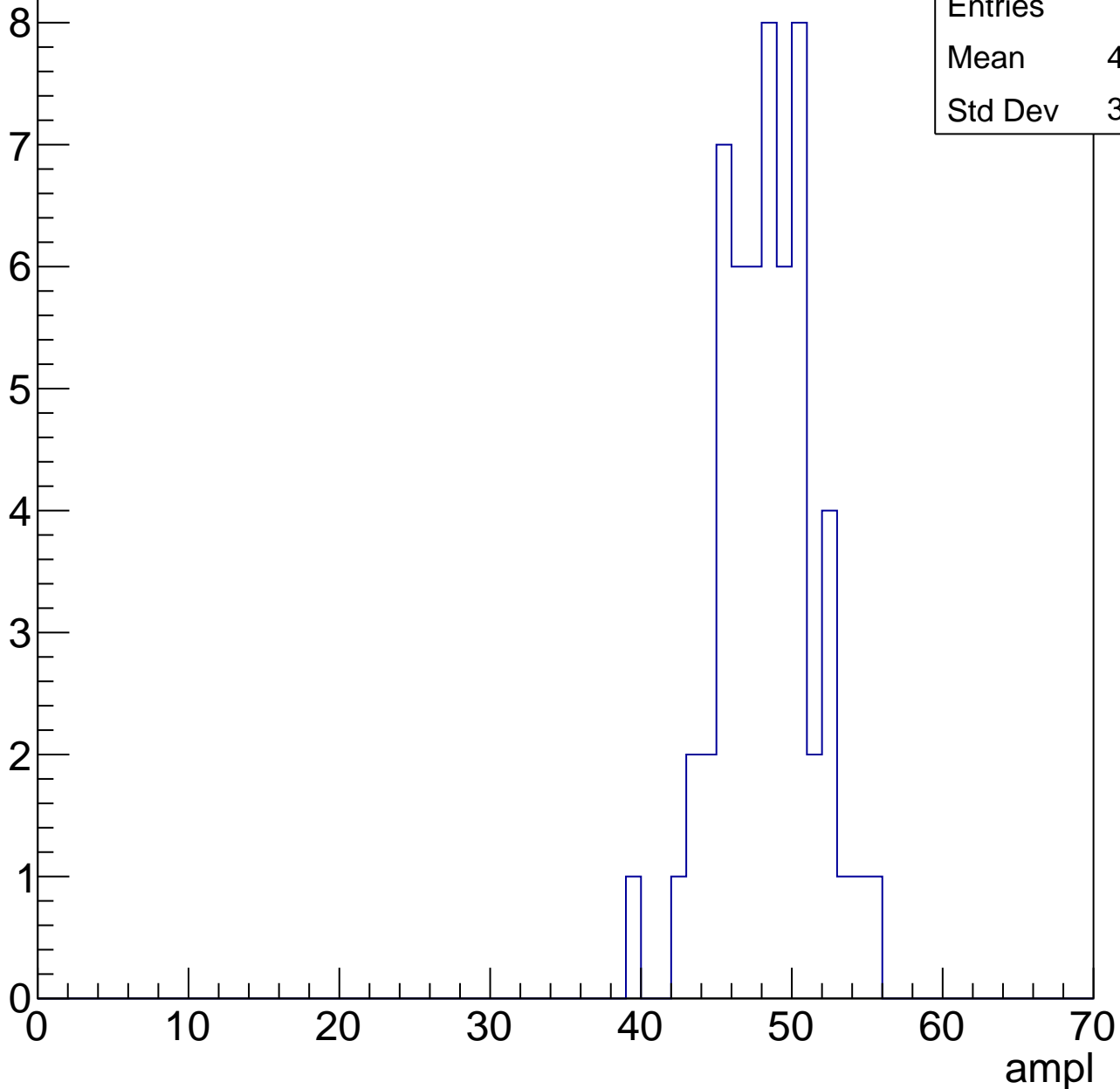


# B1L000S, U21-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	47.82
Std Dev	3.048

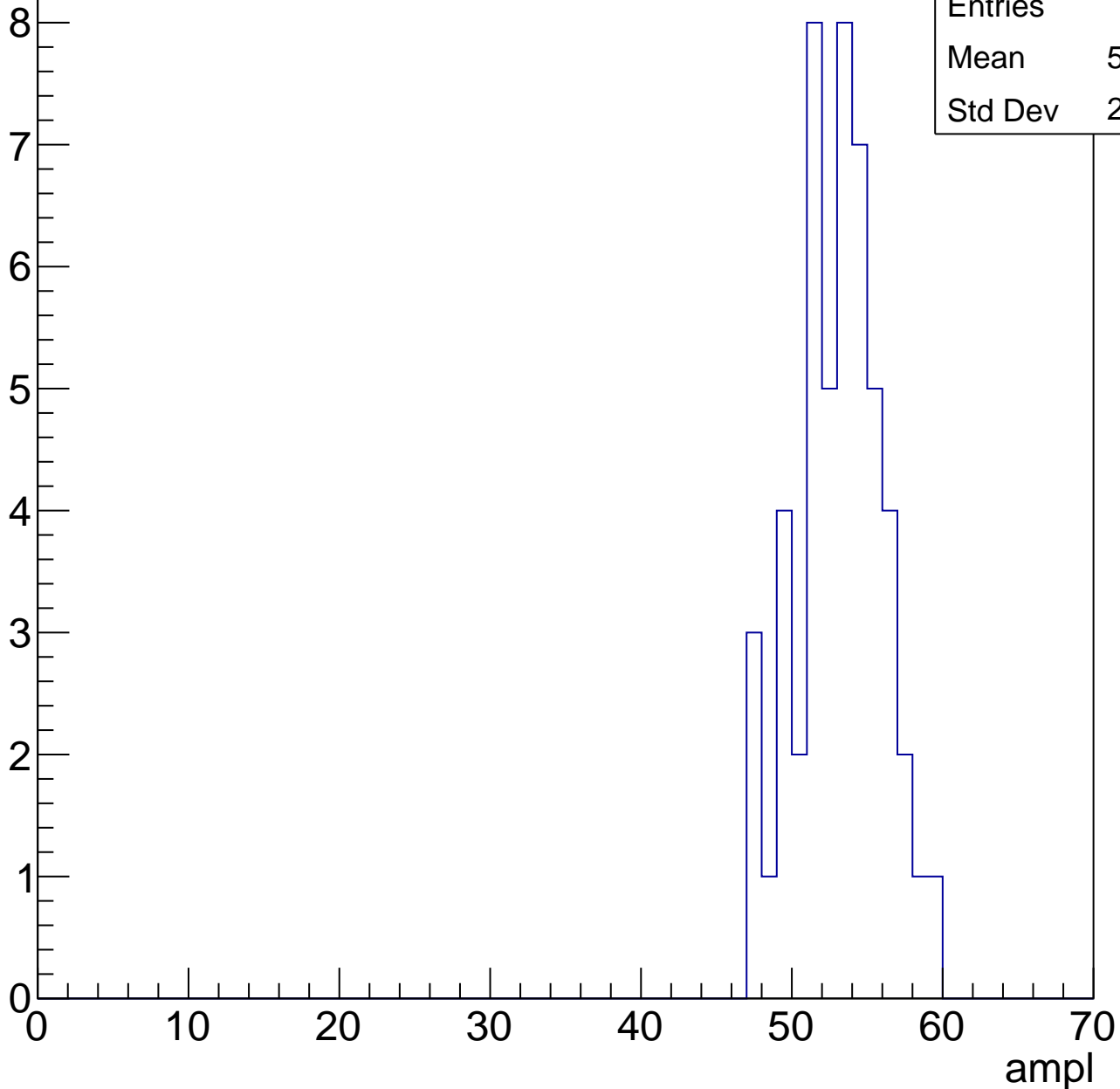


# B1L000S, U21-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	52.65
Std Dev	2.806

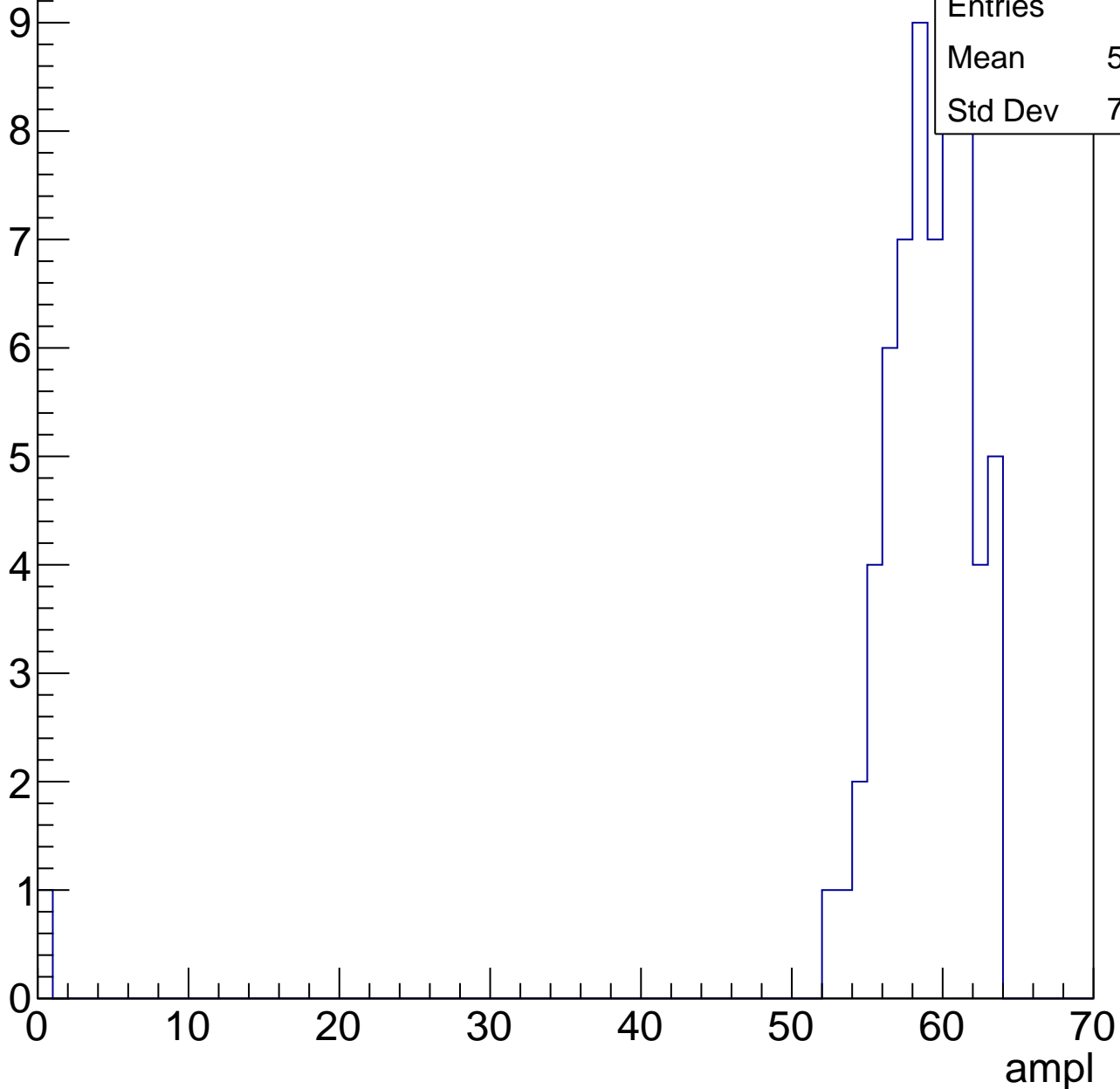


# B1L000S, U21-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	57.72
Std Dev	7.729

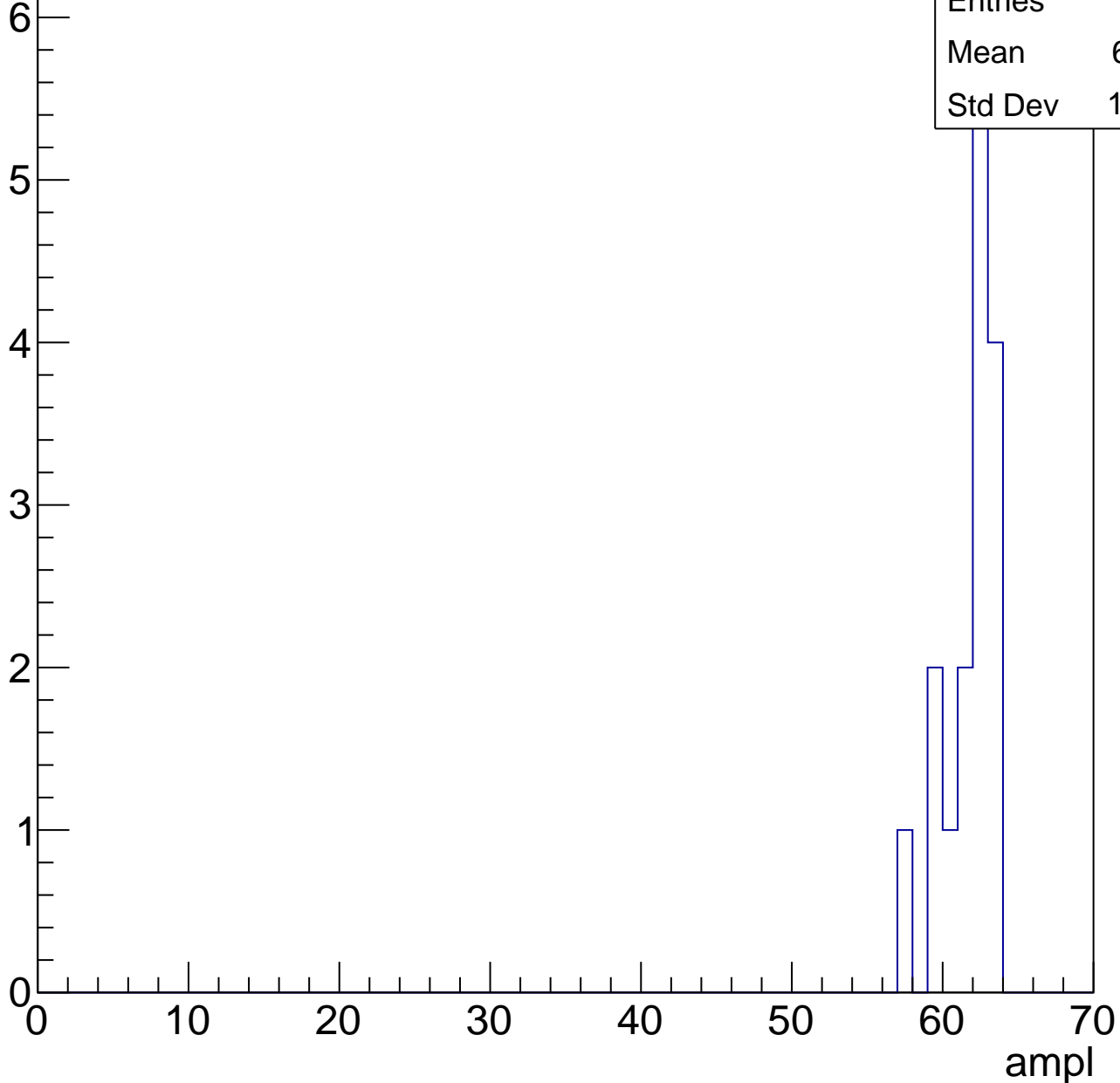


# B1L000S, U21-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.31
Std Dev	1.685

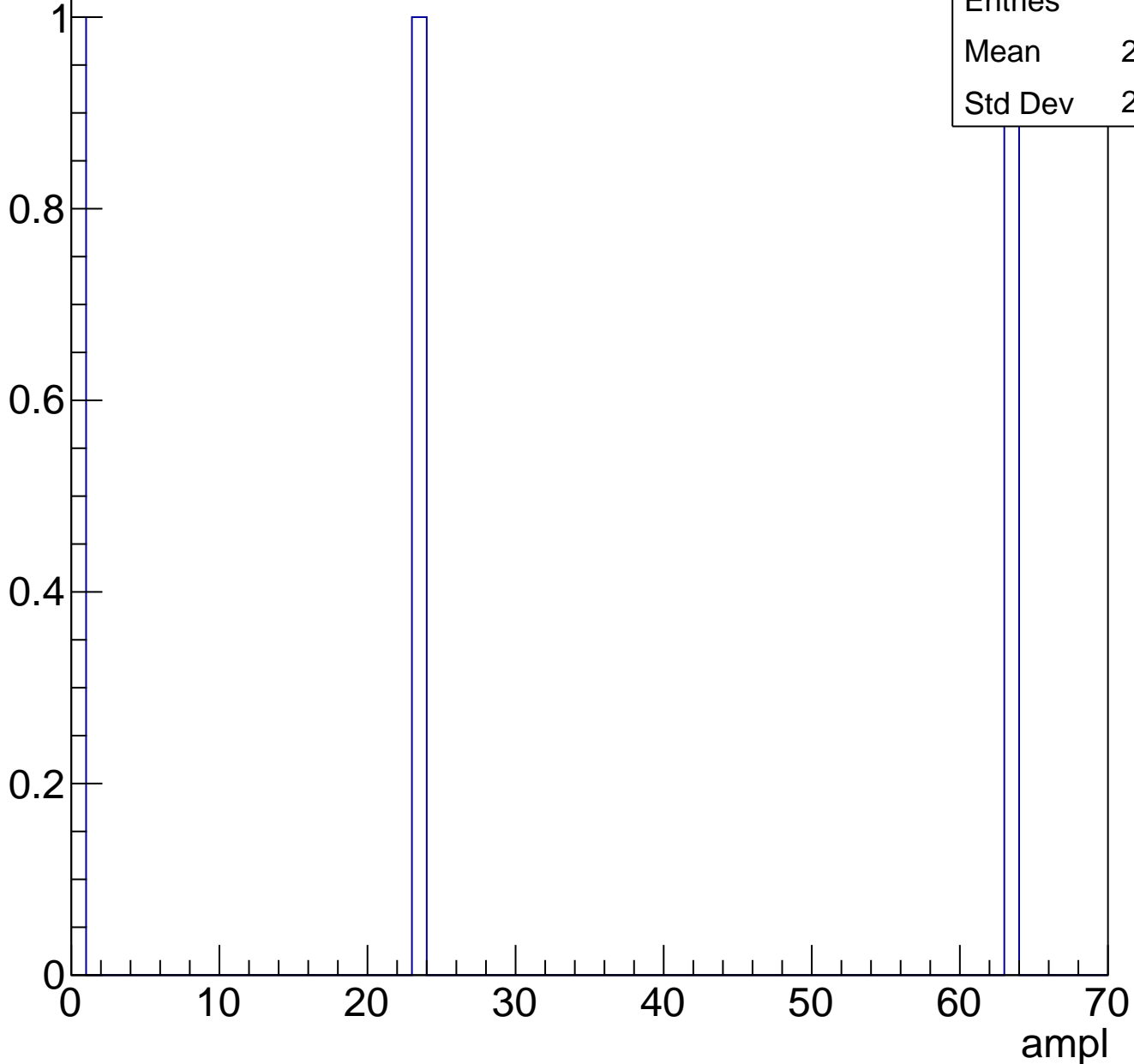




# B1L000S, U21-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.67
Std Dev	26.03

# B1L000S, U21-ch125, adc0

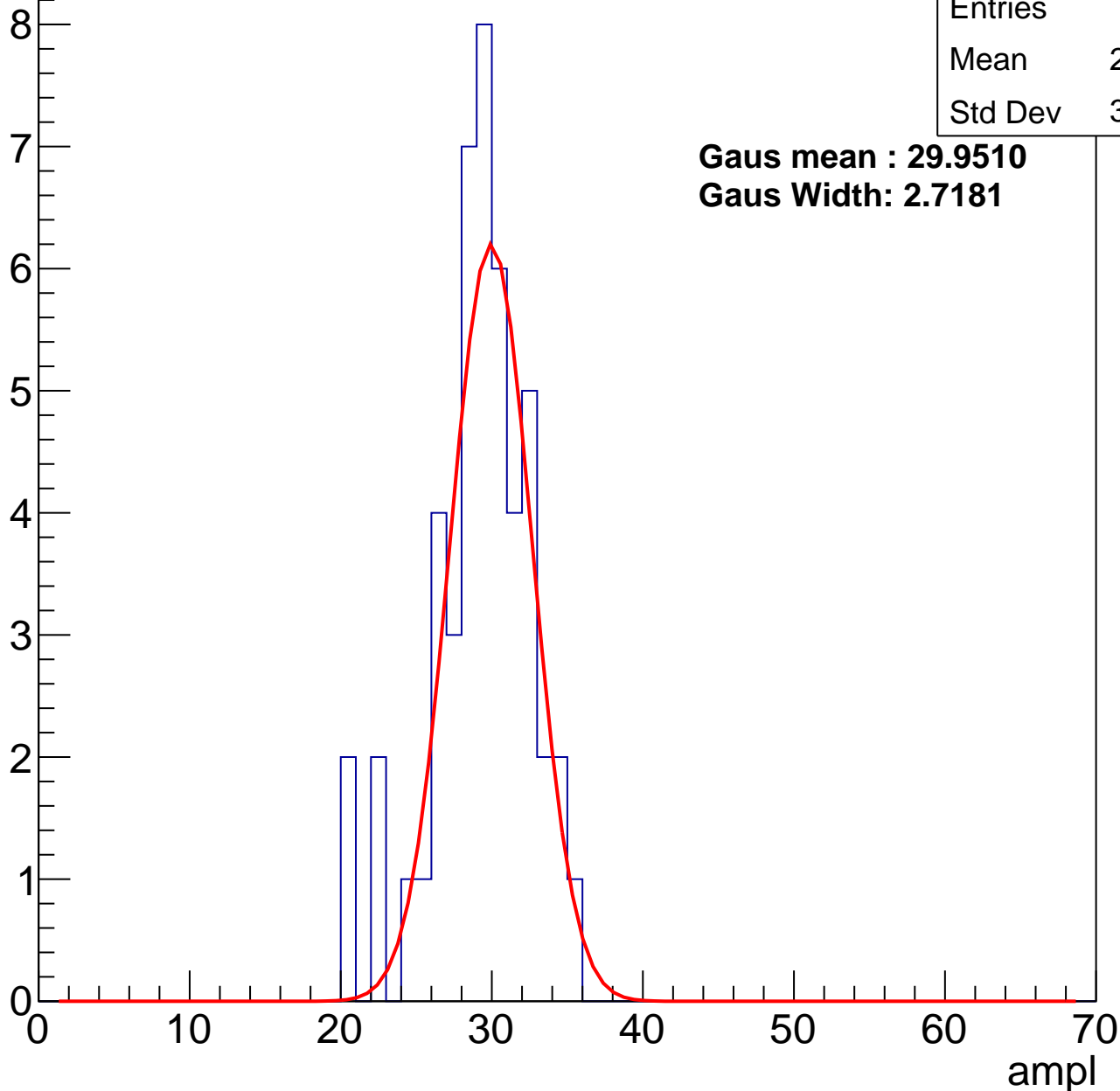
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	28.73
Std Dev	3.346

**Gaus mean : 29.9510**

**Gaus Width: 2.7181**



# B1L000S, U21-ch125, adc1

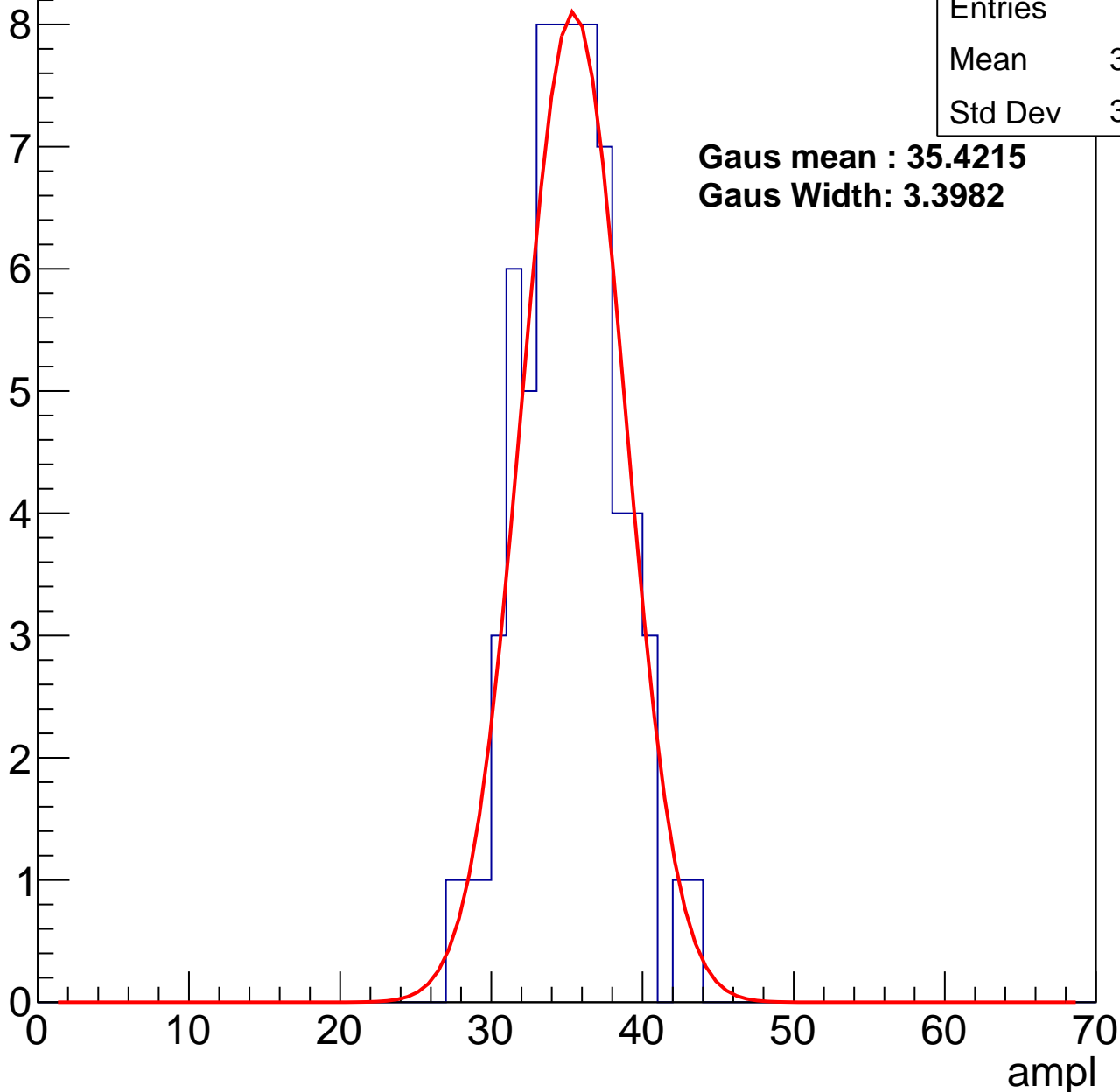
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.72
Std Dev	3.239

**Gaus mean : 35.4215**

**Gaus Width: 3.3982**



# B1L000S, U21-ch125, adc2

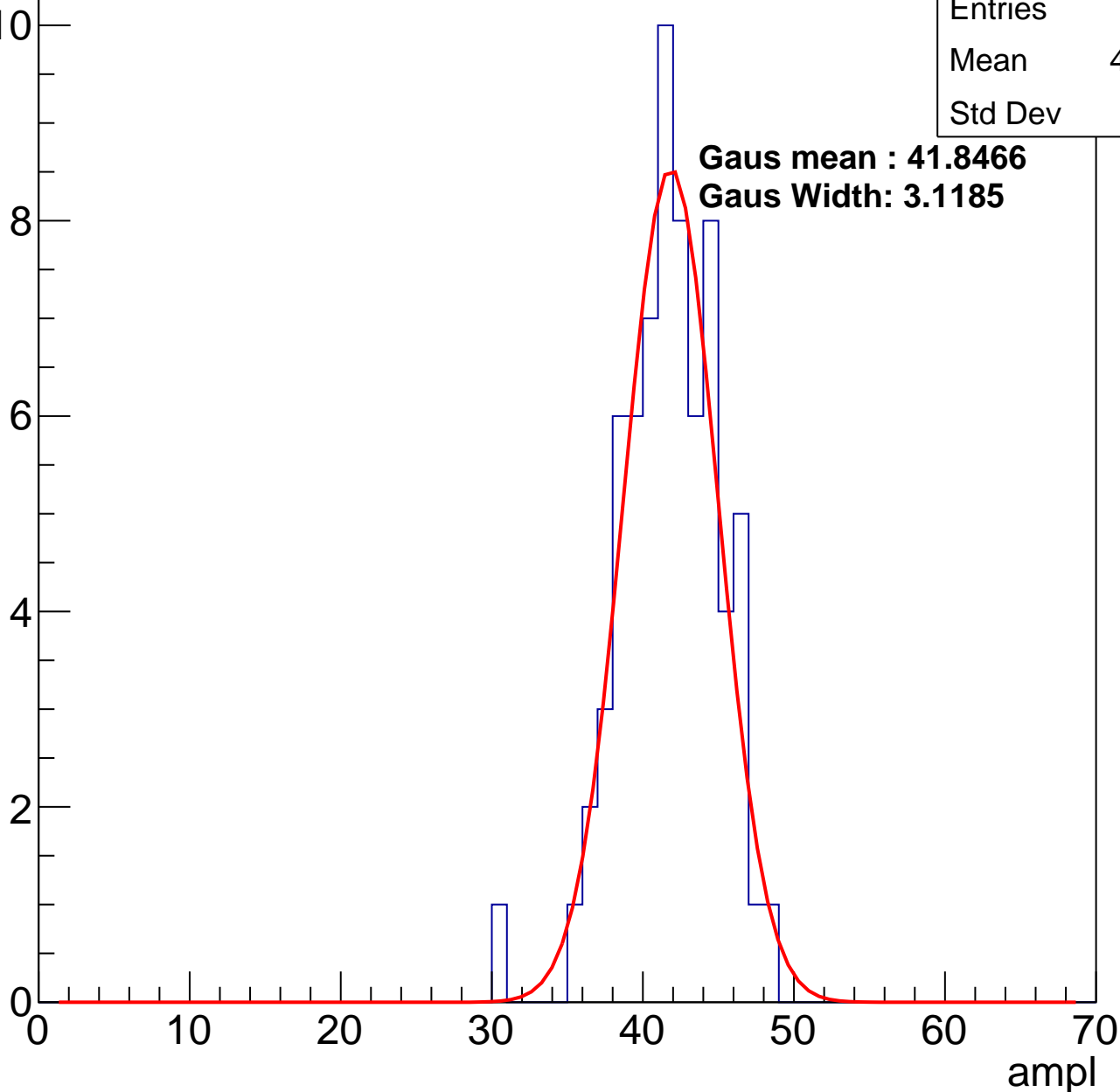
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	41.32
Std Dev	3.21

**Gaus mean : 41.8466**

**Gaus Width: 3.1185**

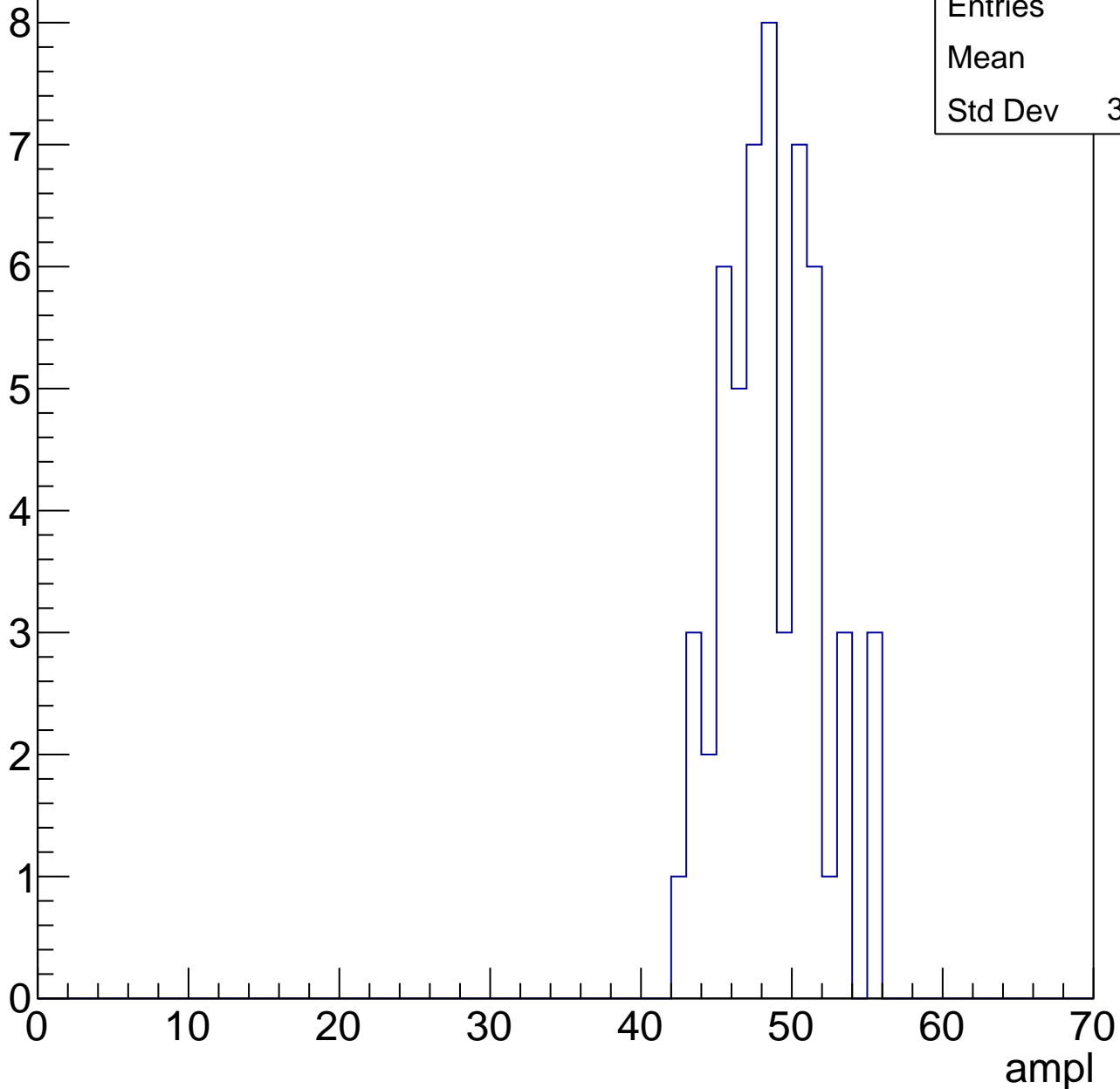


# B1L000S, U21-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

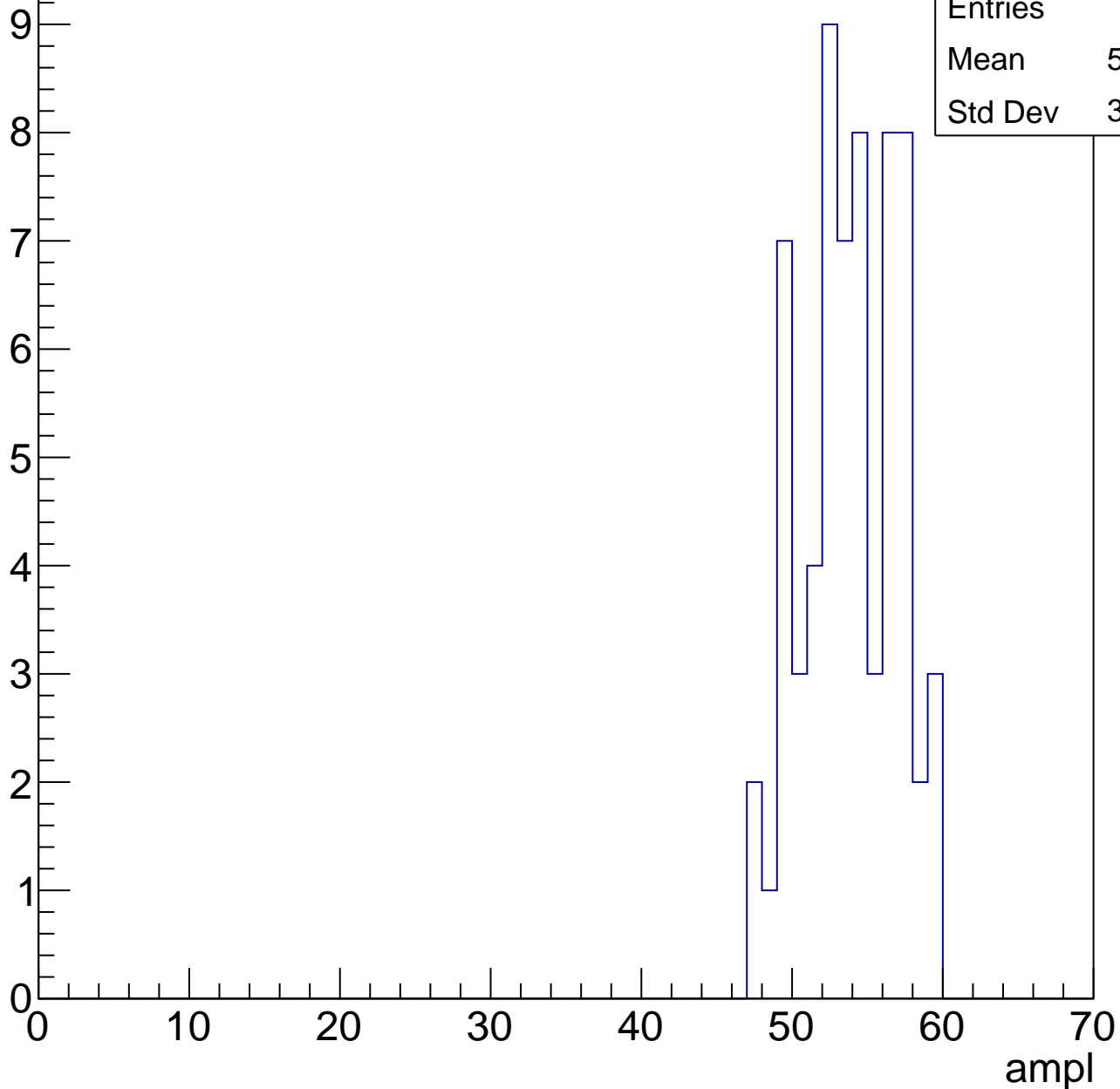
Entries	55
Mean	48.2
Std Dev	3.147



# B1L000S, U21-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



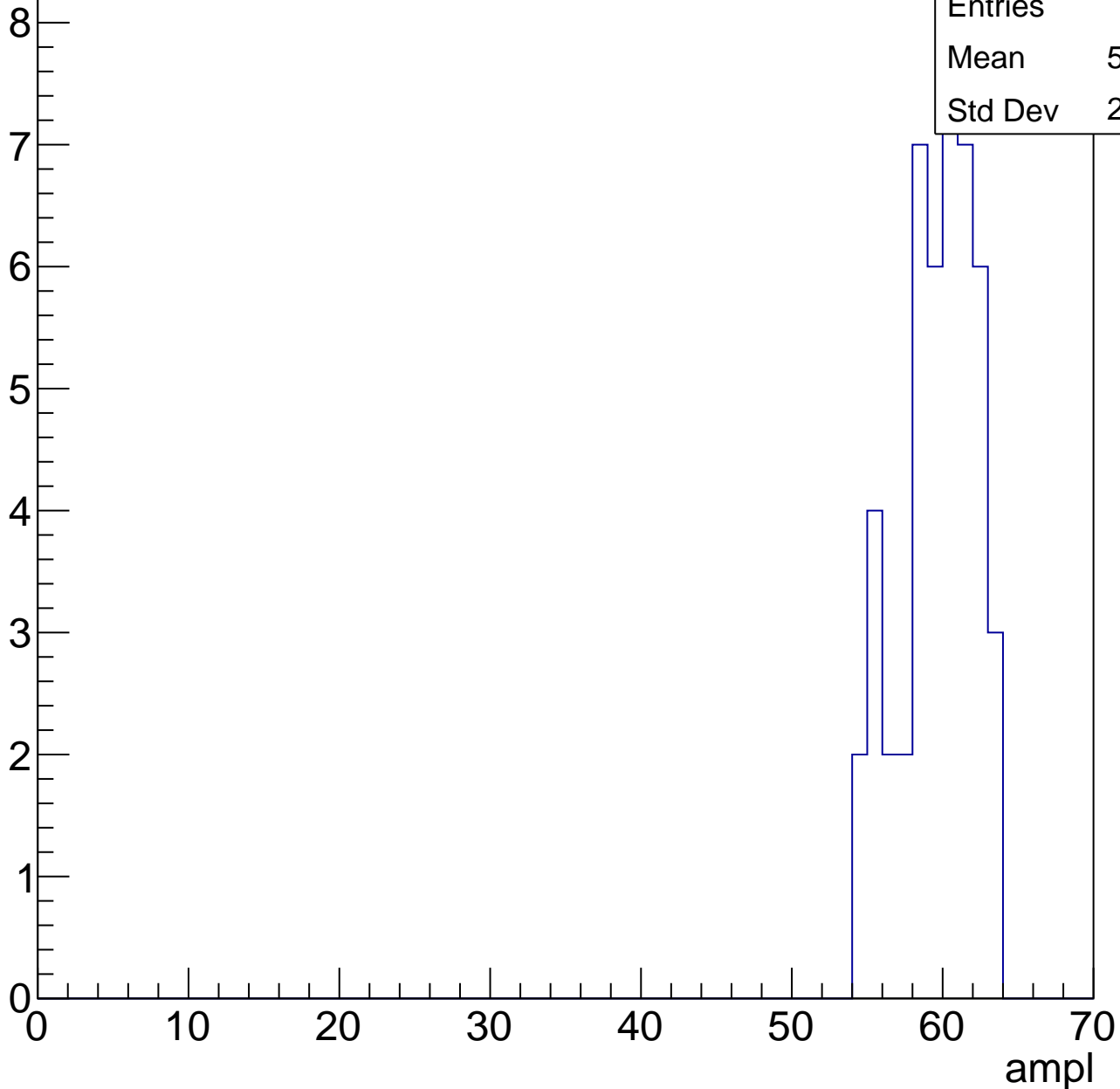
Entries	65
Mean	53.42
Std Dev	3.108

# B1L000S, U21-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	59.19
Std Dev	2.455

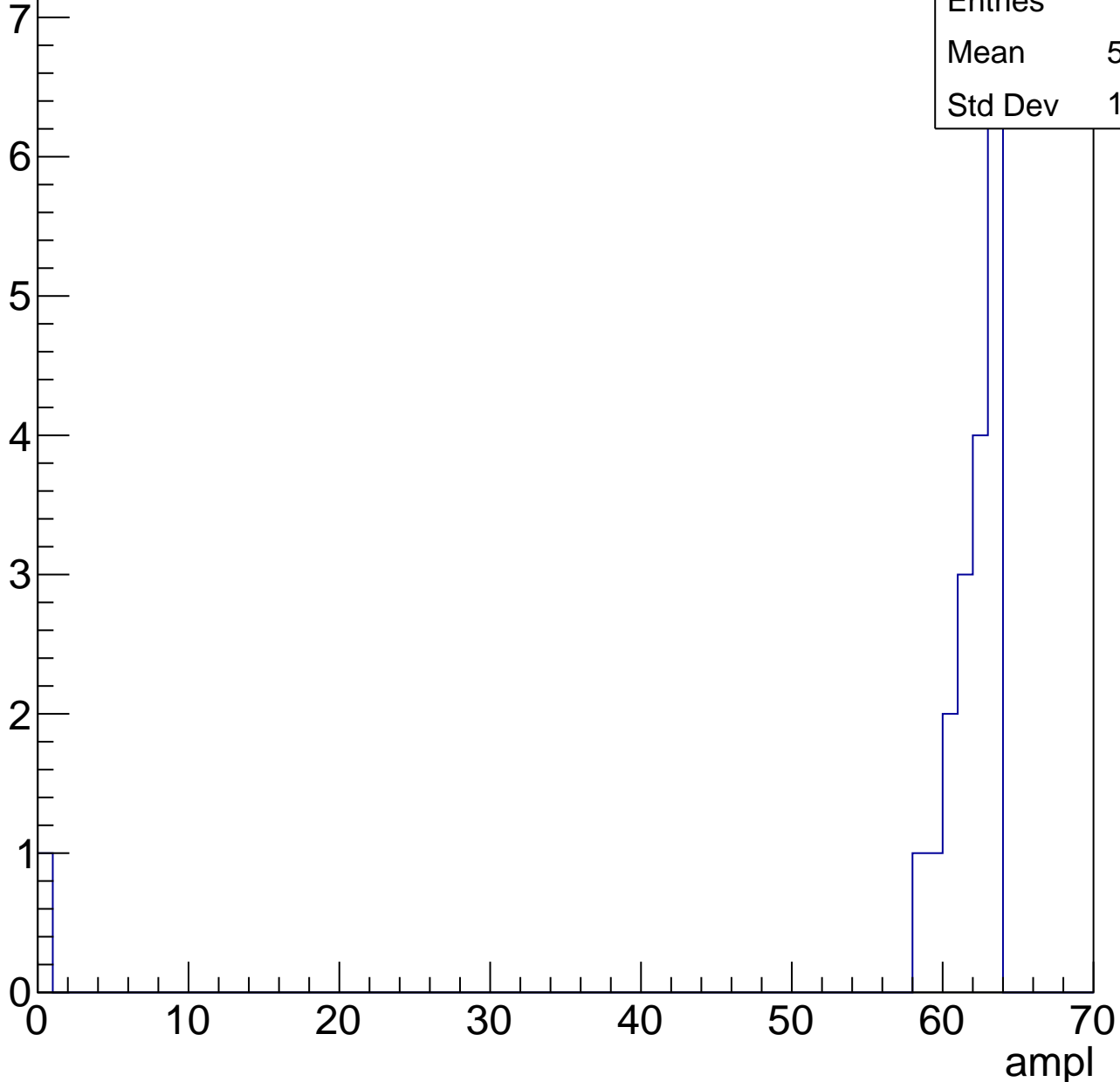


# B1L000S, U21-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.37
Std Dev	13.83





# B1L000S, U21-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	18
Std Dev	0

ampl

# B1L000S, U21-ch126, adc0

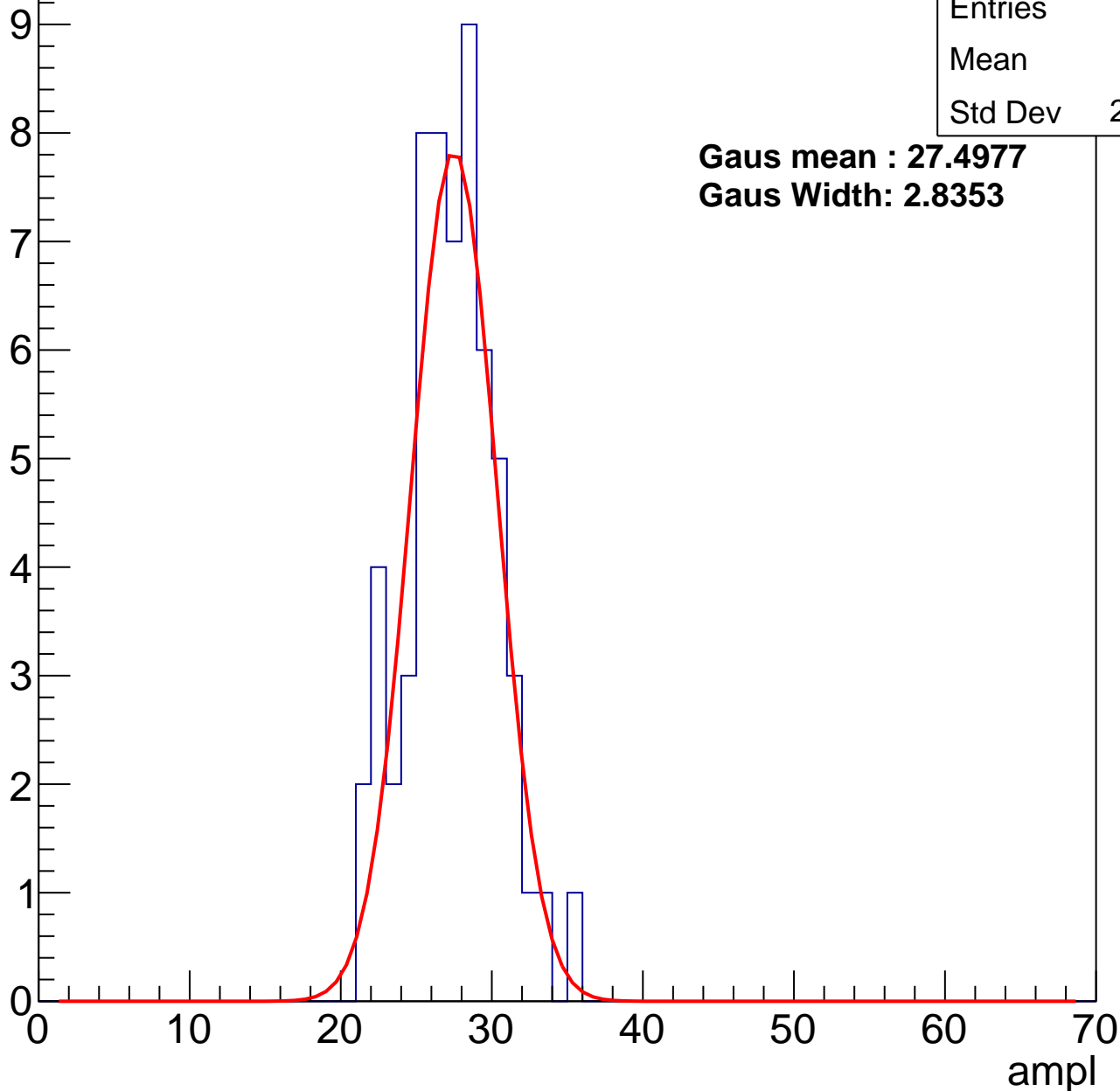
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	26.9
Std Dev	2.948

**Gaus mean : 27.4977**

**Gaus Width: 2.8353**



# B1L000S, U21-ch126, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	33.5
Std Dev	3.305

**Gaus mean : 33.9071**

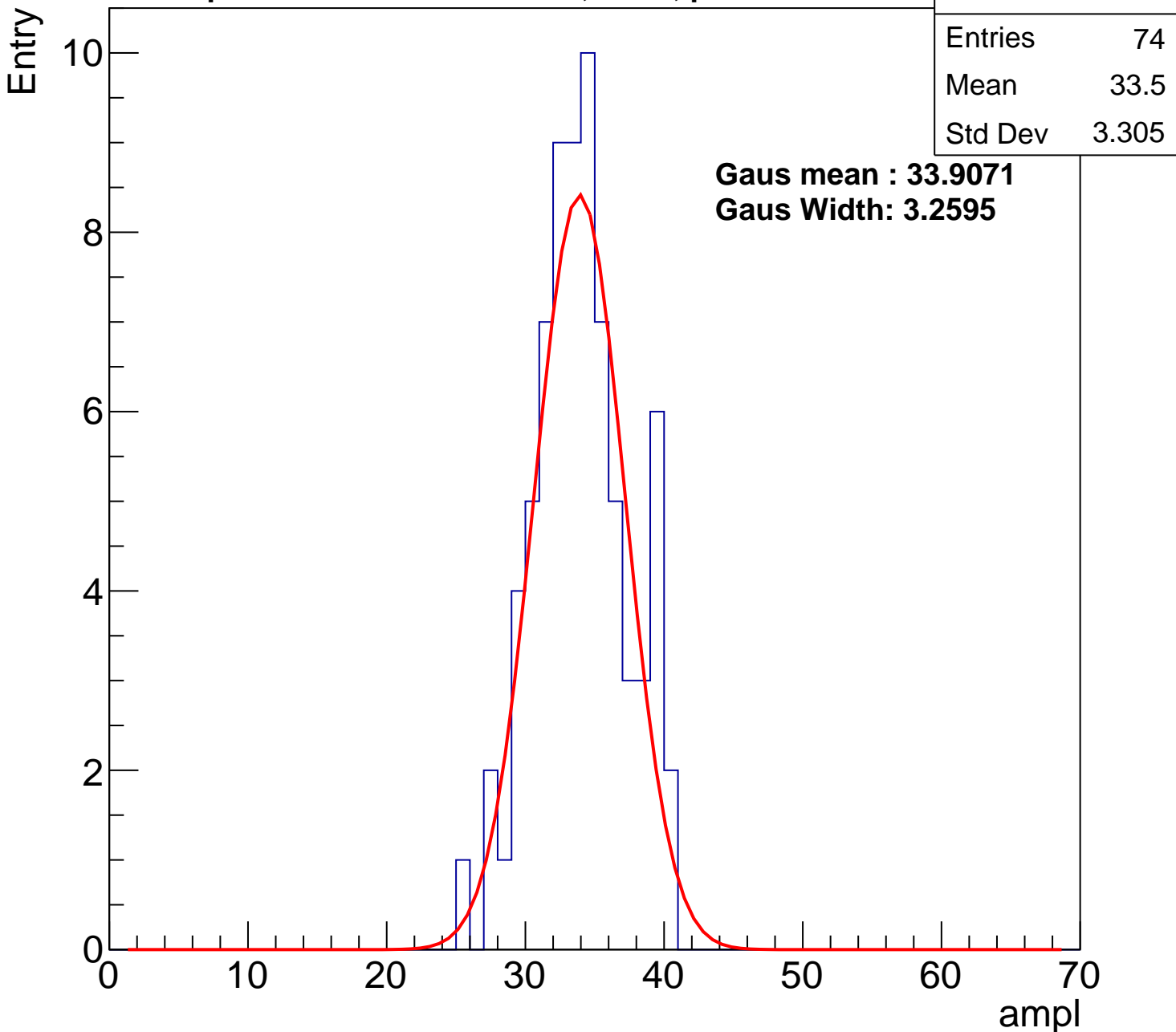
**Gaus Width: 3.2595**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch126, adc2

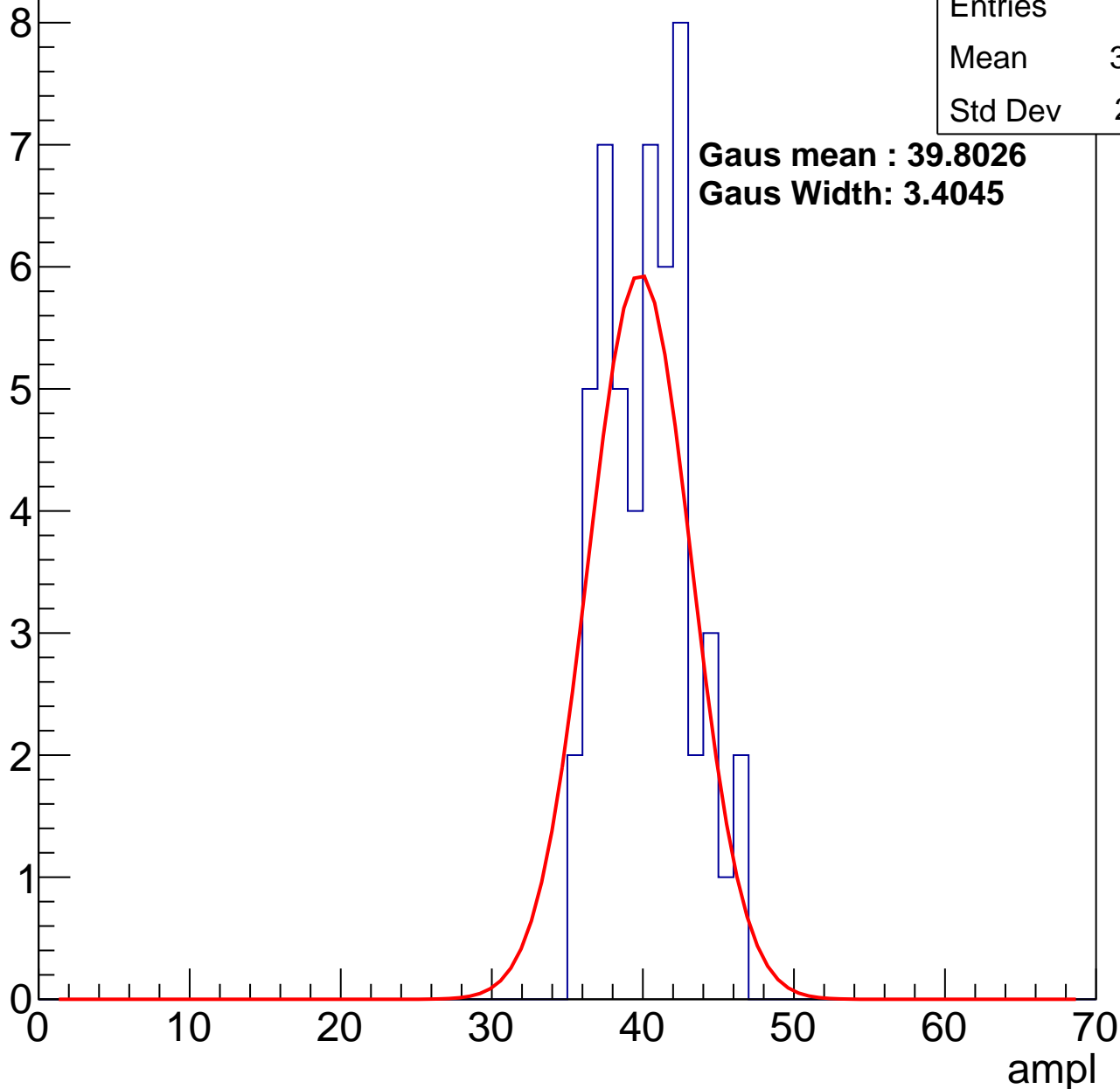
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	39.85
Std Dev	2.831

**Gaus mean : 39.8026**

**Gaus Width: 3.4045**

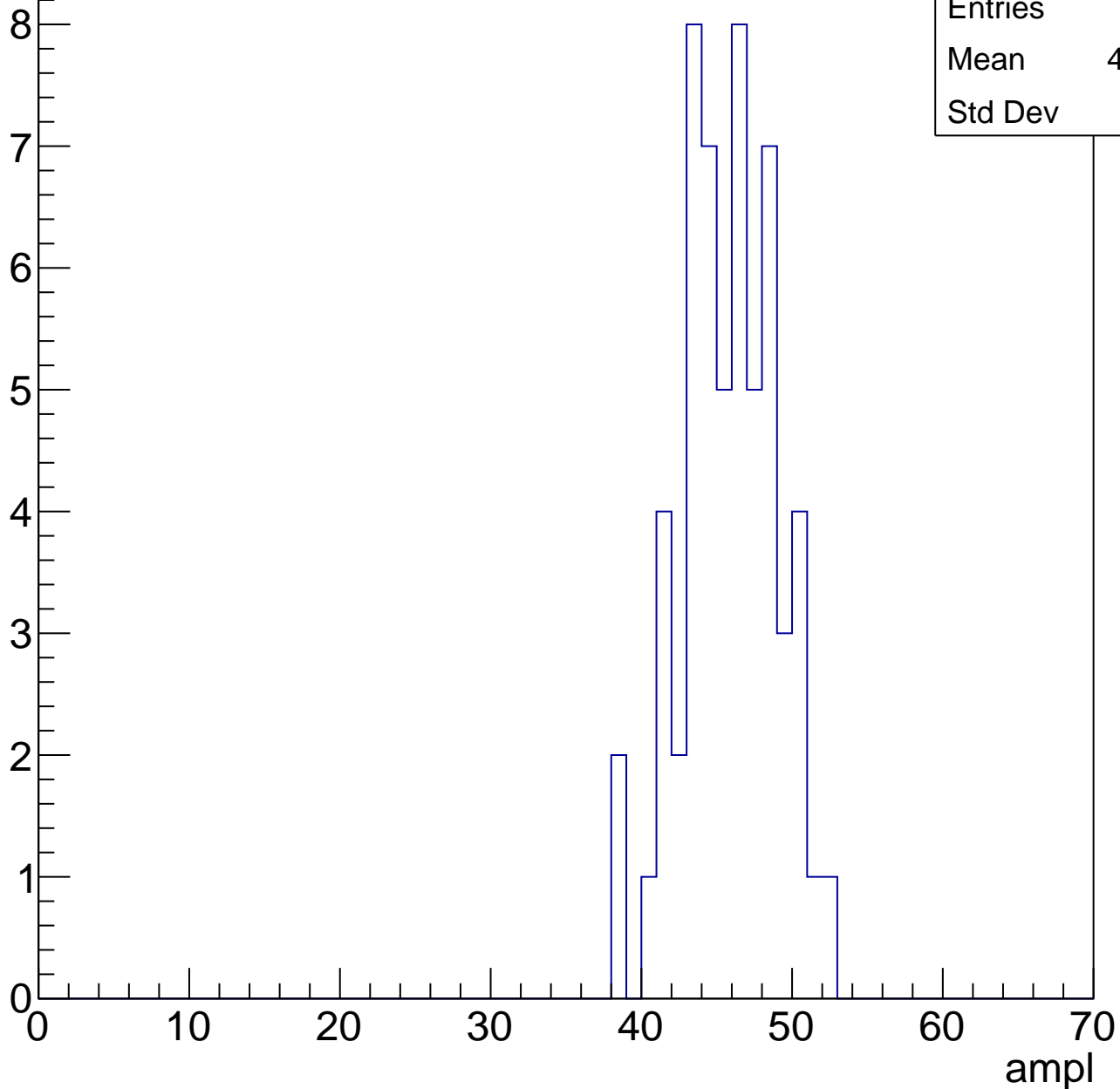


# B1L000S, U21-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	45.34
Std Dev	3.11

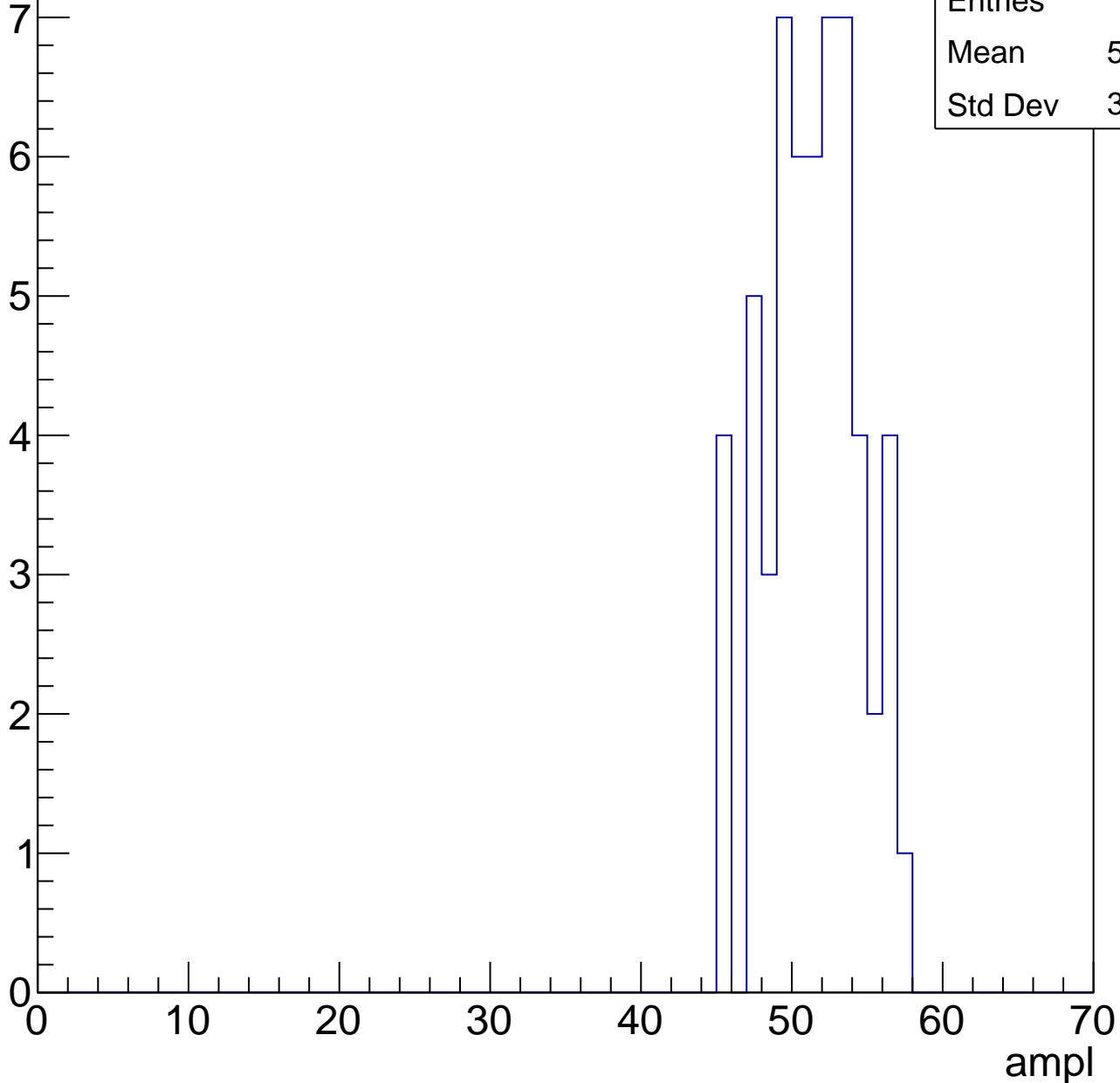


# B1L000S, U21-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	50.89
Std Dev	3.057

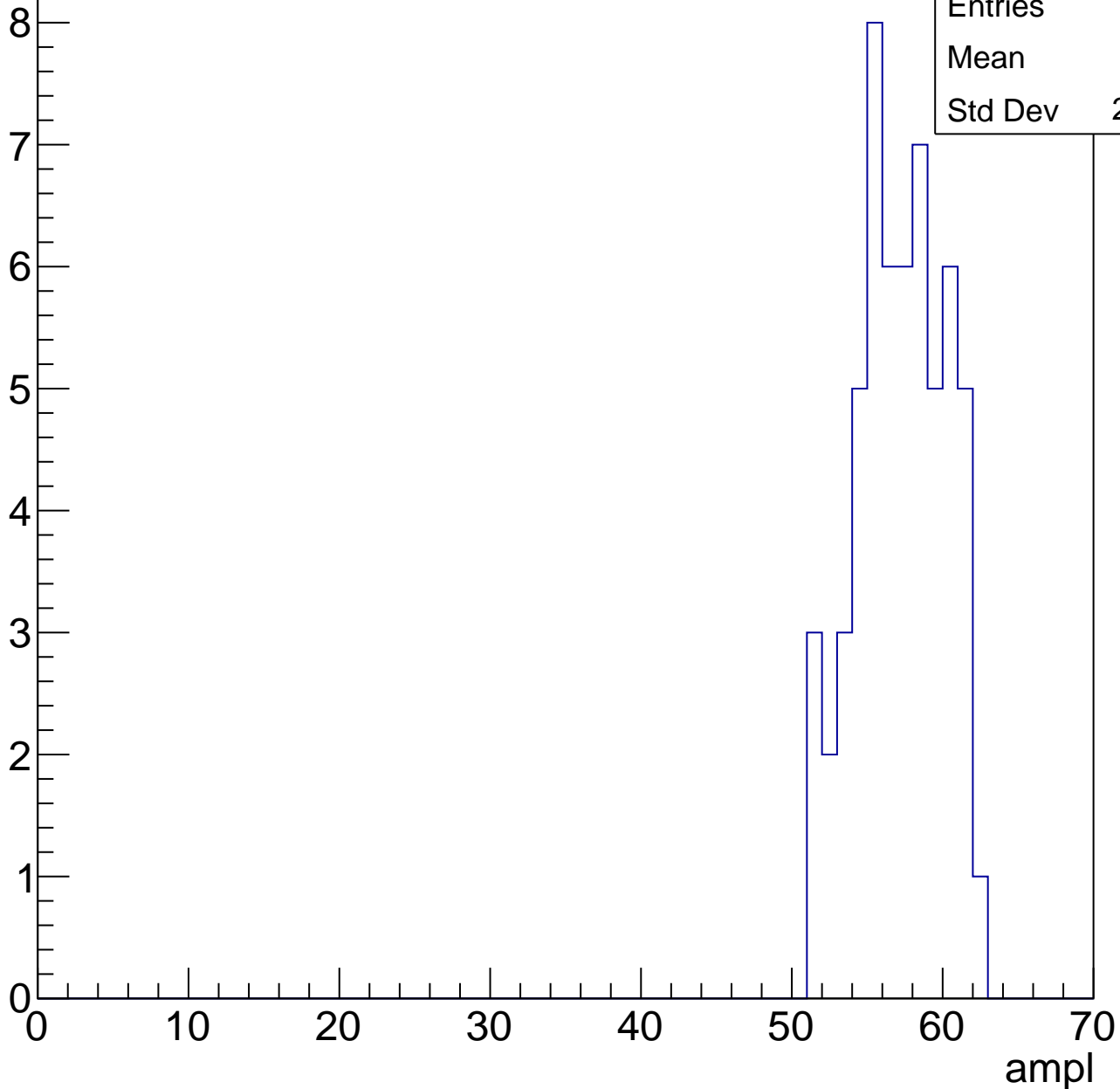


# B1L000S, U21-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	56.7
Std Dev	2.871

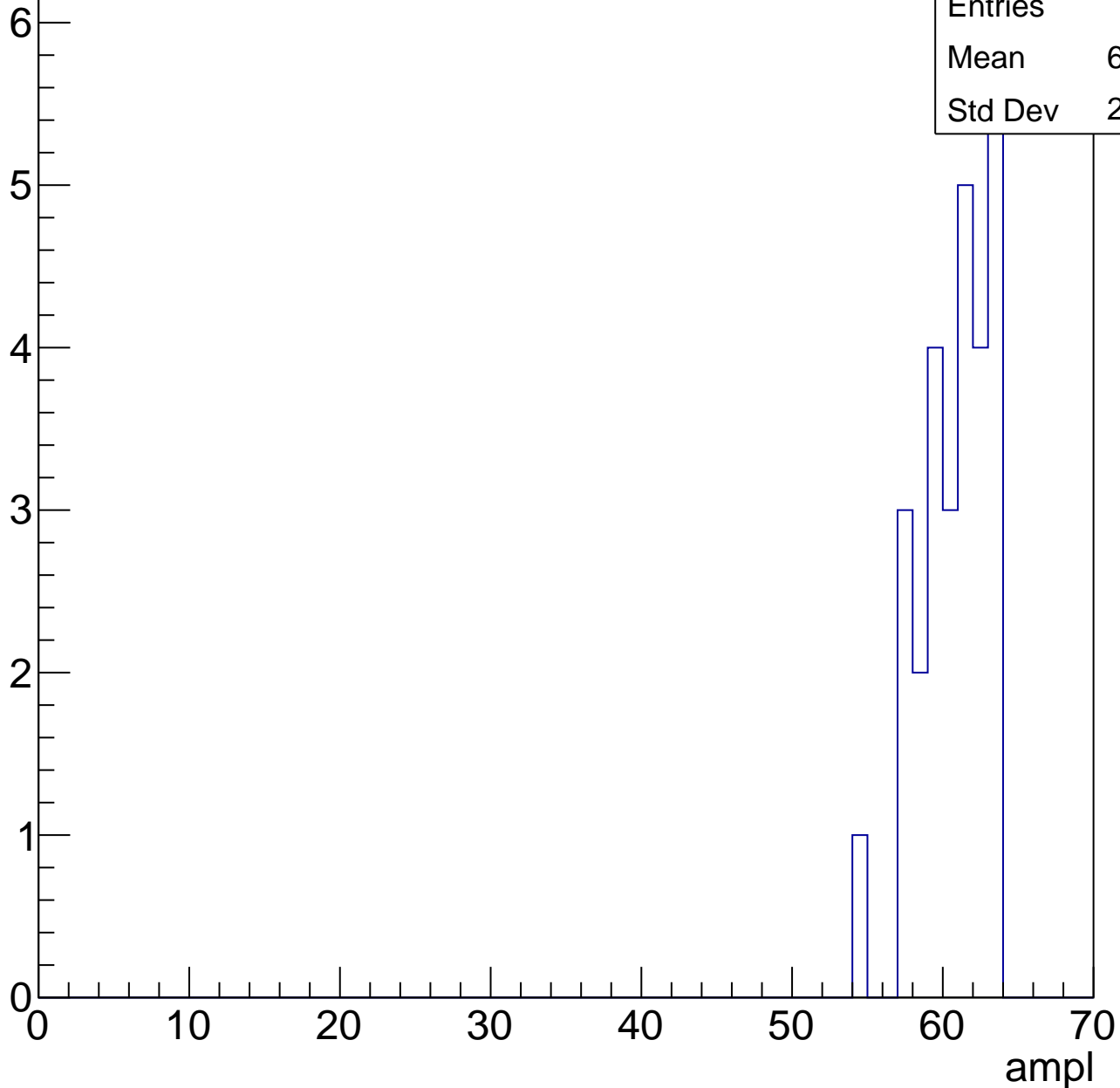


# B1L000S, U21-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	60.29
Std Dev	2.297



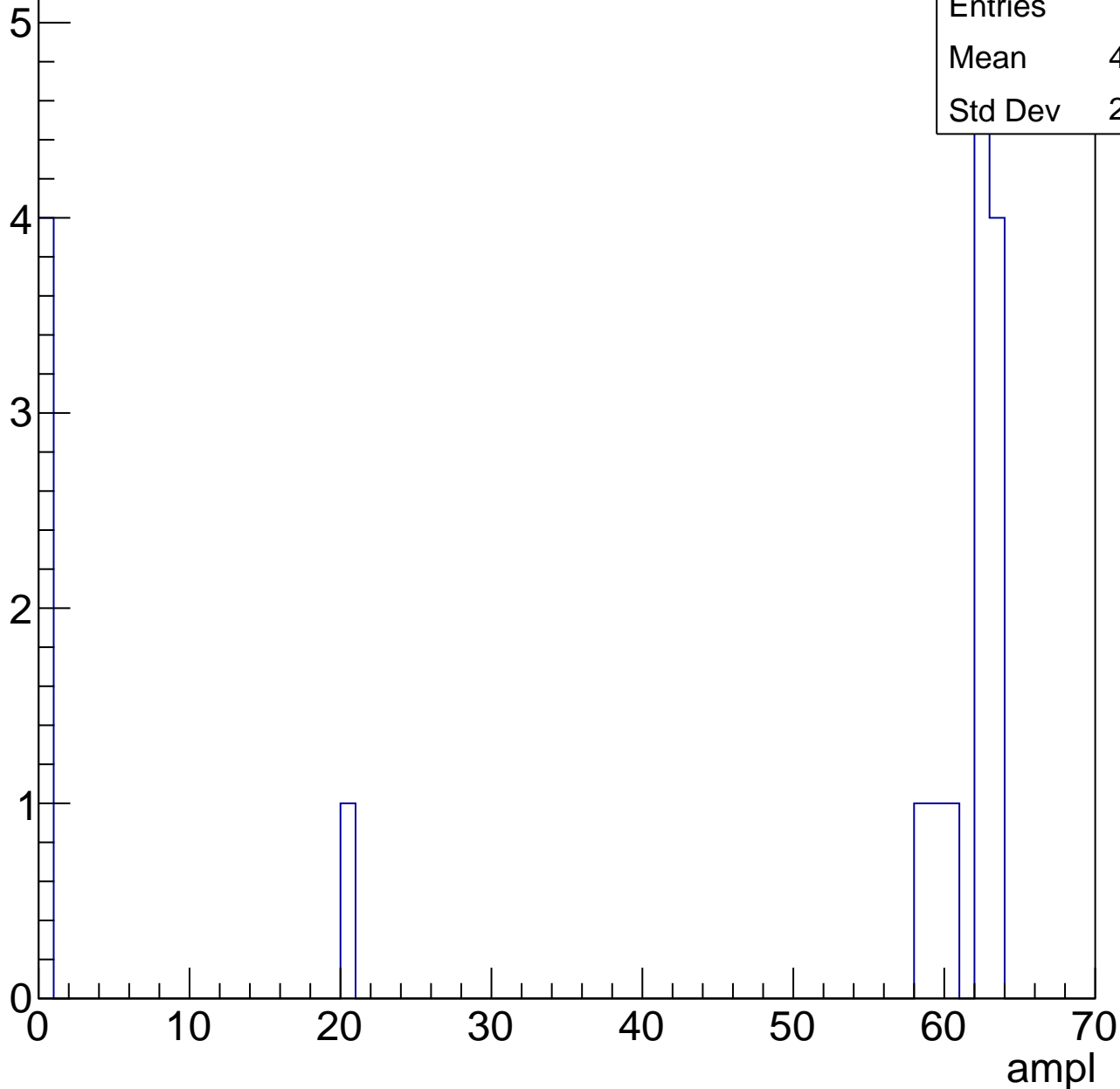


# B1L000S, U21-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	44.65
Std Dev	26.63



# B1L000S, U21-ch127, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	27.09
Std Dev	4.71

**Gaus mean : 28.4388**

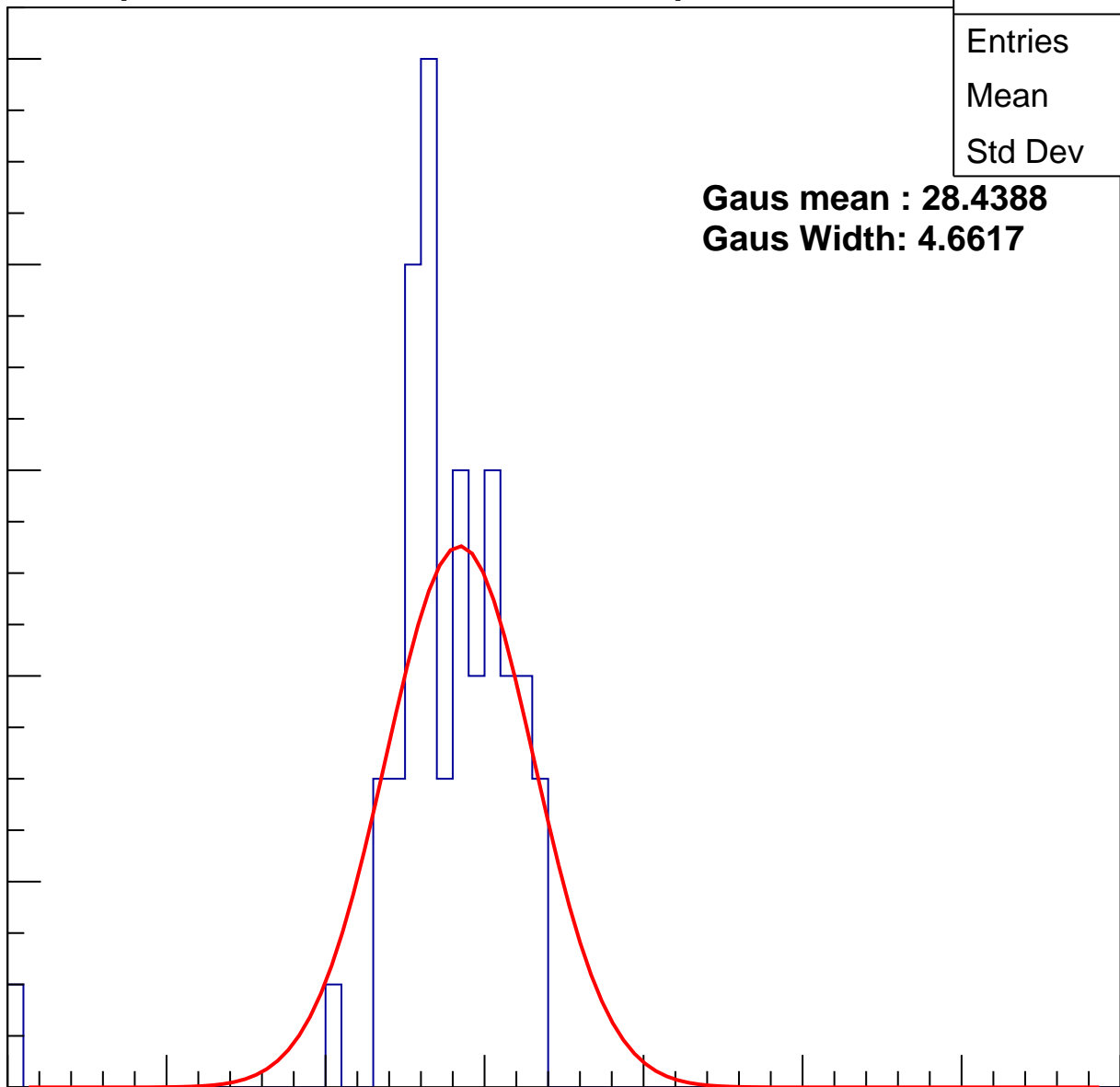
**Gaus Width: 4.6617**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U21-ch127, adc1

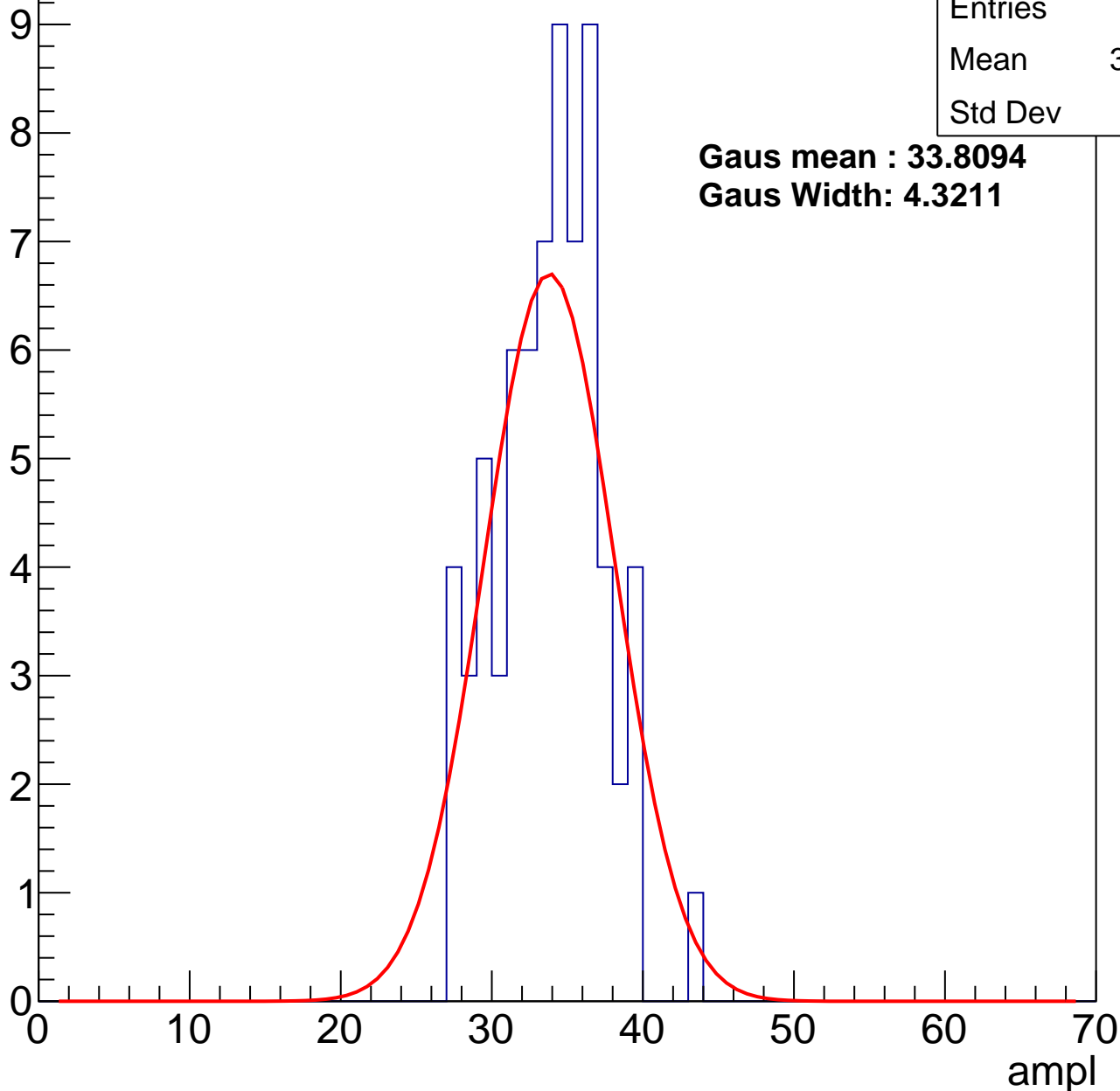
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	33.34
Std Dev	3.43

**Gaus mean : 33.8094**

**Gaus Width: 4.3211**



# B1L000S, U21-ch127, adc2

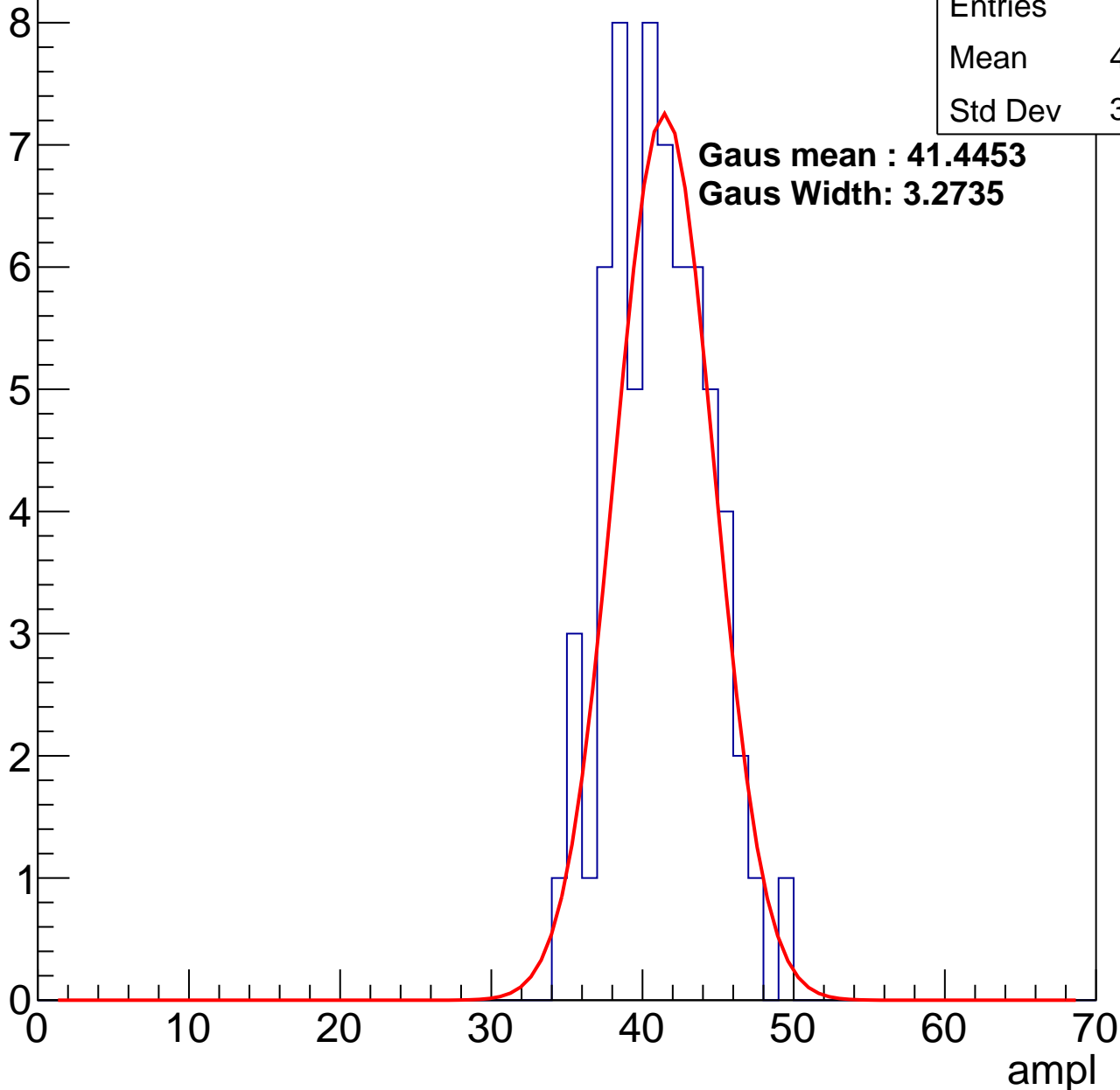
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	40.64
Std Dev	3.208

**Gaus mean : 41.4453**

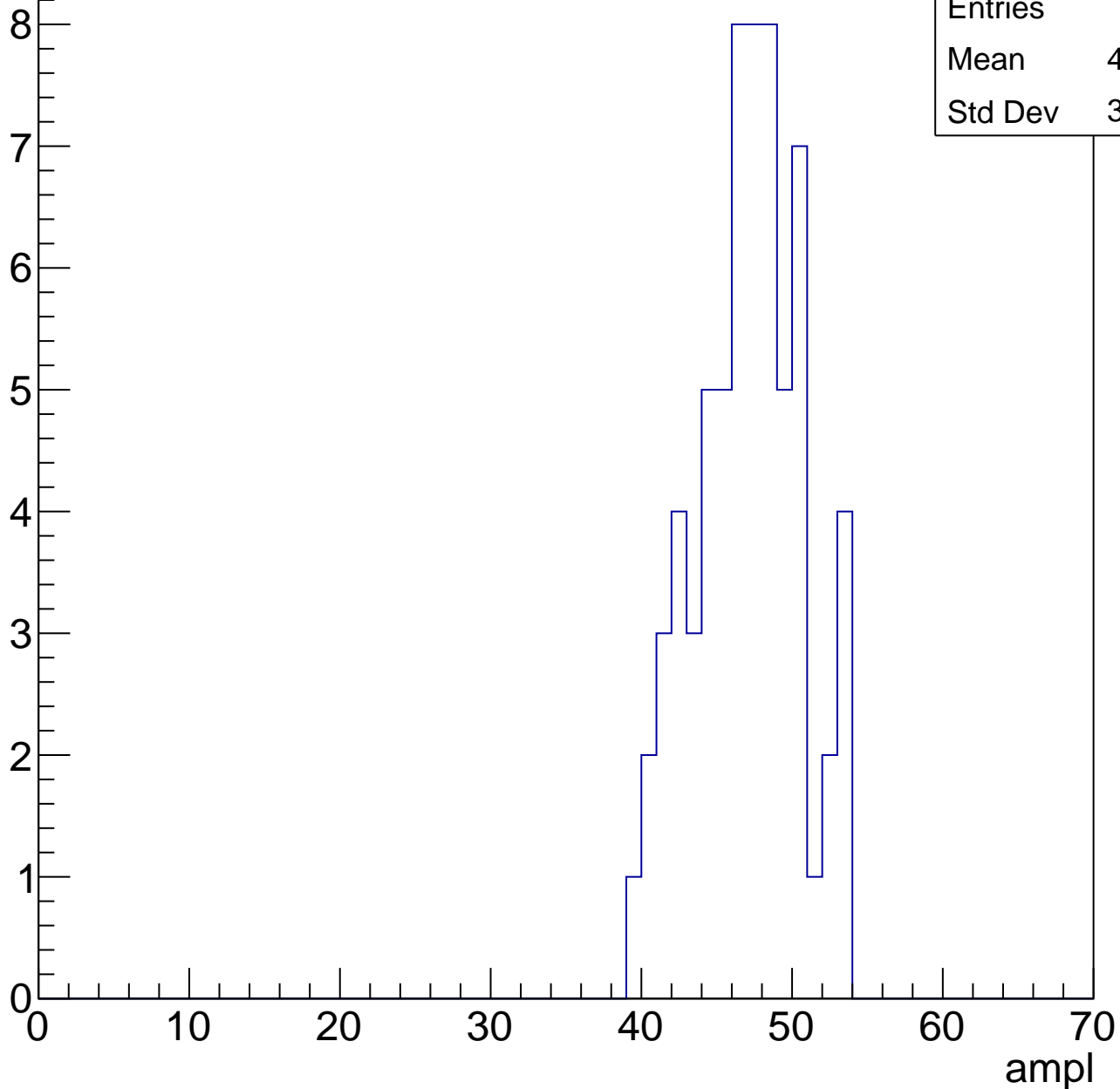
**Gaus Width: 3.2735**



# B1L000S, U21-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



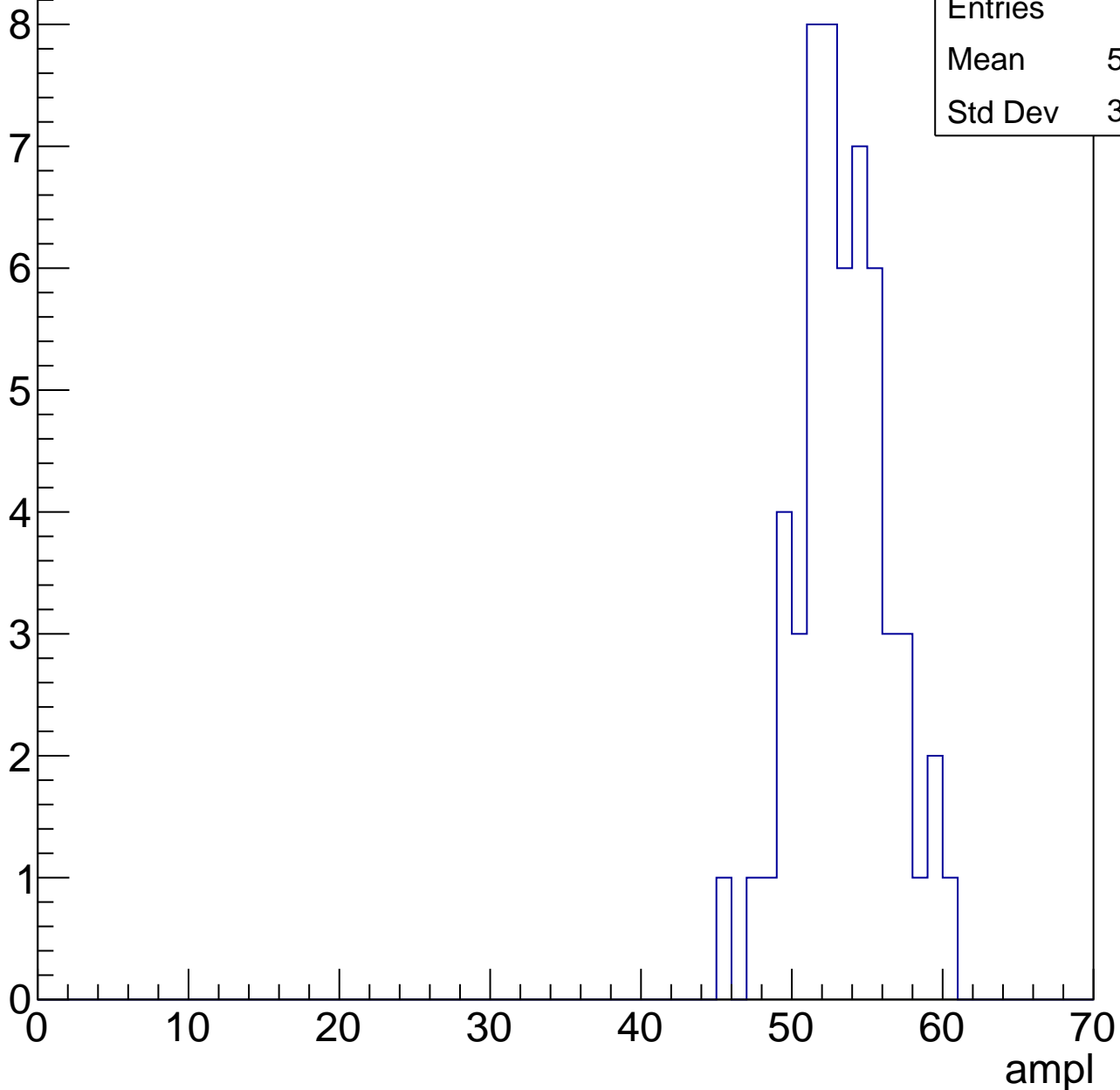
Entries	66
Mean	46.58
Std Dev	3.438

# B1L000S, U21-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	52.93
Std Dev	3.038

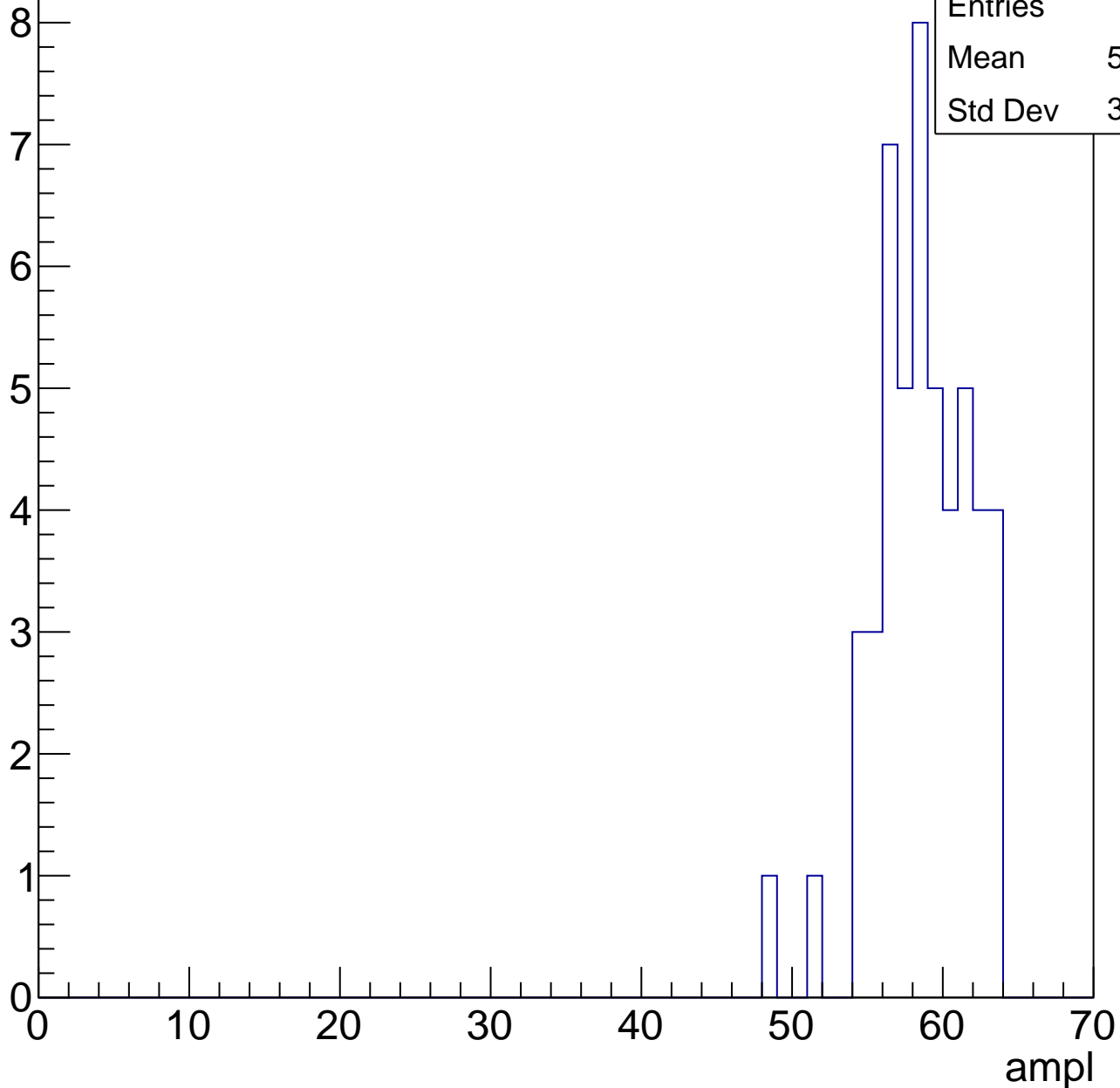


# B1L000S, U21-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.14
Std Dev	3.118

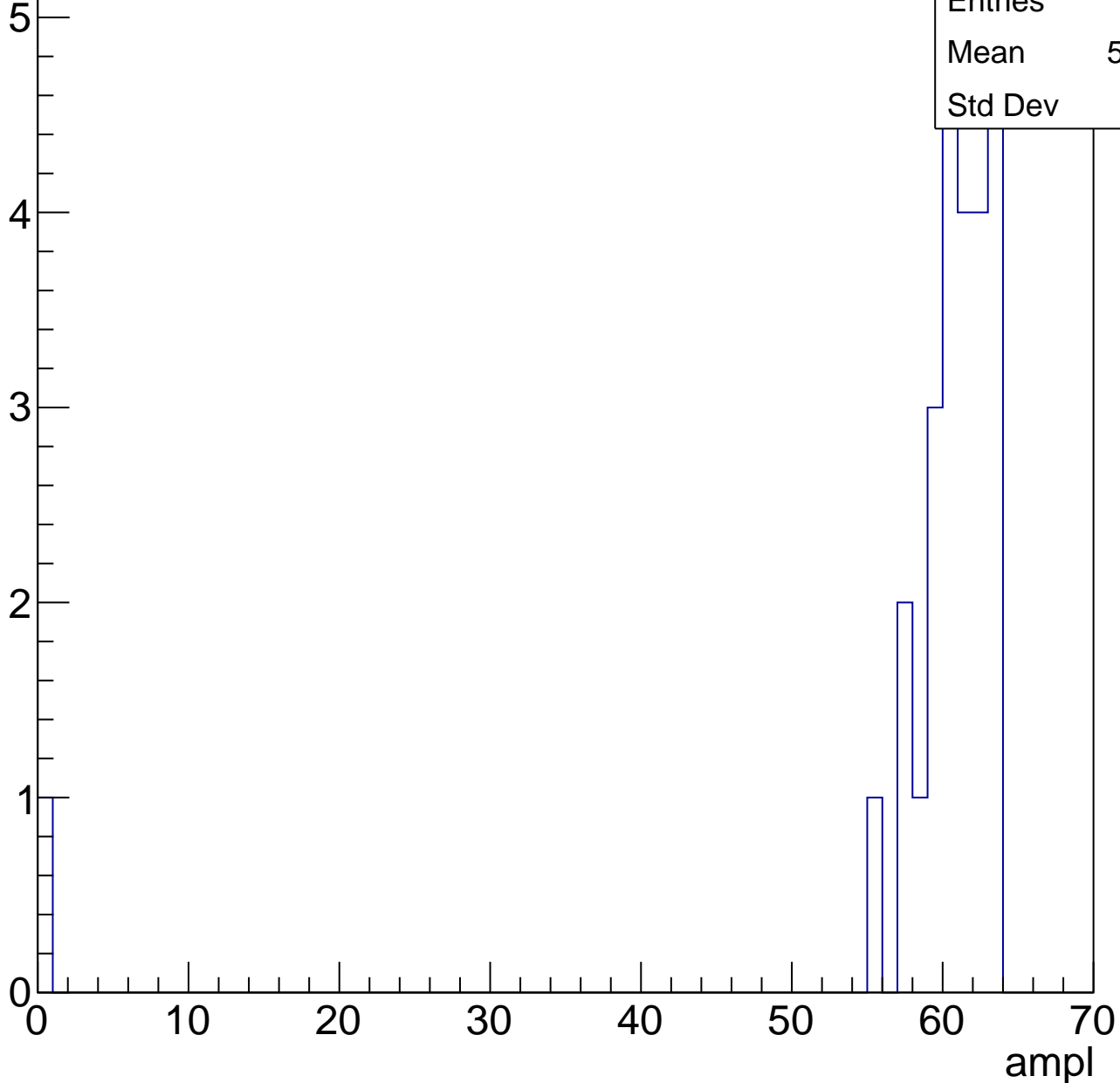


# B1L000S, U21-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.12
Std Dev	11.8

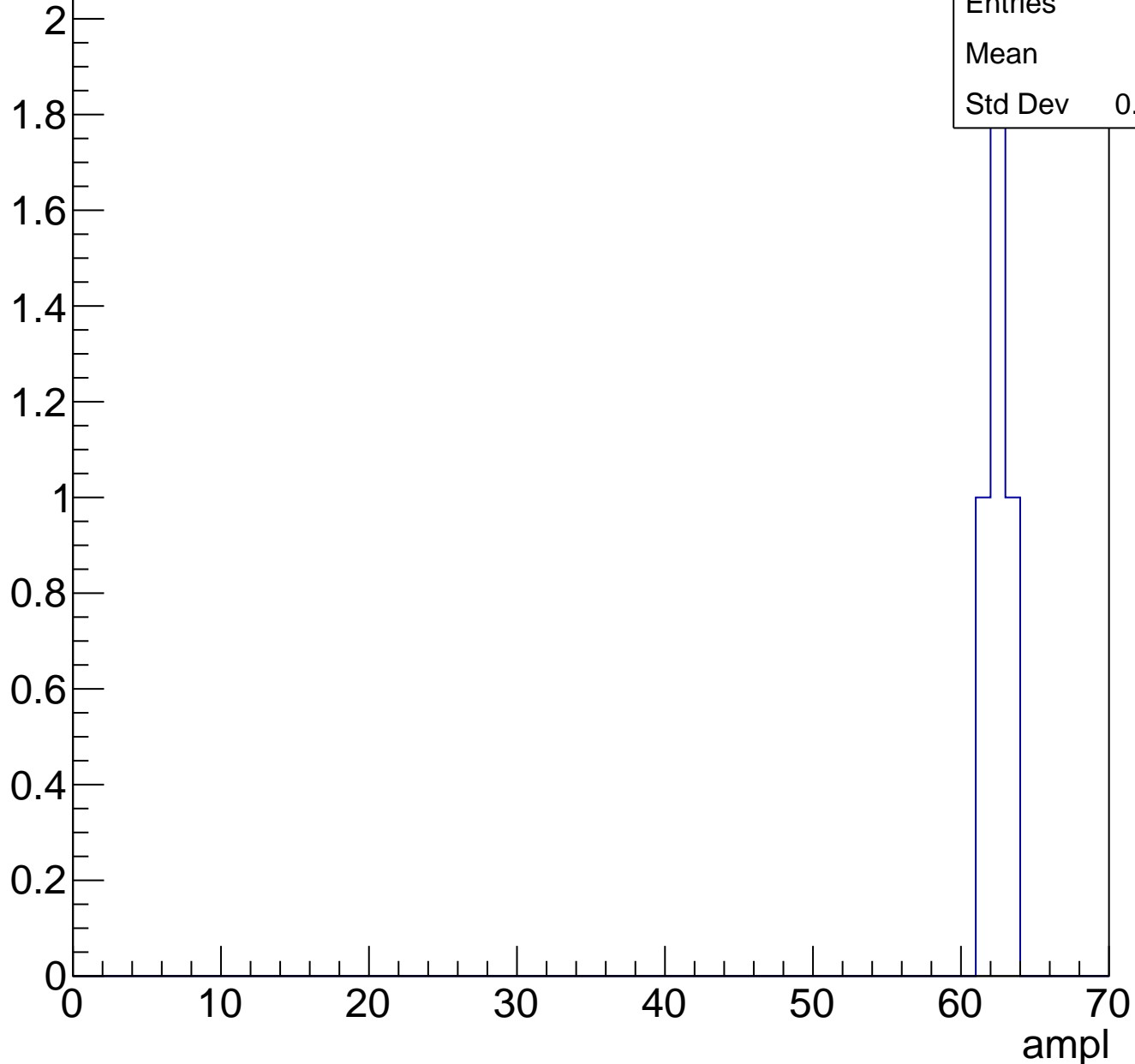




# B1L000S, U21-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U21-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

