

B1L102S, U14-ch0, adc0

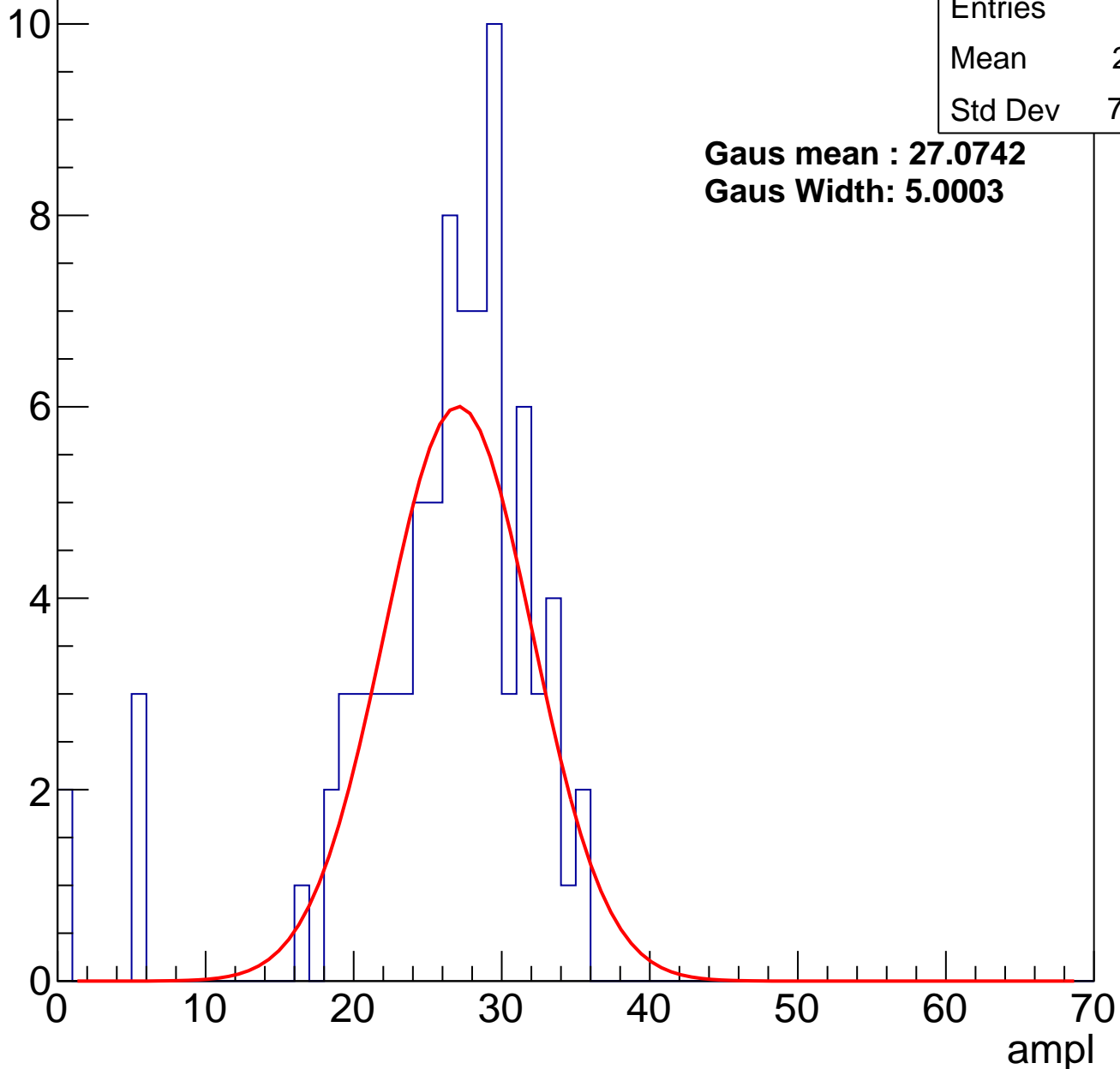
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	25.21
Std Dev	7.015

Gaus mean : 27.0742

Gaus Width: 5.0003

Entry



B1L102S, U14-ch0, adc1

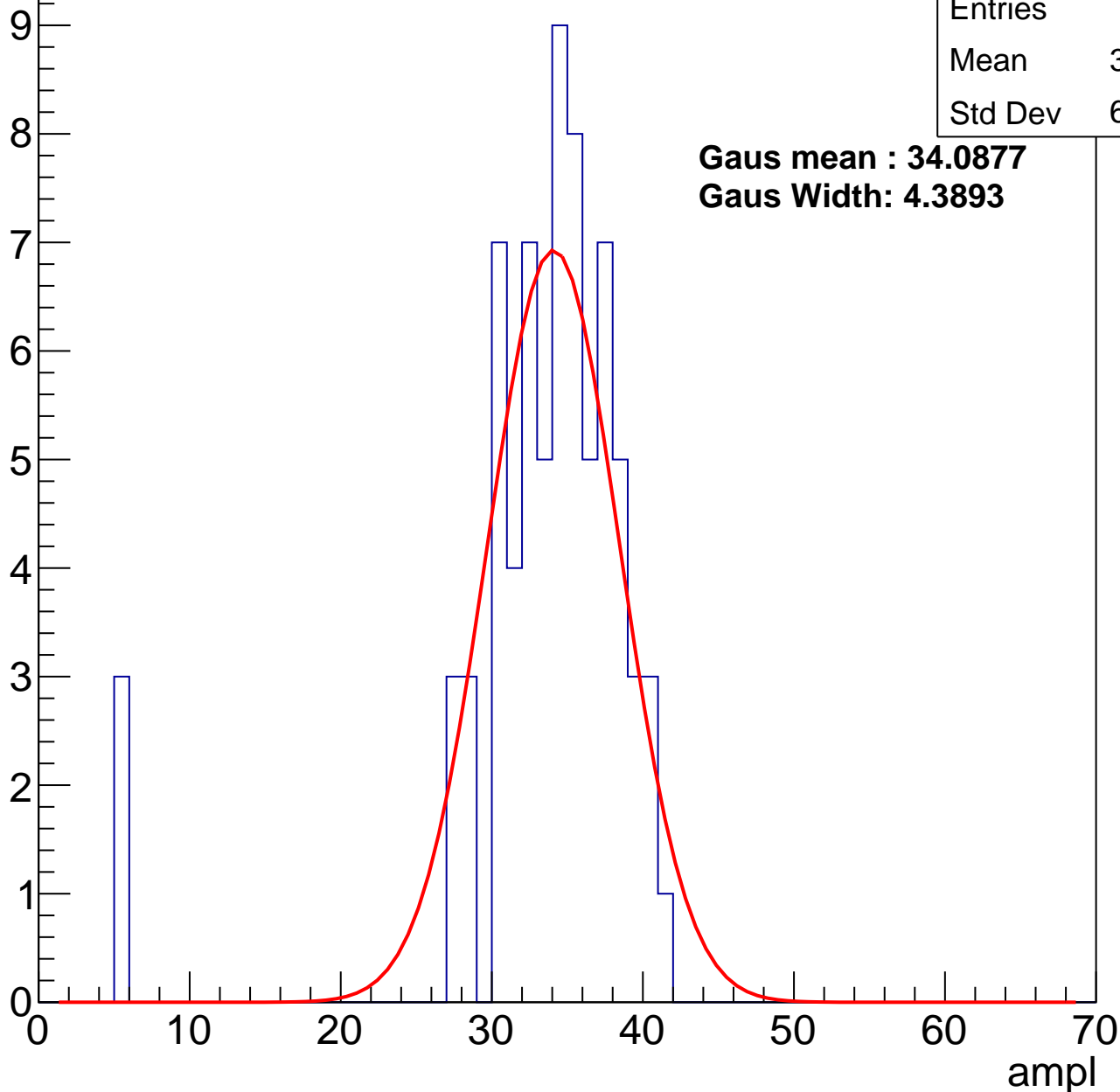
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	32.82
Std Dev	6.683

Gaus mean : 34.0877

Gaus Width: 4.3893



B1L102S, U14-ch0, adc2

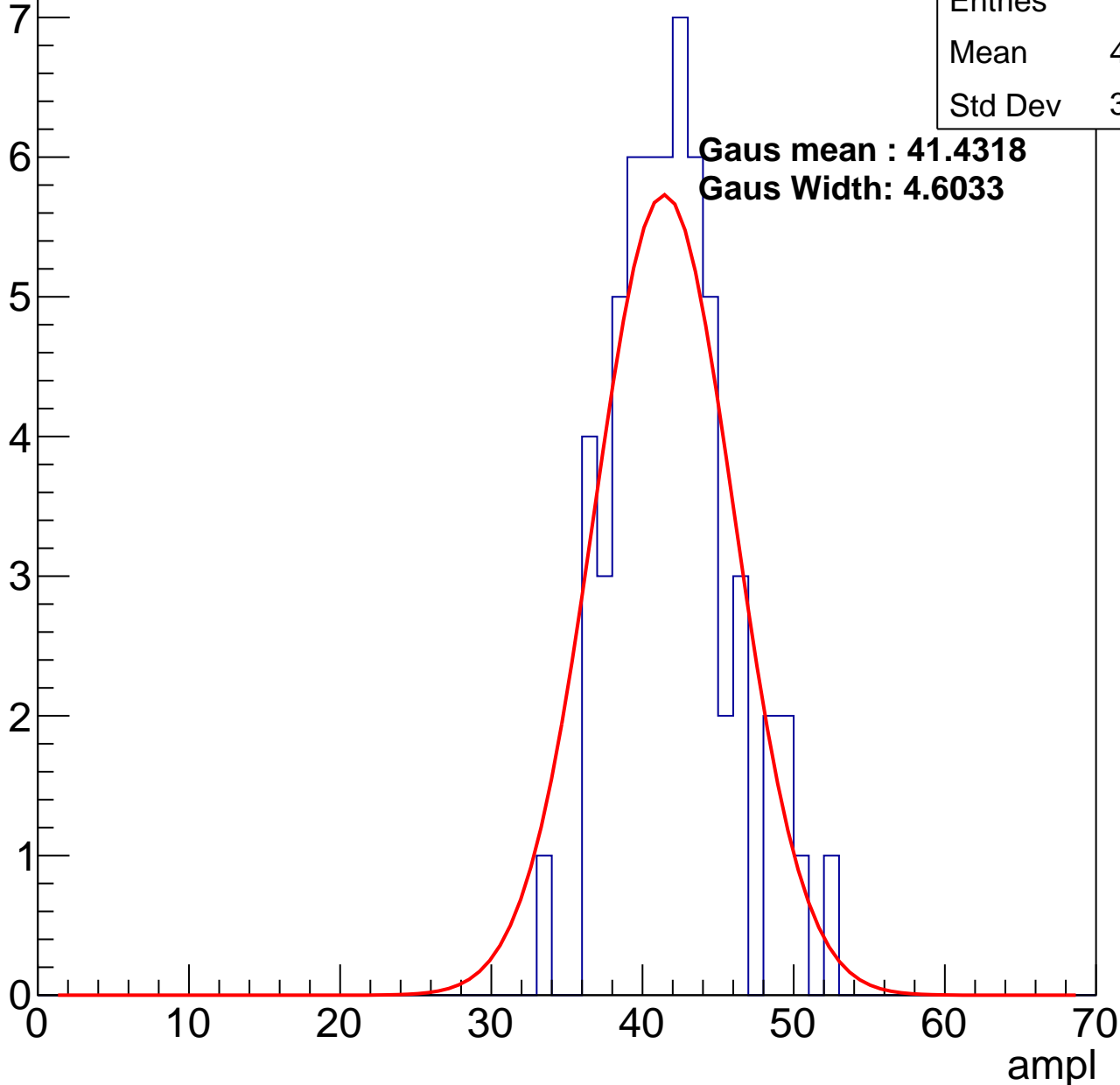
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.57
Std Dev	3.827

Gaus mean : 41.4318

Gaus Width: 4.6033

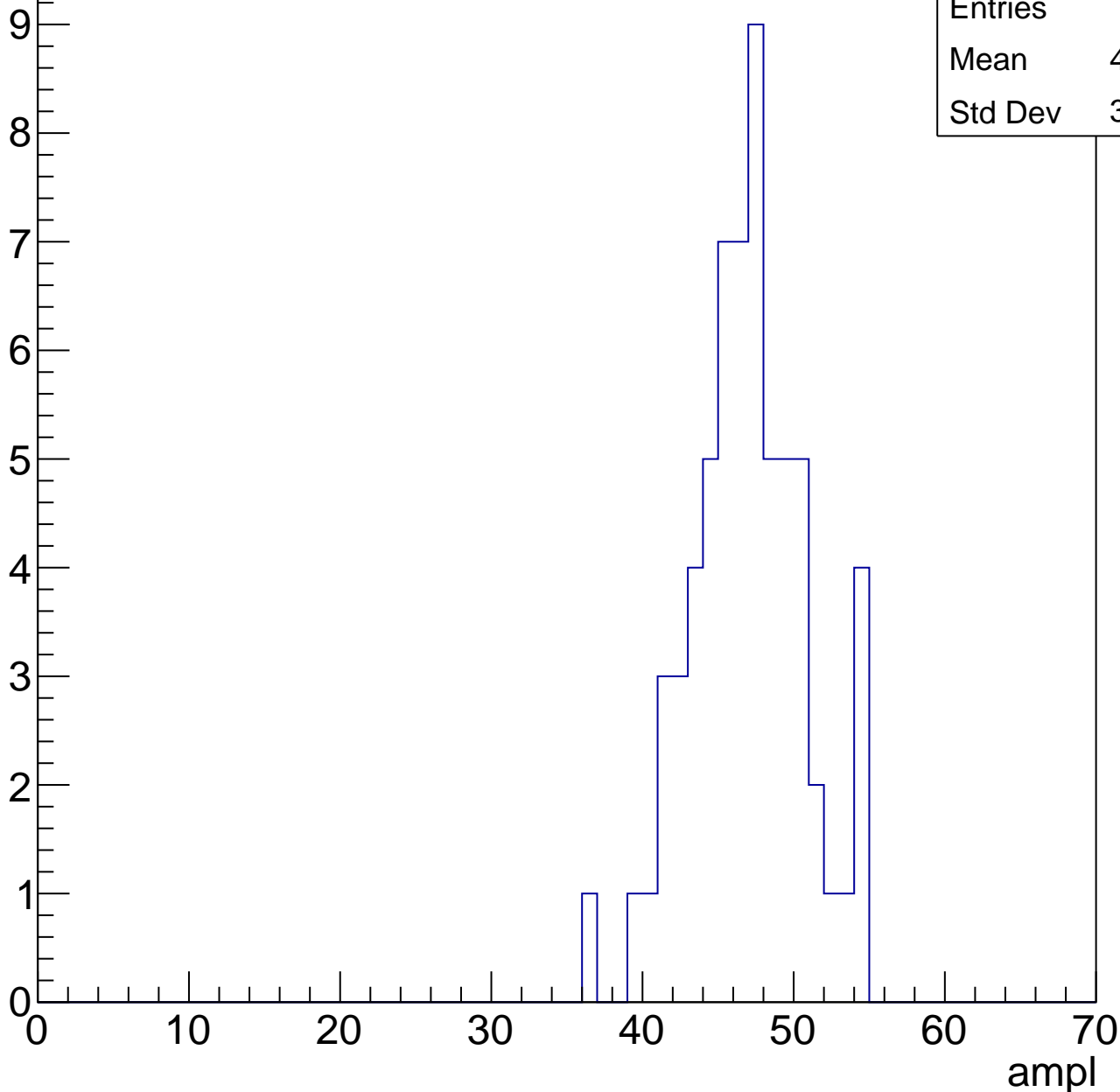


B1L102S, U14-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

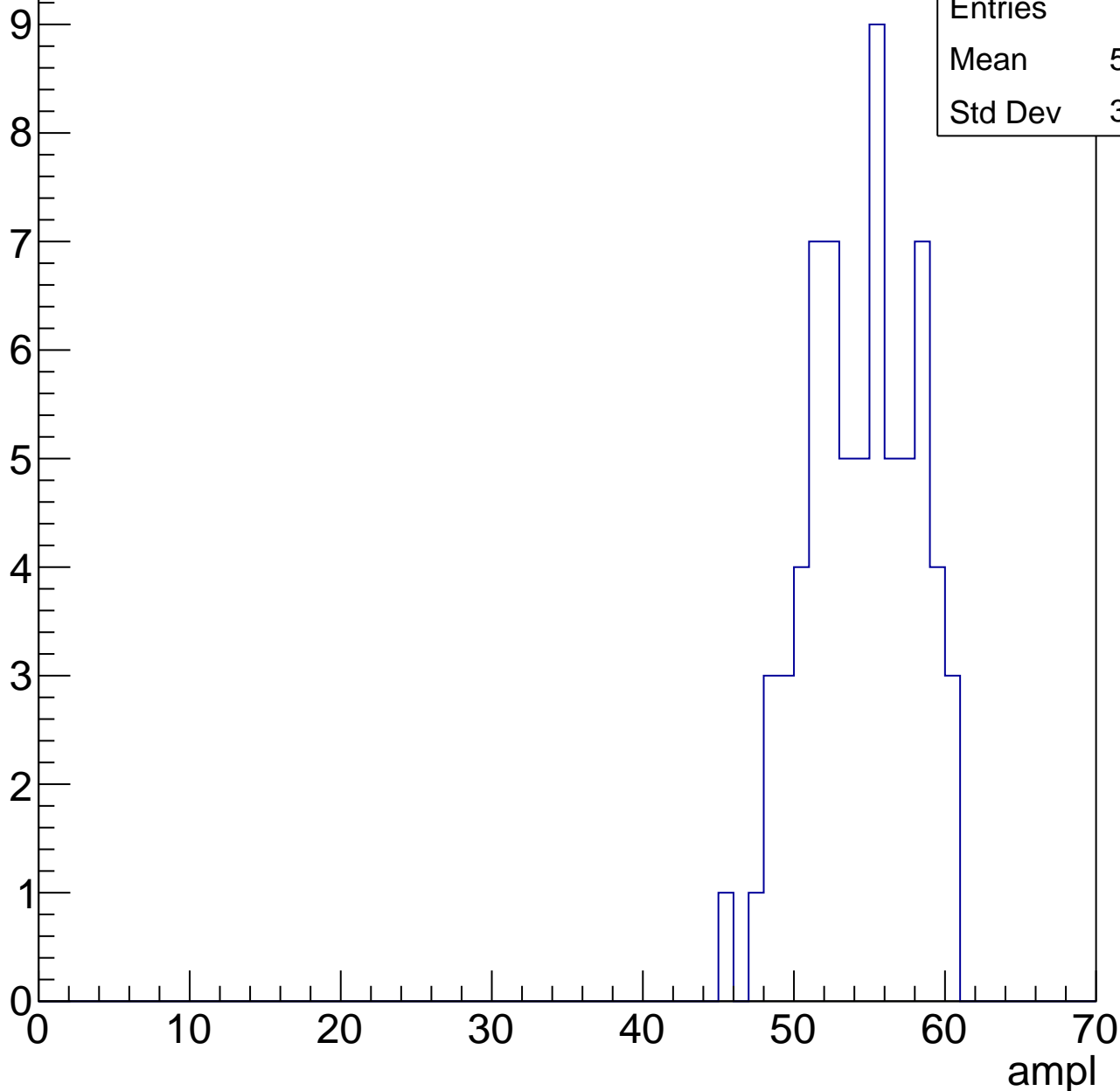
Entries	64
Mean	46.47
Std Dev	3.754



B1L102S, U14-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



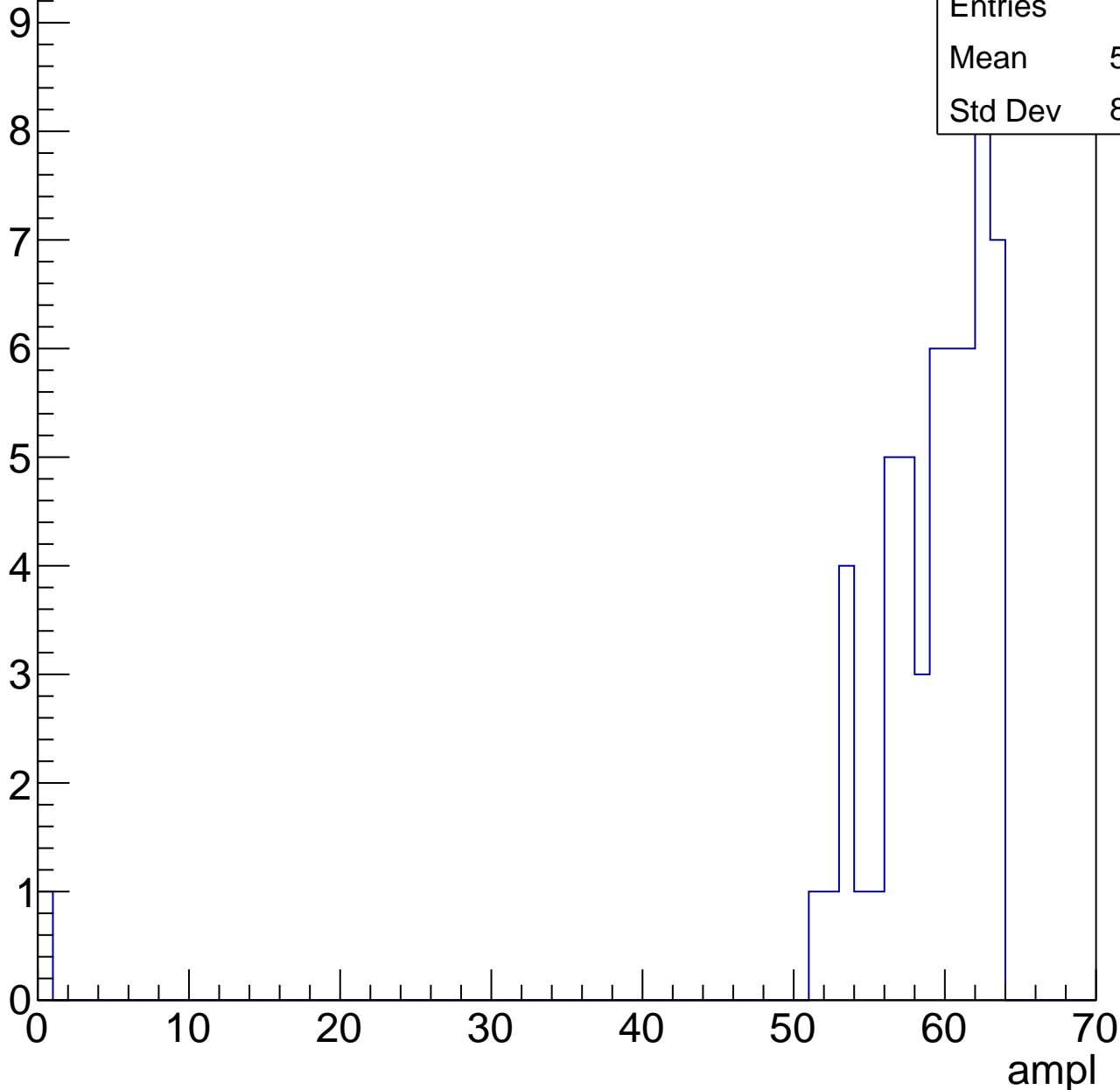
Entries	69
Mean	53.93
Std Dev	3.544

B1L102S, U14-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

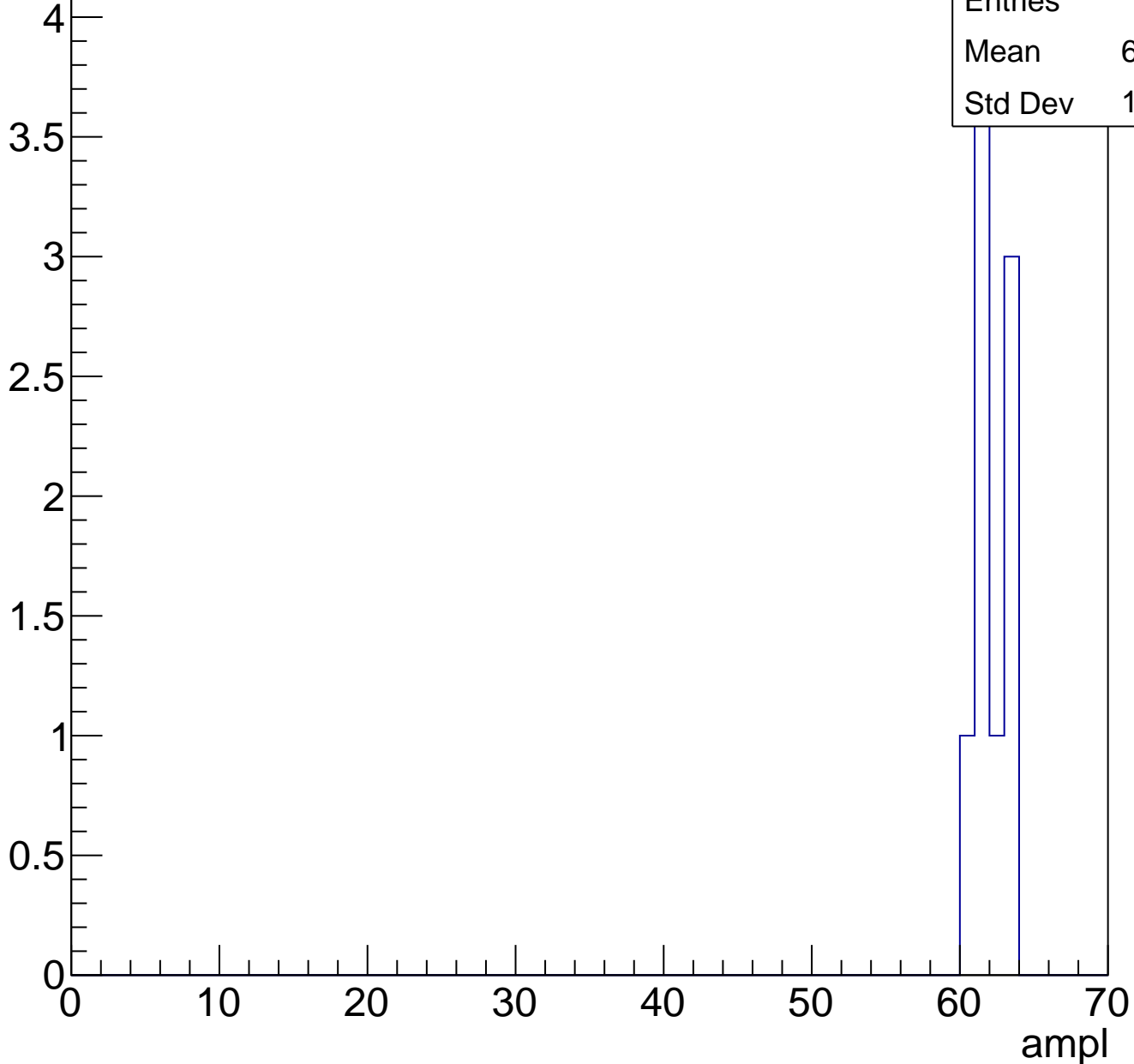
Entries	56
Mean	57.89
Std Dev	8.453



B1L102S, U14-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch1, adc0

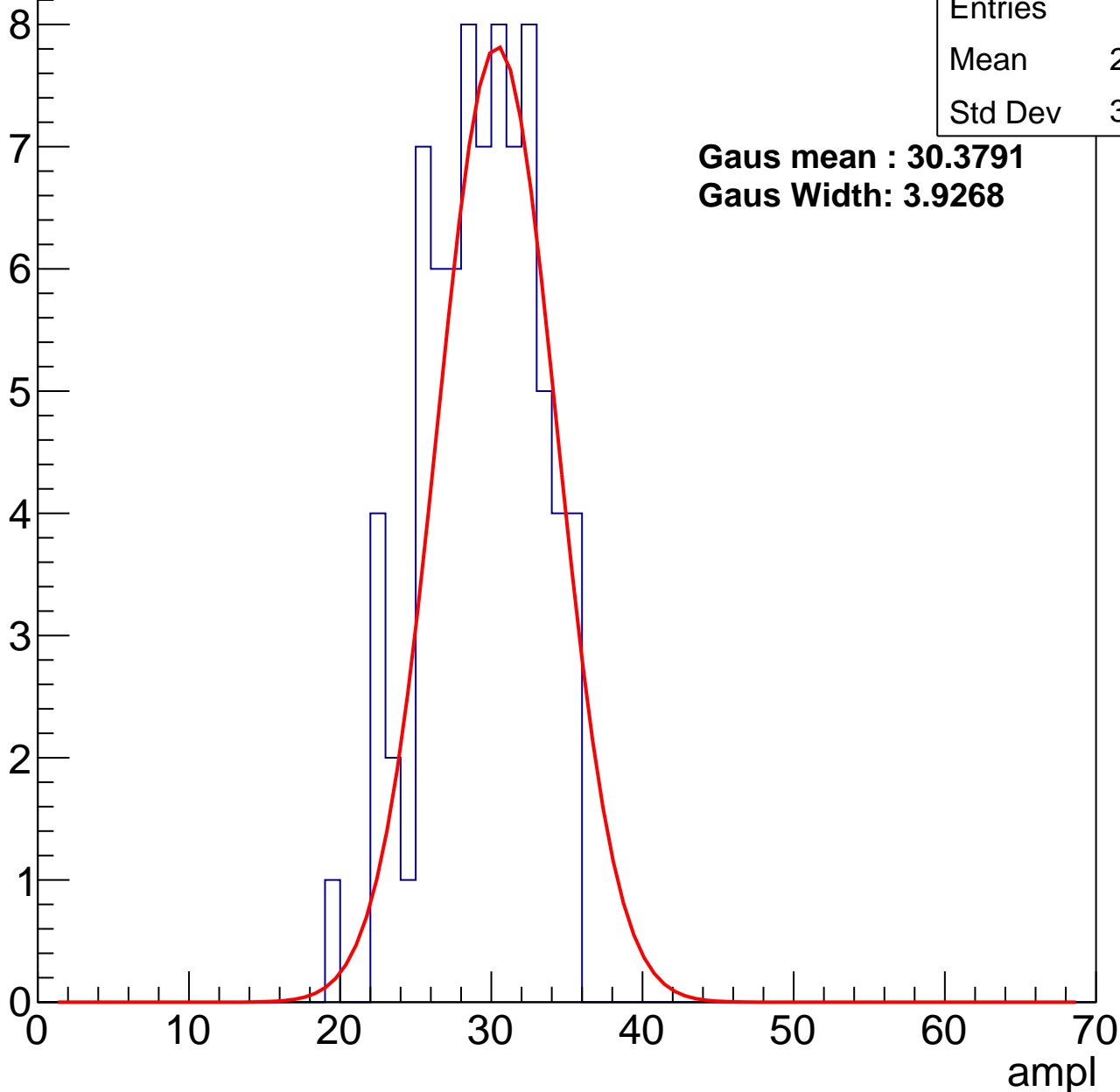
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.86
Std Dev	3.619

Gaus mean : 30.3791

Gaus Width: 3.9268



B1L102S, U14-ch1, adc1

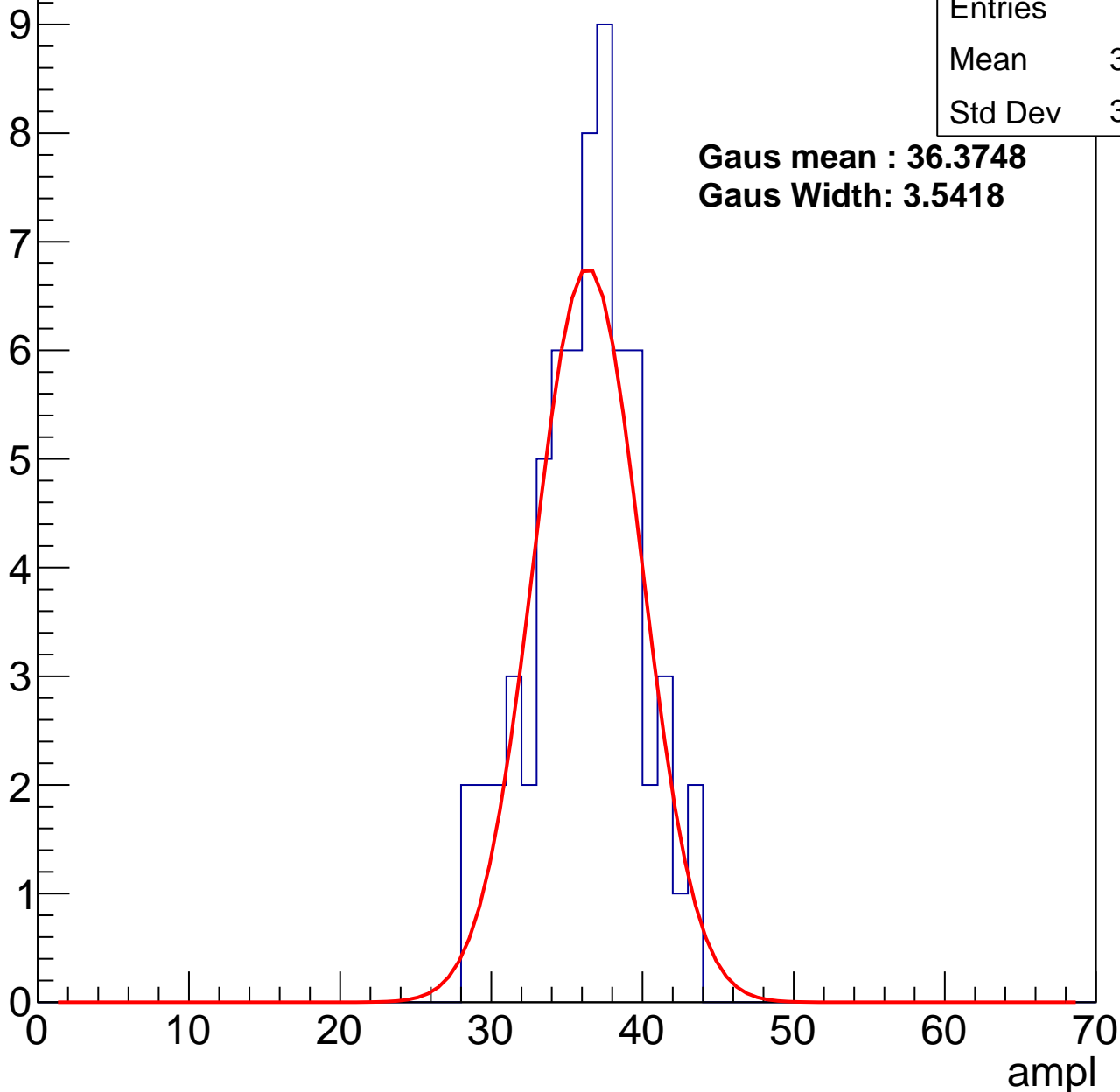
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.75
Std Dev	3.517

Gaus mean : 36.3748

Gaus Width: 3.5418



B1L102S, U14-ch1, adc2

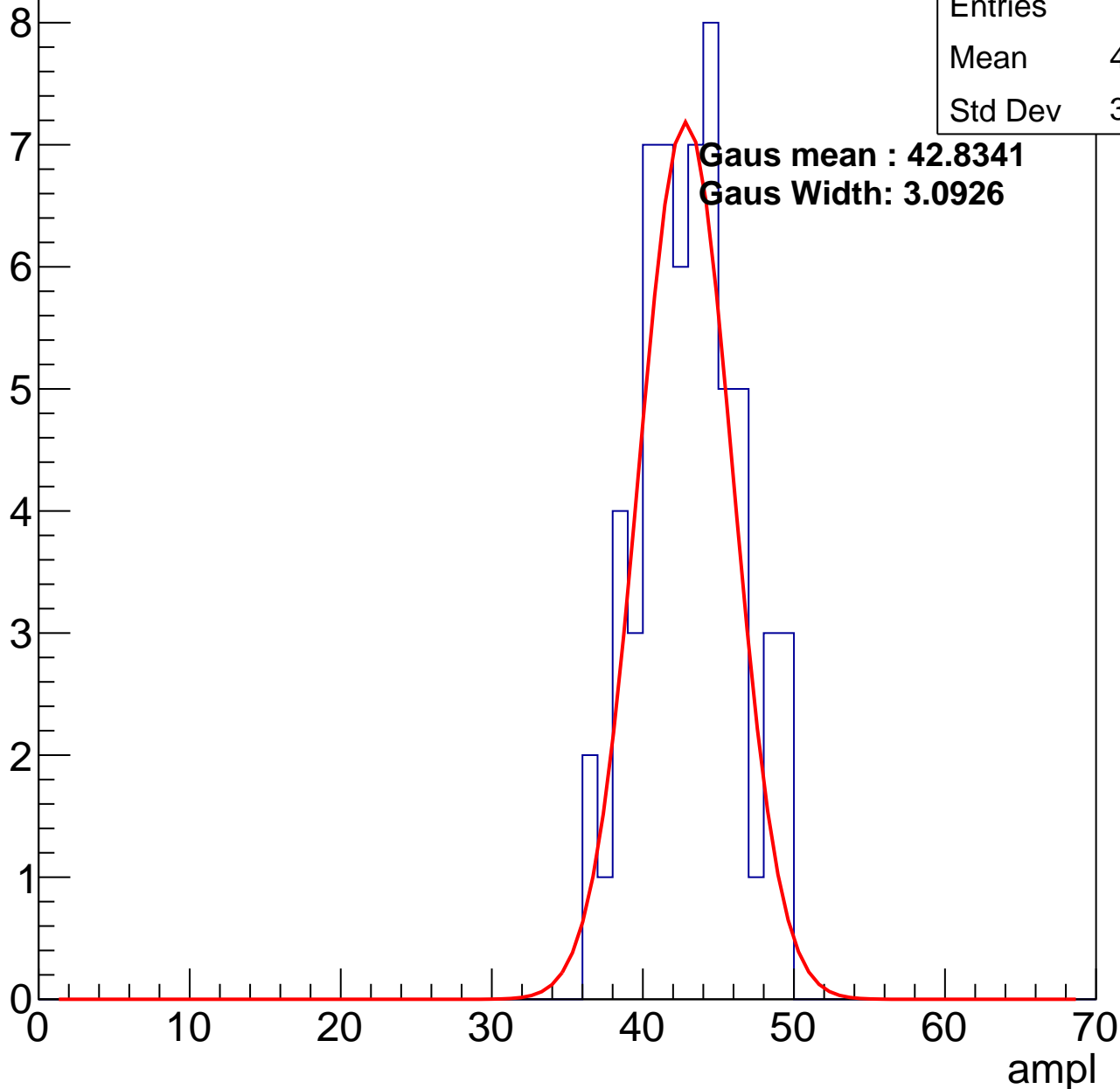
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.63
Std Dev	3.219

Gaus mean : 42.8341

Gaus Width: 3.0926

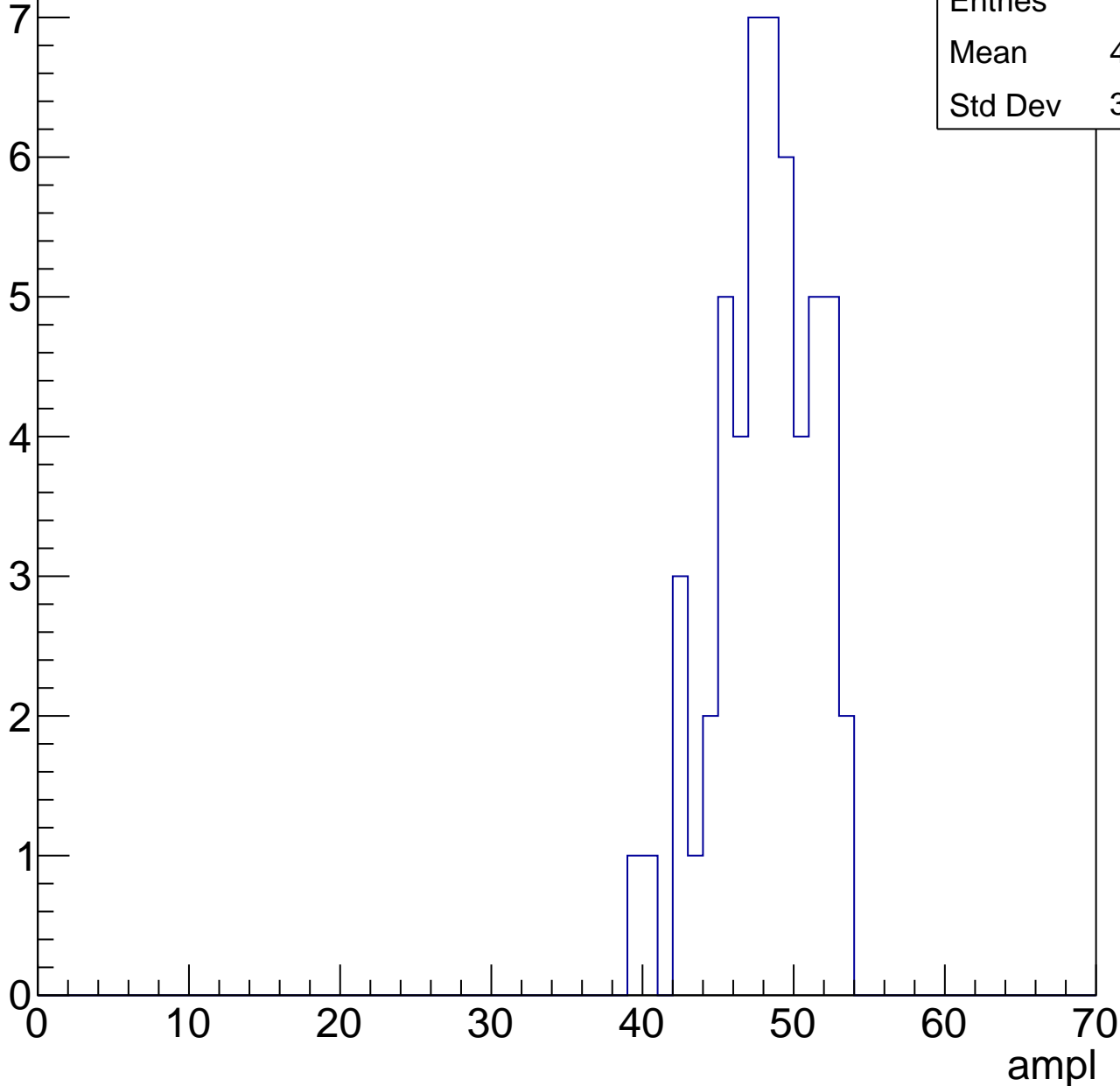


B1L102S, U14-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	47.64
Std Dev	3.274

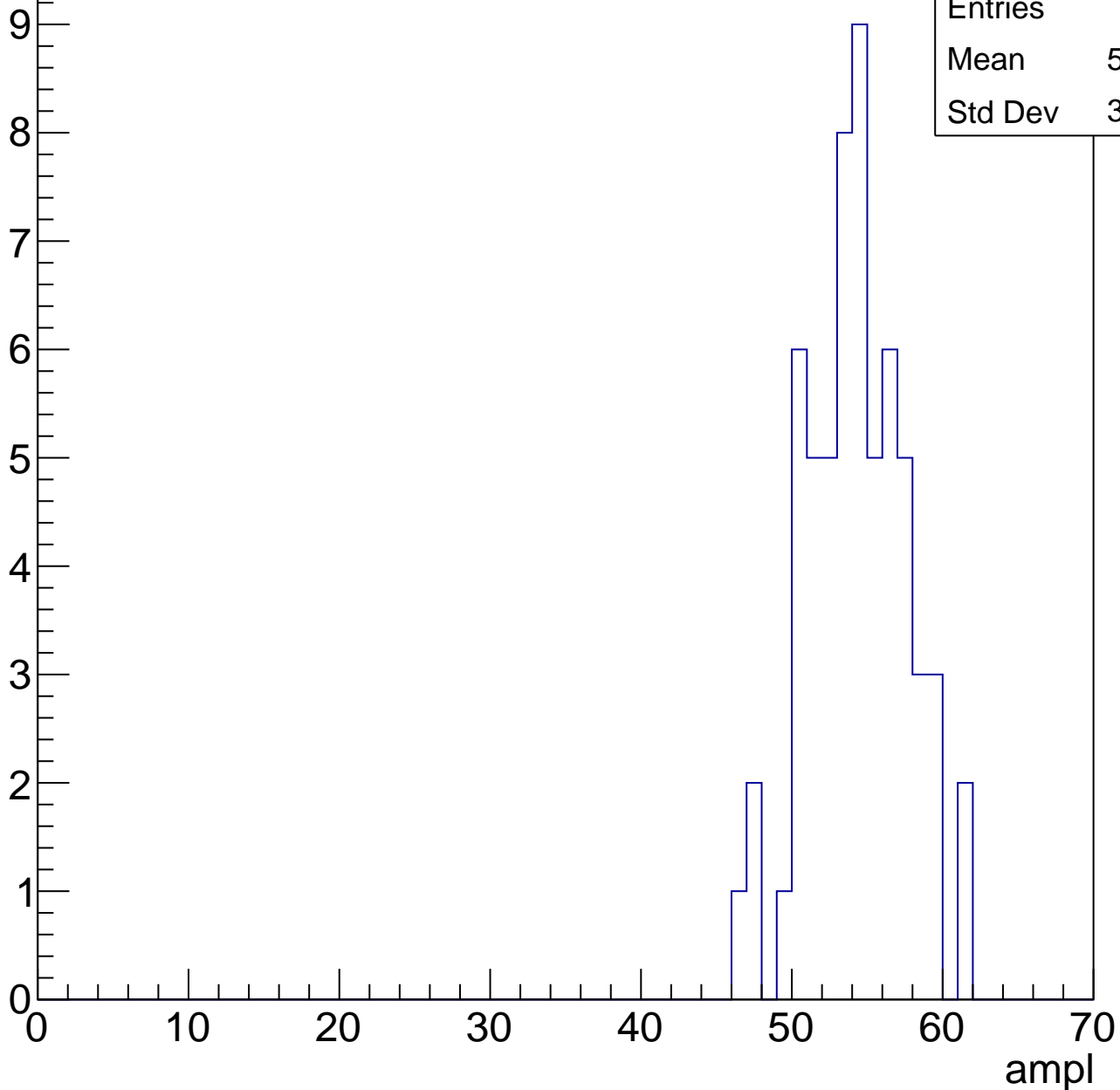


B1L102S, U14-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.82
Std Dev	3.262

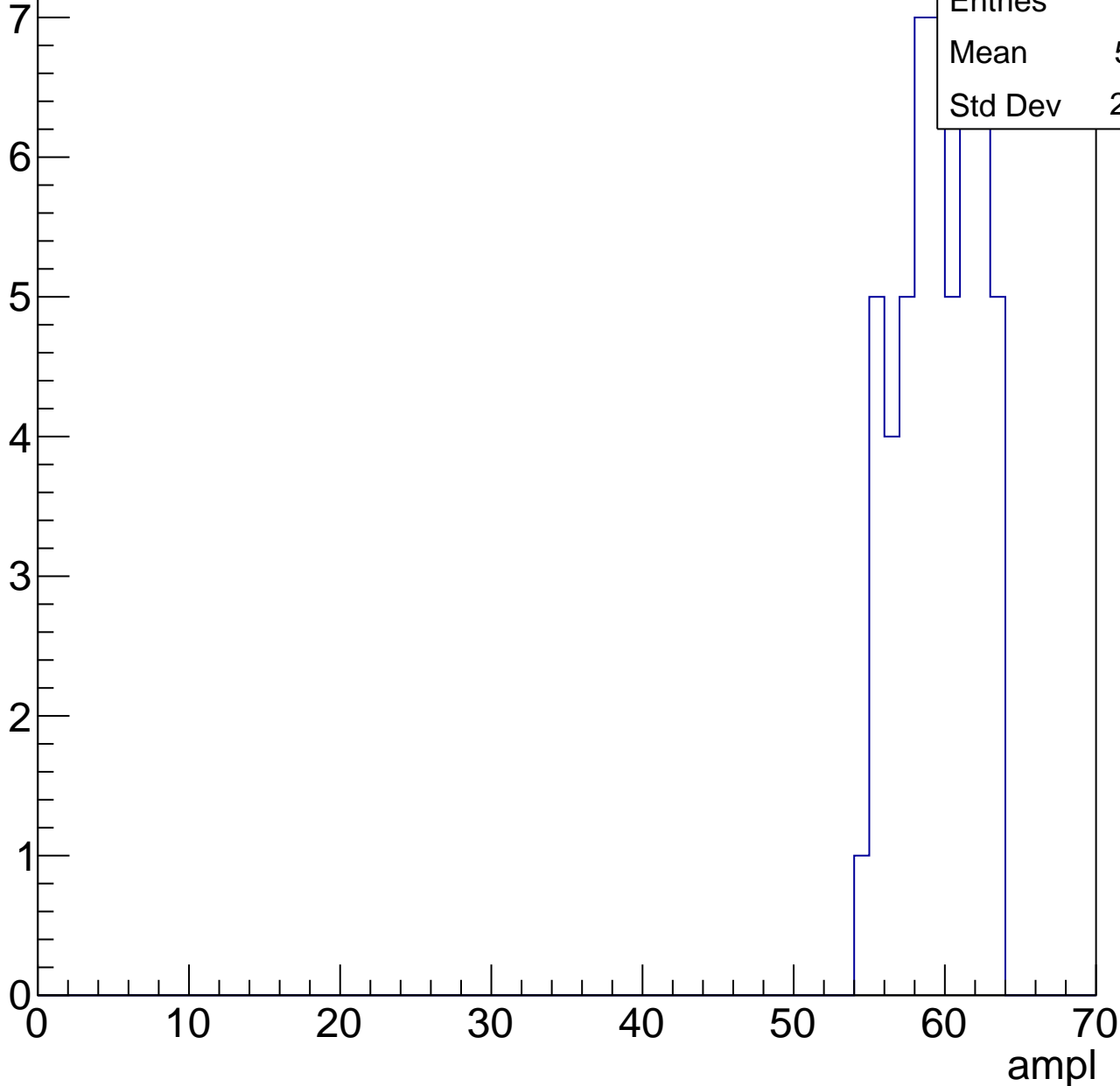


B1L102S, U14-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	59.11
Std Dev	2.545

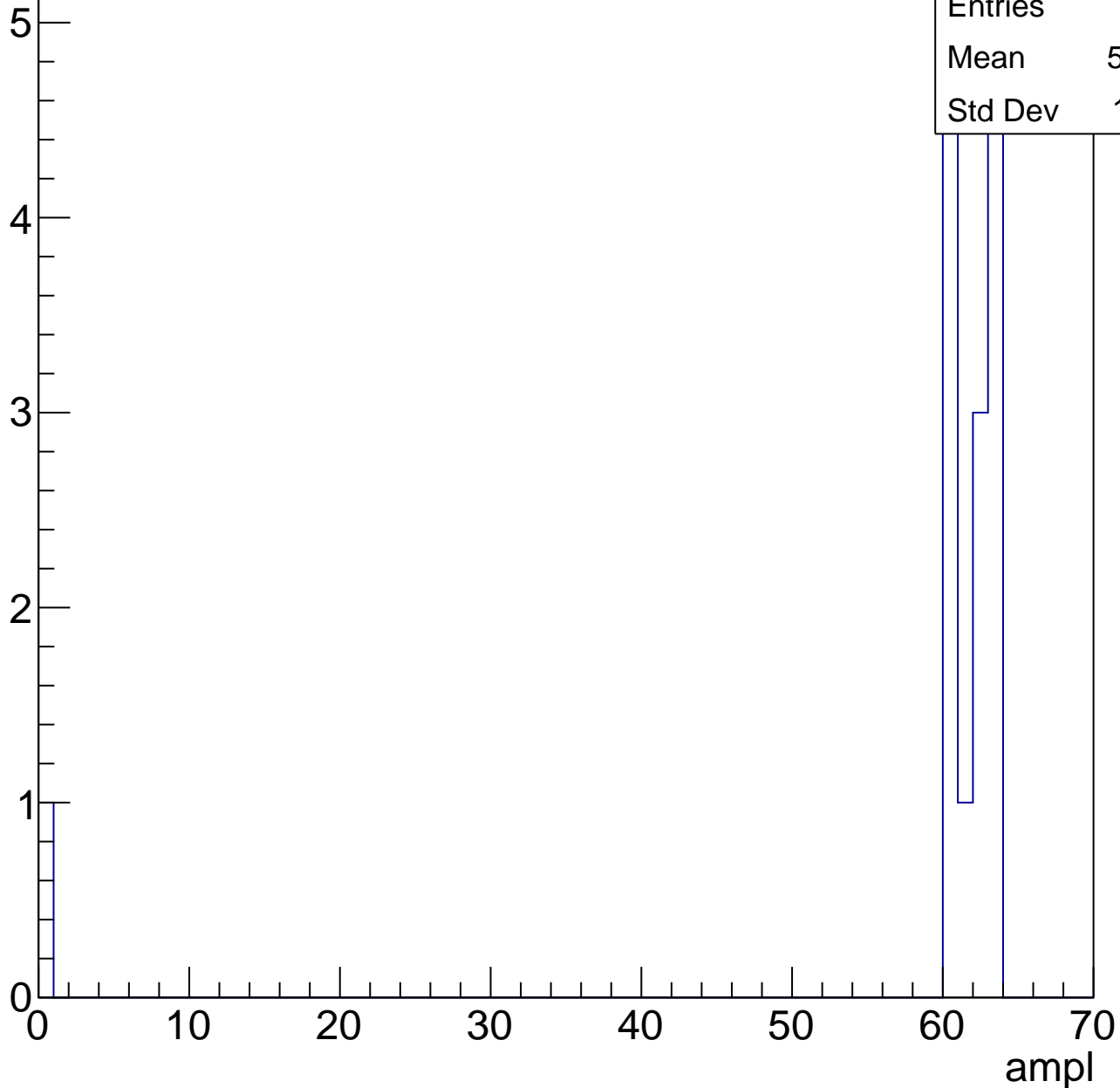


B1L102S, U14-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.47
Std Dev	15.41



B1L102S, U14-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch2, adc0

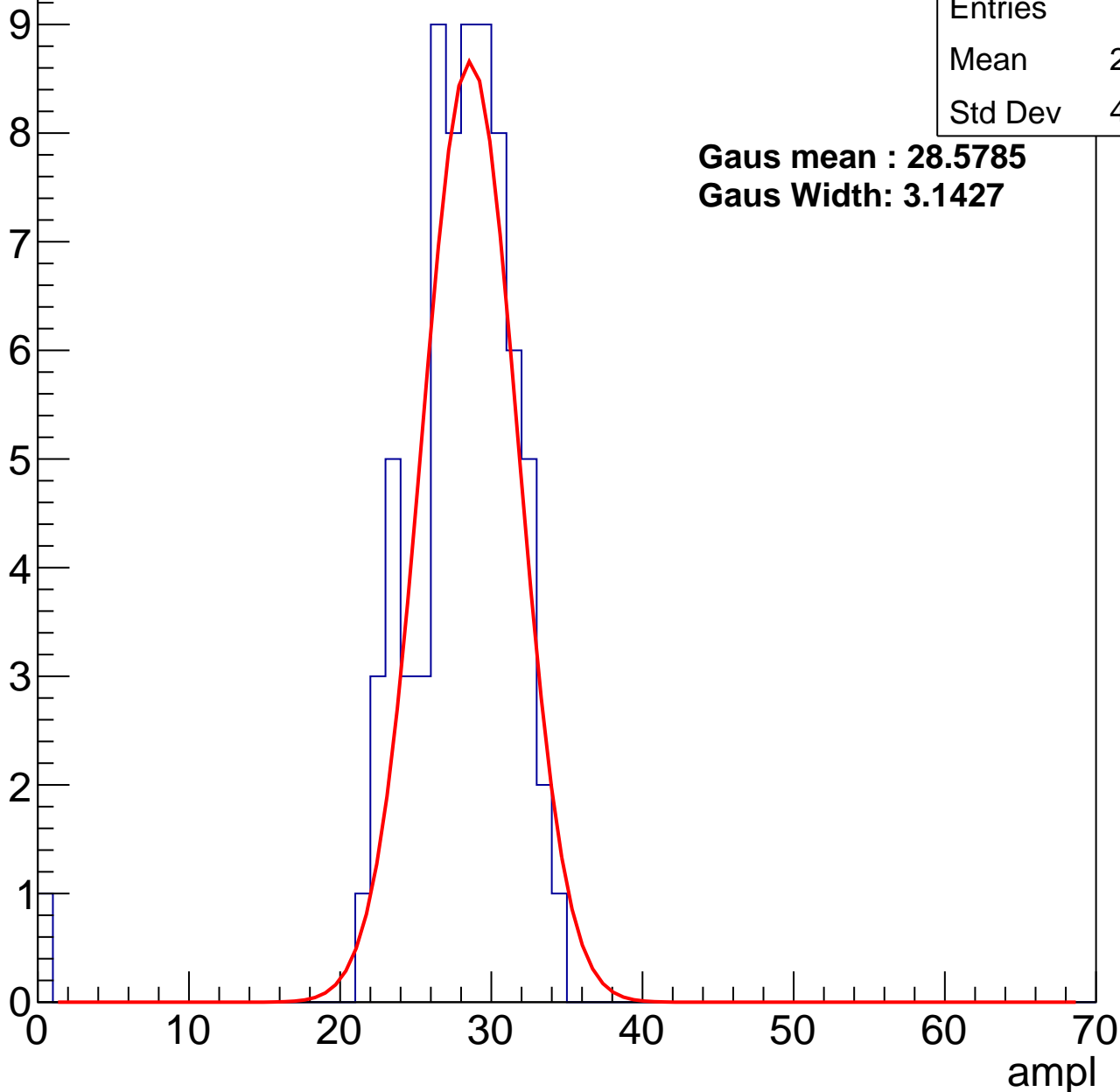
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.37
Std Dev	4.406

Gaus mean : 28.5785

Gaus Width: 3.1427



B1L102S, U14-ch2, adc1

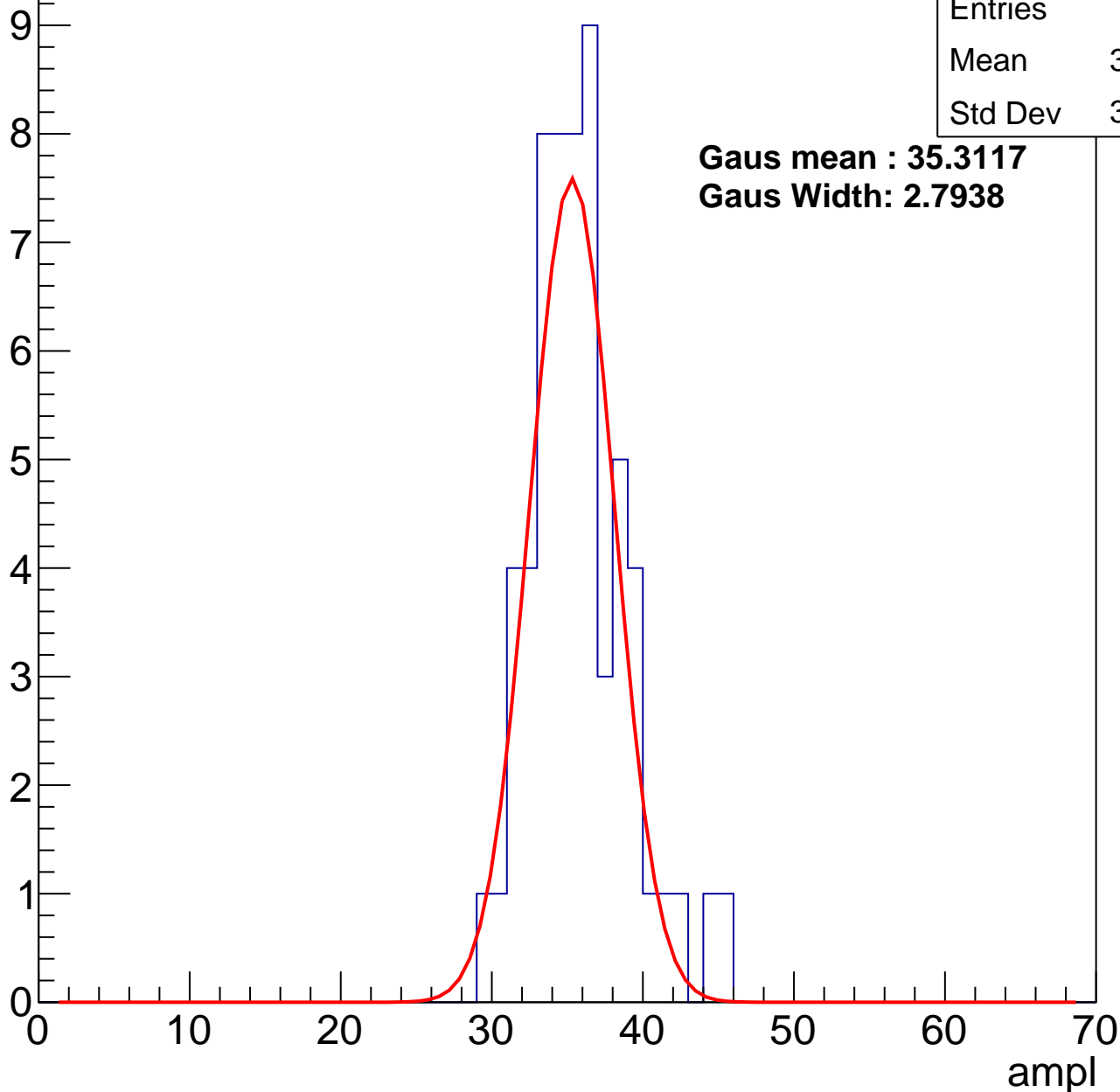
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.33
Std Dev	3.202

Gaus mean : 35.3117

Gaus Width: 2.7938



B1L102S, U14-ch2, adc2

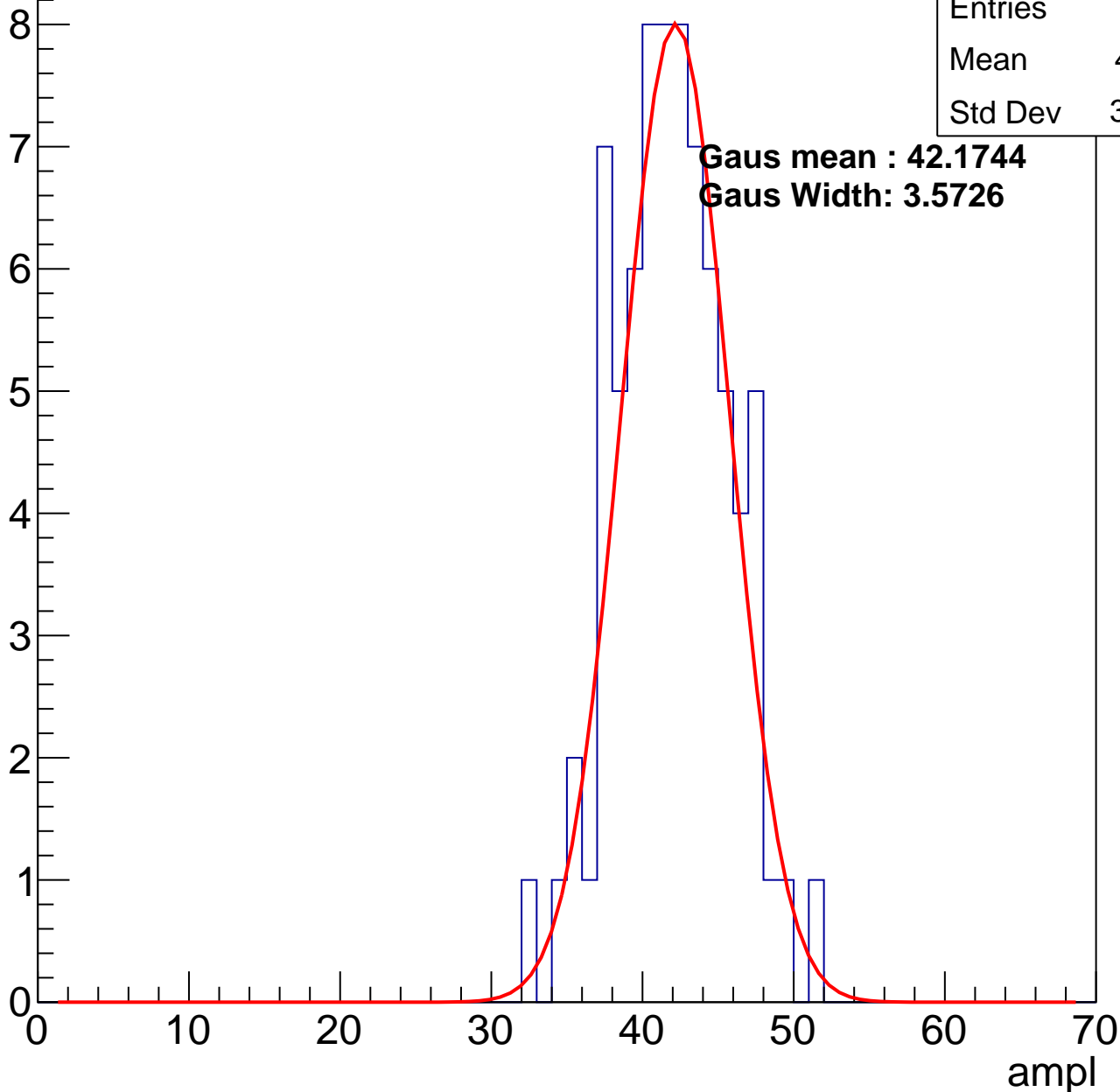
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	41.51
Std Dev	3.709

Gaus mean : 42.1744

Gaus Width: 3.5726

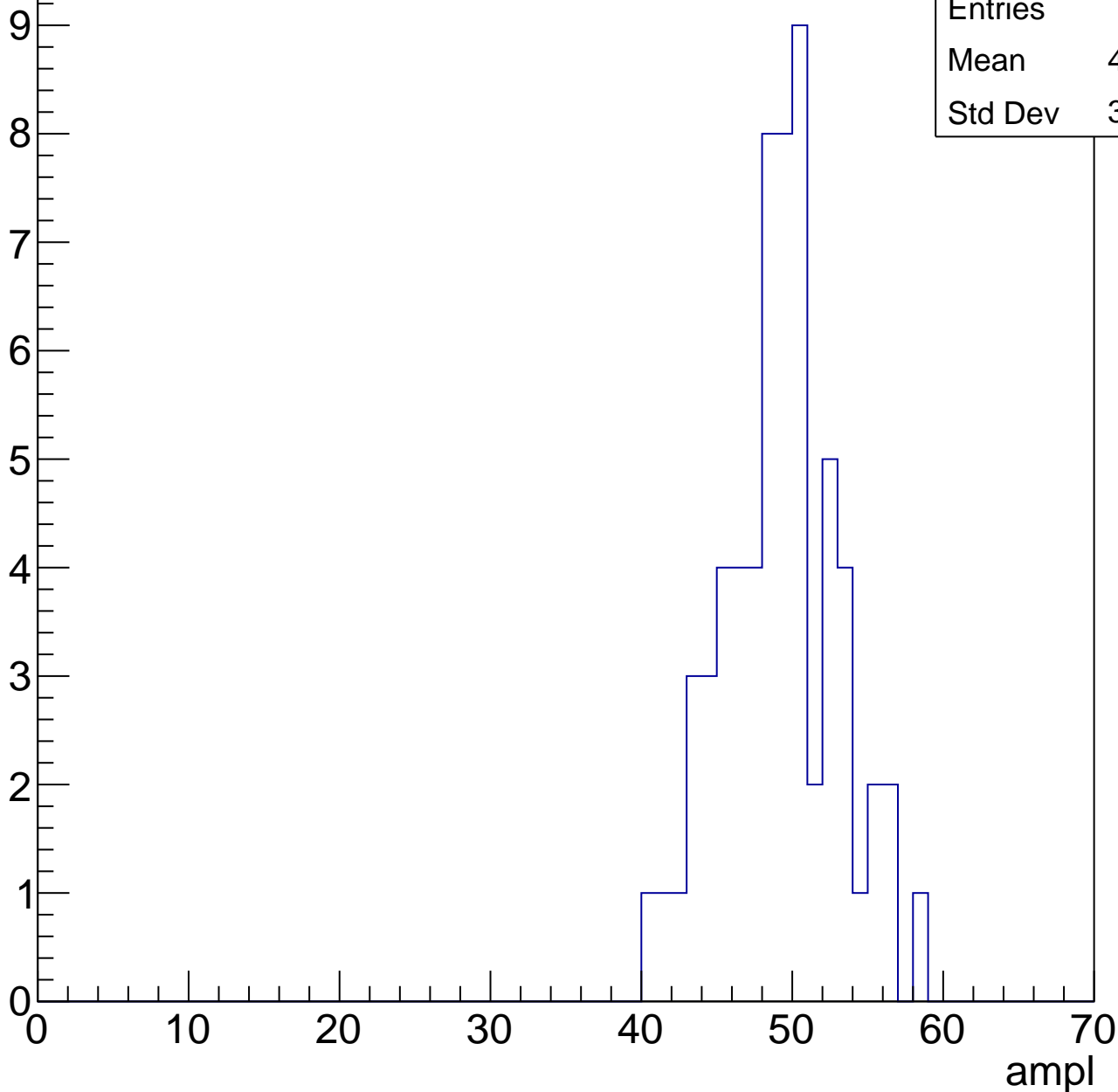


B1L102S, U14-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.73
Std Dev	3.776

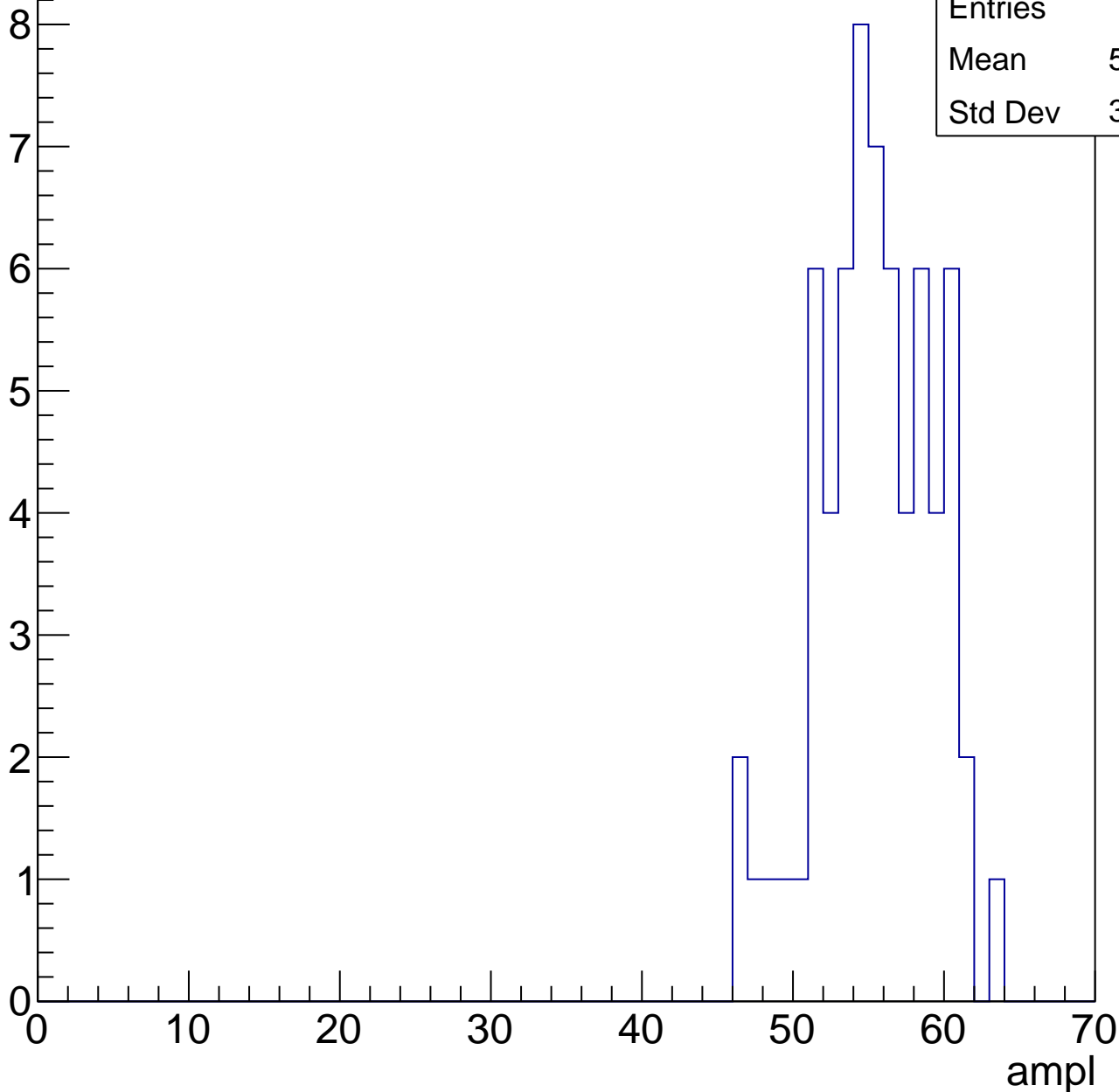


B1L102S, U14-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.97
Std Dev	3.746

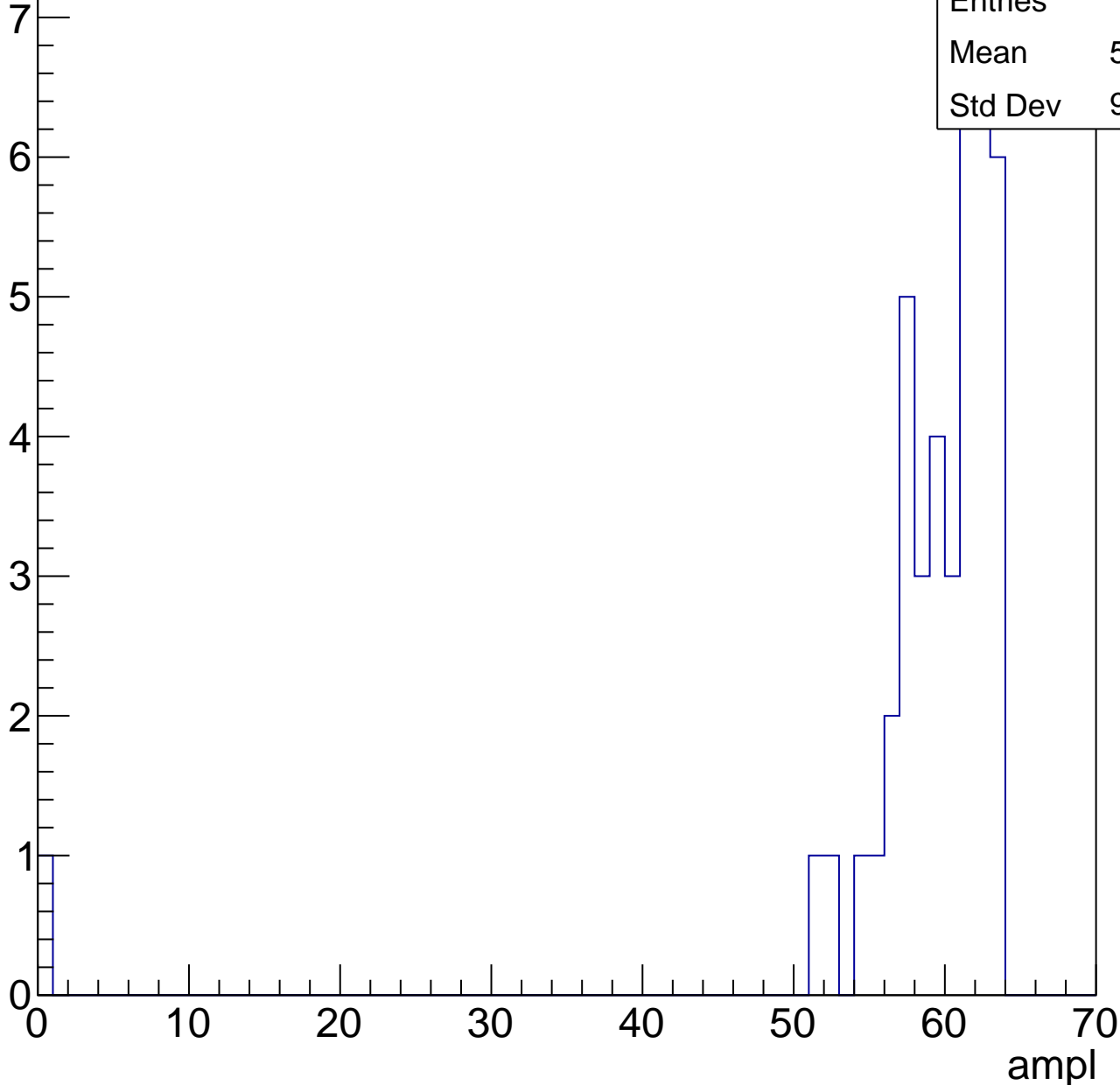


B1L102S, U14-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

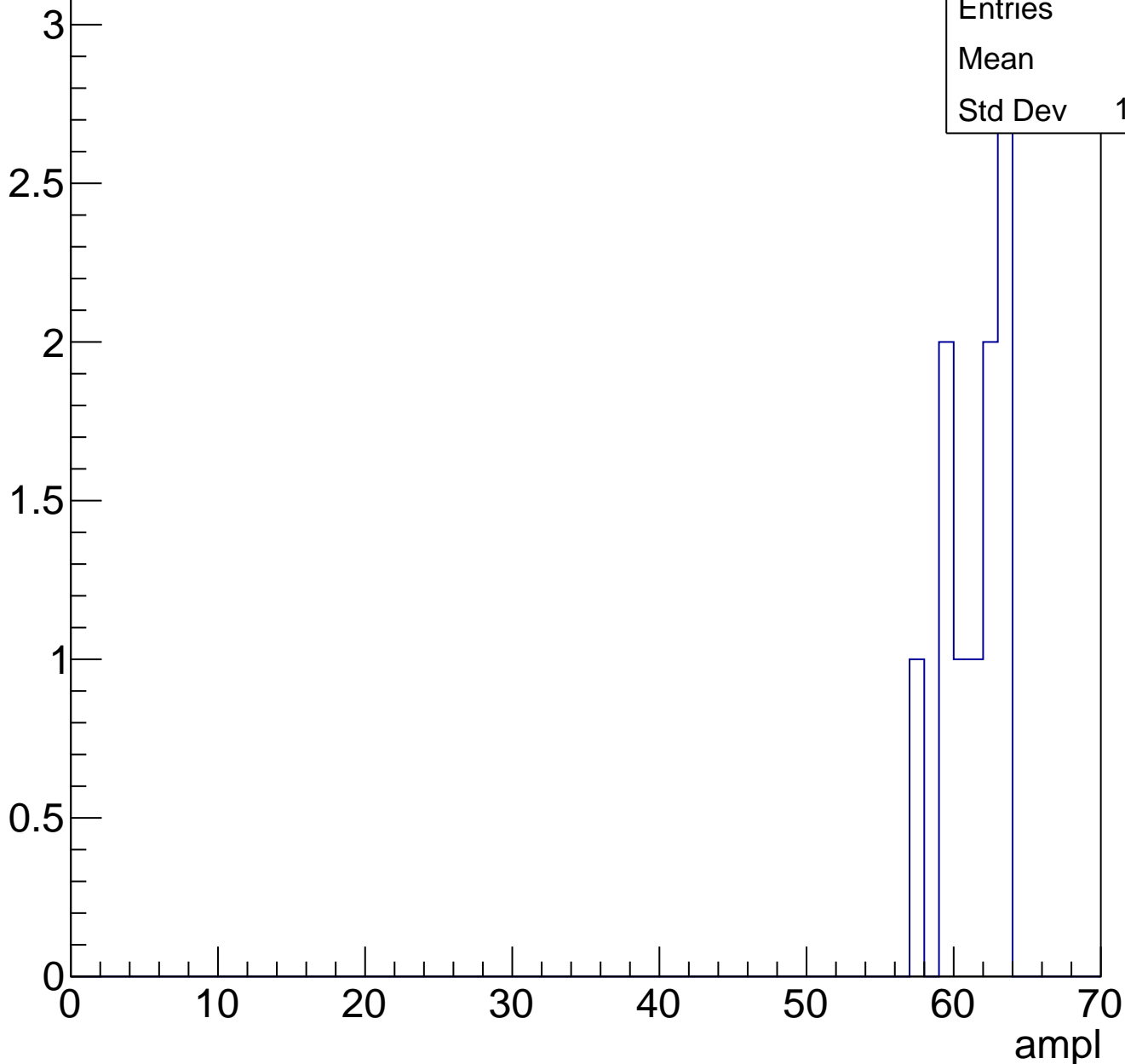
Entries	42
Mean	58.05
Std Dev	9.547



B1L102S, U14-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

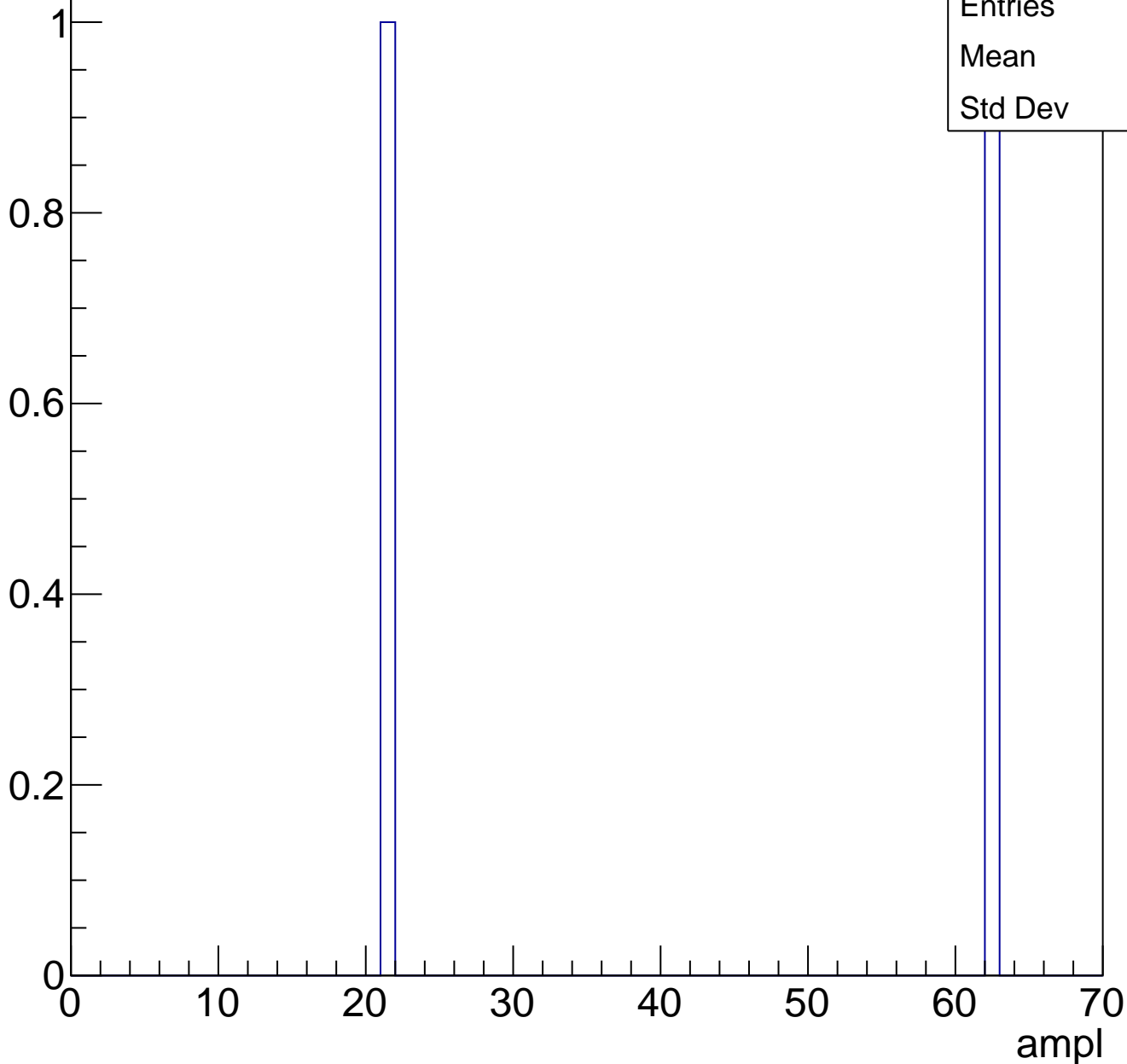
Entry



B1L102S, U14-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch3, adc0

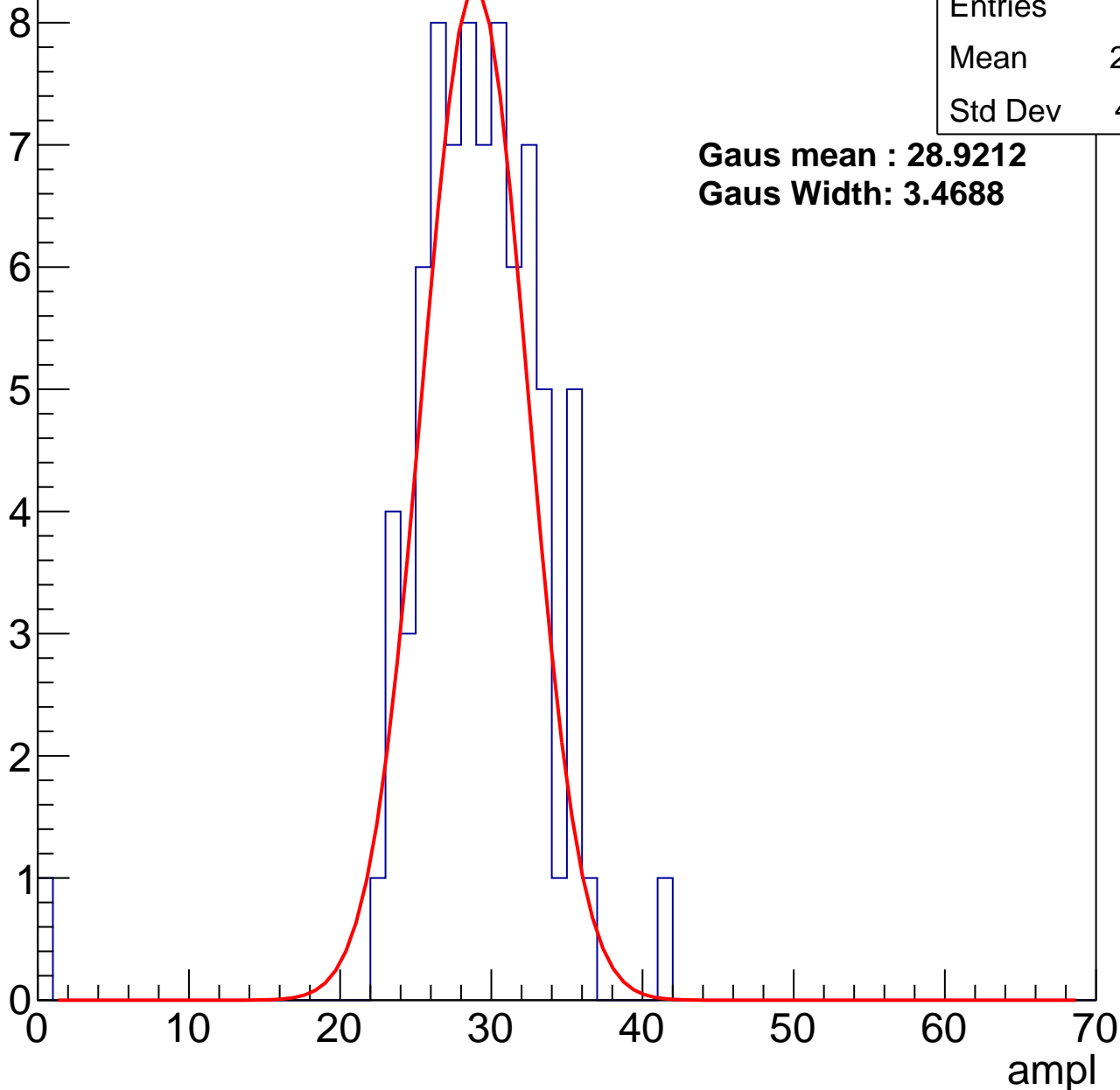
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.62
Std Dev	4.871

Gaus mean : 28.9212

Gaus Width: 3.4688



B1L102S, U14-ch3, adc1

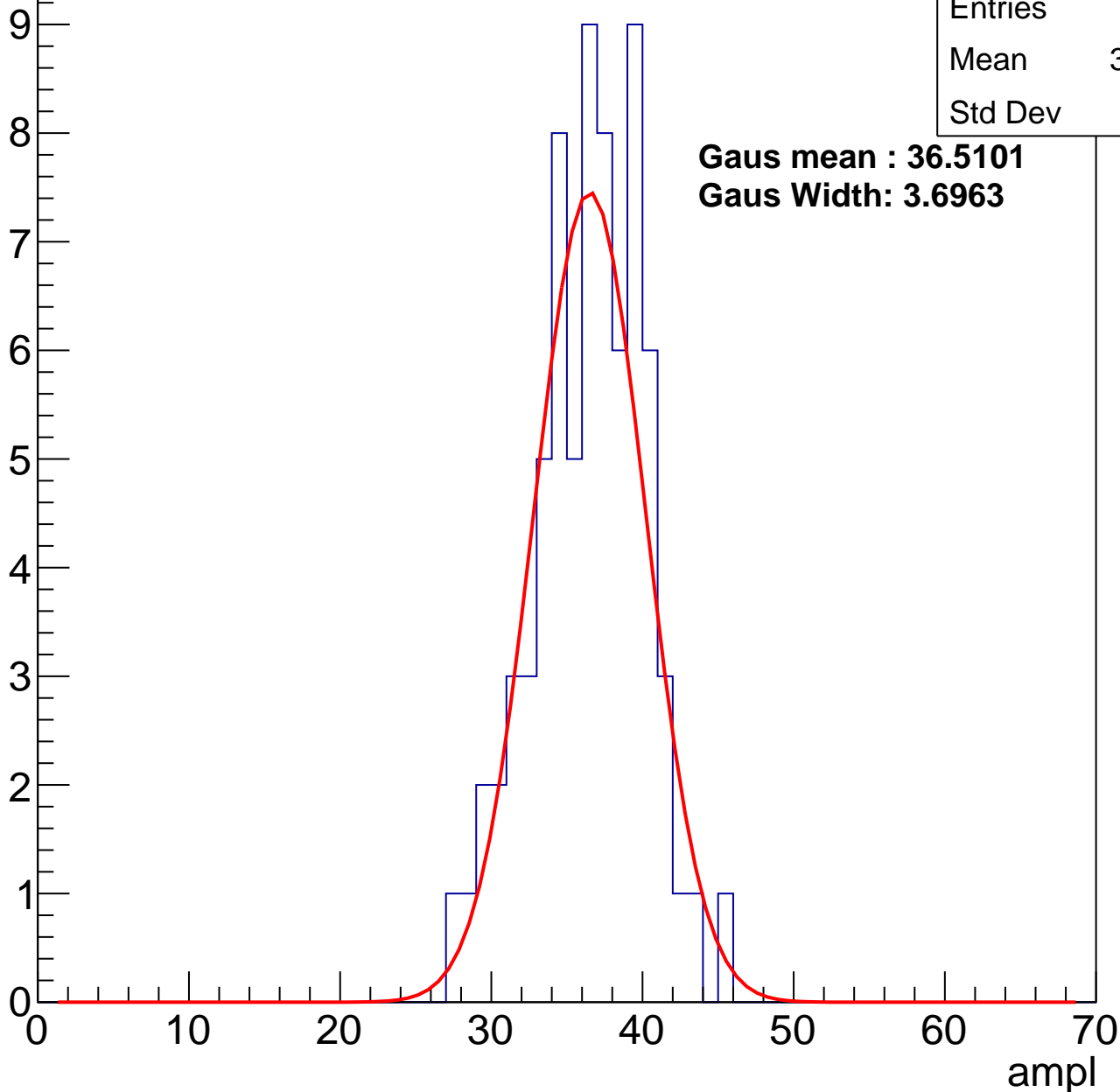
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.03
Std Dev	3.62

Gaus mean : 36.5101

Gaus Width: 3.6963



B1L102S, U14-ch3, adc2

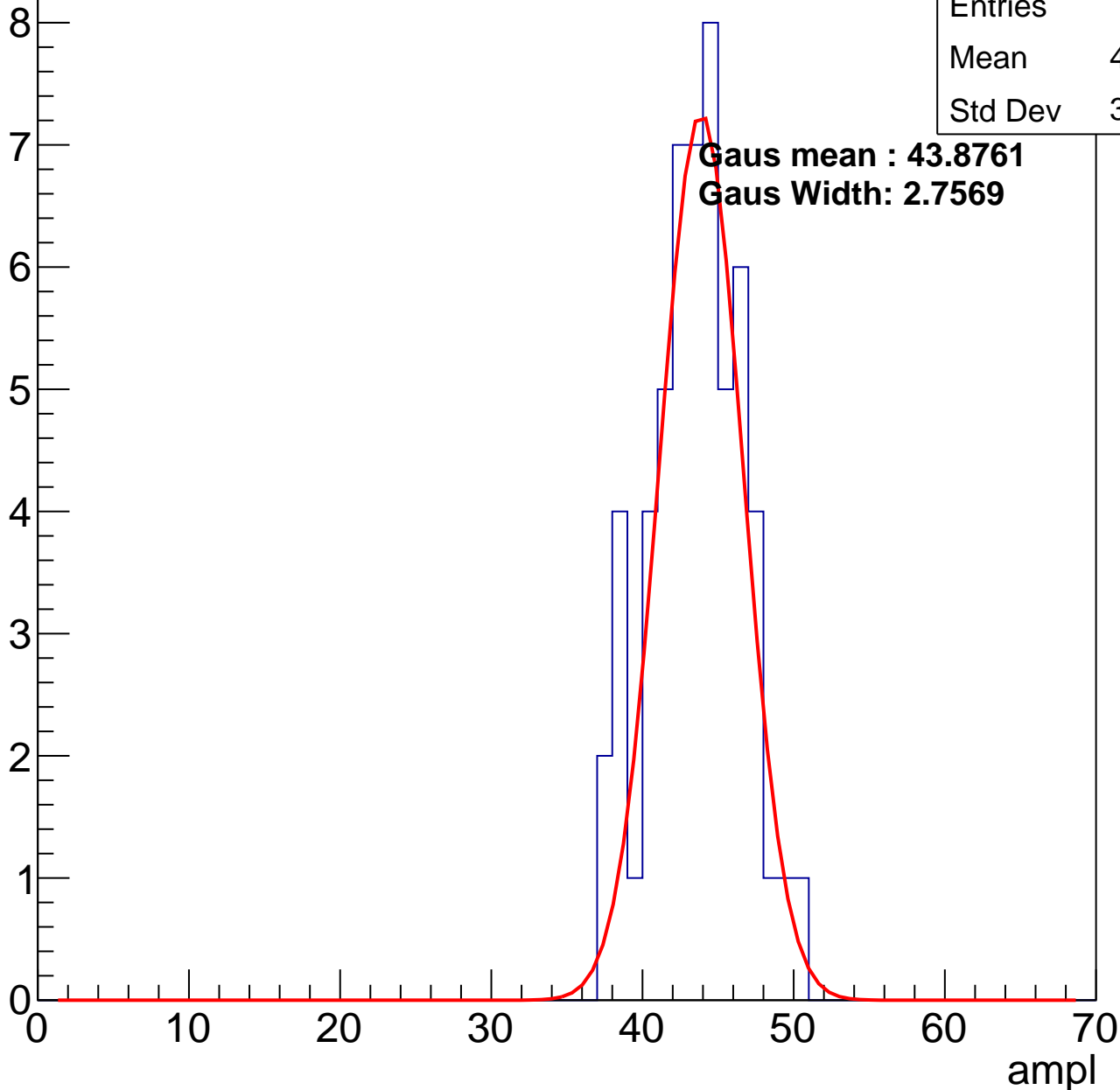
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.09
Std Dev	3.008

Gaus mean : 43.8761

Gaus Width: 2.7569

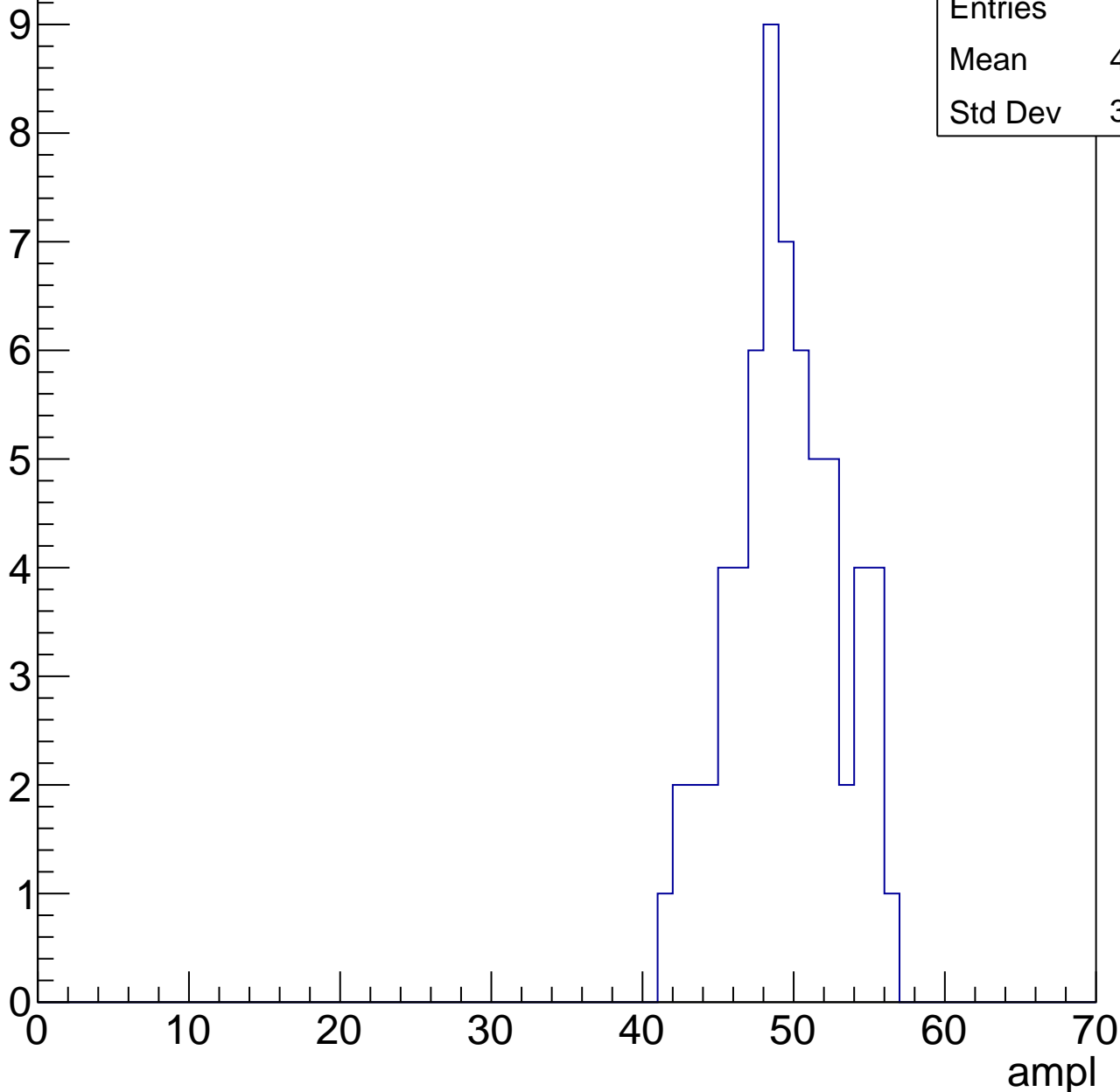


B1L102S, U14-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.95
Std Dev	3.564

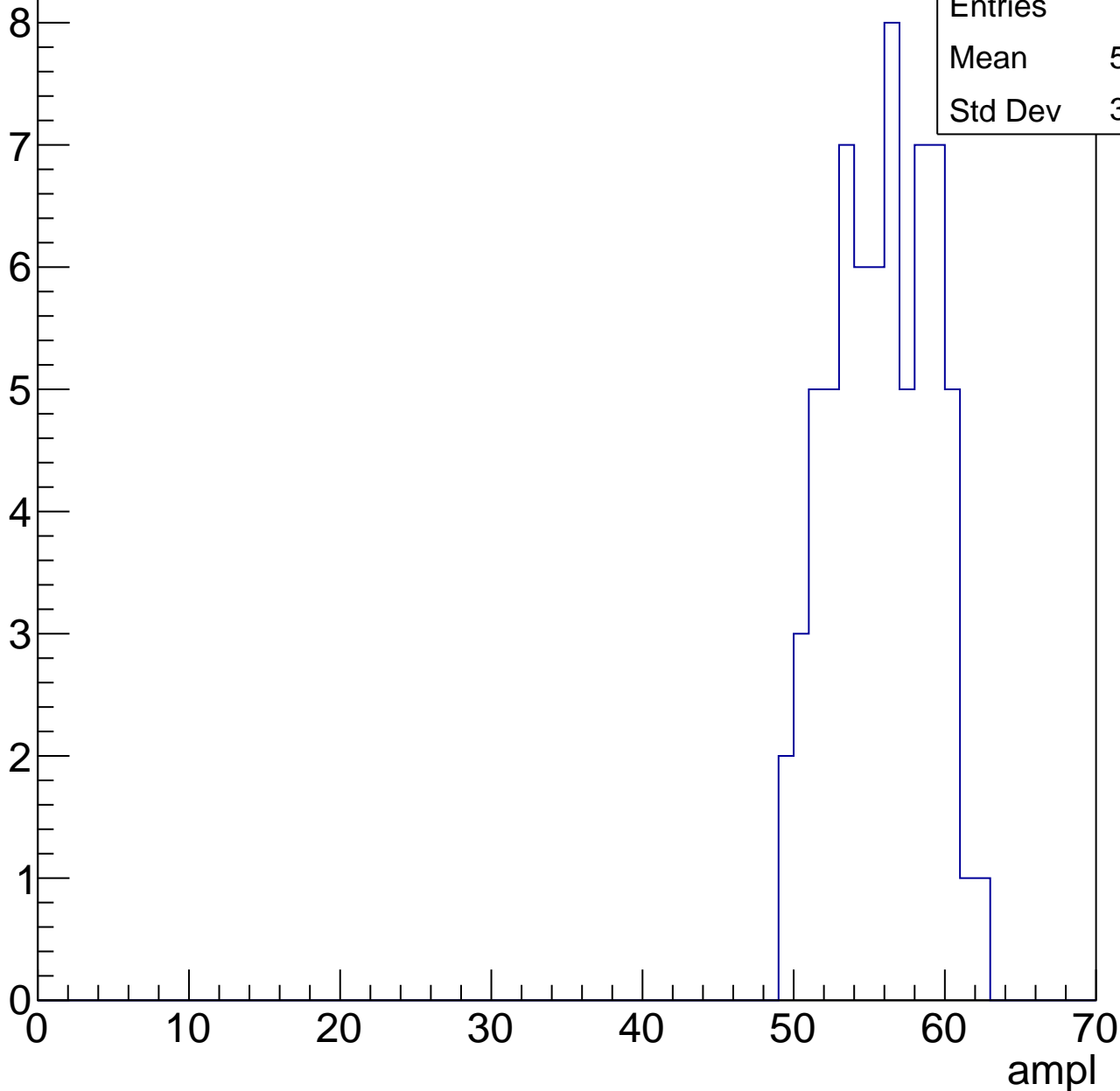


B1L102S, U14-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	55.34
Std Dev	3.234

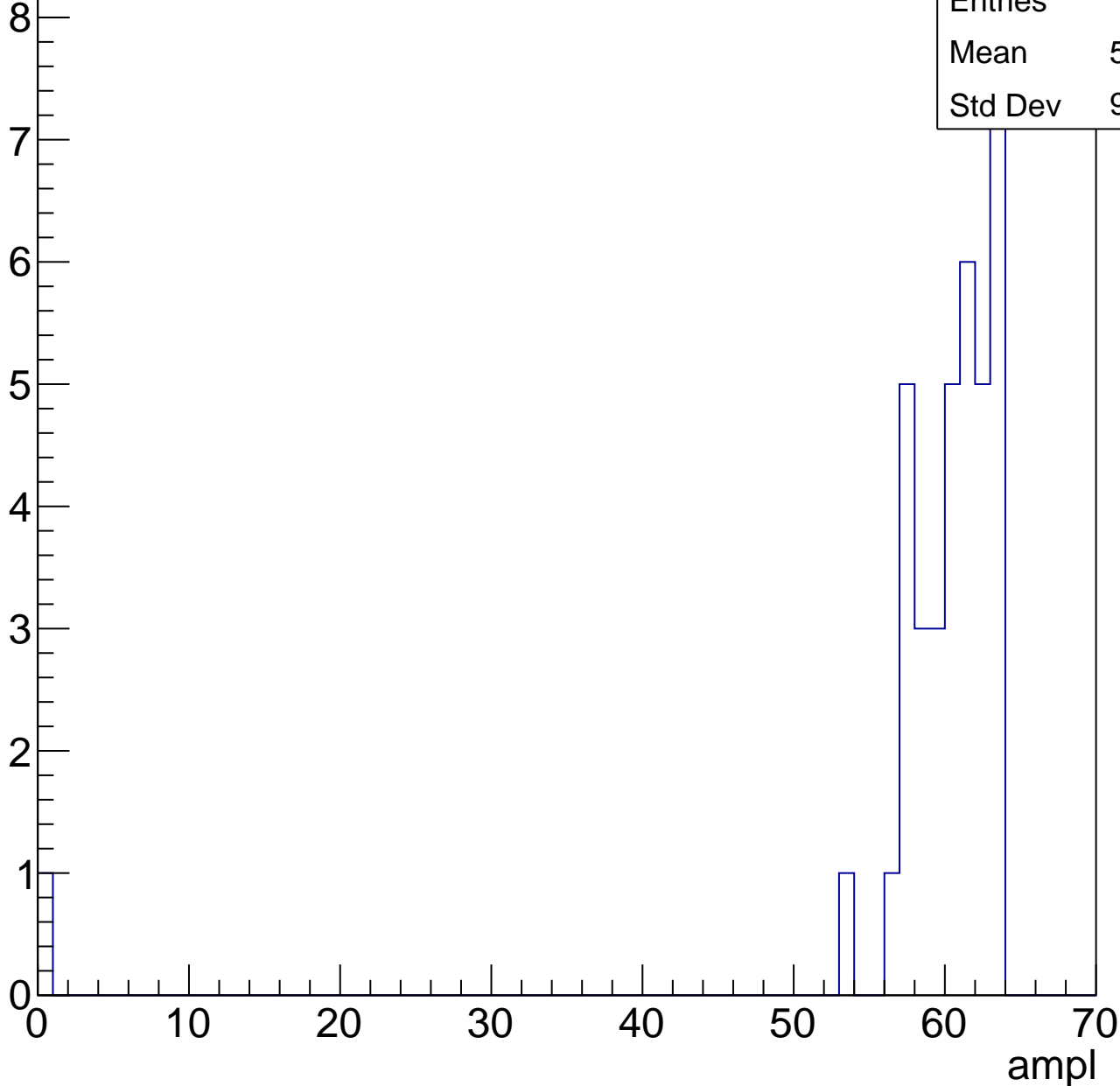


B1L102S, U14-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.55
Std Dev	9.925



B1L102S, U14-ch3, adc6

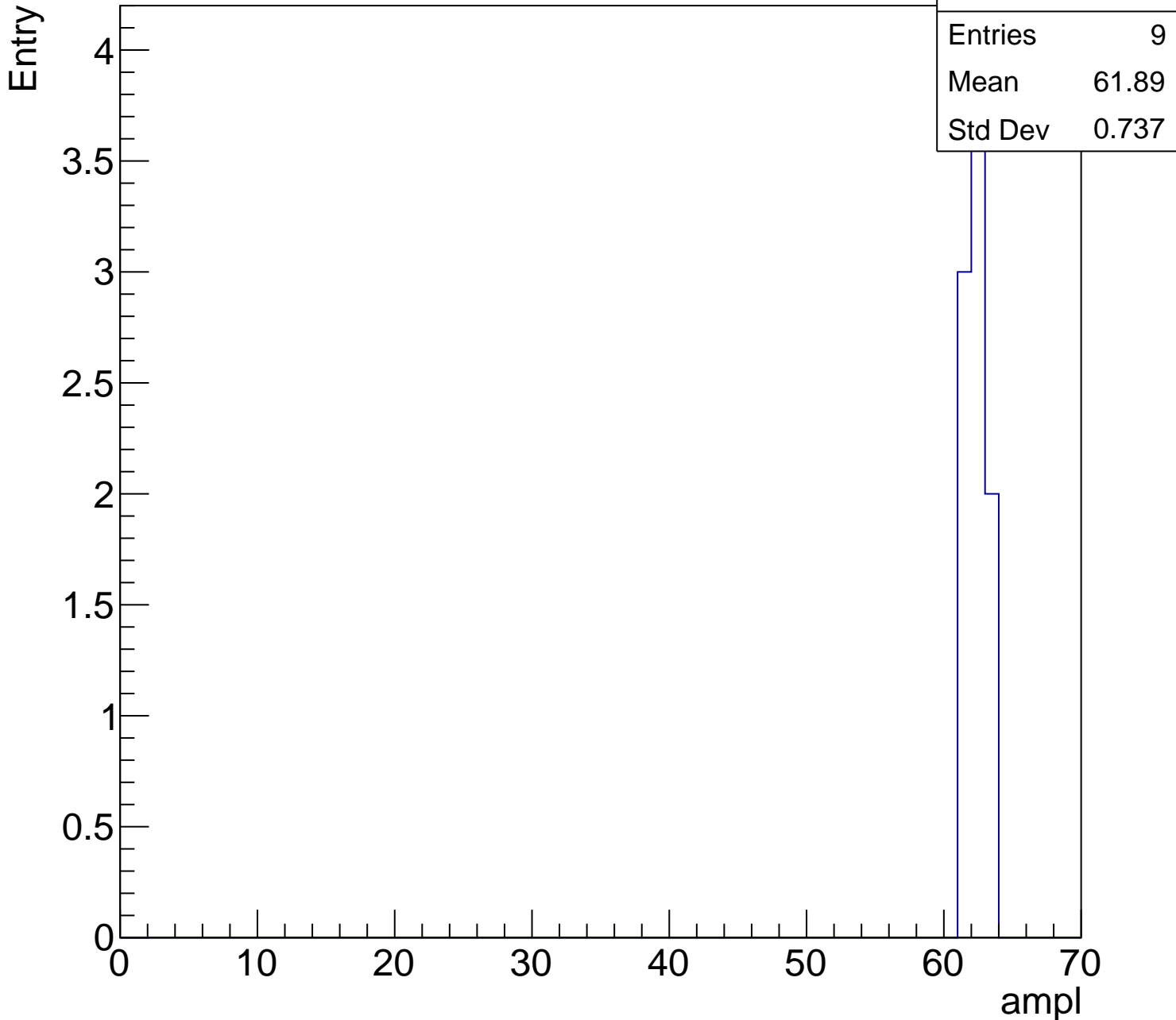
calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.89
Std Dev	0.737

ampl



B1L102S, U14-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U14-ch4, adc0

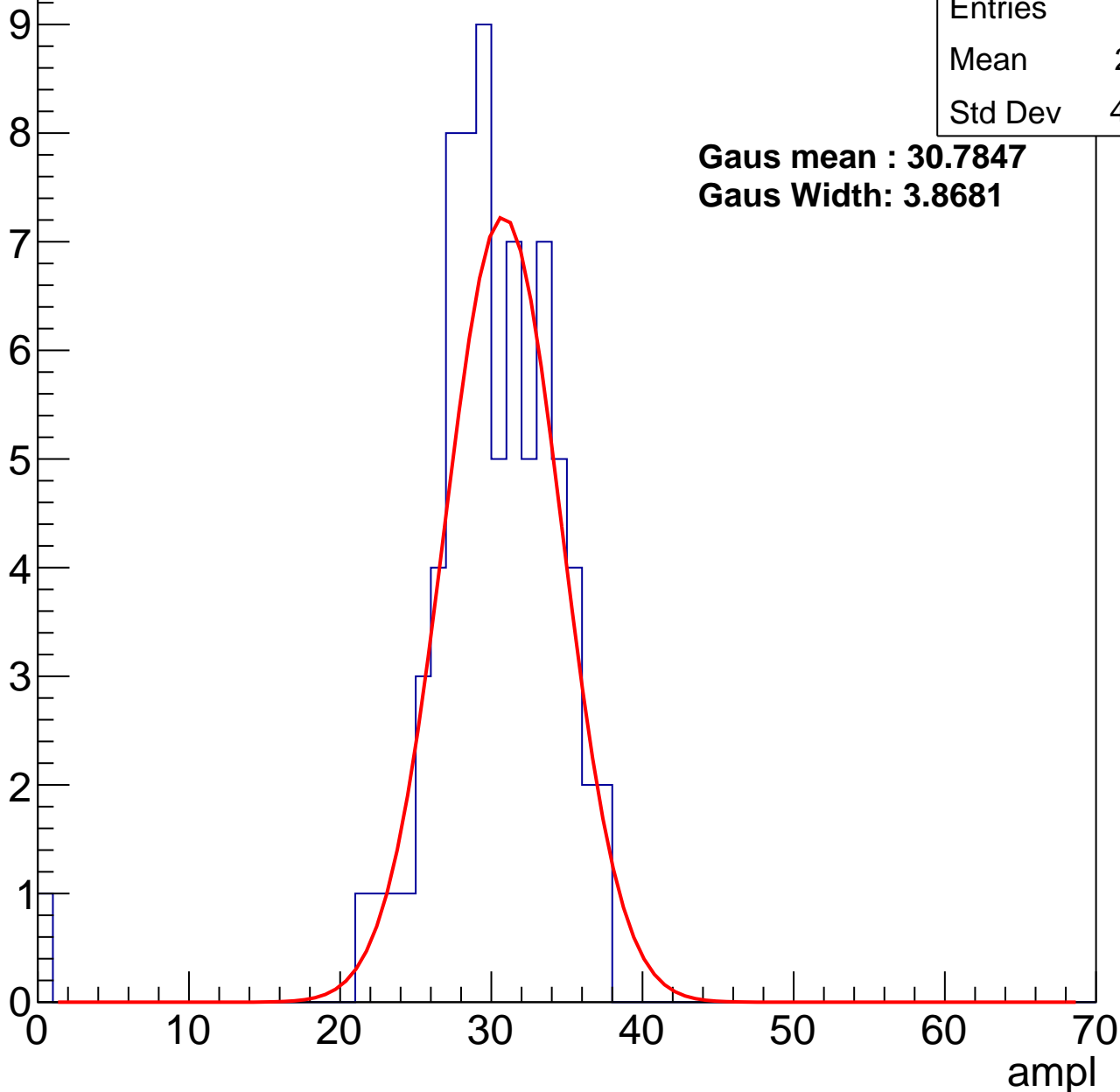
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29.51
Std Dev	4.938

Gaus mean : 30.7847

Gaus Width: 3.8681



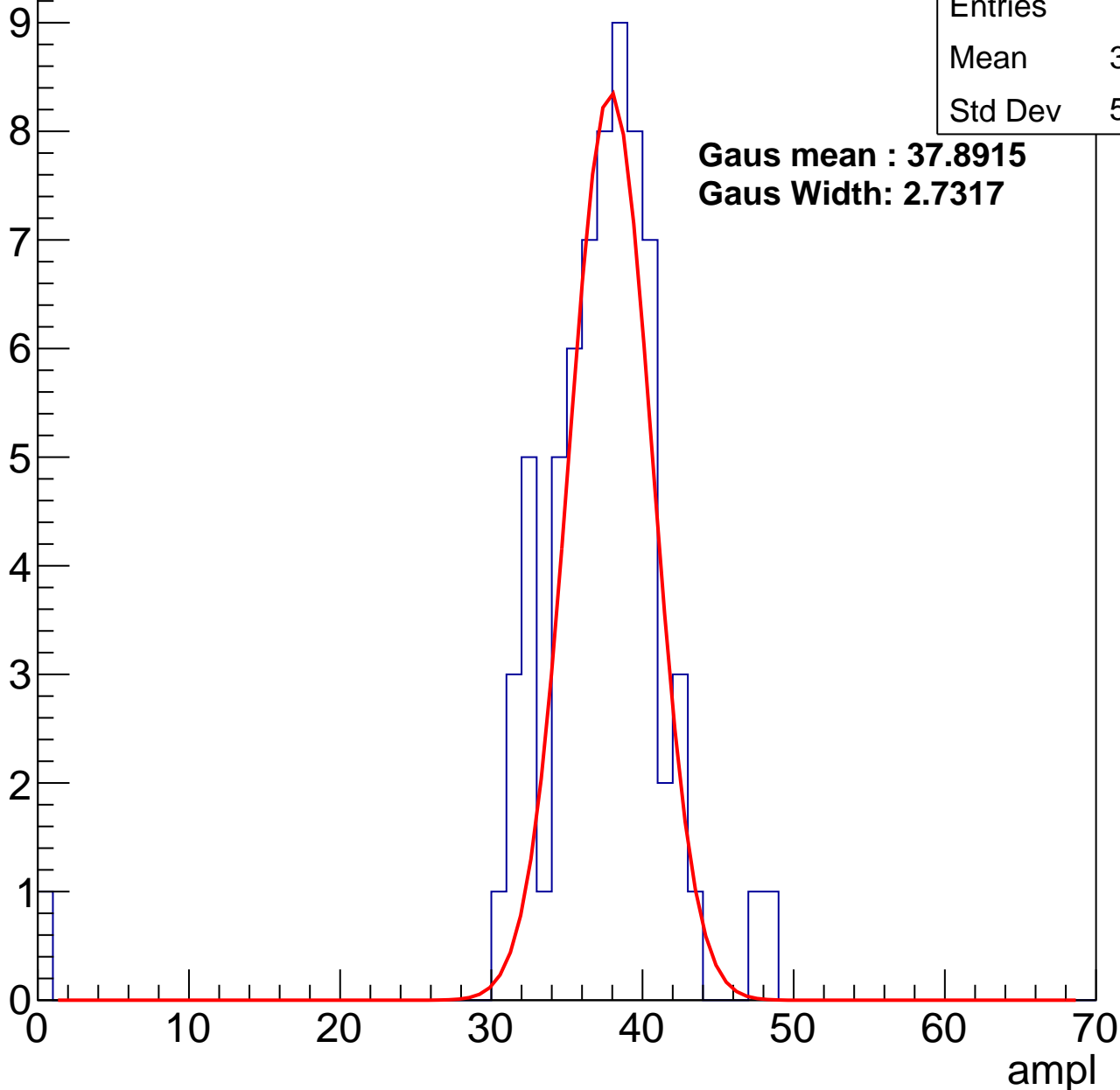
B1L102S, U14-ch4, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.58
Std Dev	5.645

Gaus mean : 37.8915
Gaus Width: 2.7317



B1L102S, U14-ch4, adc2

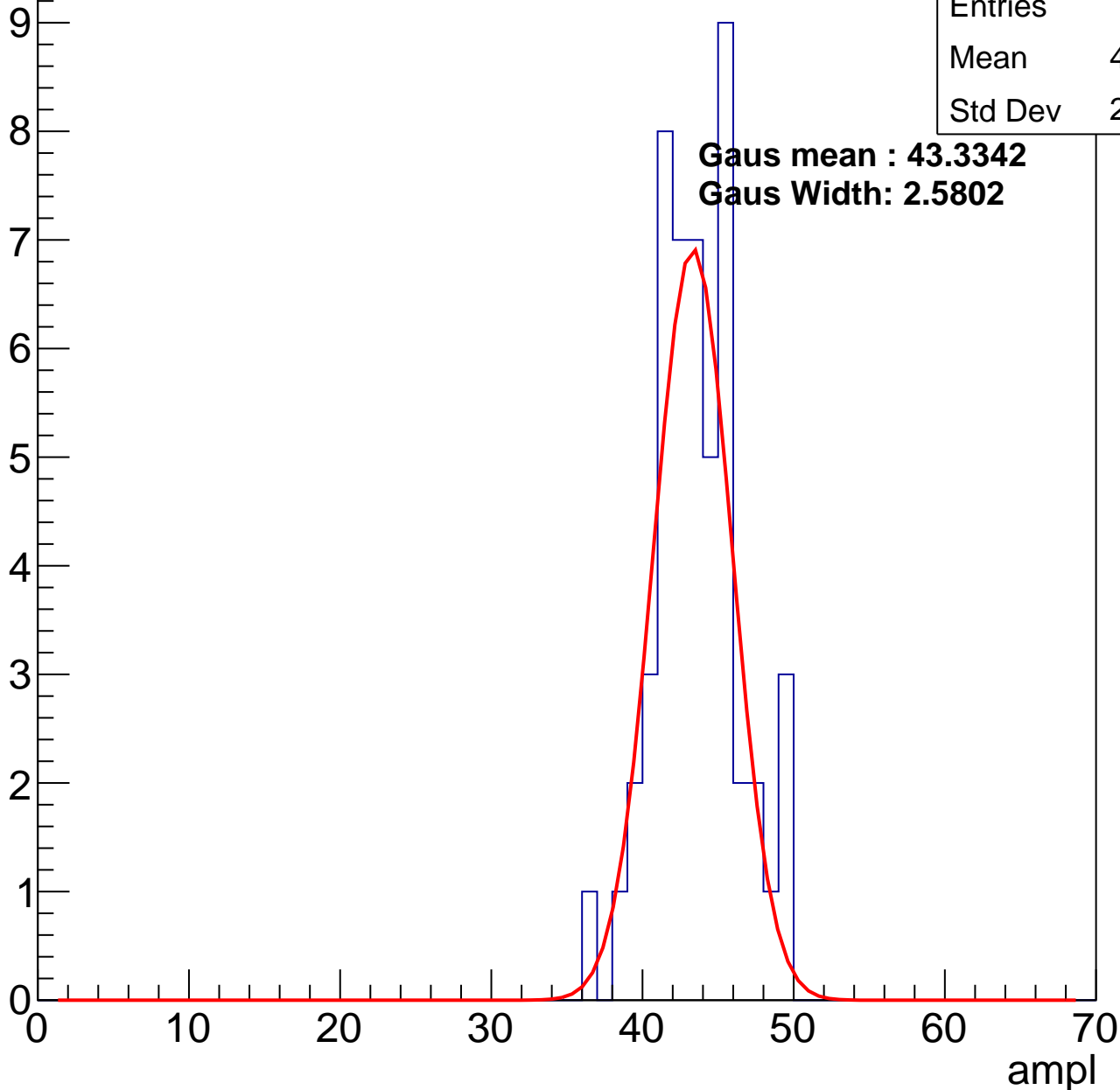
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	43.16
Std Dev	2.782

Gaus mean : 43.3342

Gaus Width: 2.5802

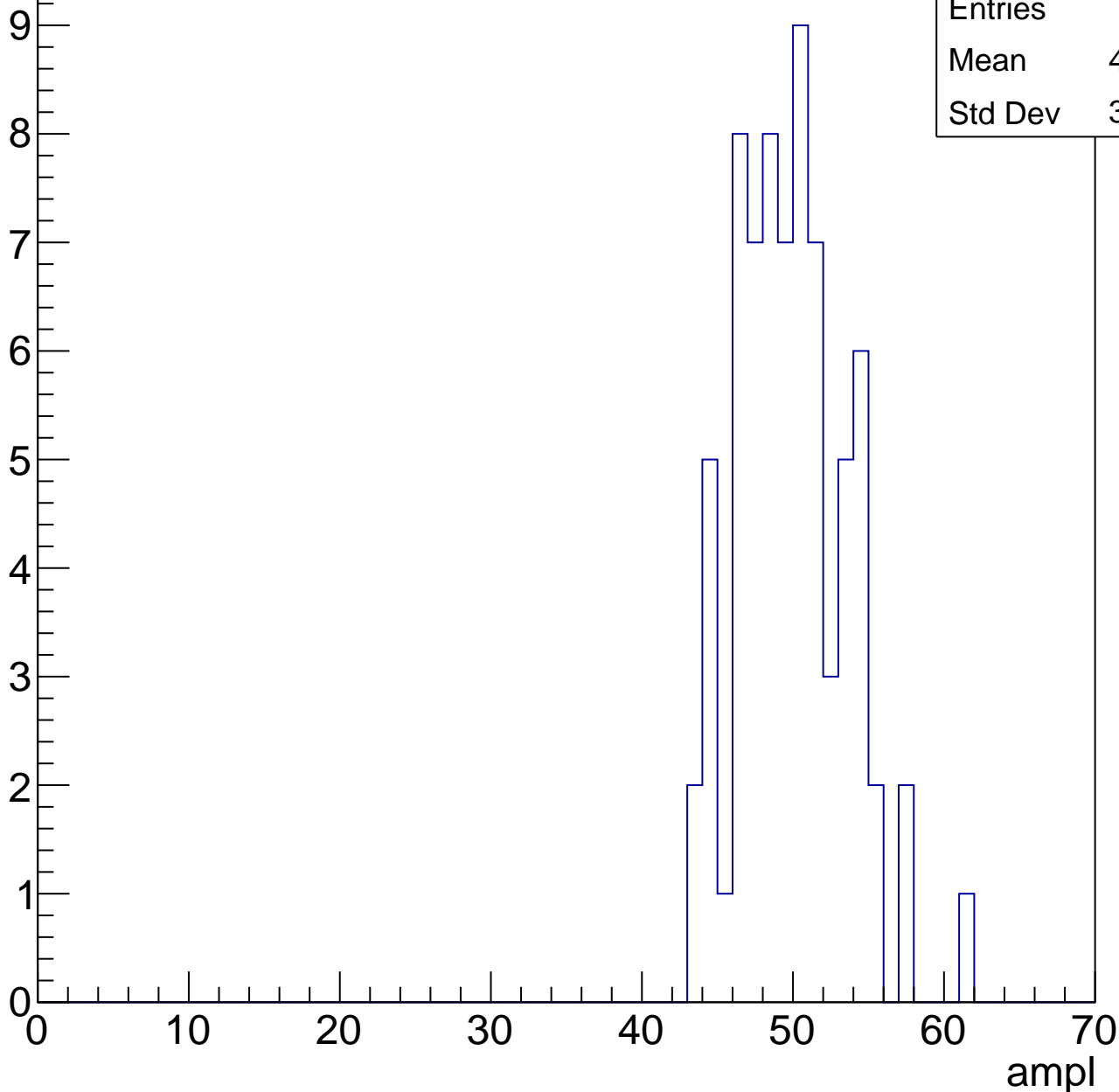


B1L102S, U14-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.48
Std Dev	3.593



B1L102S, U14-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

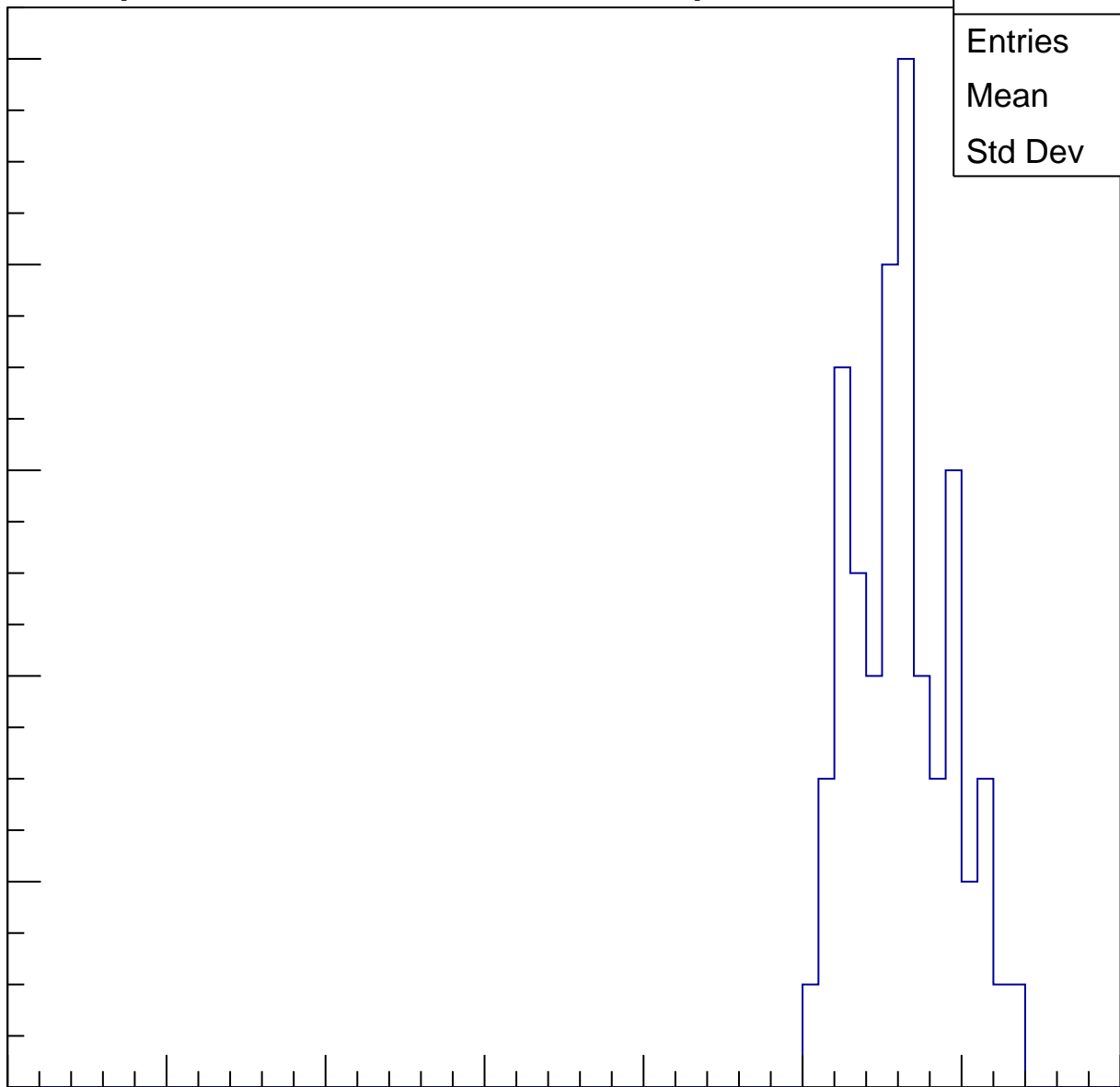
Entries	58
Mean	55.72
Std Dev	3.078

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

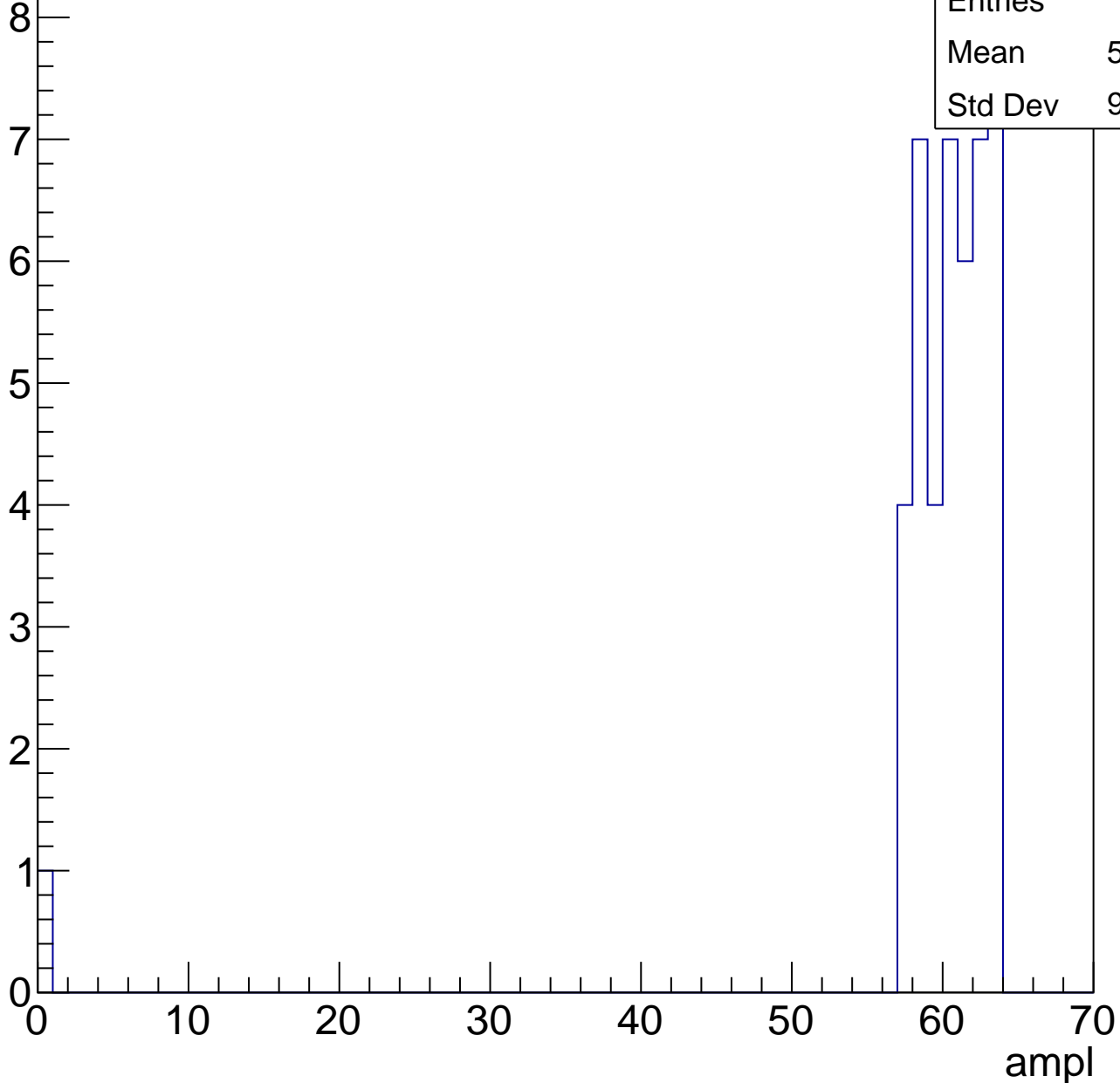


B1L102S, U14-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

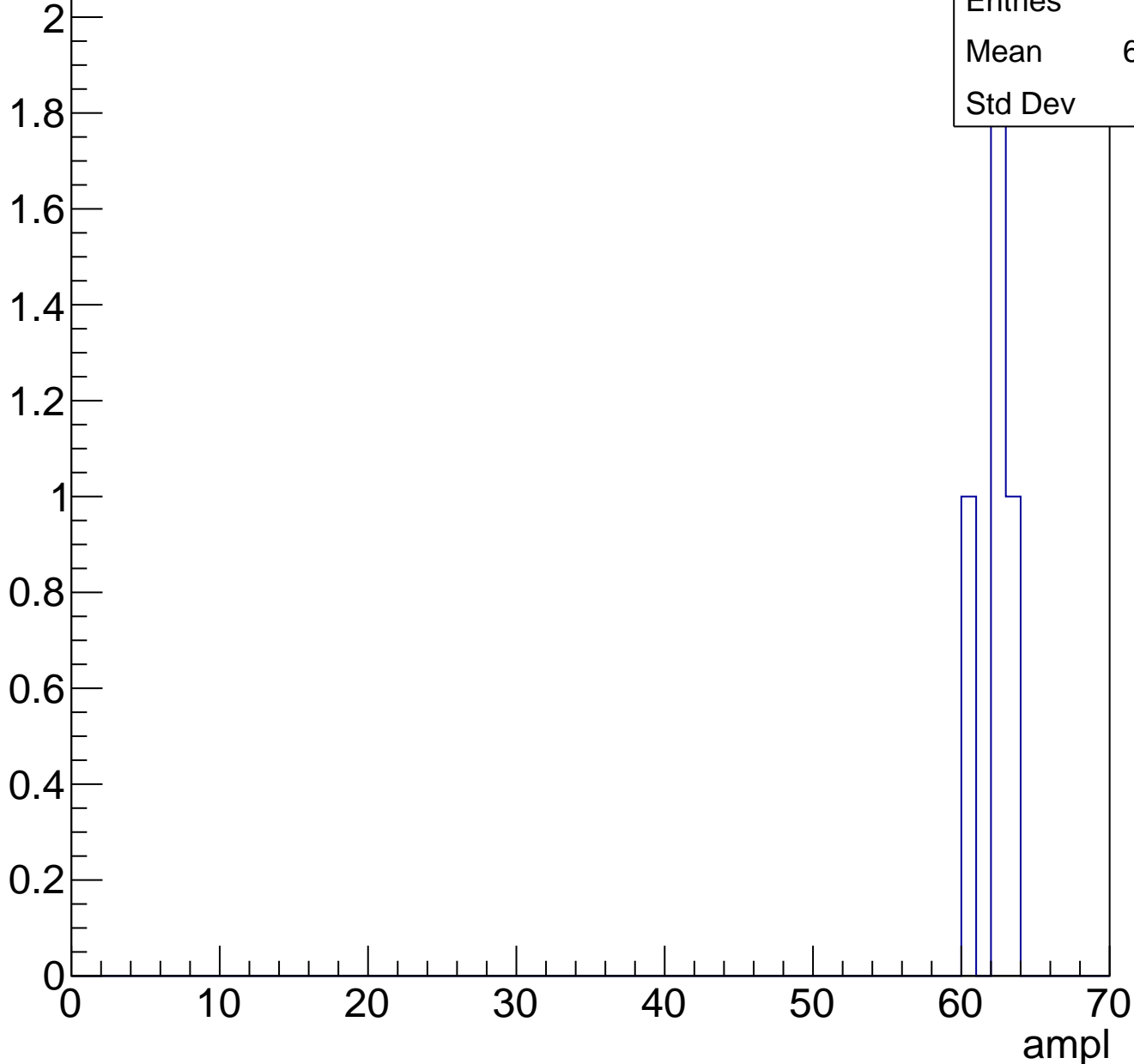
Entries	44
Mean	58.95
Std Dev	9.202



B1L102S, U14-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch5, adc0

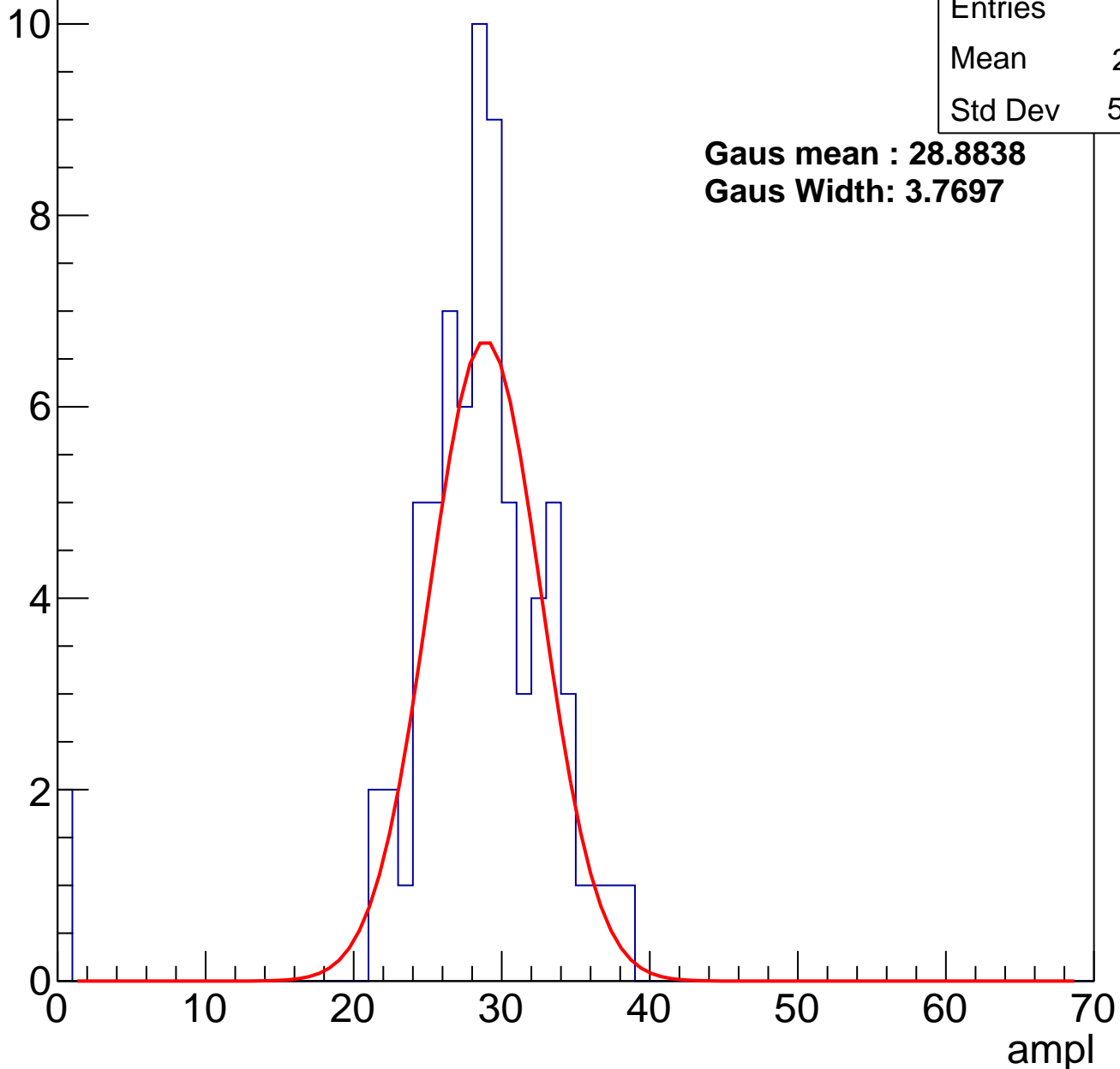
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	27.71
Std Dev	5.925

Gaus mean : 28.8838

Gaus Width: 3.7697

Entry



B1L102S, U14-ch5, adc1

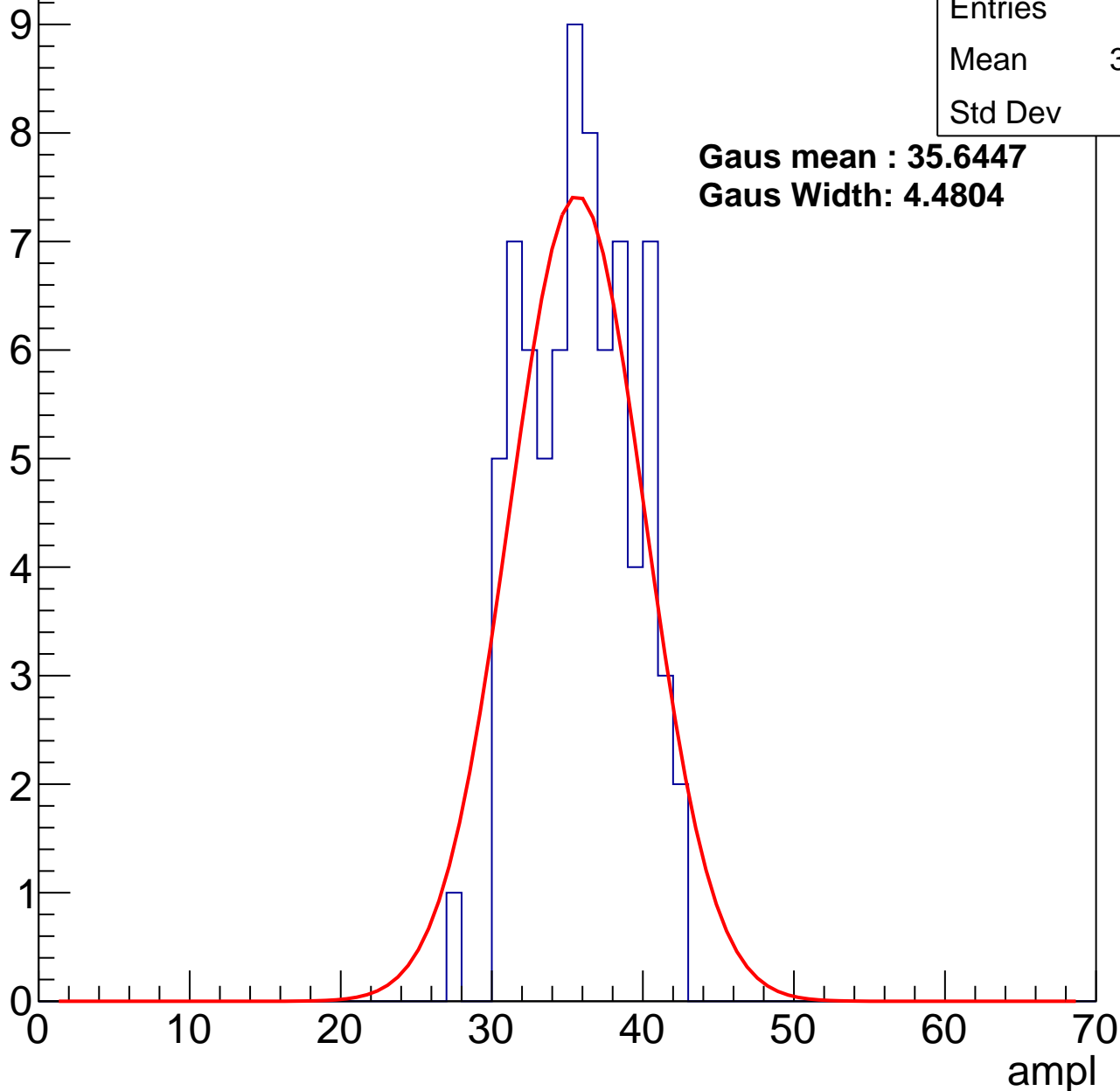
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.38
Std Dev	3.46

Gaus mean : 35.6447

Gaus Width: 4.4804



B1L102S, U14-ch5, adc2

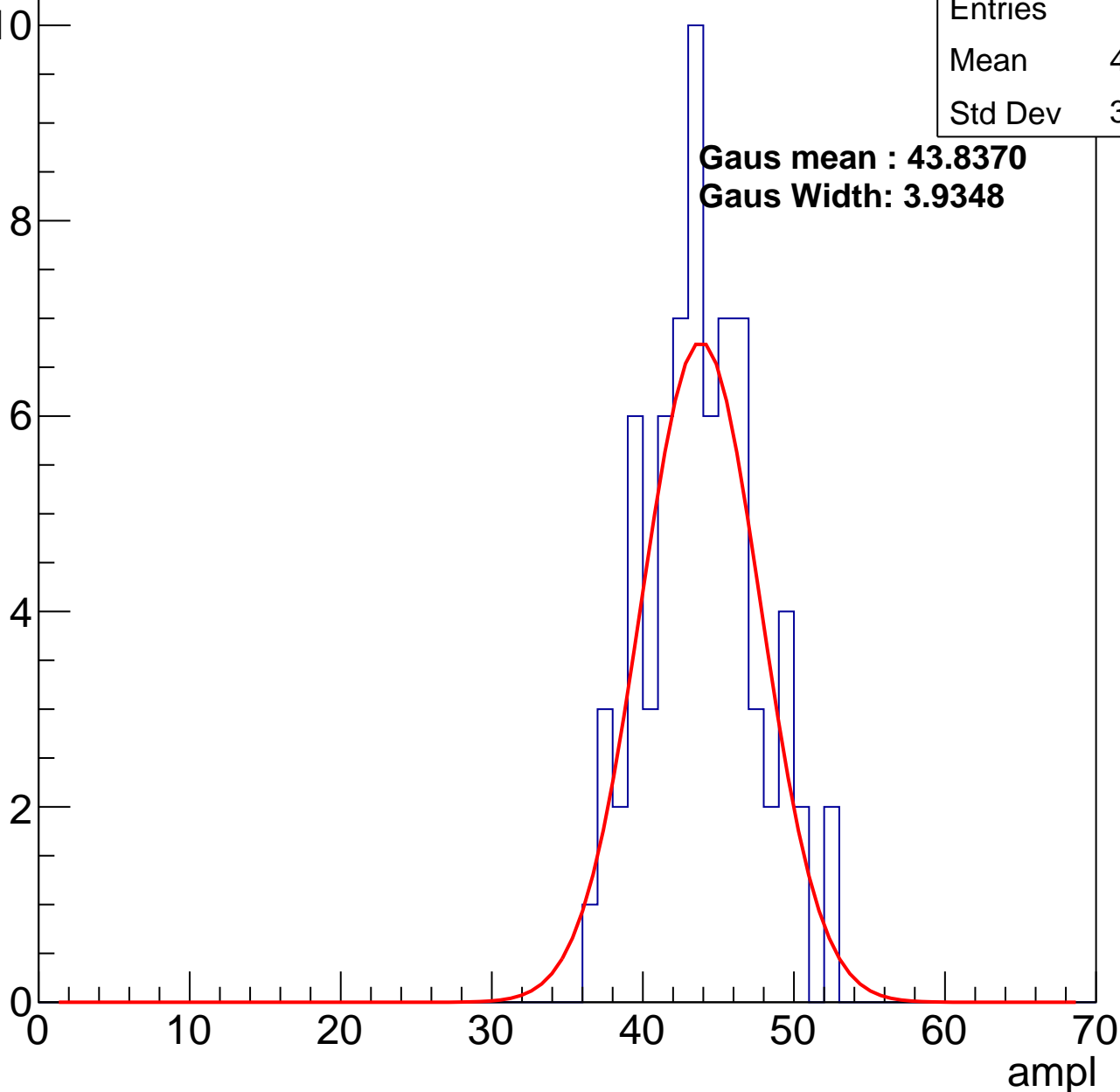
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.45
Std Dev	3.649

Gaus mean : 43.8370

Gaus Width: 3.9348

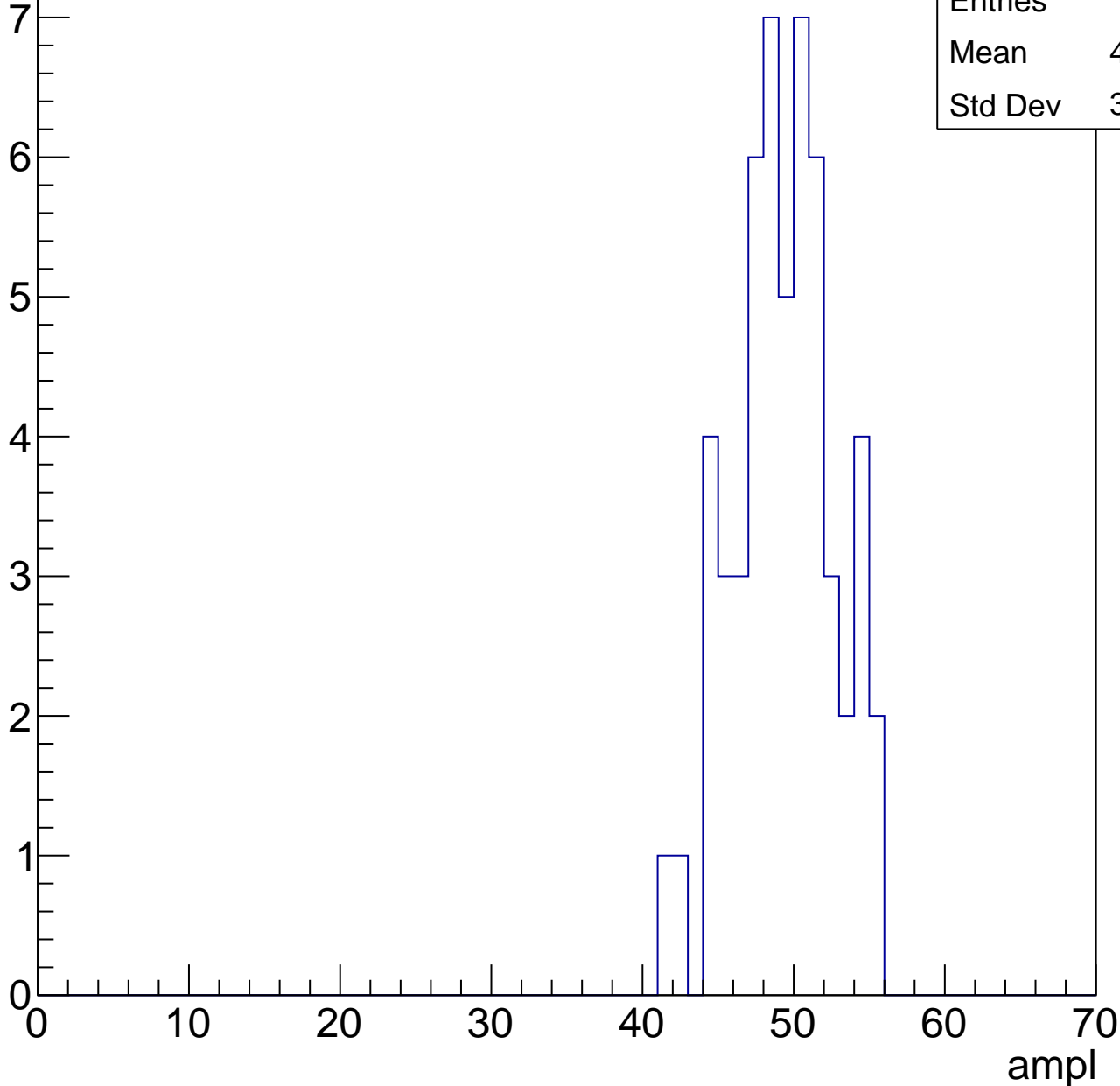


B1L102S, U14-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

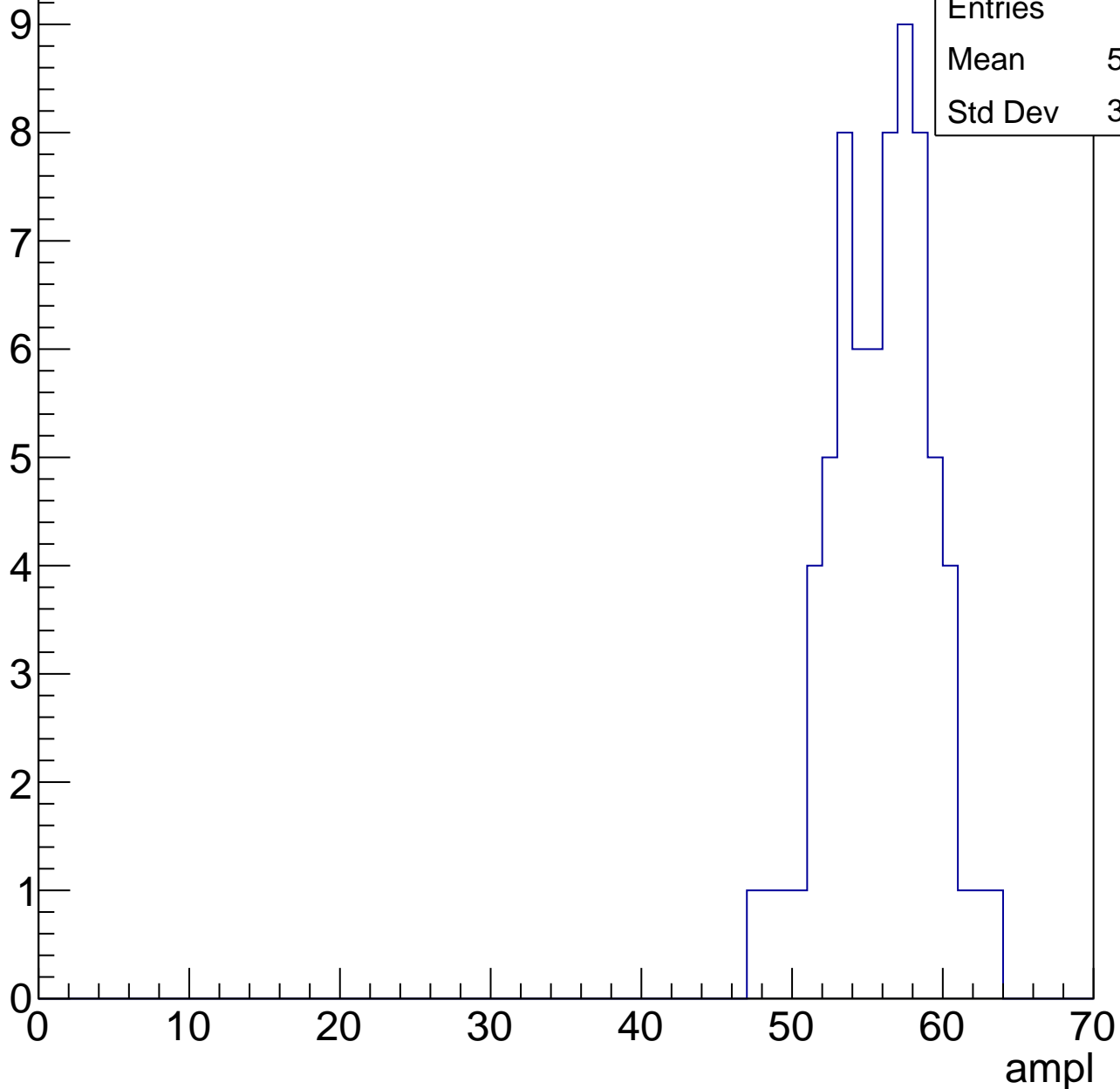
Entries	54
Mean	48.87
Std Dev	3.278



B1L102S, U14-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

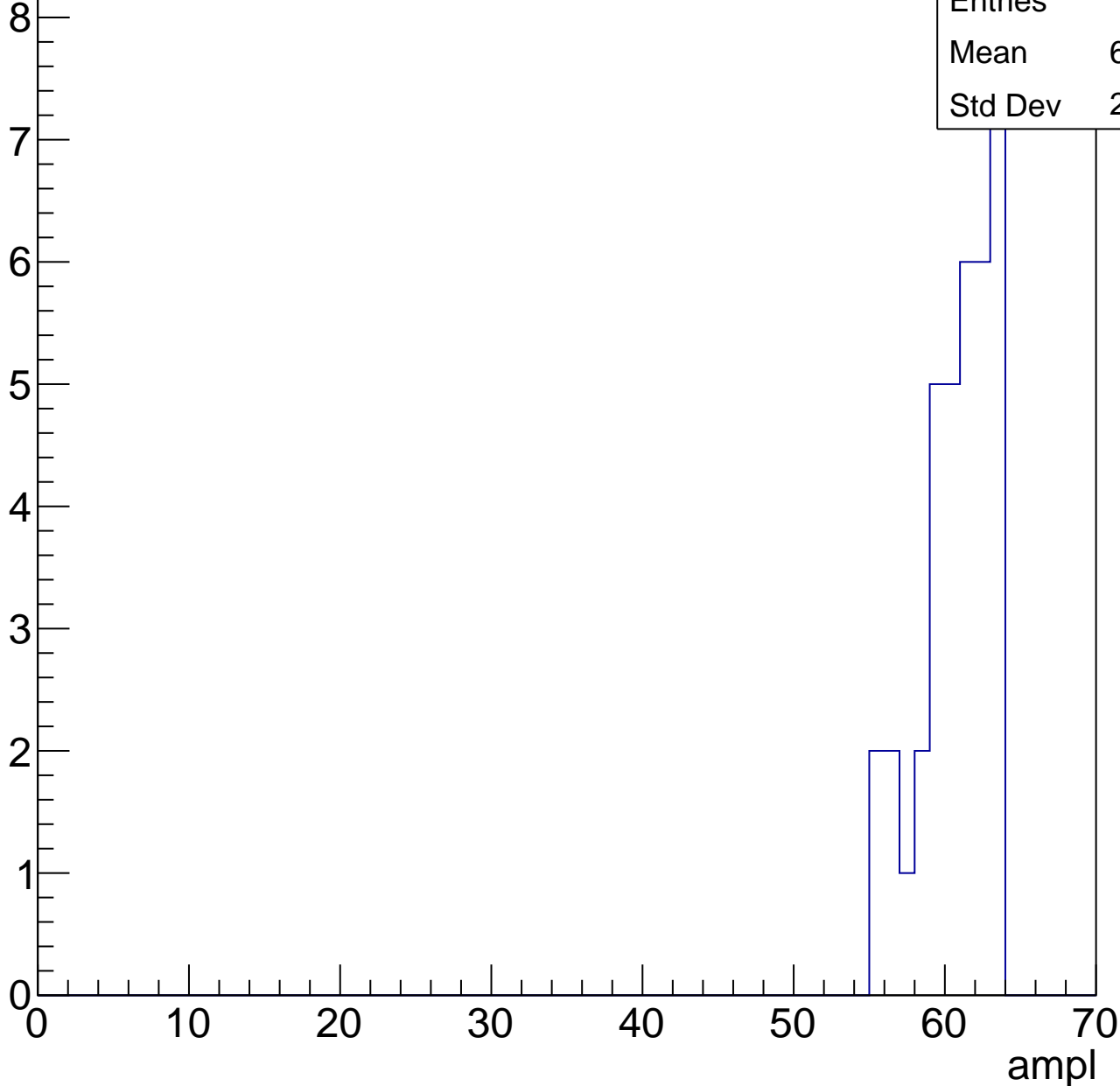


B1L102S, U14-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

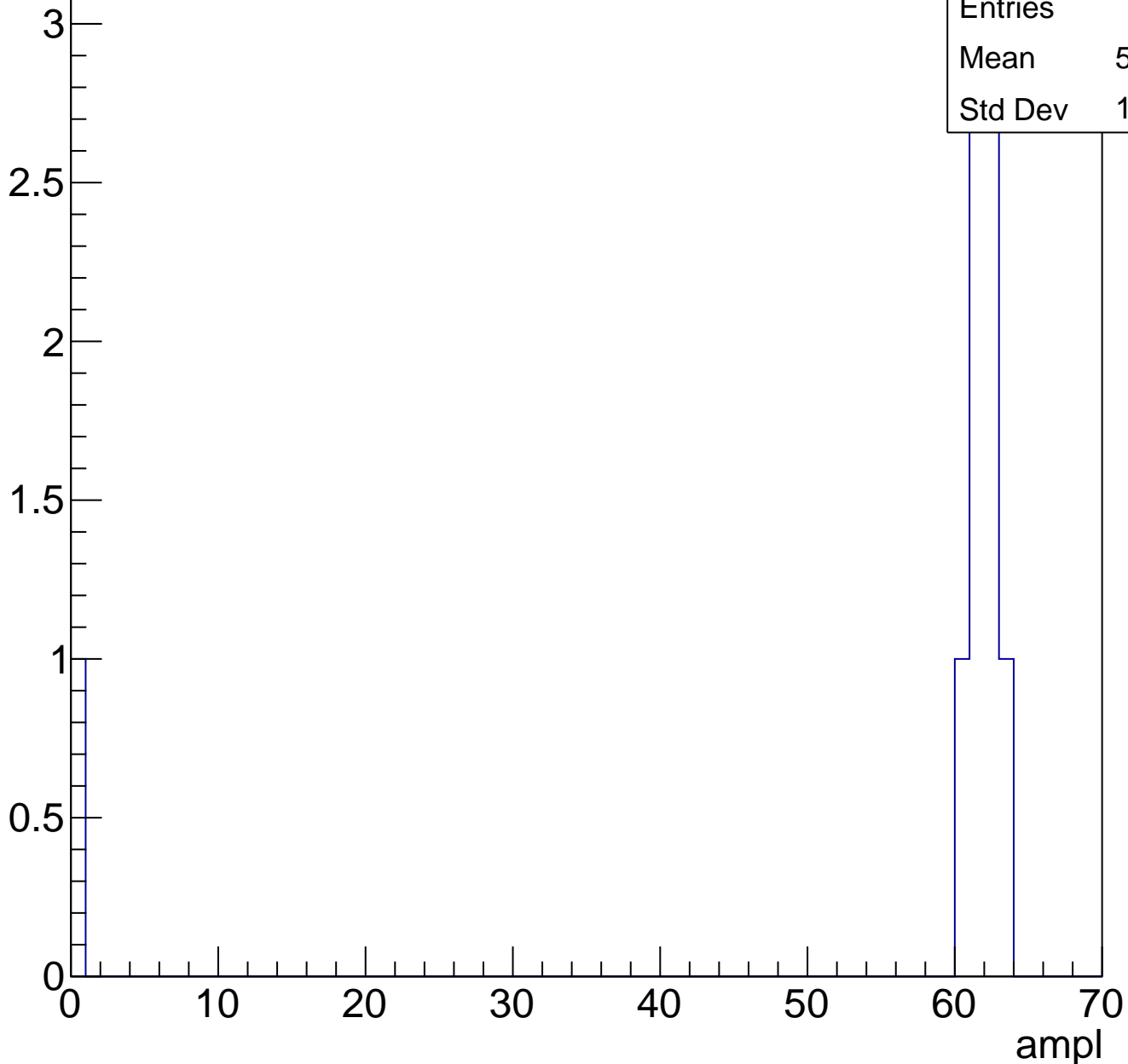
Entries	37
Mean	60.32
Std Dev	2.337



B1L102S, U14-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	9
Mean	54.67
Std Dev	19.34

B1L102S, U14-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch6, adc0

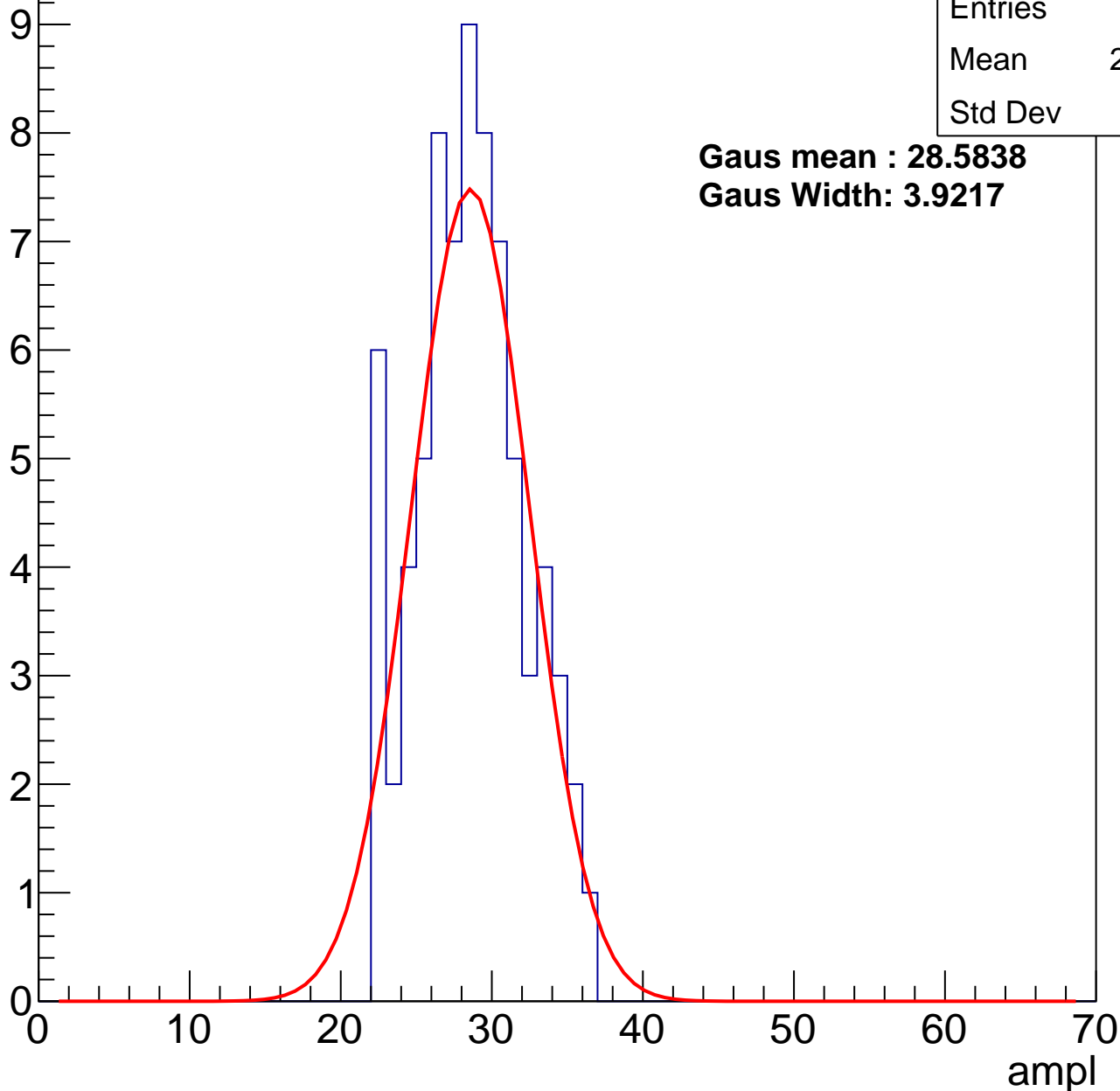
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.12
Std Dev	3.51

Gaus mean : 28.5838

Gaus Width: 3.9217



B1L102S, U14-ch6, adc1

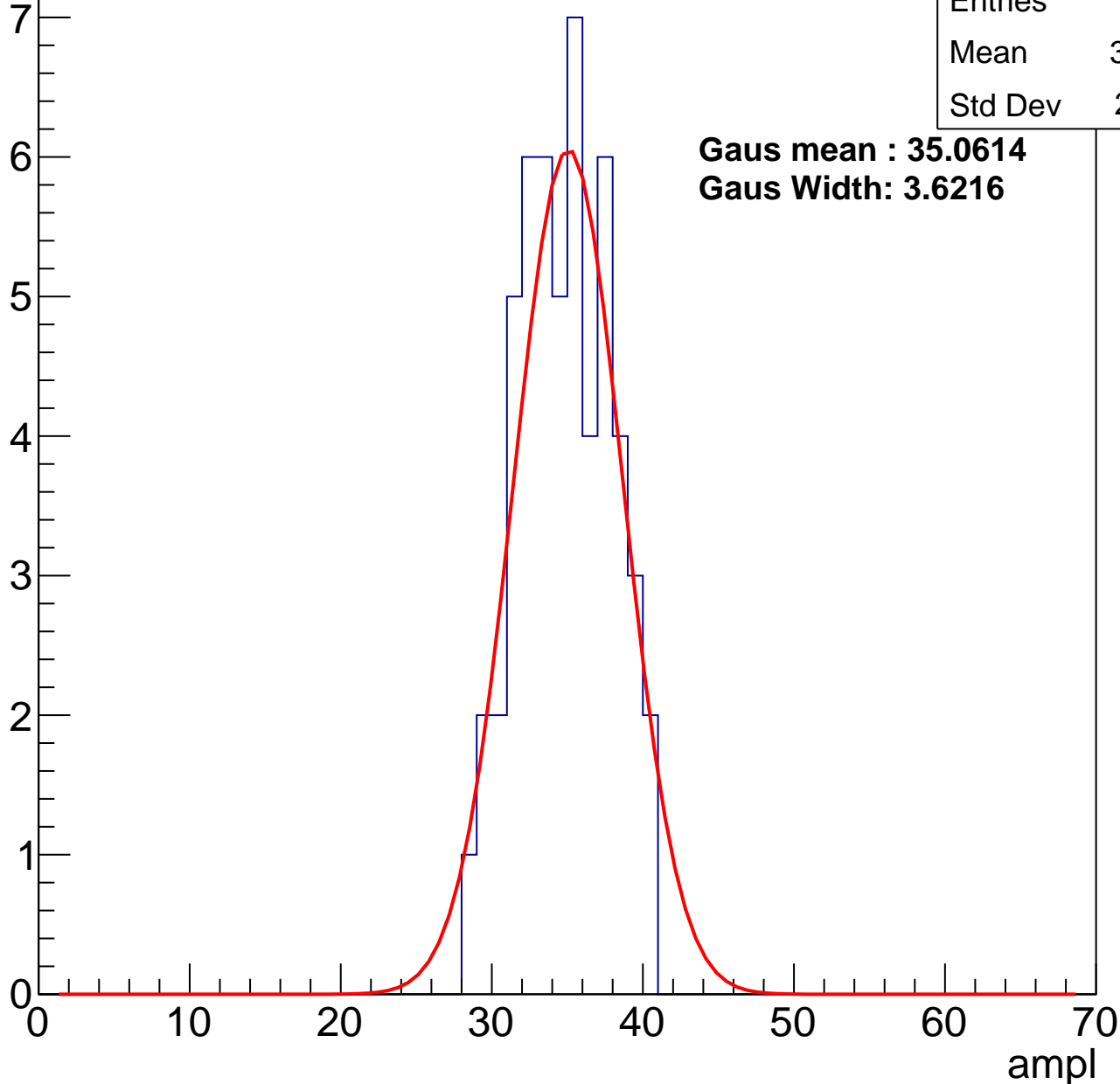
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.36
Std Dev	2.991

Gaus mean : 35.0614

Gaus Width: 3.6216



B1L102S, U14-ch6, adc2

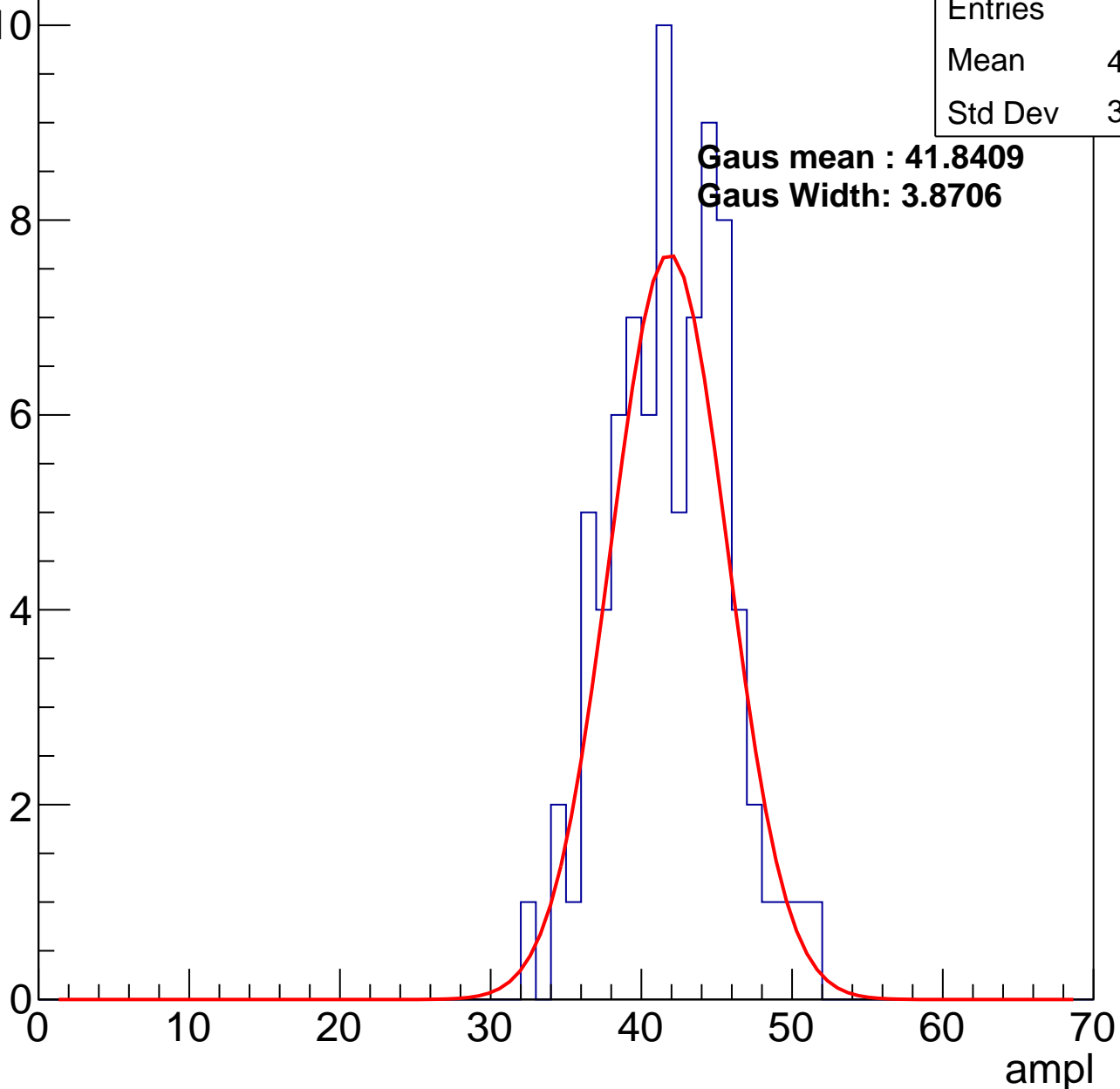
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	41.44
Std Dev	3.823

Gaus mean : 41.8409

Gaus Width: 3.8706

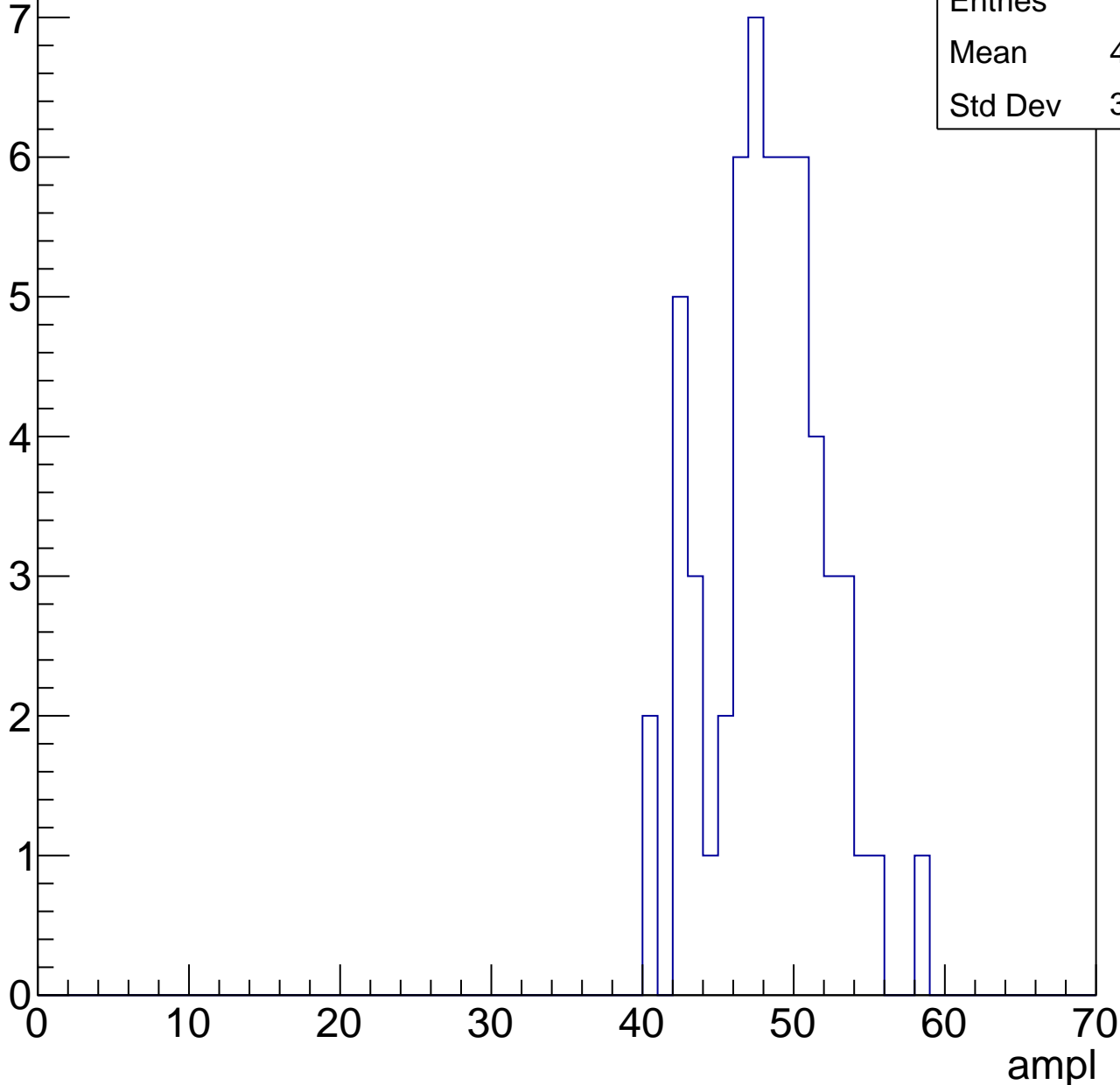


B1L102S, U14-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

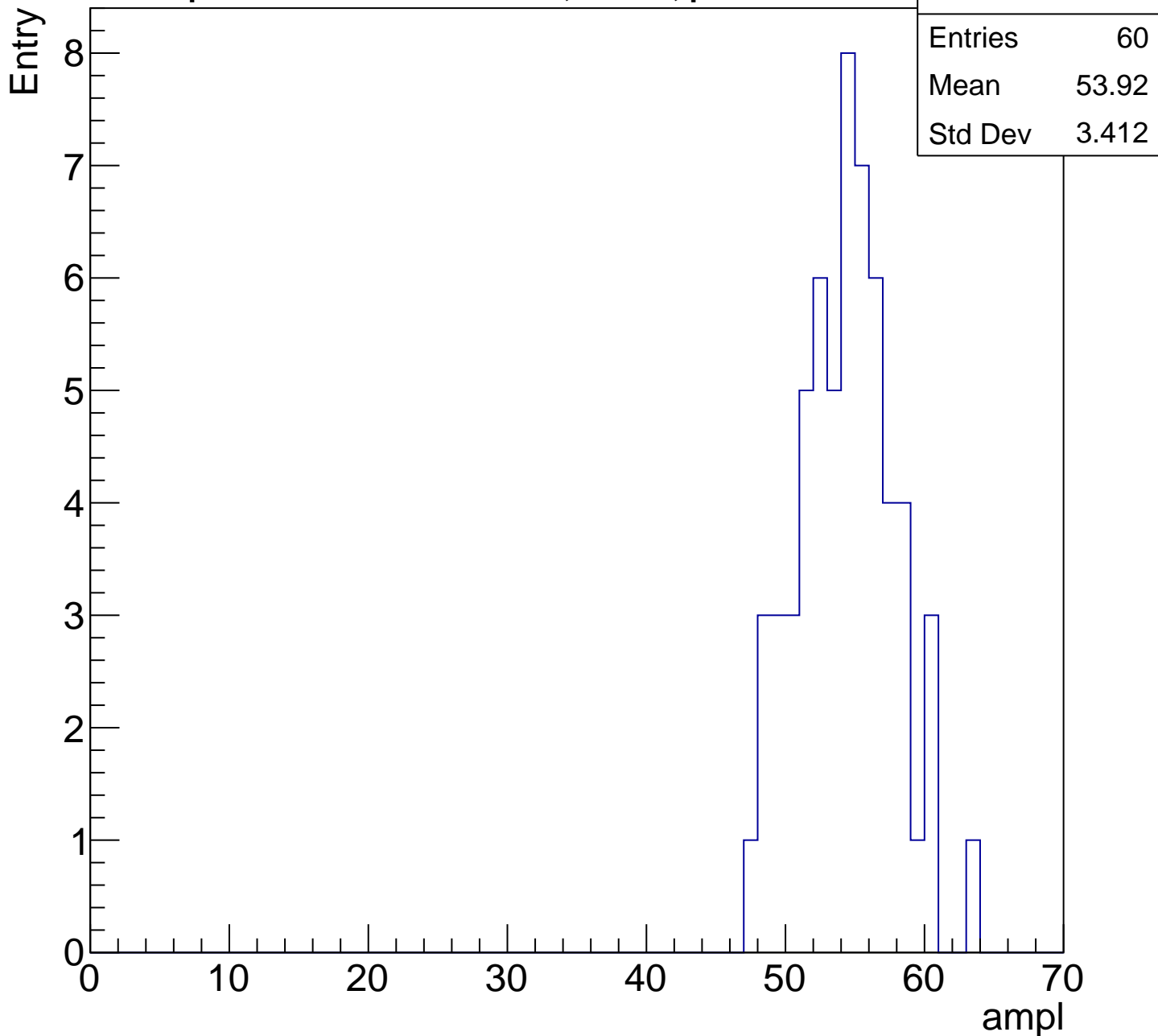
Entry

Entries	57
Mean	47.82
Std Dev	3.793



B1L102S, U14-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

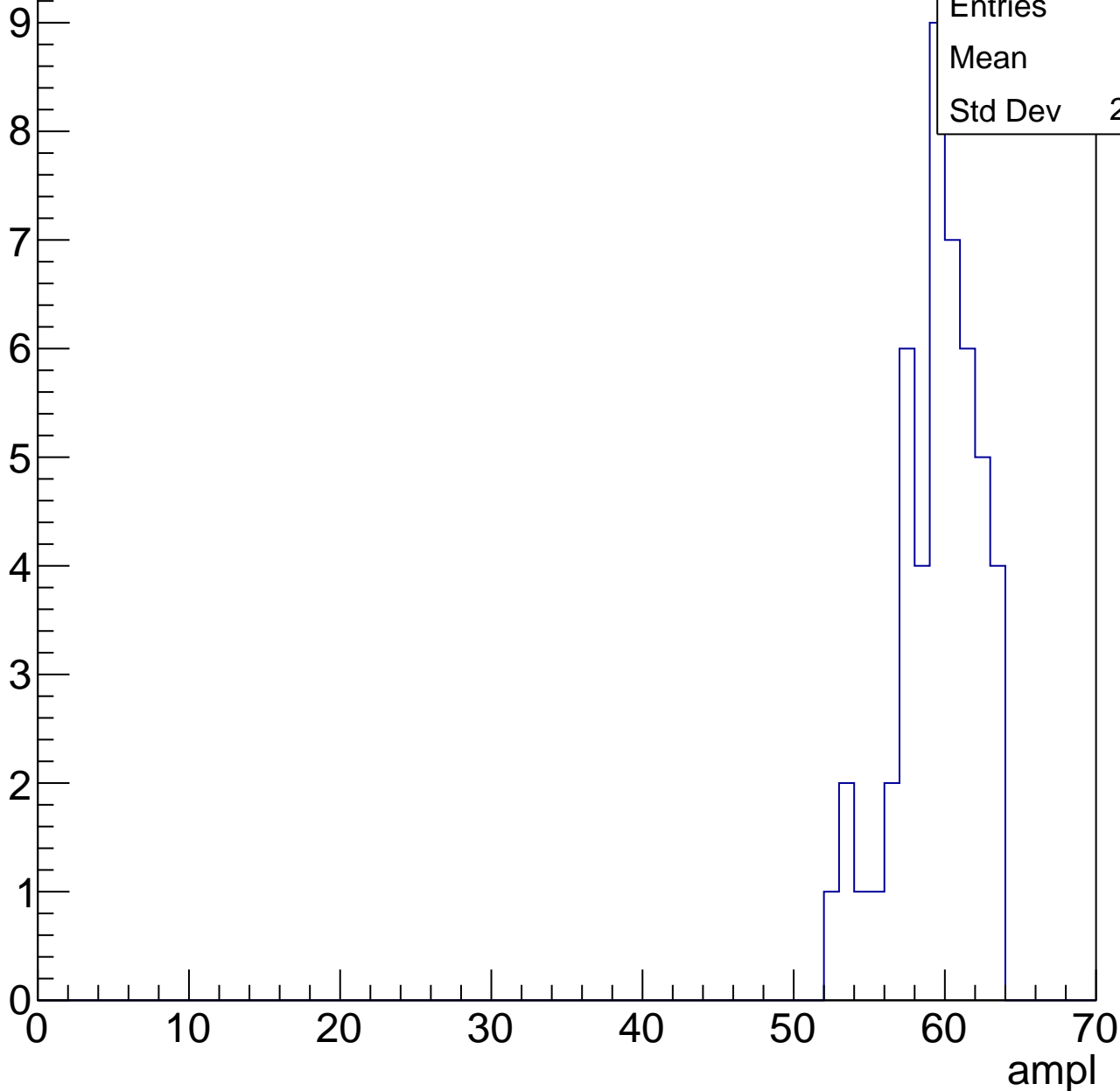


B1L102S, U14-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	59
Std Dev	2.693

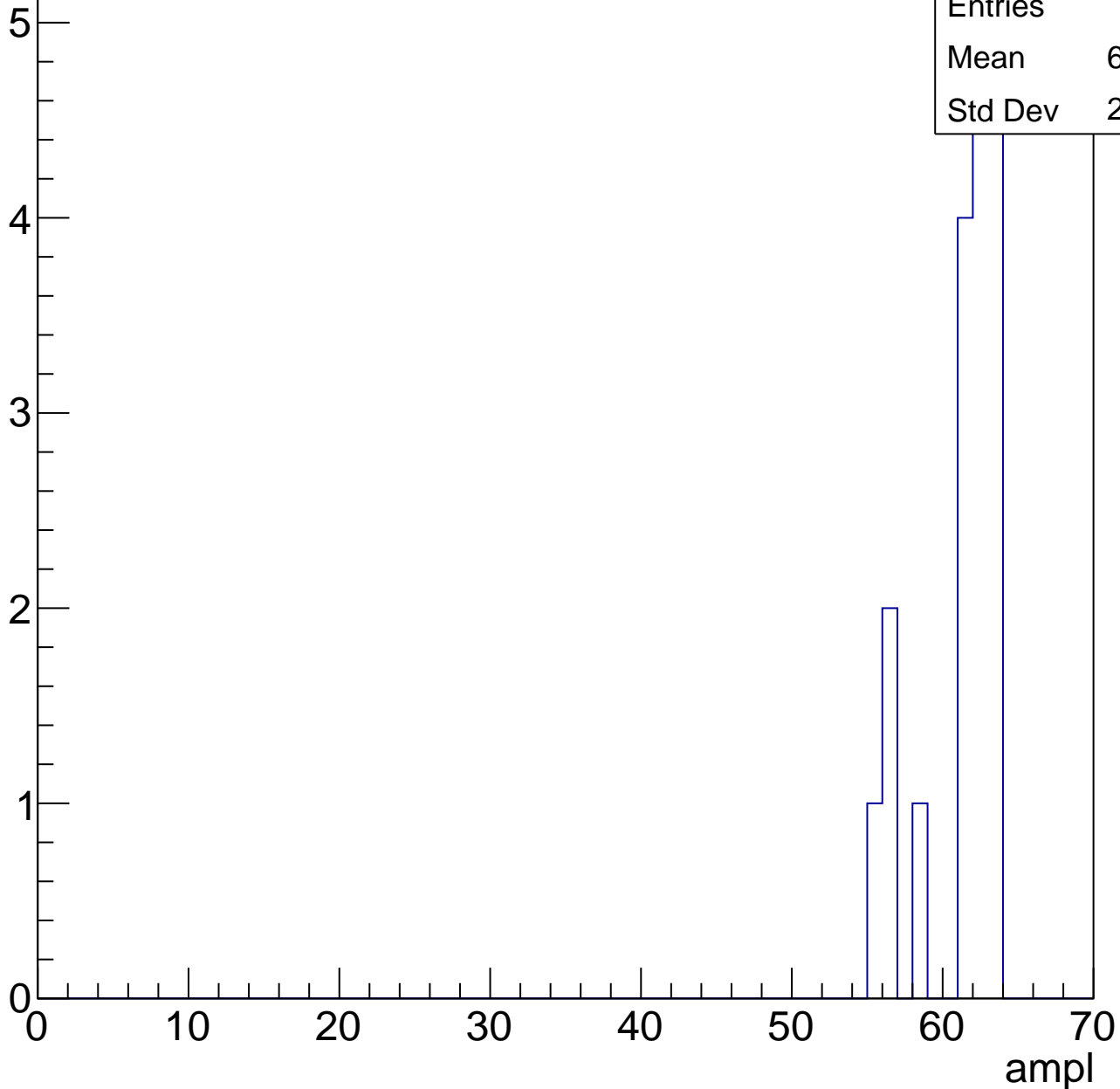


B1L102S, U14-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

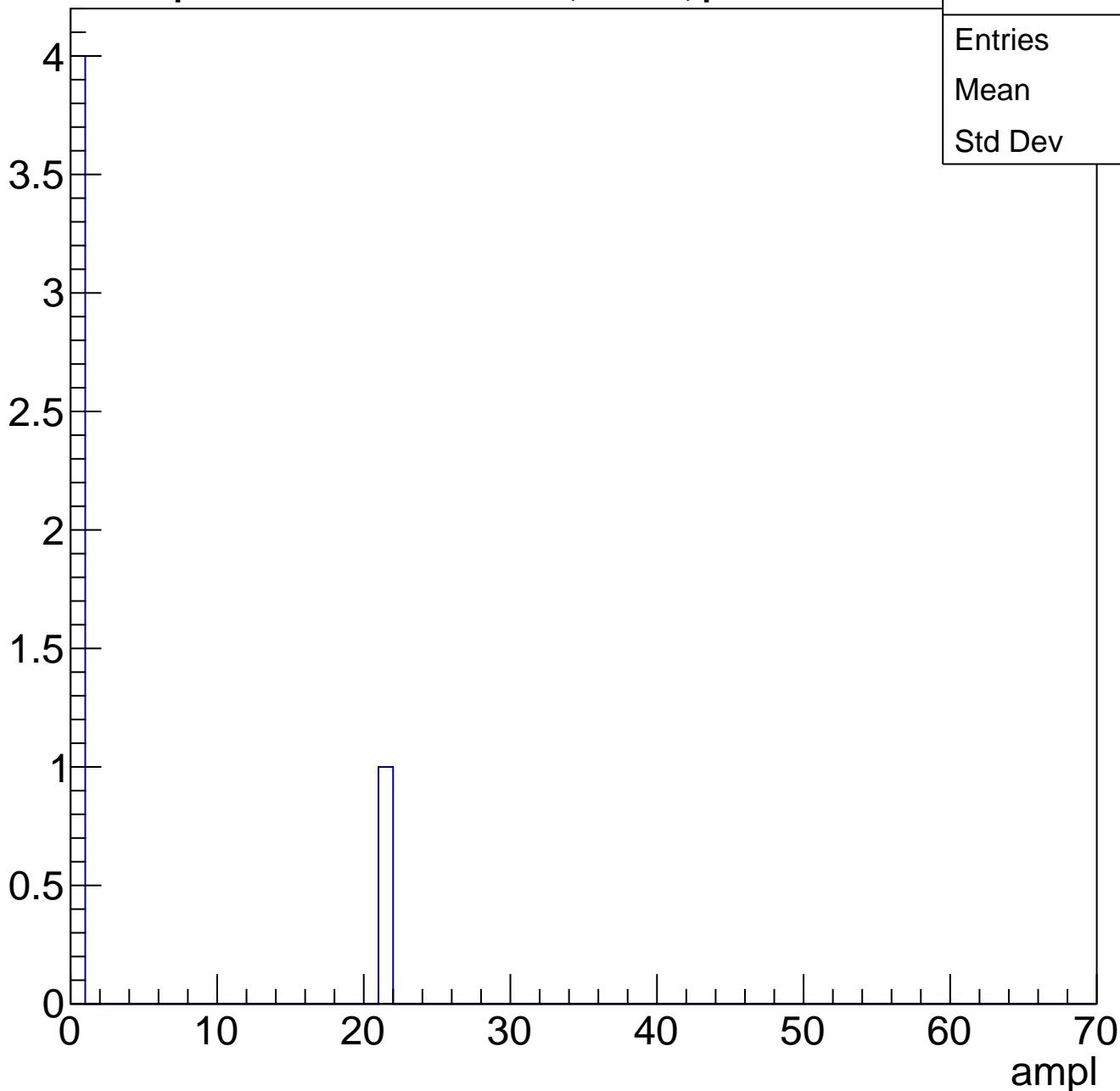
Entries	18
Mean	60.78
Std Dev	2.572



B1L102S, U14-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	4.2
Std Dev	8.4

B1L102S, U14-ch7, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	27.24
Std Dev	6.484

Gaus mean : 28.3862

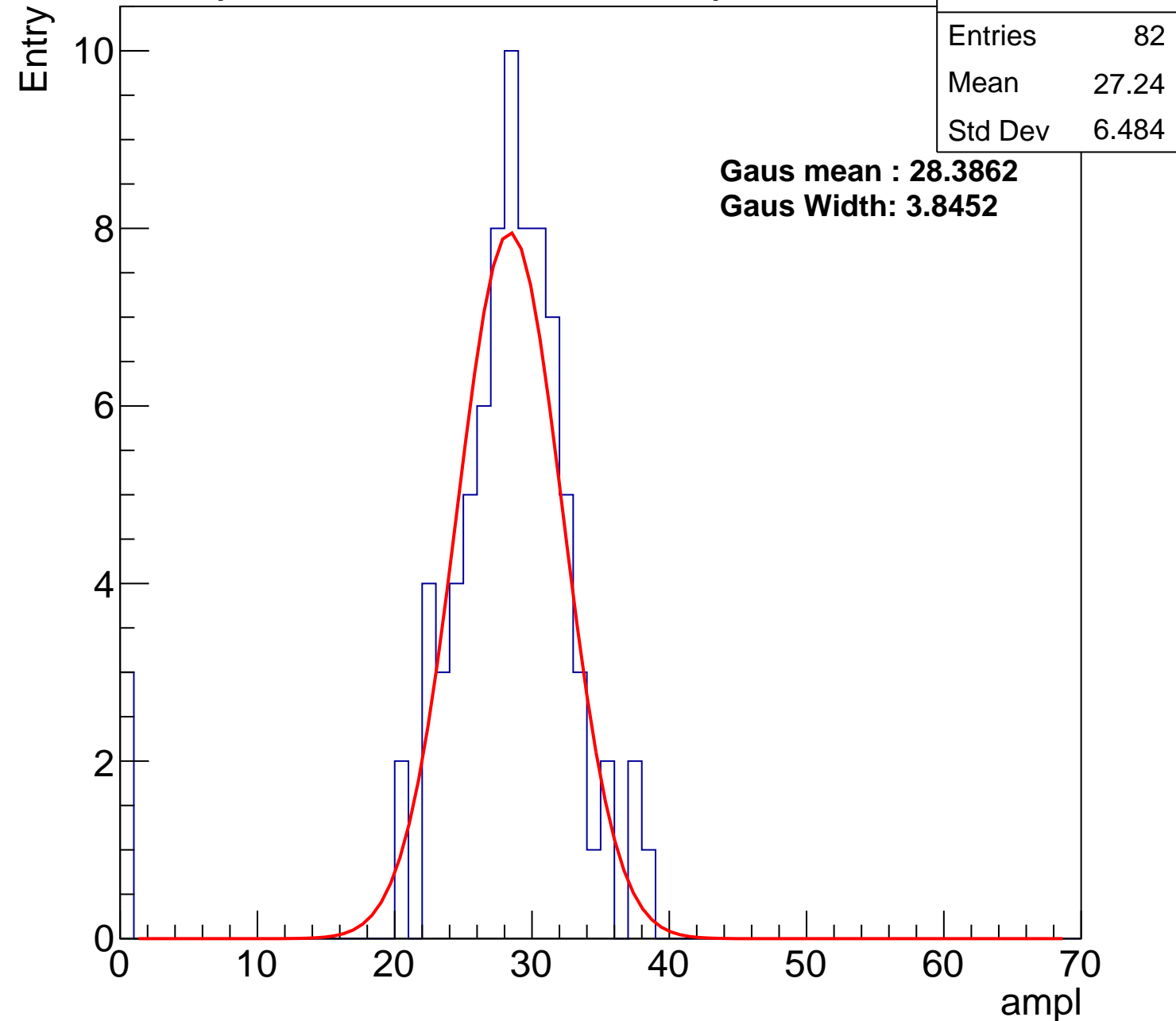
Gaus Width: 3.8452

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch7, adc1

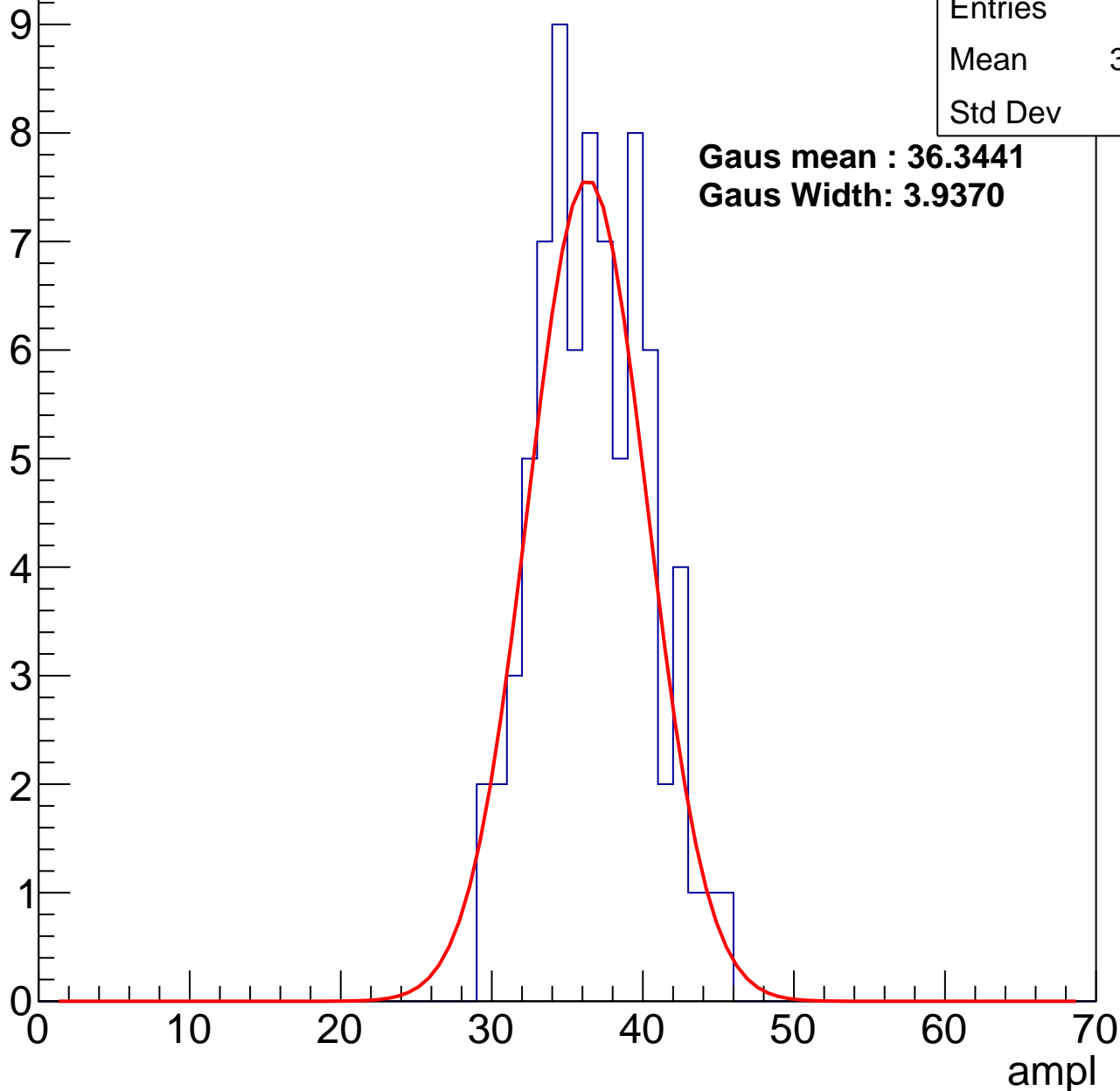
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.22
Std Dev	3.62

Gaus mean : 36.3441

Gaus Width: 3.9370



B1L102S, U14-ch7, adc2

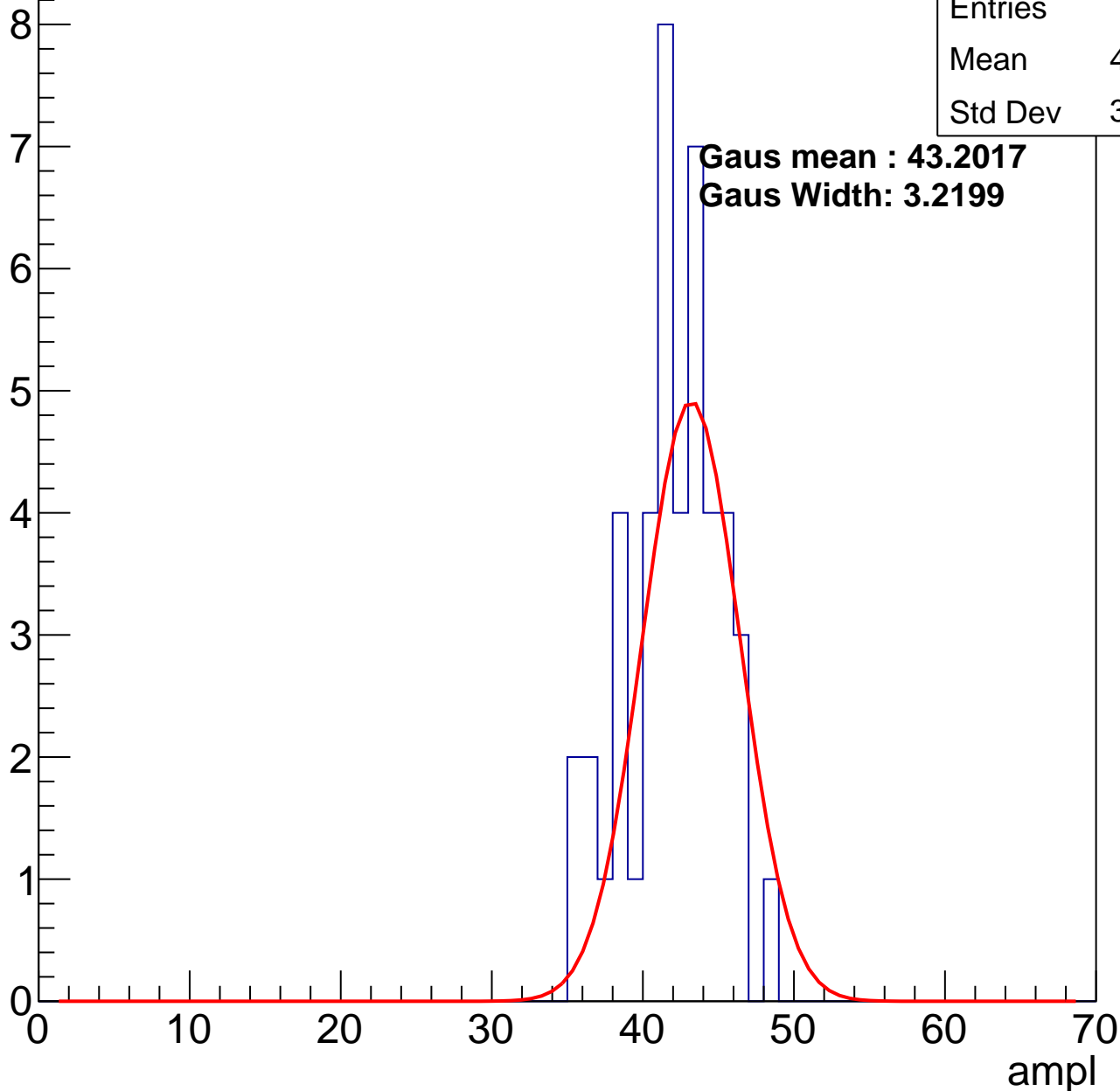
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	41.53
Std Dev	3.074

Gaus mean : 43.2017

Gaus Width: 3.2199

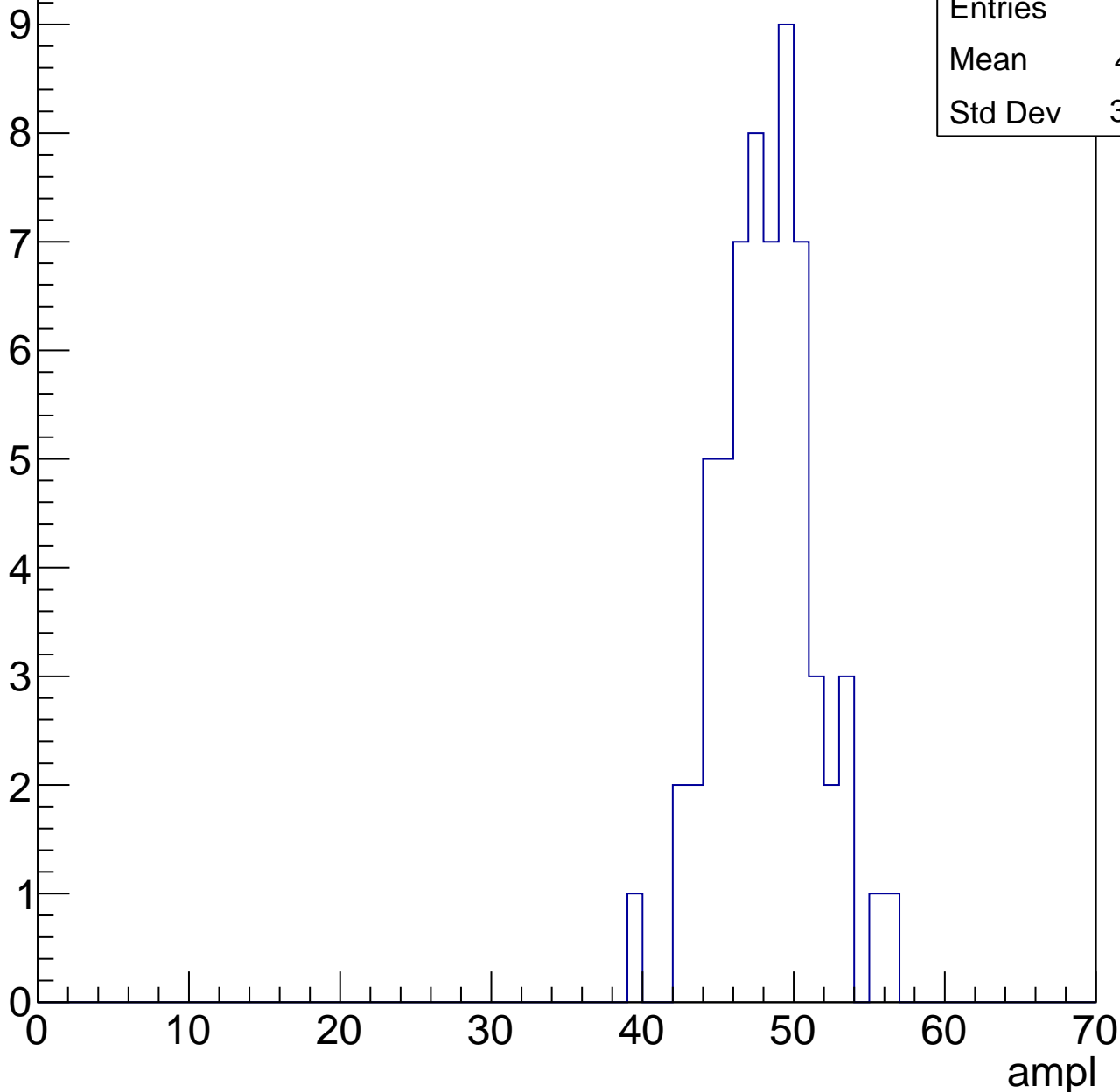


B1L102S, U14-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

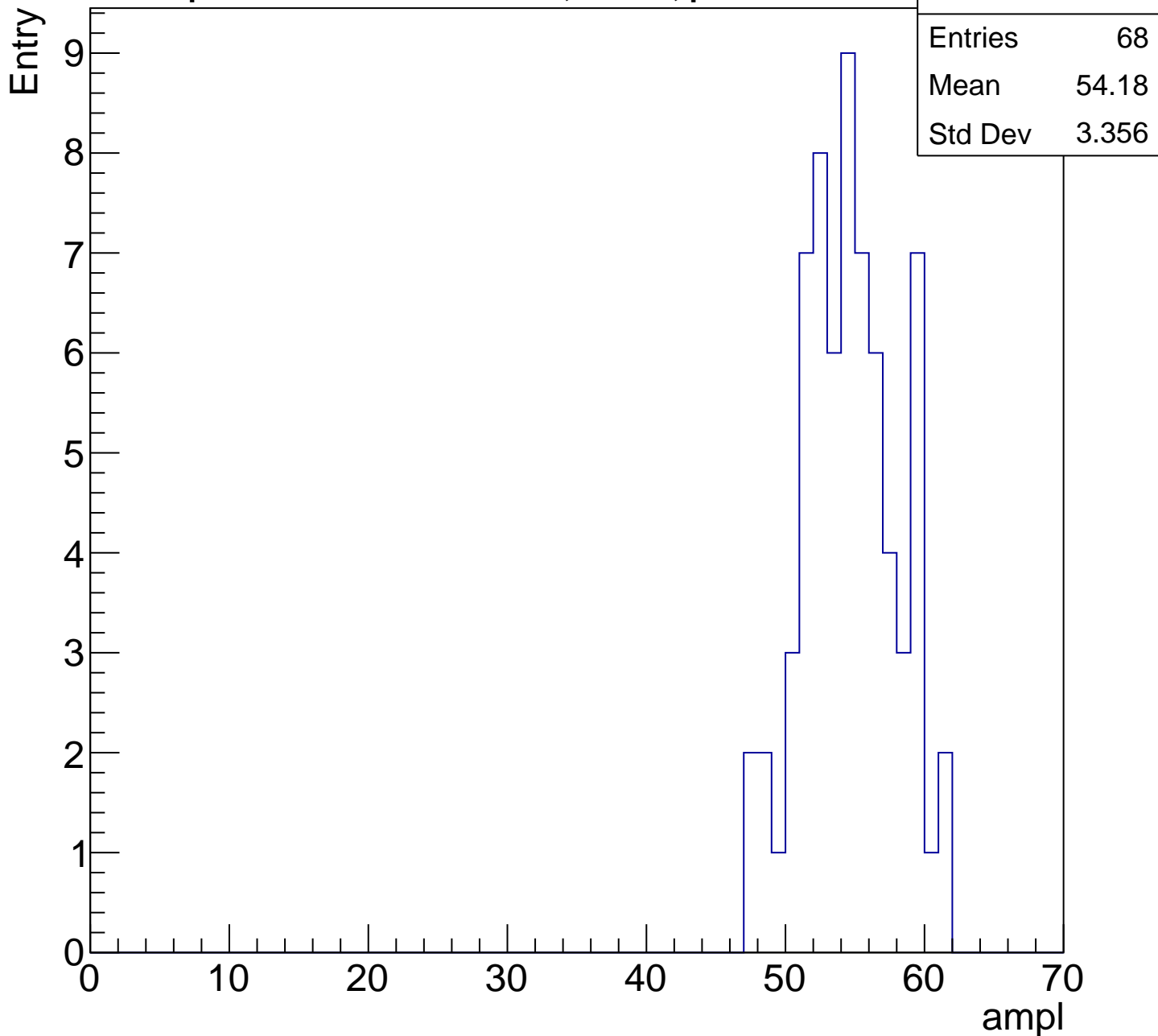
Entry

Entries	63
Mean	47.71
Std Dev	3.194



B1L102S, U14-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

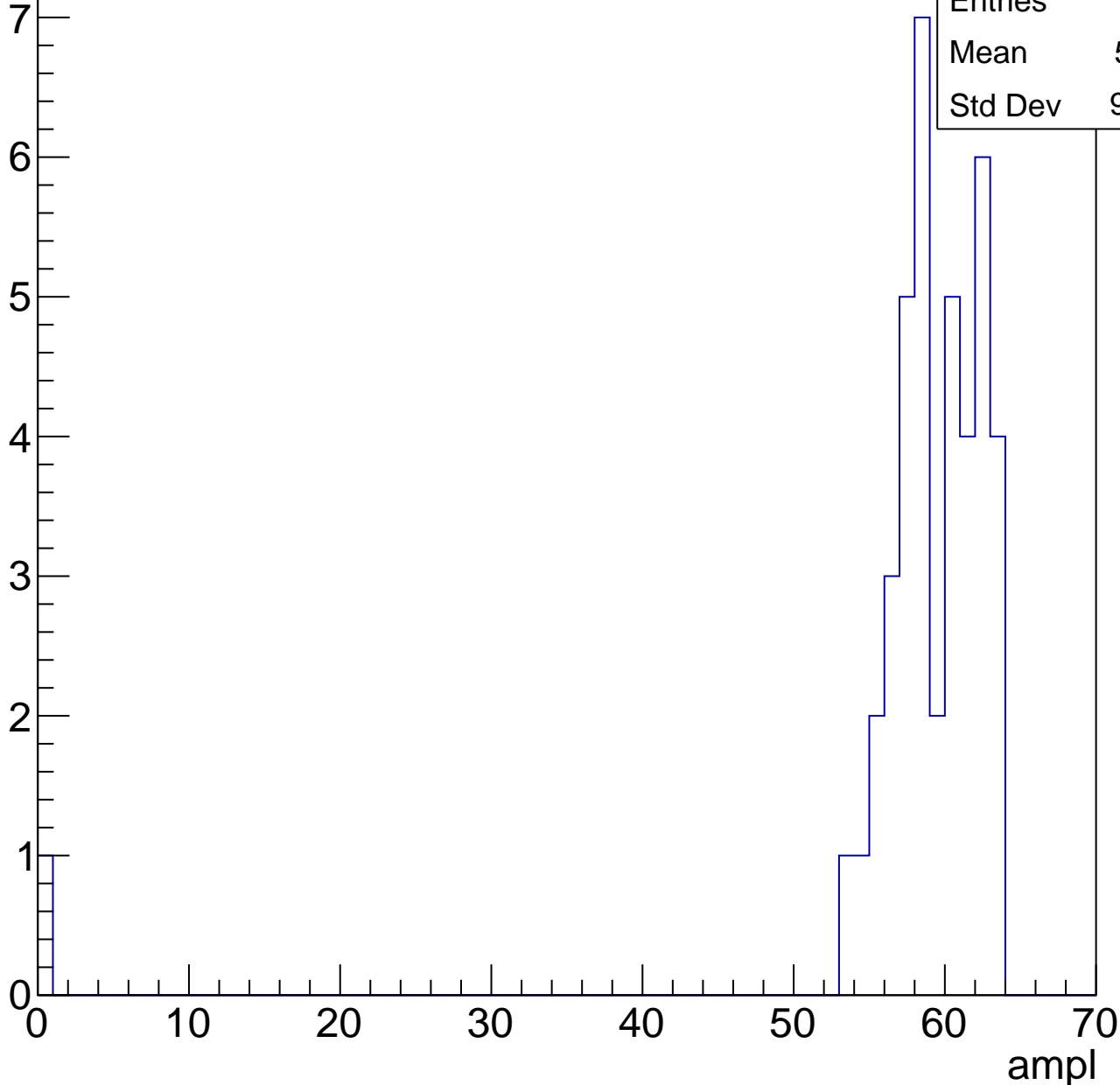


B1L102S, U14-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	57.61
Std Dev	9.484

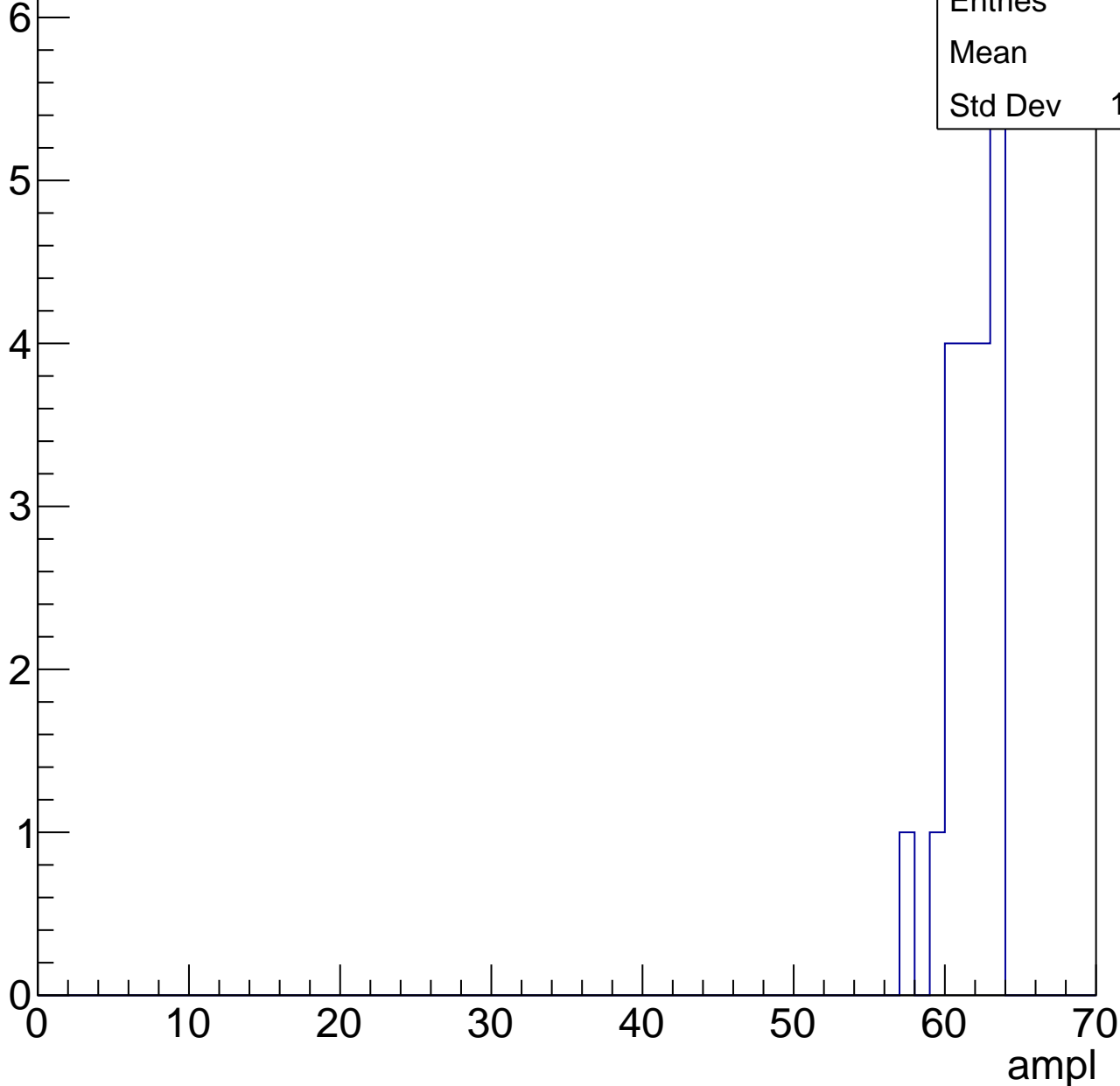


B1L102S, U14-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	61.3
Std Dev	1.584



B1L102S, U14-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch8, adc0

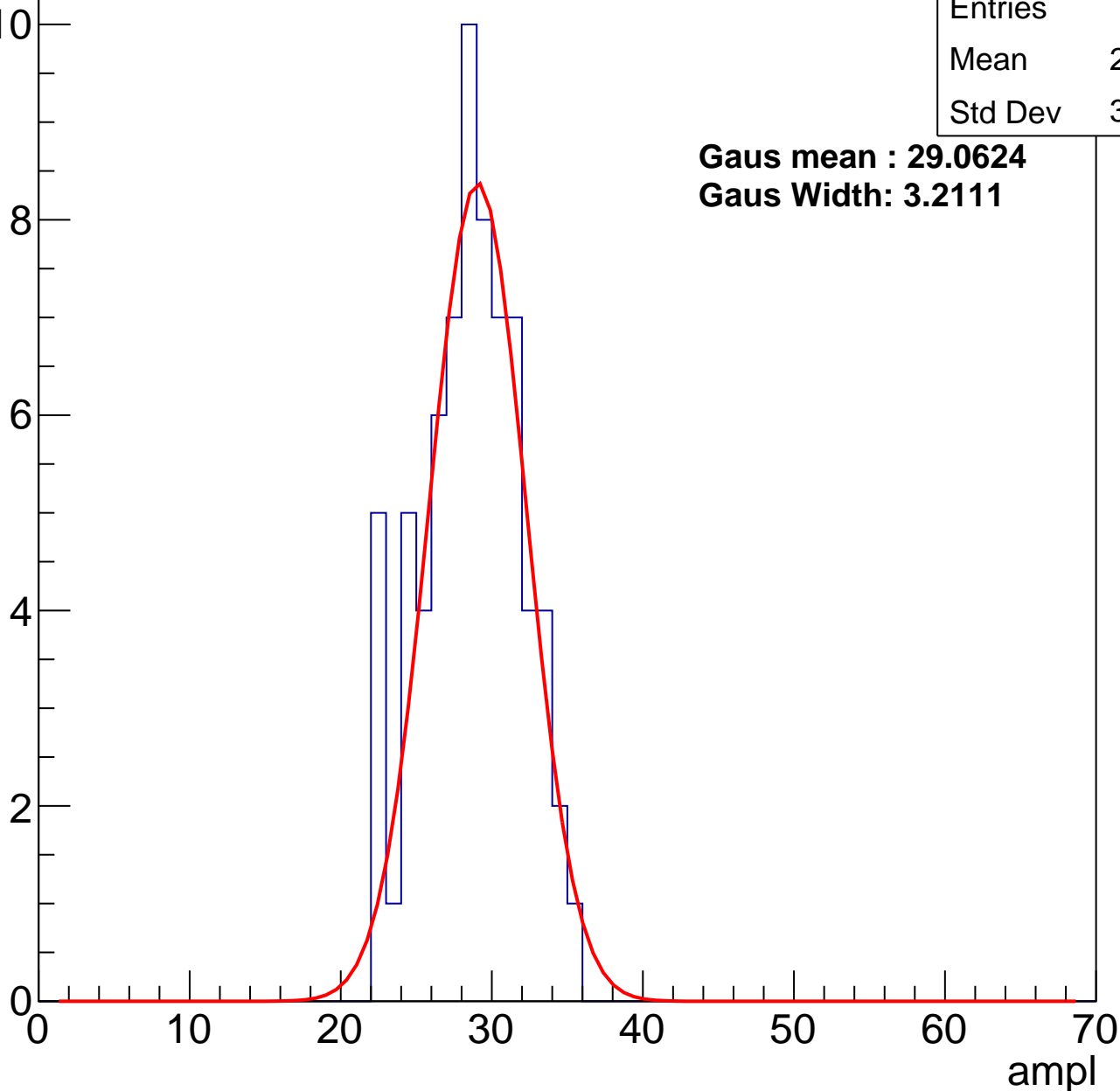
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.17
Std Dev	3.215

Gaus mean : 29.0624

Gaus Width: 3.2111



B1L102S, U14-ch8, adc1

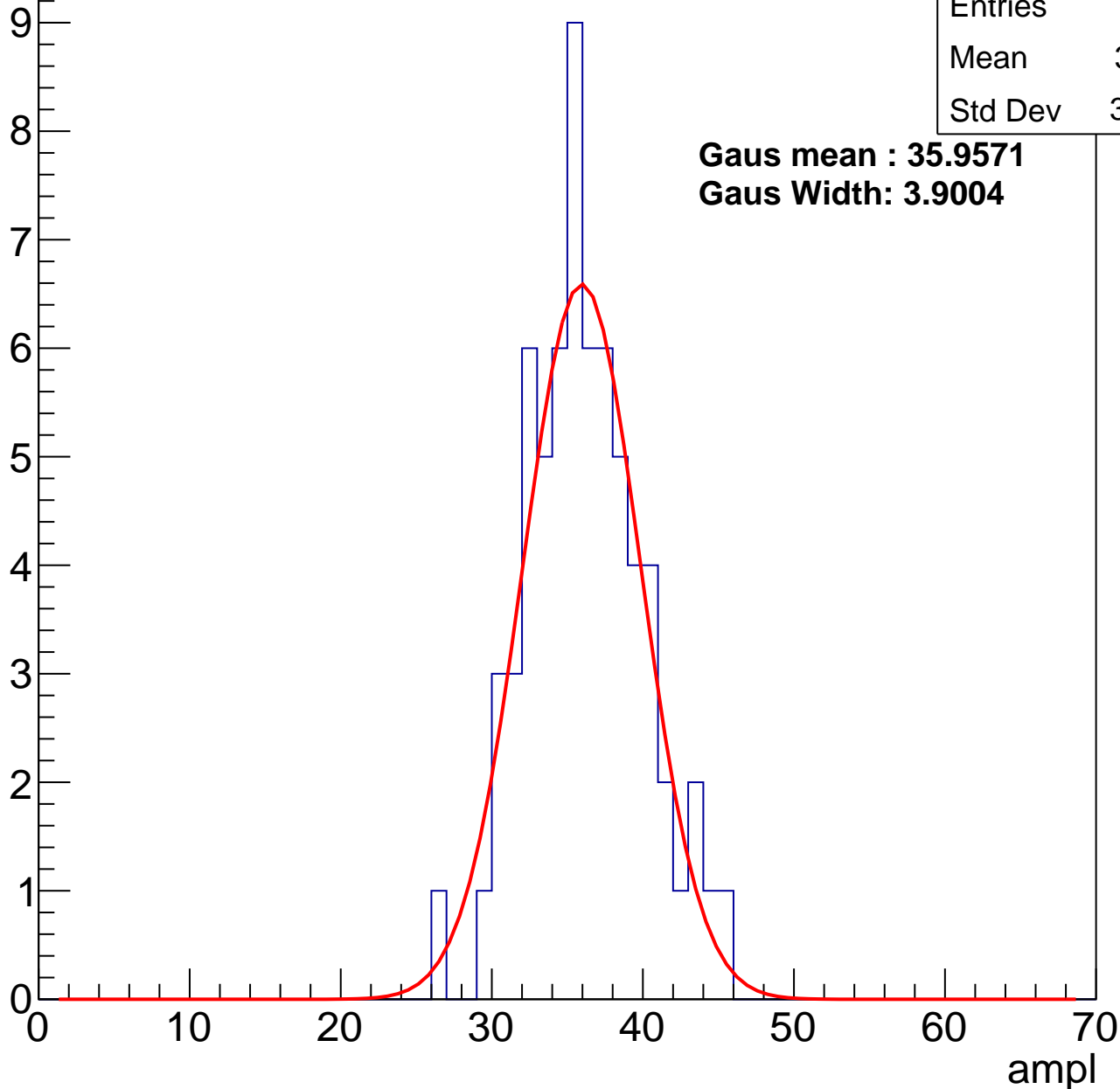
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.71
Std Dev	3.805

Gaus mean : 35.9571

Gaus Width: 3.9004



B1L102S, U14-ch8, adc2

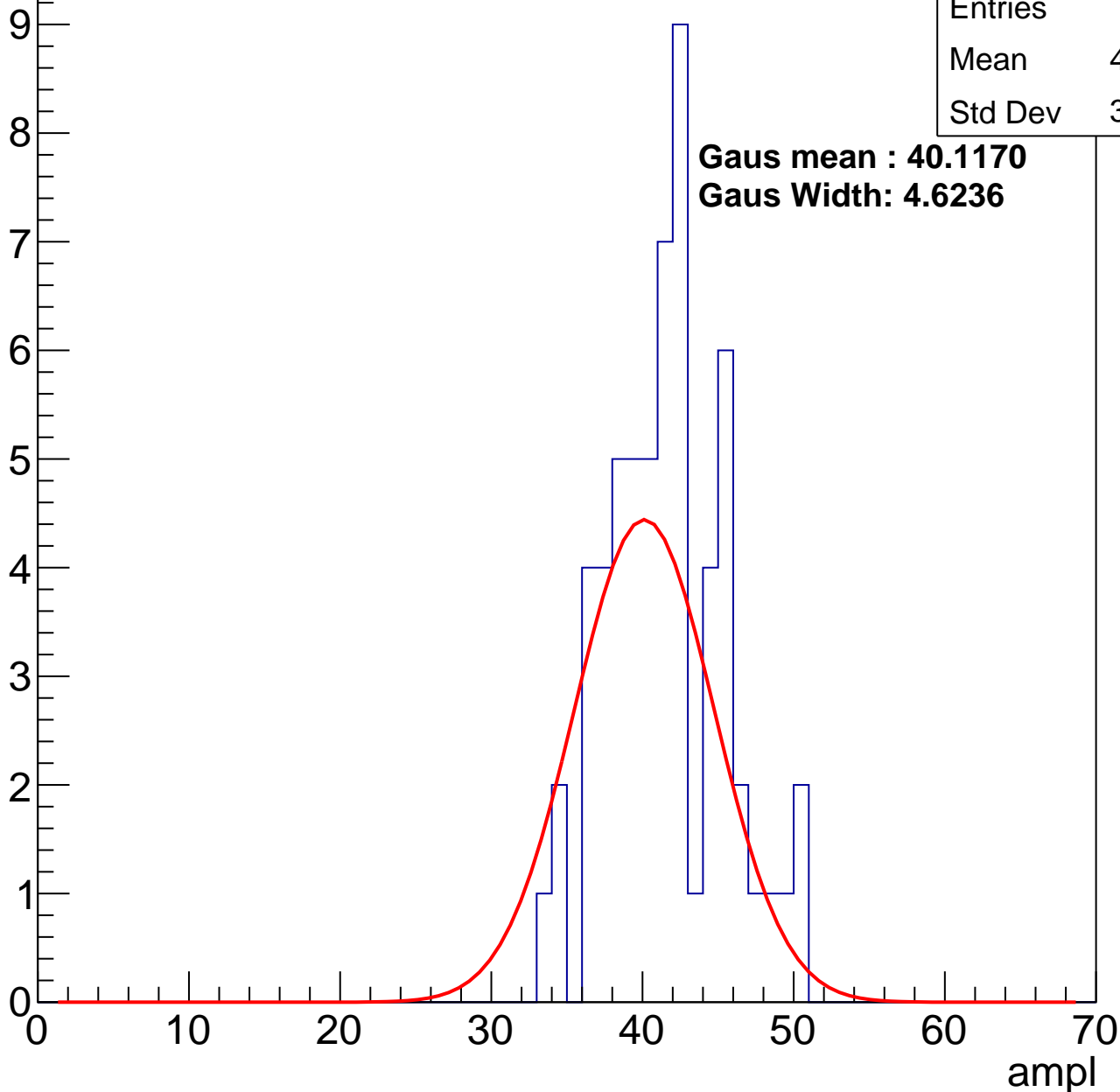
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.13
Std Dev	3.875

Gaus mean : 40.1170

Gaus Width: 4.6236

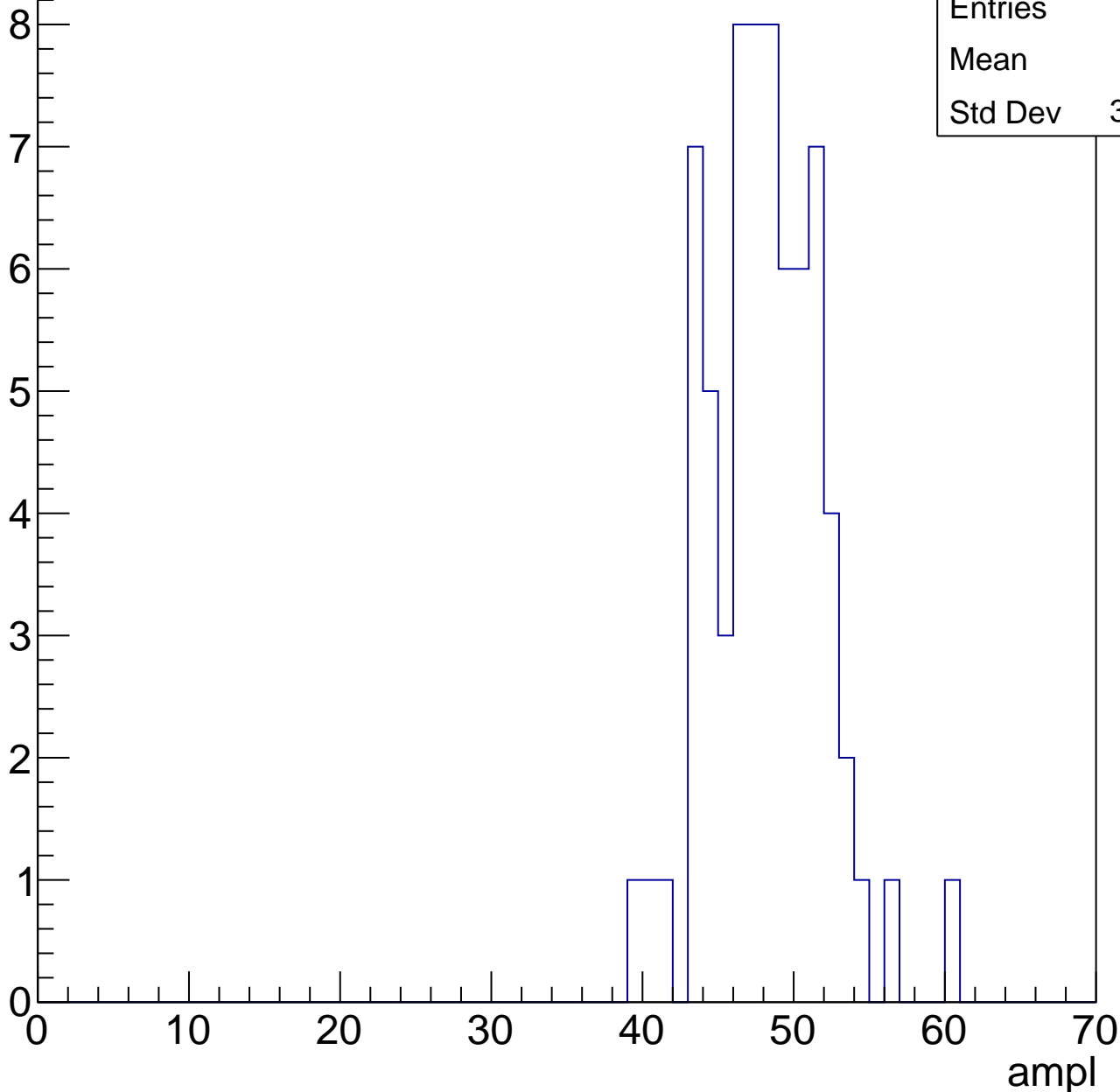


B1L102S, U14-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.7
Std Dev	3.705



B1L102S, U14-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	54.41
Std Dev	3.04

Entry

10

8

6

4

2

0

0

10

20

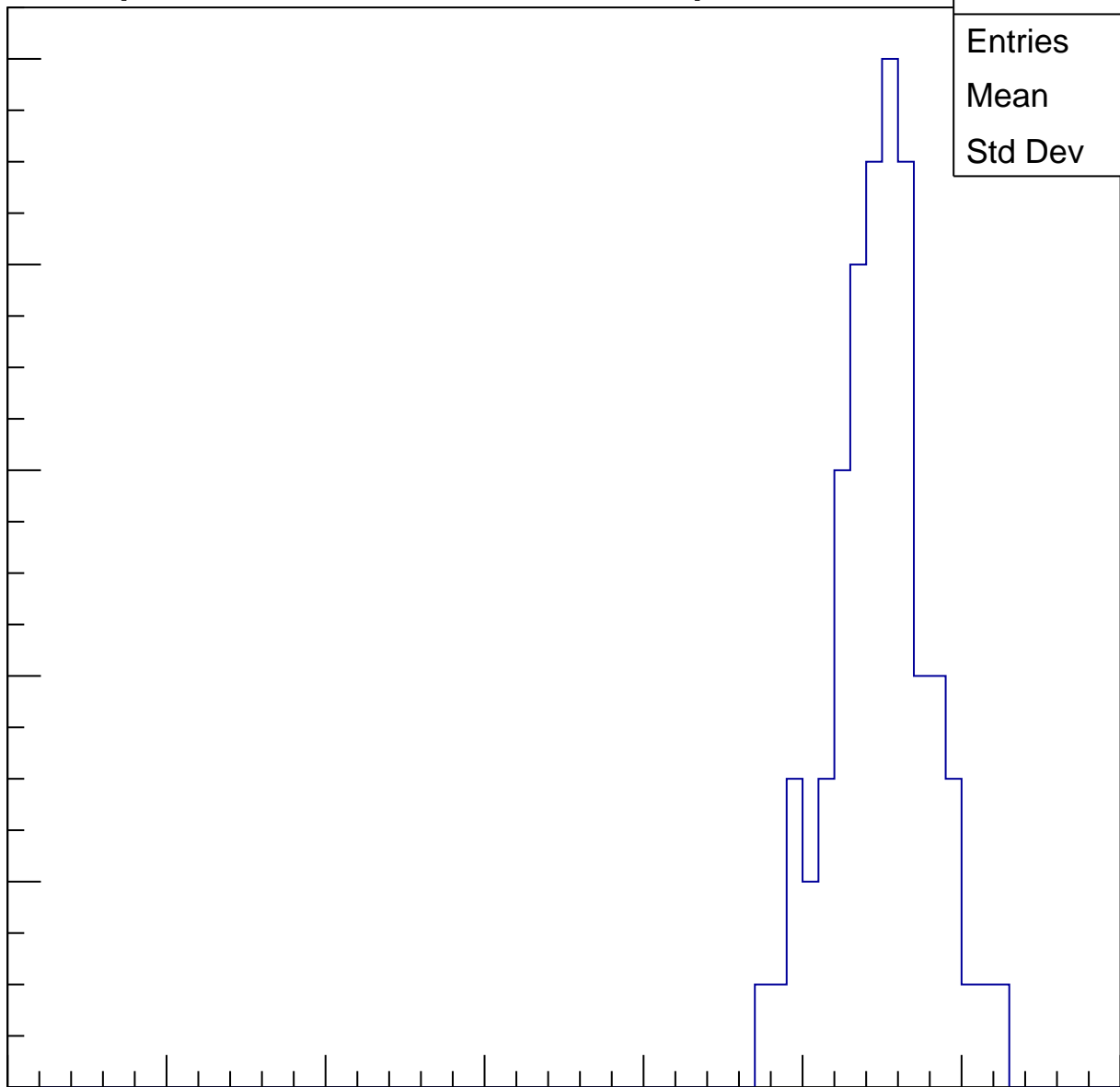
30

40

50

60

ampl

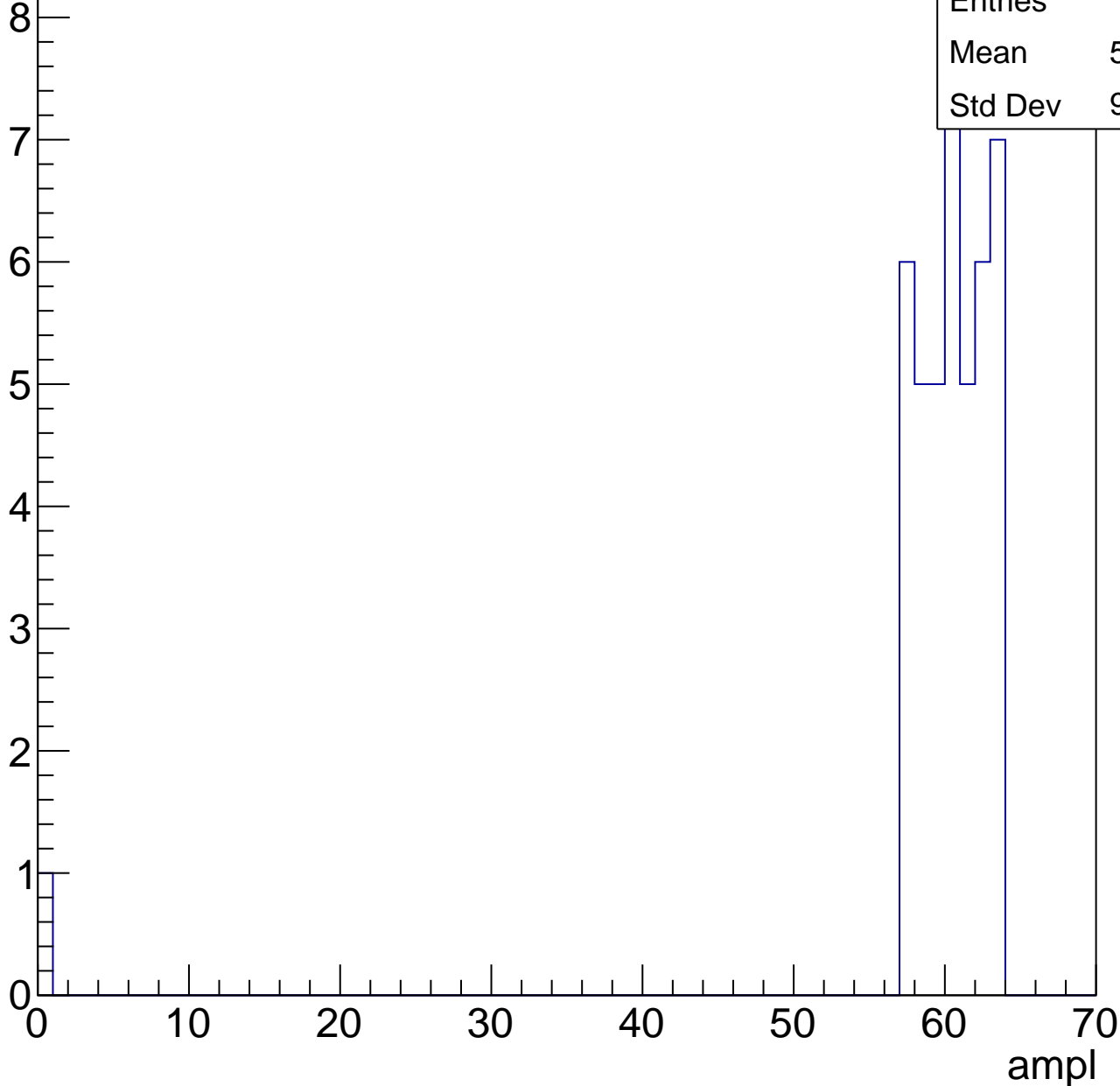


B1L102S, U14-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.72
Std Dev	9.277



B1L102S, U14-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

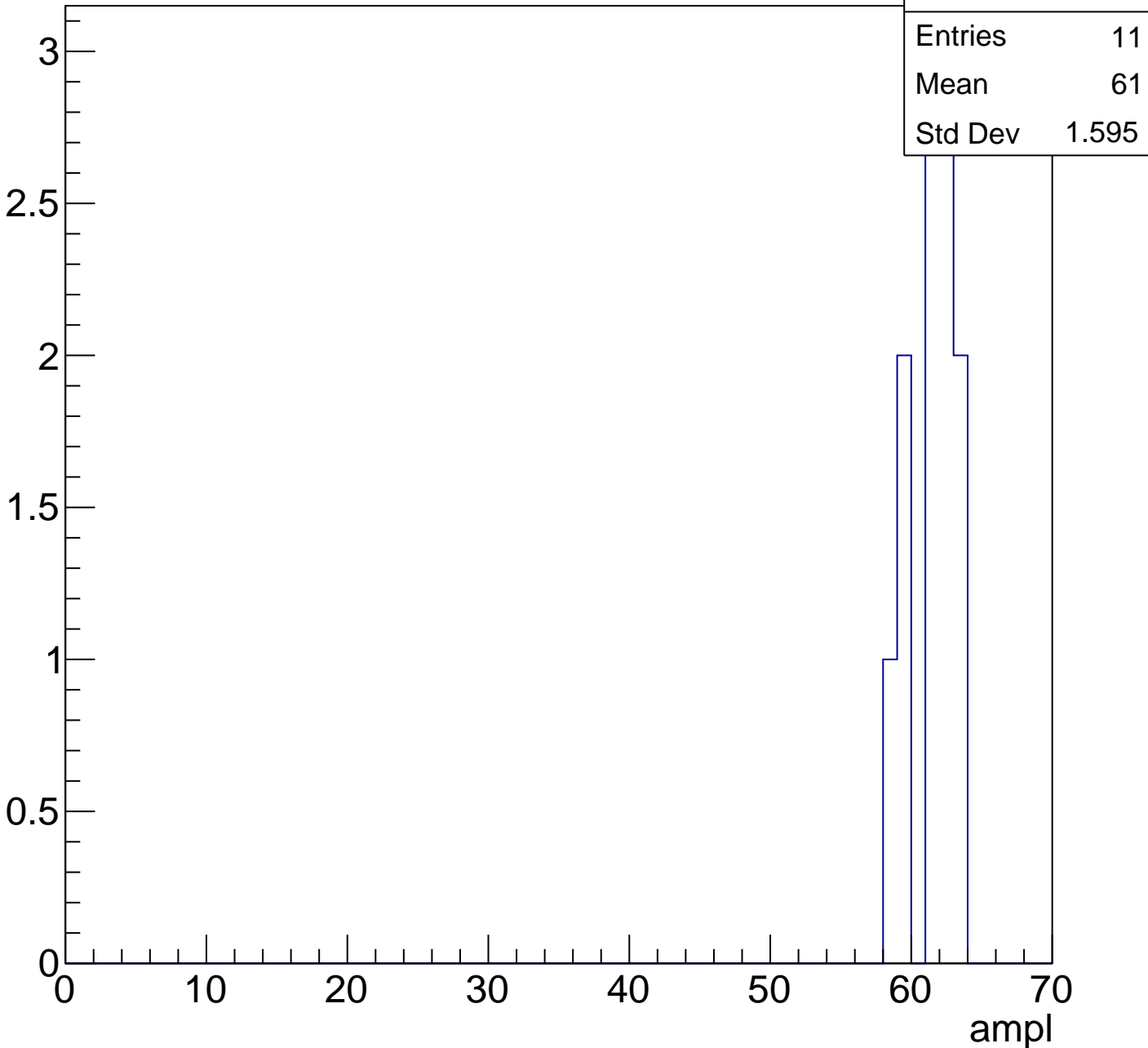
11

Mean

61

Std Dev

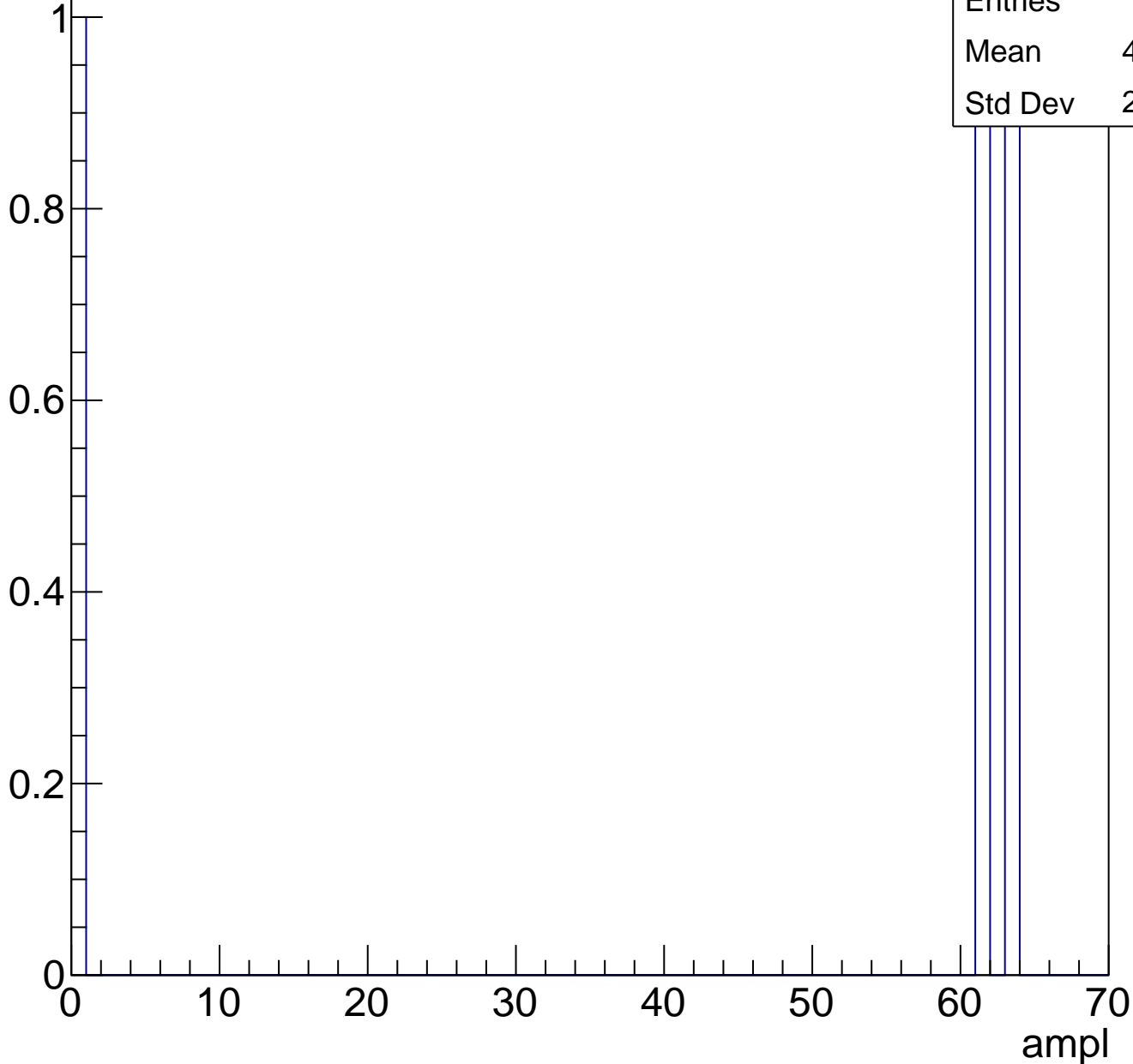
1.595



B1L102S, U14-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch9, adc0

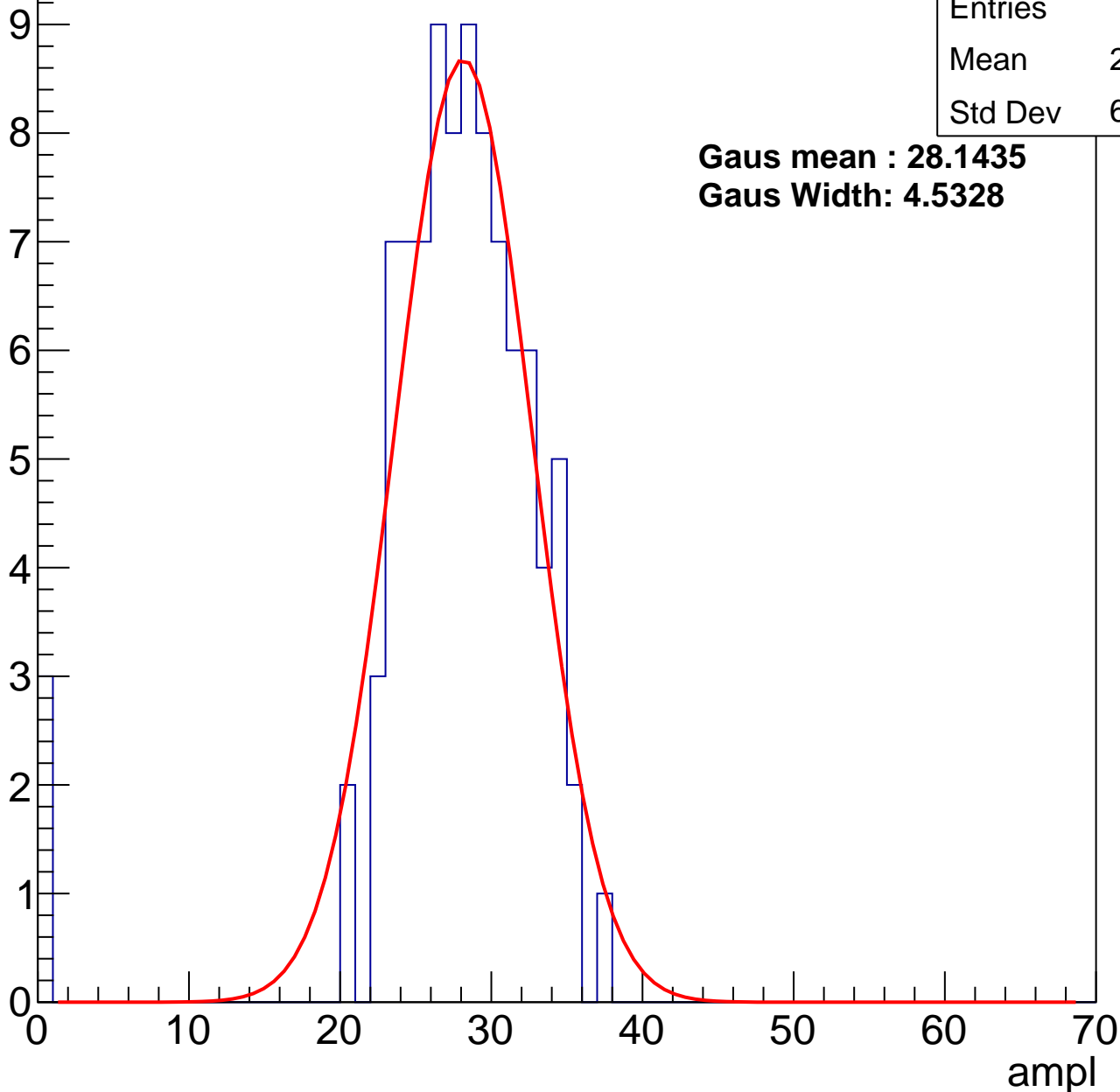
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	94
Mean	27.03
Std Dev	6.136

Gaus mean : 28.1435

Gaus Width: 4.5328



B1L102S, U14-ch9, adc1

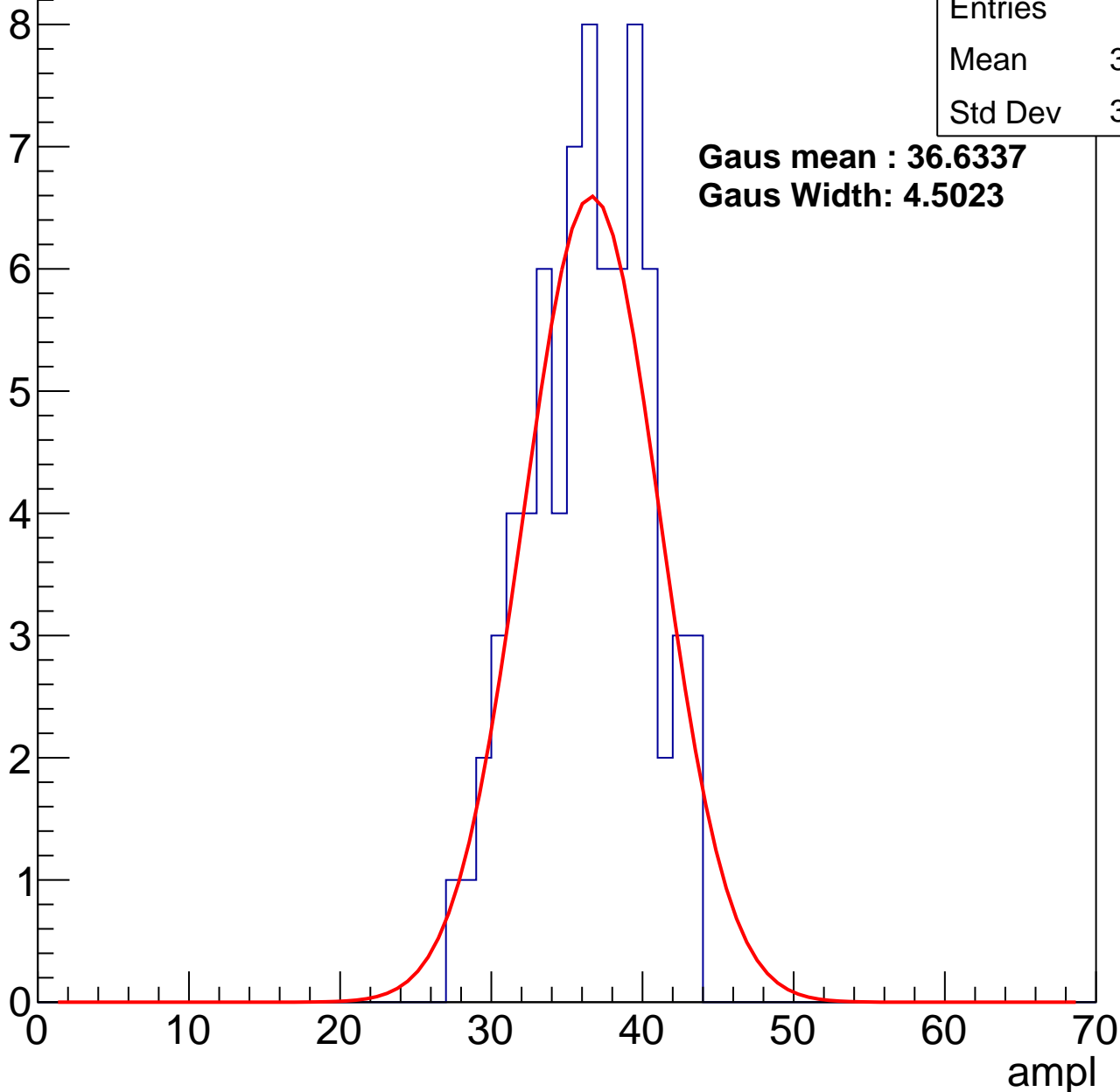
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.96
Std Dev	3.847

Gaus mean : 36.6337

Gaus Width: 4.5023



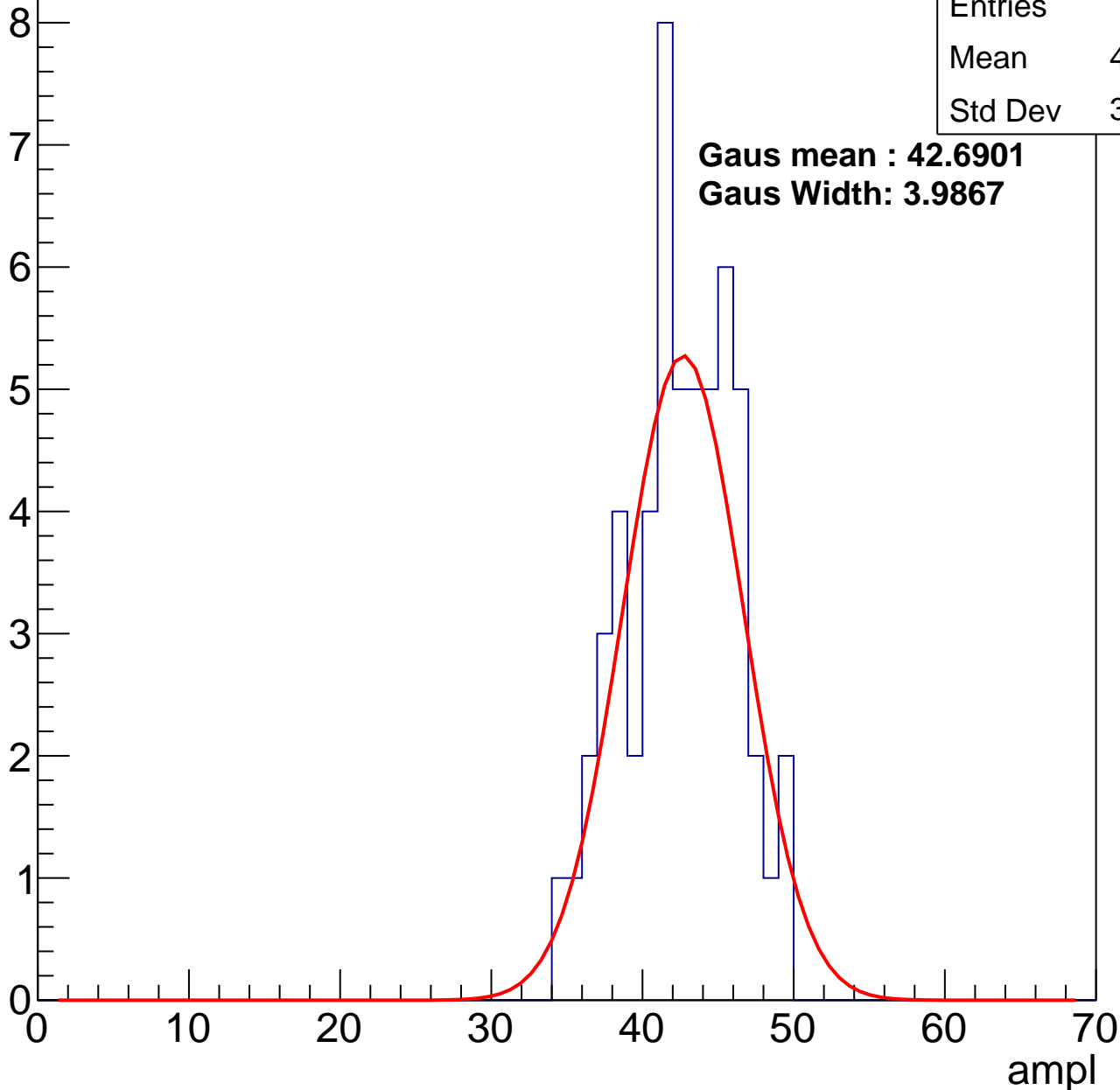
B1L102S, U14-ch9, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.05
Std Dev	3.558

Gaus mean : 42.6901
Gaus Width: 3.9867

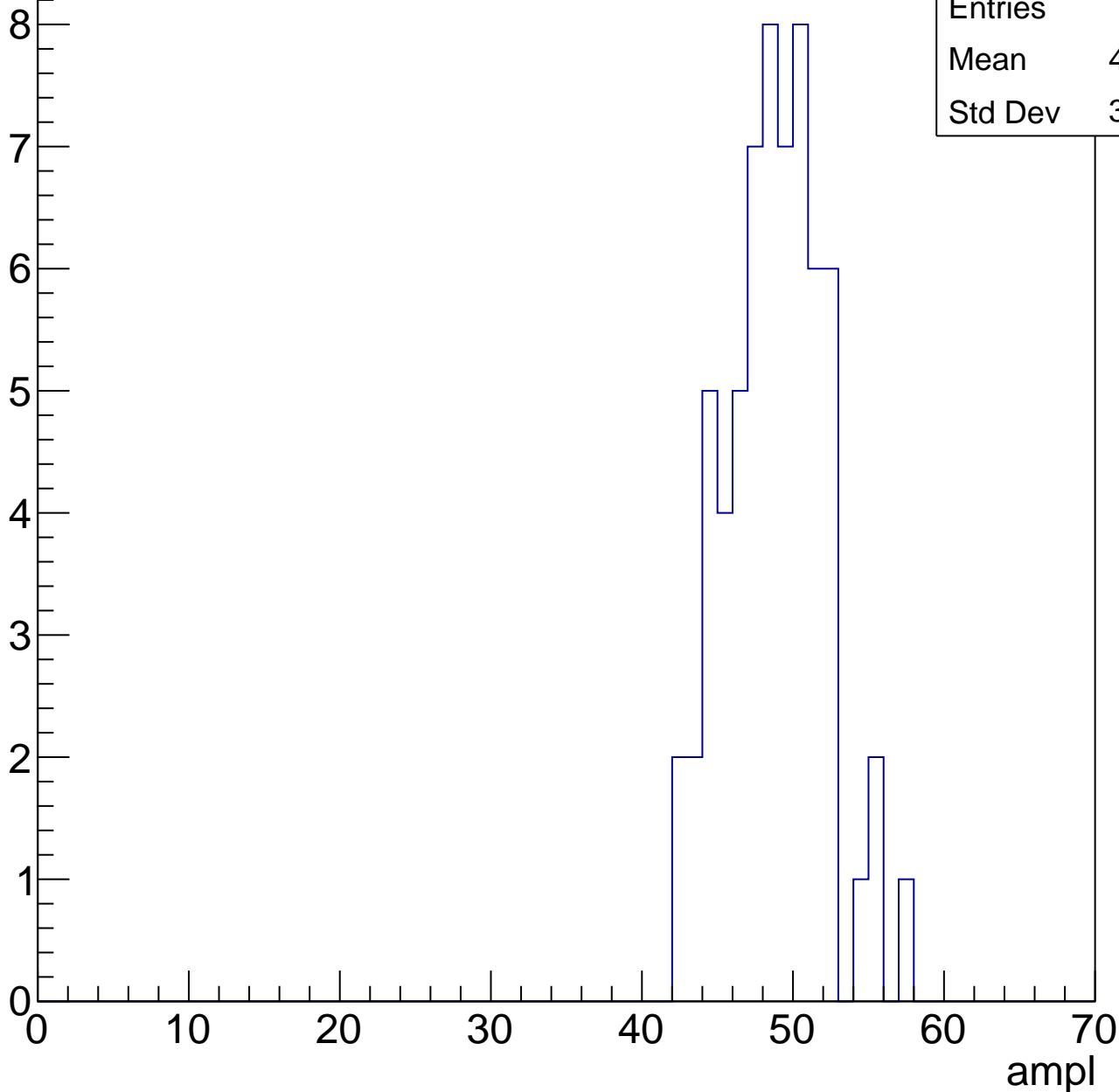


B1L102S, U14-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.36
Std Dev	3.213



B1L102S, U14-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

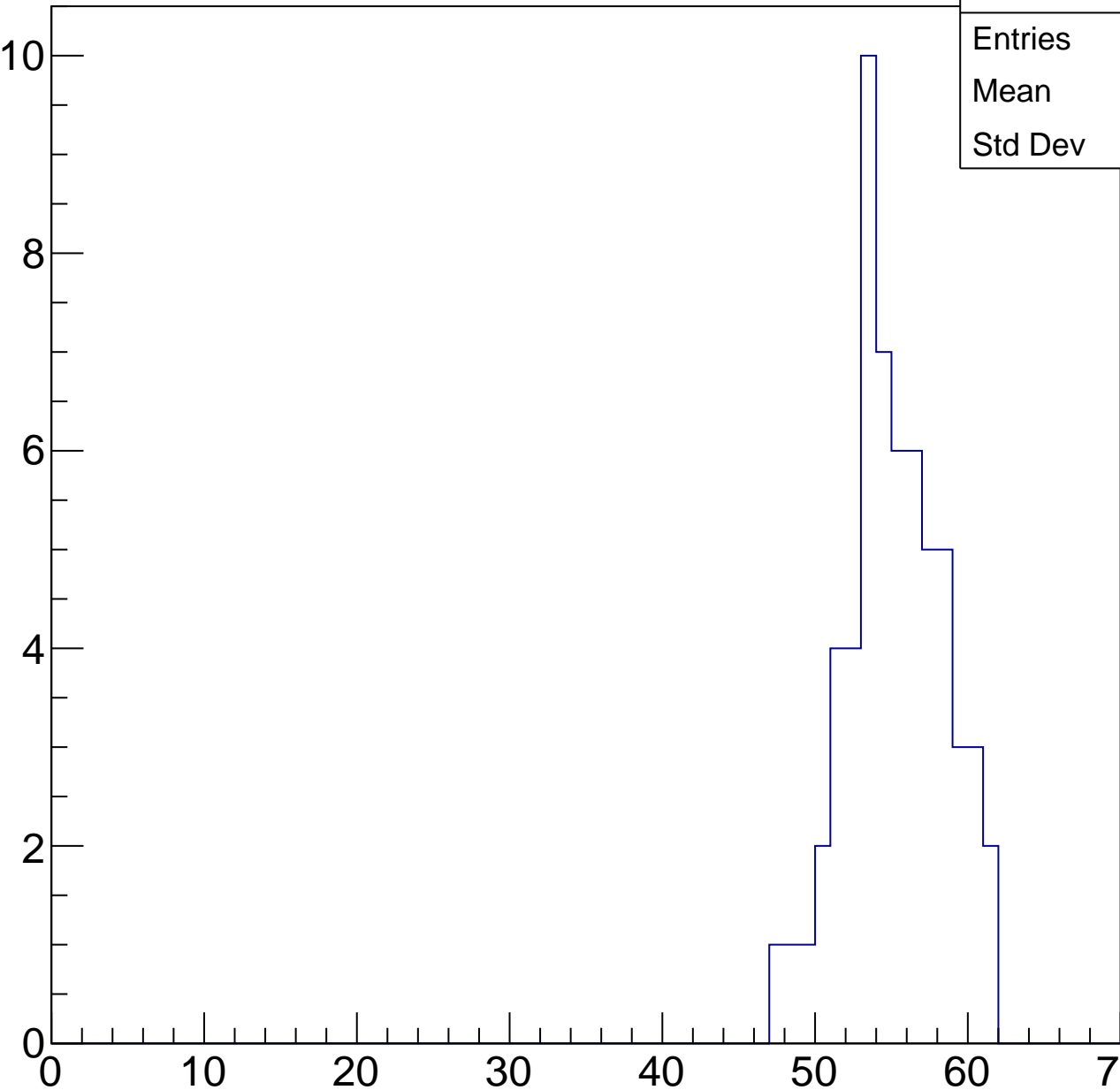
Entries	60
Mean	54.73
Std Dev	3.183

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

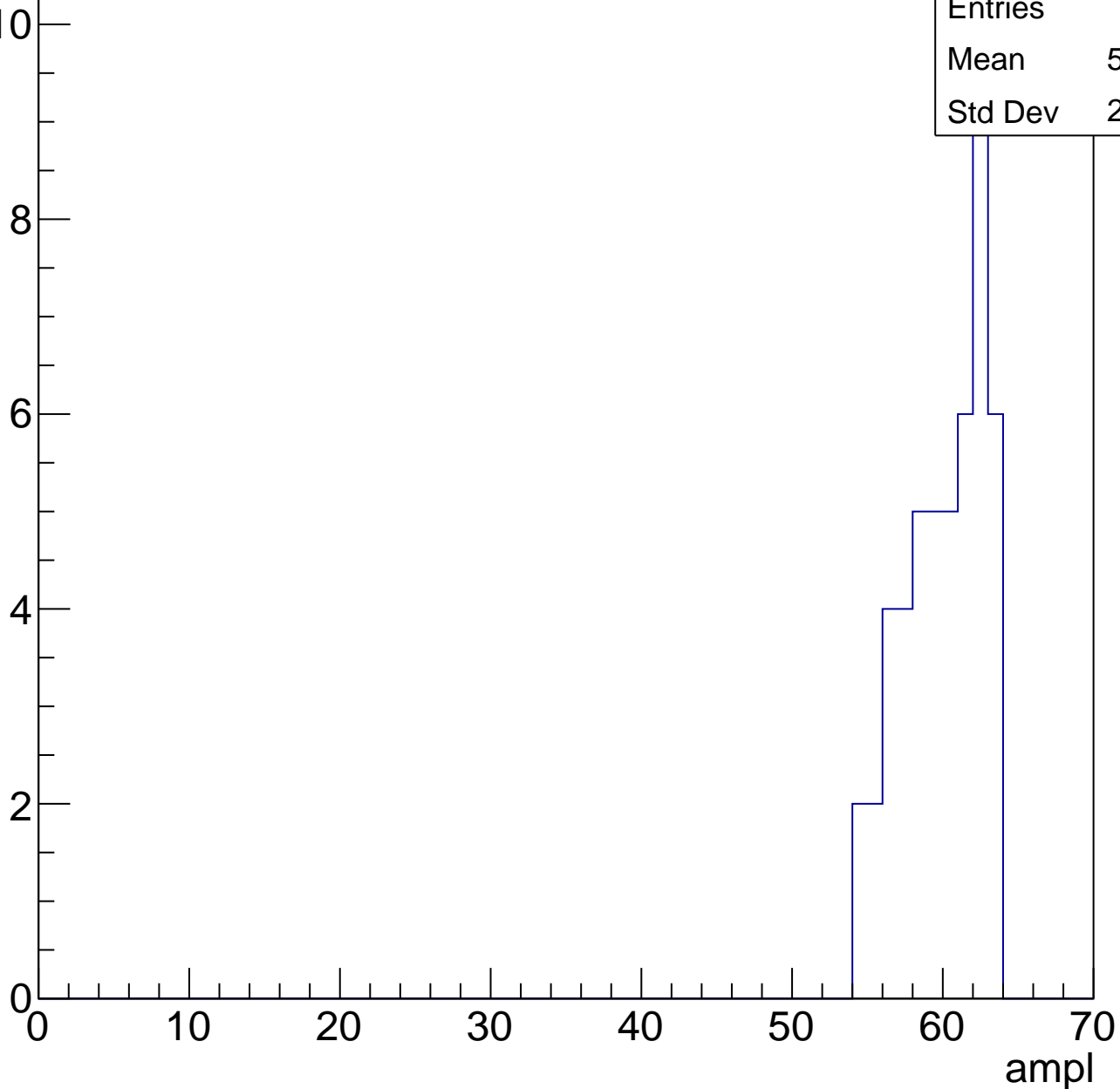


B1L102S, U14-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

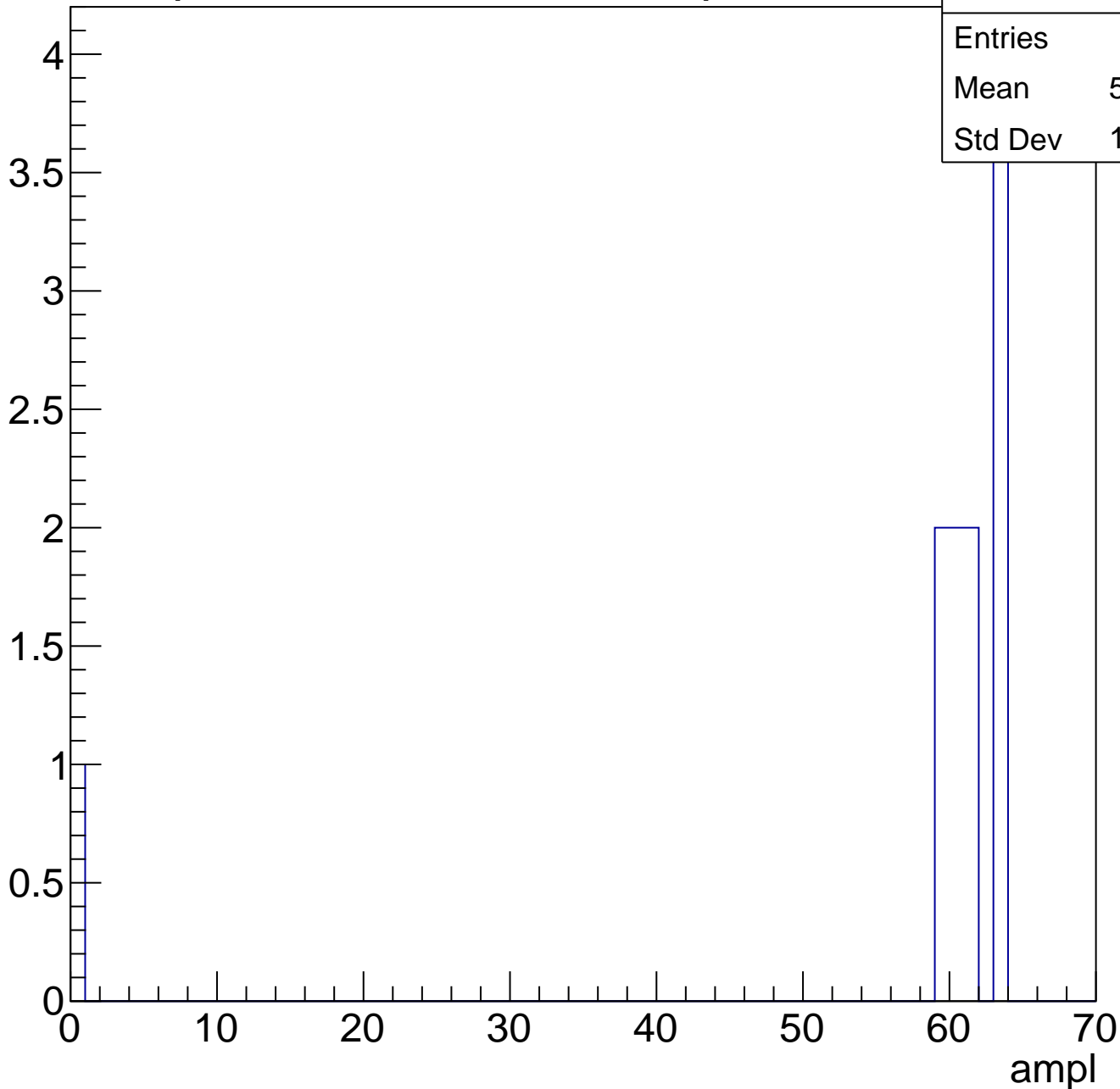
Entries	49
Mean	59.57
Std Dev	2.626



B1L102S, U14-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch10, adc0

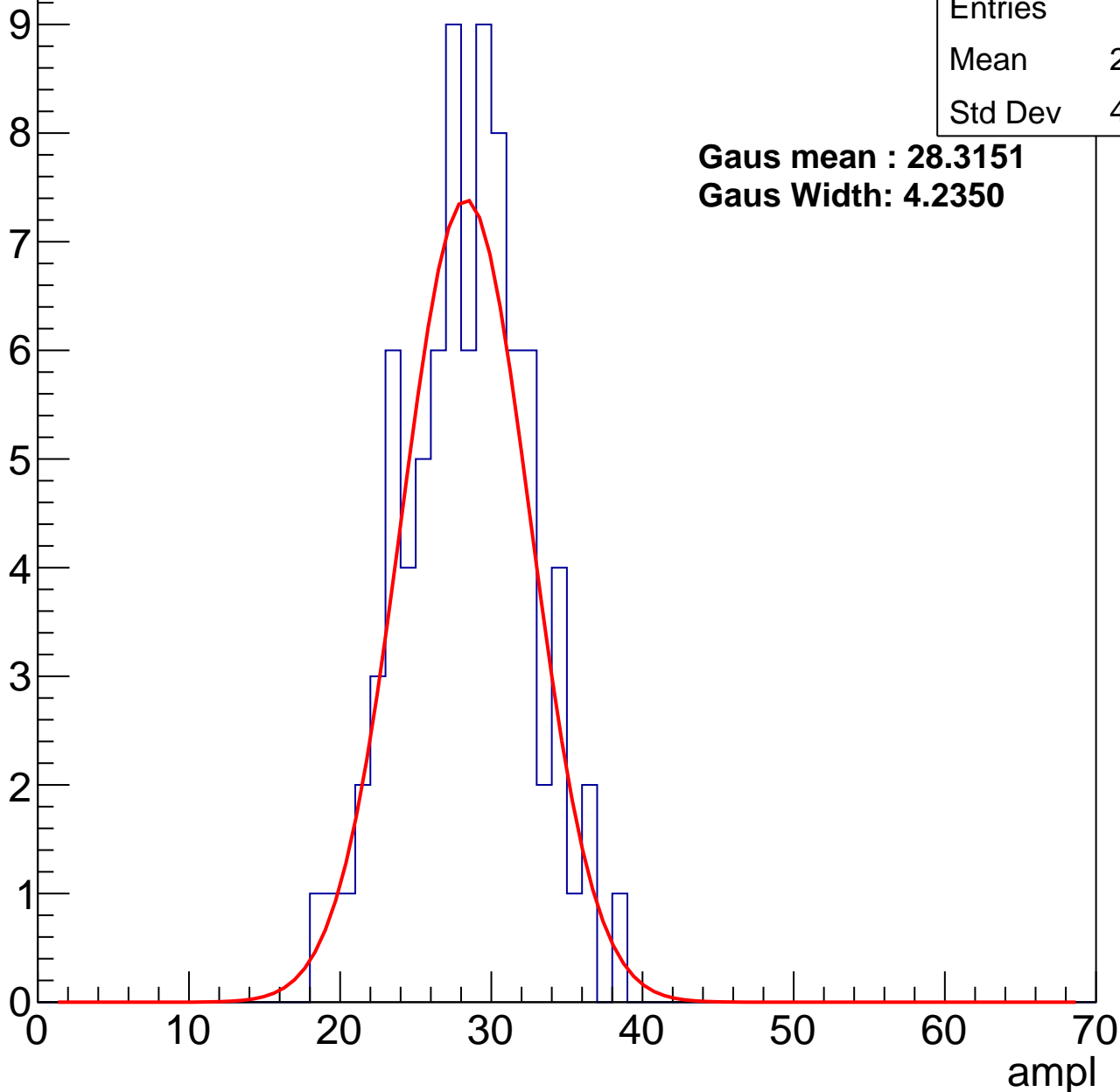
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.92
Std Dev	4.113

Gaus mean : 28.3151

Gaus Width: 4.2350



B1L102S, U14-ch10, adc1

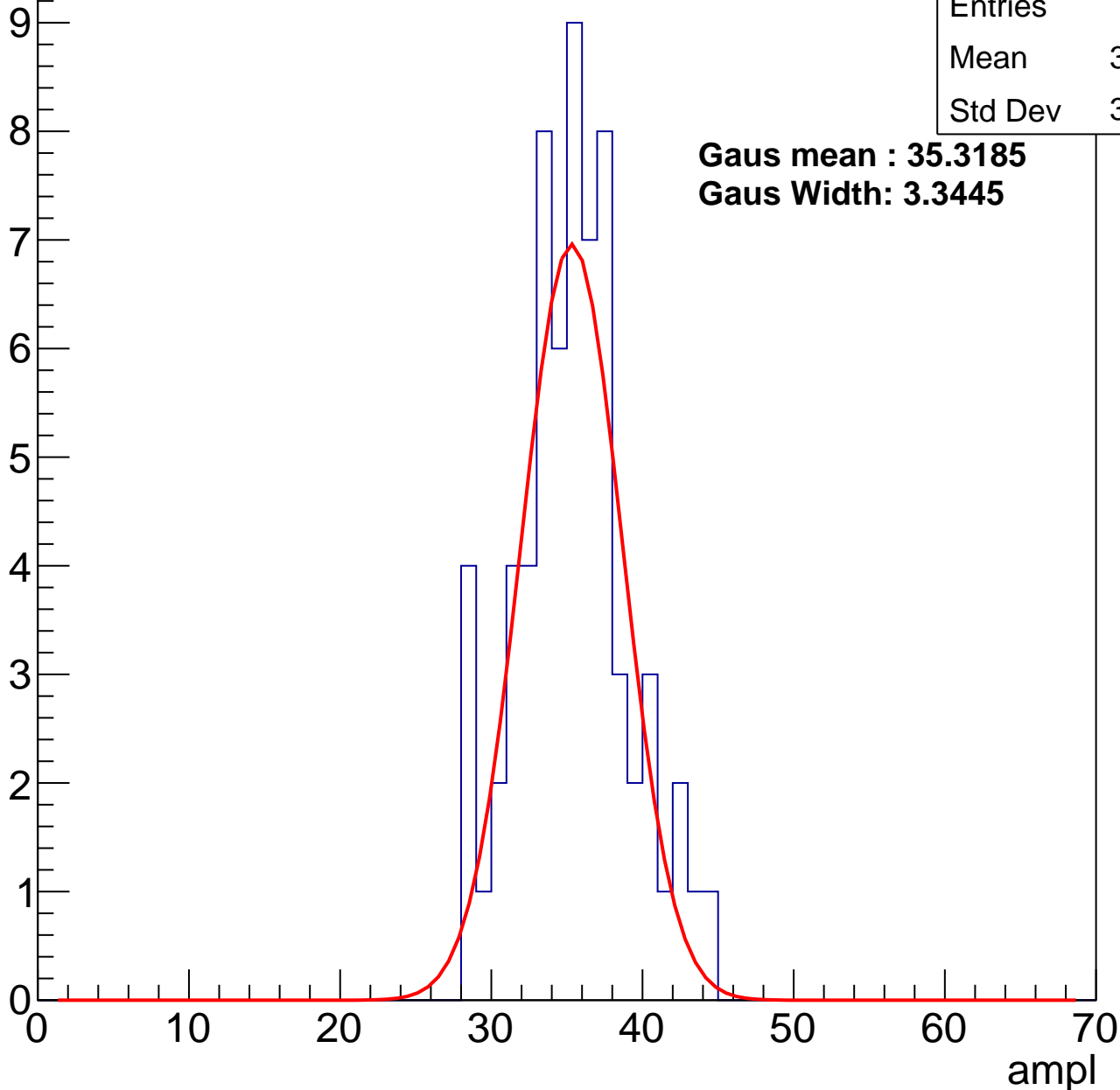
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.97
Std Dev	3.635

Gaus mean : 35.3185

Gaus Width: 3.3445



B1L102S, U14-ch10, adc2

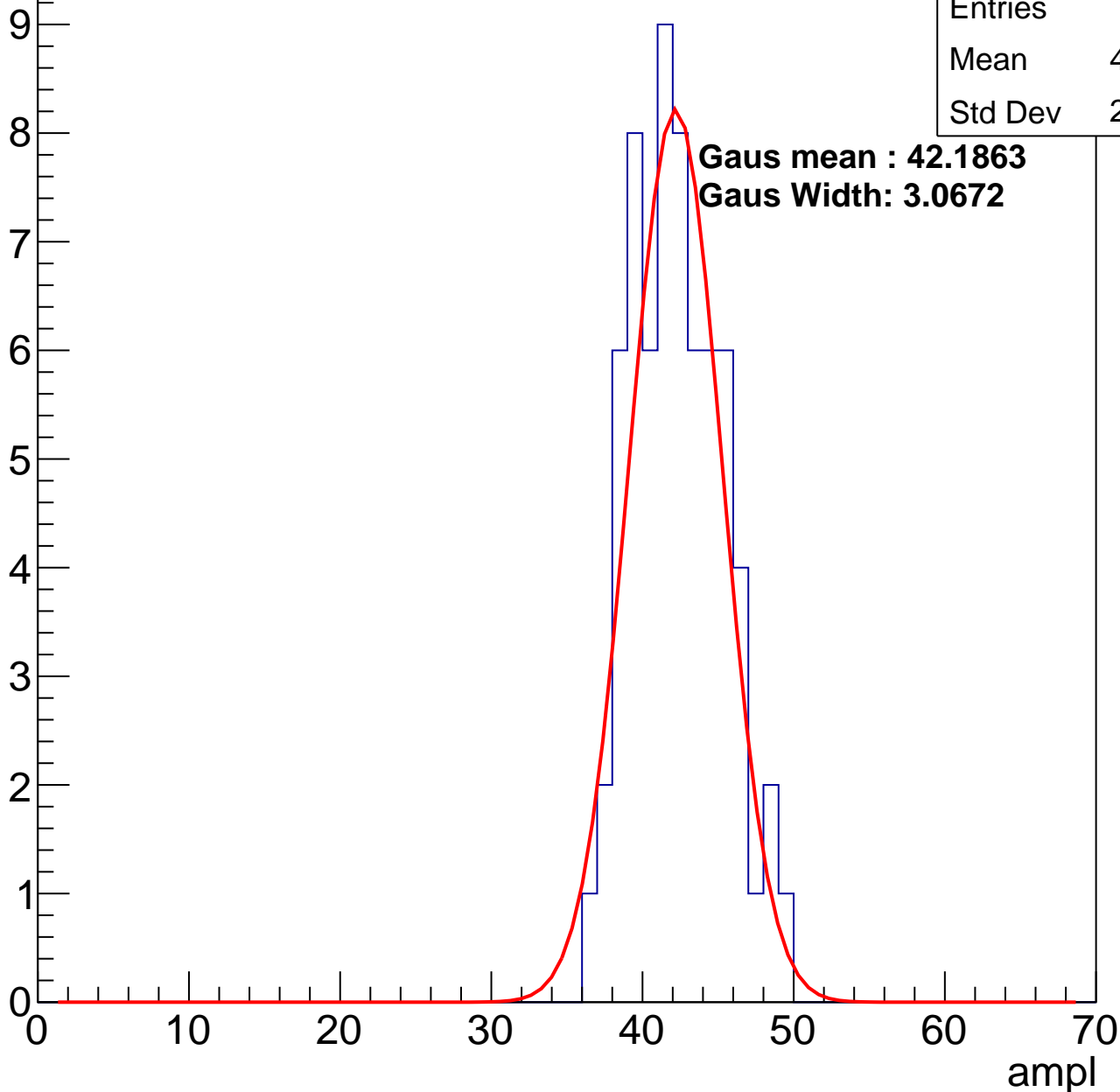
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.86
Std Dev	2.964

Gaus mean : 42.1863

Gaus Width: 3.0672

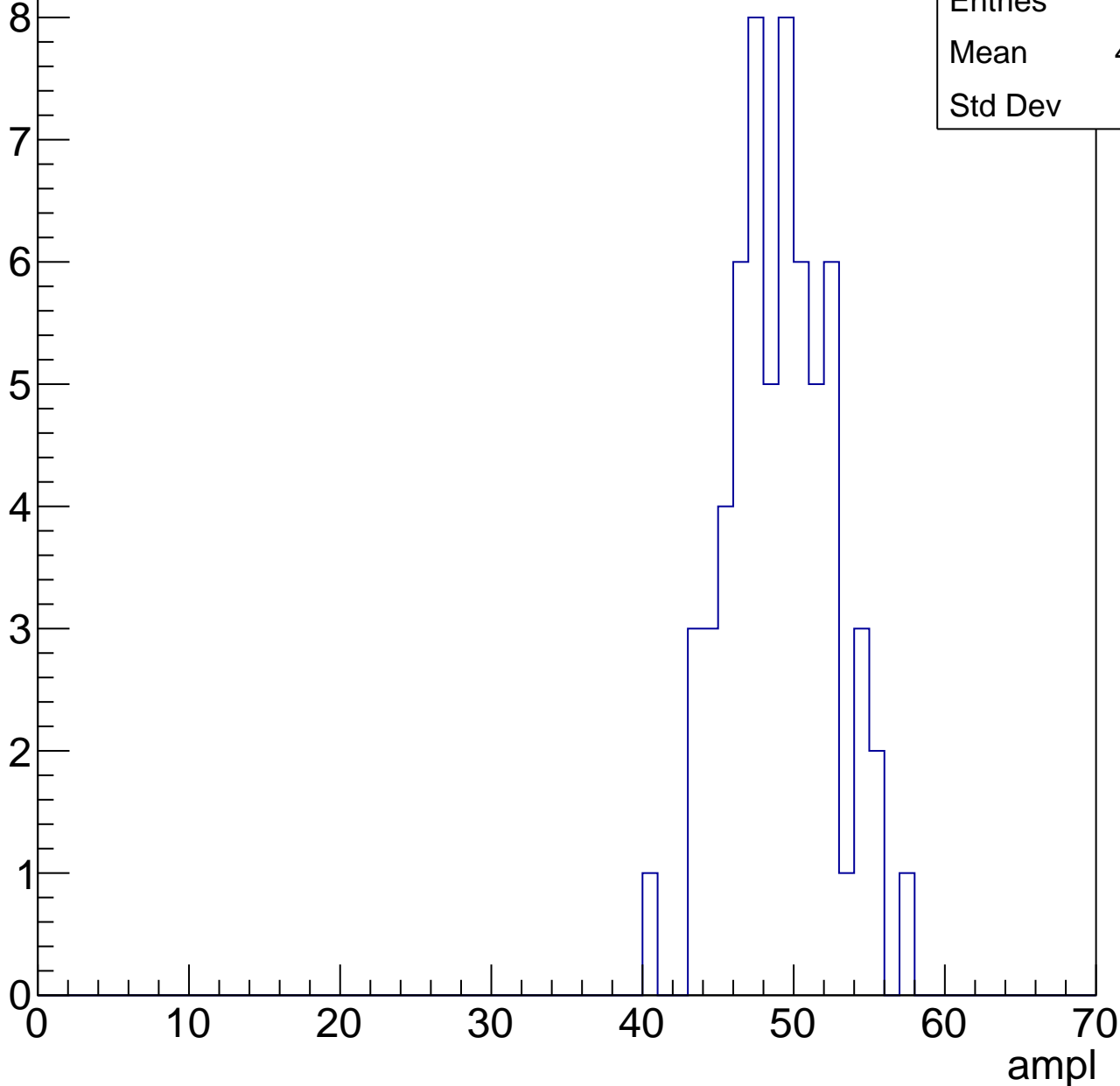


B1L102S, U14-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.61
Std Dev	3.4

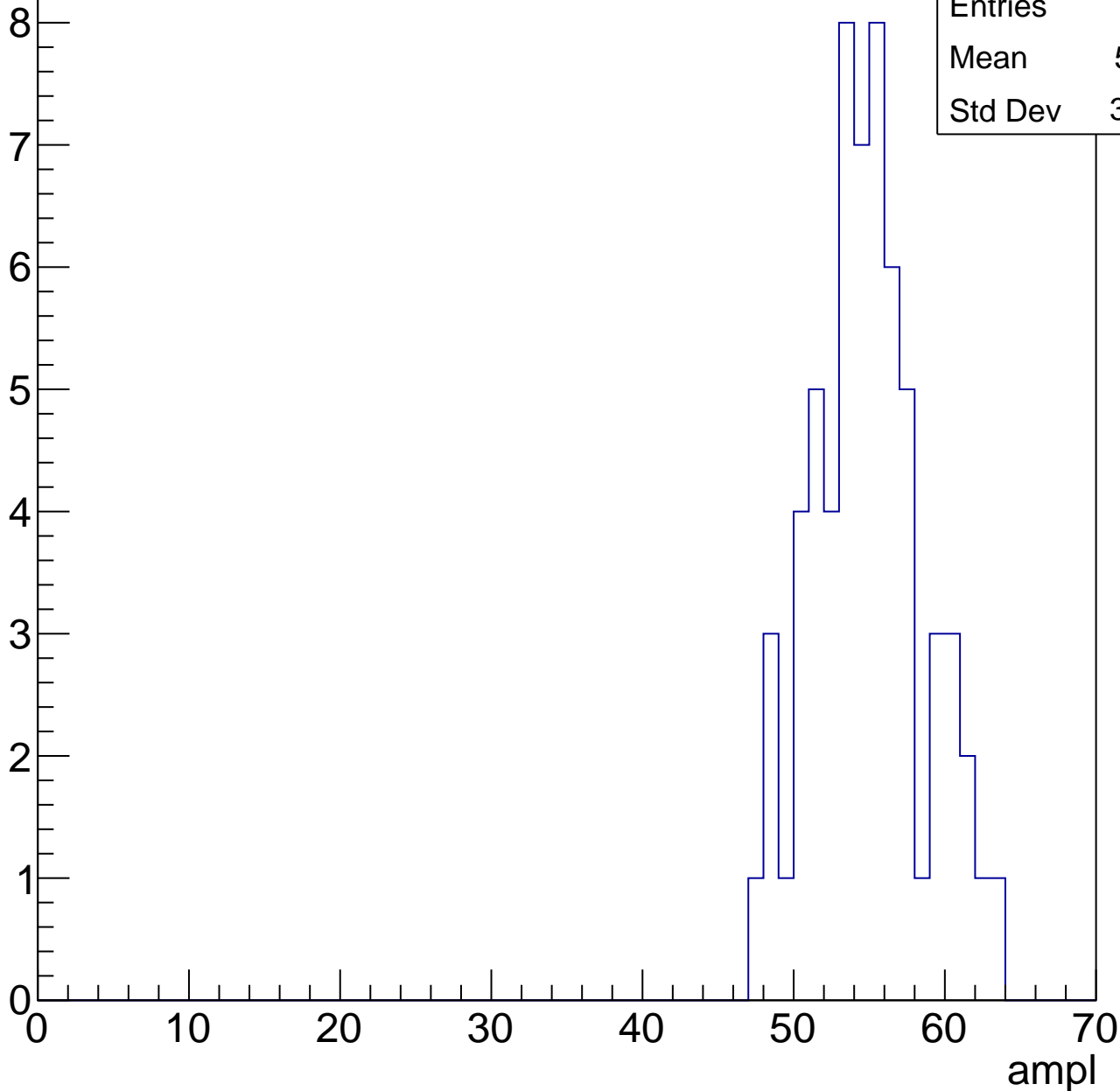


B1L102S, U14-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.41
Std Dev	3.615

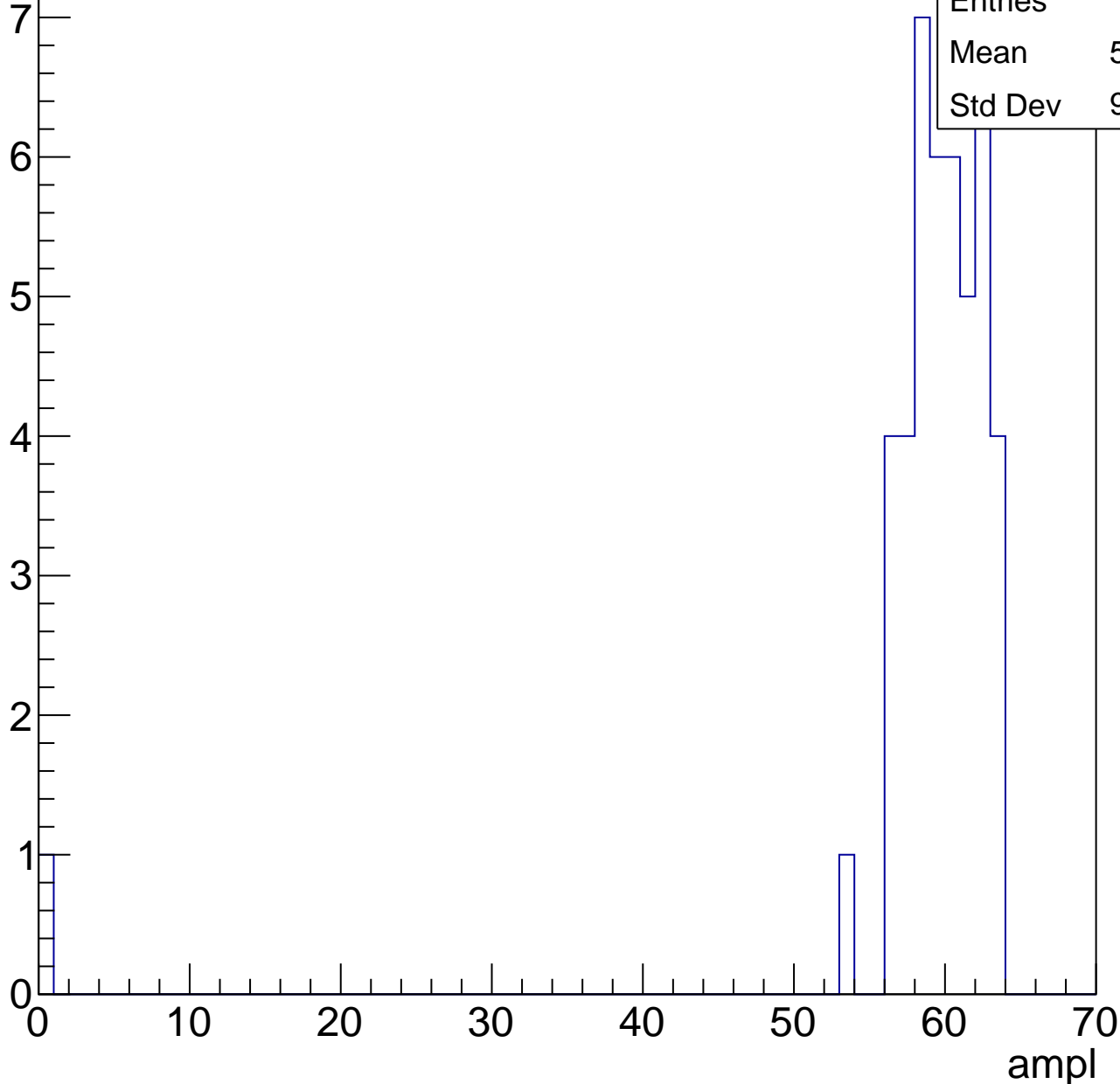


B1L102S, U14-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.13
Std Dev	9.062

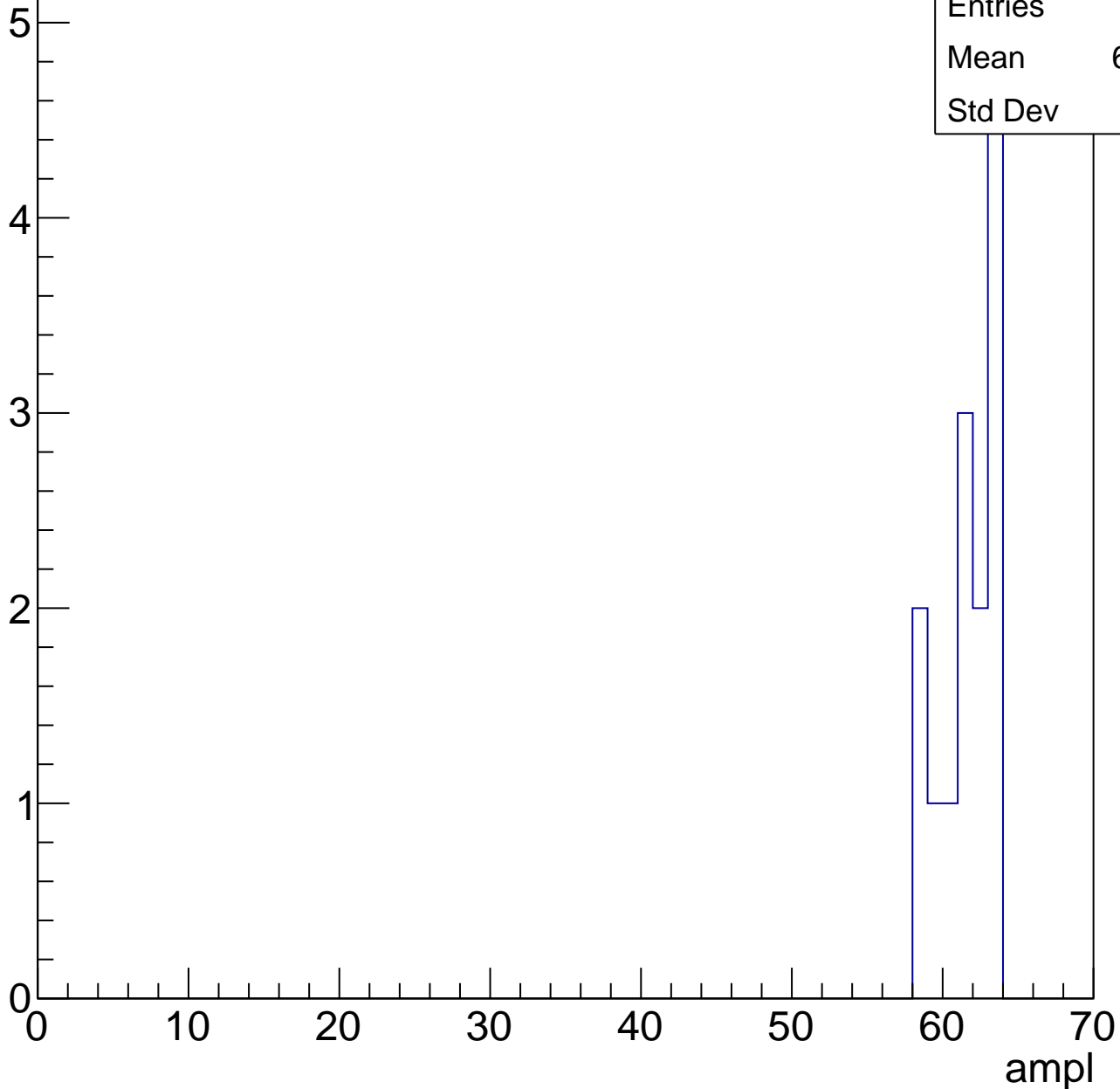


B1L102S, U14-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.21
Std Dev	1.78



B1L102S, U14-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch11, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	28.52
Std Dev	4.745

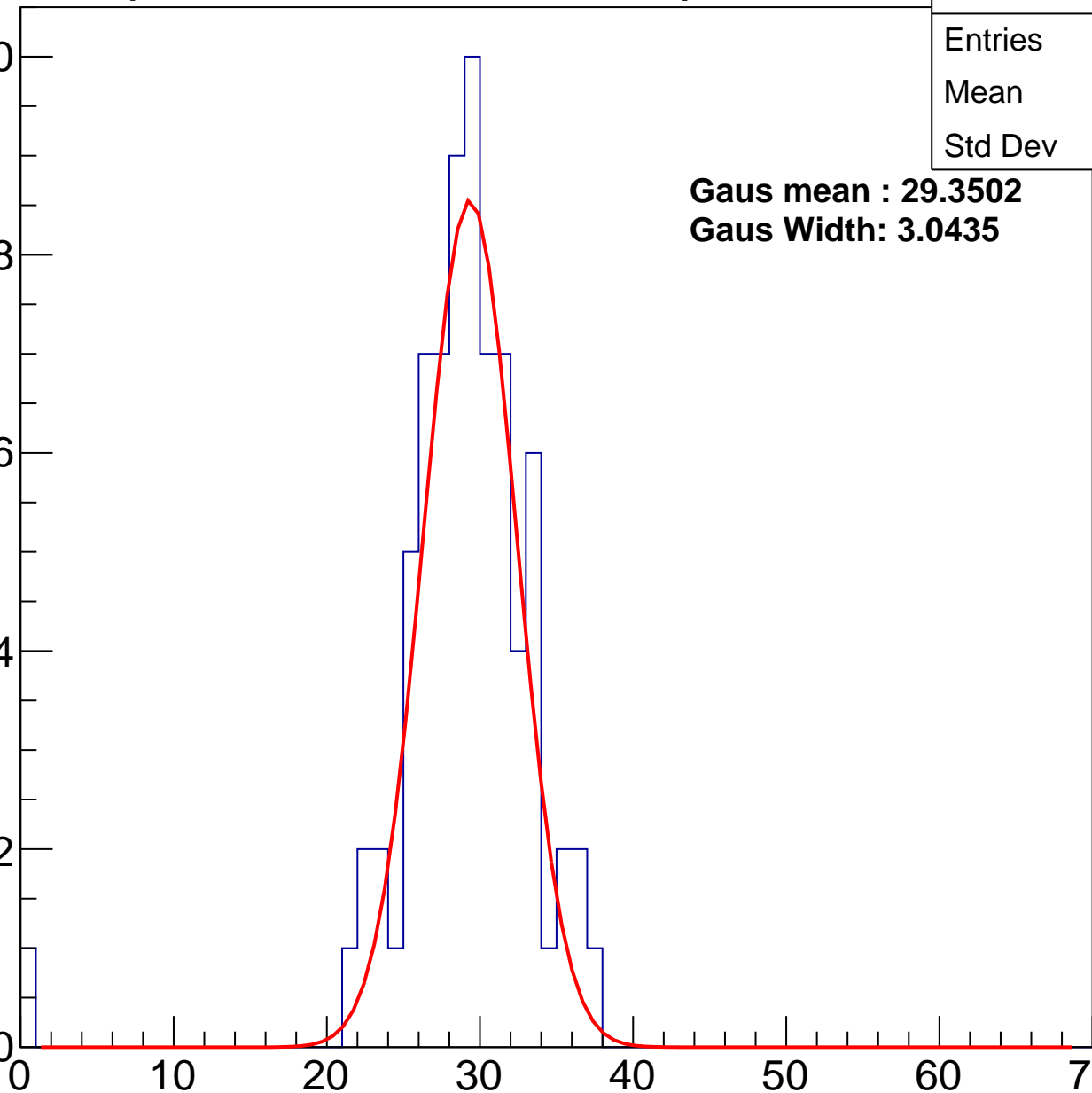
Gaus mean : 29.3502

Gaus Width: 3.0435

Entry

10
8
6
4
2
0

ampl



B1L102S, U14-ch11, adc1

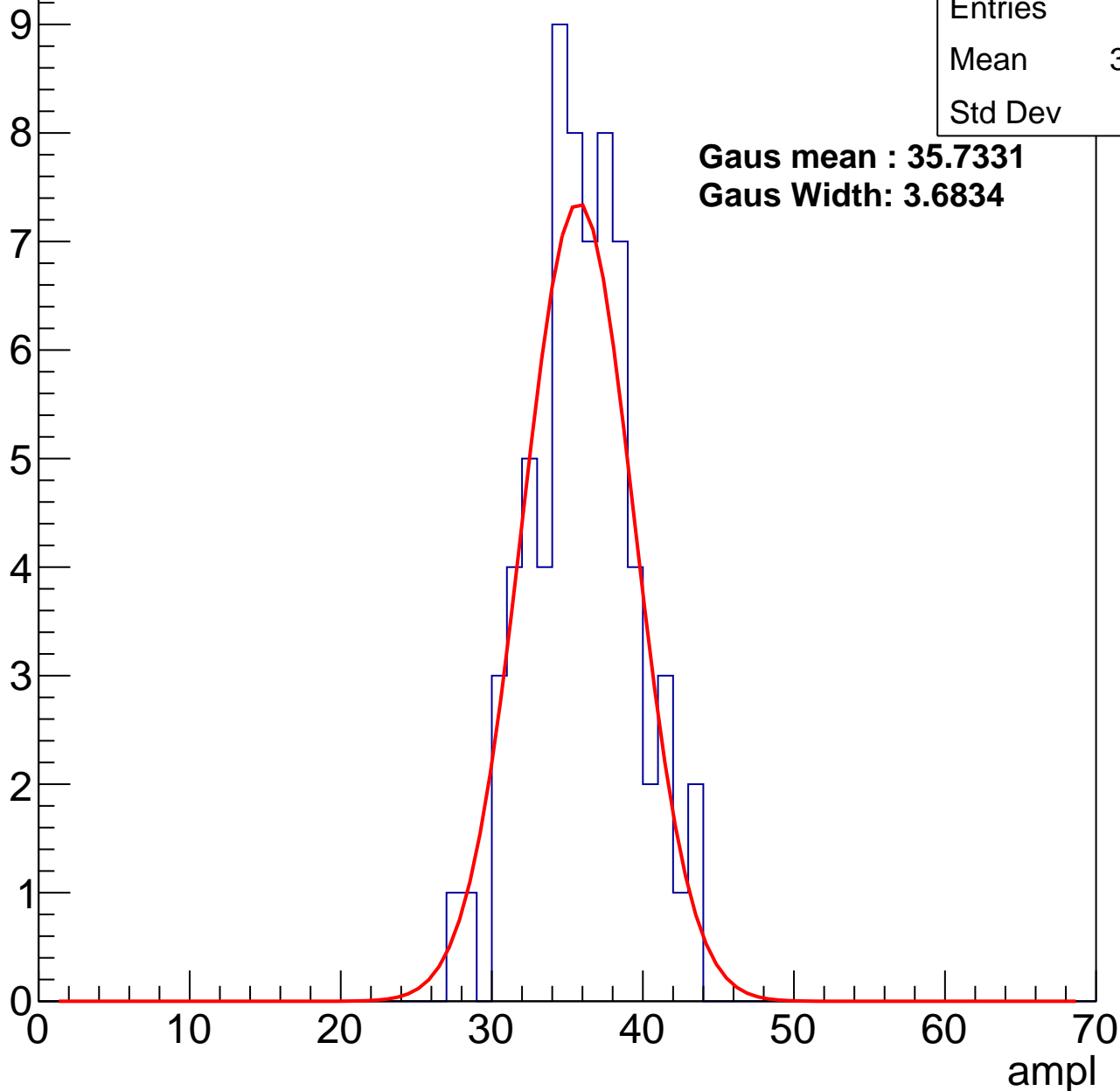
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.48
Std Dev	3.42

Gaus mean : 35.7331

Gaus Width: 3.6834



B1L102S, U14-ch11, adc2

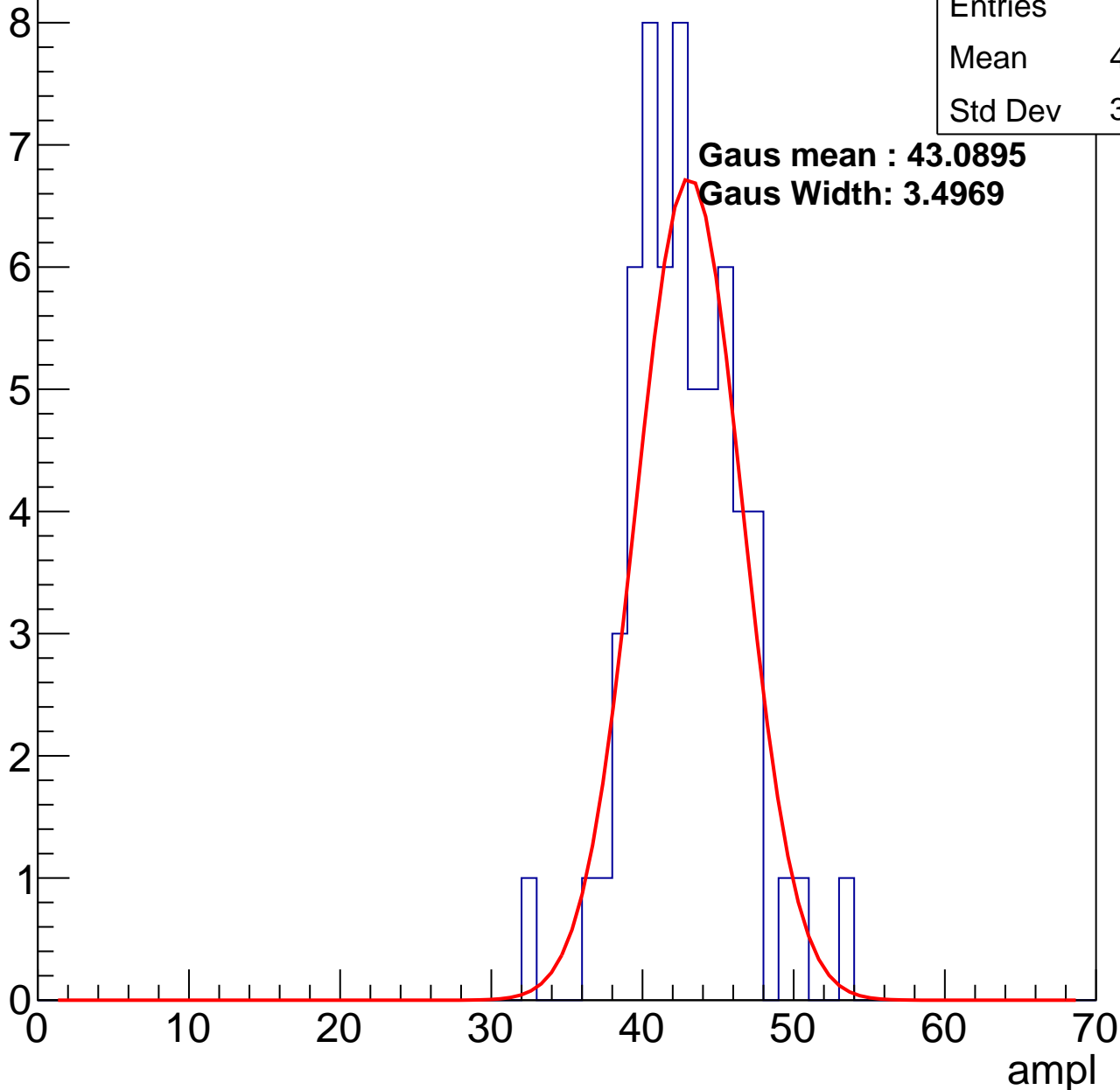
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.36
Std Dev	3.553

Gaus mean : 43.0895

Gaus Width: 3.4969

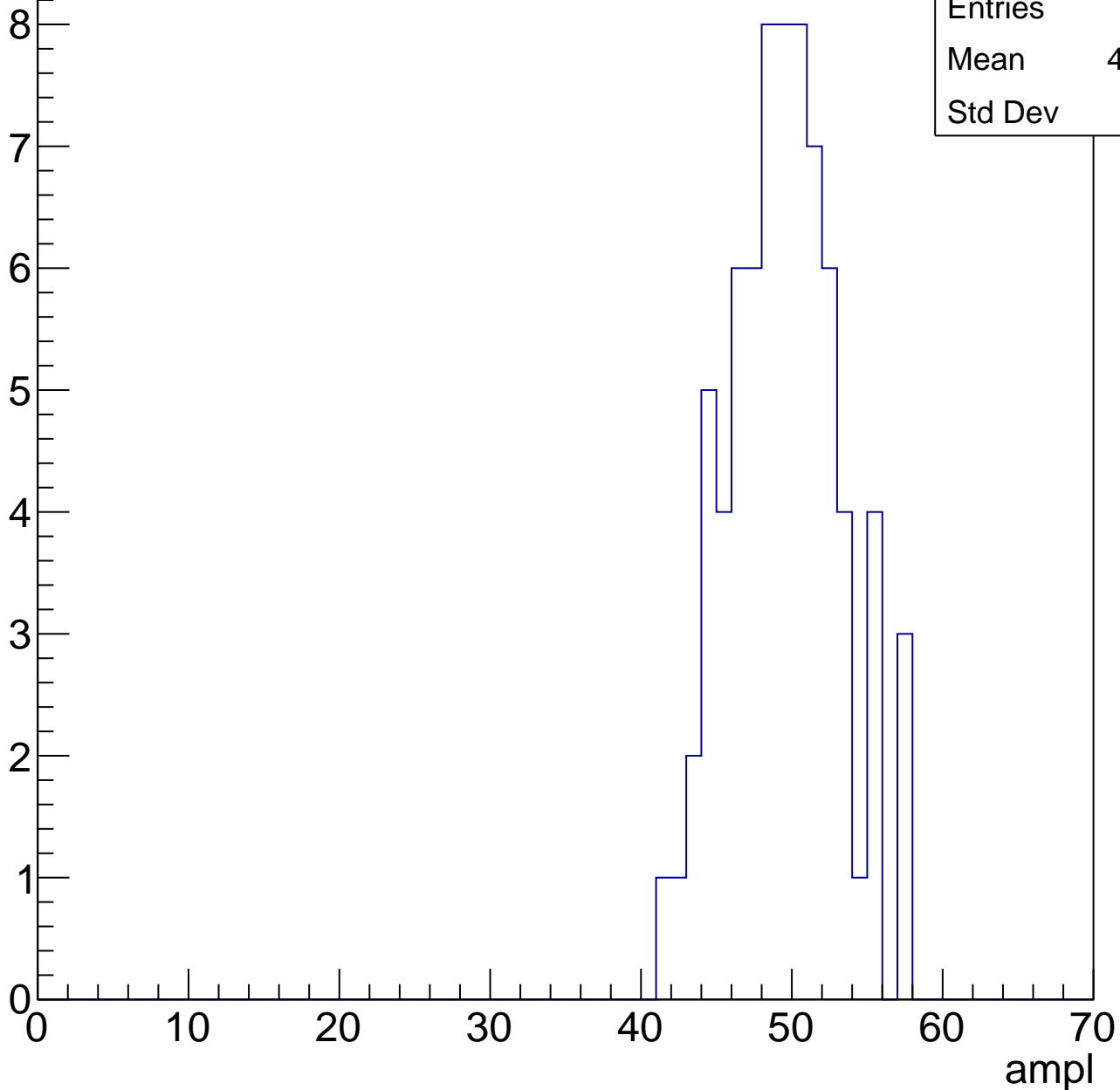


B1L102S, U14-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

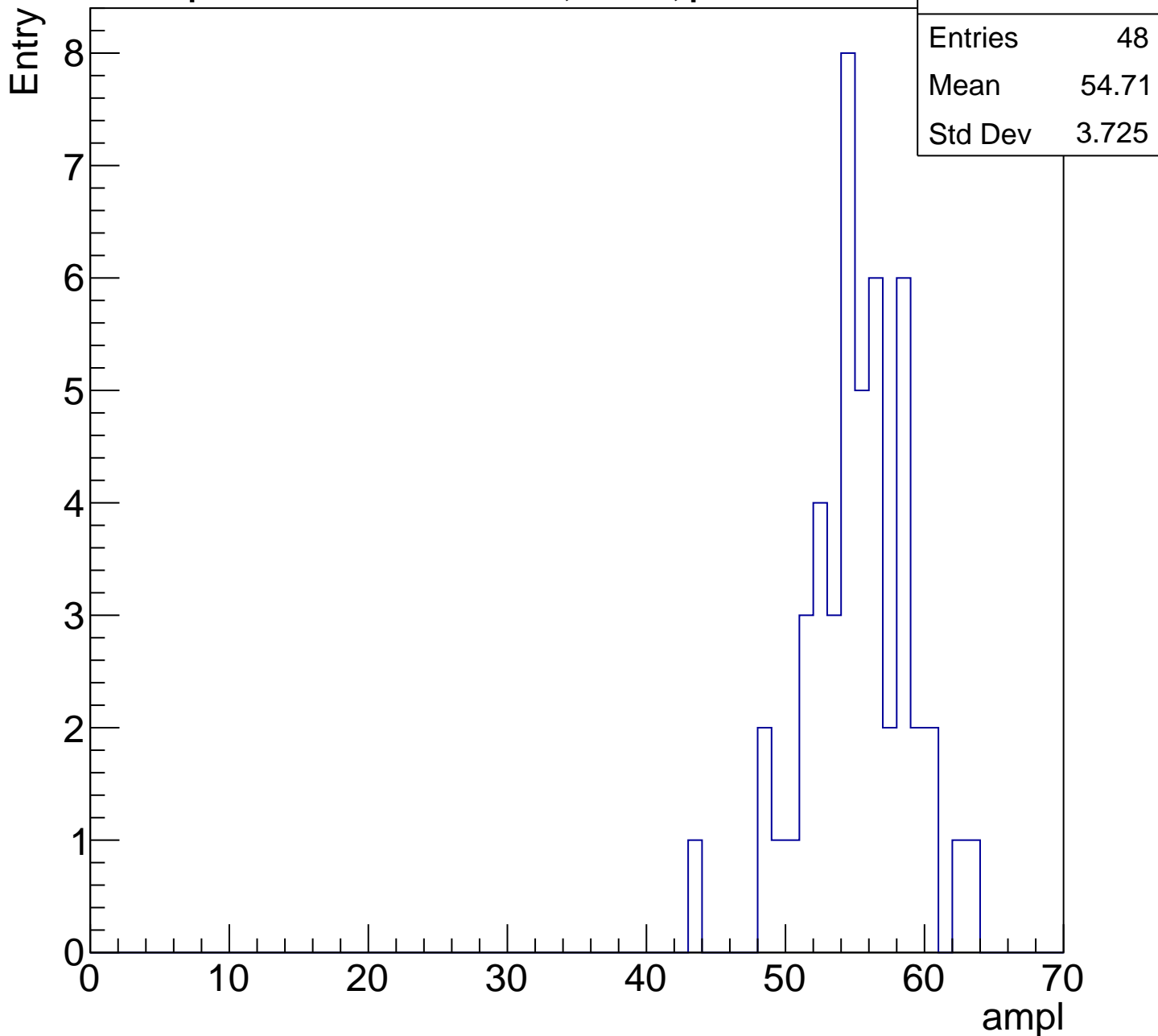
Entry

Entries	74
Mean	49.04
Std Dev	3.63



B1L102S, U14-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

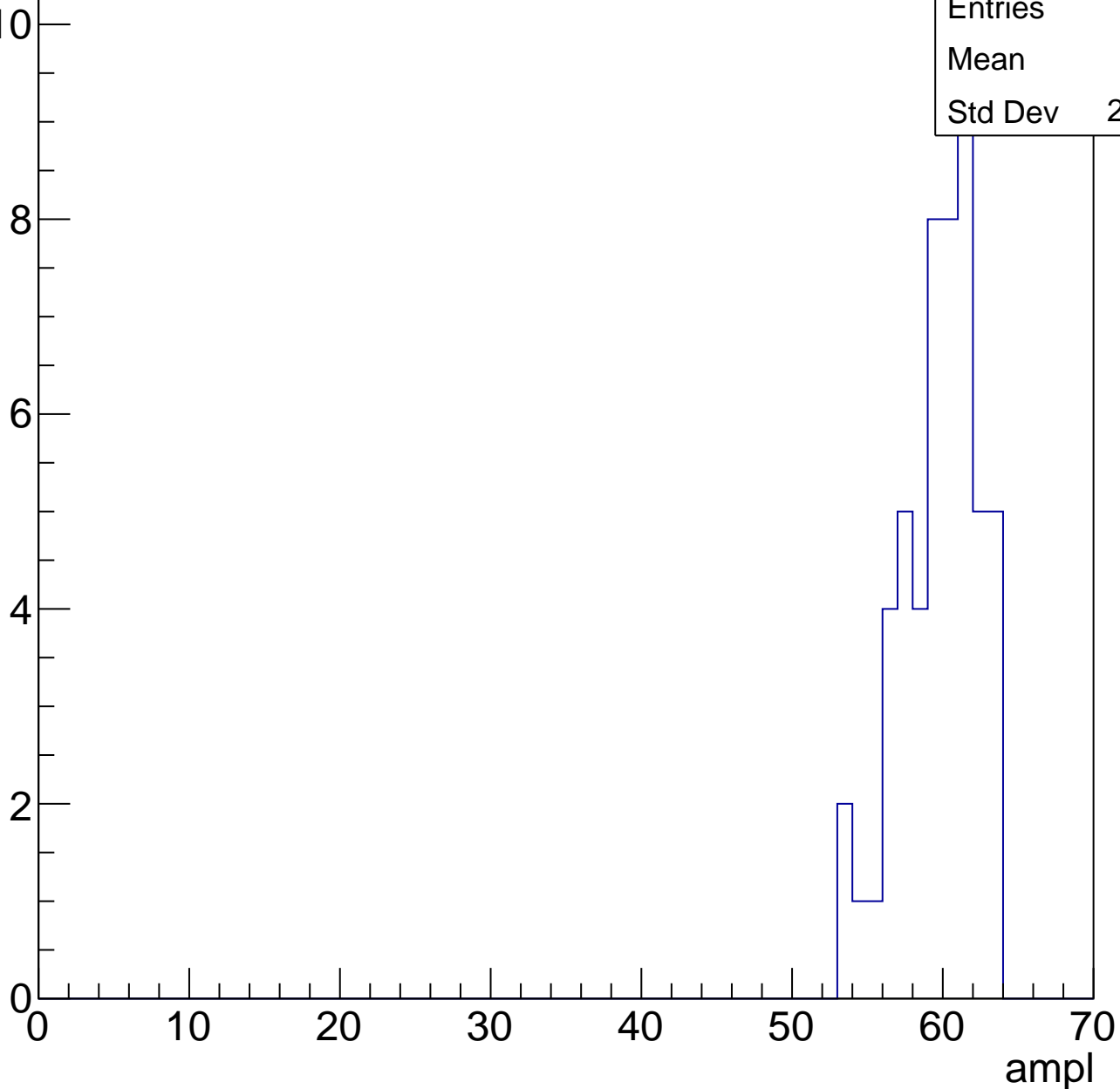


B1L102S, U14-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

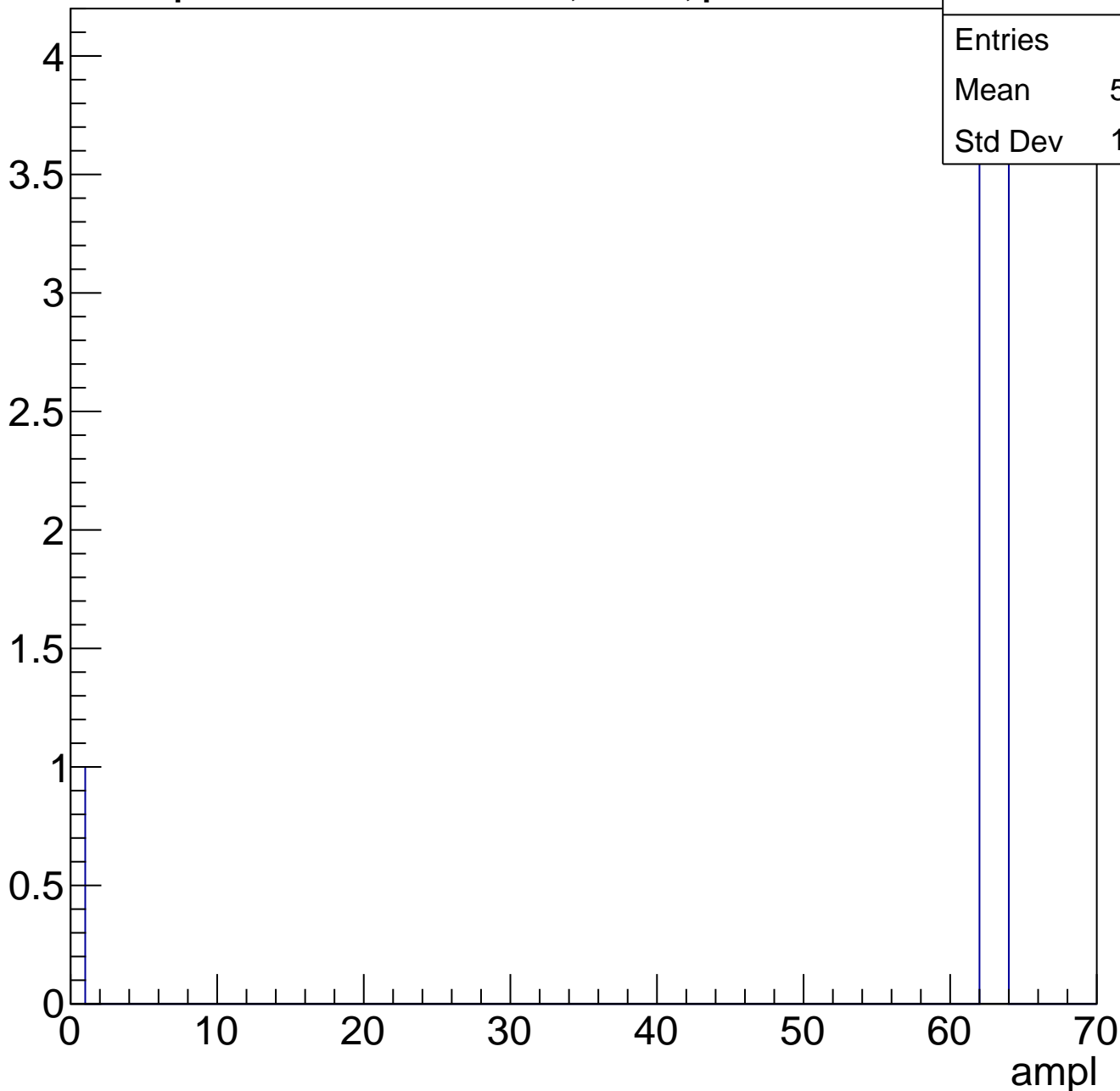
Entries	53
Mean	59.3
Std Dev	2.537



B1L102S, U14-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

B1L102S, U14-ch12, adc0

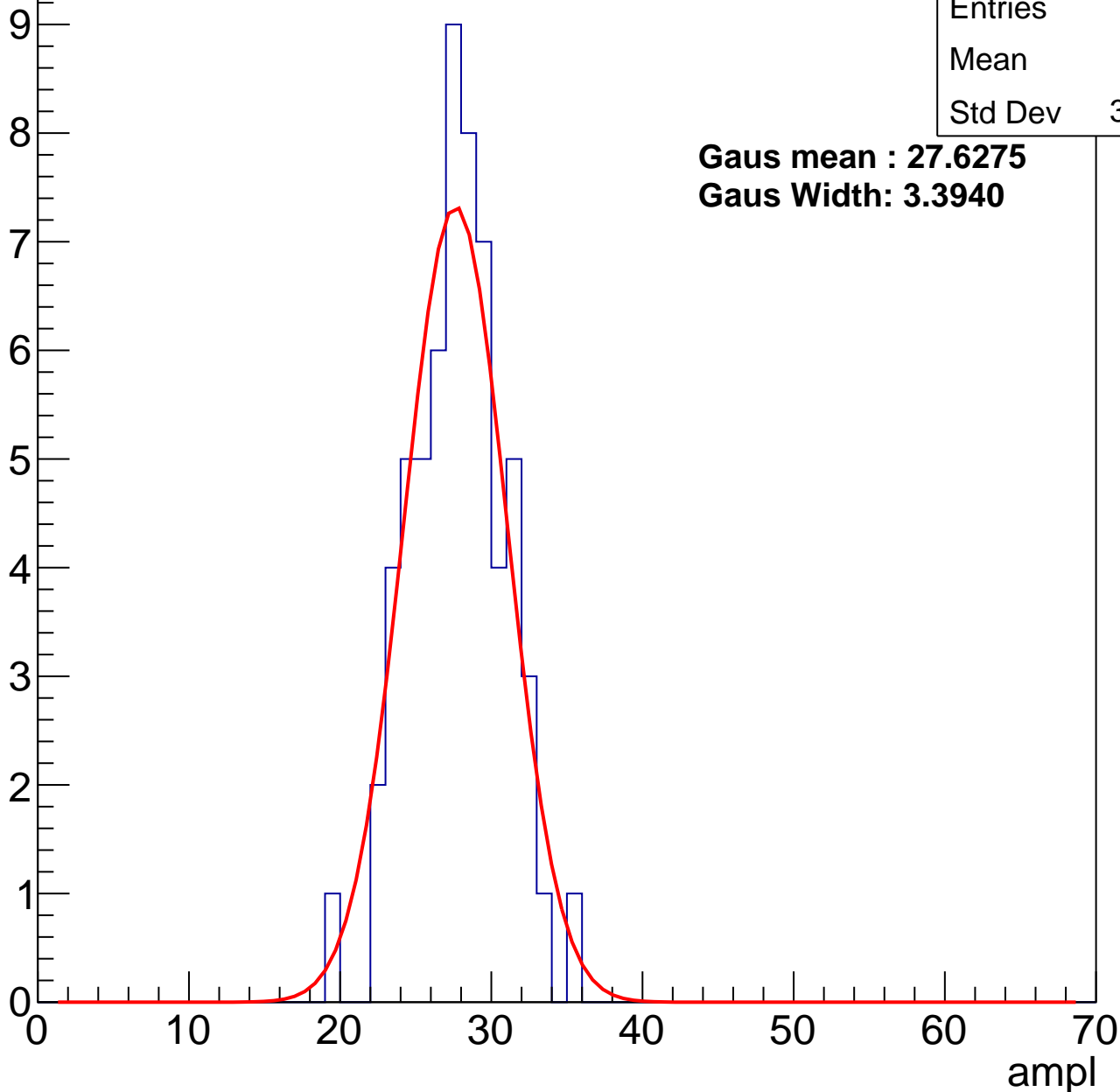
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.3
Std Dev	3.059

Gaus mean : 27.6275

Gaus Width: 3.3940



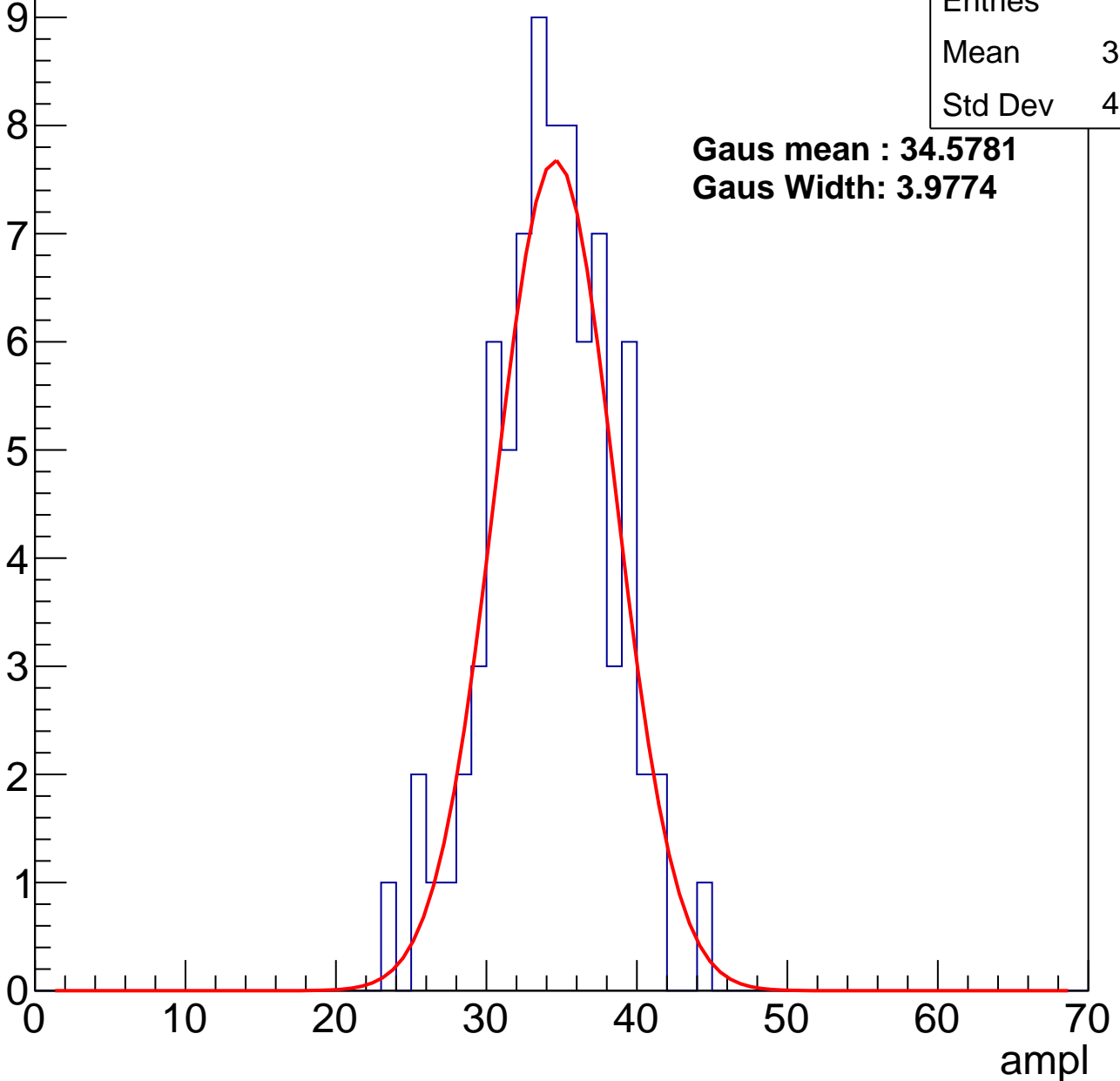
B1L102S, U14-ch12, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	33.83
Std Dev	4.002

Gaus mean : 34.5781
Gaus Width: 3.9774



B1L102S, U14-ch12, adc2

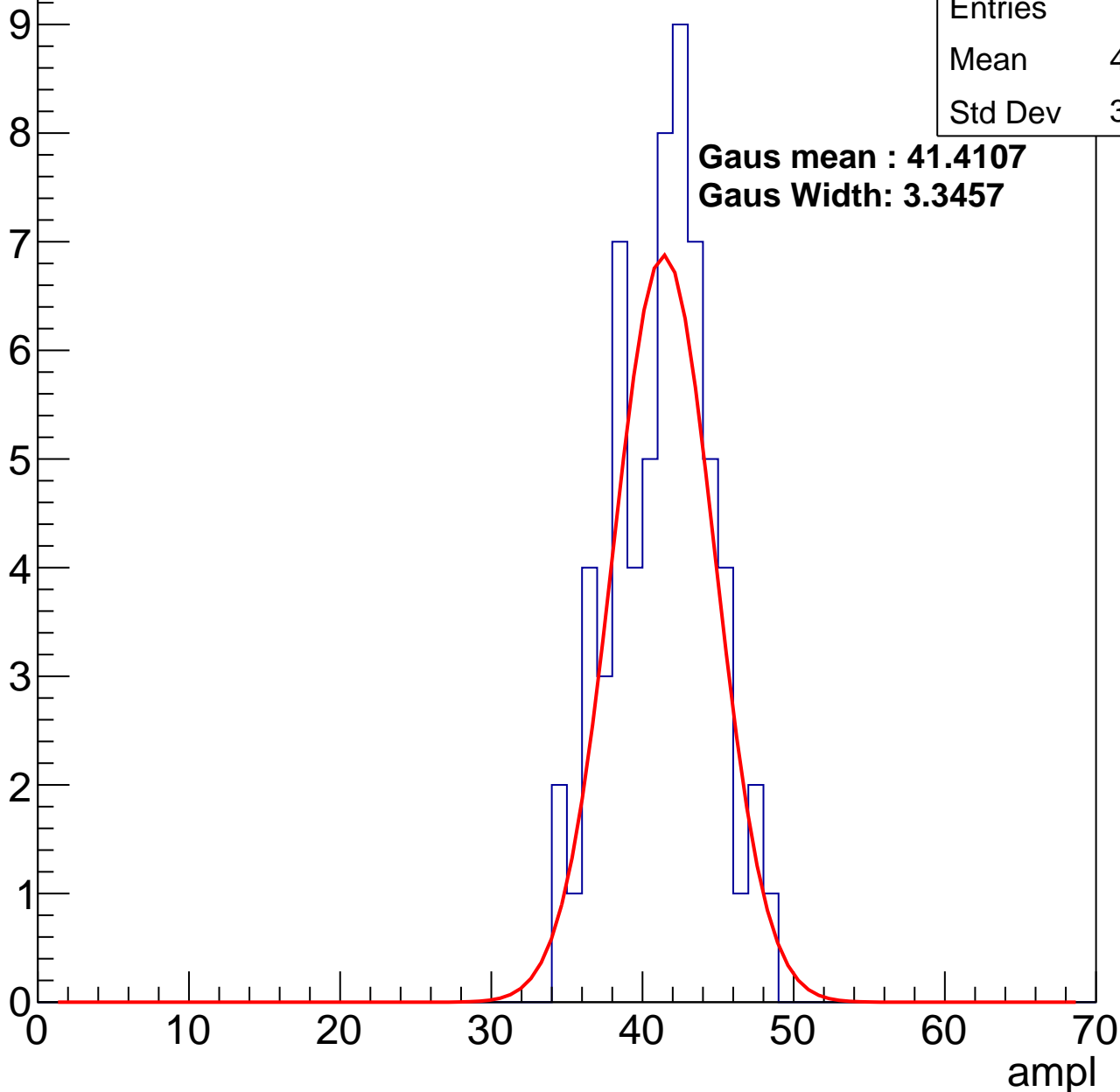
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	40.87
Std Dev	3.229

Gaus mean : 41.4107

Gaus Width: 3.3457

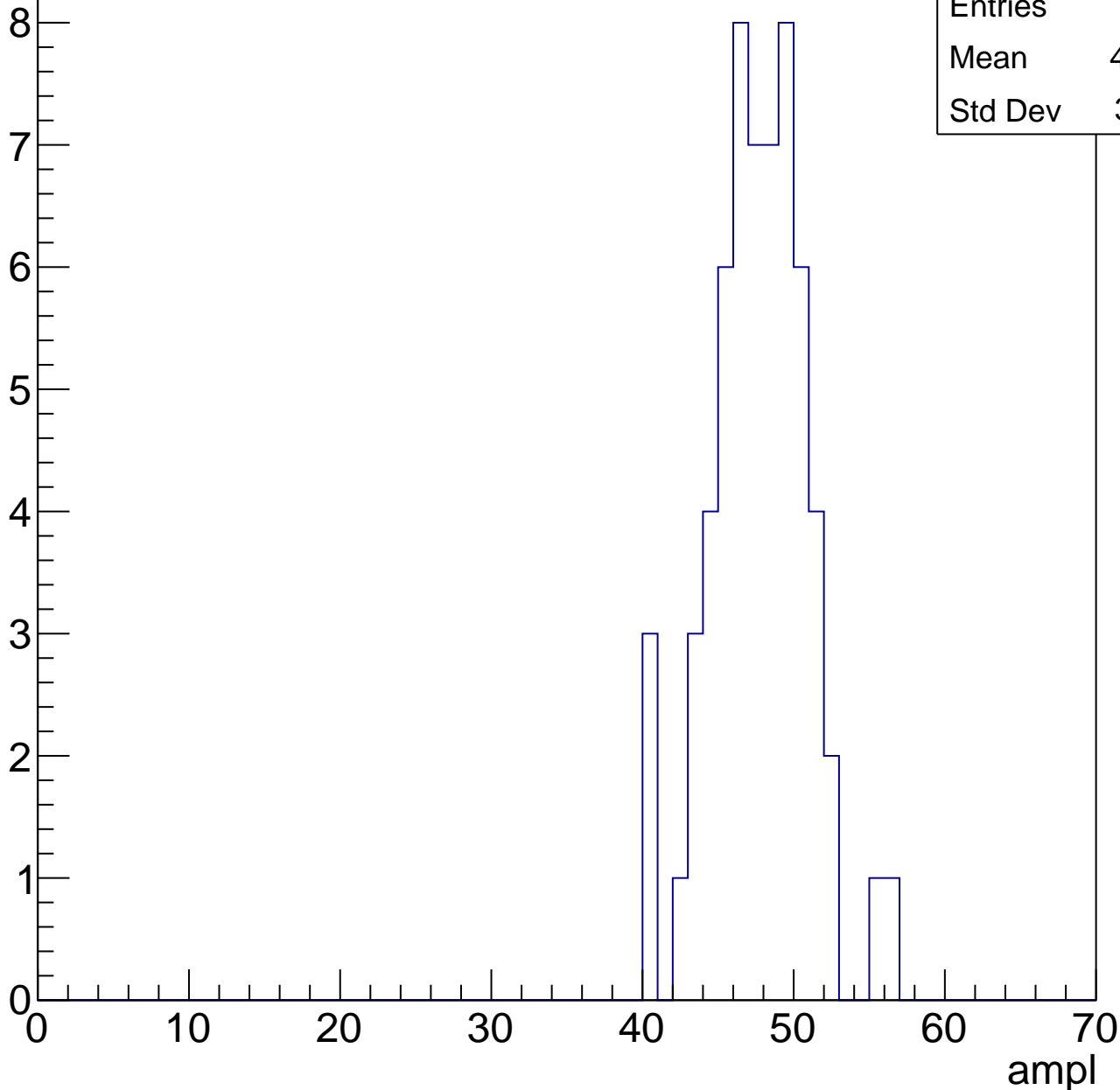


B1L102S, U14-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.23
Std Dev	3.231

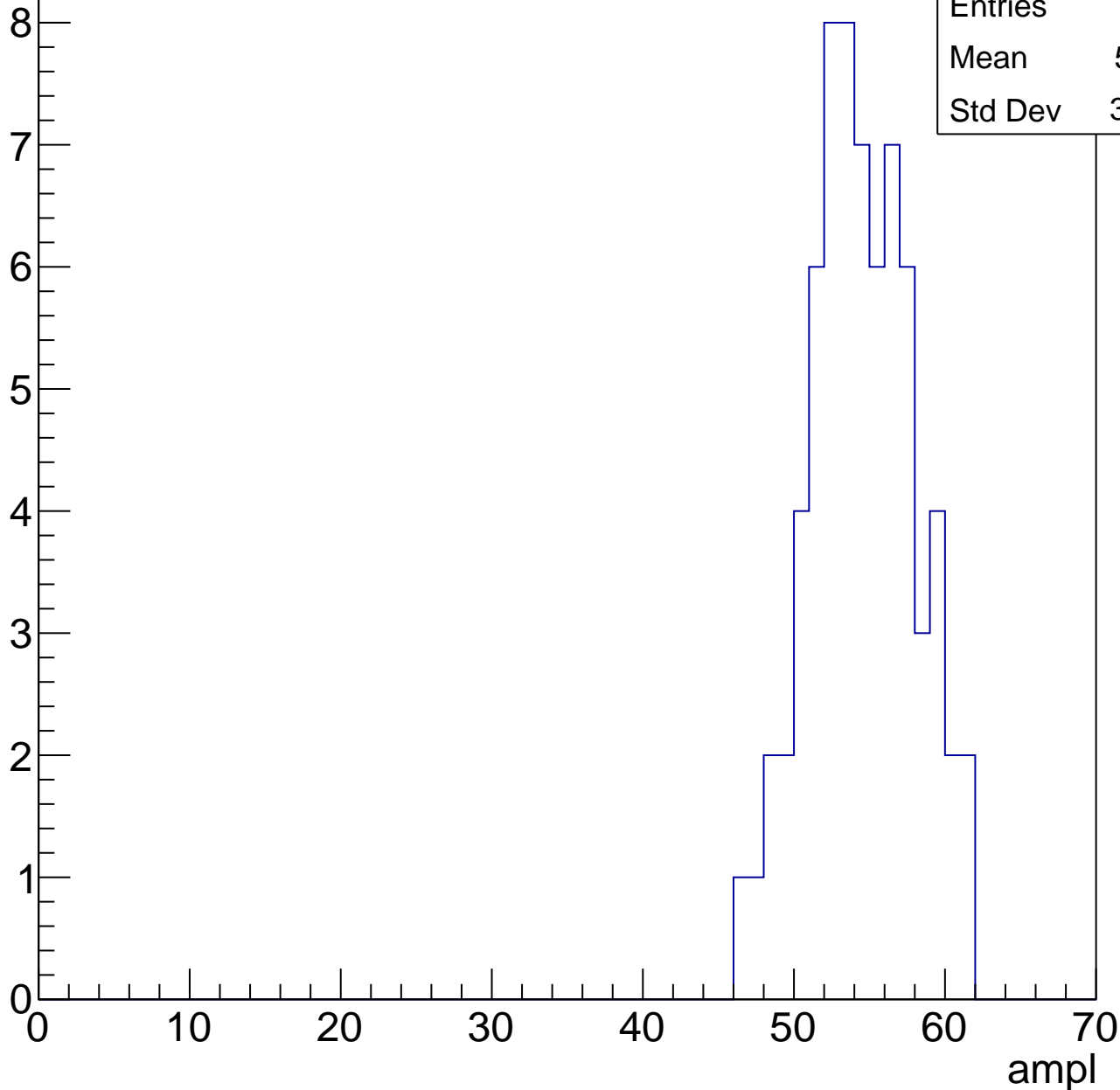


B1L102S, U14-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	54.01
Std Dev	3.403

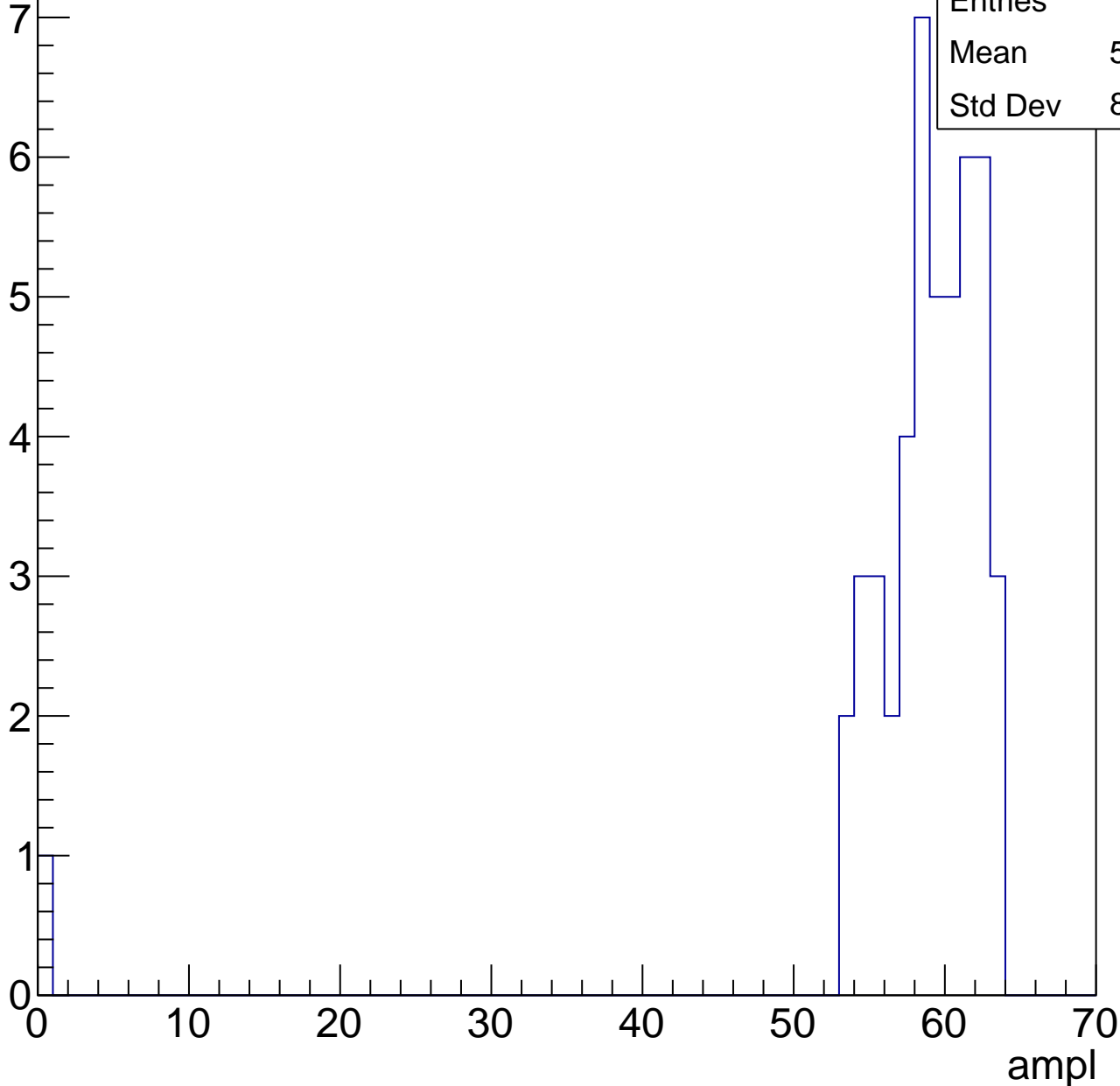


B1L102S, U14-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.47
Std Dev	8.918

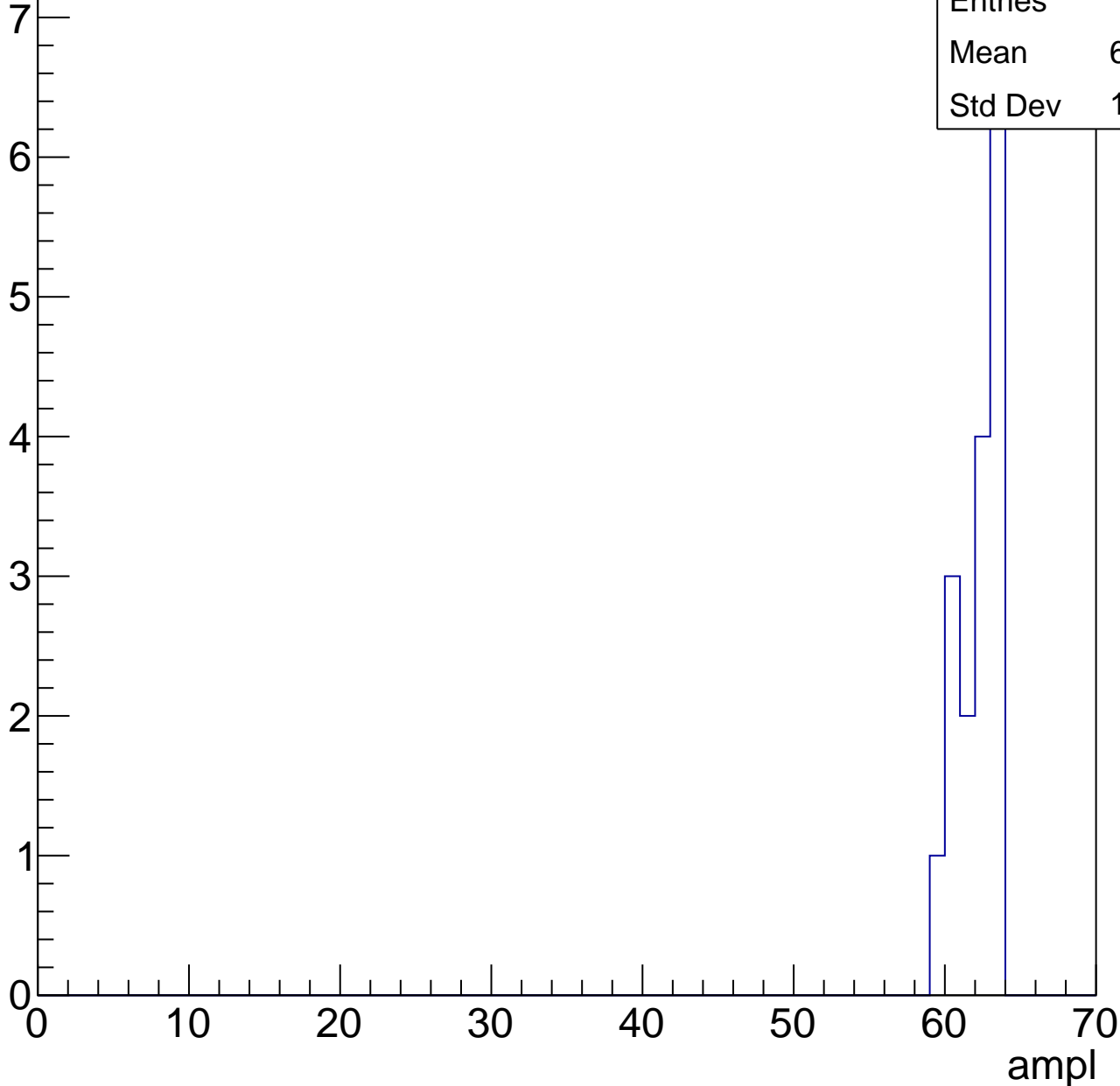


B1L102S, U14-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.76
Std Dev	1.307



B1L102S, U14-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch13, adc0

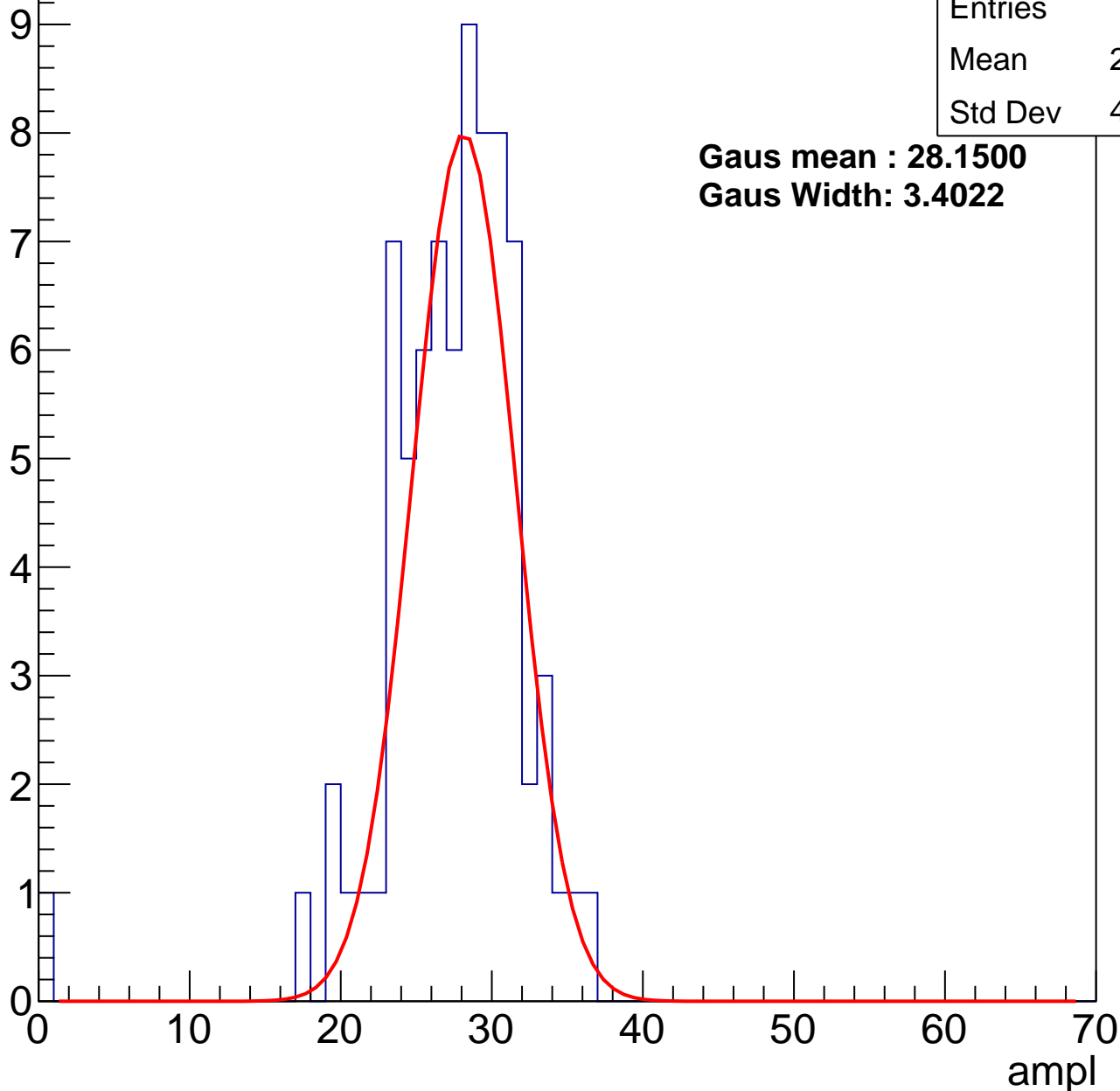
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	26.95
Std Dev	4.846

Gaus mean : 28.1500

Gaus Width: 3.4022



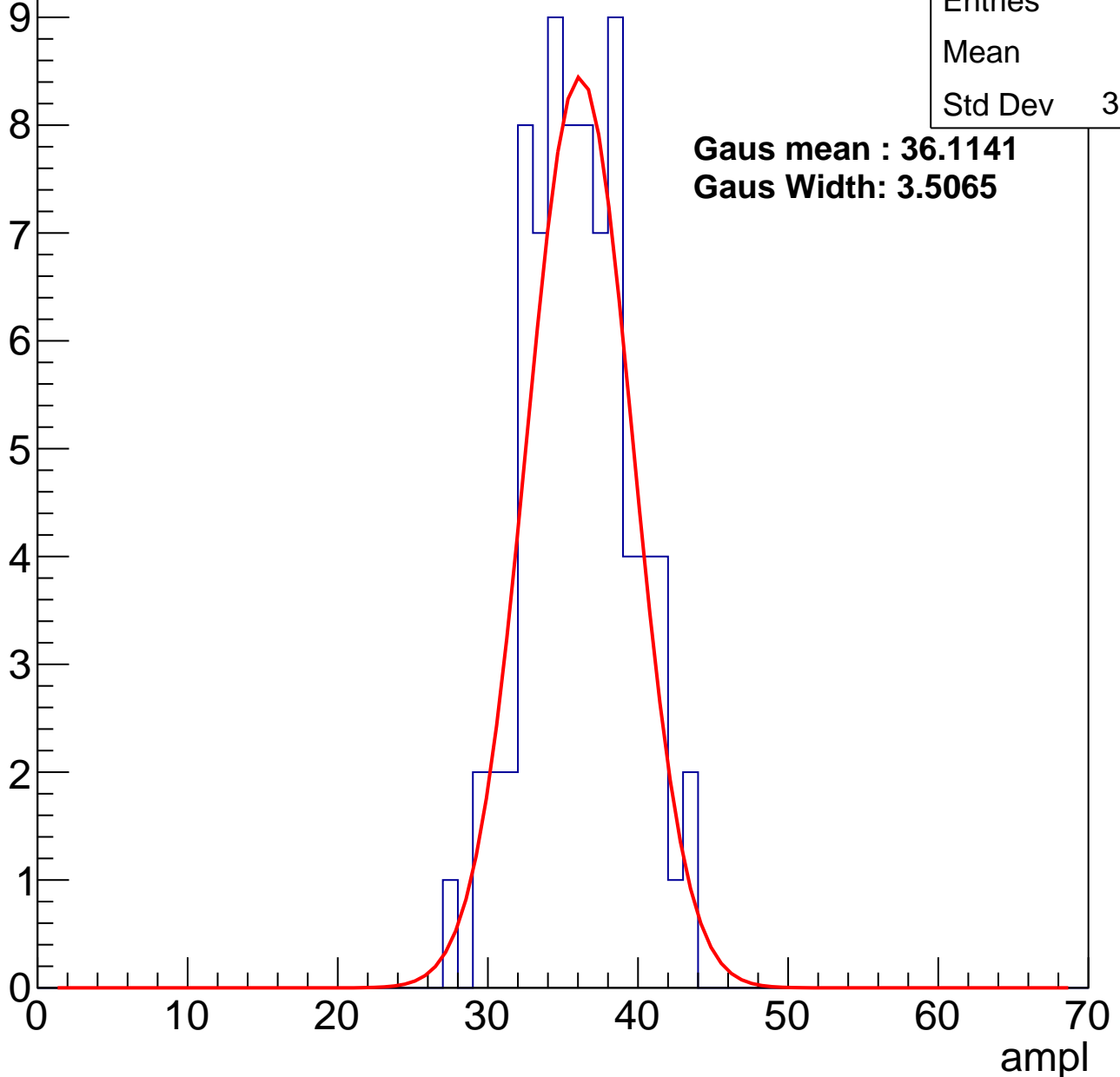
B1L102S, U14-ch13, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.6
Std Dev	3.398

Gaus mean : 36.1141
Gaus Width: 3.5065



B1L102S, U14-ch13, adc2

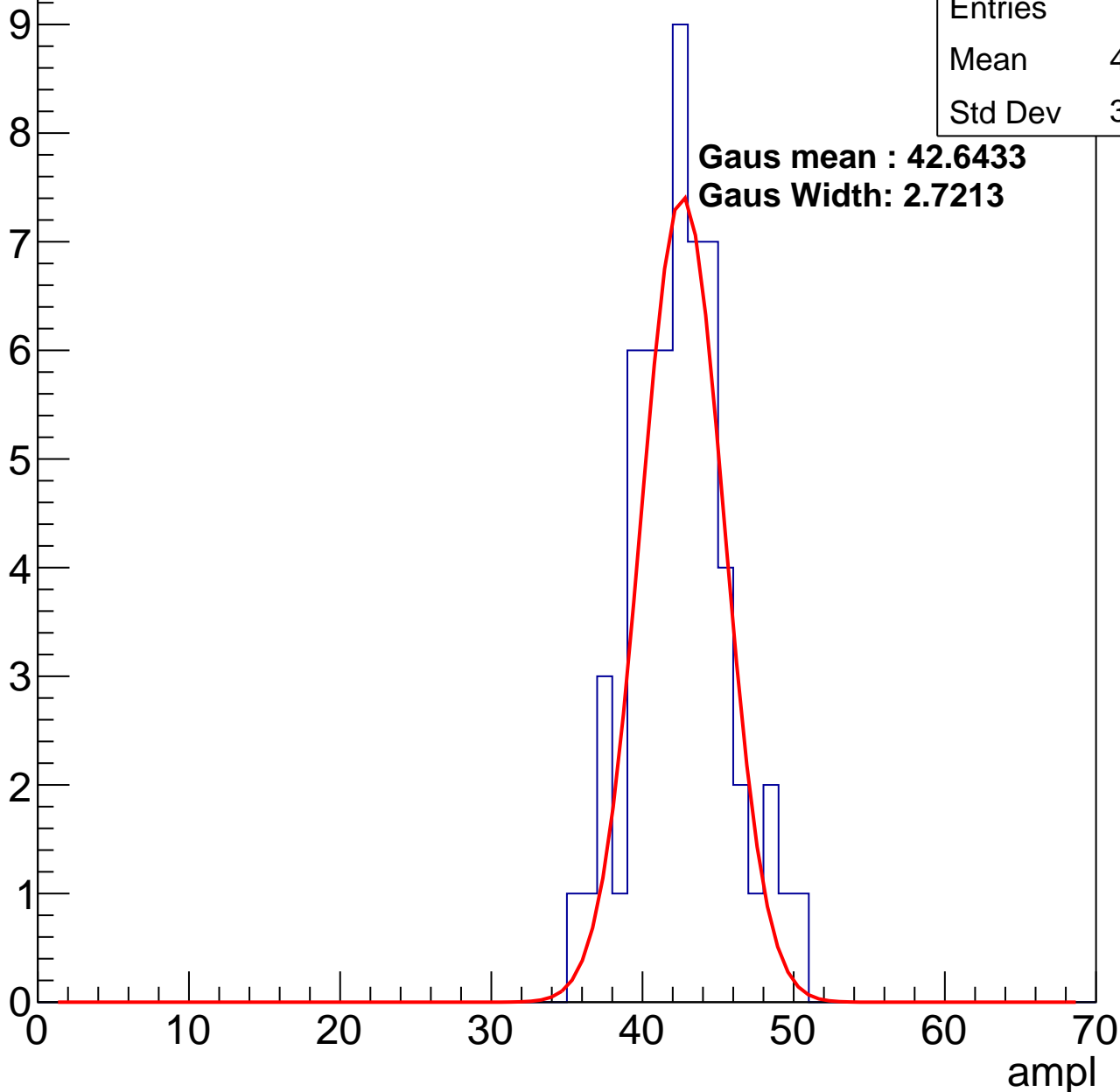
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.09
Std Dev	3.142

Gaus mean : 42.6433

Gaus Width: 2.7213

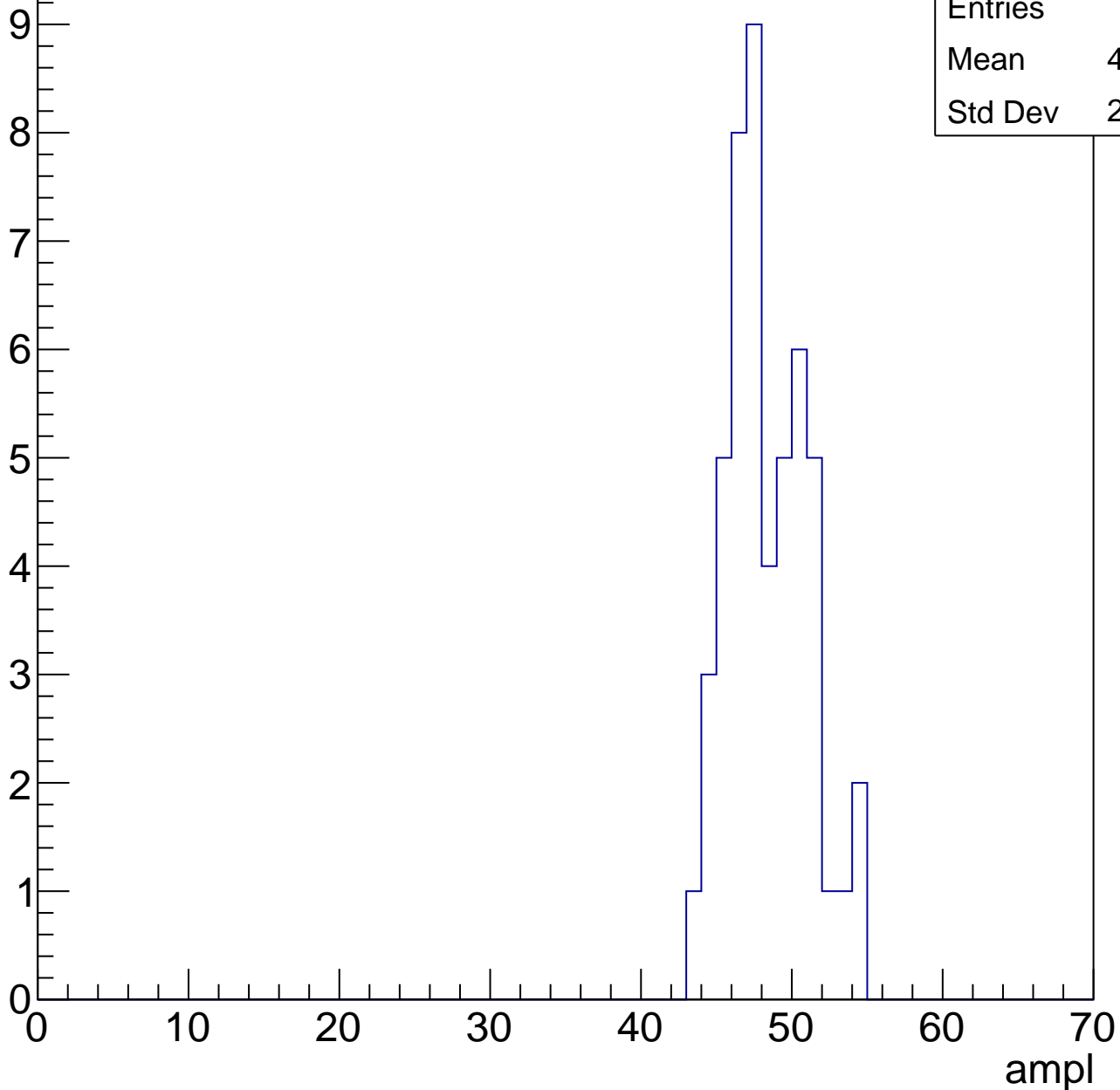


B1L102S, U14-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	47.92
Std Dev	2.629

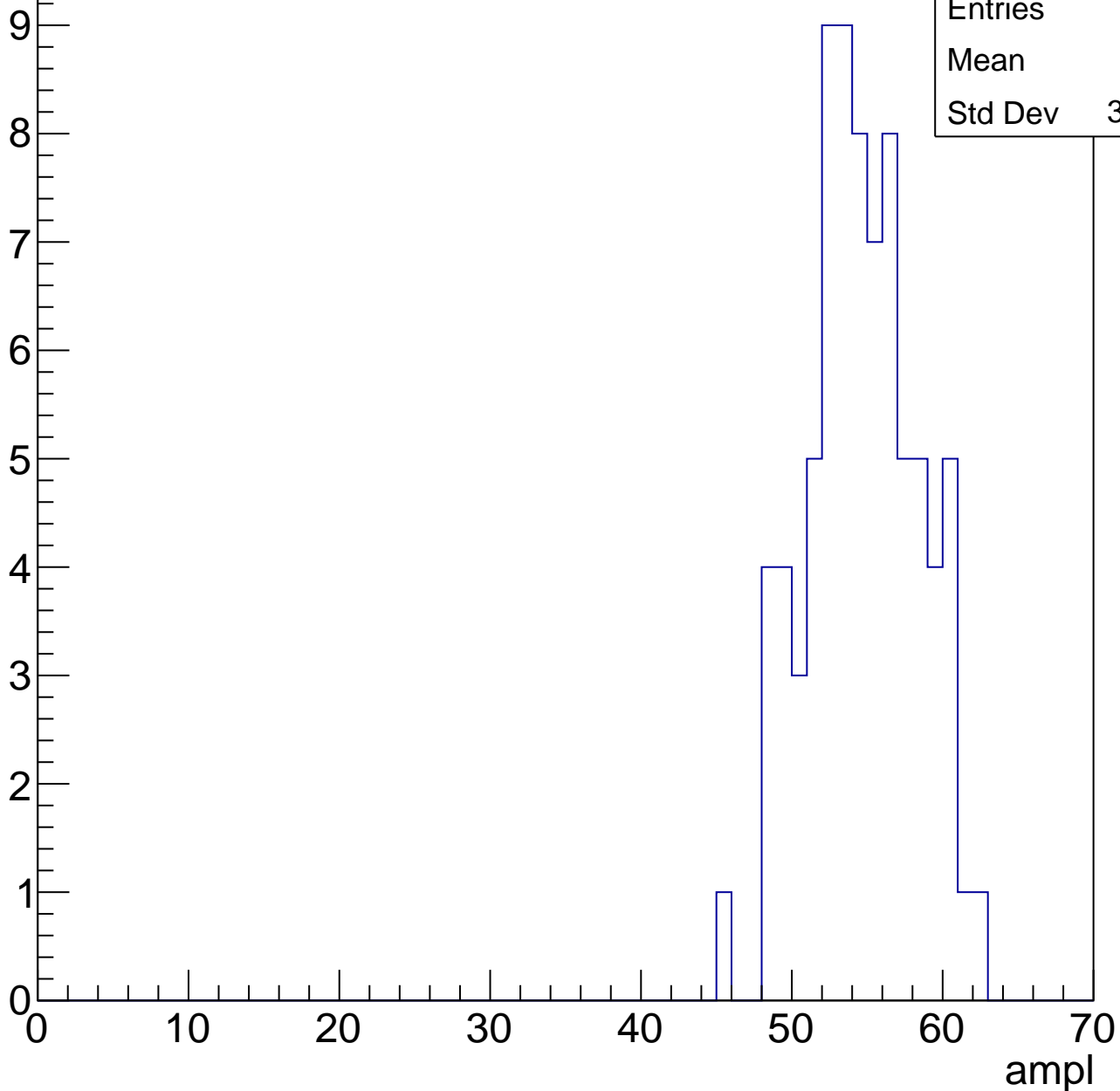


B1L102S, U14-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	54.2
Std Dev	3.588

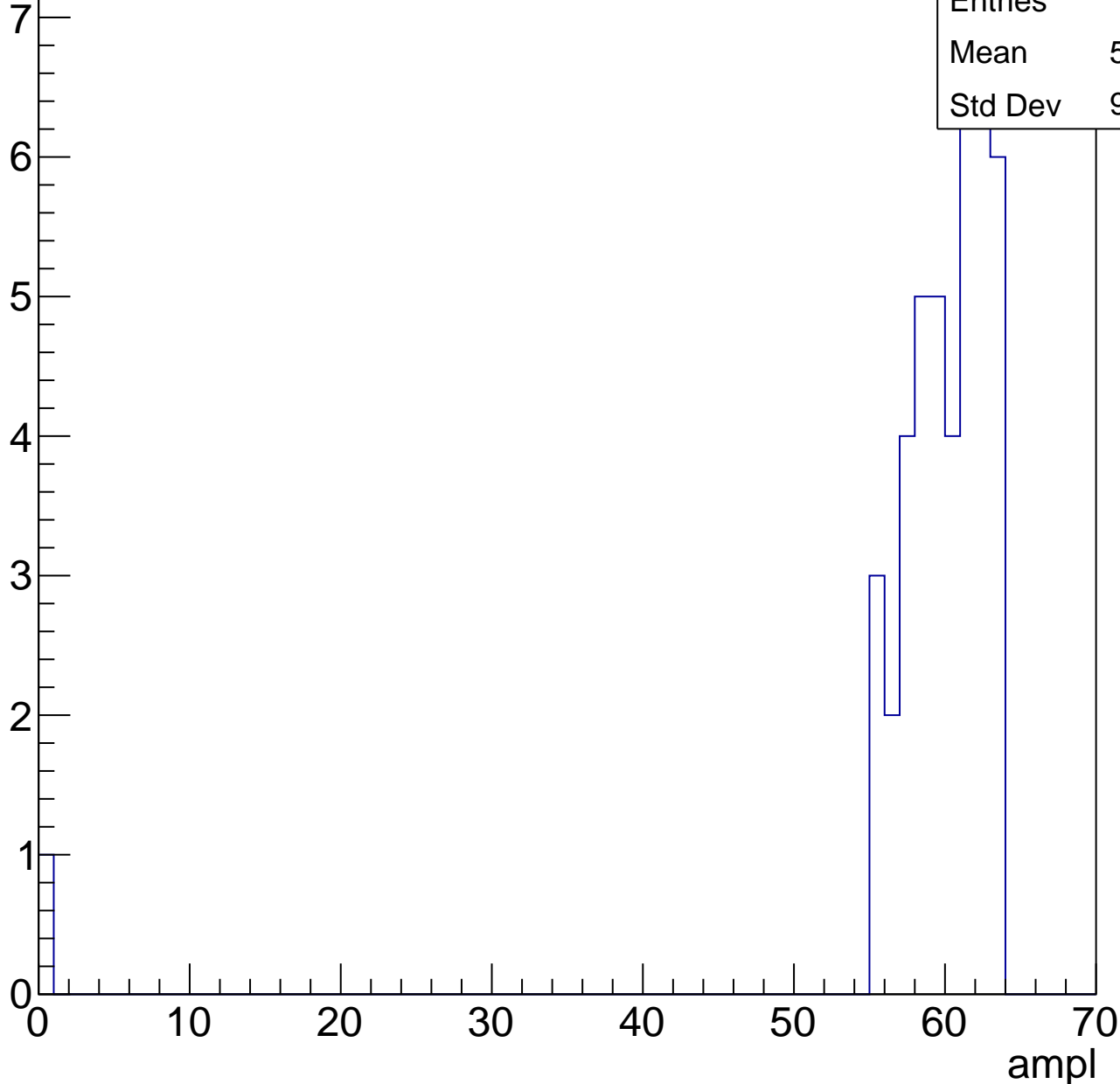


B1L102S, U14-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

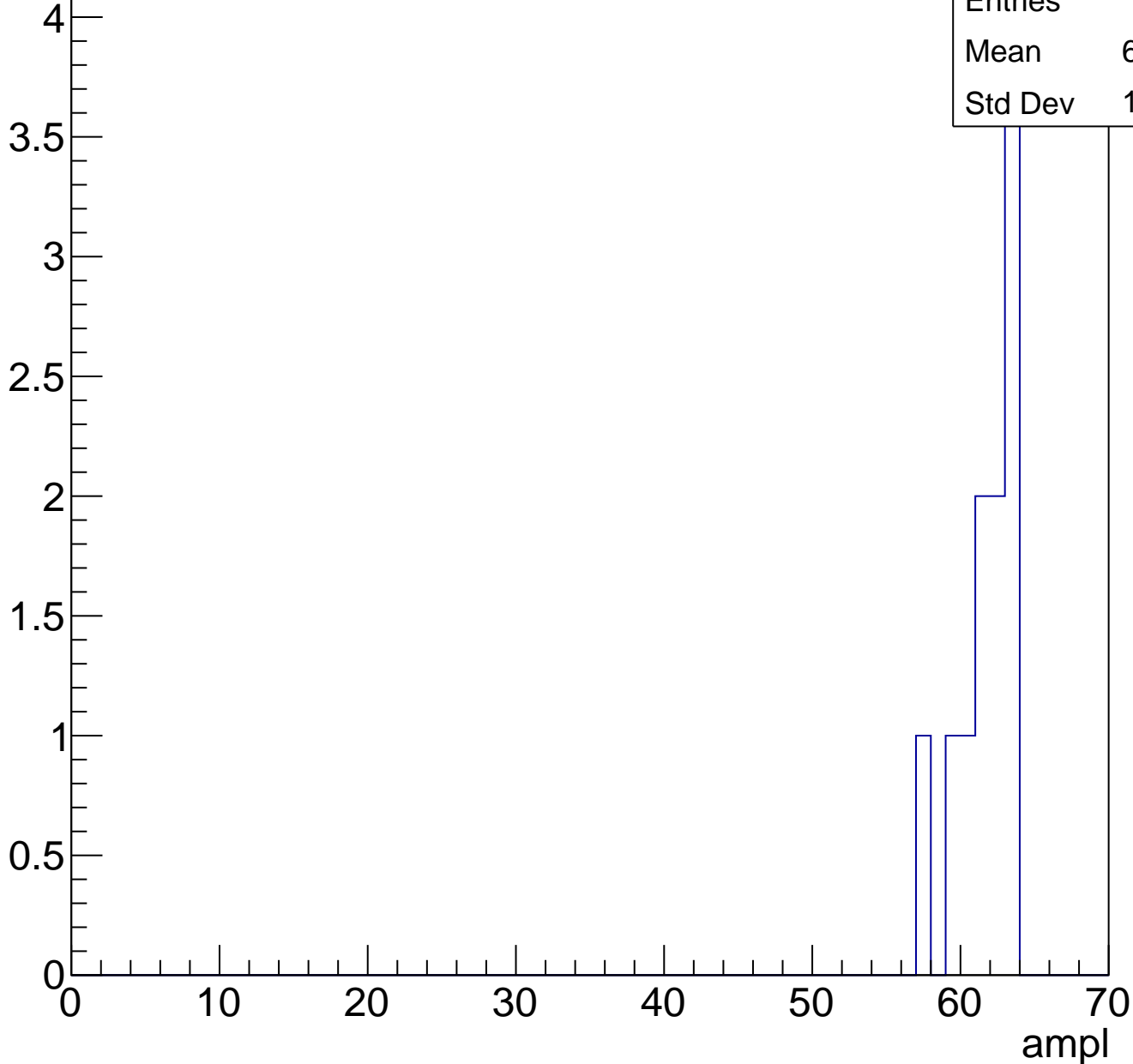
Entries	44
Mean	58.39
Std Dev	9.223



B1L102S, U14-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch14, adc0

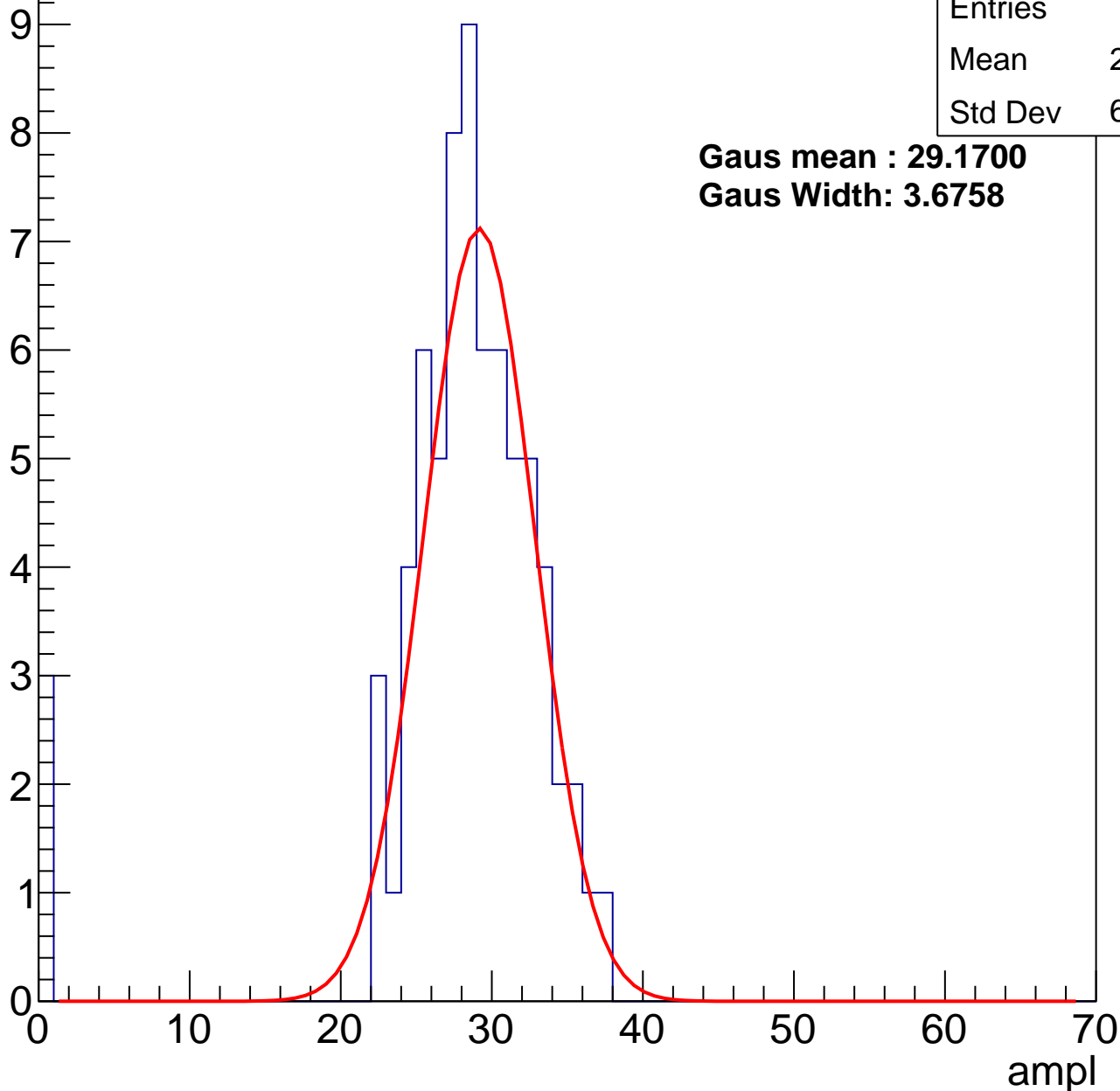
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.39
Std Dev	6.687

Gaus mean : 29.1700

Gaus Width: 3.6758



B1L102S, U14-ch14, adc1

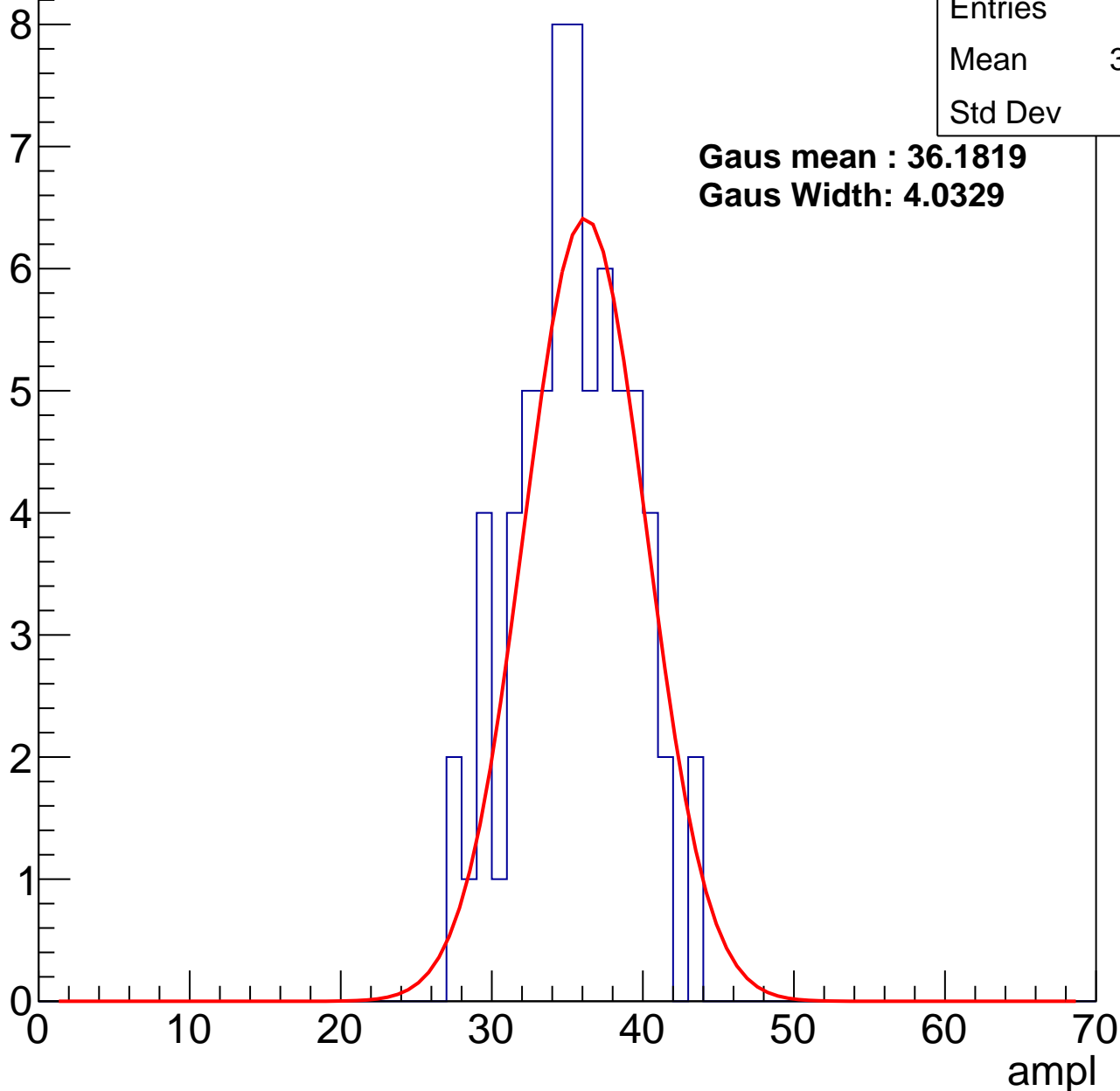
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.99
Std Dev	3.74

Gaus mean : 36.1819

Gaus Width: 4.0329



B1L102S, U14-ch14, adc2

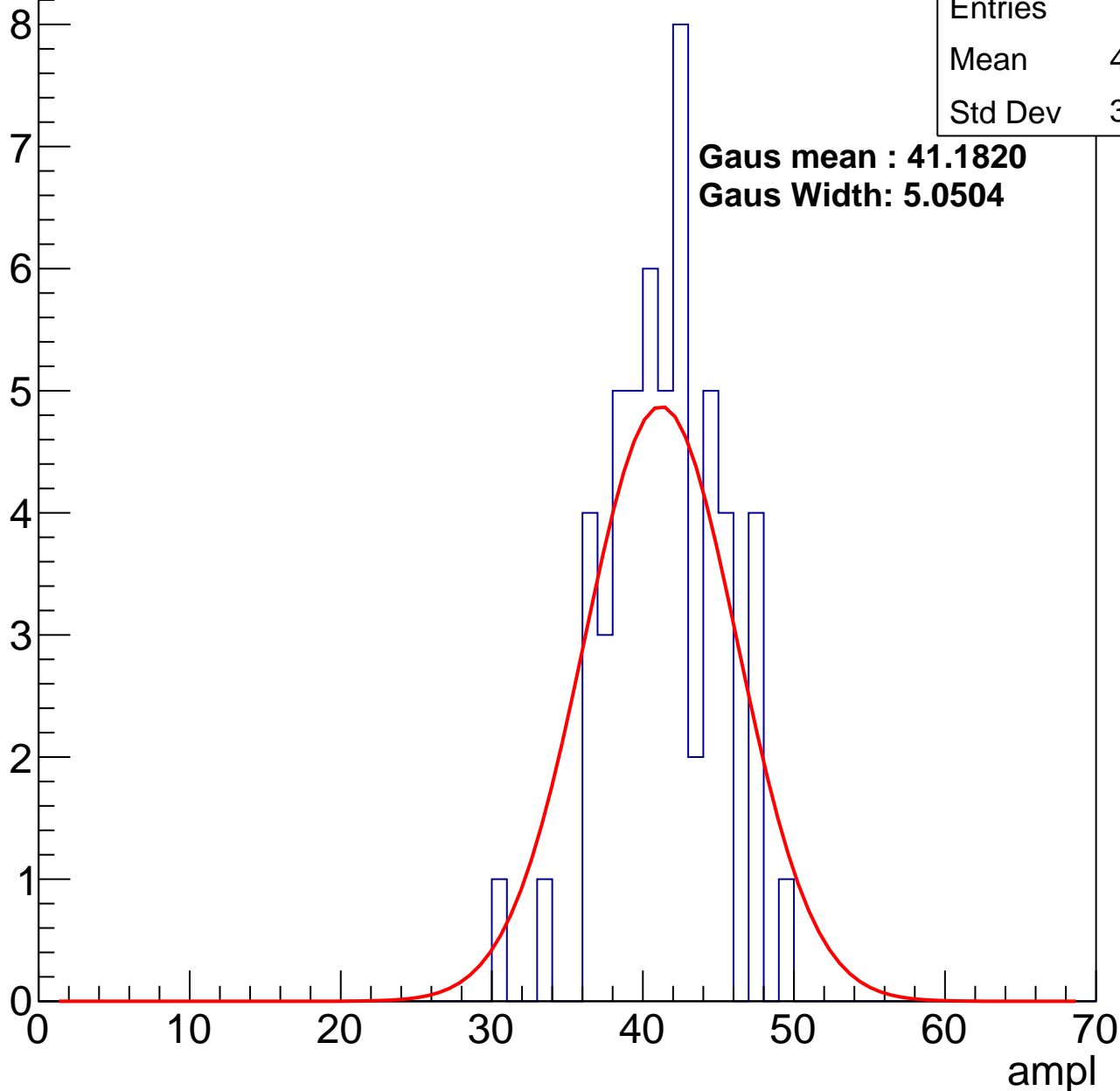
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	40.87
Std Dev	3.692

Gaus mean : 41.1820

Gaus Width: 5.0504



B1L102S, U14-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	46.86
Std Dev	3.162

Entry

10

8

6

4

2

0

0

10

20

30

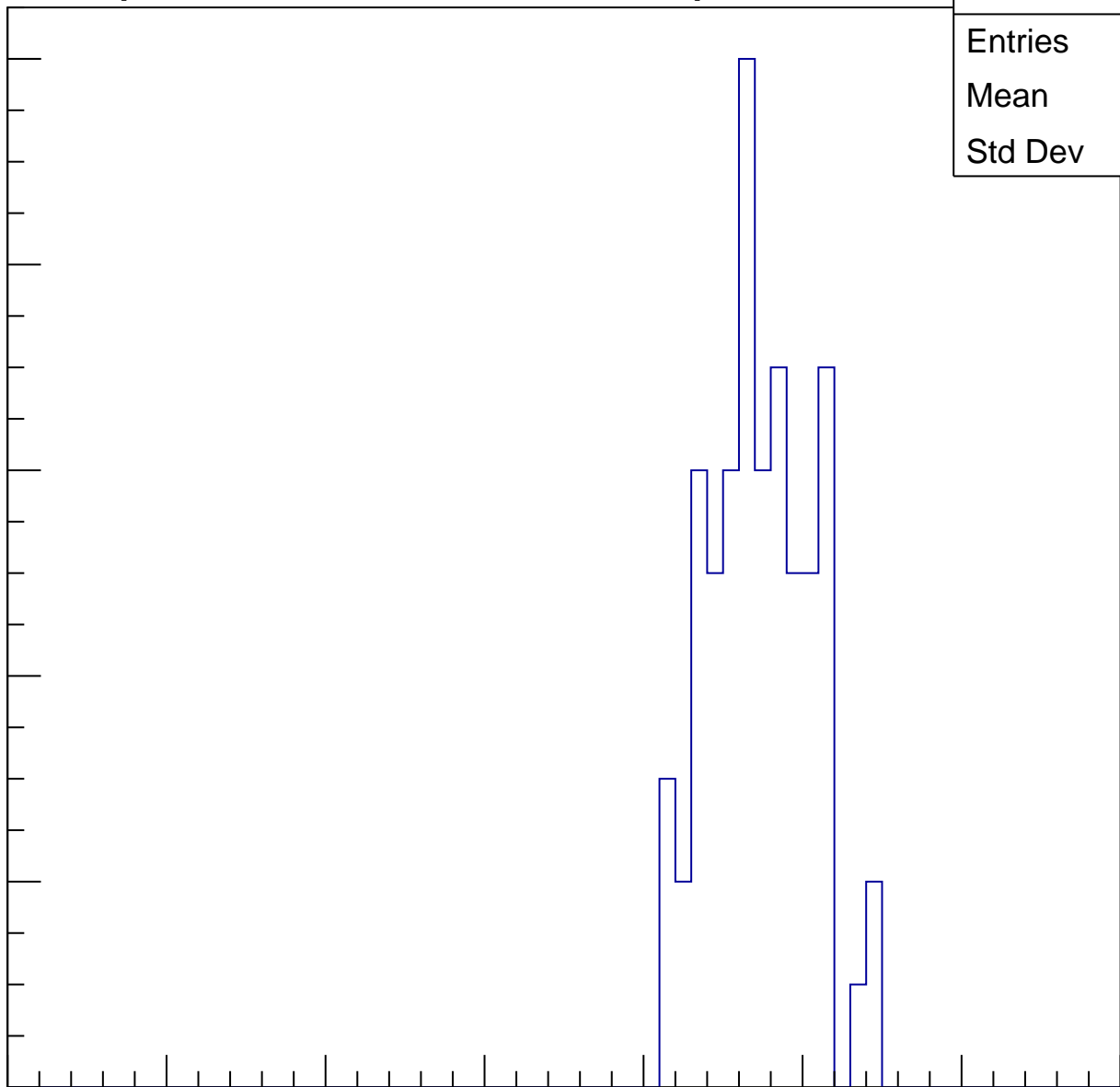
40

50

60

70

ampl



B1L102S, U14-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	53.52
Std Dev	3.128

Entry

10

8

6

4

2

0

0

10

20

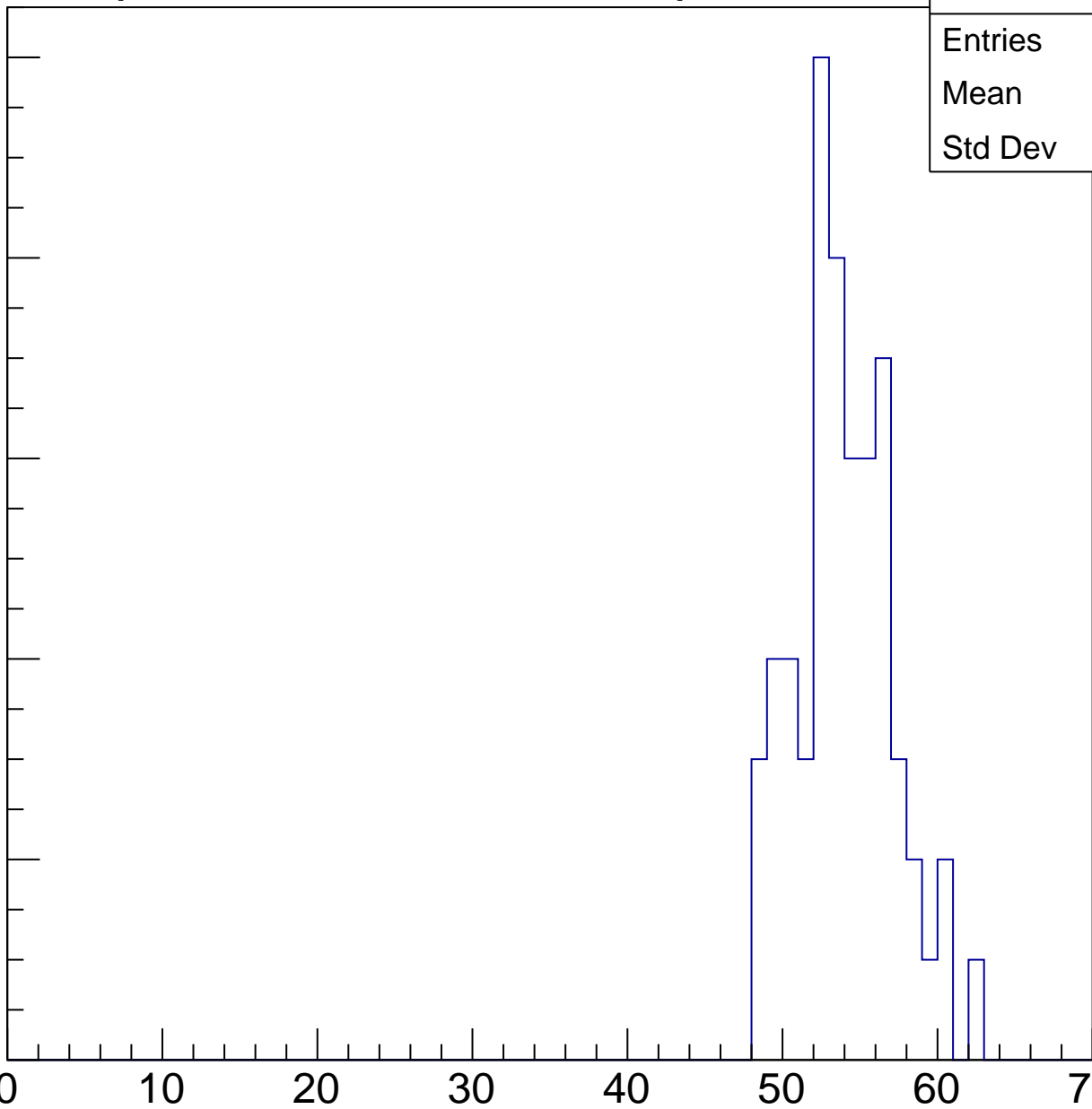
30

40

50

60

ampl

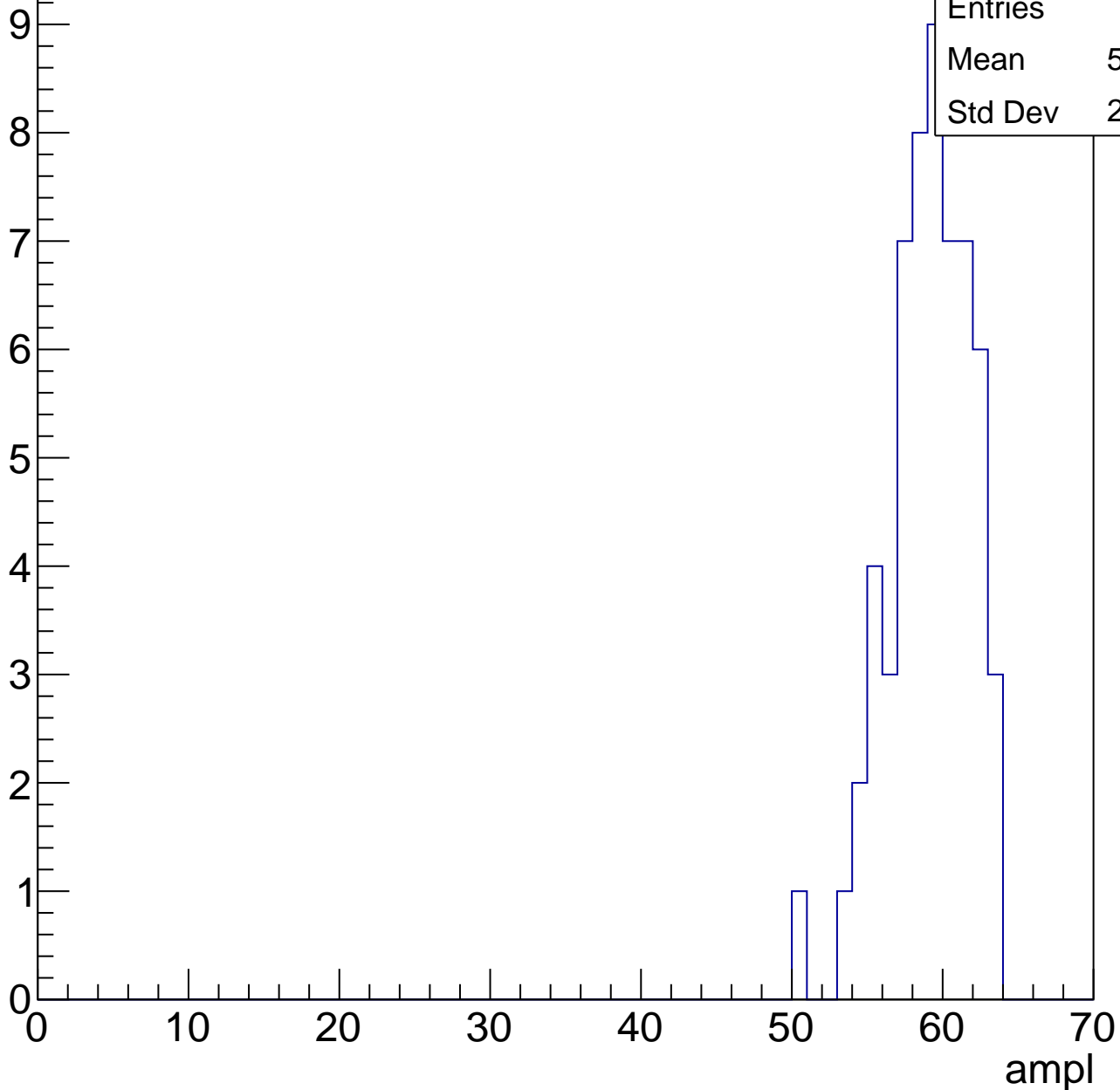


B1L102S, U14-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.64
Std Dev	2.702

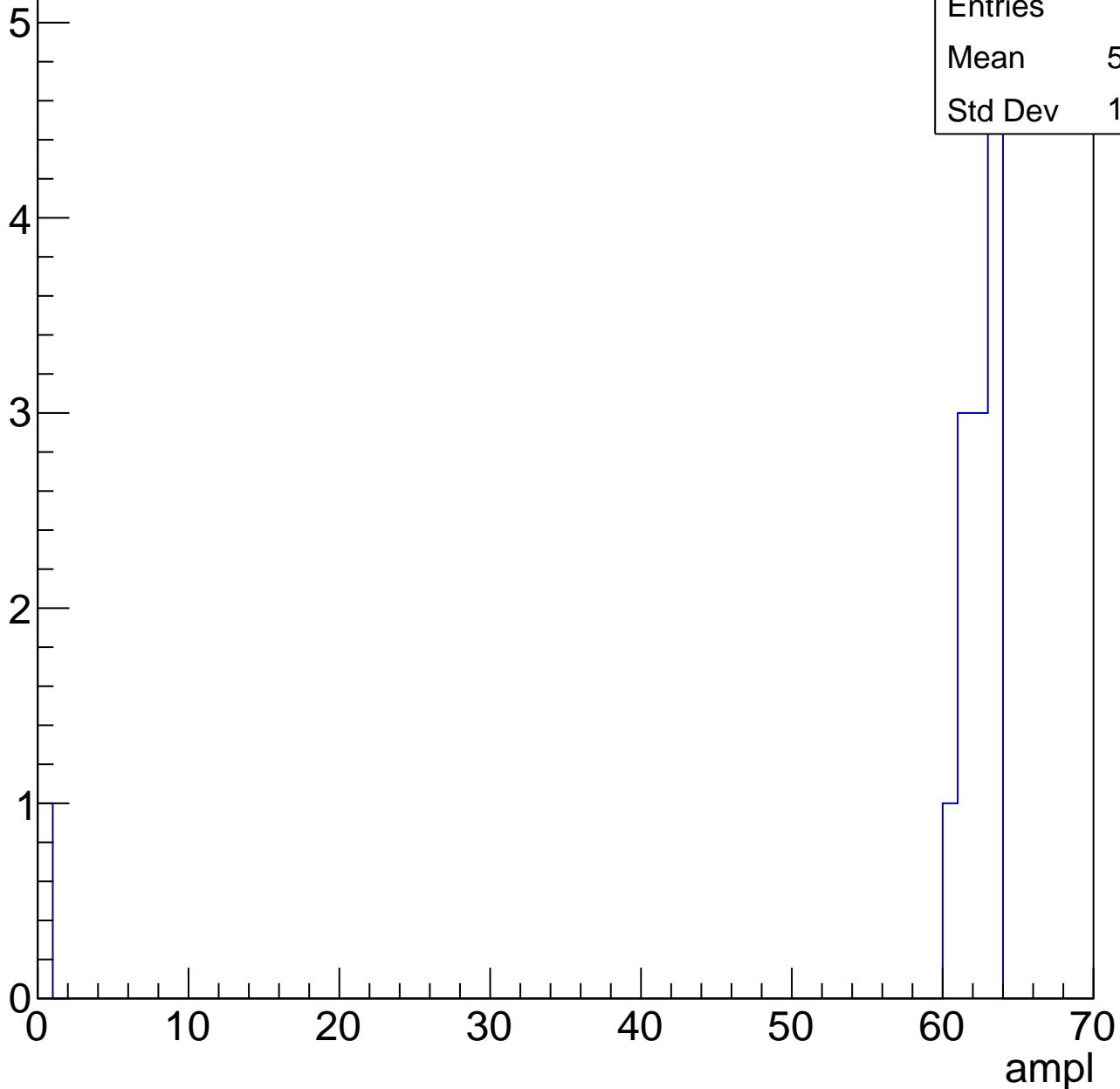


B1L102S, U14-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	57.23
Std Dev	16.55



B1L102S, U14-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch15, adc0

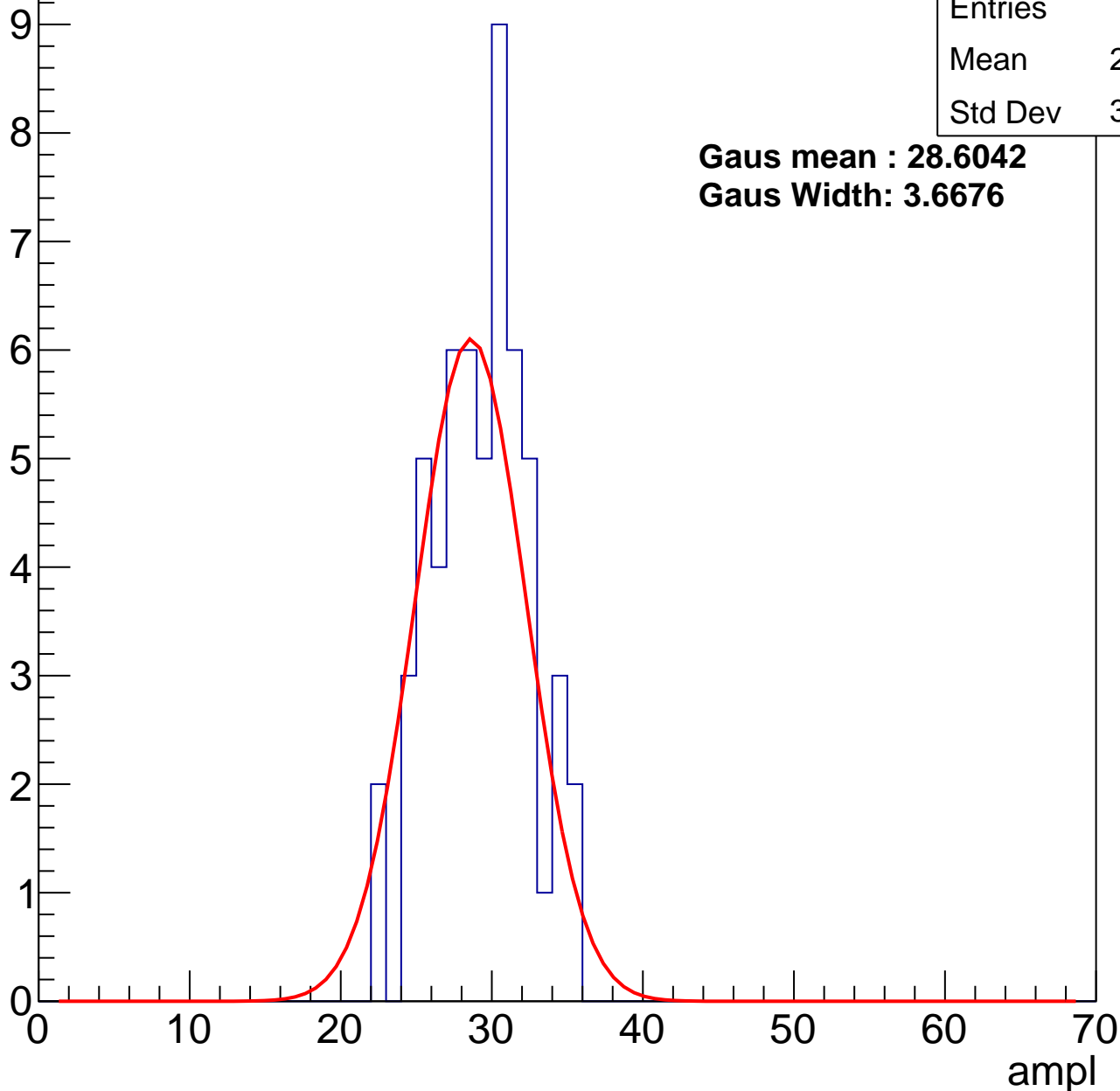
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	28.79
Std Dev	3.127

Gaus mean : 28.6042

Gaus Width: 3.6676



B1L102S, U14-ch15, adc1

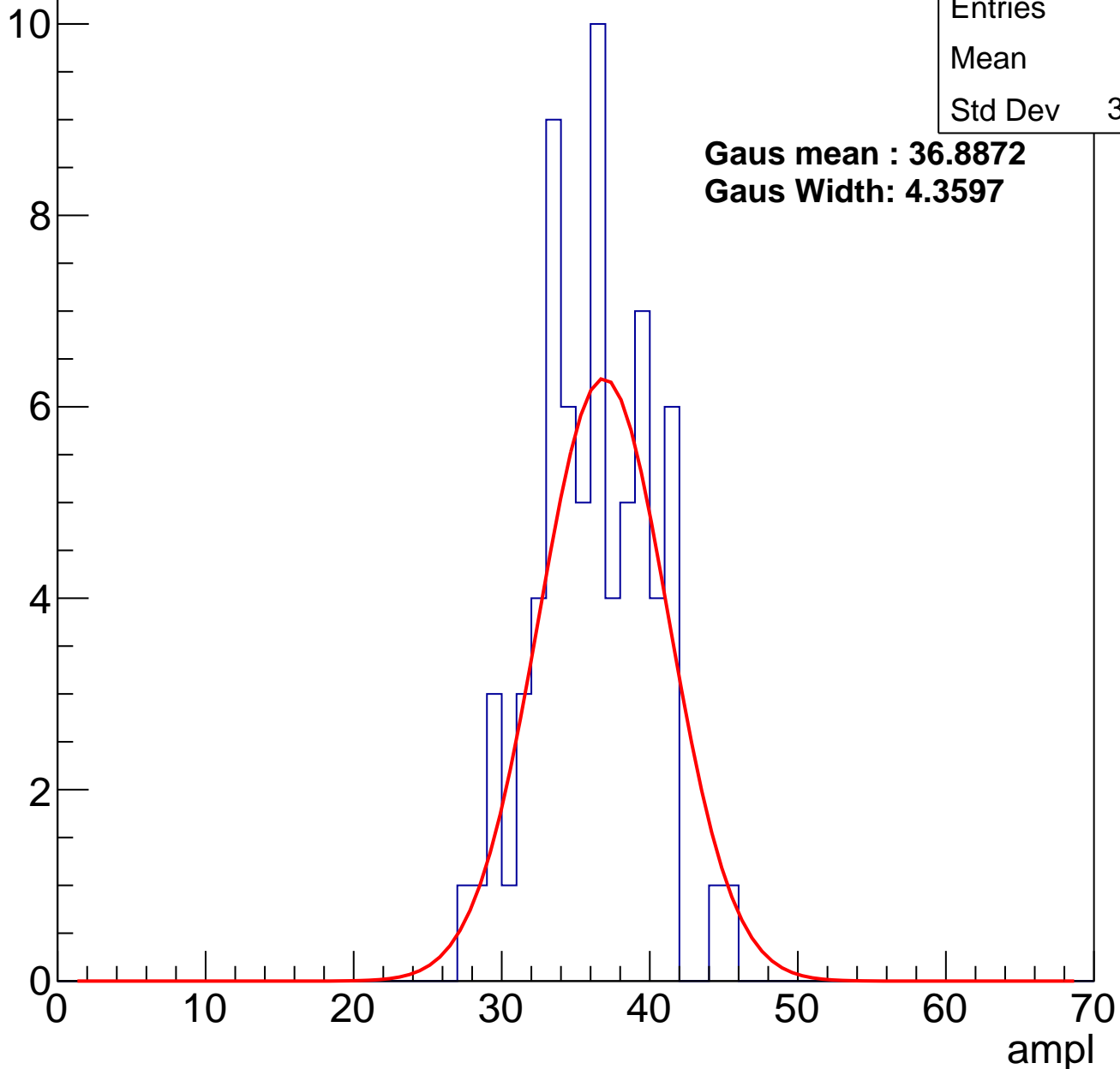
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	35.7
Std Dev	3.784

Gaus mean : 36.8872

Gaus Width: 4.3597

Entry



B1L102S, U14-ch15, adc2

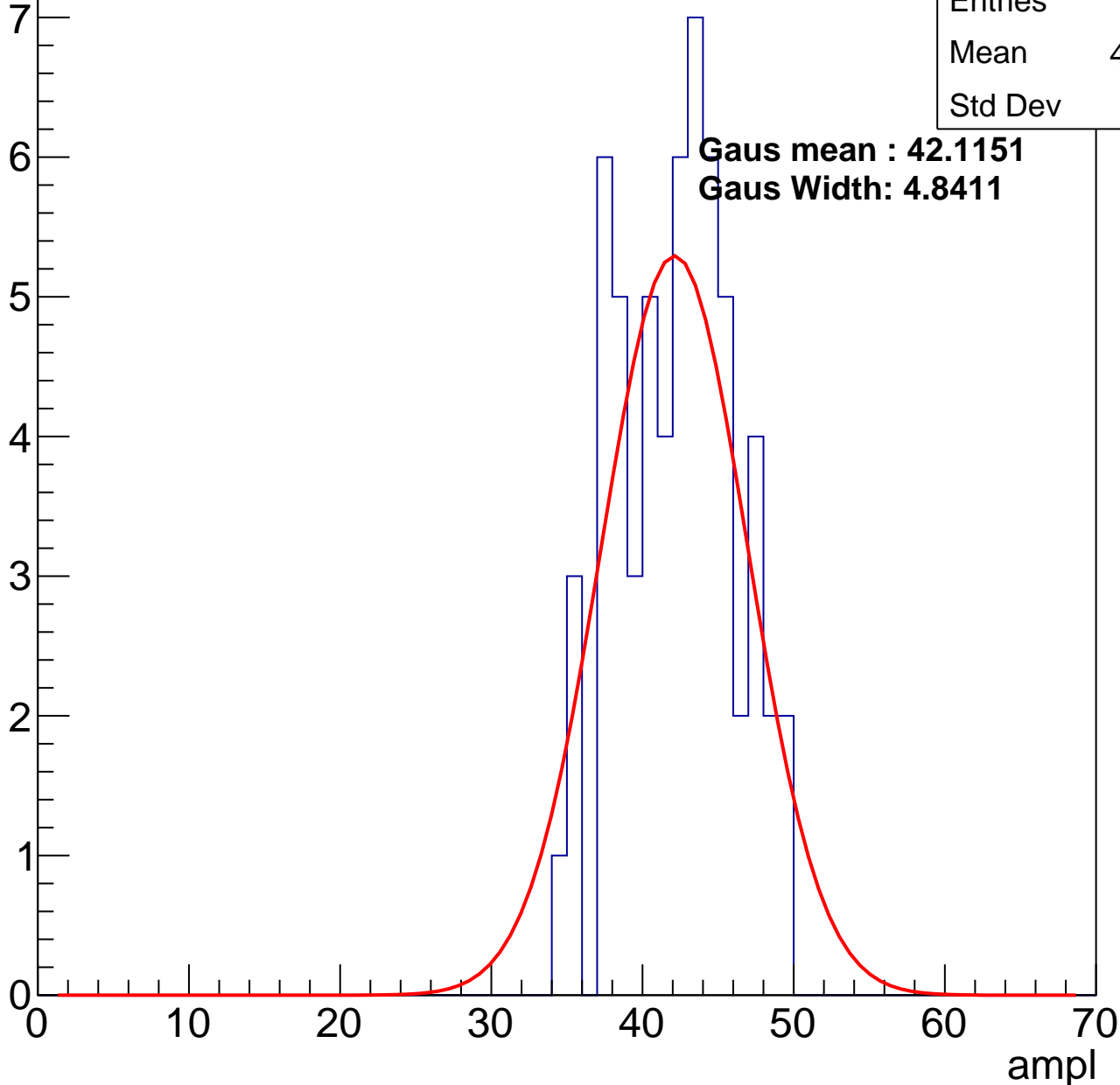
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.77
Std Dev	3.77

Gaus mean : 42.1151

Gaus Width: 4.8411

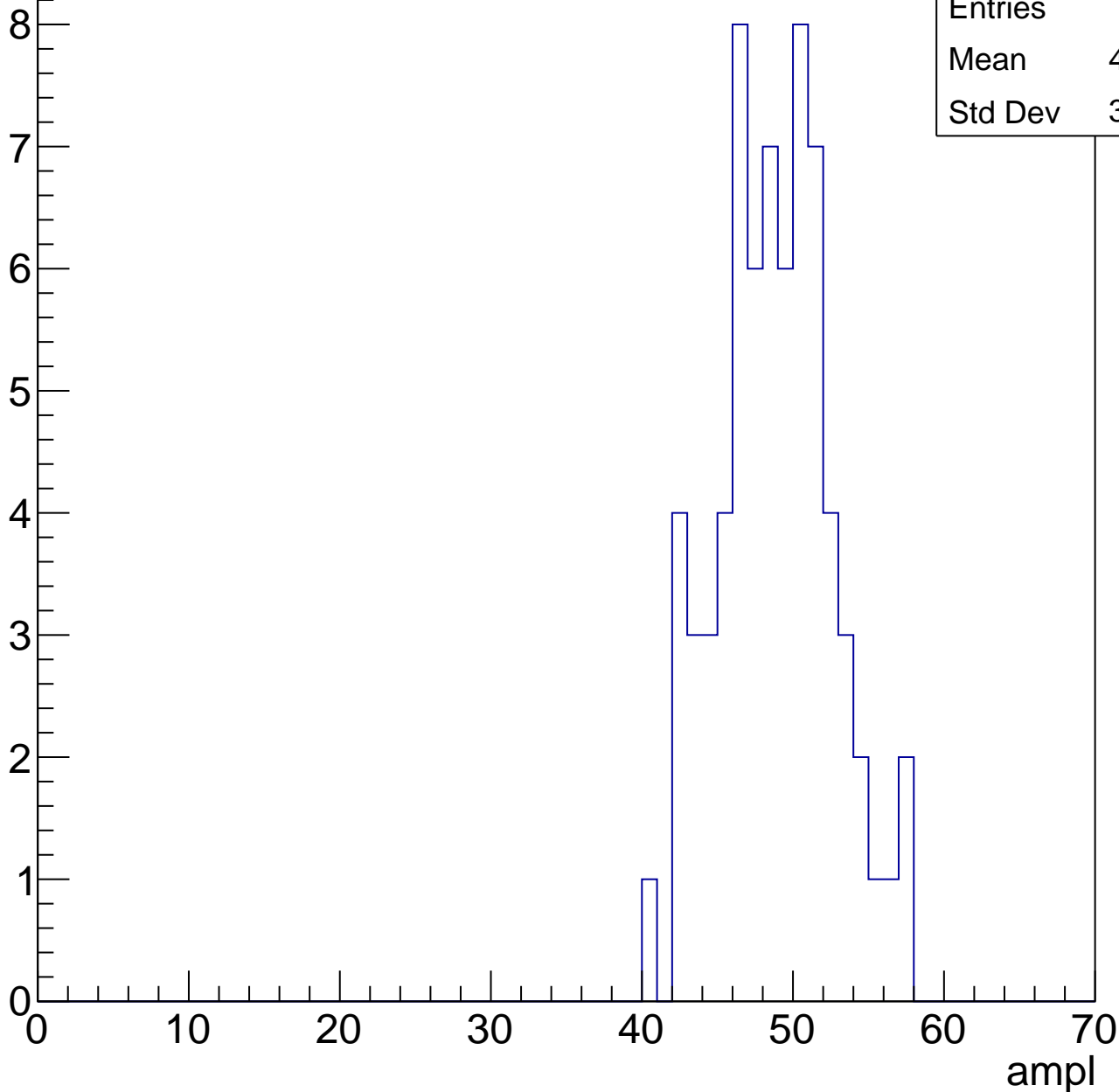


B1L102S, U14-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.37
Std Dev	3.746

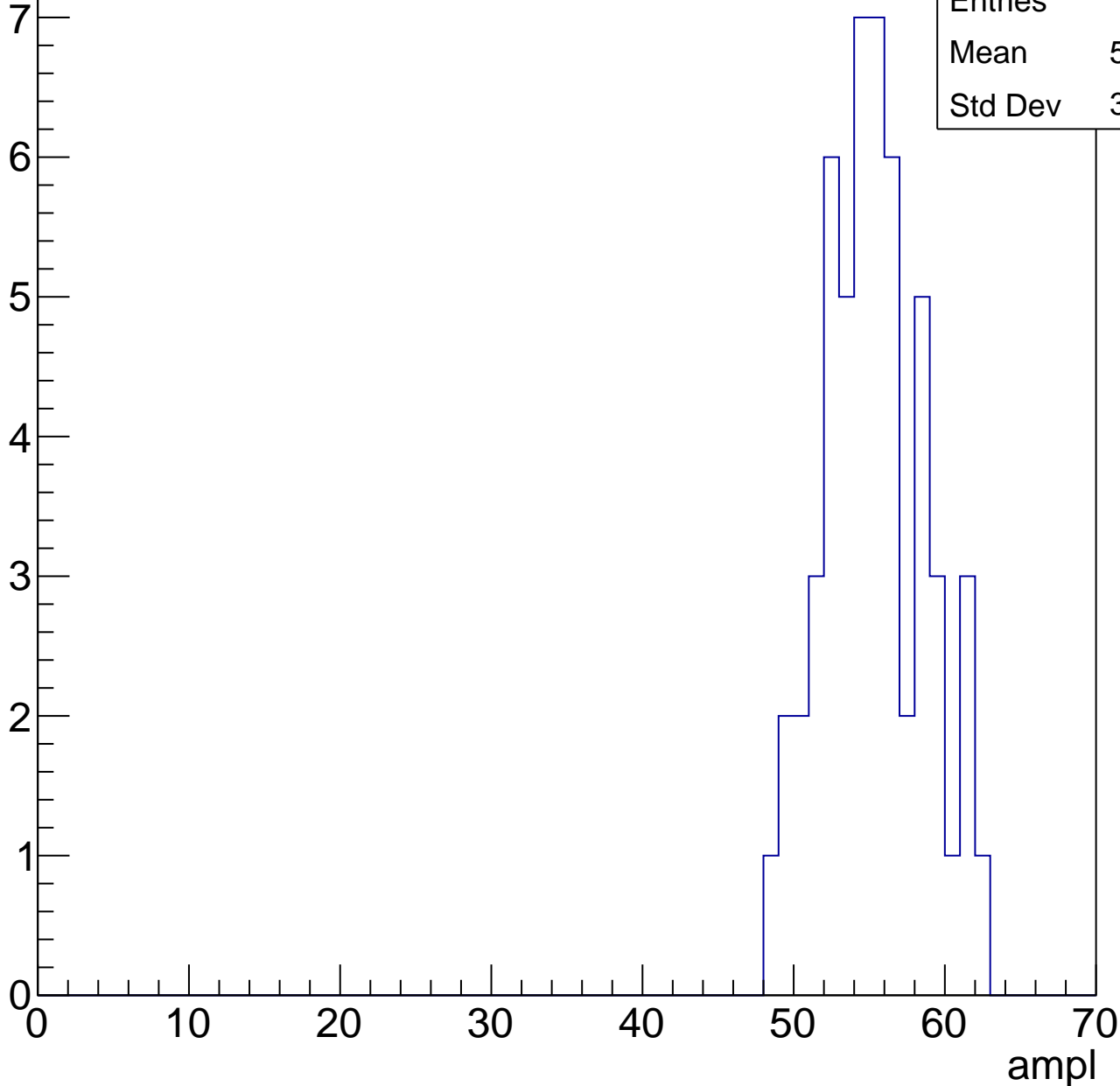


B1L102S, U14-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.83
Std Dev	3.298

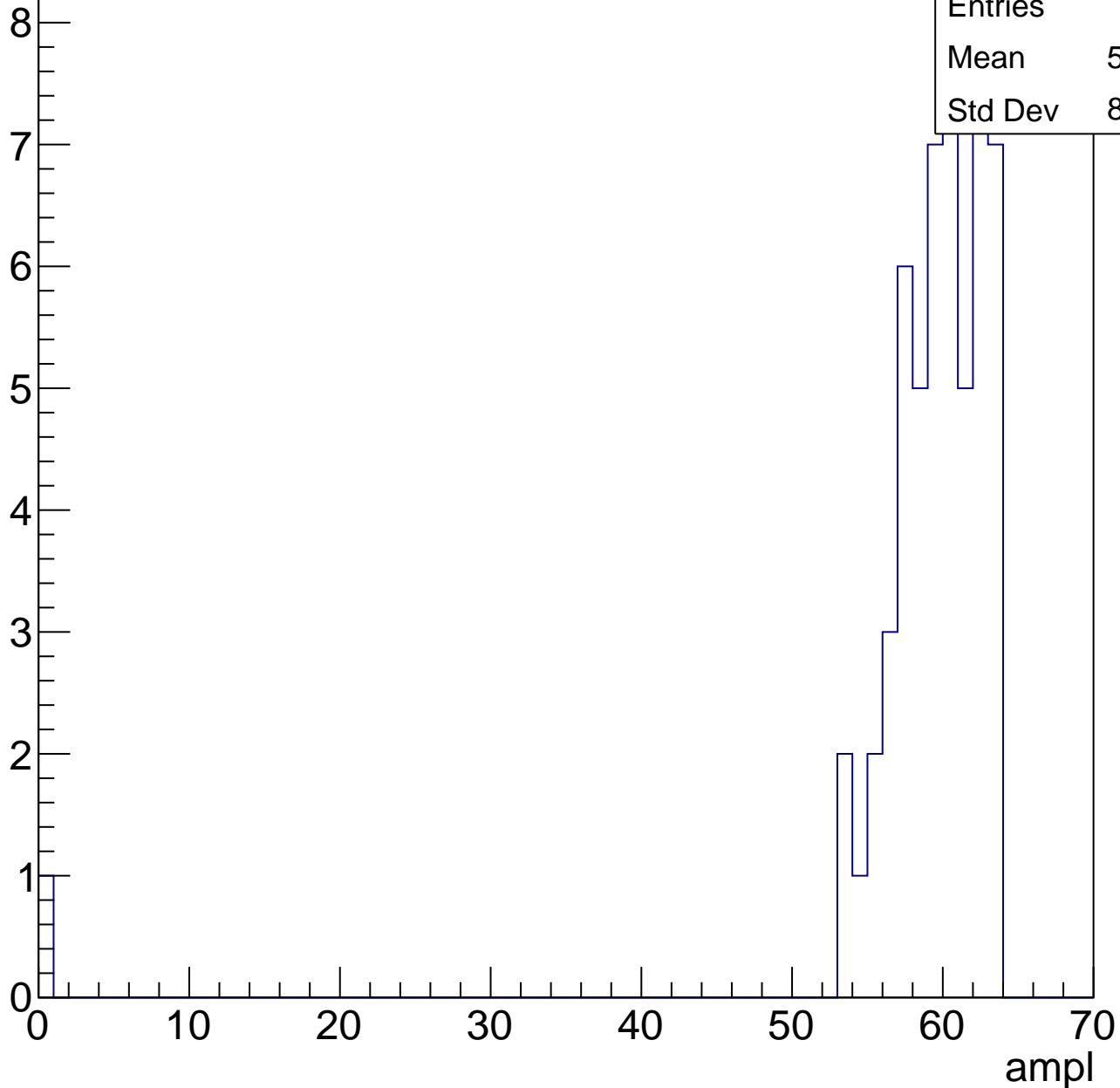


B1L102S, U14-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.27
Std Dev	8.365



B1L102S, U14-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch16, adc0

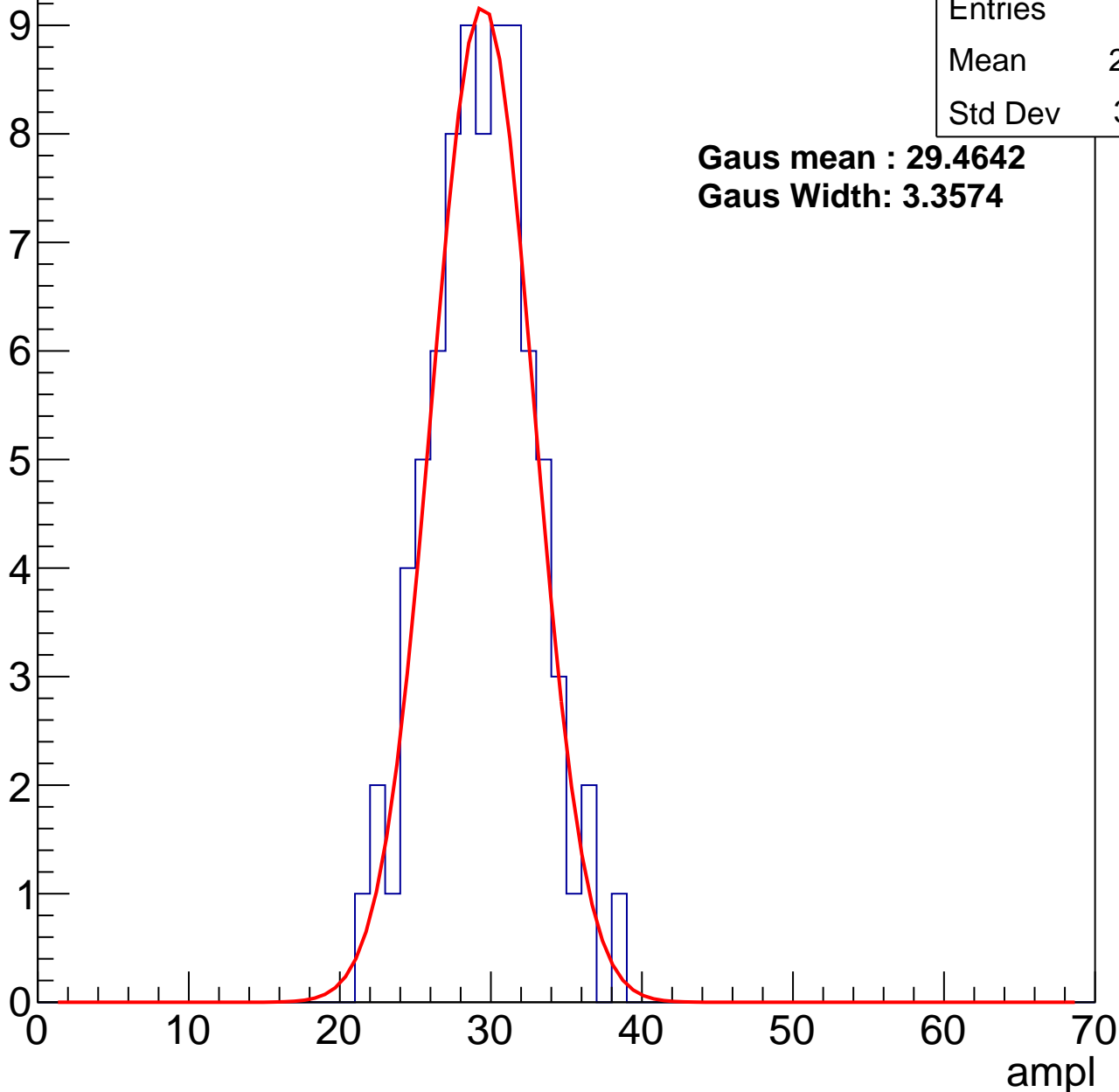
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.98
Std Dev	3.431

Gaus mean : 29.4642

Gaus Width: 3.3574



B1L102S, U14-ch16, adc1

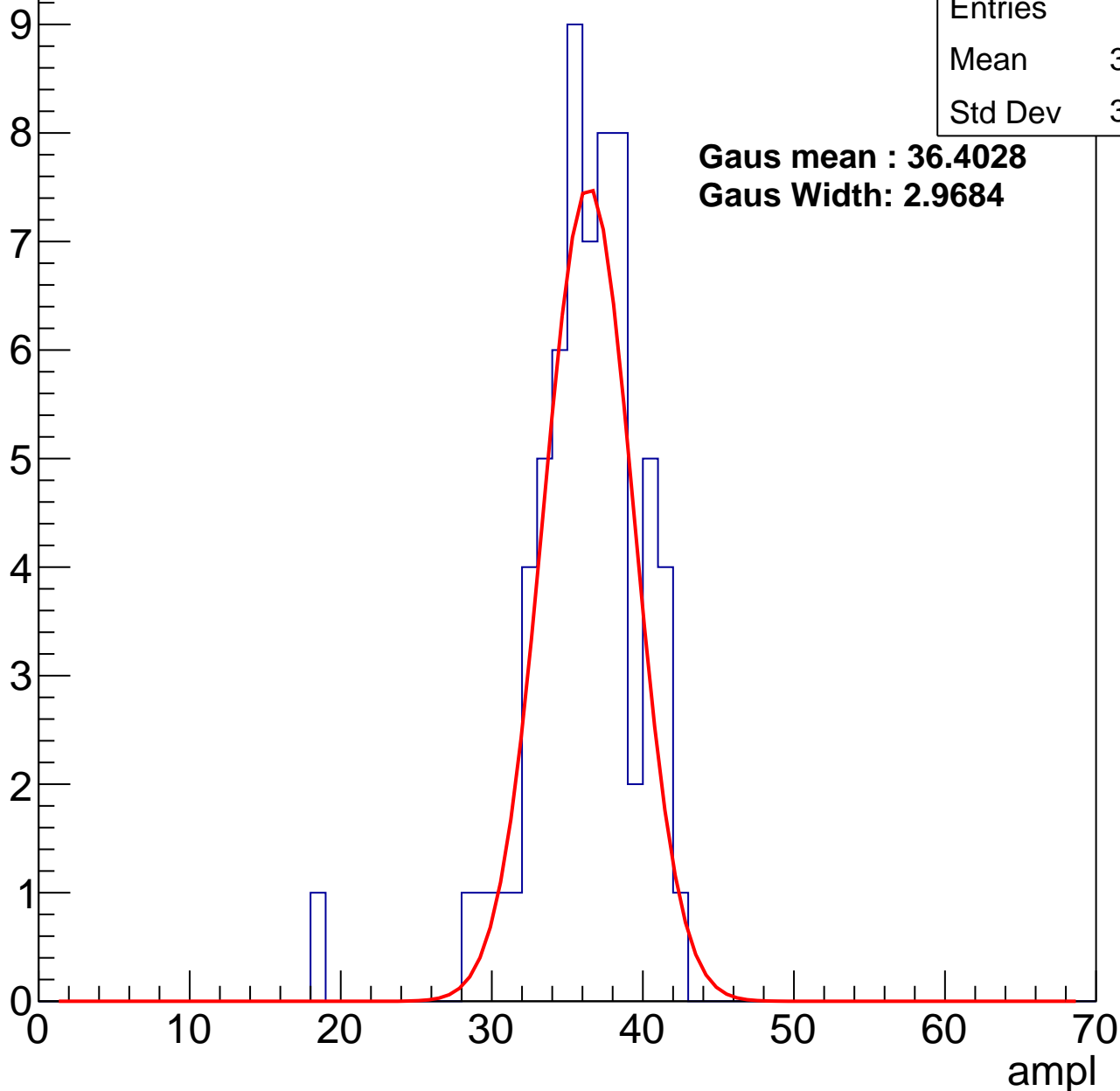
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.69
Std Dev	3.766

Gaus mean : 36.4028

Gaus Width: 2.9684



B1L102S, U14-ch16, adc2

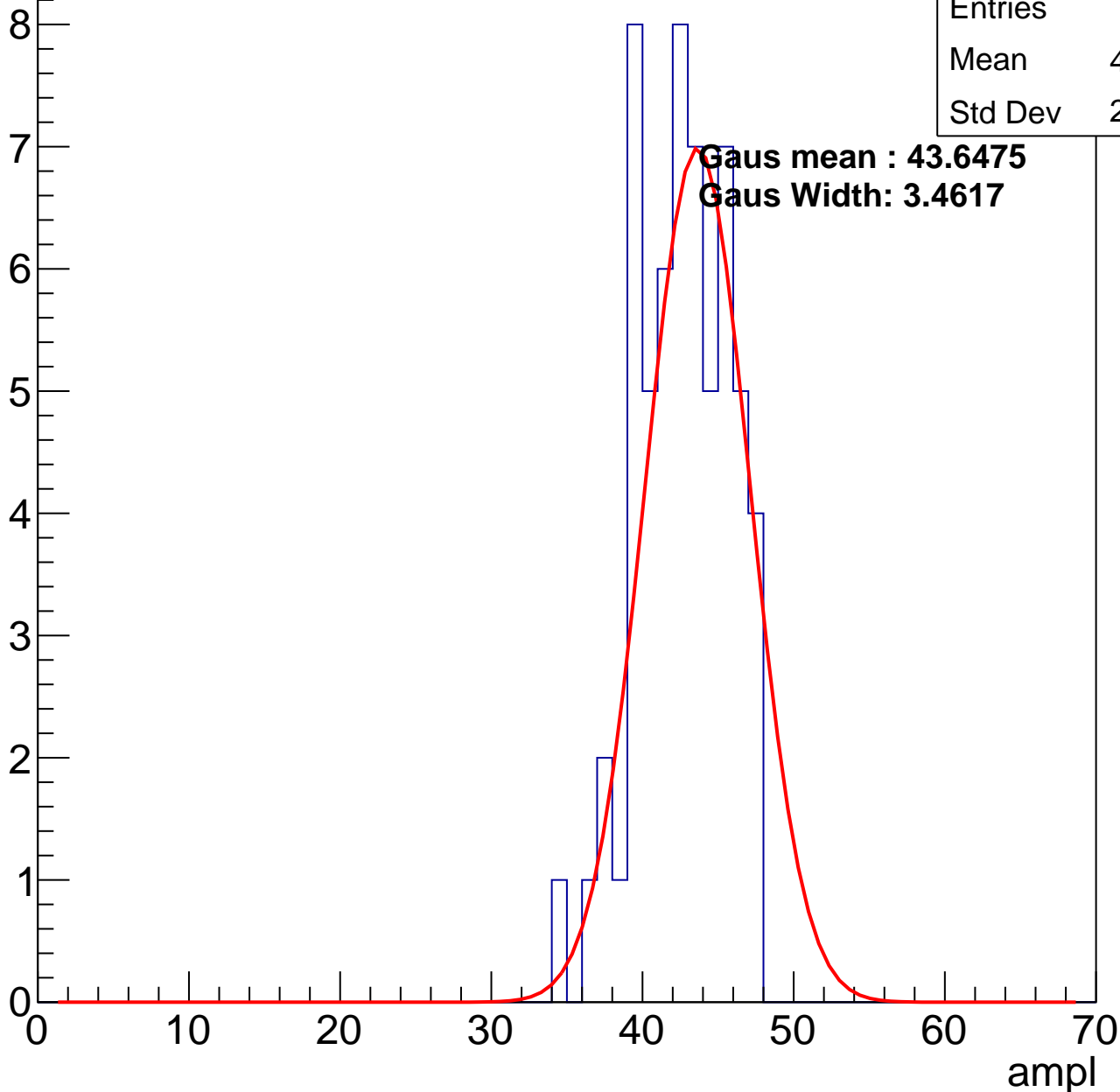
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.17
Std Dev	2.979

Gaus mean : 43.6475

Gaus Width: 3.4617



B1L102S, U14-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

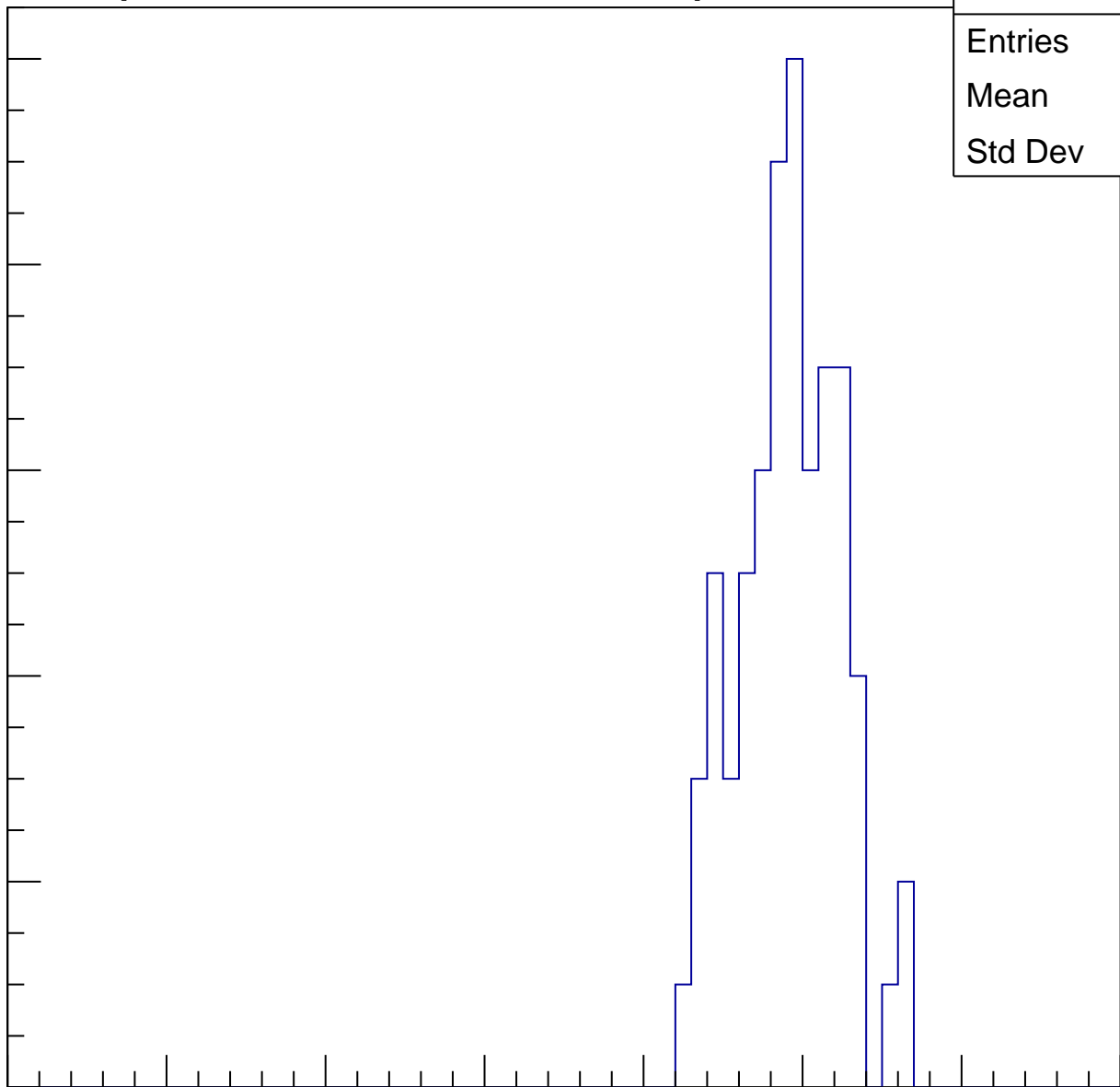
Entries	69
Mean	48.7
Std Dev	3.177

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

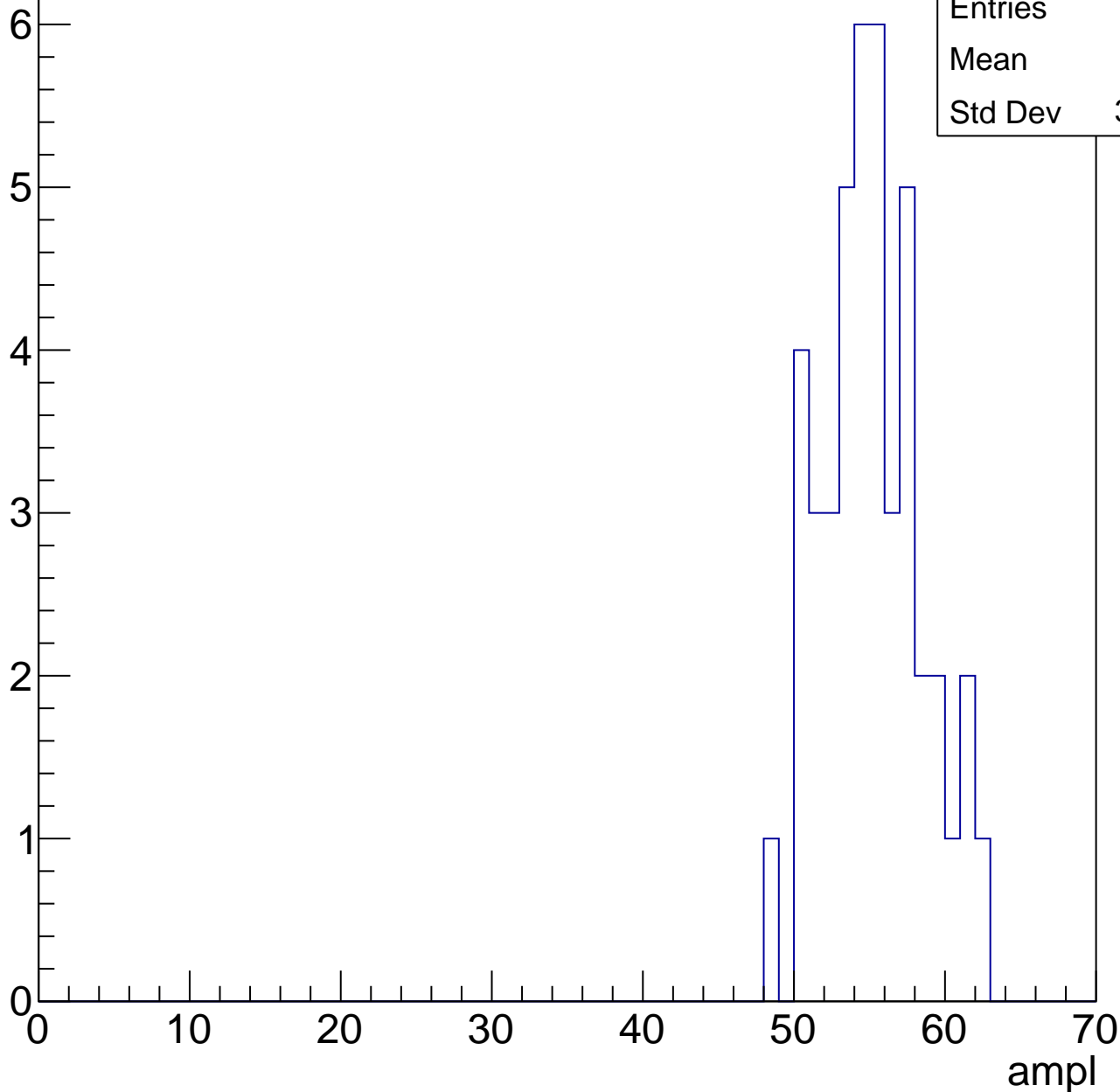


B1L102S, U14-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

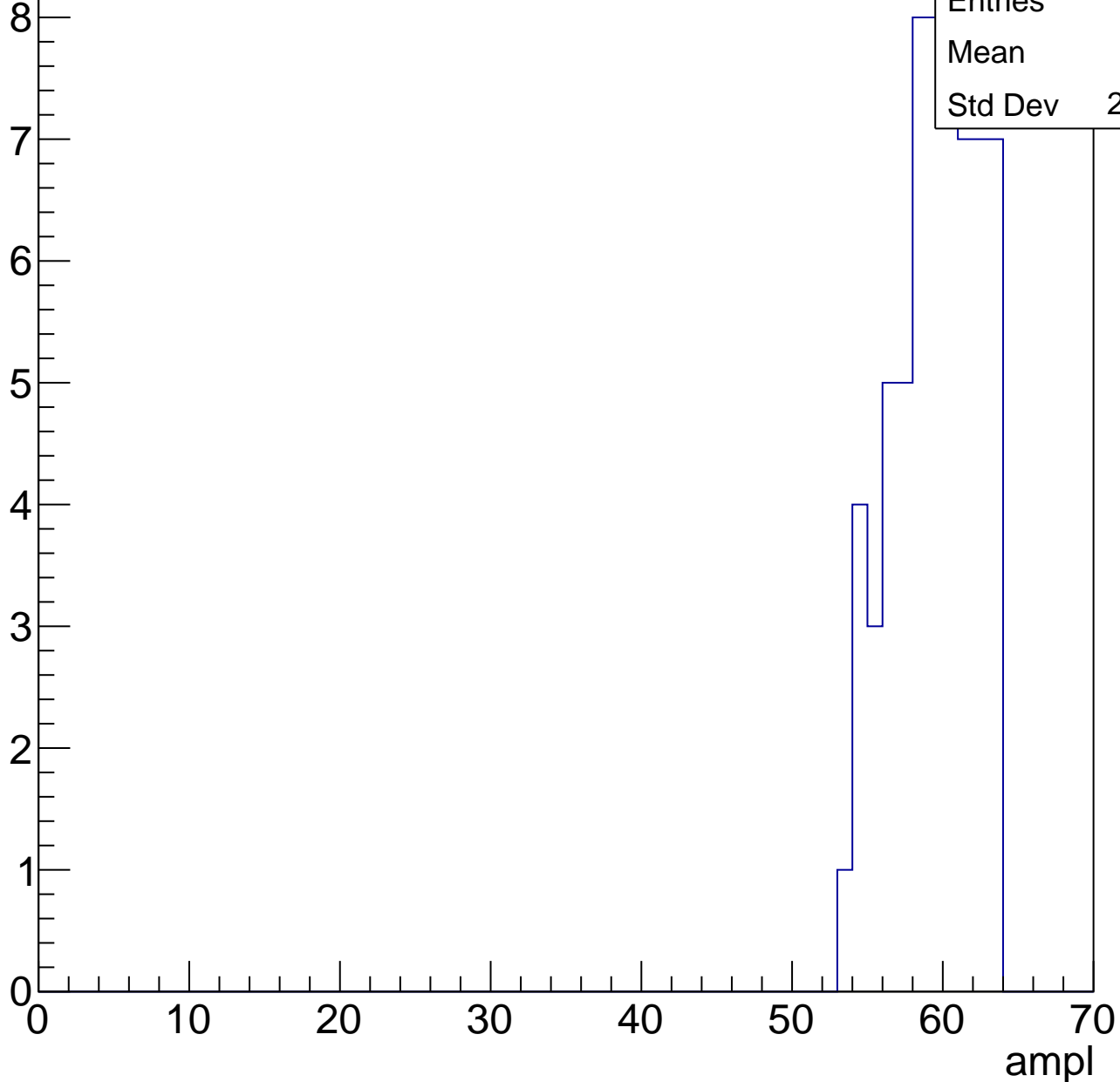
Entries	44
Mean	54.7
Std Dev	3.251



B1L102S, U14-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

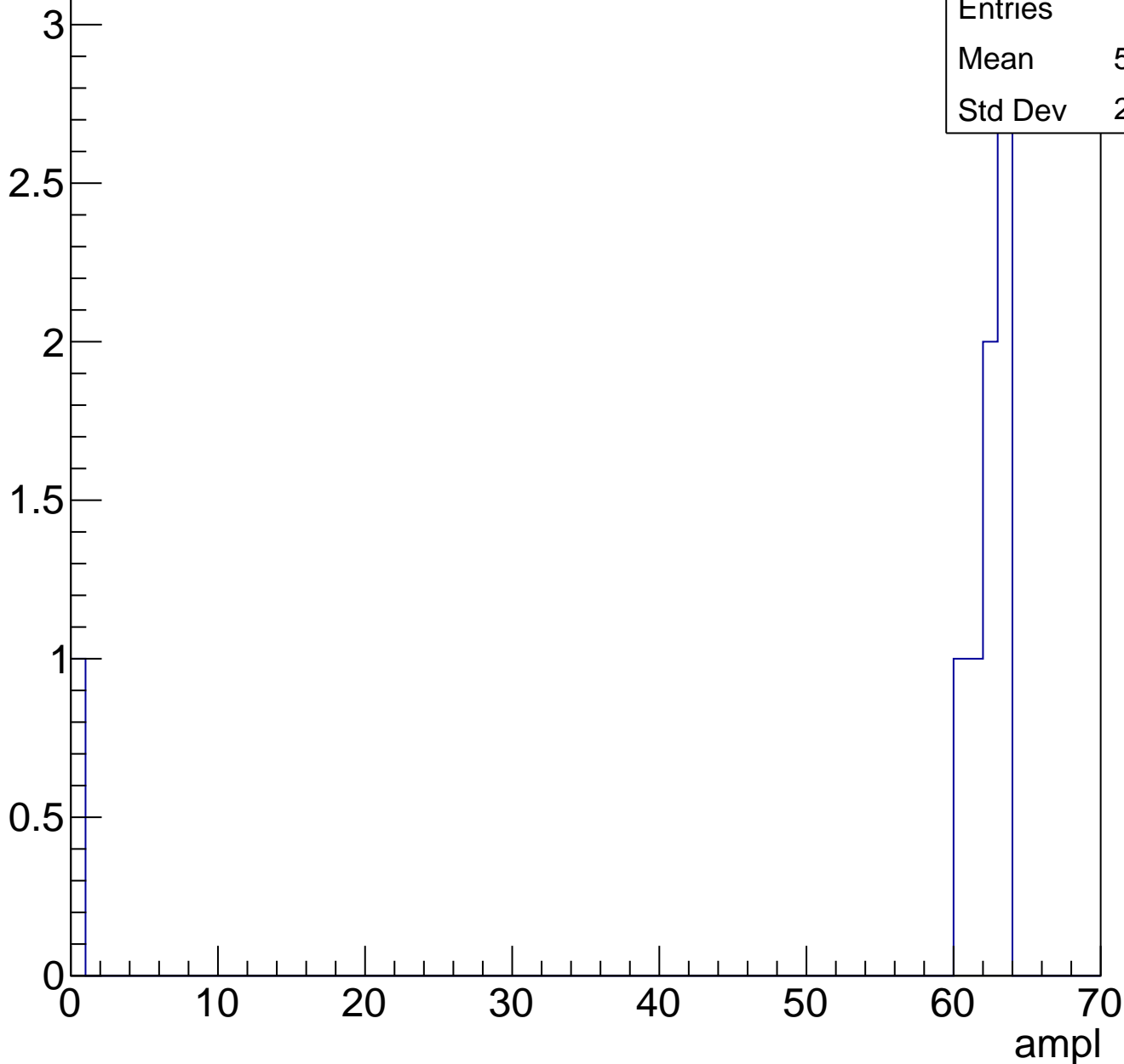


Entries	63
Mean	59
Std Dev	2.726

B1L102S, U14-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch17, adc0

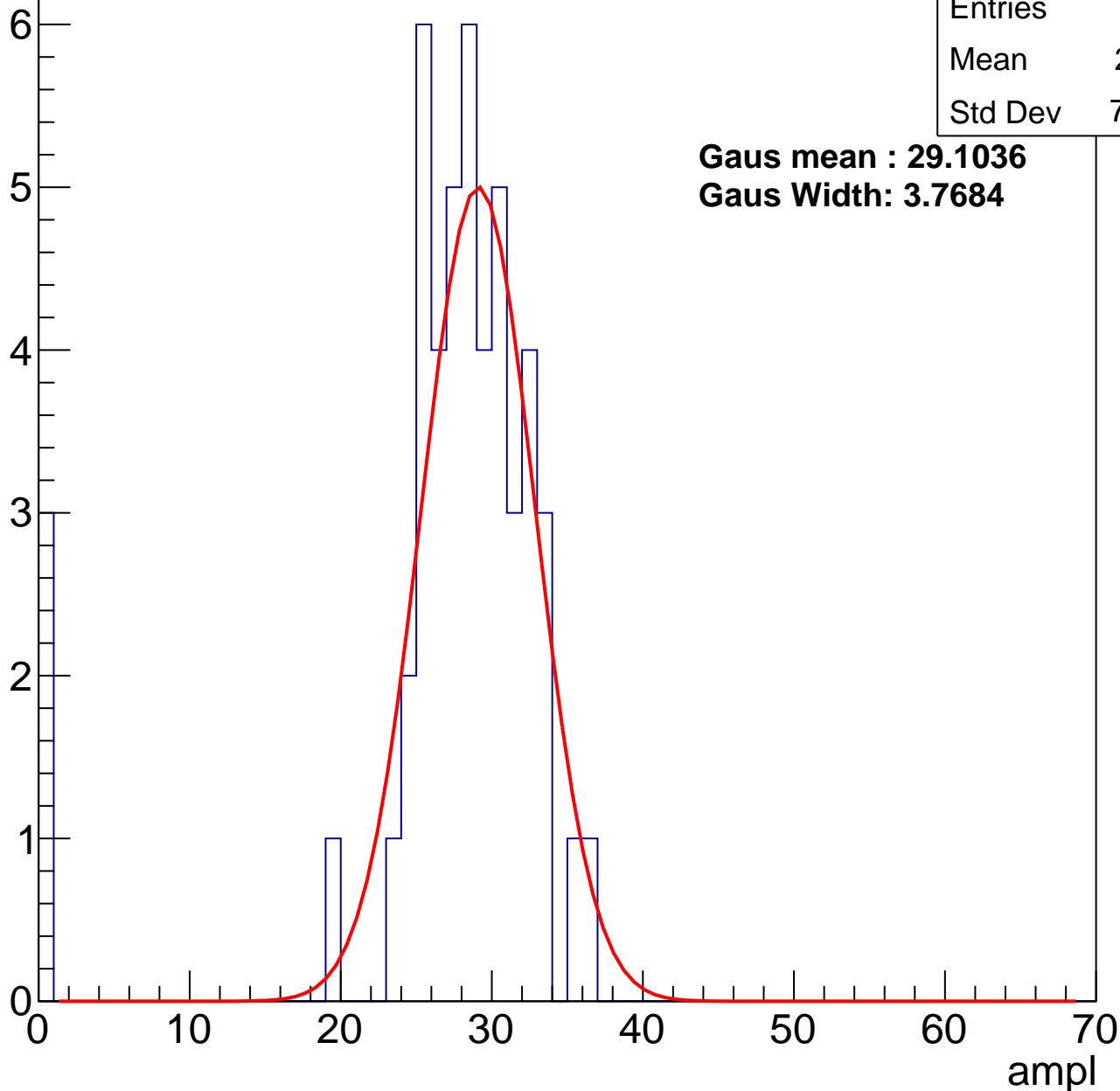
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	26.61
Std Dev	7.524

Gaus mean : 29.1036

Gaus Width: 3.7684



B1L102S, U14-ch17, adc1

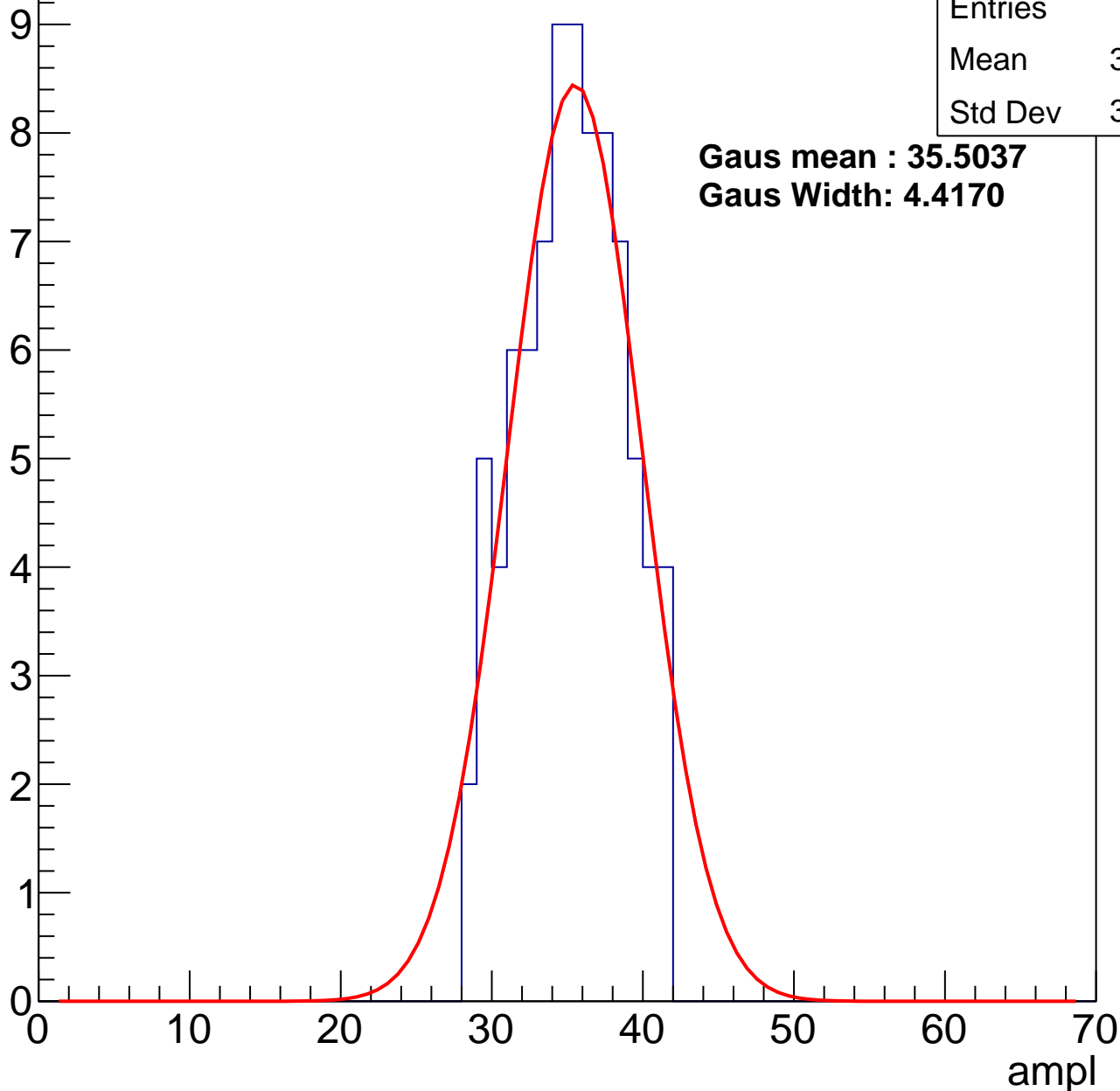
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	34.76
Std Dev	3.428

Gaus mean : 35.5037

Gaus Width: 4.4170



B1L102S, U14-ch17, adc2

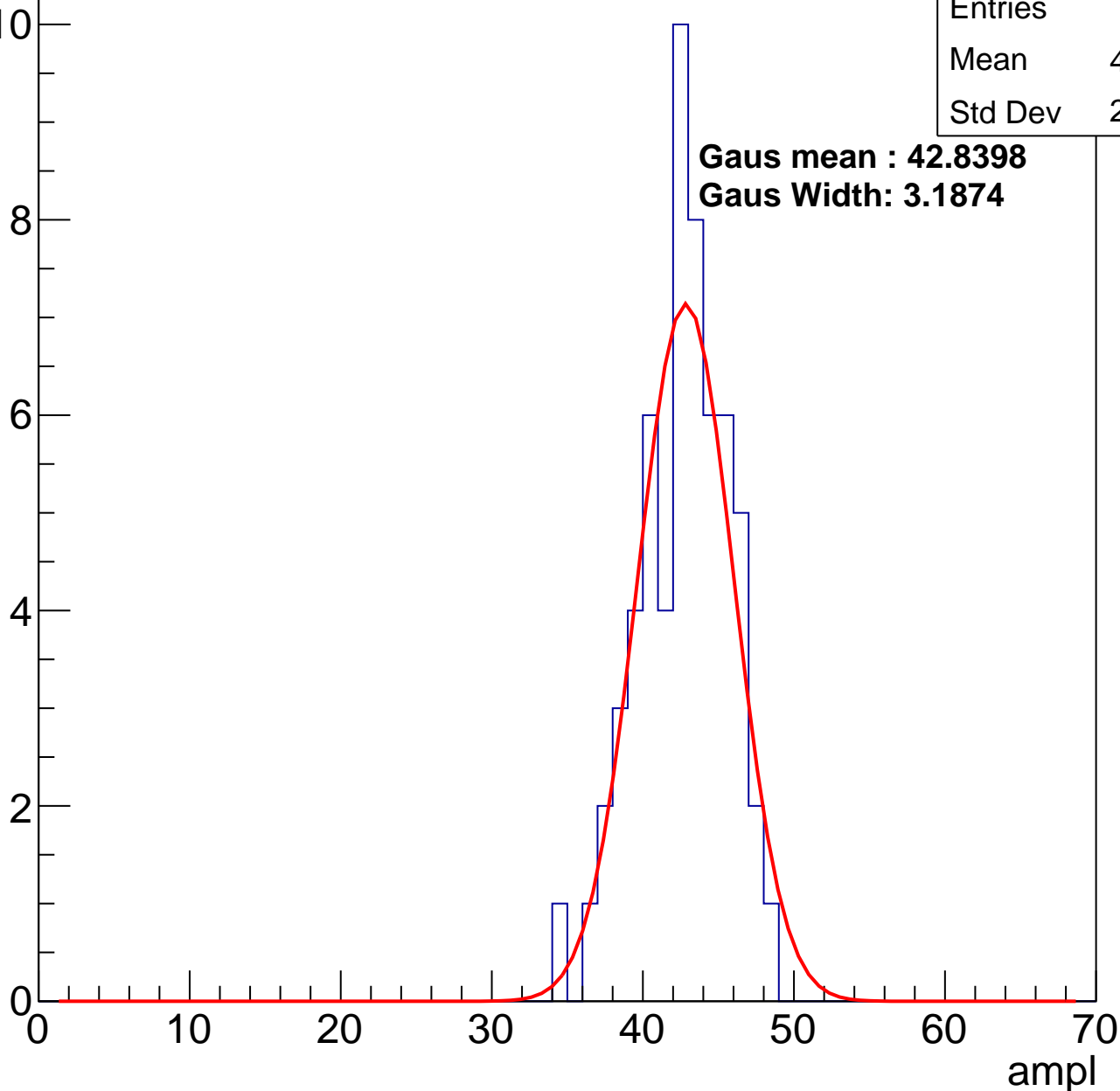
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.17
Std Dev	2.947

Gaus mean : 42.8398

Gaus Width: 3.1874

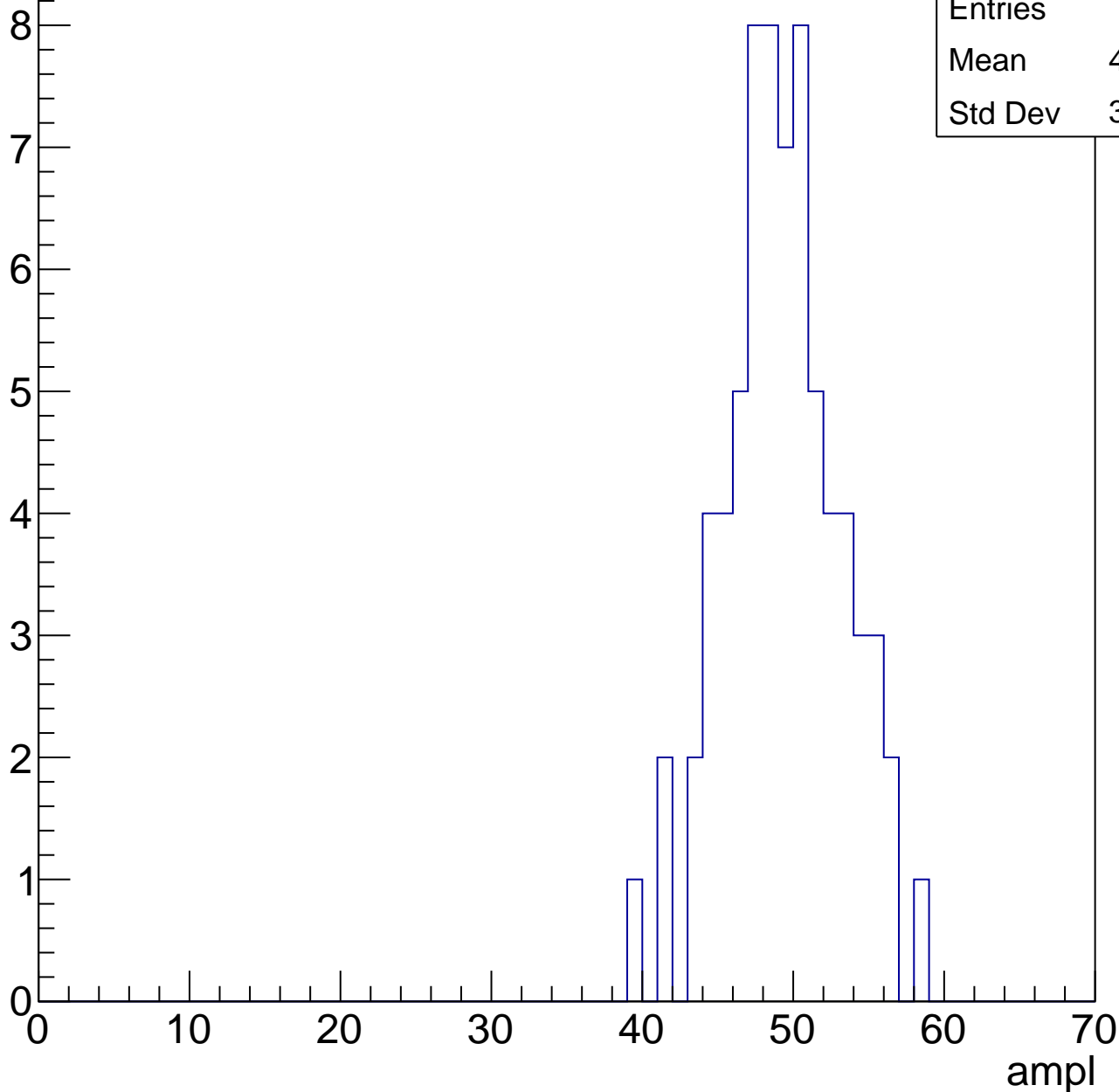


B1L102S, U14-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	48.85
Std Dev	3.822

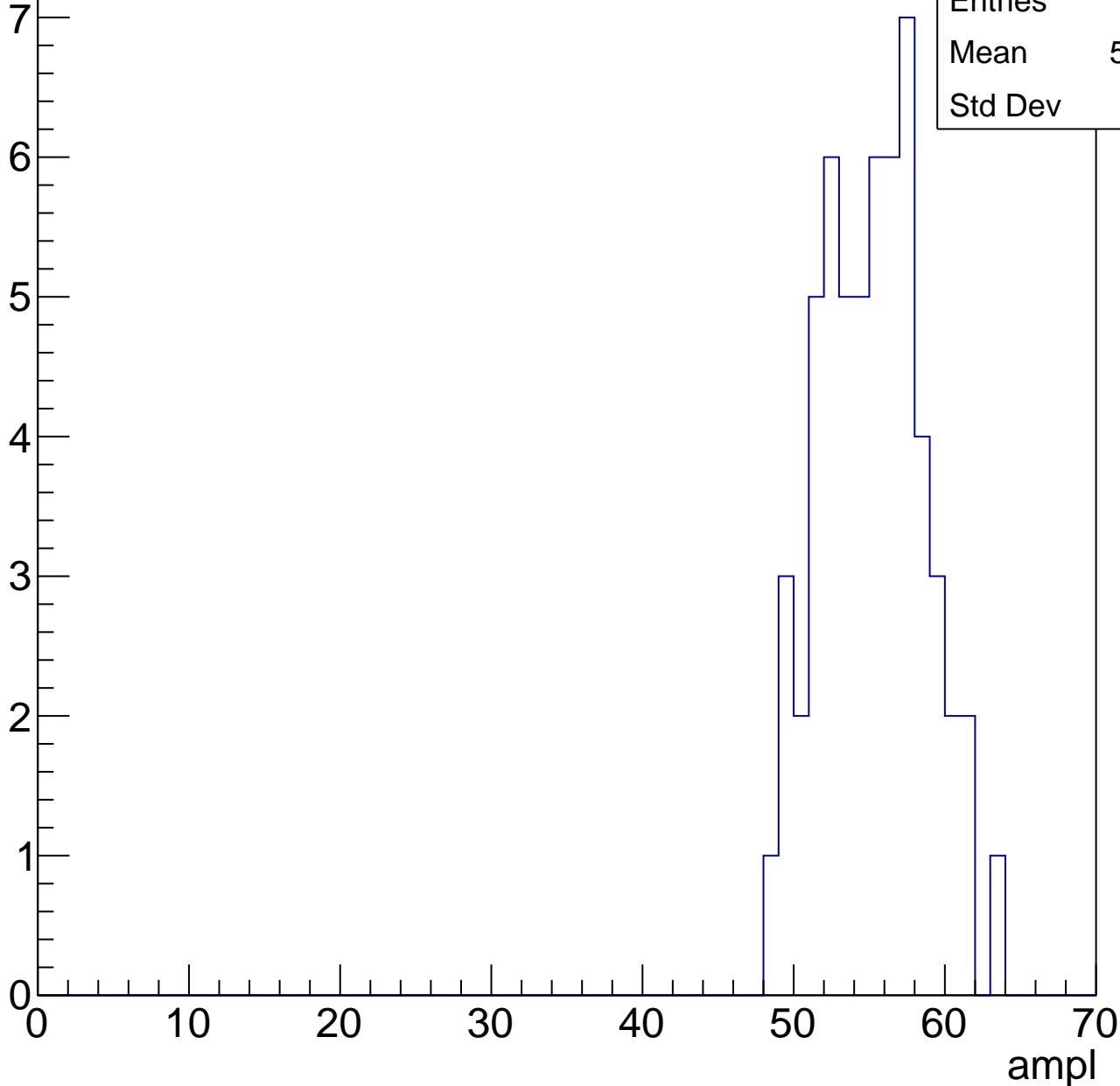


B1L102S, U14-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.76
Std Dev	3.39



B1L102S, U14-ch17, adc5

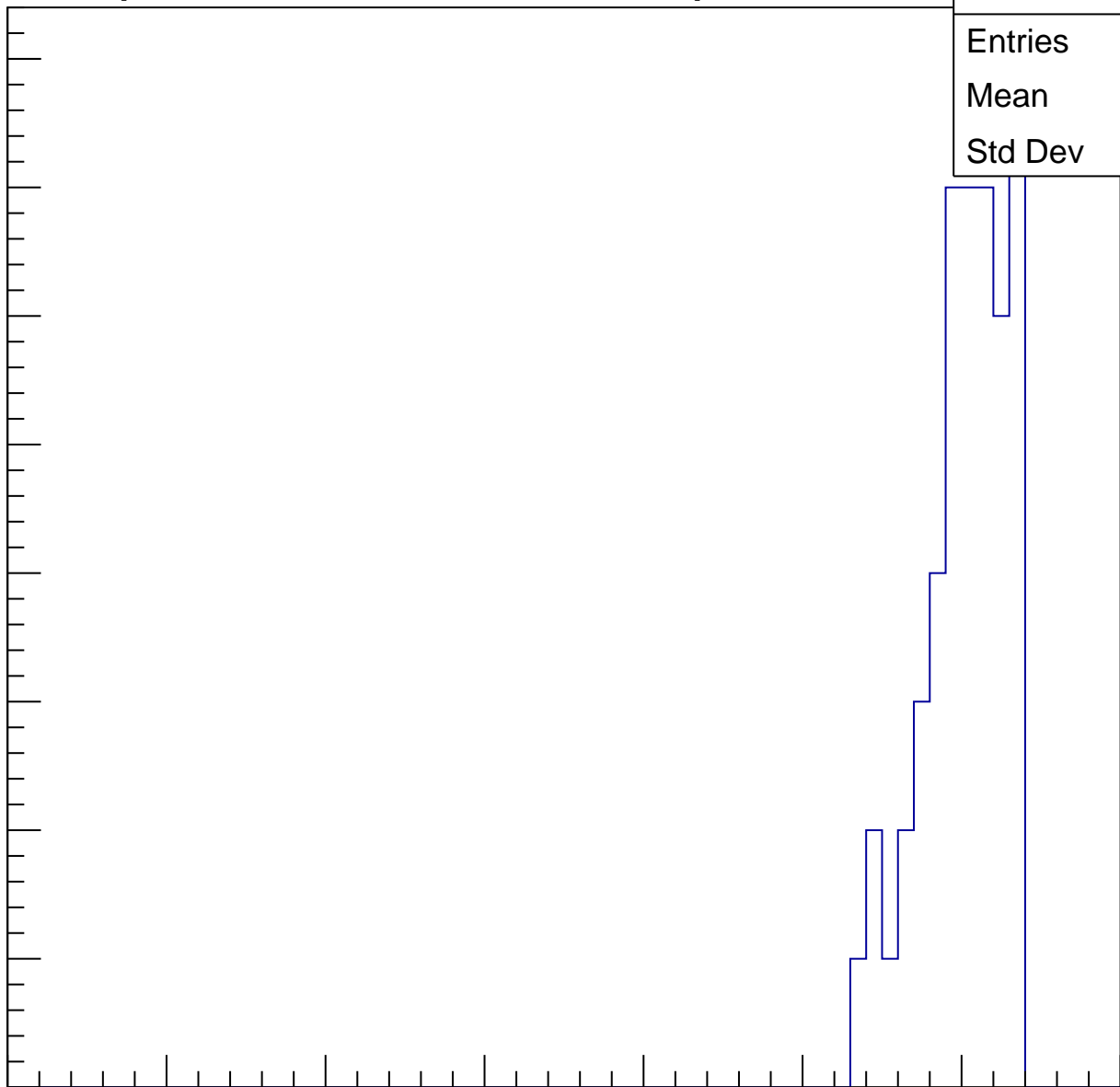
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.73
Std Dev	2.612

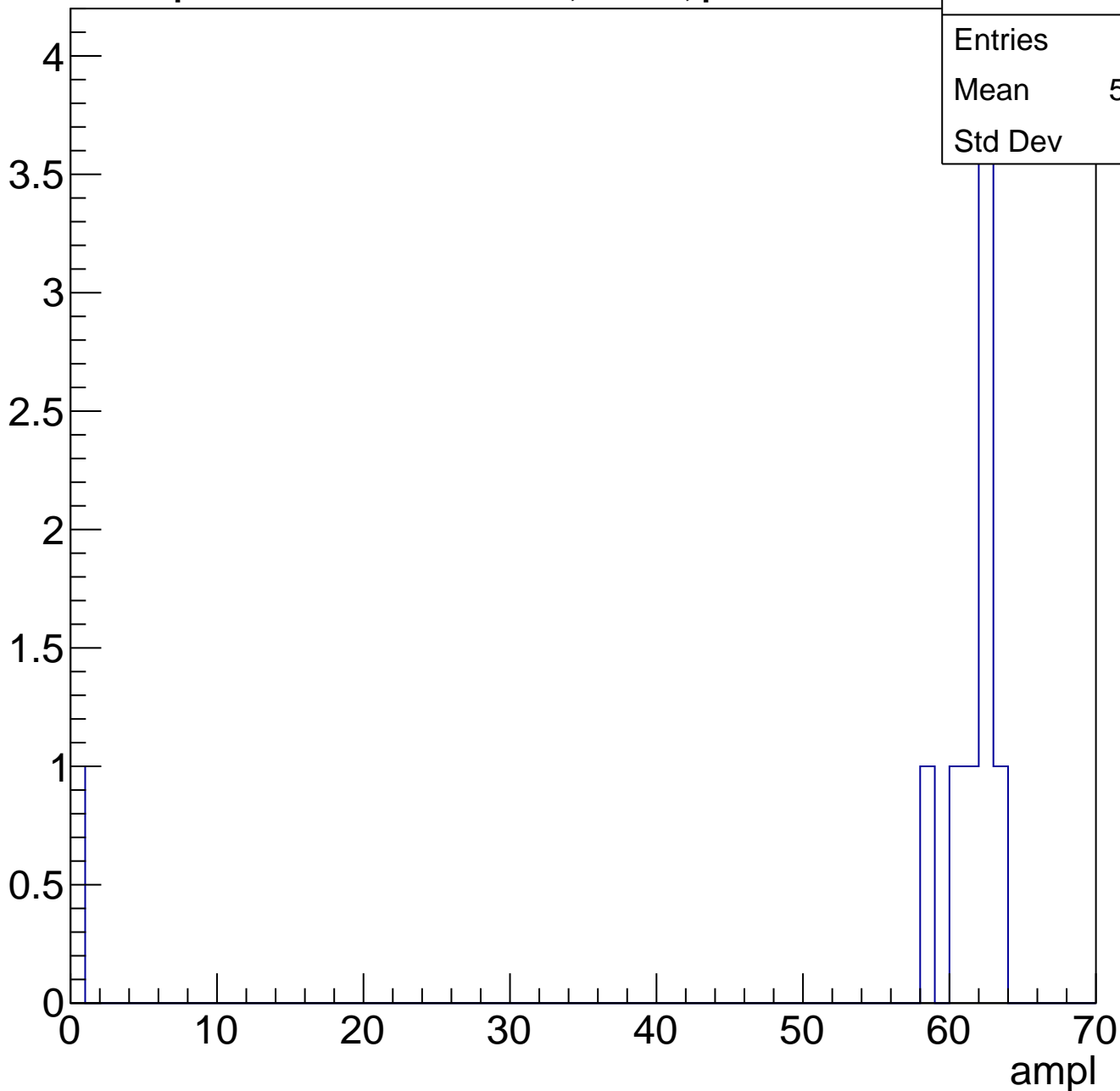
ampl



B1L102S, U14-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch18, adc0

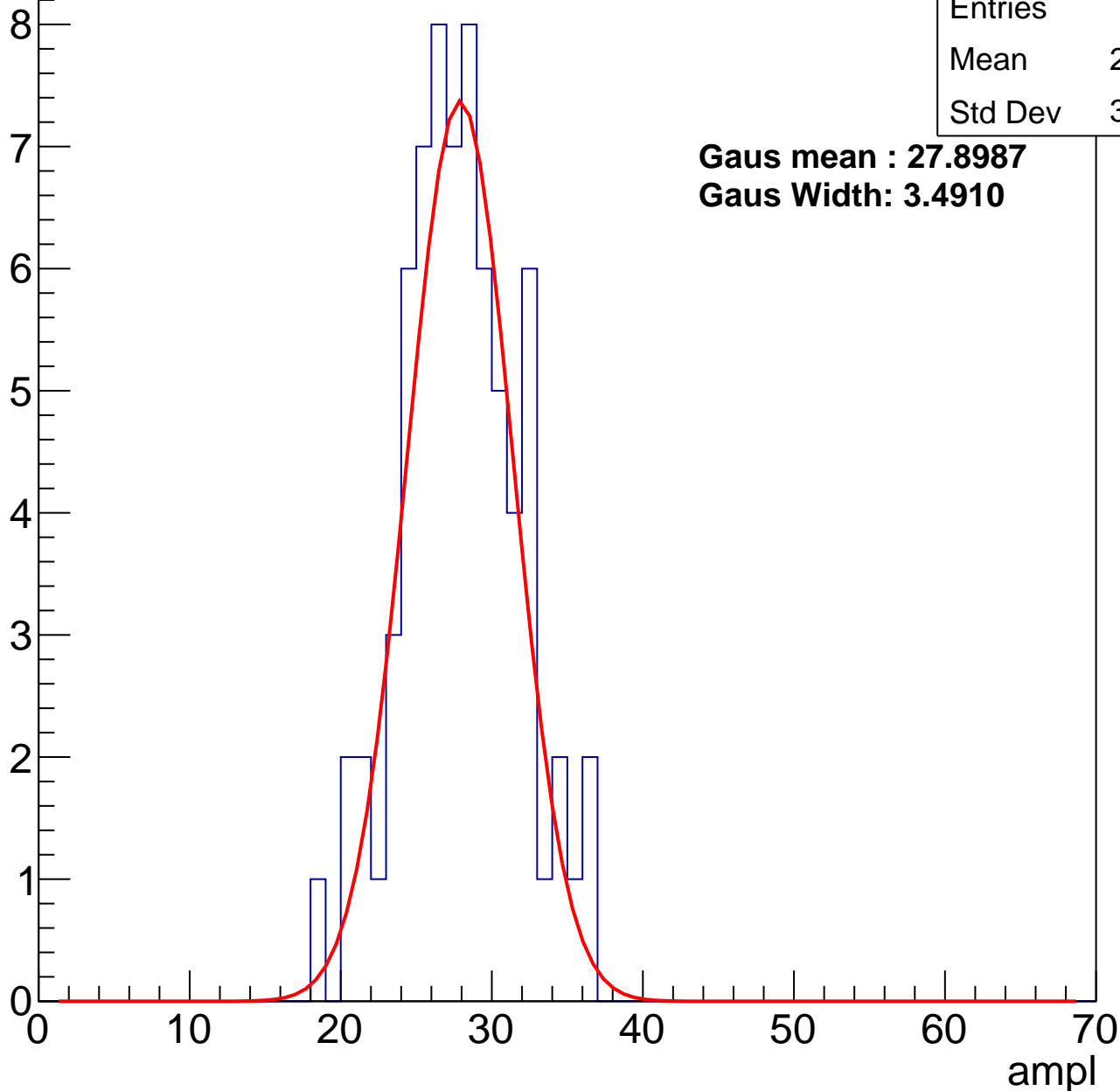
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.49
Std Dev	3.815

Gaus mean : 27.8987

Gaus Width: 3.4910



B1L102S, U14-ch18, adc1

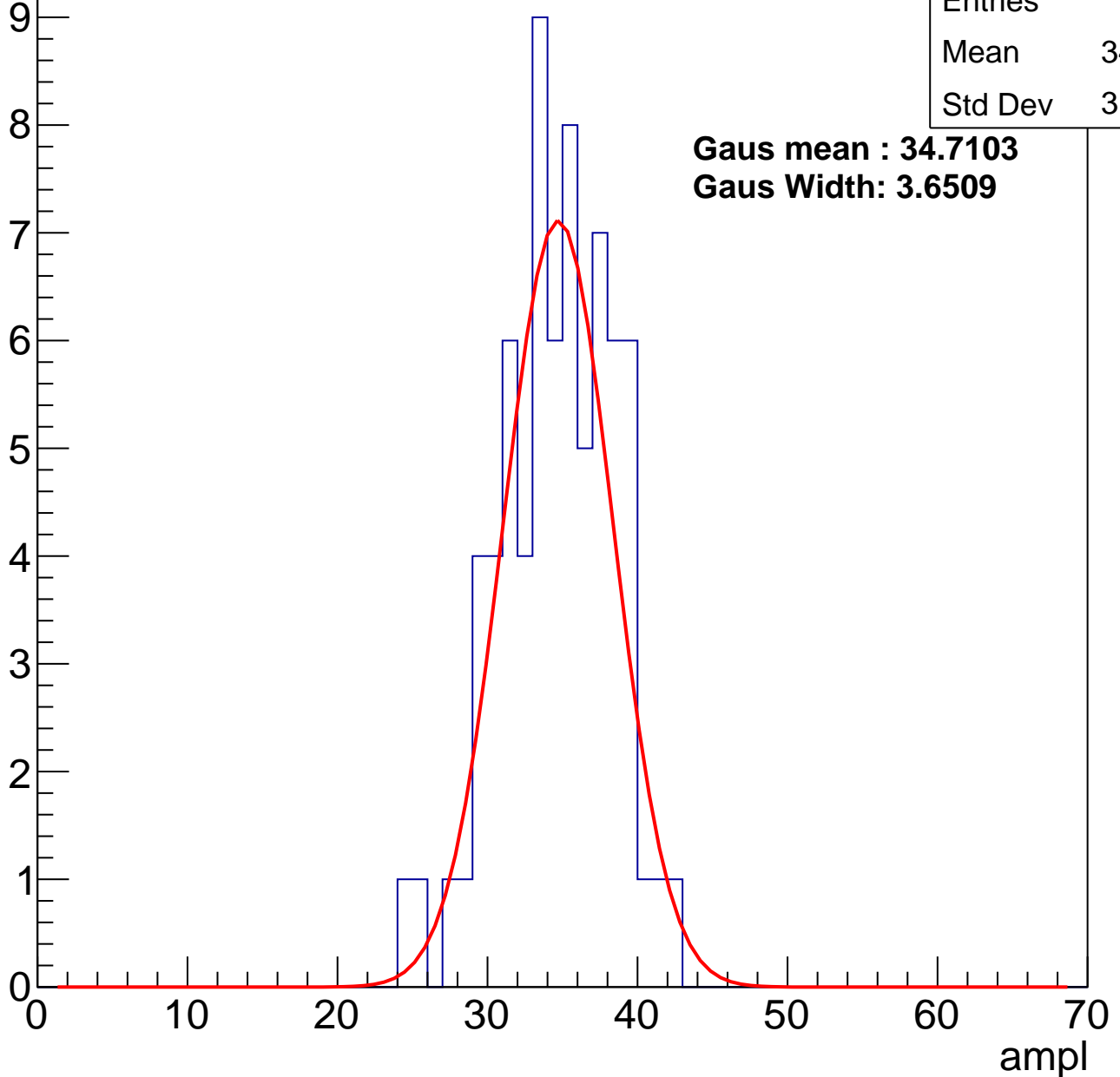
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.15
Std Dev	3.714

Gaus mean : 34.7103

Gaus Width: 3.6509



B1L102S, U14-ch18, adc2

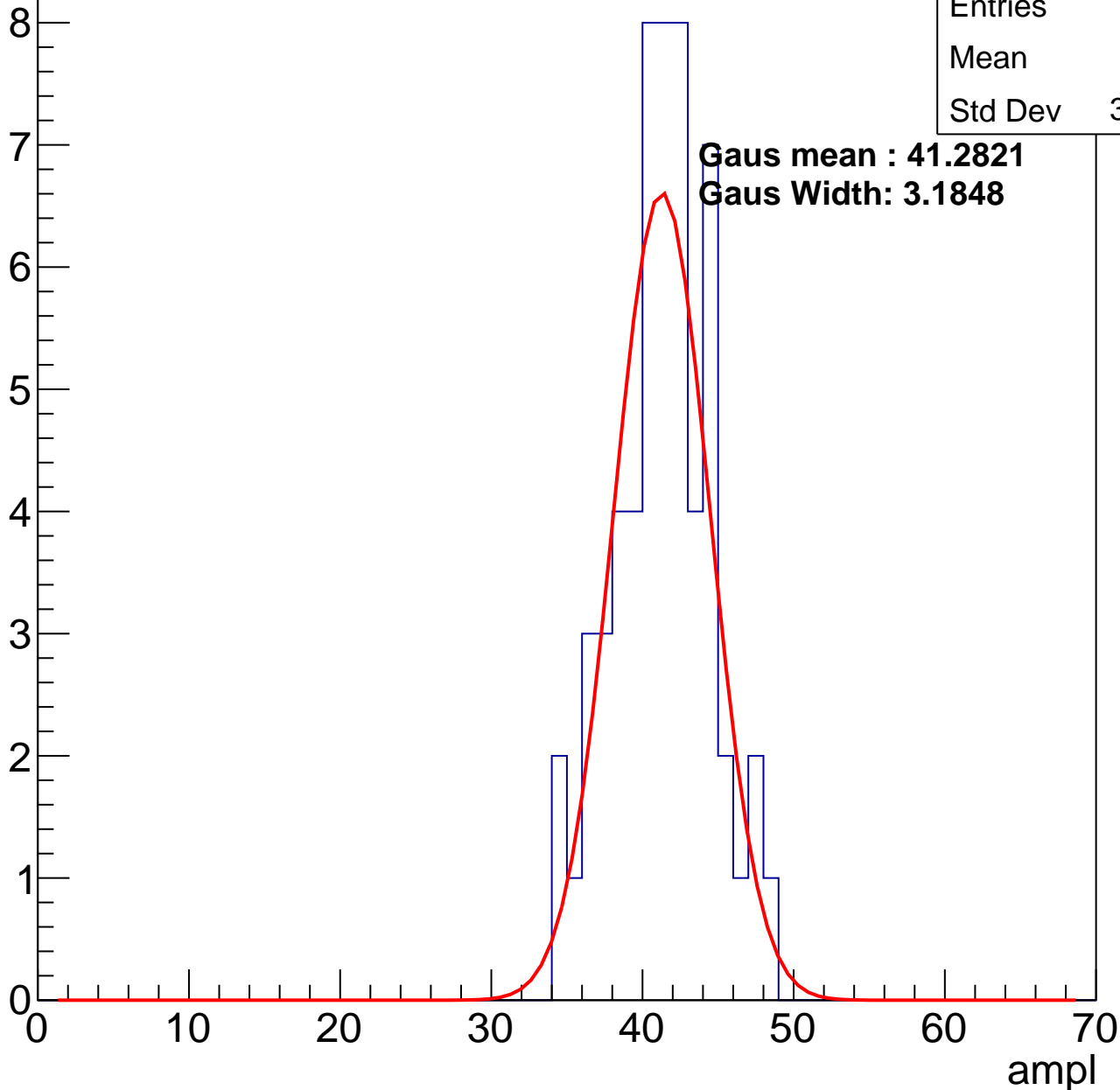
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	40.9
Std Dev	3.166

Gaus mean : 41.2821

Gaus Width: 3.1848

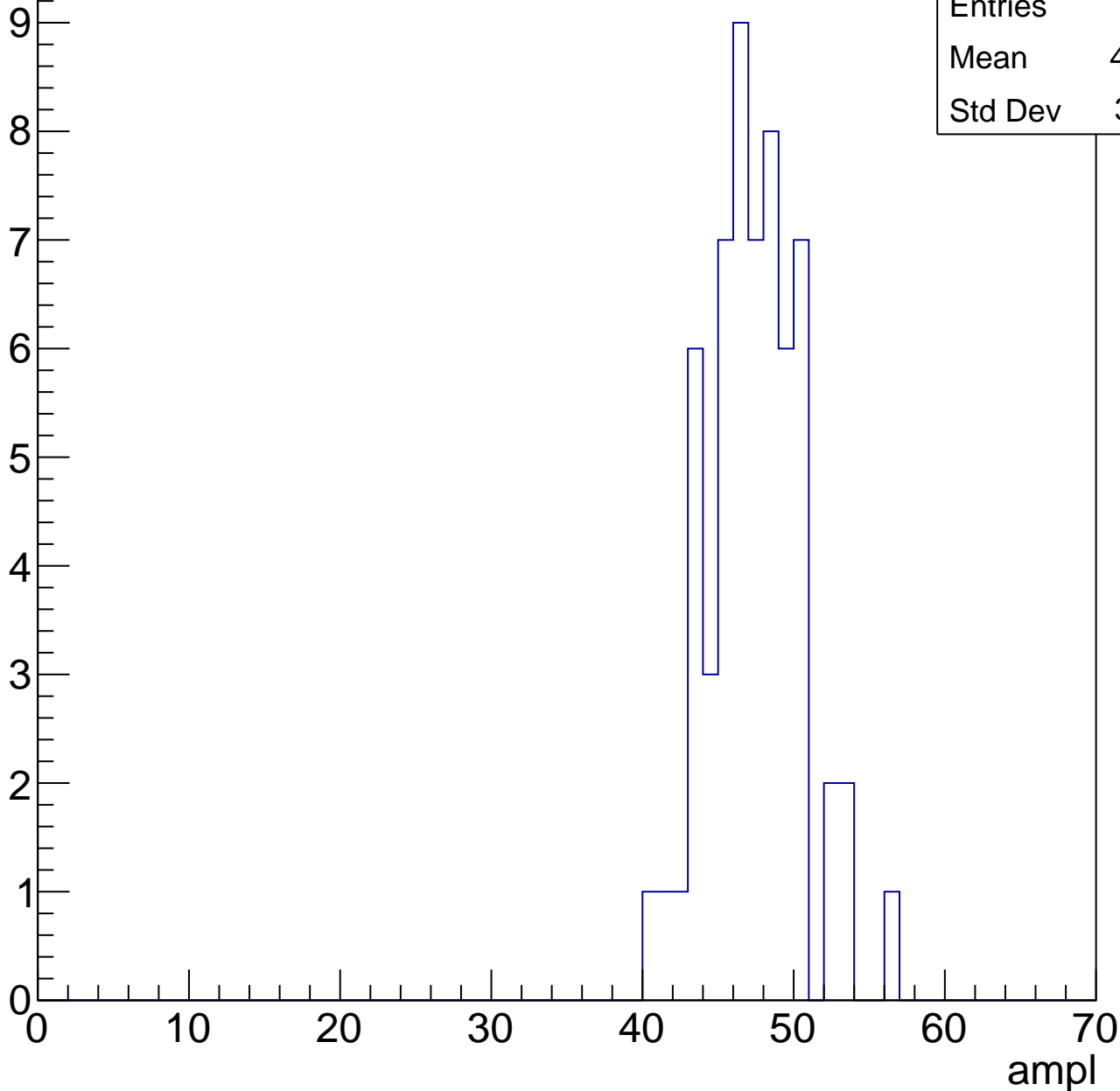


B1L102S, U14-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	46.97
Std Dev	3.051

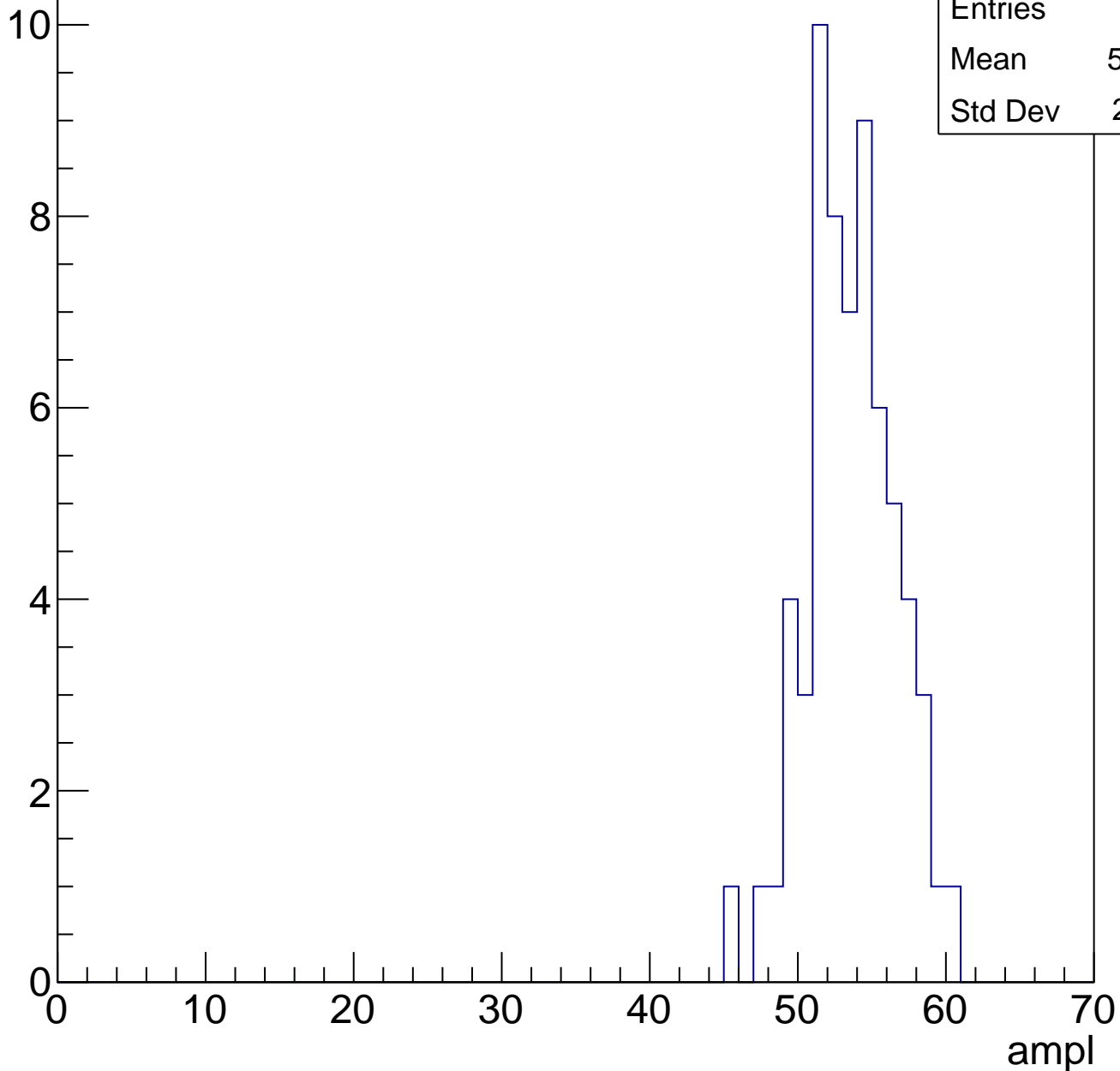


B1L102S, U14-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	53.12
Std Dev	2.971

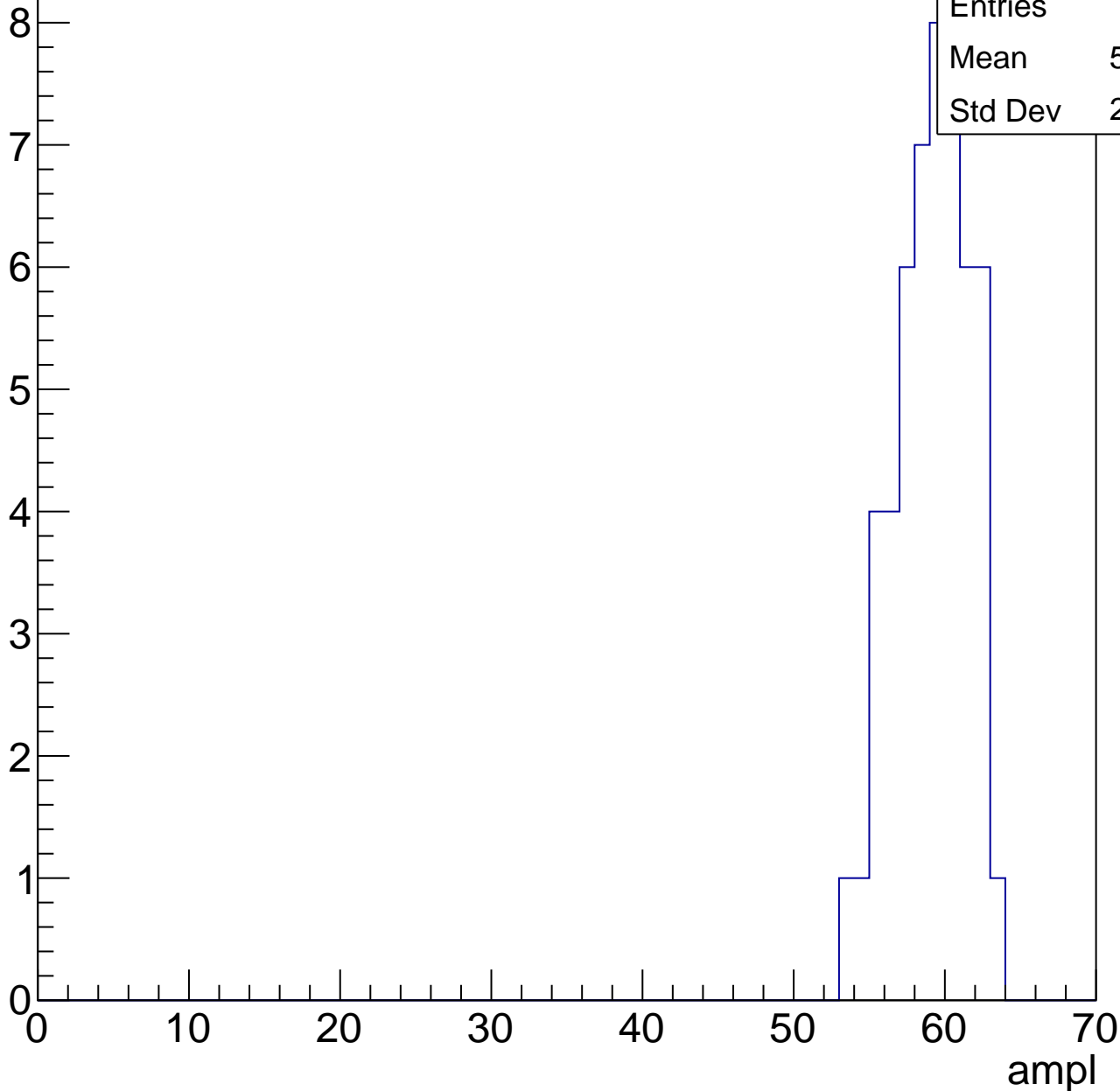


B1L102S, U14-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.69
Std Dev	2.358

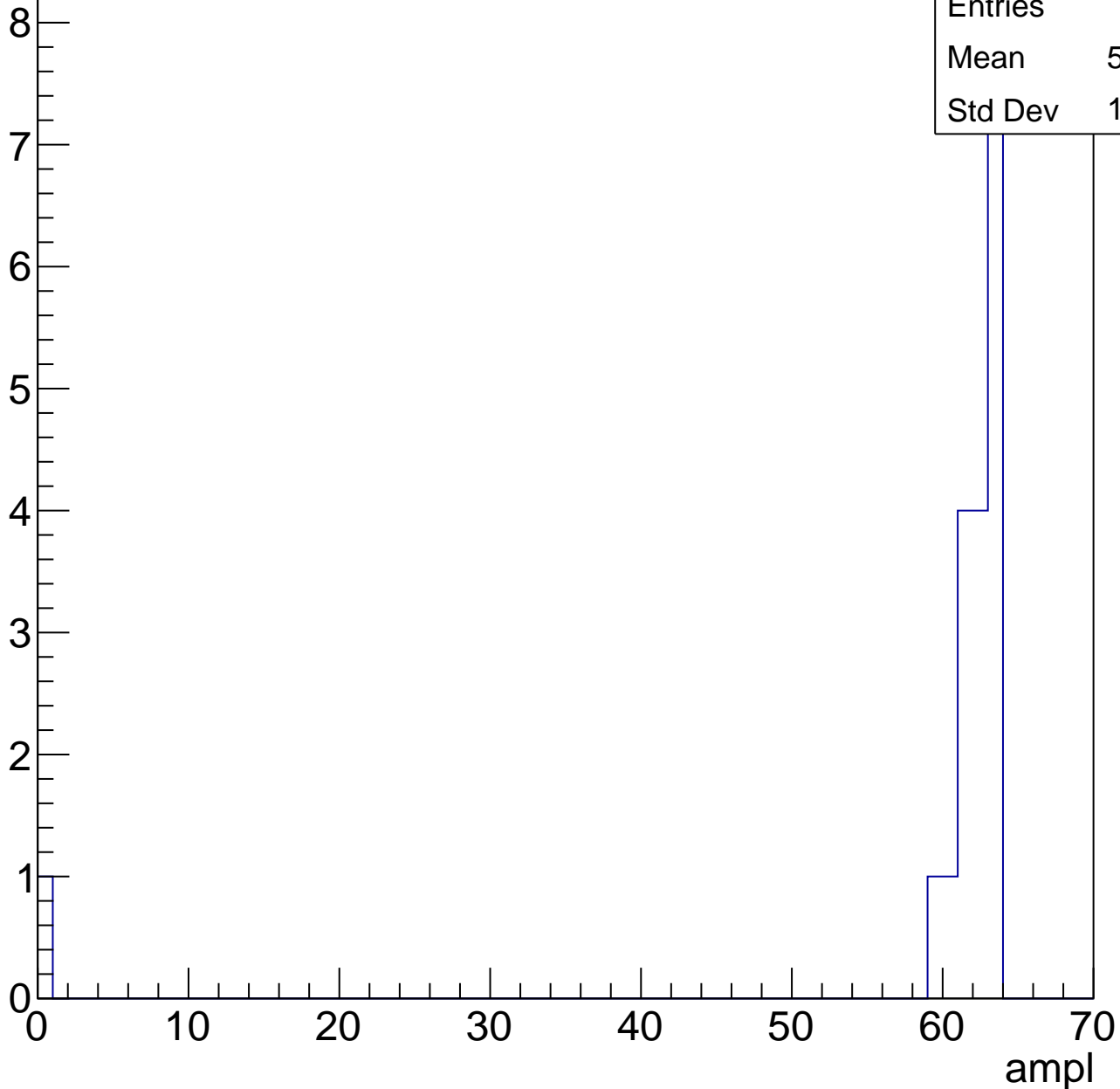


B1L102S, U14-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.68
Std Dev	13.88



B1L102S, U14-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch19, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	27.5
Std Dev	5.356

Gaus mean : 28.8131

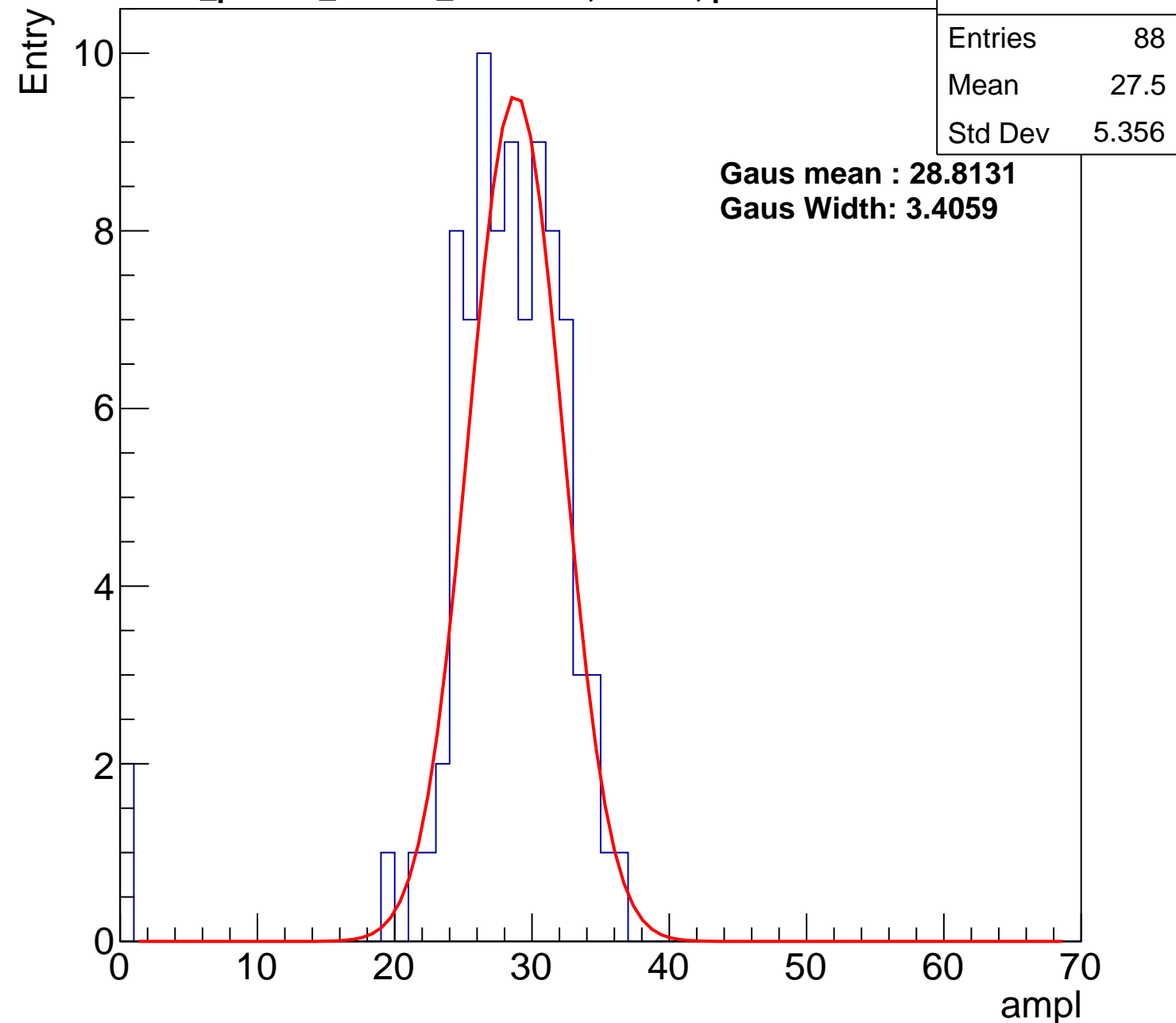
Gaus Width: 3.4059

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch19, adc1

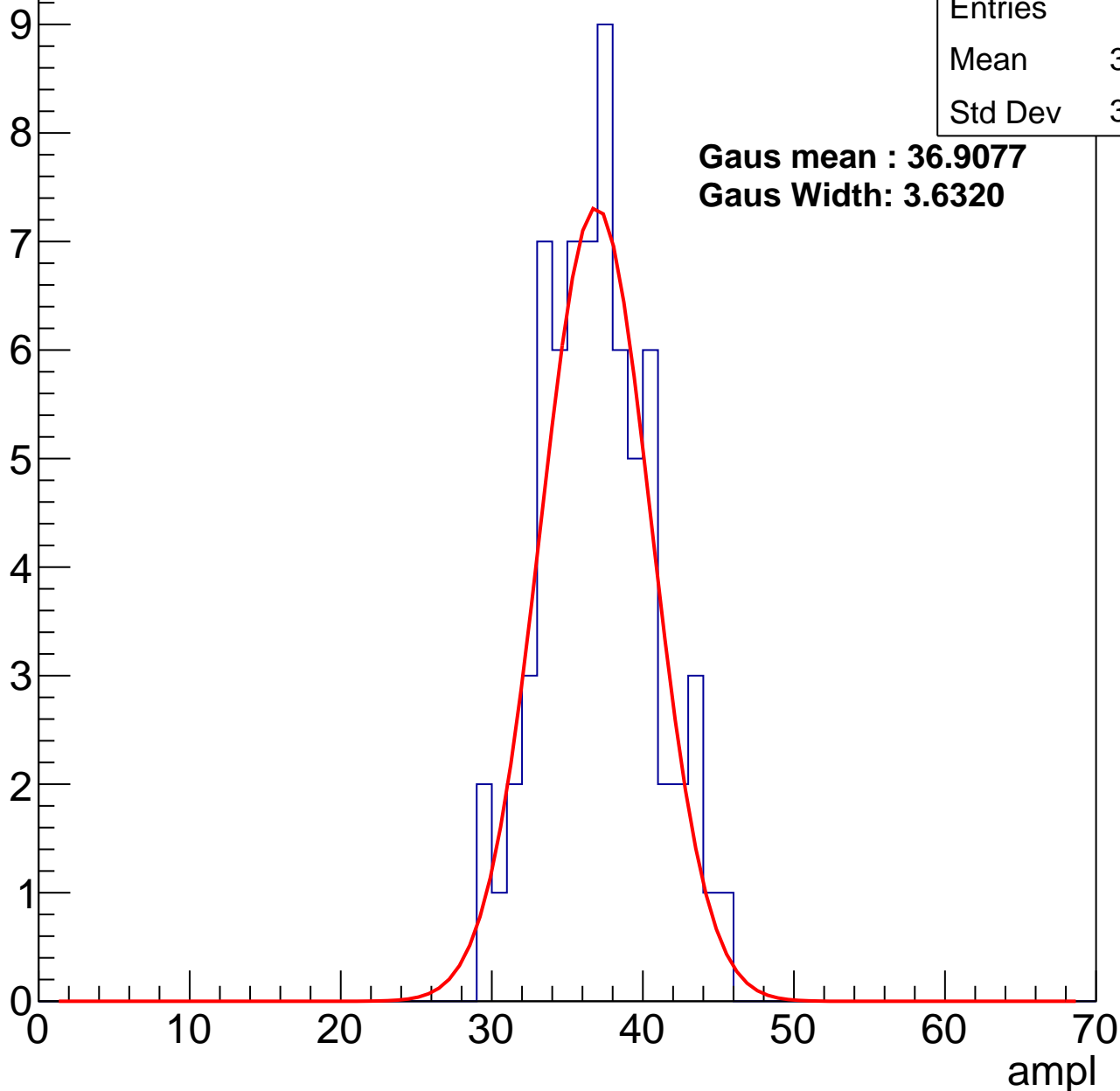
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.54
Std Dev	3.572

Gaus mean : 36.9077

Gaus Width: 3.6320



B1L102S, U14-ch19, adc2

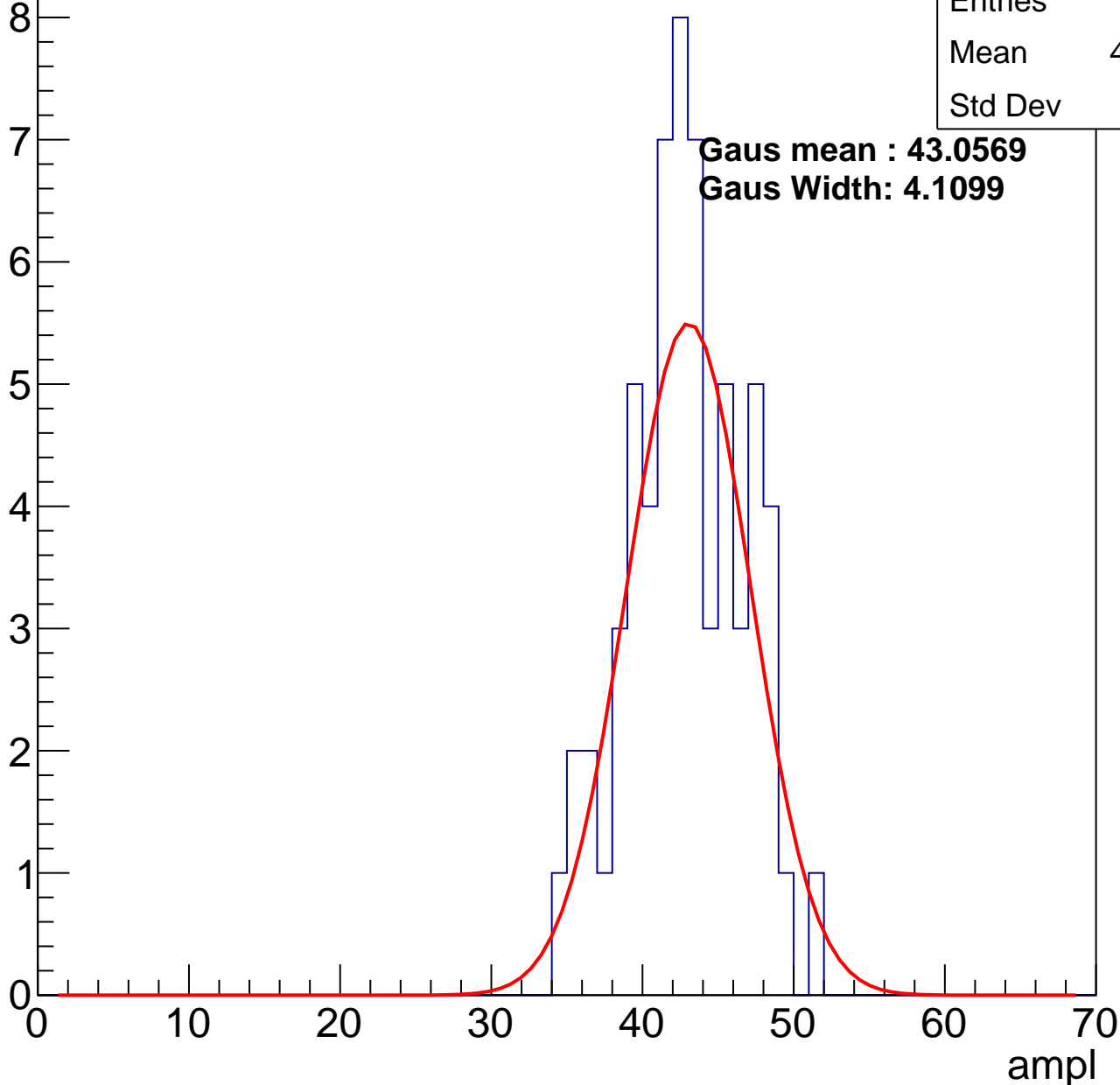
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.39
Std Dev	3.76

Gaus mean : 43.0569

Gaus Width: 4.1099

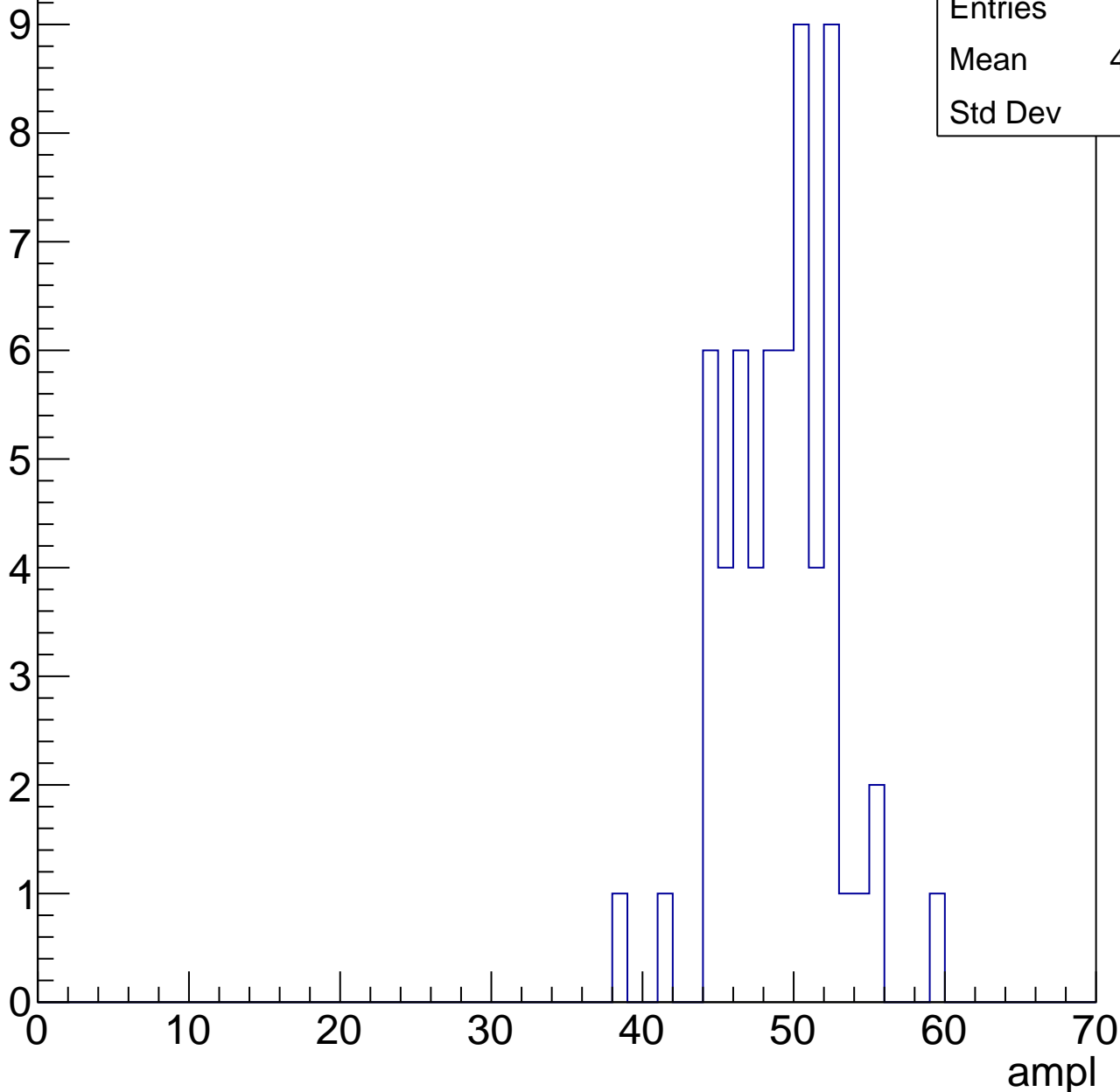


B1L102S, U14-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.64
Std Dev	3.59

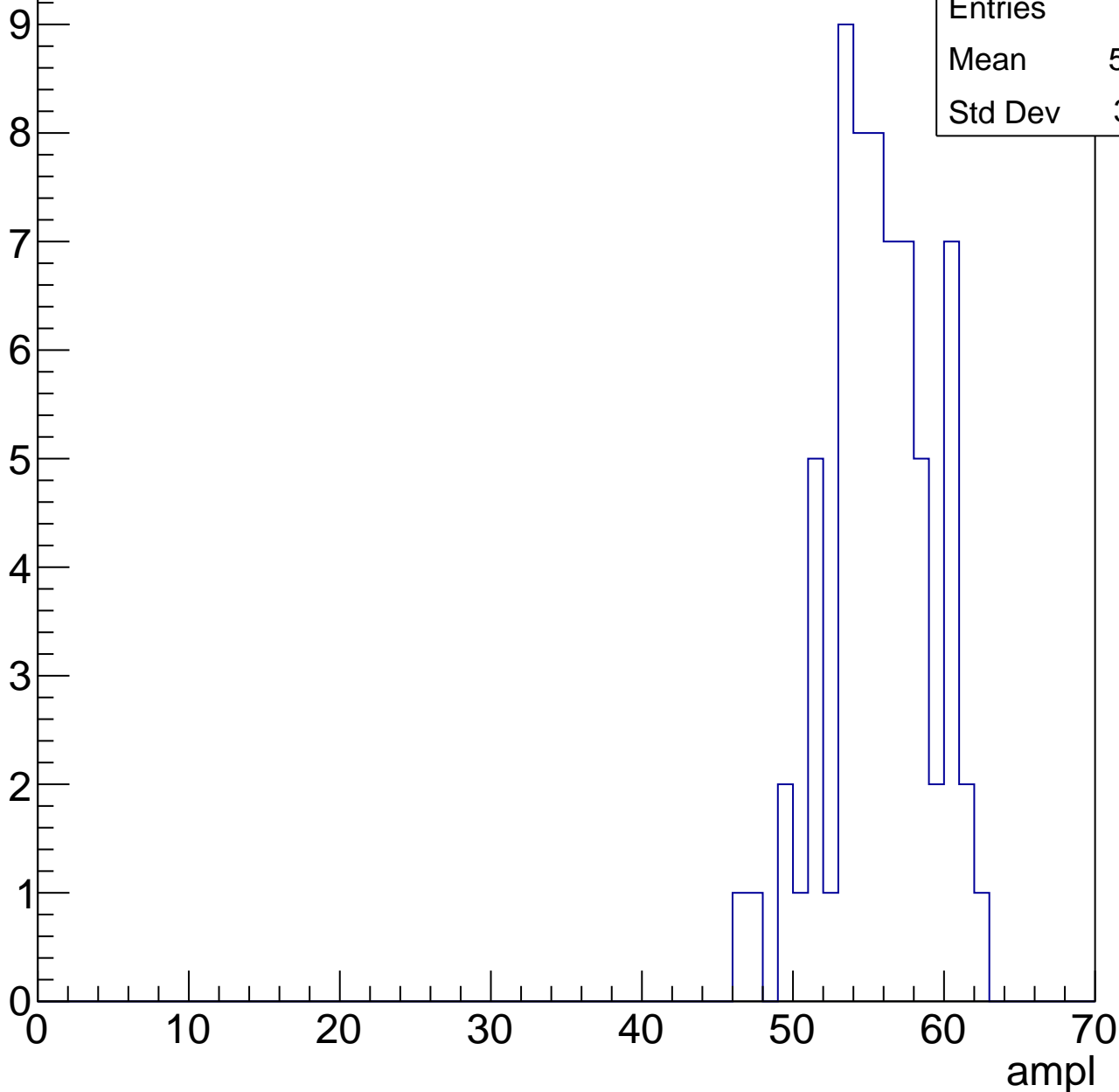


B1L102S, U14-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	55.22
Std Dev	3.411



B1L102S, U14-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

41

Mean

59.61

Std Dev

2.81

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

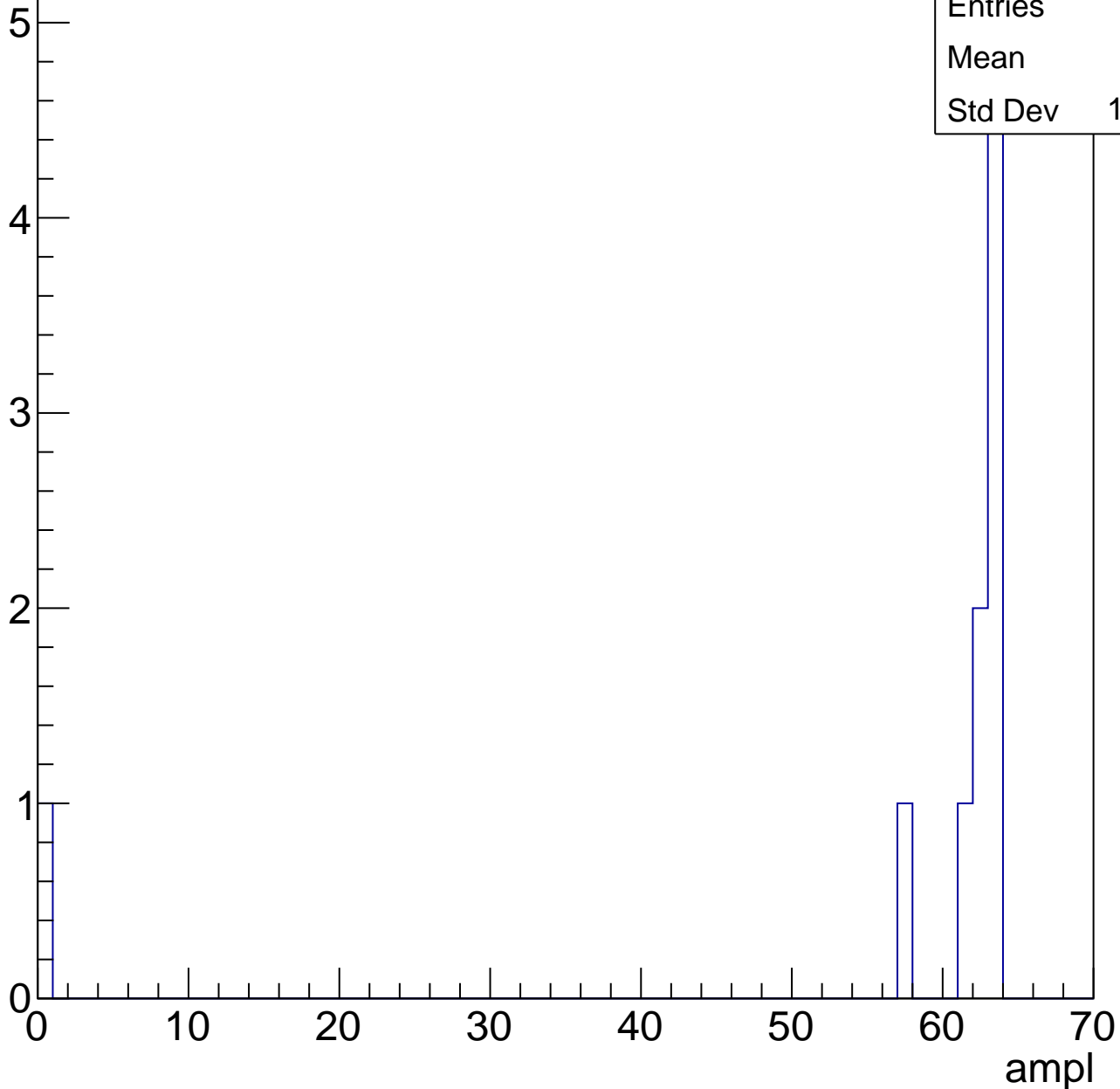
7

B1L102S, U14-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	55.7
Std Dev	18.65



B1L102S, U14-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U14-ch20, adc0

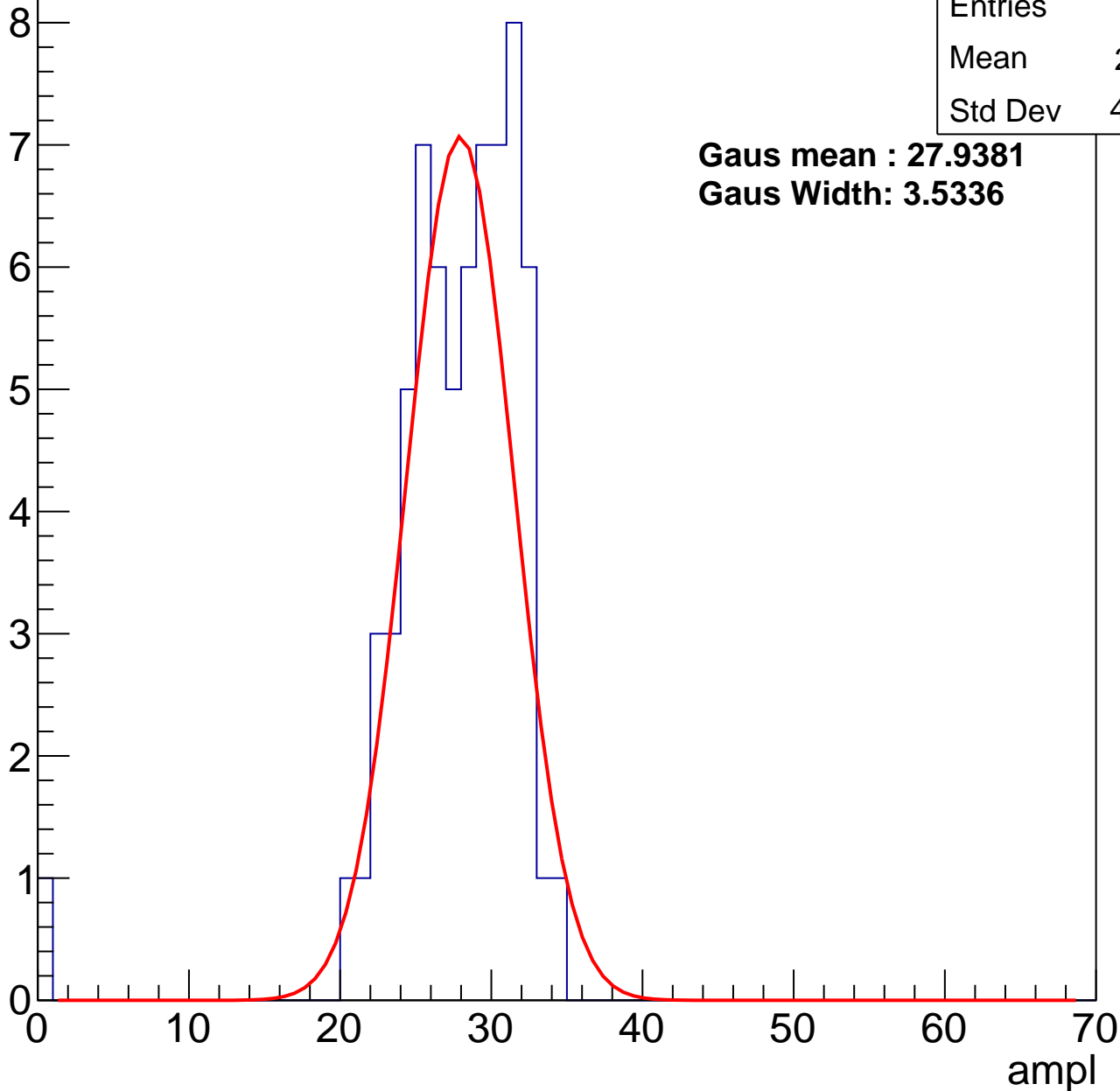
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.21
Std Dev	4.654

Gaus mean : 27.9381

Gaus Width: 3.5336



B1L102S, U14-ch20, adc1

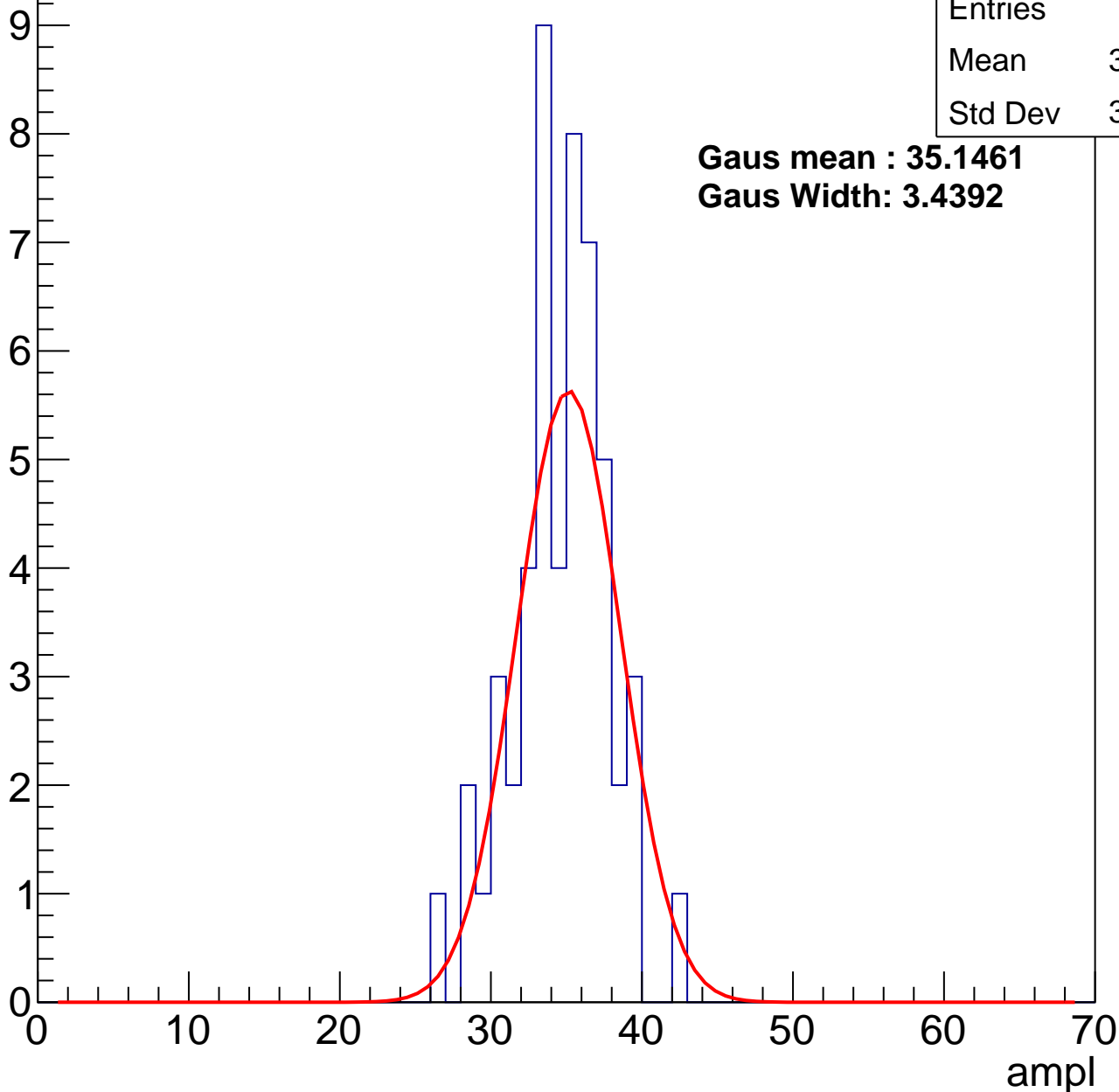
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.15
Std Dev	3.109

Gaus mean : 35.1461

Gaus Width: 3.4392



B1L102S, U14-ch20, adc2

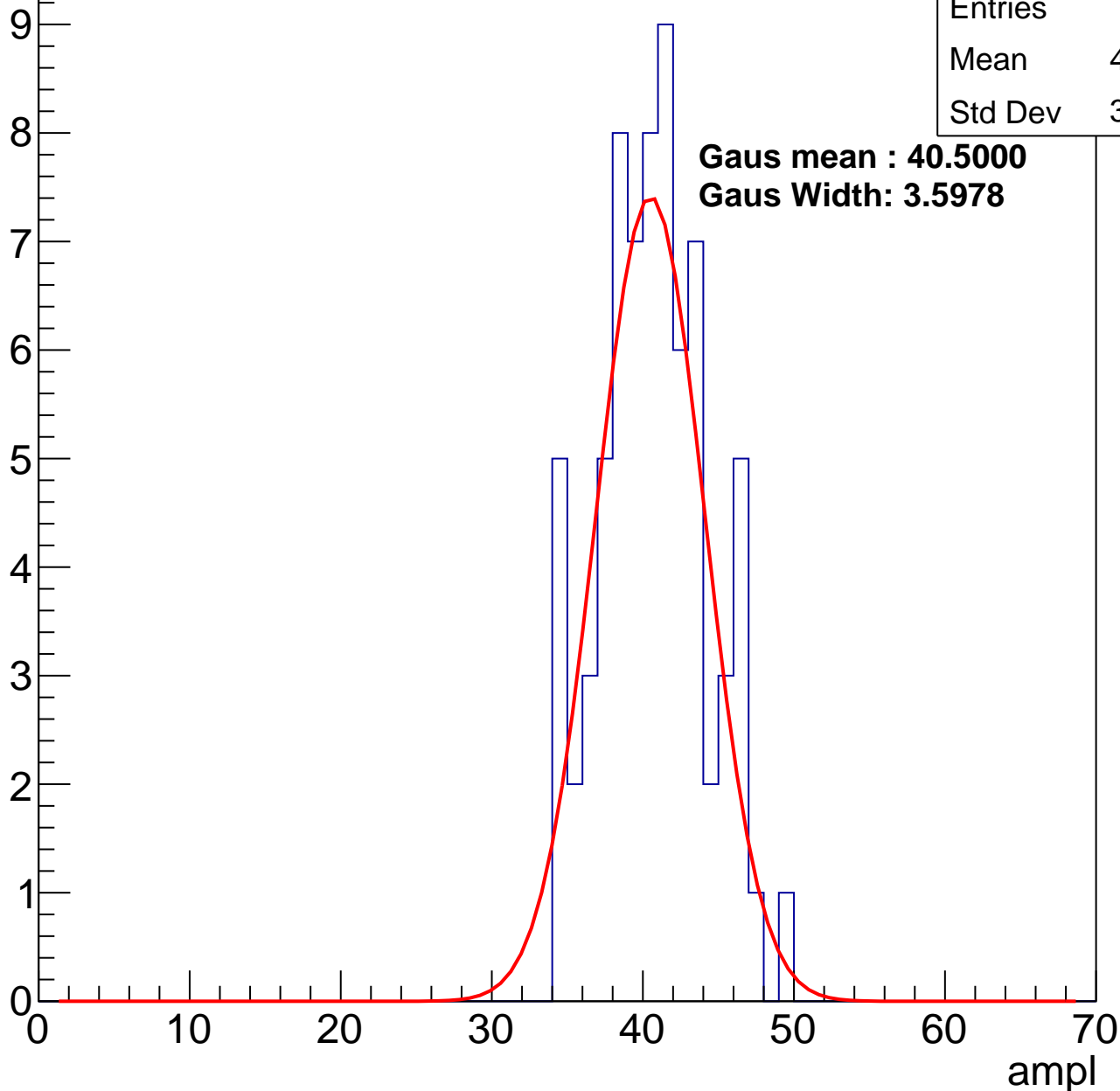
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	40.29
Std Dev	3.474

Gaus mean : 40.5000

Gaus Width: 3.5978

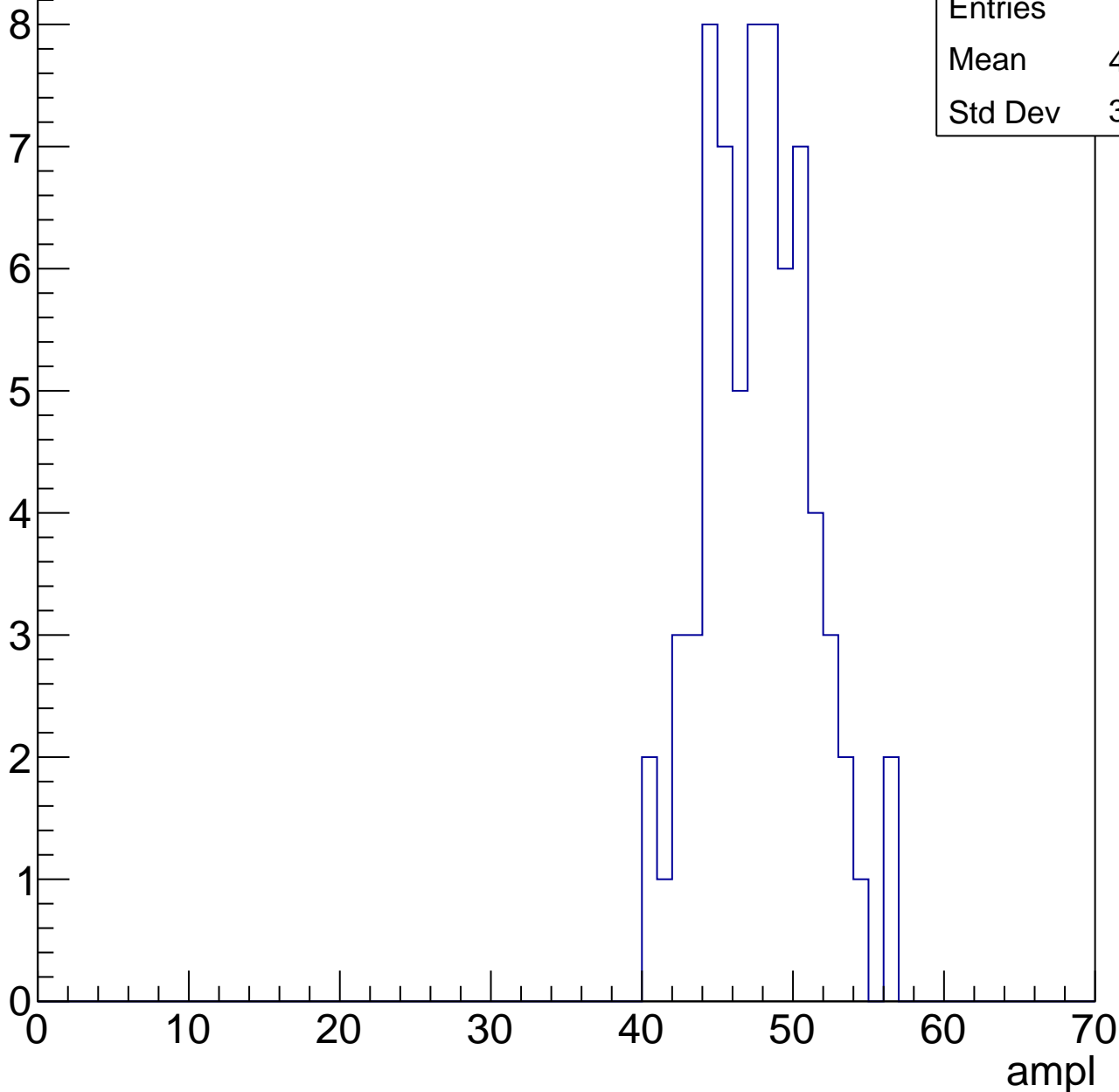


B1L102S, U14-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.27
Std Dev	3.533

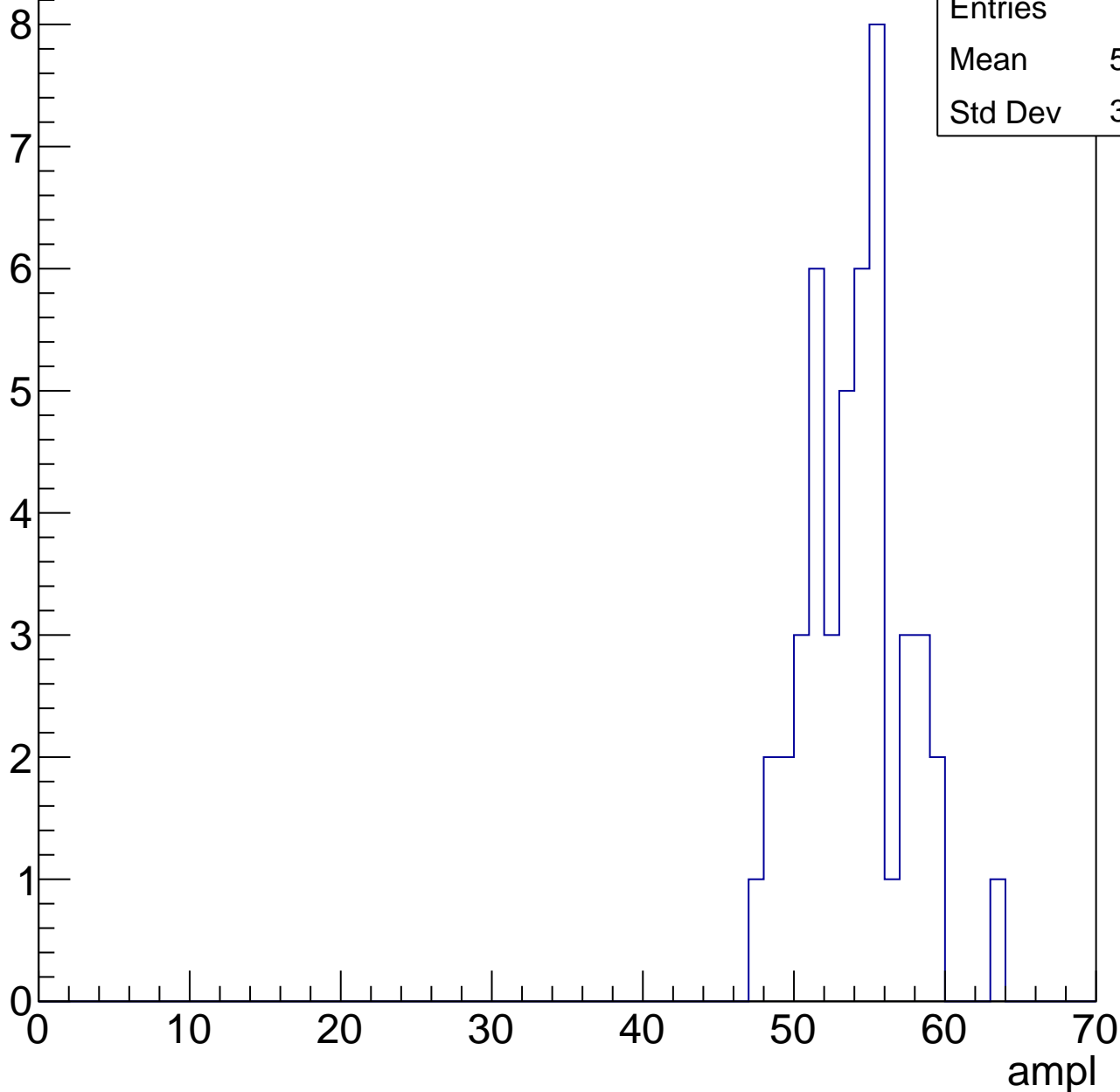


B1L102S, U14-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	53.57
Std Dev	3.295

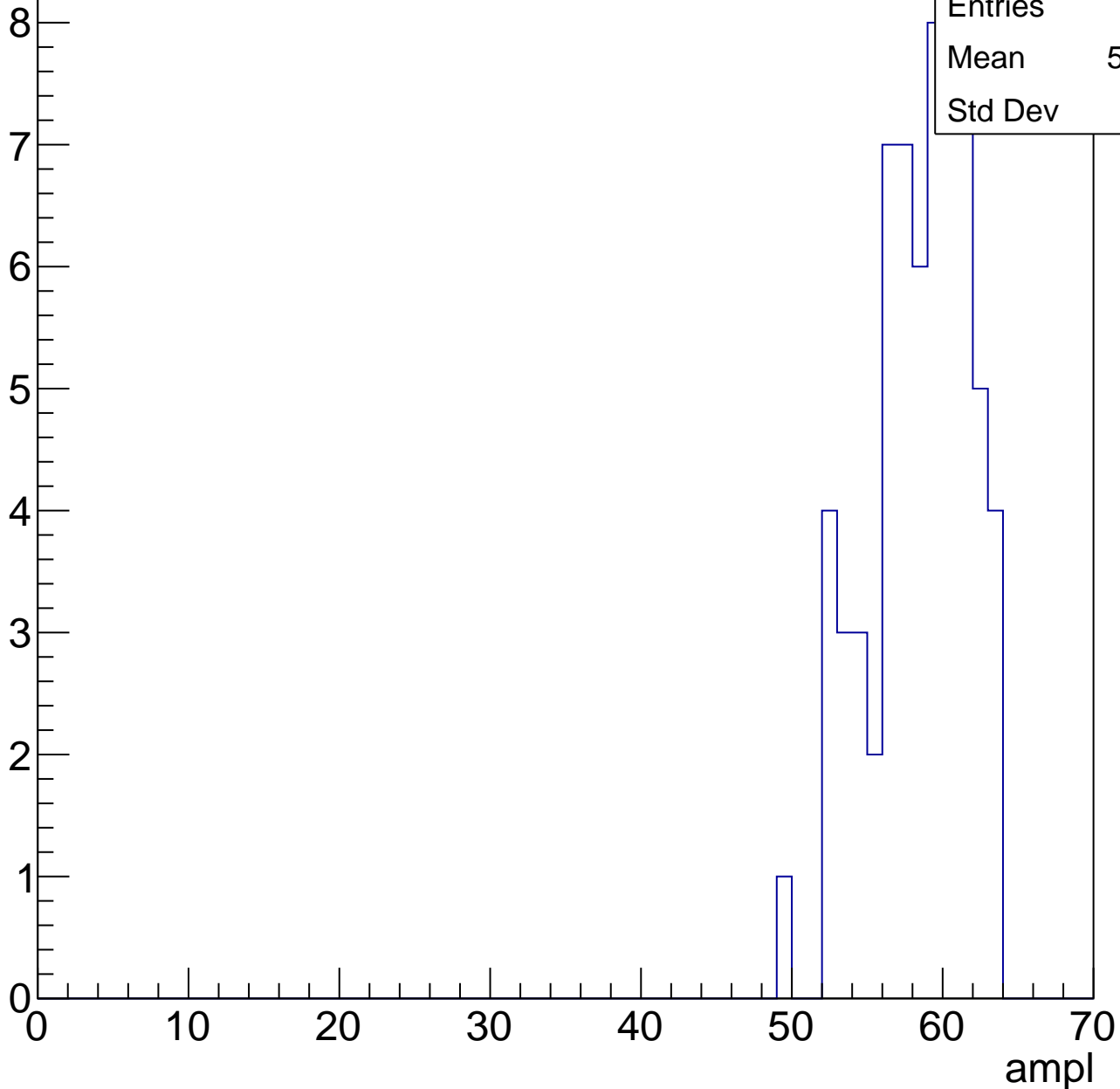


B1L102S, U14-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	58.02
Std Dev	3.24

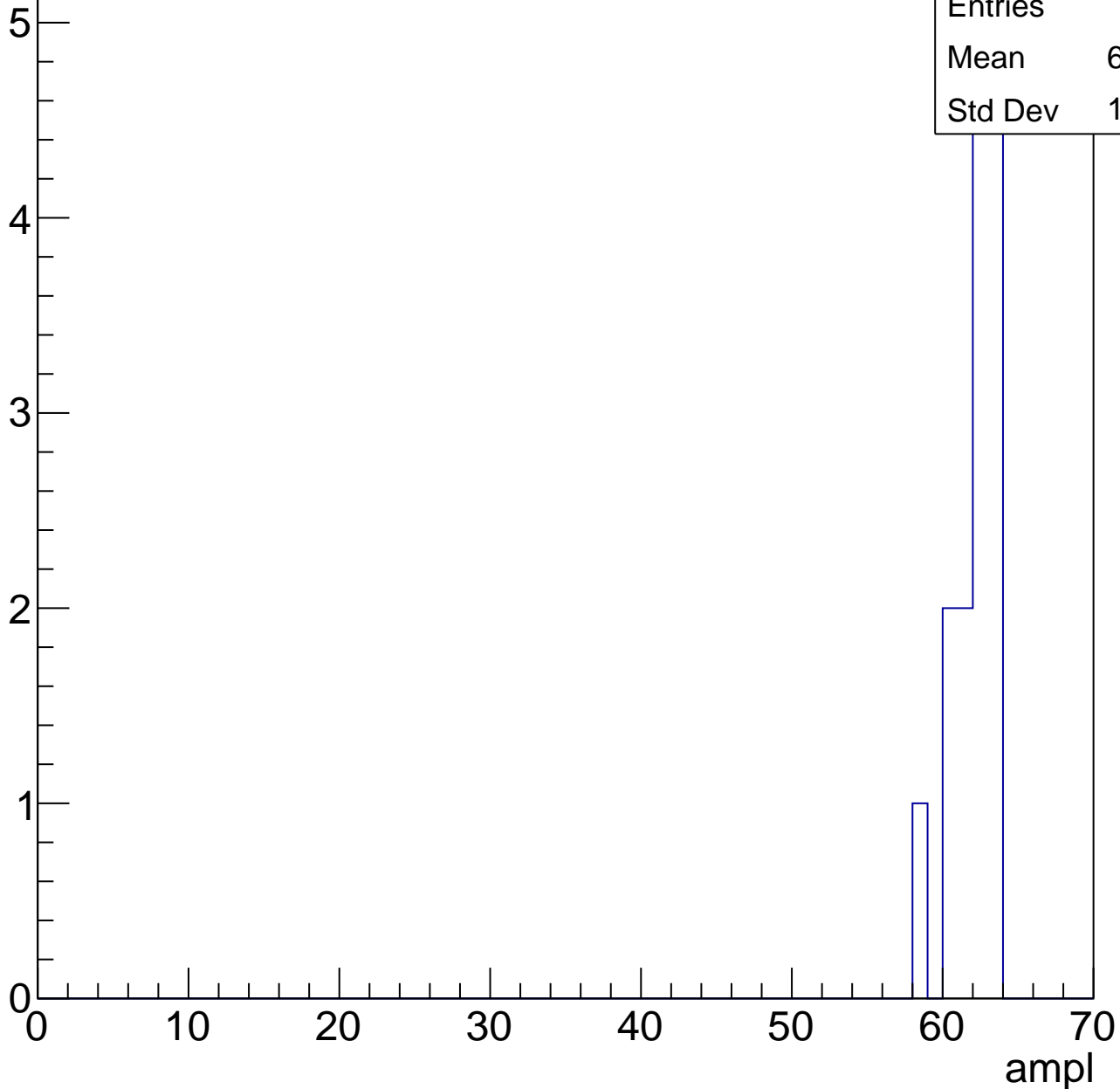


B1L102S, U14-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.67
Std Dev	1.398



B1L102S, U14-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch21, adc0

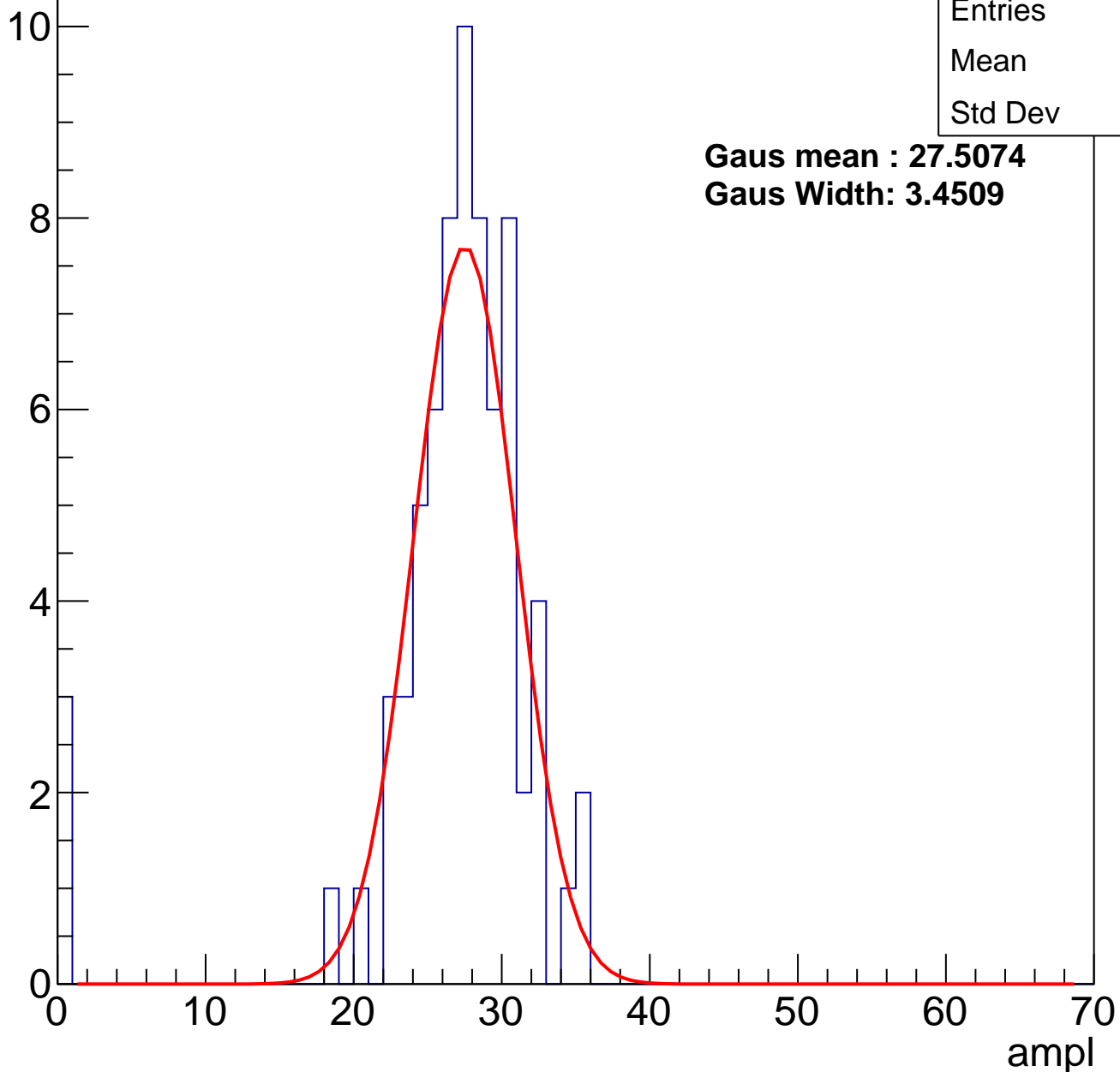
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	26.1
Std Dev	6.36

Gaus mean : 27.5074

Gaus Width: 3.4509

Entry



B1L102S, U14-ch21, adc1

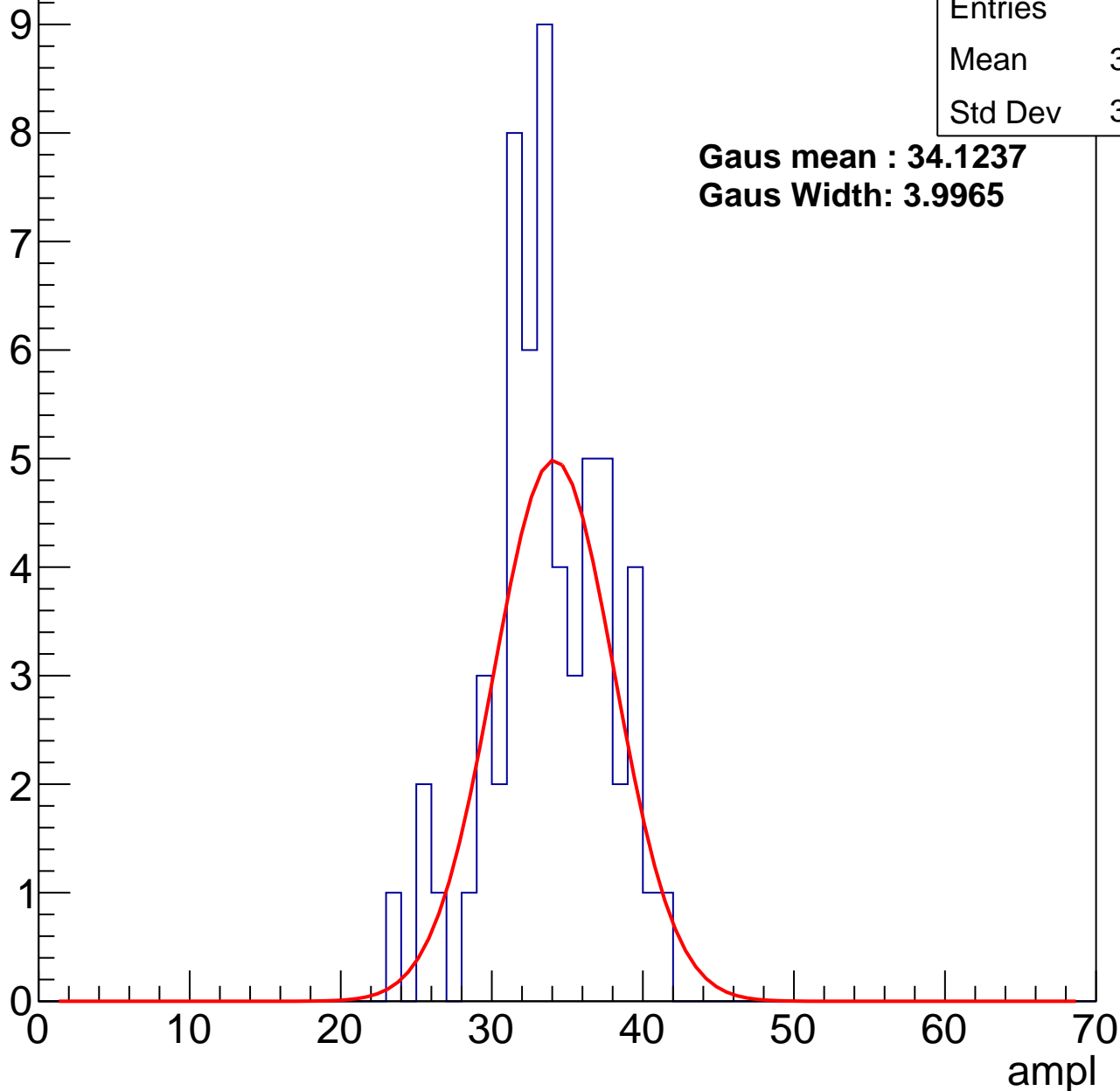
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	33.28
Std Dev	3.827

Gaus mean : 34.1237

Gaus Width: 3.9965



B1L102S, U14-ch21, adc2

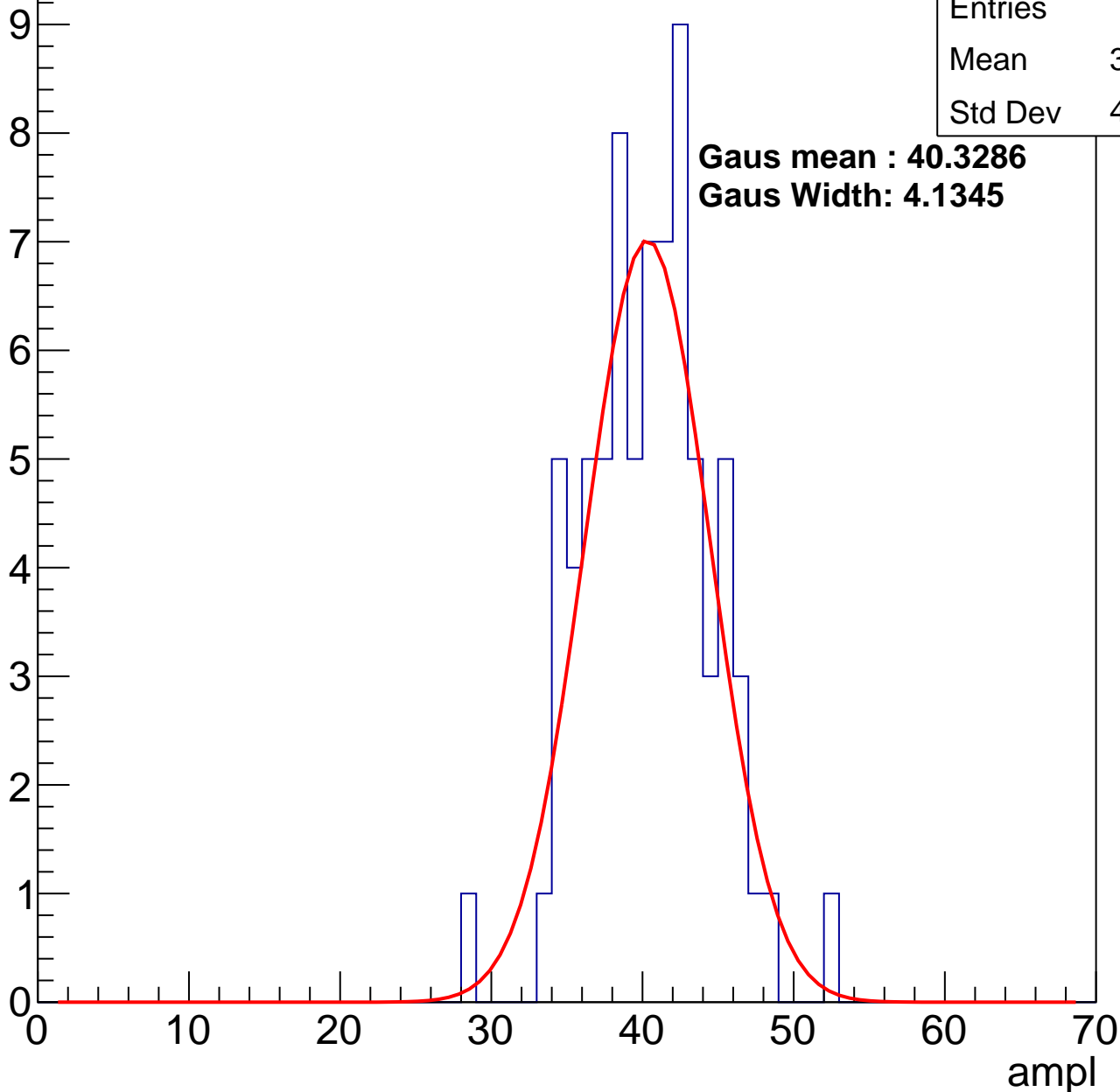
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	39.96
Std Dev	4.073

Gaus mean : 40.3286

Gaus Width: 4.1345

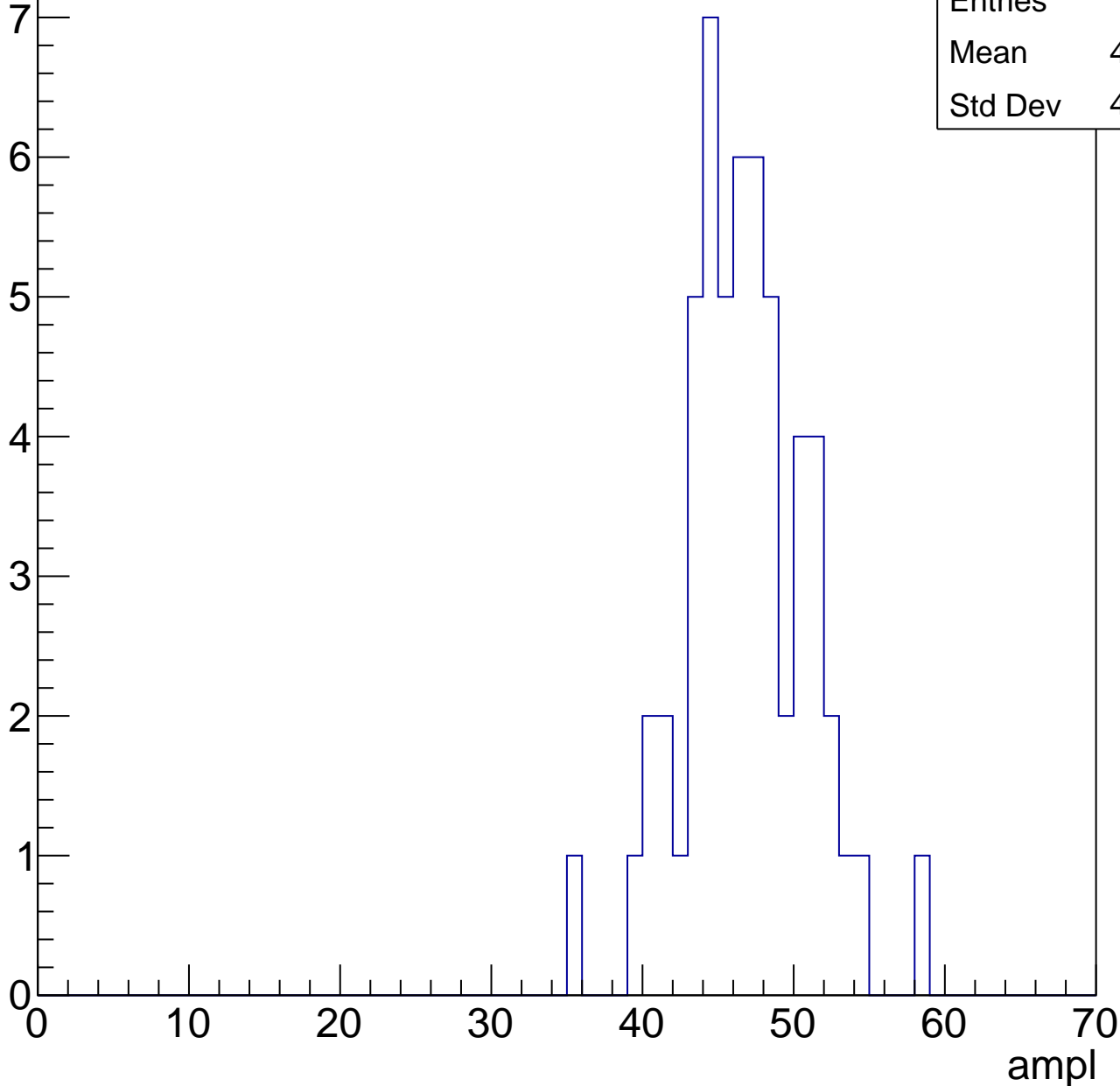


B1L102S, U14-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	46.34
Std Dev	4.046

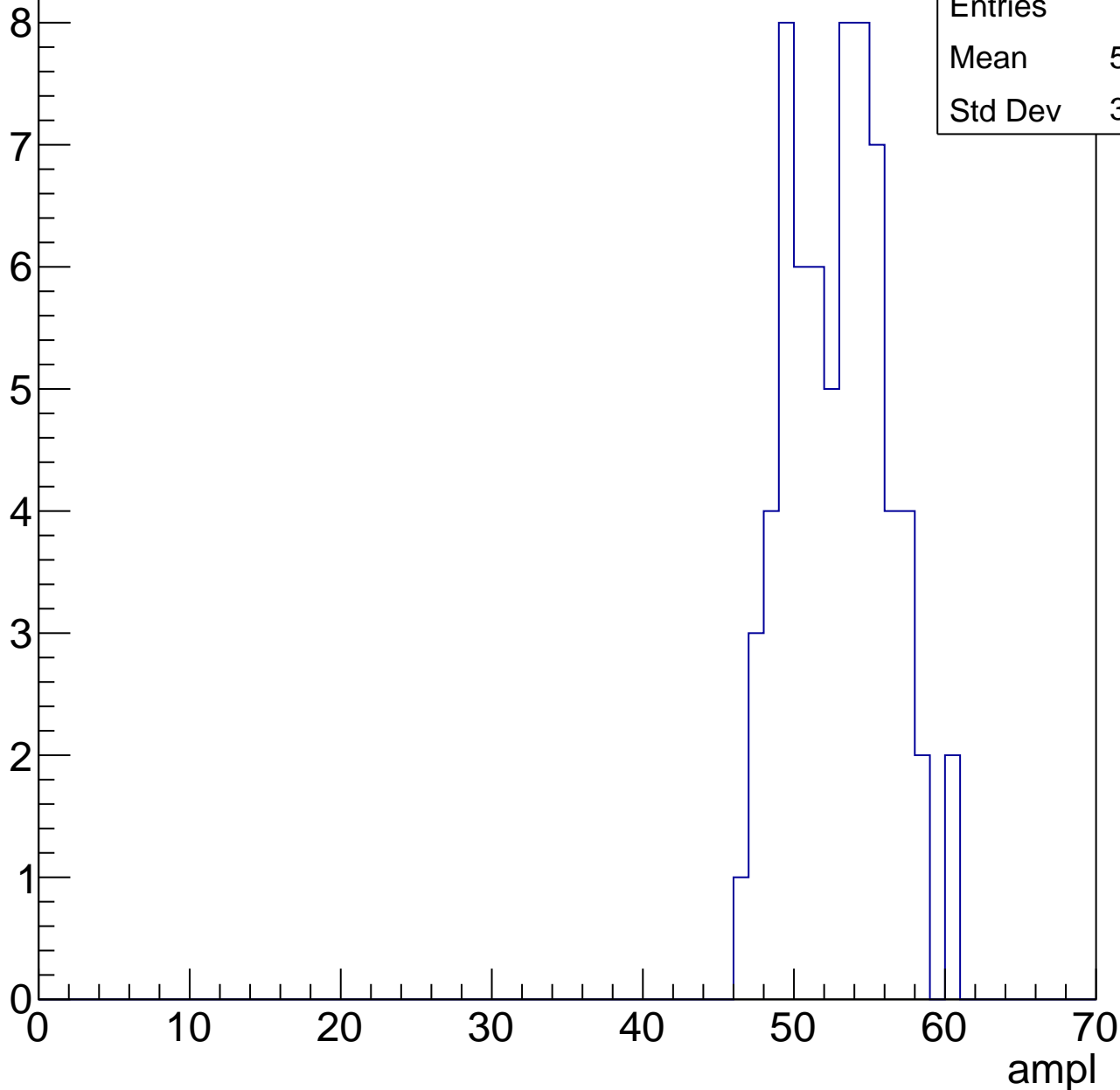


B1L102S, U14-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	52.44
Std Dev	3.278

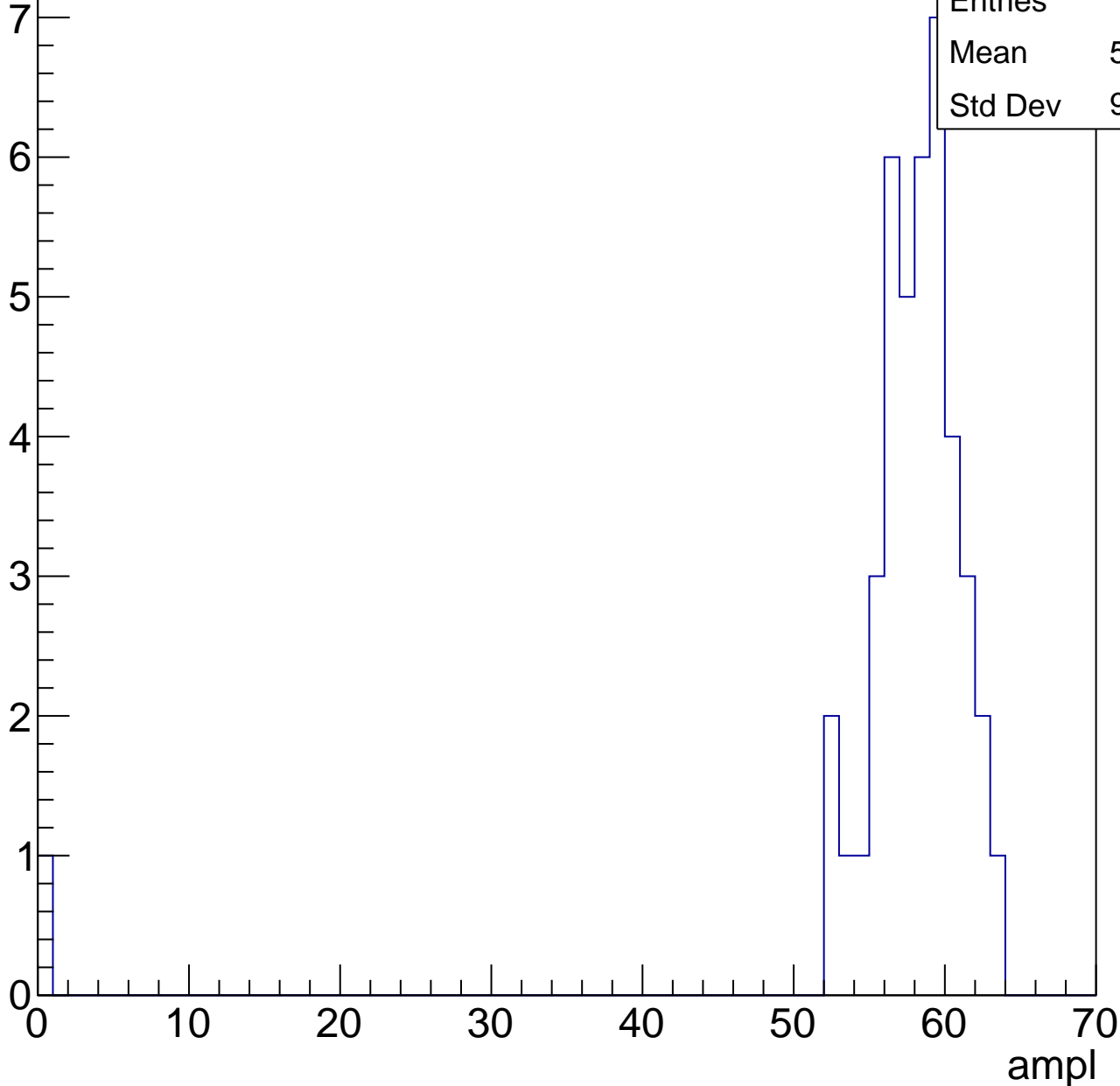


B1L102S, U14-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

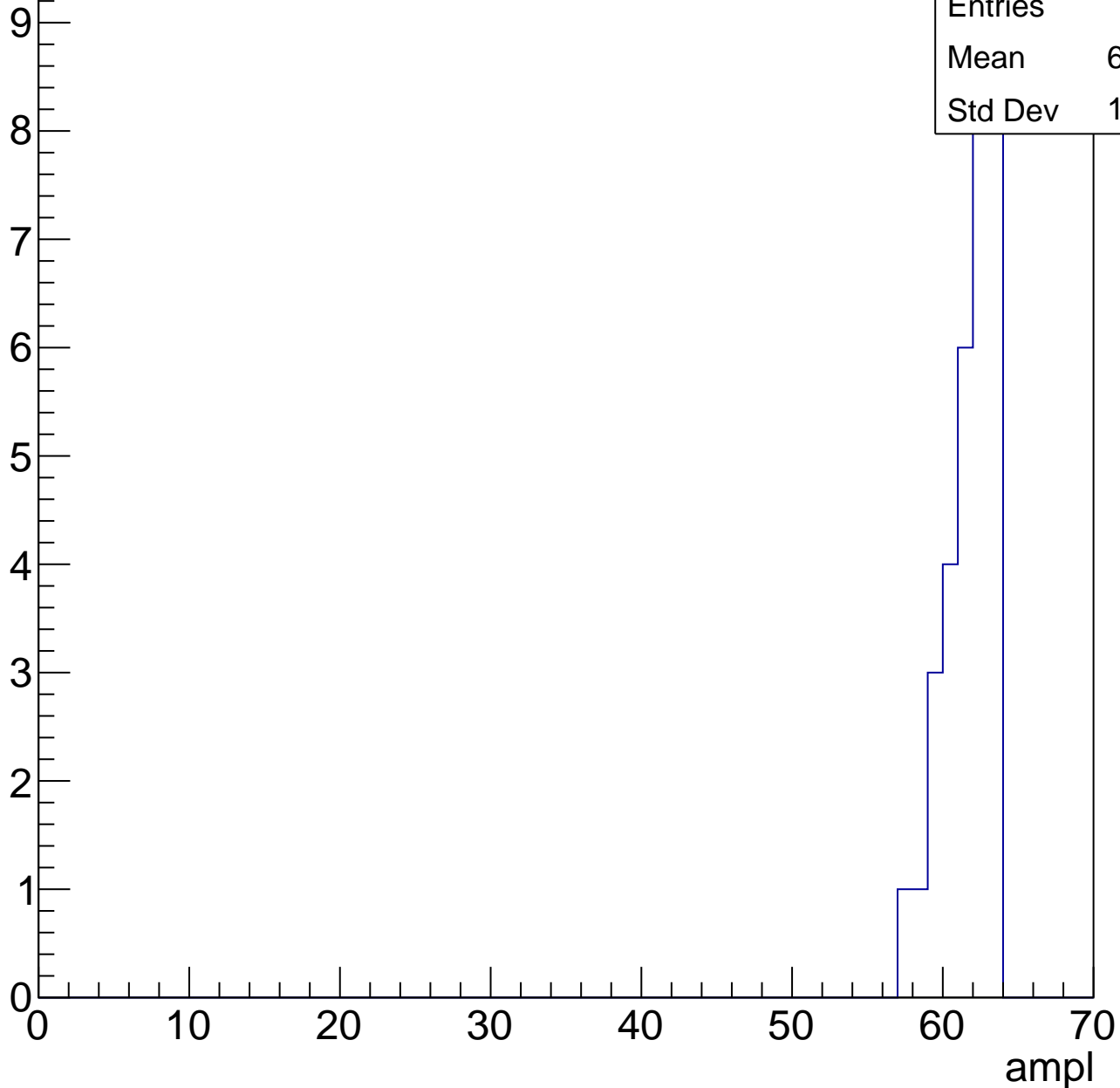
Entries	42
Mean	56.38
Std Dev	9.168



B1L102S, U14-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

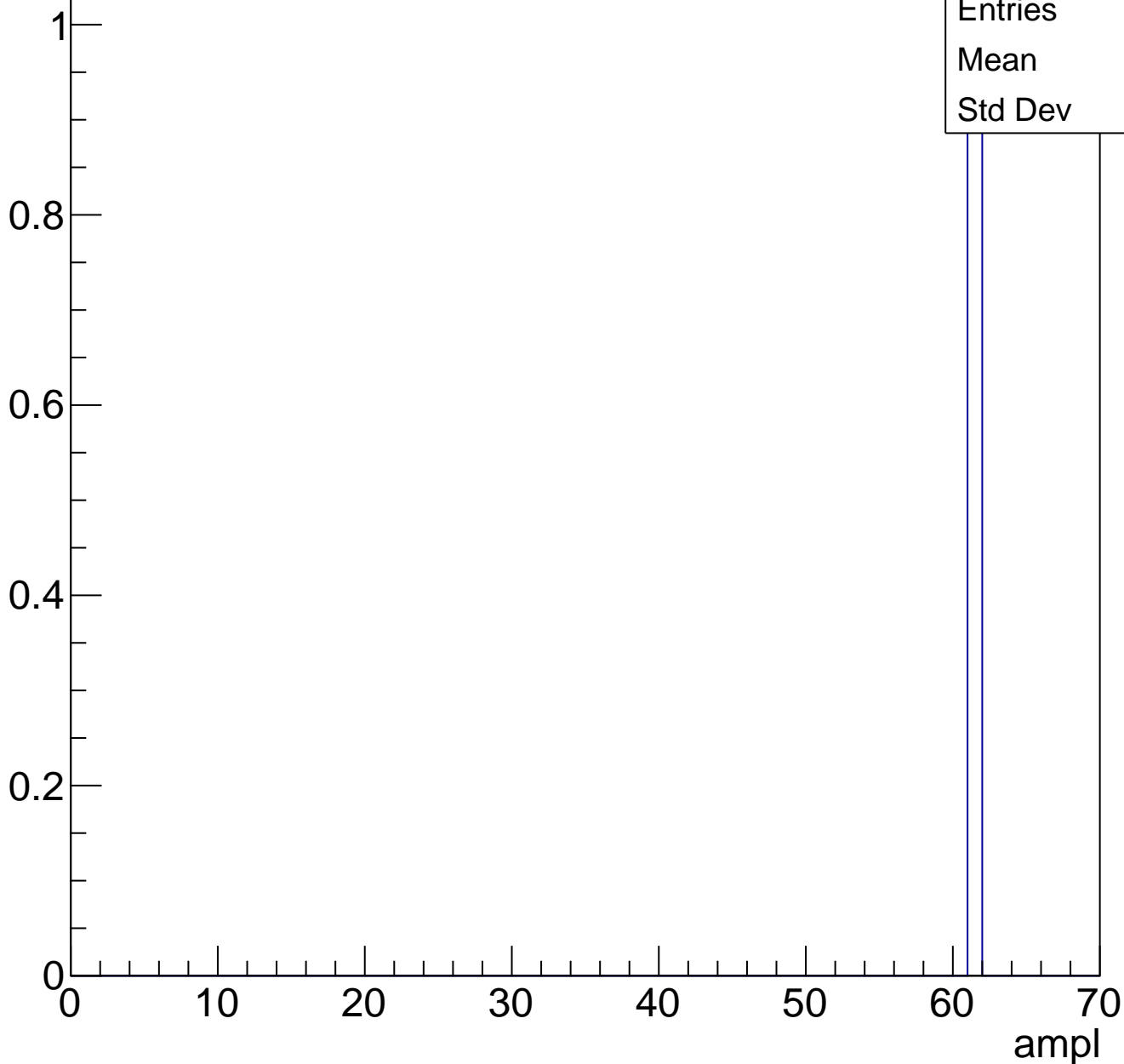
Entry



B1L102S, U14-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch22, adc0

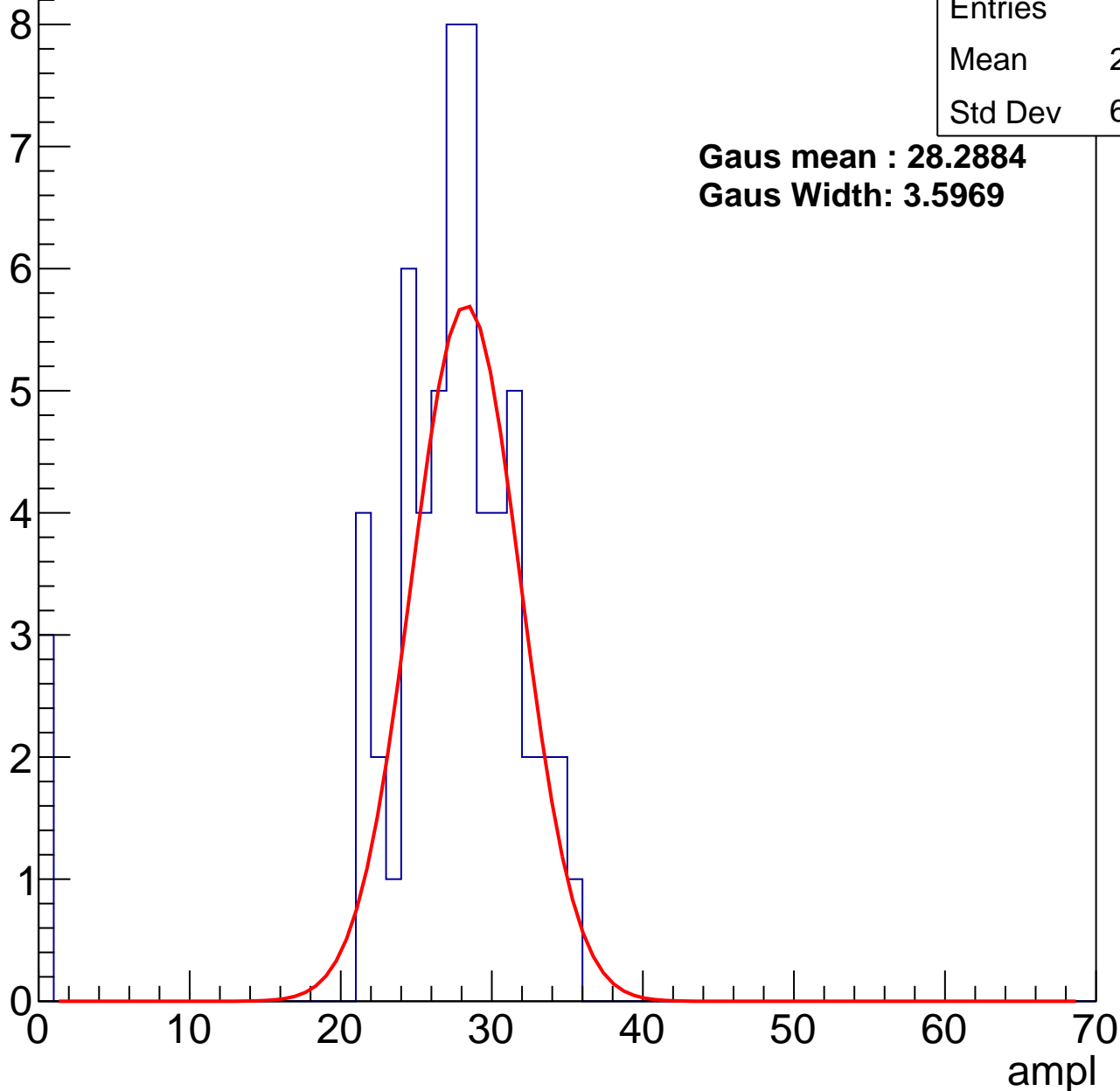
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	26.05
Std Dev	6.824

Gaus mean : 28.2884

Gaus Width: 3.5969



B1L102S, U14-ch22, adc1

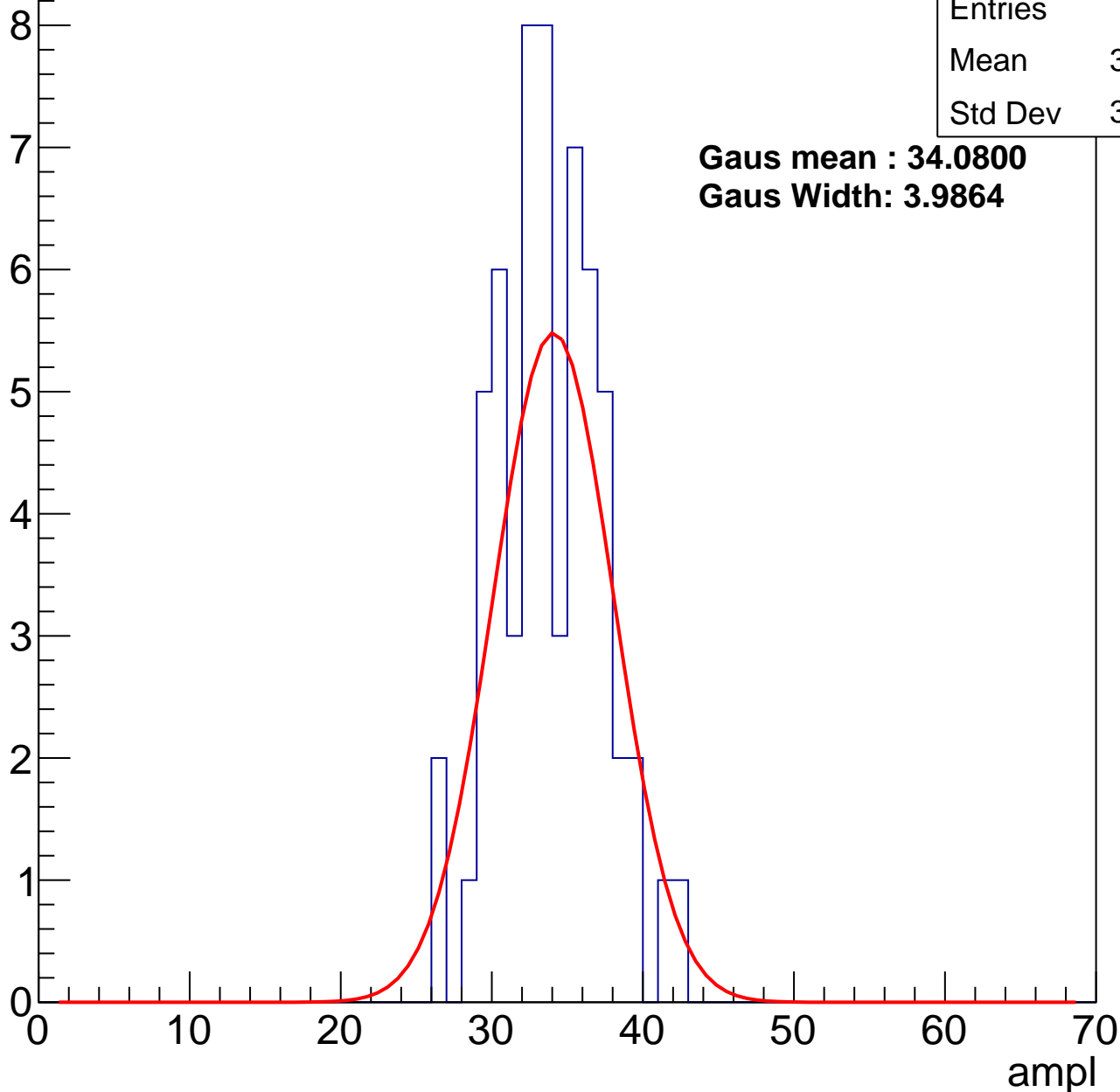
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	33.38
Std Dev	3.416

Gaus mean : 34.0800

Gaus Width: 3.9864



B1L102S, U14-ch22, adc2

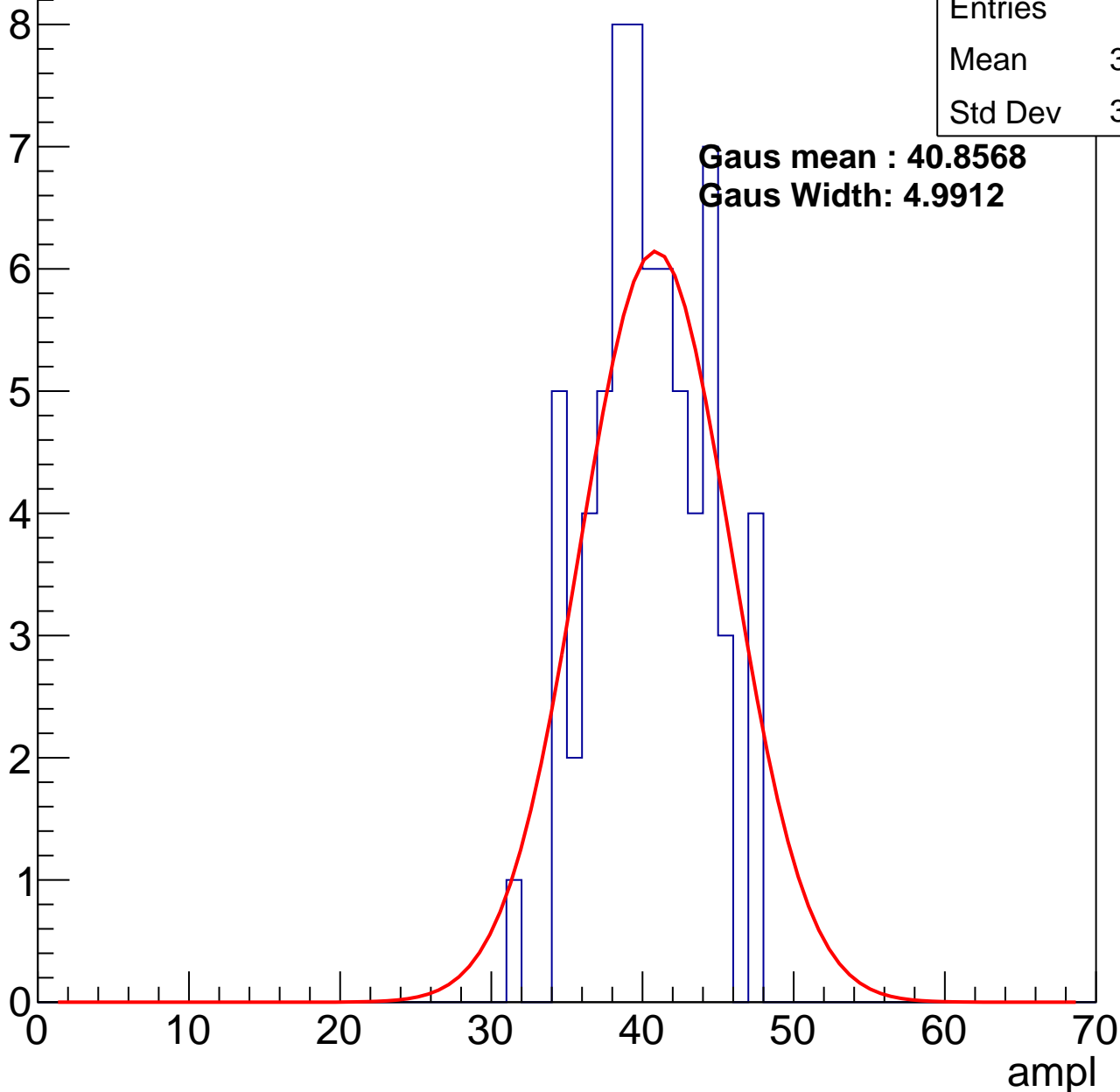
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	39.93
Std Dev	3.647

Gaus mean : 40.8568

Gaus Width: 4.9912

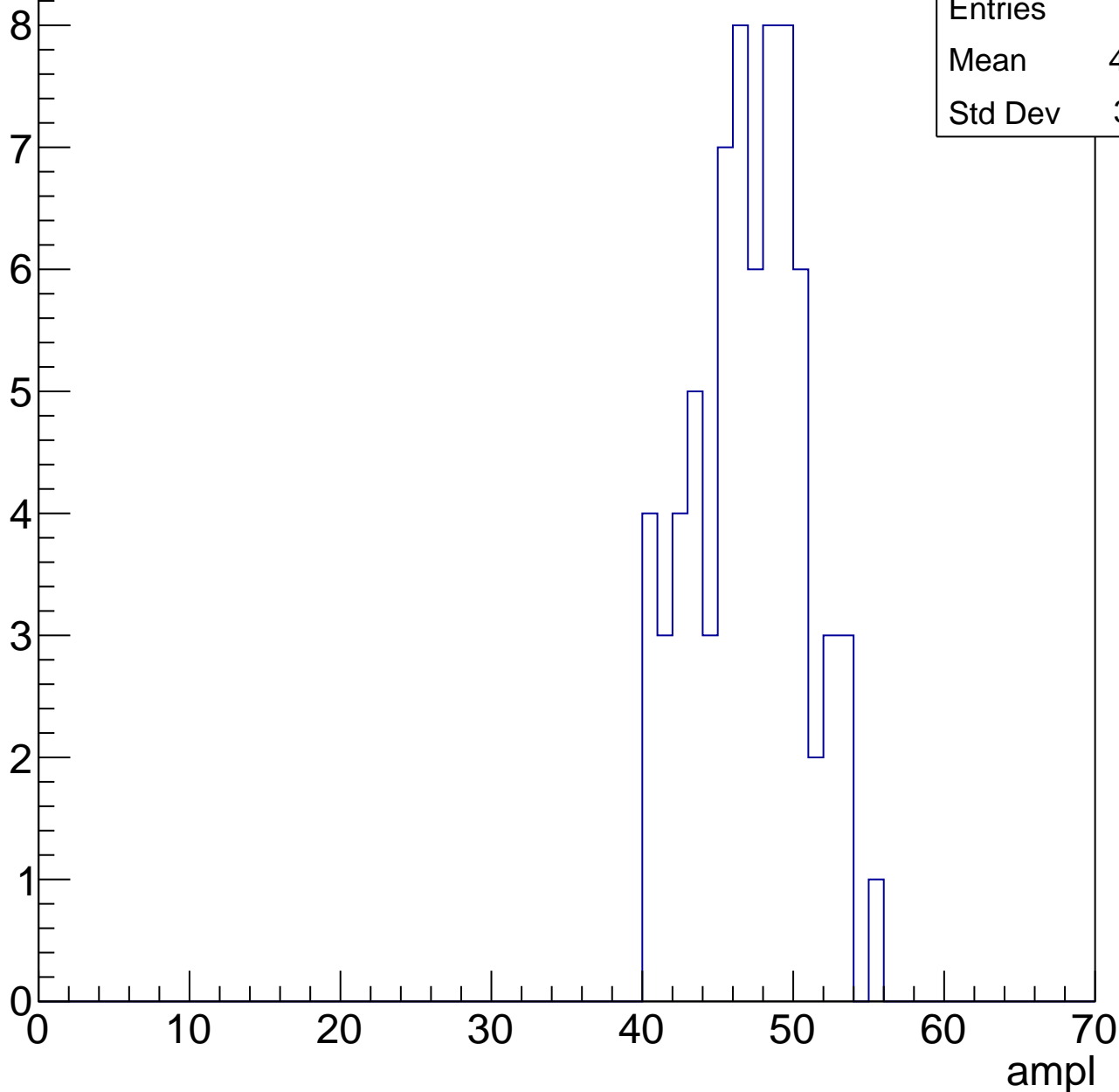


B1L102S, U14-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	46.63
Std Dev	3.581

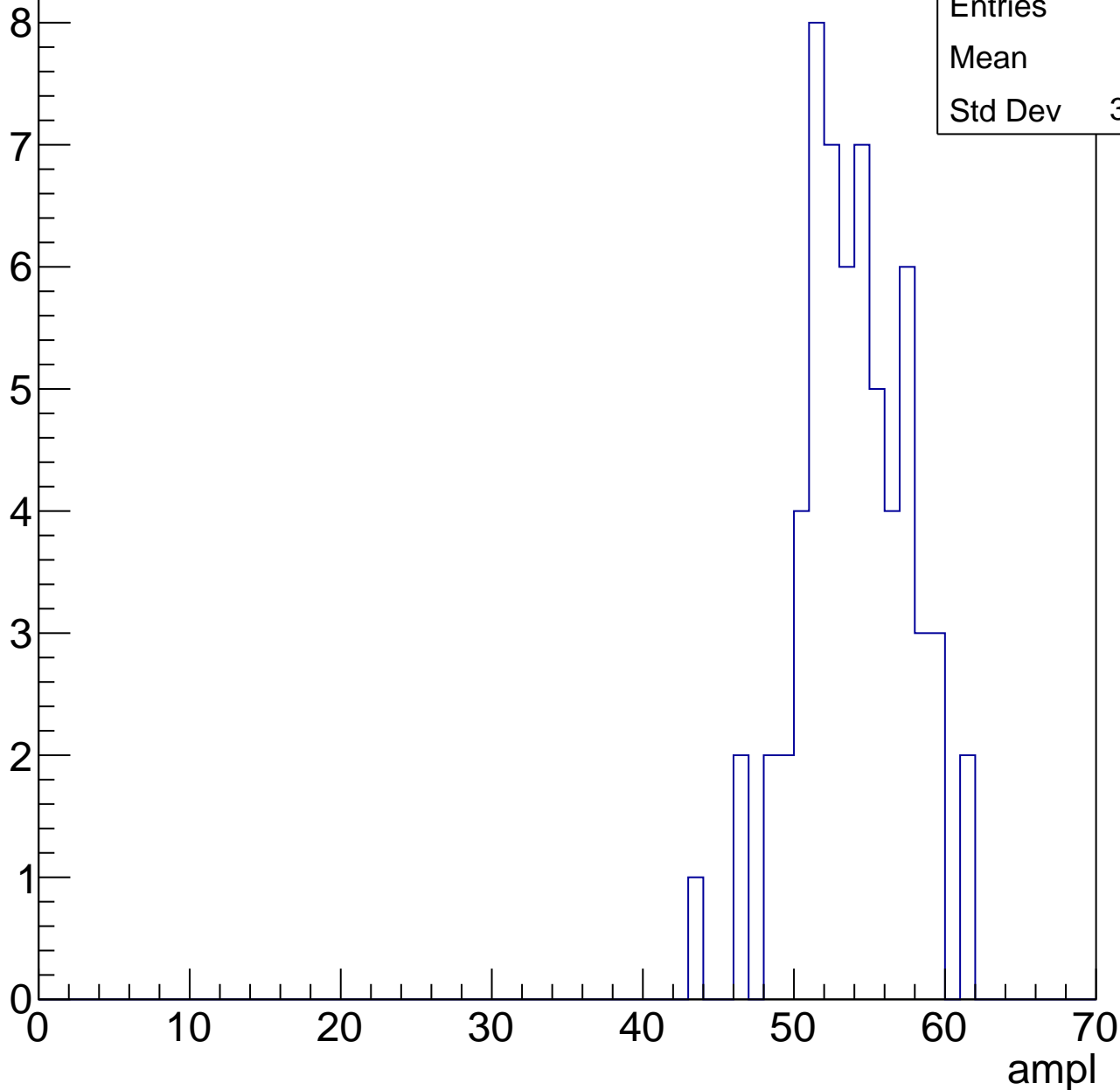


B1L102S, U14-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	53.4
Std Dev	3.625



B1L102S, U14-ch22, adc5

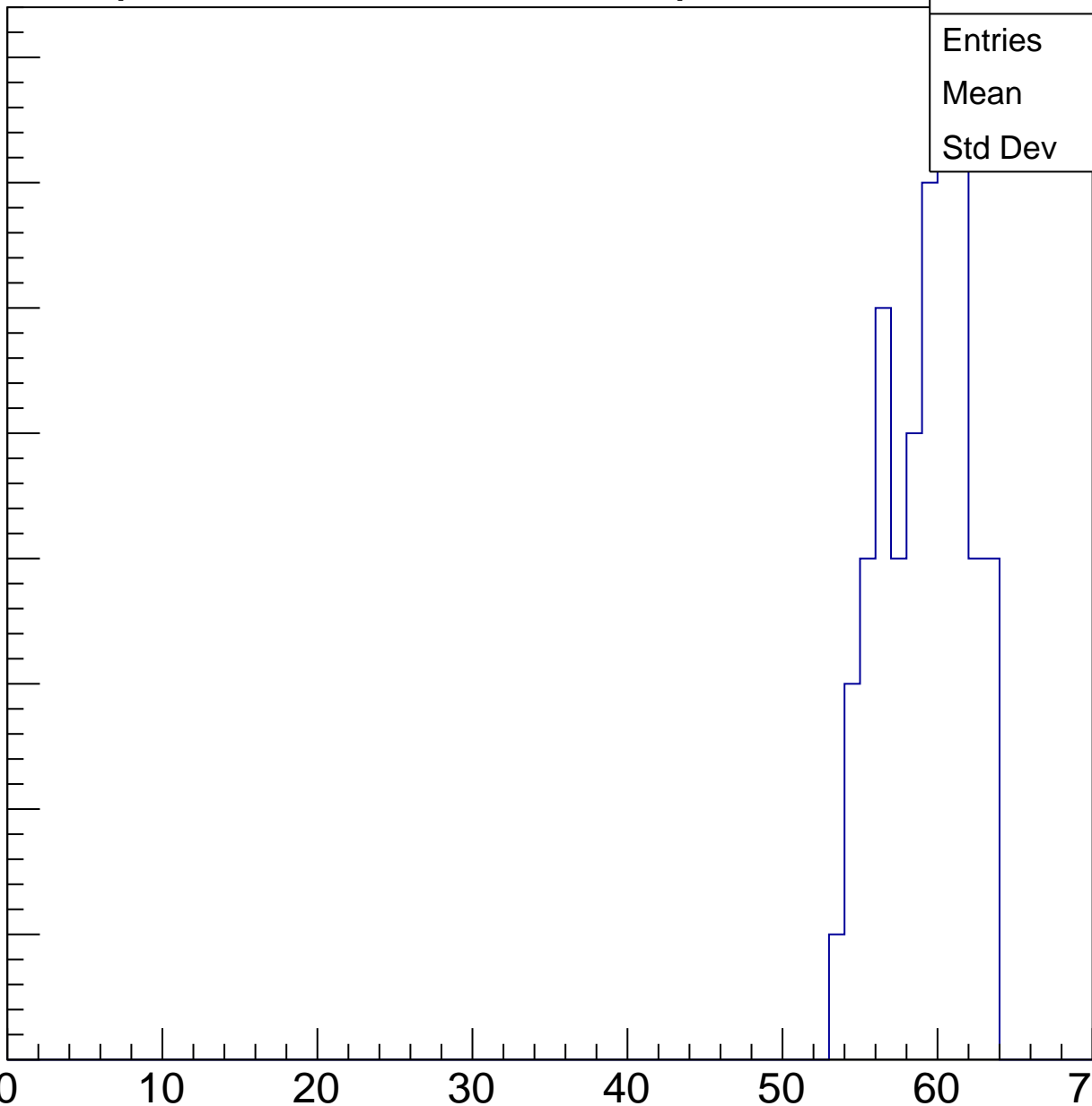
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.7
Std Dev	2.671

ampl

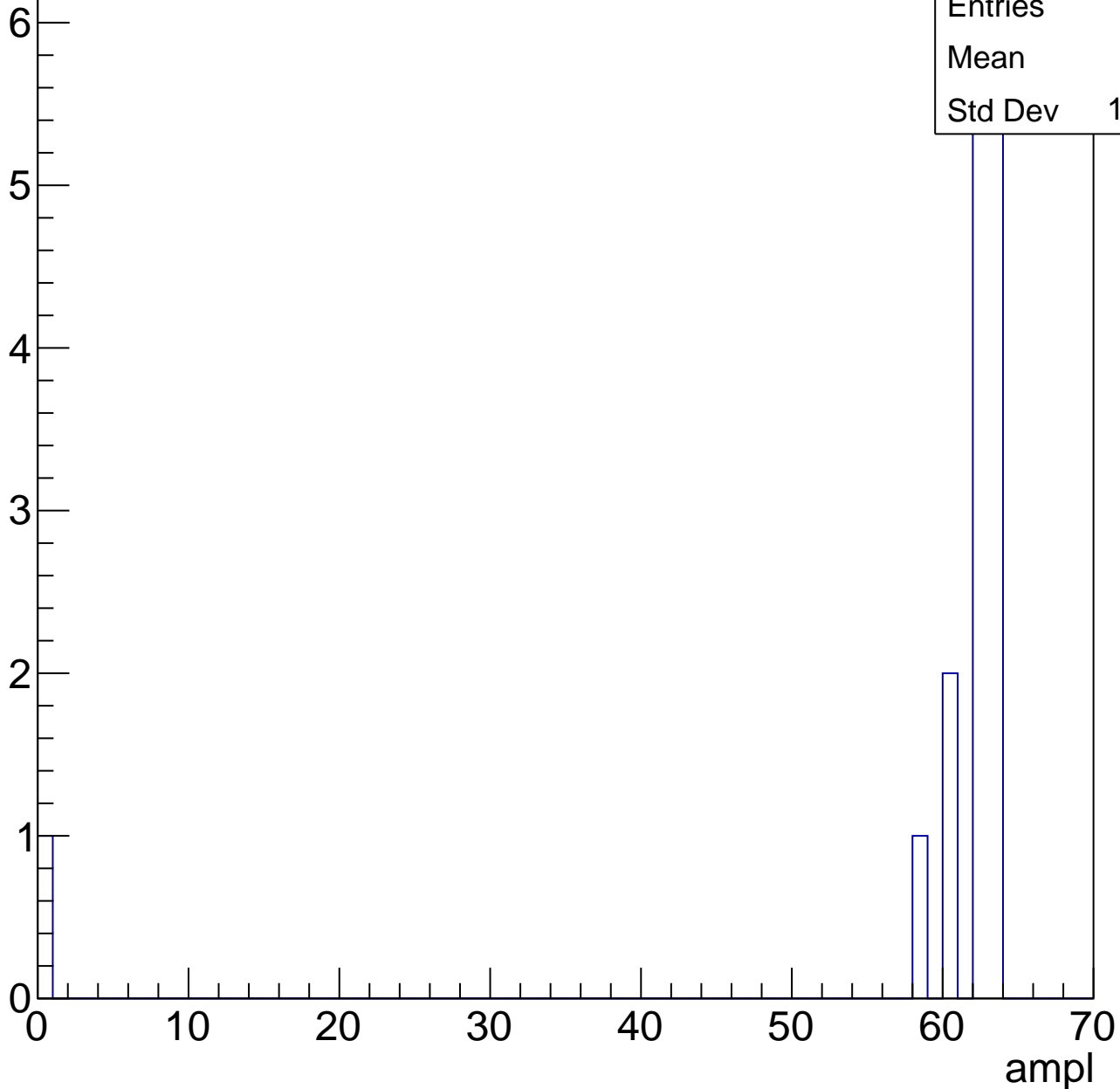


B1L102S, U14-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

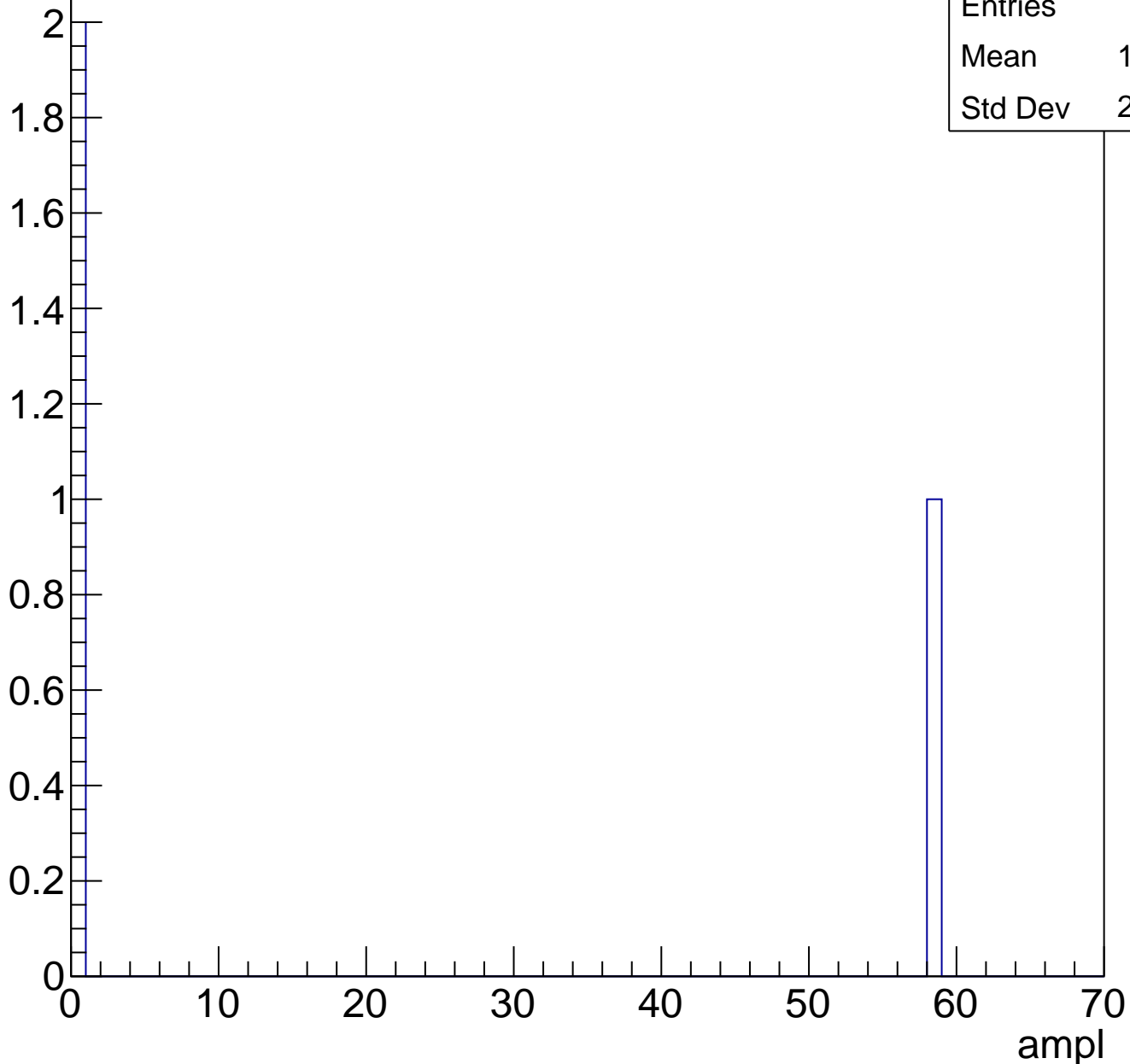
Entries	16
Mean	58
Std Dev	15.04



B1L102S, U14-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	19.33
Std Dev	27.34

B1L102S, U14-ch23, adc0

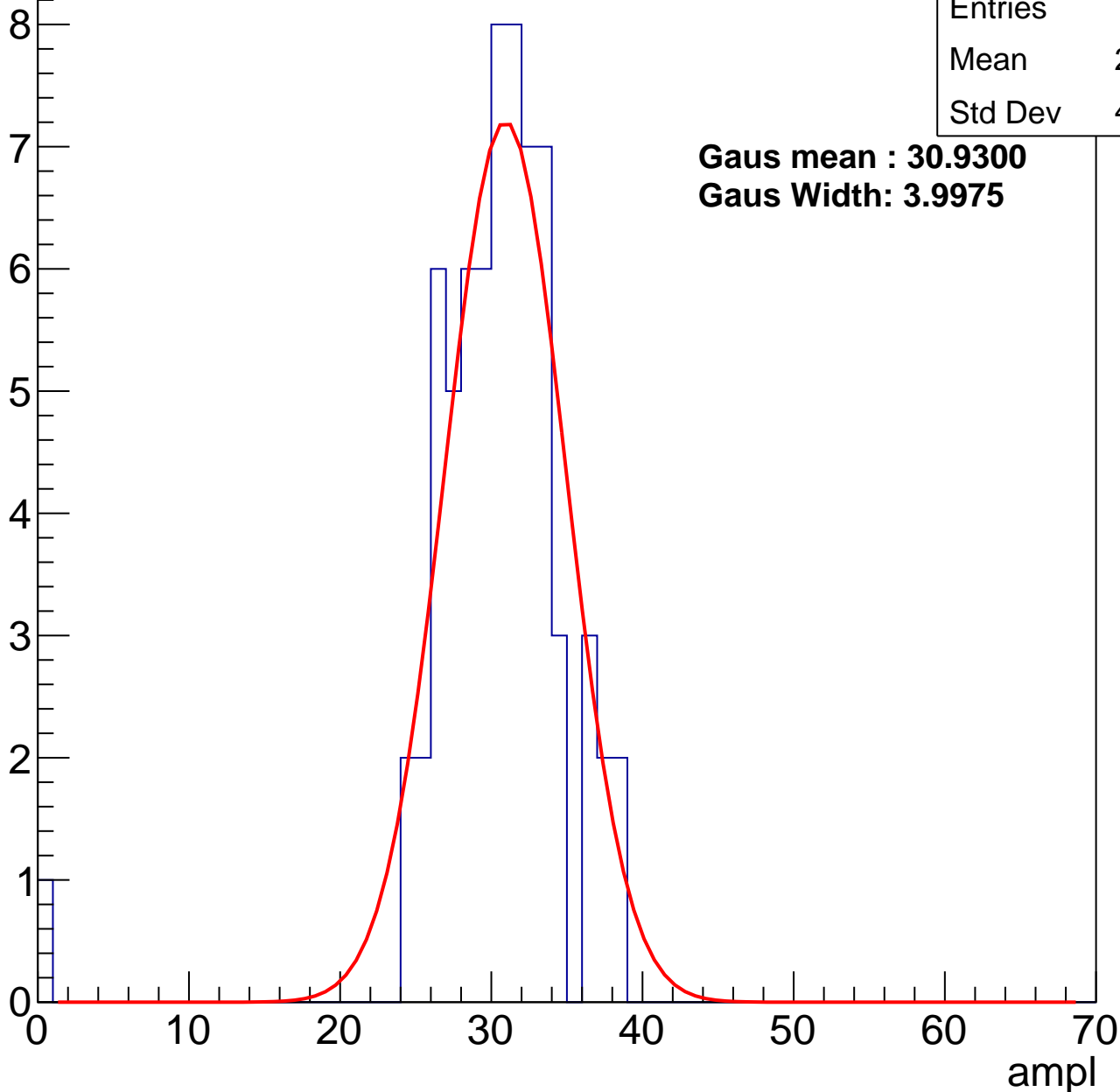
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.91
Std Dev	4.961

Gaus mean : 30.9300

Gaus Width: 3.9975



B1L102S, U14-ch23, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.82
Std Dev	2.982

Gaus mean : 37.2589

Gaus Width: 3.1903

10

8

6

4

2

0

0

10

20

30

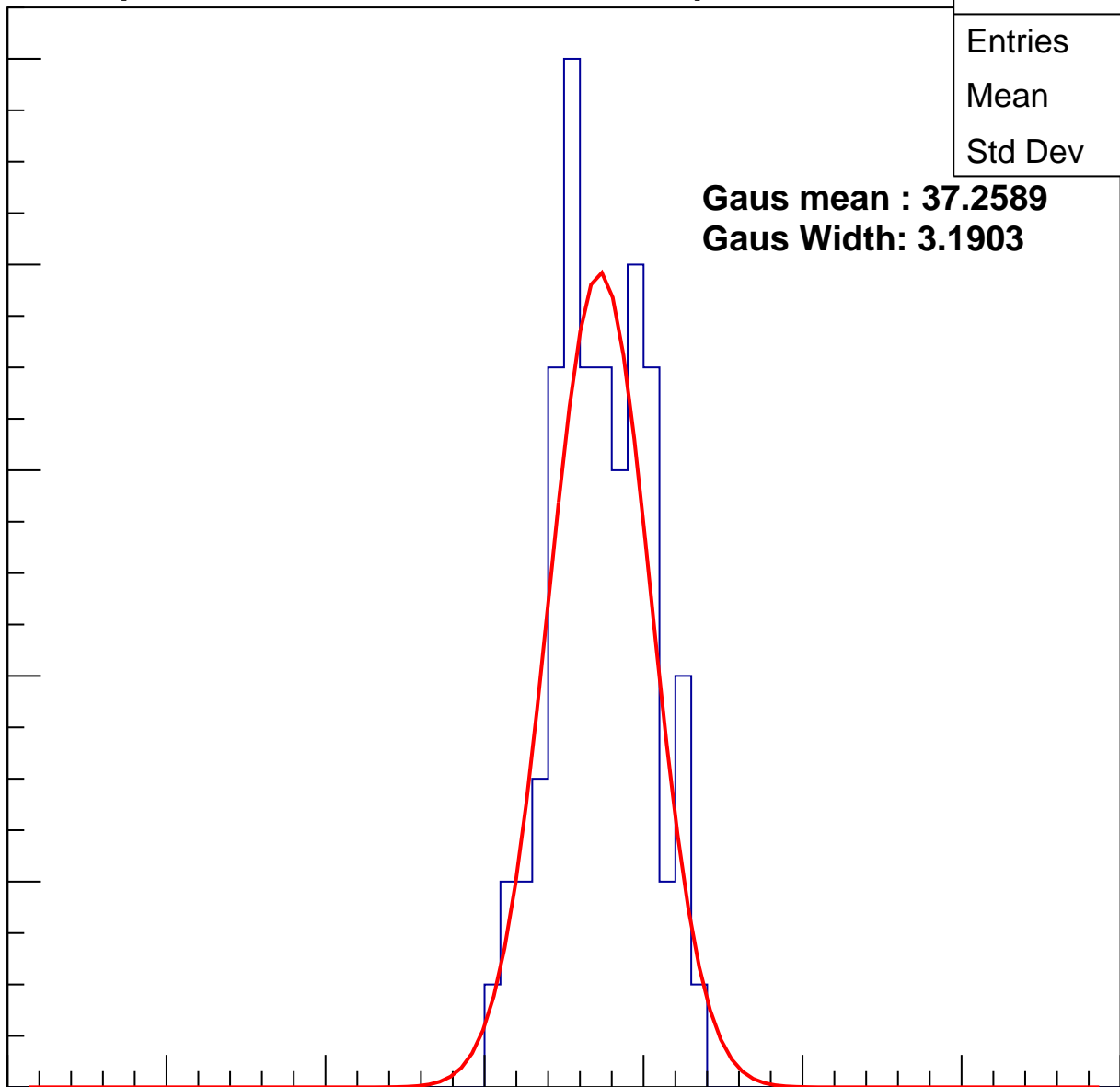
40

50

60

70

ampl



B1L102S, U14-ch23, adc2

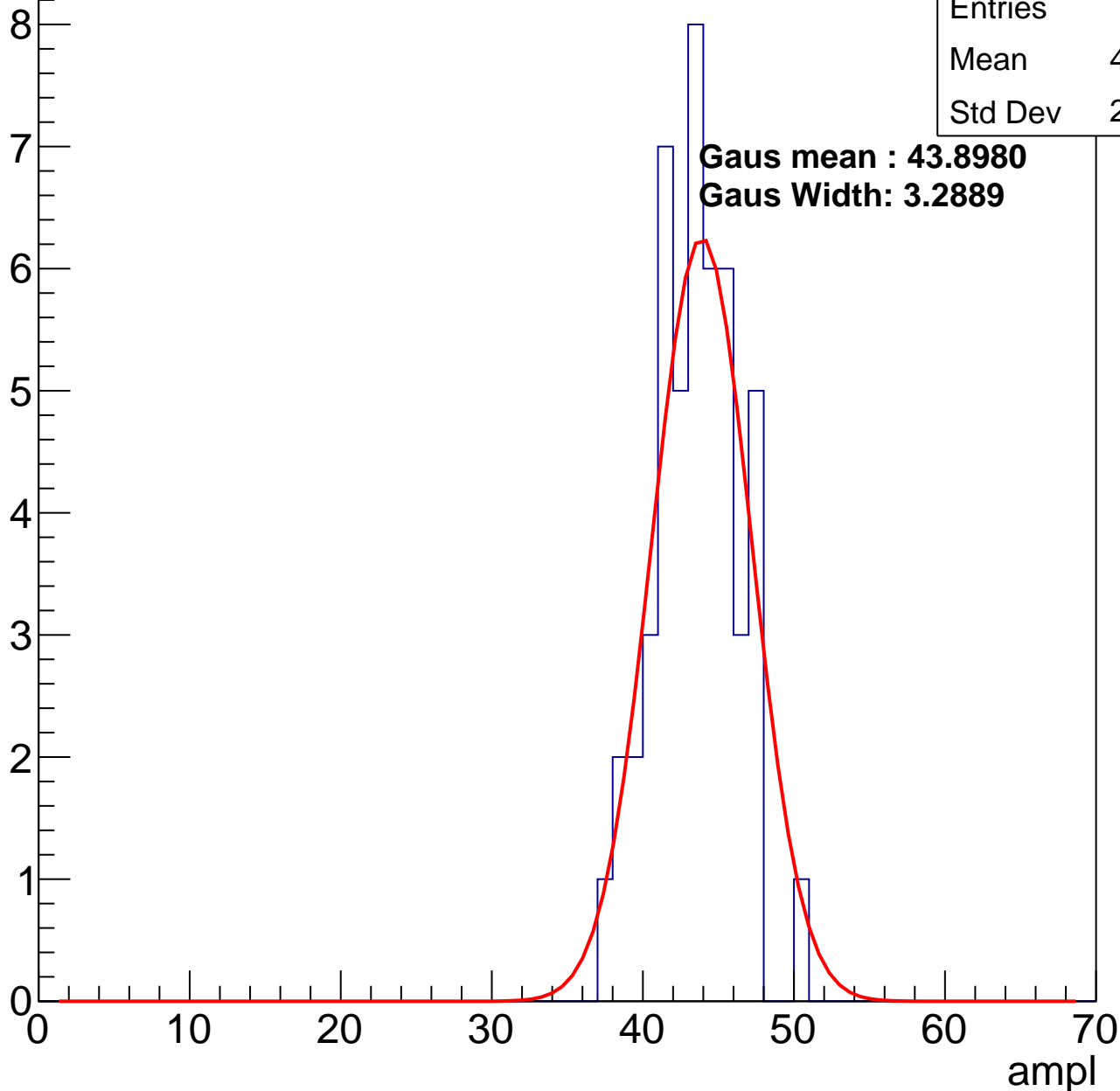
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.04
Std Dev	2.725

Gaus mean : 43.8980

Gaus Width: 3.2889

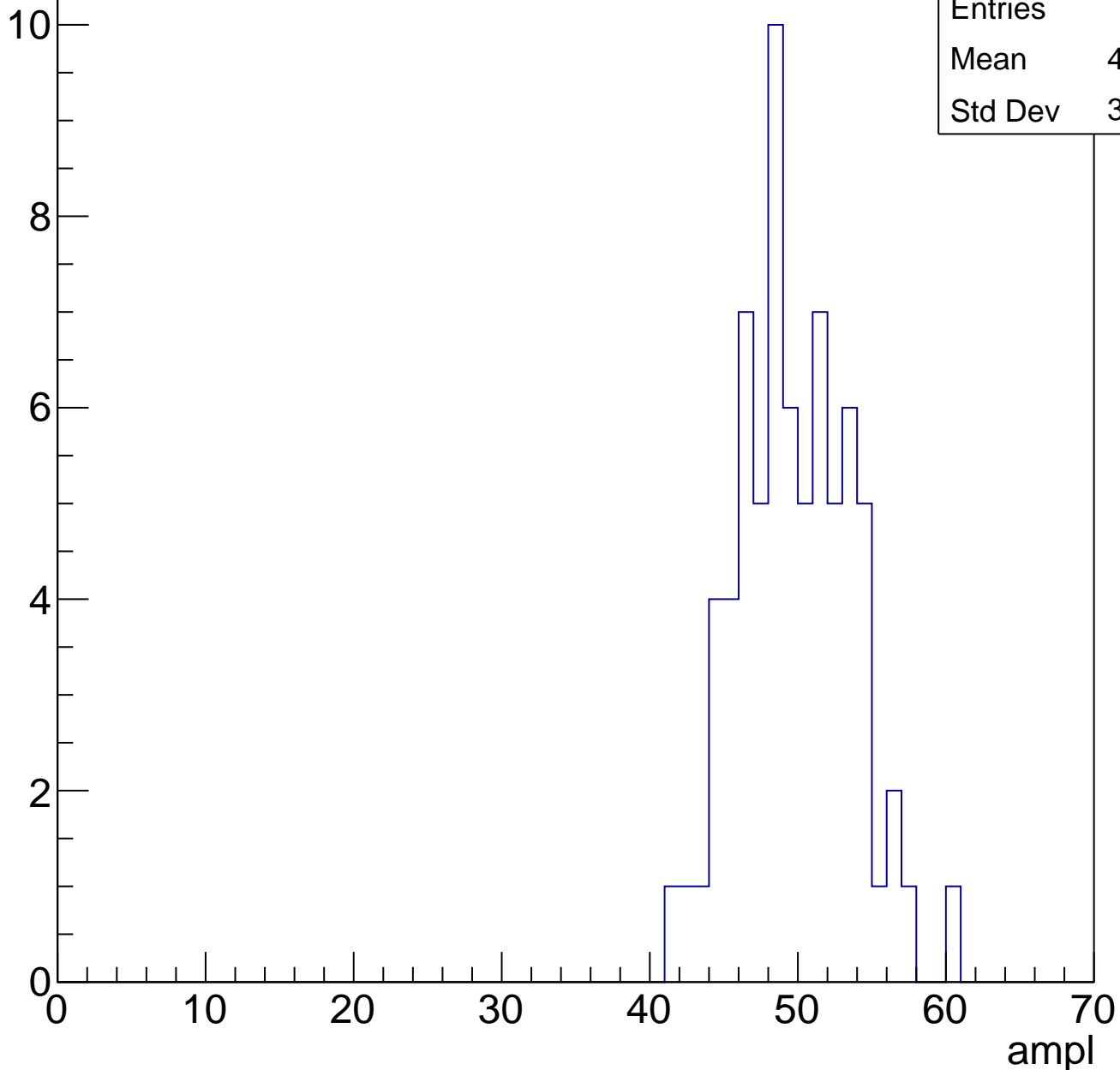


B1L102S, U14-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	49.33
Std Dev	3.756

Entry

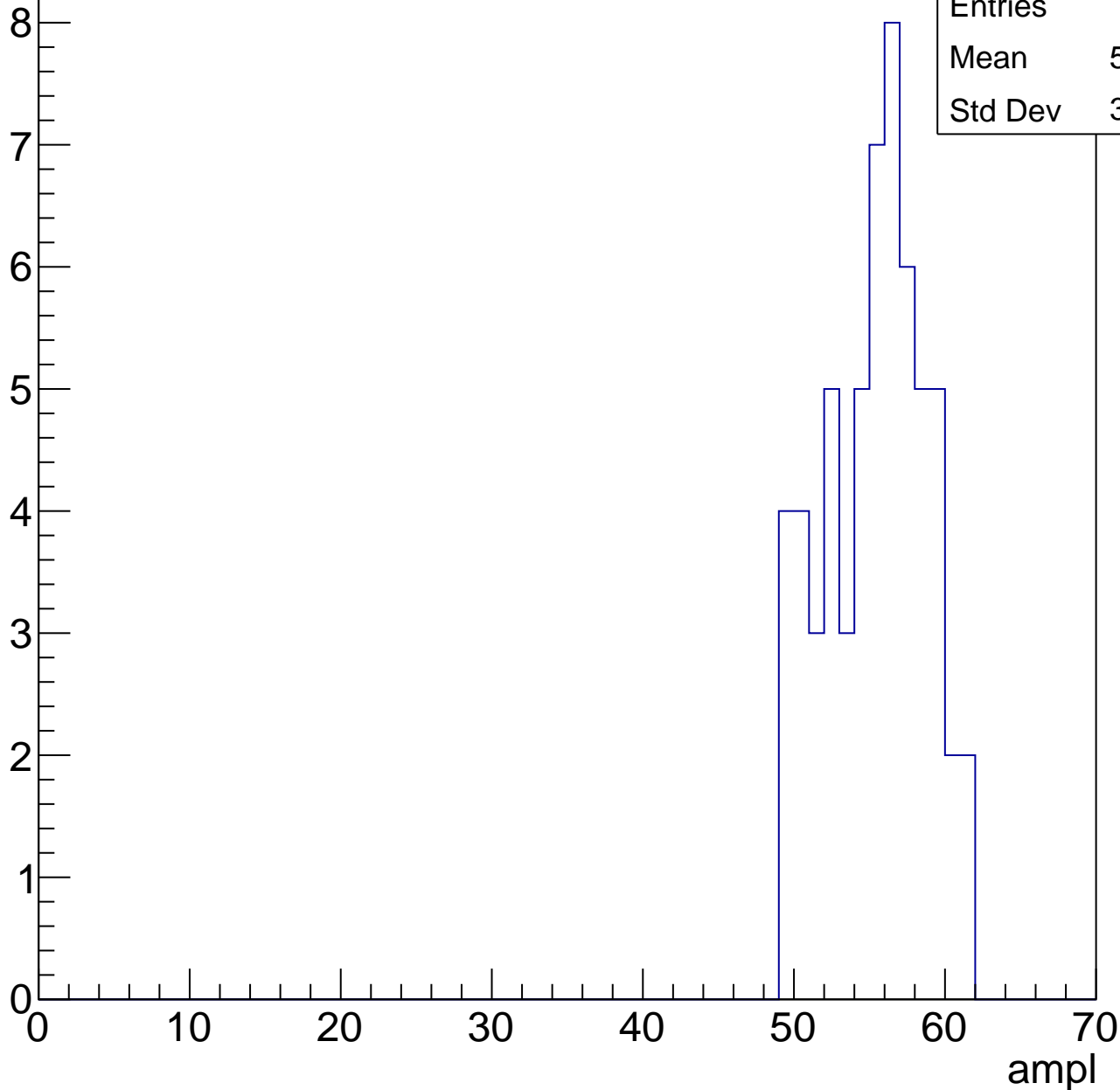


B1L102S, U14-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.92
Std Dev	3.274

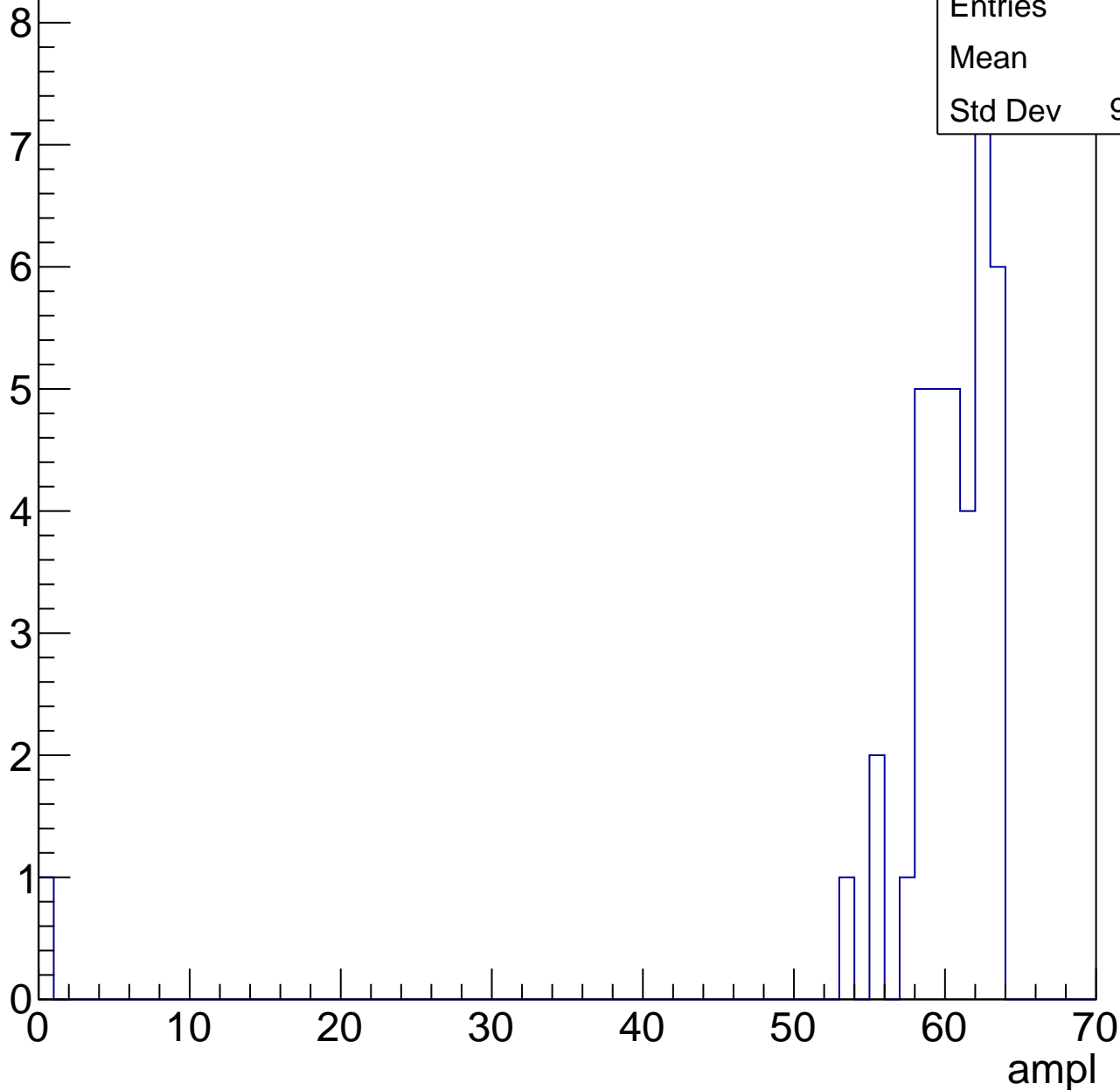


B1L102S, U14-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

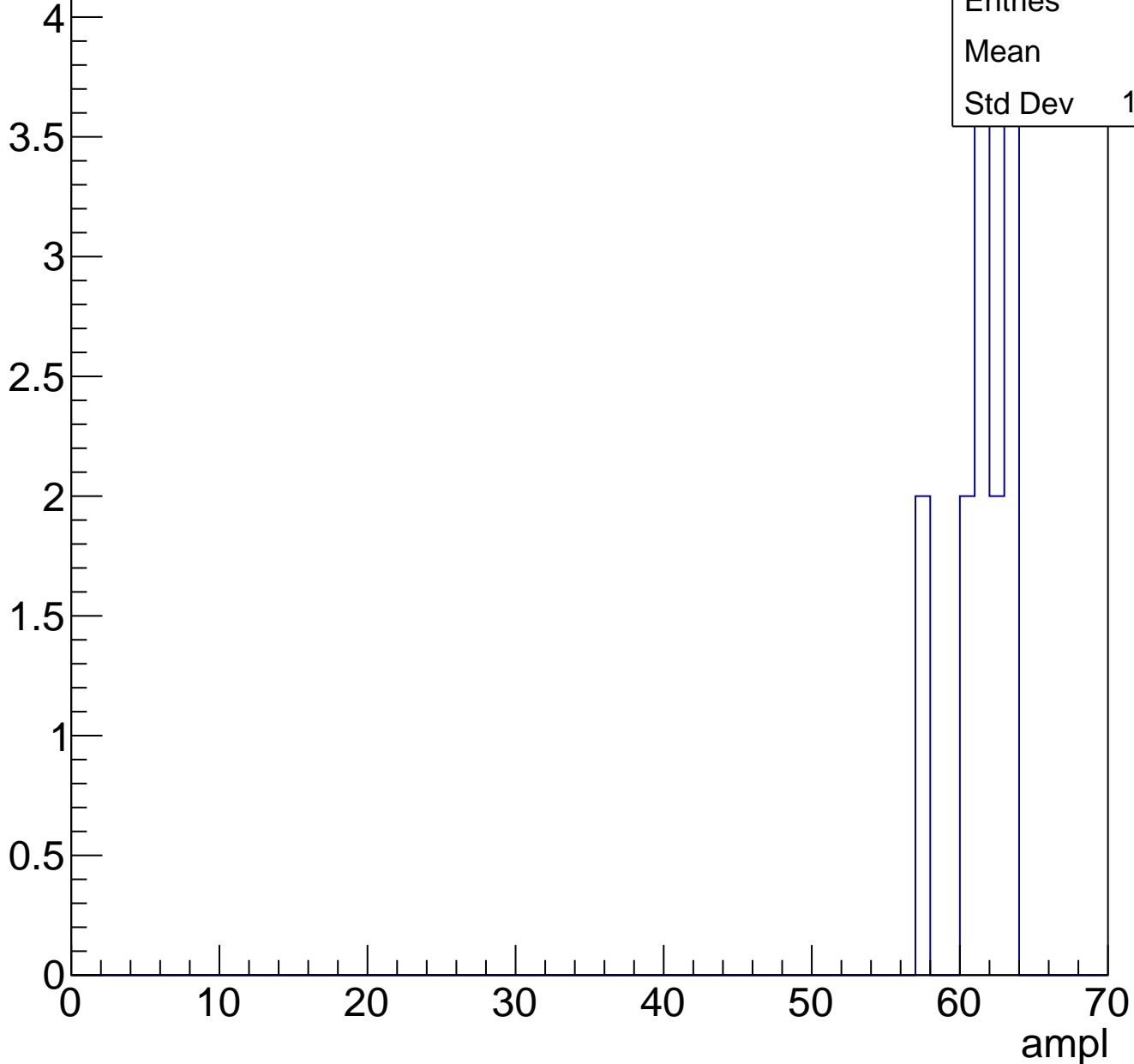
Entries	38
Mean	58.5
Std Dev	9.917



B1L102S, U14-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch24, adc0

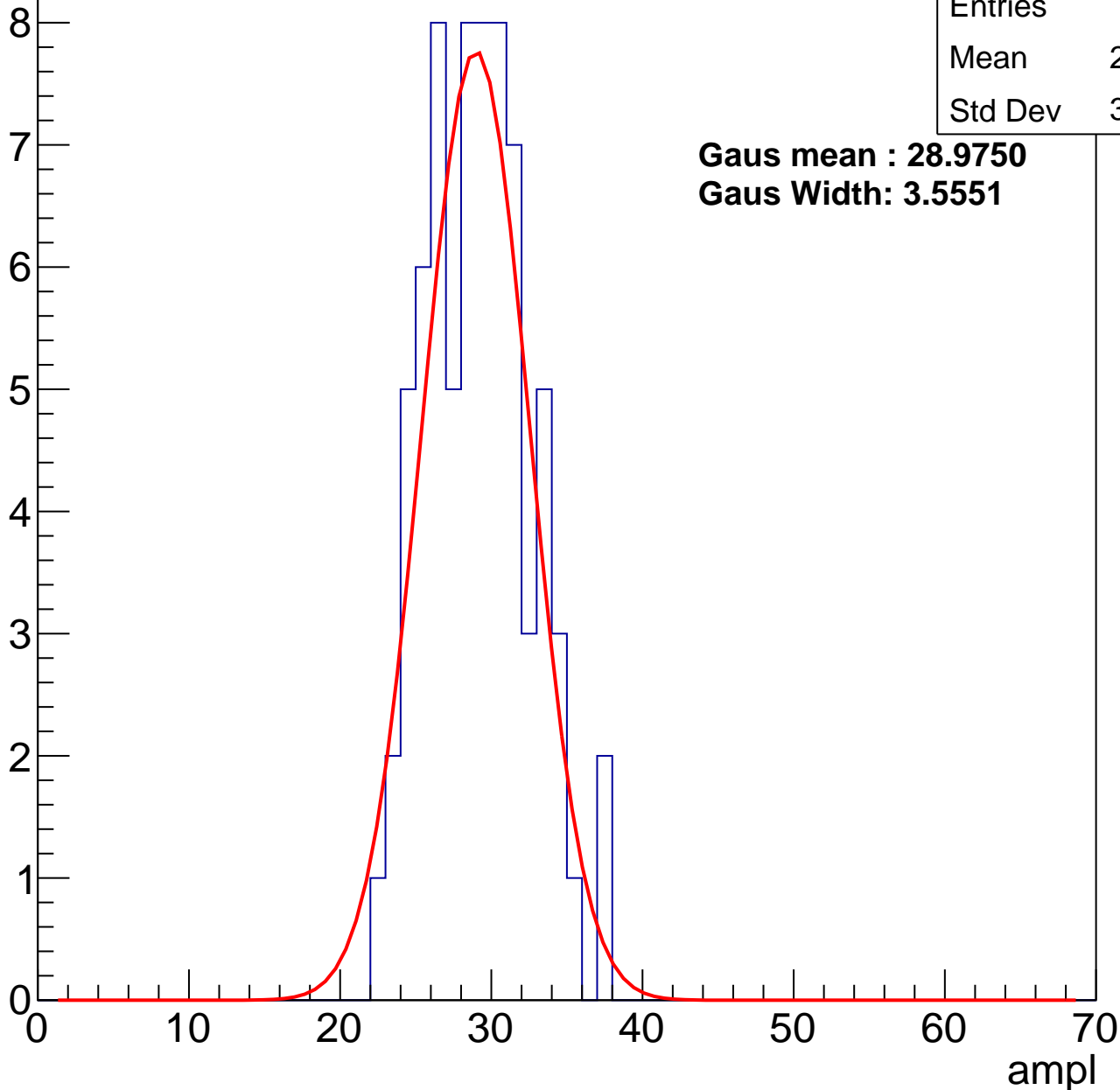
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.69
Std Dev	3.369

Gaus mean : 28.9750

Gaus Width: 3.5551



B1L102S, U14-ch24, adc1

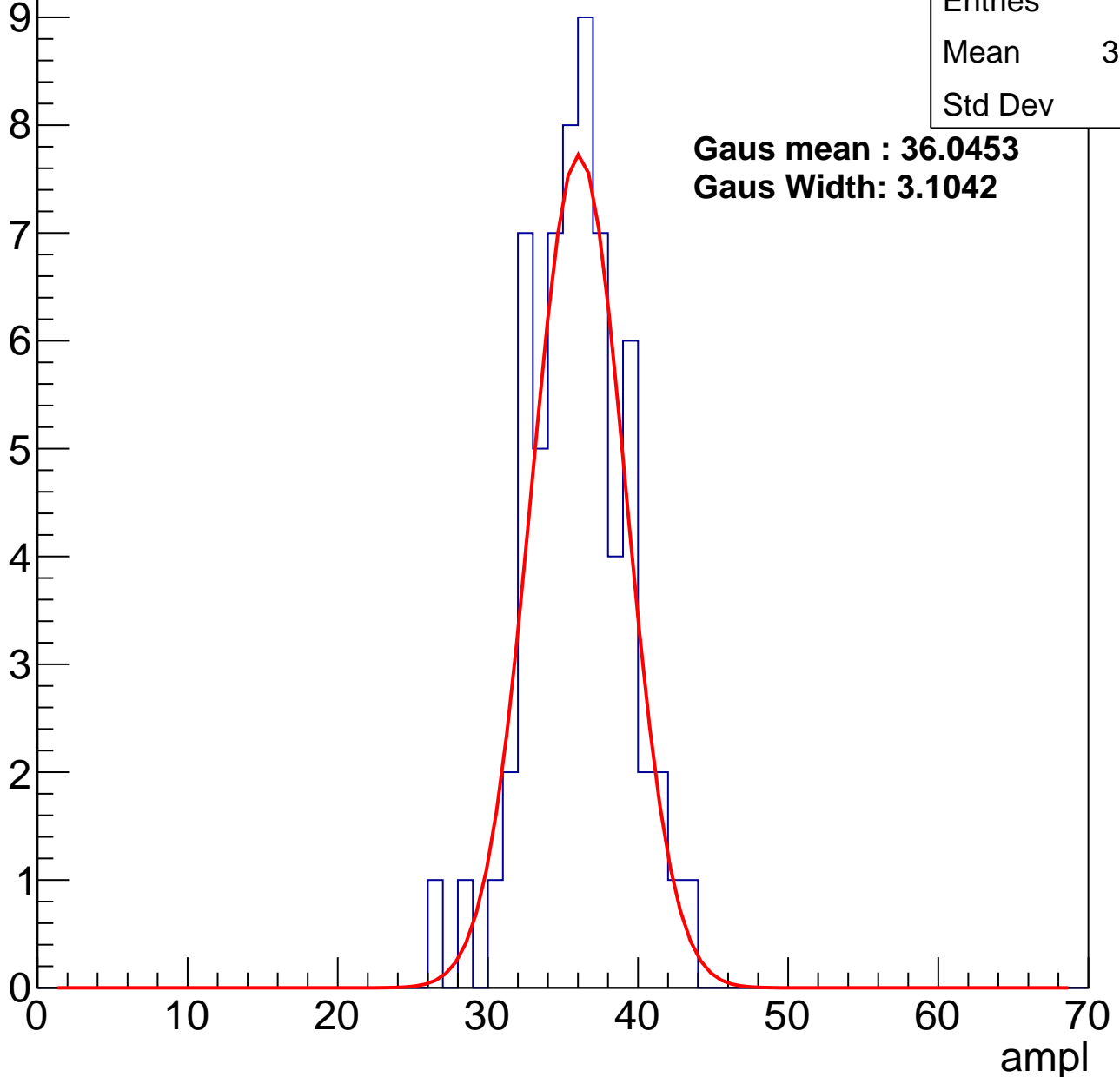
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.45
Std Dev	3.23

Gaus mean : 36.0453

Gaus Width: 3.1042

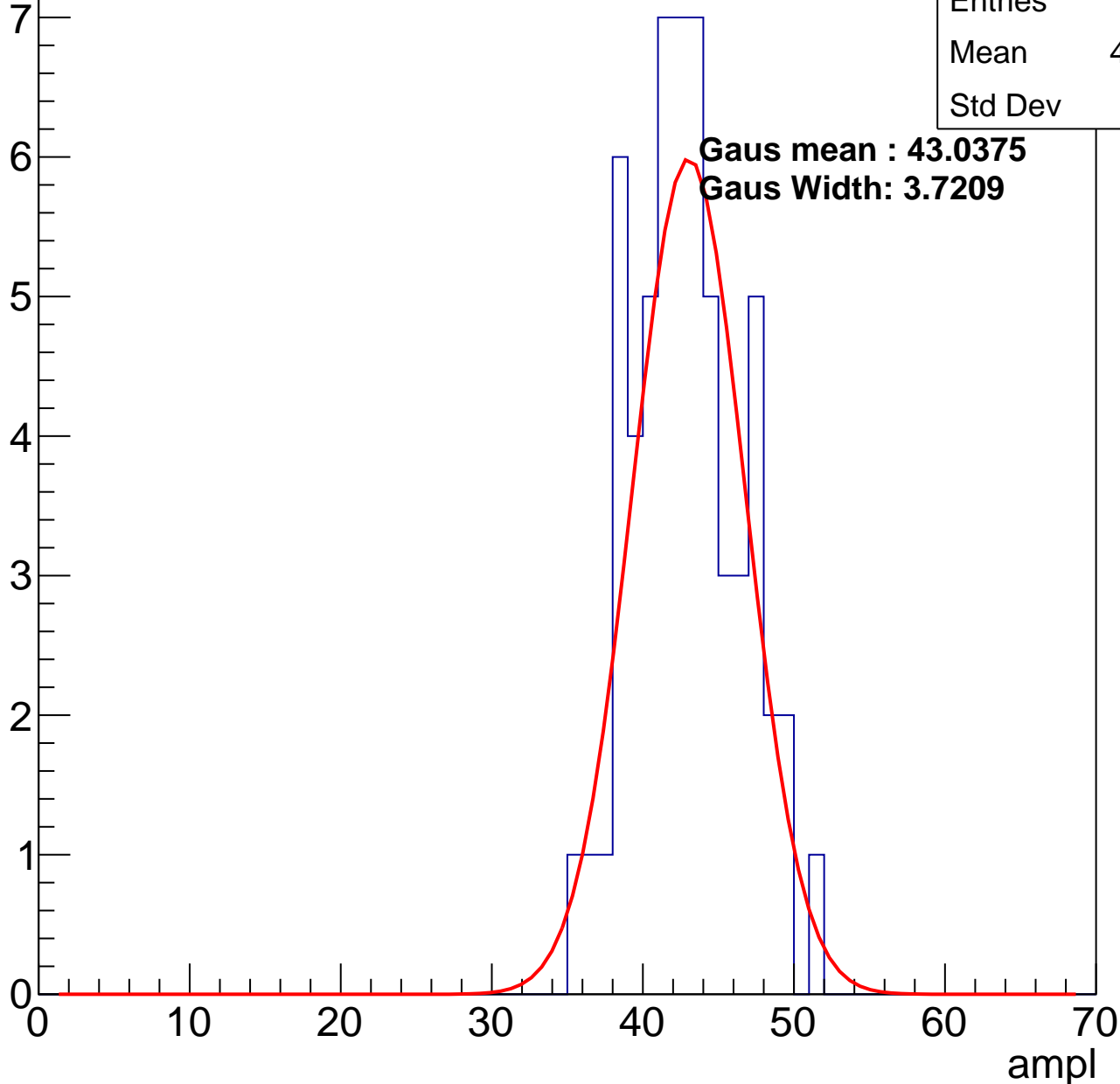


B1L102S, U14-ch24, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.45
Std Dev	3.5

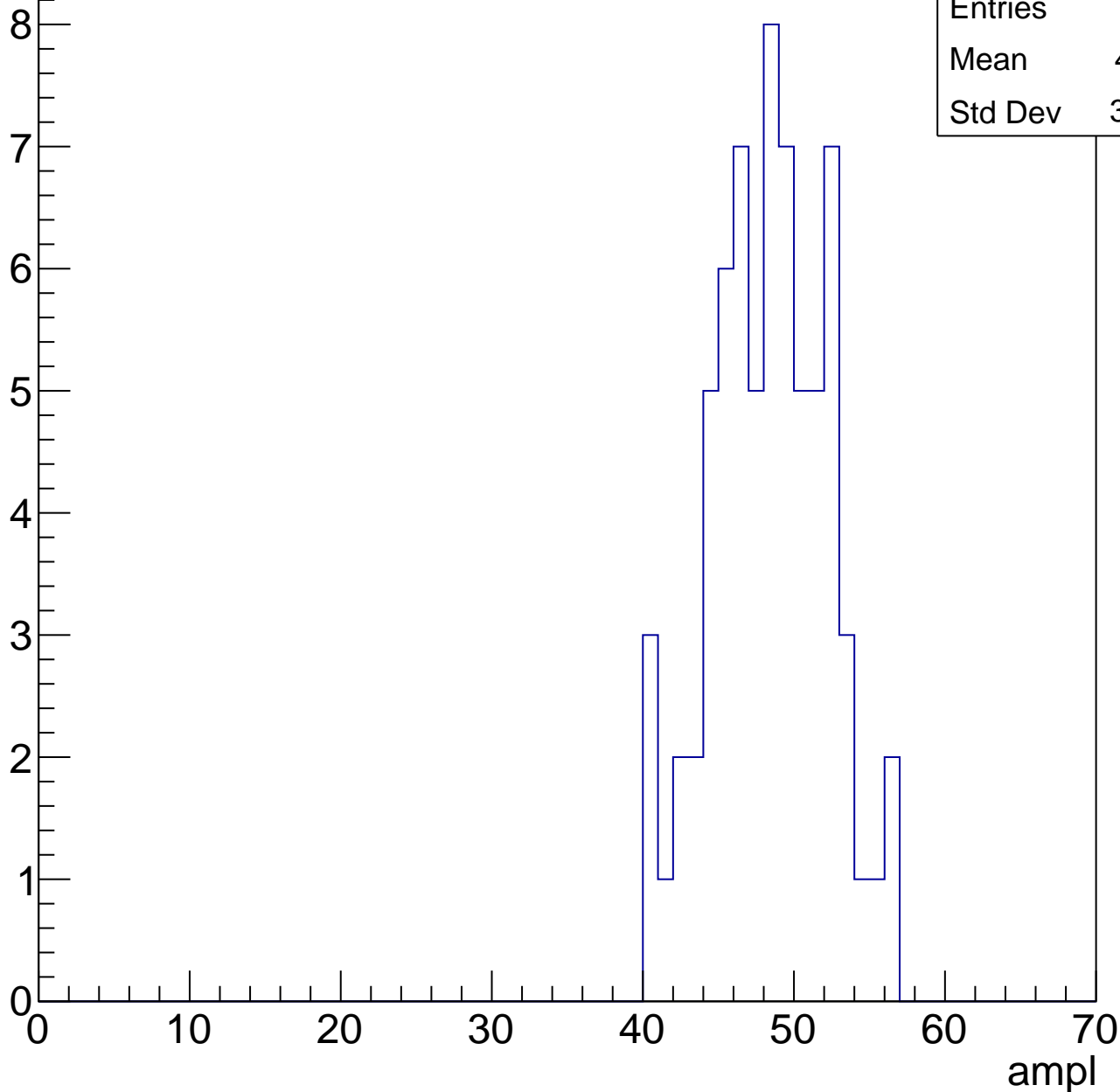


B1L102S, U14-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.91
Std Dev	3.782

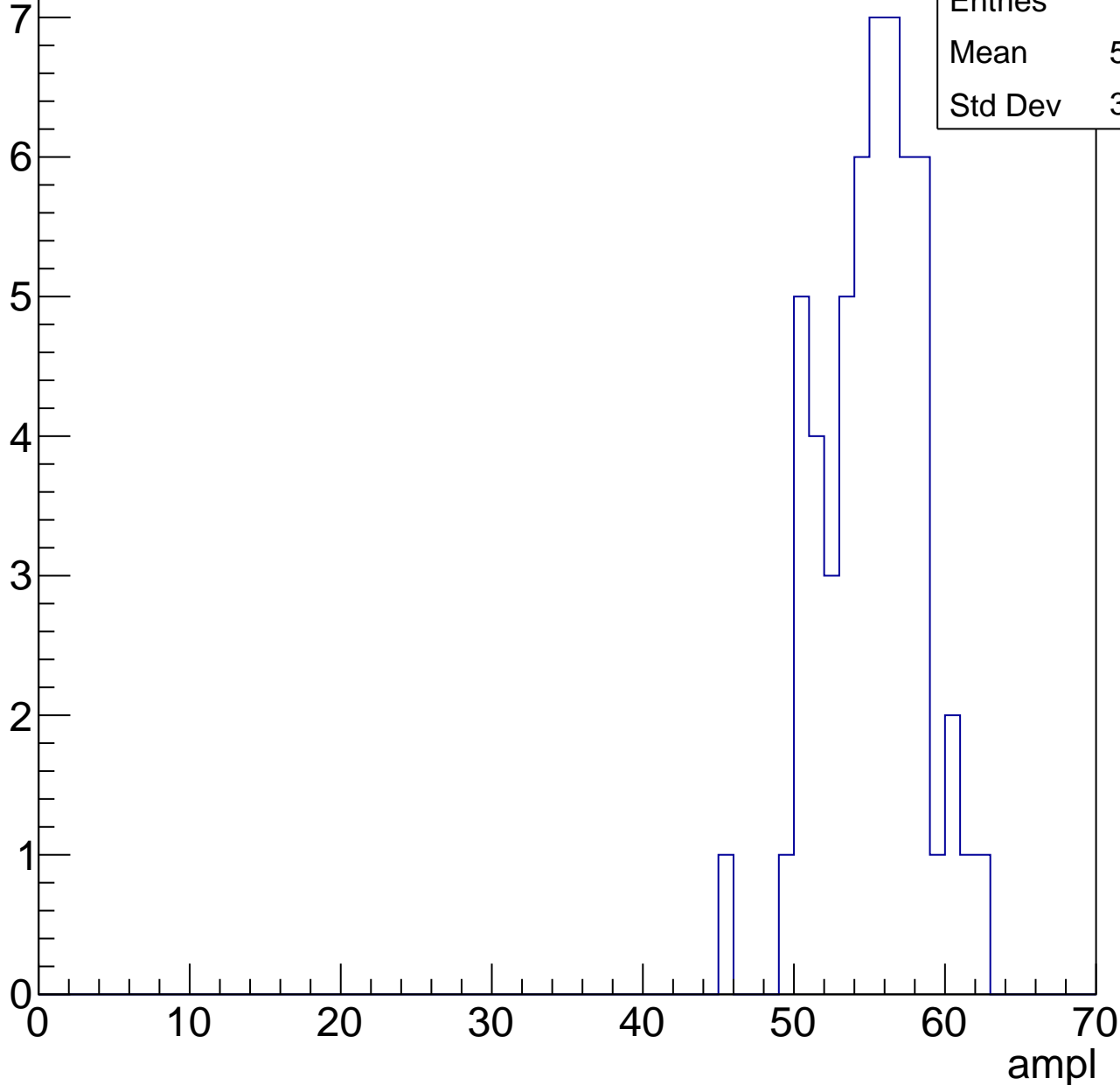


B1L102S, U14-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

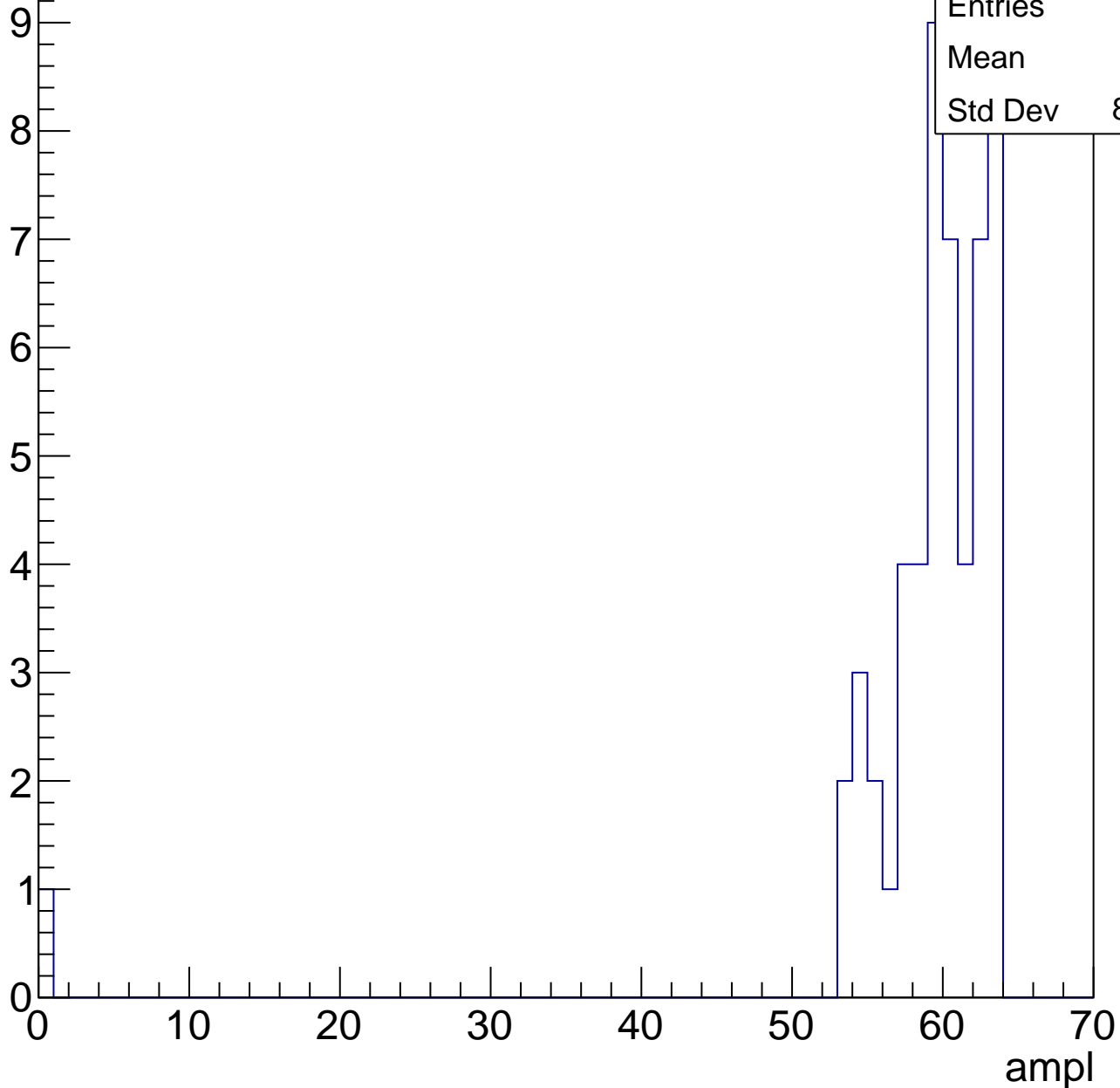
Entries	56
Mean	54.68
Std Dev	3.296



B1L102S, U14-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

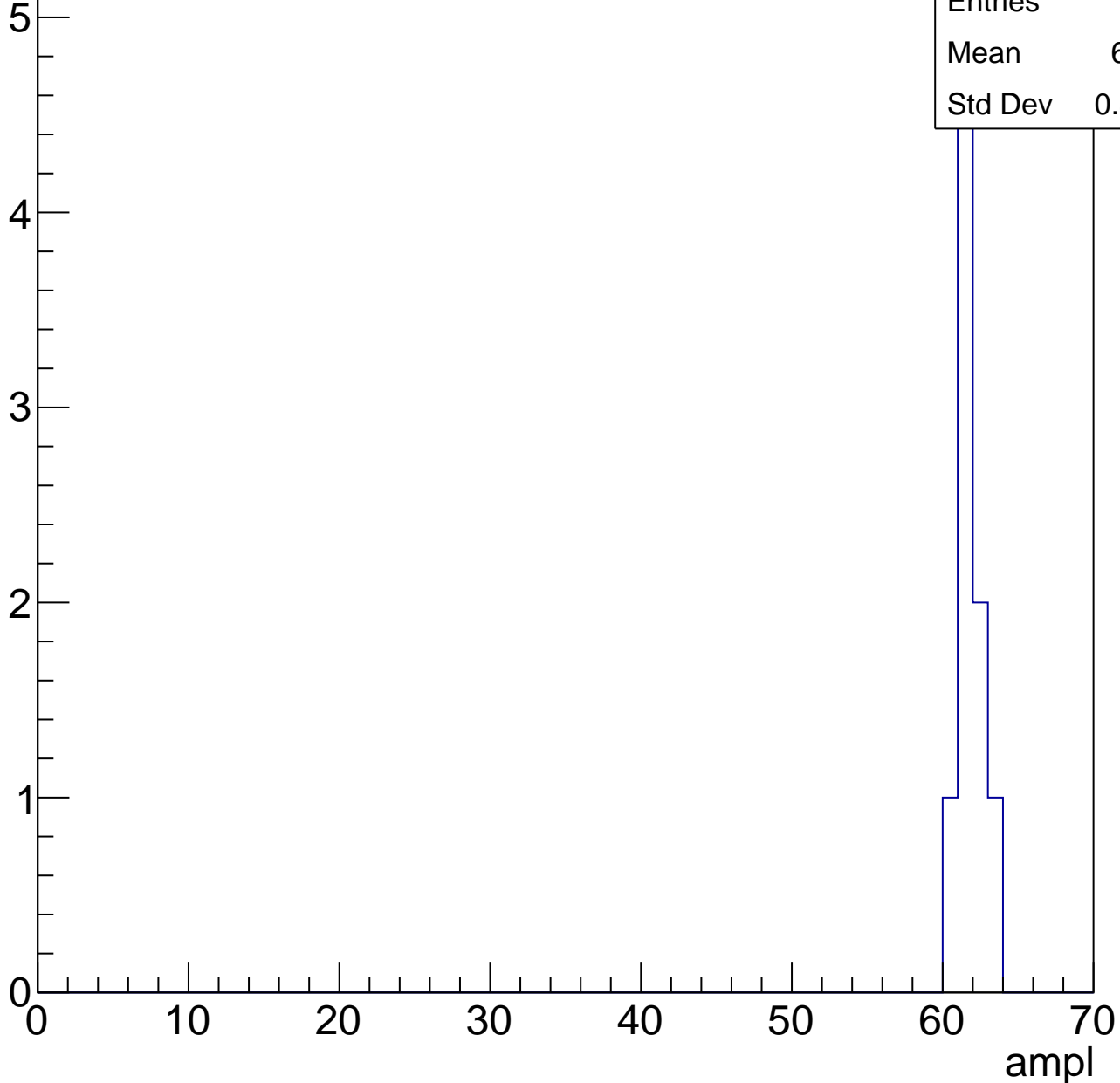


B1L102S, U14-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.33
Std Dev	0.8165



B1L102S, U14-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch25, adc0

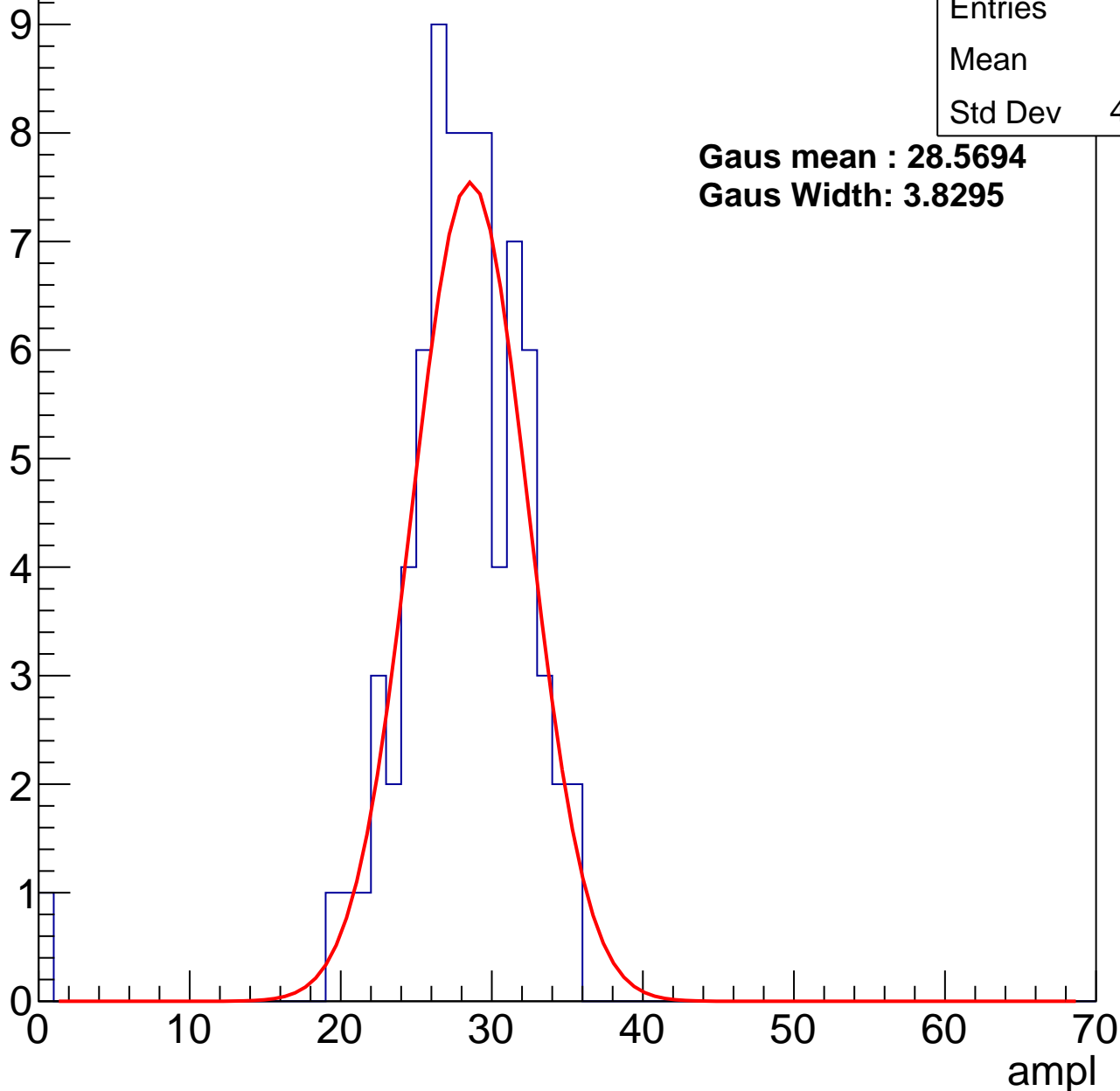
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.5
Std Dev	4.739

Gaus mean : 28.5694

Gaus Width: 3.8295



B1L102S, U14-ch25, adc1

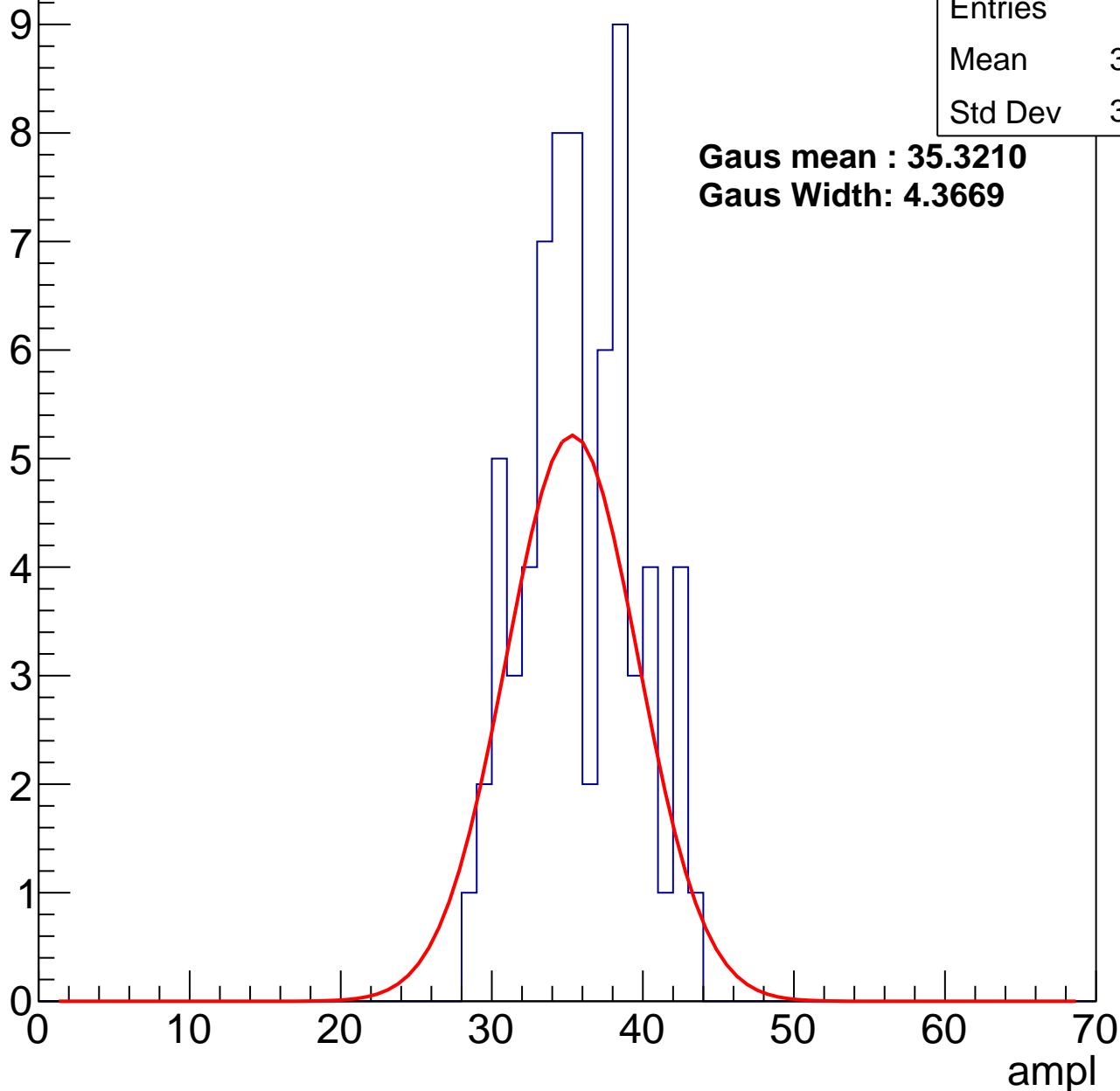
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.37
Std Dev	3.654

Gaus mean : 35.3210

Gaus Width: 4.3669



B1L102S, U14-ch25, adc2

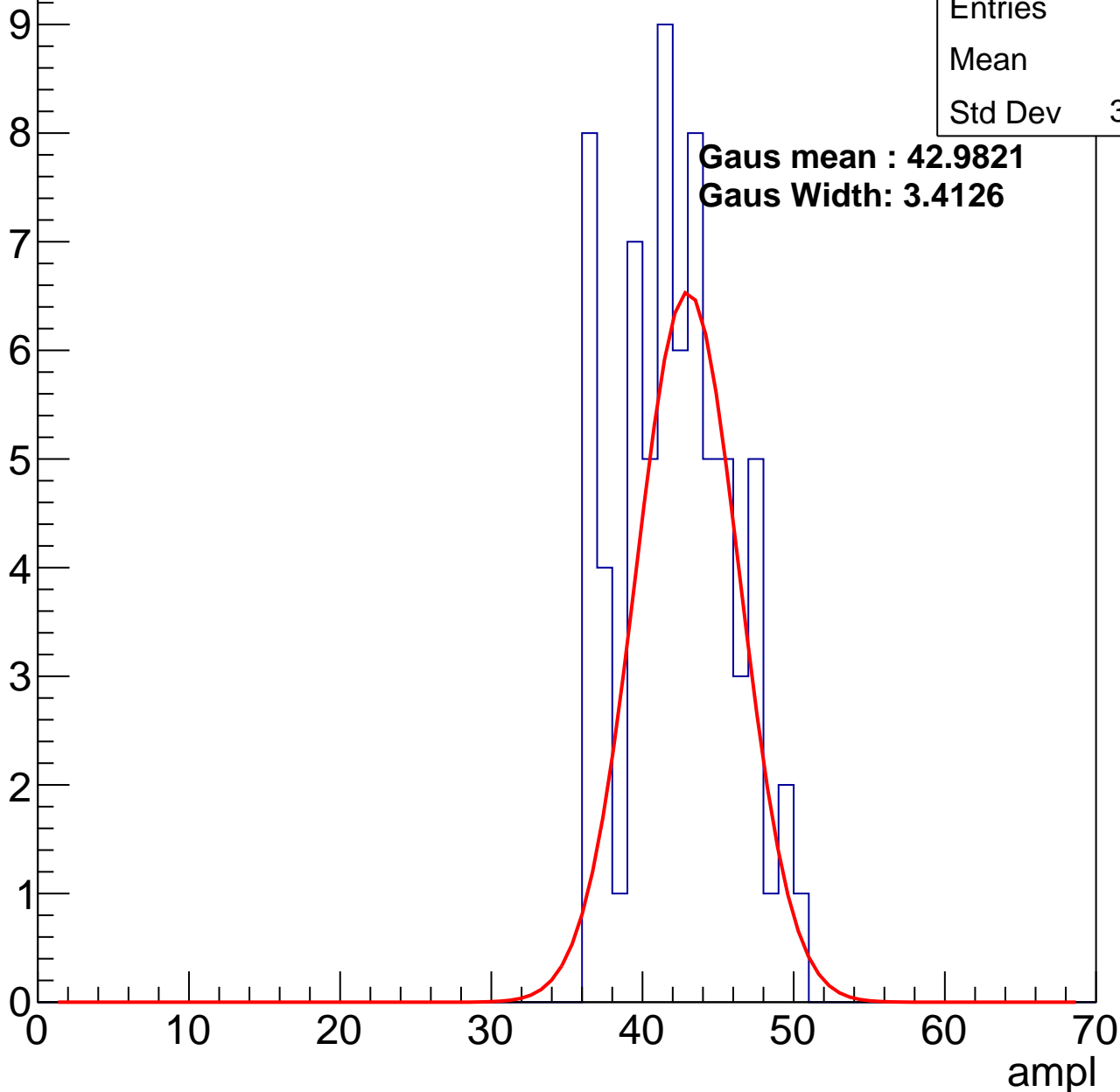
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.8
Std Dev	3.659

Gaus mean : 42.9821

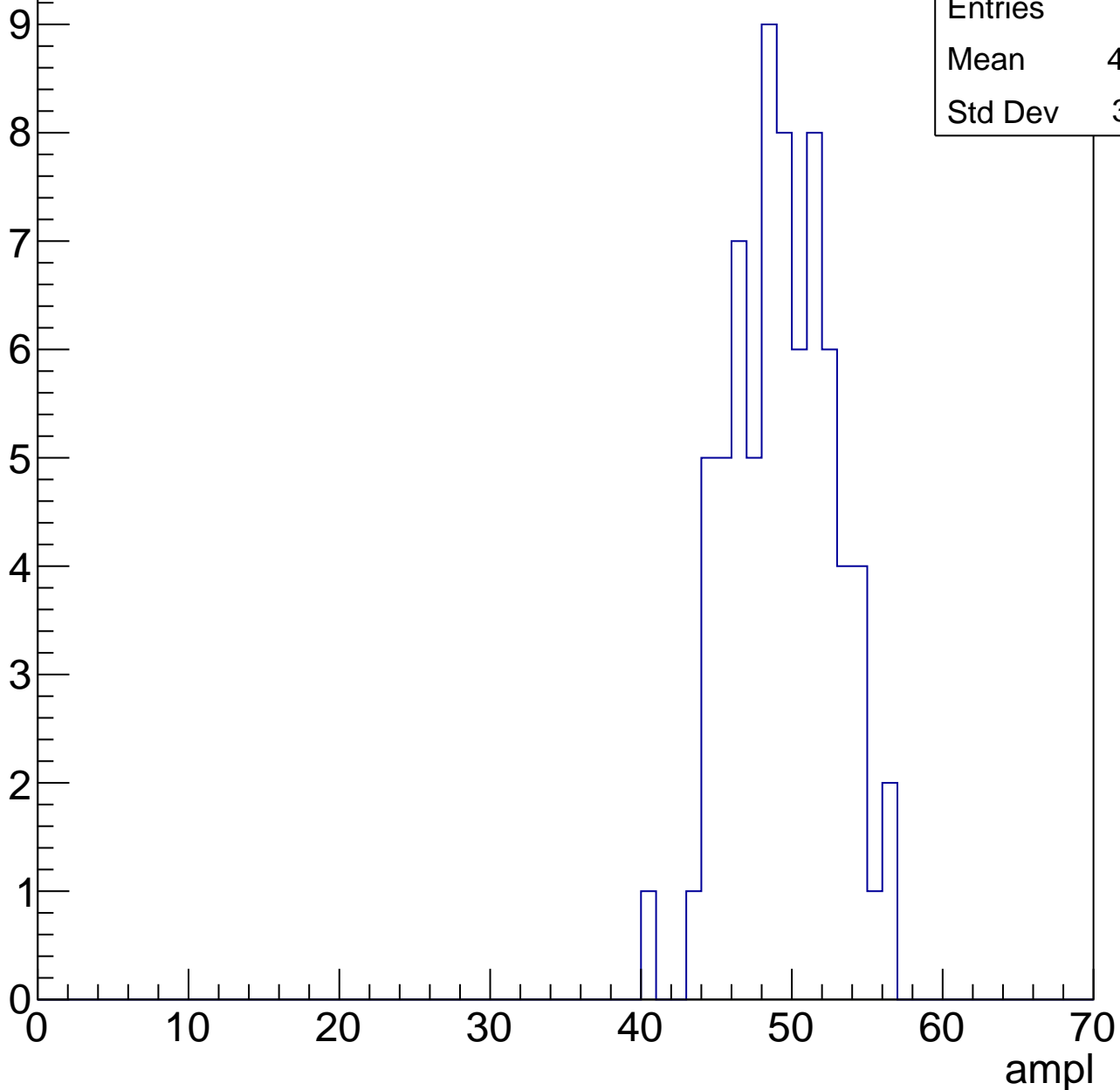
Gaus Width: 3.4126



B1L102S, U14-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



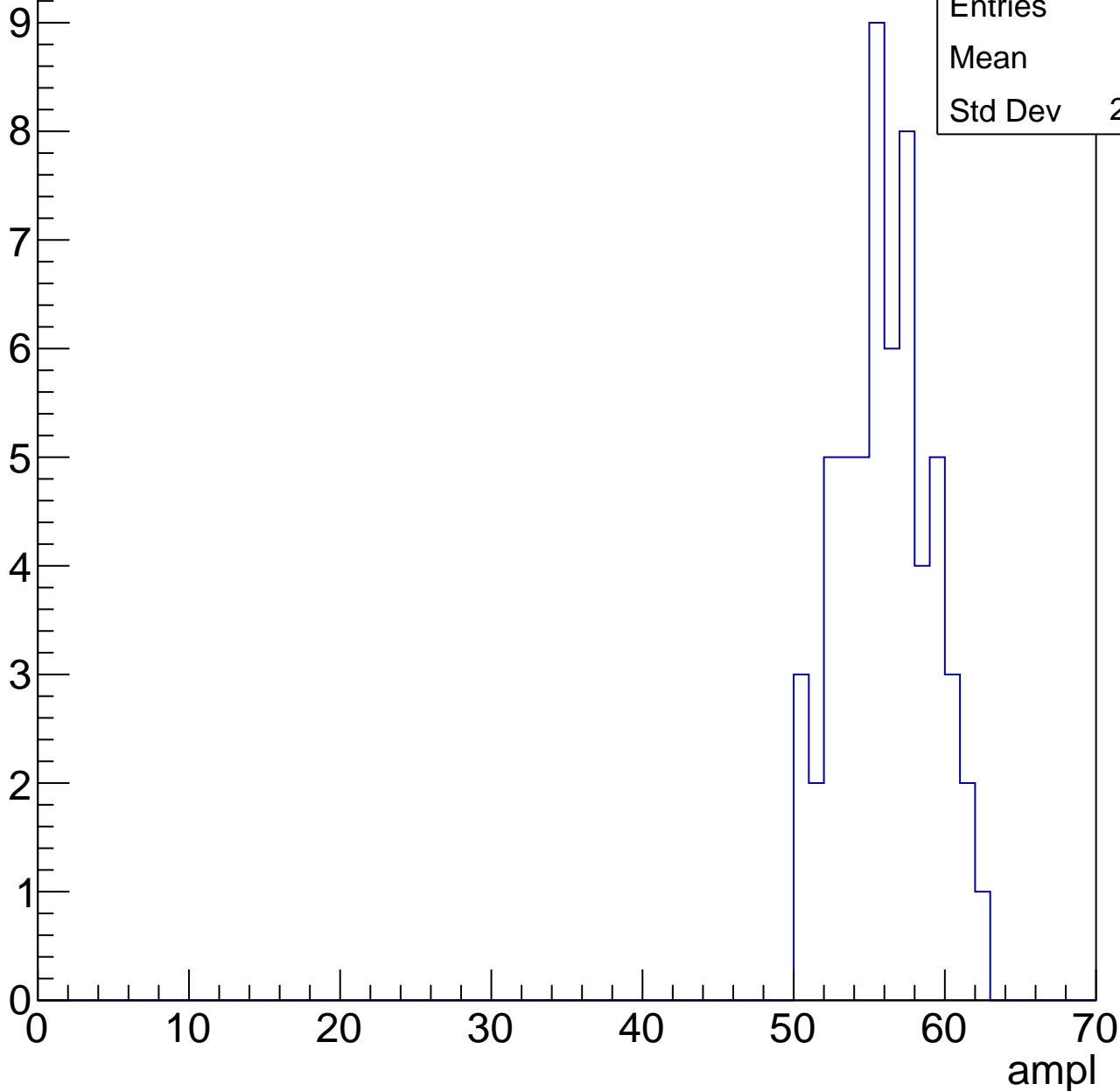
Entries	72
Mean	48.94
Std Dev	3.341

B1L102S, U14-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.6
Std Dev	2.953

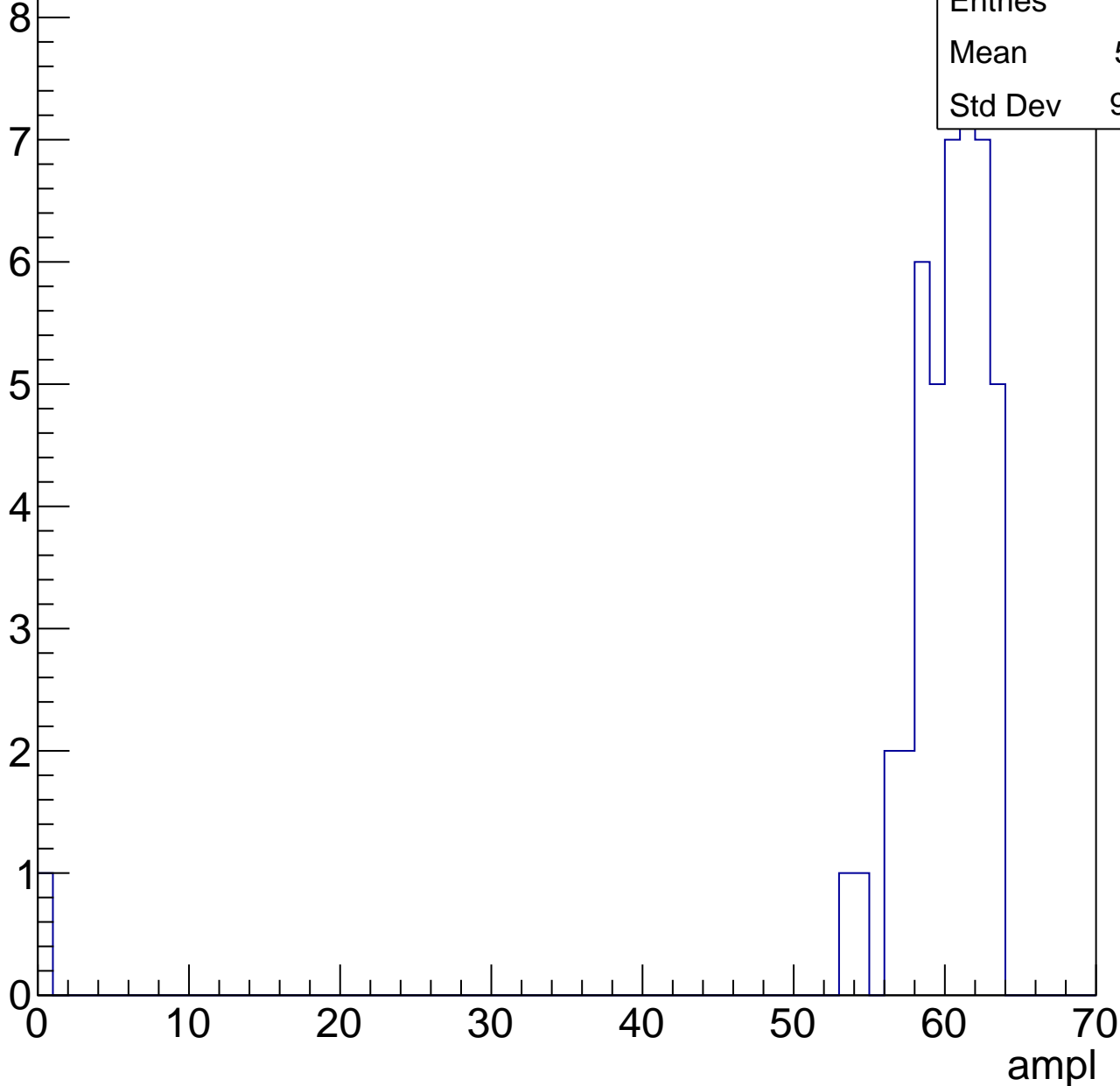


B1L102S, U14-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.51
Std Dev	9.123

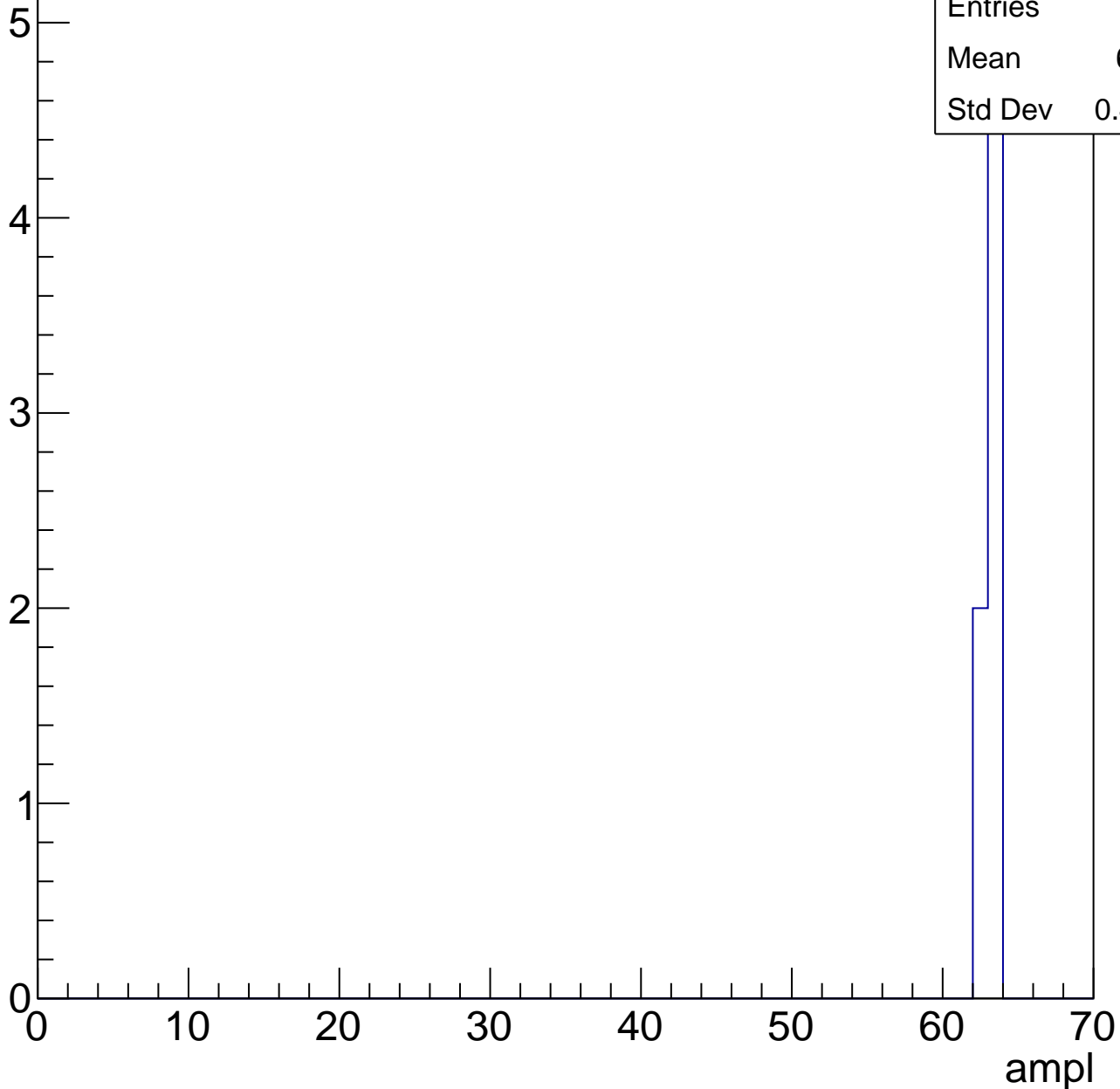


B1L102S, U14-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B1L102S, U14-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U14-ch26, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	28.7
Std Dev	6.602

Gaus mean : 30.3512

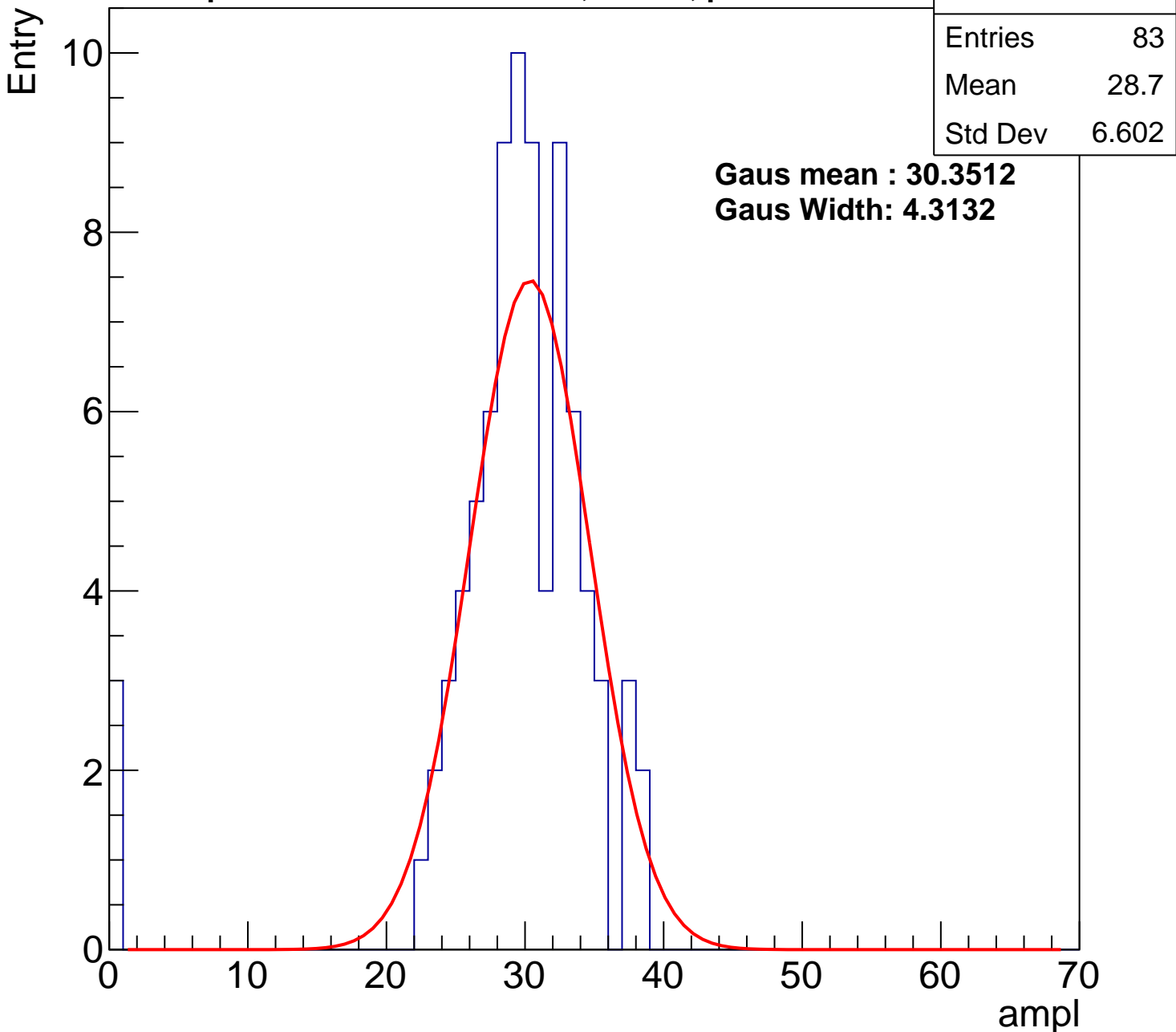
Gaus Width: 4.3132

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch26, adc1

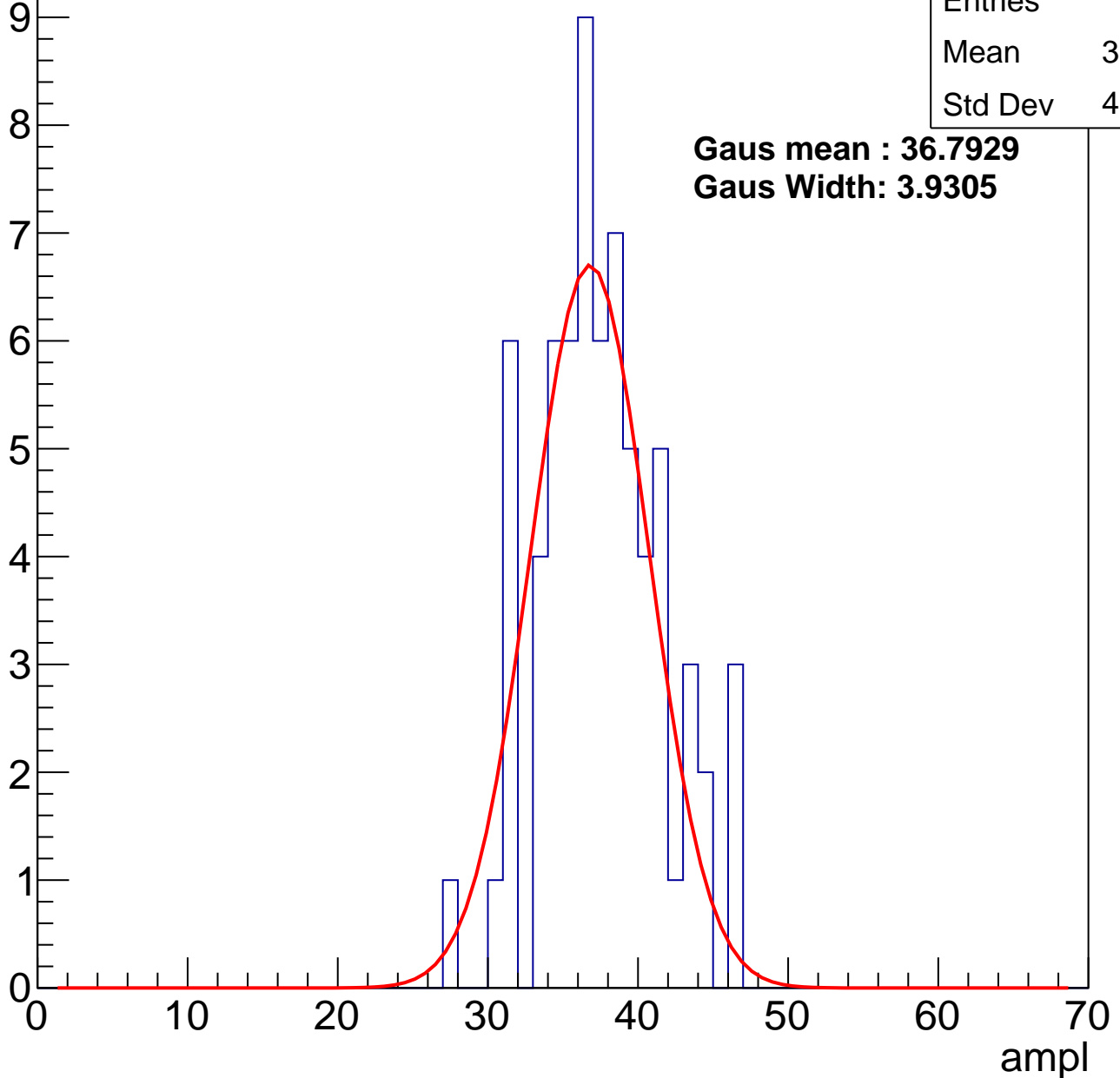
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.07
Std Dev	4.052

Gaus mean : 36.7929

Gaus Width: 3.9305



B1L102S, U14-ch26, adc2

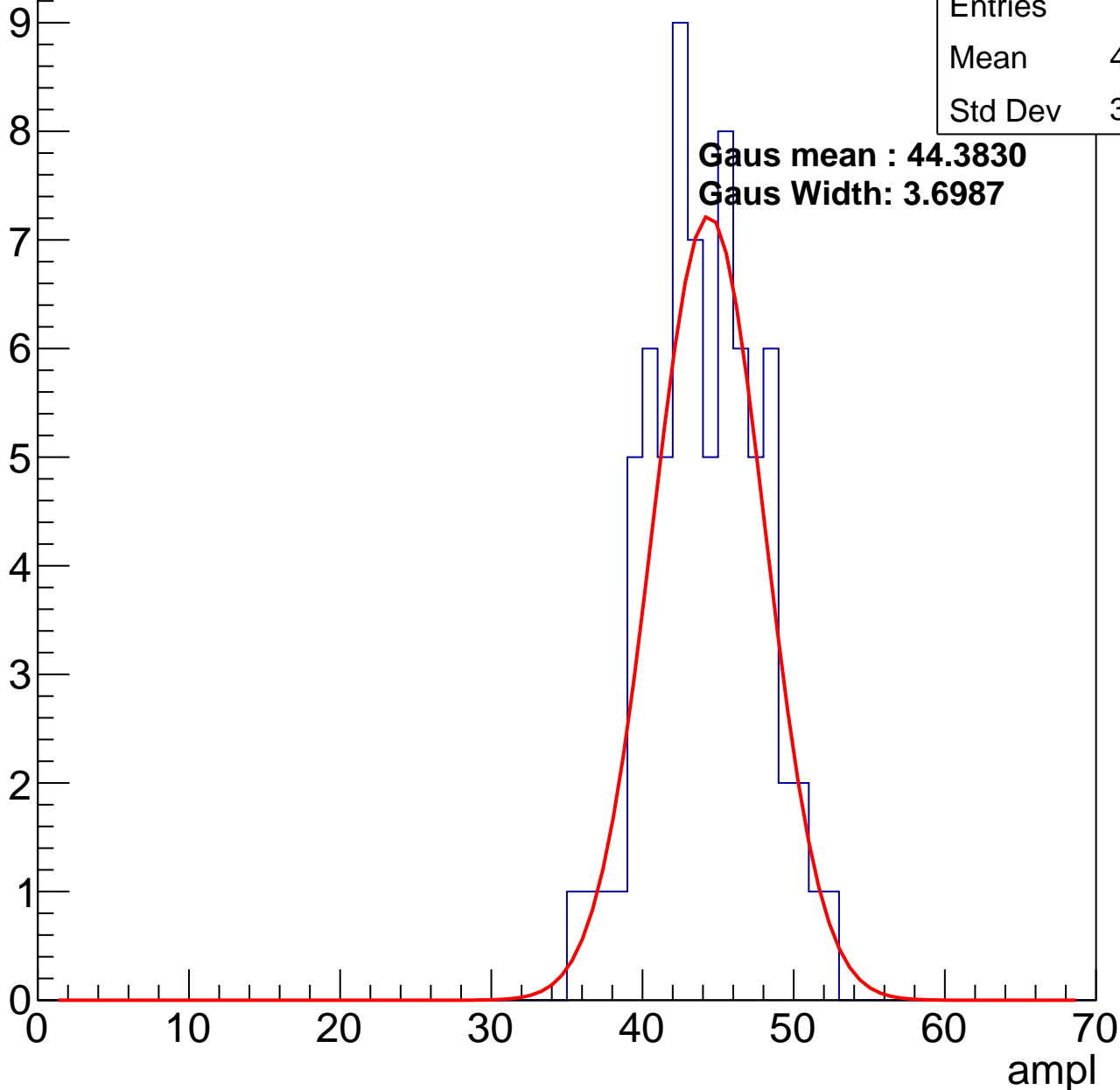
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.68
Std Dev	3.613

Gaus mean : 44.3830

Gaus Width: 3.6987

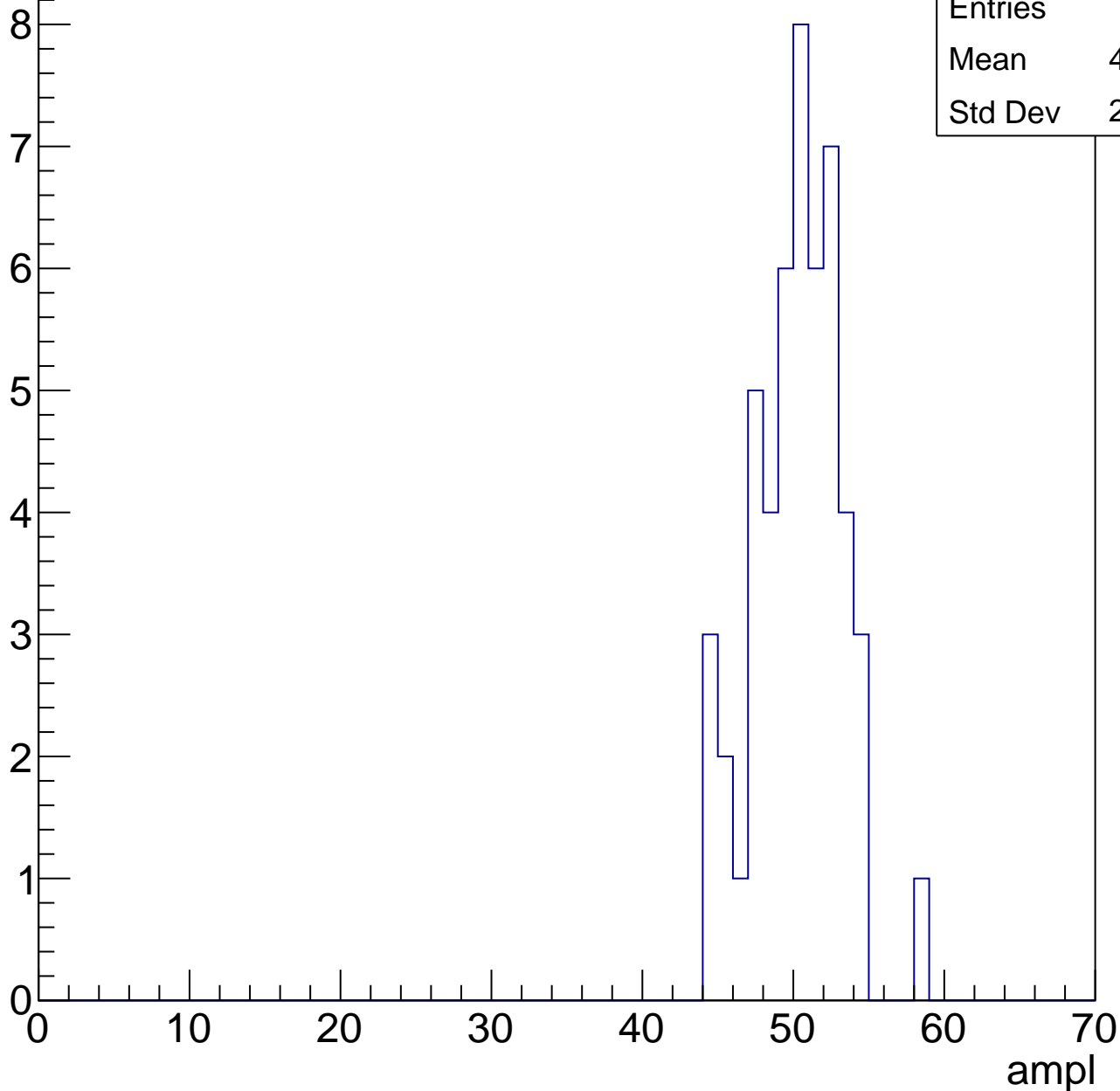


B1L102S, U14-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	49.82
Std Dev	2.903



B1L102S, U14-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

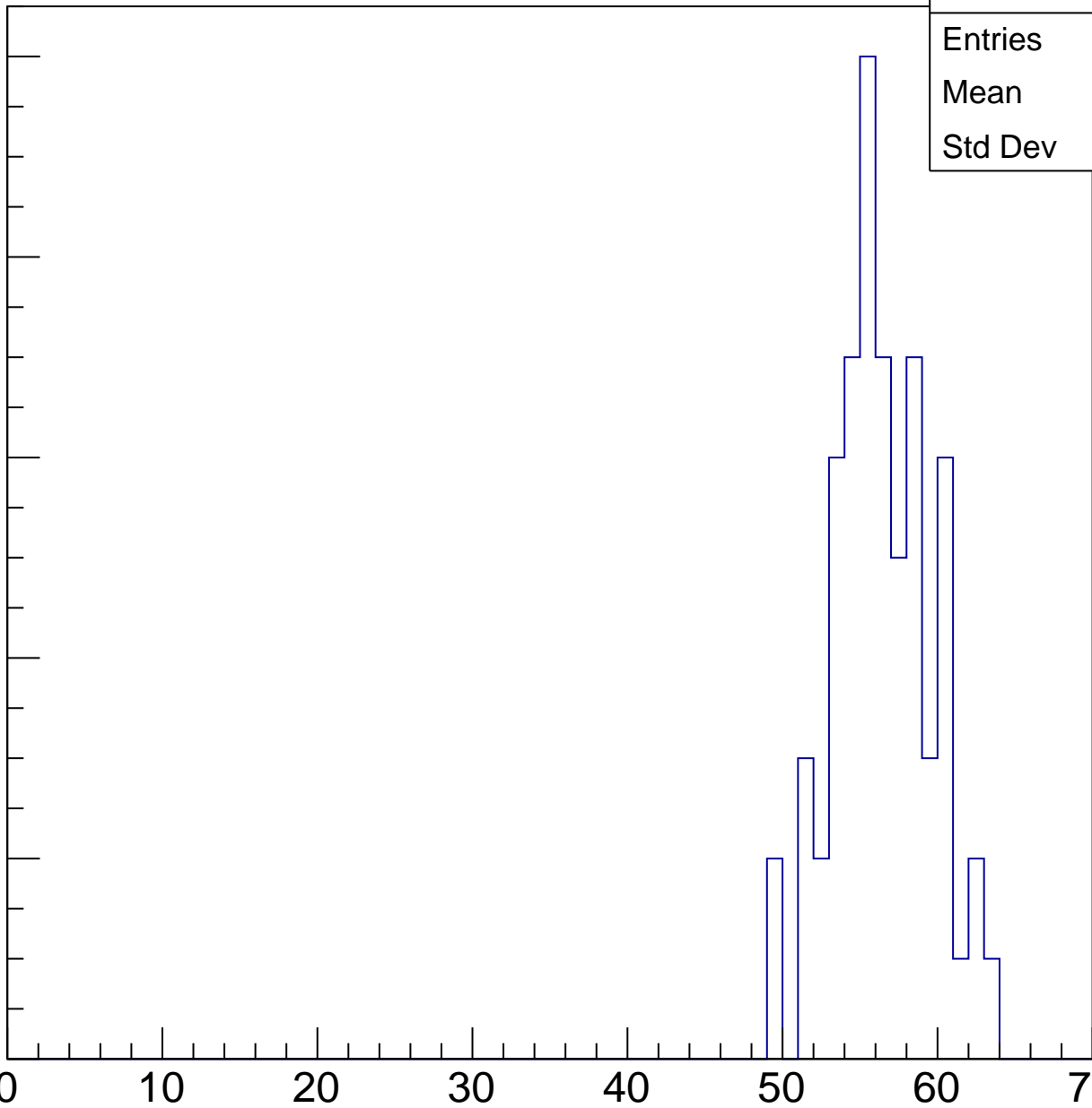
Entries	62
Mean	55.95
Std Dev	3.108

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U14-ch26, adc5

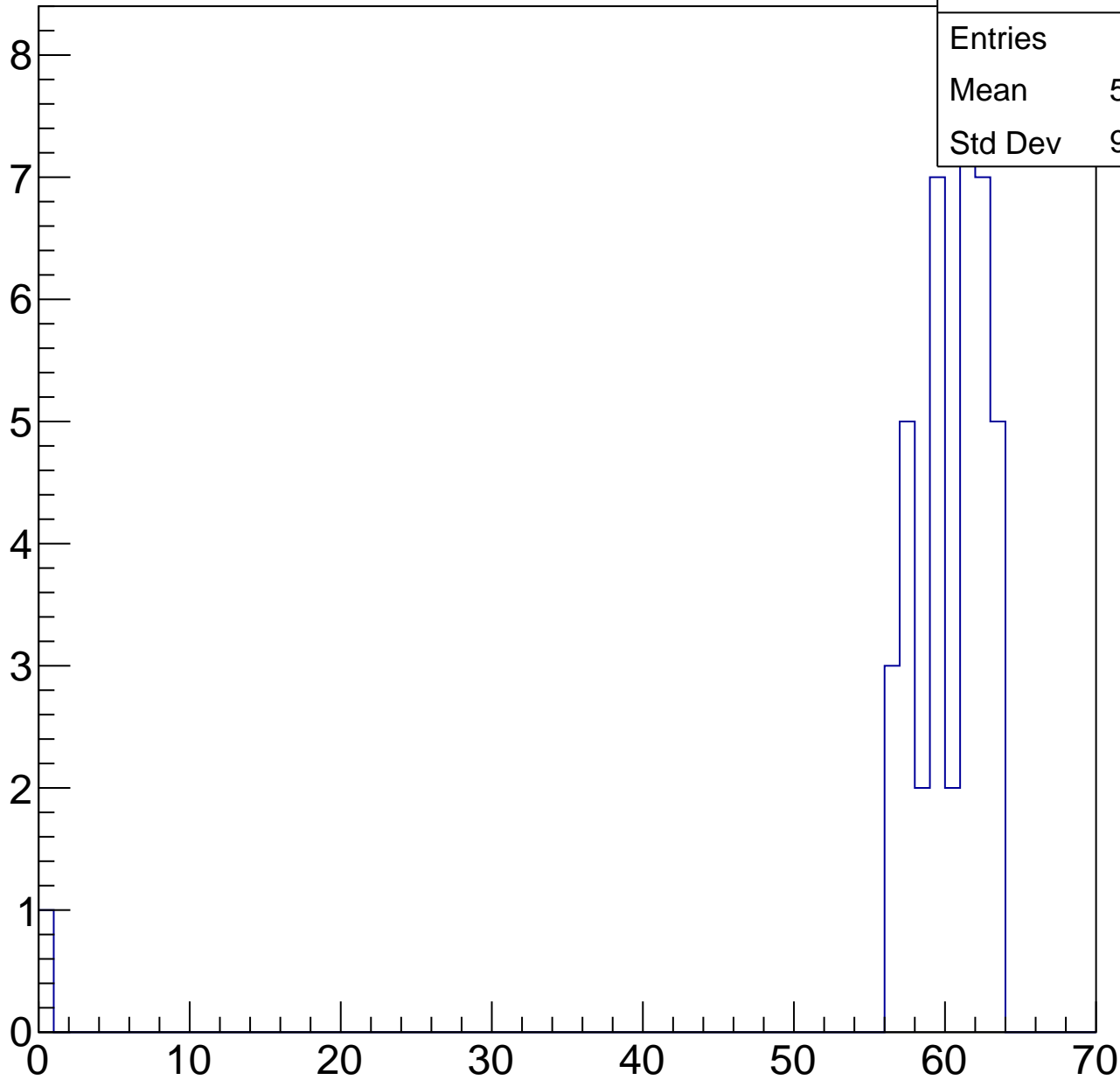
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.48
Std Dev	9.612

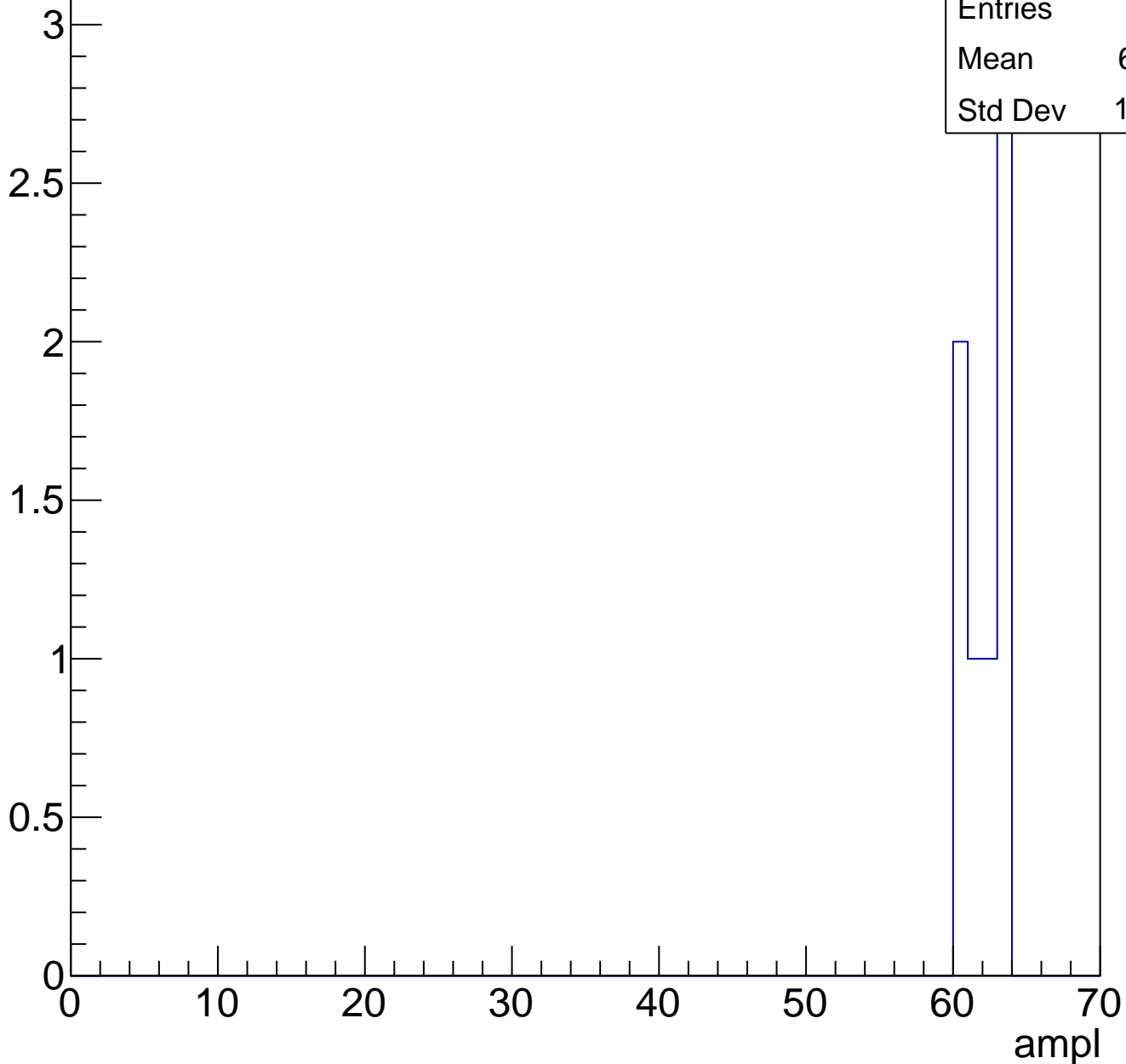
ampl



B1L102S, U14-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch27, adc0

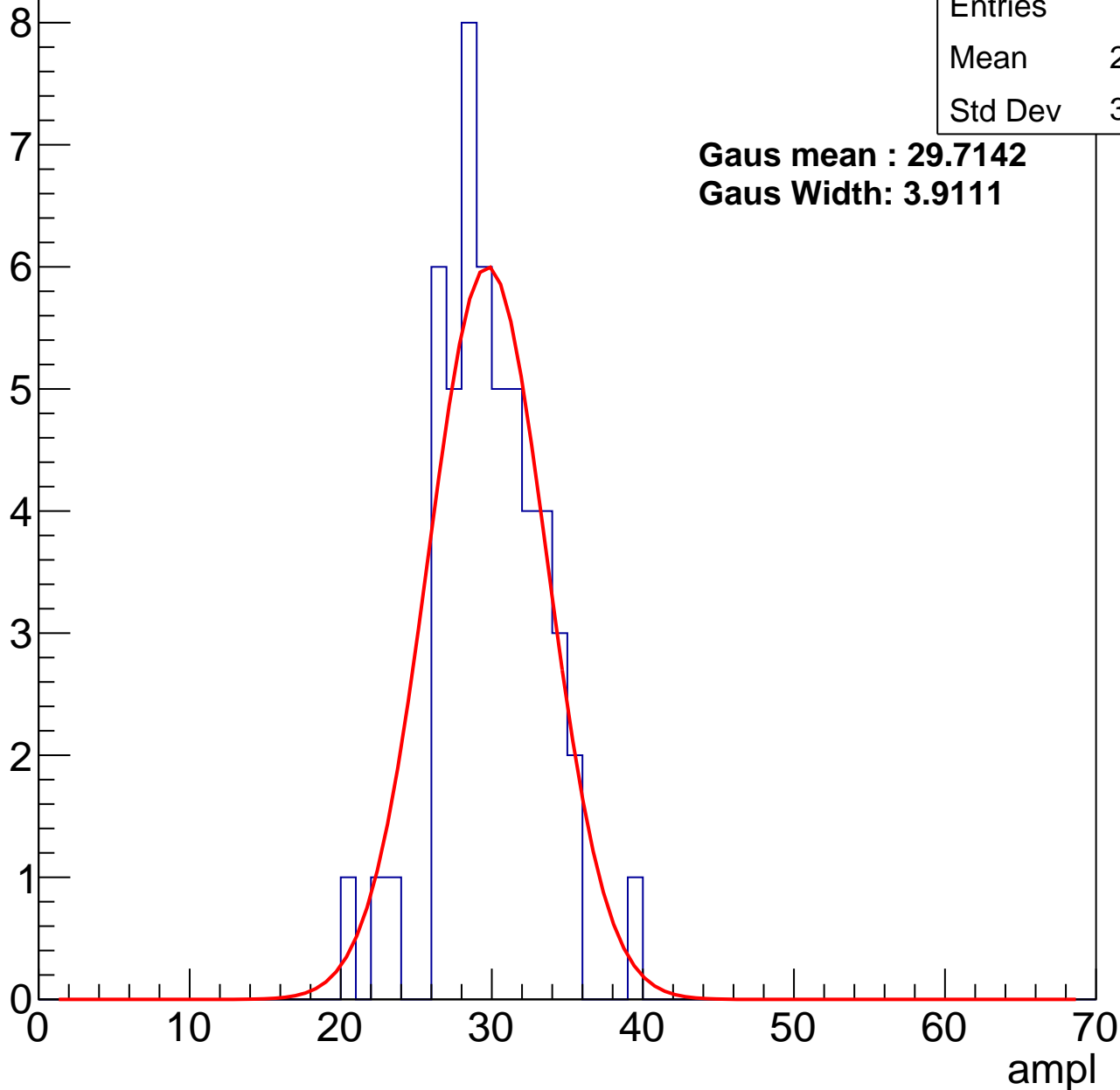
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	29.42
Std Dev	3.427

Gaus mean : 29.7142

Gaus Width: 3.9111



B1L102S, U14-ch27, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	35.25
Std Dev	3.399

Gaus mean : 35.3724

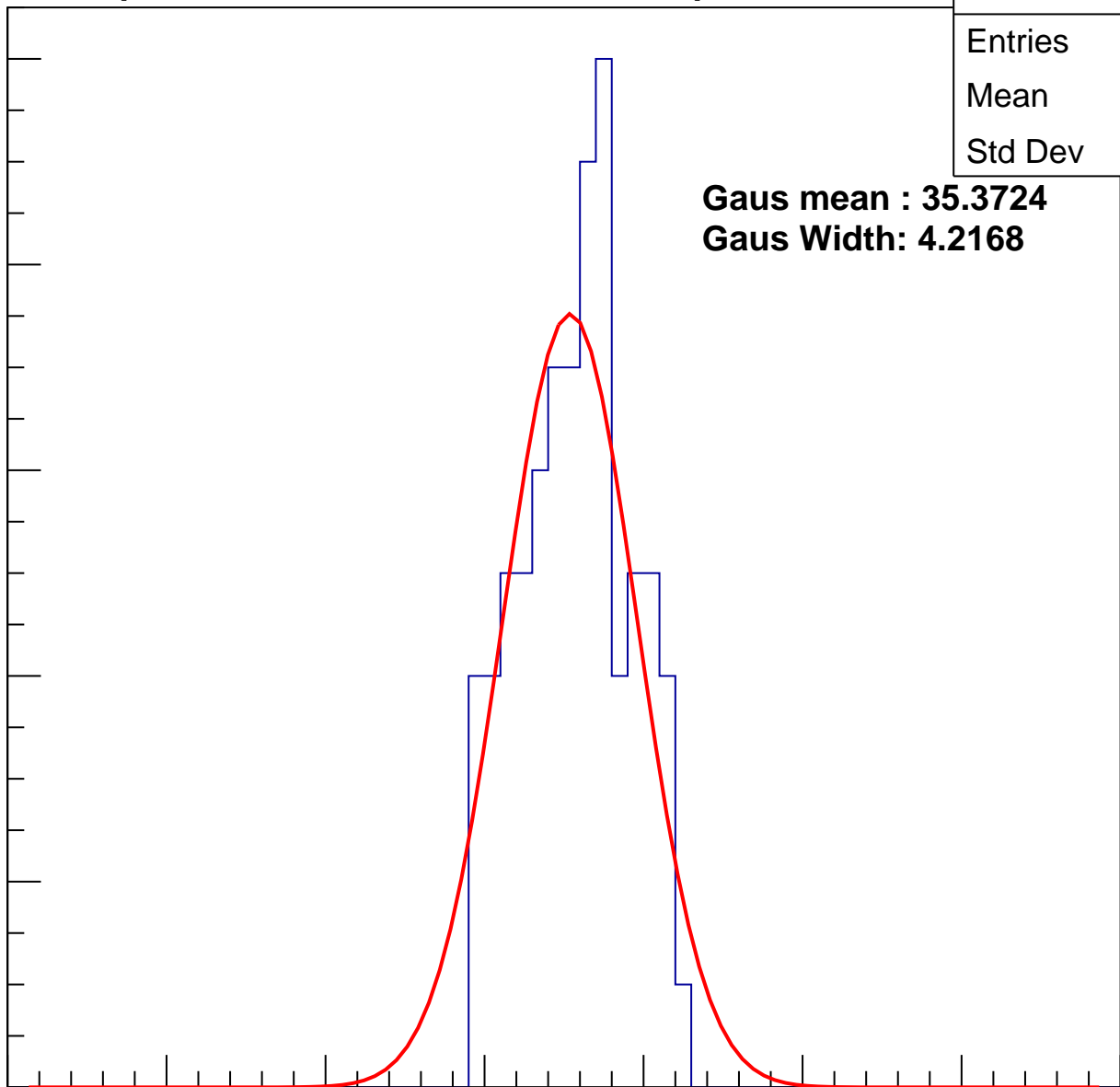
Gaus Width: 4.2168

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch27, adc2

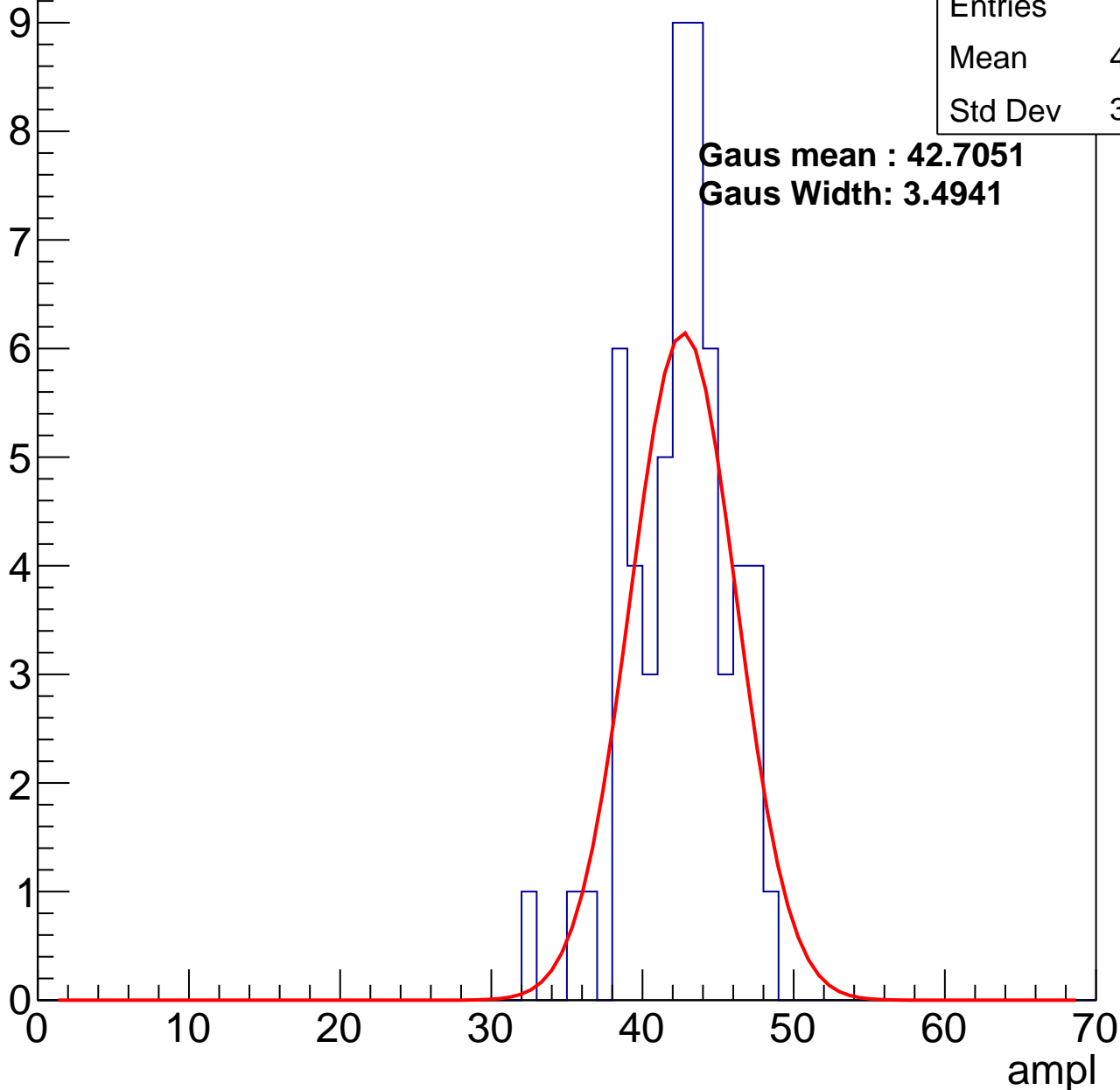
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.04
Std Dev	3.228

Gaus mean : 42.7051

Gaus Width: 3.4941

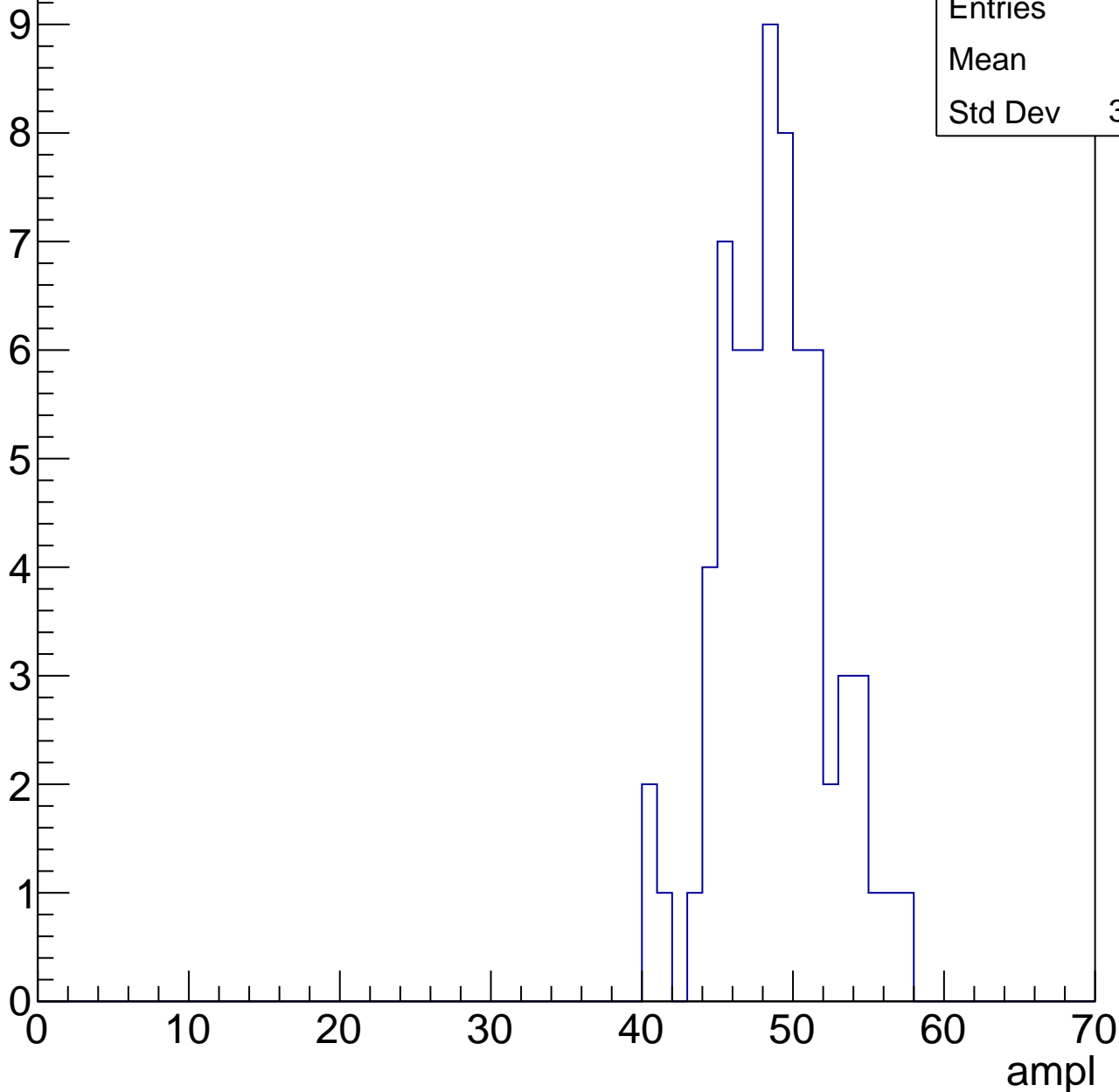


B1L102S, U14-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.3
Std Dev	3.558

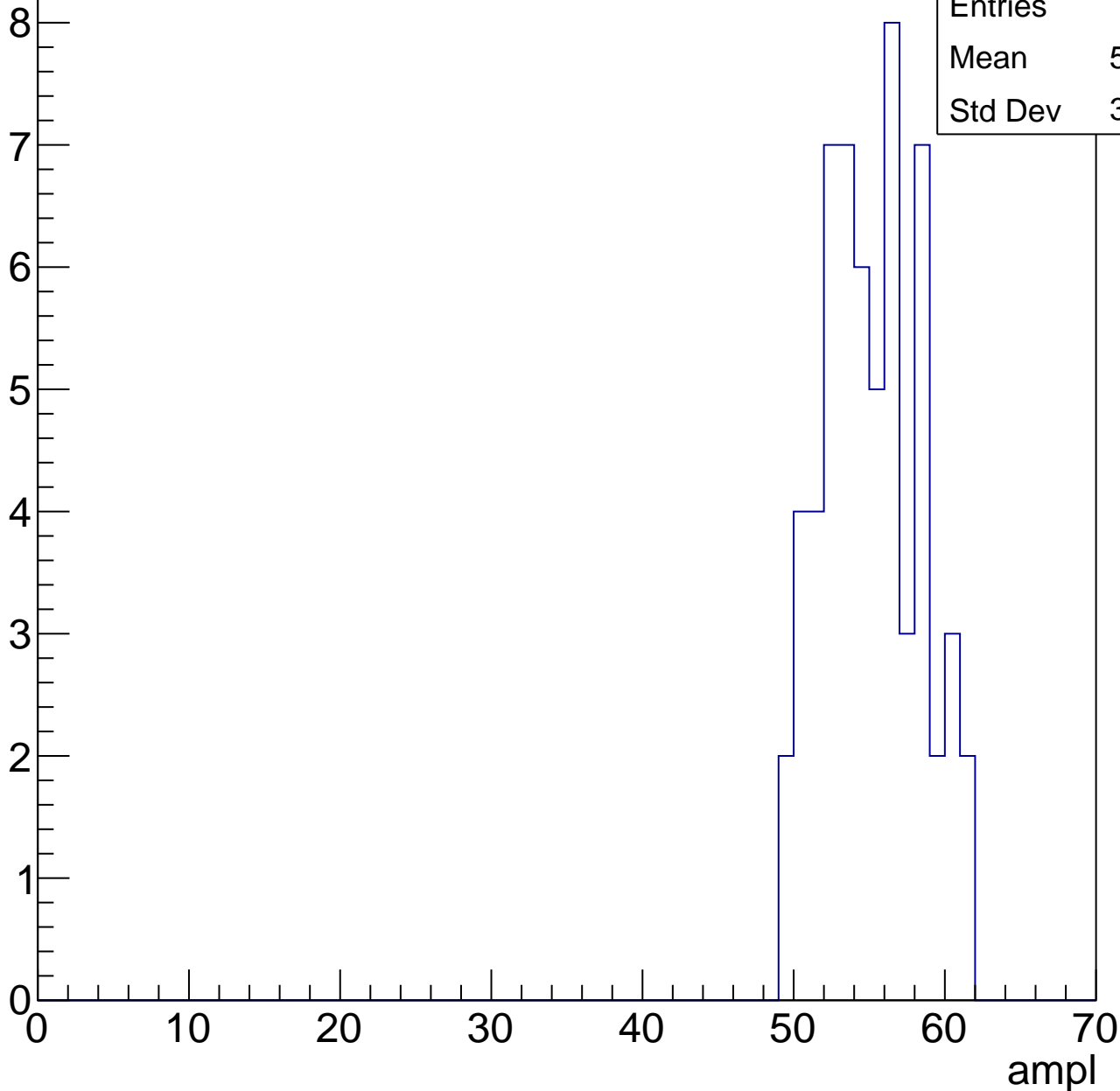


B1L102S, U14-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.68
Std Dev	3.133



B1L102S, U14-ch27, adc5

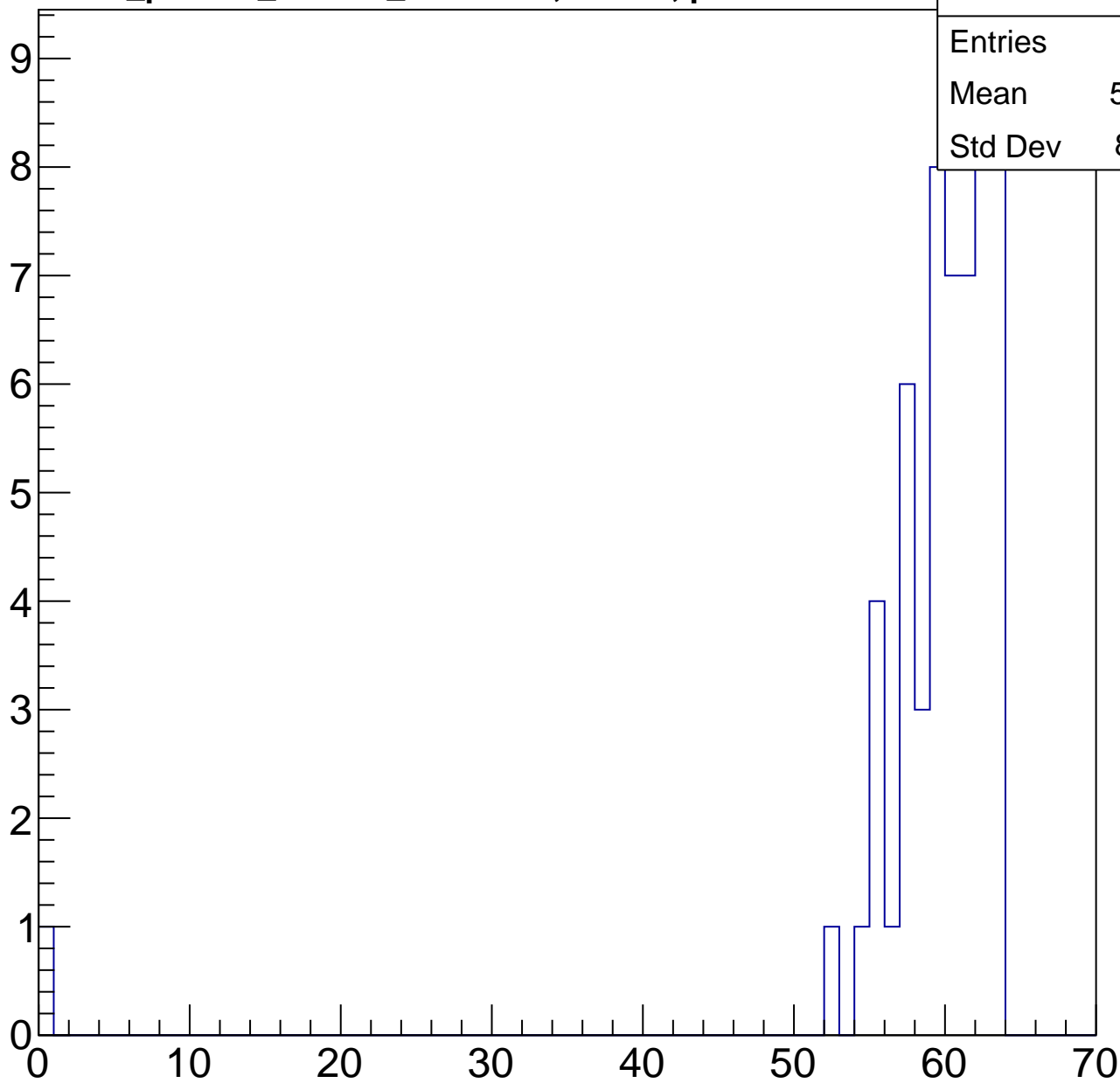
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.57
Std Dev	8.341

ampl



B1L102S, U14-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U14-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U14-ch28, adc0

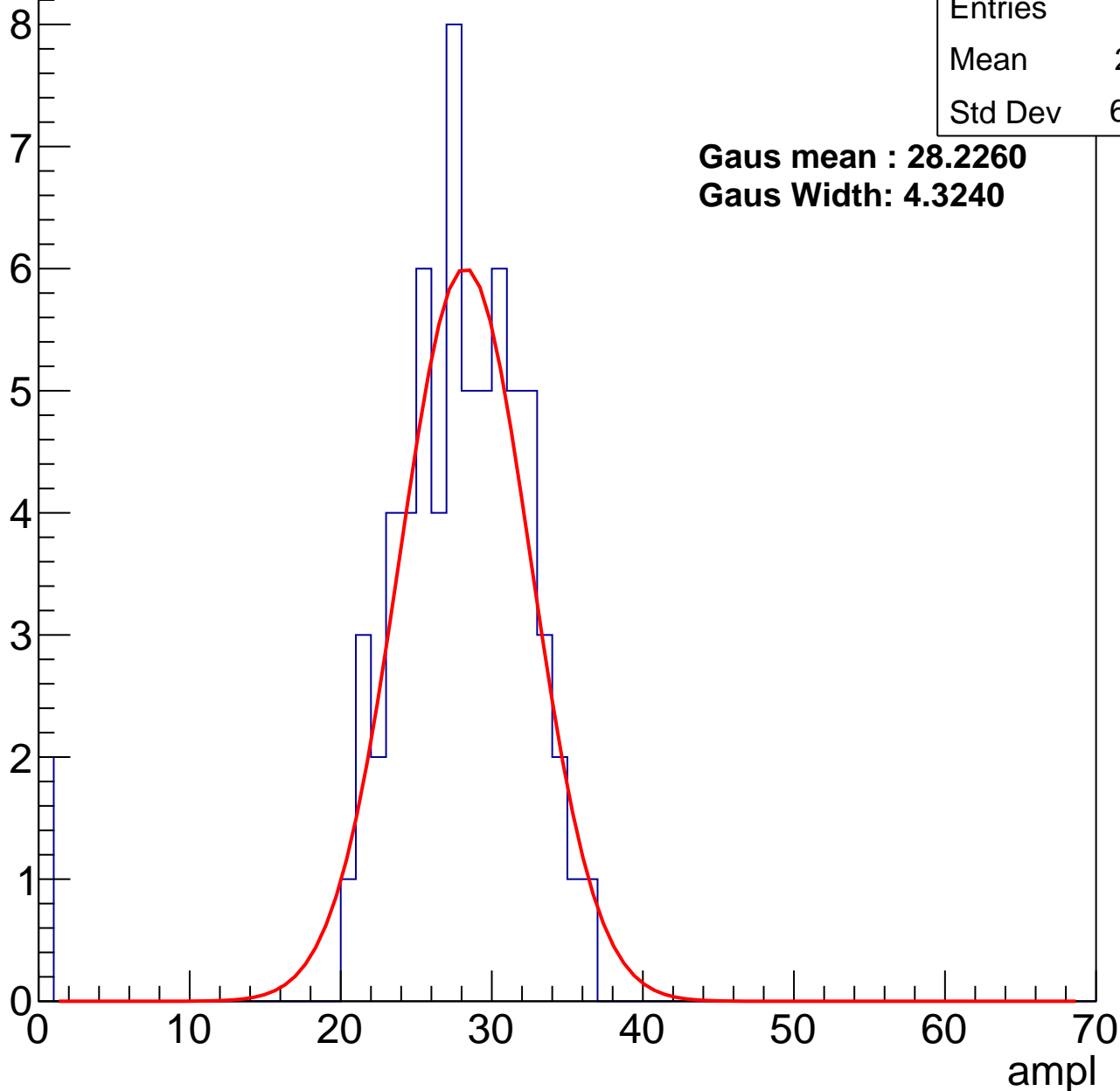
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	26.91
Std Dev	6.019

Gaus mean : 28.2260

Gaus Width: 4.3240



B1L102S, U14-ch28, adc1

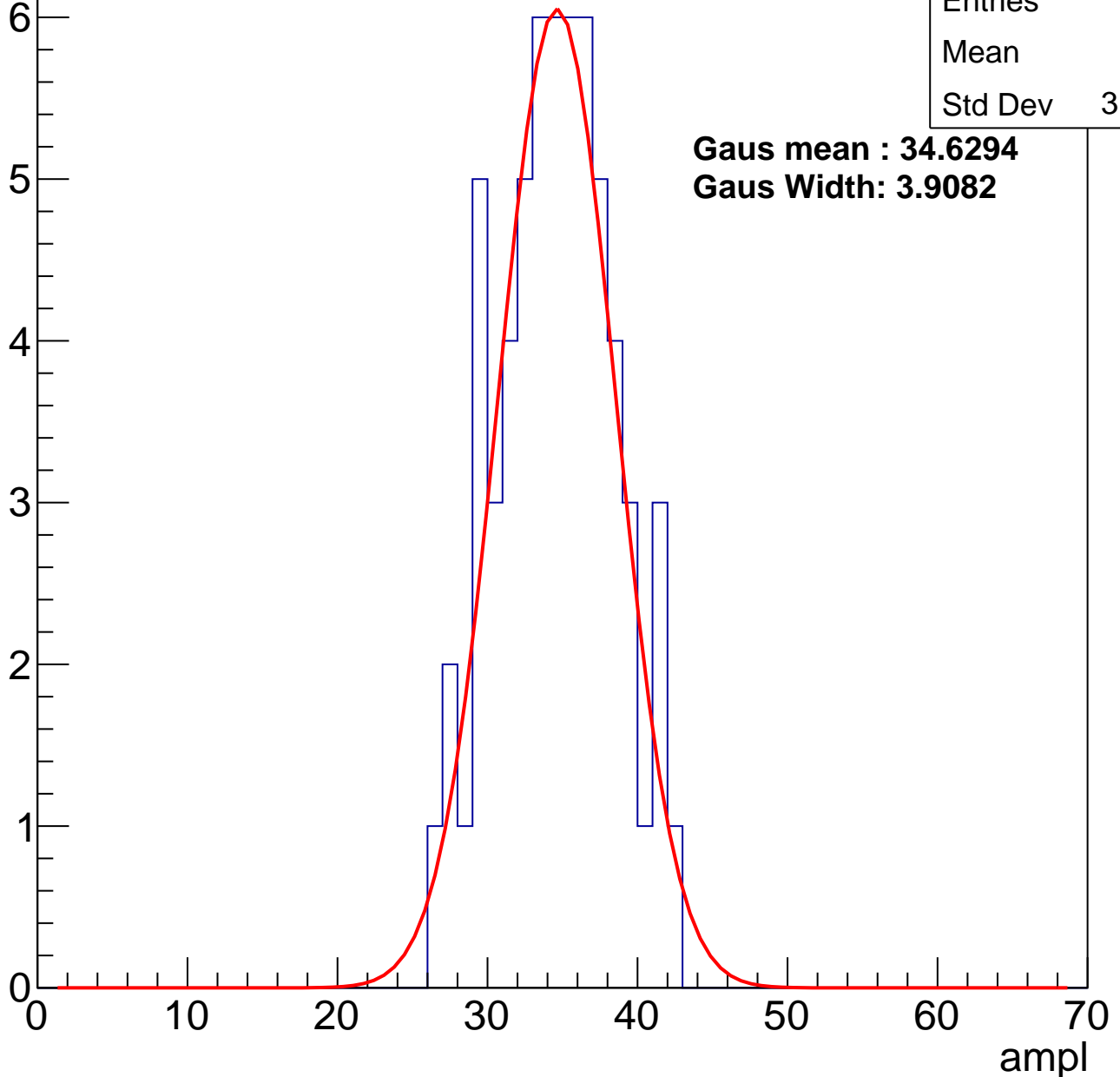
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.1
Std Dev	3.796

Gaus mean : 34.6294

Gaus Width: 3.9082



B1L102S, U14-ch28, adc2

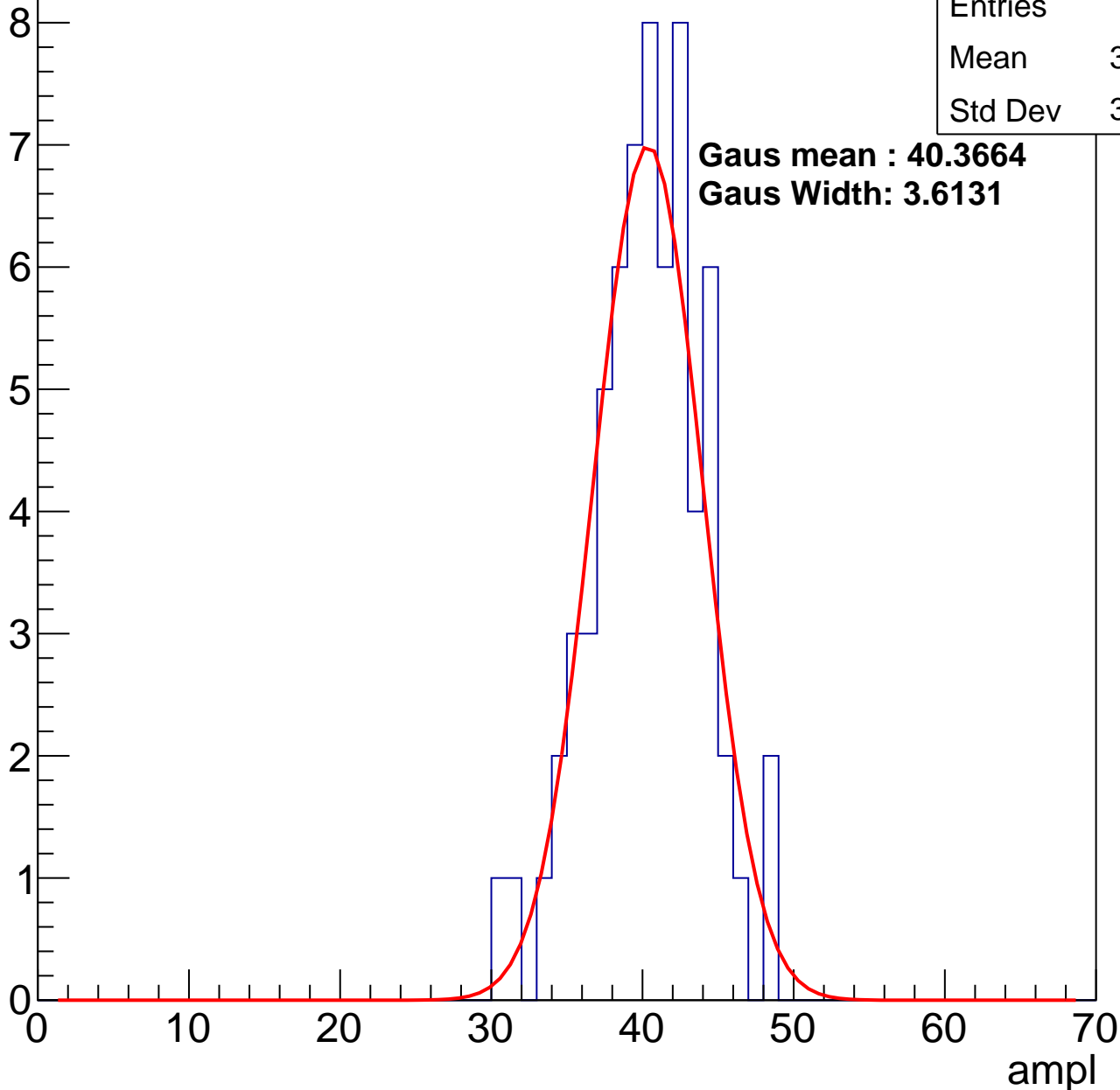
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	39.86
Std Dev	3.659

Gaus mean : 40.3664

Gaus Width: 3.6131



B1L102S, U14-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

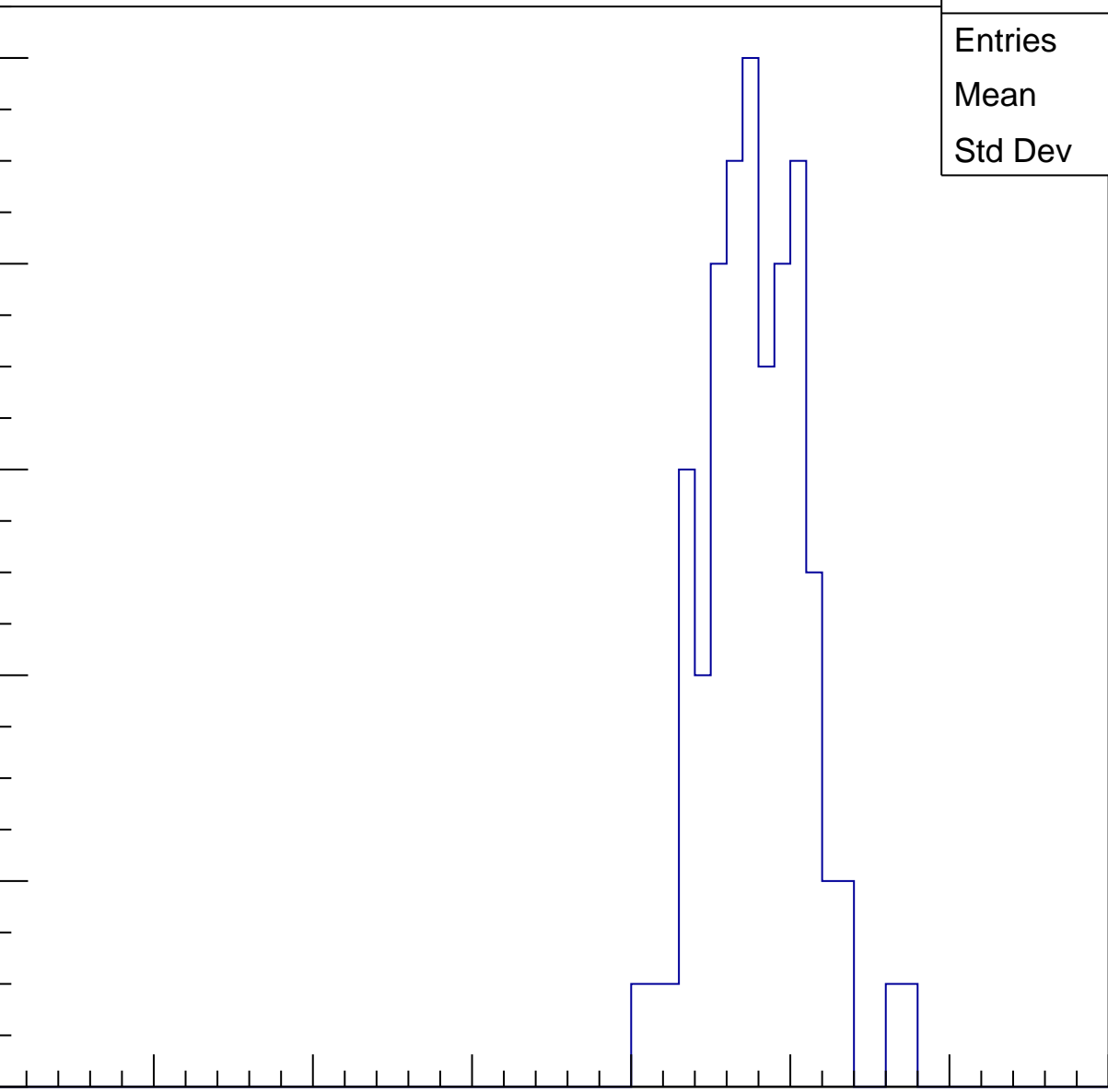
Entries	75
Mean	47.43
Std Dev	3.209

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

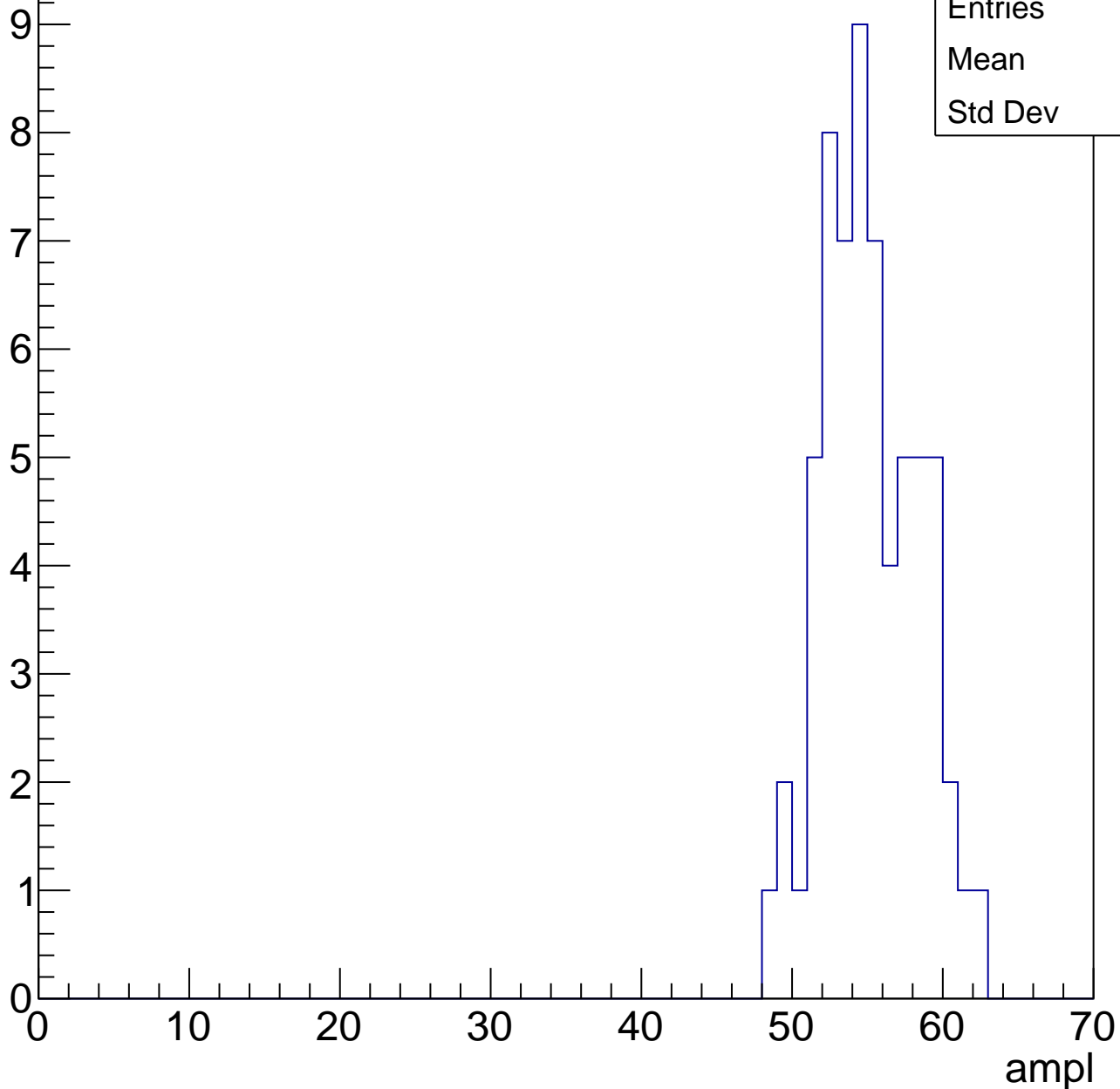
ampl



B1L102S, U14-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



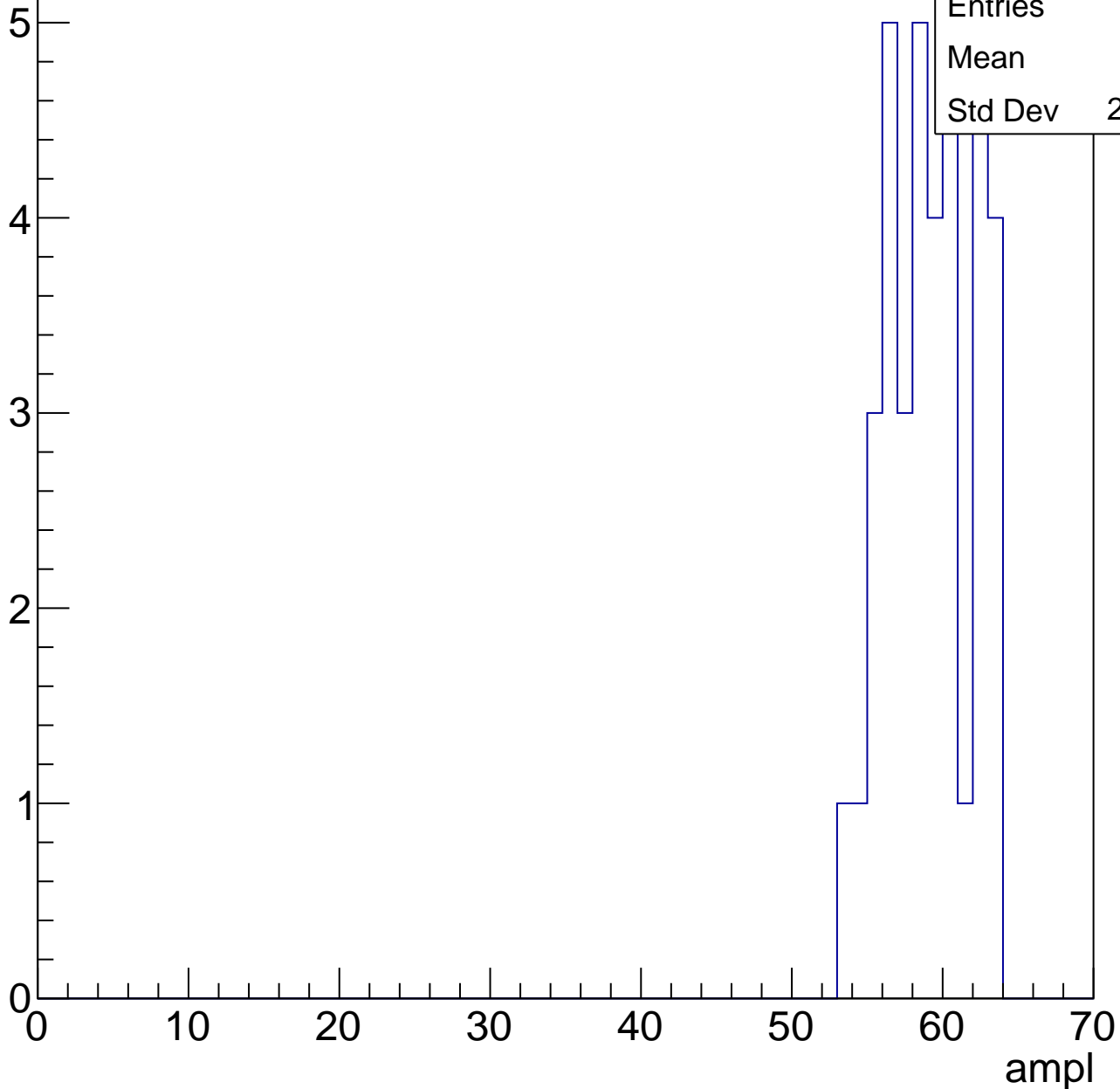
Entries	63
Mean	54.7
Std Dev	3.12

B1L102S, U14-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.7
Std Dev	2.779

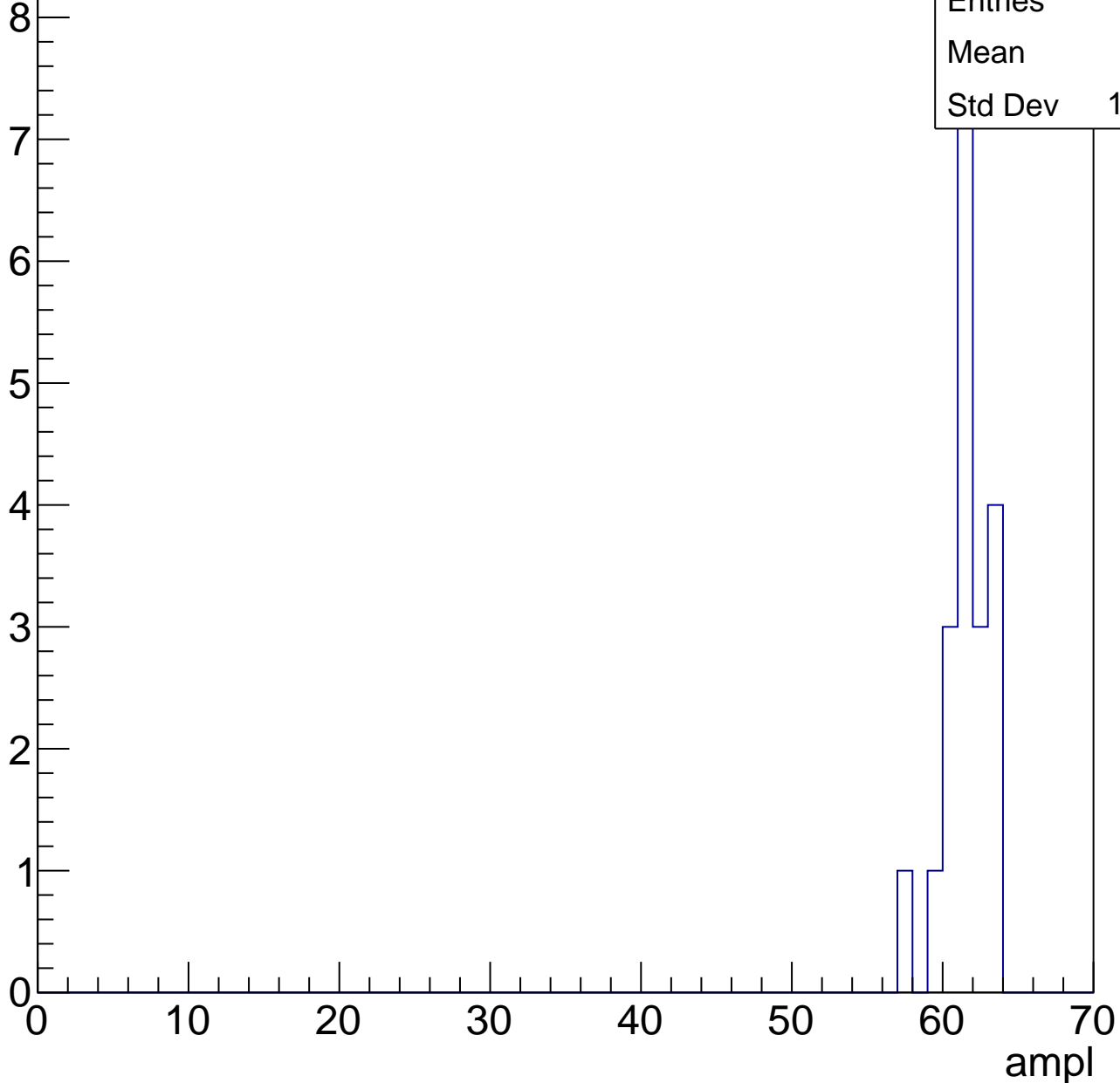


B1L102S, U14-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	61.1
Std Dev	1.446



B1L102S, U14-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch29, adc0

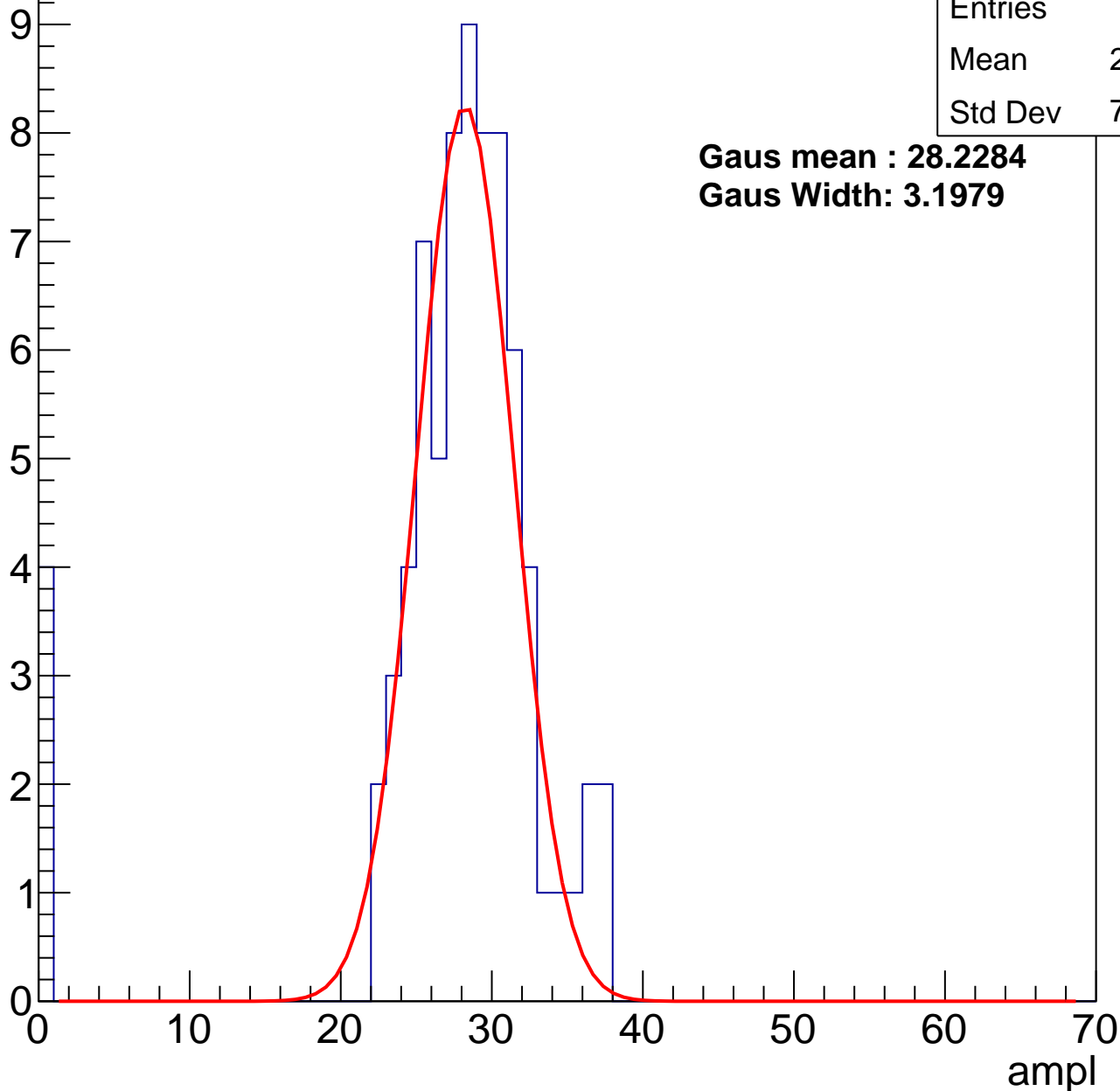
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	26.88
Std Dev	7.213

Gaus mean : 28.2284

Gaus Width: 3.1979



B1L102S, U14-ch29, adc1

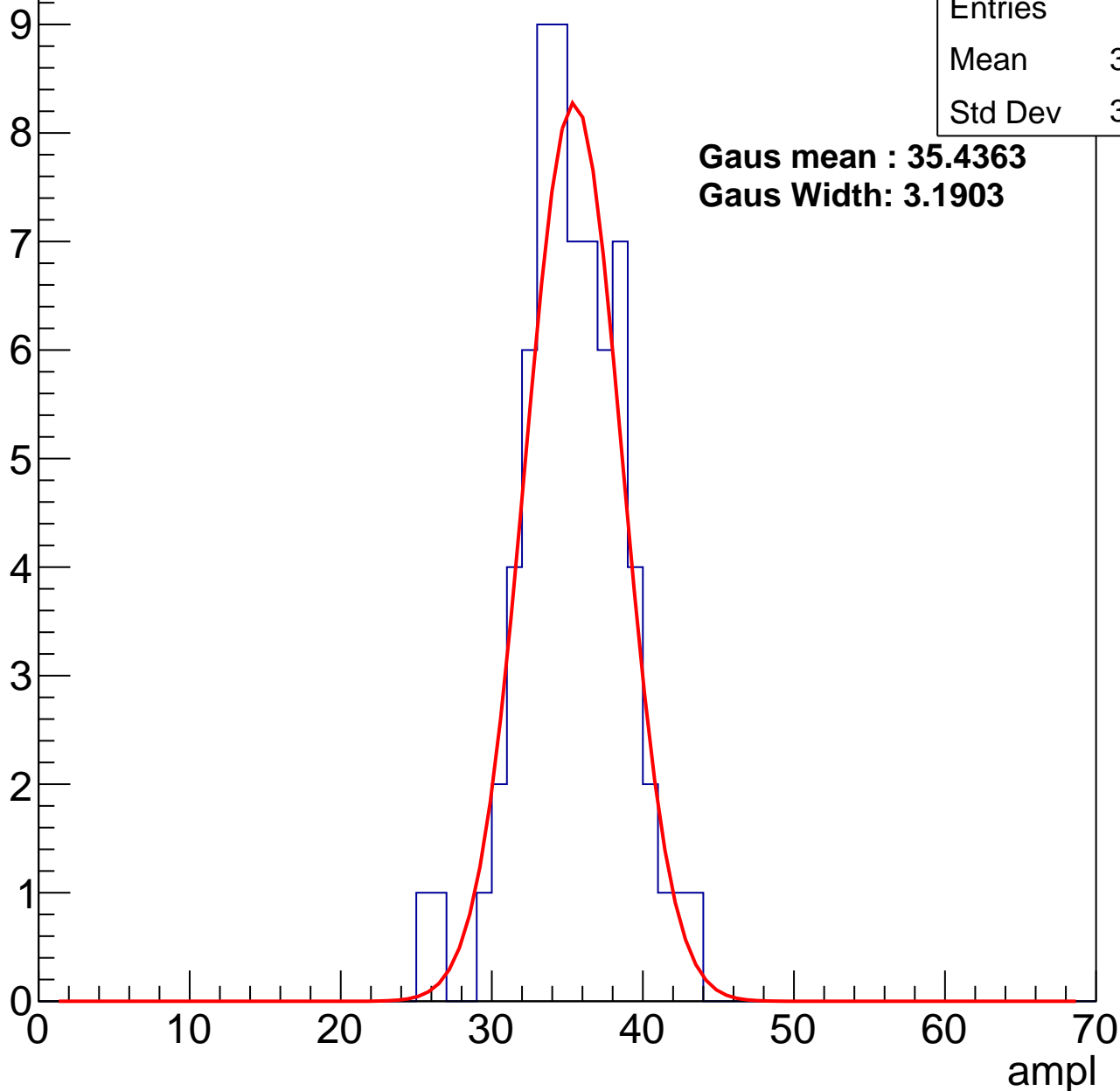
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.87
Std Dev	3.375

Gaus mean : 35.4363

Gaus Width: 3.1903



B1L102S, U14-ch29, adc2

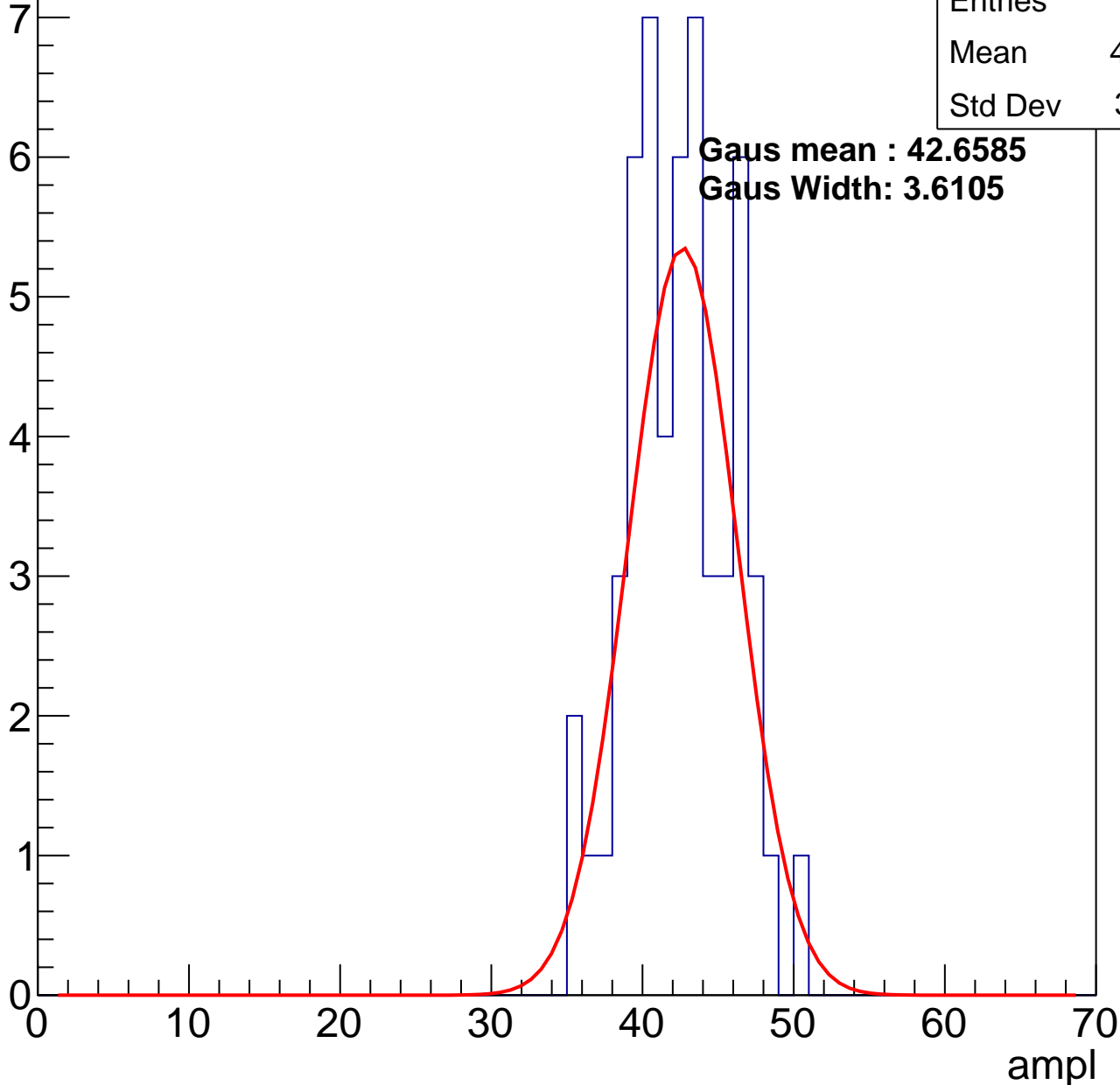
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42.04
Std Dev	3.361

Gaus mean : 42.6585

Gaus Width: 3.6105

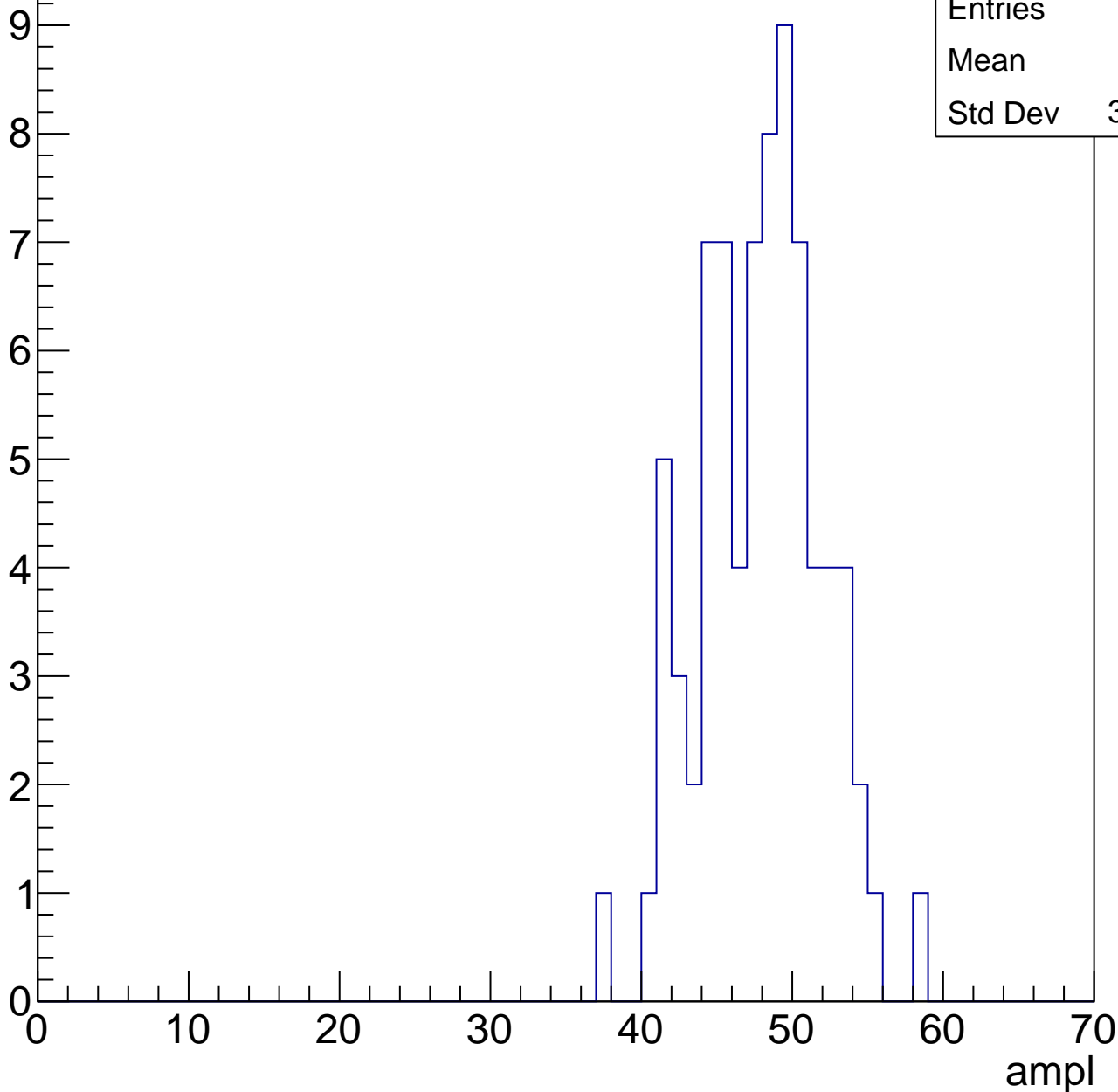


B1L102S, U14-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	47.4
Std Dev	3.975

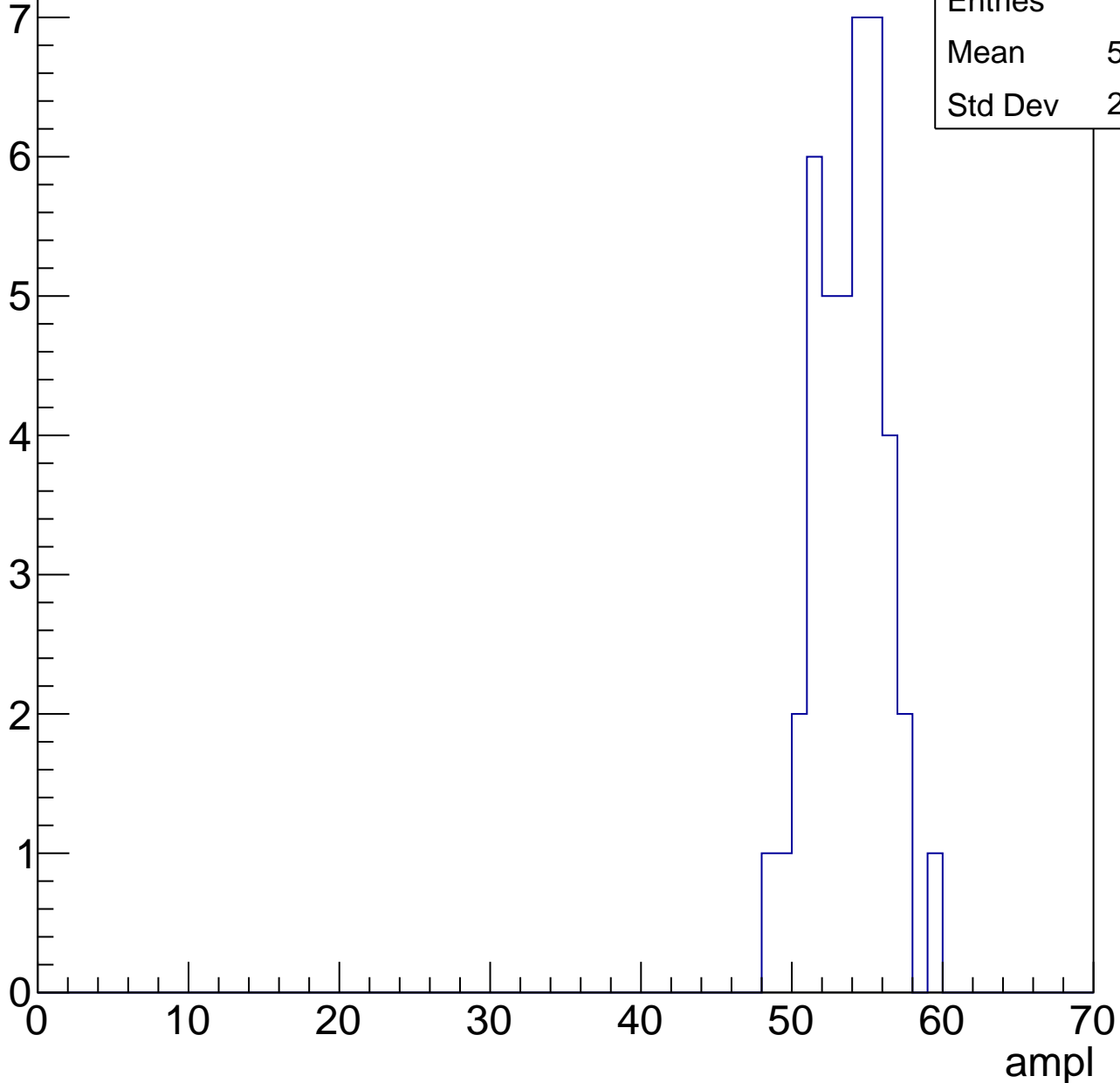


B1L102S, U14-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

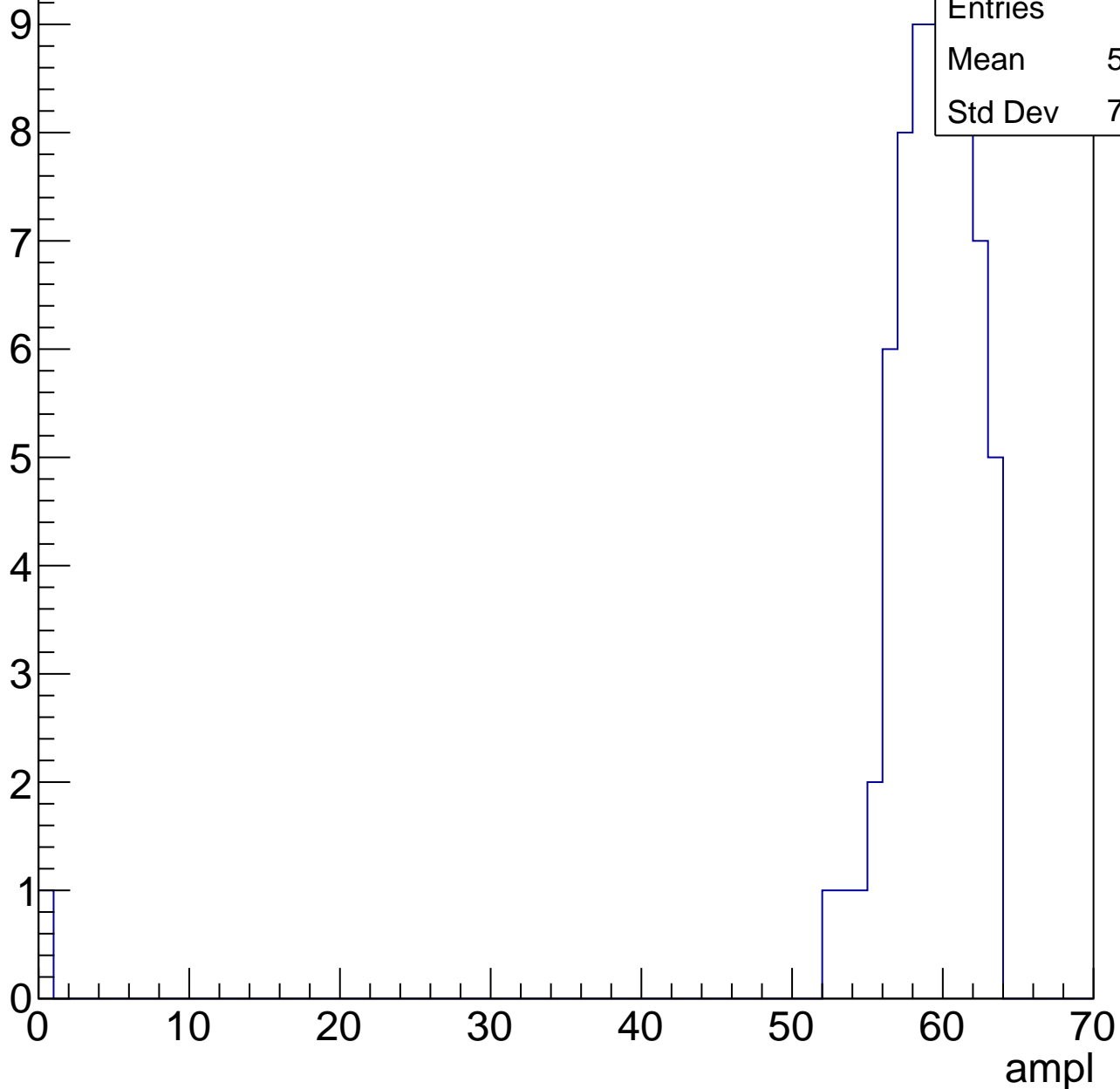
Entries	41
Mean	53.37
Std Dev	2.324



B1L102S, U14-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

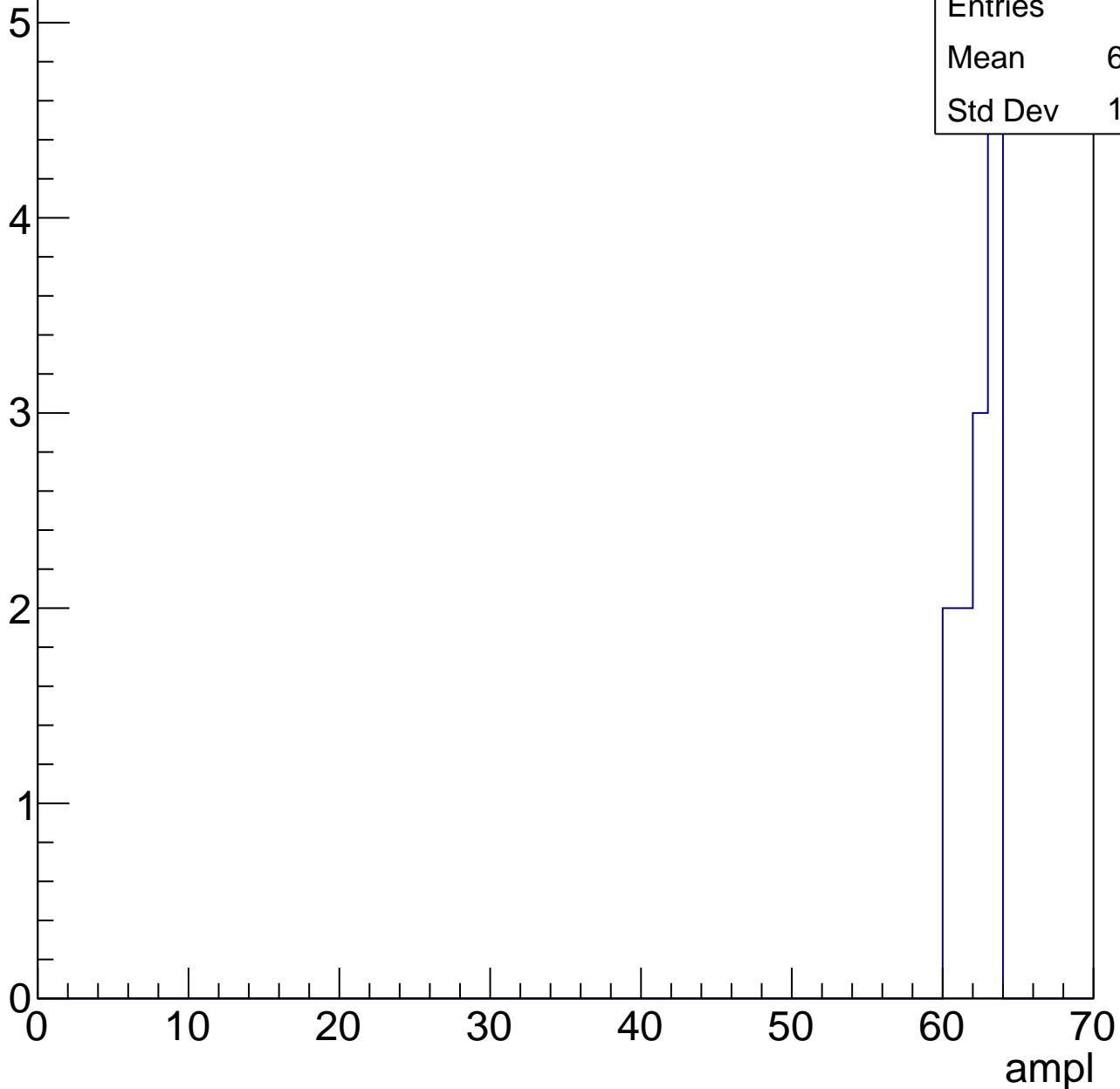


B1L102S, U14-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.92
Std Dev	1.115



B1L102S, U14-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch30, adc0

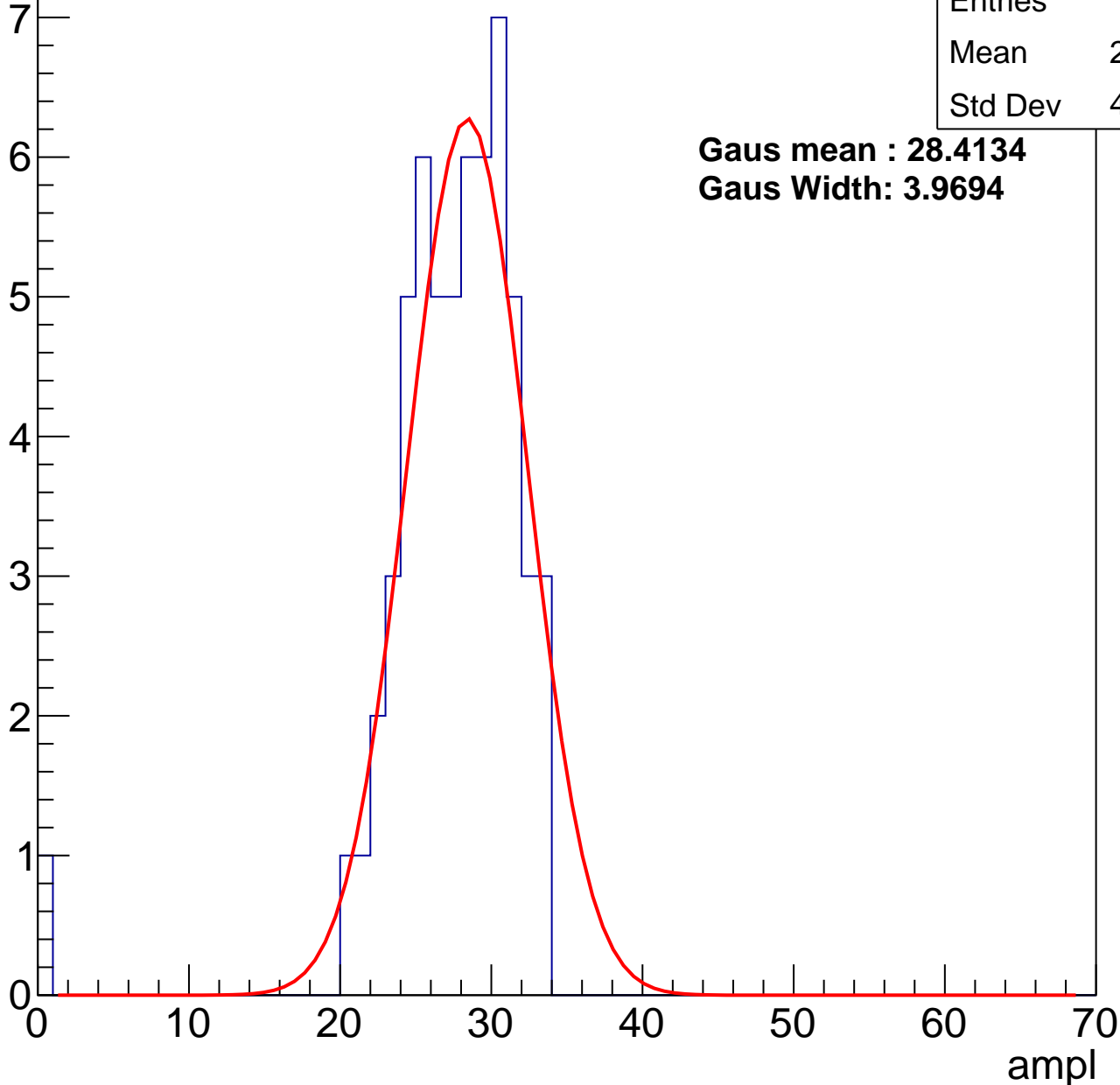
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	26.97
Std Dev	4.769

Gaus mean : 28.4134

Gaus Width: 3.9694



B1L102S, U14-ch30, adc1

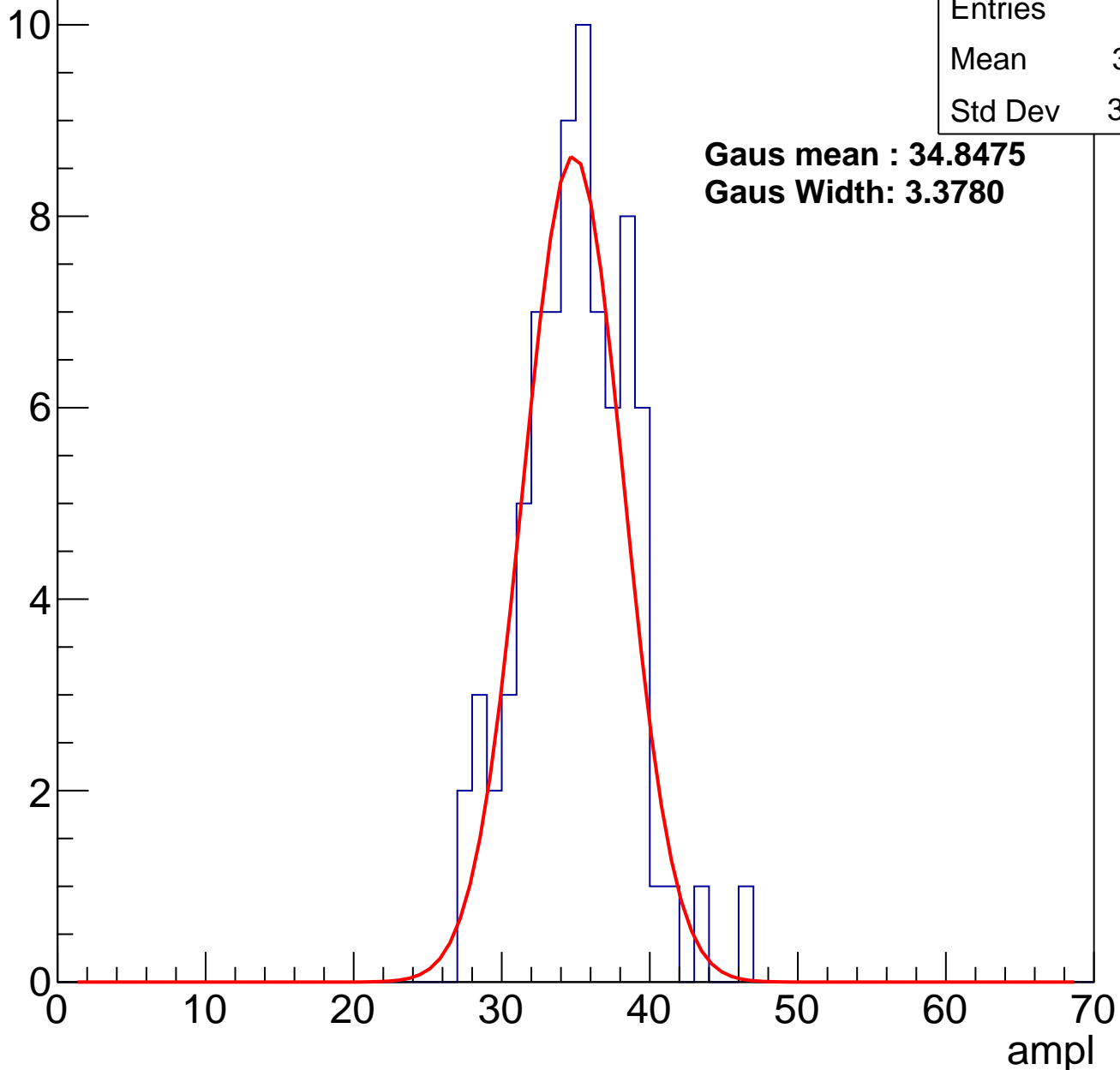
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	34.61
Std Dev	3.619

Gaus mean : 34.8475

Gaus Width: 3.3780

Entry



B1L102S, U14-ch30, adc2

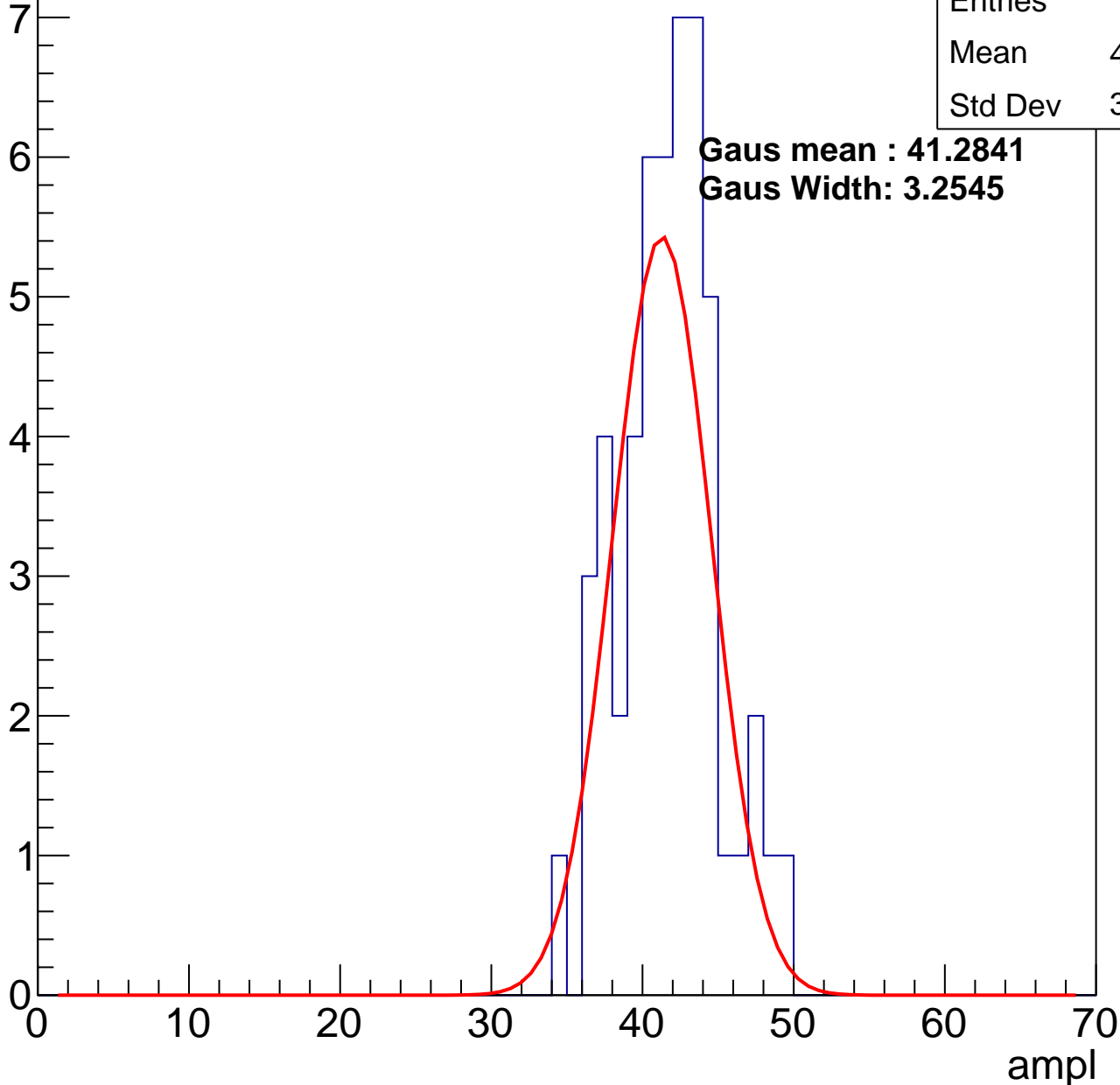
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.27
Std Dev	3.224

Gaus mean : 41.2841

Gaus Width: 3.2545

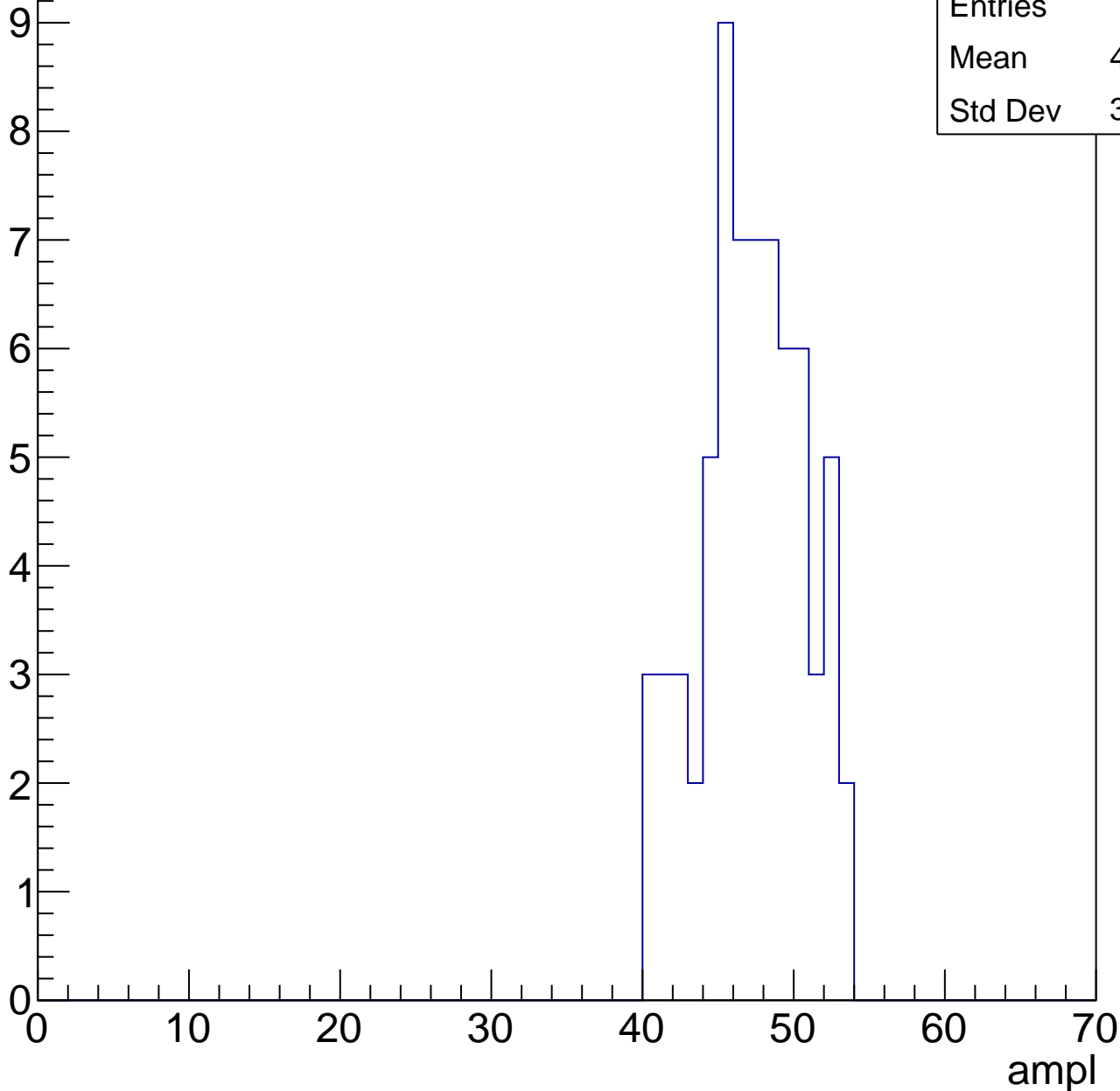


B1L102S, U14-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	46.76
Std Dev	3.379

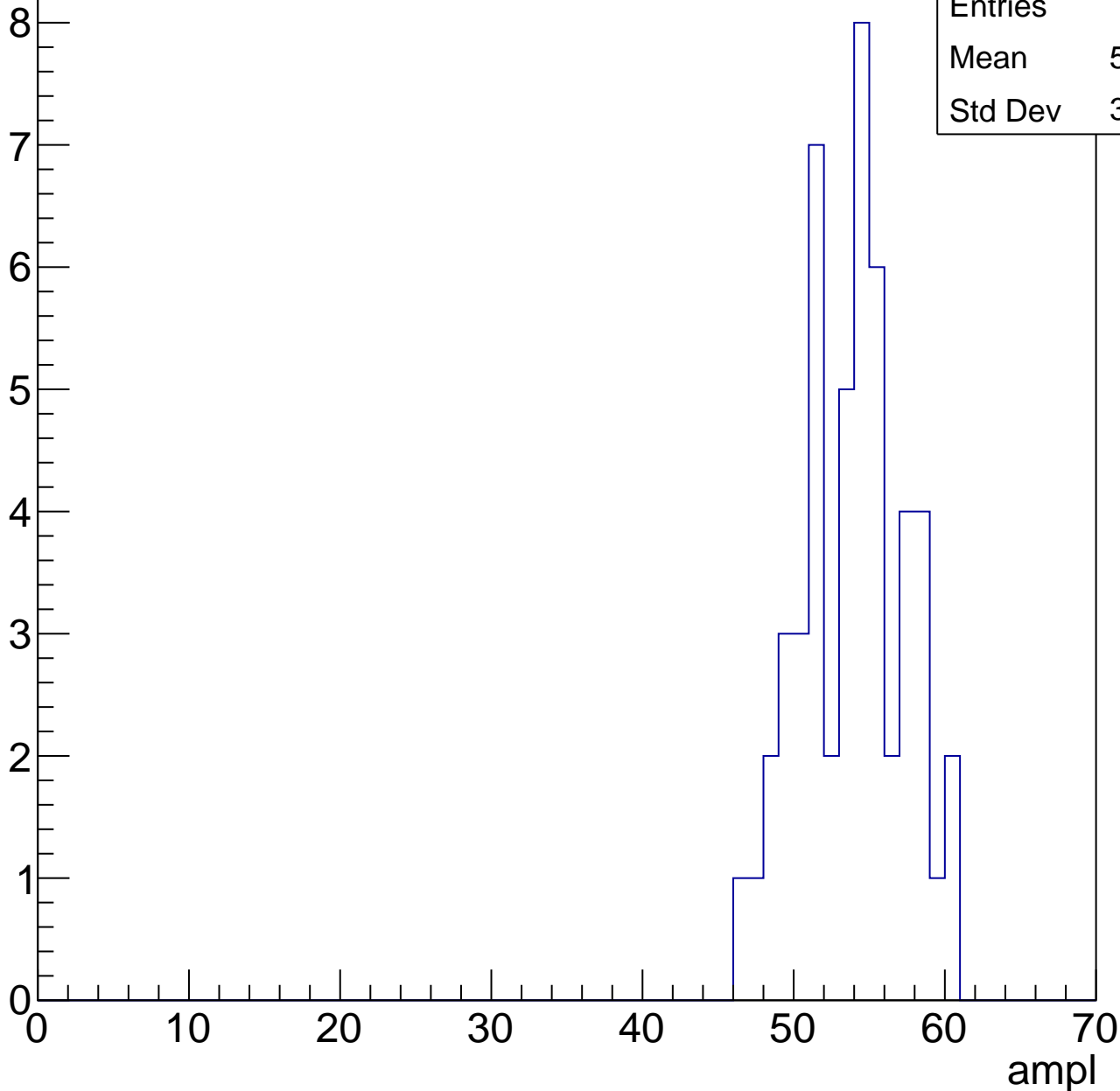


B1L102S, U14-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

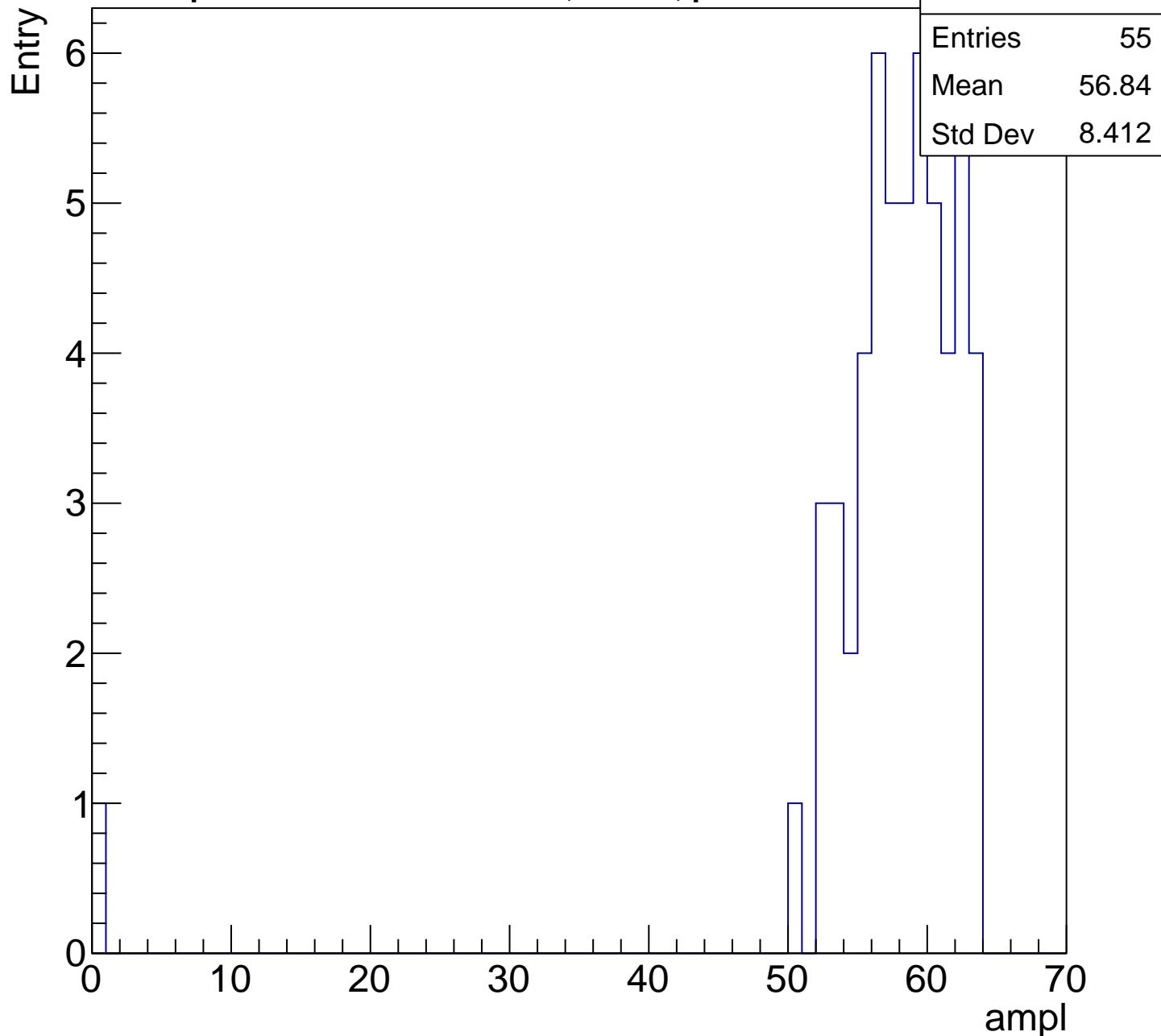
Entry

Entries	51
Mean	53.43
Std Dev	3.368



B1L102S, U14-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

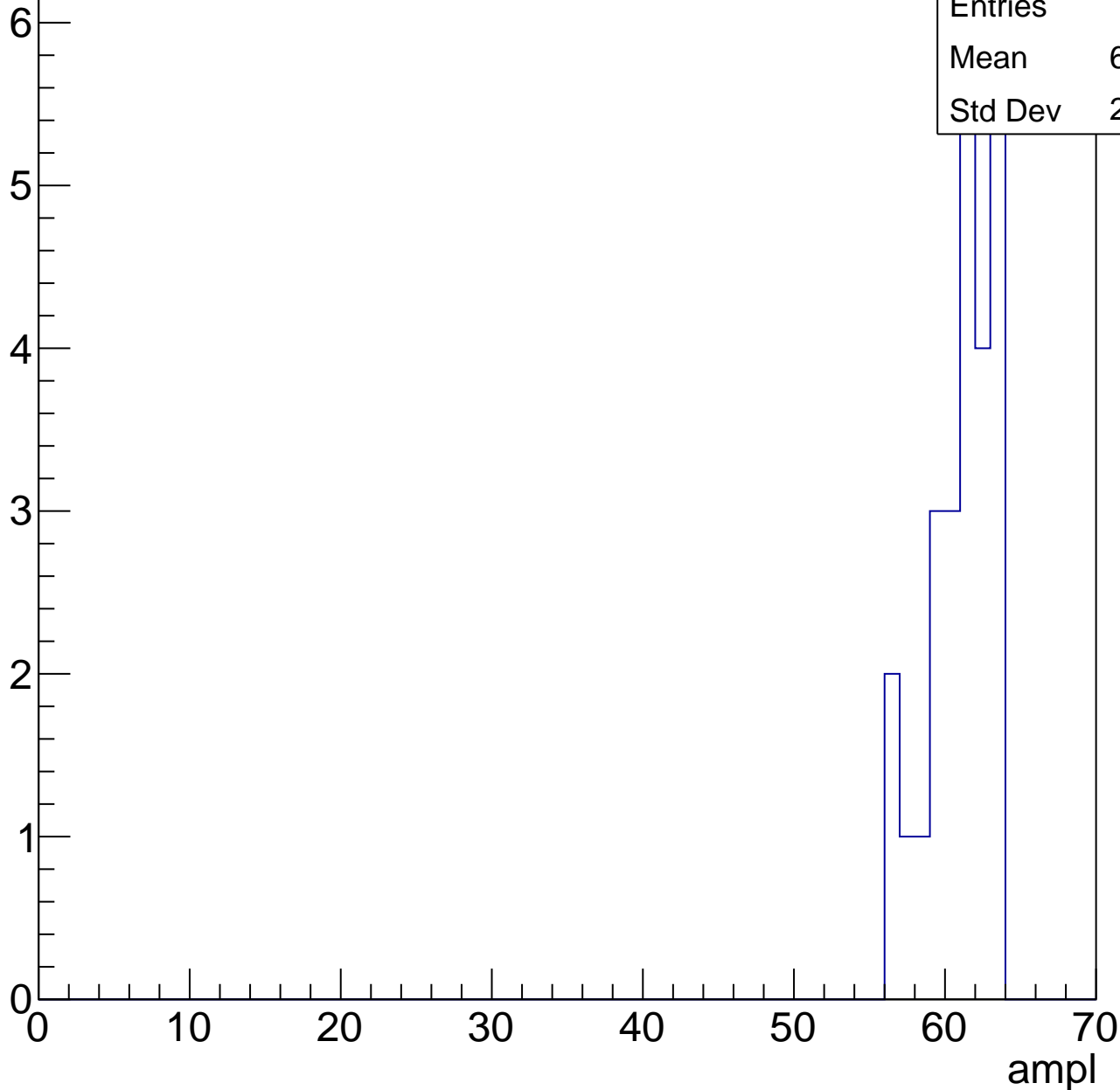


B1L102S, U14-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	60.62
Std Dev	2.095



B1L102S, U14-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch31, adc0

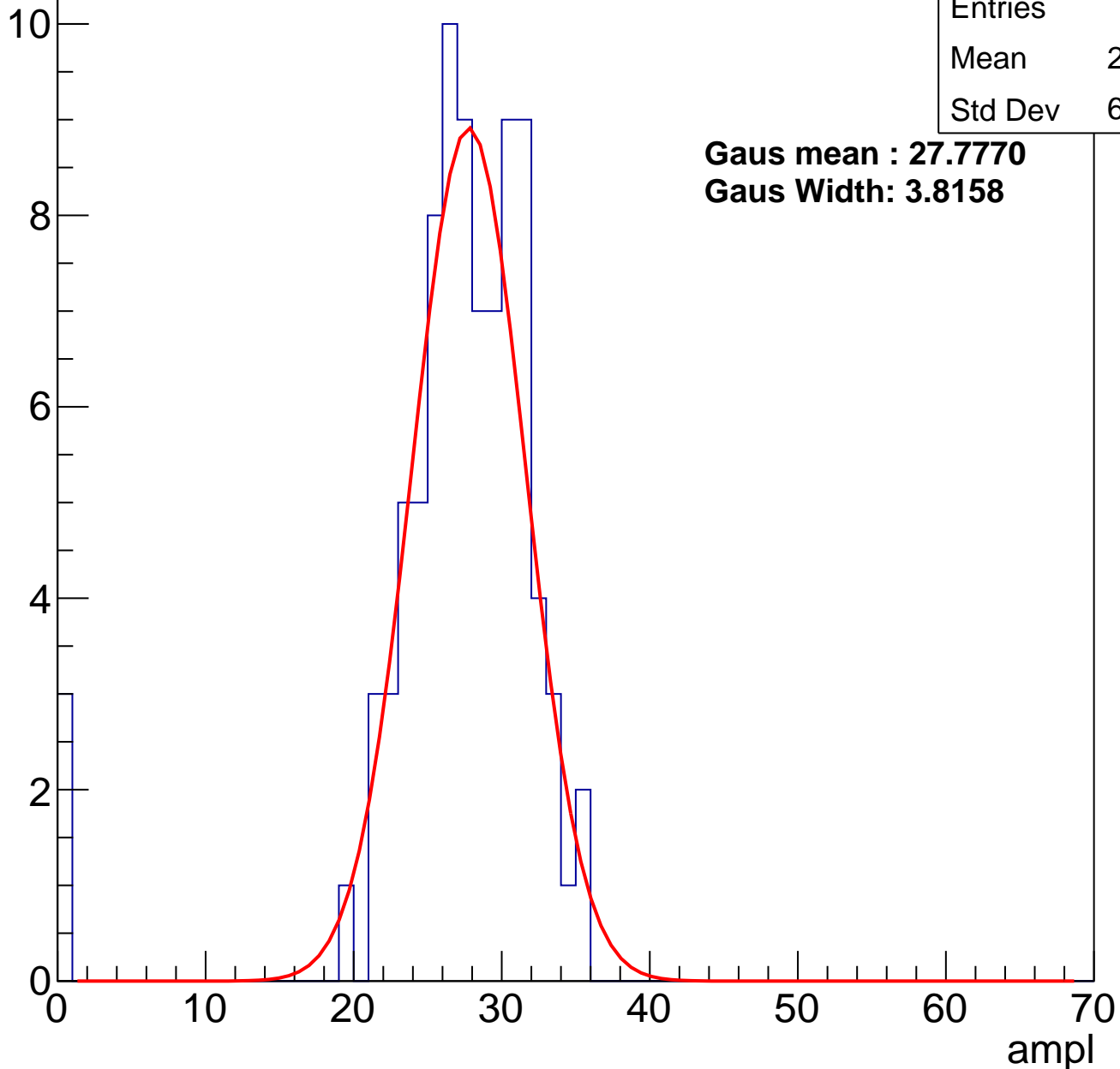
calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	26.57
Std Dev	6.024

Gaus mean : 27.7770

Gaus Width: 3.8158

Entry



B1L102S, U14-ch31, adc1

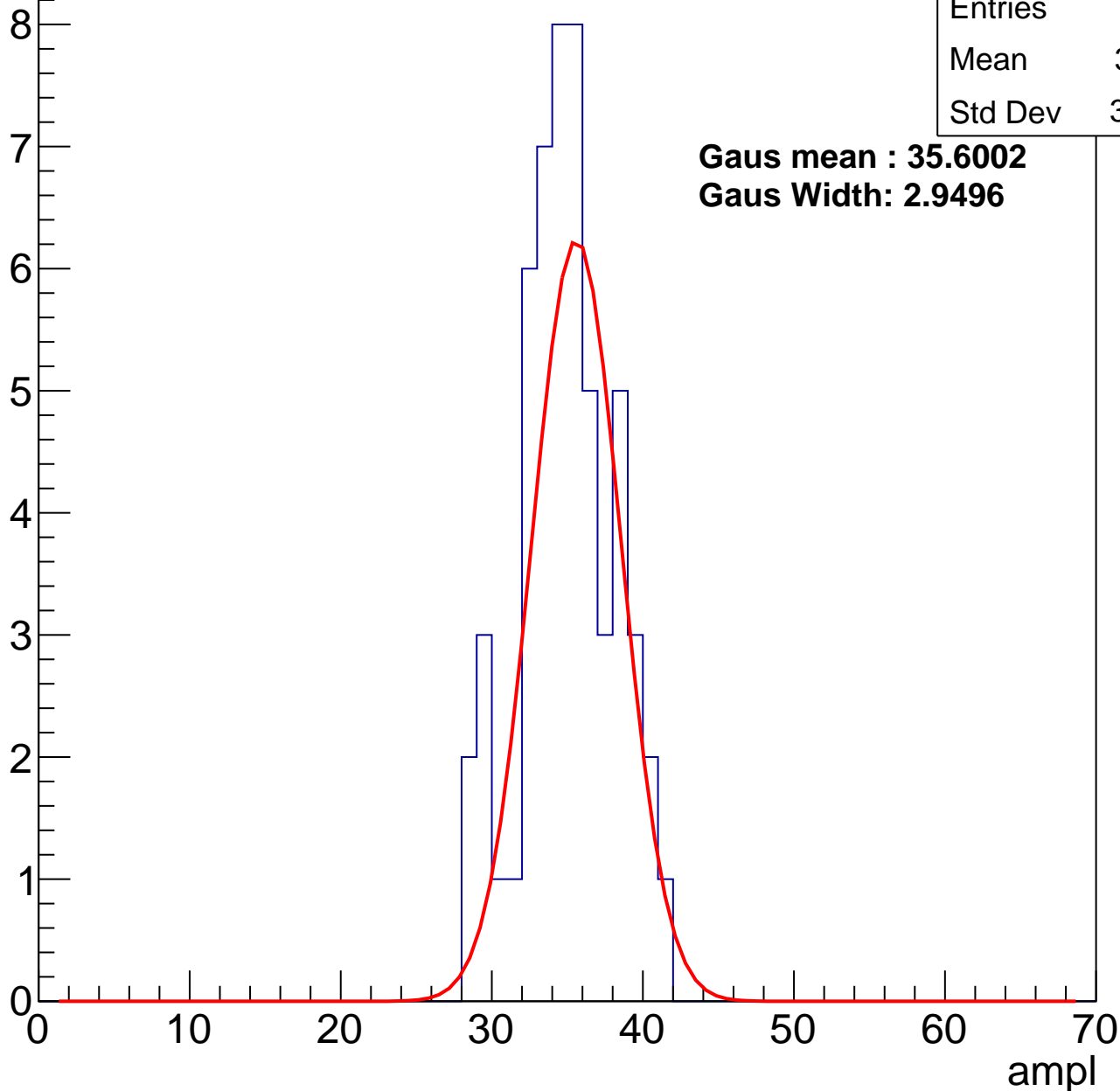
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	34.51
Std Dev	3.074

Gaus mean : 35.6002

Gaus Width: 2.9496



B1L102S, U14-ch31, adc2

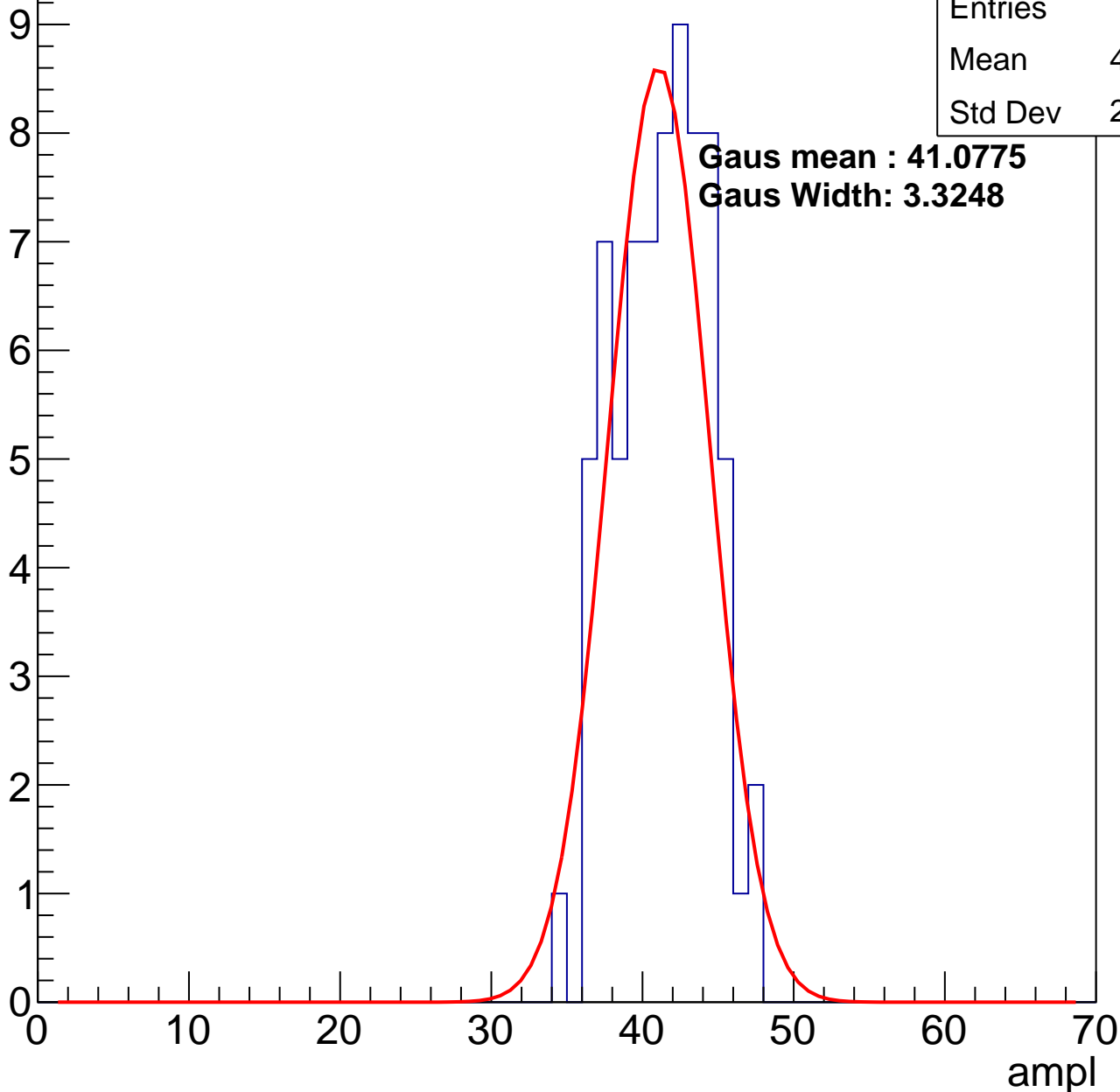
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	40.86
Std Dev	2.995

Gaus mean : 41.0775

Gaus Width: 3.3248

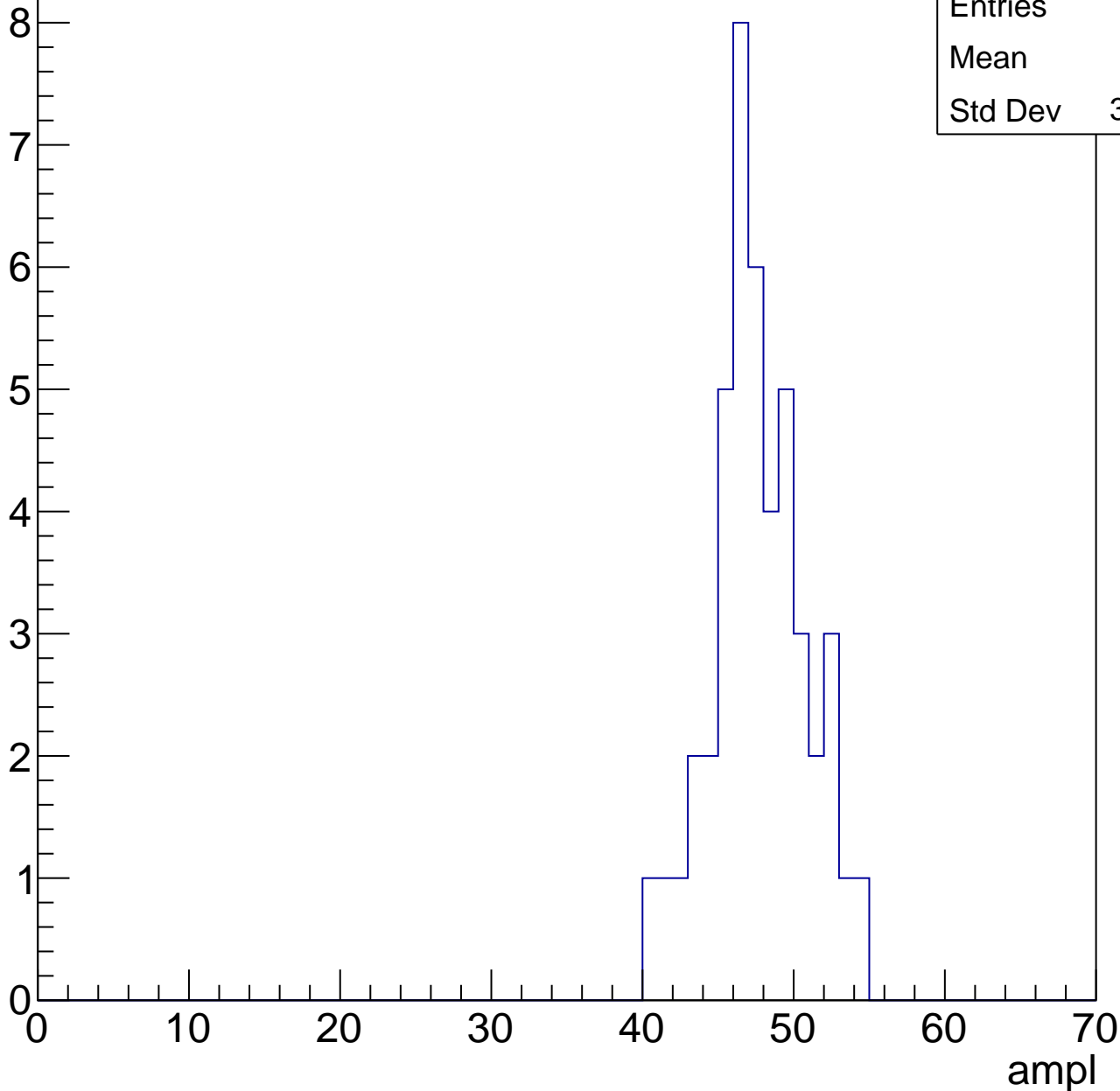


B1L102S, U14-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	47.2
Std Dev	3.088

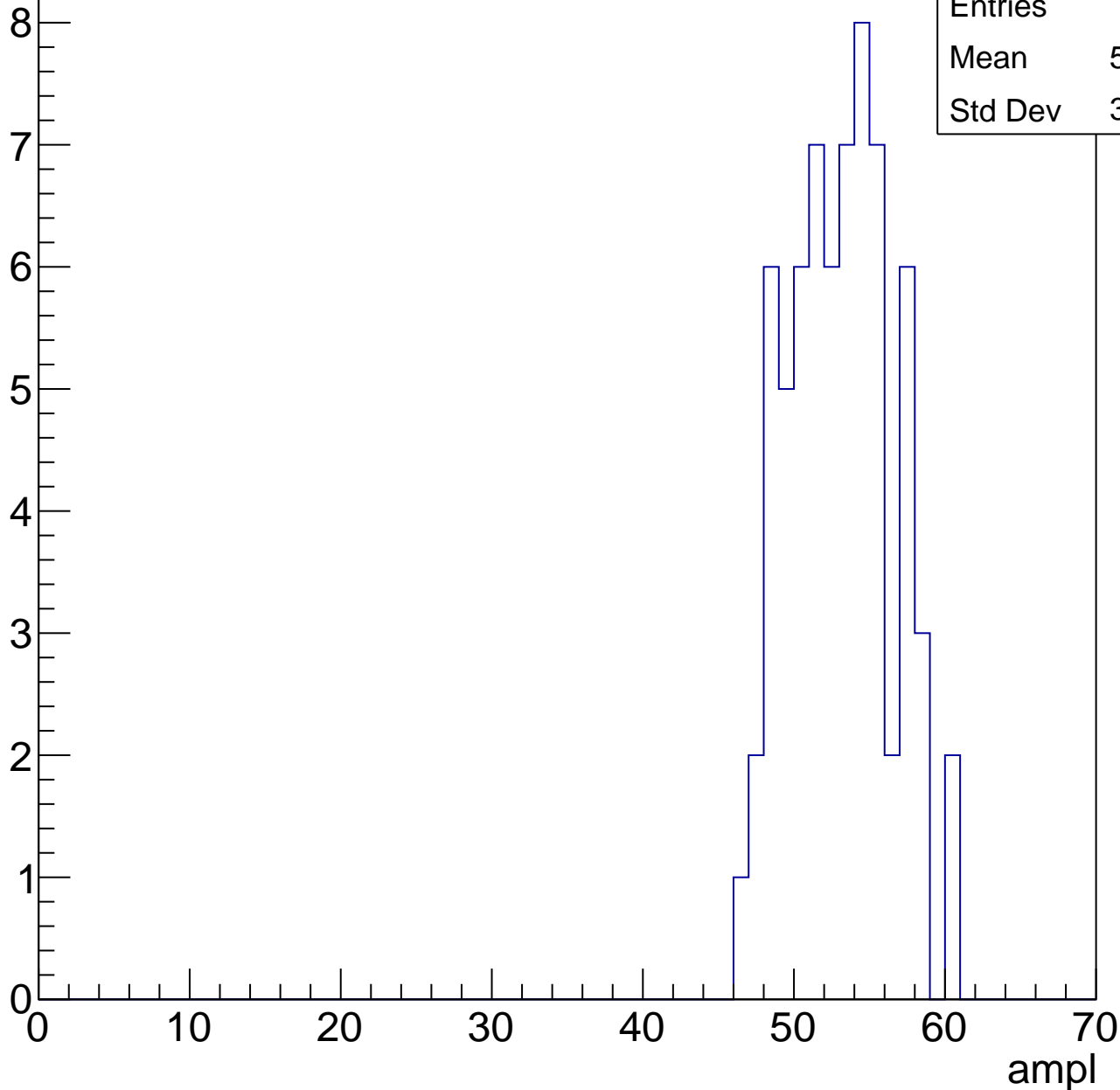


B1L102S, U14-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

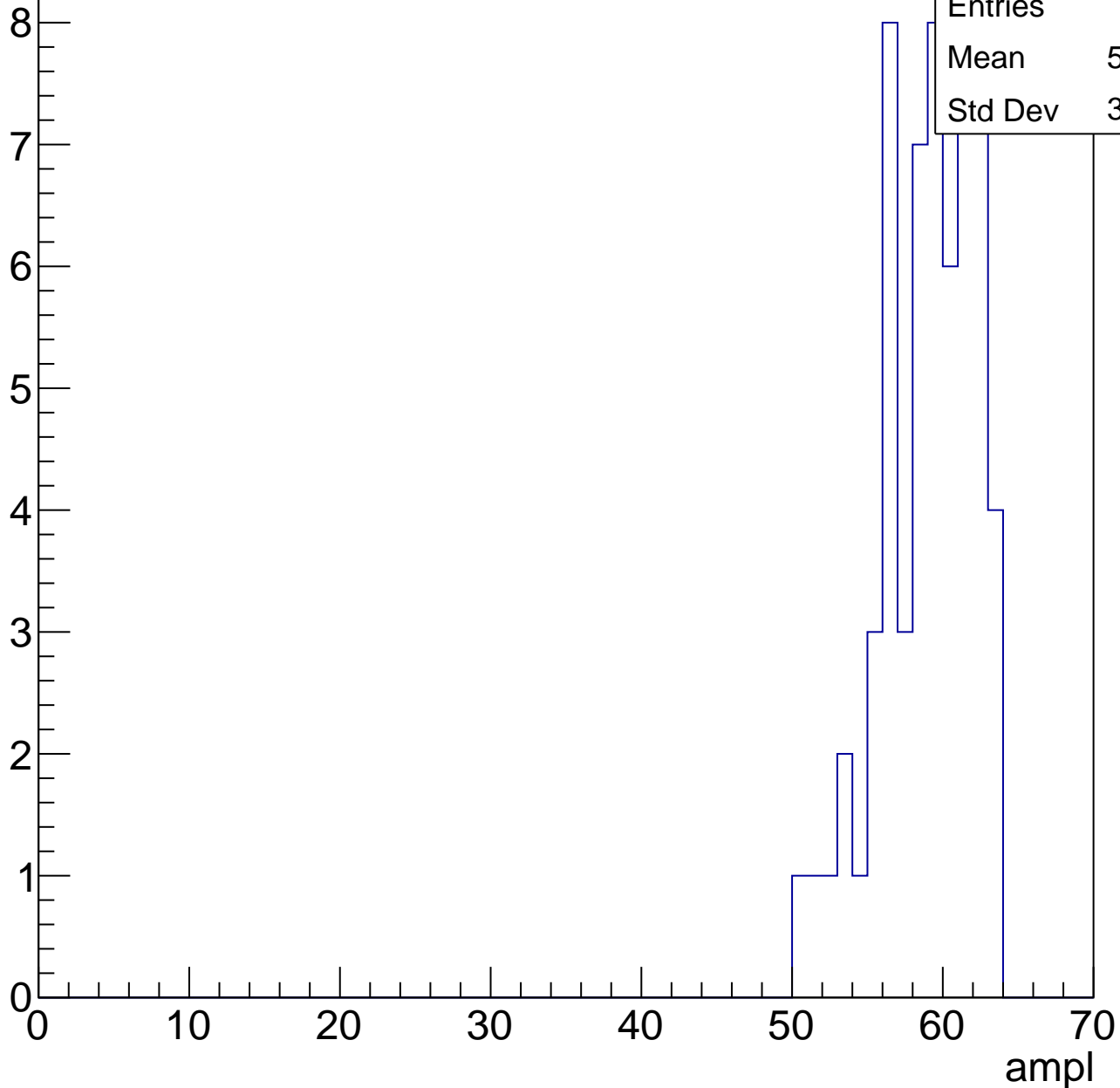
Entries	68
Mean	52.62
Std Dev	3.326



B1L102S, U14-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

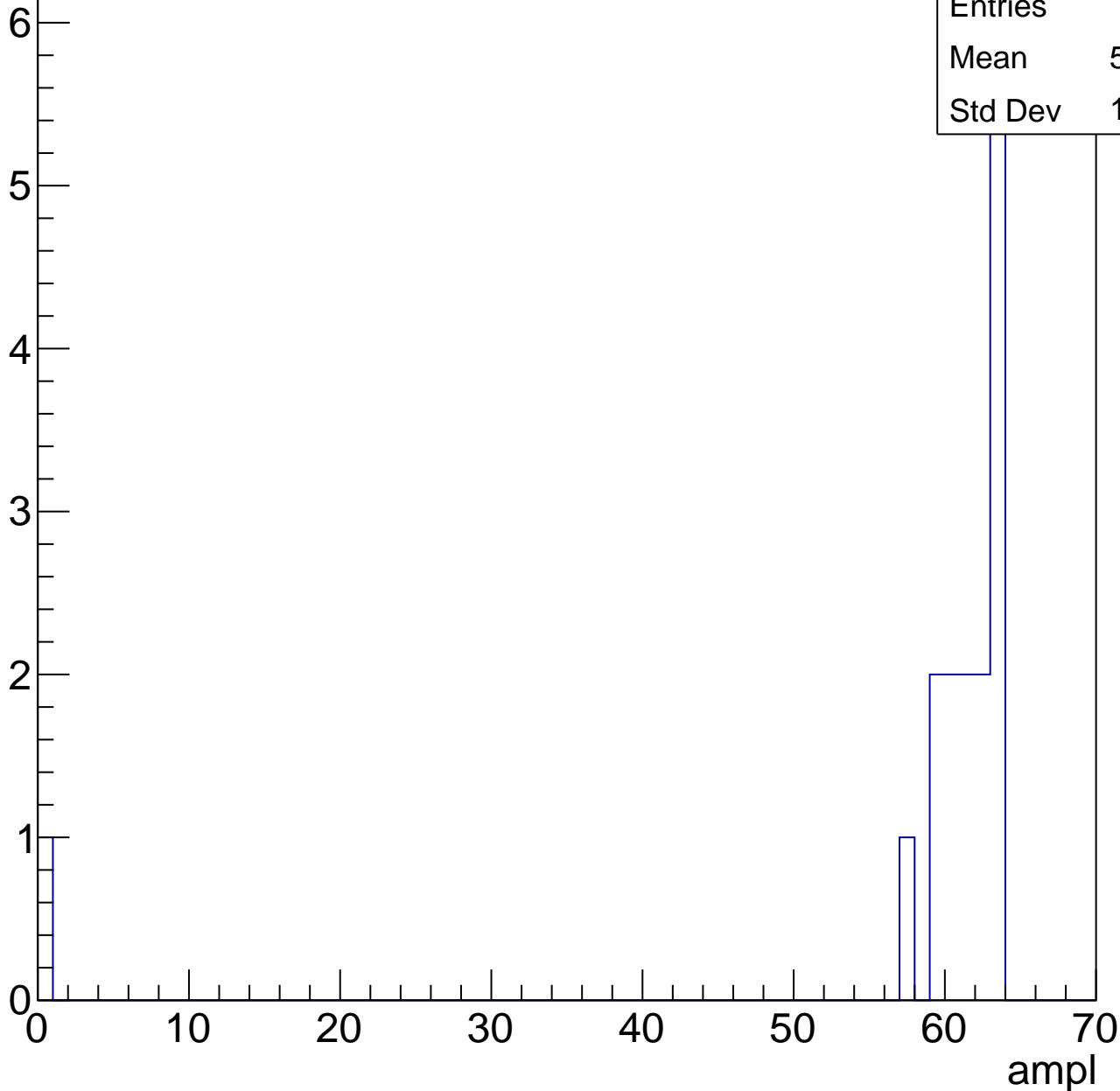


B1L102S, U14-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.44
Std Dev	14.94



B1L102S, U14-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch32, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	26.65
Std Dev	6.236

Gaus mean : 28.5927

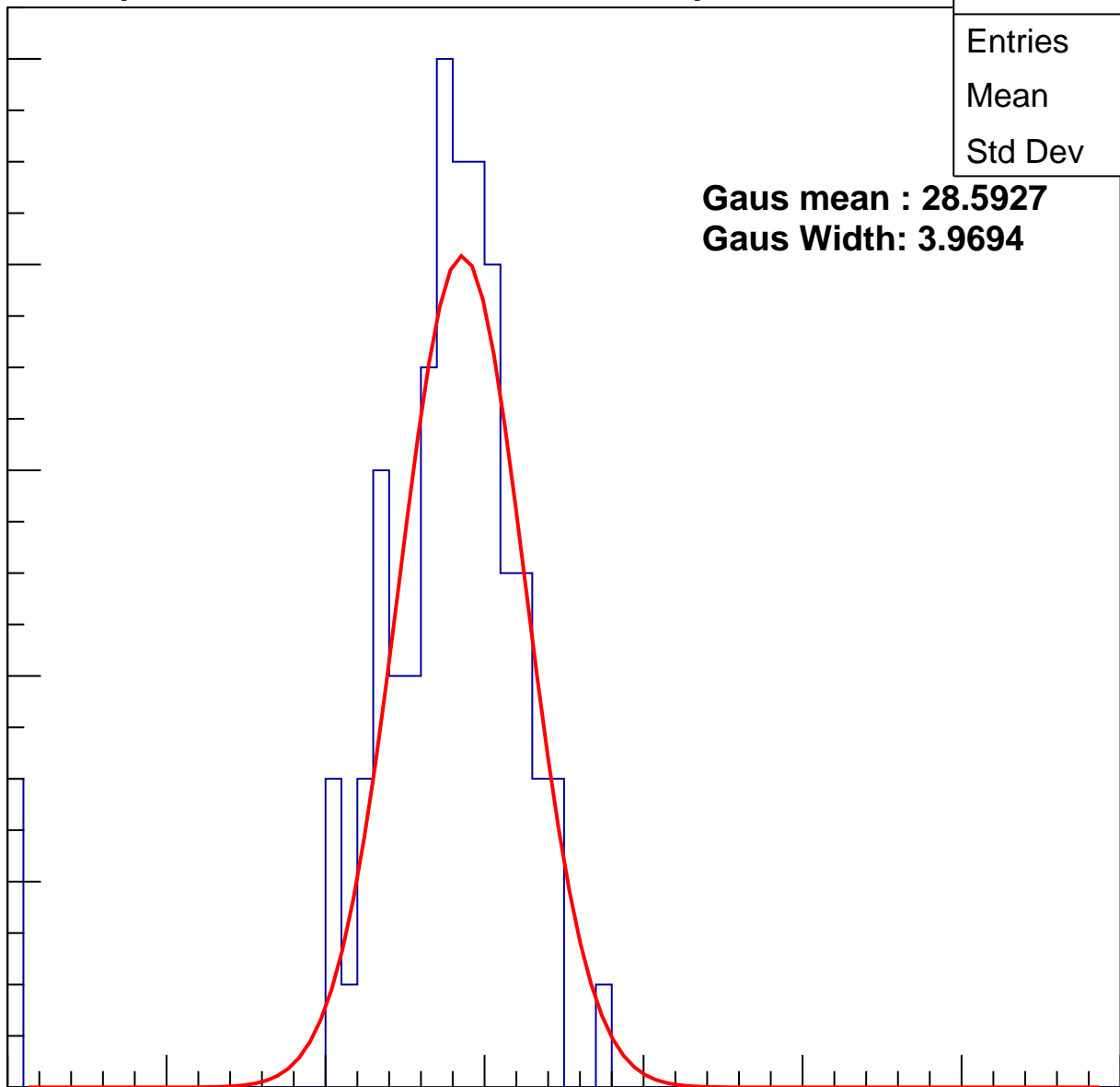
Gaus Width: 3.9694

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch32, adc1

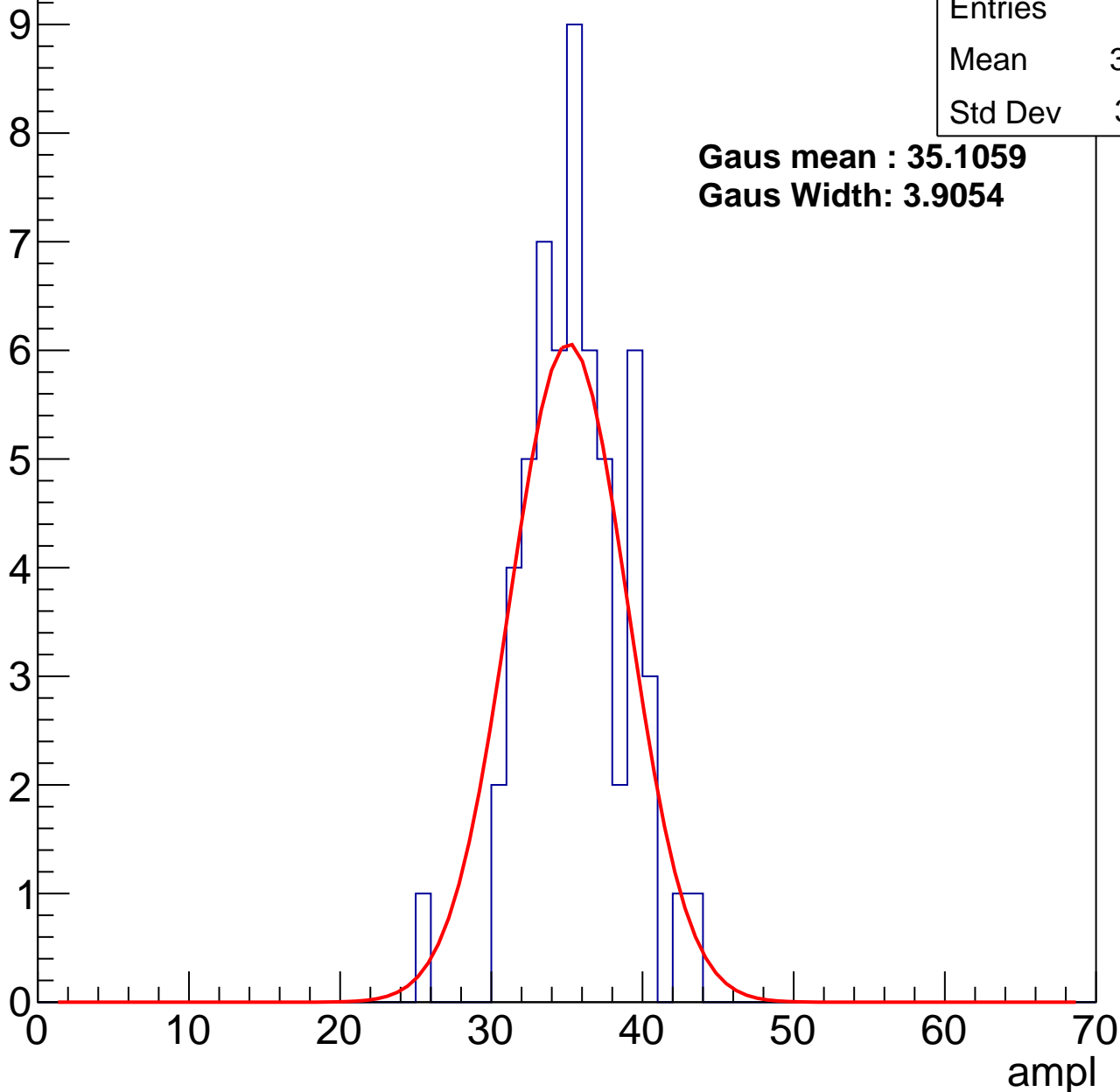
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.09
Std Dev	3.271

Gaus mean : 35.1059

Gaus Width: 3.9054



B1L102S, U14-ch32, adc2

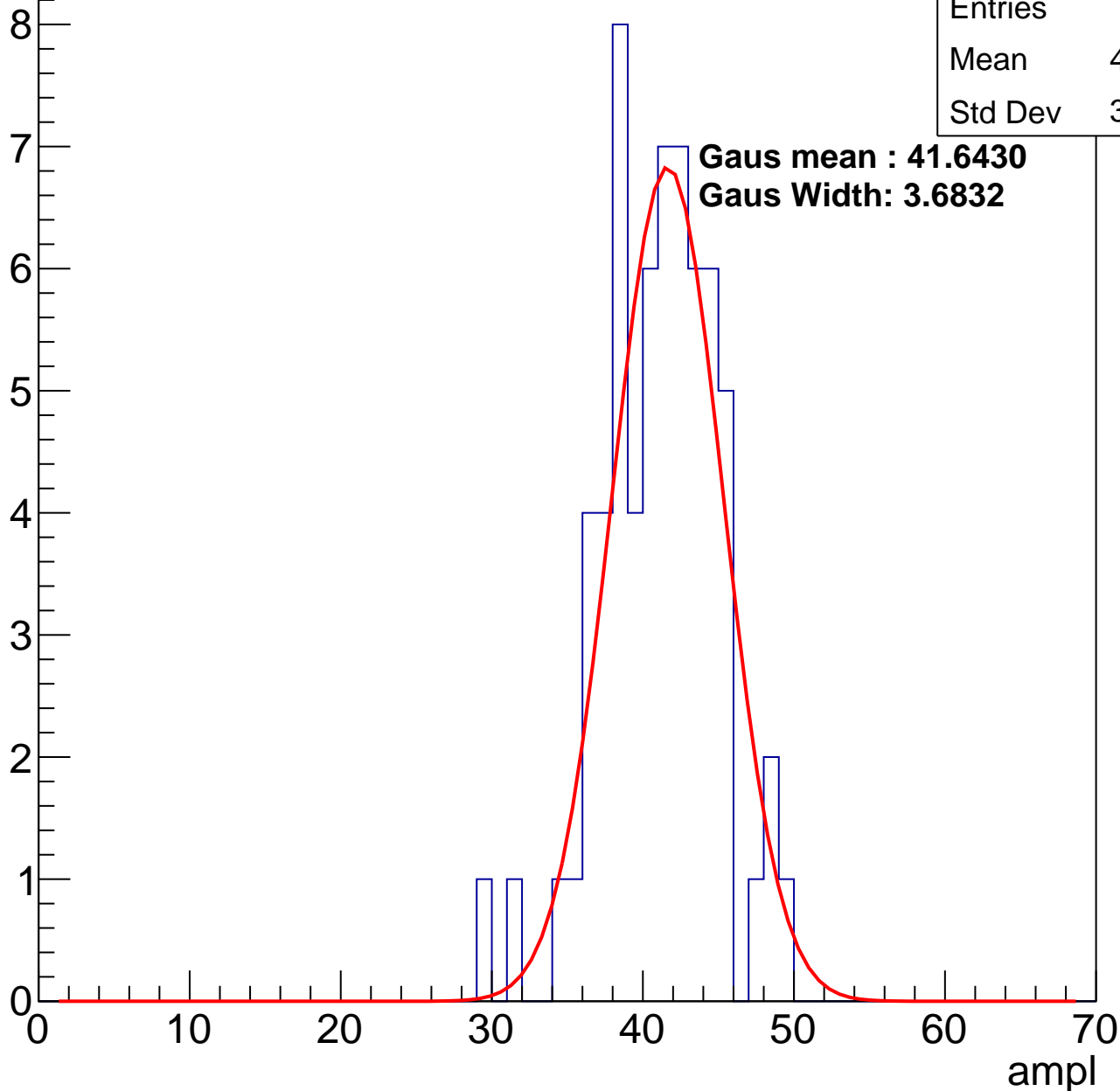
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	40.63
Std Dev	3.805

Gaus mean : 41.6430

Gaus Width: 3.6832

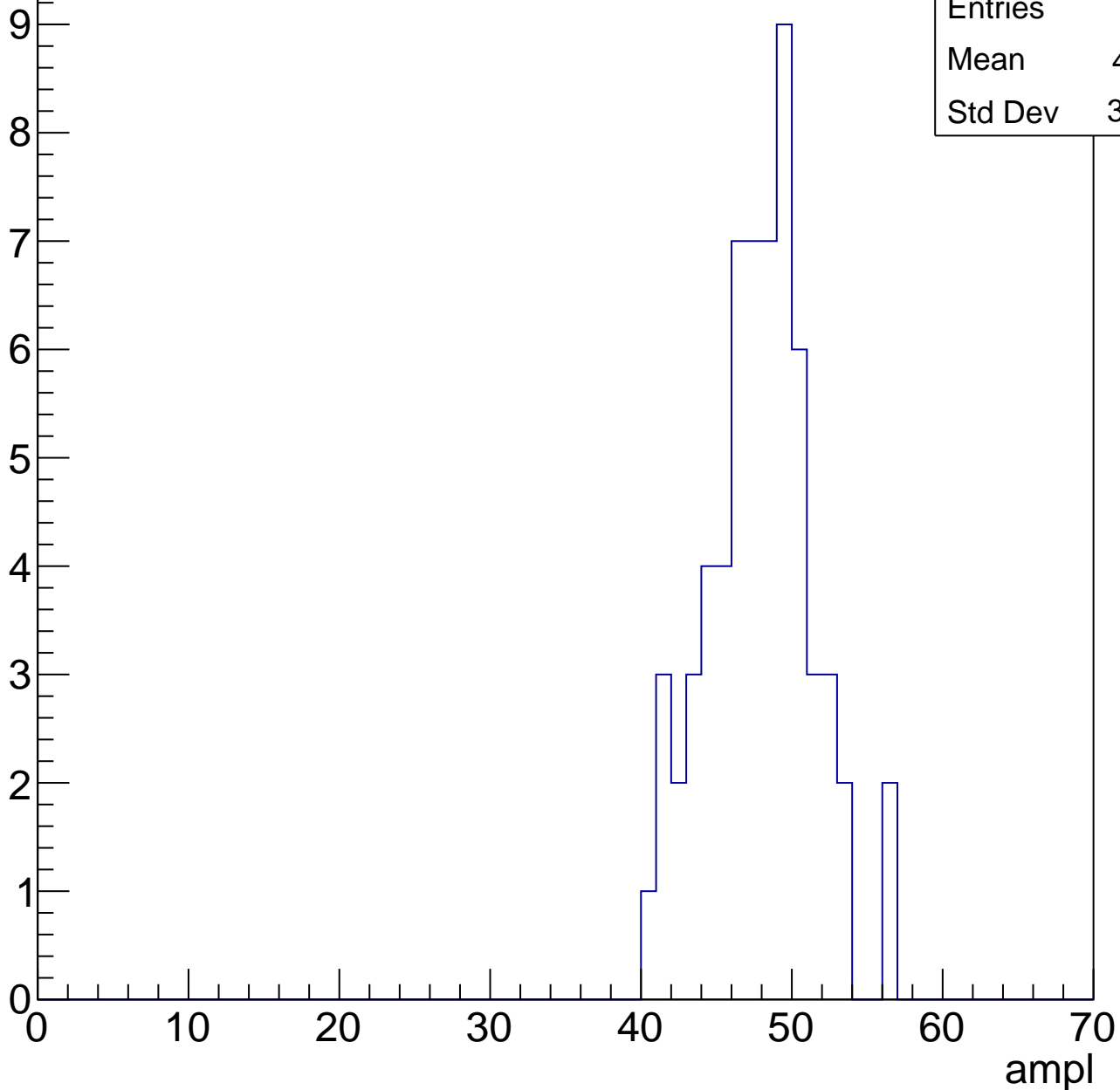


B1L102S, U14-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.41
Std Dev	3.467

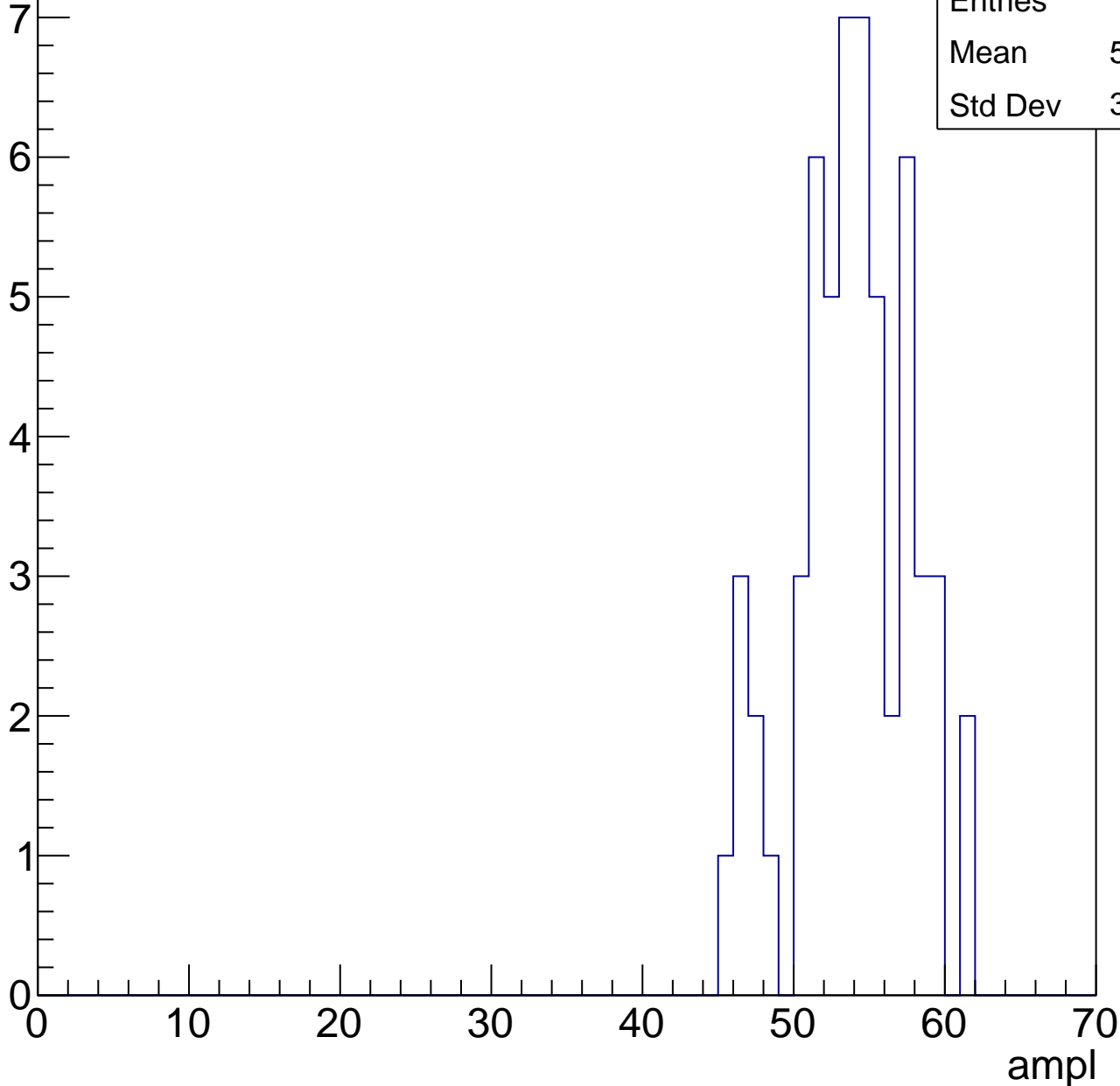


B1L102S, U14-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.43
Std Dev	3.793

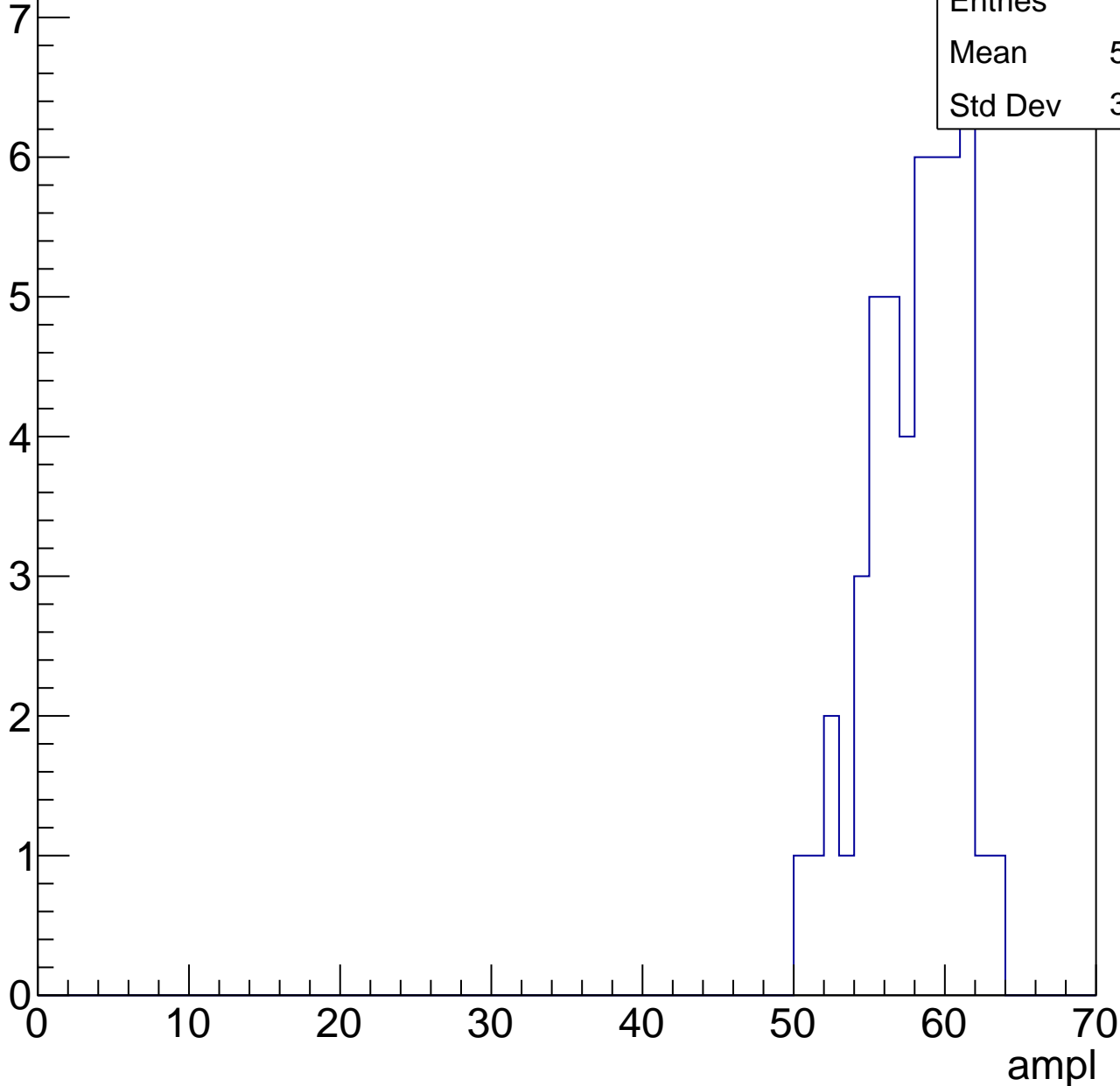


B1L102S, U14-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.49
Std Dev	3.025

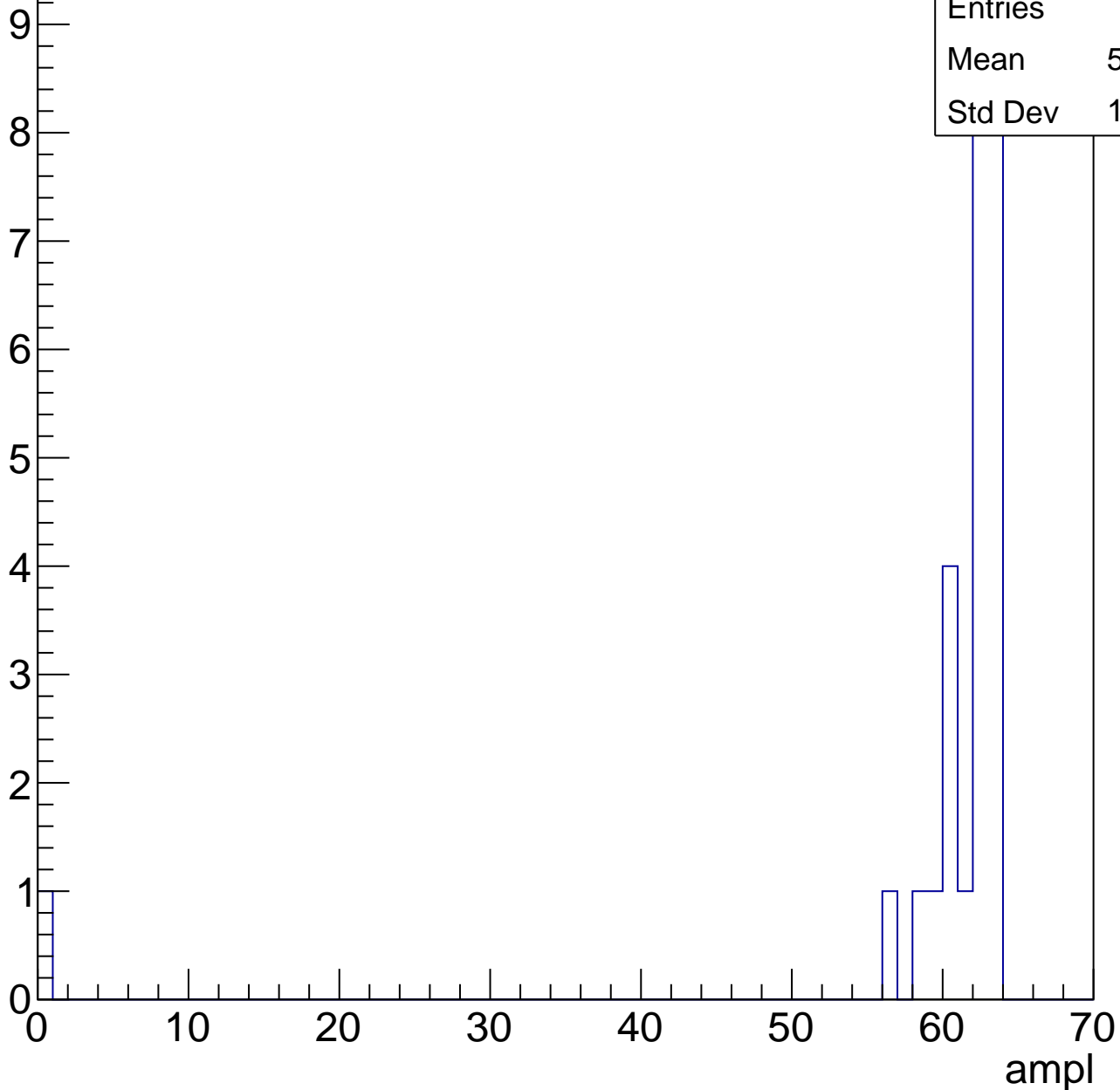


B1L102S, U14-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	59.08
Std Dev	11.94



B1L102S, U14-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch33, adc0

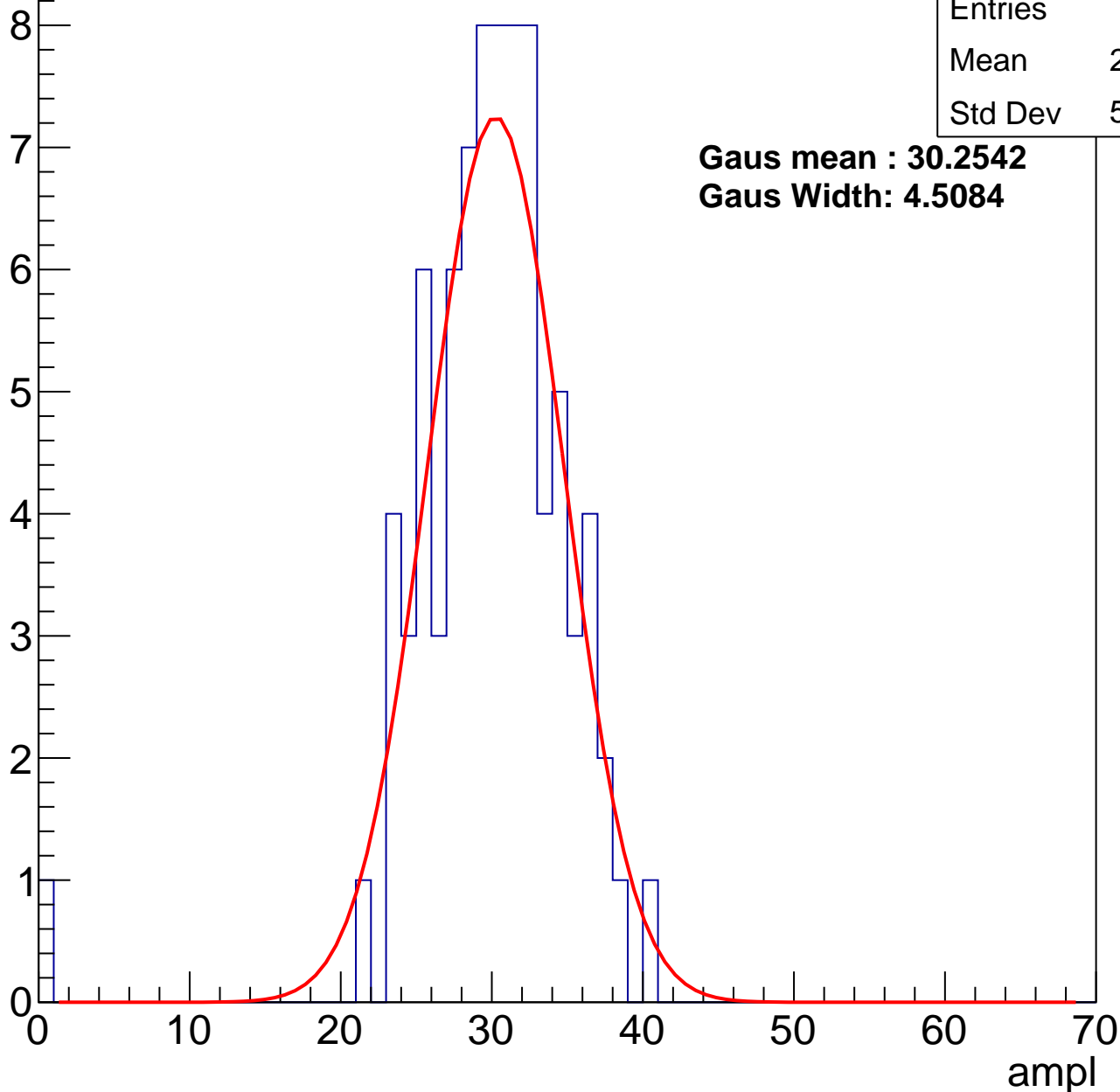
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	29.52
Std Dev	5.138

Gaus mean : 30.2542

Gaus Width: 4.5084



B1L102S, U14-ch33, adc1

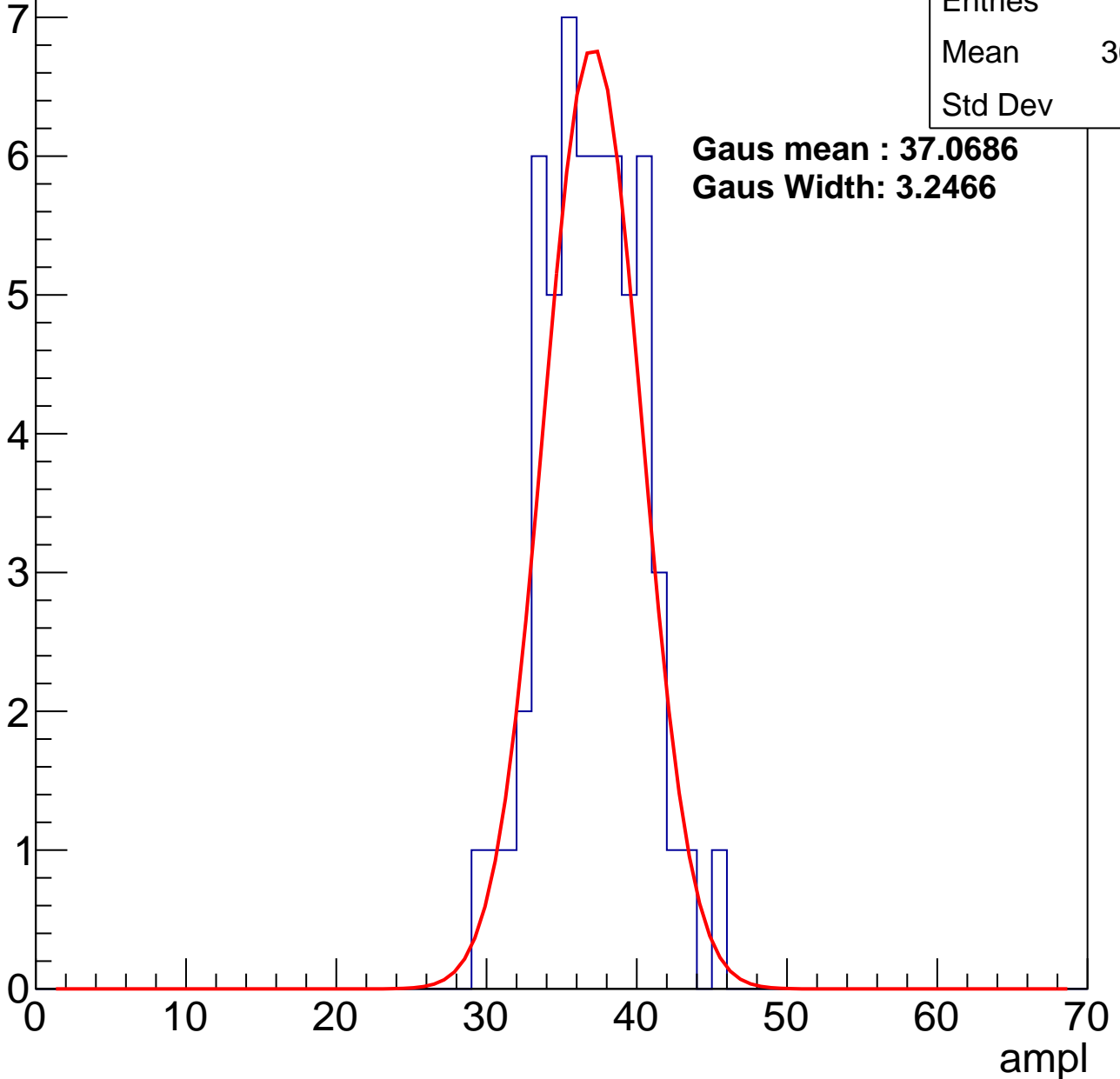
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.57
Std Dev	3.26

Gaus mean : 37.0686

Gaus Width: 3.2466



B1L102S, U14-ch33, adc2

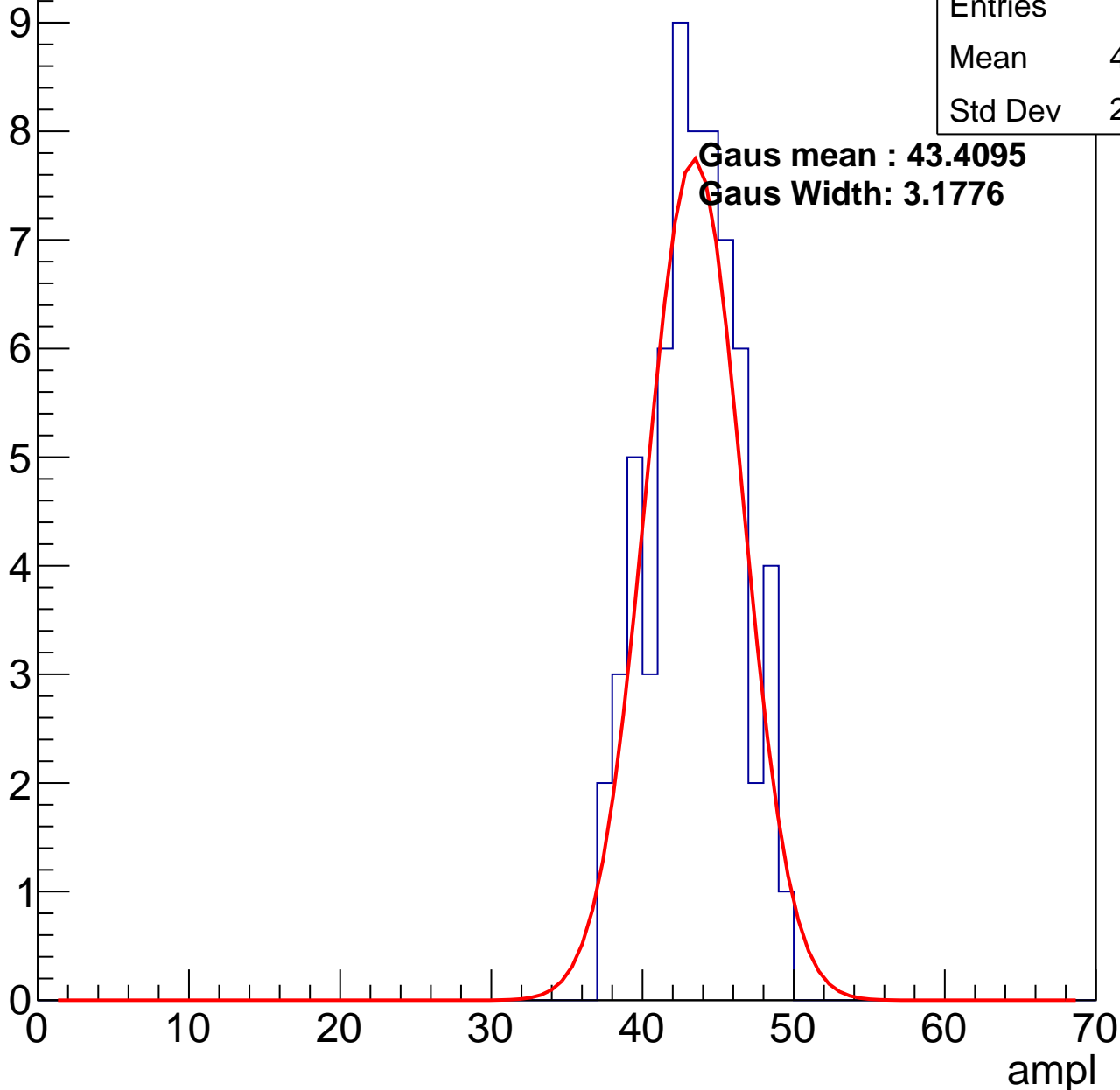
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.95
Std Dev	2.918

Gaus mean : 43.4095

Gaus Width: 3.1776



B1L102S, U14-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

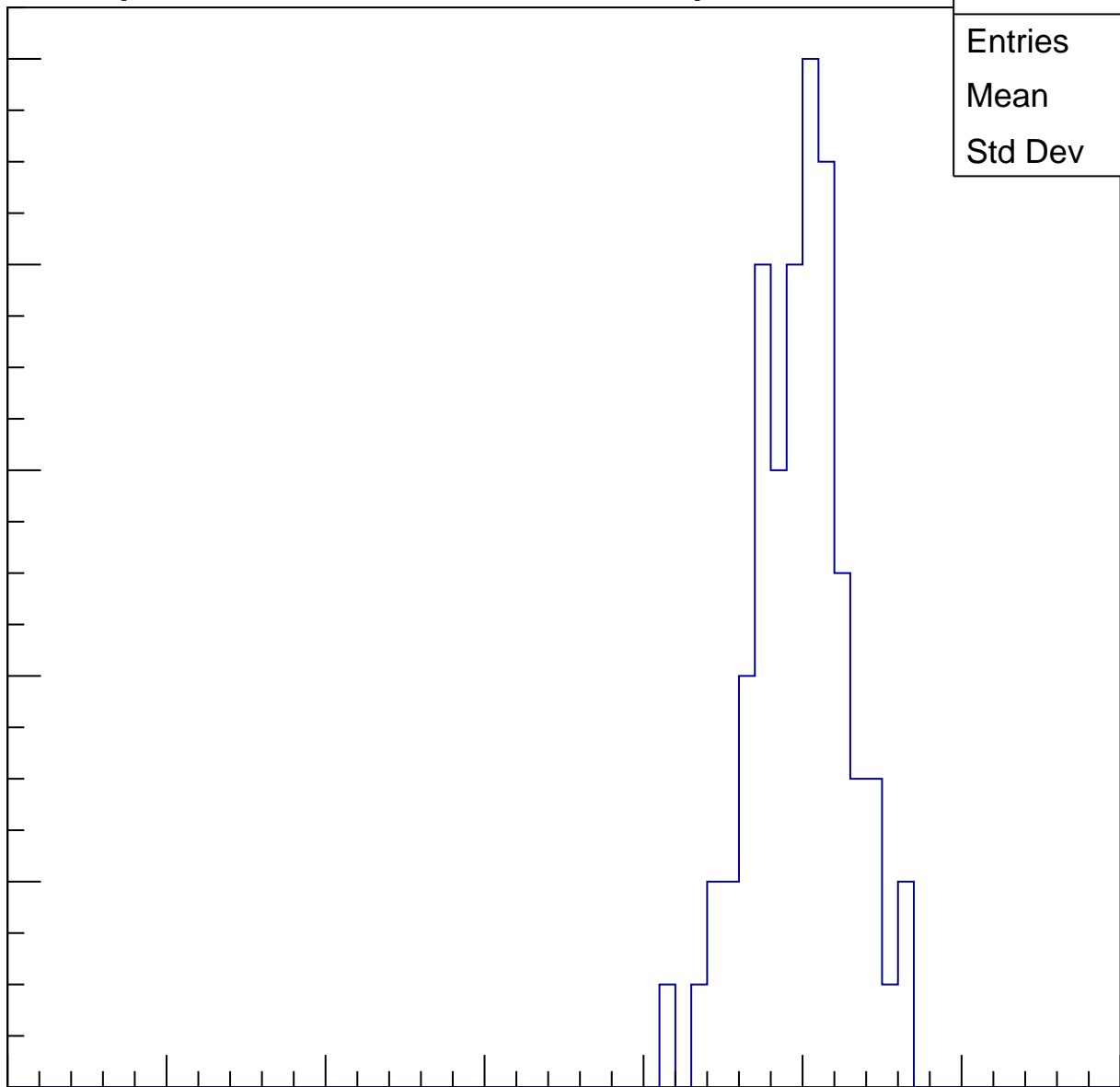
Entries	65
Mean	49.37
Std Dev	3.026

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

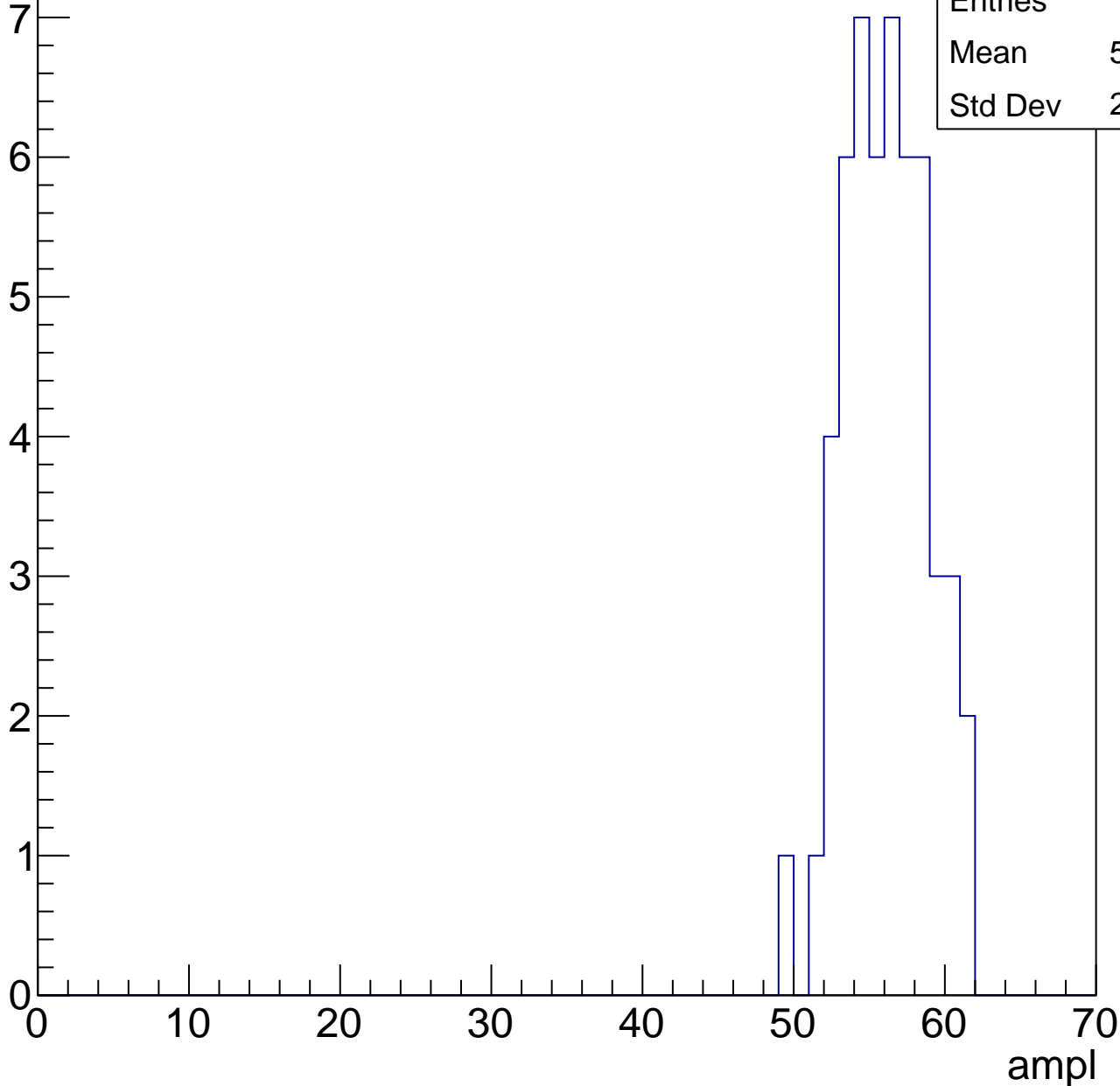


B1L102S, U14-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.67
Std Dev	2.687

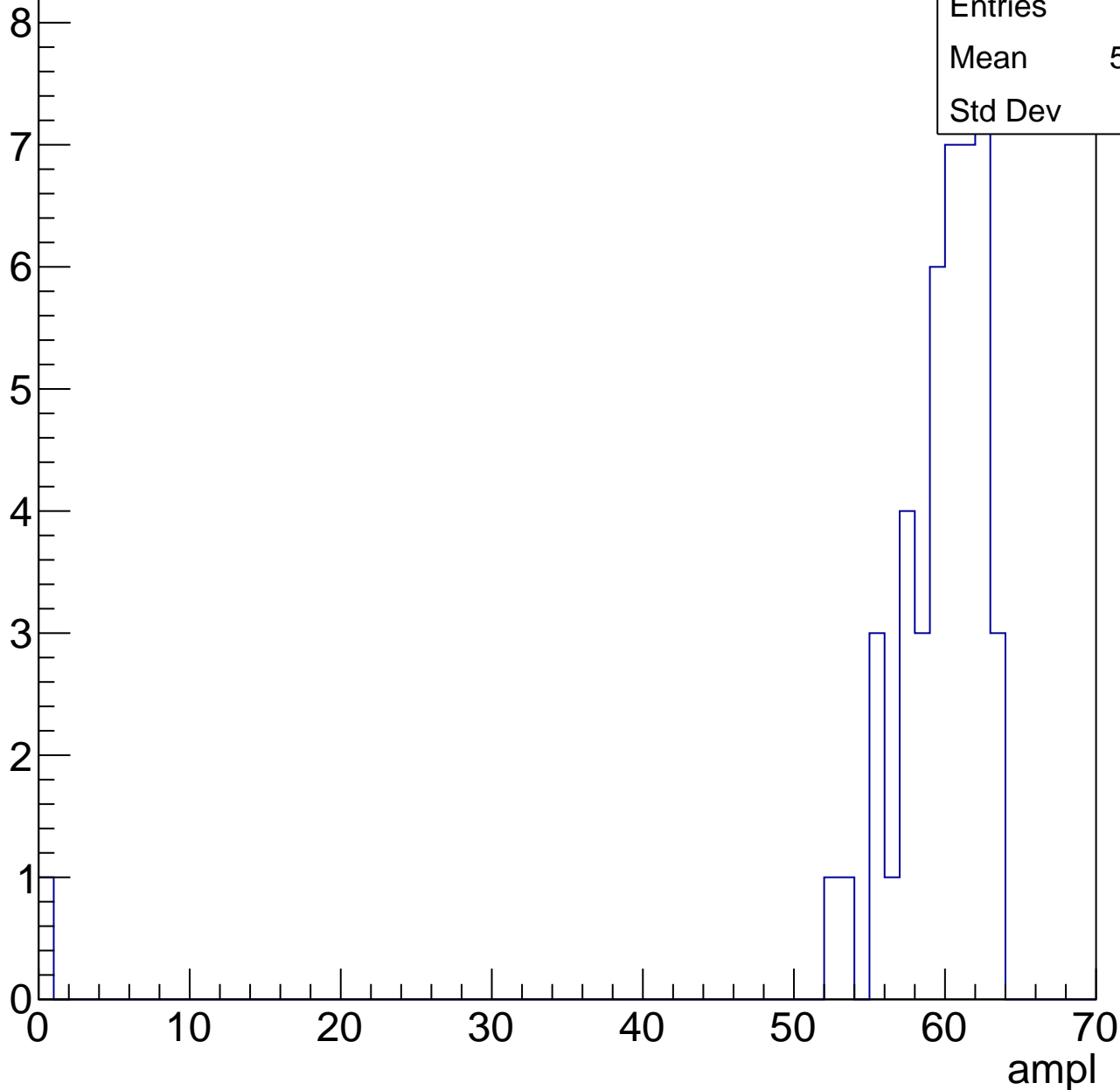


B1L102S, U14-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.09
Std Dev	9.14

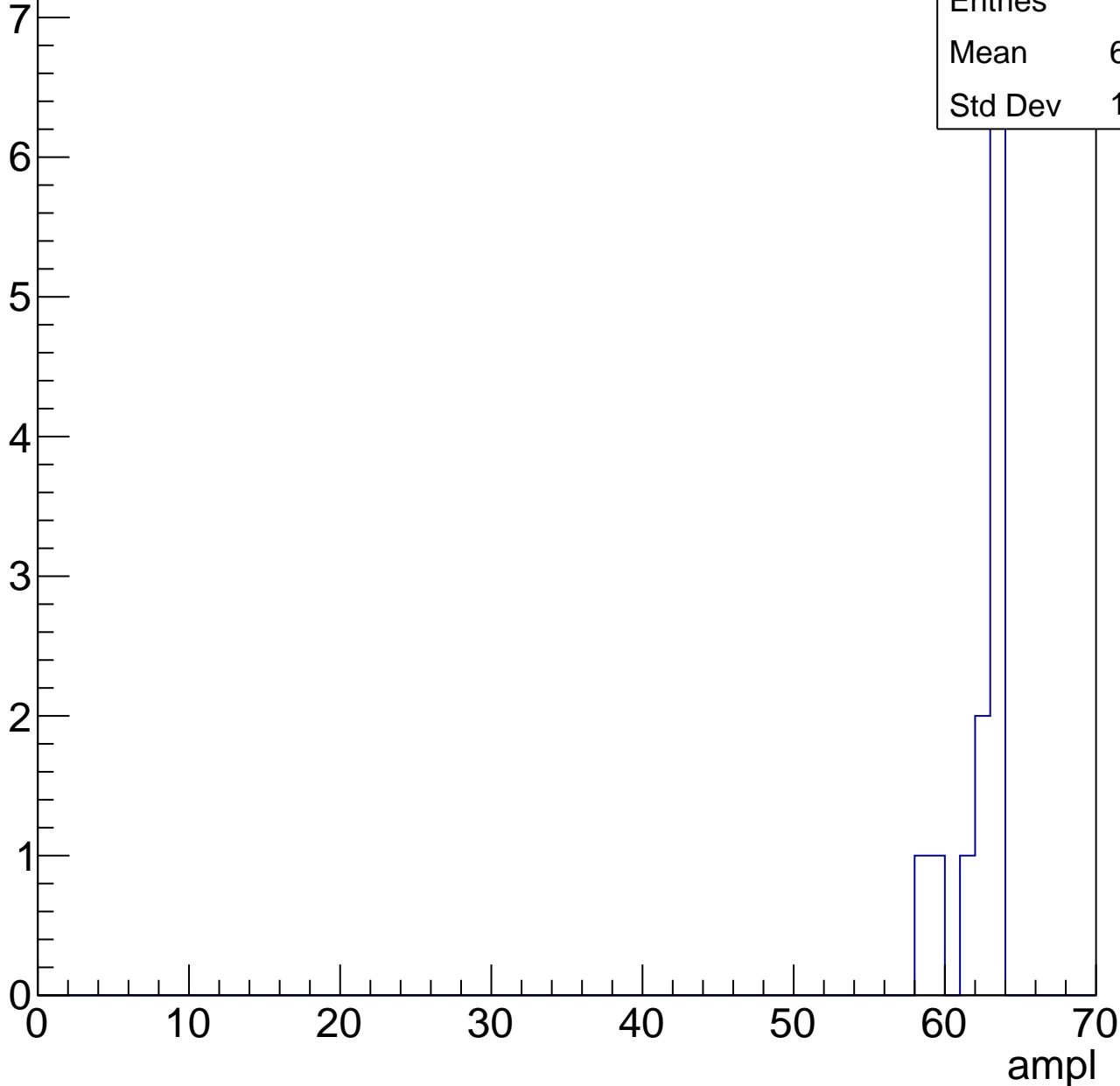


B1L102S, U14-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.92
Std Dev	1.656



B1L102S, U14-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U14-ch34, adc0

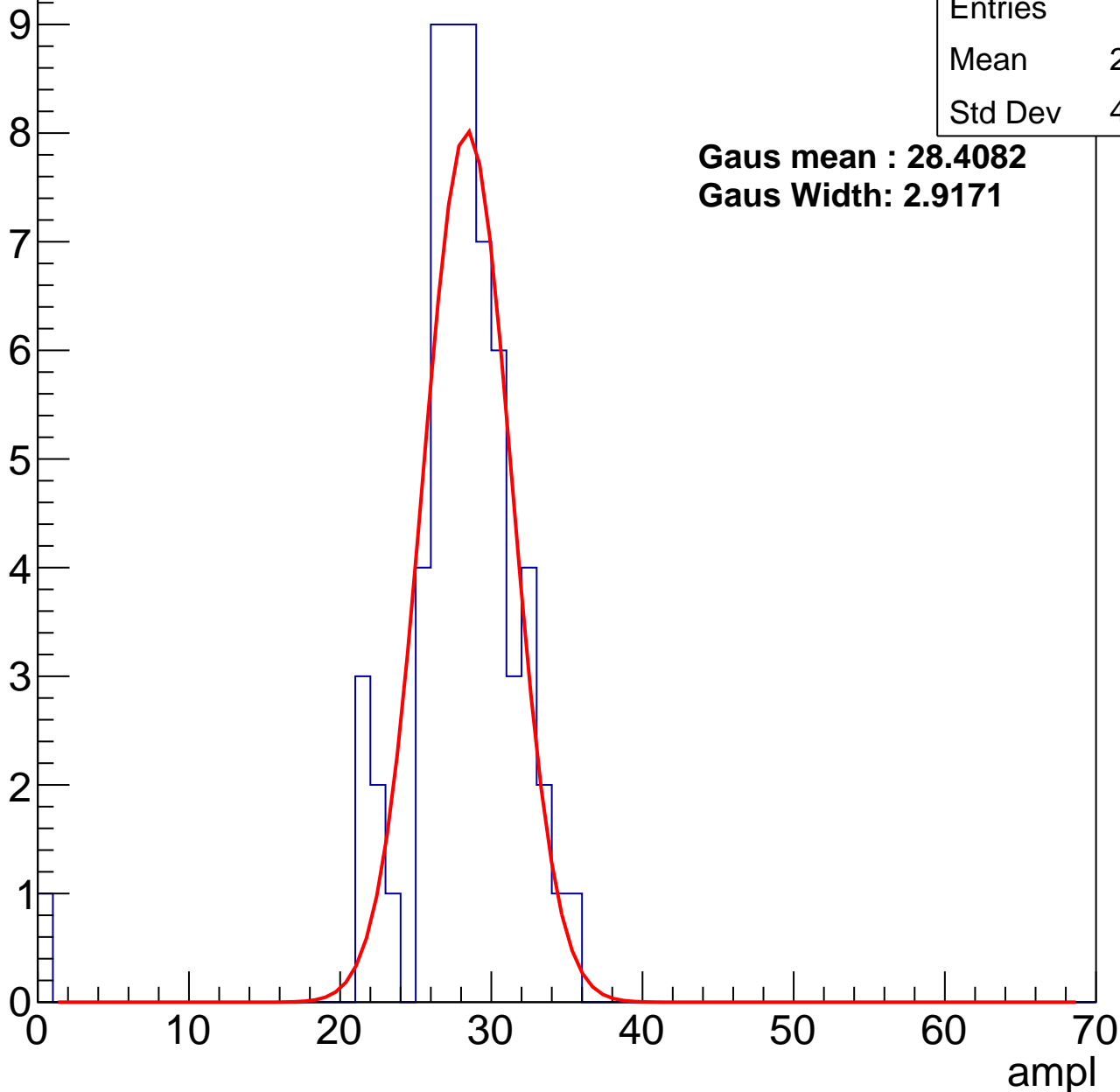
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.39
Std Dev	4.654

Gaus mean : 28.4082

Gaus Width: 2.9171



B1L102S, U14-ch34, adc1

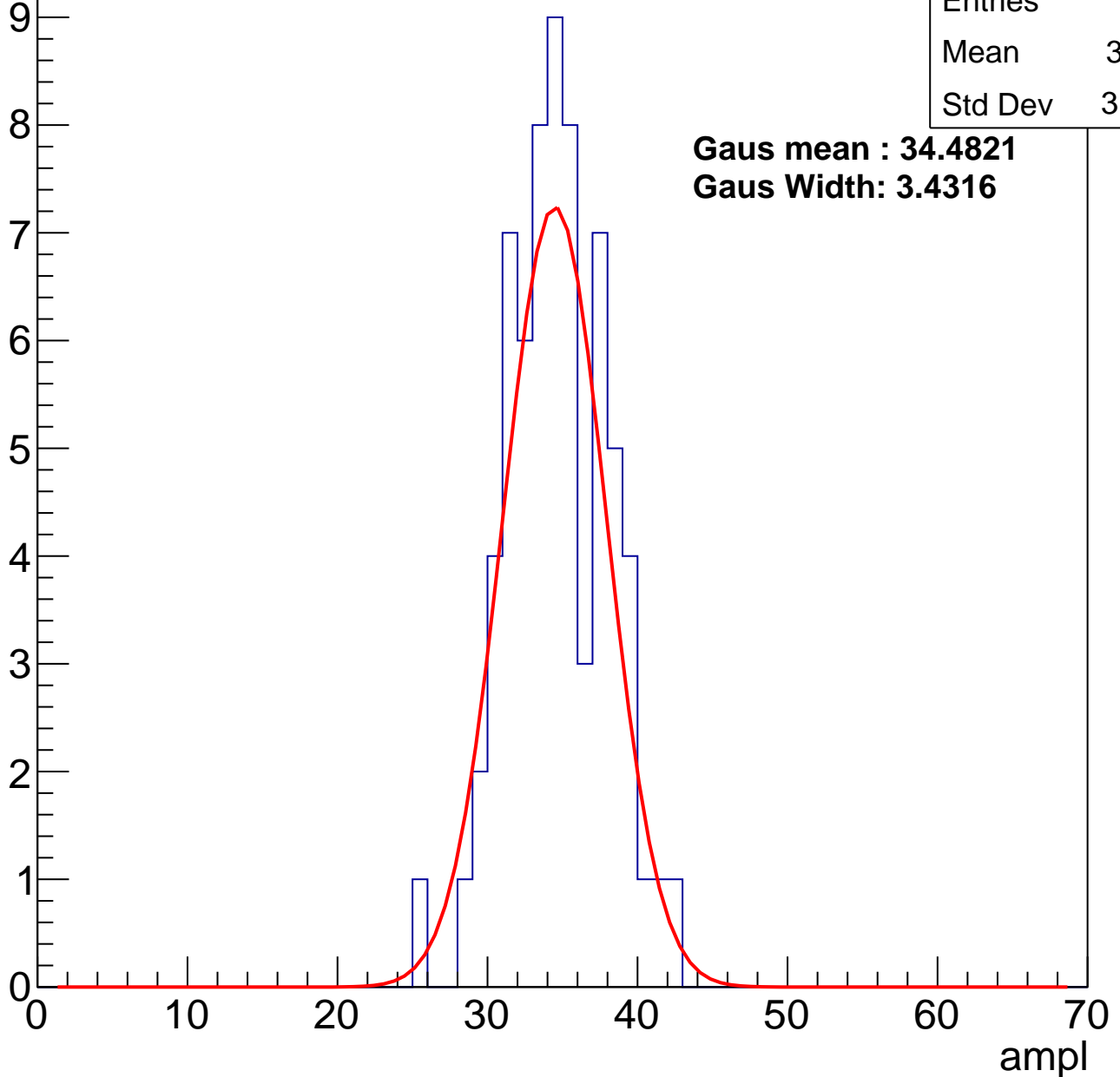
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.21
Std Dev	3.288

Gaus mean : 34.4821

Gaus Width: 3.4316



B1L102S, U14-ch34, adc2

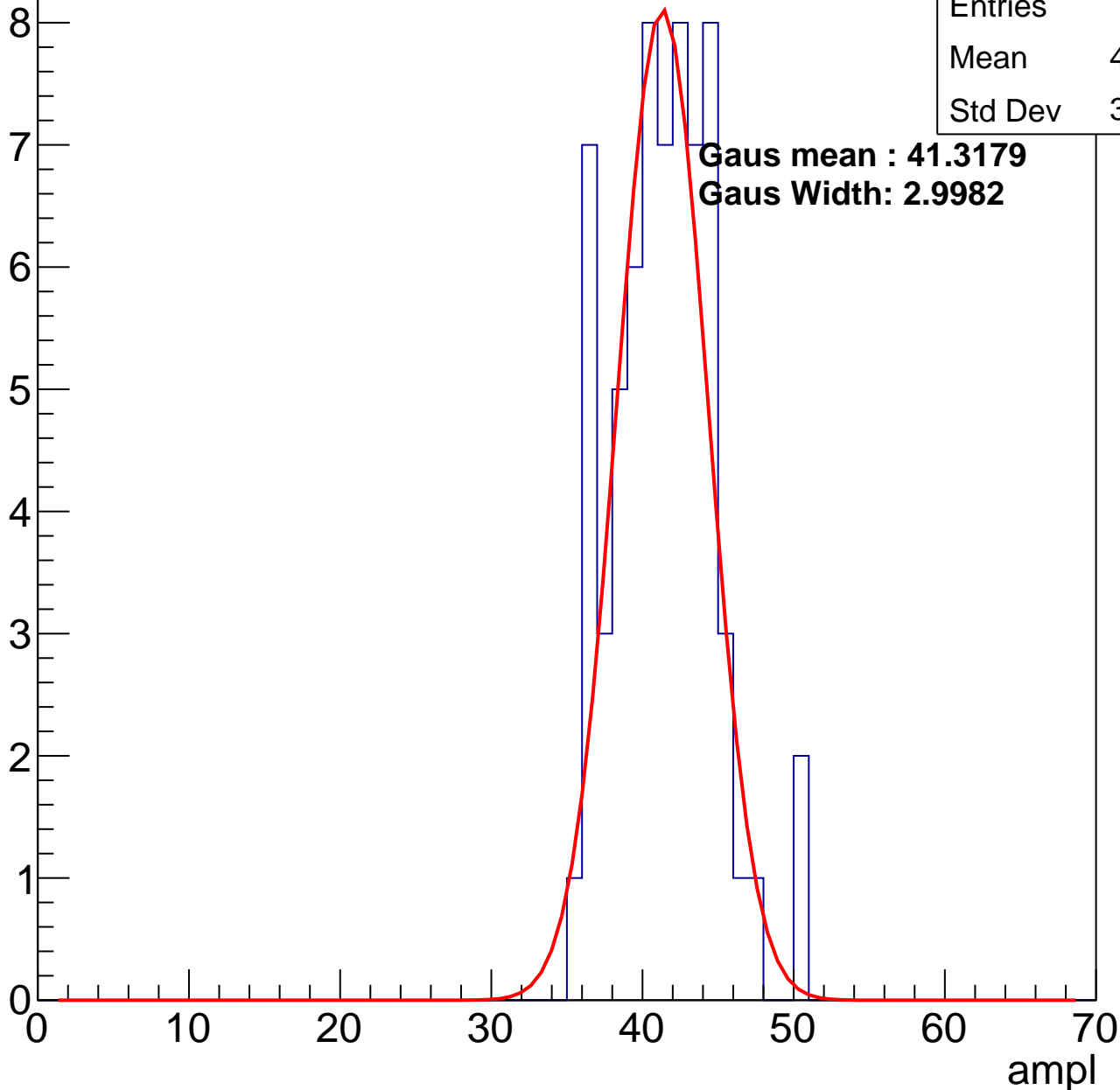
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	40.99
Std Dev	3.267

Gaus mean : 41.3179

Gaus Width: 2.9982

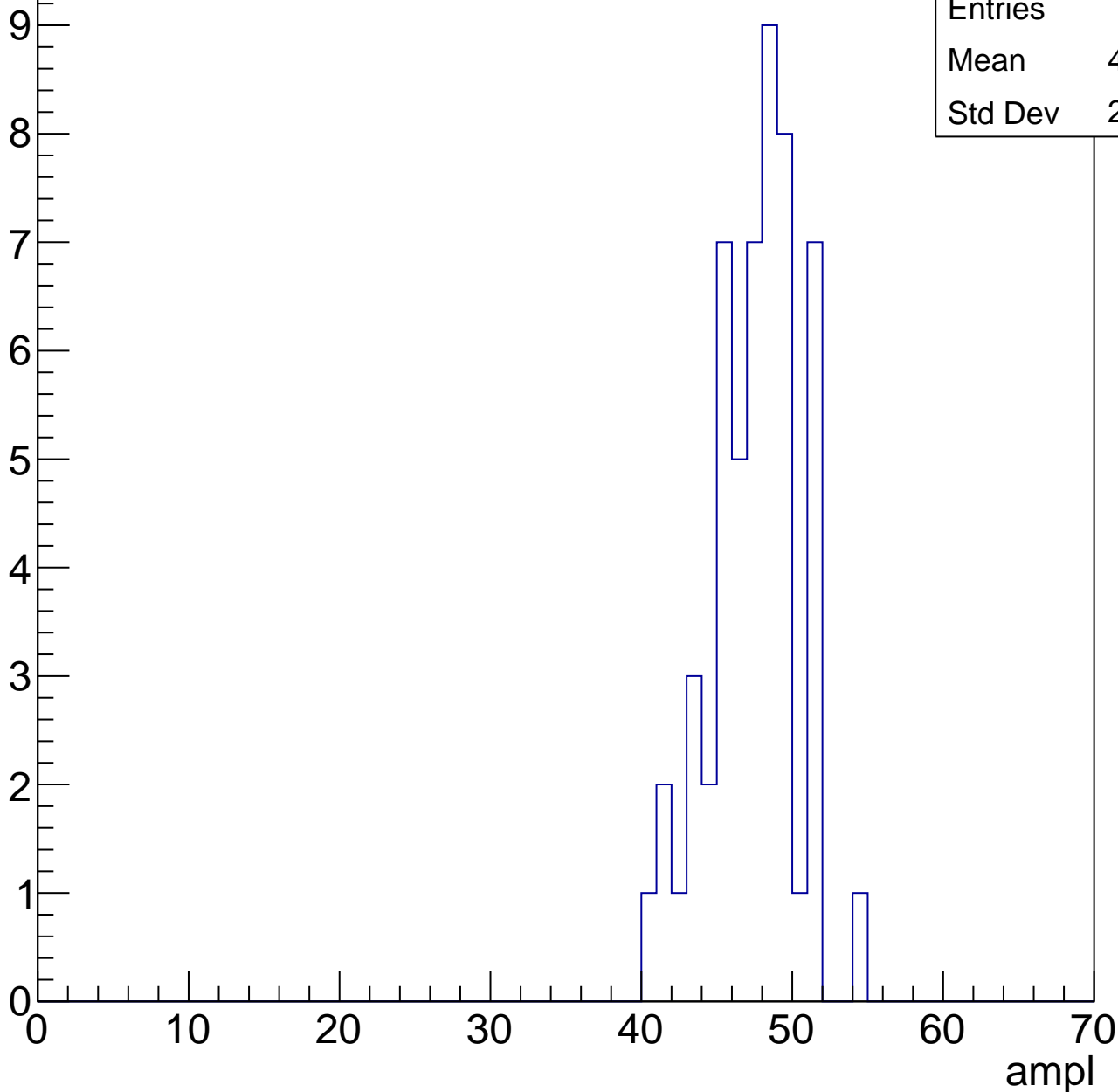


B1L102S, U14-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	47.04
Std Dev	2.906

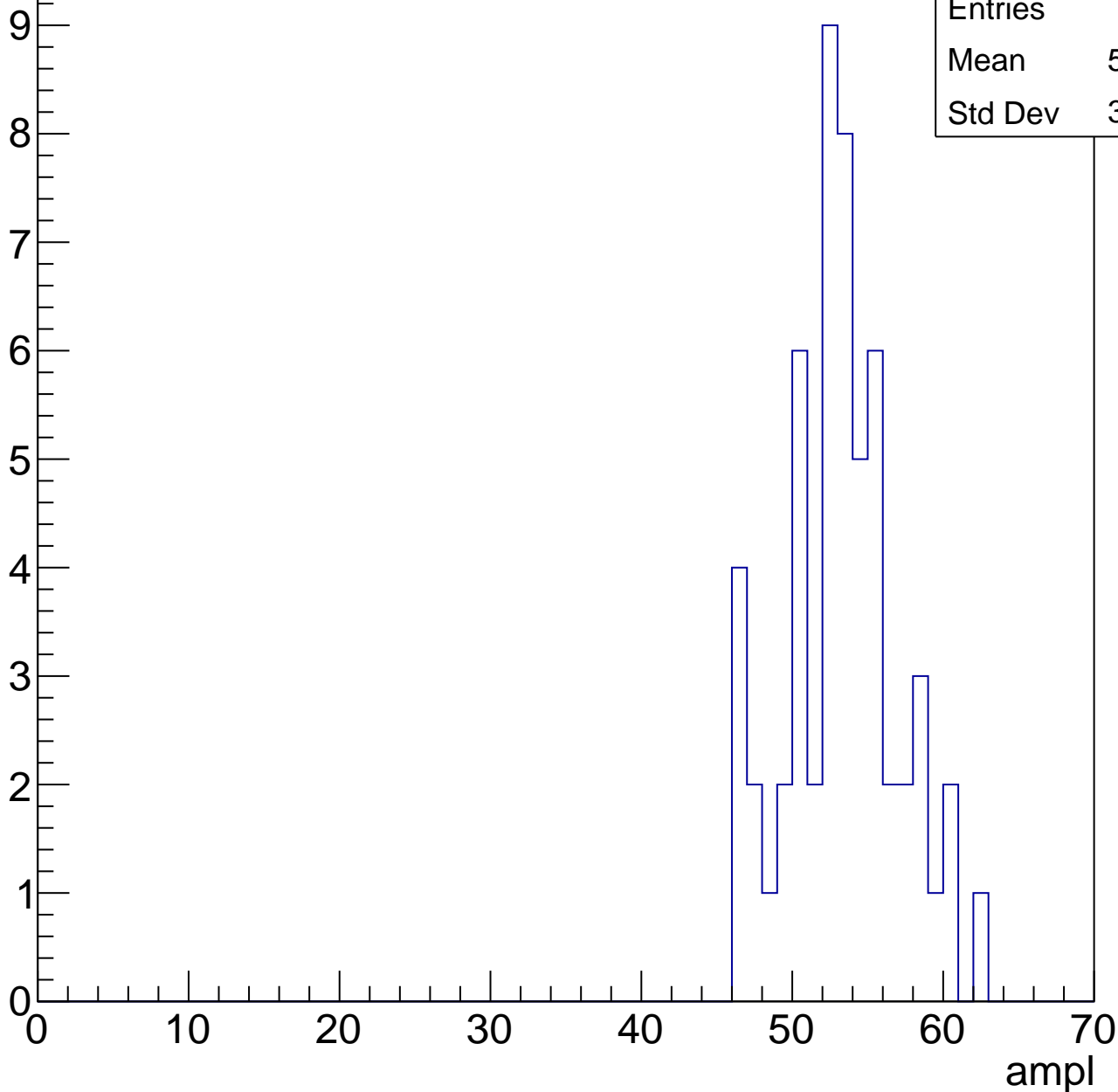


B1L102S, U14-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	52.84
Std Dev	3.693

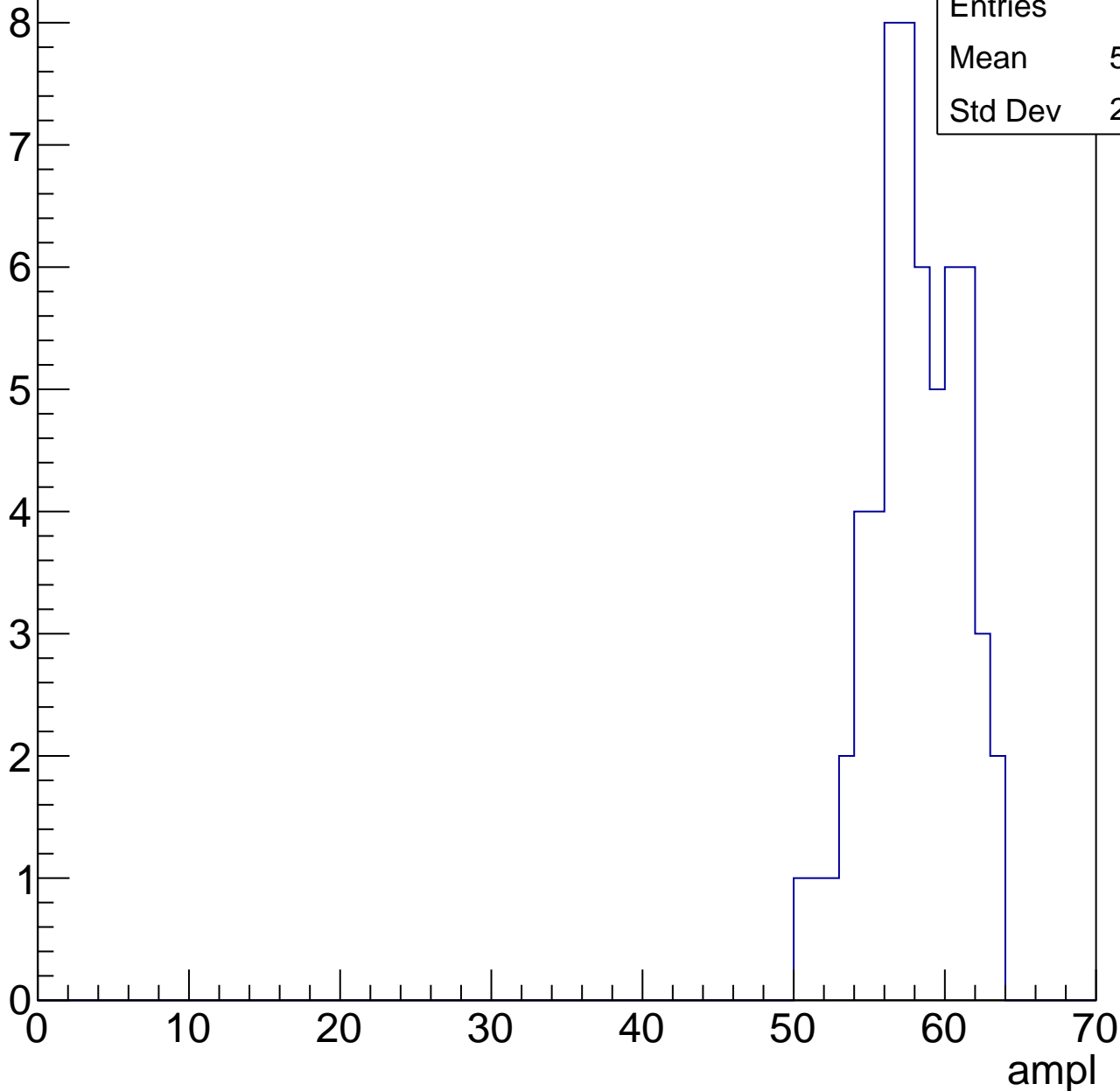


B1L102S, U14-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.54
Std Dev	2.986

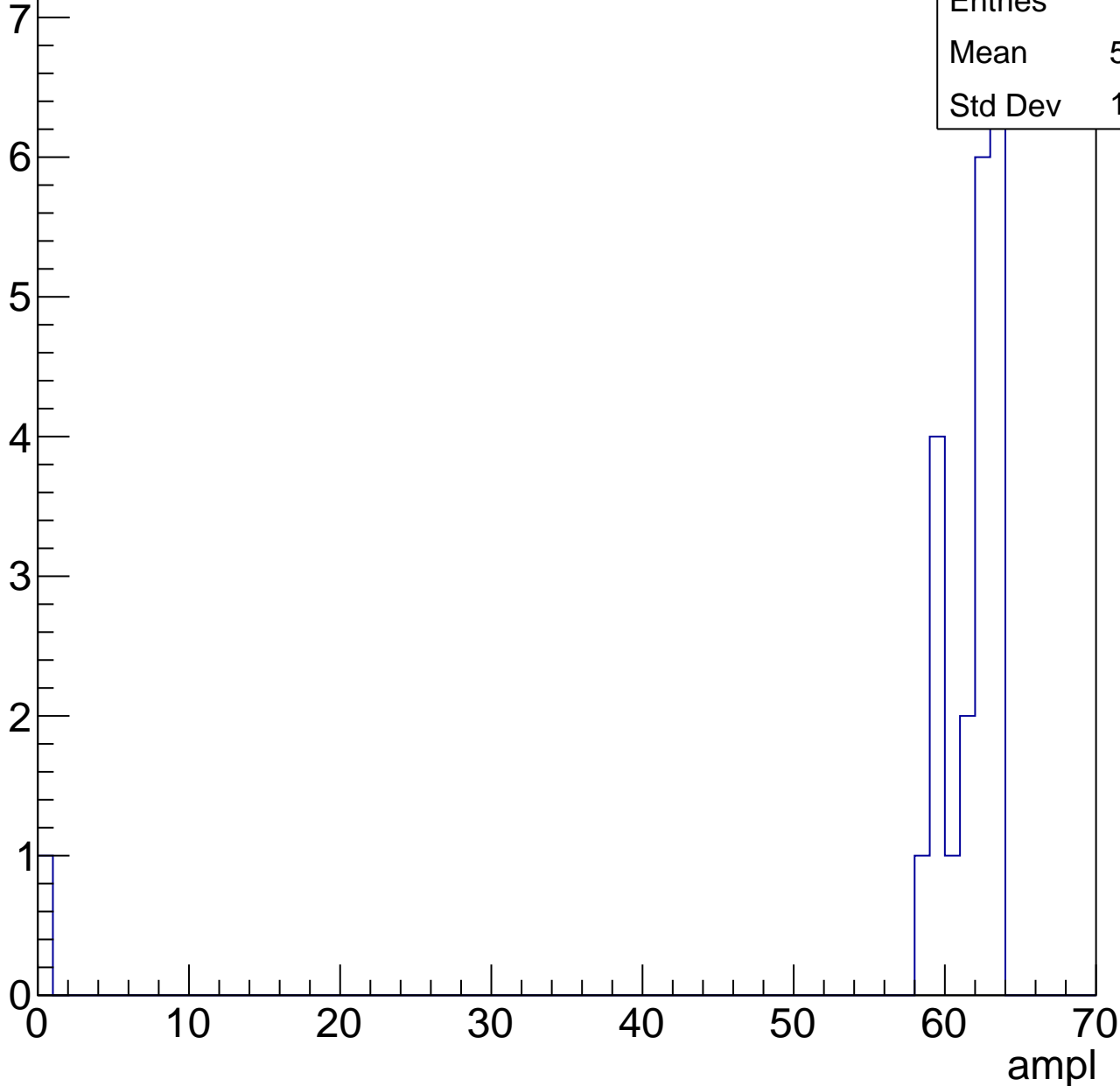


B1L102S, U14-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

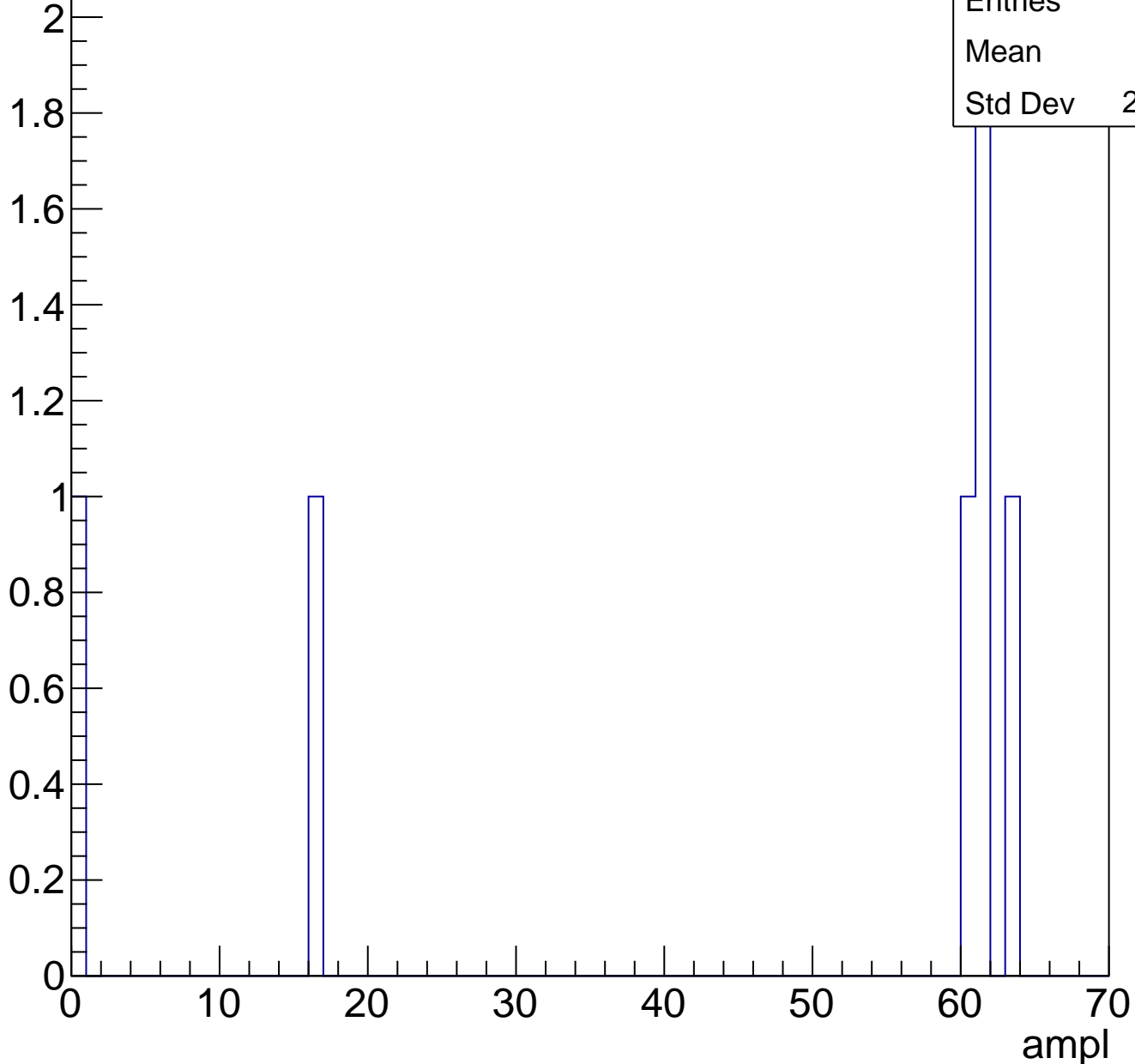
Entries	22
Mean	58.59
Std Dev	12.89



B1L102S, U14-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	6
Mean	43.5
Std Dev	25.54

B1L102S, U14-ch35, adc0

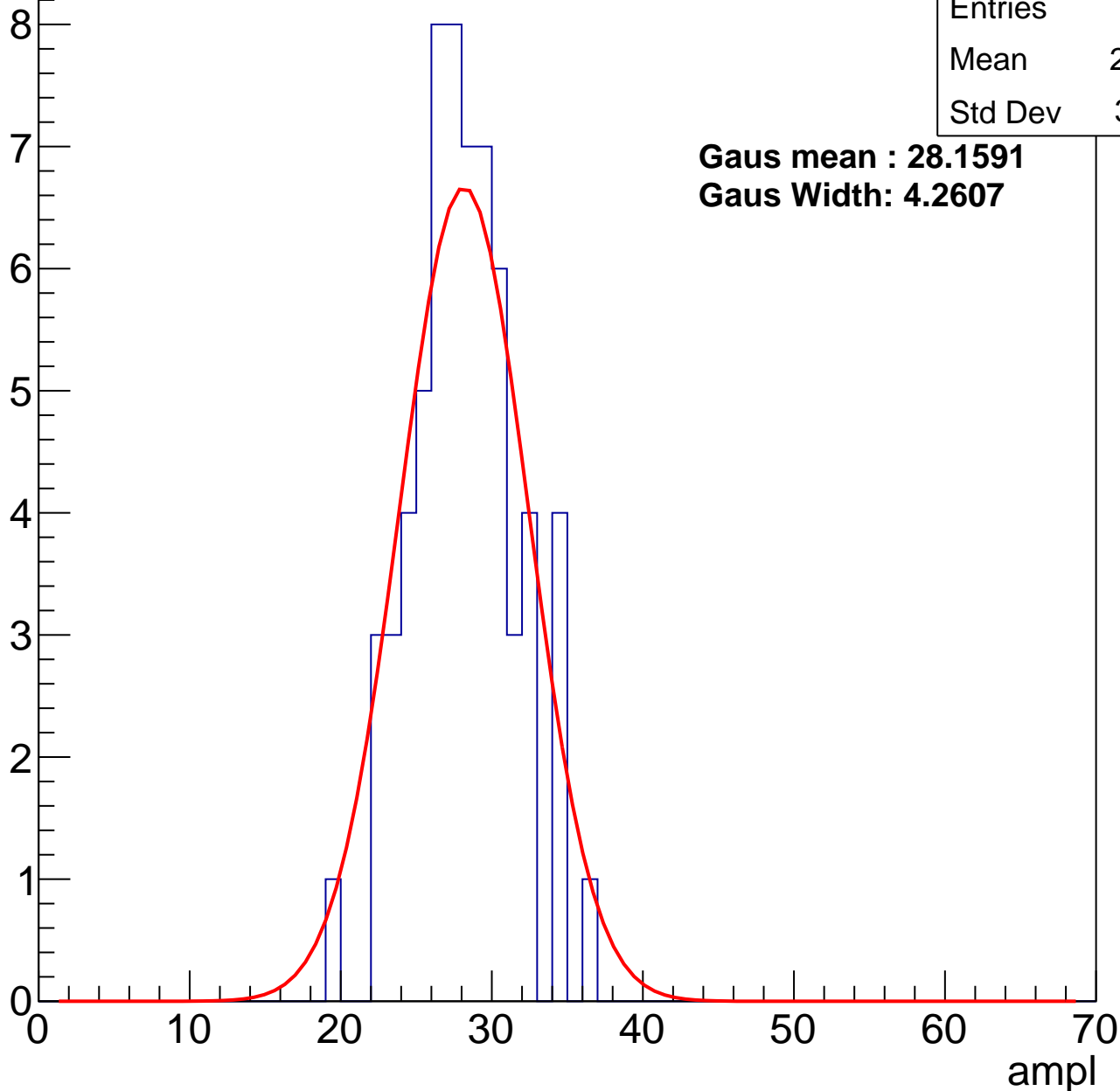
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.67
Std Dev	3.391

Gaus mean : 28.1591

Gaus Width: 4.2607



B1L102S, U14-ch35, adc1

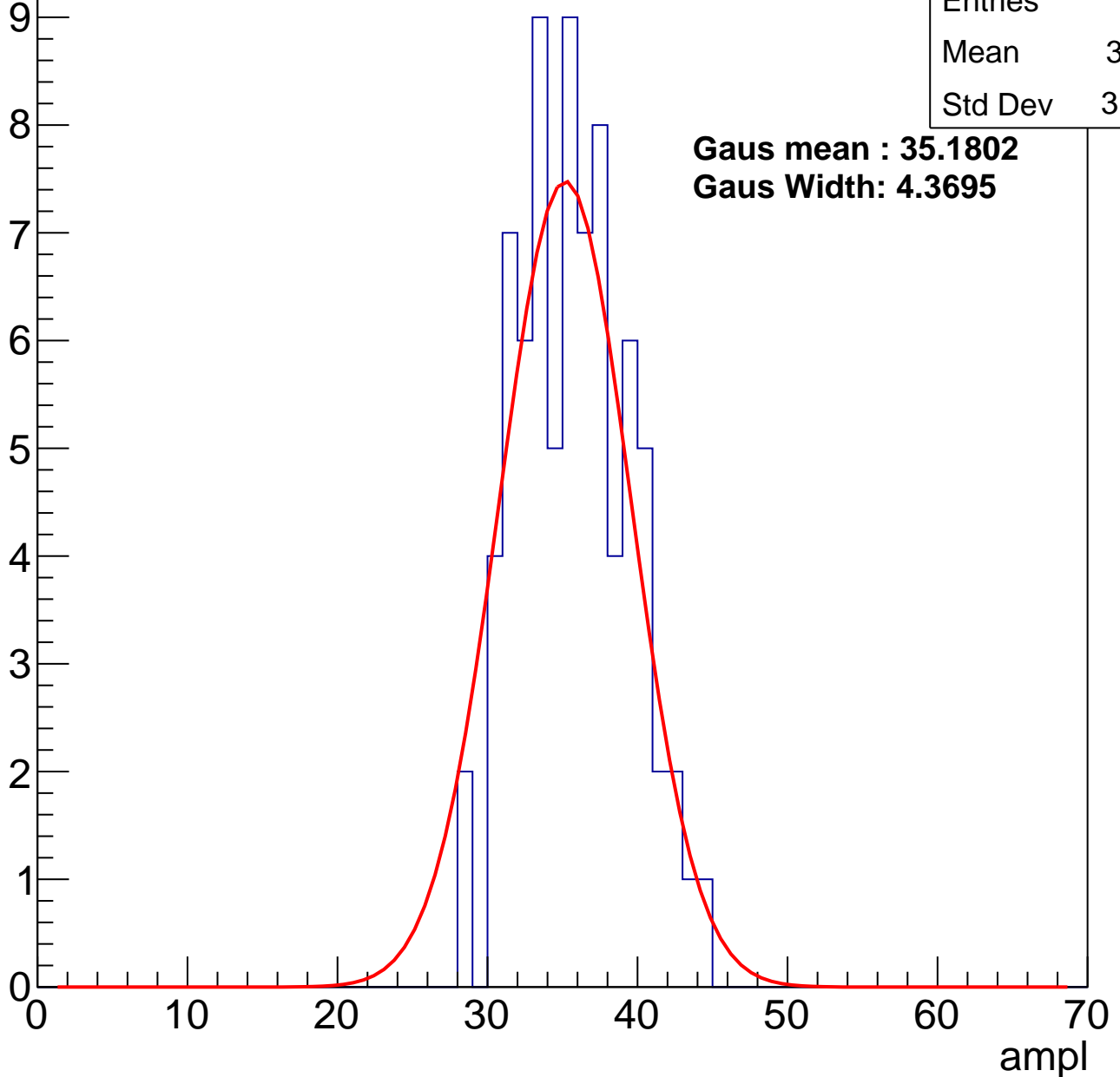
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.31
Std Dev	3.596

Gaus mean : 35.1802

Gaus Width: 4.3695



B1L102S, U14-ch35, adc2

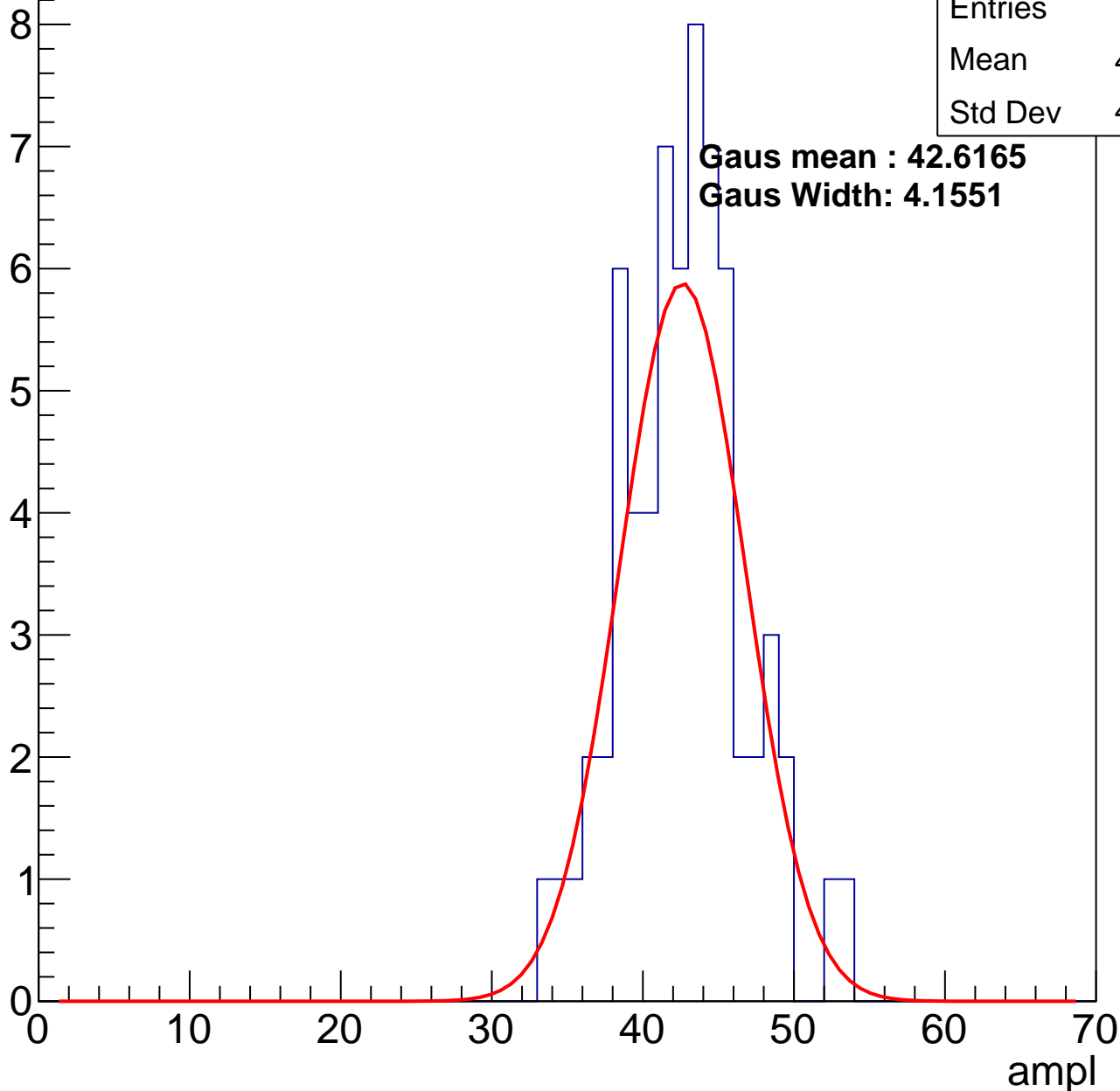
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.21
Std Dev	4.021

Gaus mean : 42.6165

Gaus Width: 4.1551

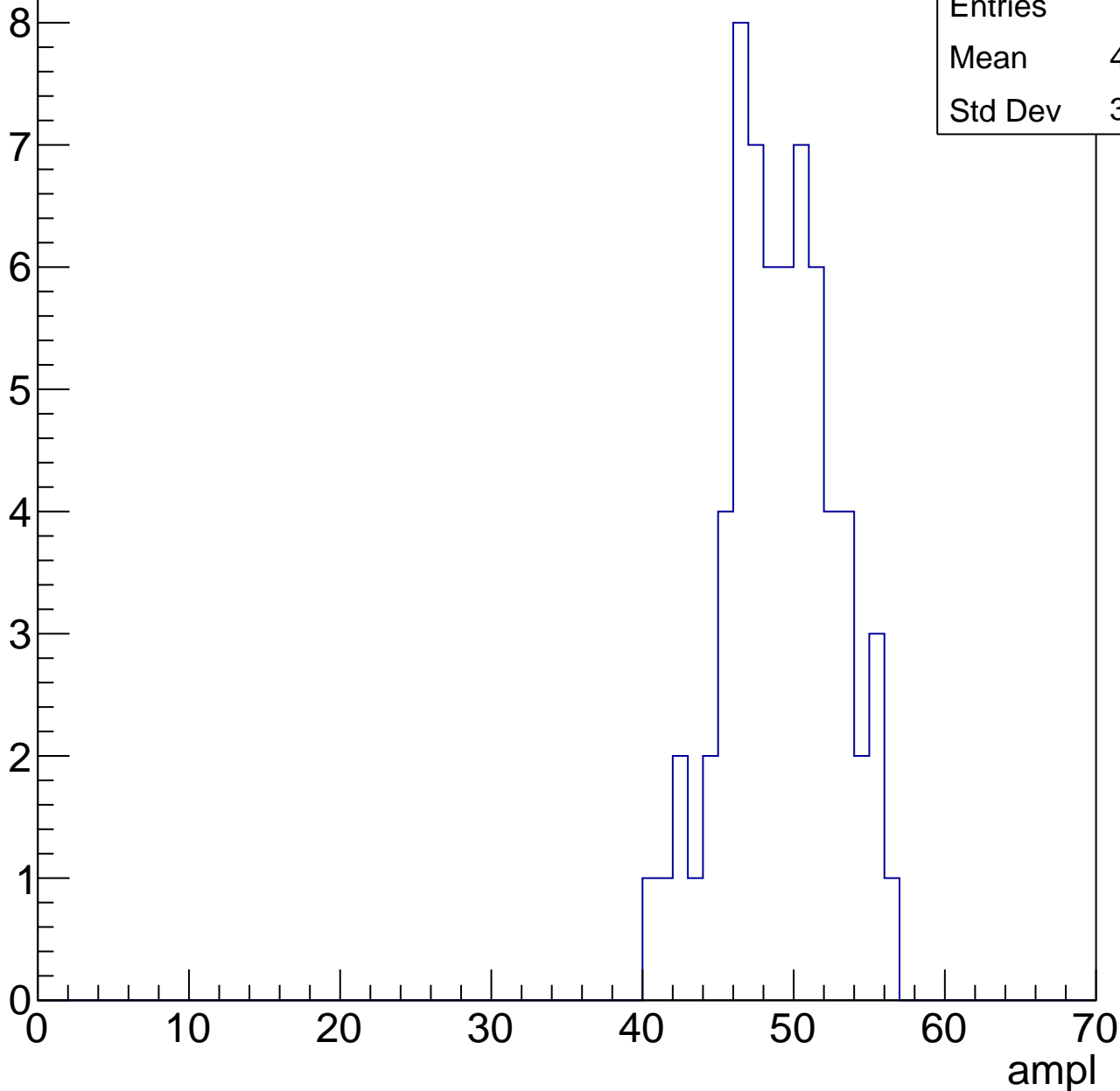


B1L102S, U14-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.62
Std Dev	3.568

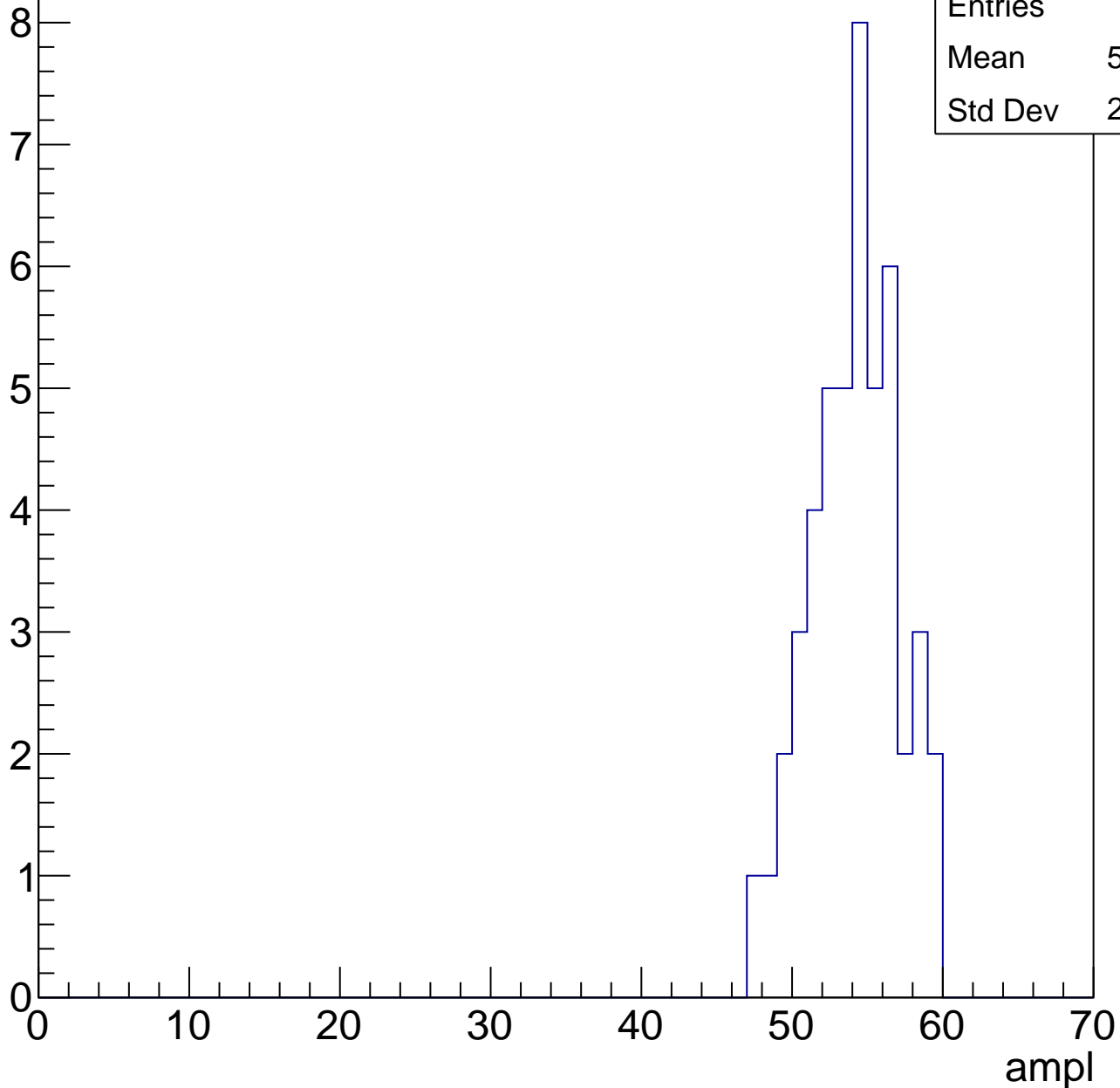


B1L102S, U14-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	53.64
Std Dev	2.854



B1L102S, U14-ch35, adc5

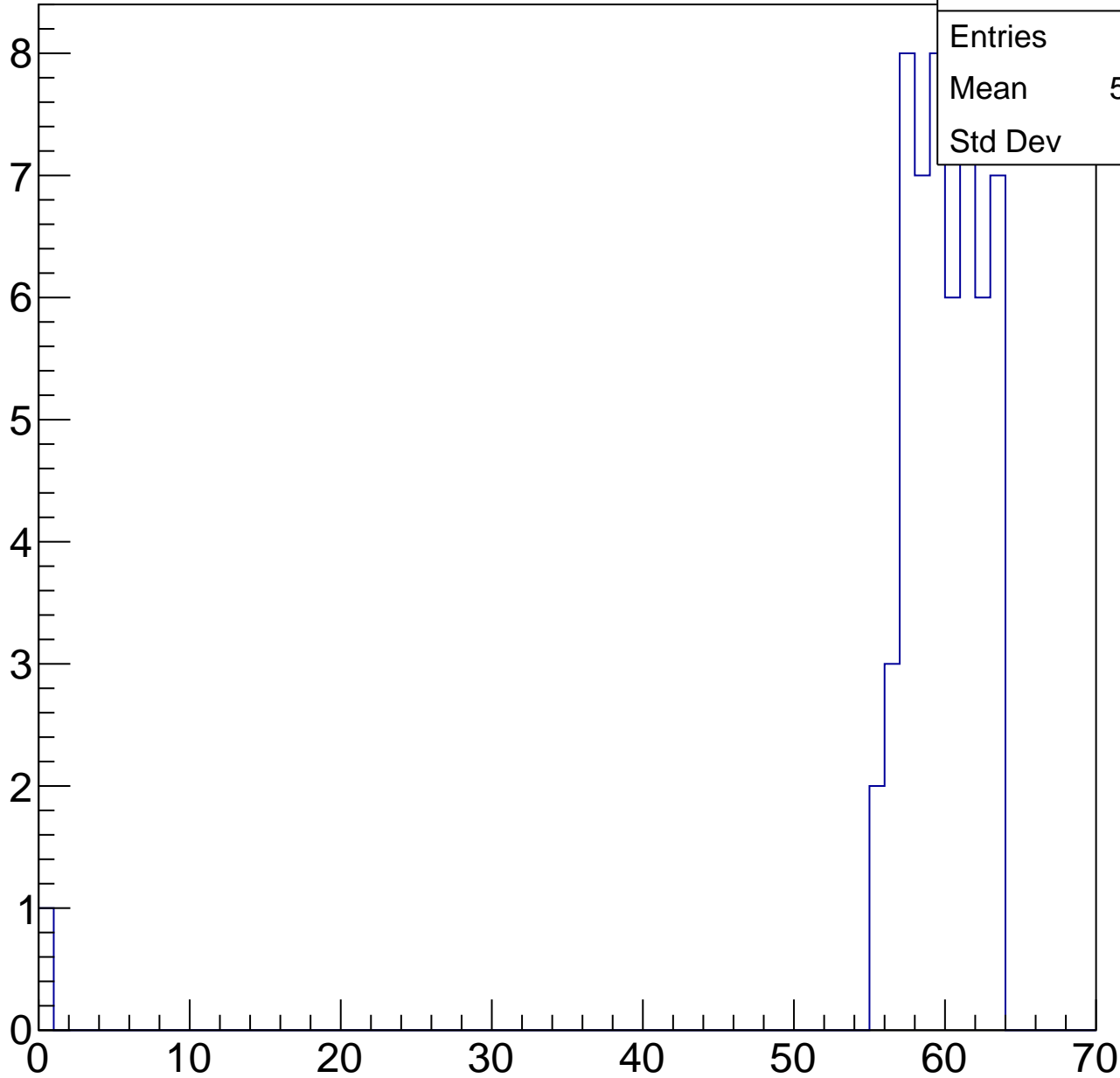
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.45
Std Dev	8.2

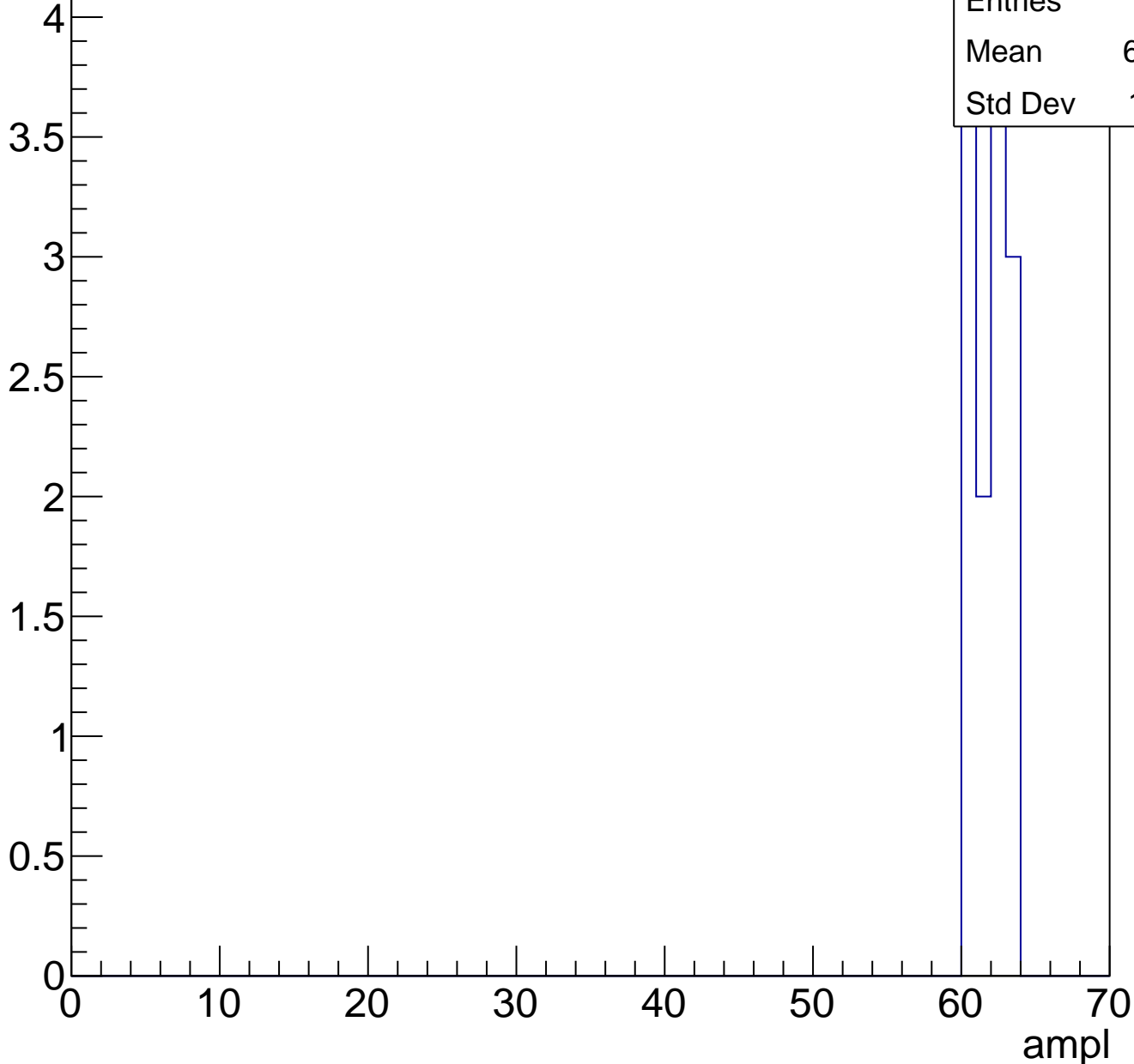
ampl



B1L102S, U14-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch36, adc0

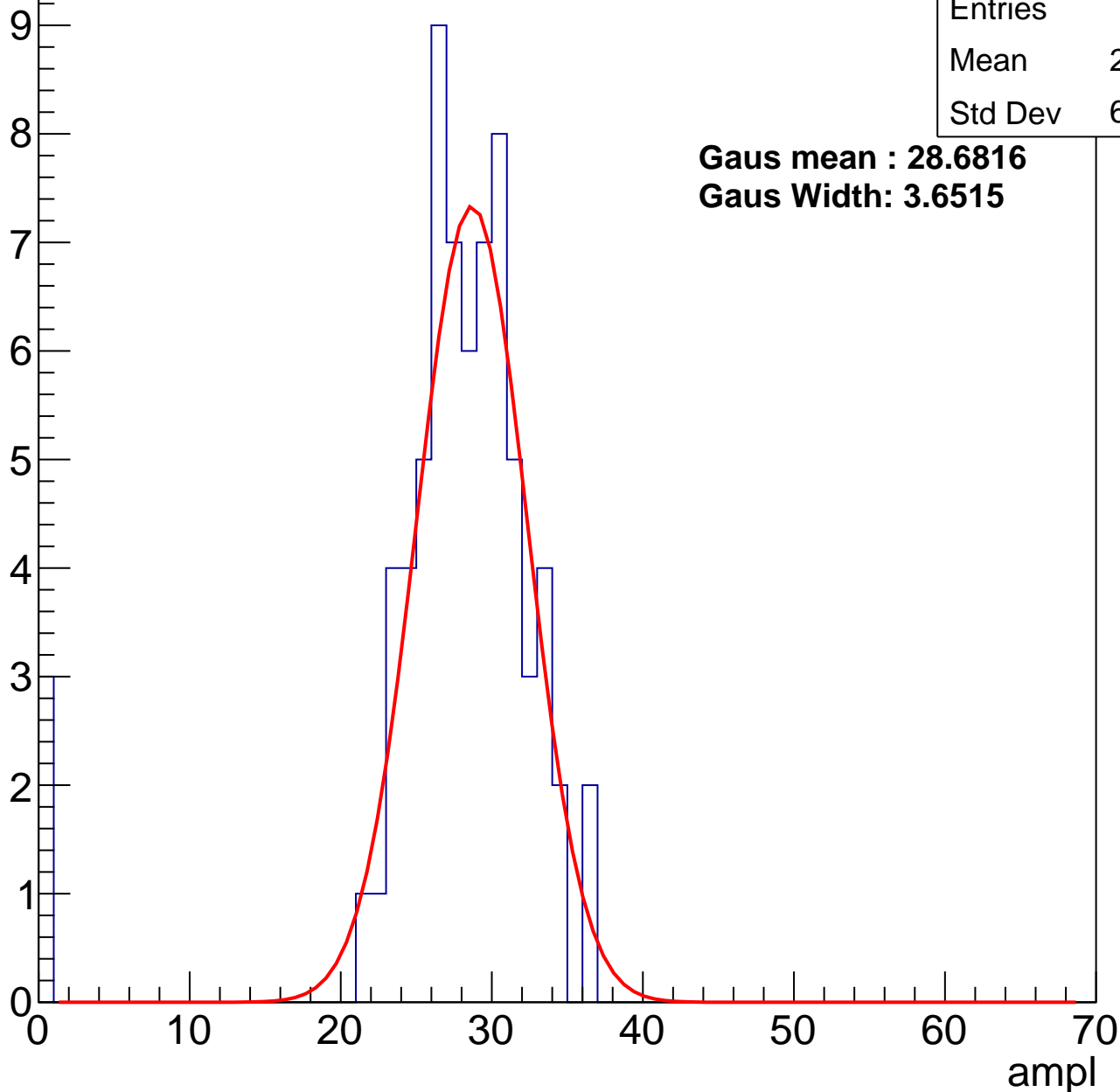
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	26.94
Std Dev	6.545

Gaus mean : 28.6816

Gaus Width: 3.6515



B1L102S, U14-ch36, adc1

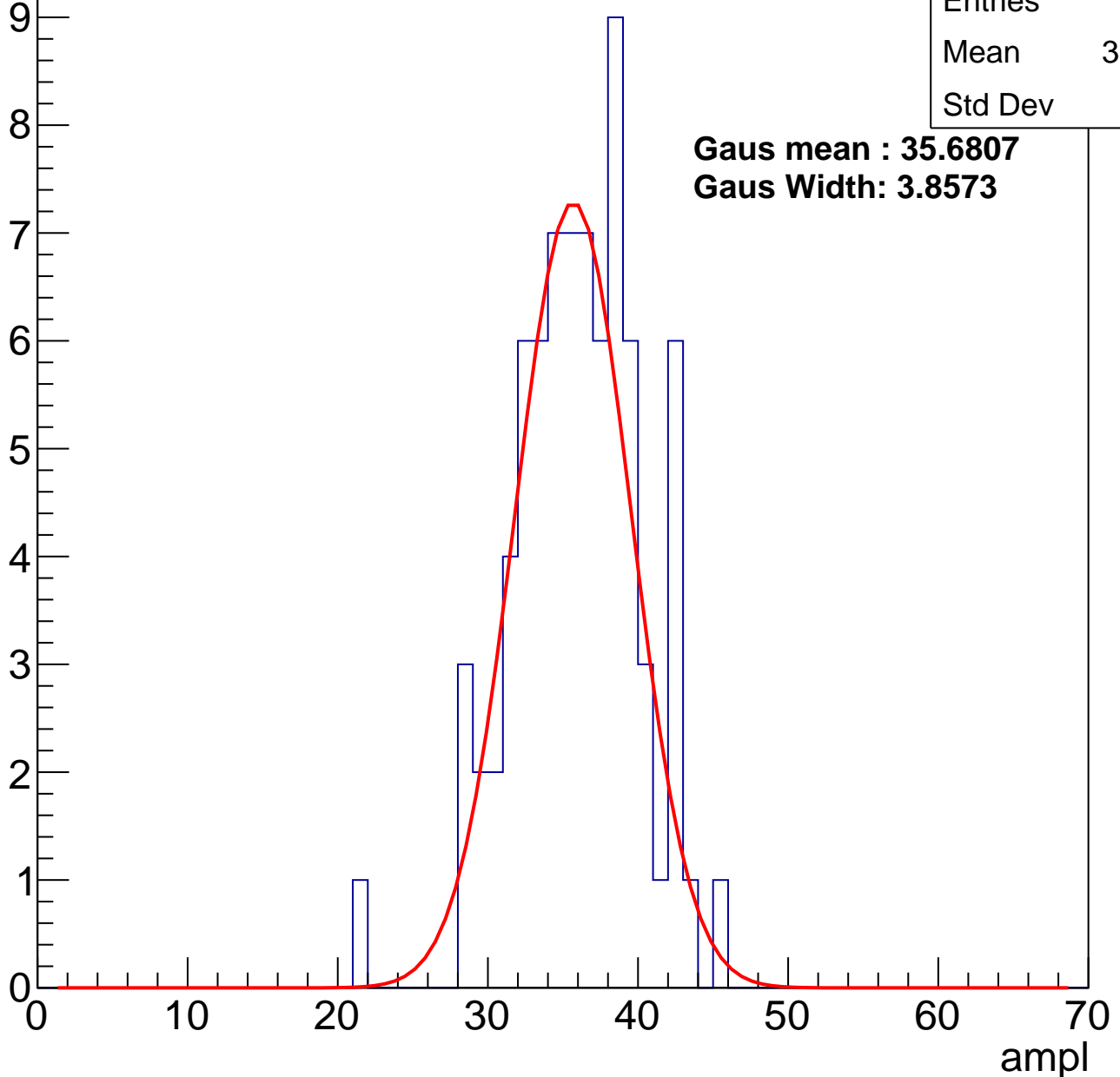
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.53
Std Dev	4.19

Gaus mean : 35.6807

Gaus Width: 3.8573



B1L102S, U14-ch36, adc2

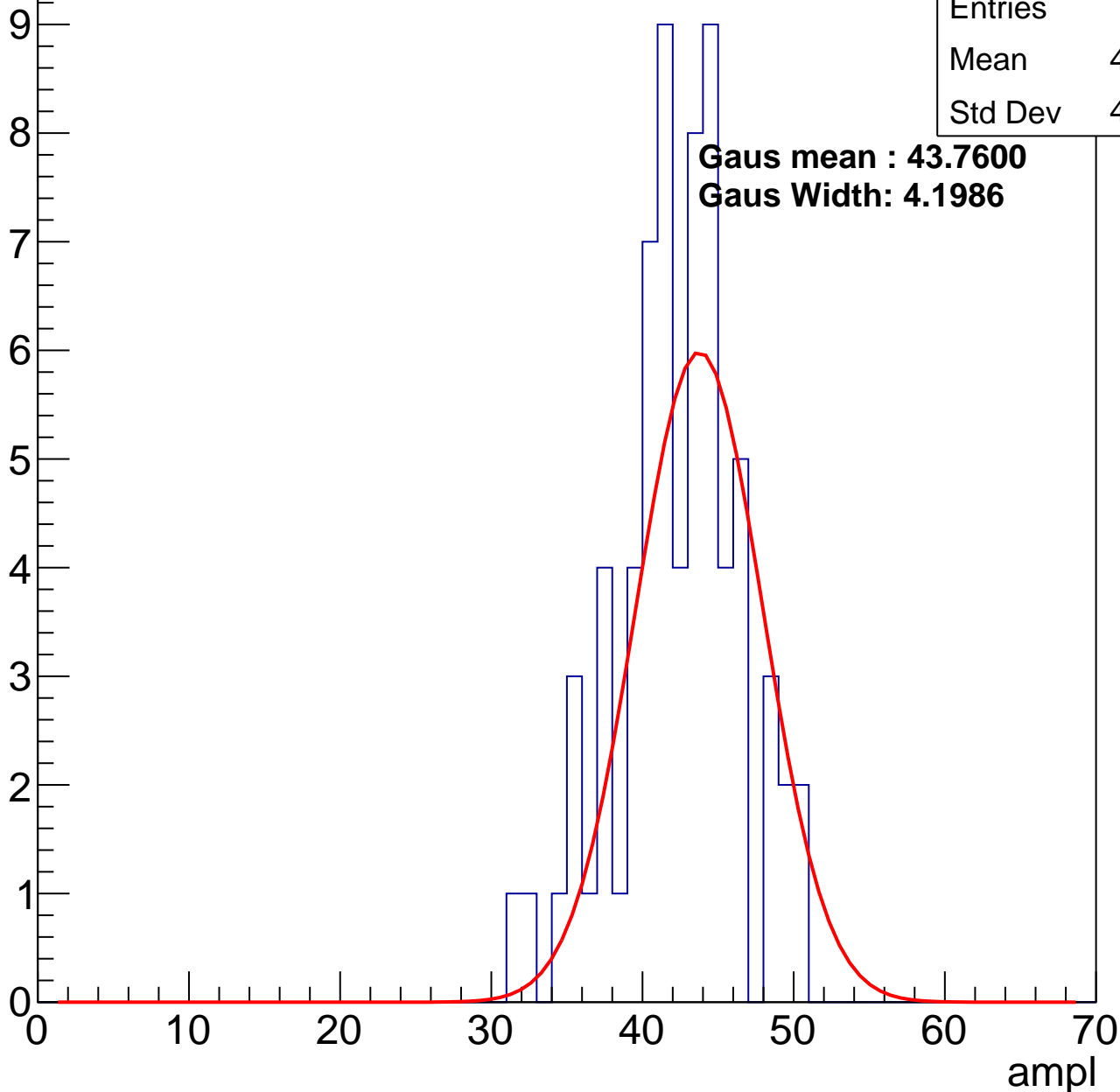
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.87
Std Dev	4.082

Gaus mean : 43.7600

Gaus Width: 4.1986

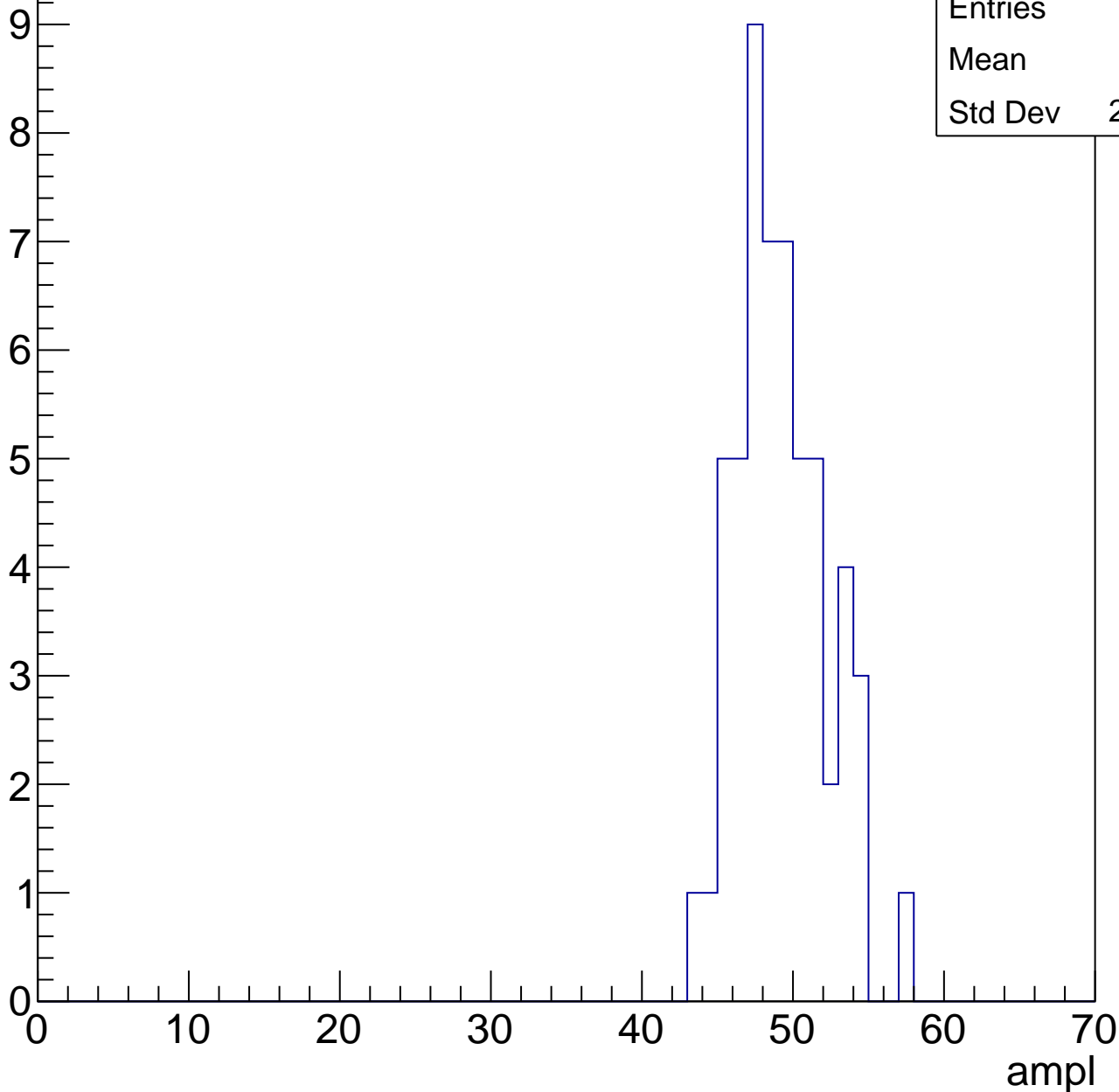


B1L102S, U14-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

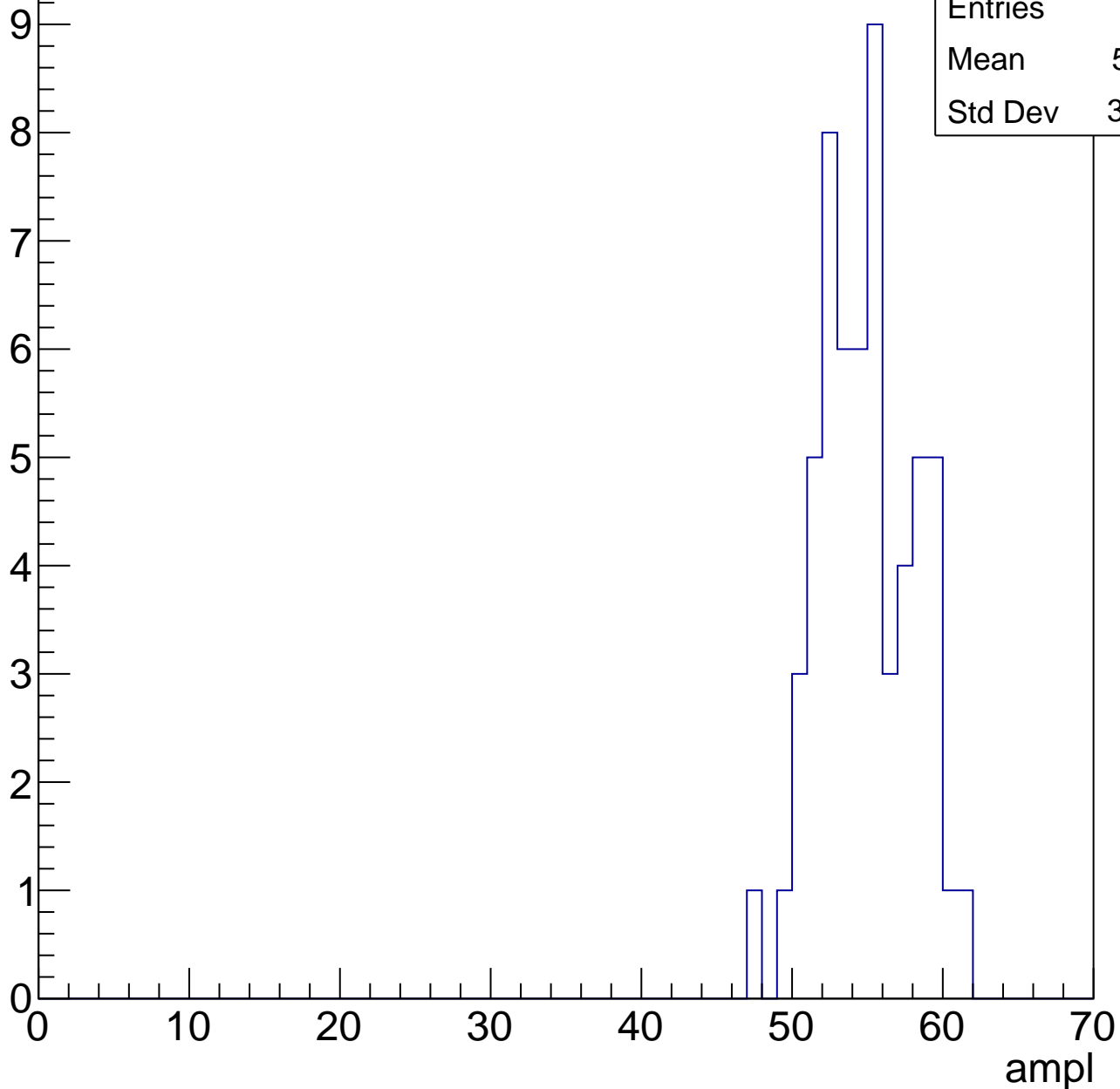
Entries	55
Mean	48.8
Std Dev	2.932



B1L102S, U14-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

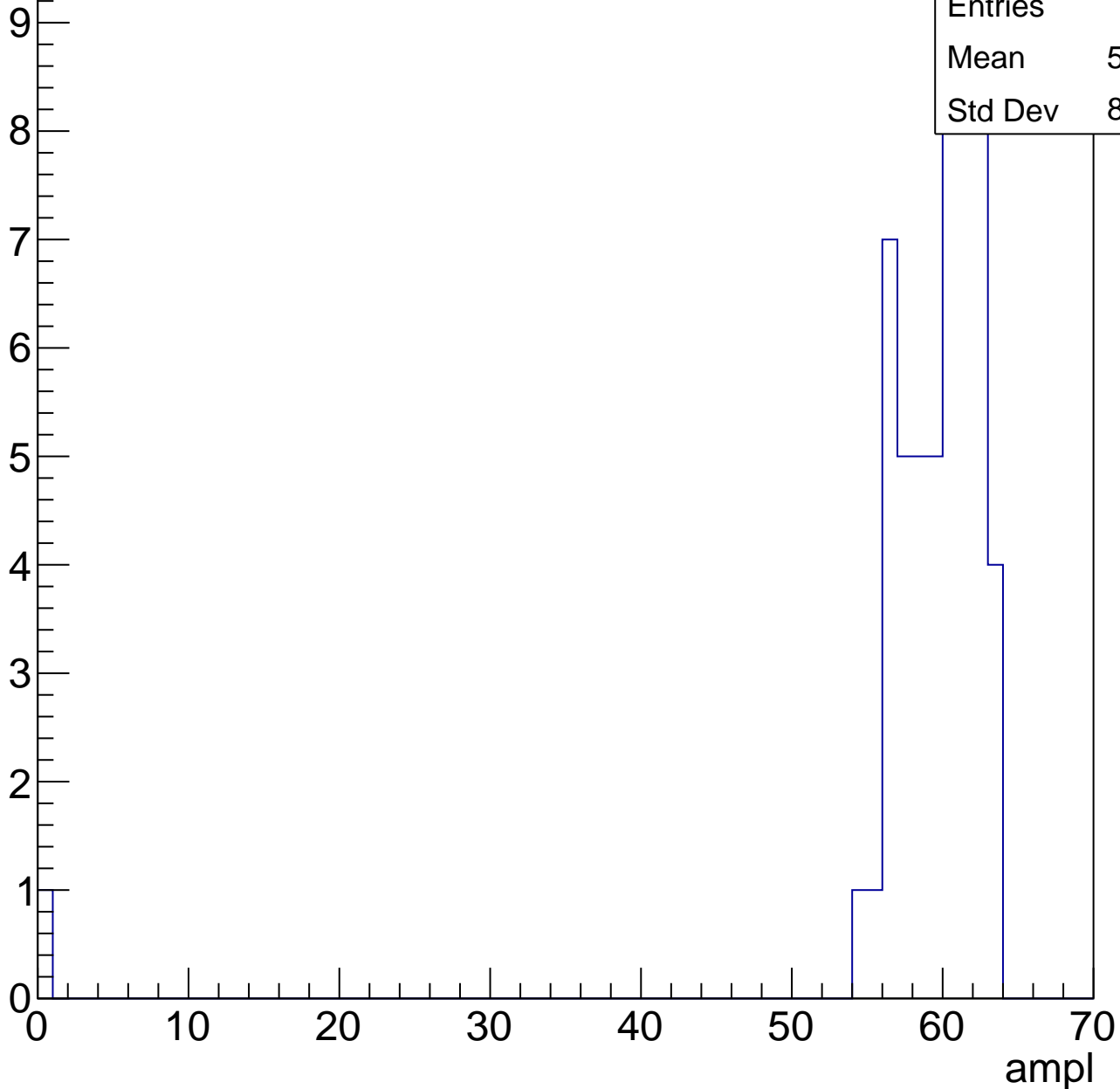


B1L102S, U14-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.39
Std Dev	8.215

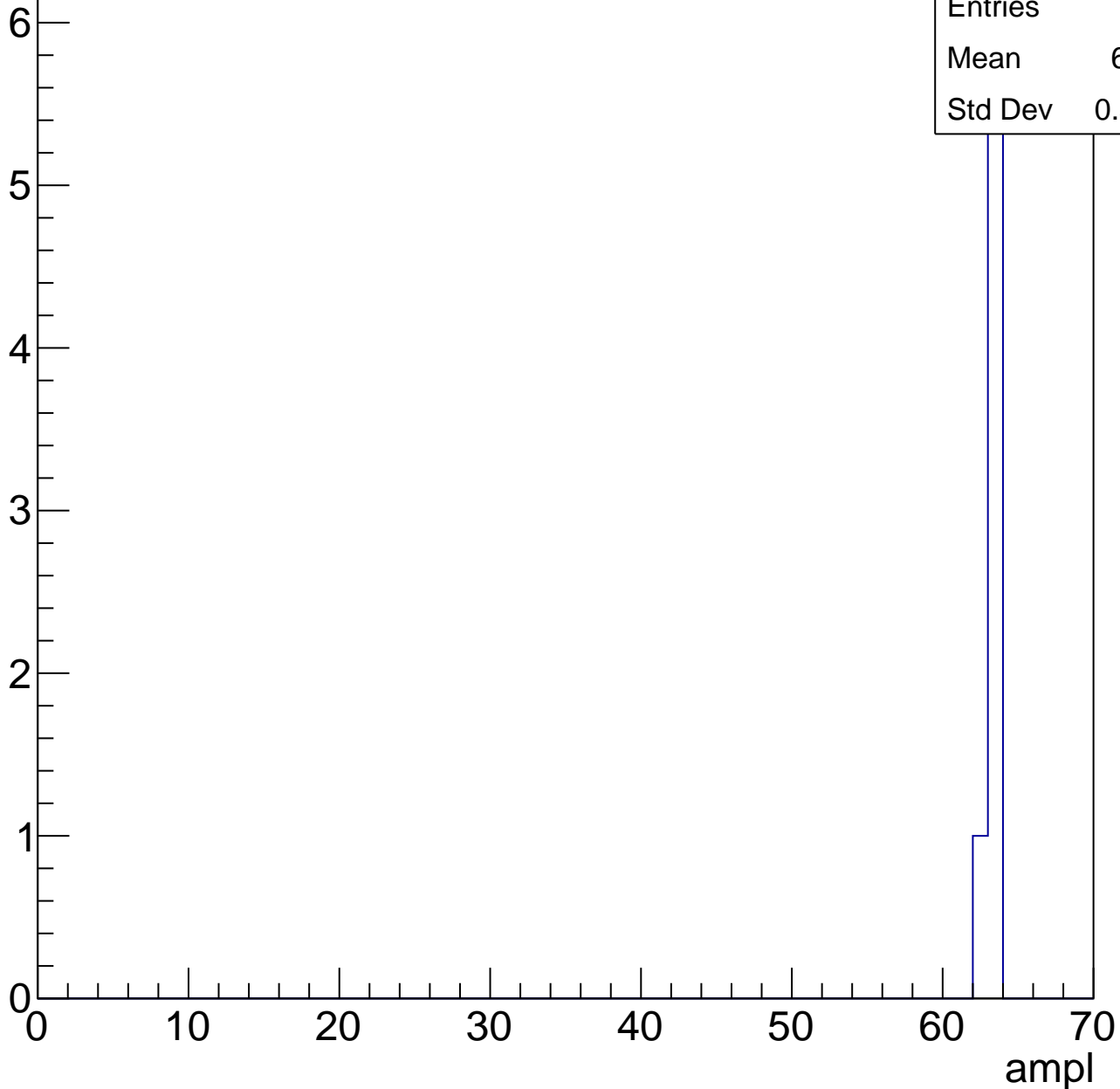


B1L102S, U14-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.86
Std Dev	0.3499



B1L102S, U14-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch37, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	28.7
Std Dev	4.71

Gaus mean : 29.2332

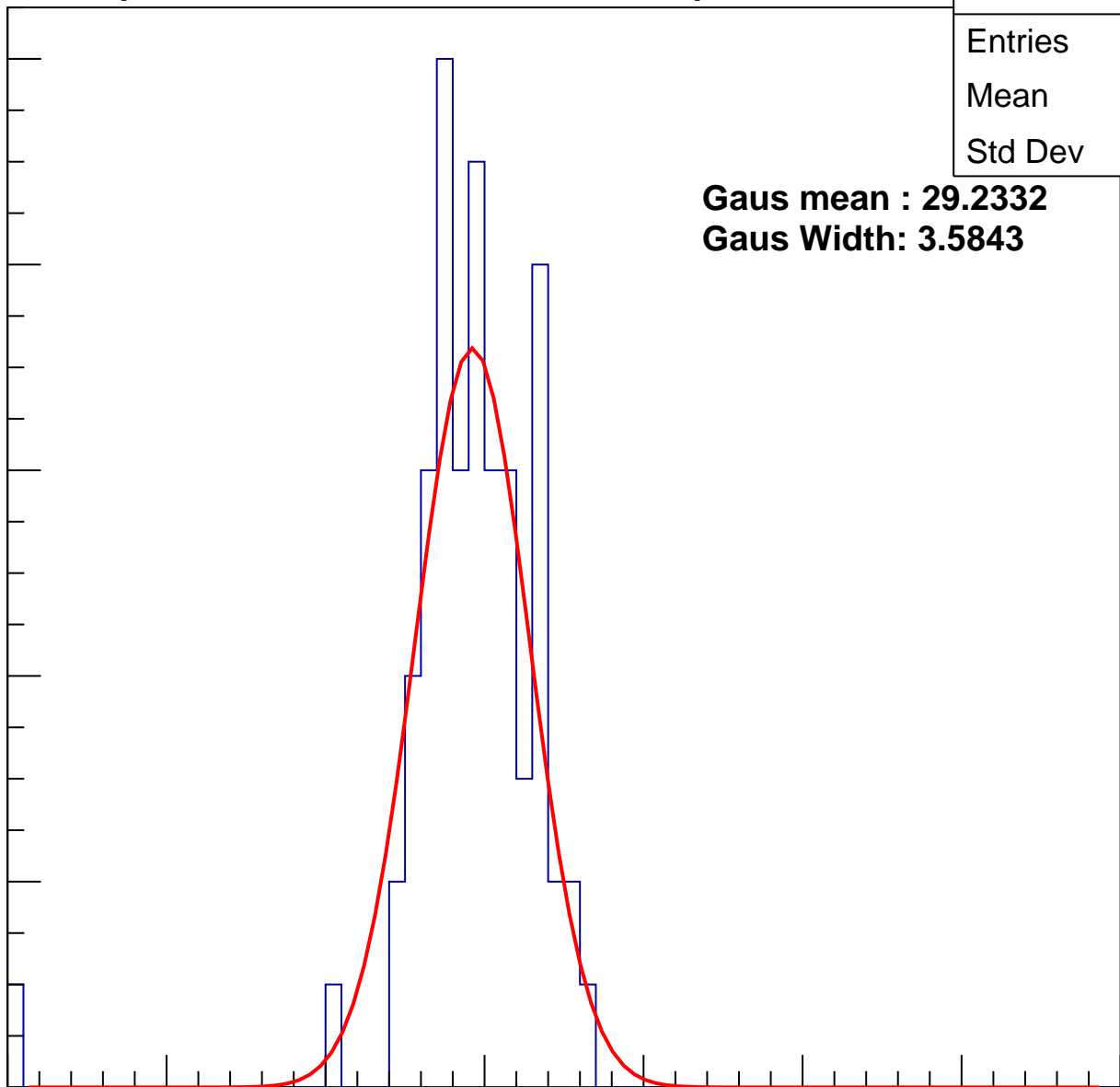
Gaus Width: 3.5843

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



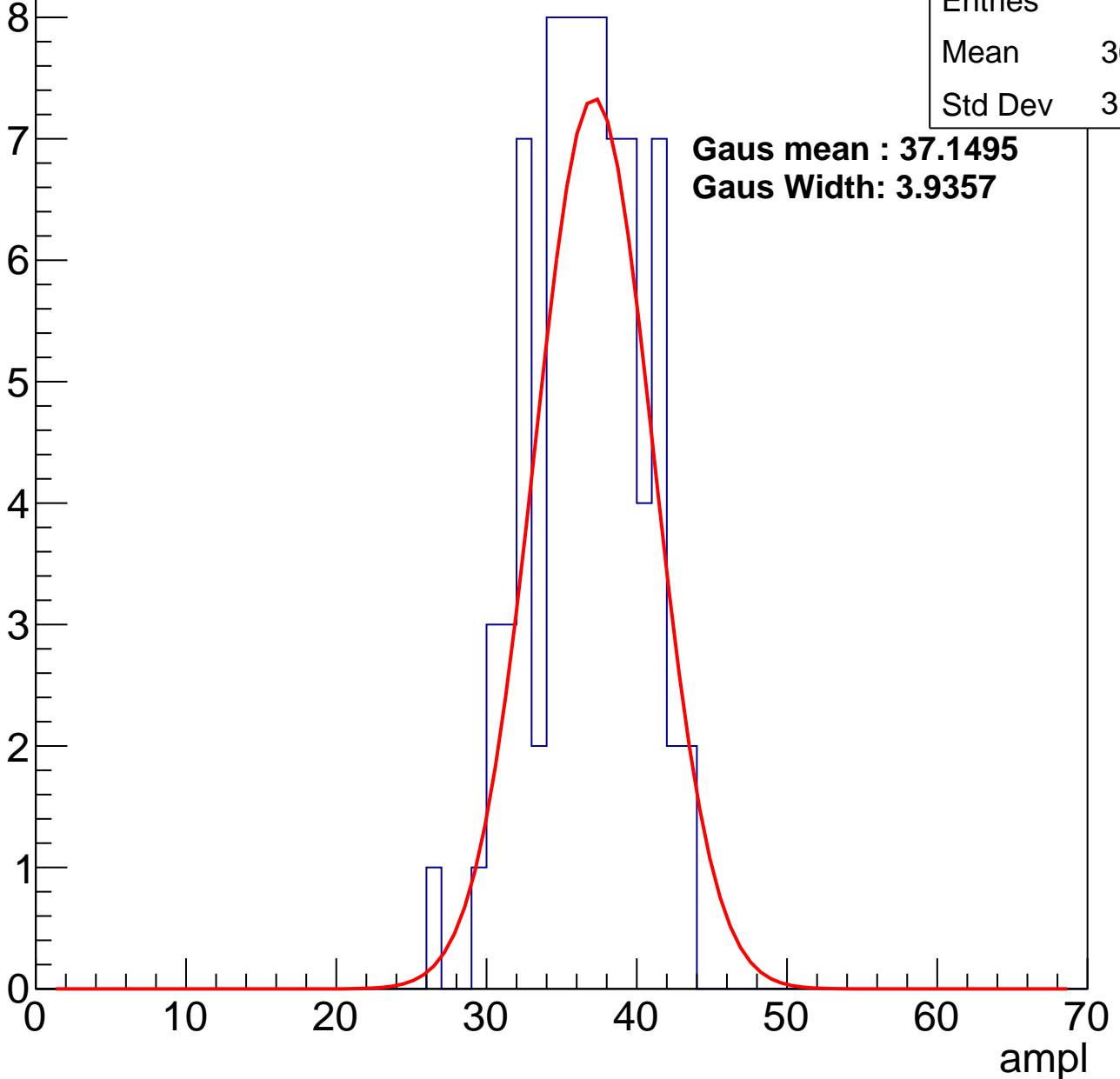
B1L102S, U14-ch37, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.15
Std Dev	3.595

Gaus mean : 37.1495
Gaus Width: 3.9357



B1L102S, U14-ch37, adc2

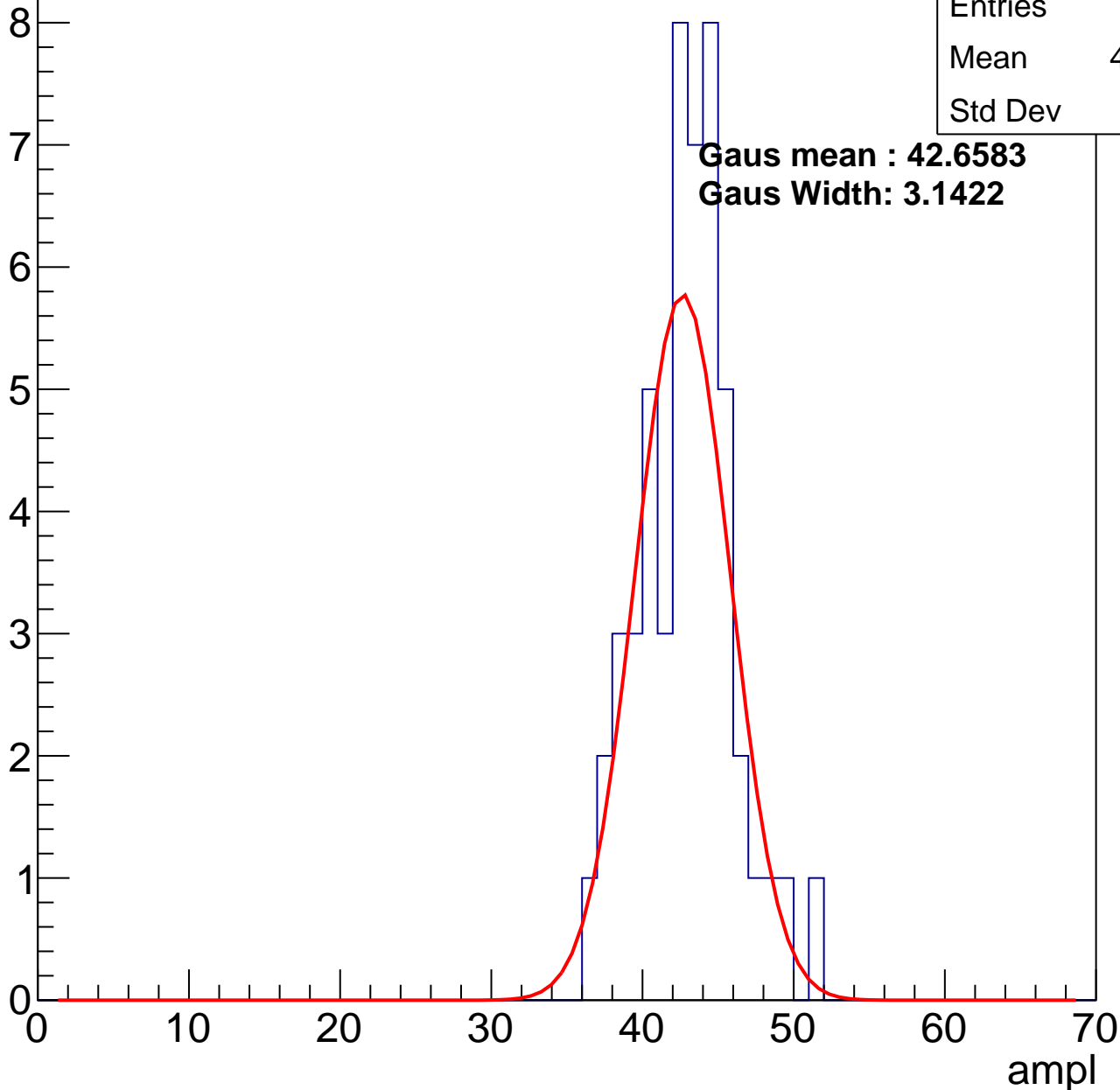
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.45
Std Dev	3.07

Gaus mean : 42.6583

Gaus Width: 3.1422

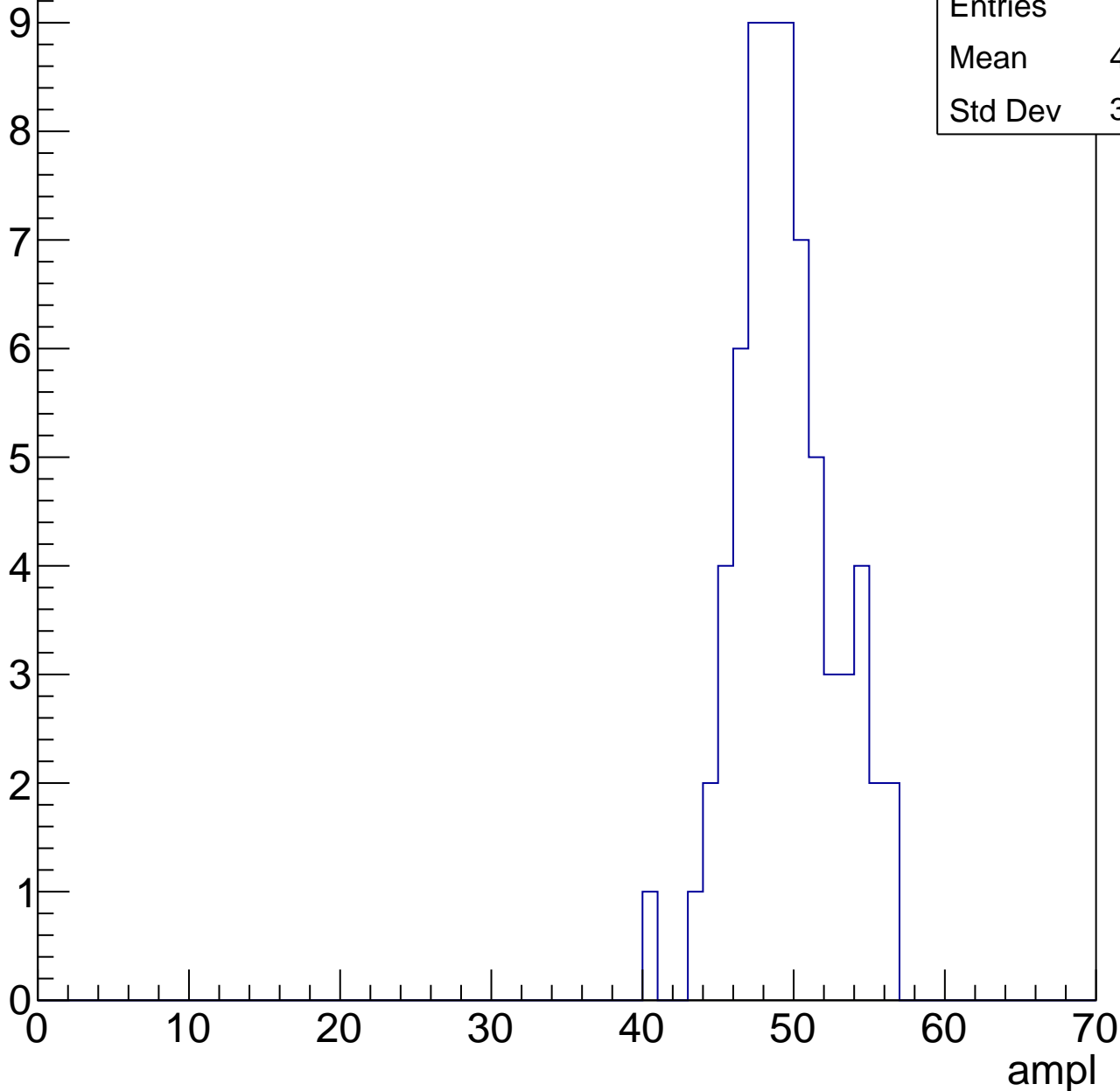


B1L102S, U14-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.97
Std Dev	3.237

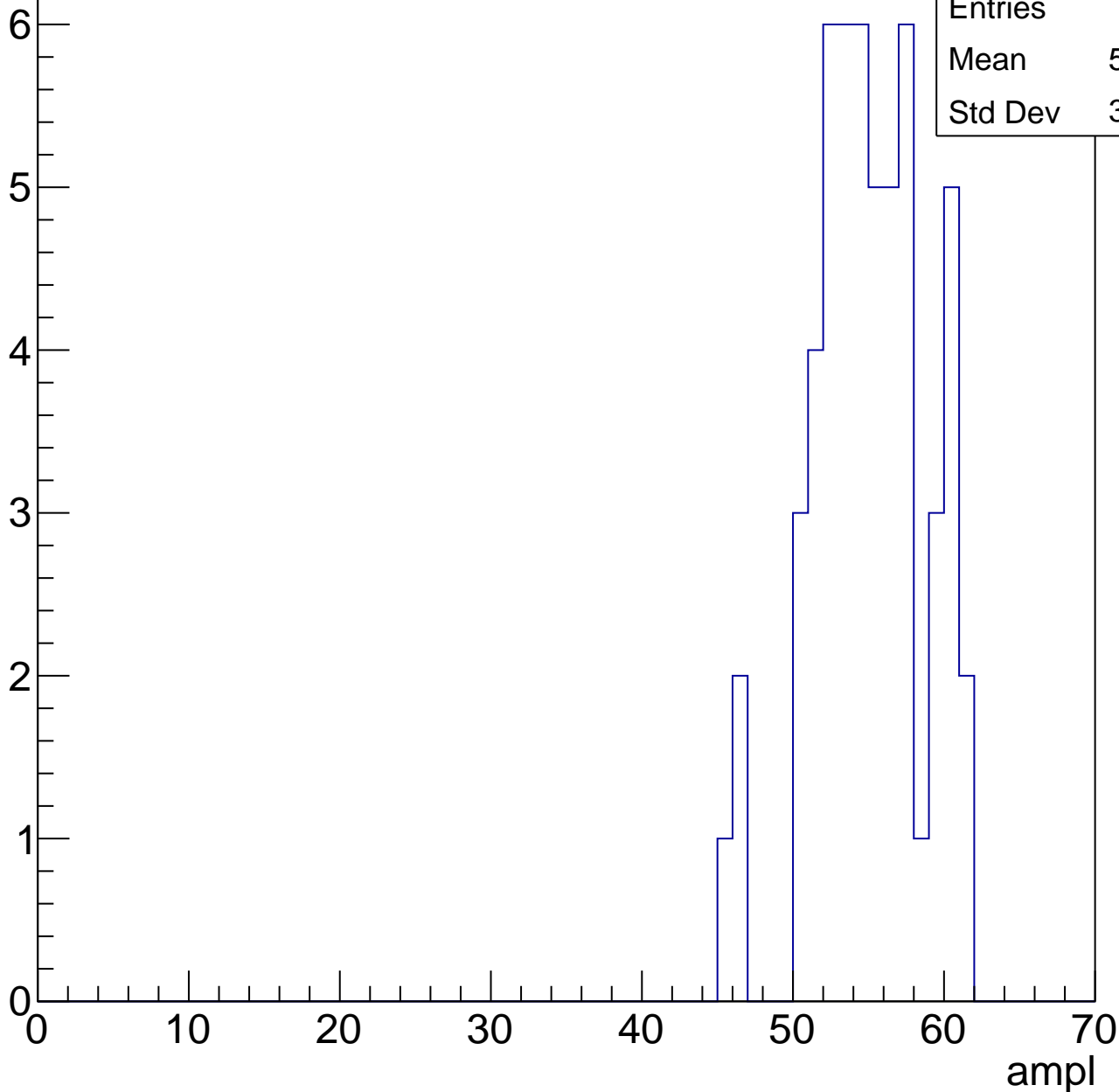


B1L102S, U14-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.53
Std Dev	3.707

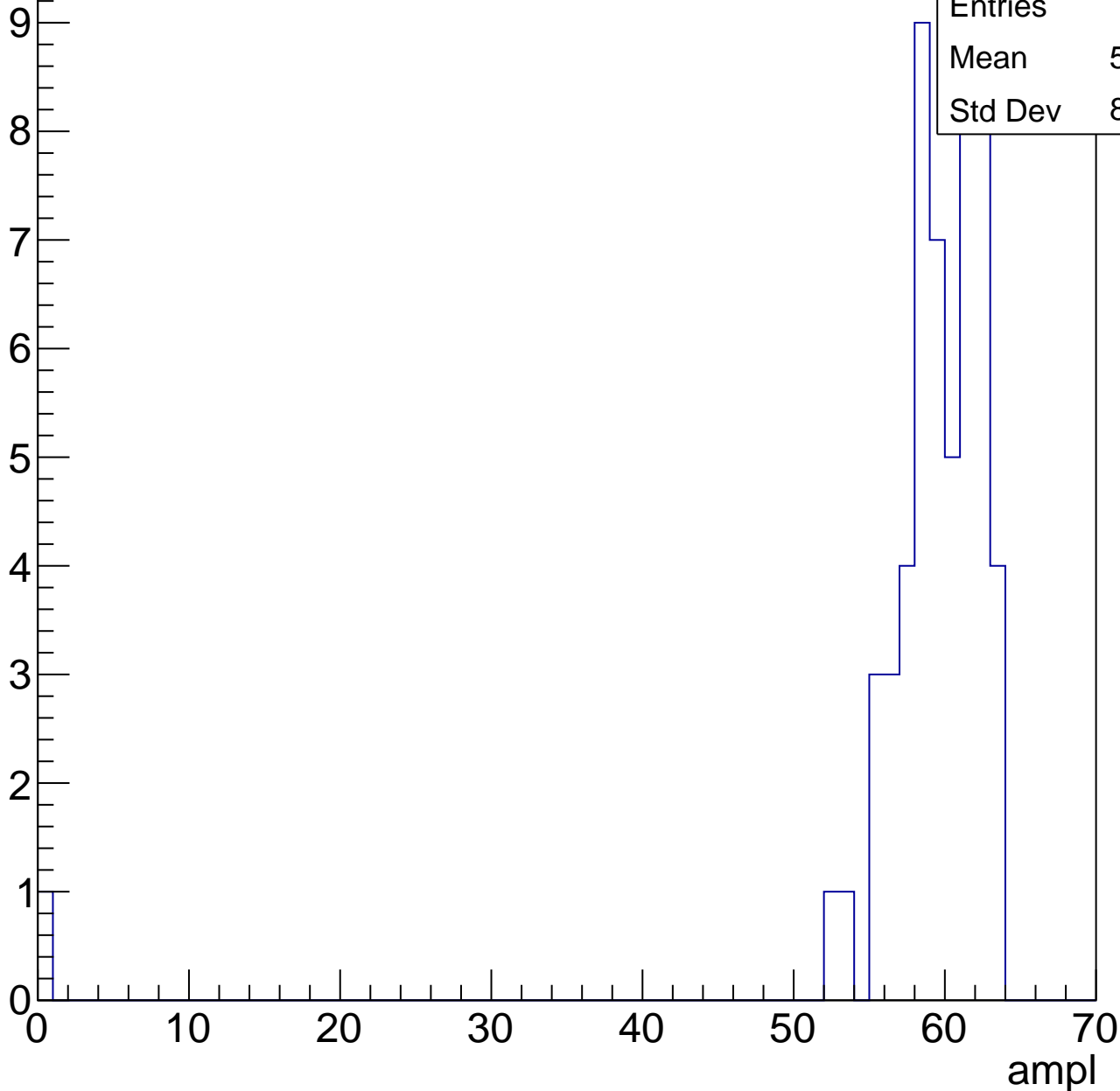


B1L102S, U14-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.09
Std Dev	8.383



B1L102S, U14-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch38, adc0

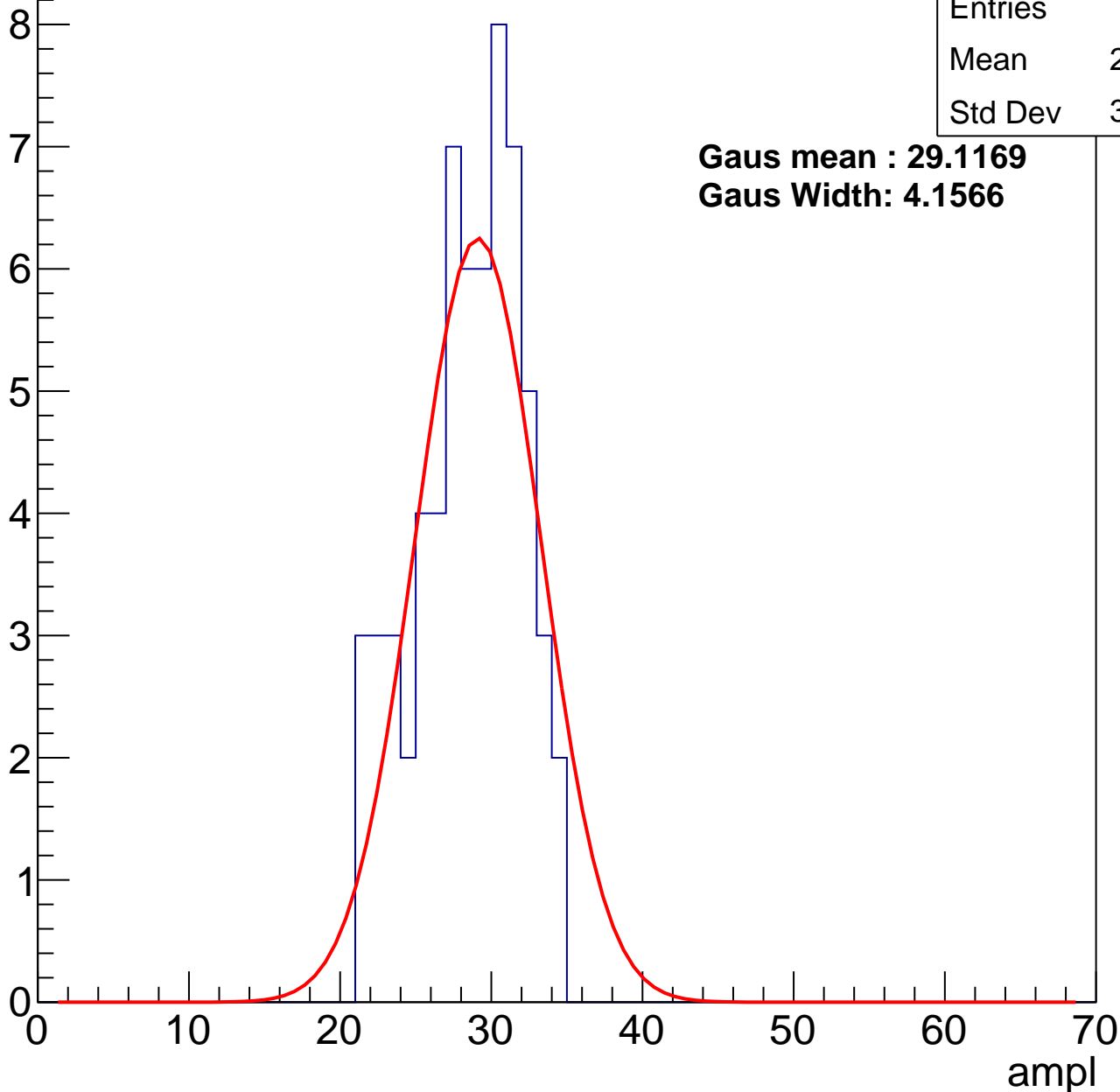
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.02
Std Dev	3.448

Gaus mean : 29.1169

Gaus Width: 4.1566



B1L102S, U14-ch38, adc1

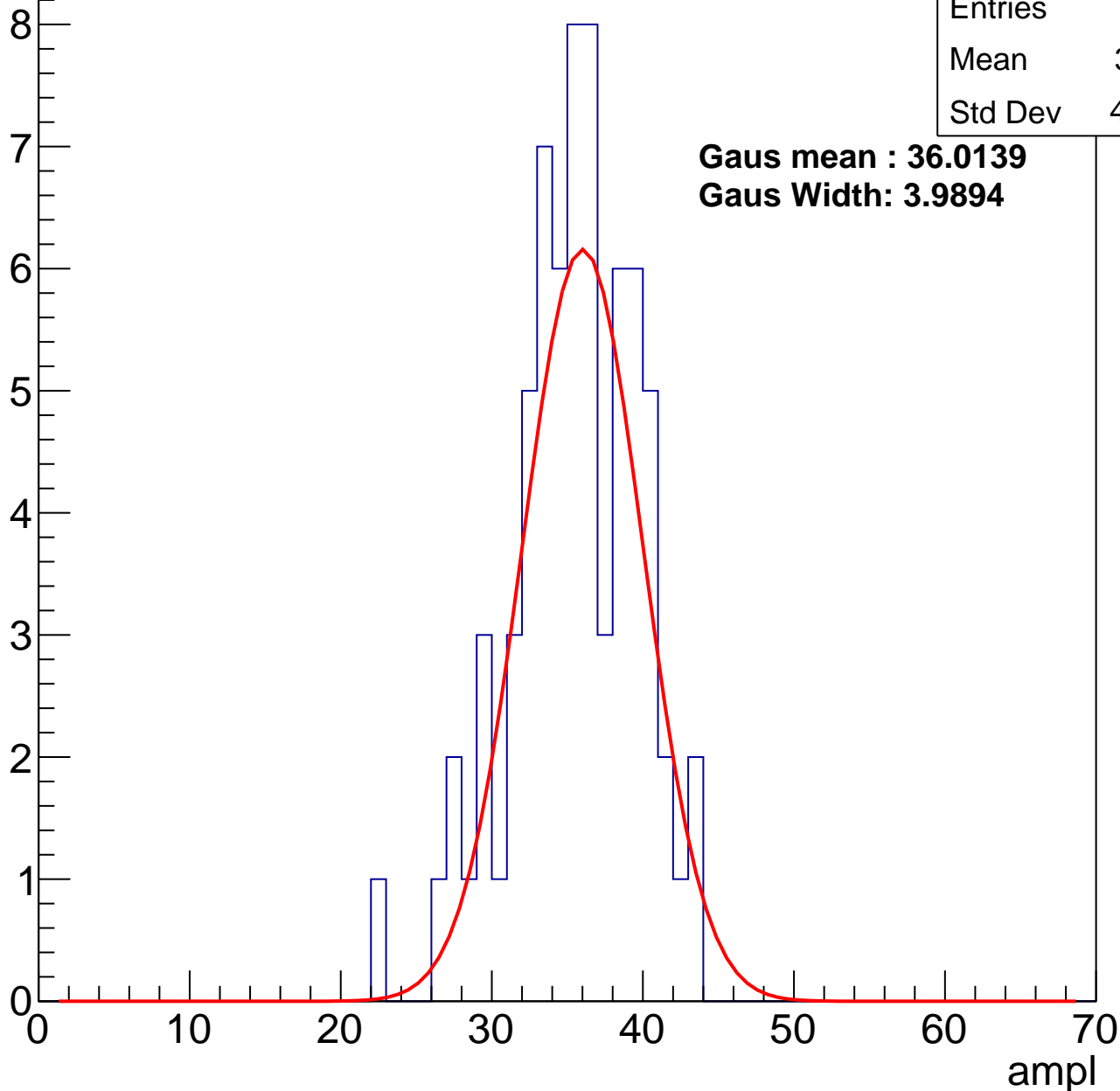
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.01
Std Dev	4.164

Gaus mean : 36.0139

Gaus Width: 3.9894



B1L102S, U14-ch38, adc2

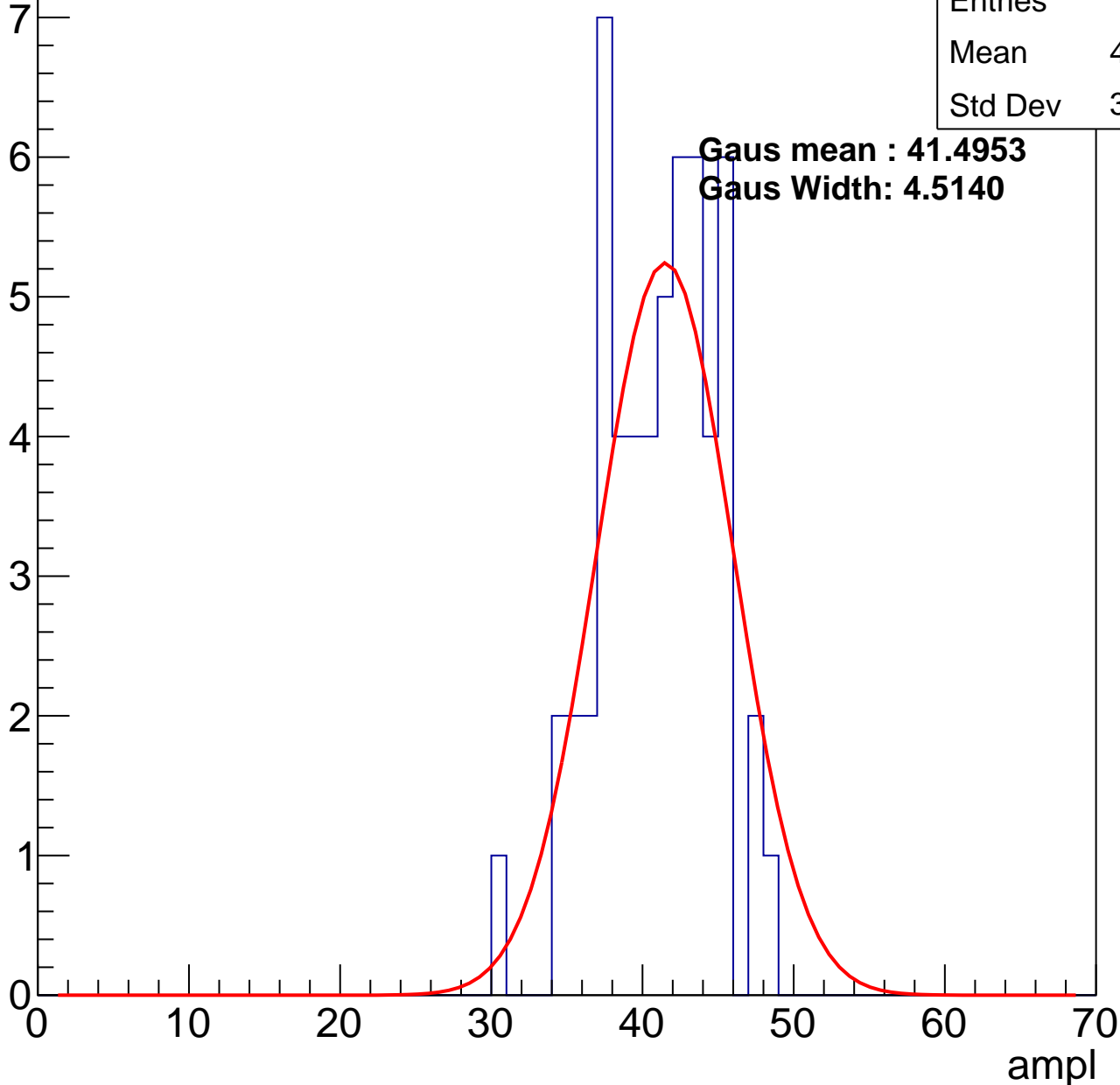
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	40.54
Std Dev	3.732

Gaus mean : 41.4953

Gaus Width: 4.5140



B1L102S, U14-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

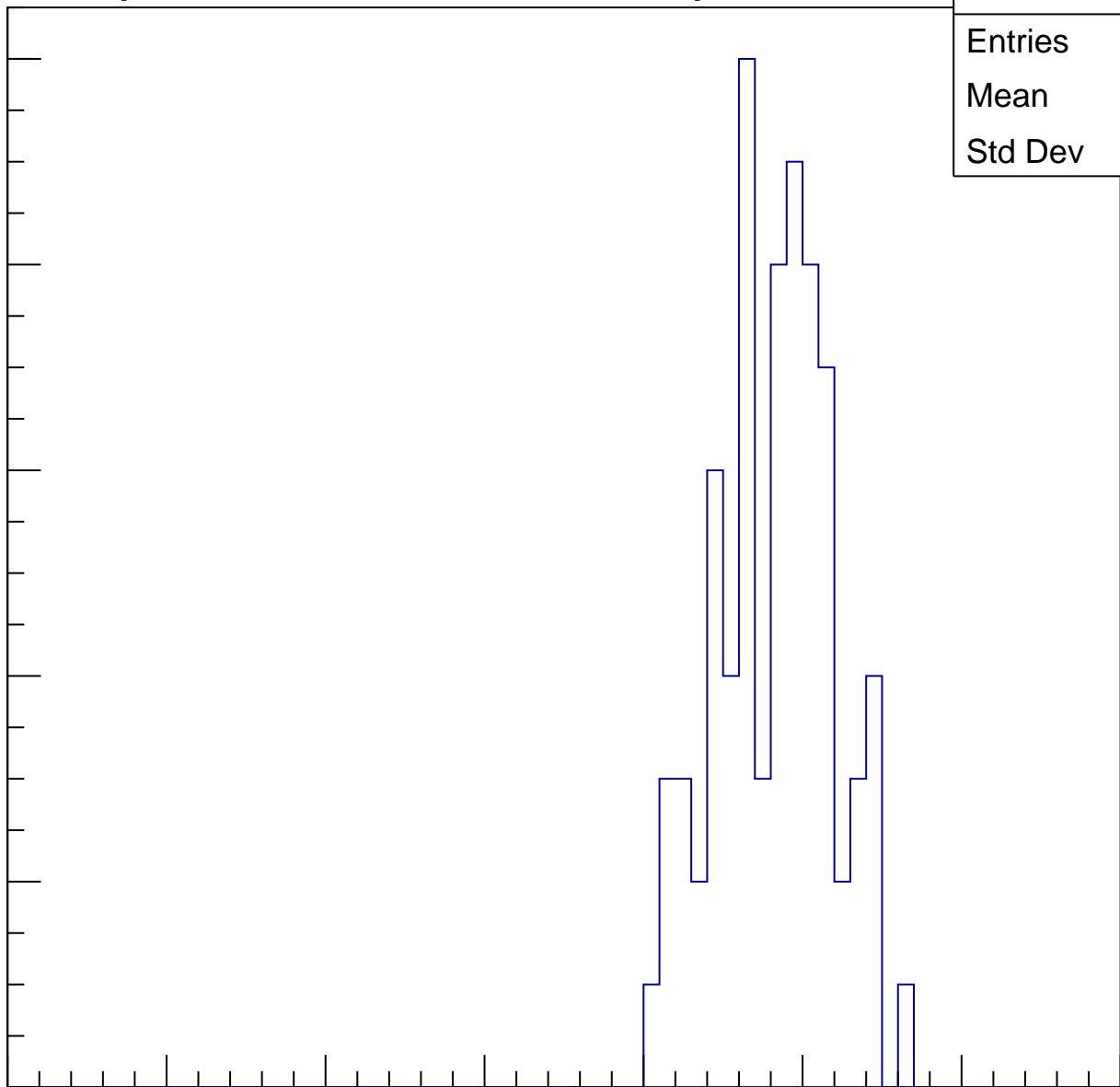
Entries	74
Mean	47.8
Std Dev	3.602

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

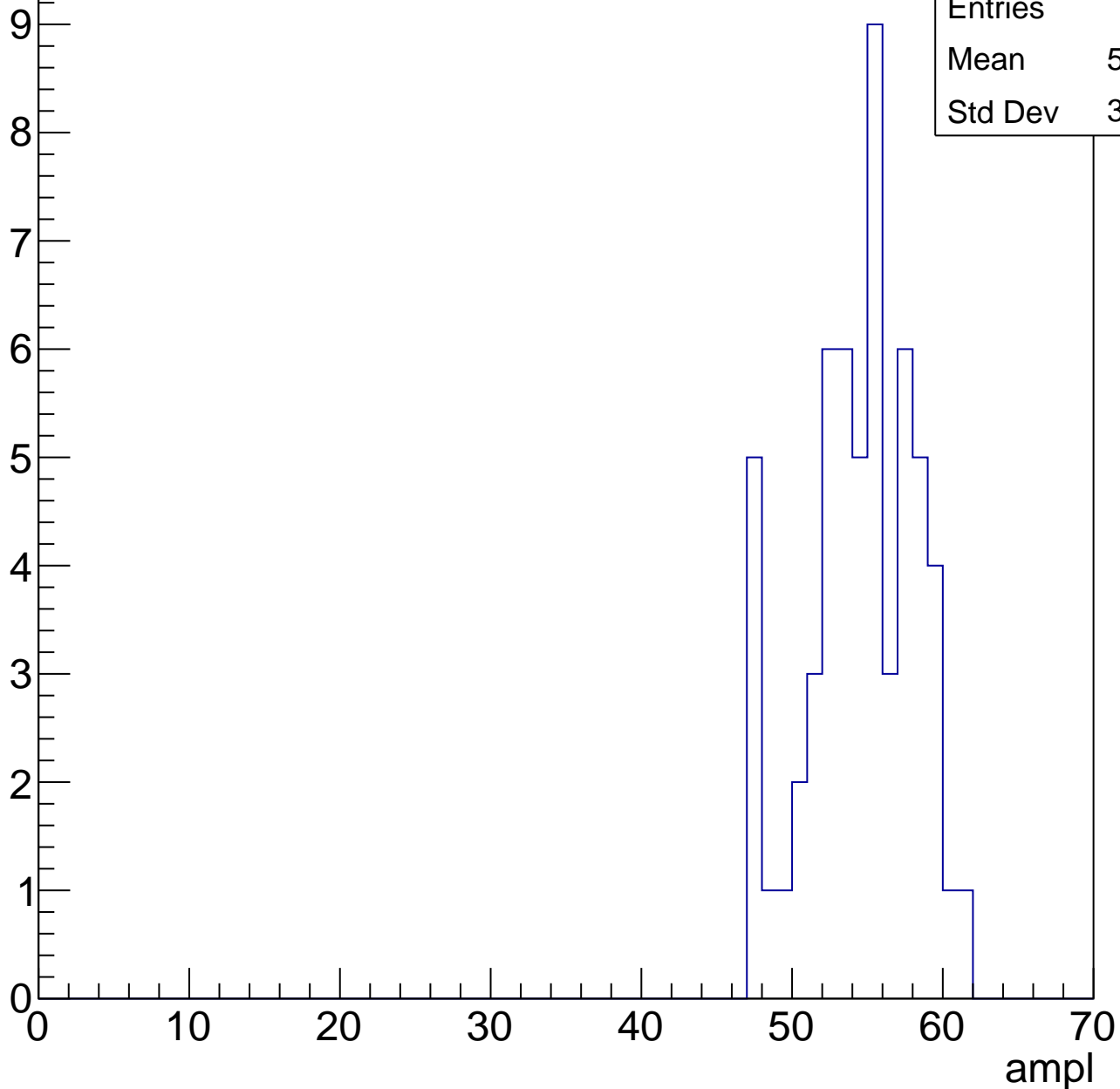
ampl



B1L102S, U14-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 57.59

Std Dev 9.057

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

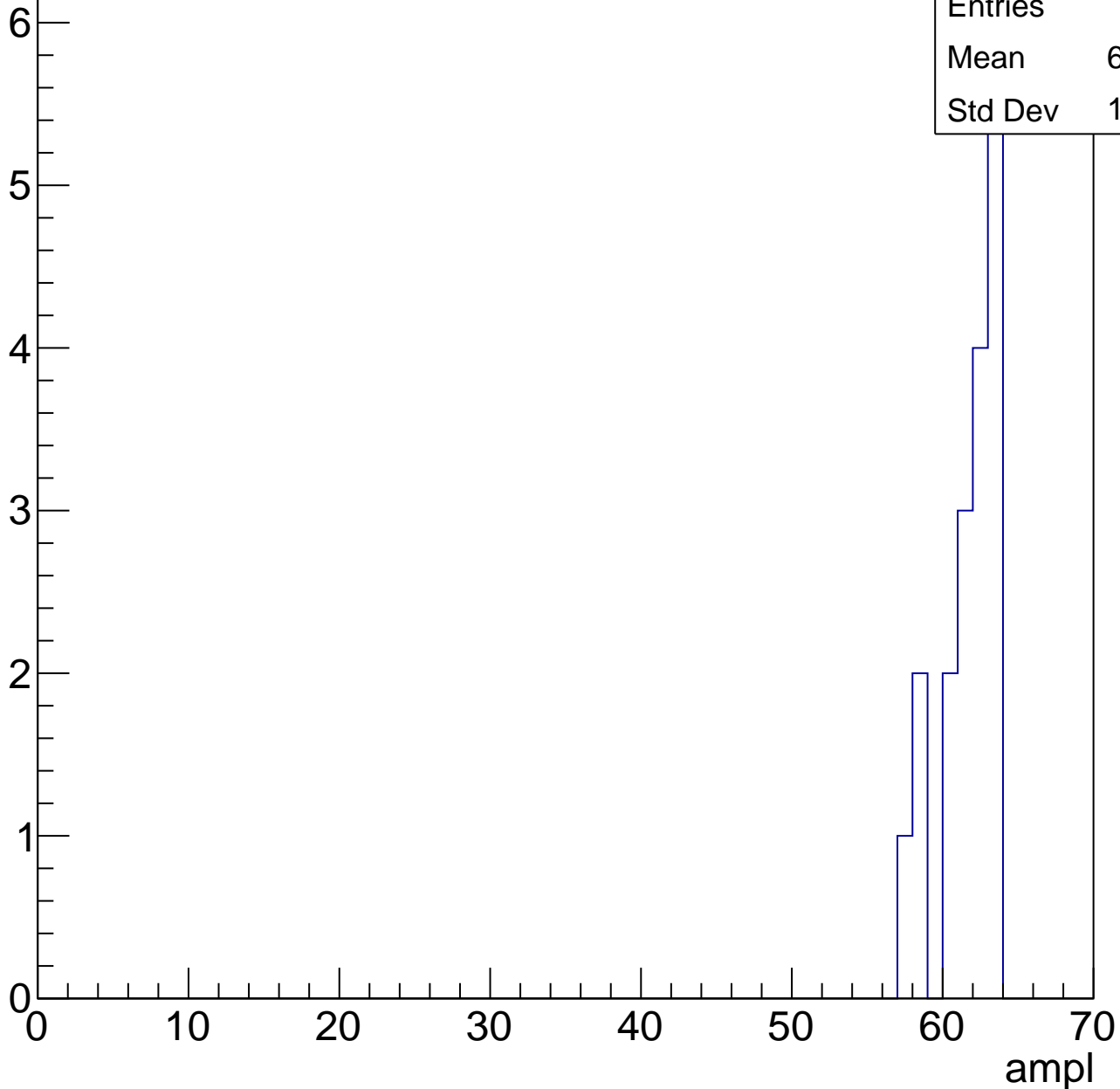
7

B1L102S, U14-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

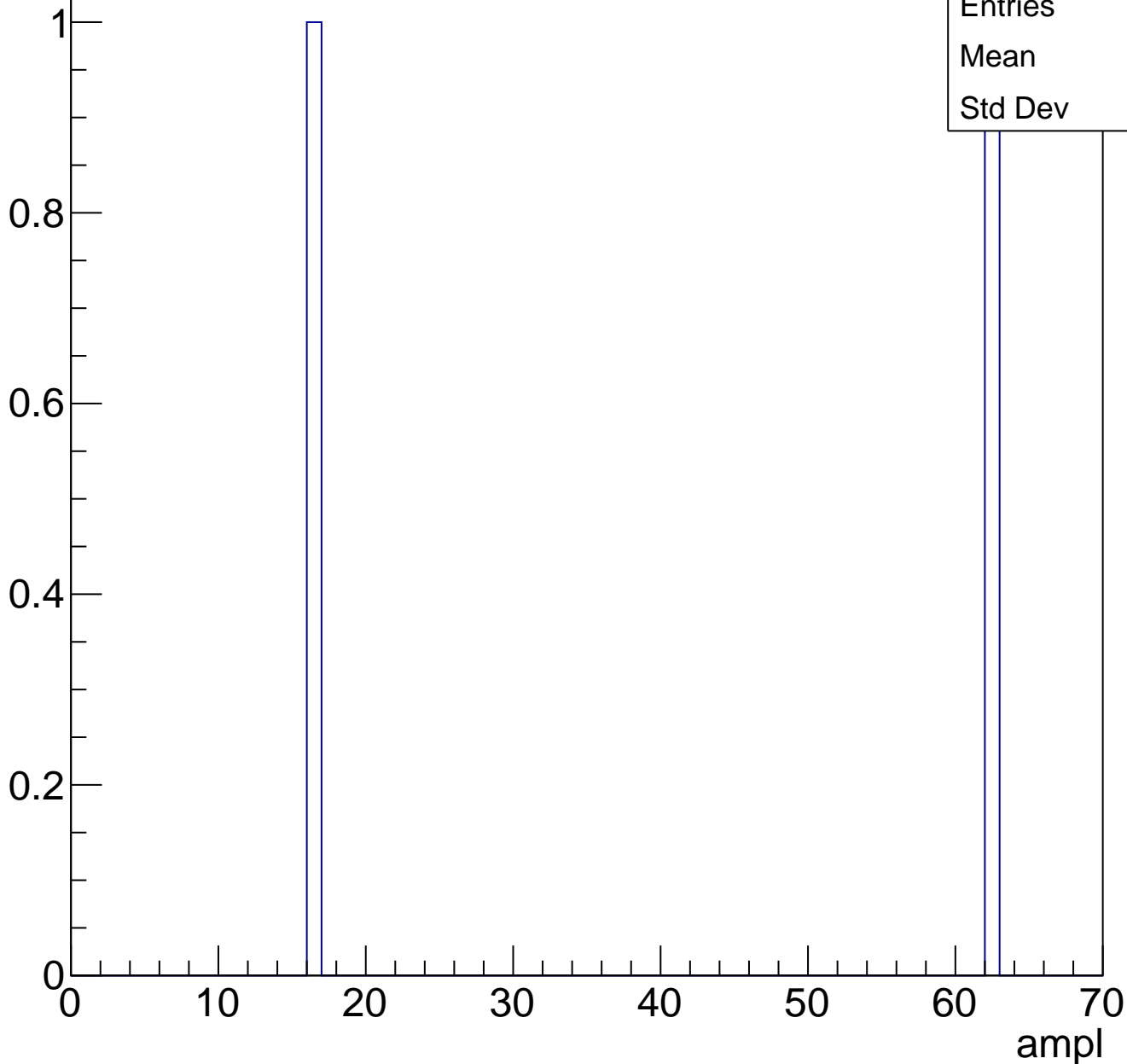
Entries	18
Mean	61.22
Std Dev	1.872



B1L102S, U14-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch39, adc0

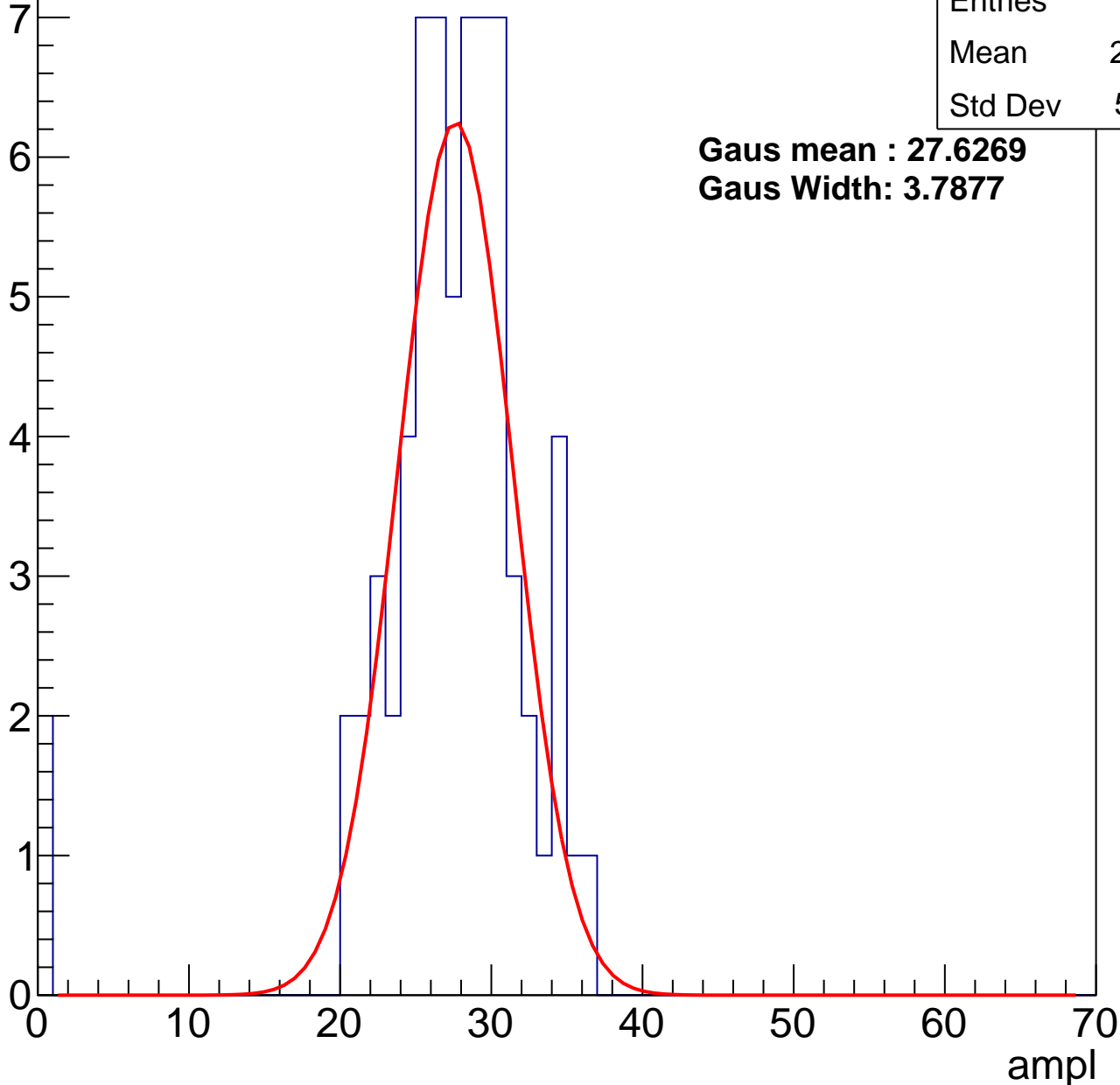
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	26.69
Std Dev	5.951

Gaus mean : 27.6269

Gaus Width: 3.7877



B1L102S, U14-ch39, adc1

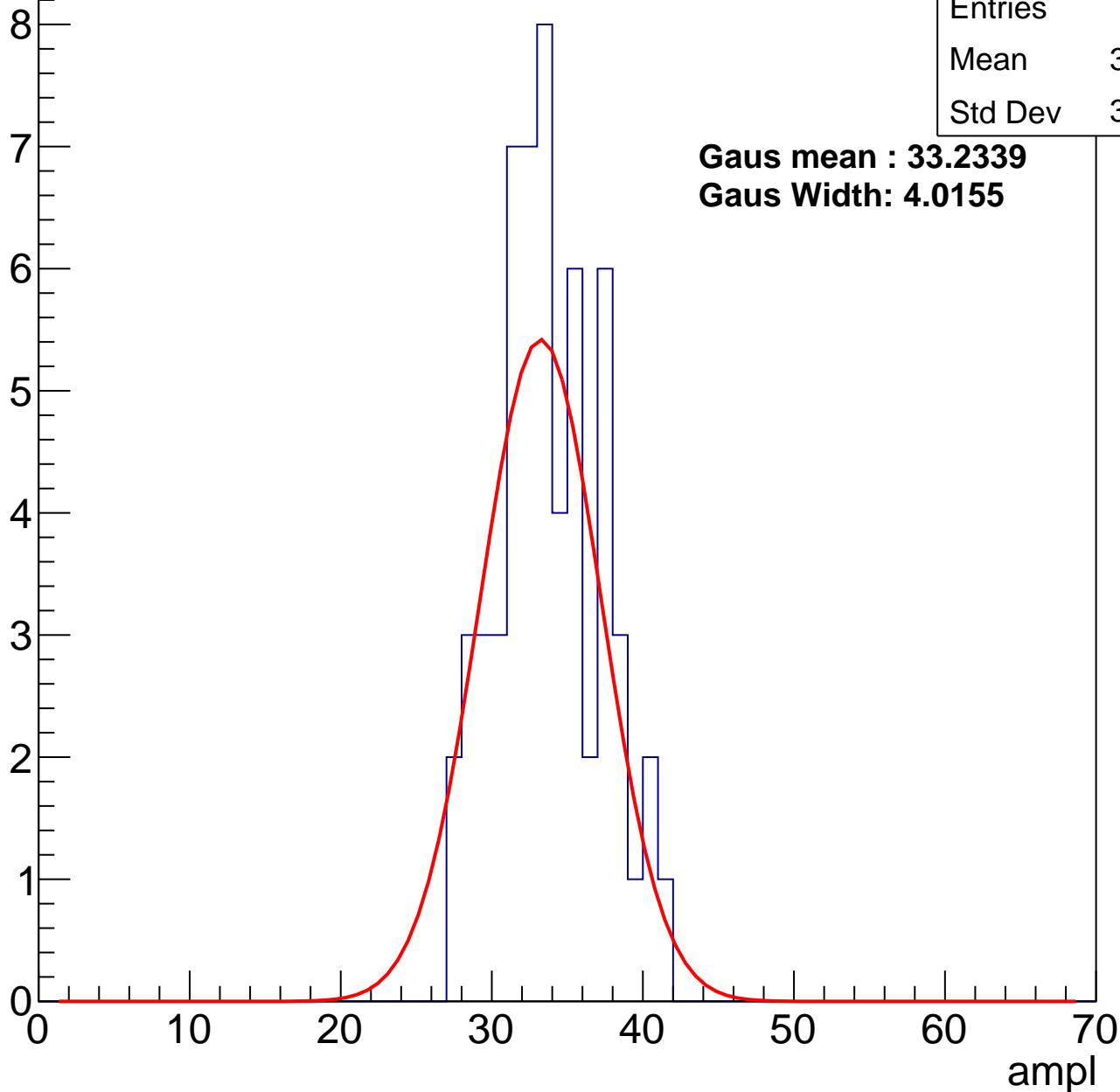
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	33.34
Std Dev	3.386

Gaus mean : 33.2339

Gaus Width: 4.0155



B1L102S, U14-ch39, adc2

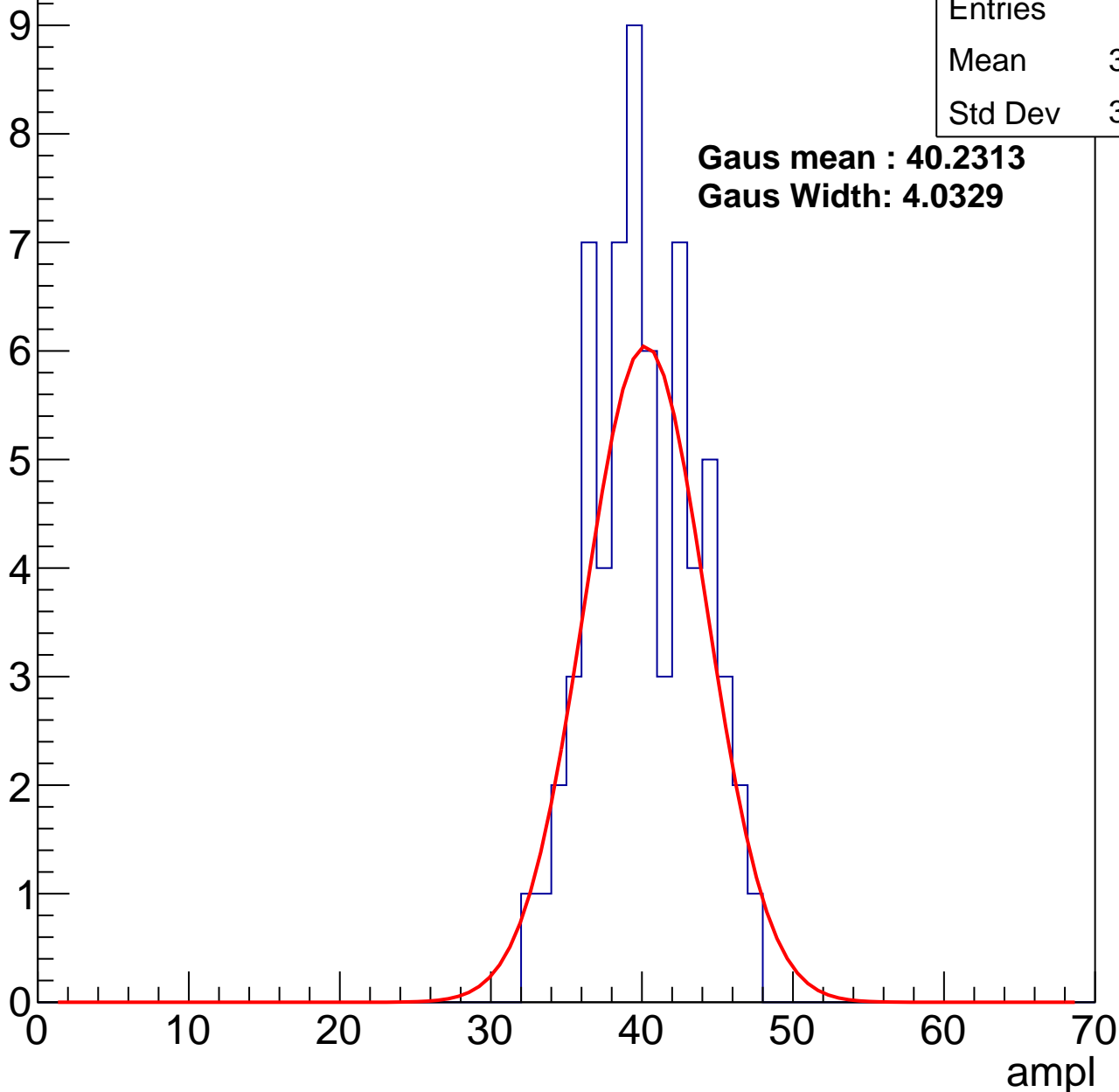
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	39.66
Std Dev	3.465

Gaus mean : 40.2313

Gaus Width: 4.0329

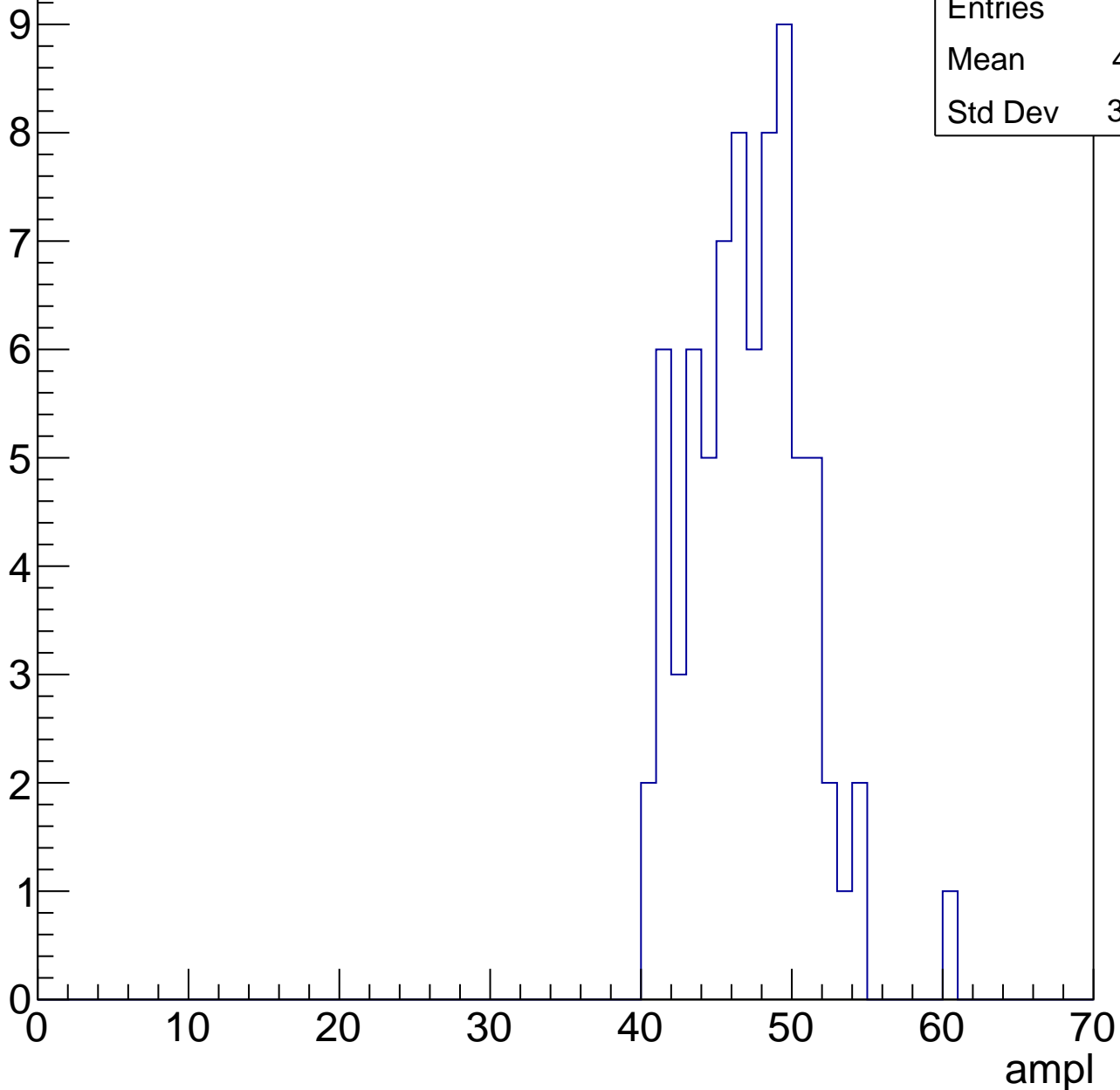


B1L102S, U14-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	46.71
Std Dev	3.773

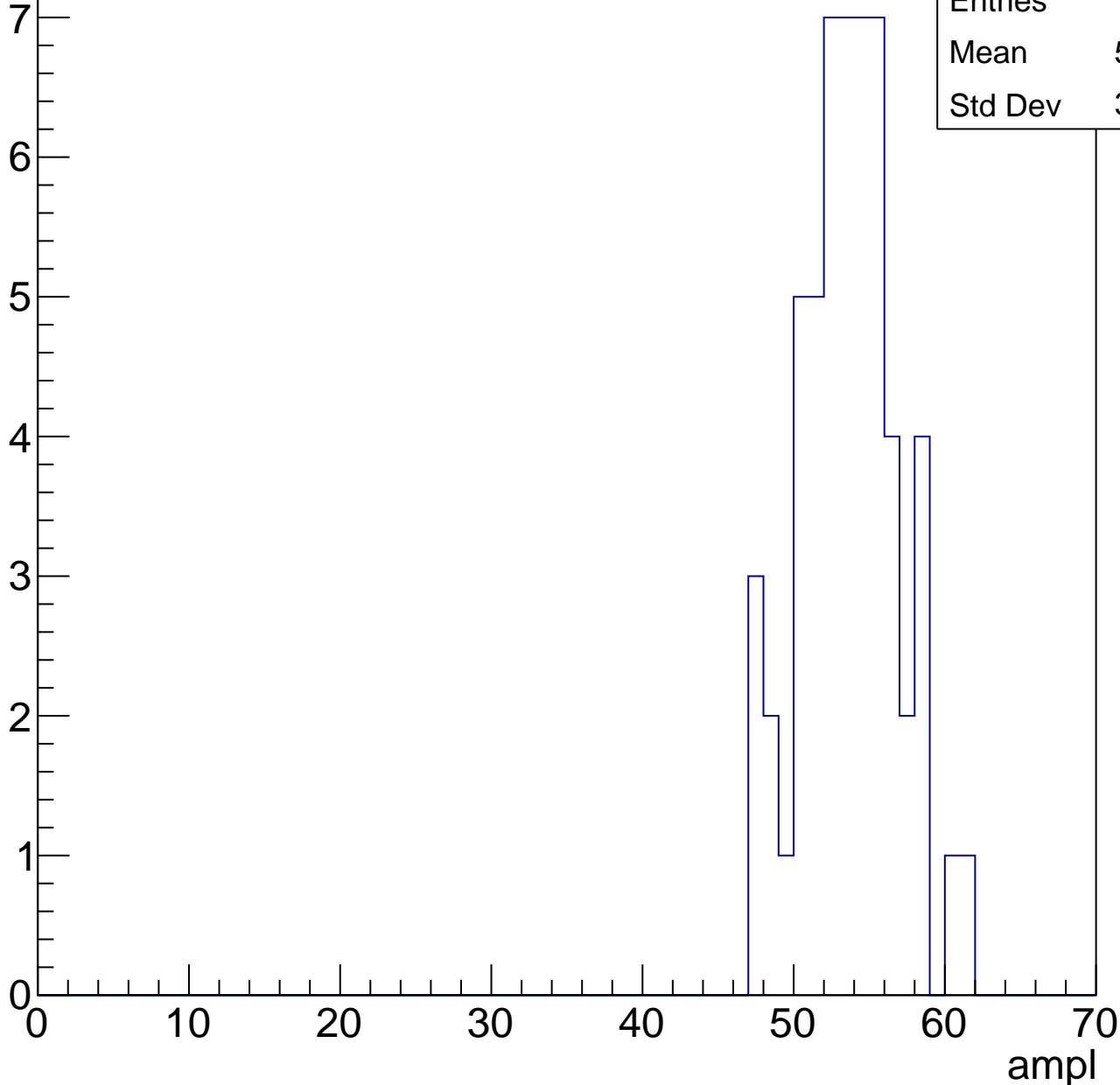


B1L102S, U14-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.21
Std Dev	3.161

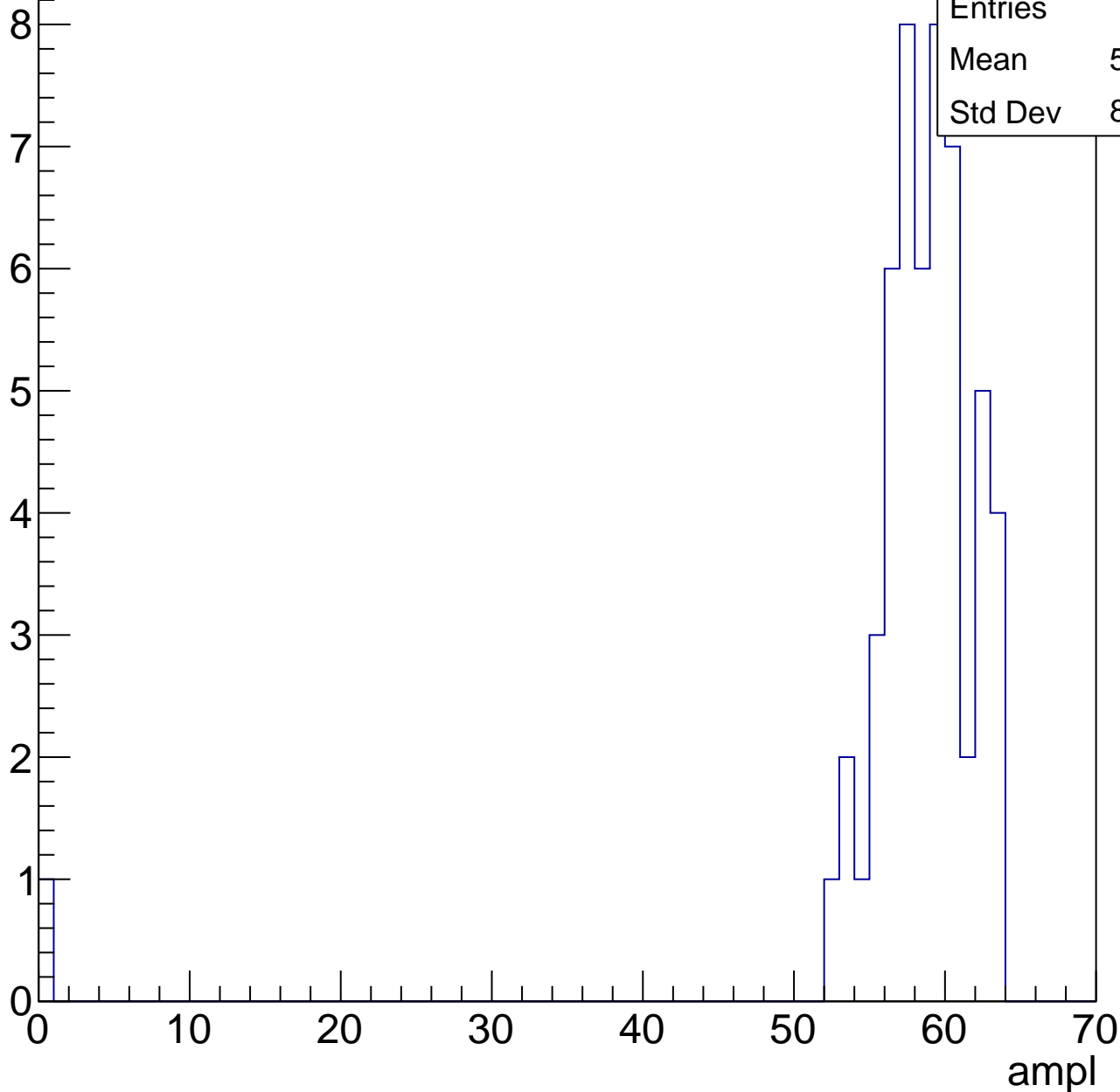


B1L102S, U14-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.28
Std Dev	8.312

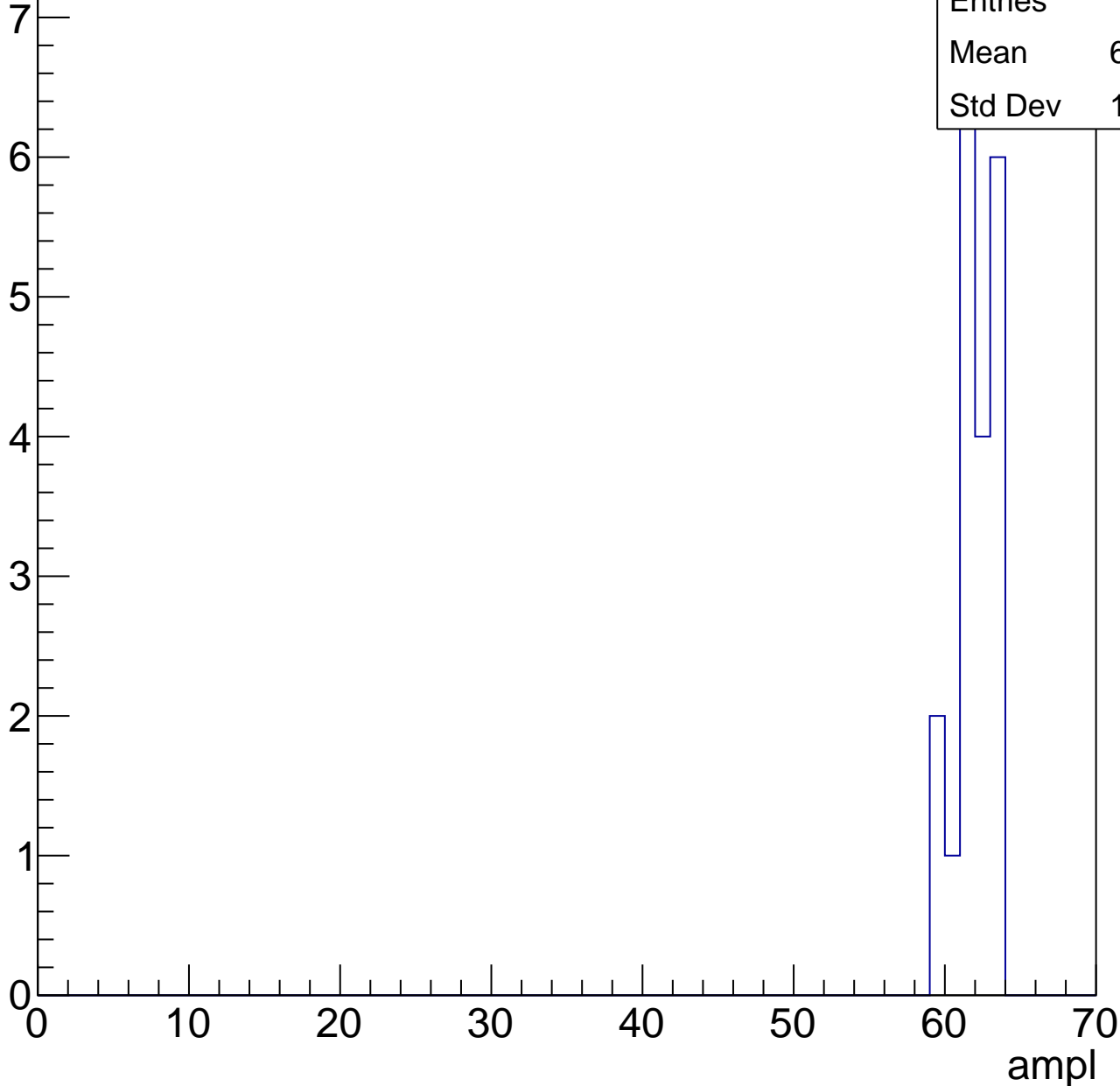


B1L102S, U14-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	61.55
Std Dev	1.244



B1L102S, U14-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch40, adc0

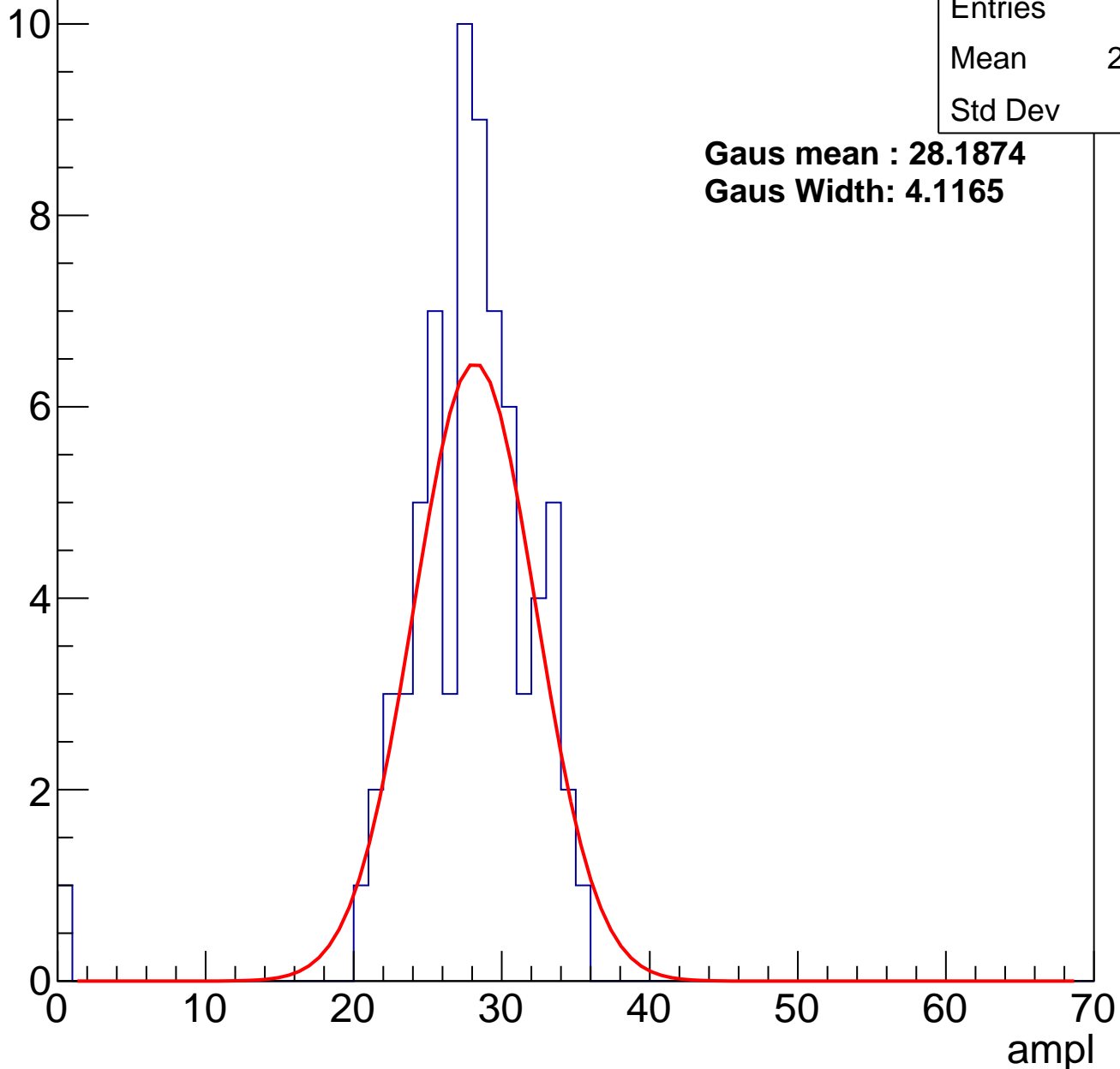
calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	27.28
Std Dev	4.75

Gaus mean : 28.1874

Gaus Width: 4.1165

Entry



B1L102S, U14-ch40, adc1

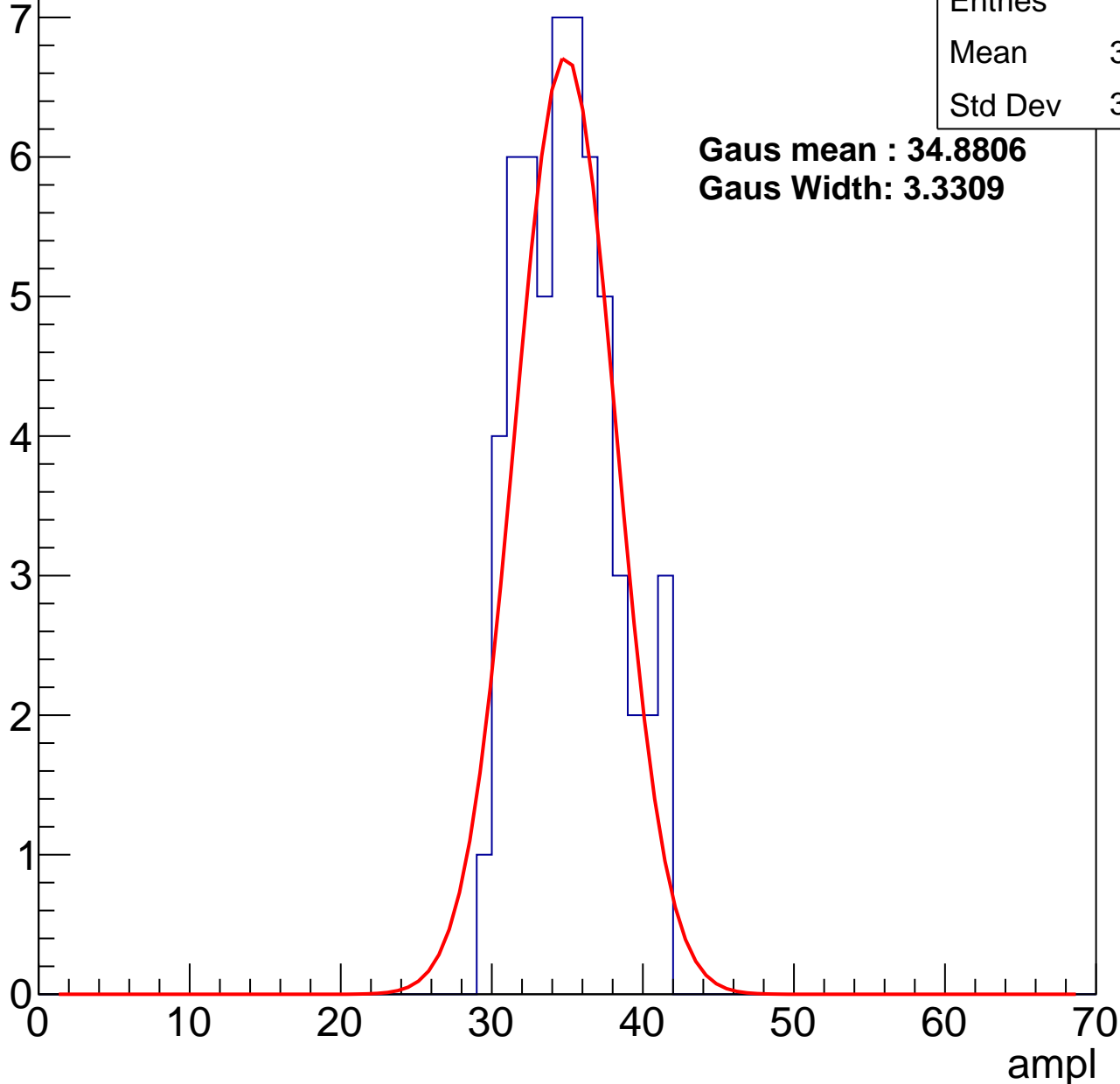
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.58
Std Dev	3.095

Gaus mean : 34.8806

Gaus Width: 3.3309



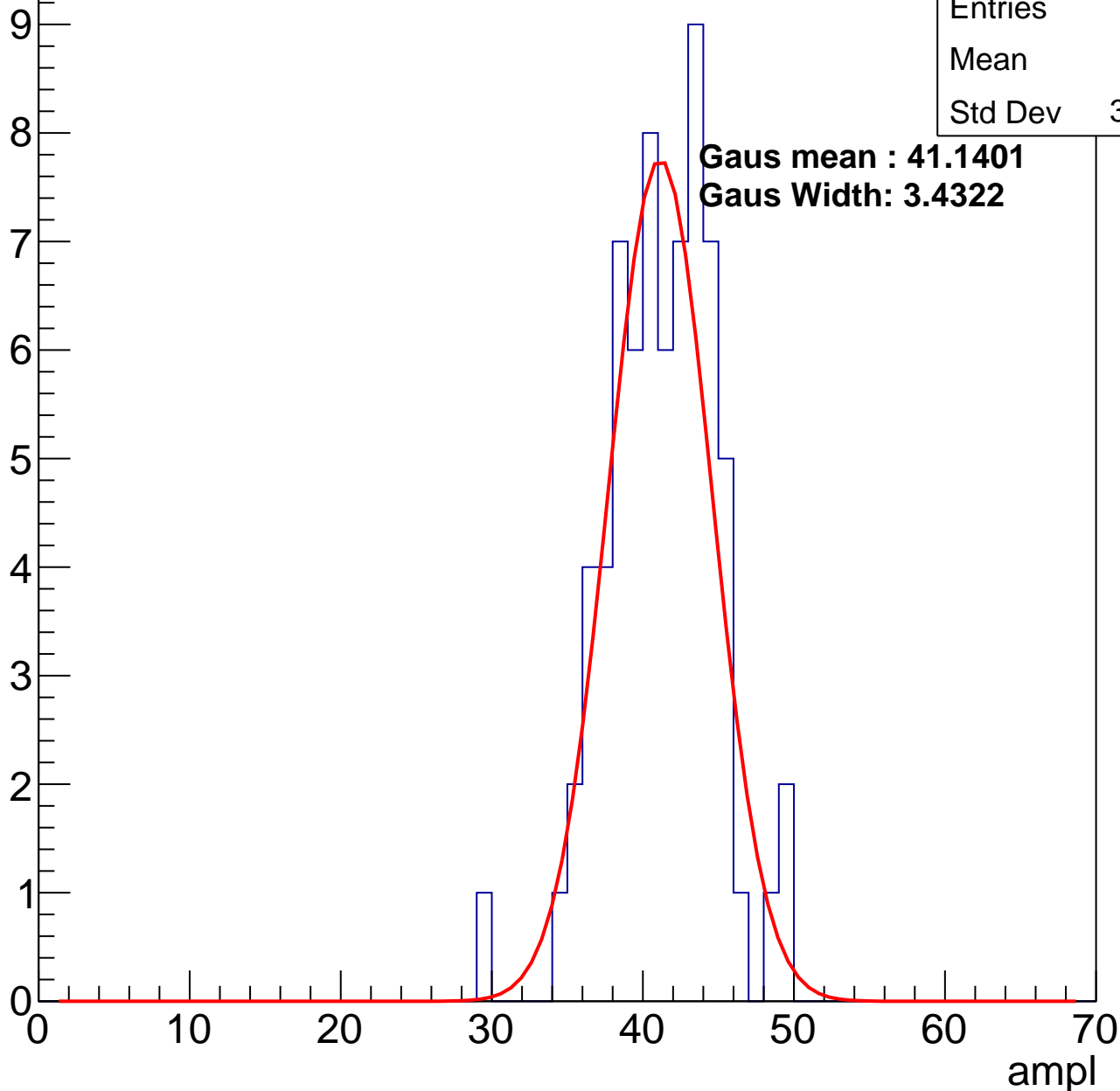
B1L102S, U14-ch40, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	40.8
Std Dev	3.579

Gaus mean : 41.1401
Gaus Width: 3.4322

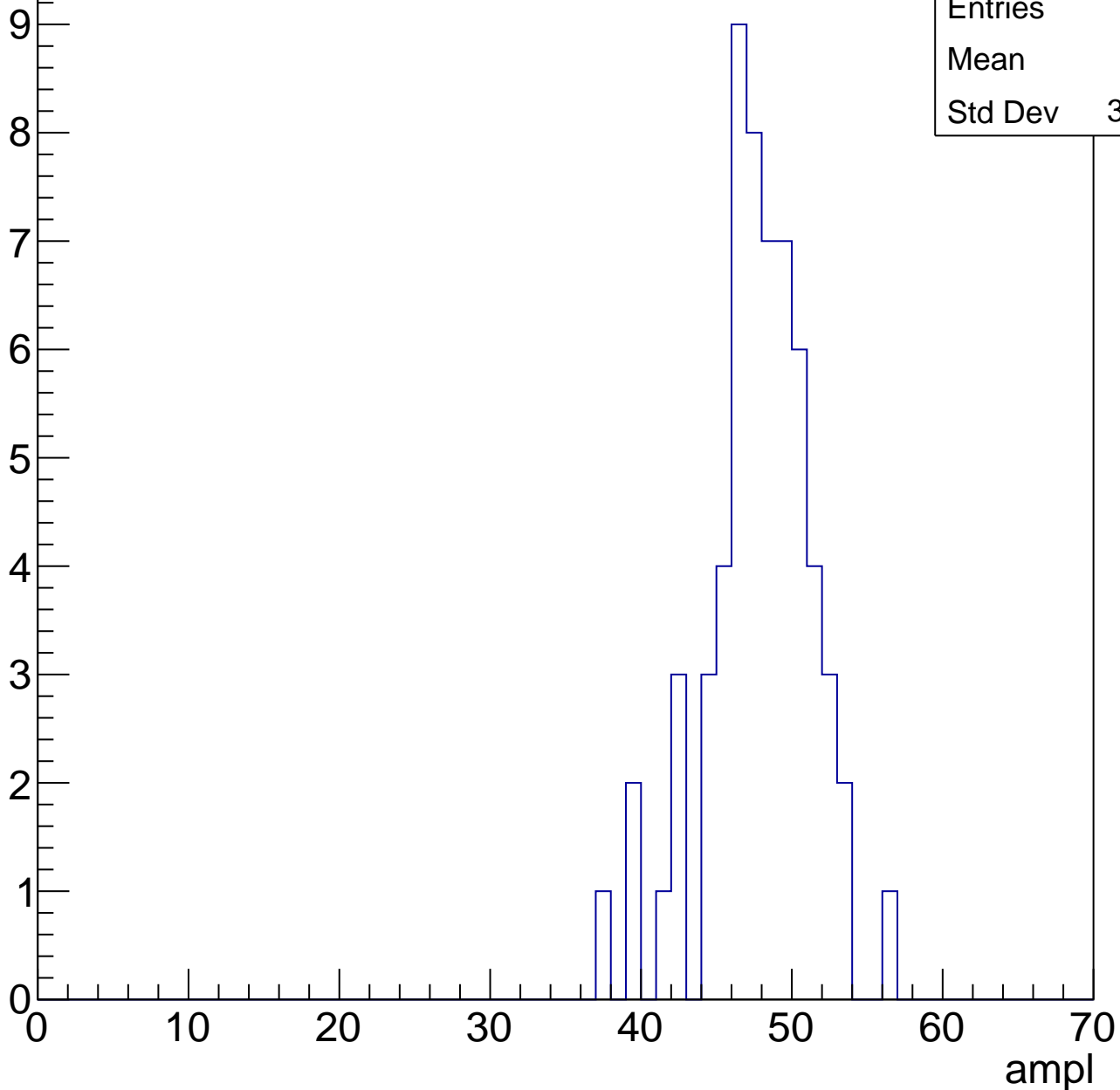


B1L102S, U14-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.3
Std Dev	3.545

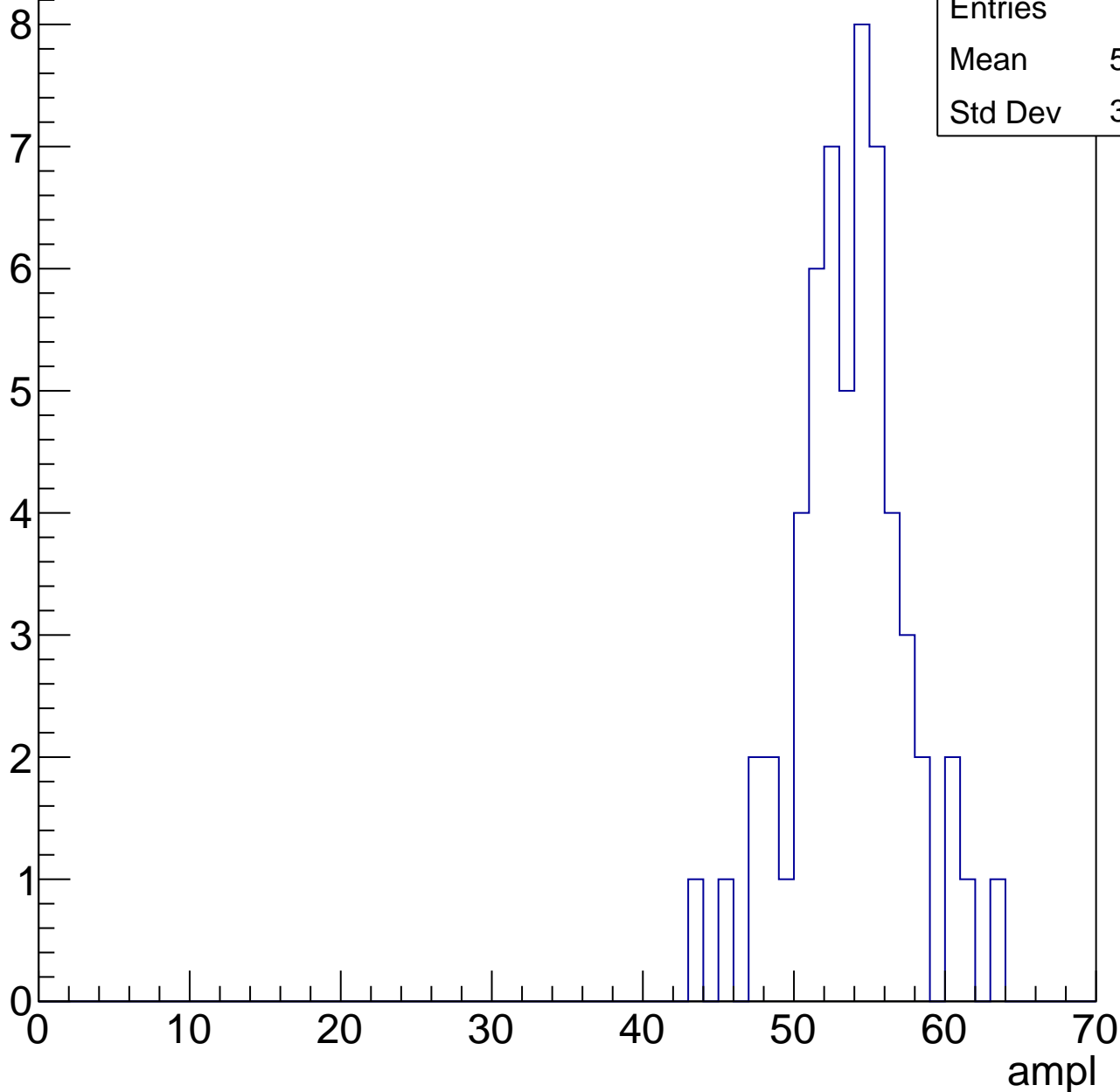


B1L102S, U14-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.23
Std Dev	3.742

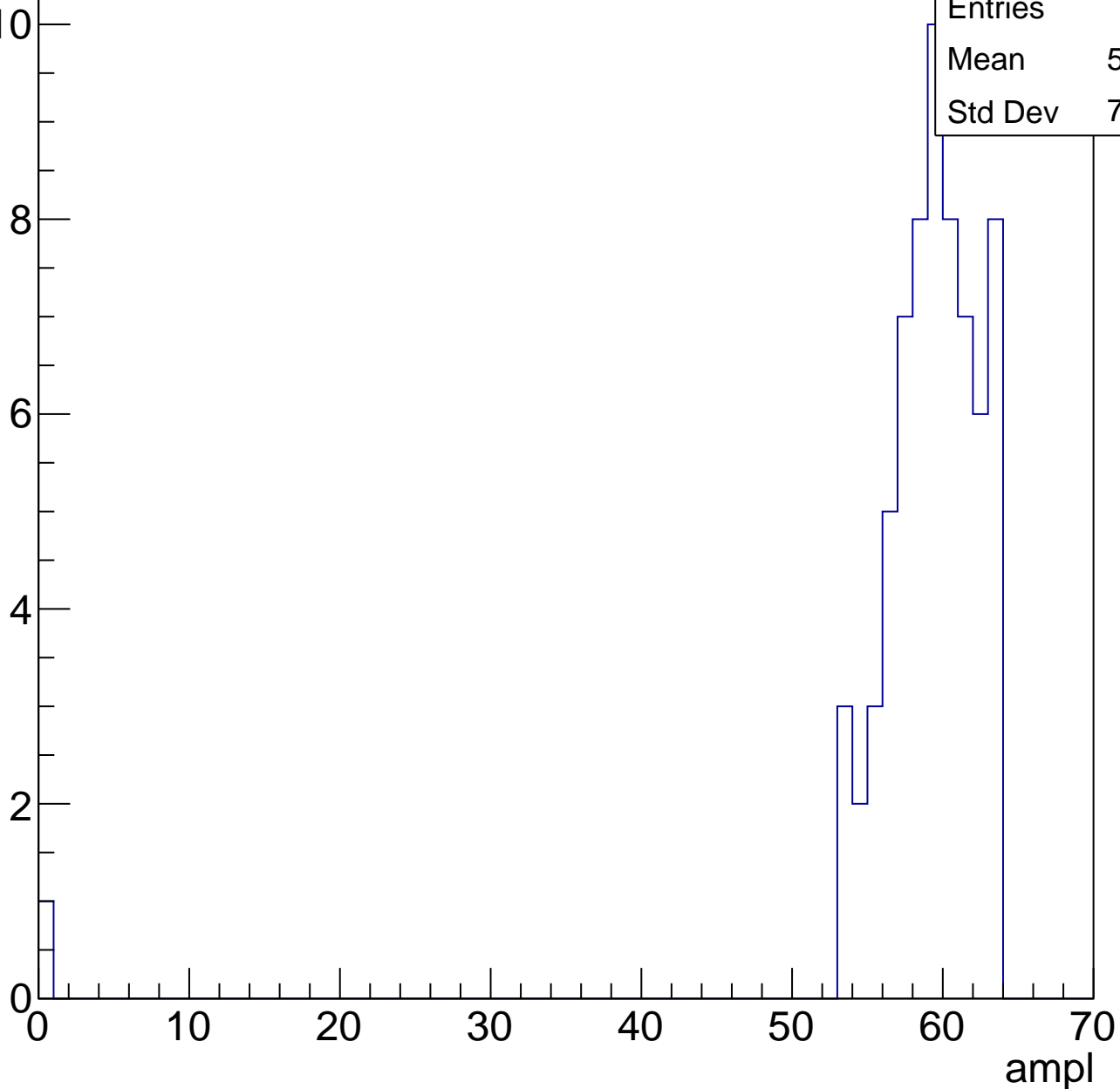


B1L102S, U14-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

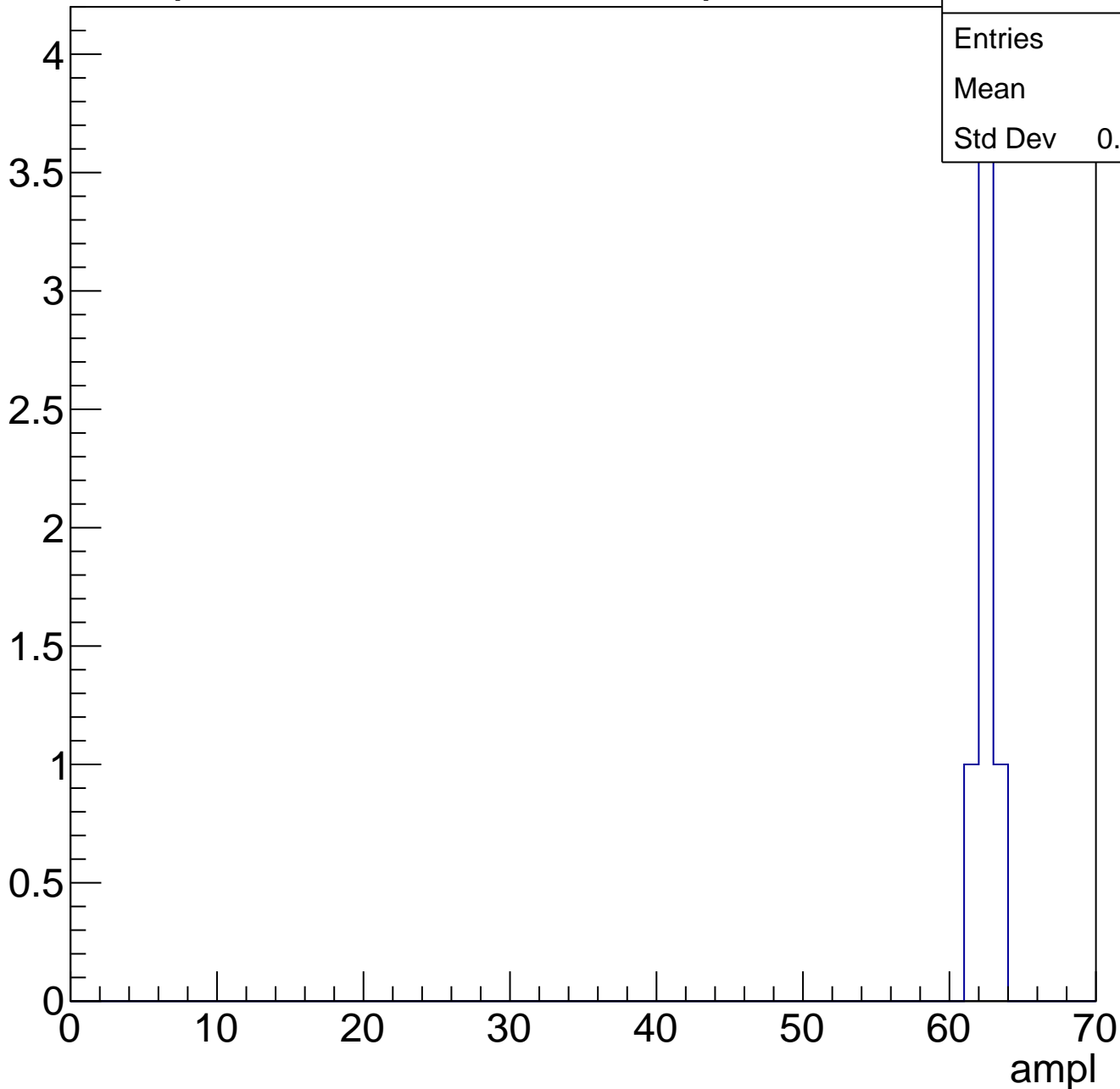
Entries	68
Mean	58.06
Std Dev	7.598



B1L102S, U14-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

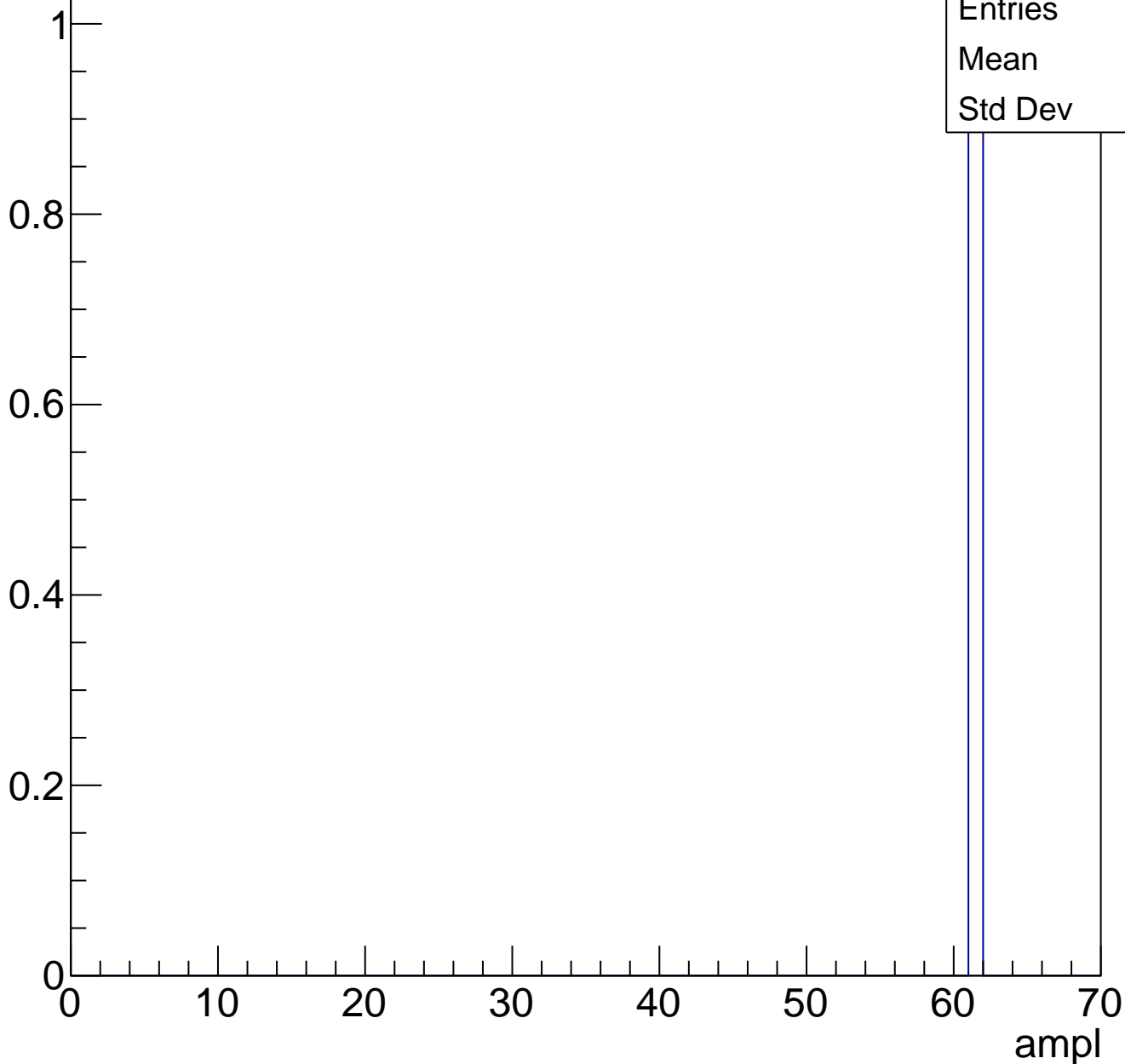
Entry



B1L102S, U14-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch41, adc0

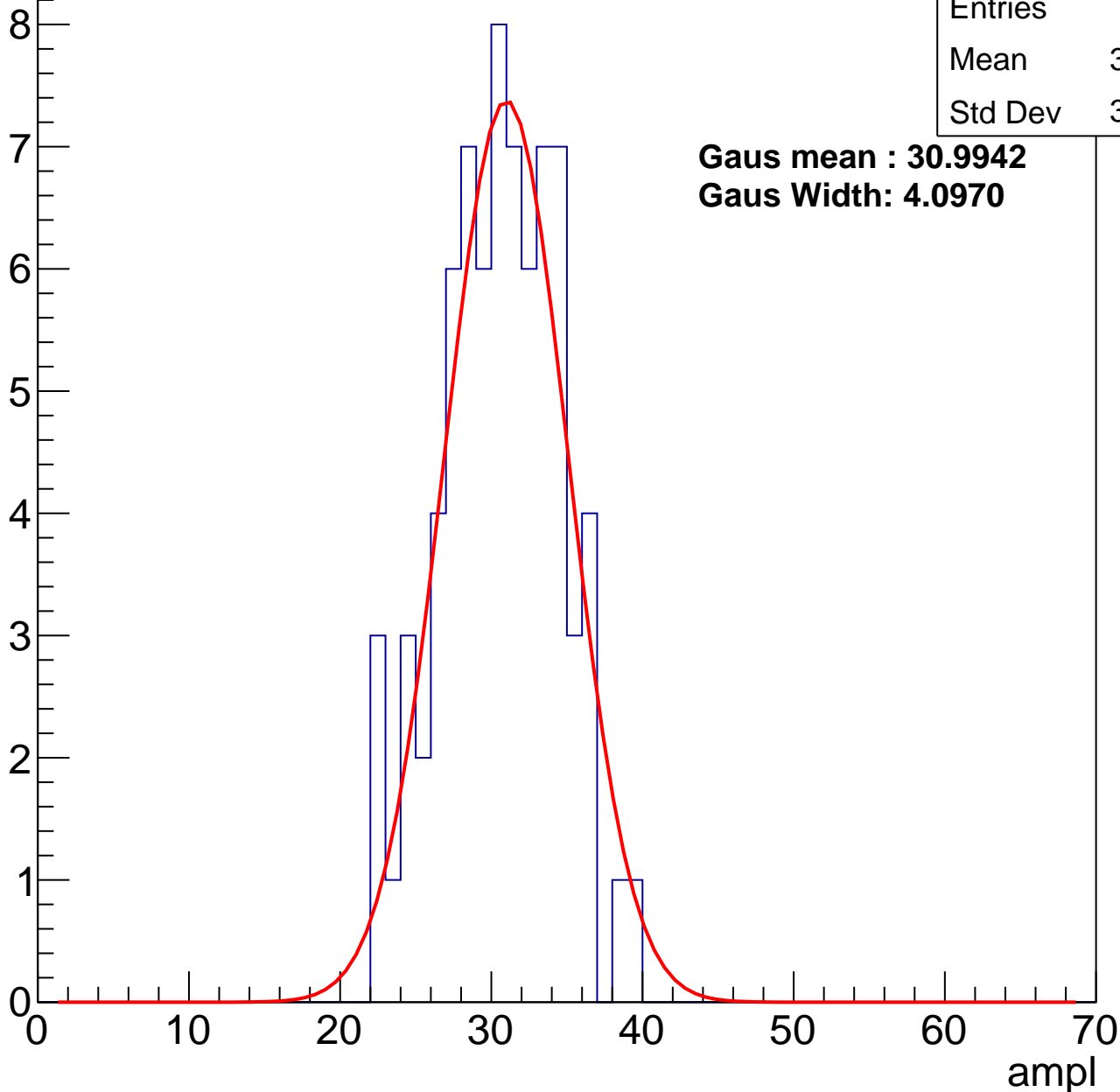
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	30.14
Std Dev	3.838

Gaus mean : 30.9942

Gaus Width: 4.0970



B1L102S, U14-ch41, adc1

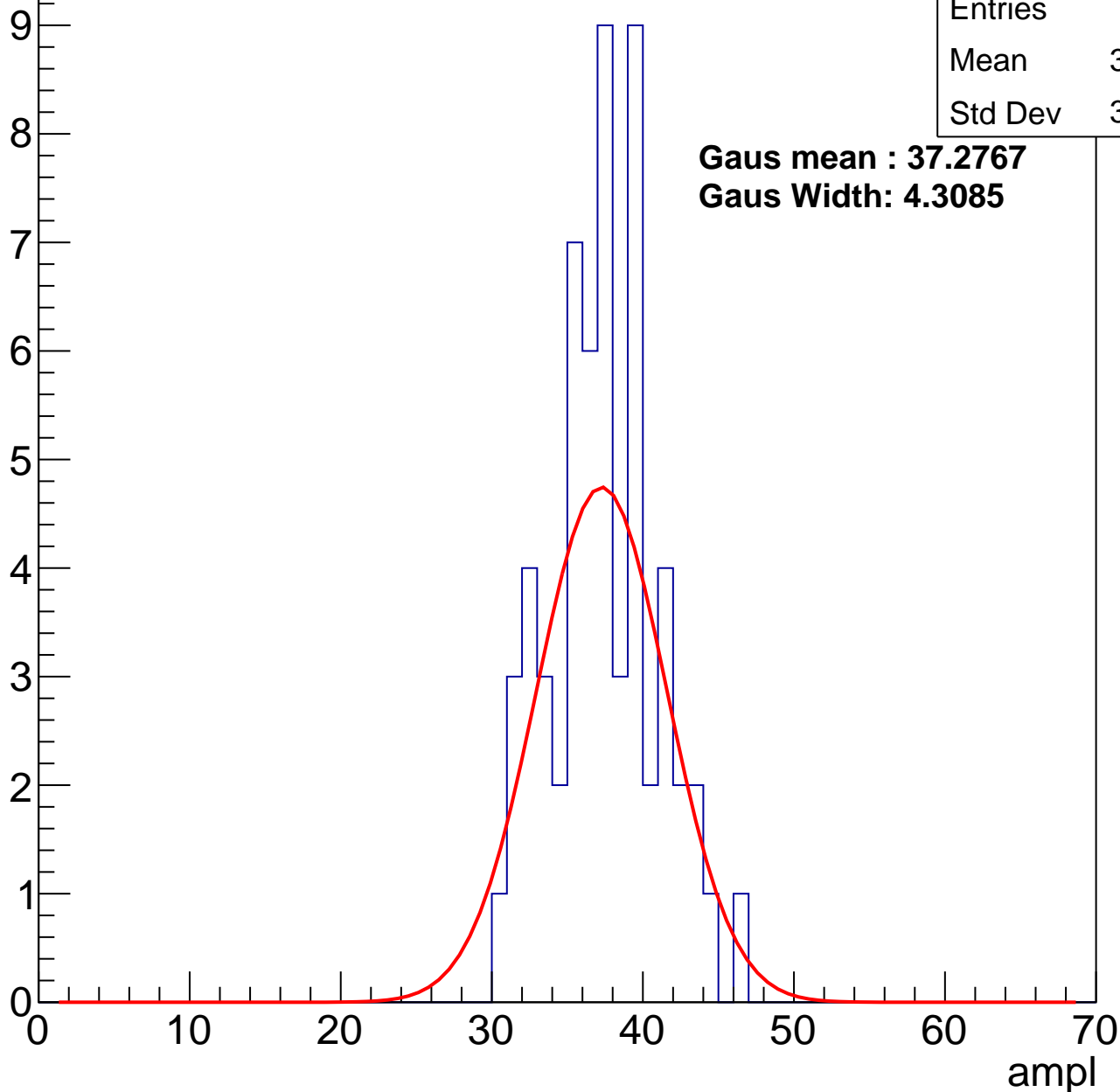
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.97
Std Dev	3.517

Gaus mean : 37.2767

Gaus Width: 4.3085



B1L102S, U14-ch41, adc2

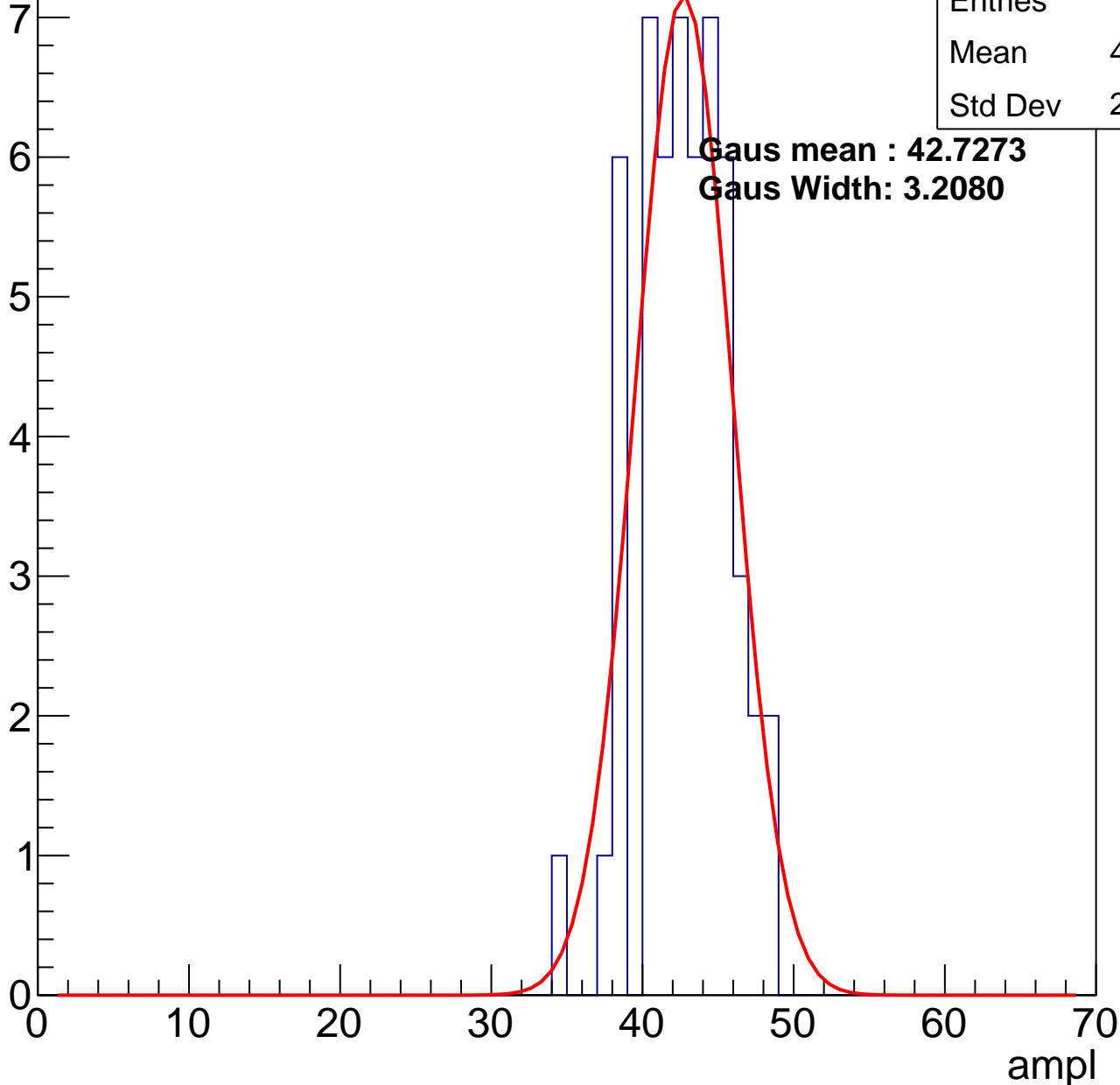
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42.28
Std Dev	2.959

Gaus mean : 42.7273

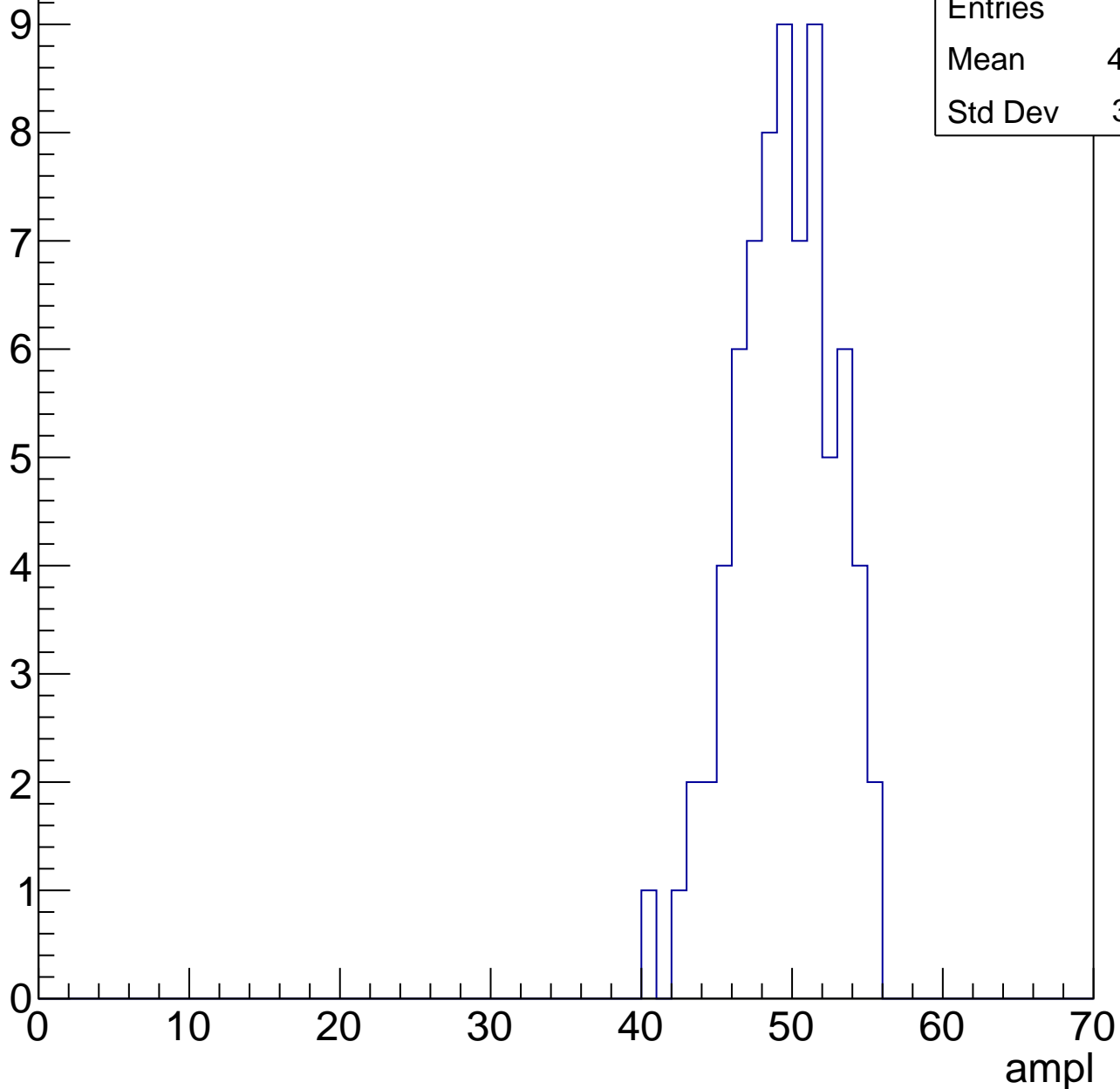
Gaus Width: 3.2080



B1L102S, U14-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



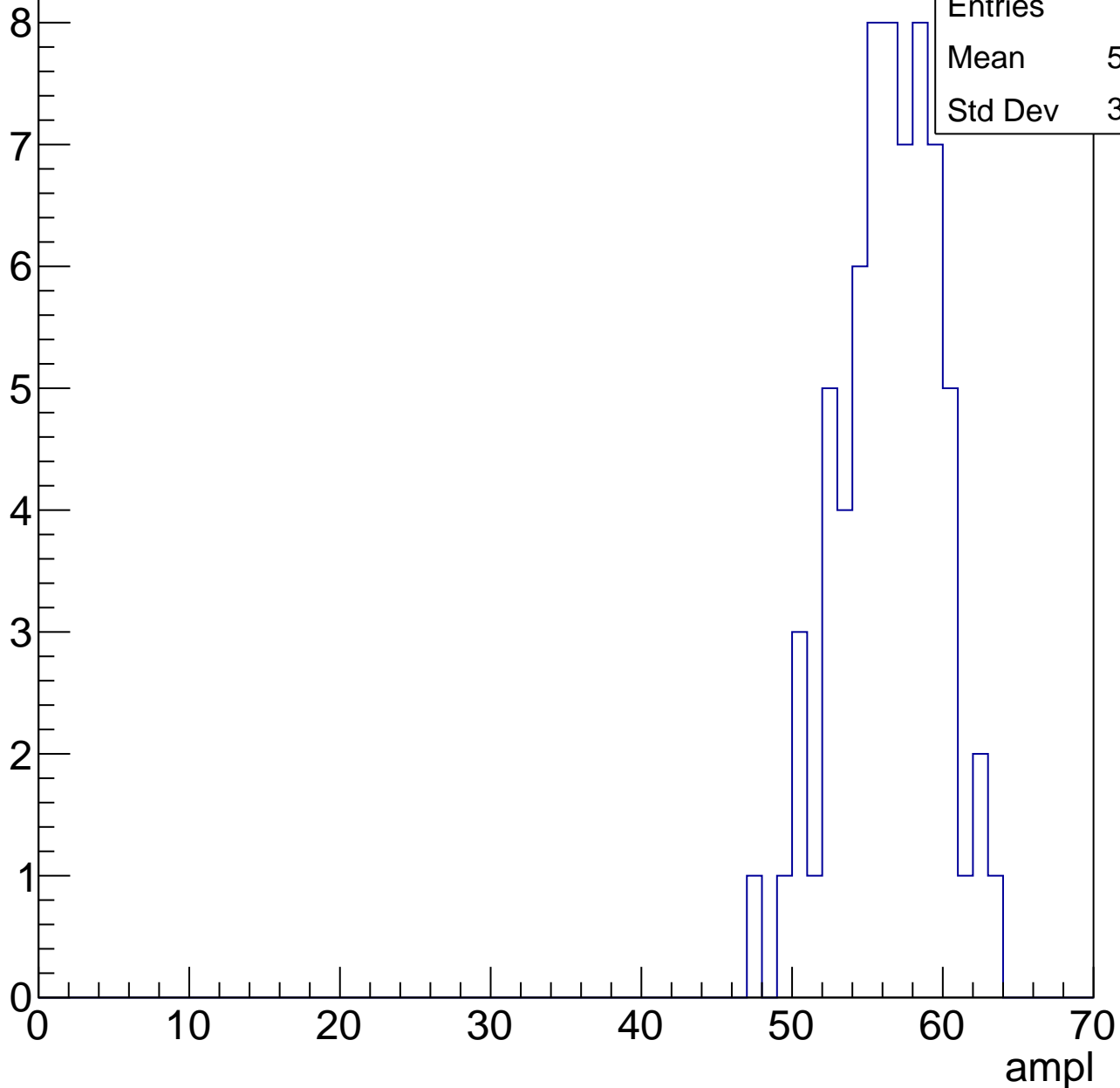
Entries	73
Mean	49.03
Std Dev	3.231

B1L102S, U14-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	55.96
Std Dev	3.296



B1L102S, U14-ch41, adc5

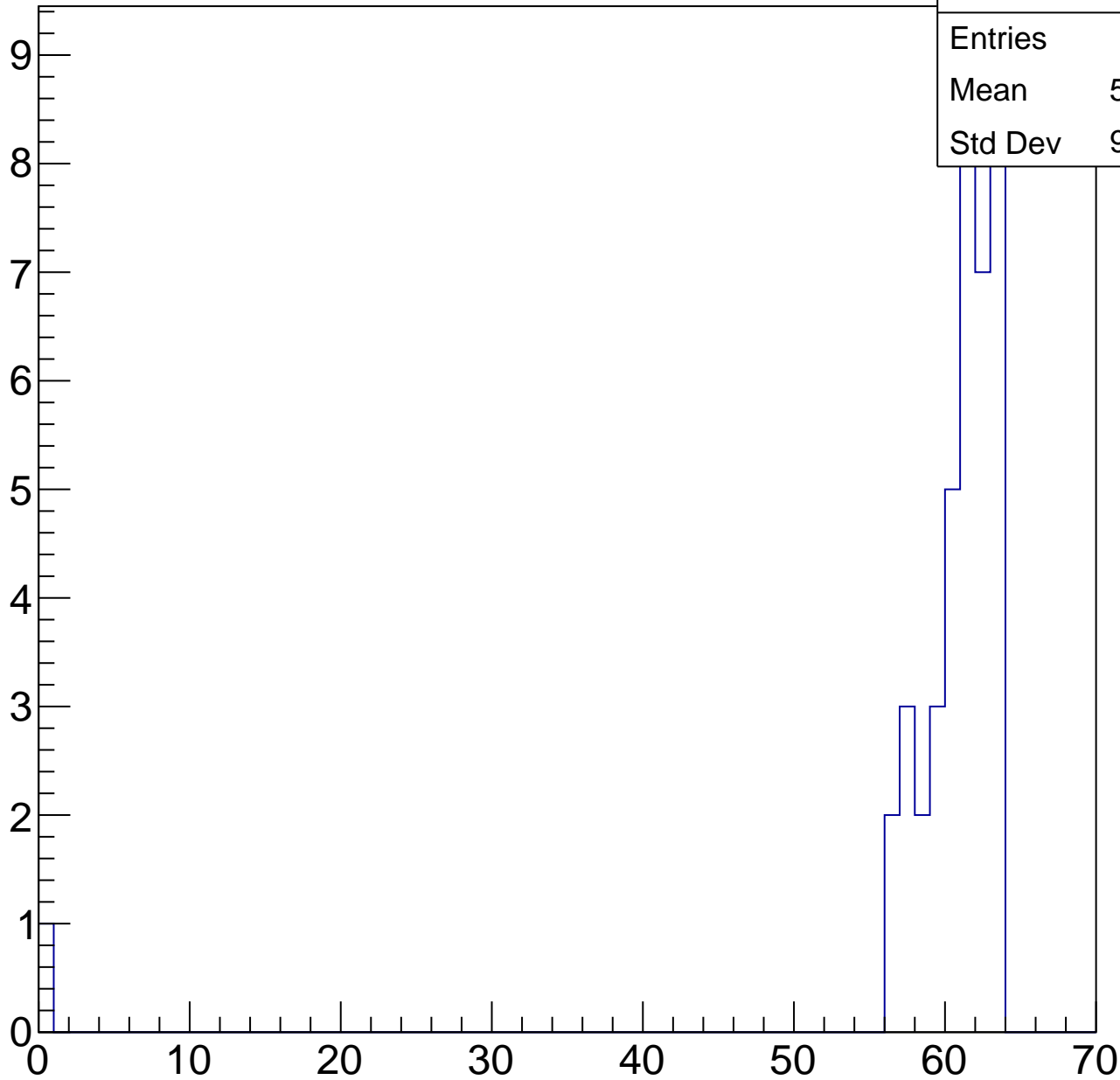
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.08
Std Dev	9.676

ampl



B1L102S, U14-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch42, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	26.48
Std Dev	5.451

Gaus mean : 27.0527

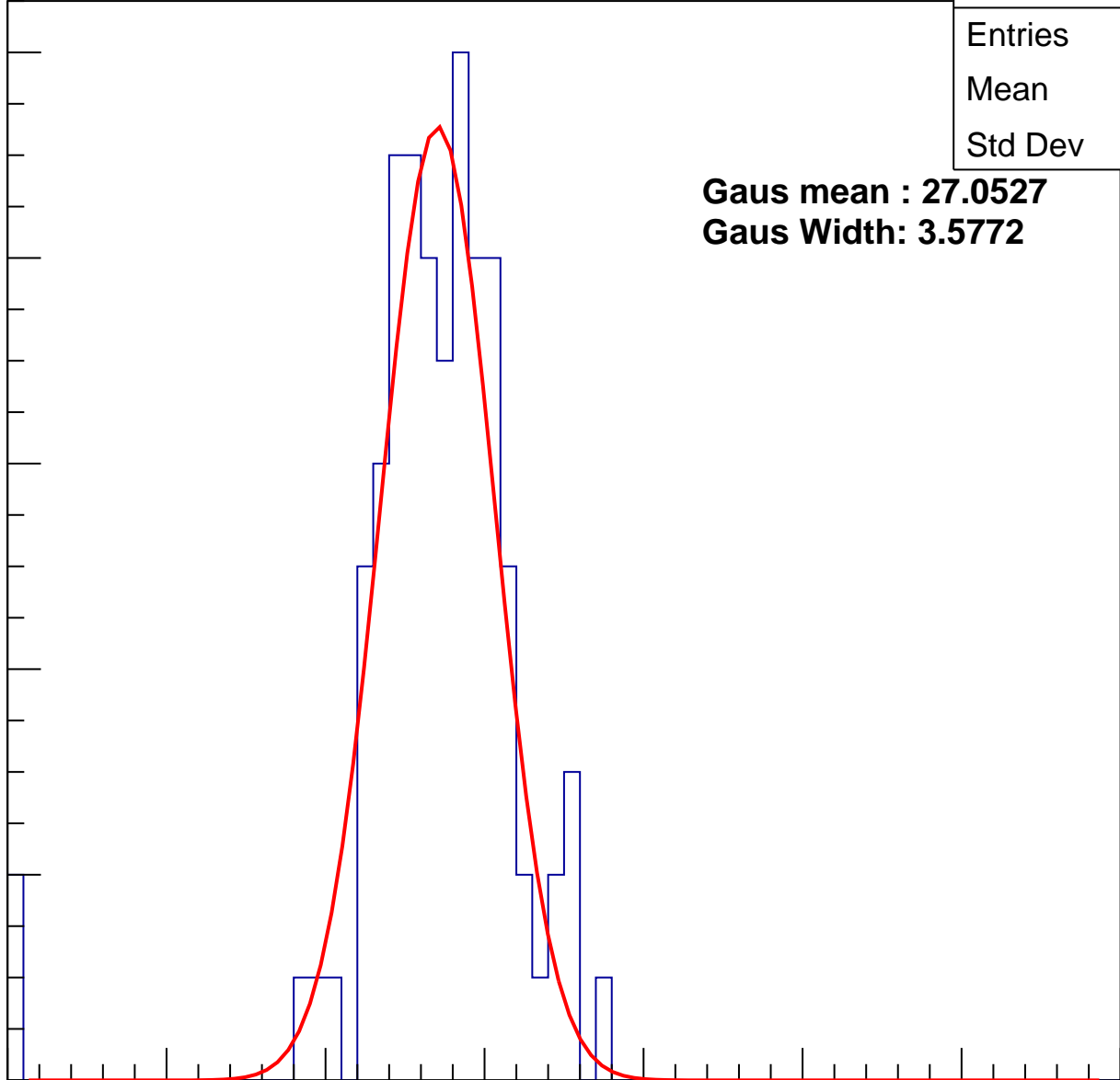
Gaus Width: 3.5772

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch42, adc1

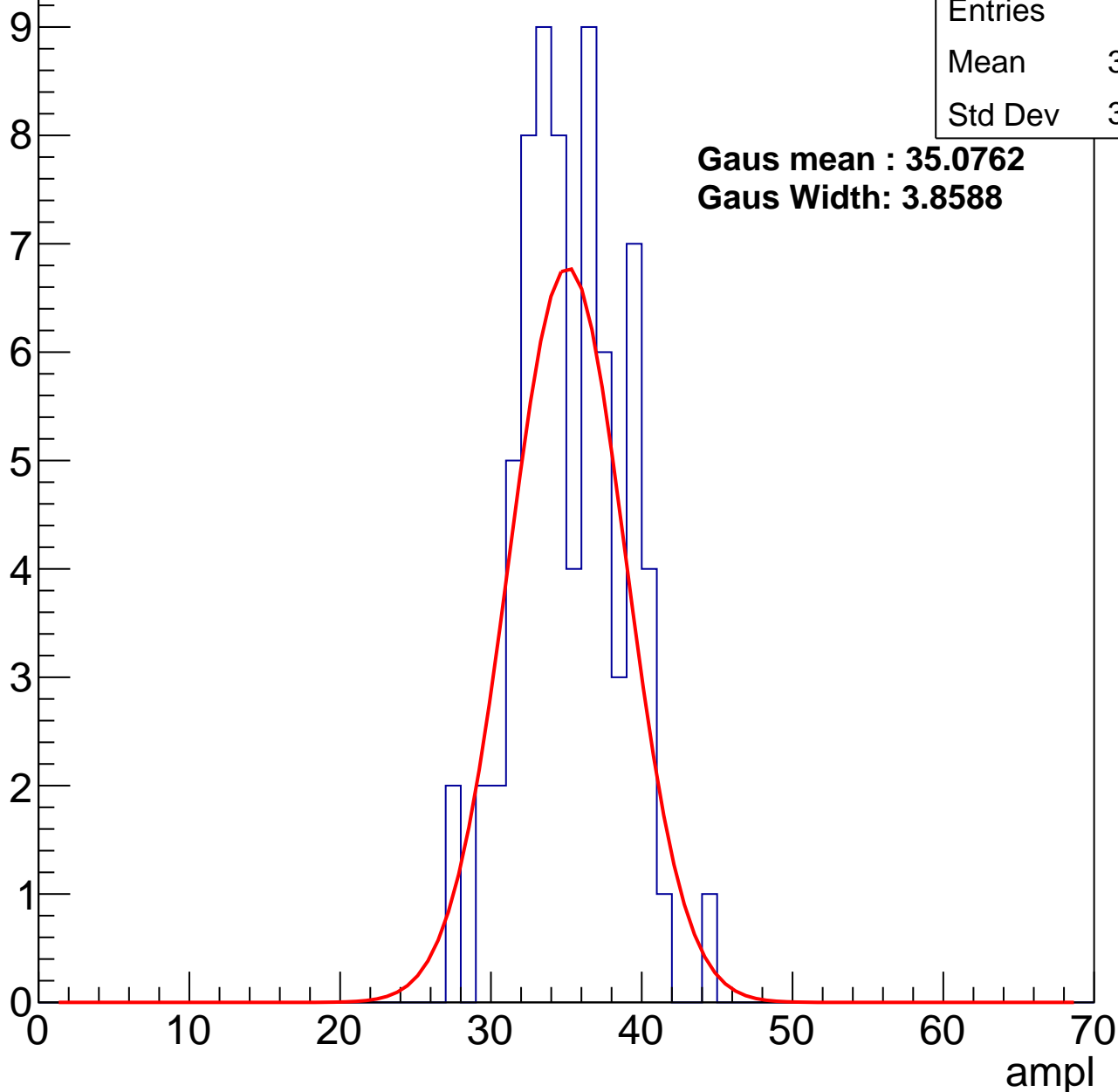
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.79
Std Dev	3.423

Gaus mean : 35.0762

Gaus Width: 3.8588



B1L102S, U14-ch42, adc2

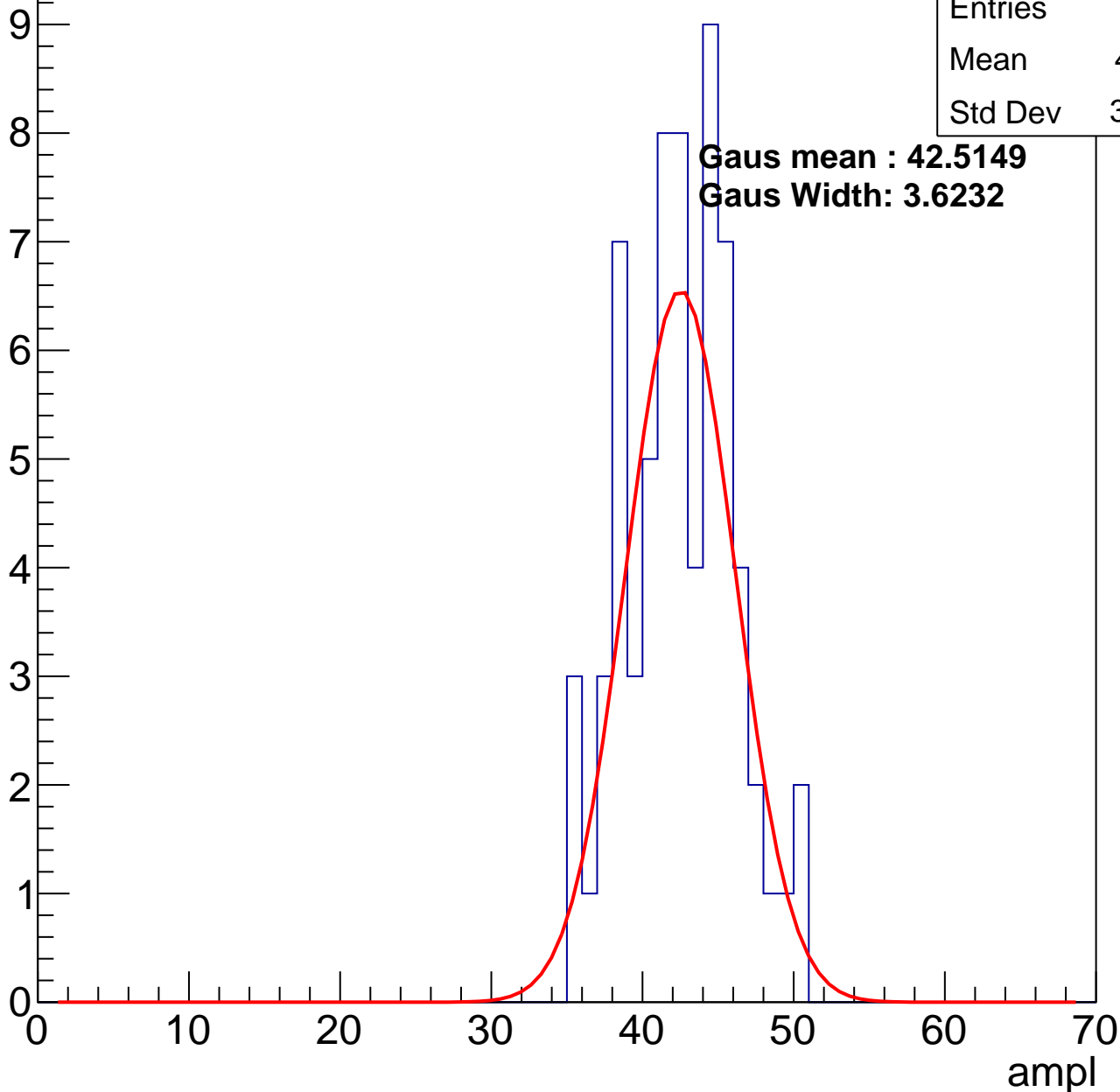
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.01
Std Dev	3.546

Gaus mean : 42.5149

Gaus Width: 3.6232

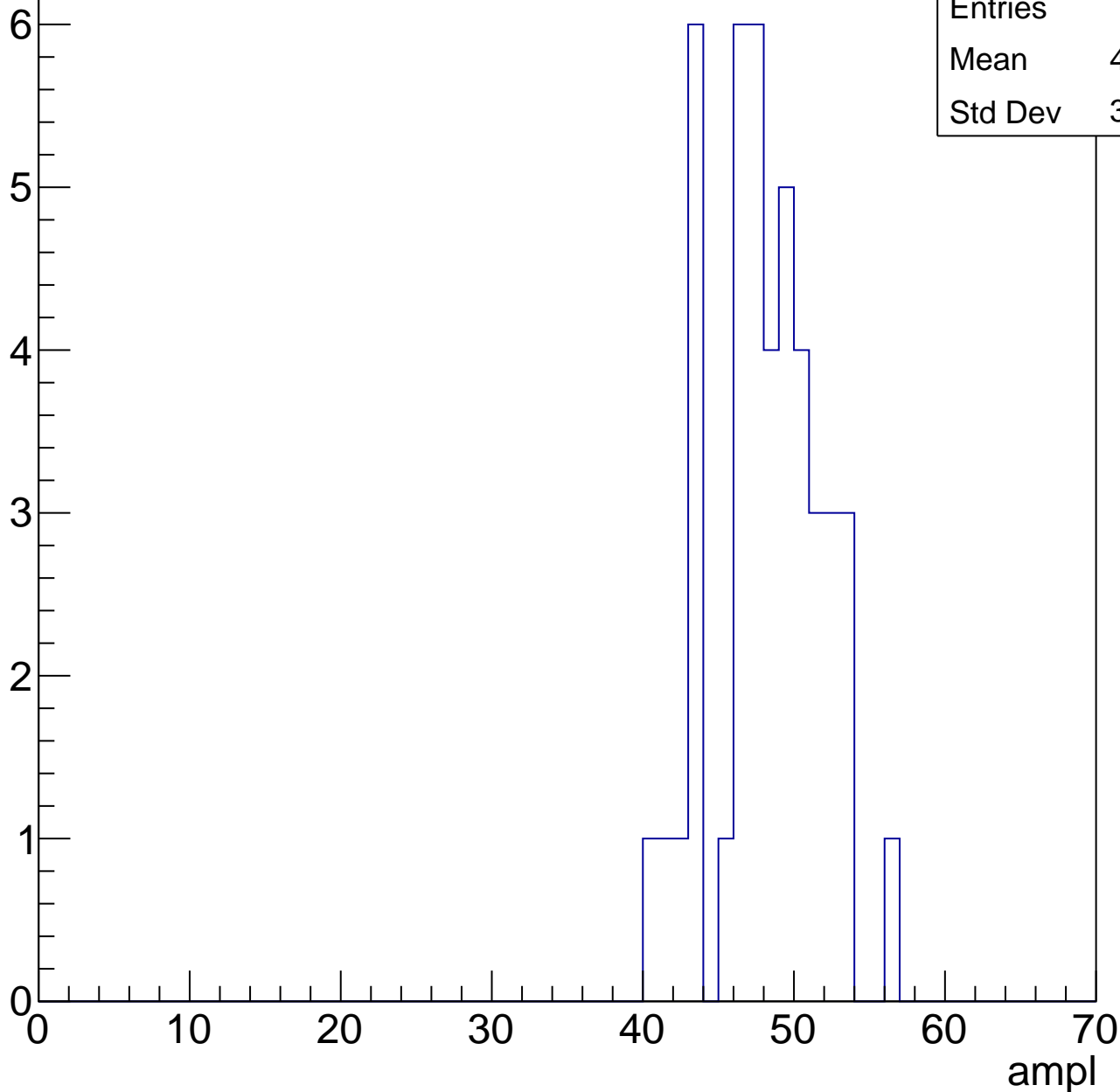


B1L102S, U14-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

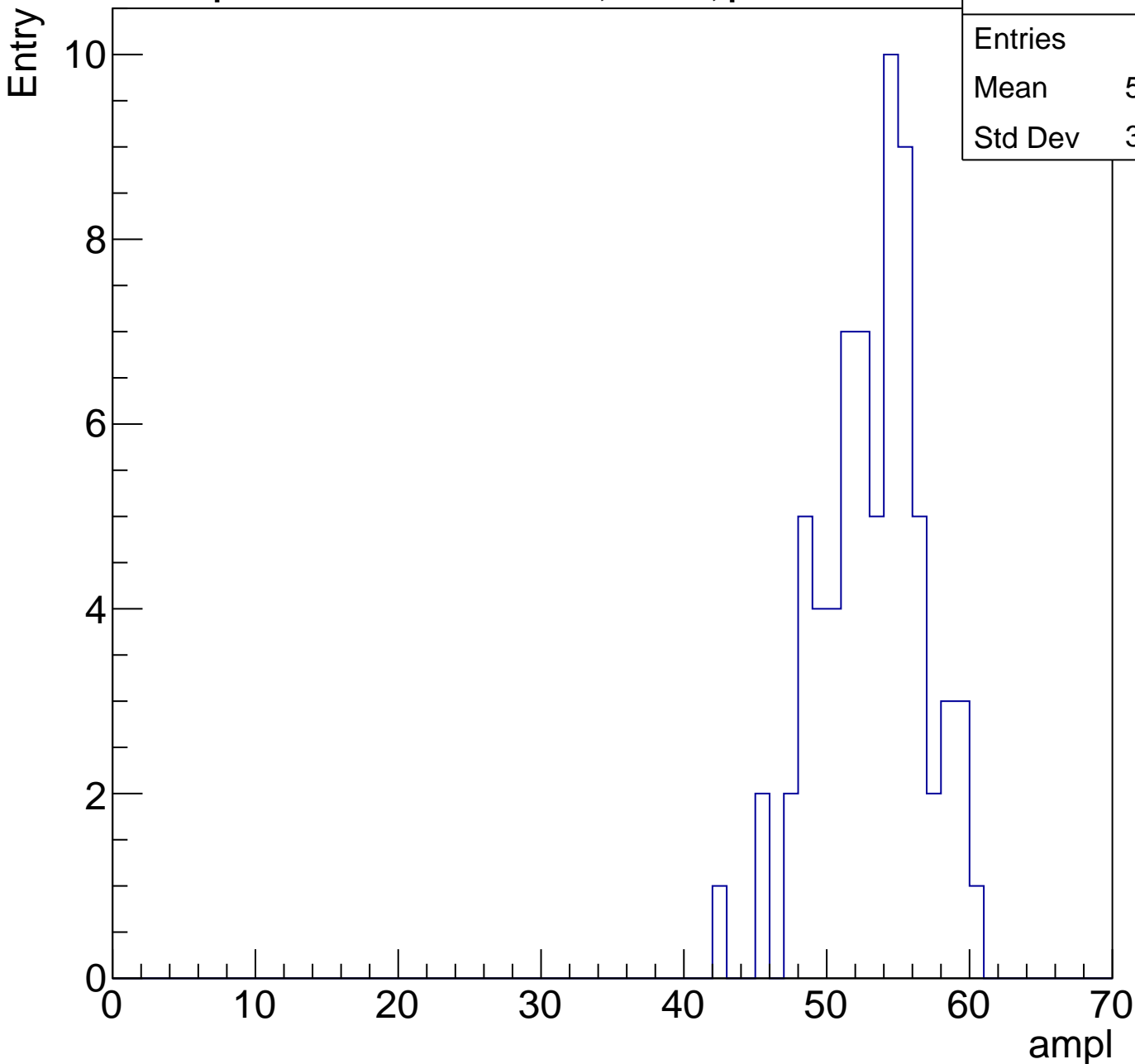
Entries	45
Mean	47.67
Std Dev	3.553



B1L102S, U14-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	52.69
Std Dev	3.643



B1L102S, U14-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	57.41
Std Dev	9.061

ampl

0

10

20

30

40

50

60

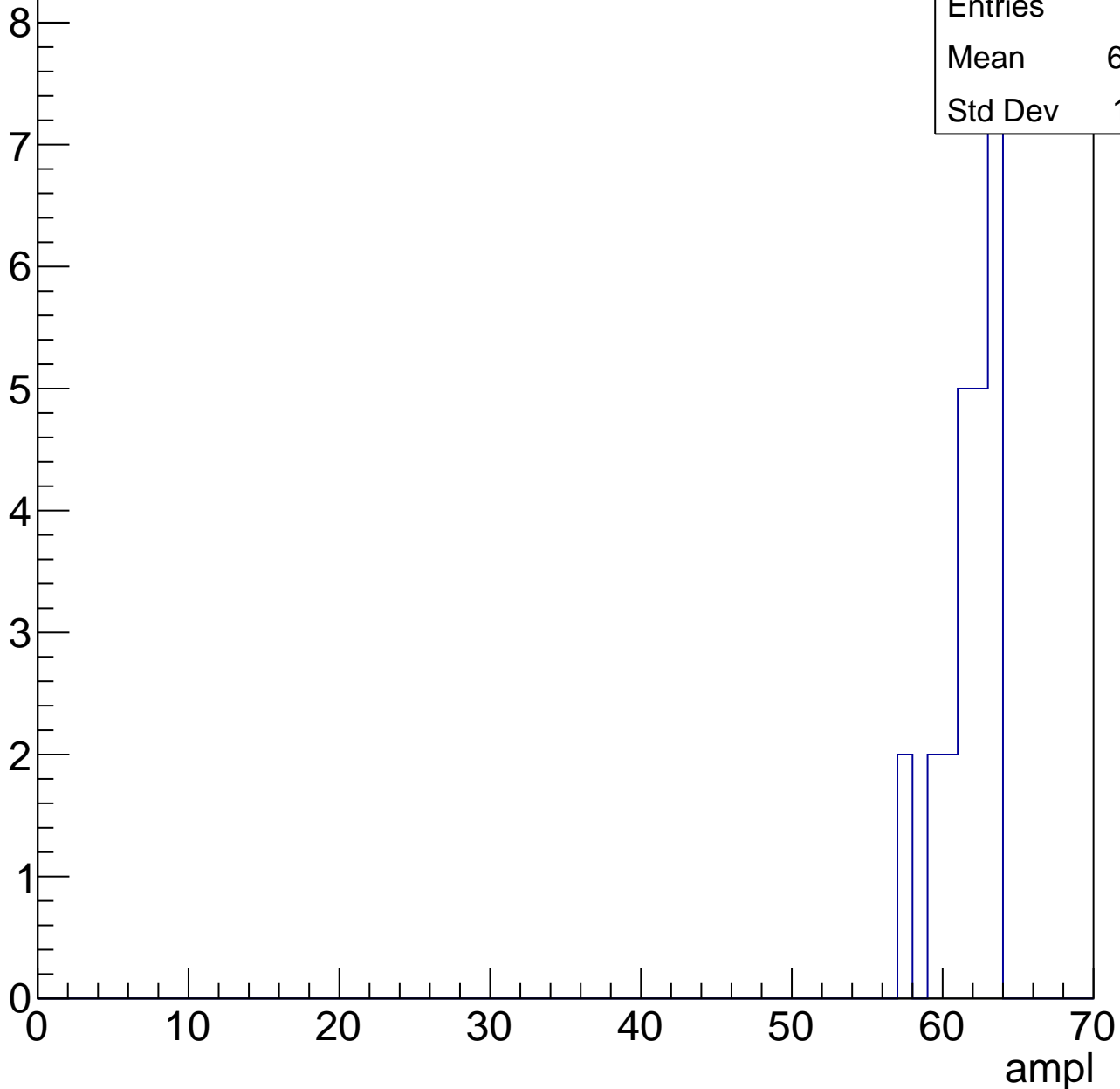
70

B1L102S, U14-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	24
Mean	61.29
Std Dev	1.791



B1L102S, U14-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	25
Std Dev	27.31

ampl

B1L102S, U14-ch43, adc0

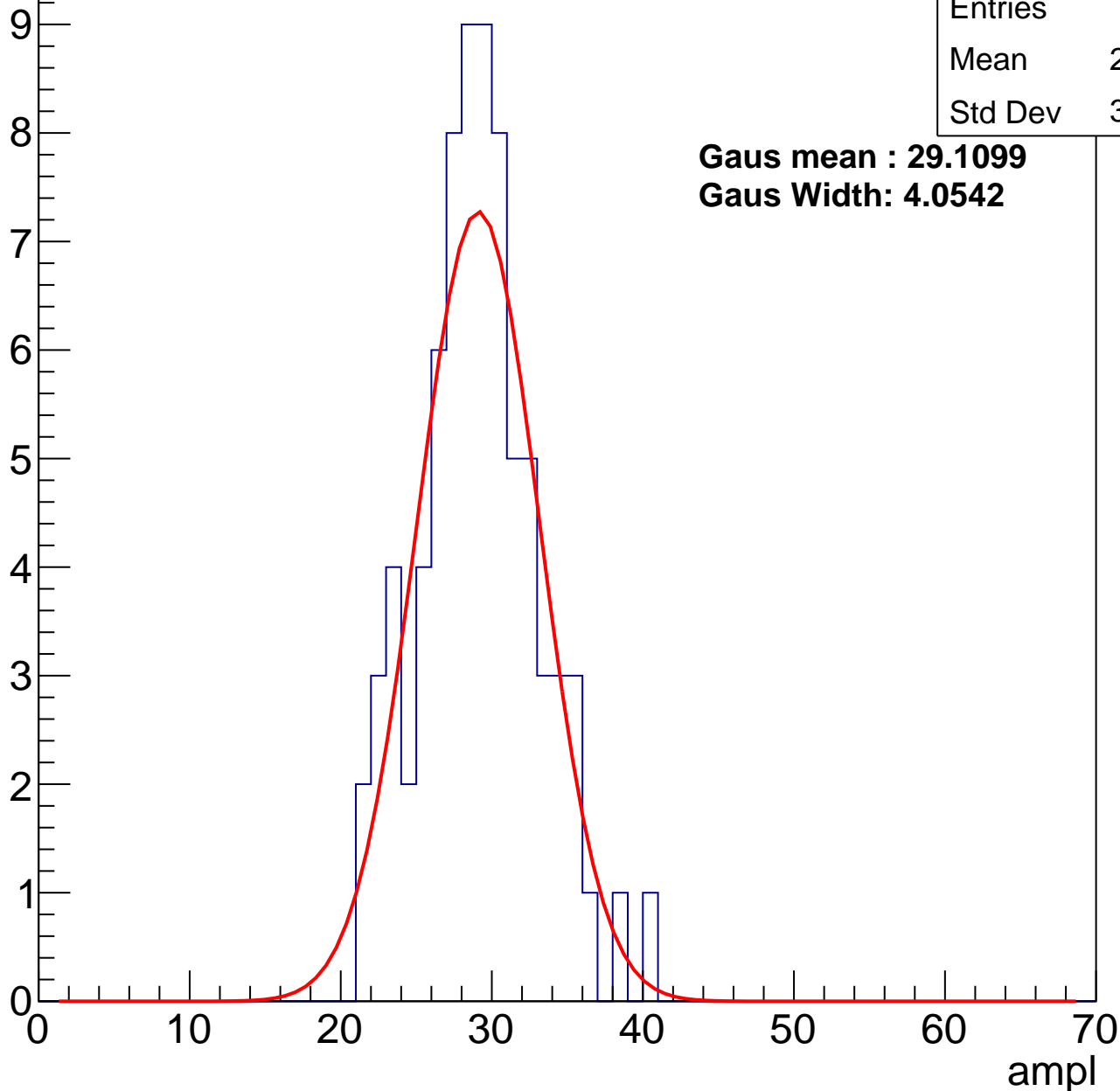
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.68
Std Dev	3.906

Gaus mean : 29.1099

Gaus Width: 4.0542



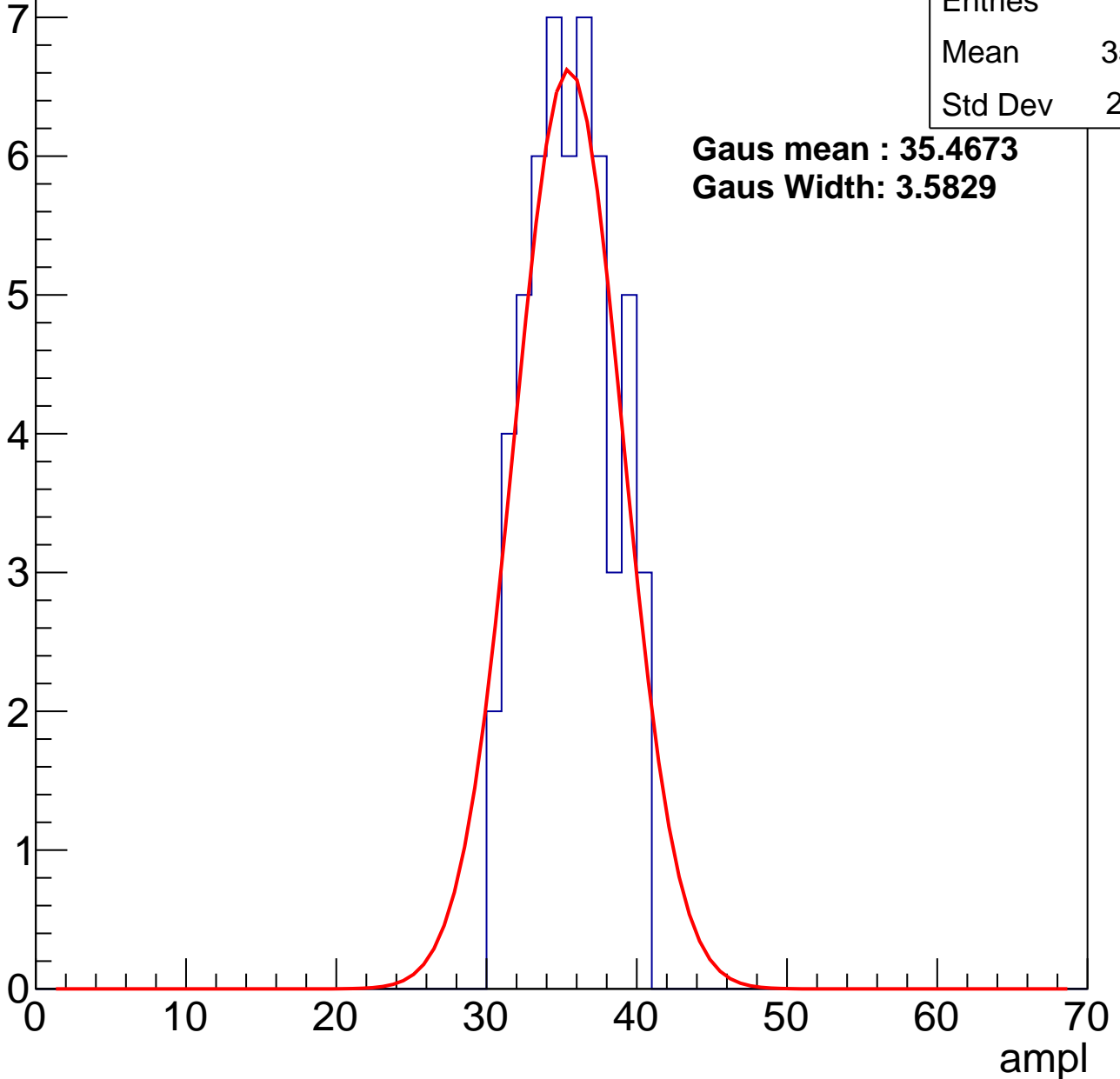
B1L102S, U14-ch43, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.06
Std Dev	2.731

Gaus mean : 35.4673
Gaus Width: 3.5829



B1L102S, U14-ch43, adc2

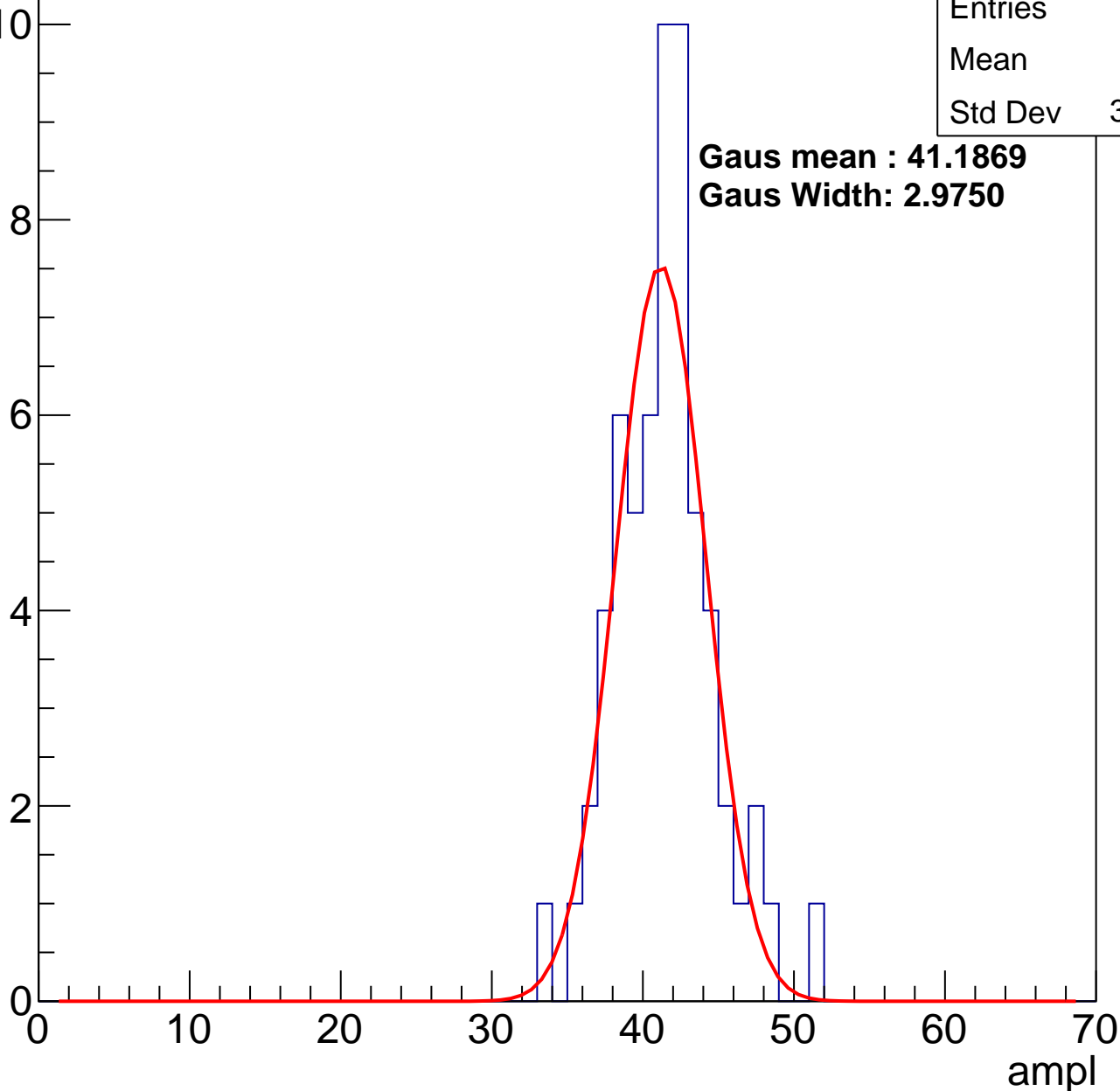
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41
Std Dev	3.234

Gaus mean : 41.1869

Gaus Width: 2.9750

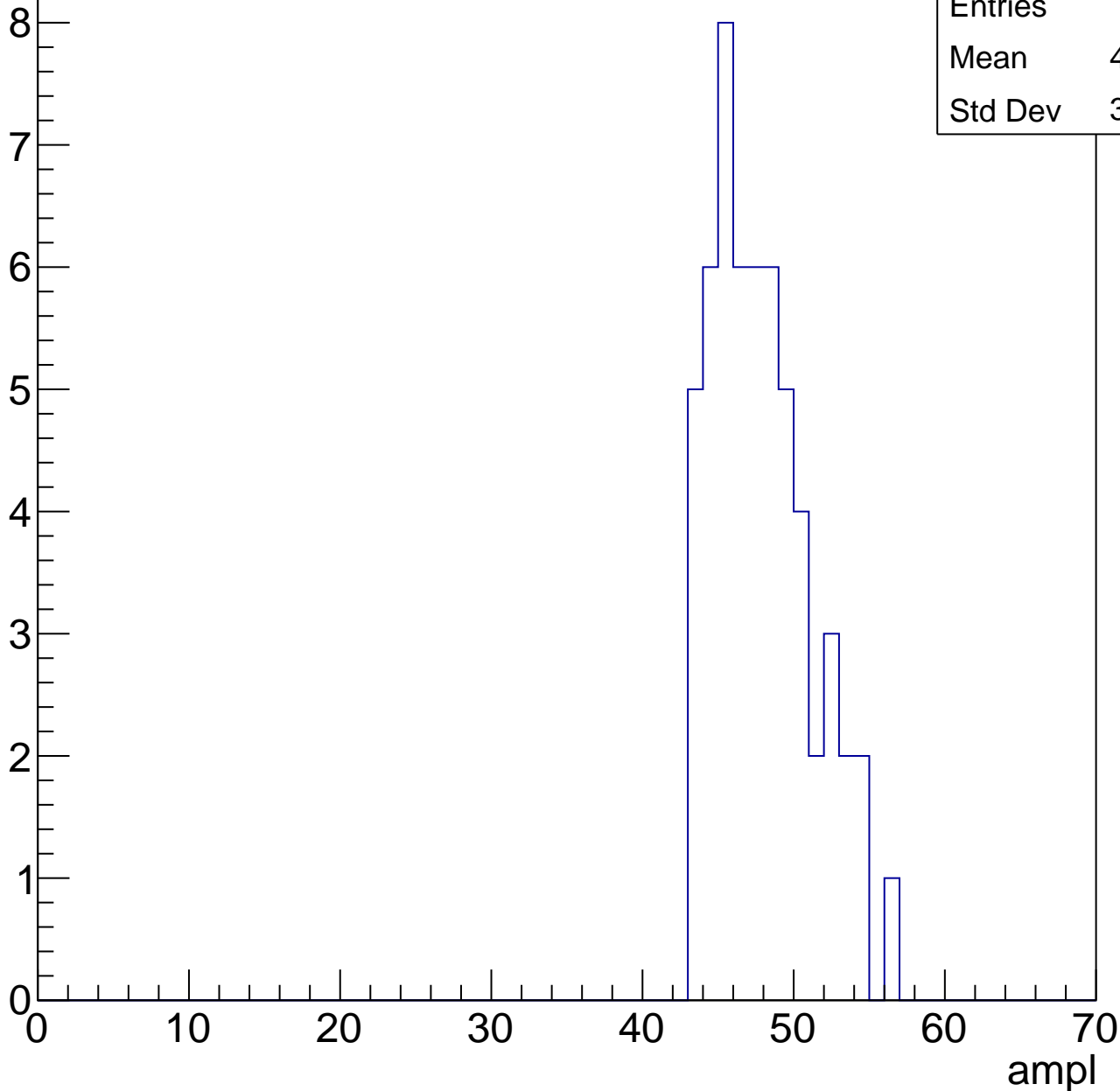


B1L102S, U14-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	47.46
Std Dev	3.218

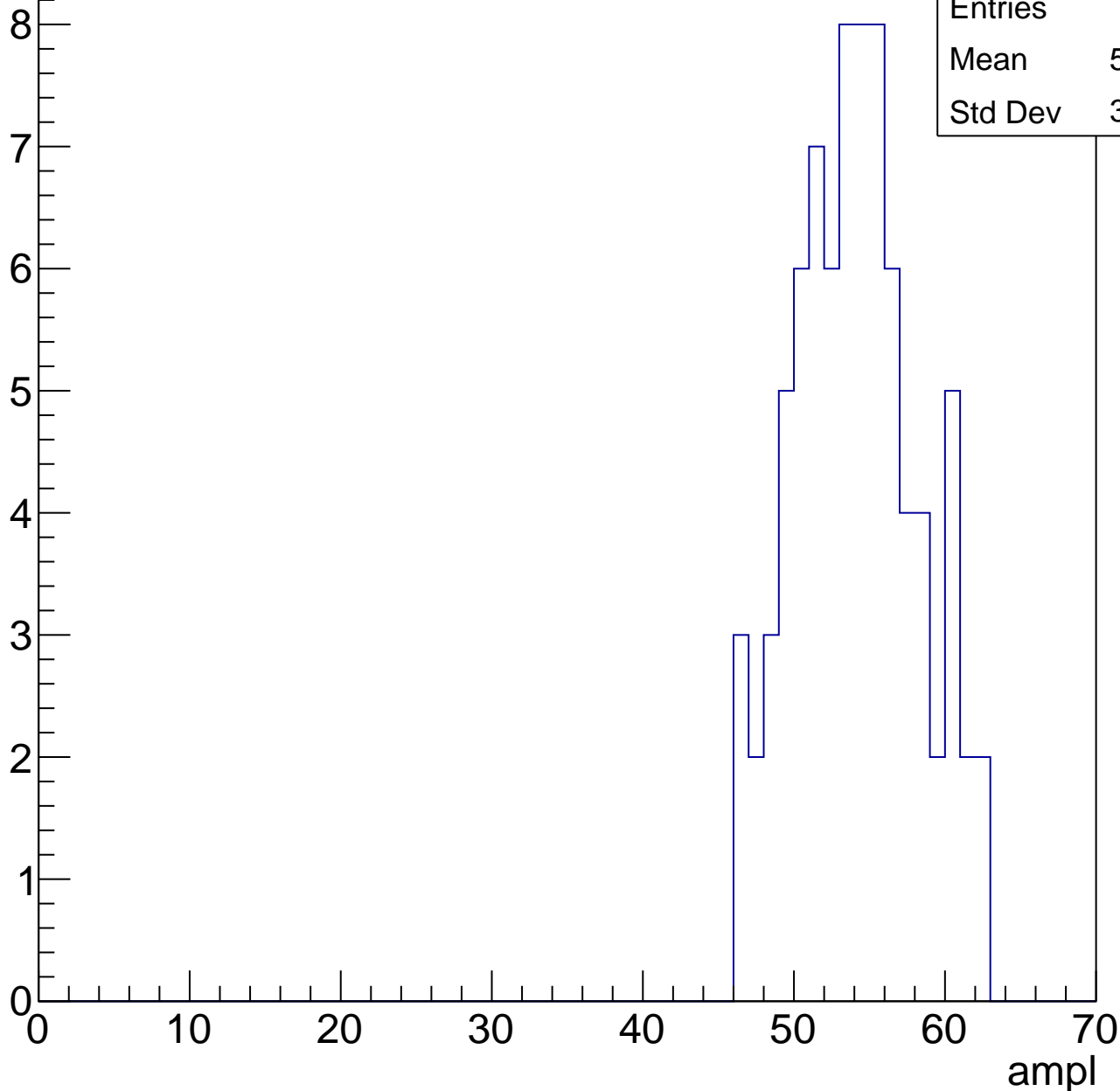


B1L102S, U14-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

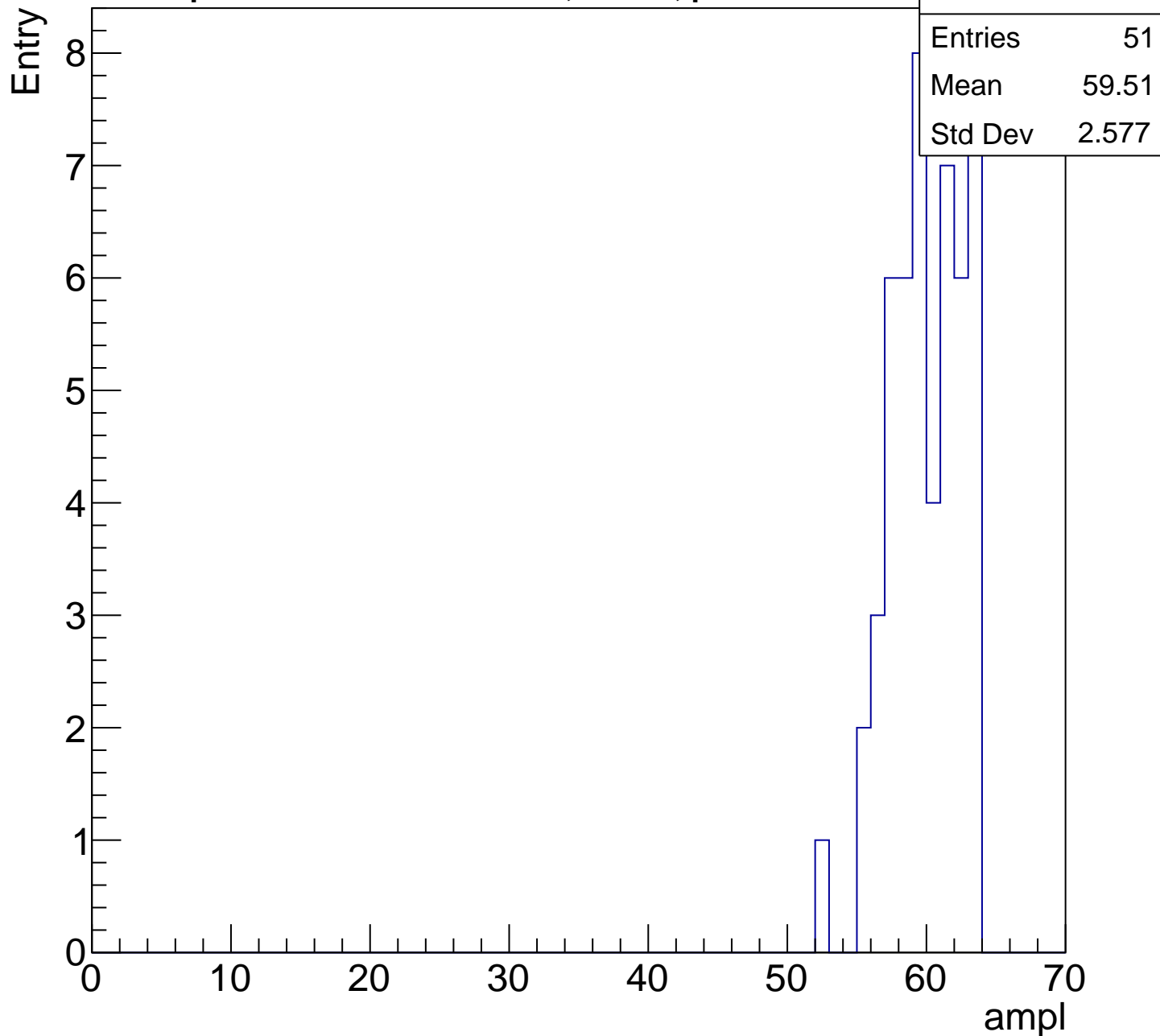
Entry

Entries	81
Mean	53.65
Std Dev	3.994



B1L102S, U14-ch43, adc5

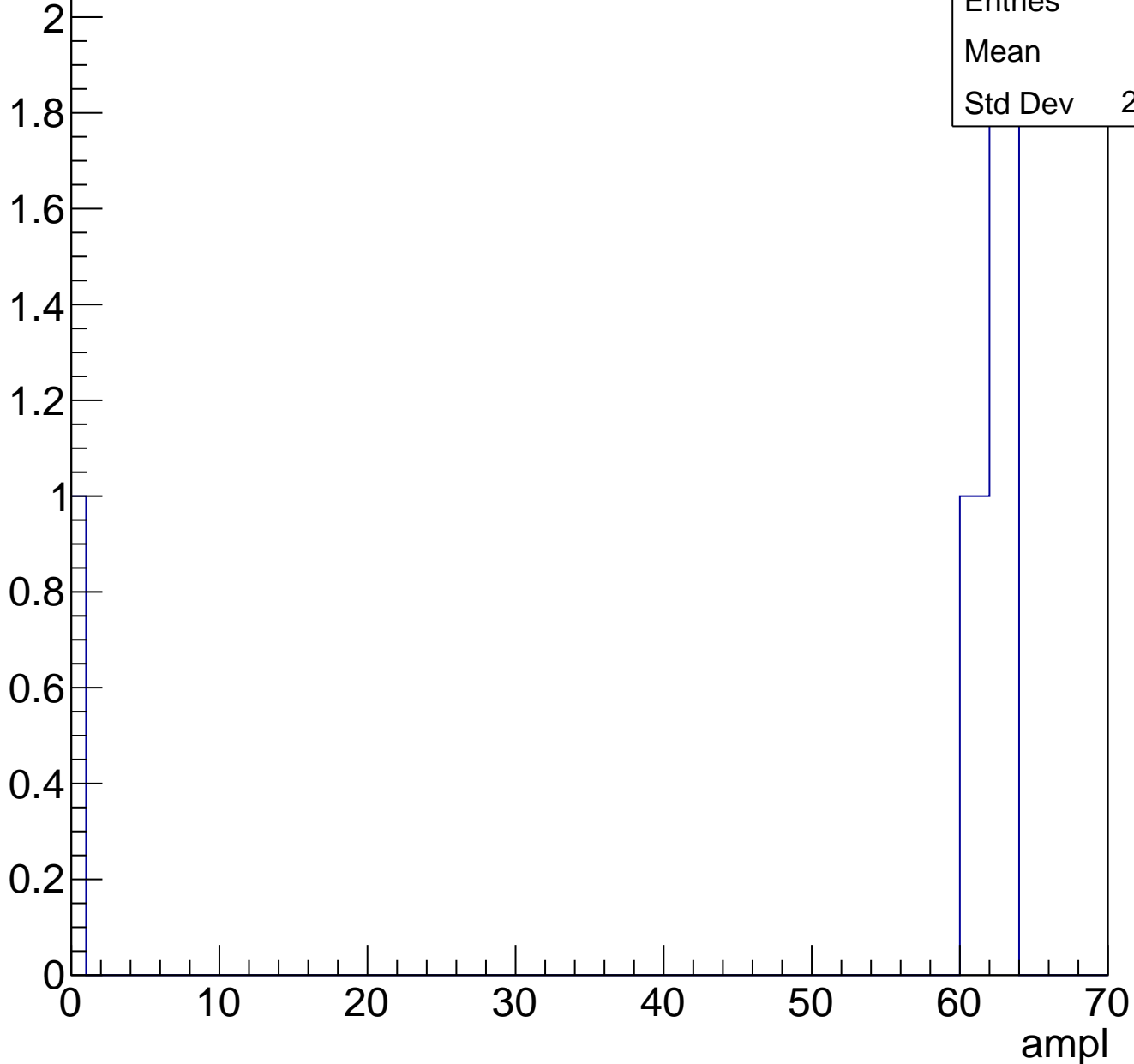
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U14-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch44, adc0

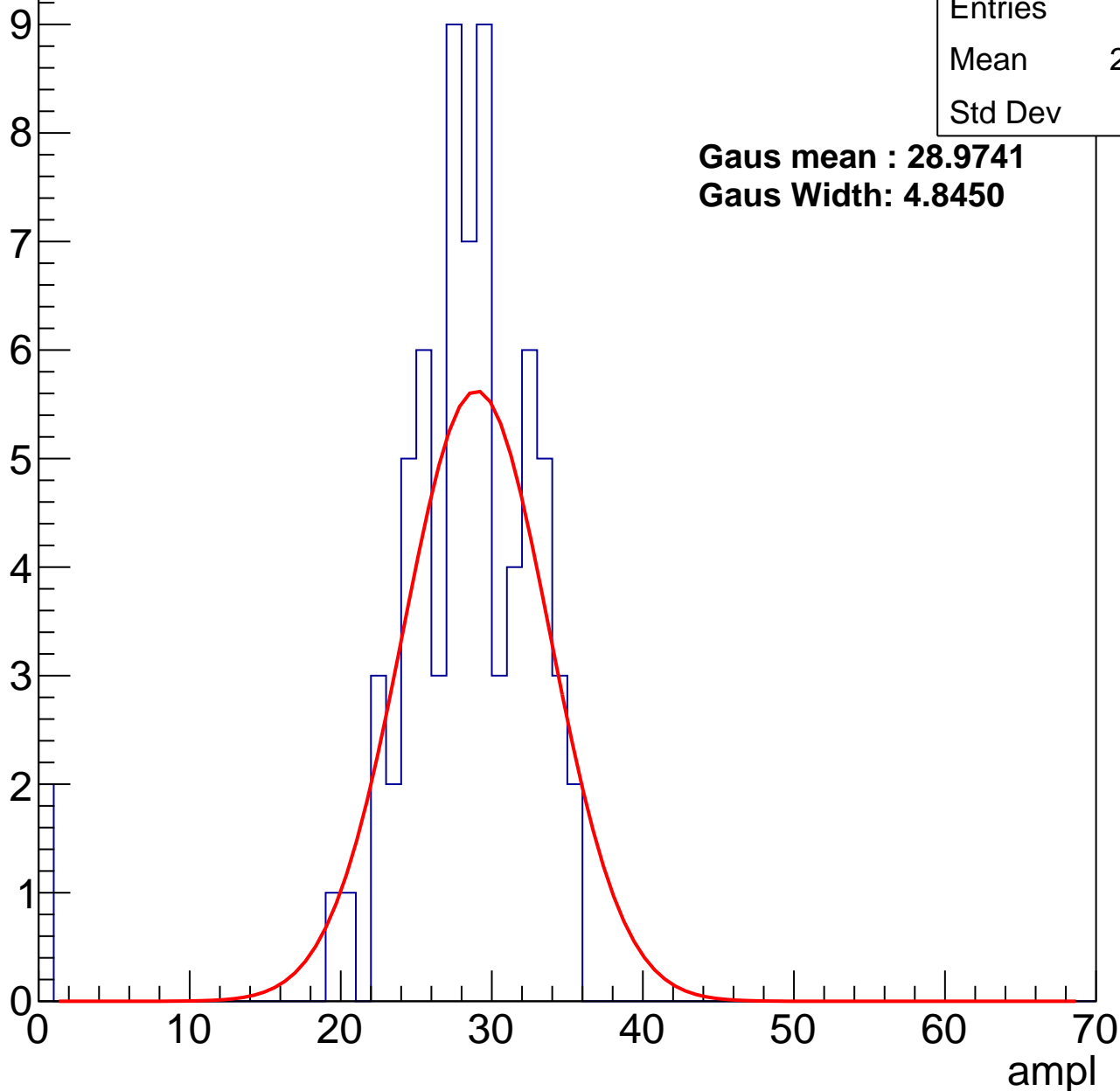
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.35
Std Dev	5.91

Gaus mean : 28.9741

Gaus Width: 4.8450



B1L102S, U14-ch44, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	35.5
Std Dev	3.719

Gaus mean : 36.2616

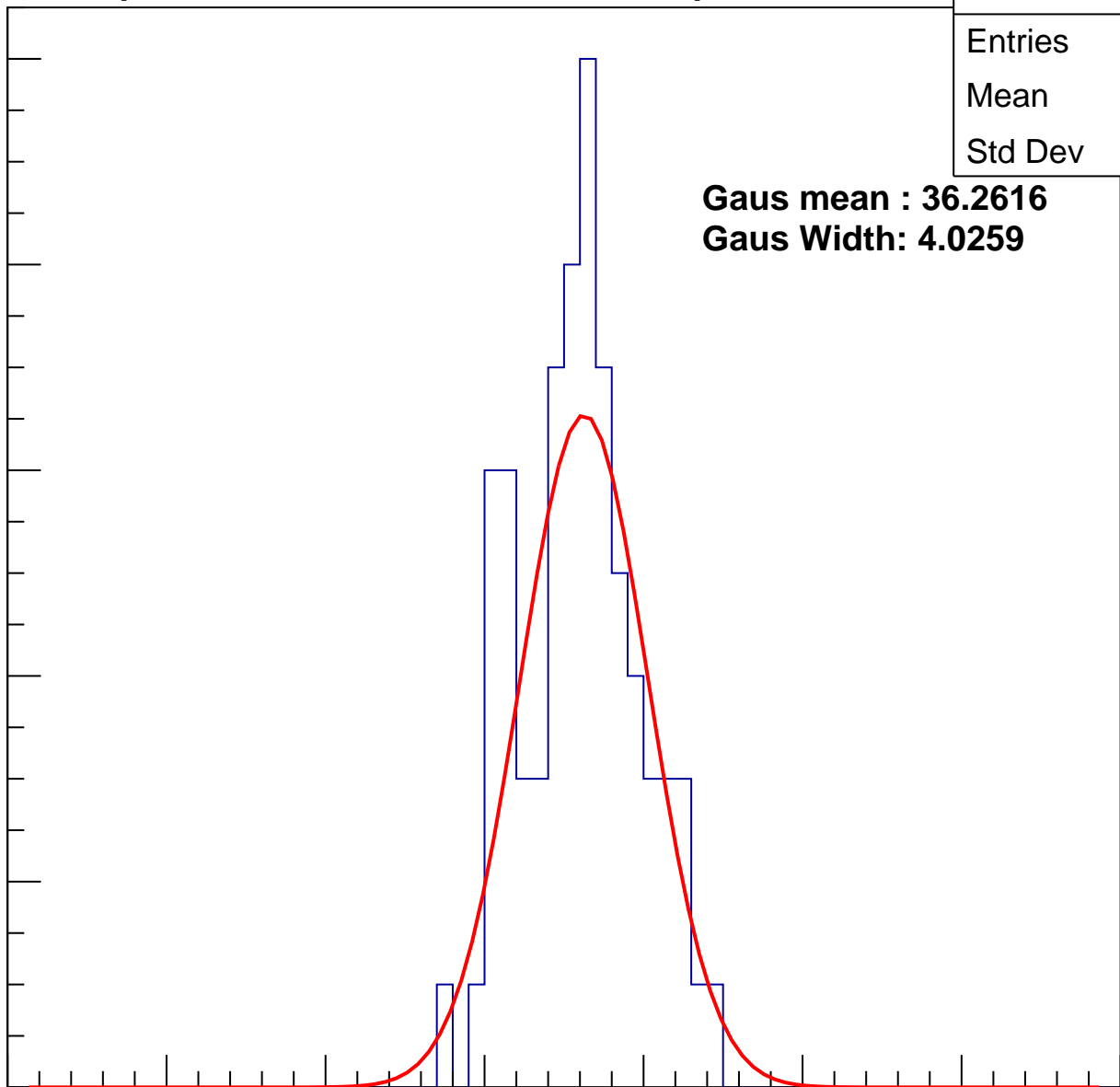
Gaus Width: 4.0259

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch44, adc2

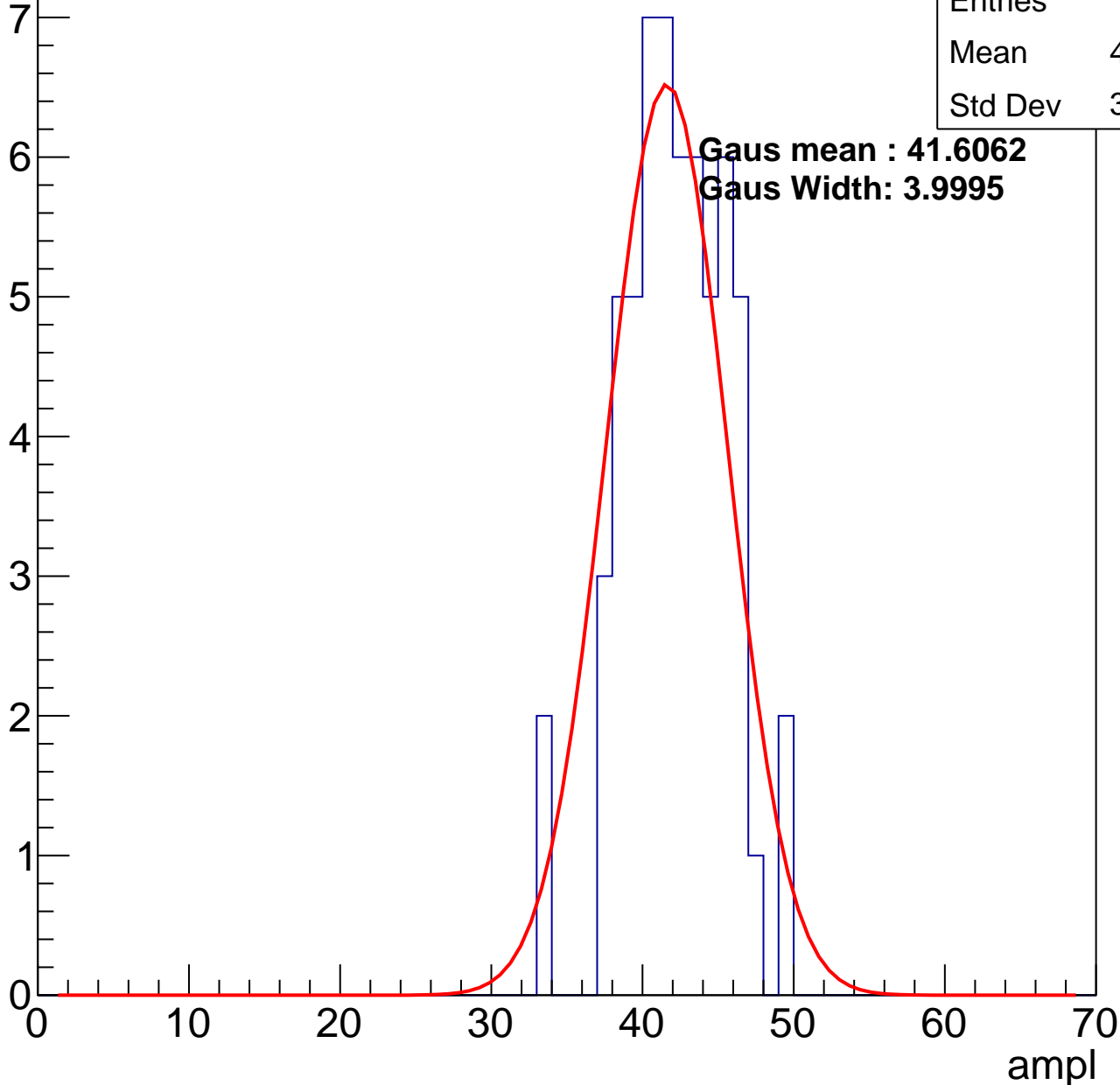
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.73
Std Dev	3.356

Gaus mean : 41.6062

Gaus Width: 3.9995

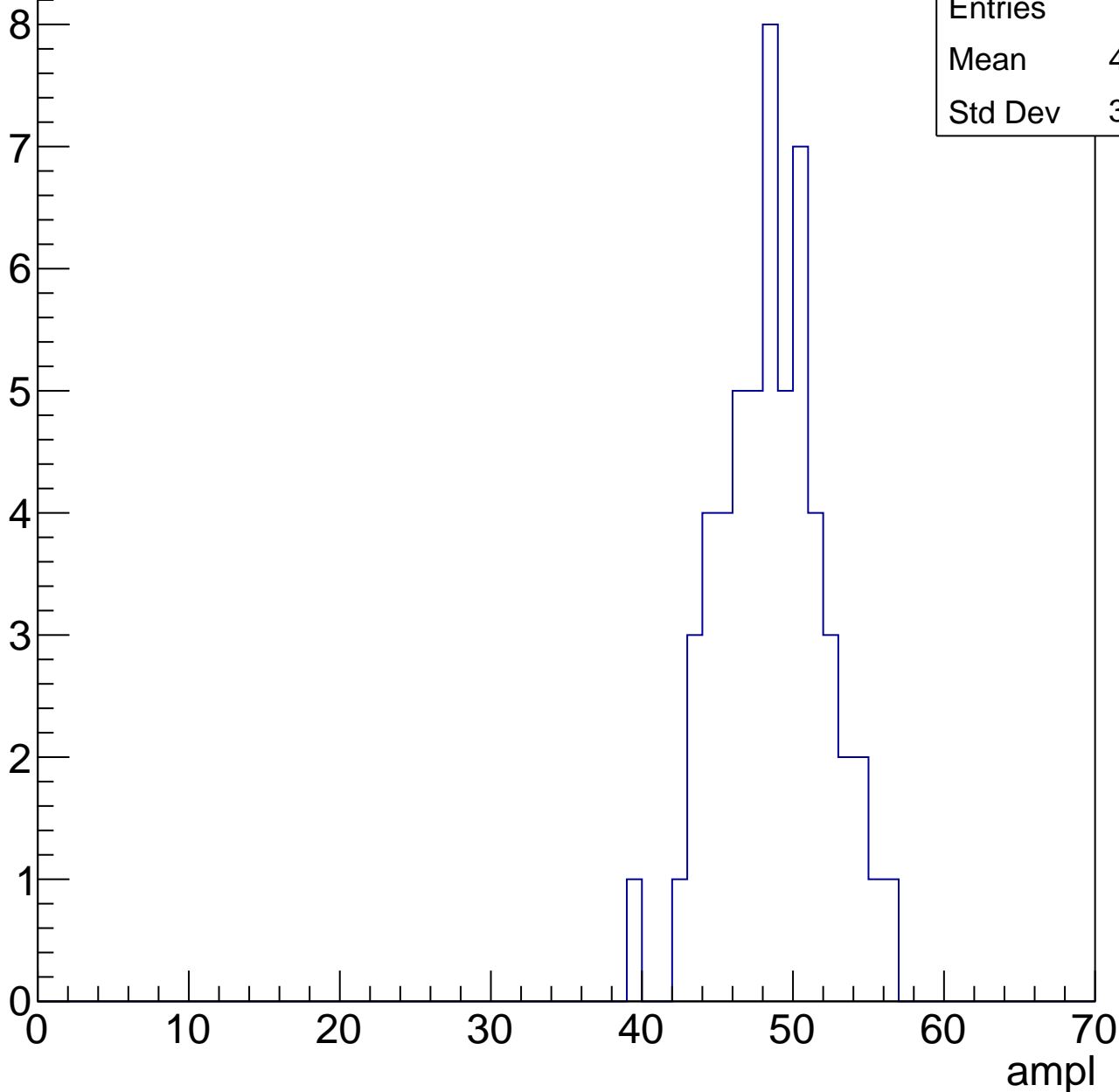


B1L102S, U14-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.12
Std Dev	3.454

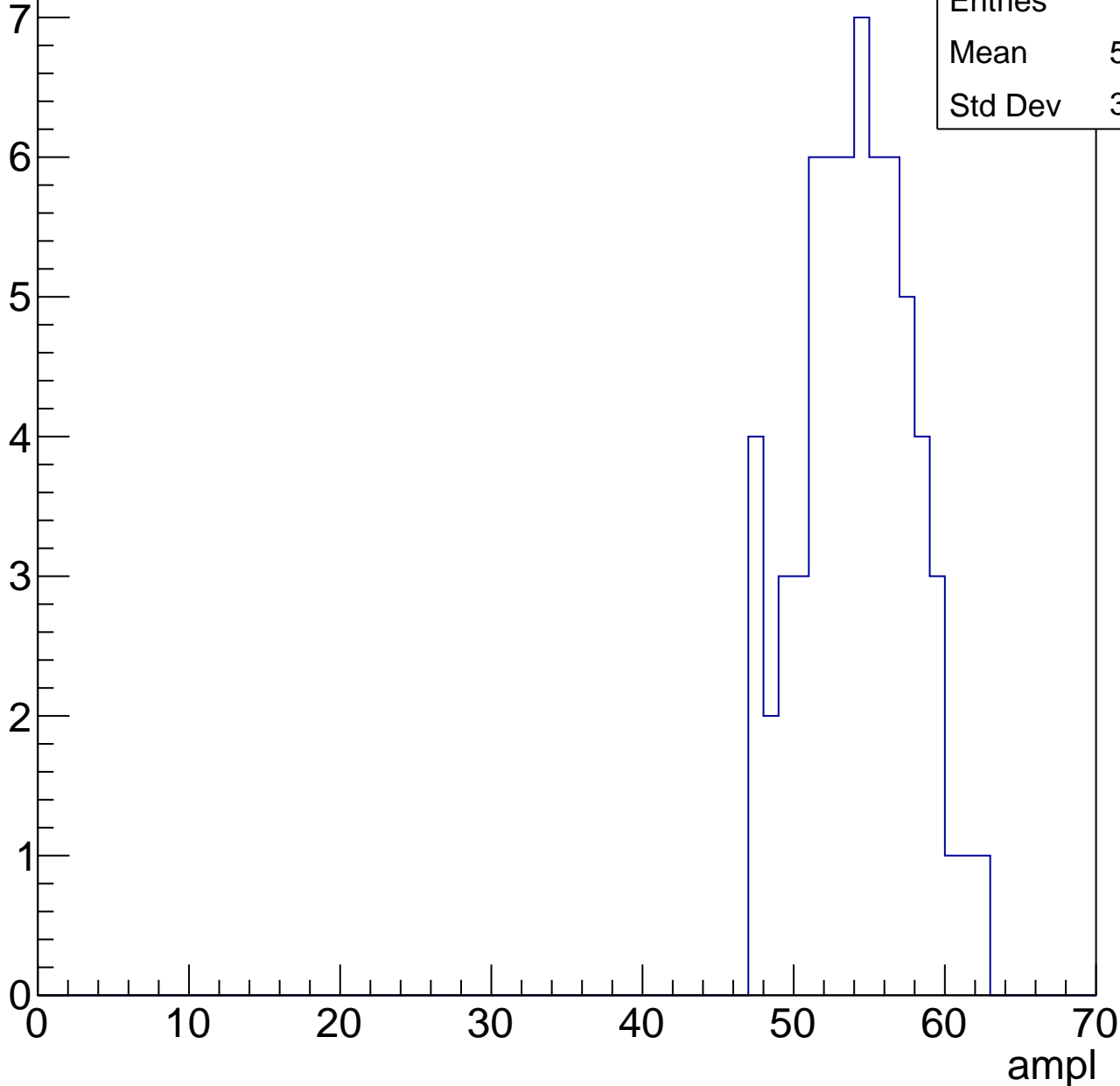


B1L102S, U14-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	53.72
Std Dev	3.608

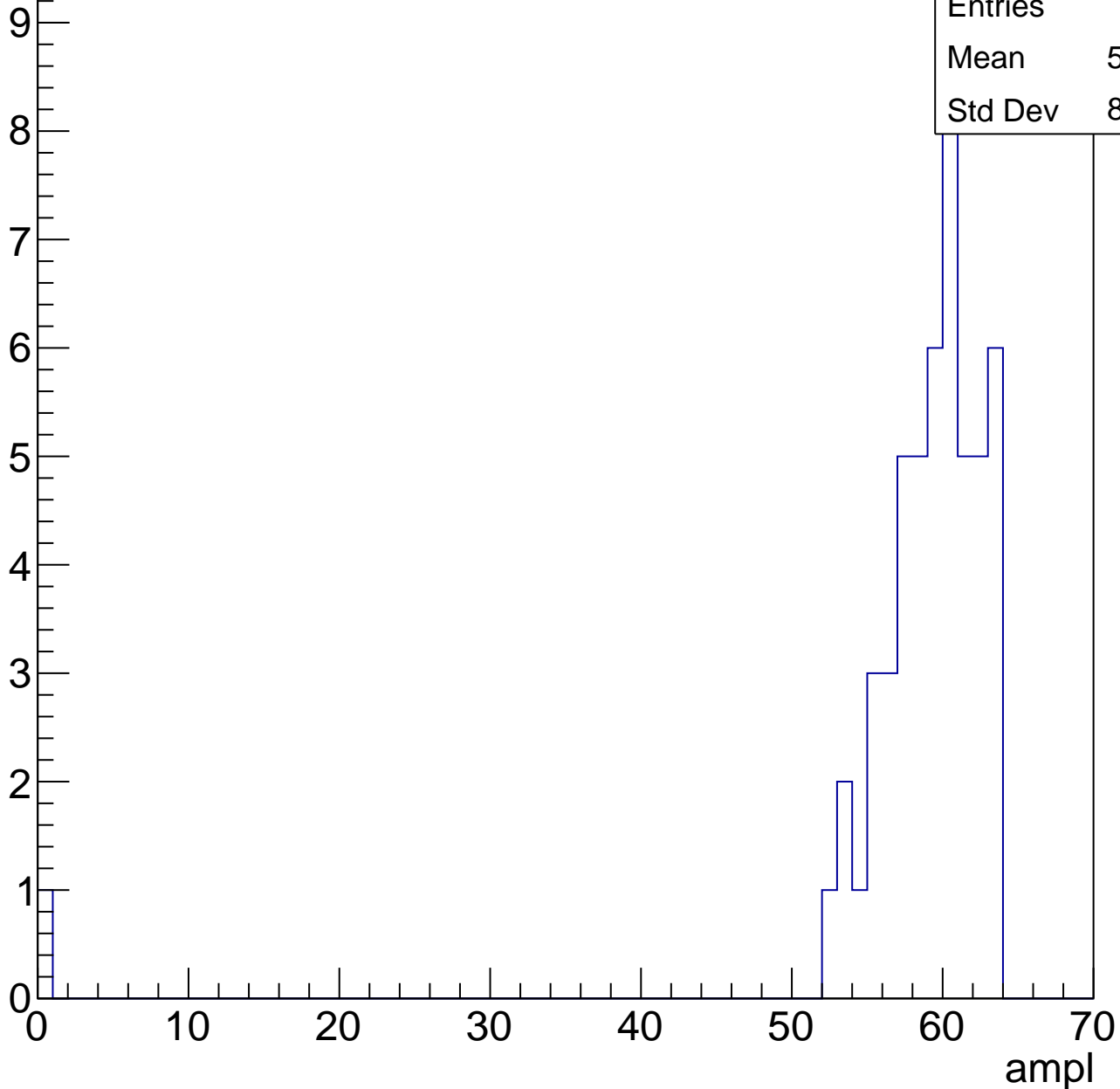


B1L102S, U14-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

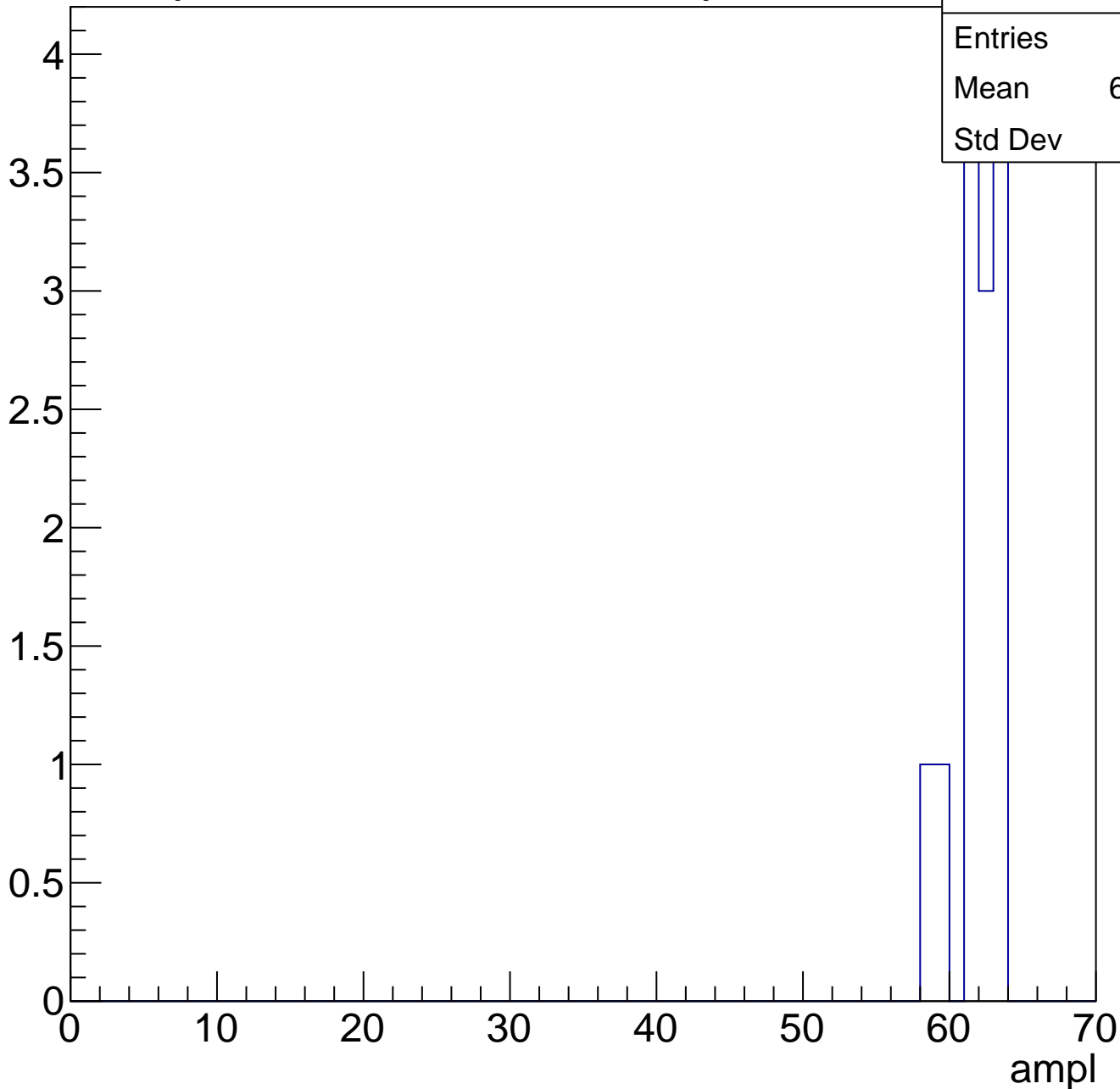
Entries	52
Mean	57.83
Std Dev	8.577



B1L102S, U14-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch45, adc0

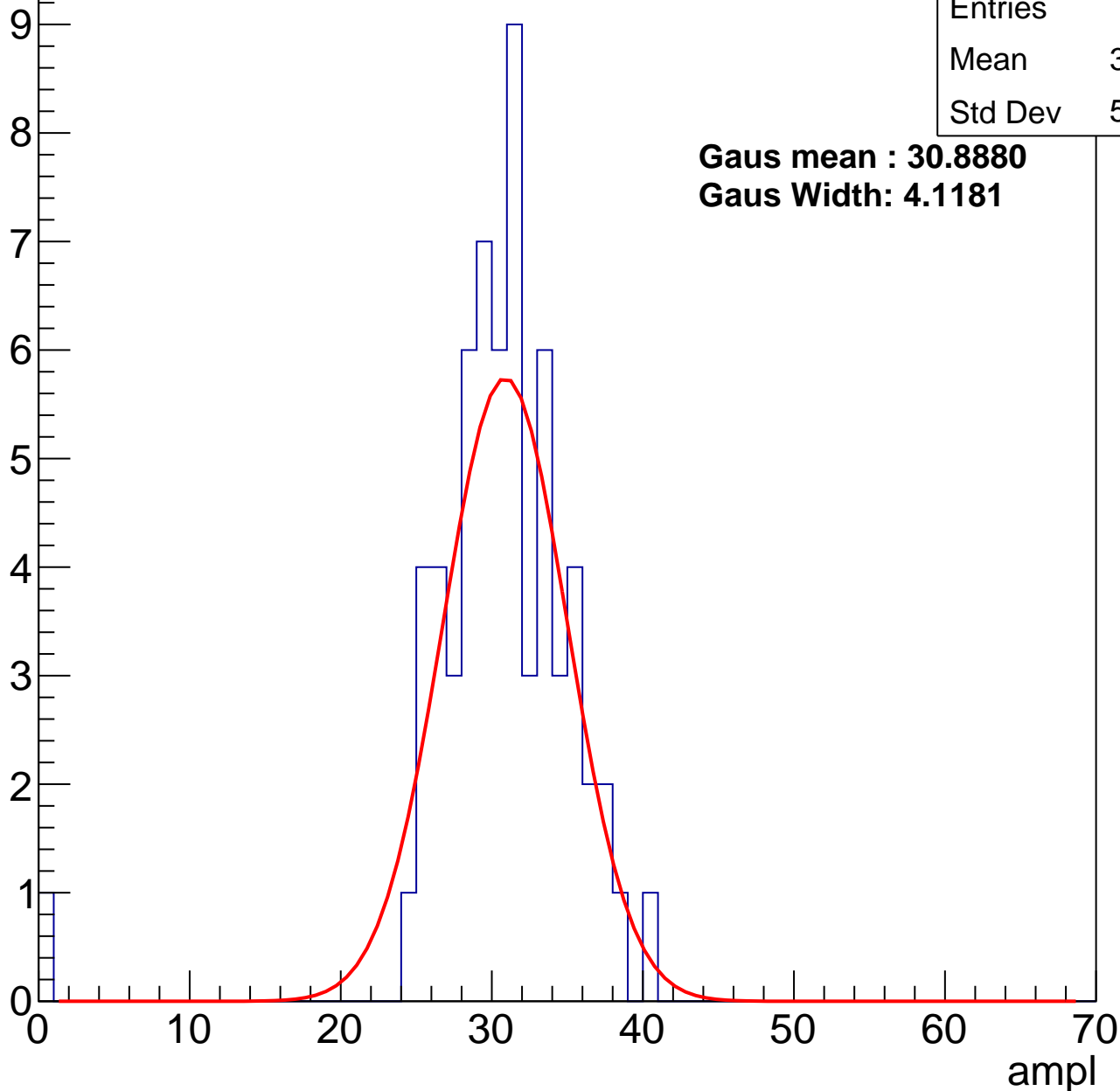
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	30.14
Std Dev	5.216

Gaus mean : 30.8880

Gaus Width: 4.1181



B1L102S, U14-ch45, adc1

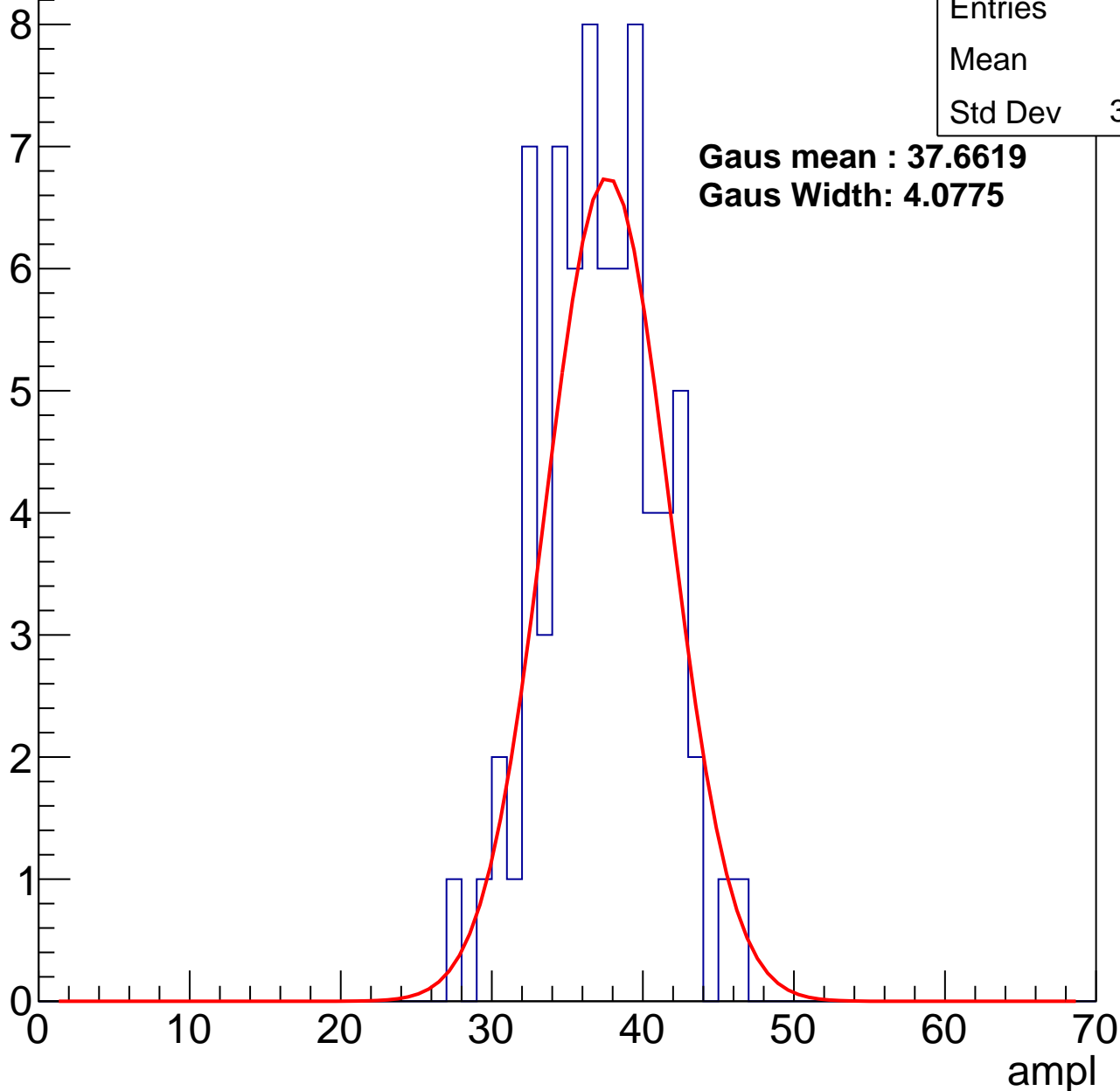
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.7
Std Dev	3.863

Gaus mean : 37.6619

Gaus Width: 4.0775



B1L102S, U14-ch45, adc2

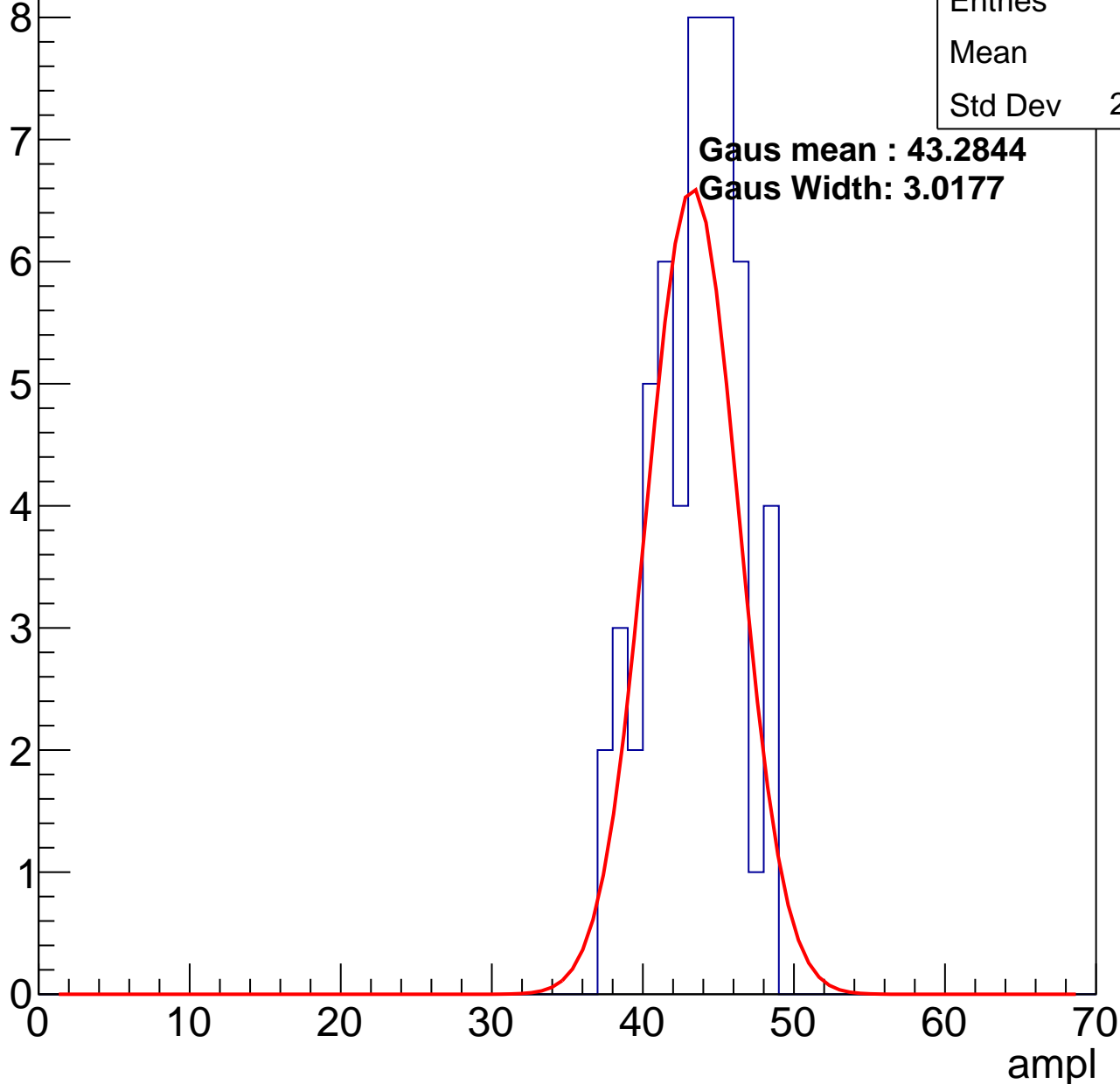
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43
Std Dev	2.847

Gaus mean : 43.2844

Gaus Width: 3.0177

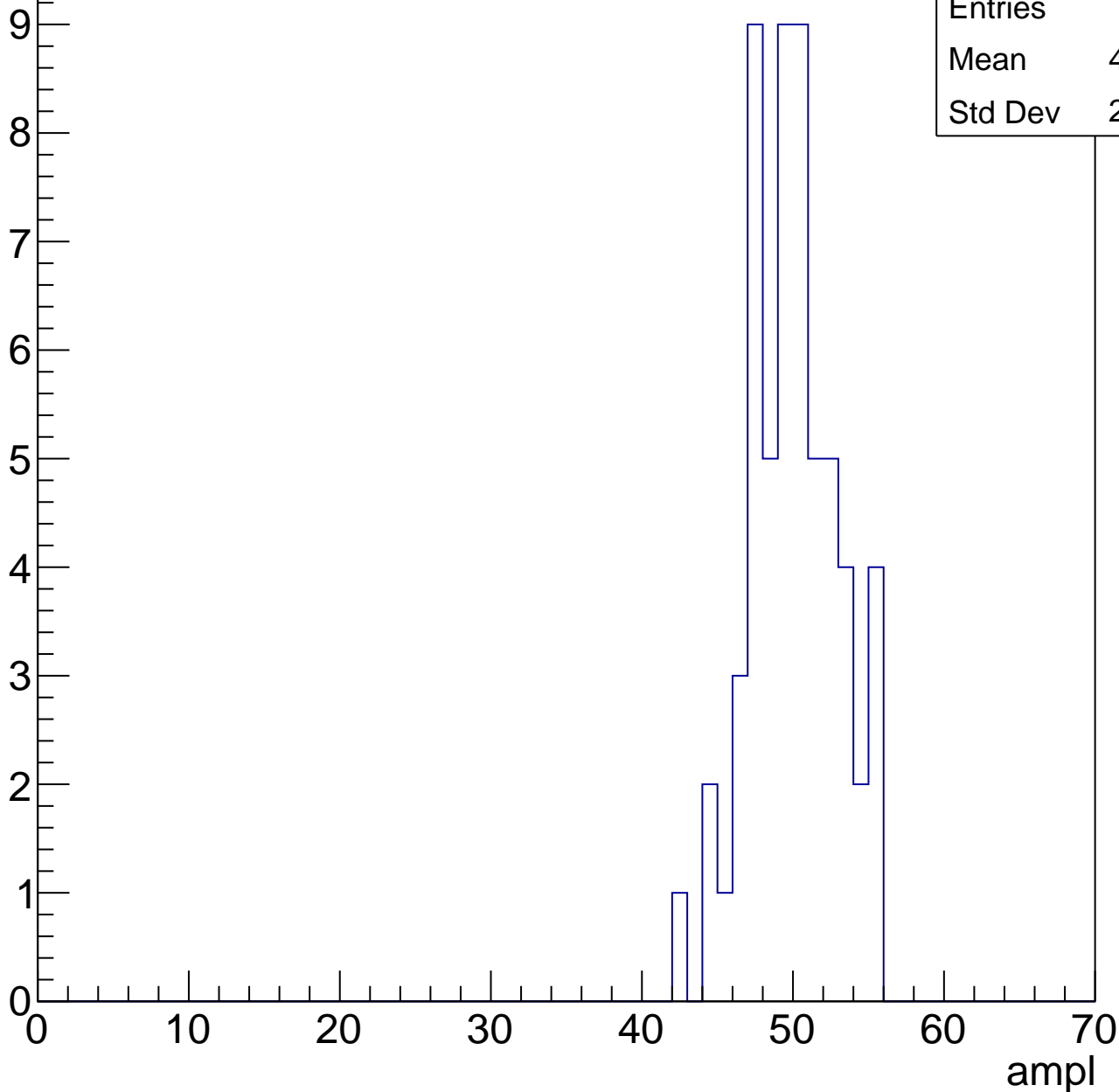


B1L102S, U14-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

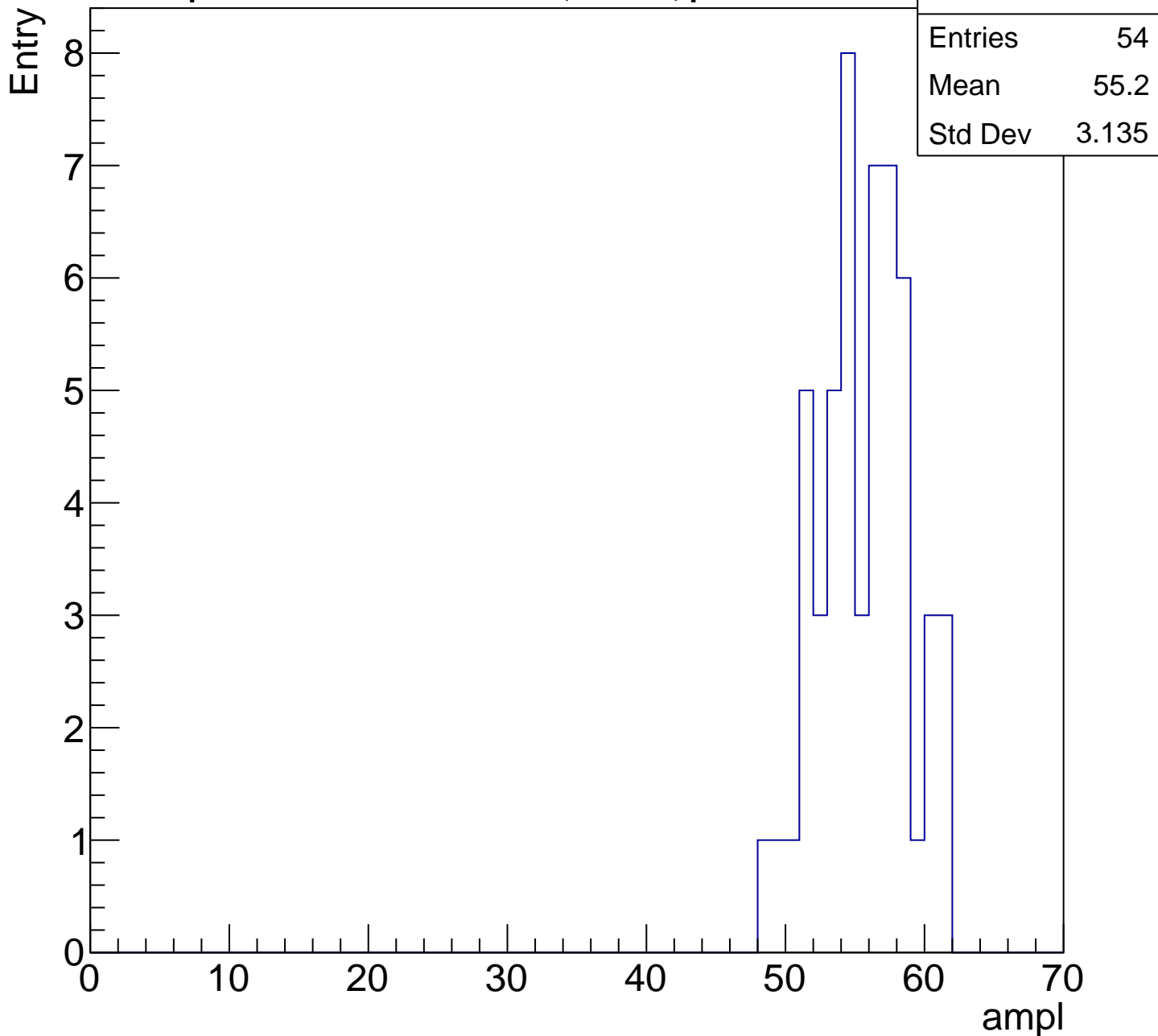
Entry

Entries	59
Mean	49.53
Std Dev	2.907



B1L102S, U14-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U14-ch45, adc5

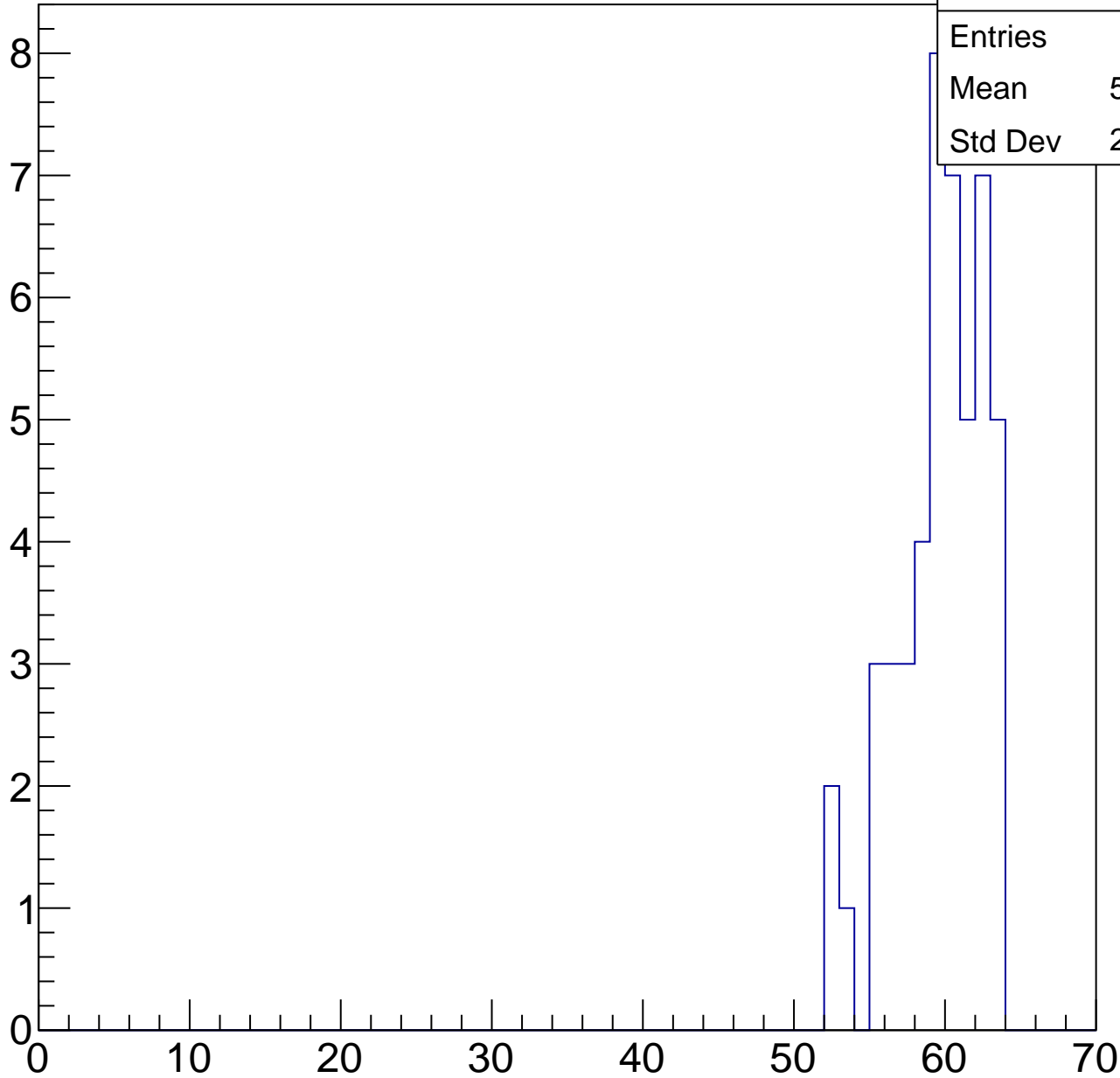
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.15
Std Dev	2.865

ampl

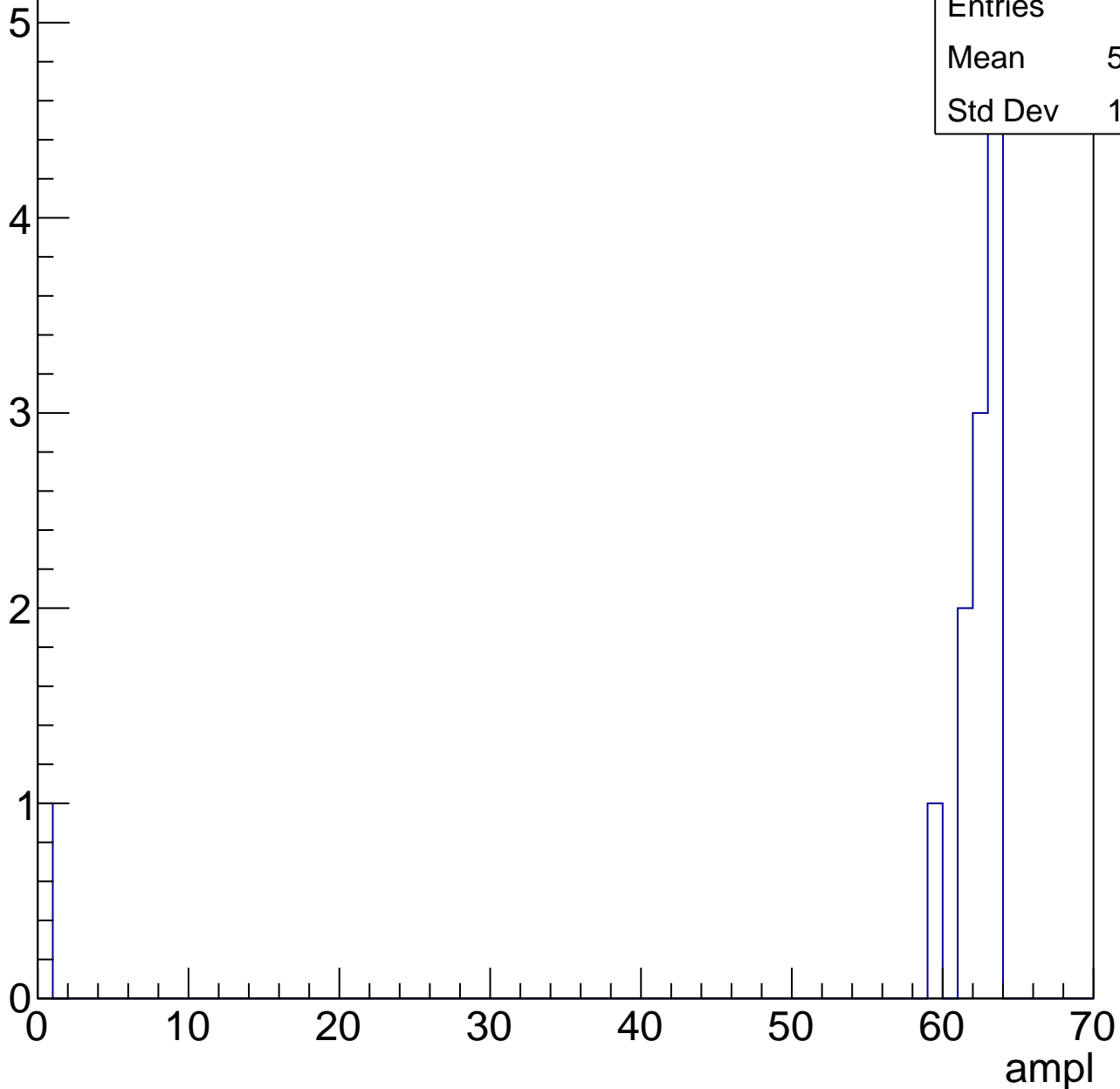


B1L102S, U14-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	56.83
Std Dev	17.17



B1L102S, U14-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch46, adc0

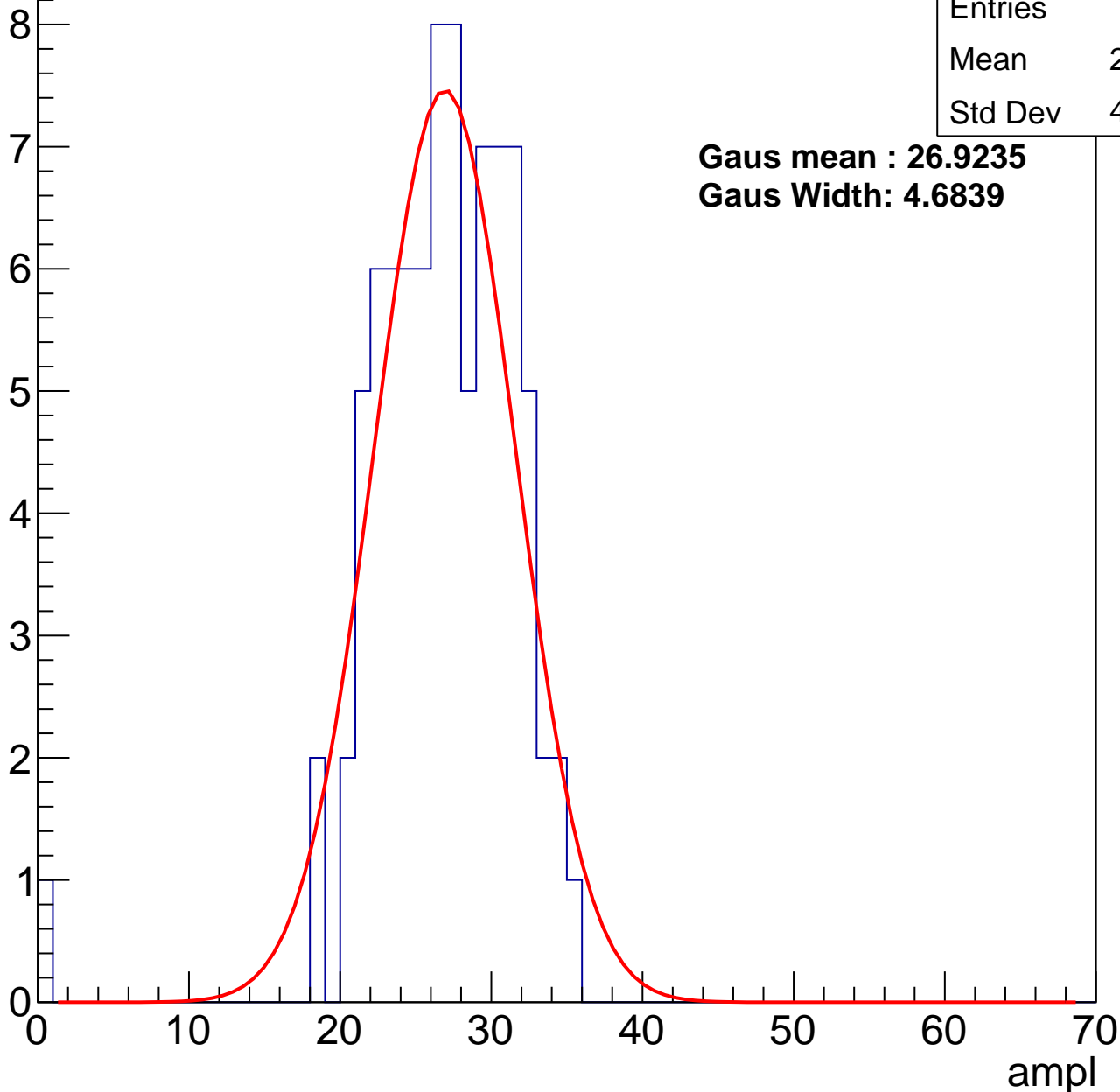
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	26.37
Std Dev	4.863

Gaus mean : 26.9235

Gaus Width: 4.6839



B1L102S, U14-ch46, adc1

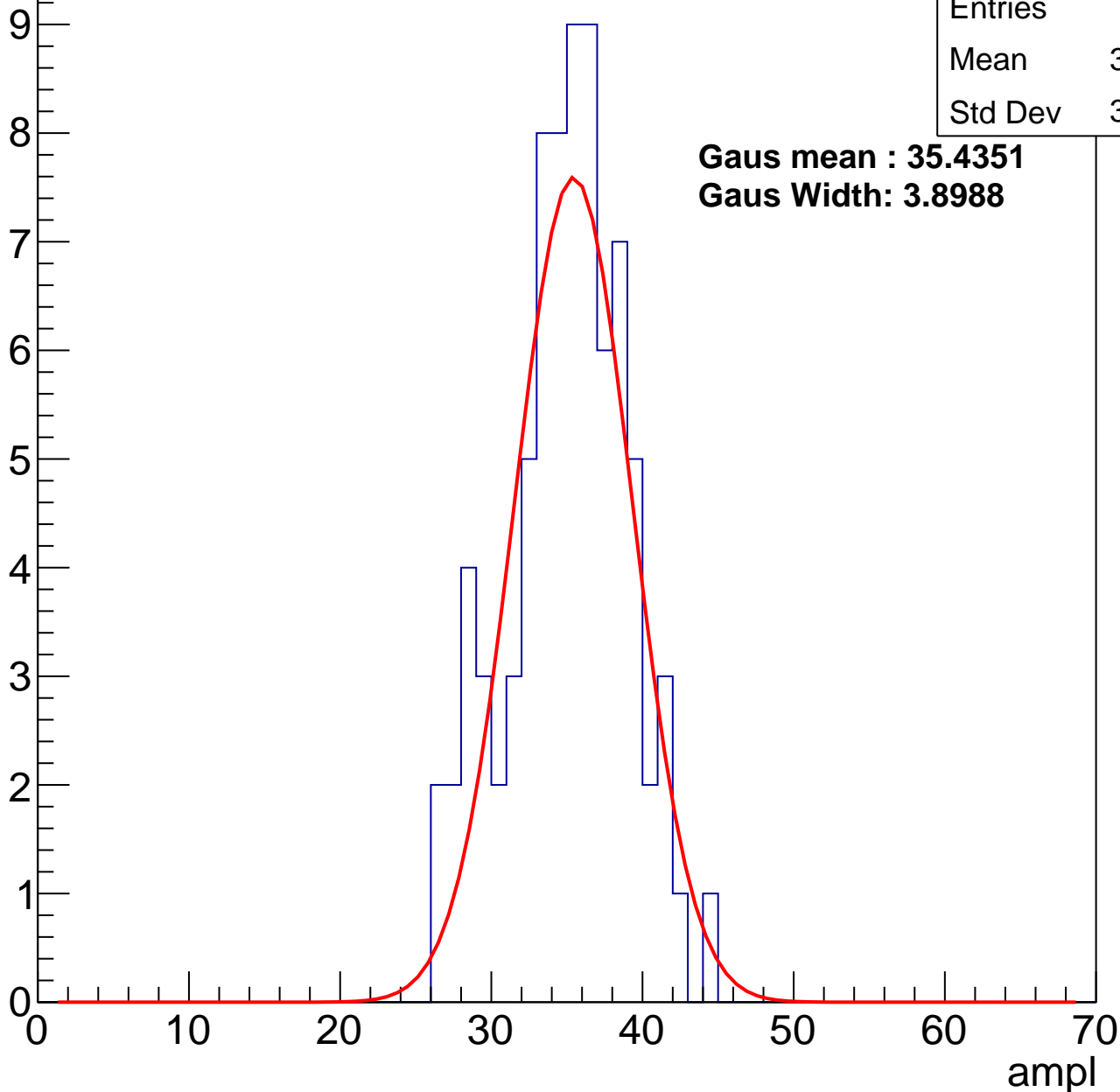
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.56
Std Dev	3.914

Gaus mean : 35.4351

Gaus Width: 3.8988



B1L102S, U14-ch46, adc2

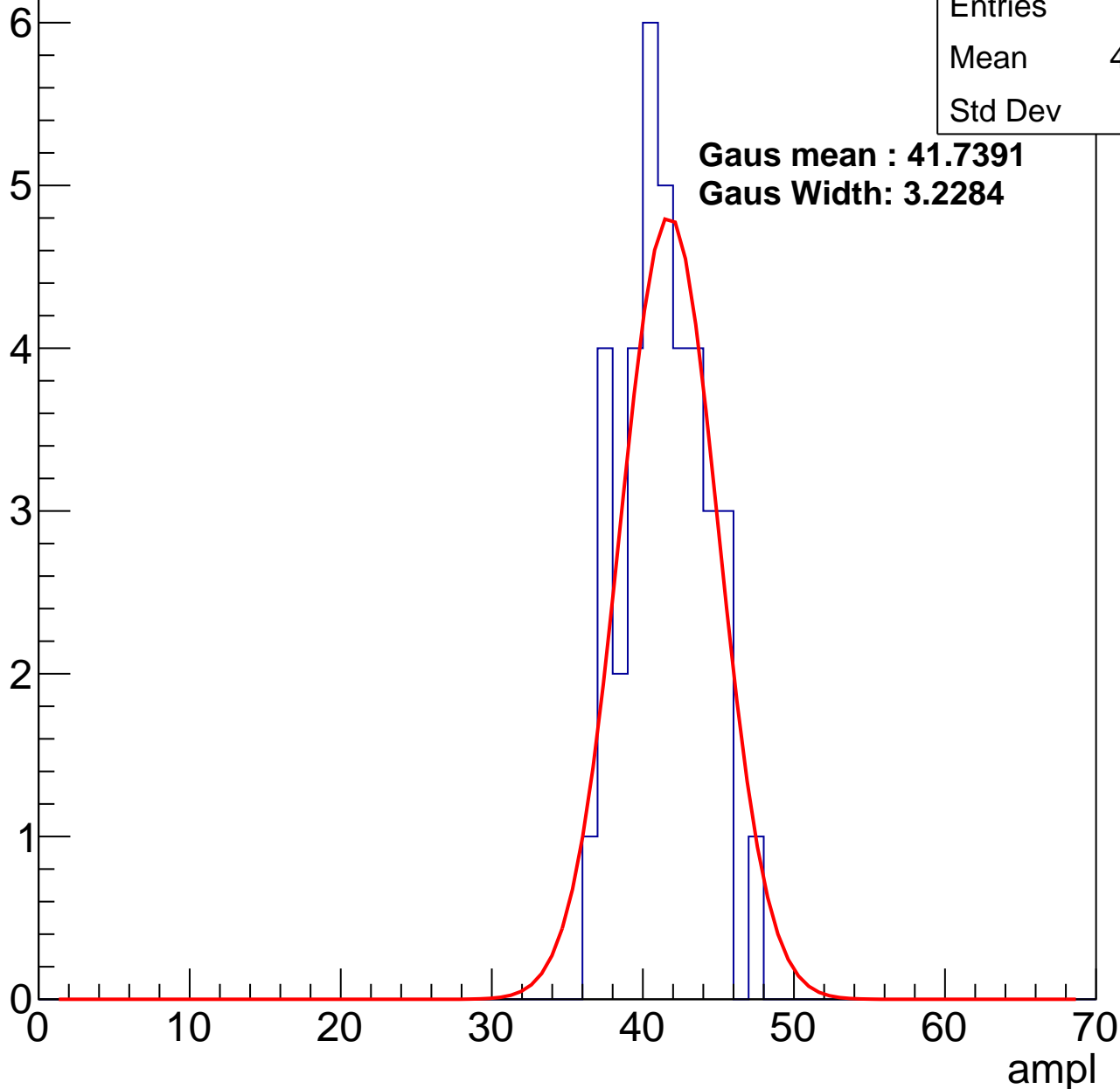
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	40.95
Std Dev	2.65

Gaus mean : 41.7391

Gaus Width: 3.2284



B1L102S, U14-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	92
Mean	47.3
Std Dev	3.945

Entry

10

8

6

4

2

0

0

10

20

30

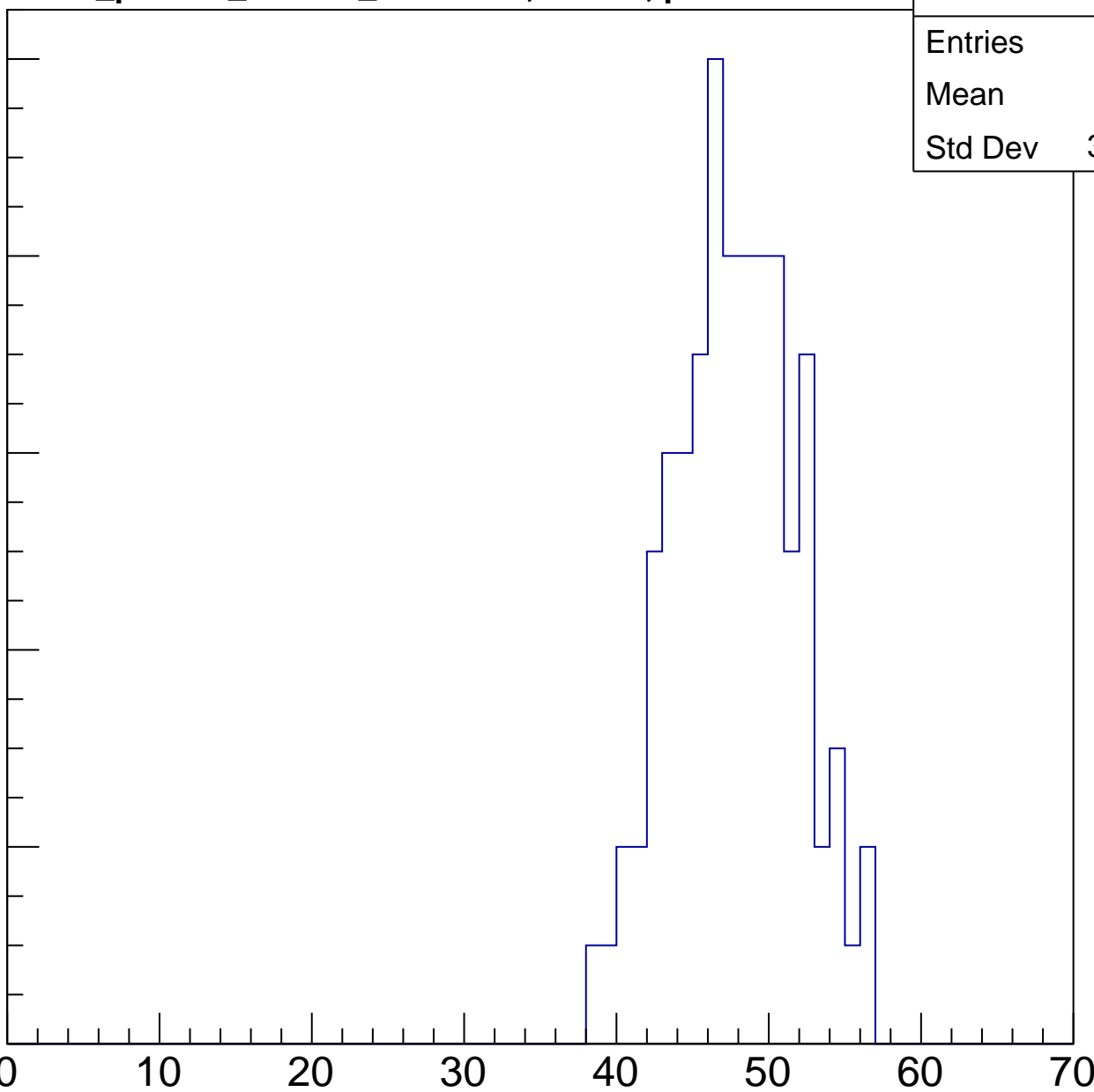
40

50

60

70

ampl

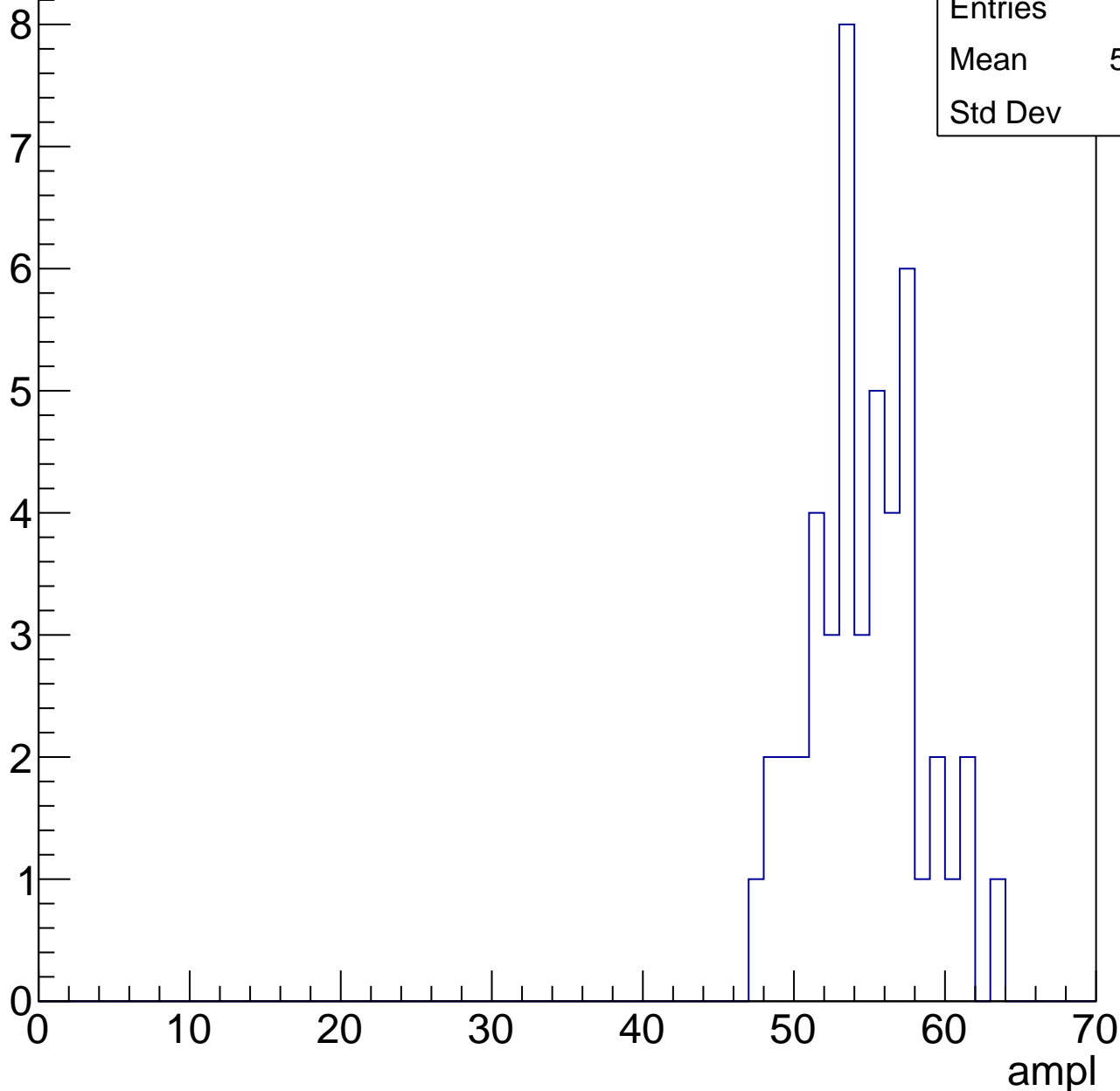


B1L102S, U14-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.23
Std Dev	3.61

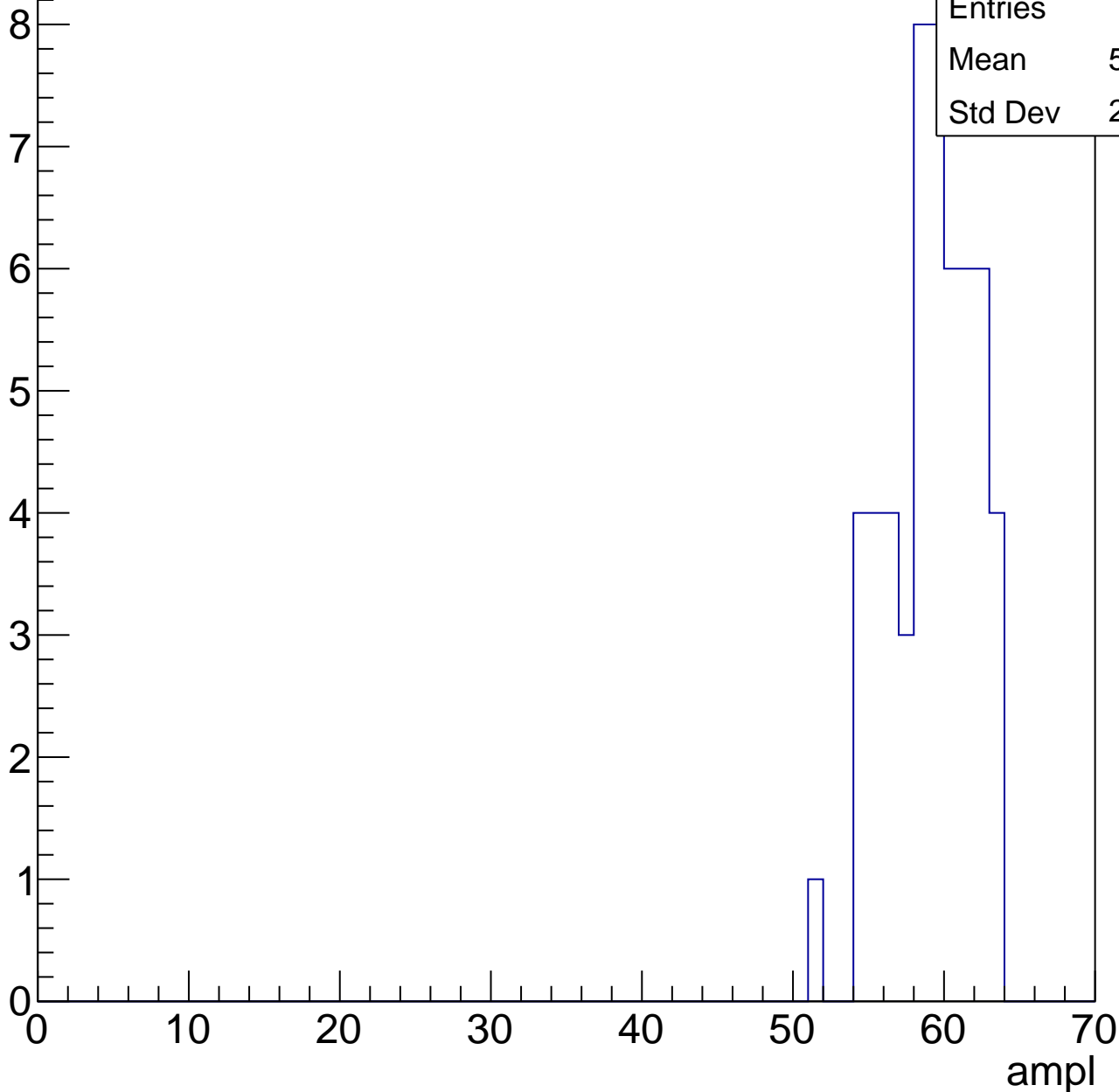


B1L102S, U14-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.67
Std Dev	2.809

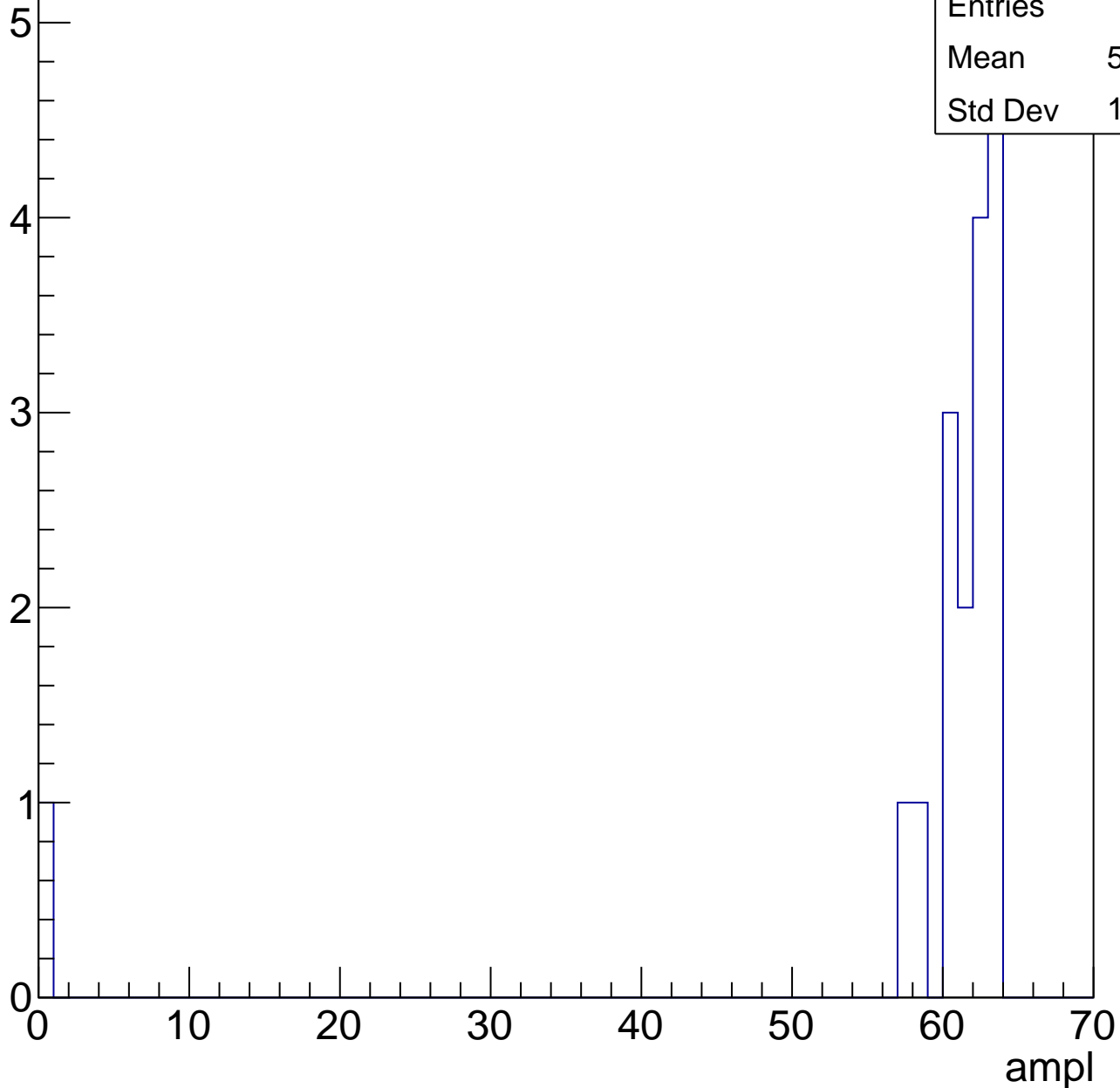


B1L102S, U14-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.65
Std Dev	14.52



B1L102S, U14-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch47, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	27.47
Std Dev	4.767

Gaus mean : 28.6675

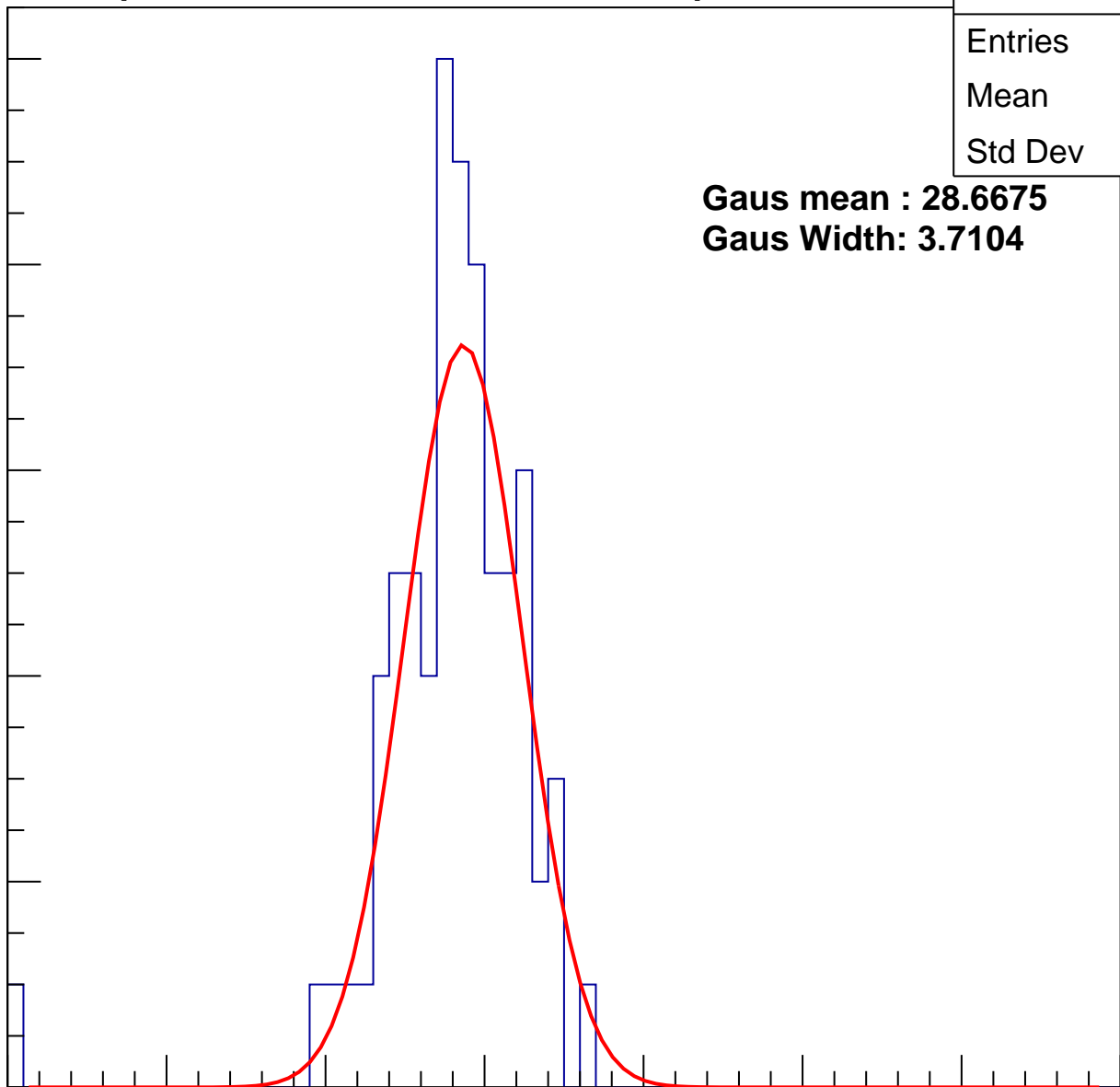
Gaus Width: 3.7104

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch47, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.25
Std Dev	3.707

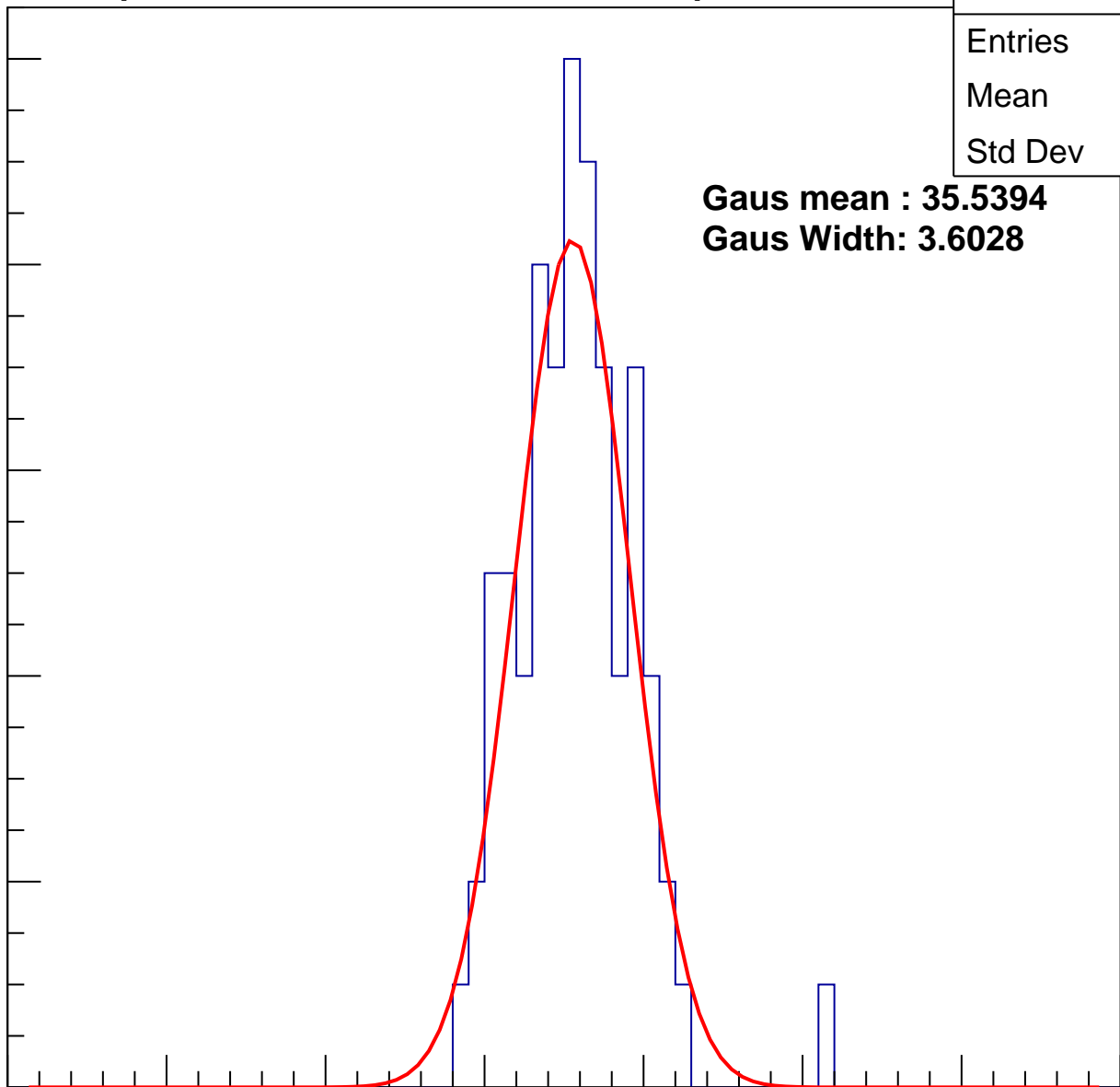
Gaus mean : 35.5394

Gaus Width: 3.6028

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch47, adc2

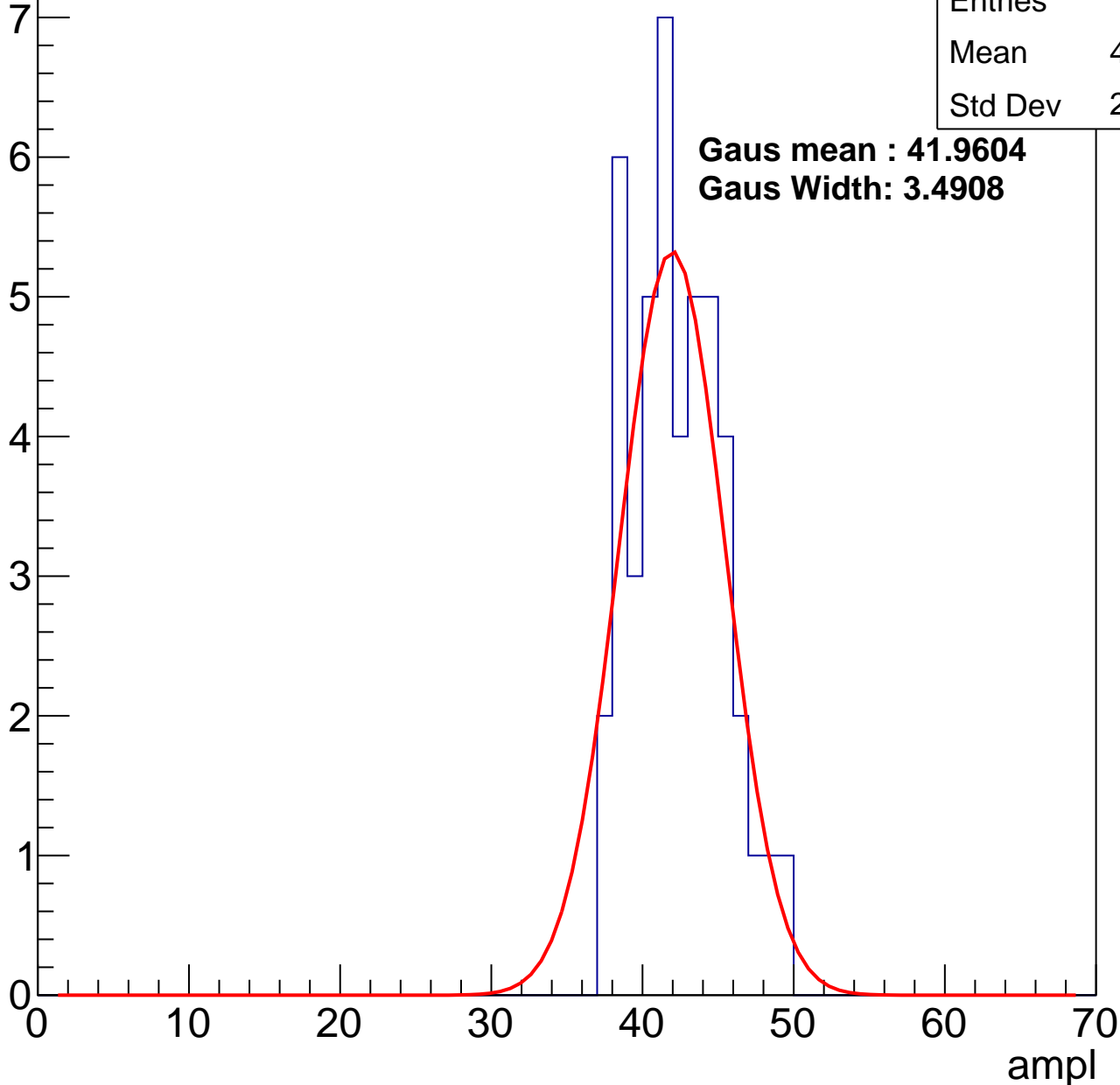
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	41.85
Std Dev	2.956

Gaus mean : 41.9604

Gaus Width: 3.4908

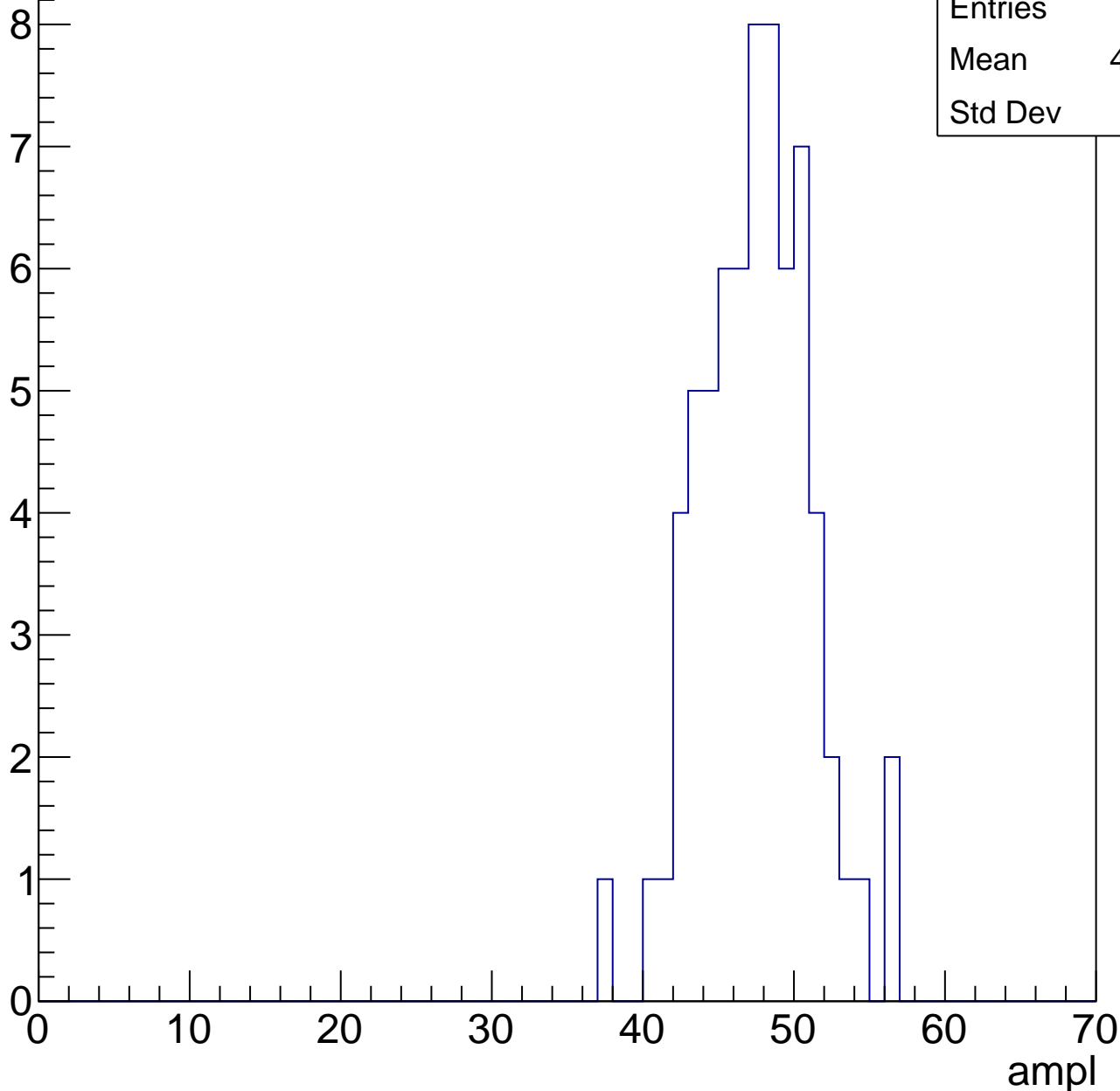


B1L102S, U14-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	47.03
Std Dev	3.63

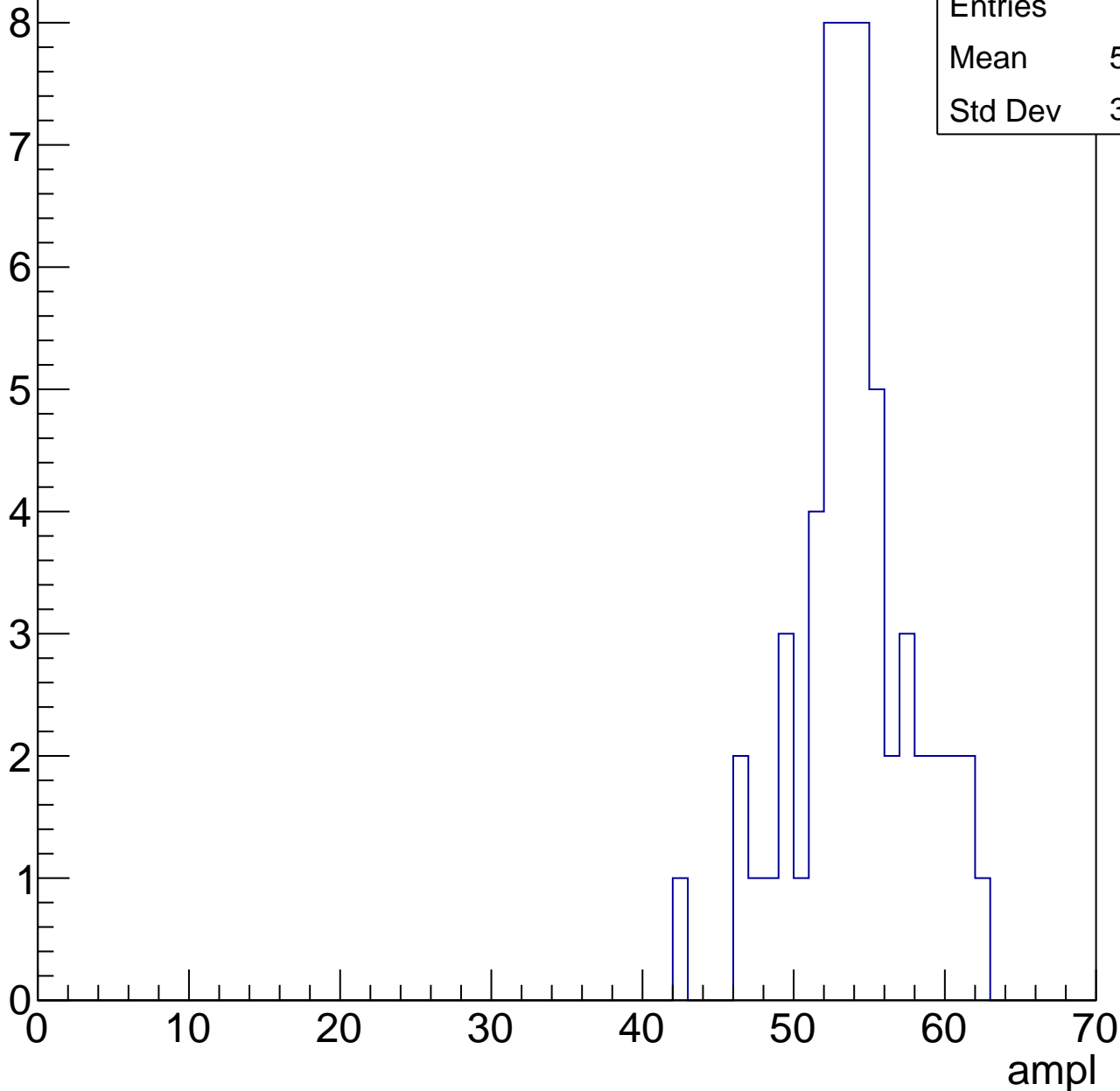


B1L102S, U14-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.54
Std Dev	3.937

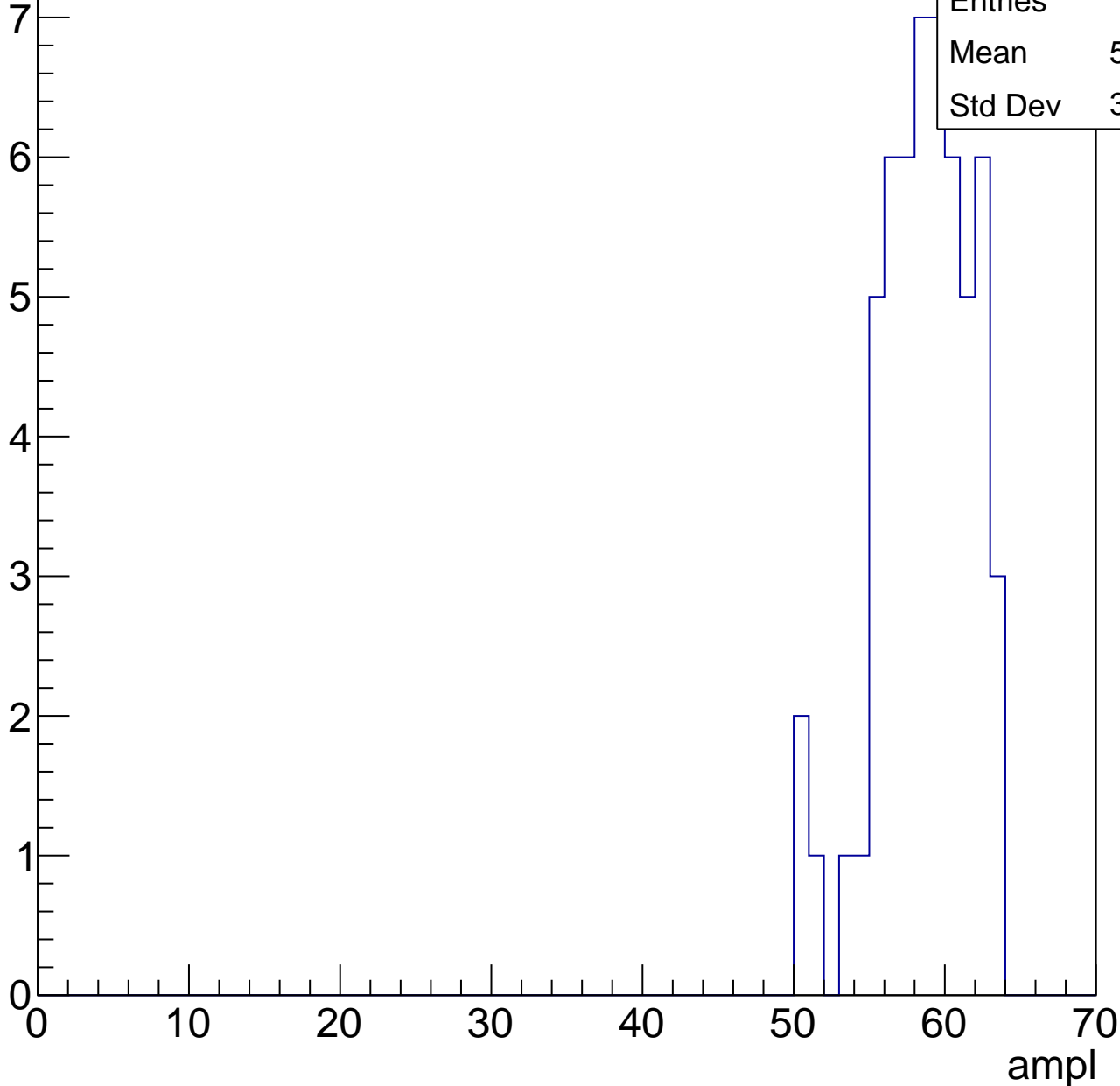


B1L102S, U14-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.14
Std Dev	3.102

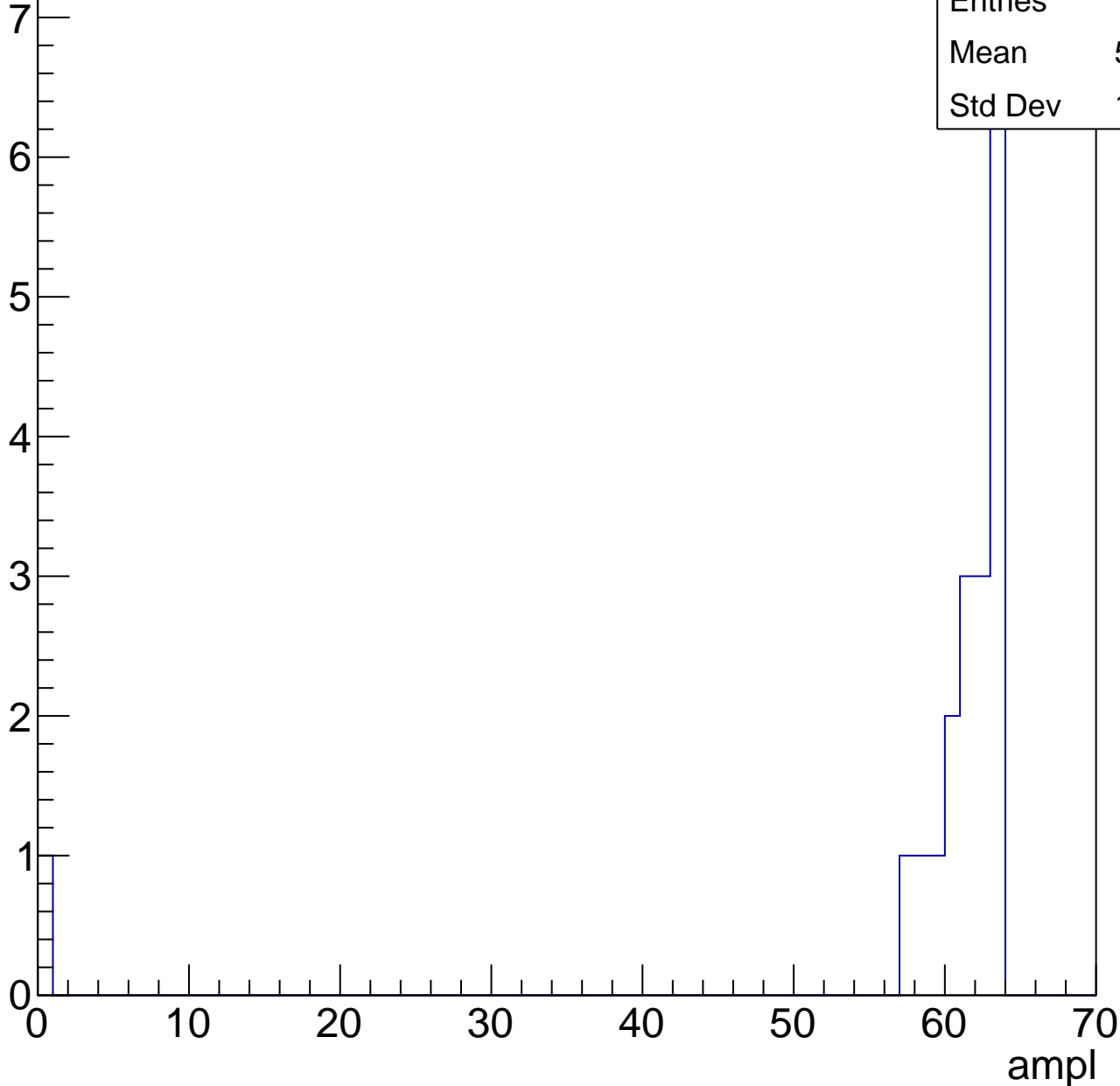


B1L102S, U14-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.11
Std Dev	13.81



B1L102S, U14-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch48, adc0

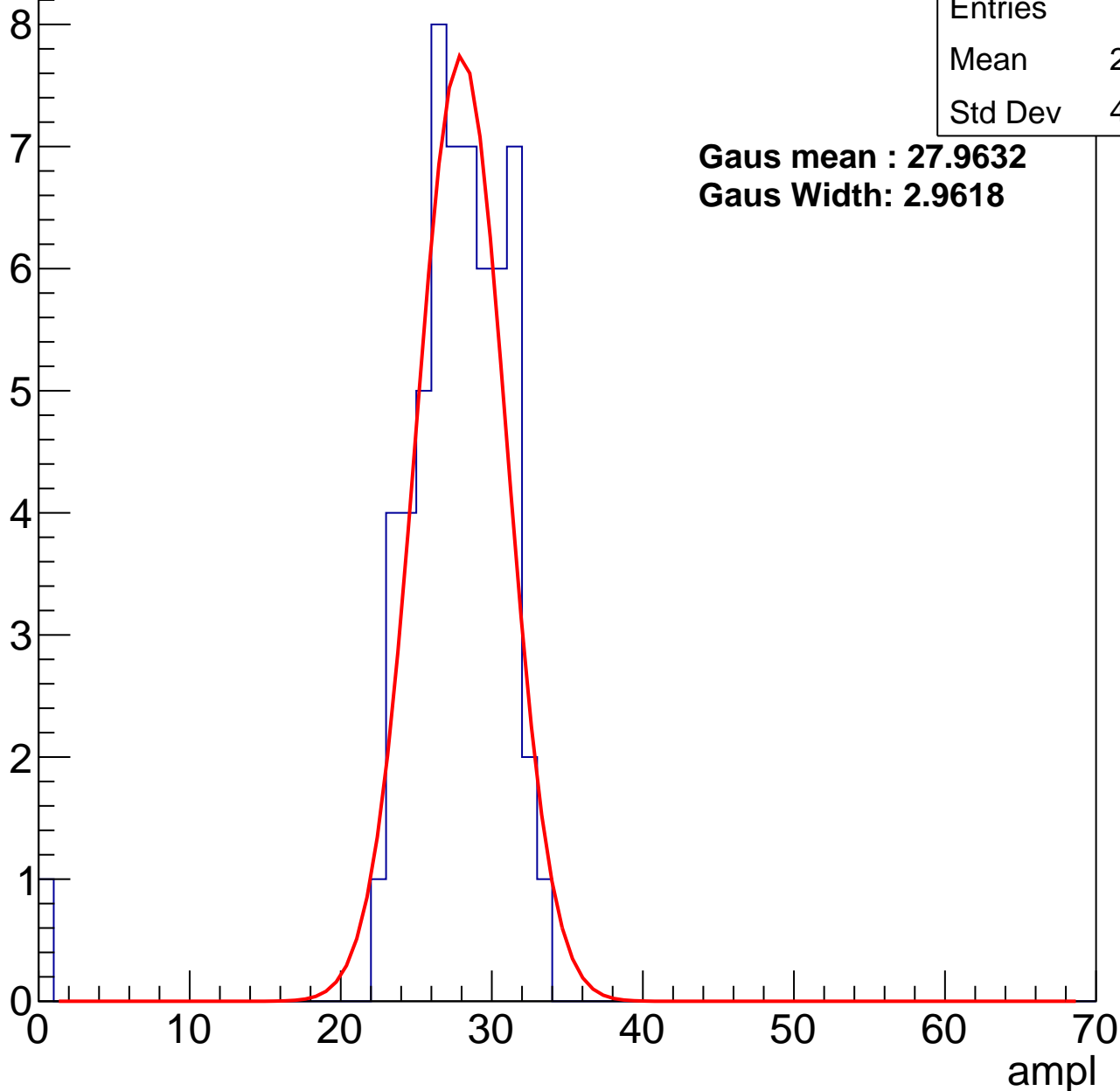
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.05
Std Dev	4.443

Gaus mean : 27.9632

Gaus Width: 2.9618



B1L102S, U14-ch48, adc1

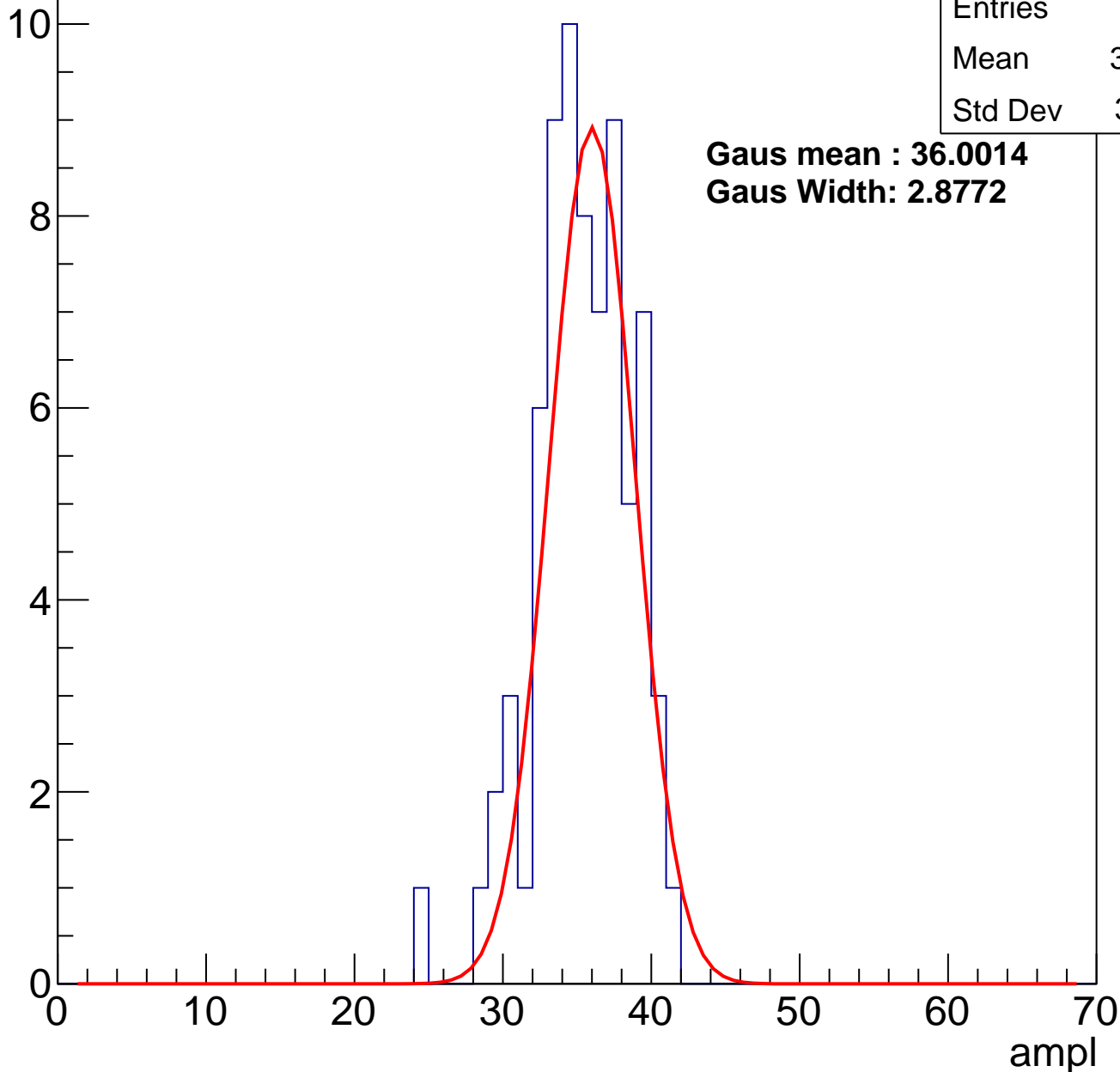
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	34.92
Std Dev	3.191

Gaus mean : 36.0014

Gaus Width: 2.8772

Entry



B1L102S, U14-ch48, adc2

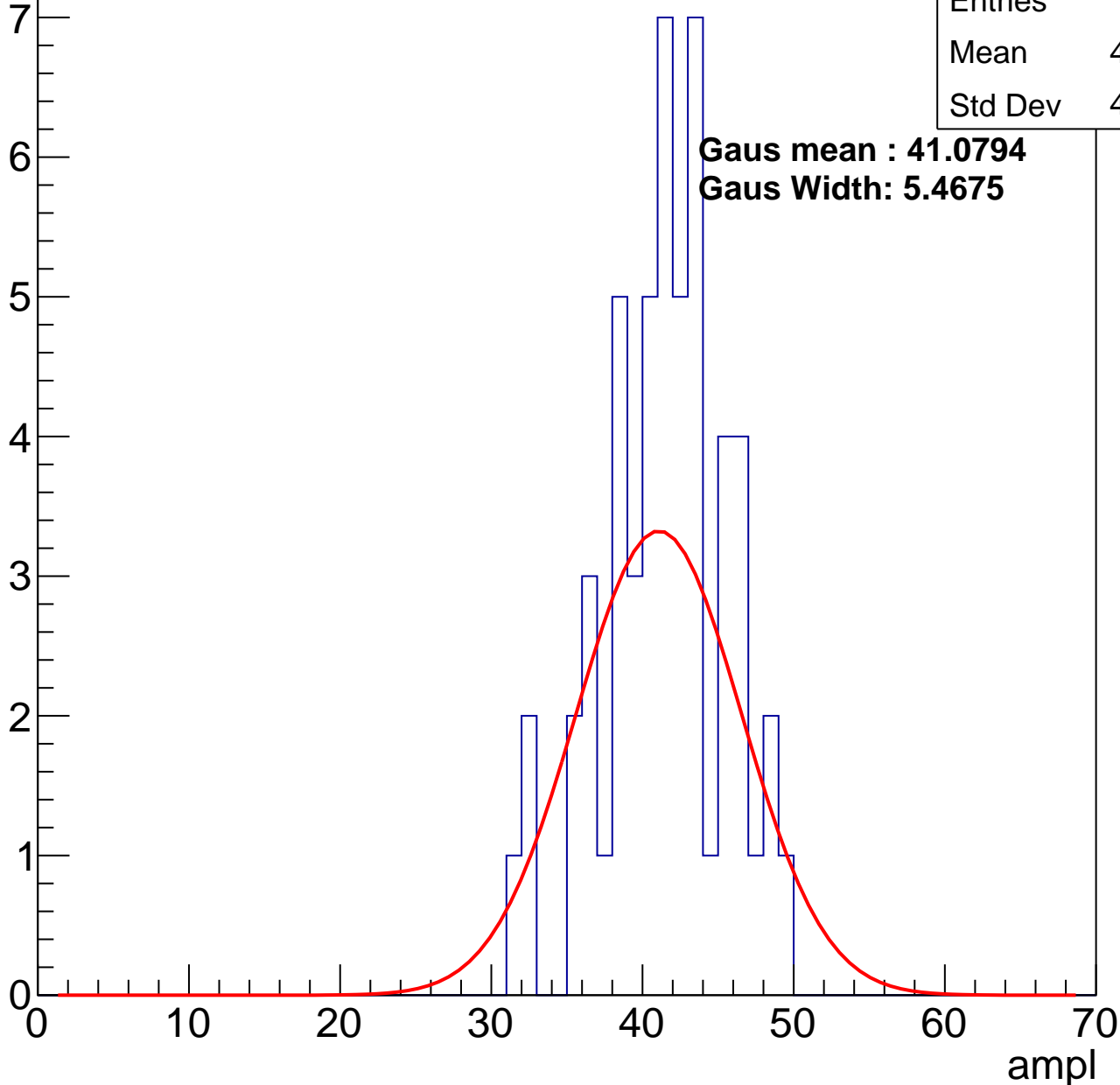
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.02
Std Dev	4.062

Gaus mean : 41.0794

Gaus Width: 5.4675

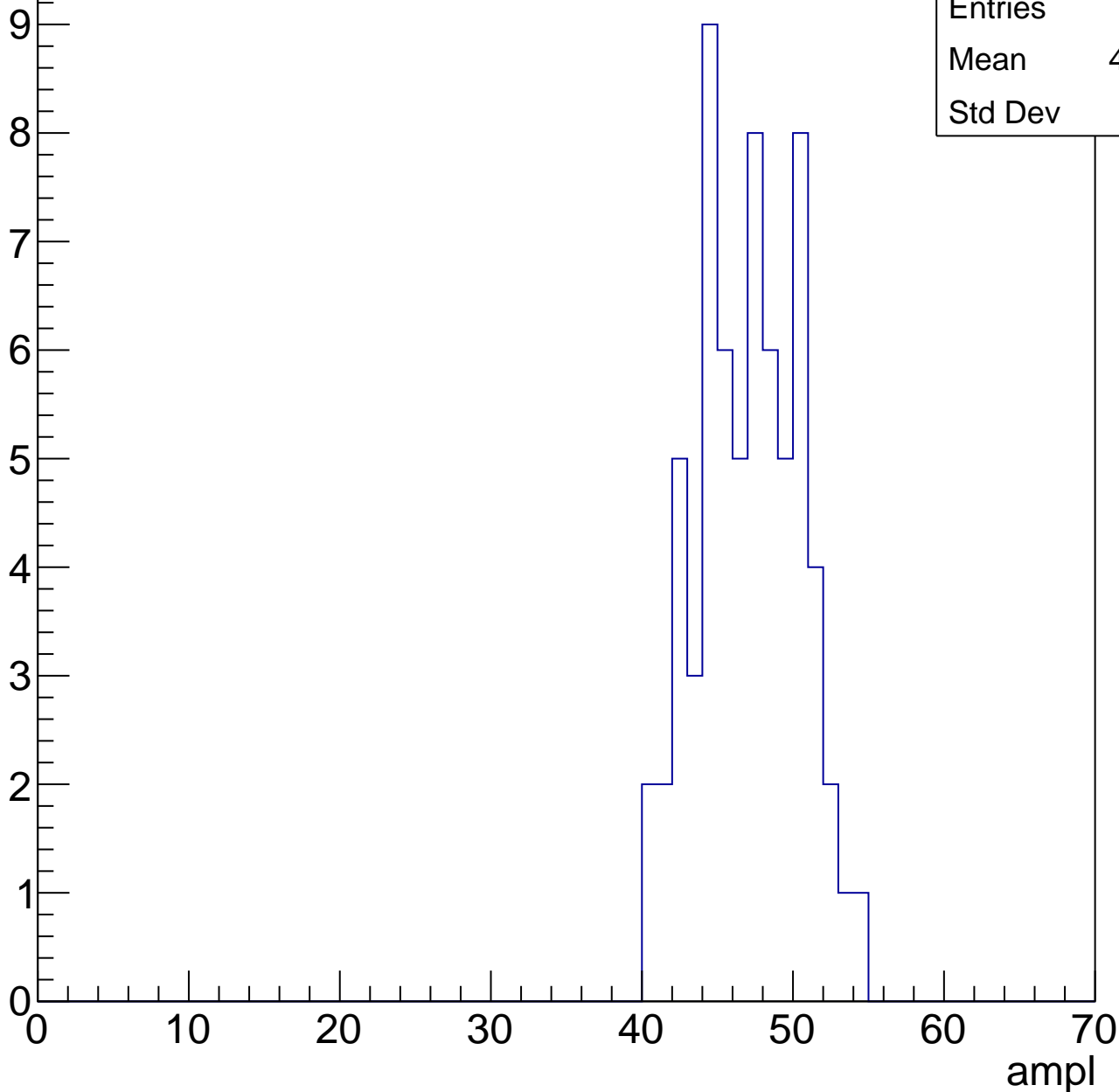


B1L102S, U14-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	46.58
Std Dev	3.32

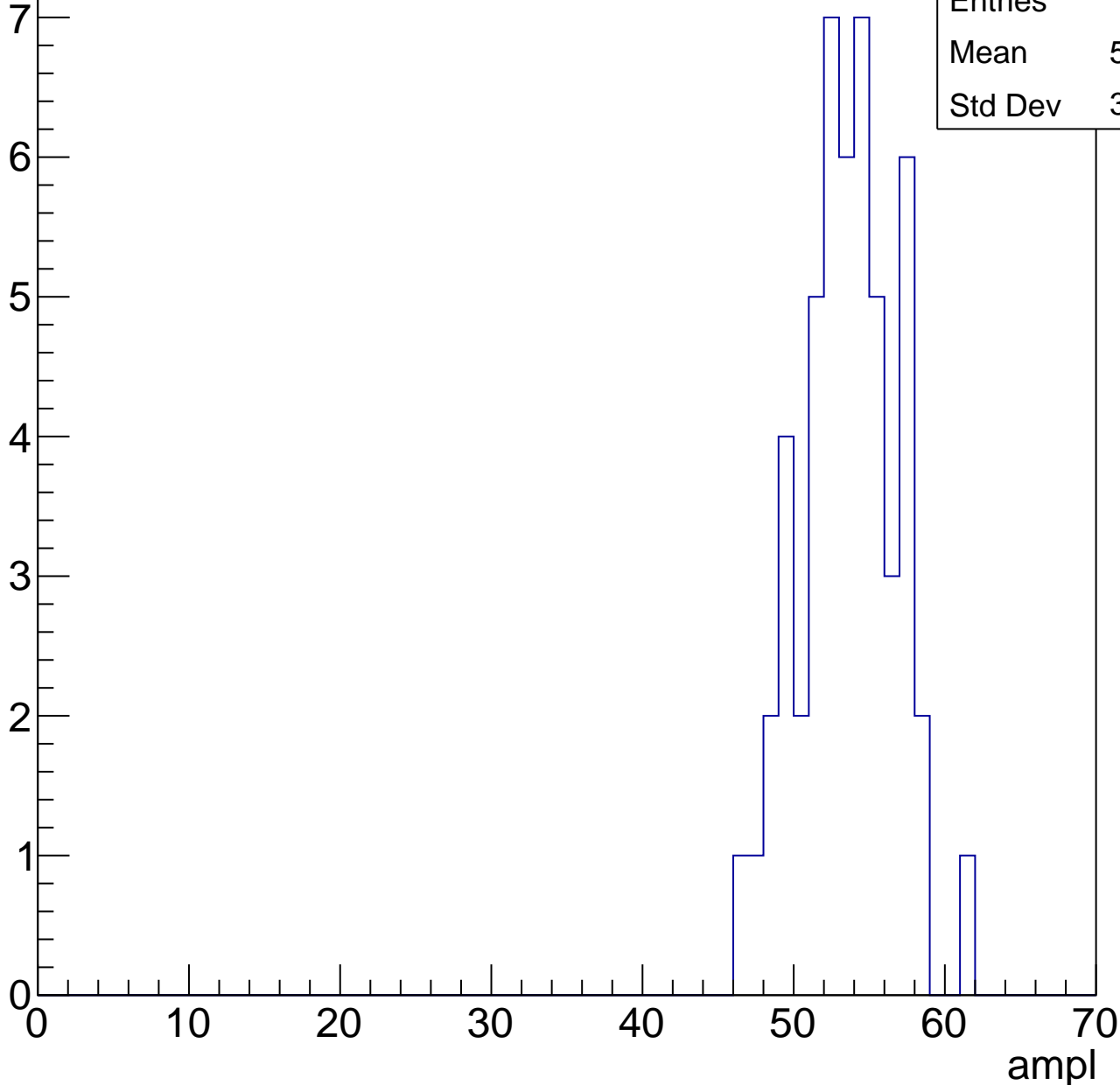


B1L102S, U14-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.12
Std Dev	3.123

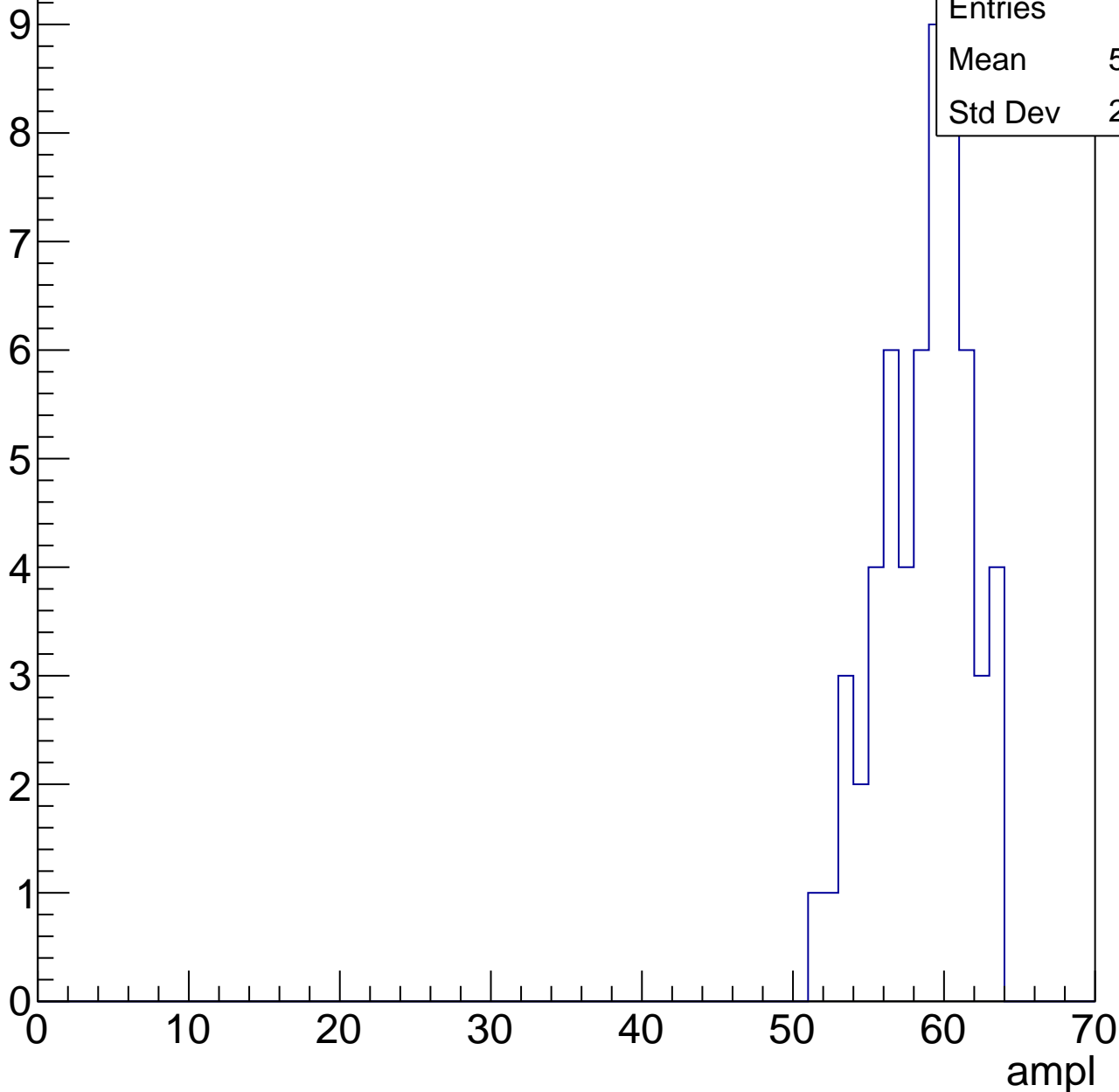


B1L102S, U14-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	58.19
Std Dev	2.952

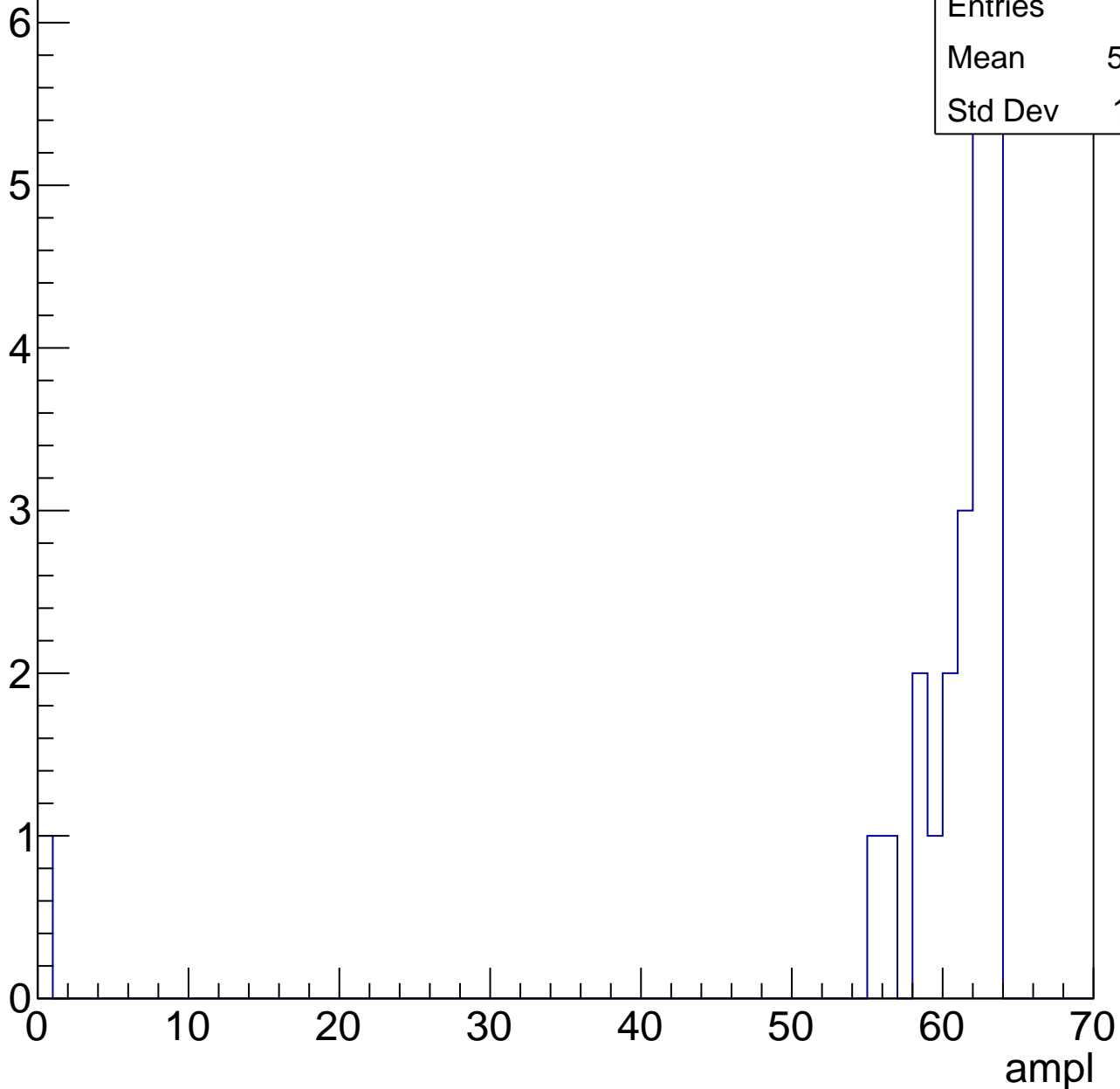


B1L102S, U14-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	58.22
Std Dev	12.61



B1L102S, U14-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch49, adc0

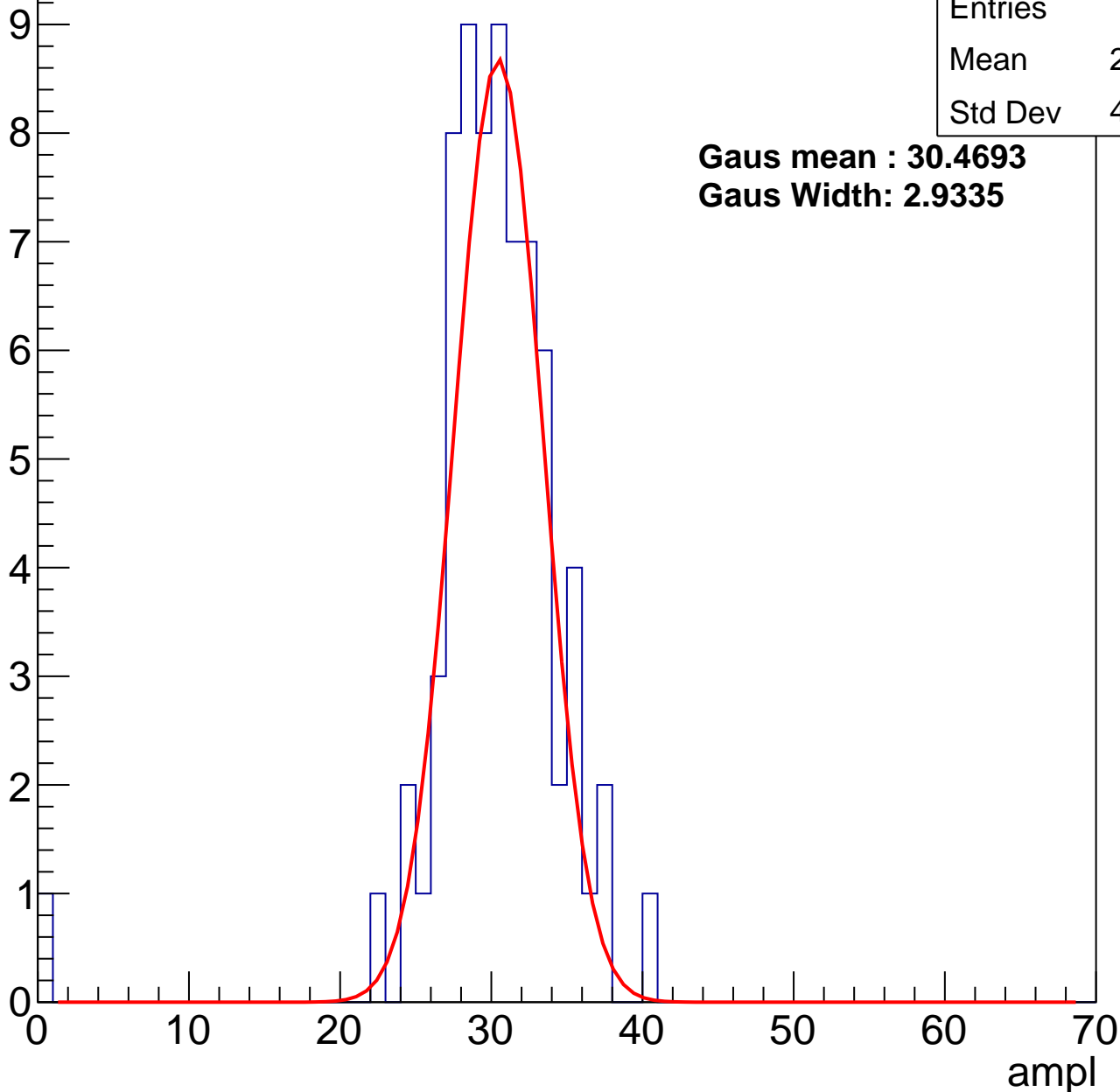
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.72
Std Dev	4.822

Gaus mean : 30.4693

Gaus Width: 2.9335



B1L102S, U14-ch49, adc1

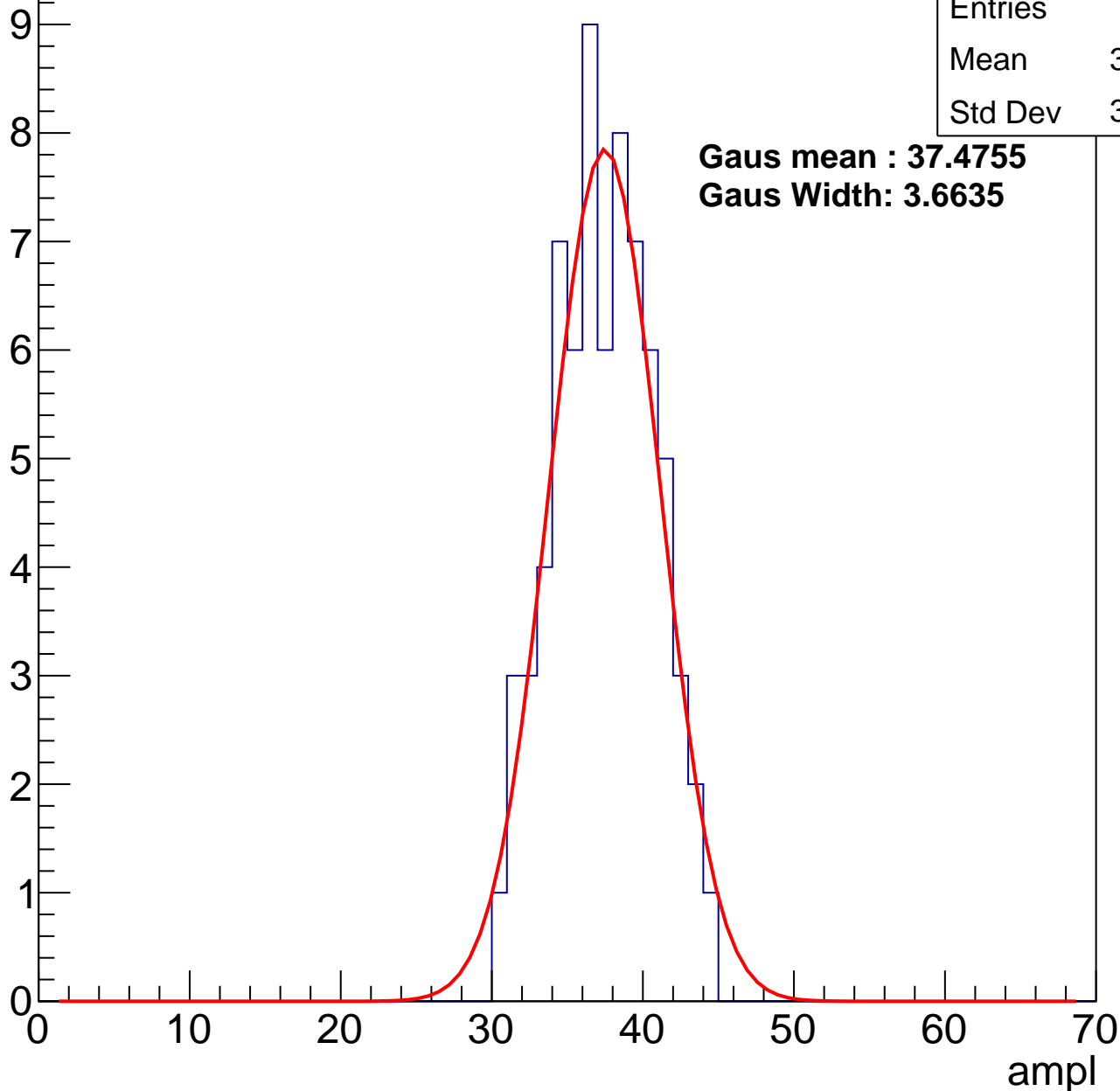
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.94
Std Dev	3.267

Gaus mean : 37.4755

Gaus Width: 3.6635



B1L102S, U14-ch49, adc2

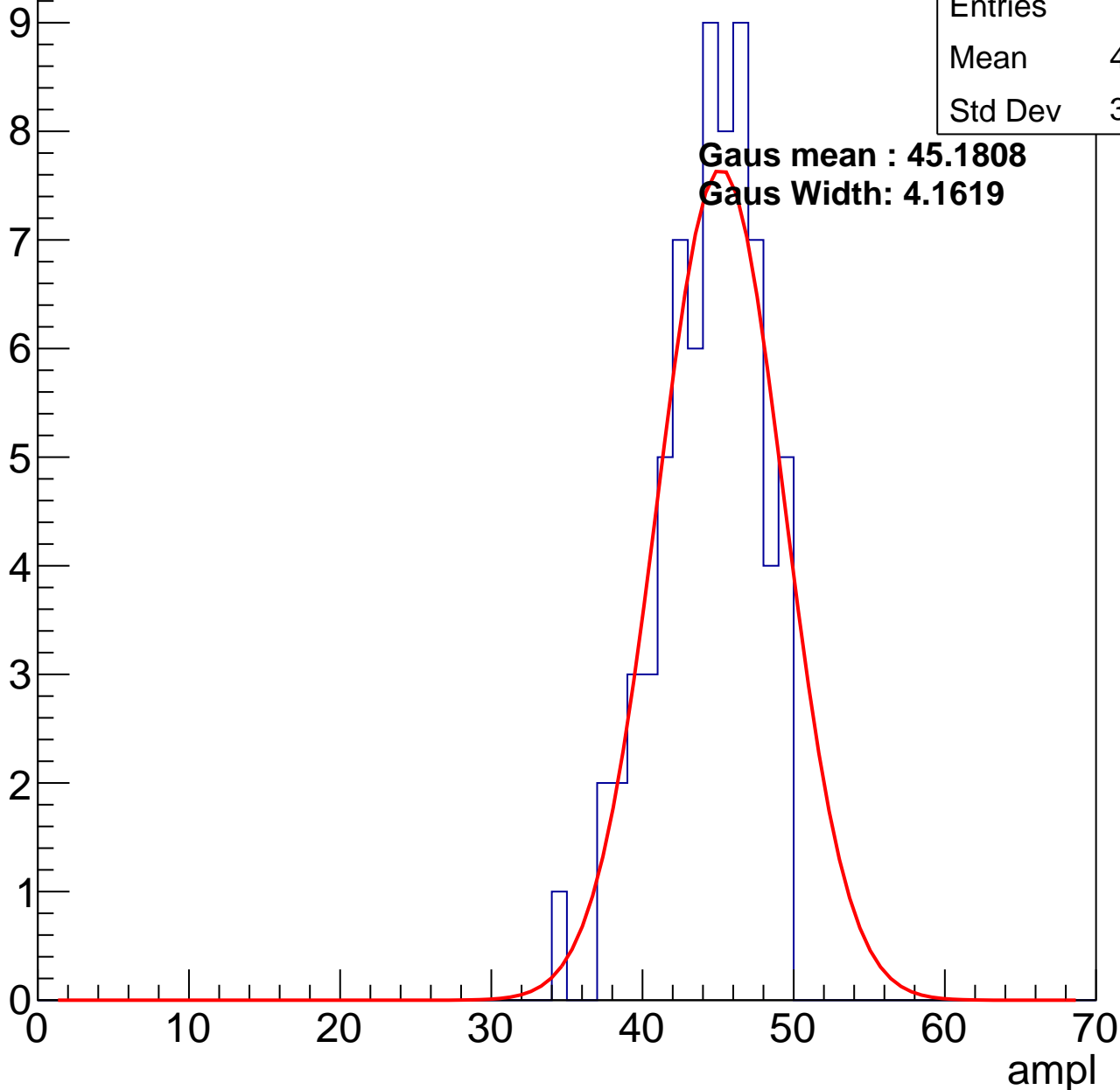
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.86
Std Dev	3.286

Gaus mean : 45.1808

Gaus Width: 4.1619

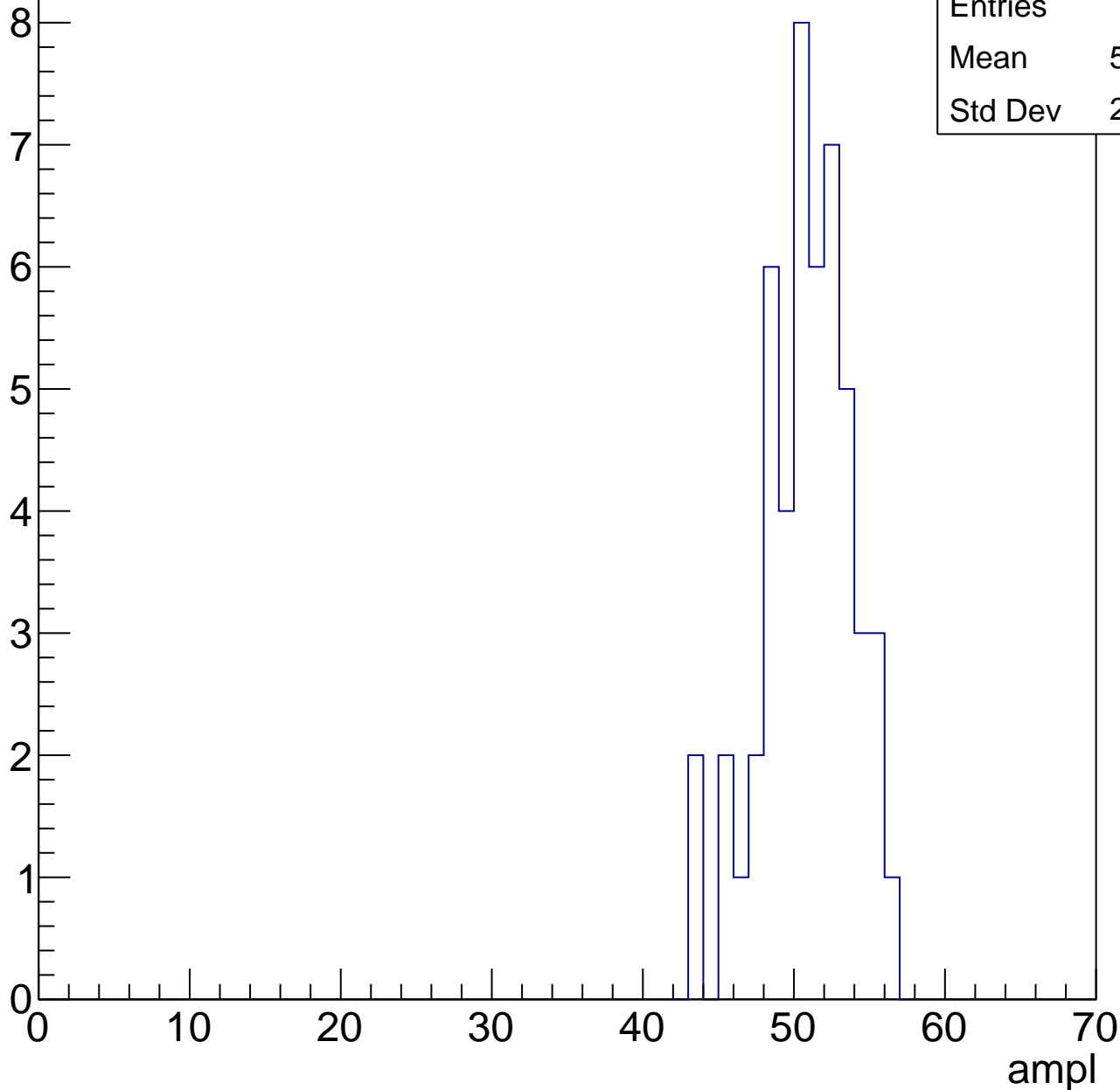


B1L102S, U14-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	50.36
Std Dev	2.972

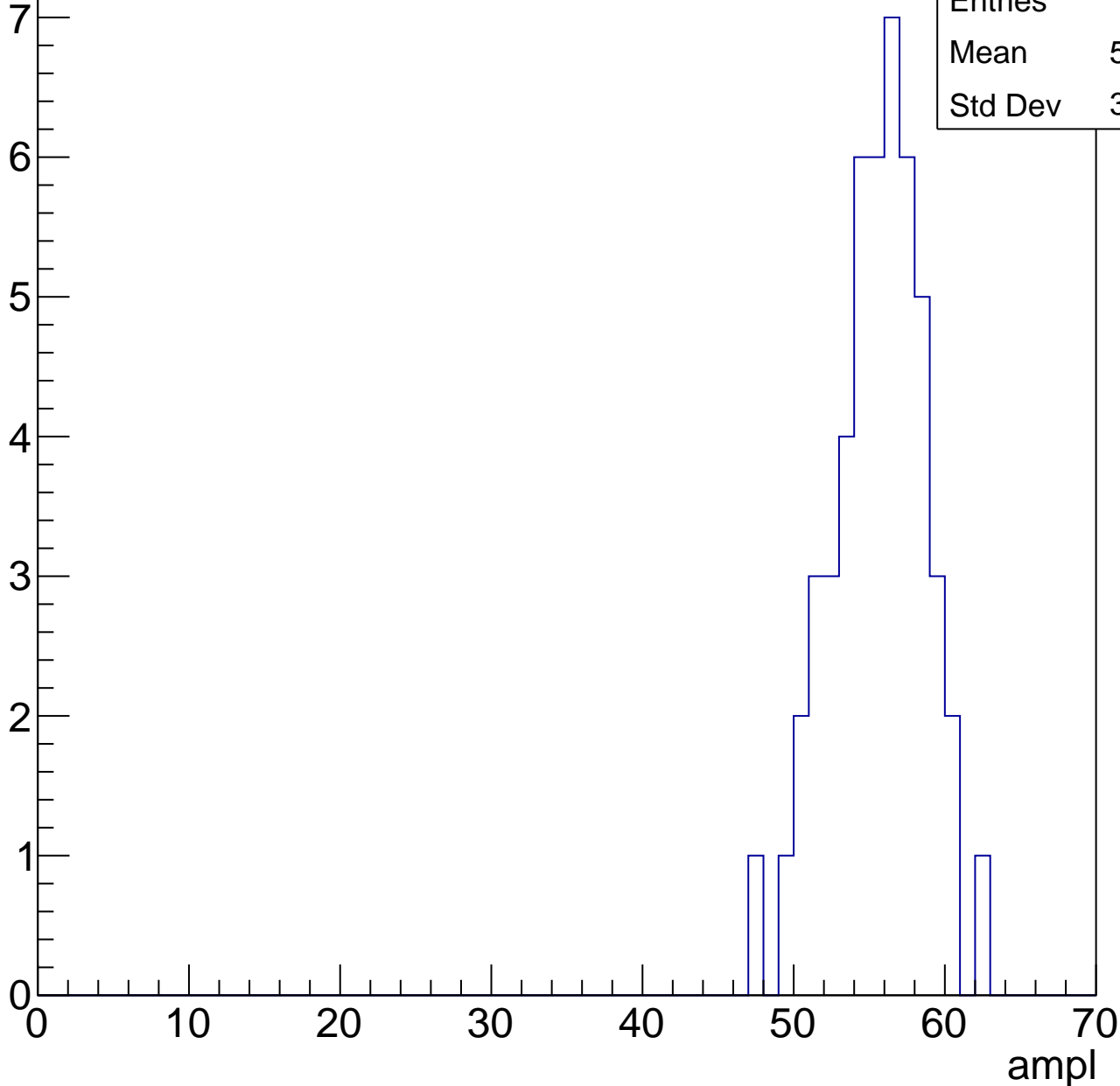


B1L102S, U14-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

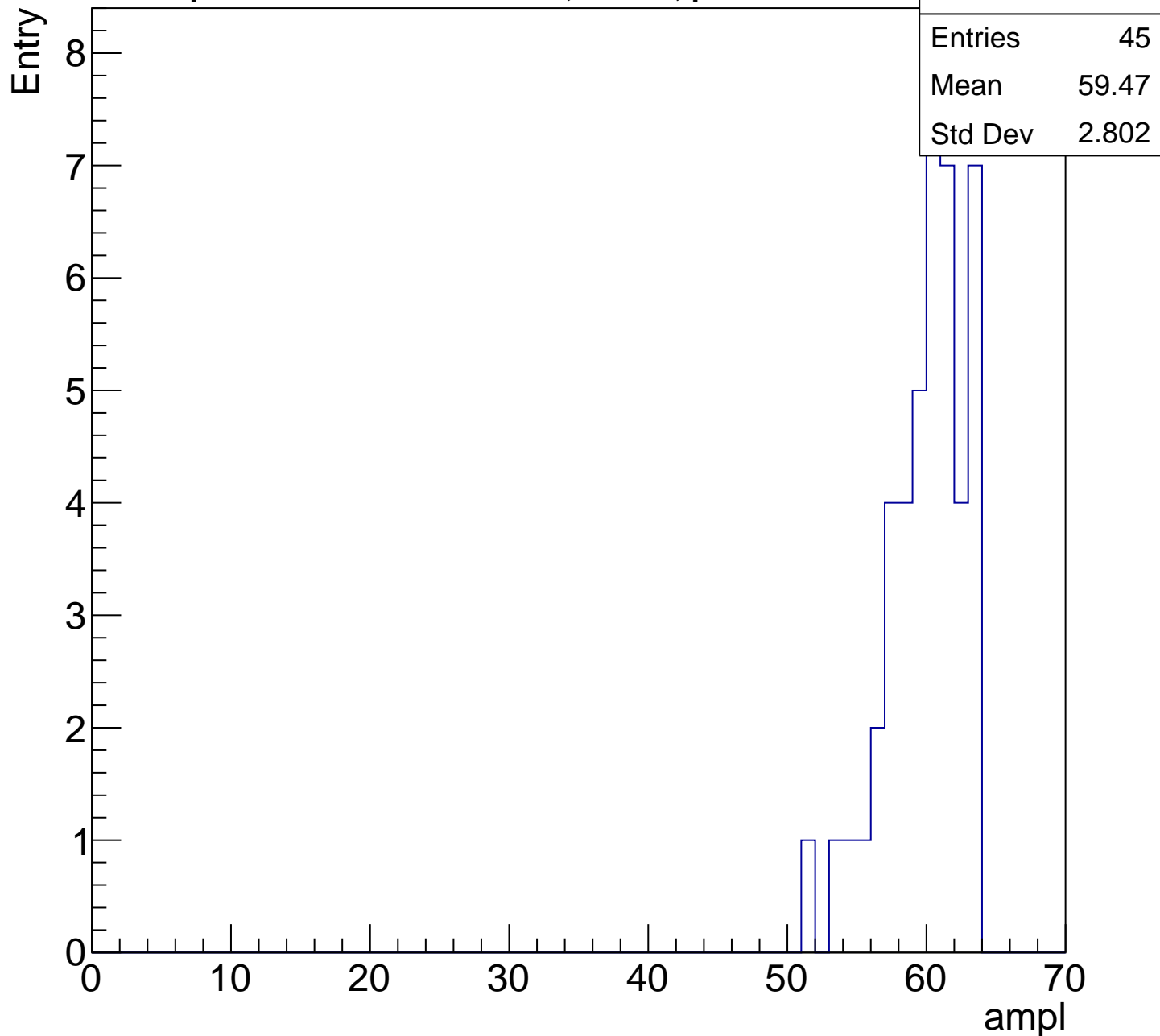
Entry

Entries	50
Mean	55.08
Std Dev	3.065



B1L102S, U14-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

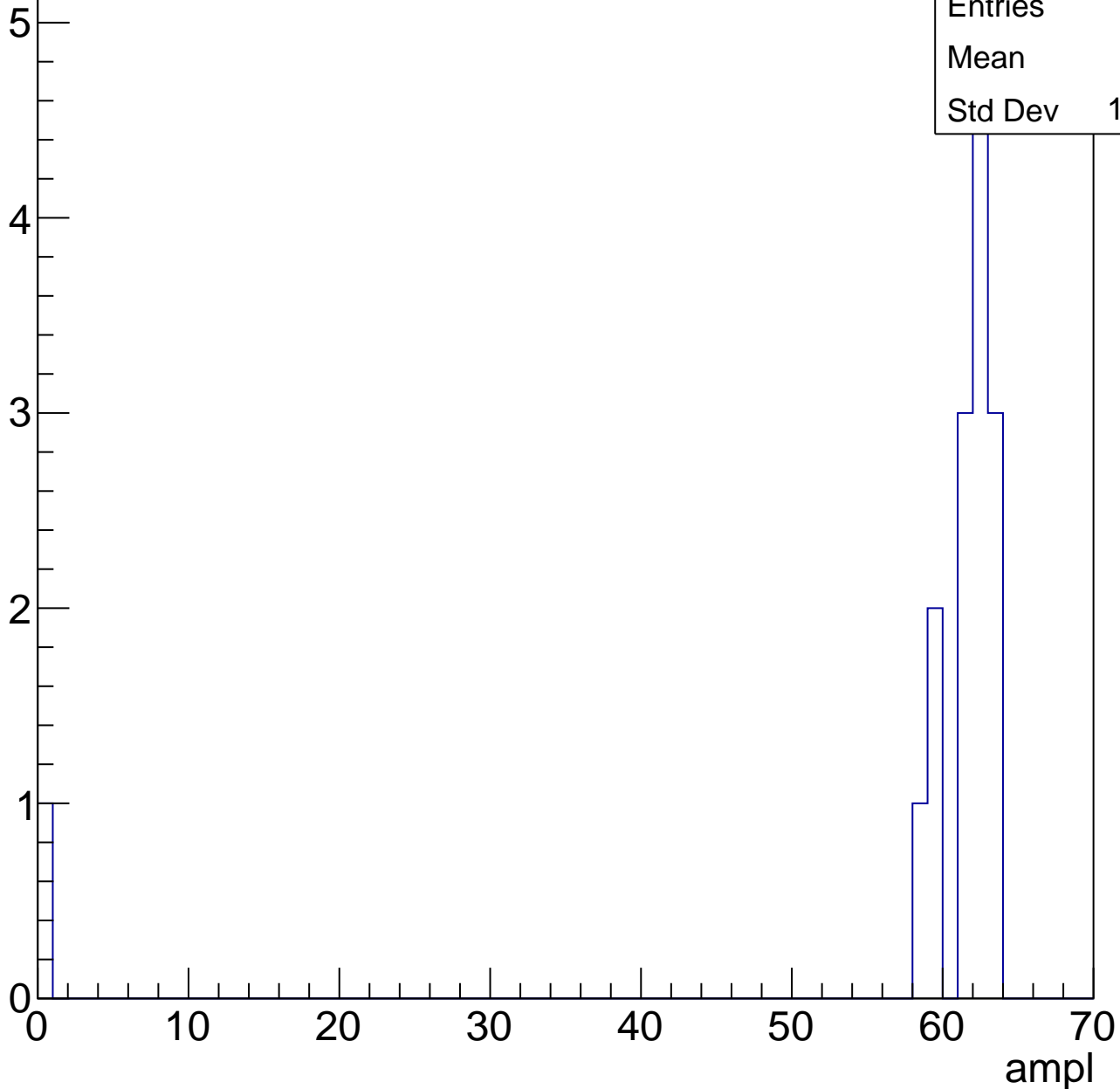


B1L102S, U14-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.2
Std Dev	15.36



B1L102S, U14-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch50, adc0

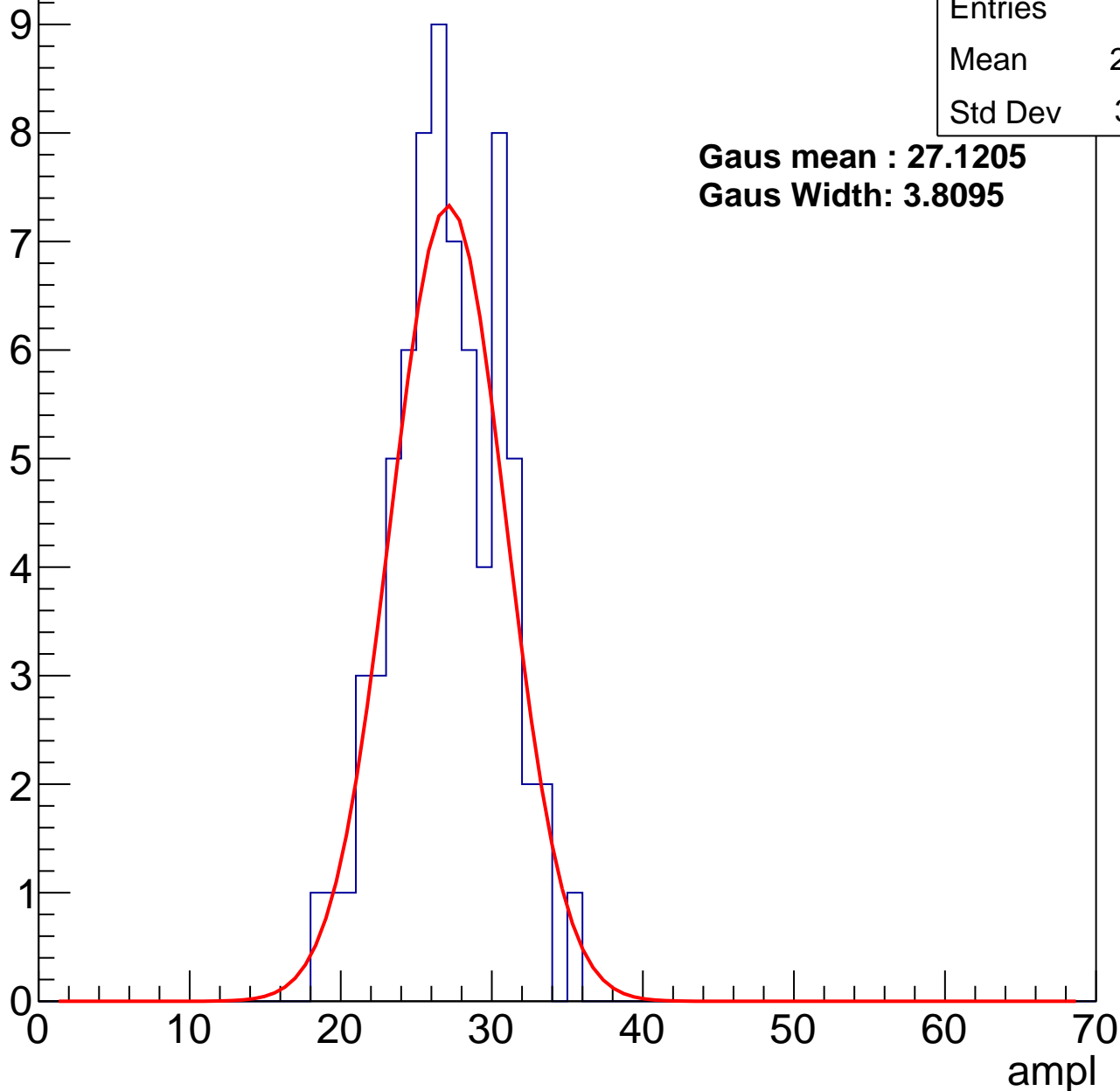
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	26.56
Std Dev	3.531

Gaus mean : 27.1205

Gaus Width: 3.8095



B1L102S, U14-ch50, adc1

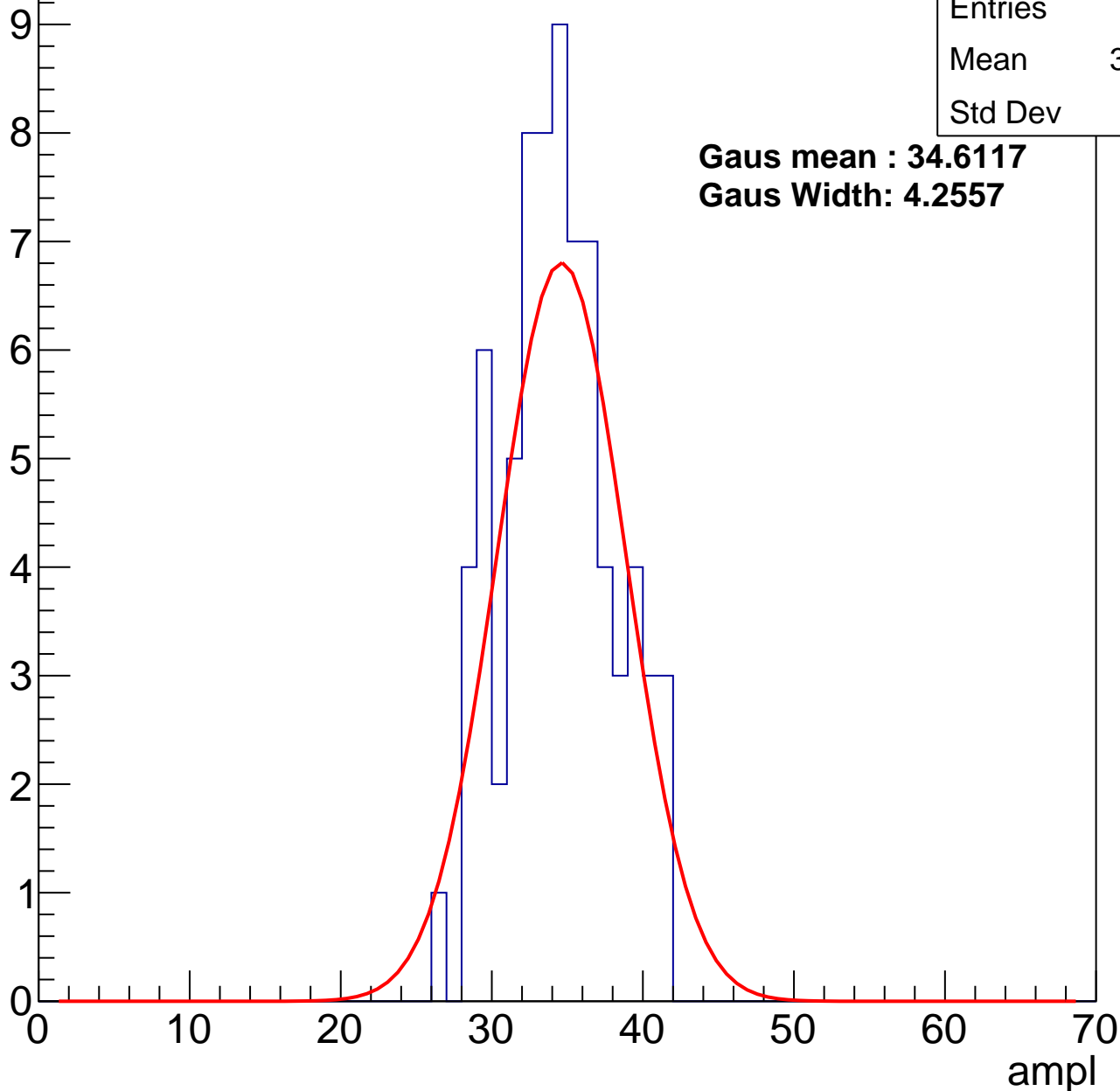
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	33.93
Std Dev	3.58

Gaus mean : 34.6117

Gaus Width: 4.2557



B1L102S, U14-ch50, adc2

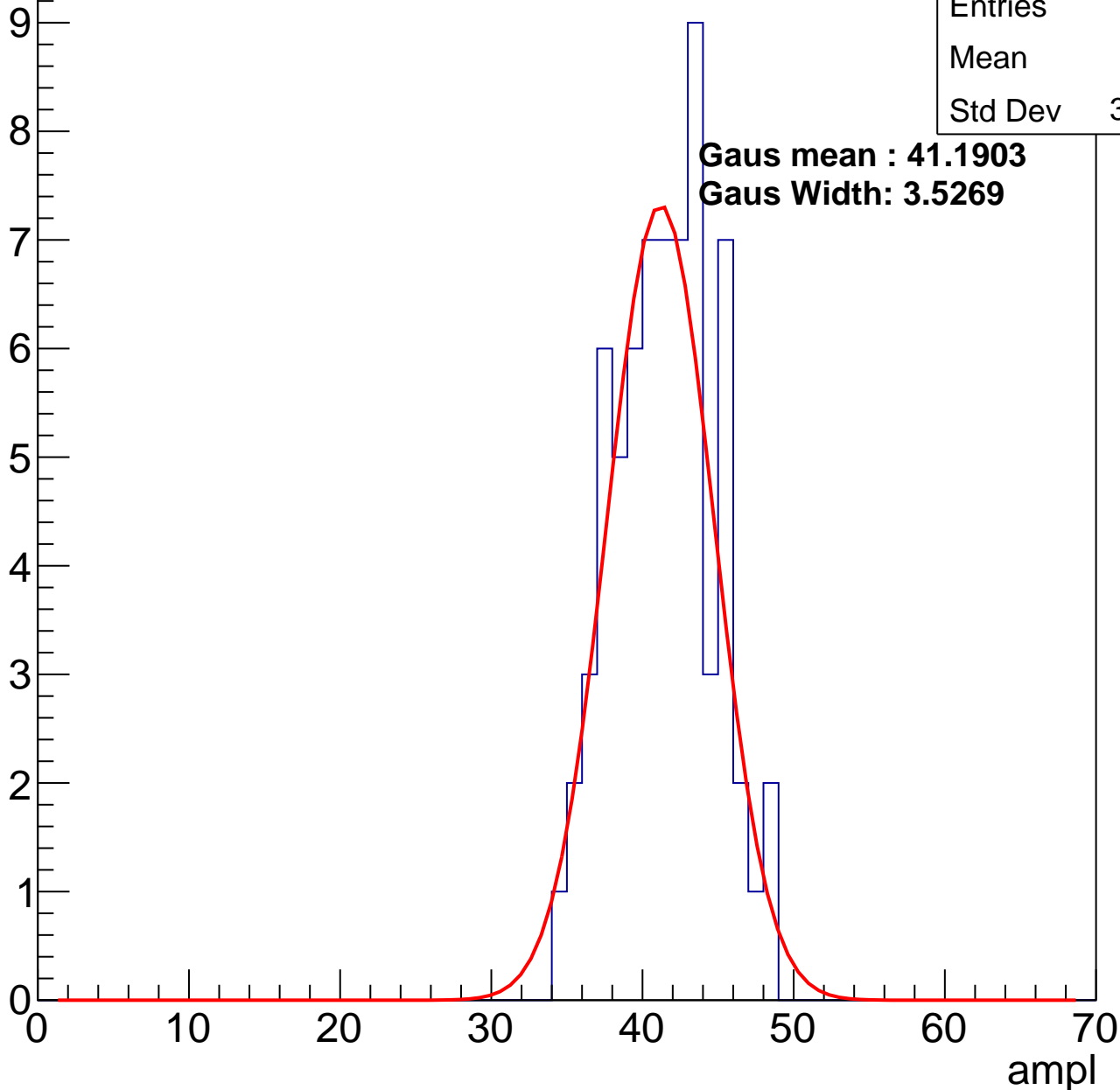
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41
Std Dev	3.285

Gaus mean : 41.1903

Gaus Width: 3.5269

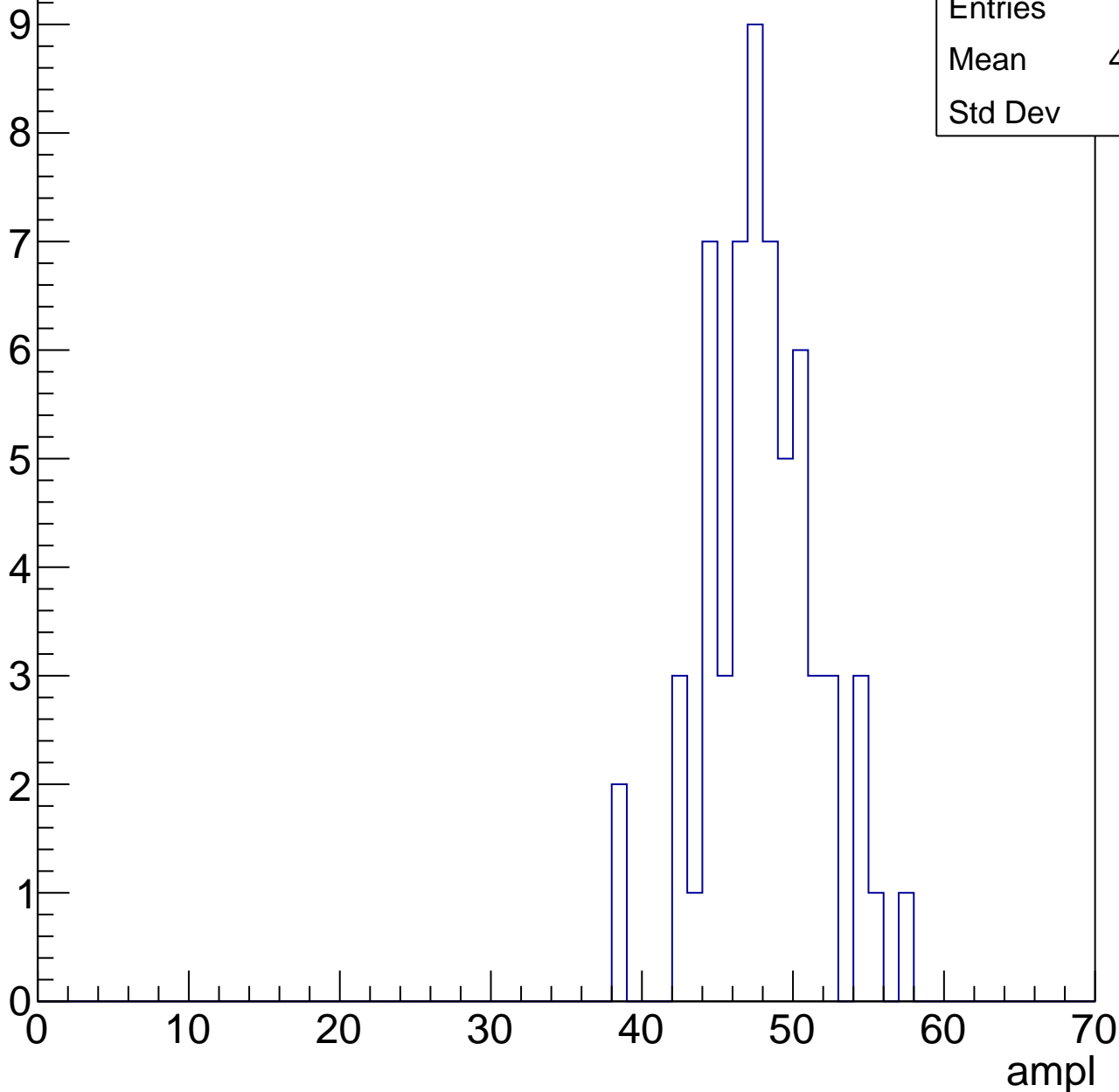


B1L102S, U14-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.49
Std Dev	3.7

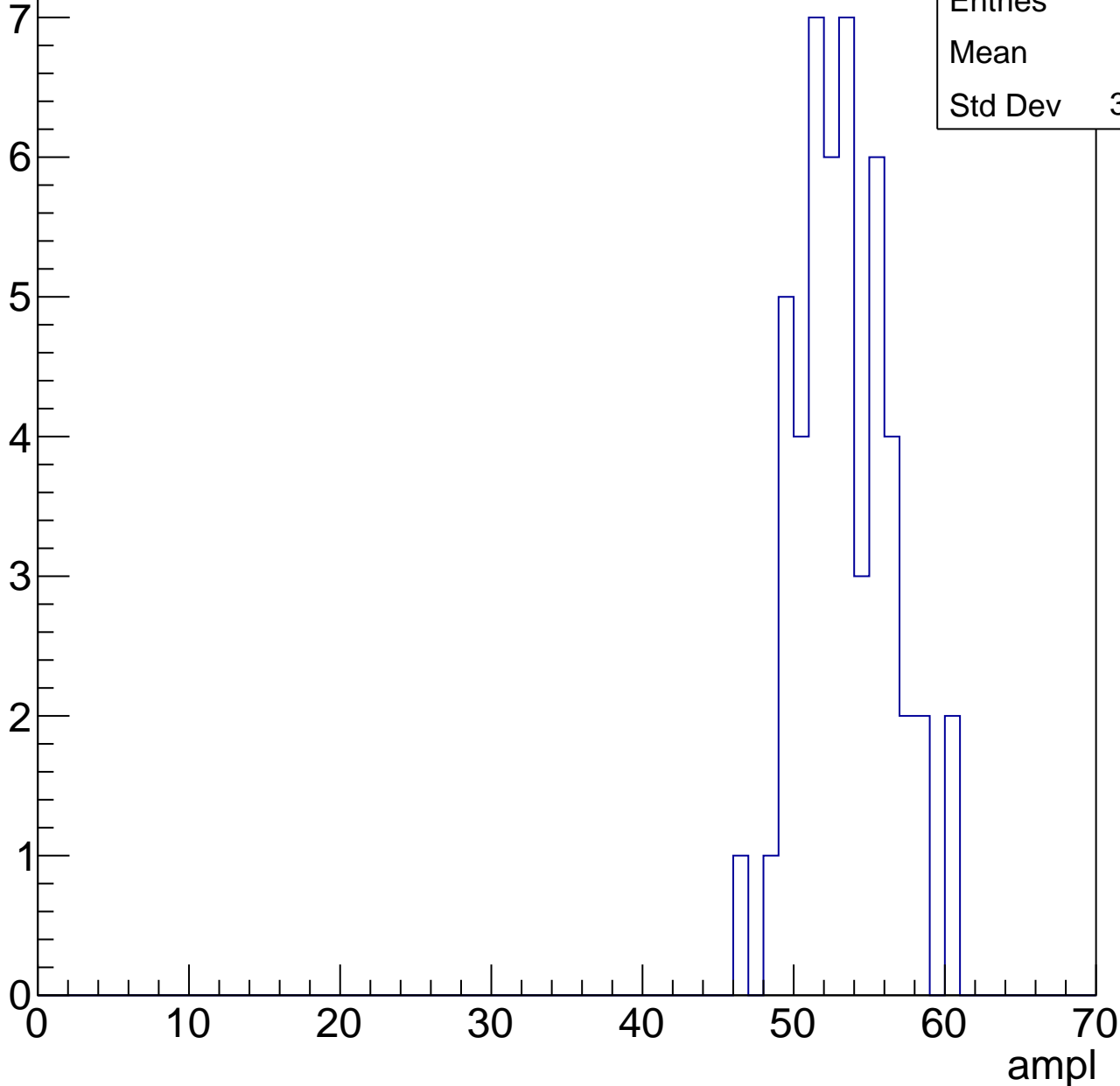


B1L102S, U14-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

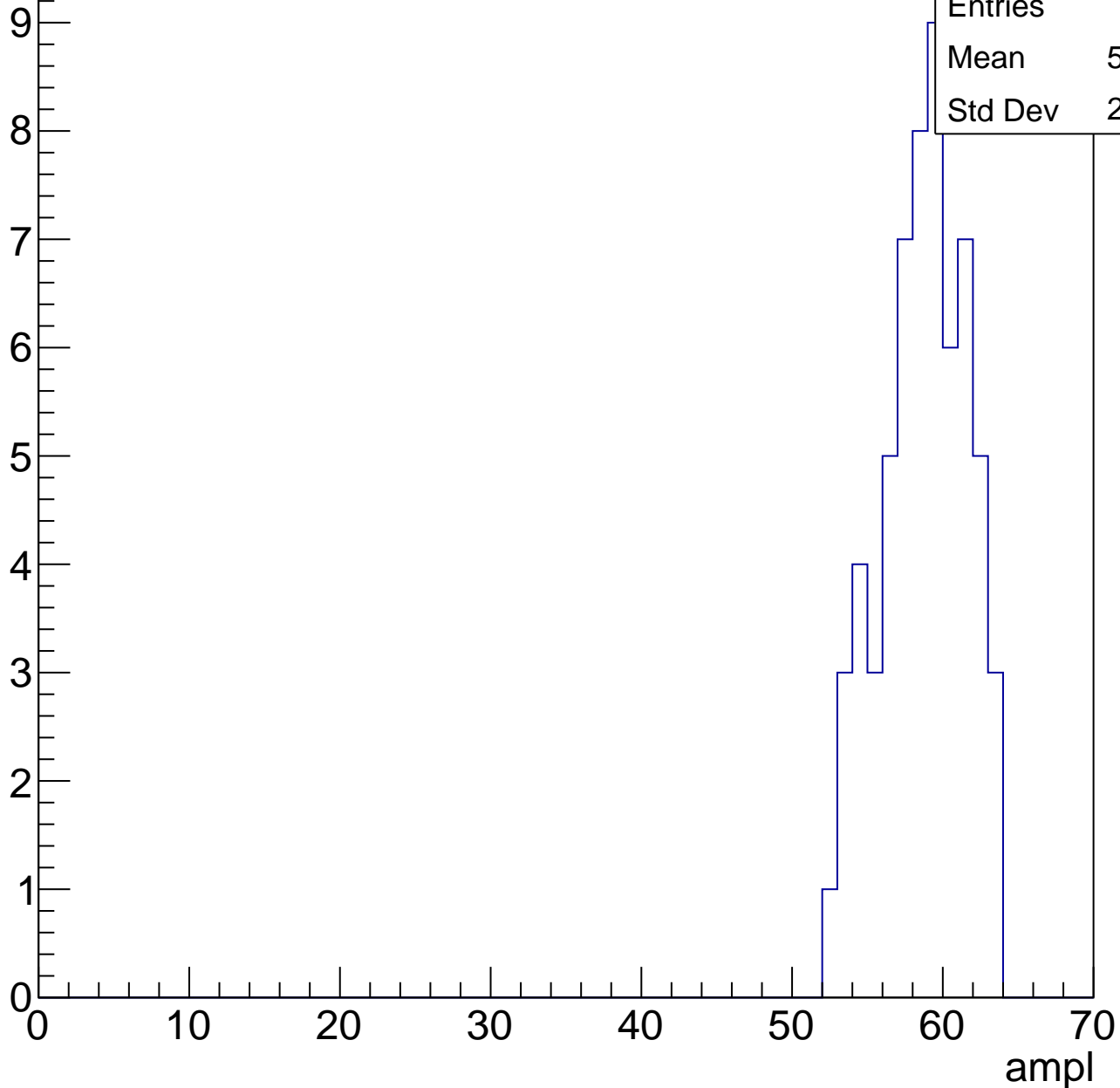
Entries	50
Mean	52.9
Std Dev	3.055



B1L102S, U14-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

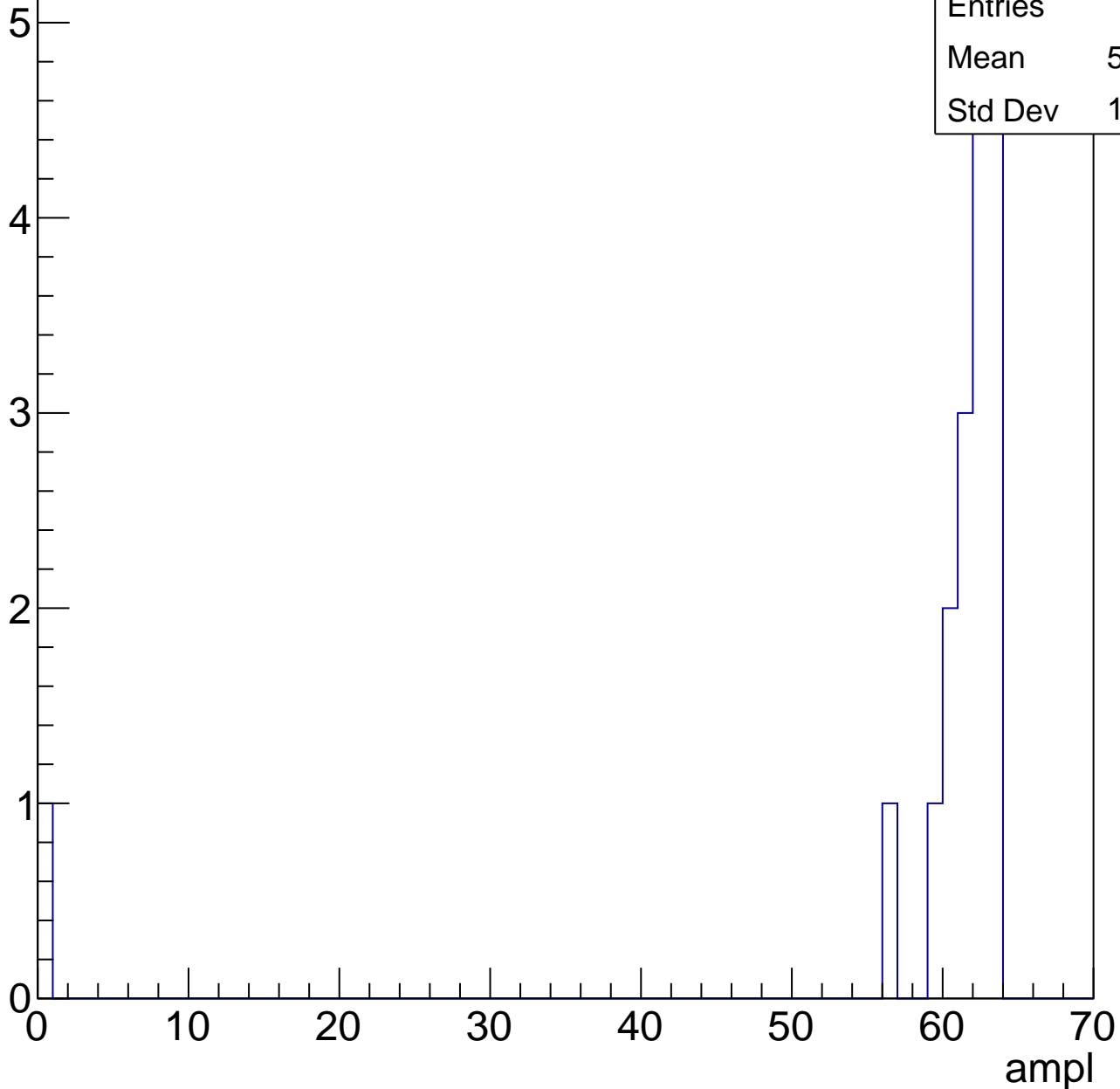


B1L102S, U14-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

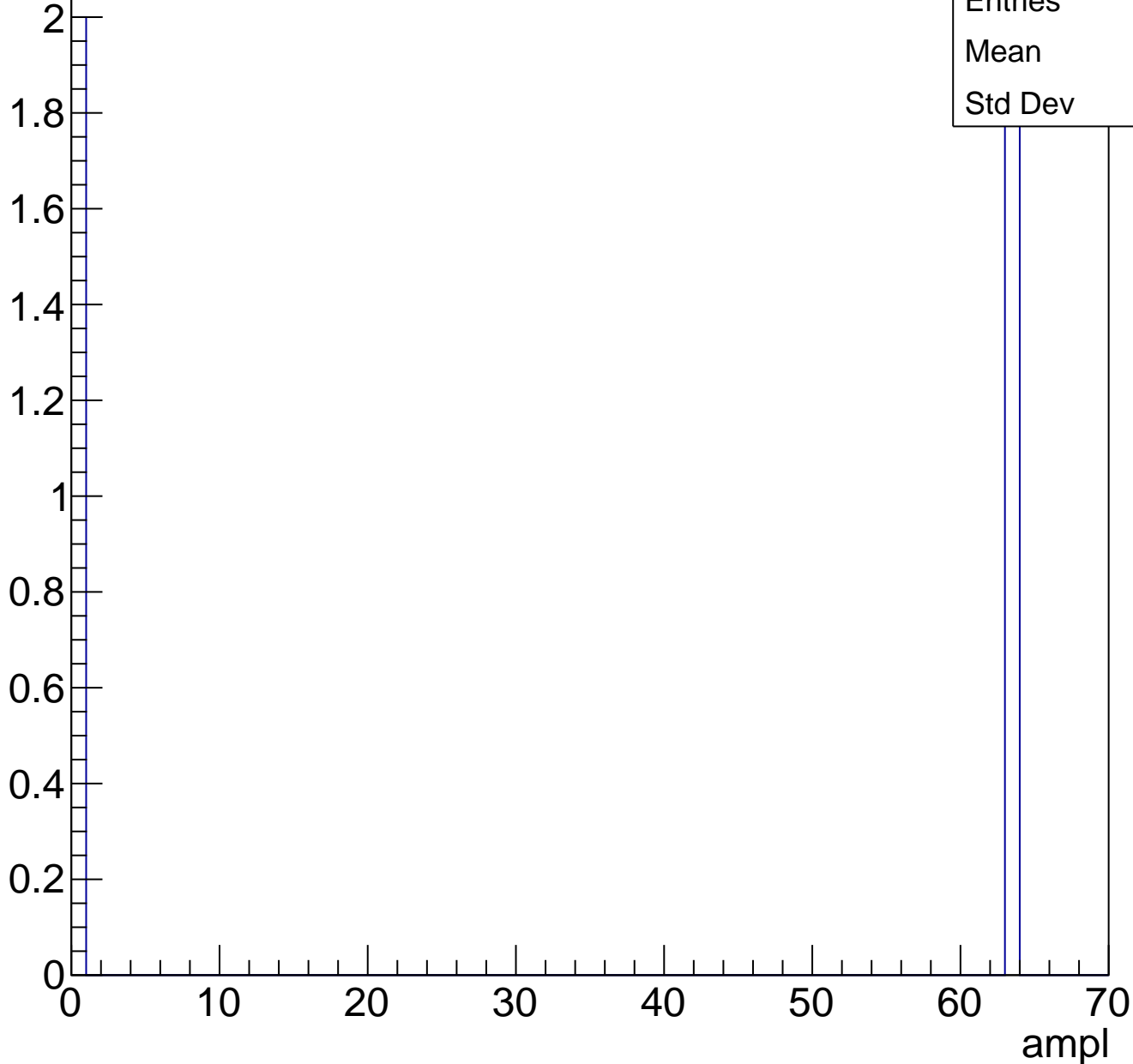
Entries	18
Mean	57.94
Std Dev	14.16



B1L102S, U14-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch51, adc0

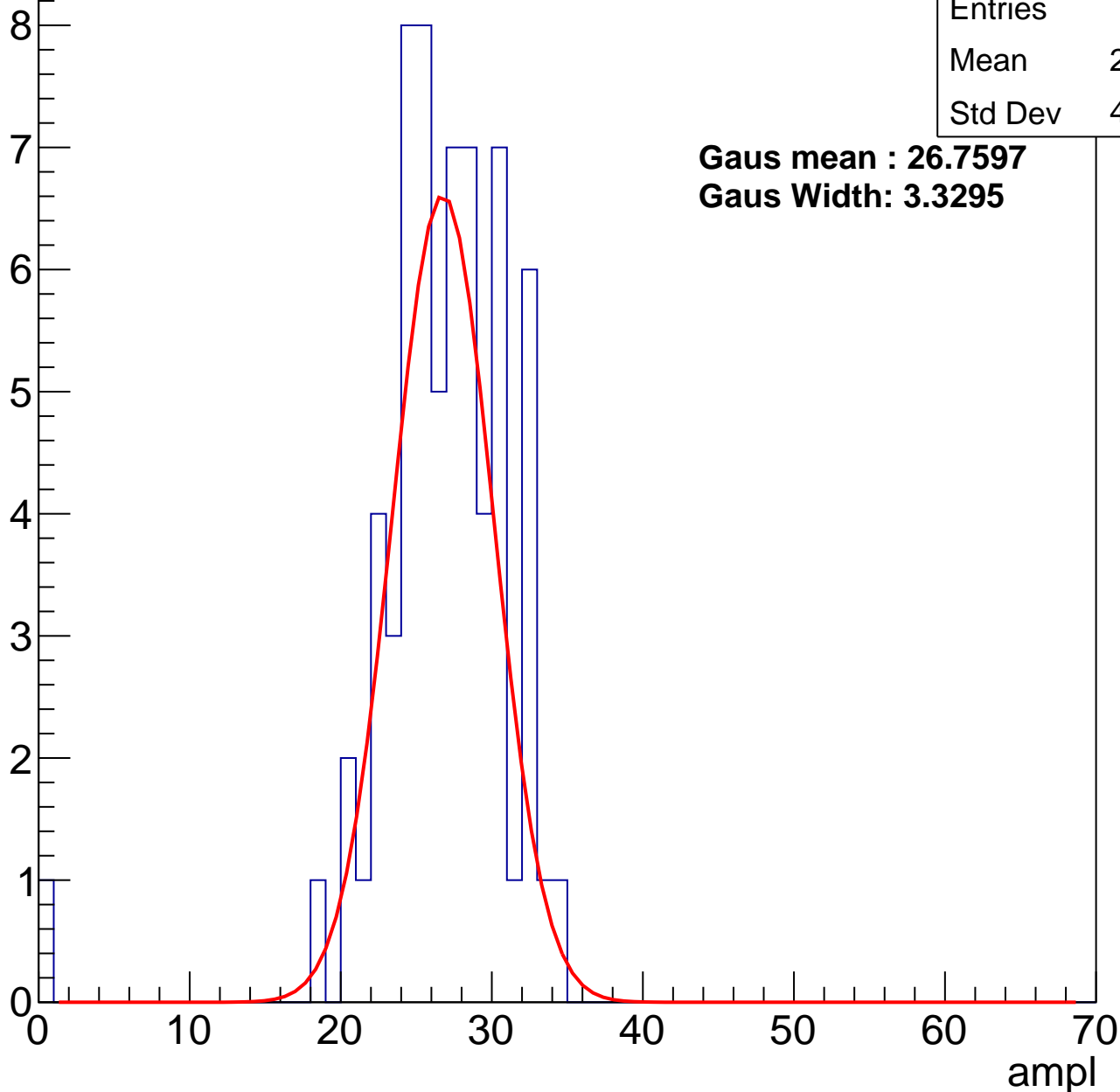
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	26.25
Std Dev	4.733

Gaus mean : 26.7597

Gaus Width: 3.3295



B1L102S, U14-ch51, adc1

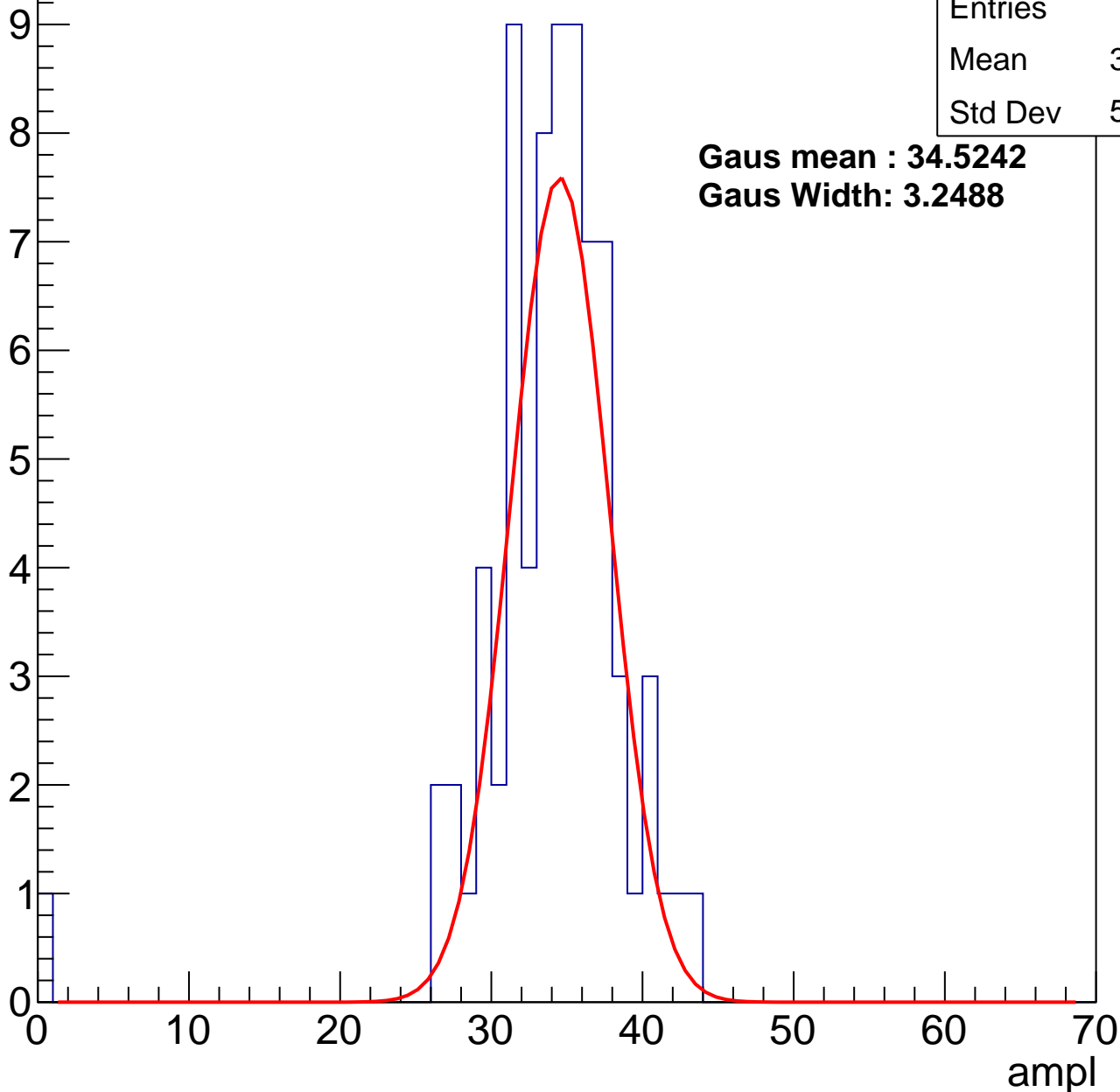
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	33.49
Std Dev	5.305

Gaus mean : 34.5242

Gaus Width: 3.2488



B1L102S, U14-ch51, adc2

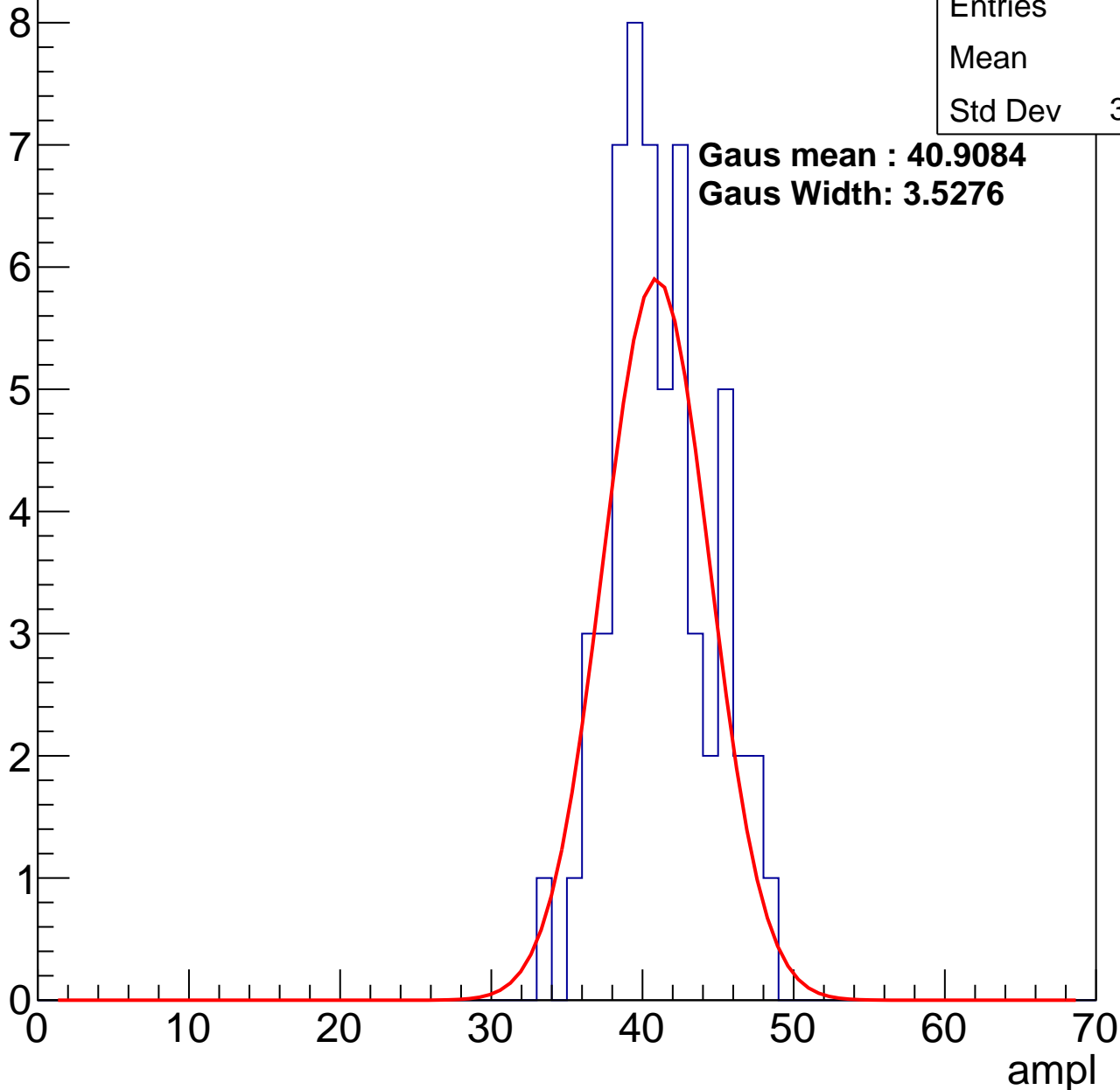
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	40.7
Std Dev	3.266

Gaus mean : 40.9084

Gaus Width: 3.5276

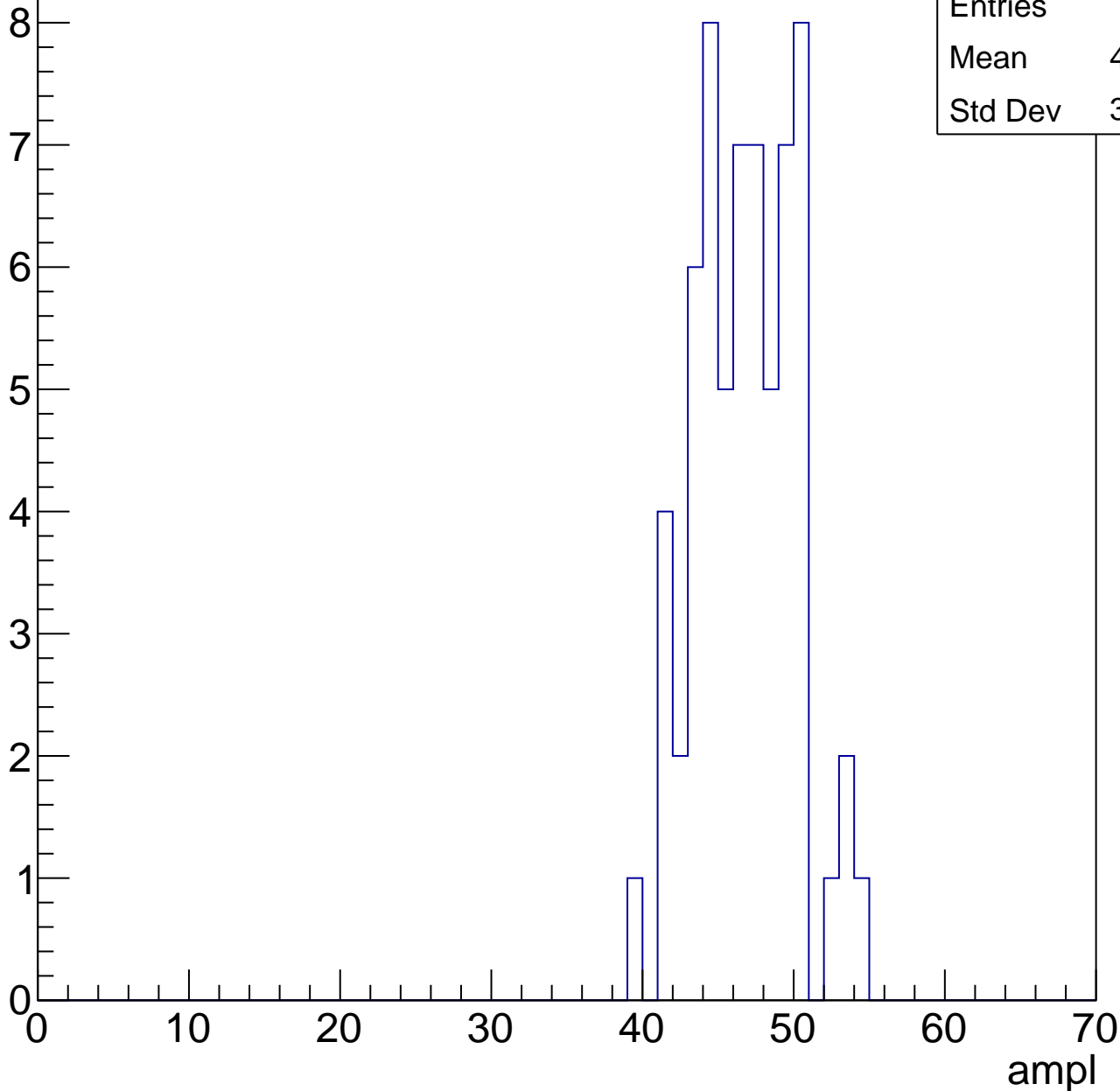


B1L102S, U14-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	46.38
Std Dev	3.257



B1L102S, U14-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

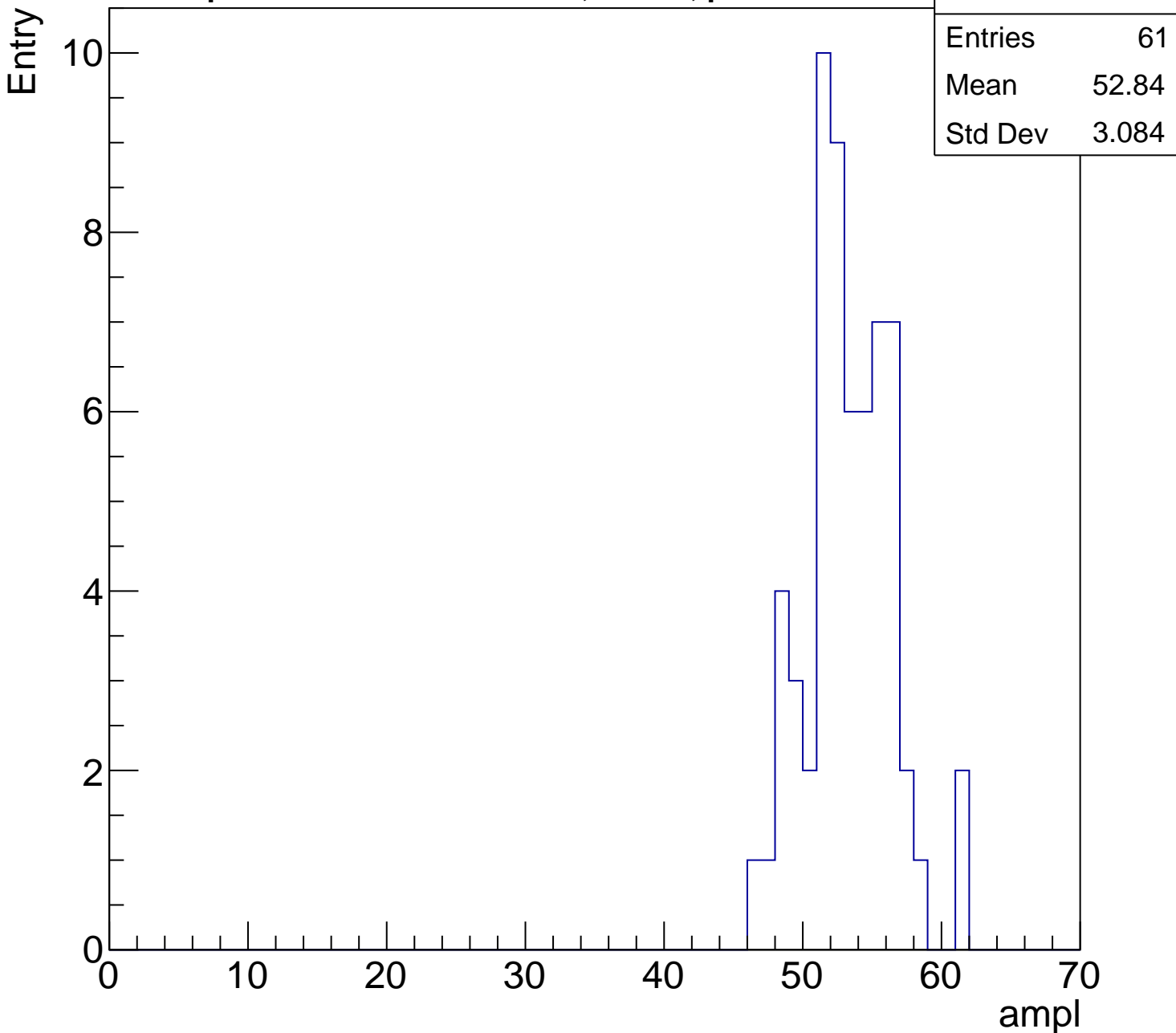
Entries	61
Mean	52.84
Std Dev	3.084

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

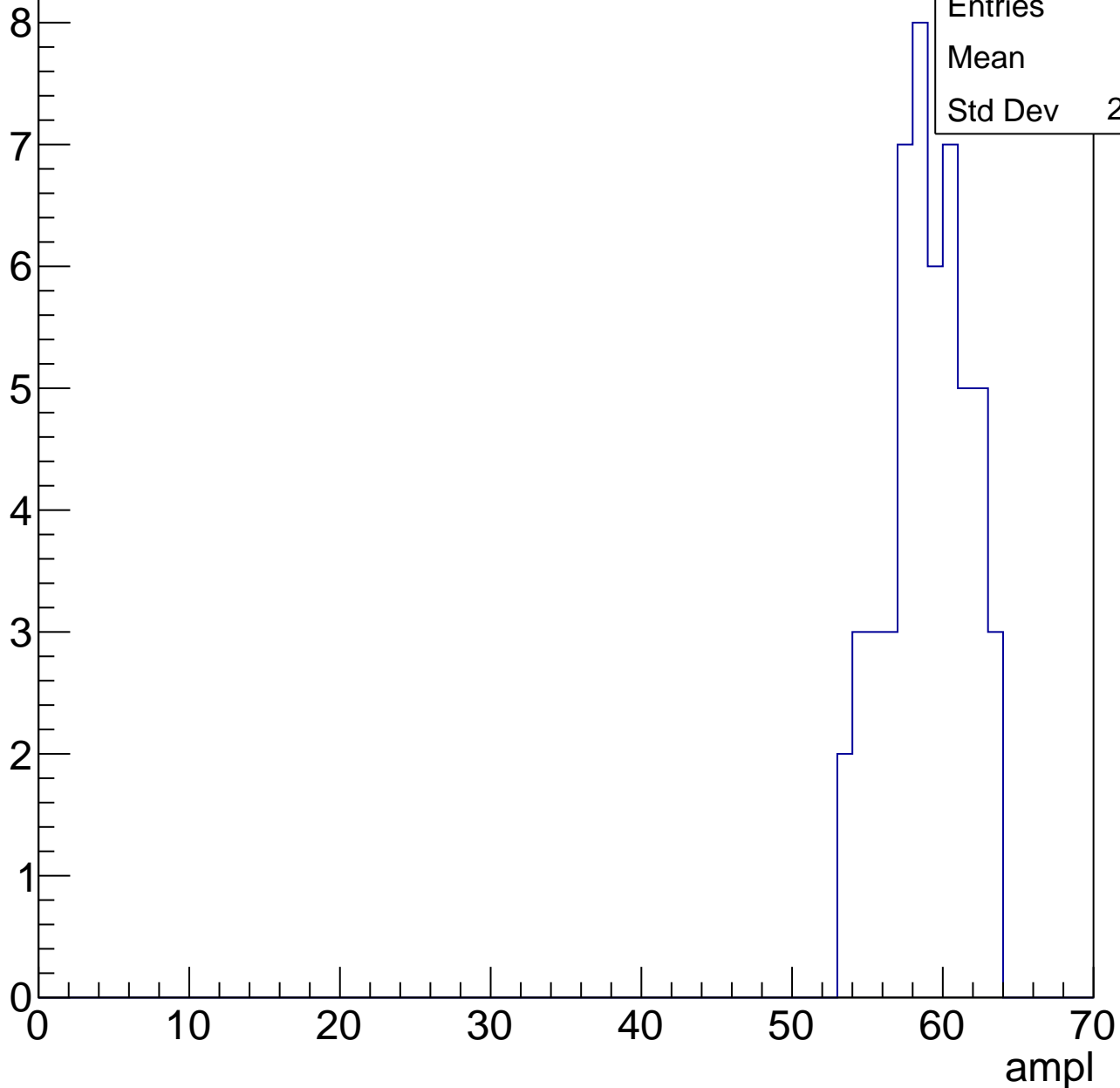
ampl



B1L102S, U14-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



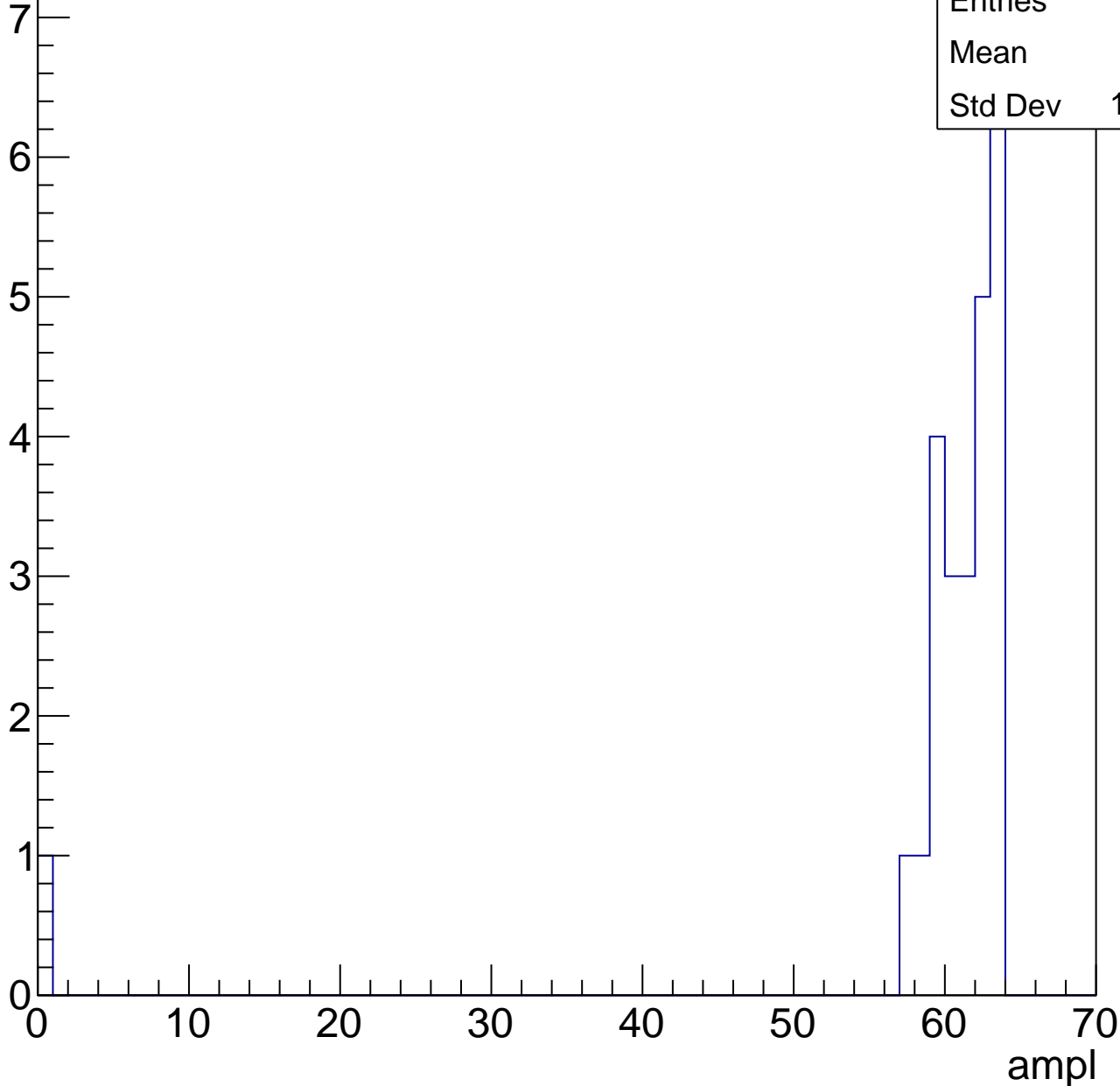
Entries	52
Mean	58.5
Std Dev	2.649

B1L102S, U14-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	58.6
Std Dev	12.09



B1L102S, U14-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch52, adc0

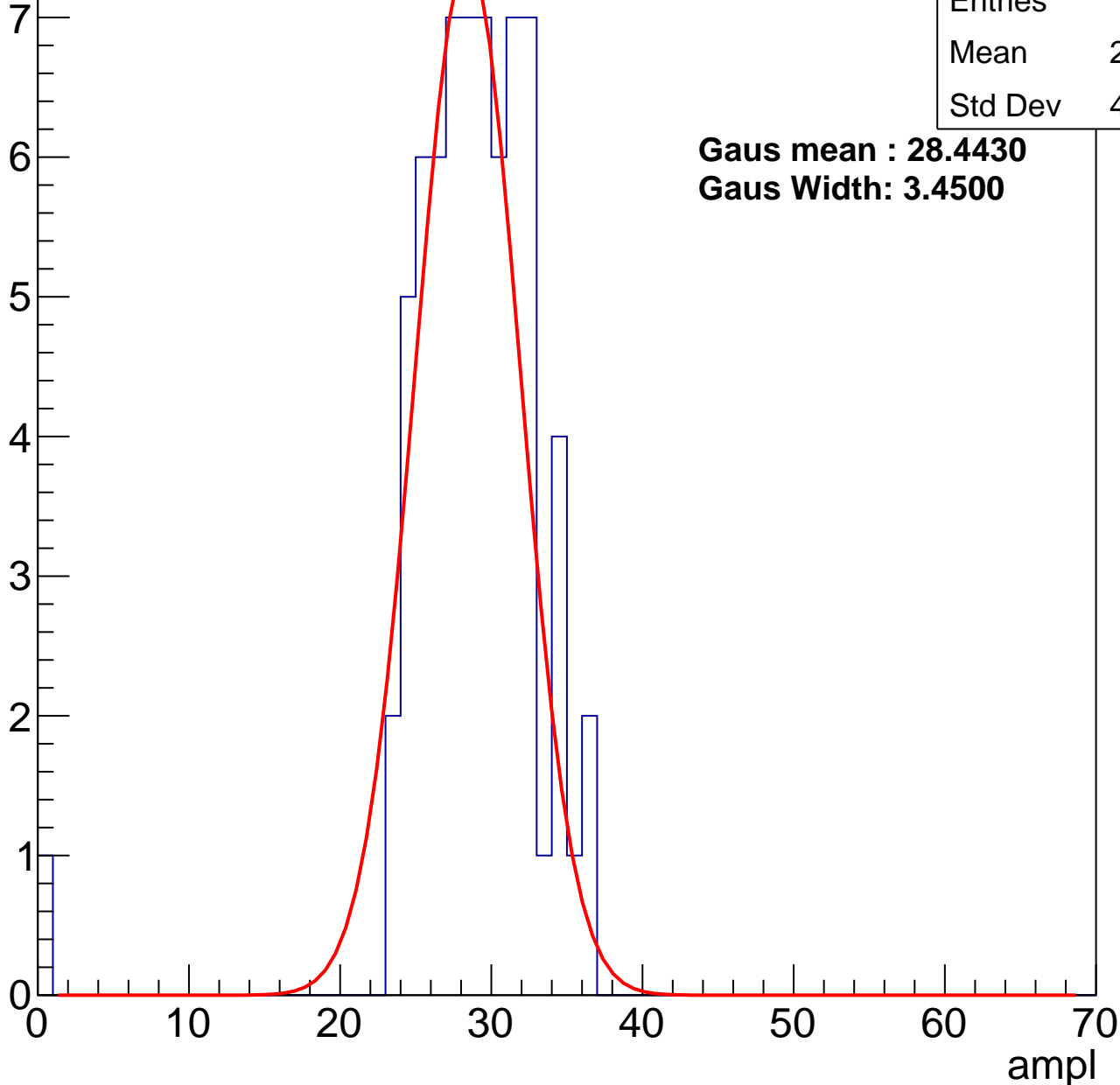
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.36
Std Dev	4.727

Gaus mean : 28.4430

Gaus Width: 3.4500



B1L102S, U14-ch52, adc1

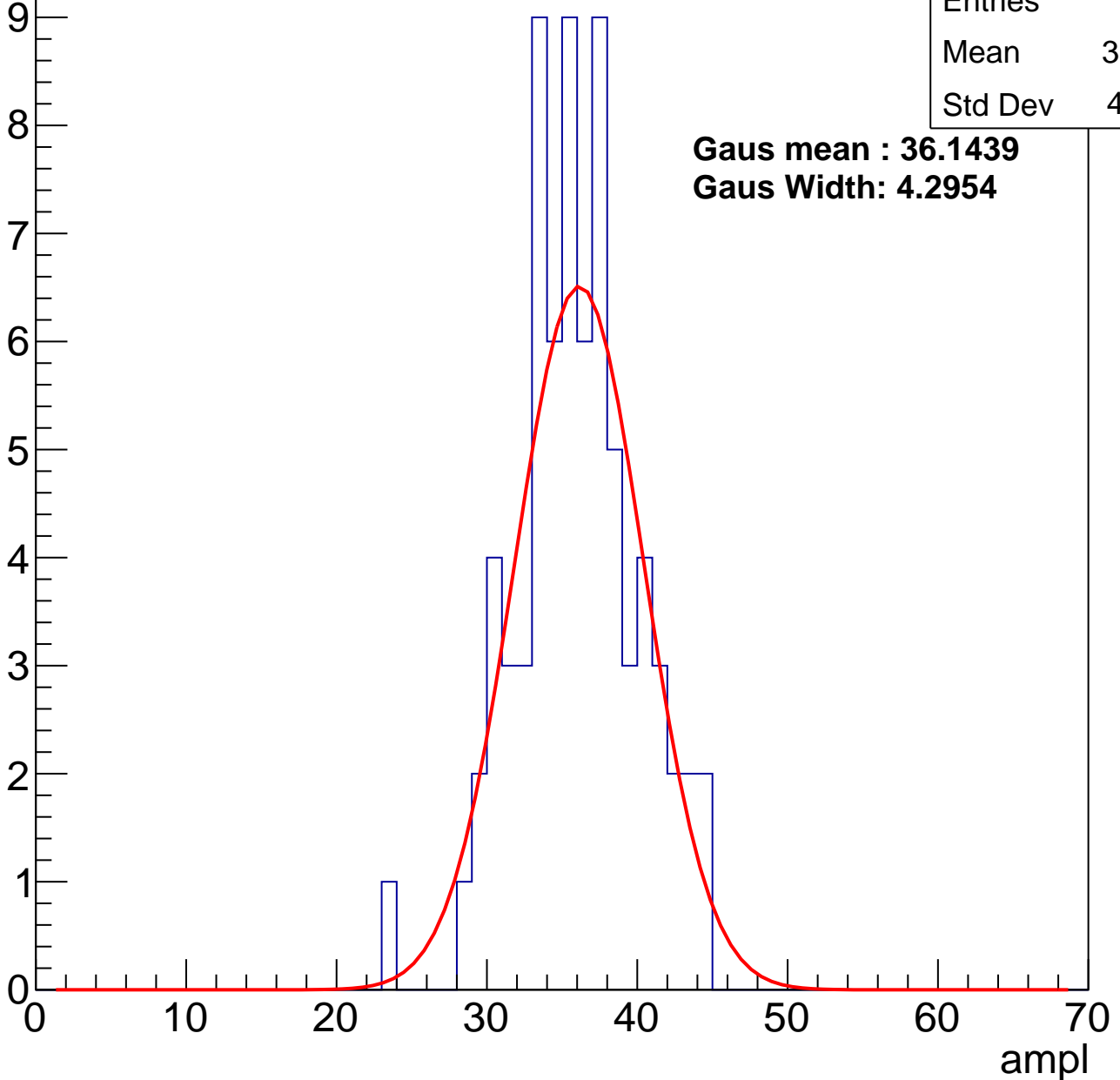
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.55
Std Dev	4.011

Gaus mean : 36.1439

Gaus Width: 4.2954



B1L102S, U14-ch52, adc2

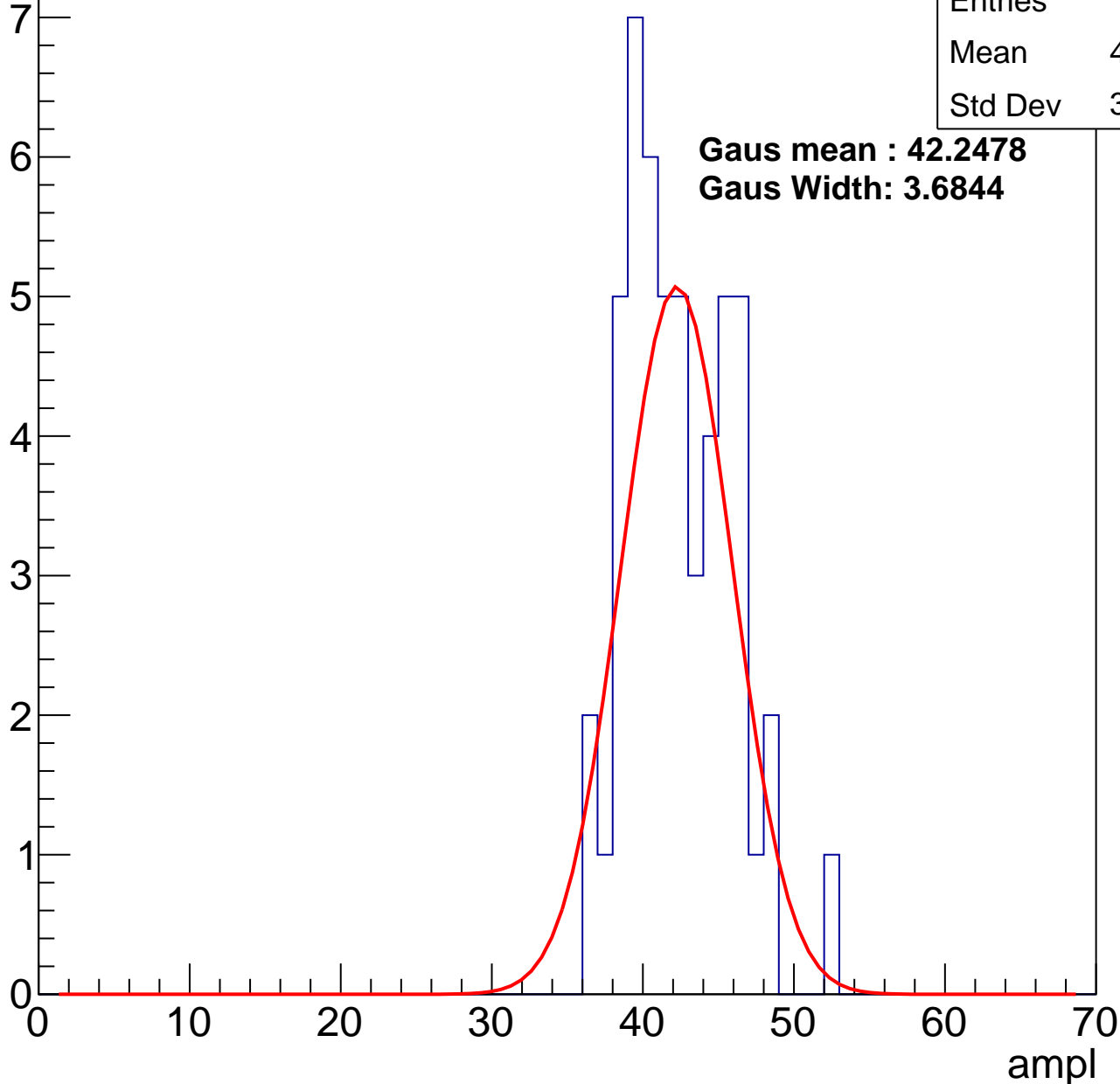
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	41.96
Std Dev	3.425

Gaus mean : 42.2478

Gaus Width: 3.6844

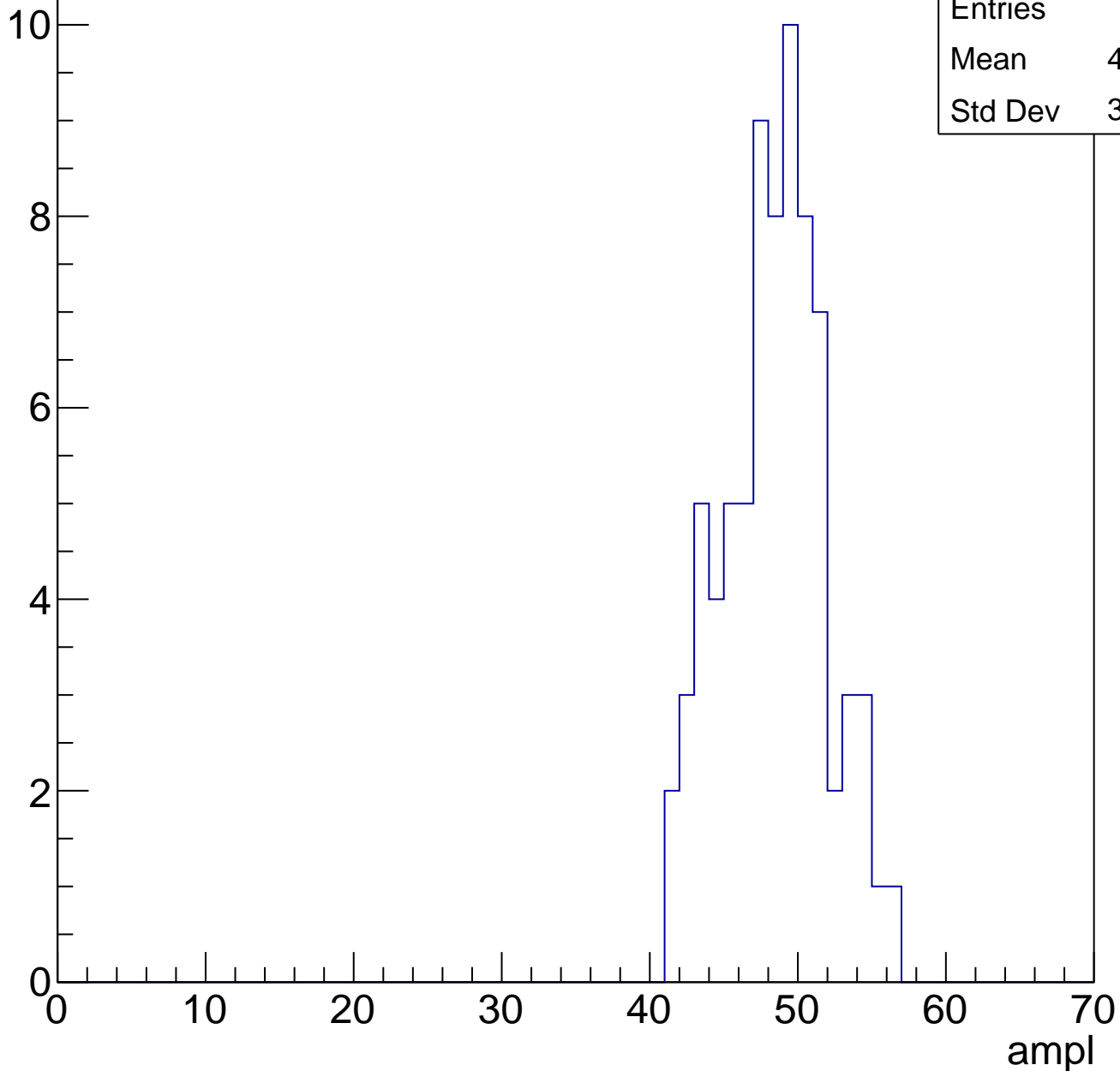


B1L102S, U14-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	47.95
Std Dev	3.445

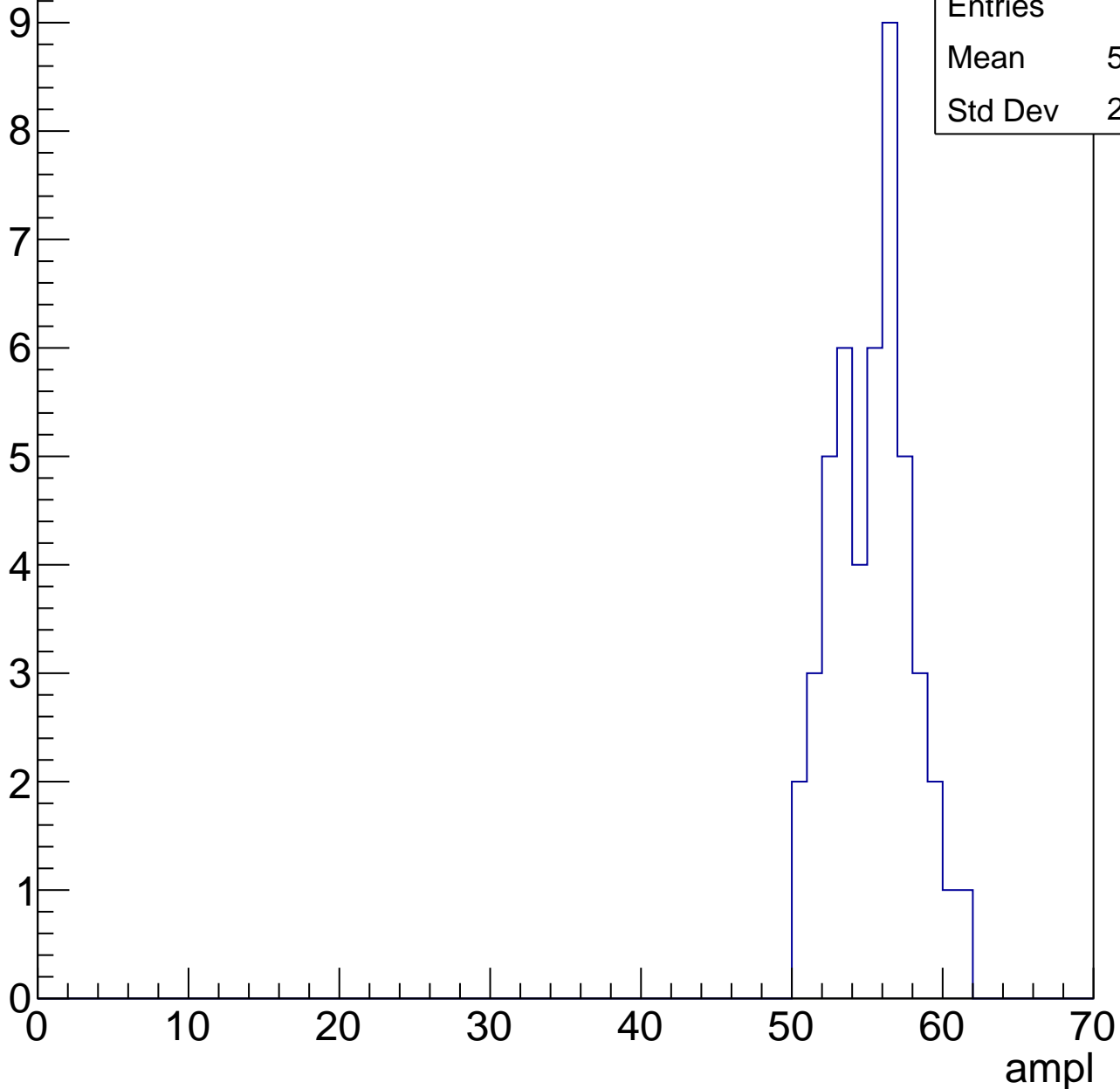


B1L102S, U14-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.87
Std Dev	2.606

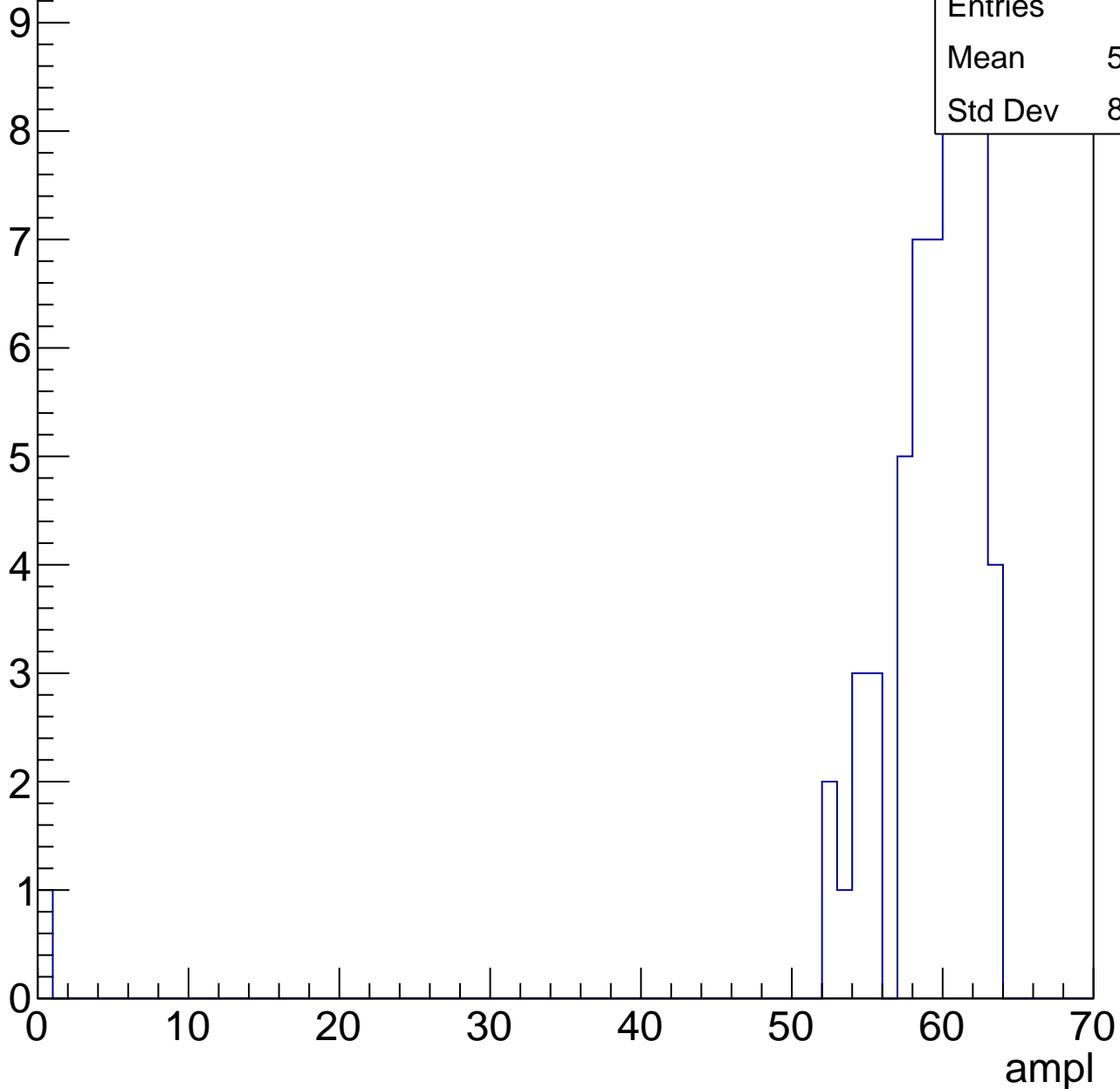


B1L102S, U14-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.02
Std Dev	8.188

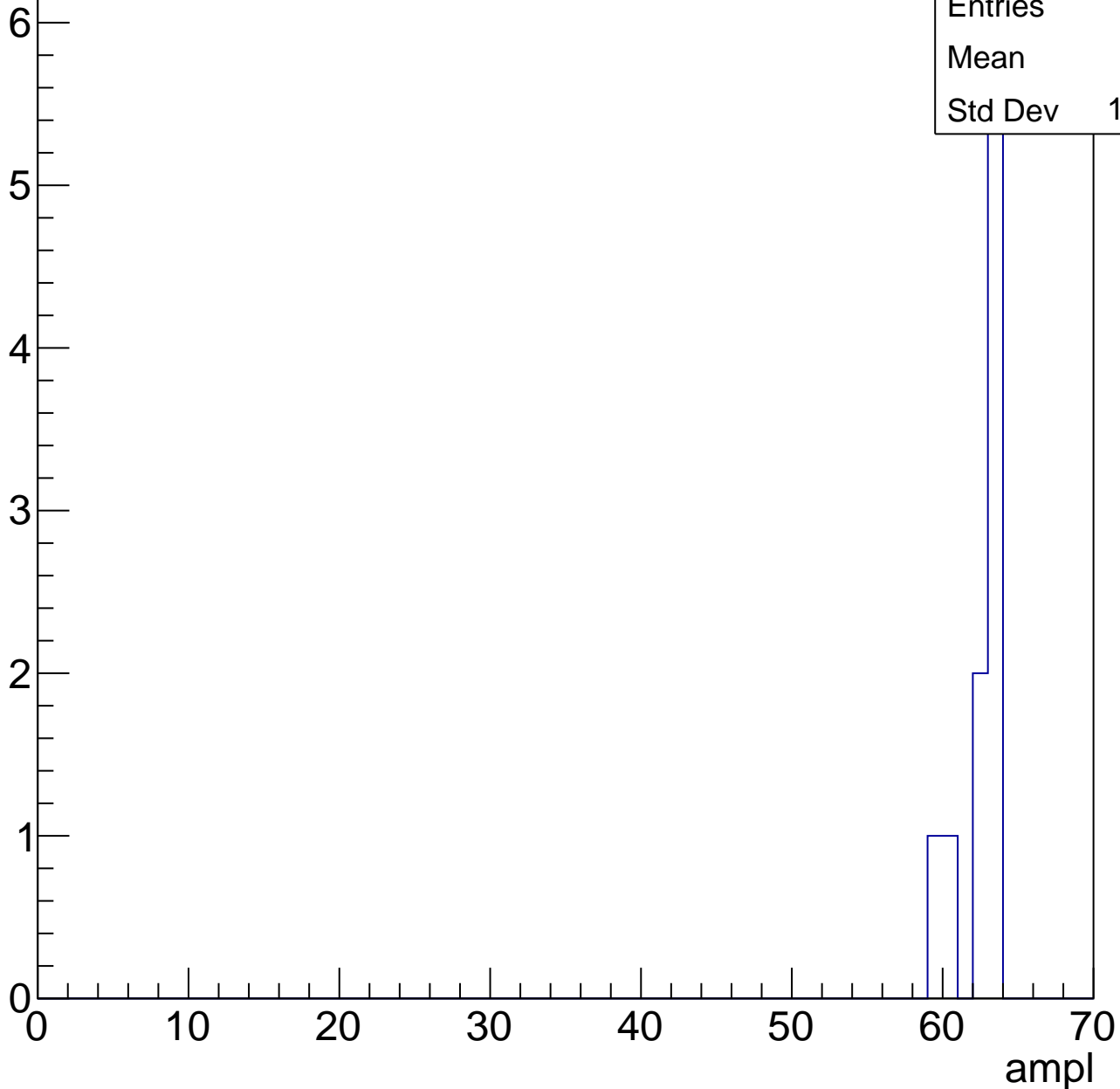


B1L102S, U14-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62.1
Std Dev	1.375



B1L102S, U14-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch53, adc0

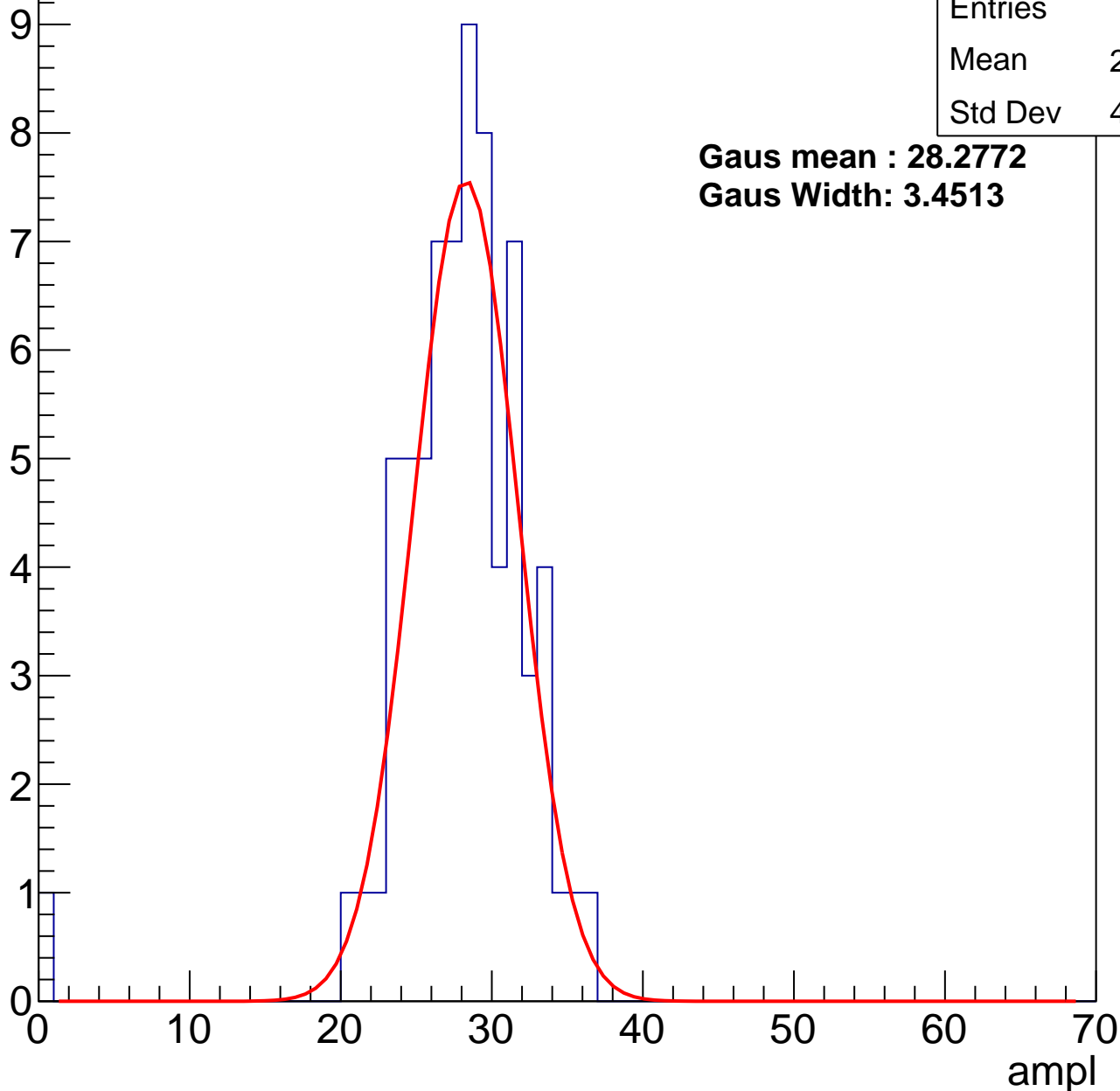
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.44
Std Dev	4.717

Gaus mean : 28.2772

Gaus Width: 3.4513



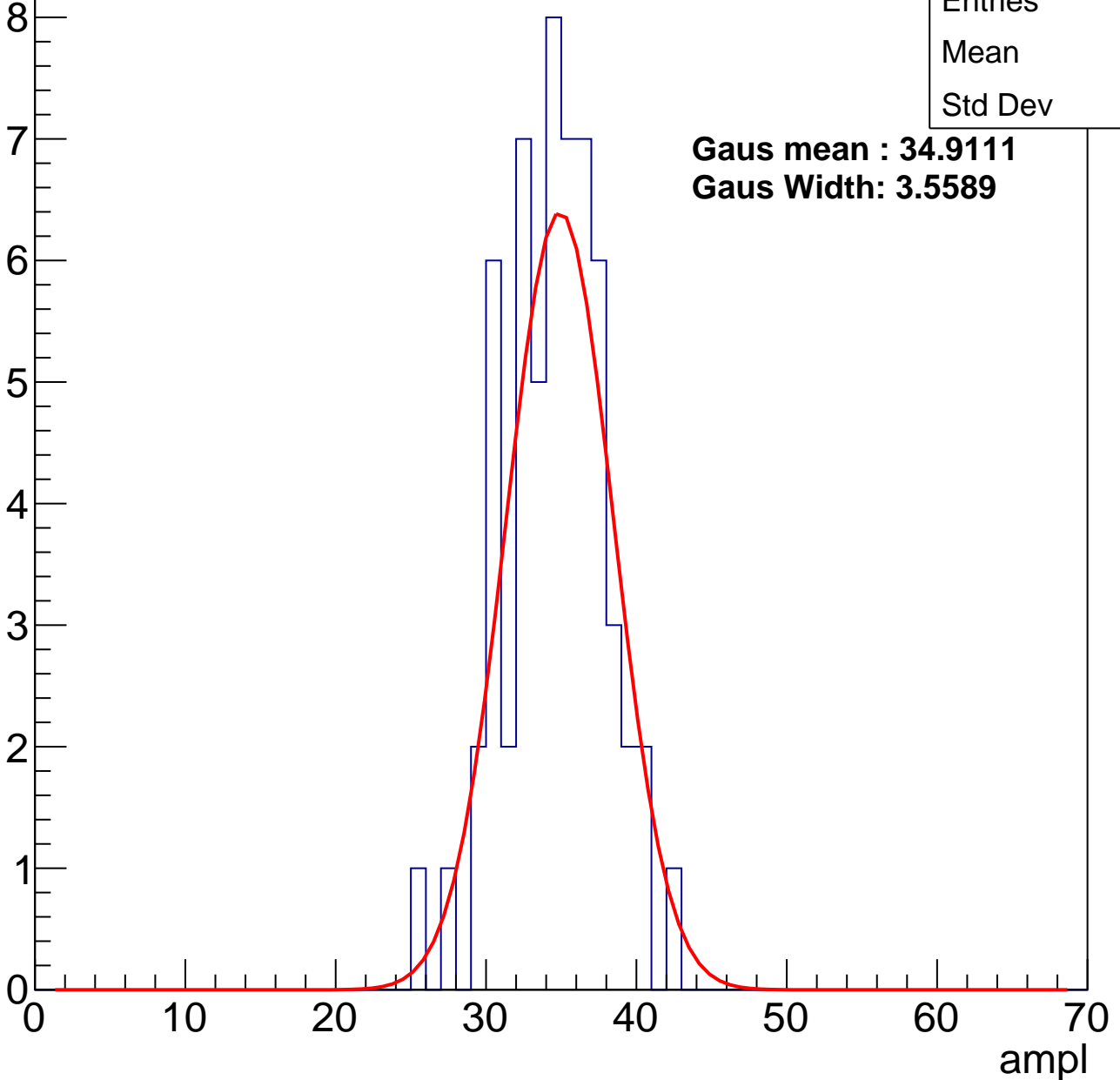
B1L102S, U14-ch53, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.1
Std Dev	3.29

Gaus mean : 34.9111
Gaus Width: 3.5589



B1L102S, U14-ch53, adc2

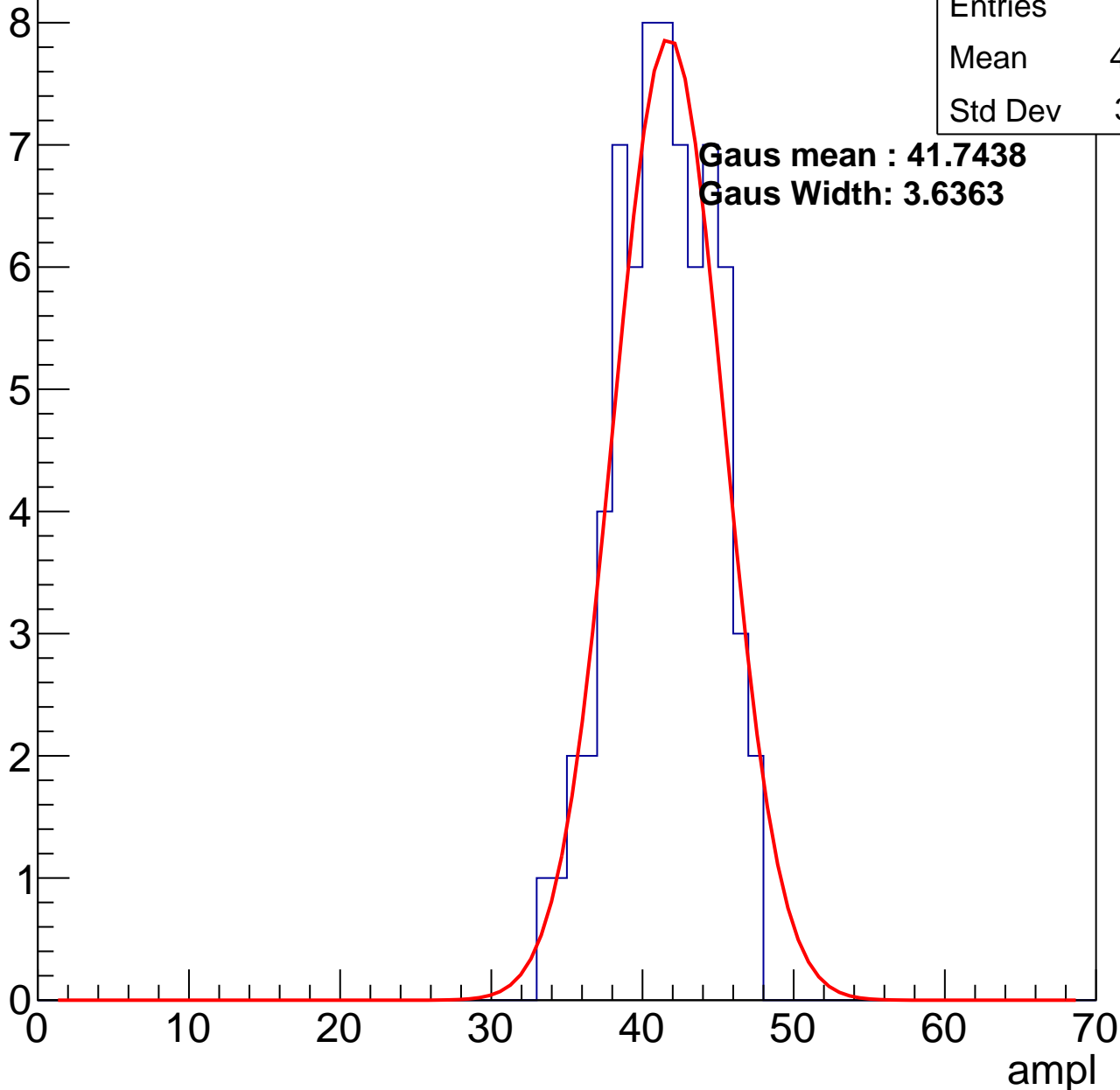
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40.96
Std Dev	3.231

Gaus mean : 41.7438

Gaus Width: 3.6363

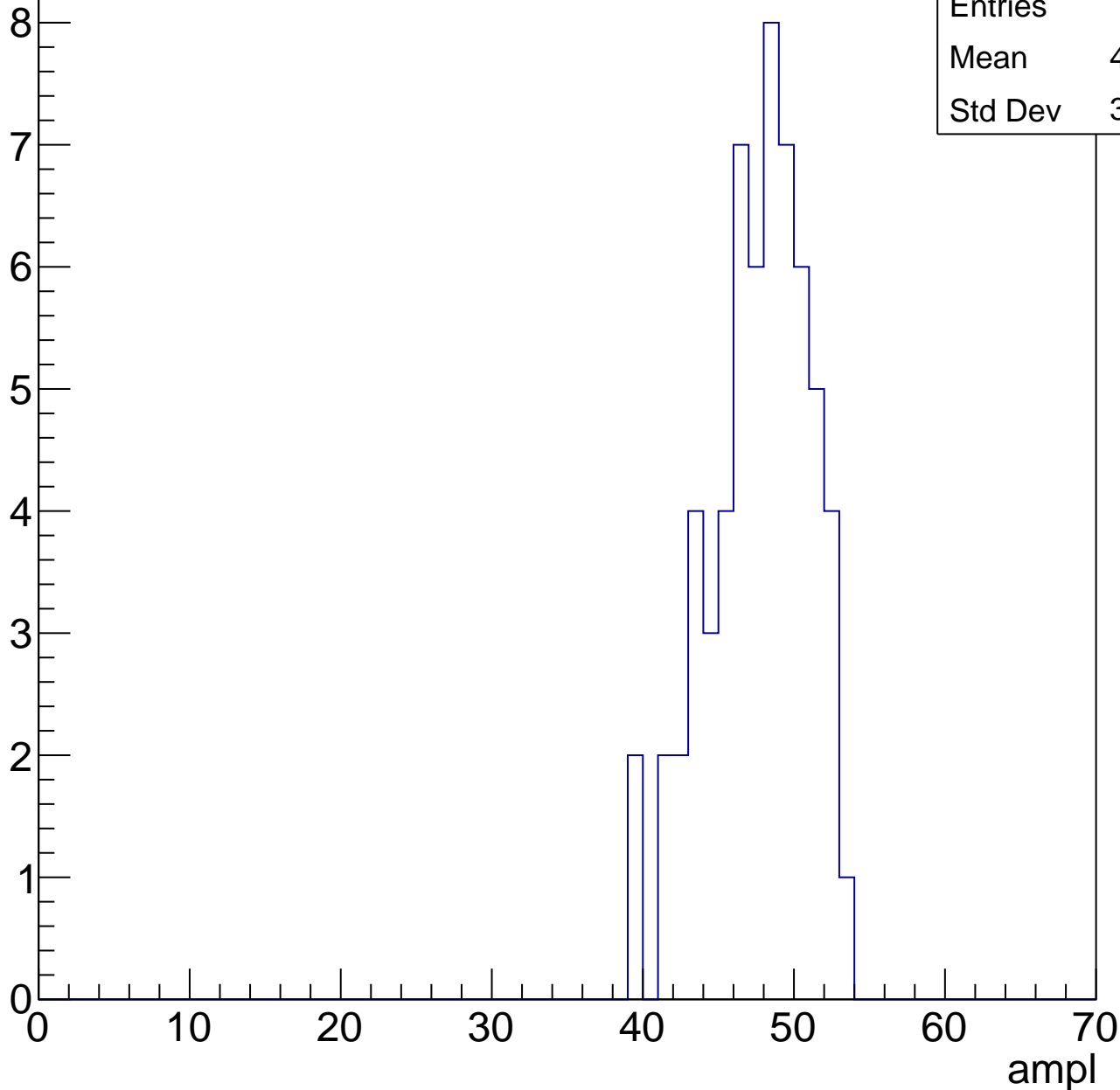


B1L102S, U14-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.13
Std Dev	3.312

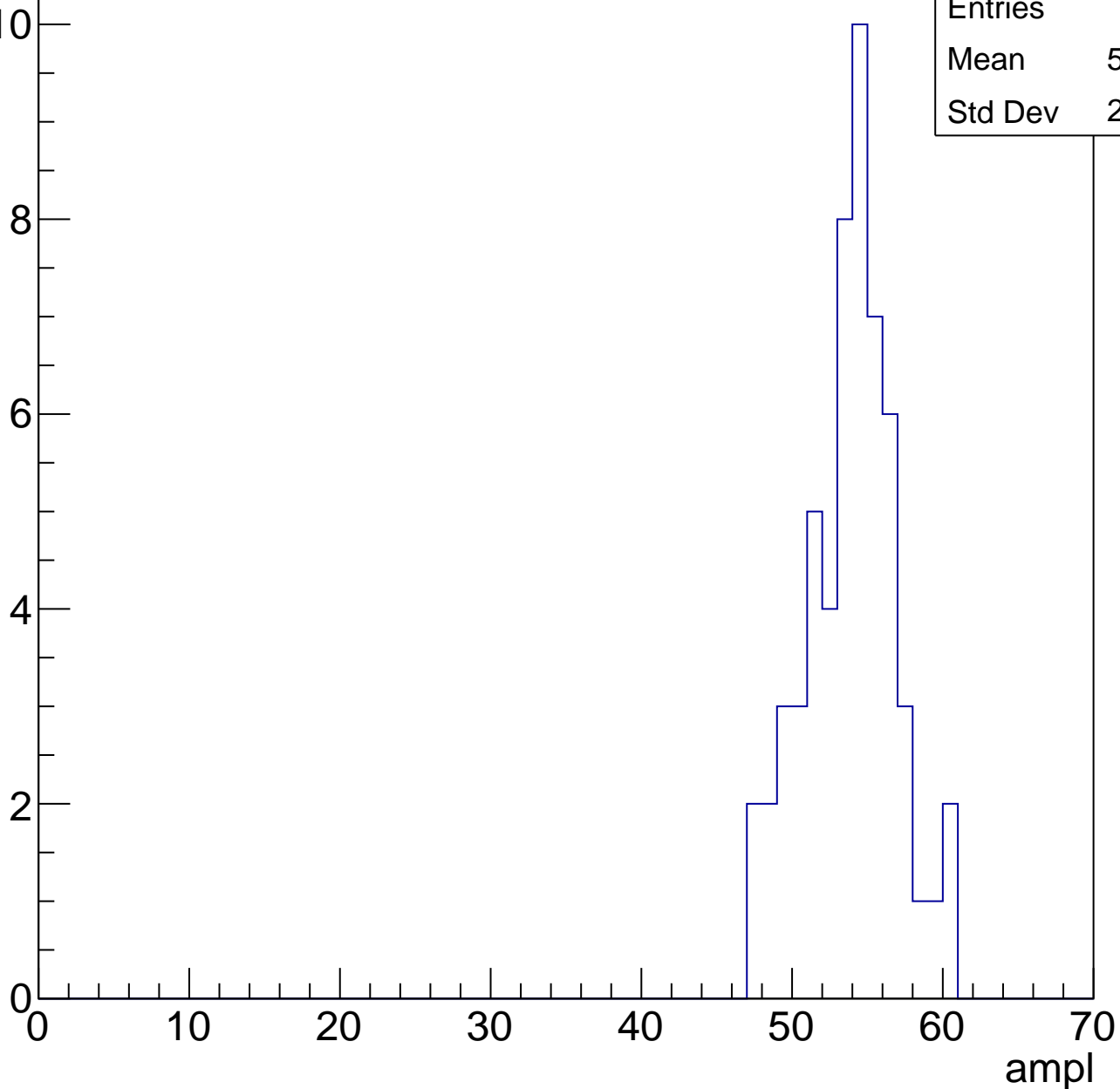


B1L102S, U14-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.39
Std Dev	2.996

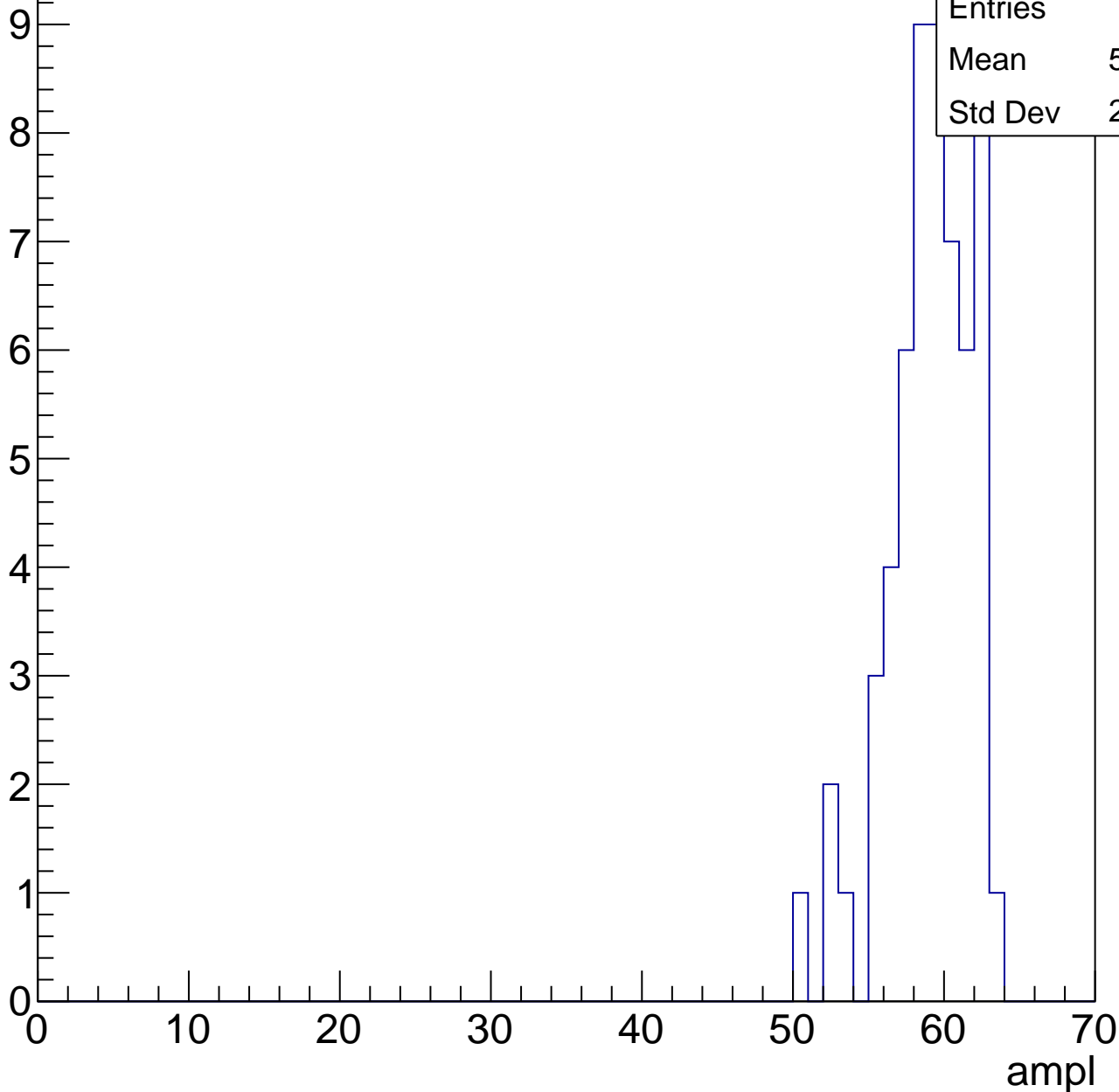


B1L102S, U14-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	58.53
Std Dev	2.773

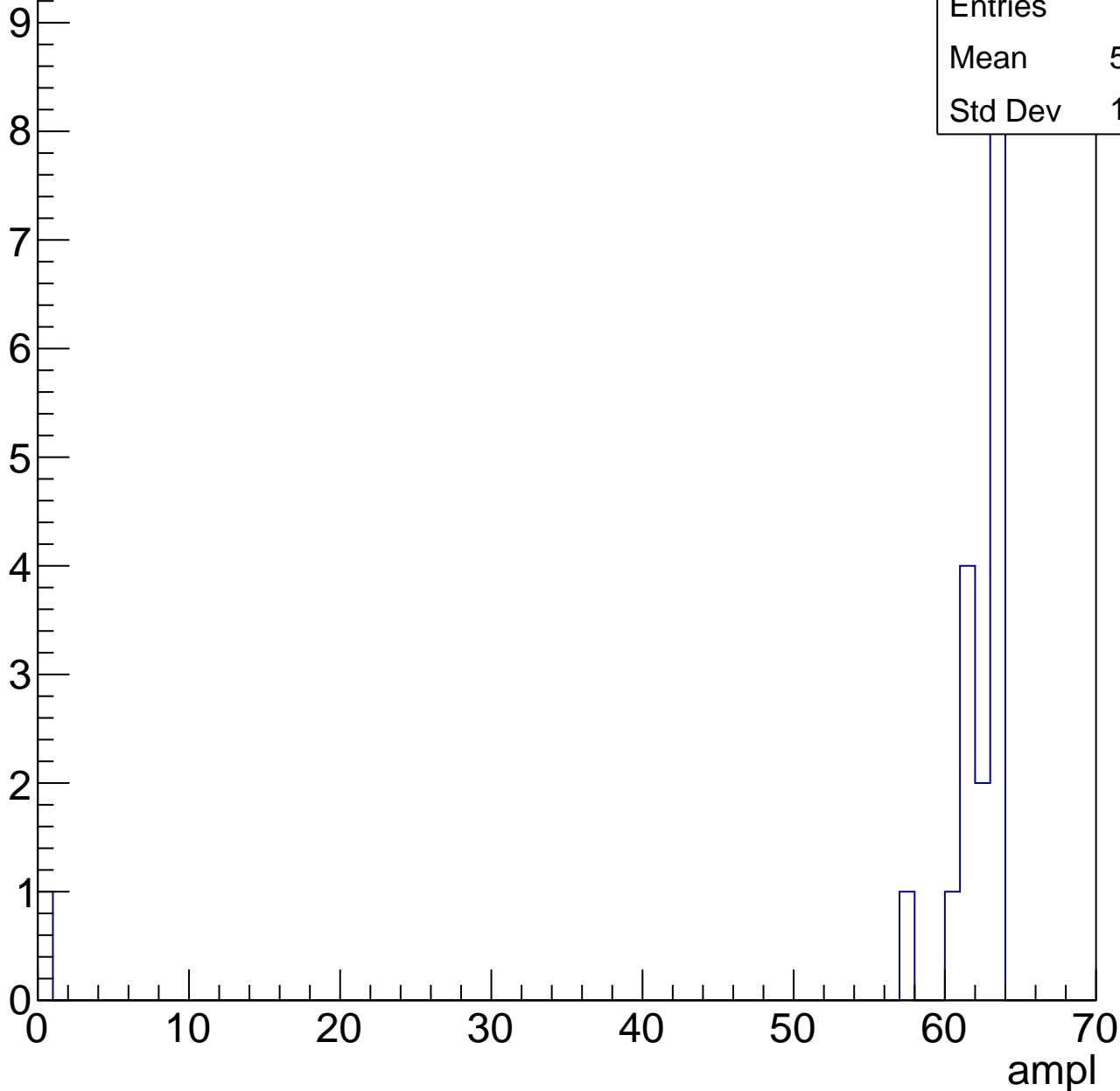


B1L102S, U14-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	58.44
Std Dev	14.26



B1L102S, U14-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L102S, U14-ch54, adc0

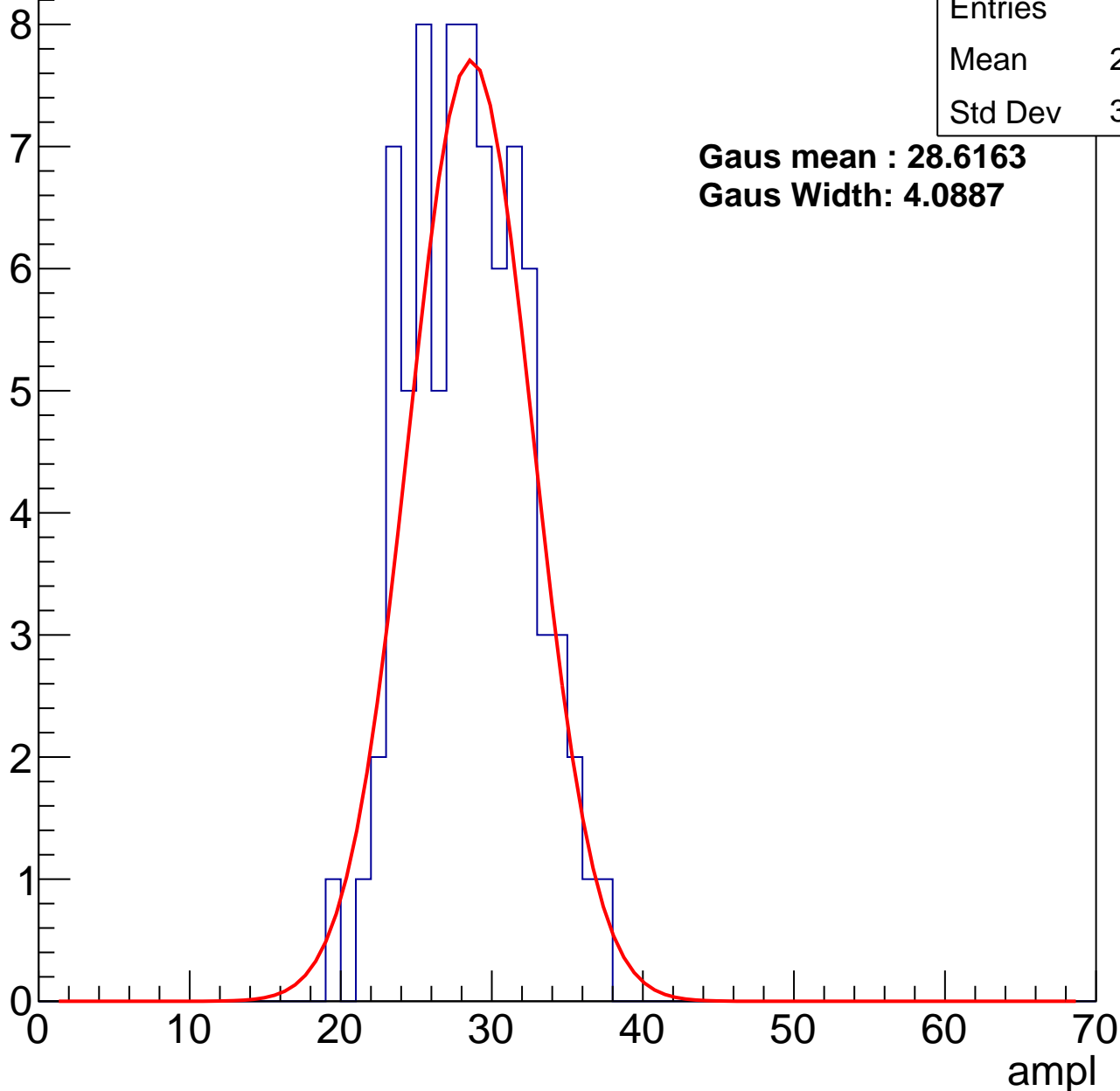
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.04
Std Dev	3.805

Gaus mean : 28.6163

Gaus Width: 4.0887



B1L102S, U14-ch54, adc1

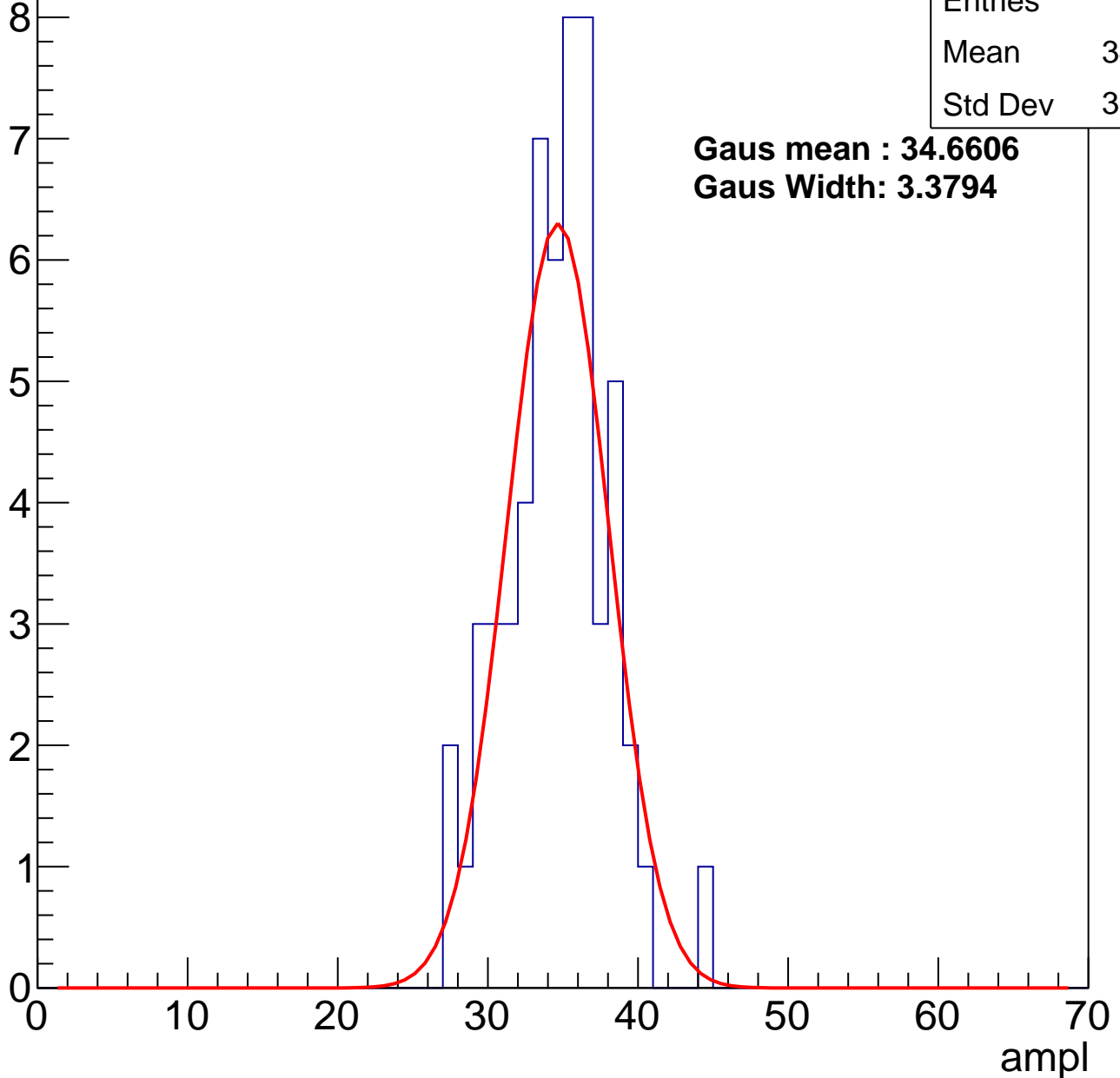
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.14
Std Dev	3.348

Gaus mean : 34.6606

Gaus Width: 3.3794



B1L102S, U14-ch54, adc2

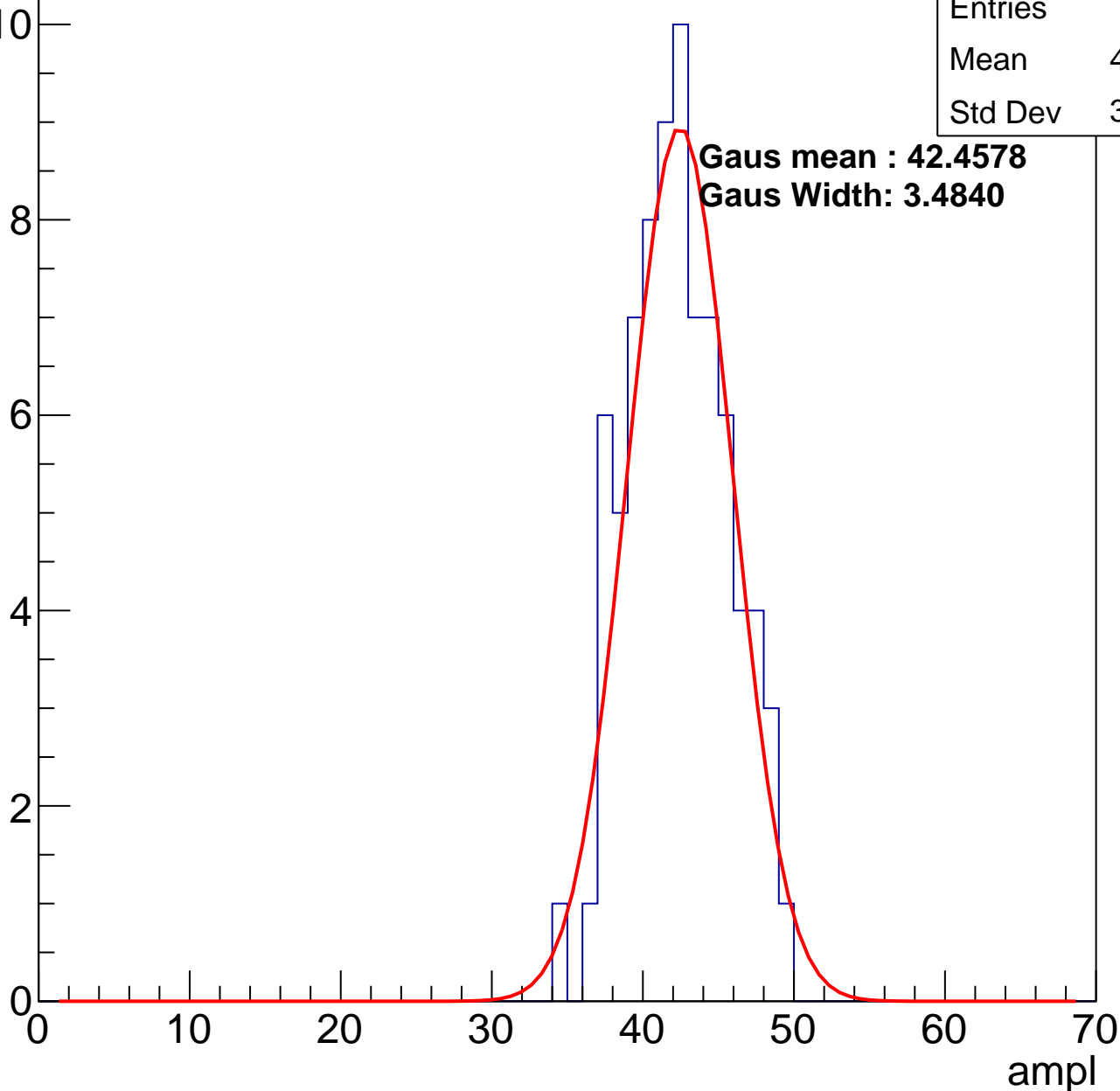
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	41.87
Std Dev	3.266

Gaus mean : 42.4578

Gaus Width: 3.4840

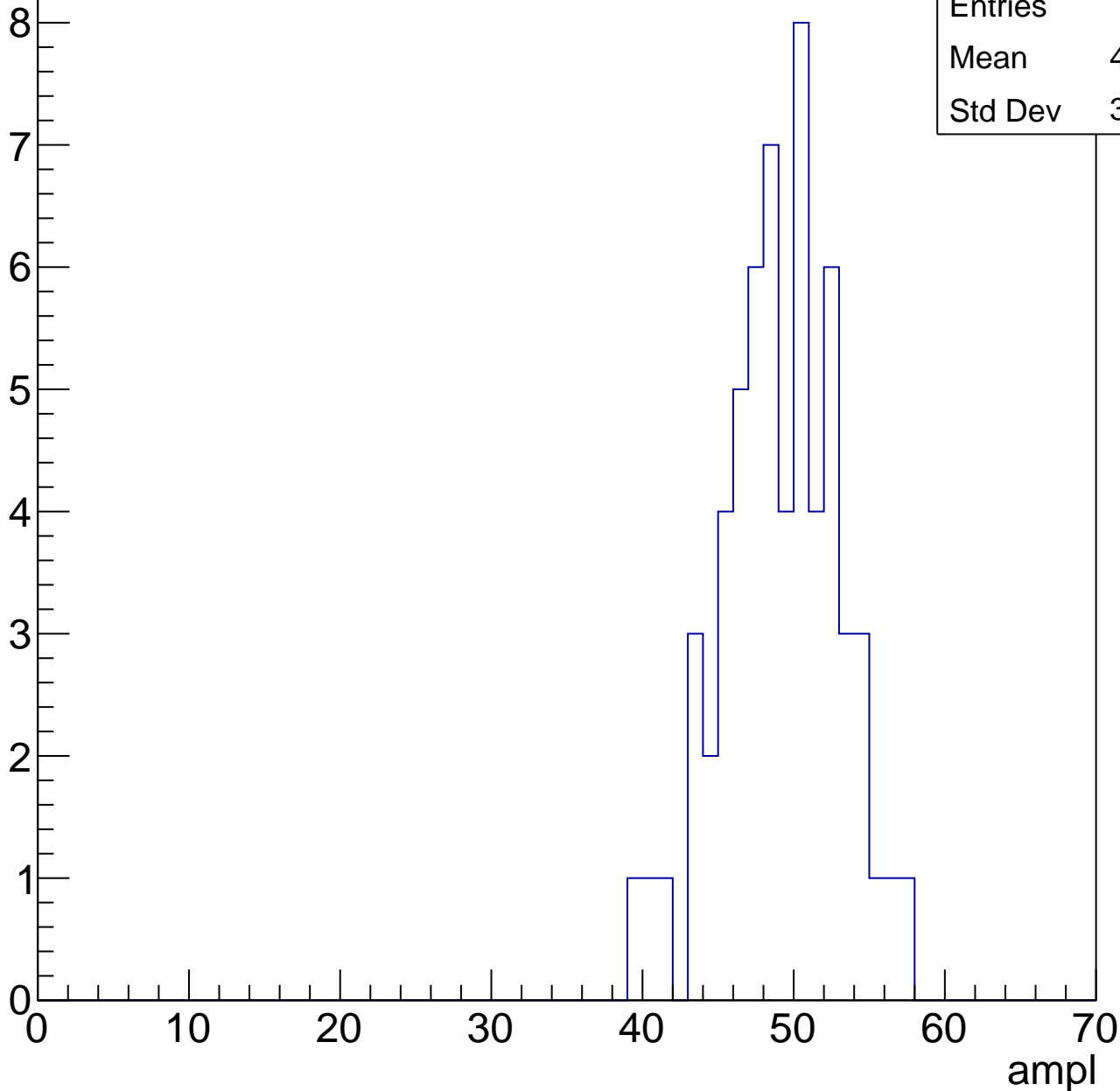


B1L102S, U14-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.62
Std Dev	3.808

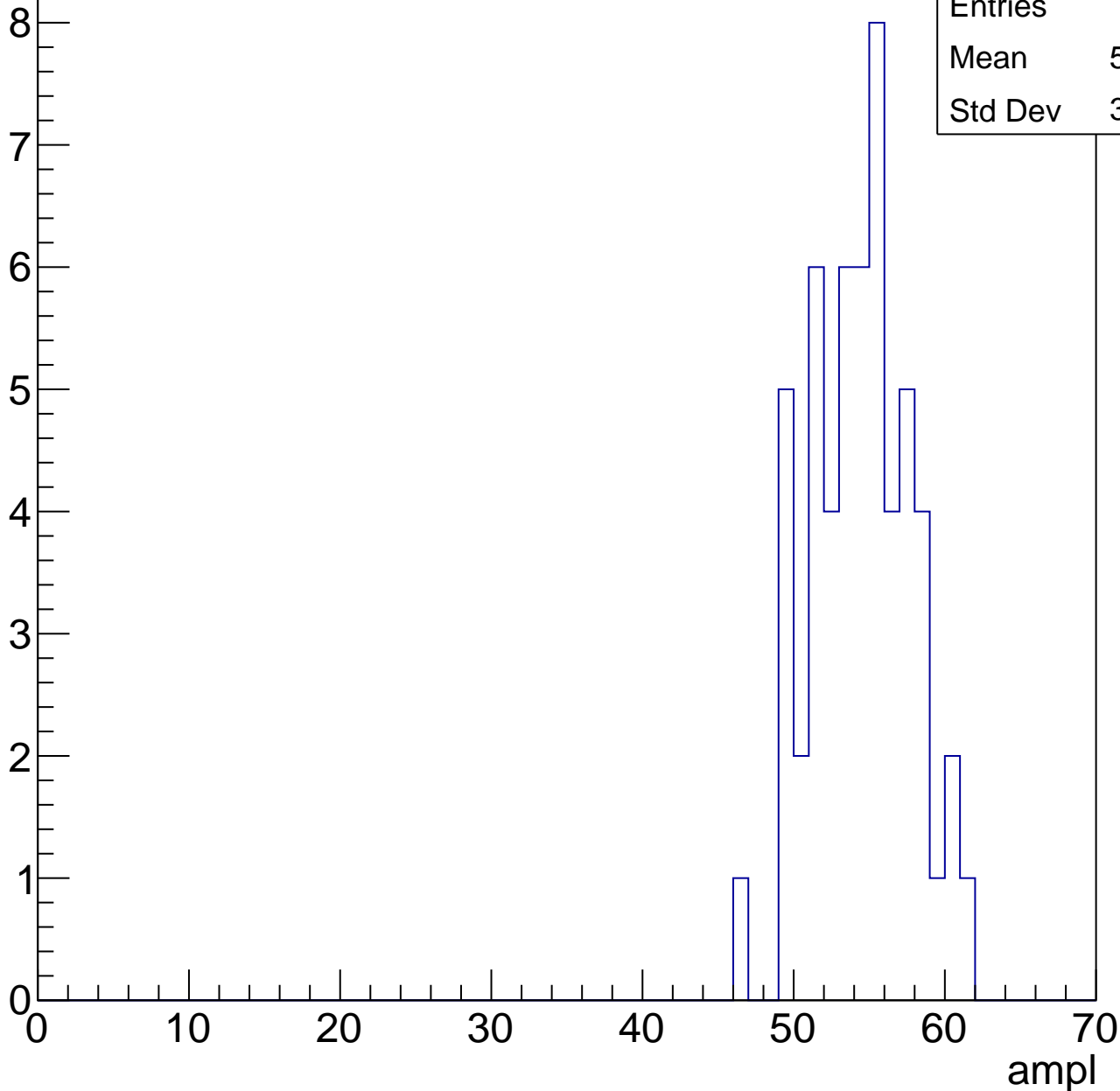


B1L102S, U14-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

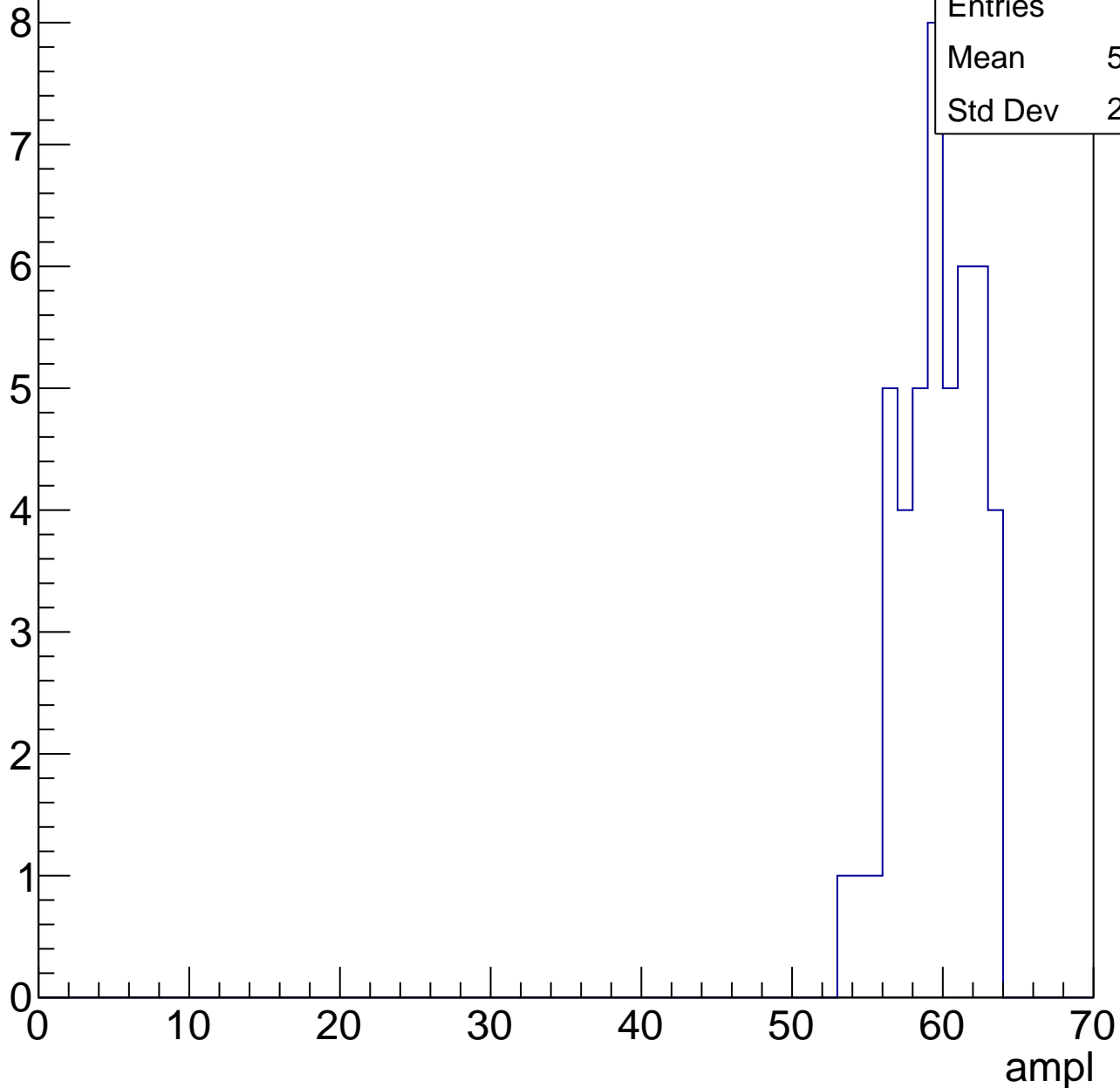
Entries	55
Mean	53.96
Std Dev	3.236



B1L102S, U14-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



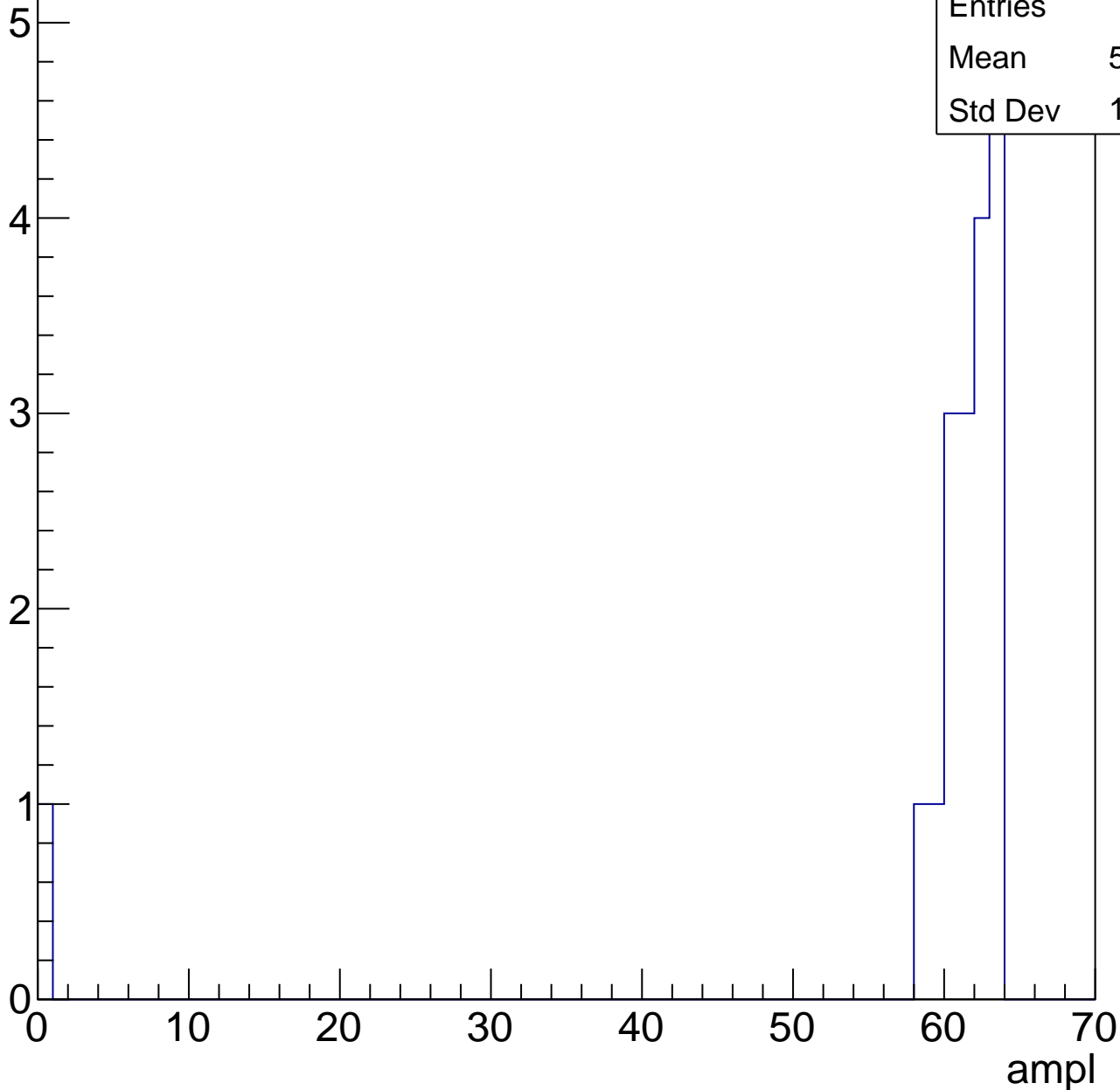
Entries	46
Mean	59.17
Std Dev	2.505

B1L102S, U14-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	57.94
Std Dev	14.13



B1L102S, U14-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch55, adc0

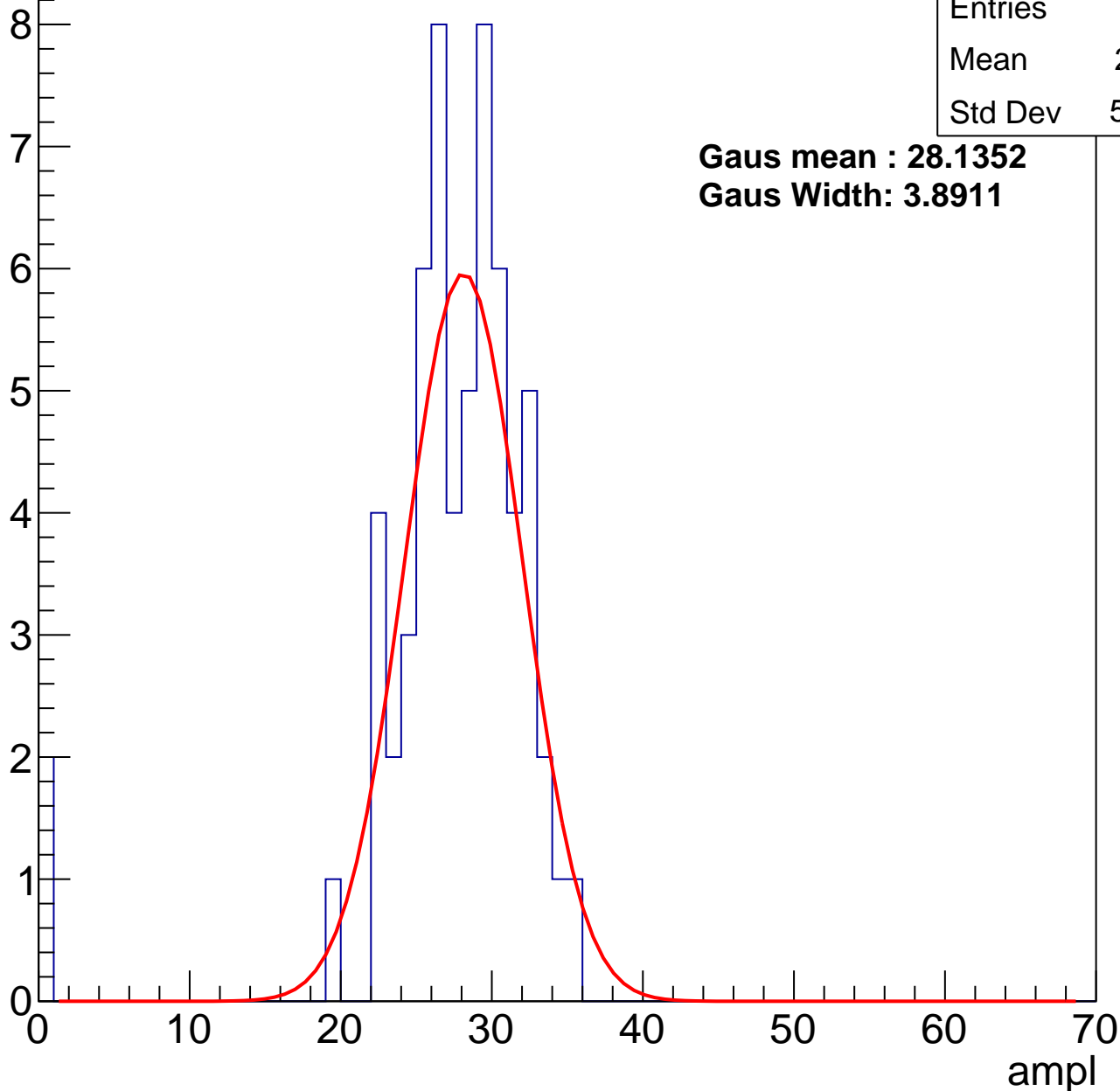
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	26.81
Std Dev	5.924

Gaus mean : 28.1352

Gaus Width: 3.8911



B1L102S, U14-ch55, adc1

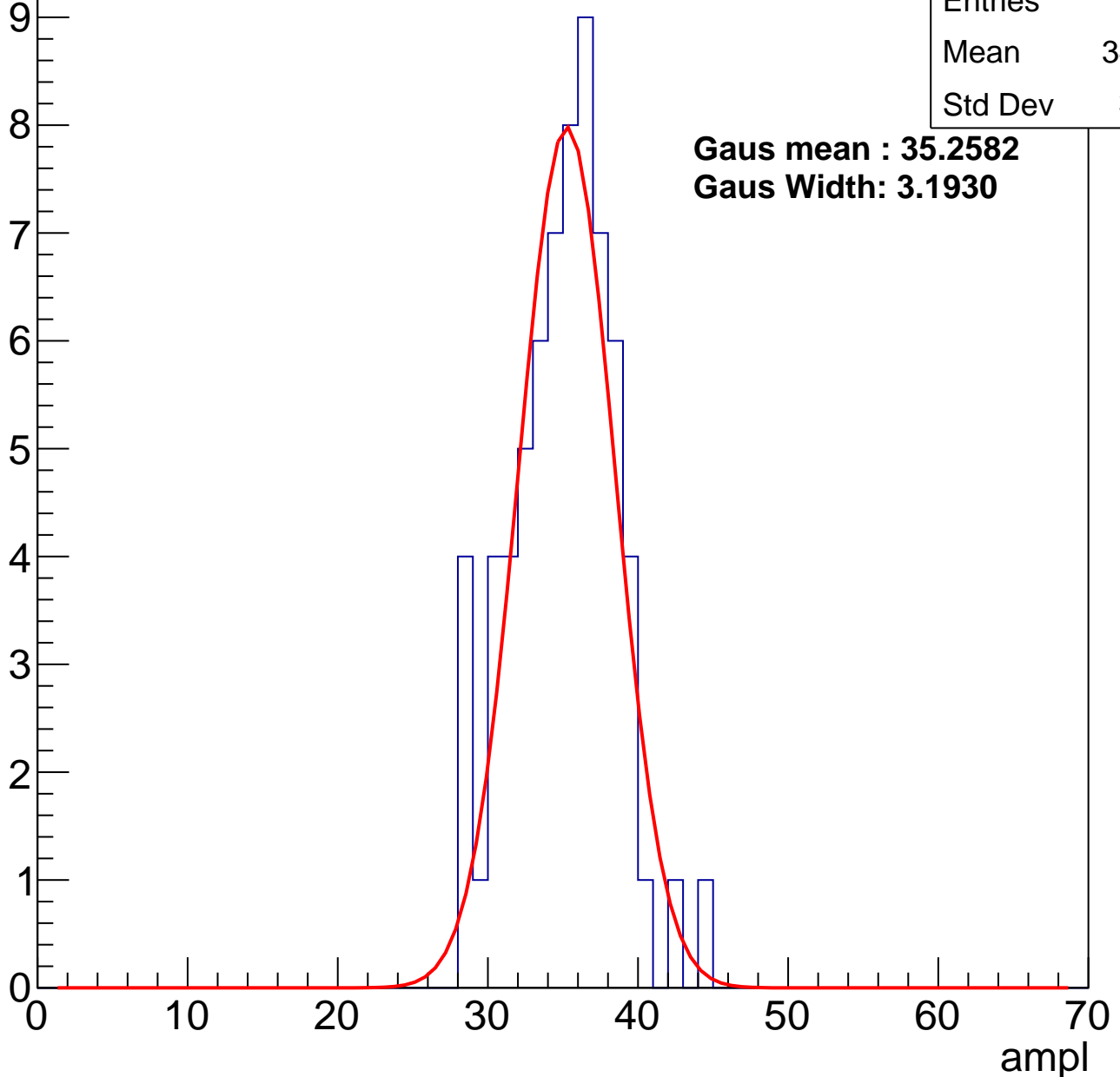
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.62
Std Dev	3.37

Gaus mean : 35.2582

Gaus Width: 3.1930



B1L102S, U14-ch55, adc2

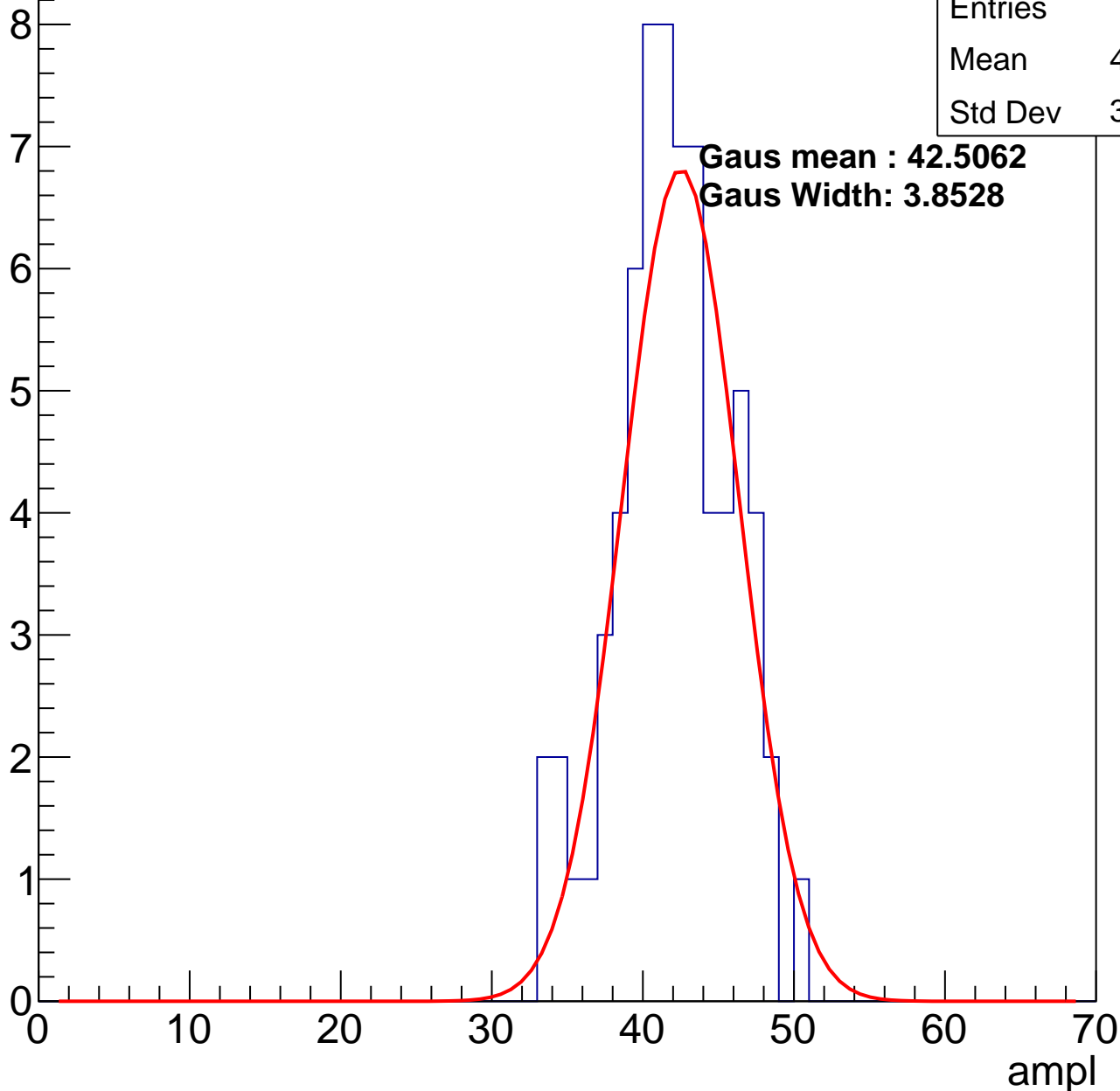
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.52
Std Dev	3.744

Gaus mean : 42.5062

Gaus Width: 3.8528

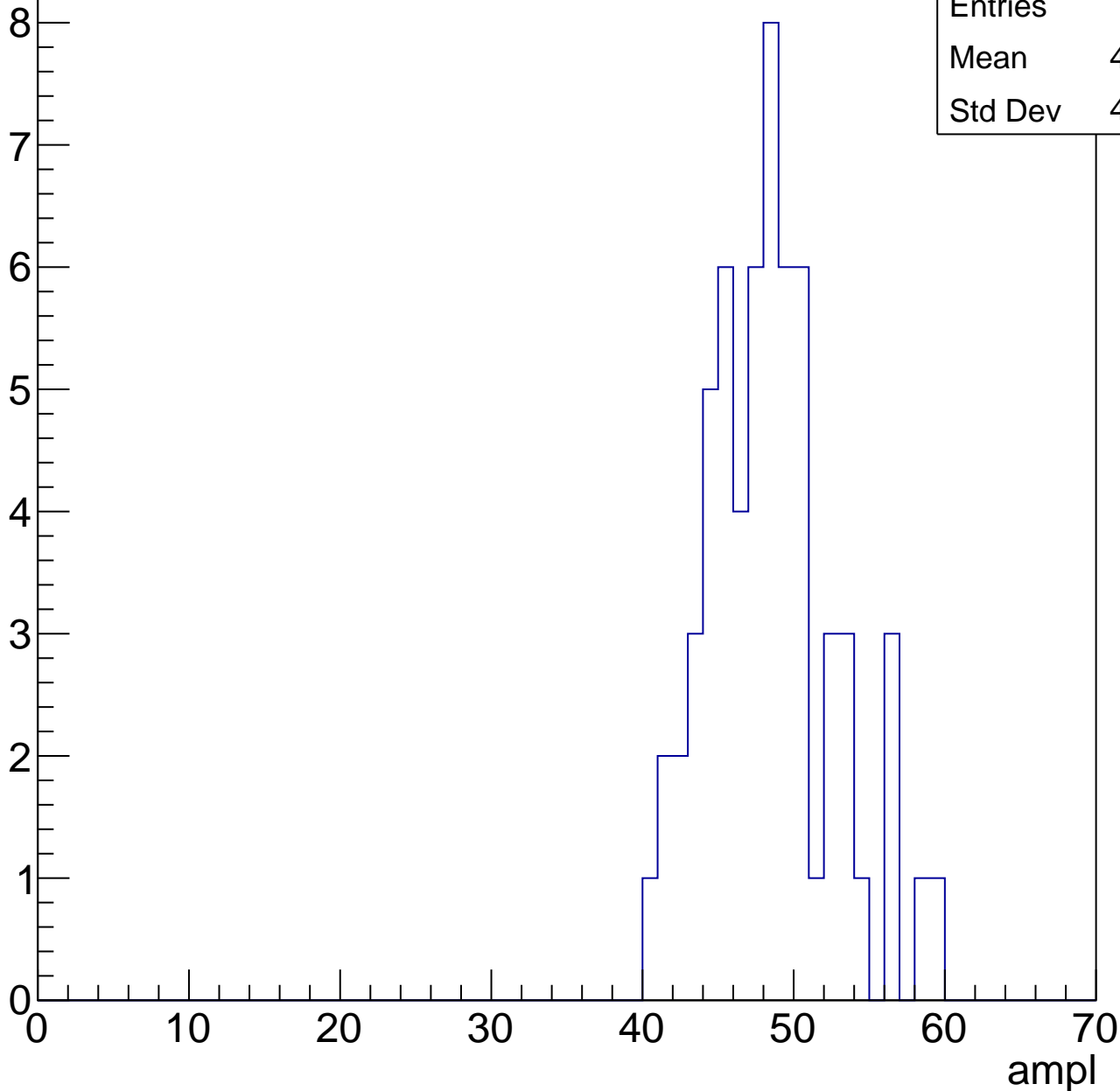


B1L102S, U14-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	47.97
Std Dev	4.166

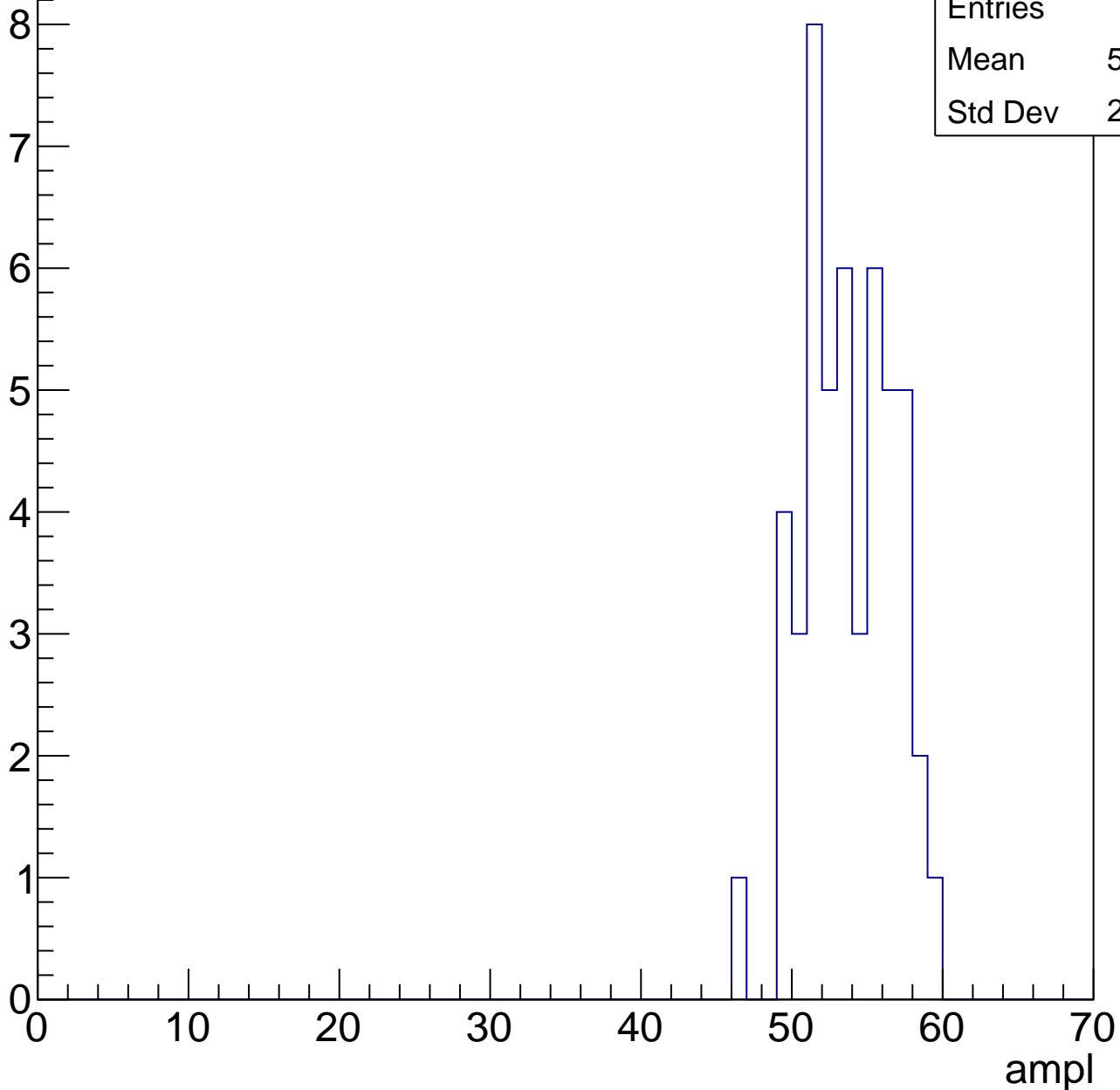


B1L102S, U14-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.27
Std Dev	2.898

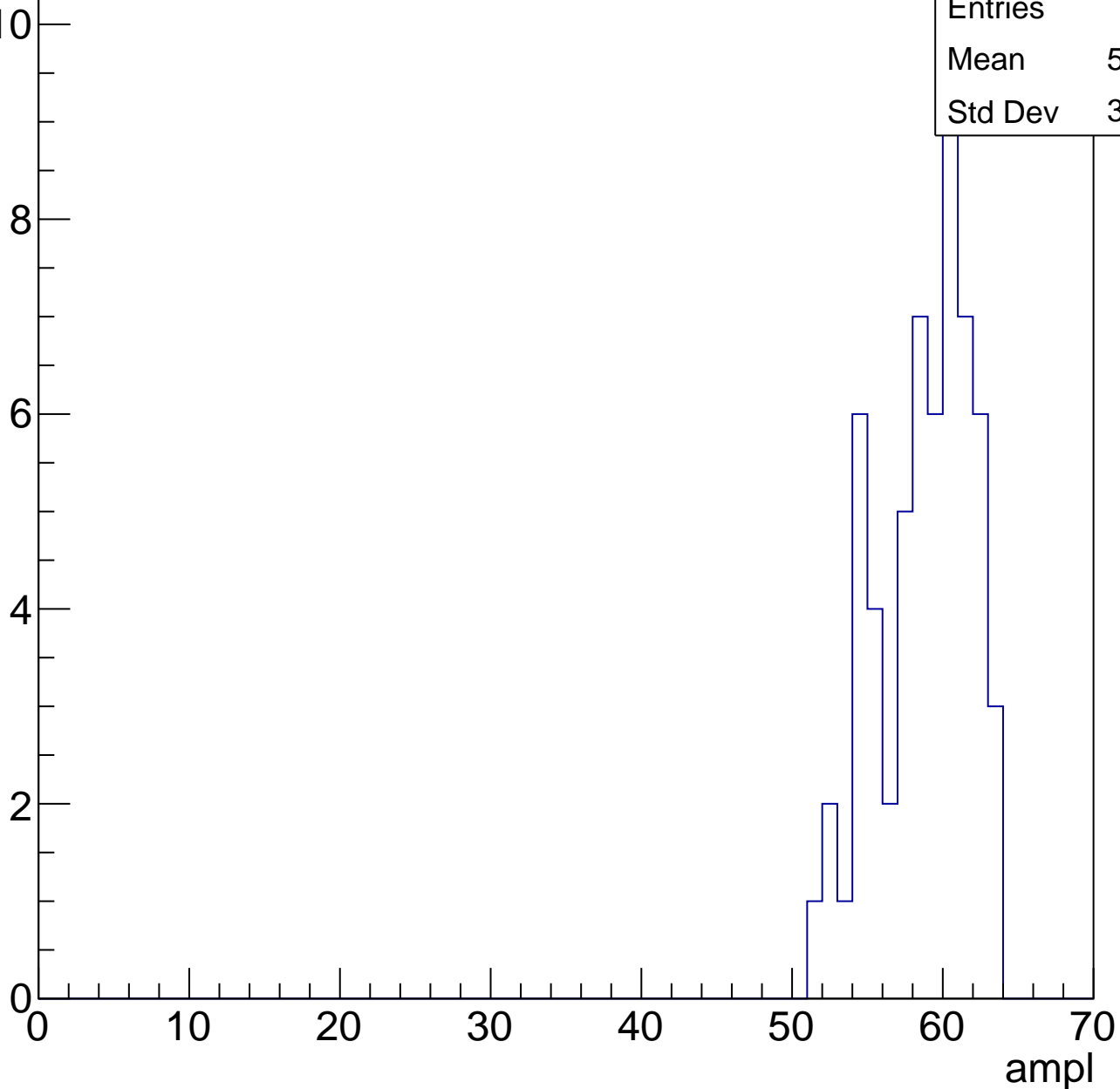


B1L102S, U14-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.28
Std Dev	3.072

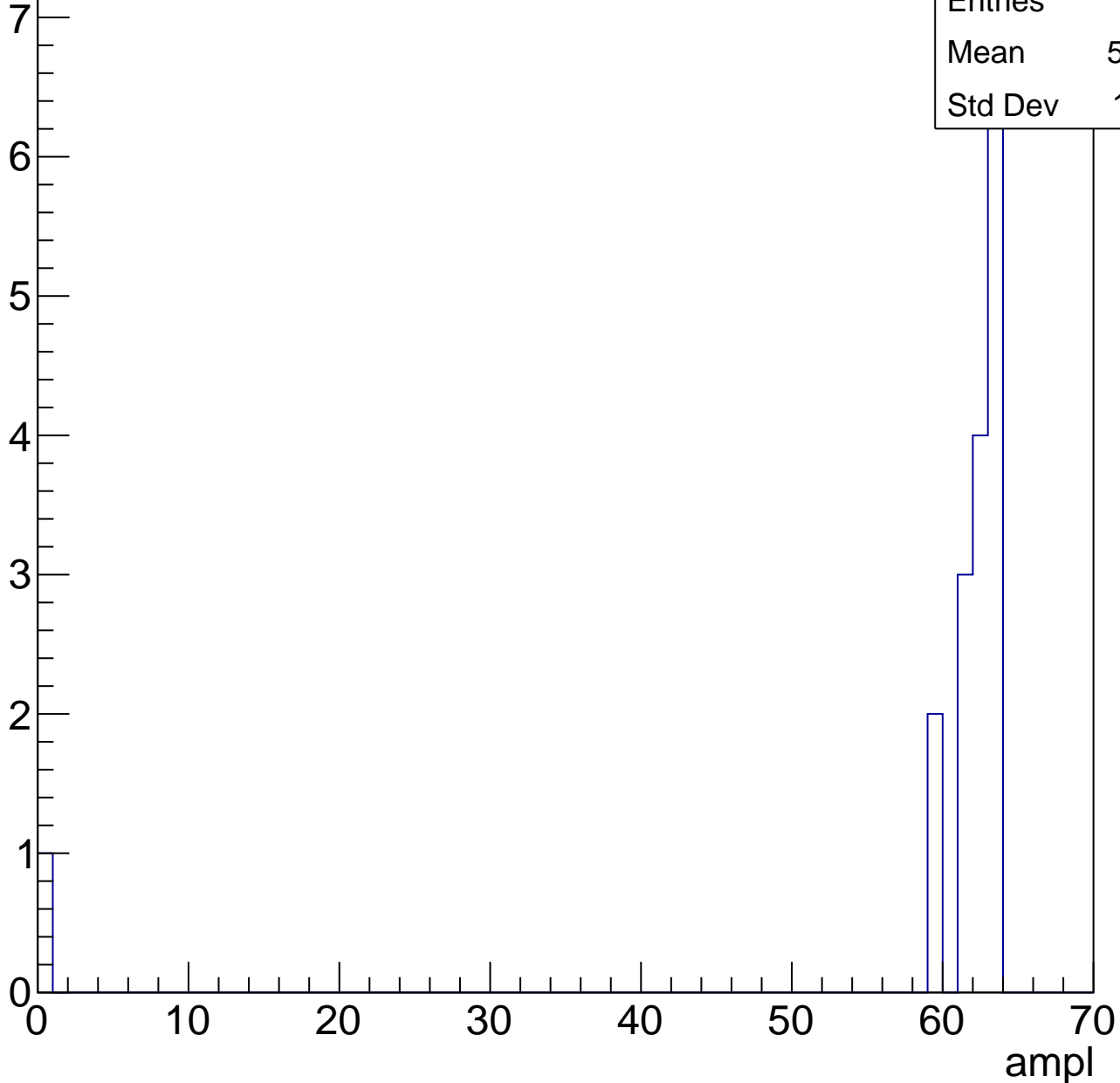


B1L102S, U14-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	58.24
Std Dev	14.61



B1L102S, U14-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch56, adc0

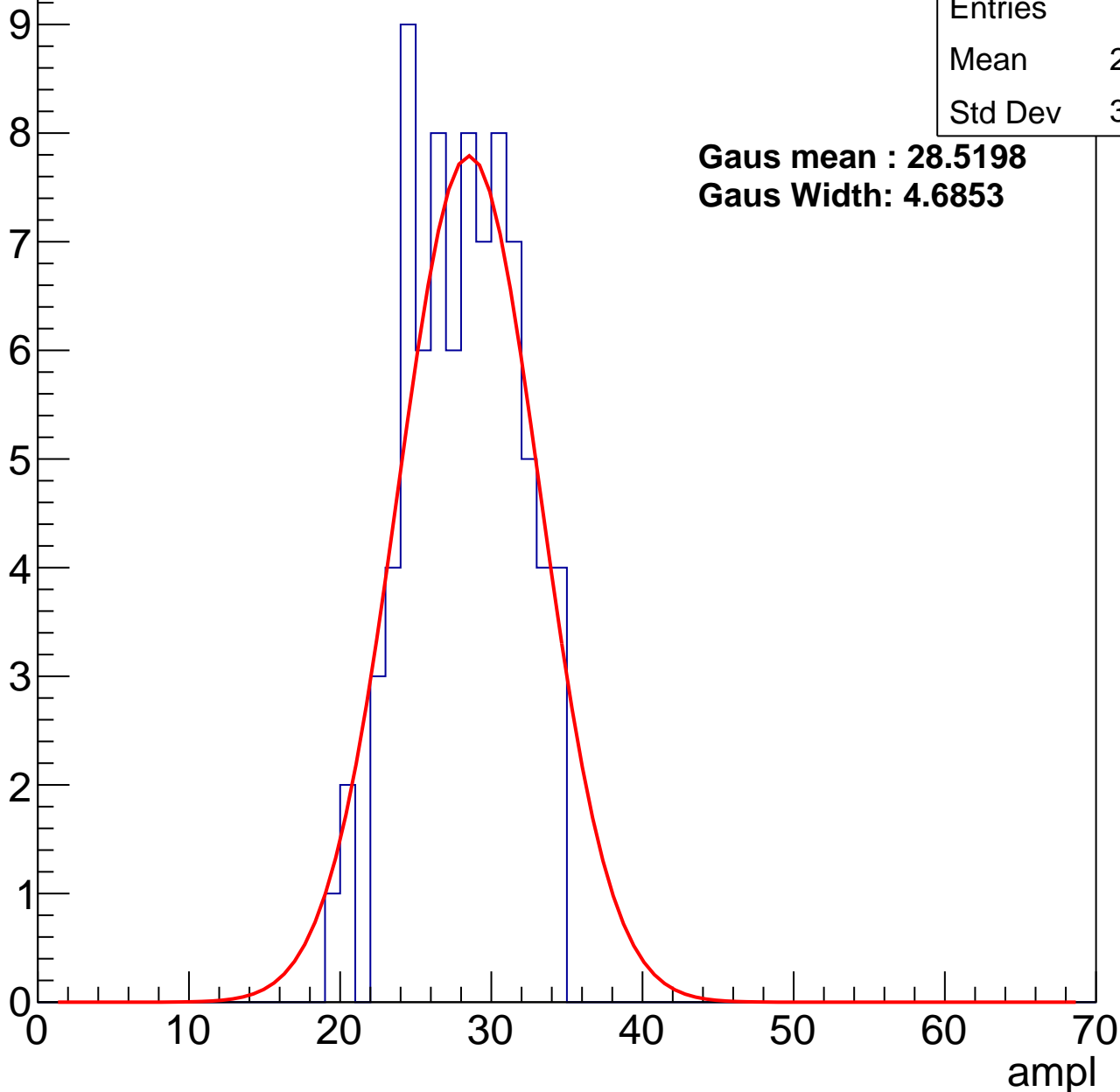
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	27.62
Std Dev	3.608

Gaus mean : 28.5198

Gaus Width: 4.6853



B1L102S, U14-ch56, adc1

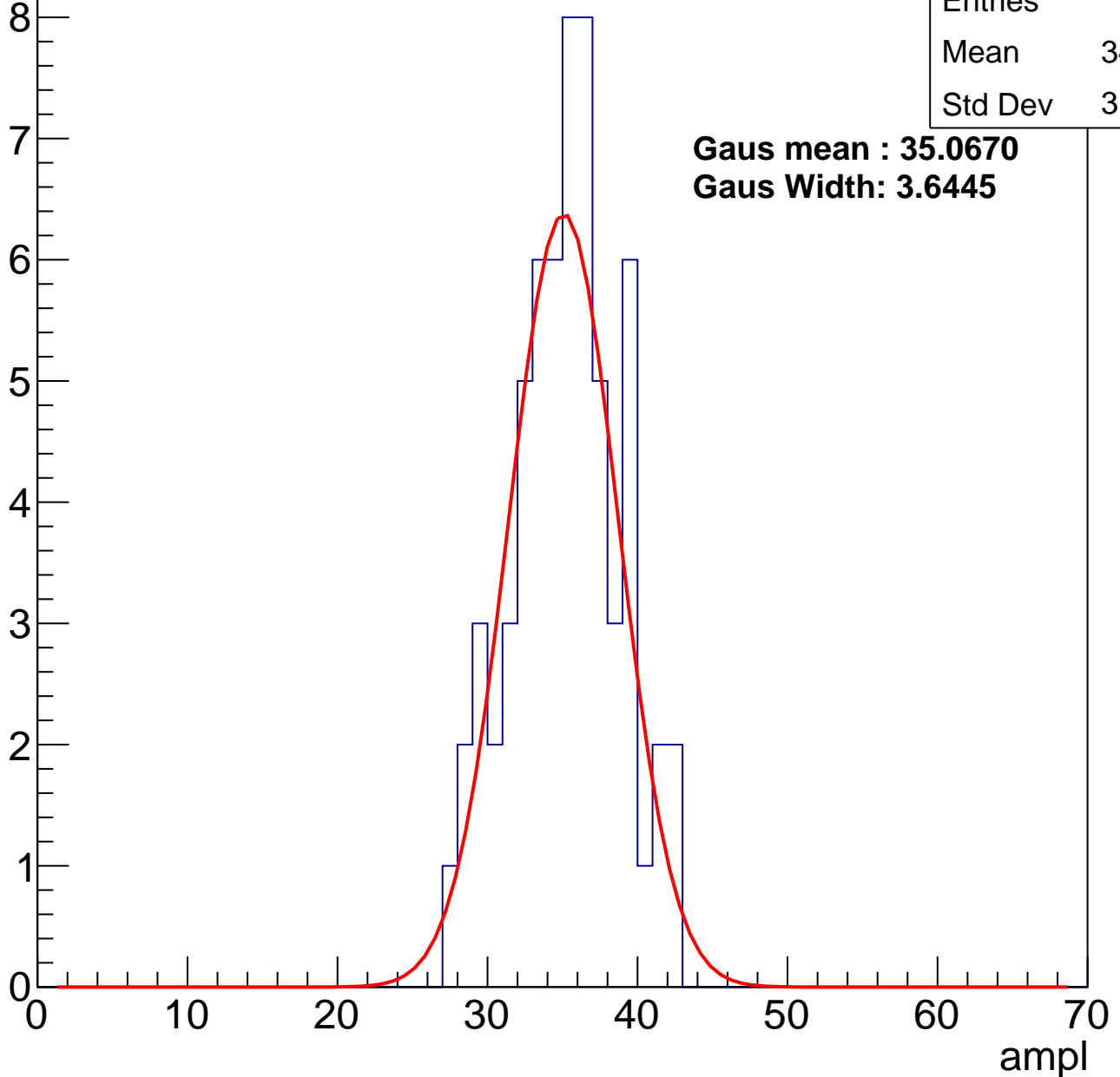
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.79
Std Dev	3.533

Gaus mean : 35.0670

Gaus Width: 3.6445



B1L102S, U14-ch56, adc2

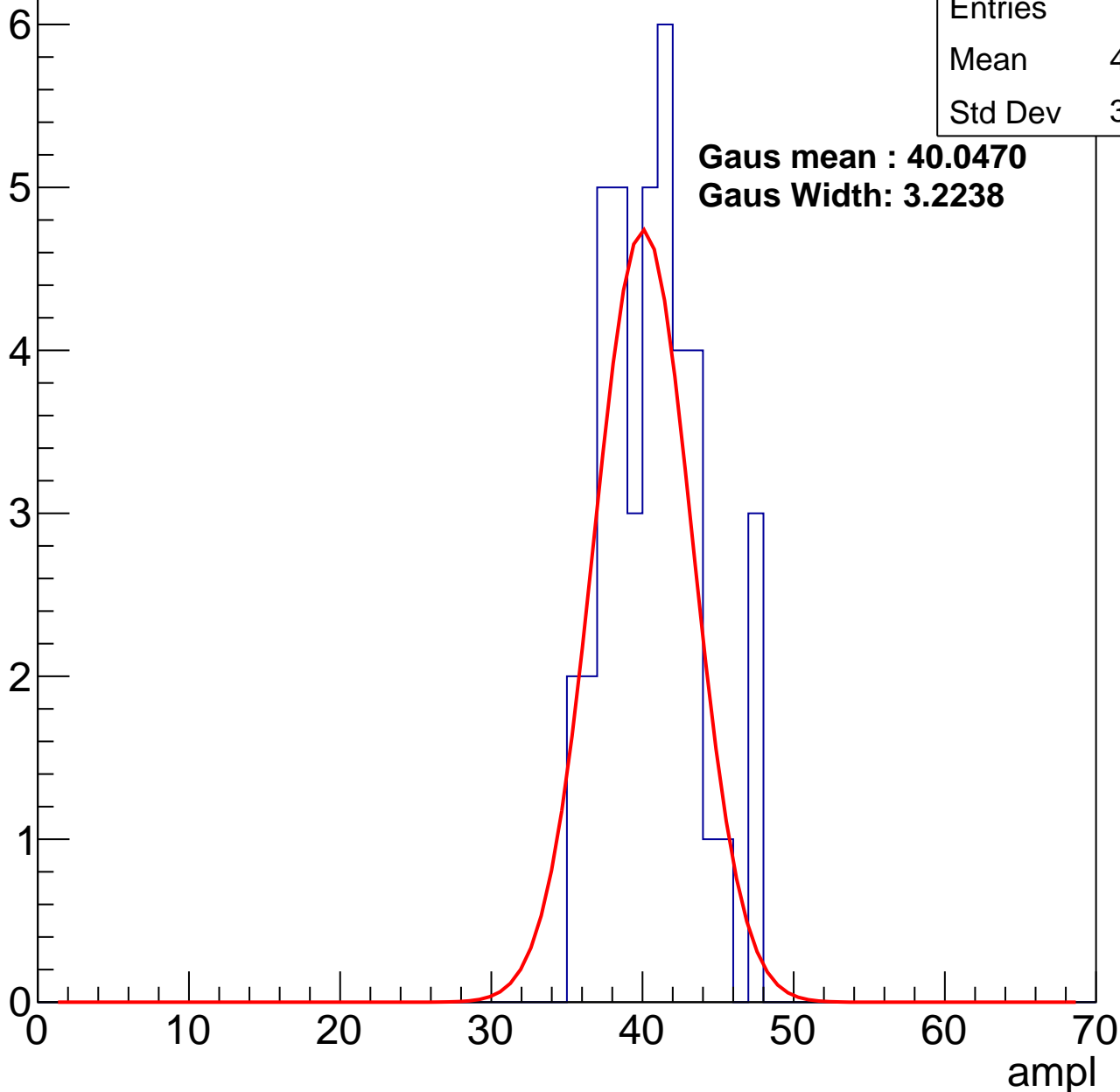
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	40.24
Std Dev	3.098

Gaus mean : 40.0470

Gaus Width: 3.2238



B1L102S, U14-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	45.9
Std Dev	3.564

Entry

10

8

6

4

2

0

0

10

20

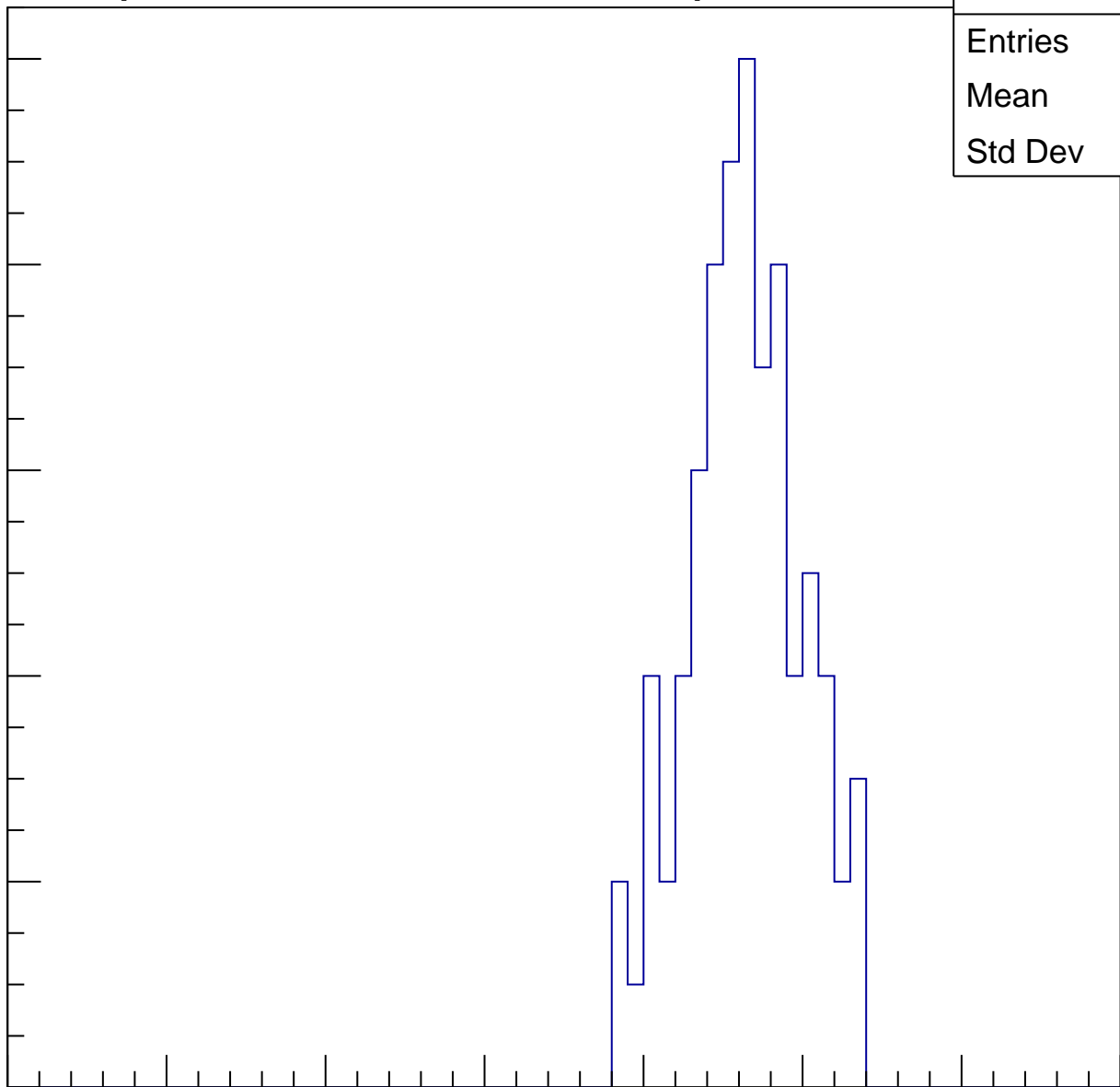
30

40

50

60

ampl

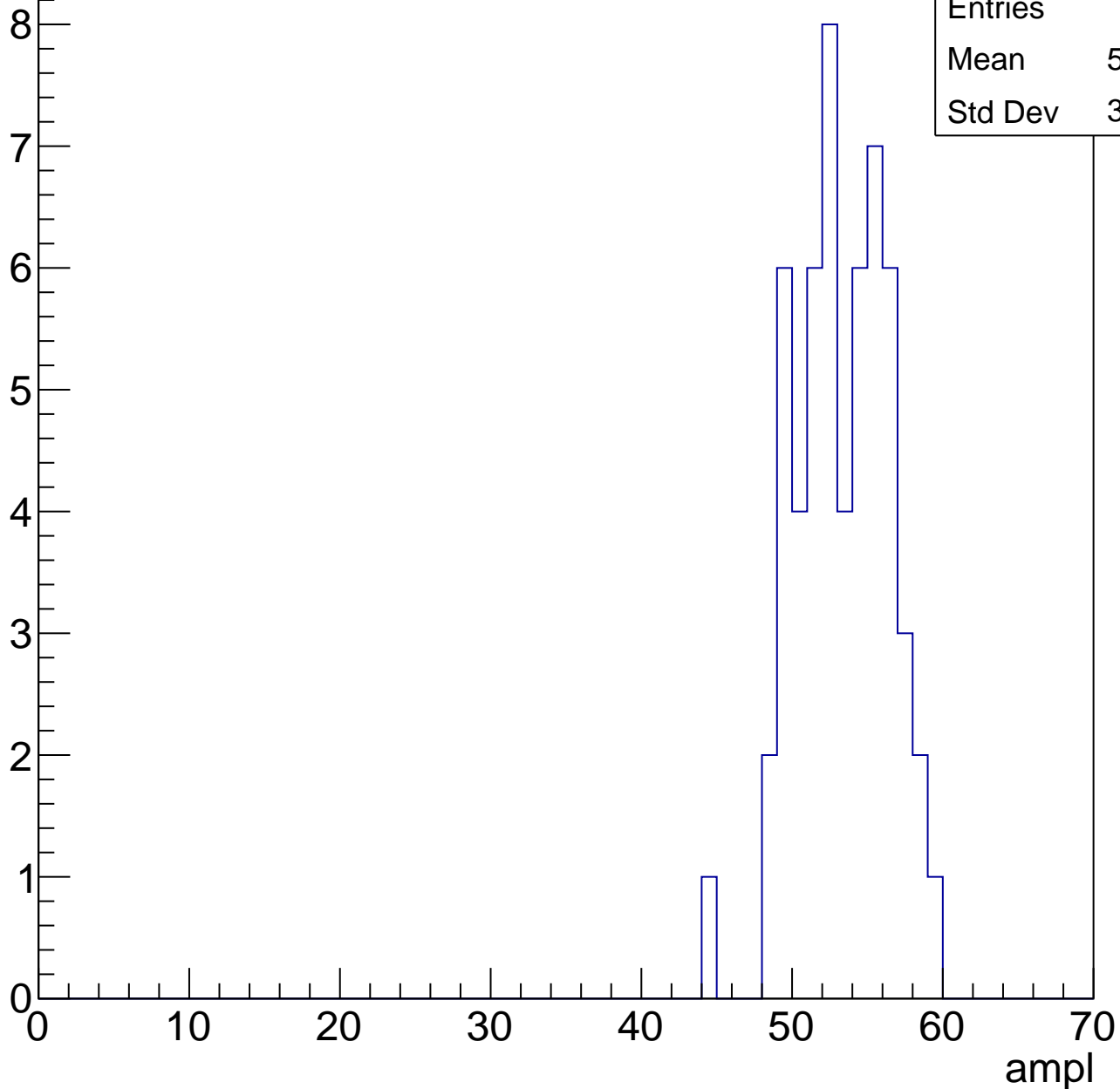


B1L102S, U14-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	52.84
Std Dev	3.034

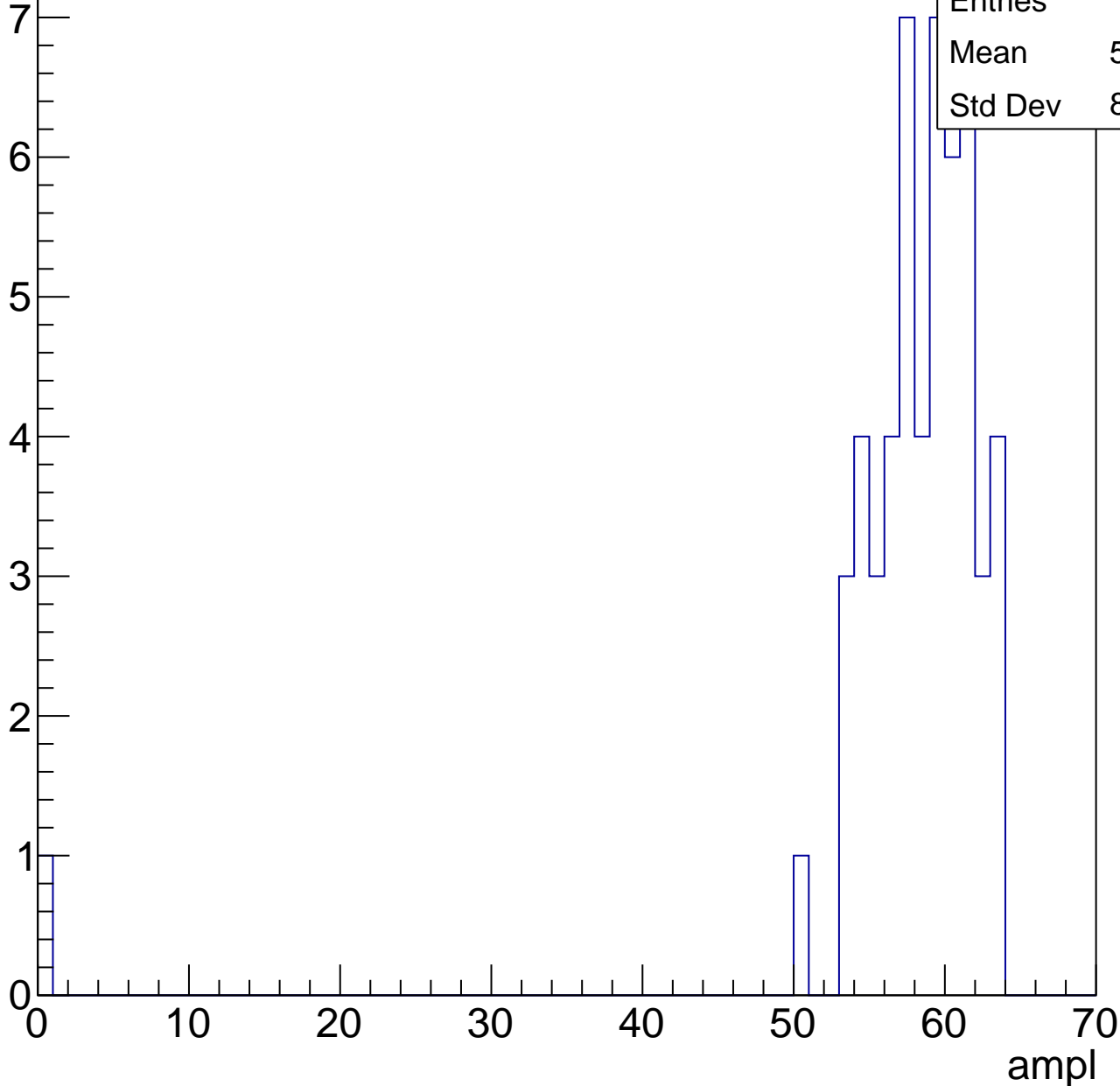


B1L102S, U14-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.09
Std Dev	8.405

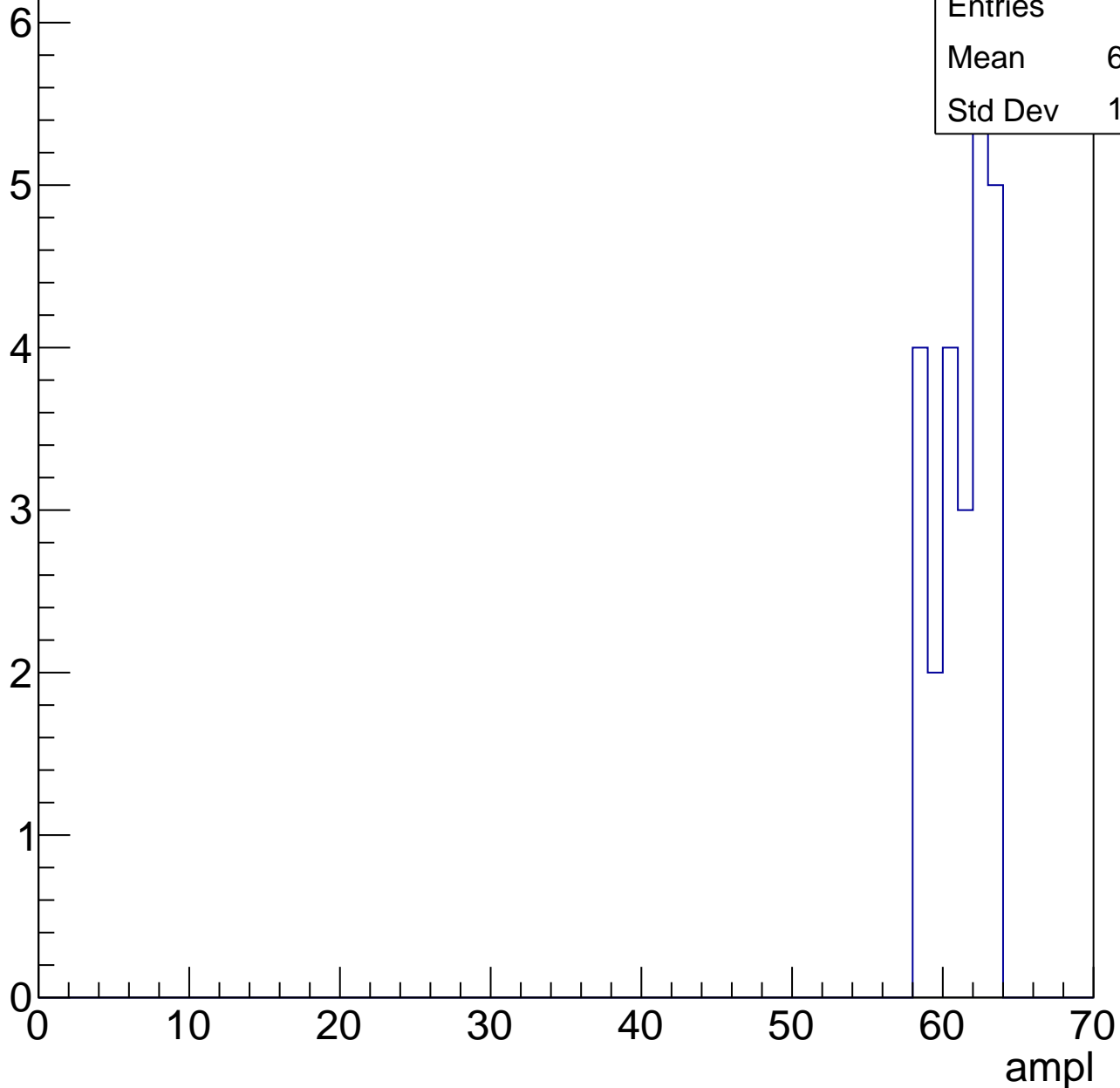


B1L102S, U14-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

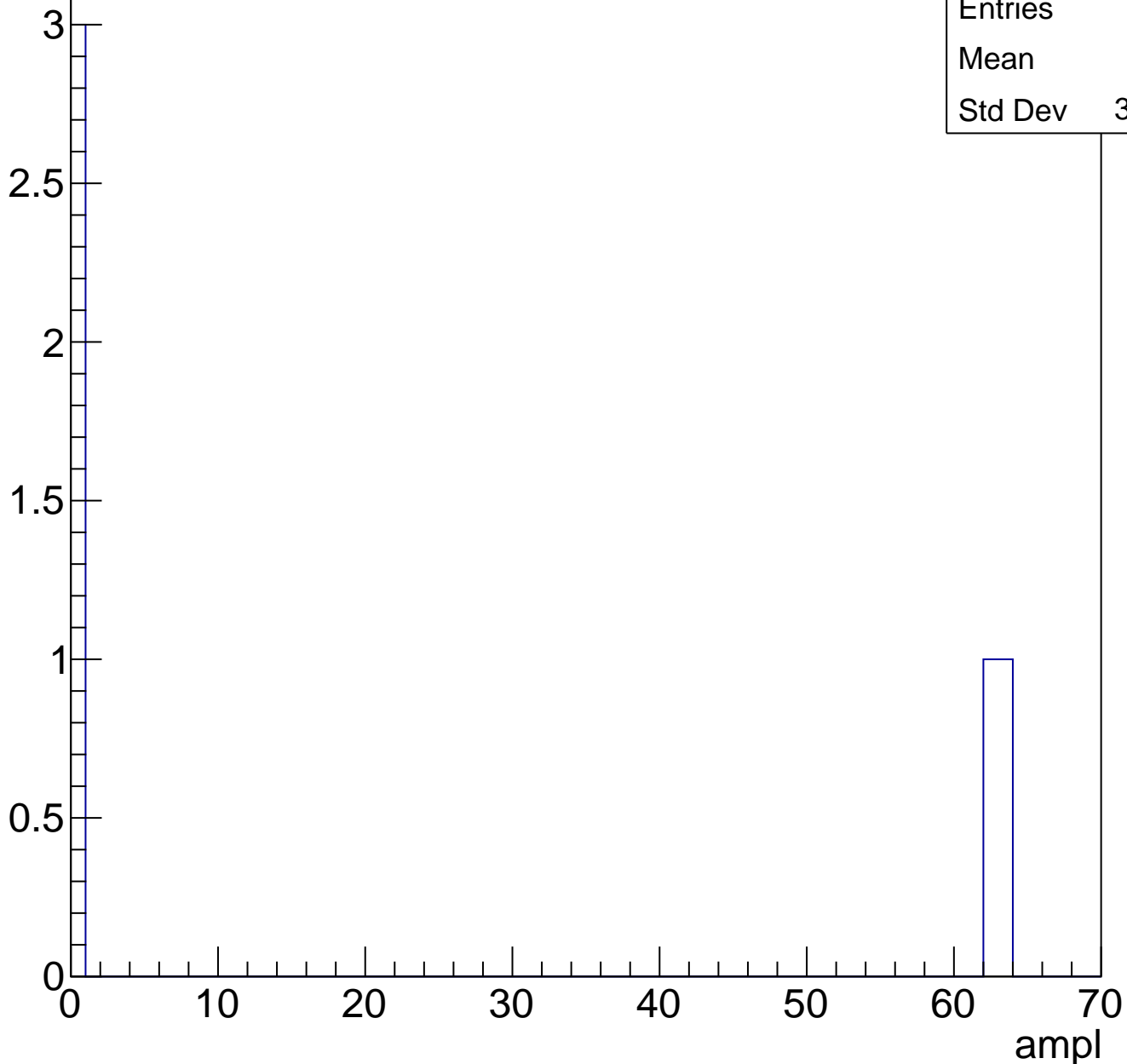
Entries	24
Mean	60.83
Std Dev	1.748



B1L102S, U14-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch57, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	26.08
Std Dev	6.69

Gaus mean : 28.1234

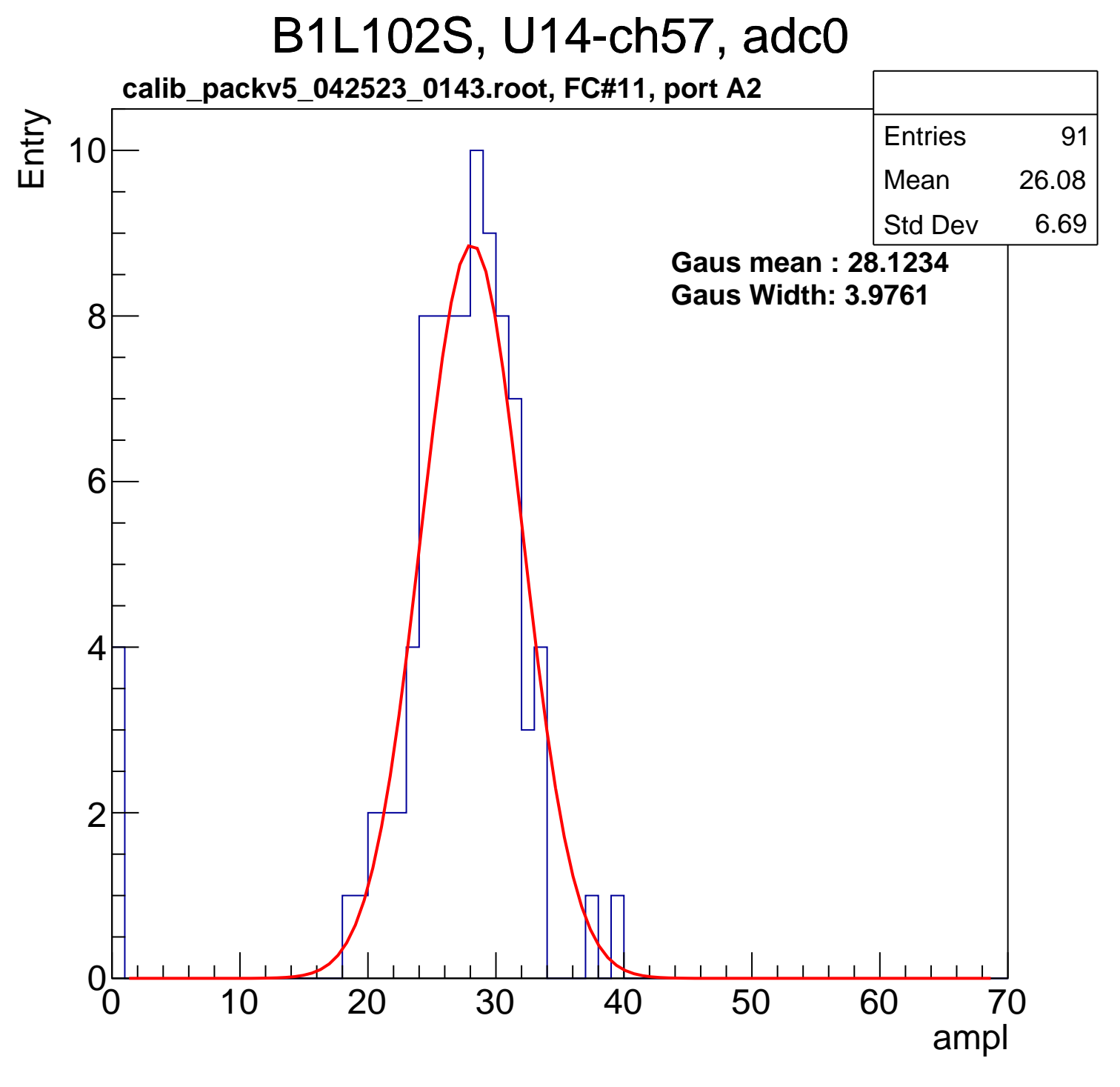
Gaus Width: 3.9761

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch57, adc1

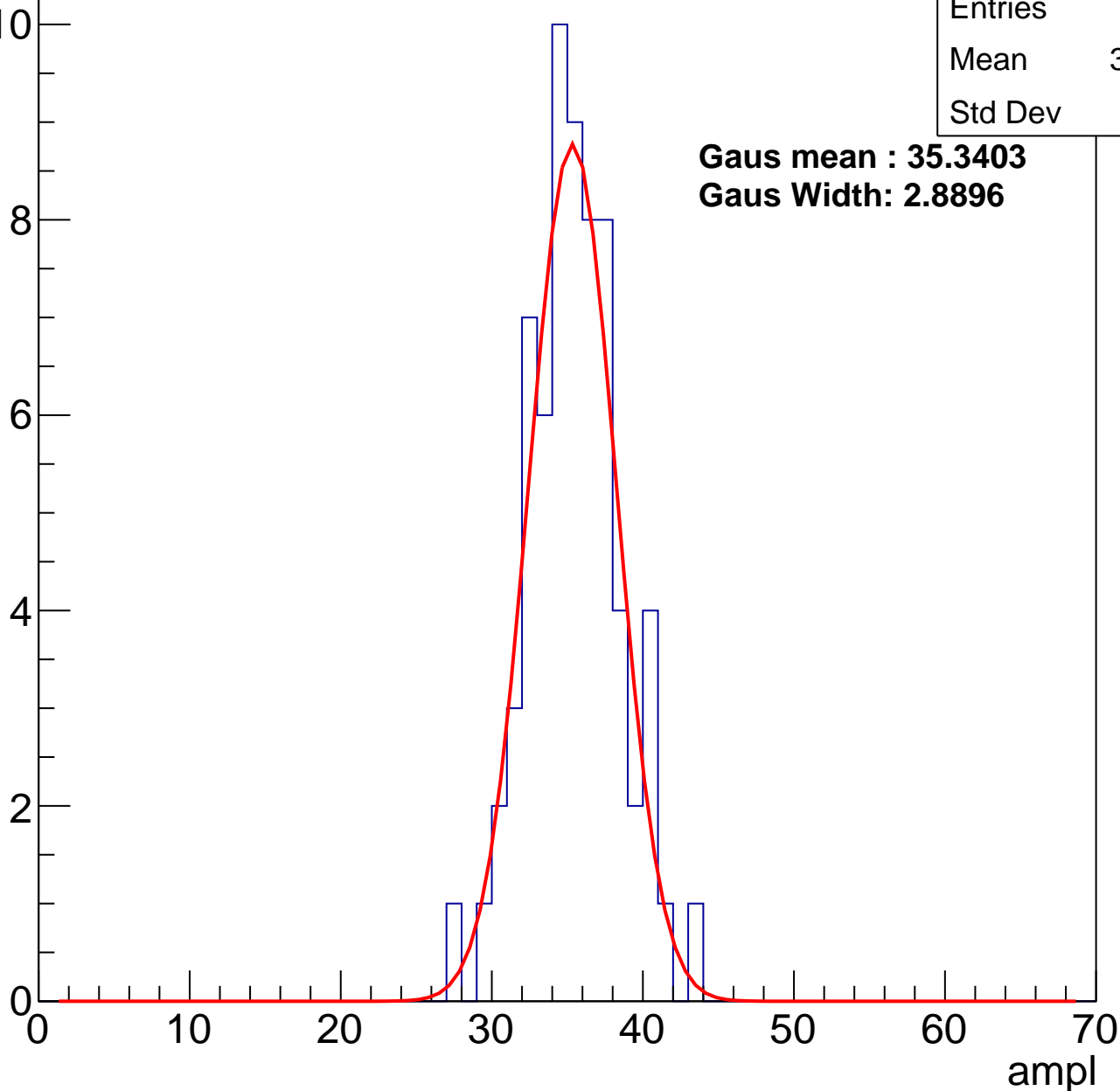
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.99
Std Dev	3

Gaus mean : 35.3403

Gaus Width: 2.8896



B1L102S, U14-ch57, adc2

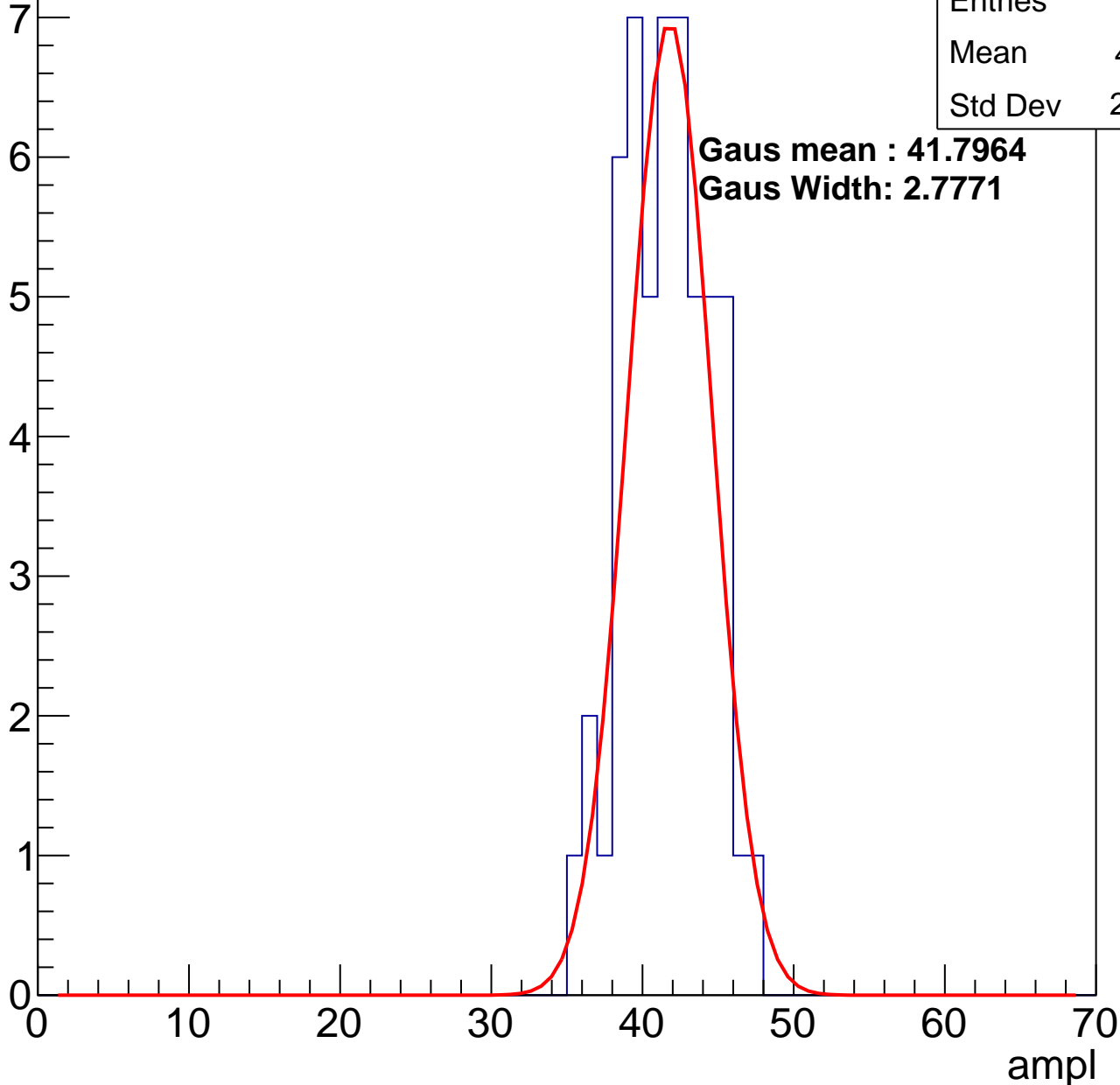
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.11
Std Dev	2.752

Gaus mean : 41.7964

Gaus Width: 2.7771

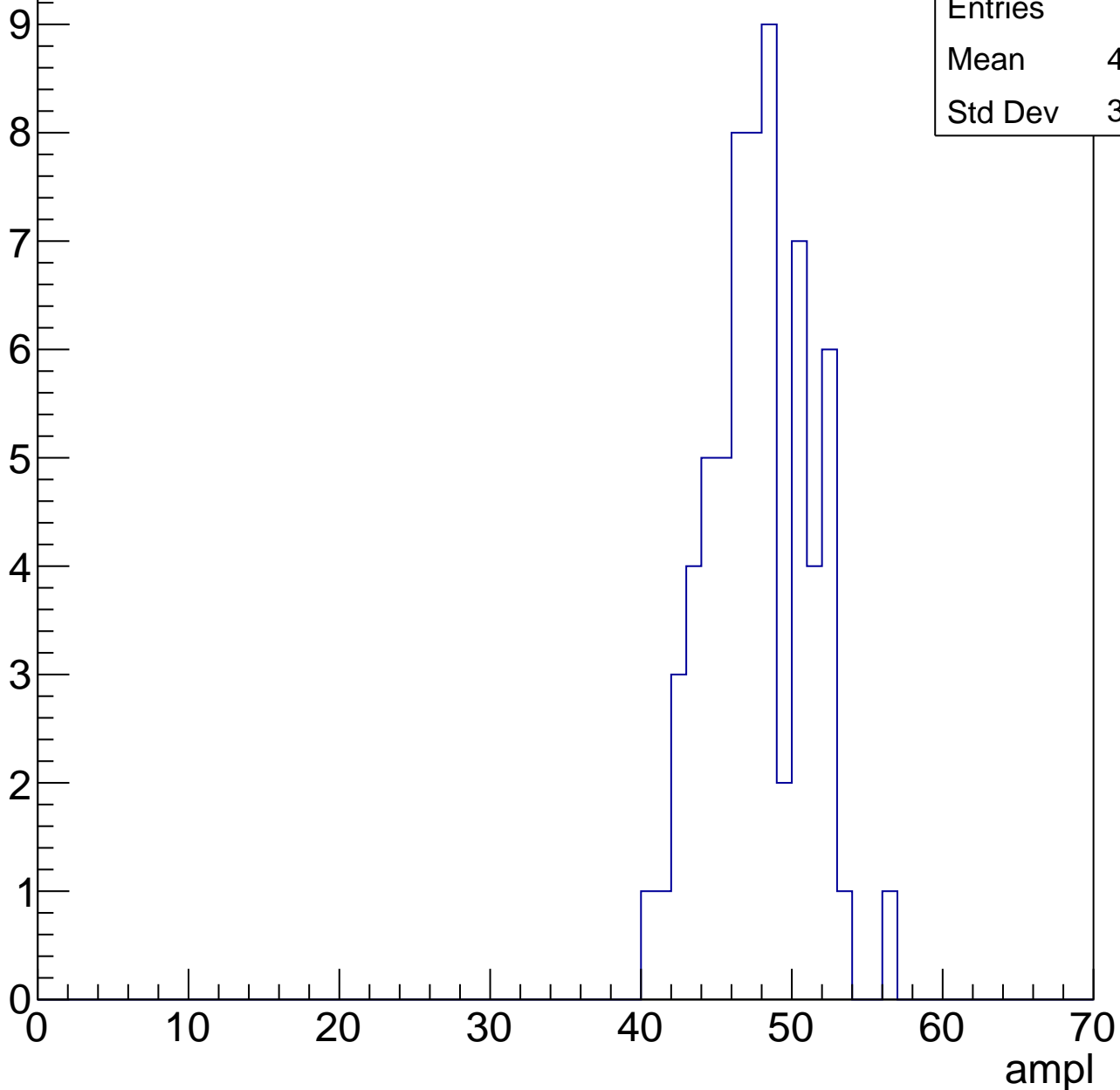


B1L102S, U14-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.28
Std Dev	3.289

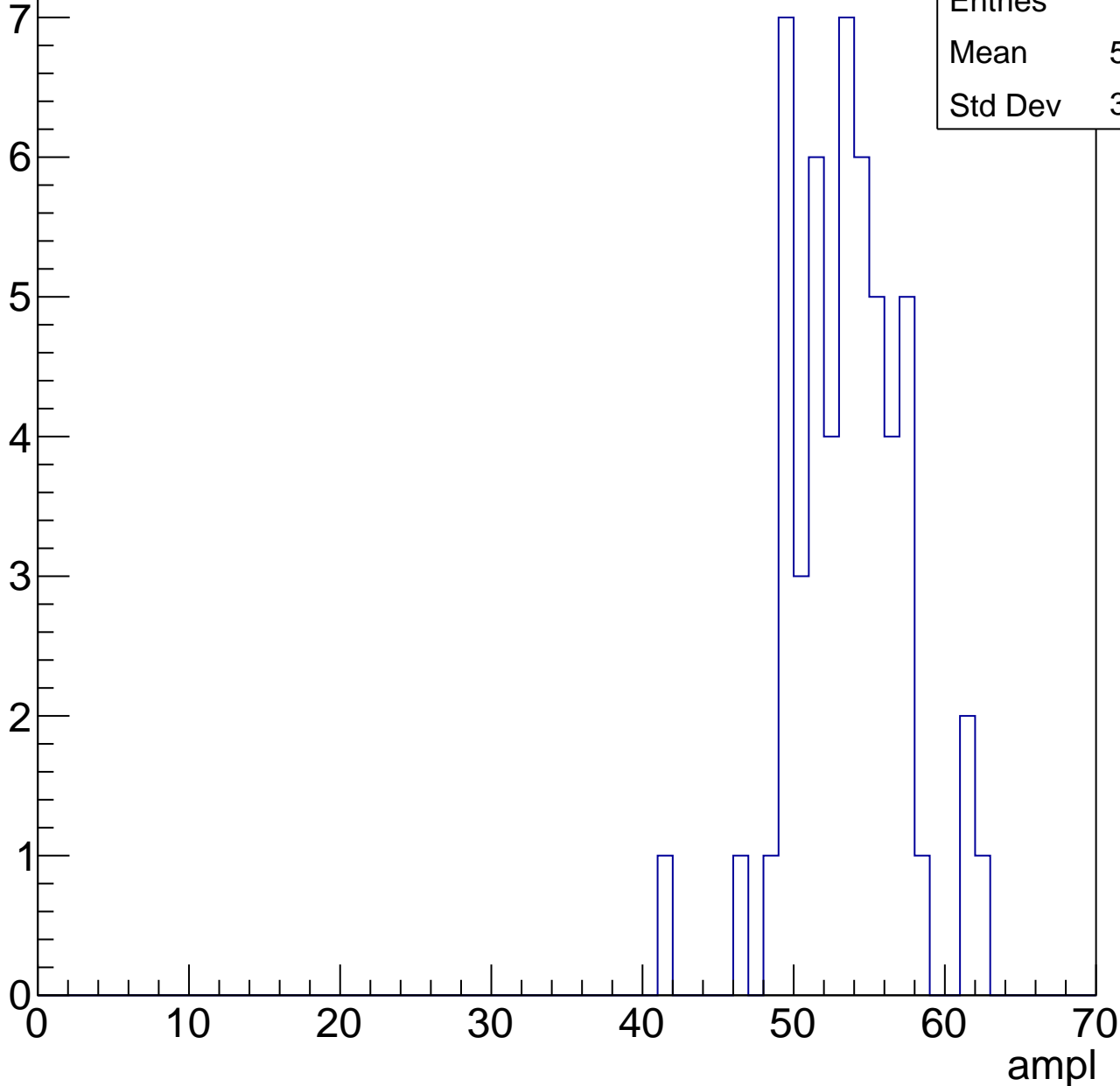


B1L102S, U14-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

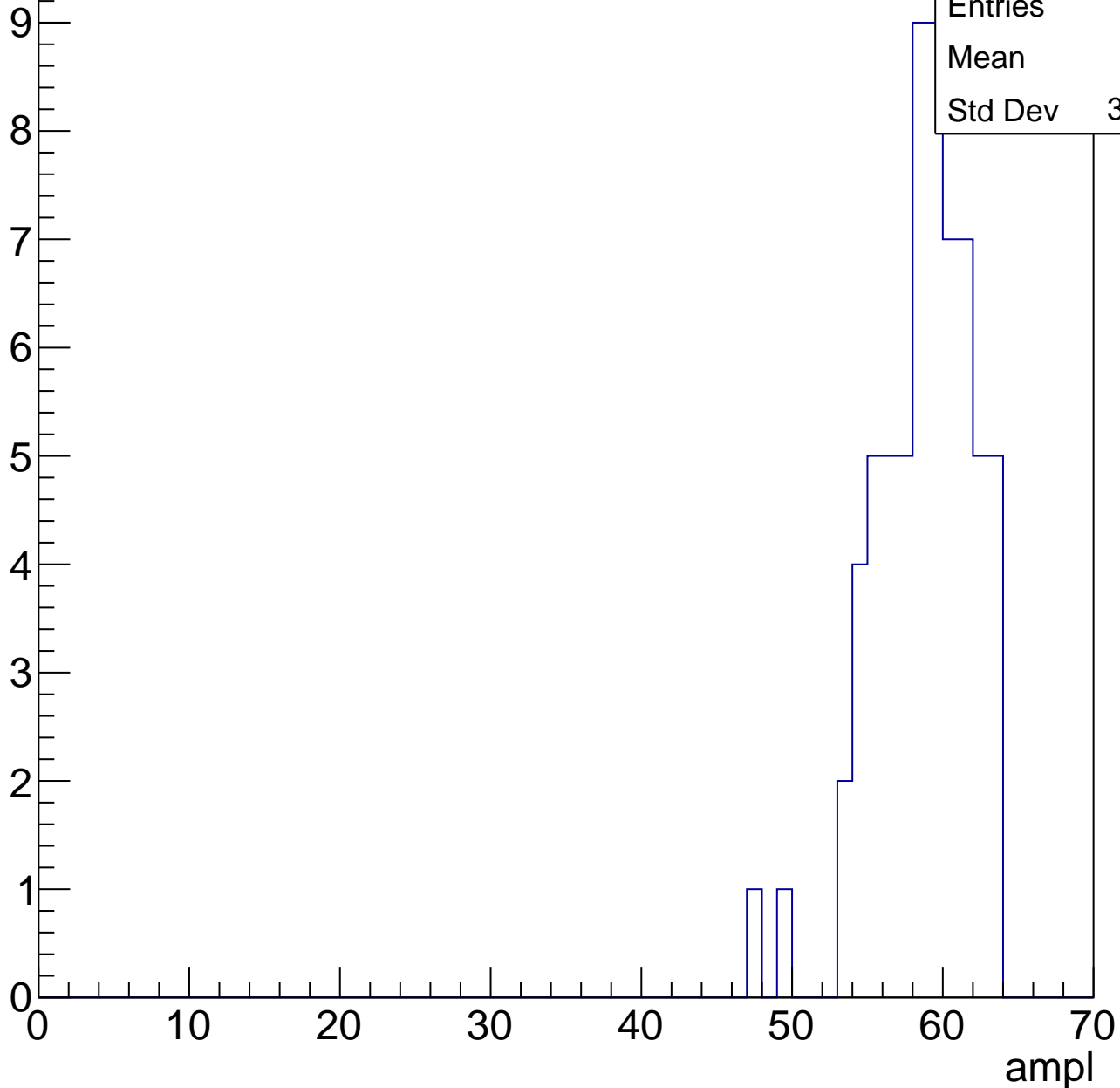
Entries	54
Mean	53.02
Std Dev	3.754



B1L102S, U14-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

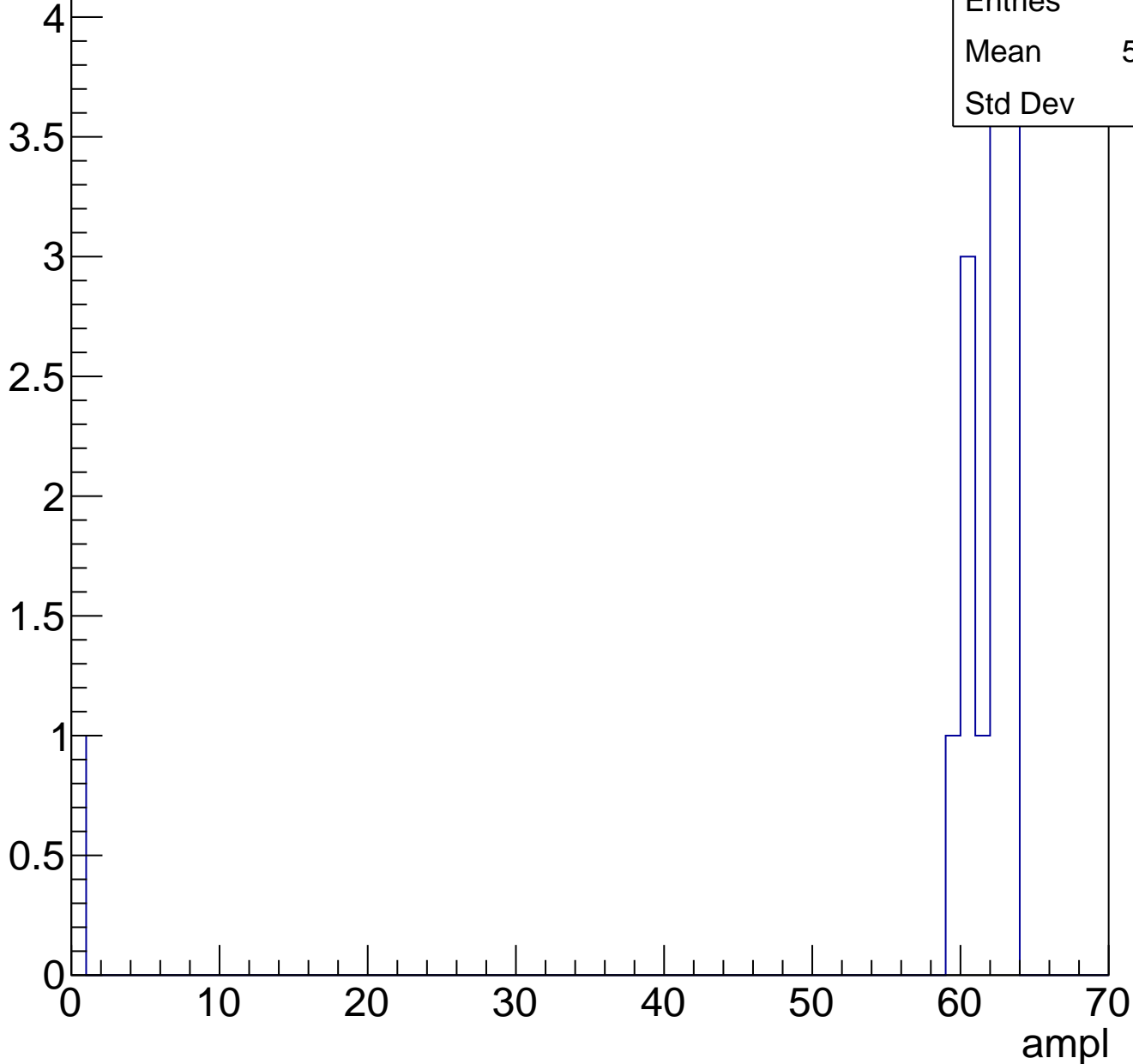
Entry



B1L102S, U14-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

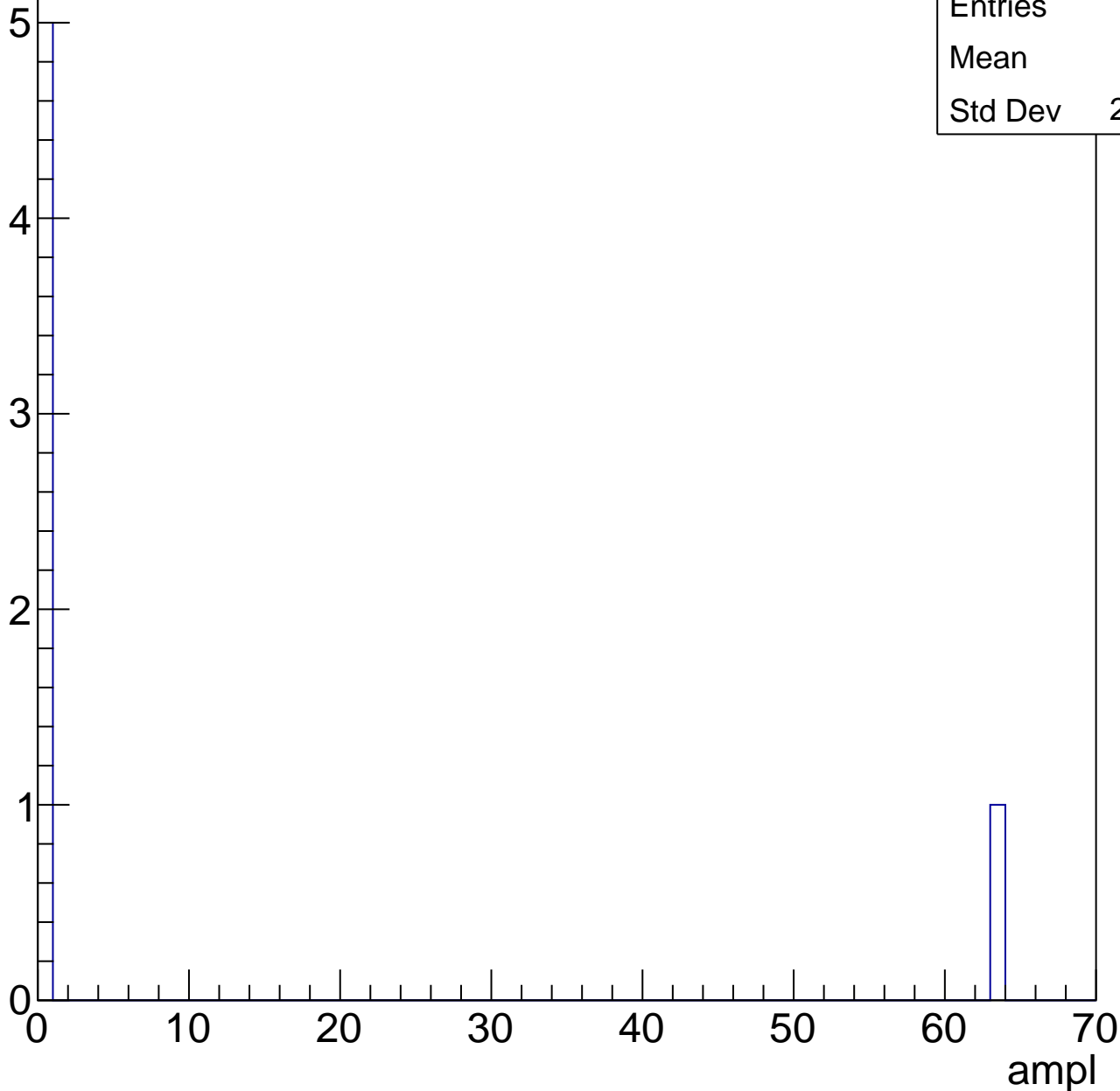


B1L102S, U14-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	6
Mean	10.5
Std Dev	23.48



B1L102S, U14-ch58, adc0

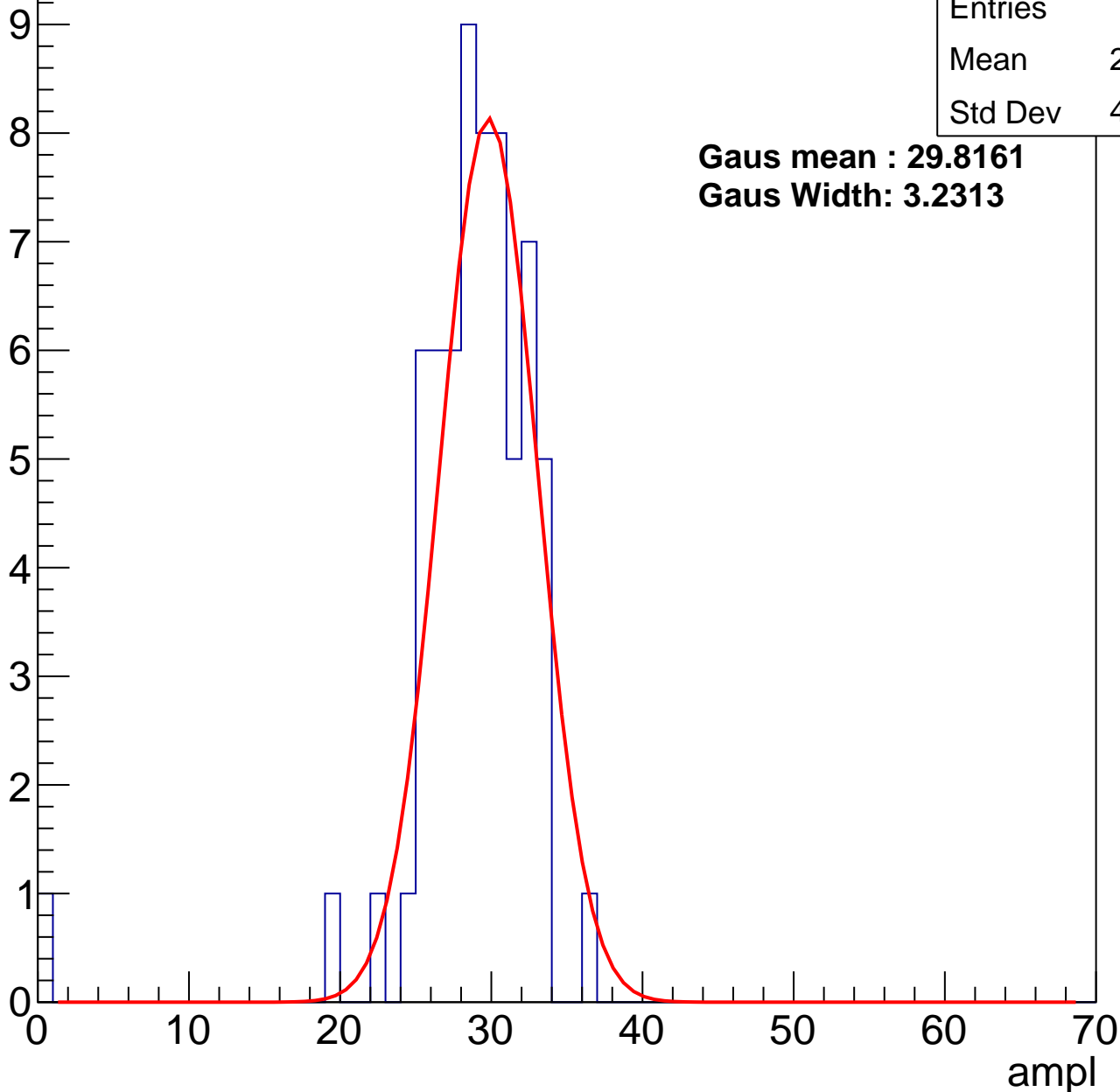
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.26
Std Dev	4.612

Gaus mean : 29.8161

Gaus Width: 3.2313



B1L102S, U14-ch58, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	55
Mean	35.75
Std Dev	3.088

Gaus mean : 35.6909

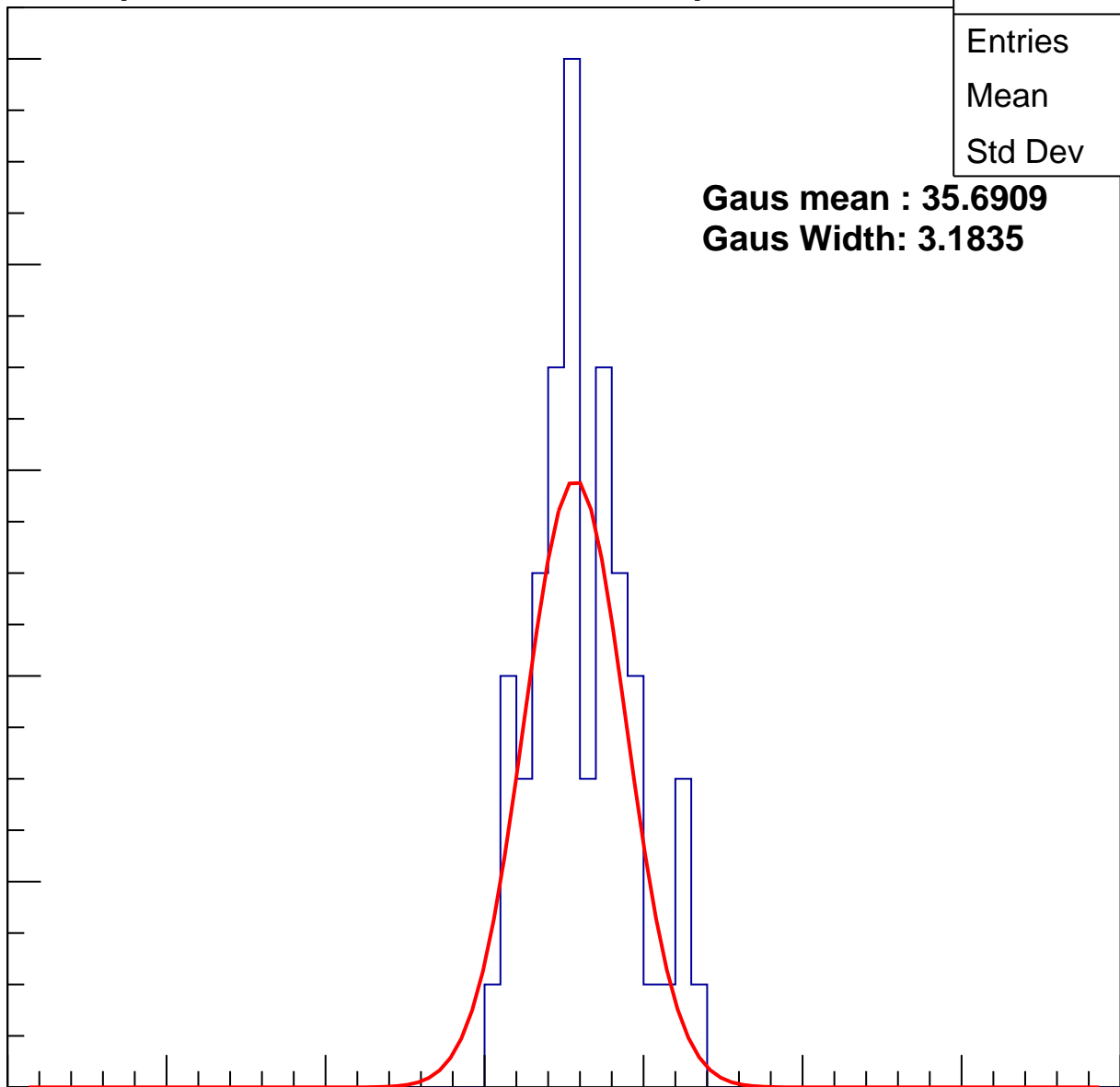
Gaus Width: 3.1835

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch58, adc2

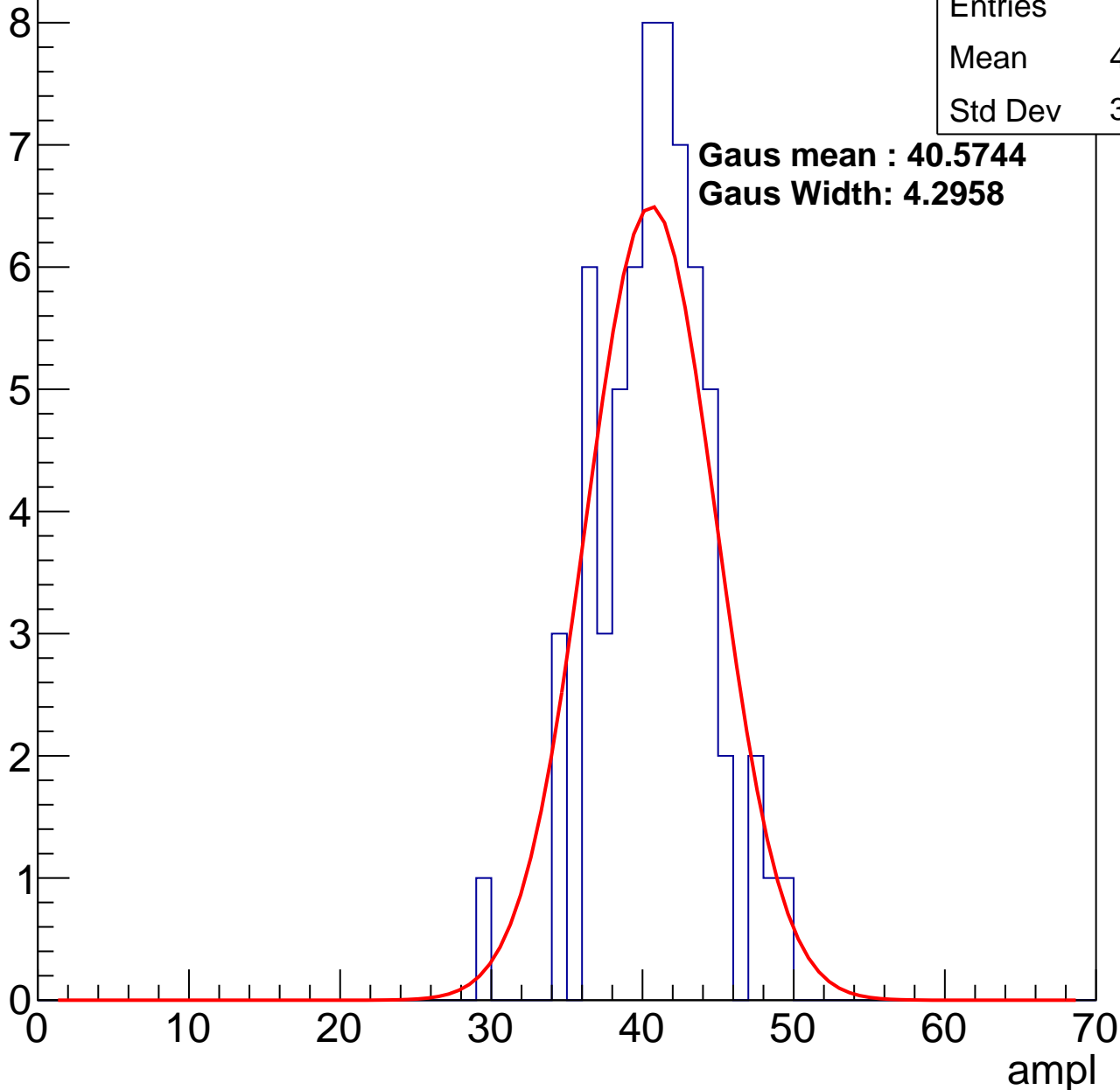
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	40.36
Std Dev	3.607

Gaus mean : 40.5744

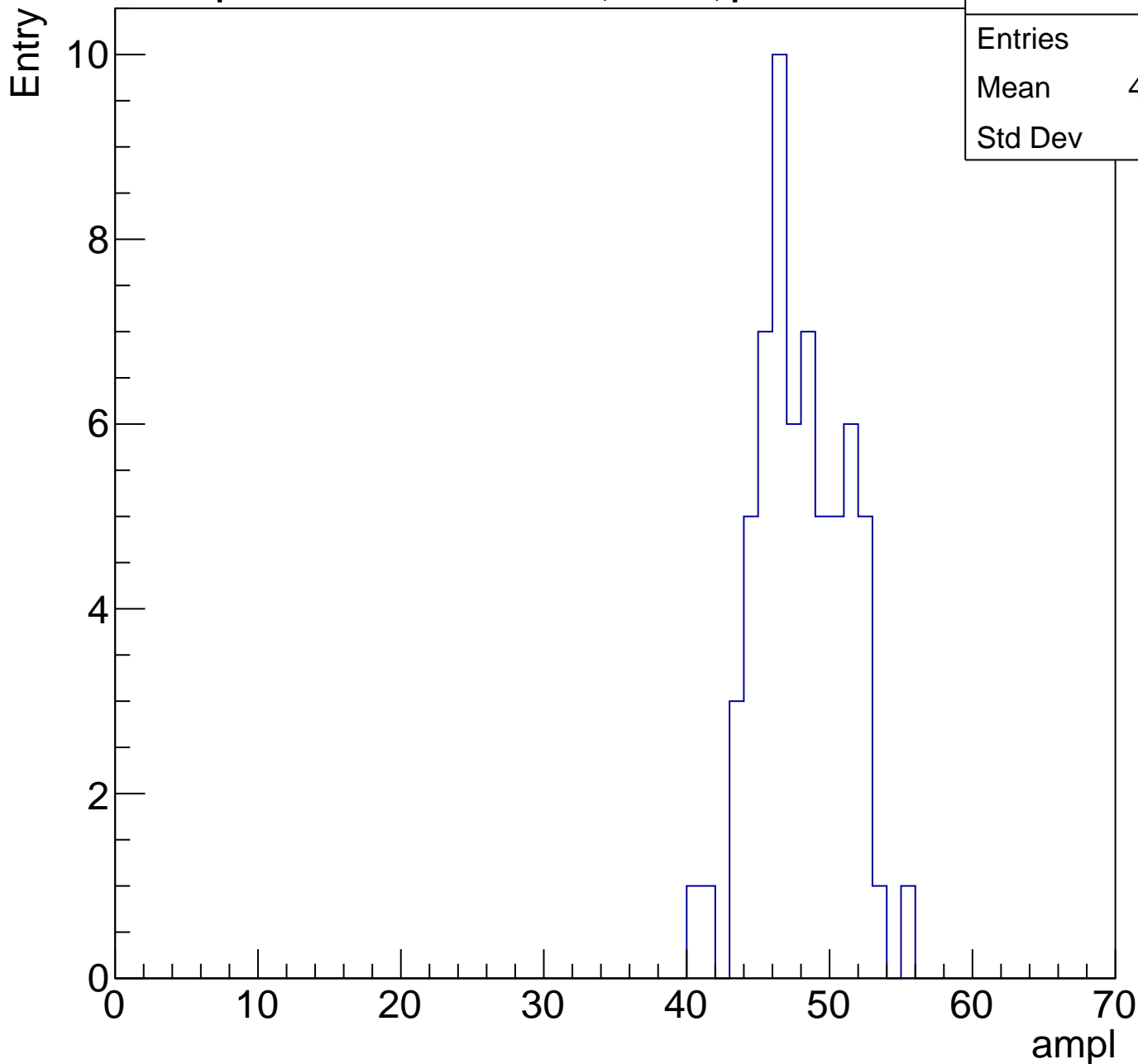
Gaus Width: 4.2958



B1L102S, U14-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	47.49
Std Dev	3.07

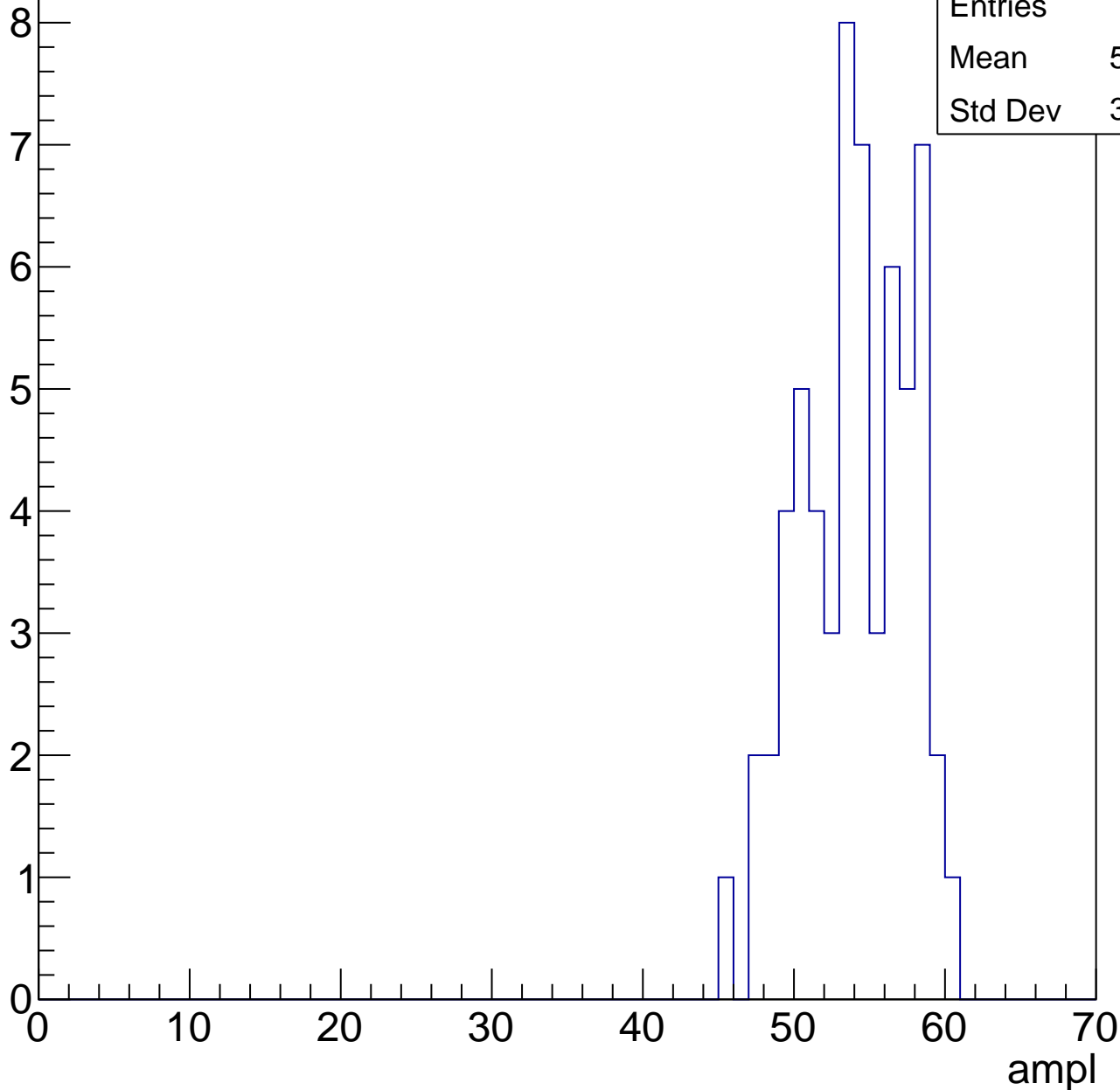


B1L102S, U14-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.55
Std Dev	3.523



B1L102S, U14-ch58, adc5

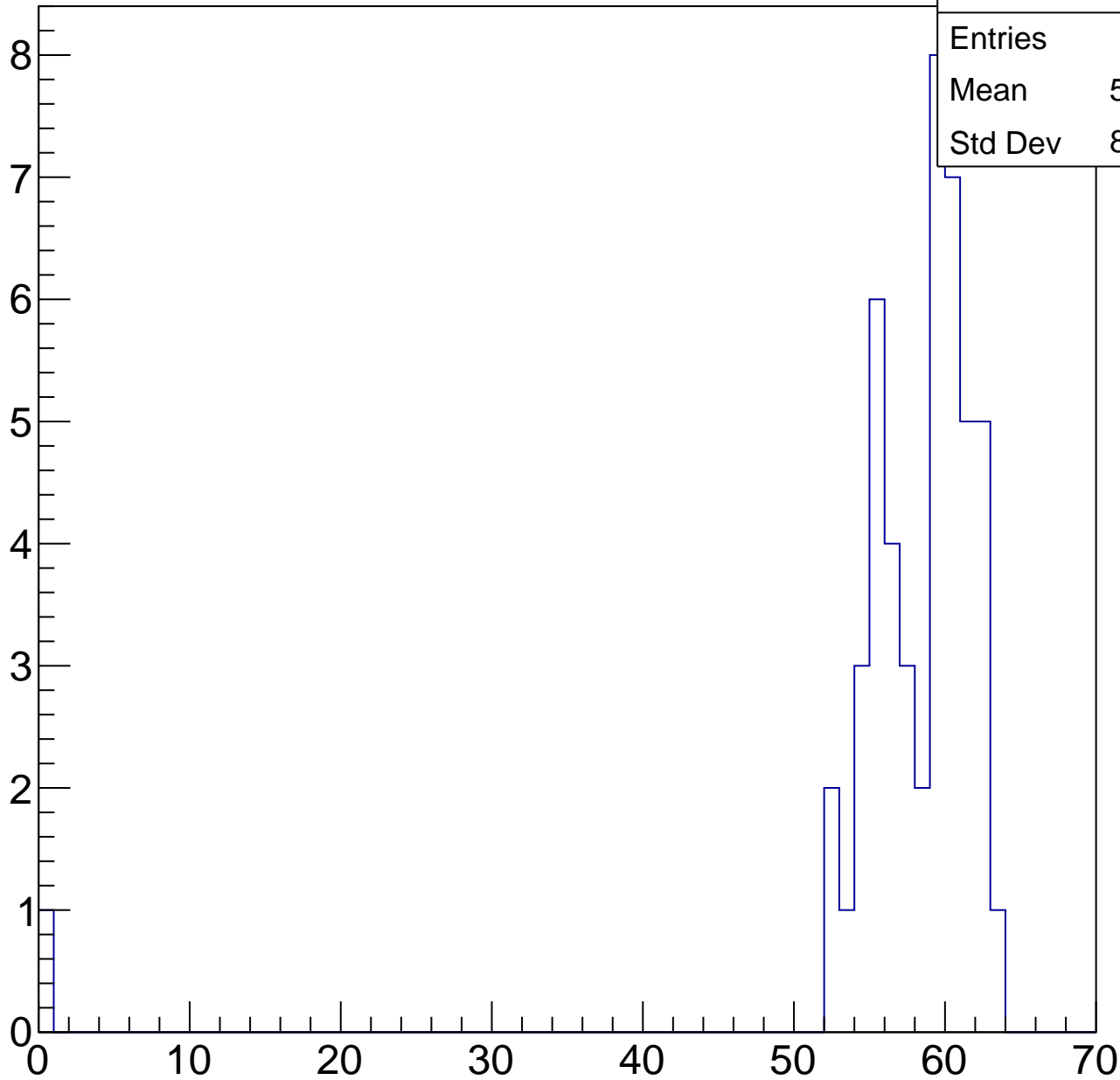
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	56.88
Std Dev	8.788

ampl

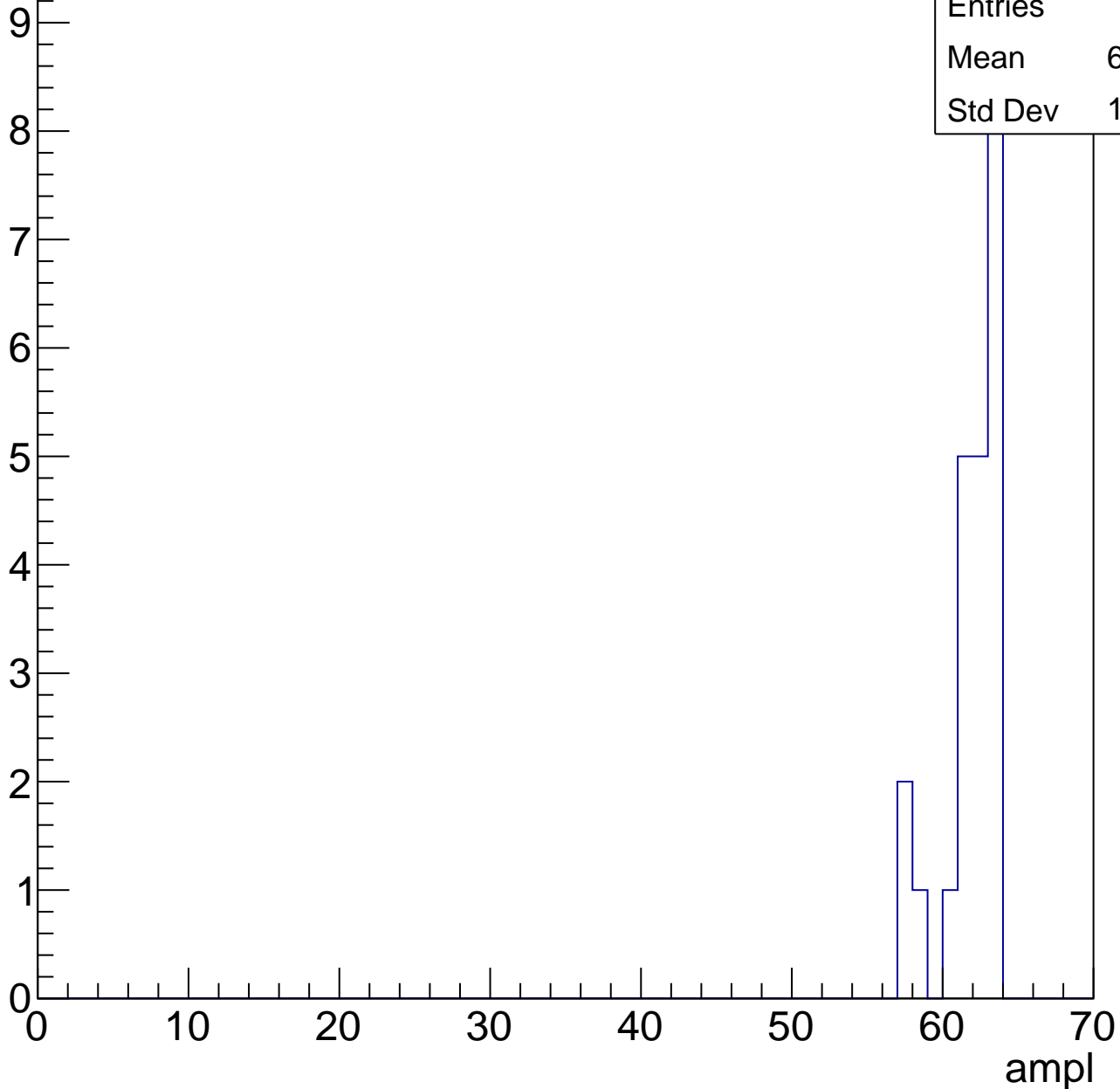


B1L102S, U14-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

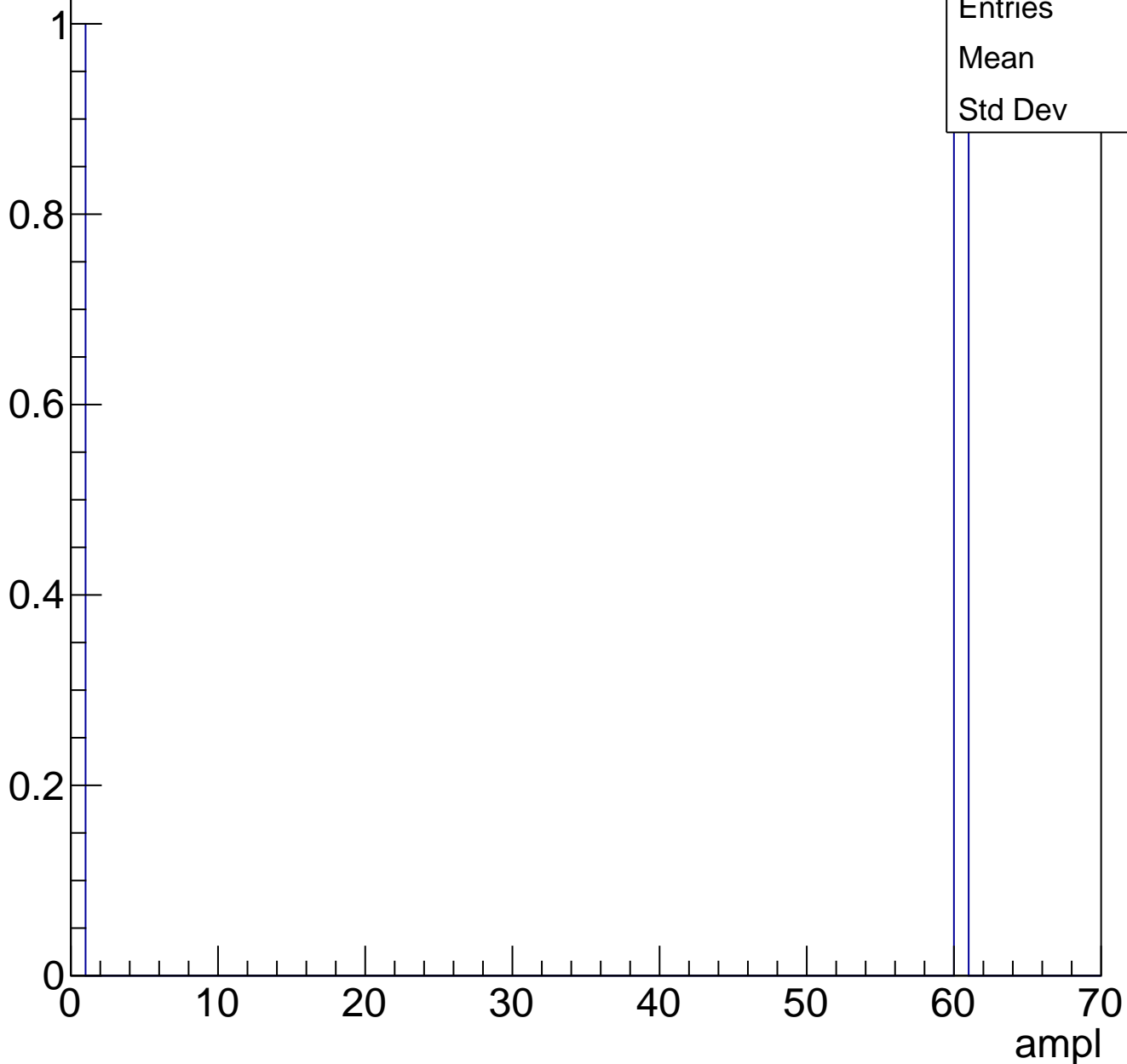
Entries	23
Mean	61.48
Std Dev	1.838



B1L102S, U14-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch59, adc0

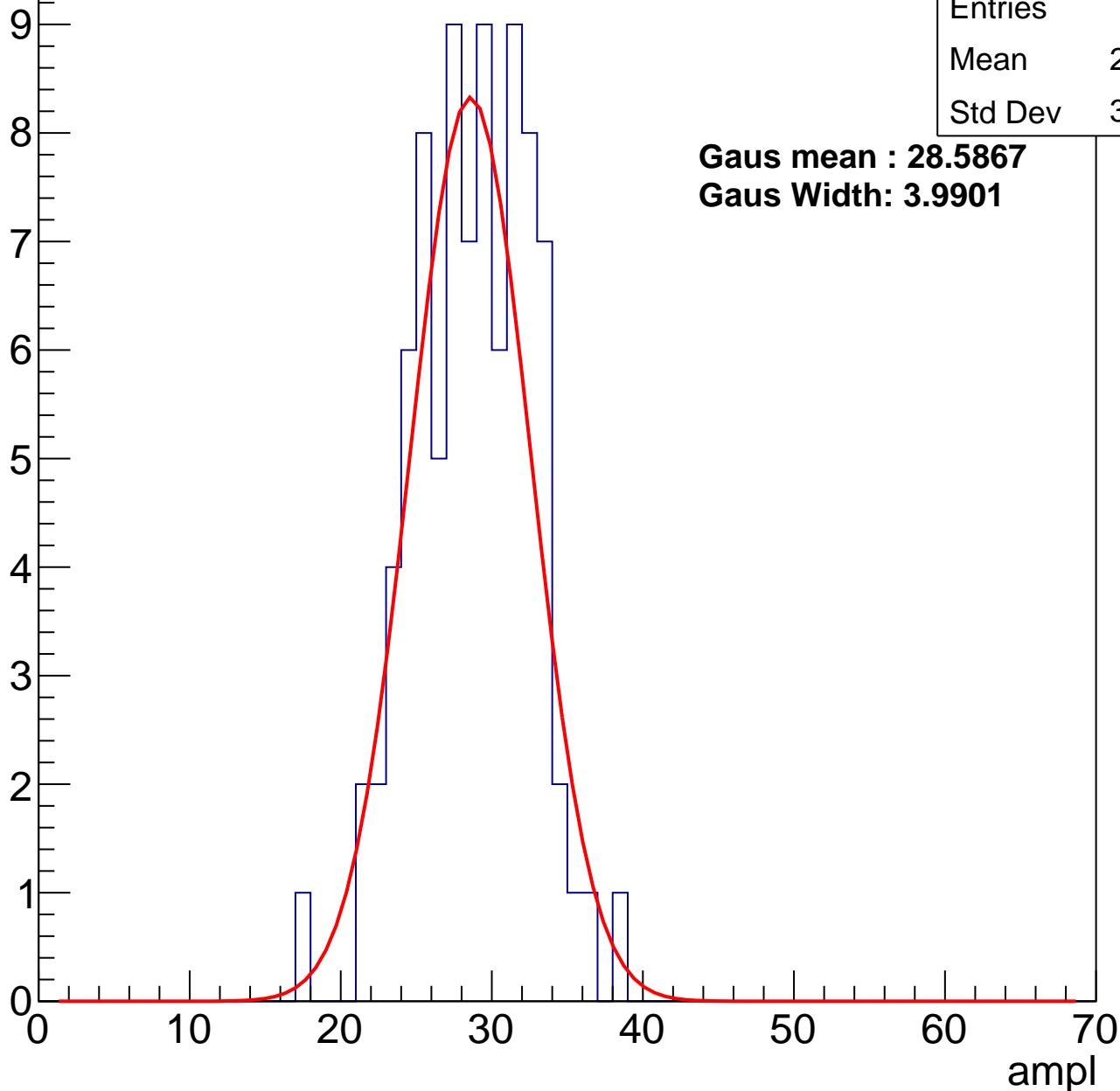
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	28.32
Std Dev	3.815

Gaus mean : 28.5867

Gaus Width: 3.9901



B1L102S, U14-ch59, adc1

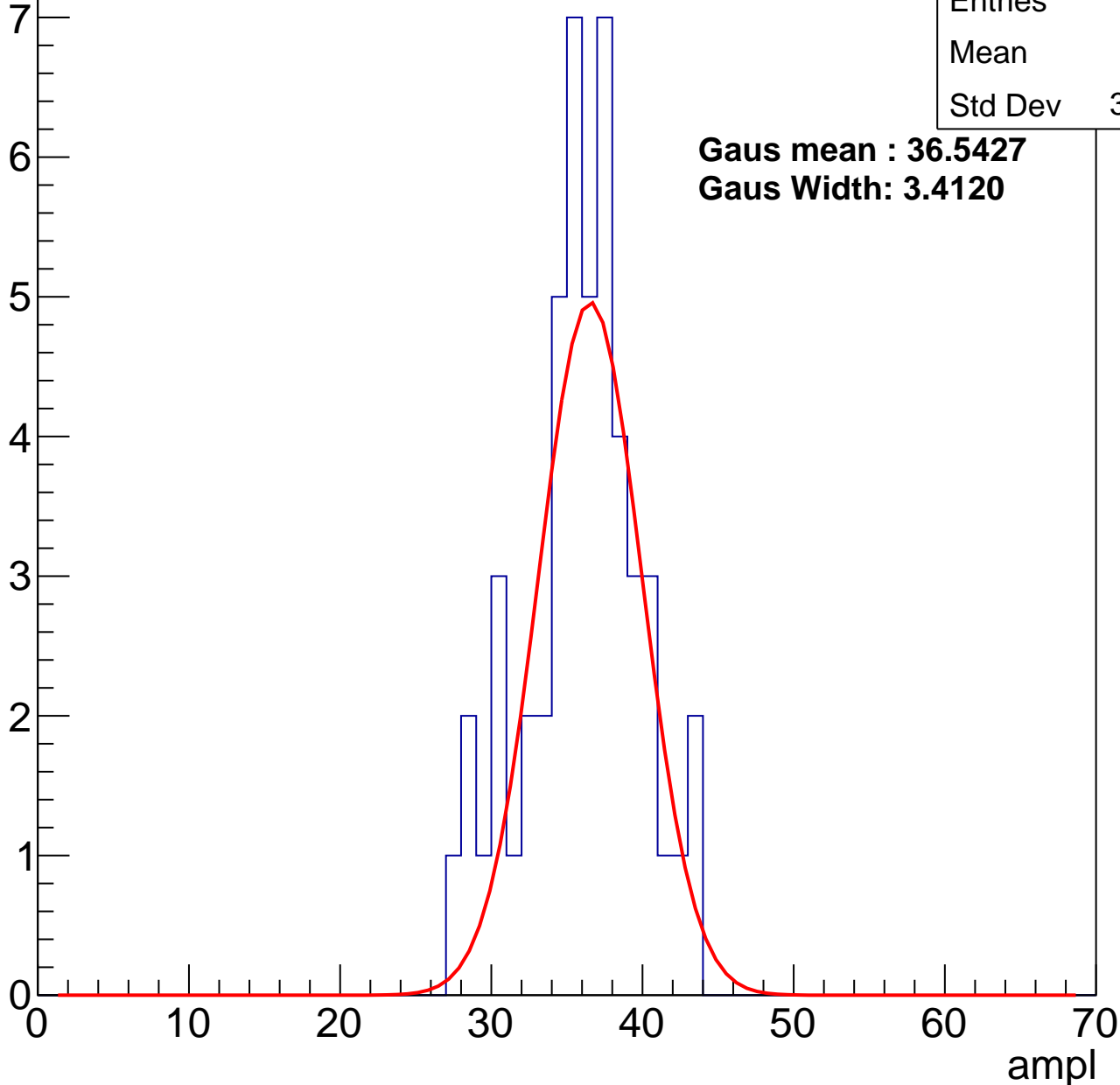
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	35.5
Std Dev	3.775

Gaus mean : 36.5427

Gaus Width: 3.4120



B1L102S, U14-ch59, adc2

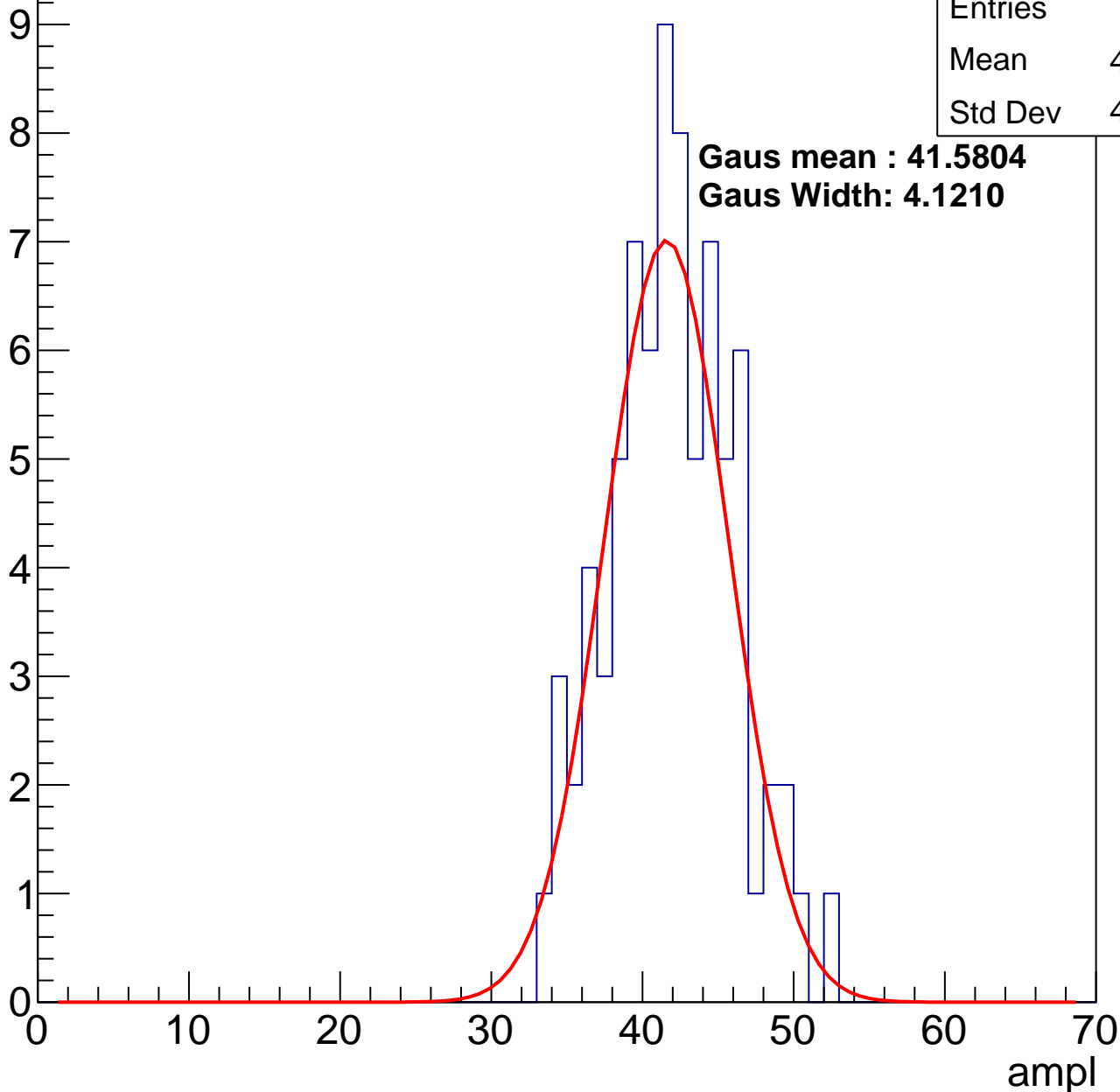
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	41.47
Std Dev	4.047

Gaus mean : 41.5804

Gaus Width: 4.1210

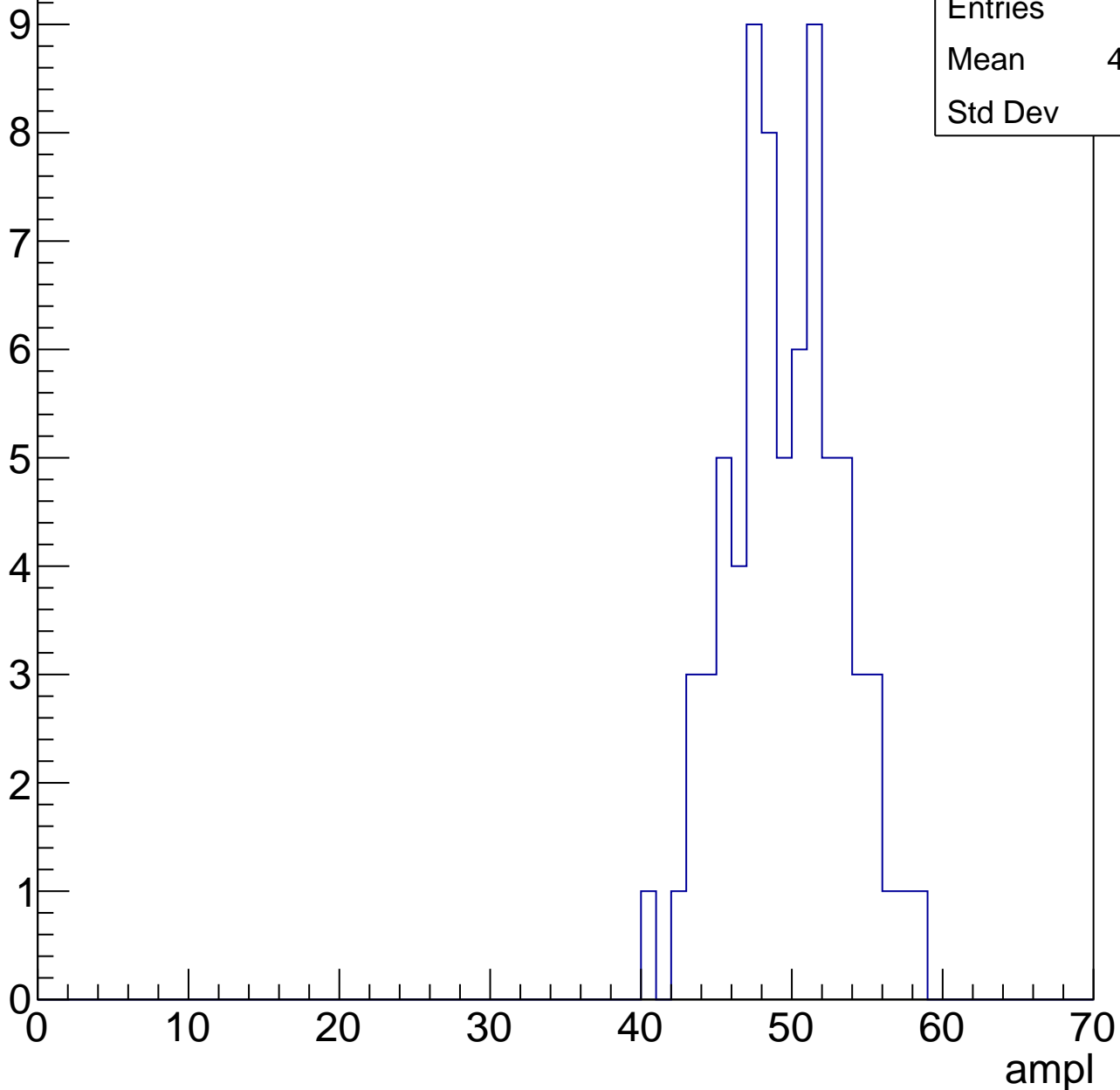


B1L102S, U14-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.12
Std Dev	3.73

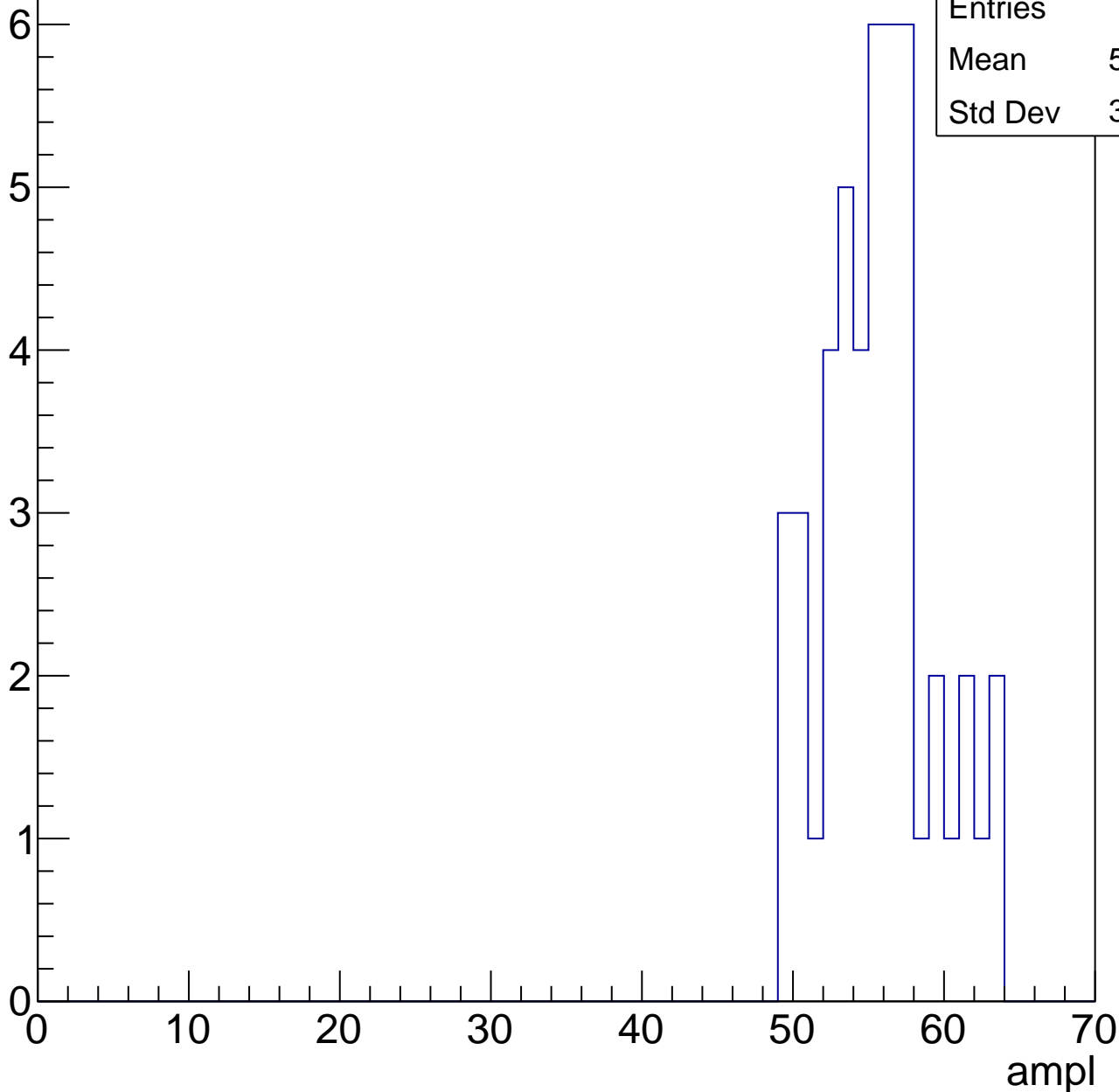


B1L102S, U14-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	55.13
Std Dev	3.583

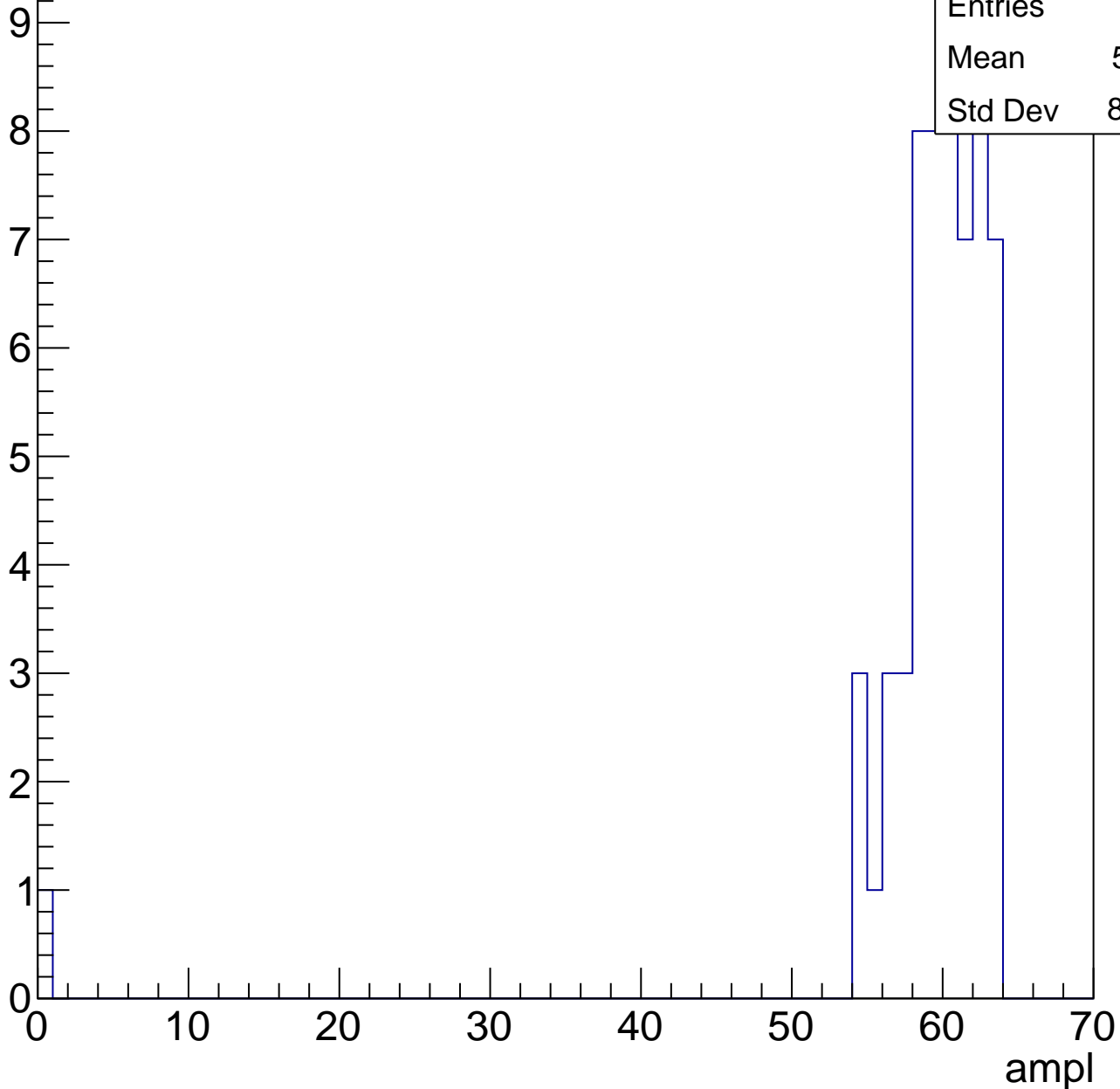


B1L102S, U14-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	58.61
Std Dev	8.068



B1L102S, U14-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch60, adc0

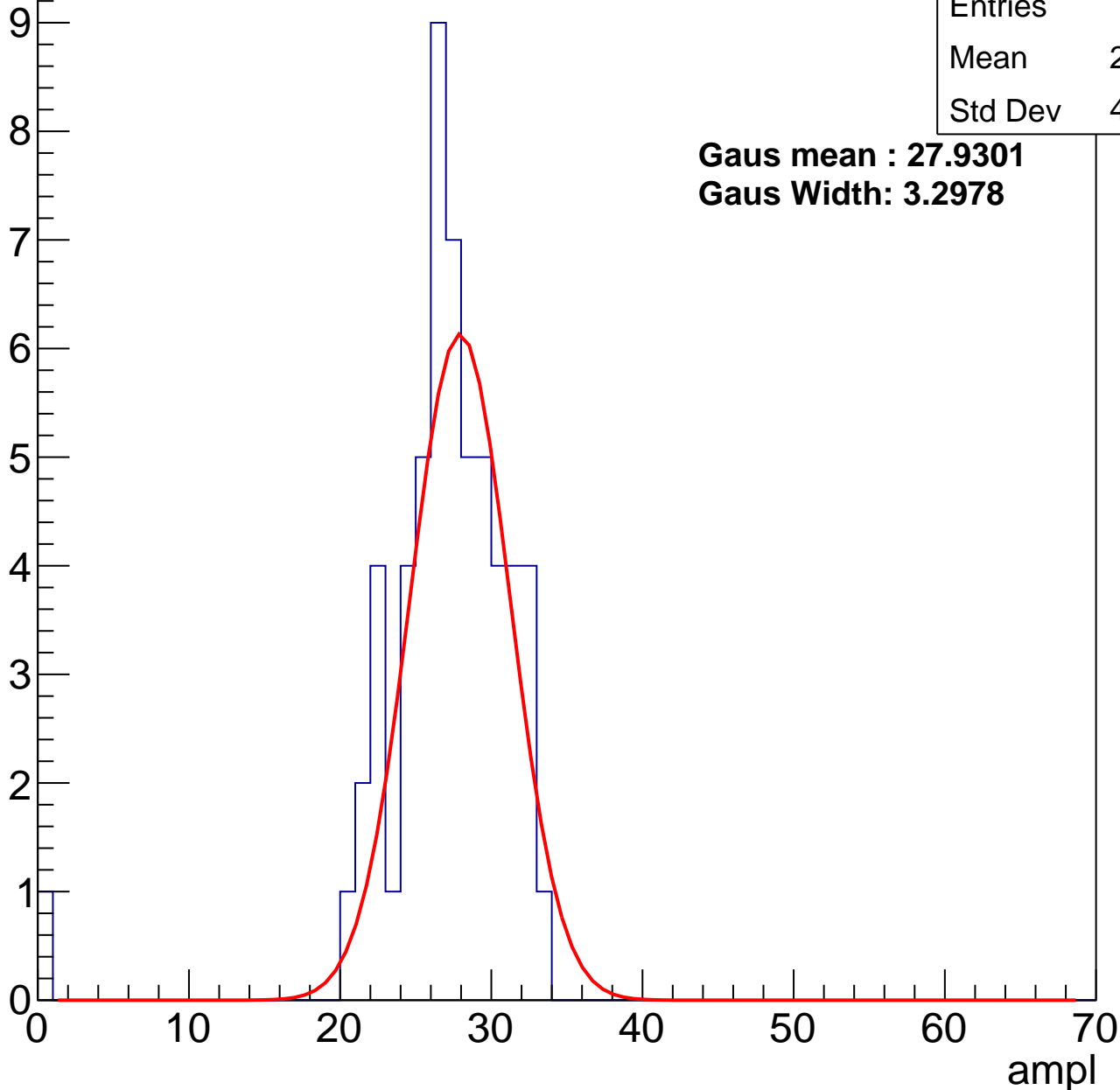
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	26.44
Std Dev	4.728

Gaus mean : 27.9301

Gaus Width: 3.2978



B1L102S, U14-ch60, adc1

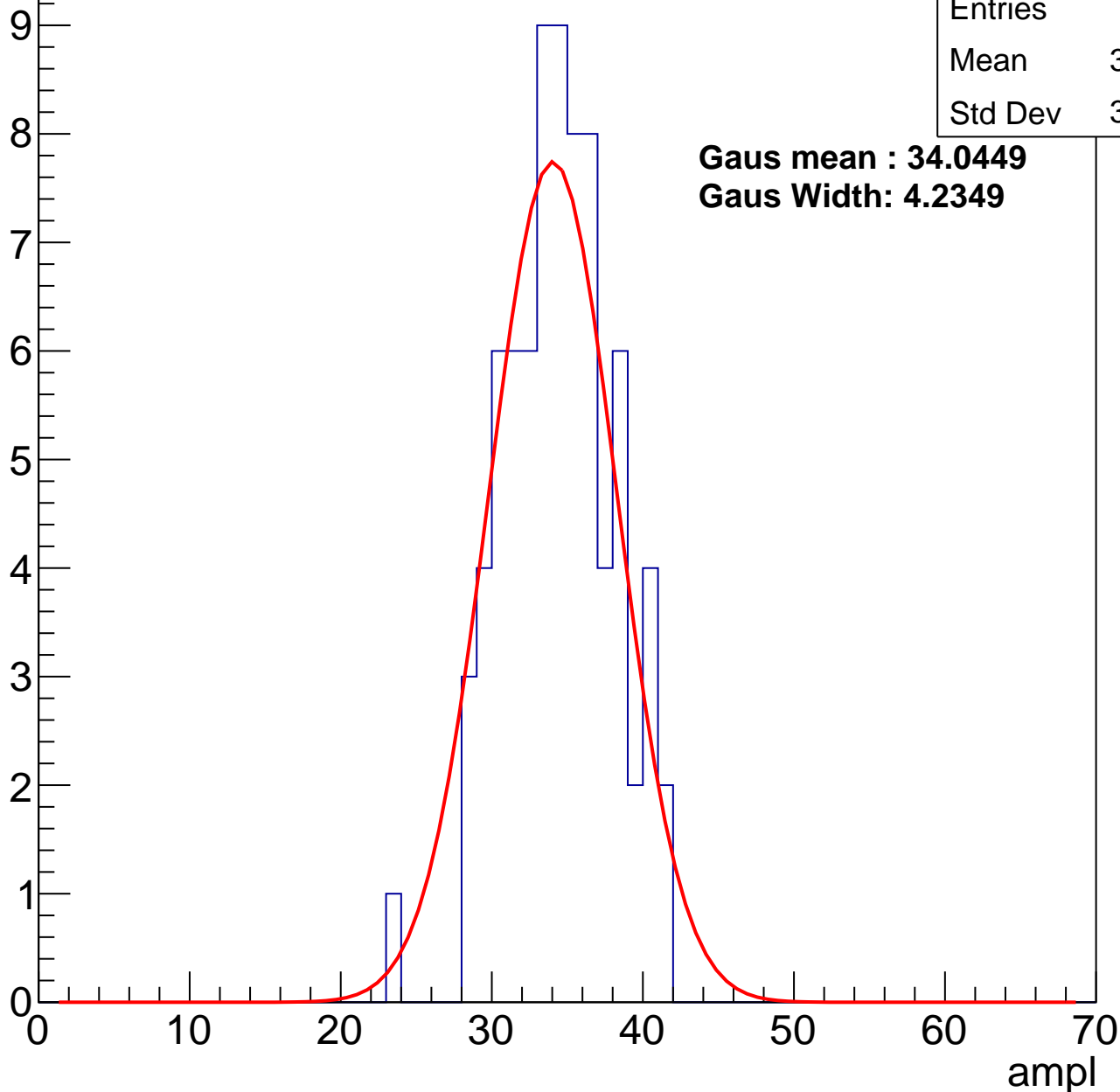
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	33.95
Std Dev	3.537

Gaus mean : 34.0449

Gaus Width: 4.2349



B1L102S, U14-ch60, adc2

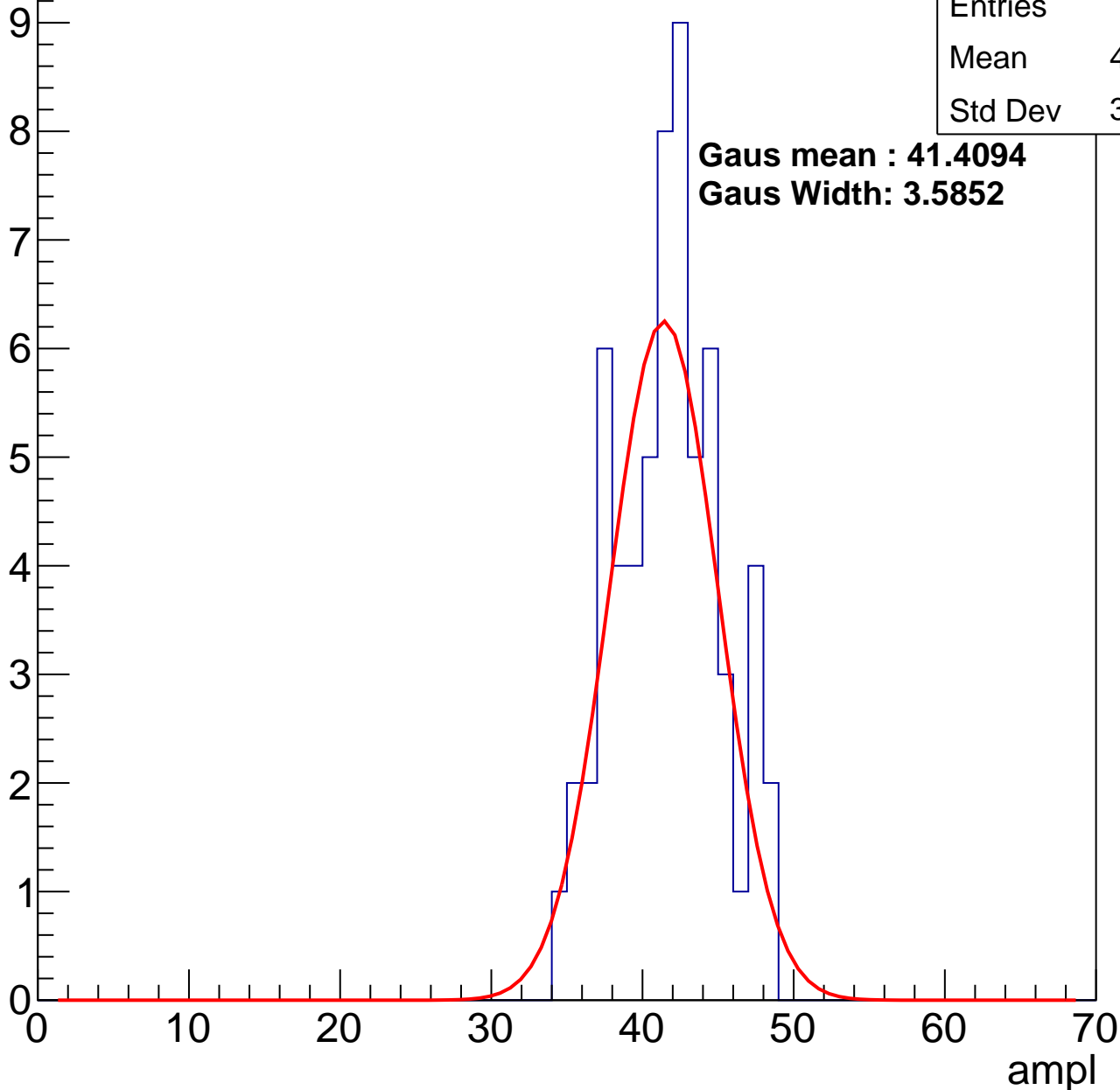
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.23
Std Dev	3.405

Gaus mean : 41.4094

Gaus Width: 3.5852

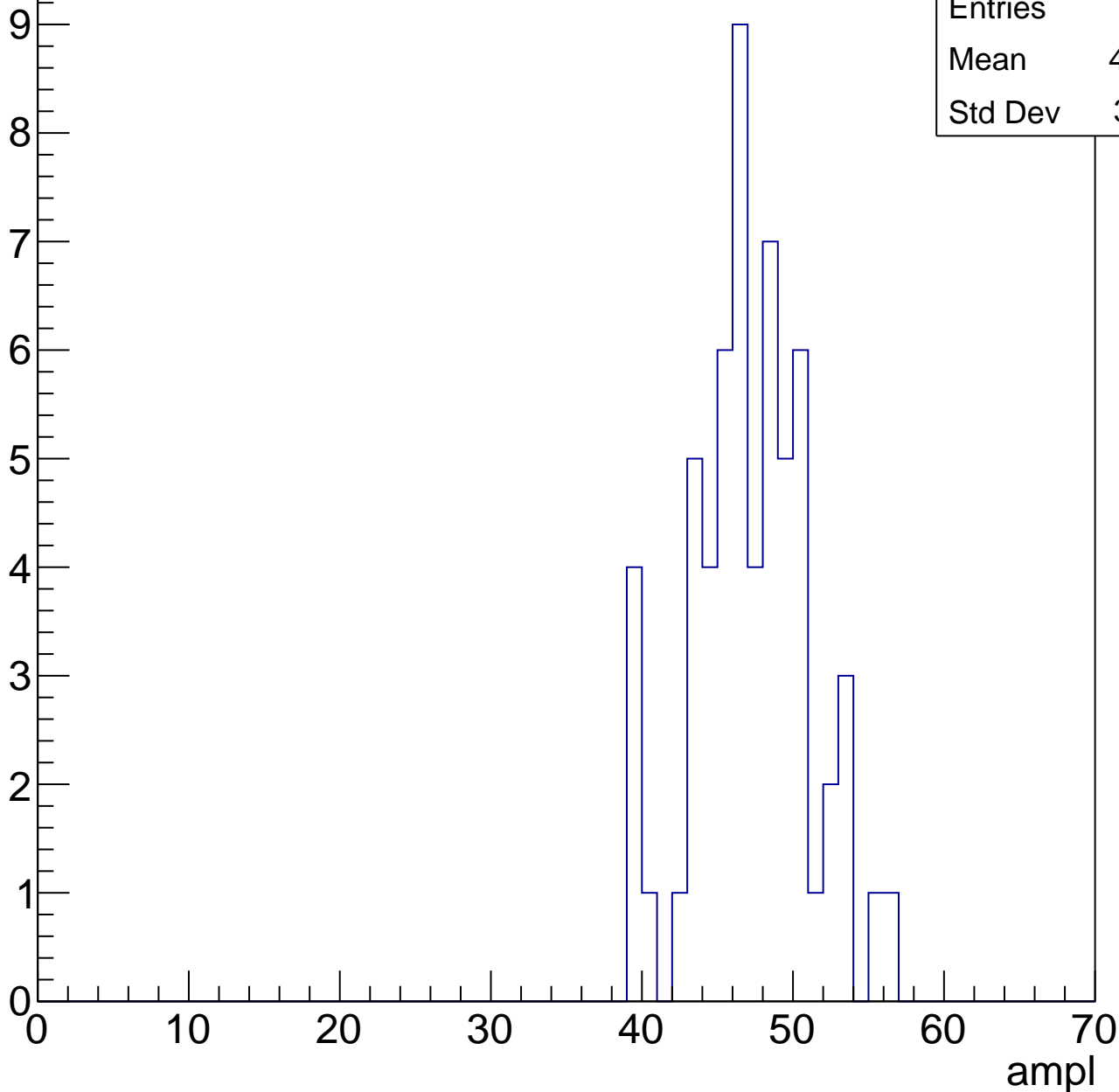


B1L102S, U14-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	46.78
Std Dev	3.821

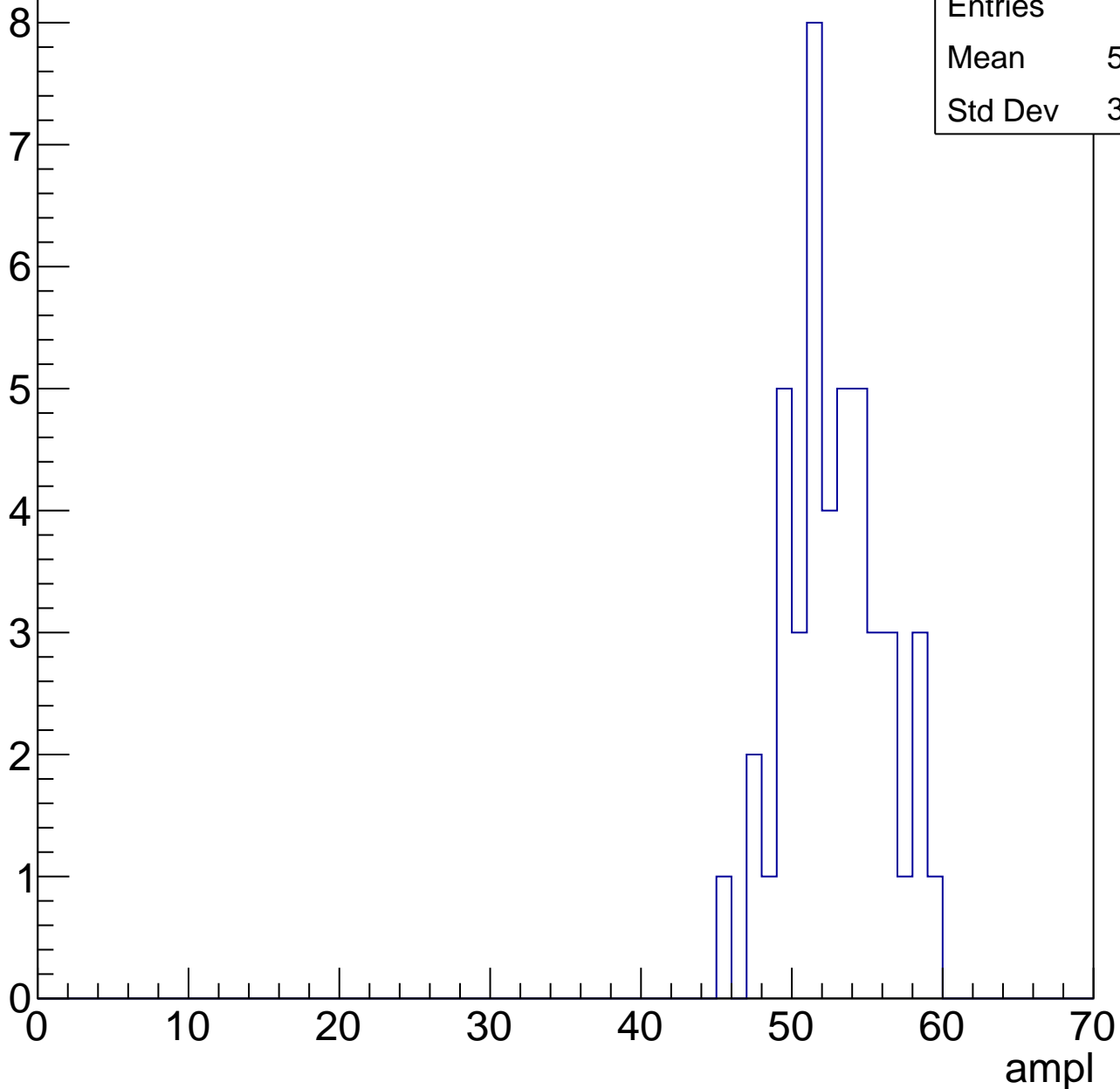


B1L102S, U14-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

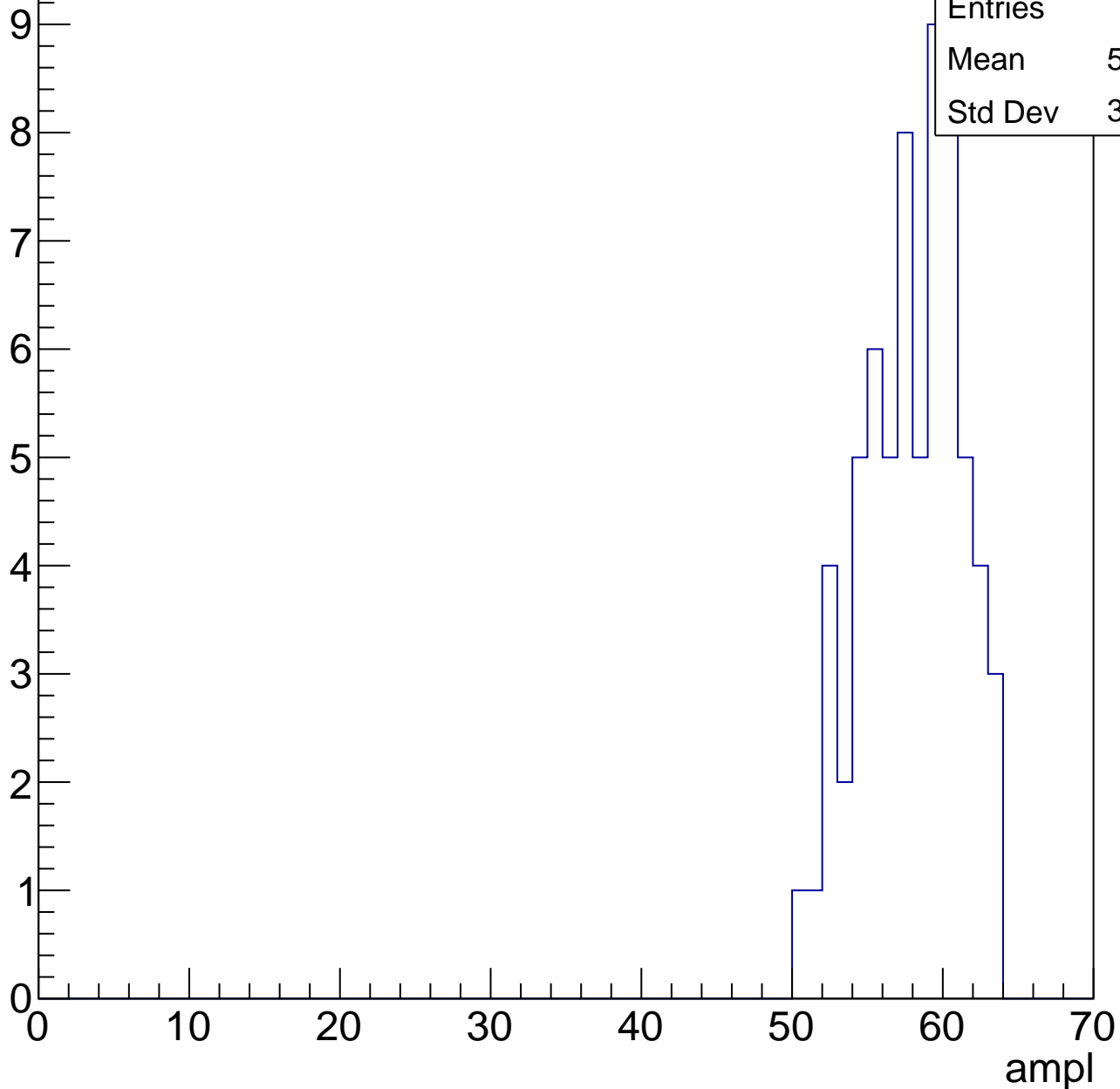
Entries	45
Mean	52.36
Std Dev	3.184



B1L102S, U14-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



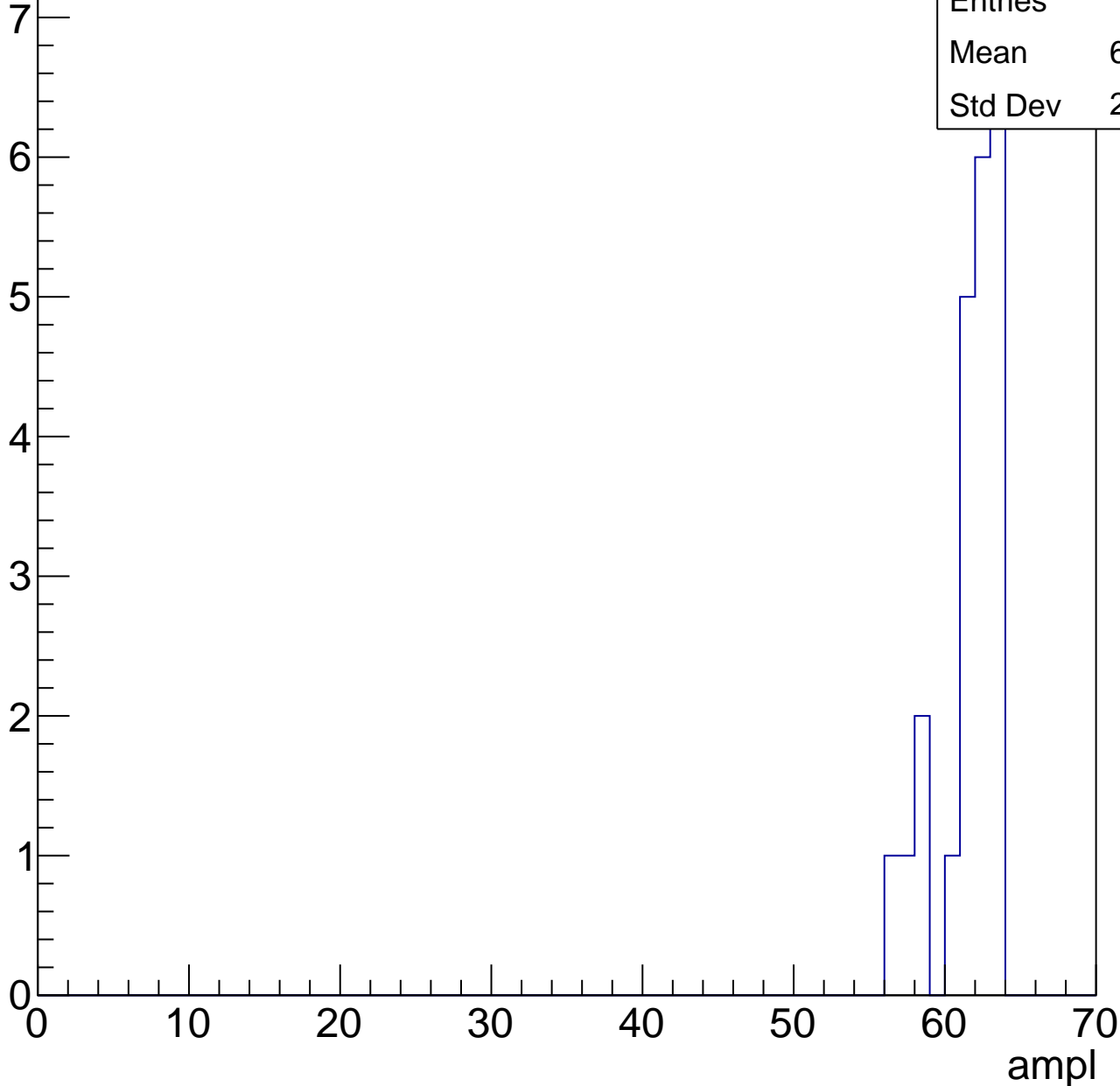
Entries	67
Mean	57.52
Std Dev	3.192

B1L102S, U14-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	61.17
Std Dev	2.014



B1L102S, U14-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L102S, U14-ch61, adc0

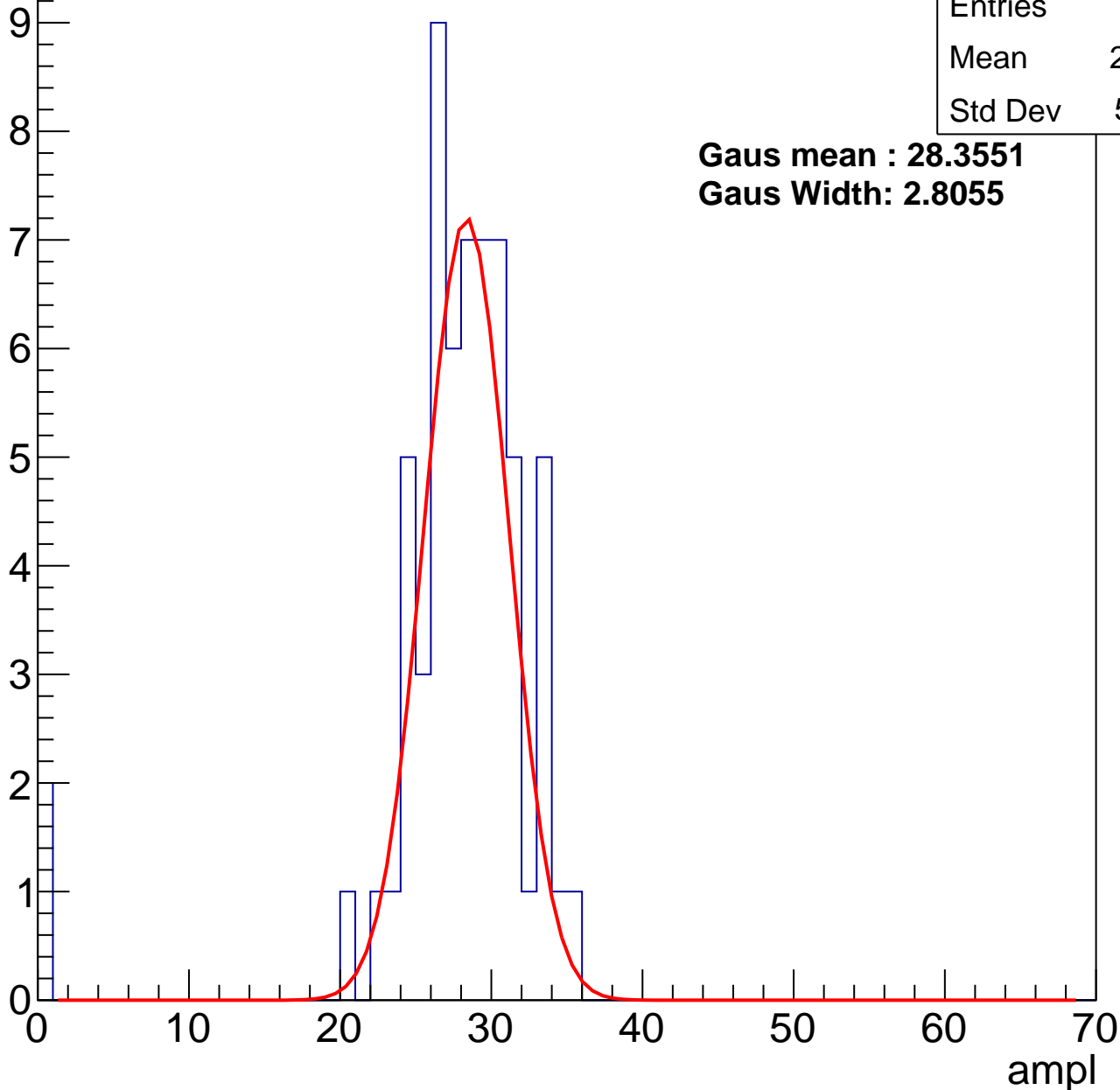
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.19
Std Dev	5.831

Gaus mean : 28.3551

Gaus Width: 2.8055



B1L102S, U14-ch61, adc1

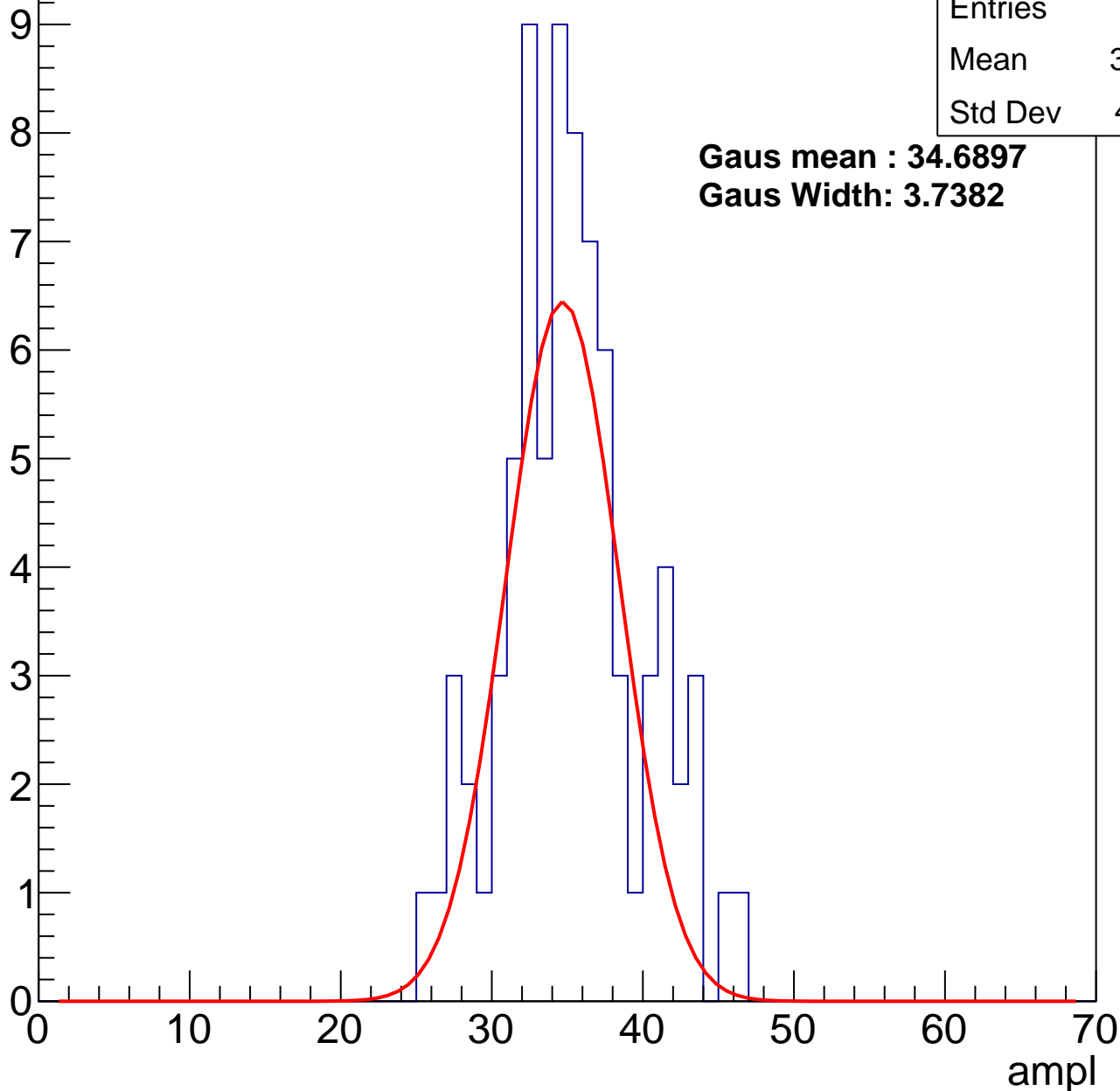
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	34.82
Std Dev	4.491

Gaus mean : 34.6897

Gaus Width: 3.7382



B1L102S, U14-ch61, adc2

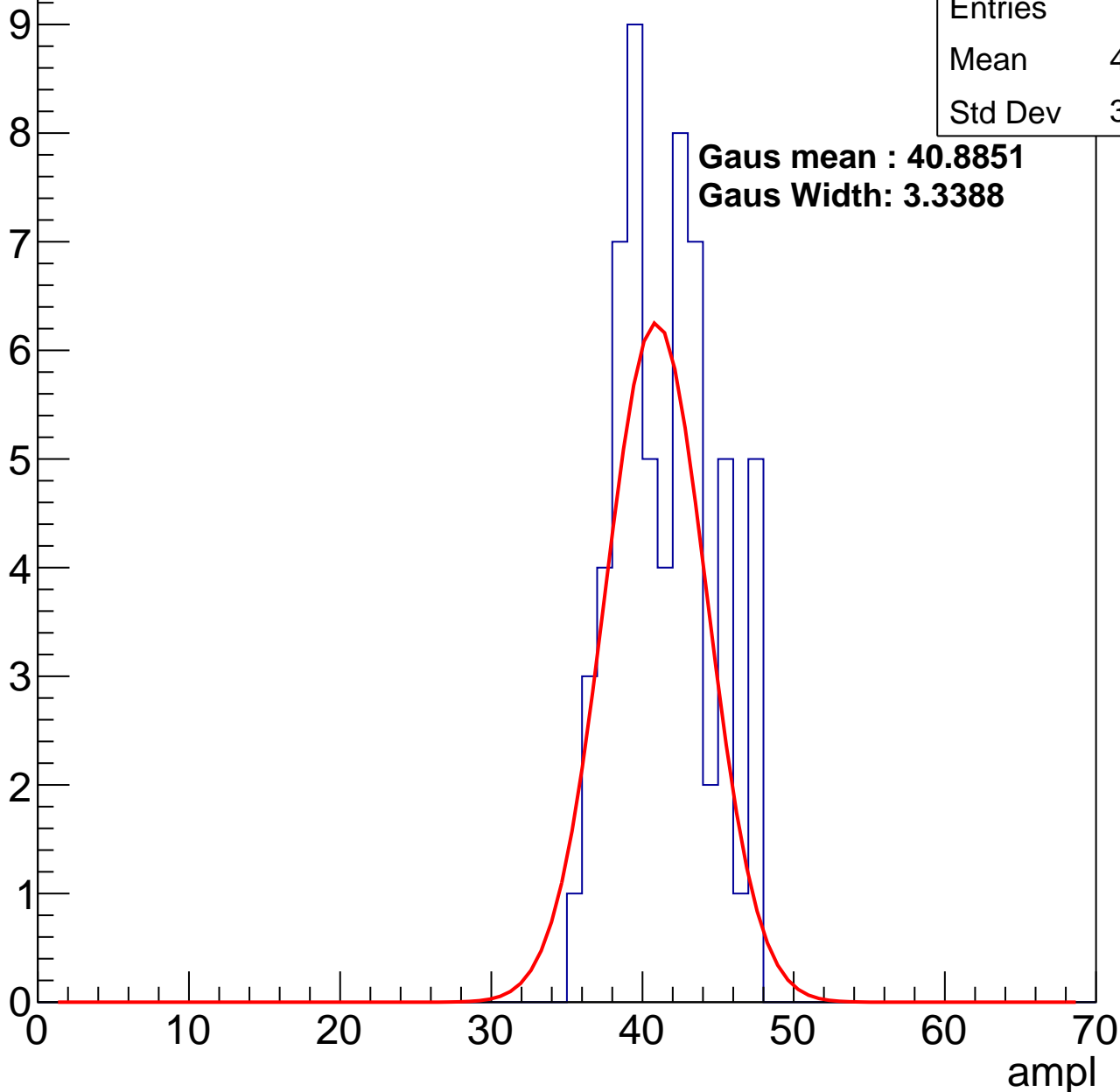
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.03
Std Dev	3.183

Gaus mean : 40.8851

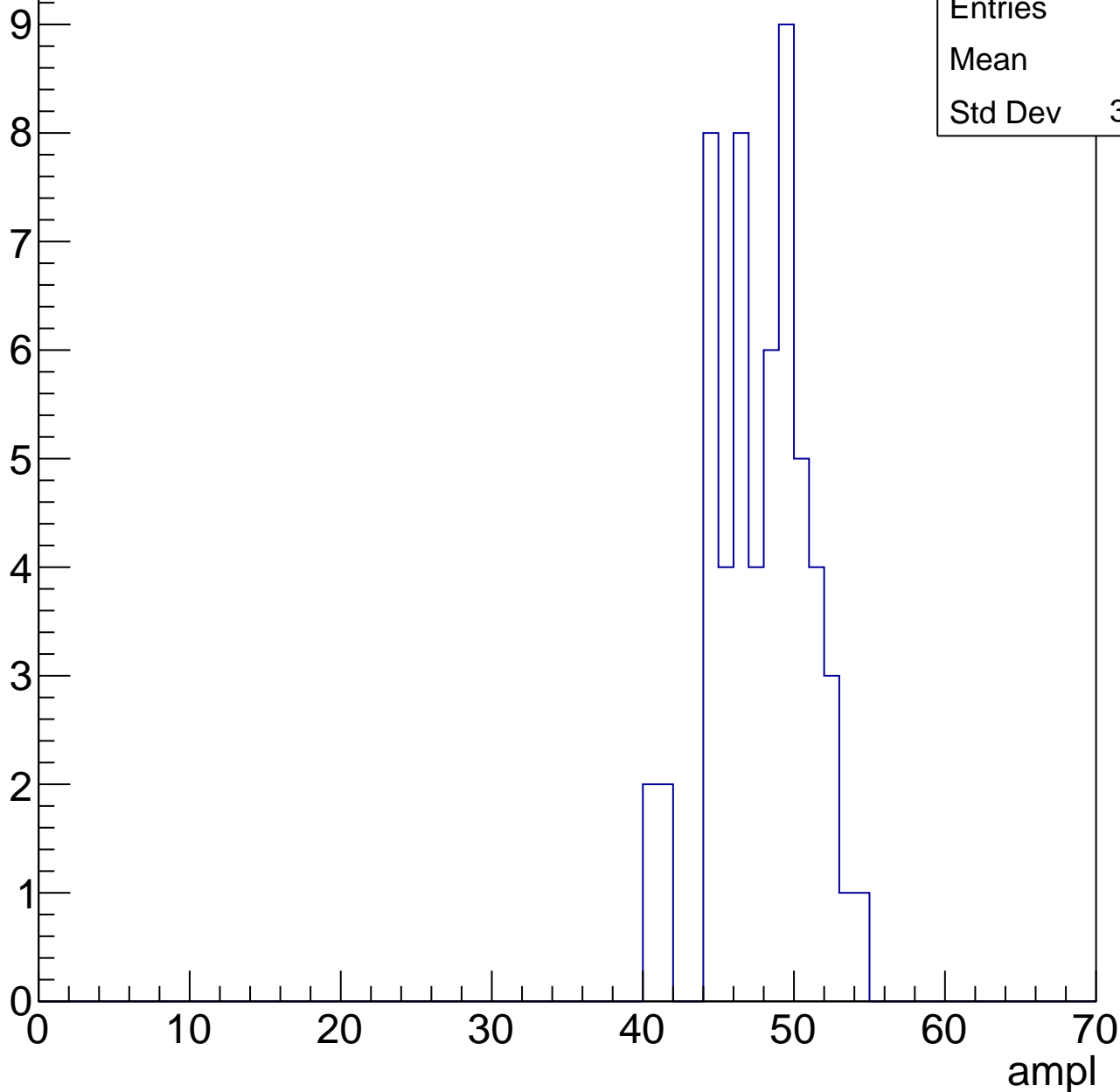
Gaus Width: 3.3388



B1L102S, U14-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



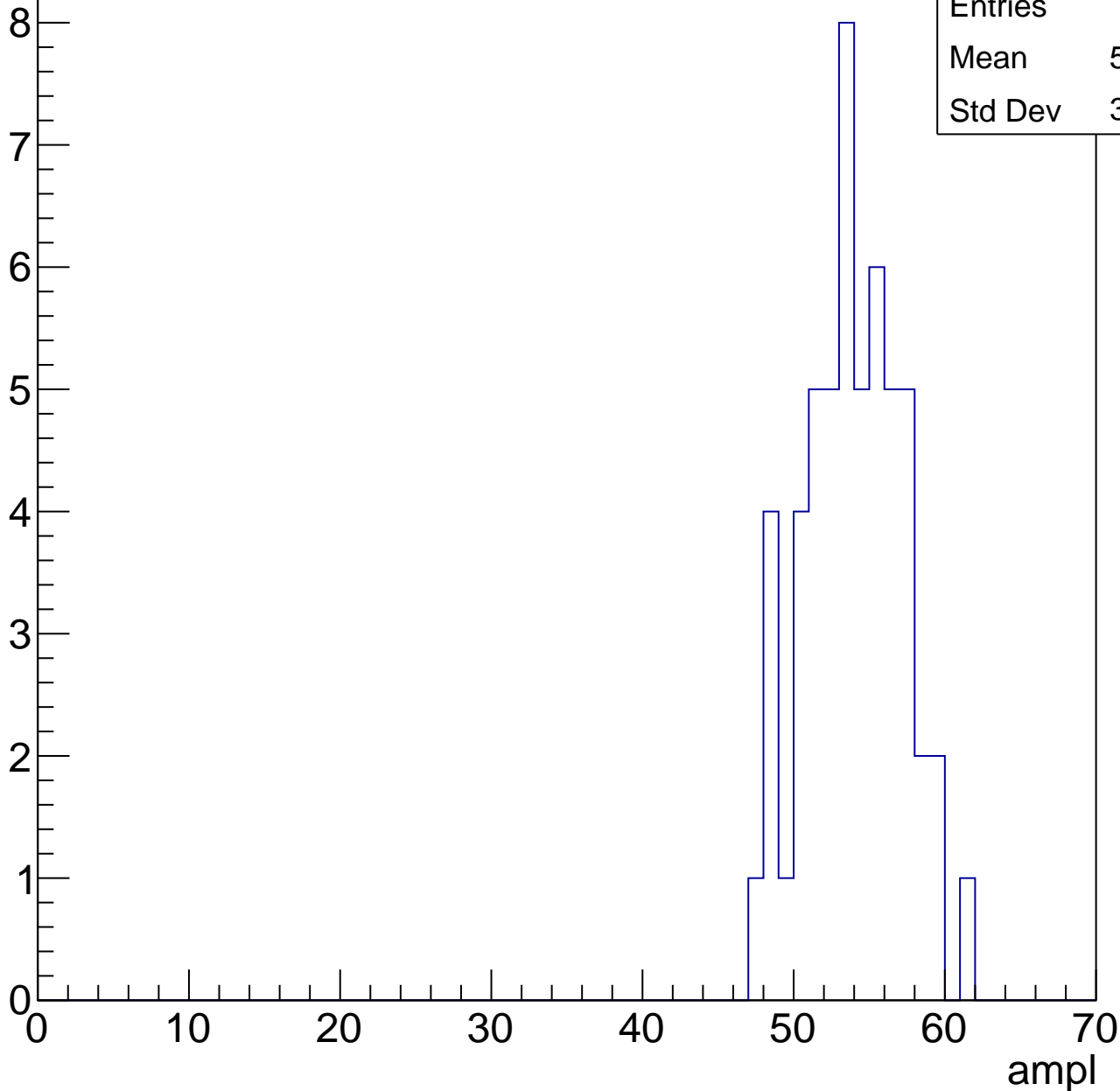
Entries	57
Mean	47.3
Std Dev	3.168

B1L102S, U14-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.46
Std Dev	3.166

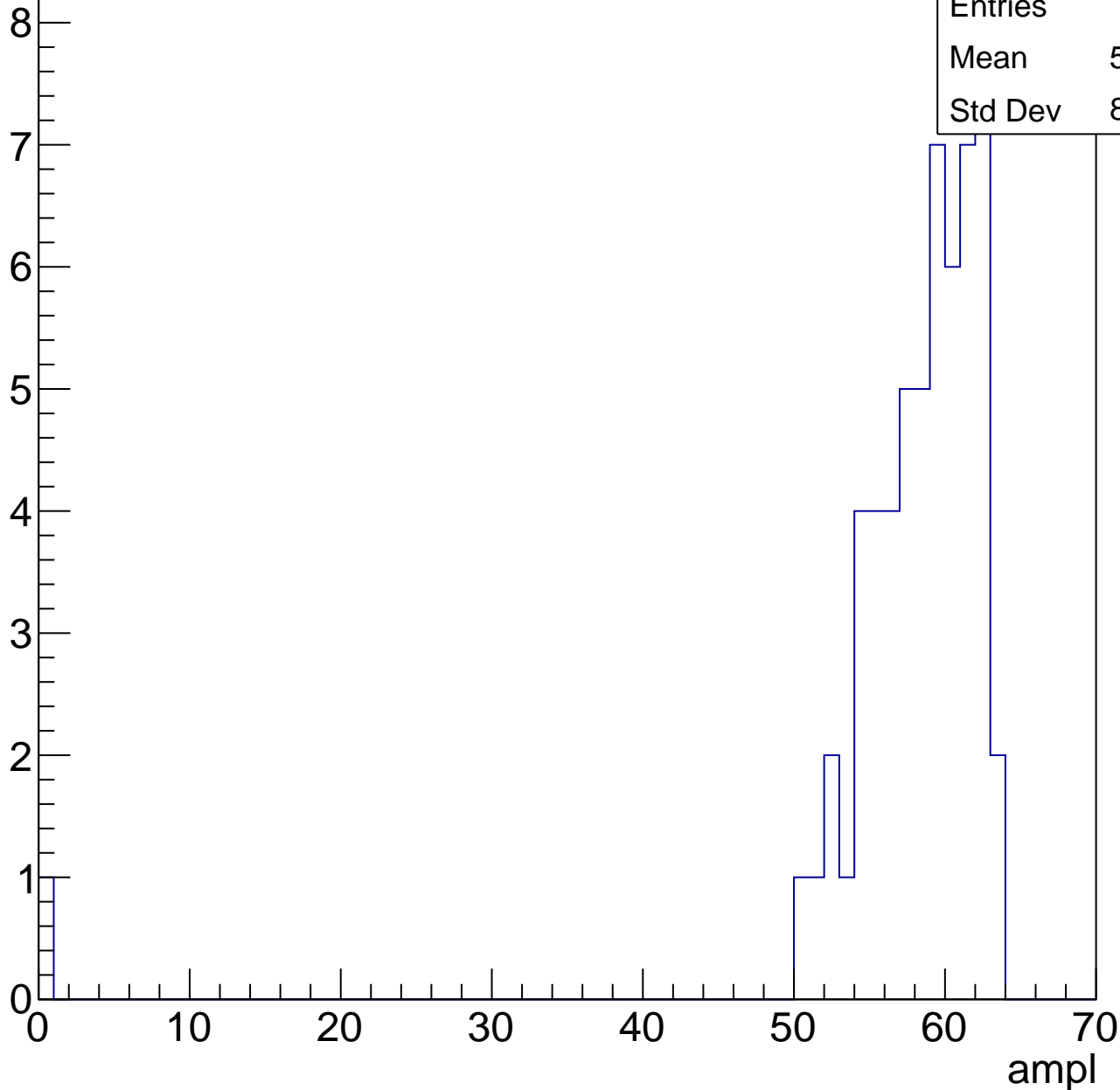


B1L102S, U14-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.16
Std Dev	8.227

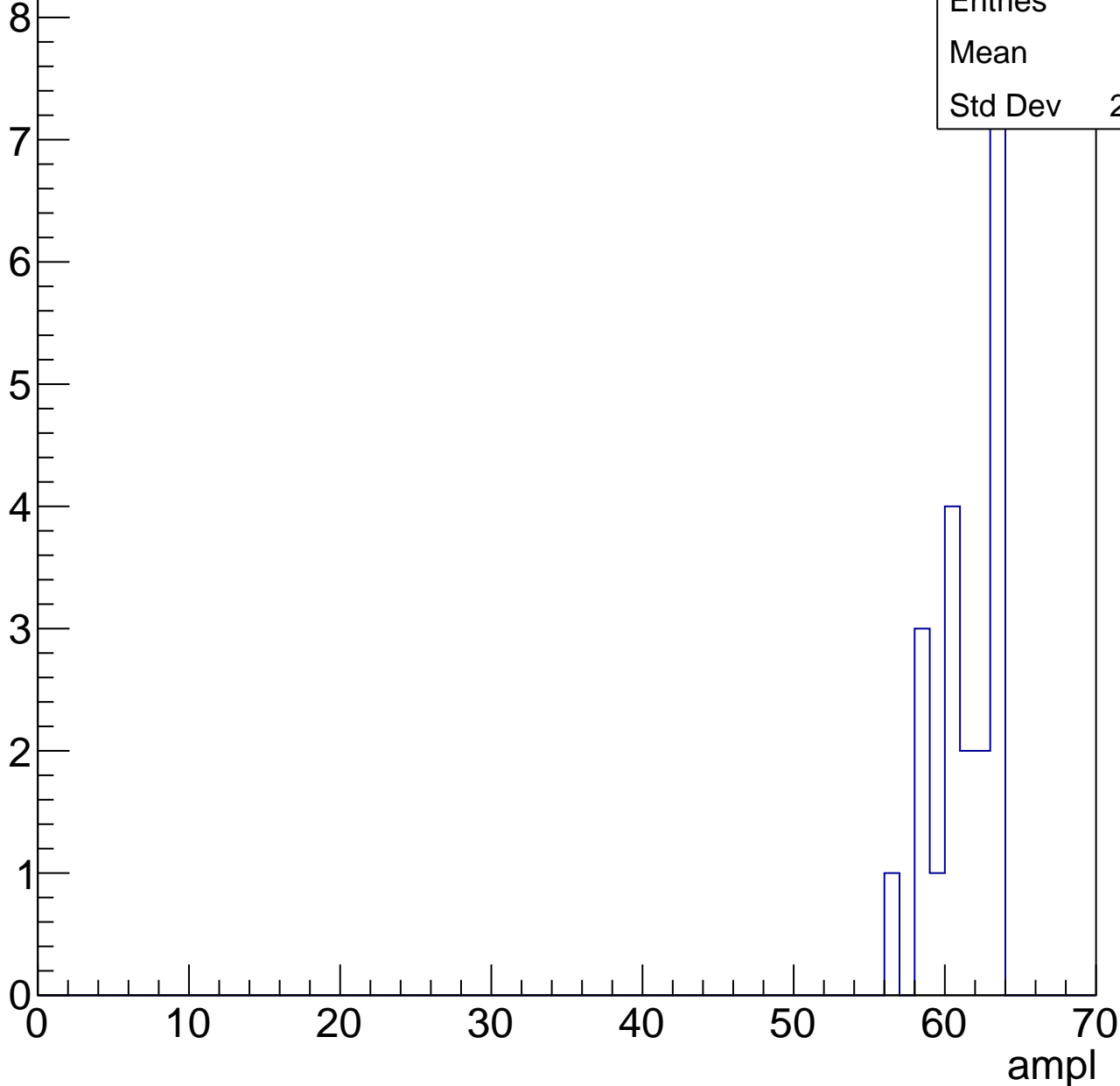


B1L102S, U14-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	60.9
Std Dev	2.114



B1L102S, U14-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U14-ch62, adc0

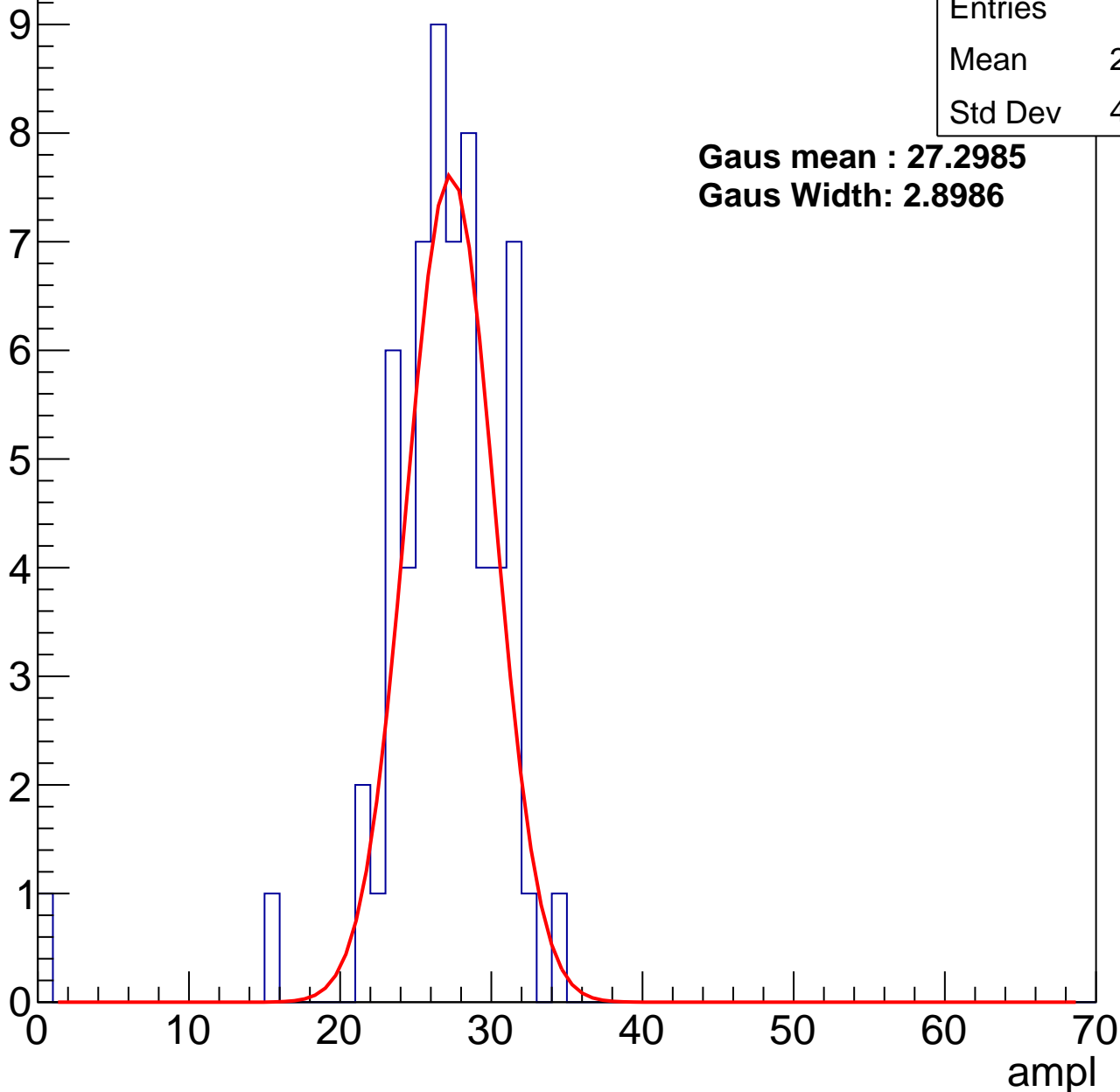
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	26.25
Std Dev	4.629

Gaus mean : 27.2985

Gaus Width: 2.8986



B1L102S, U14-ch62, adc1

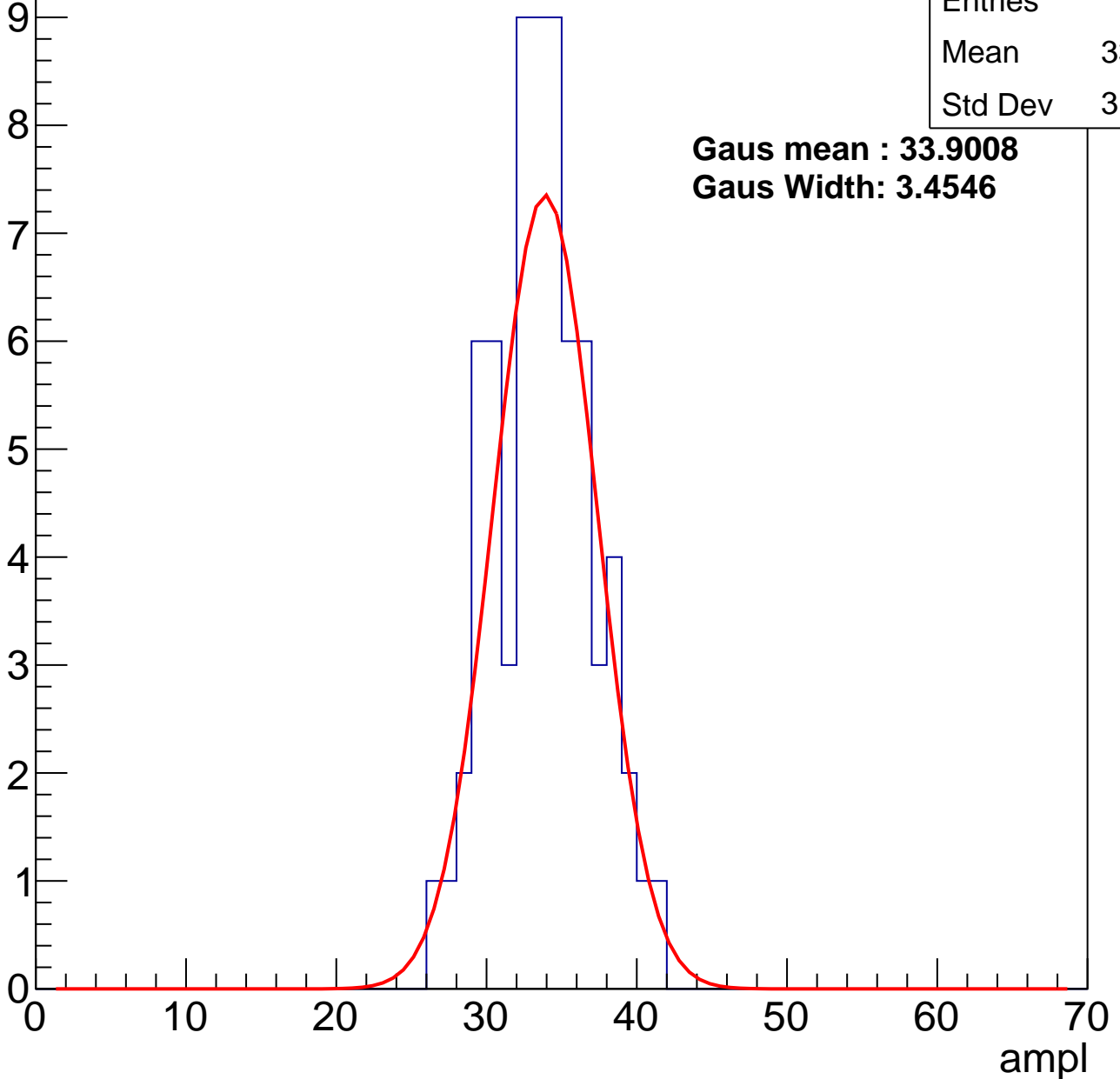
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	33.26
Std Dev	3.233

Gaus mean : 33.9008

Gaus Width: 3.4546



B1L102S, U14-ch62, adc2

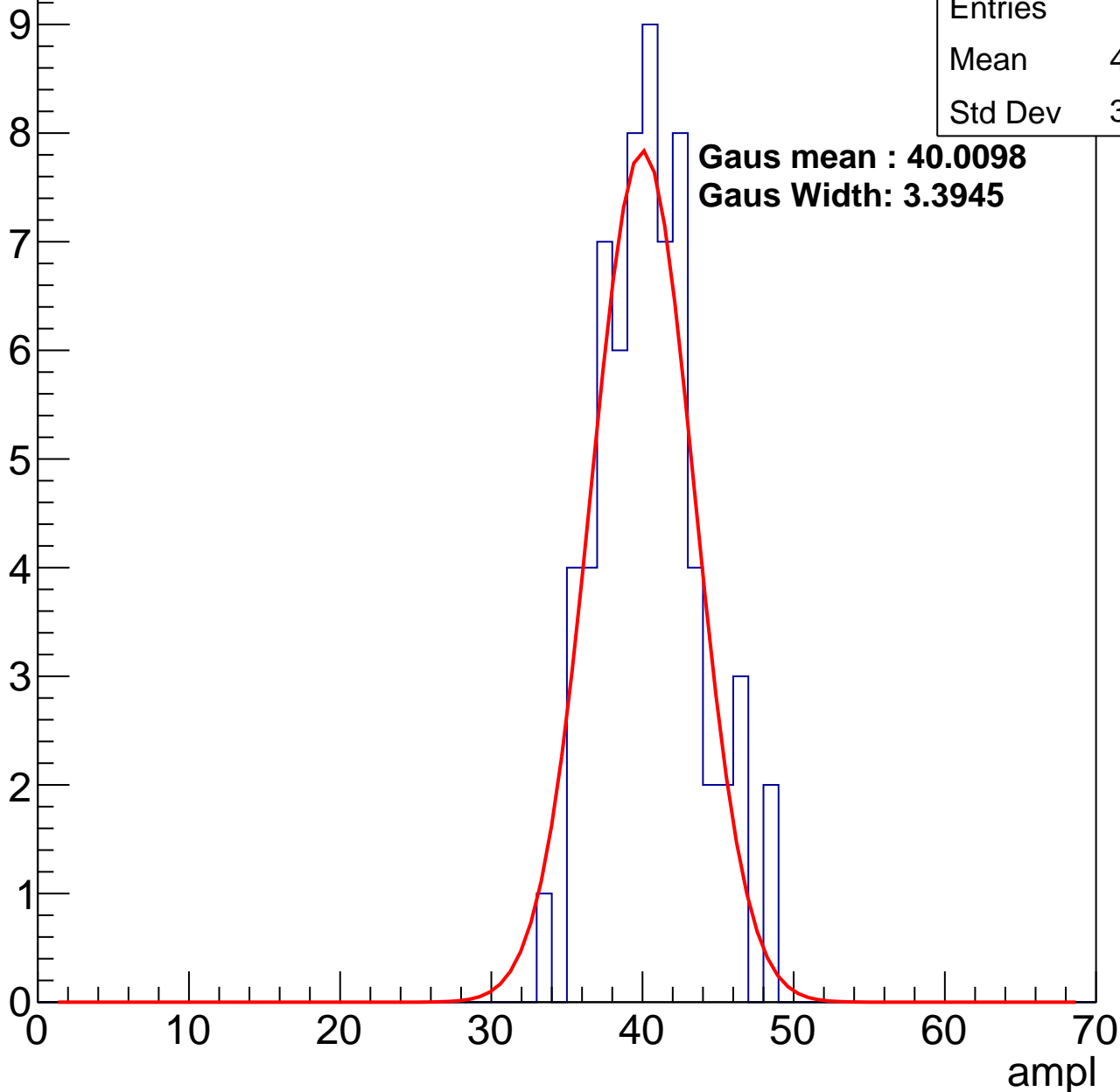
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	40.04
Std Dev	3.234

Gaus mean : 40.0098

Gaus Width: 3.3945

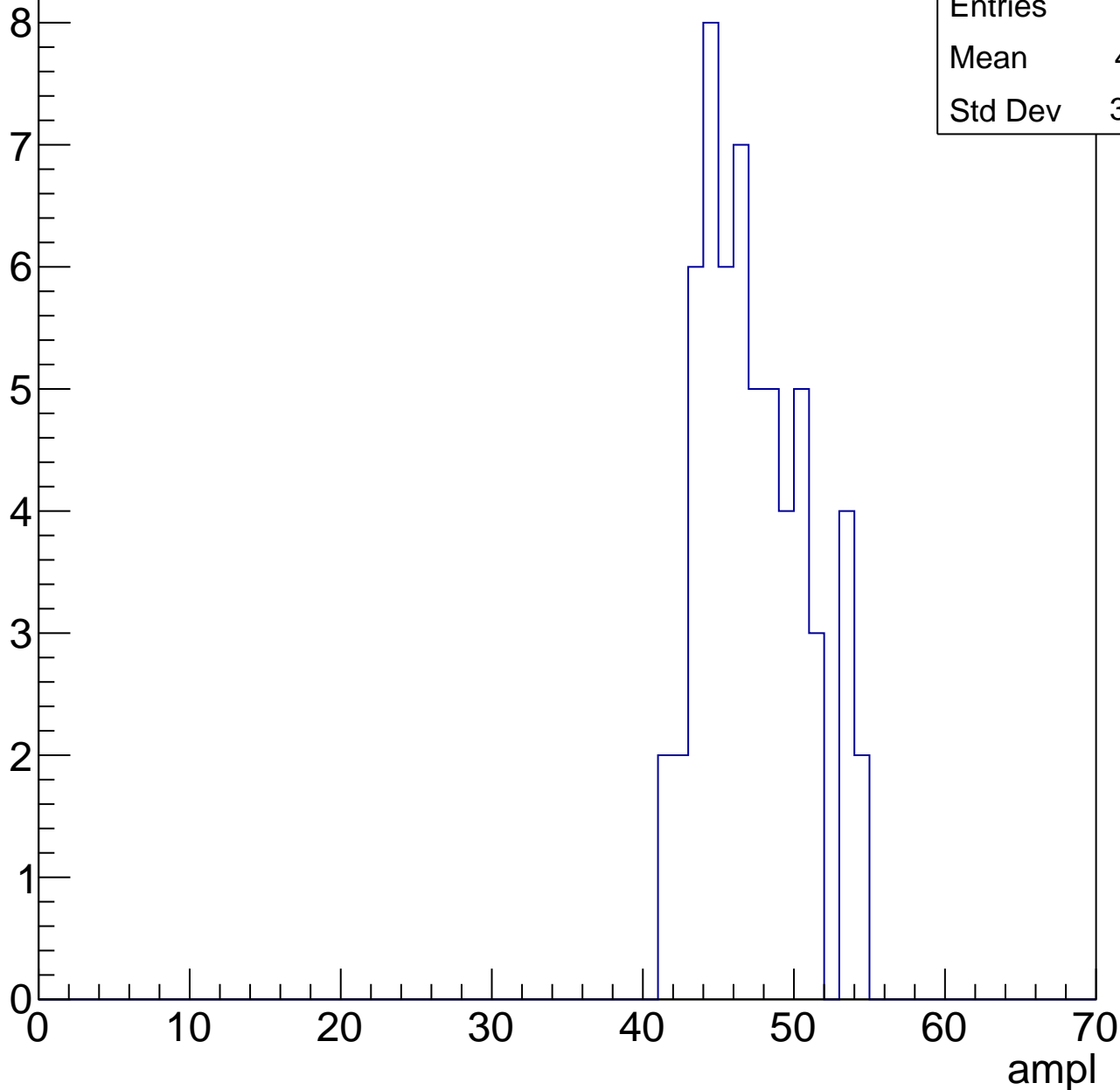


B1L102S, U14-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	46.81
Std Dev	3.382

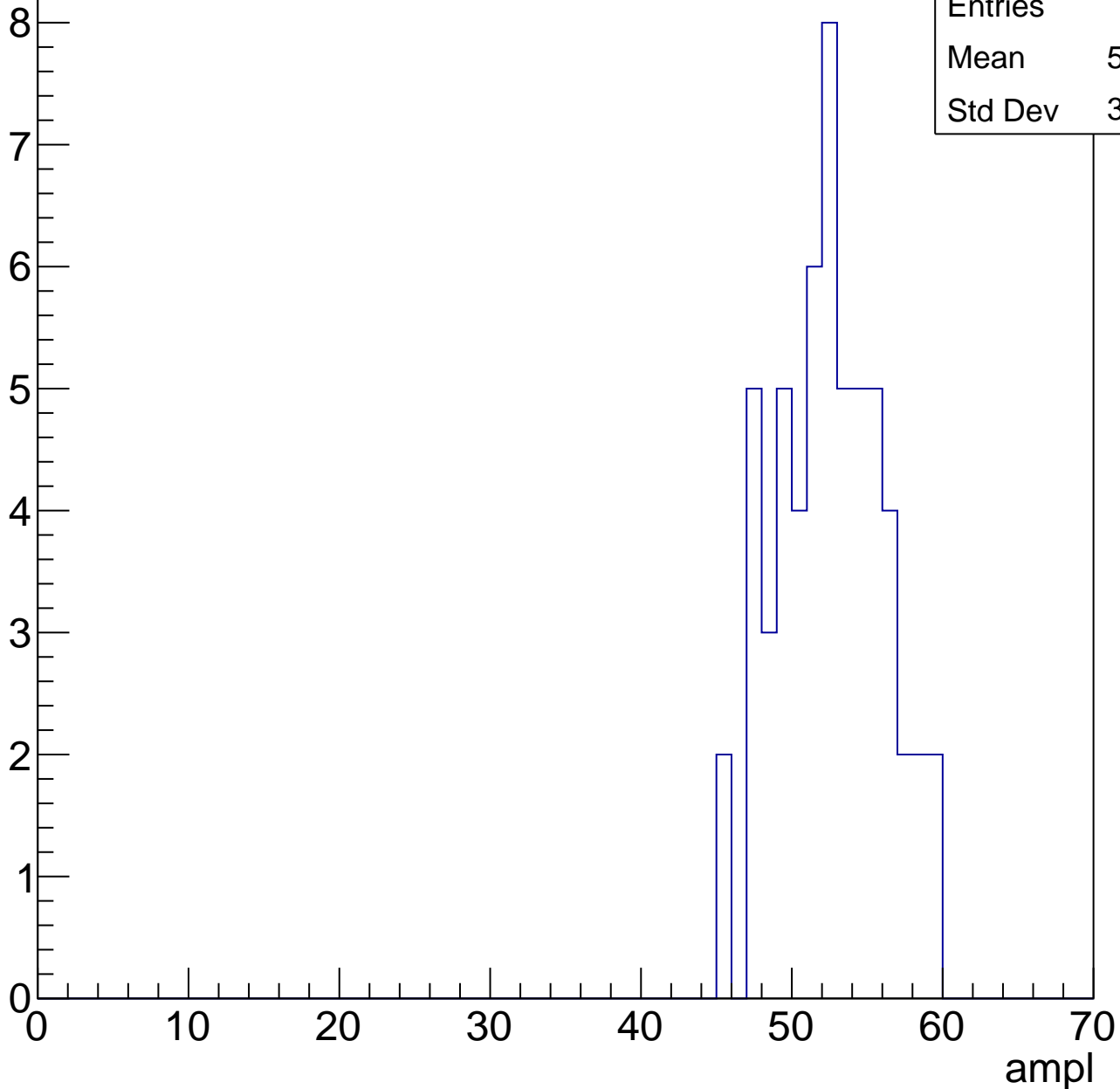


B1L102S, U14-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	52.03
Std Dev	3.454

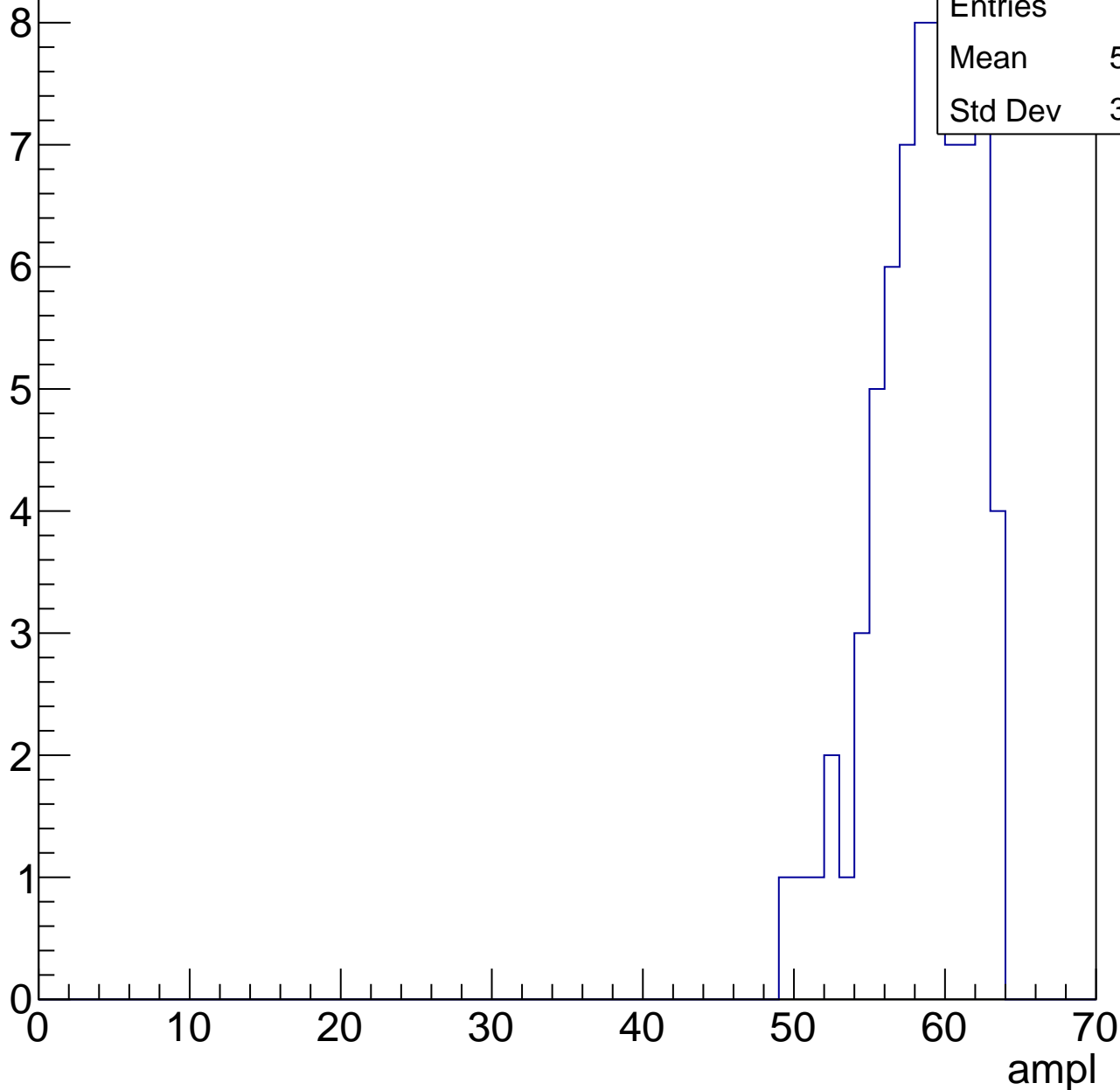


B1L102S, U14-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	58.12
Std Dev	3.277

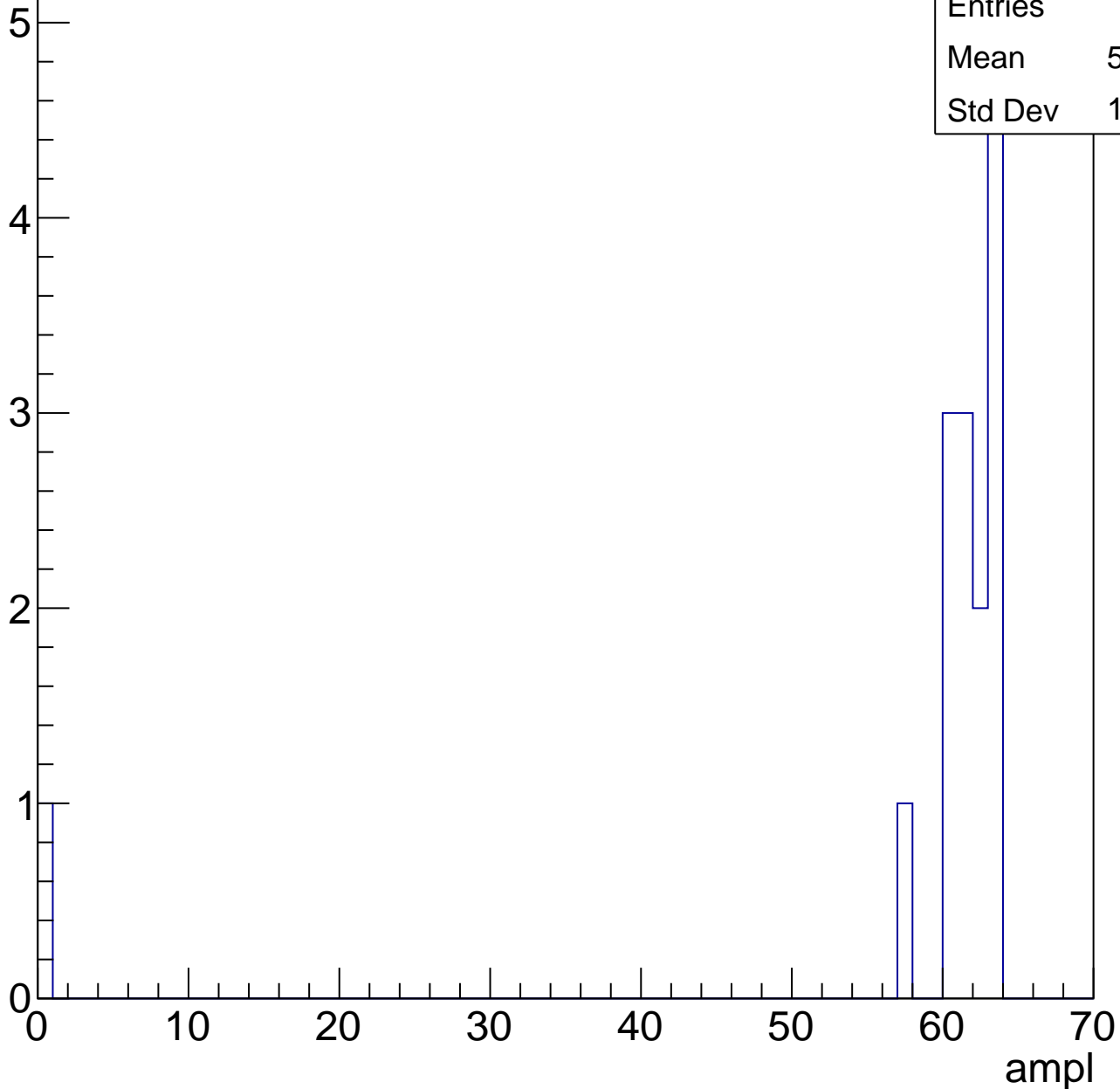


B1L102S, U14-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.27
Std Dev	15.39



B1L102S, U14-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch63, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	27.37
Std Dev	4.701

Gaus mean : 27.7539

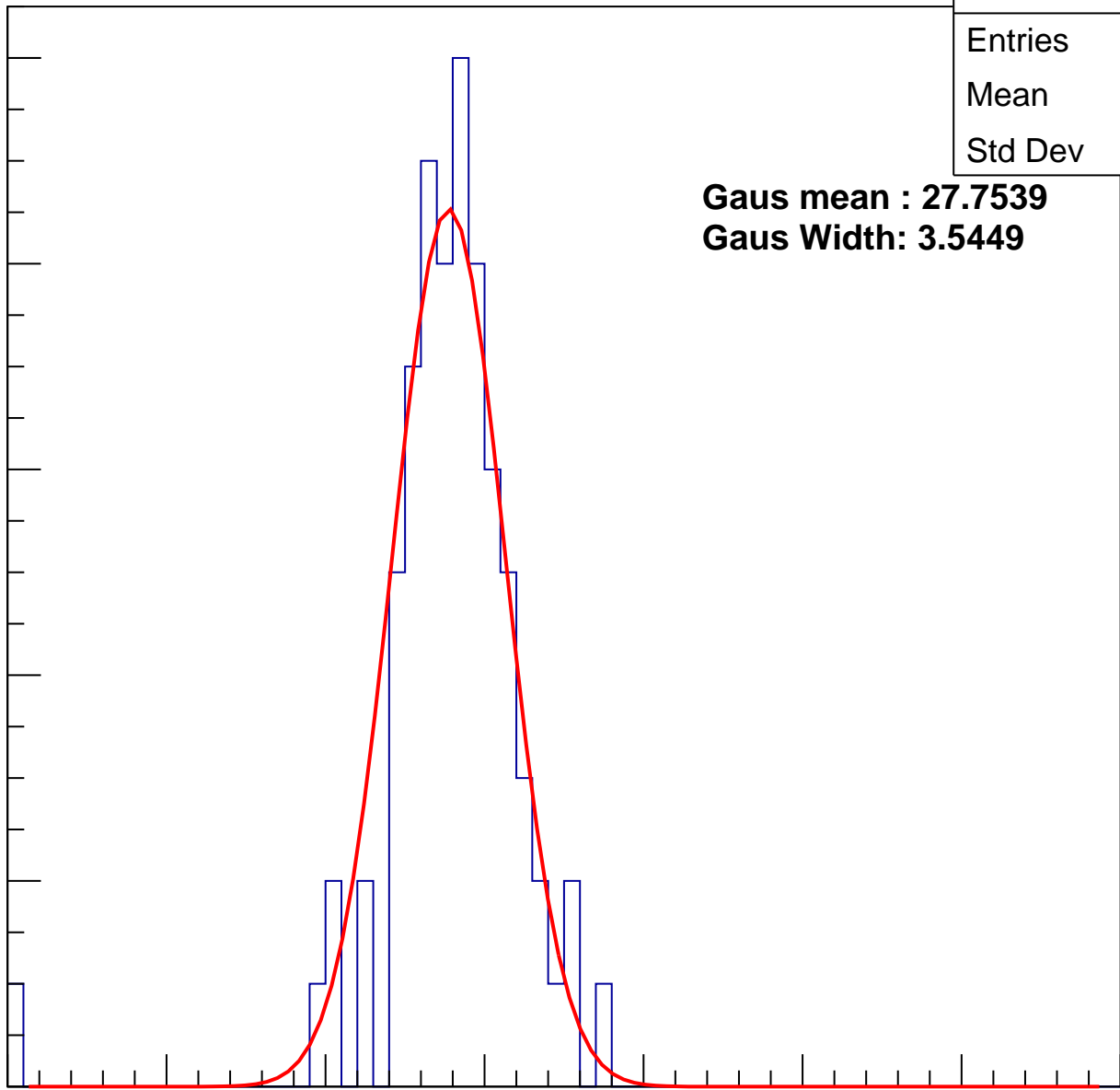
Gaus Width: 3.5449

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch63, adc1

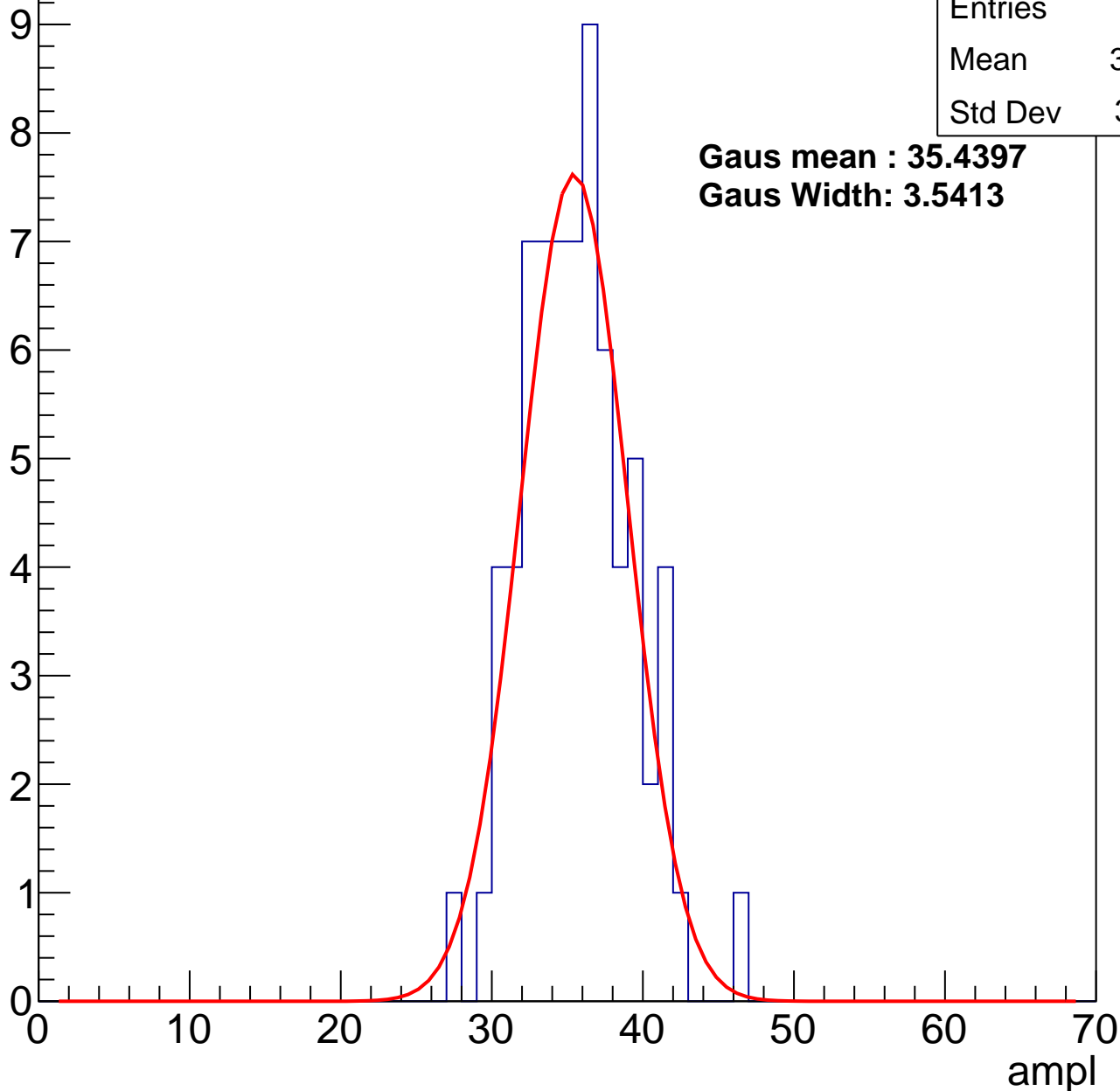
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.19
Std Dev	3.531

Gaus mean : 35.4397

Gaus Width: 3.5413

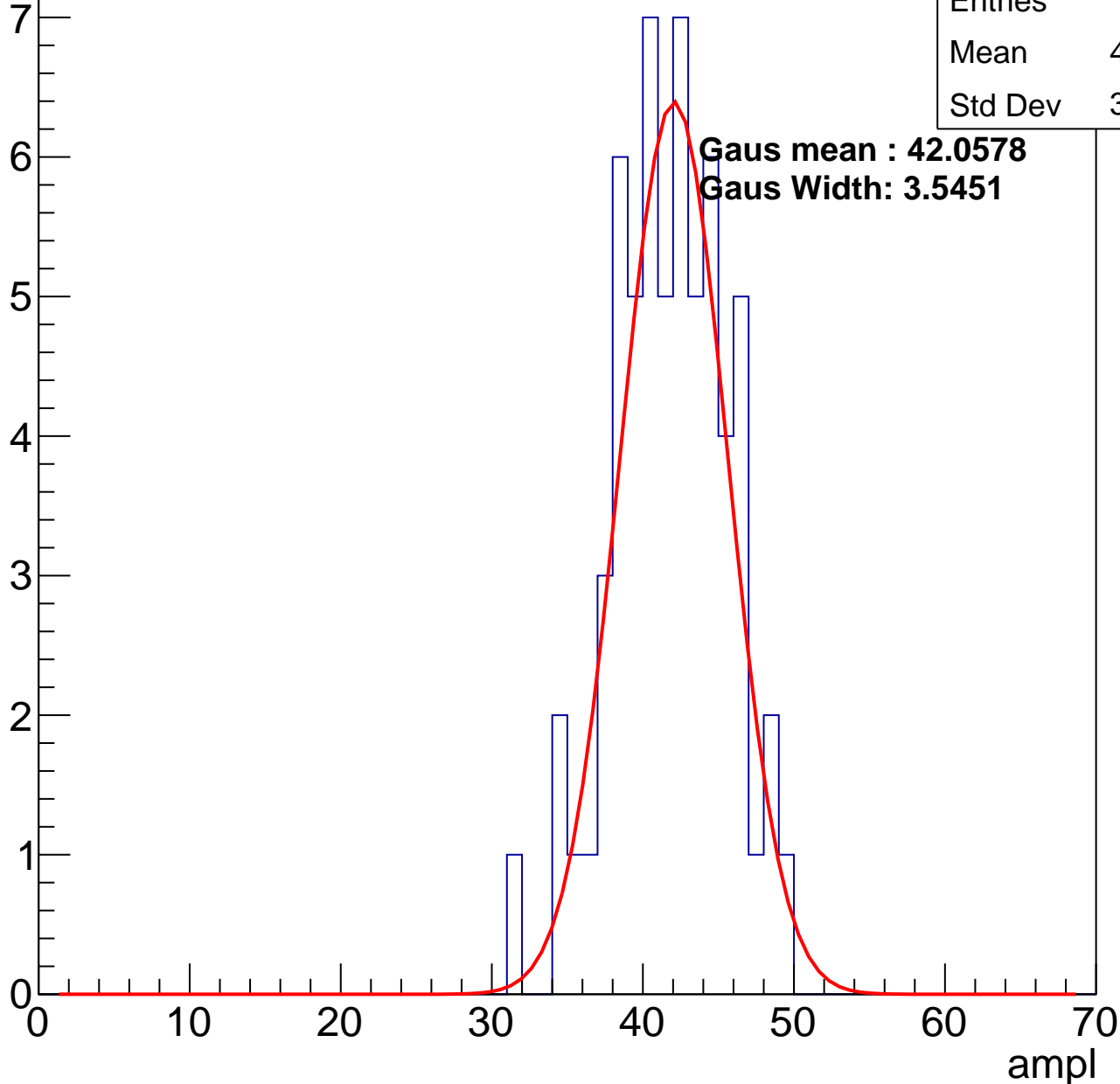


B1L102S, U14-ch63, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.35
Std Dev	3.699

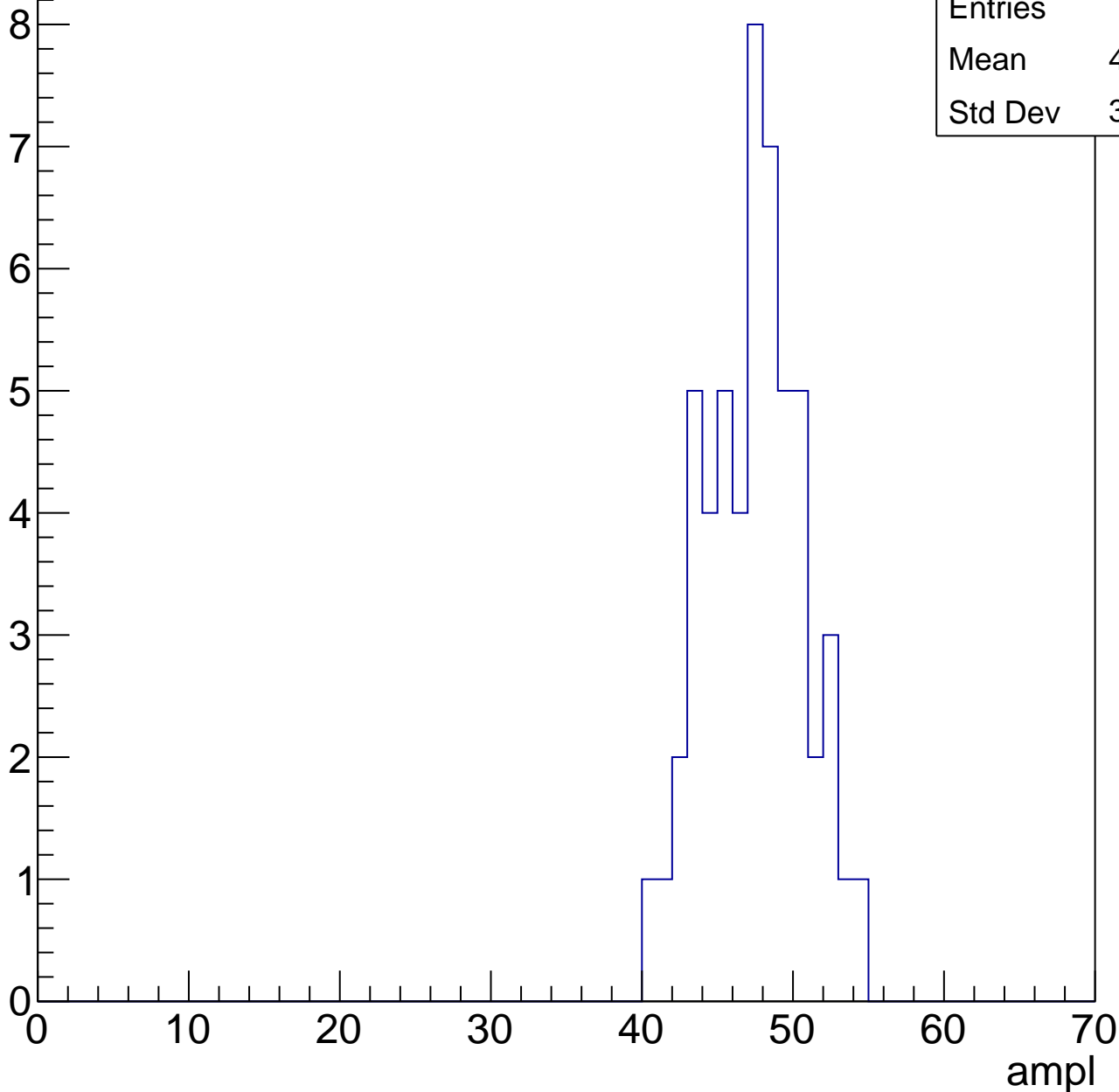


B1L102S, U14-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	46.98
Std Dev	3.159

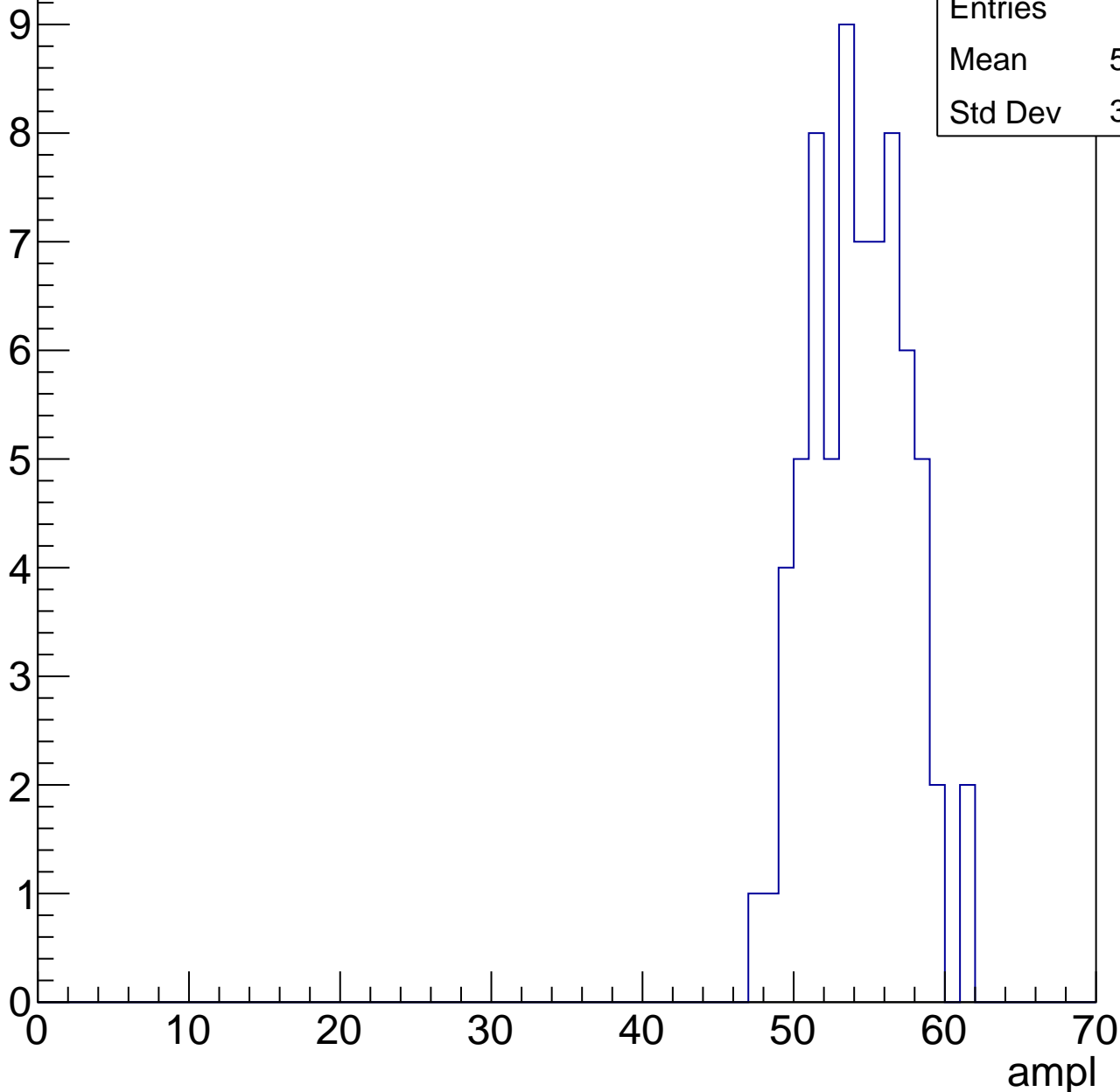


B1L102S, U14-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	53.84
Std Dev	3.129

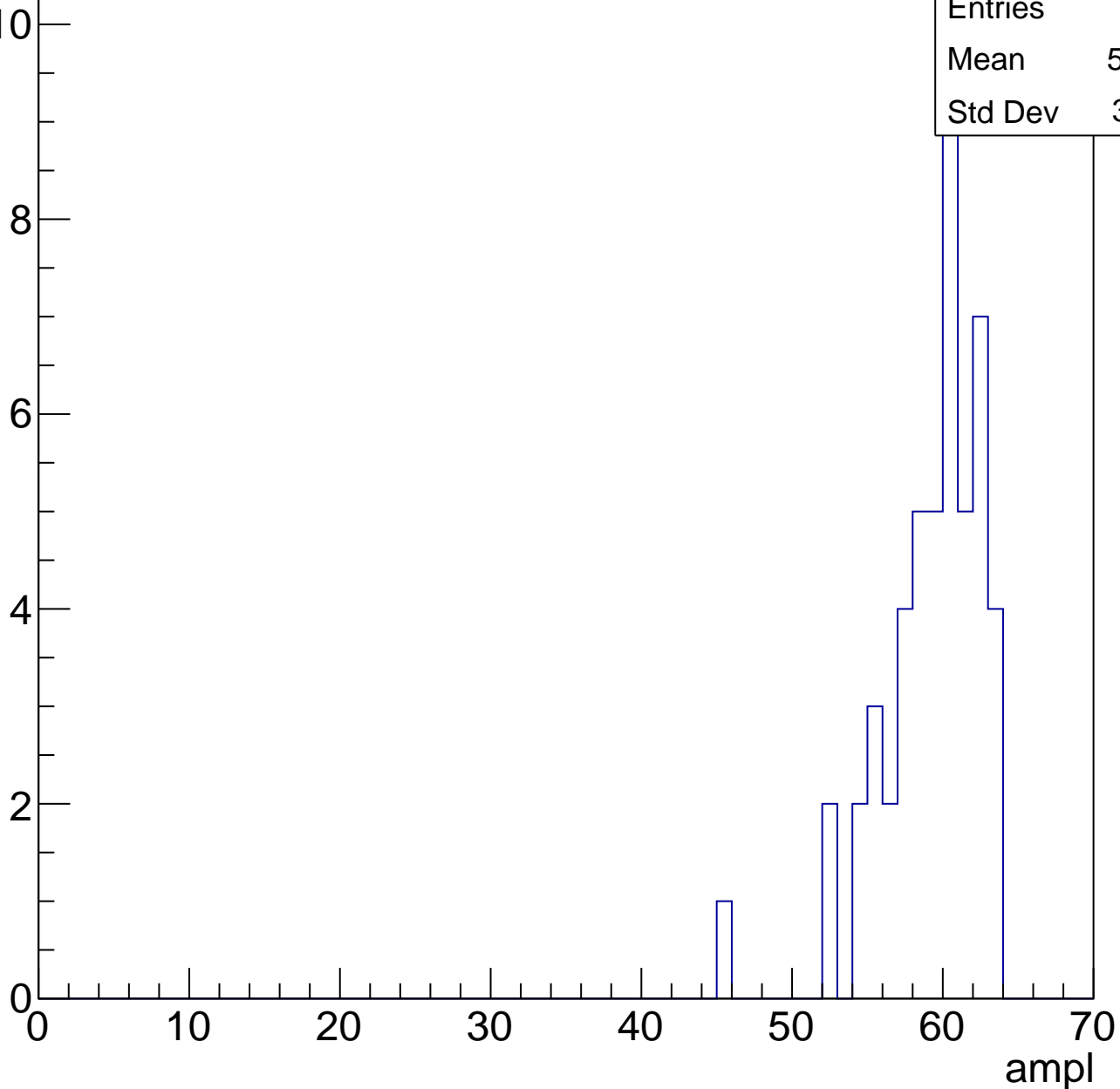


B1L102S, U14-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.76
Std Dev	3.421

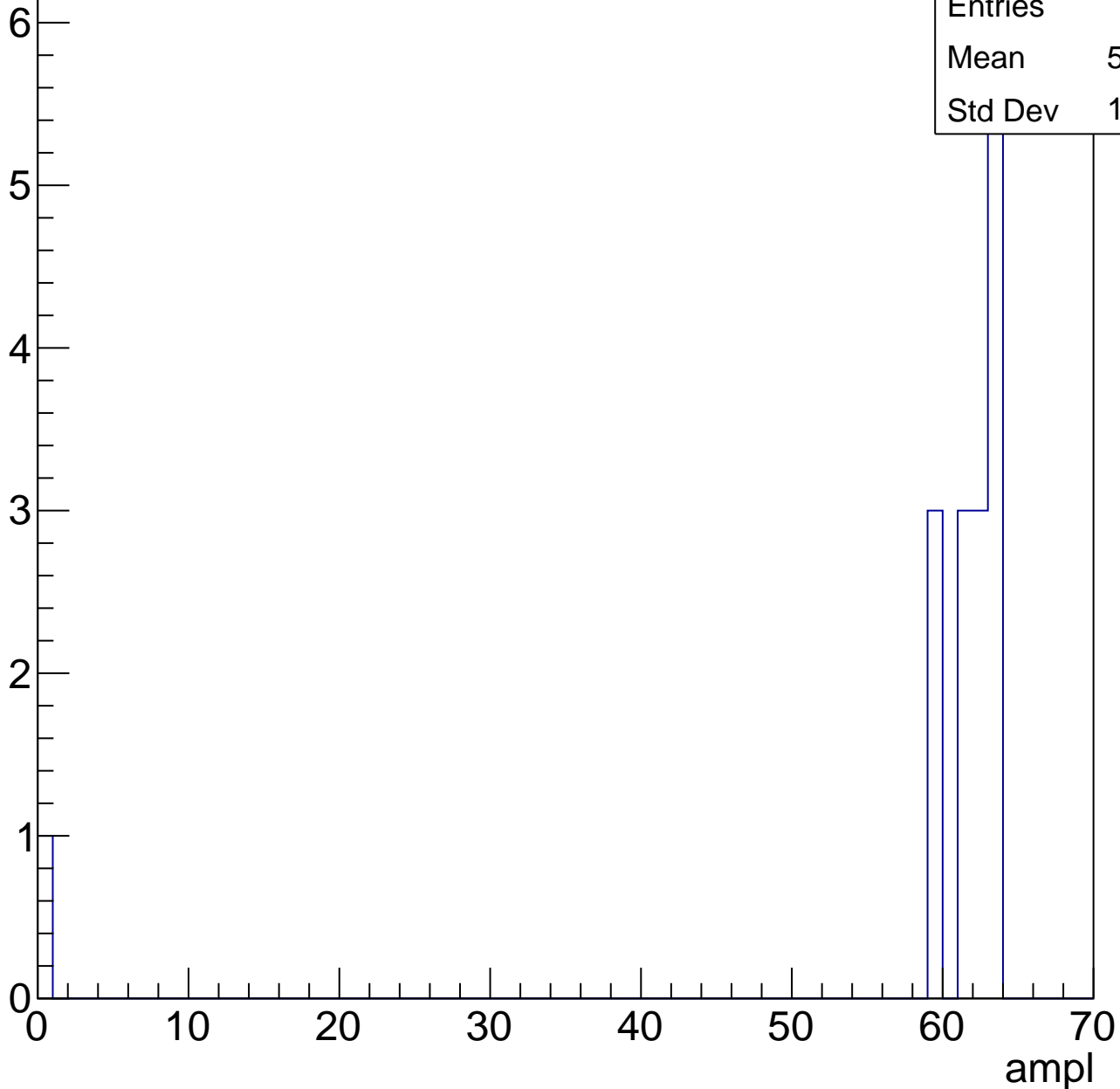


B1L102S, U14-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.75
Std Dev	14.98



B1L102S, U14-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch64, adc0

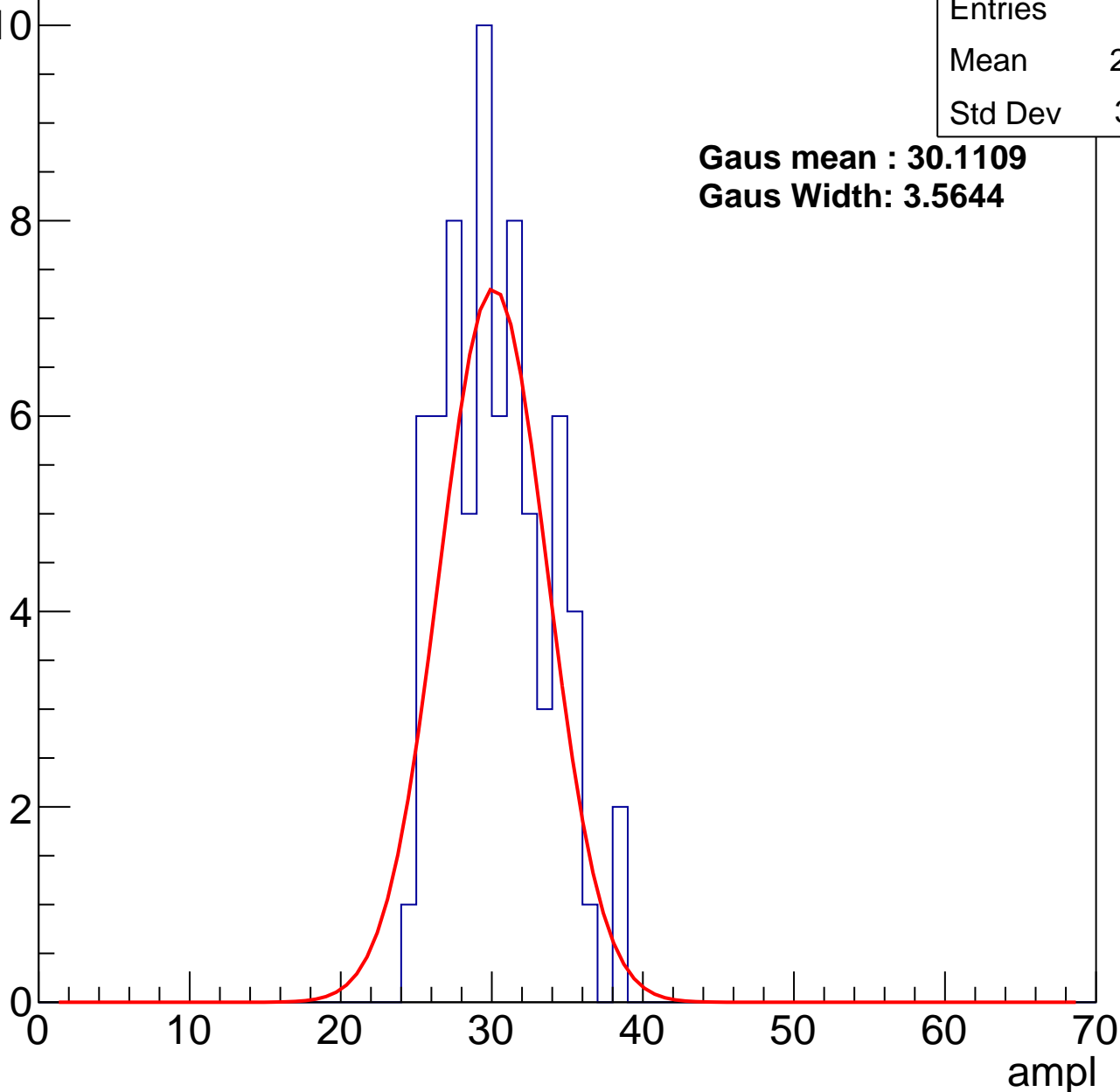
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.85
Std Dev	3.351

Gaus mean : 30.1109

Gaus Width: 3.5644



B1L102S, U14-ch64, adc1

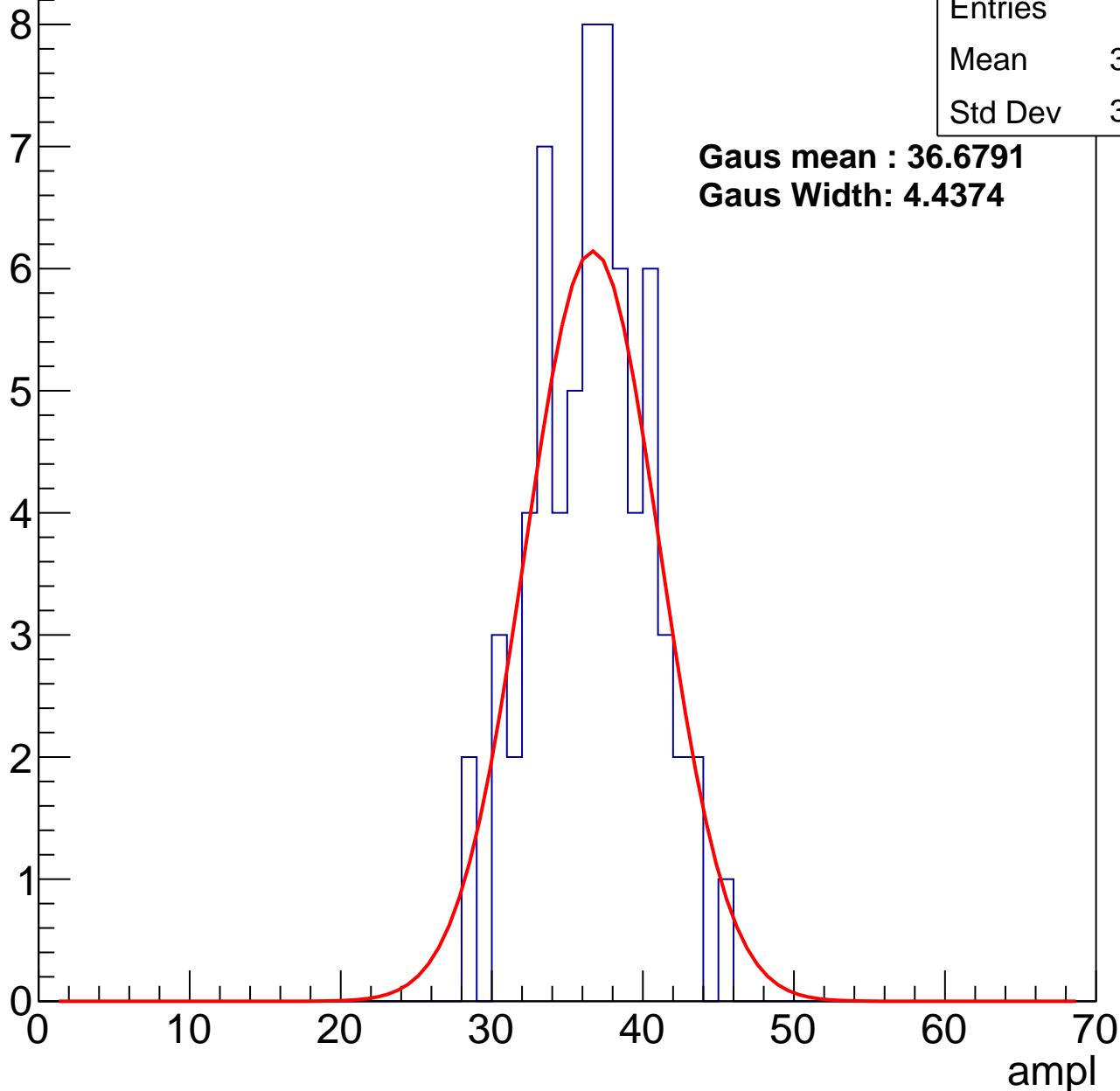
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.18
Std Dev	3.709

Gaus mean : 36.6791

Gaus Width: 4.4374



B1L102S, U14-ch64, adc2

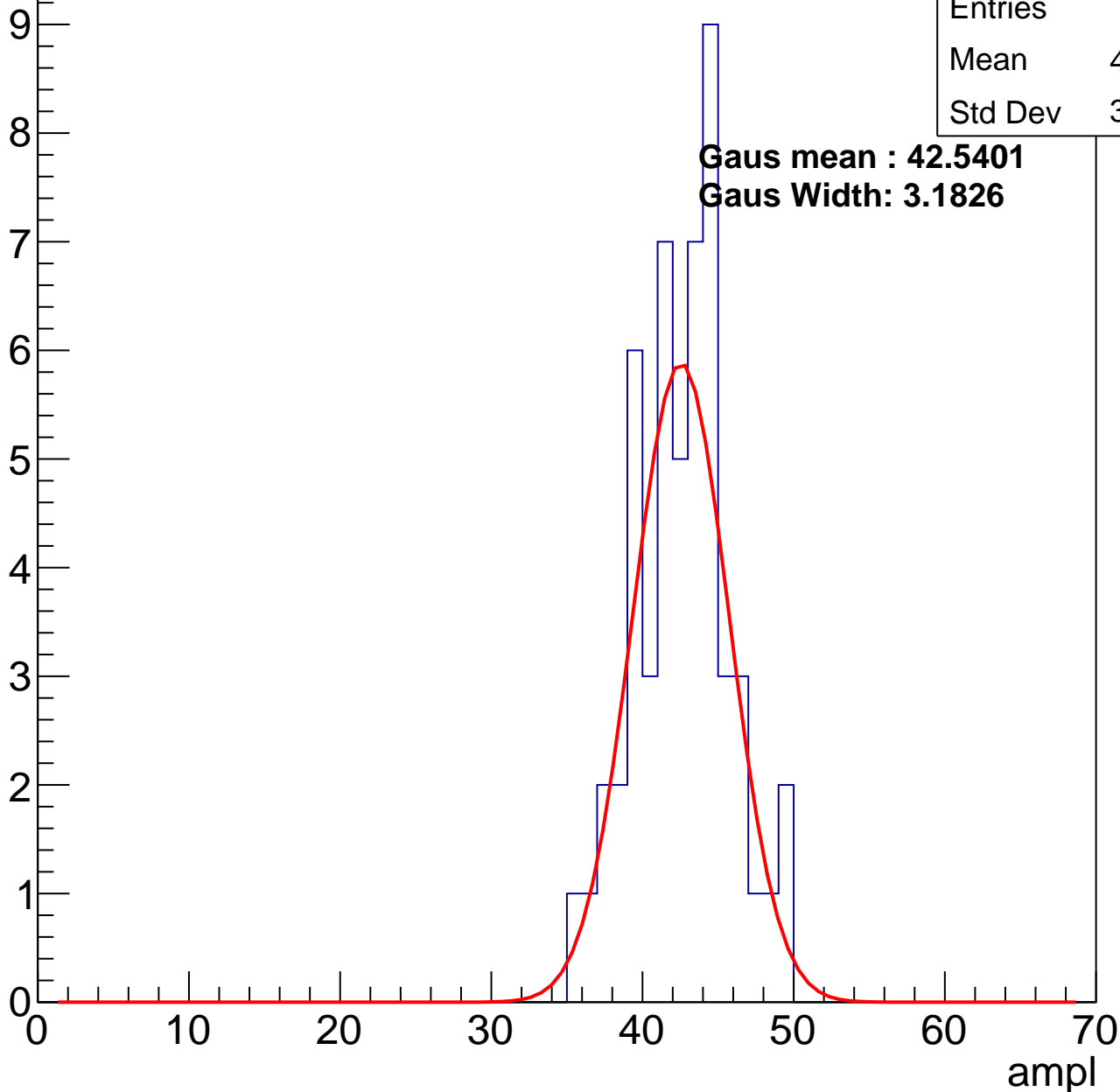
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.17
Std Dev	3.119

Gaus mean : 42.5401

Gaus Width: 3.1826

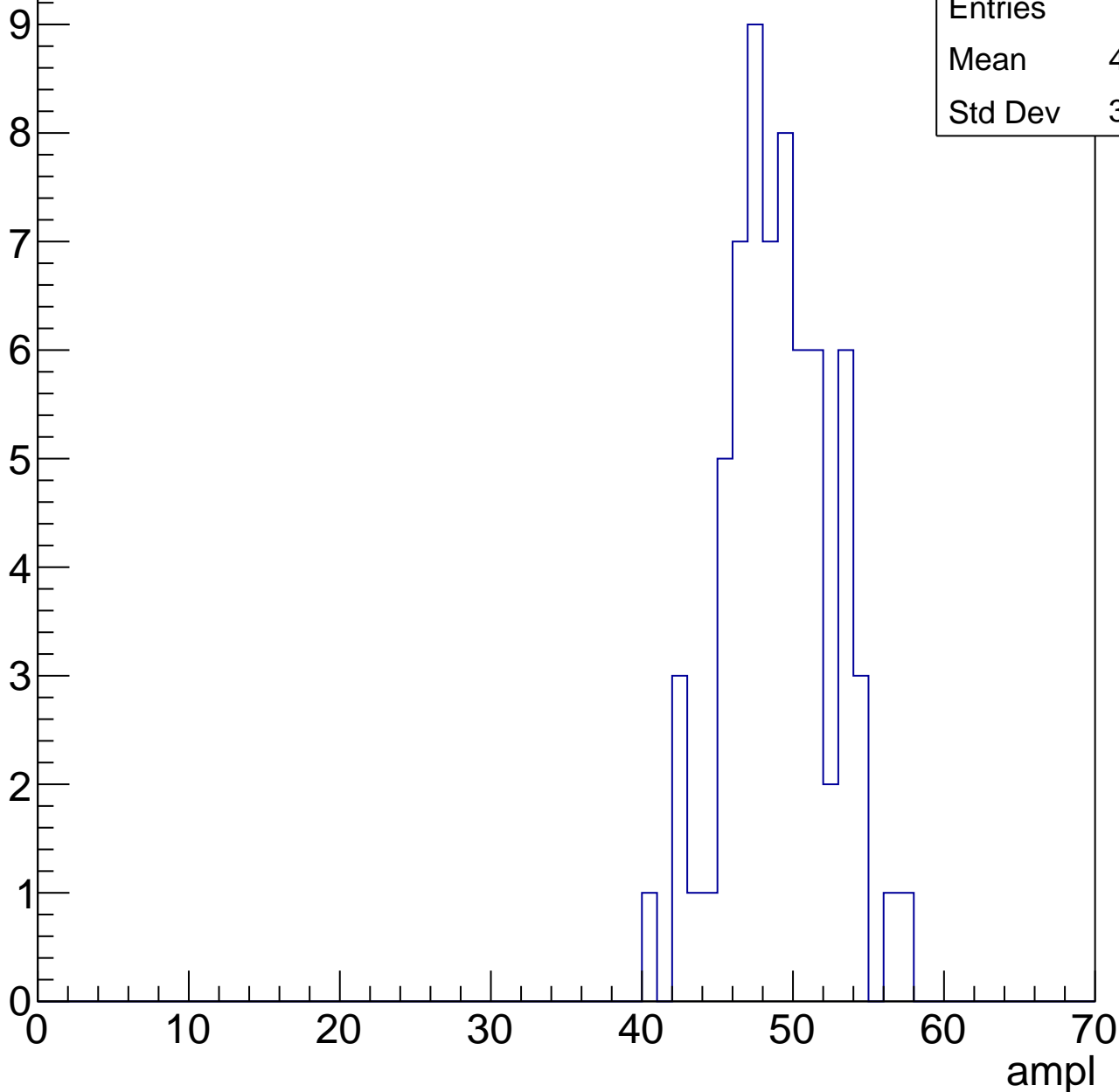


B1L102S, U14-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.57
Std Dev	3.443

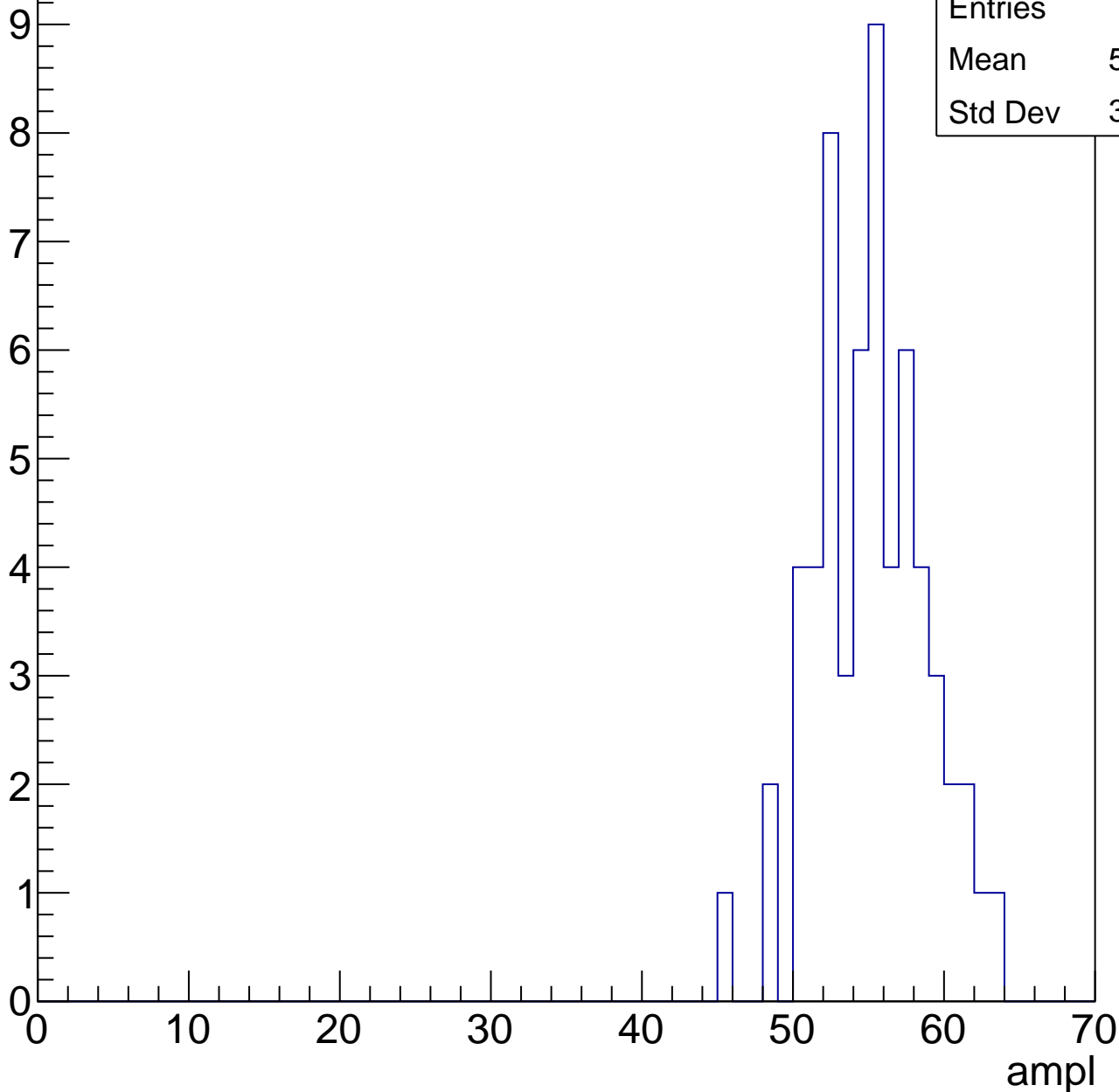


B1L102S, U14-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.68
Std Dev	3.635



B1L102S, U14-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	49
Mean	59.43
Std Dev	2.563

ampl

0

10

20

30

40

50

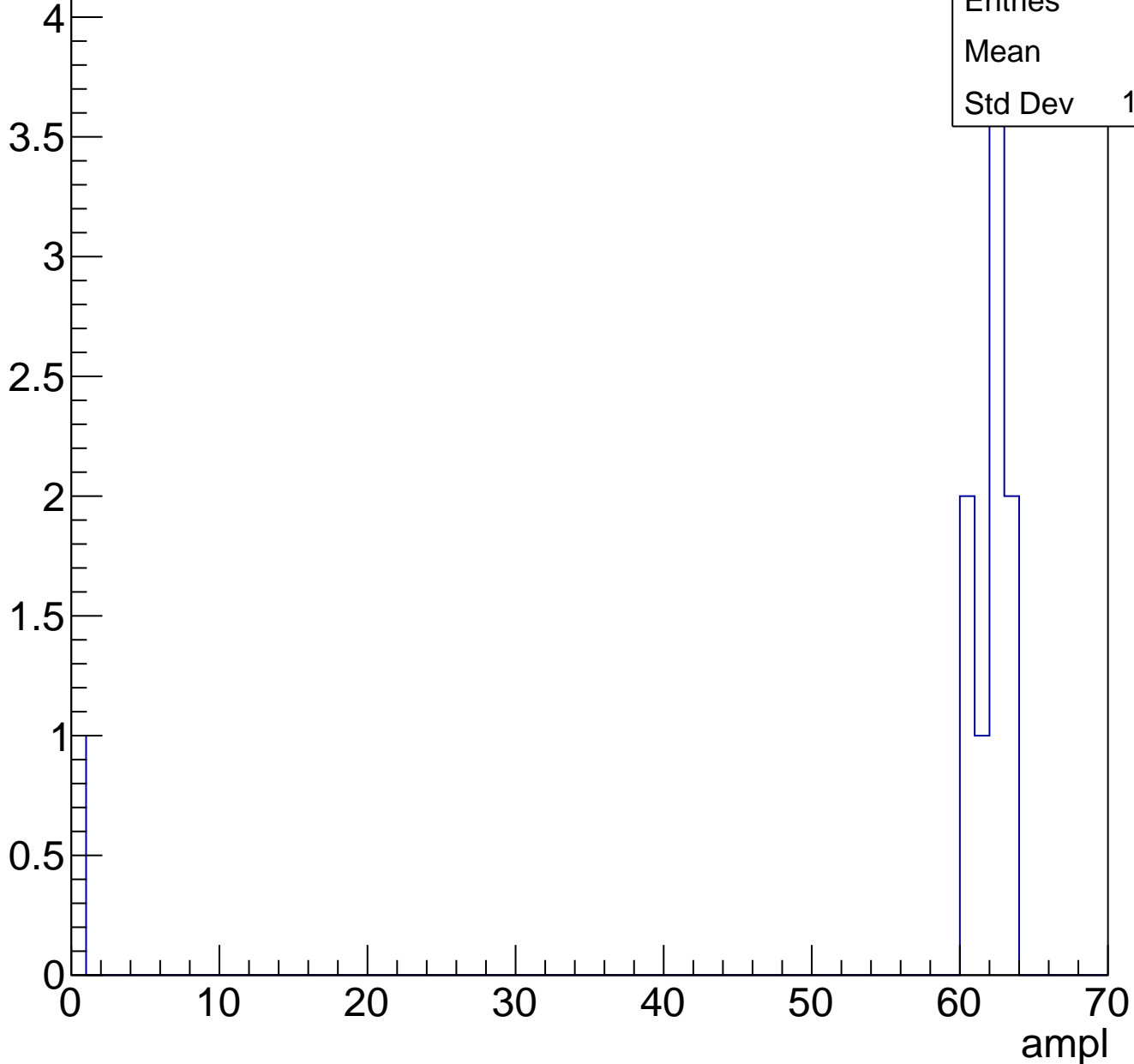
60

70

B1L102S, U14-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch65, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	29.06
Std Dev	4.992

Gaus mean : 29.8484

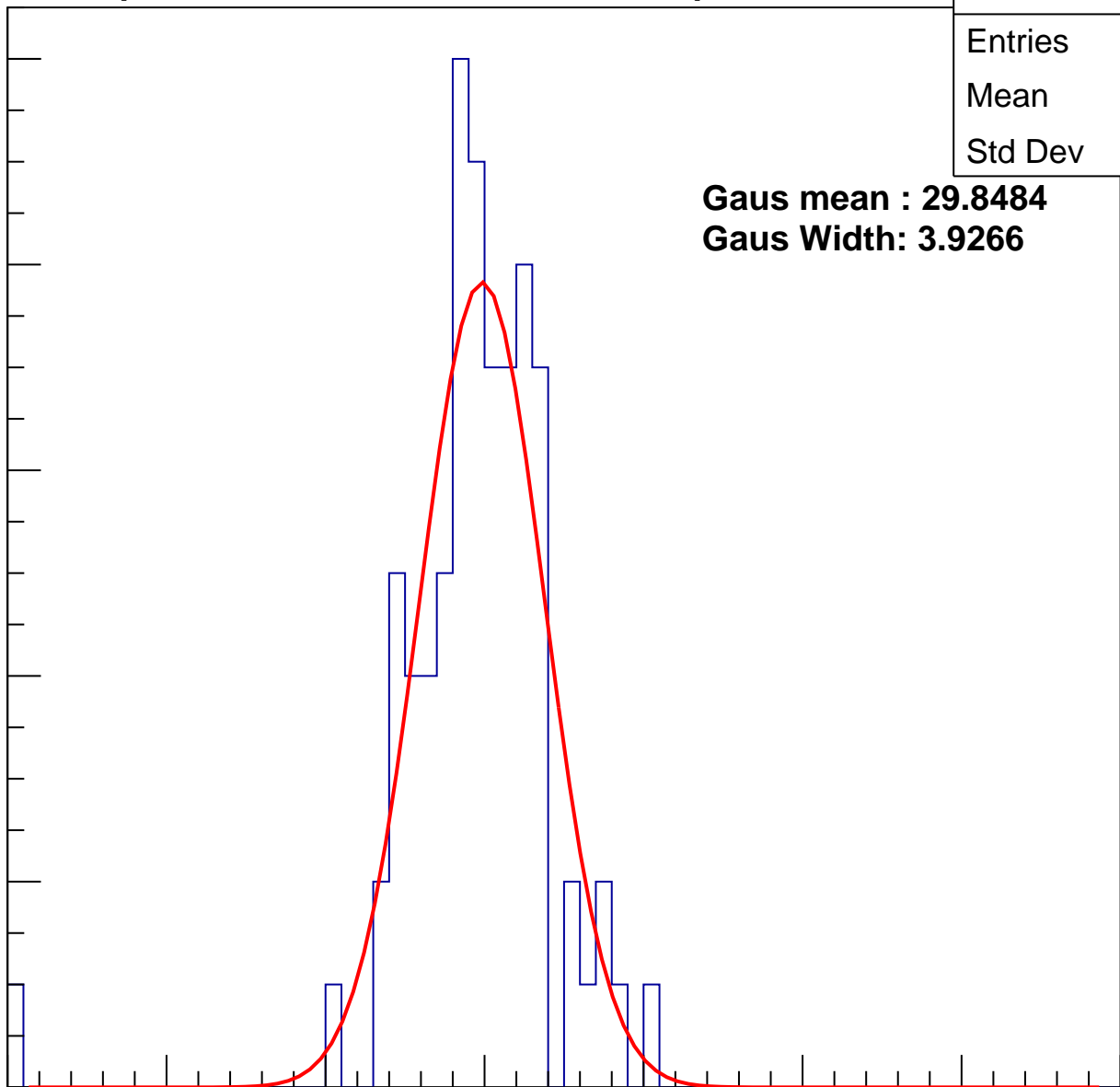
Gaus Width: 3.9266

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch65, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	35.82
Std Dev	3.078

Gaus mean : 36.9496

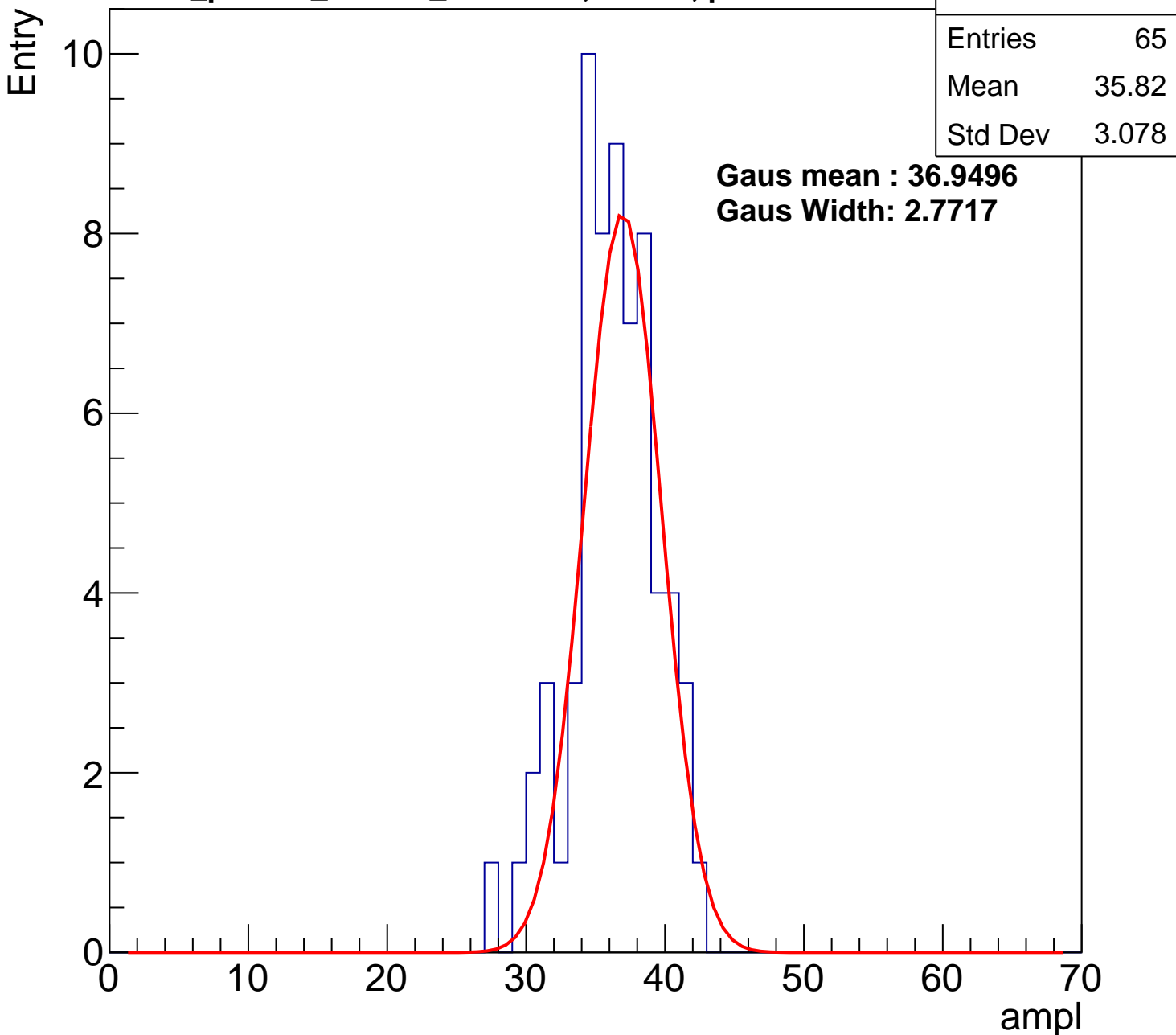
Gaus Width: 2.7717

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch65, adc2

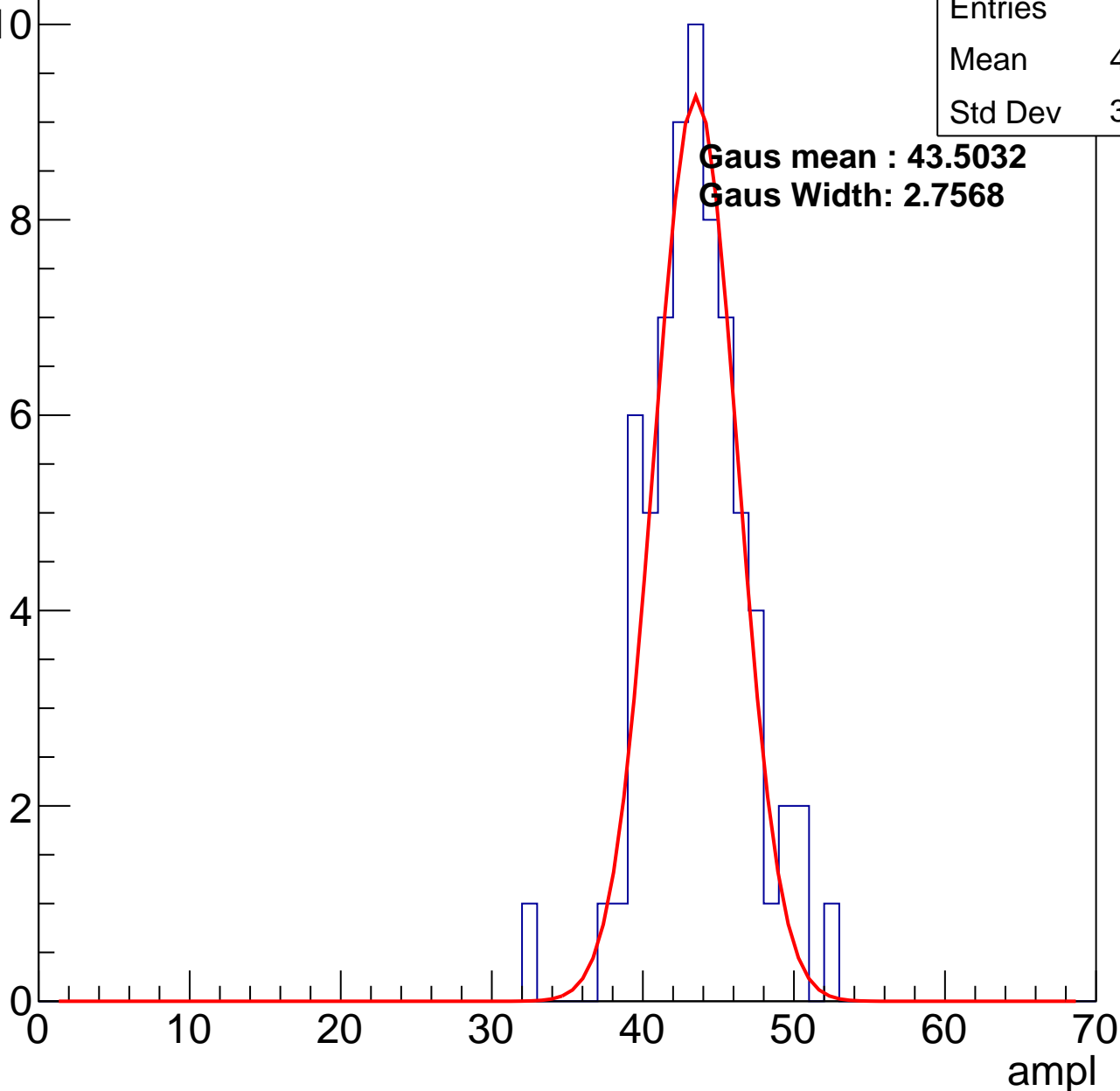
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	43.13
Std Dev	3.338

Gaus mean : 43.5032

Gaus Width: 2.7568

Entry

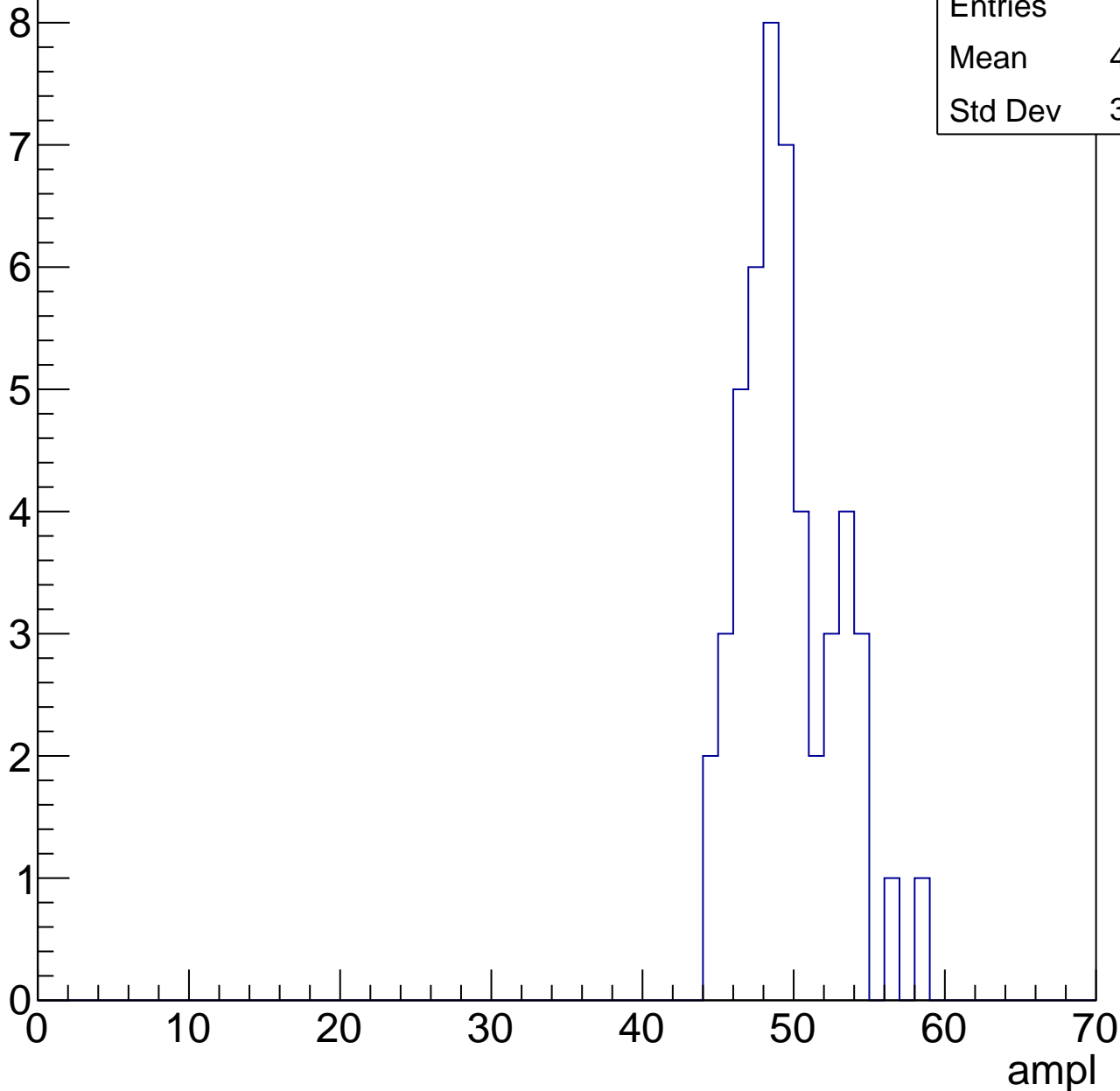


B1L102S, U14-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

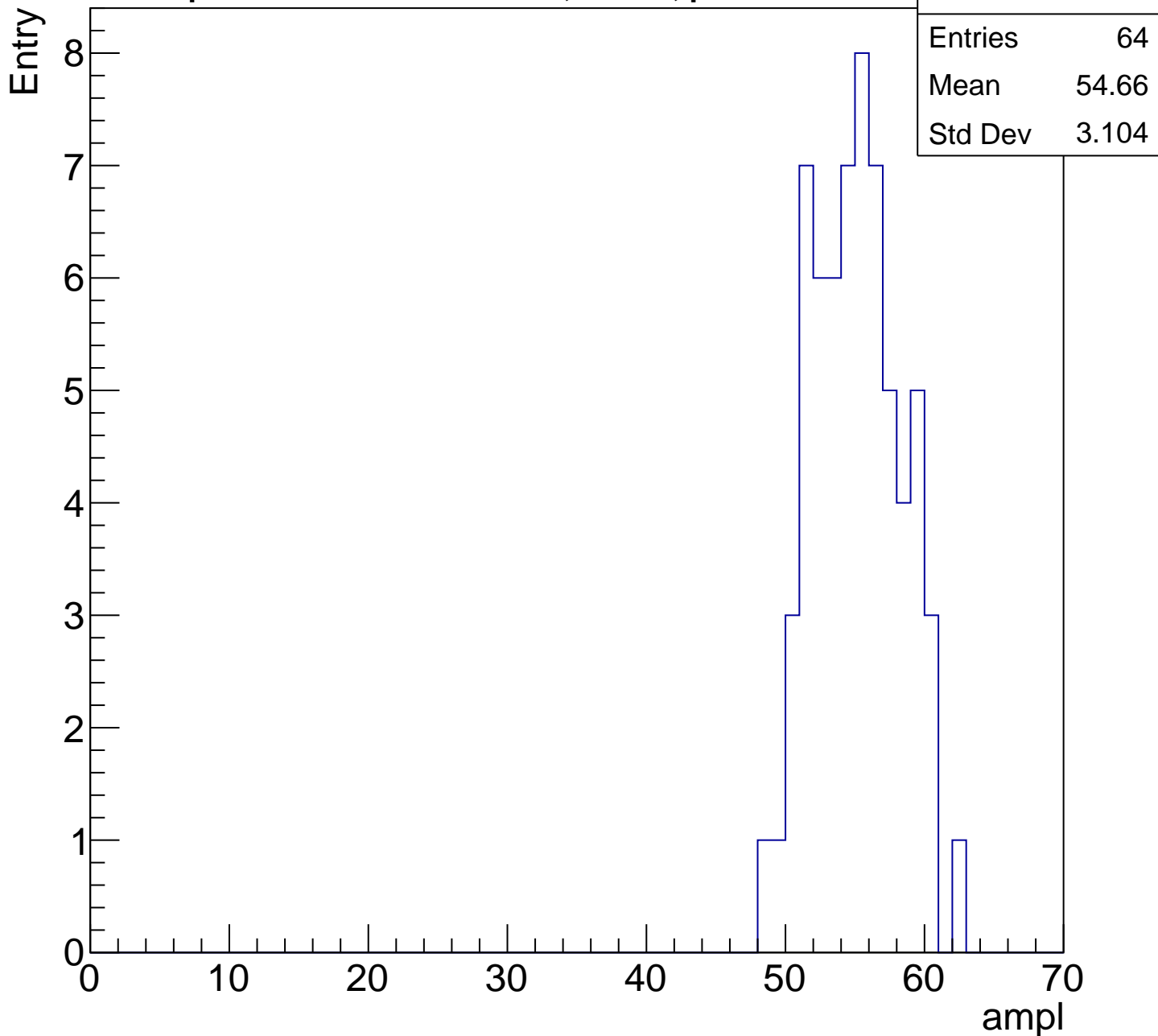
Entry

Entries	49
Mean	49.14
Std Dev	3.136



B1L102S, U14-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

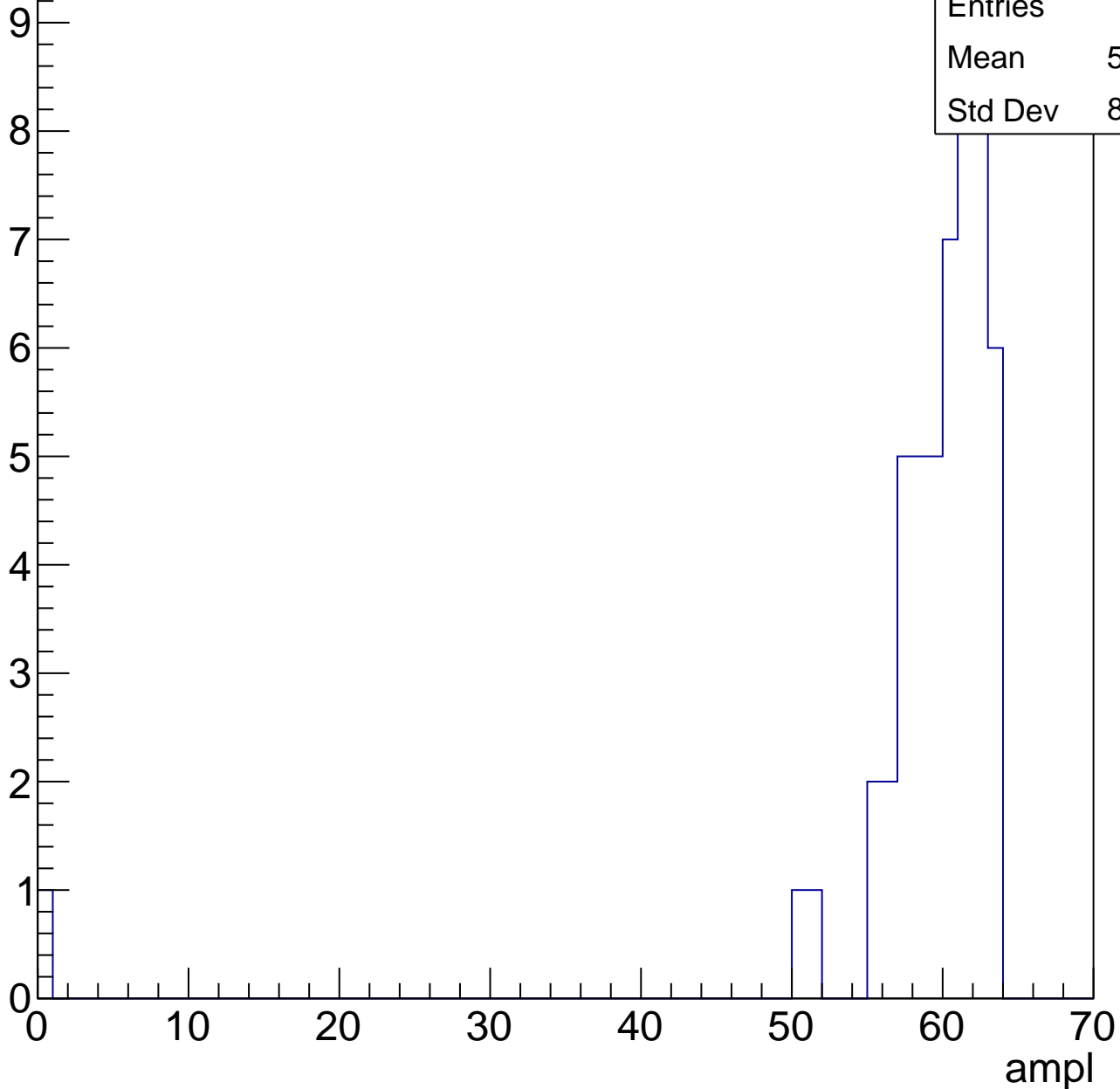


B1L102S, U14-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.38
Std Dev	8.652



B1L102S, U14-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch66, adc0

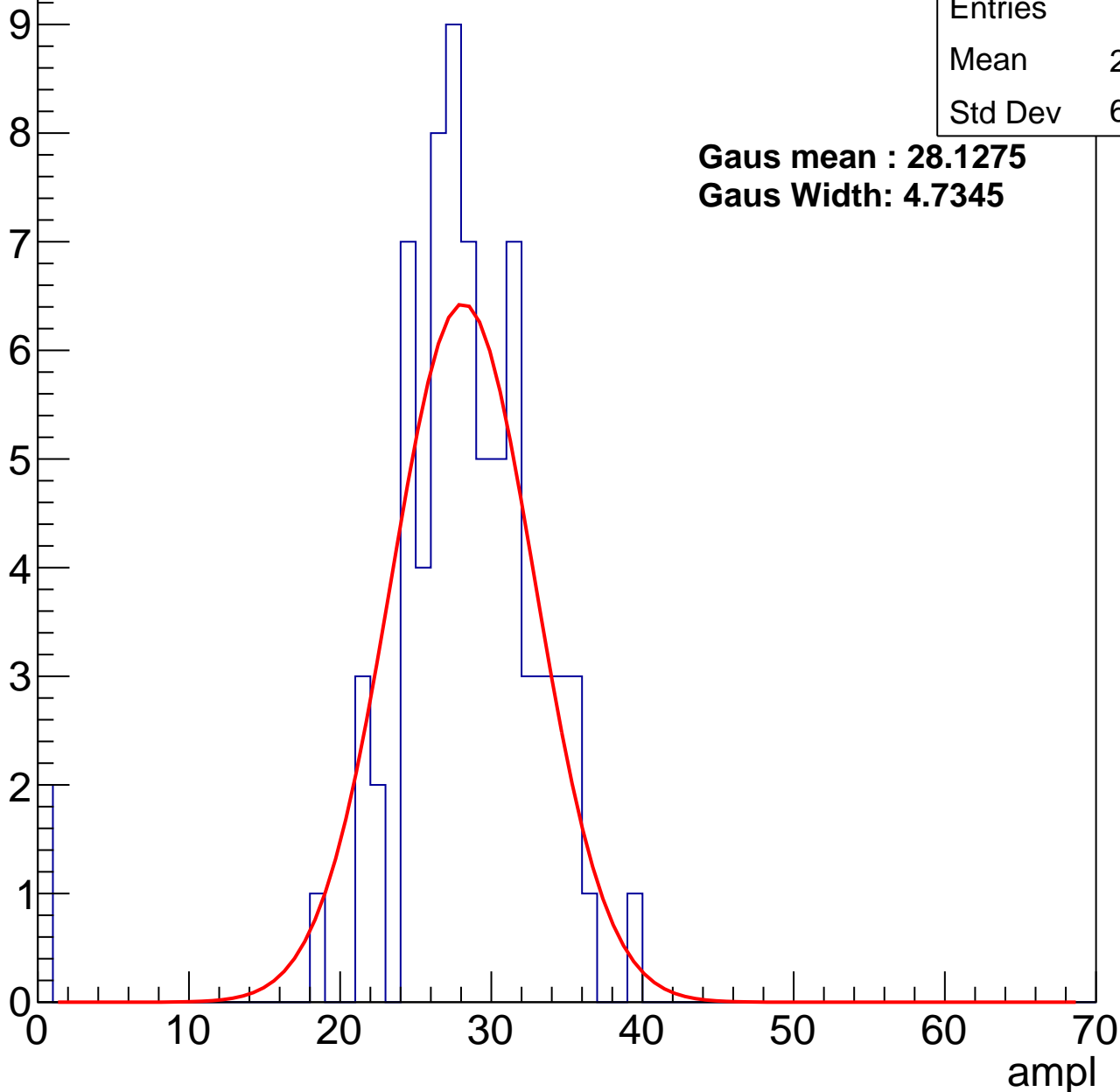
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.42
Std Dev	6.043

Gaus mean : 28.1275

Gaus Width: 4.7345



B1L102S, U14-ch66, adc1

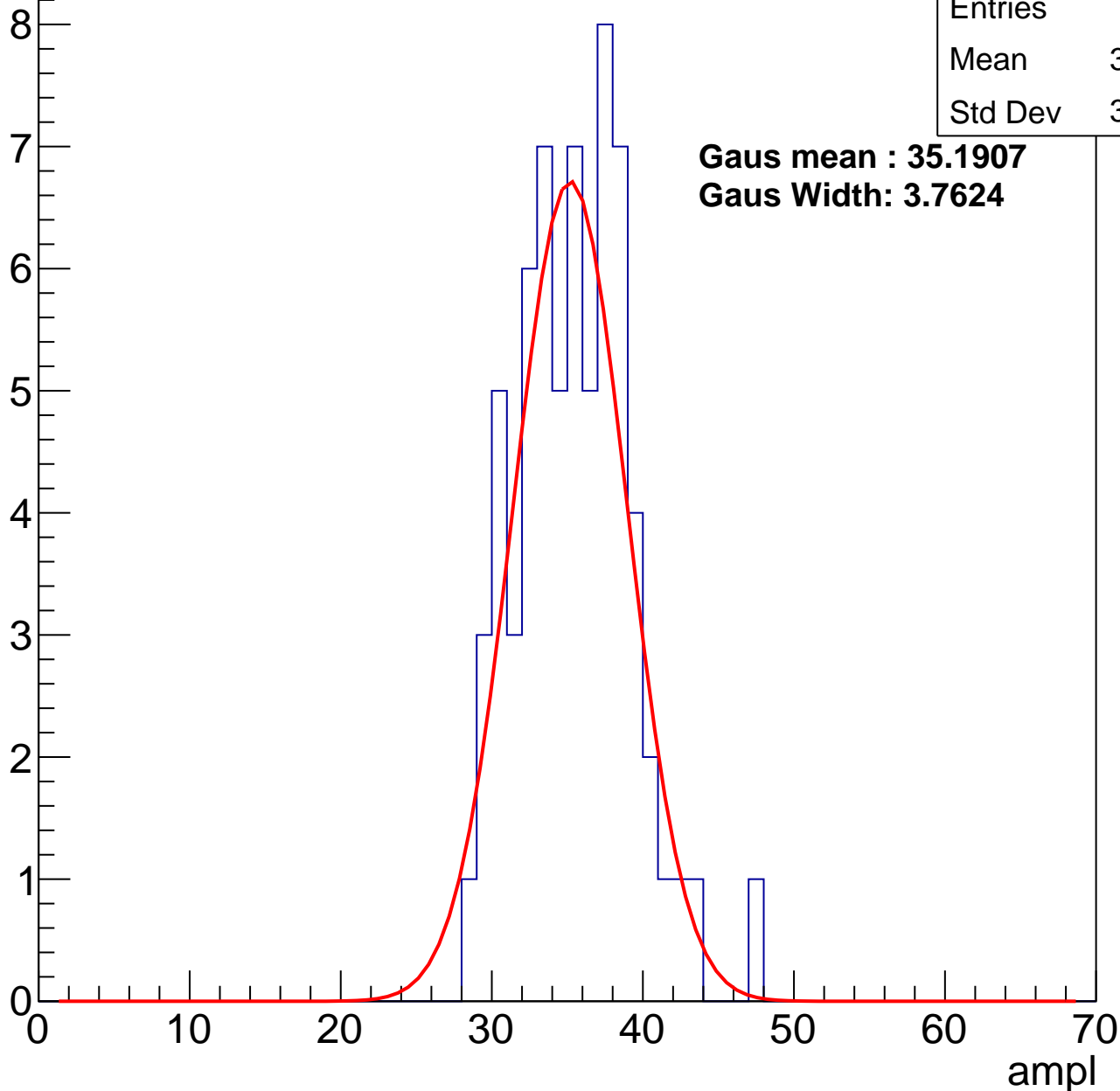
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.03
Std Dev	3.714

Gaus mean : 35.1907

Gaus Width: 3.7624



B1L102S, U14-ch66, adc2

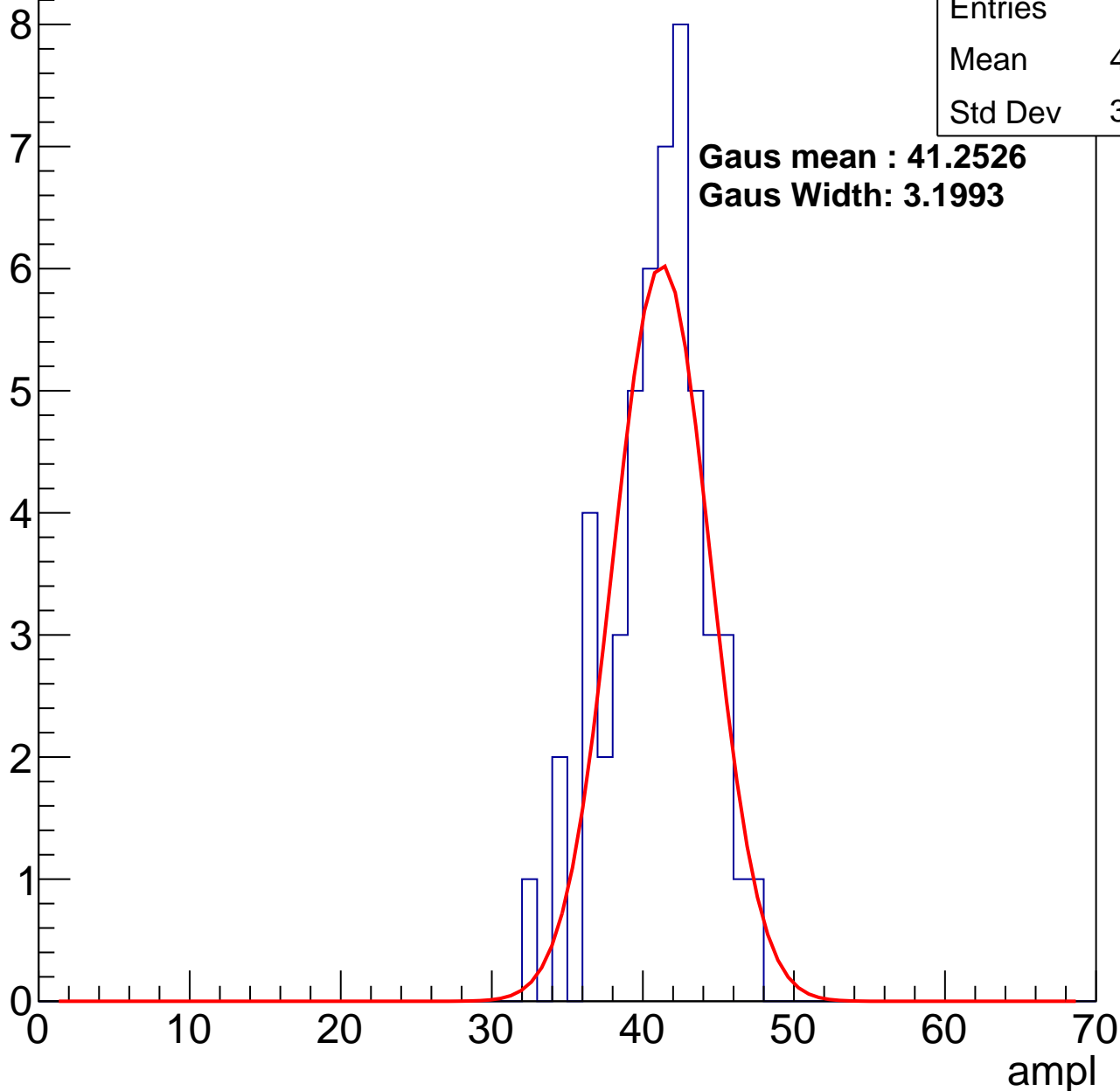
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	40.49
Std Dev	3.177

Gaus mean : 41.2526

Gaus Width: 3.1993

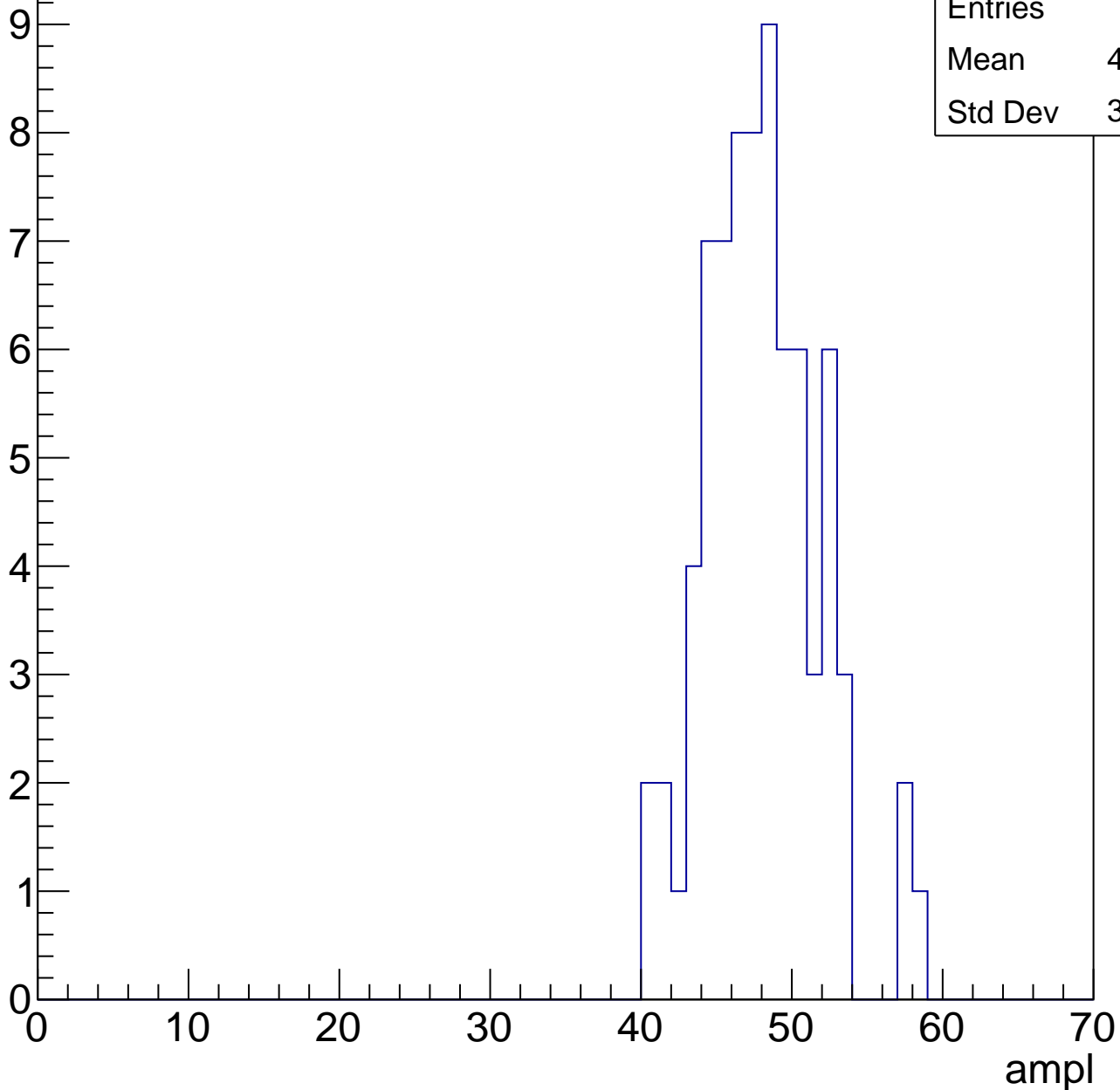


B1L102S, U14-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

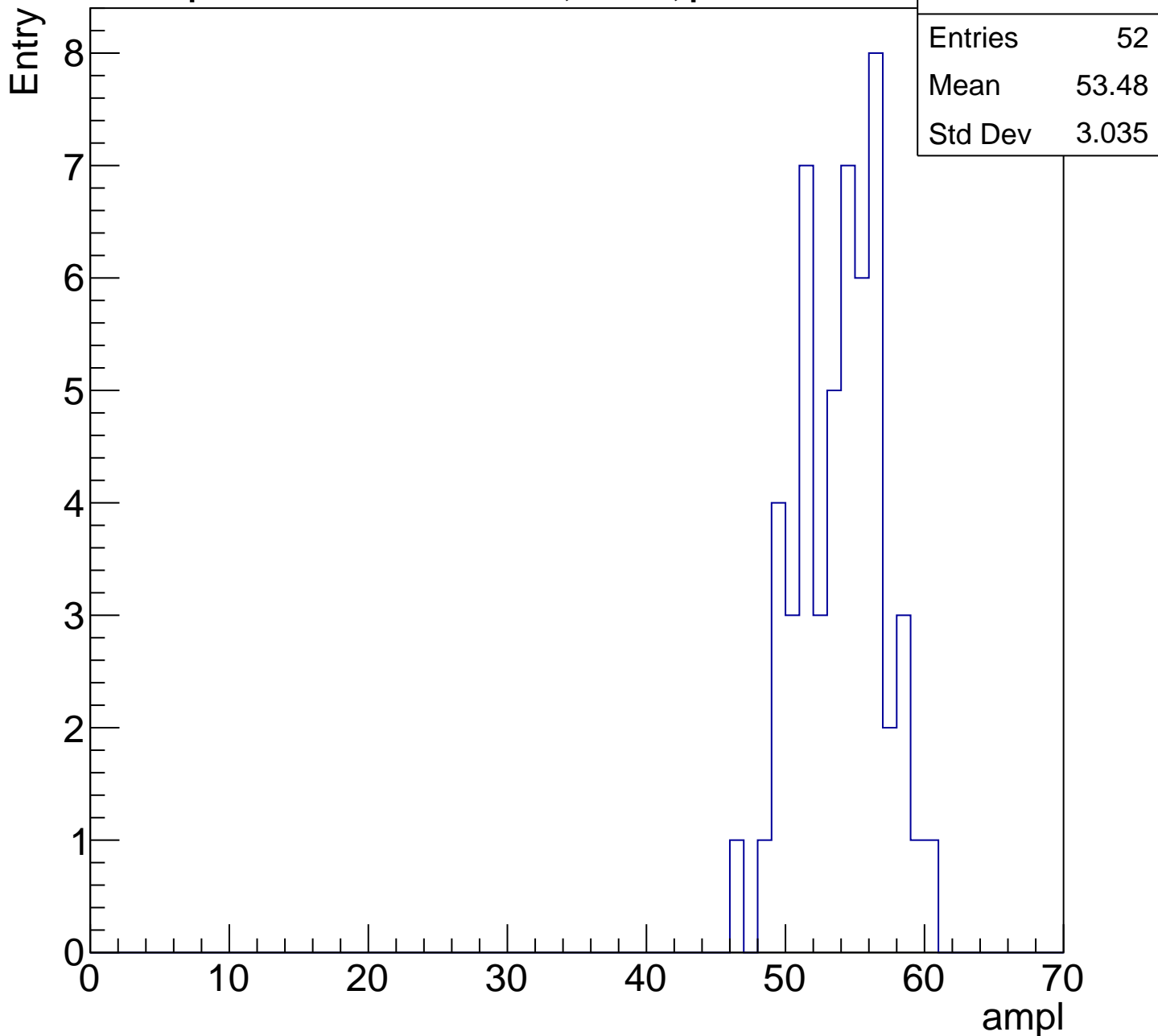
Entry

Entries	75
Mean	47.53
Std Dev	3.743



B1L102S, U14-ch66, adc4

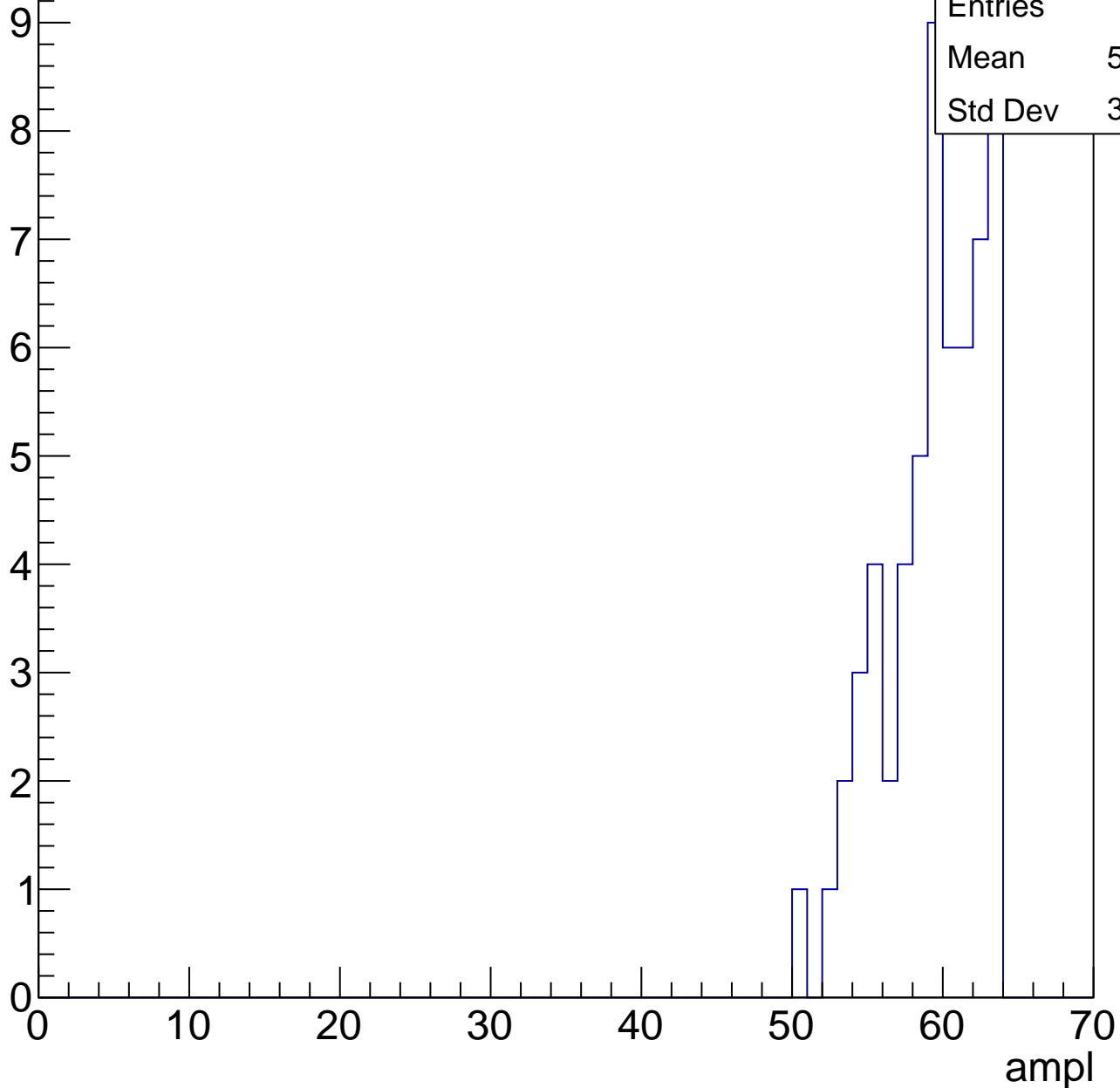
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U14-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

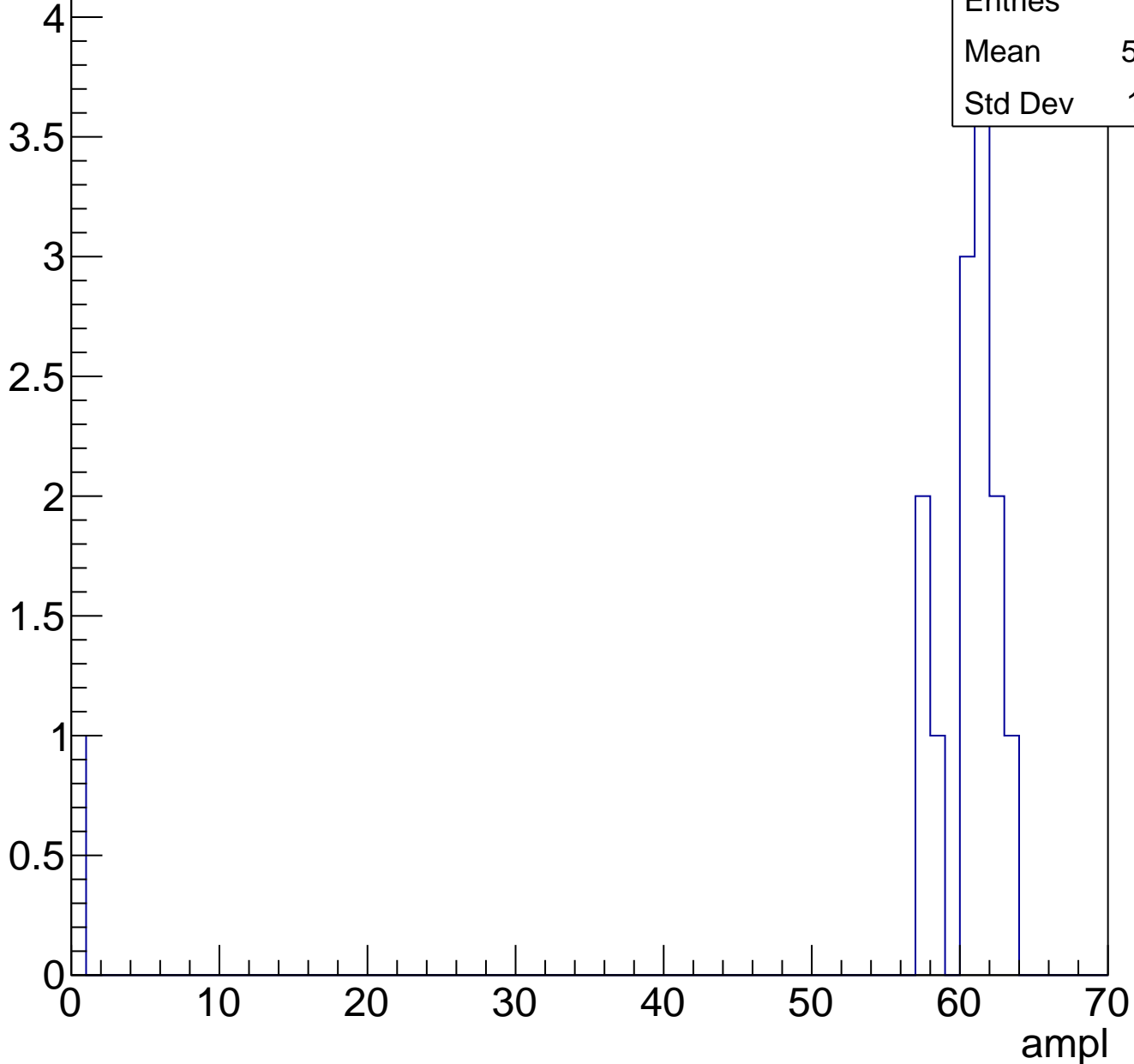
Entry



B1L102S, U14-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch67, adc0

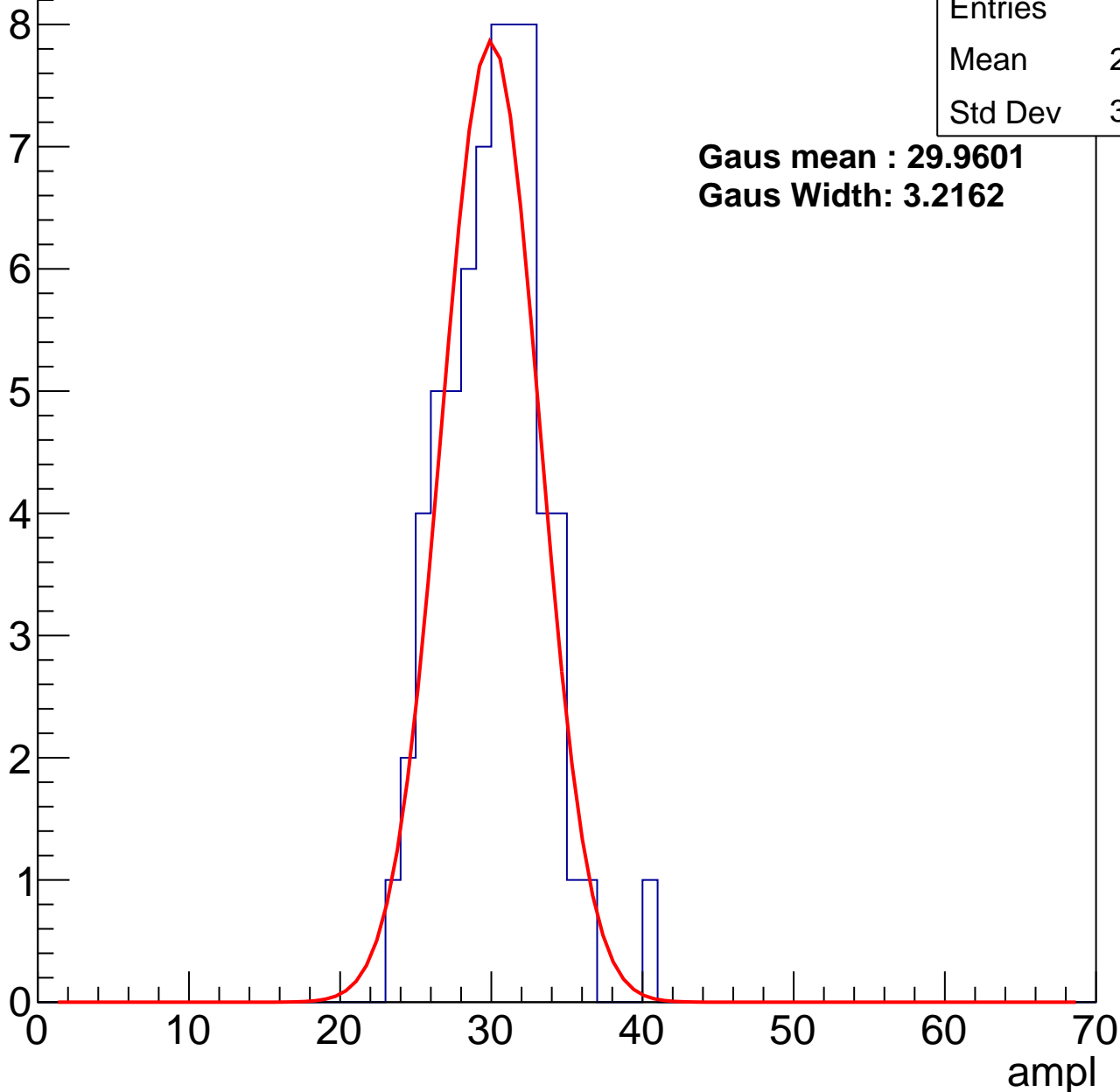
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.69
Std Dev	3.215

Gaus mean : 29.9601

Gaus Width: 3.2162



B1L102S, U14-ch67, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	36.91
Std Dev	4.178

Gaus mean : 37.0859

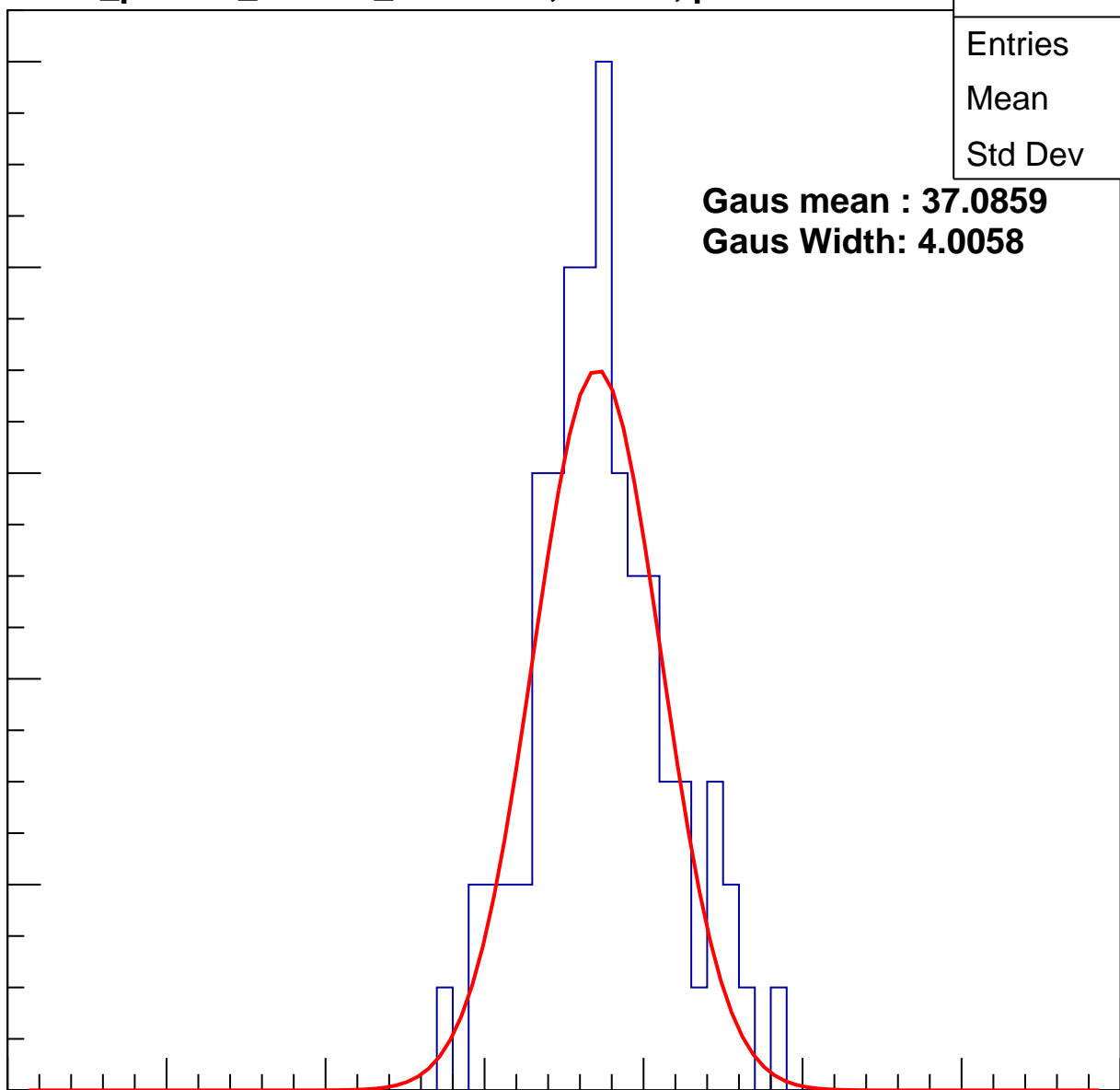
Gaus Width: 4.0058

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch67, adc2

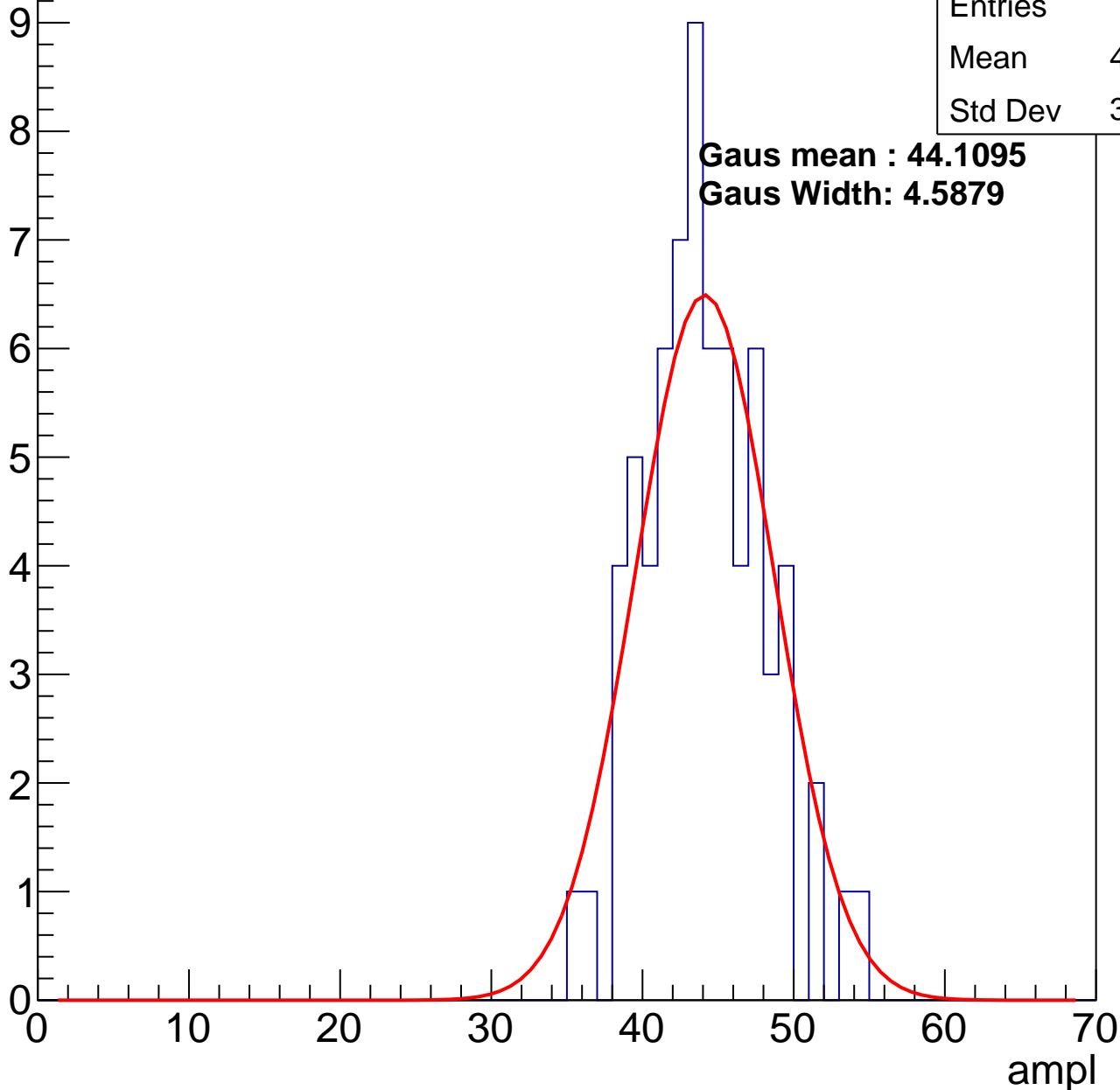
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.63
Std Dev	3.896

Gaus mean : 44.1095

Gaus Width: 4.5879

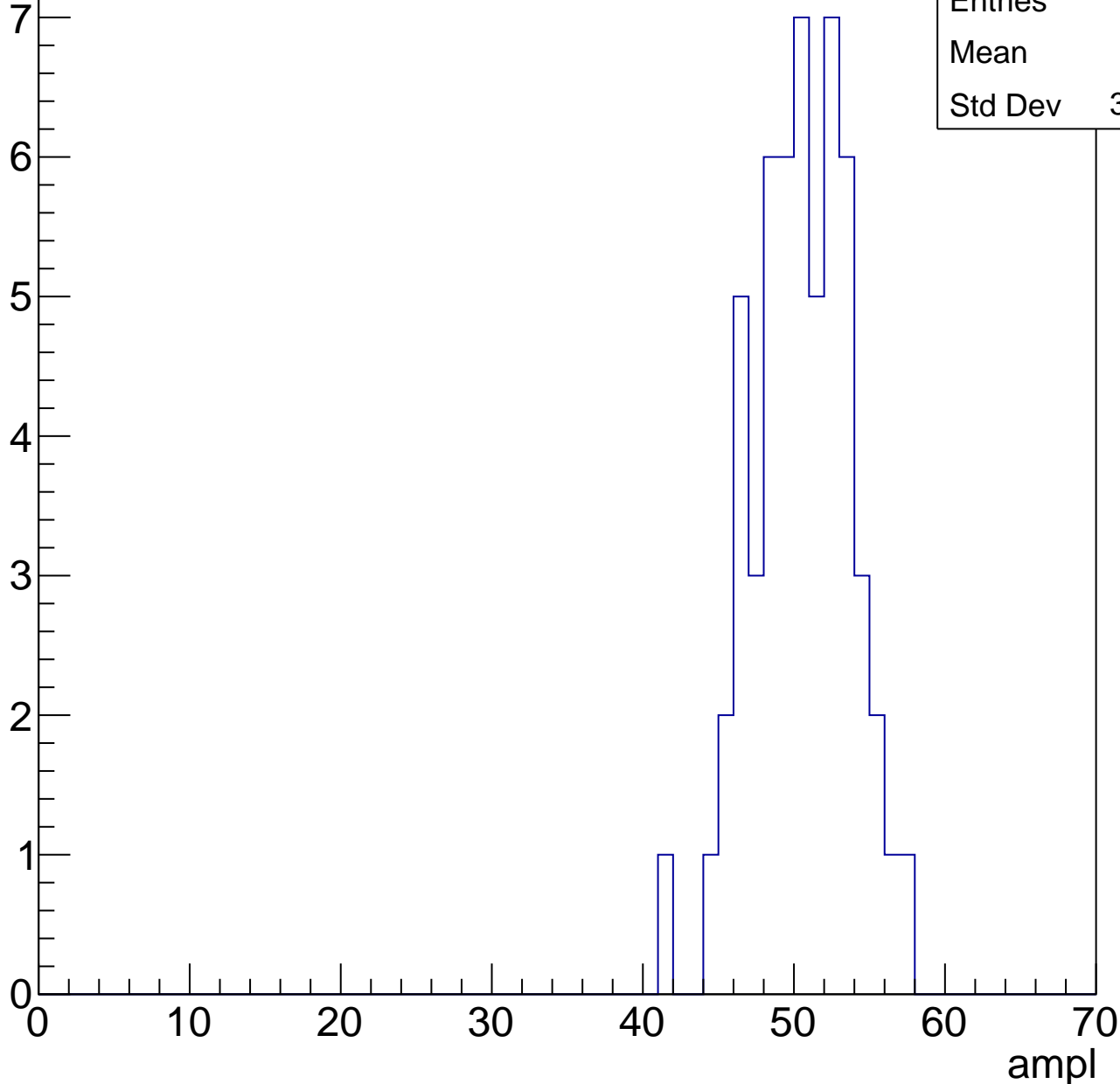


B1L102S, U14-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	50
Std Dev	3.202

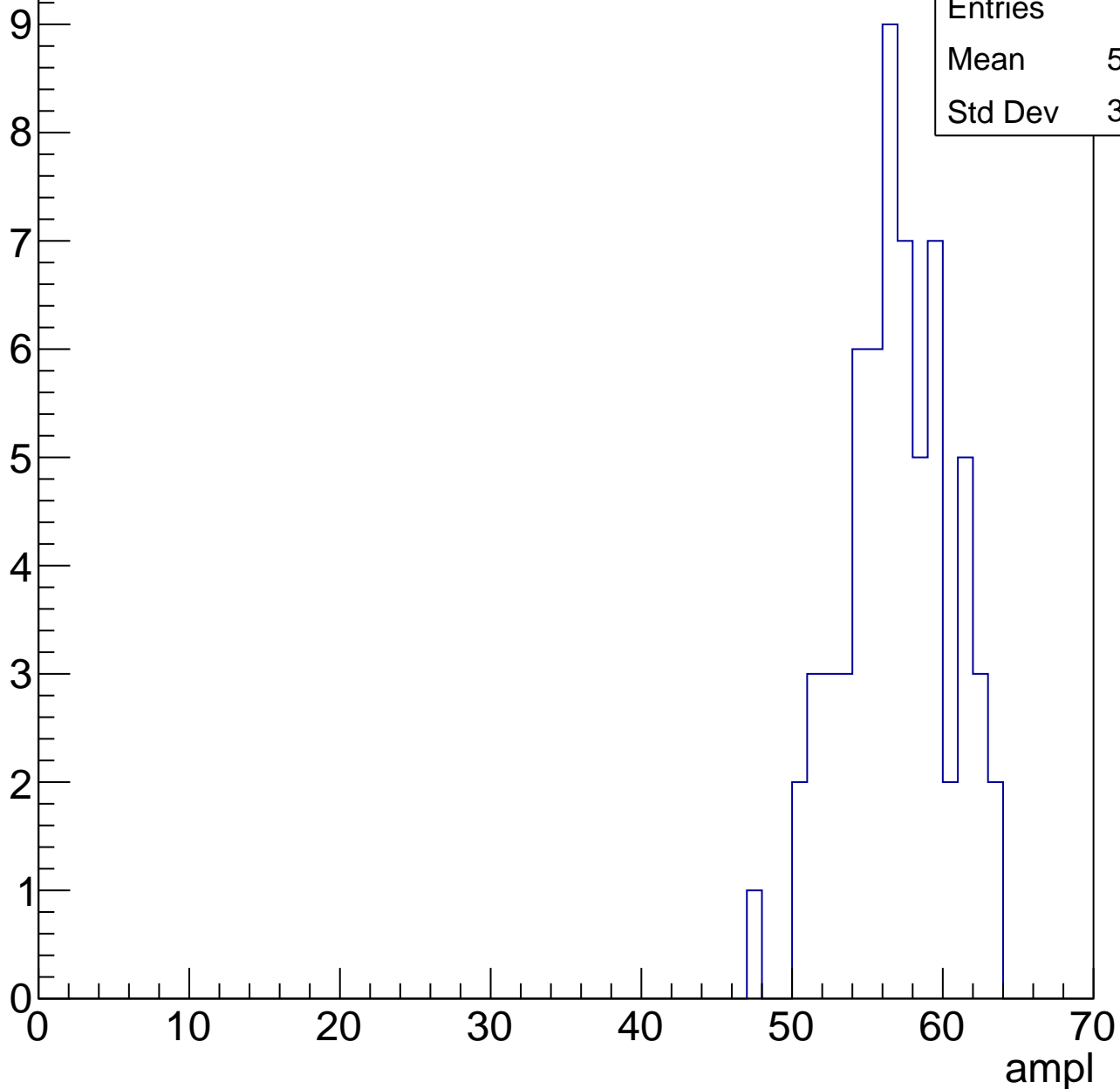


B1L102S, U14-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	56.44
Std Dev	3.477

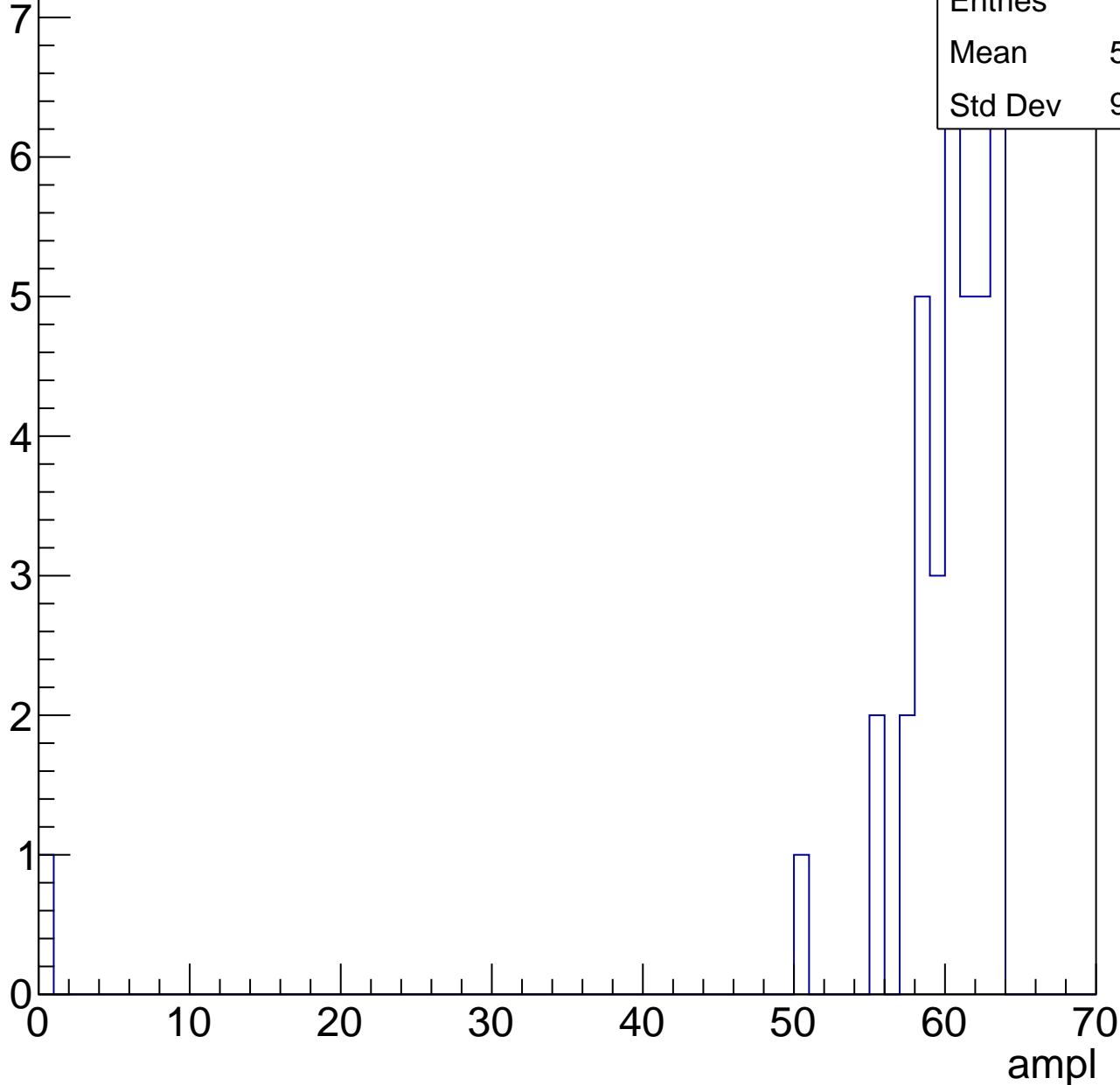


B1L102S, U14-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.34
Std Dev	9.966



B1L102S, U14-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

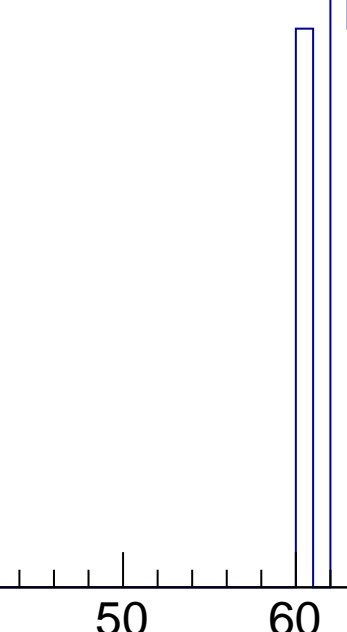
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl



B1L102S, U14-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch68, adc0

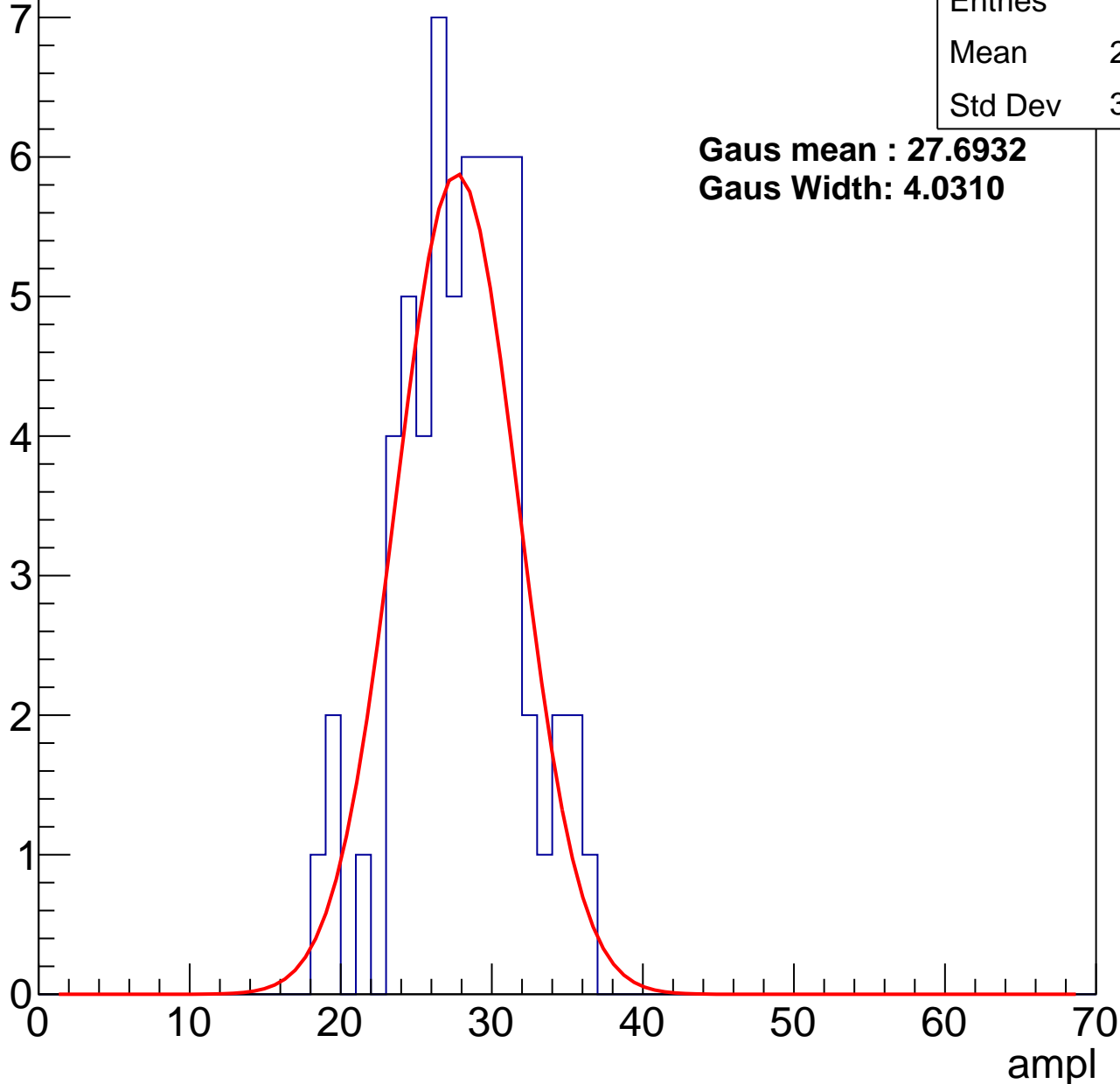
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.62
Std Dev	3.897

Gaus mean : 27.6932

Gaus Width: 4.0310



B1L102S, U14-ch68, adc1

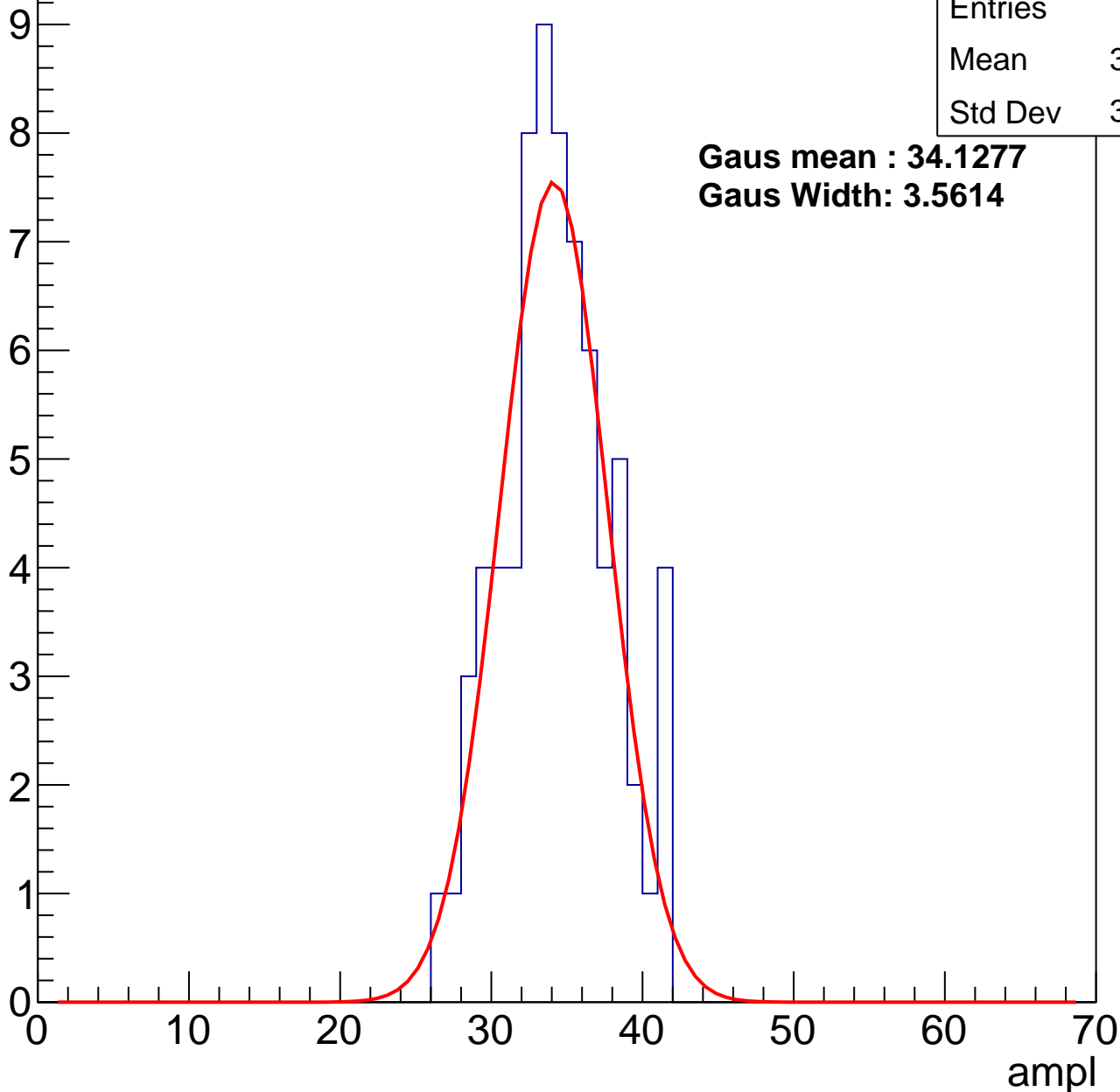
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	33.85
Std Dev	3.539

Gaus mean : 34.1277

Gaus Width: 3.5614



B1L102S, U14-ch68, adc2

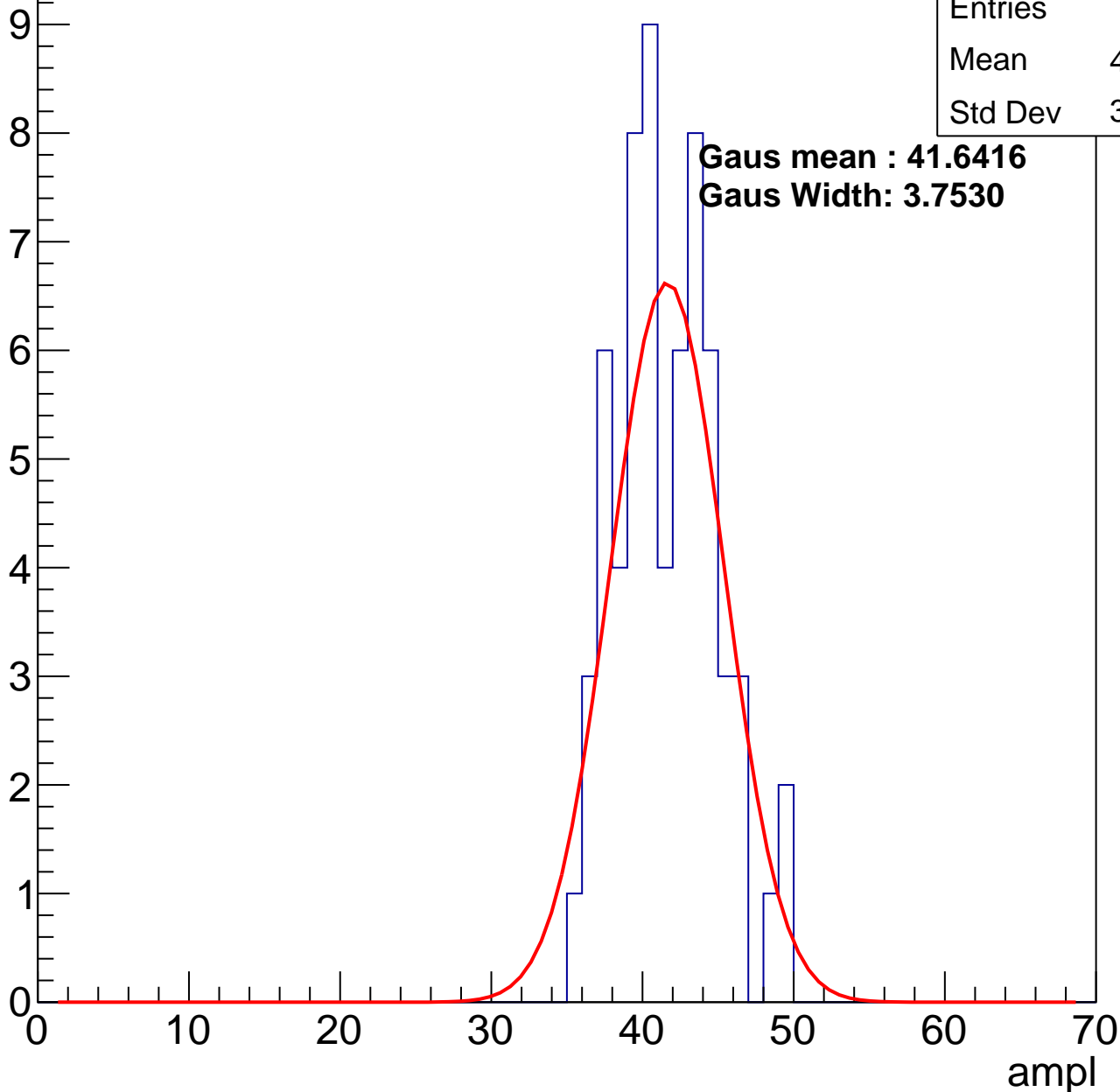
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.12
Std Dev	3.248

Gaus mean : 41.6416

Gaus Width: 3.7530

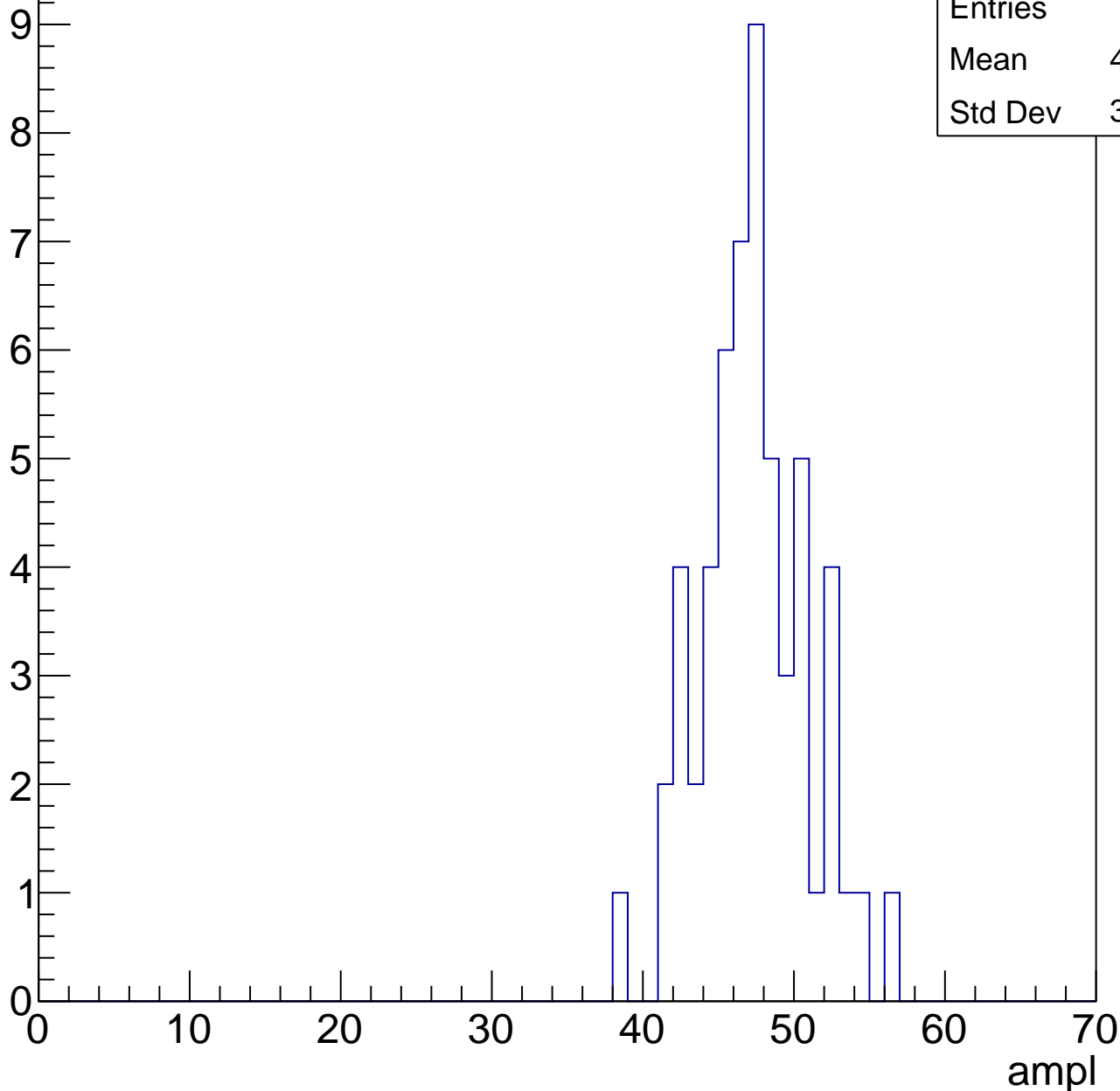


B1L102S, U14-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	46.86
Std Dev	3.523

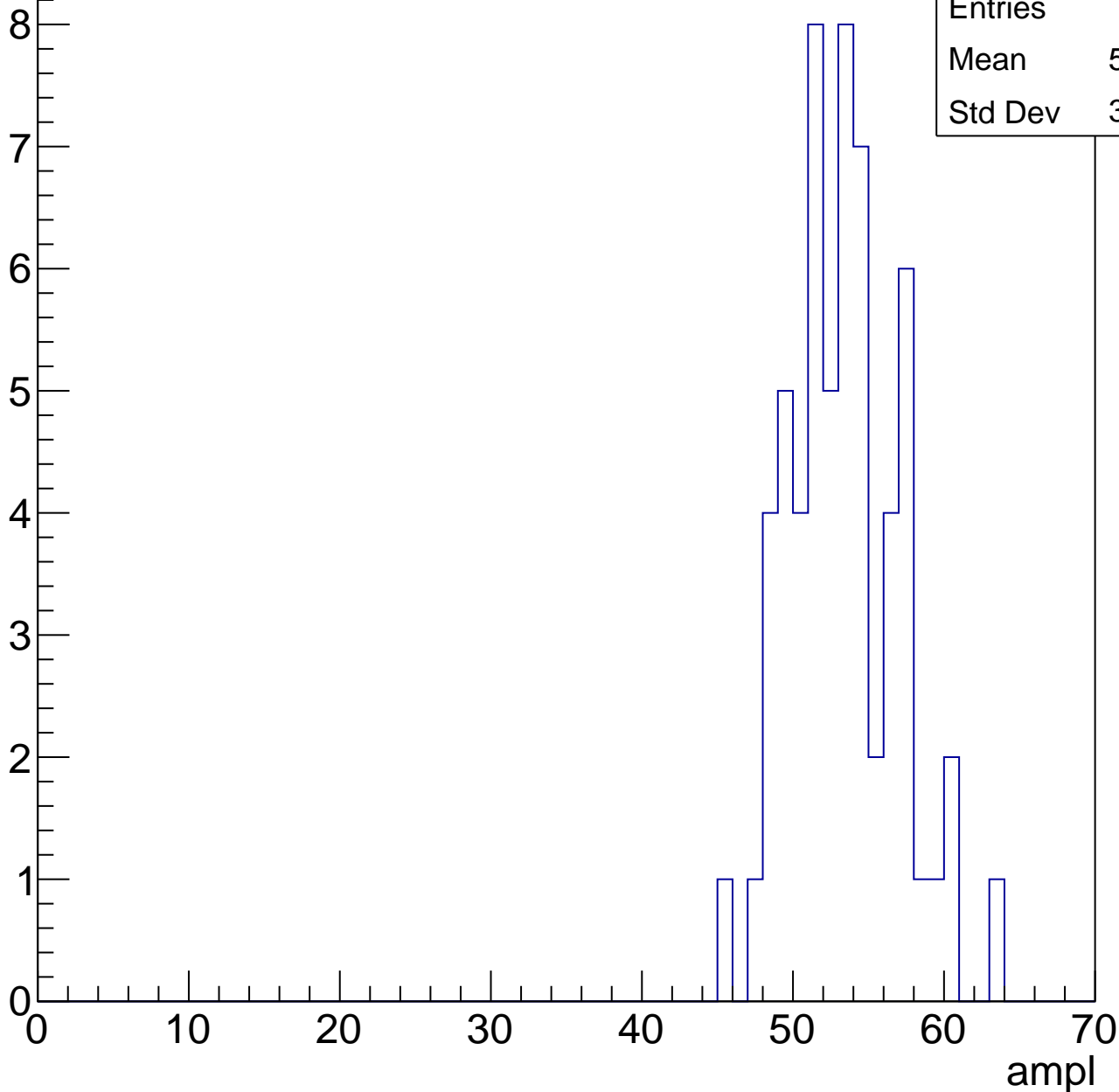


B1L102S, U14-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

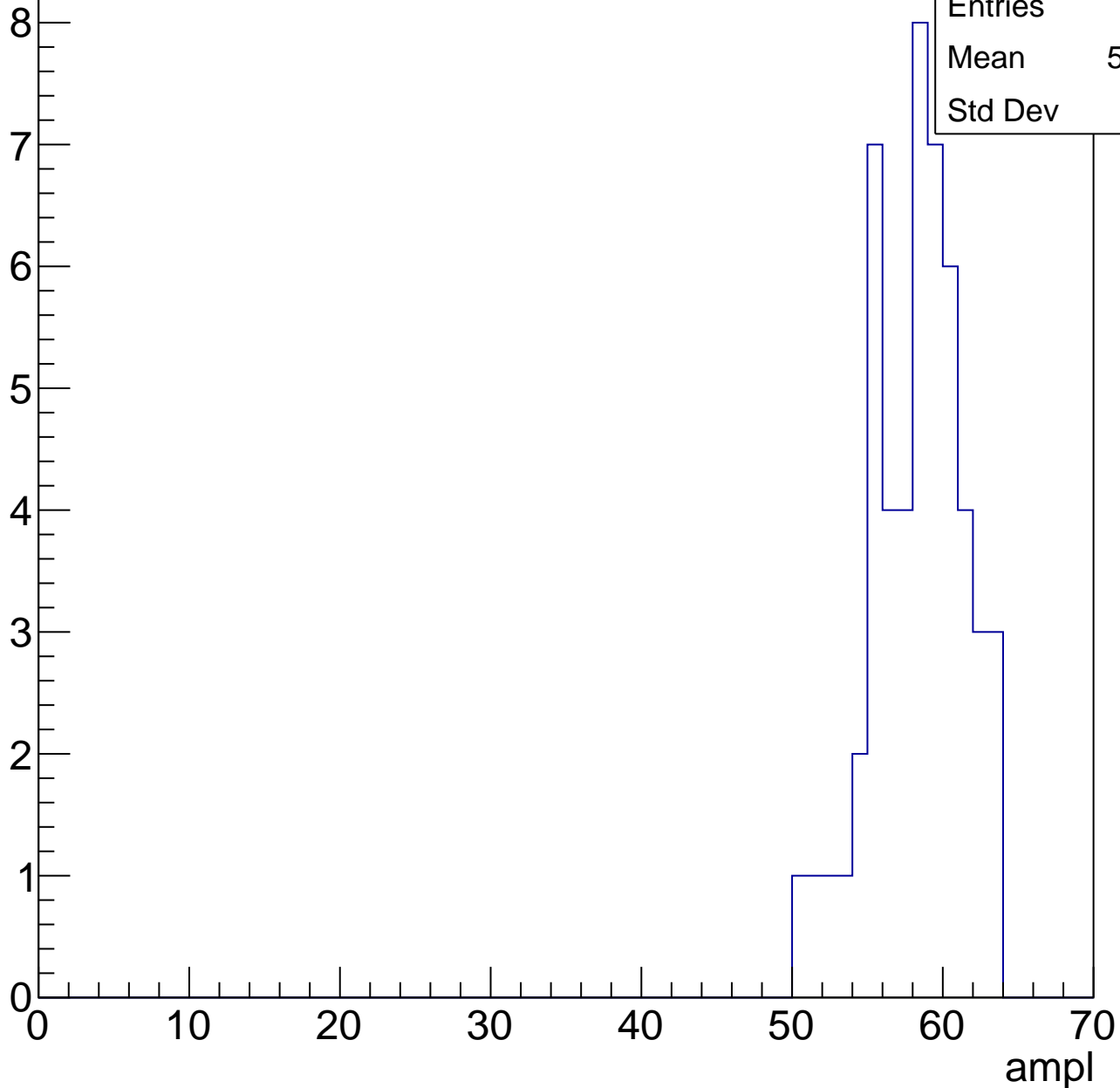
Entries	60
Mean	52.92
Std Dev	3.556



B1L102S, U14-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



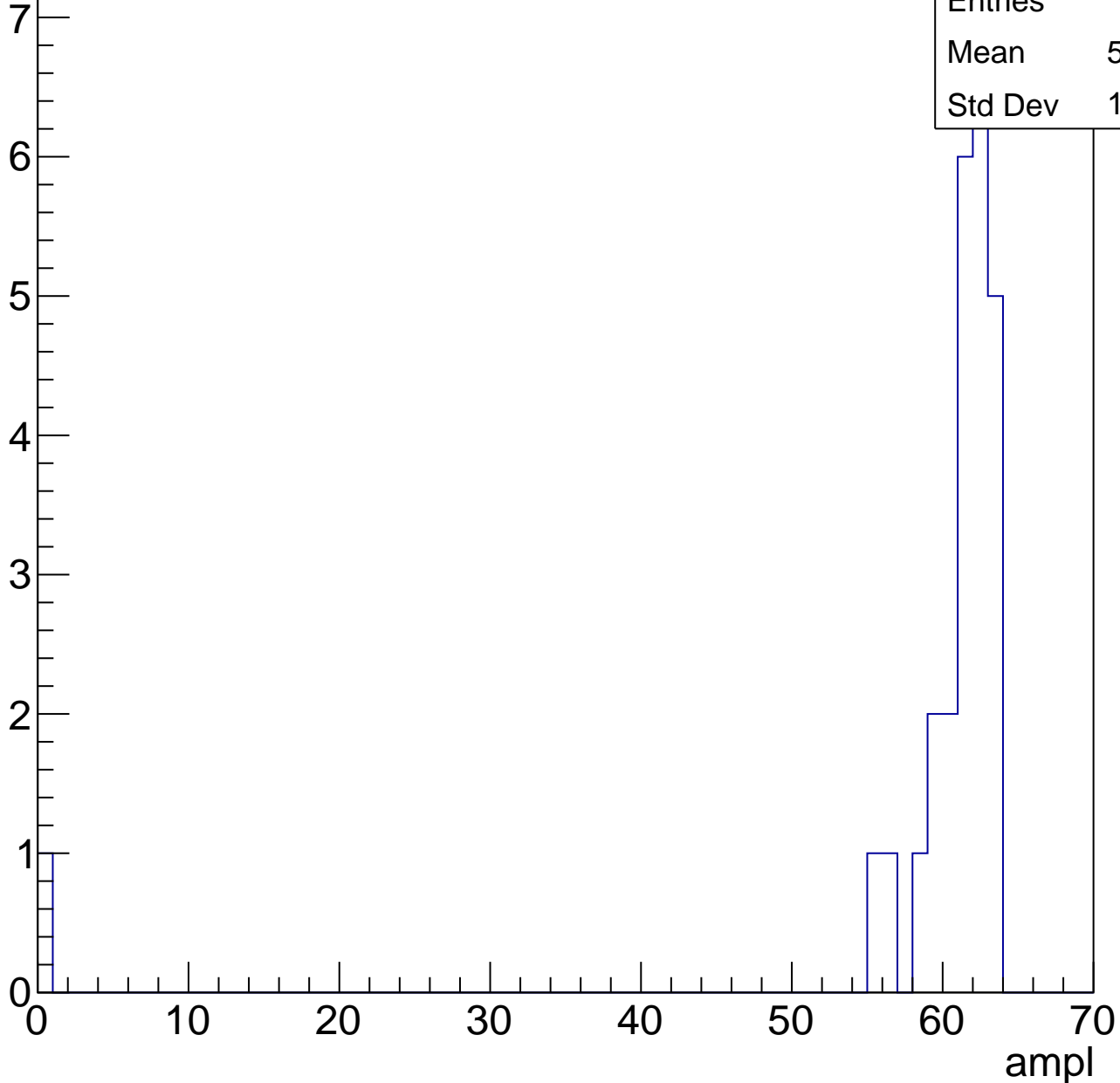
Entries	52
Mean	57.83
Std Dev	3.03

B1L102S, U14-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	26
Mean	58.54
Std Dev	11.88



B1L102S, U14-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch69, adc0

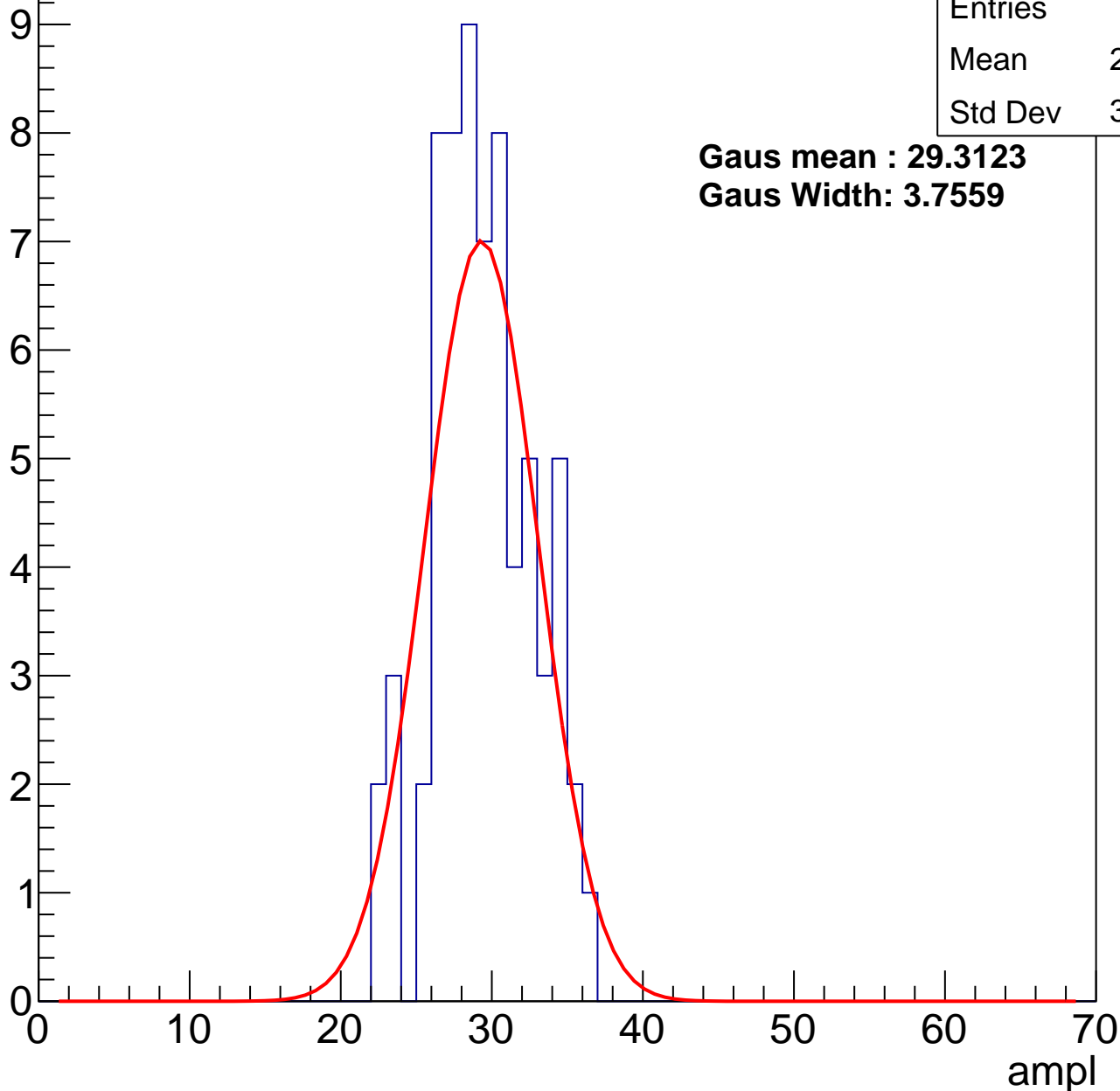
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.97
Std Dev	3.264

Gaus mean : 29.3123

Gaus Width: 3.7559



B1L102S, U14-ch69, adc1

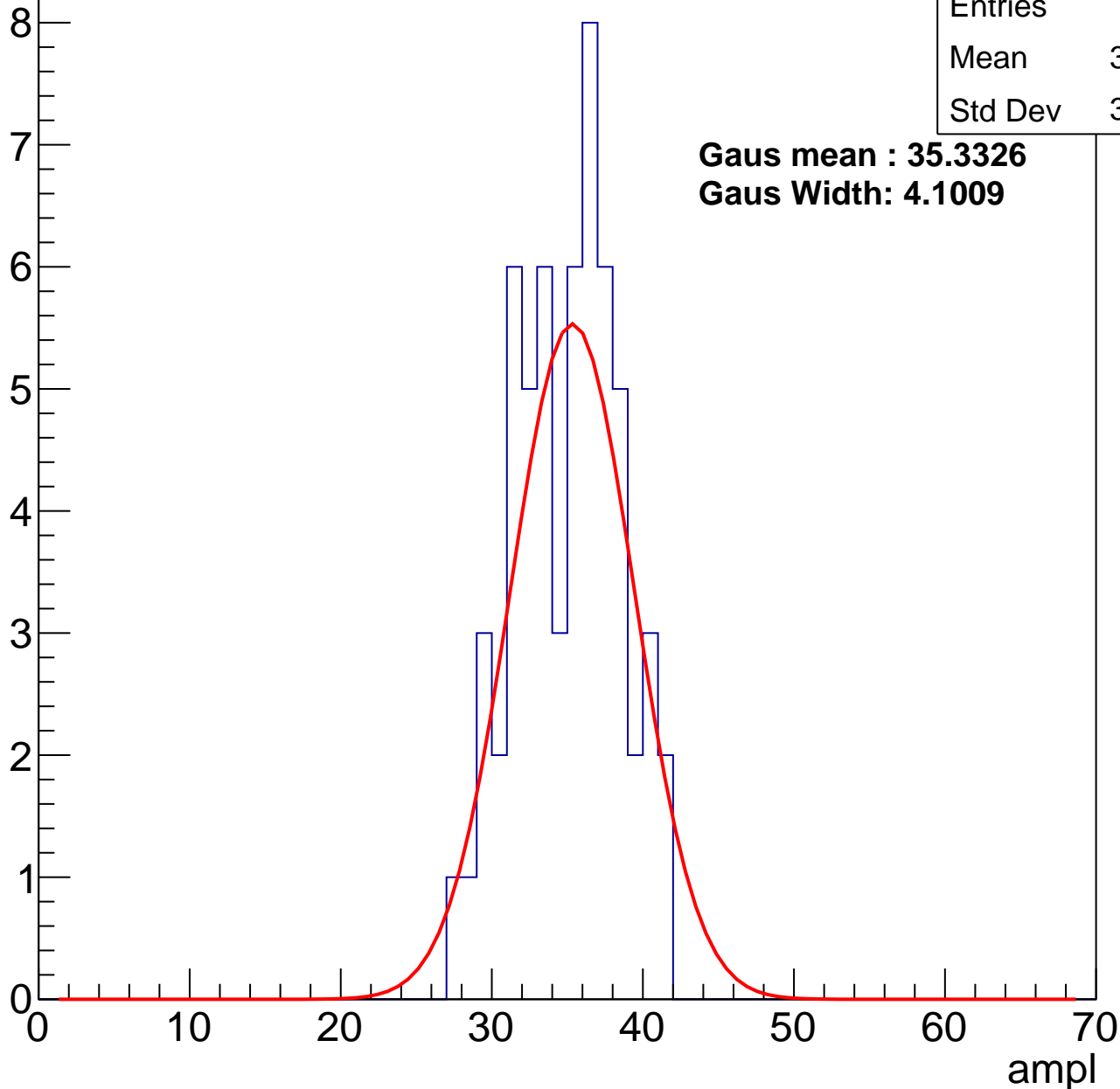
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	34.54
Std Dev	3.402

Gaus mean : 35.3326

Gaus Width: 4.1009



B1L102S, U14-ch69, adc2

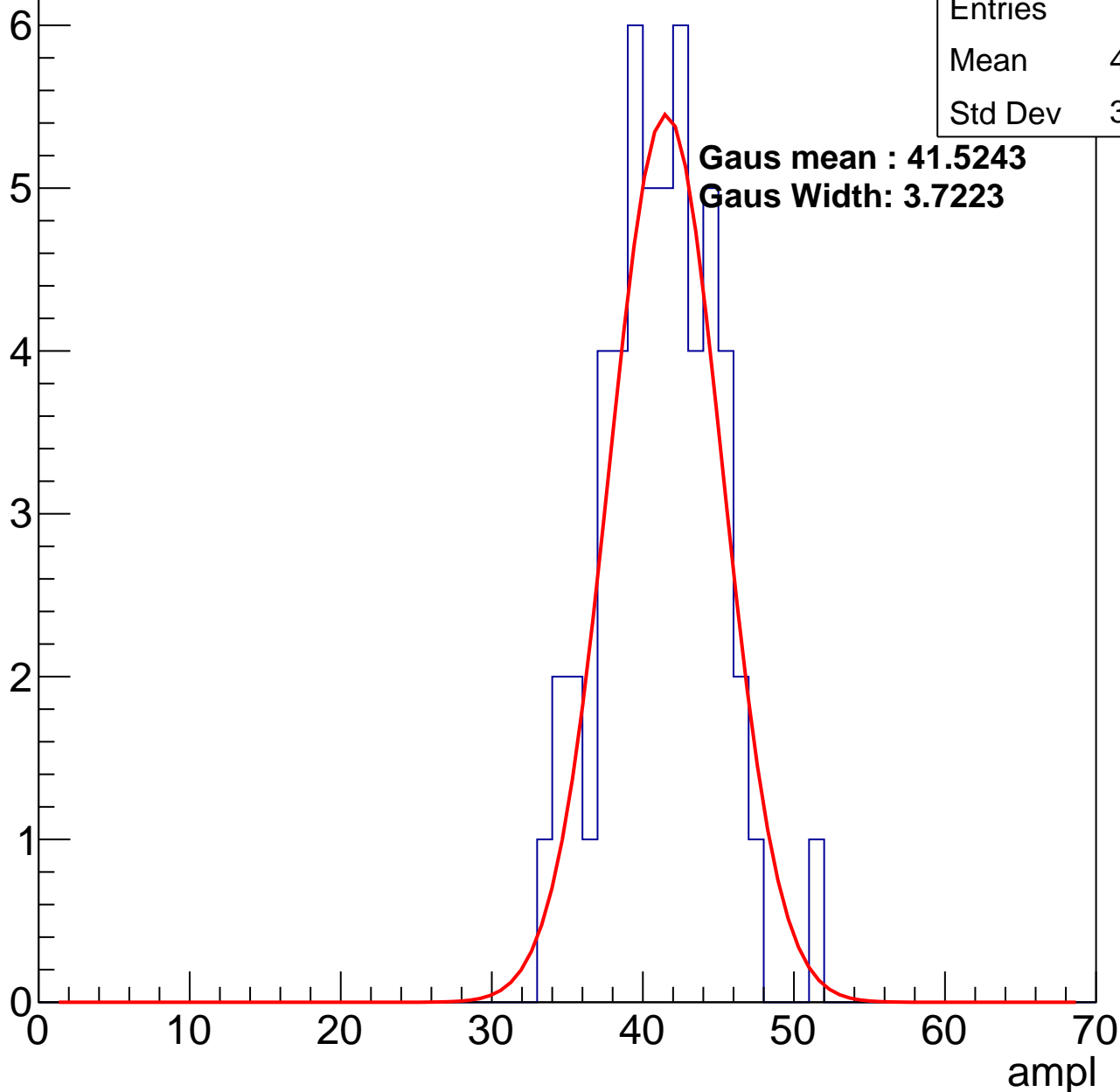
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	40.75
Std Dev	3.639

Gaus mean : 41.5243

Gaus Width: 3.7223



B1L102S, U14-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	46.45
Std Dev	3.645

Entry

10

8

6

4

2

0

0

10

20

30

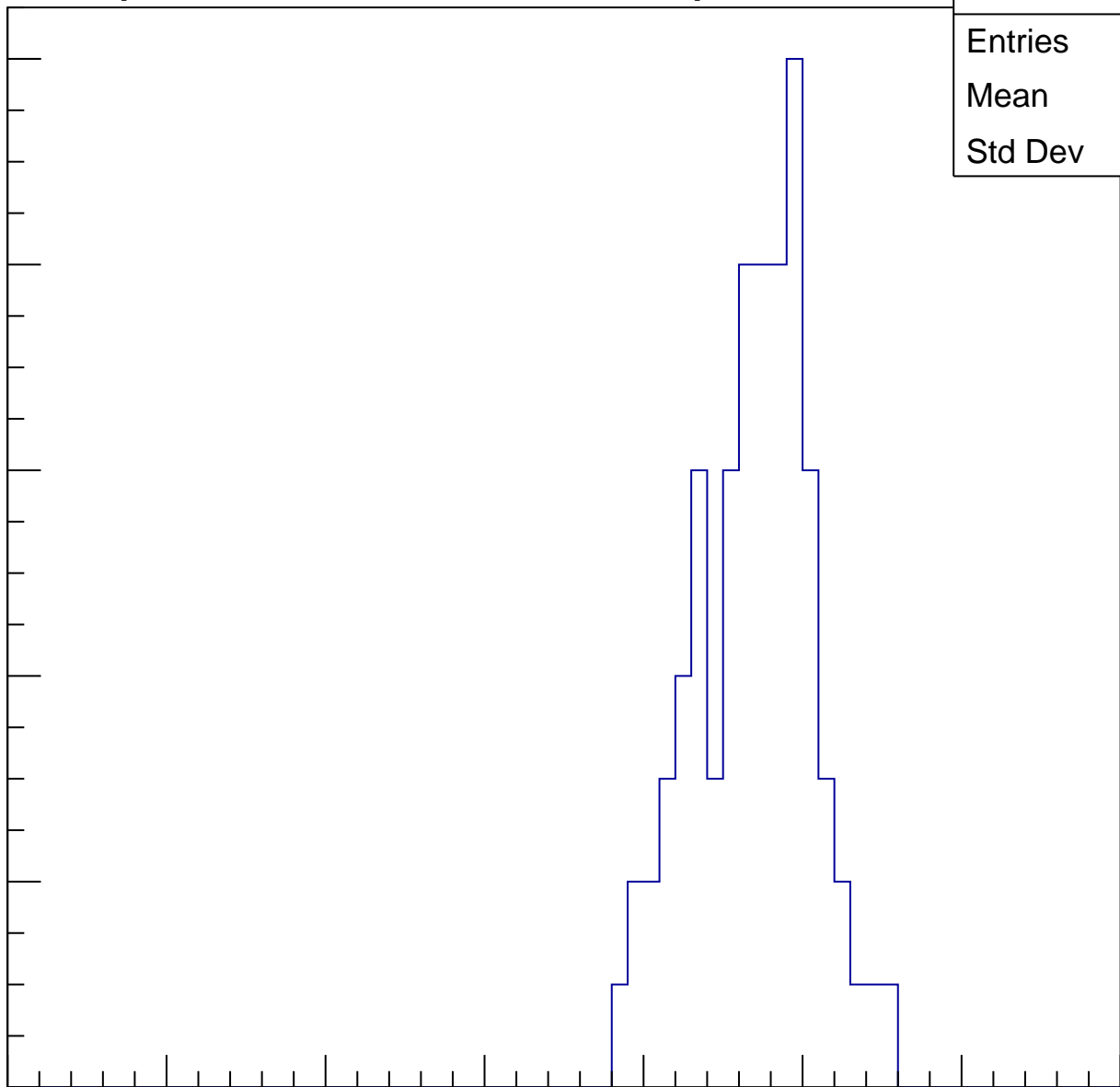
40

50

60

70

ampl

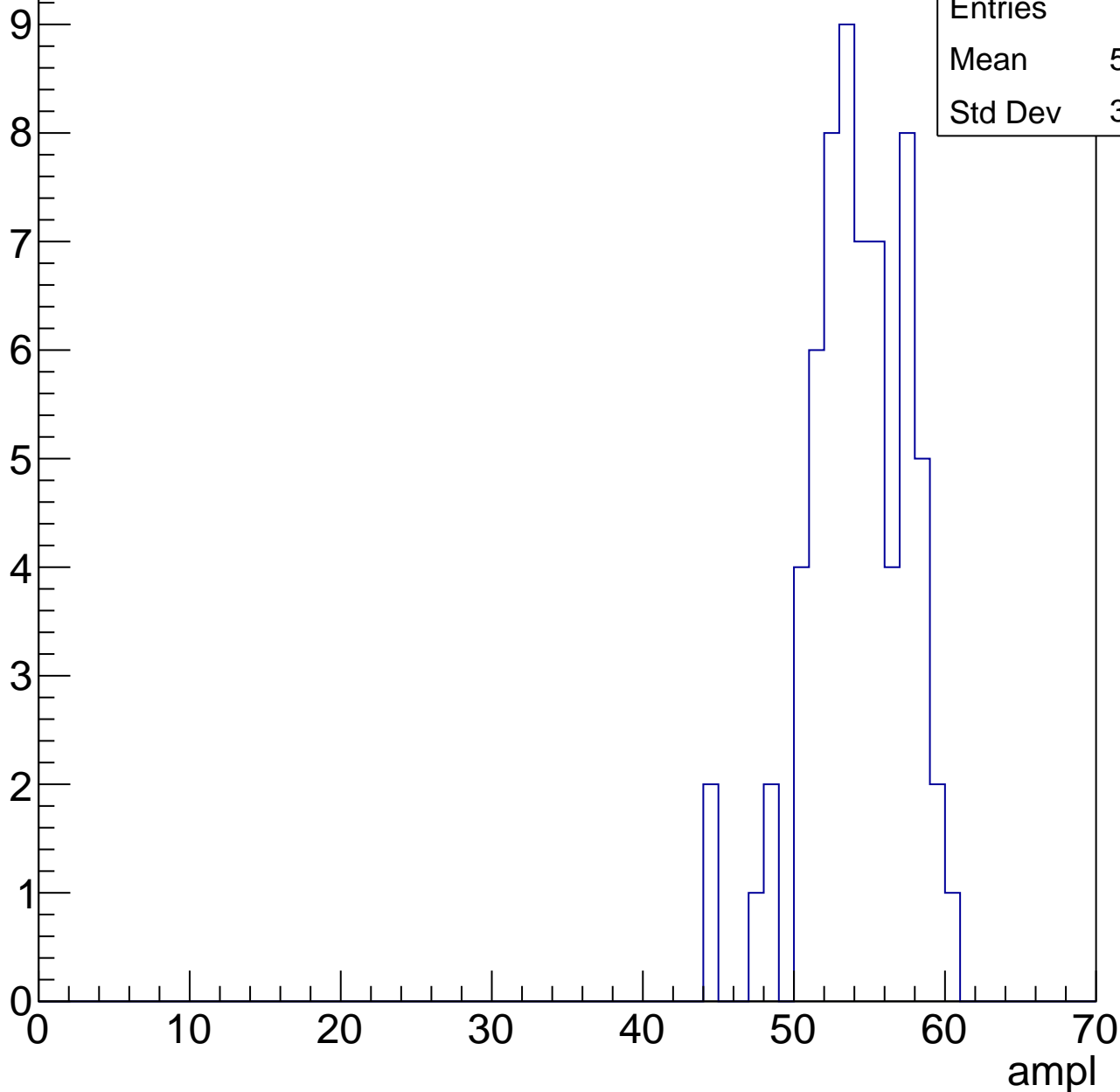


B1L102S, U14-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	53.65
Std Dev	3.333



B1L102S, U14-ch69, adc5

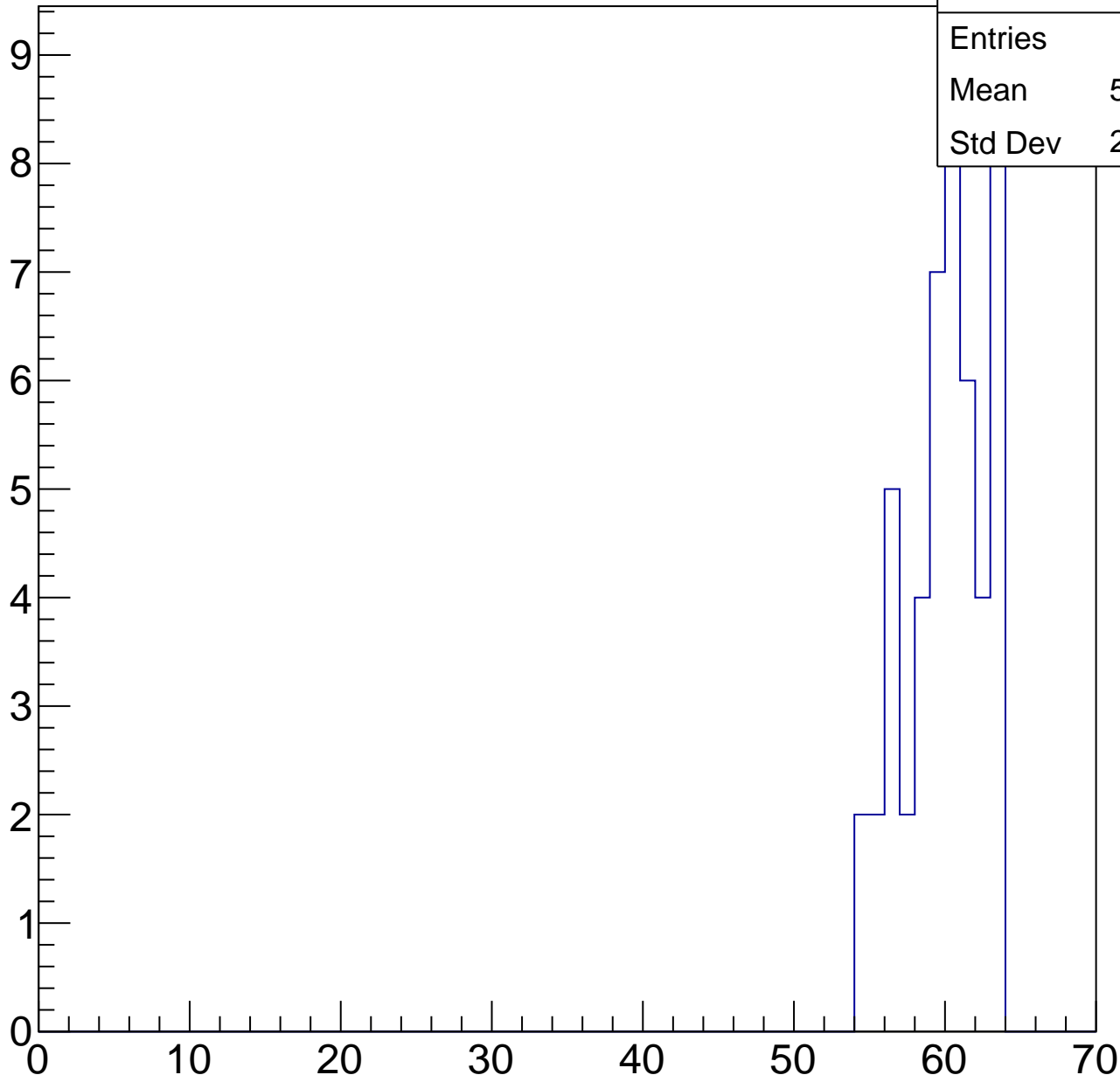
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.49
Std Dev	2.572

ampl

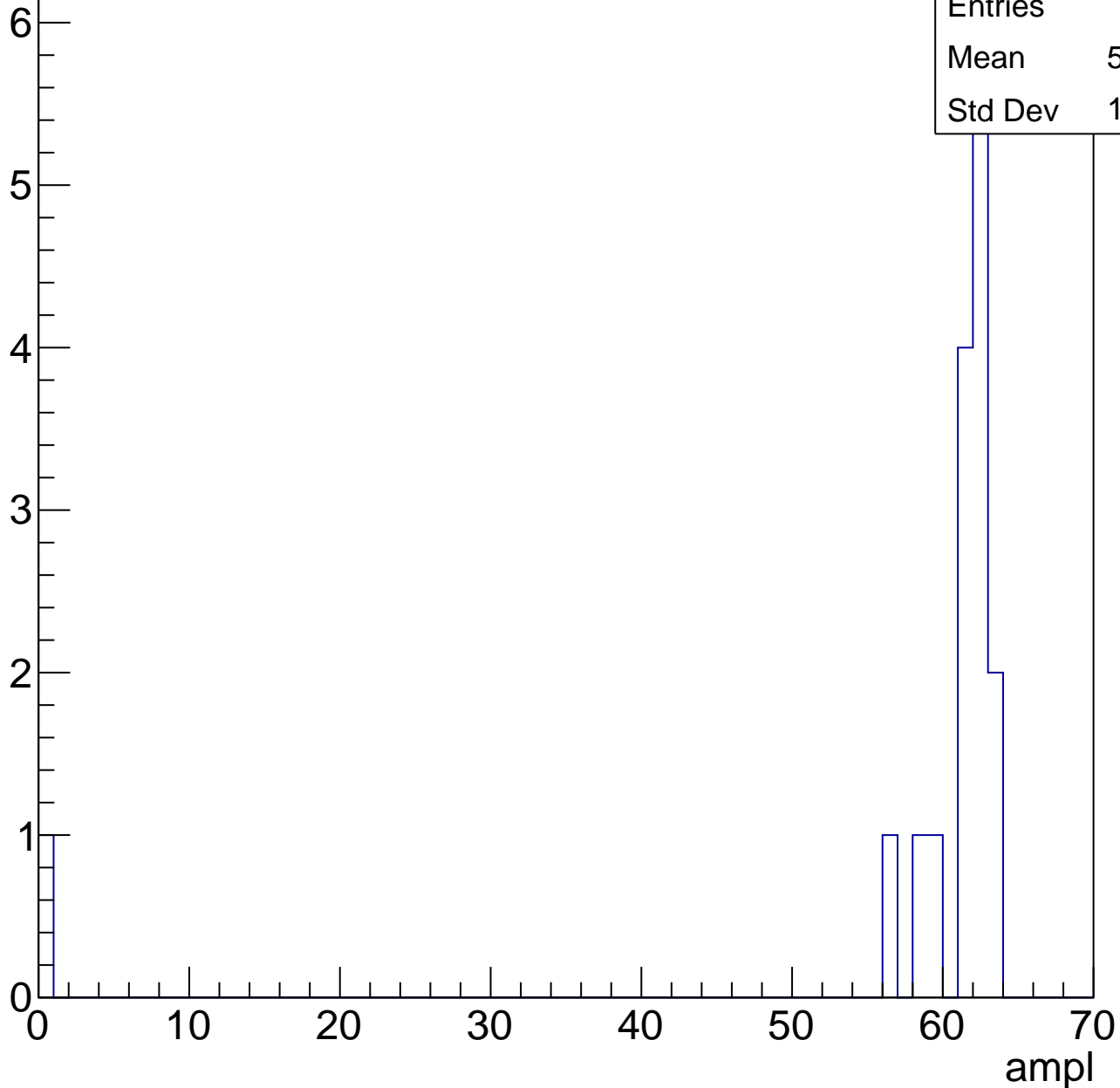


B1L102S, U14-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.19
Std Dev	14.88



B1L102S, U14-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch70, adc0

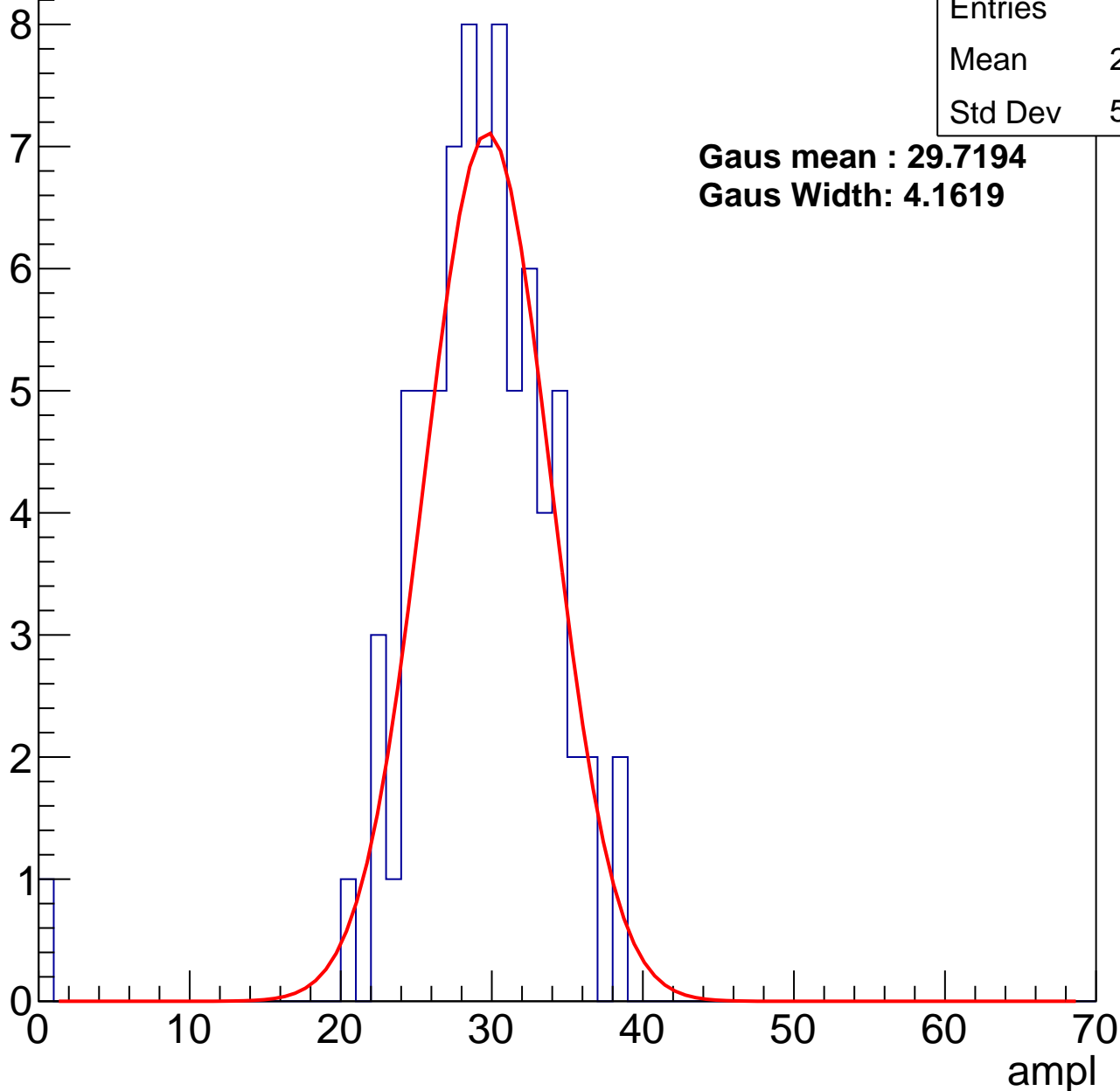
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.66
Std Dev	5.078

Gaus mean : 29.7194

Gaus Width: 4.1619



B1L102S, U14-ch70, adc1

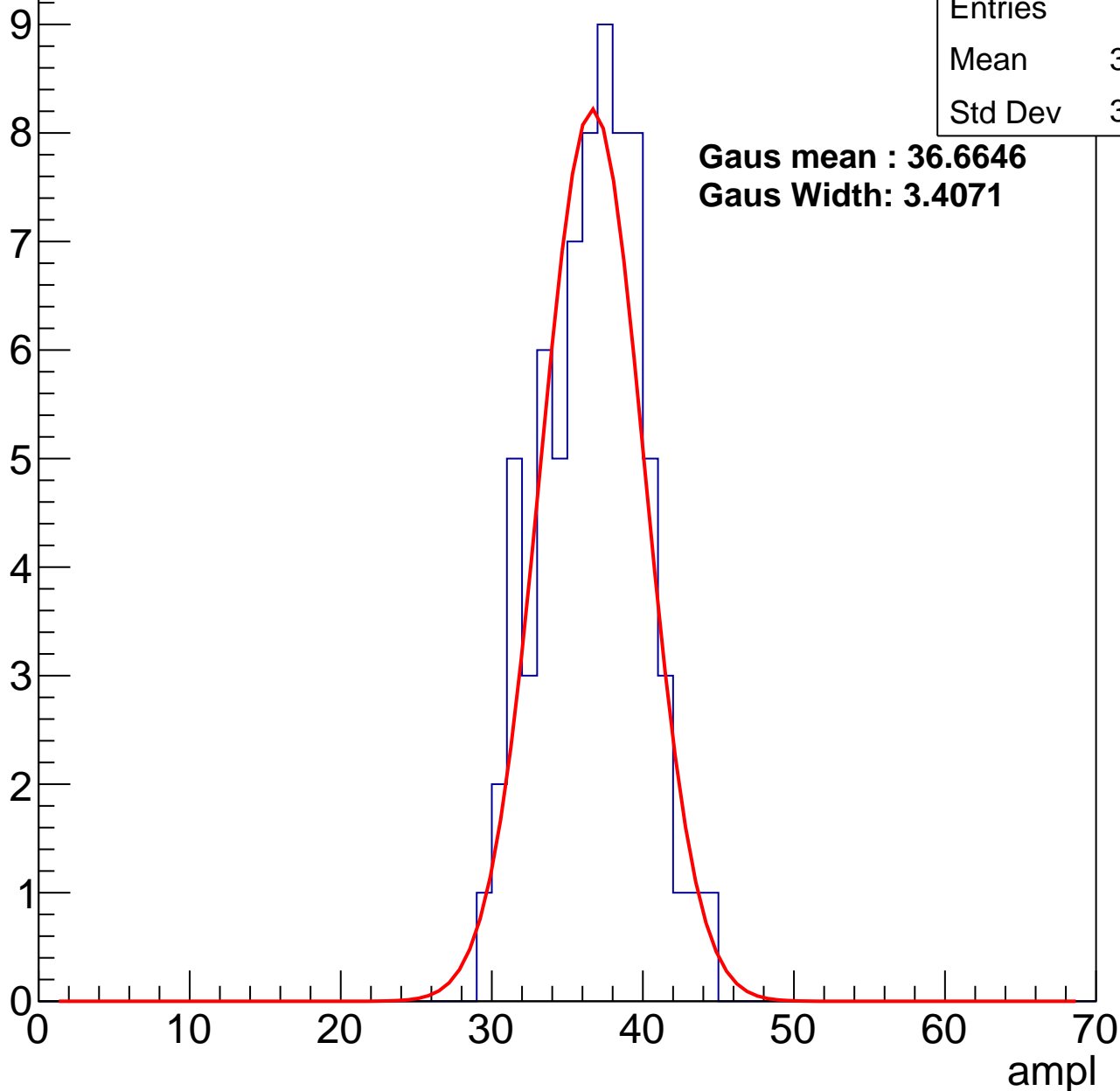
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.19
Std Dev	3.288

Gaus mean : 36.6646

Gaus Width: 3.4071



B1L102S, U14-ch70, adc2

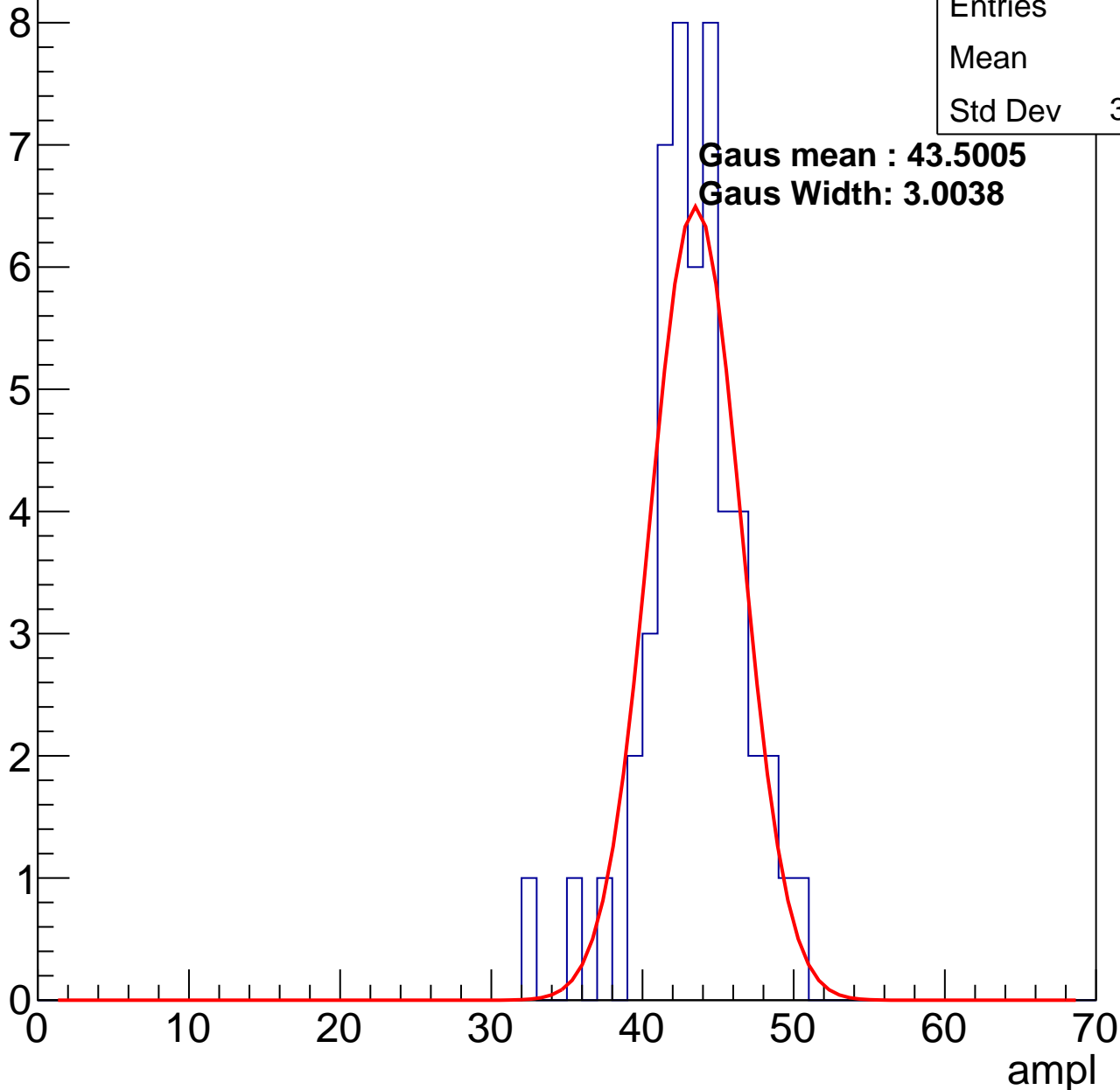
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.9
Std Dev	3.262

Gaus mean : 43.5005

Gaus Width: 3.0038

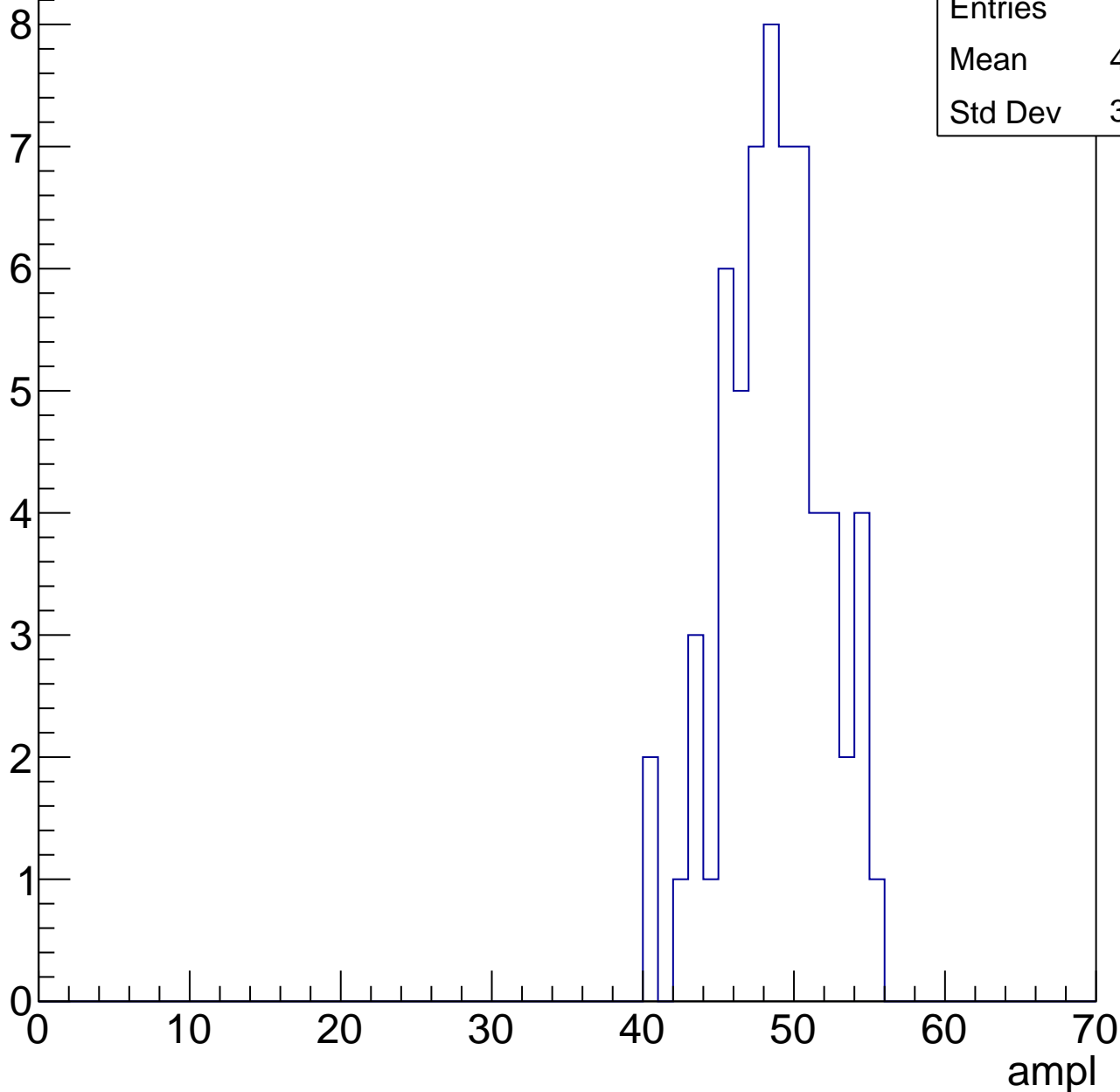


B1L102S, U14-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

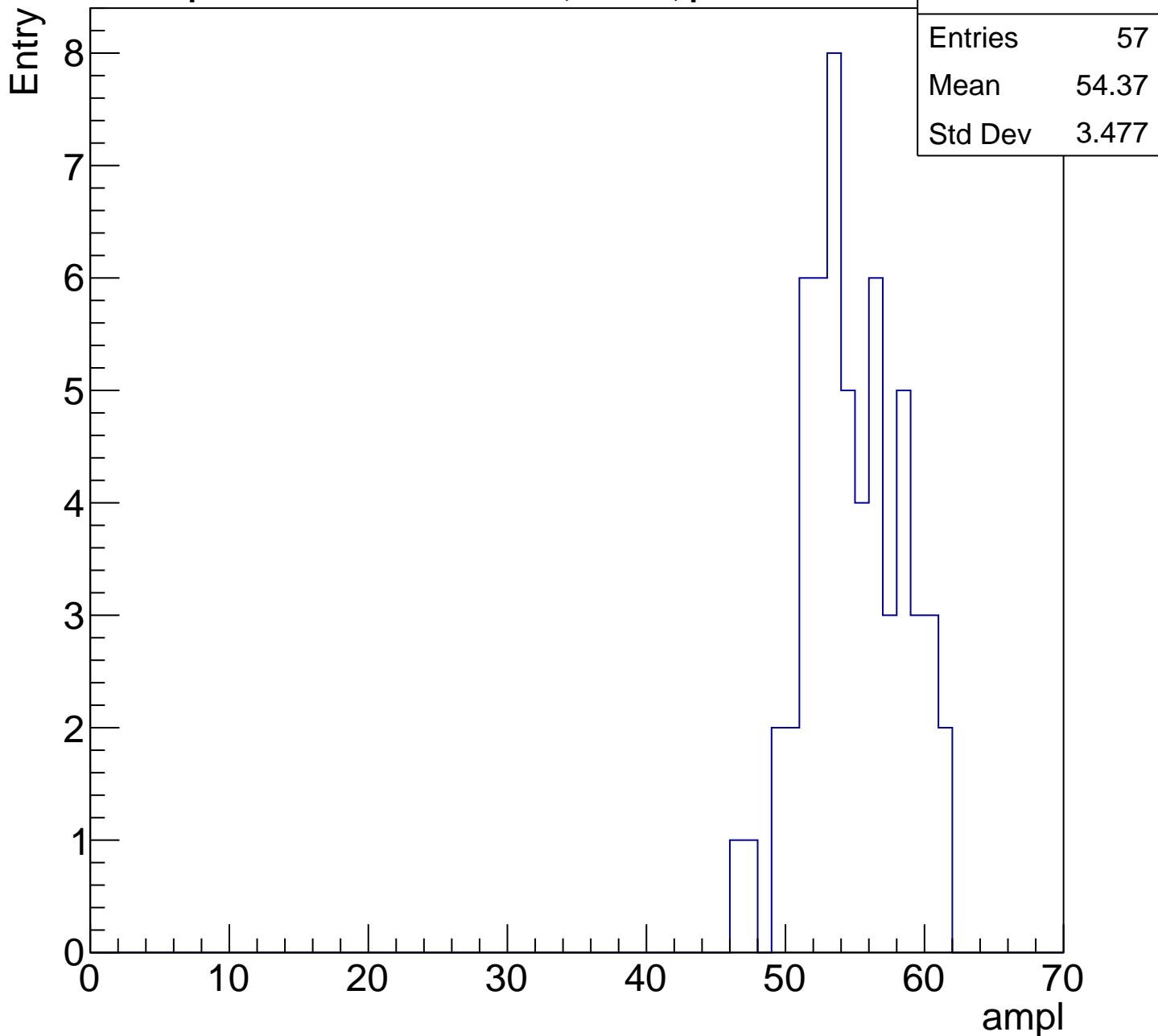
Entry

Entries	62
Mean	48.23
Std Dev	3.386



B1L102S, U14-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

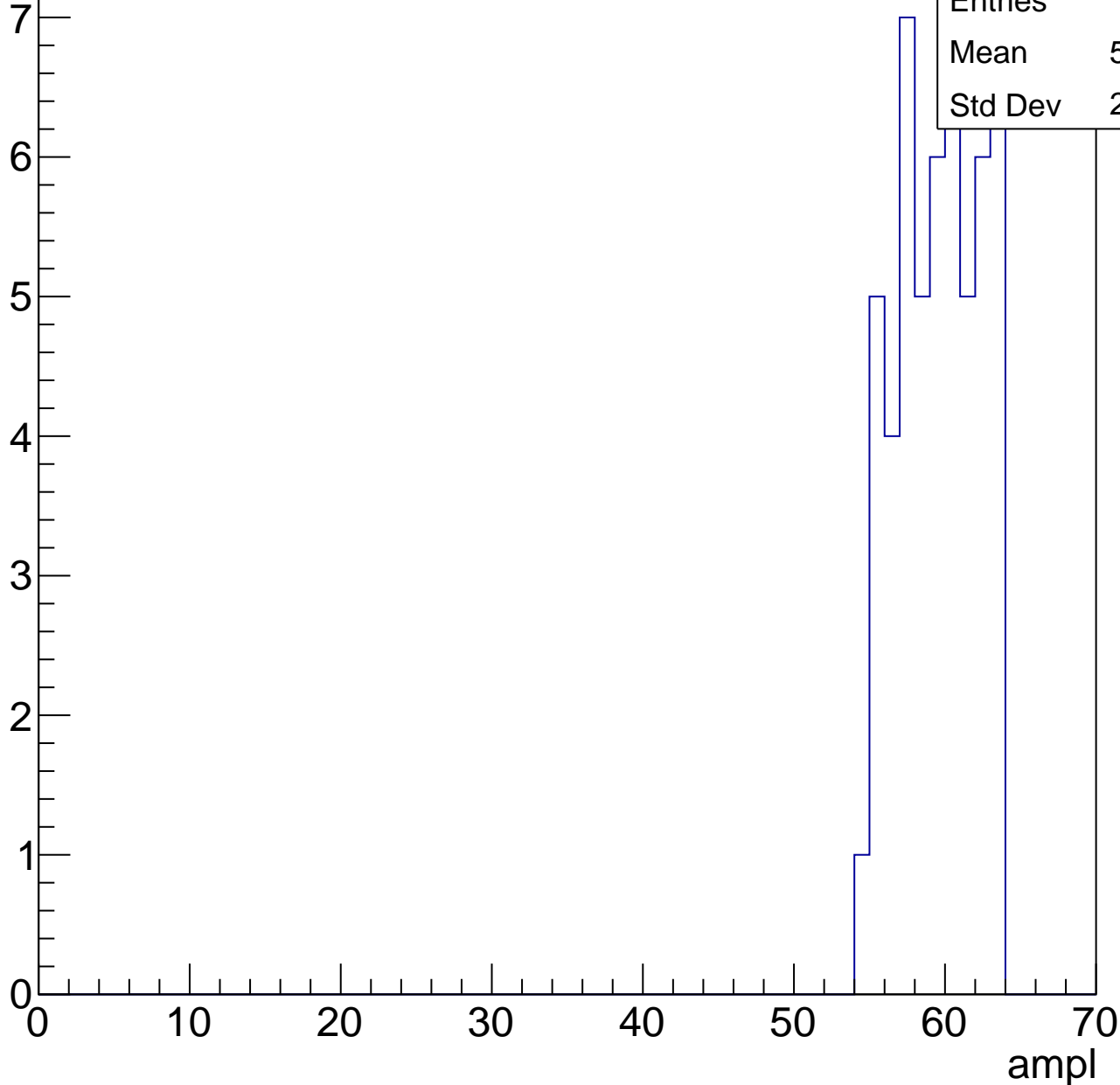


B1L102S, U14-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

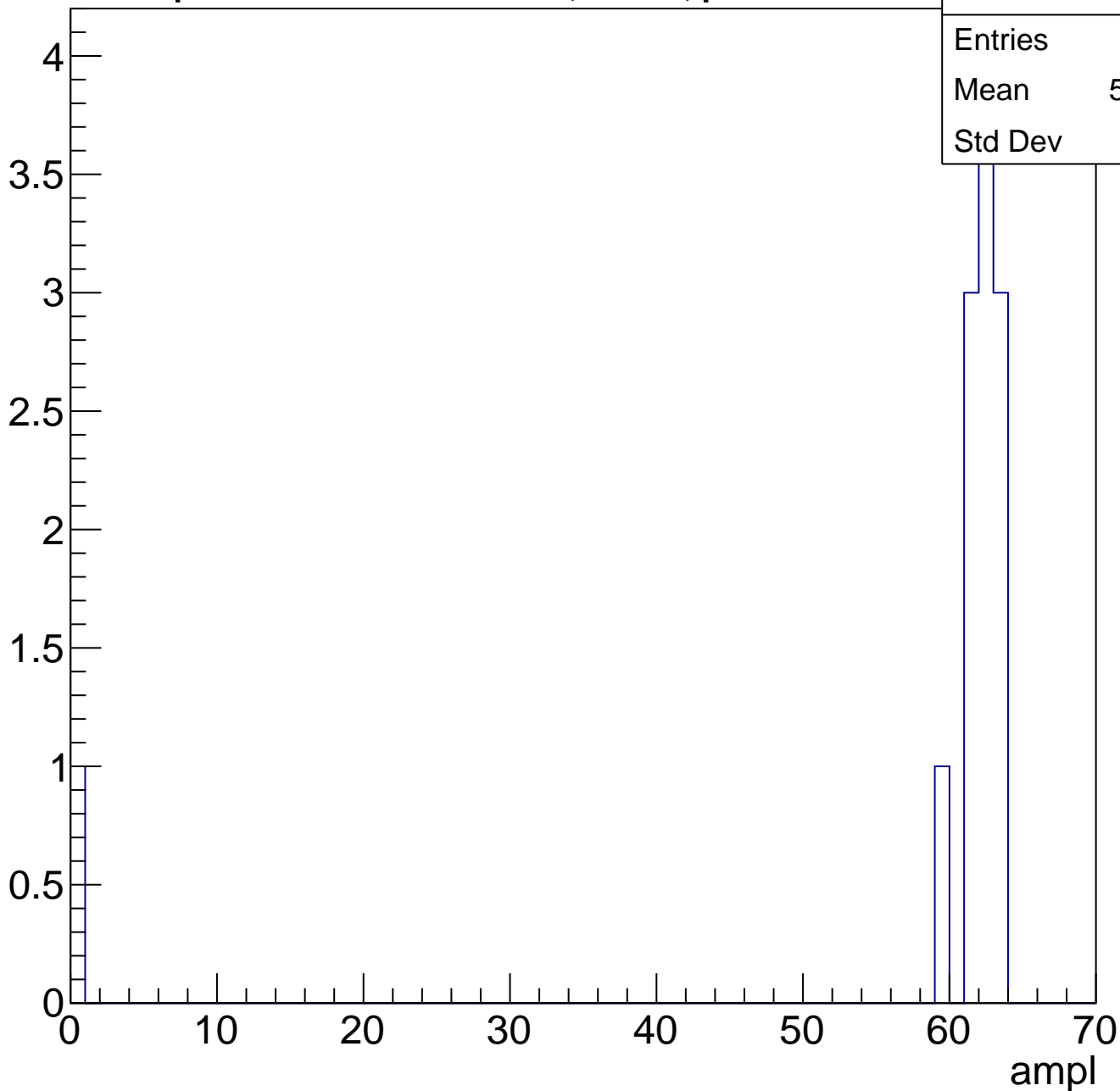
Entries	53
Mean	59.13
Std Dev	2.628



B1L102S, U14-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

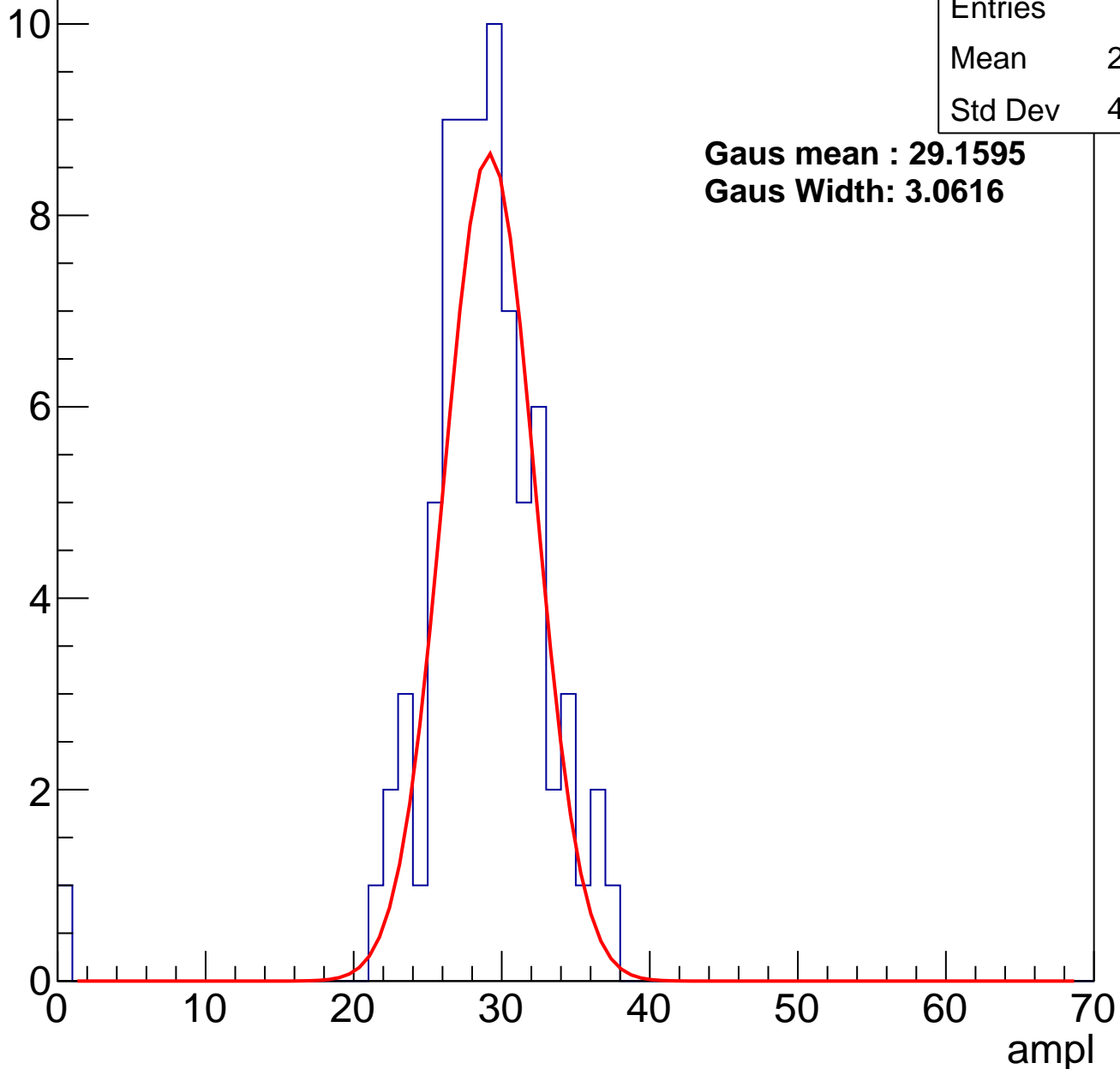
B1L102S, U14-ch71, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	28.19
Std Dev	4.668

Gaus mean : 29.1595
Gaus Width: 3.0616

Entry



B1L102S, U14-ch71, adc1

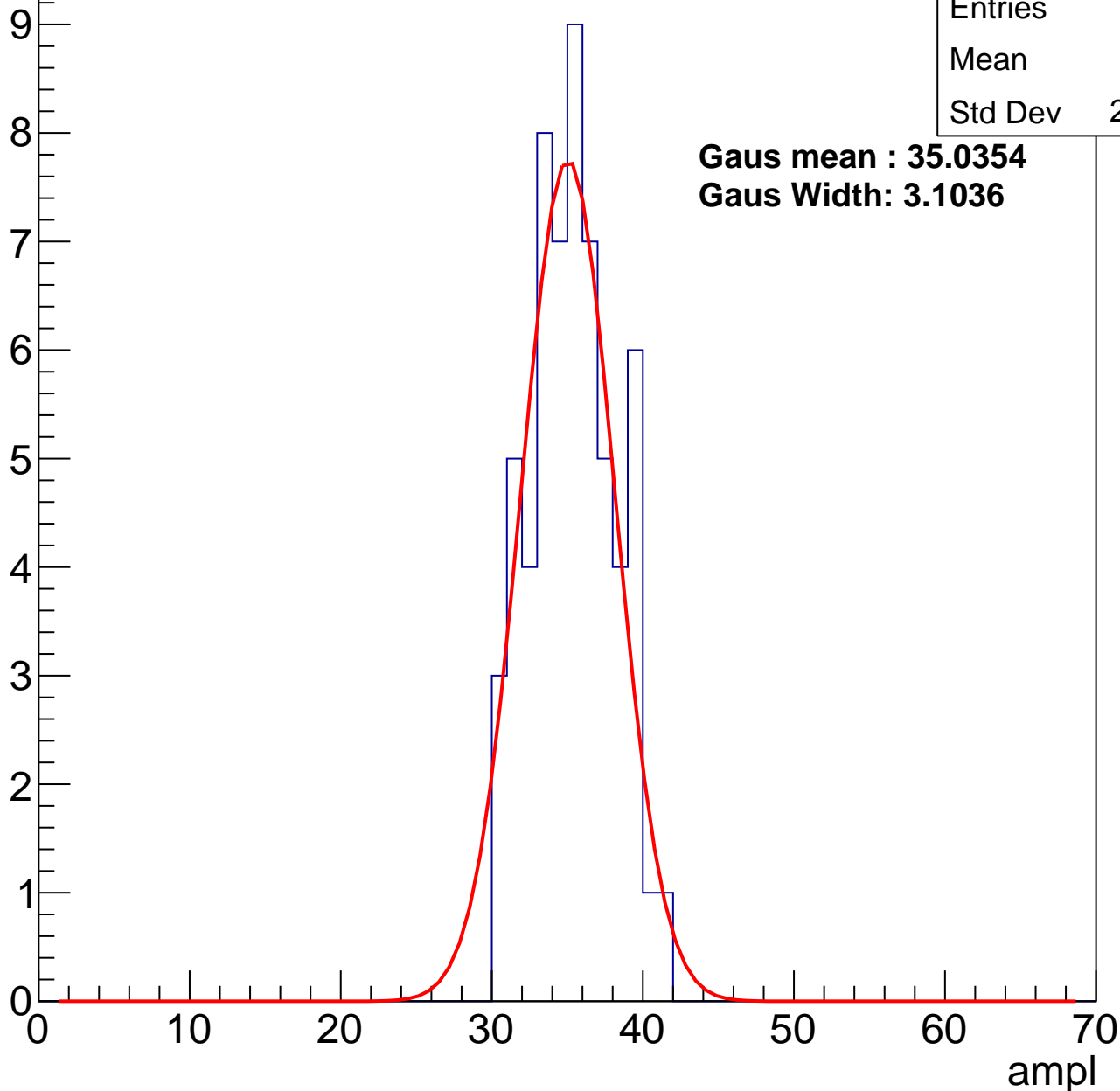
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.9
Std Dev	2.737

Gaus mean : 35.0354

Gaus Width: 3.1036



B1L102S, U14-ch71, adc2

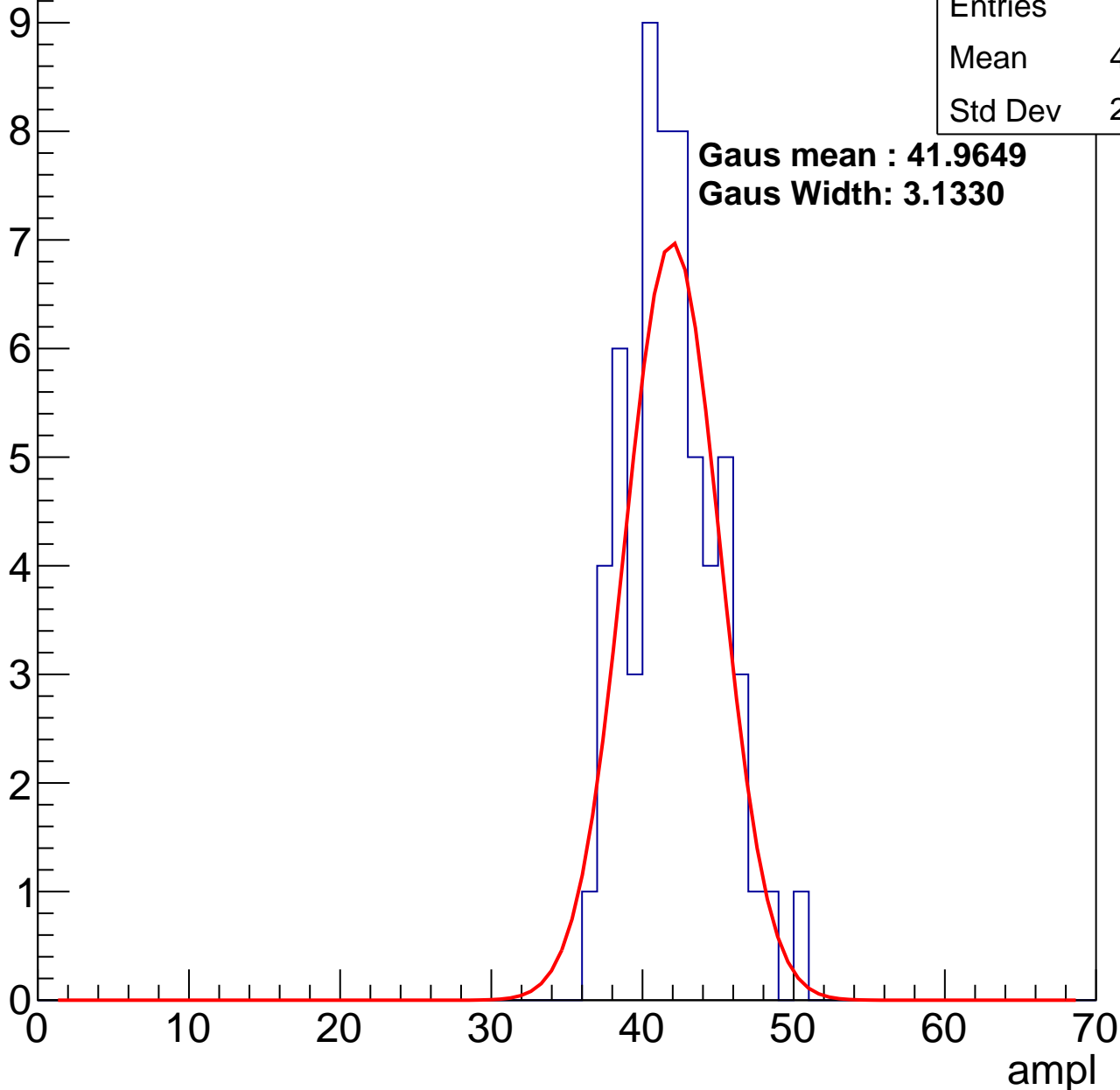
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.56
Std Dev	2.999

Gaus mean : 41.9649

Gaus Width: 3.1330

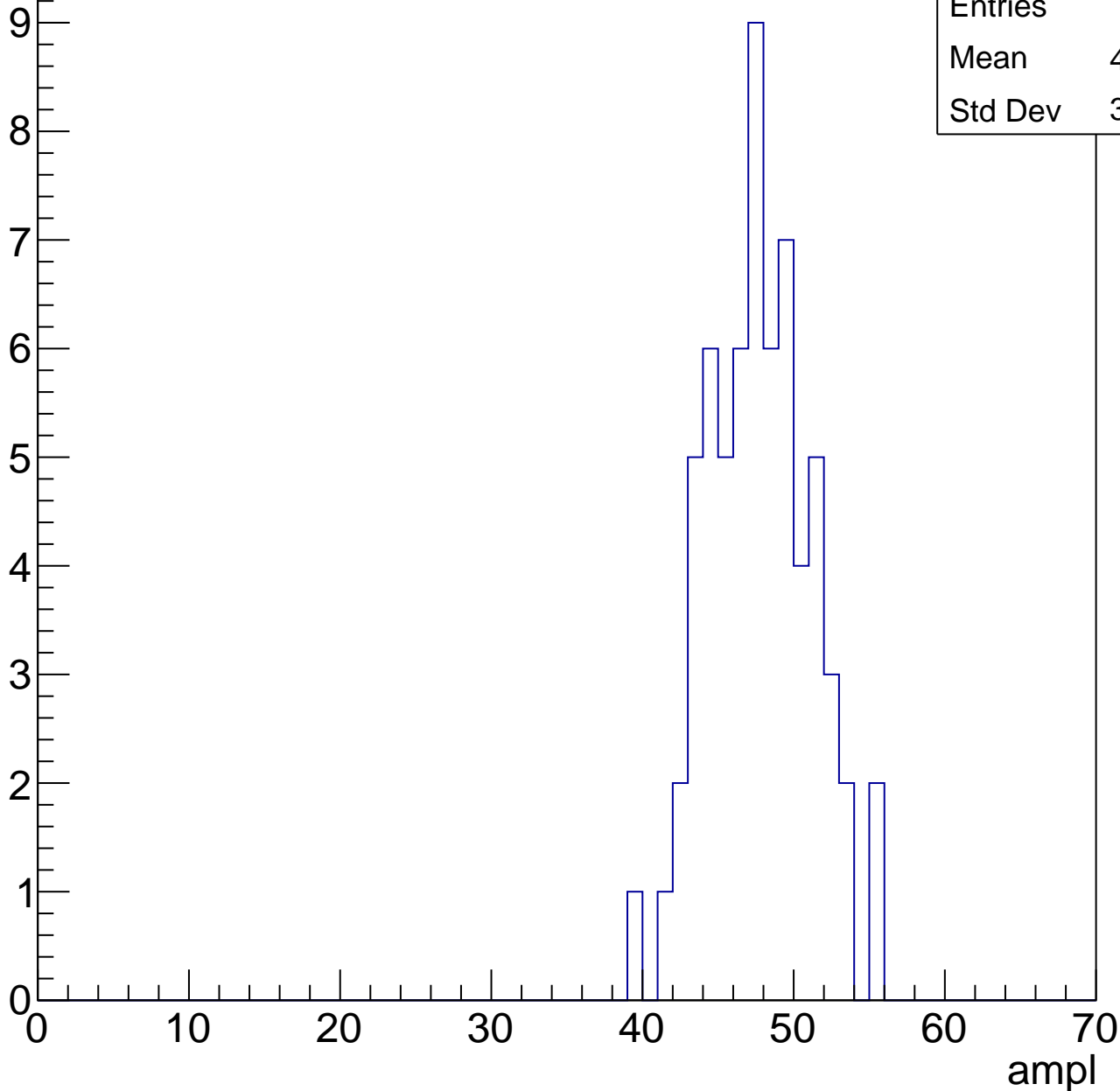


B1L102S, U14-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.27
Std Dev	3.378

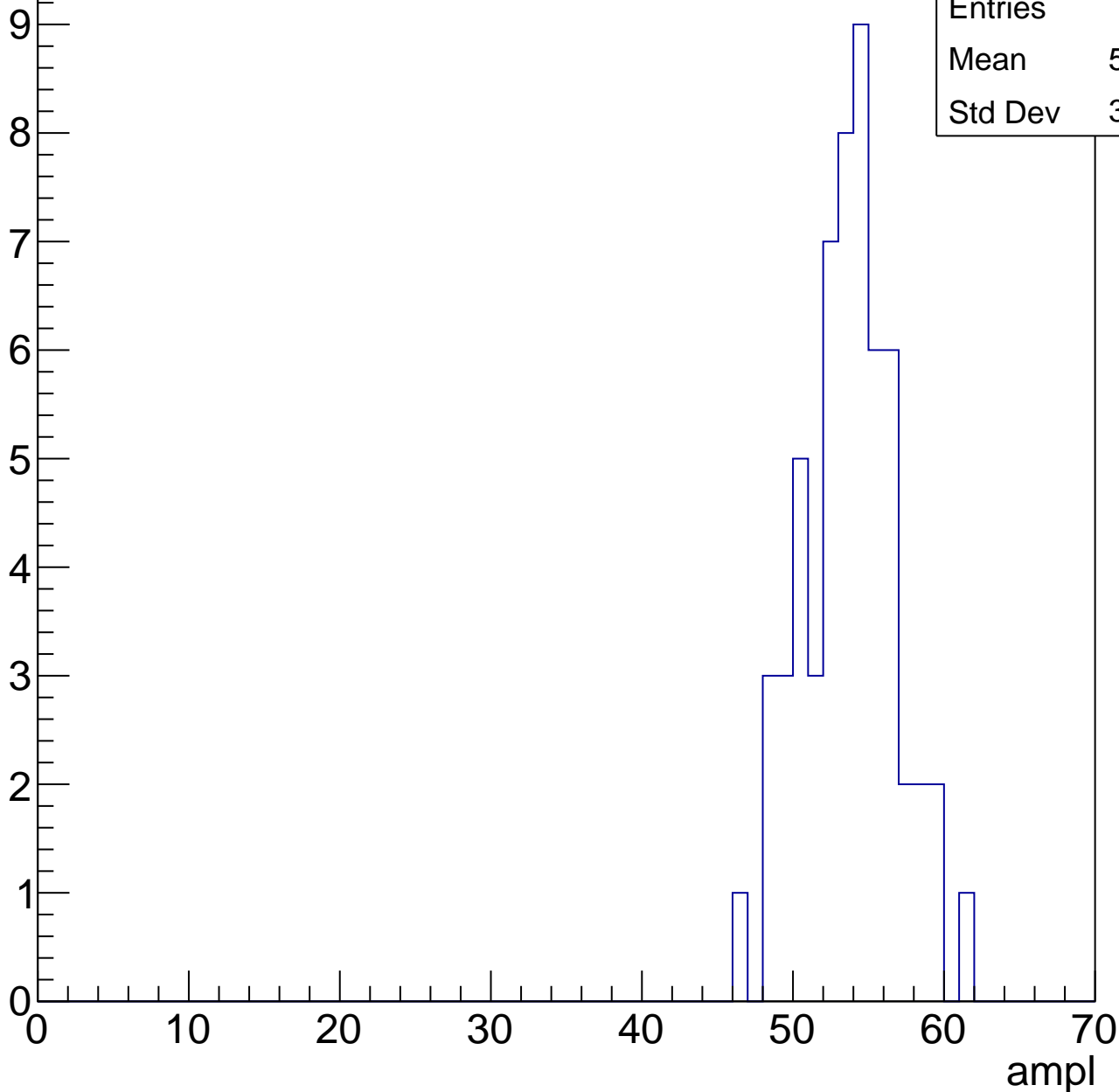


B1L102S, U14-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	53.26
Std Dev	3.043

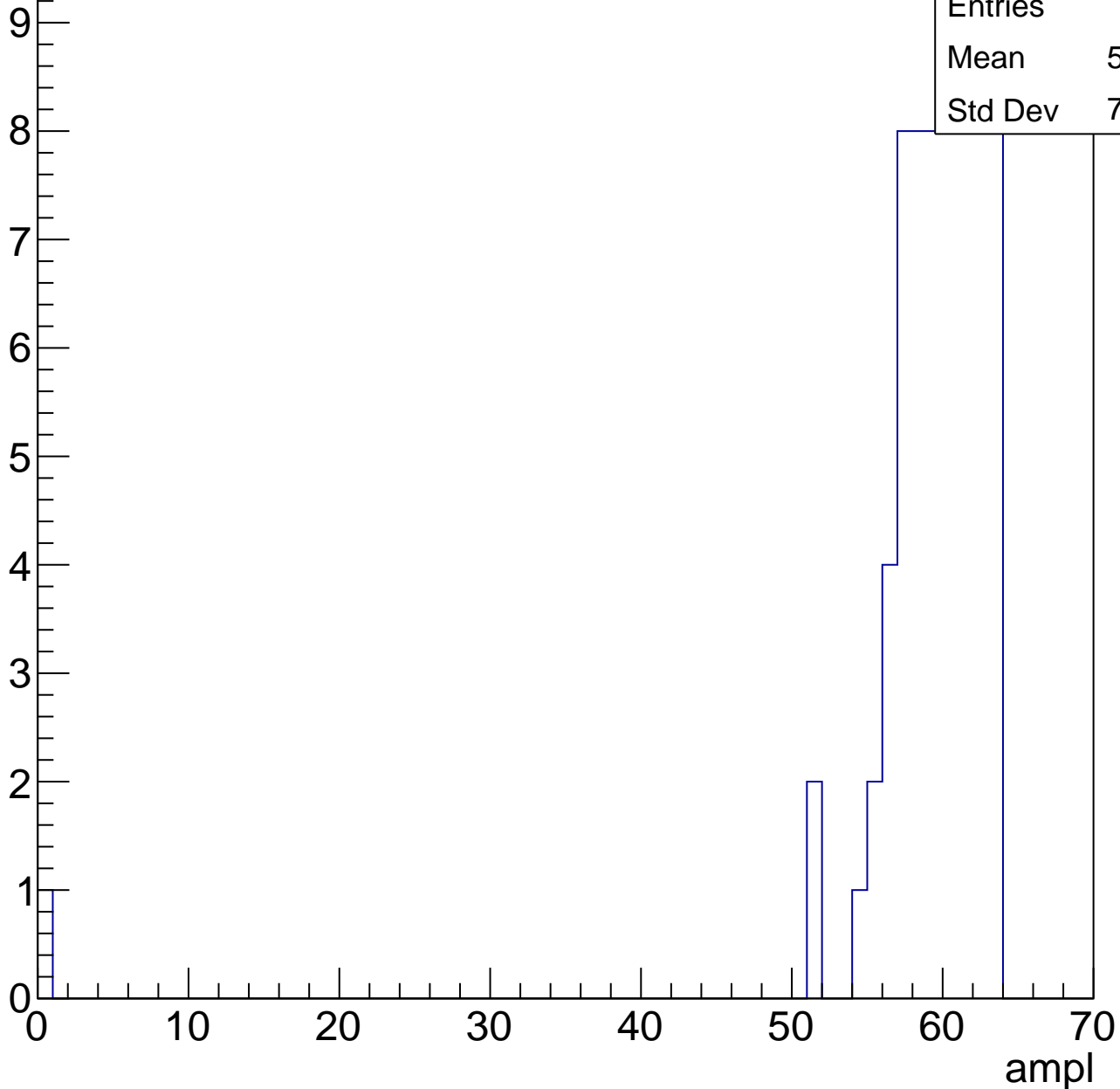


B1L102S, U14-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	58.43
Std Dev	7.647



B1L102S, U14-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	61.6
Std Dev	1.02

B1L102S, U14-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U14-ch72, adc0

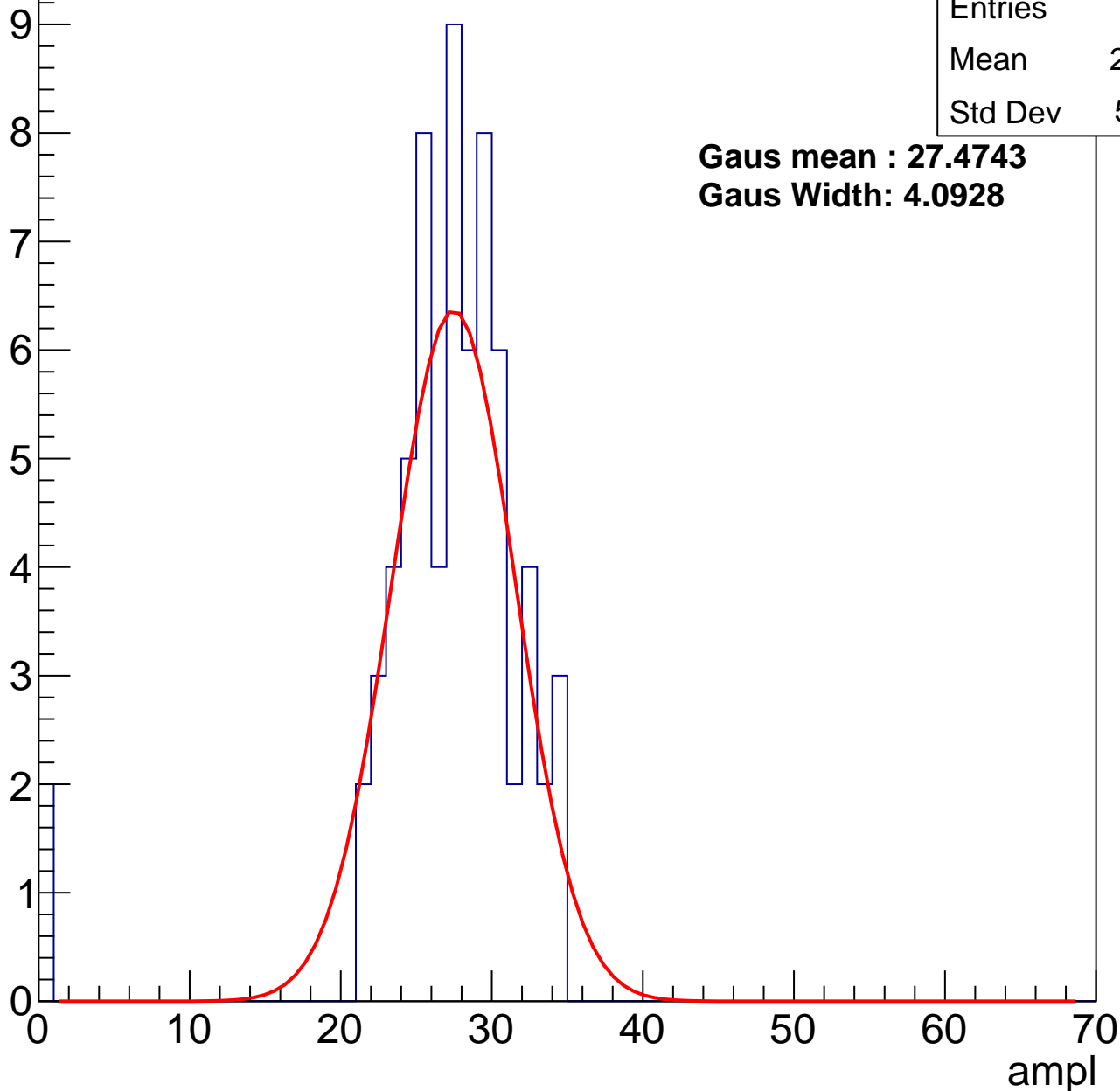
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	26.54
Std Dev	5.661

Gaus mean : 27.4743

Gaus Width: 4.0928



B1L102S, U14-ch72, adc1

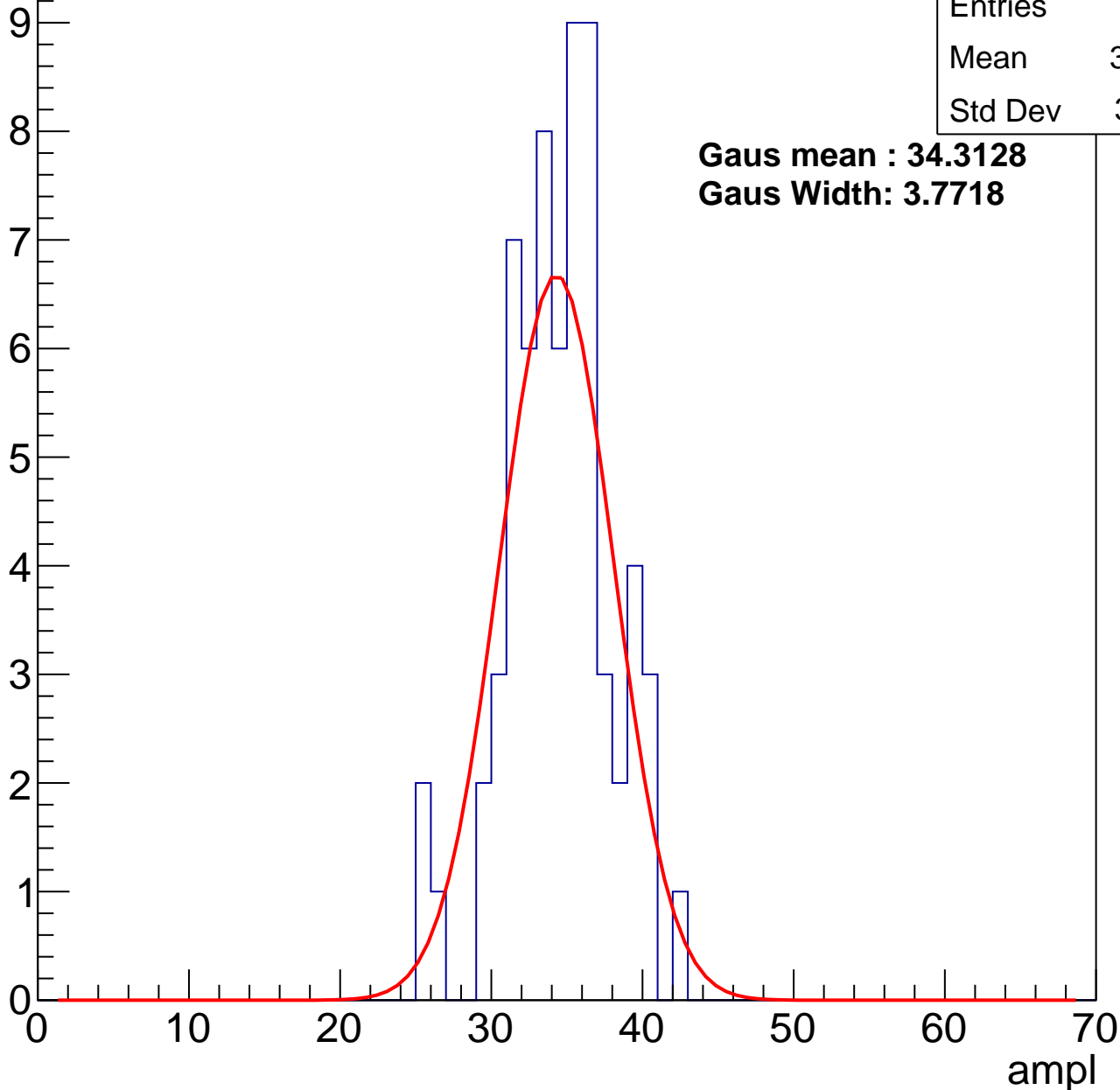
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.02
Std Dev	3.471

Gaus mean : 34.3128

Gaus Width: 3.7718

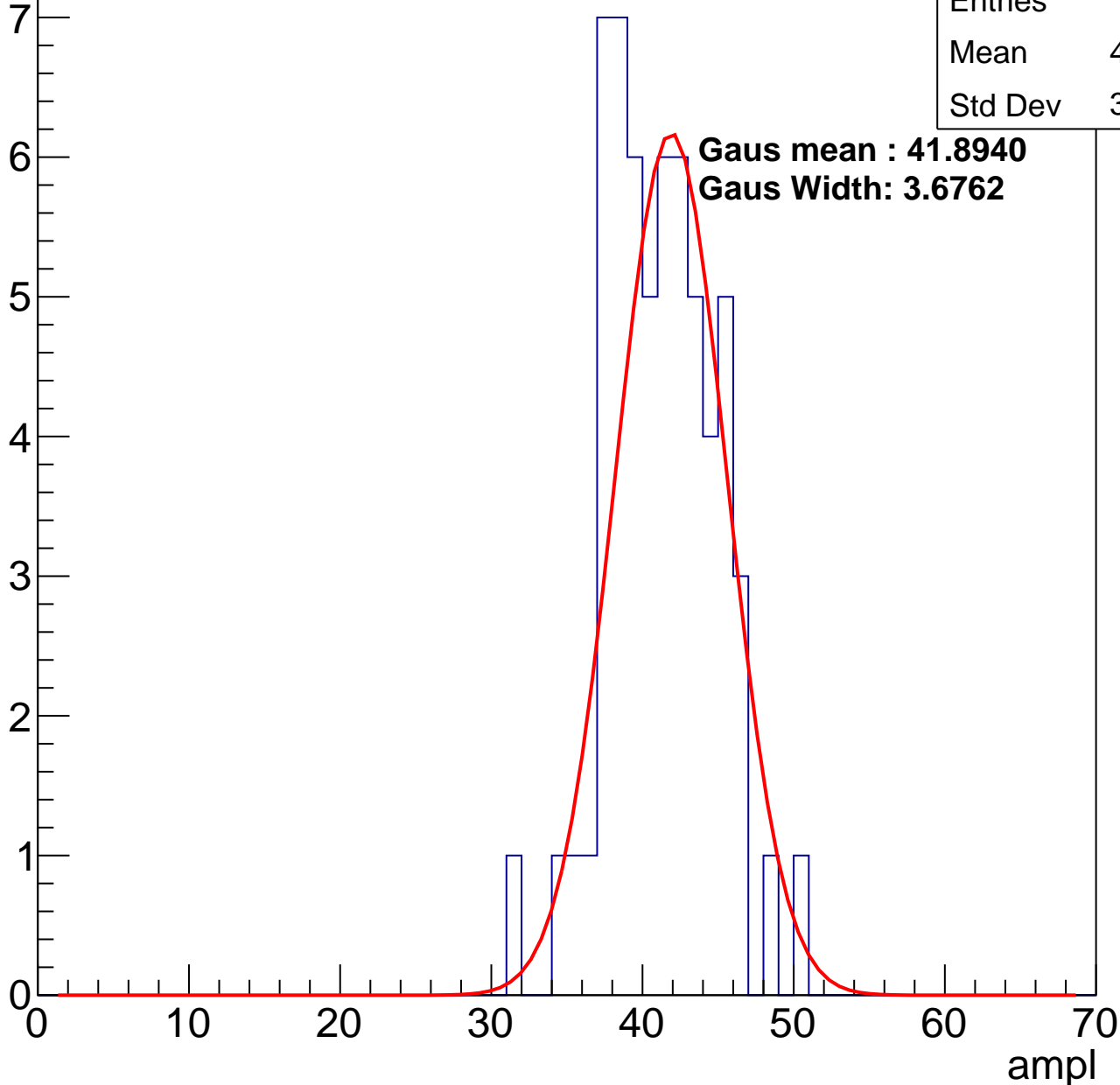


B1L102S, U14-ch72, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	40.75
Std Dev	3.553

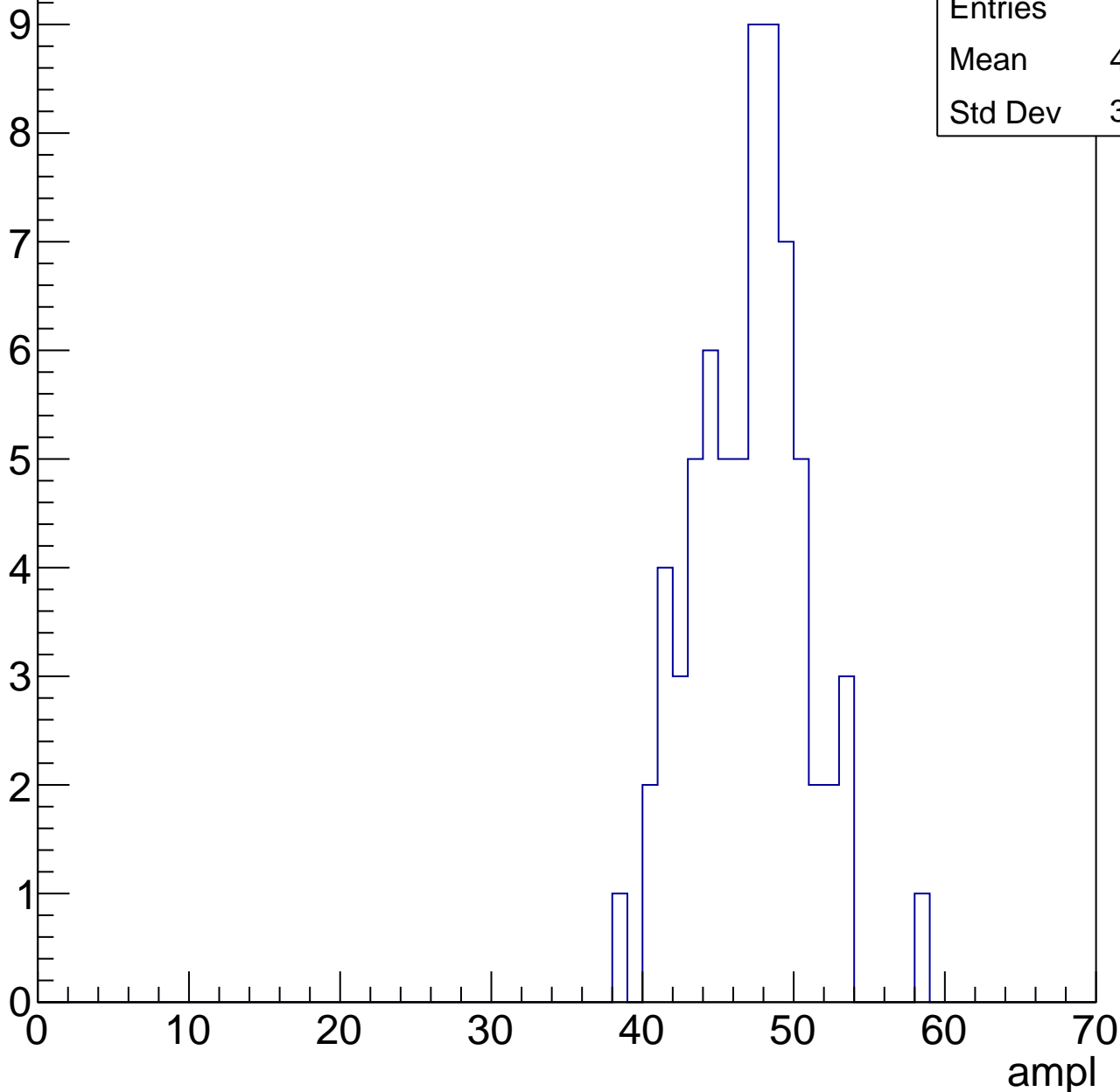


B1L102S, U14-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	46.57
Std Dev	3.685

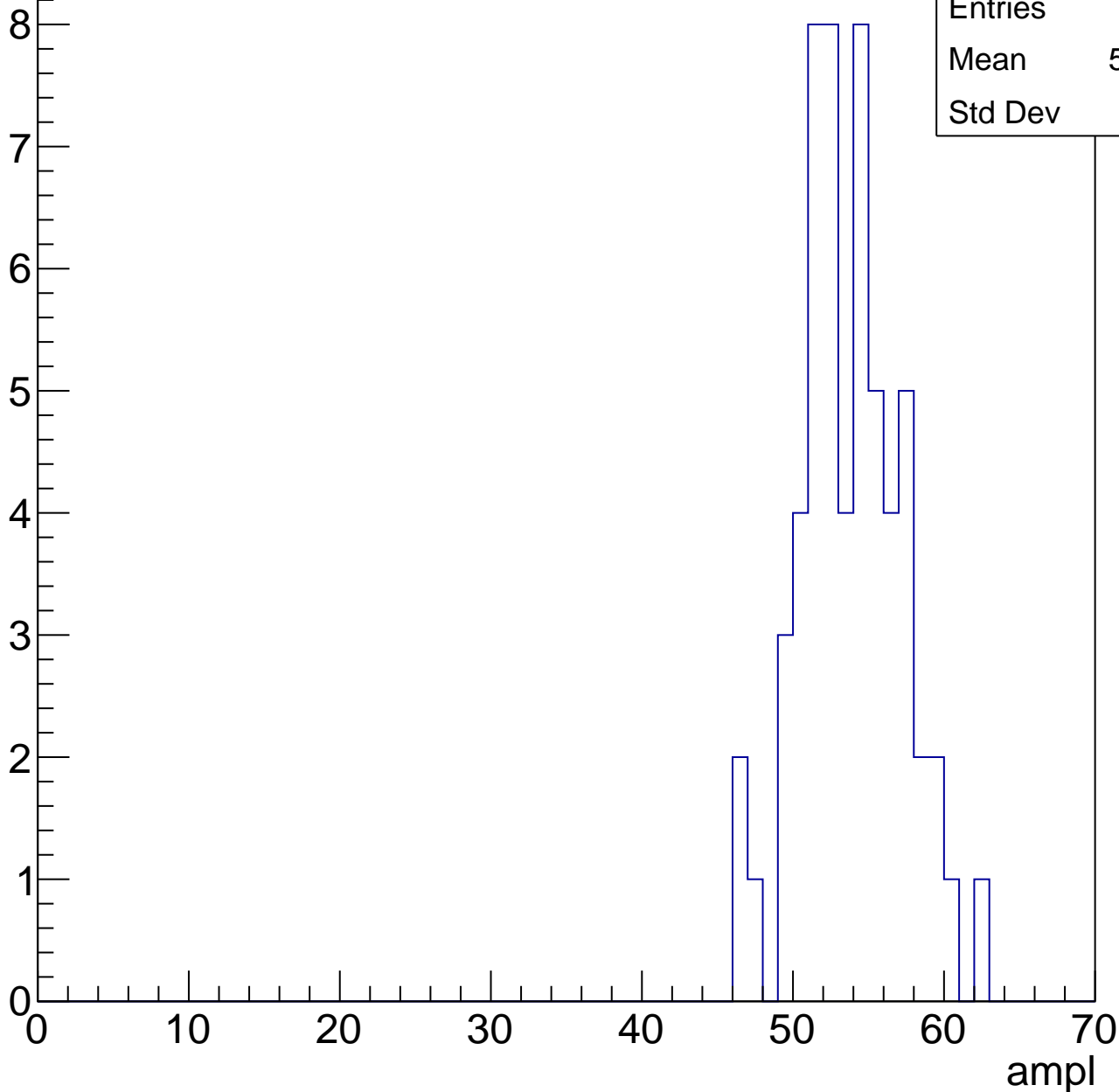


B1L102S, U14-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	53.34
Std Dev	3.34

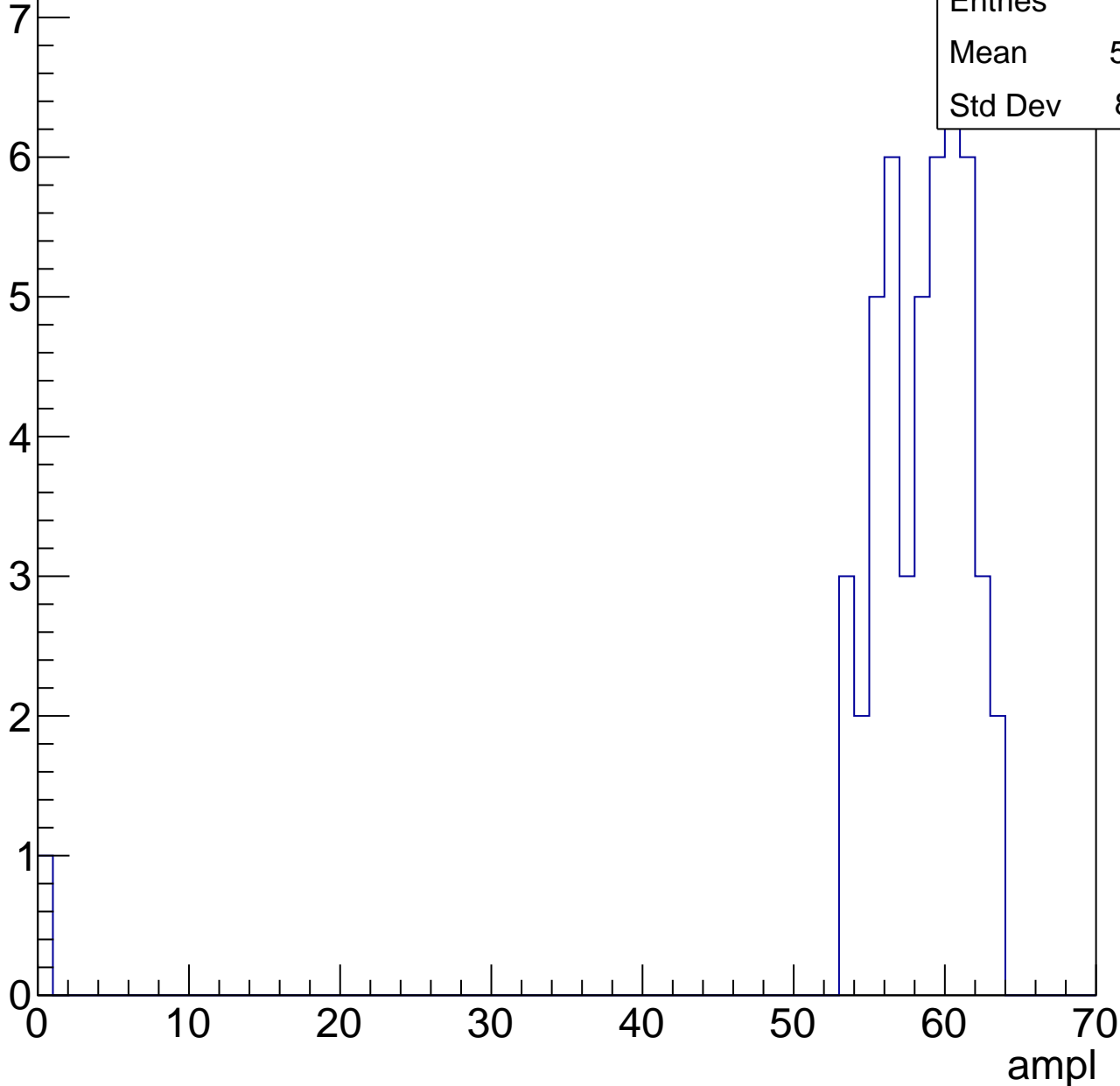


B1L102S, U14-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.96
Std Dev	8.661

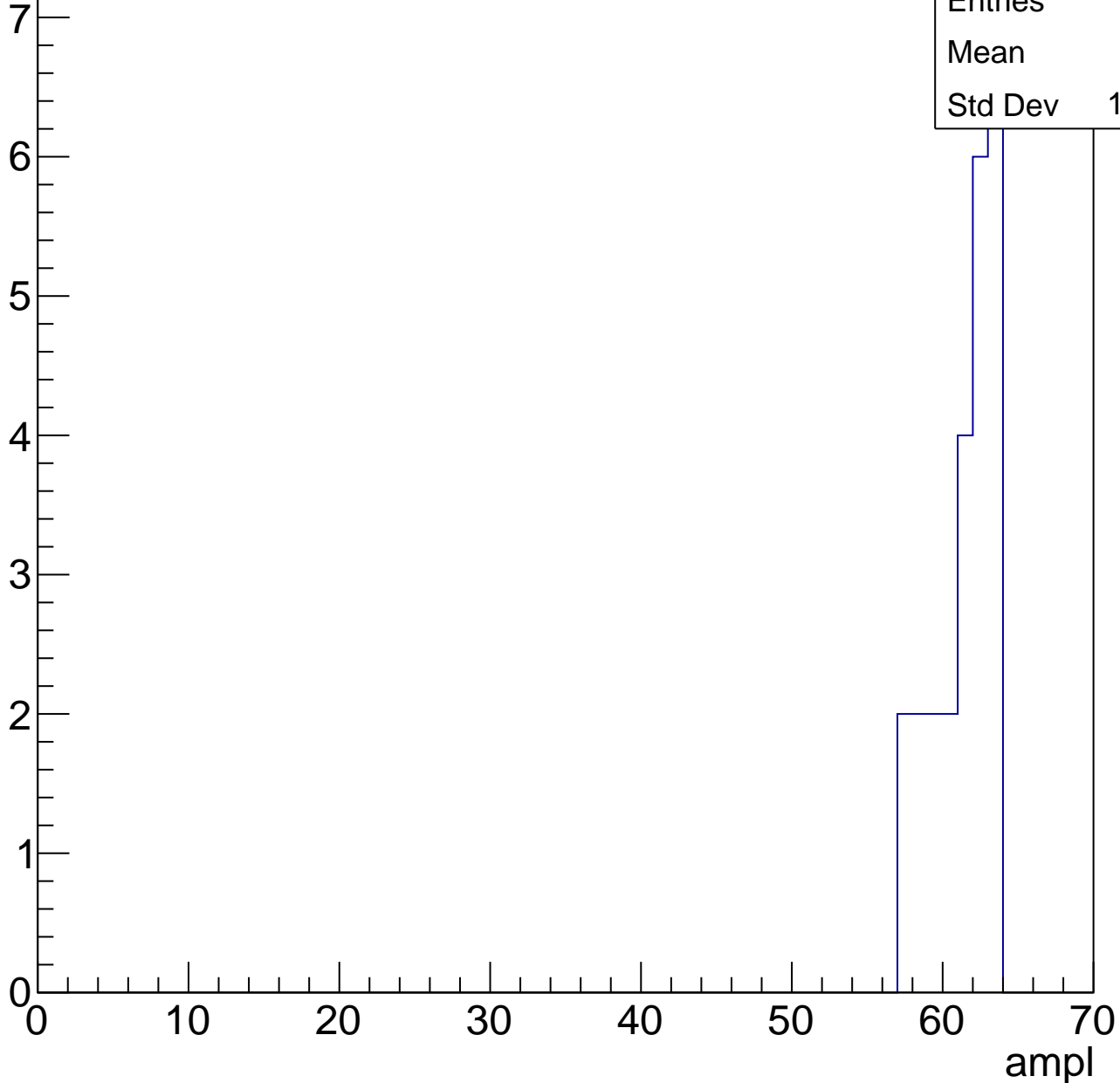


B1L102S, U14-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	61
Std Dev	1.939



B1L102S, U14-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch73, adc0

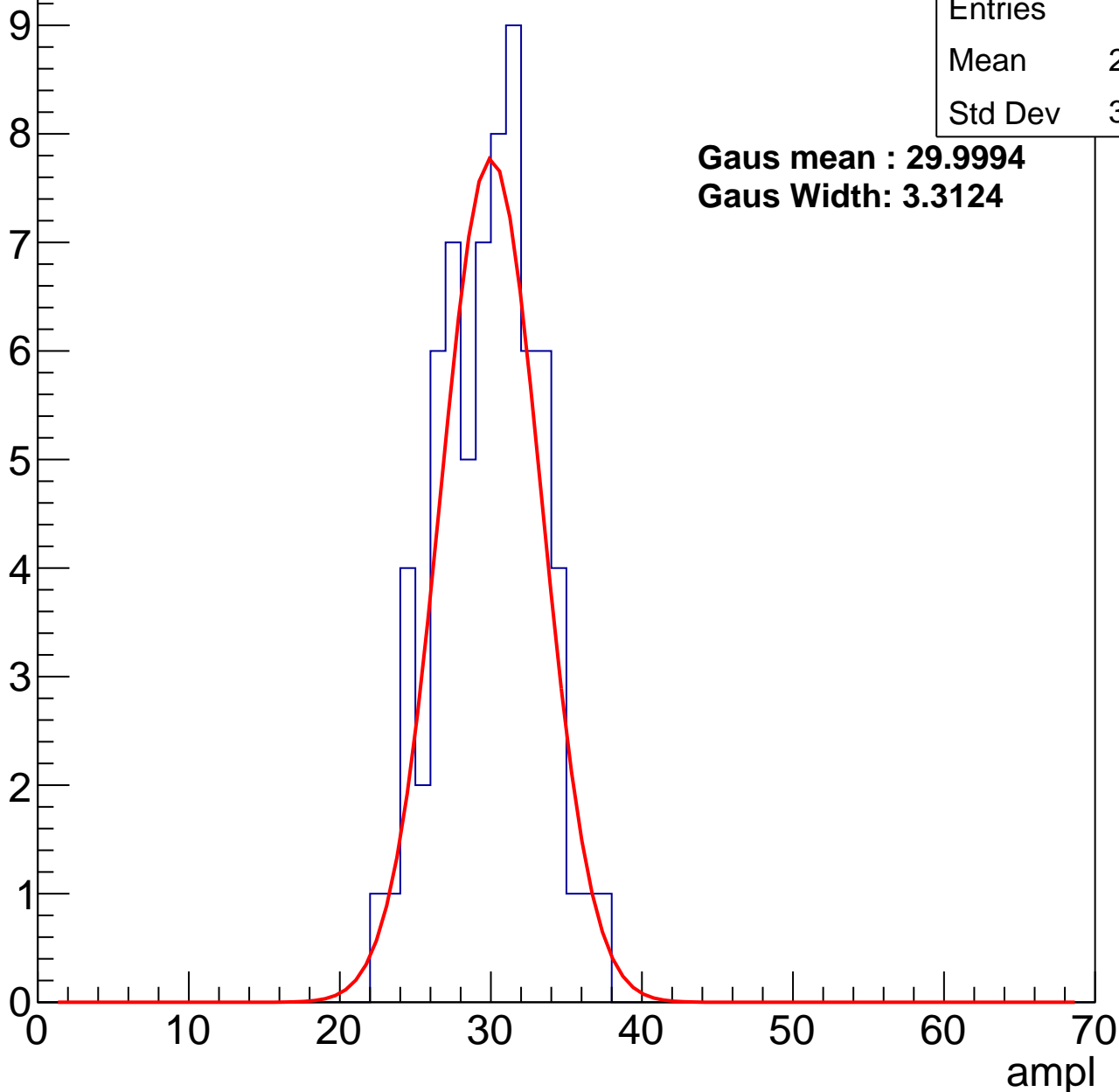
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.45
Std Dev	3.259

Gaus mean : 29.9994

Gaus Width: 3.3124



B1L102S, U14-ch73, adc1

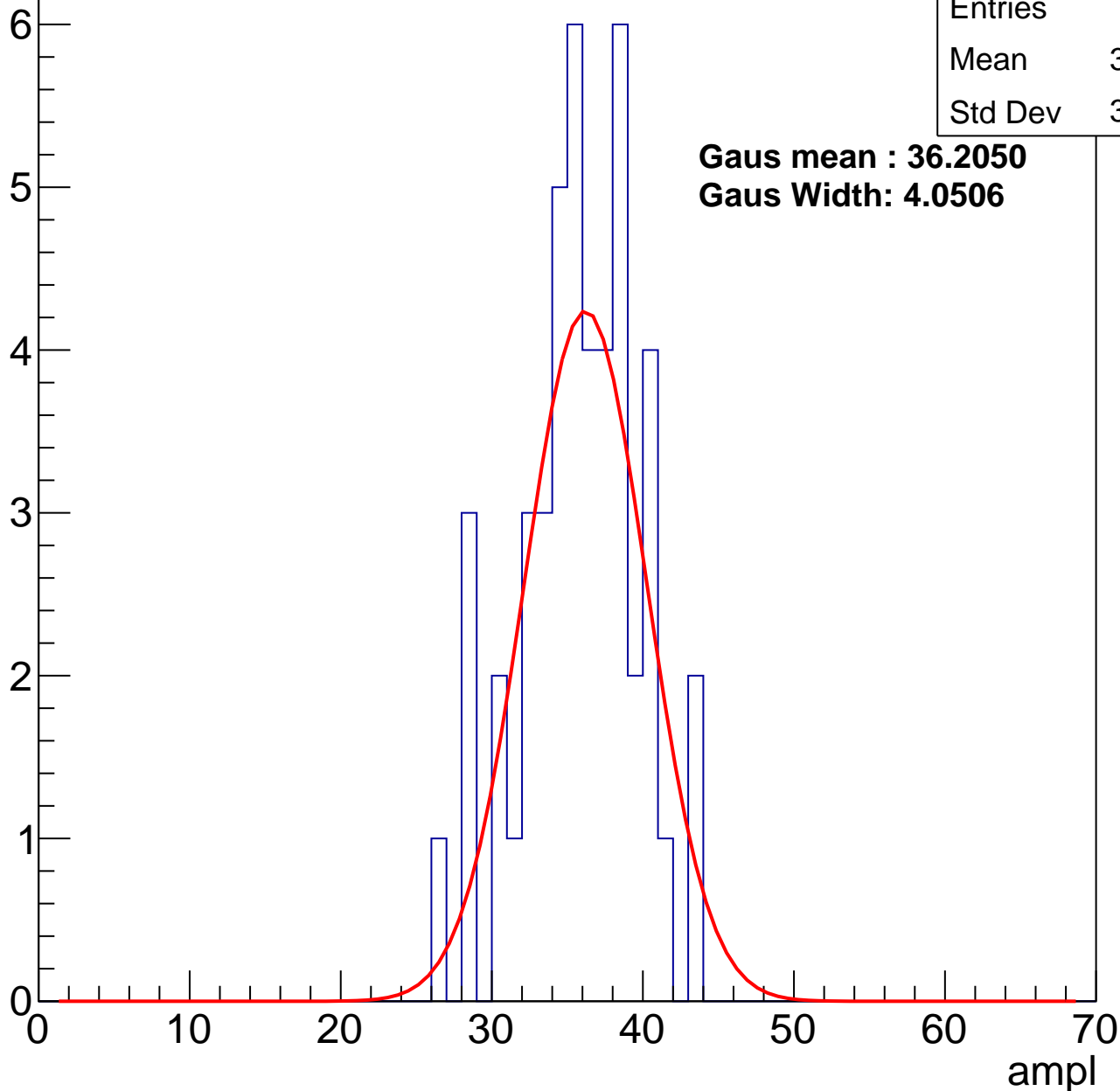
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	35.34
Std Dev	3.866

Gaus mean : 36.2050

Gaus Width: 4.0506



B1L102S, U14-ch73, adc2

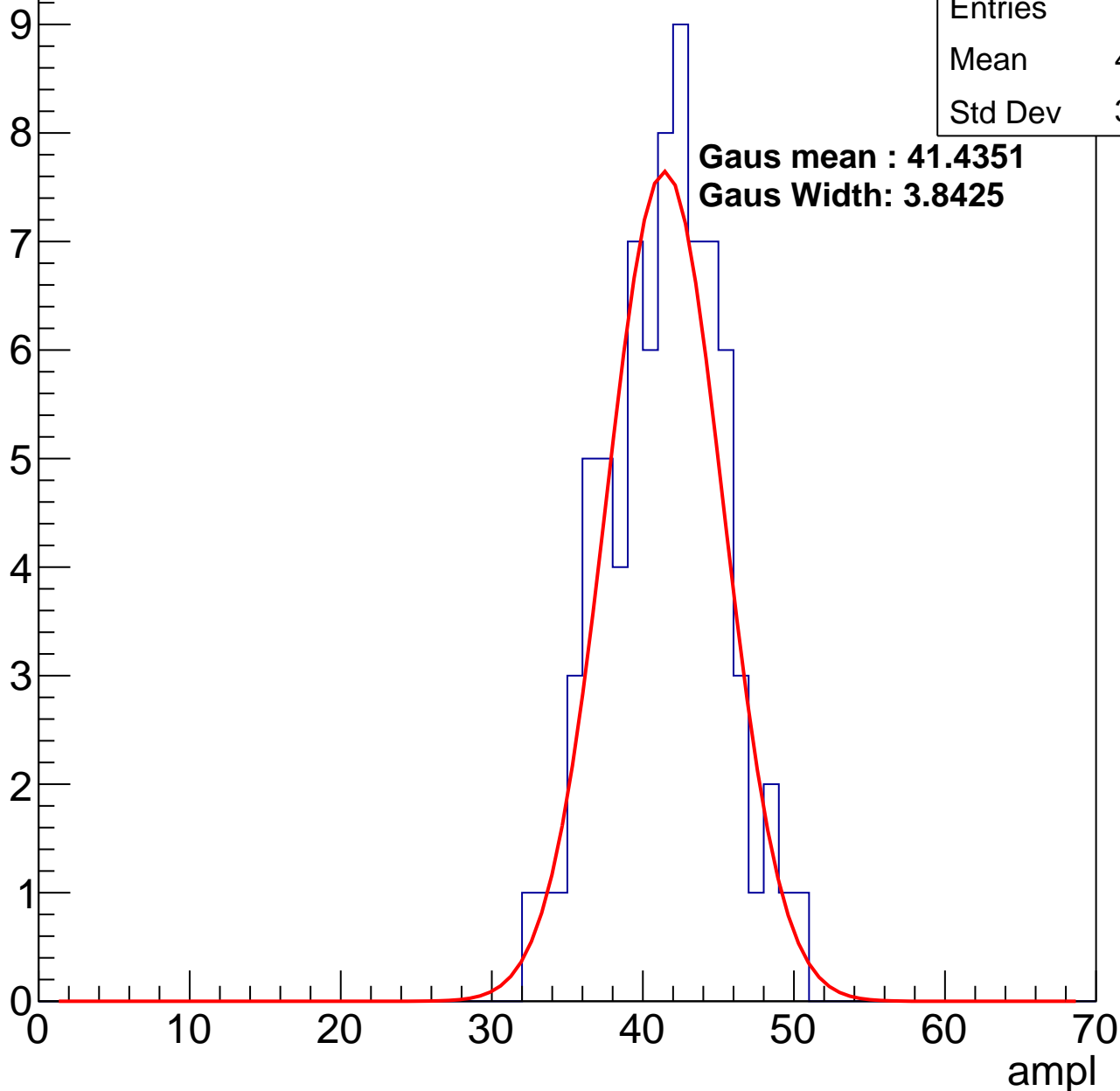
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	41.01
Std Dev	3.801

Gaus mean : 41.4351

Gaus Width: 3.8425

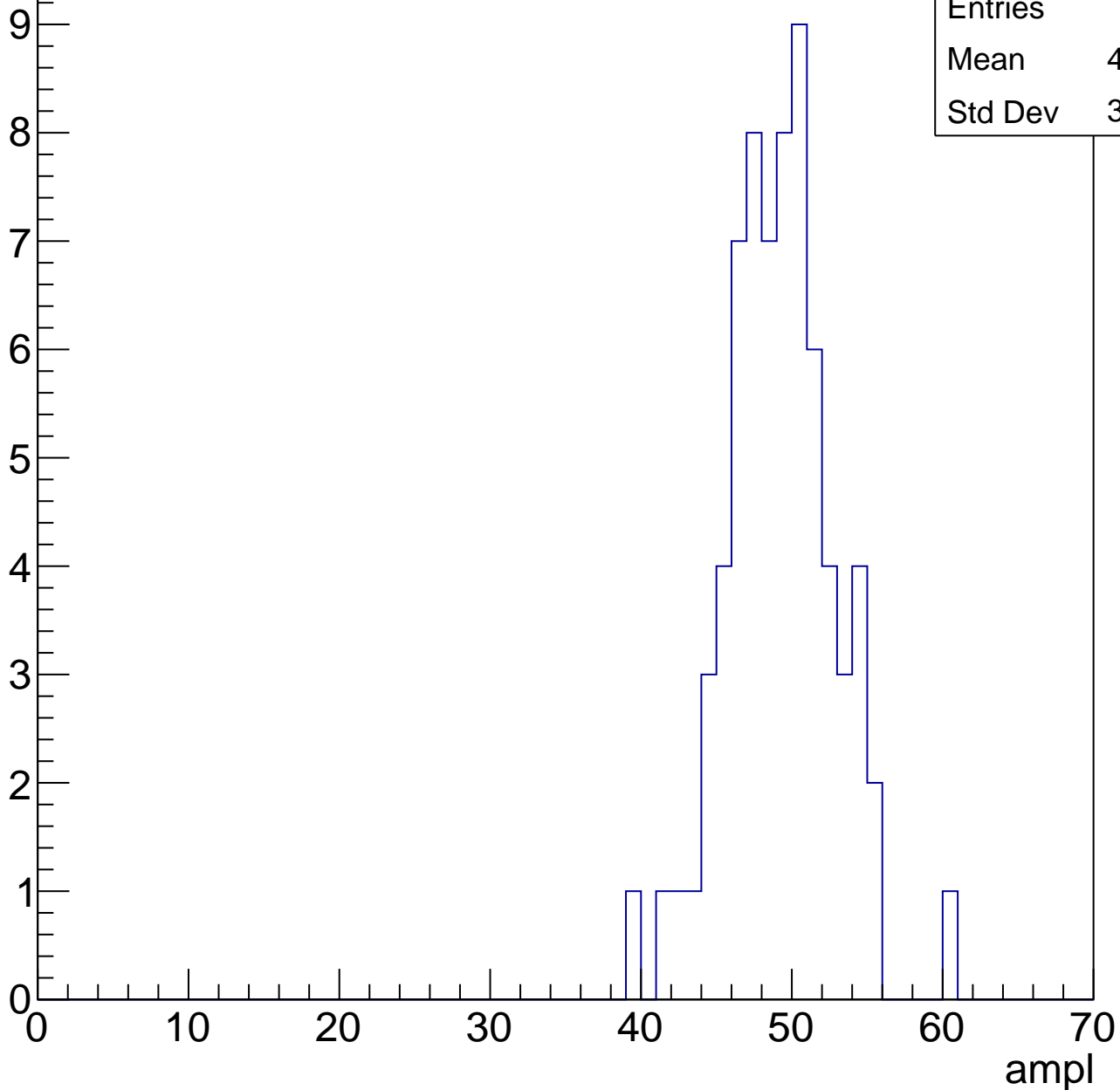


B1L102S, U14-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

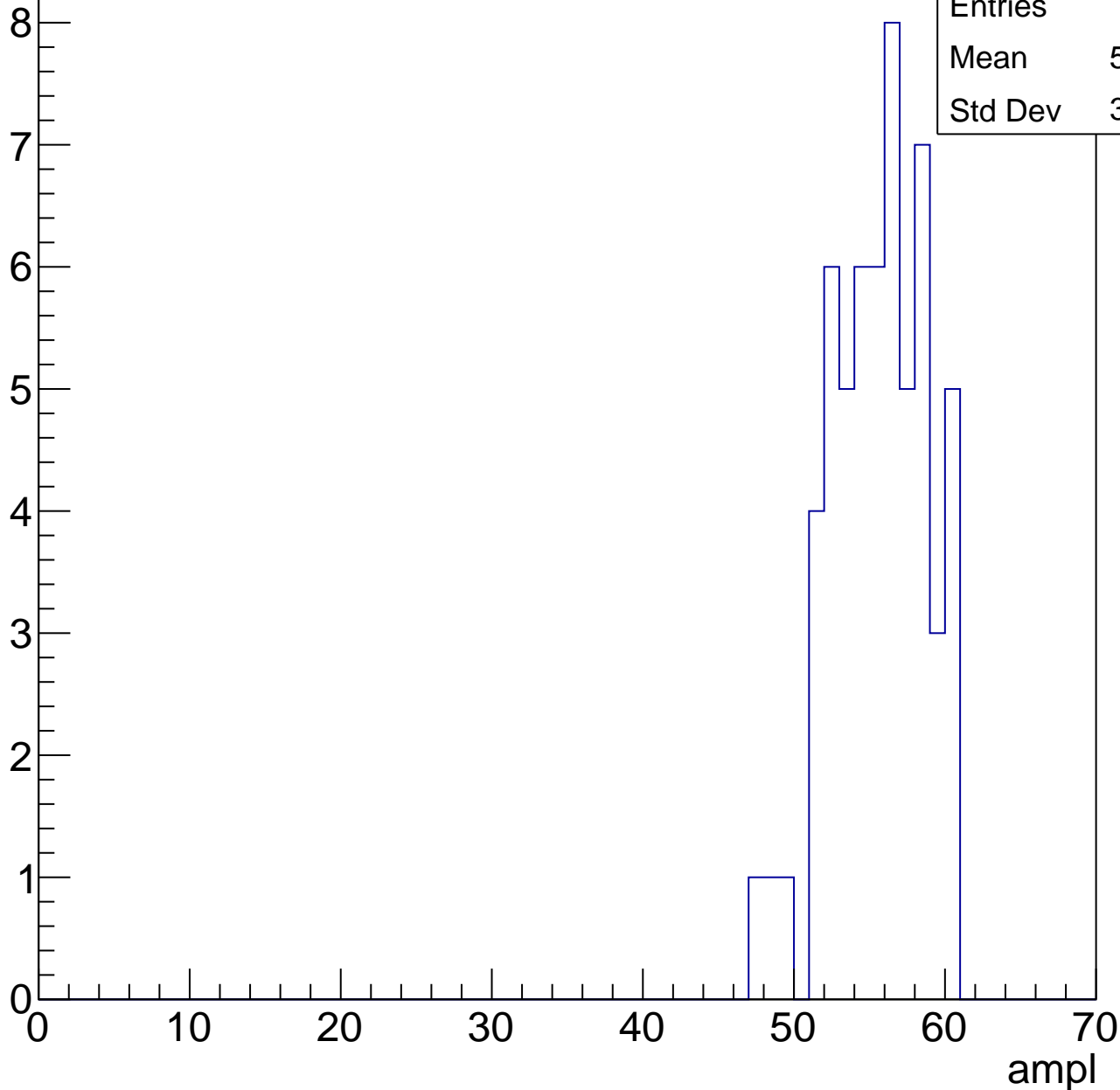
Entries	70
Mean	48.74
Std Dev	3.576



B1L102S, U14-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

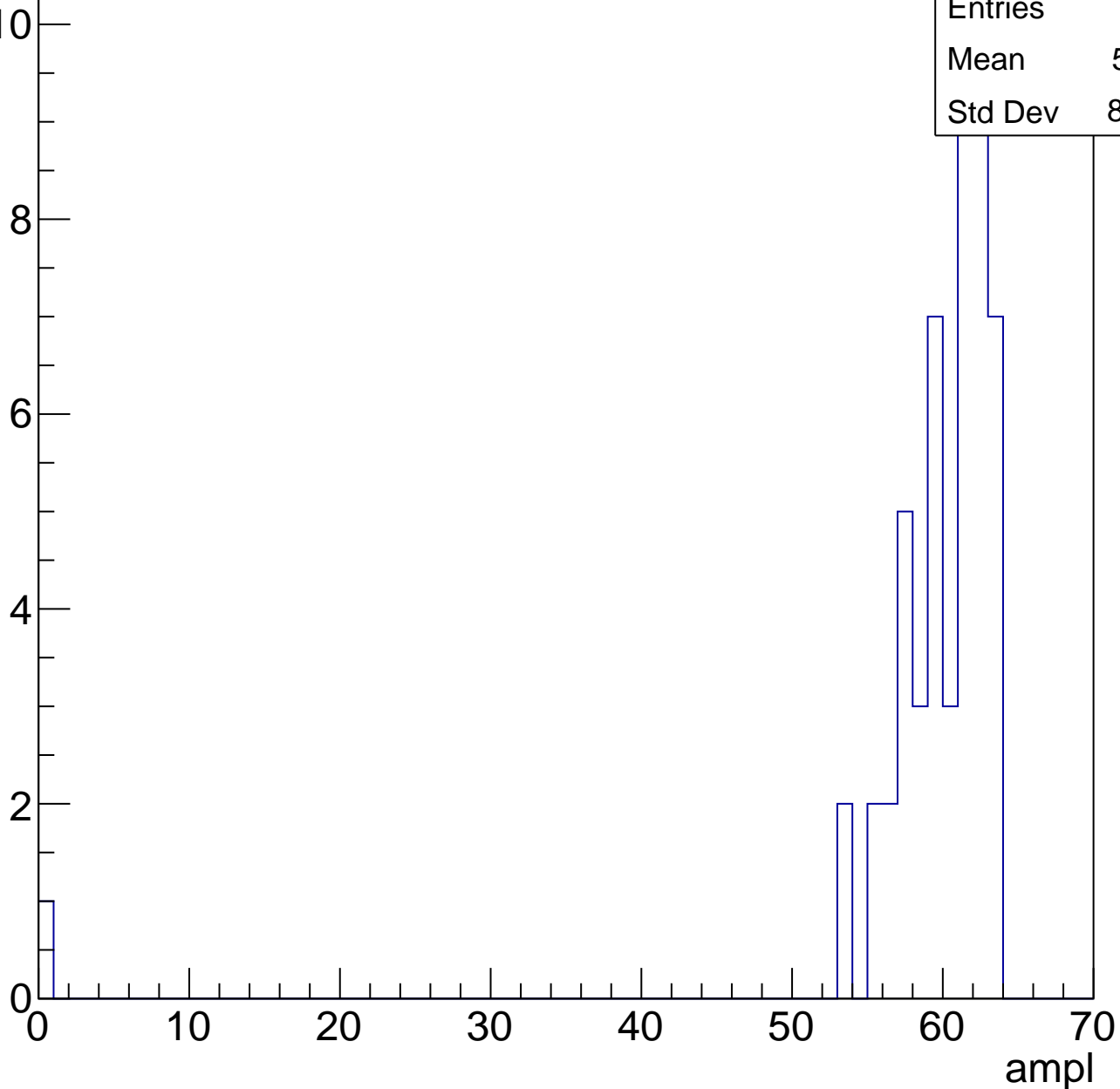


B1L102S, U14-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.61
Std Dev	8.693



B1L102S, U14-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch74, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	26.86
Std Dev	3.708

Gaus mean : 27.2018

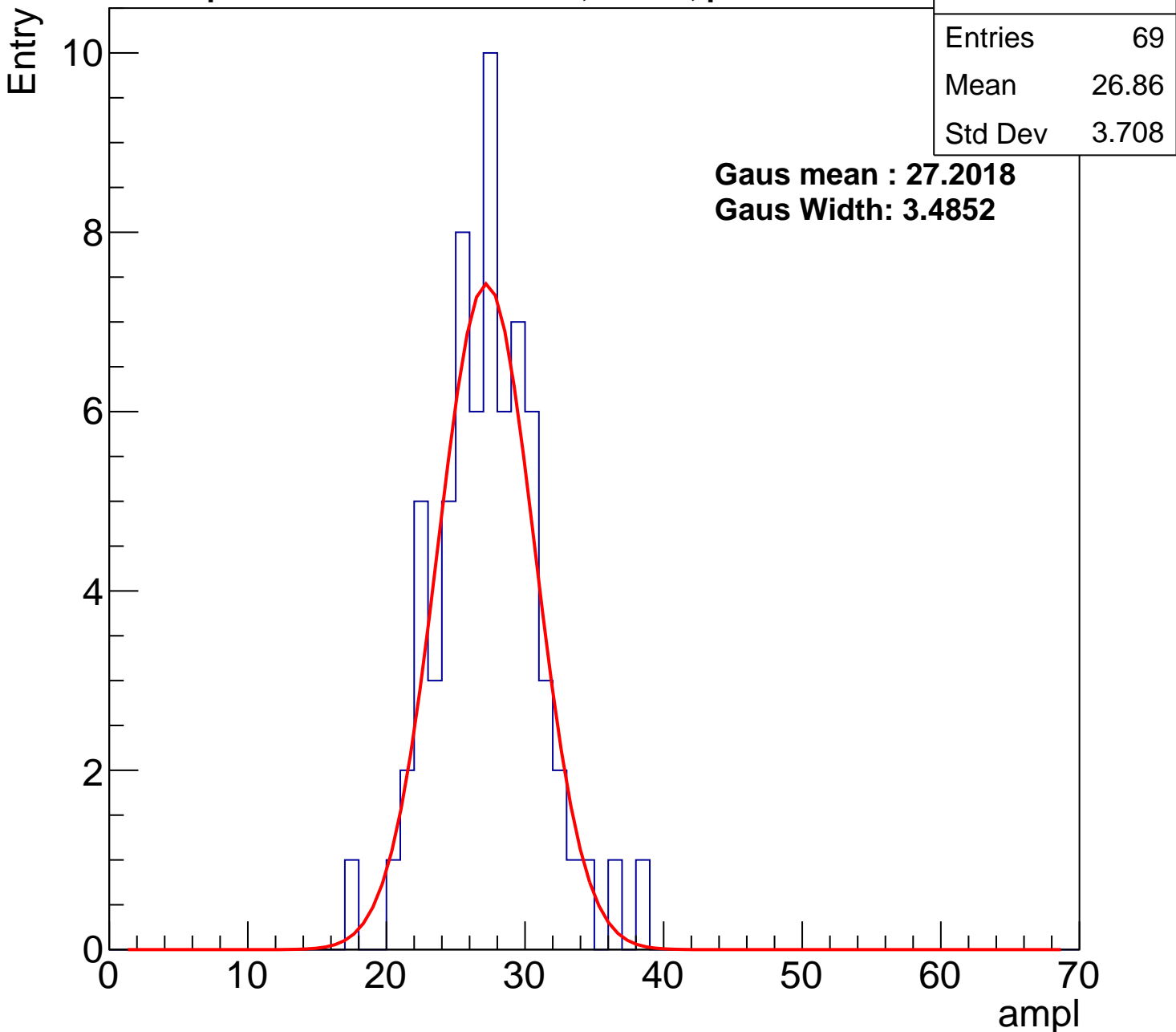
Gaus Width: 3.4852

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch74, adc1

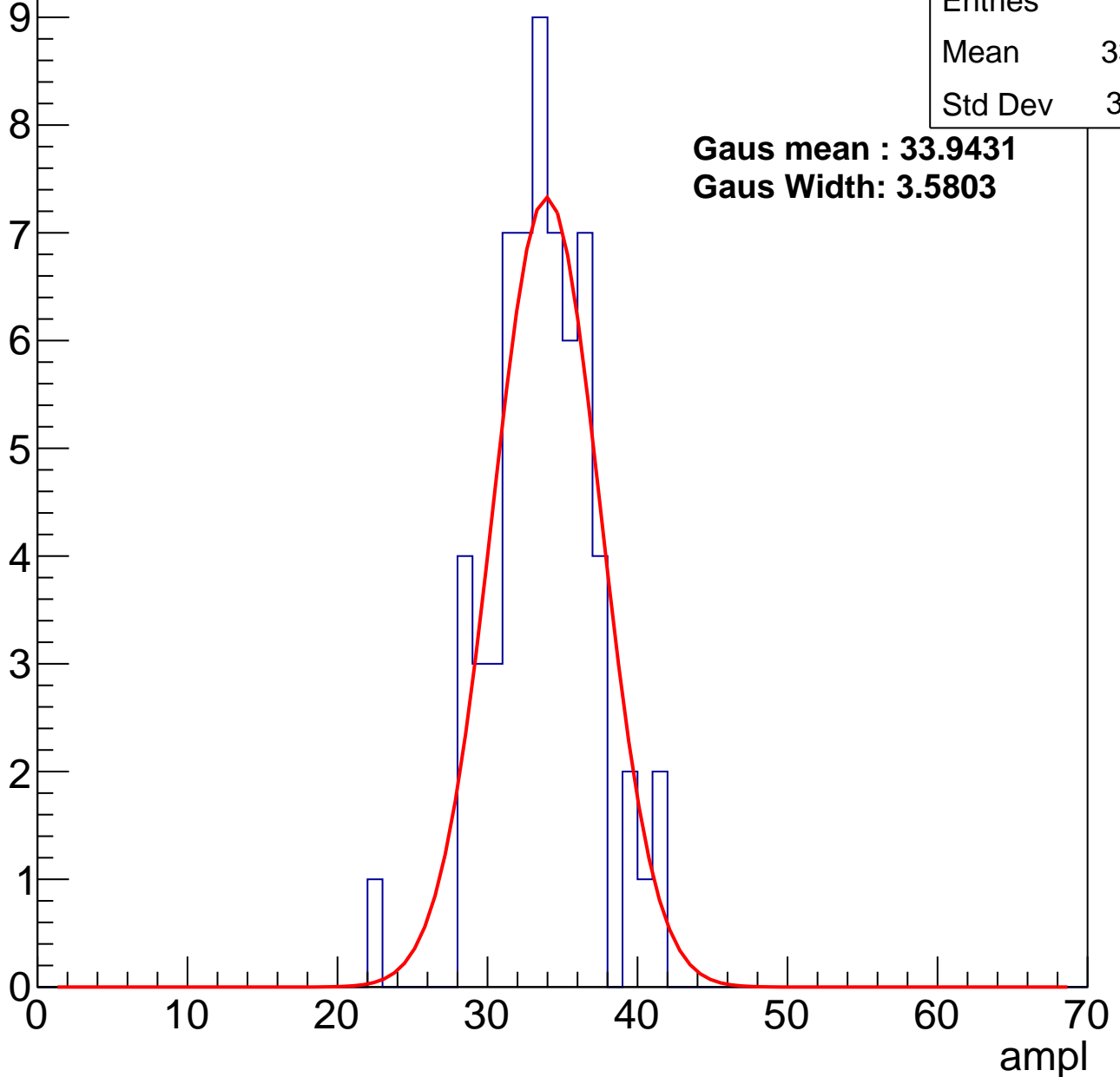
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	33.29
Std Dev	3.411

Gaus mean : 33.9431

Gaus Width: 3.5803



B1L102S, U14-ch74, adc2

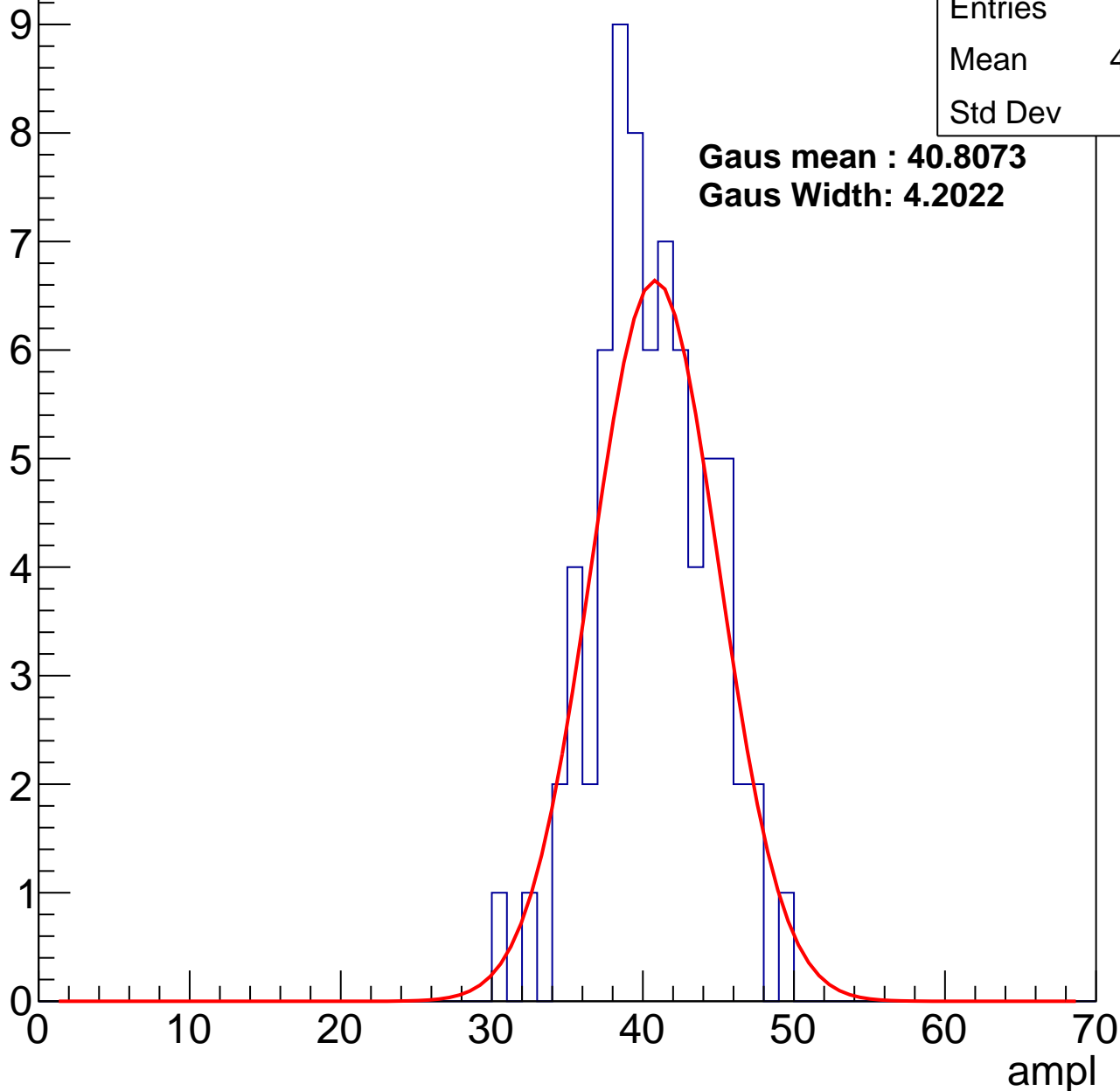
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	40.13
Std Dev	3.73

Gaus mean : 40.8073

Gaus Width: 4.2022

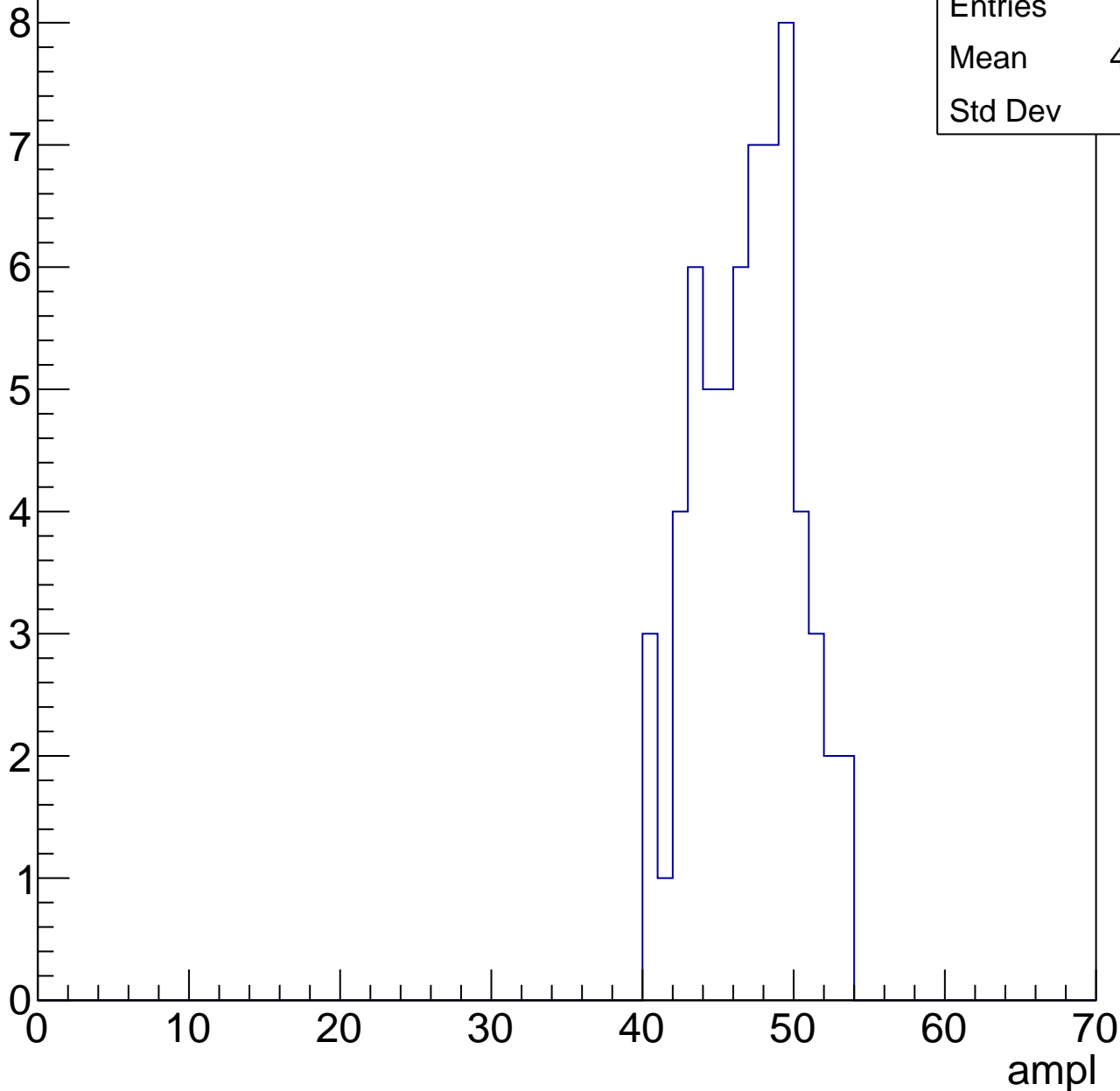


B1L102S, U14-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	46.48
Std Dev	3.28

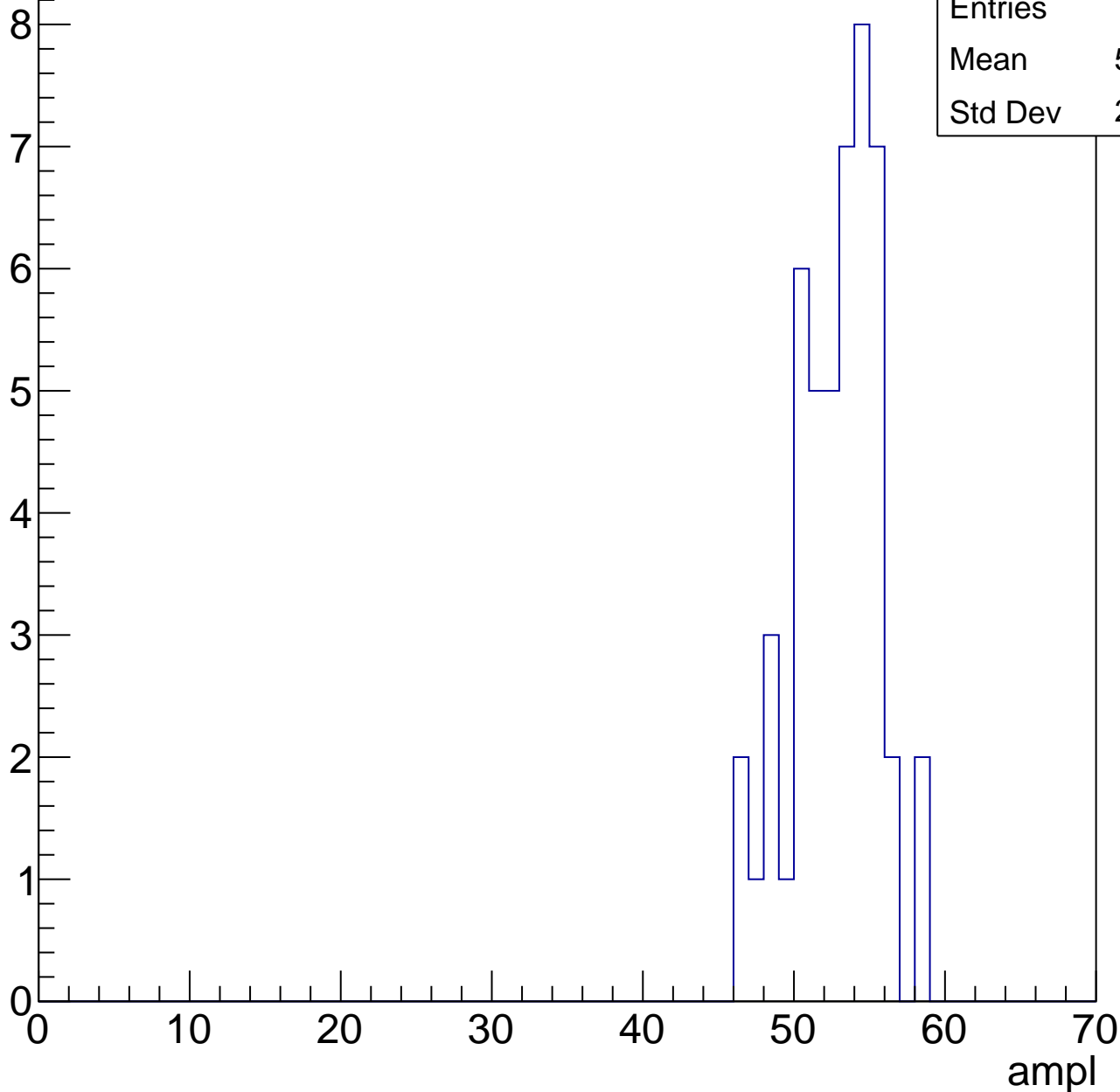


B1L102S, U14-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

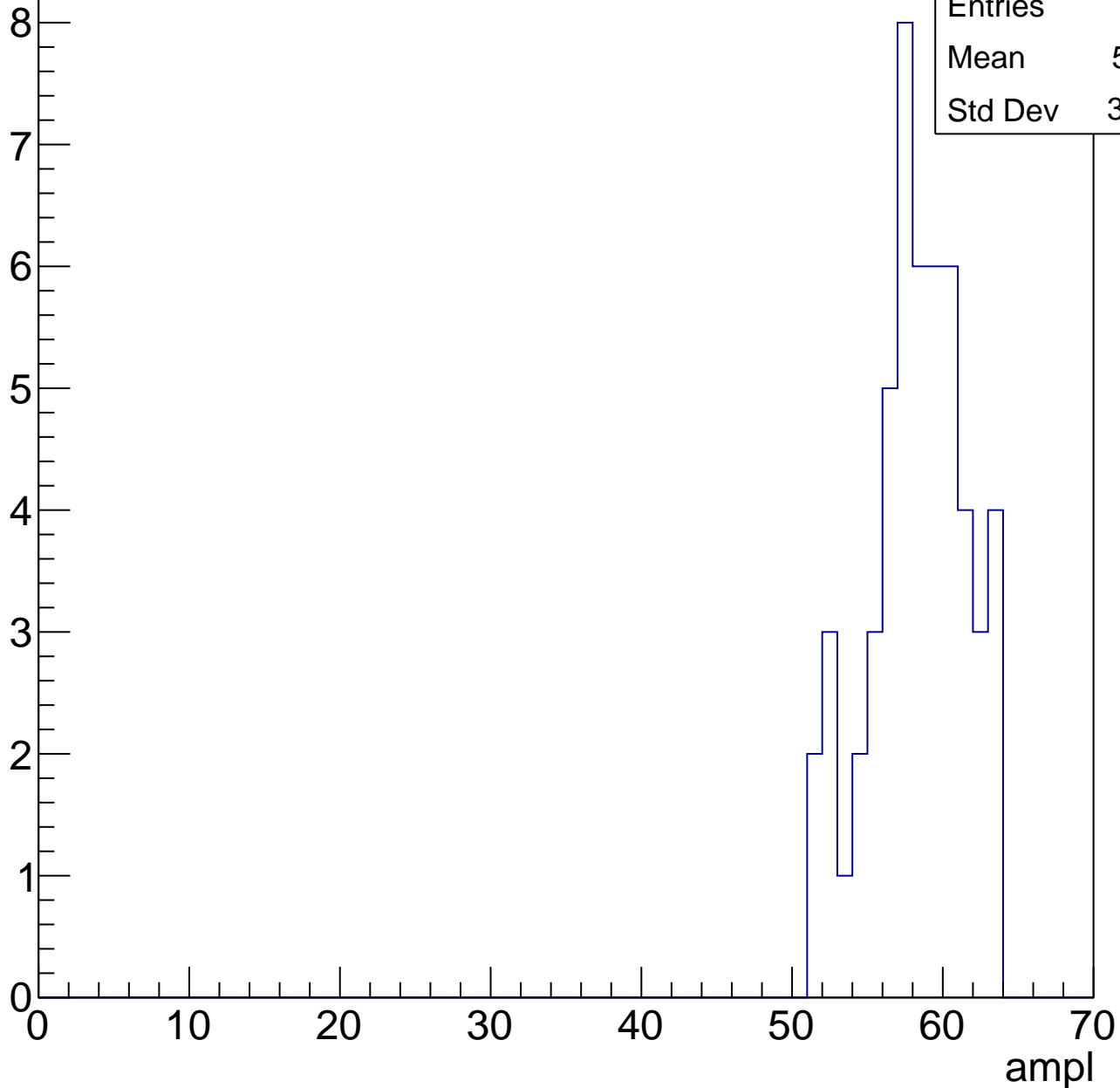
Entries	49
Mean	52.31
Std Dev	2.801



B1L102S, U14-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

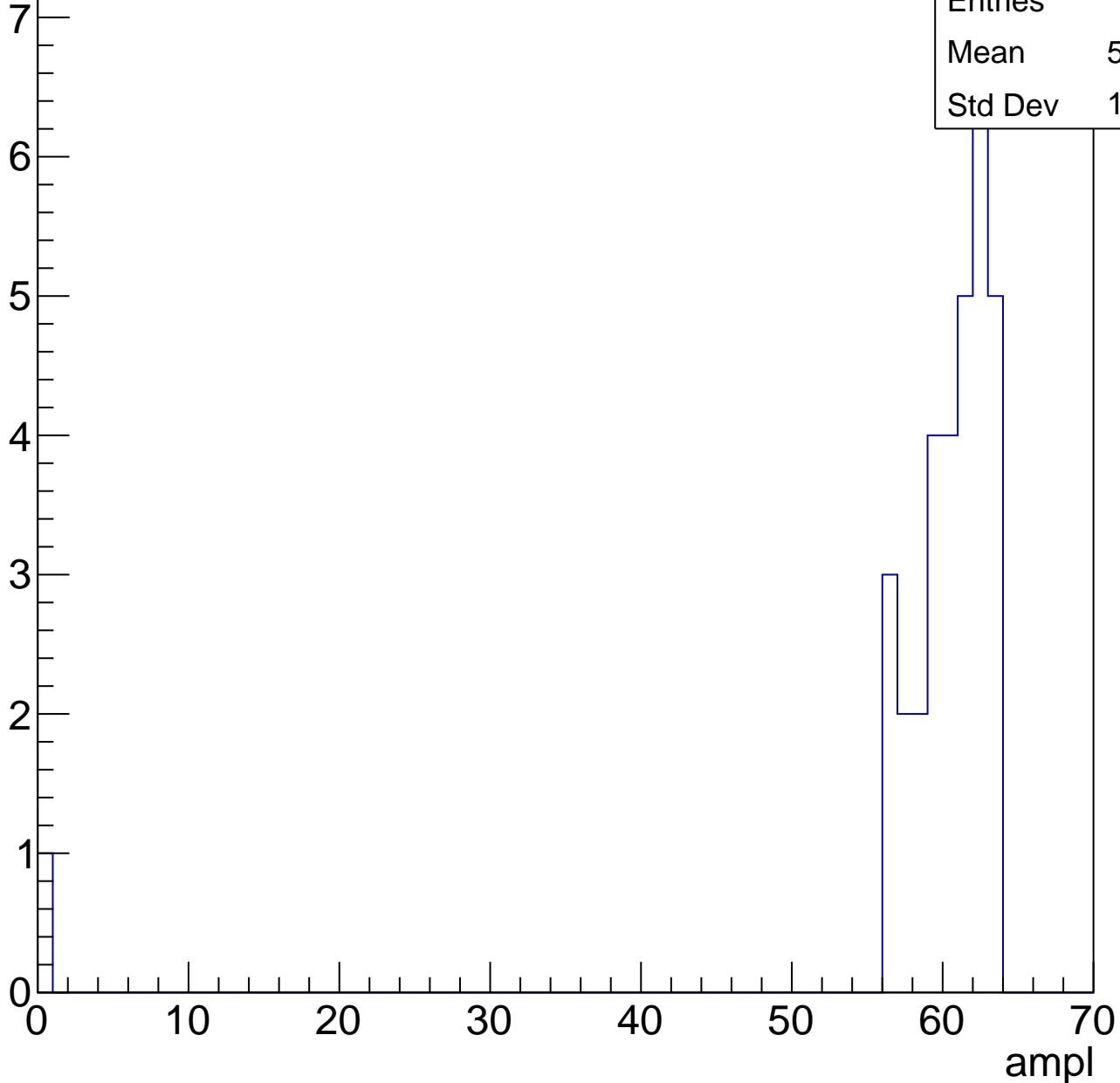


B1L102S, U14-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

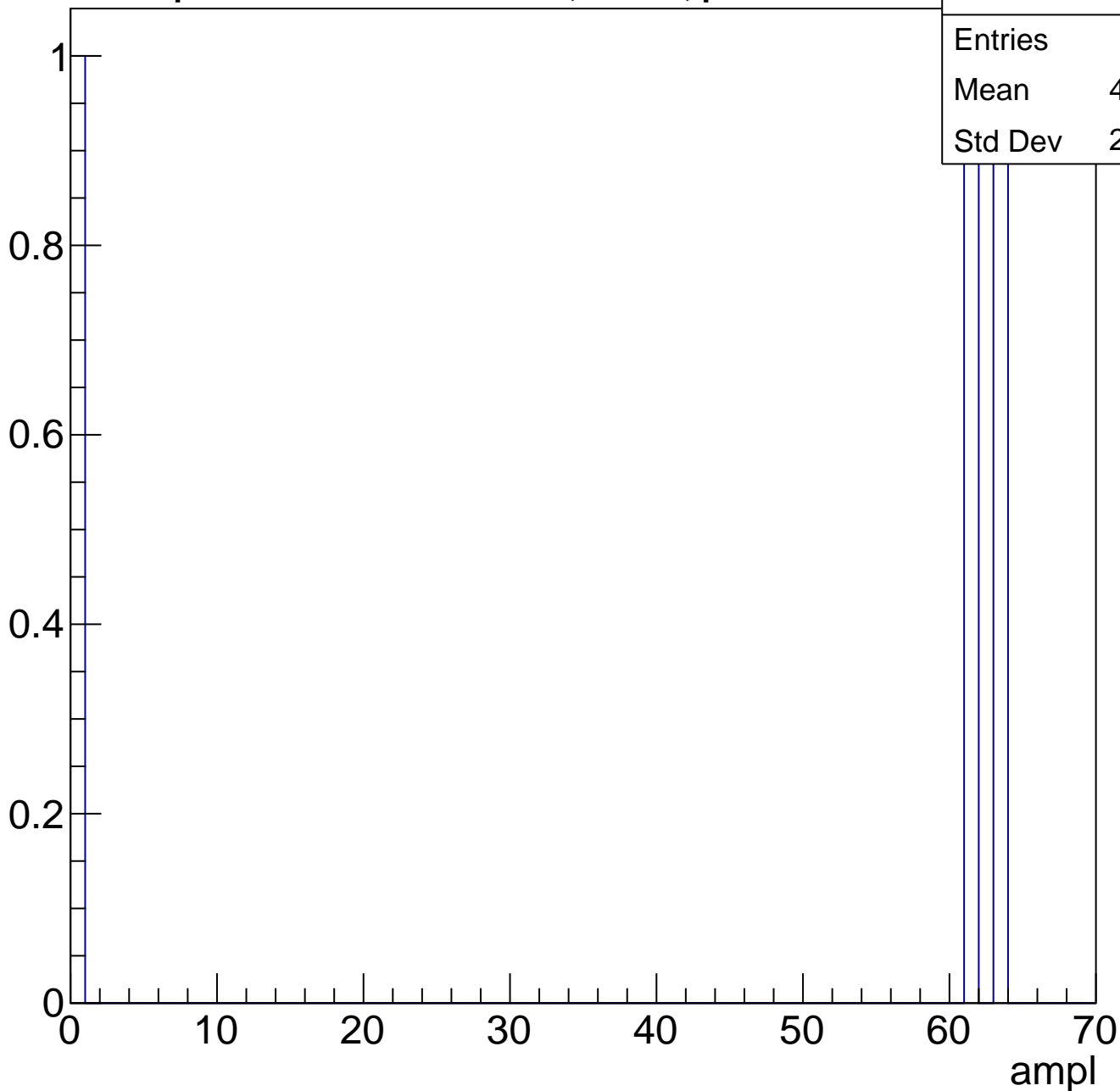
Entries	33
Mean	58.42
Std Dev	10.55



B1L102S, U14-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch75, adc0

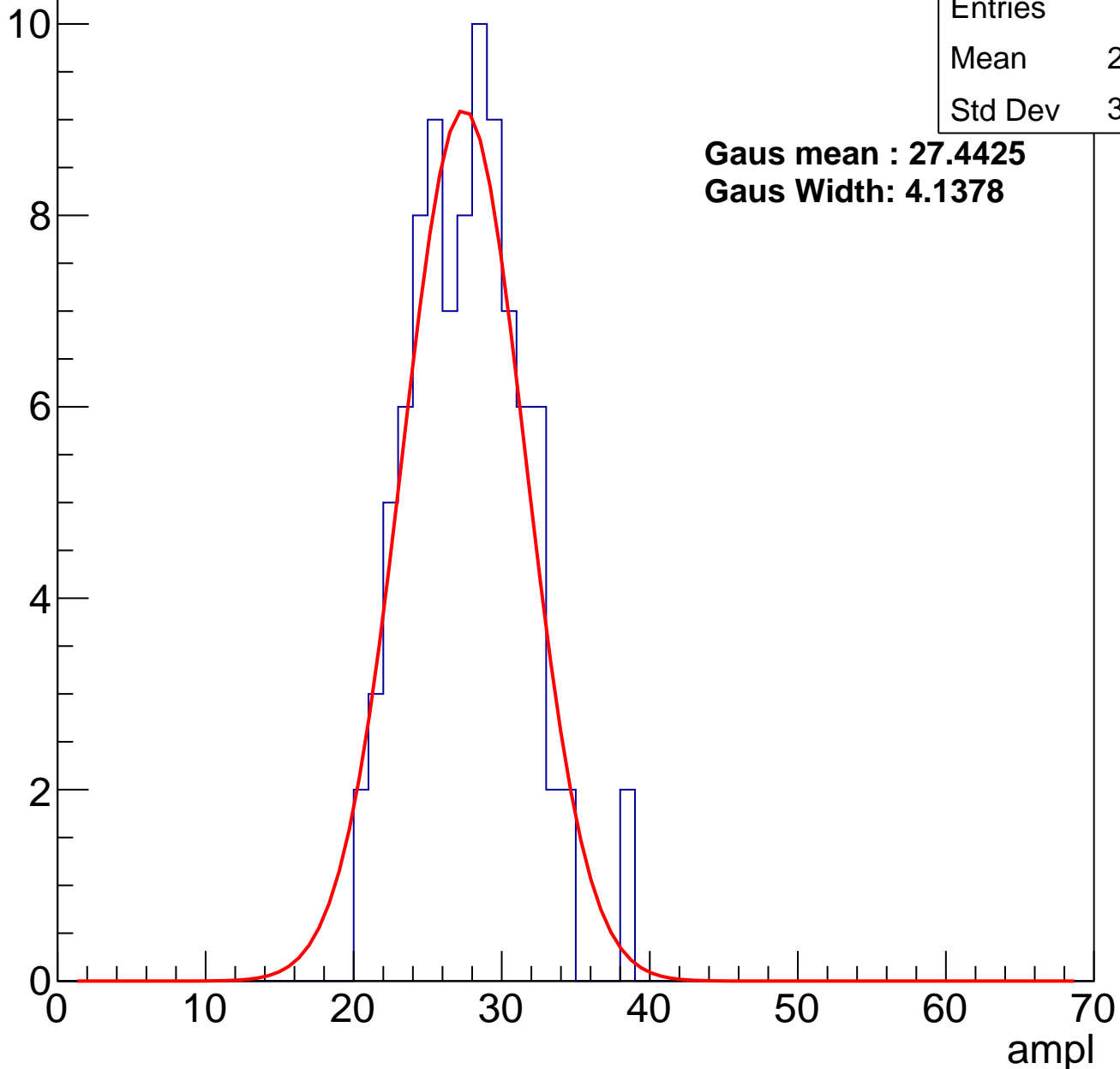
calib_packv5_042523_0143.root, FC#11, port A2

Entries	92
Mean	27.23
Std Dev	3.765

Gaus mean : 27.4425

Gaus Width: 4.1378

Entry



B1L102S, U14-ch75, adc1

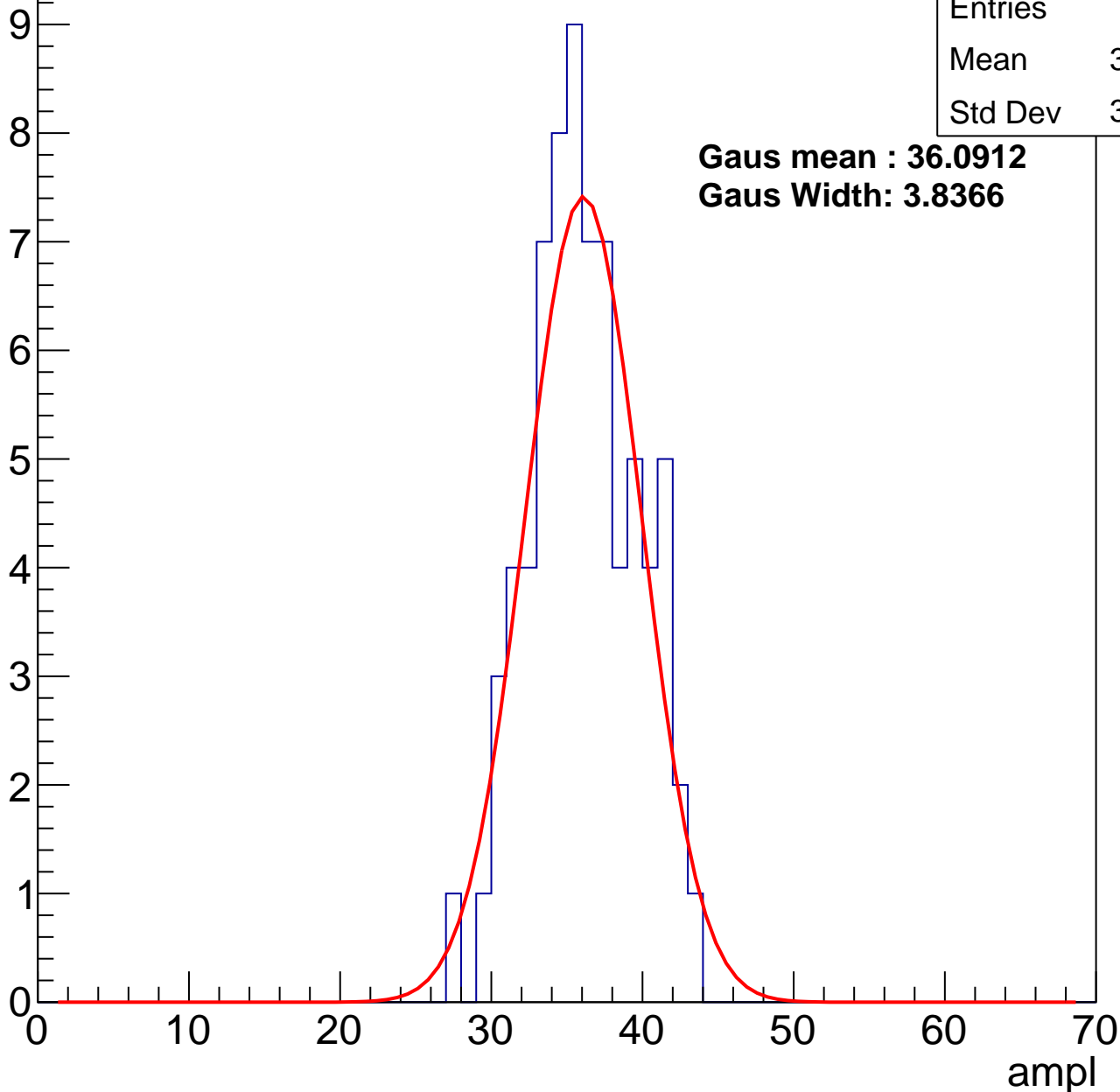
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.64
Std Dev	3.485

Gaus mean : 36.0912

Gaus Width: 3.8366



B1L102S, U14-ch75, adc2

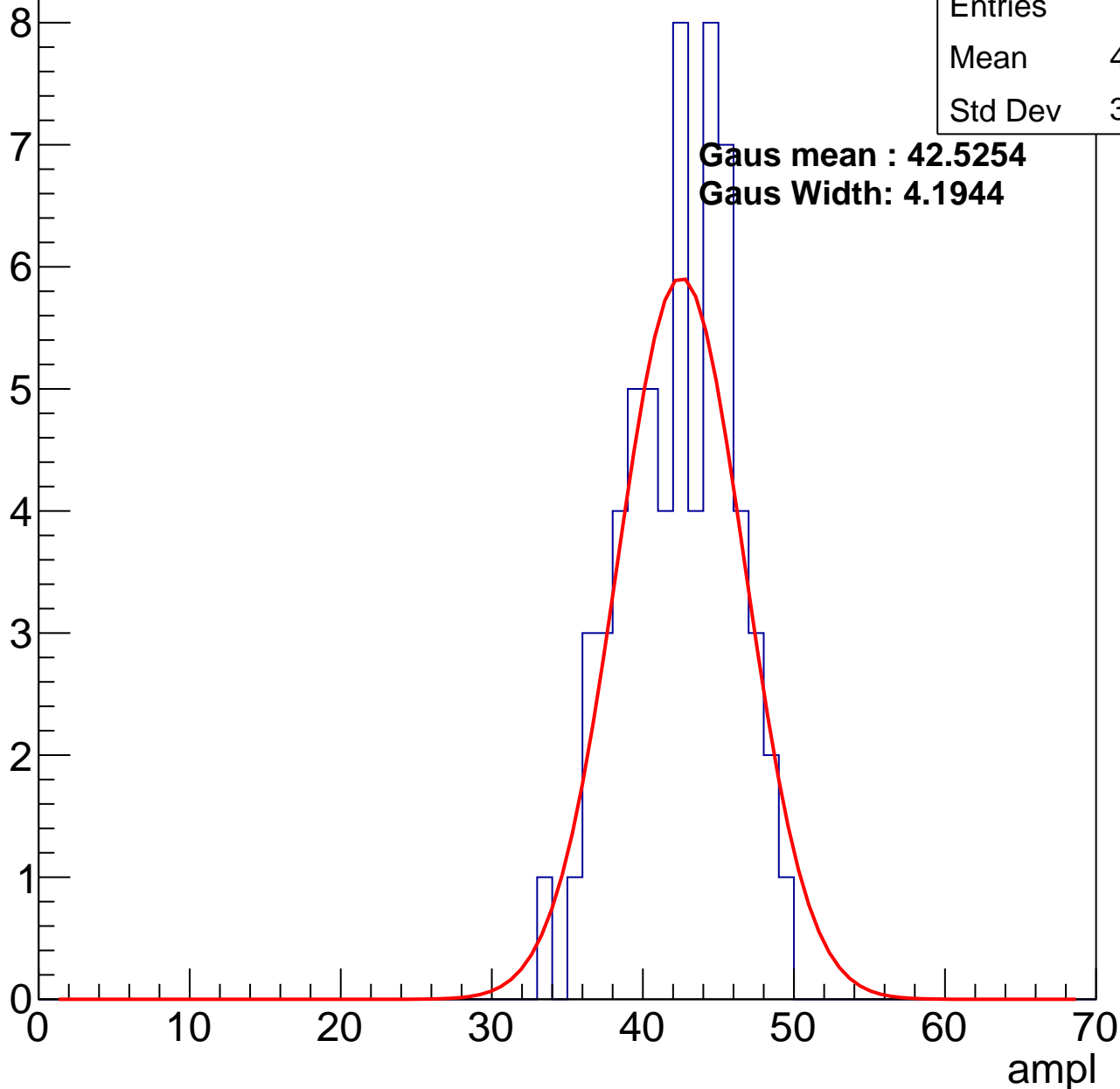
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.95
Std Dev	3.574

Gaus mean : 42.5254

Gaus Width: 4.1944

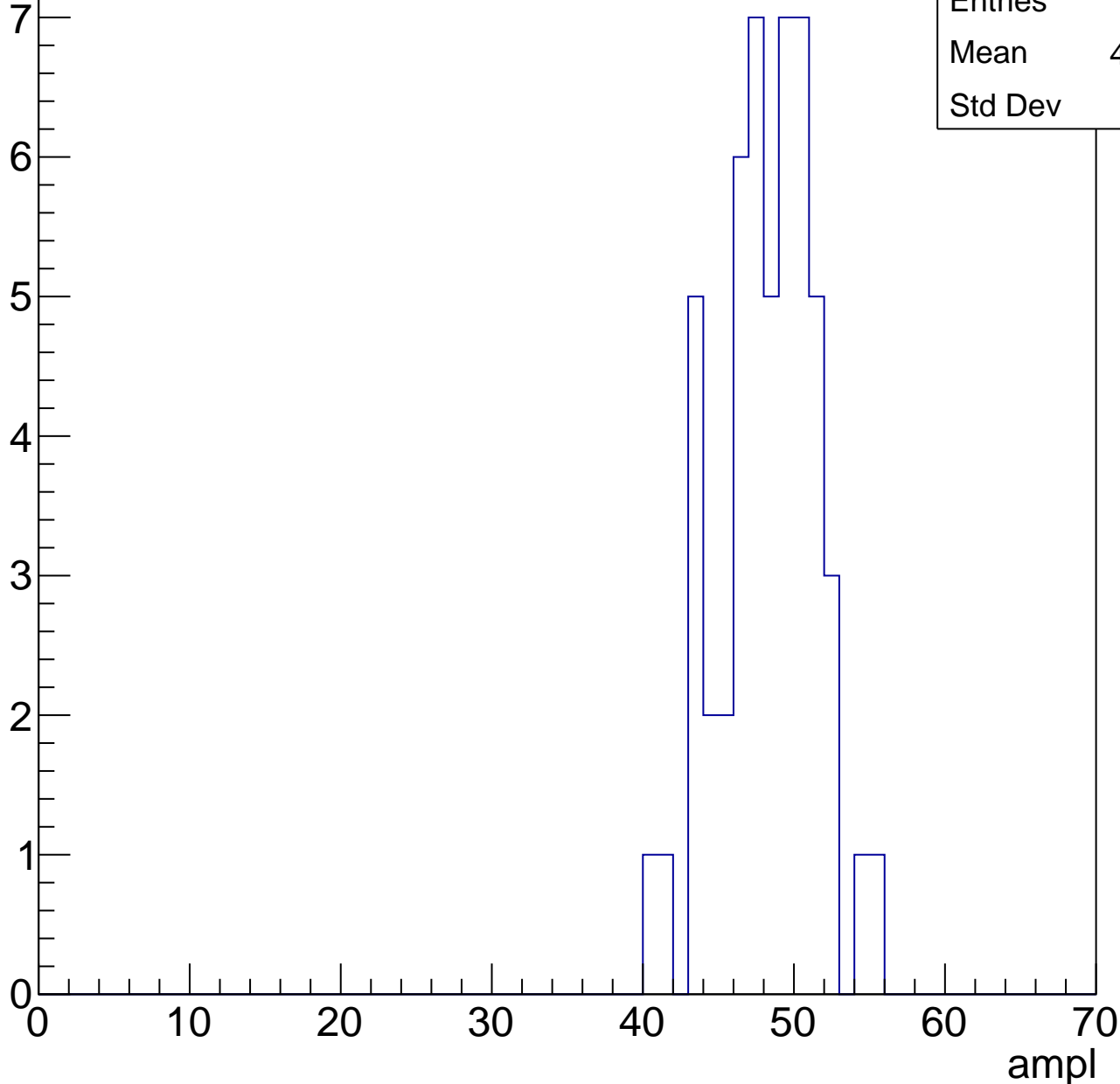


B1L102S, U14-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	47.77
Std Dev	3.16

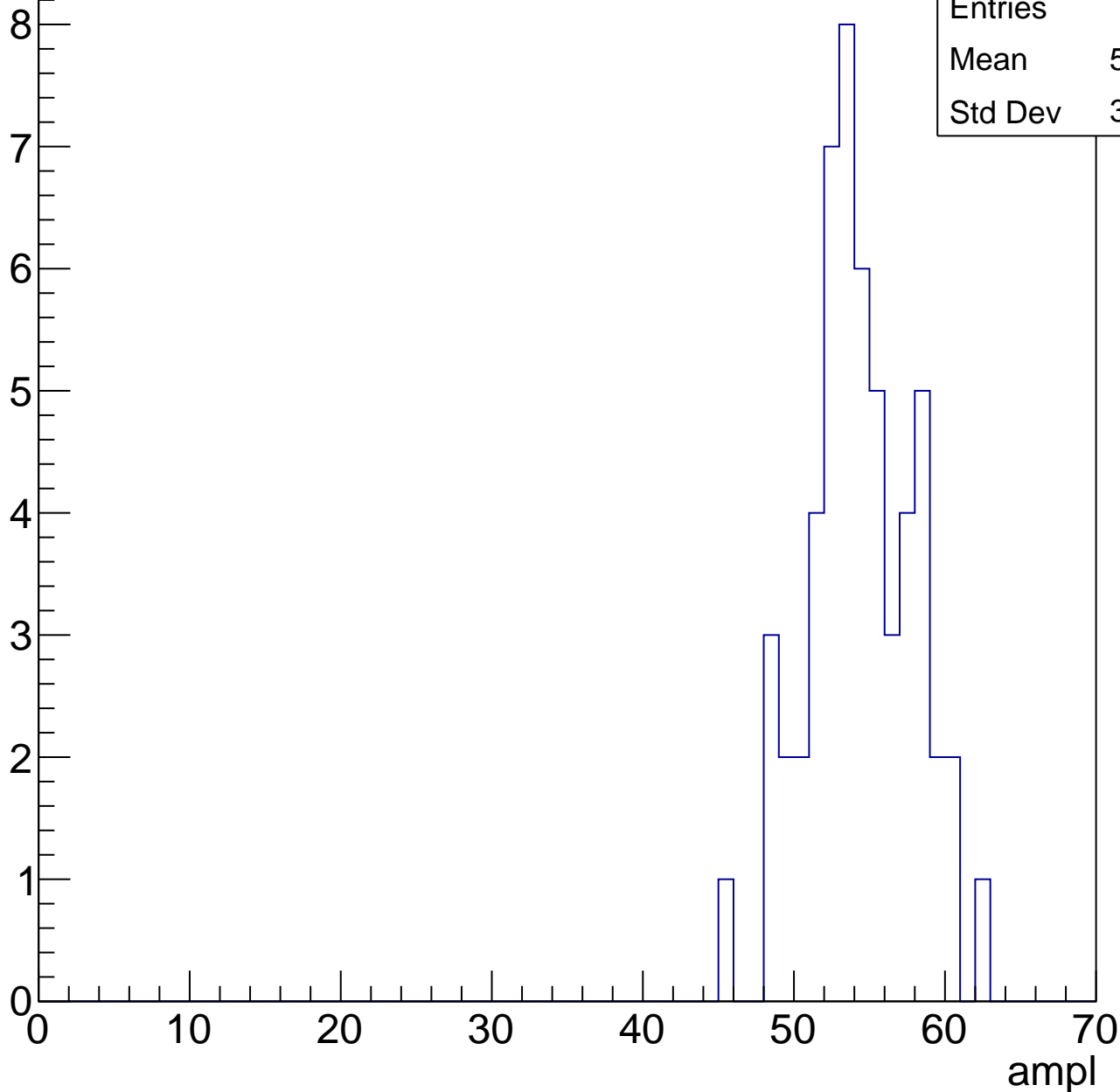


B1L102S, U14-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	53.89
Std Dev	3.468



B1L102S, U14-ch75, adc5

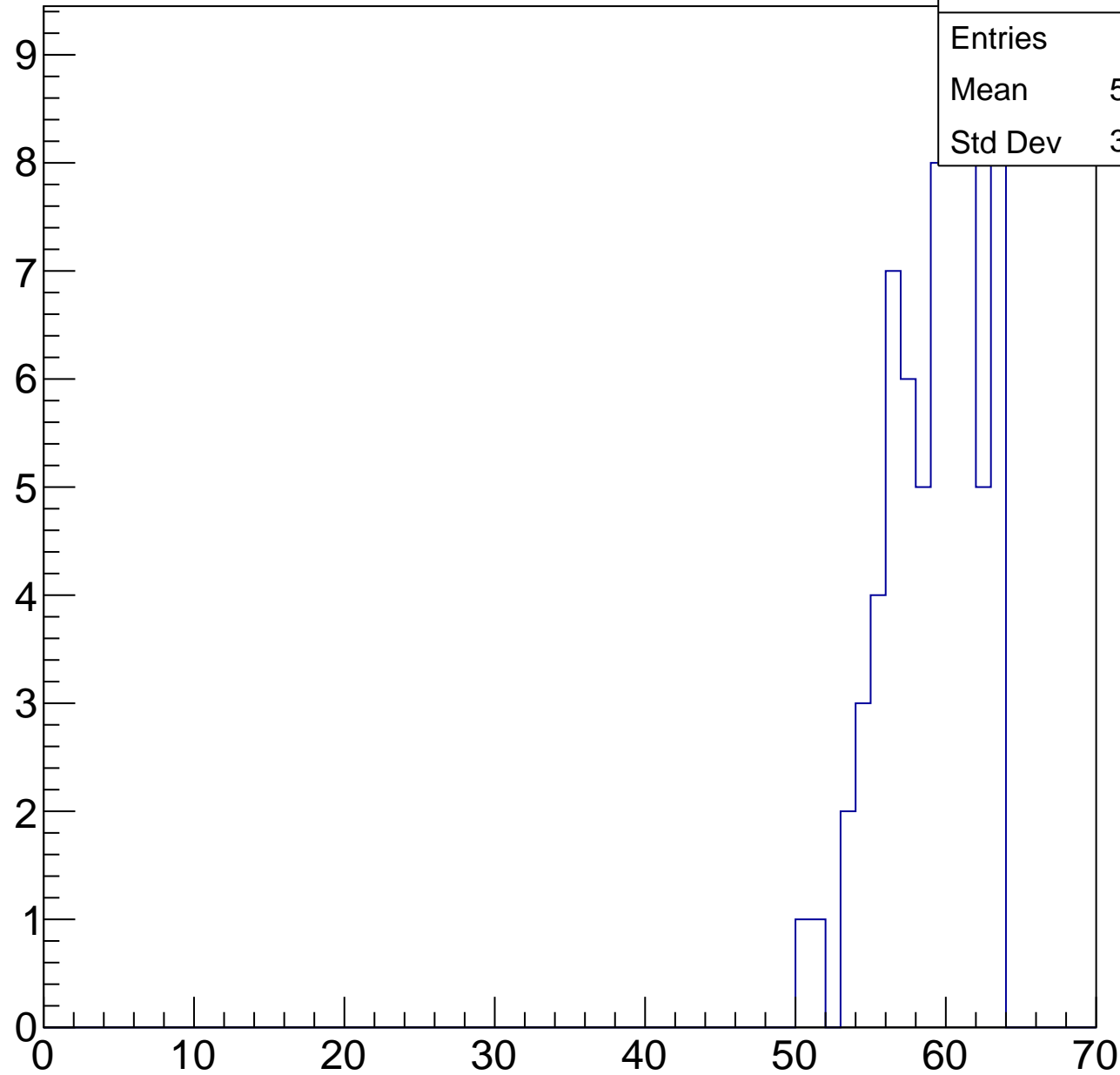
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	58.63
Std Dev	3.128

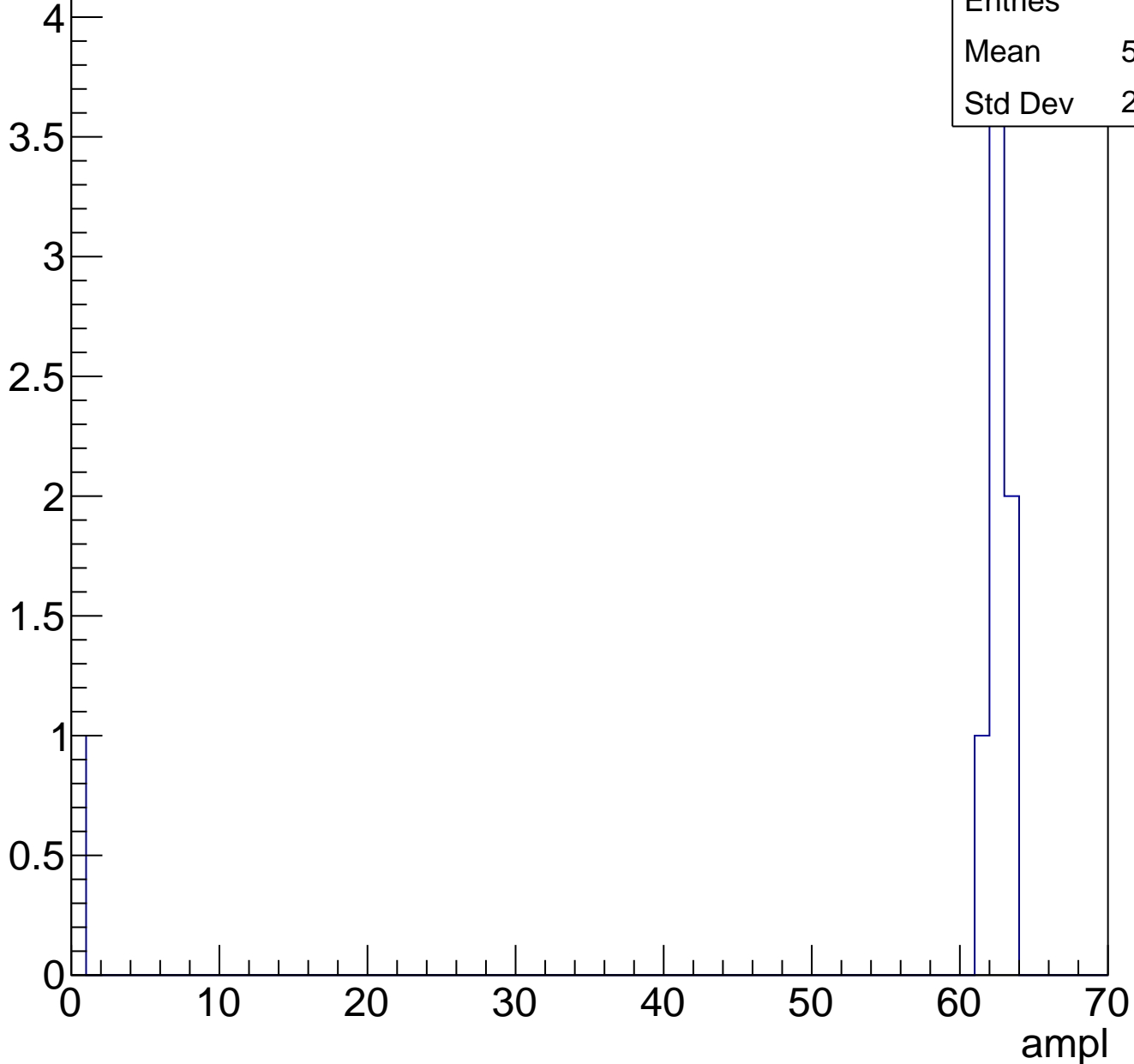
ampl



B1L102S, U14-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U14-ch76, adc0

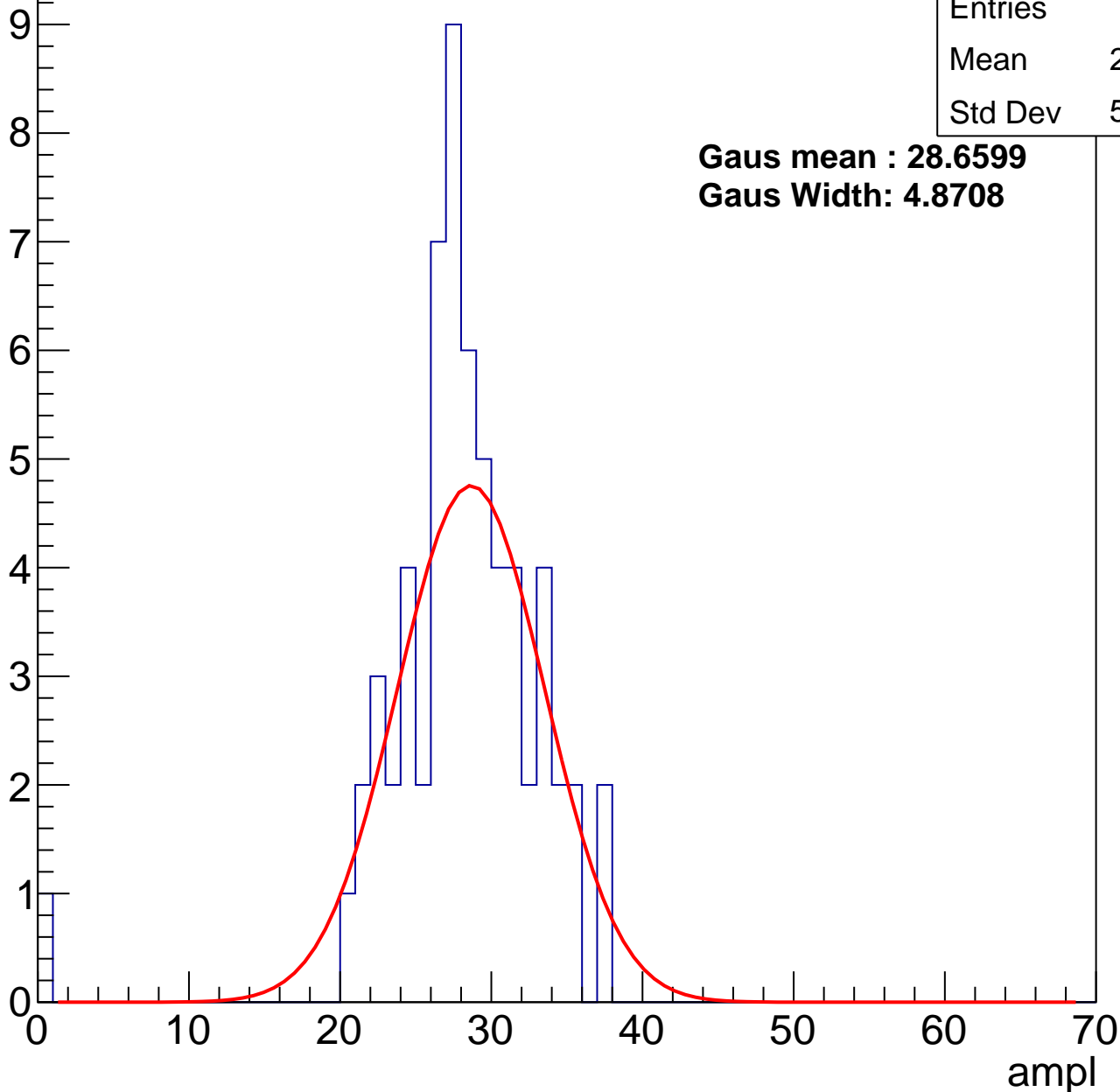
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.58
Std Dev	5.287

Gaus mean : 28.6599

Gaus Width: 4.8708



B1L102S, U14-ch76, adc1

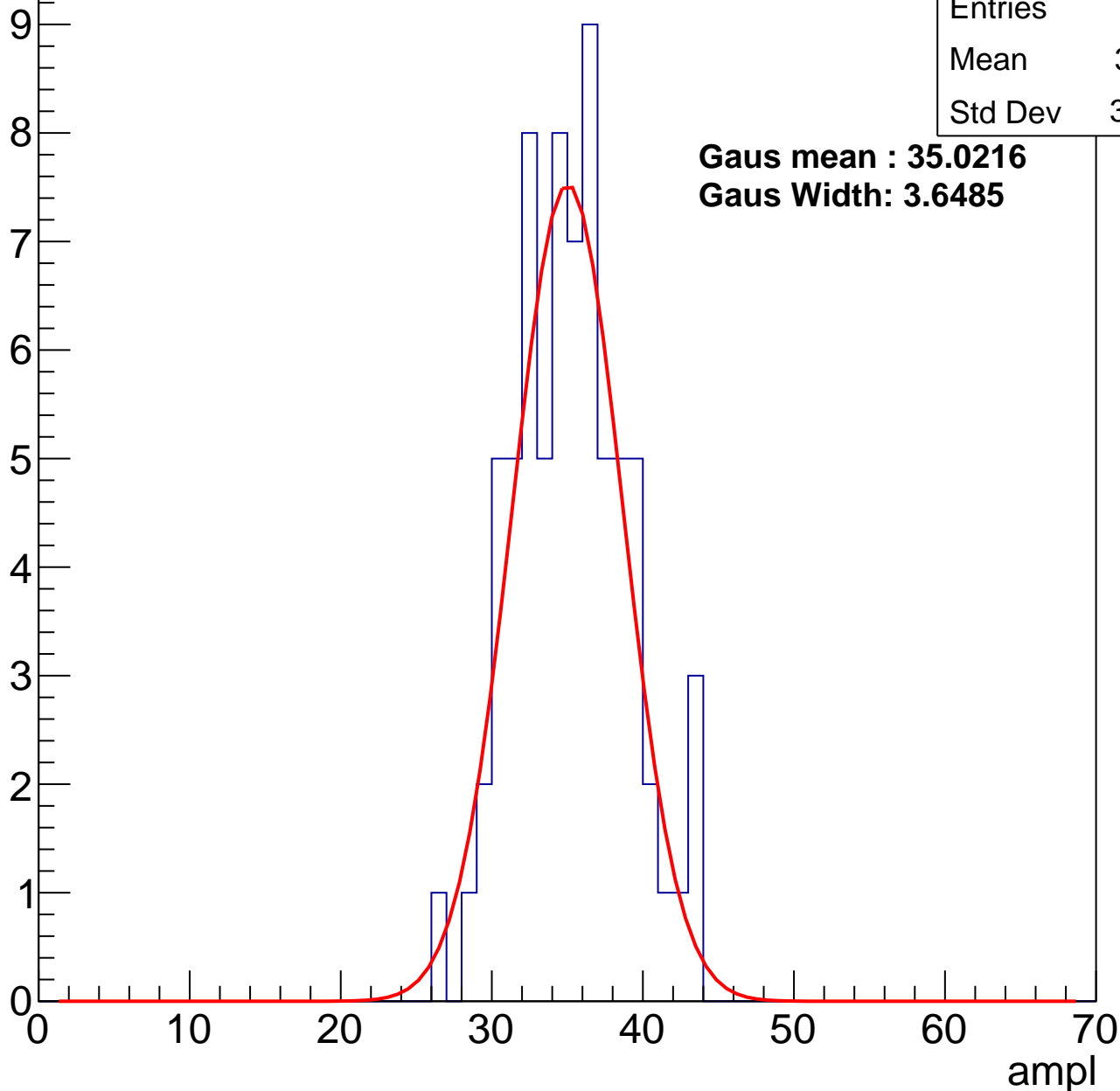
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.81
Std Dev	3.674

Gaus mean : 35.0216

Gaus Width: 3.6485



B1L102S, U14-ch76, adc2

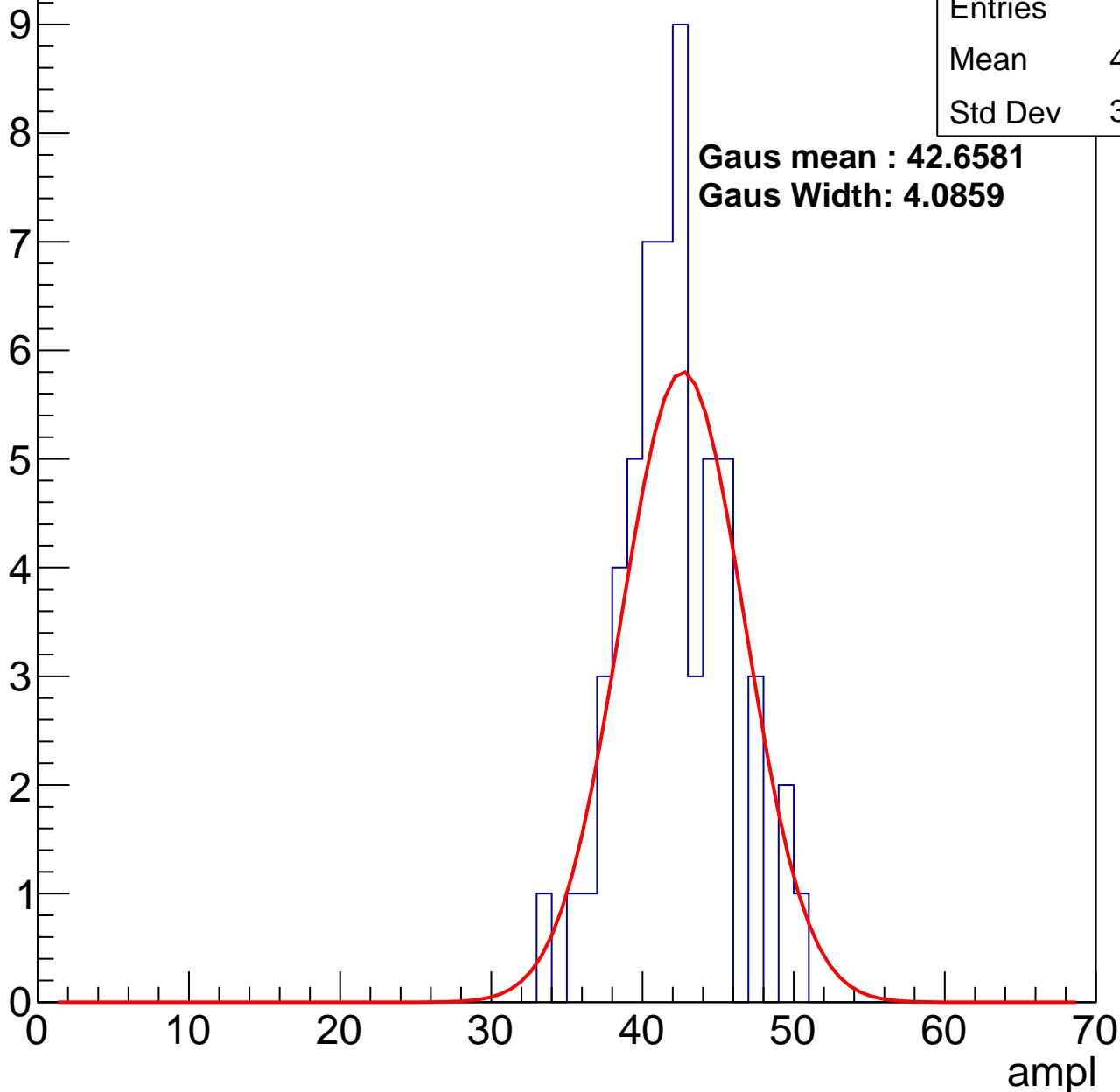
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.58
Std Dev	3.454

Gaus mean : 42.6581

Gaus Width: 4.0859

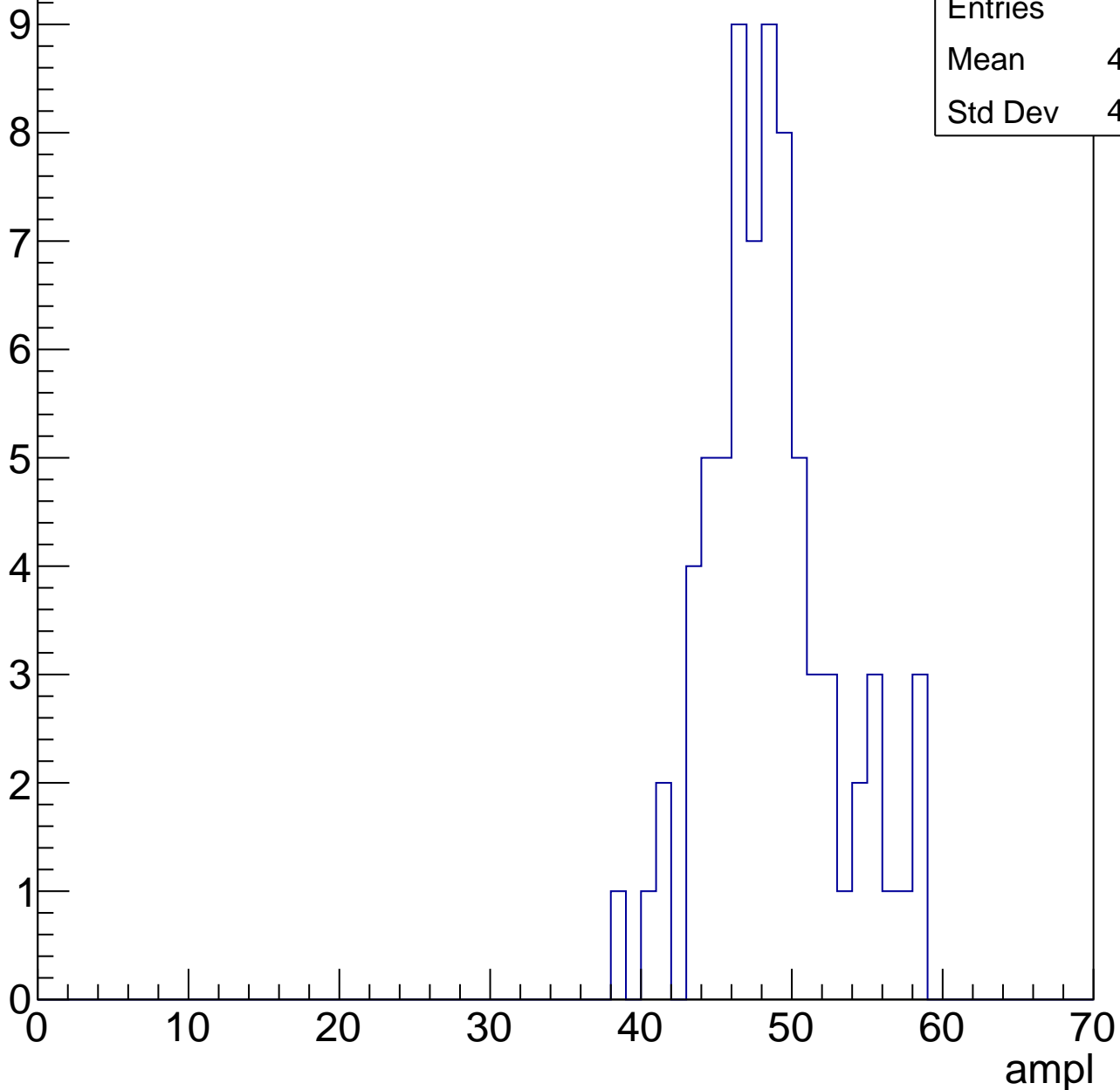


B1L102S, U14-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	48.16
Std Dev	4.252

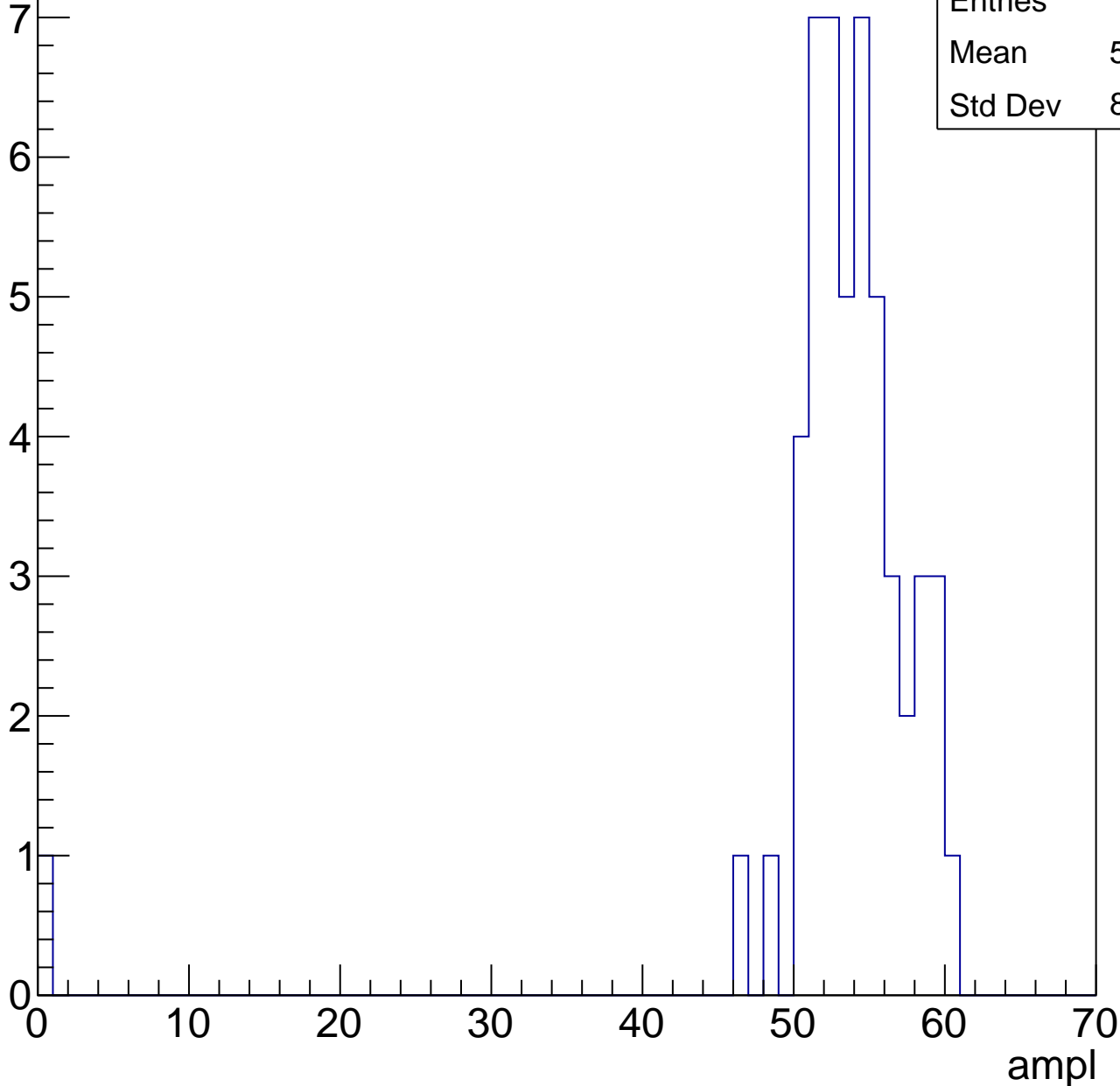


B1L102S, U14-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

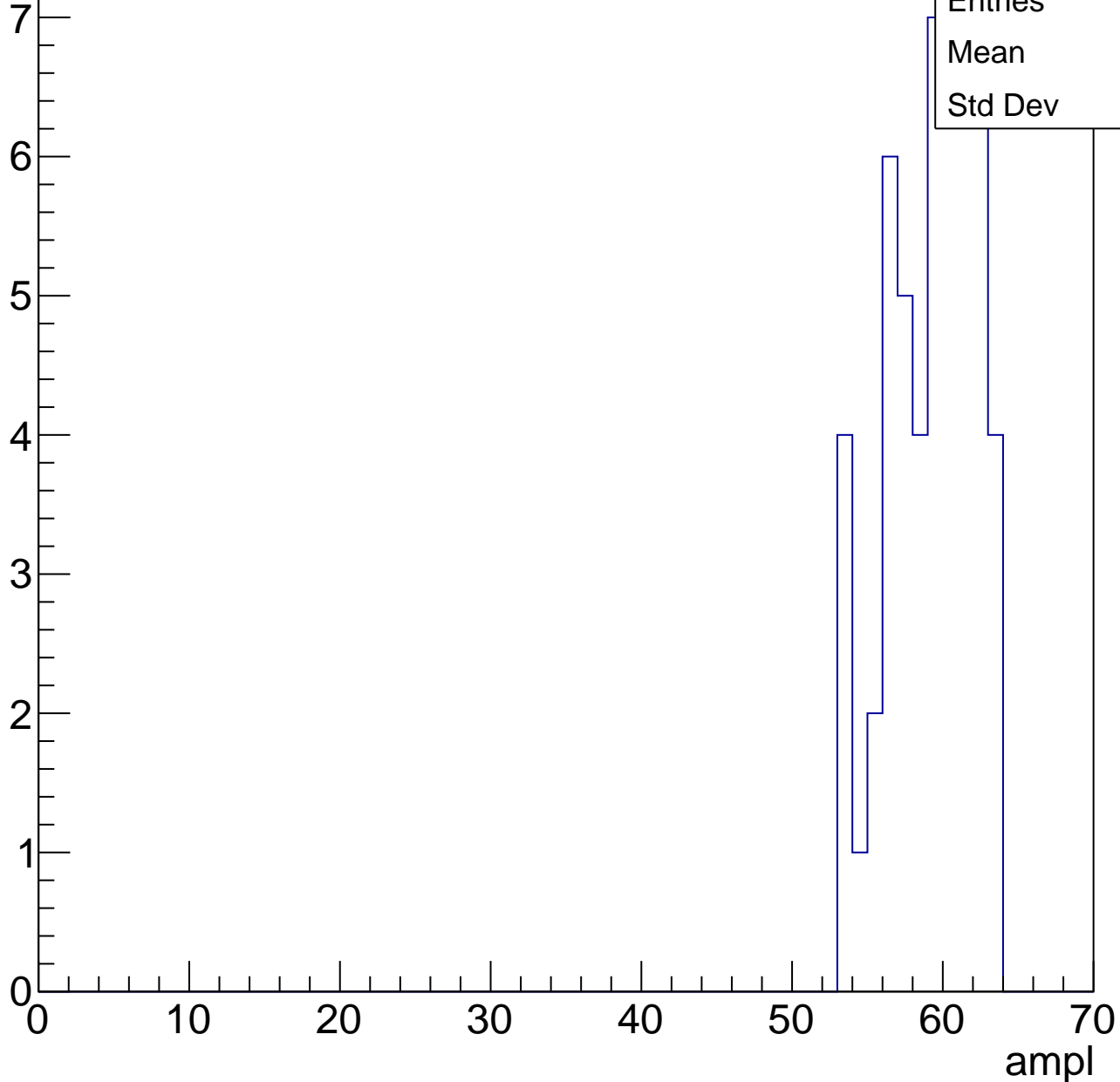
Entries	50
Mean	52.52
Std Dev	8.075



B1L102S, U14-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

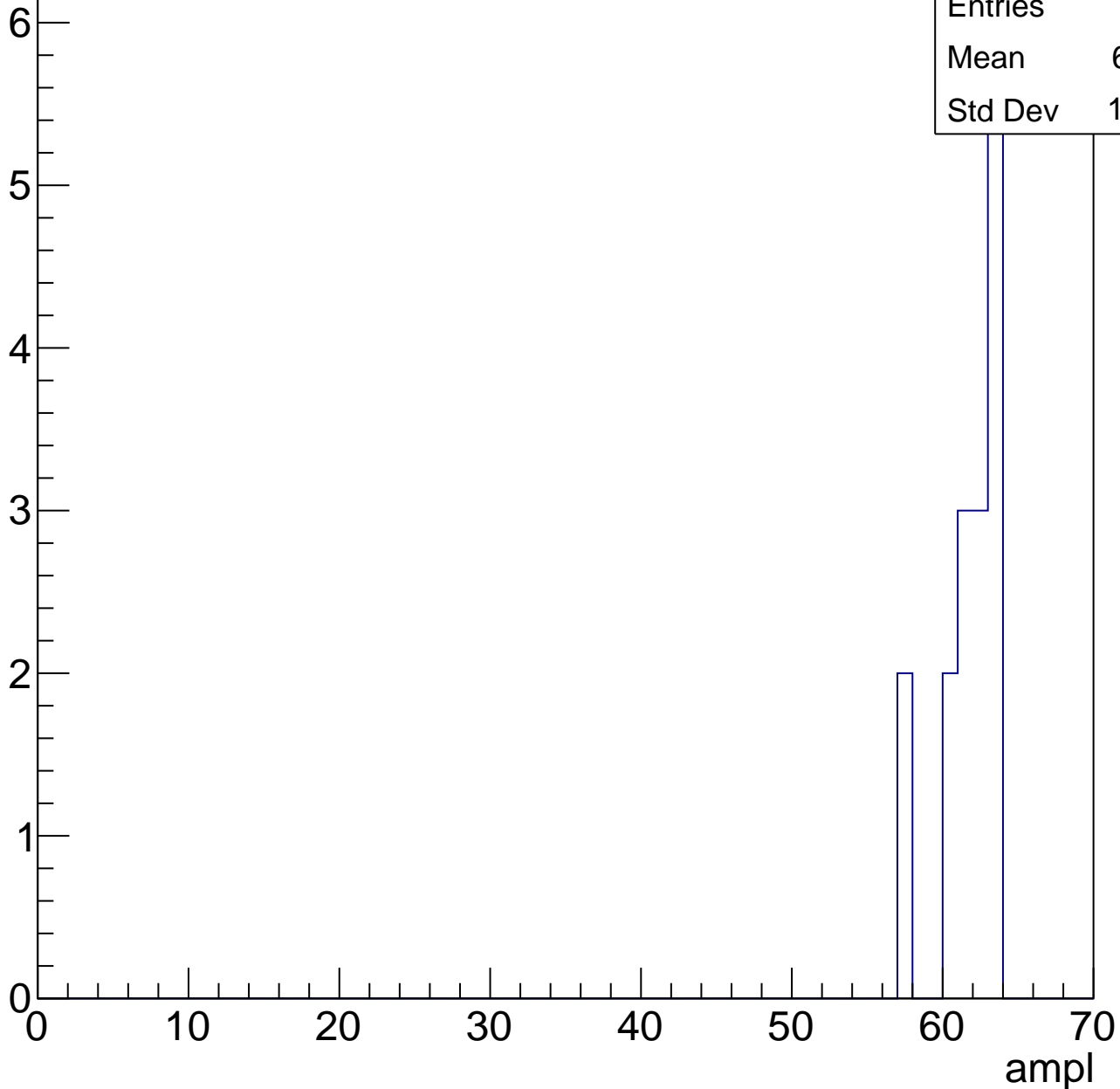


B1L102S, U14-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.31
Std Dev	1.927



B1L102S, U14-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch77, adc0

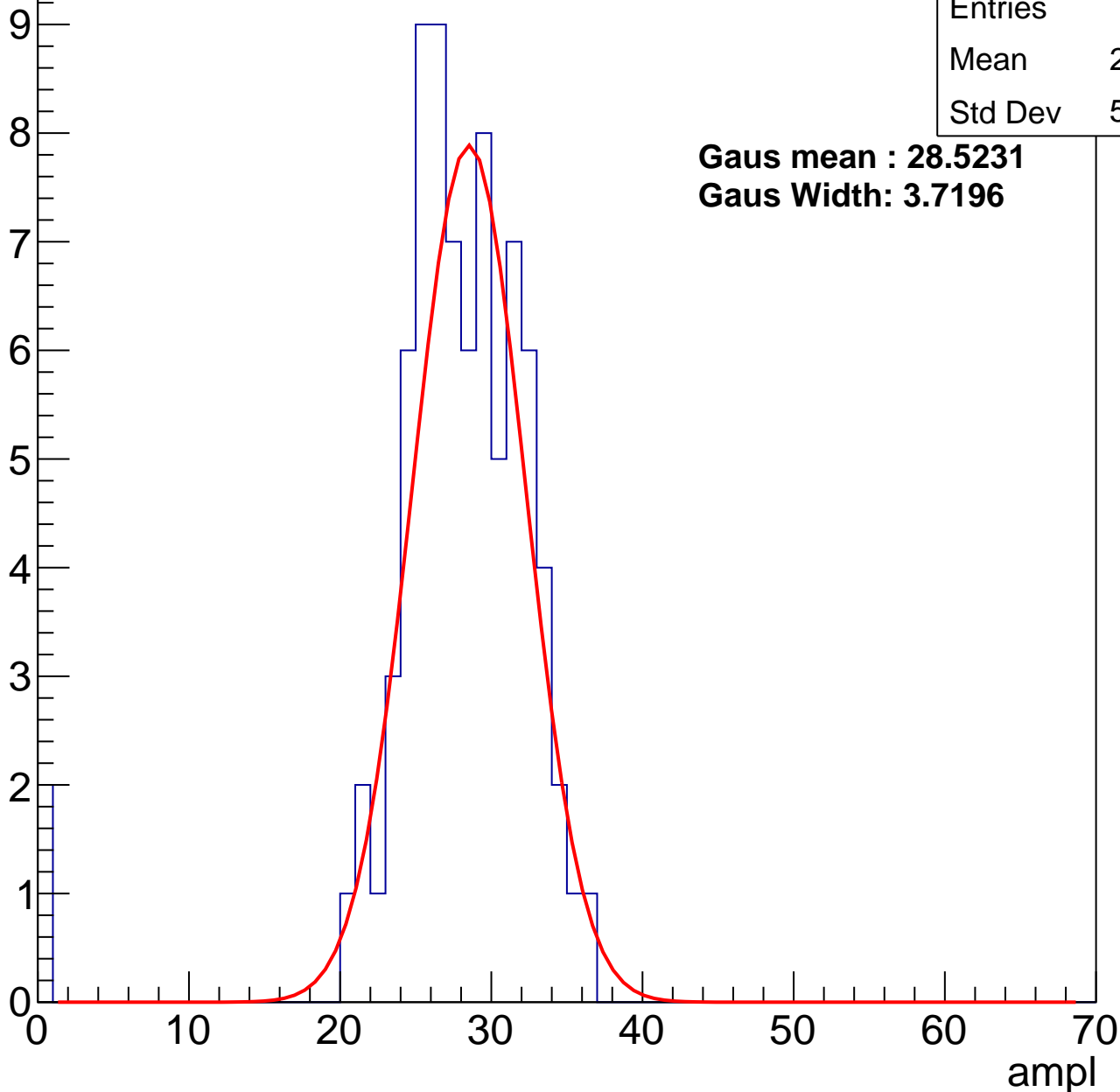
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.19
Std Dev	5.568

Gaus mean : 28.5231

Gaus Width: 3.7196



B1L102S, U14-ch77, adc1

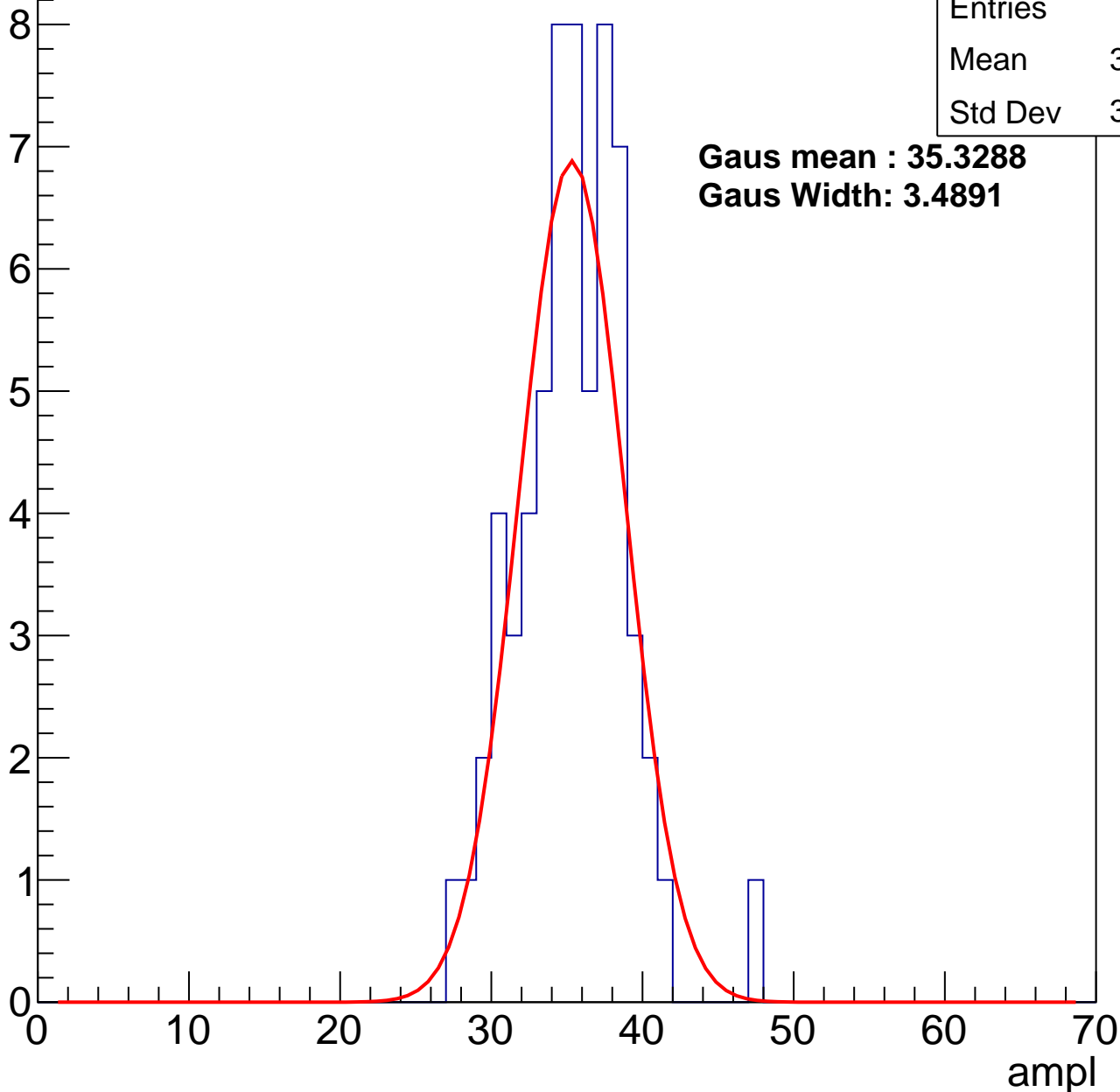
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.89
Std Dev	3.506

Gaus mean : 35.3288

Gaus Width: 3.4891



B1L102S, U14-ch77, adc2

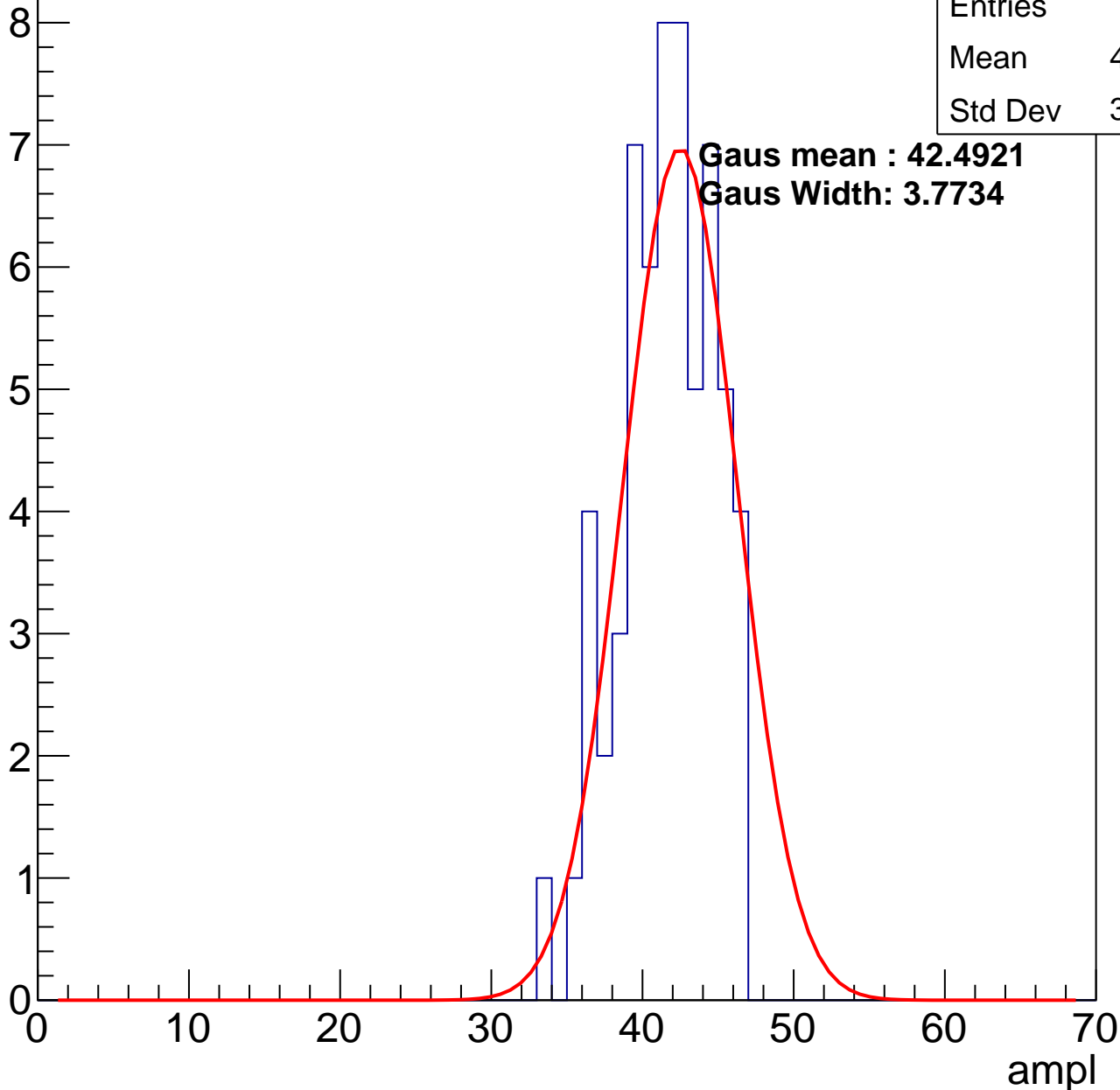
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.13
Std Dev	3.038

Gaus mean : 42.4921

Gaus Width: 3.7734

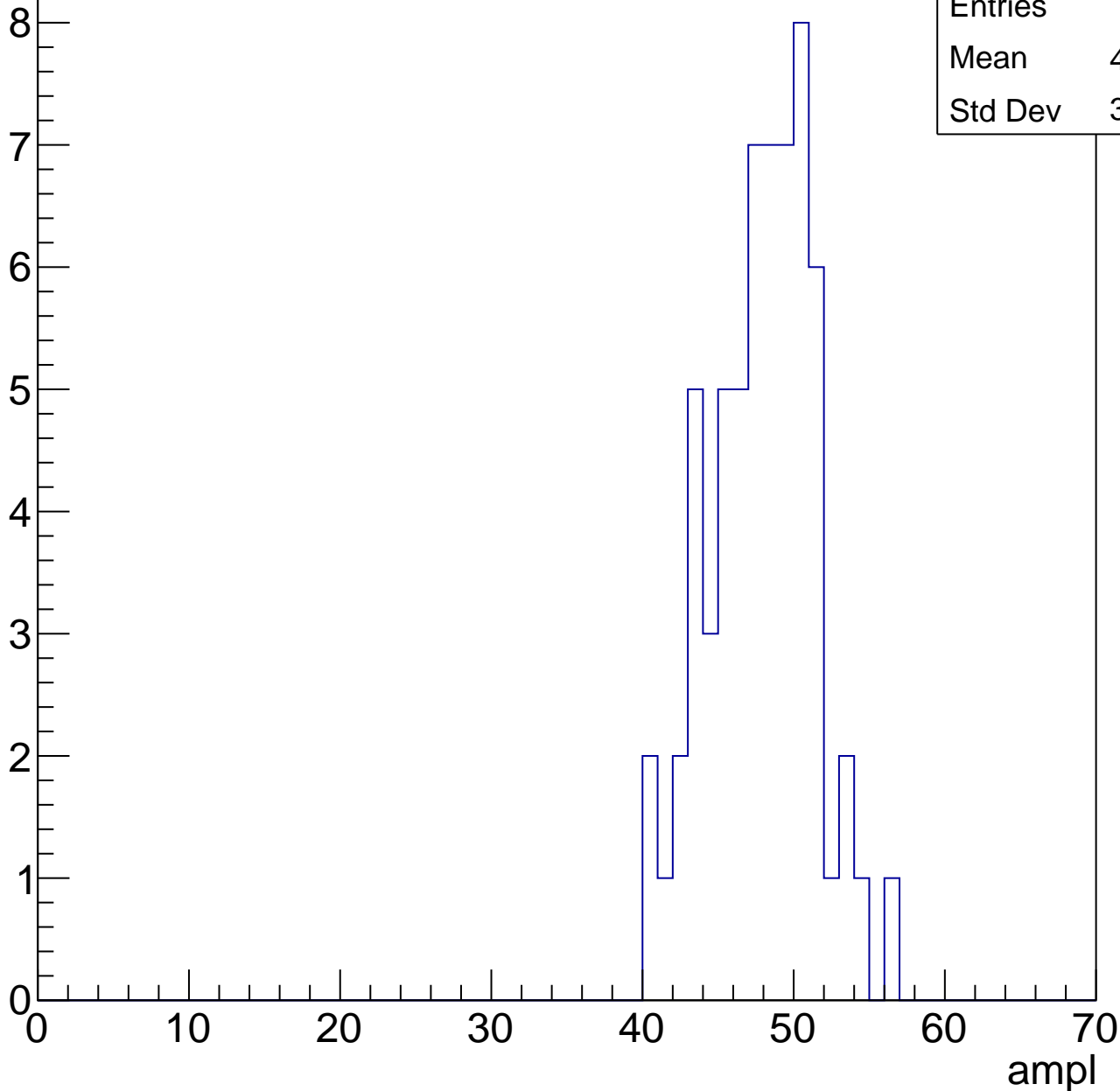


B1L102S, U14-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.44
Std Dev	3.412

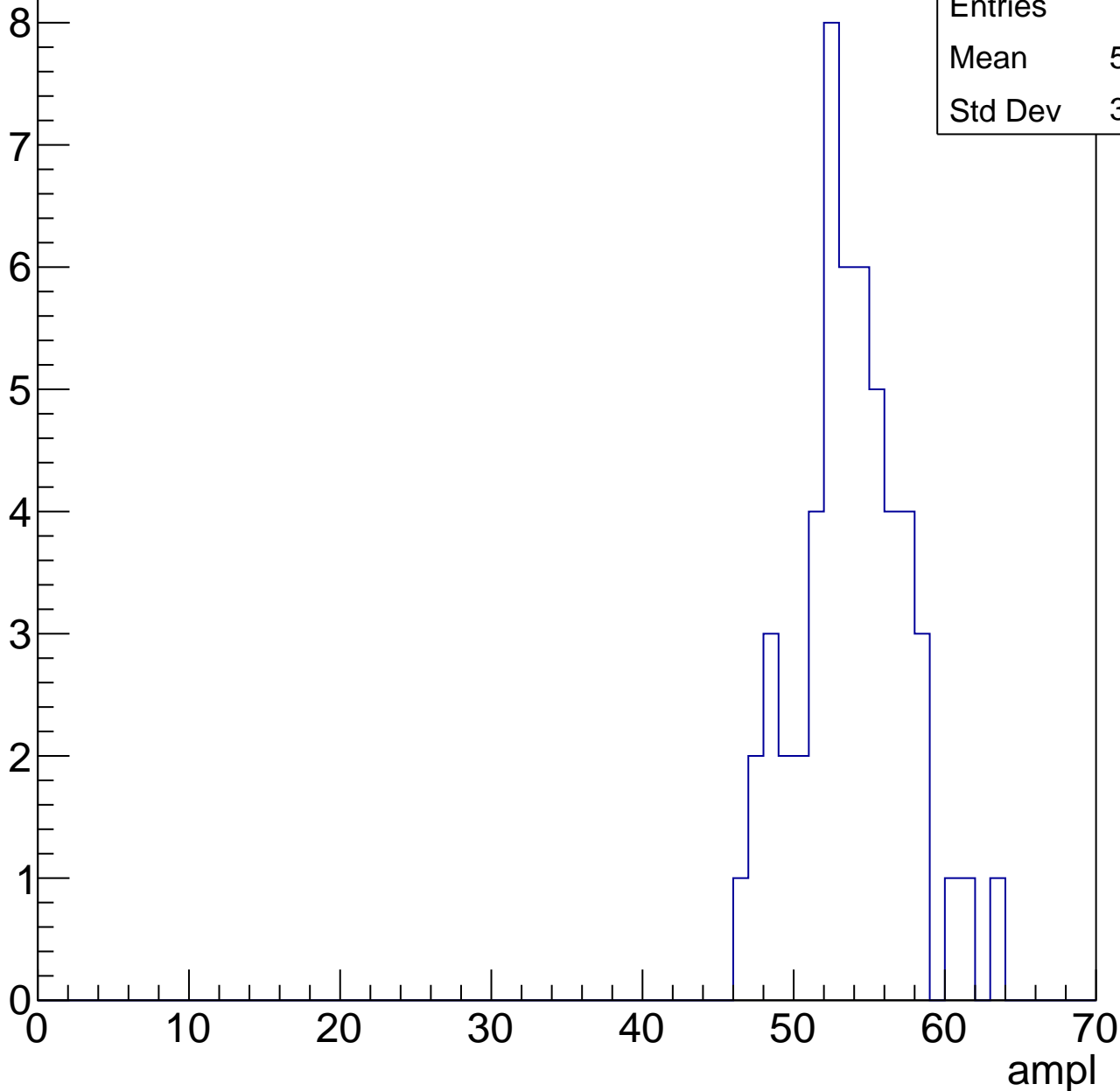


B1L102S, U14-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	53.38
Std Dev	3.573



B1L102S, U14-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	57.63
Std Dev	7.707

Entry

10

8

6

4

2

0

0

10

20

30

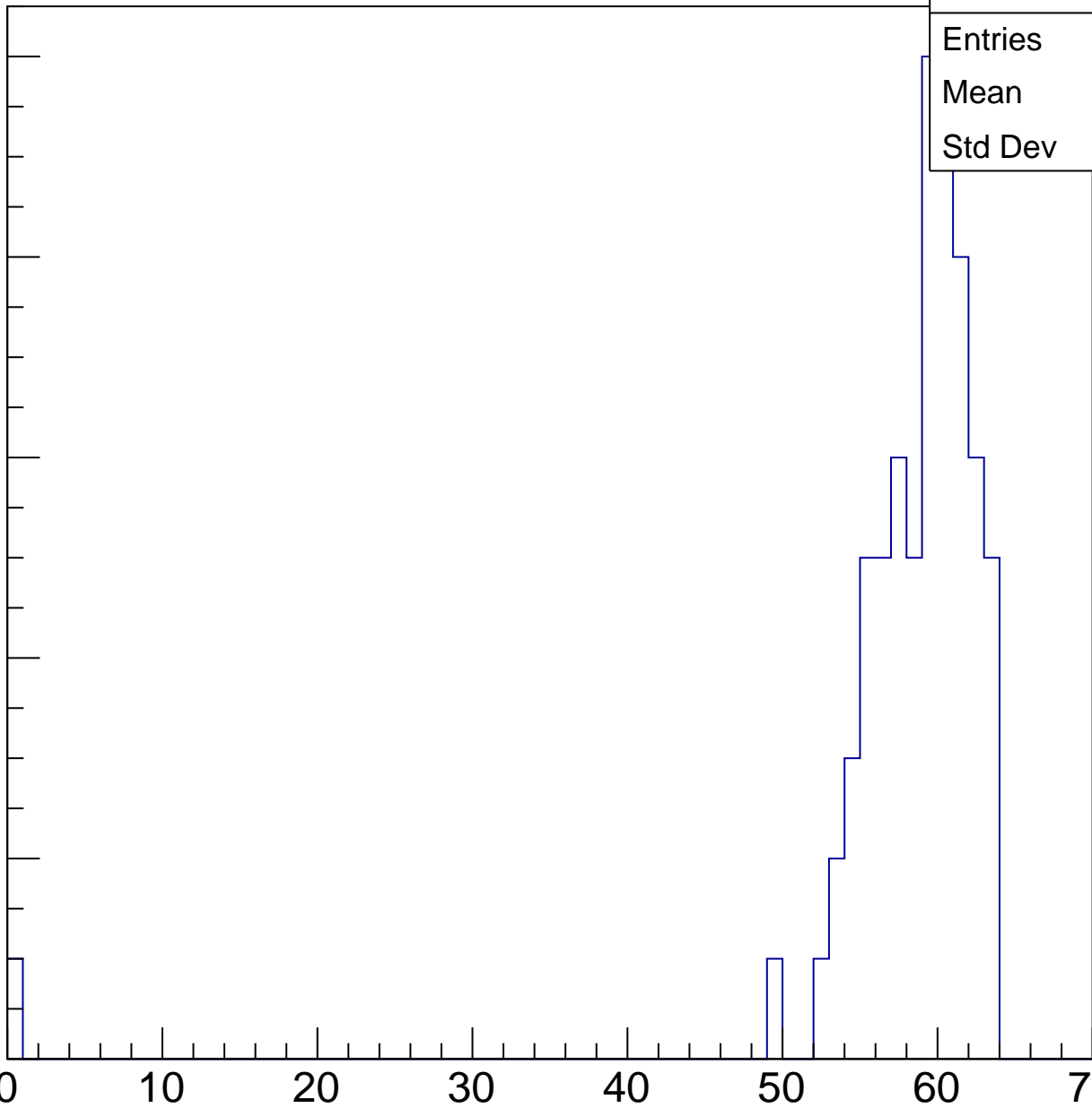
40

50

60

70

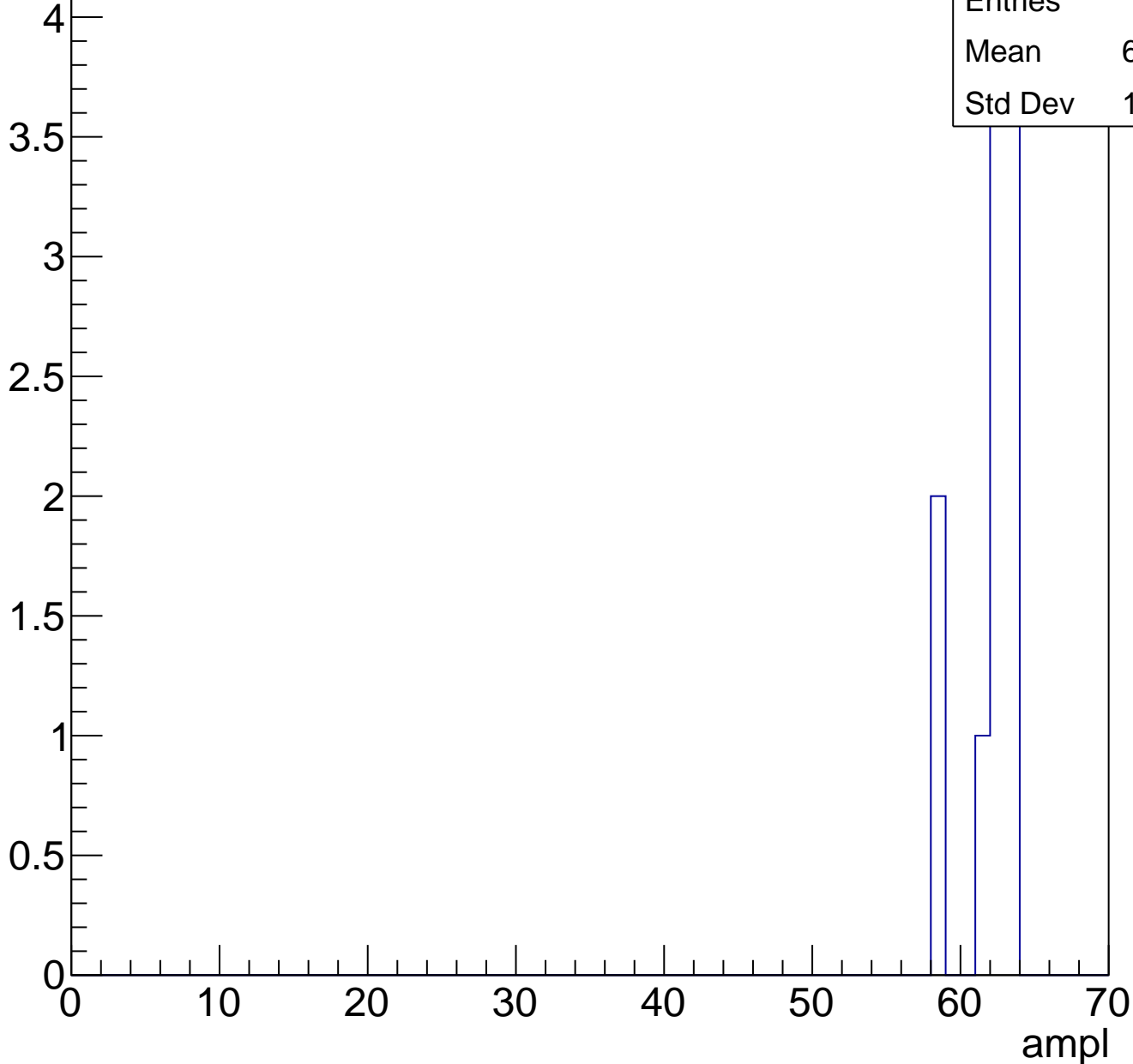
ampl



B1L102S, U14-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch78, adc0

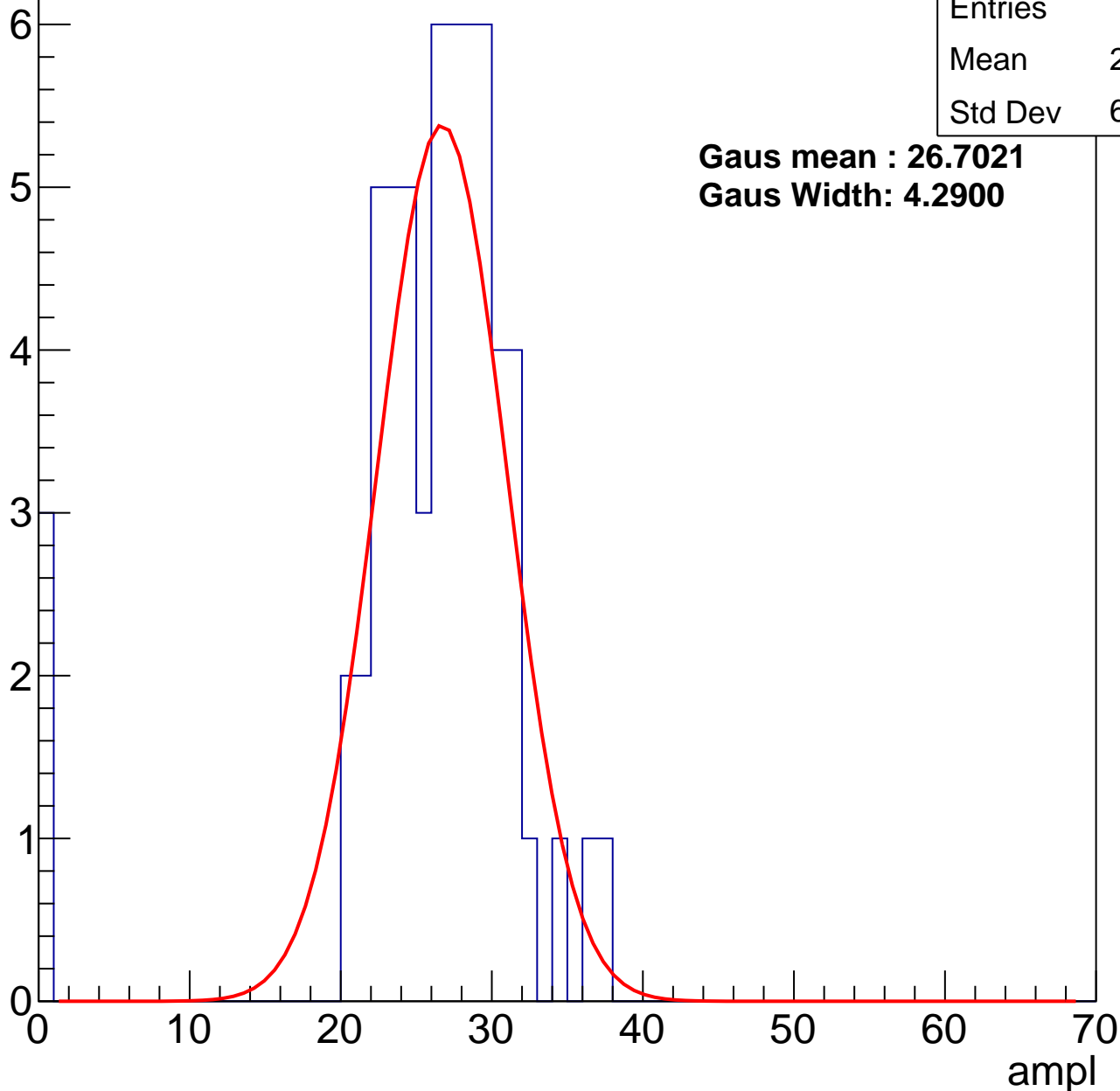
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	25.33
Std Dev	6.823

Gaus mean : 26.7021

Gaus Width: 4.2900



B1L102S, U14-ch78, adc1

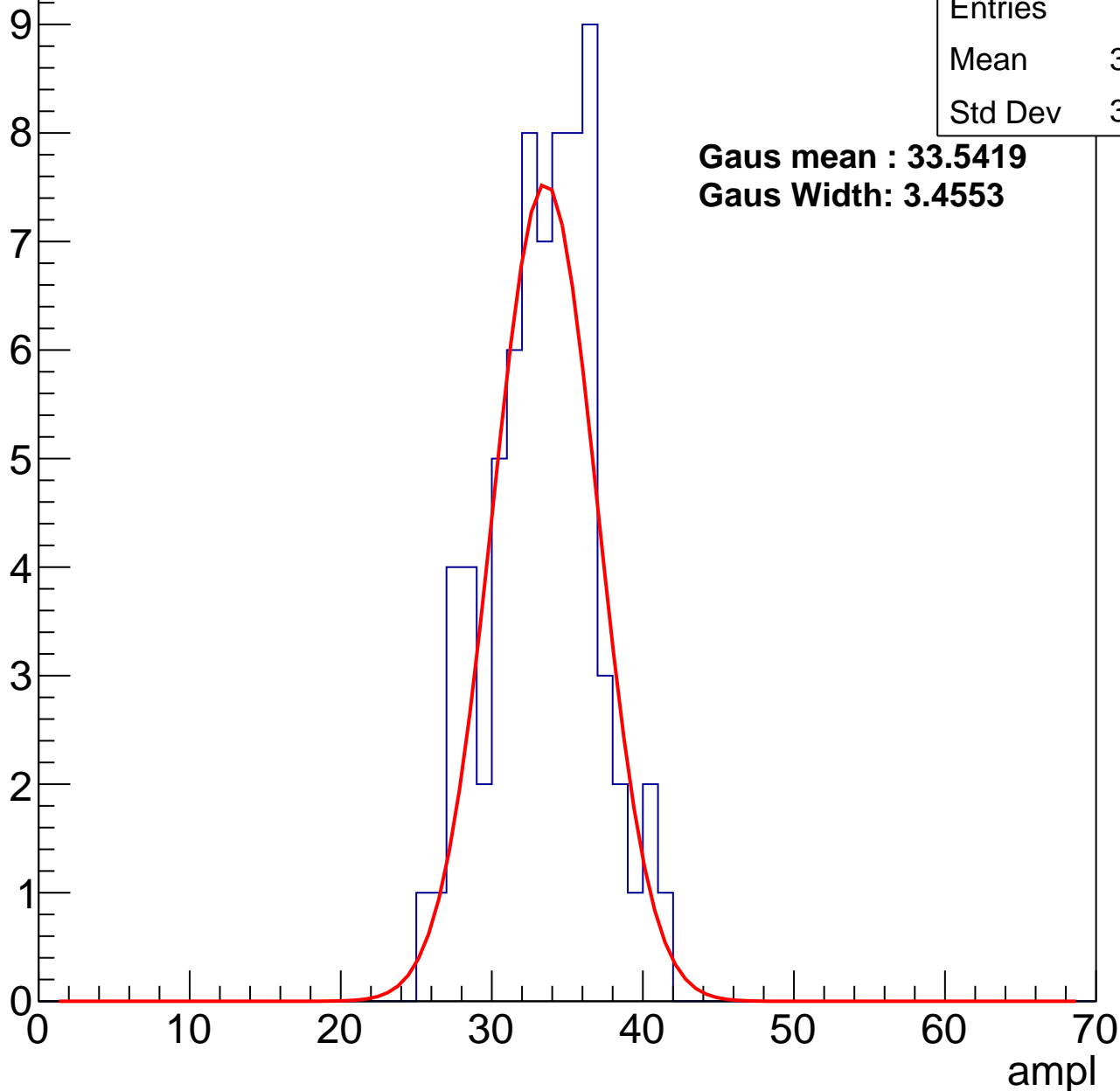
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	32.99
Std Dev	3.502

Gaus mean : 33.5419

Gaus Width: 3.4553



B1L102S, U14-ch78, adc2

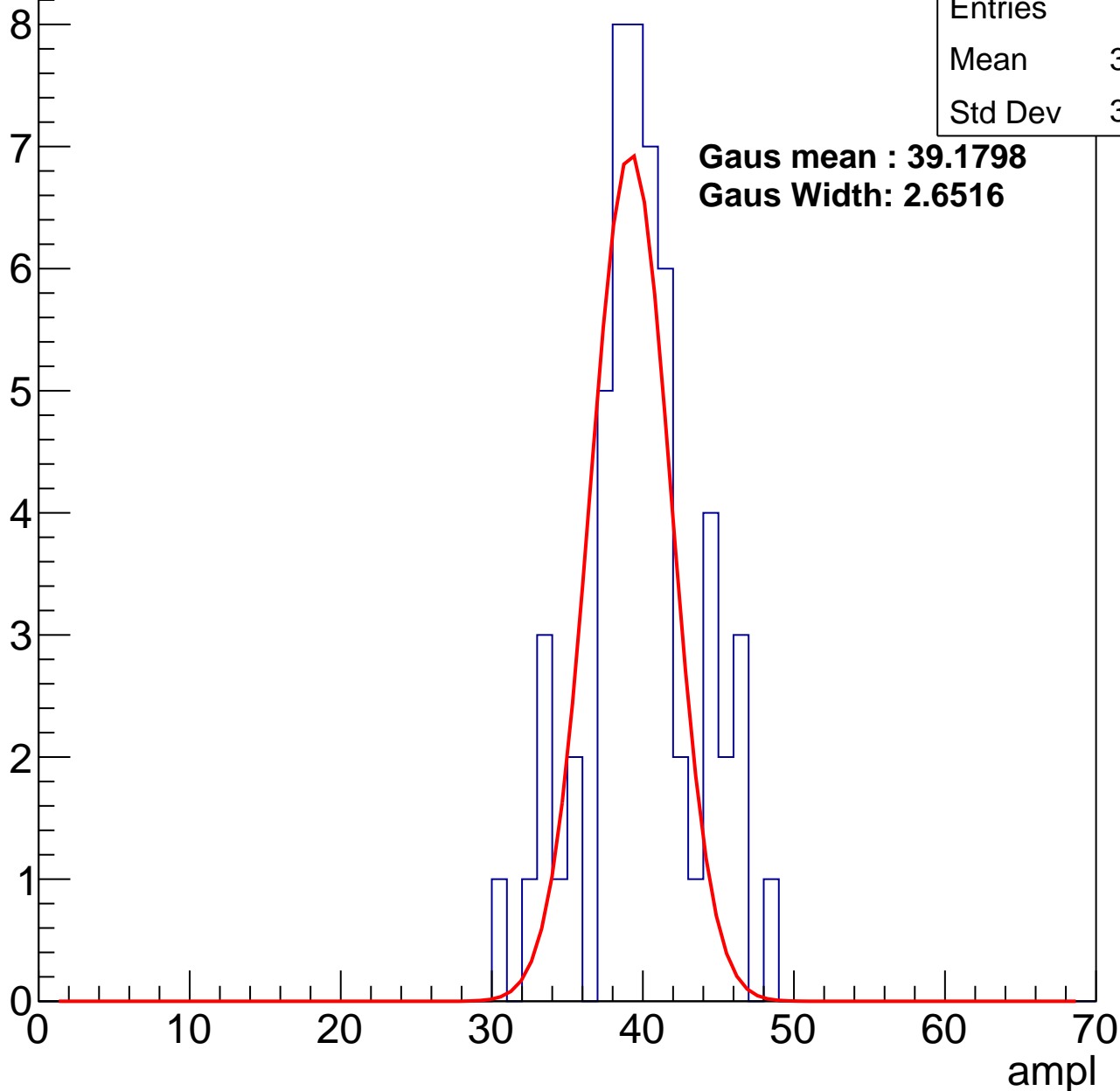
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	39.47
Std Dev	3.756

Gaus mean : 39.1798

Gaus Width: 2.6516

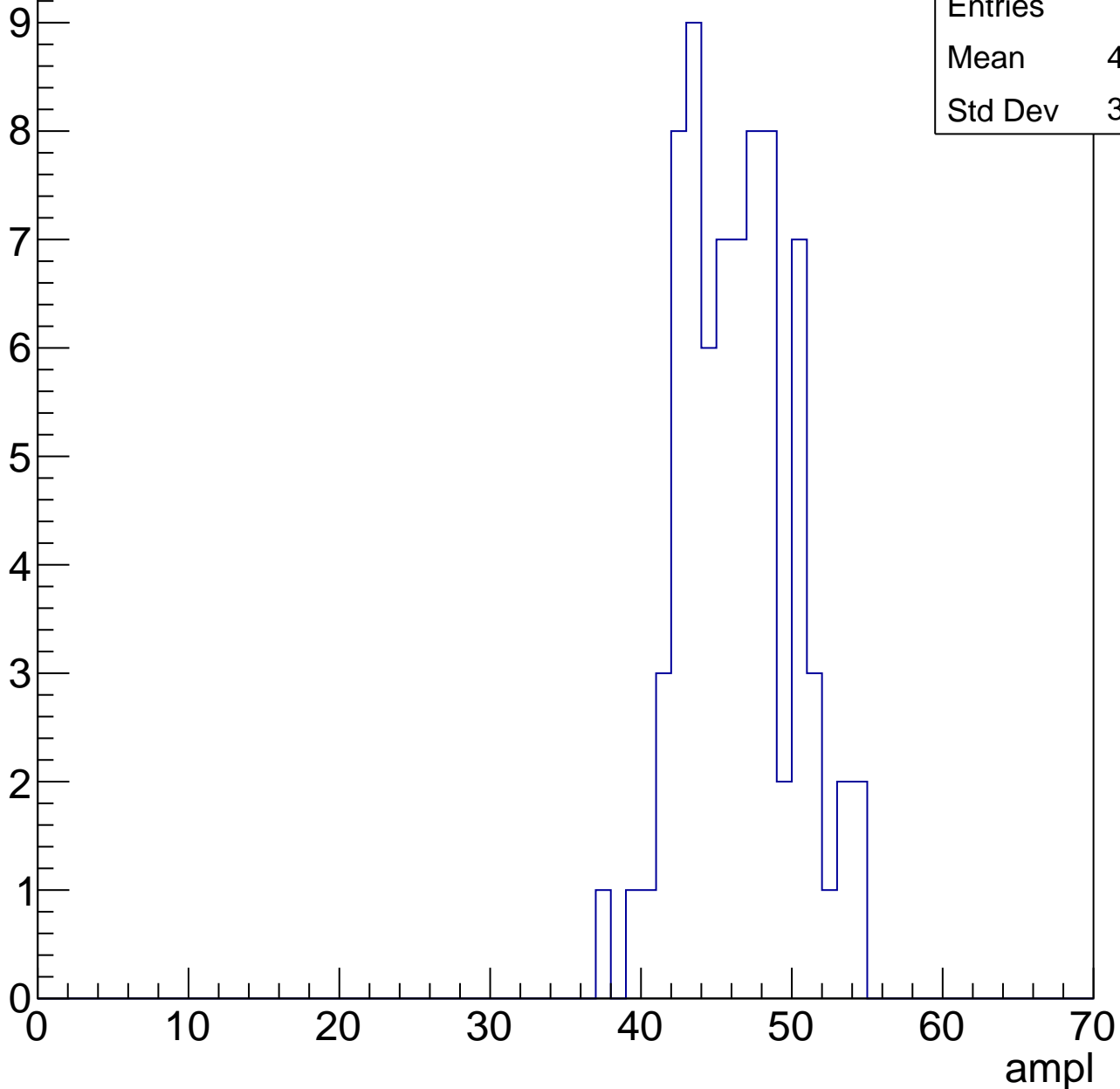


B1L102S, U14-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	45.92
Std Dev	3.605

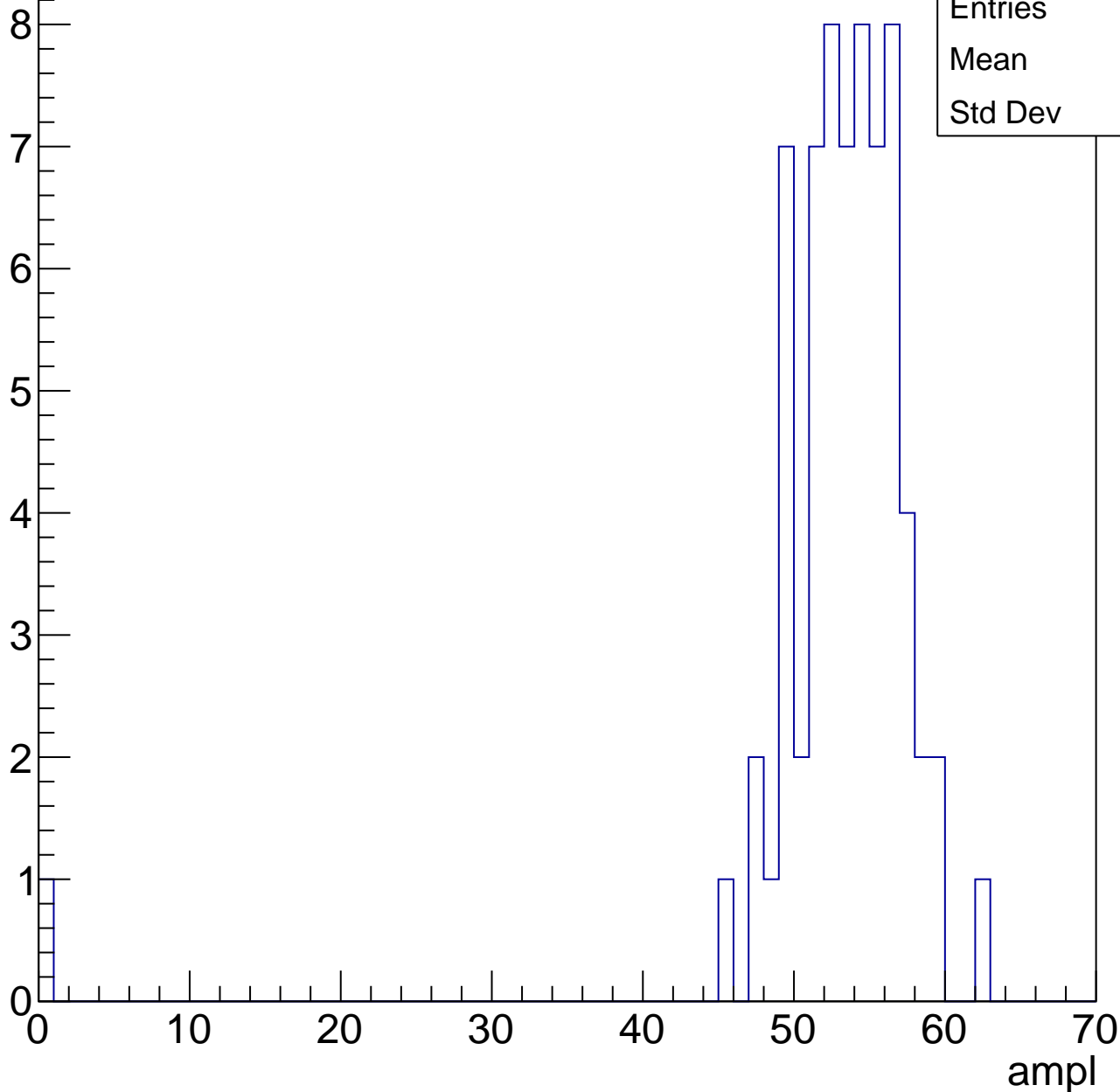


B1L102S, U14-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	52.4
Std Dev	7.16

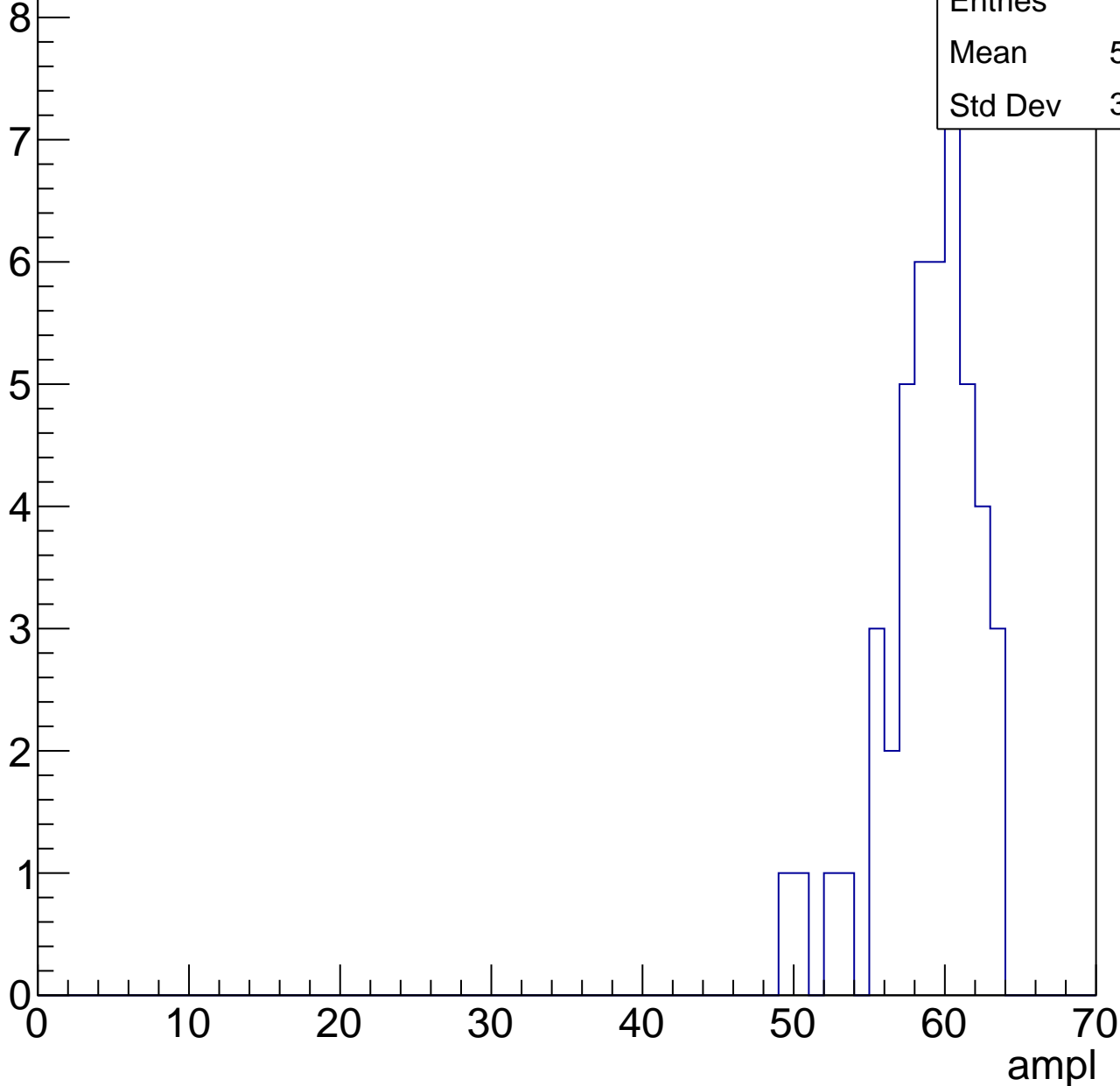


B1L102S, U14-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.48
Std Dev	3.154

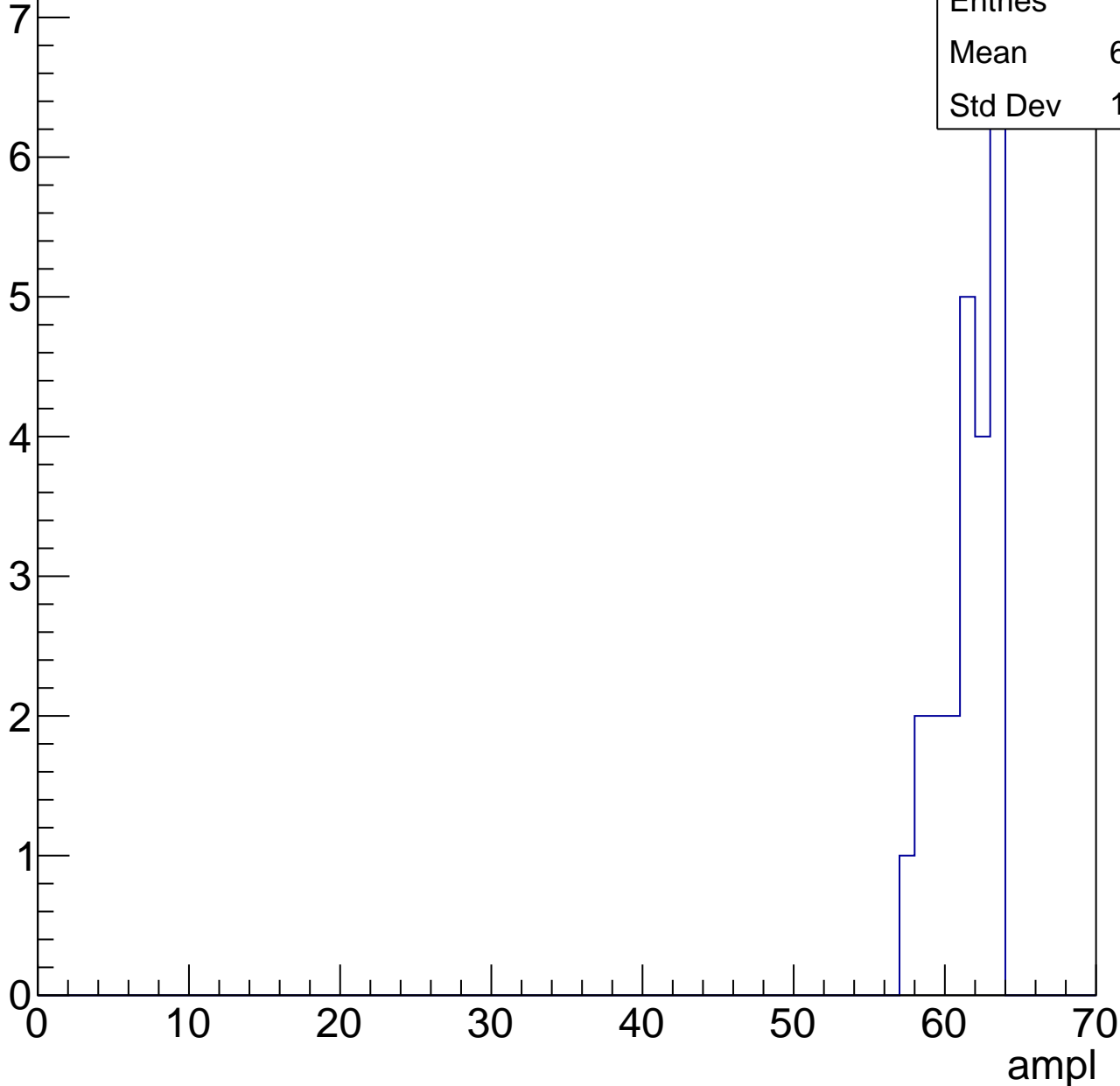


B1L102S, U14-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	61.09
Std Dev	1.816



B1L102S, U14-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch79, adc0

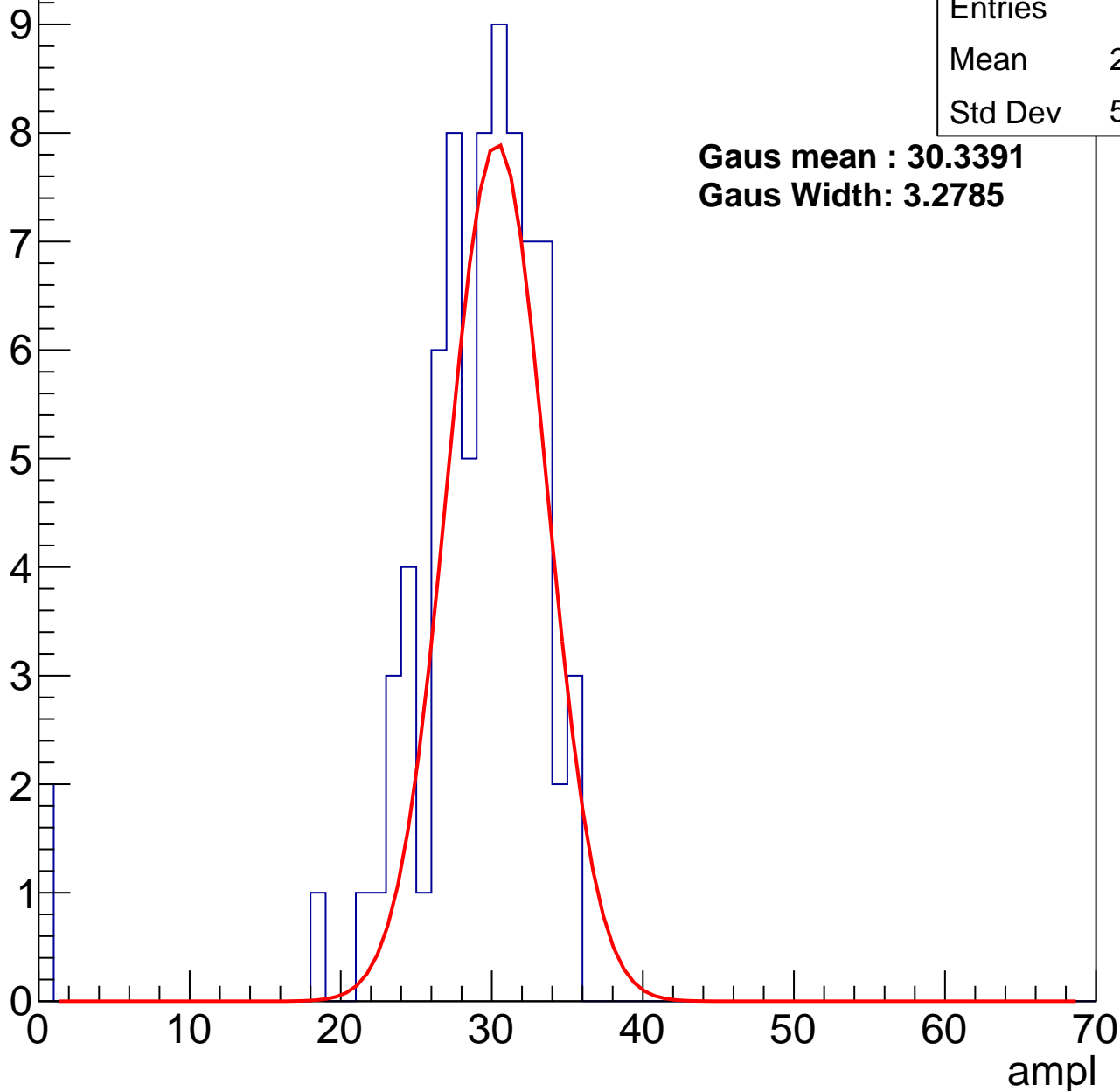
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.17
Std Dev	5.796

Gaus mean : 30.3391

Gaus Width: 3.2785



B1L102S, U14-ch79, adc1

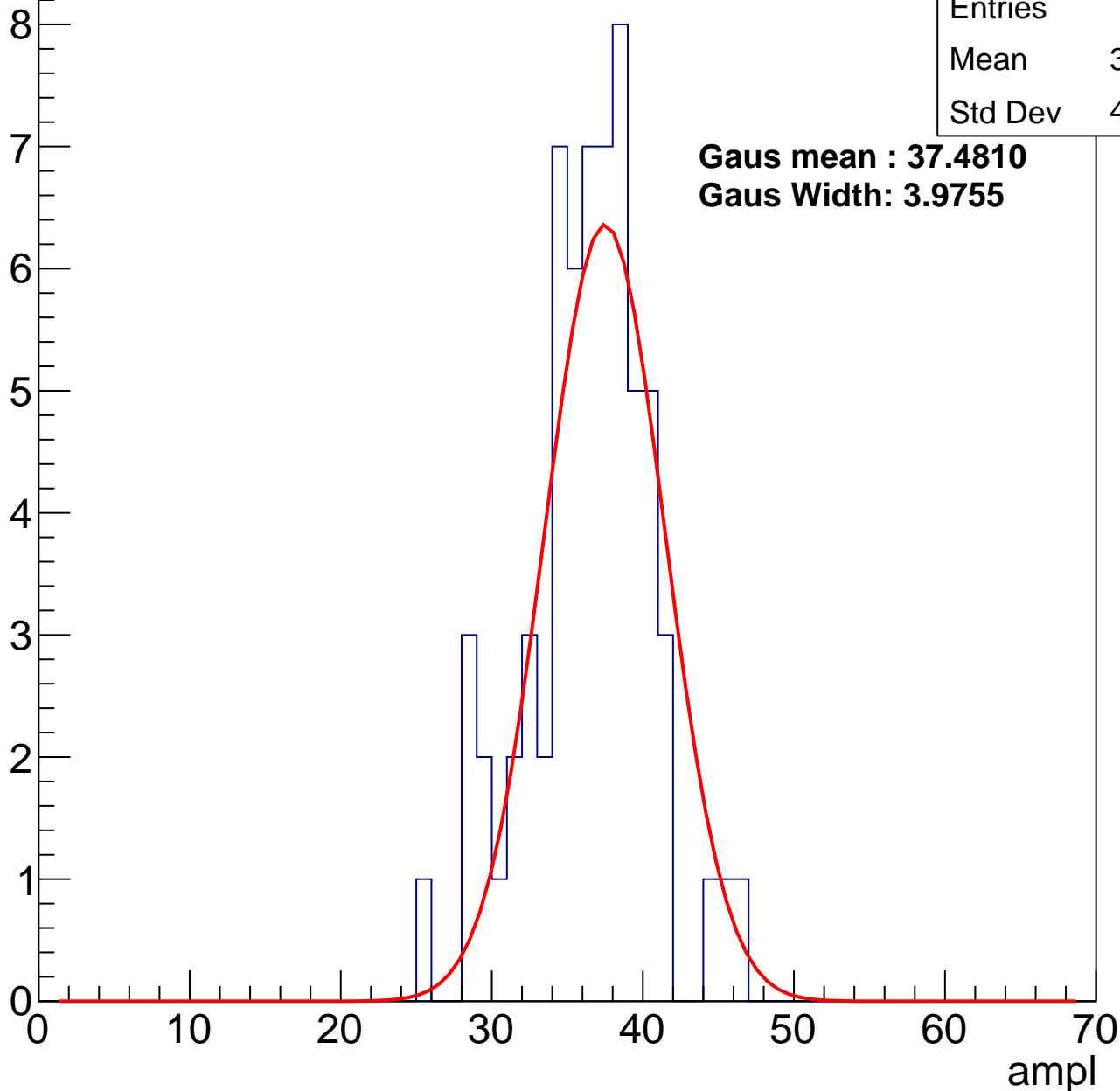
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.95
Std Dev	4.078

Gaus mean : 37.4810

Gaus Width: 3.9755



B1L102S, U14-ch79, adc2

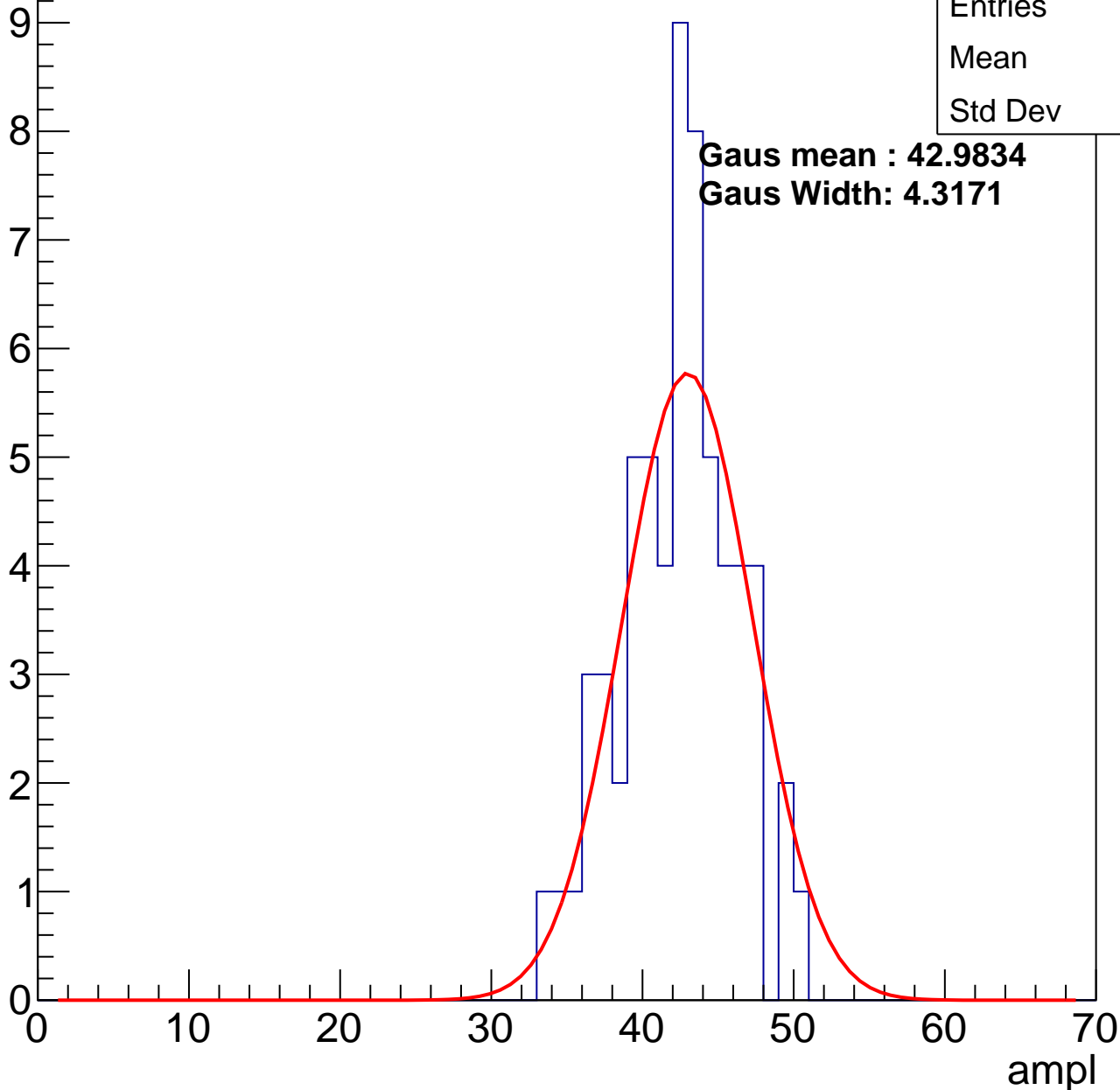
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.9
Std Dev	3.74

Gaus mean : 42.9834

Gaus Width: 4.3171

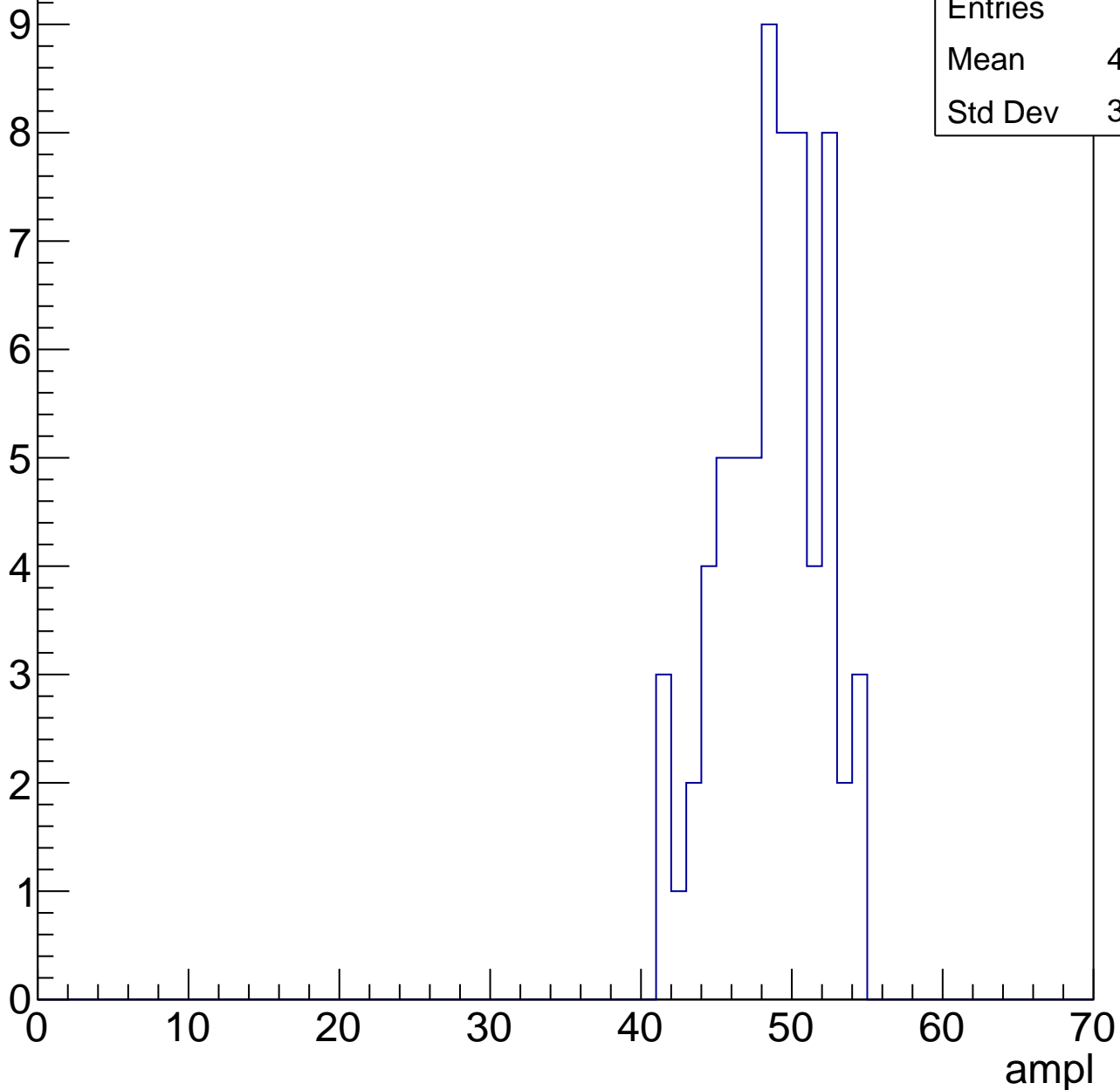


B1L102S, U14-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.19
Std Dev	3.293

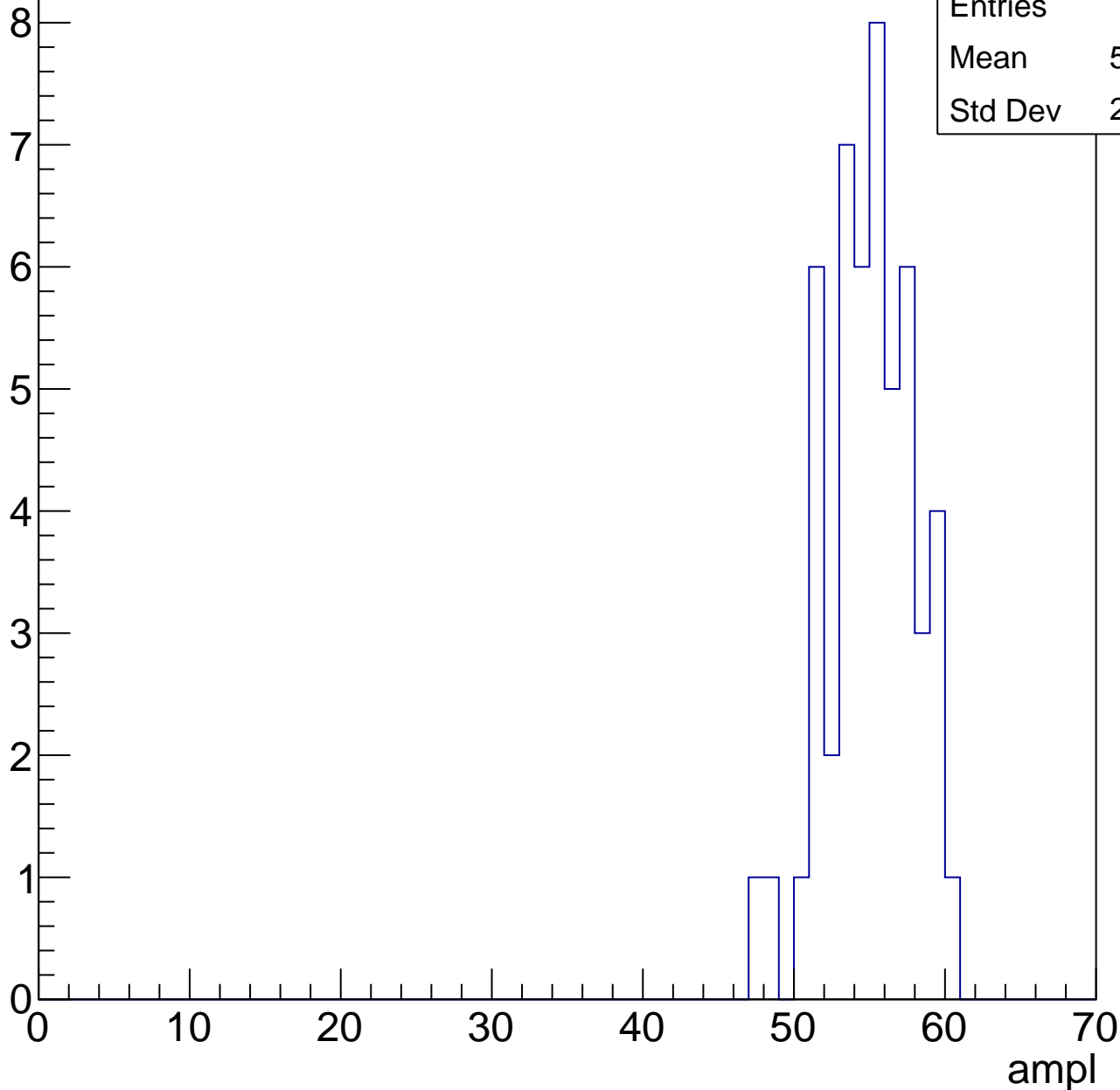


B1L102S, U14-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

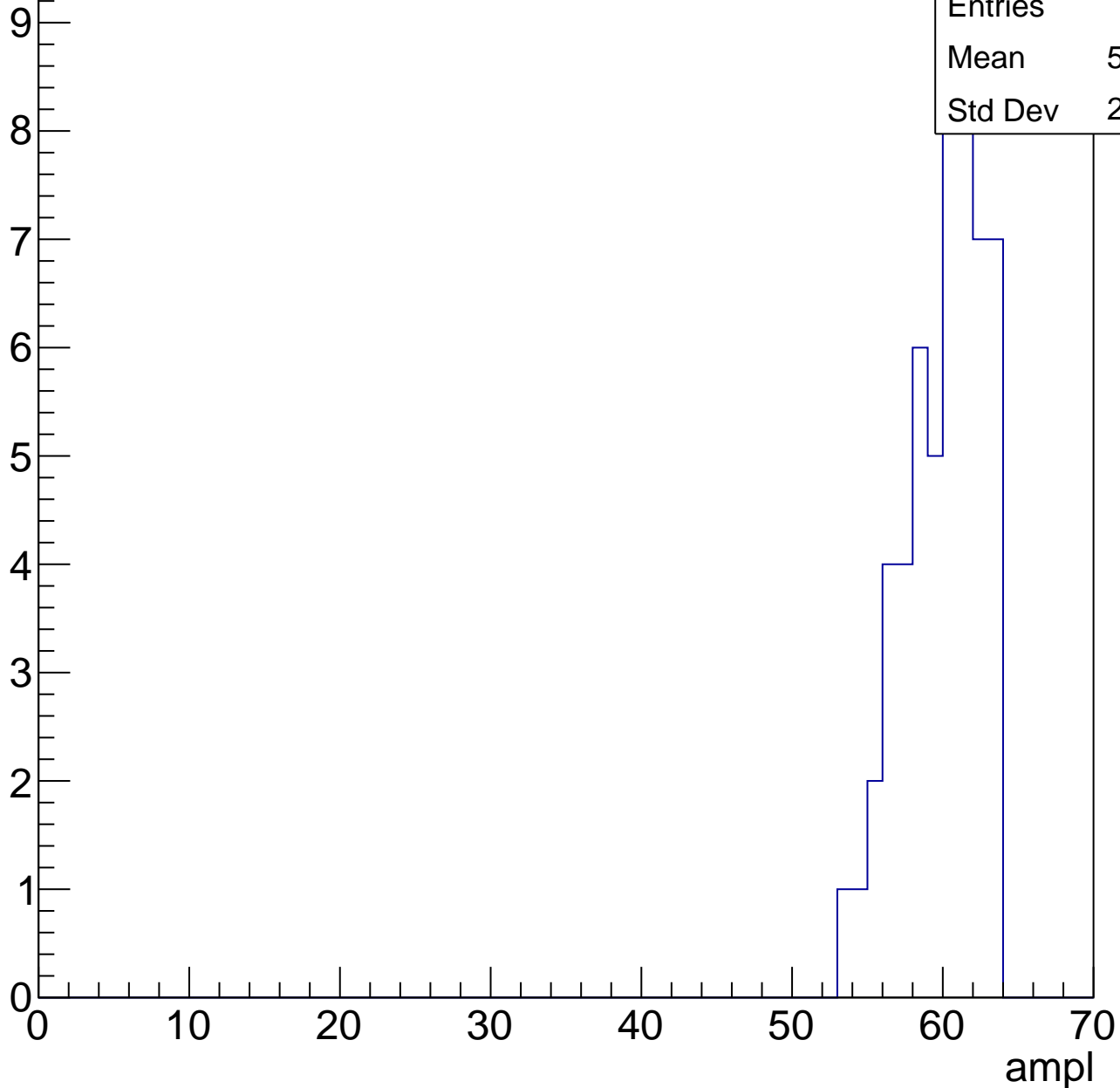
Entries	51
Mean	54.55
Std Dev	2.872



B1L102S, U14-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

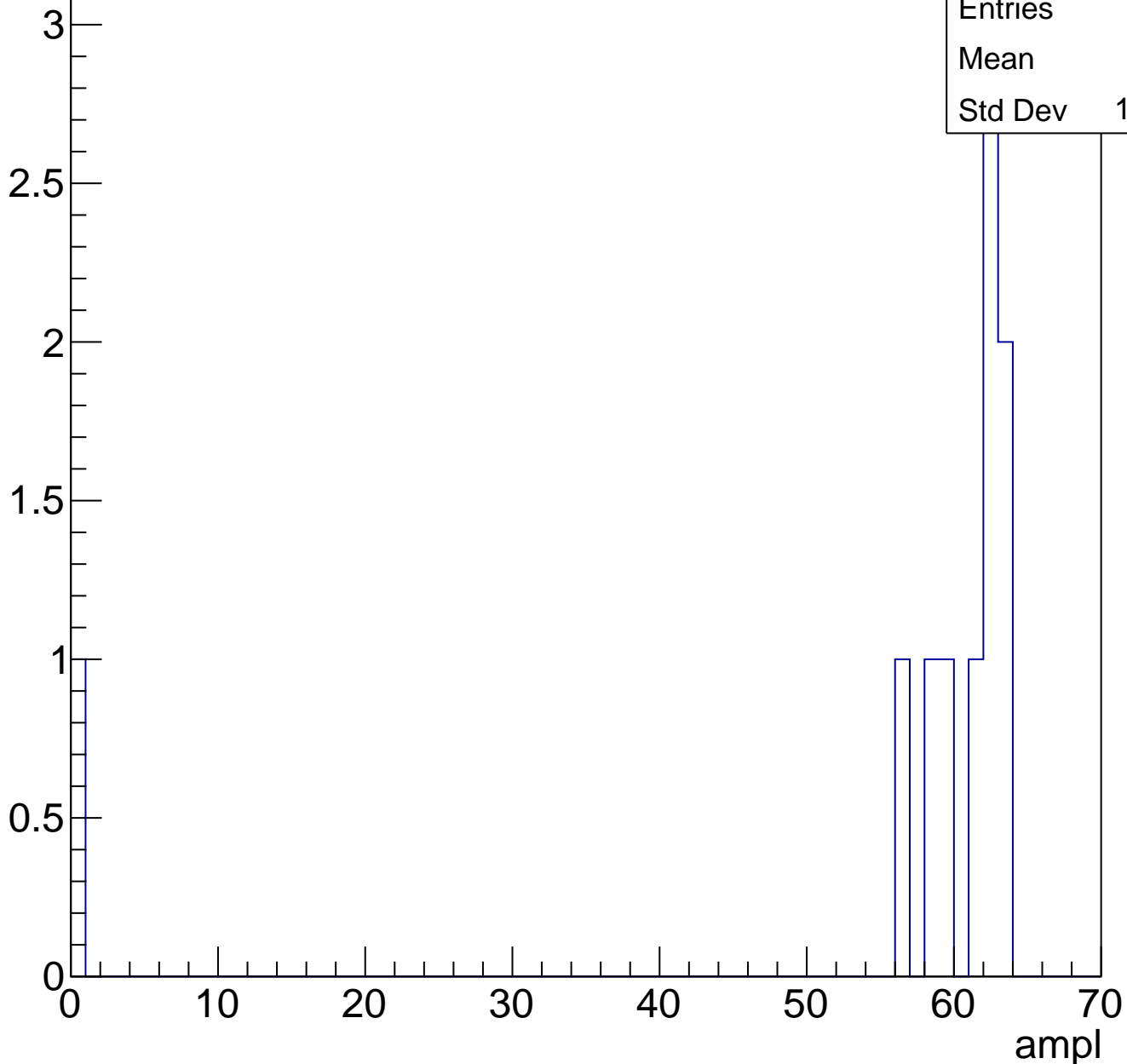
Entry



B1L102S, U14-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch80, adc0

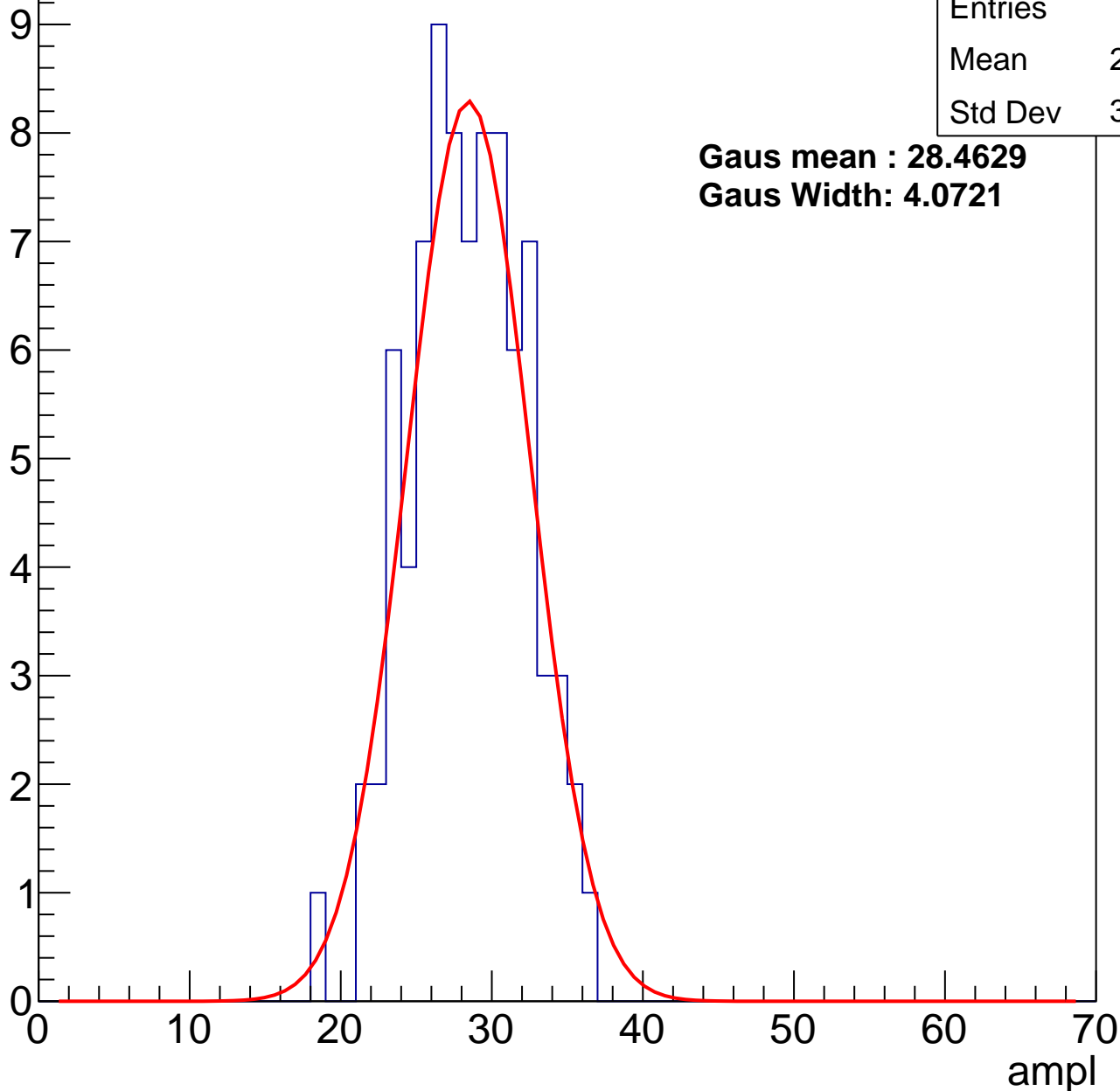
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	27.95
Std Dev	3.687

Gaus mean : 28.4629

Gaus Width: 4.0721



B1L102S, U14-ch80, adc1

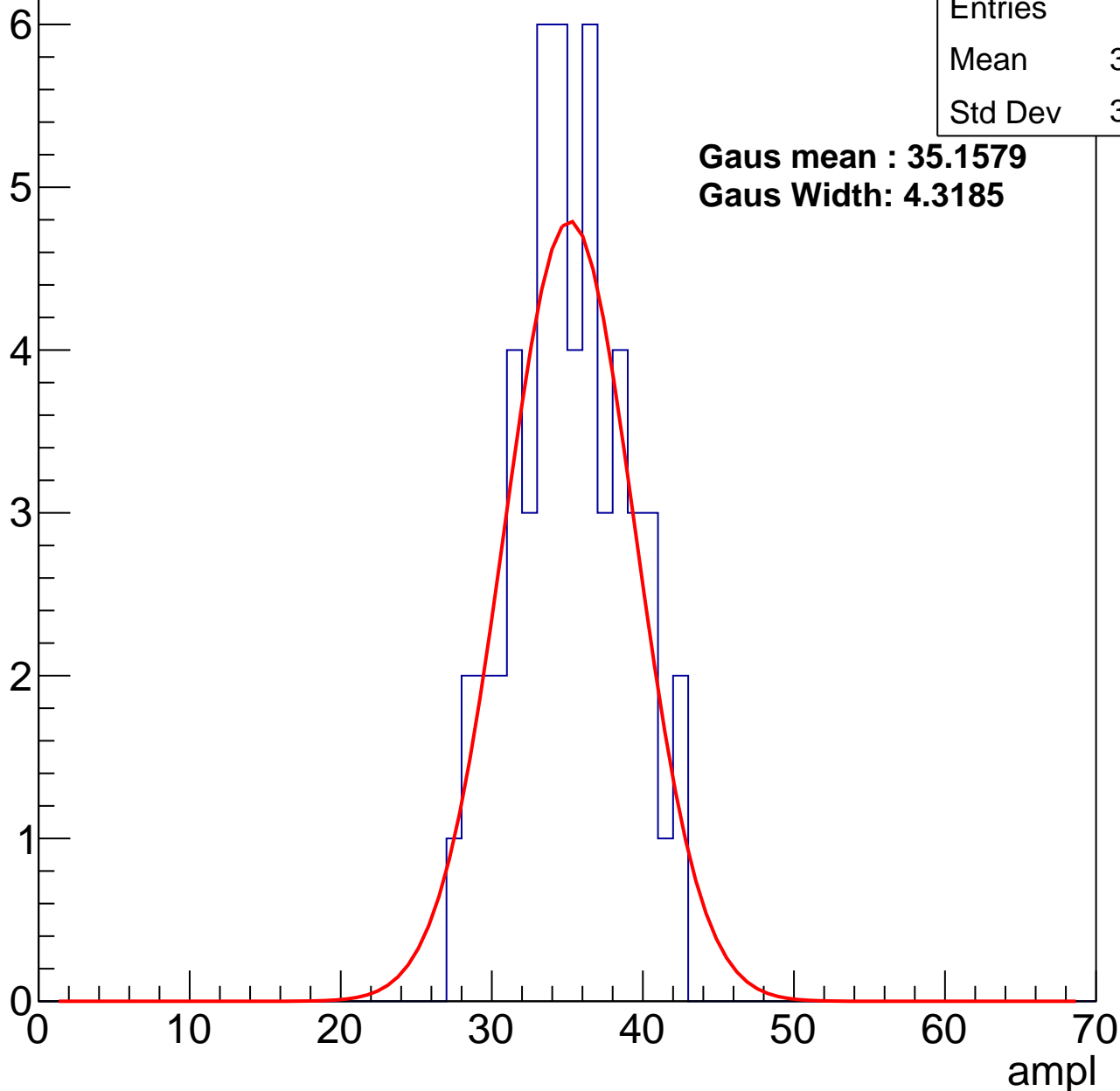
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.69
Std Dev	3.698

Gaus mean : 35.1579

Gaus Width: 4.3185



B1L102S, U14-ch80, adc2

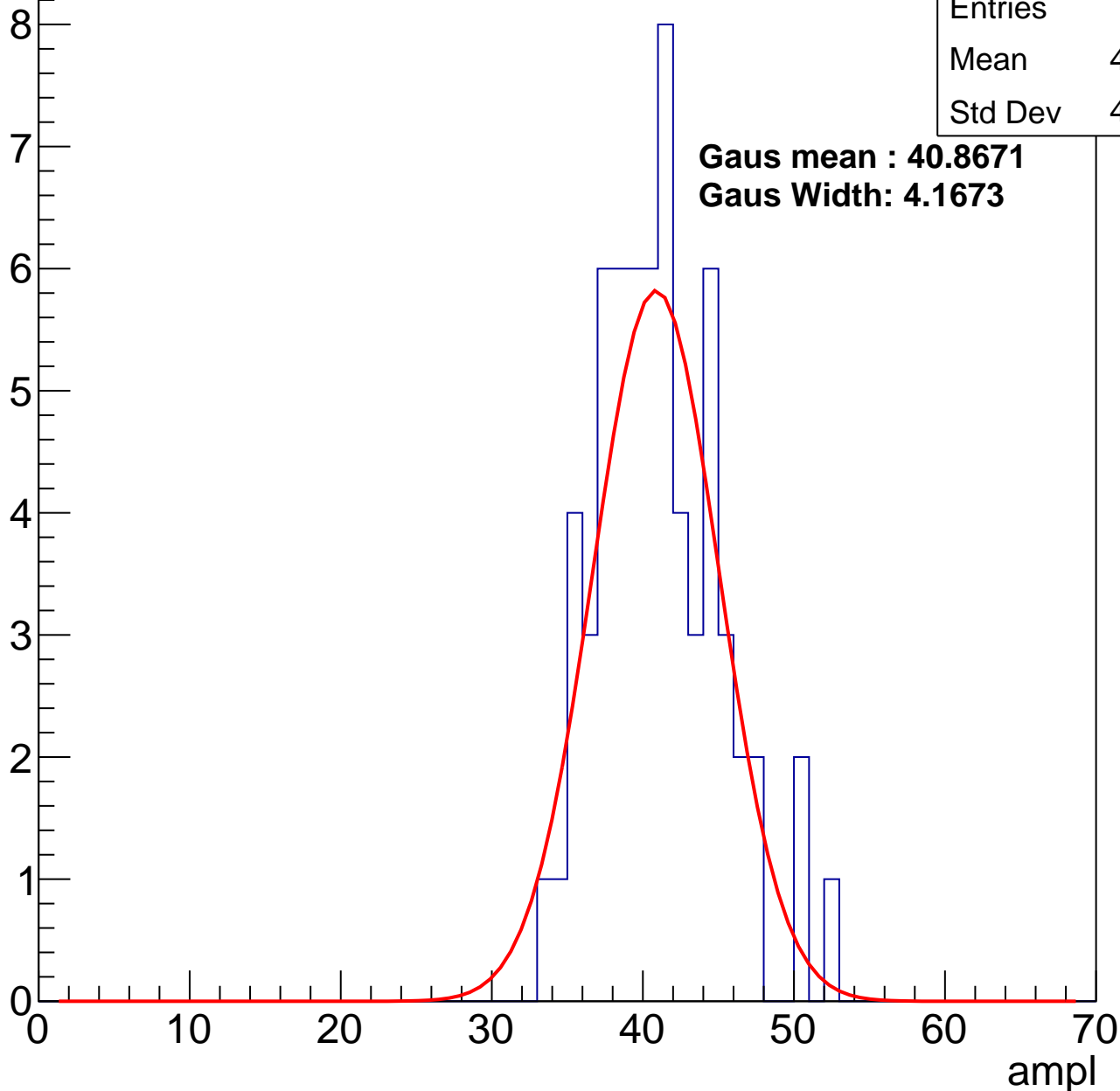
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	40.64
Std Dev	4.005

Gaus mean : 40.8671

Gaus Width: 4.1673

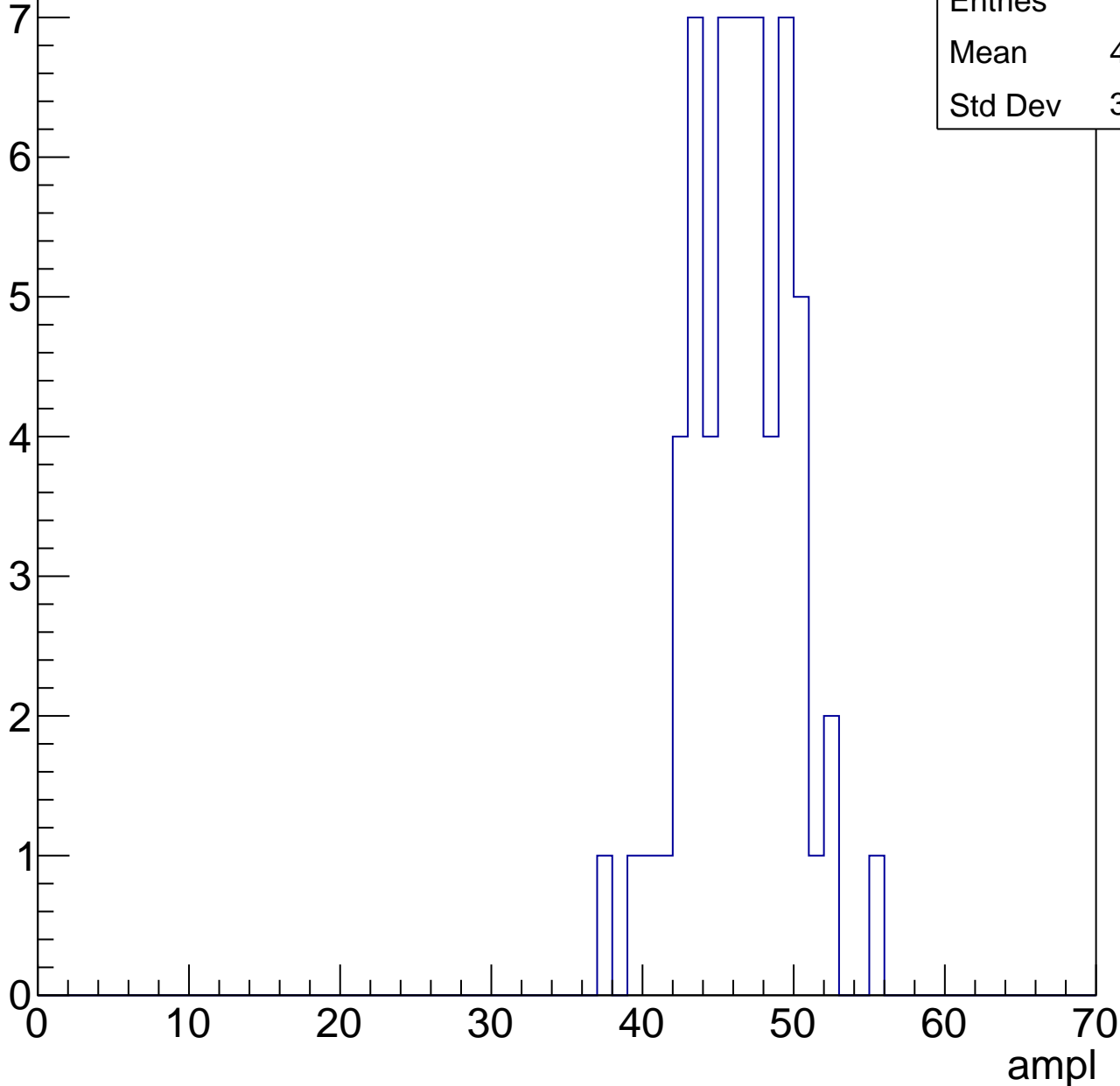


B1L102S, U14-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	46.05
Std Dev	3.378

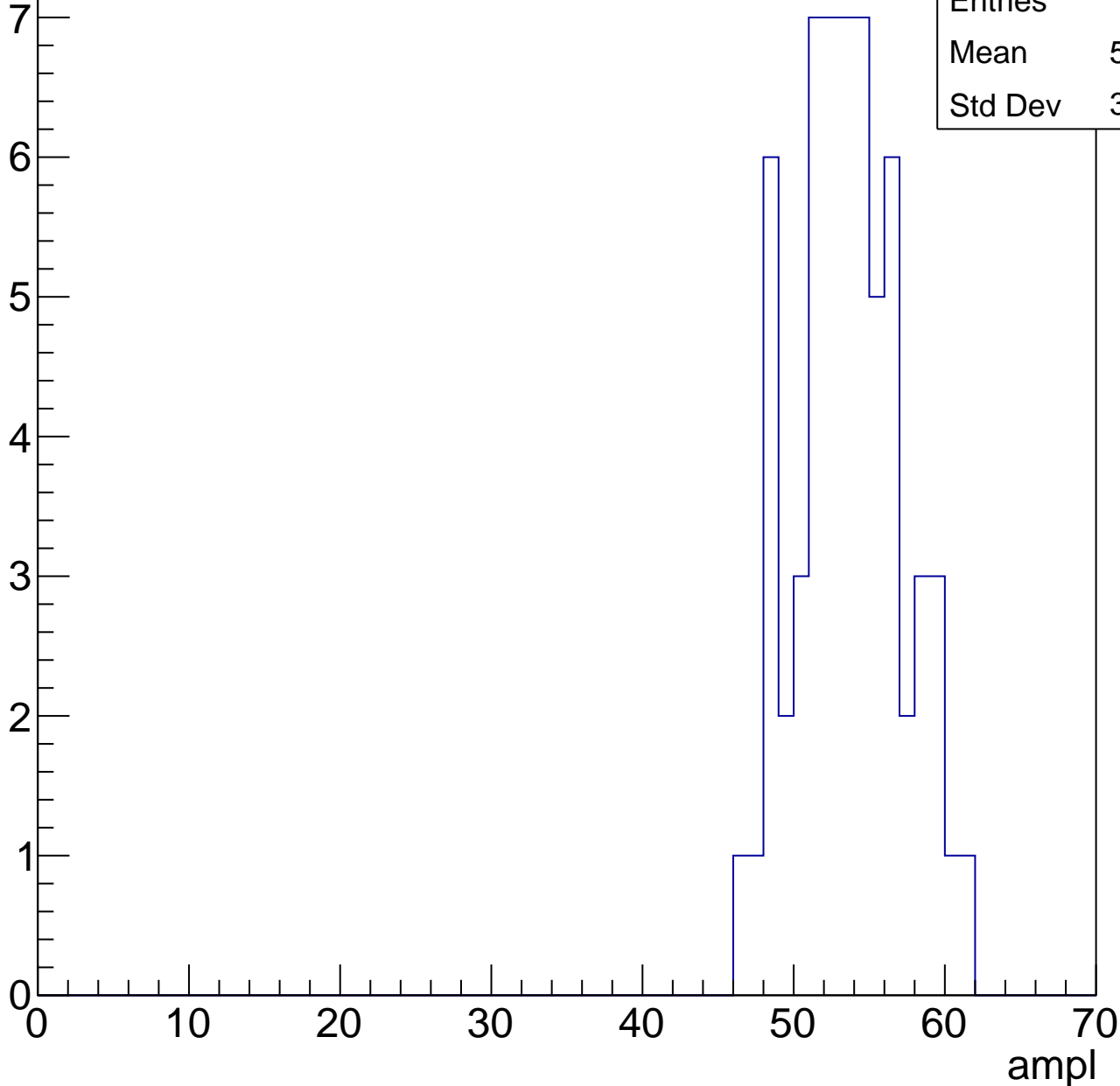


B1L102S, U14-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	53.16
Std Dev	3.446

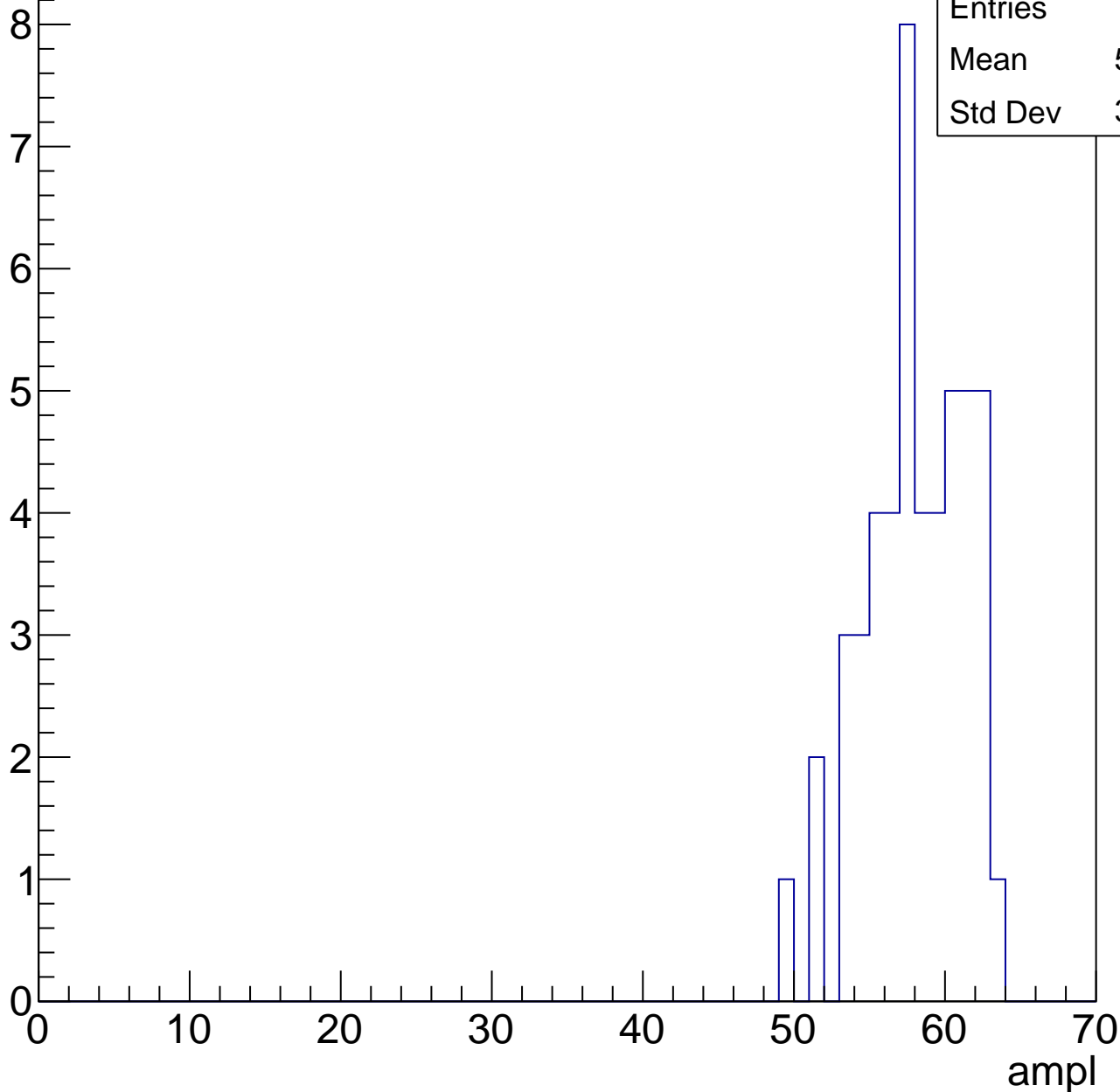


B1L102S, U14-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.51
Std Dev	3.271

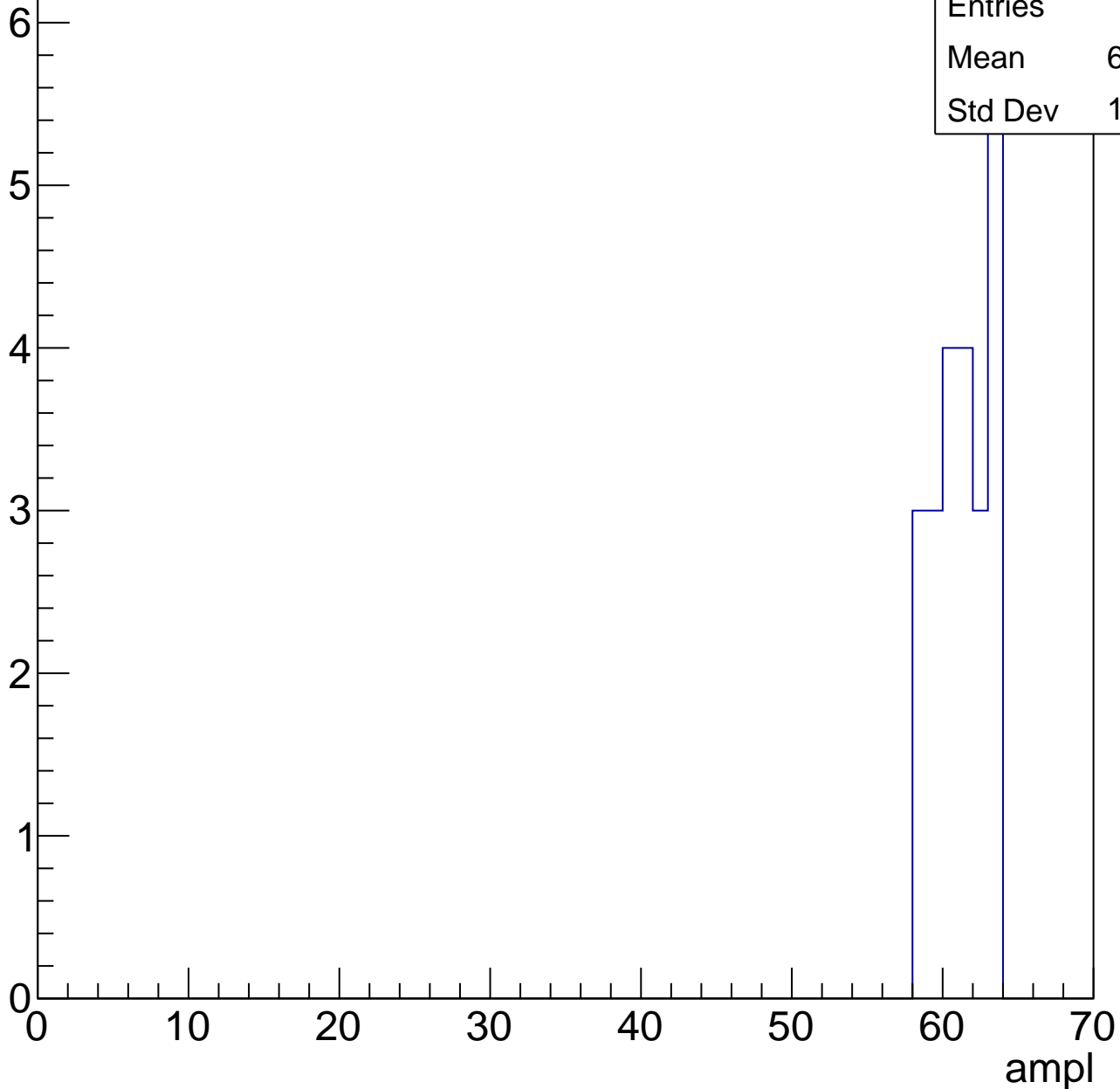


B1L102S, U14-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

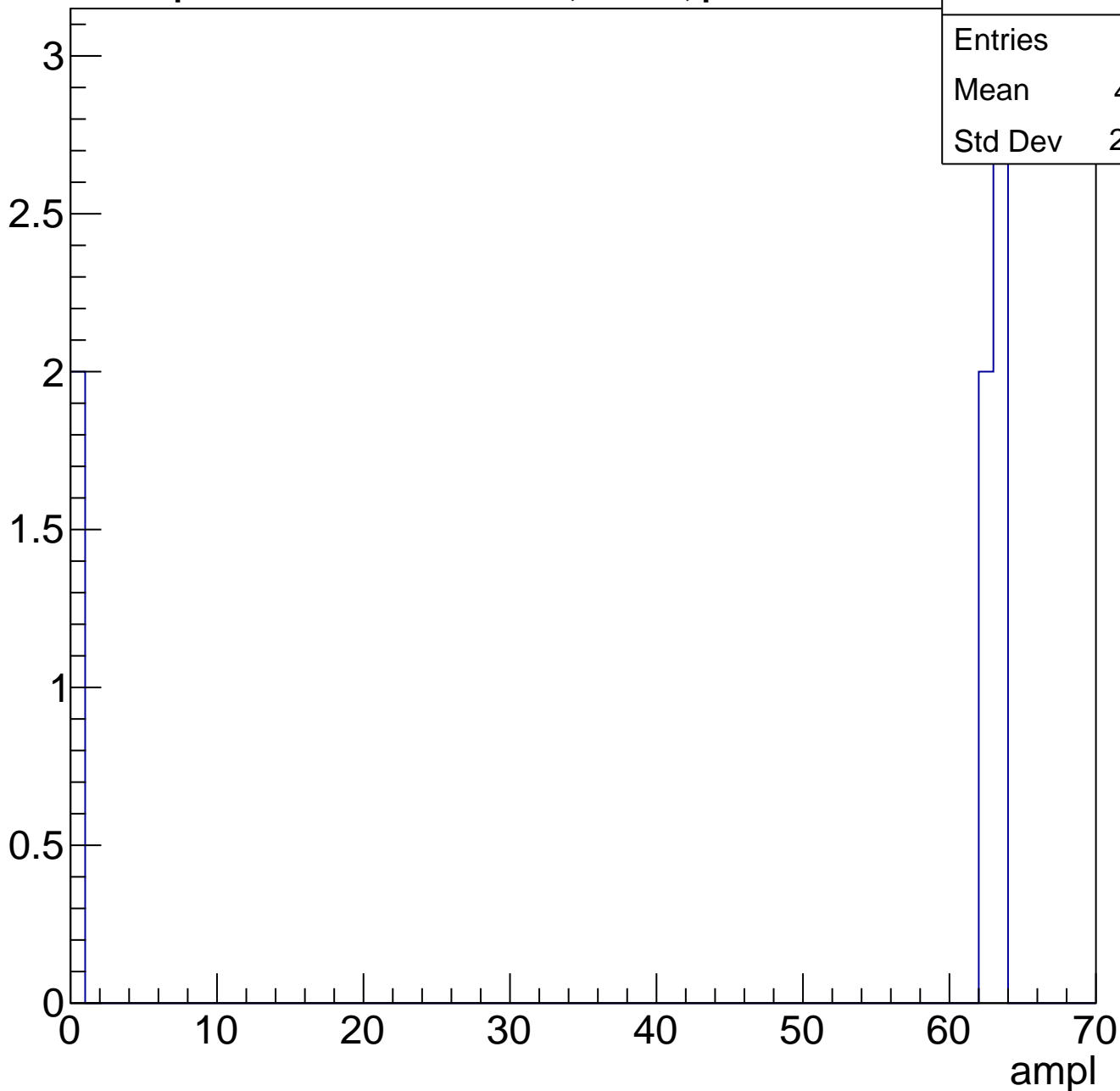
Entries	23
Mean	60.83
Std Dev	1.736



B1L102S, U14-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch81, adc0

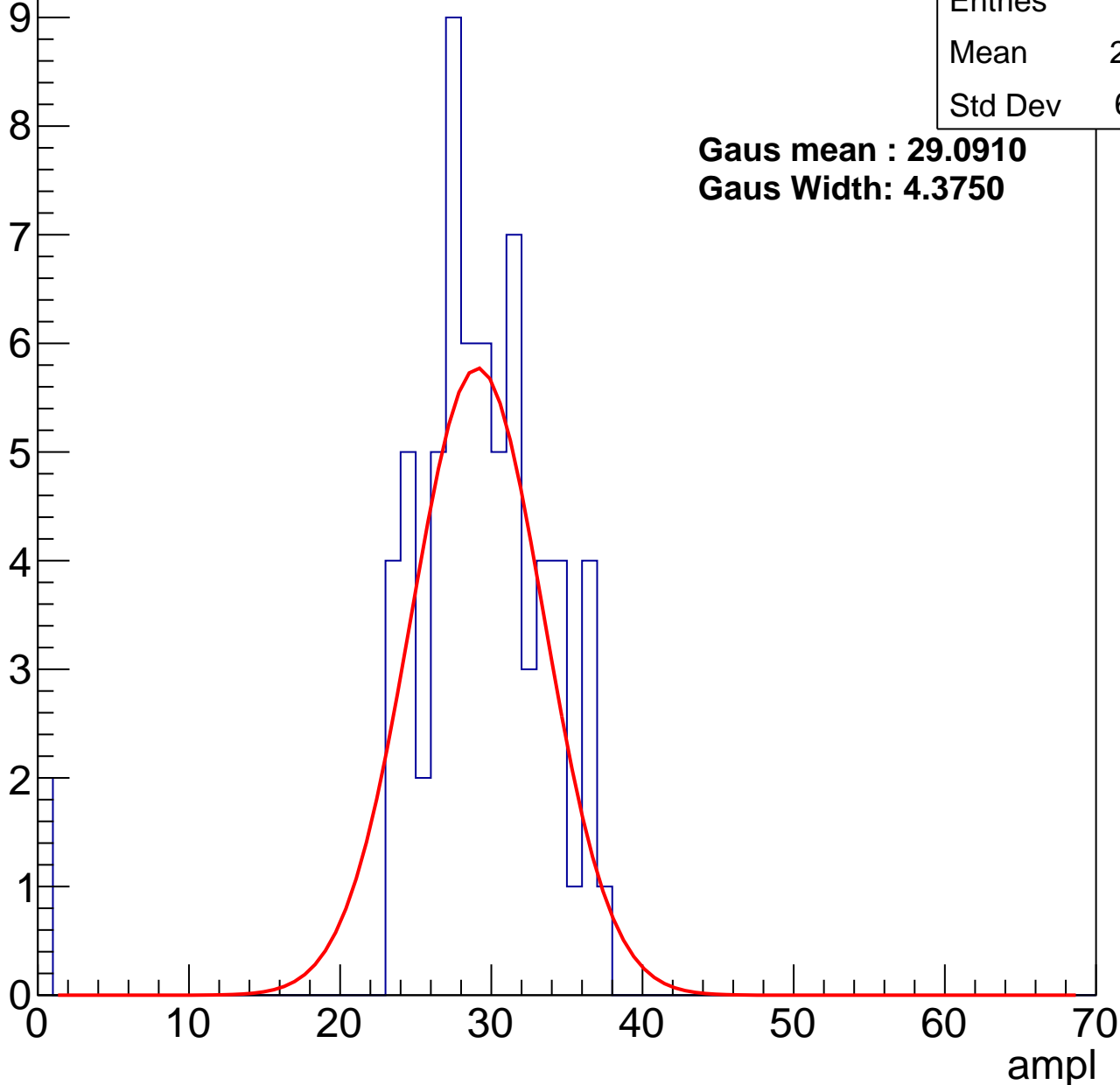
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.29
Std Dev	6.131

Gaus mean : 29.0910

Gaus Width: 4.3750



B1L102S, U14-ch81, adc1

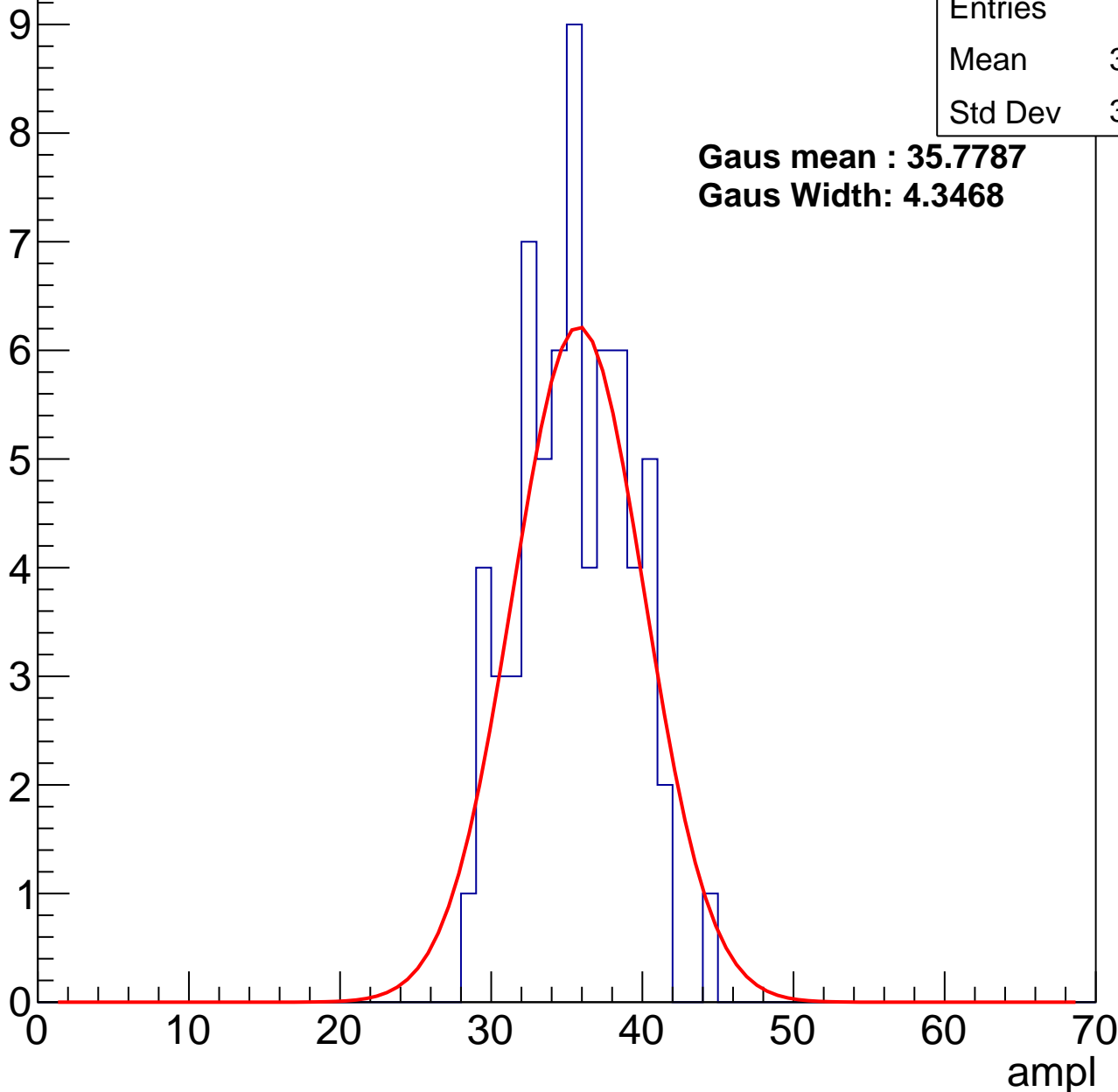
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.02
Std Dev	3.544

Gaus mean : 35.7787

Gaus Width: 4.3468



B1L102S, U14-ch81, adc2

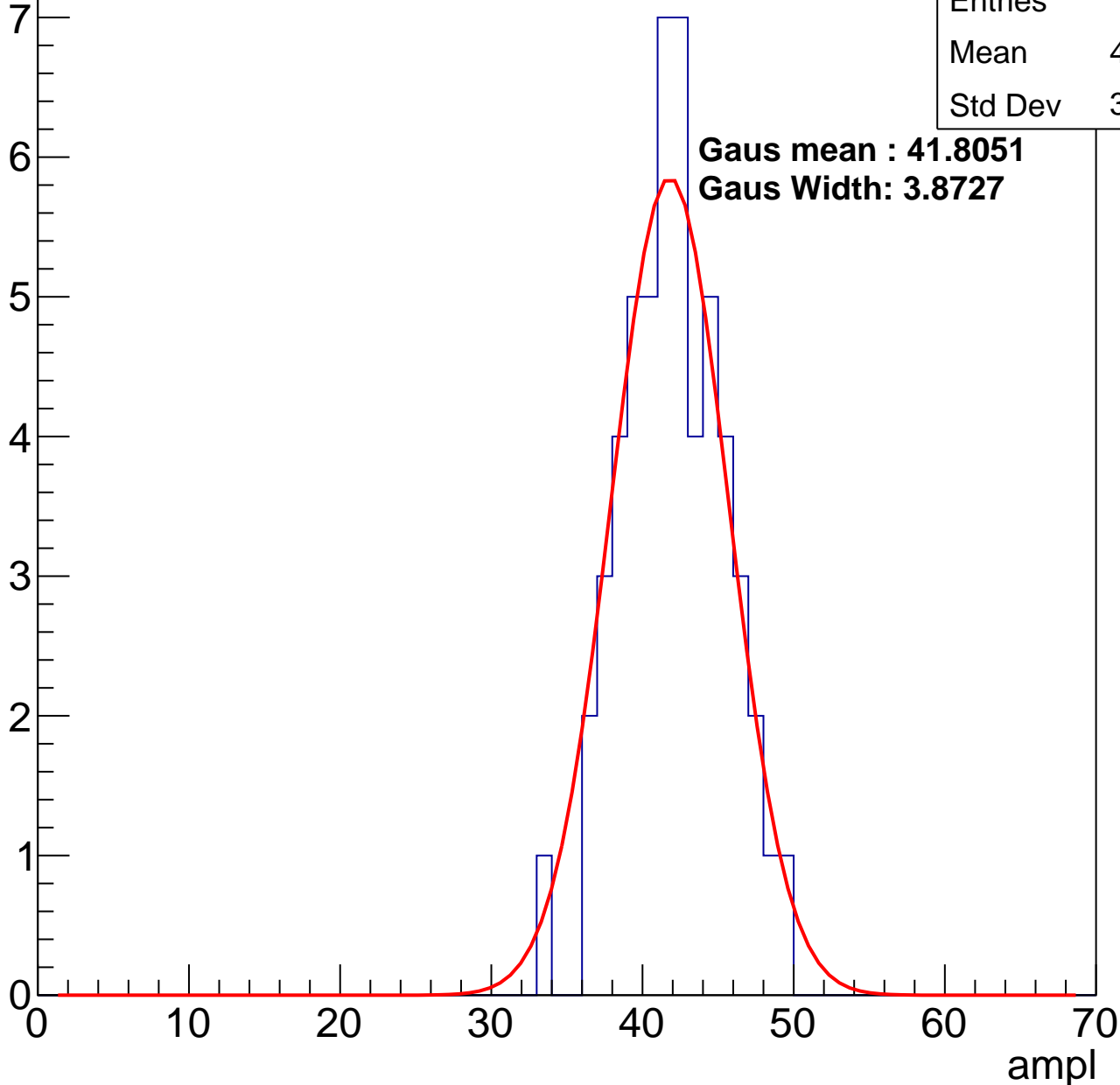
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.57
Std Dev	3.326

Gaus mean : 41.8051

Gaus Width: 3.8727



B1L102S, U14-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

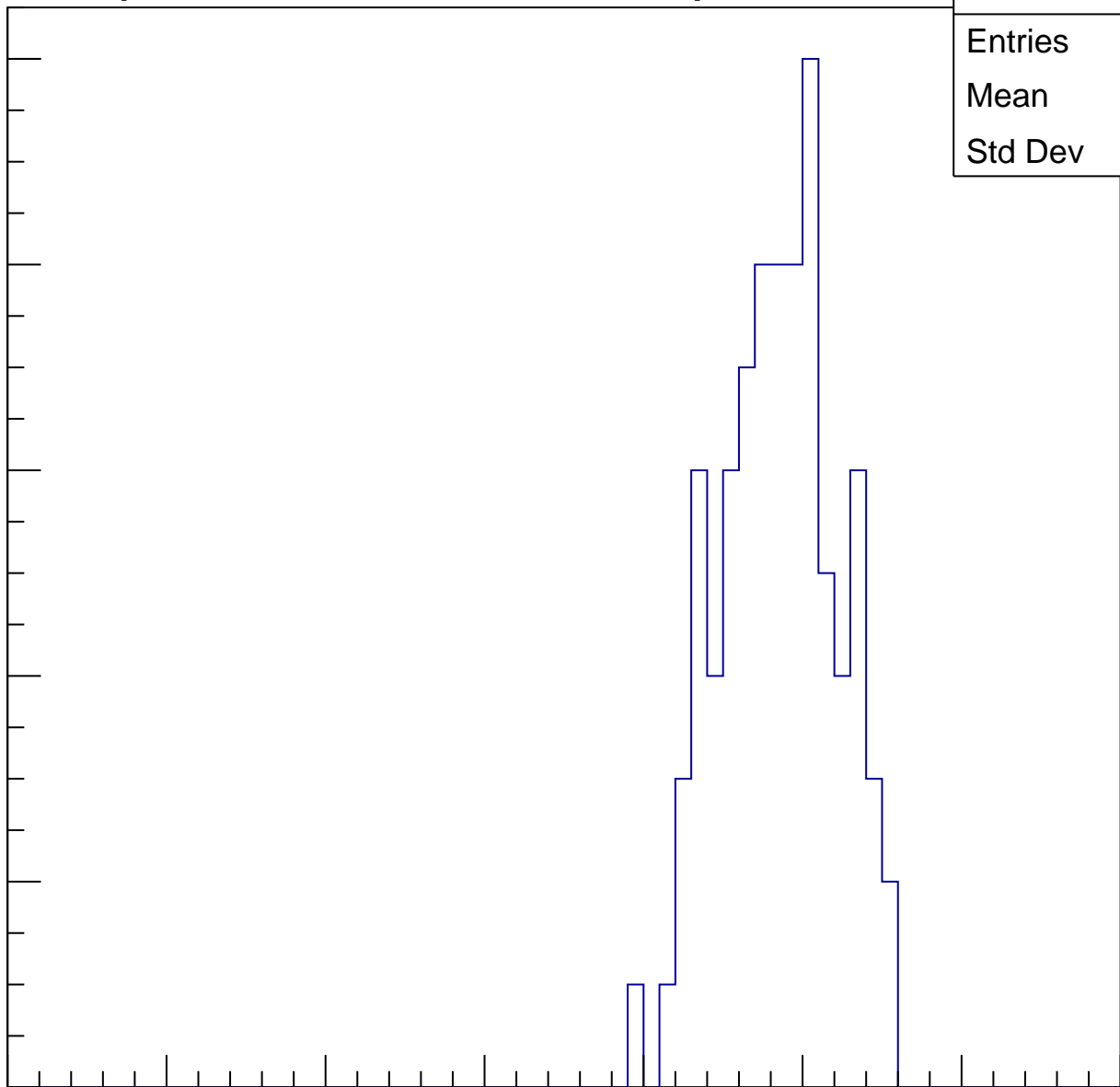
Entries	82
Mean	48.01
Std Dev	3.587

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

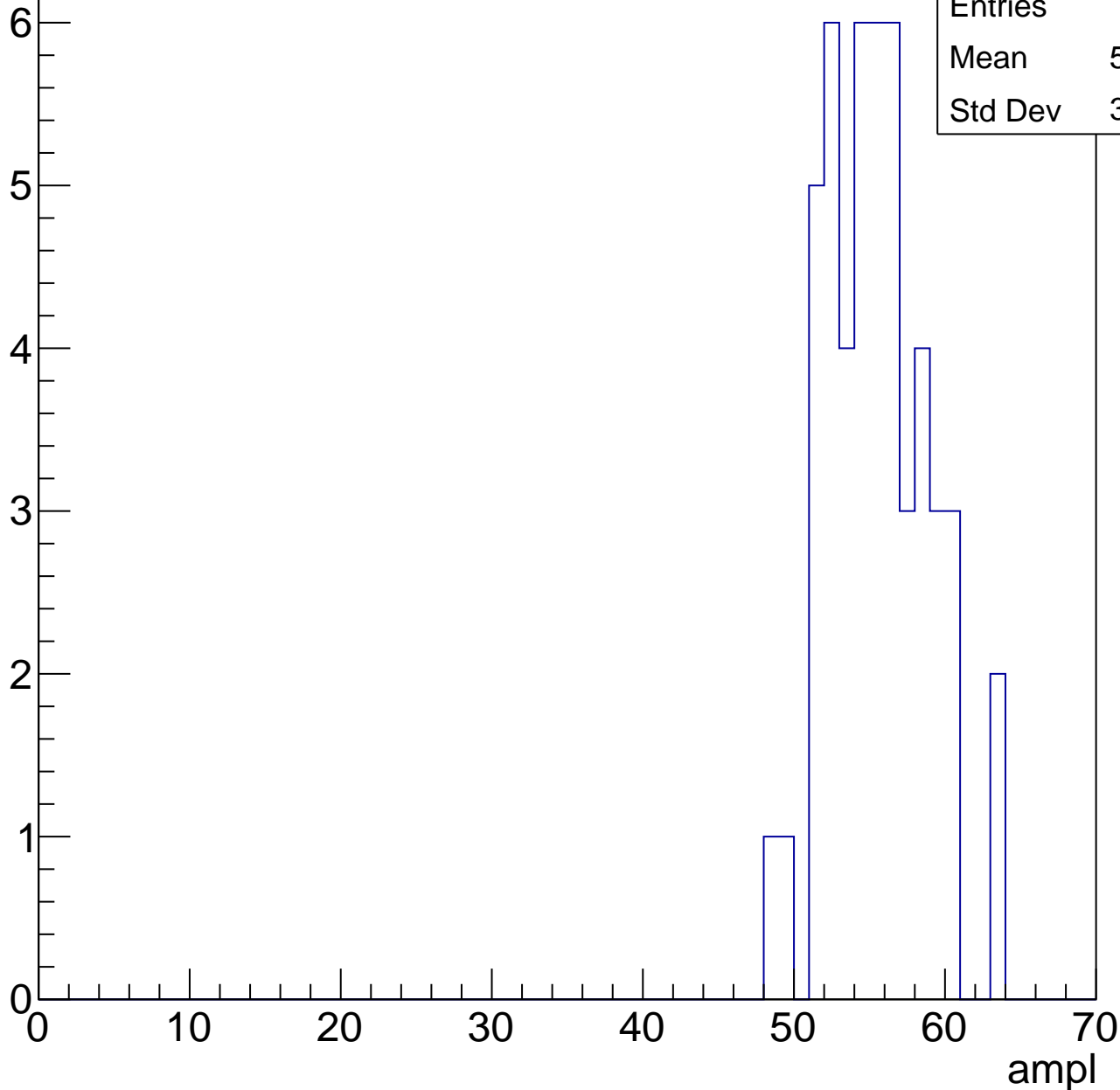


B1L102S, U14-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.04
Std Dev	3.304

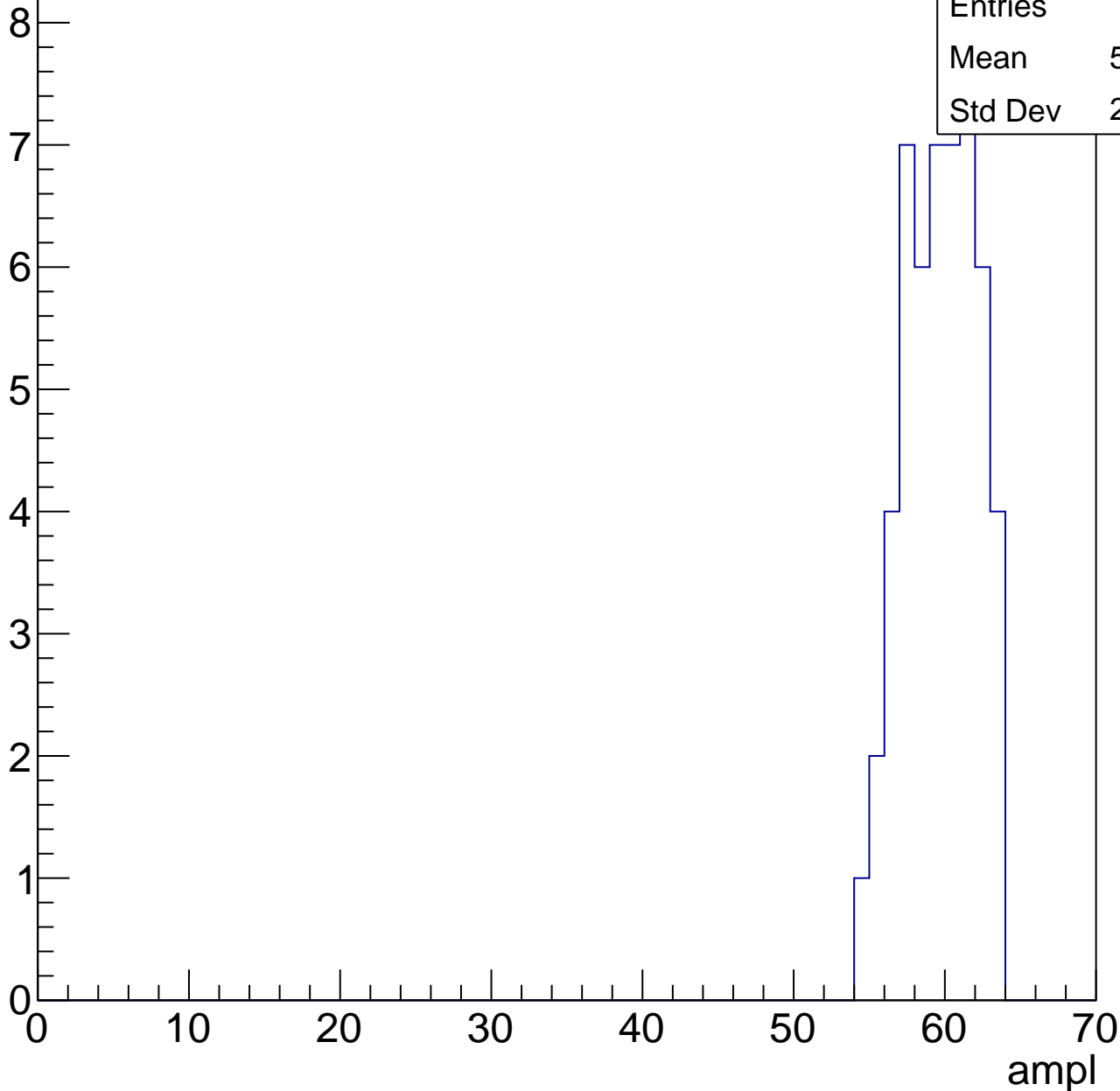


B1L102S, U14-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

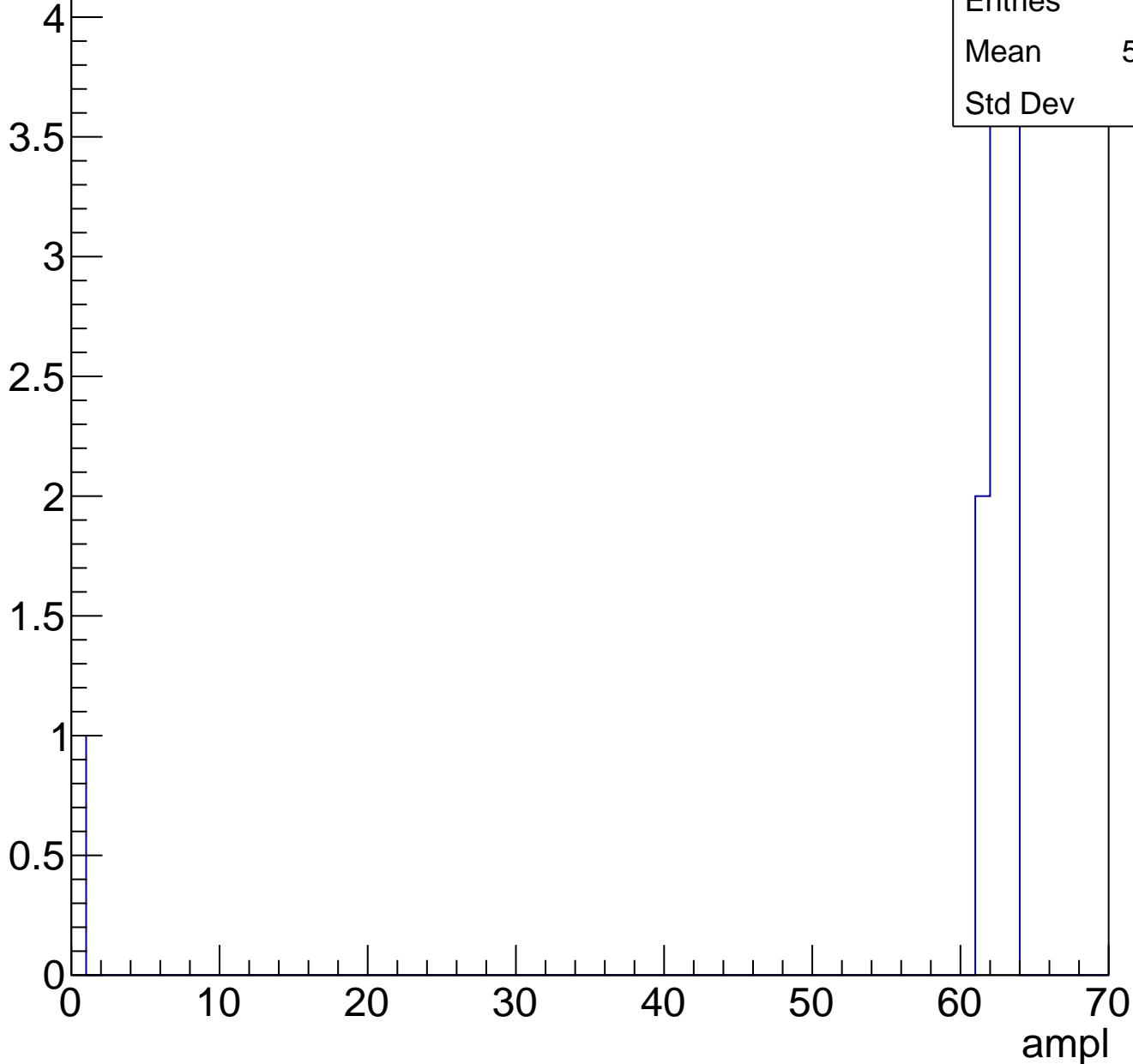
Entries	52
Mean	59.23
Std Dev	2.326



B1L102S, U14-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	11
Mean	56.55
Std Dev	17.9

B1L102S, U14-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch82, adc0

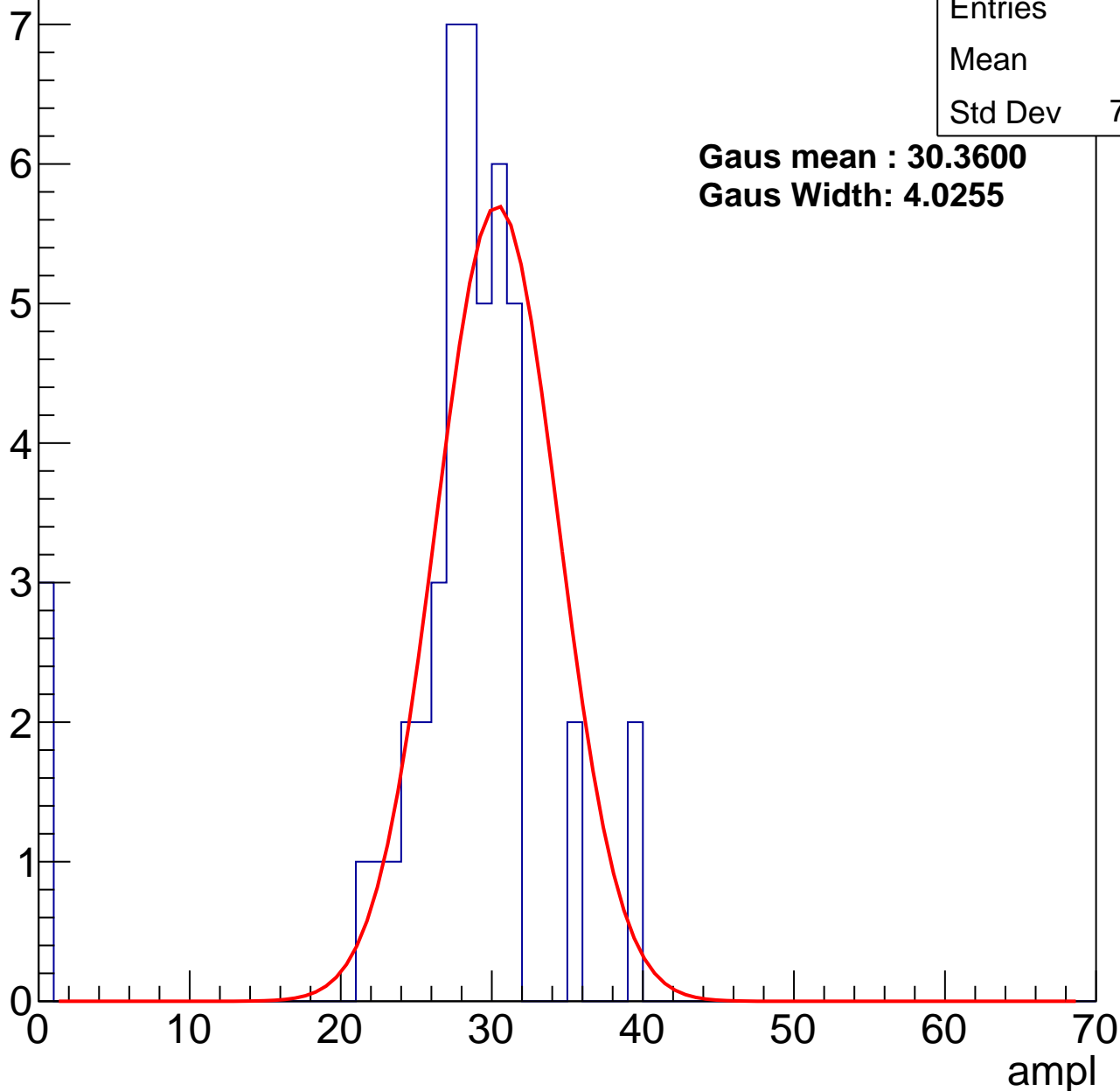
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	26.7
Std Dev	7.809

Gaus mean : 30.3600

Gaus Width: 4.0255



B1L102S, U14-ch82, adc1

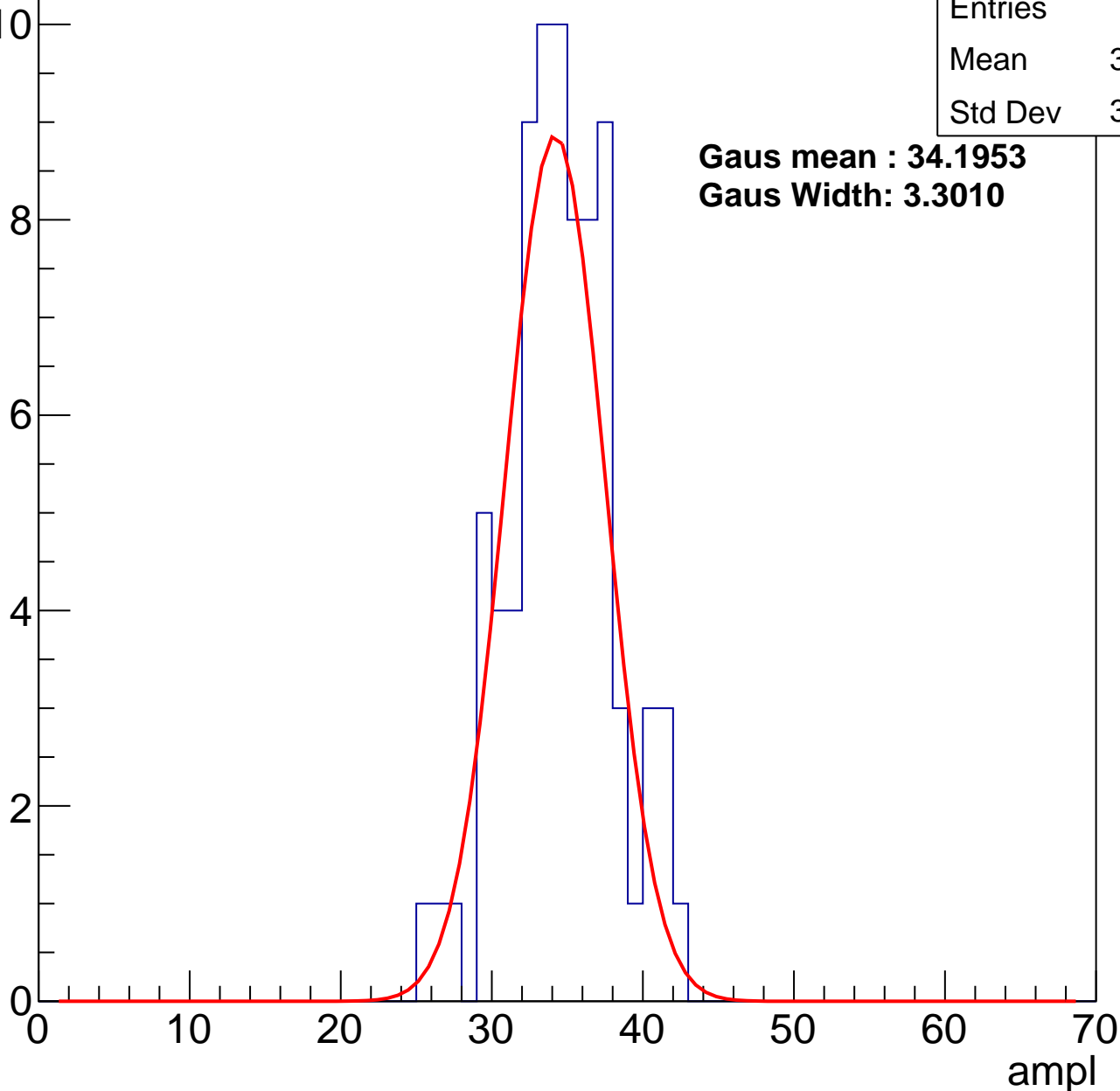
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	34.12
Std Dev	3.473

Gaus mean : 34.1953

Gaus Width: 3.3010



B1L102S, U14-ch82, adc2

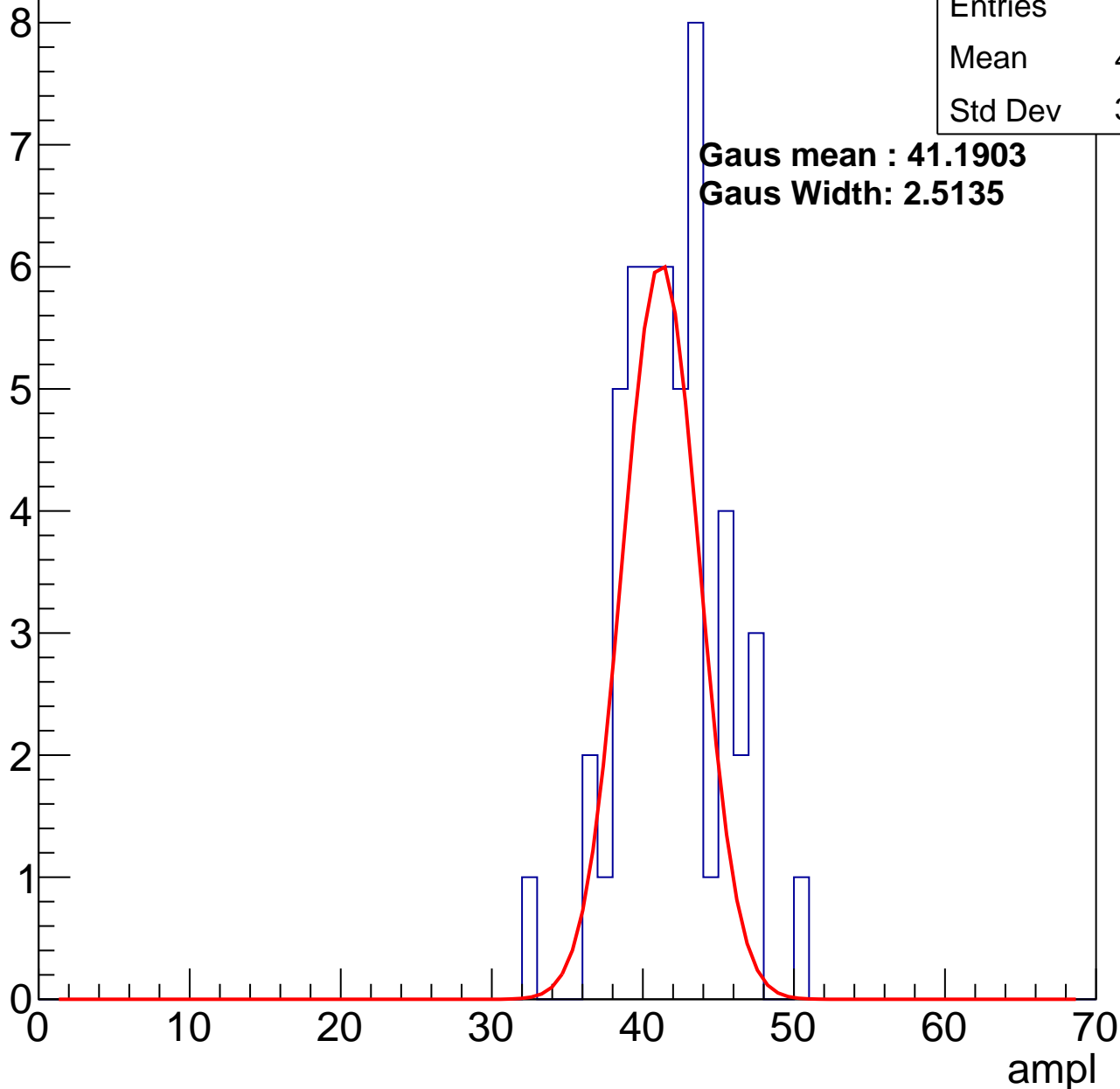
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.41
Std Dev	3.321

Gaus mean : 41.1903

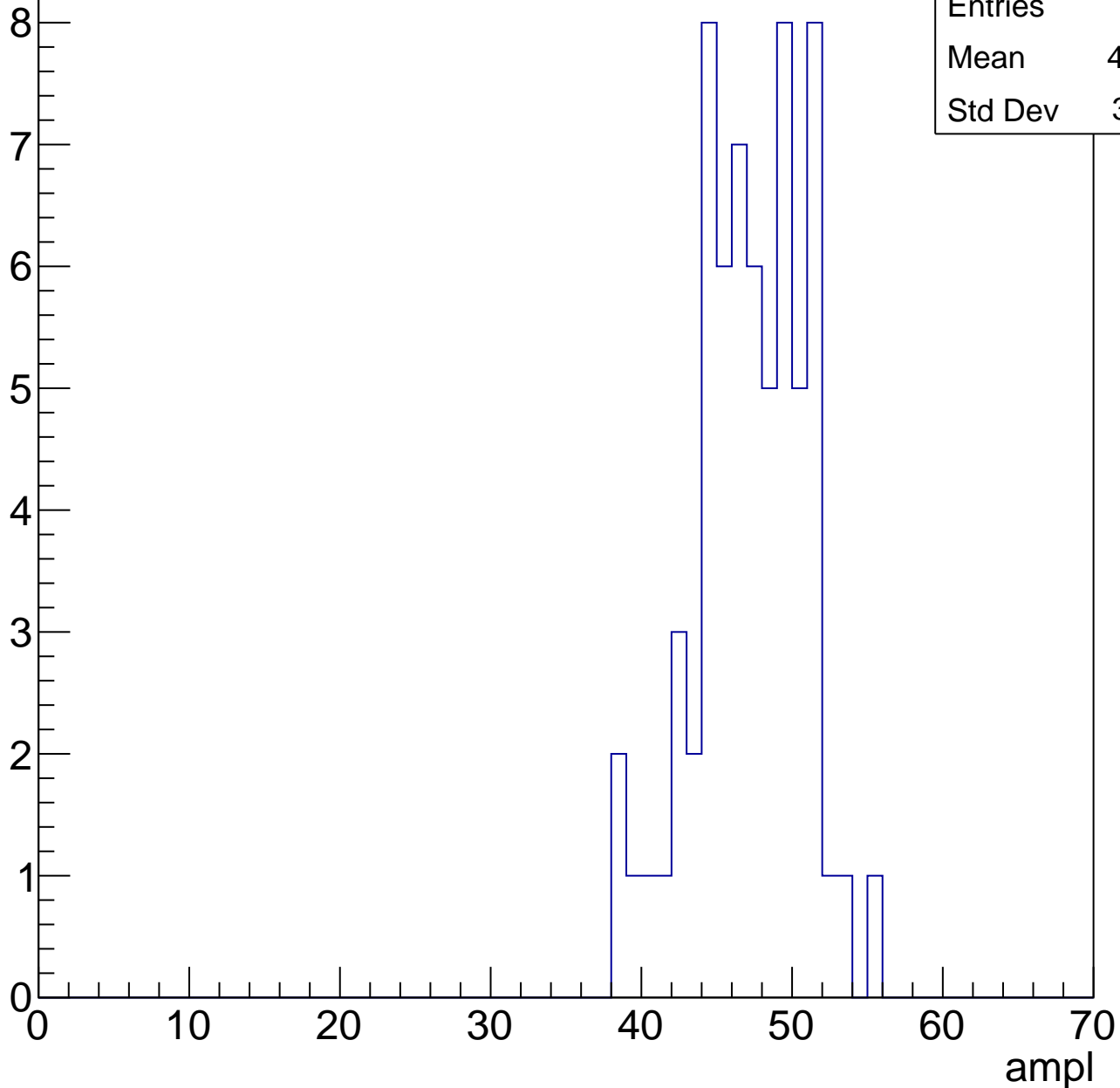
Gaus Width: 2.5135



B1L102S, U14-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



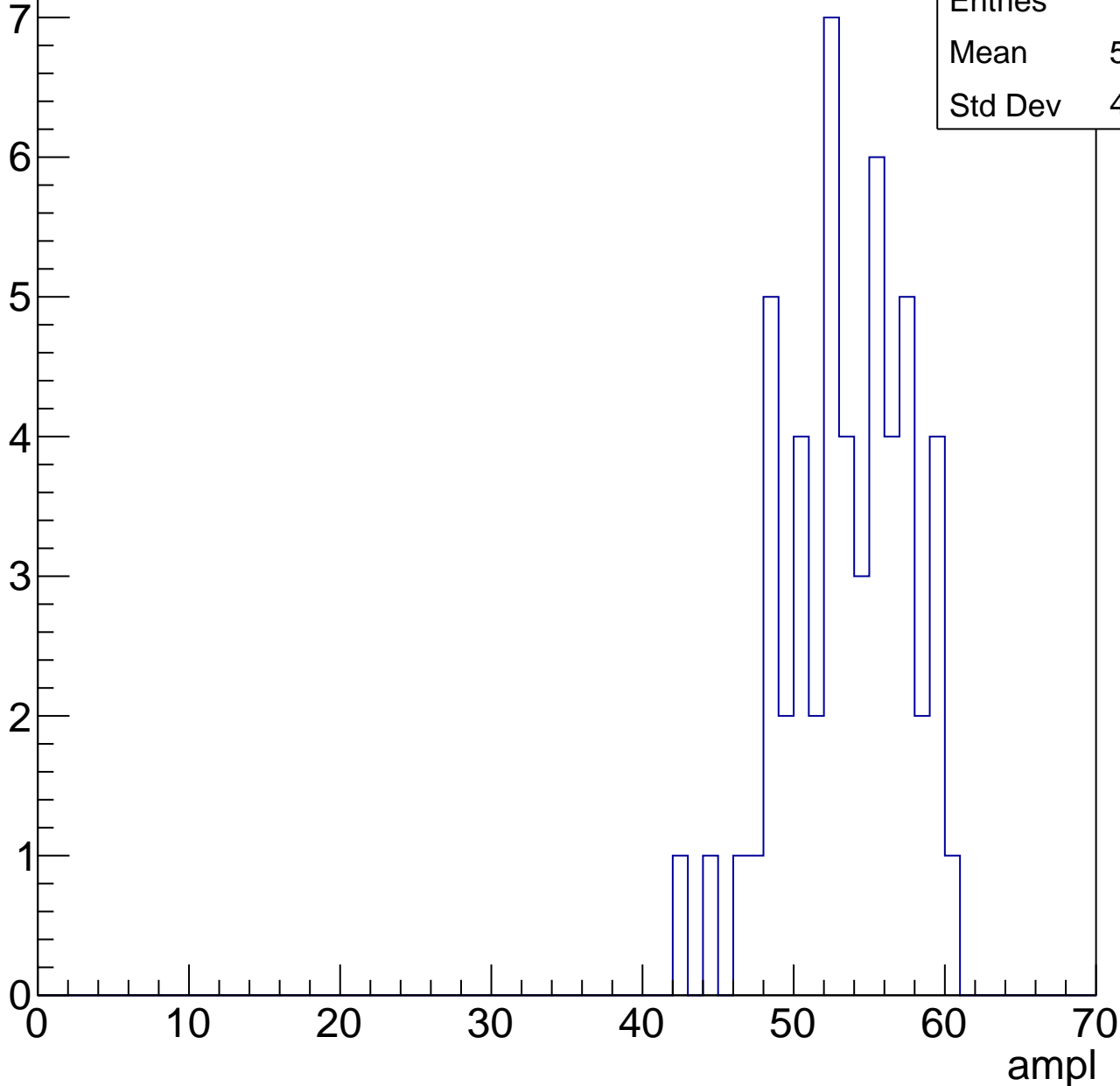
Entries	66
Mean	46.73
Std Dev	3.591

B1L102S, U14-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

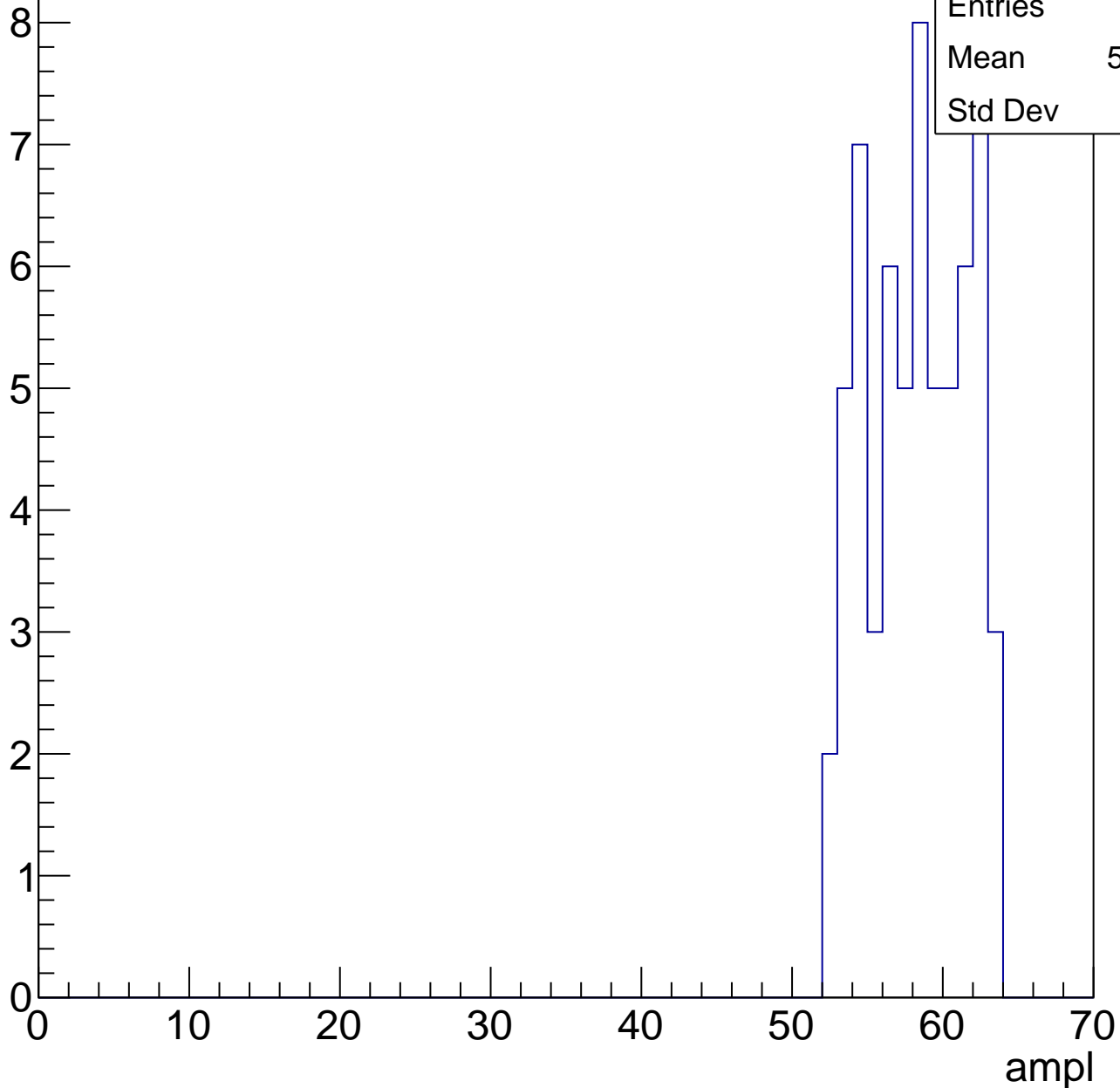
Entries	53
Mean	52.98
Std Dev	4.077



B1L102S, U14-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

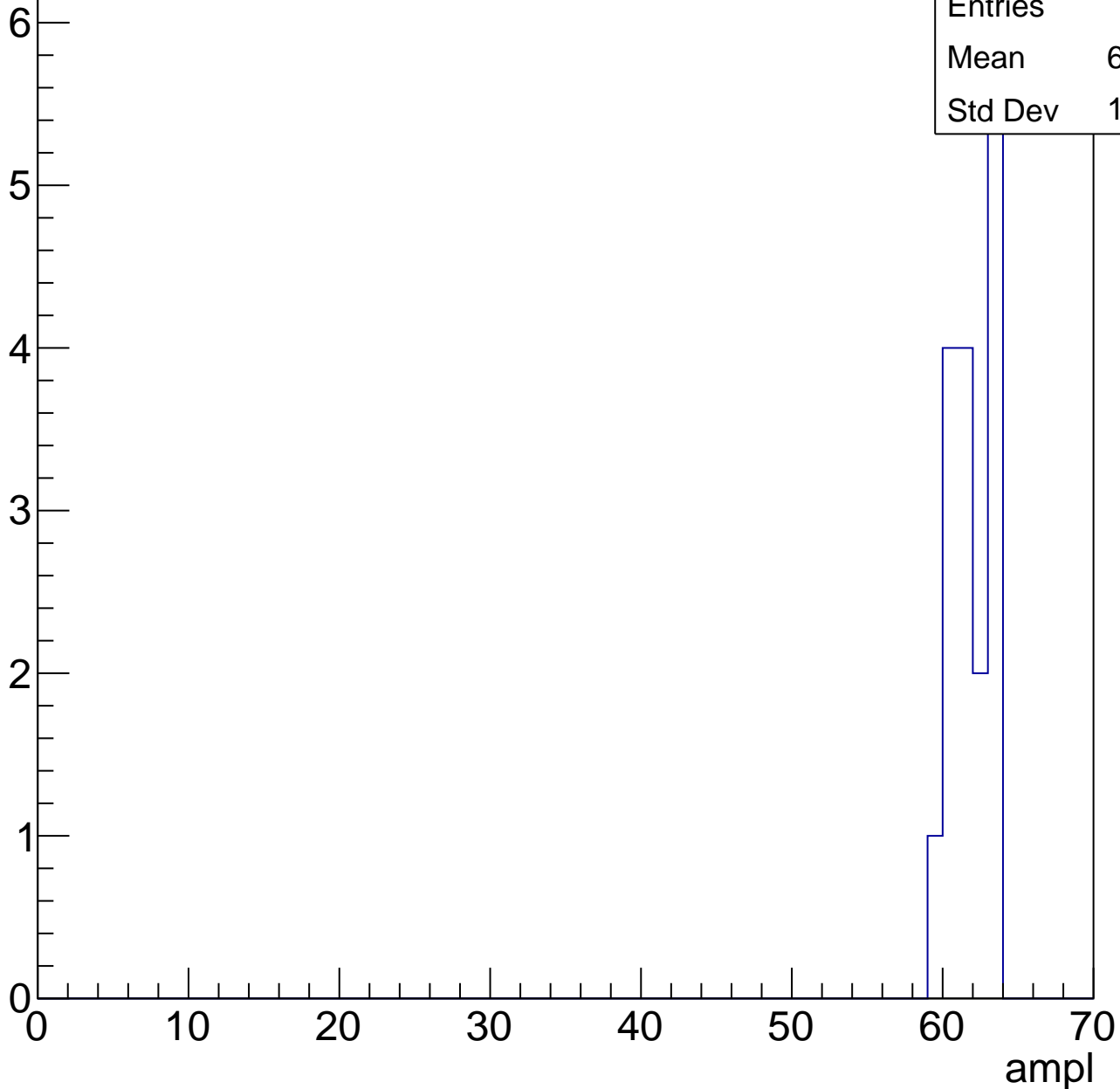


B1L102S, U14-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

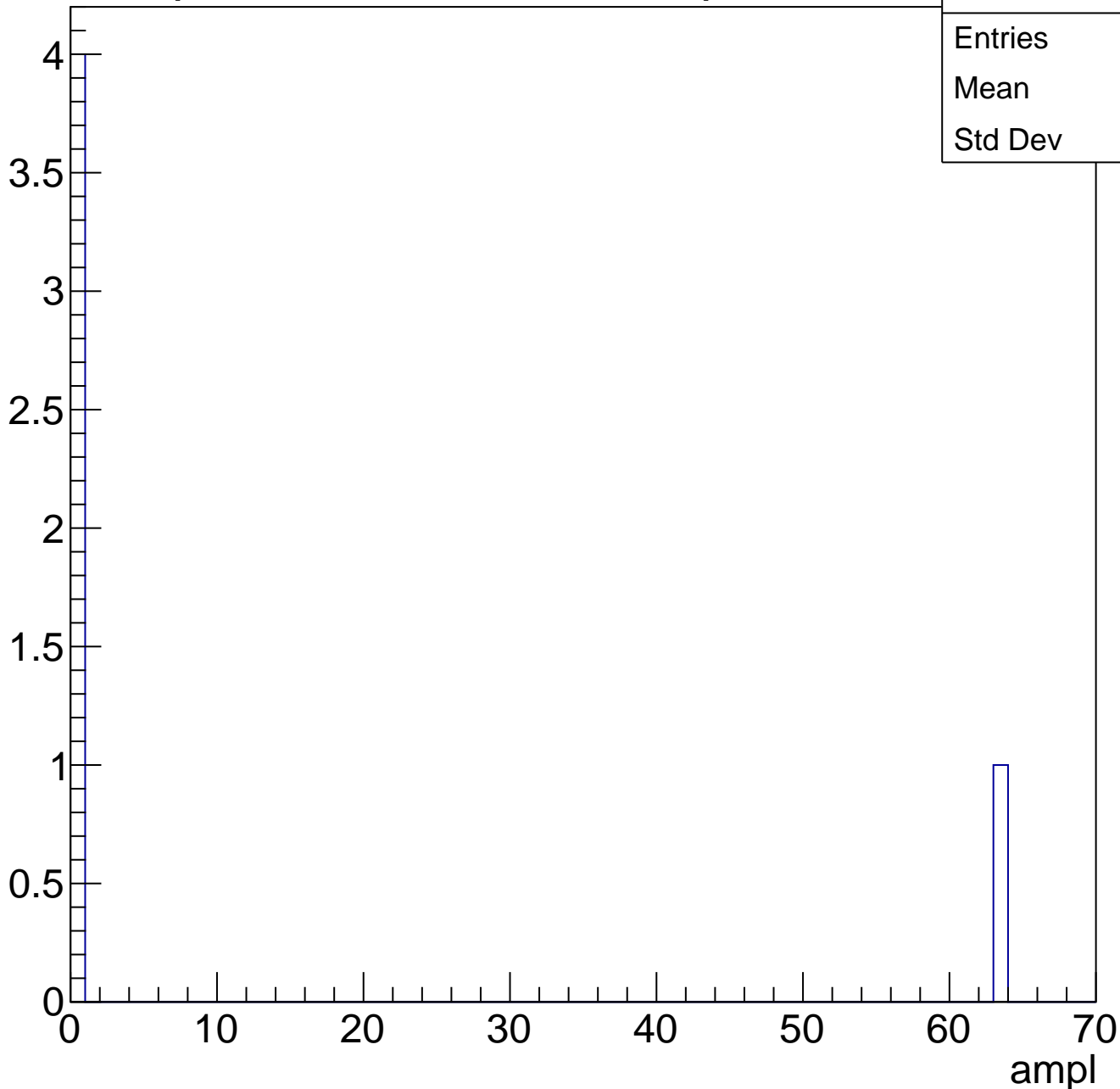
Entries	17
Mean	61.47
Std Dev	1.334



B1L102S, U14-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch83, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	28.9
Std Dev	3.785

Gaus mean : 29.4452

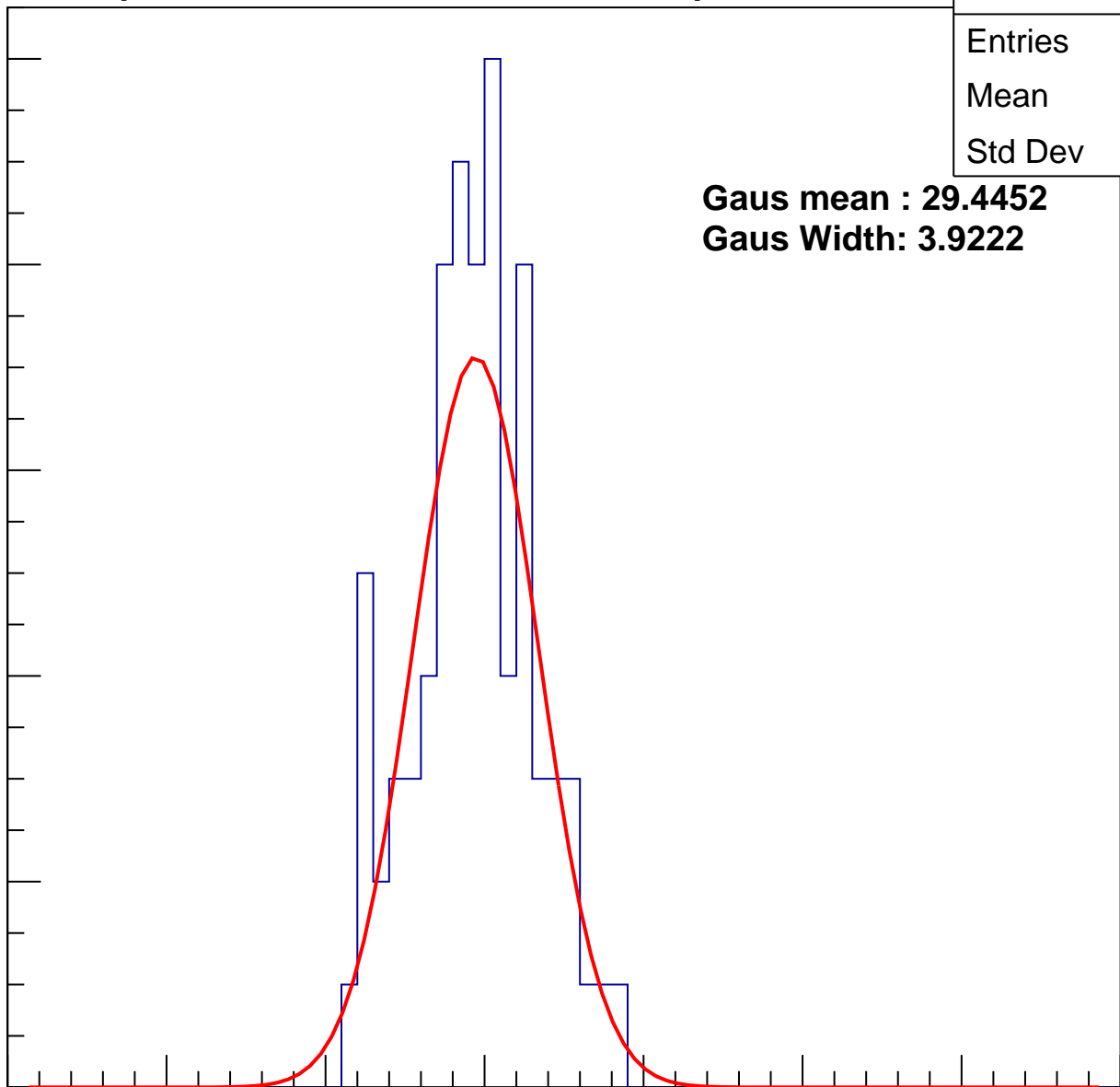
Gaus Width: 3.9222

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



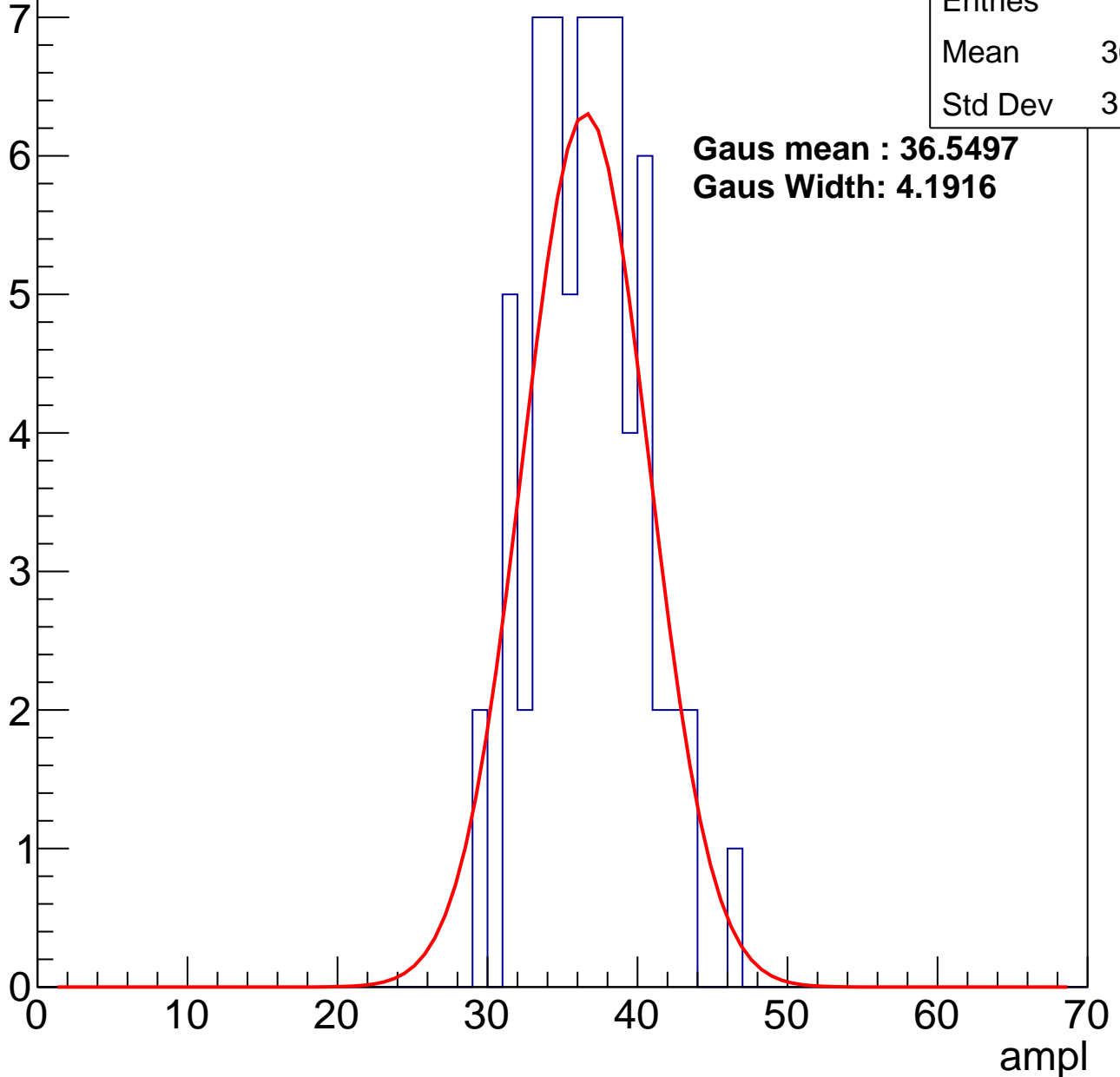
B1L102S, U14-ch83, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.24
Std Dev	3.555

Gaus mean : 36.5497
Gaus Width: 4.1916



B1L102S, U14-ch83, adc2

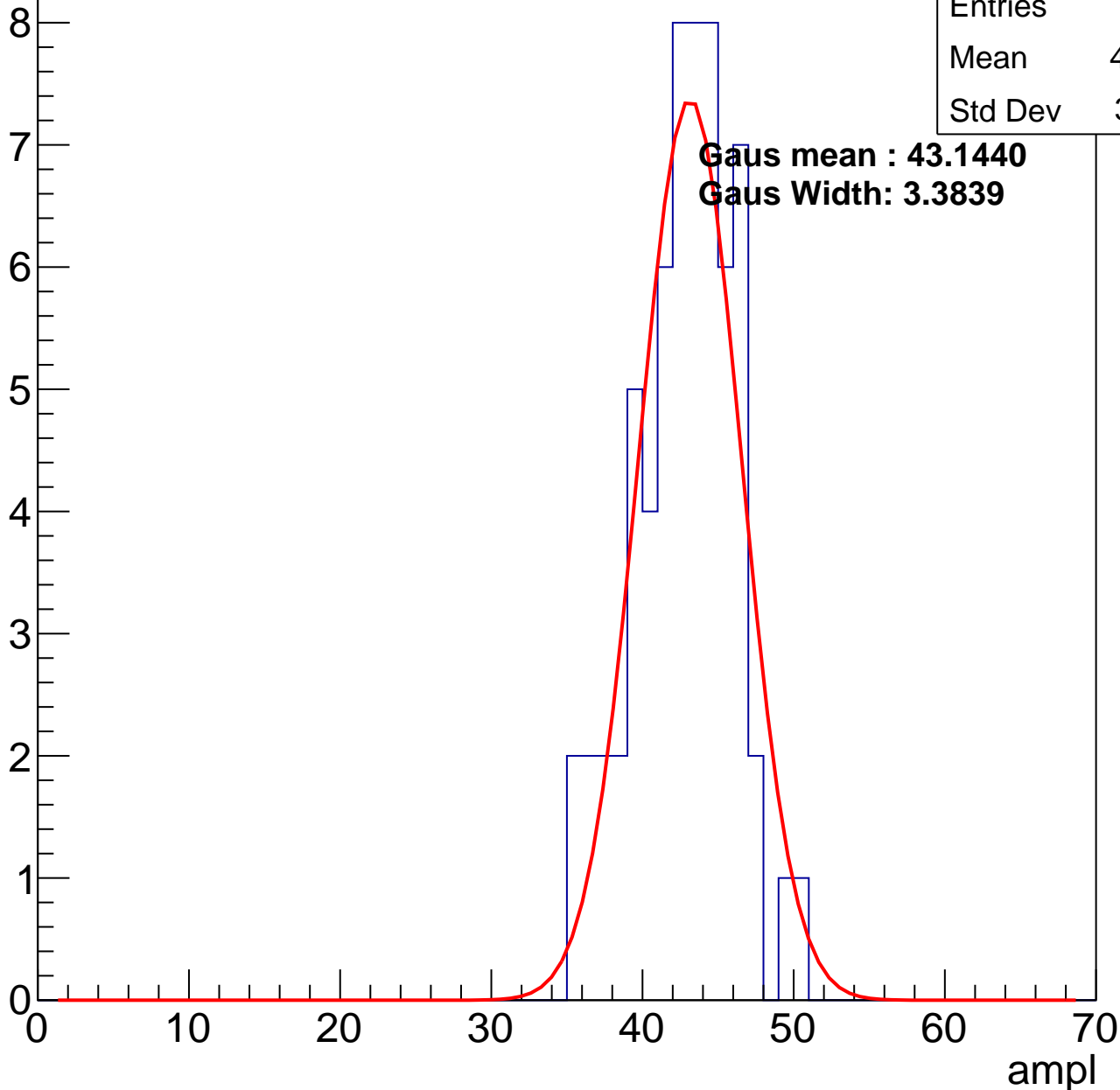
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.34
Std Dev	3.261

Gaus mean : 43.1440

Gaus Width: 3.3839

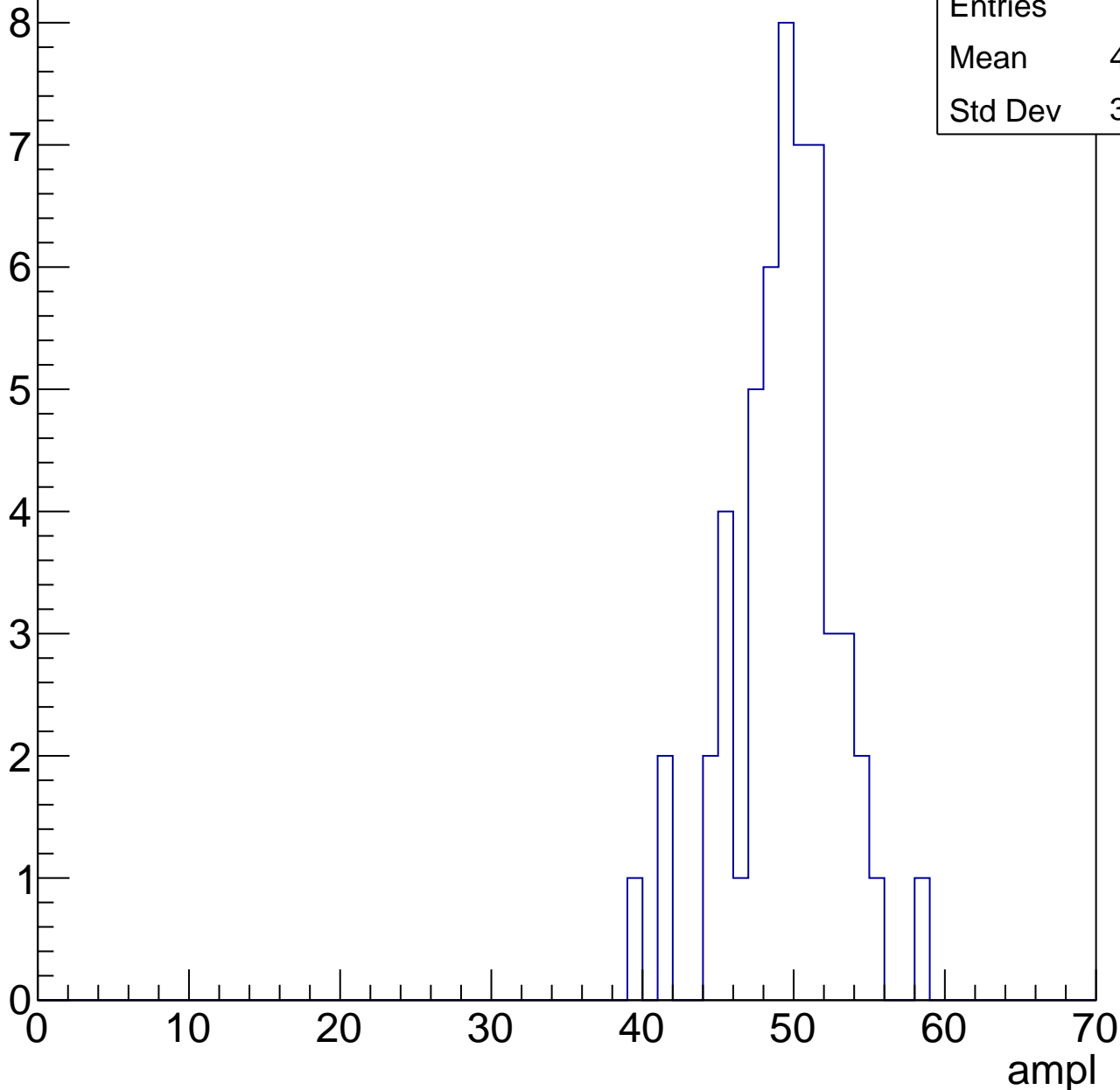


B1L102S, U14-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.92
Std Dev	3.512

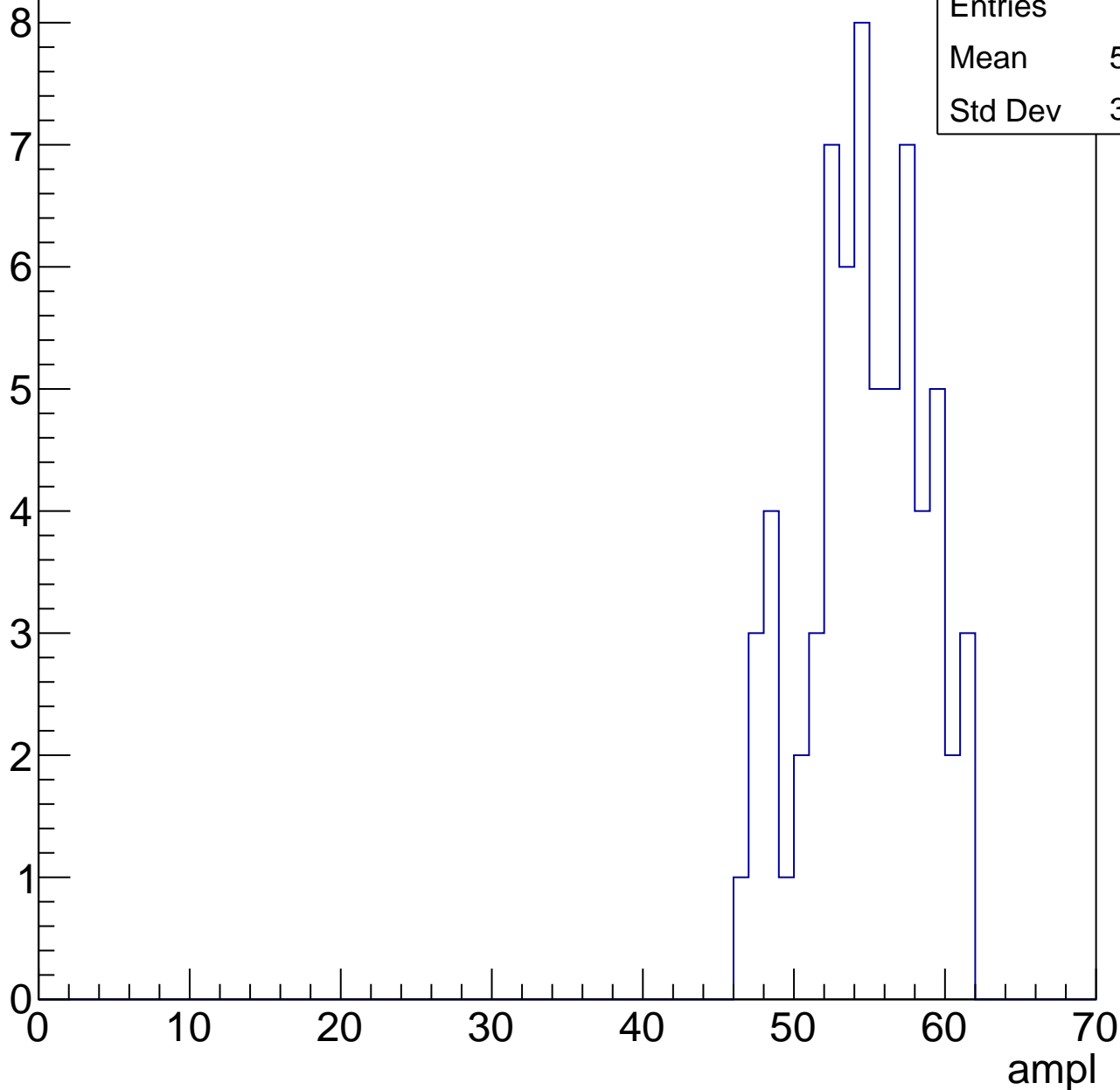


B1L102S, U14-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.23
Std Dev	3.825

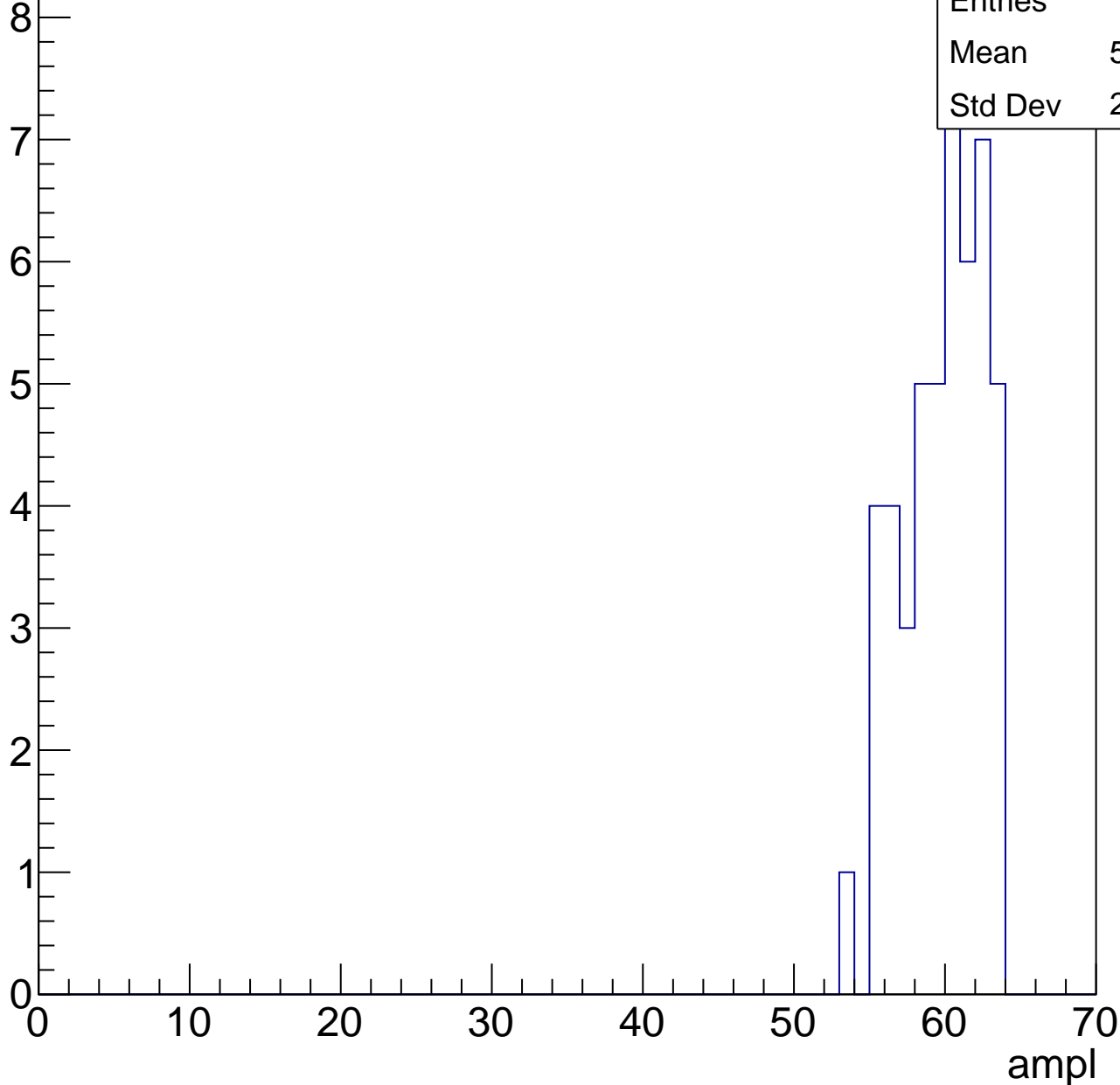


B1L102S, U14-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	59.33
Std Dev	2.593

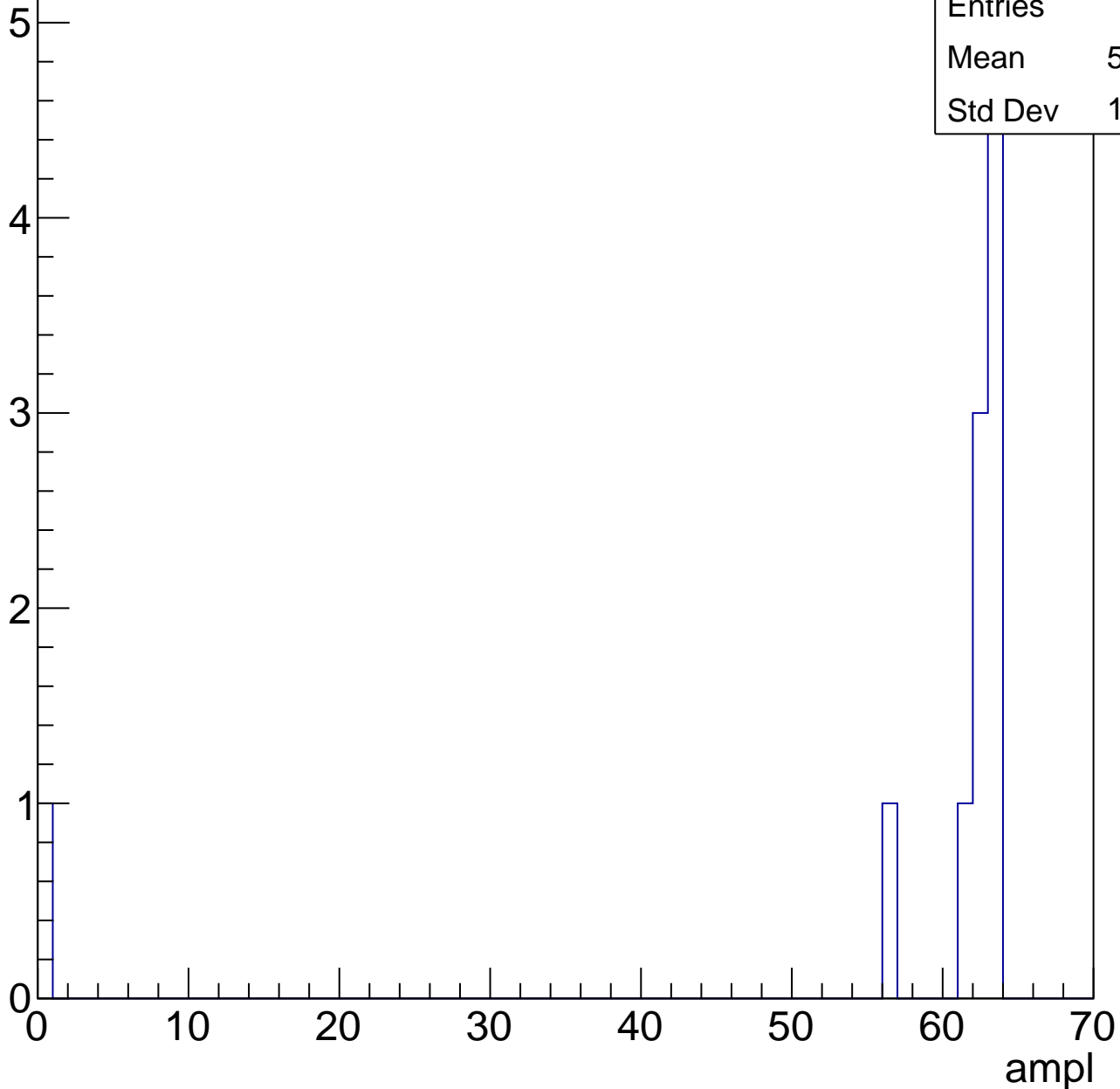


B1L102S, U14-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	56.18
Std Dev	17.87



B1L102S, U14-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch84, adc0

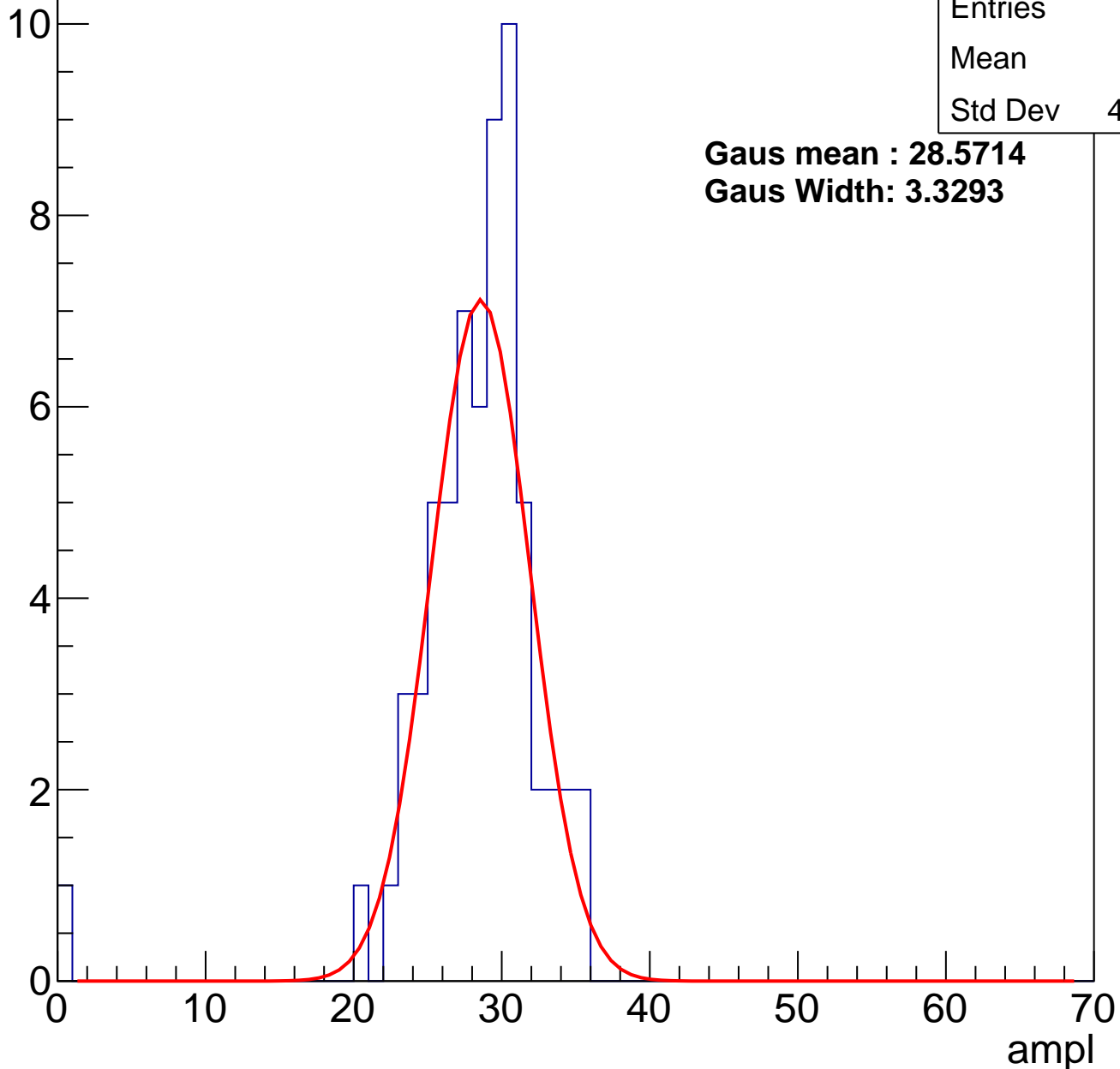
calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	27.8
Std Dev	4.714

Gaus mean : 28.5714

Gaus Width: 3.3293

Entry



B1L102S, U14-ch84, adc1

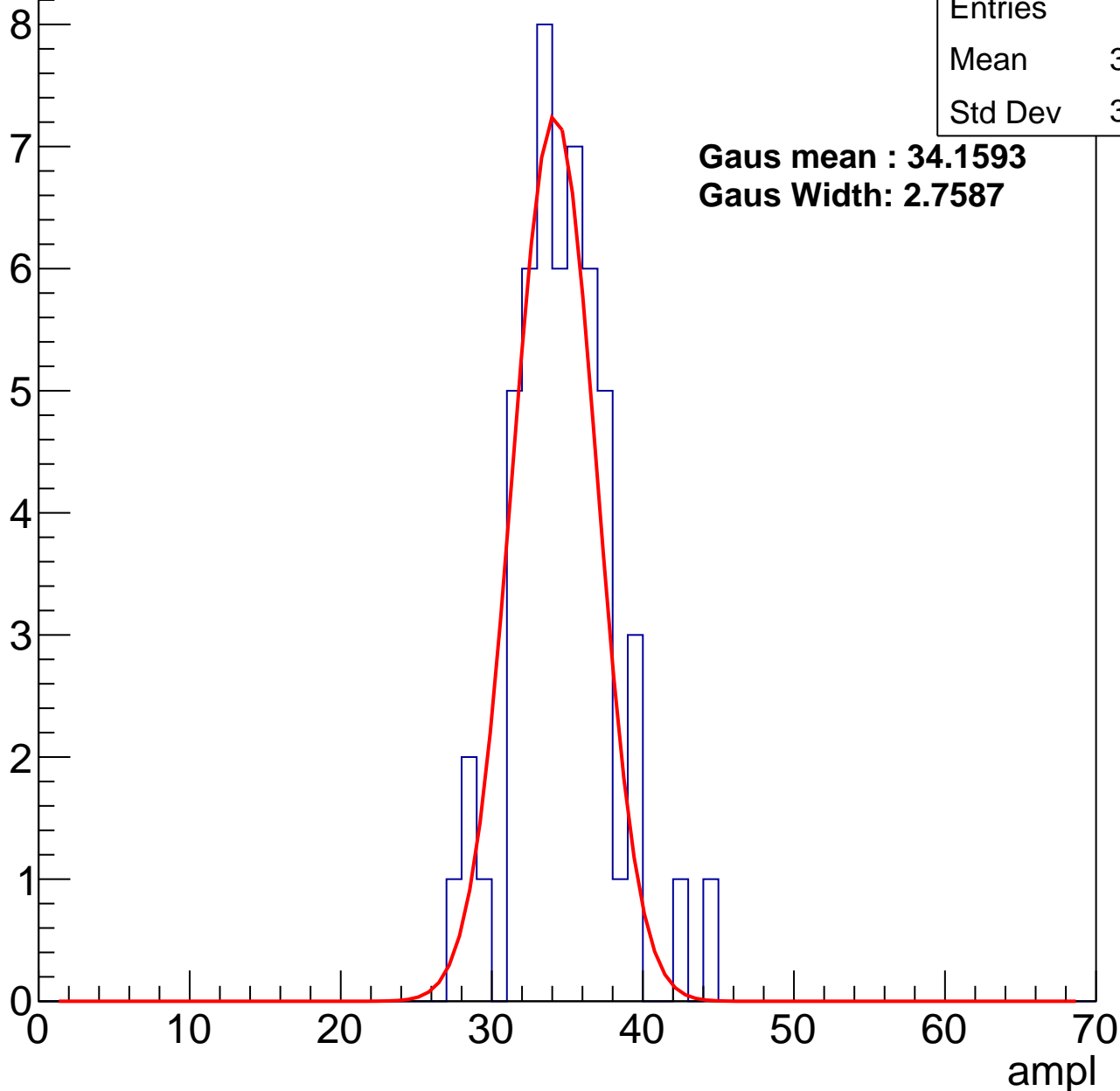
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.23
Std Dev	3.225

Gaus mean : 34.1593

Gaus Width: 2.7587



B1L102S, U14-ch84, adc2

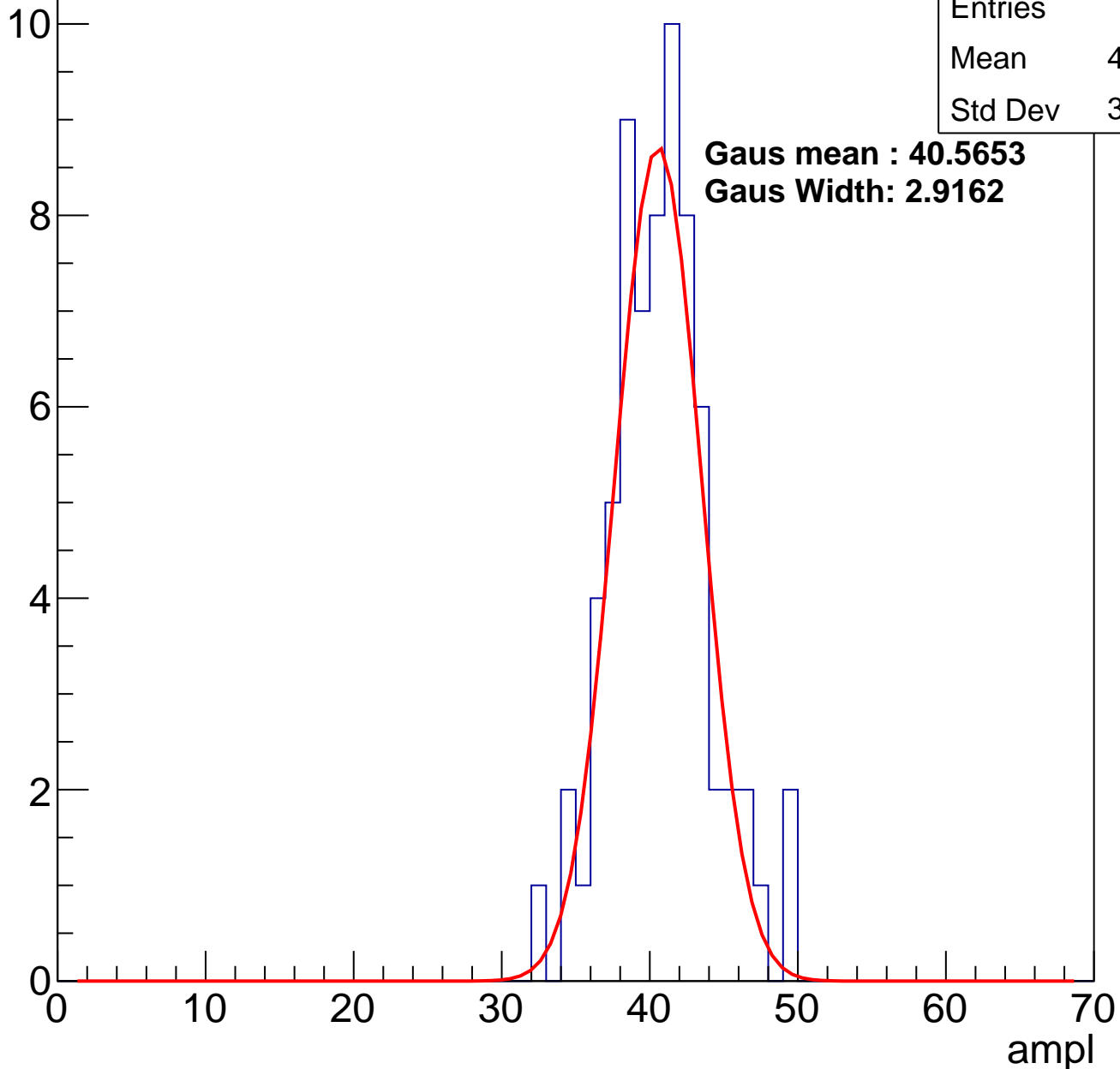
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	40.26
Std Dev	3.315

Gaus mean : 40.5653

Gaus Width: 2.9162

Entry

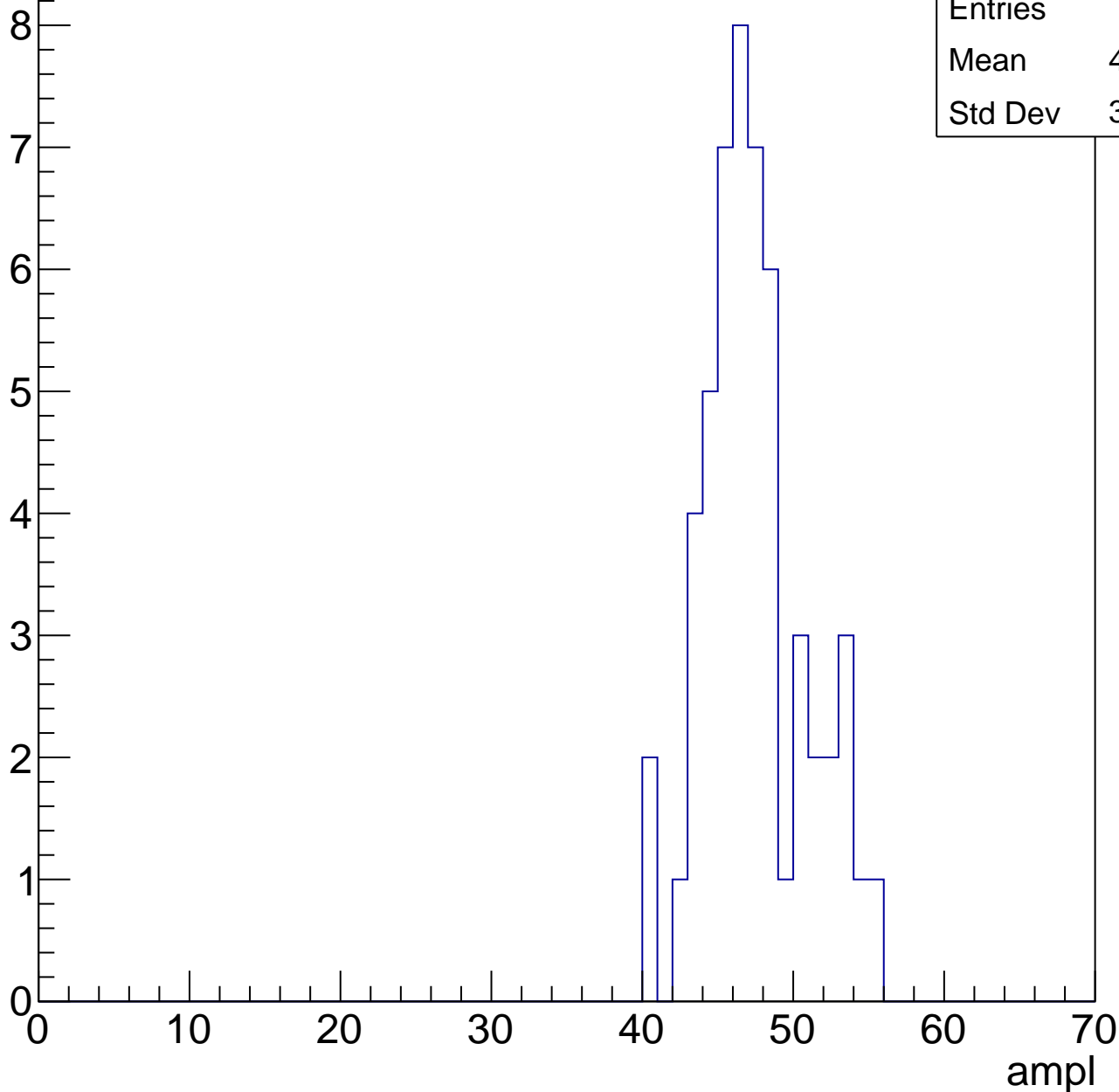


B1L102S, U14-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	46.92
Std Dev	3.386

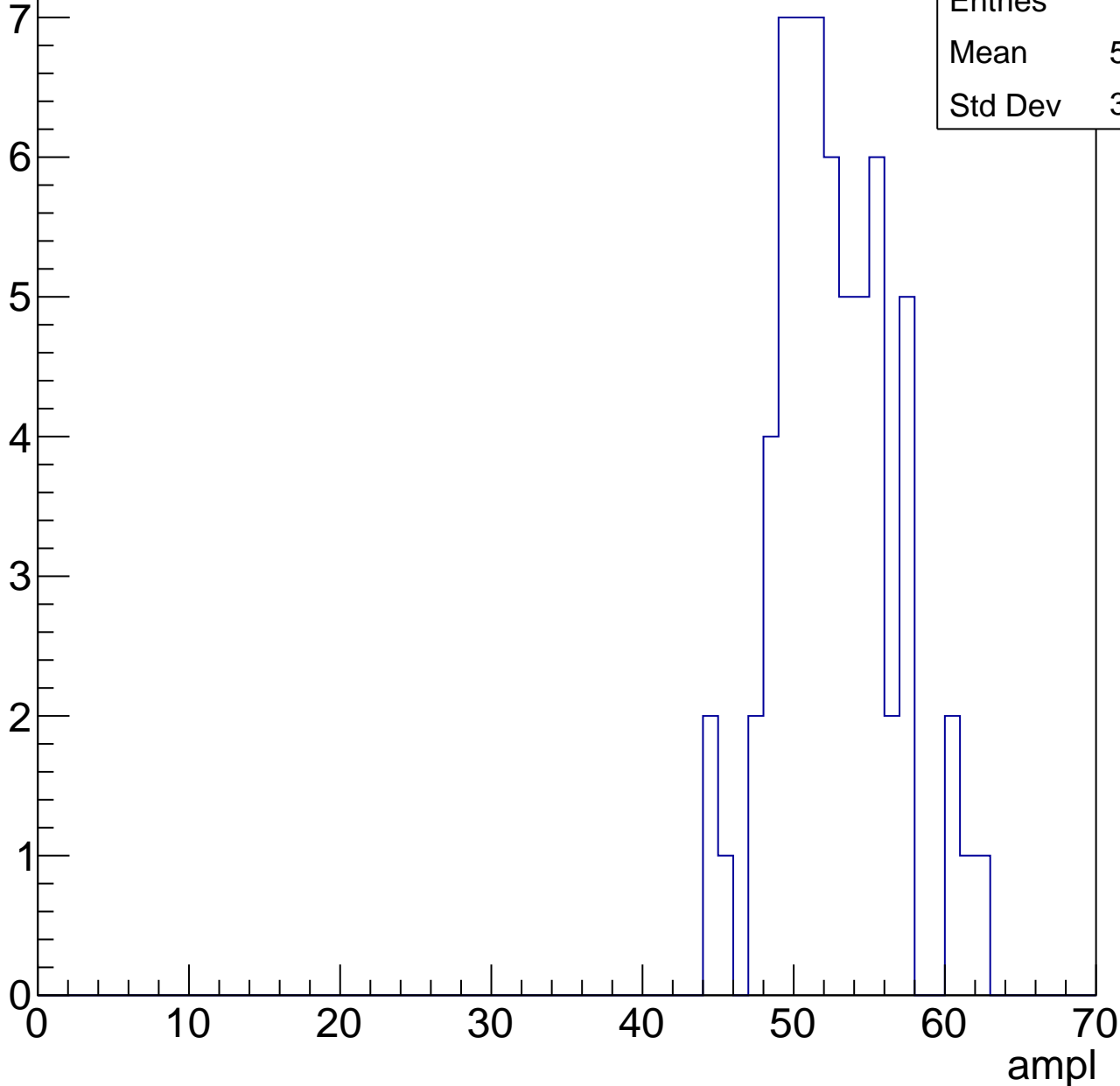


B1L102S, U14-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	52.16
Std Dev	3.847

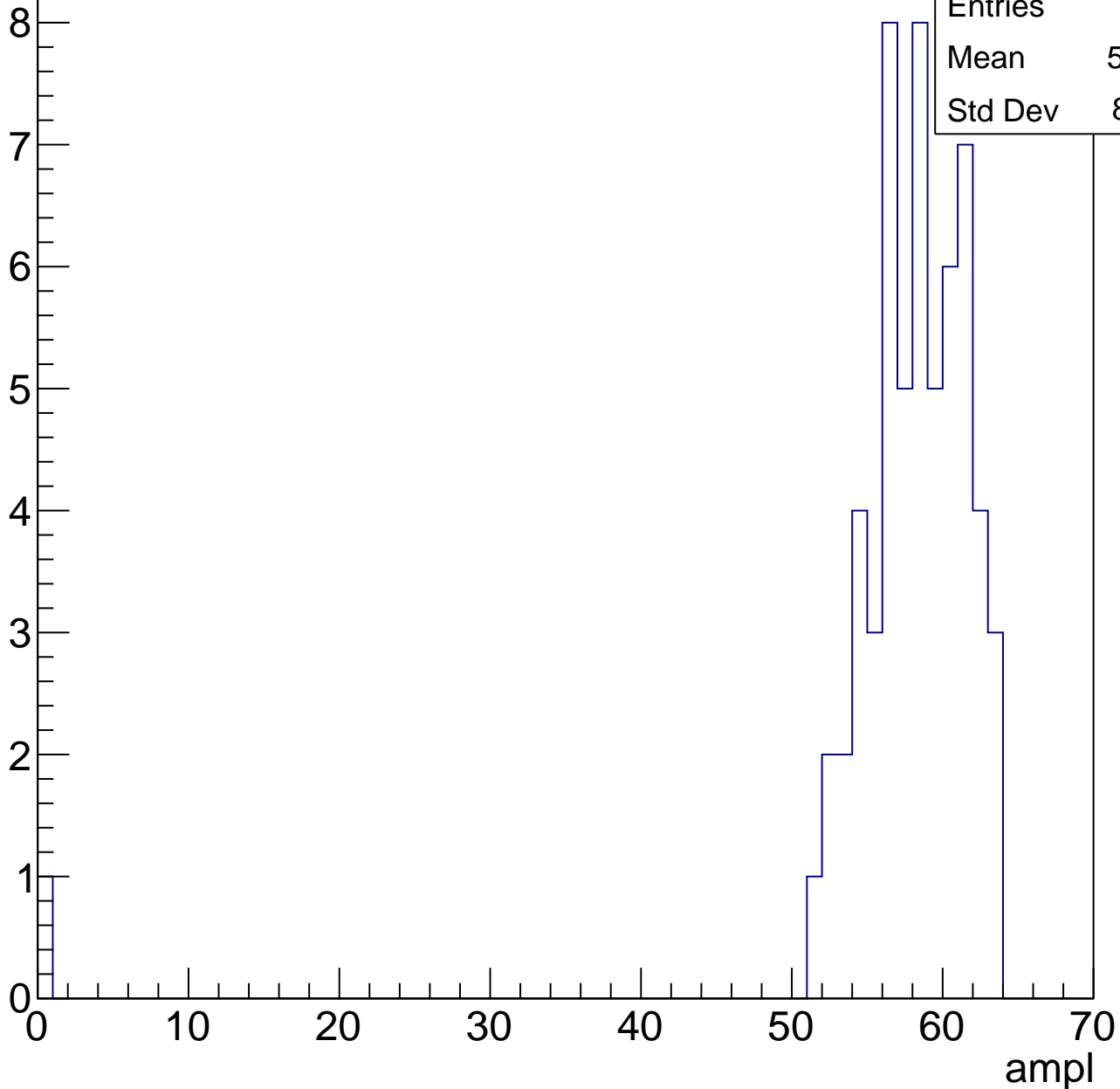


B1L102S, U14-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.92
Std Dev	8.051

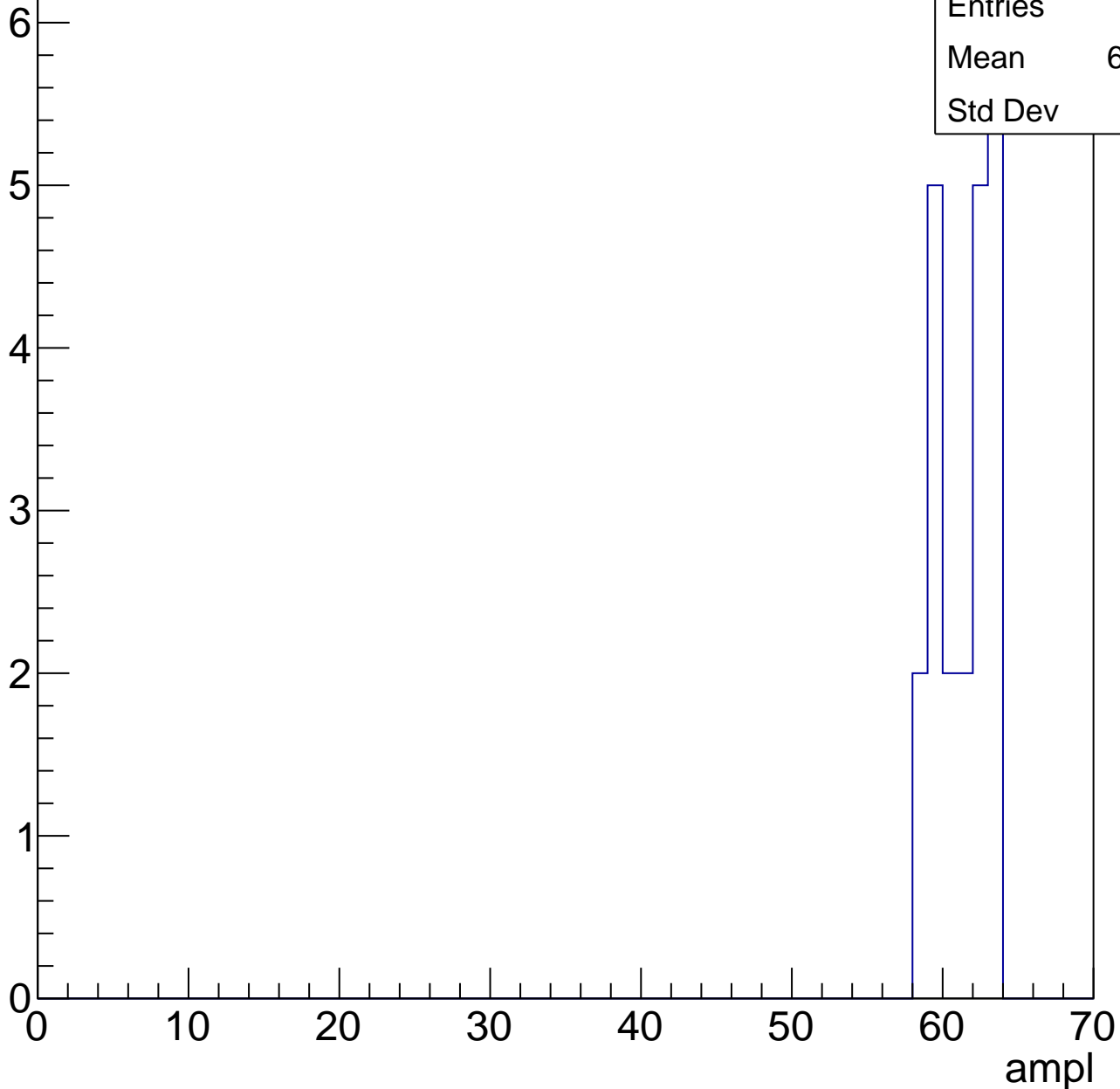


B1L102S, U14-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	60.95
Std Dev	1.77



B1L102S, U14-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	41.5
Std Dev	21.5

ampl

B1L102S, U14-ch85, adc0

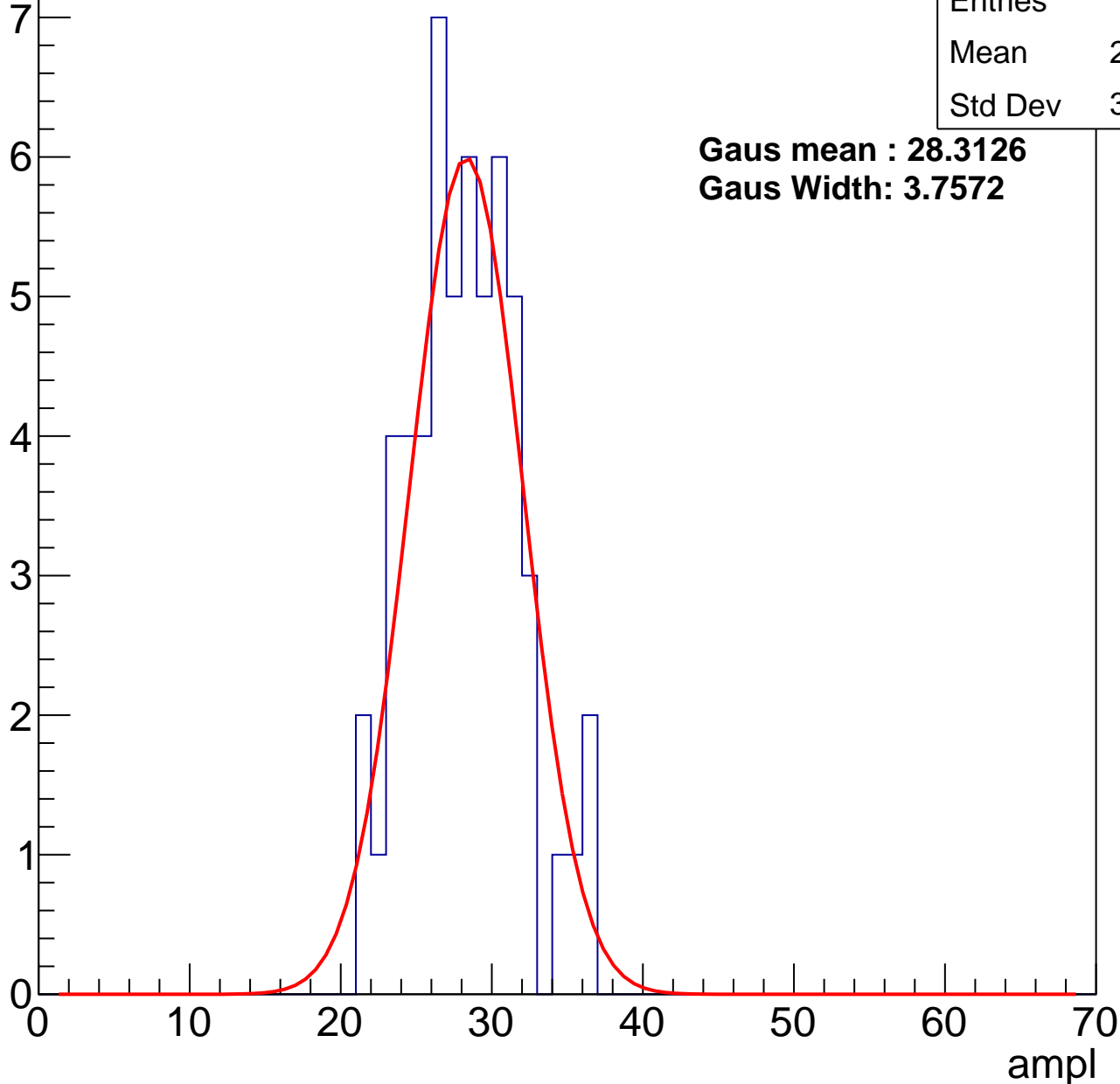
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	27.75
Std Dev	3.532

Gaus mean : 28.3126

Gaus Width: 3.7572



B1L102S, U14-ch85, adc1

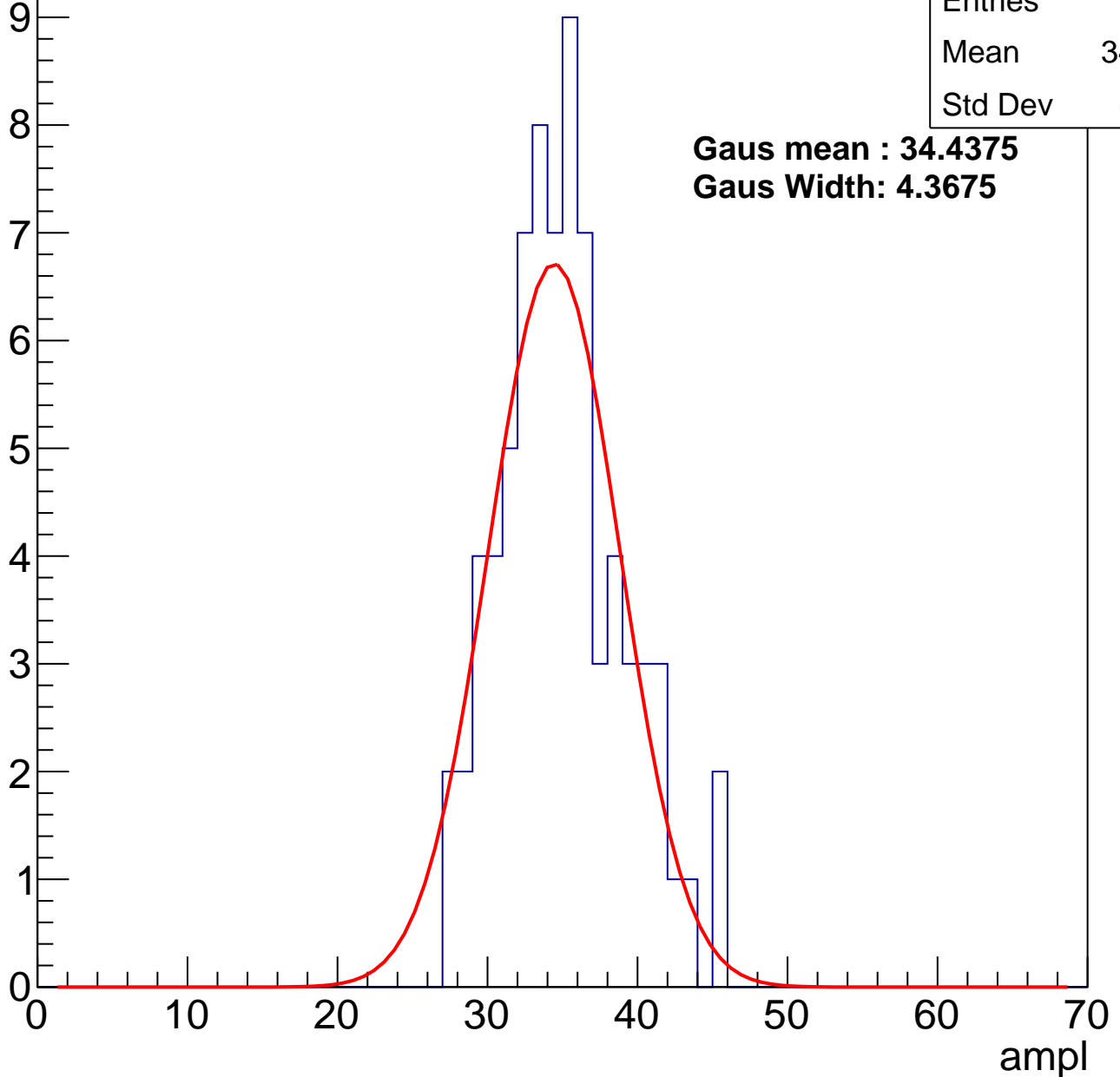
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	34.56
Std Dev	4.06

Gaus mean : 34.4375

Gaus Width: 4.3675



B1L102S, U14-ch85, adc2

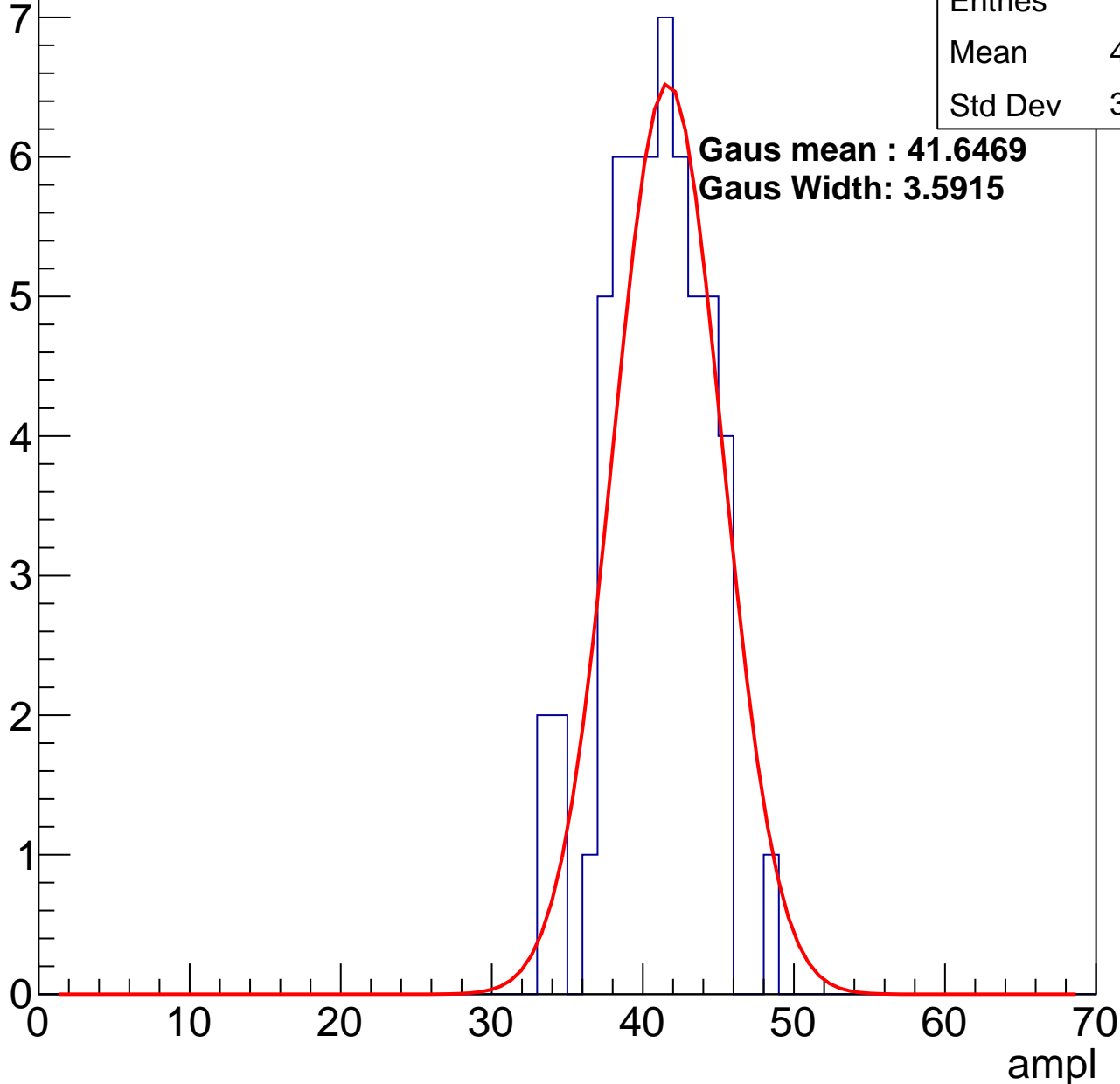
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	40.34
Std Dev	3.203

Gaus mean : 41.6469

Gaus Width: 3.5915



B1L102S, U14-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

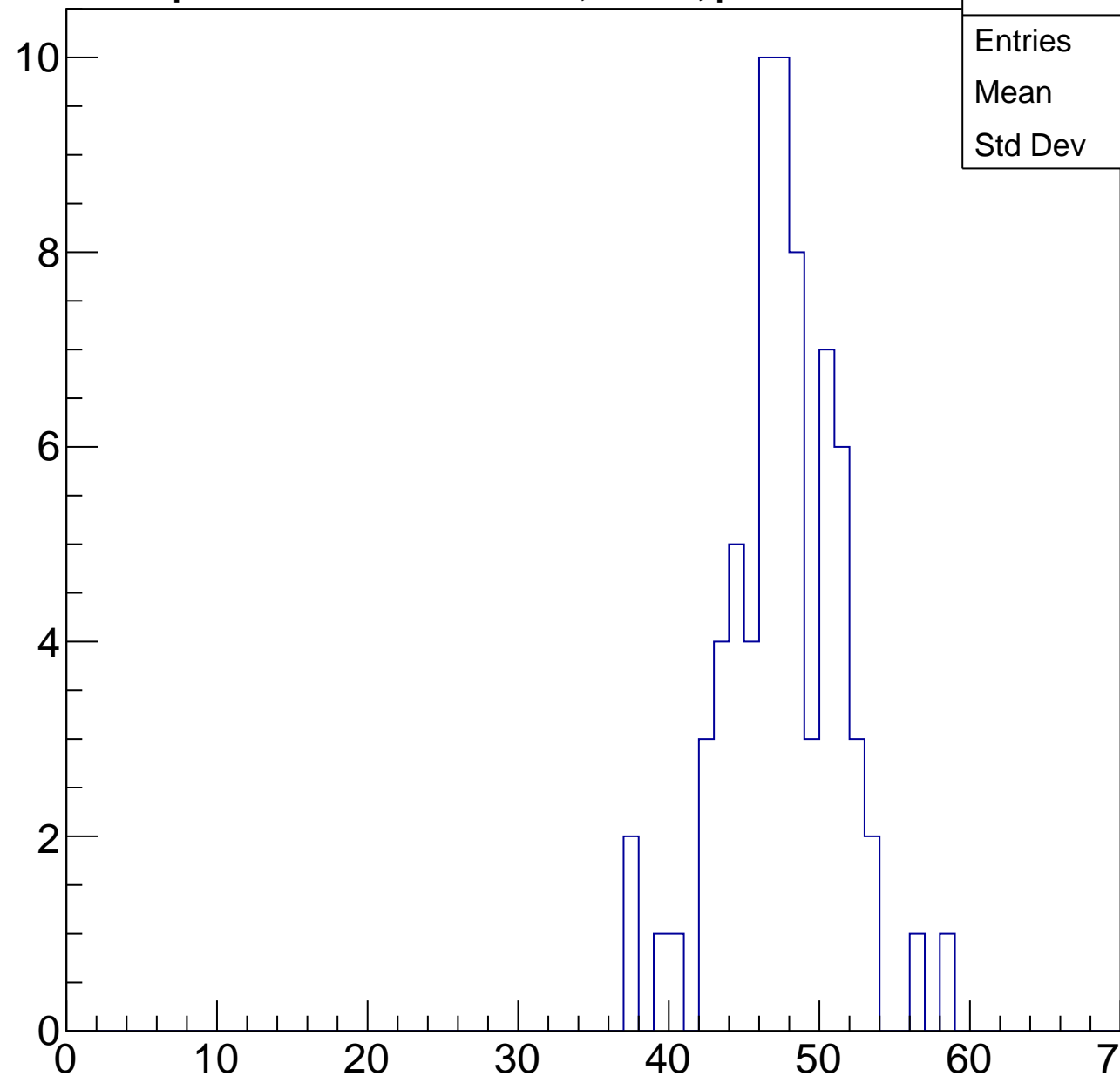
Entries	71
Mean	47.1
Std Dev	3.846

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

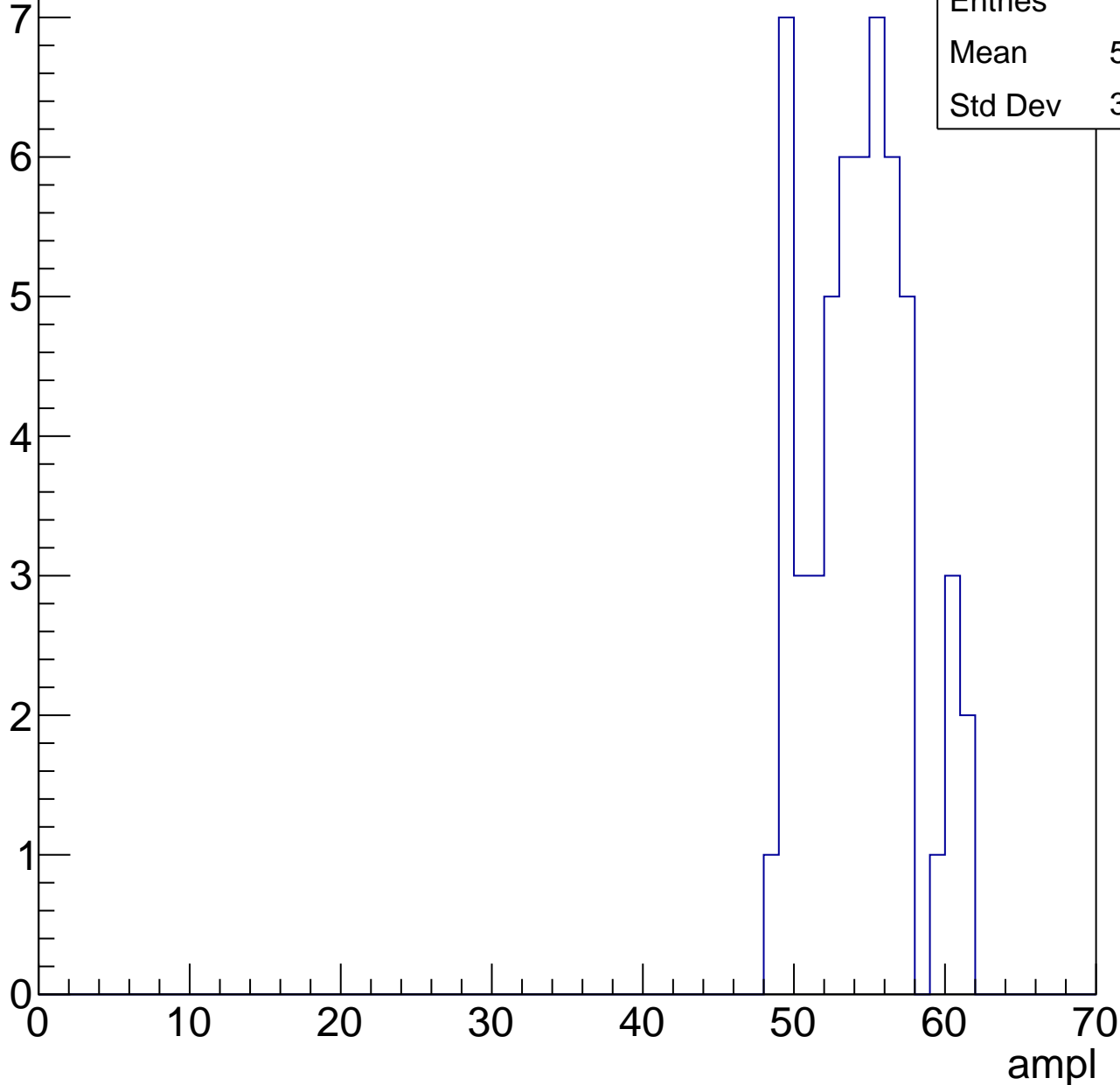
ampl



B1L102S, U14-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

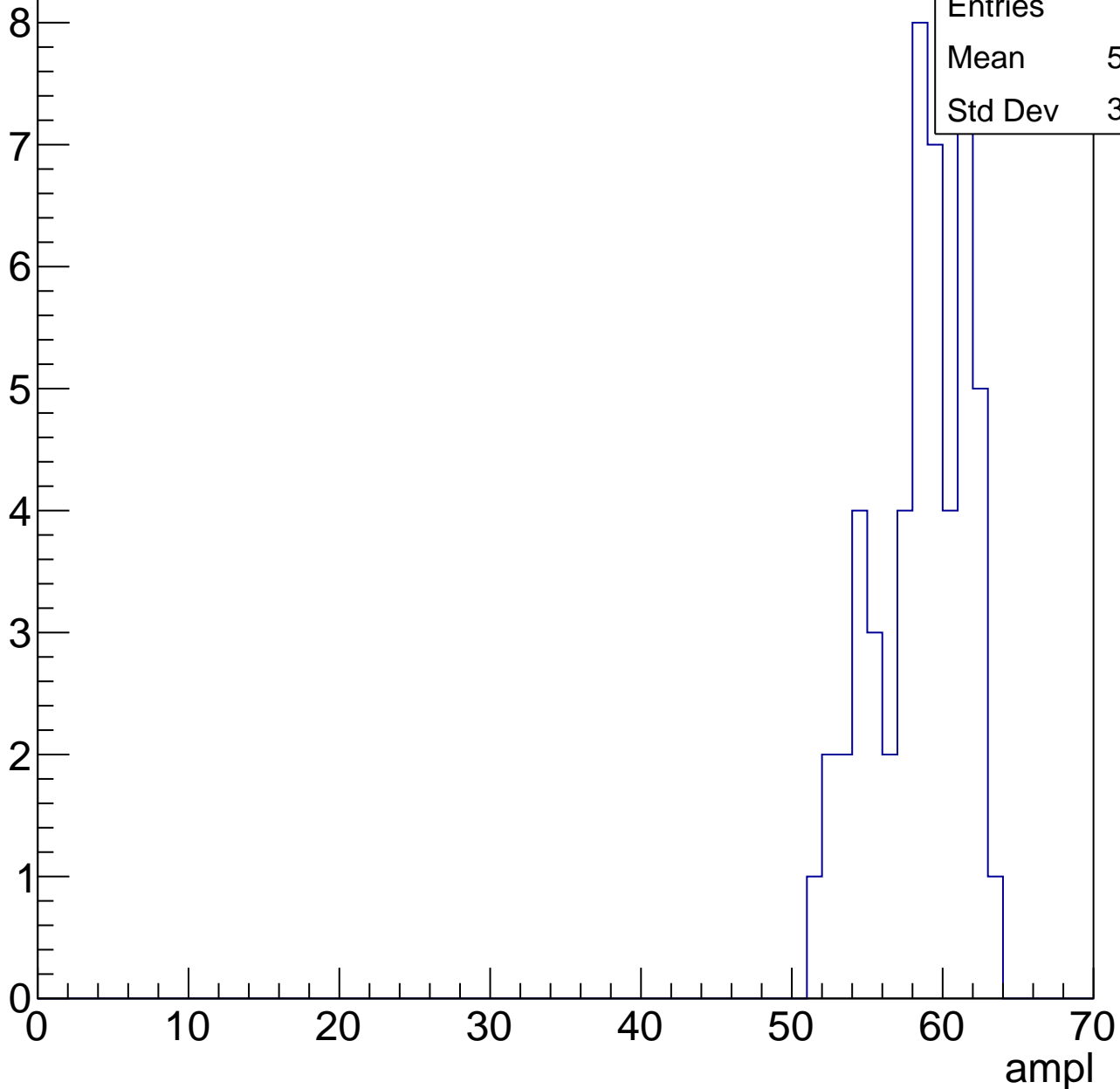


B1L102S, U14-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.04
Std Dev	3.048

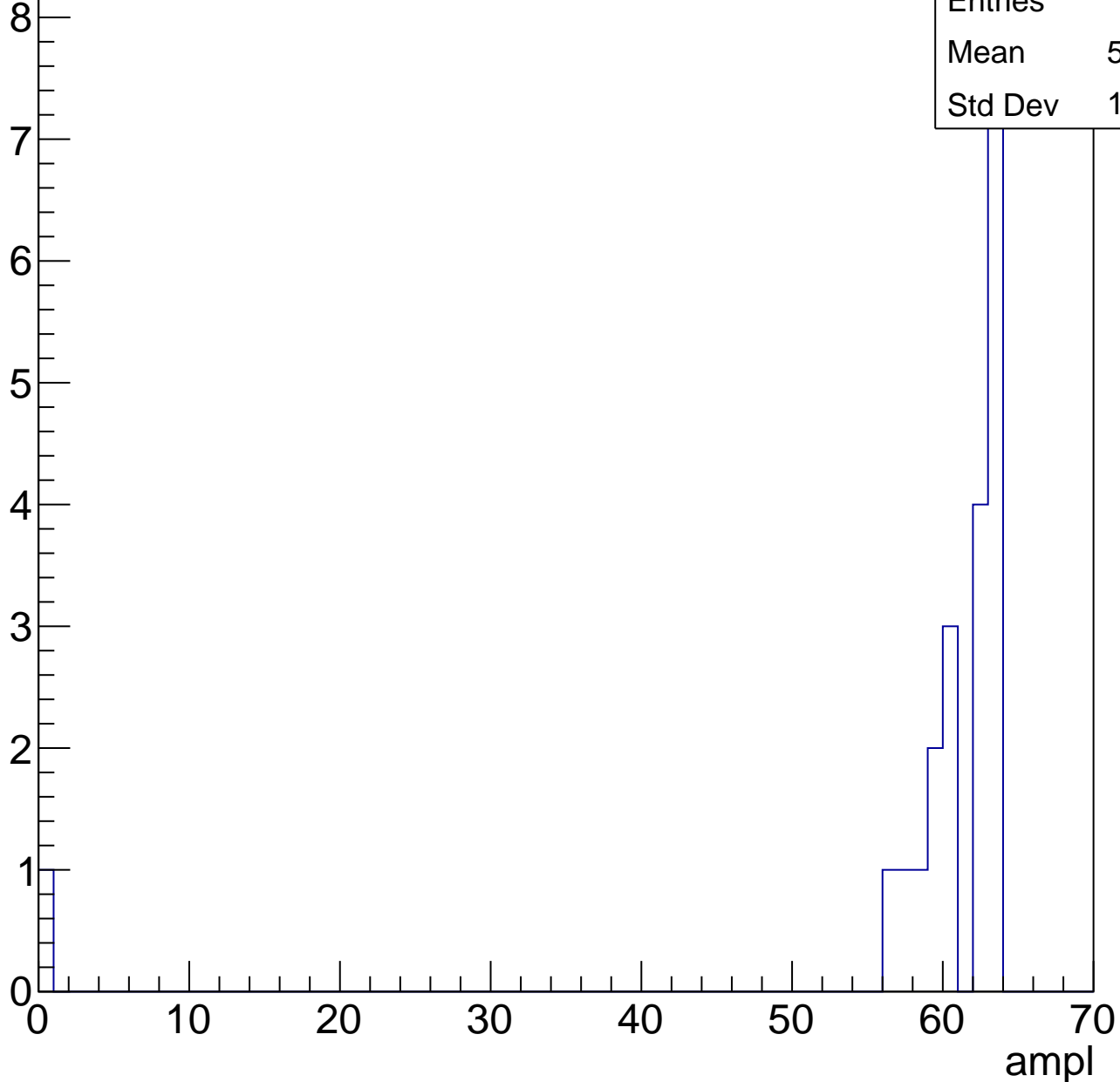


B1L102S, U14-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	58.14
Std Dev	13.18



B1L102S, U14-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch86, adc0

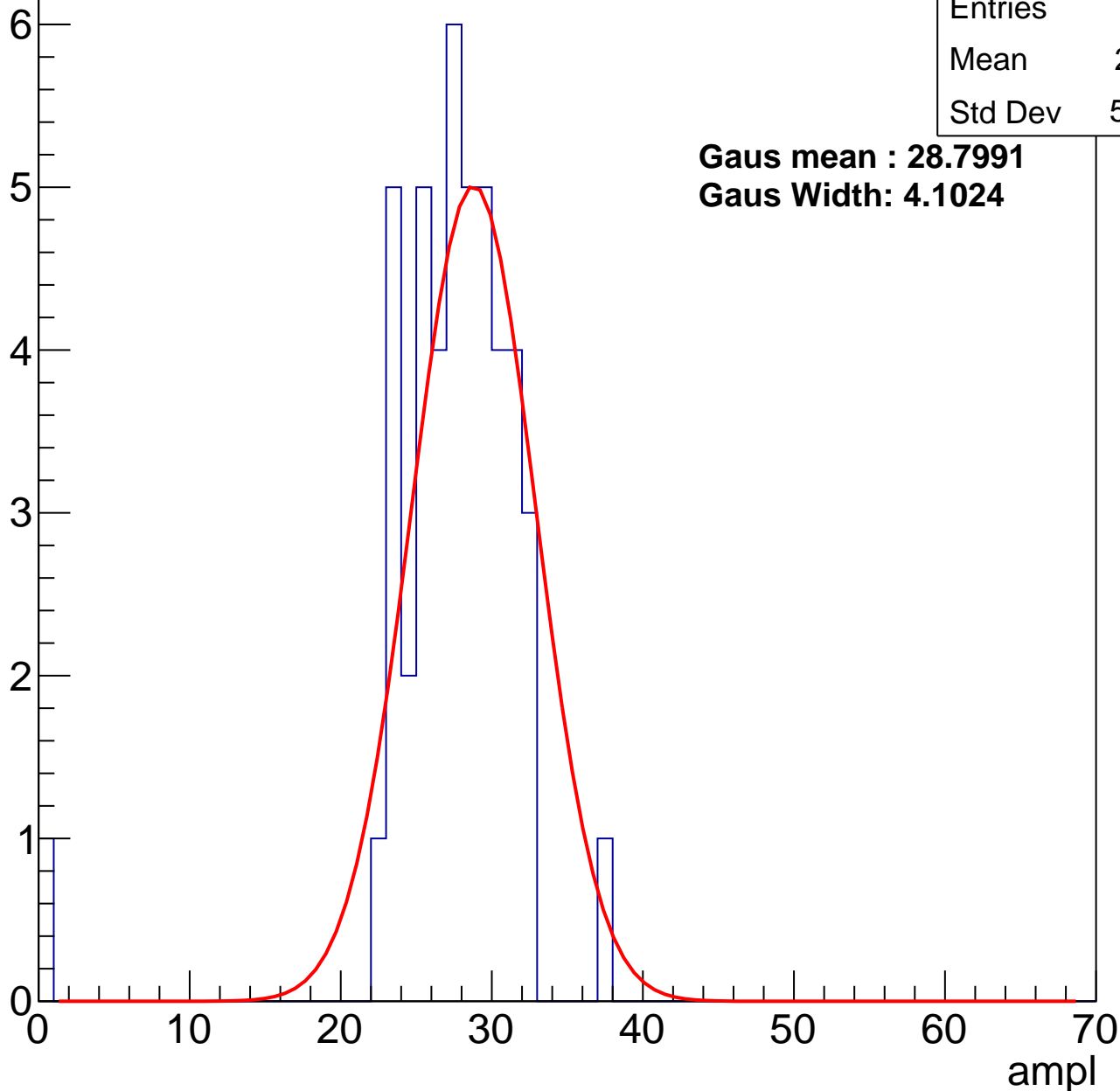
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	26.91
Std Dev	5.055

Gaus mean : 28.7991

Gaus Width: 4.1024



B1L102S, U14-ch86, adc1

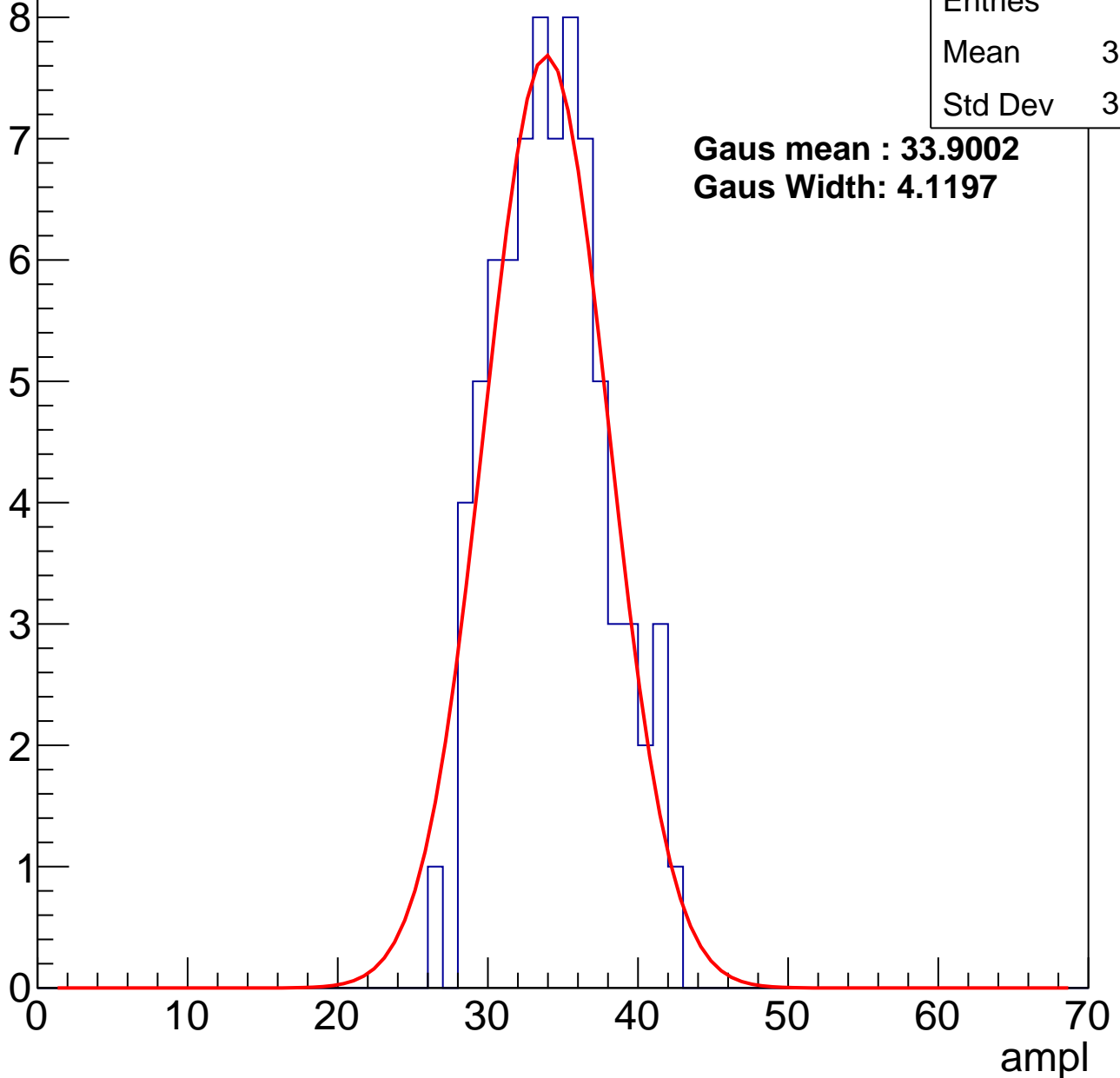
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	33.79
Std Dev	3.632

Gaus mean : 33.9002

Gaus Width: 4.1197



B1L102S, U14-ch86, adc2

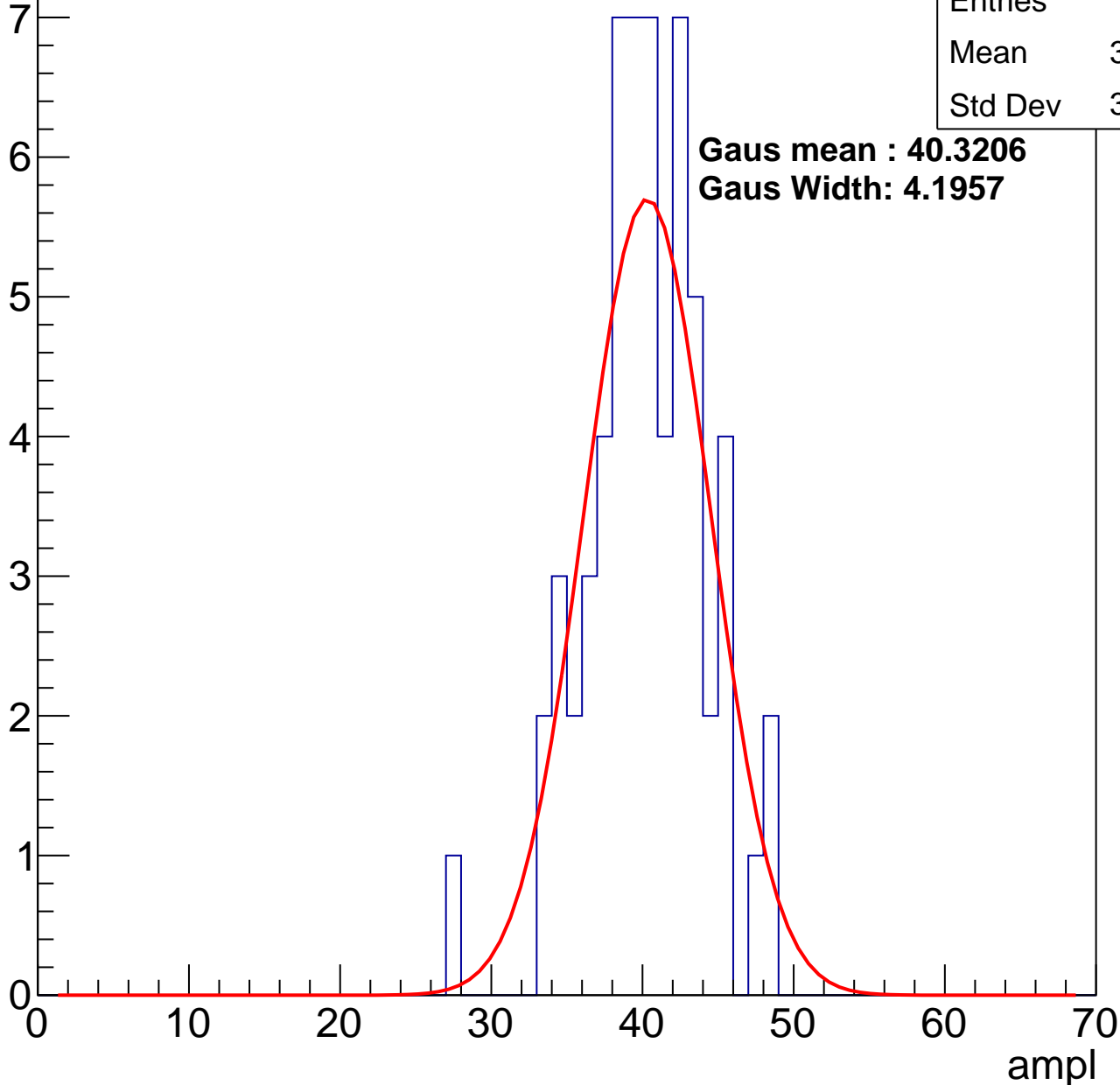
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	39.74
Std Dev	3.896

Gaus mean : 40.3206

Gaus Width: 4.1957

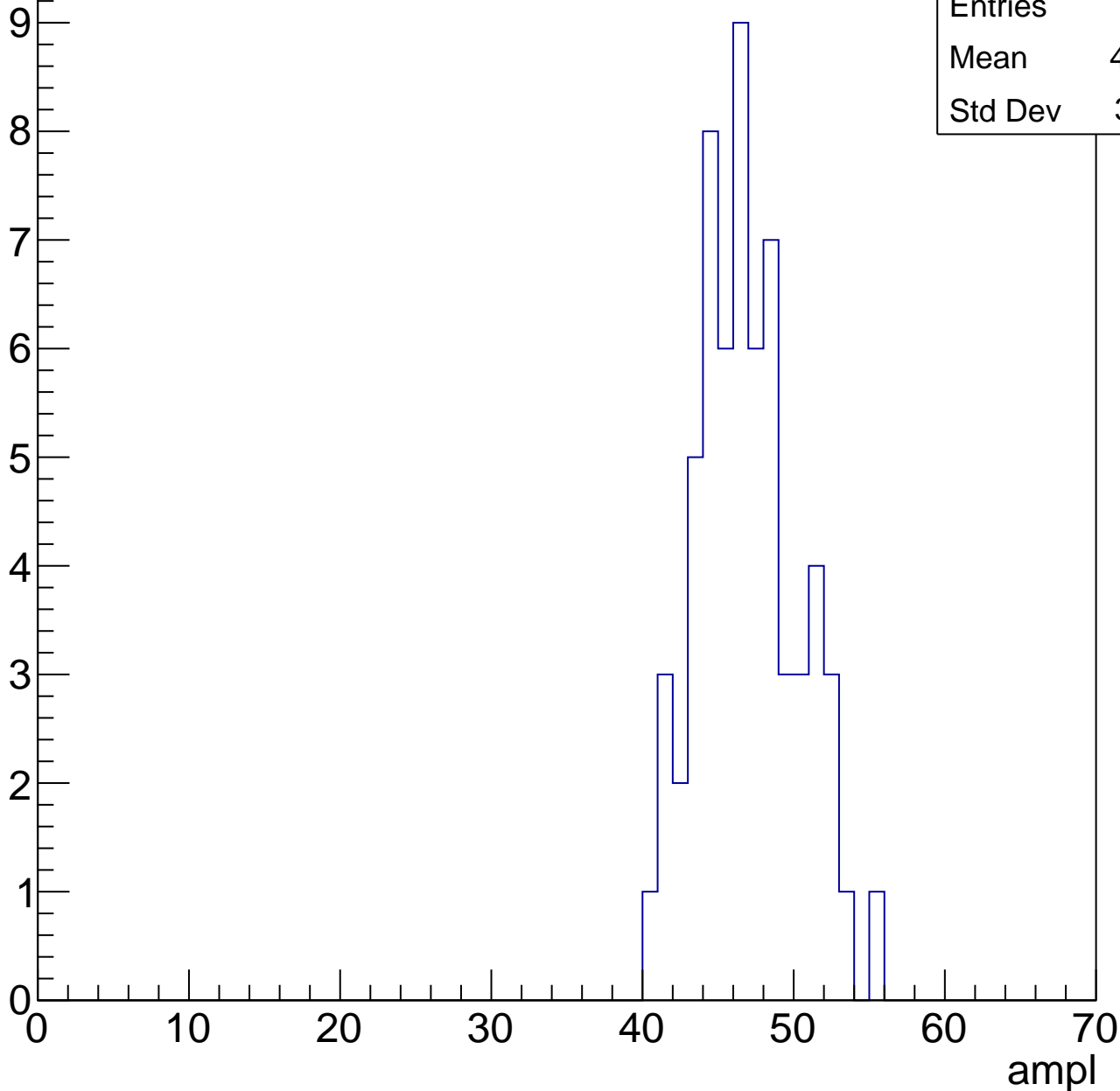


B1L102S, U14-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	46.47
Std Dev	3.261

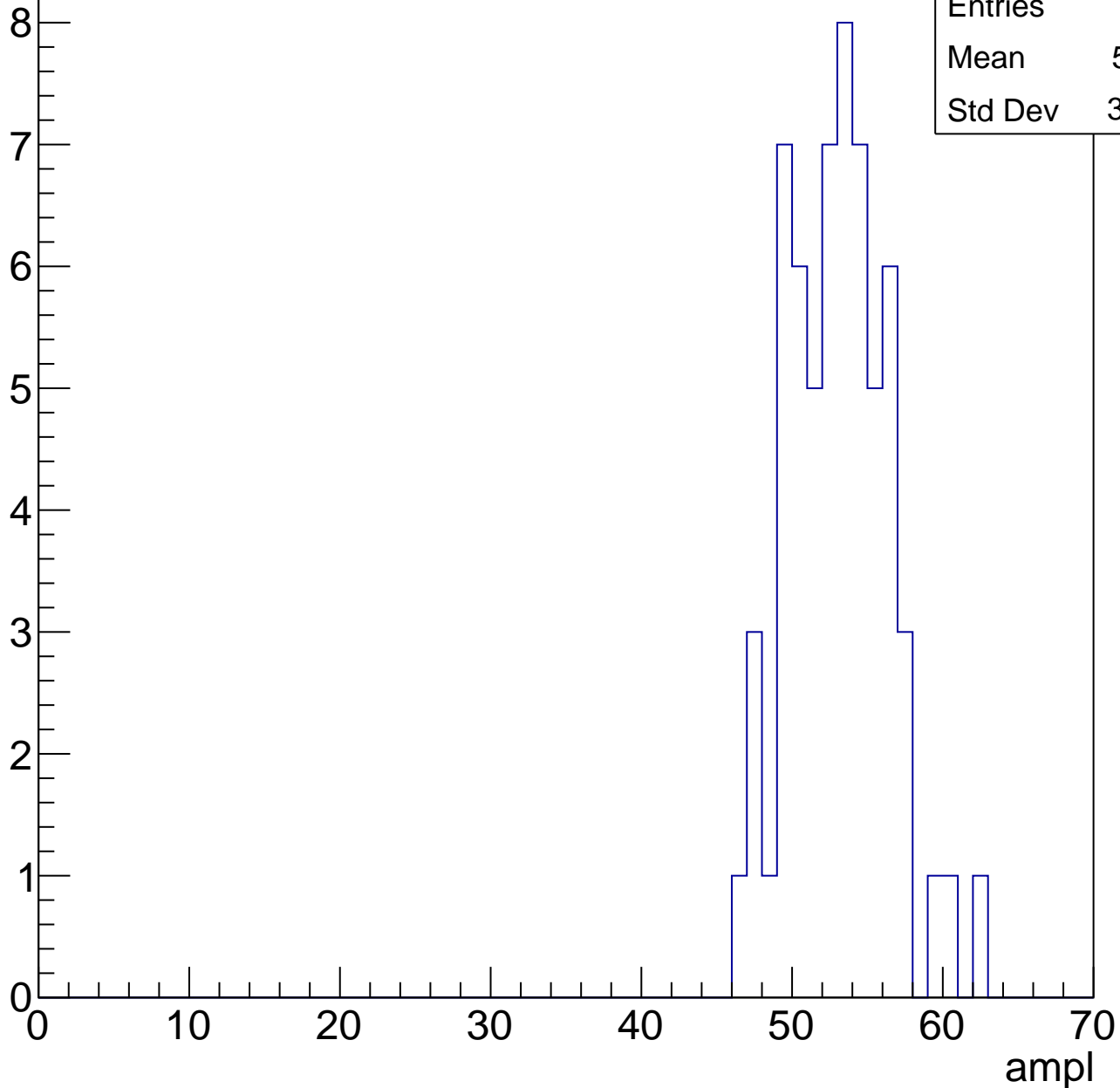


B1L102S, U14-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	52.61
Std Dev	3.264

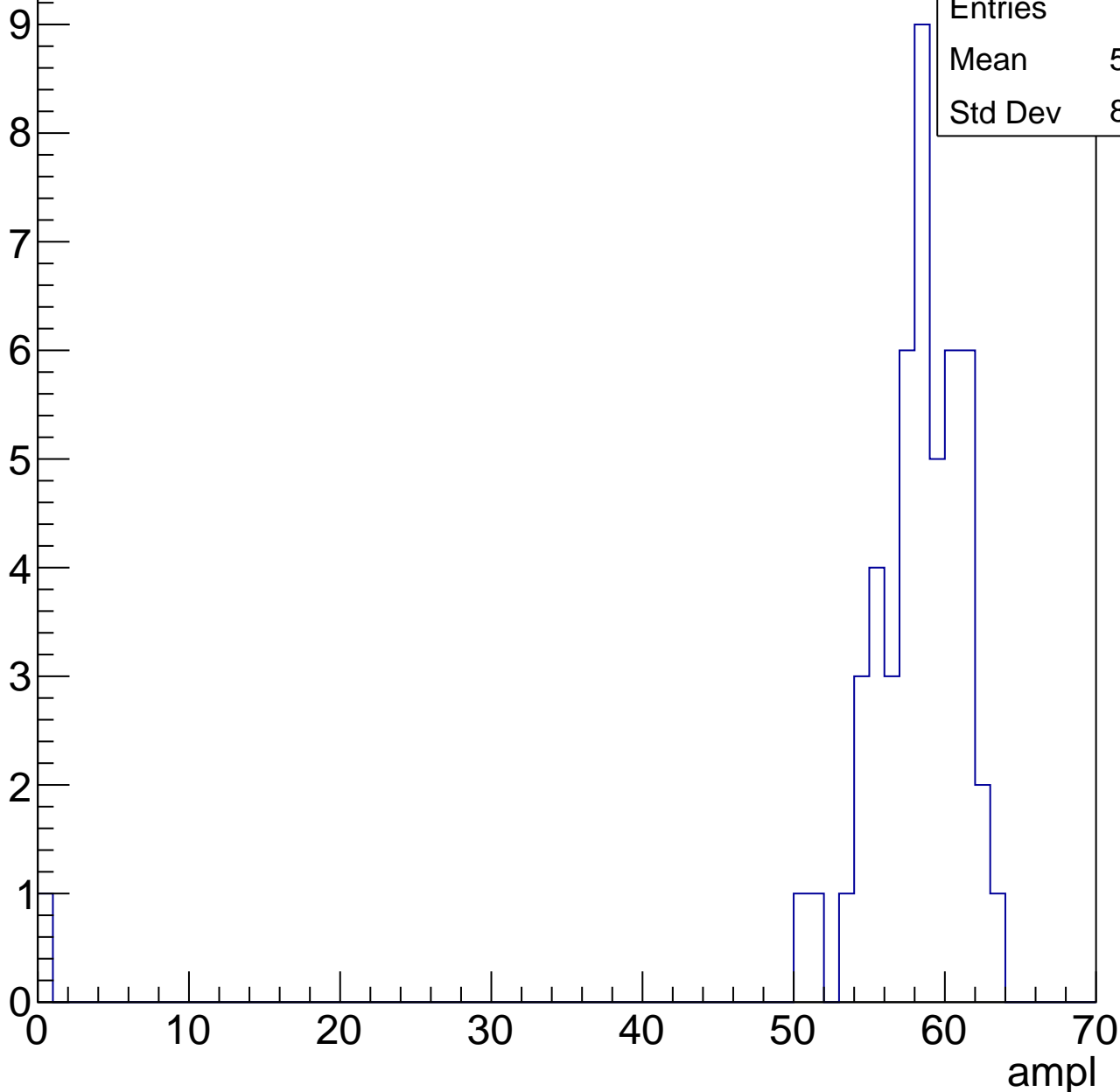


B1L102S, U14-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.65
Std Dev	8.637

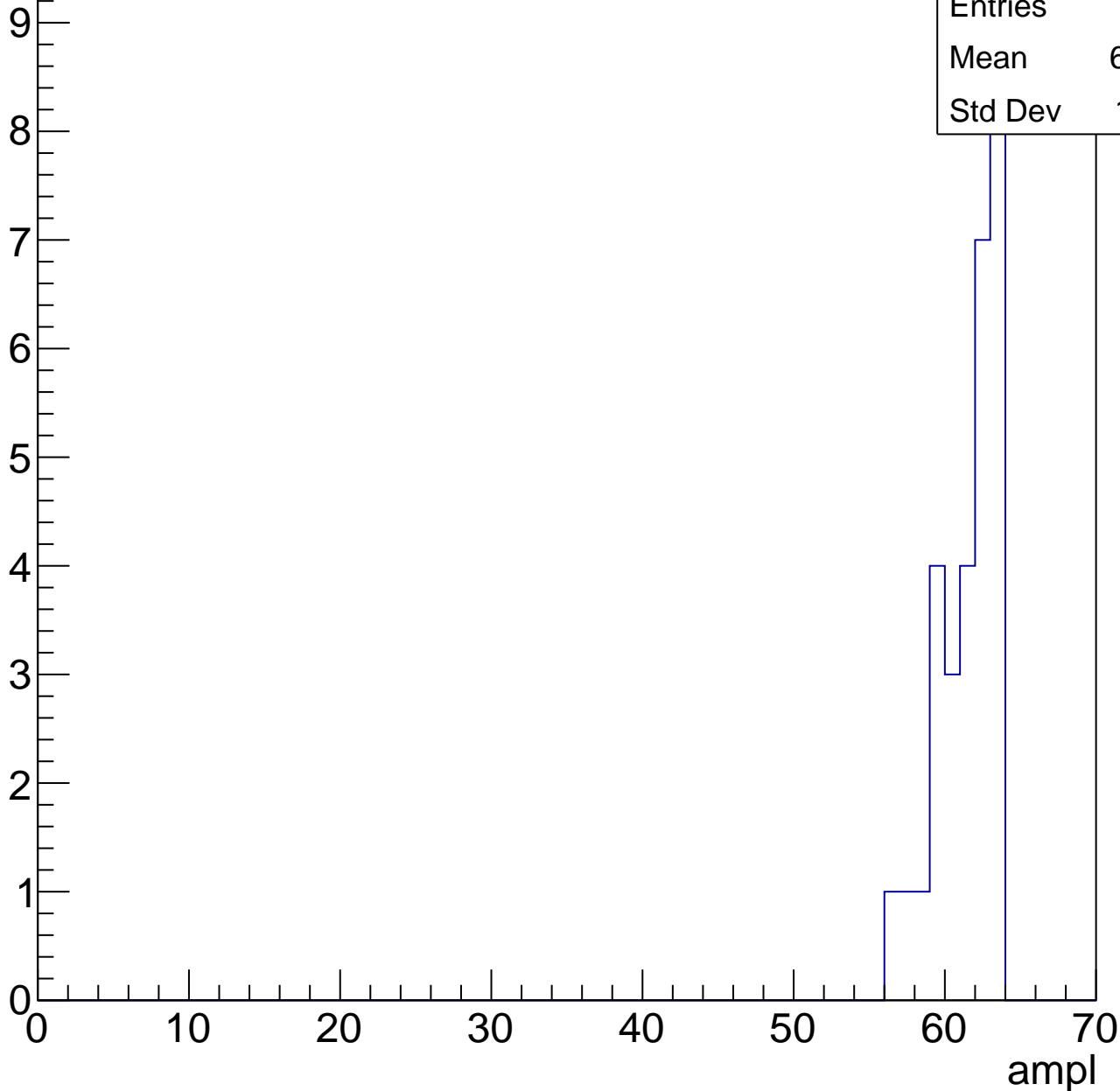


B1L102S, U14-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	61.07
Std Dev	1.931



B1L102S, U14-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch87, adc0

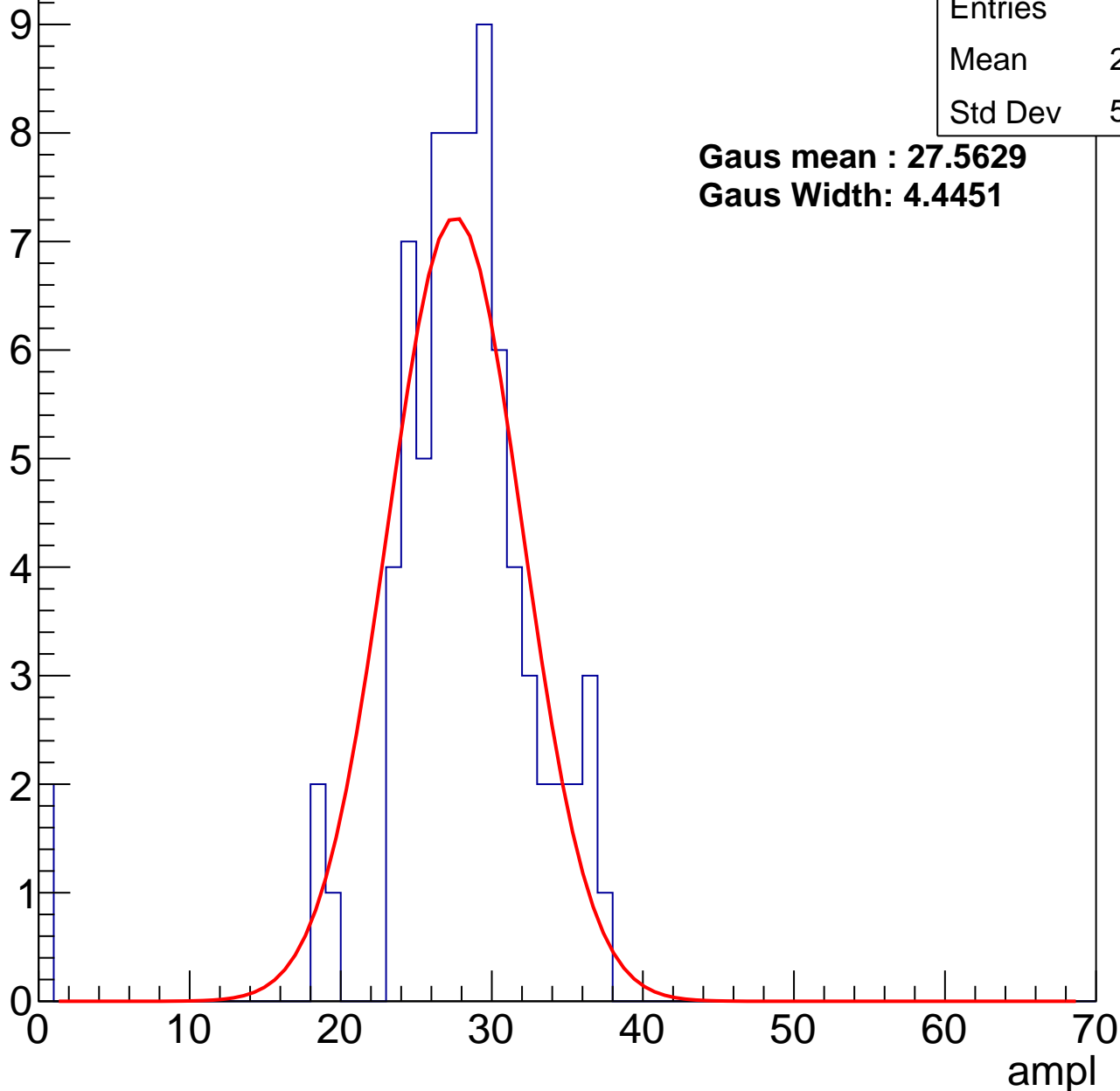
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	27.25
Std Dev	5.935

Gaus mean : 27.5629

Gaus Width: 4.4451



B1L102S, U14-ch87, adc1

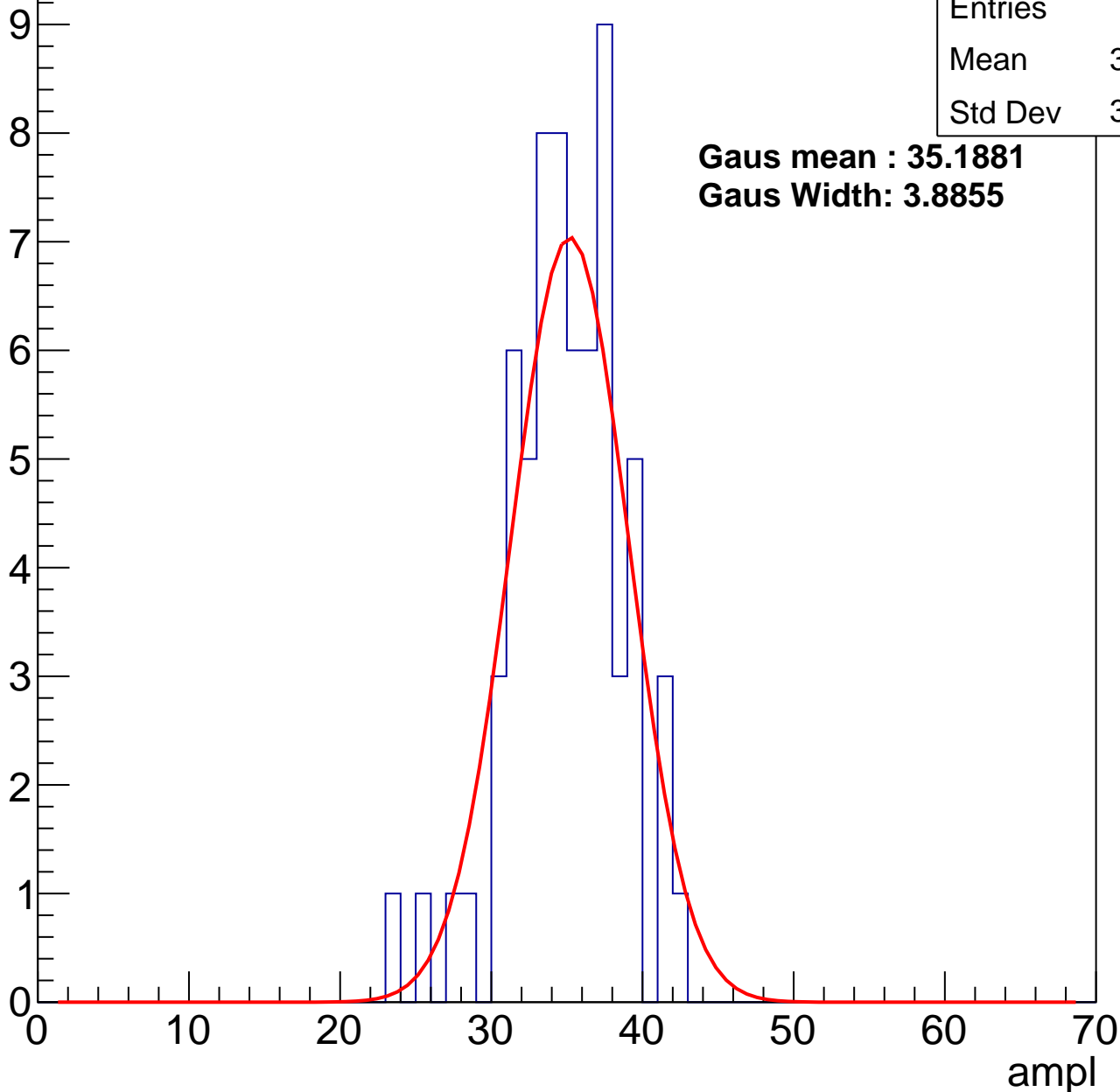
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.45
Std Dev	3.662

Gaus mean : 35.1881

Gaus Width: 3.8855



B1L102S, U14-ch87, adc2

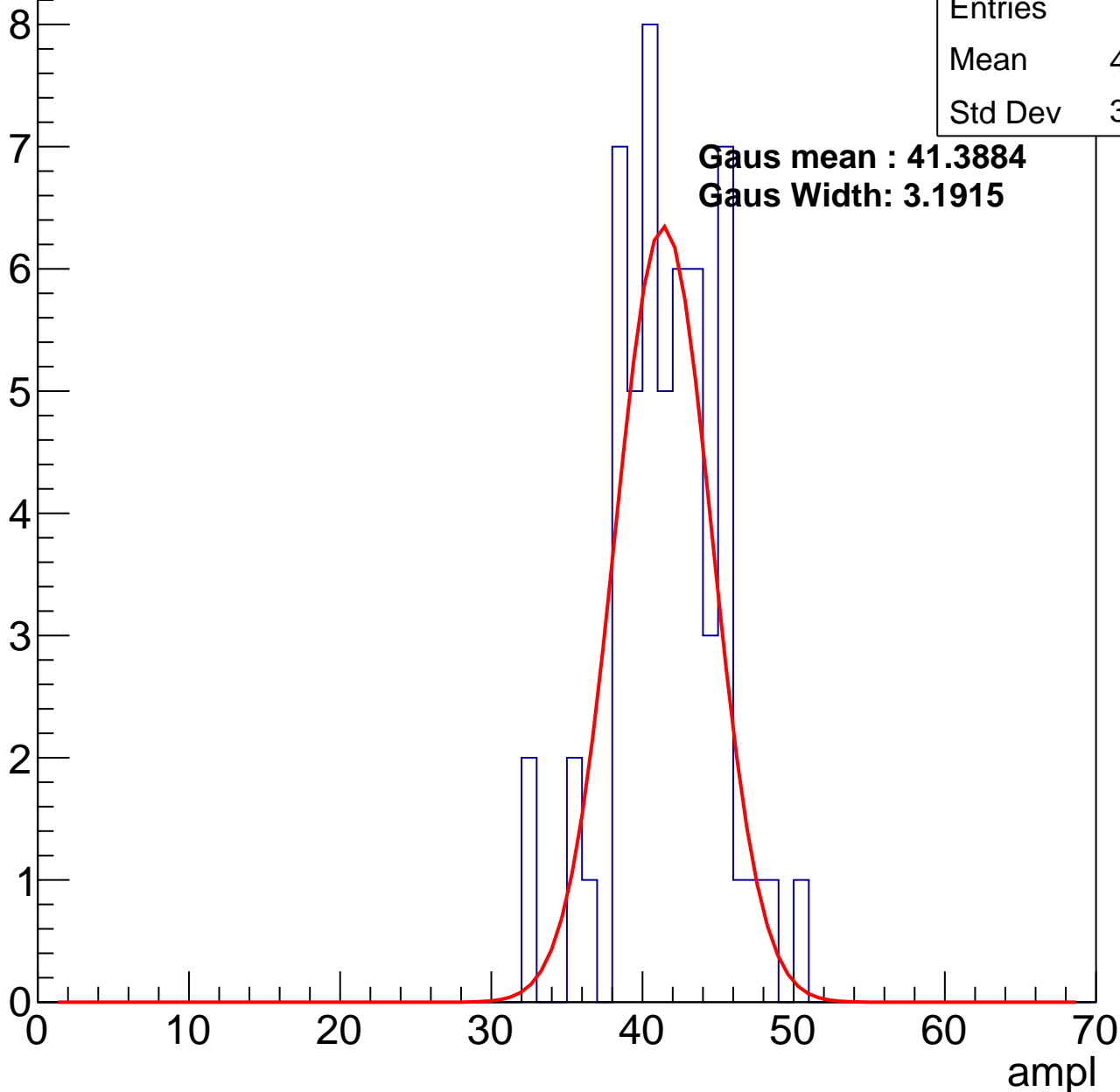
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.14
Std Dev	3.558

Gaus mean : 41.3884

Gaus Width: 3.1915



B1L102S, U14-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	47.66
Std Dev	3.649

Entry

10

8

6

4

2

0

0

10

20

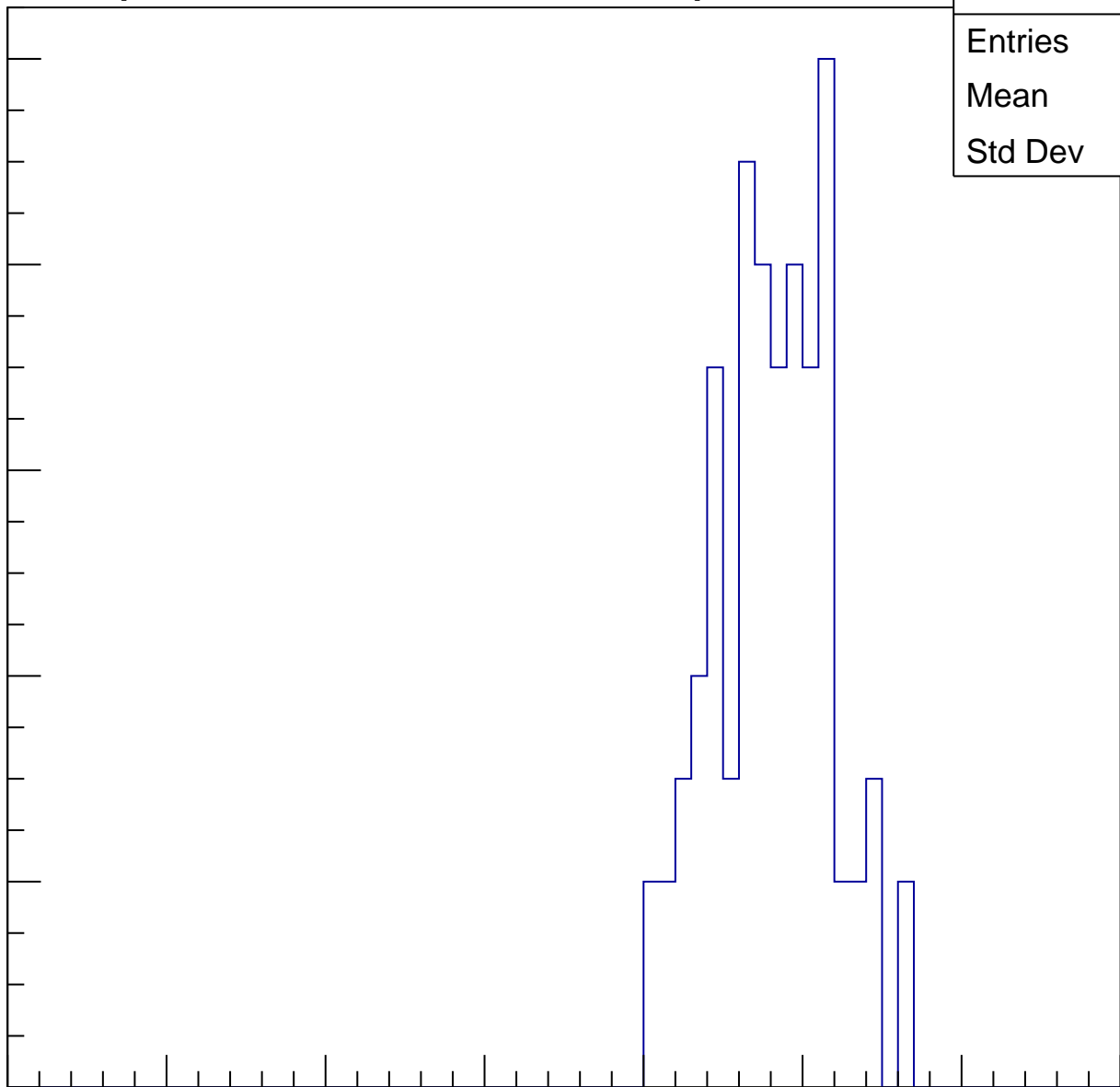
30

40

50

60

ampl

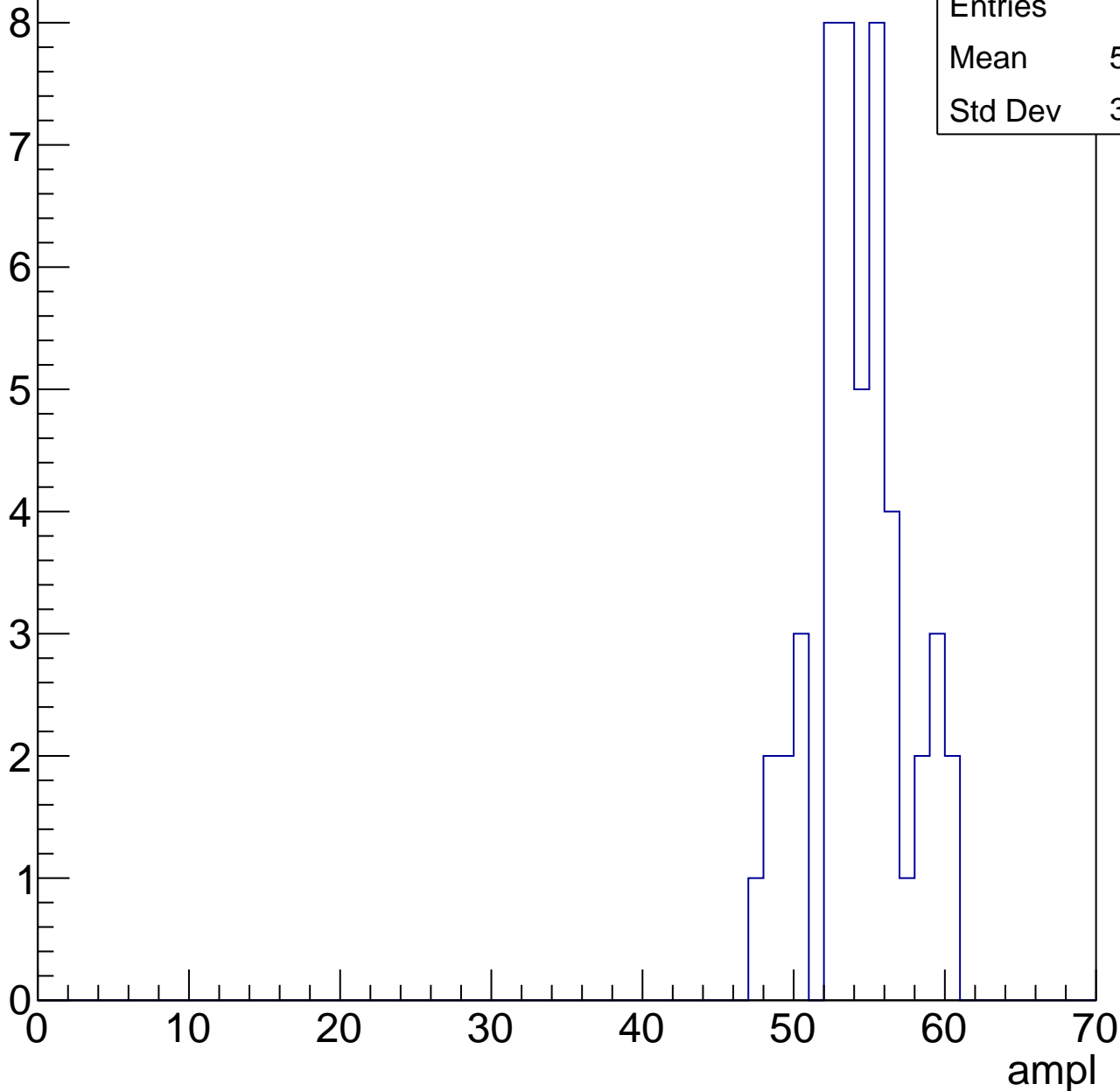


B1L102S, U14-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.78
Std Dev	3.092

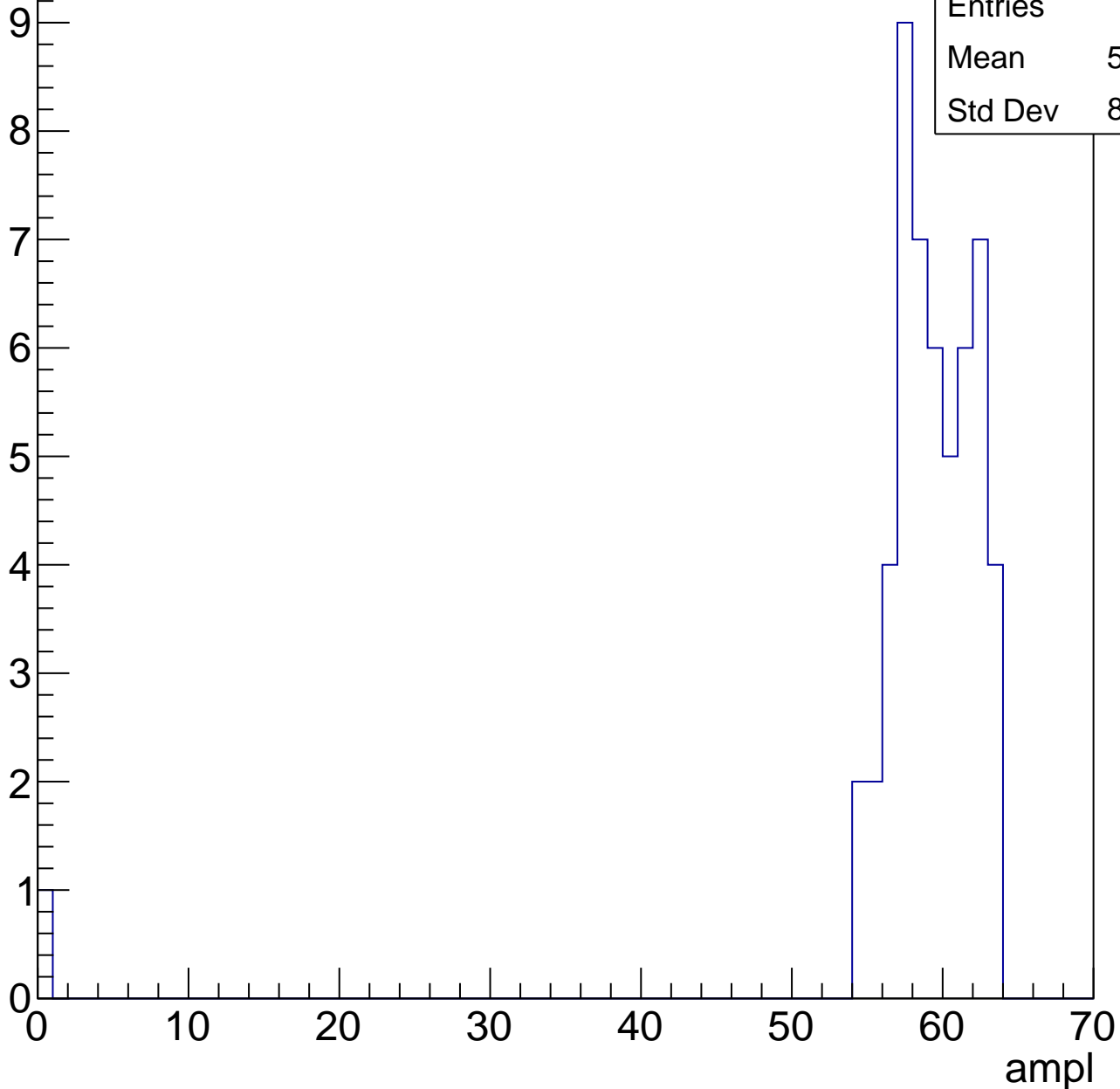


B1L102S, U14-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.87
Std Dev	8.389

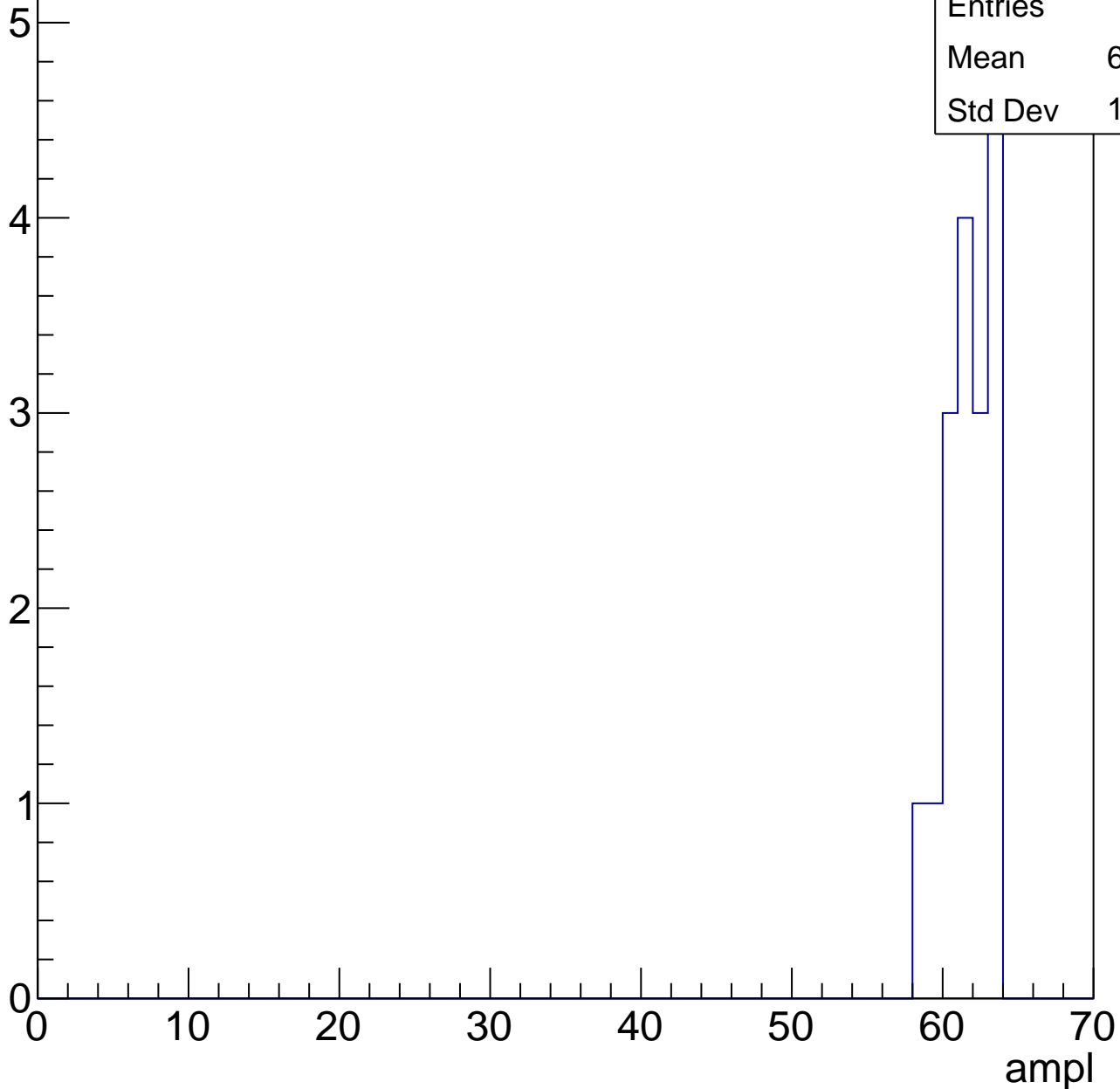


B1L102S, U14-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.29
Std Dev	1.486



B1L102S, U14-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch88, adc0

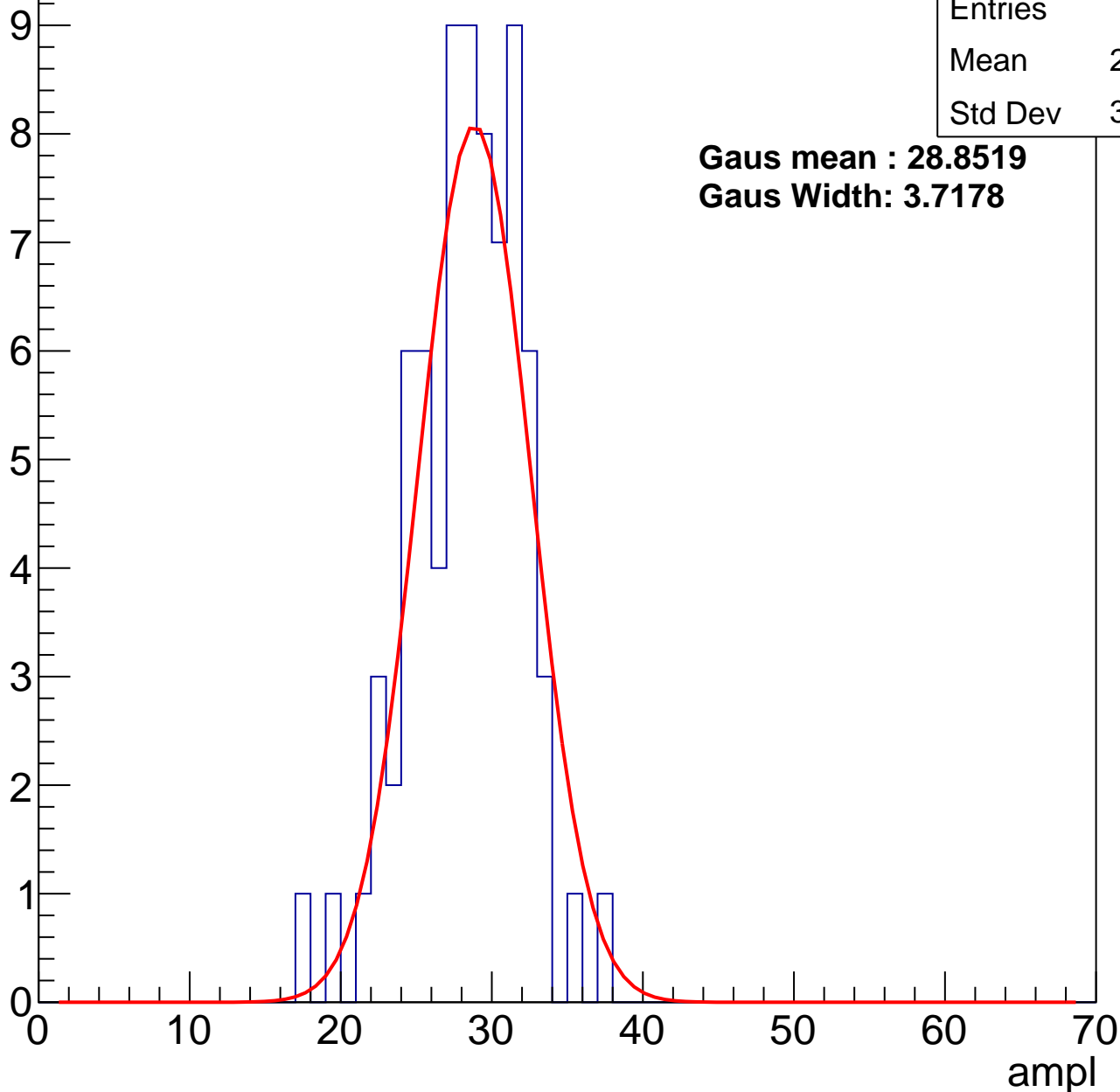
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	27.87
Std Dev	3.605

Gaus mean : 28.8519

Gaus Width: 3.7178



B1L102S, U14-ch88, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	35.67
Std Dev	3.488

Gaus mean : 36.2844

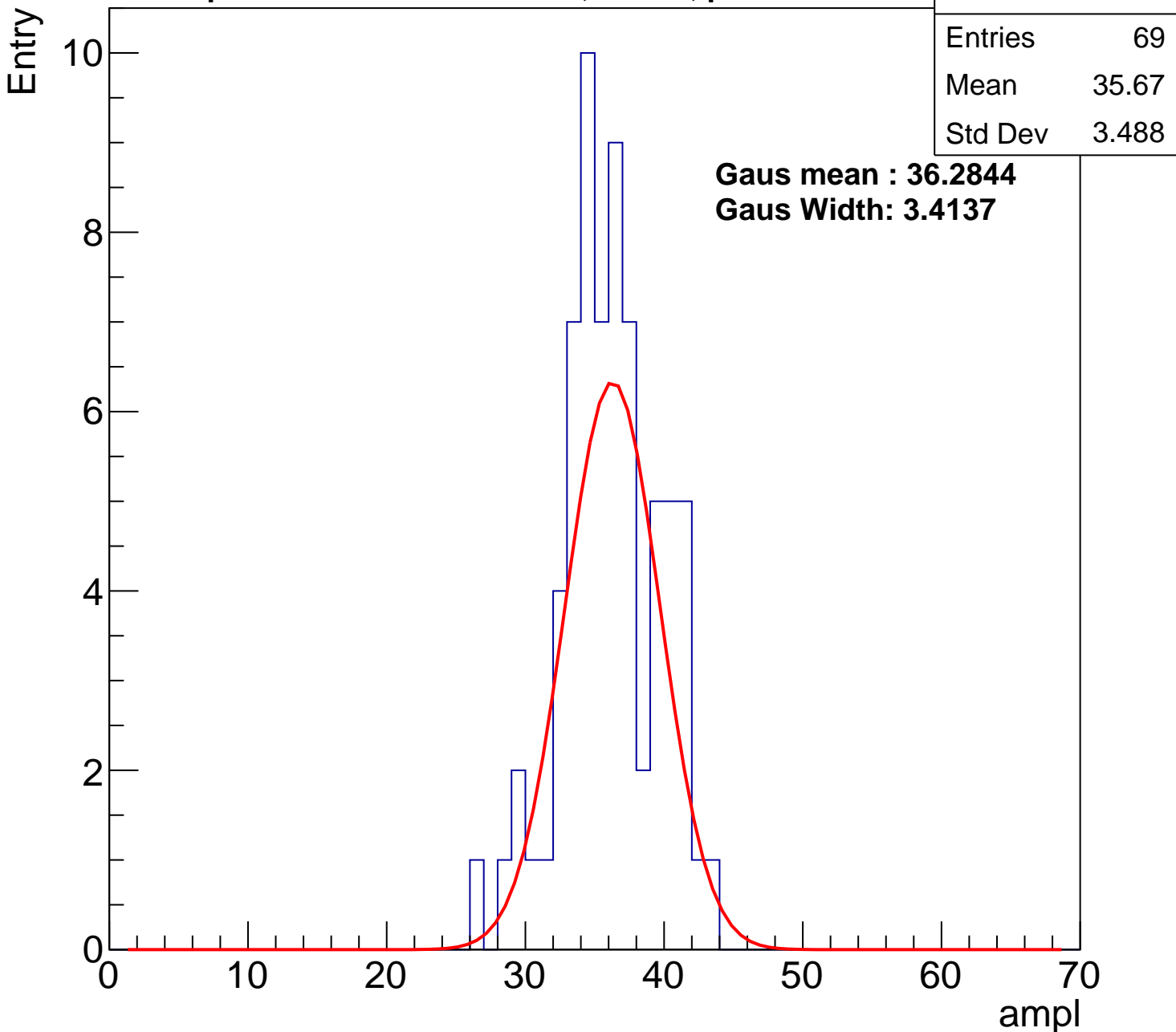
Gaus Width: 3.4137

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch88, adc2

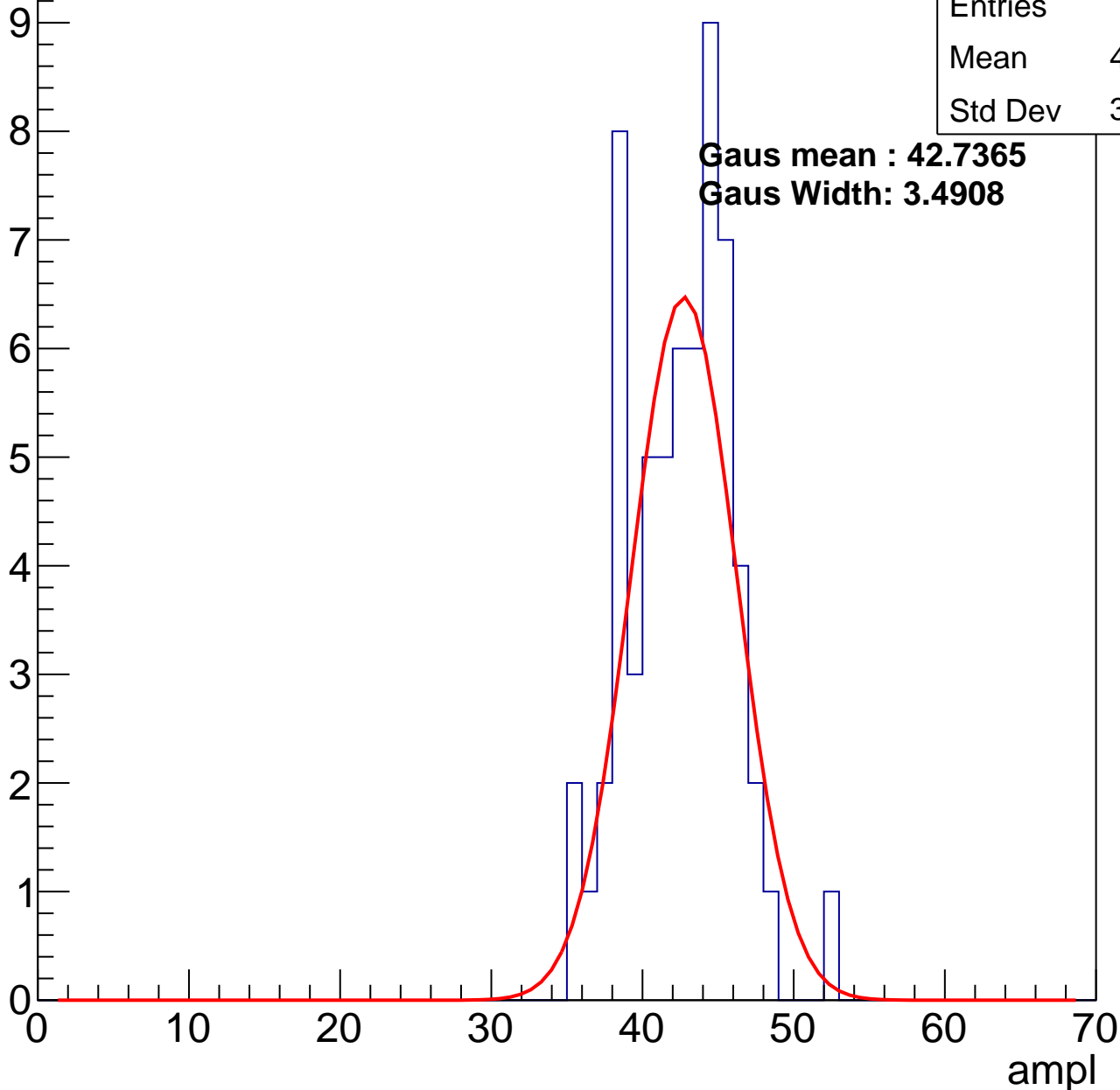
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.02
Std Dev	3.405

Gaus mean : 42.7365

Gaus Width: 3.4908

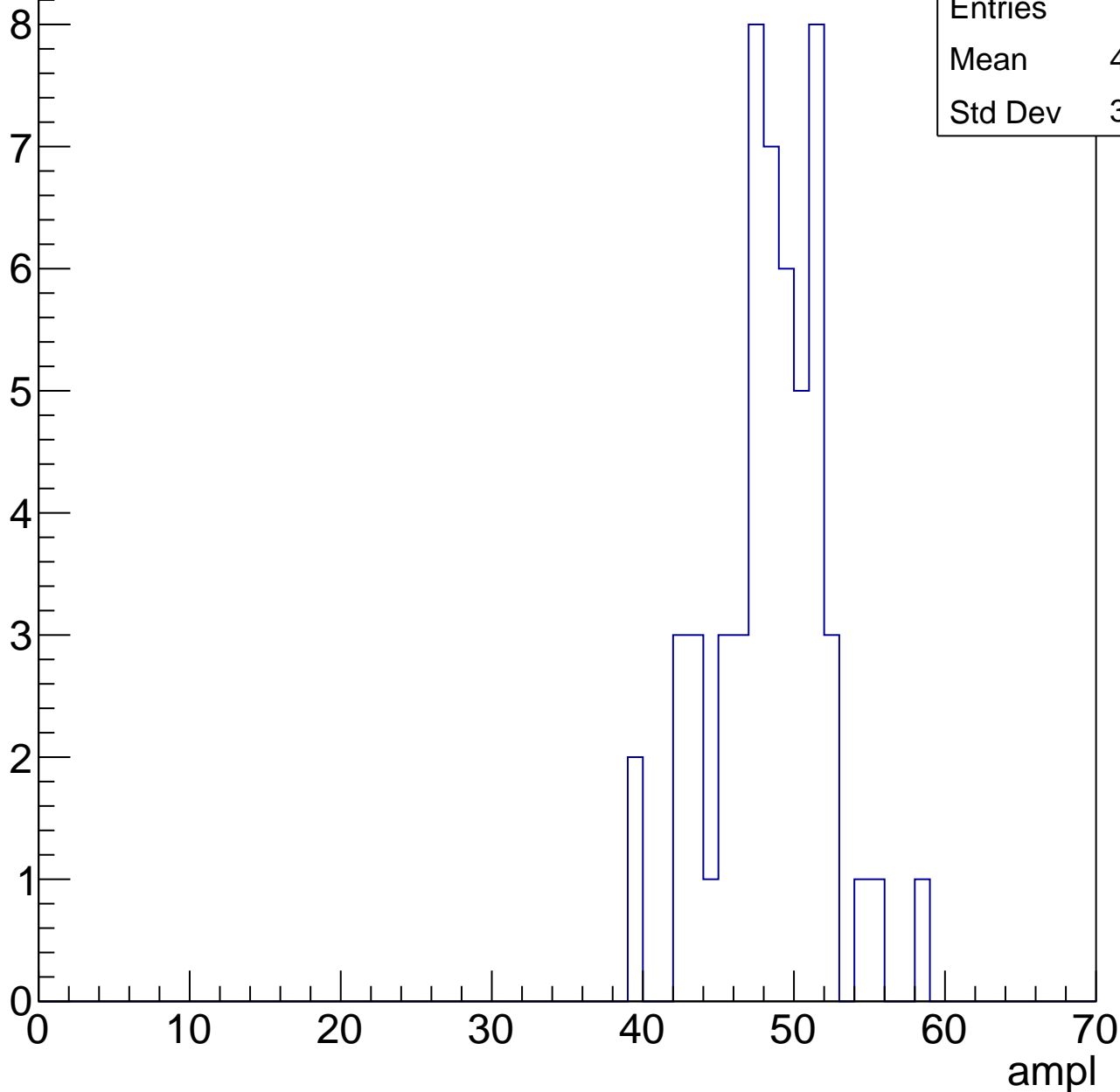


B1L102S, U14-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

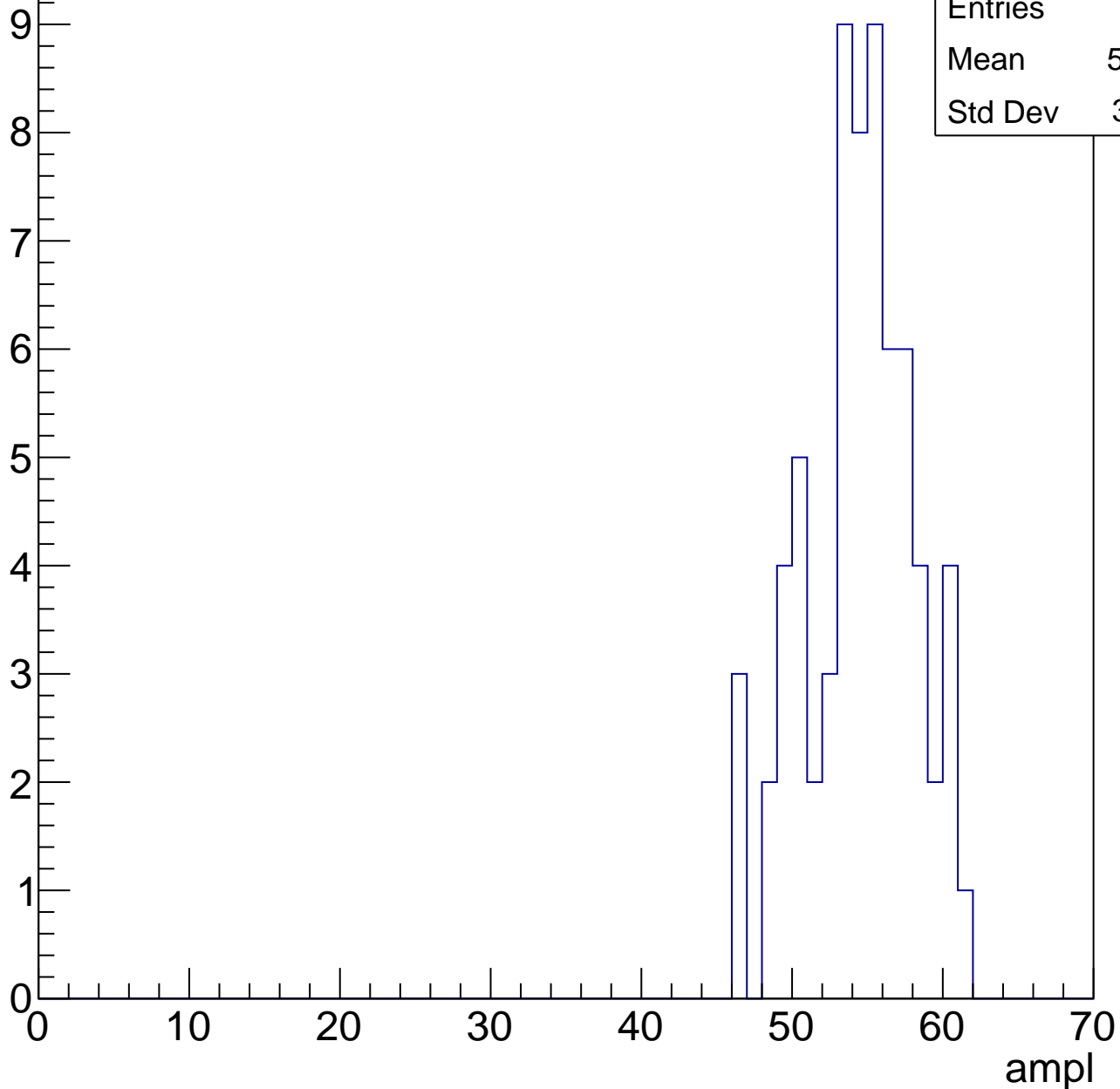
Entries	55
Mean	47.95
Std Dev	3.665



B1L102S, U14-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

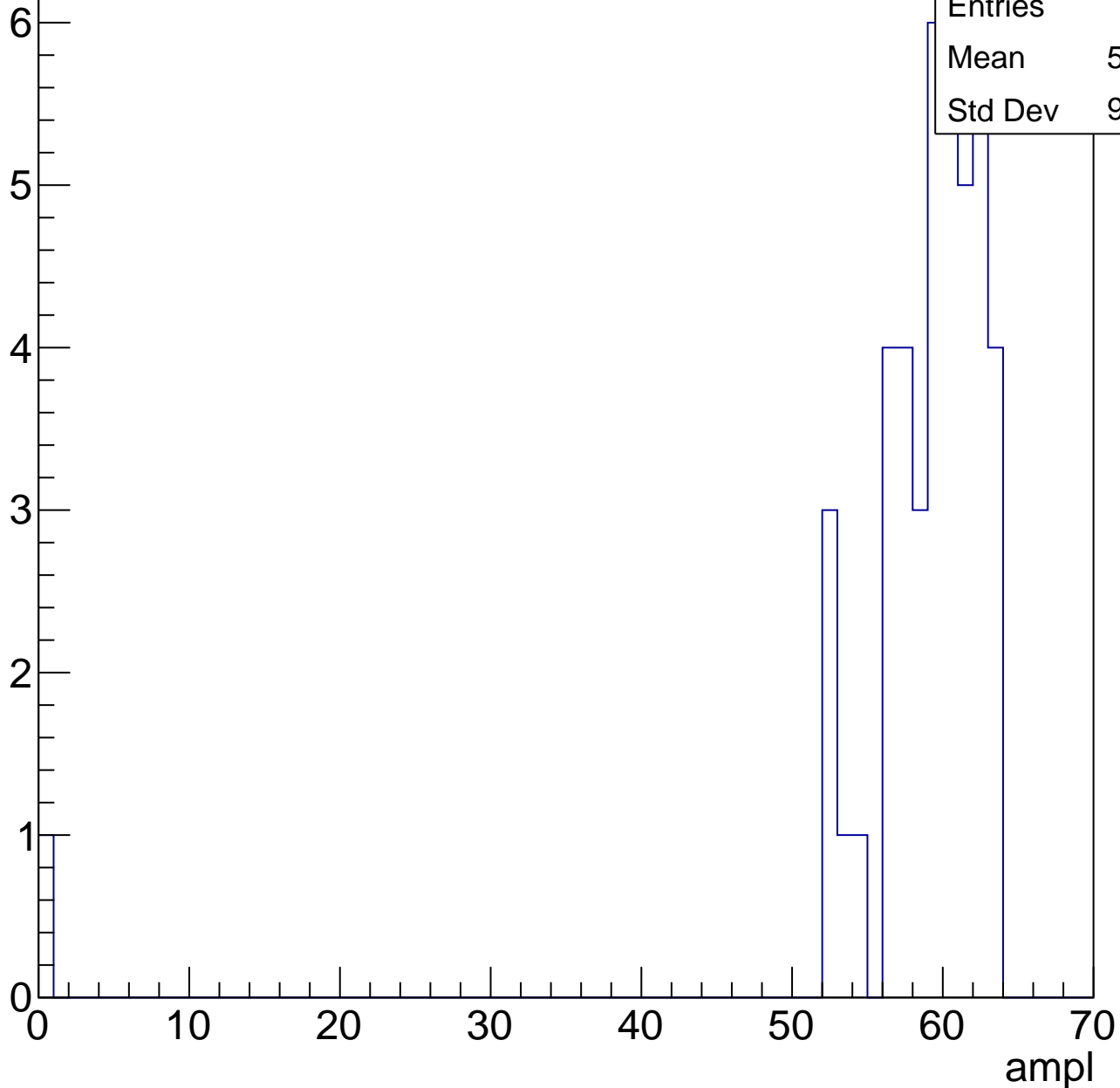


Entries	68
Mean	53.99
Std Dev	3.591

B1L102S, U14-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

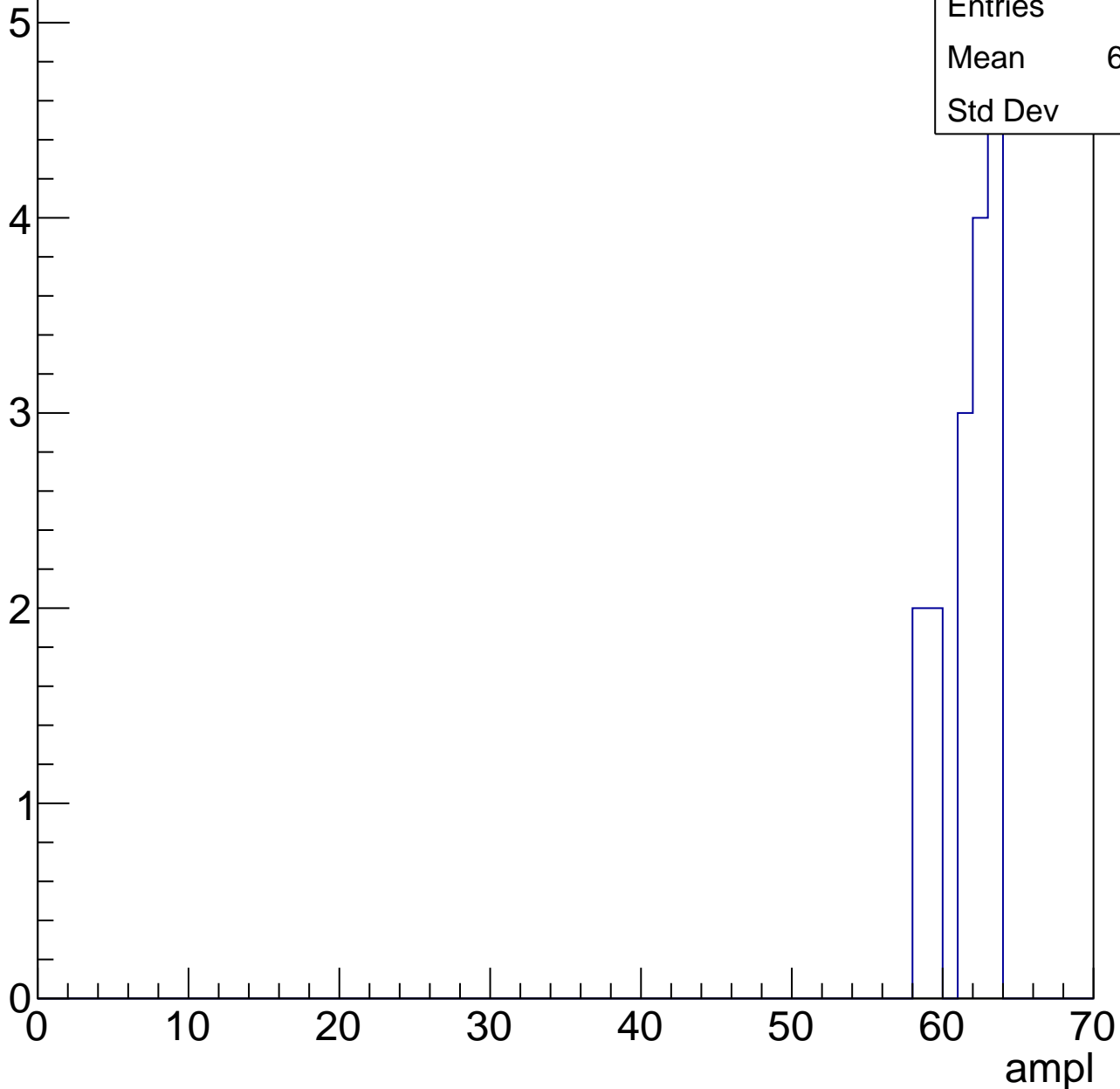


B1L102S, U14-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

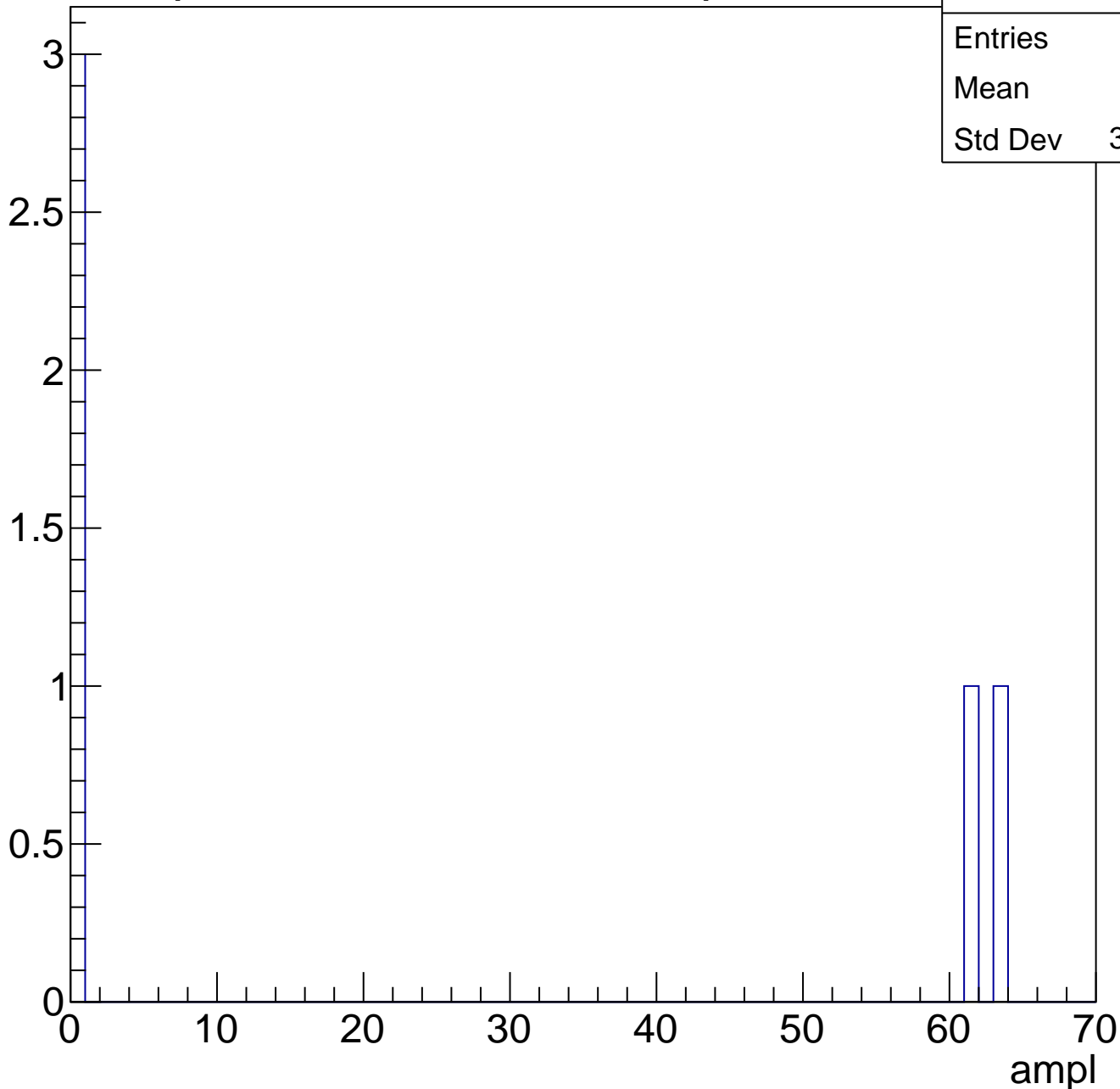
Entries	16
Mean	61.25
Std Dev	1.75



B1L102S, U14-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch89, adc0

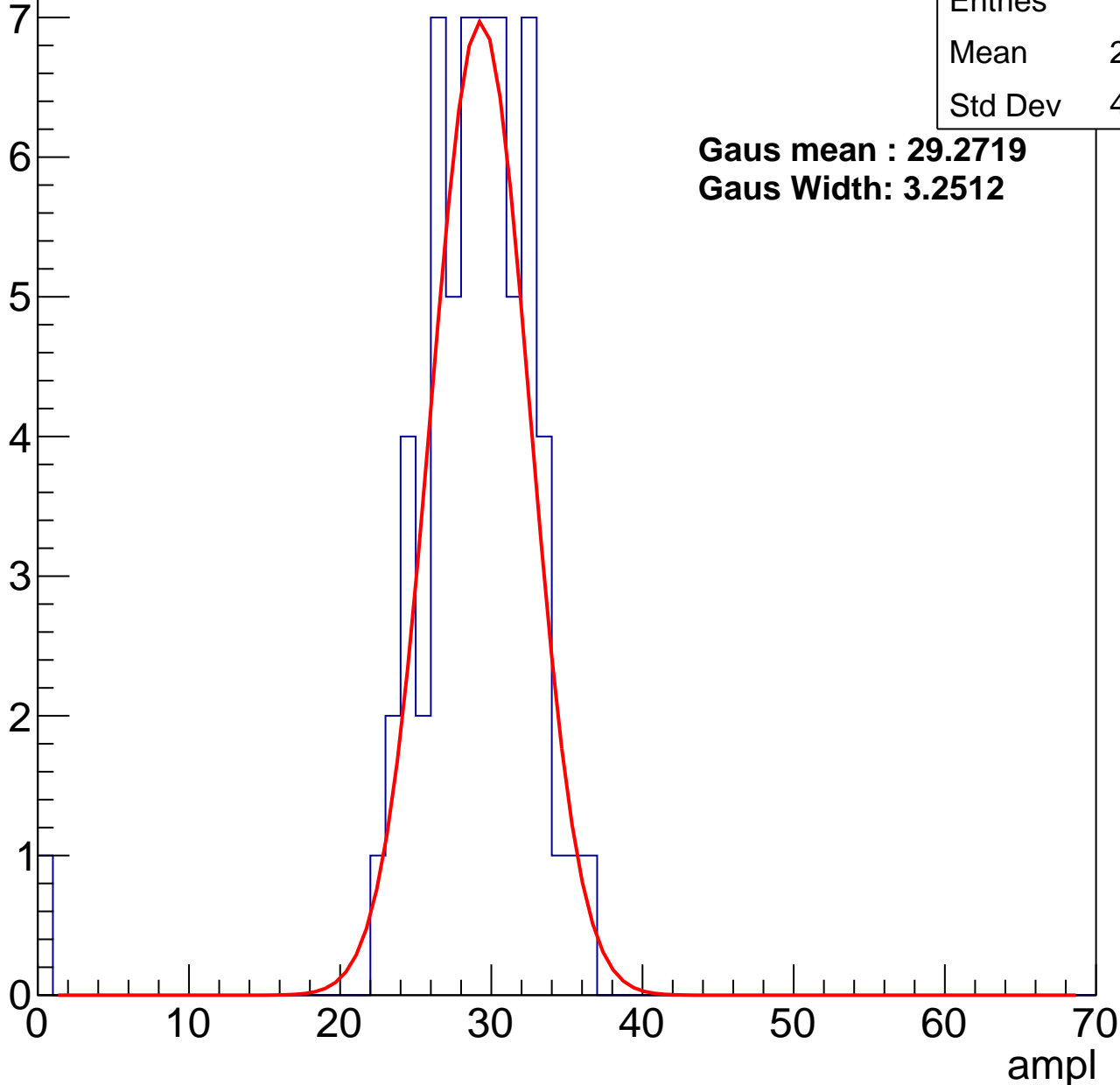
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.32
Std Dev	4.785

Gaus mean : 29.2719

Gaus Width: 3.2512



B1L102S, U14-ch89, adc1

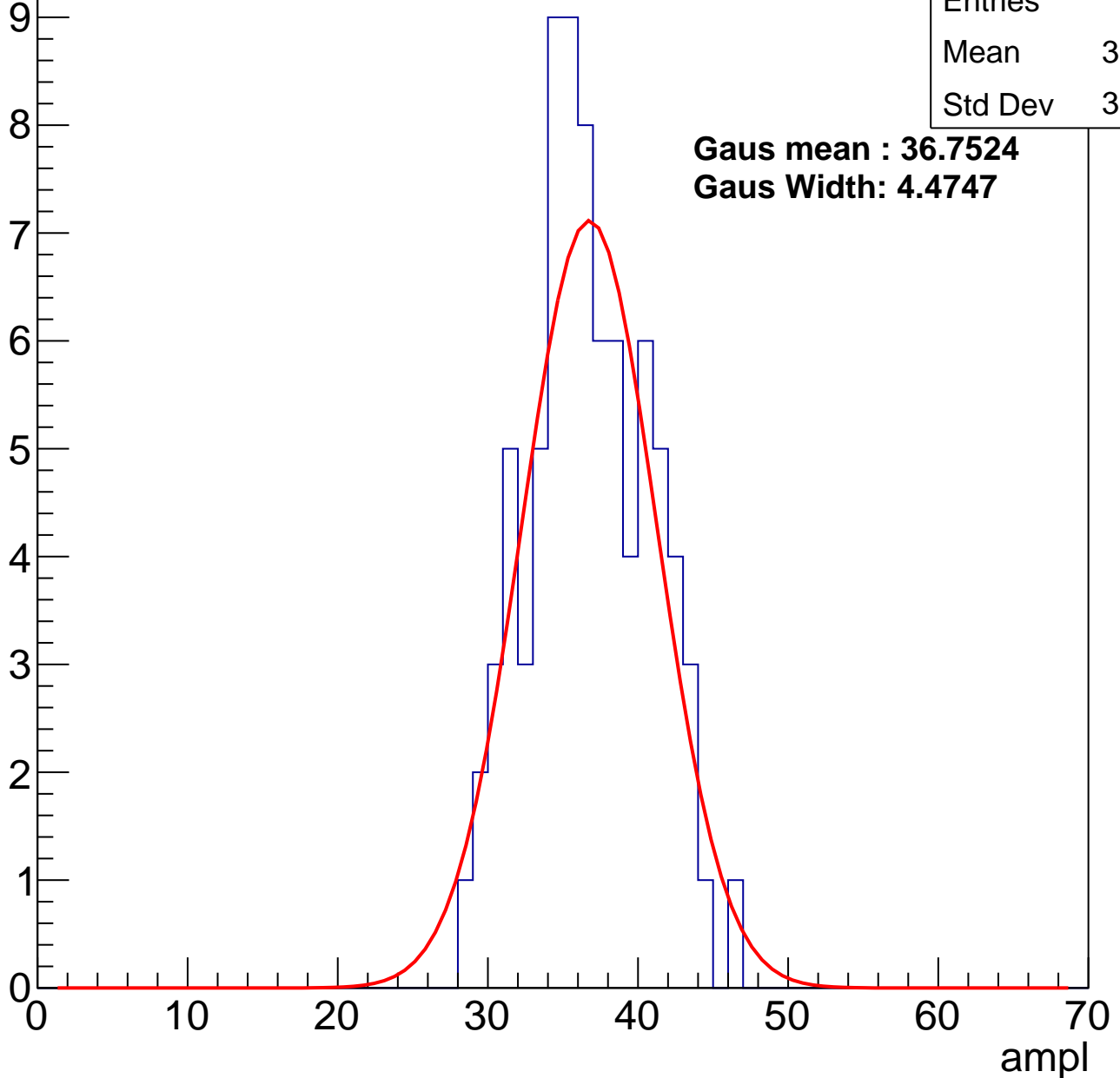
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.28
Std Dev	3.957

Gaus mean : 36.7524

Gaus Width: 4.4747



B1L102S, U14-ch89, adc2

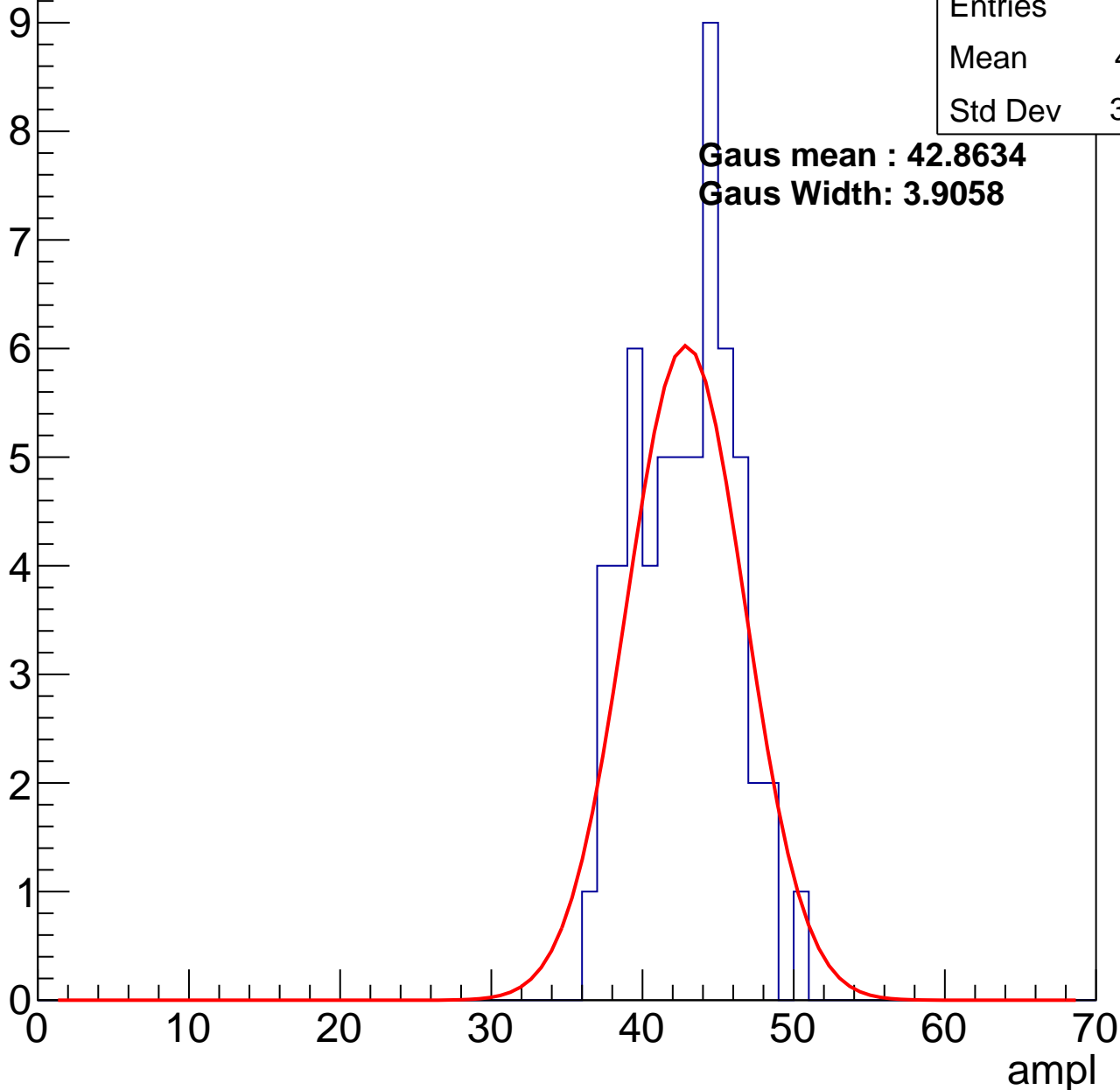
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.31
Std Dev	3.269

Gaus mean : 42.8634

Gaus Width: 3.9058

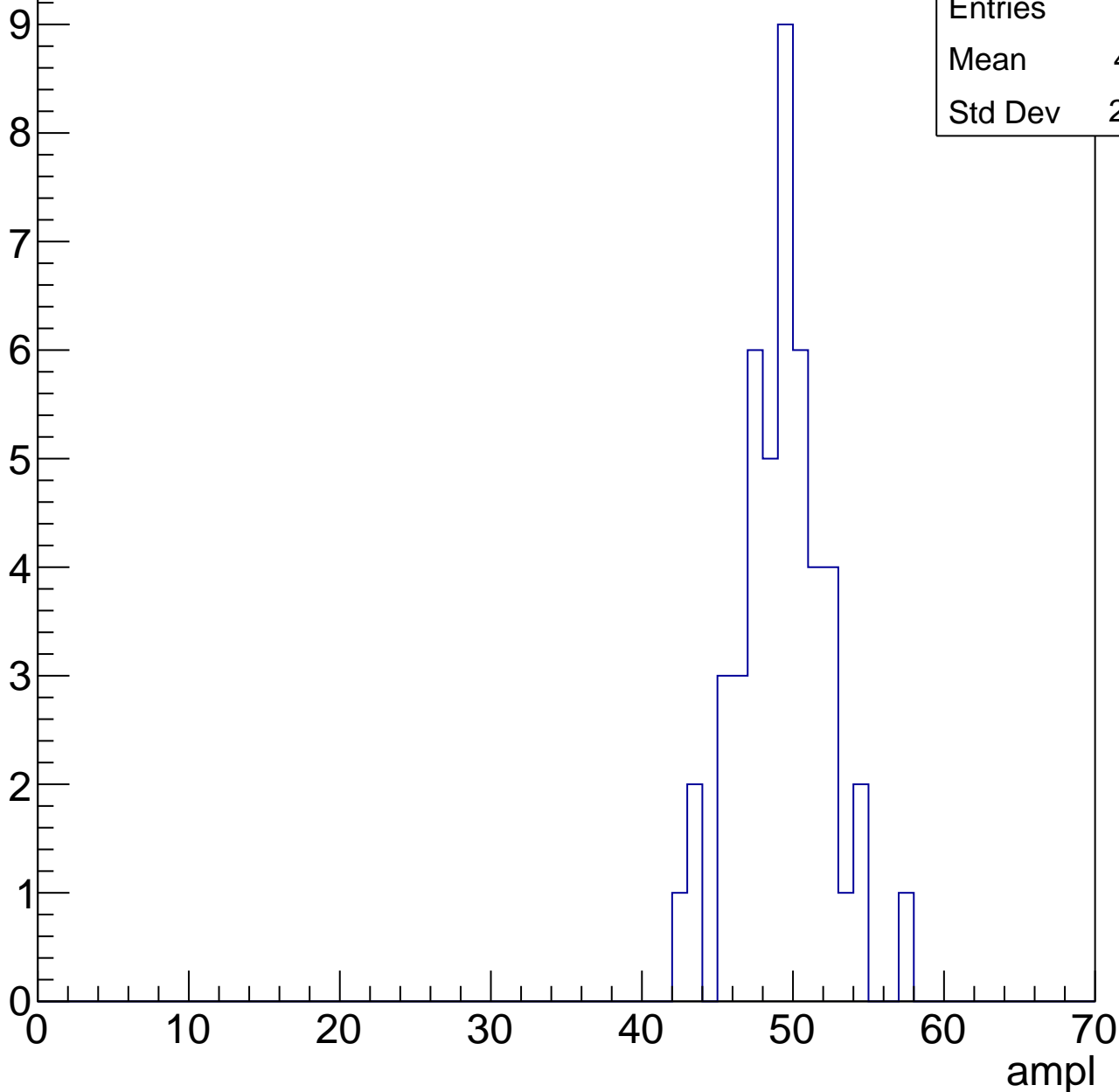


B1L102S, U14-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	48.81
Std Dev	2.958

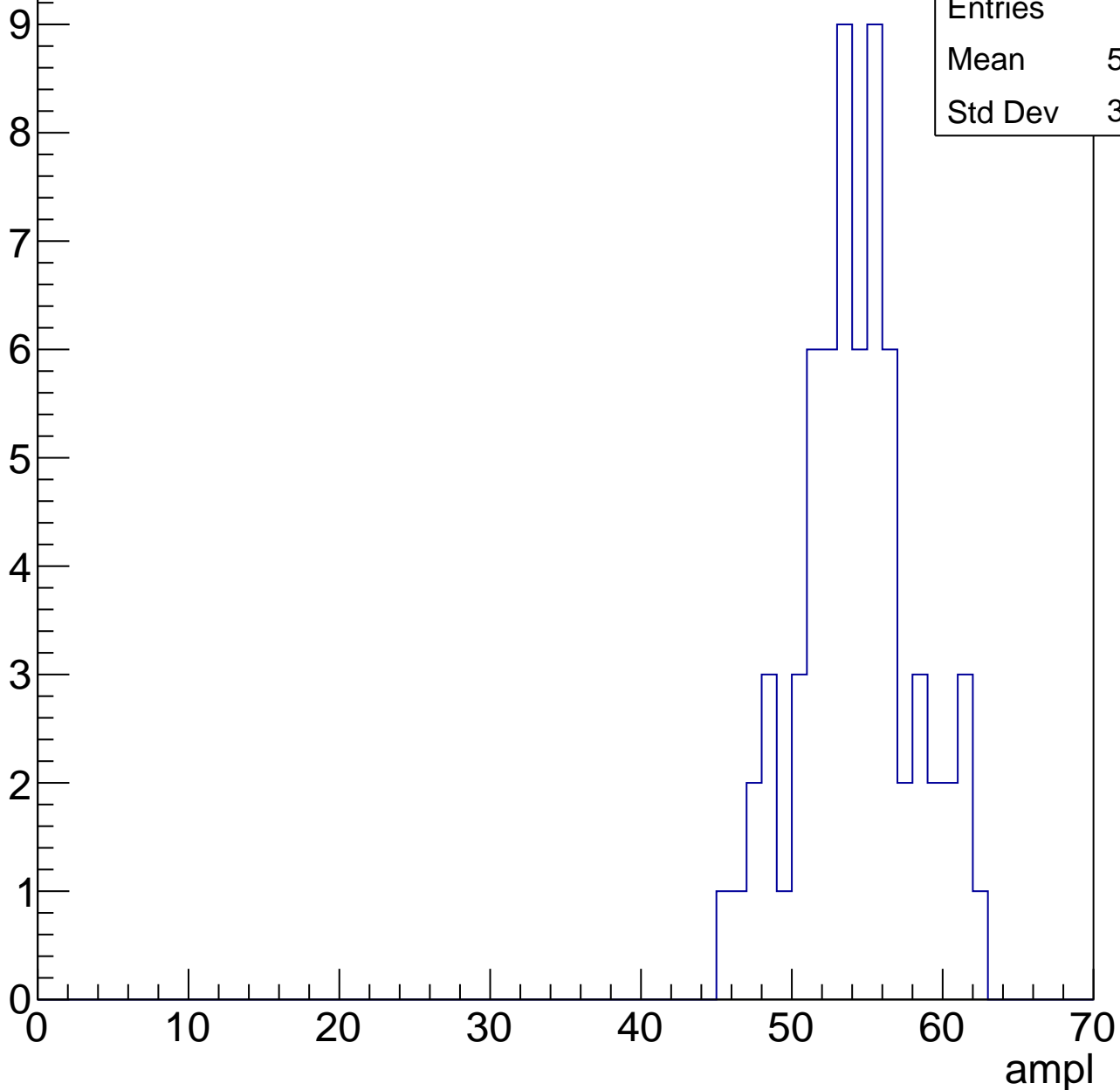


B1L102S, U14-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	53.77
Std Dev	3.773

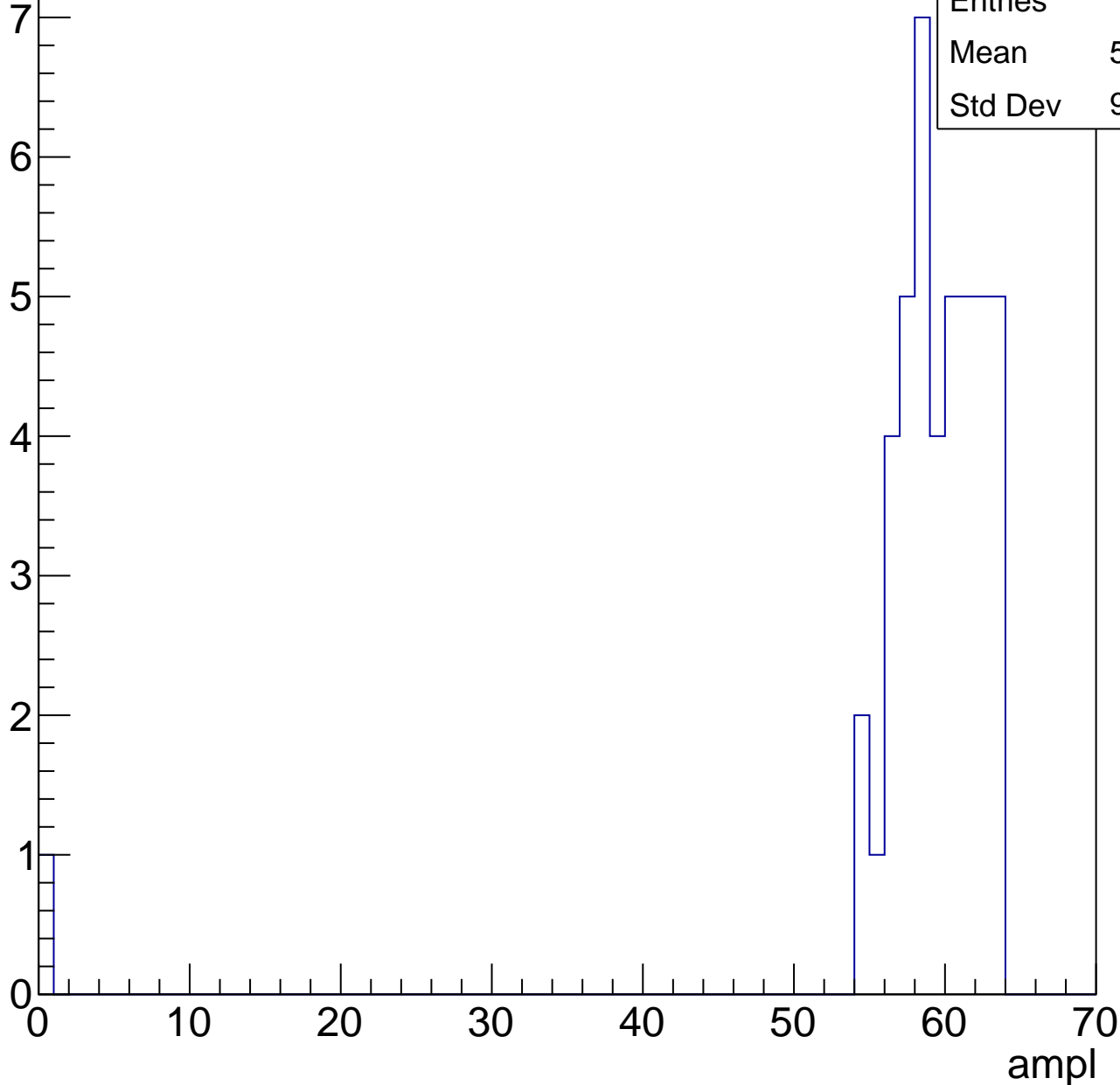


B1L102S, U14-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	57.82
Std Dev	9.168

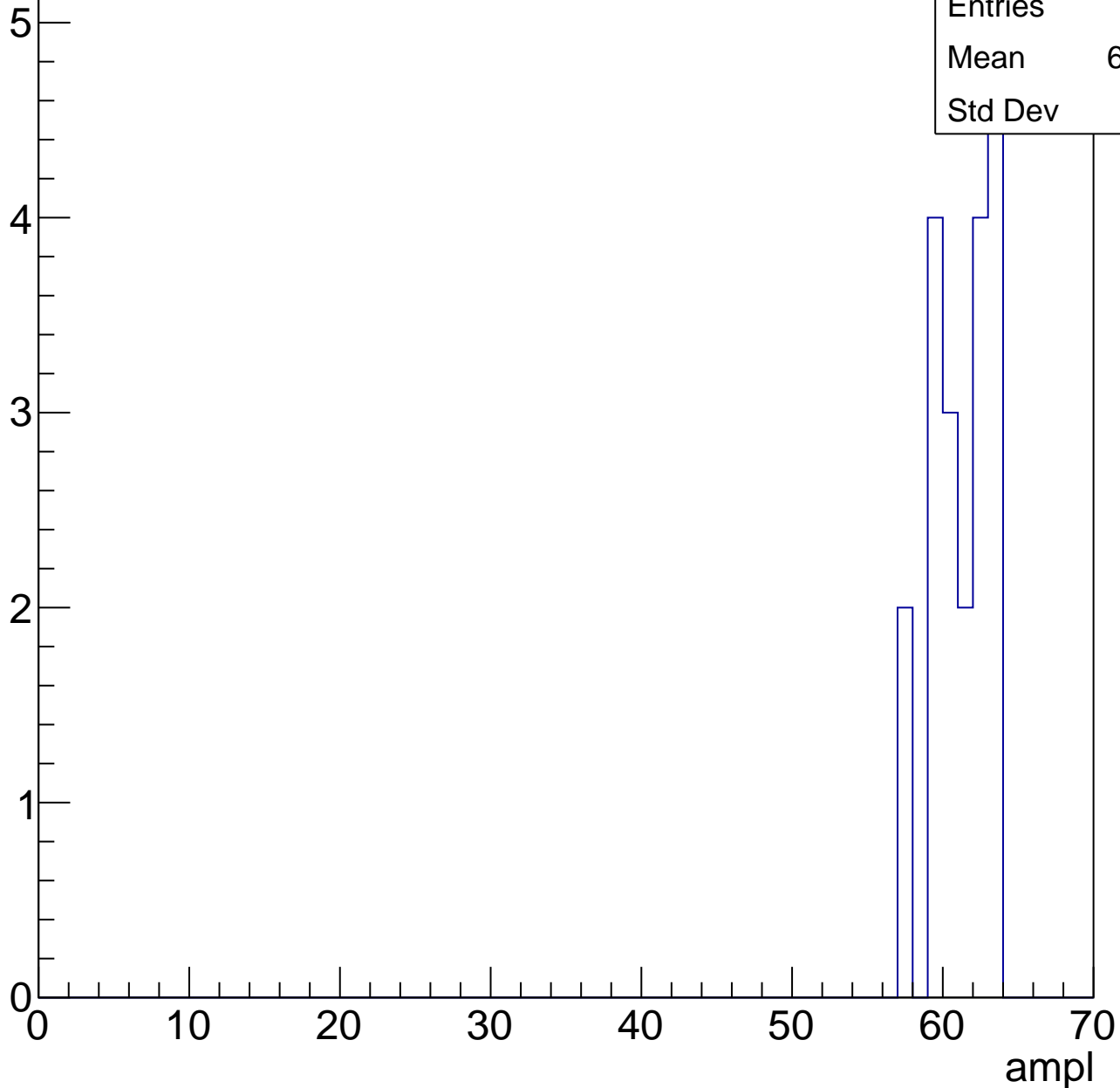


B1L102S, U14-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	60.75
Std Dev	1.92



B1L102S, U14-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch90, adc0

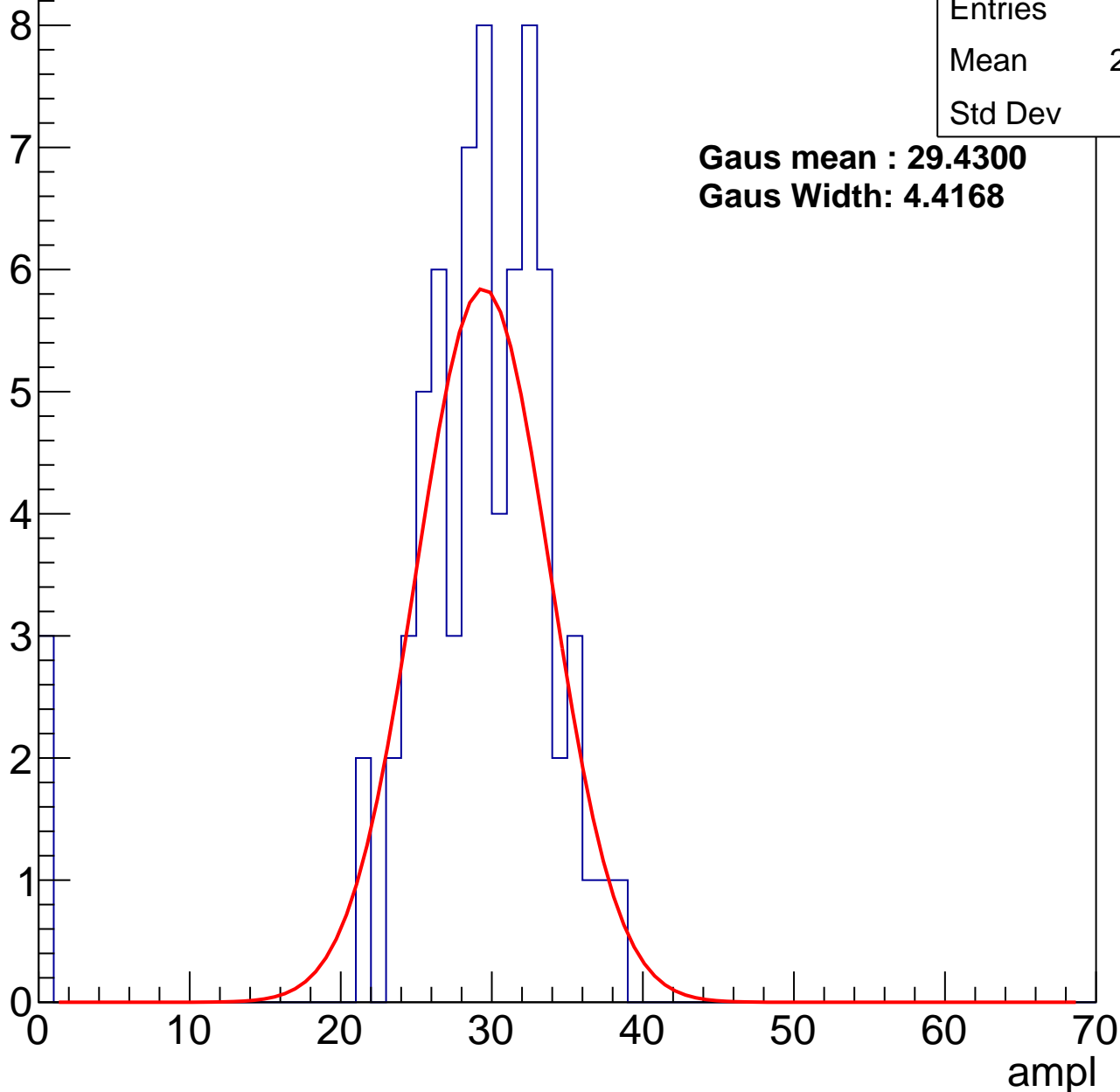
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.08
Std Dev	6.96

Gaus mean : 29.4300

Gaus Width: 4.4168



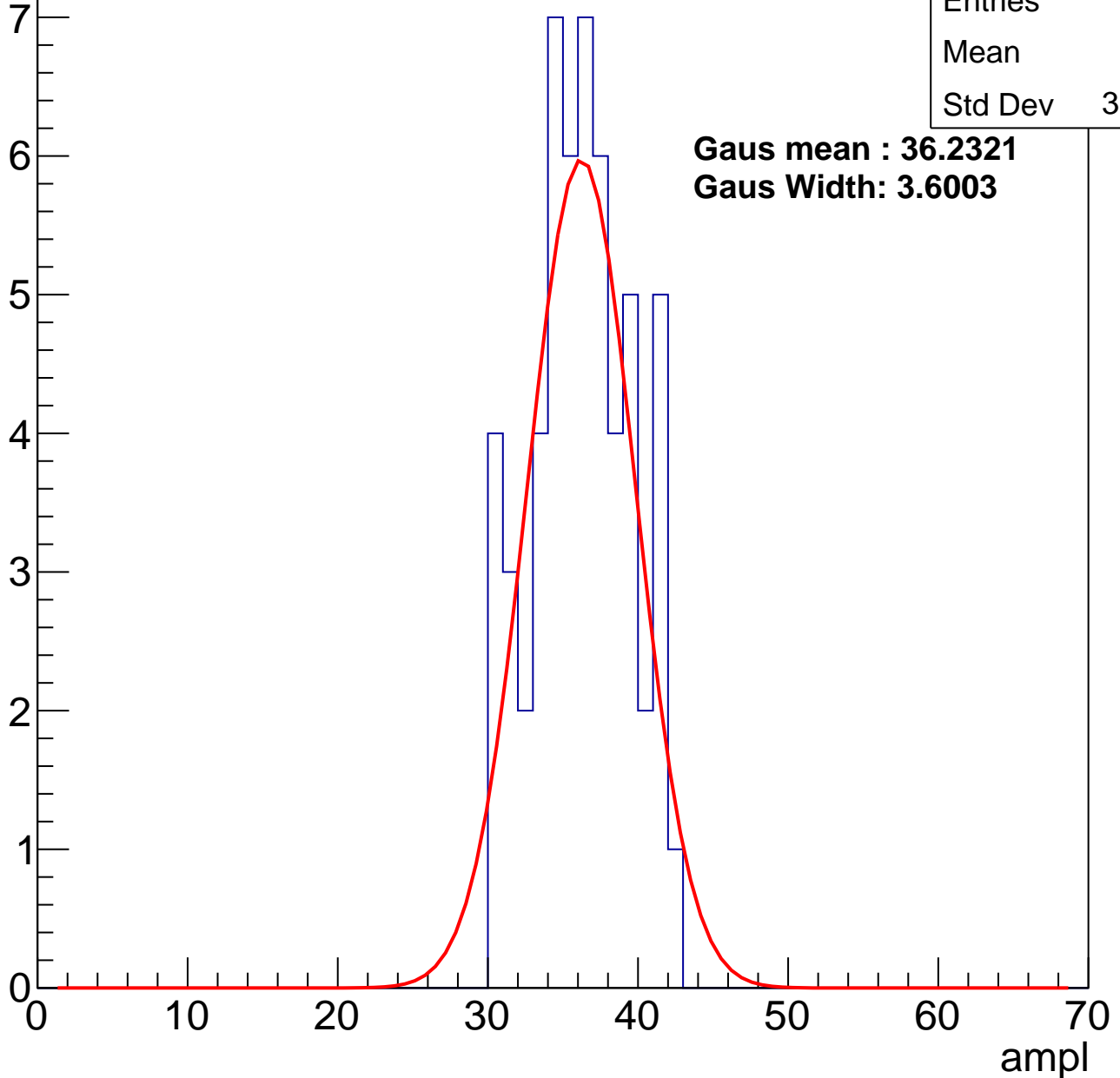
B1L102S, U14-ch90, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.8
Std Dev	3.215

Gaus mean : 36.2321
Gaus Width: 3.6003



B1L102S, U14-ch90, adc2

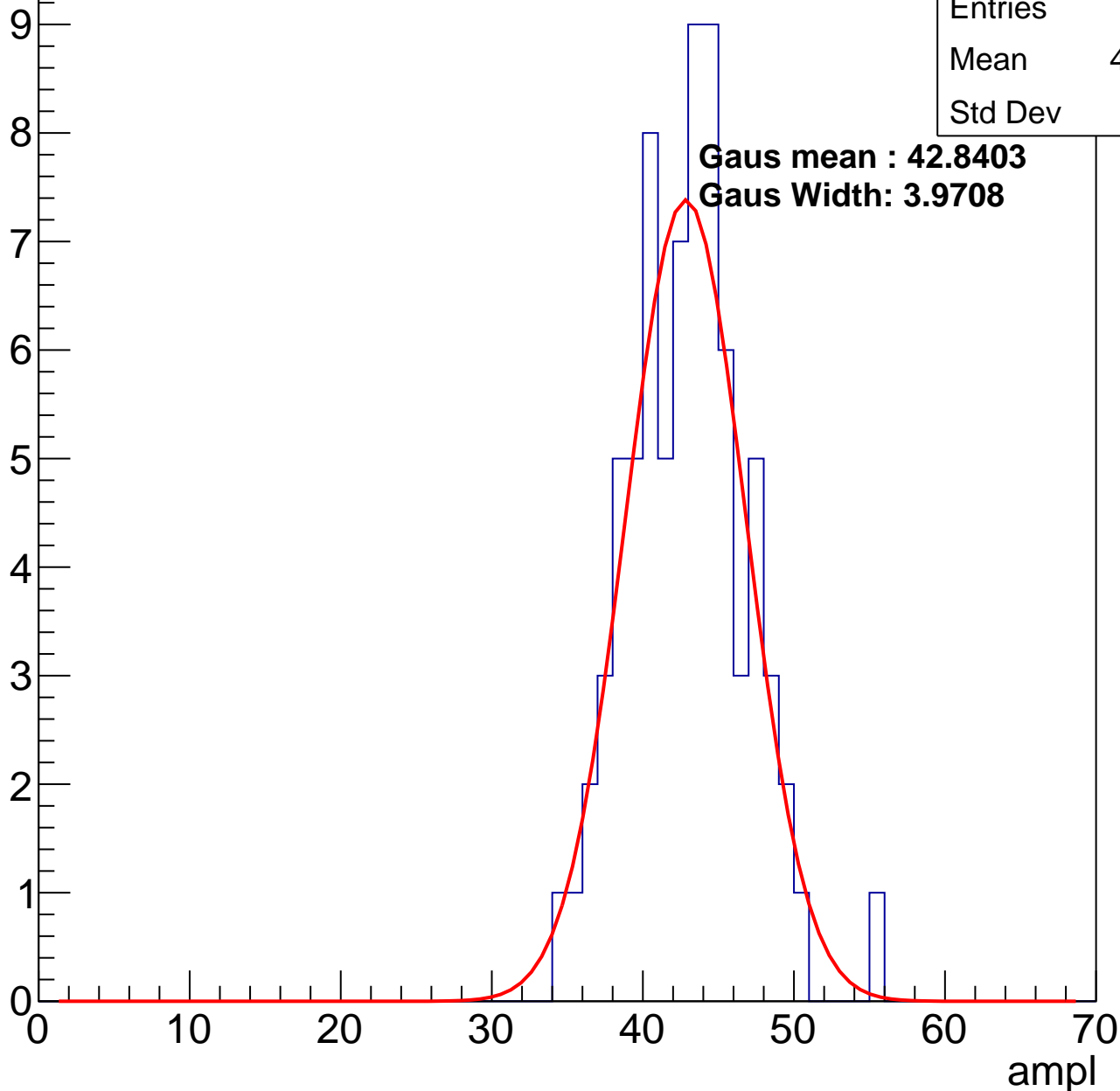
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.49
Std Dev	3.83

Gaus mean : 42.8403

Gaus Width: 3.9708

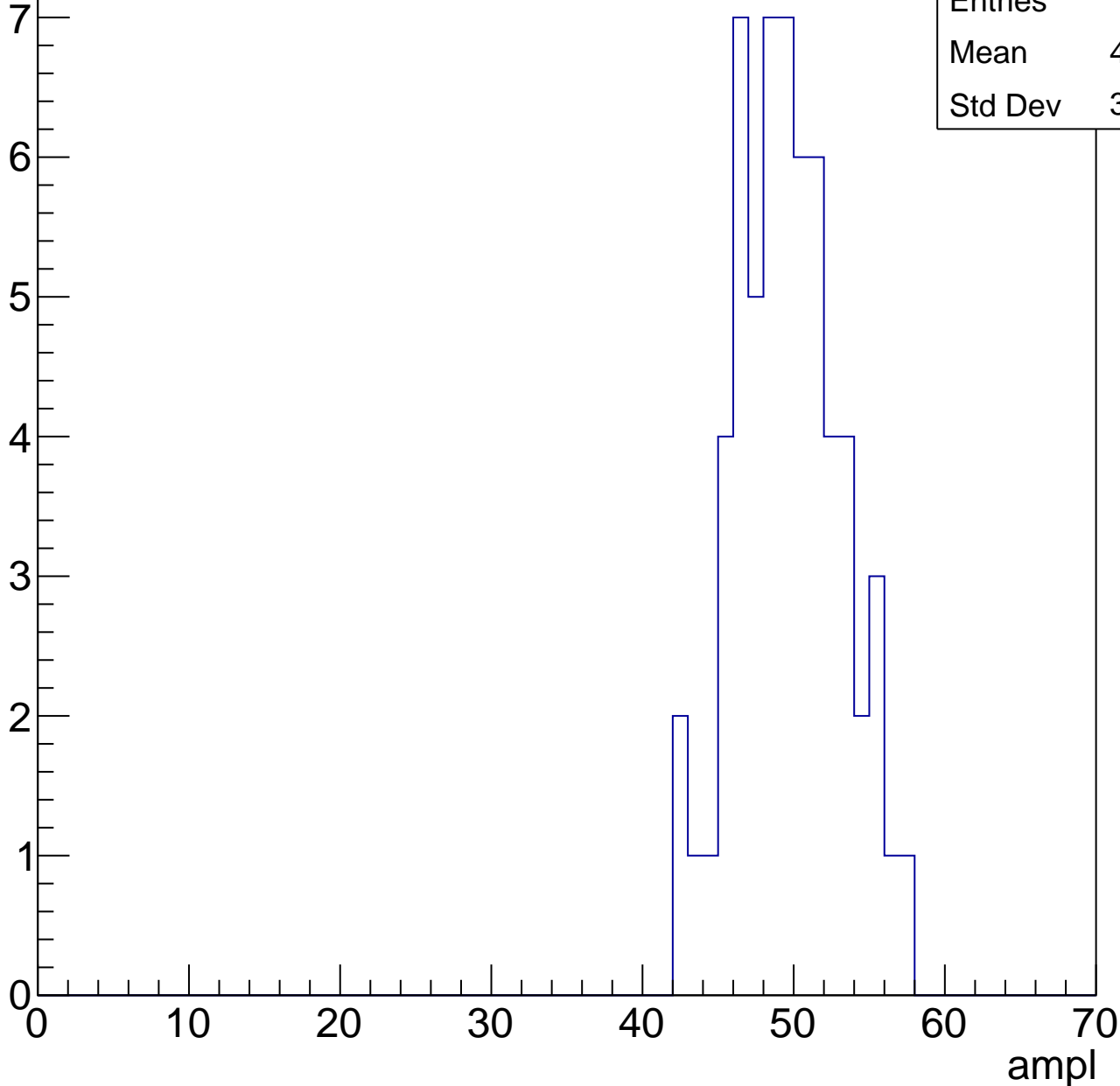


B1L102S, U14-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.16
Std Dev	3.417

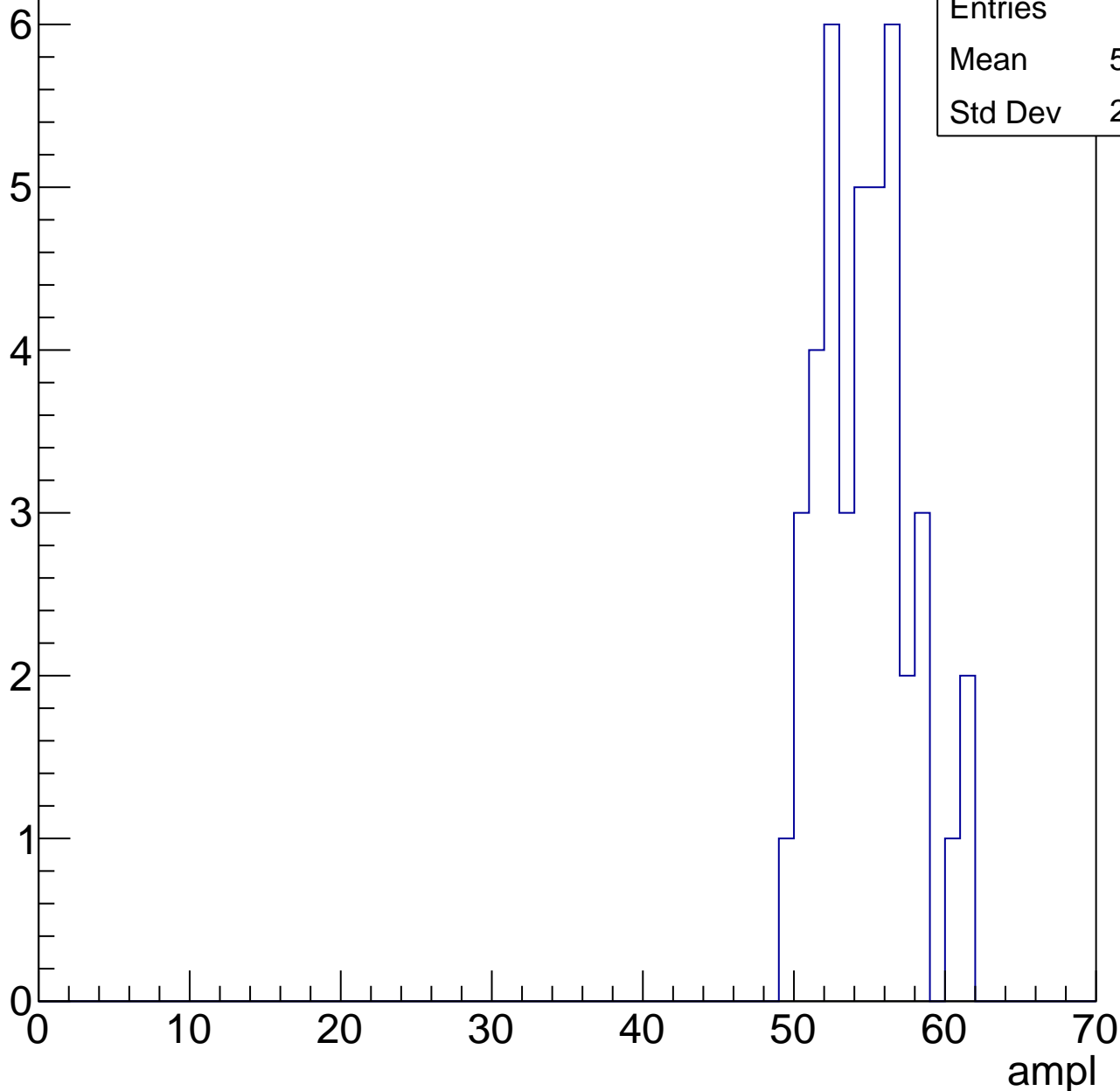


B1L102S, U14-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

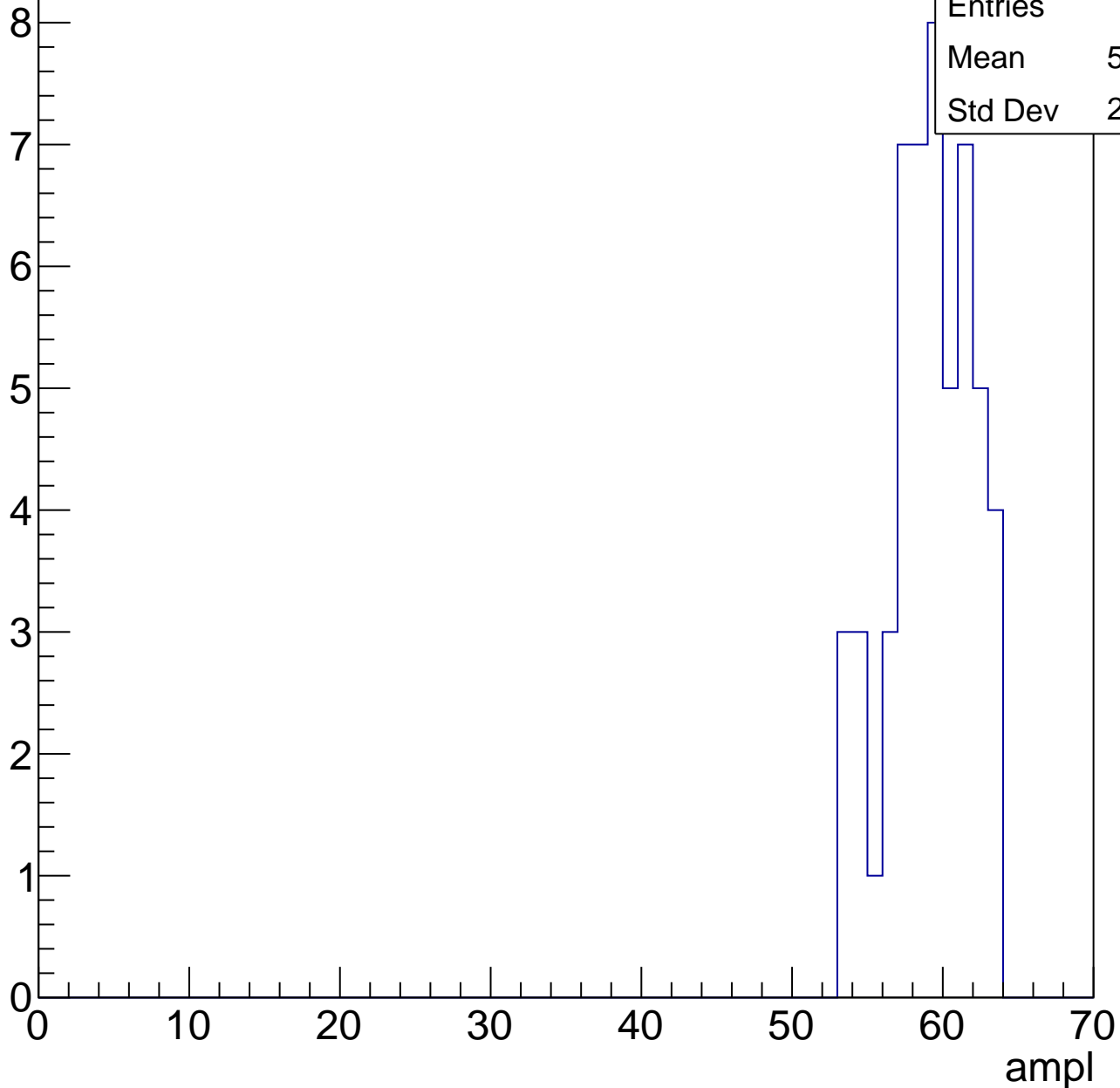
Entries	41
Mean	54.27
Std Dev	2.972



B1L102S, U14-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

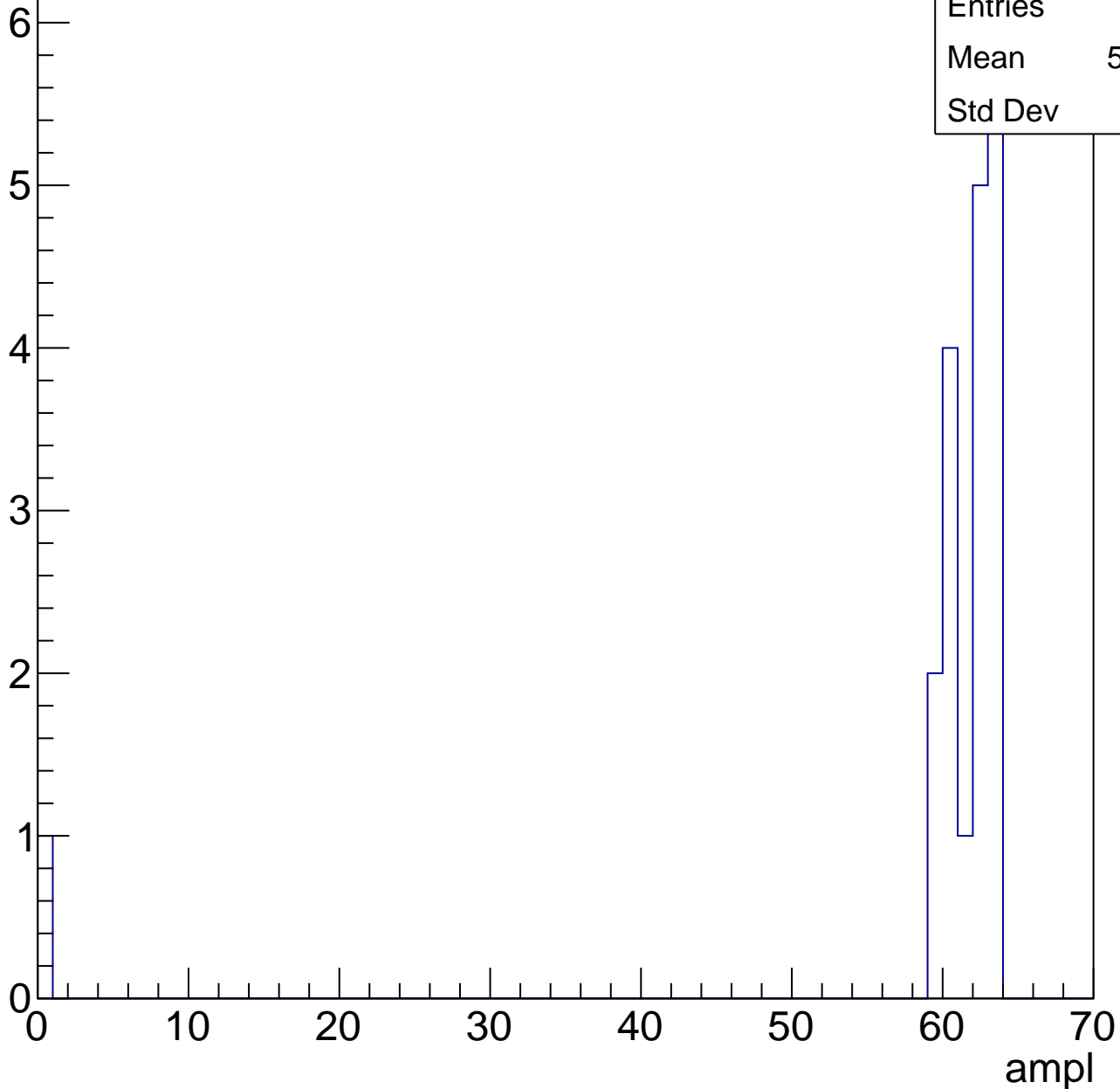


B1L102S, U14-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.26
Std Dev	13.8



B1L102S, U14-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch91, adc0

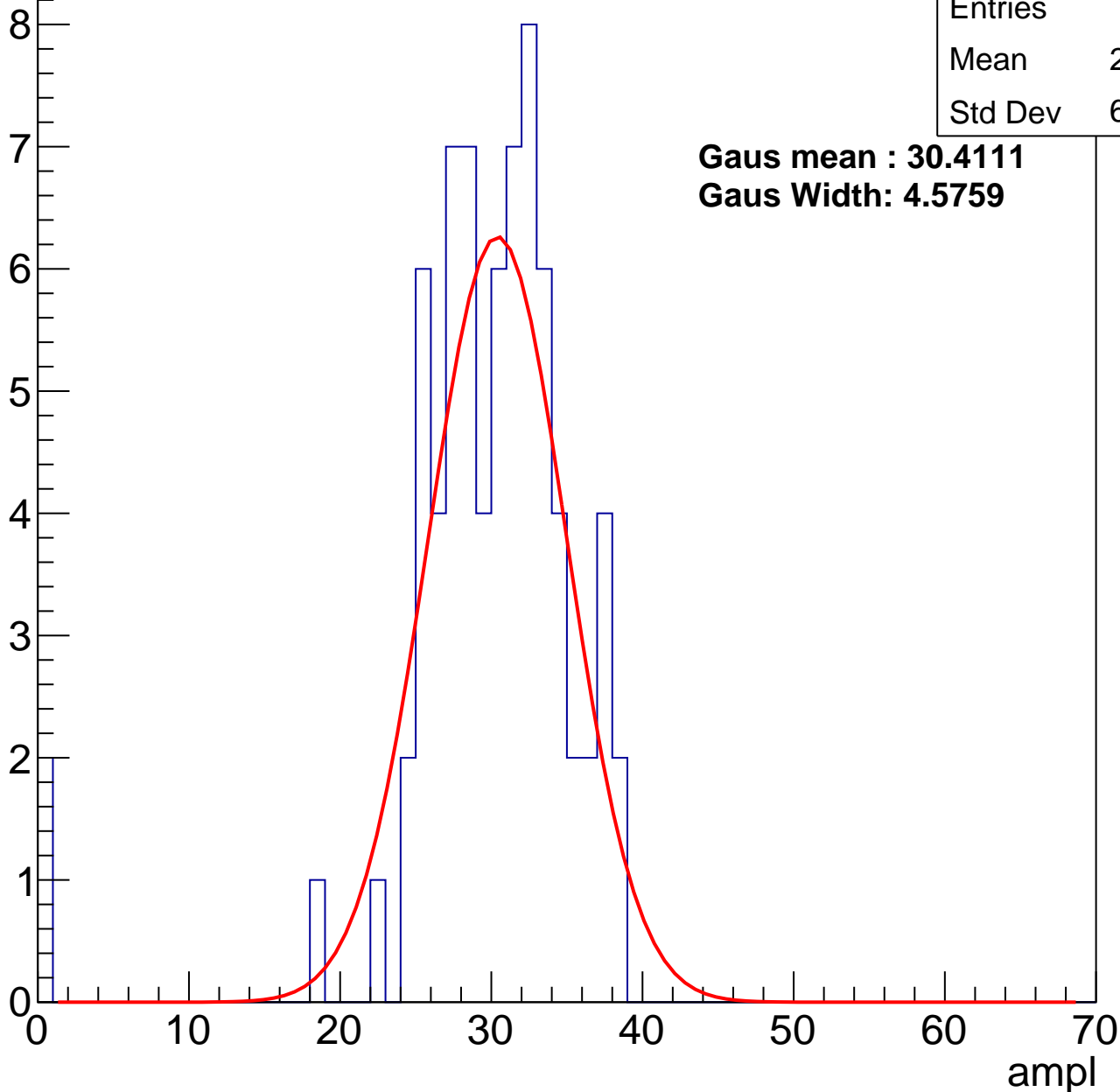
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.28
Std Dev	6.275

Gaus mean : 30.4111

Gaus Width: 4.5759



B1L102S, U14-ch91, adc1

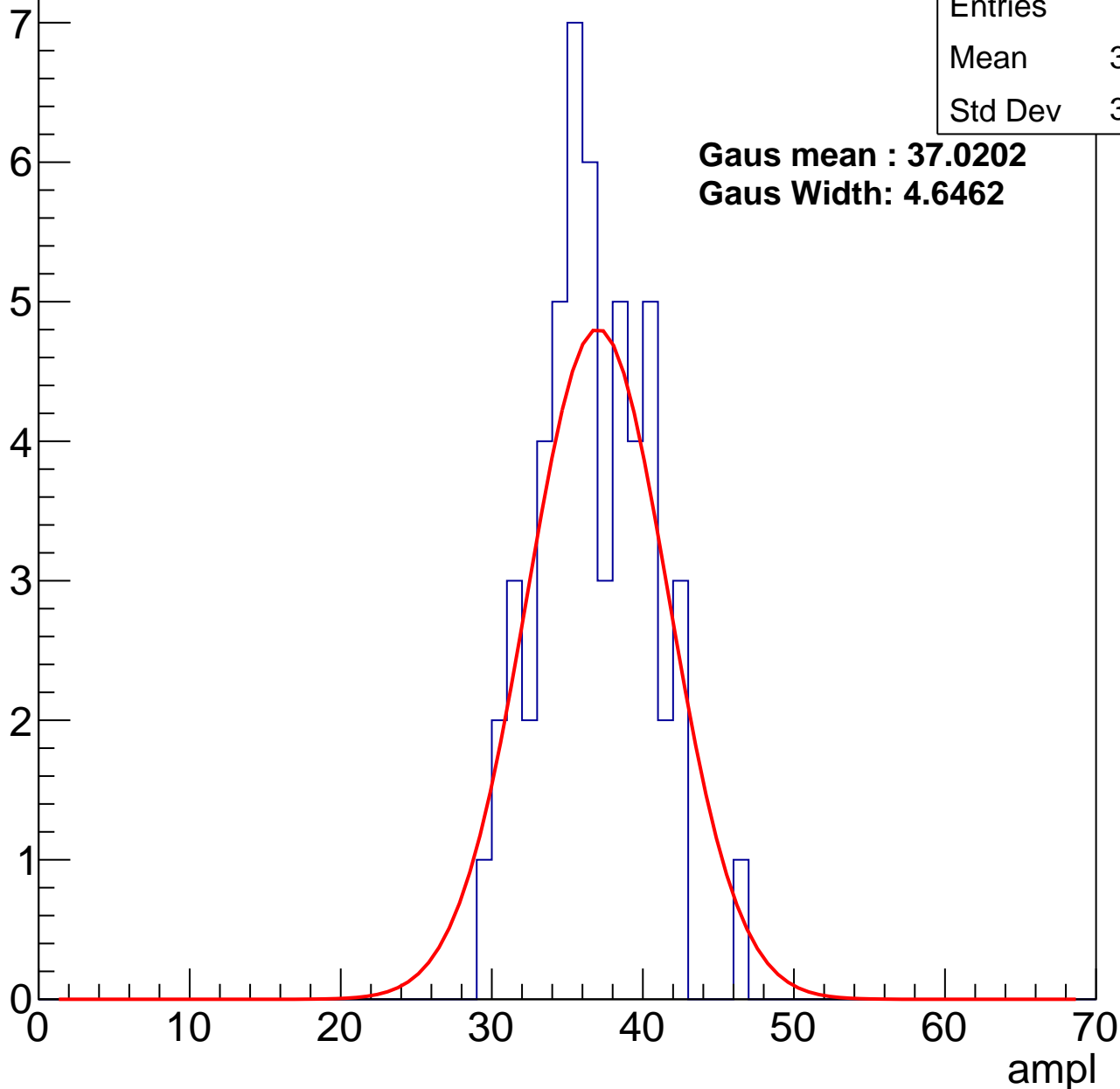
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	36.23
Std Dev	3.596

Gaus mean : 37.0202

Gaus Width: 4.6462



B1L102S, U14-ch91, adc2

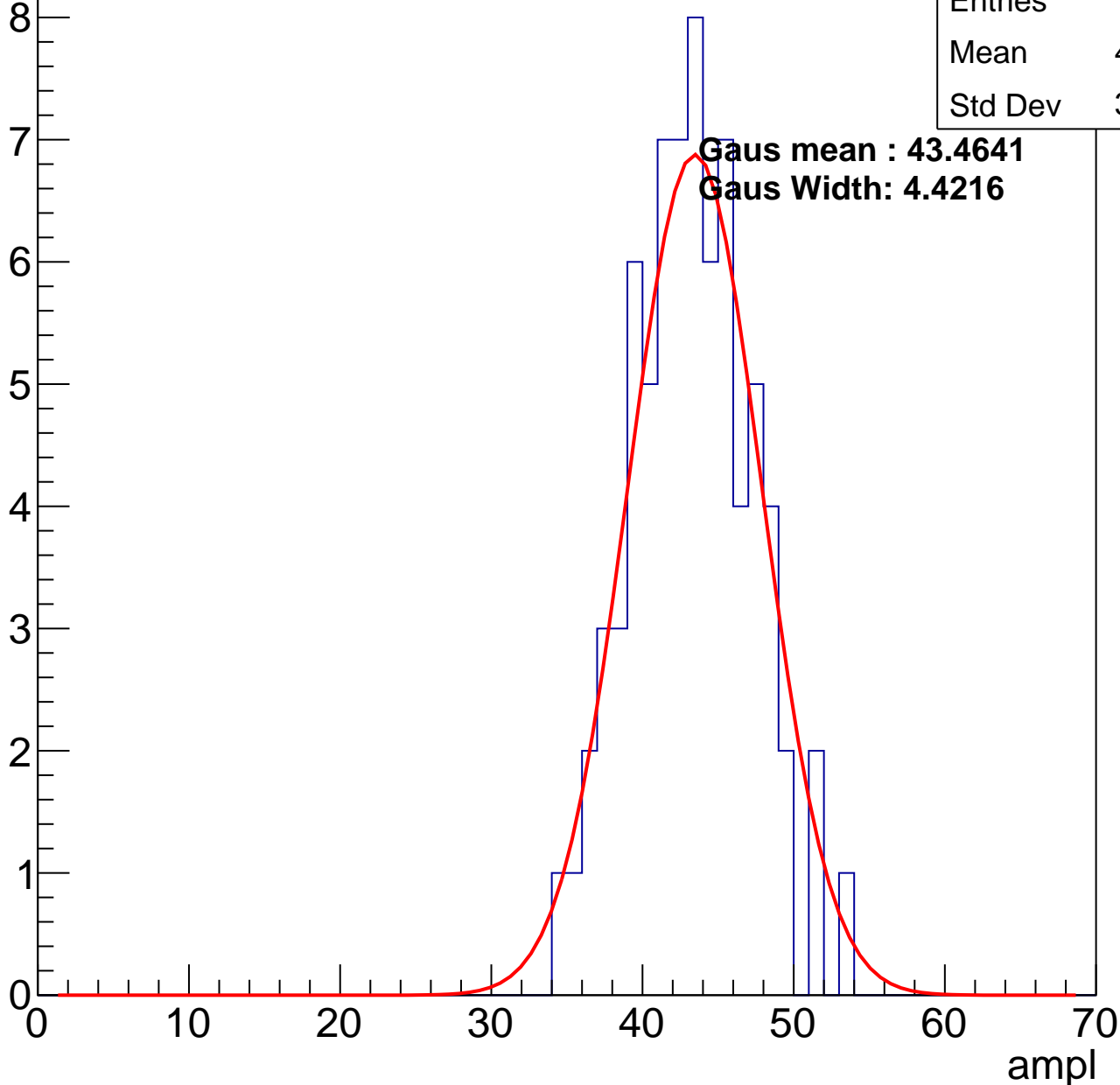
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.81
Std Dev	3.941

Gaus mean : 43.4641

Gaus Width: 4.4216

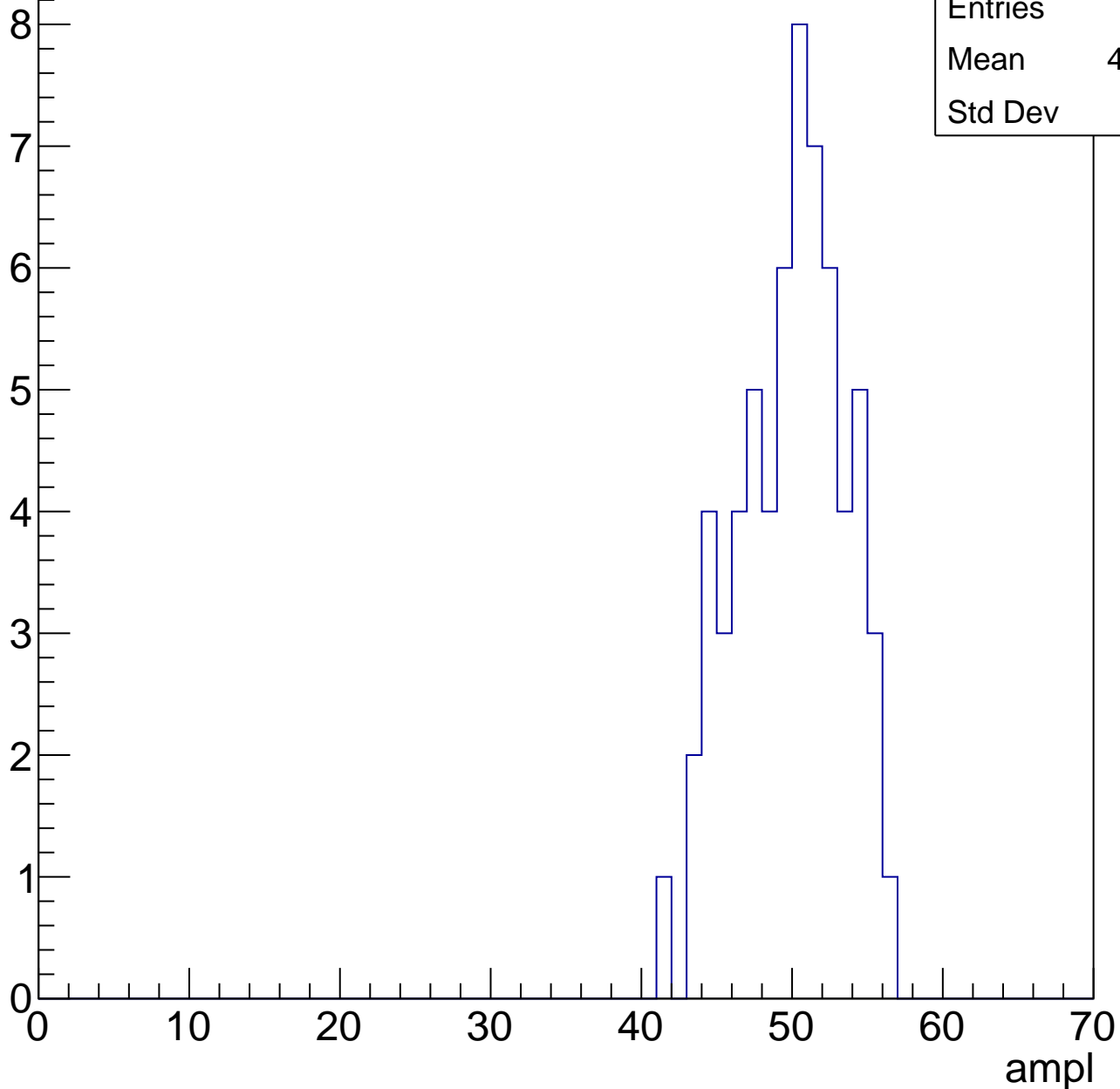


B1L102S, U14-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.44
Std Dev	3.49

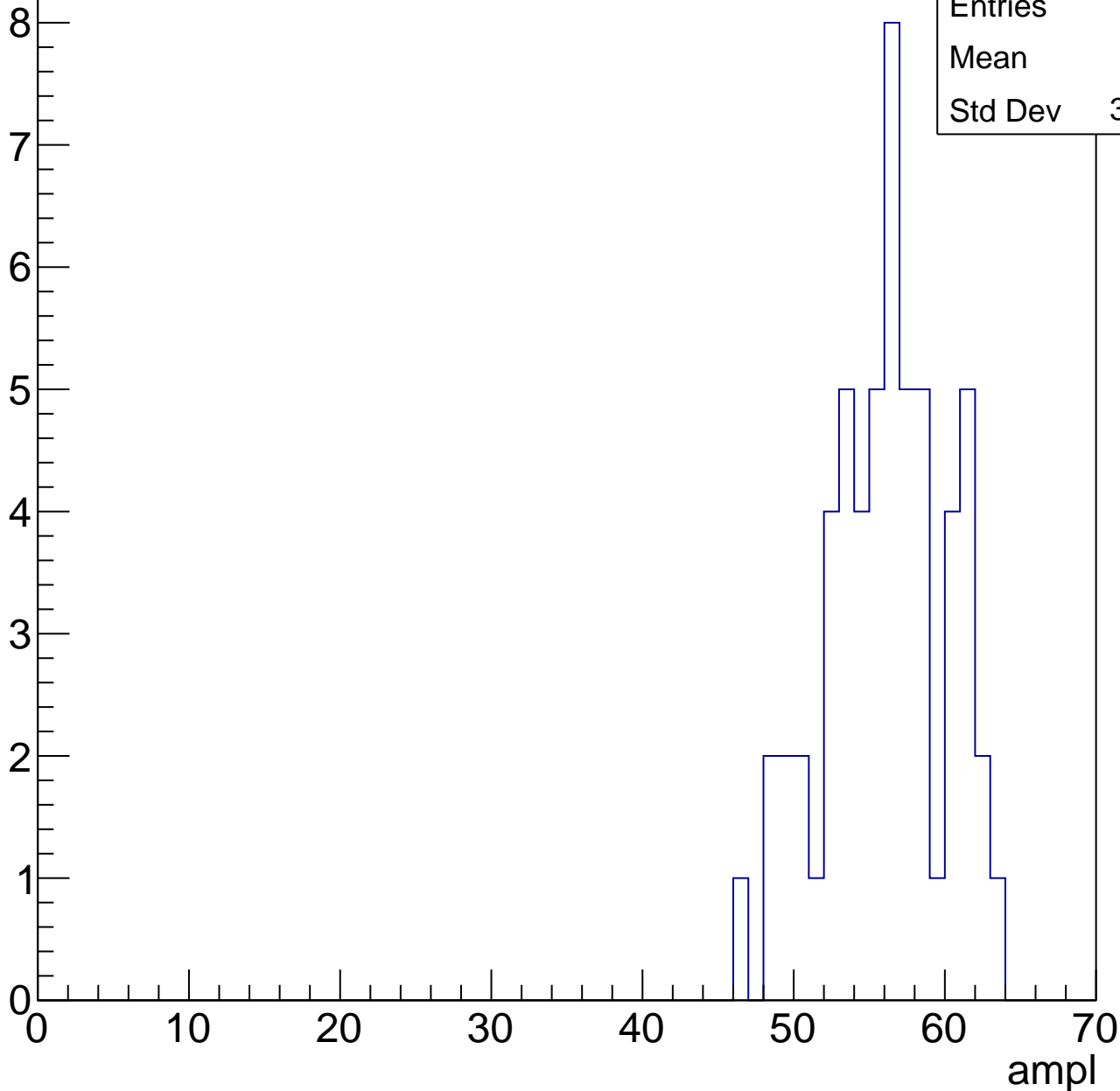


B1L102S, U14-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

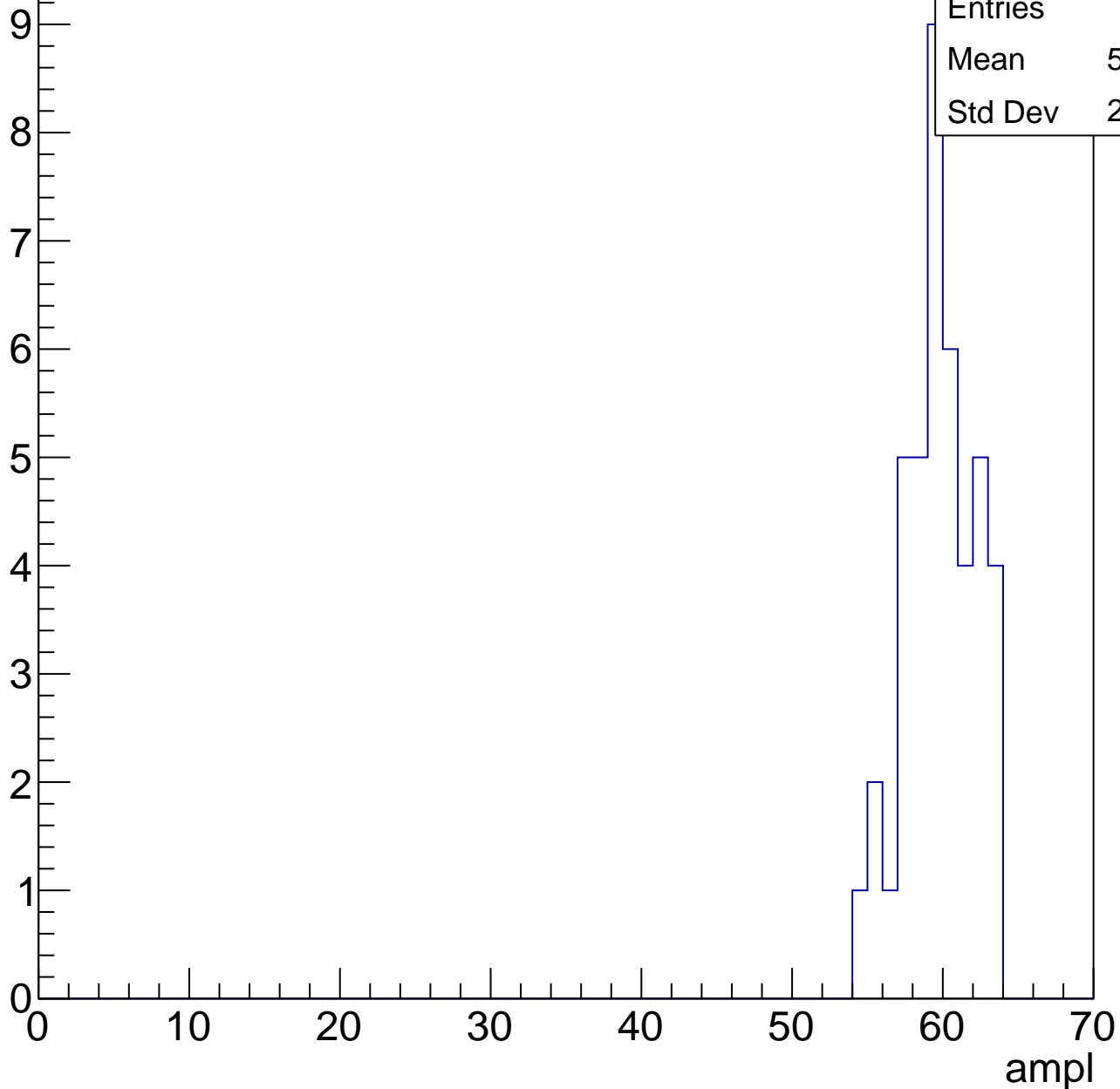
Entries	57
Mean	55.6
Std Dev	3.946



B1L102S, U14-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

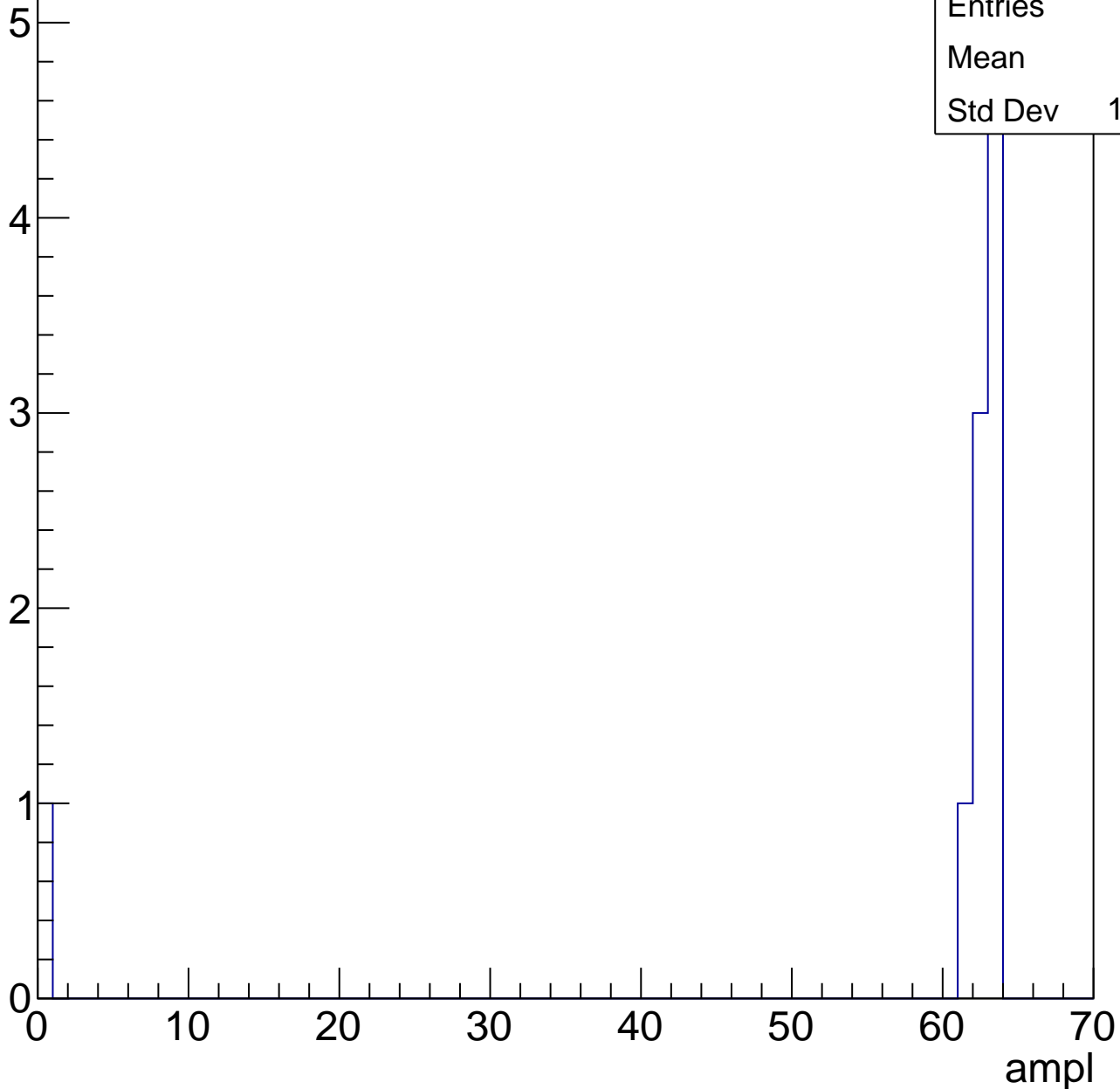


B1L102S, U14-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	56.2
Std Dev	18.74



B1L102S, U14-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch92, adc0

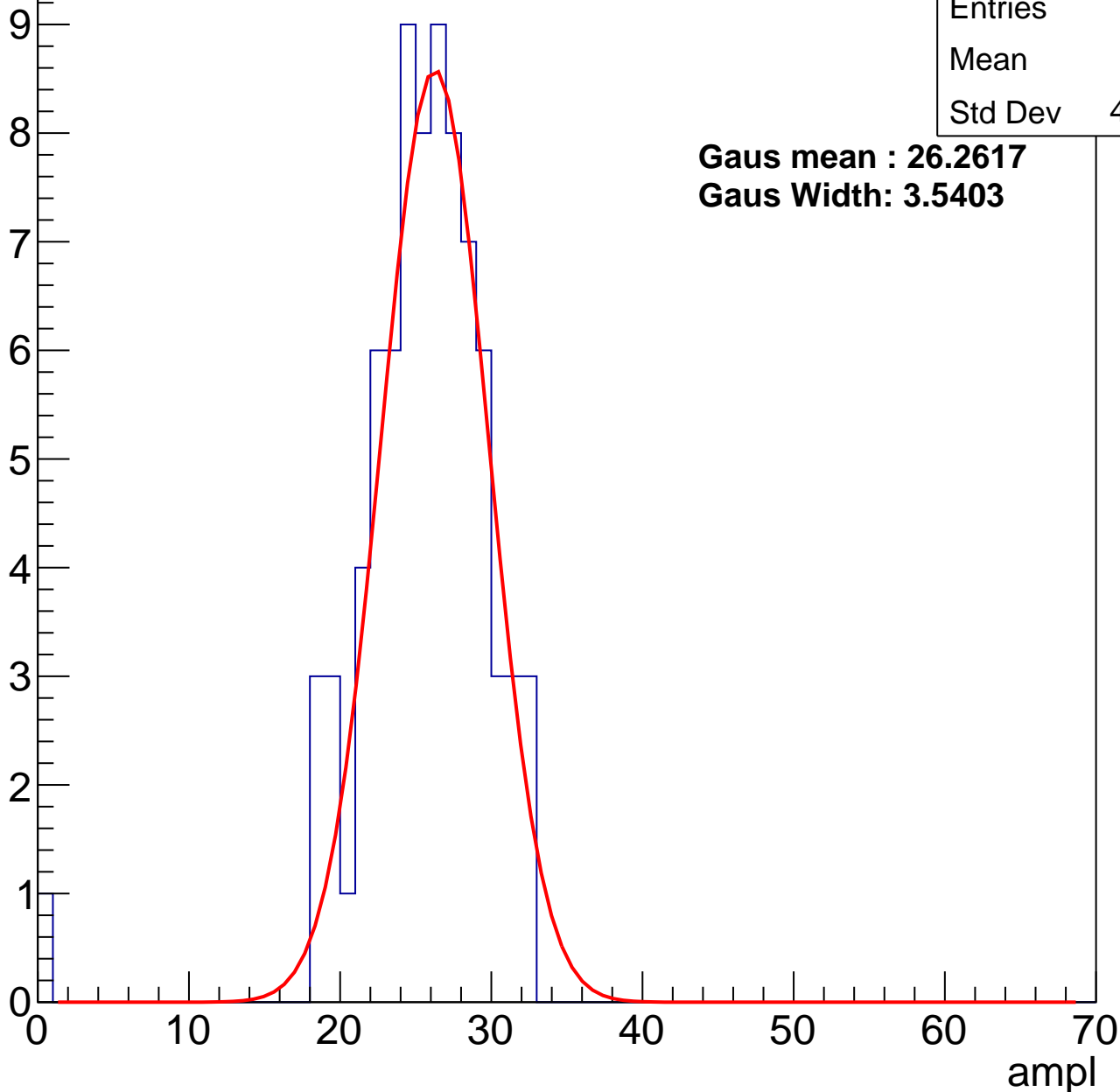
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	25
Std Dev	4.453

Gaus mean : 26.2617

Gaus Width: 3.5403



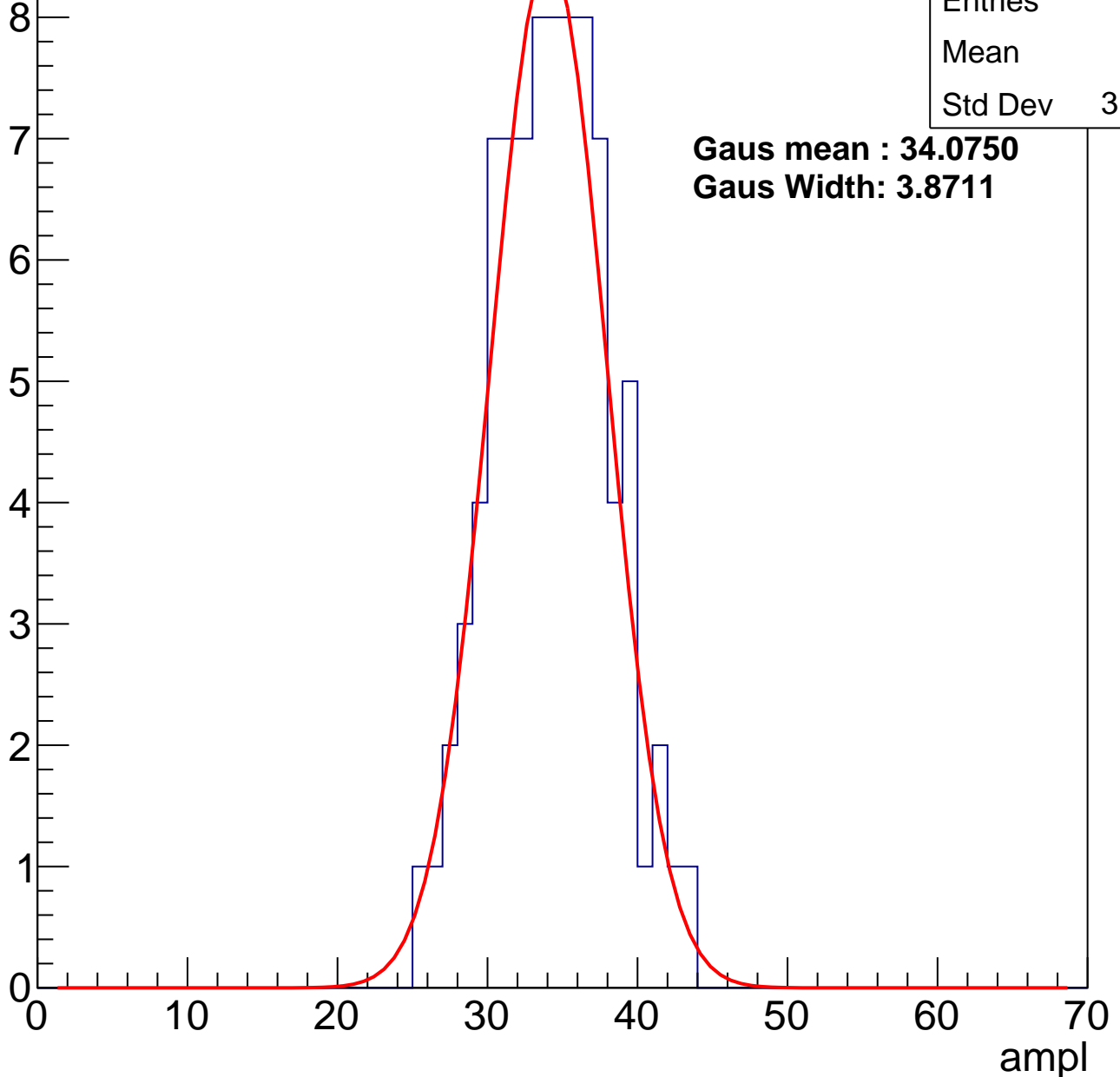
B1L102S, U14-ch92, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	33.8
Std Dev	3.803

Gaus mean : 34.0750
Gaus Width: 3.8711



B1L102S, U14-ch92, adc2

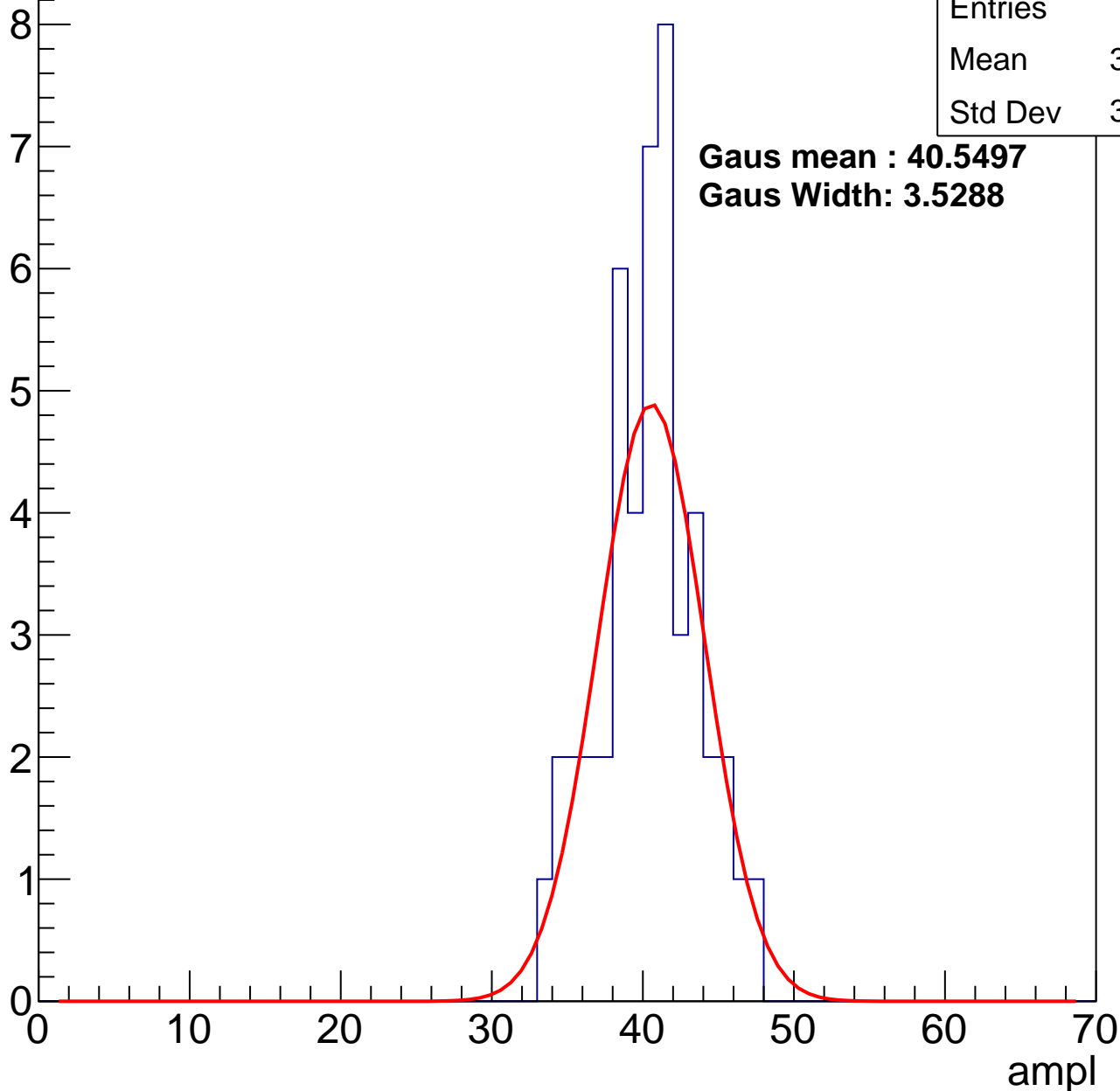
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	39.96
Std Dev	3.169

Gaus mean : 40.5497

Gaus Width: 3.5288

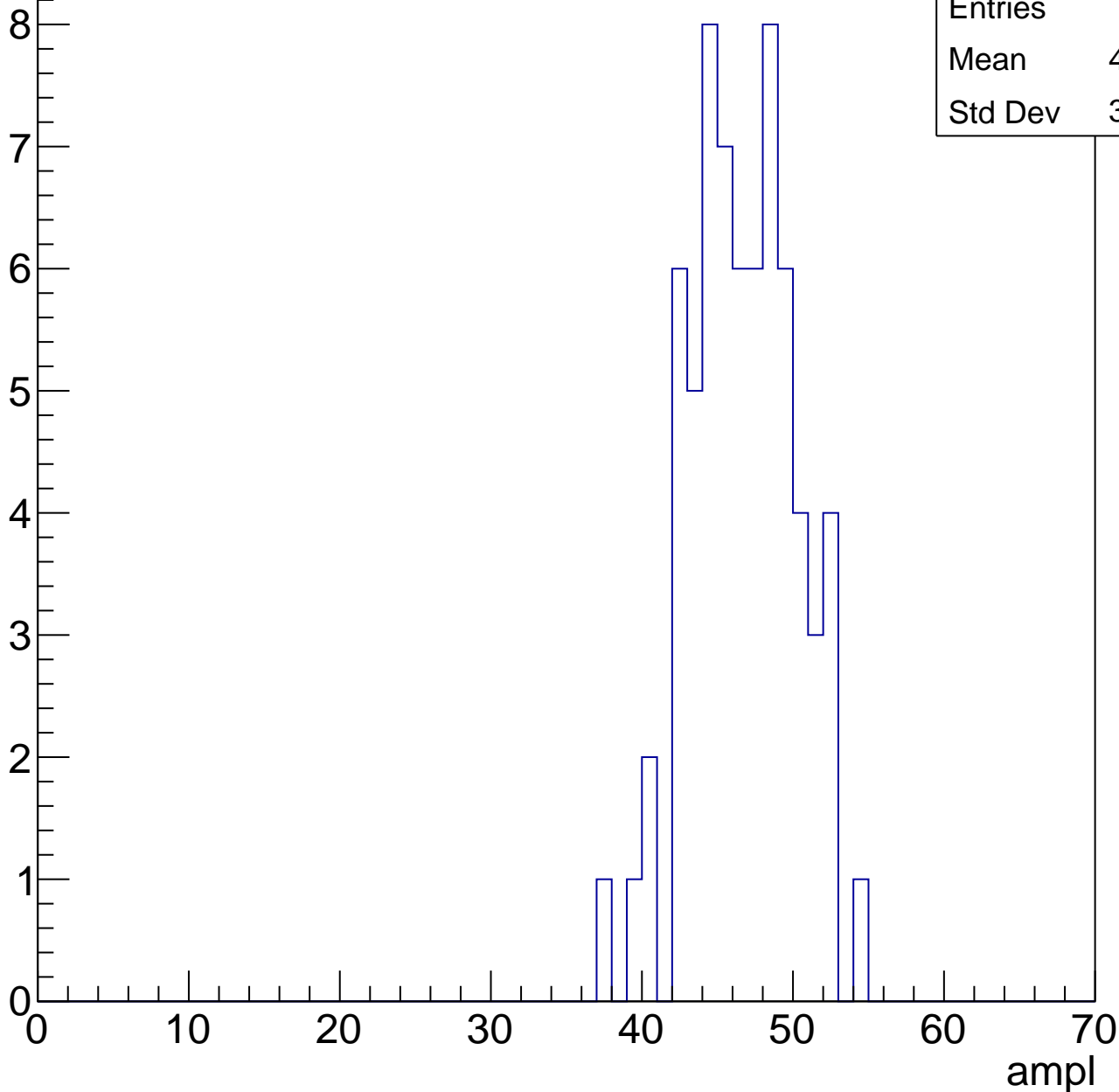


B1L102S, U14-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	46.19
Std Dev	3.469

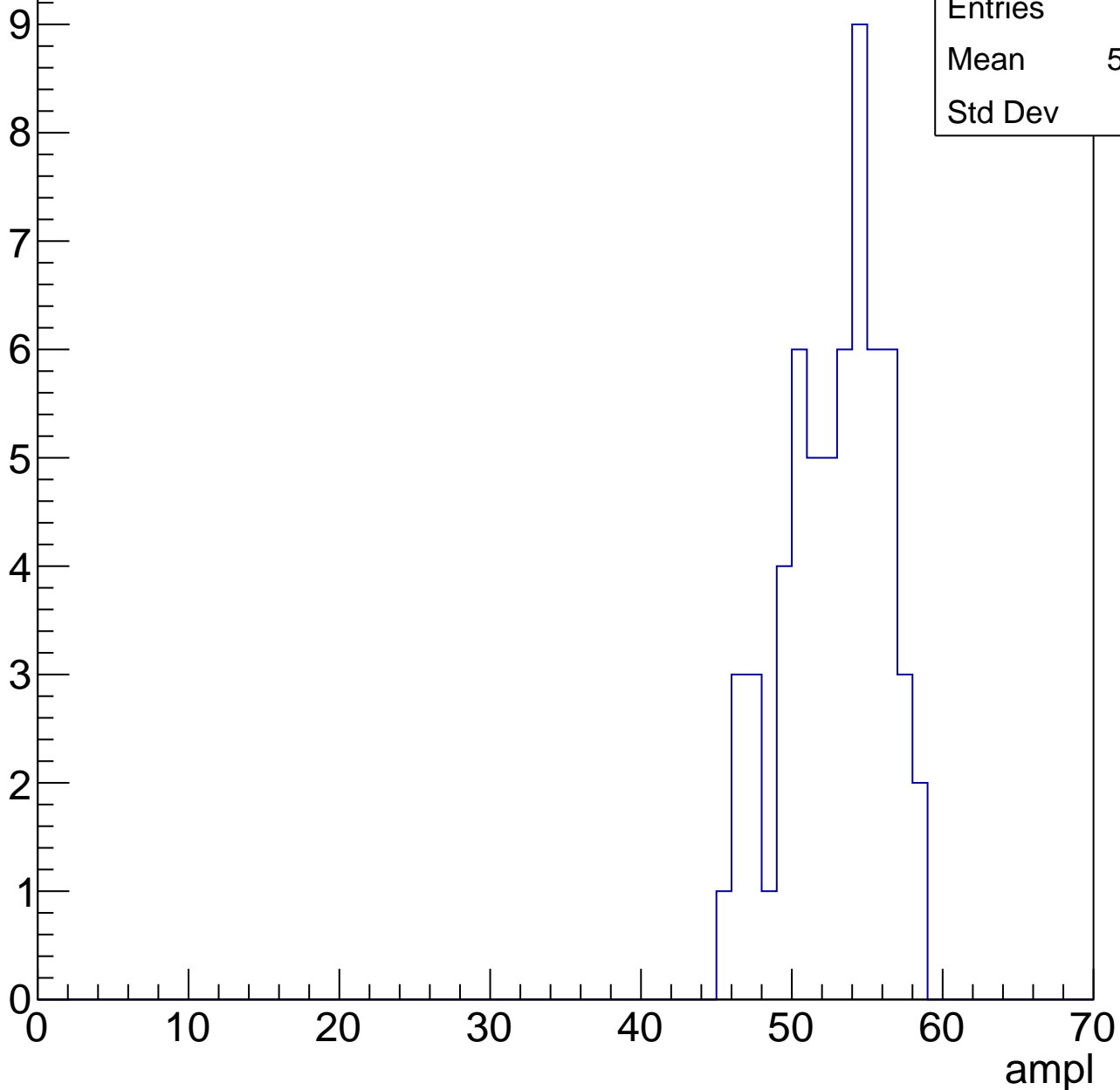


B1L102S, U14-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	52.33
Std Dev	3.28

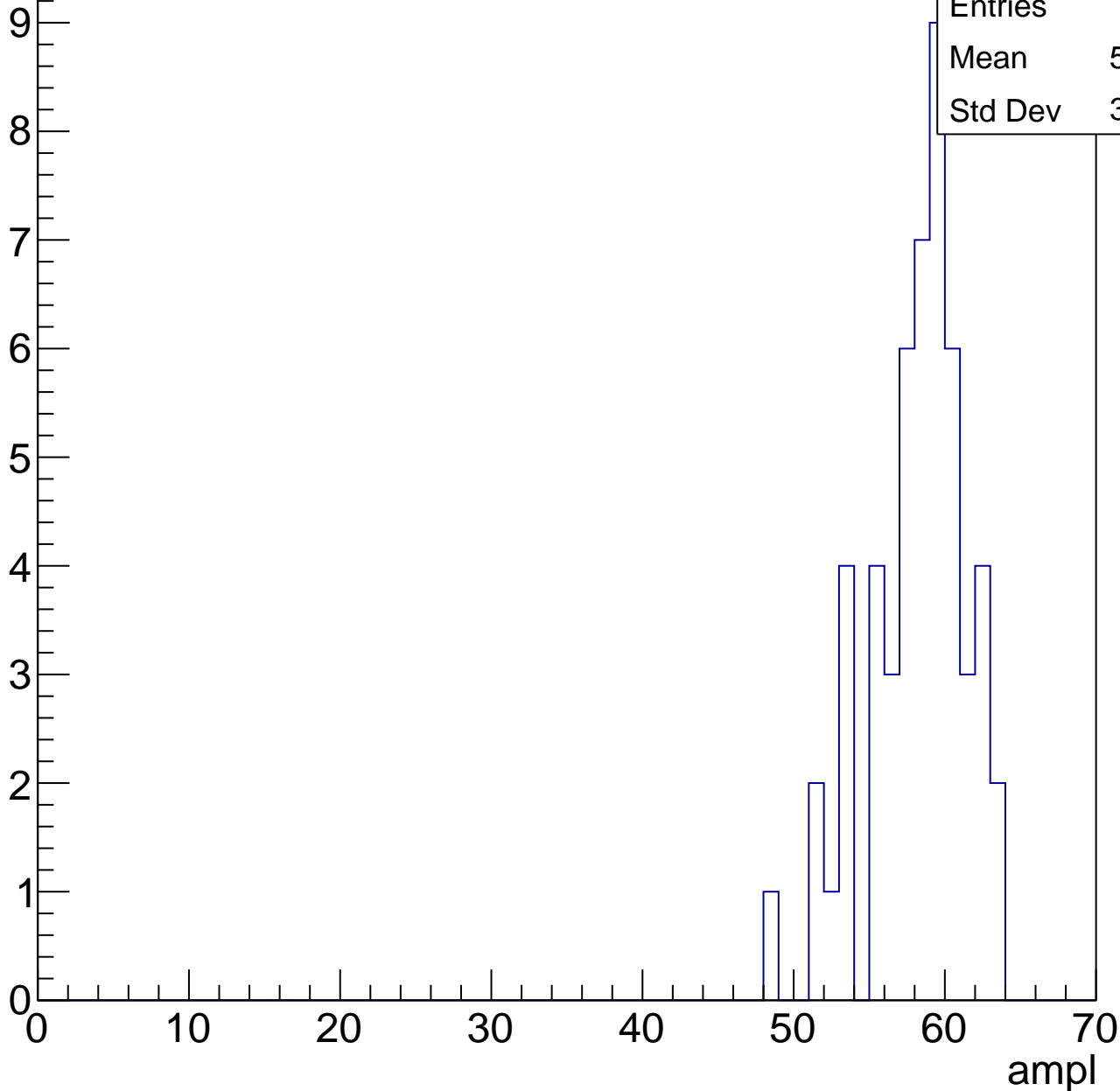


B1L102S, U14-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.65
Std Dev	3.269

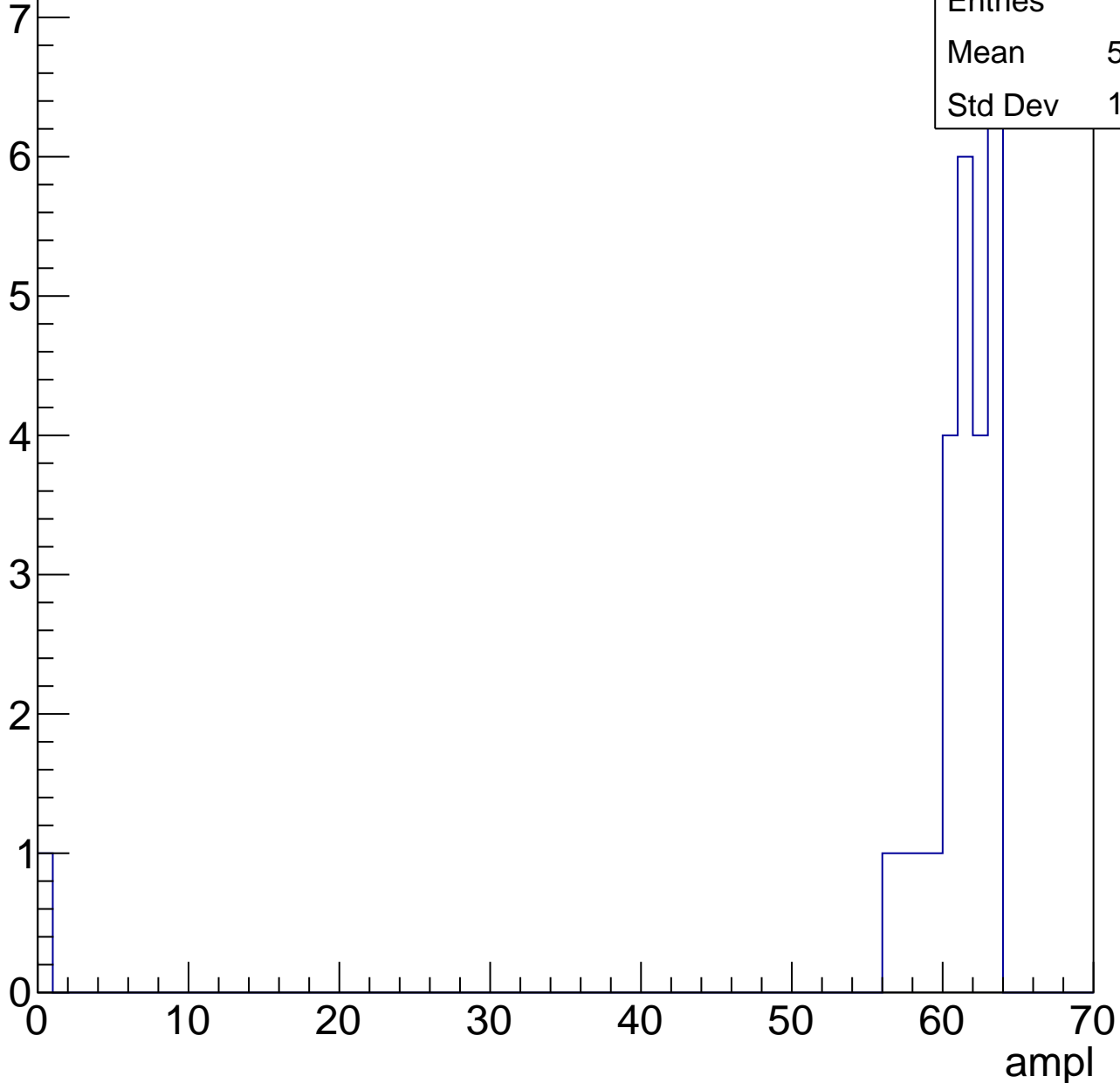


B1L102S, U14-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

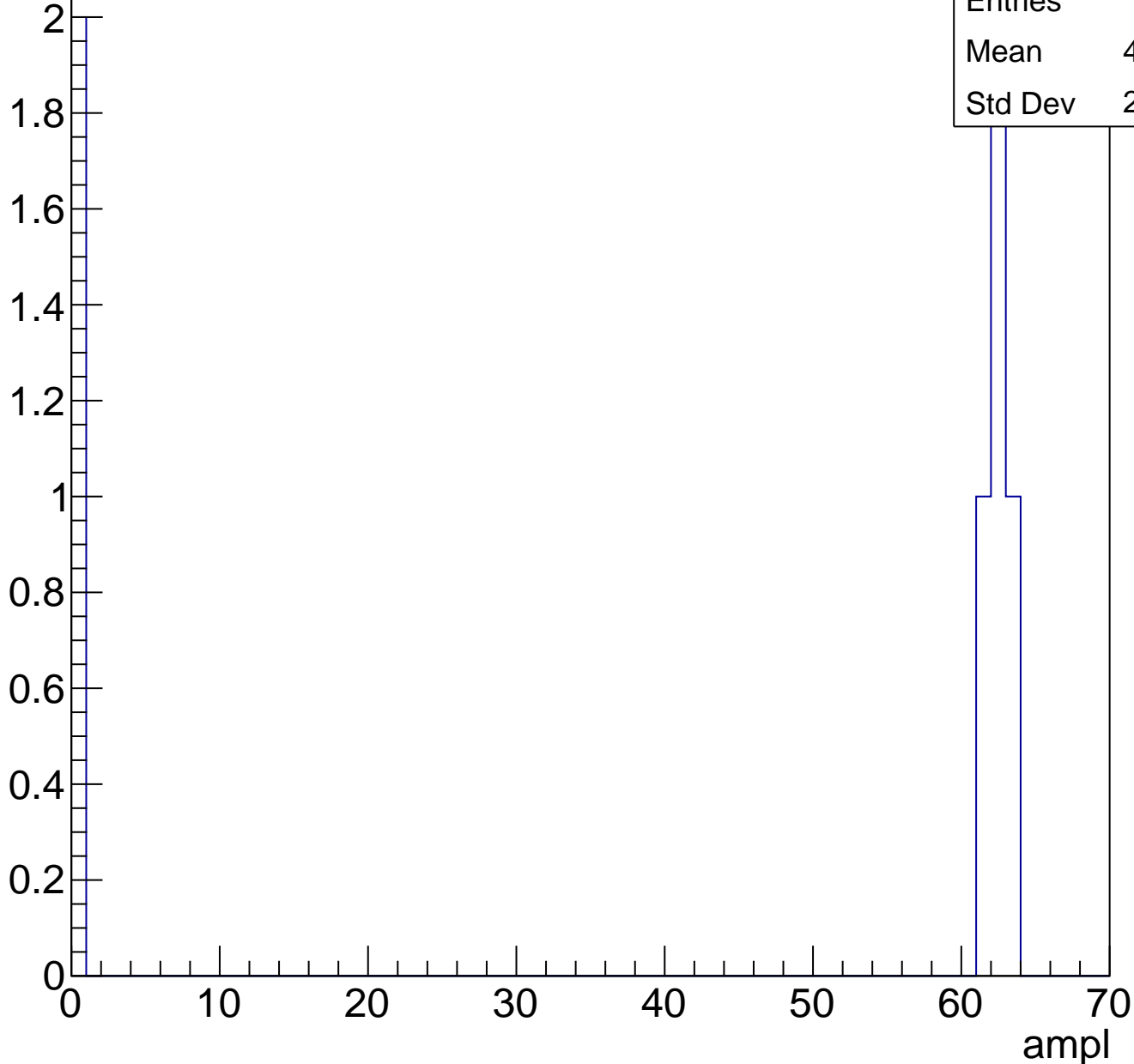
Entries	26
Mean	58.65
Std Dev	11.88



B1L102S, U14-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch93, adc0

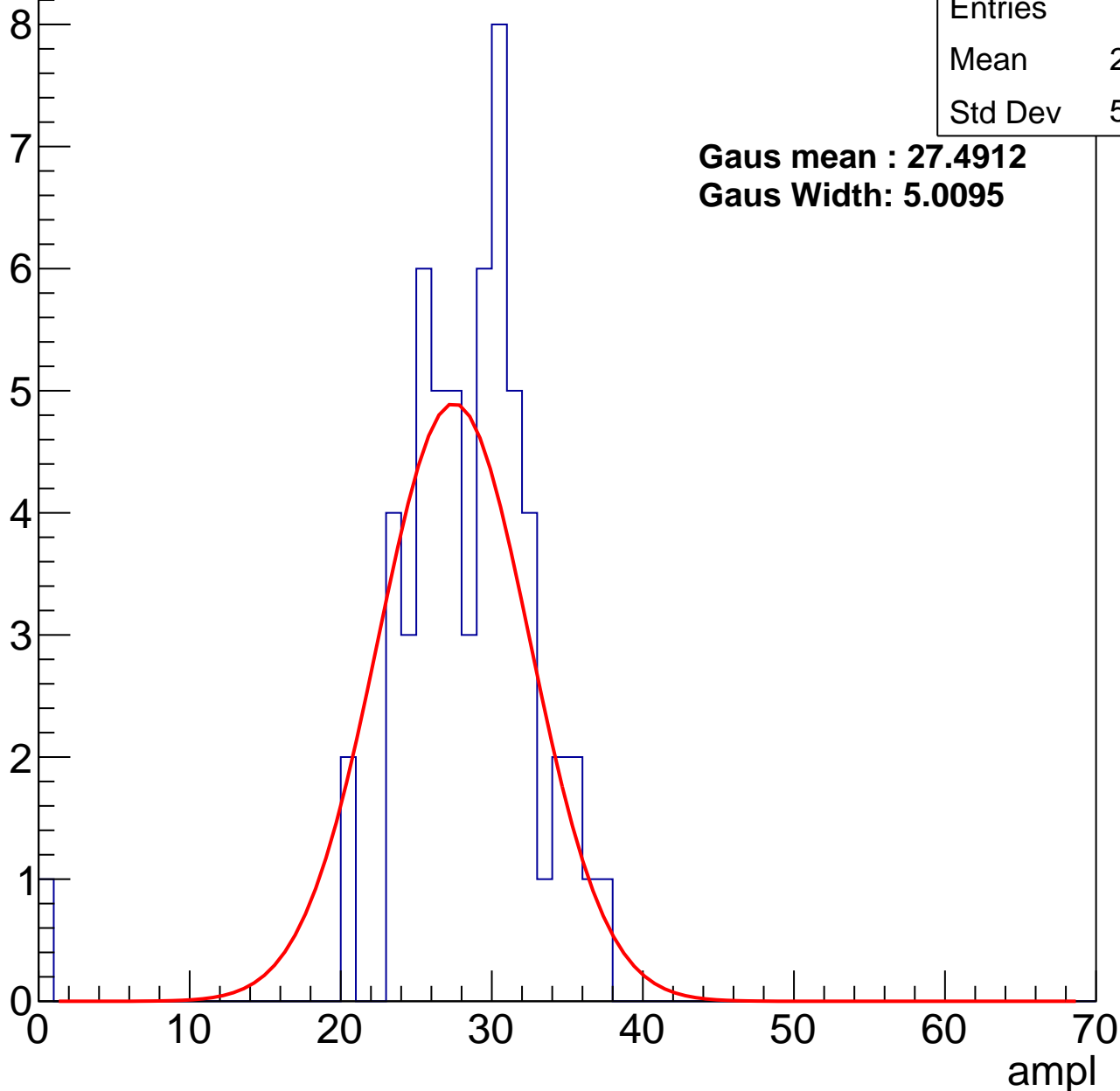
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.86
Std Dev	5.248

Gaus mean : 27.4912

Gaus Width: 5.0095



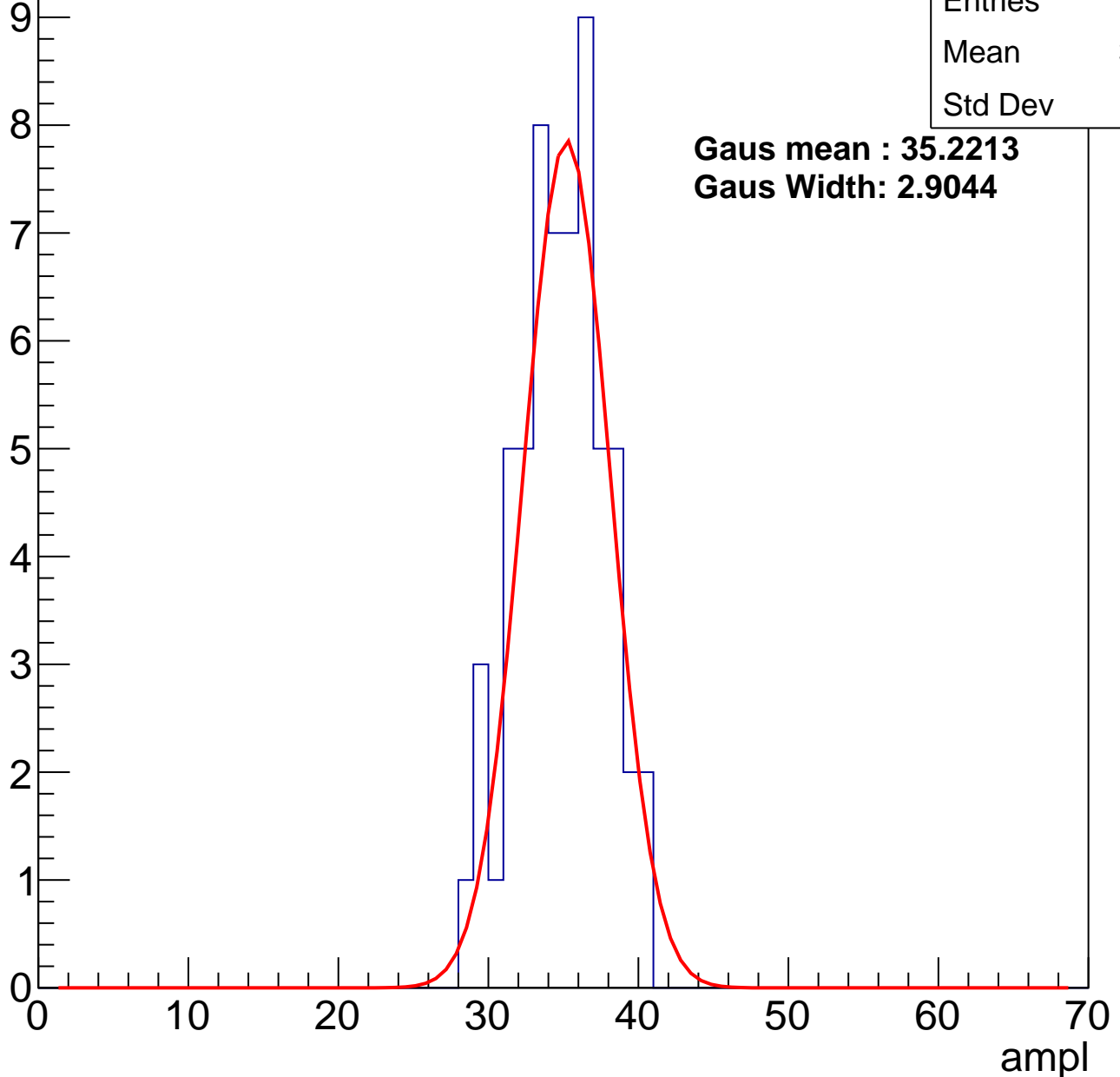
B1L102S, U14-ch93, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.4
Std Dev	2.83

Gaus mean : 35.2213
Gaus Width: 2.9044



B1L102S, U14-ch93, adc2

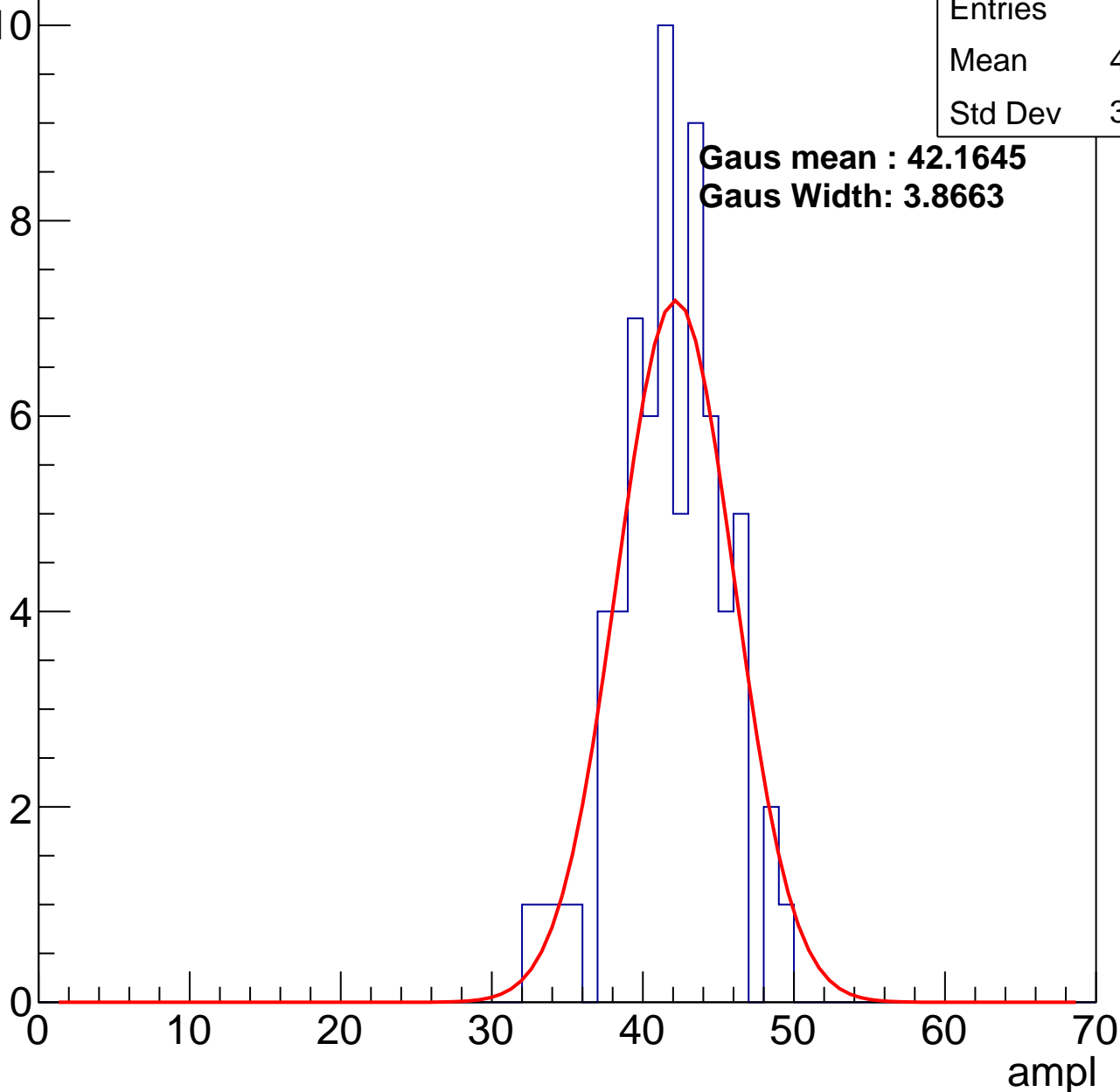
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.39
Std Dev	3.455

Gaus mean : 42.1645

Gaus Width: 3.8663

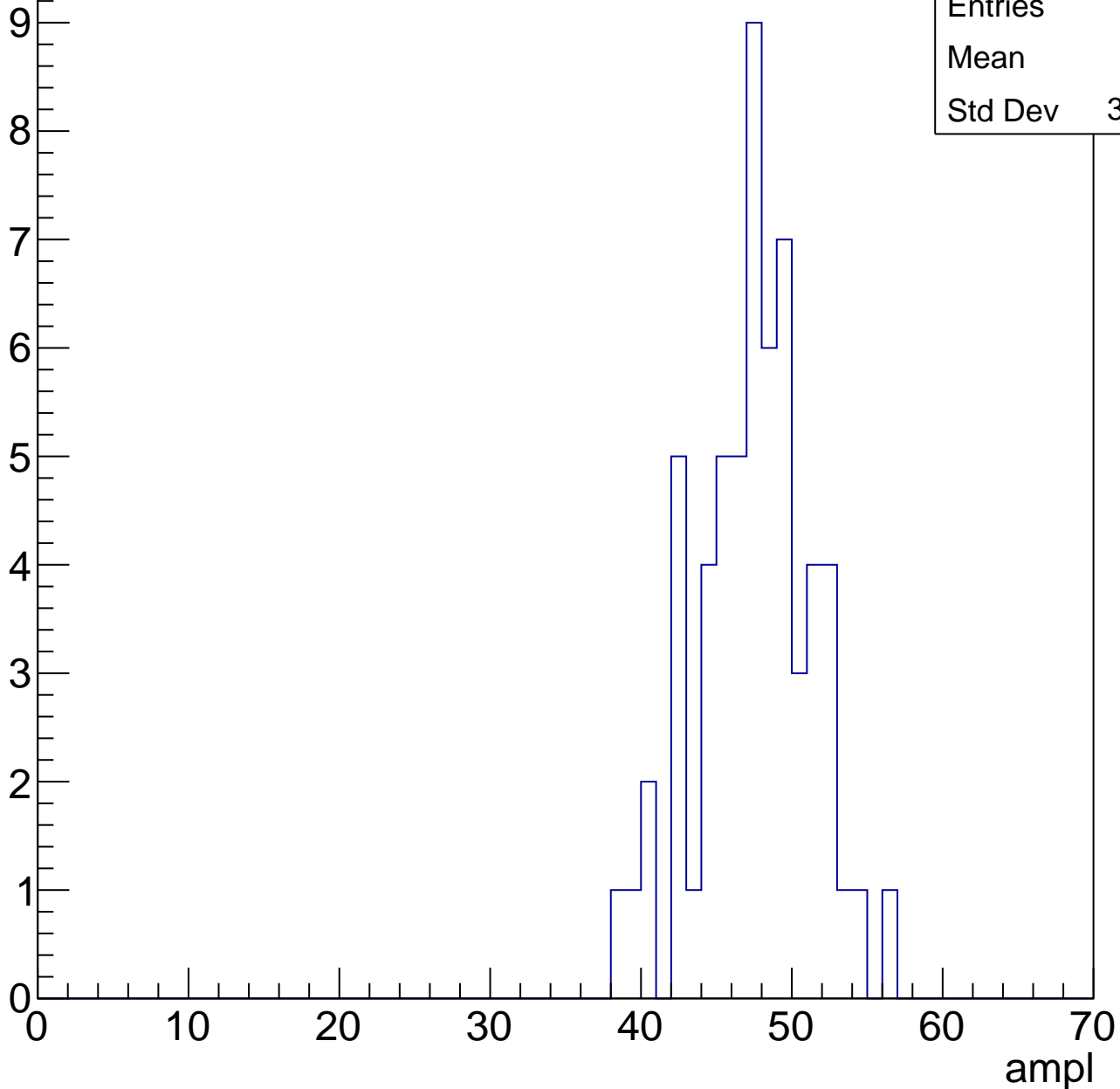


B1L102S, U14-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47
Std Dev	3.733

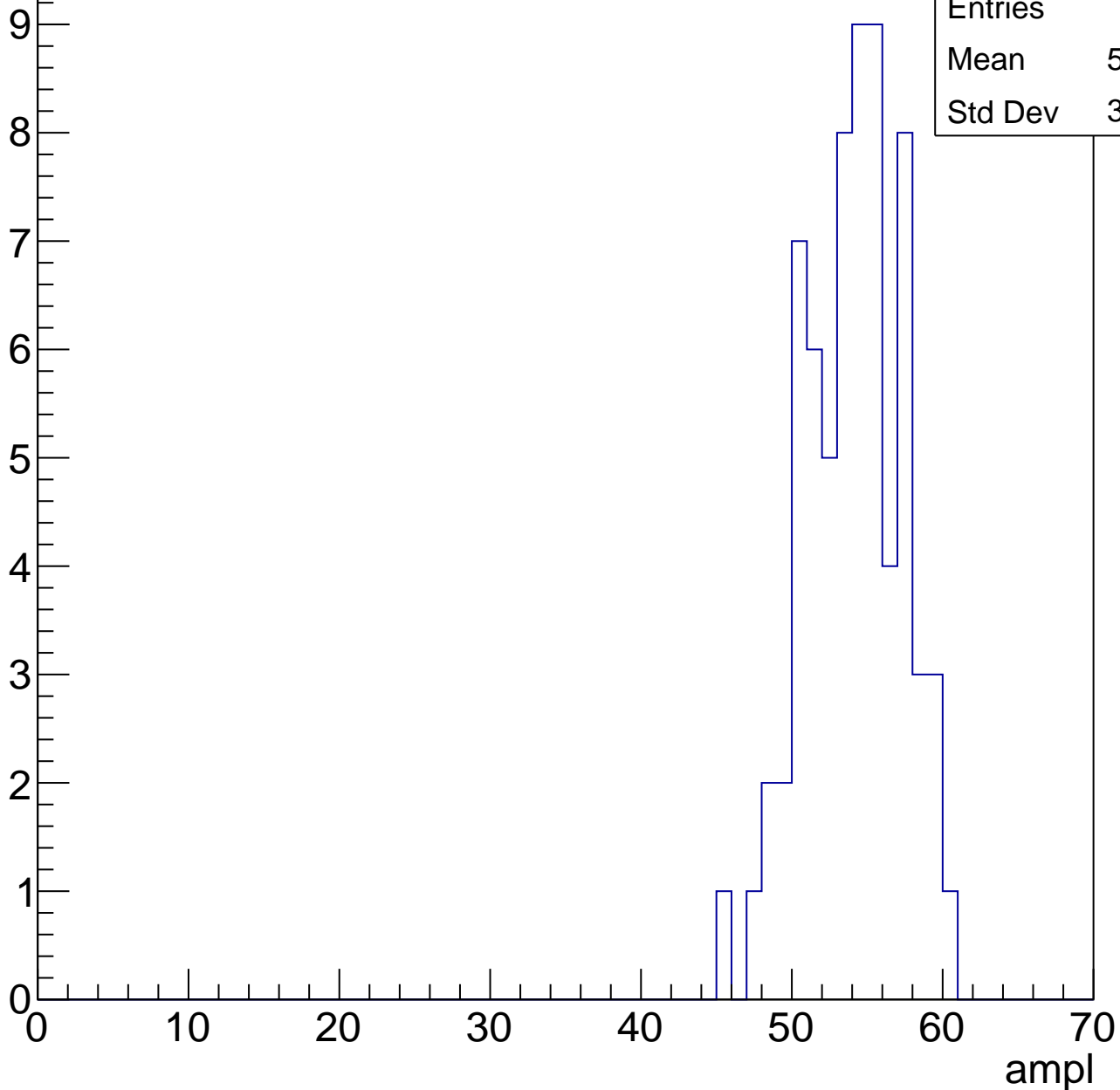


B1L102S, U14-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	53.59
Std Dev	3.164

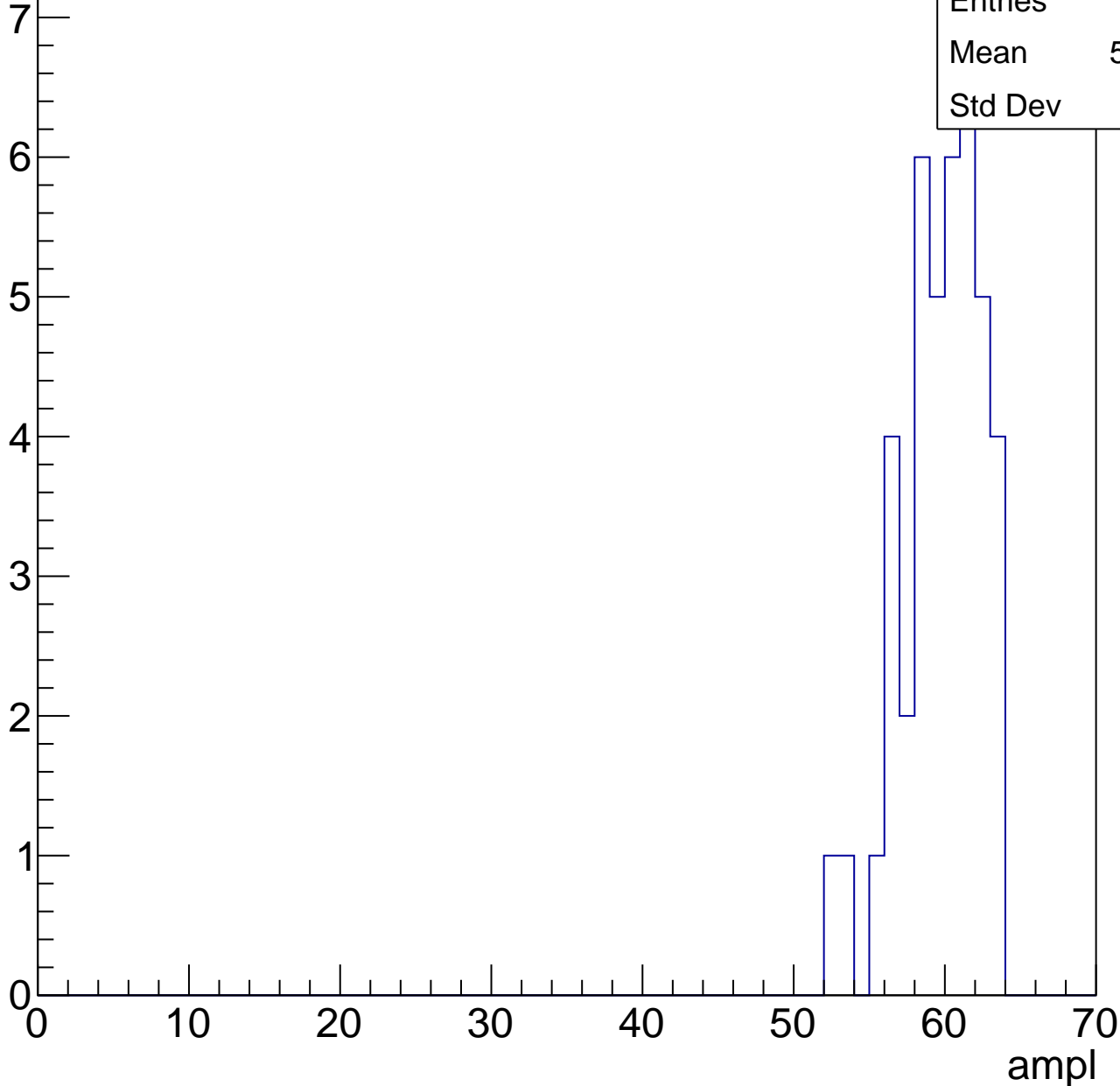


B1L102S, U14-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	59.29
Std Dev	2.63

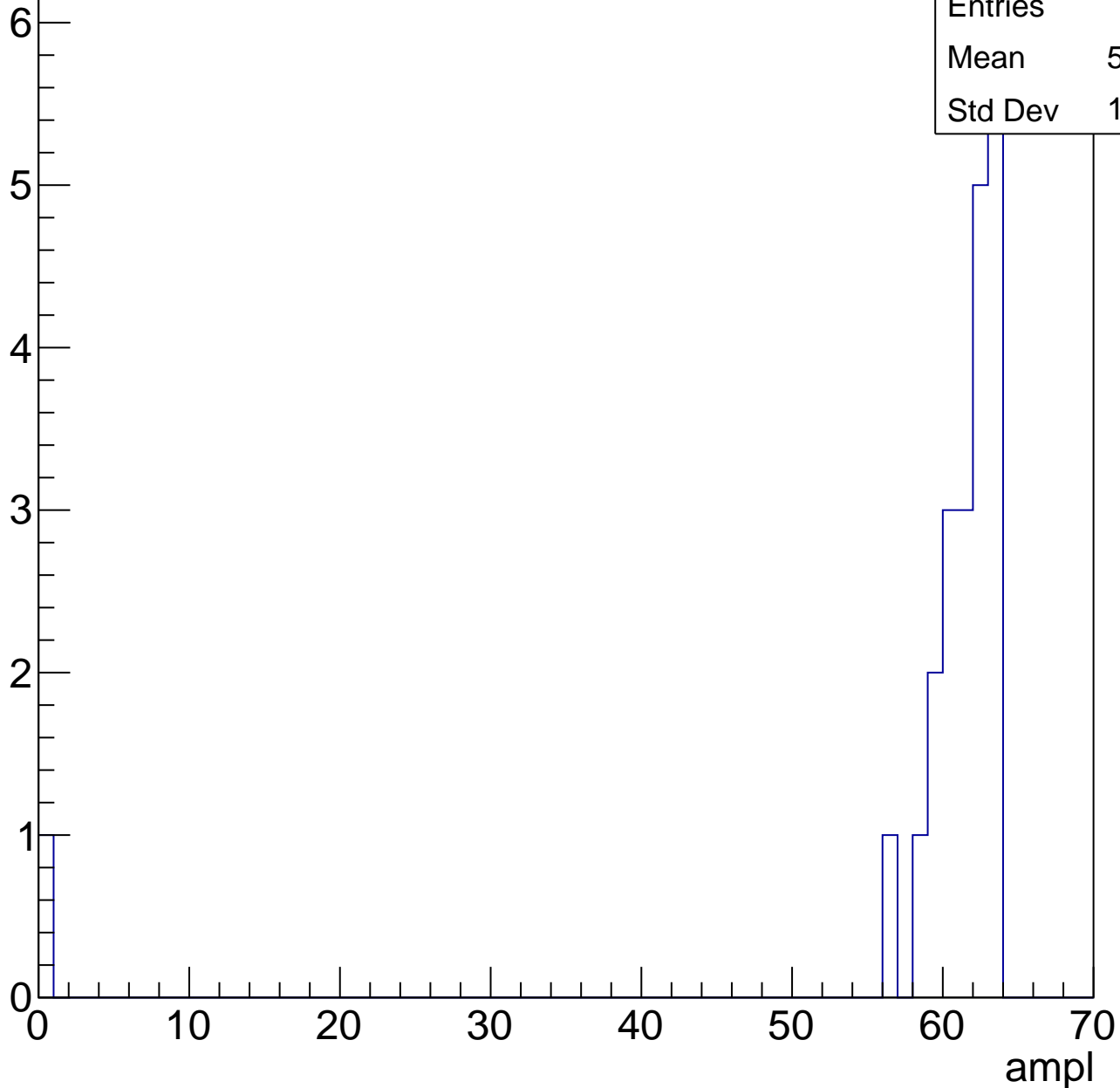


B1L102S, U14-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	58.32
Std Dev	12.86



B1L102S, U14-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch94, adc0

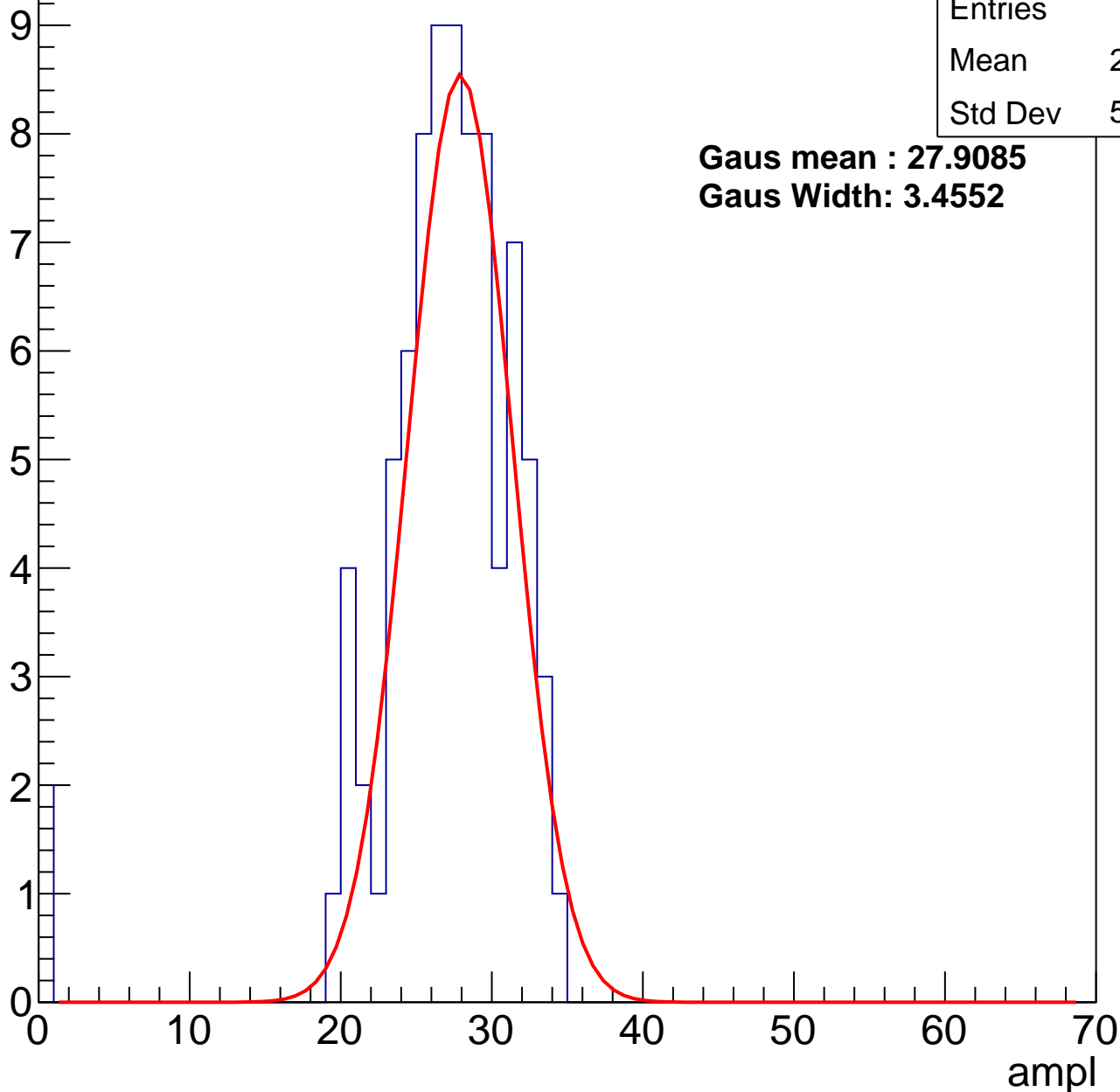
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	26.33
Std Dev	5.402

Gaus mean : 27.9085

Gaus Width: 3.4552



B1L102S, U14-ch94, adc1

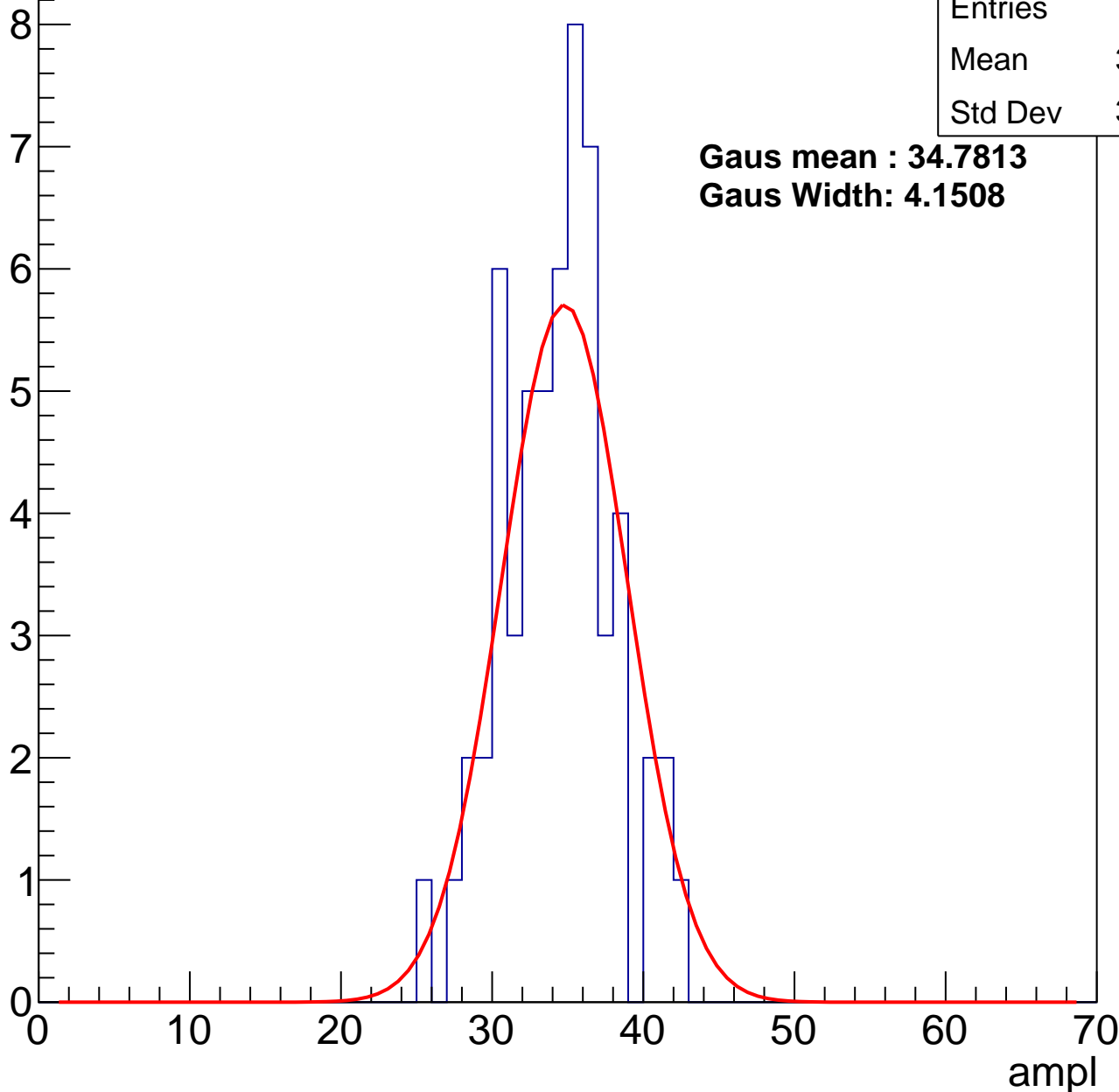
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	33.91
Std Dev	3.621

Gaus mean : 34.7813

Gaus Width: 4.1508



B1L102S, U14-ch94, adc2

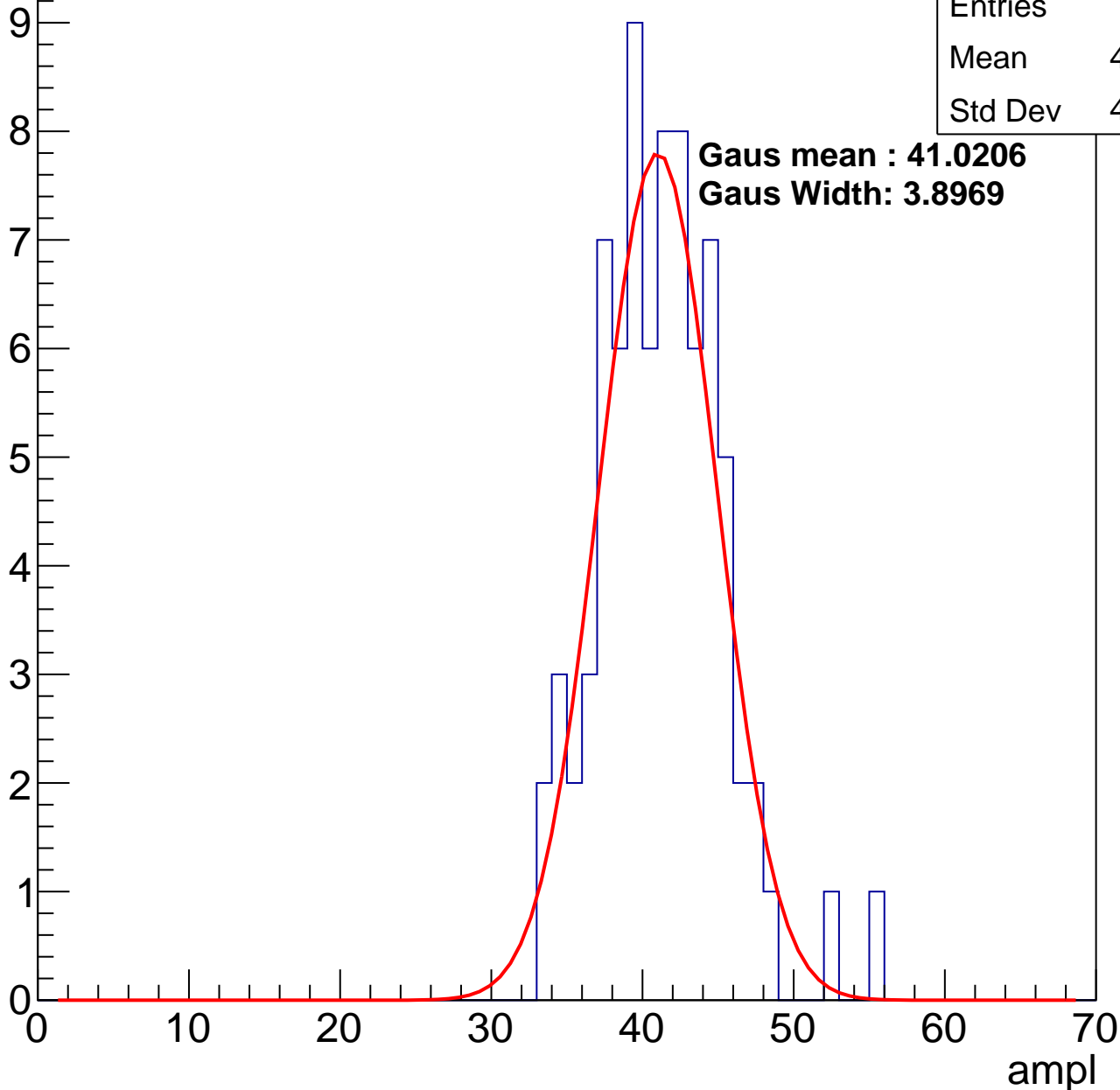
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	40.76
Std Dev	4.038

Gaus mean : 41.0206

Gaus Width: 3.8969

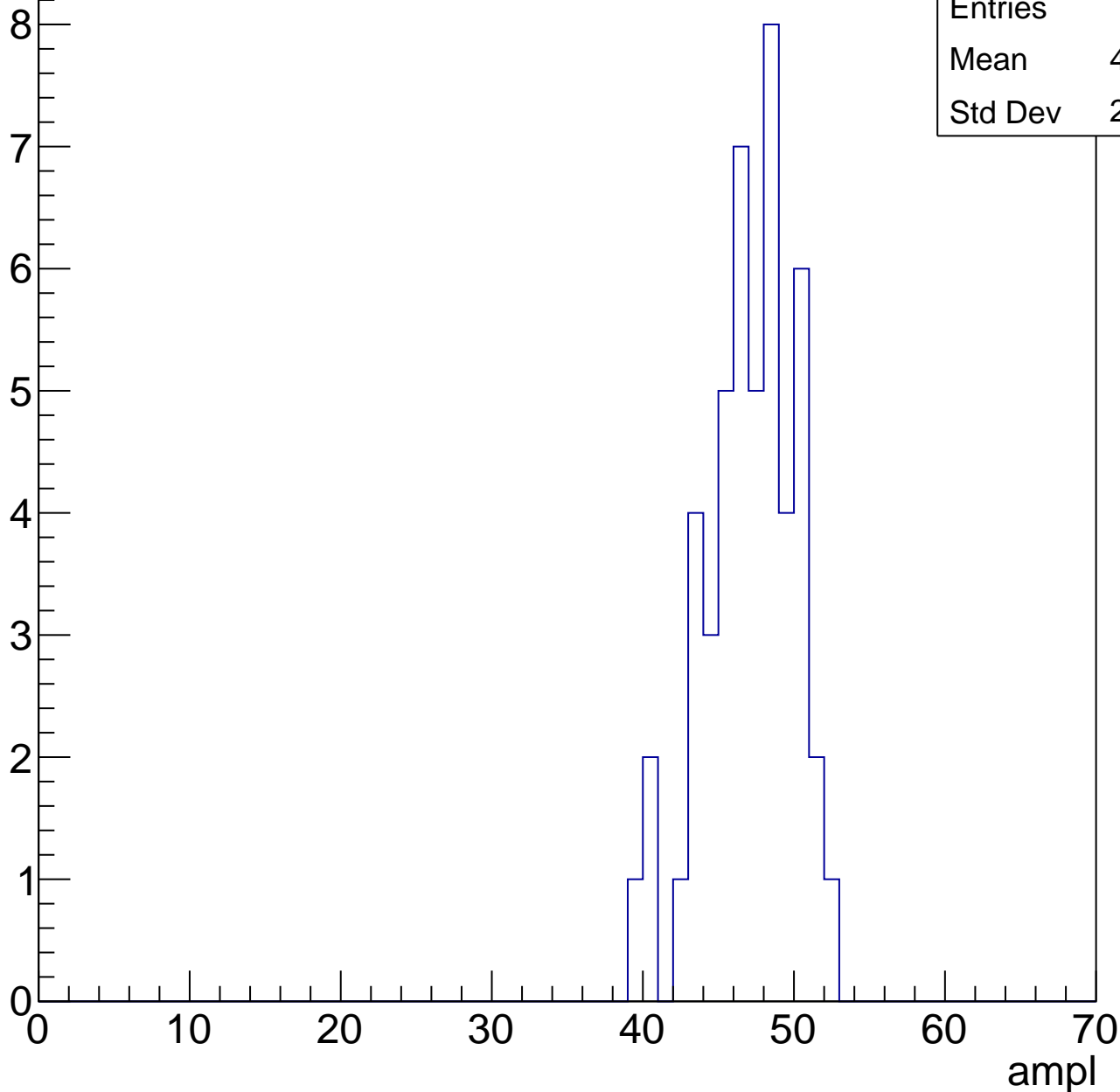


B1L102S, U14-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	46.55
Std Dev	2.963

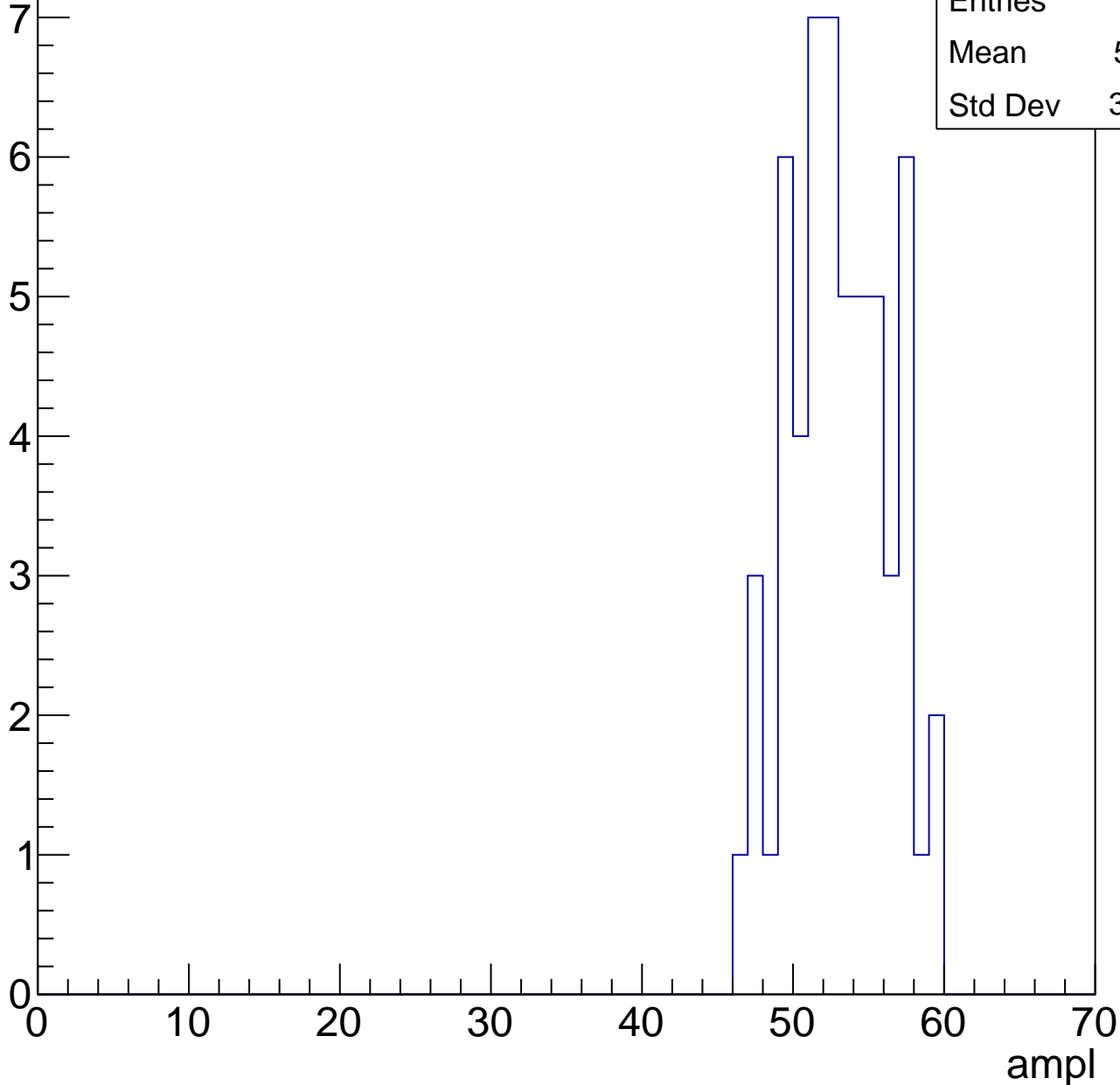


B1L102S, U14-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

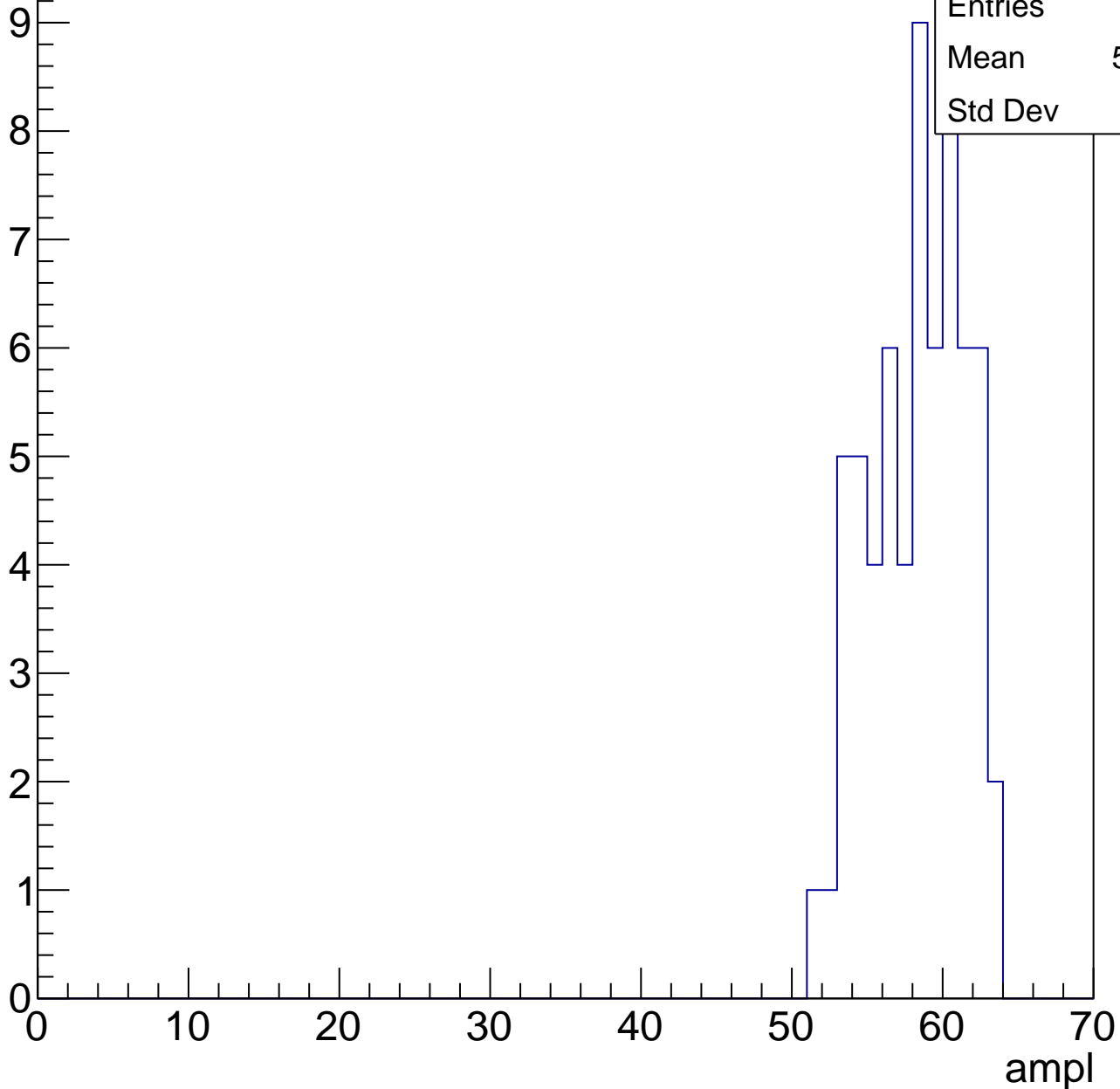
Entries	56
Mean	52.61
Std Dev	3.233



B1L102S, U14-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

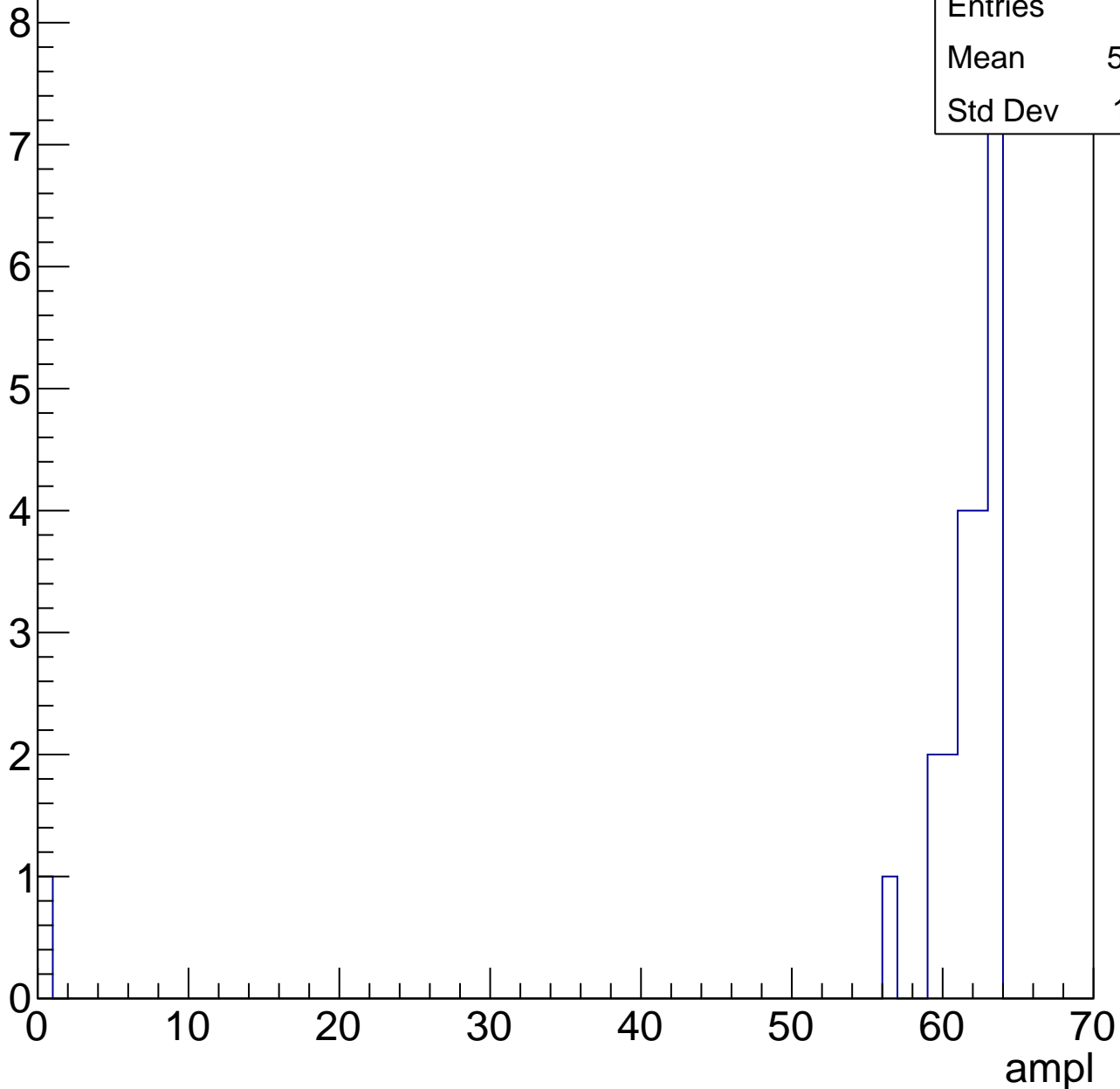


B1L102S, U14-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	58.64
Std Dev	12.91



B1L102S, U14-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch95, adc0

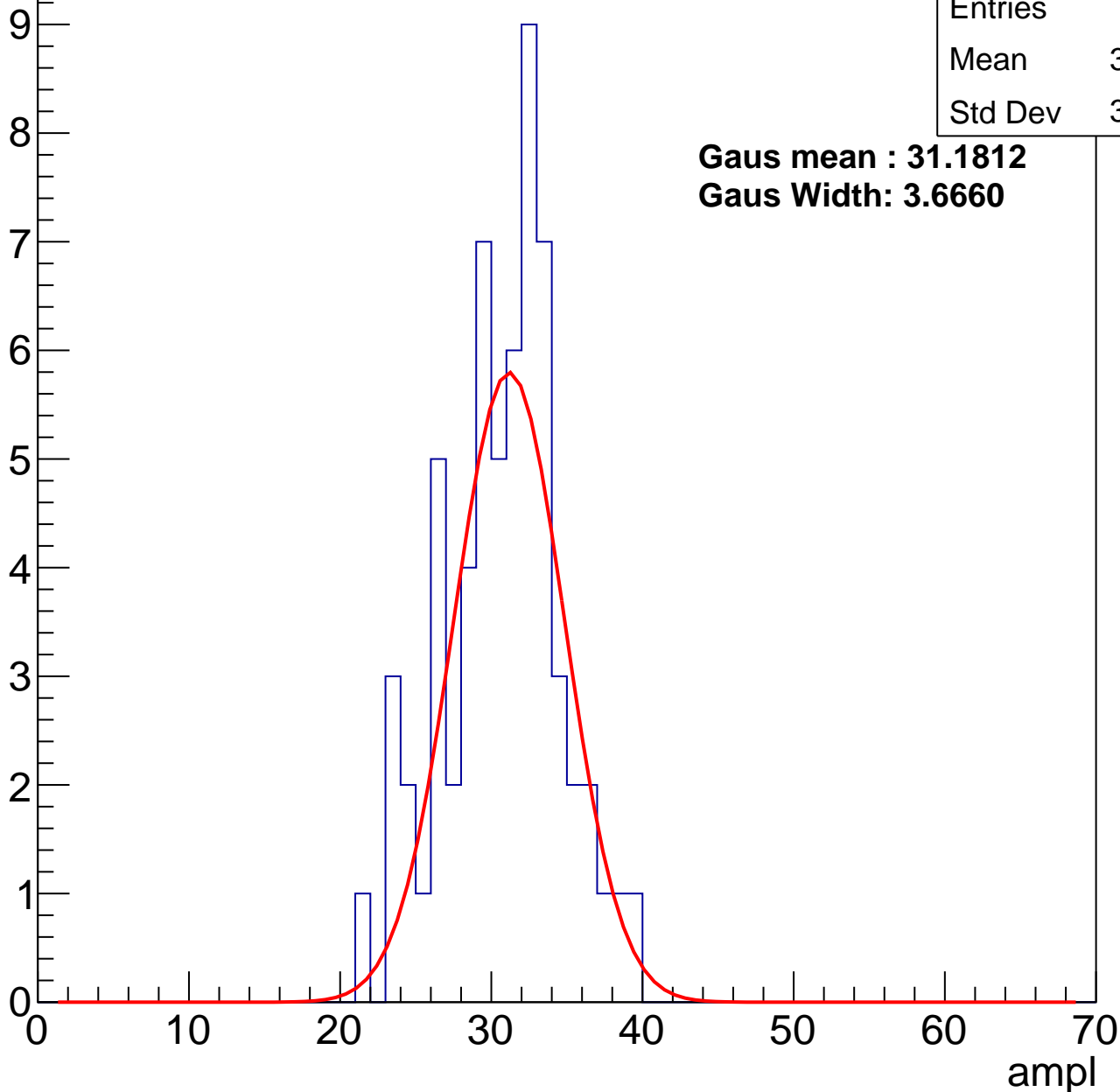
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	30.24
Std Dev	3.834

Gaus mean : 31.1812

Gaus Width: 3.6660



B1L102S, U14-ch95, adc1

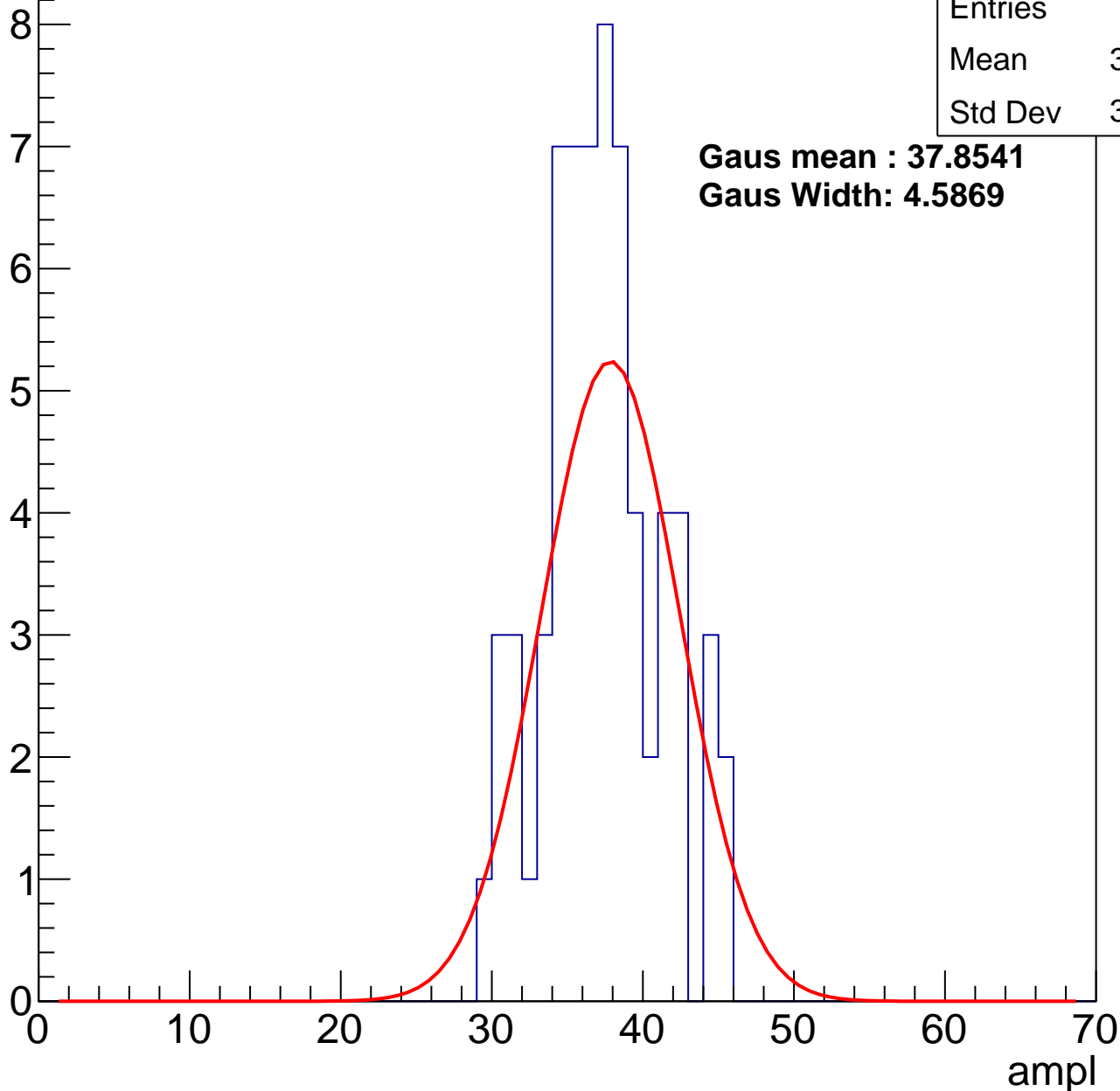
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.82
Std Dev	3.829

Gaus mean : 37.8541

Gaus Width: 4.5869



B1L102S, U14-ch95, adc2

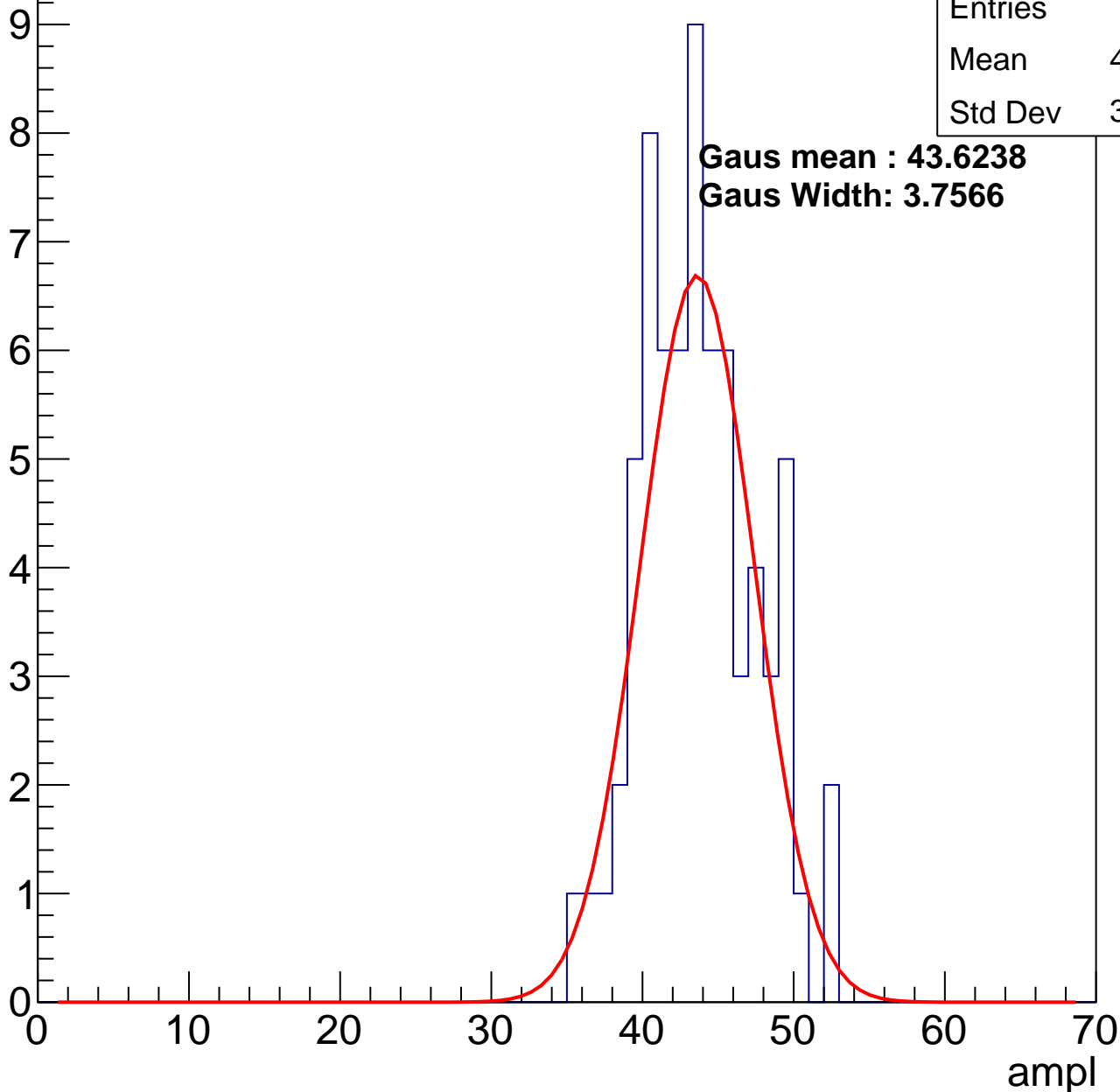
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.29
Std Dev	3.734

Gaus mean : 43.6238

Gaus Width: 3.7566

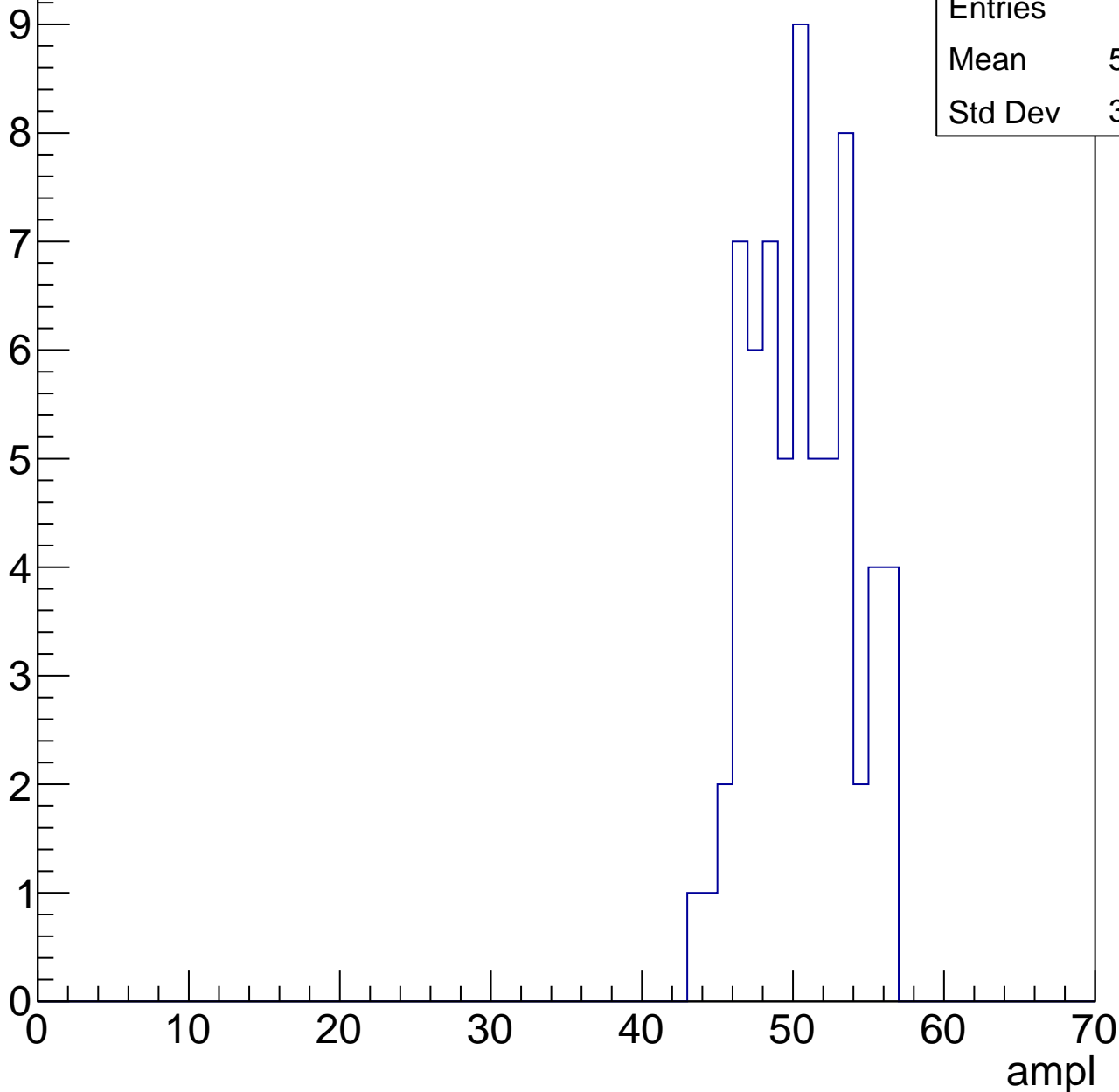


B1L102S, U14-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.05
Std Dev	3.273

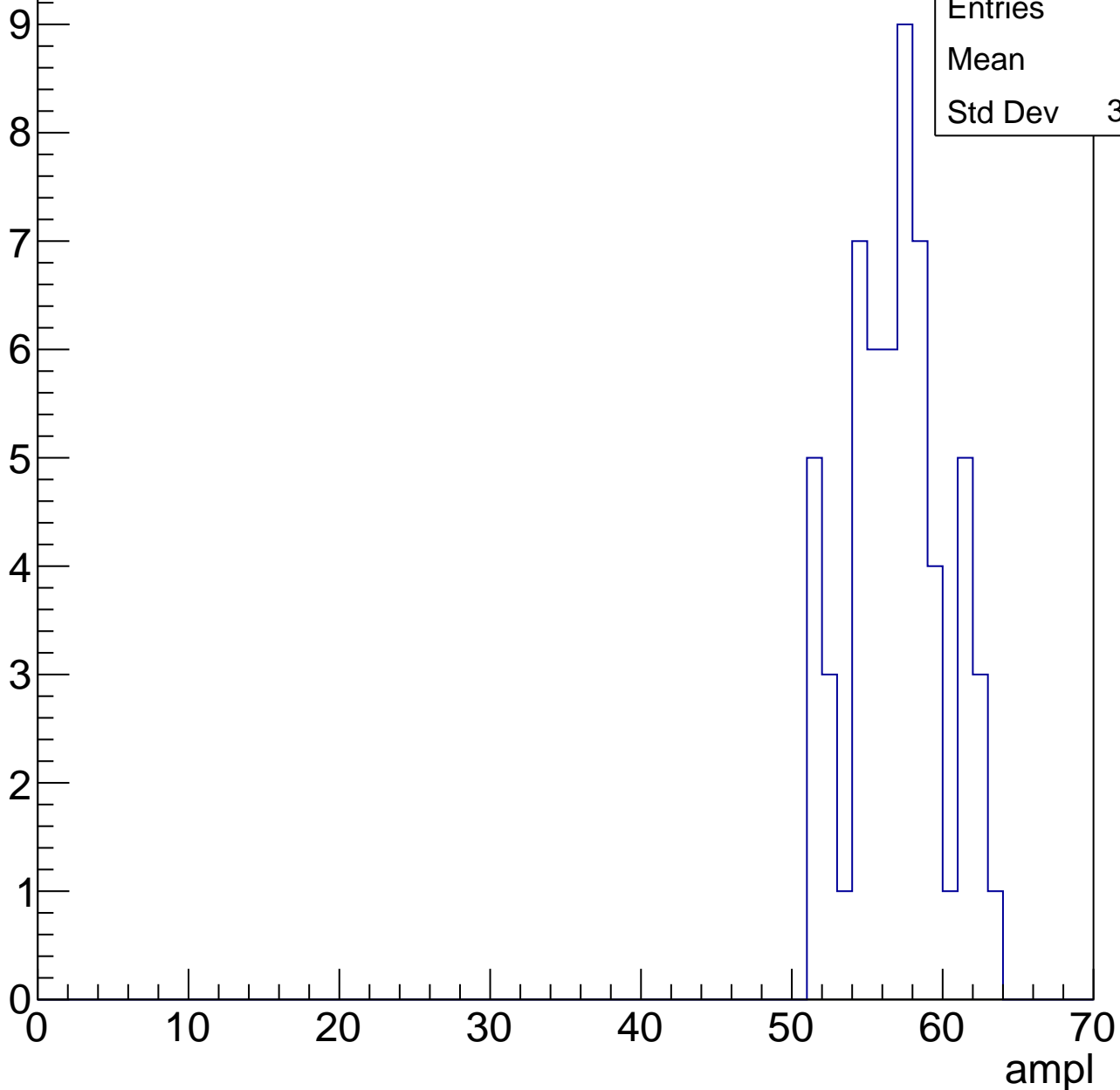


B1L102S, U14-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.5
Std Dev	3.142

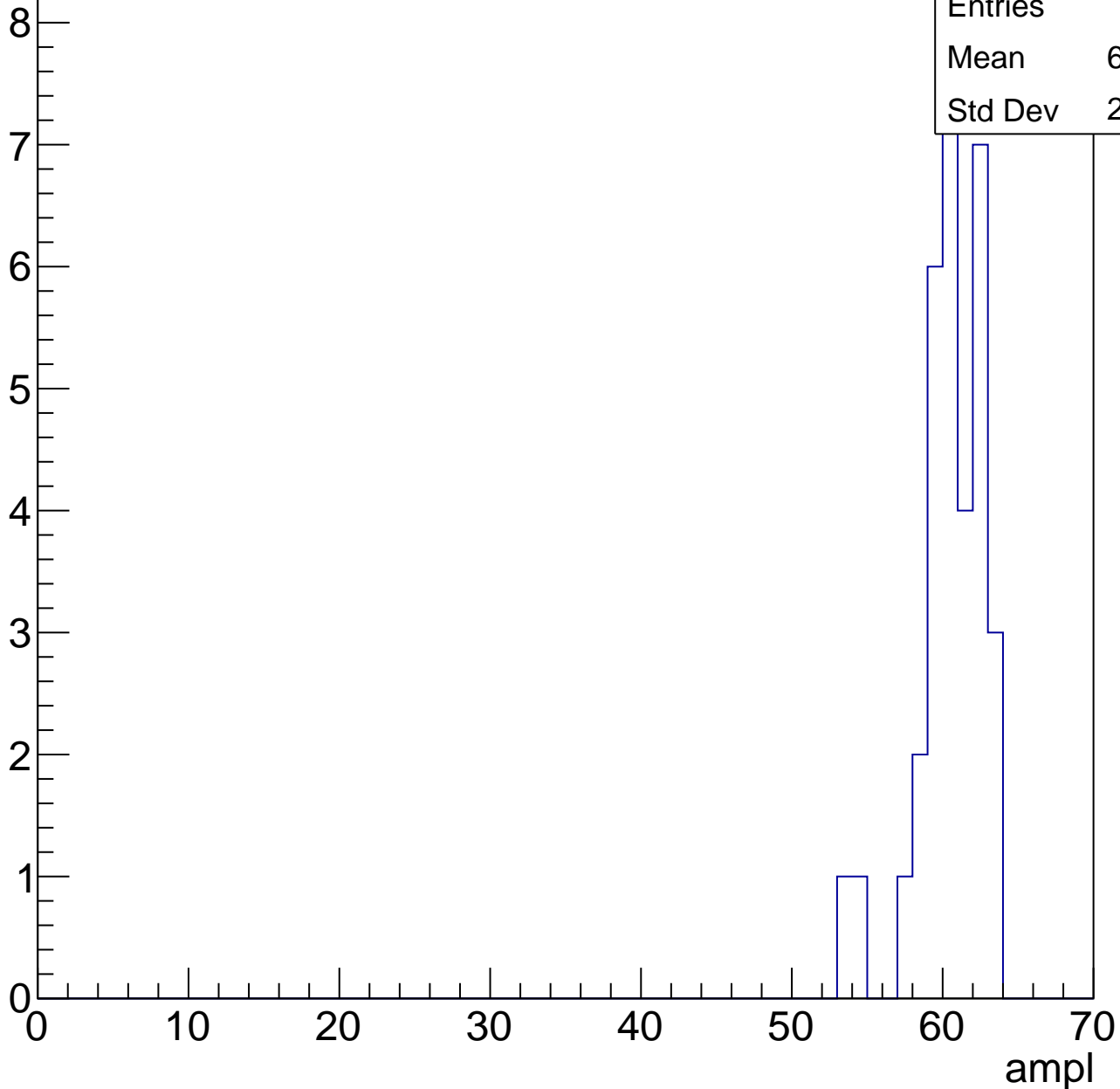


B1L102S, U14-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	60.03
Std Dev	2.249

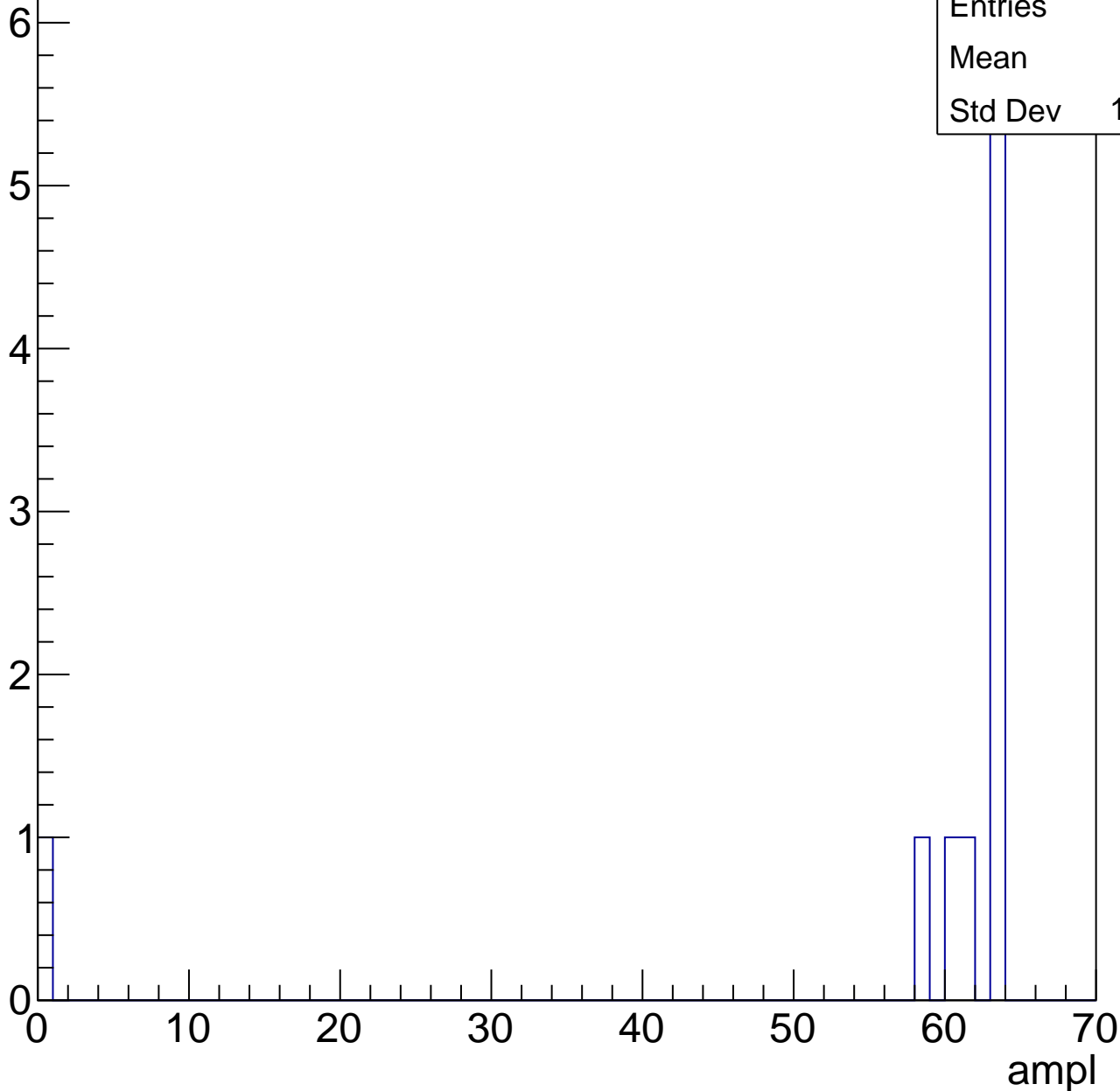


B1L102S, U14-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

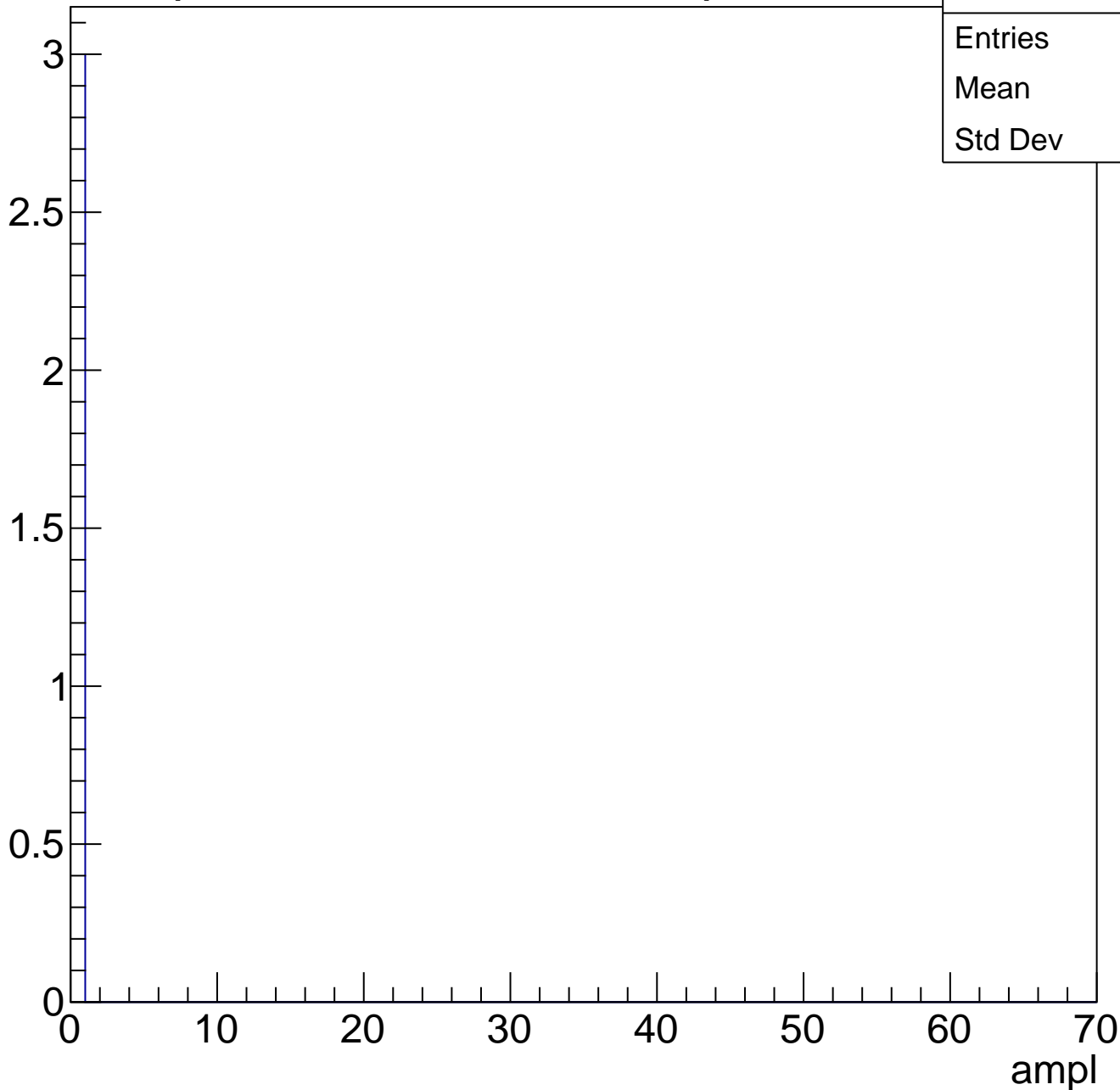
Entries	10
Mean	55.7
Std Dev	18.64



B1L102S, U14-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U14-ch96, adc0

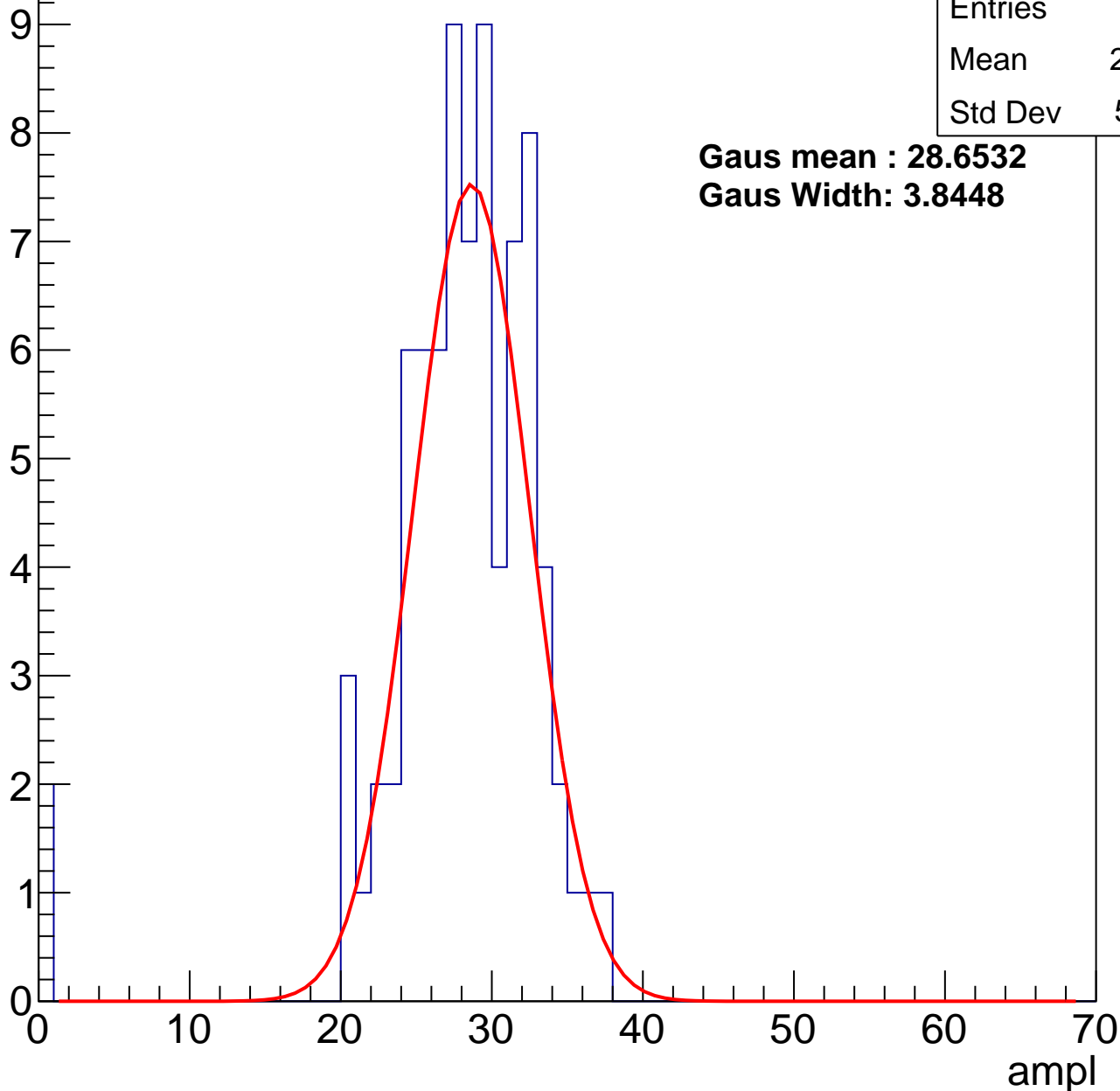
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.43
Std Dev	5.741

Gaus mean : 28.6532

Gaus Width: 3.8448



B1L102S, U14-ch96, adc1

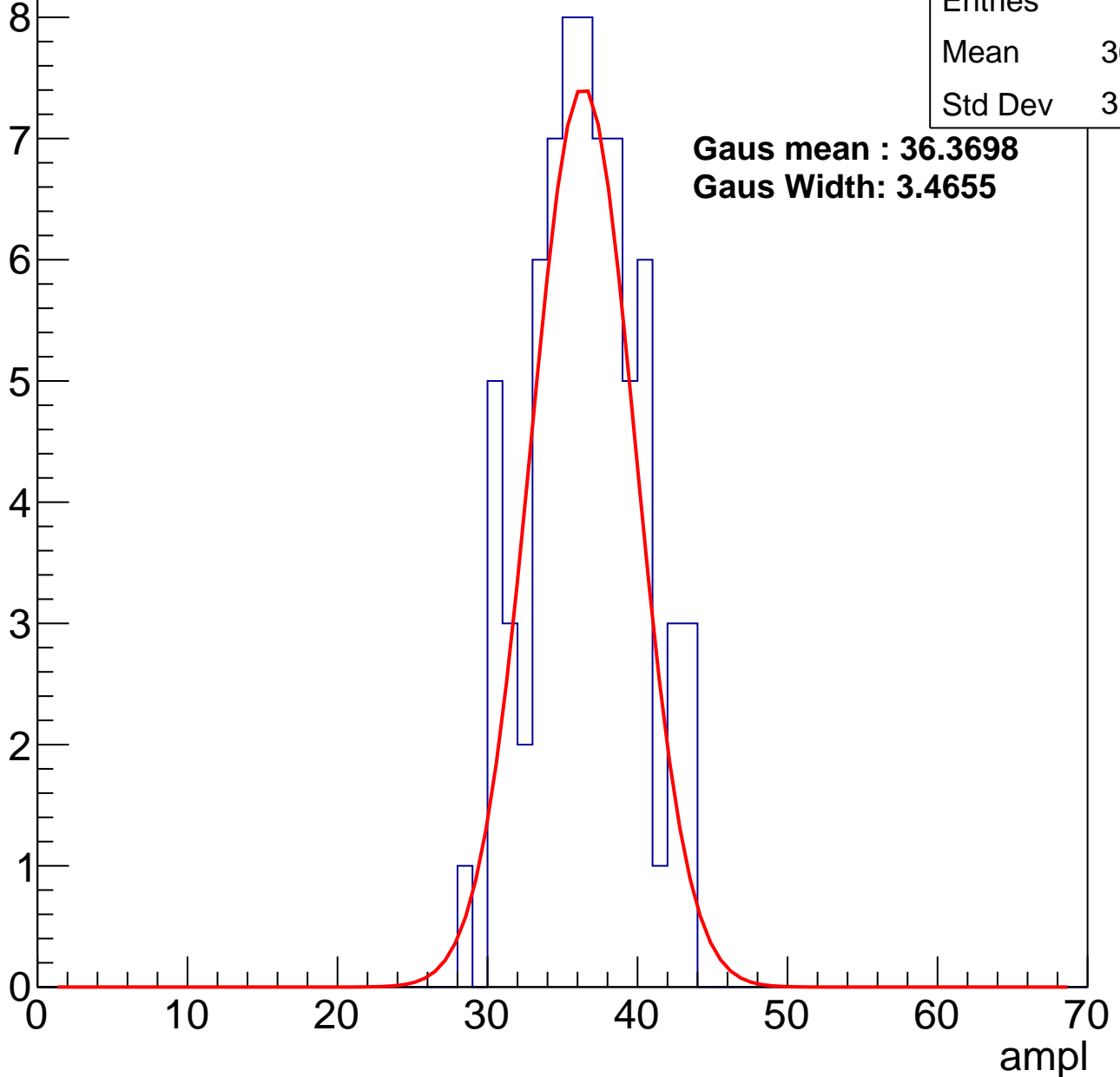
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.04
Std Dev	3.529

Gaus mean : 36.3698

Gaus Width: 3.4655



B1L102S, U14-ch96, adc2

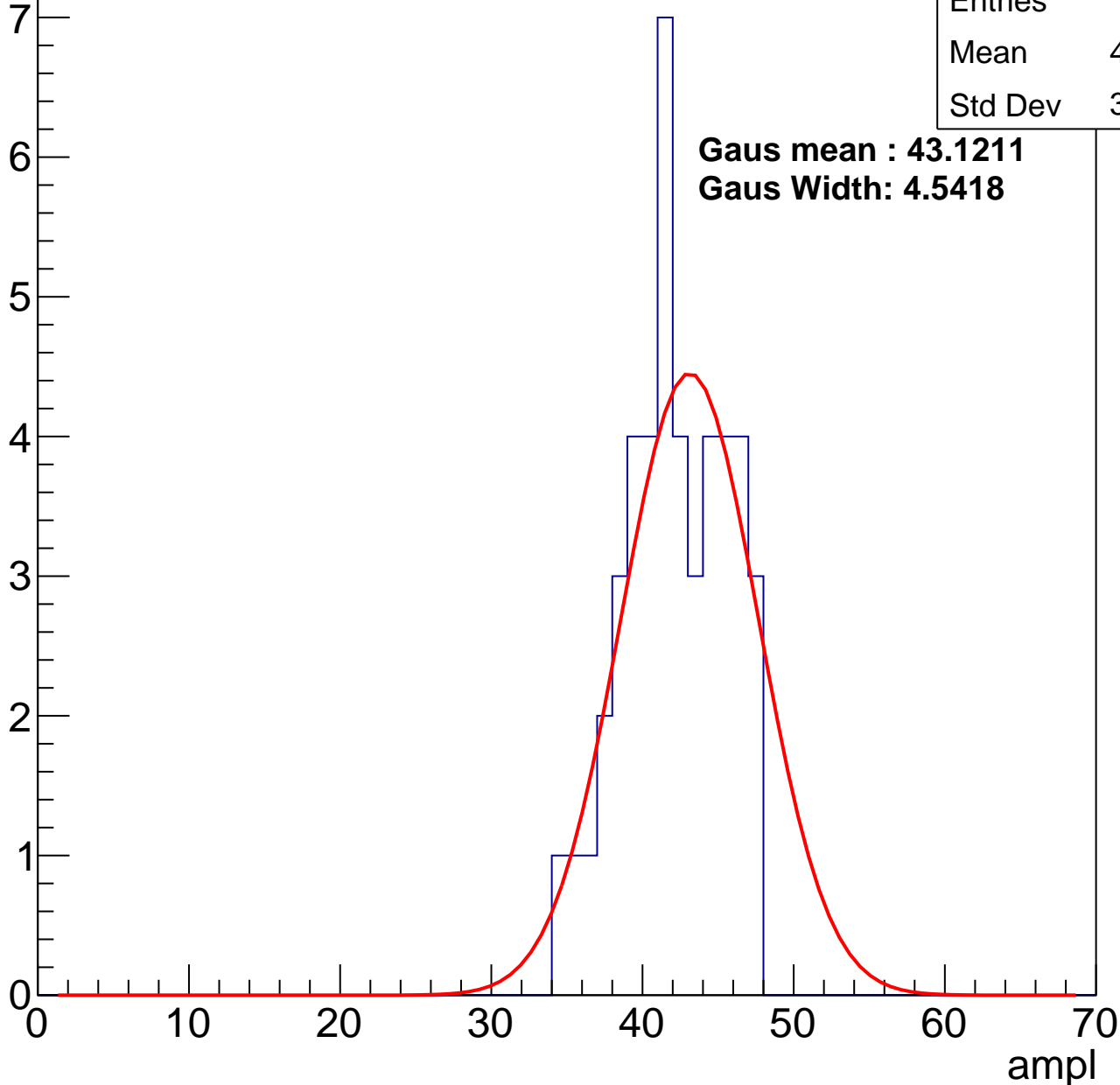
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	41.64
Std Dev	3.314

Gaus mean : 43.1211

Gaus Width: 4.5418

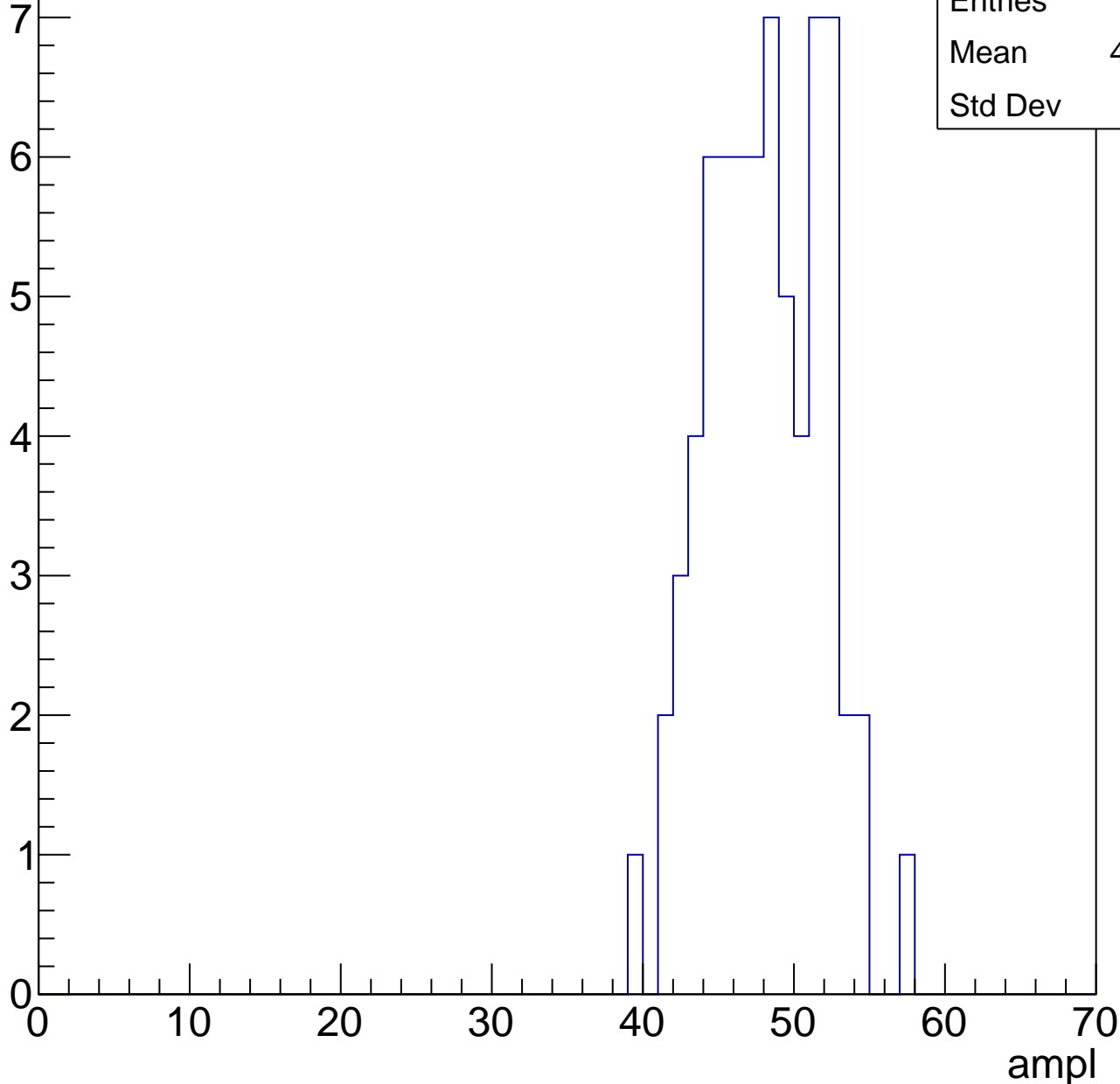


B1L102S, U14-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.59
Std Dev	3.72

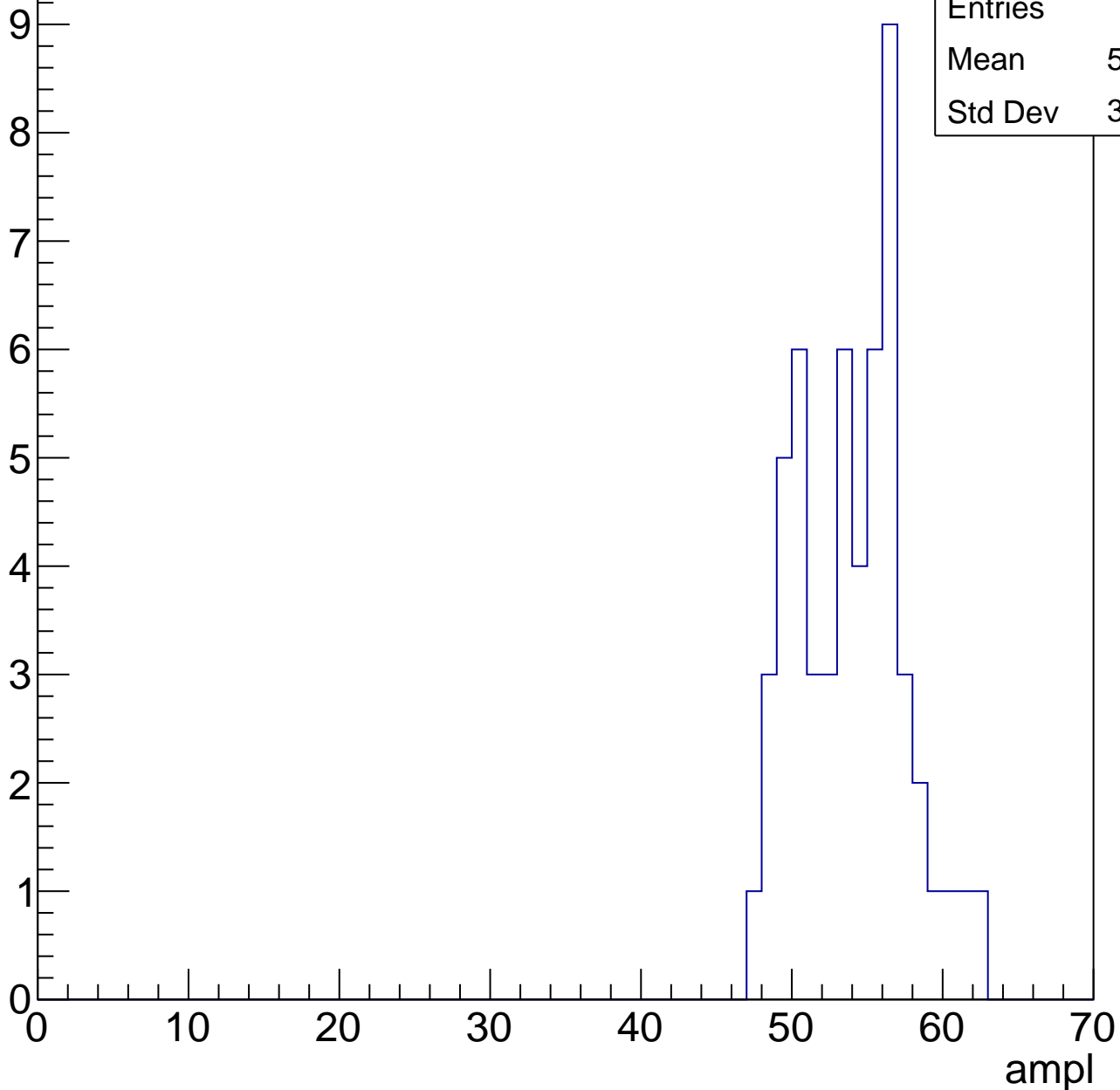


B1L102S, U14-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

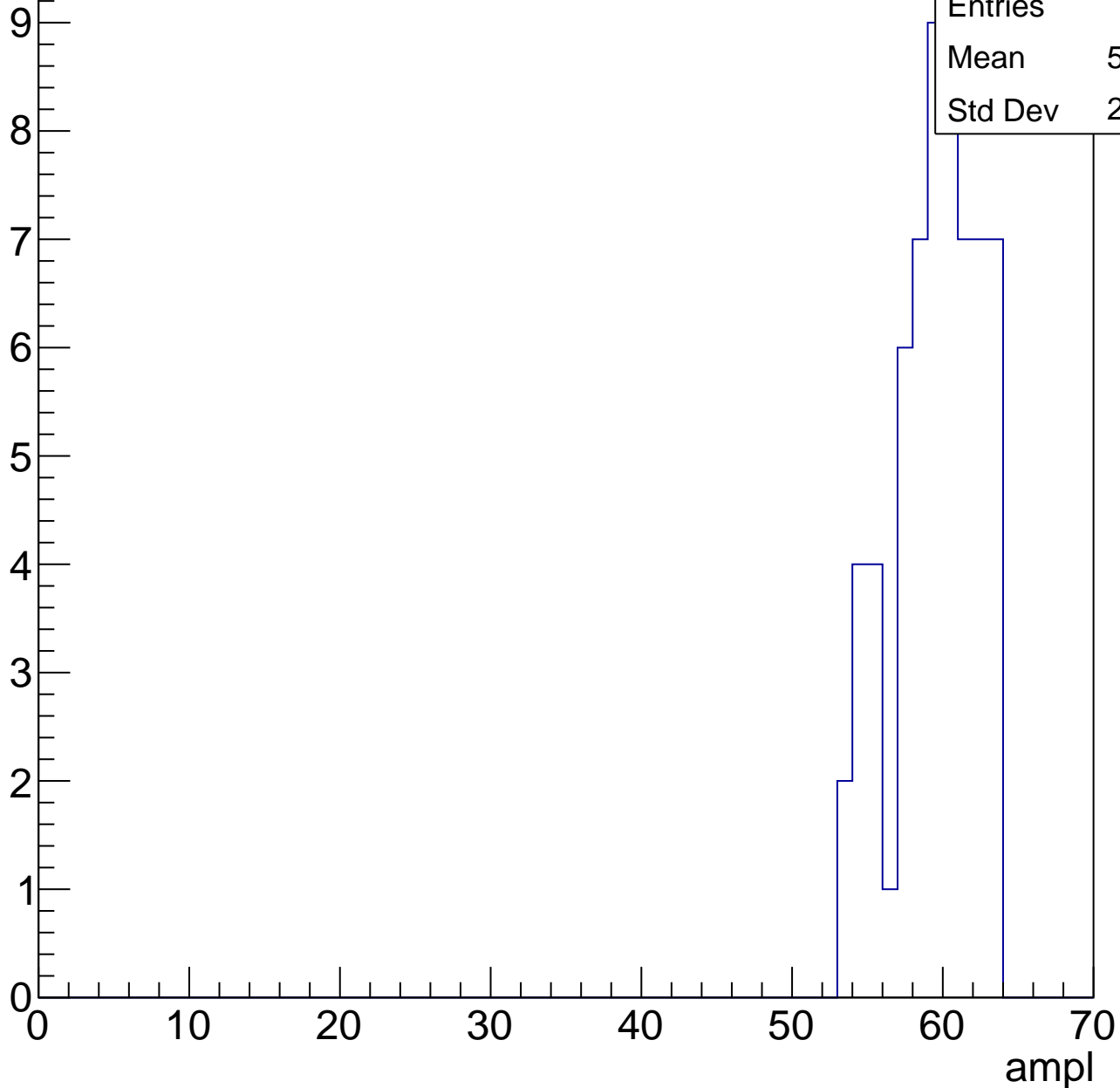
Entries	55
Mean	53.49
Std Dev	3.526



B1L102S, U14-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

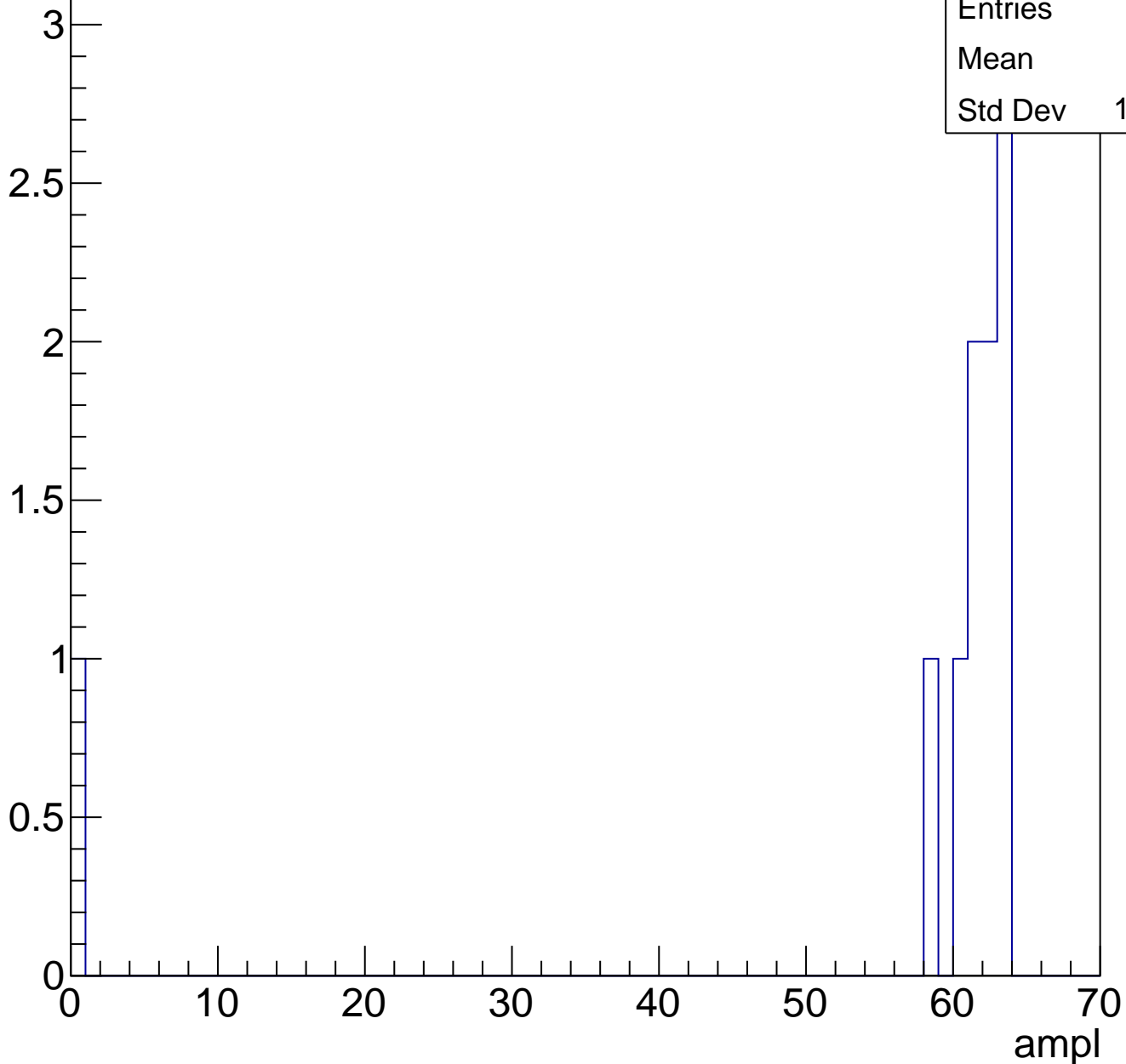


Entries	62
Mean	59.02
Std Dev	2.803

B1L102S, U14-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

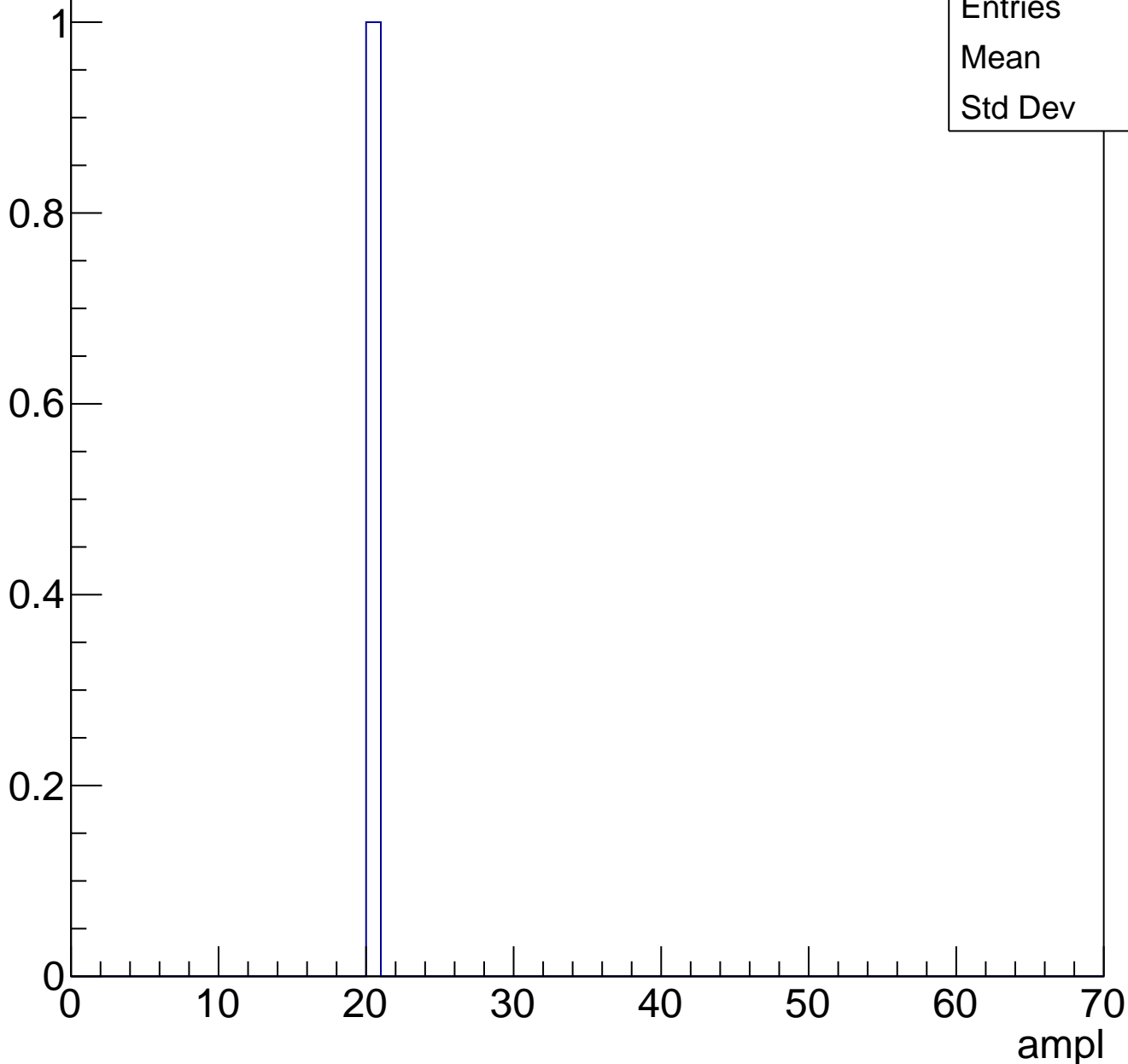
Entry



B1L102S, U14-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch97, adc0

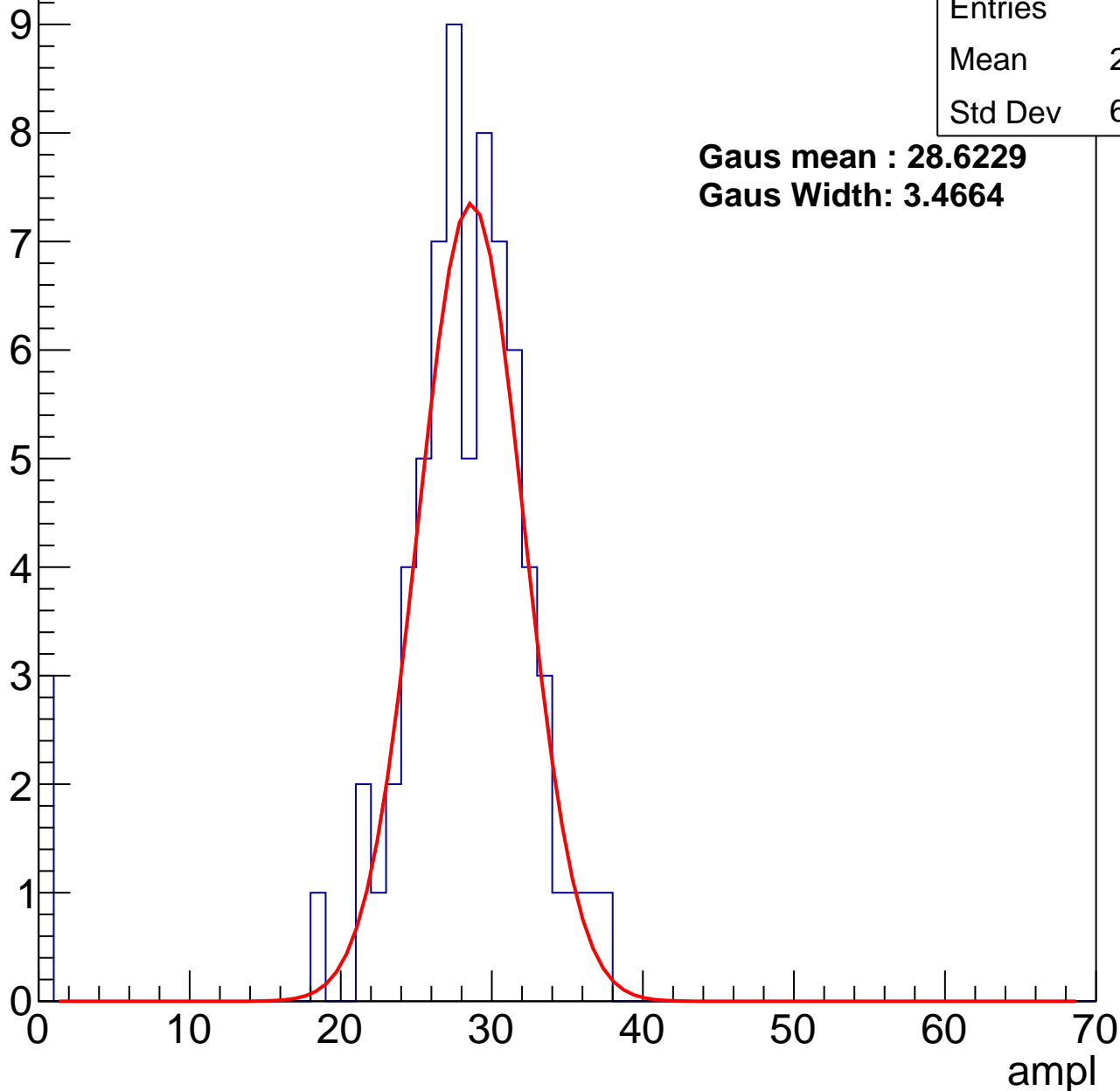
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	26.92
Std Dev	6.669

Gaus mean : 28.6229

Gaus Width: 3.4664



B1L102S, U14-ch97, adc1

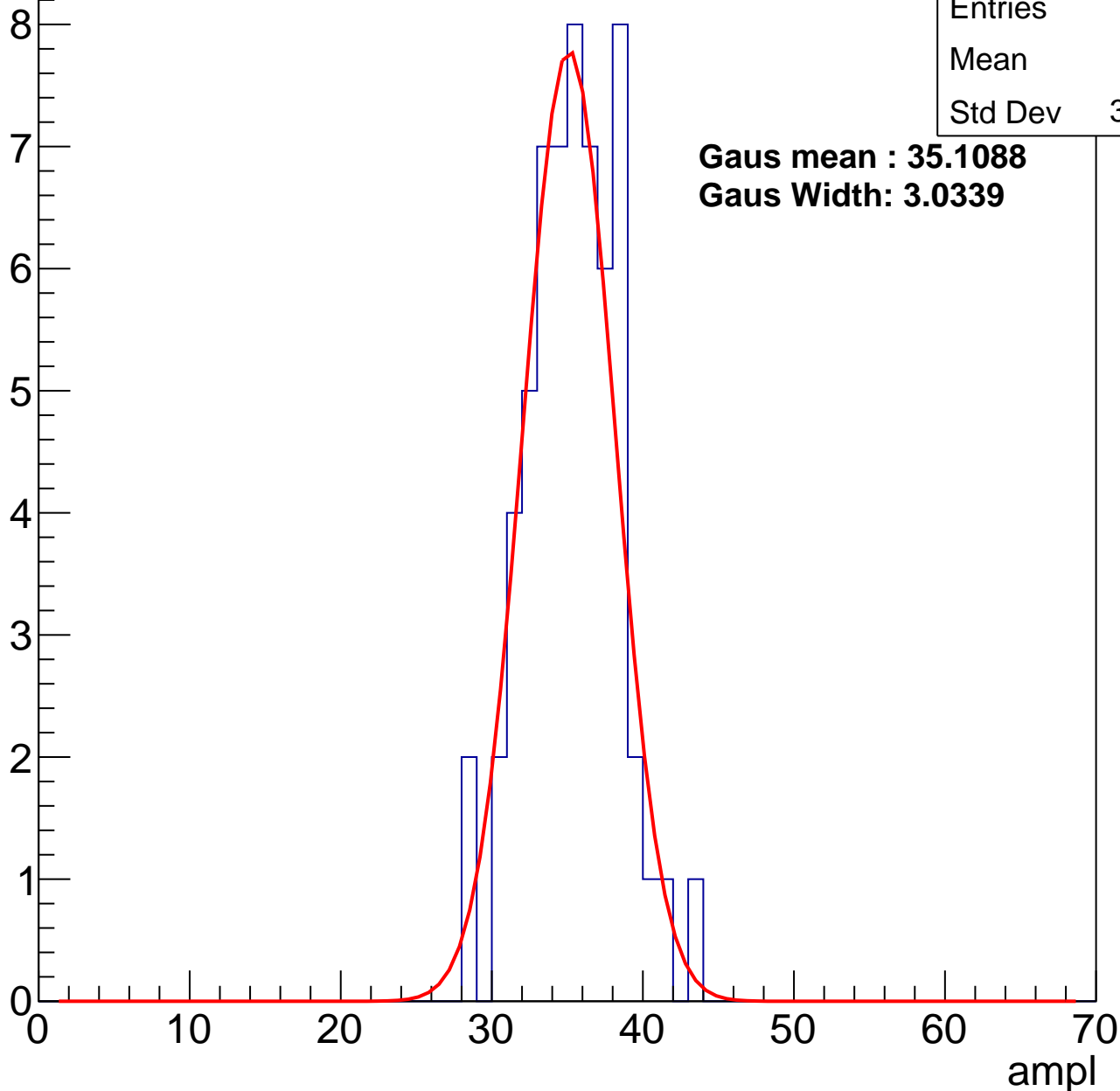
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.9
Std Dev	3.007

Gaus mean : 35.1088

Gaus Width: 3.0339



B1L102S, U14-ch97, adc2

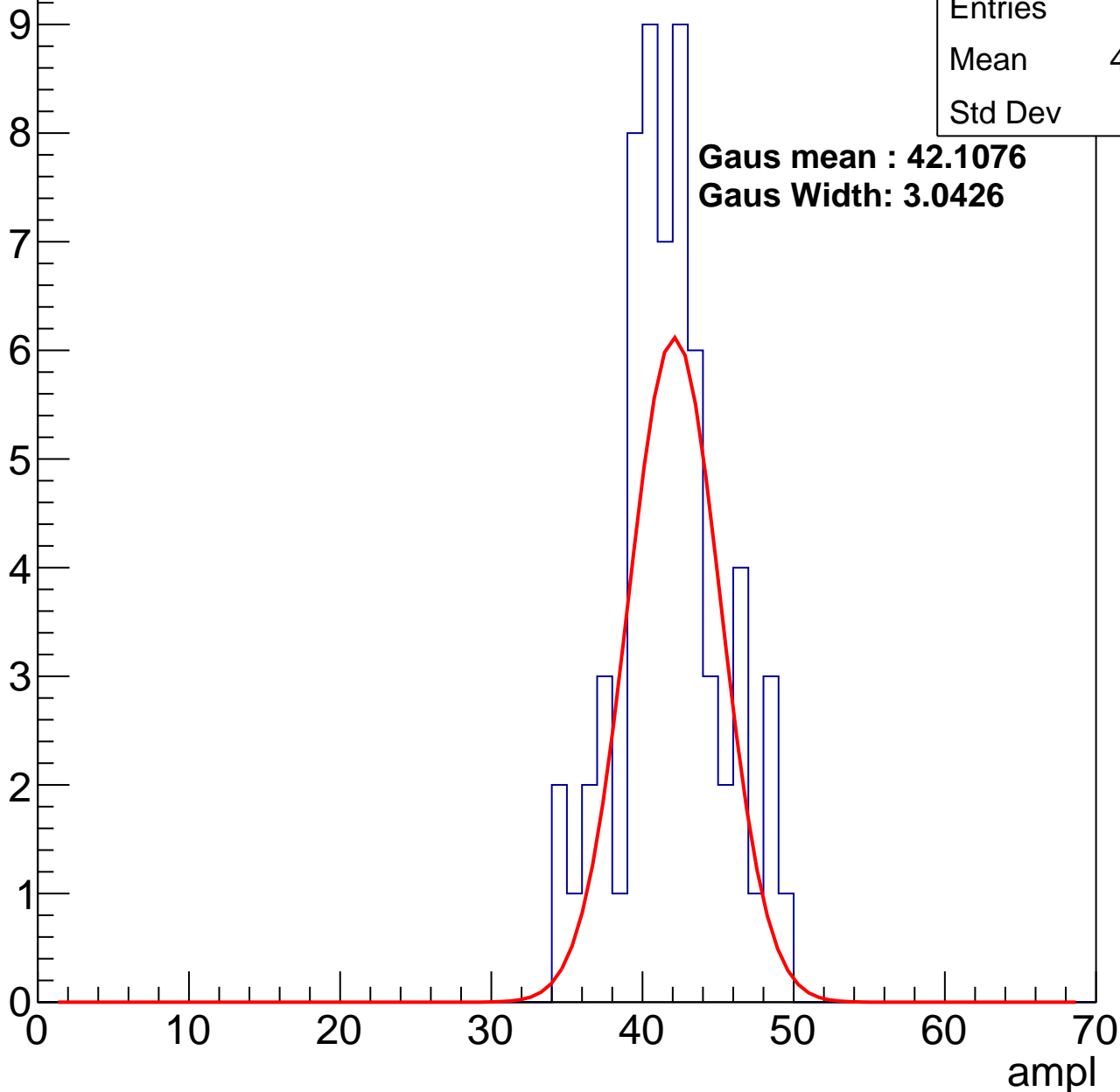
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.37
Std Dev	3.39

Gaus mean : 42.1076

Gaus Width: 3.0426

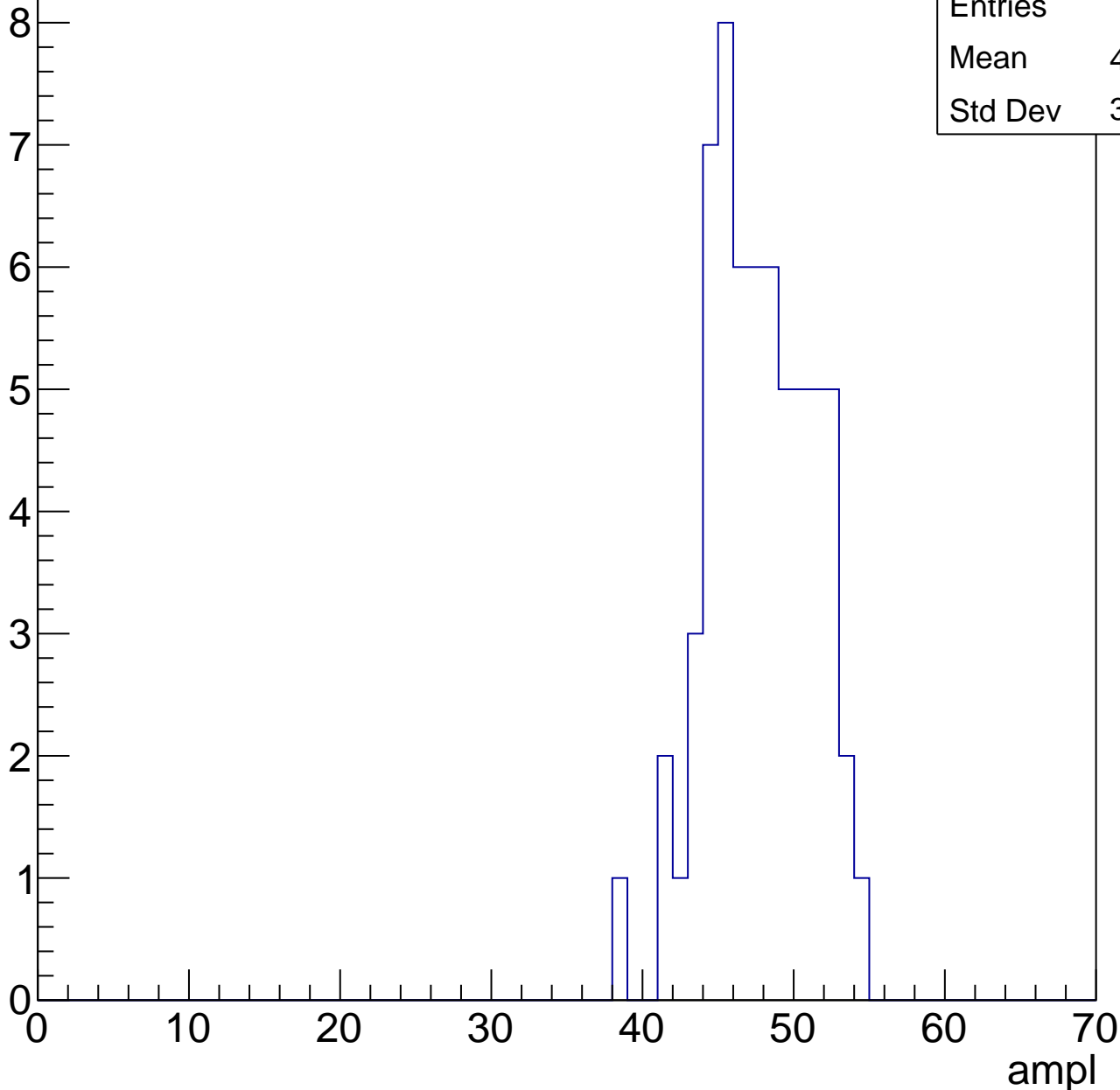


B1L102S, U14-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.22
Std Dev	3.383

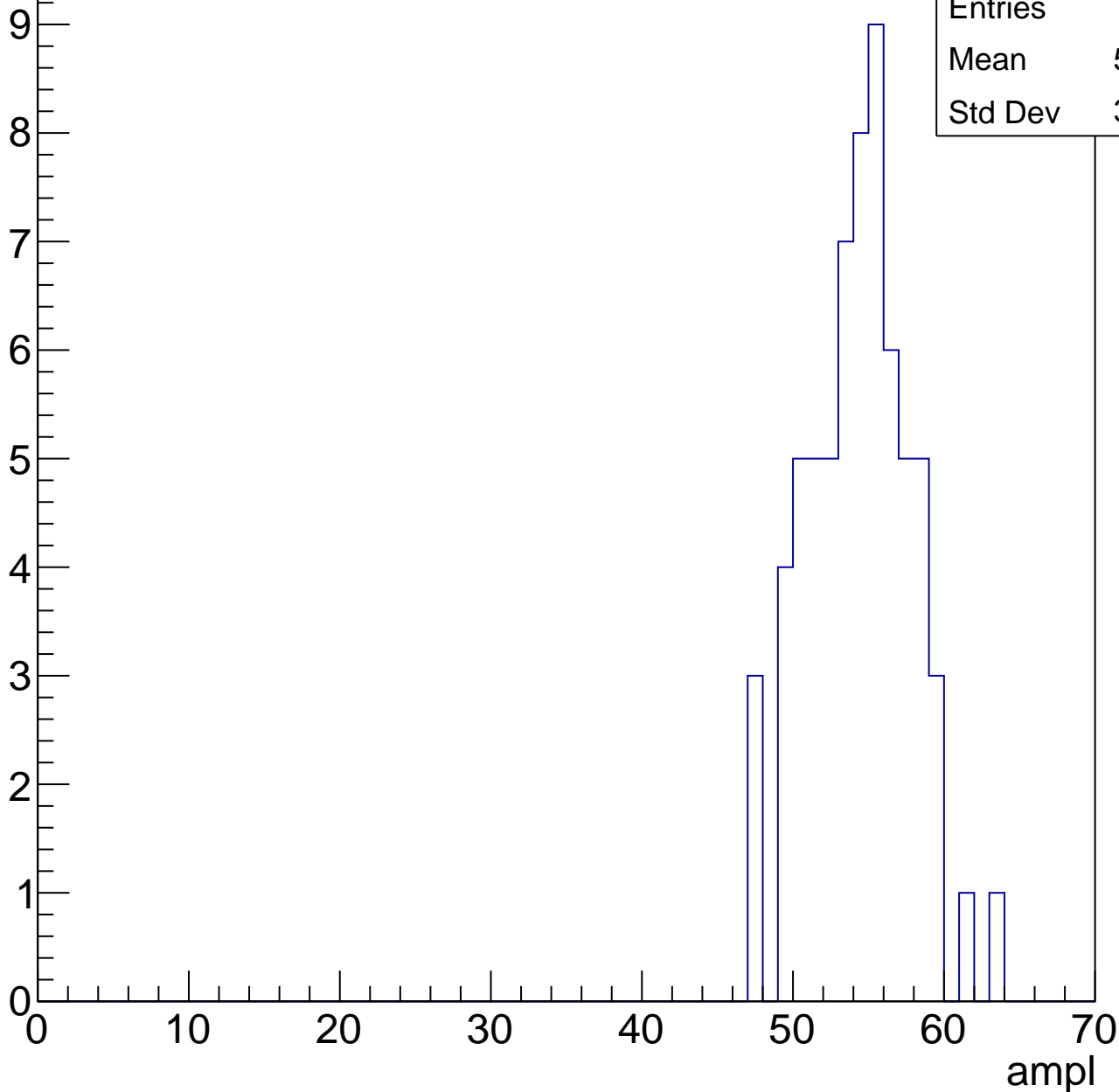


B1L102S, U14-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	53.91
Std Dev	3.371



B1L102S, U14-ch97, adc5

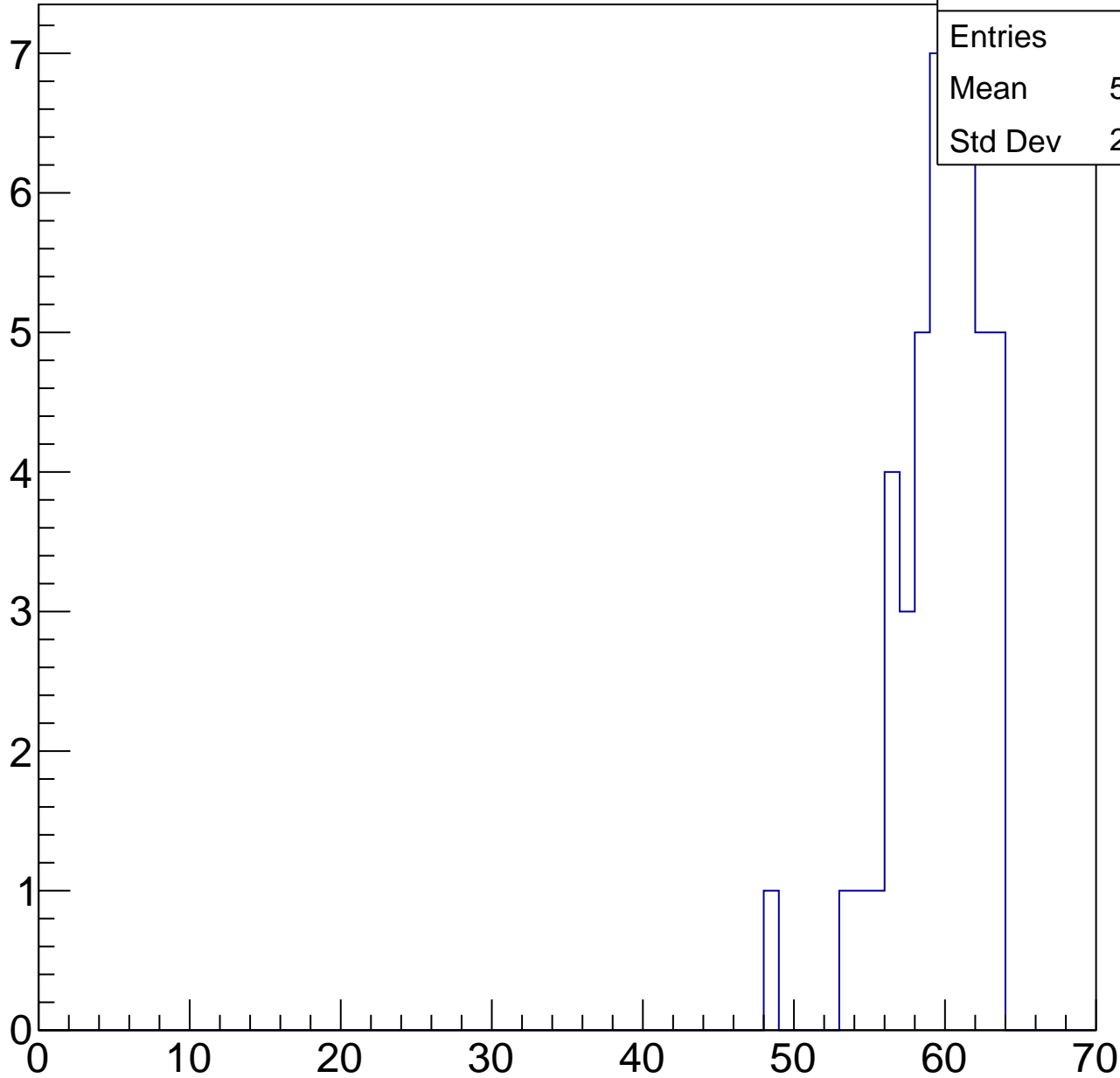
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	47
Mean	59.15
Std Dev	2.953

ampl

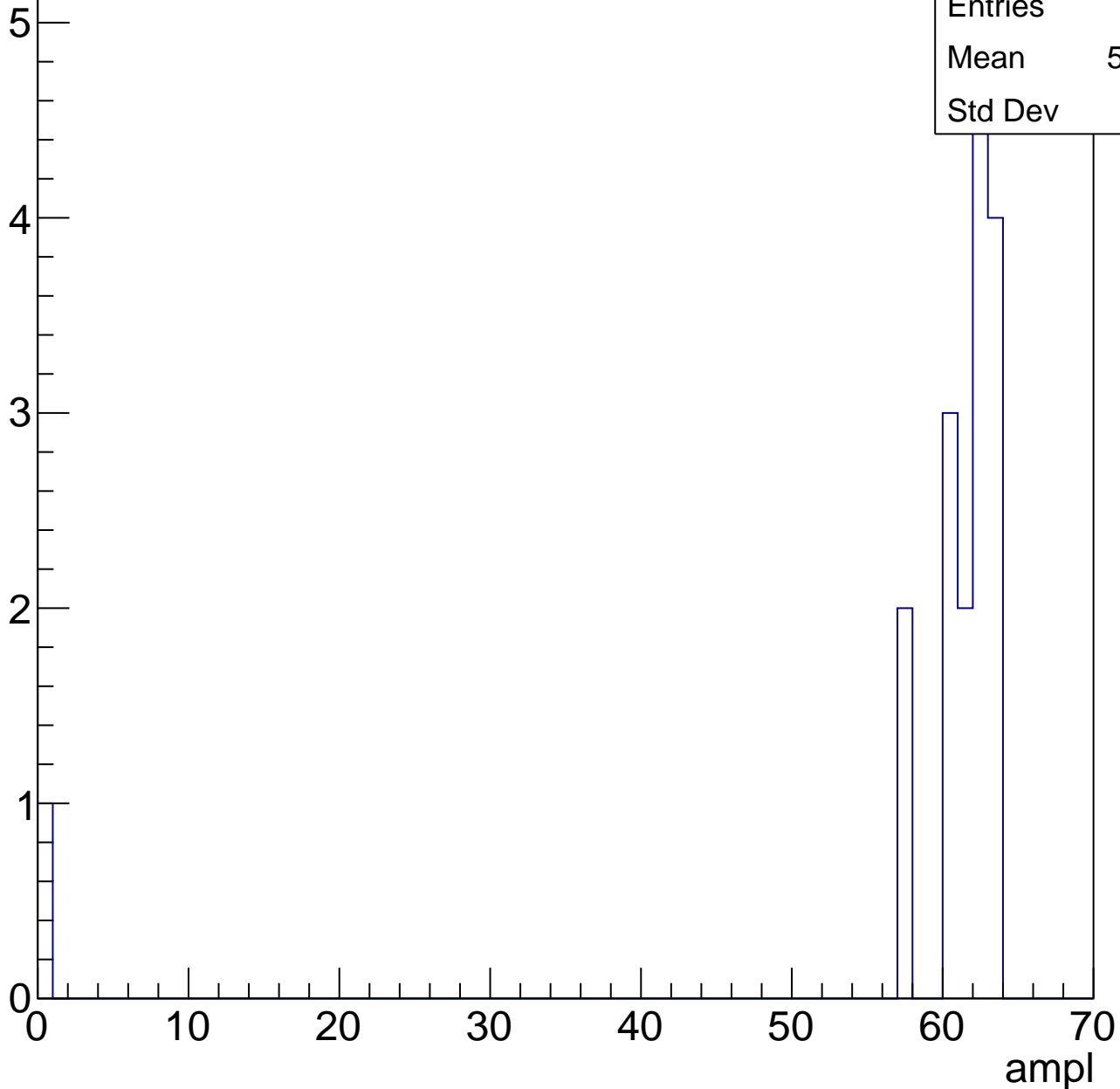


B1L102S, U14-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.53
Std Dev	14.5



B1L102S, U14-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch98, adc0

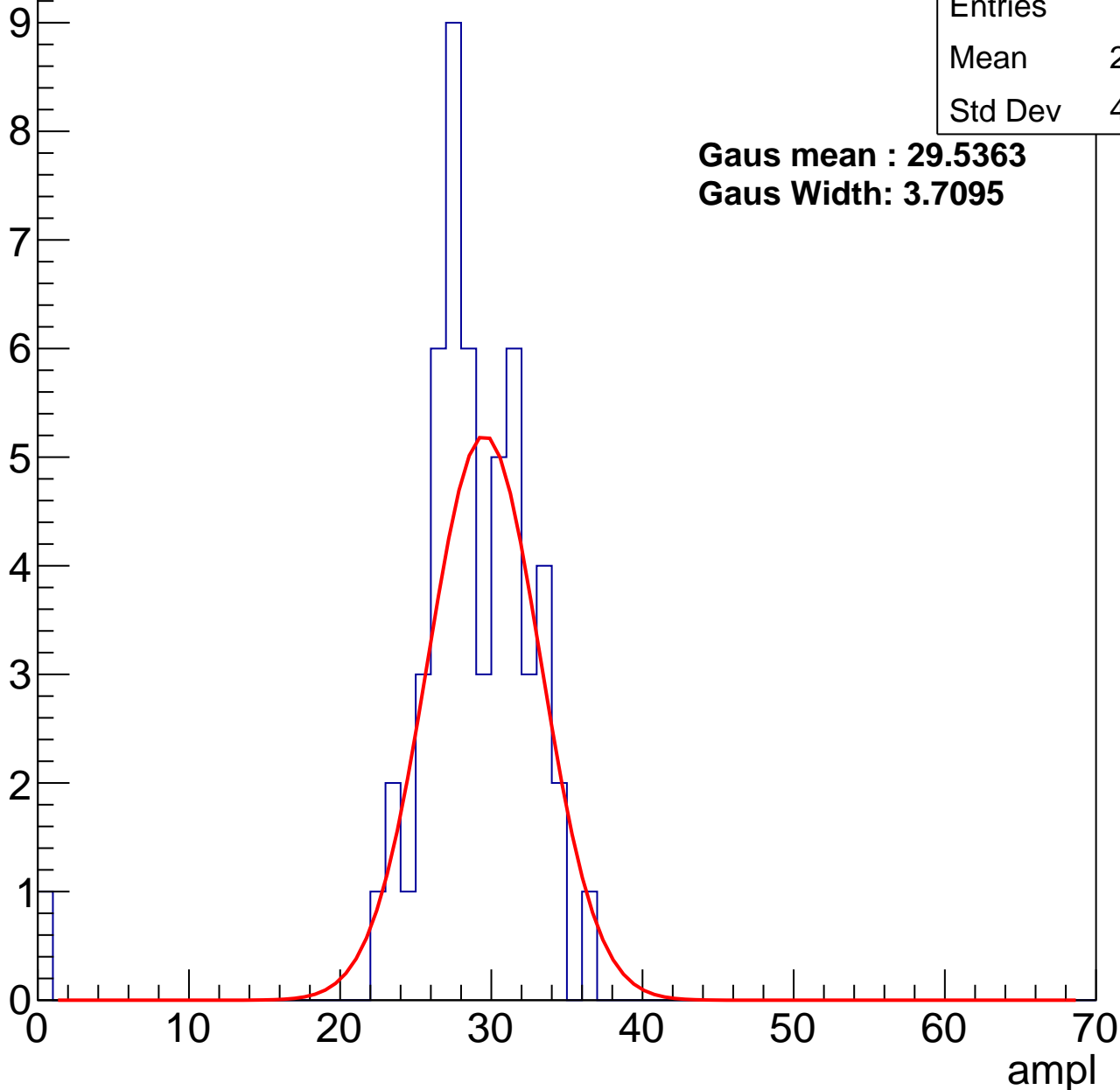
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	28.09
Std Dev	4.965

Gaus mean : 29.5363

Gaus Width: 3.7095



B1L102S, U14-ch98, adc1

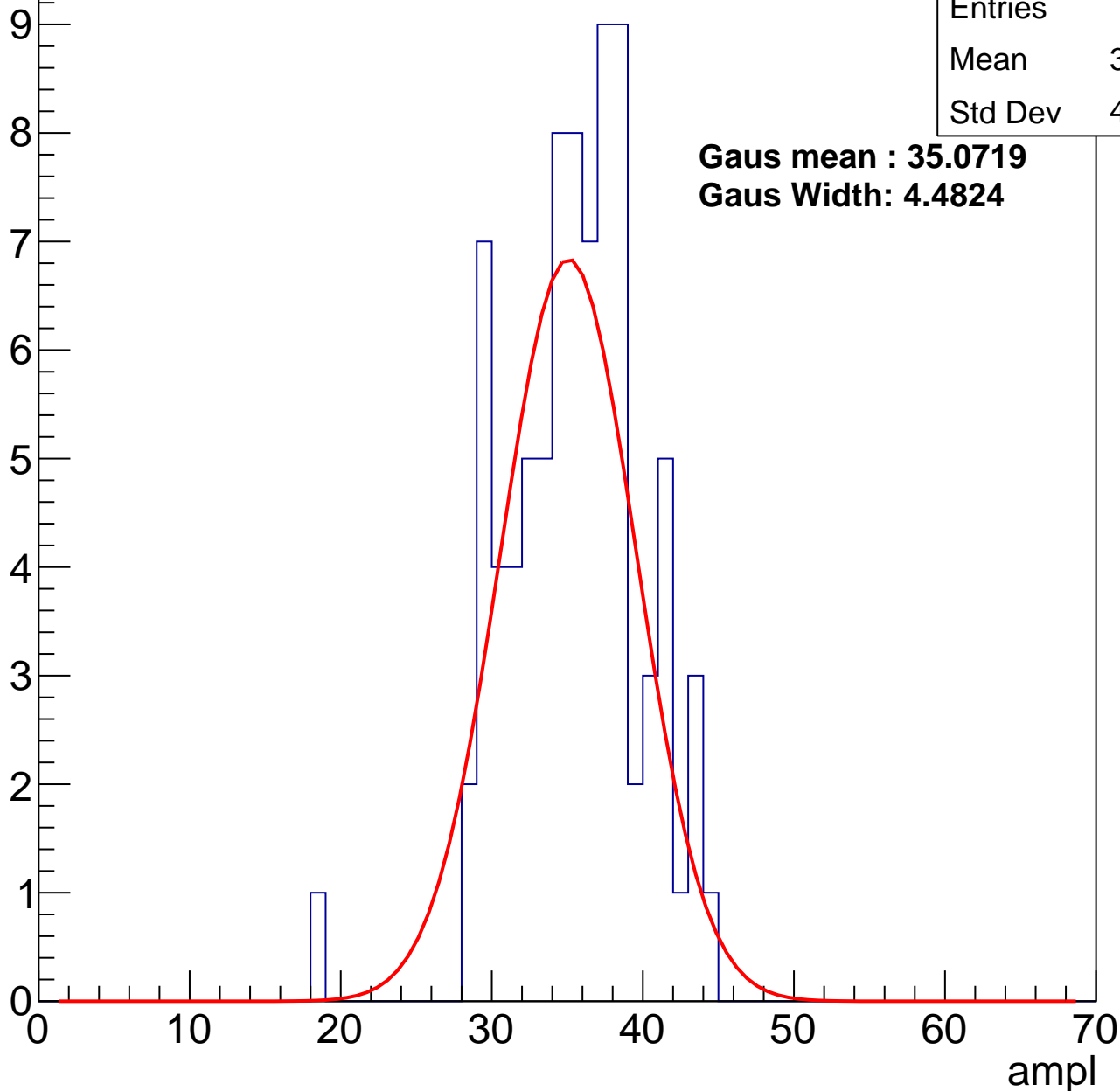
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	35.04
Std Dev	4.376

Gaus mean : 35.0719

Gaus Width: 4.4824



B1L102S, U14-ch98, adc2

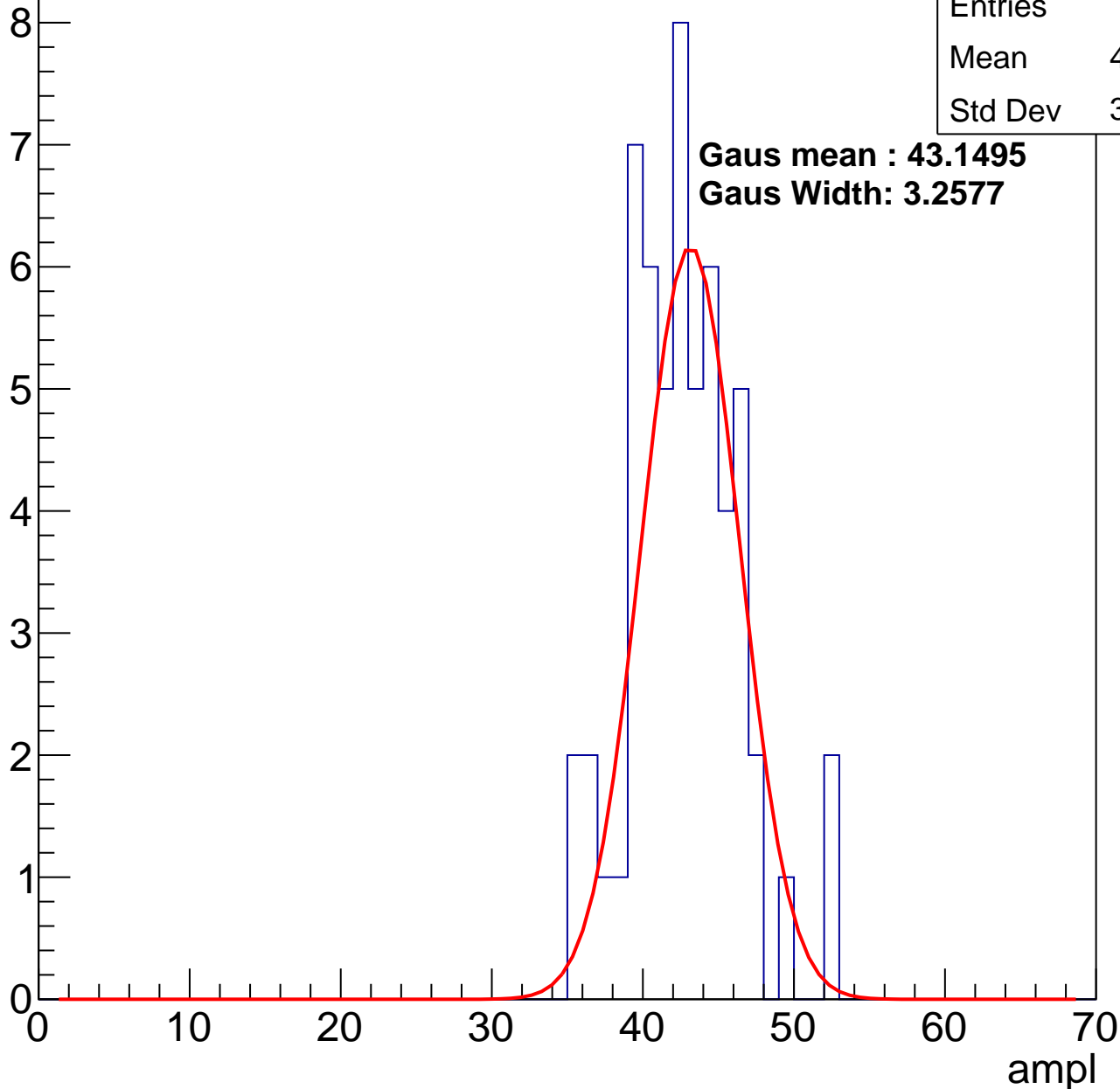
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.23
Std Dev	3.613

Gaus mean : 43.1495

Gaus Width: 3.2577

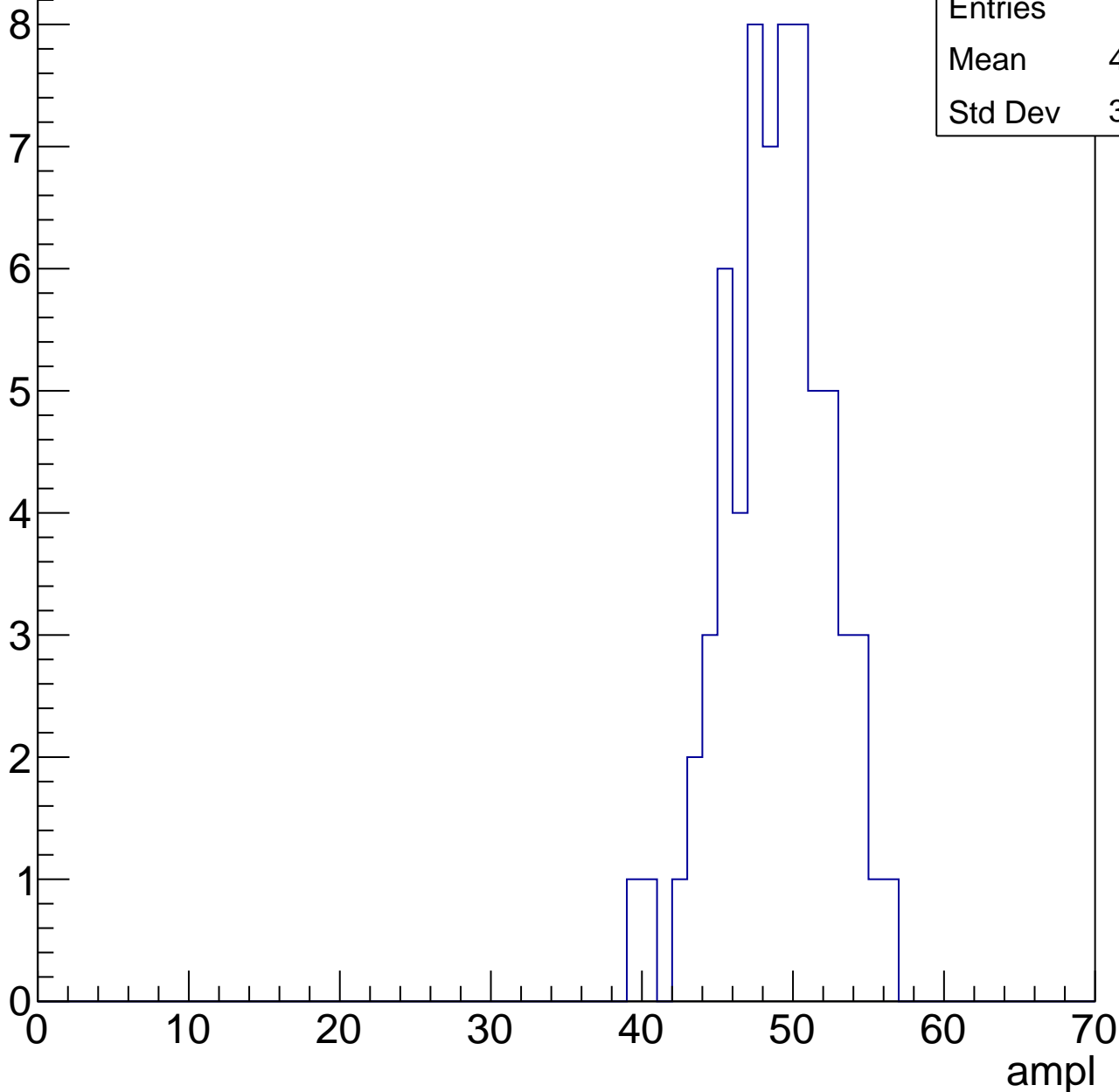


B1L102S, U14-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.42
Std Dev	3.469

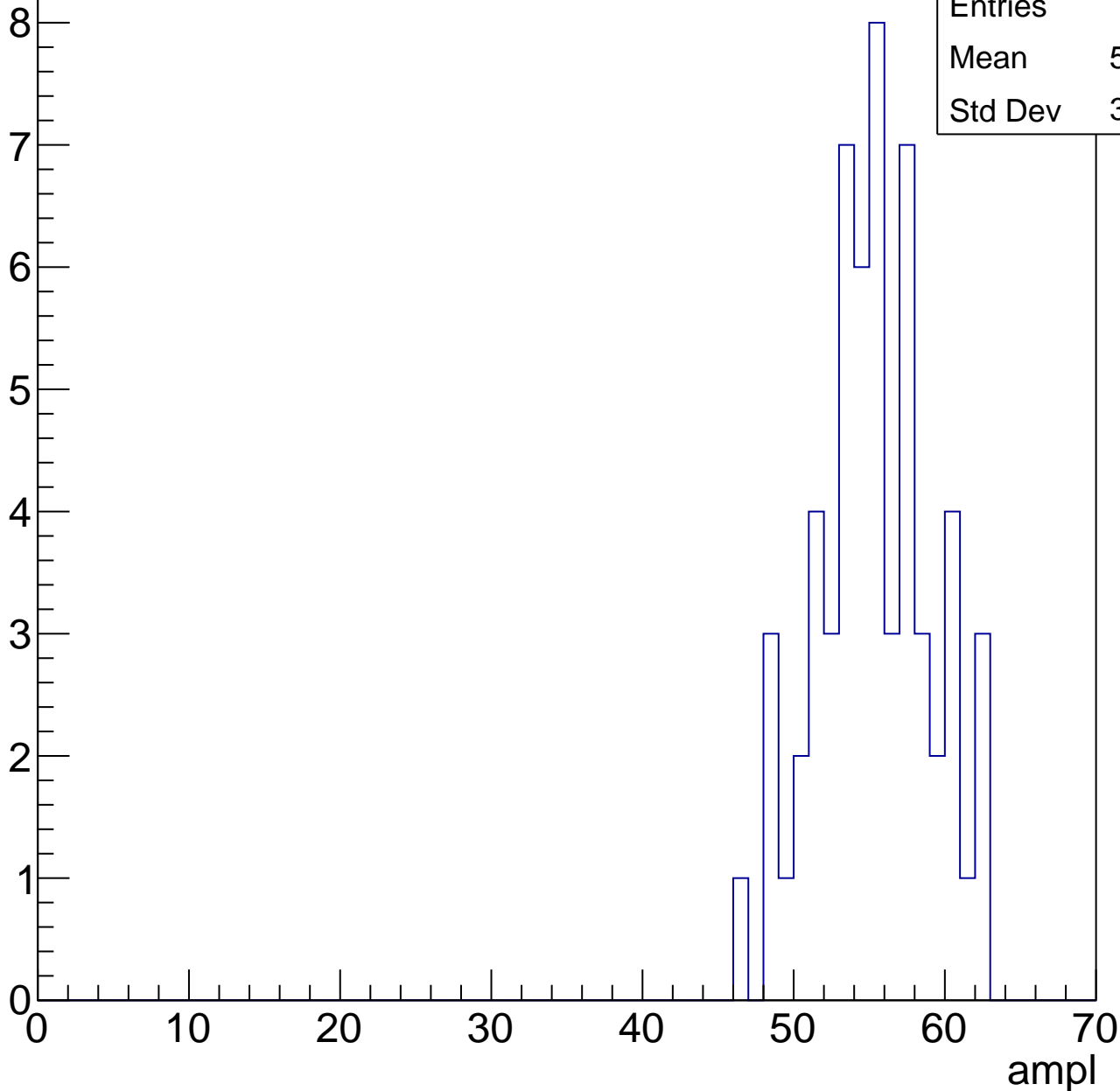


B1L102S, U14-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.83
Std Dev	3.738

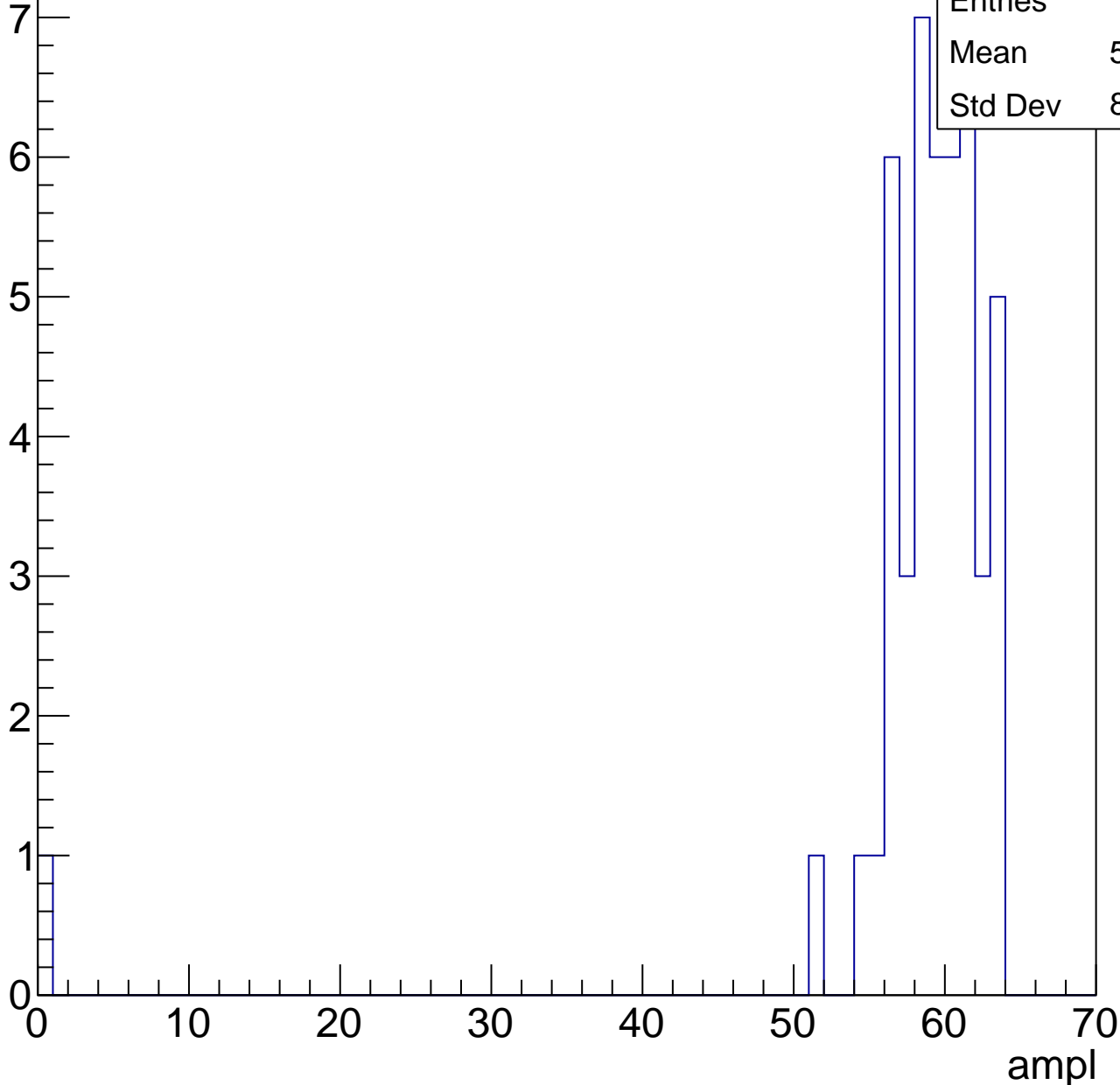


B1L102S, U14-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

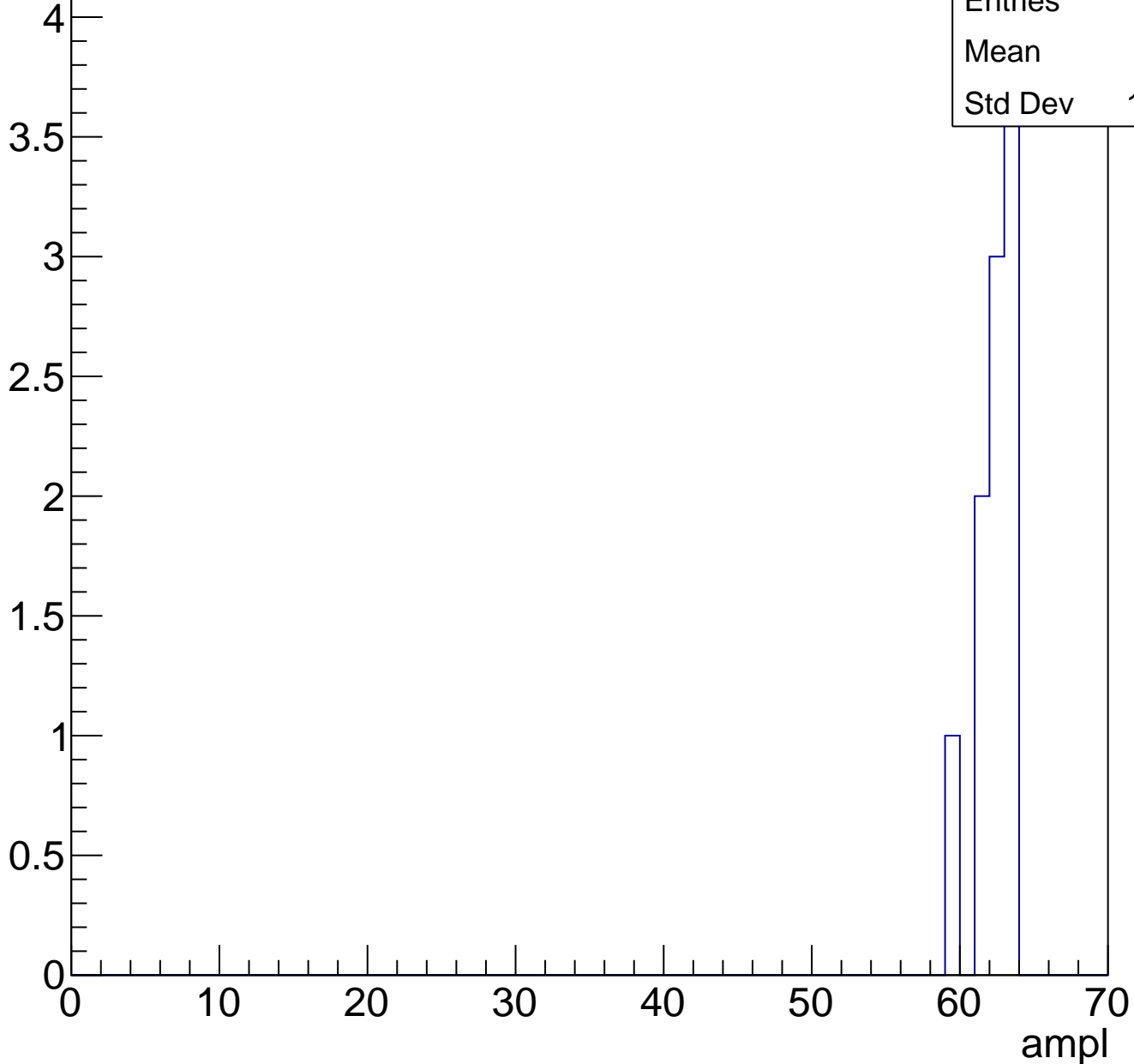
Entries	47
Mean	57.77
Std Dev	8.907



B1L102S, U14-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

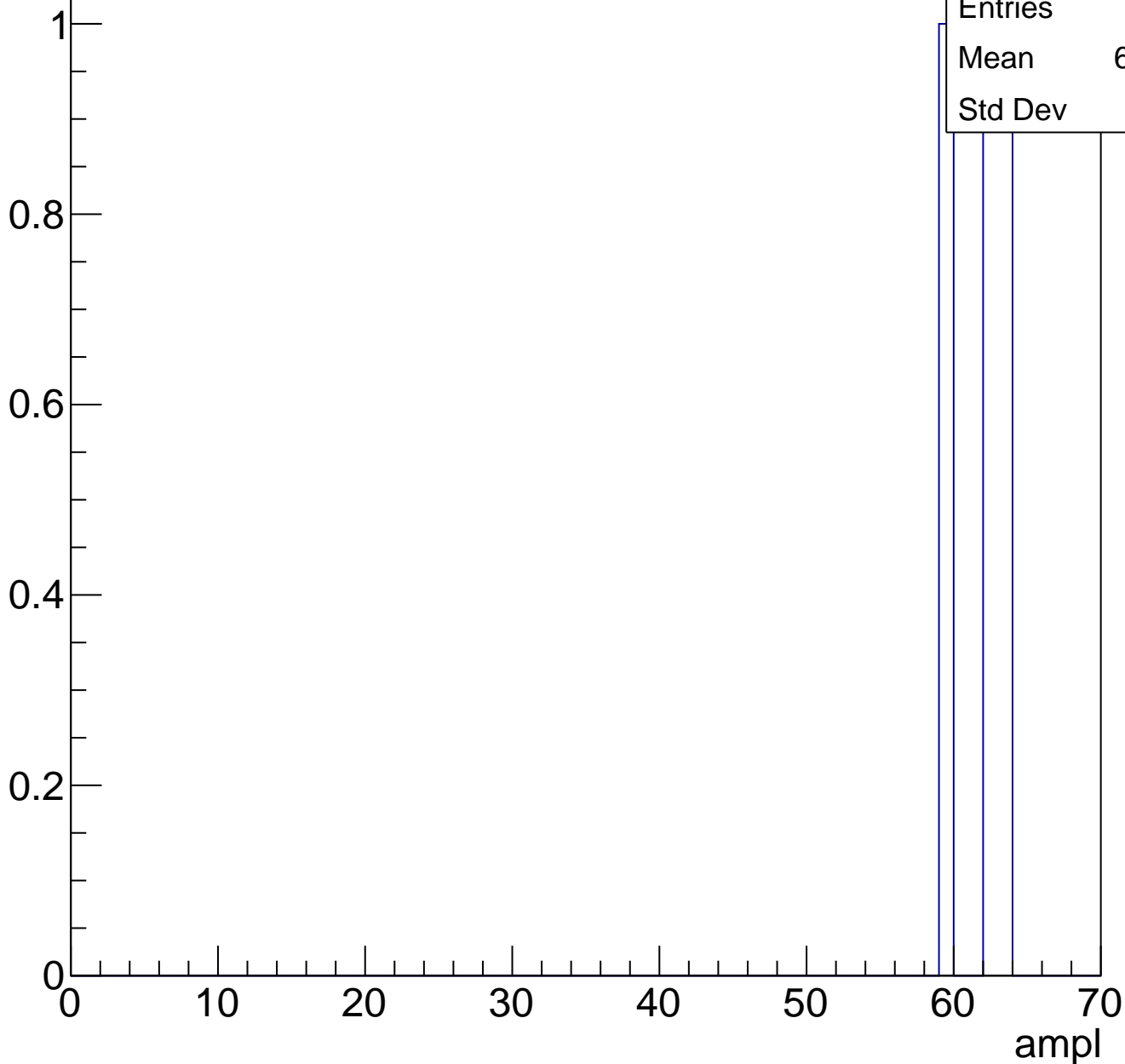
Entry



B1L102S, U14-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch99, adc0

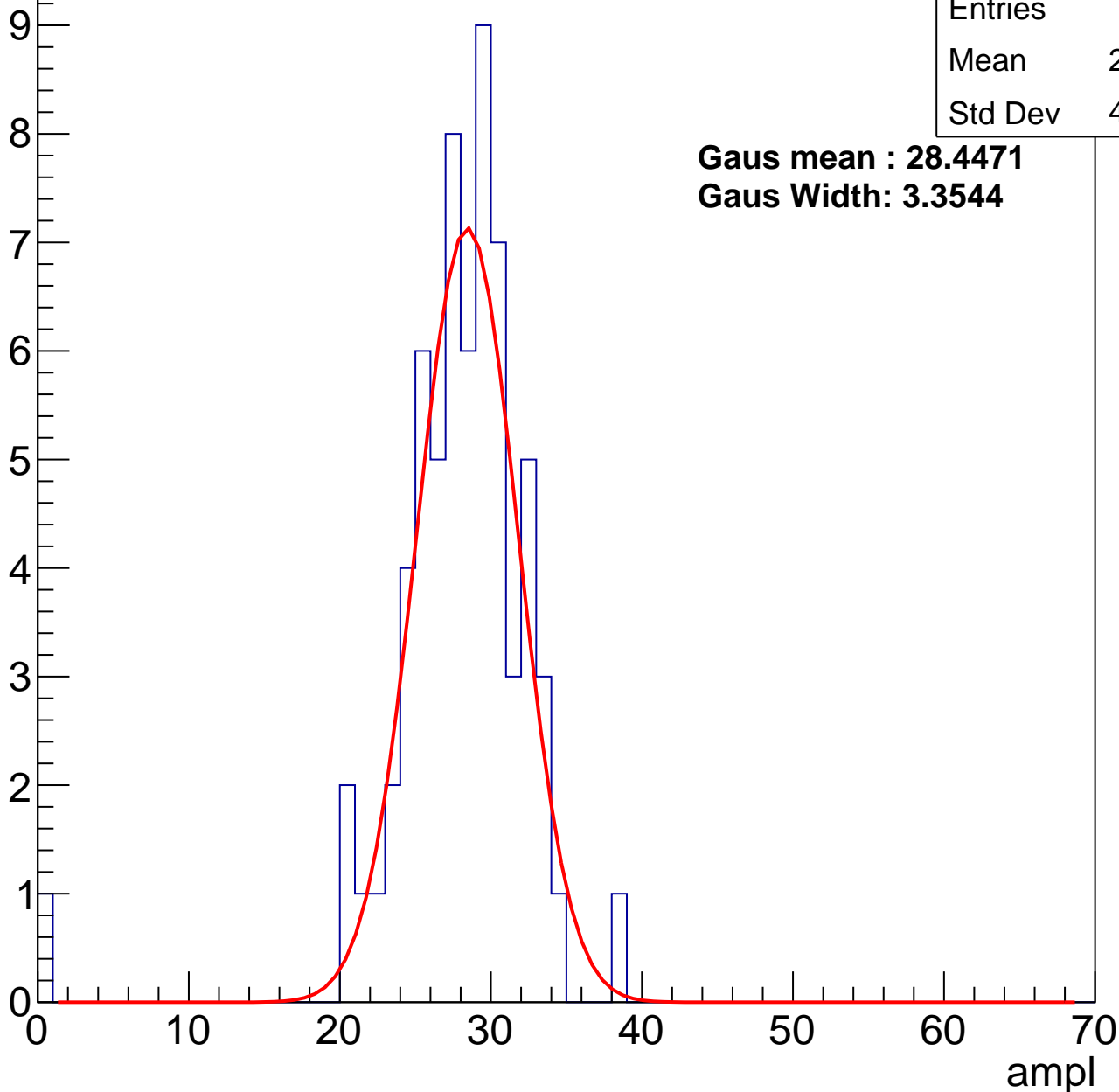
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.45
Std Dev	4.849

Gaus mean : 28.4471

Gaus Width: 3.3544



B1L102S, U14-ch99, adc1

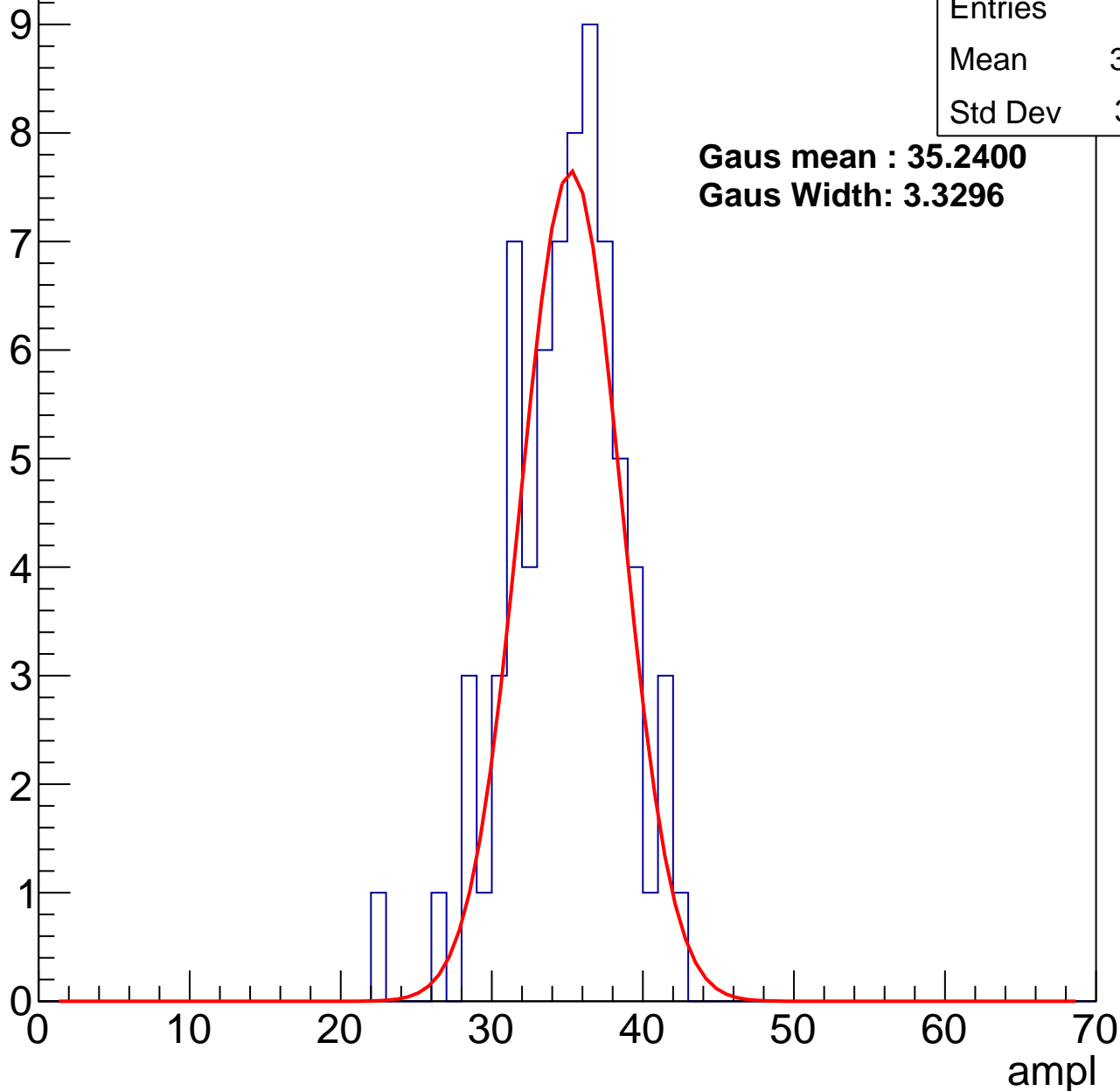
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.45
Std Dev	3.741

Gaus mean : 35.2400

Gaus Width: 3.3296



B1L102S, U14-ch99, adc2

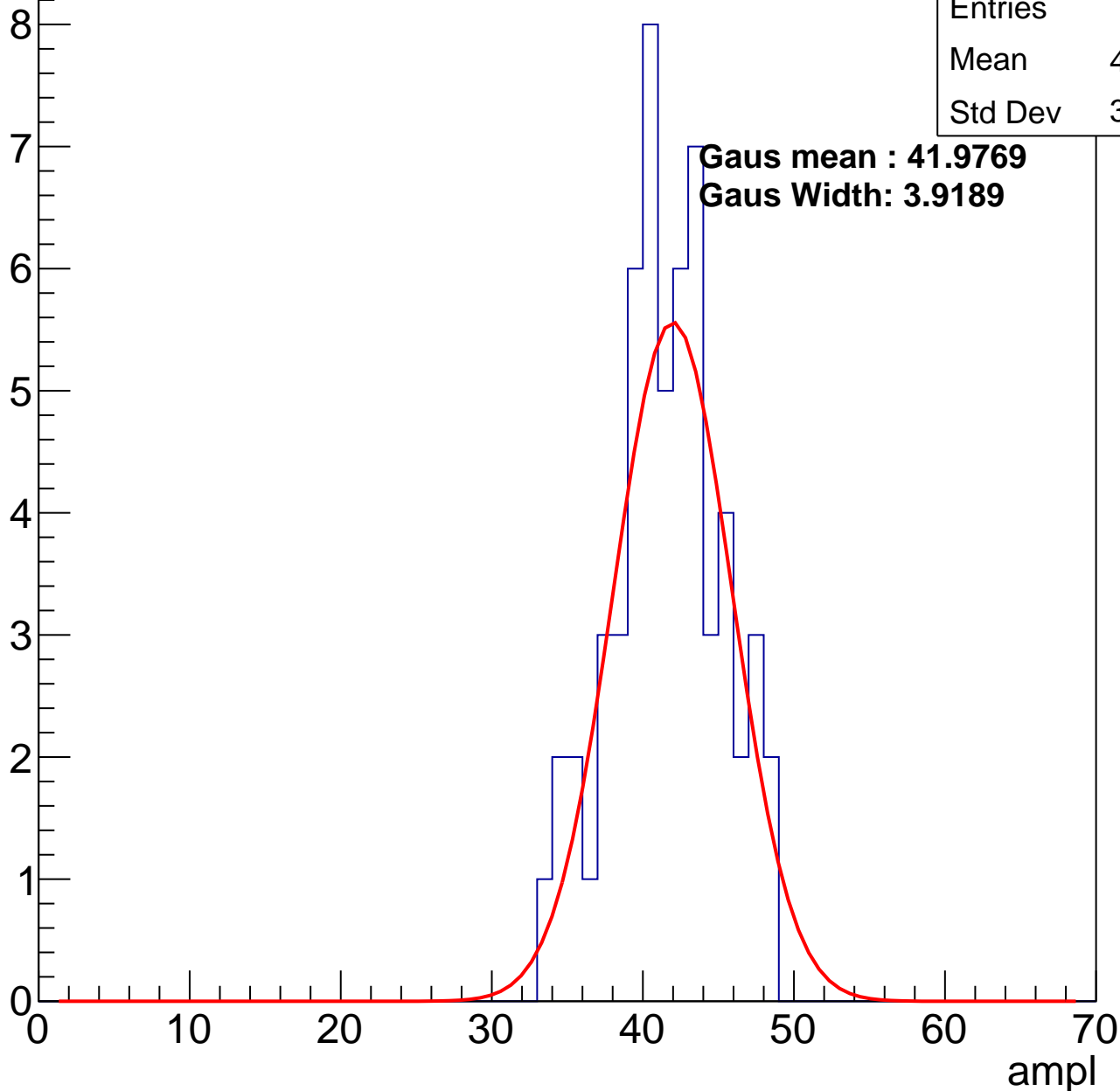
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	41.12
Std Dev	3.587

Gaus mean : 41.9769

Gaus Width: 3.9189

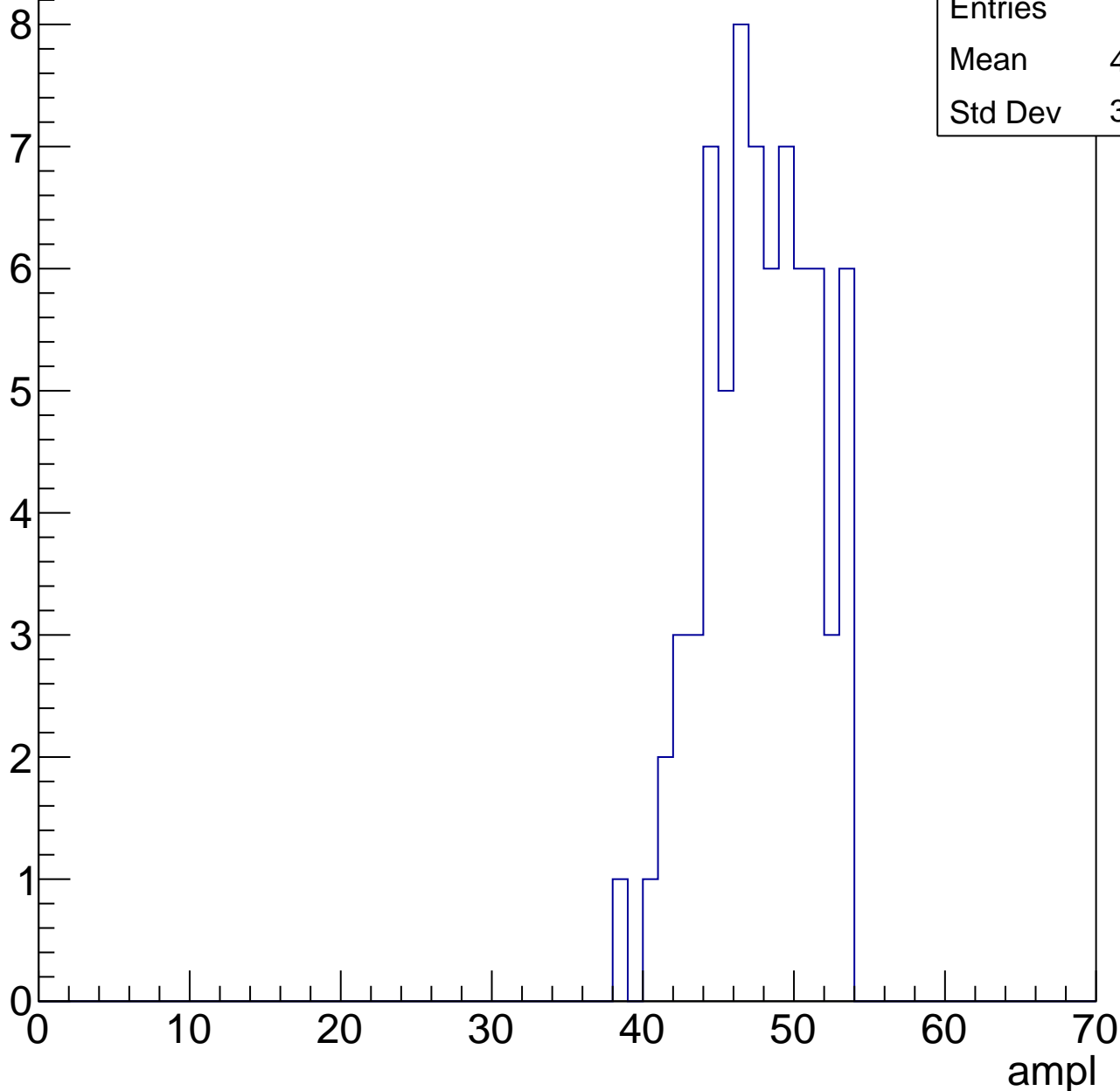


B1L102S, U14-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	47.27
Std Dev	3.532

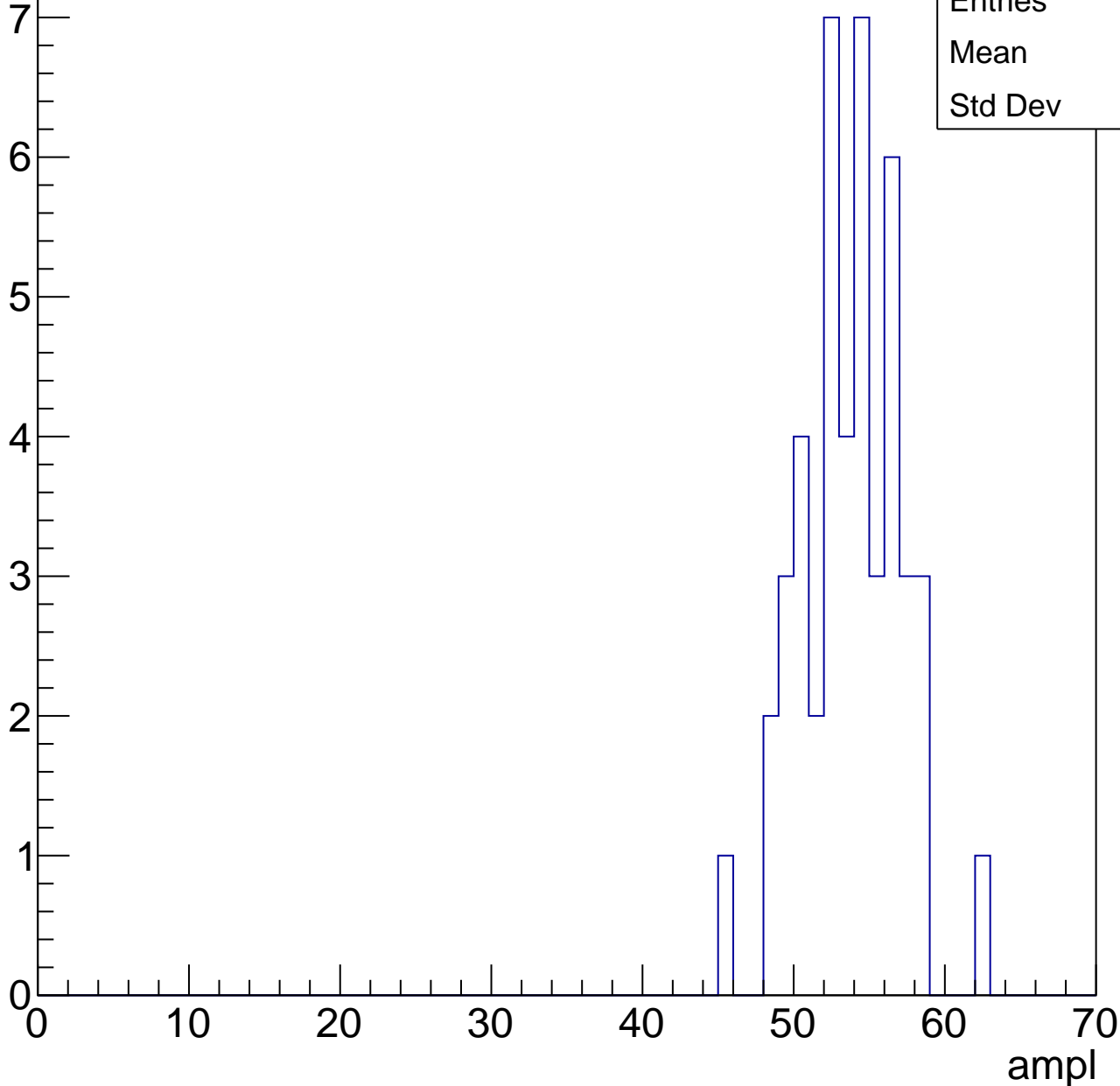


B1L102S, U14-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	53.3
Std Dev	3.25

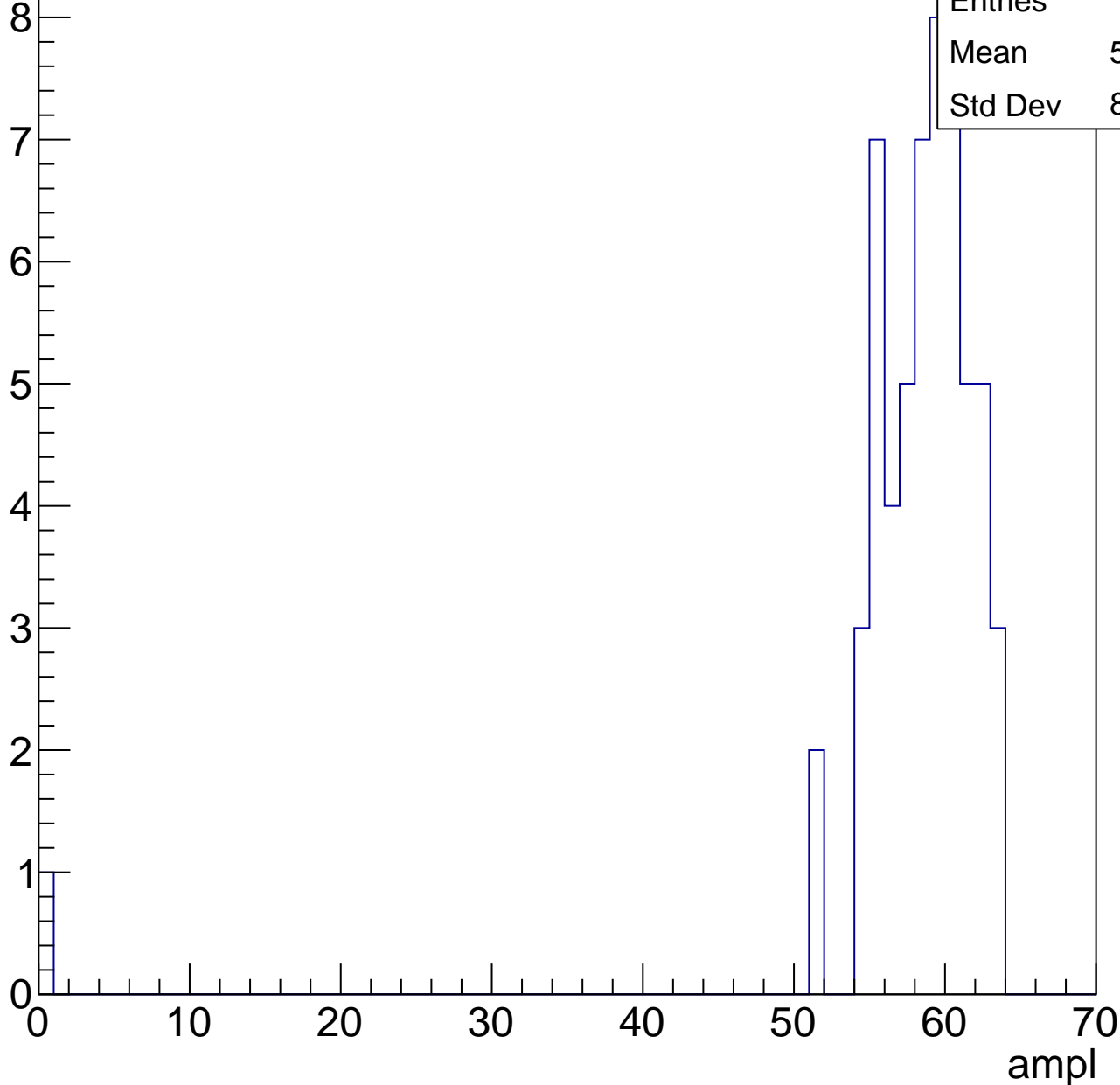


B1L102S, U14-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.24
Std Dev	8.095

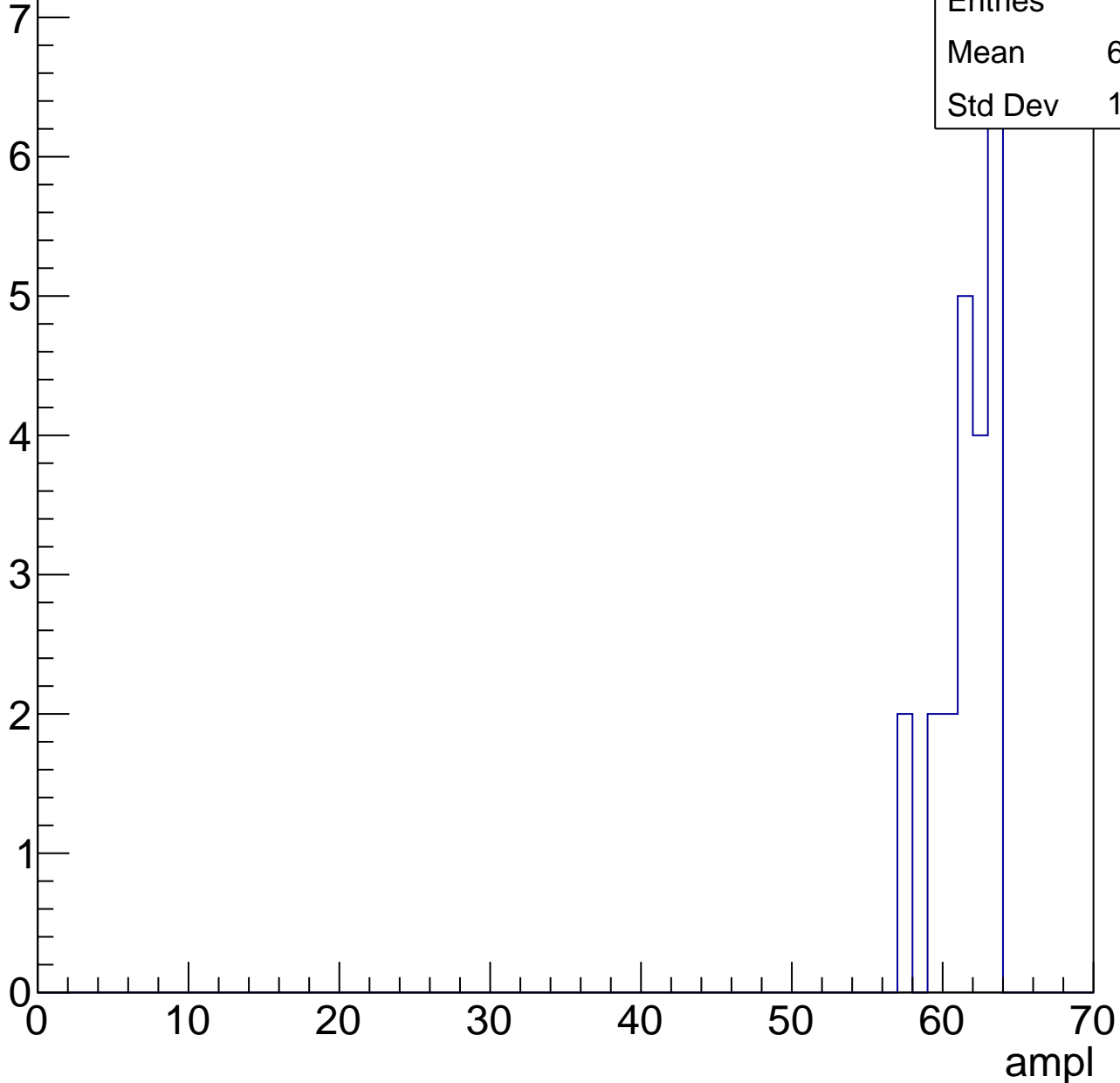


B1L102S, U14-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	61.18
Std Dev	1.825



B1L102S, U14-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch100, adc0

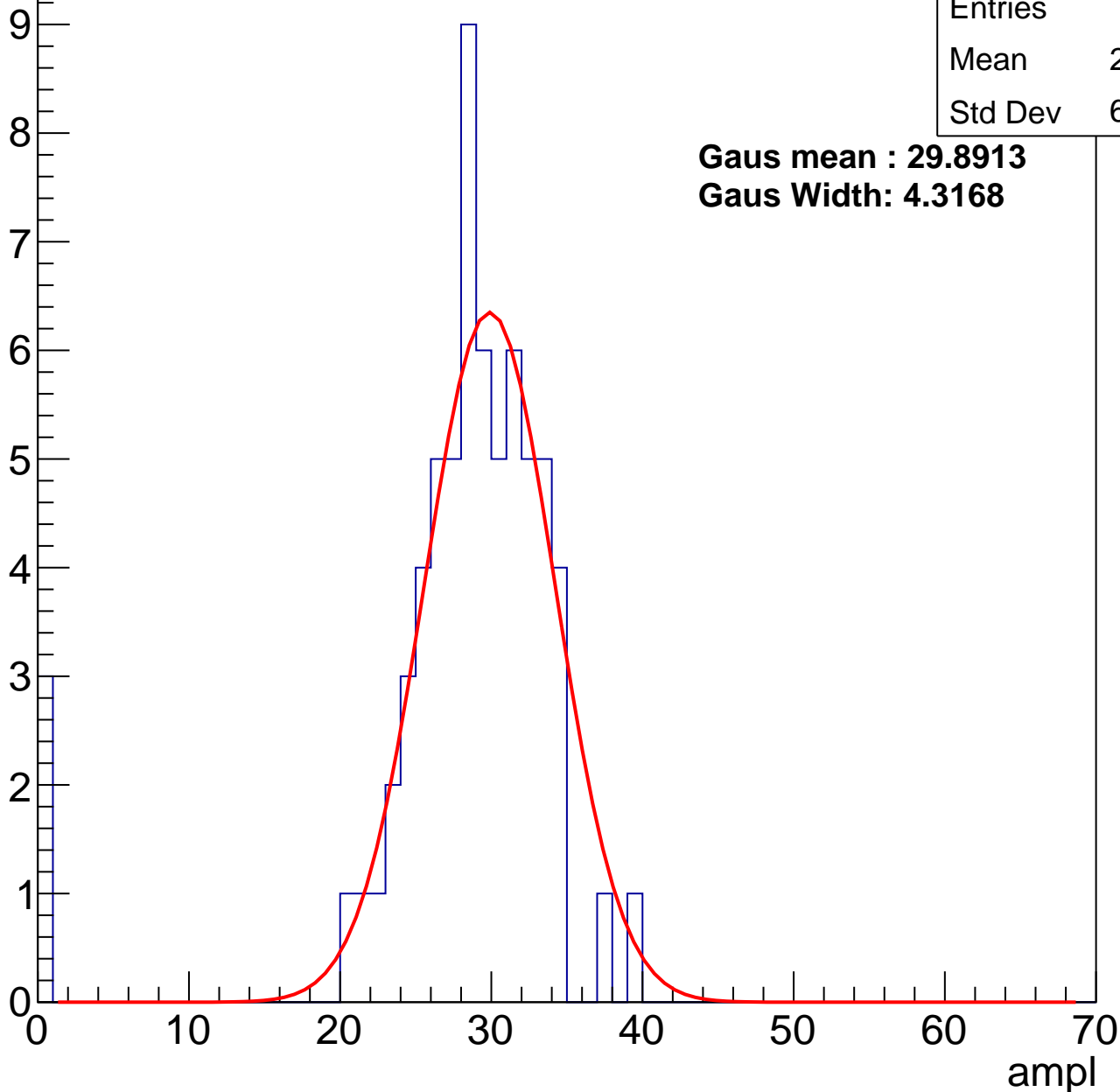
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.54
Std Dev	6.993

Gaus mean : 29.8913

Gaus Width: 4.3168



B1L102S, U14-ch100, adc1

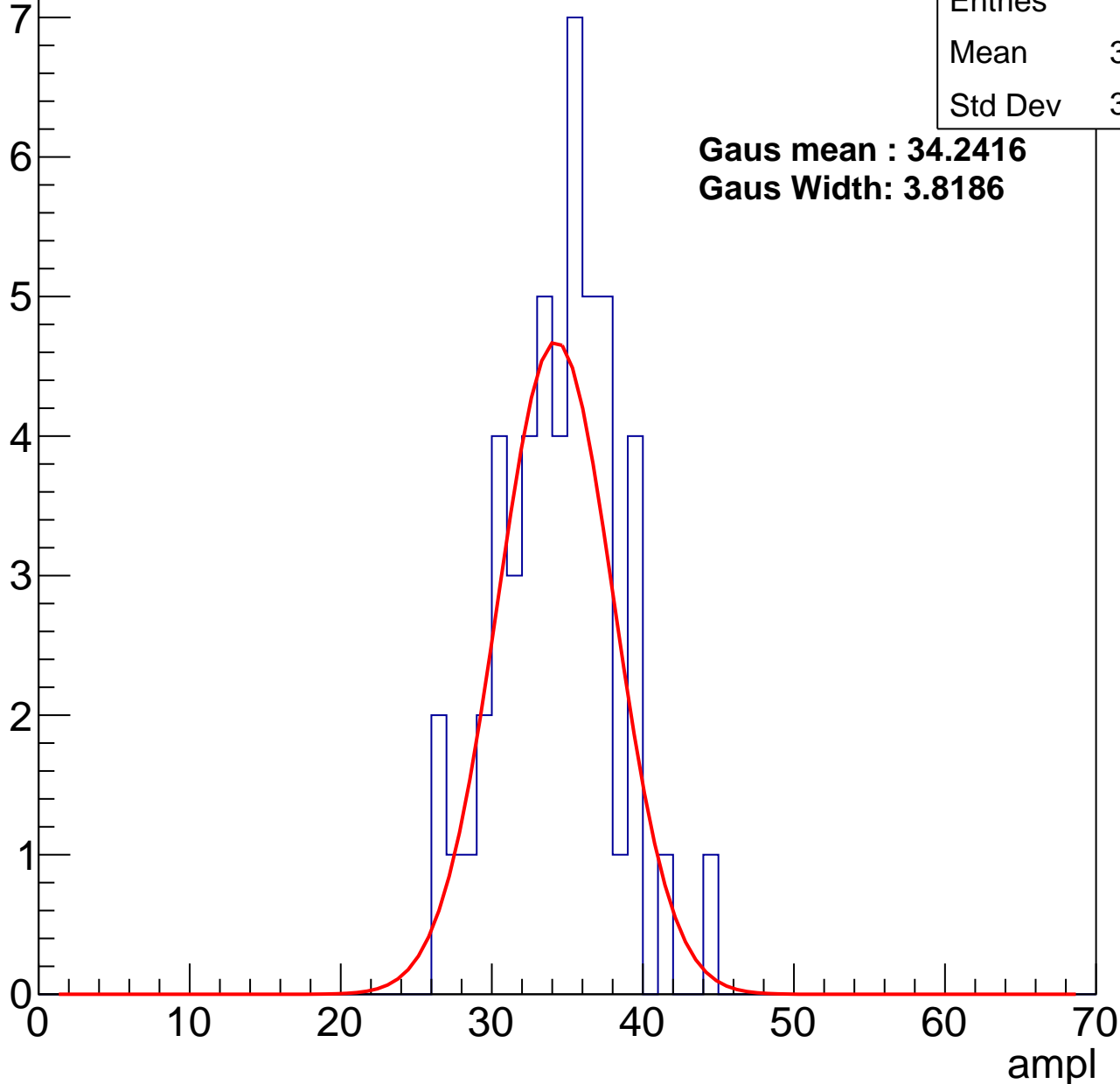
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	33.92
Std Dev	3.773

Gaus mean : 34.2416

Gaus Width: 3.8186



B1L102S, U14-ch100, adc2

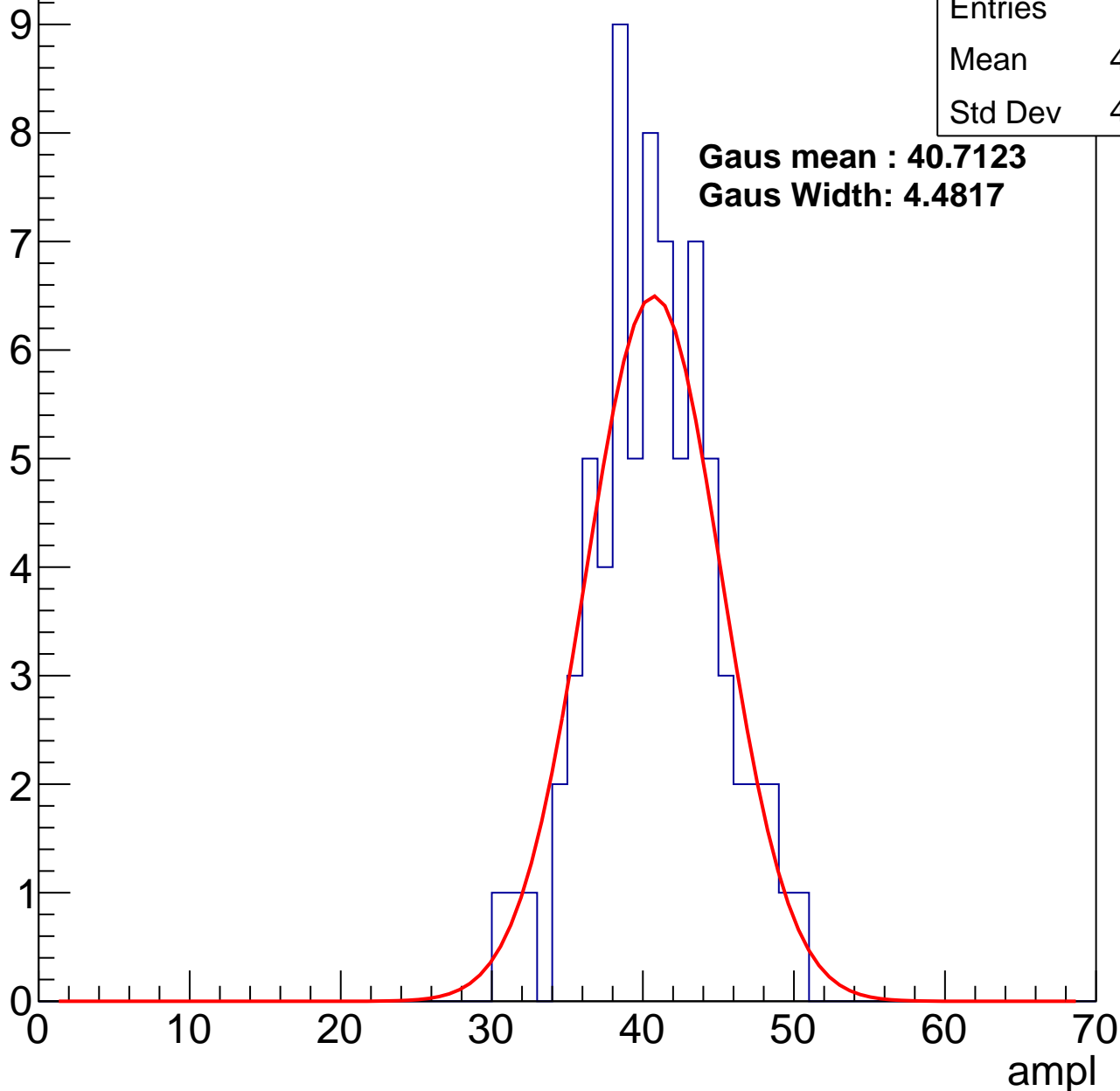
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	40.34
Std Dev	4.134

Gaus mean : 40.7123

Gaus Width: 4.4817

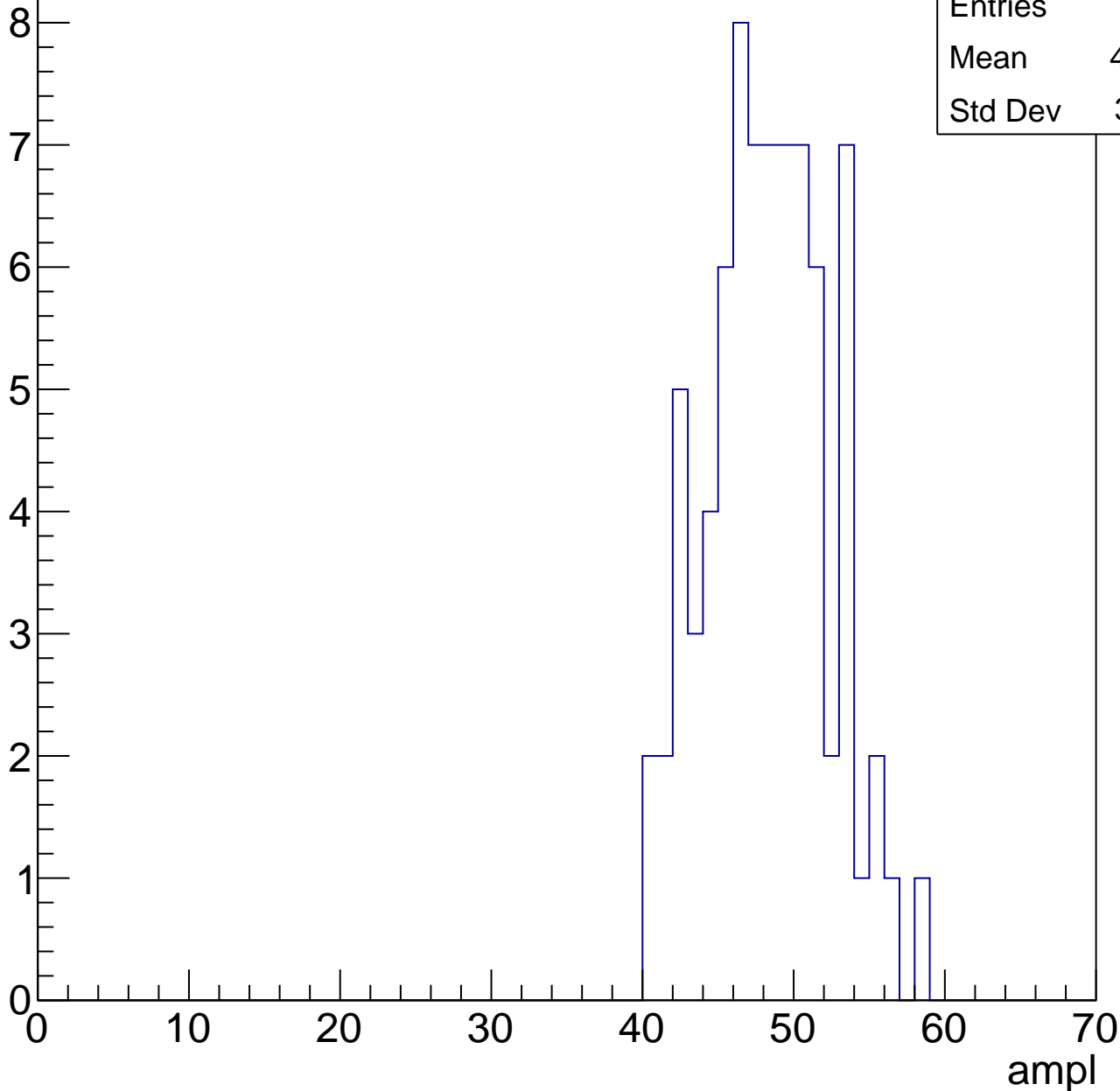


B1L102S, U14-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	47.85
Std Dev	3.971

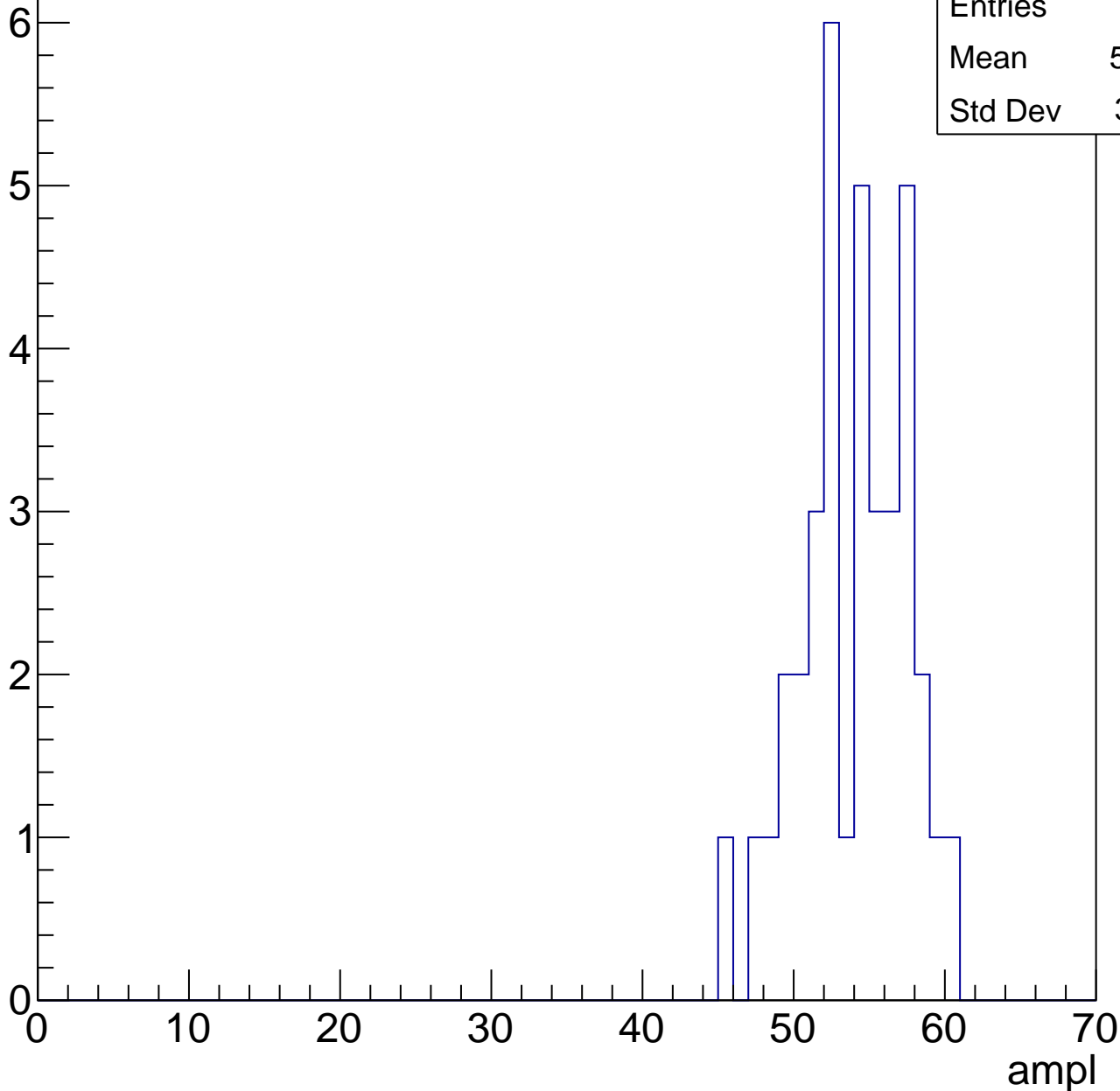


B1L102S, U14-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	53.49
Std Dev	3.461

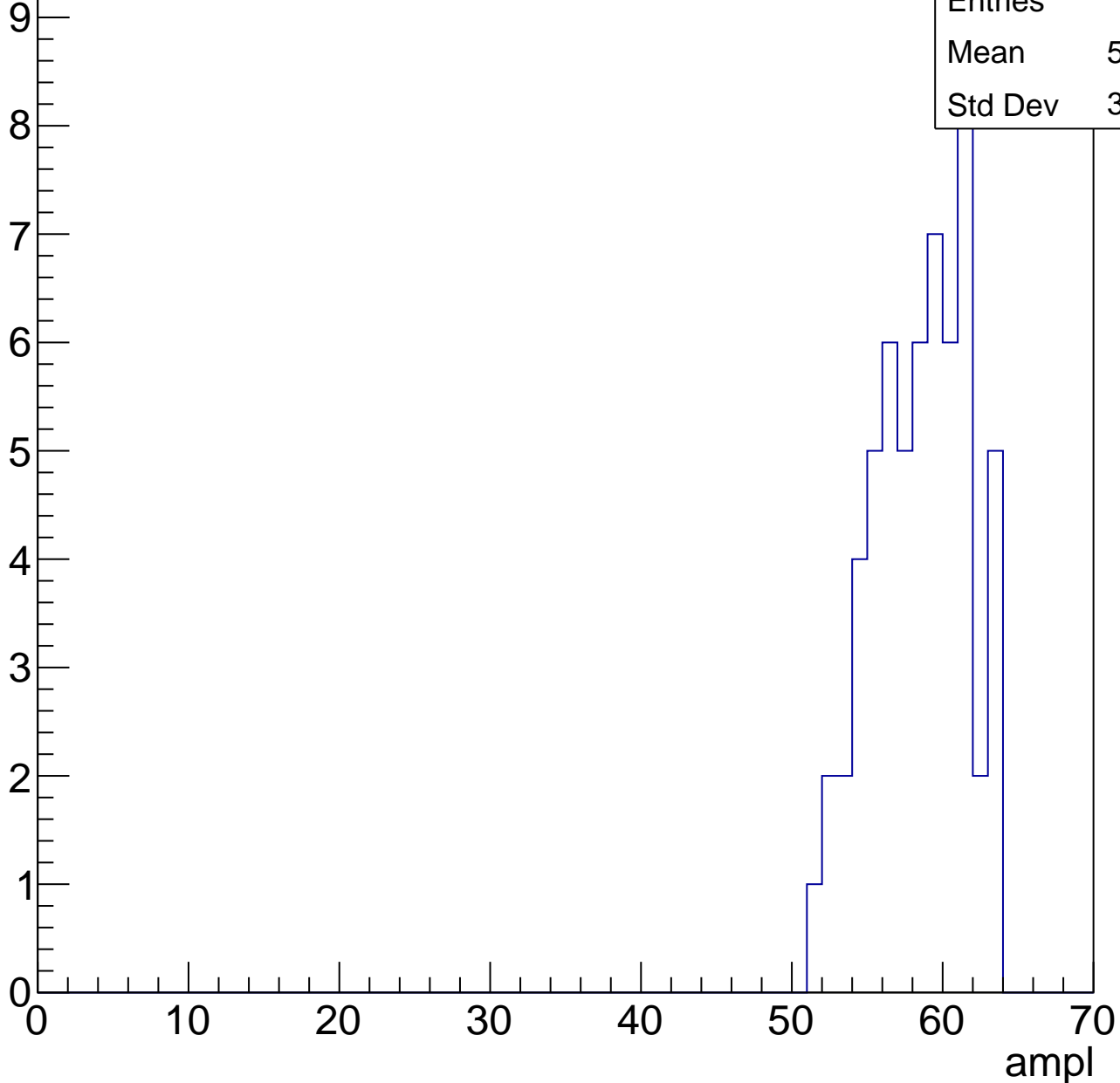


B1L102S, U14-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.03
Std Dev	3.104

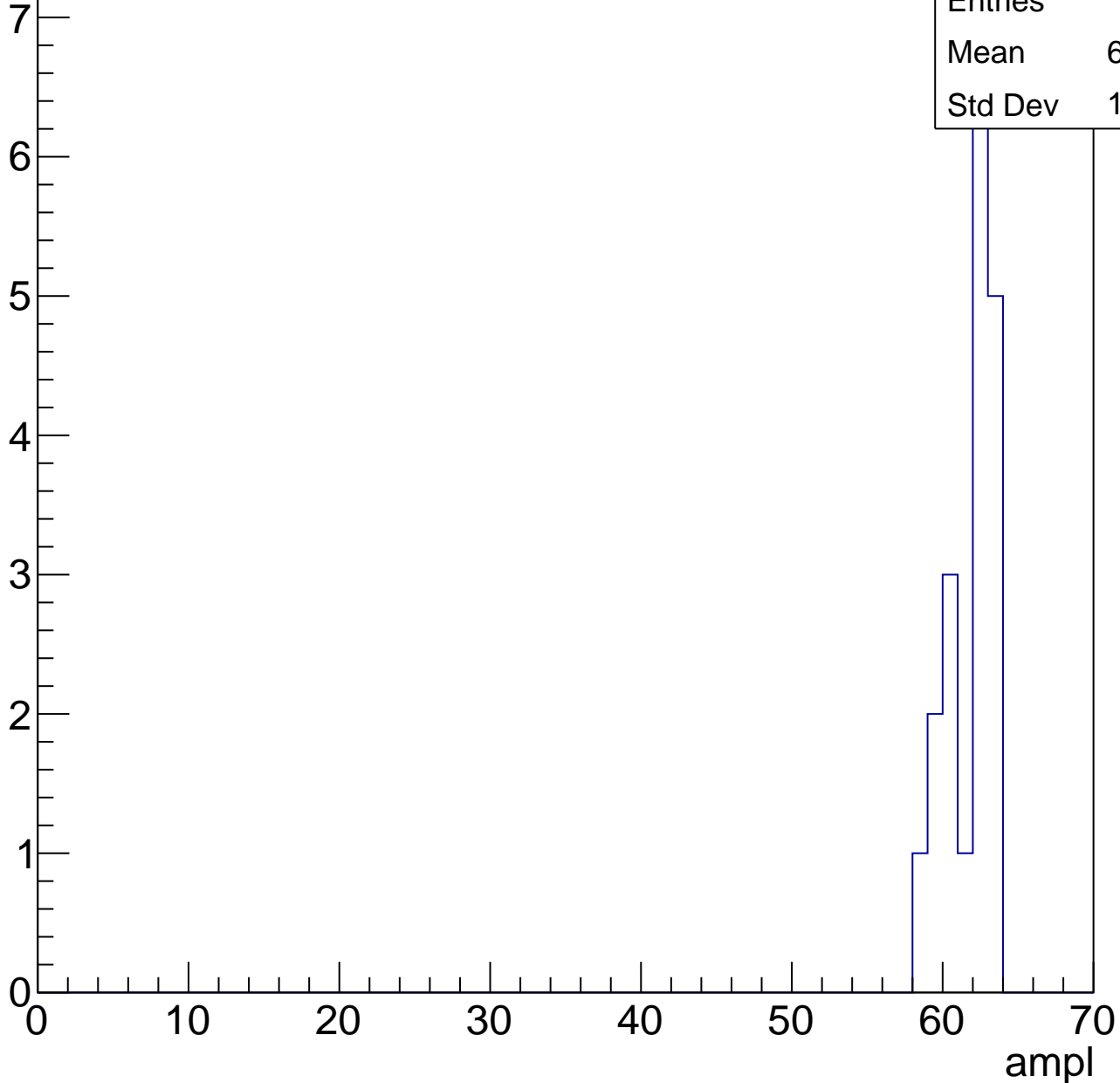


B1L102S, U14-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

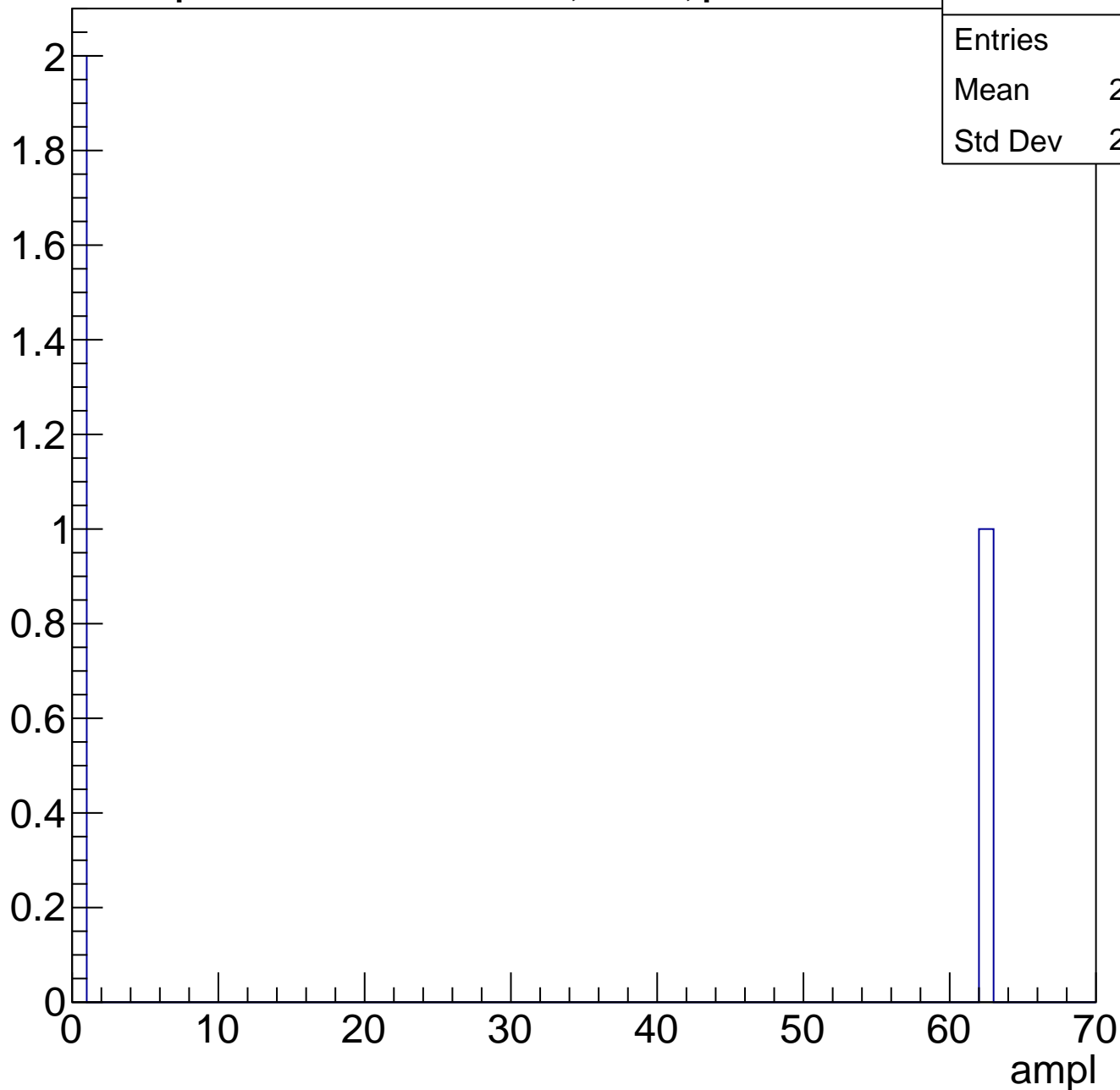
Entries	19
Mean	61.37
Std Dev	1.529



B1L102S, U14-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch101, adc0

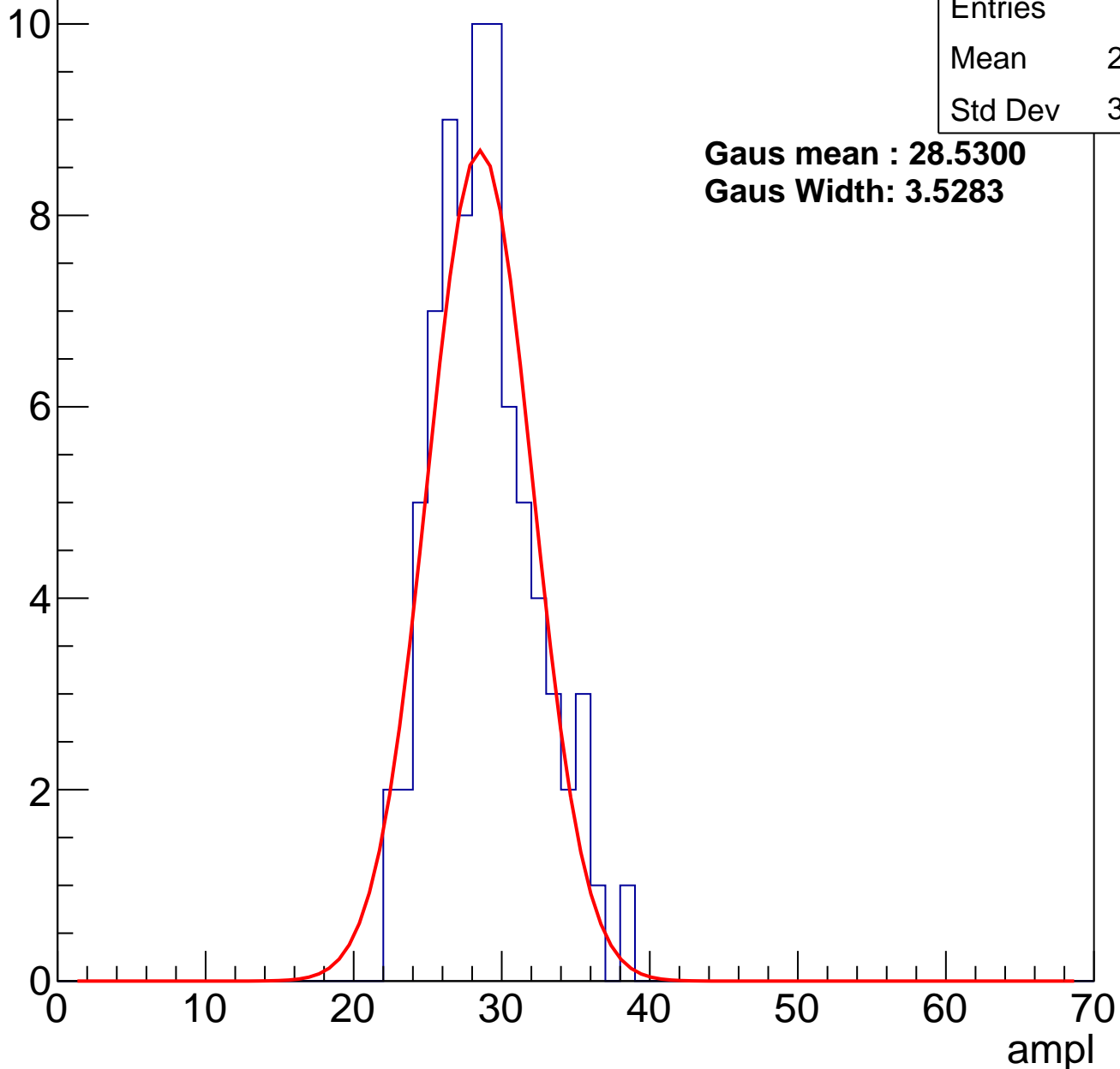
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.38
Std Dev	3.394

Gaus mean : 28.5300

Gaus Width: 3.5283

Entry



B1L102S, U14-ch101, adc1

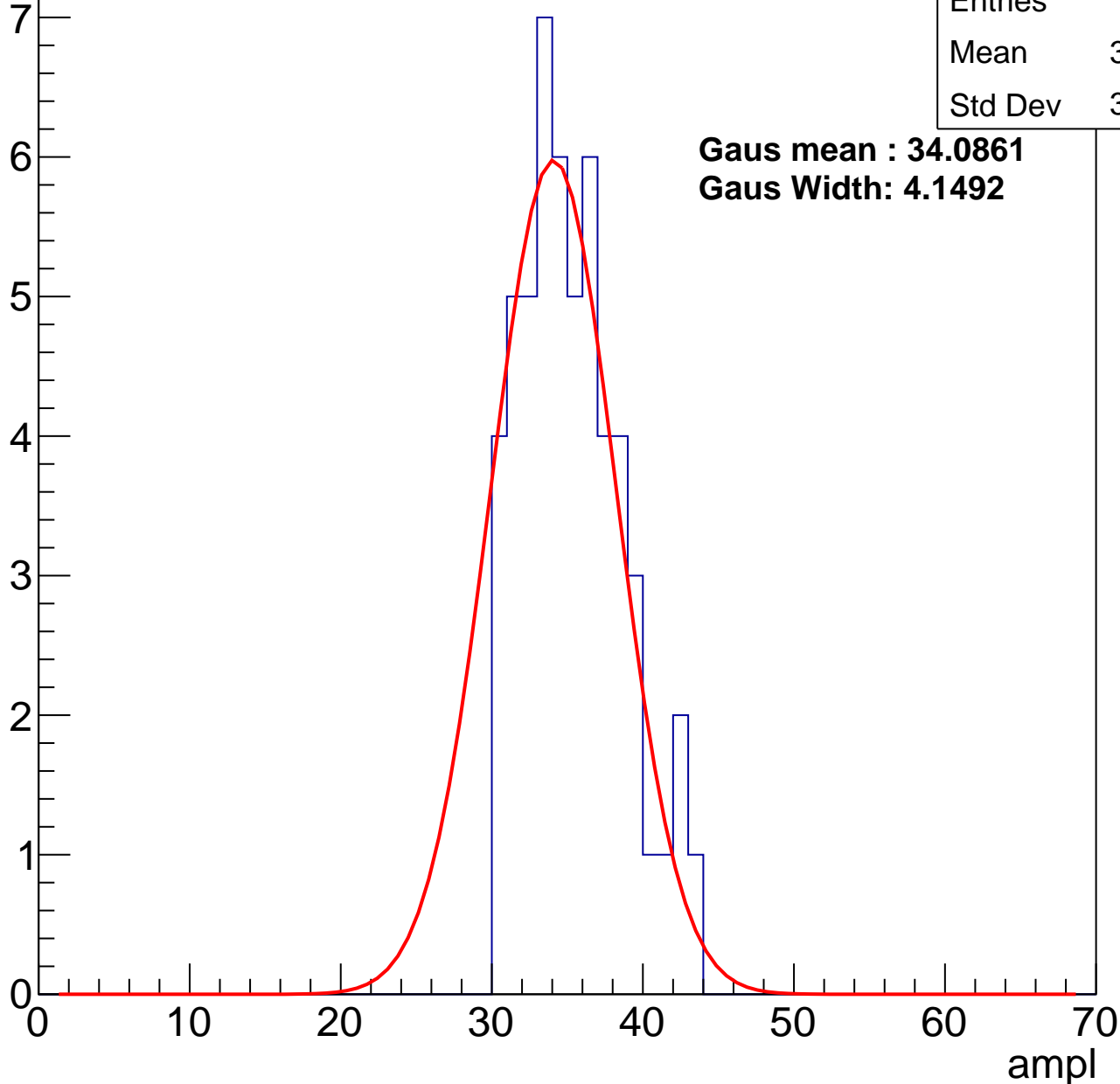
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	34.93
Std Dev	3.299

Gaus mean : 34.0861

Gaus Width: 4.1492



B1L102S, U14-ch101, adc2

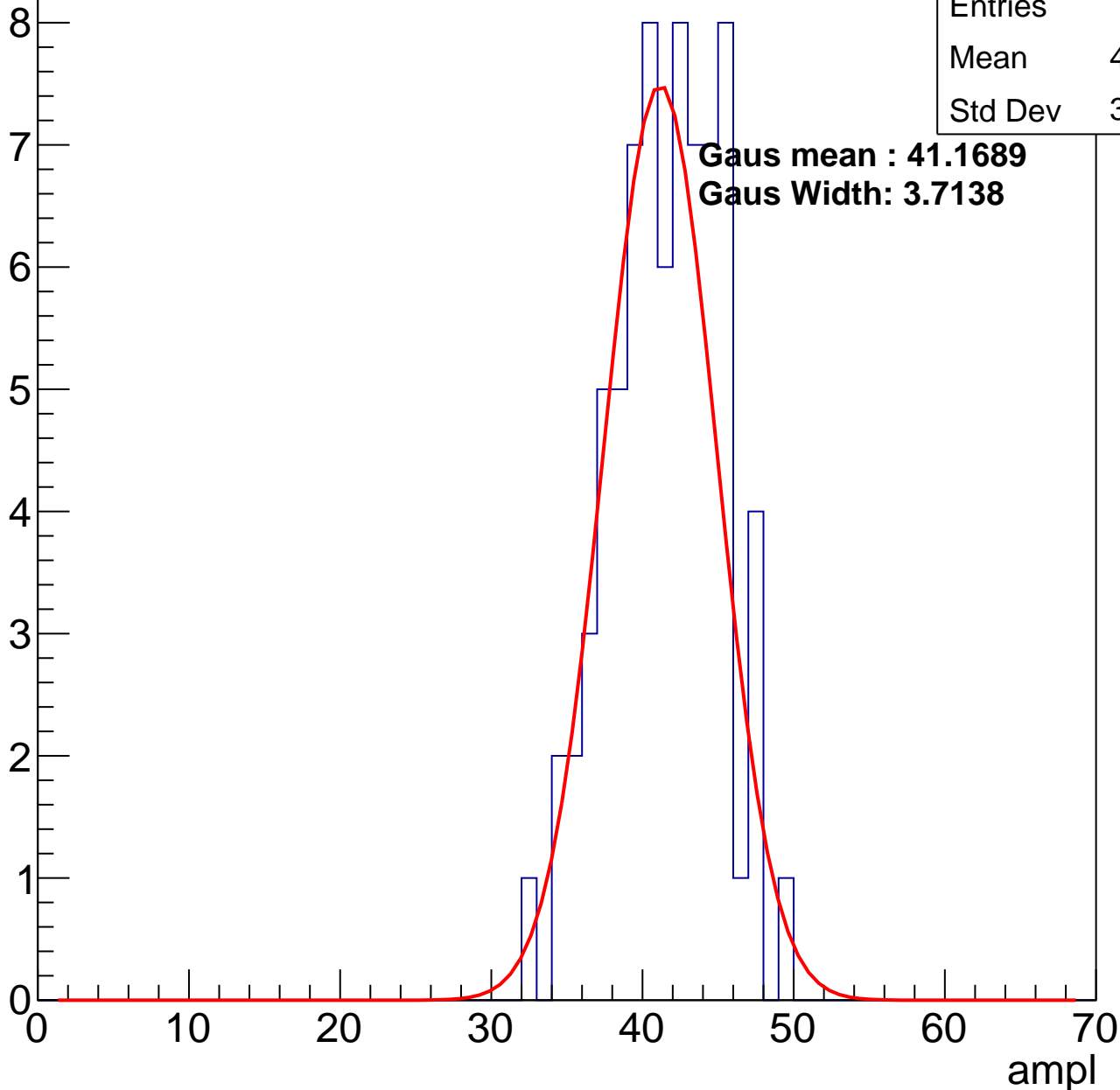
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.07
Std Dev	3.553

Gaus mean : 41.1689

Gaus Width: 3.7138

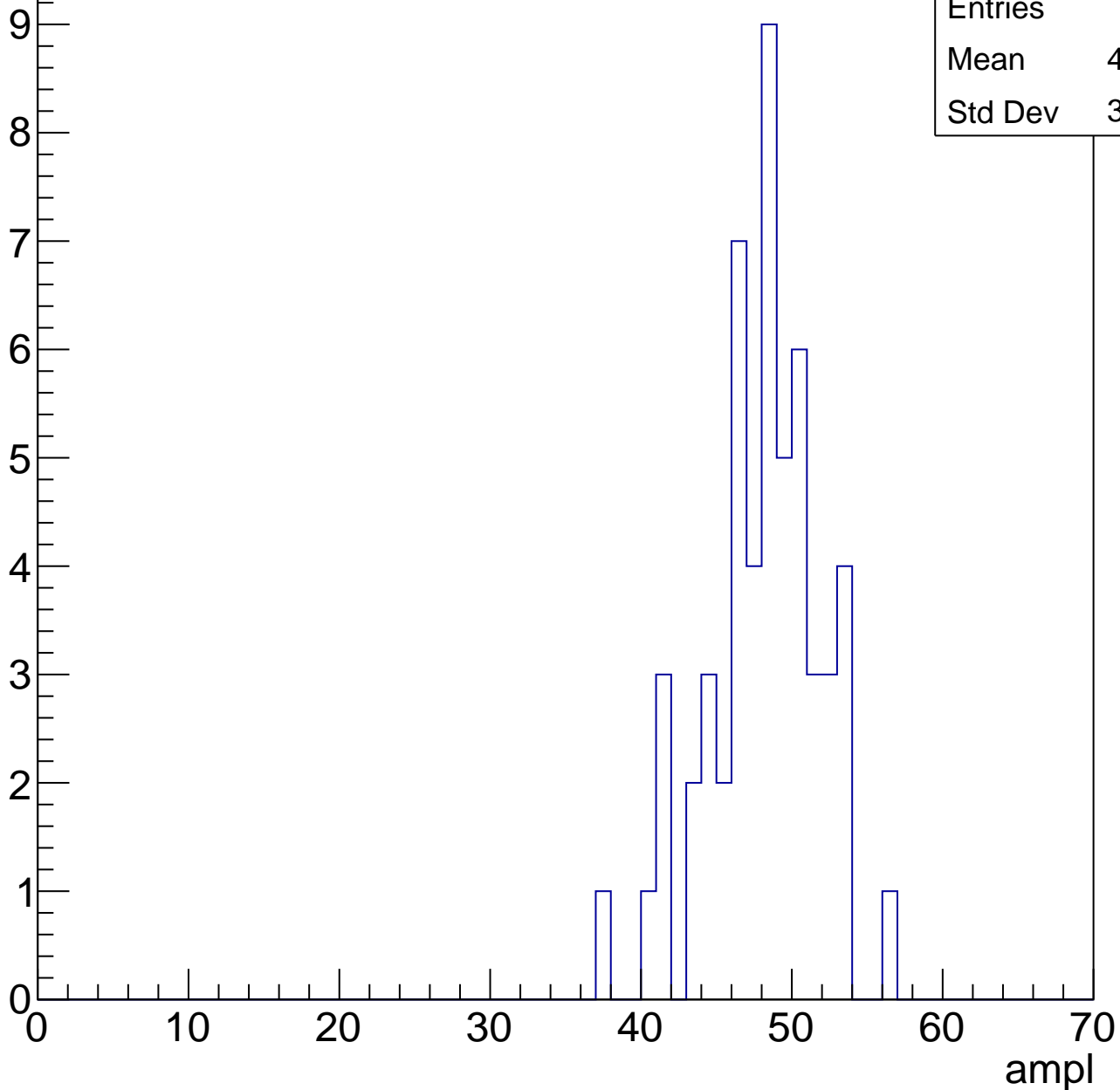


B1L102S, U14-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

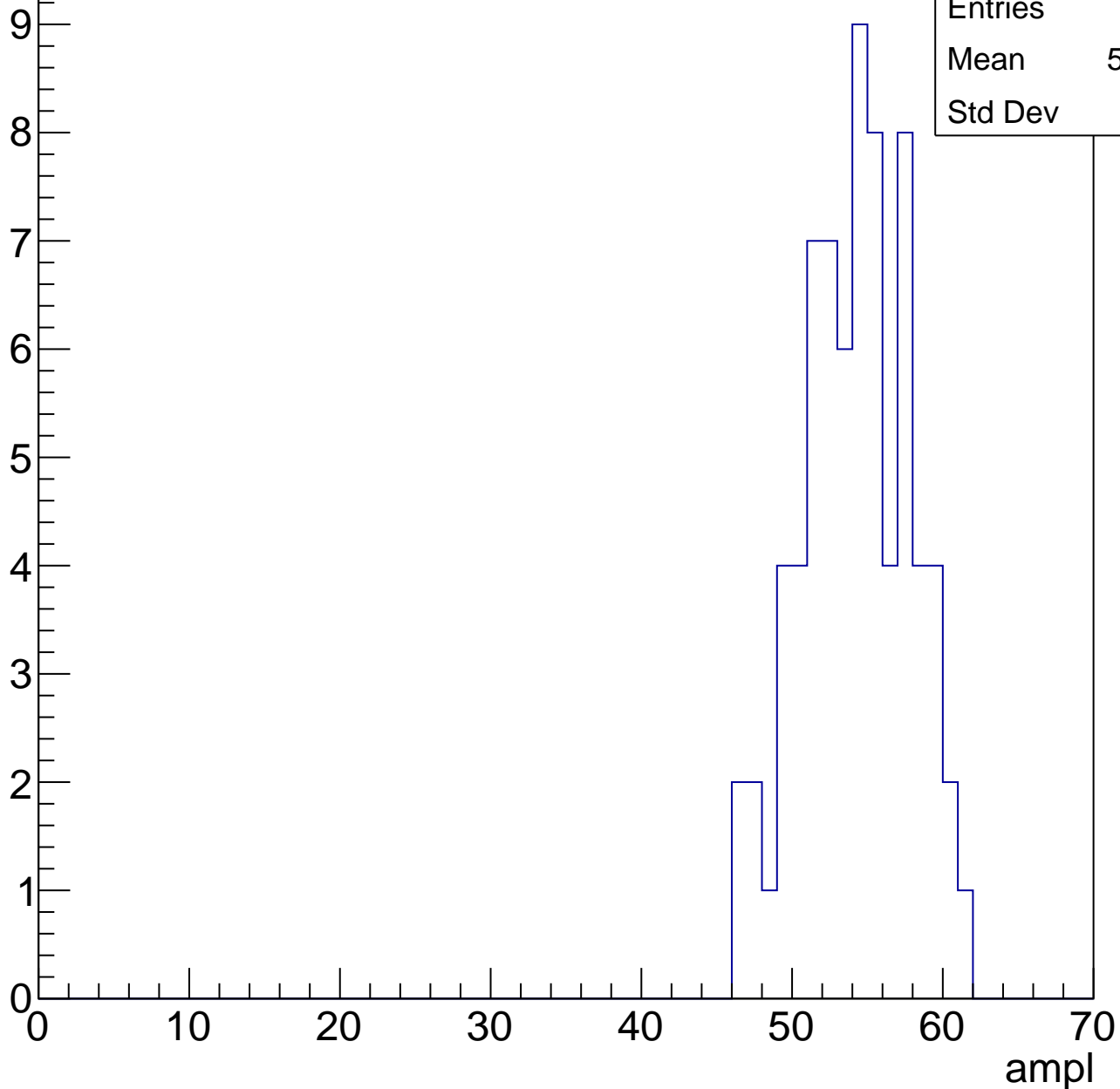
Entries	54
Mean	47.63
Std Dev	3.703



B1L102S, U14-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

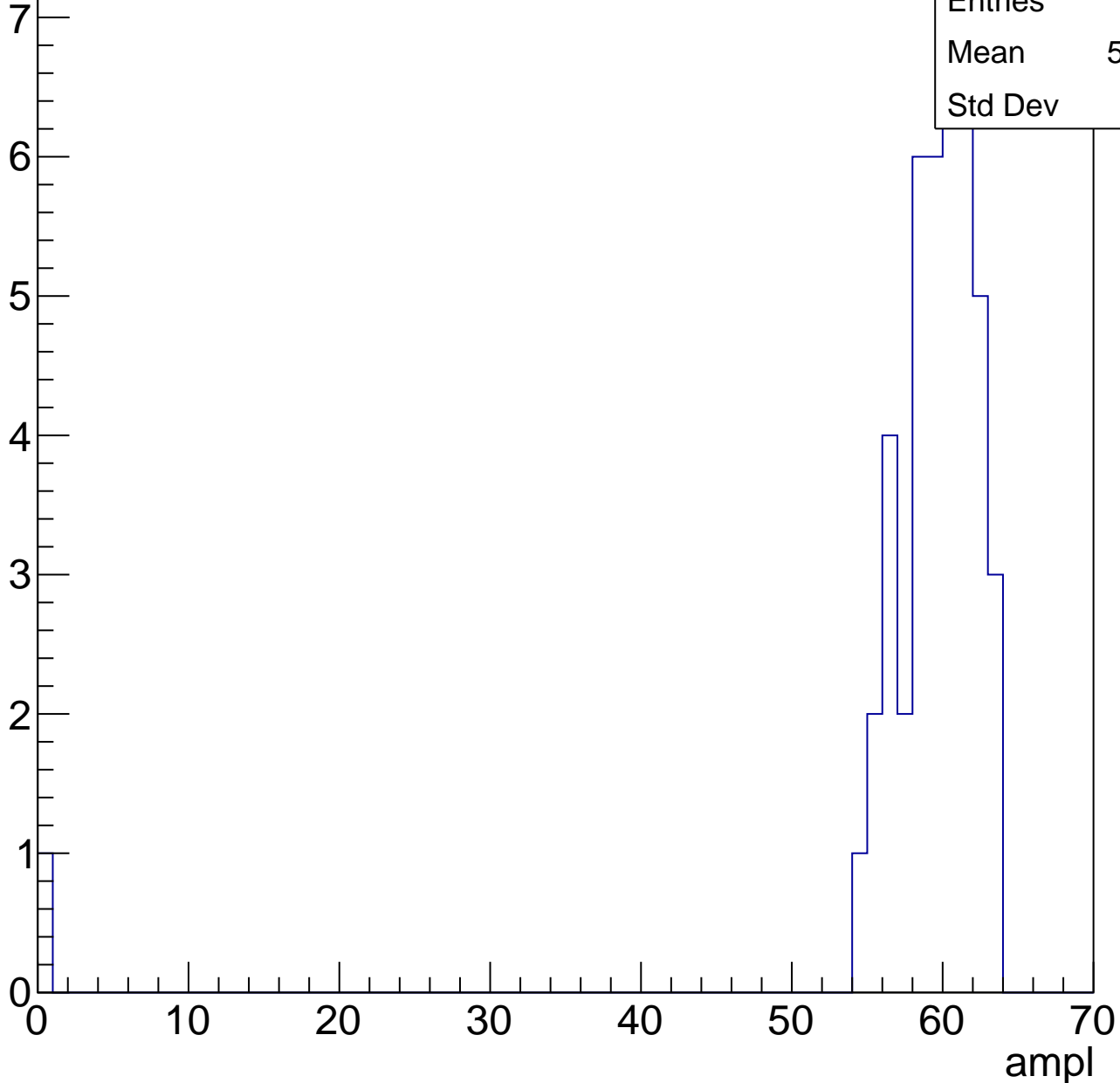


B1L102S, U14-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	57.95
Std Dev	9.13

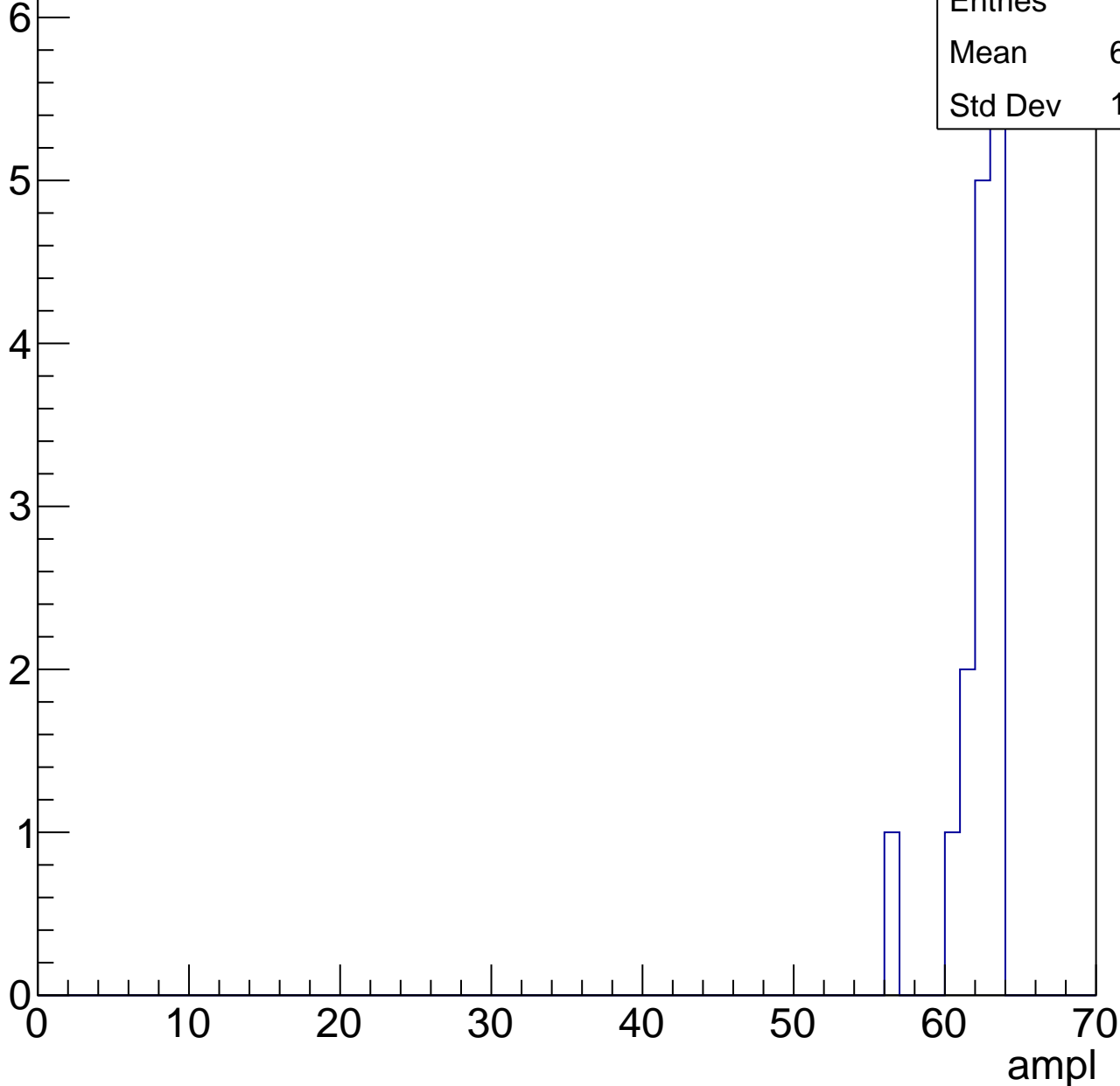


B1L102S, U14-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

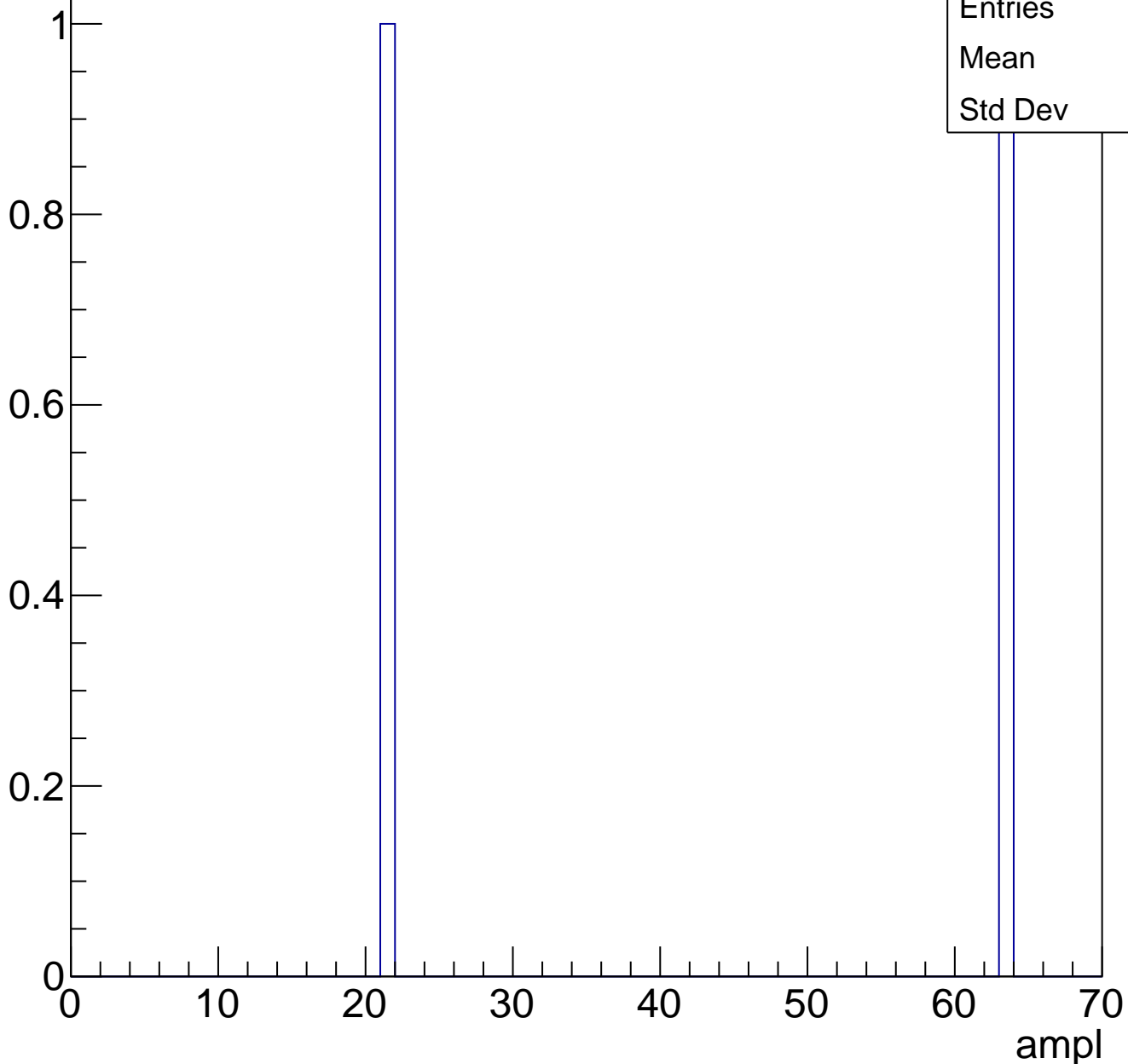
Entries	15
Mean	61.73
Std Dev	1.769



B1L102S, U14-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	42
Std Dev	21

B1L102S, U14-ch102, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	27.1
Std Dev	5

Gaus mean : 27.9029

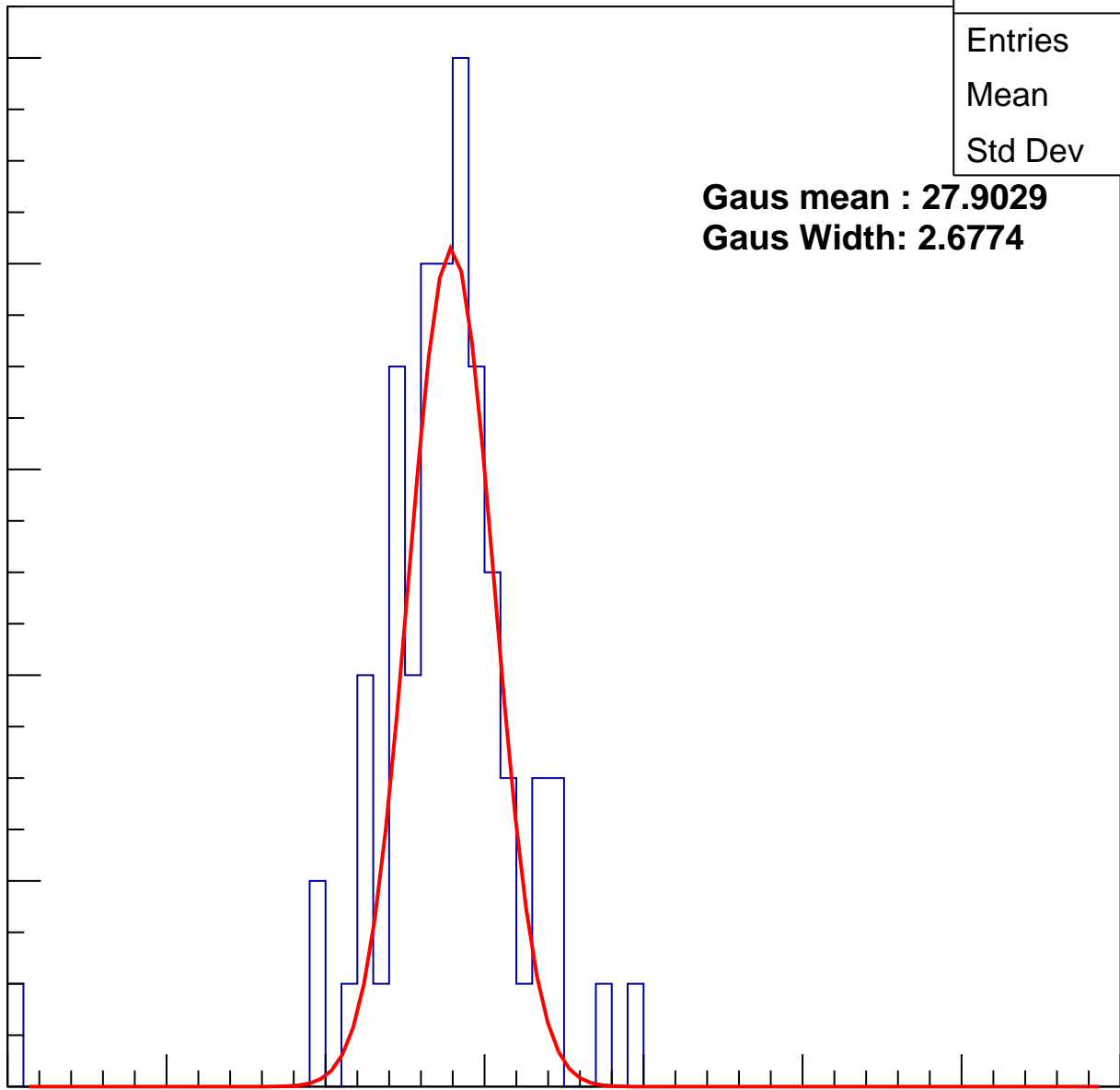
Gaus Width: 2.6774

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch102, adc1

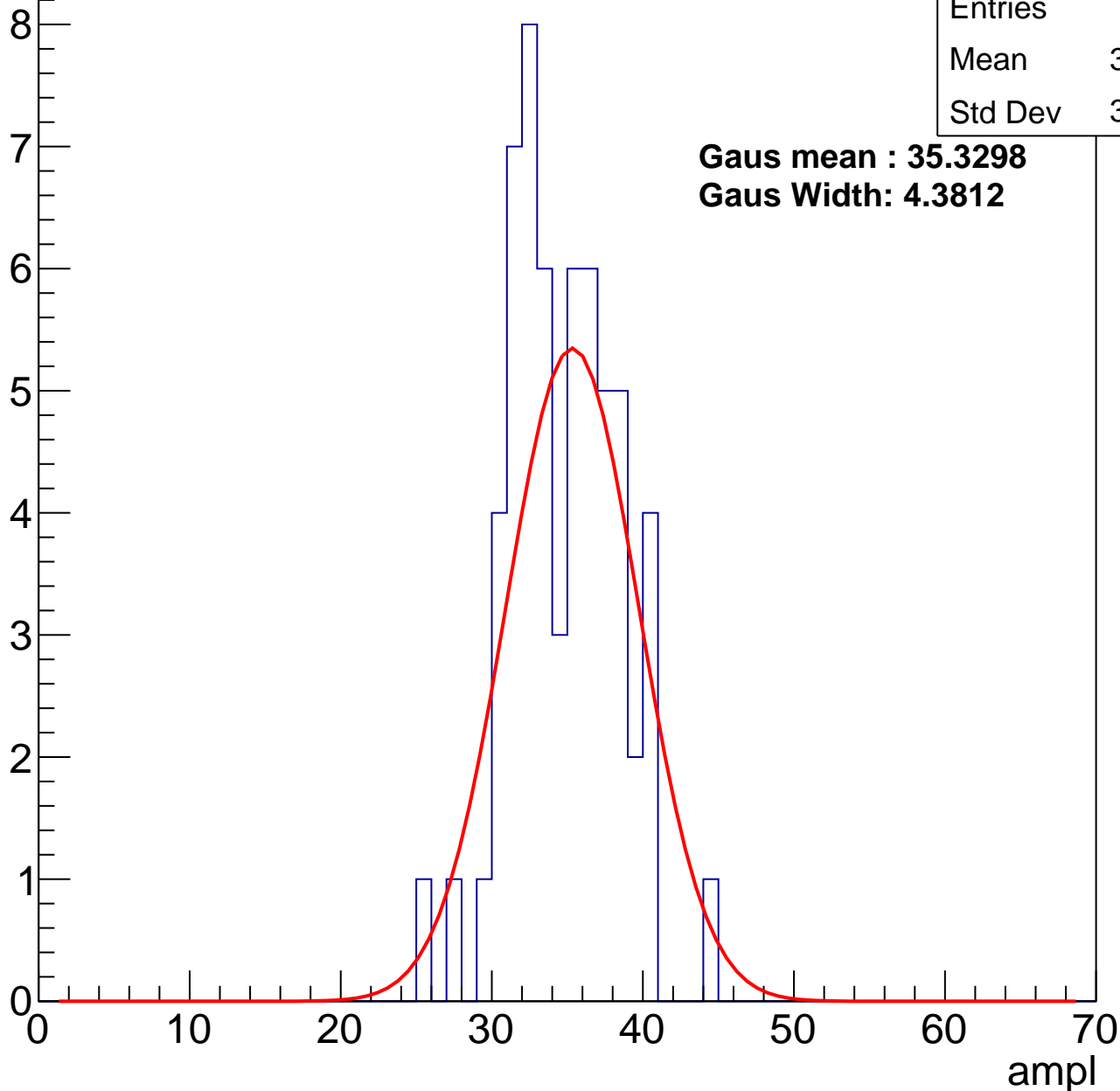
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.28
Std Dev	3.569

Gaus mean : 35.3298

Gaus Width: 4.3812



B1L102S, U14-ch102, adc2

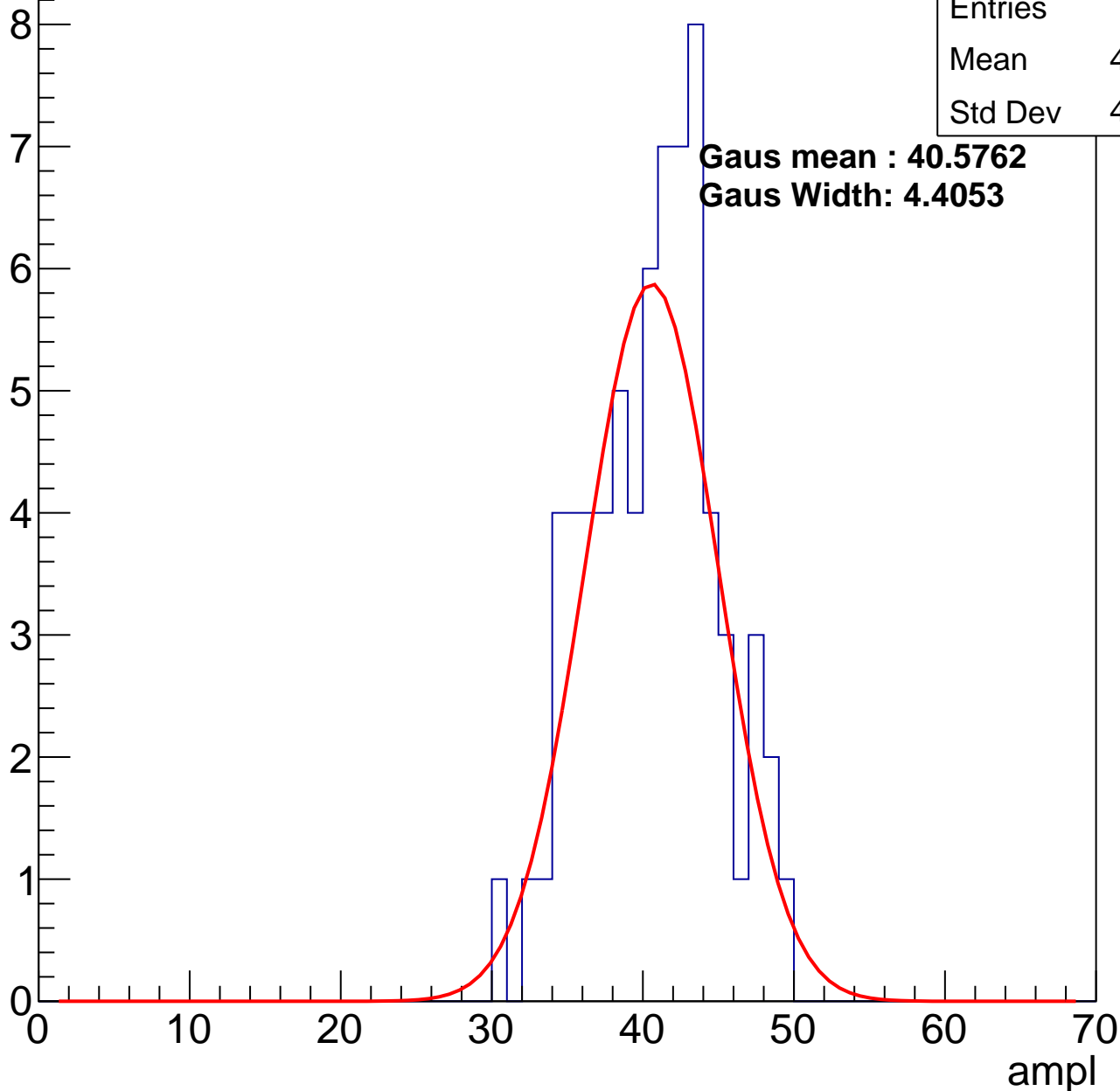
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40.24
Std Dev	4.166

Gaus mean : 40.5762

Gaus Width: 4.4053

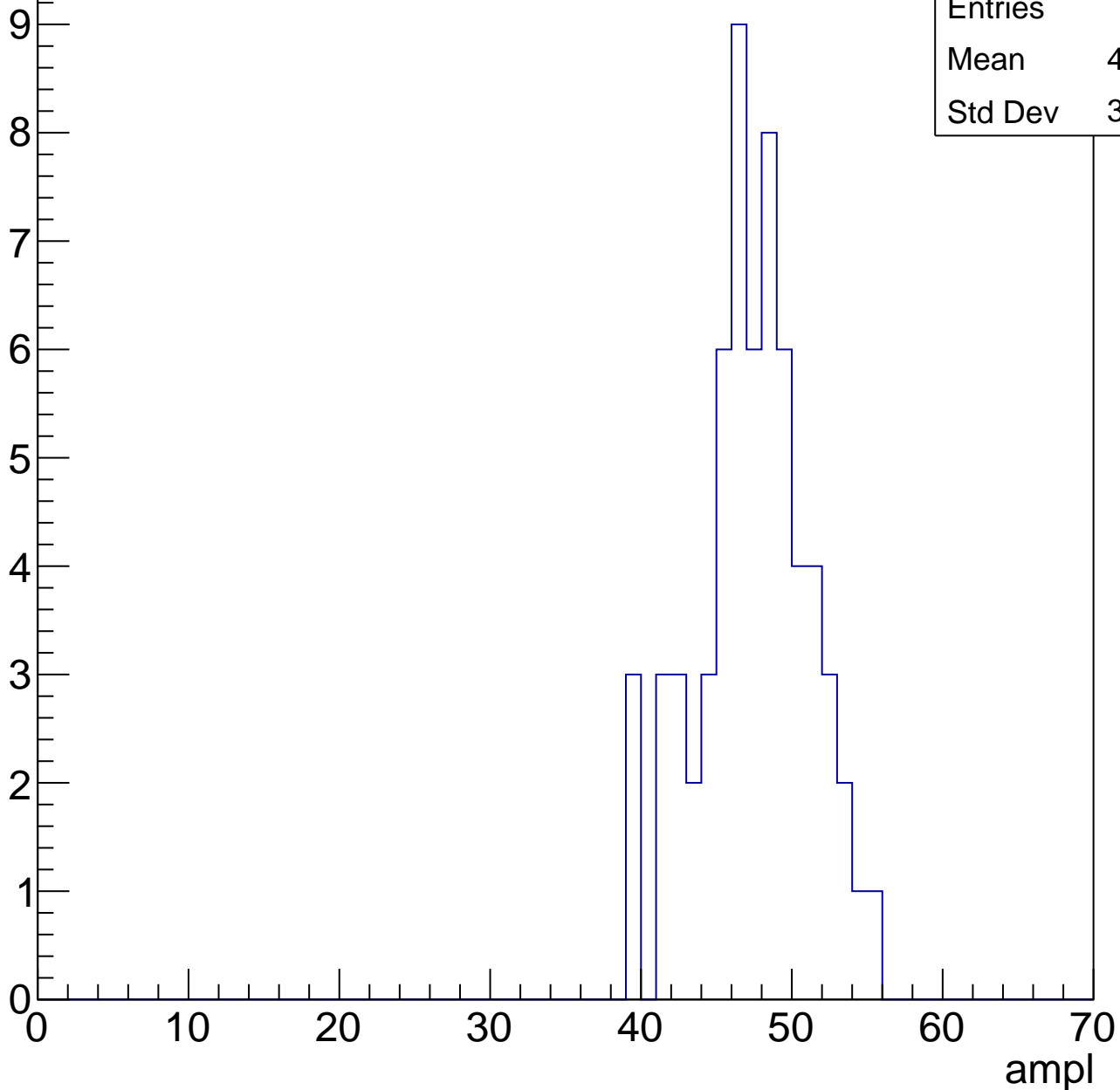


B1L102S, U14-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	46.92
Std Dev	3.663

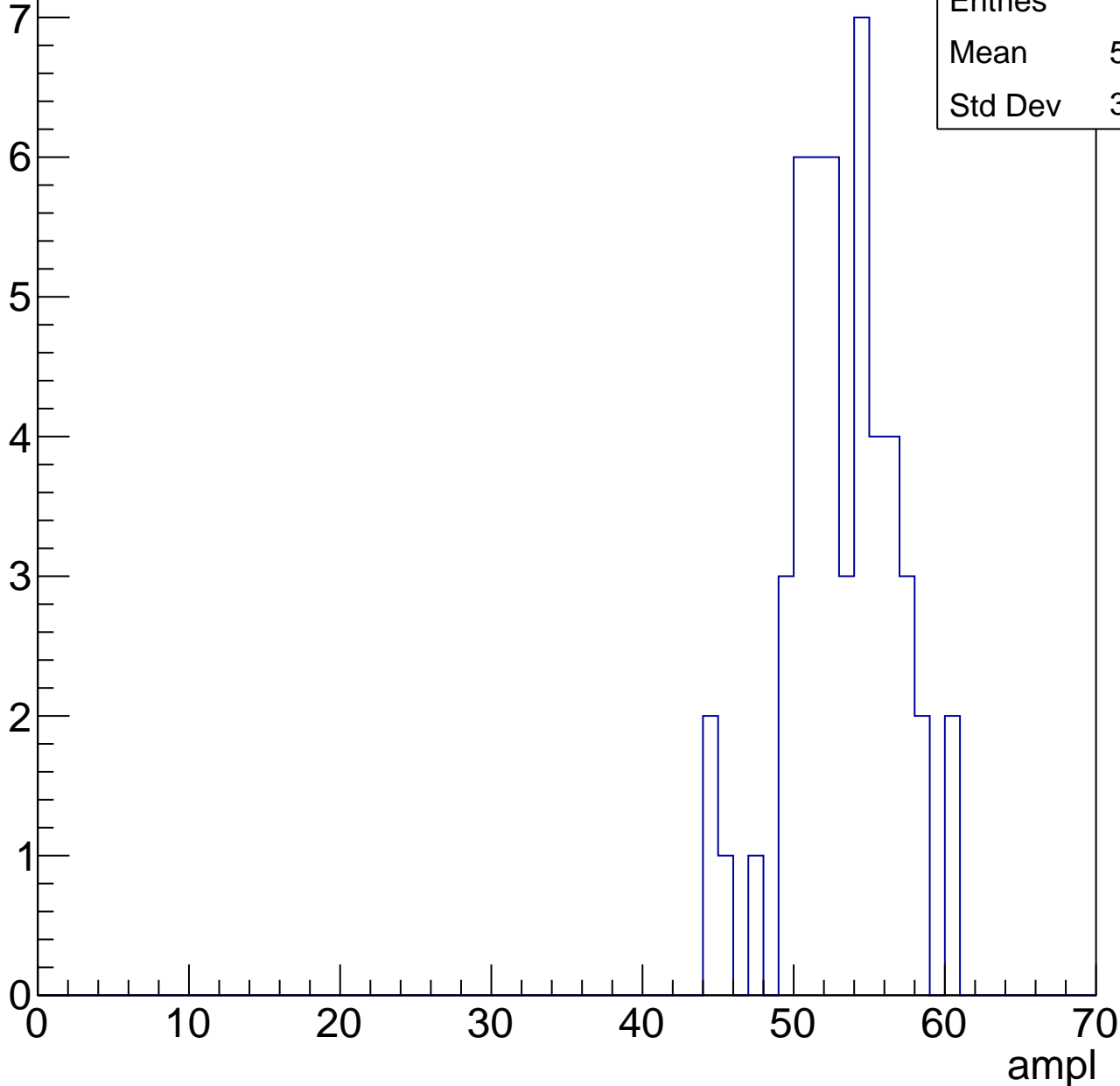


B1L102S, U14-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	52.66
Std Dev	3.587

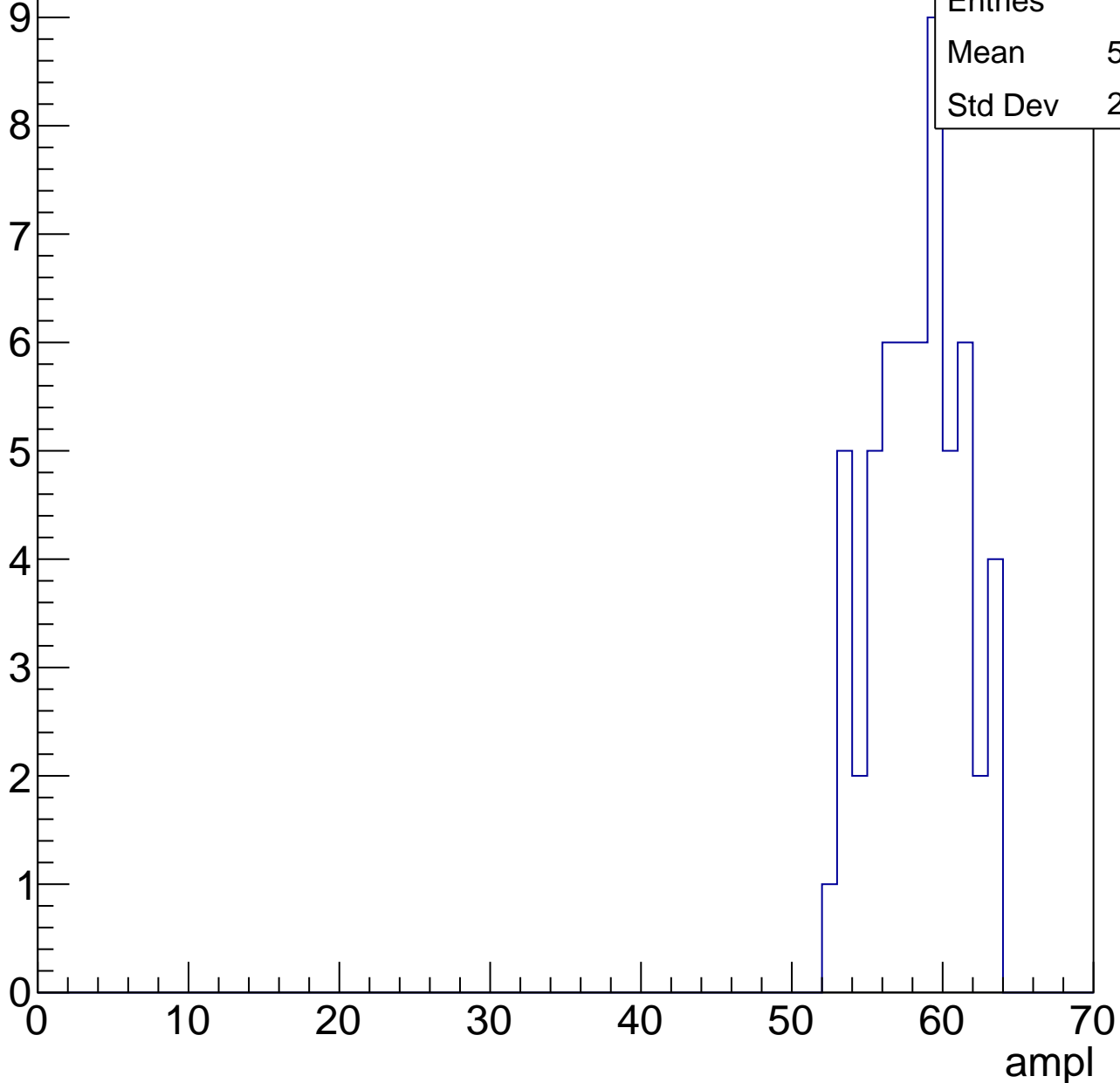


B1L102S, U14-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.88
Std Dev	2.908

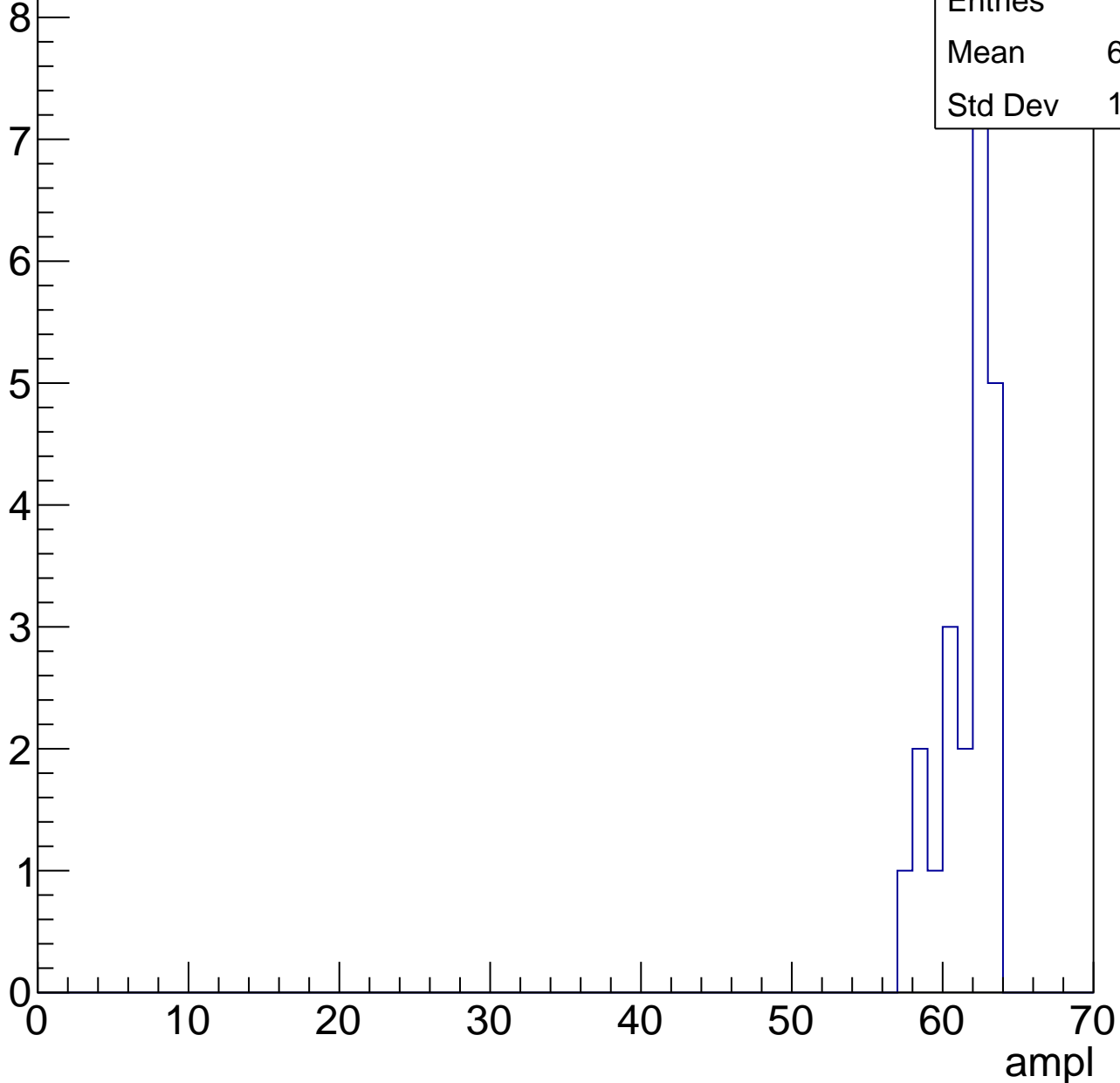


B1L102S, U14-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	61.14
Std Dev	1.766



B1L102S, U14-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

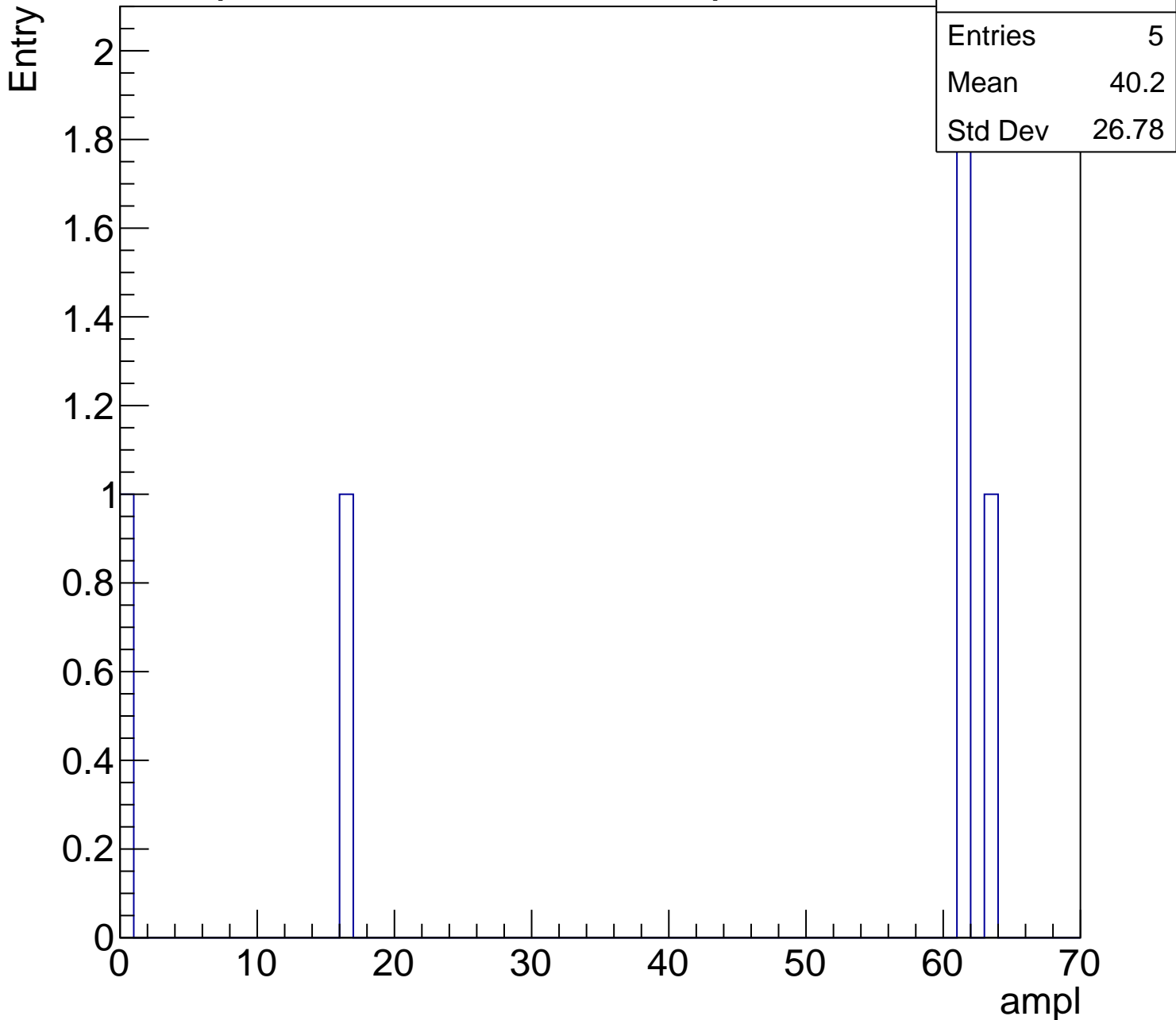
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	40.2
Std Dev	26.78

0 10 20 30 40 50 60 70

ampl



B1L102S, U14-ch103, adc0

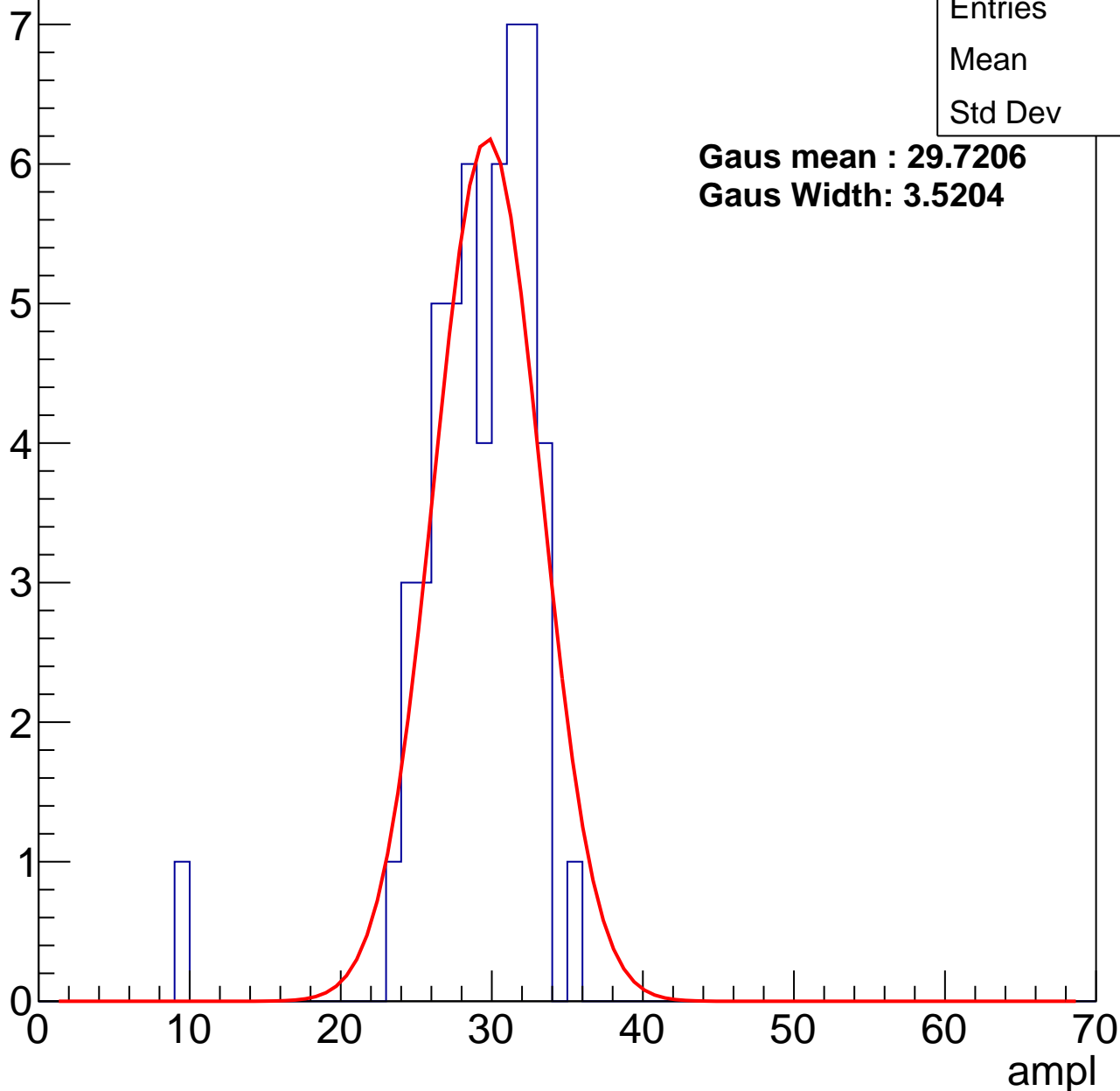
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	28.6
Std Dev	3.93

Gaus mean : 29.7206

Gaus Width: 3.5204



B1L102S, U14-ch103, adc1

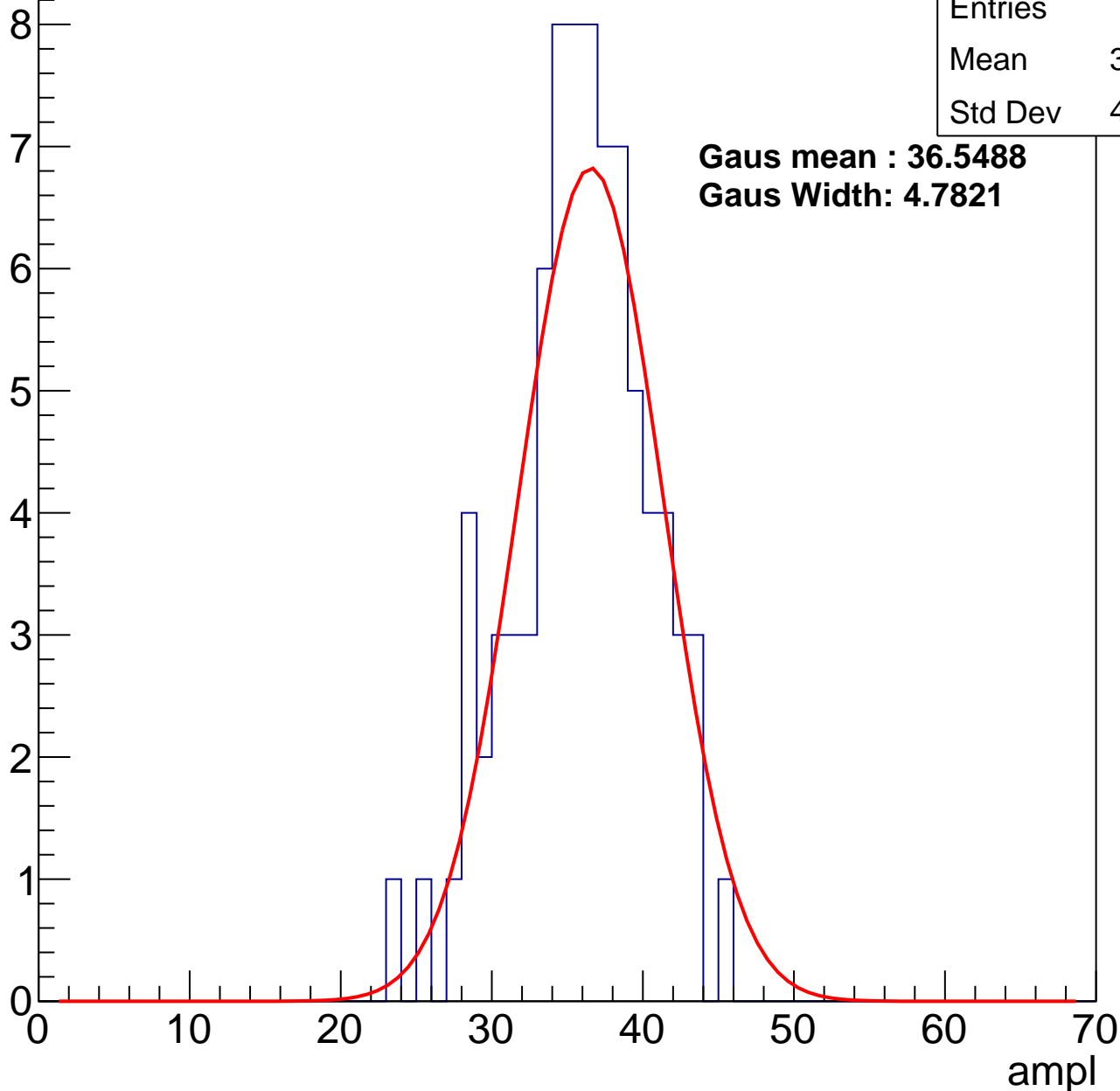
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.44
Std Dev	4.423

Gaus mean : 36.5488

Gaus Width: 4.7821



B1L102S, U14-ch103, adc2

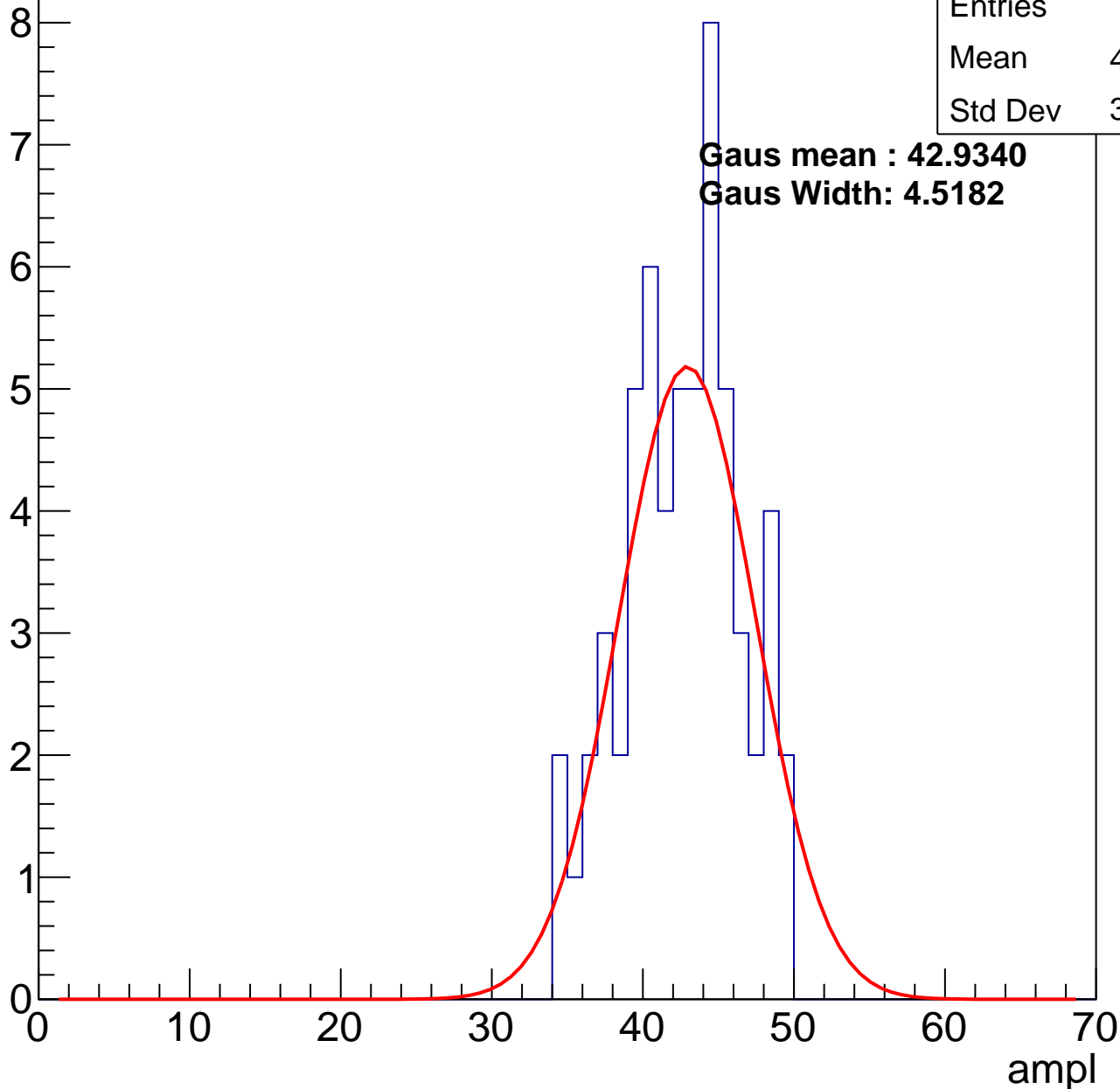
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.12
Std Dev	3.796

Gaus mean : 42.9340

Gaus Width: 4.5182

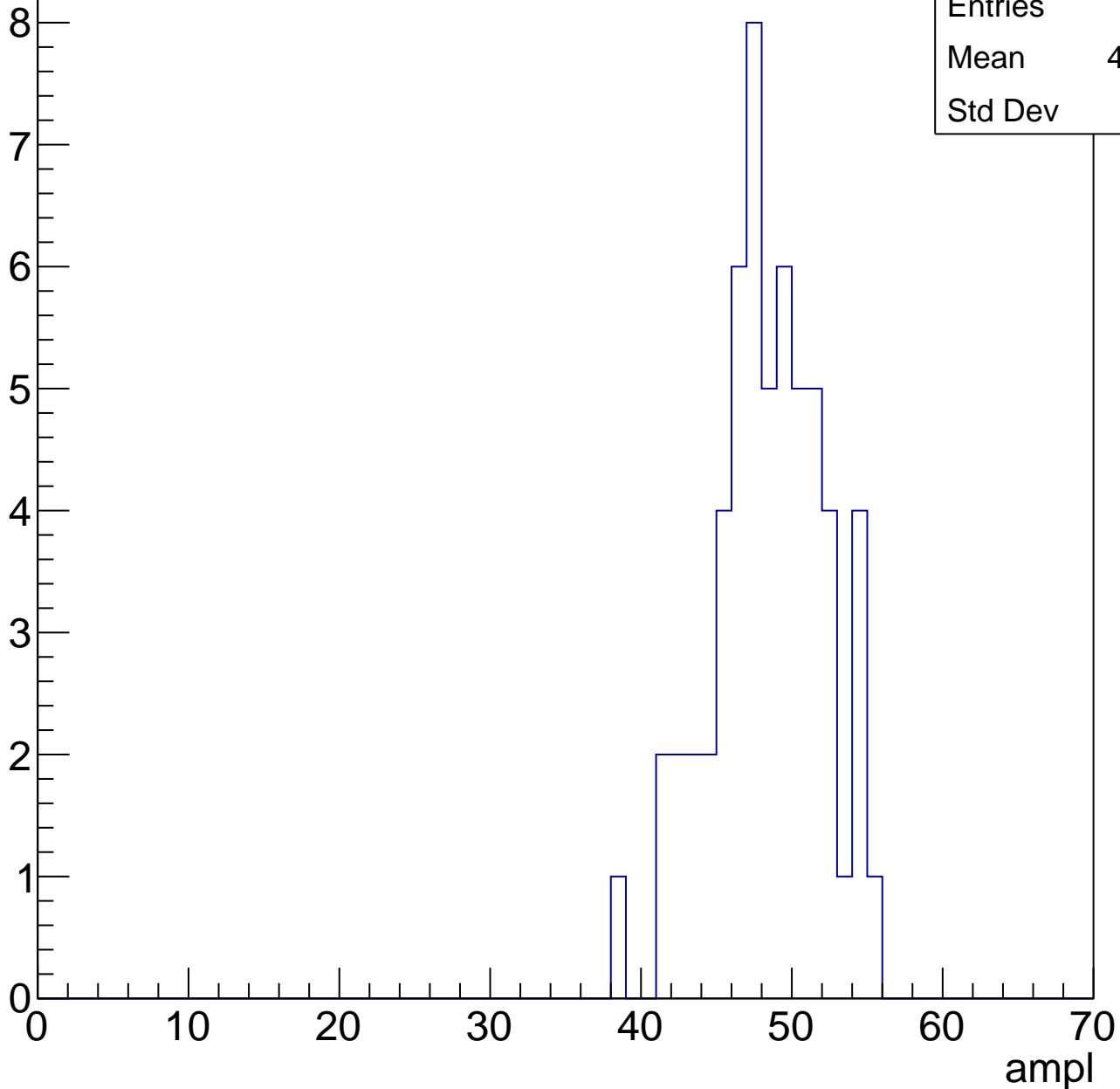


B1L102S, U14-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	47.95
Std Dev	3.65

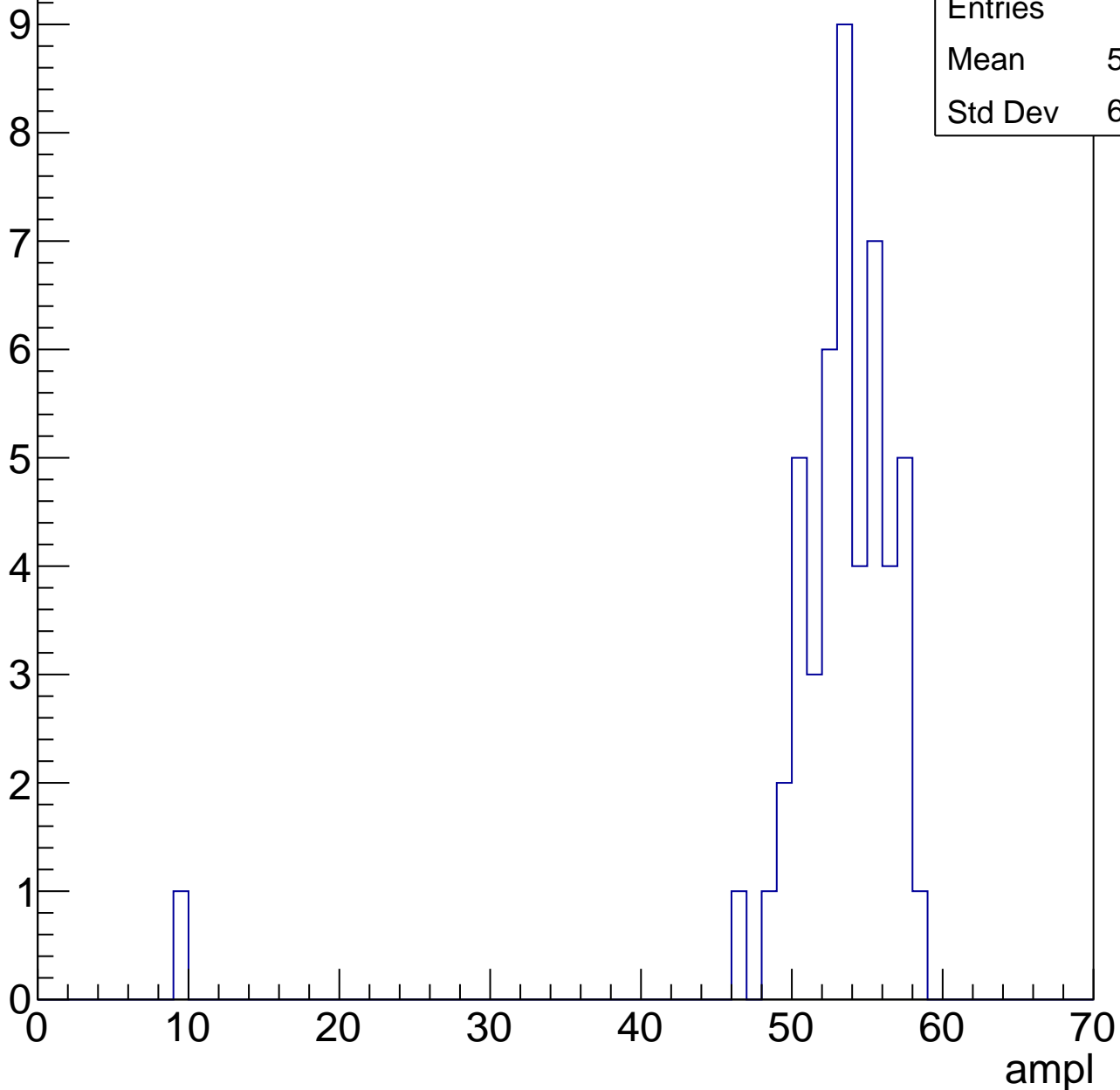


B1L102S, U14-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	52.27
Std Dev	6.779



B1L102S, U14-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	58.28
Std Dev	7.56

Entry

10

8

6

4

2

0

0

10

20

30

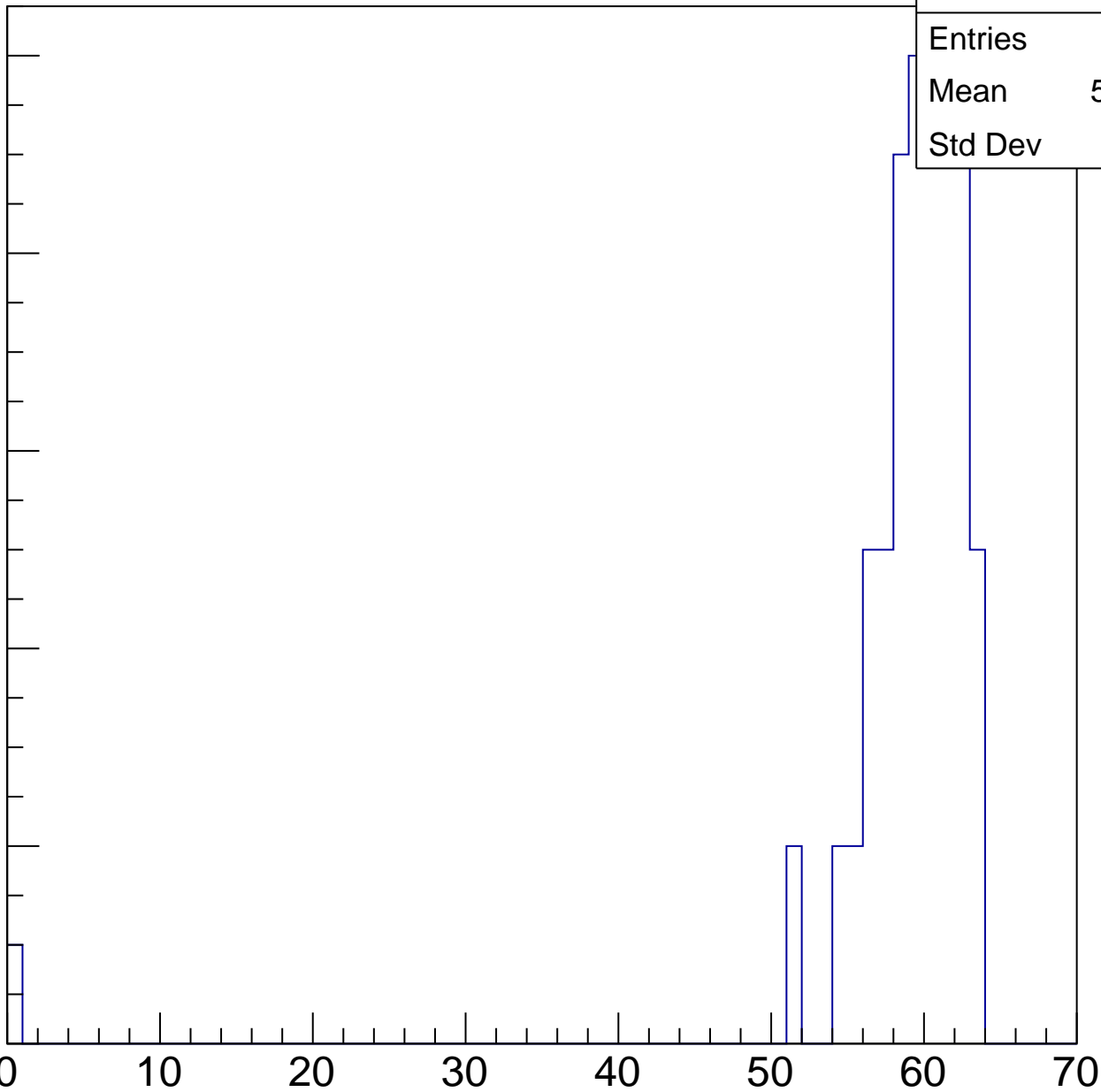
40

50

60

ampl

70

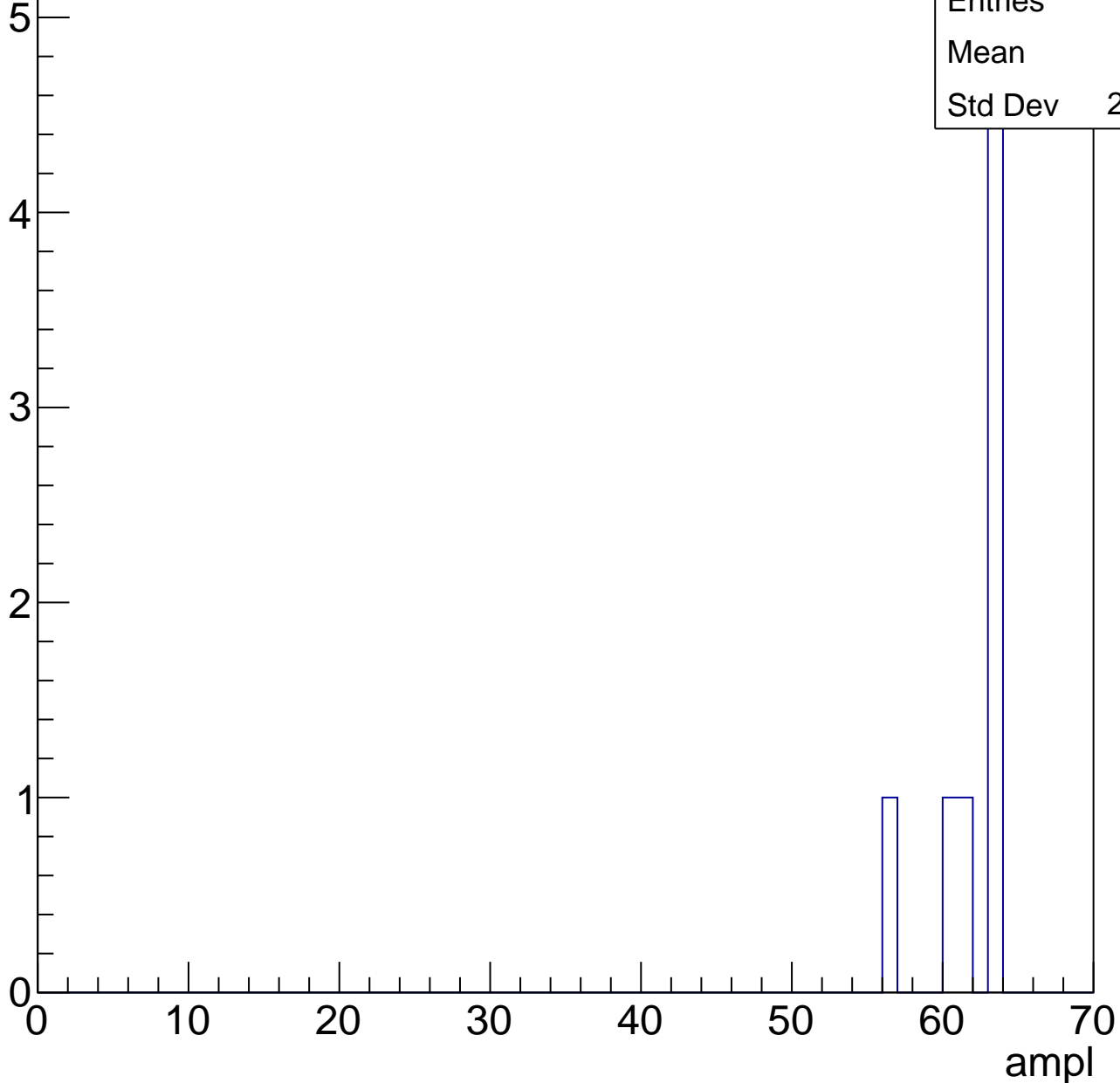


B1L102S, U14-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	61.5
Std Dev	2.345



B1L102S, U14-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch104, adc0

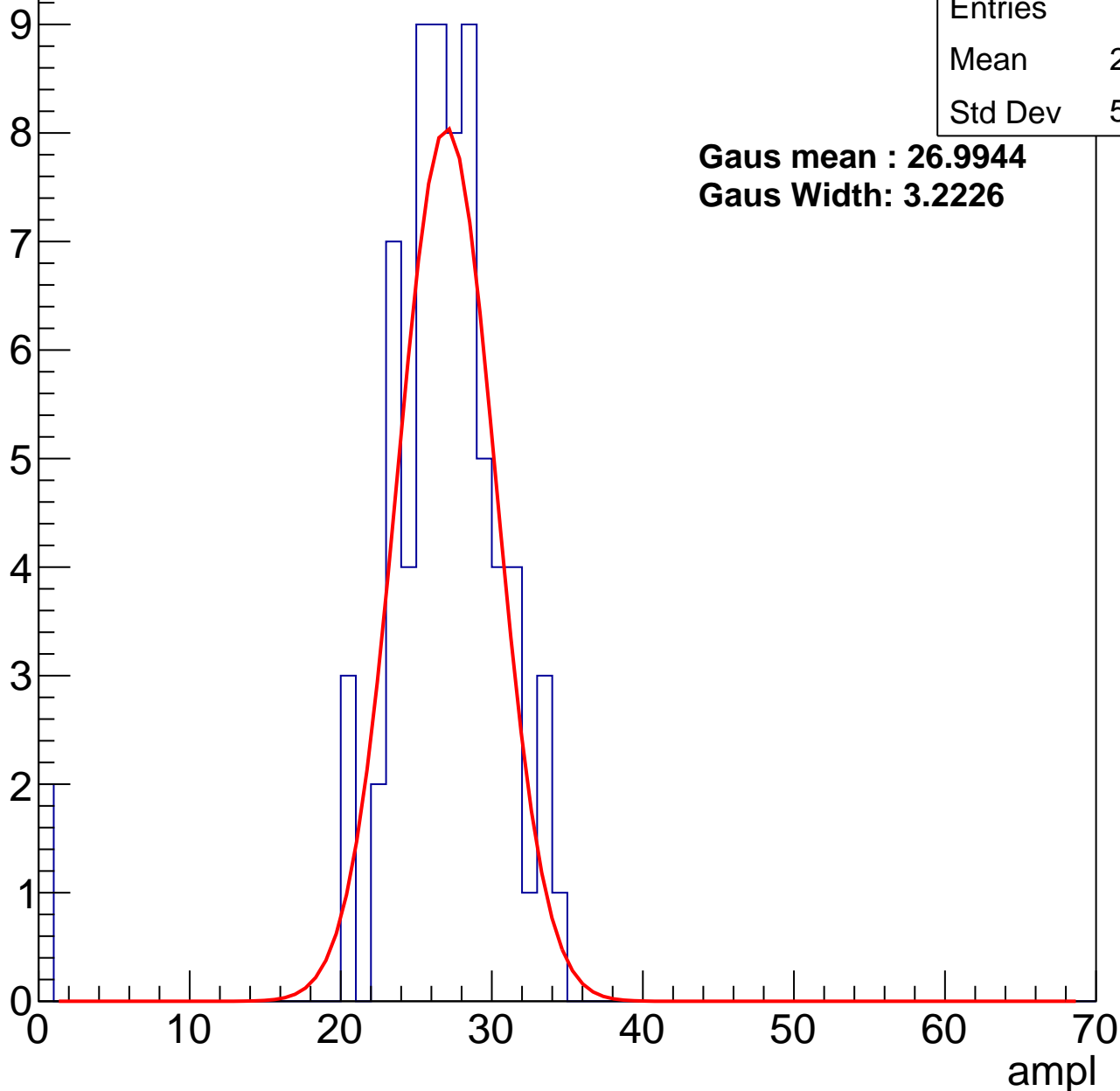
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	25.94
Std Dev	5.412

Gaus mean : 26.9944

Gaus Width: 3.2226



B1L102S, U14-ch104, adc1

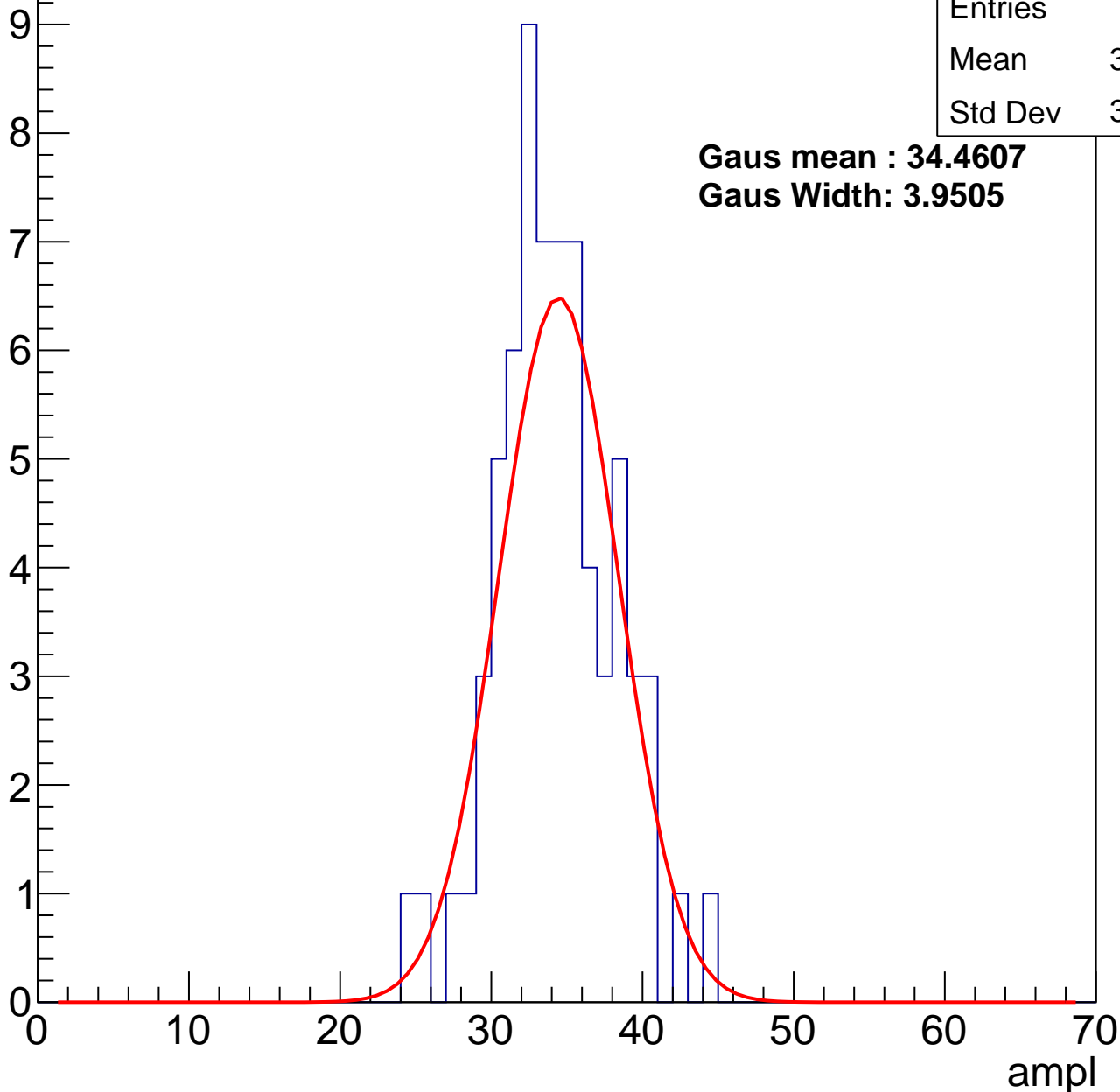
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	33.78
Std Dev	3.819

Gaus mean : 34.4607

Gaus Width: 3.9505



B1L102S, U14-ch104, adc2

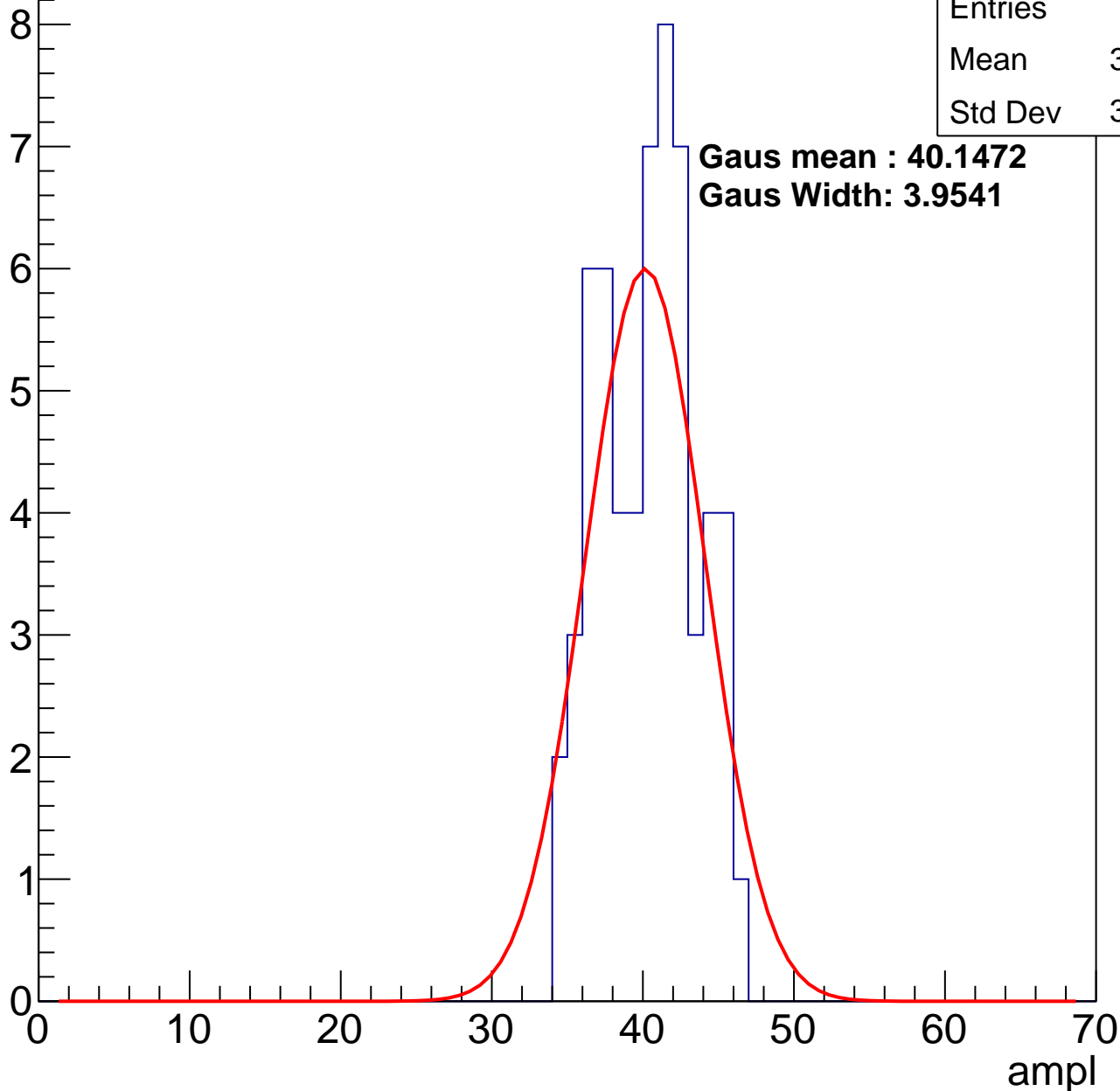
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	39.86
Std Dev	3.132

Gaus mean : 40.1472

Gaus Width: 3.9541

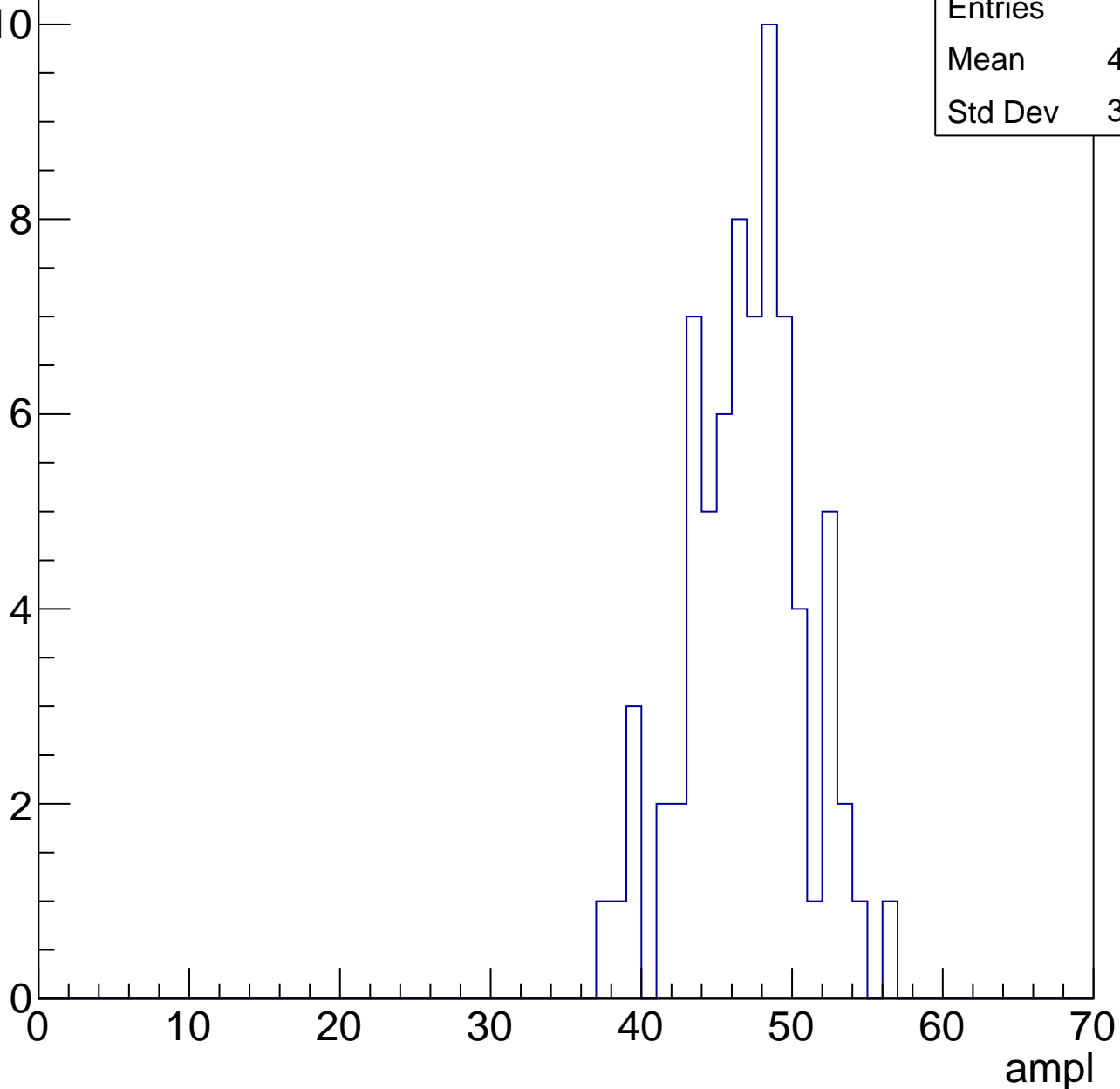


B1L102S, U14-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	46.52
Std Dev	3.854

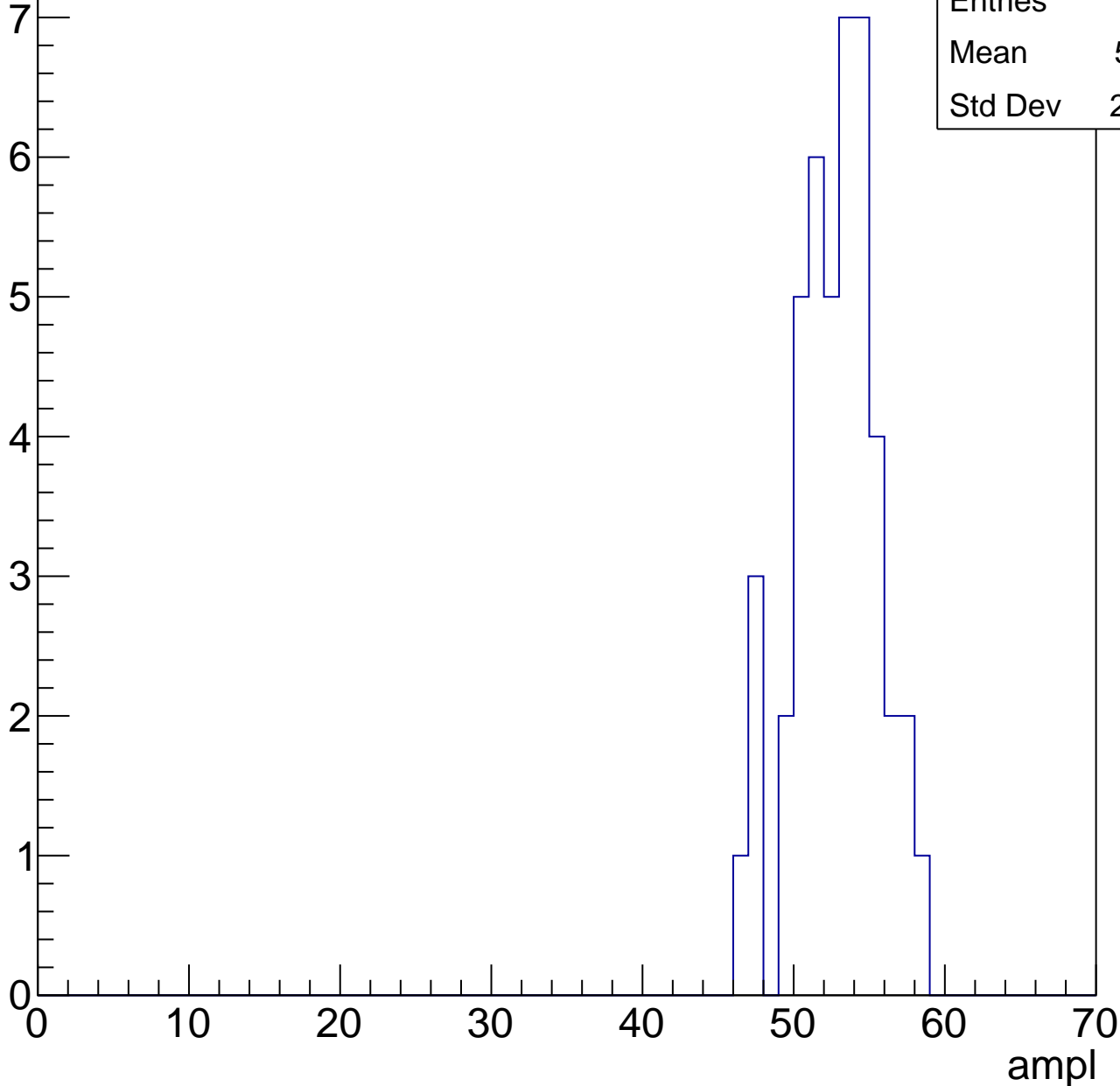


B1L102S, U14-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	52.31
Std Dev	2.747

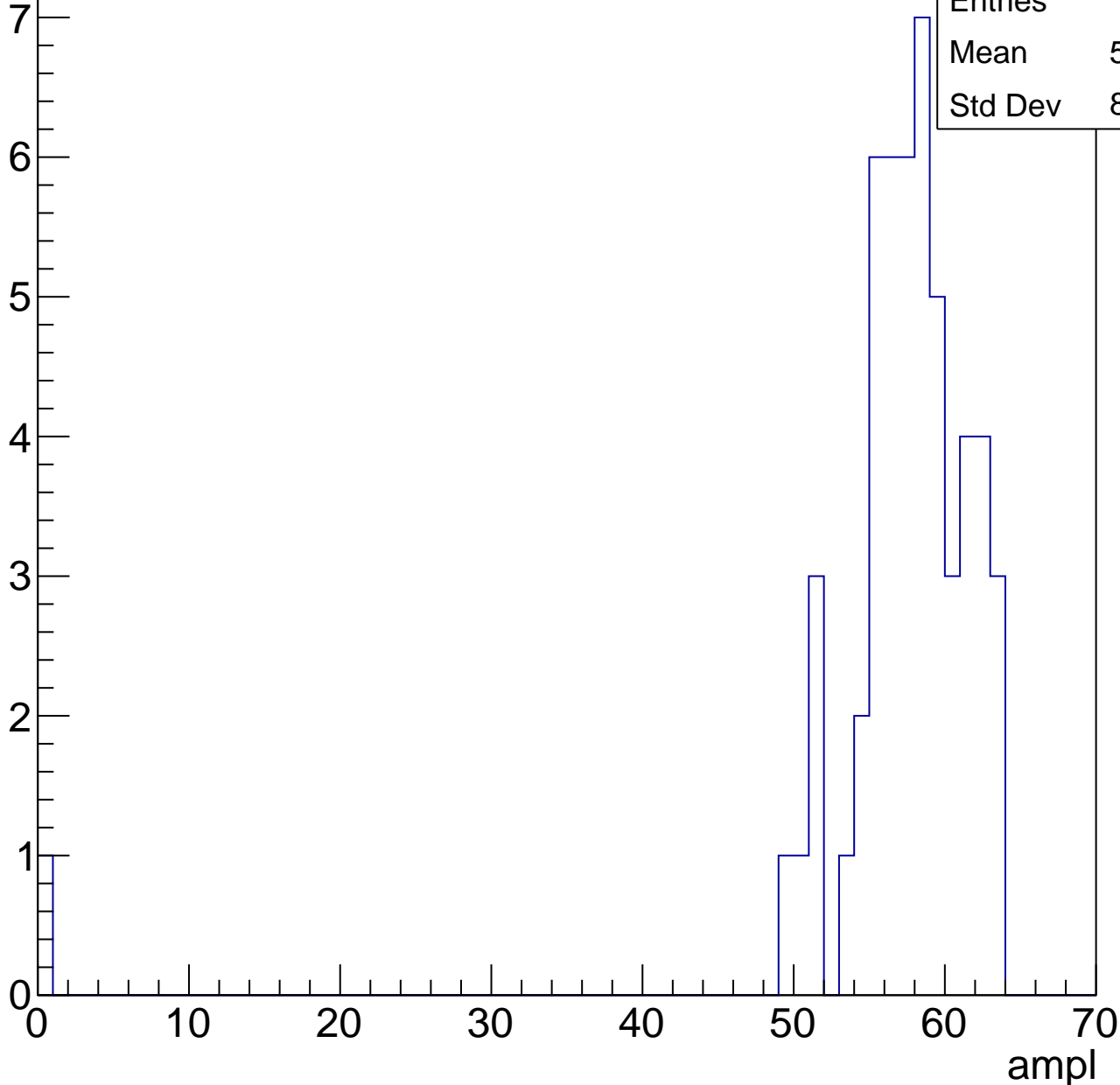


B1L102S, U14-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

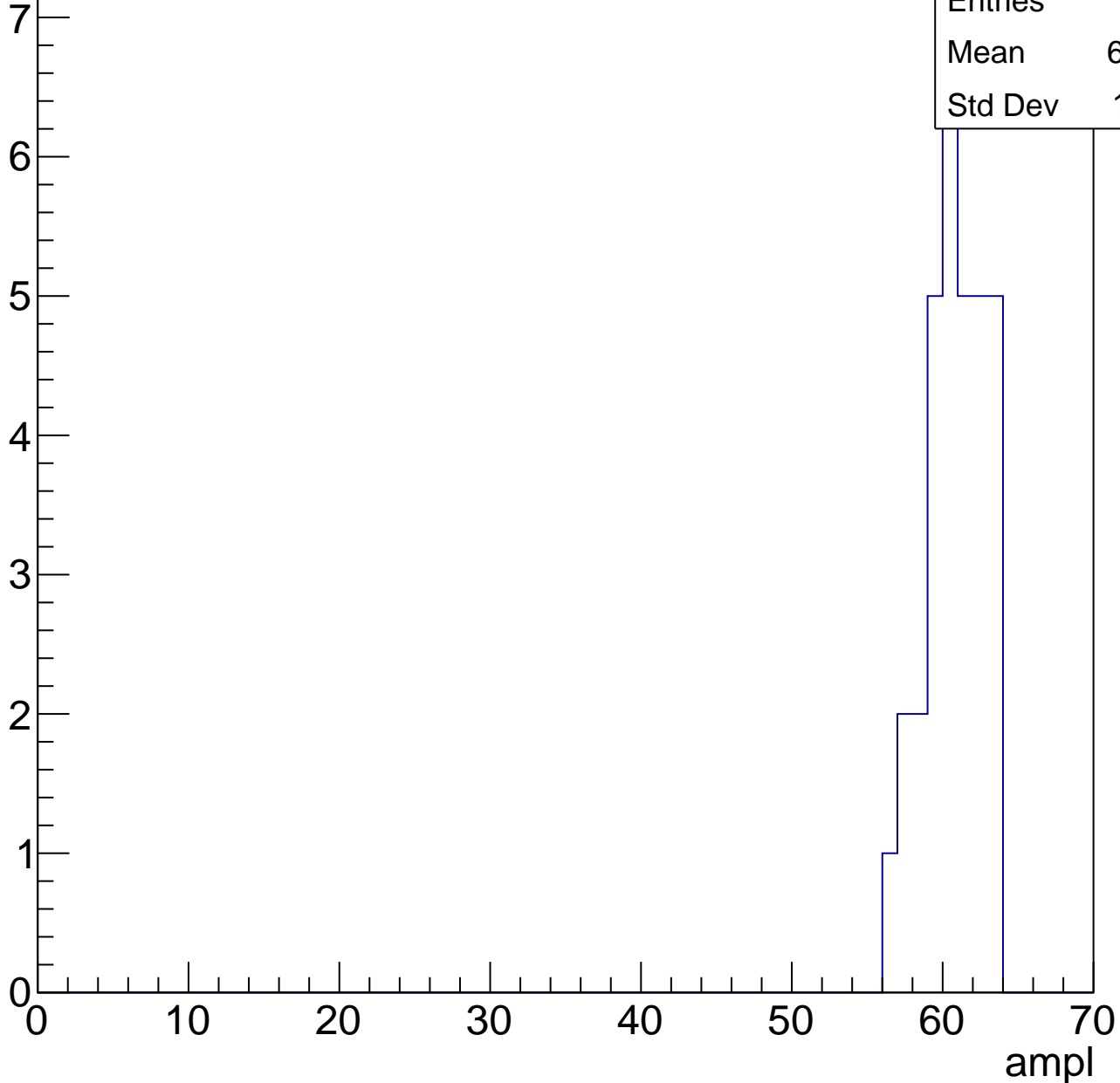
Entries	53
Mean	56.28
Std Dev	8.504



B1L102S, U14-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

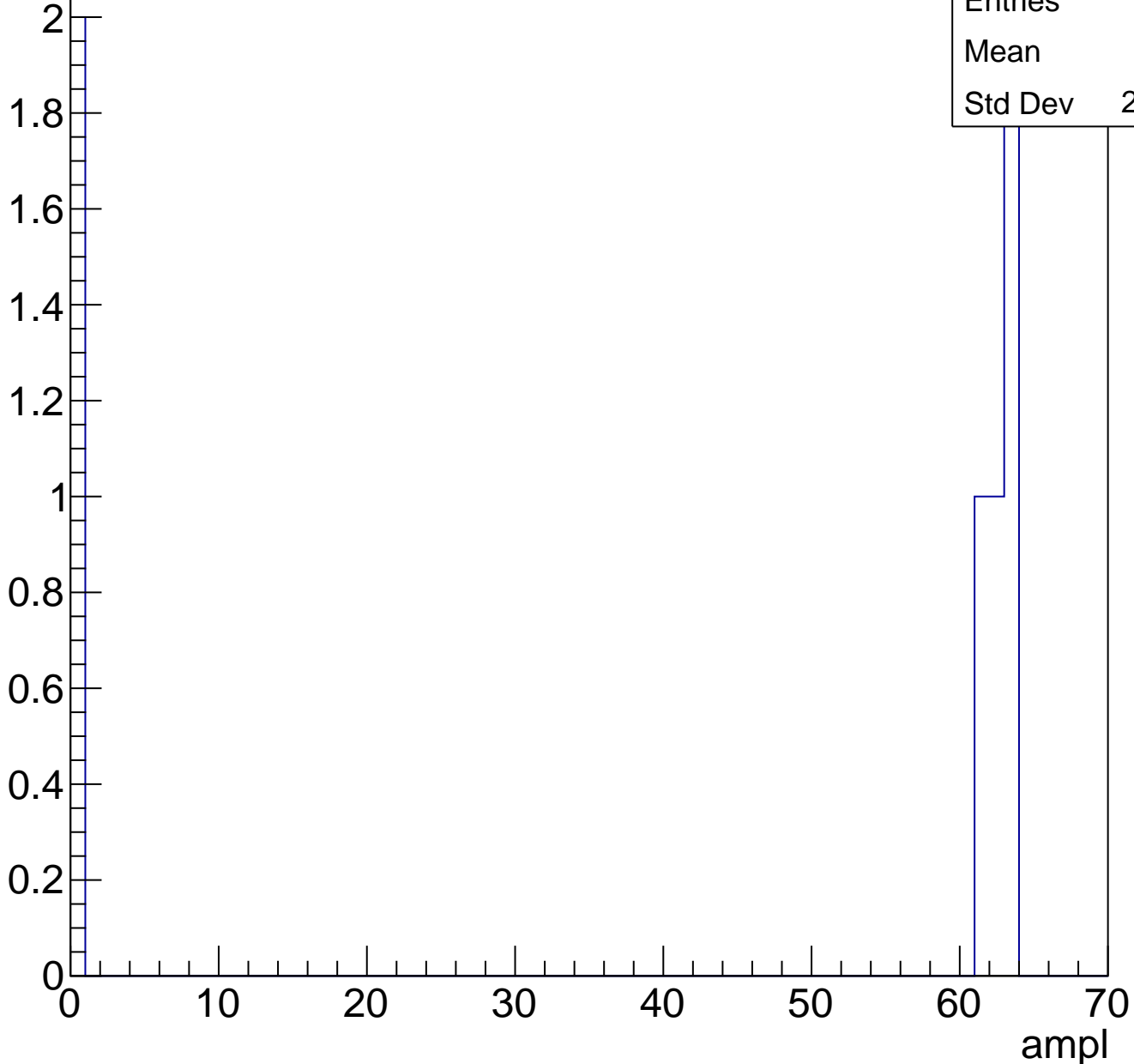
Entry



B1L102S, U14-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch105, adc0

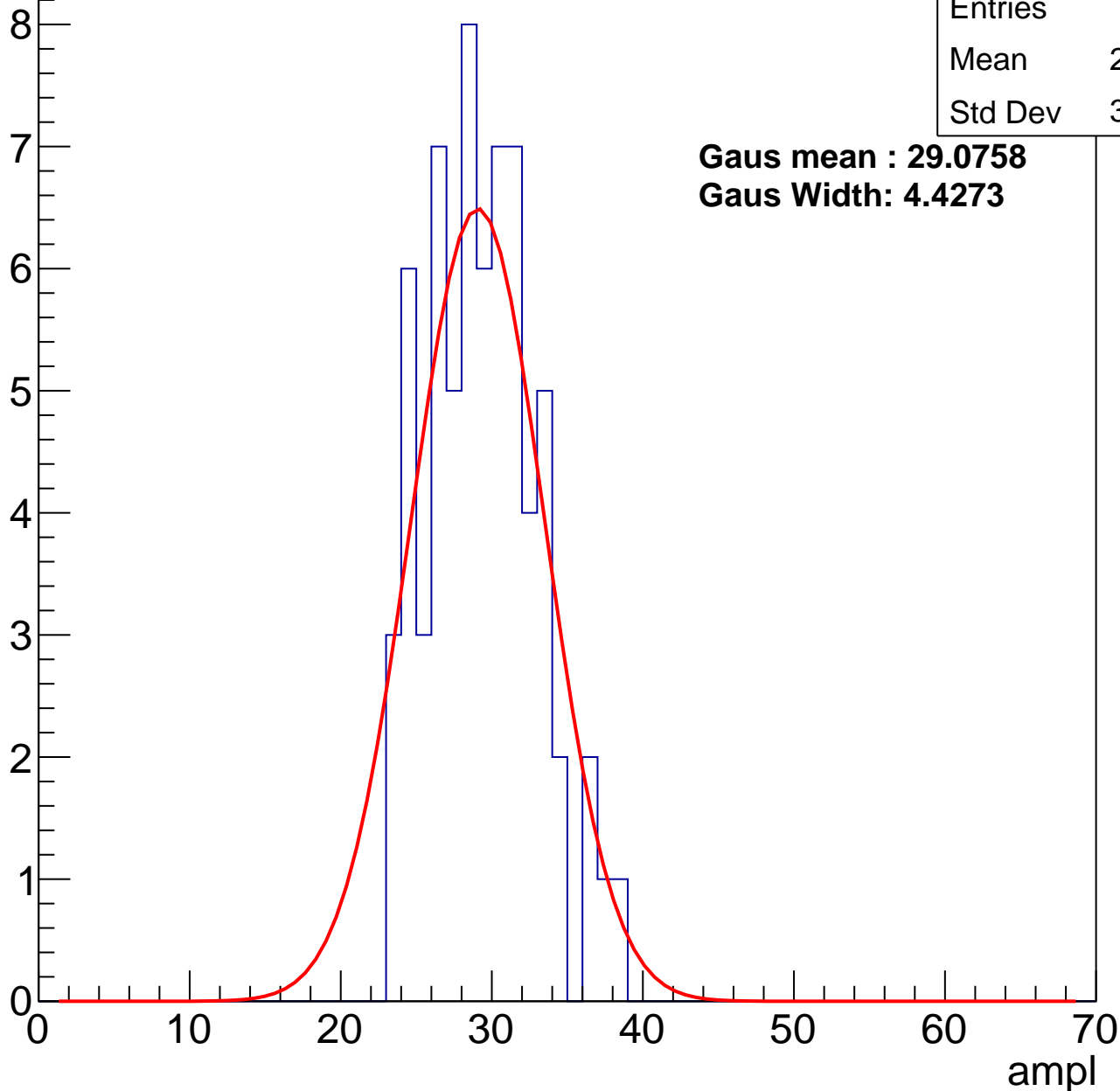
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.93
Std Dev	3.542

Gaus mean : 29.0758

Gaus Width: 4.4273



B1L102S, U14-ch105, adc1

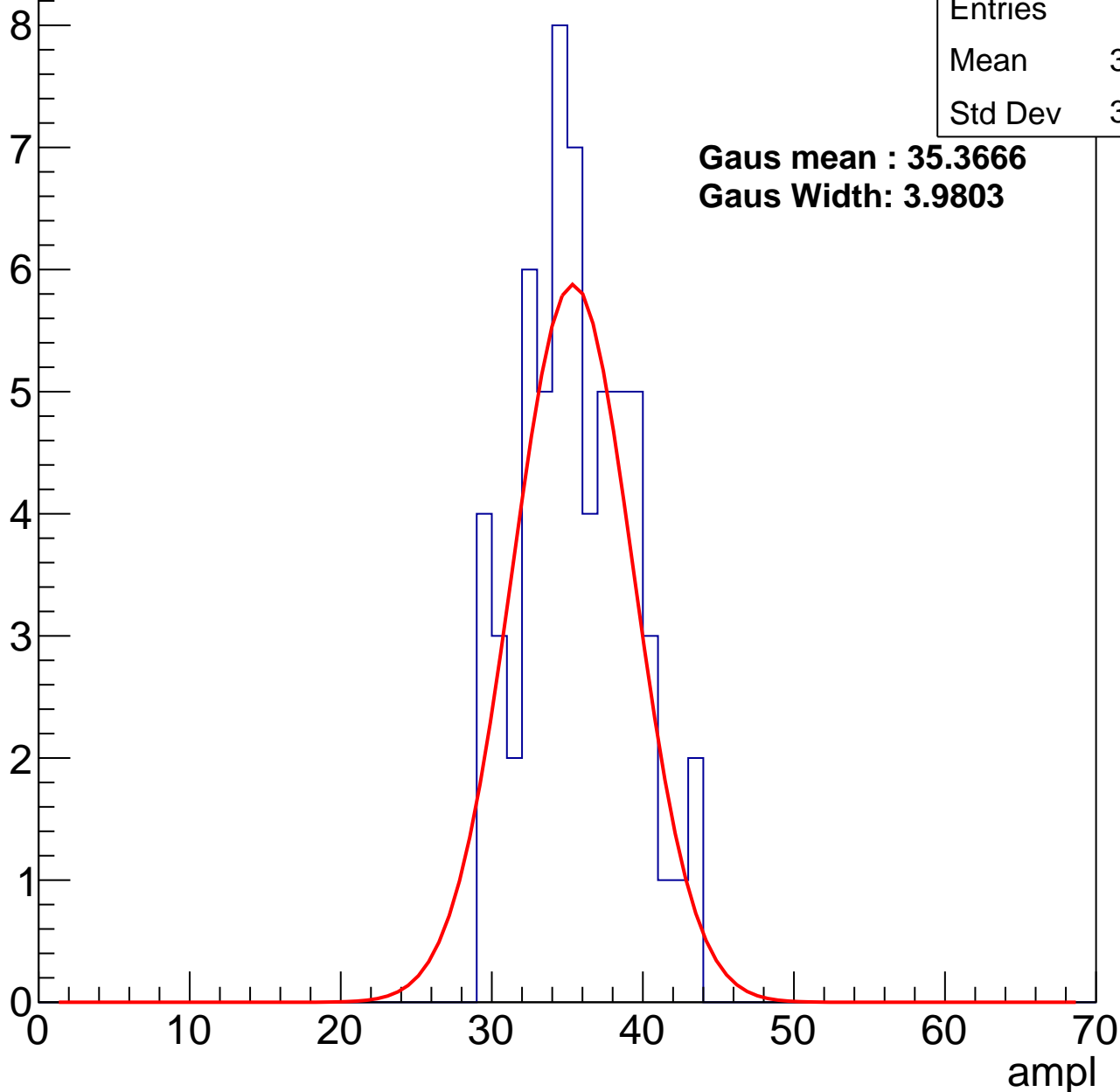
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.16
Std Dev	3.549

Gaus mean : 35.3666

Gaus Width: 3.9803

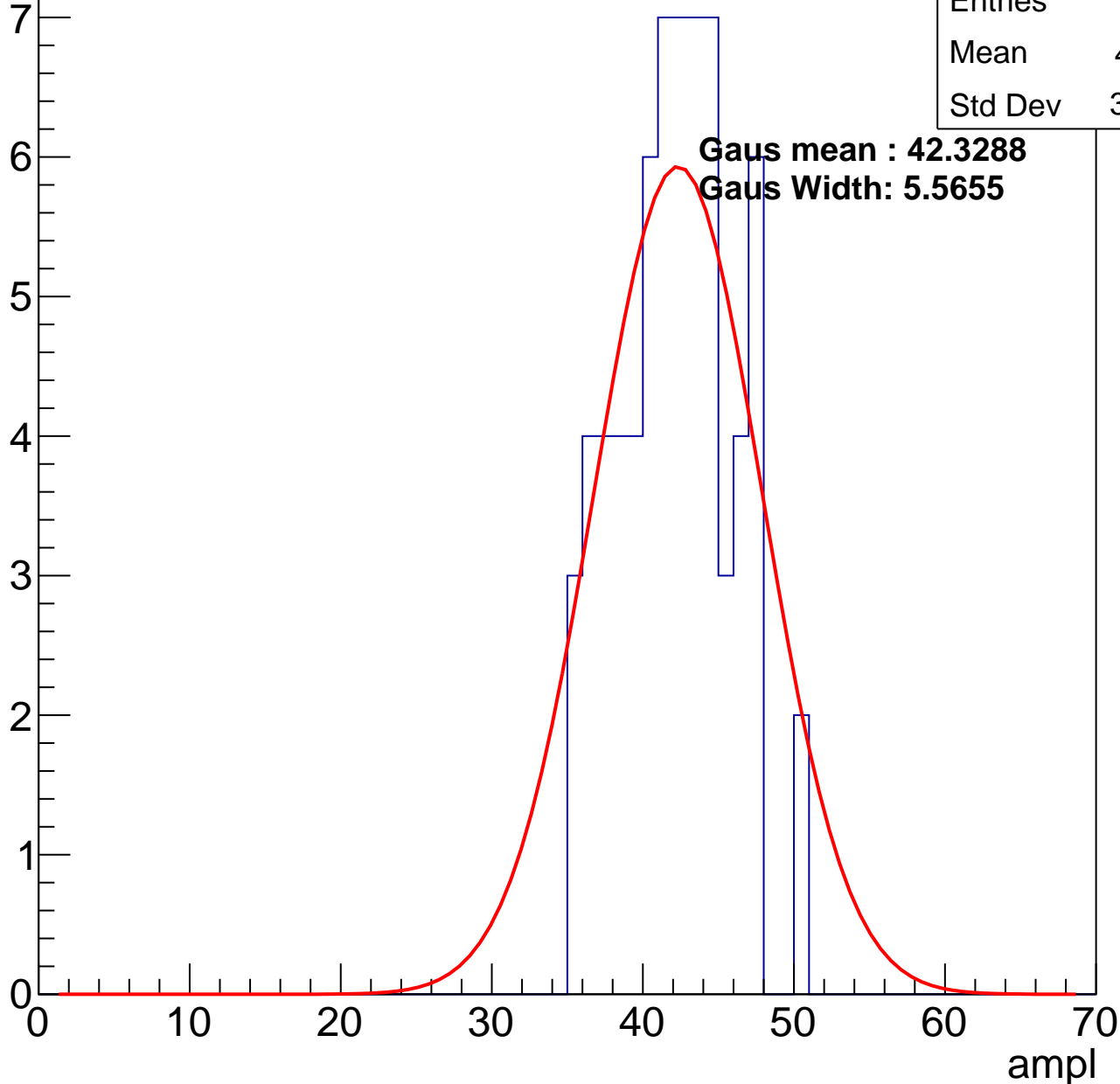


B1L102S, U14-ch105, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.71
Std Dev	3.678

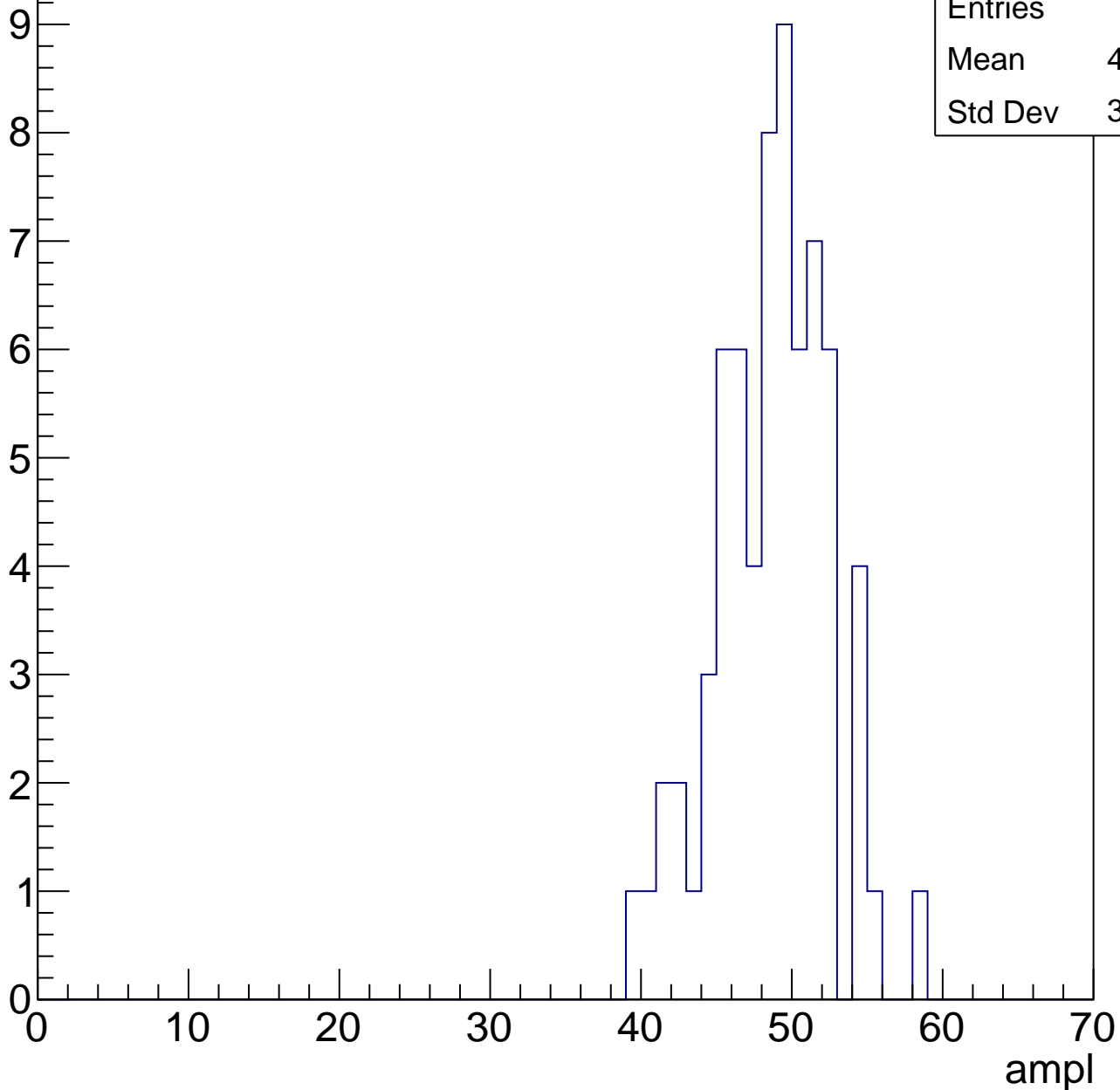


B1L102S, U14-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.19
Std Dev	3.743

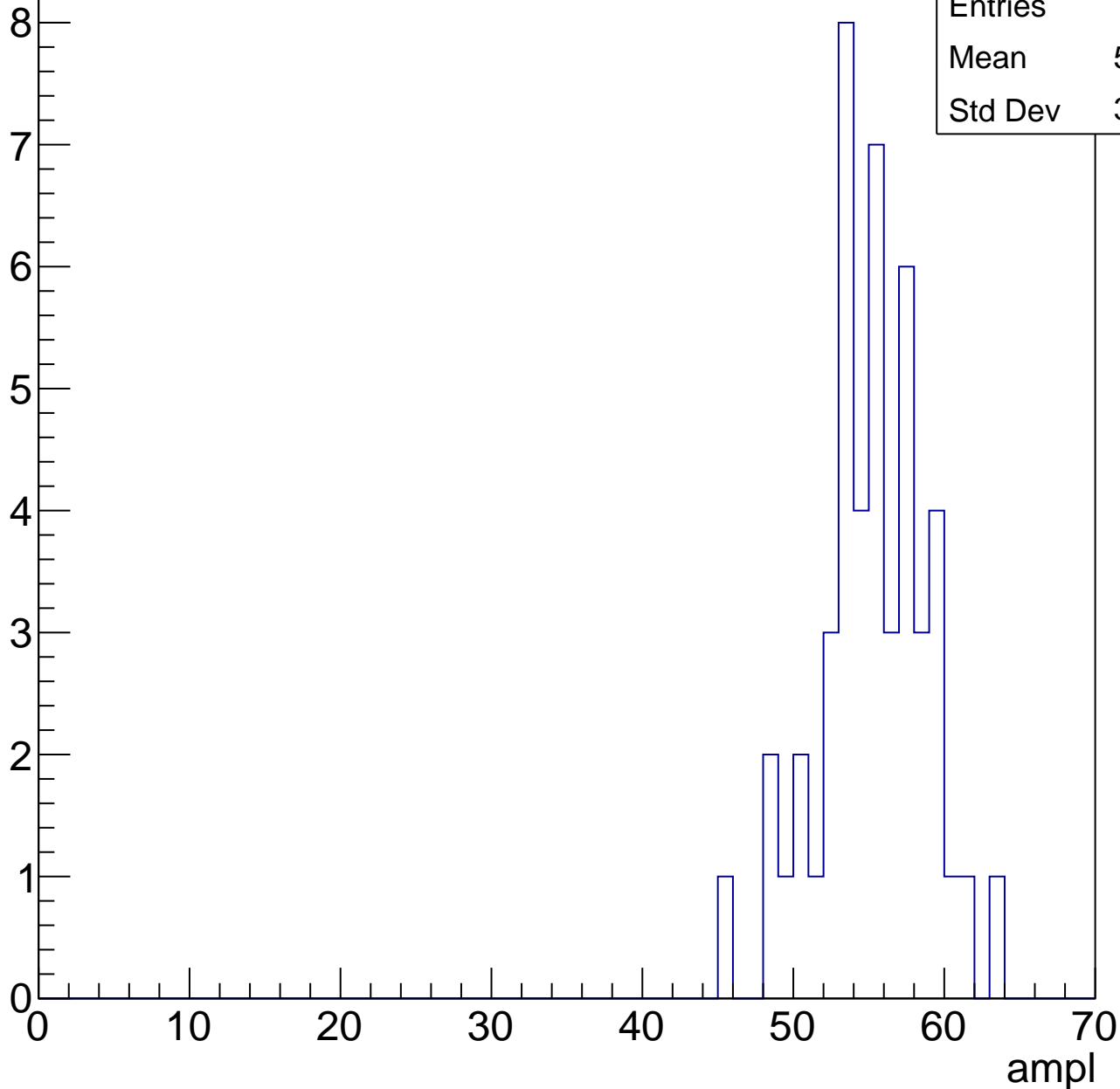


B1L102S, U14-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

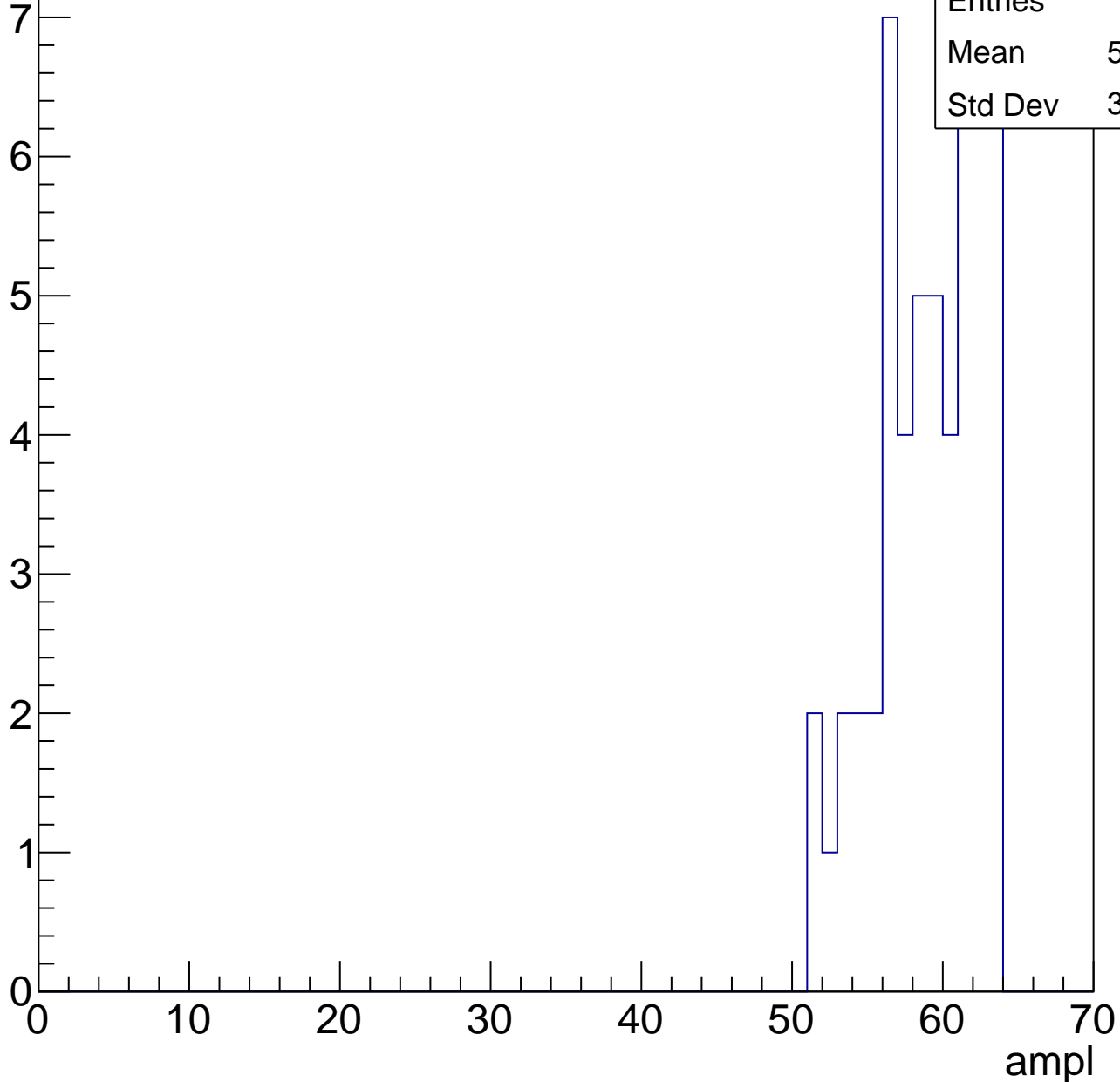
Entries	48
Mean	54.71
Std Dev	3.541



B1L102S, U14-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



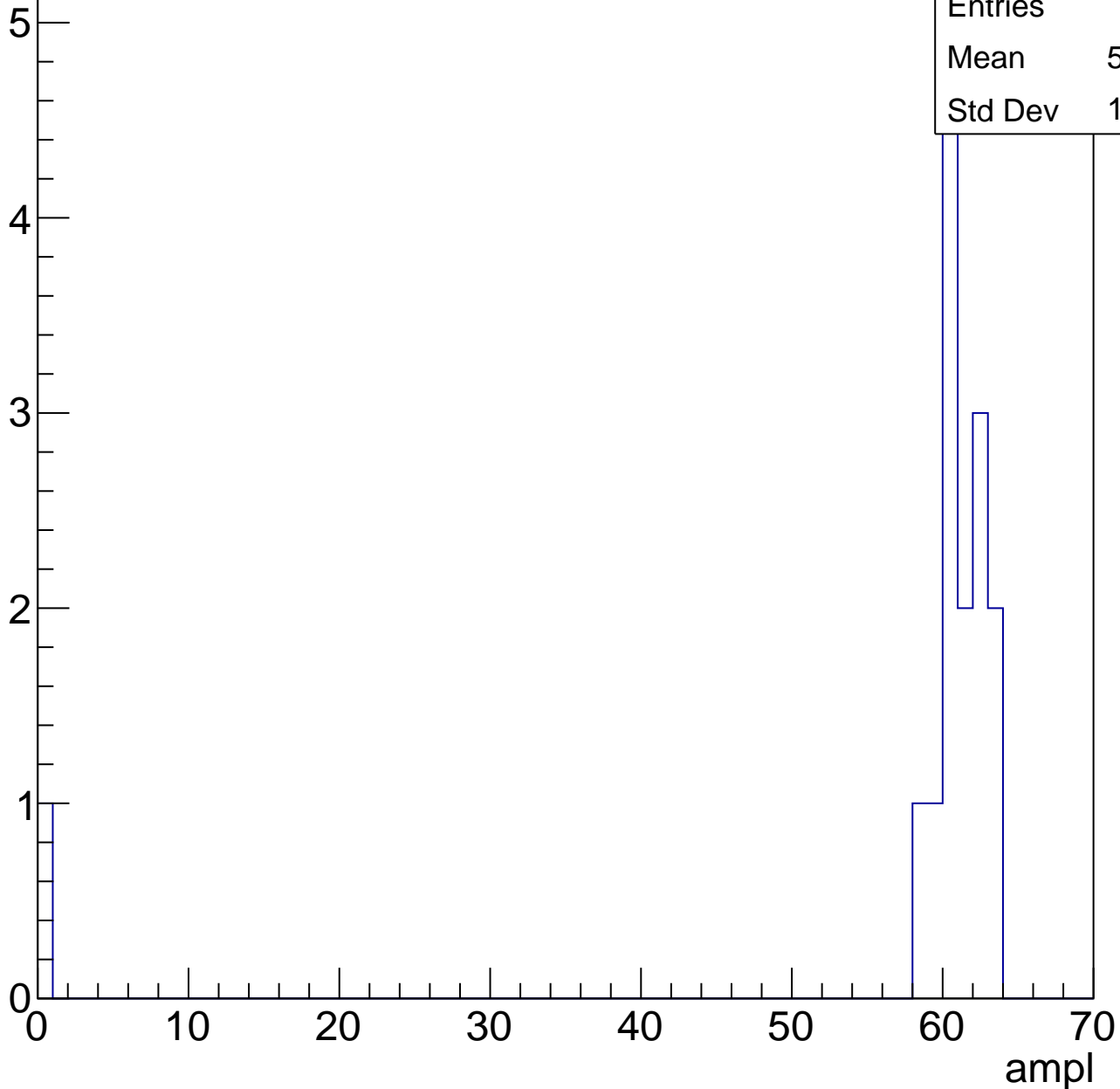
Entries	55
Mean	58.64
Std Dev	3.343

B1L102S, U14-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	56.73
Std Dev	15.22



B1L102S, U14-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch106, adc0

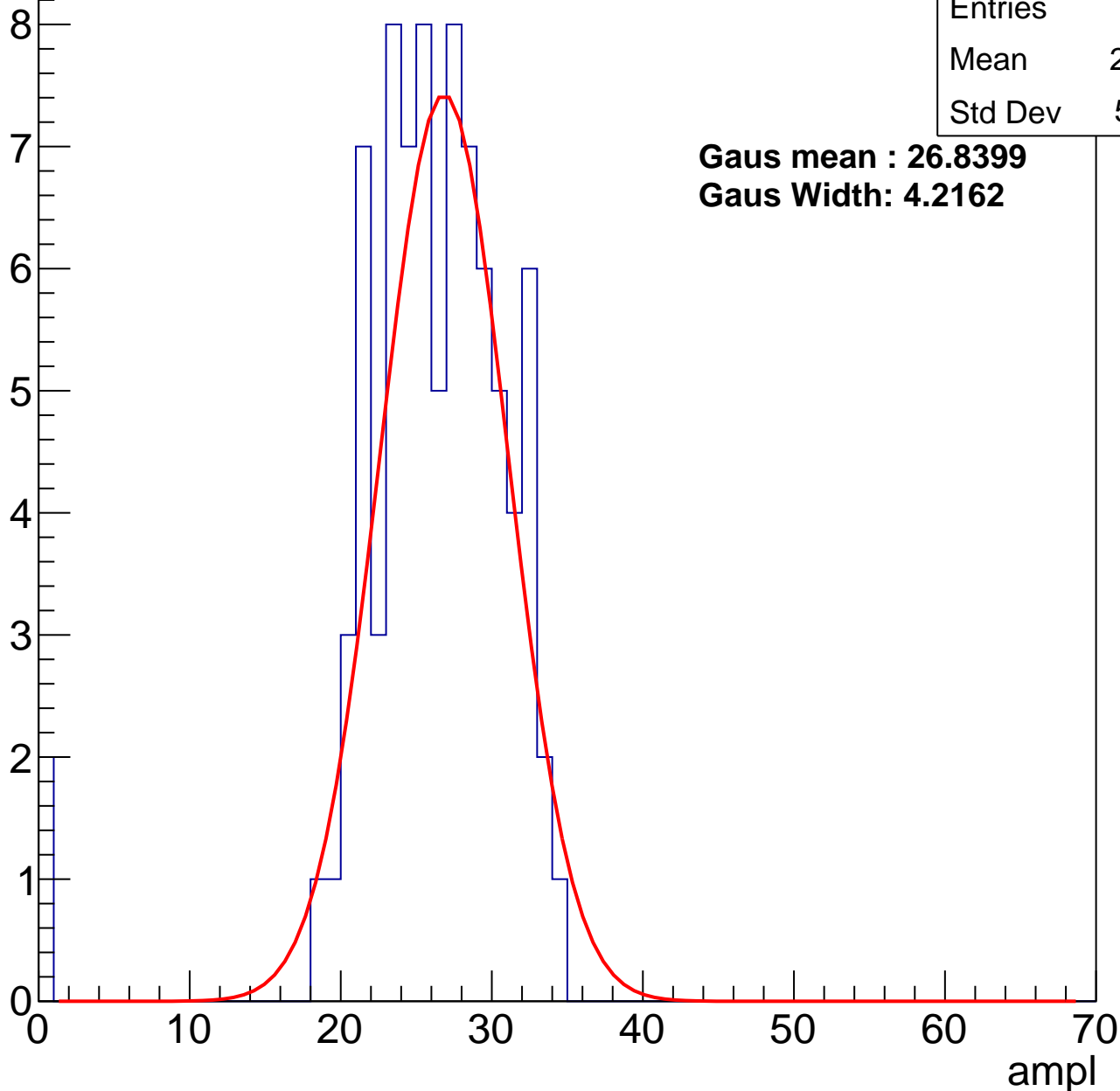
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	25.52
Std Dev	5.491

Gaus mean : 26.8399

Gaus Width: 4.2162



B1L102S, U14-ch106, adc1

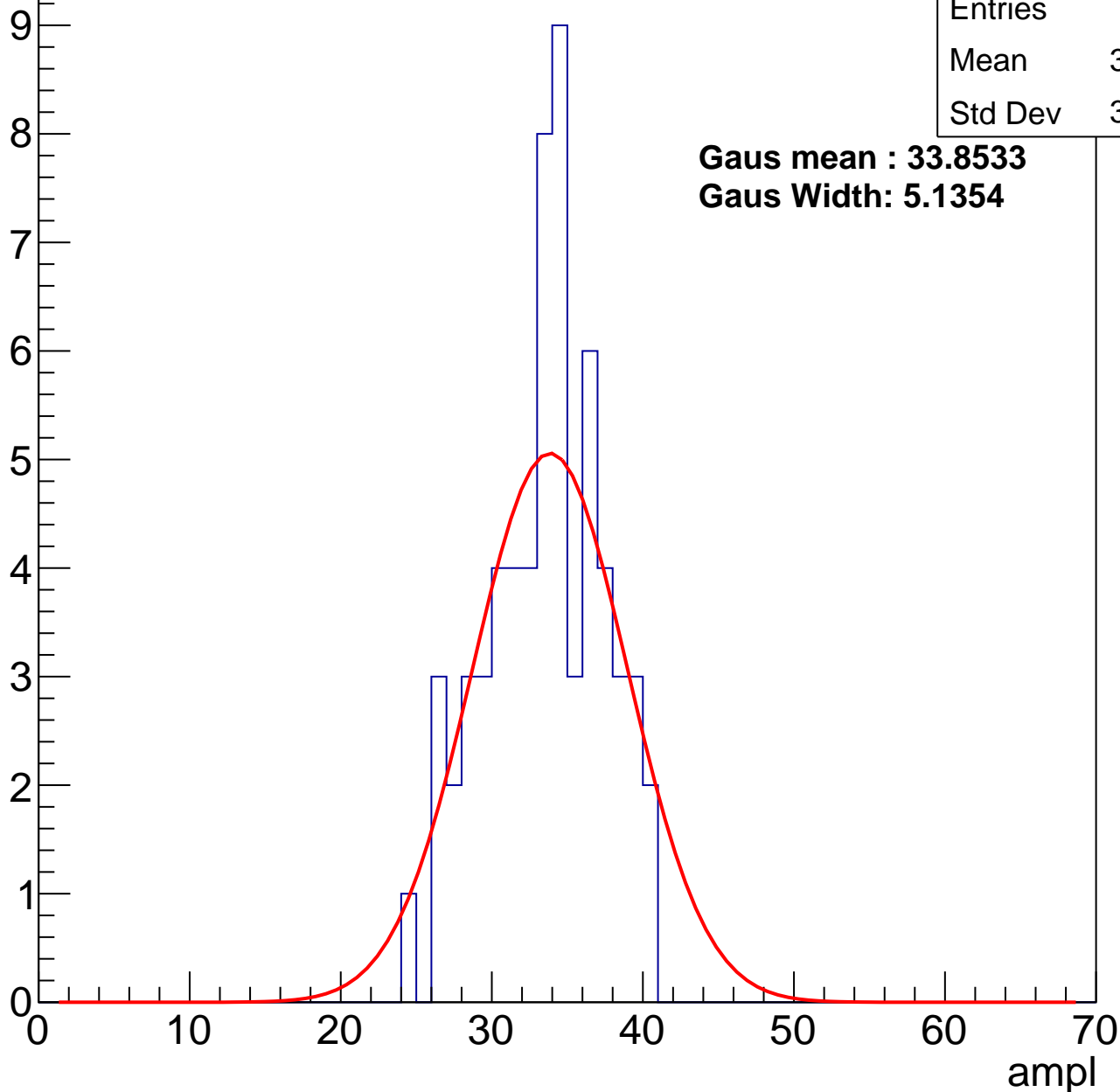
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	33.05
Std Dev	3.808

Gaus mean : 33.8533

Gaus Width: 5.1354



B1L102S, U14-ch106, adc2

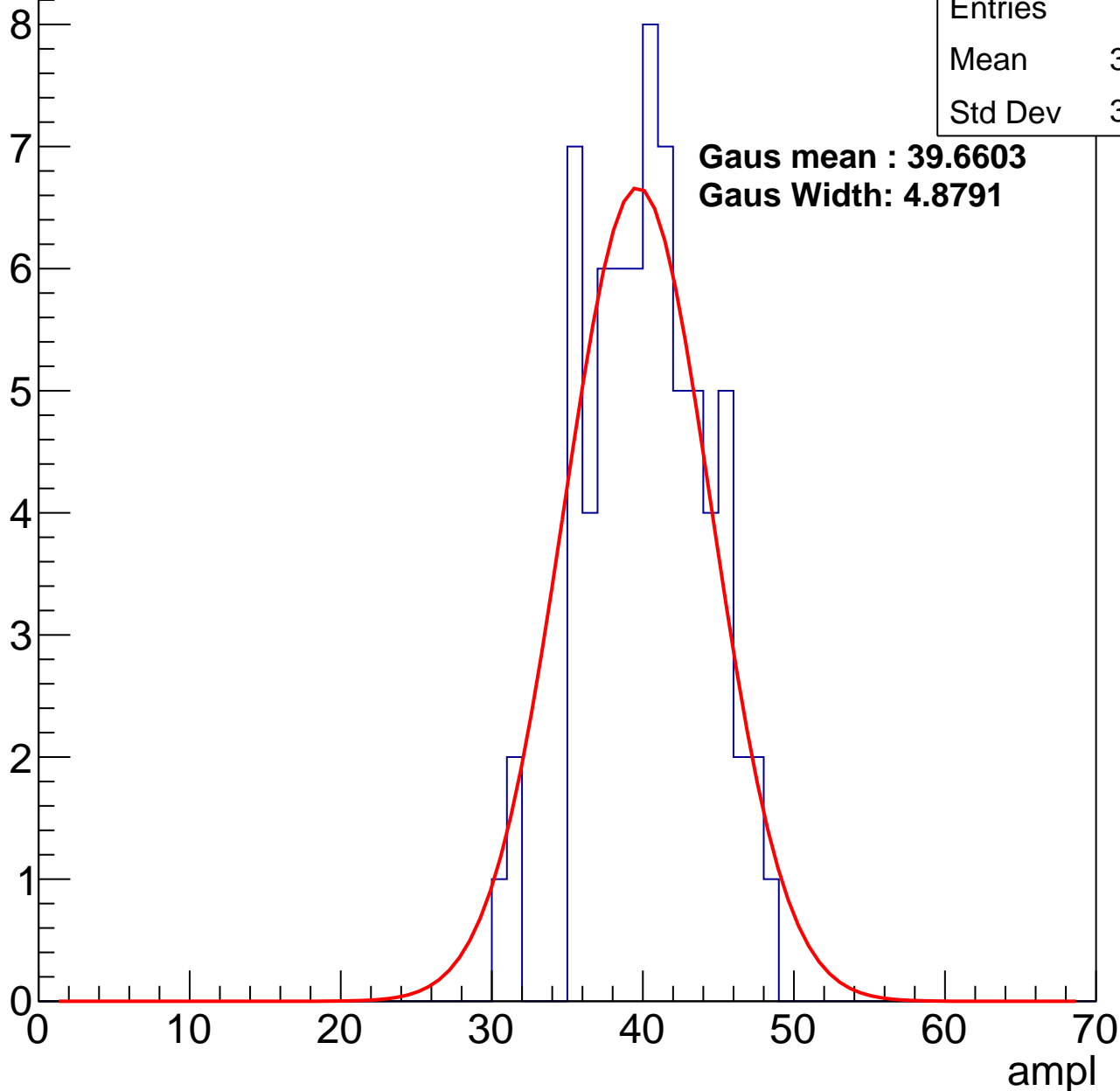
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	39.89
Std Dev	3.899

Gaus mean : 39.6603

Gaus Width: 4.8791

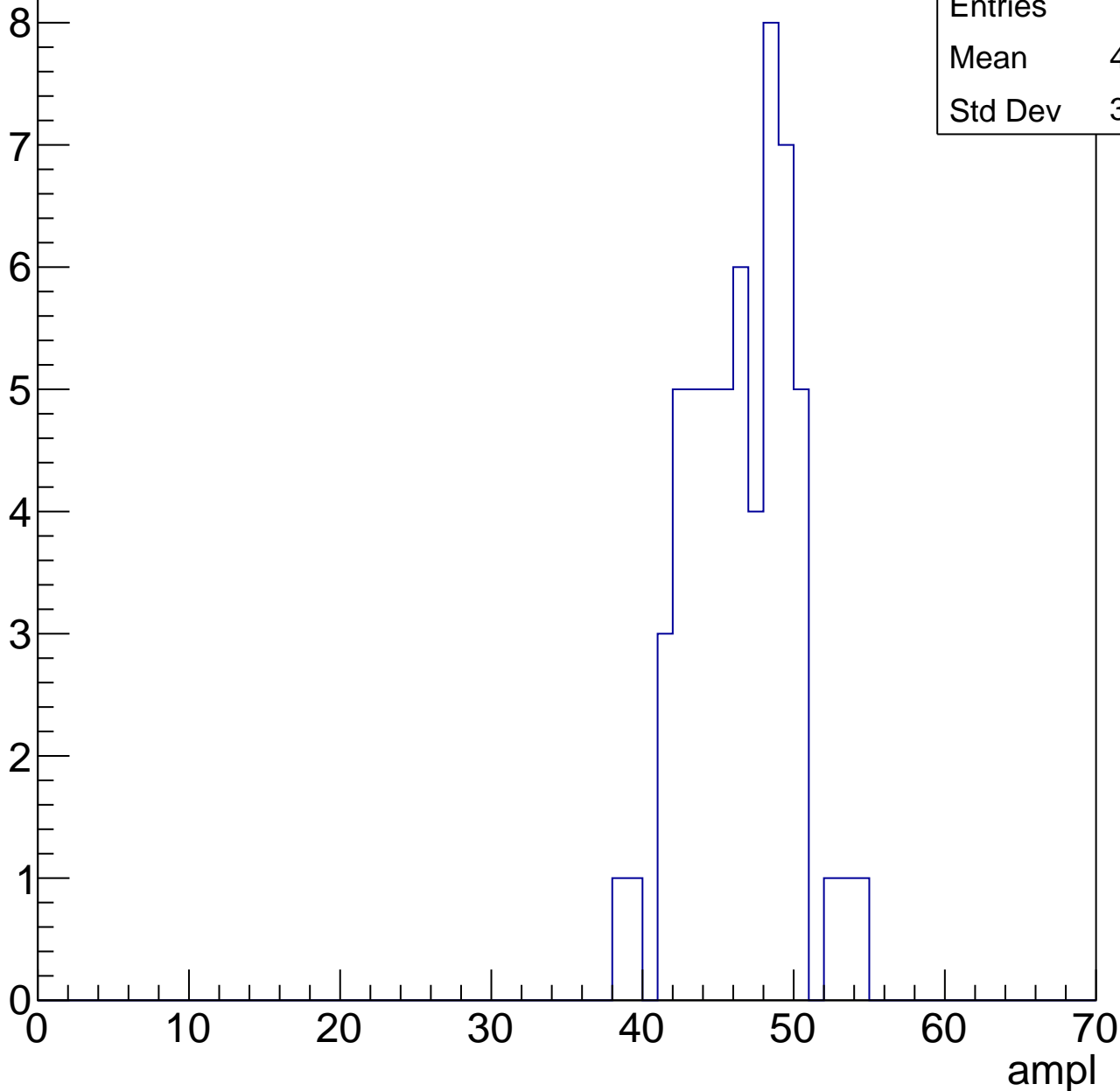


B1L102S, U14-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	46.03
Std Dev	3.388

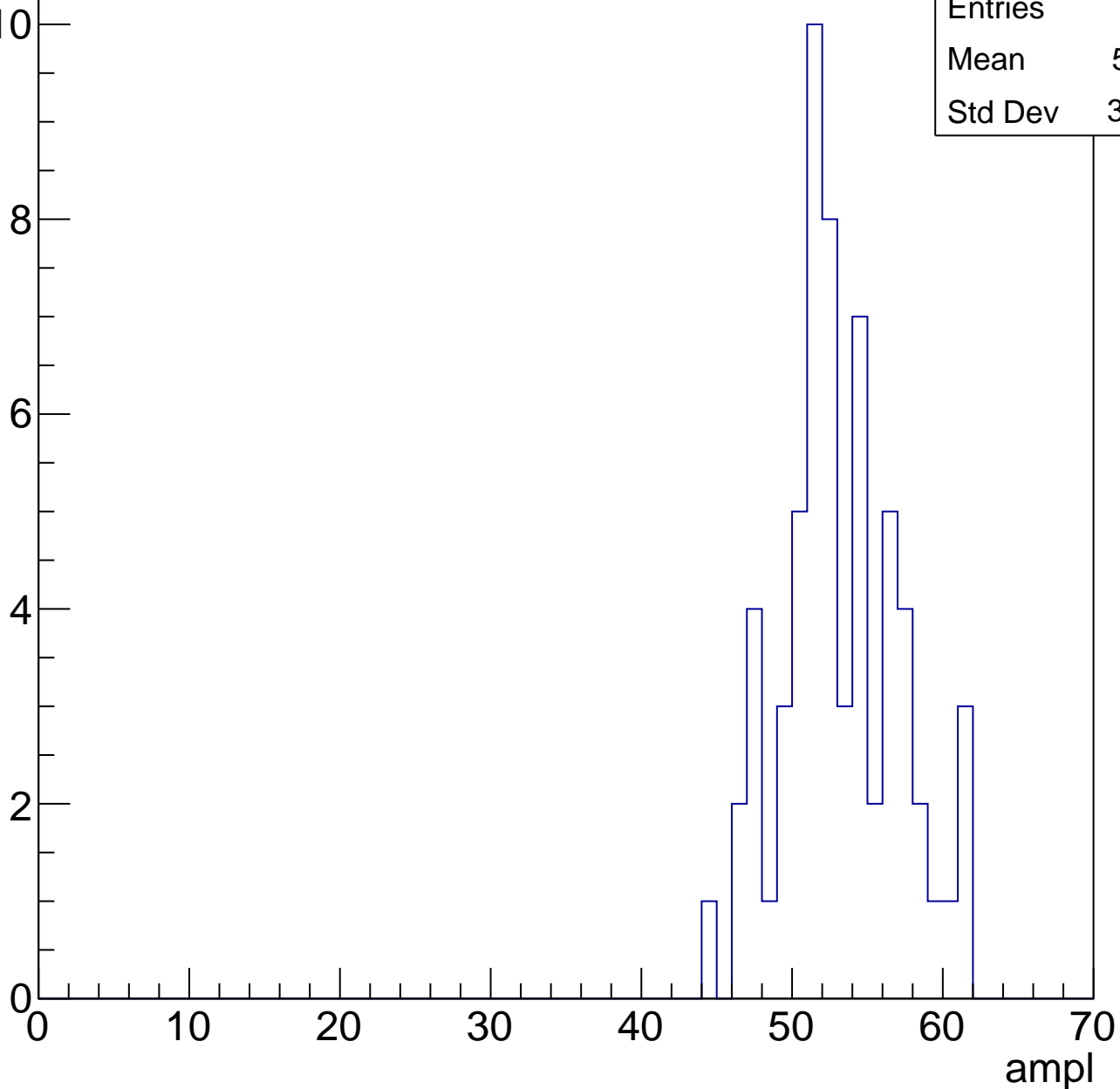


B1L102S, U14-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	52.71
Std Dev	3.883

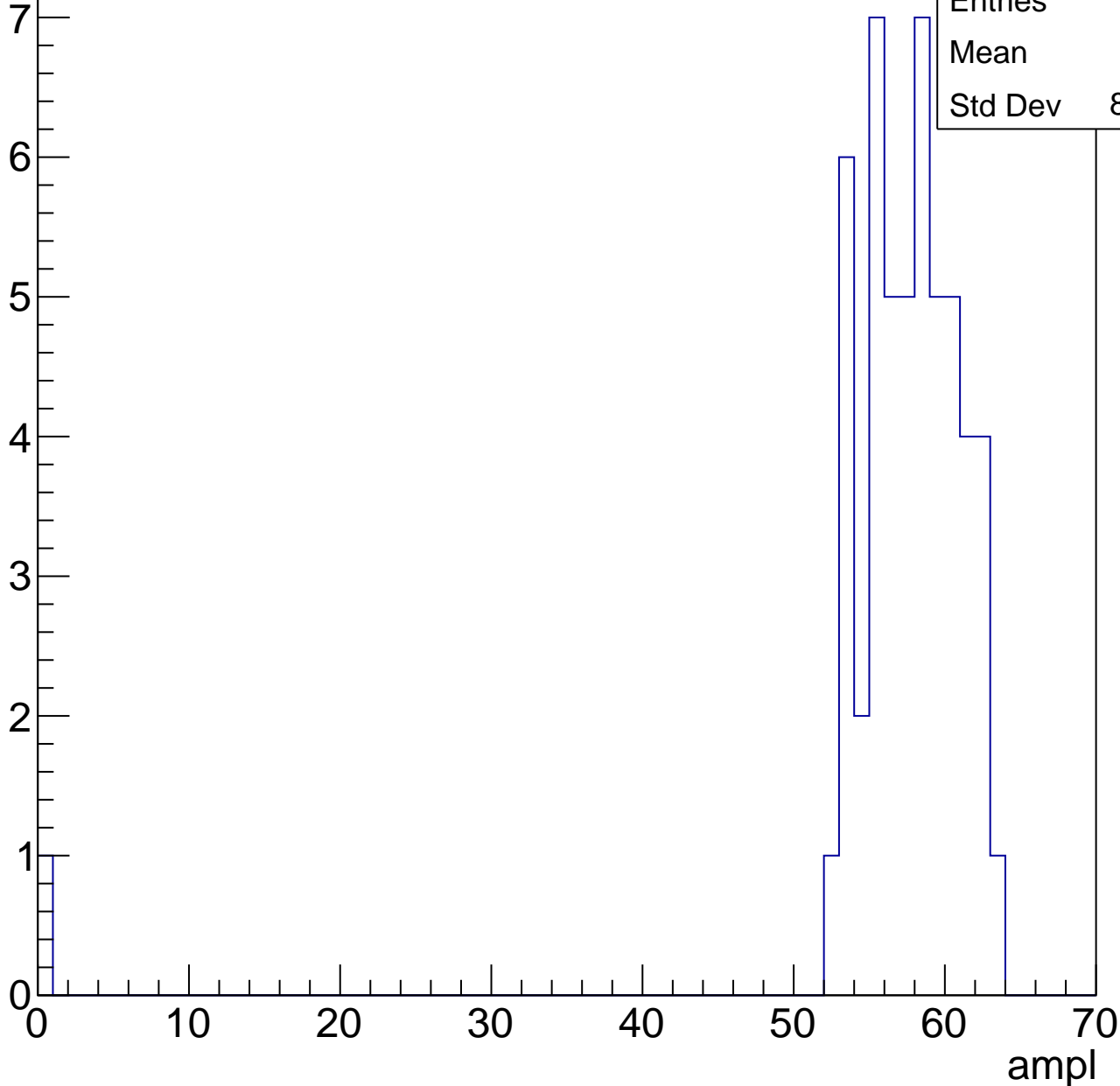


B1L102S, U14-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.3
Std Dev	8.318

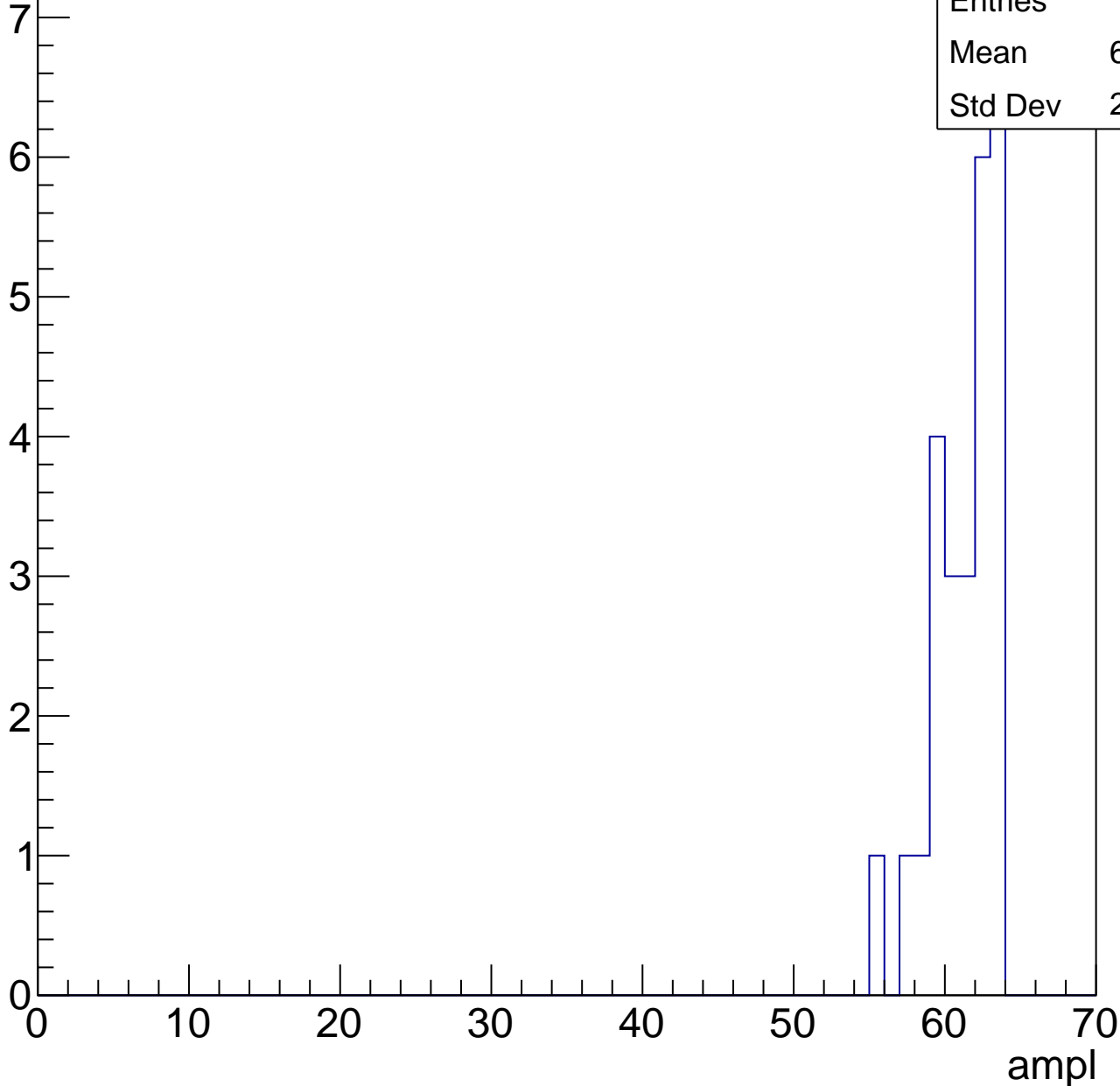


B1L102S, U14-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

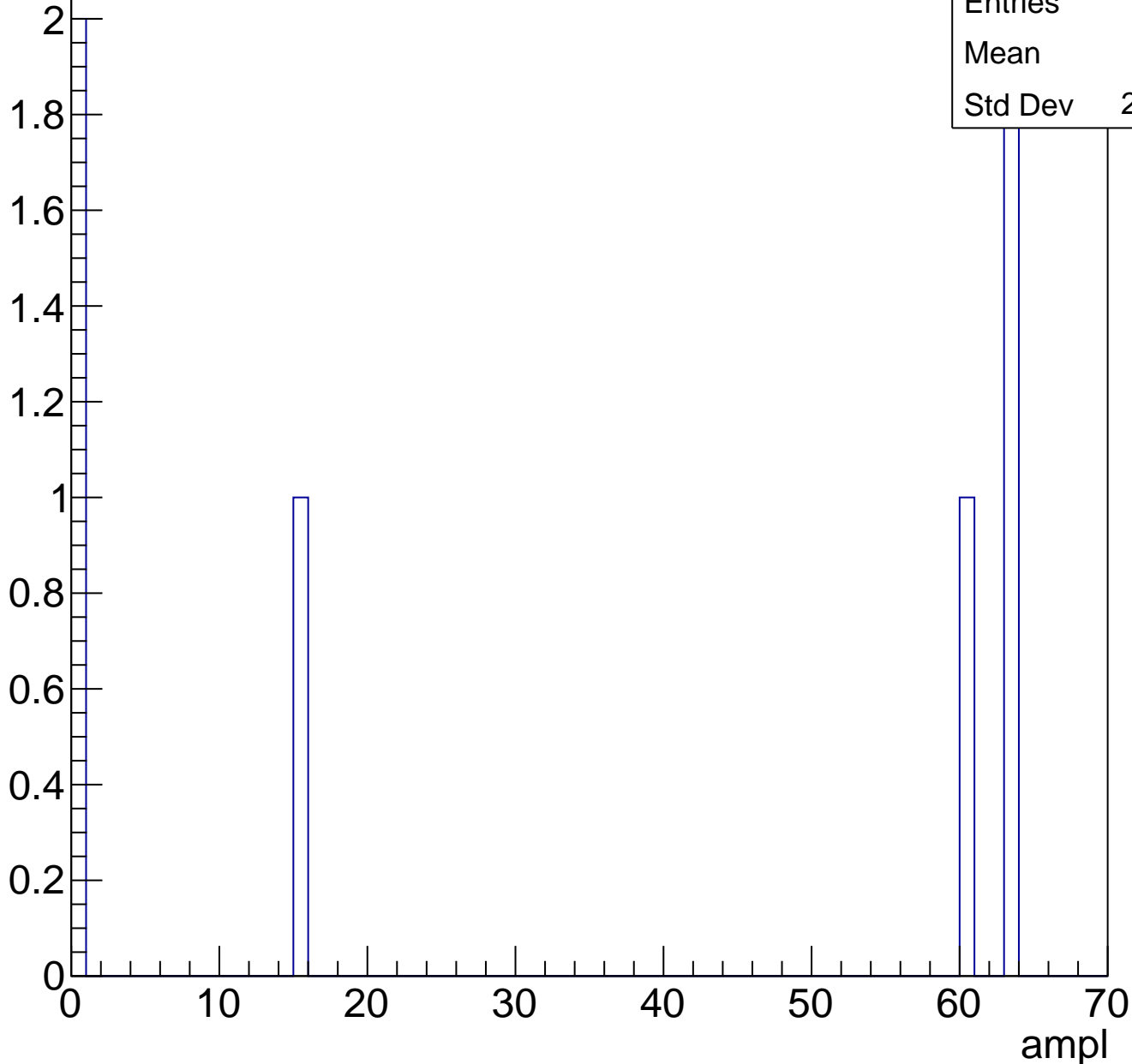
Entries	26
Mean	60.85
Std Dev	2.088



B1L102S, U14-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch107, adc0

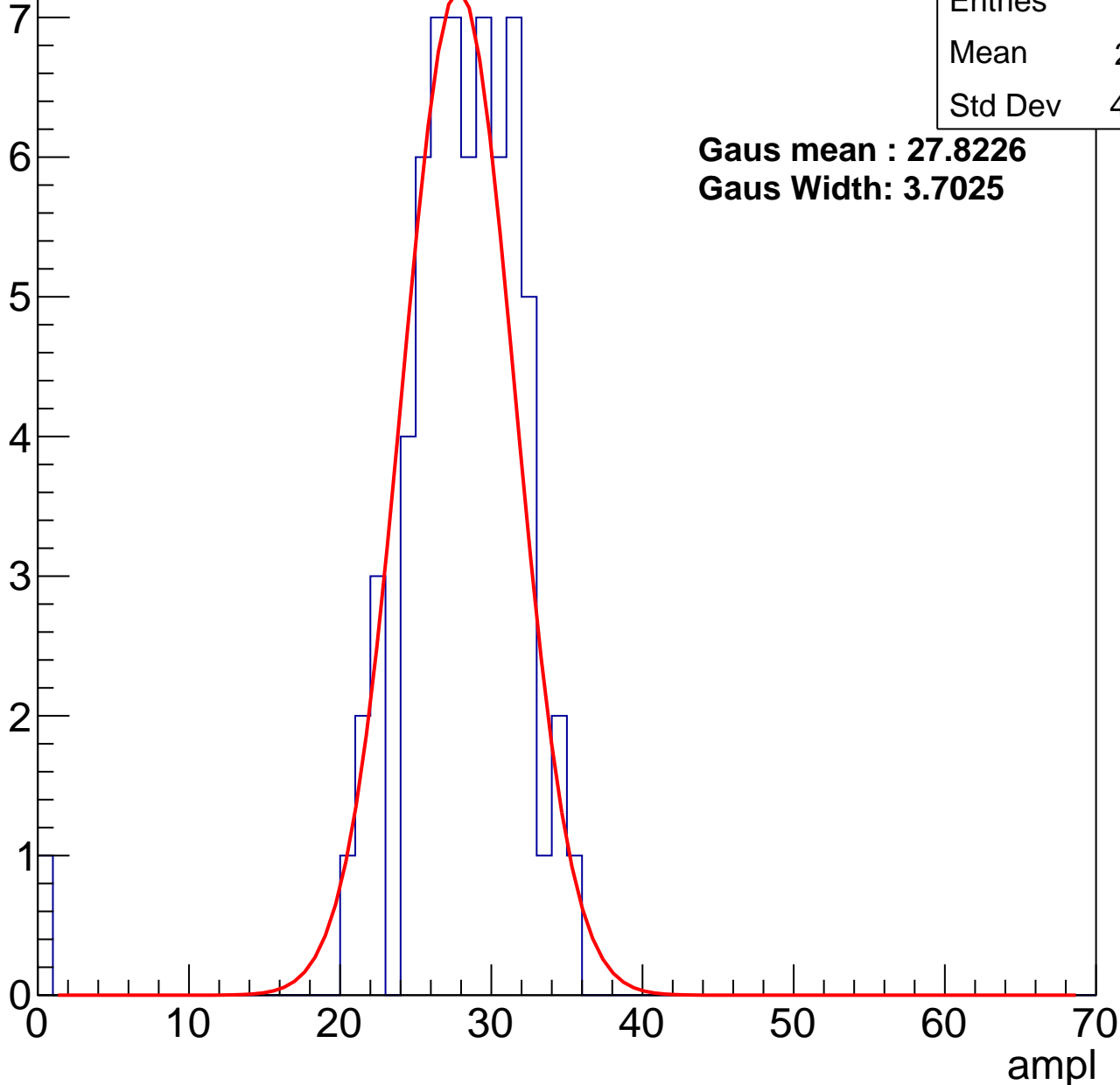
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.41
Std Dev	4.774

Gaus mean : 27.8226

Gaus Width: 3.7025



B1L102S, U14-ch107, adc1

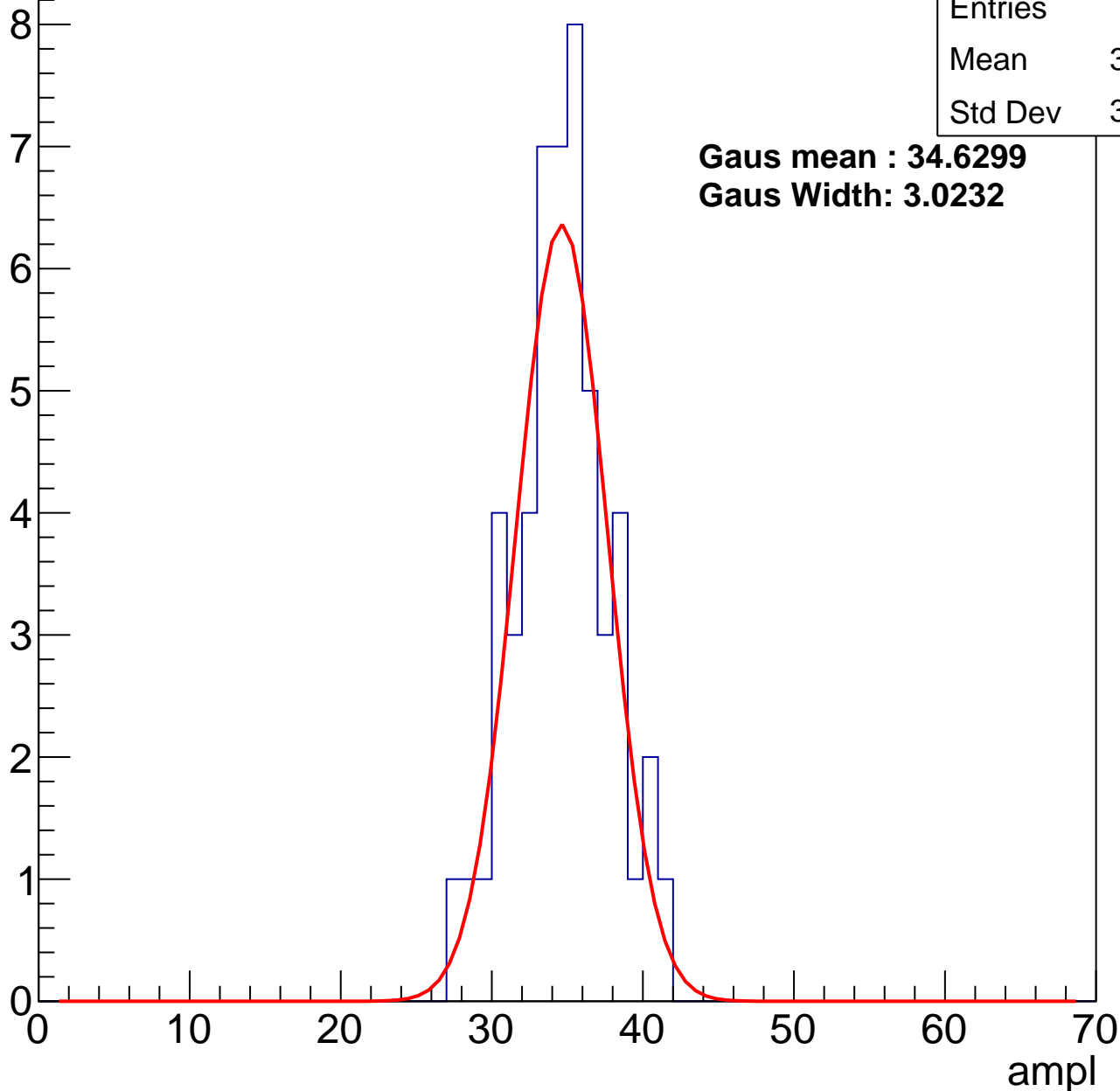
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.17
Std Dev	3.062

Gaus mean : 34.6299

Gaus Width: 3.0232



B1L102S, U14-ch107, adc2

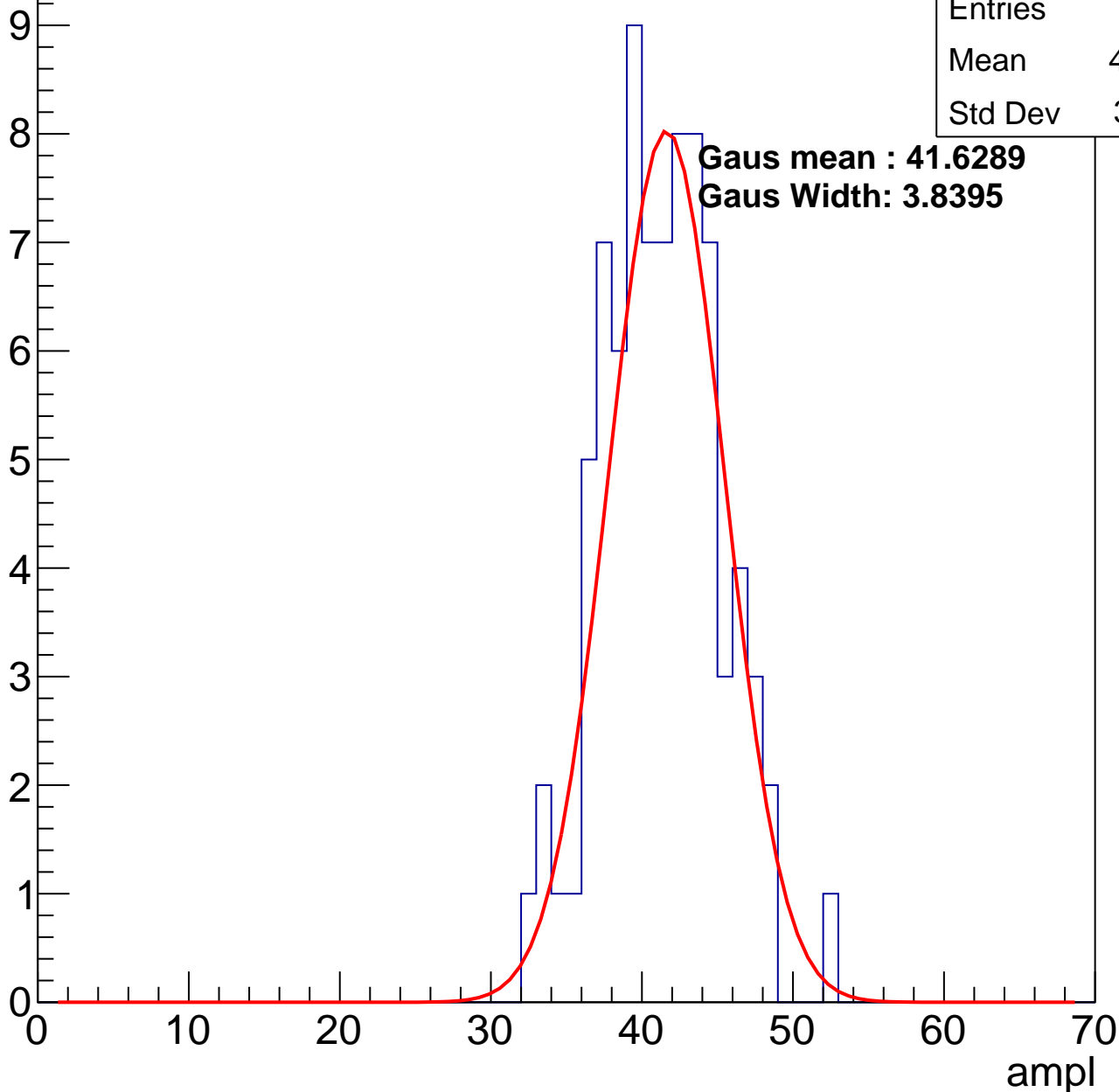
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	40.83
Std Dev	3.831

Gaus mean : 41.6289

Gaus Width: 3.8395

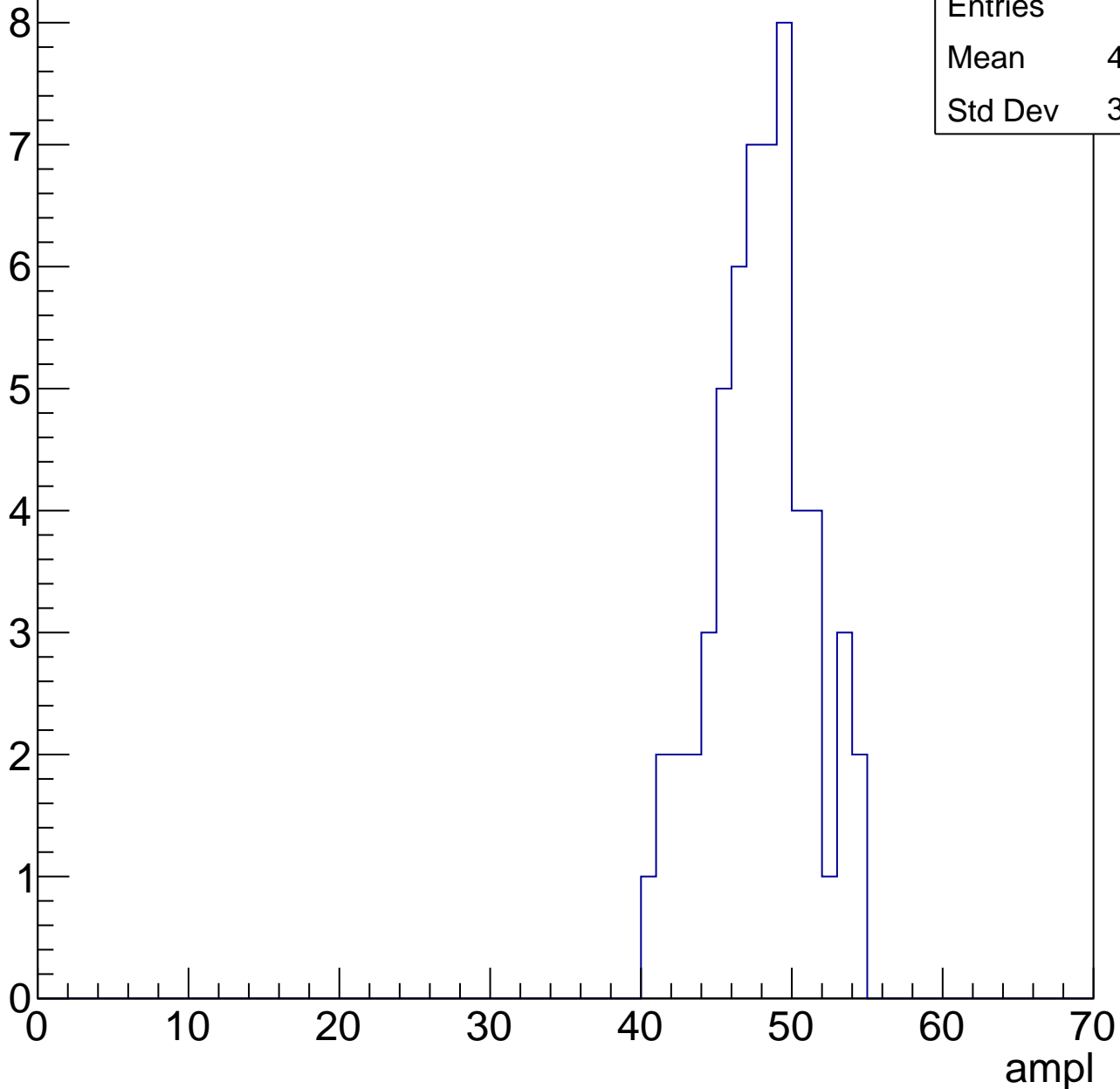


B1L102S, U14-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	47.46
Std Dev	3.282

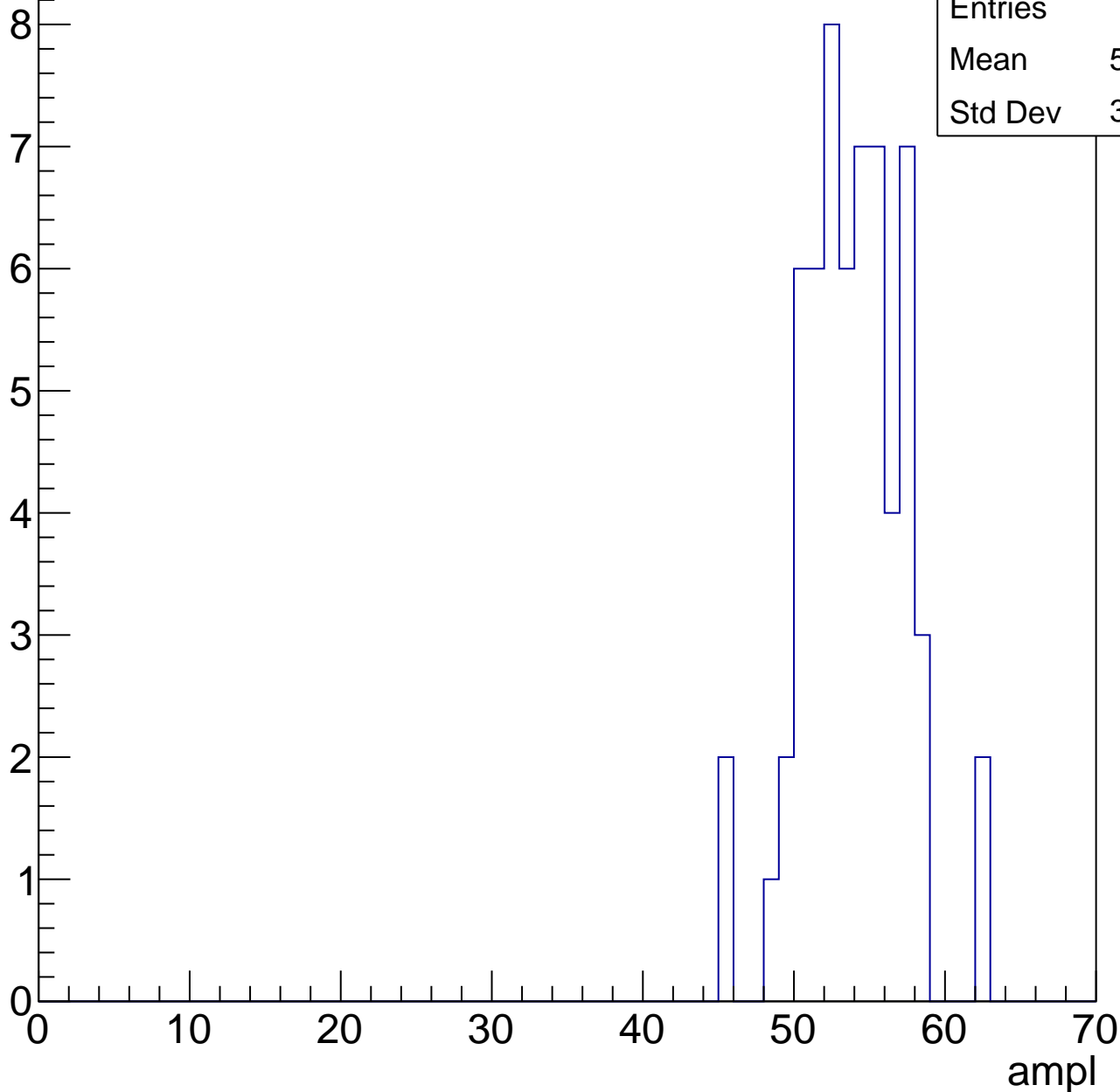


B1L102S, U14-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.44
Std Dev	3.336

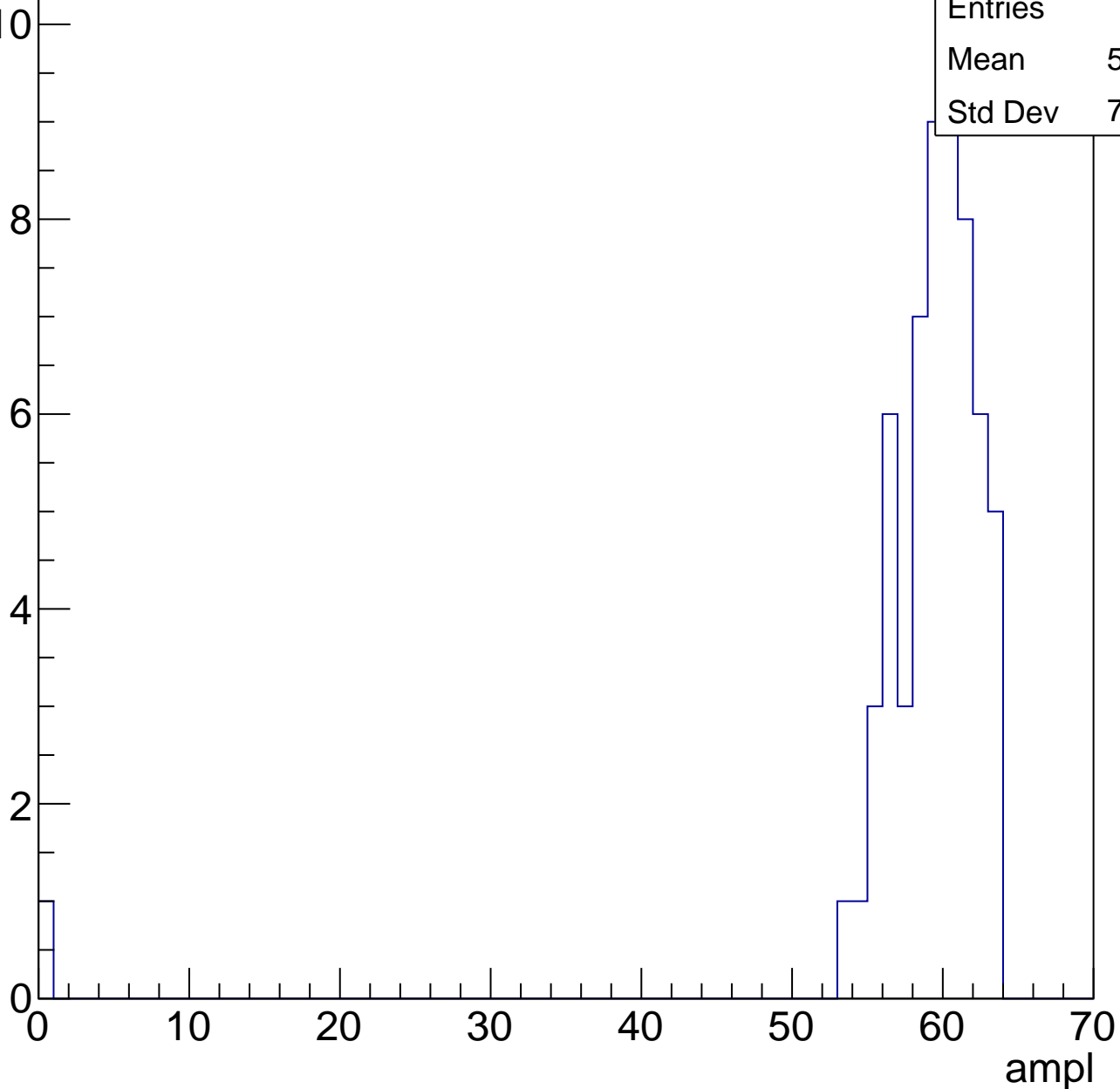


B1L102S, U14-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.18
Std Dev	7.957

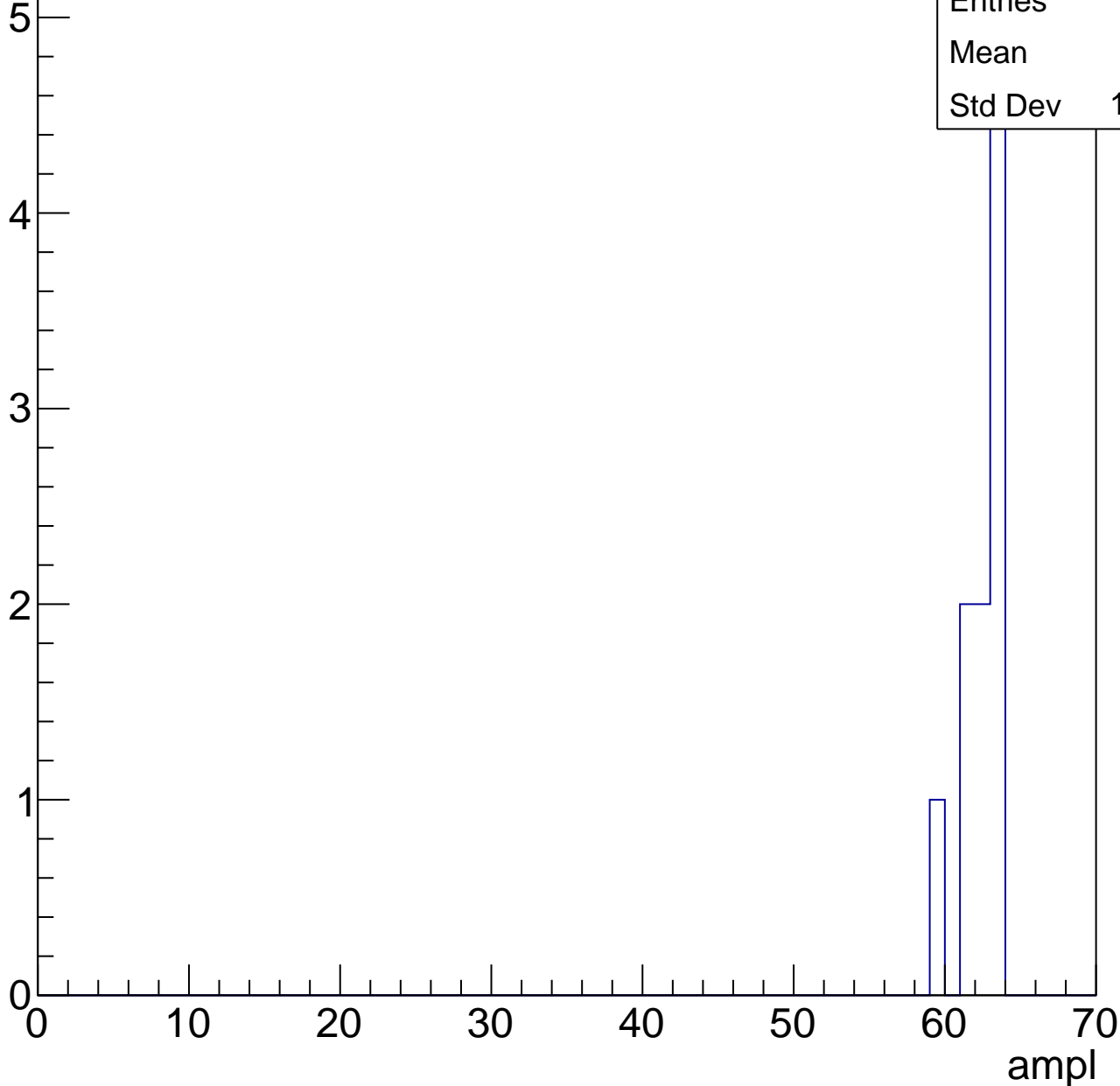


B1L102S, U14-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62
Std Dev	1.265



B1L102S, U14-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U14-ch108, adc0

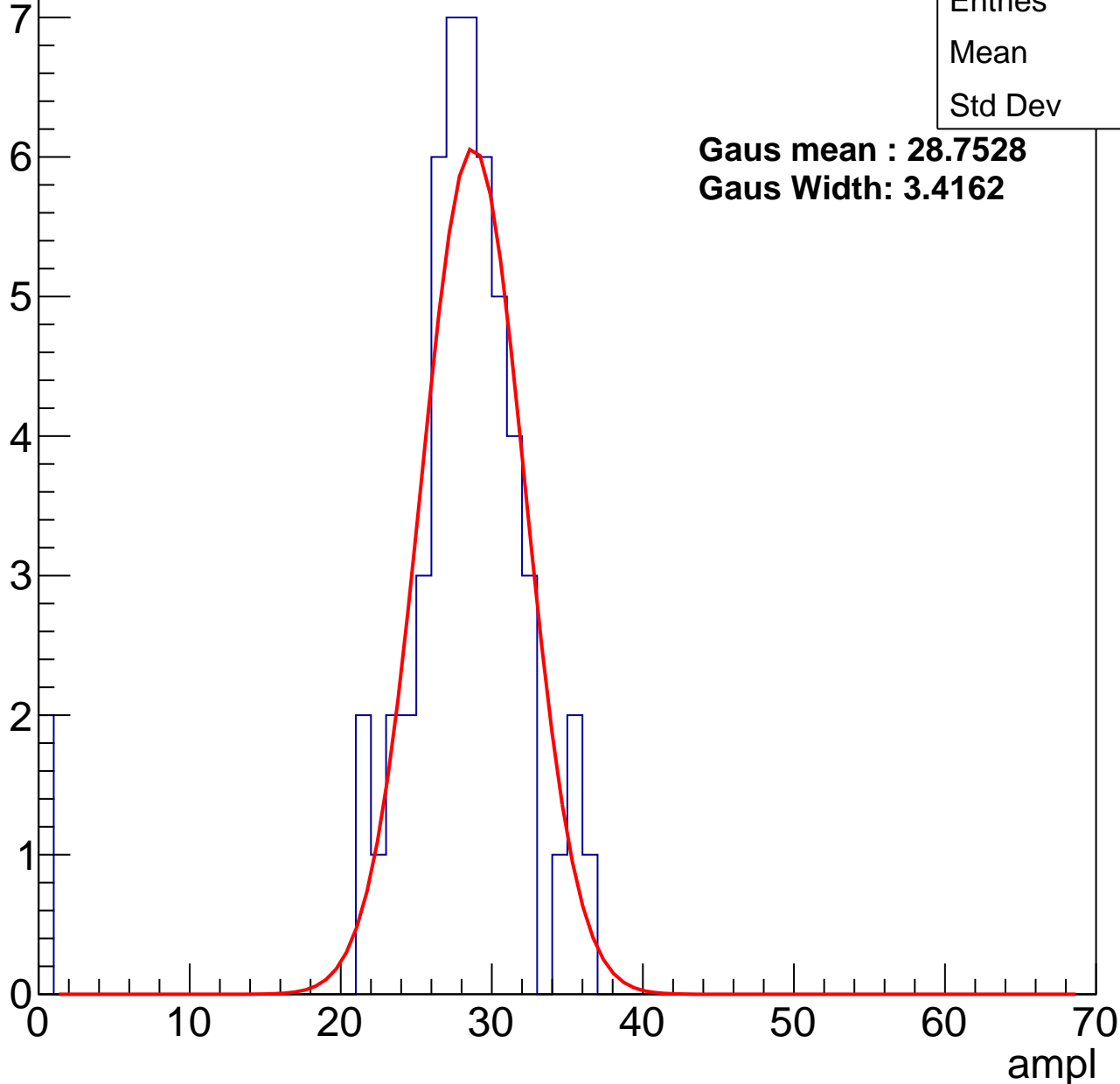
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	27
Std Dev	6.23

Gaus mean : 28.7528

Gaus Width: 3.4162



B1L102S, U14-ch108, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	35.08
Std Dev	3.622

Gaus mean : 35.2079

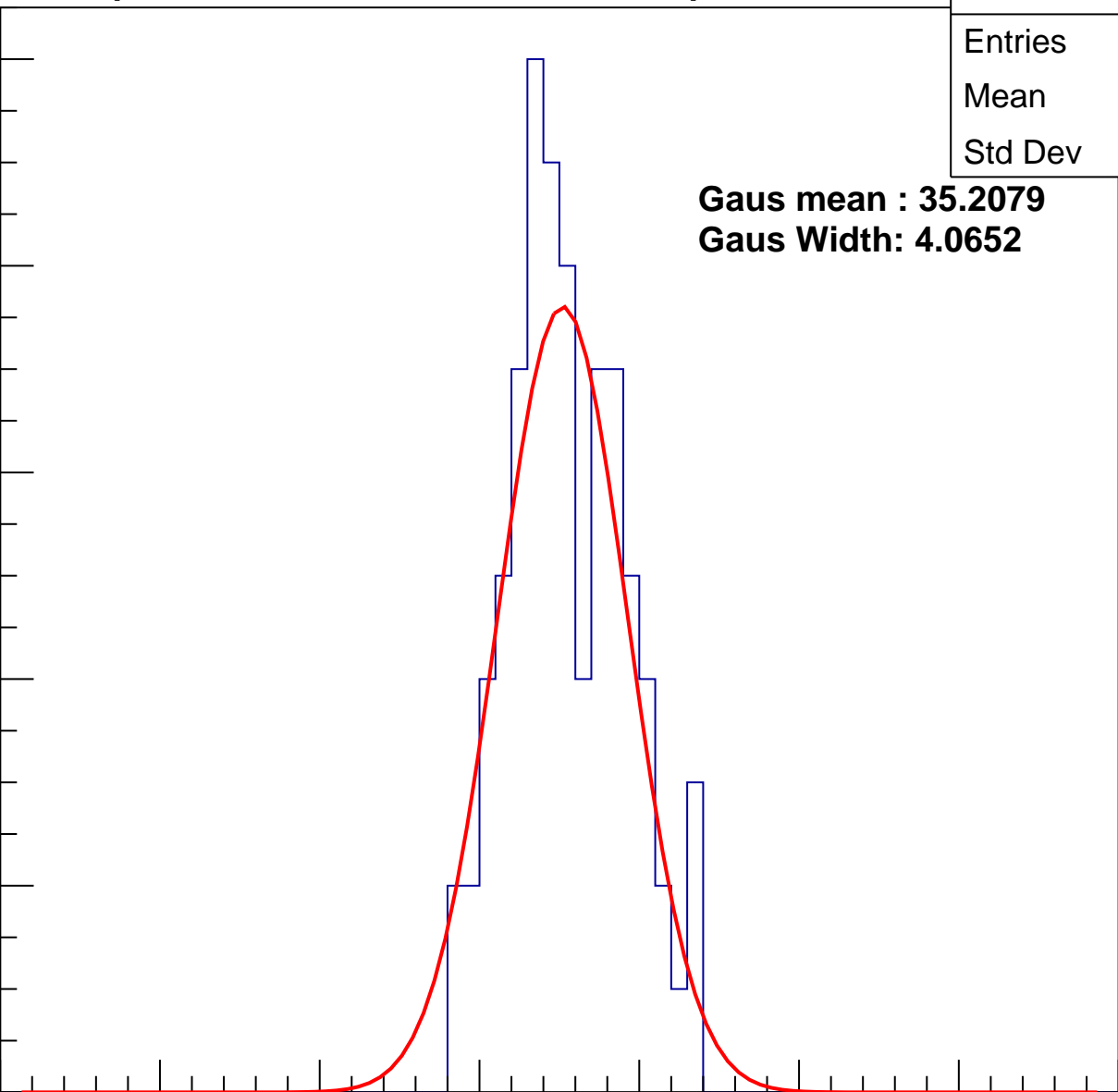
Gaus Width: 4.0652

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch108, adc2

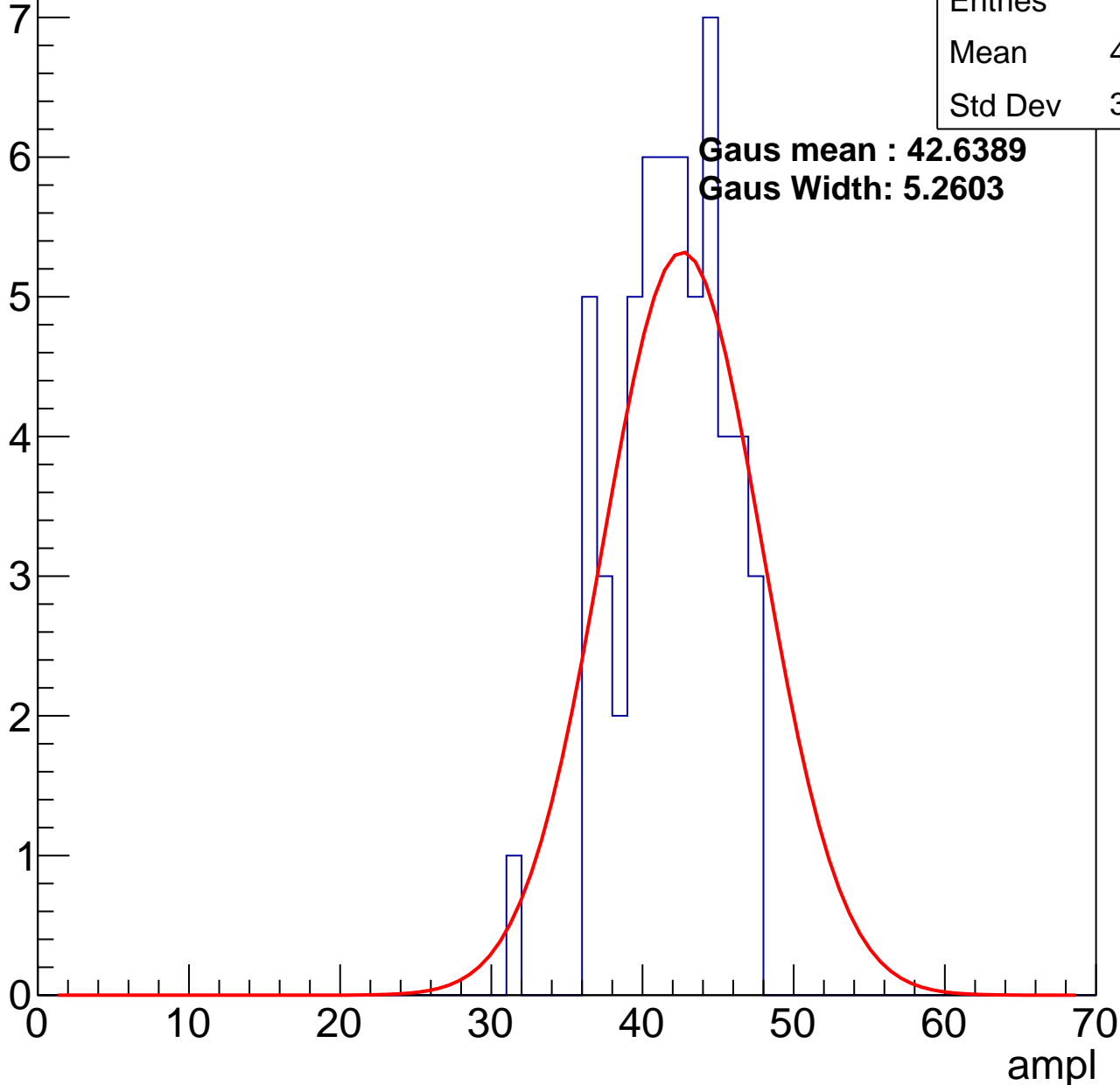
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.39
Std Dev	3.427

Gaus mean : 42.6389

Gaus Width: 5.2603

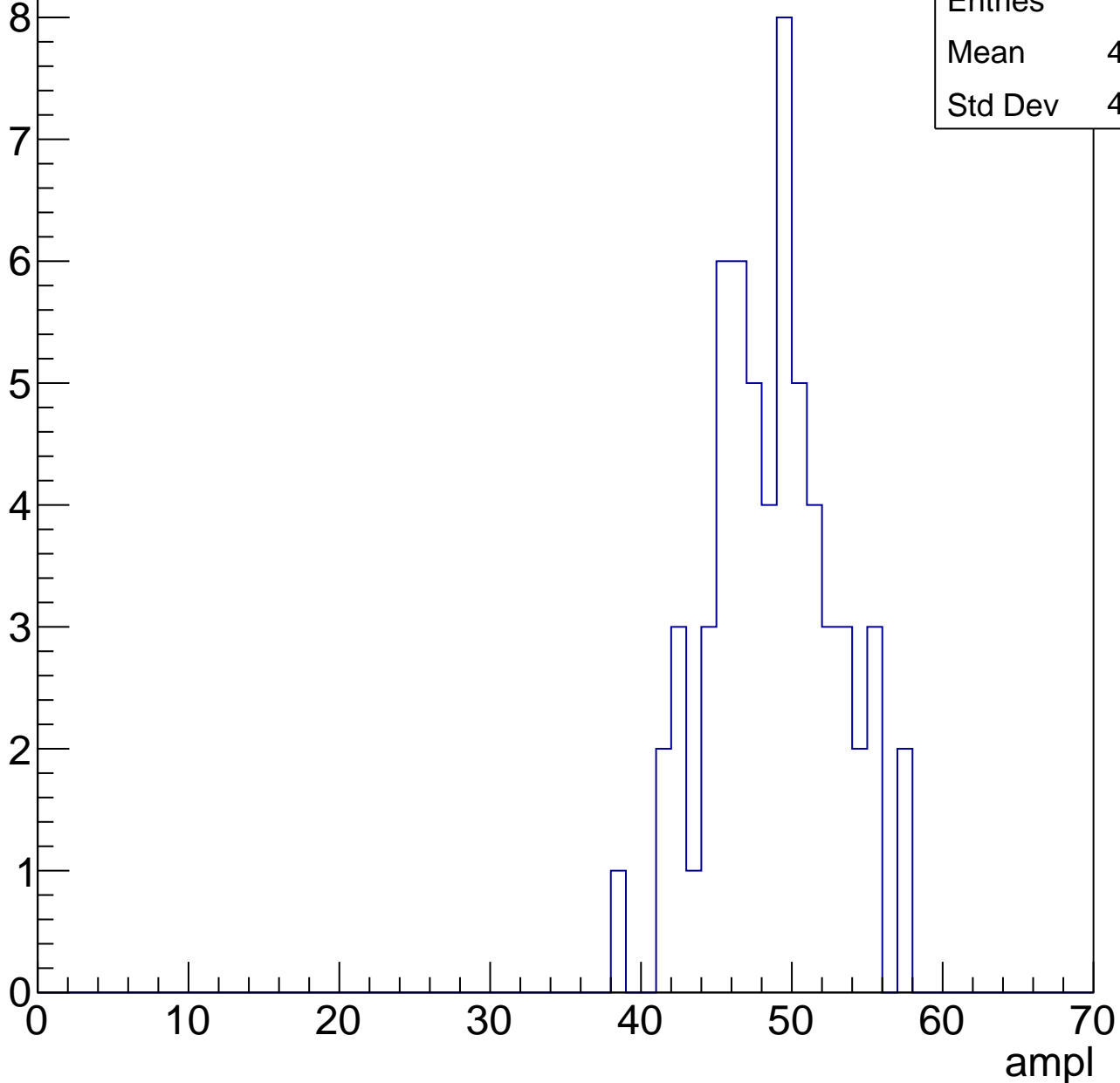


B1L102S, U14-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.23
Std Dev	4.103

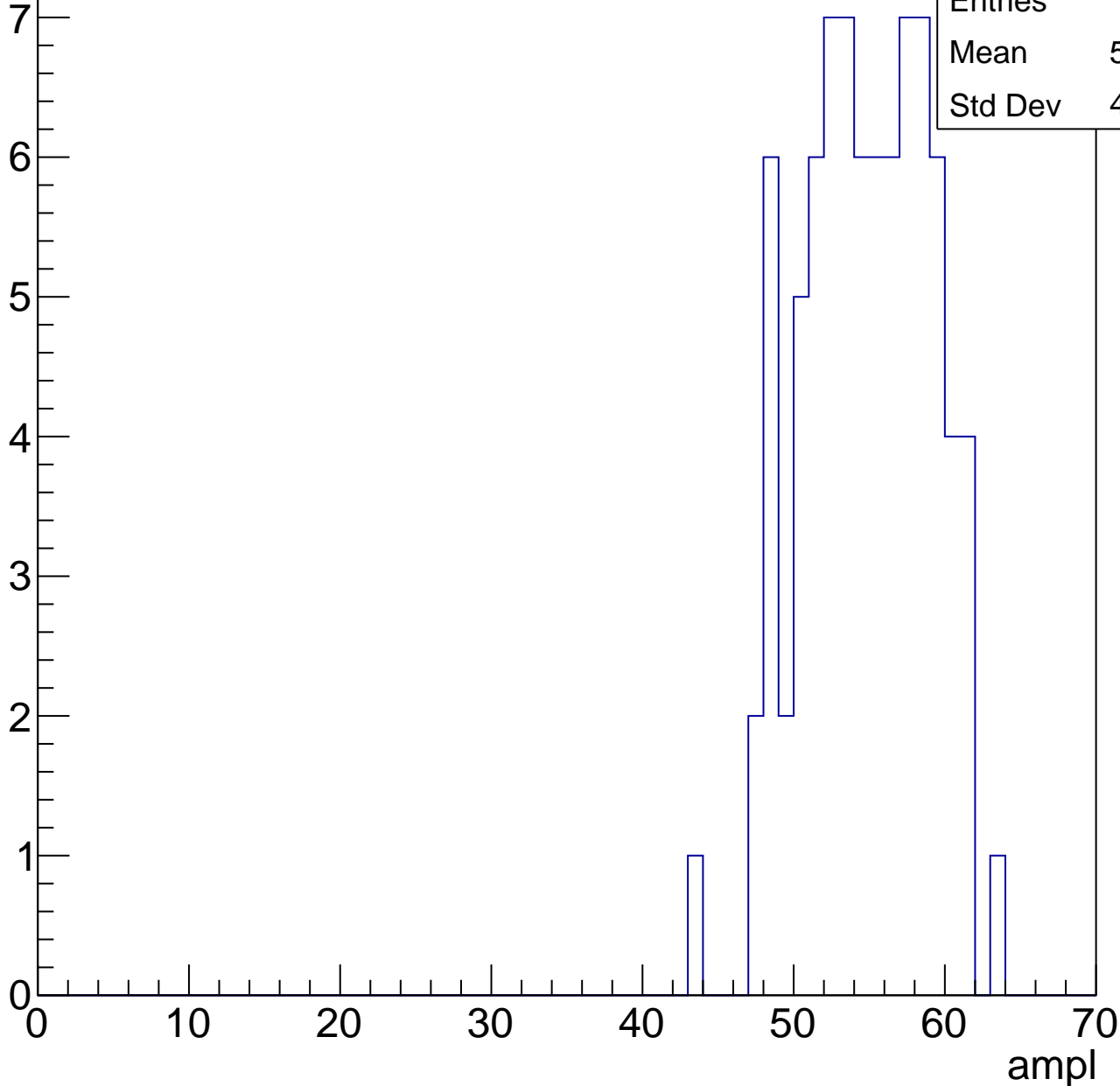


B1L102S, U14-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	54.34
Std Dev	4.137

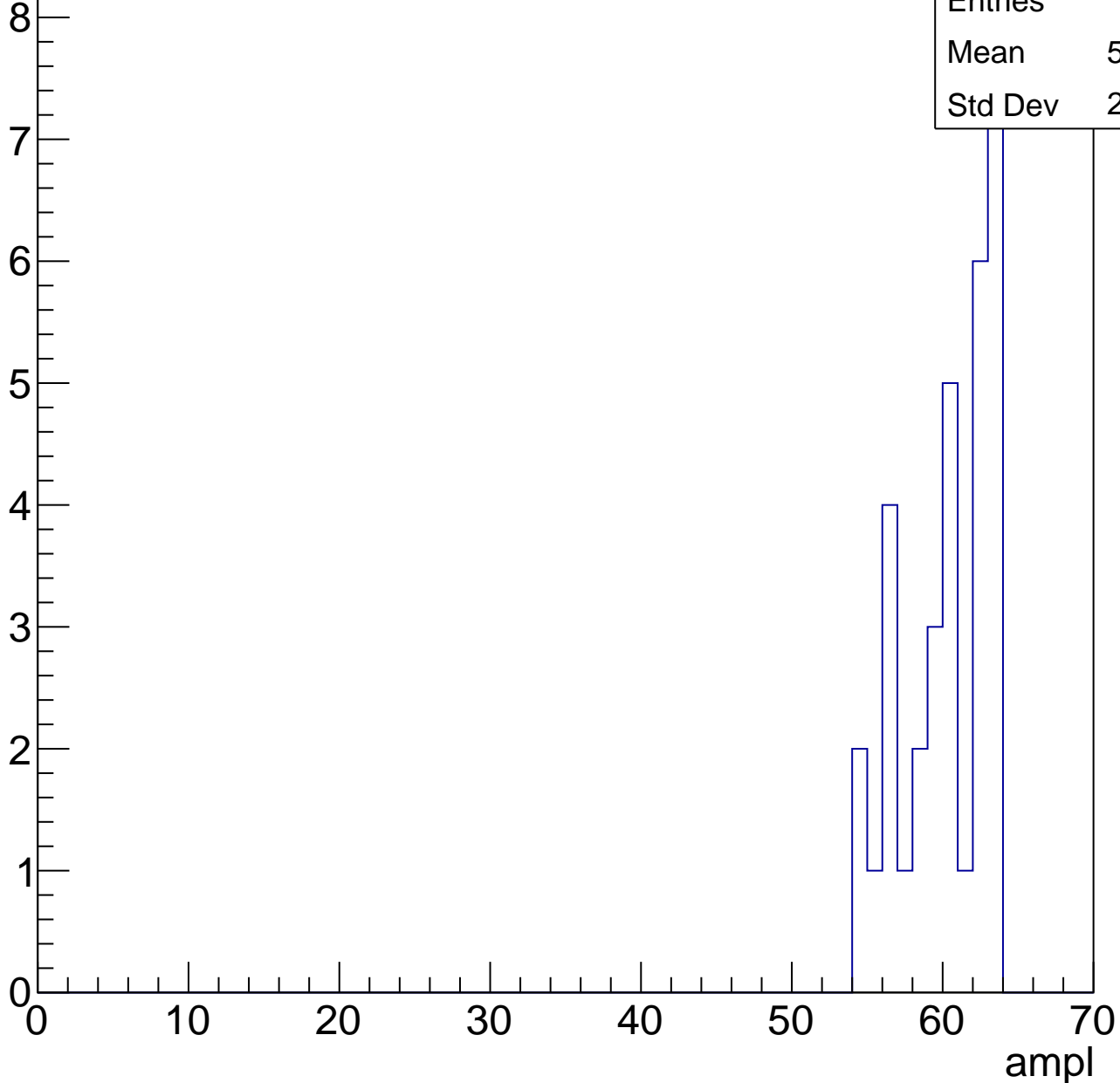


B1L102S, U14-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	59.82
Std Dev	2.897

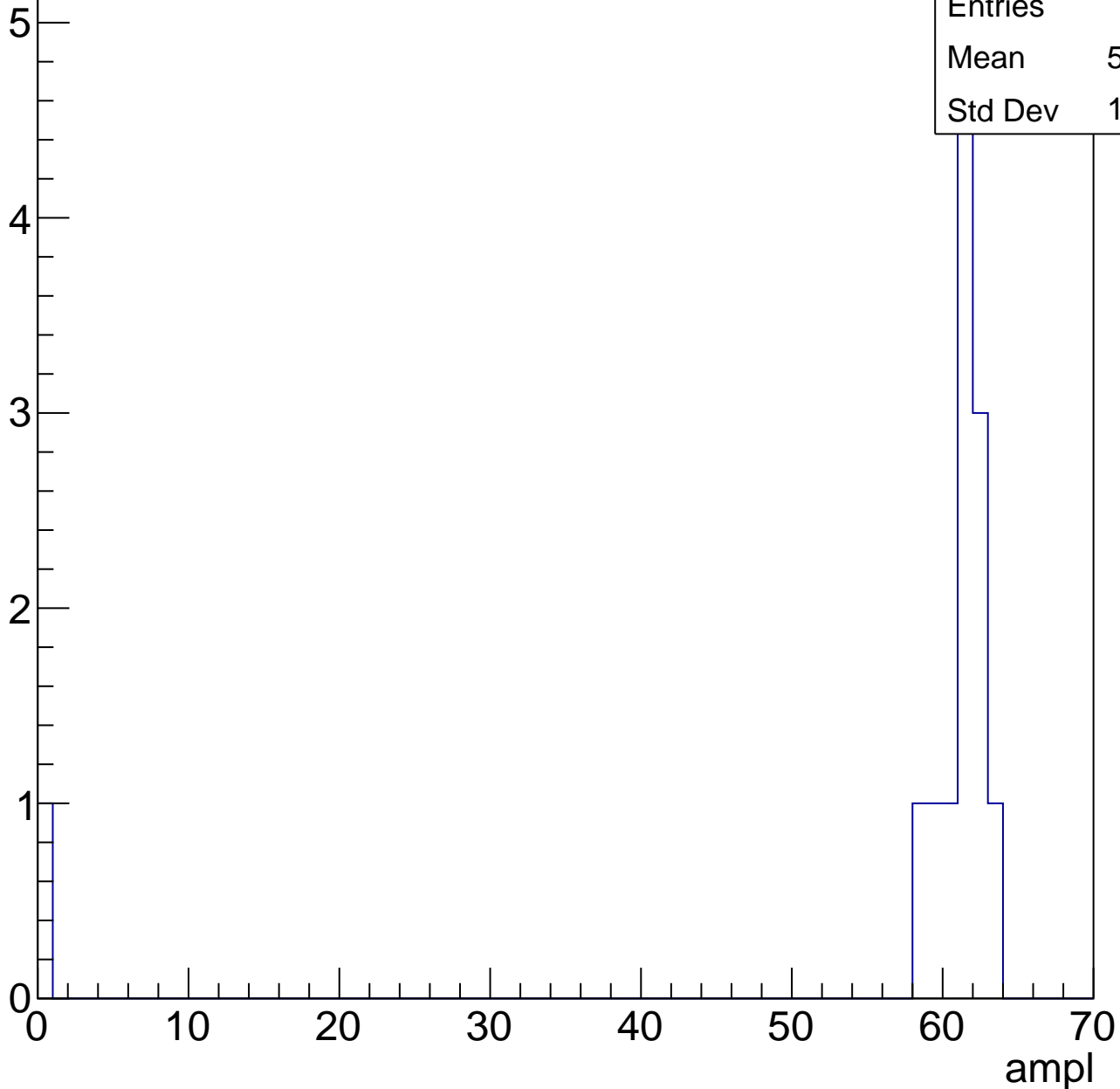


B1L102S, U14-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.23
Std Dev	16.28



B1L102S, U14-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch109, adc0

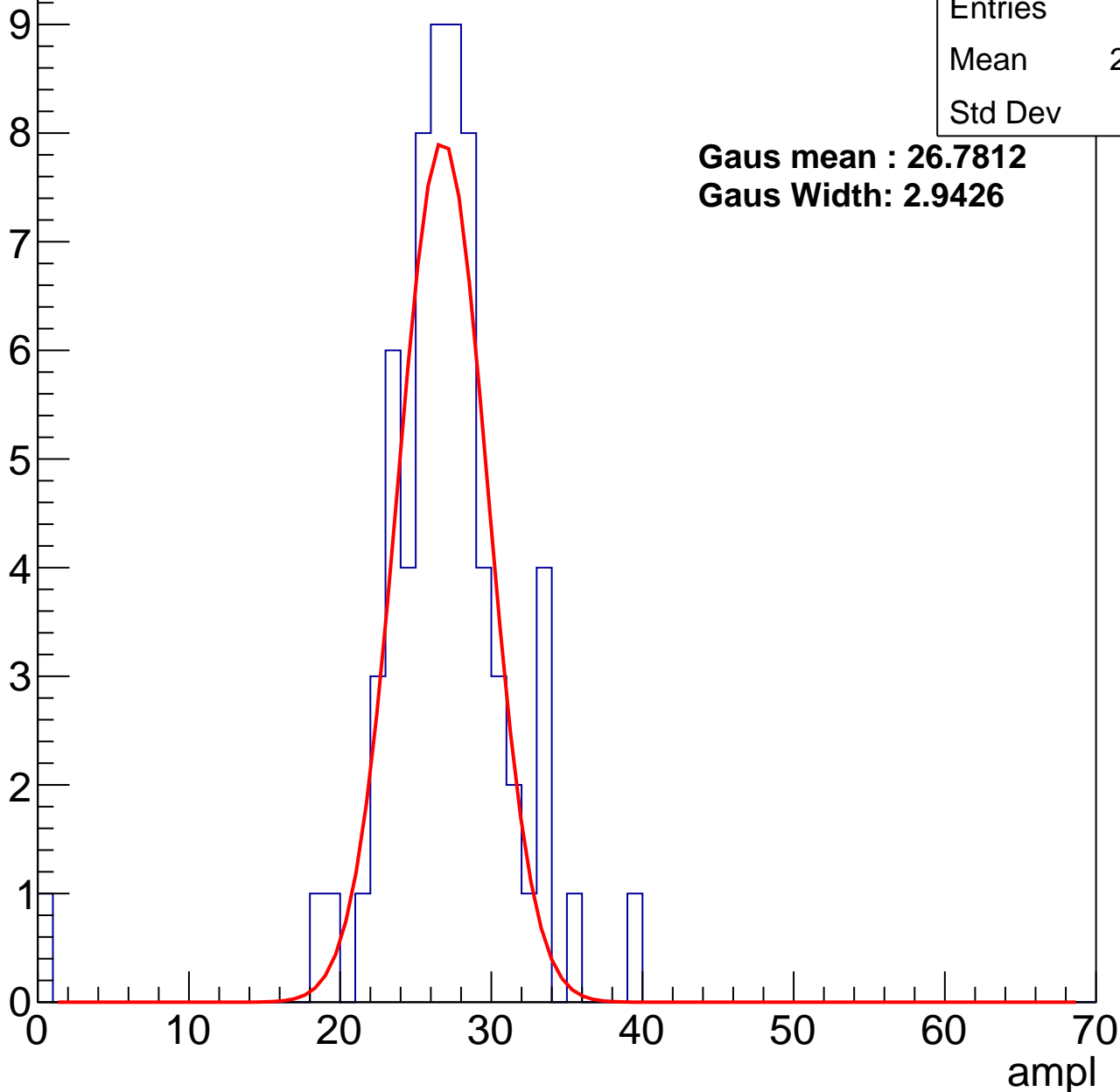
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	26.34
Std Dev	4.87

Gaus mean : 26.7812

Gaus Width: 2.9426



B1L102S, U14-ch109, adc1

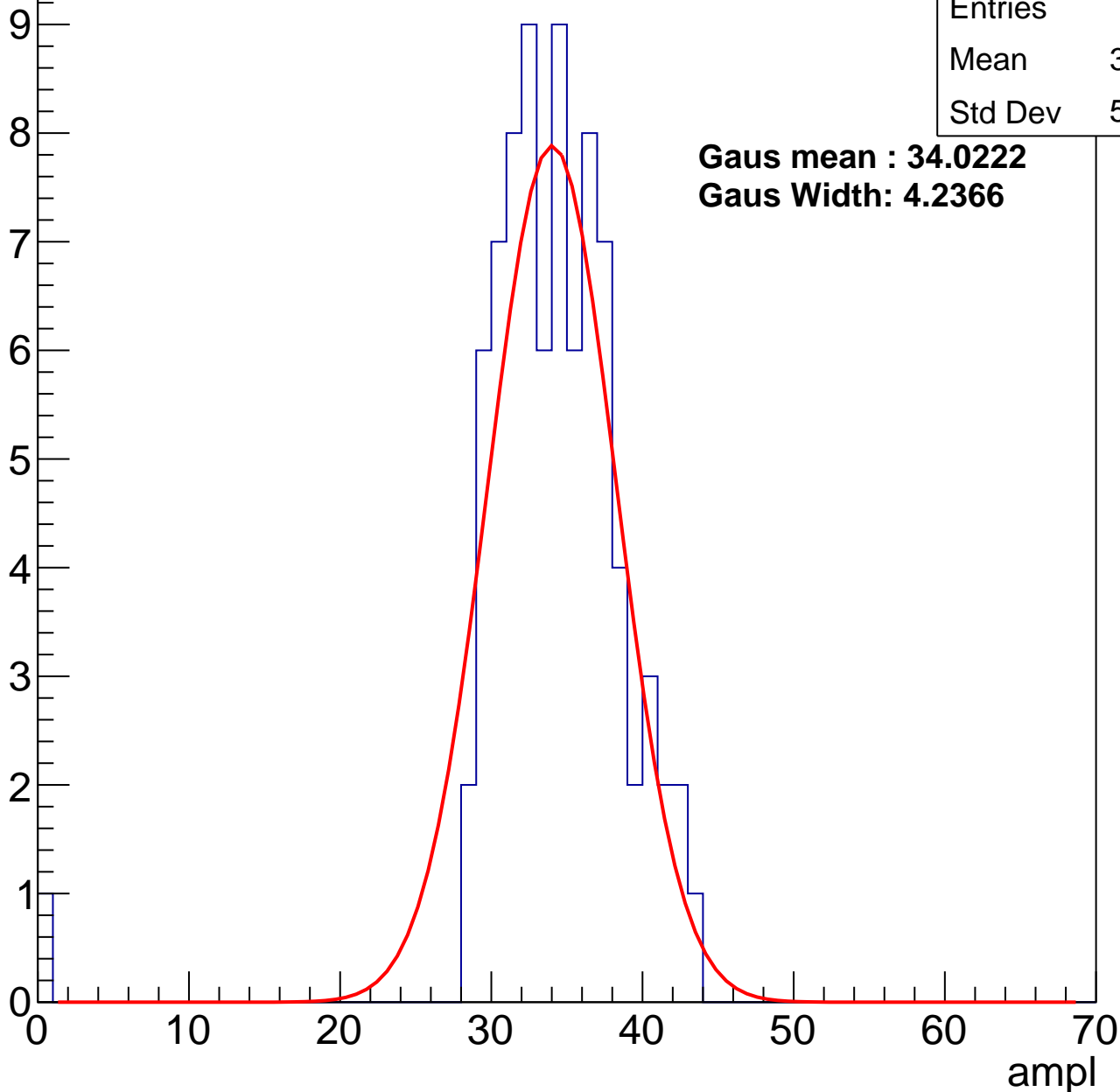
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	33.69
Std Dev	5.179

Gaus mean : 34.0222

Gaus Width: 4.2366



B1L102S, U14-ch109, adc2

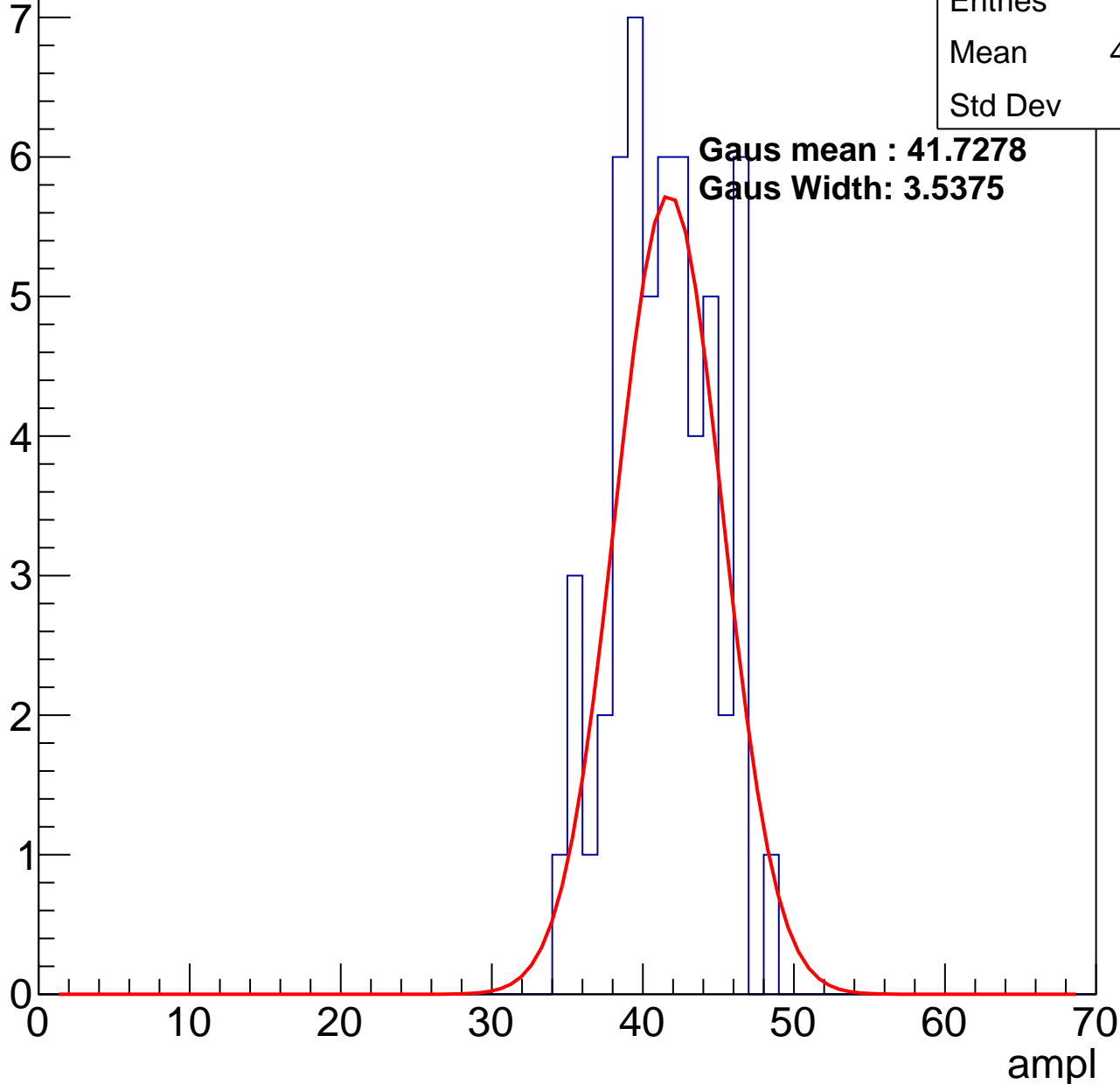
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	40.98
Std Dev	3.3

Gaus mean : 41.7278

Gaus Width: 3.5375

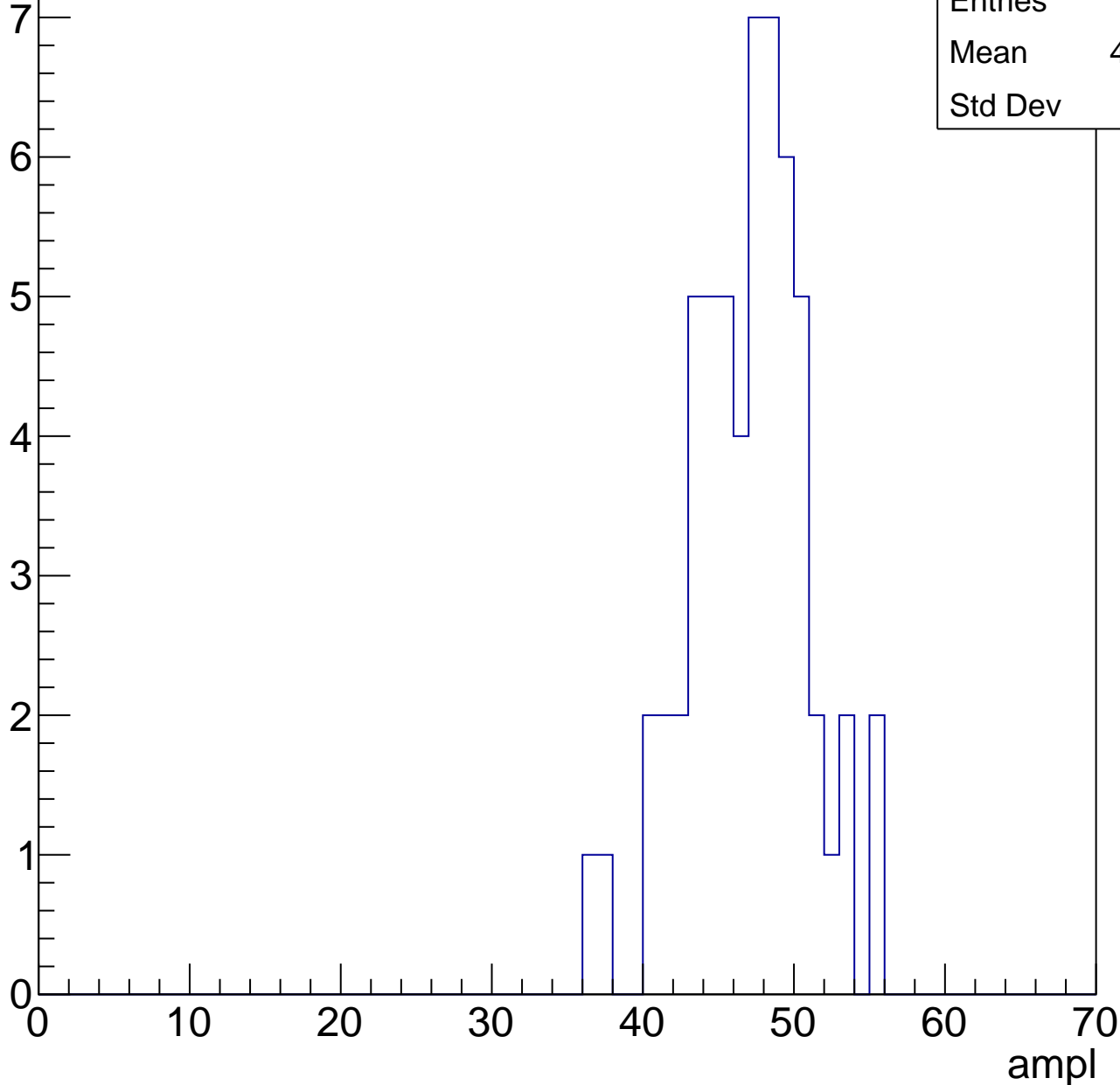


B1L102S, U14-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	46.47
Std Dev	3.92

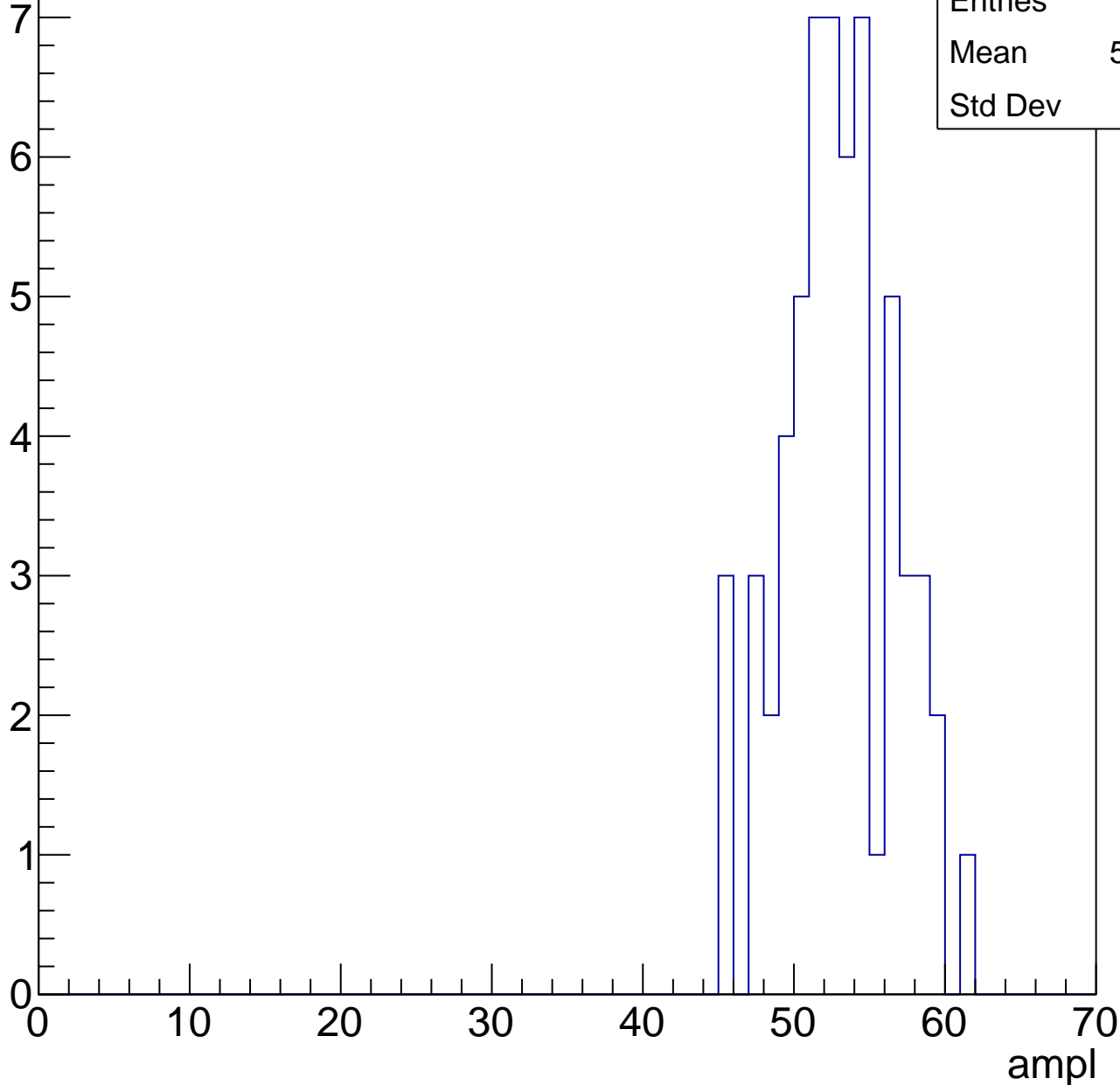


B1L102S, U14-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	52.44
Std Dev	3.66

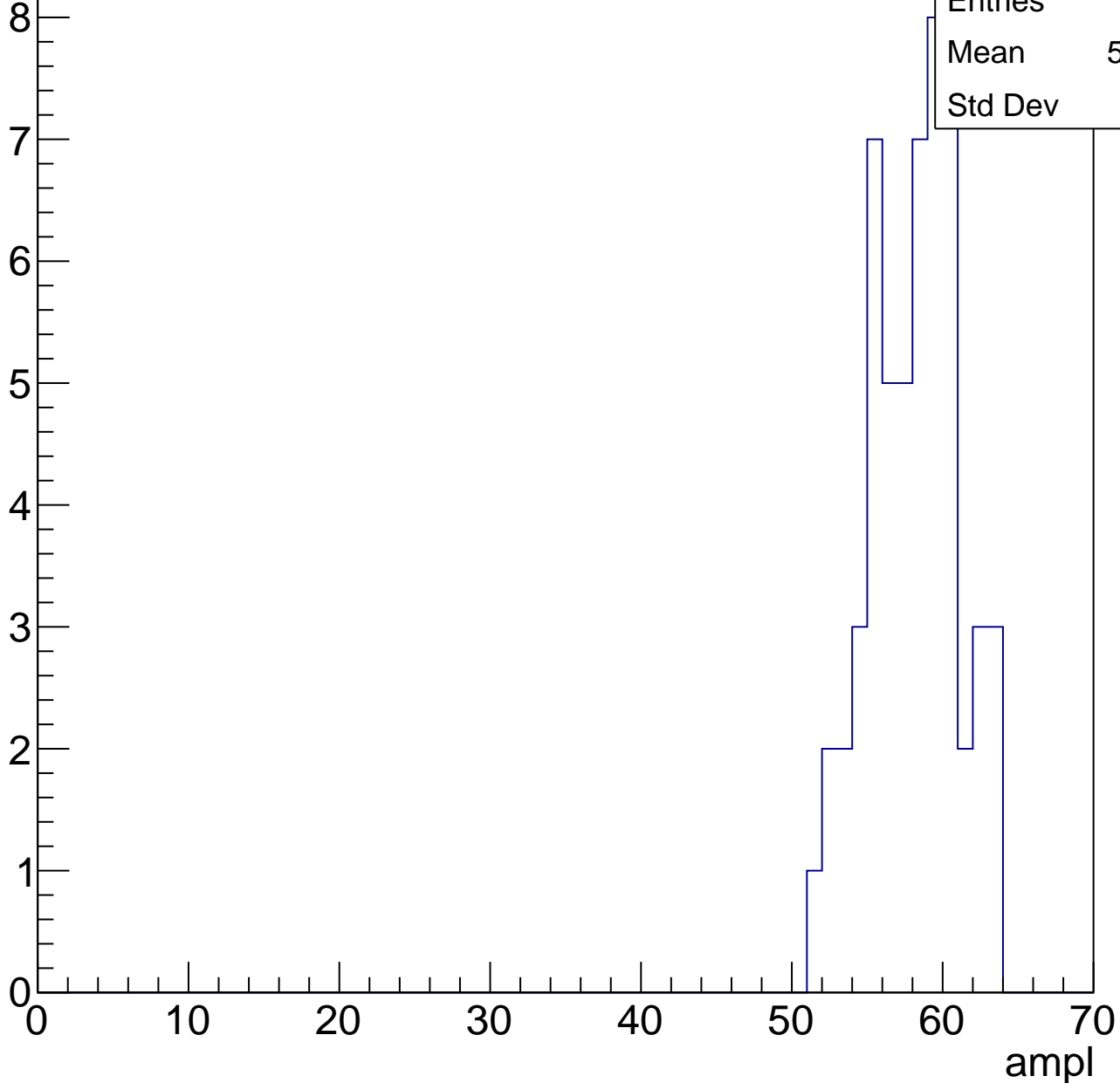


B1L102S, U14-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	57.64
Std Dev	2.93

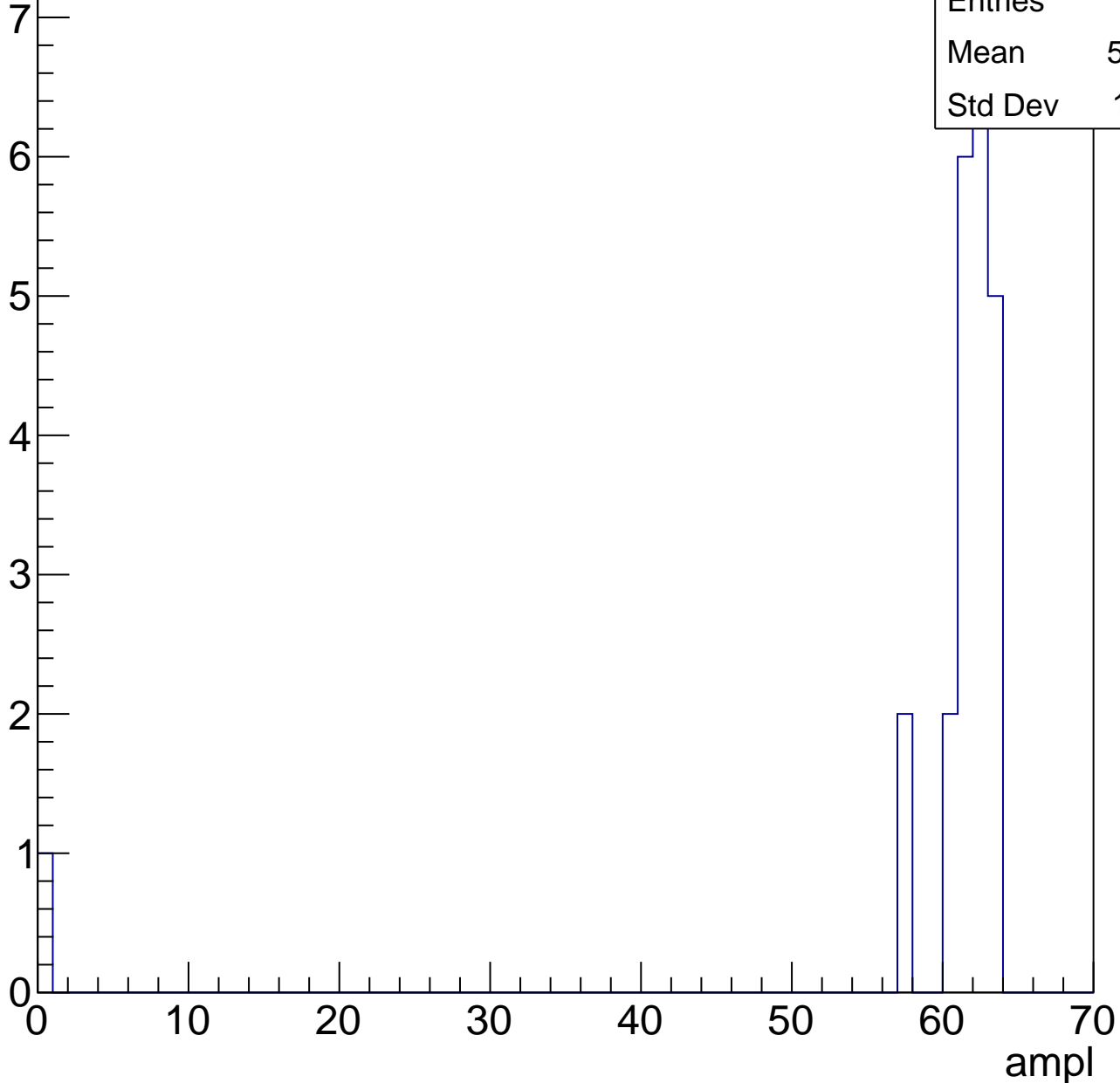


B1L102S, U14-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

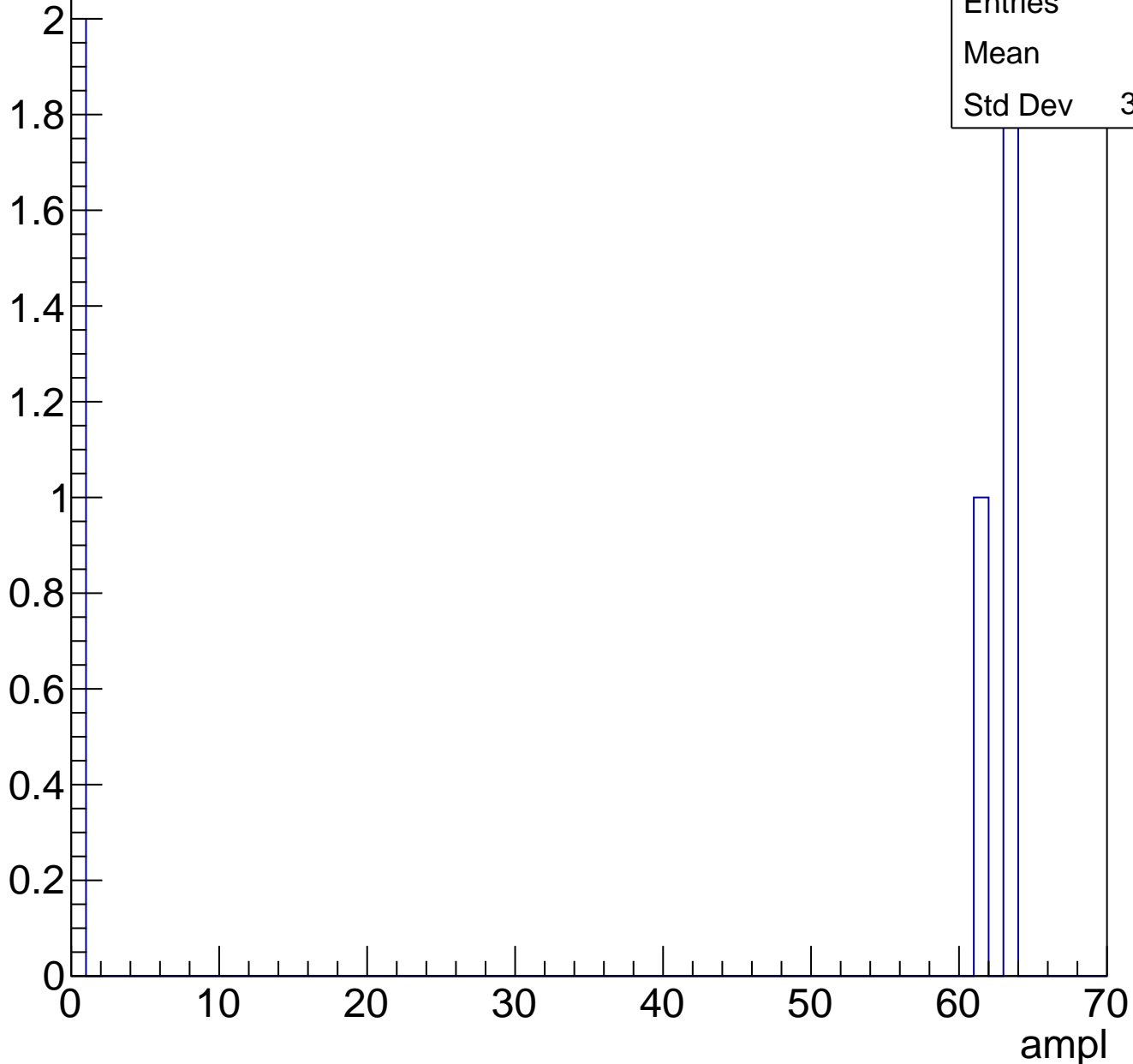
Entries	23
Mean	58.65
Std Dev	12.61



B1L102S, U14-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch110, adc0

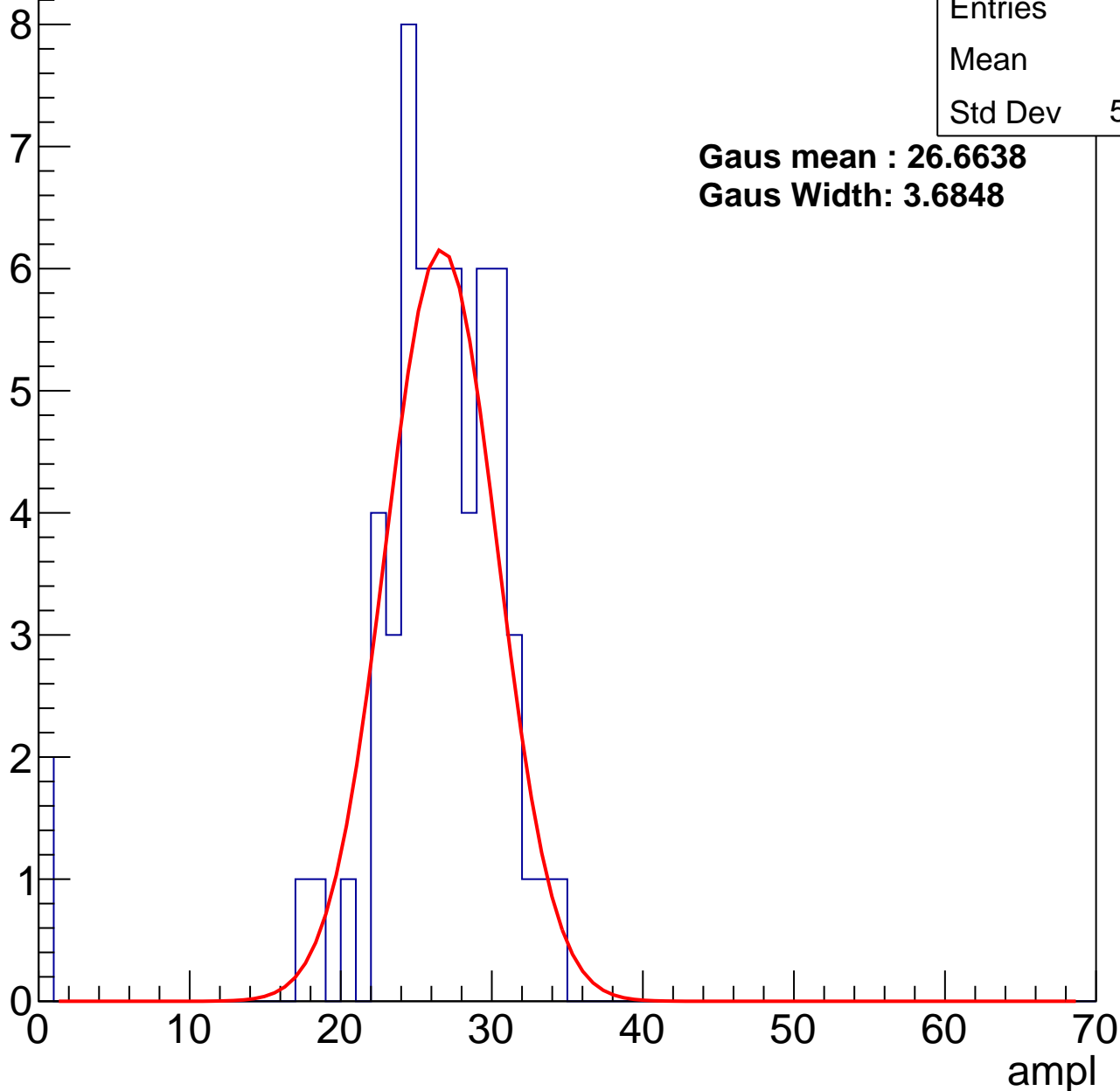
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	25.5
Std Dev	5.835

Gaus mean : 26.6638

Gaus Width: 3.6848



B1L102S, U14-ch110, adc1

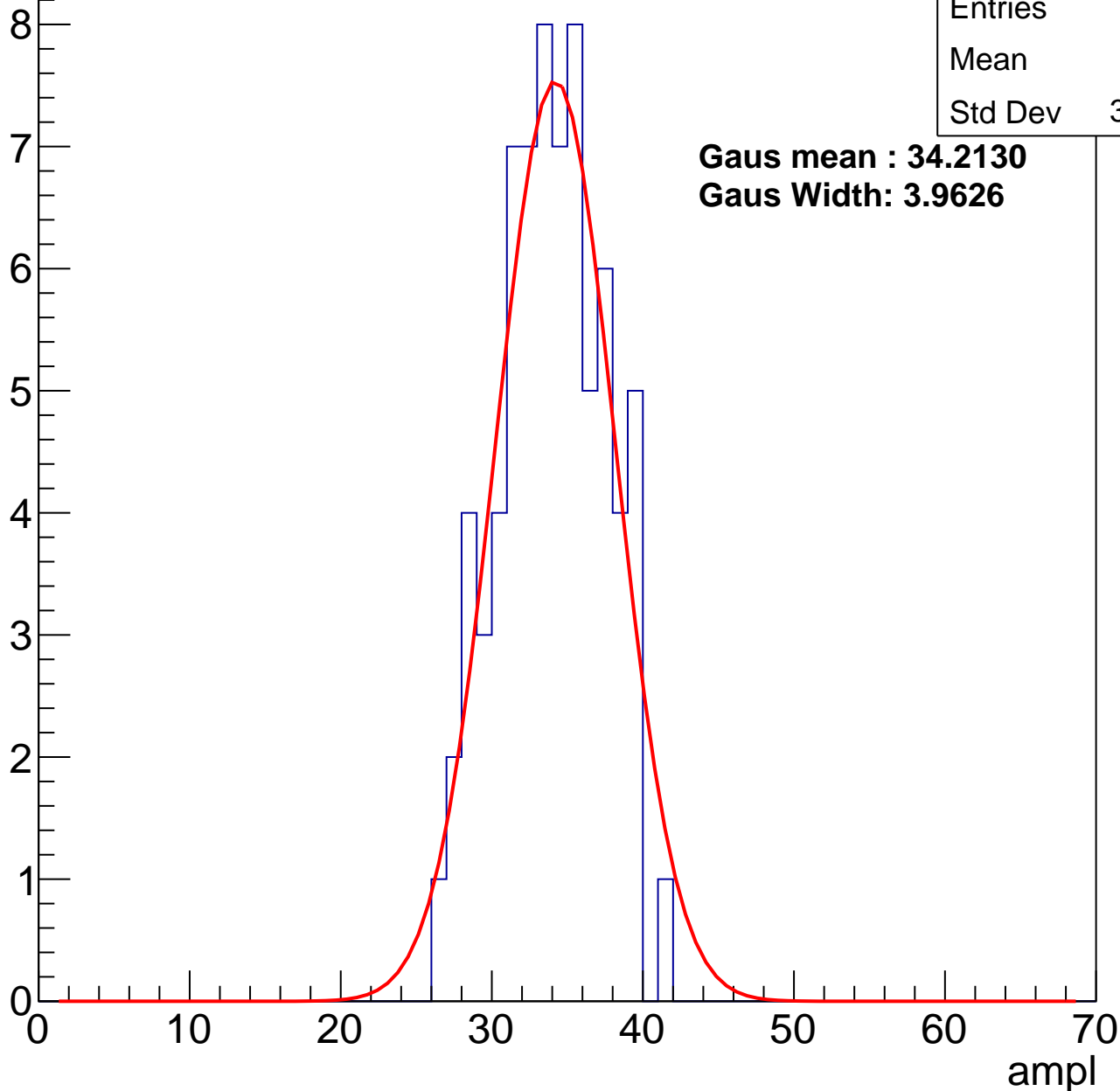
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	33.5
Std Dev	3.428

Gaus mean : 34.2130

Gaus Width: 3.9626



B1L102S, U14-ch110, adc2

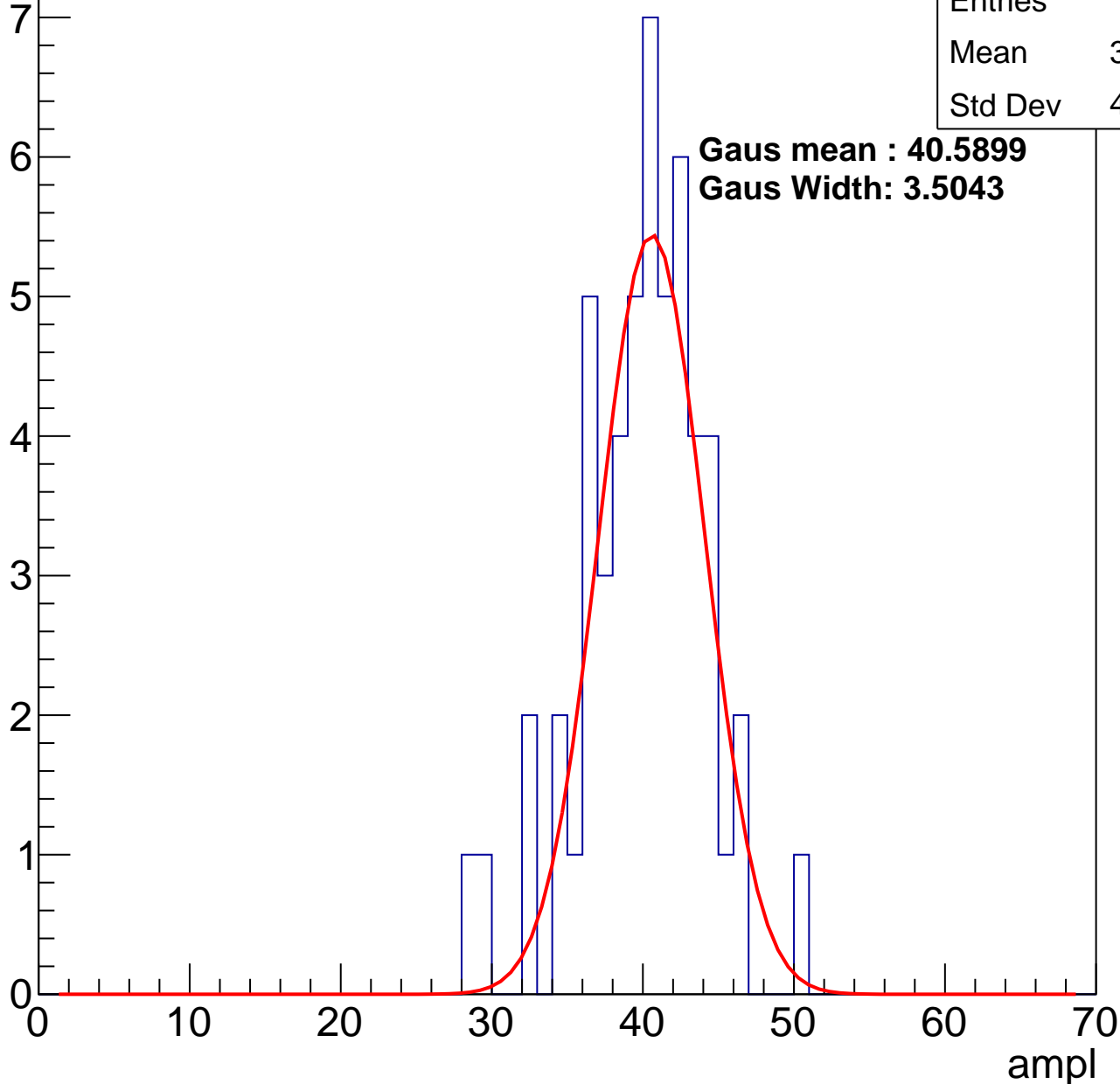
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	39.52
Std Dev	4.153

Gaus mean : 40.5899

Gaus Width: 3.5043

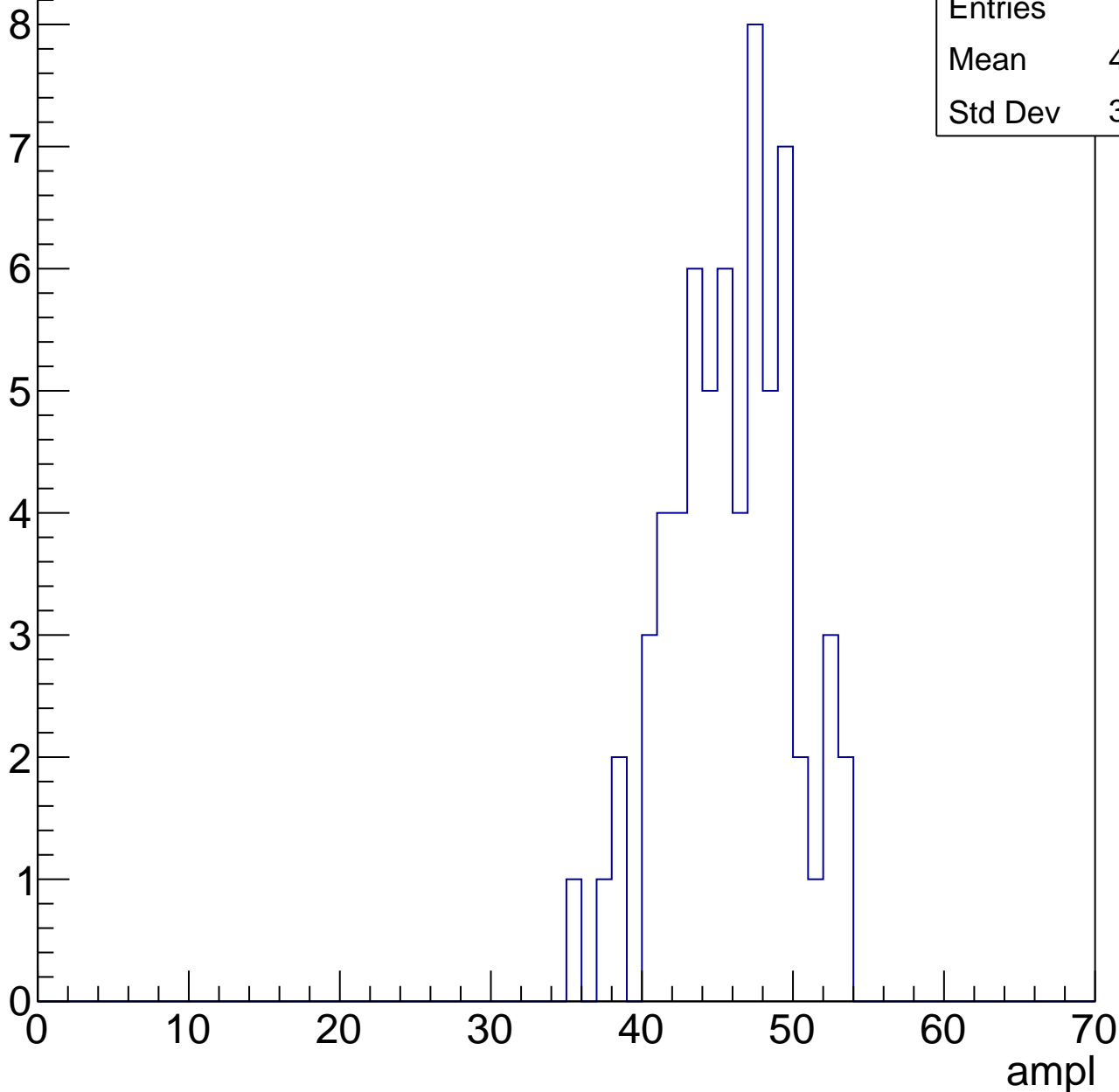


B1L102S, U14-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	45.38
Std Dev	3.975

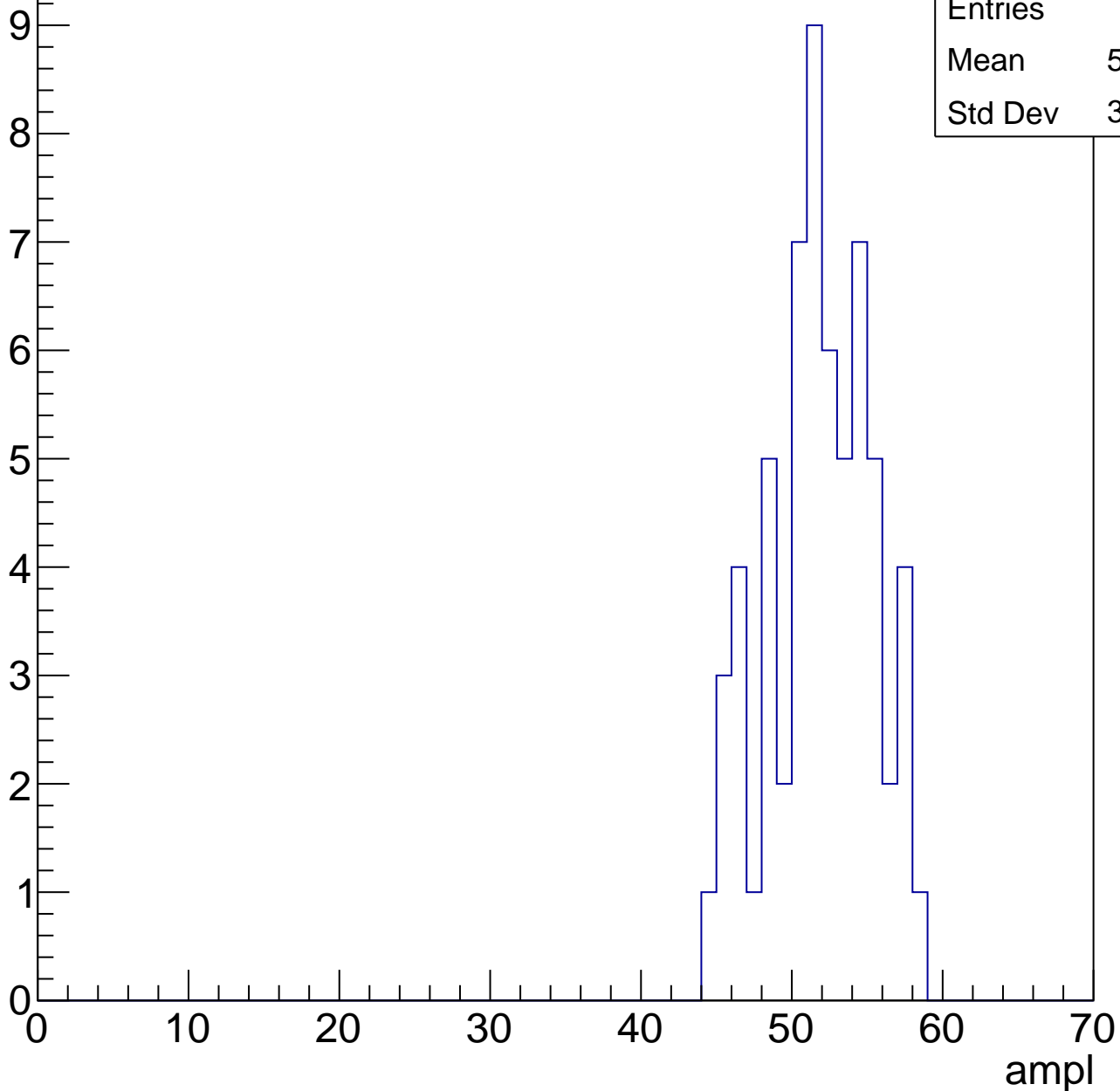


B1L102S, U14-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	51.37
Std Dev	3.447

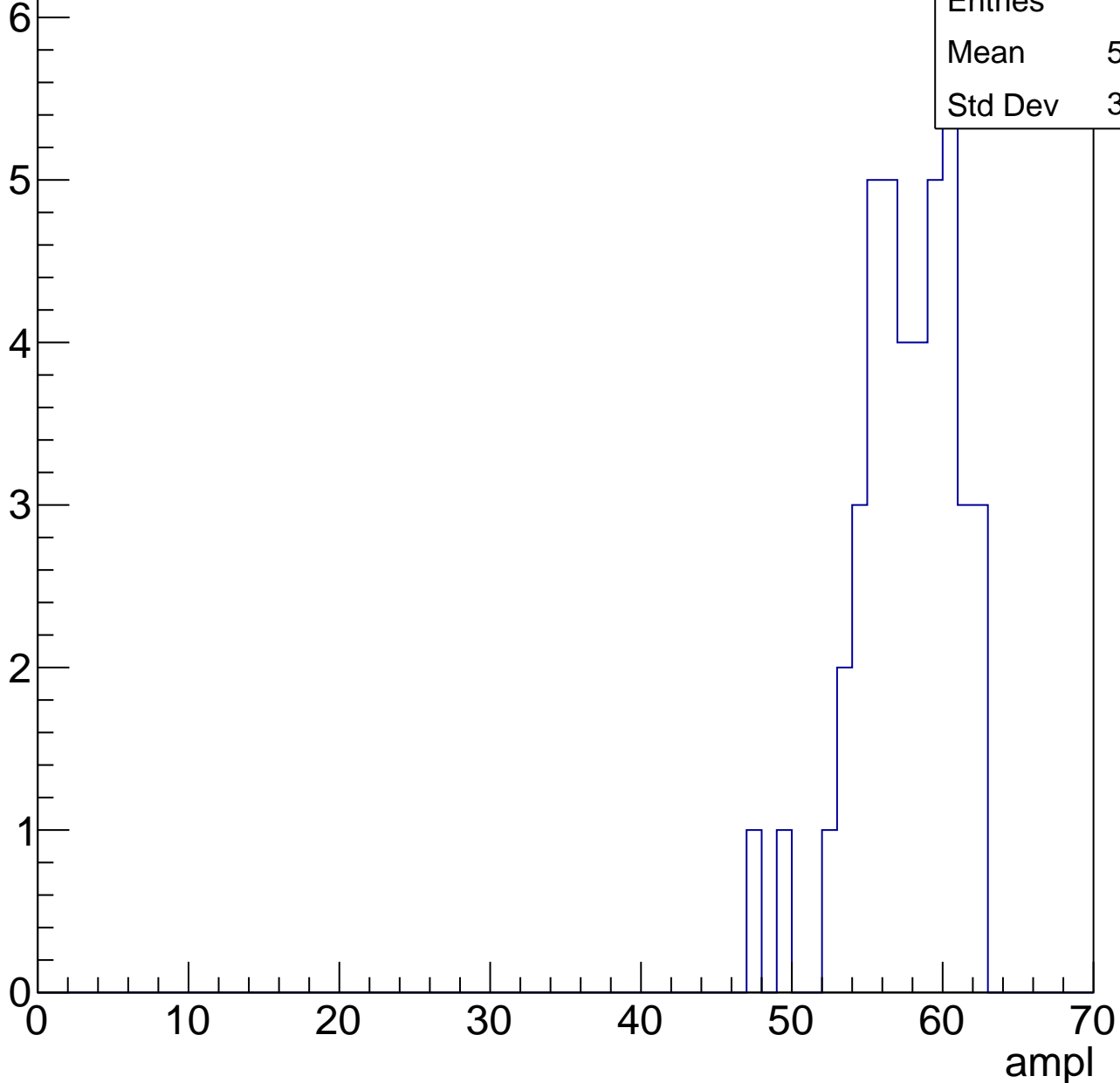


B1L102S, U14-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	57.09
Std Dev	3.319

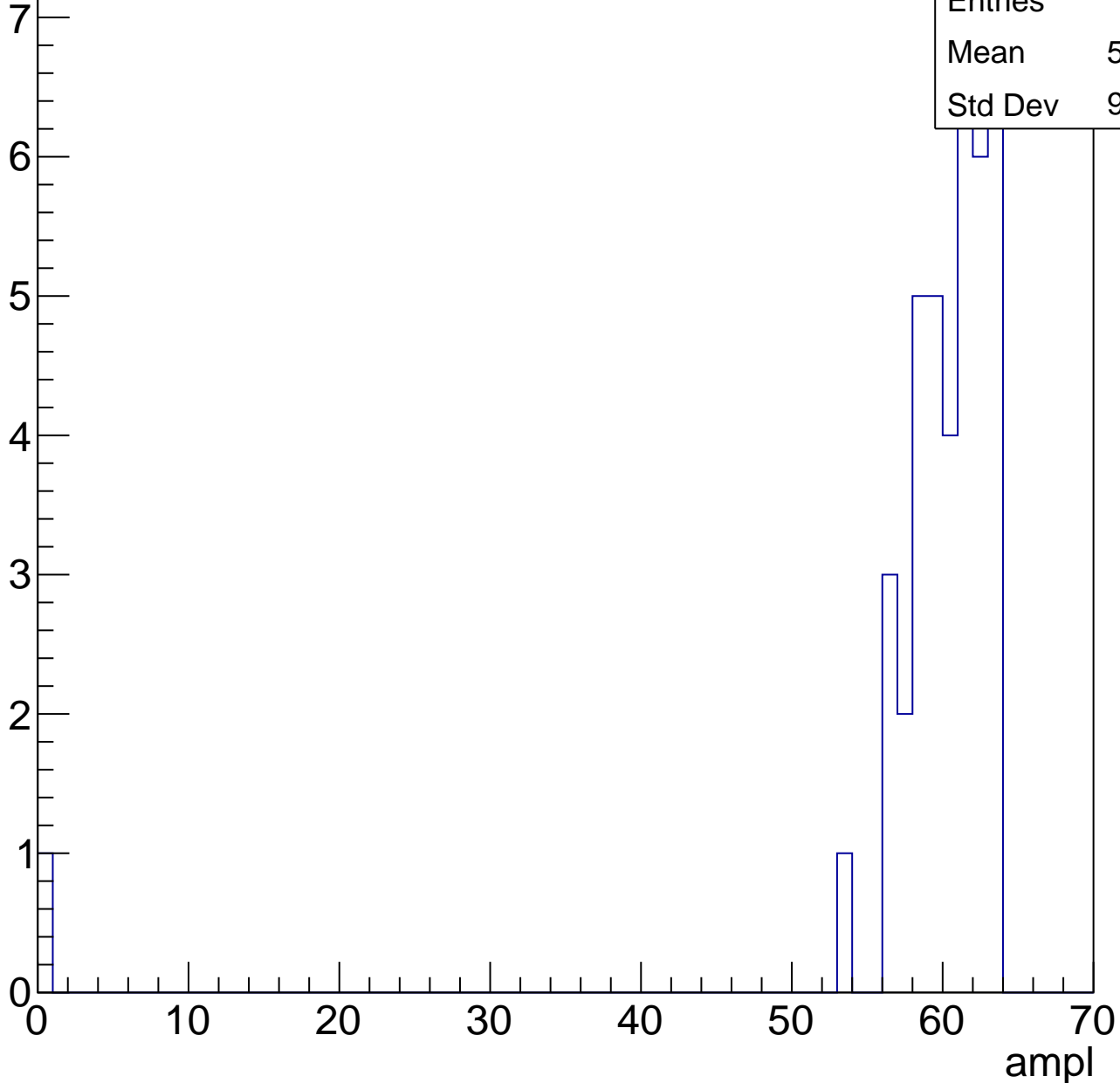


B1L102S, U14-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

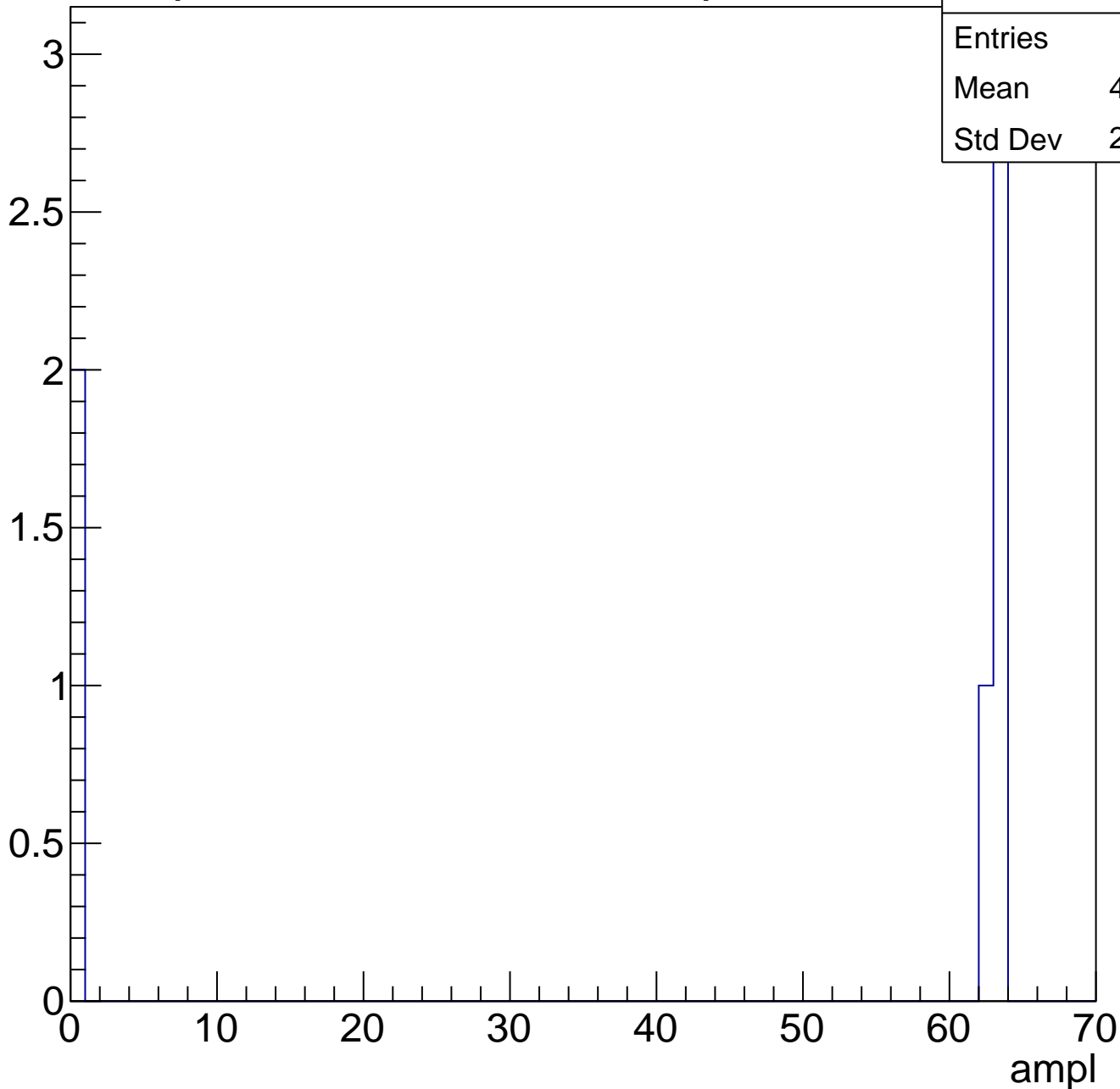
Entries	41
Mean	58.54
Std Dev	9.559



B1L102S, U14-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch111, adc0

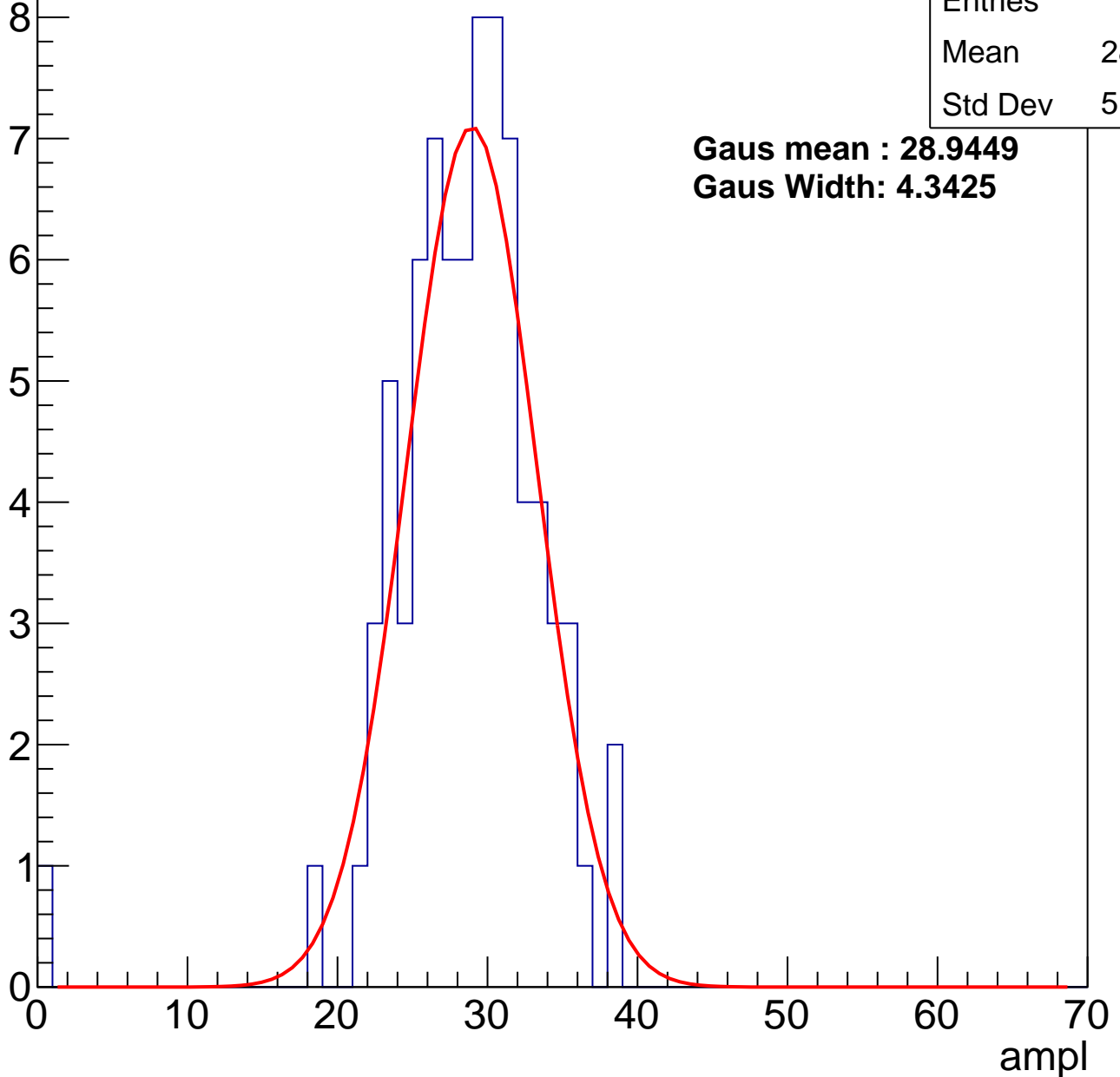
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.13
Std Dev	5.139

Gaus mean : 28.9449

Gaus Width: 4.3425



B1L102S, U14-ch111, adc1

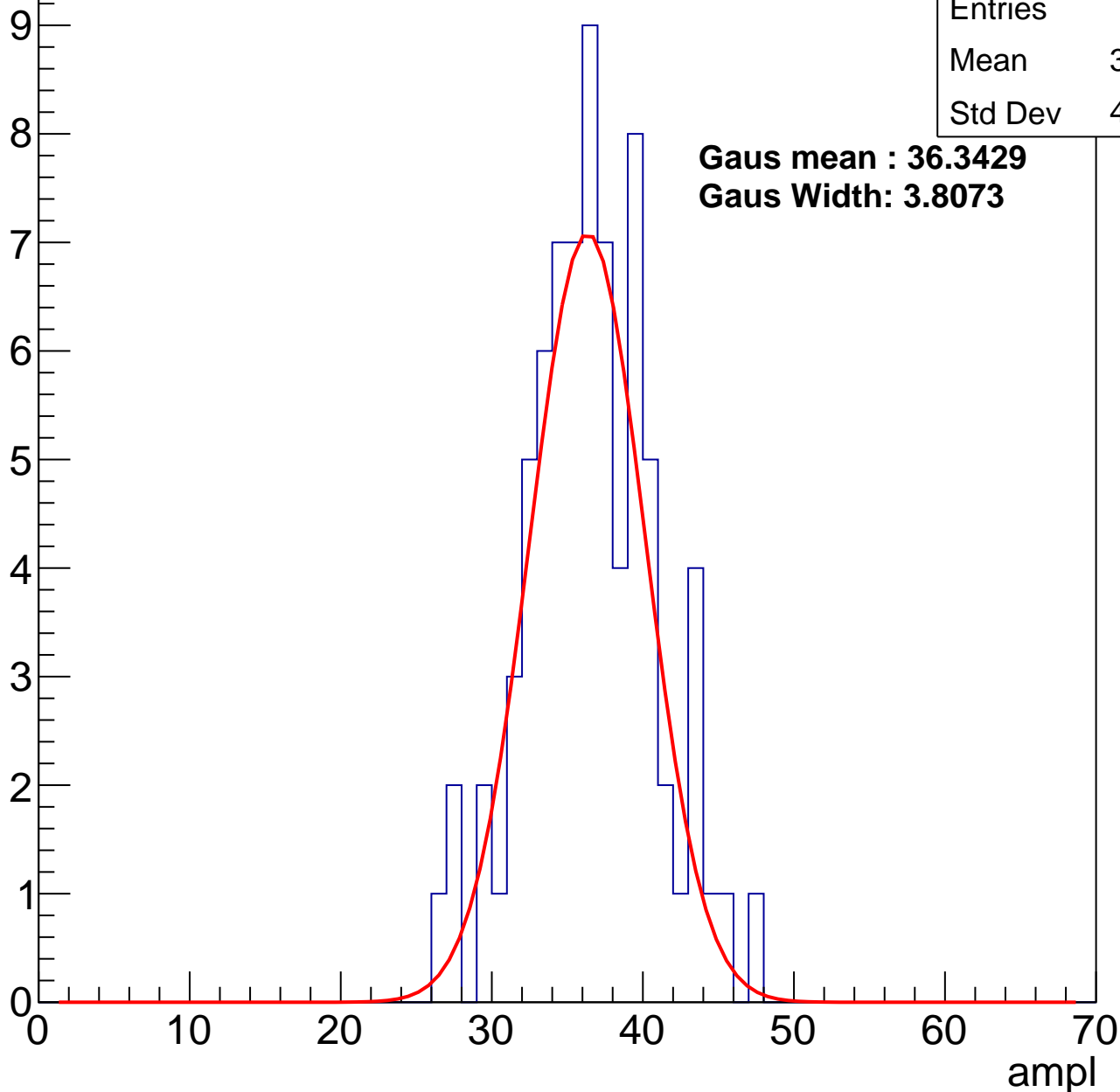
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.12
Std Dev	4.206

Gaus mean : 36.3429

Gaus Width: 3.8073



B1L102S, U14-ch111, adc2

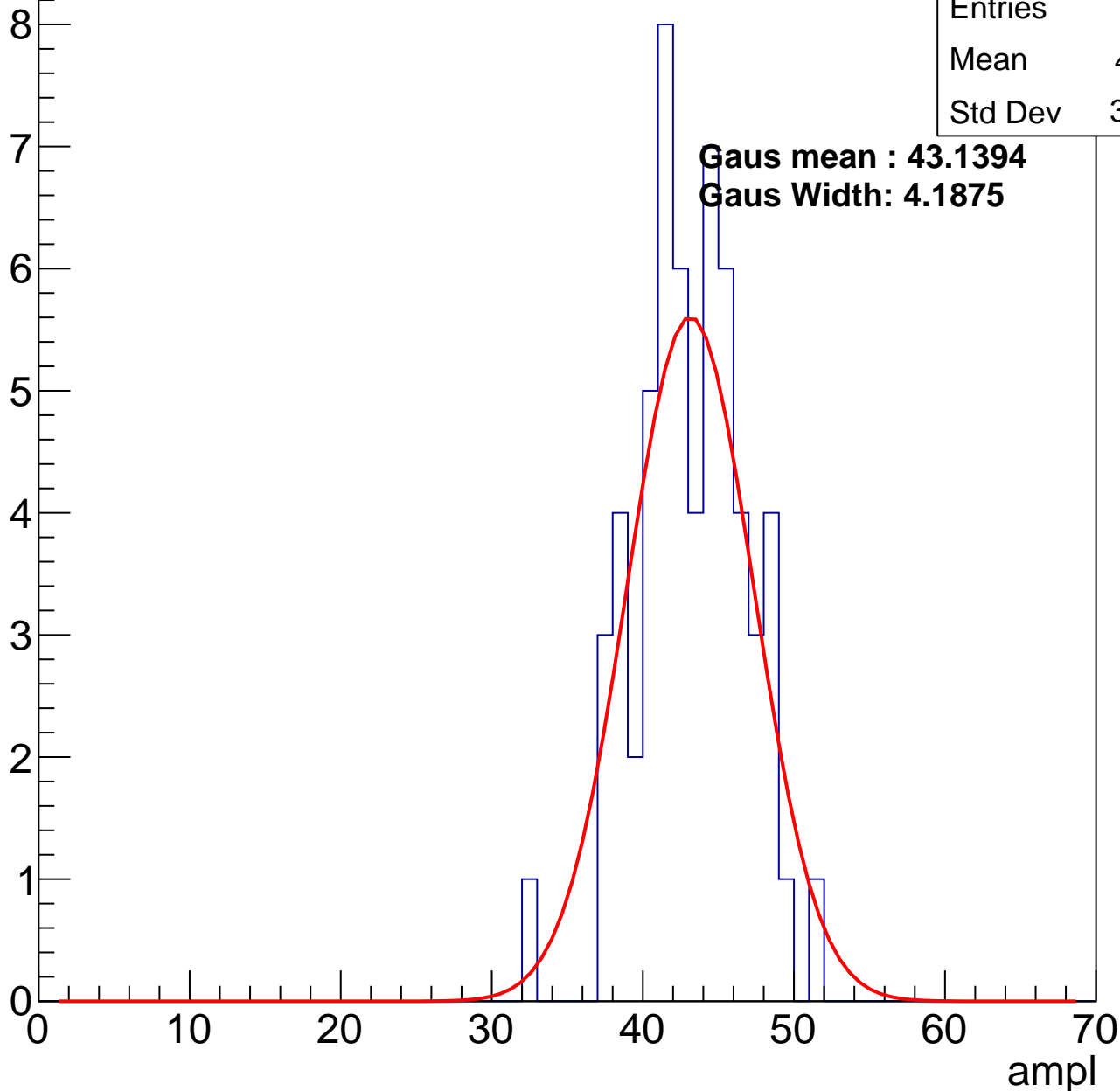
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.71
Std Dev	3.575

Gaus mean : 43.1394

Gaus Width: 4.1875

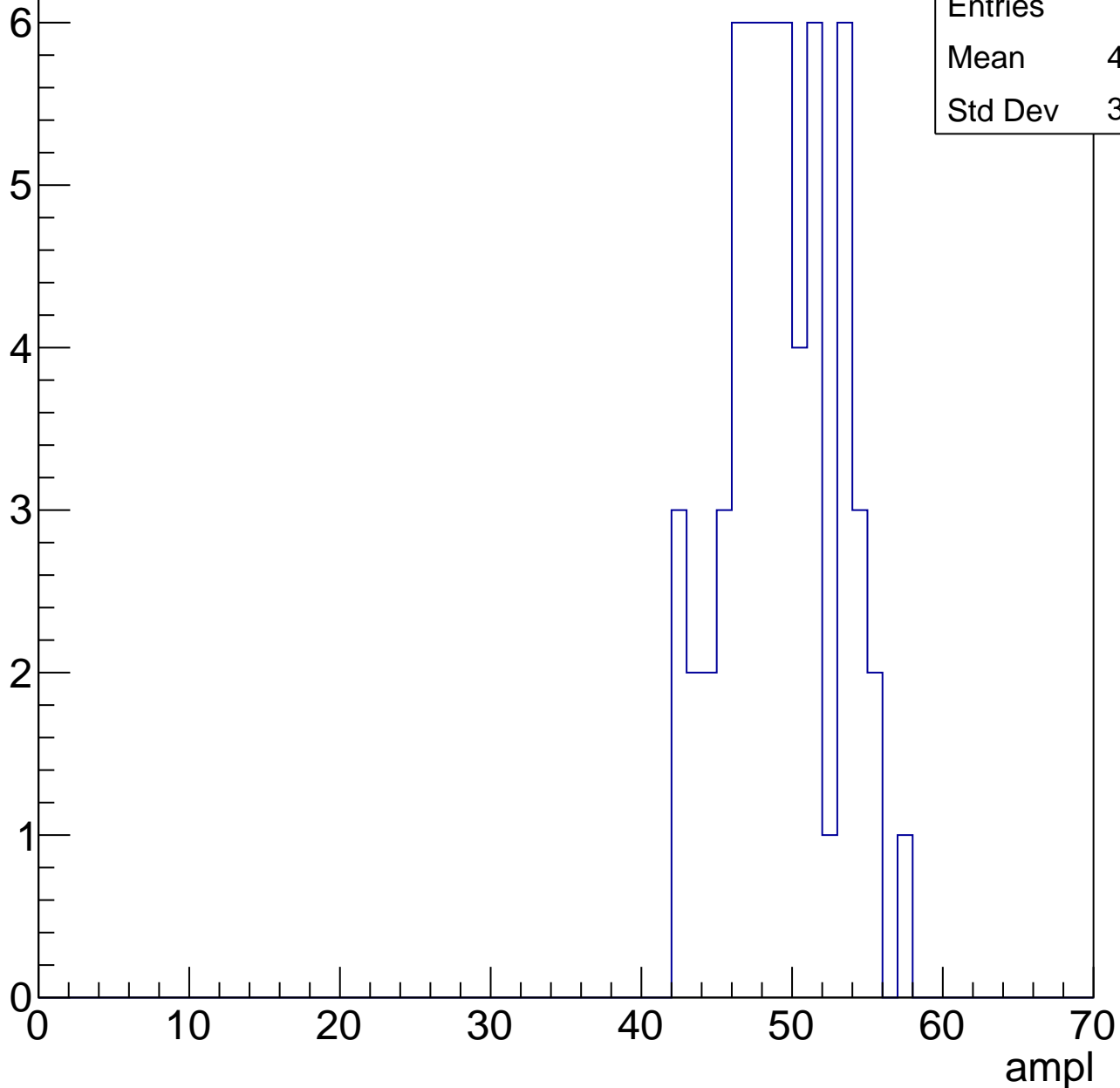


B1L102S, U14-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.77
Std Dev	3.608

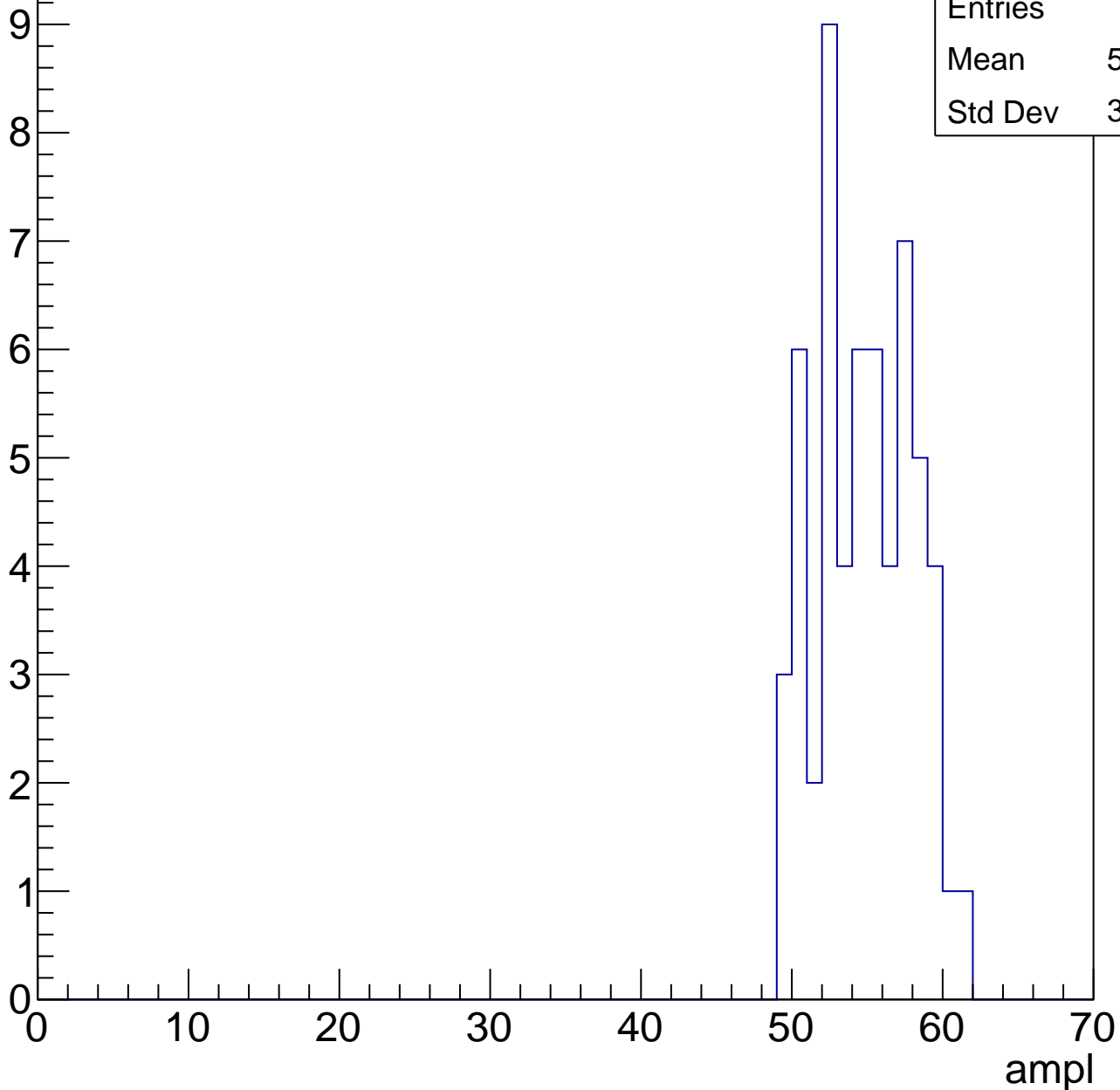


B1L102S, U14-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.36
Std Dev	3.139

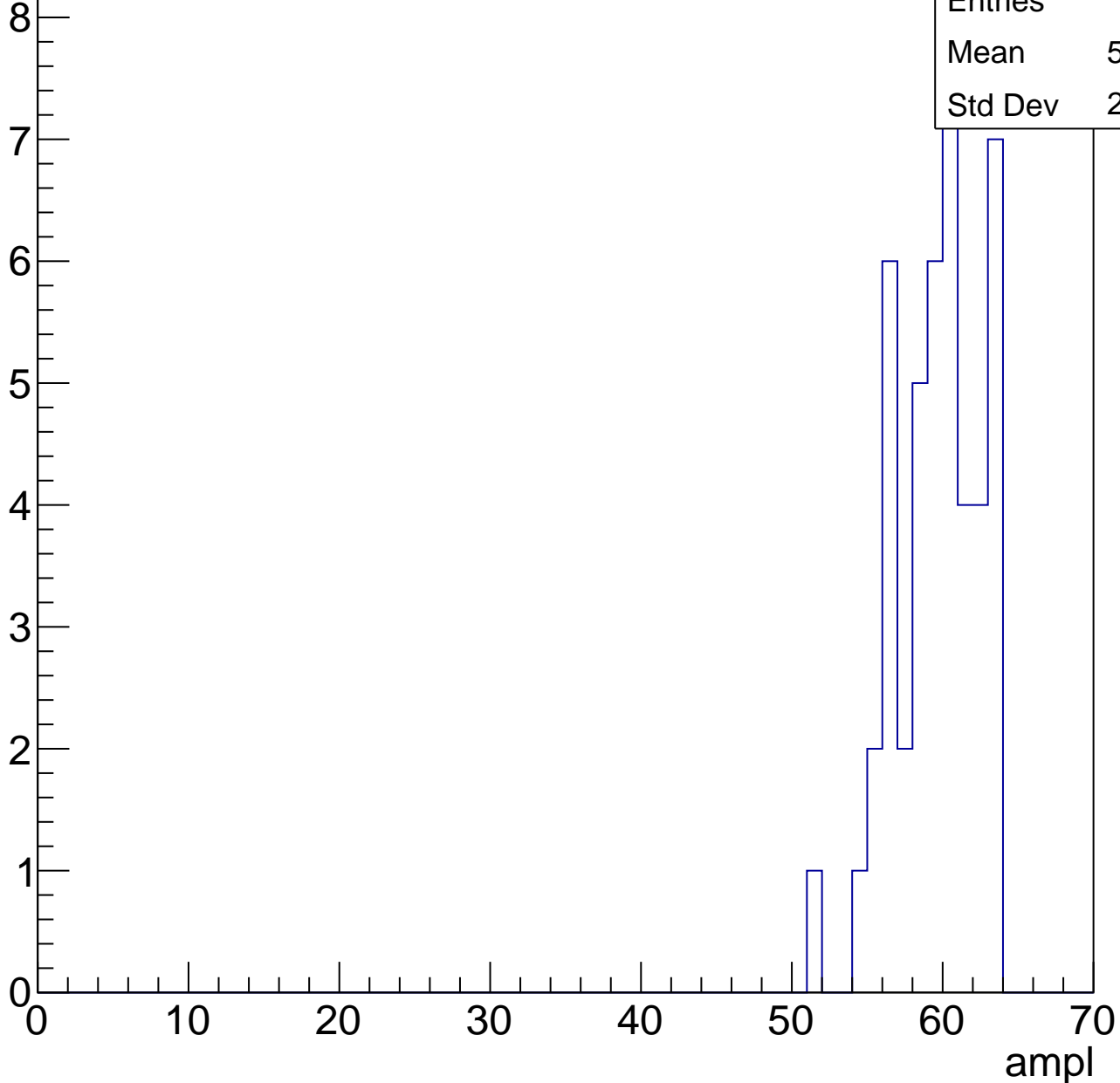


B1L102S, U14-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.17
Std Dev	2.792

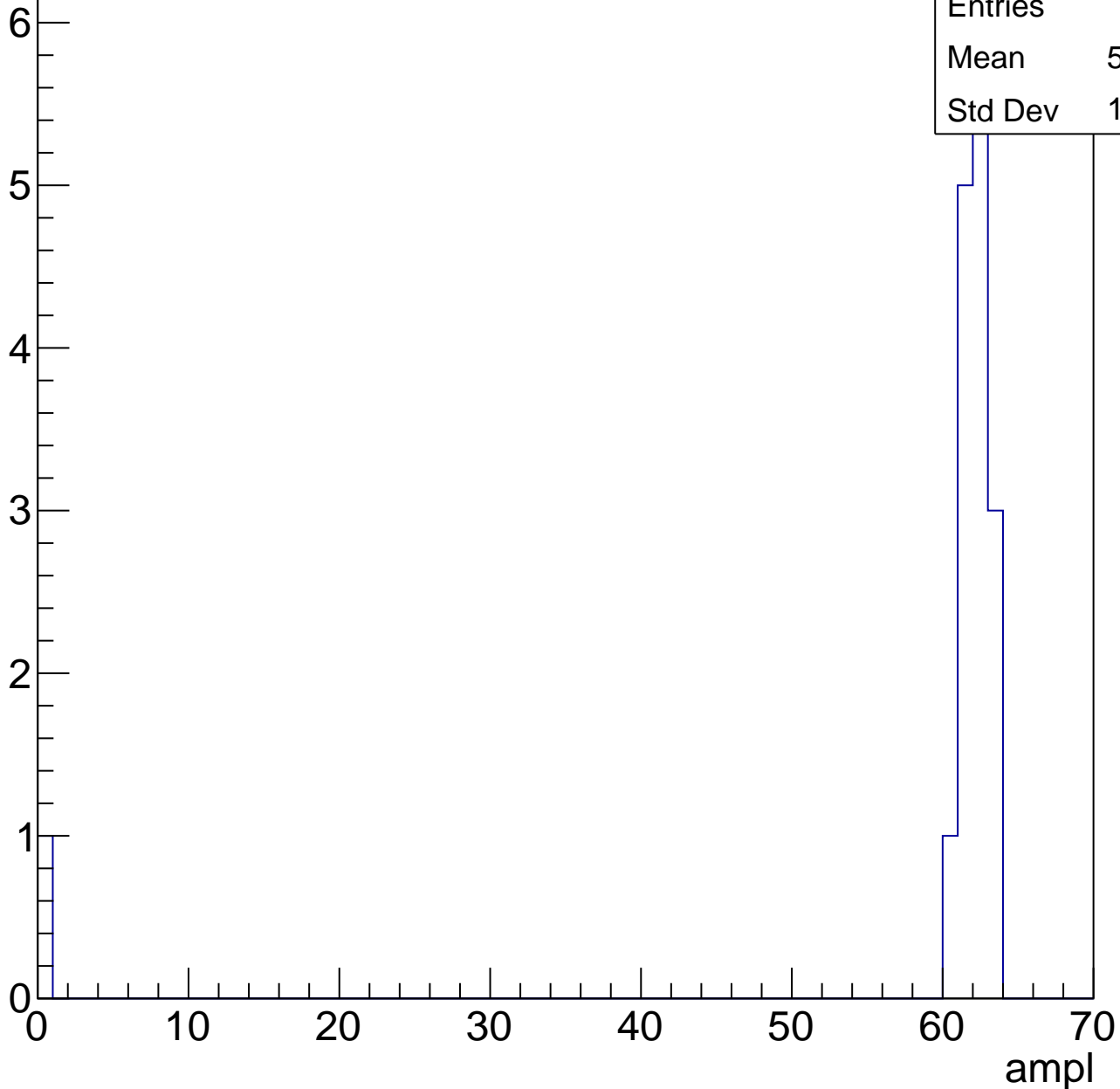


B1L102S, U14-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.88
Std Dev	14.97



B1L102S, U14-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U14-ch112, adc0

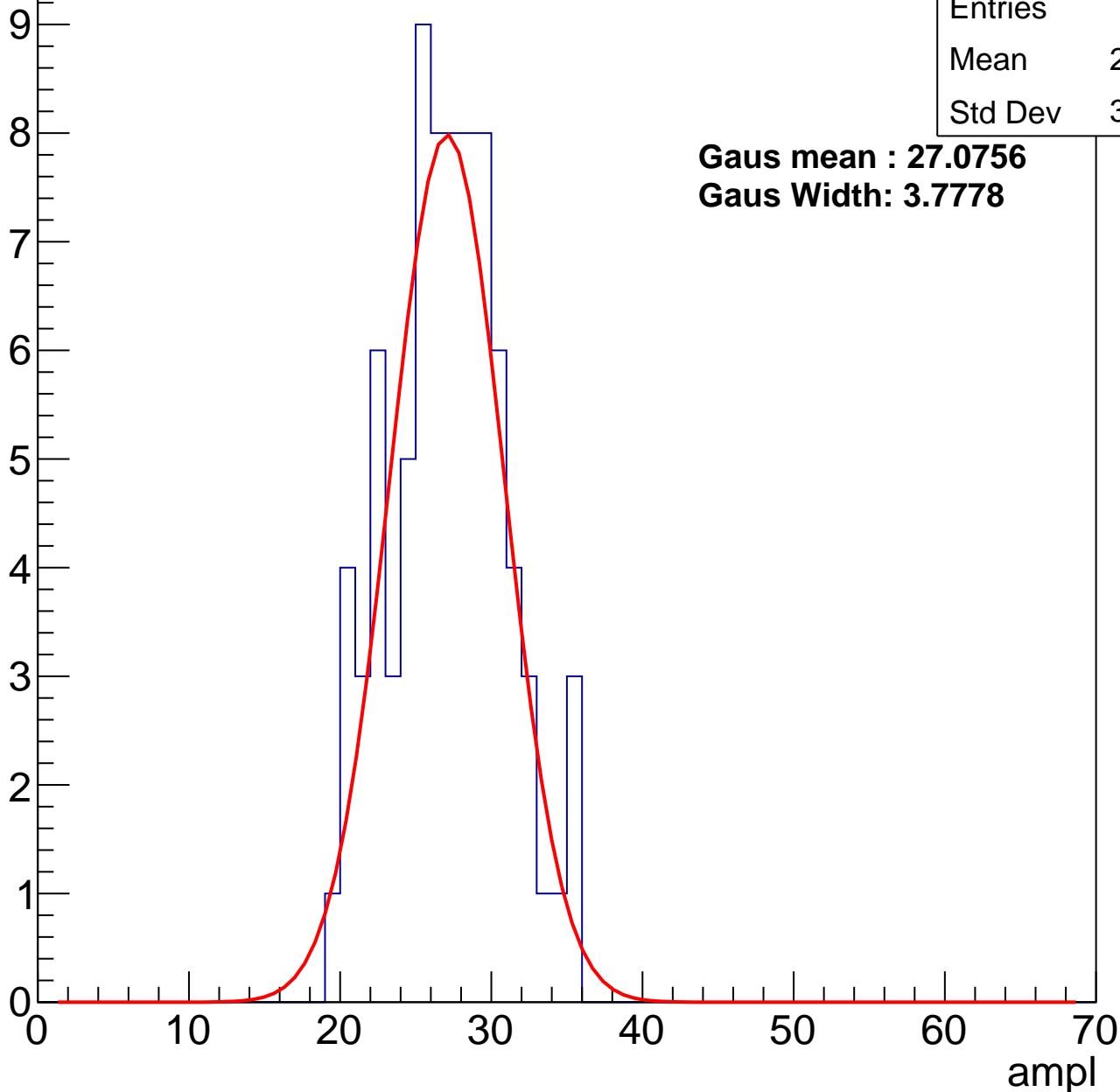
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	26.67
Std Dev	3.778

Gaus mean : 27.0756

Gaus Width: 3.7778



B1L102S, U14-ch112, adc1

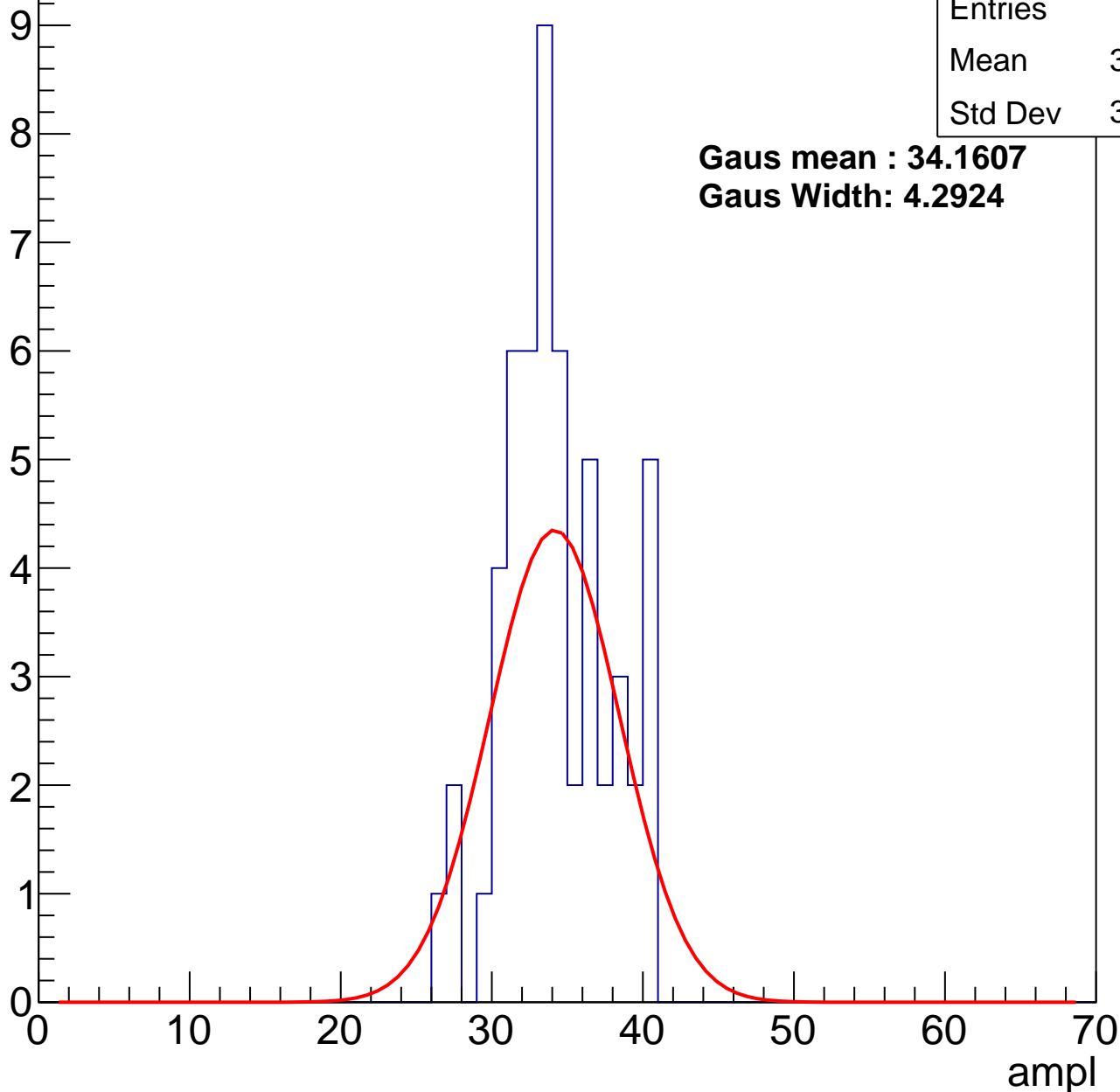
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	33.78
Std Dev	3.478

Gaus mean : 34.1607

Gaus Width: 4.2924



B1L102S, U14-ch112, adc2

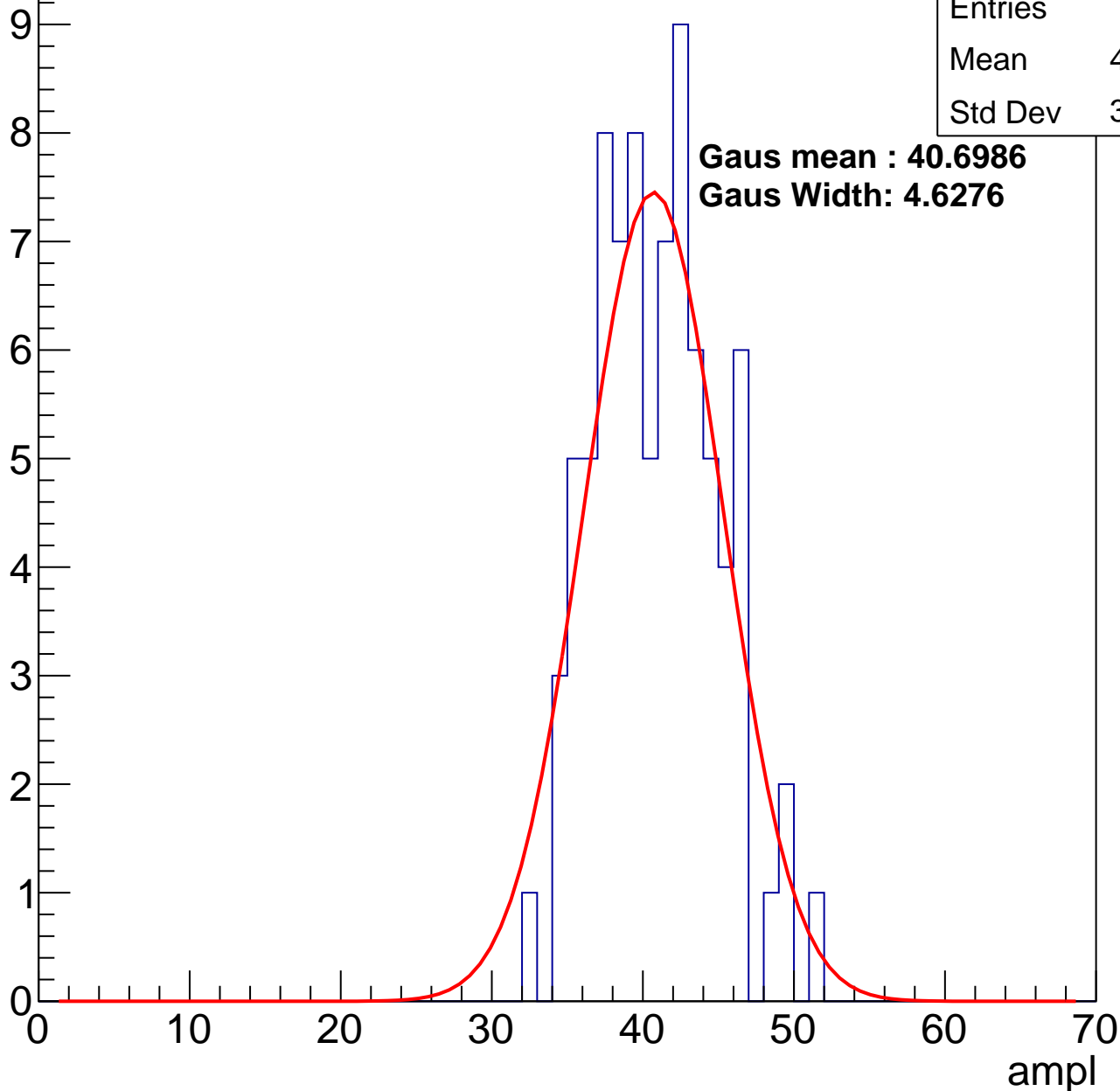
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	40.47
Std Dev	3.968

Gaus mean : 40.6986

Gaus Width: 4.6276

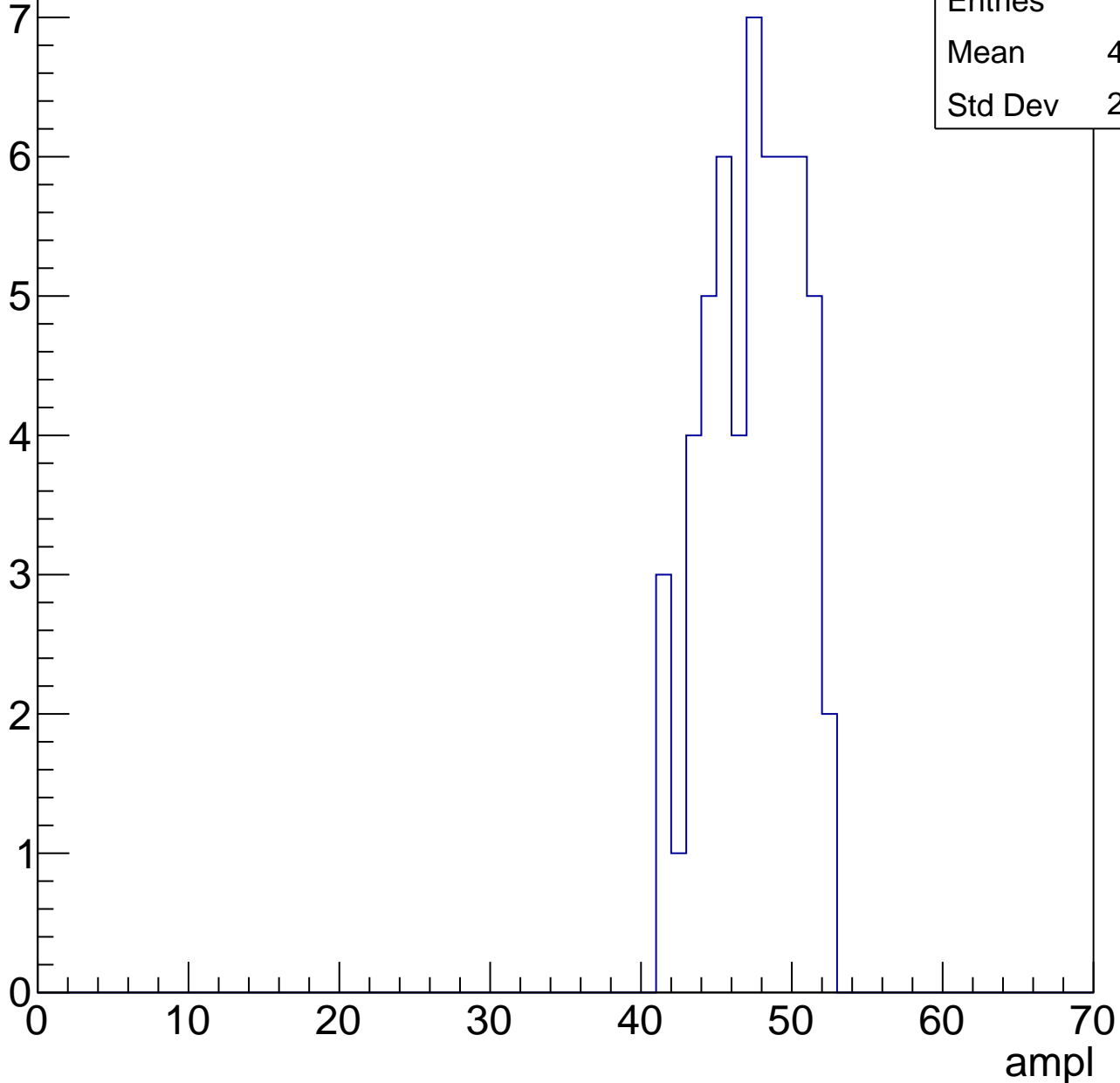


B1L102S, U14-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	46.93
Std Dev	2.966

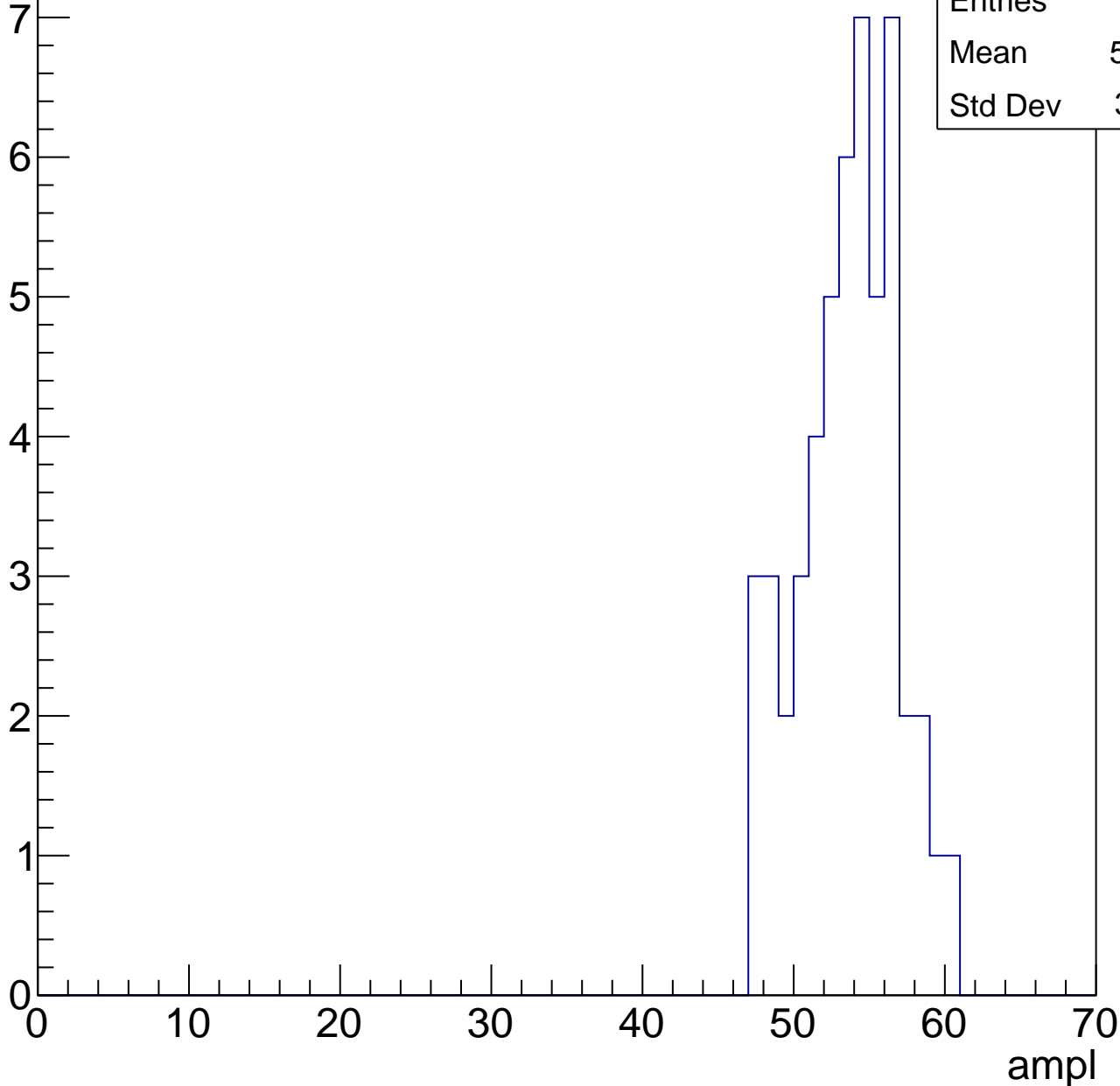


B1L102S, U14-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	53.12
Std Dev	3.191

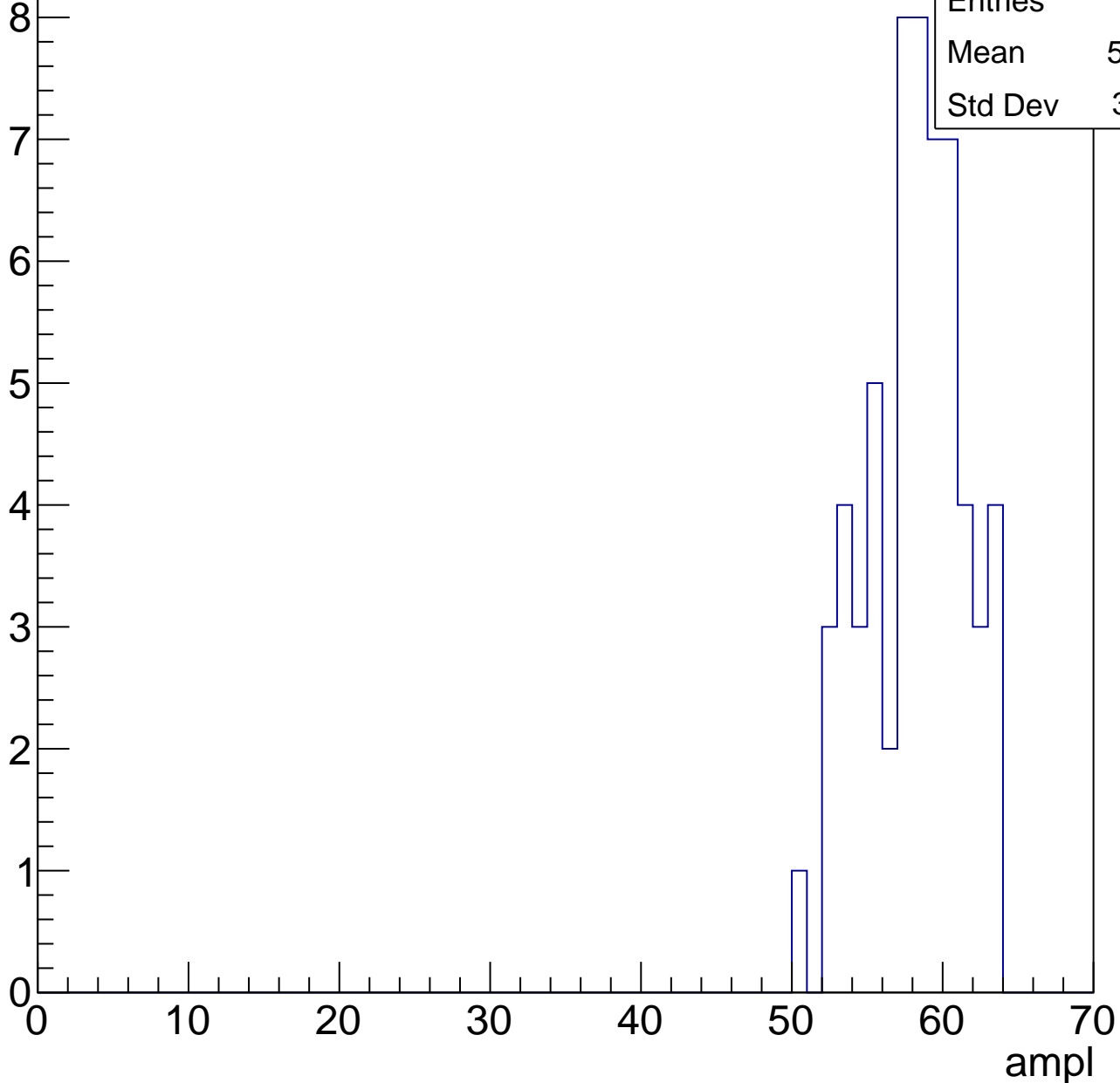


B1L102S, U14-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.66
Std Dev	3.171

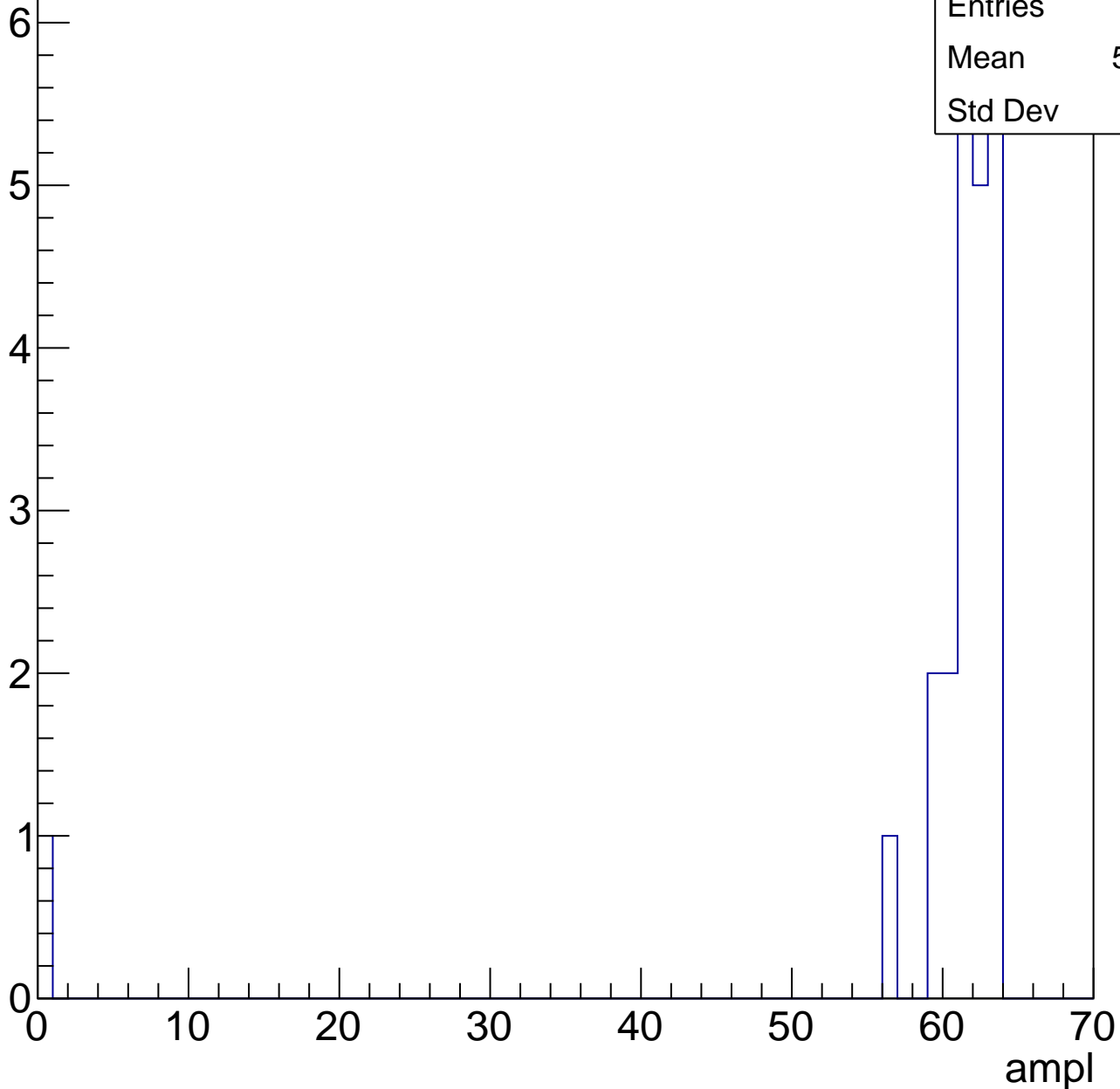


B1L102S, U14-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

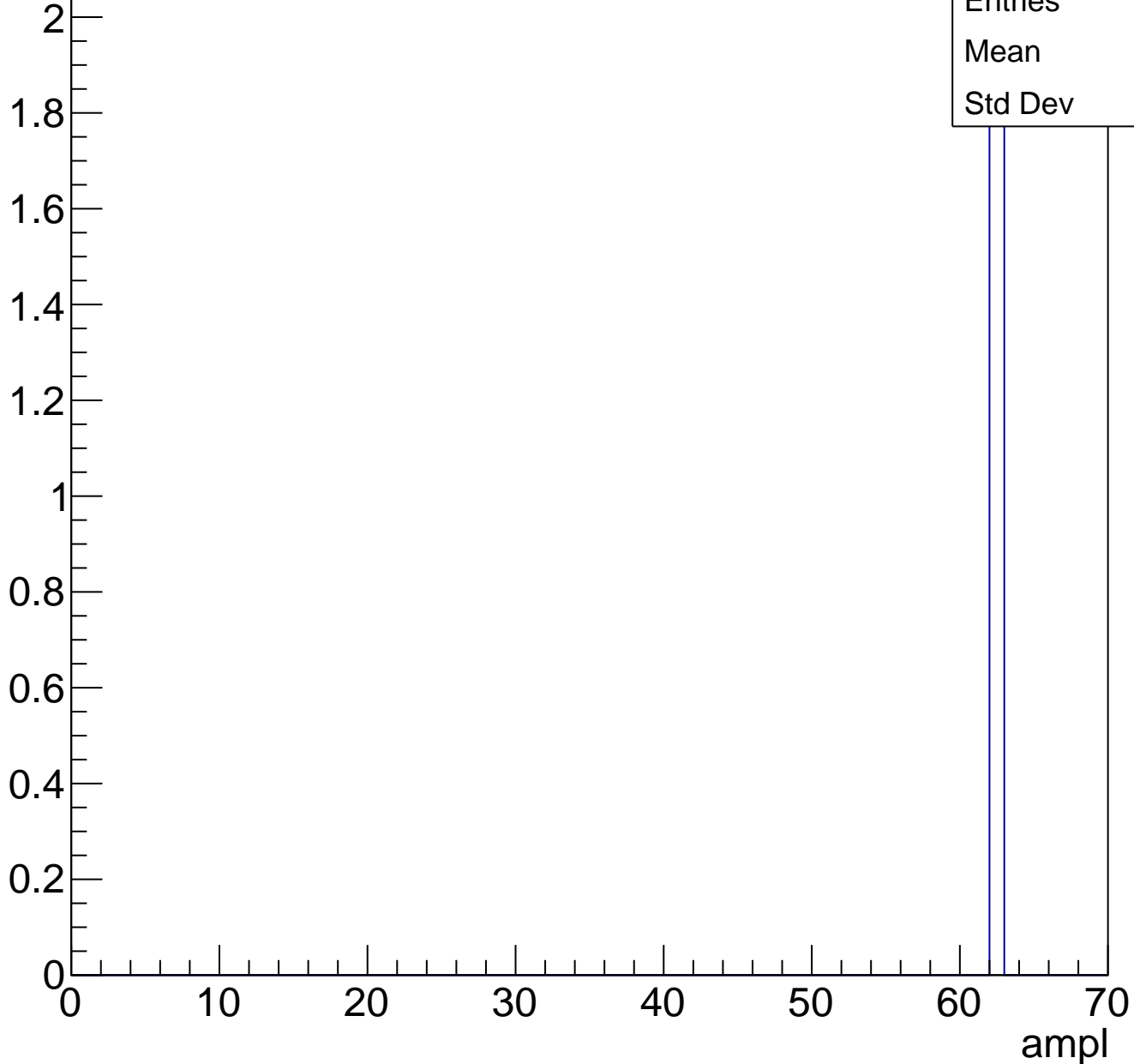
Entries	23
Mean	58.61
Std Dev	12.6



B1L102S, U14-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch113, adc0

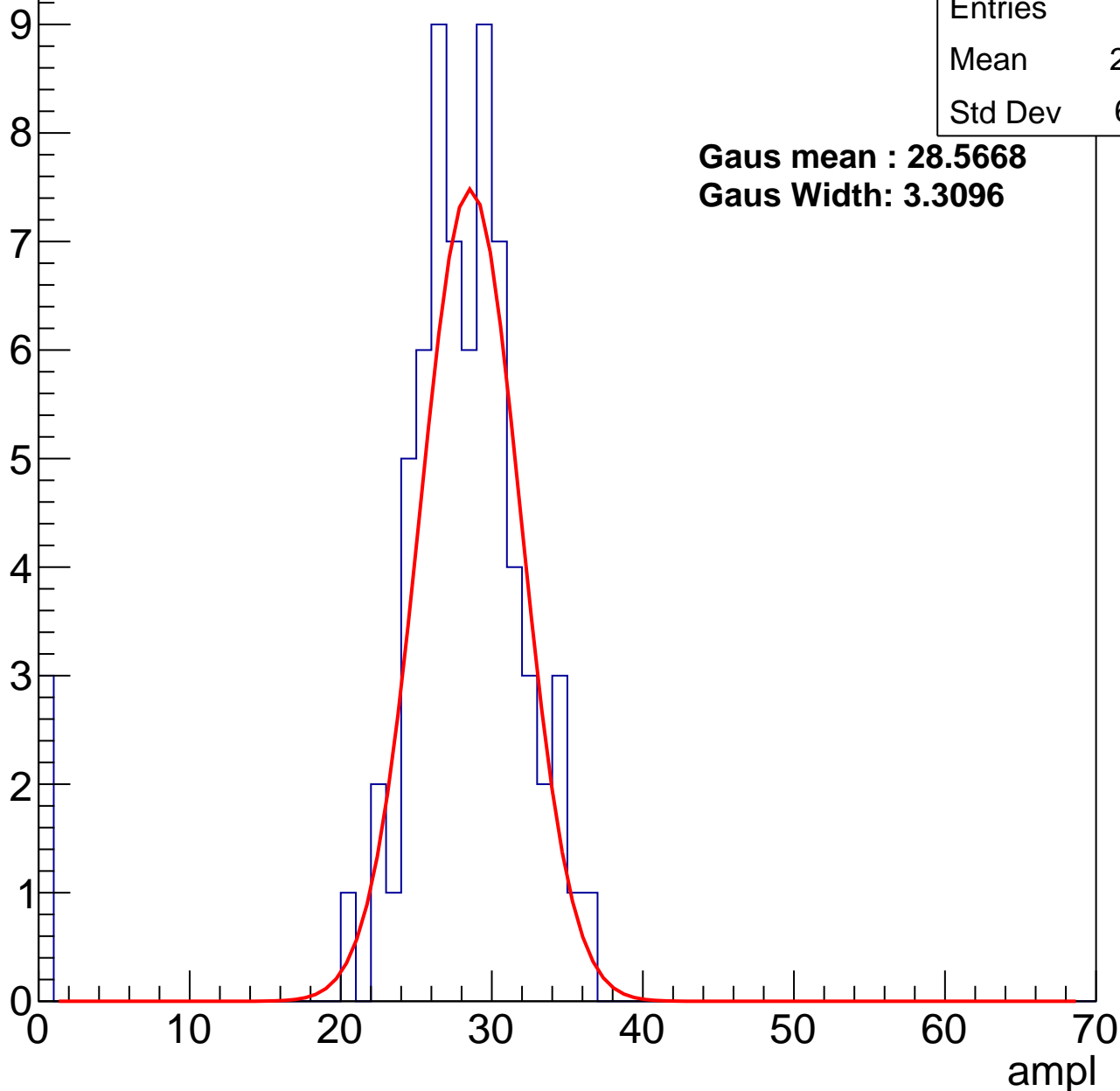
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	26.83
Std Dev	6.531

Gaus mean : 28.5668

Gaus Width: 3.3096



B1L102S, U14-ch113, adc1

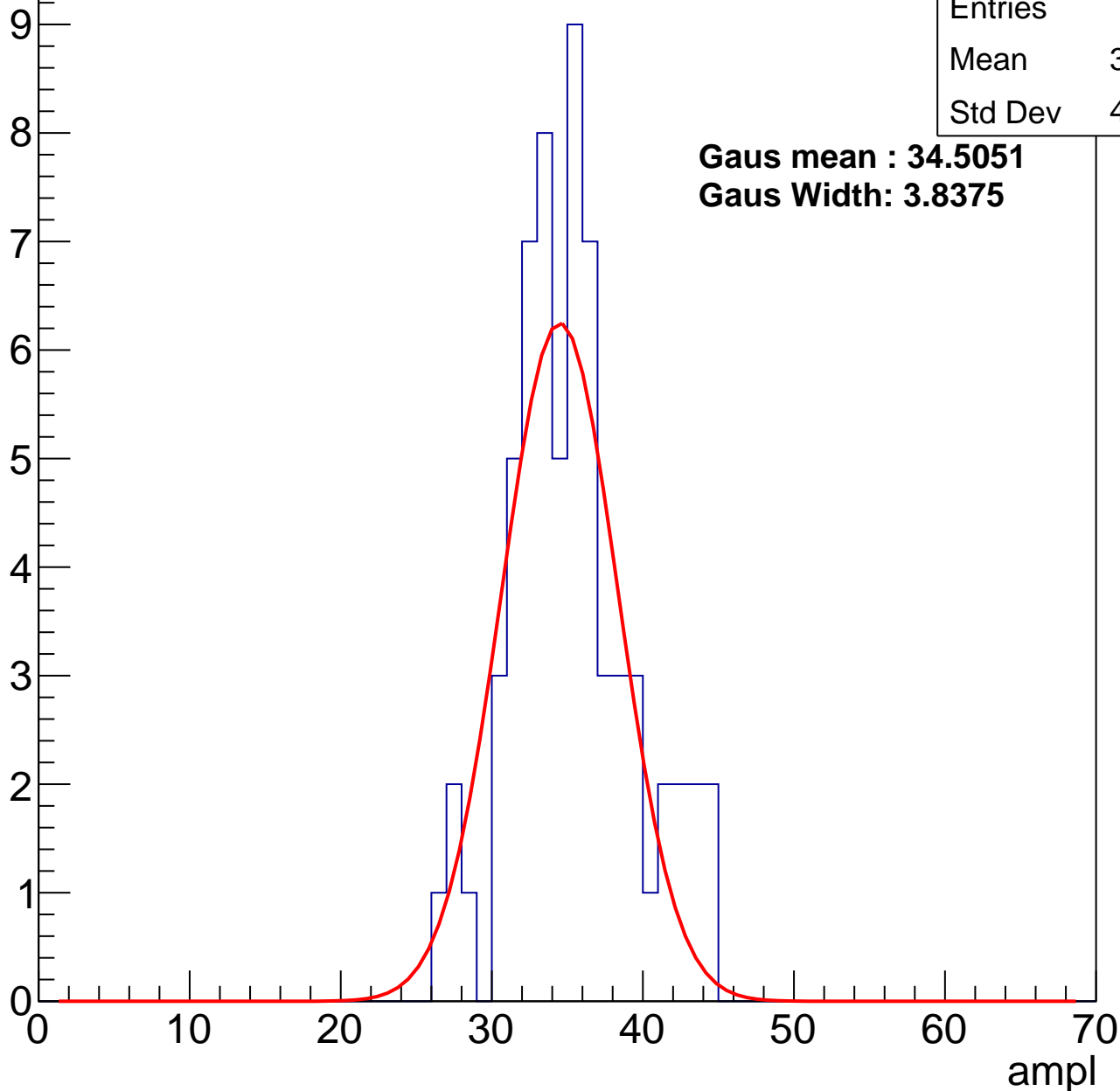
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.85
Std Dev	4.065

Gaus mean : 34.5051

Gaus Width: 3.8375



B1L102S, U14-ch113, adc2

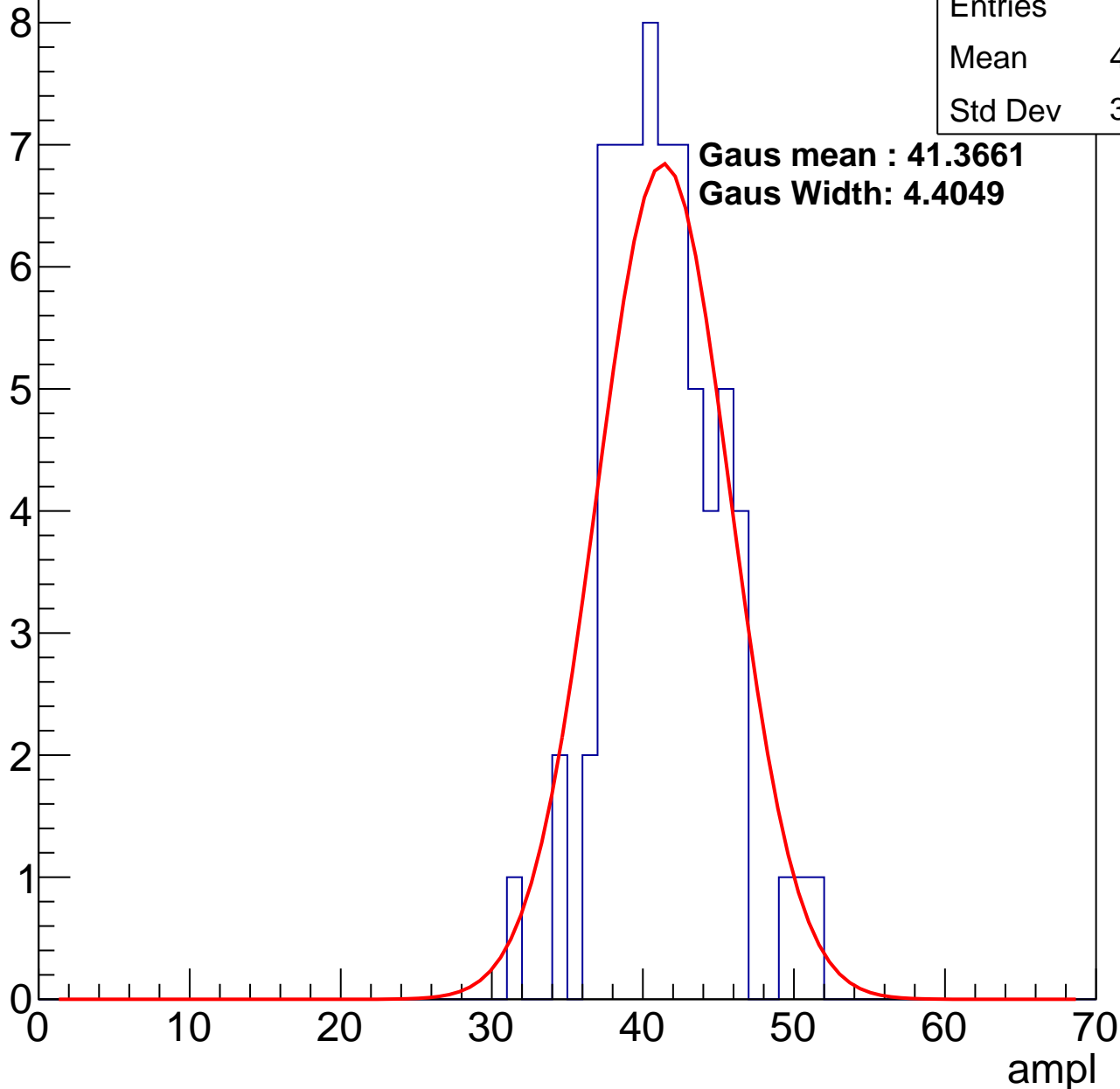
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	40.87
Std Dev	3.702

Gaus mean : 41.3661

Gaus Width: 4.4049



B1L102S, U14-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

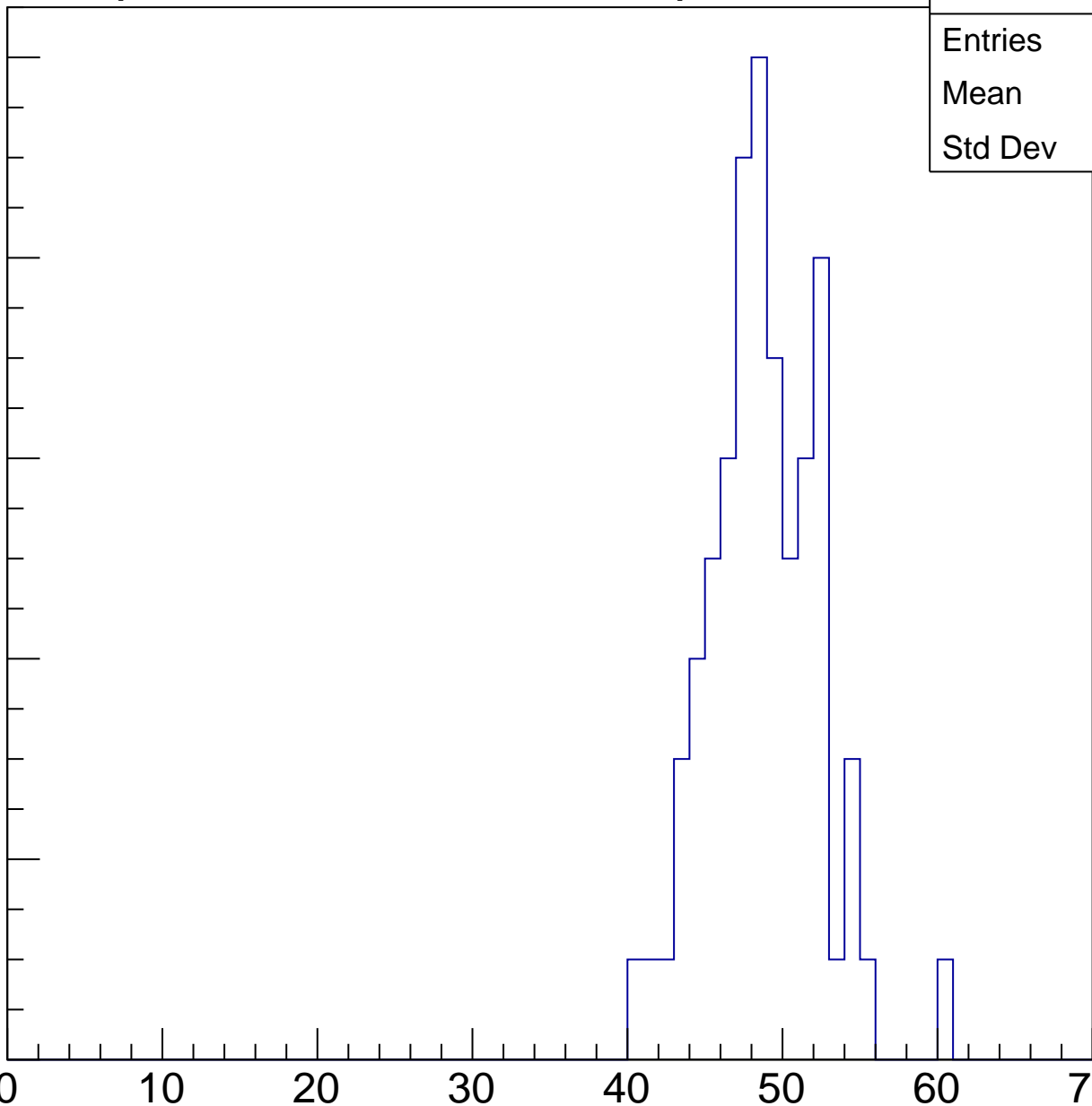
Entries	72
Mean	48.29
Std Dev	3.525

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

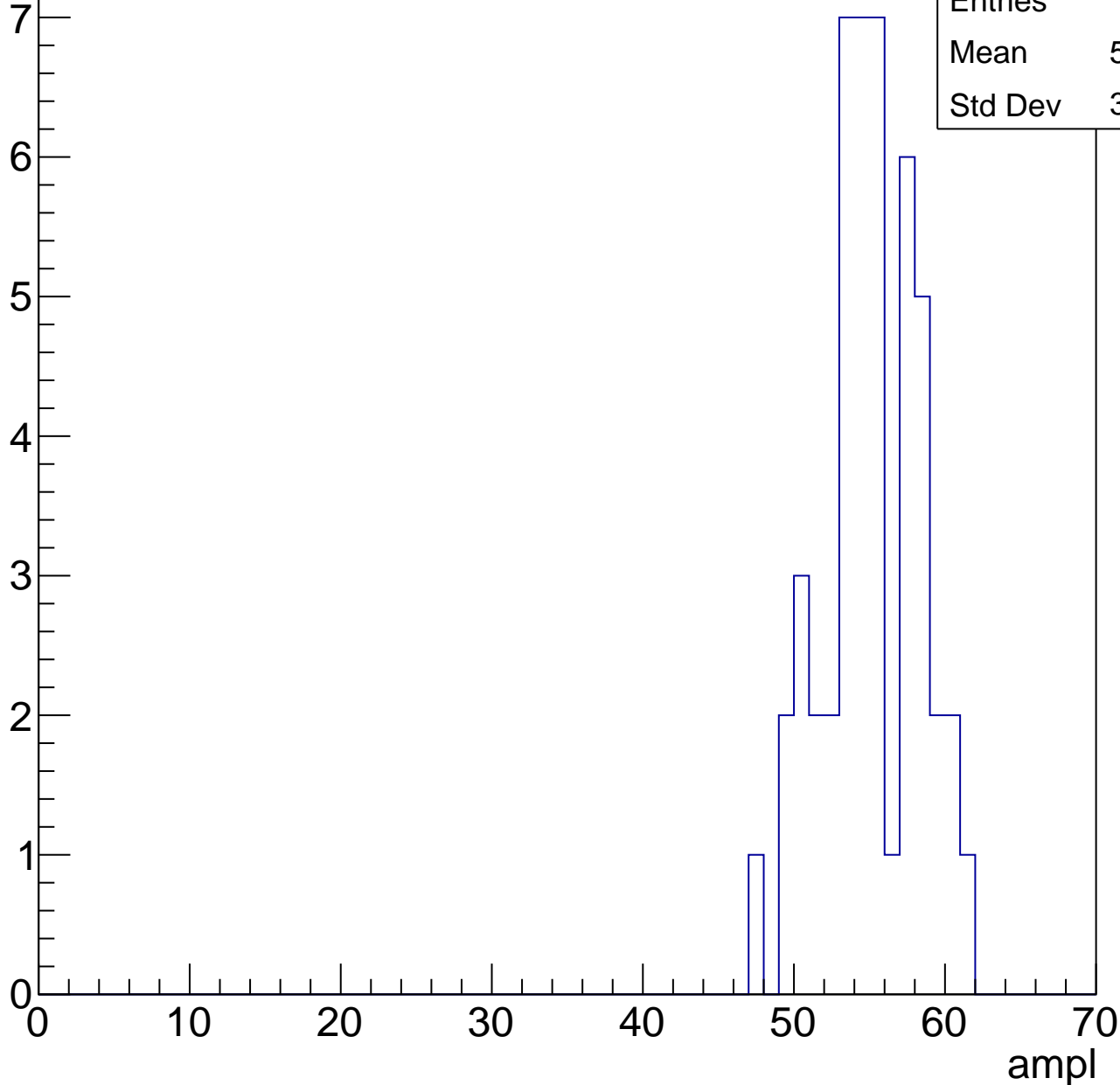


B1L102S, U14-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.62
Std Dev	3.153

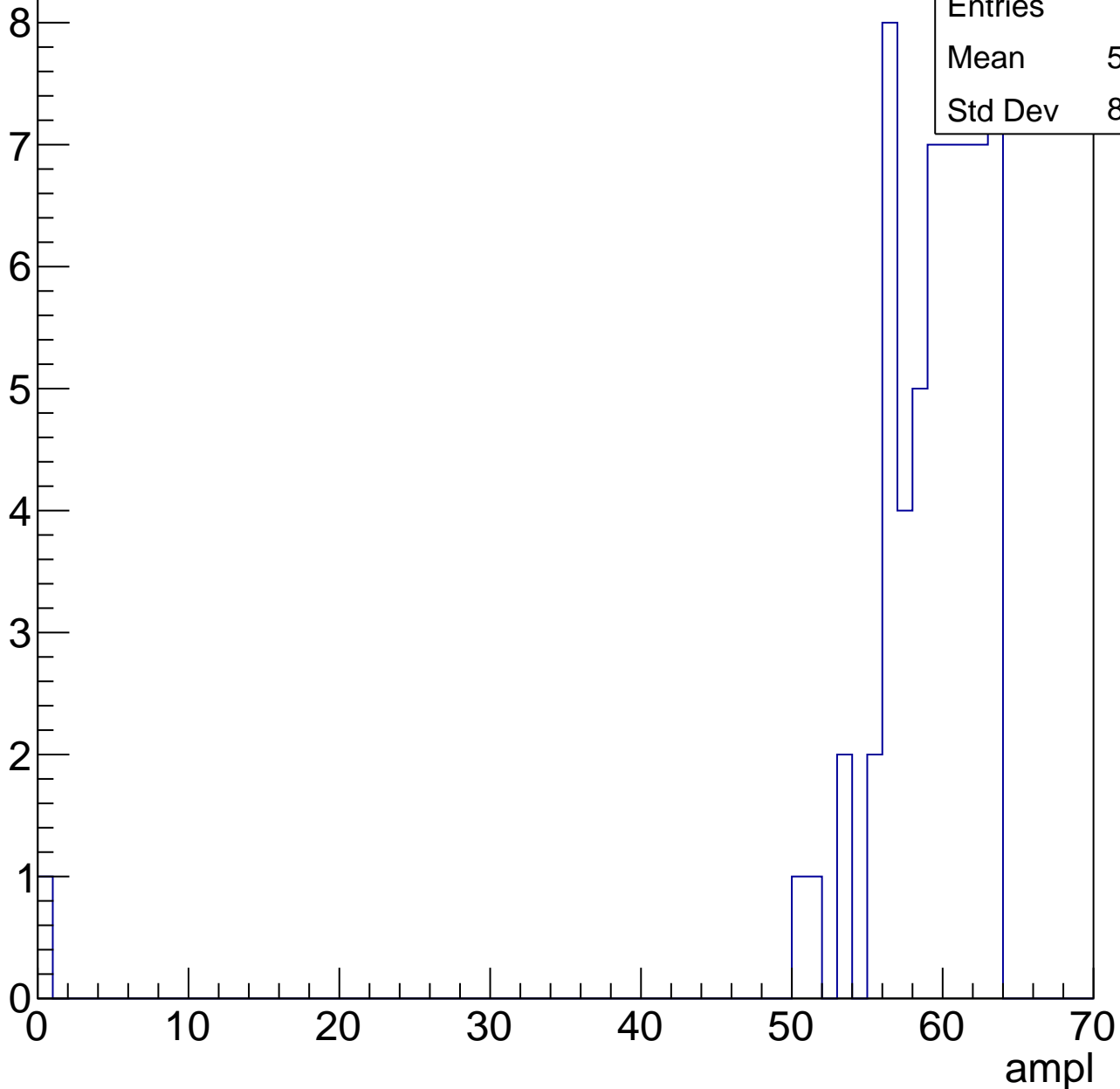


B1L102S, U14-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

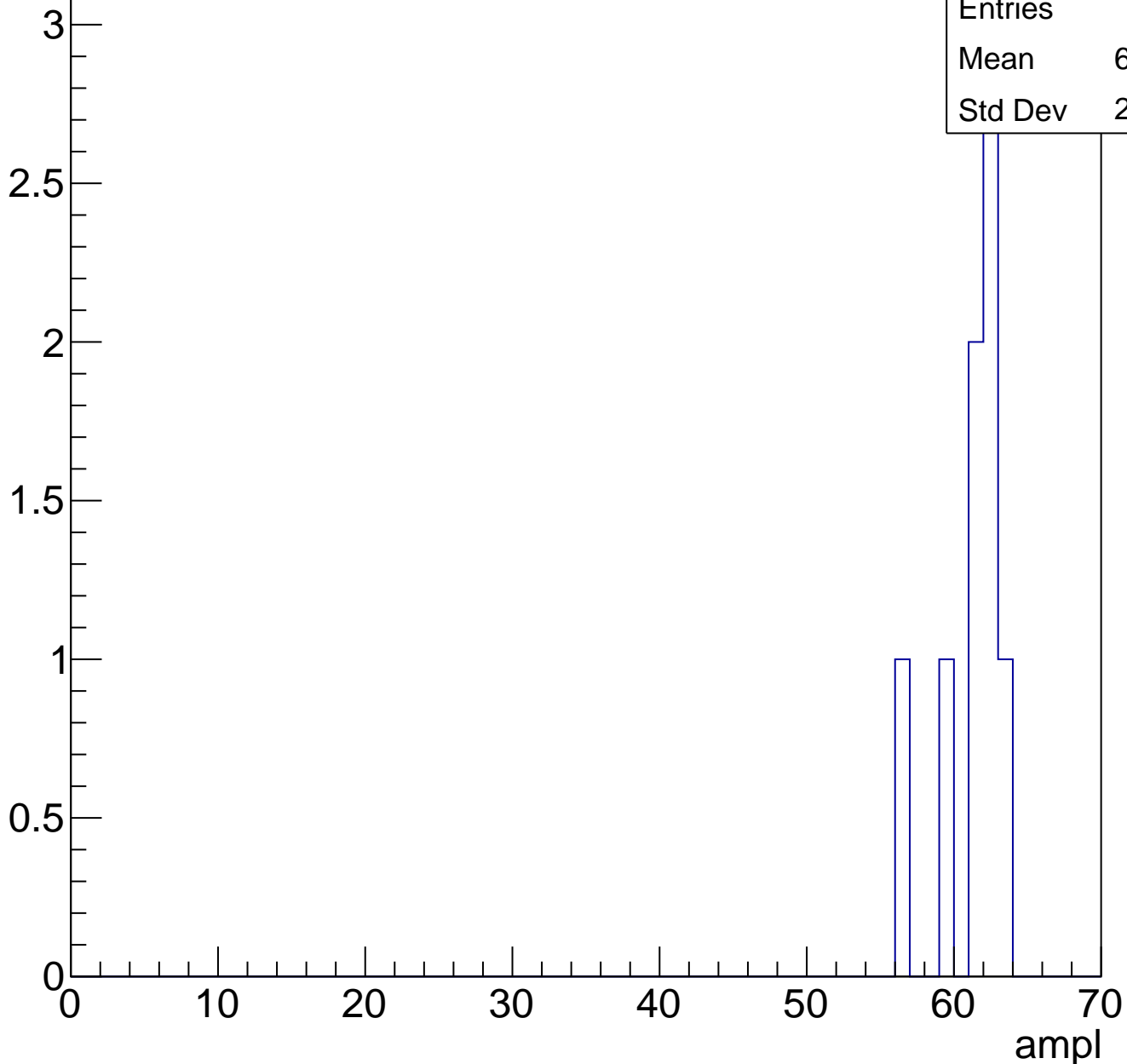
Entries	60
Mean	58.02
Std Dev	8.156



B1L102S, U14-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch114, adc0

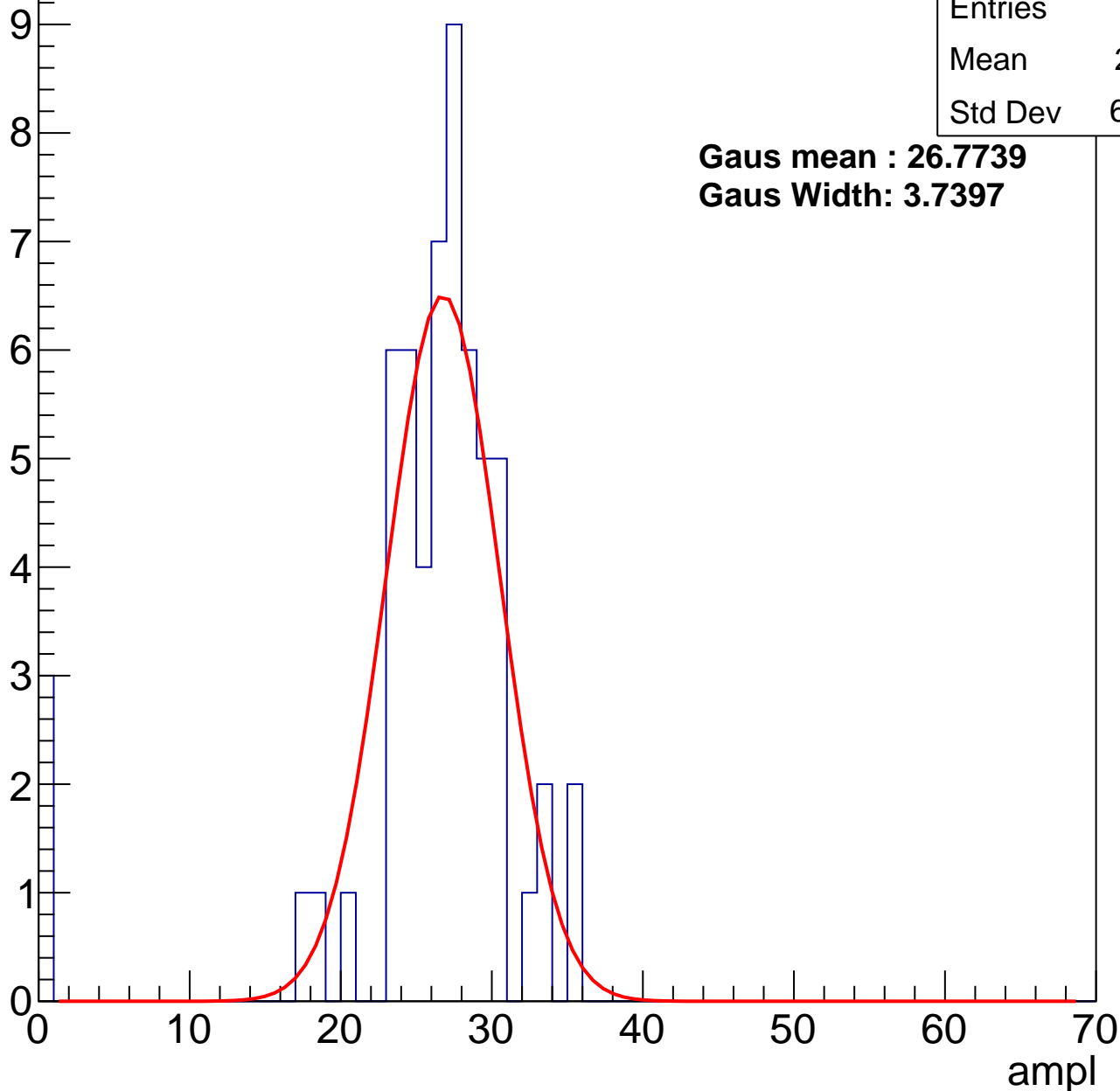
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	25.31
Std Dev	6.785

Gaus mean : 26.7739

Gaus Width: 3.7397



B1L102S, U14-ch114, adc1

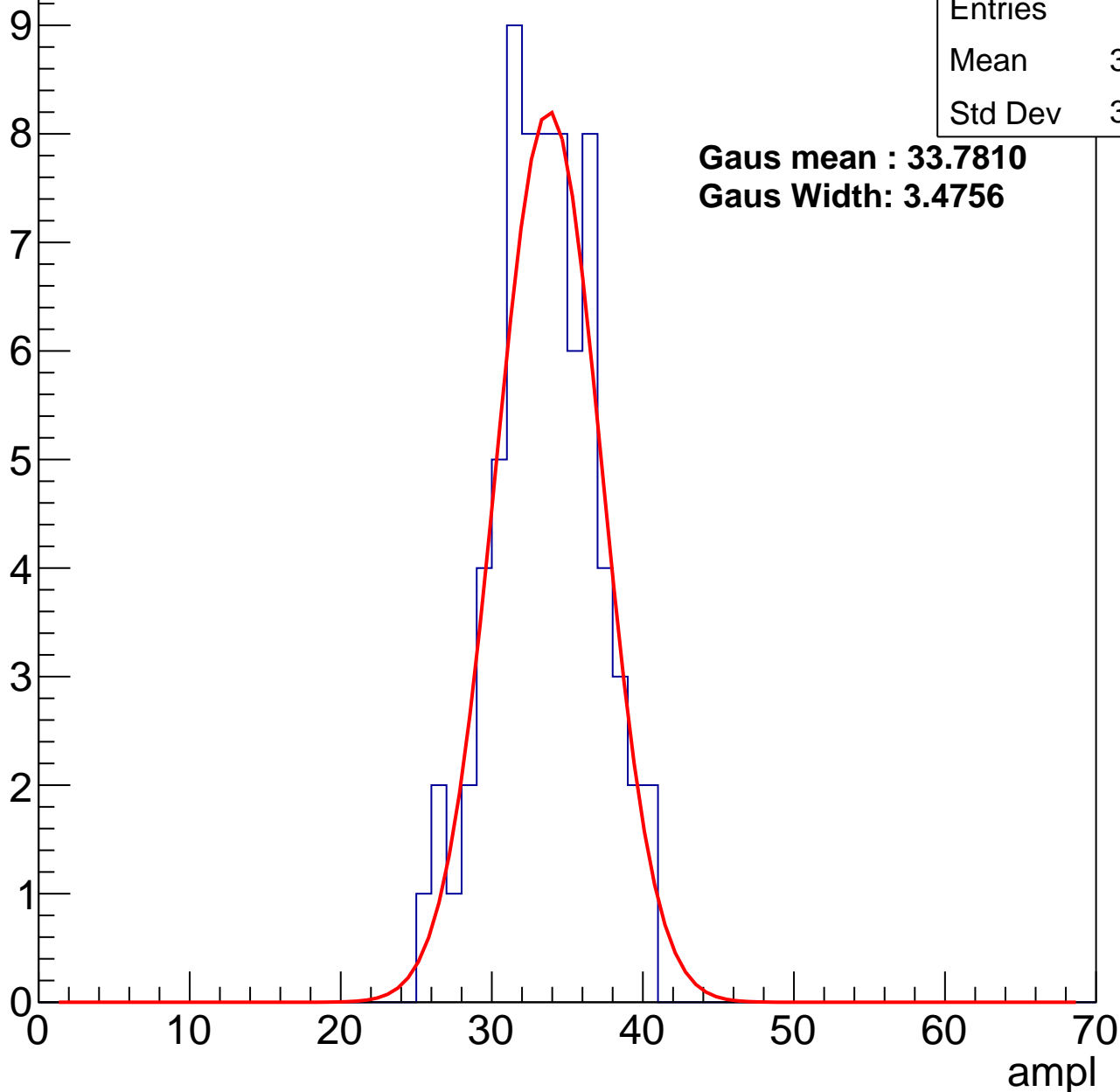
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	33.08
Std Dev	3.338

Gaus mean : 33.7810

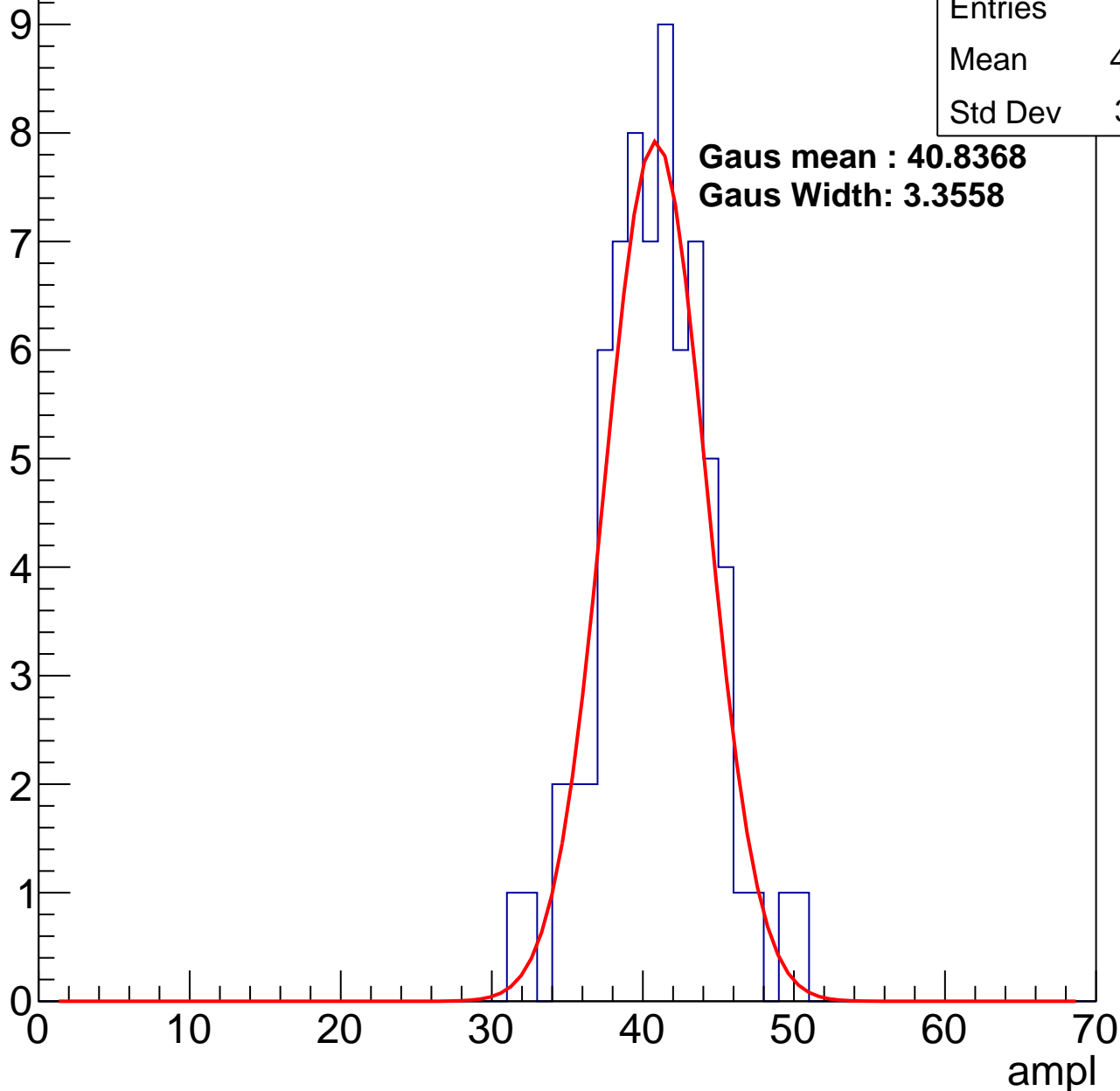
Gaus Width: 3.4756



B1L102S, U14-ch114, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

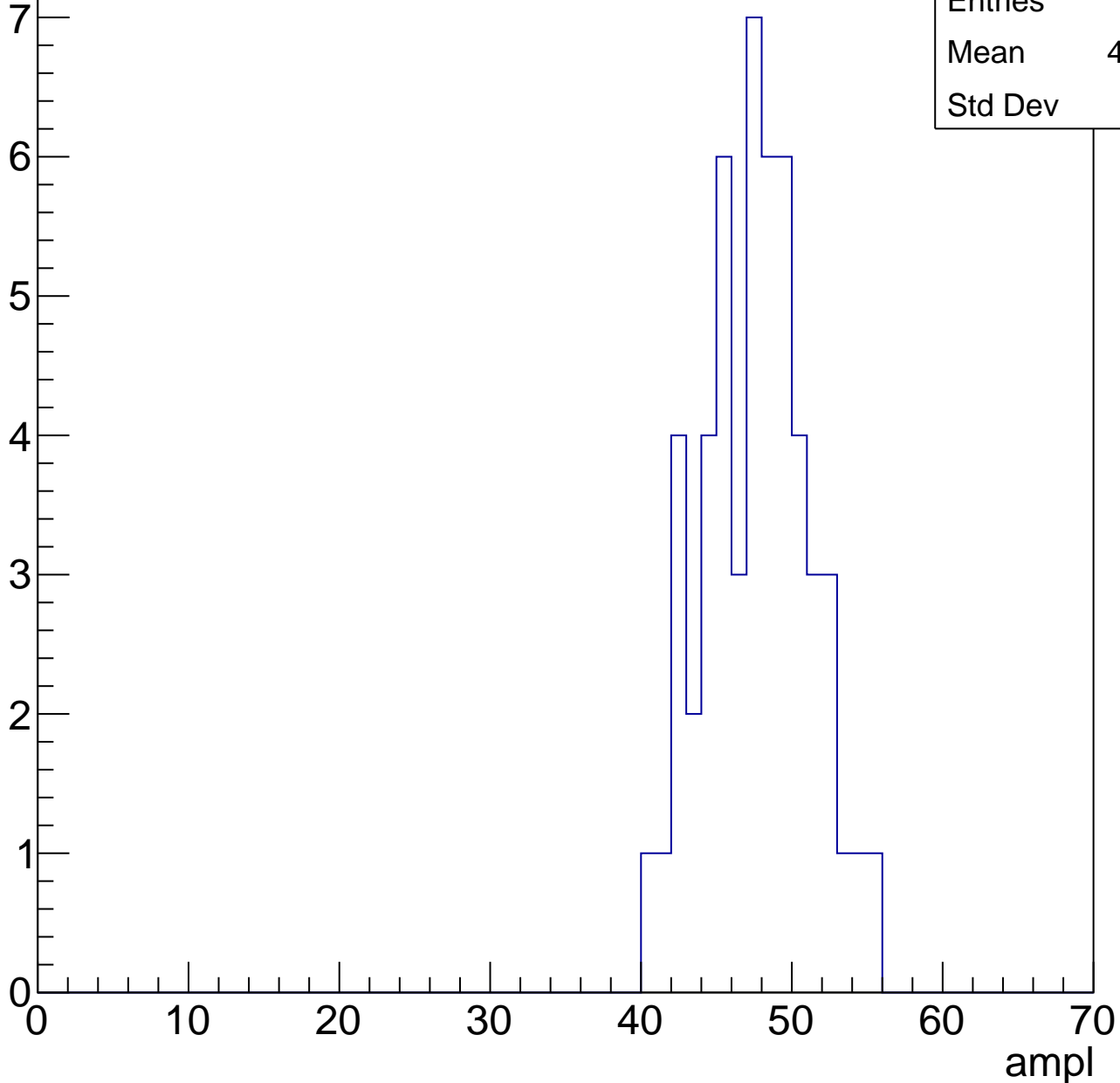


B1L102S, U14-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	47.19
Std Dev	3.41

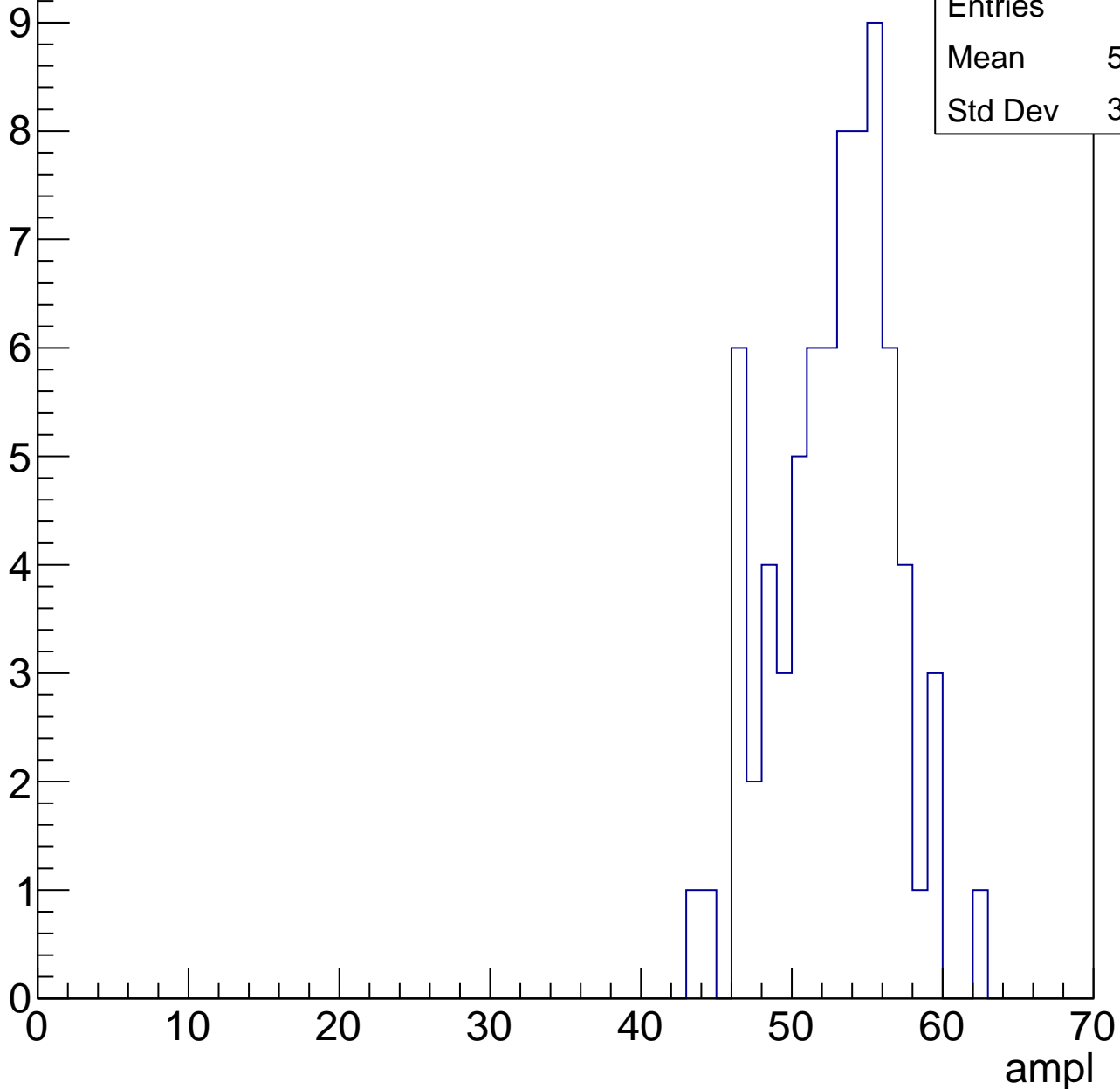


B1L102S, U14-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	52.38
Std Dev	3.886

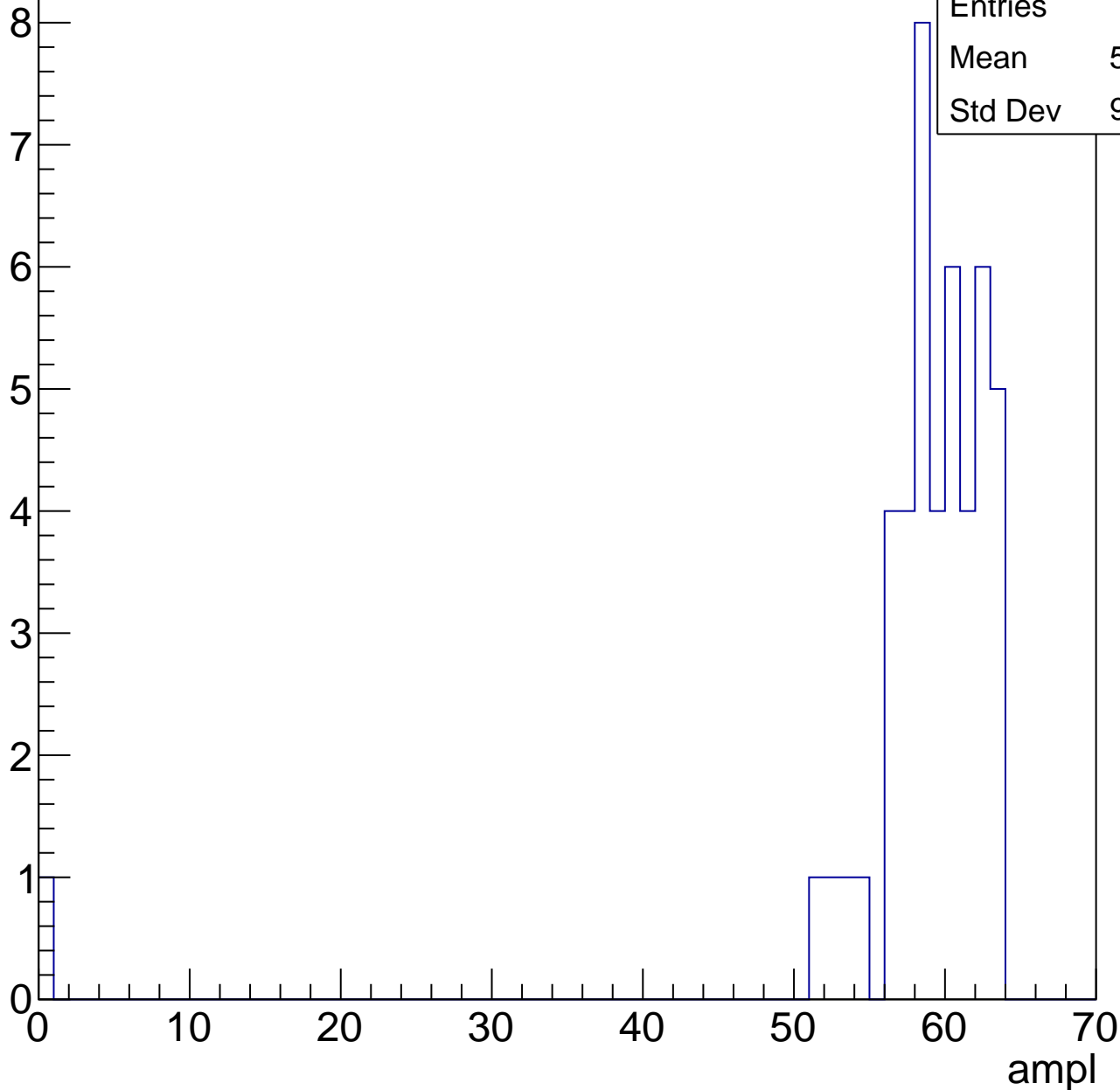


B1L102S, U14-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	57.67
Std Dev	9.077

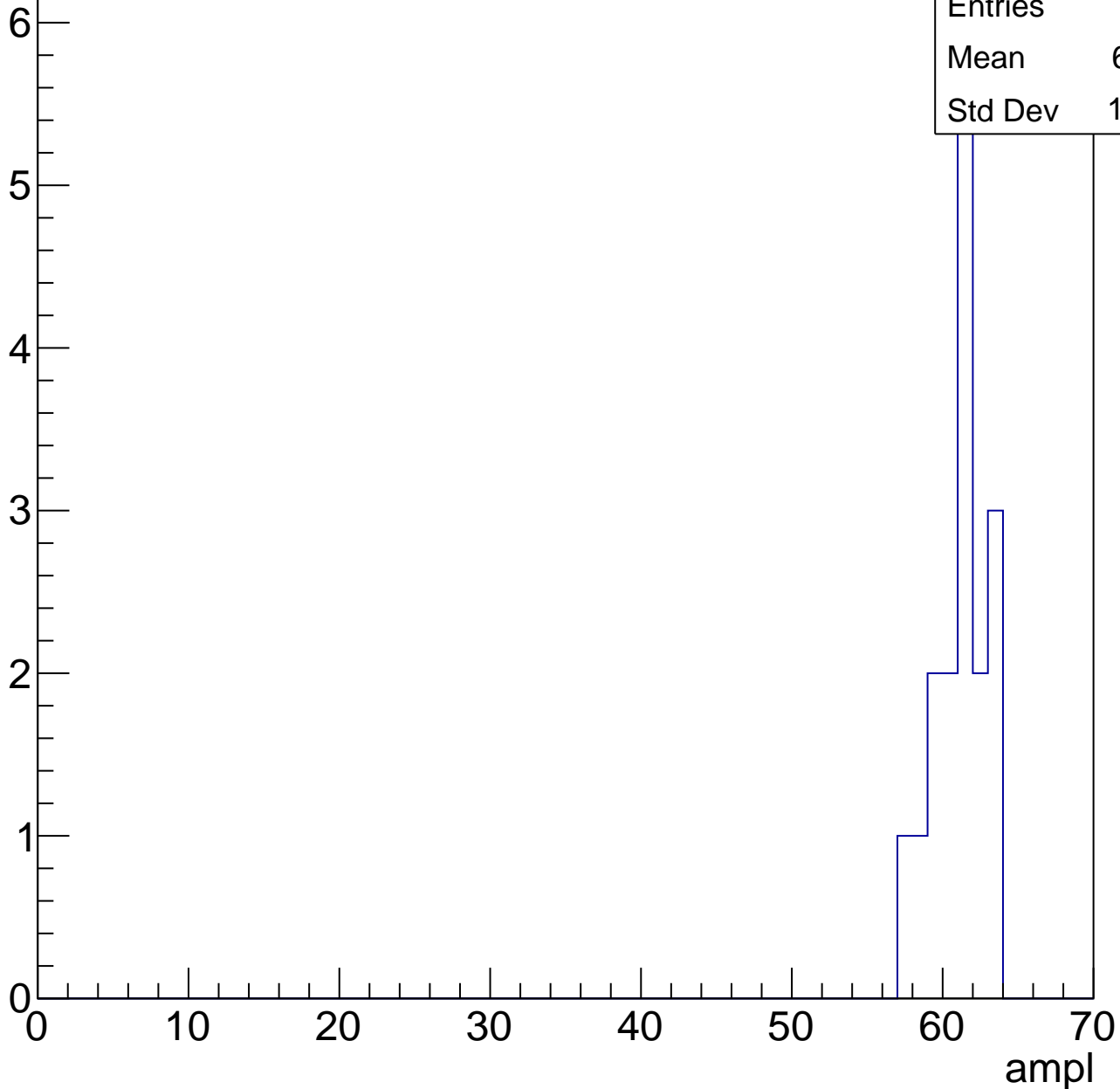


B1L102S, U14-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	60.71
Std Dev	1.672



B1L102S, U14-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

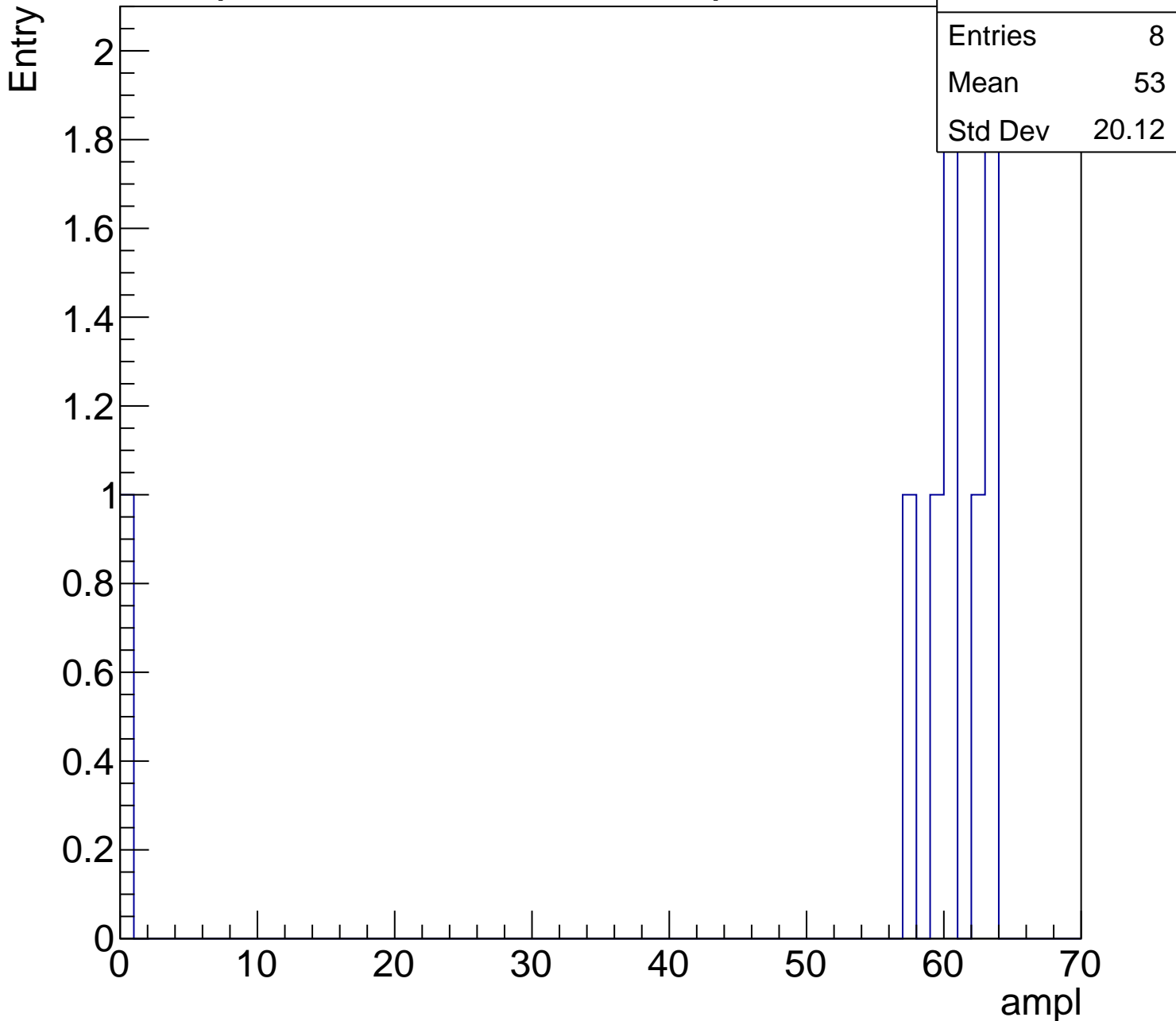
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53
Std Dev	20.12

0 10 20 30 40 50 60 70

ampl



B1L102S, U14-ch115, adc0

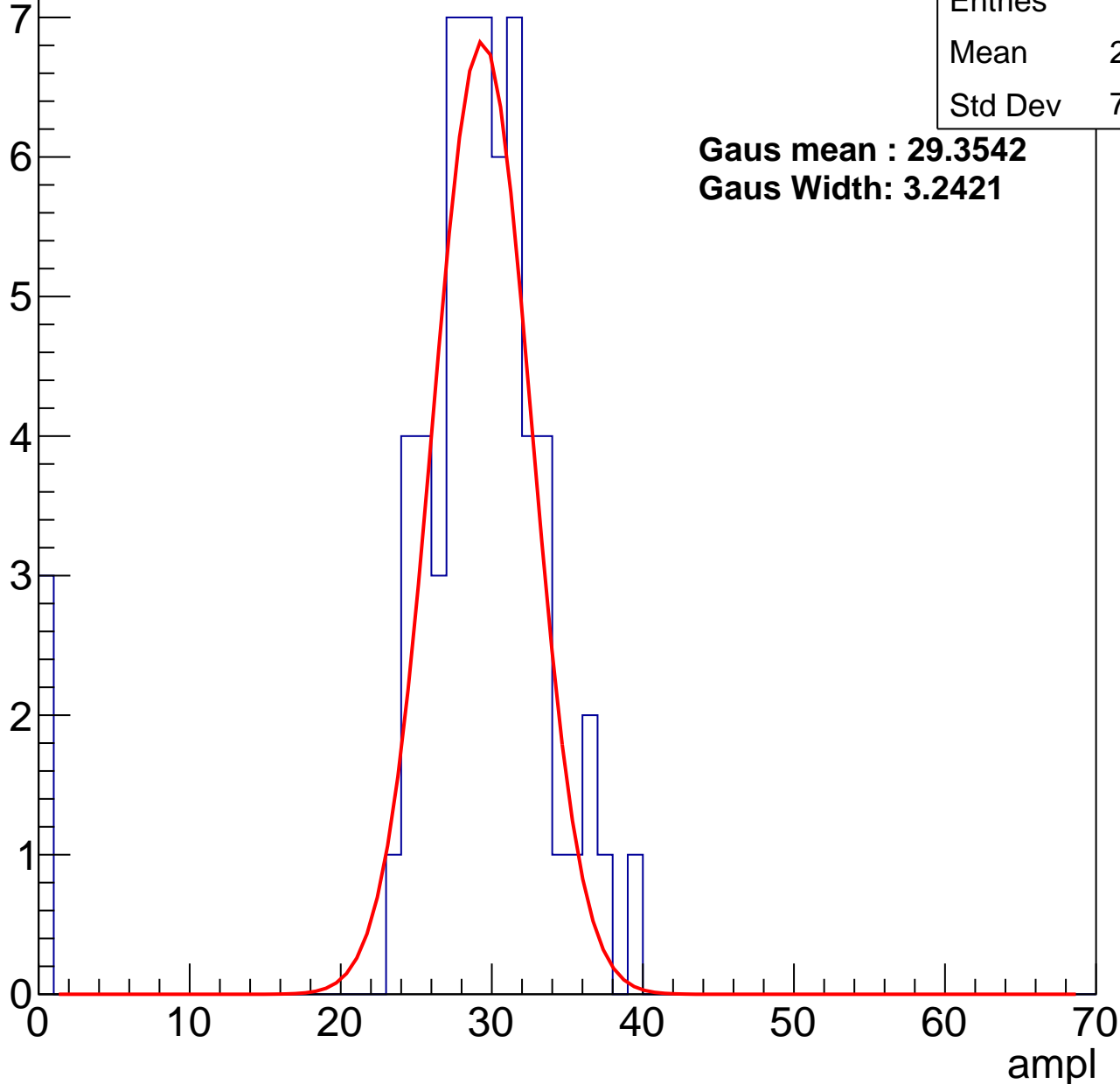
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.92
Std Dev	7.092

Gaus mean : 29.3542

Gaus Width: 3.2421



B1L102S, U14-ch115, adc1

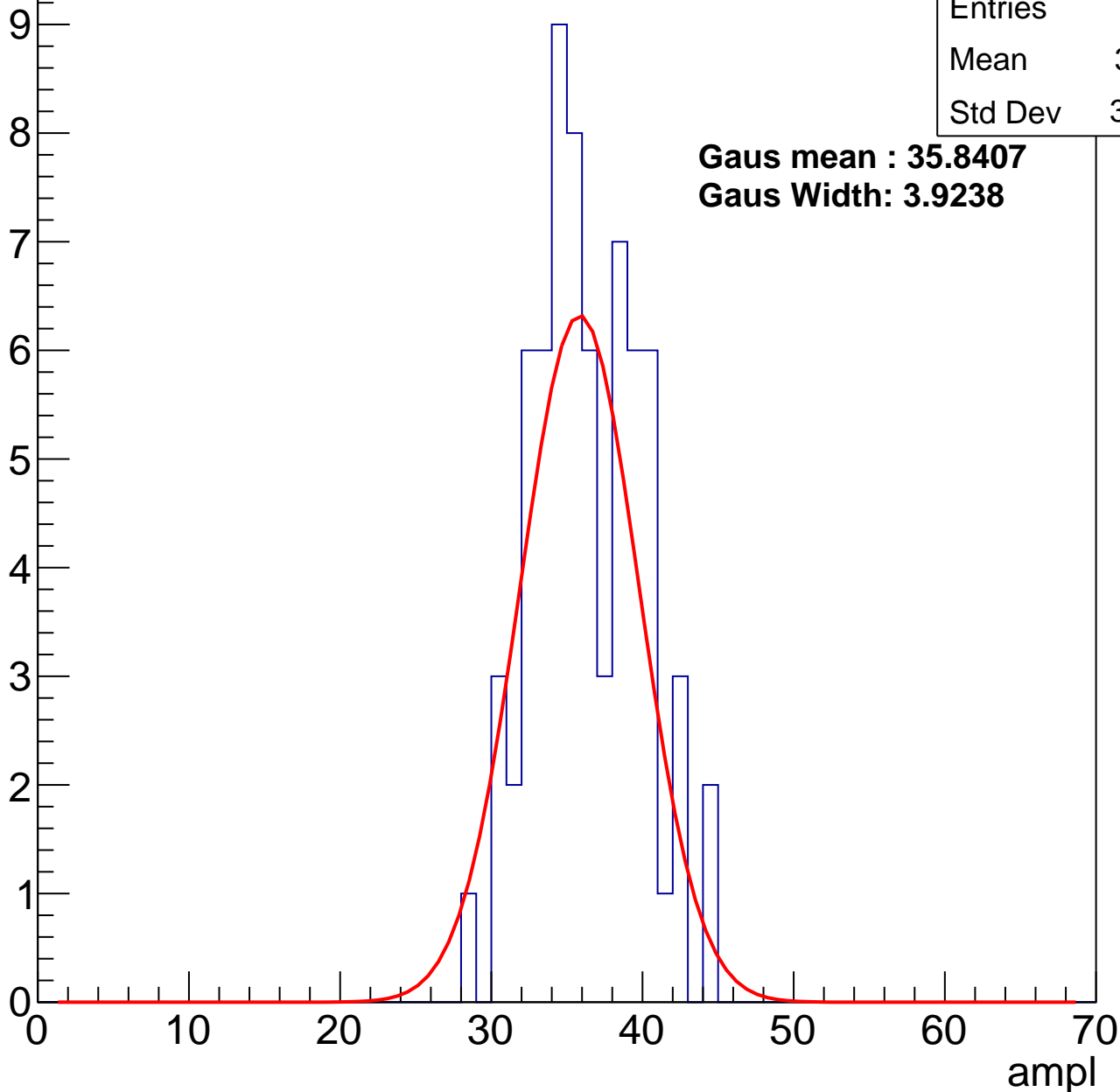
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.91
Std Dev	3.525

Gaus mean : 35.8407

Gaus Width: 3.9238



B1L102S, U14-ch115, adc2

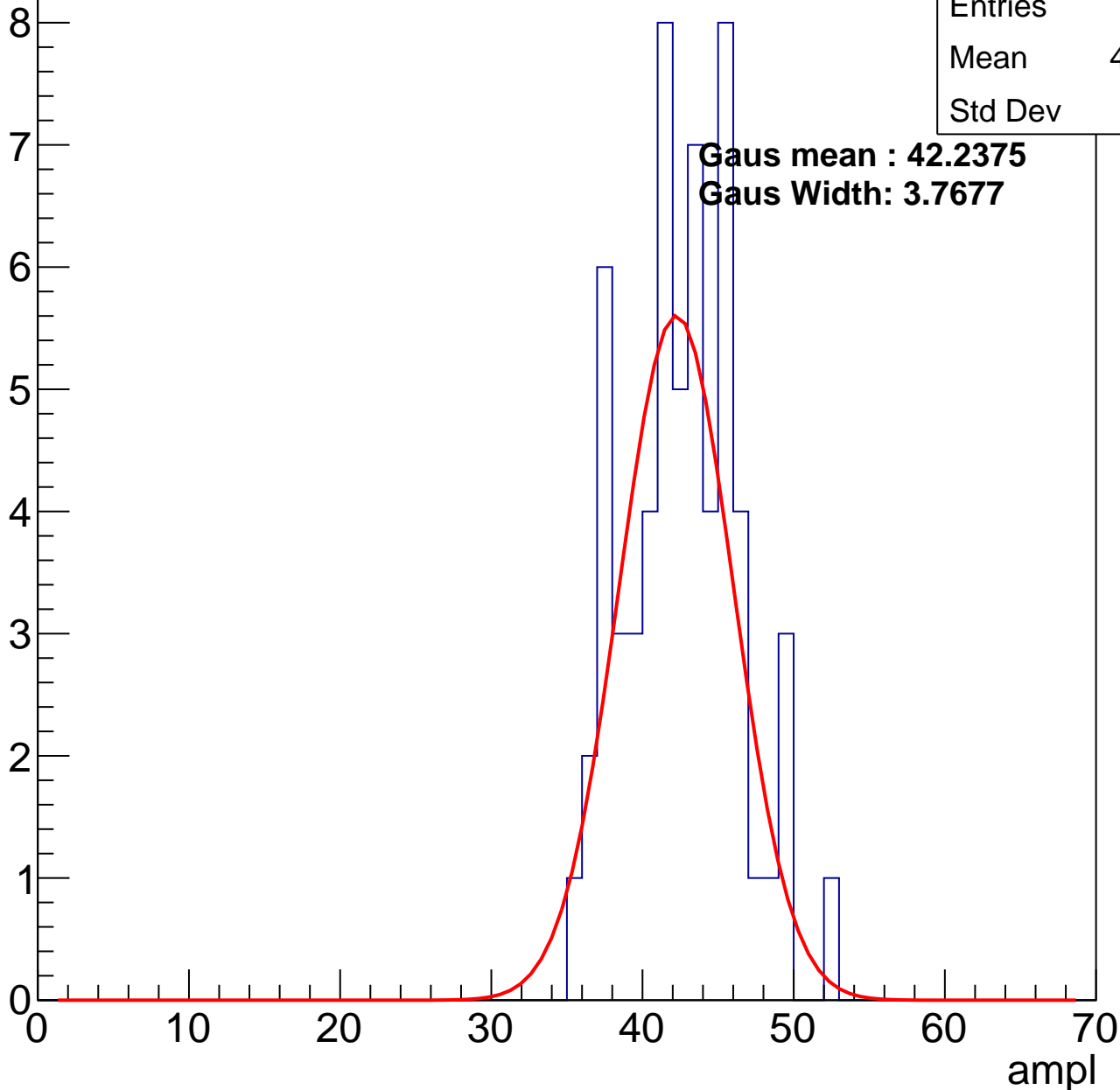
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.18
Std Dev	3.7

Gaus mean : 42.2375

Gaus Width: 3.7677



B1L102S, U14-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

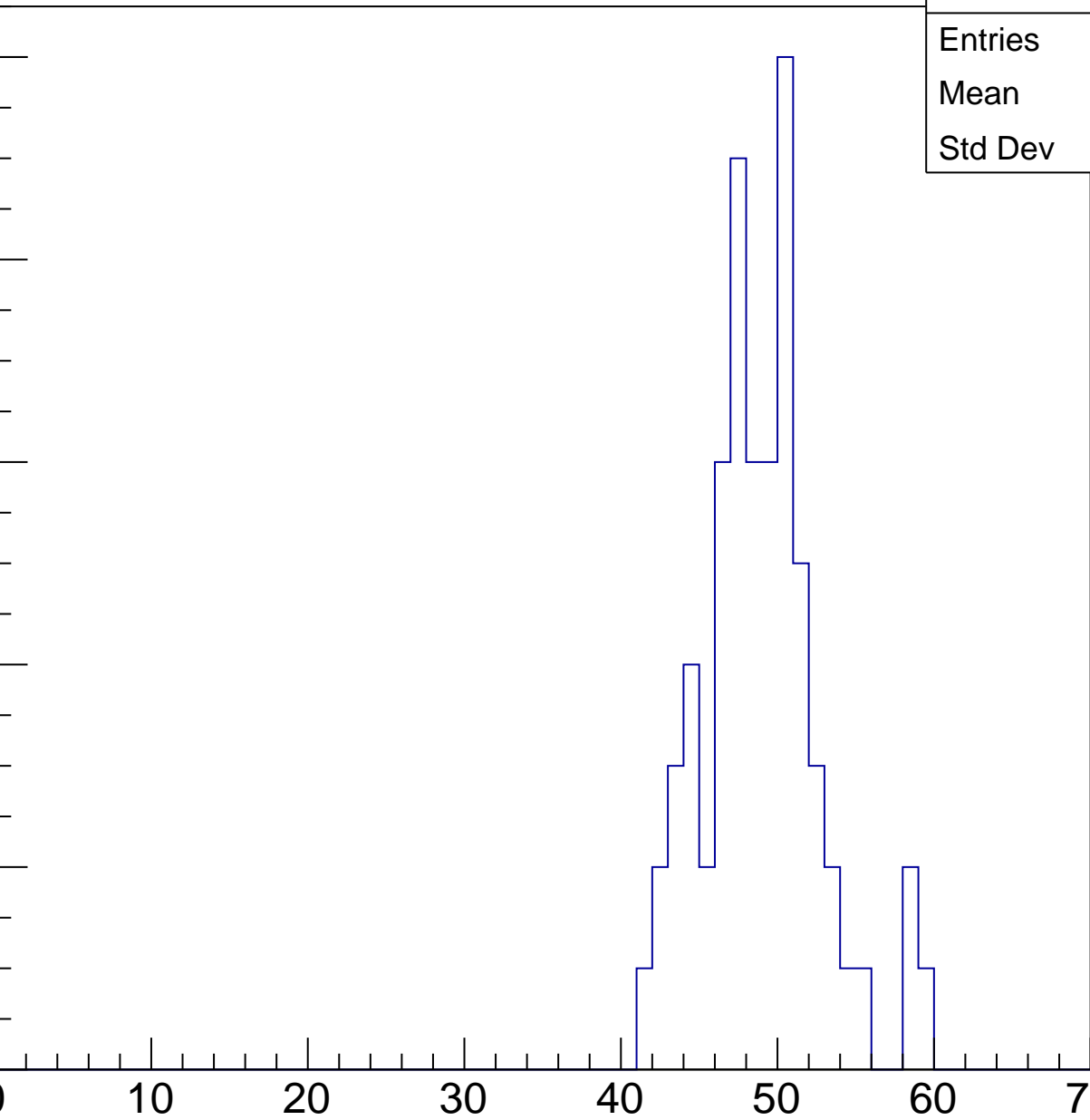
Entries	64
Mean	48.47
Std Dev	3.733

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

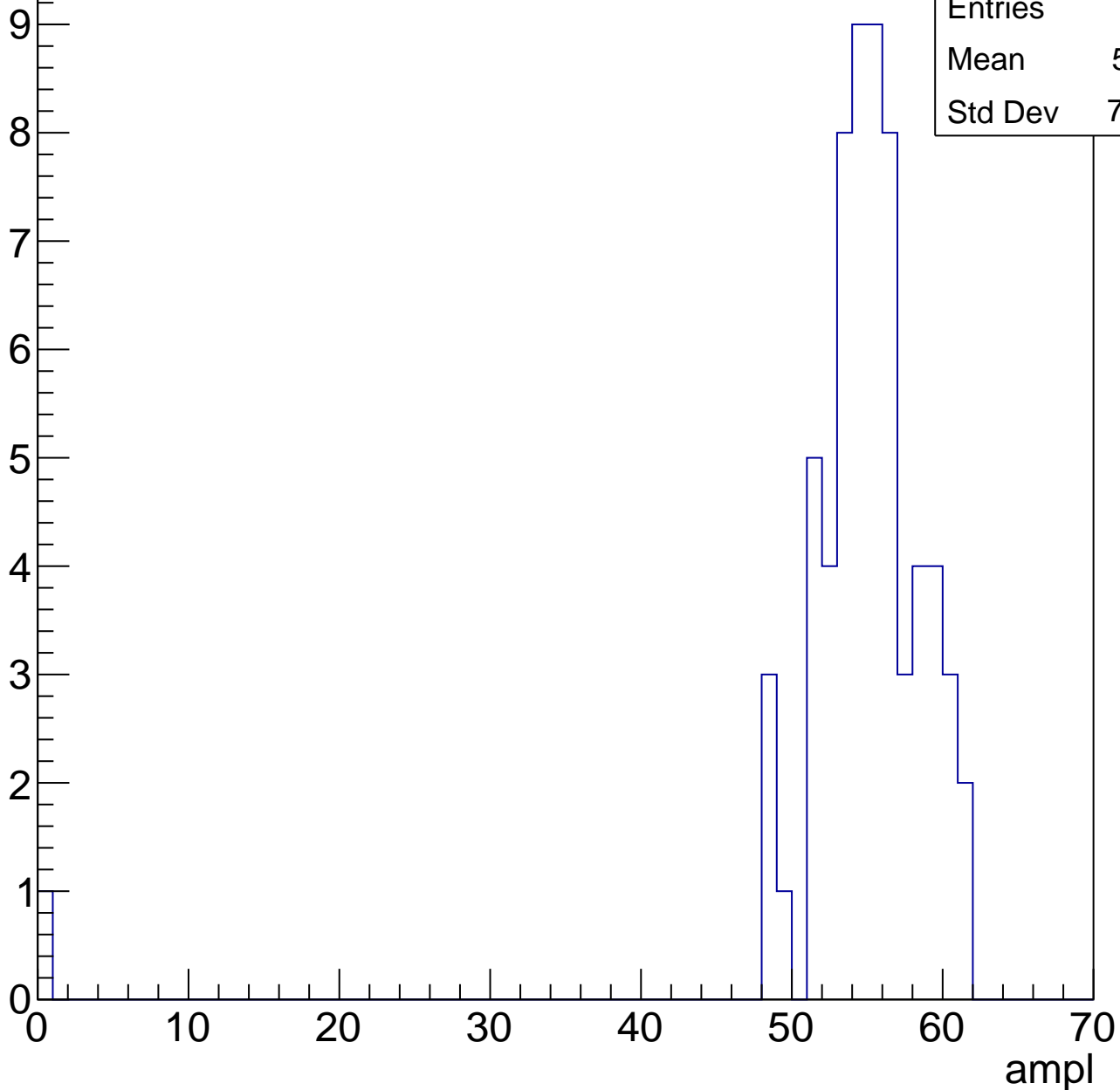


B1L102S, U14-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

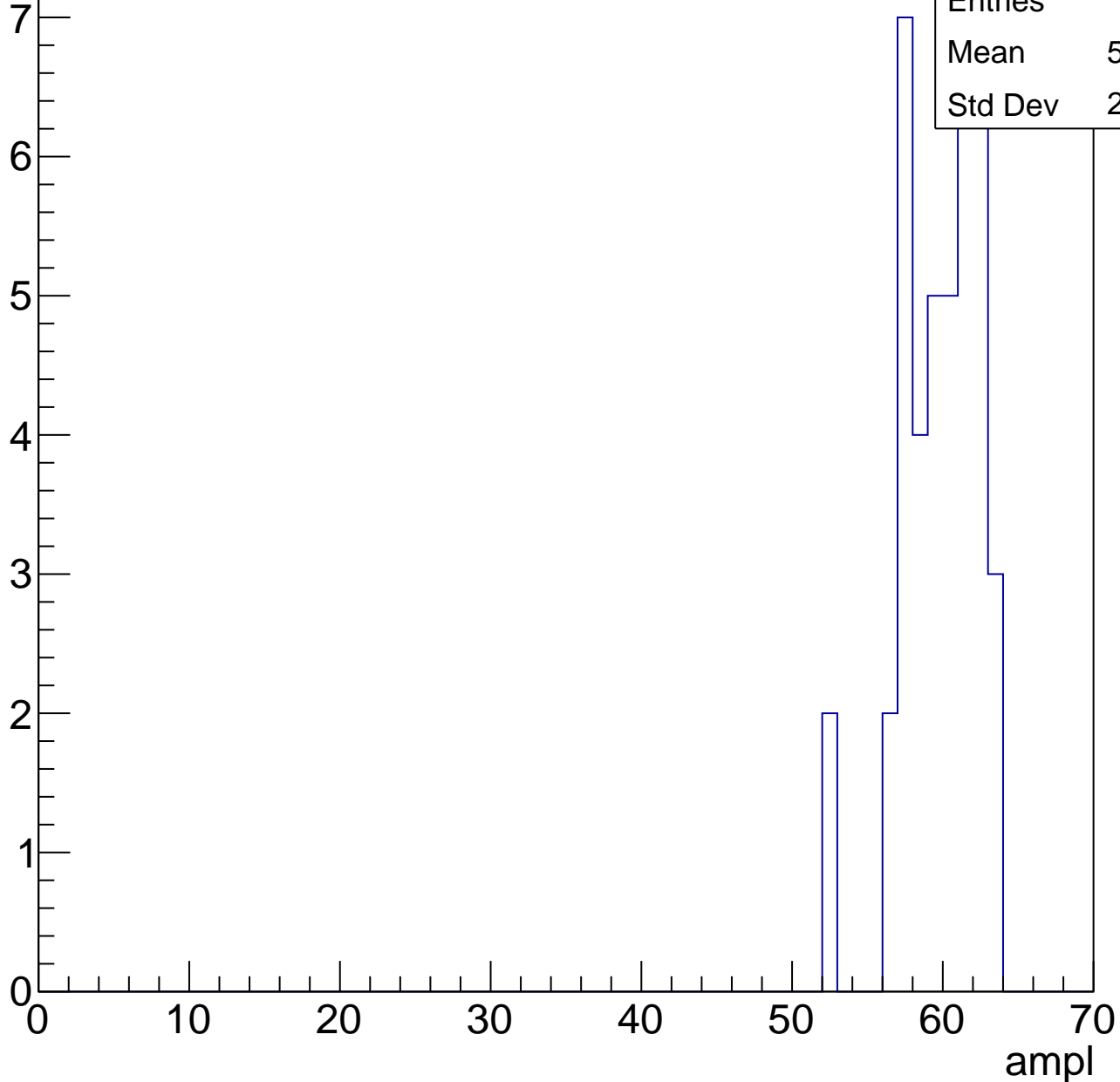
Entries	64
Mean	53.91
Std Dev	7.453



B1L102S, U14-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

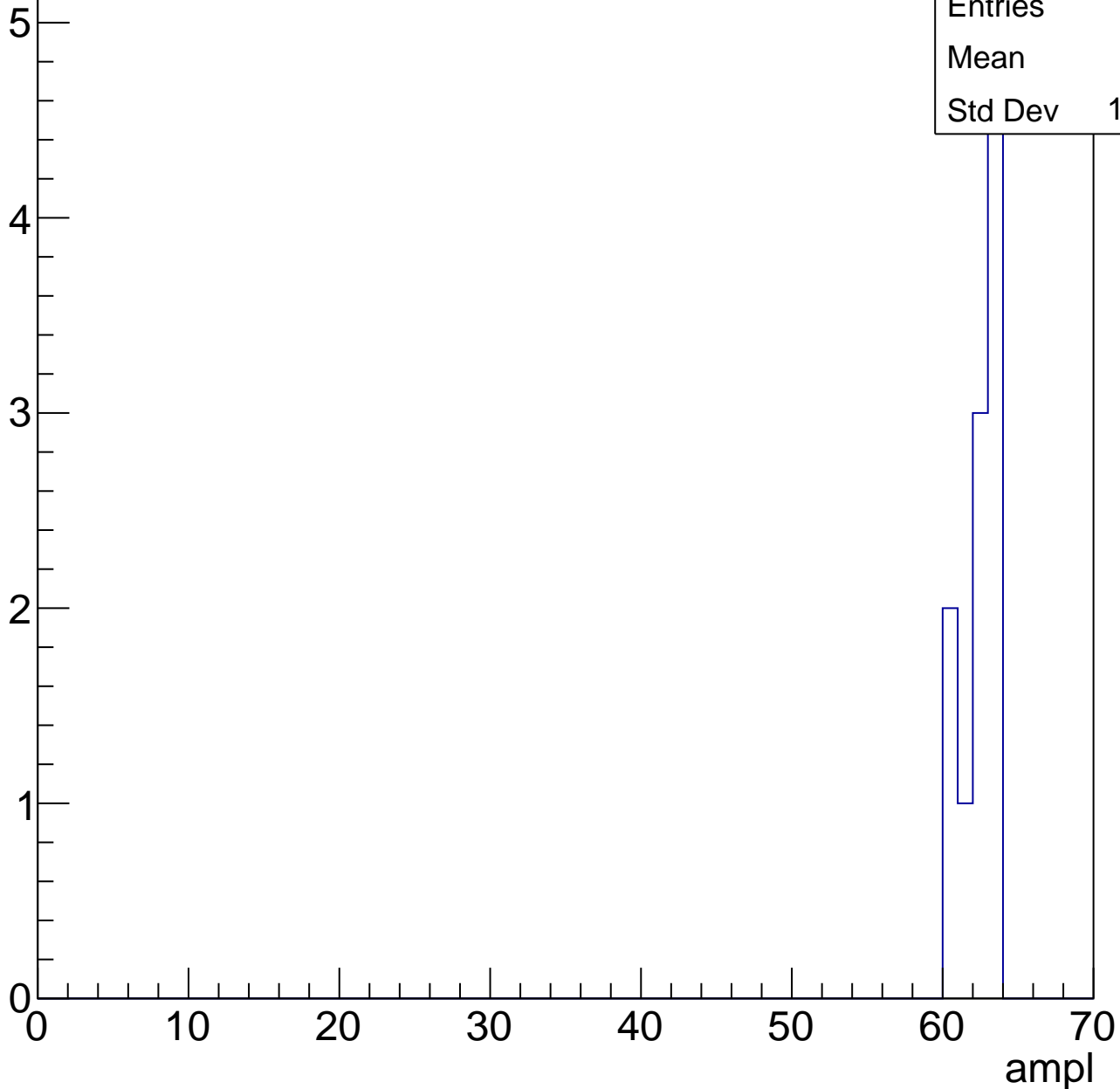


B1L102S, U14-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	62
Std Dev	1.128



B1L102S, U14-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch116, adc0

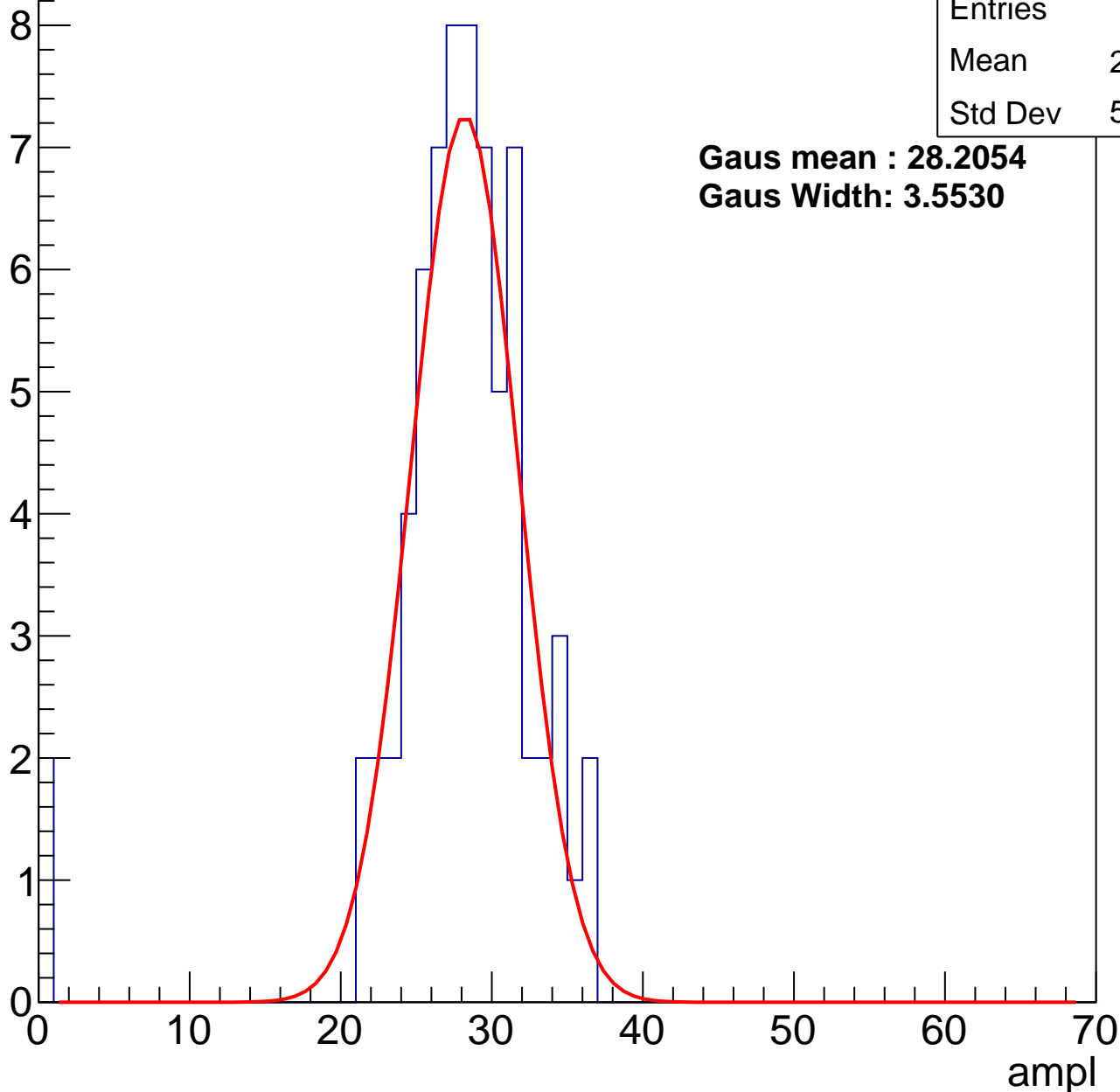
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.27
Std Dev	5.809

Gaus mean : 28.2054

Gaus Width: 3.5530



B1L102S, U14-ch116, adc1

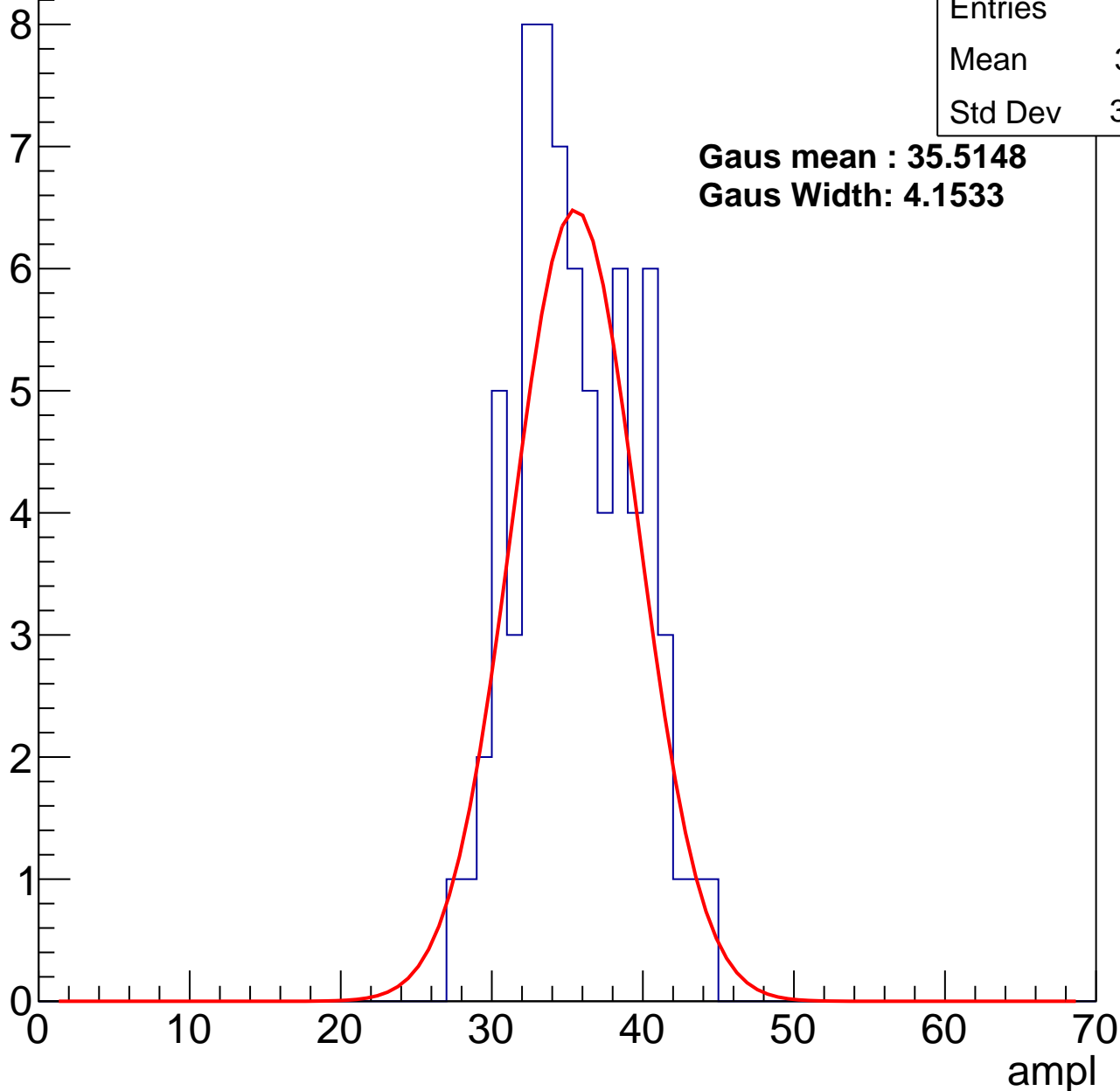
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.11
Std Dev	3.835

Gaus mean : 35.5148

Gaus Width: 4.1533



B1L102S, U14-ch116, adc2

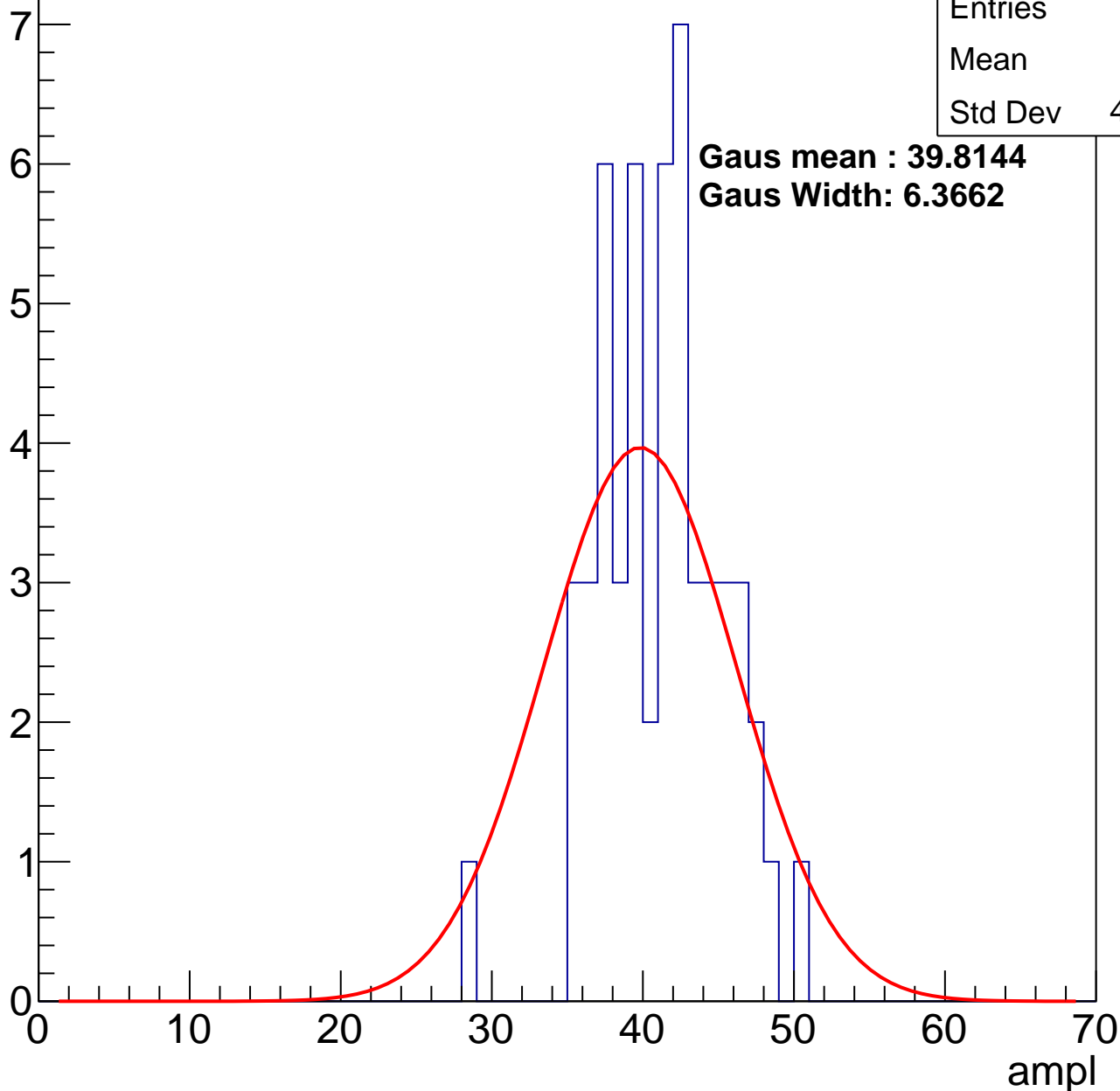
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	40.7
Std Dev	4.045

Gaus mean : 39.8144

Gaus Width: 6.3662

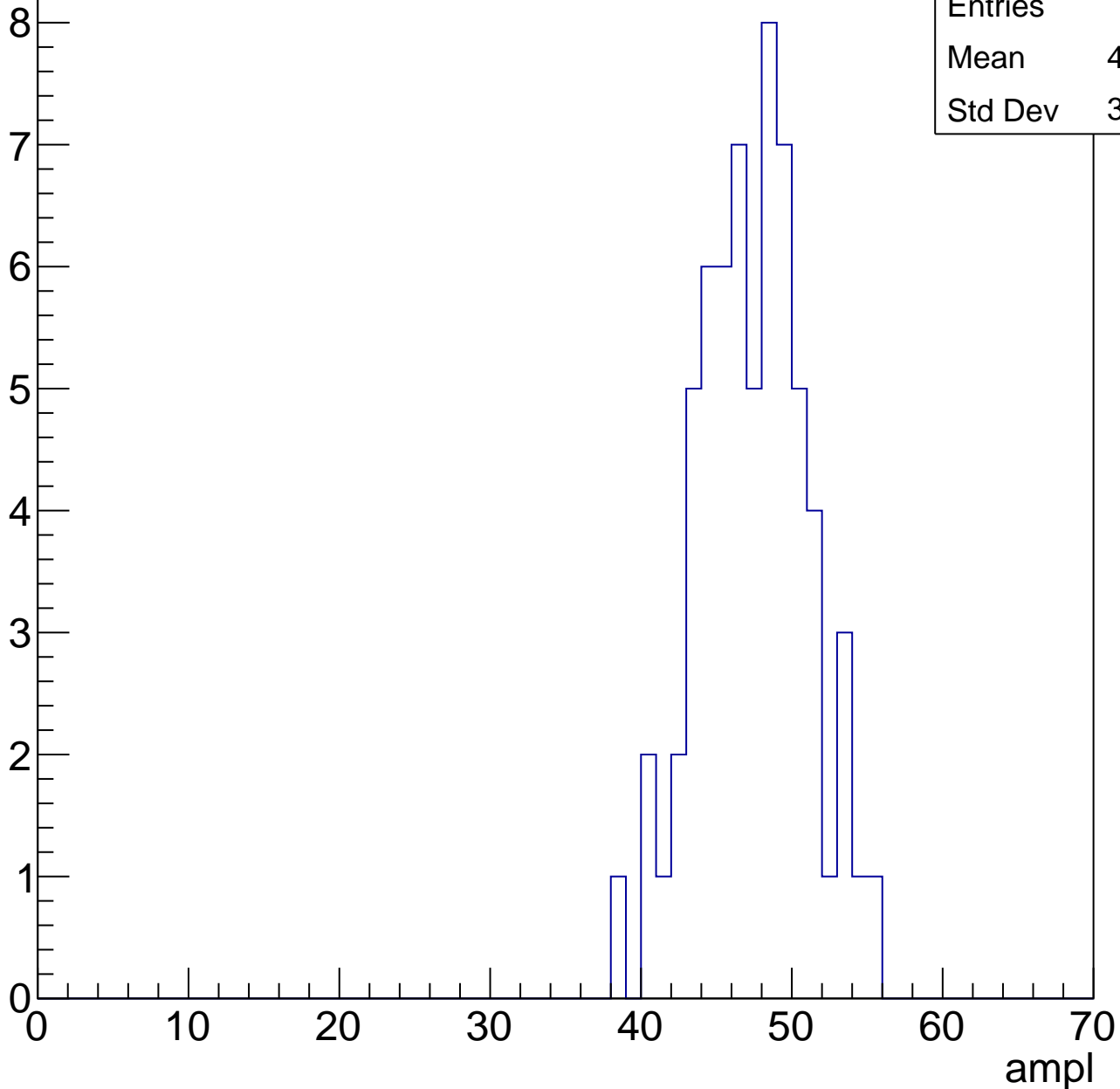


B1L102S, U14-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	46.92
Std Dev	3.553

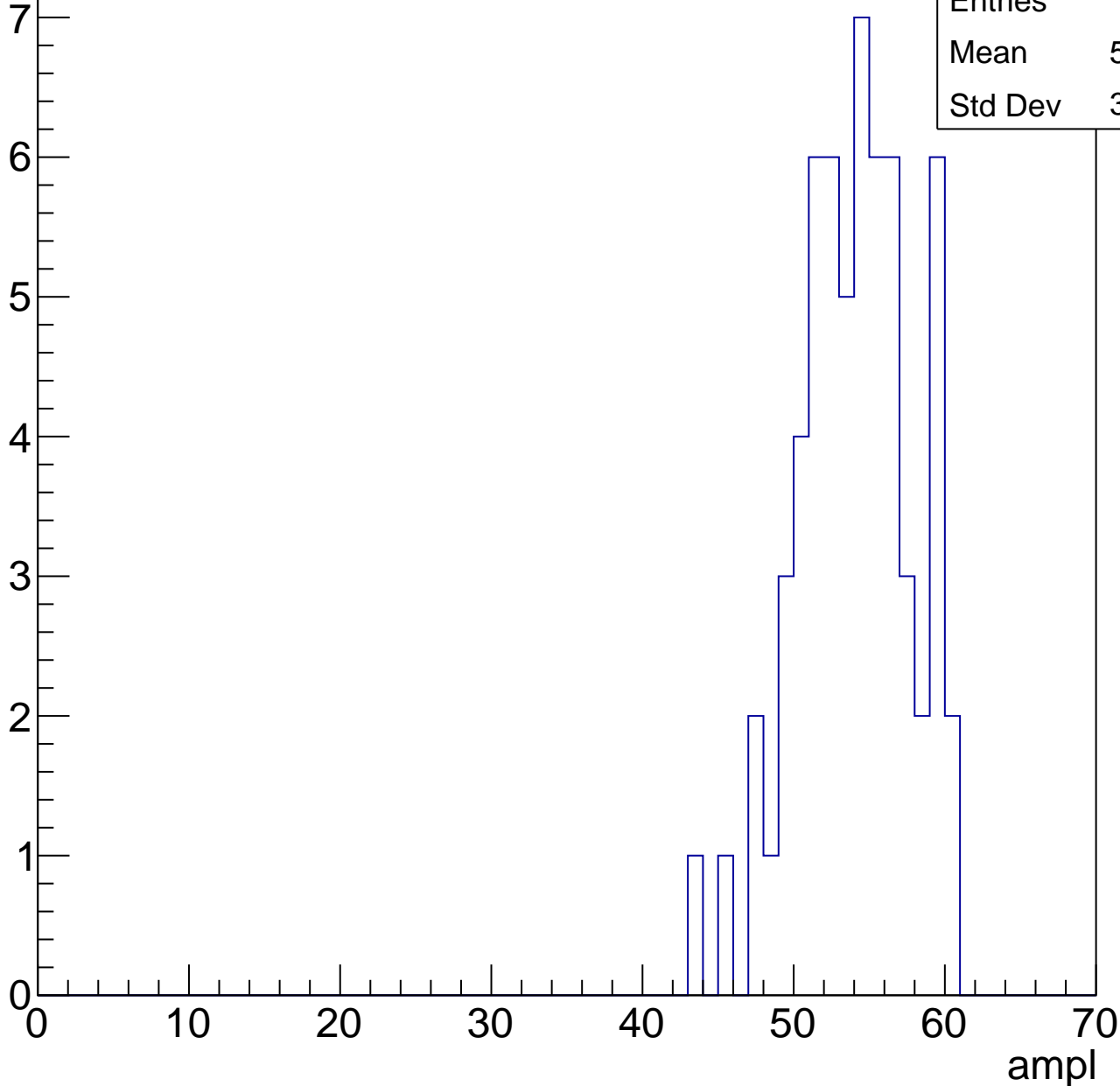


B1L102S, U14-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.52
Std Dev	3.744

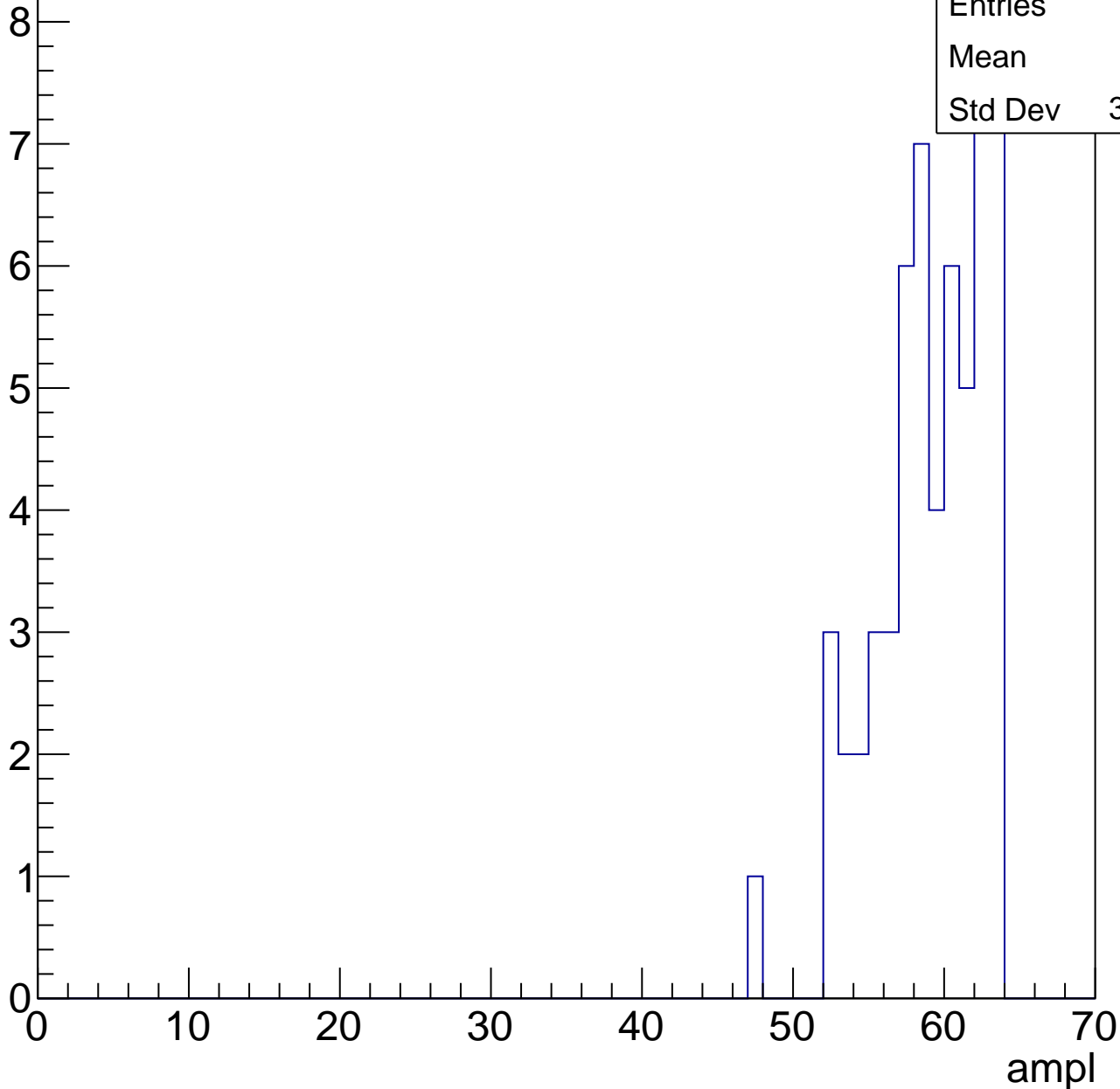


B1L102S, U14-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.6
Std Dev	3.562

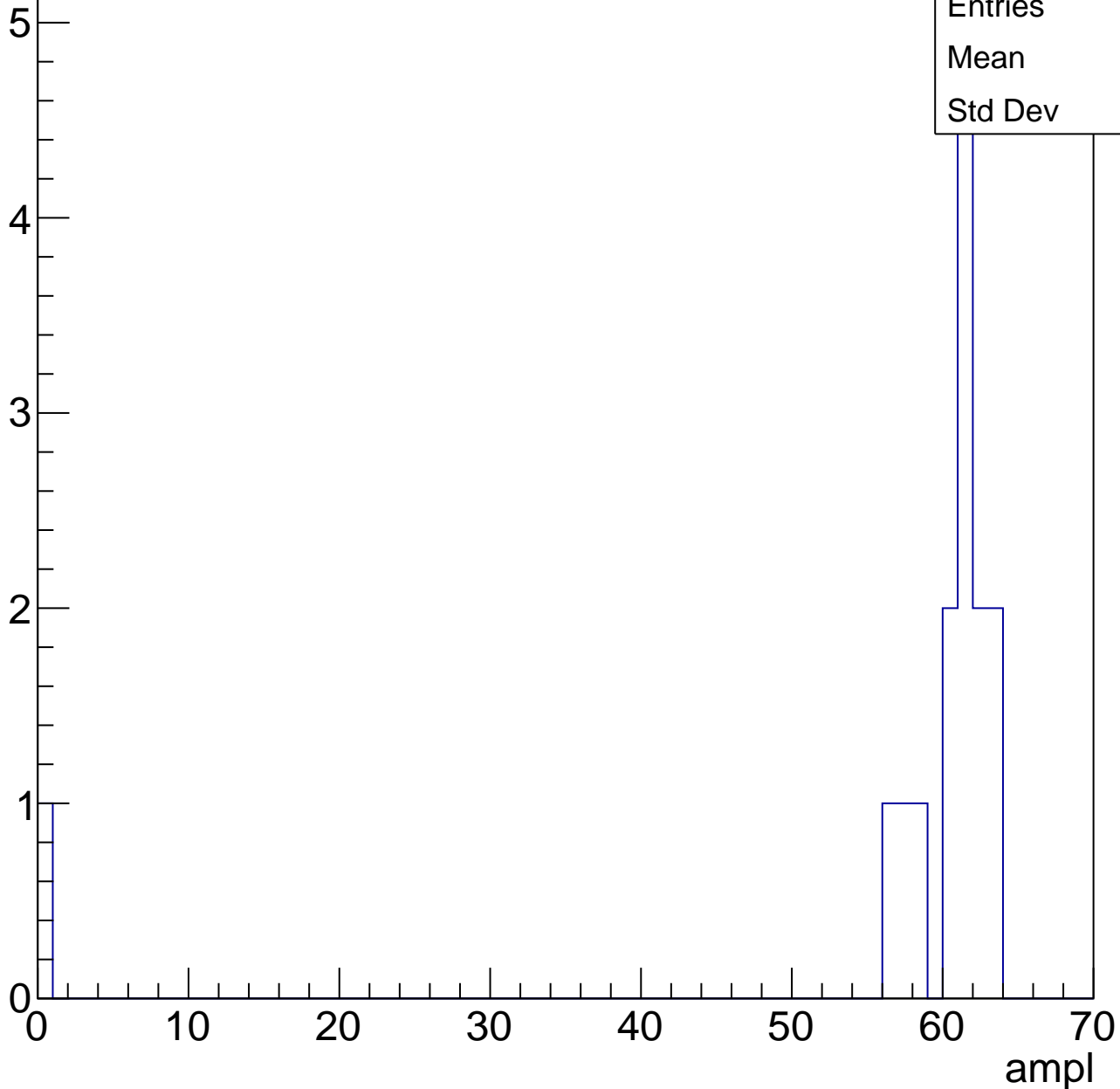


B1L102S, U14-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	56.4
Std Dev	15.2



B1L102S, U14-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U14-ch117, adc0

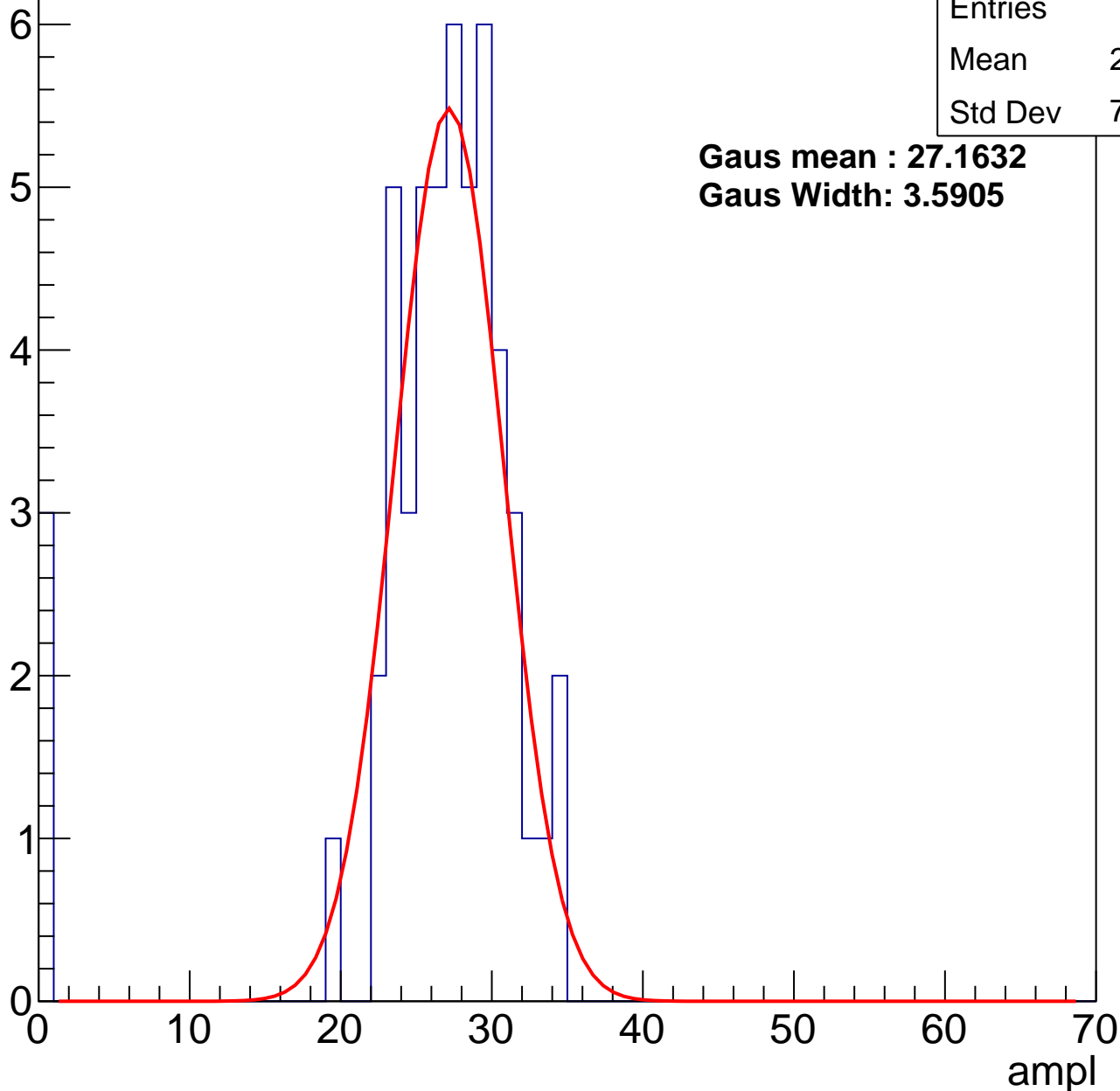
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	25.52
Std Dev	7.059

Gaus mean : 27.1632

Gaus Width: 3.5905



B1L102S, U14-ch117, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	34.03
Std Dev	3.532

Gaus mean : 34.5224

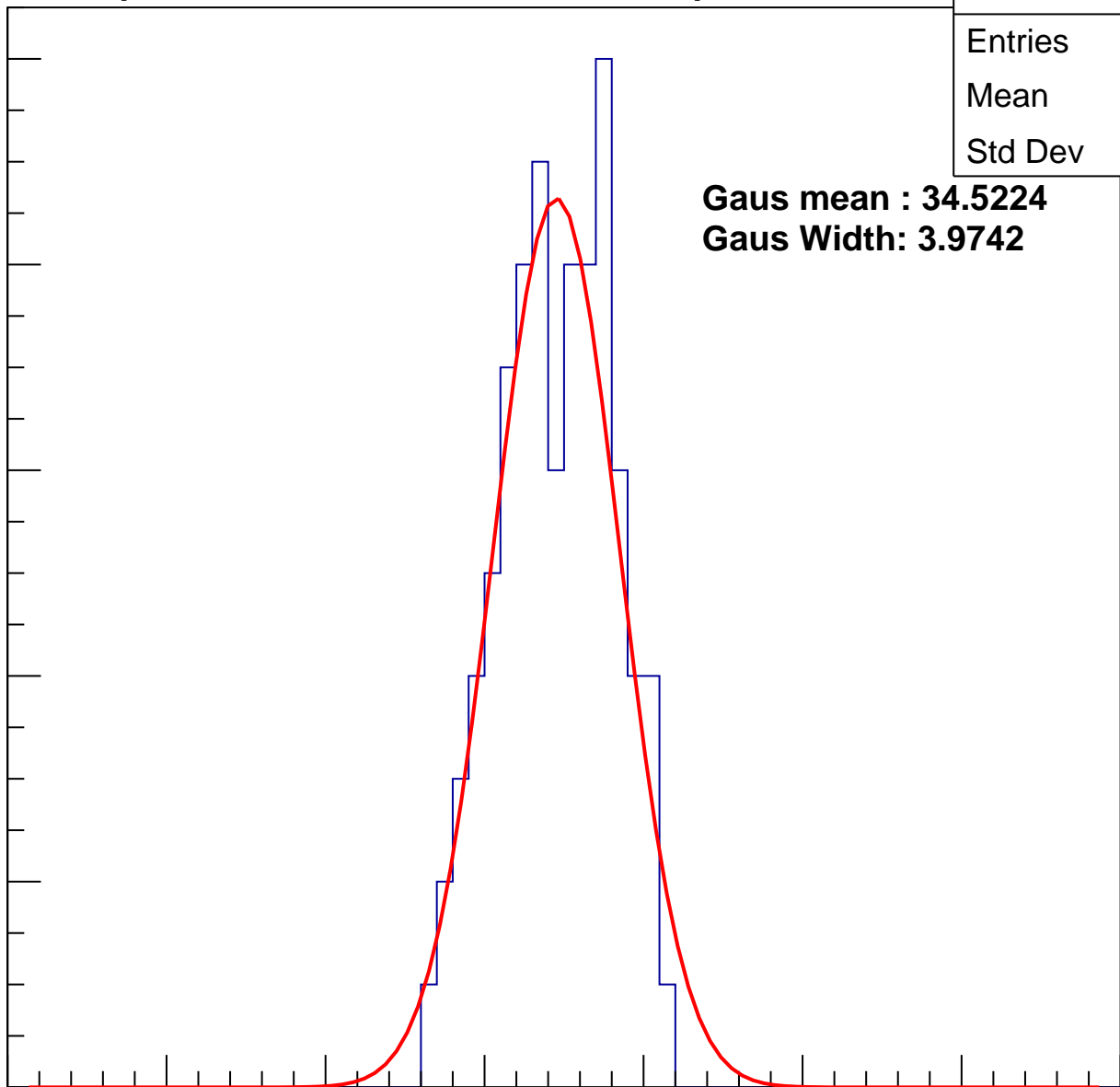
Gaus Width: 3.9742

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch117, adc2

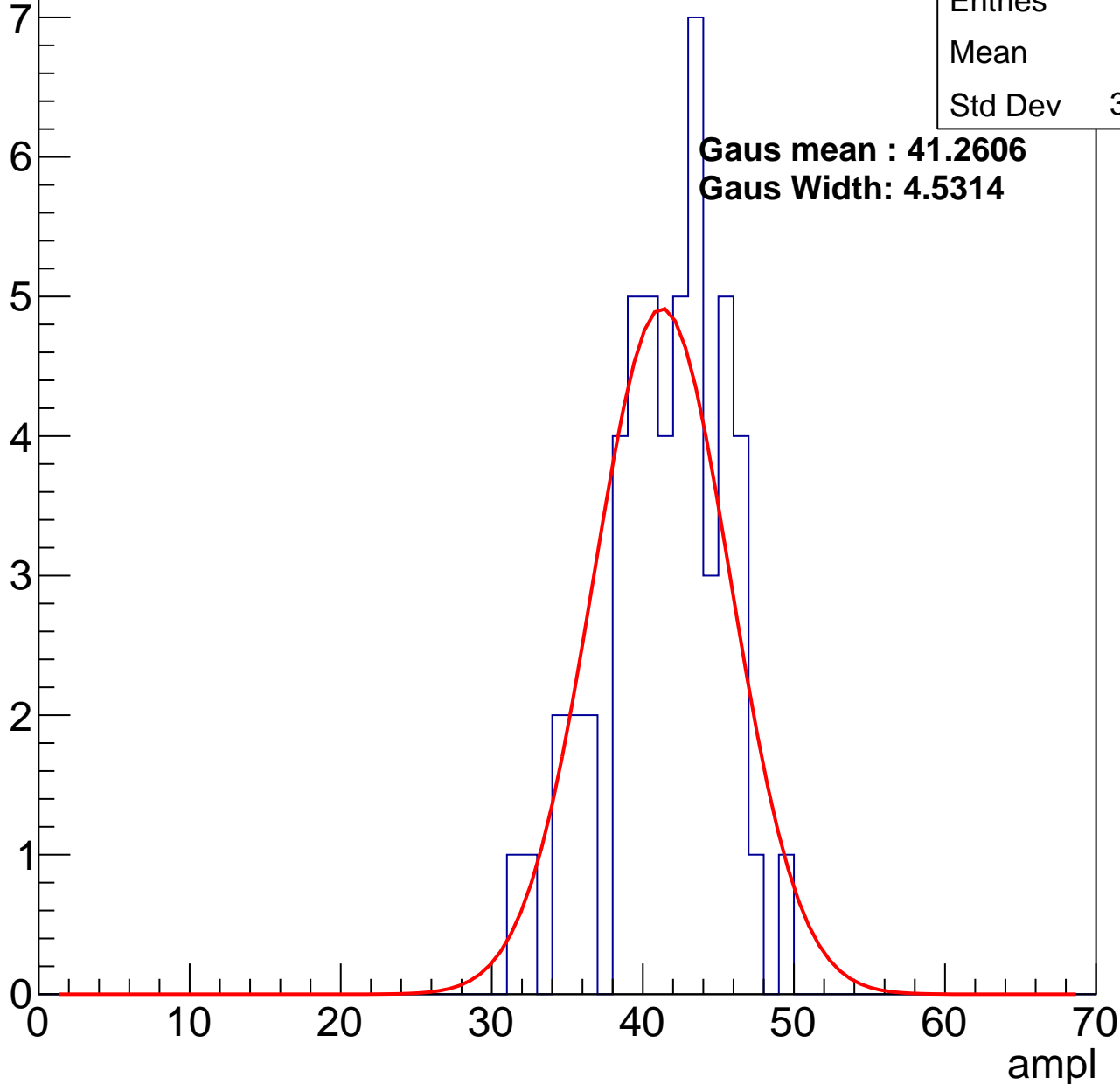
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	41
Std Dev	3.927

Gaus mean : 41.2606

Gaus Width: 4.5314

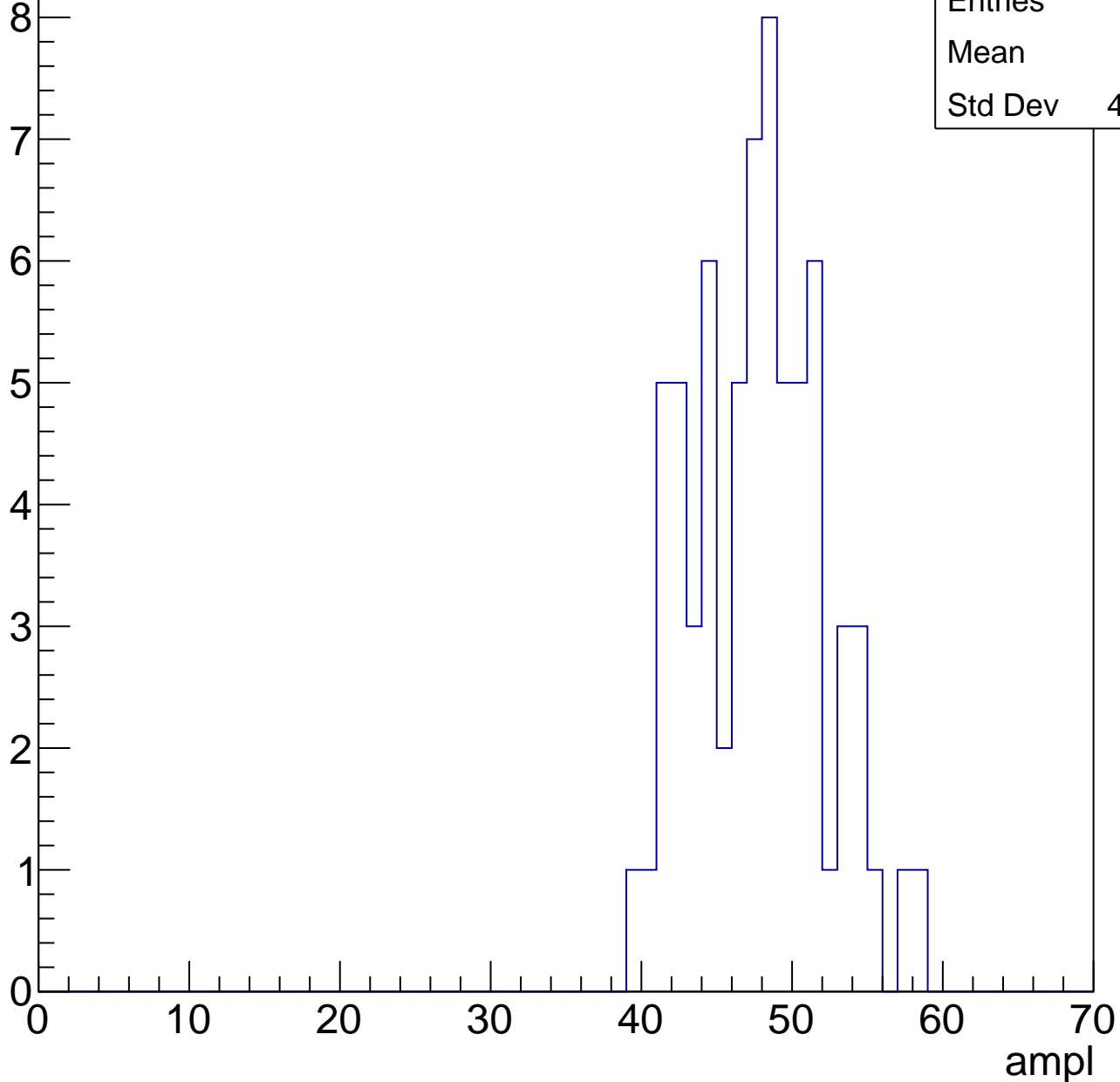


B1L102S, U14-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.3
Std Dev	4.274

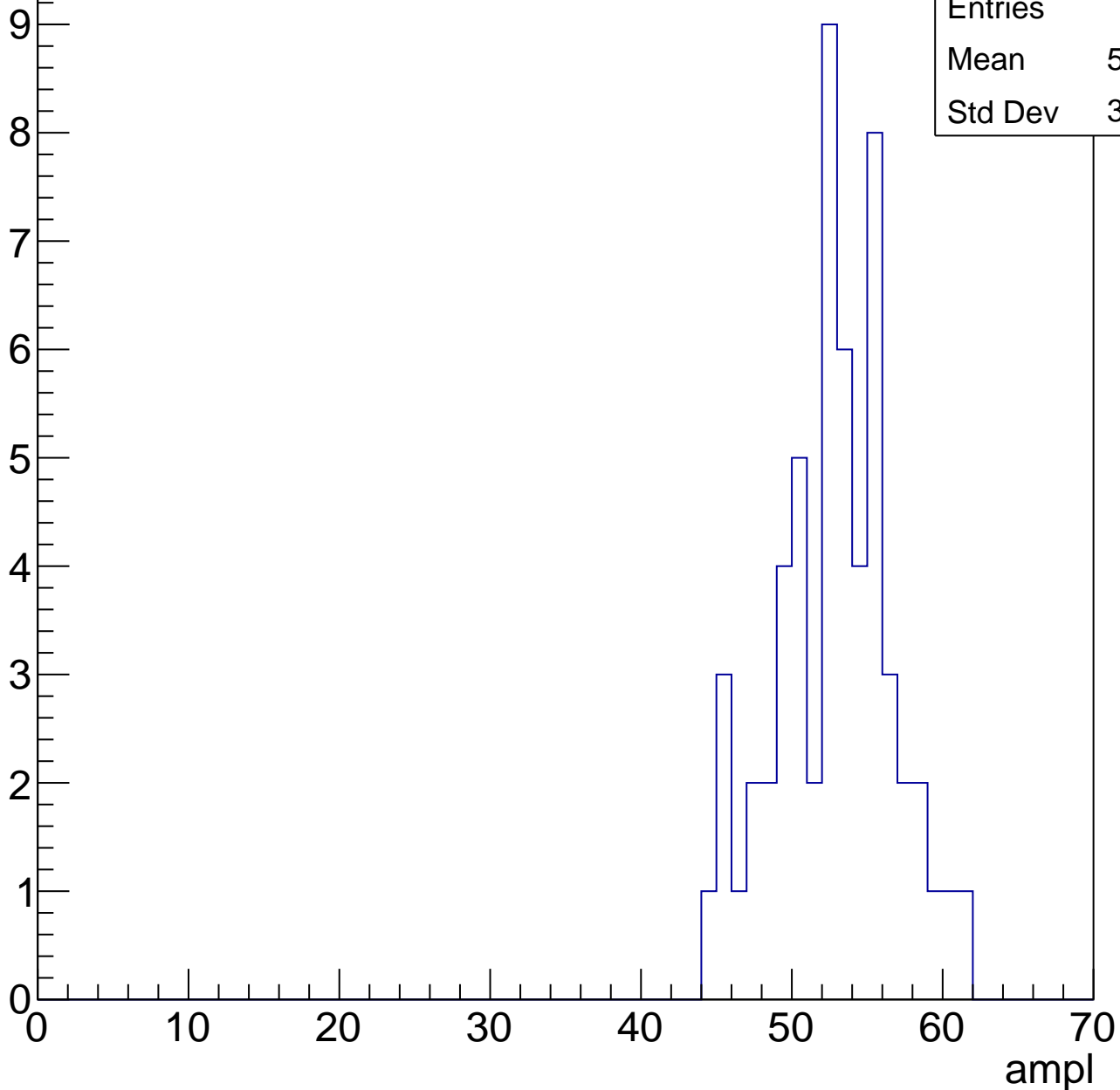


B1L102S, U14-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	52.33
Std Dev	3.836

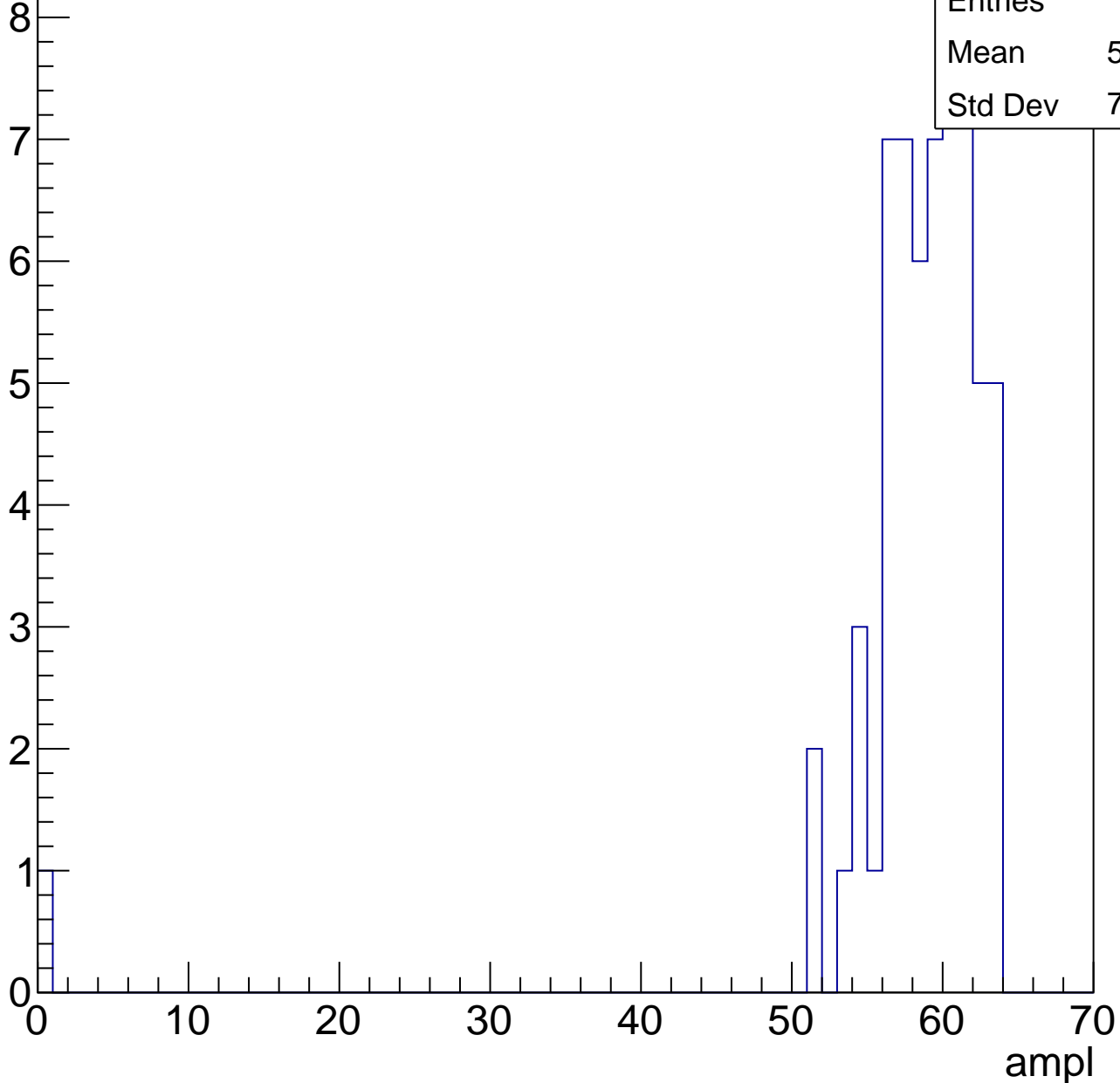


B1L102S, U14-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	57.66
Std Dev	7.983

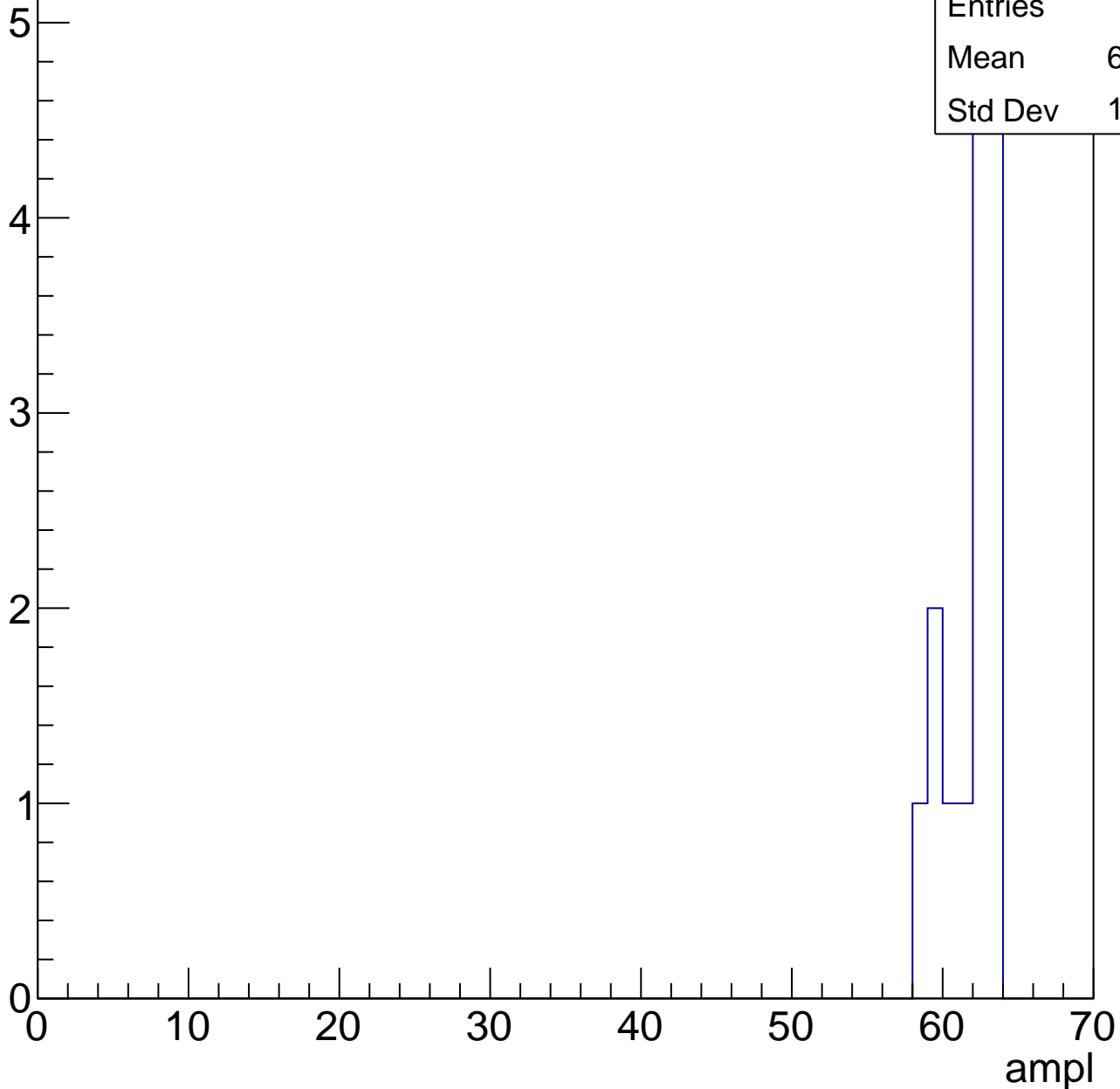


B1L102S, U14-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.47
Std Dev	1.628



B1L102S, U14-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch118, adc0

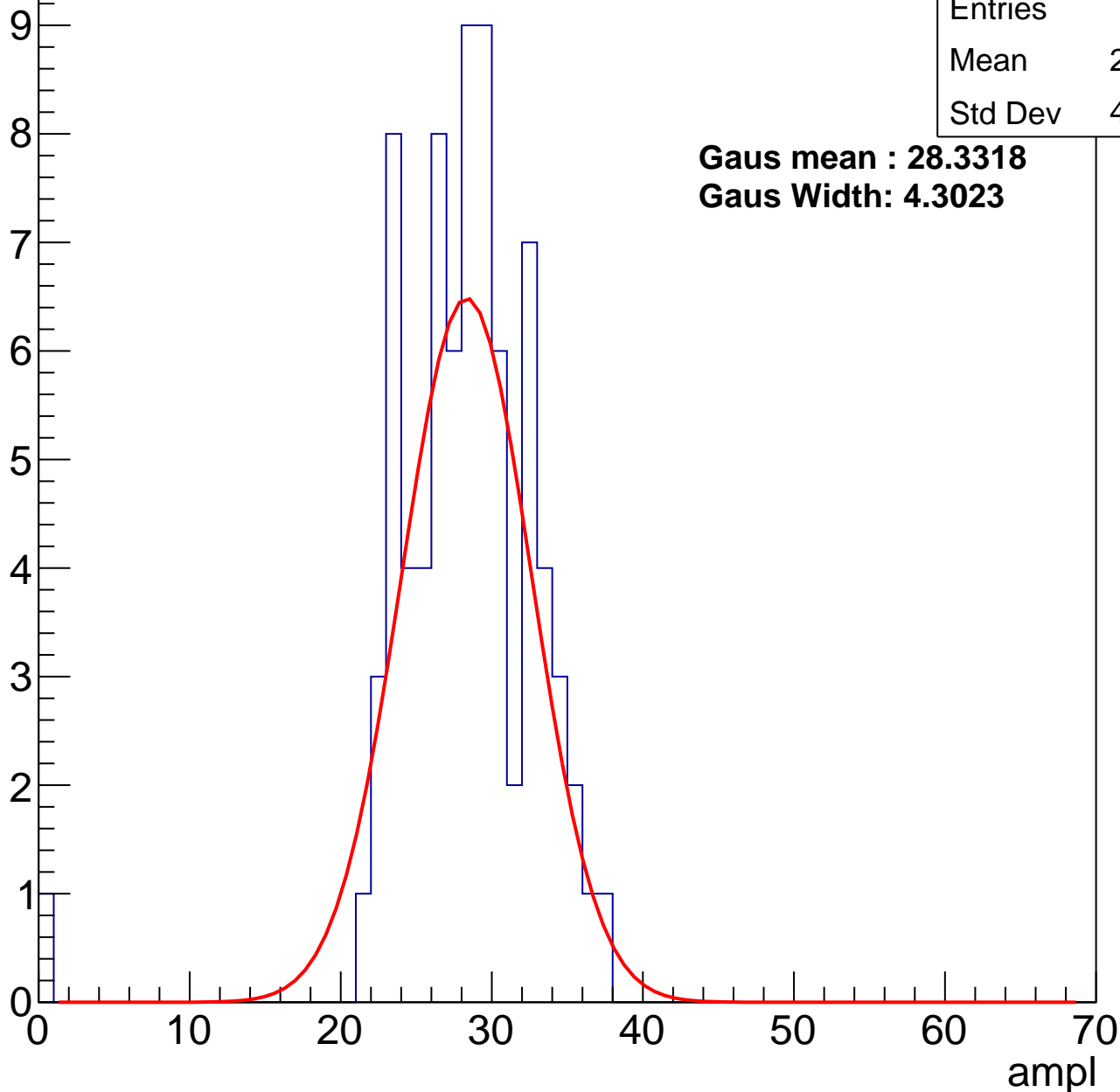
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	27.76
Std Dev	4.889

Gaus mean : 28.3318

Gaus Width: 4.3023



B1L102S, U14-ch118, adc1

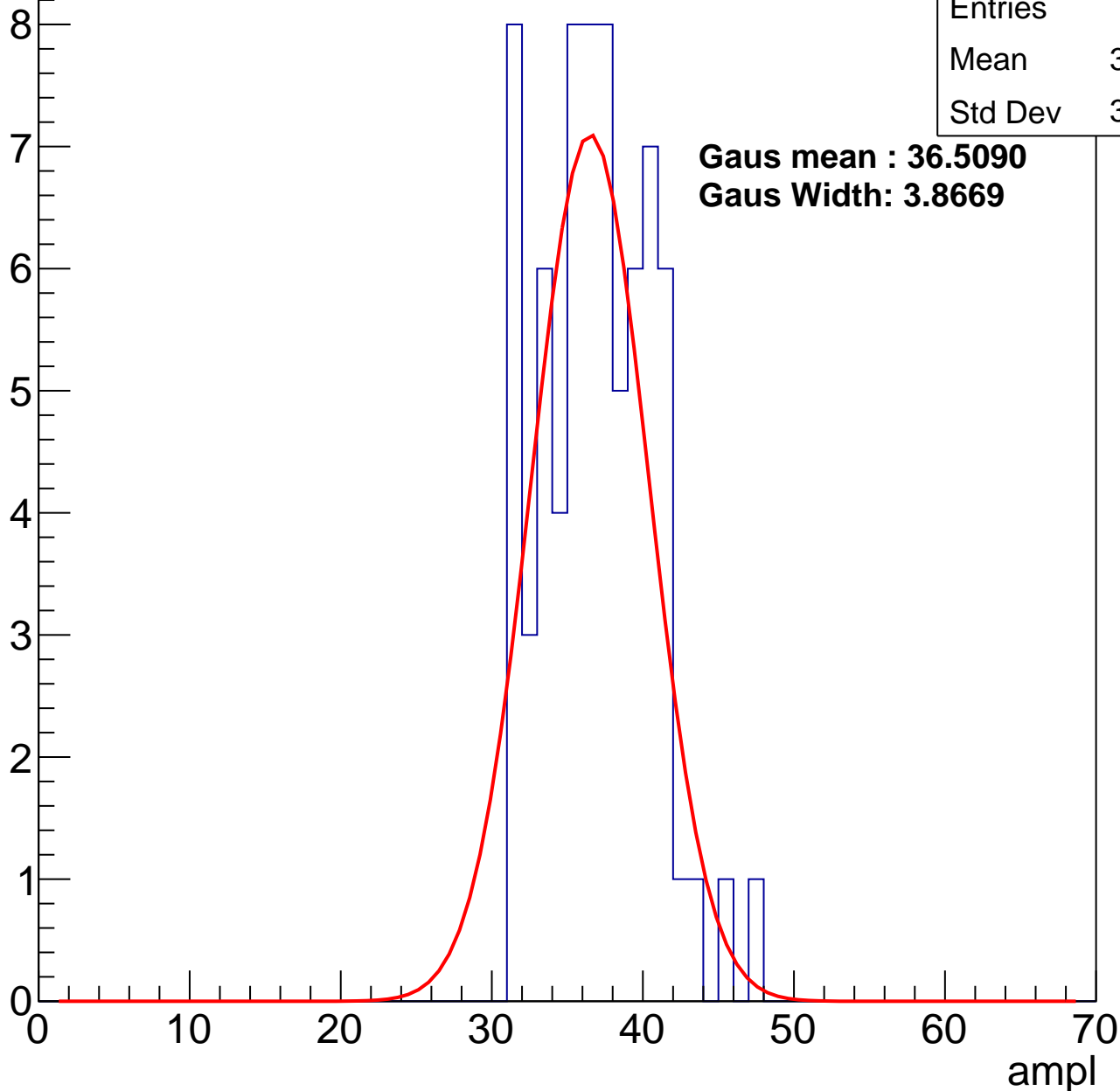
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.56
Std Dev	3.577

Gaus mean : 36.5090

Gaus Width: 3.8669



B1L102S, U14-ch118, adc2

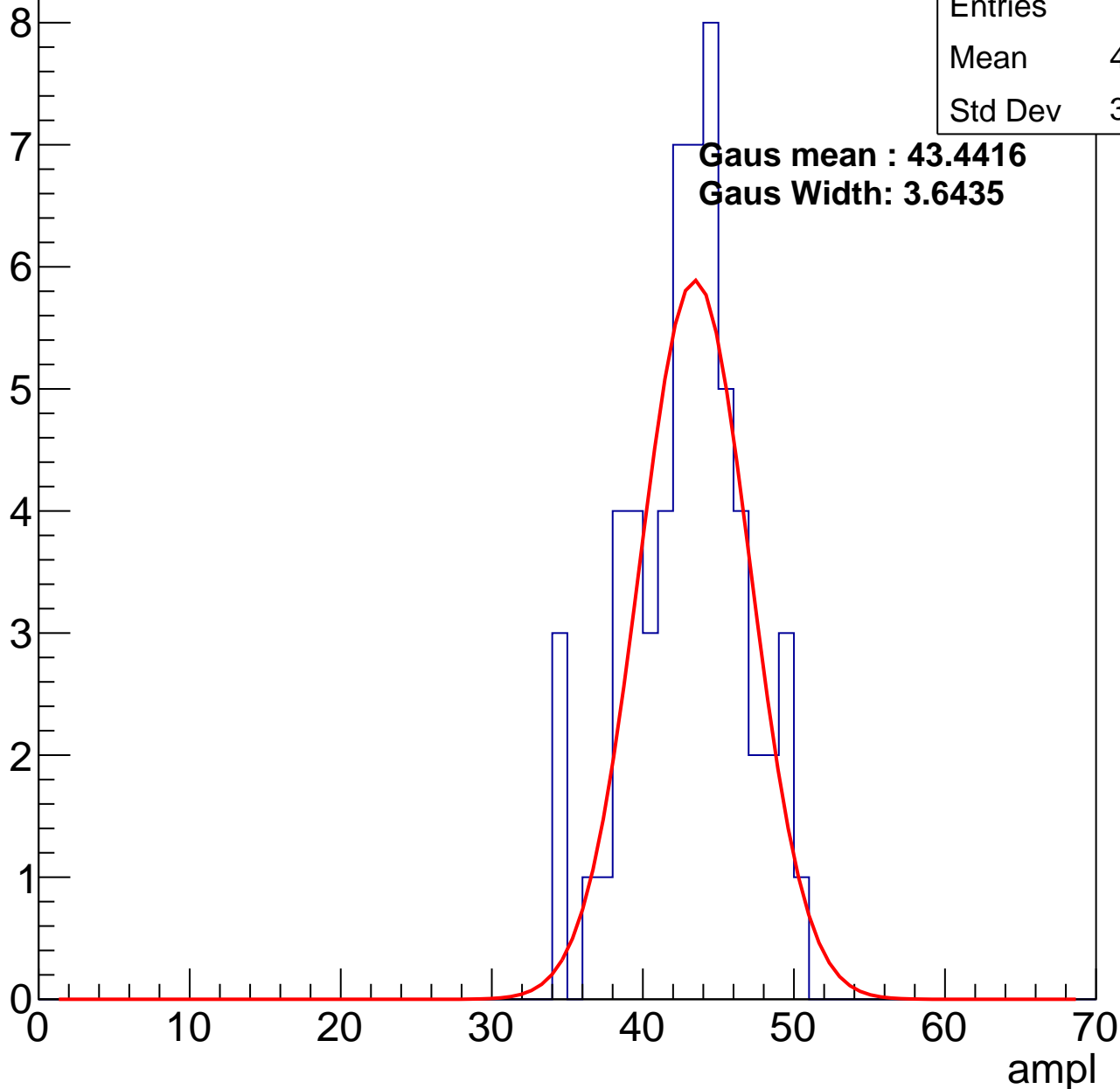
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.54
Std Dev	3.757

Gaus mean : 43.4416

Gaus Width: 3.6435

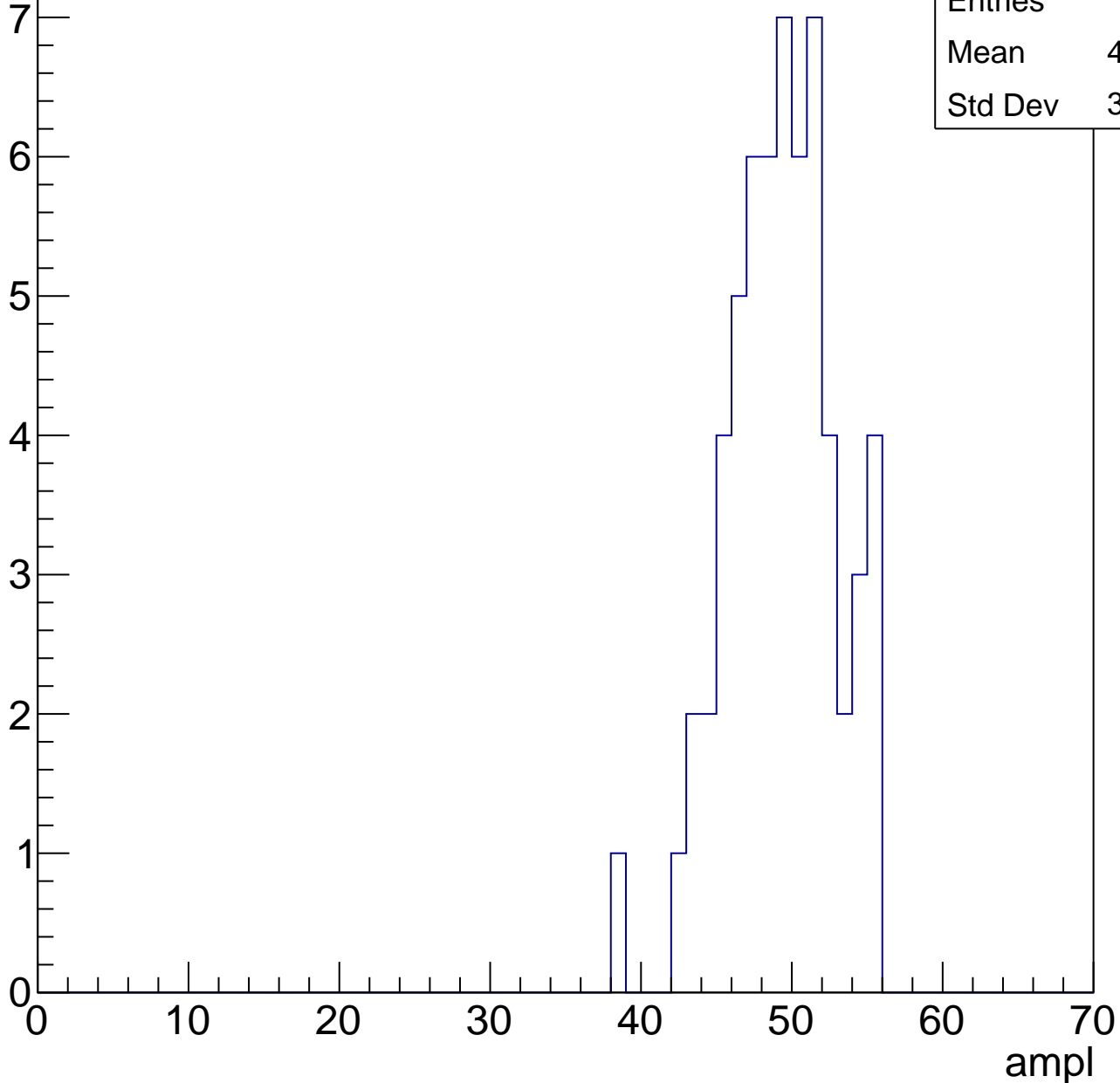


B1L102S, U14-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.83
Std Dev	3.536

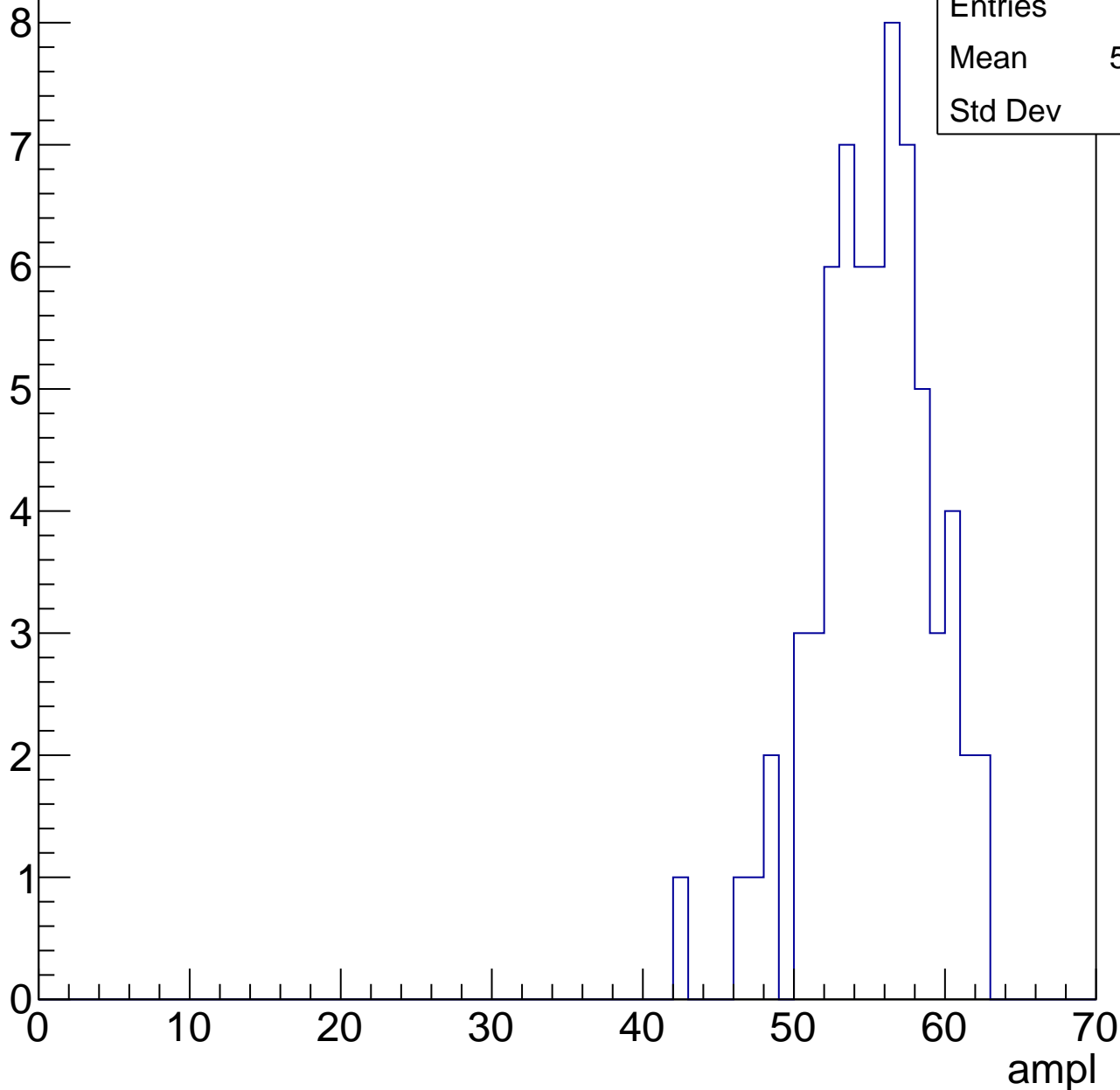


B1L102S, U14-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	54.79
Std Dev	3.9

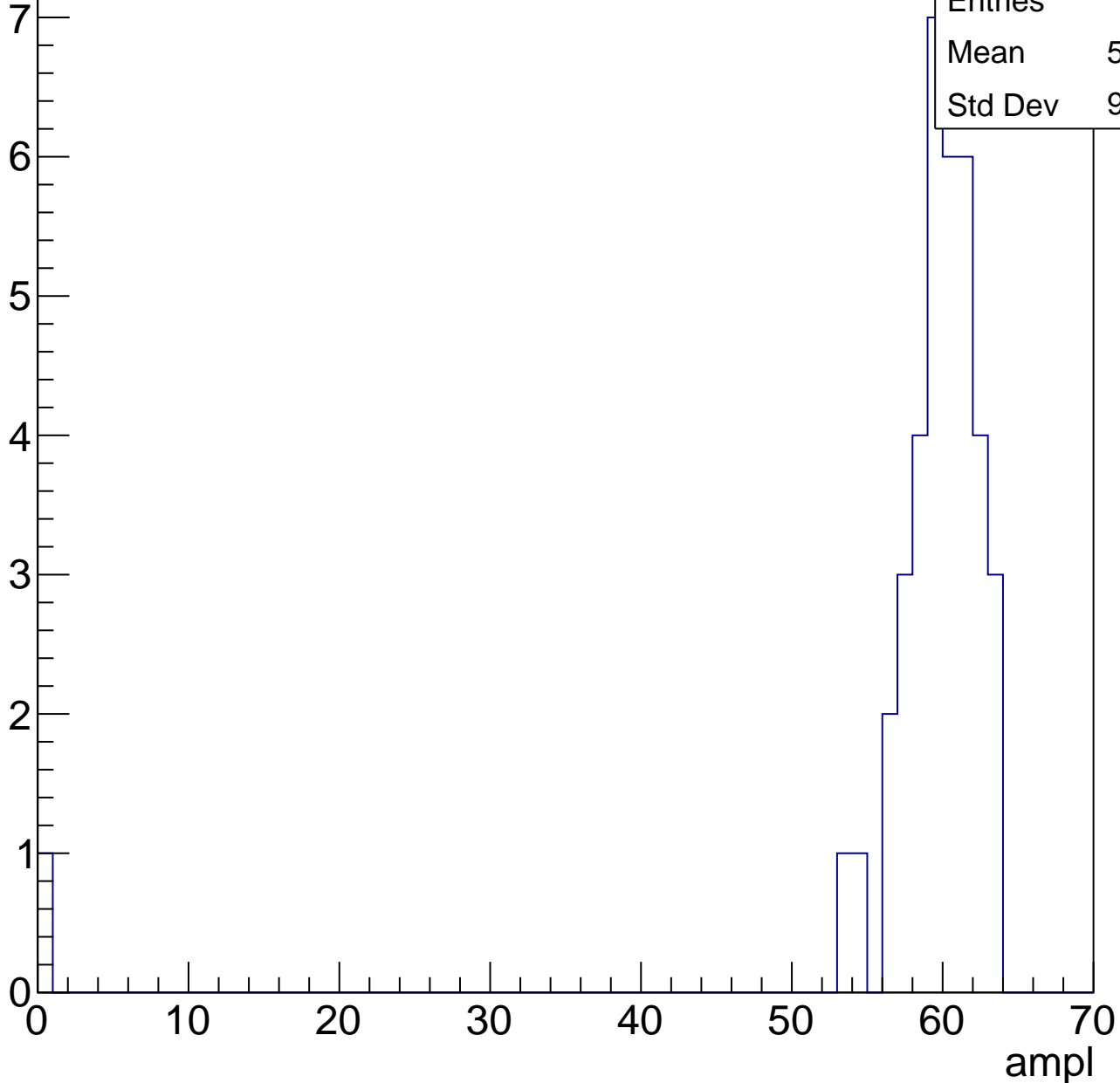


B1L102S, U14-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	57.84
Std Dev	9.786

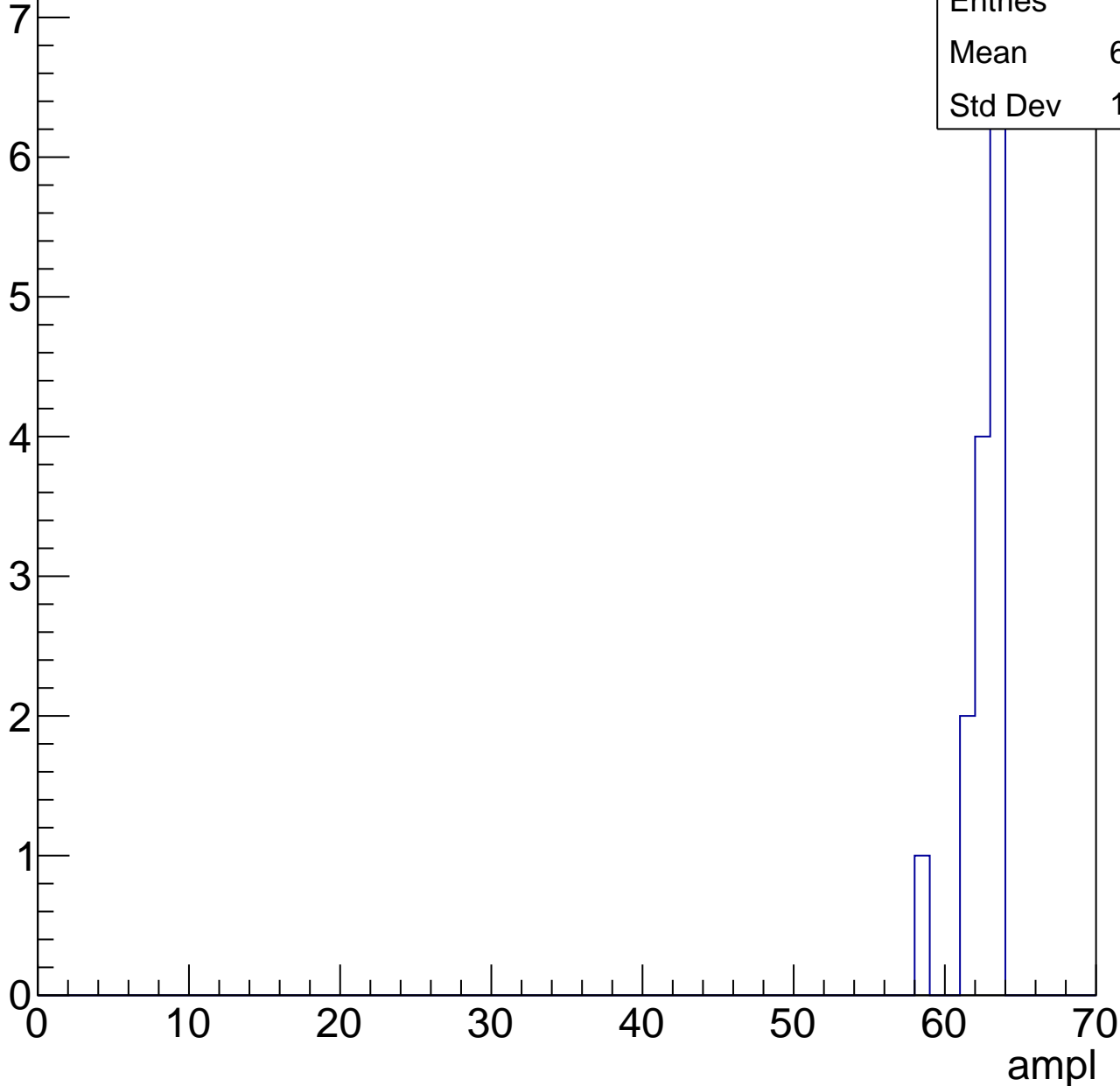


B1L102S, U14-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

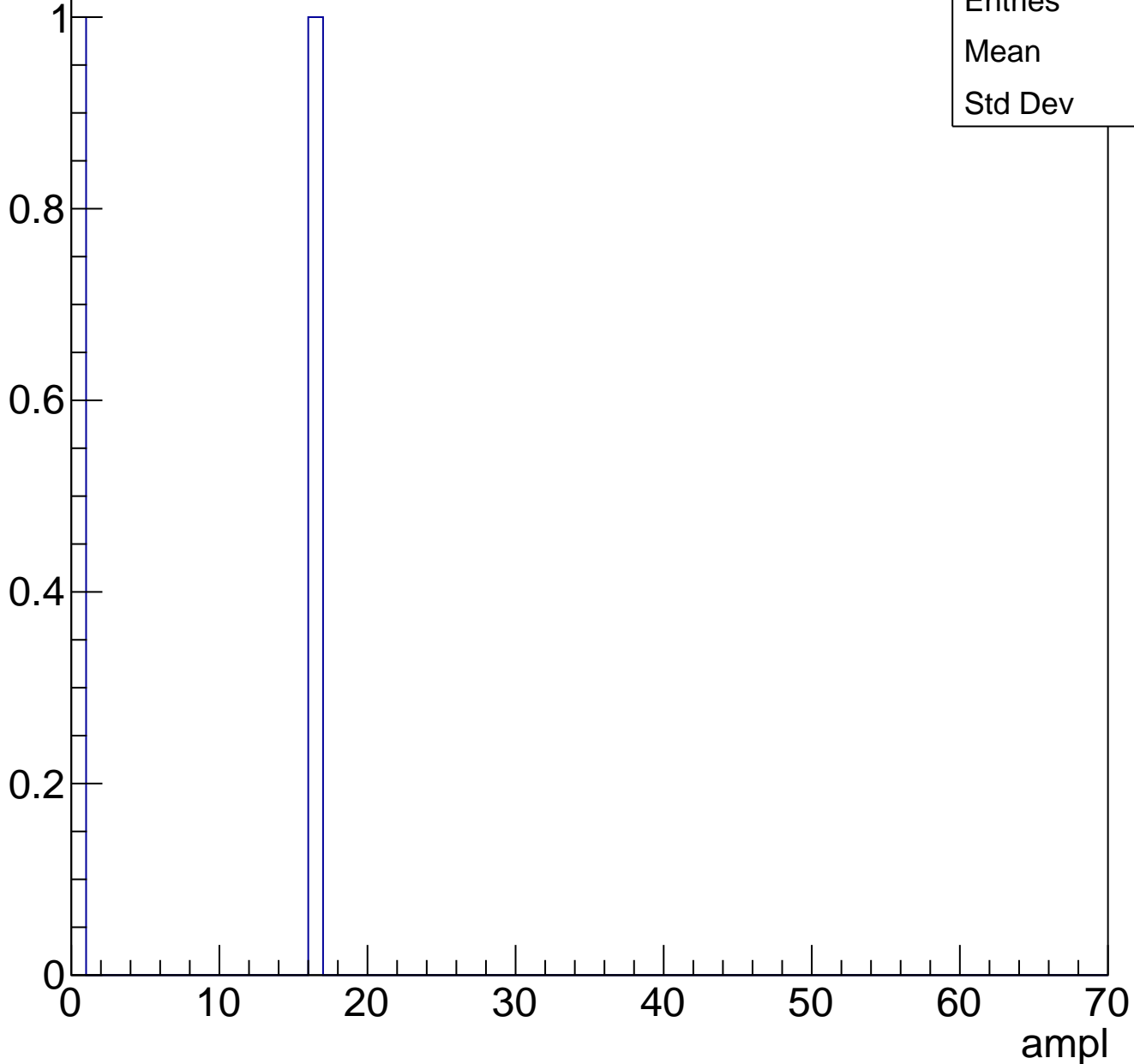
Entries	14
Mean	62.07
Std Dev	1.334



B1L102S, U14-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch119, adc0

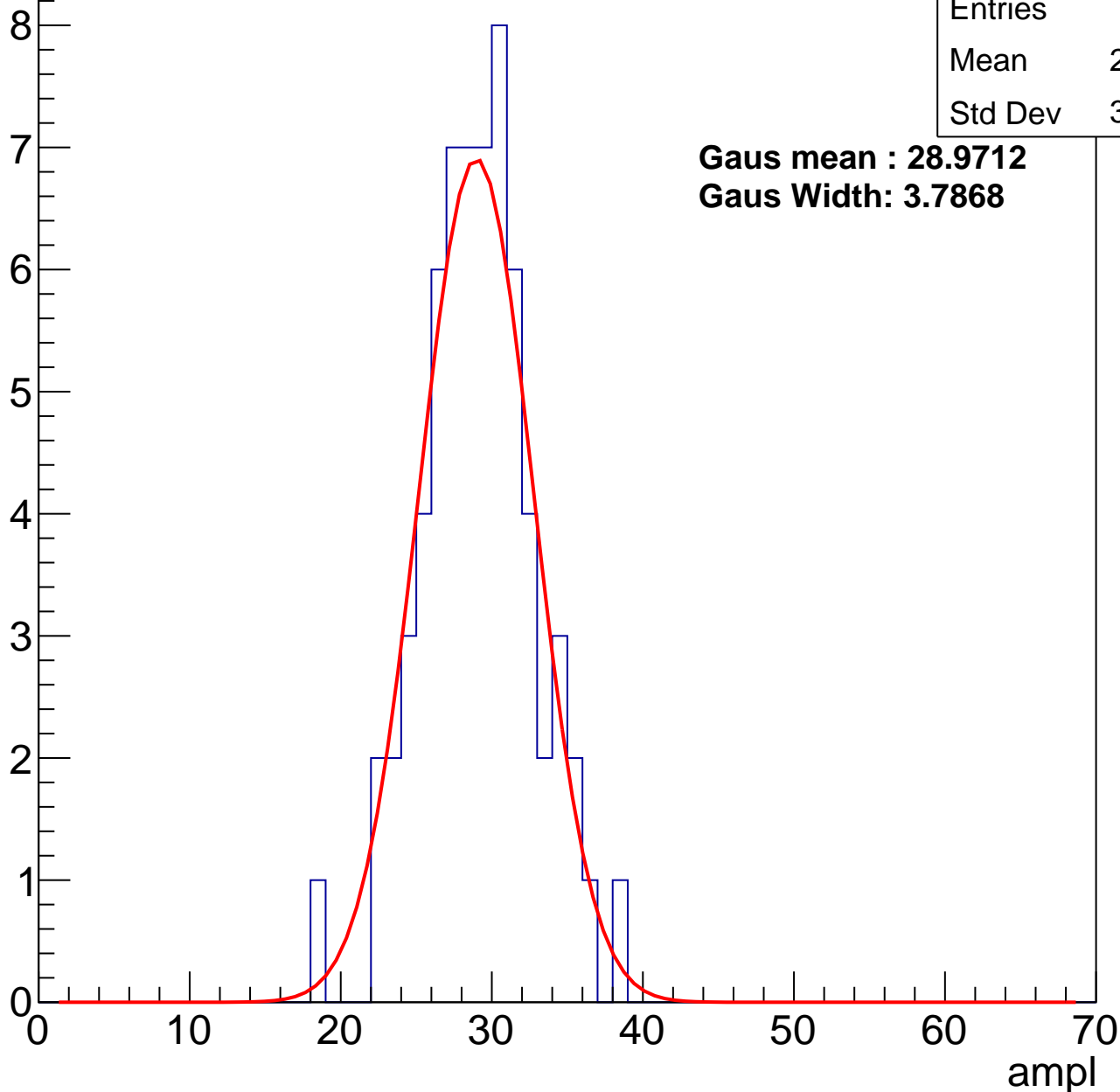
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.64
Std Dev	3.667

Gaus mean : 28.9712

Gaus Width: 3.7868



B1L102S, U14-ch119, adc1

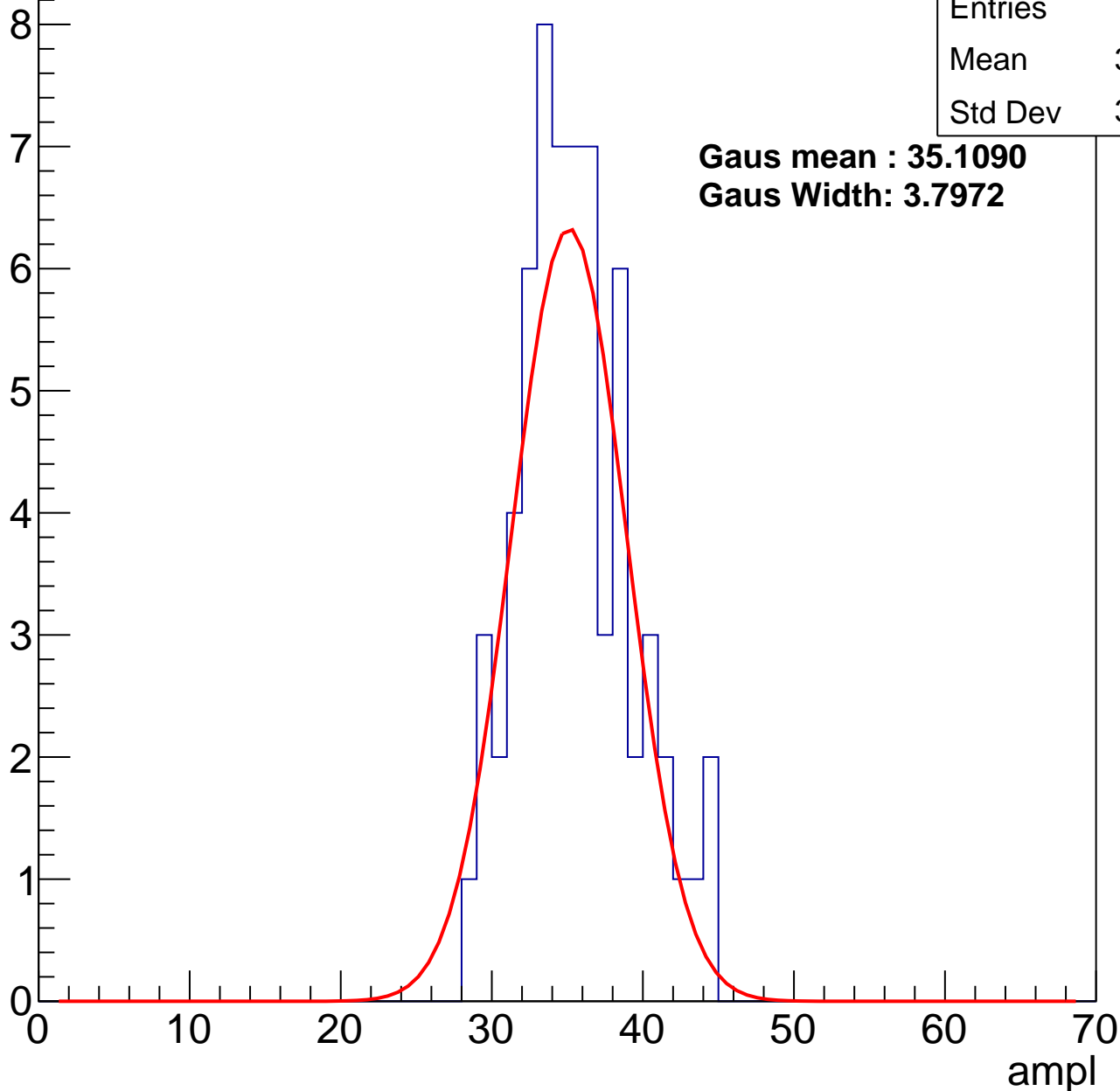
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.11
Std Dev	3.701

Gaus mean : 35.1090

Gaus Width: 3.7972



B1L102S, U14-ch119, adc2

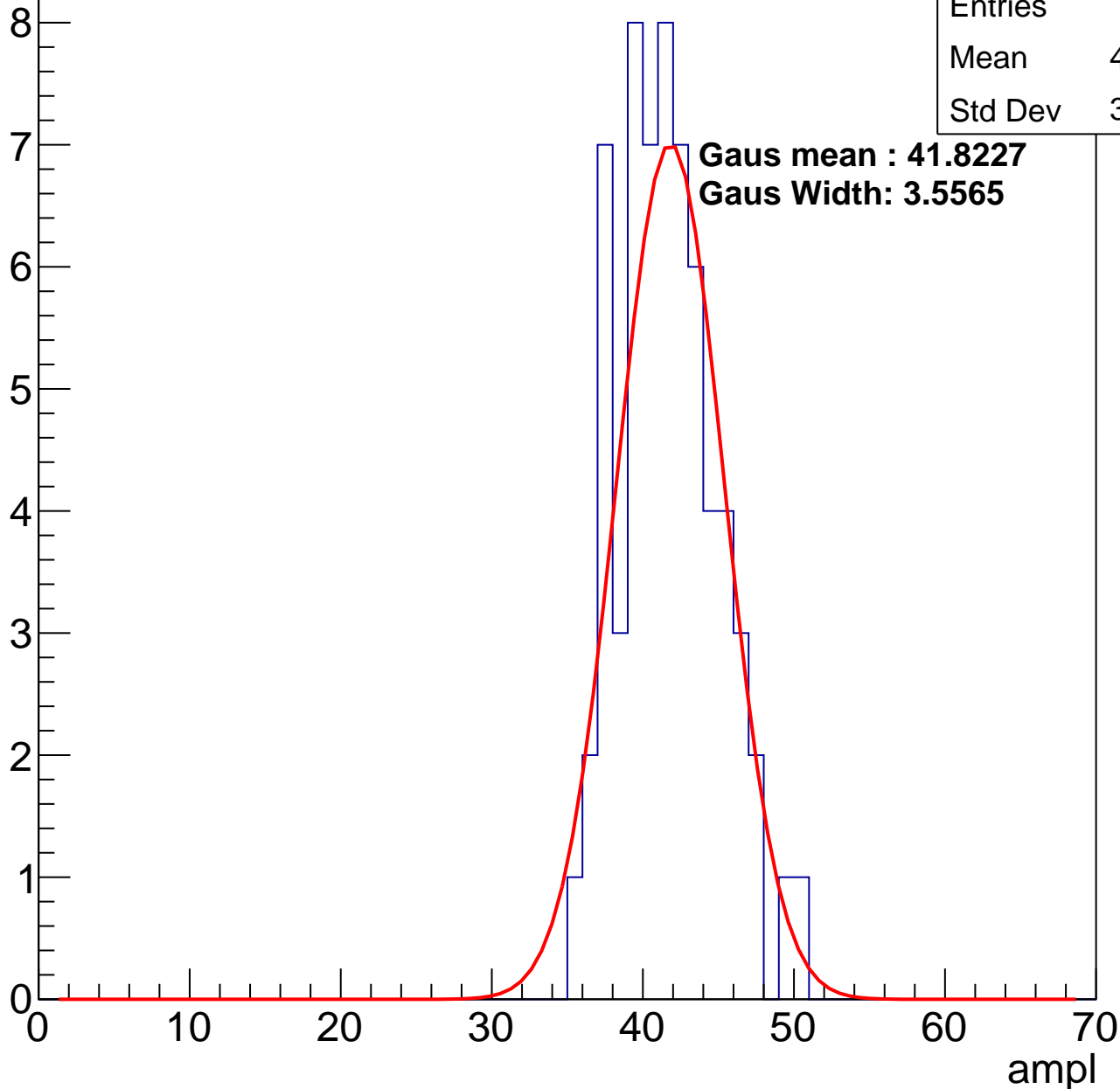
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.23
Std Dev	3.268

Gaus mean : 41.8227

Gaus Width: 3.5565



B1L102S, U14-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	48.41
Std Dev	3.308

Entry

10

8

6

4

2

0

0

10

20

30

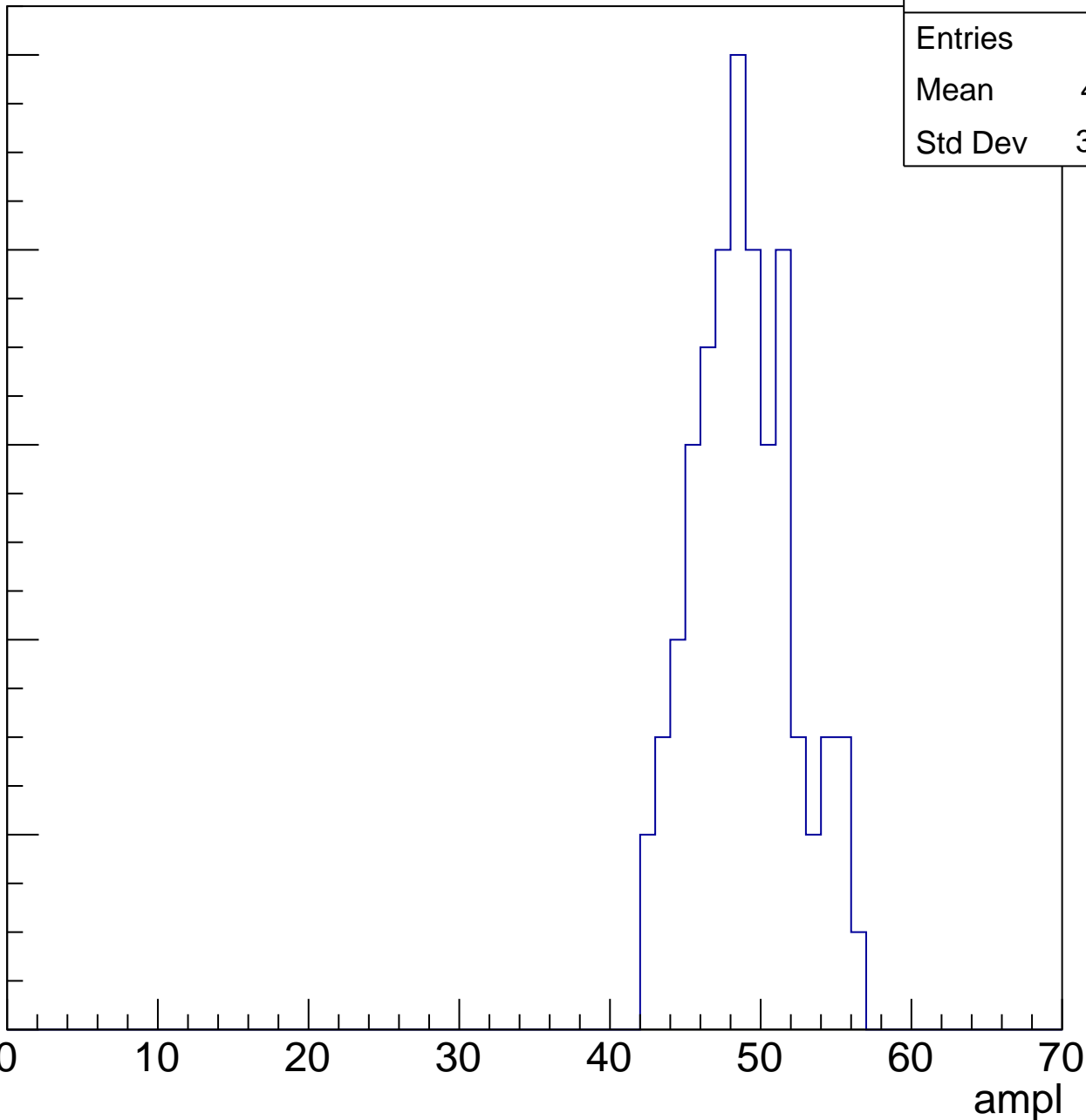
40

50

60

70

ampl

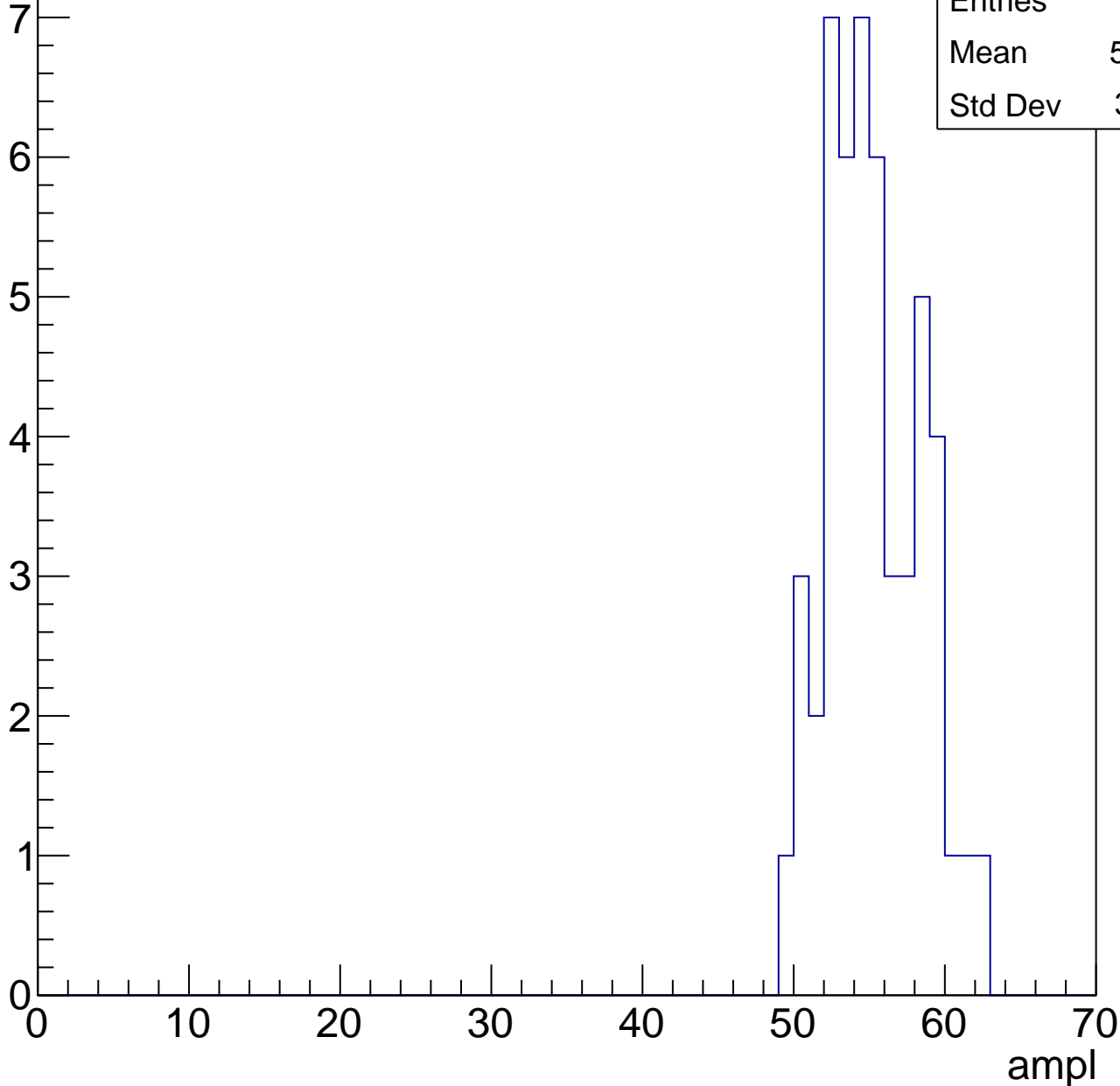


B1L102S, U14-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	54.78
Std Dev	3.061

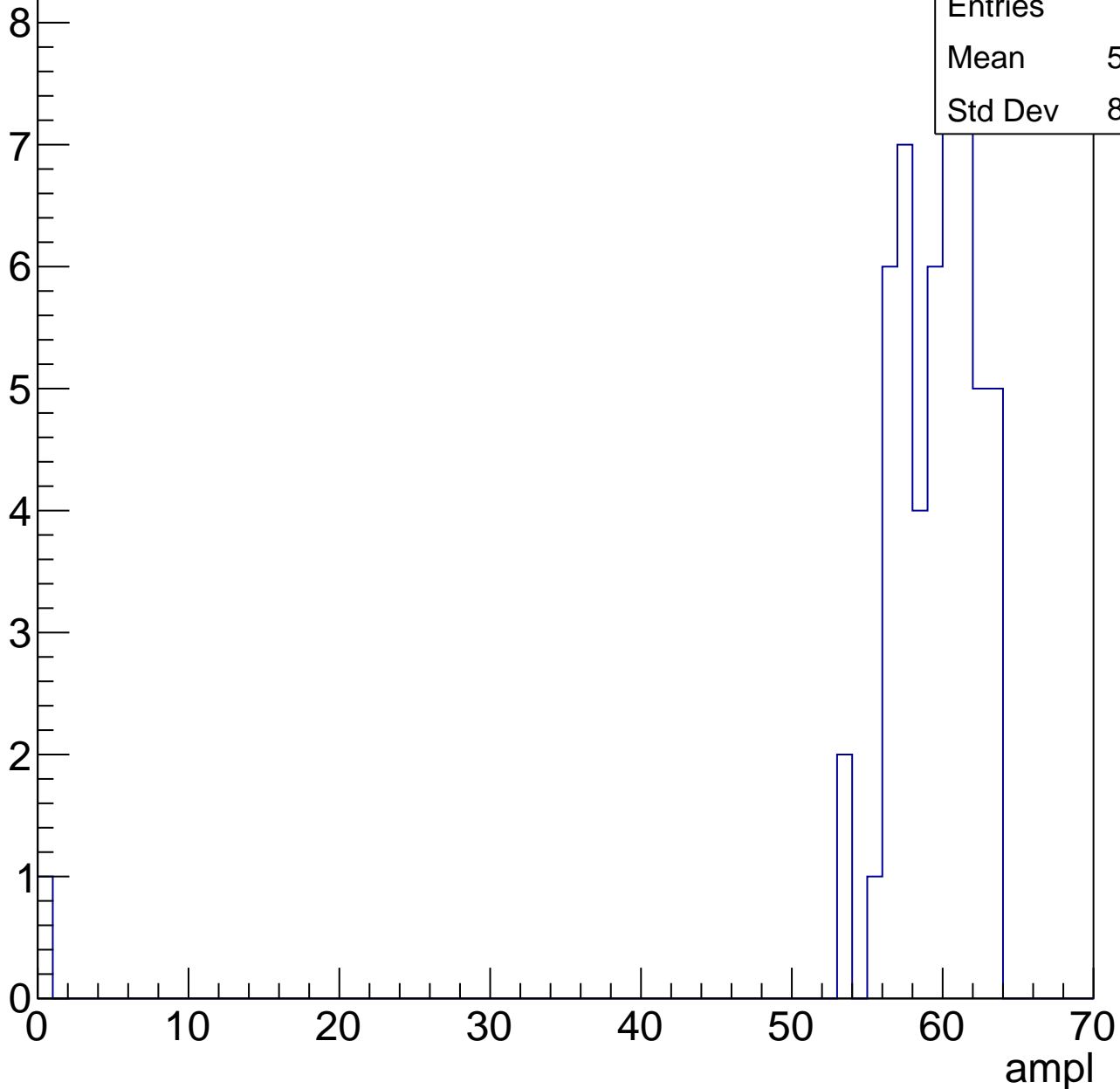


B1L102S, U14-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.02
Std Dev	8.433

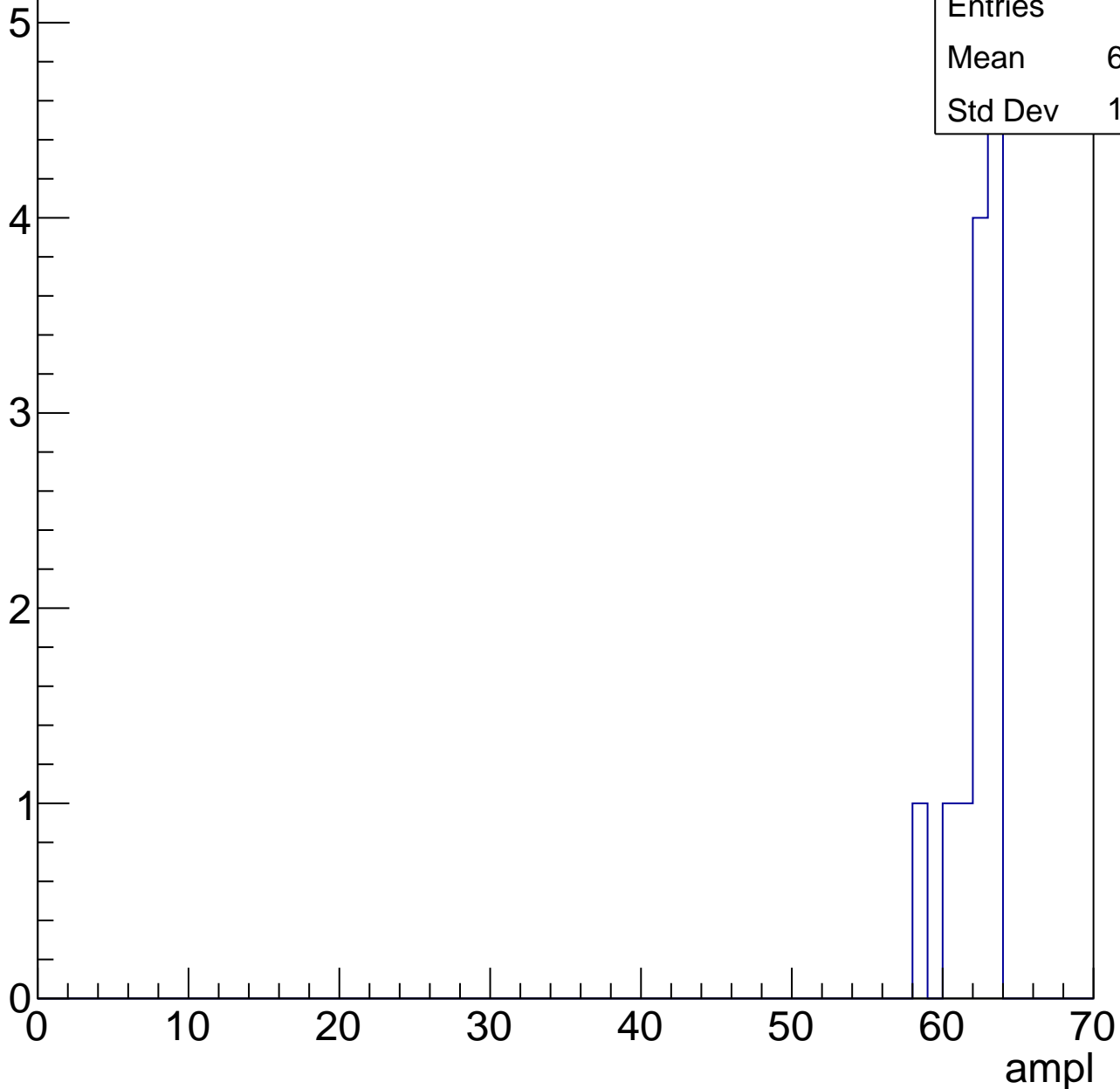


B1L102S, U14-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.83
Std Dev	1.462



B1L102S, U14-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch120, adc0

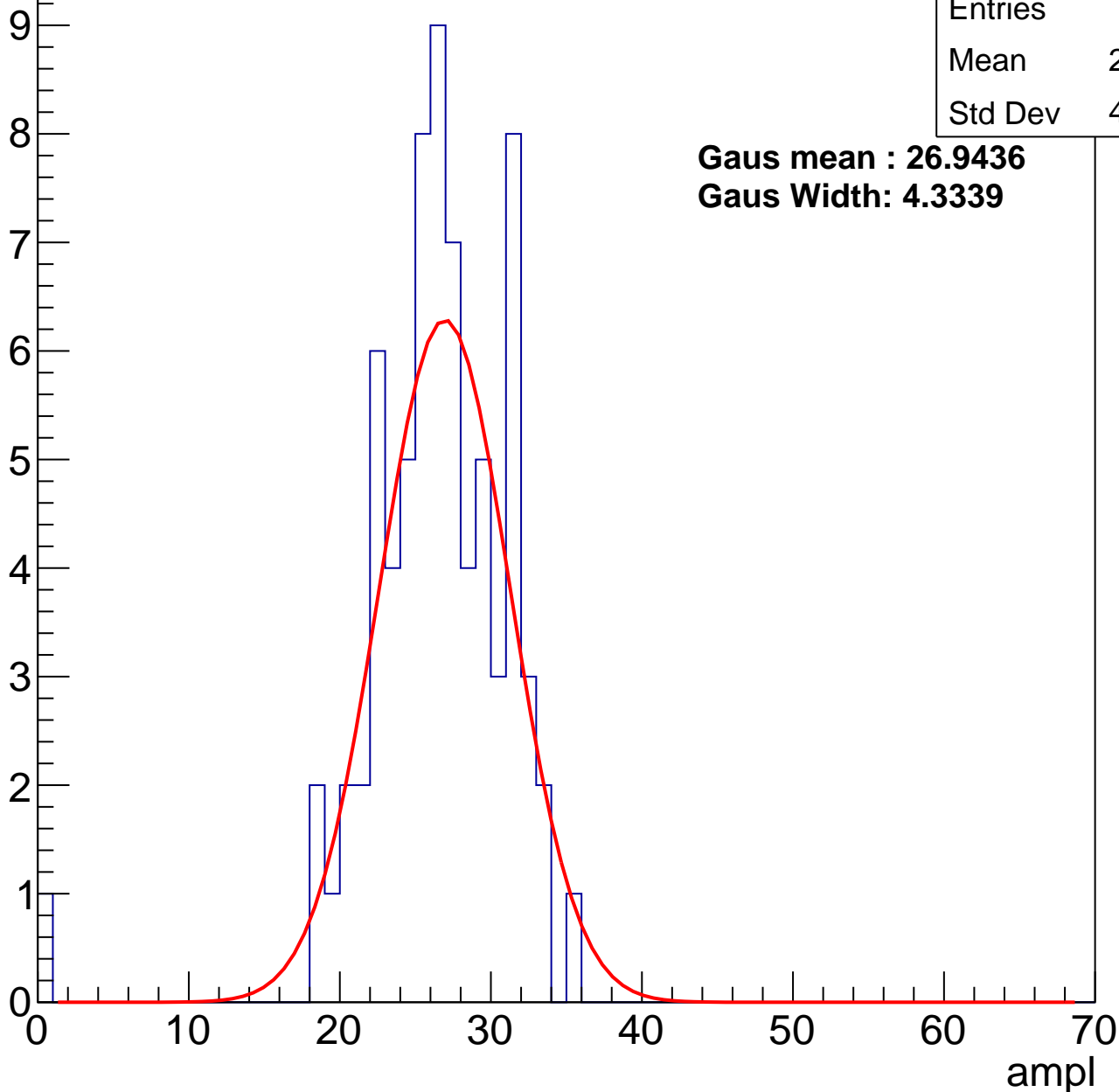
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	25.97
Std Dev	4.879

Gaus mean : 26.9436

Gaus Width: 4.3339



B1L102S, U14-ch120, adc1

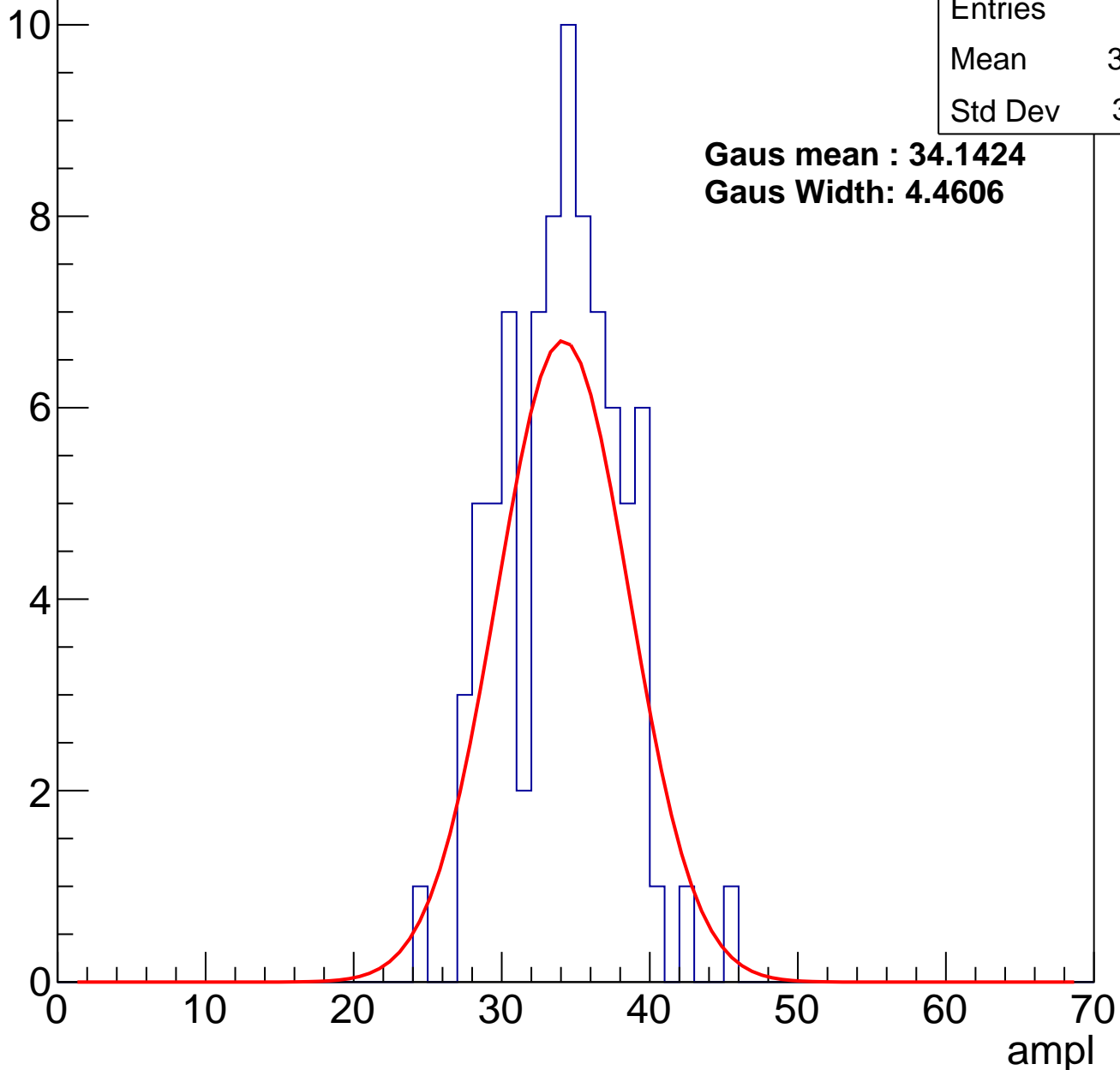
calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	33.67
Std Dev	3.881

Gaus mean : 34.1424

Gaus Width: 4.4606

Entry



B1L102S, U14-ch120, adc2

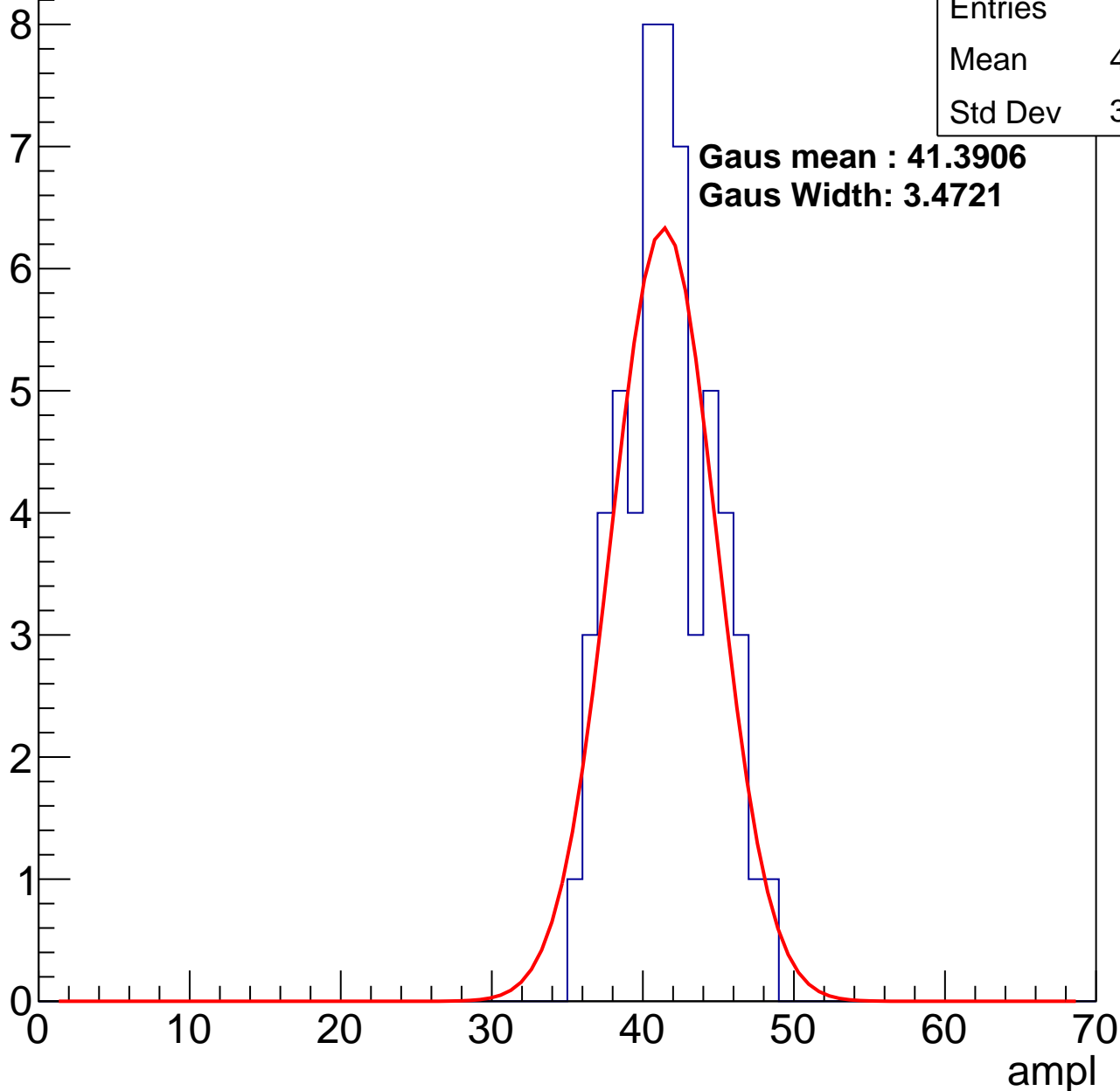
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.07
Std Dev	3.054

Gaus mean : 41.3906

Gaus Width: 3.4721

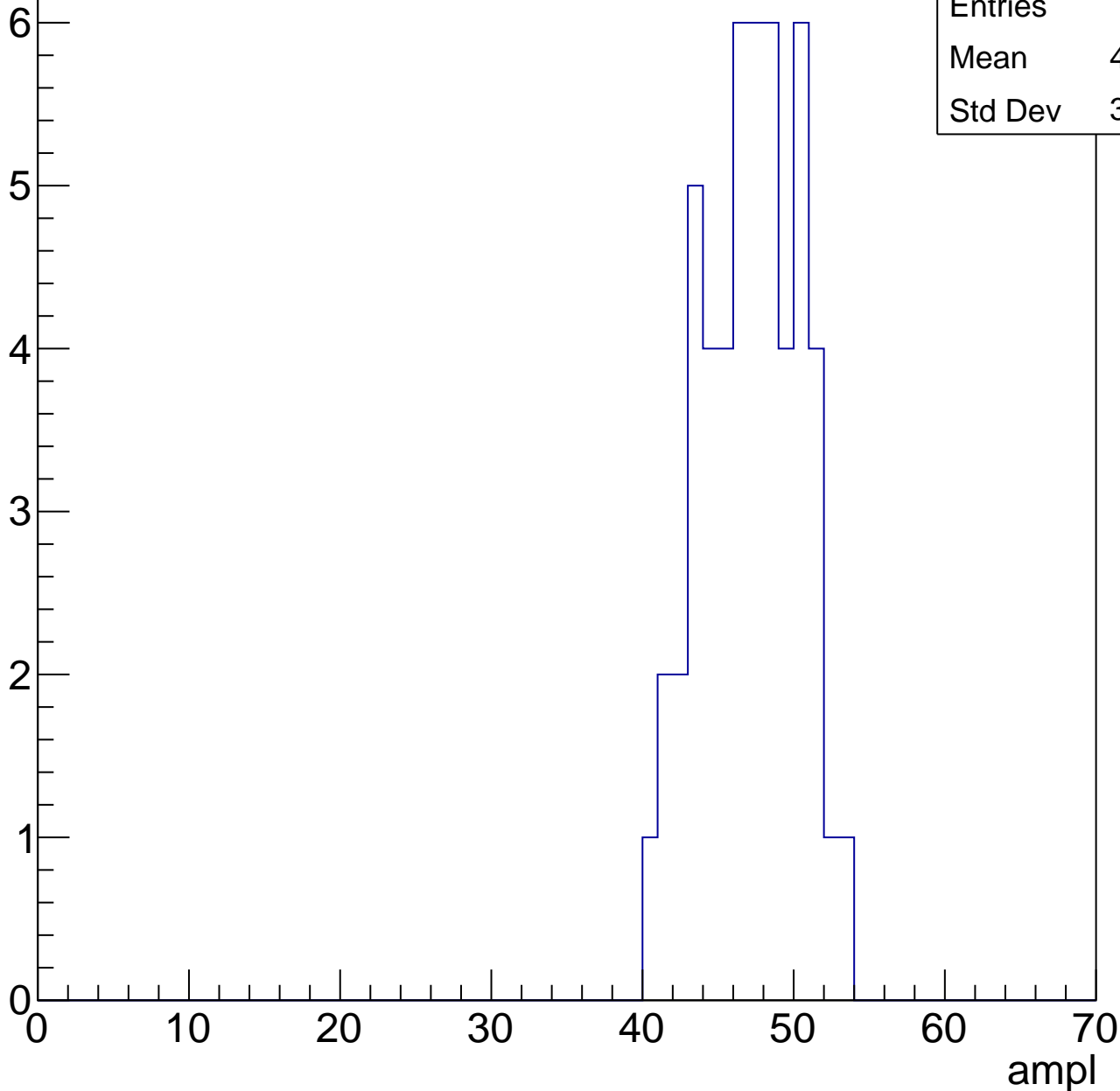


B1L102S, U14-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	46.69
Std Dev	3.117

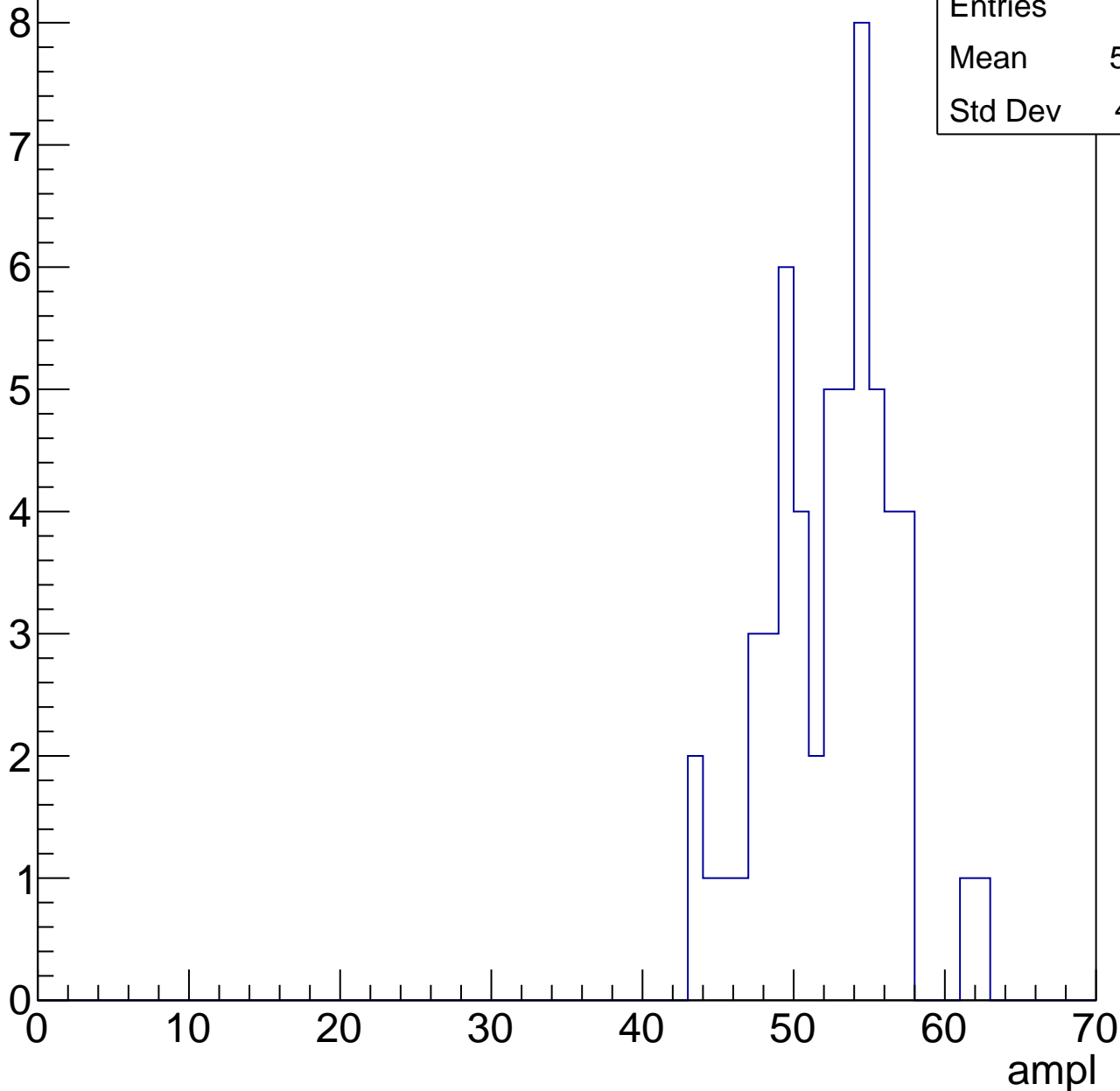


B1L102S, U14-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	51.95
Std Dev	4.081

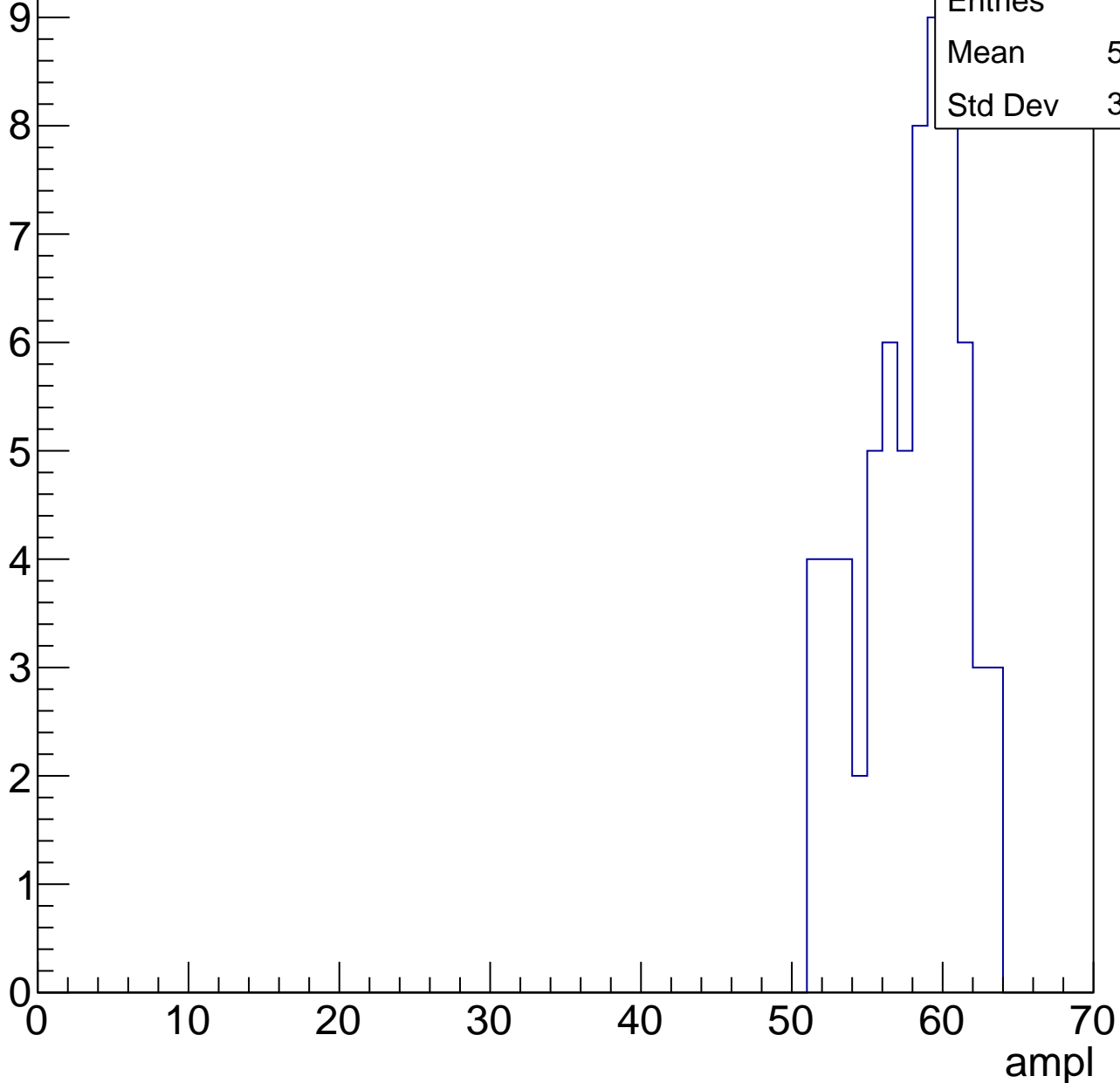


B1L102S, U14-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	57.37
Std Dev	3.318

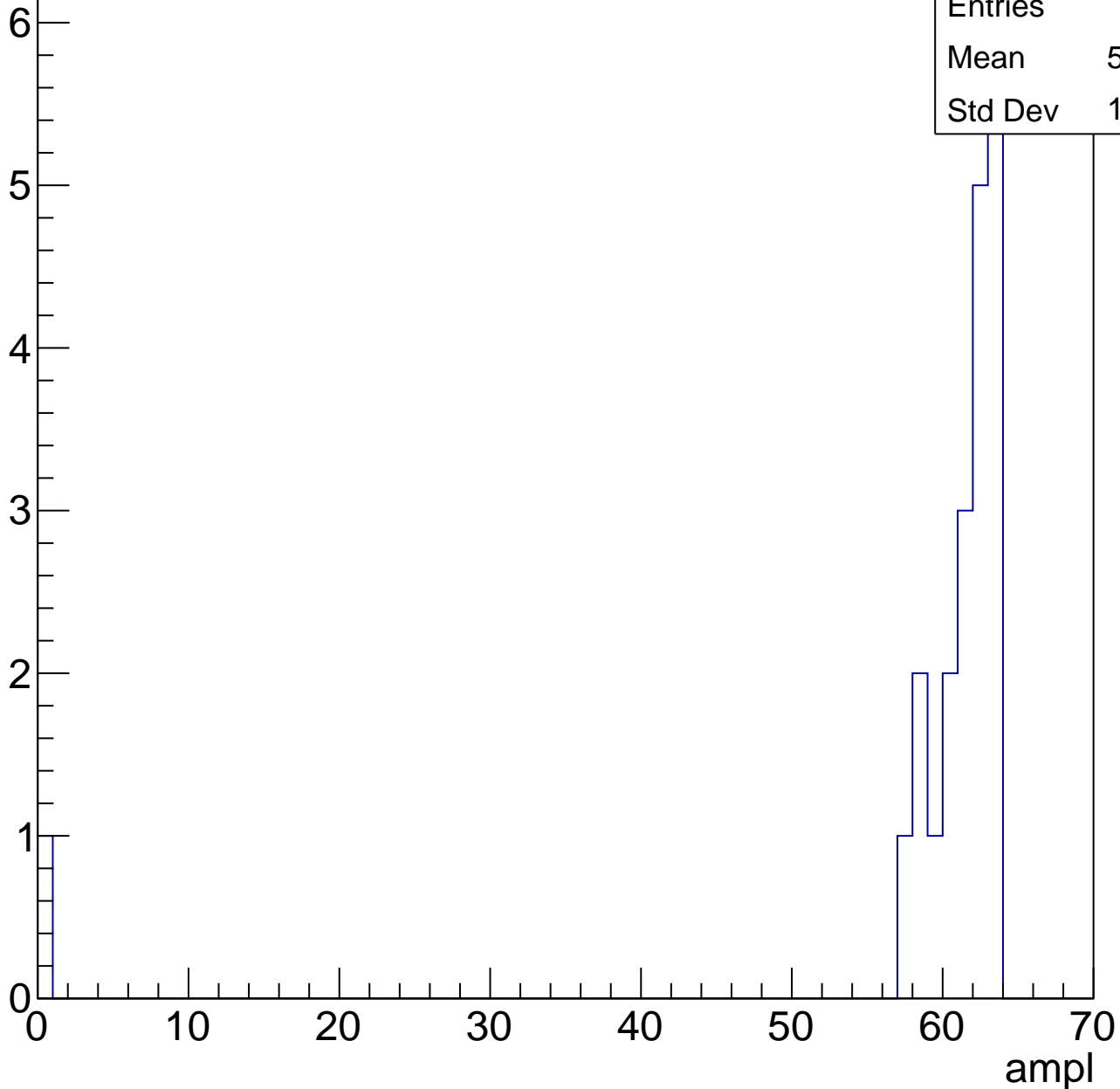


B1L102S, U14-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	58.24
Std Dev	13.15



B1L102S, U14-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch121, adc0

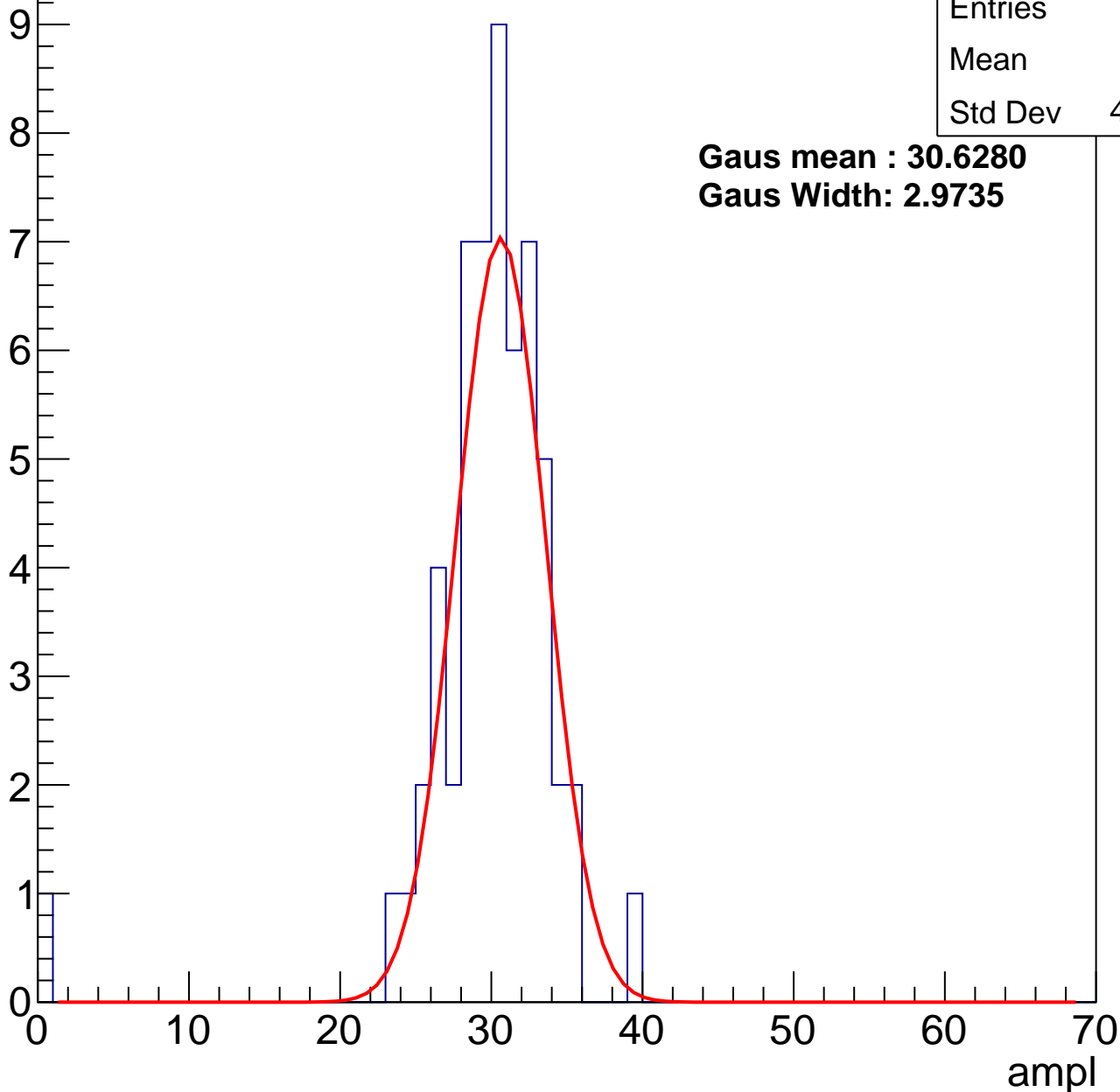
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	29.4
Std Dev	4.909

Gaus mean : 30.6280

Gaus Width: 2.9735



B1L102S, U14-ch121, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	36.39
Std Dev	3.406

Gaus mean : 36.8284

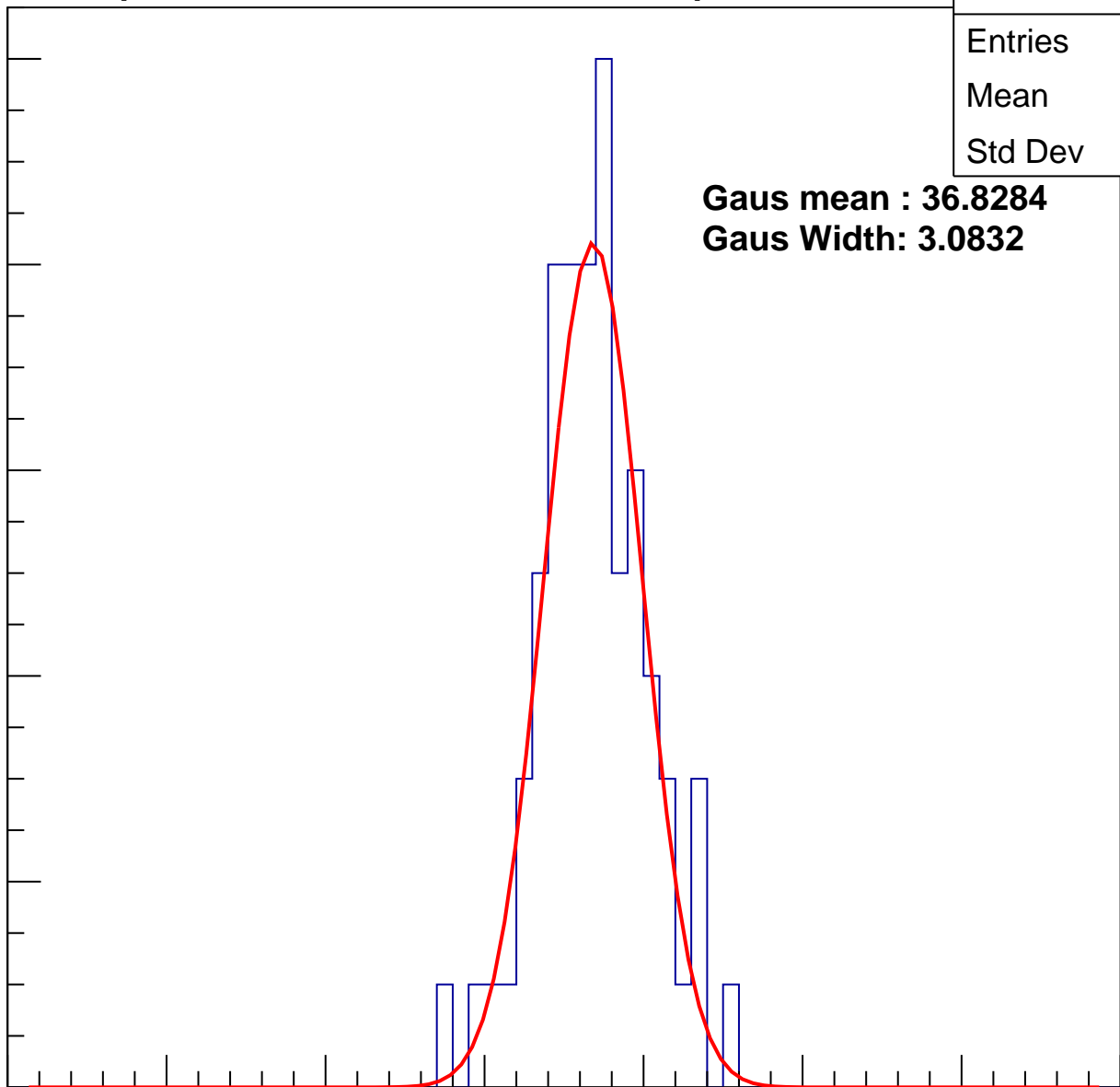
Gaus Width: 3.0832

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch121, adc2

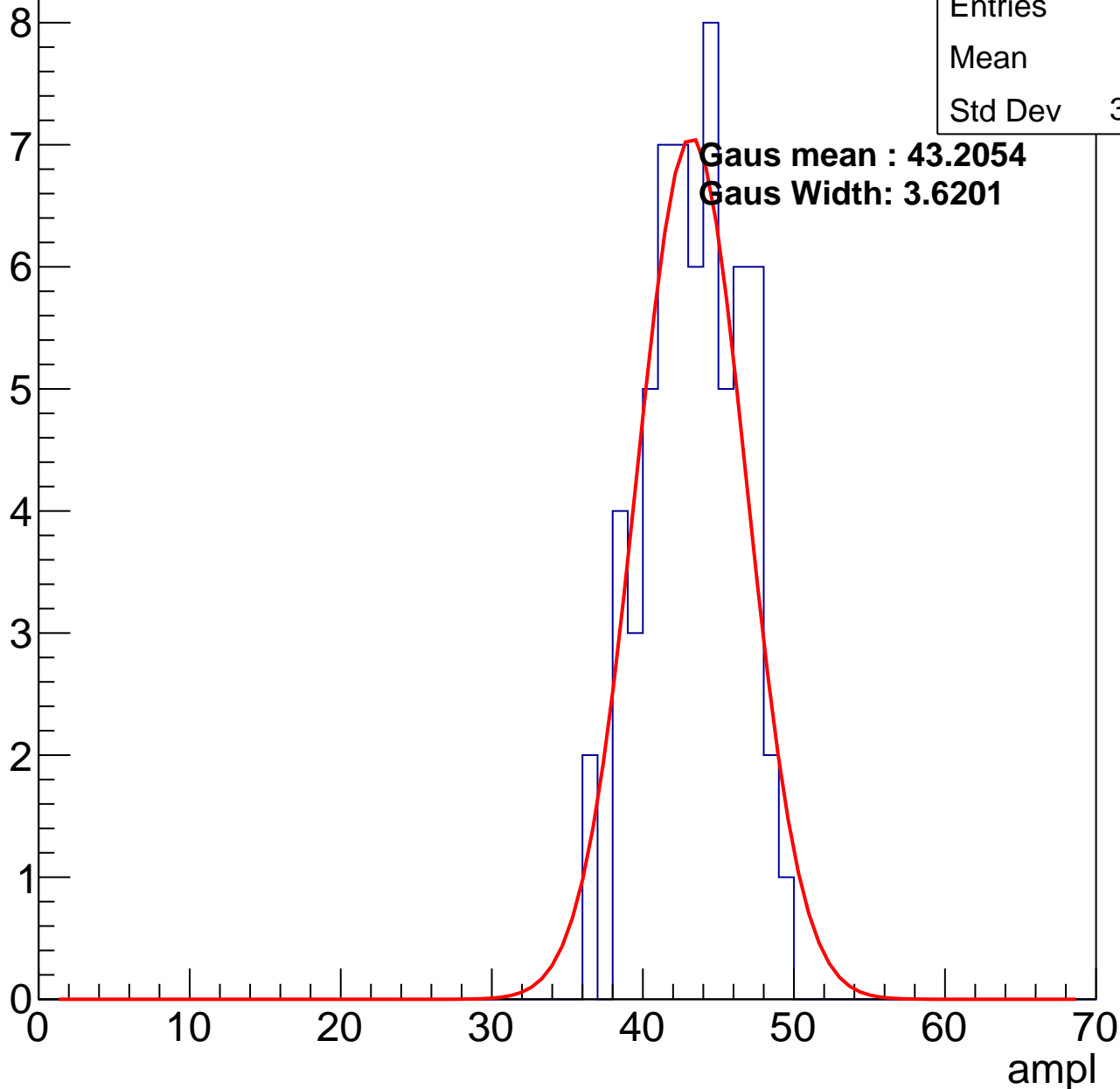
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.9
Std Dev	3.083

Gaus mean : 43.2054

Gaus Width: 3.6201

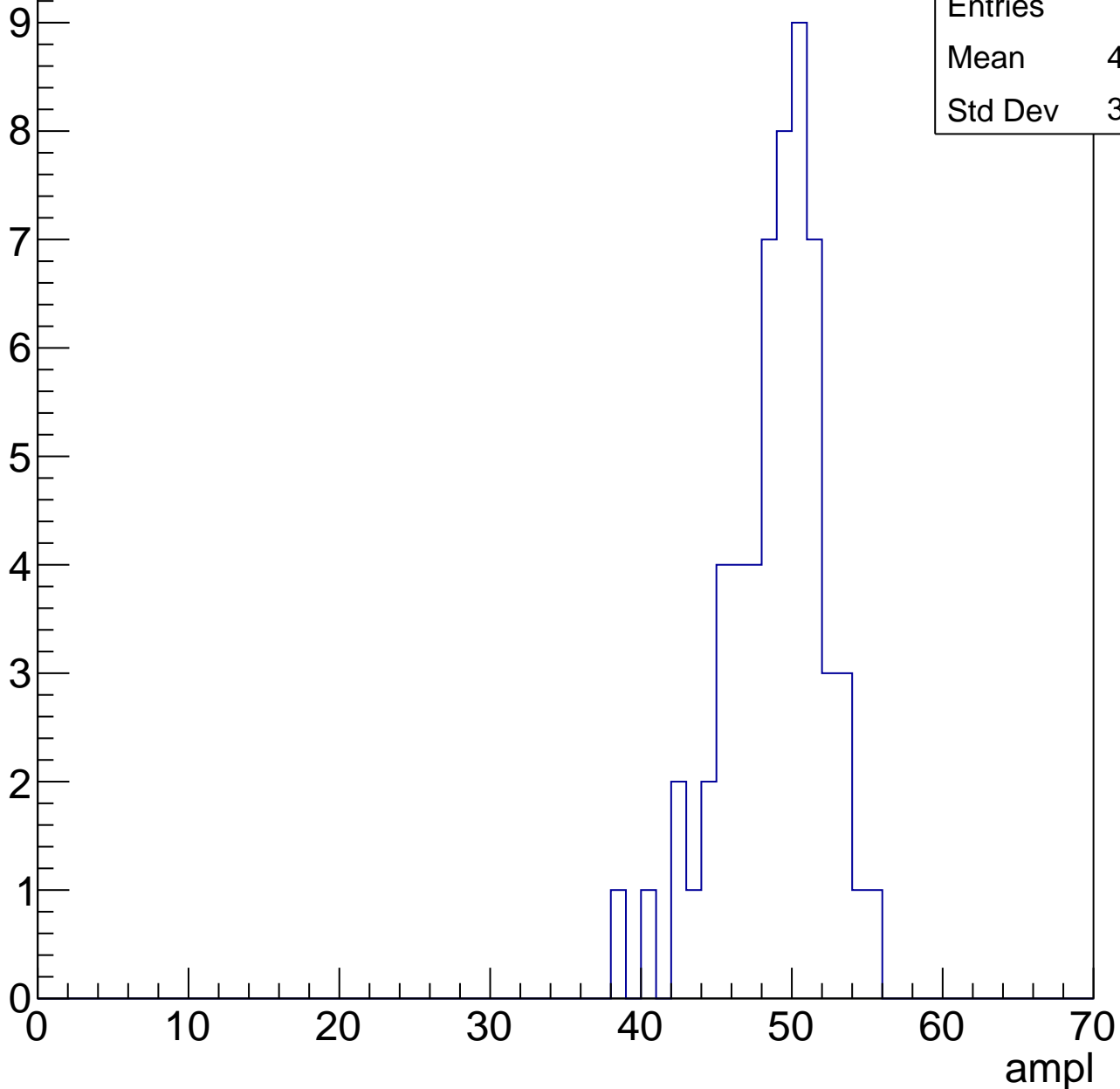


B1L102S, U14-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.34
Std Dev	3.366

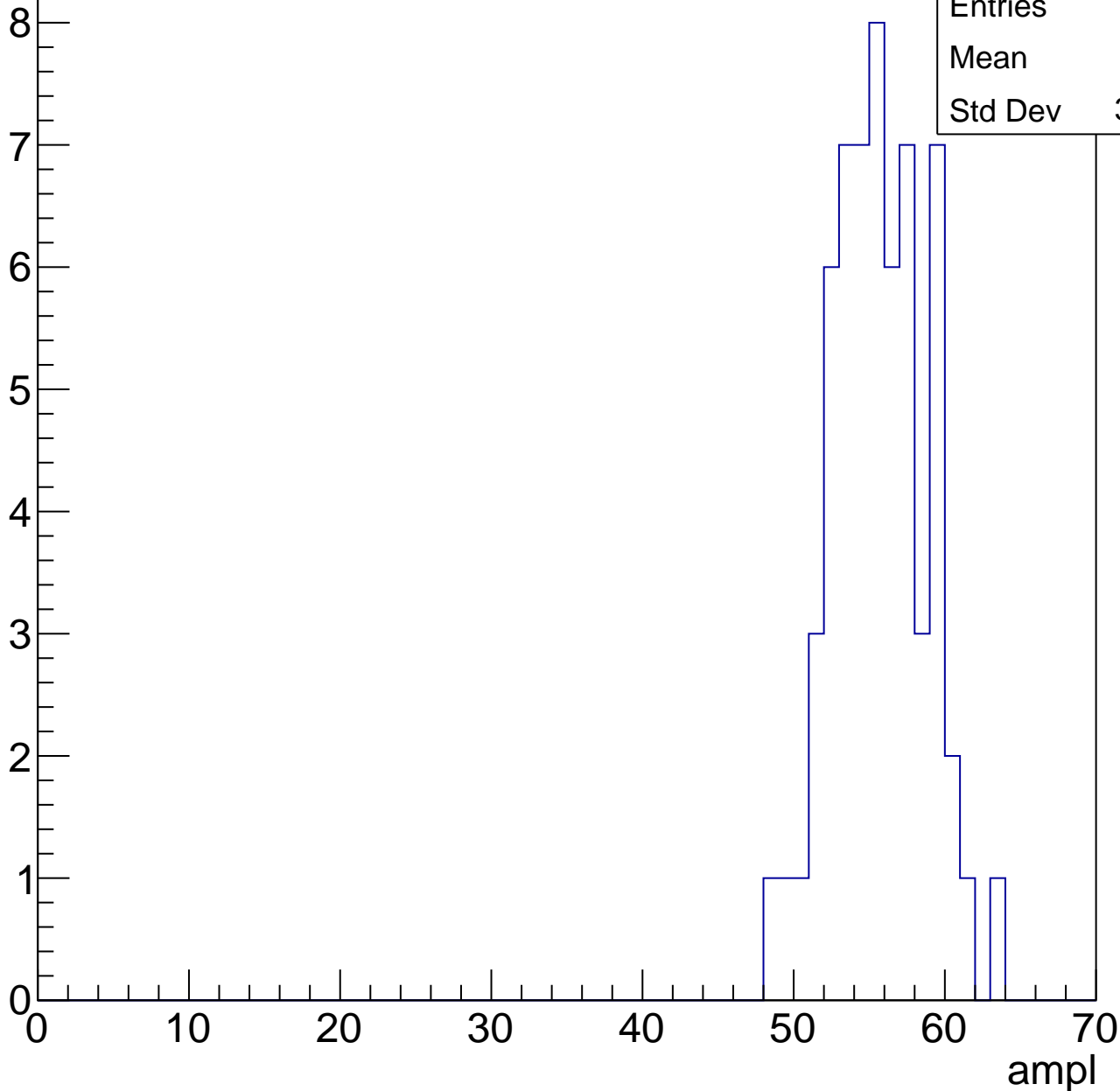


B1L102S, U14-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.2
Std Dev	3.051

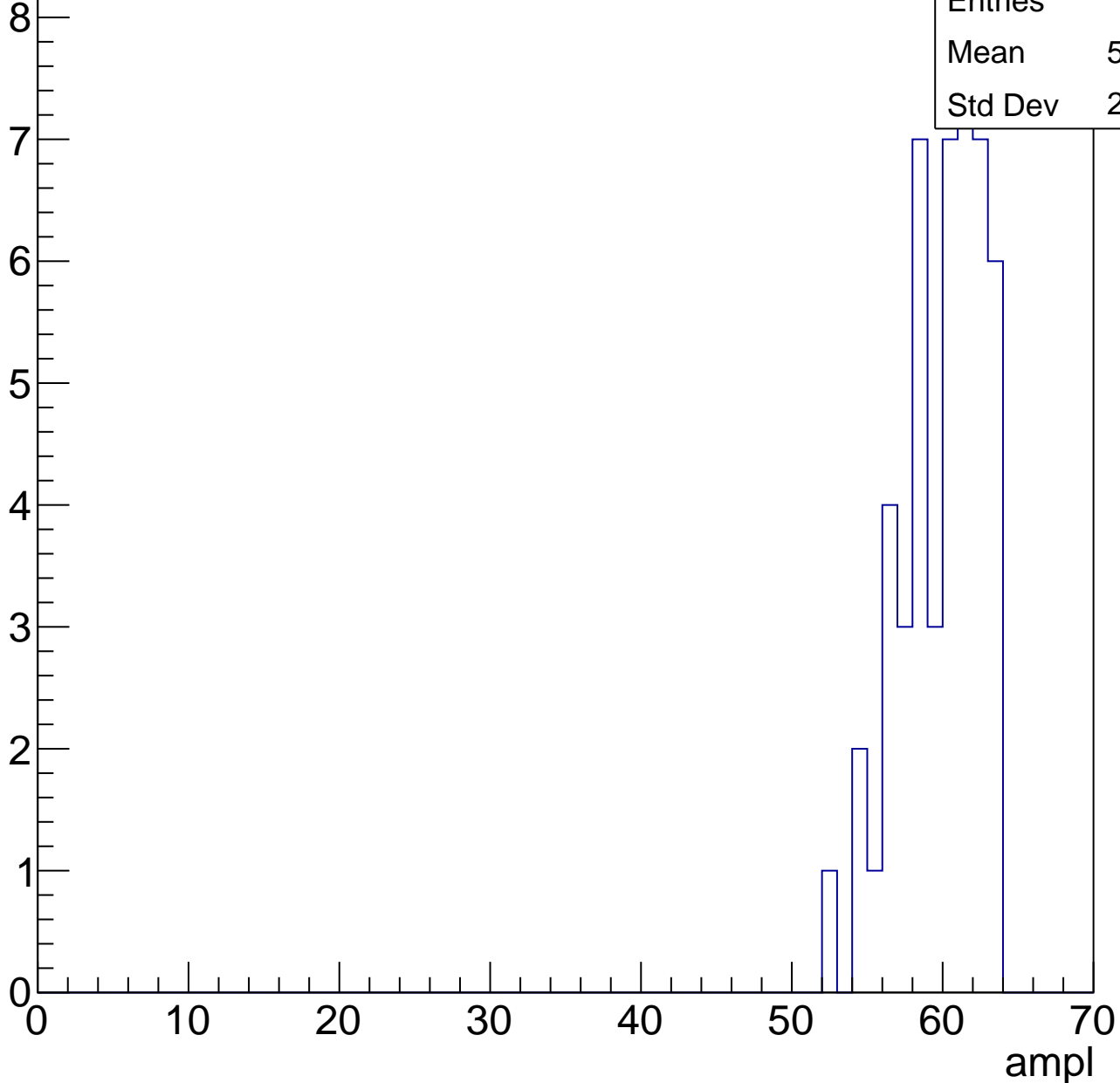


B1L102S, U14-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

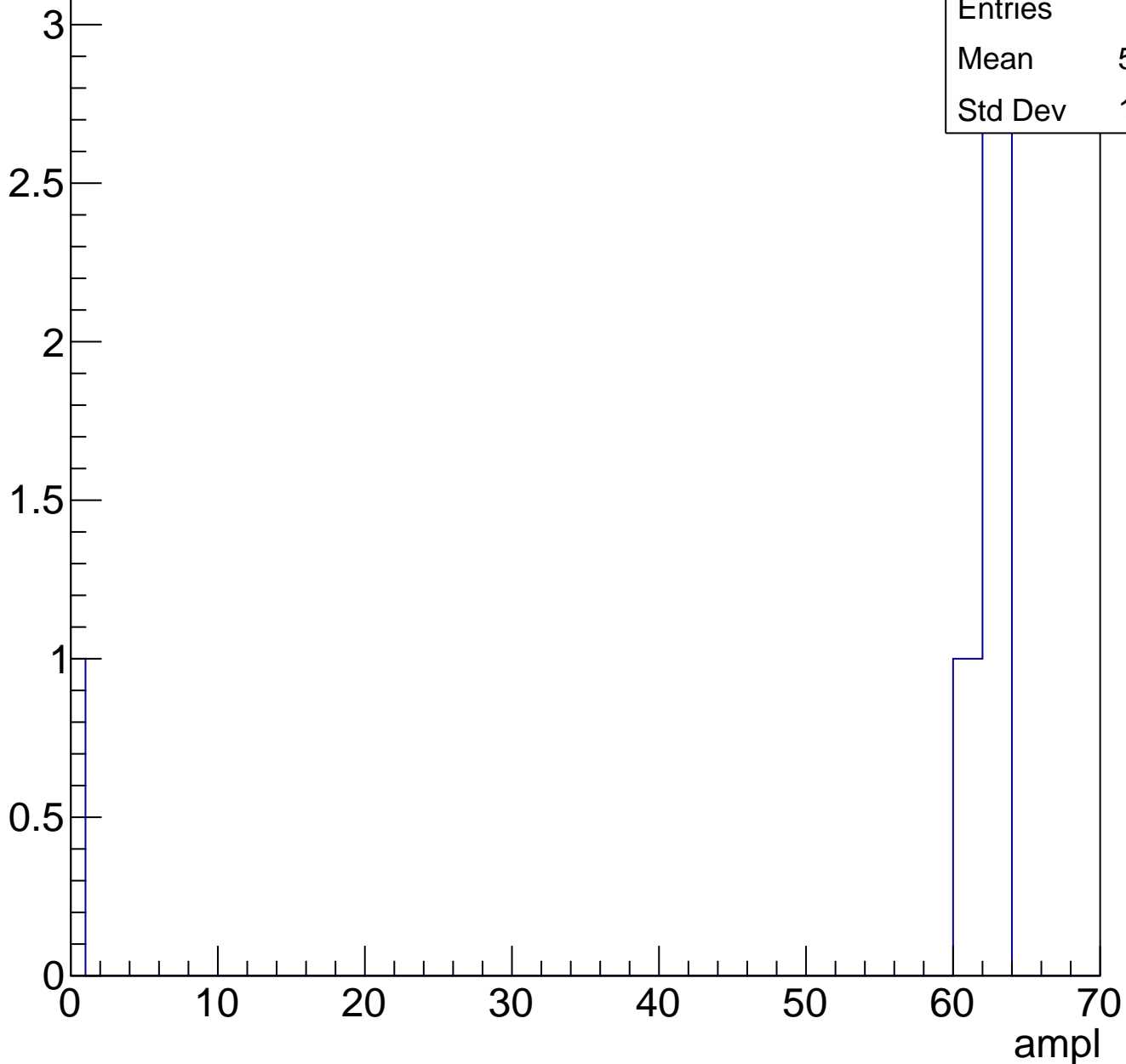
Entries	49
Mean	59.45
Std Dev	2.703



B1L102S, U14-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch122, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	27.07
Std Dev	5.6

Gaus mean : 27.6223

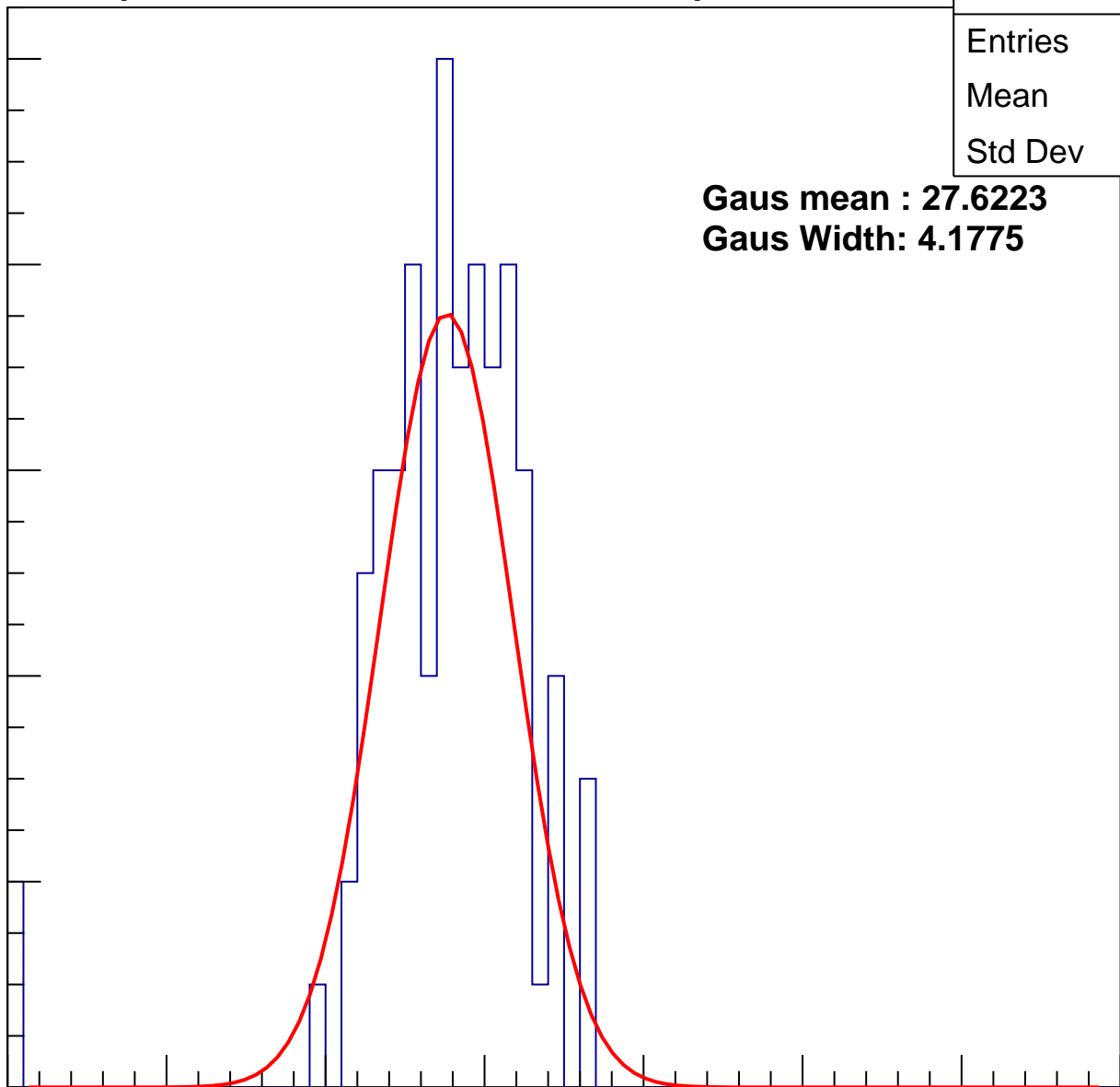
Gaus Width: 4.1775

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch122, adc1

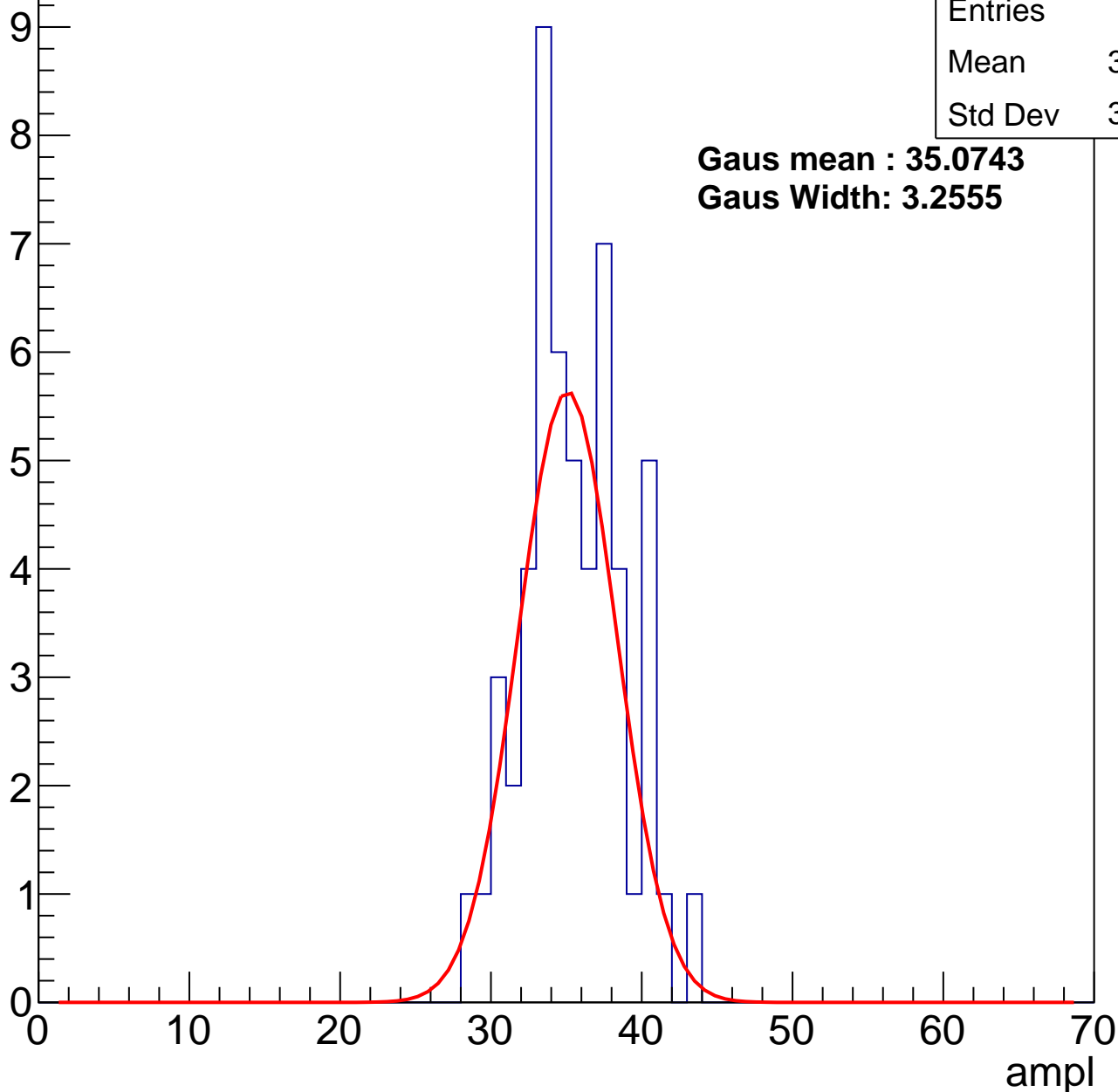
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.02
Std Dev	3.274

Gaus mean : 35.0743

Gaus Width: 3.2555



B1L102S, U14-ch122, adc2

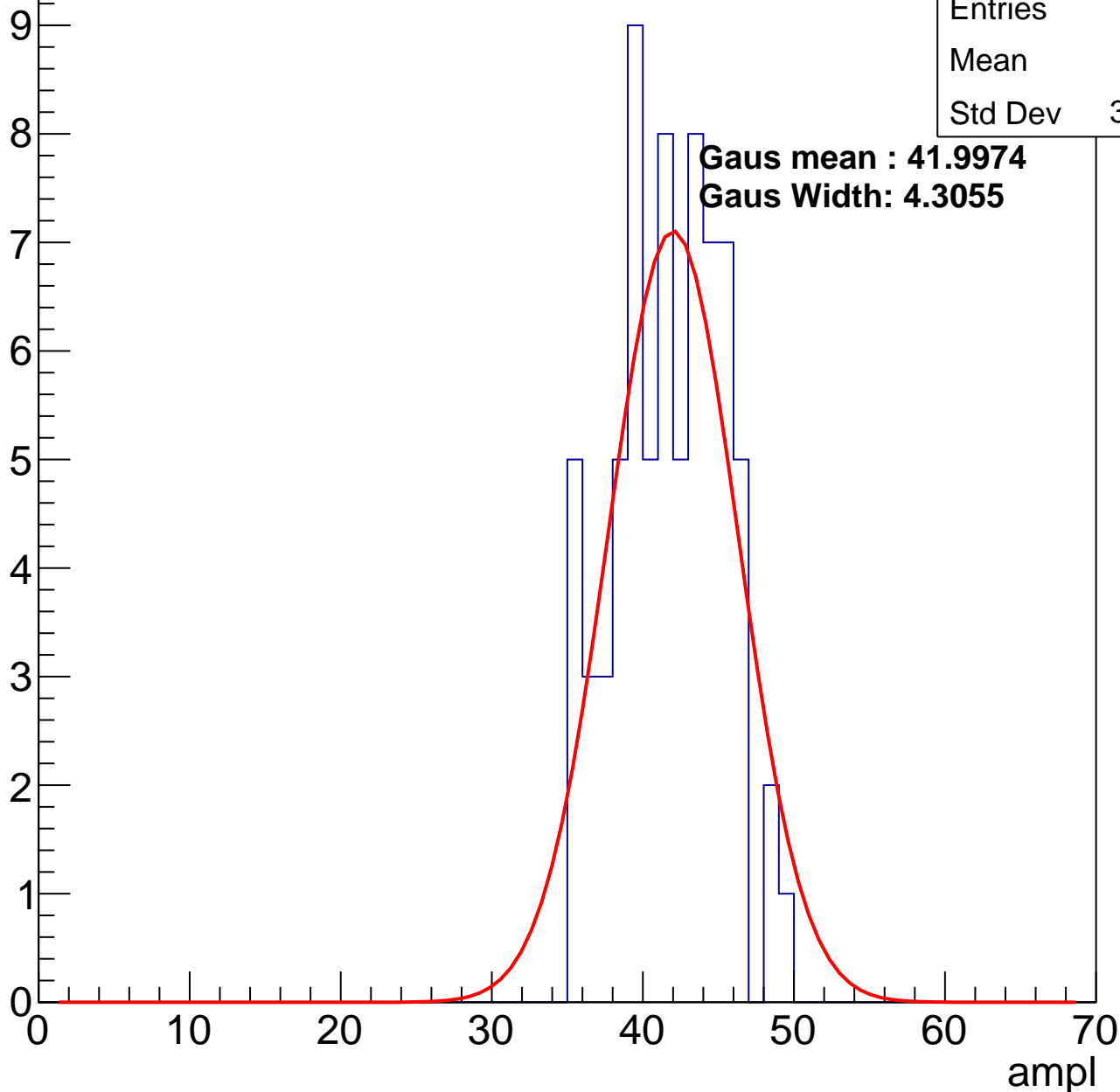
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.3
Std Dev	3.475

Gaus mean : 41.9974

Gaus Width: 4.3055

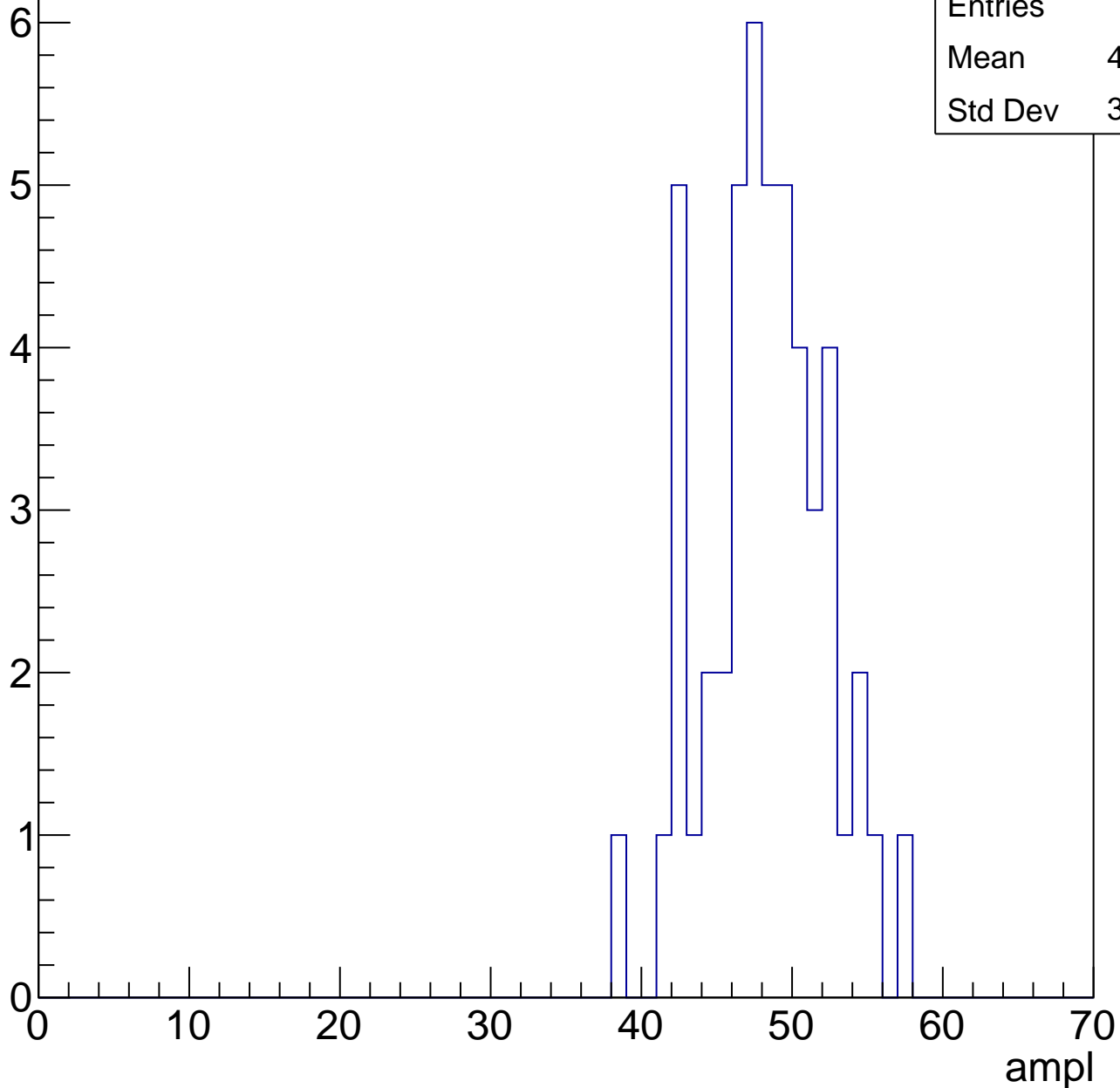


B1L102S, U14-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	47.78
Std Dev	3.955

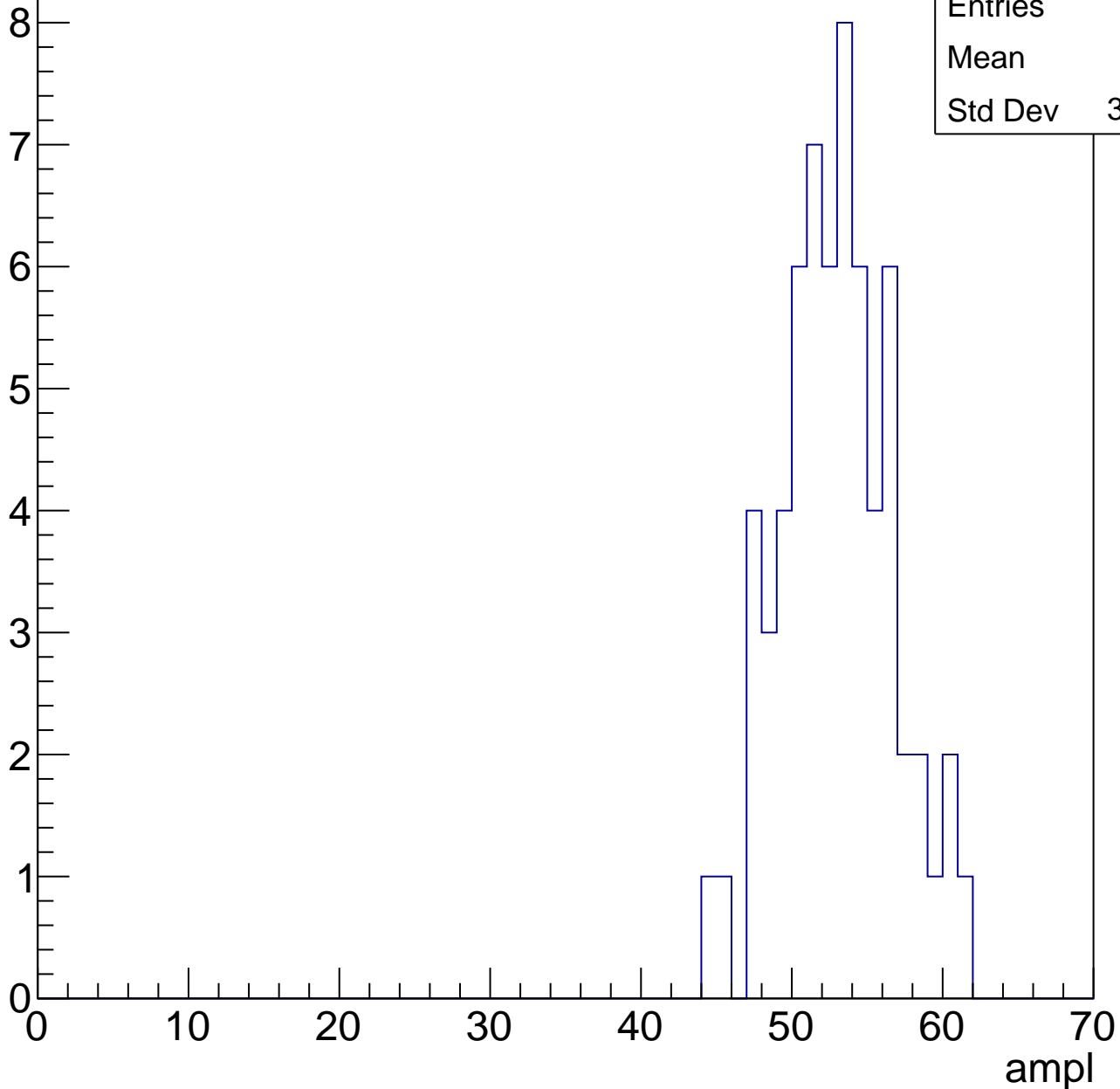


B1L102S, U14-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	52.5
Std Dev	3.657

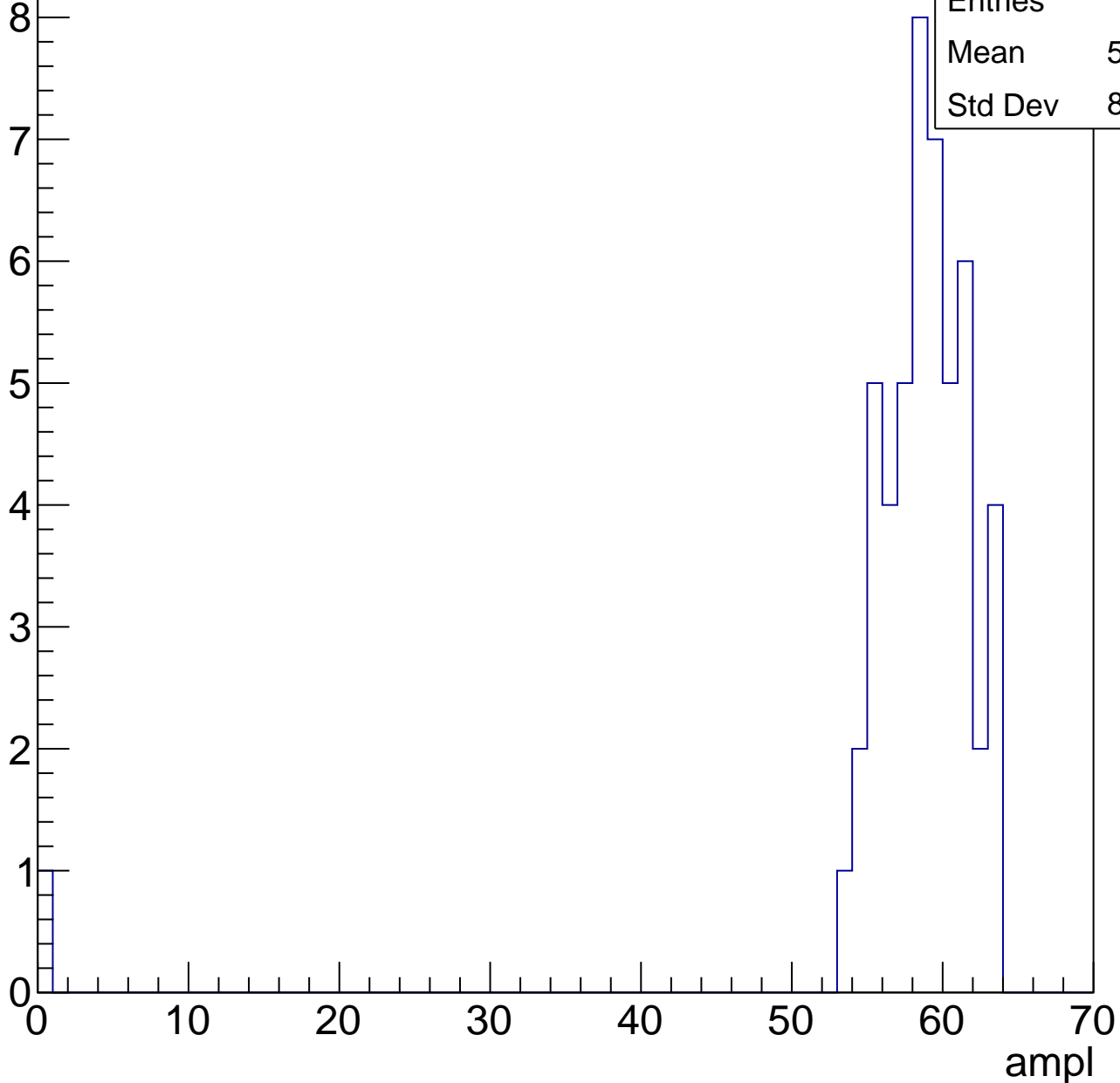


B1L102S, U14-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.28
Std Dev	8.572

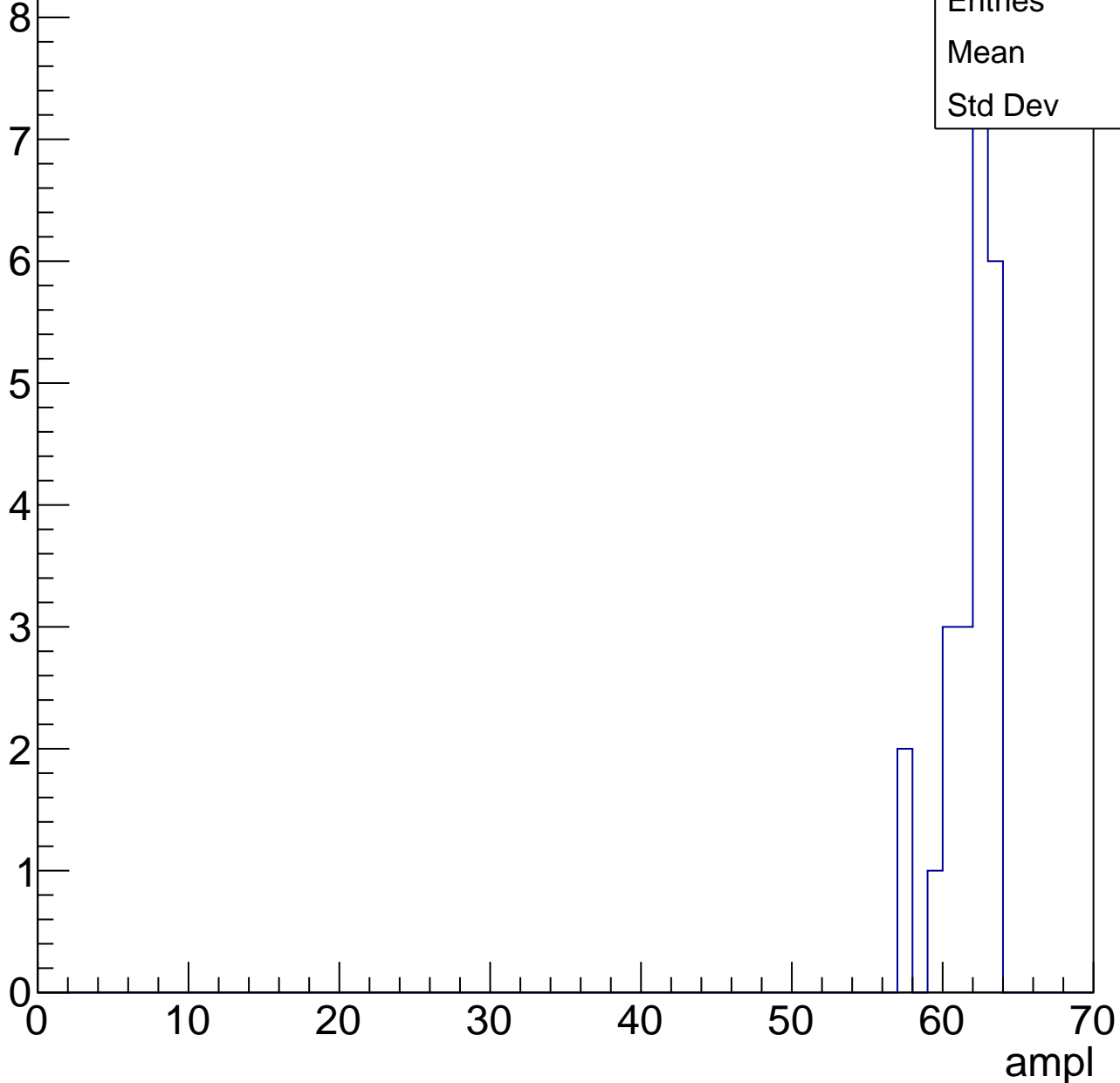


B1L102S, U14-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

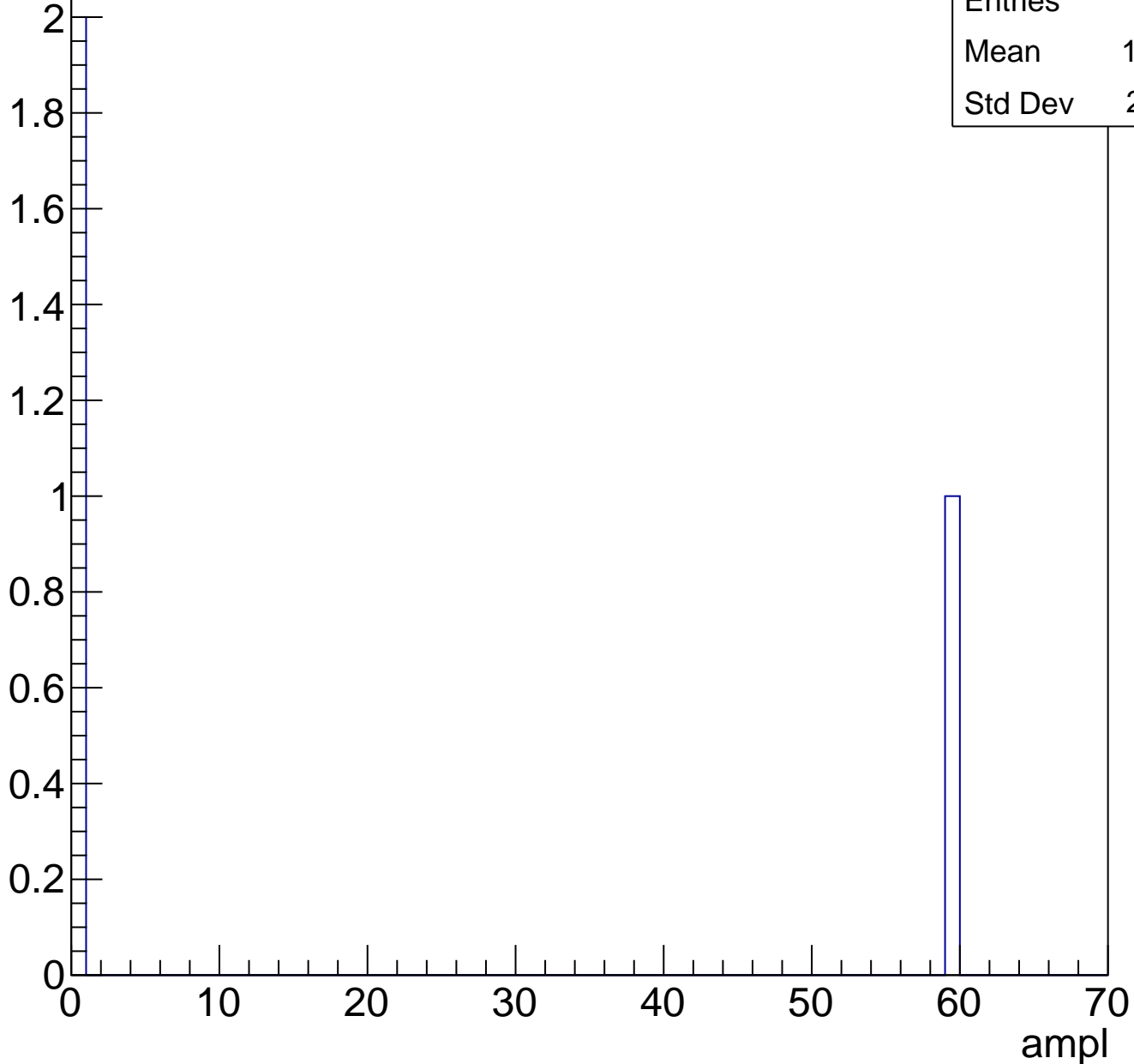
Entries	23
Mean	61.3
Std Dev	1.73



B1L102S, U14-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch123, adc0

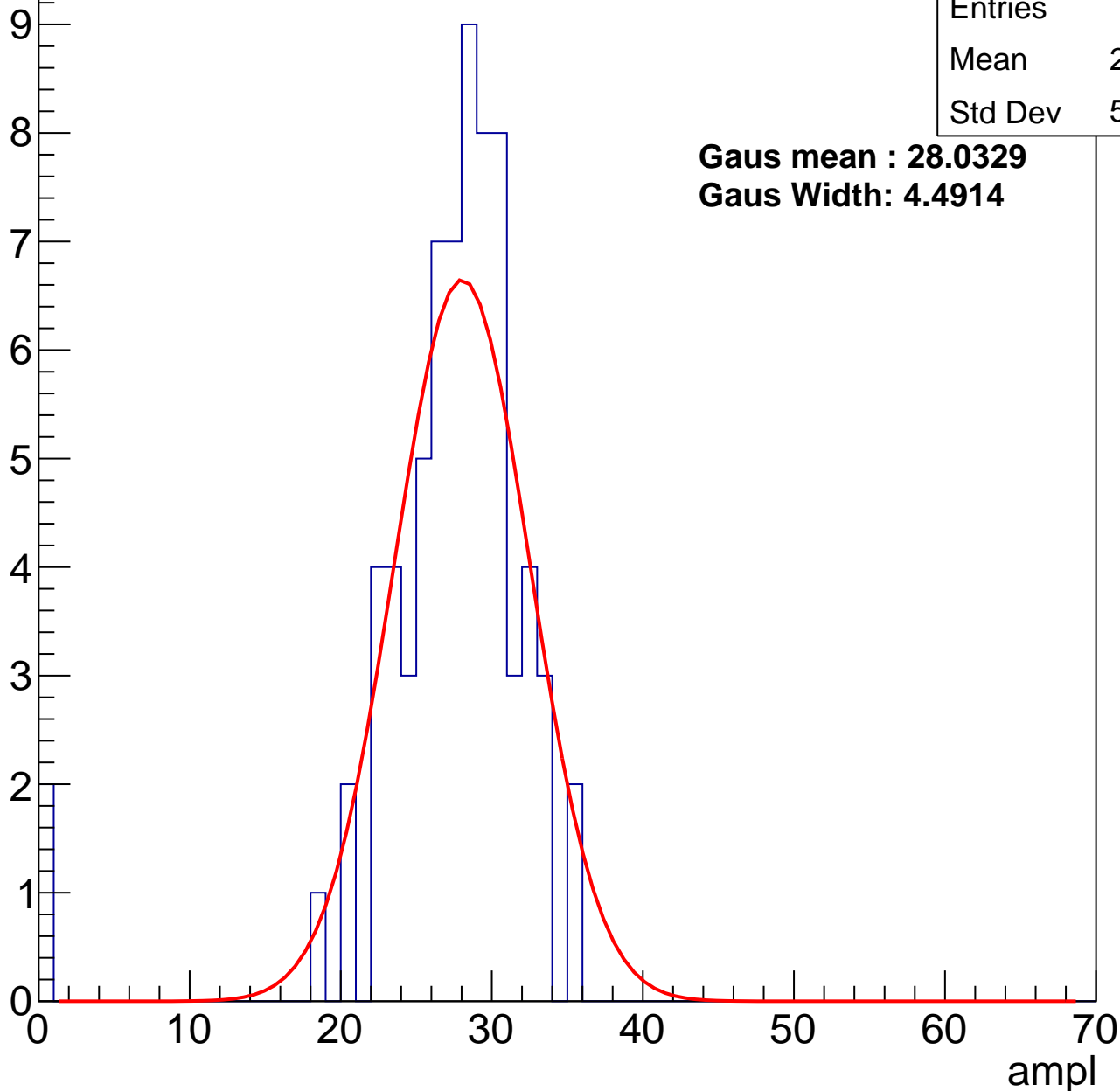
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	26.67
Std Dev	5.706

Gaus mean : 28.0329

Gaus Width: 4.4914



B1L102S, U14-ch123, adc1

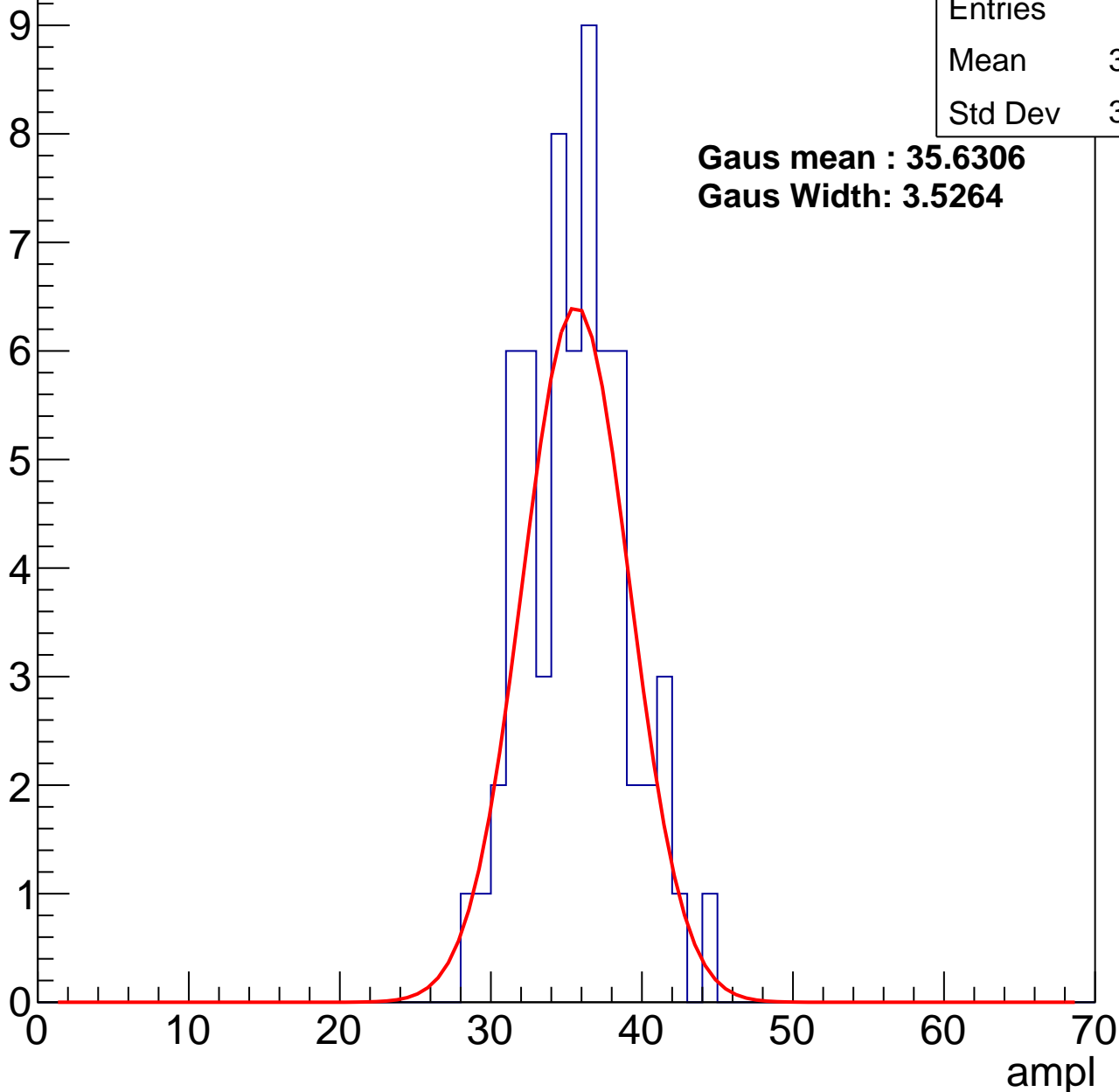
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.19
Std Dev	3.356

Gaus mean : 35.6306

Gaus Width: 3.5264



B1L102S, U14-ch123, adc2

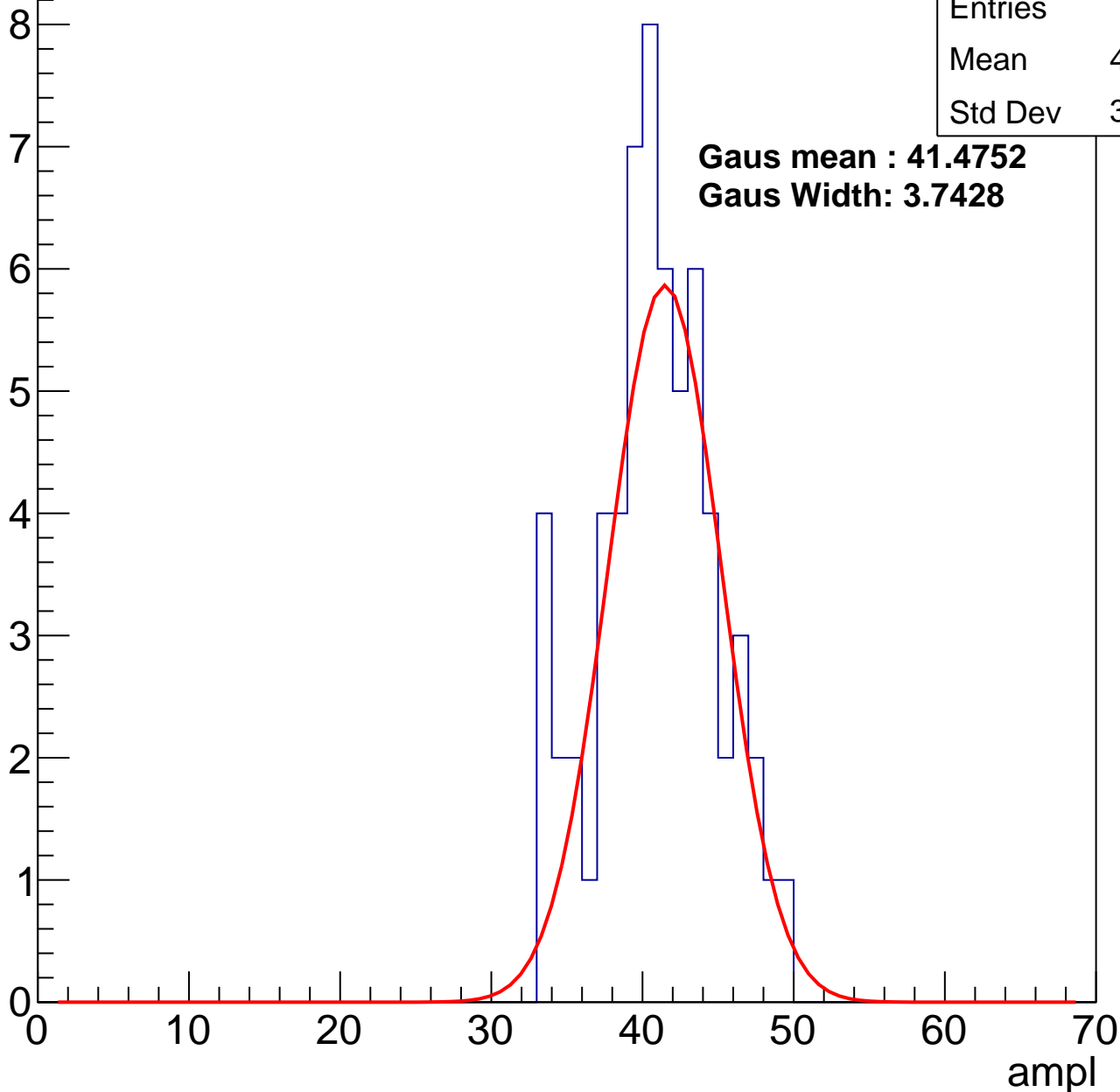
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	40.45
Std Dev	3.863

Gaus mean : 41.4752

Gaus Width: 3.7428

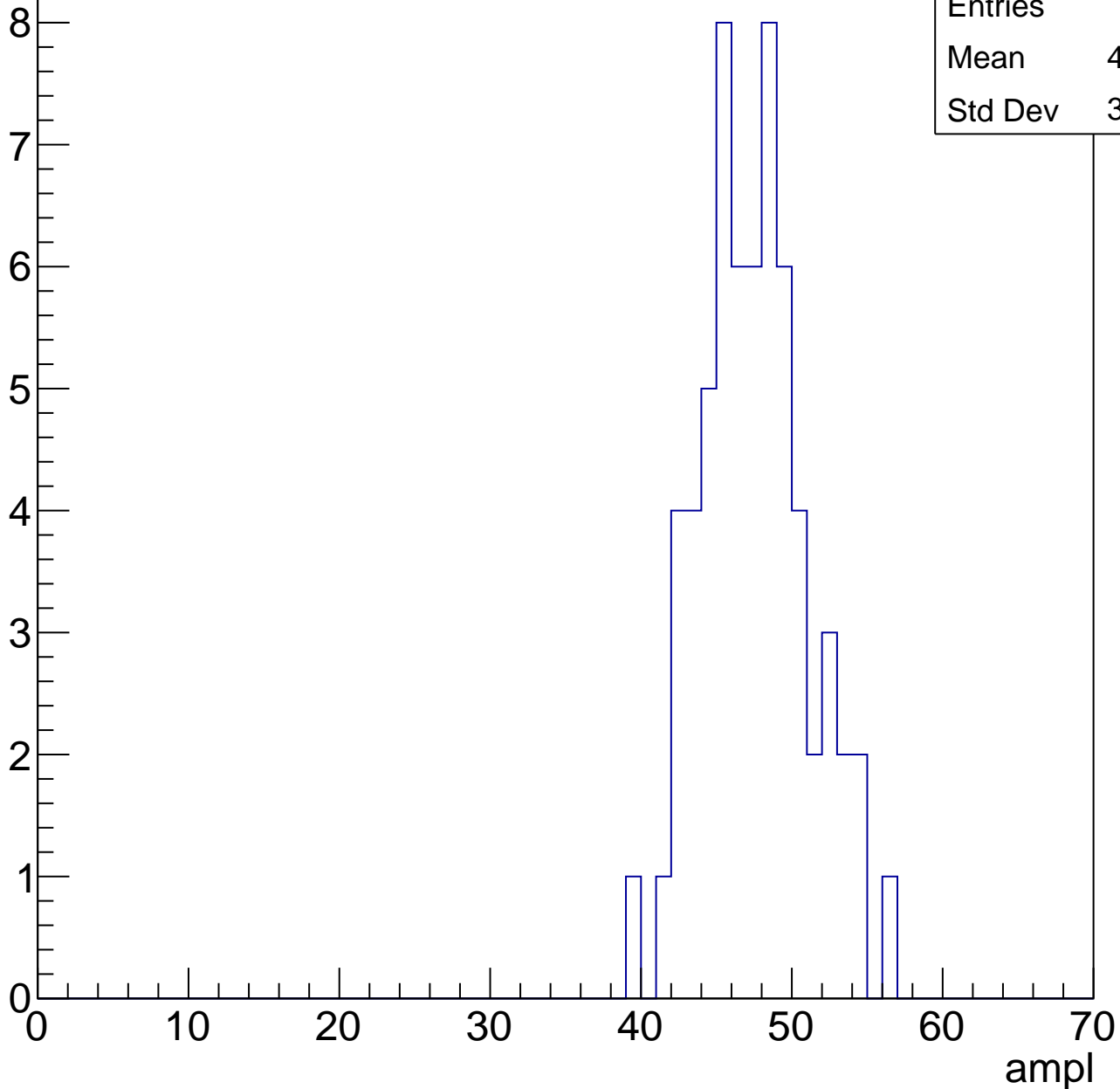


B1L102S, U14-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.05
Std Dev	3.503

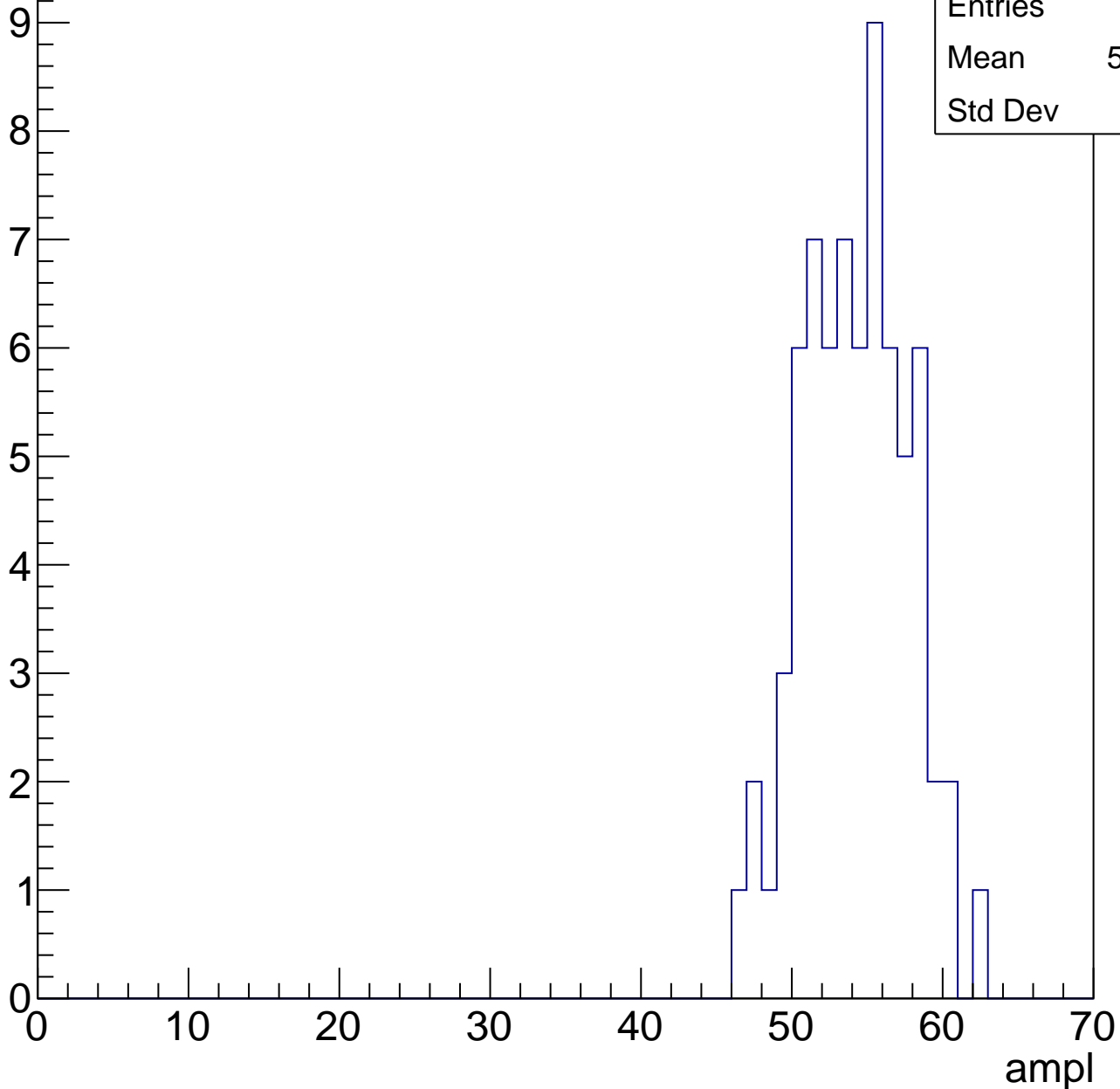


B1L102S, U14-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	53.76
Std Dev	3.42

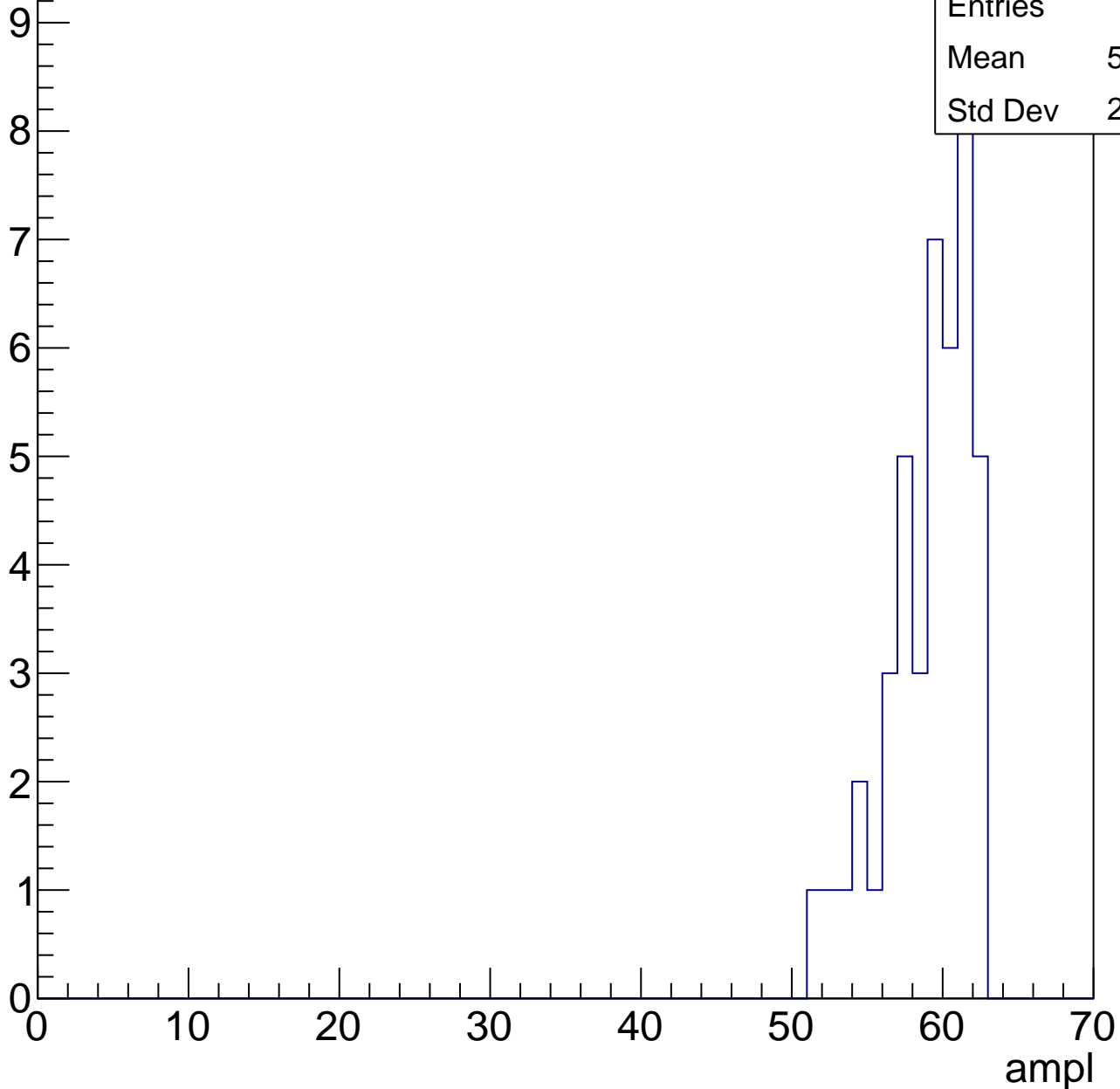


B1L102S, U14-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.59
Std Dev	2.799



B1L102S, U14-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	20
Mean	58.8
Std Dev	13.57

Entry

10

8

6

4

2

0

0

10

20

30

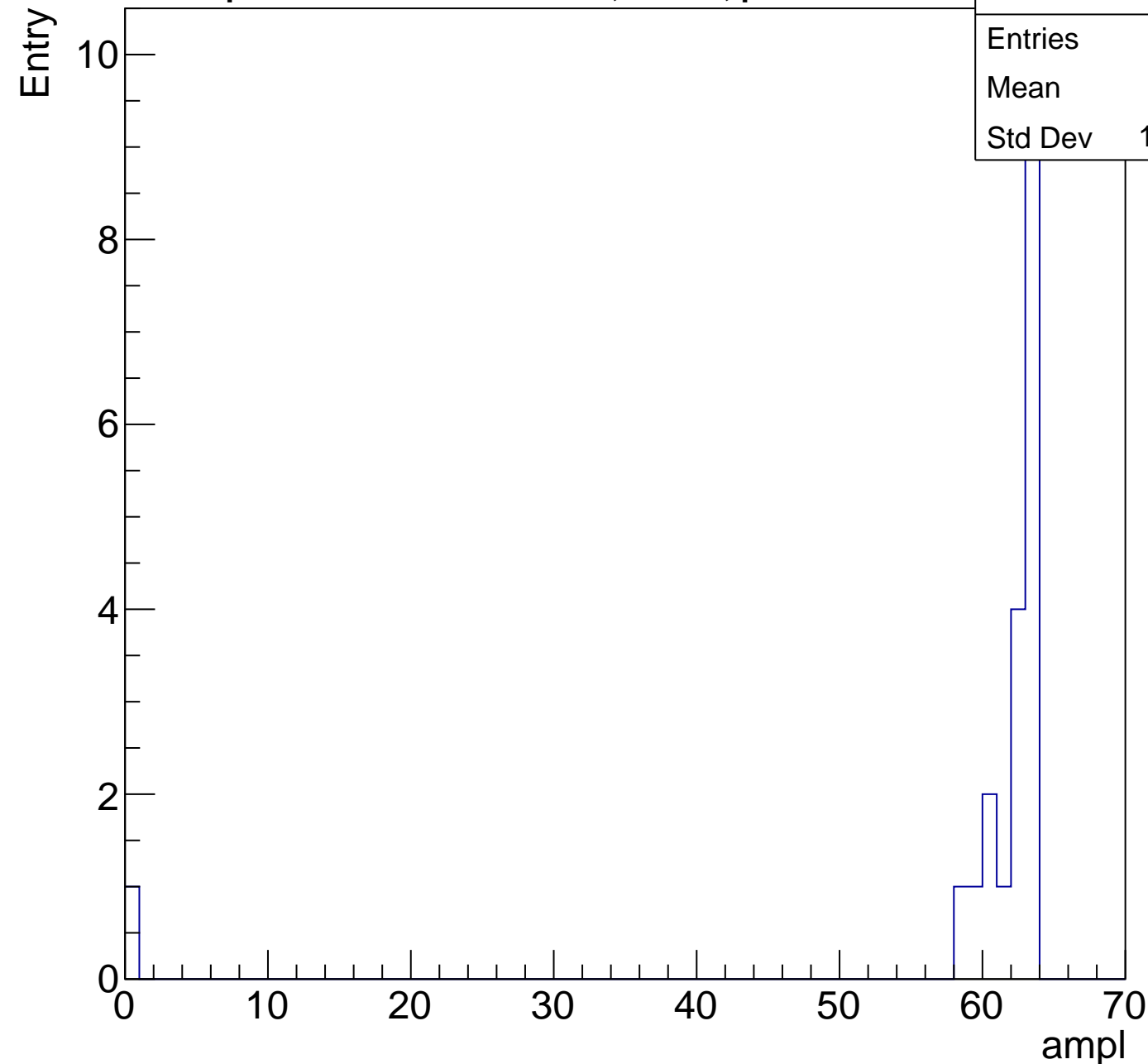
40

50

60

ampl

70



B1L102S, U14-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch124, adc0

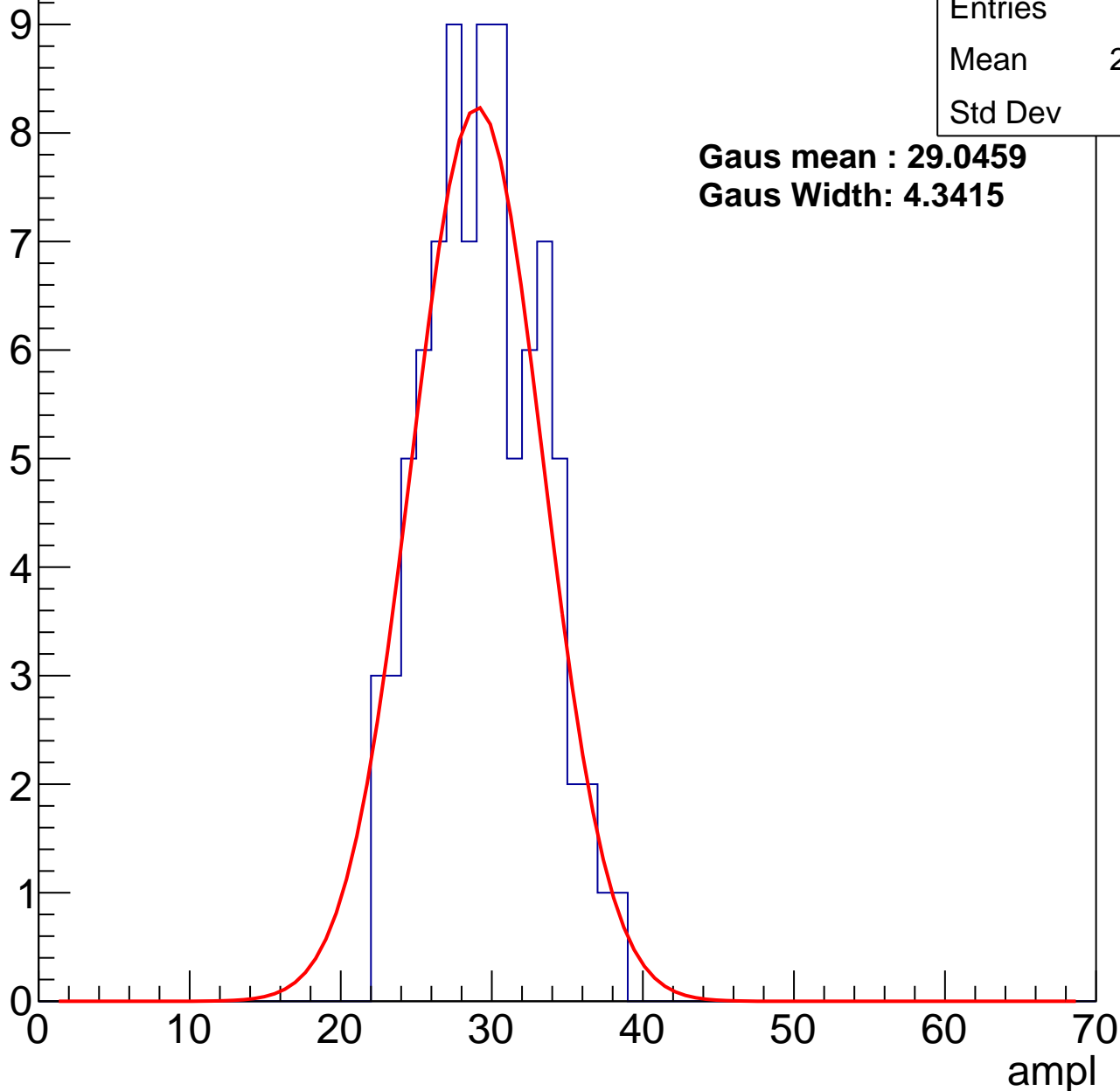
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	28.99
Std Dev	3.74

Gaus mean : 29.0459

Gaus Width: 4.3415



B1L102S, U14-ch124, adc1

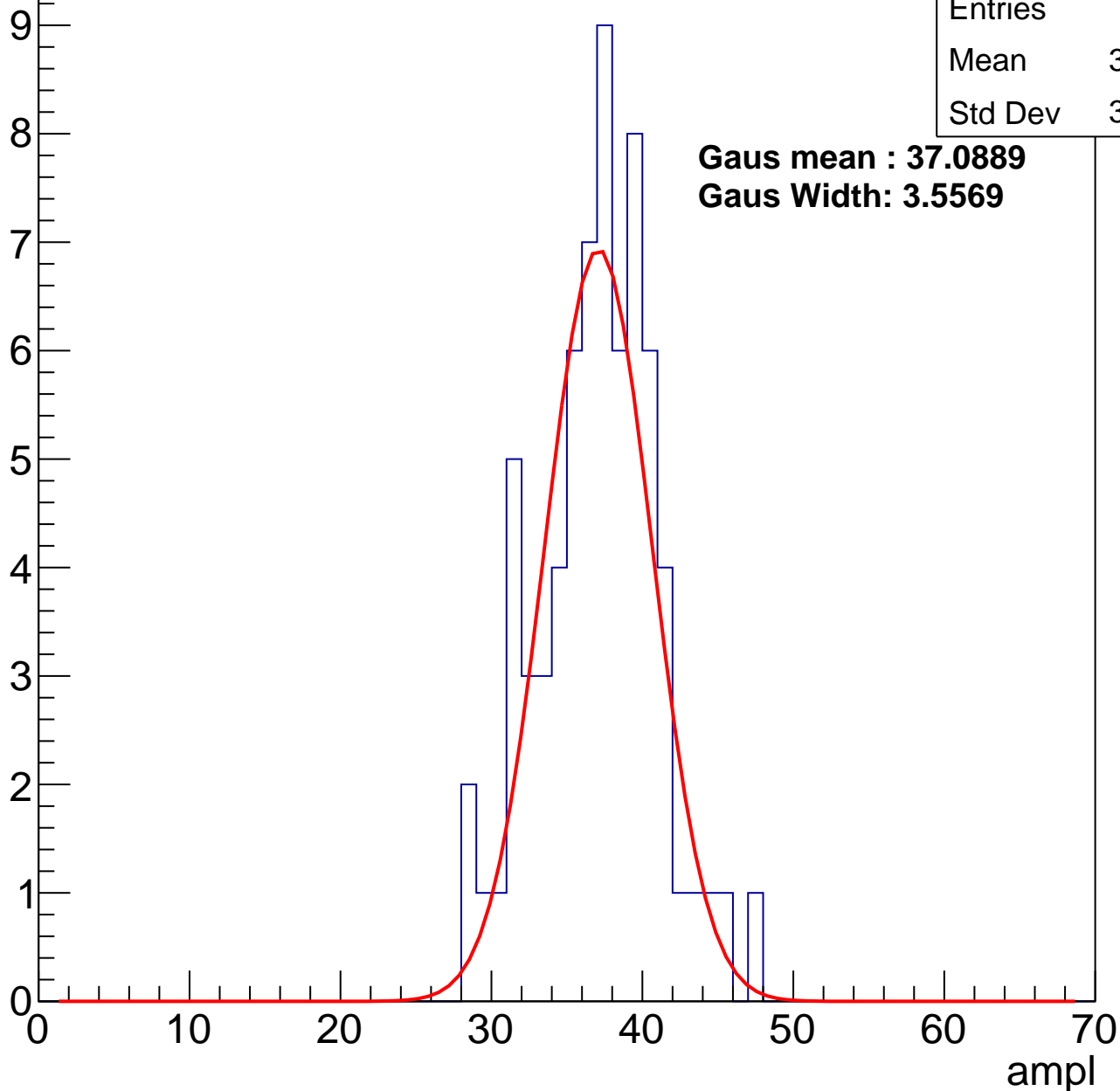
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.59
Std Dev	3.897

Gaus mean : 37.0889

Gaus Width: 3.5569



B1L102S, U14-ch124, adc2

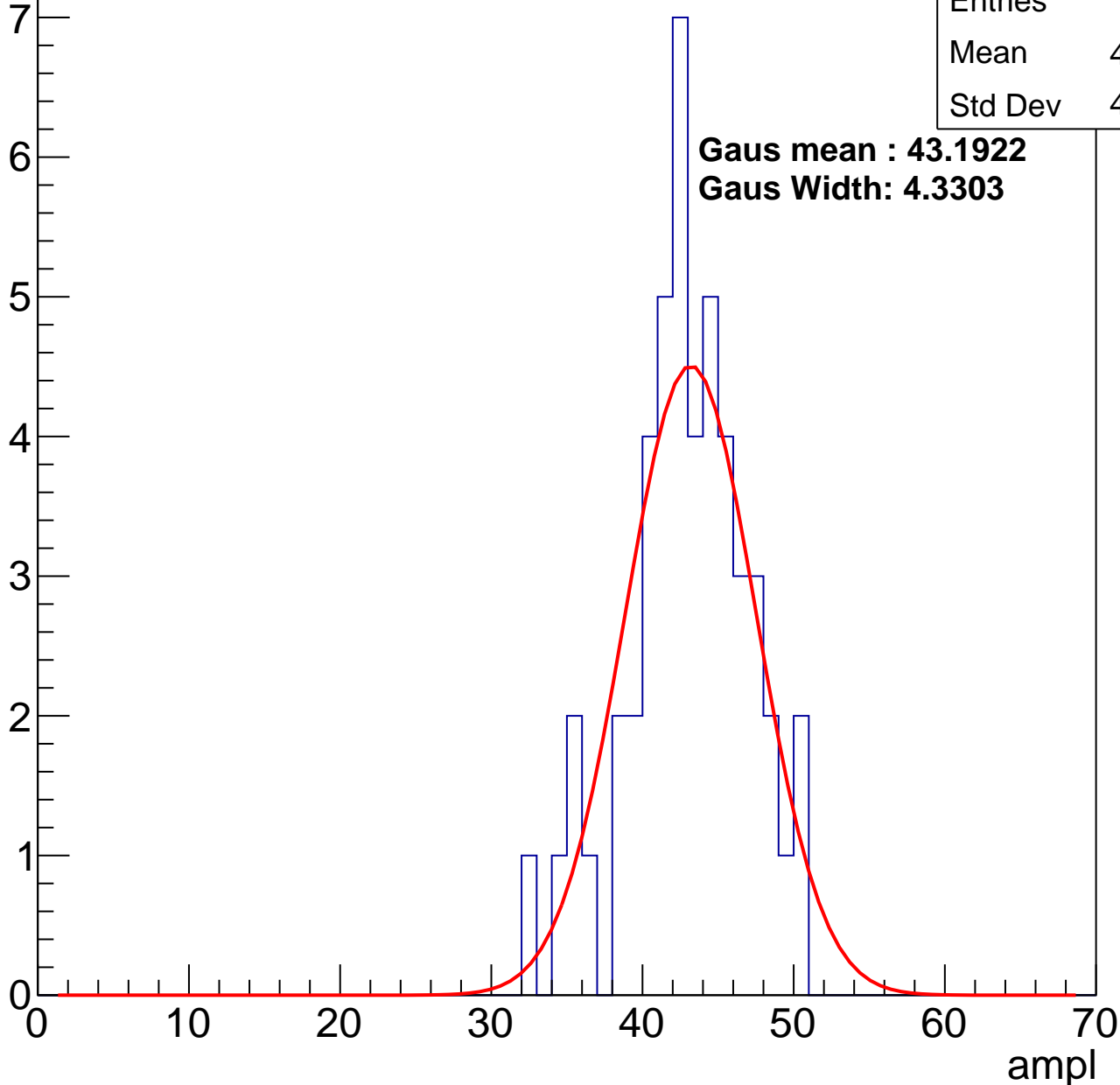
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.47
Std Dev	4.016

Gaus mean : 43.1922

Gaus Width: 4.3303

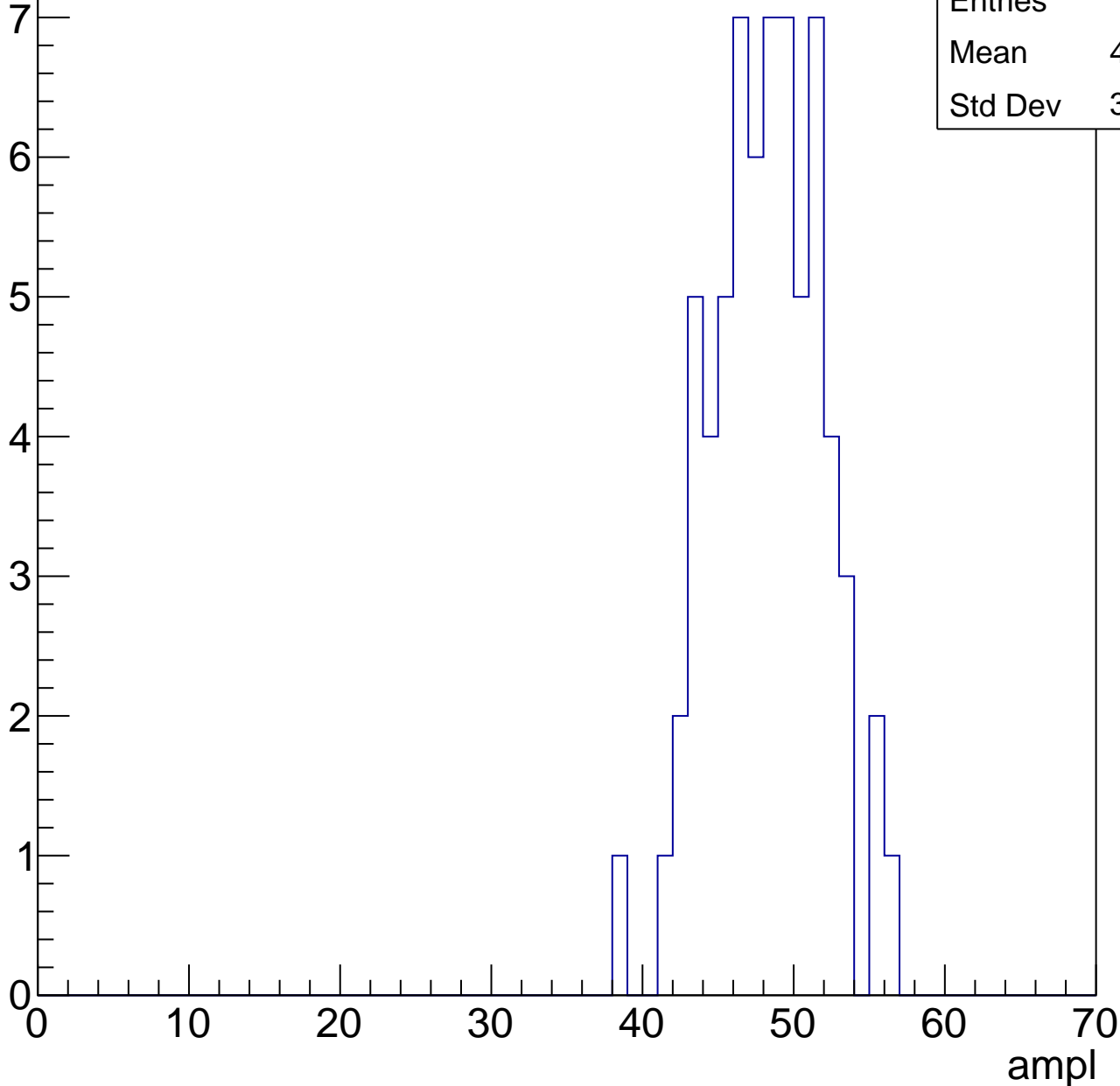


B1L102S, U14-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	47.79
Std Dev	3.618

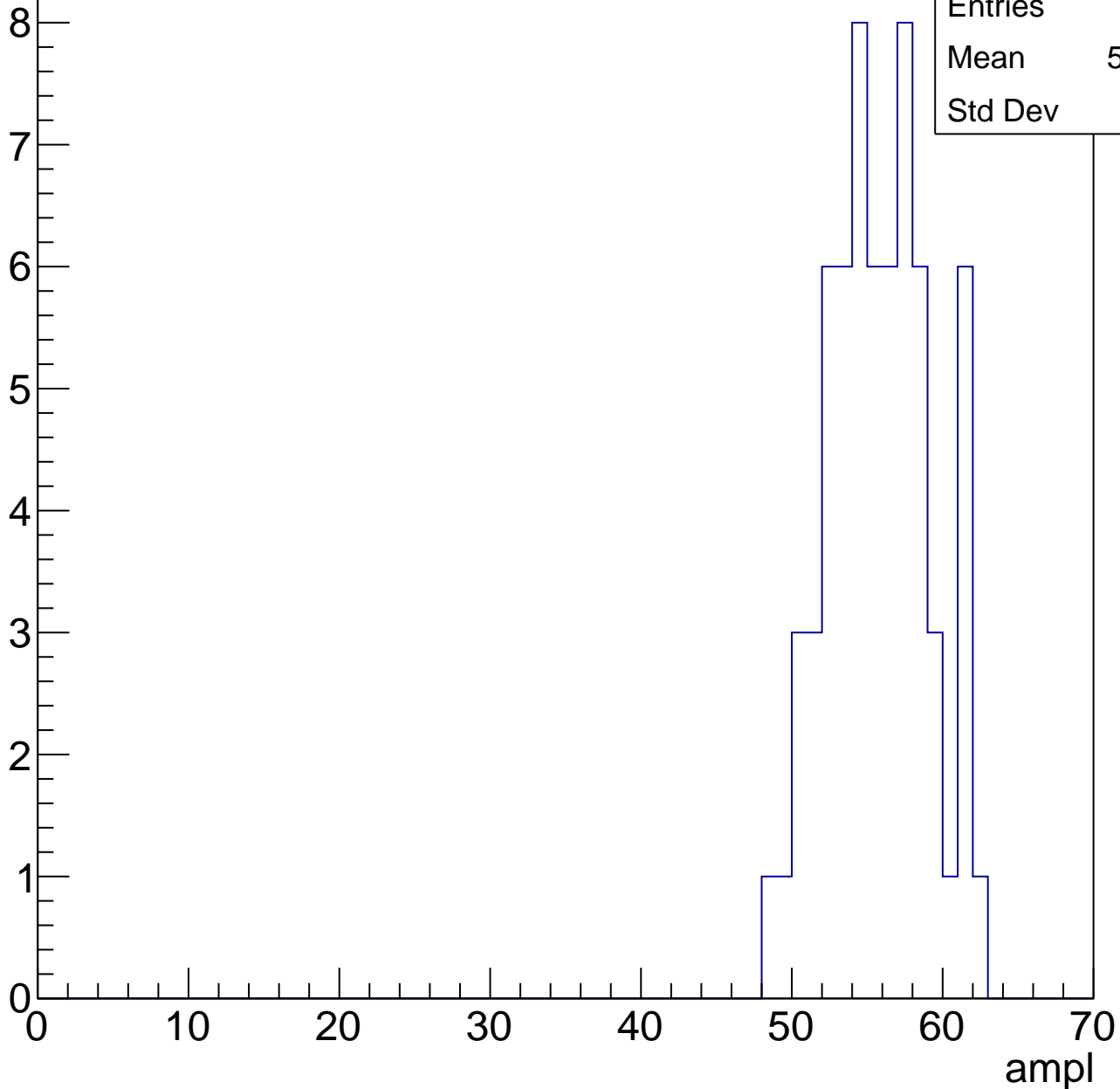


B1L102S, U14-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.34
Std Dev	3.32

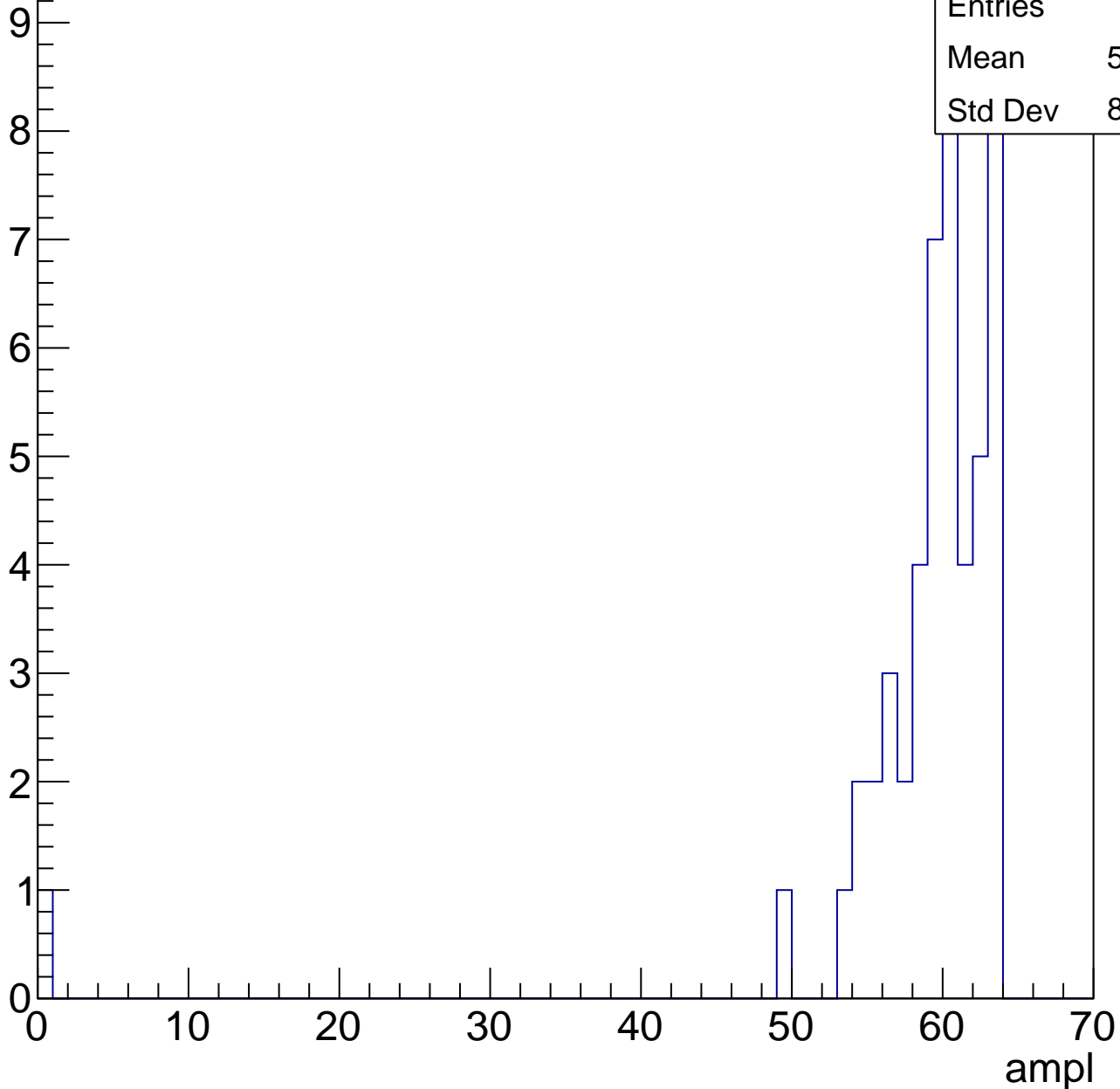


B1L102S, U14-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

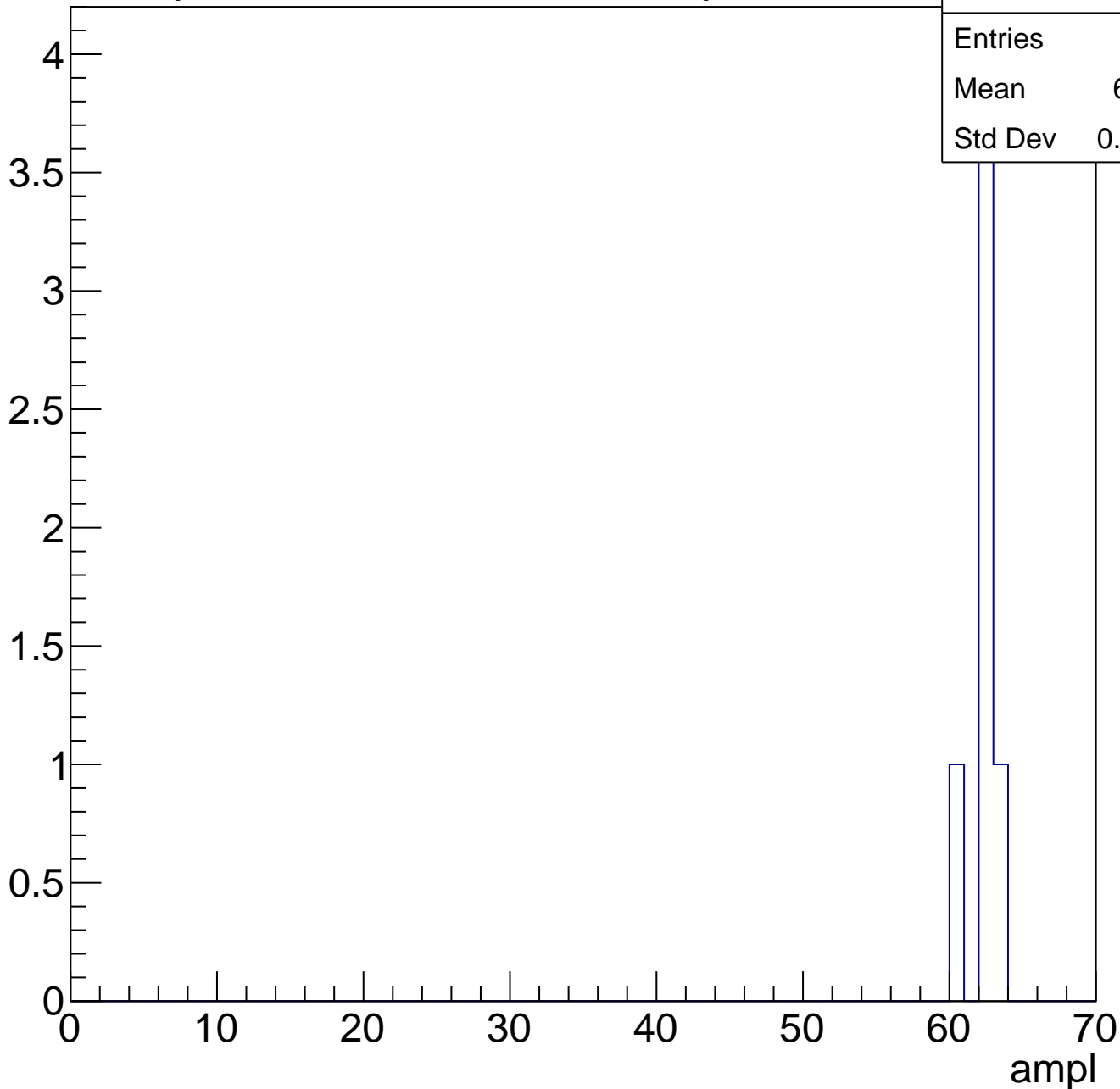
Entries	49
Mean	58.12
Std Dev	8.937



B1L102S, U14-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

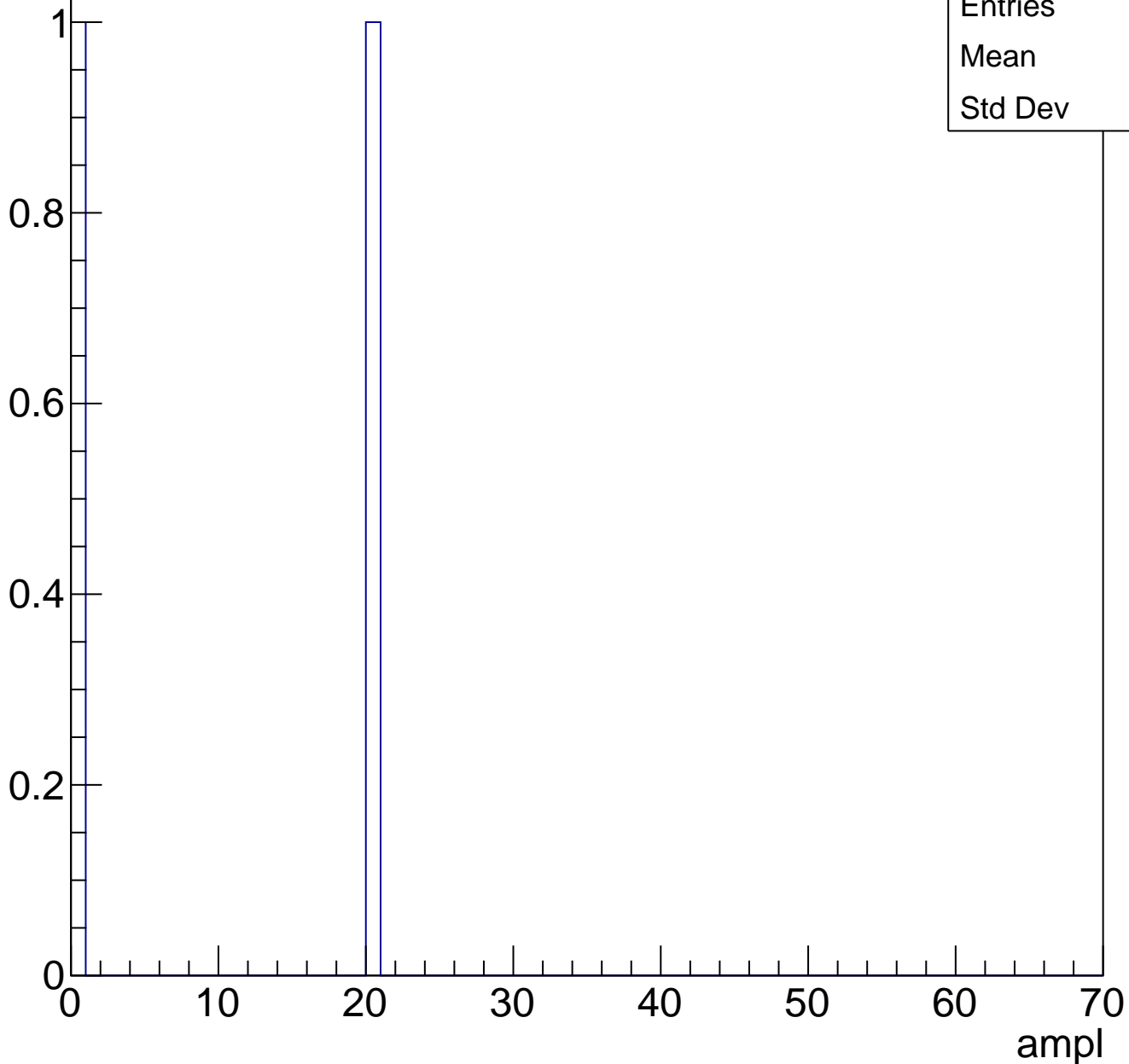
Entry



B1L102S, U14-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch125, adc0

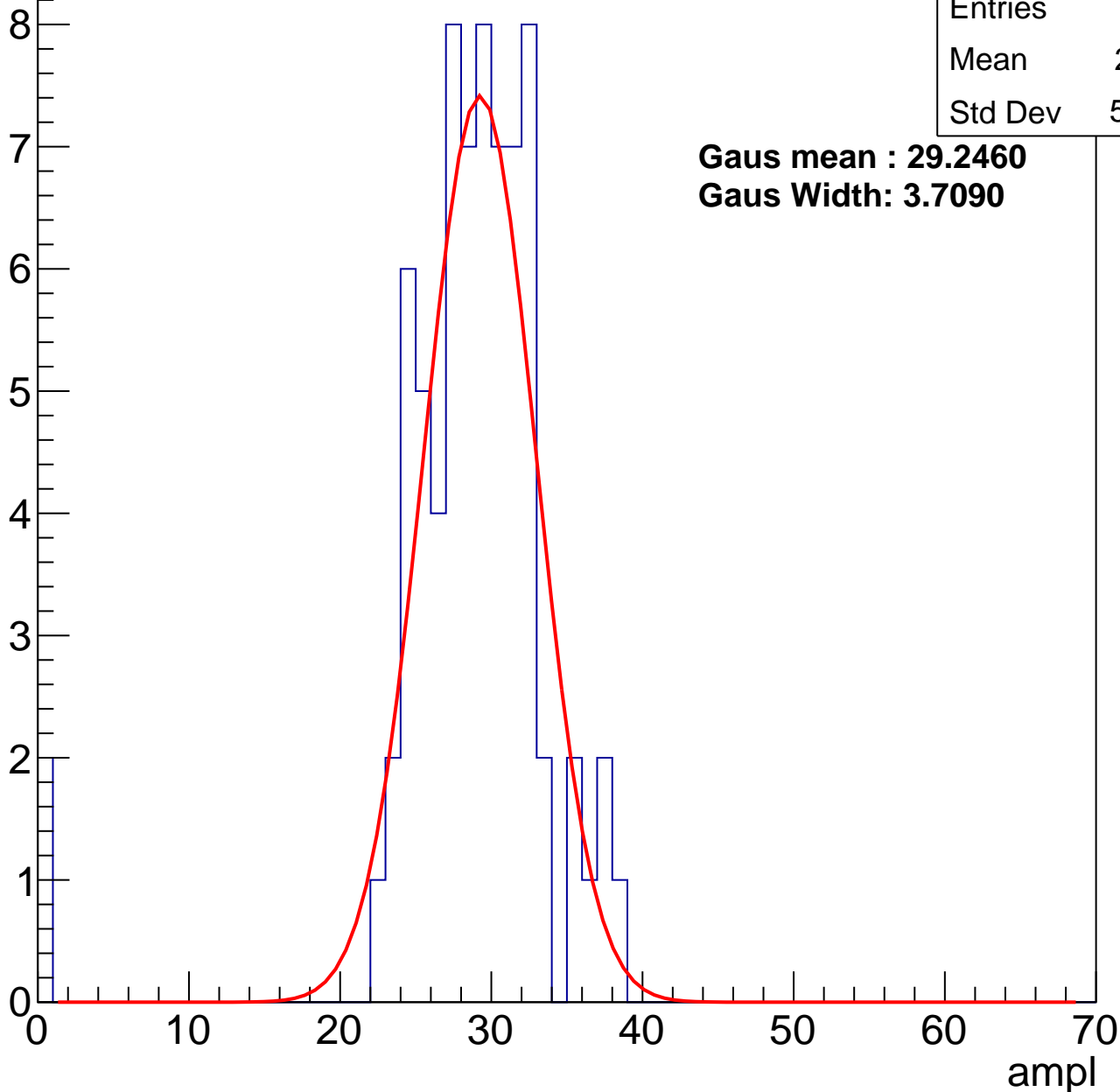
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.11
Std Dev	5.874

Gaus mean : 29.2460

Gaus Width: 3.7090



B1L102S, U14-ch125, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	34.91
Std Dev	4.067

Gaus mean : 36.2072

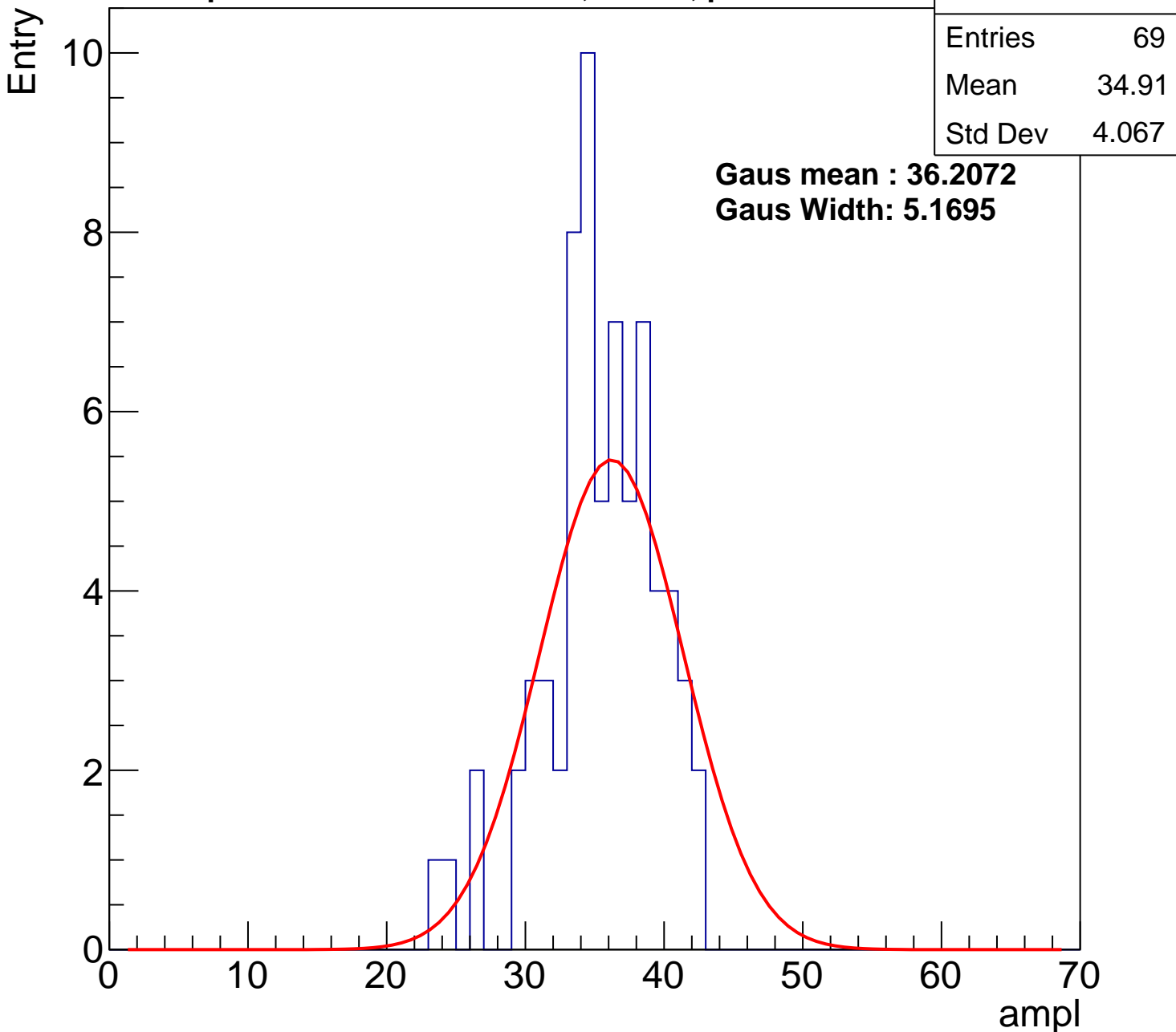
Gaus Width: 5.1695

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch125, adc2

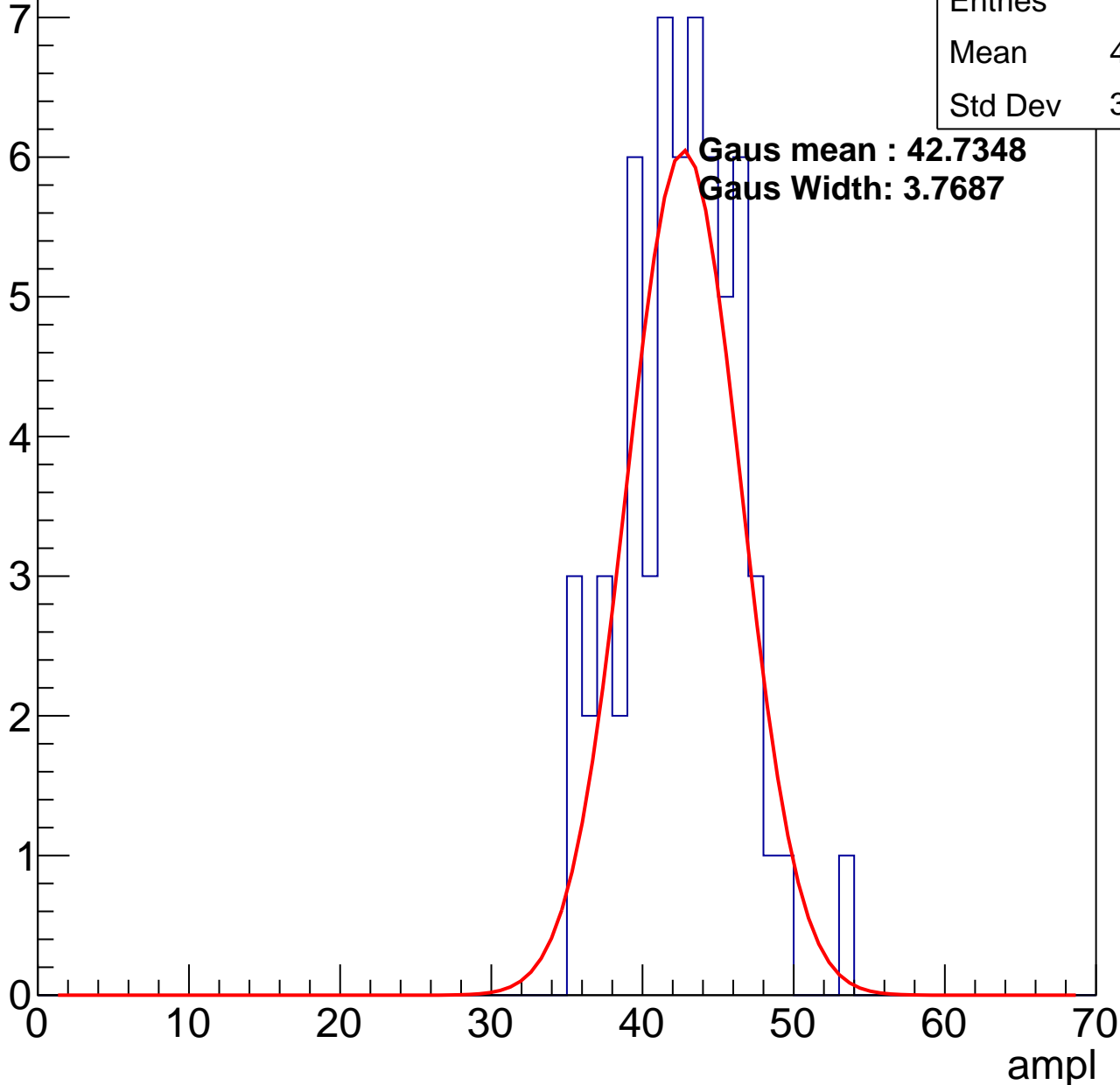
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.16
Std Dev	3.704

Gaus mean : 42.7348

Gaus Width: 3.7687

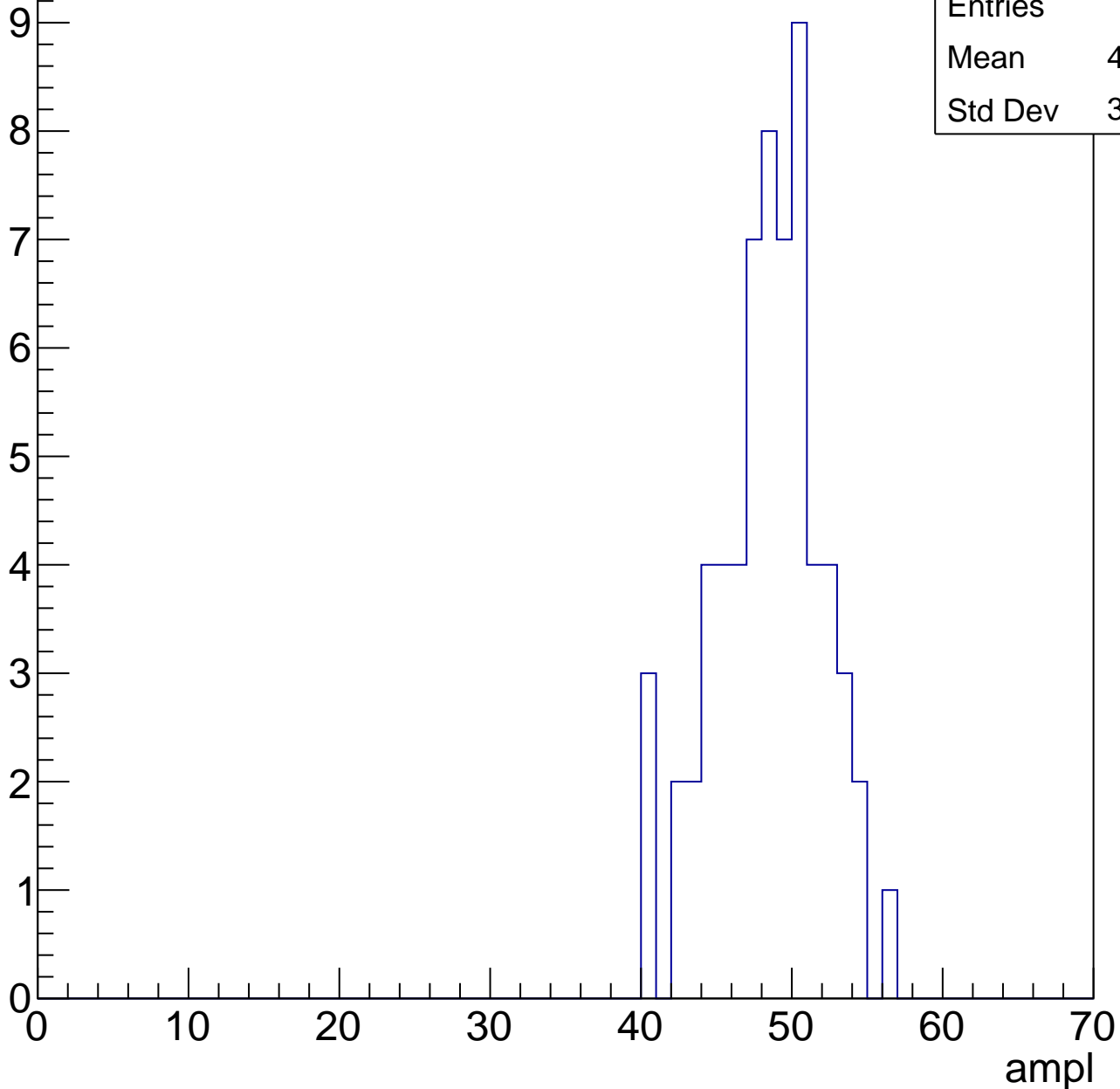


B1L102S, U14-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.98
Std Dev	3.516

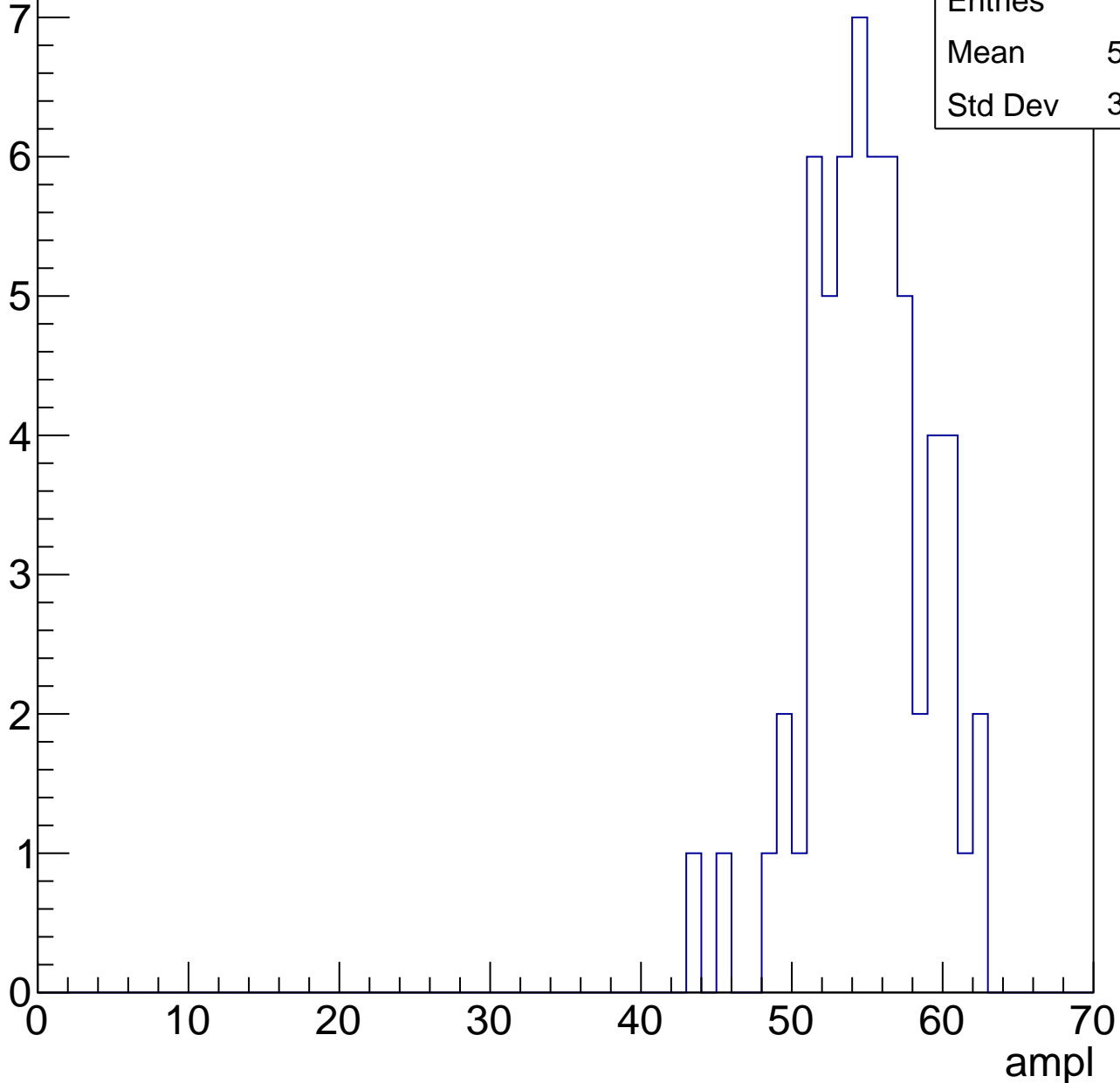


B1L102S, U14-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.57
Std Dev	3.857

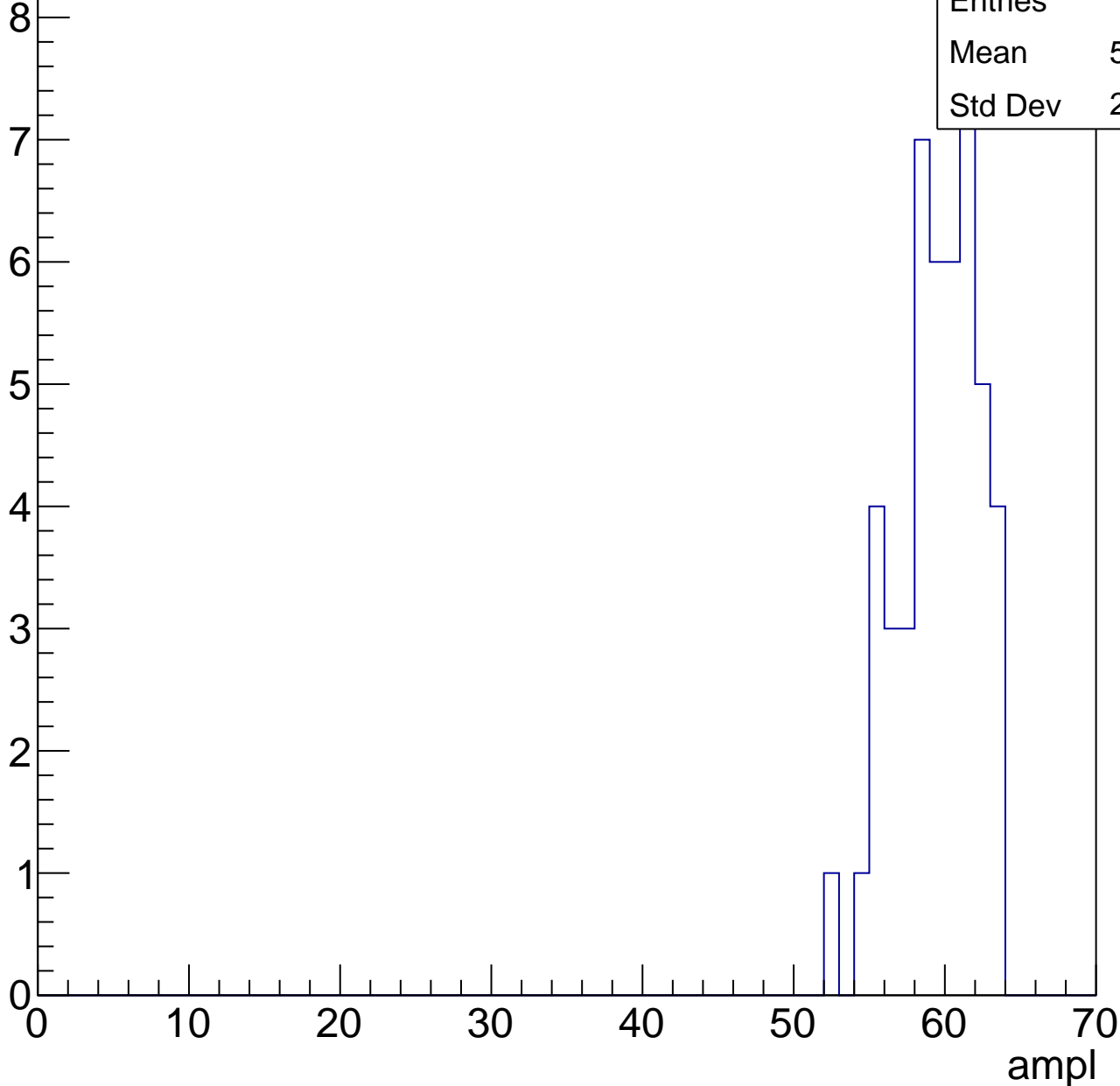


B1L102S, U14-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	59.06
Std Dev	2.625

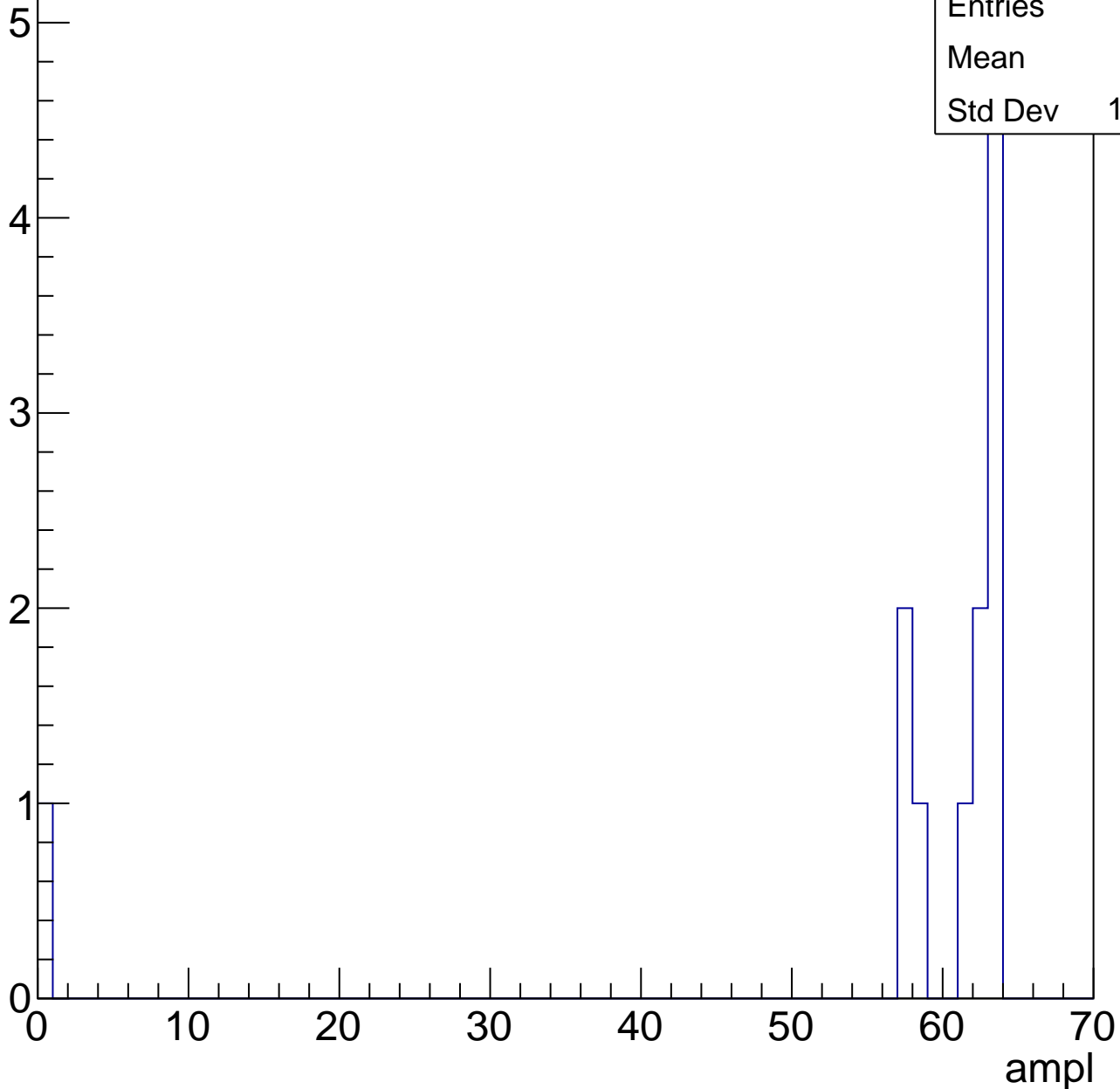


B1L102S, U14-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

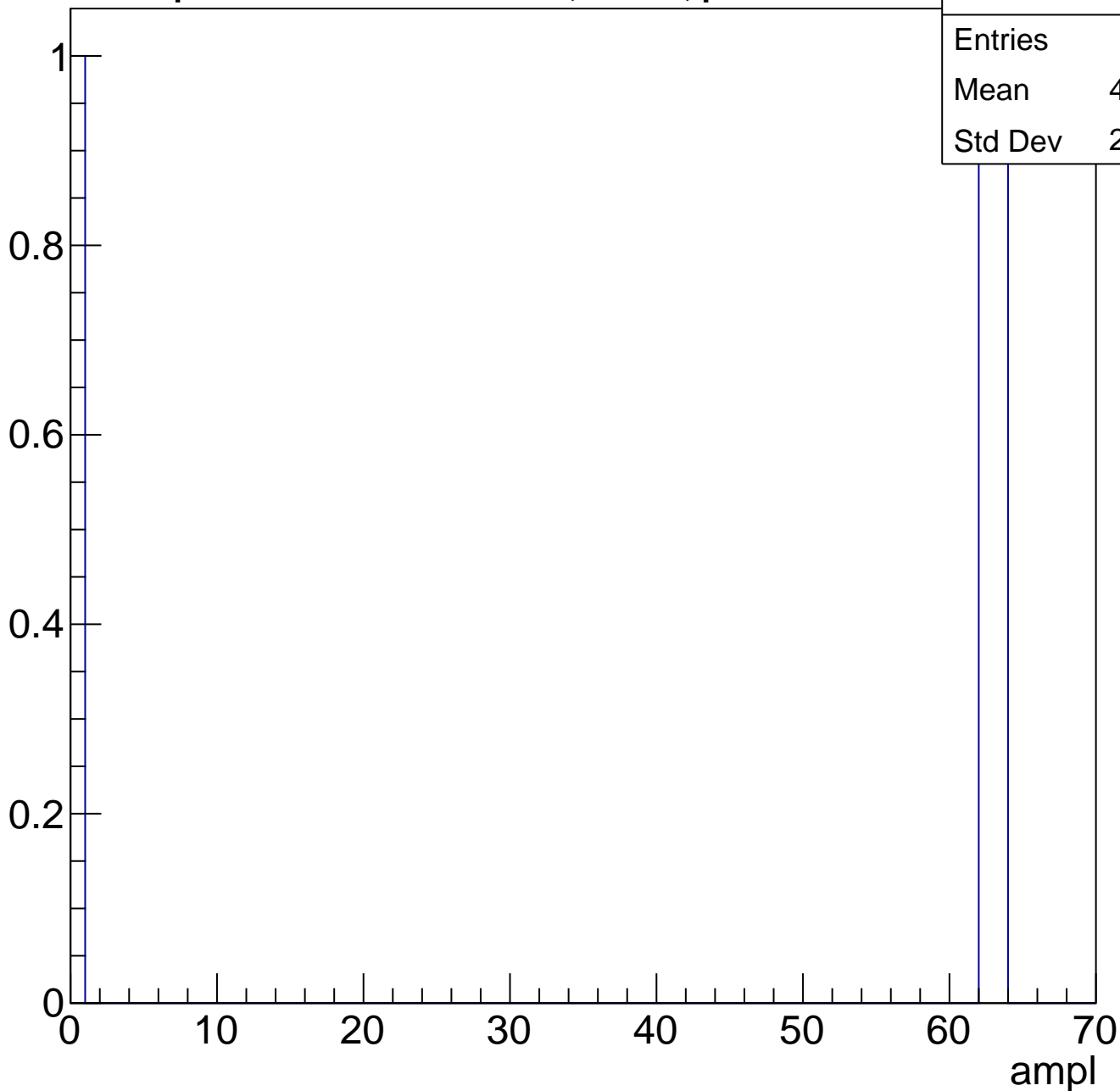
Entries	12
Mean	56
Std Dev	17.04



B1L102S, U14-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch126, adc0

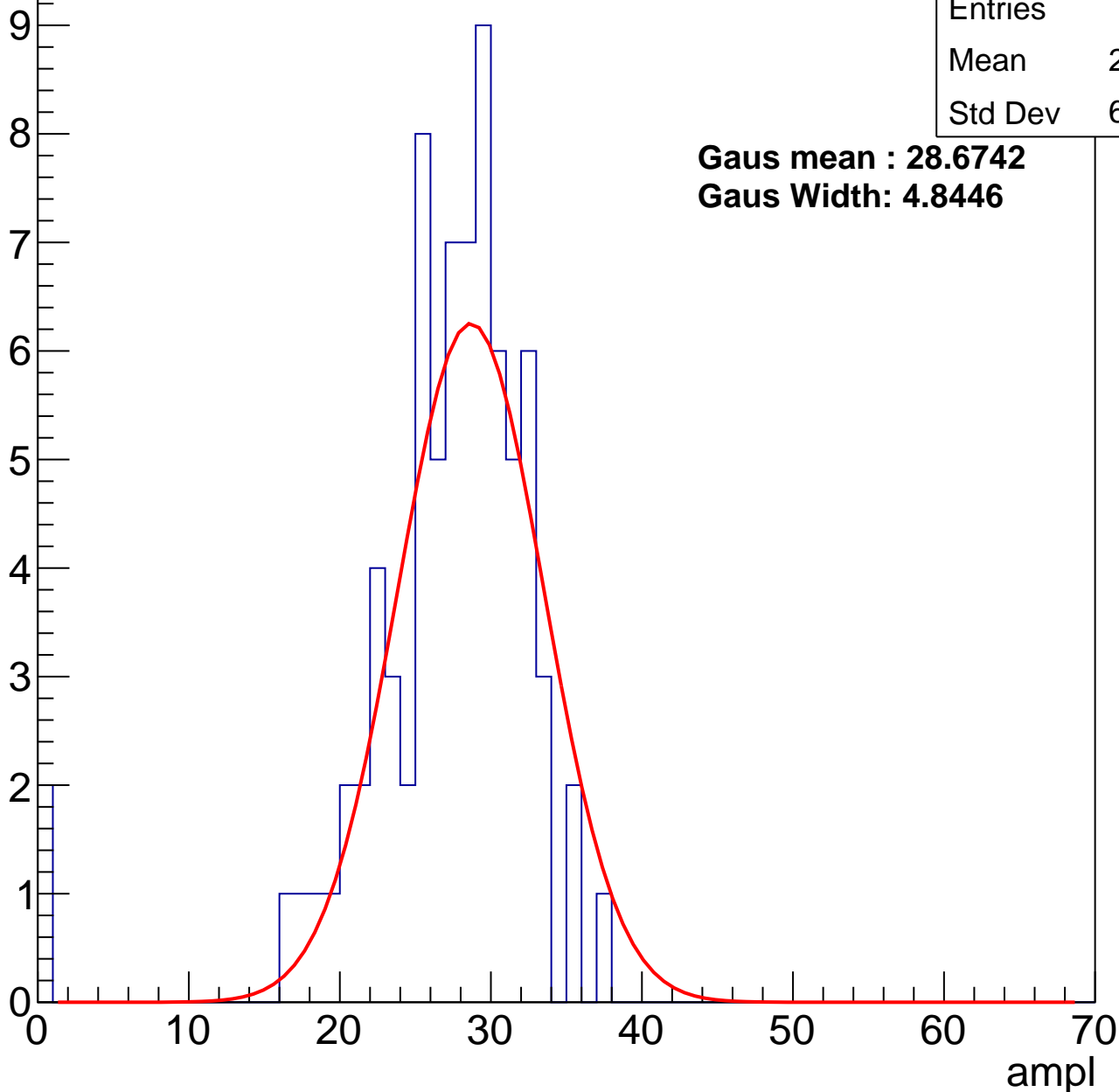
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	26.49
Std Dev	6.034

Gaus mean : 28.6742

Gaus Width: 4.8446



B1L102S, U14-ch126, adc1

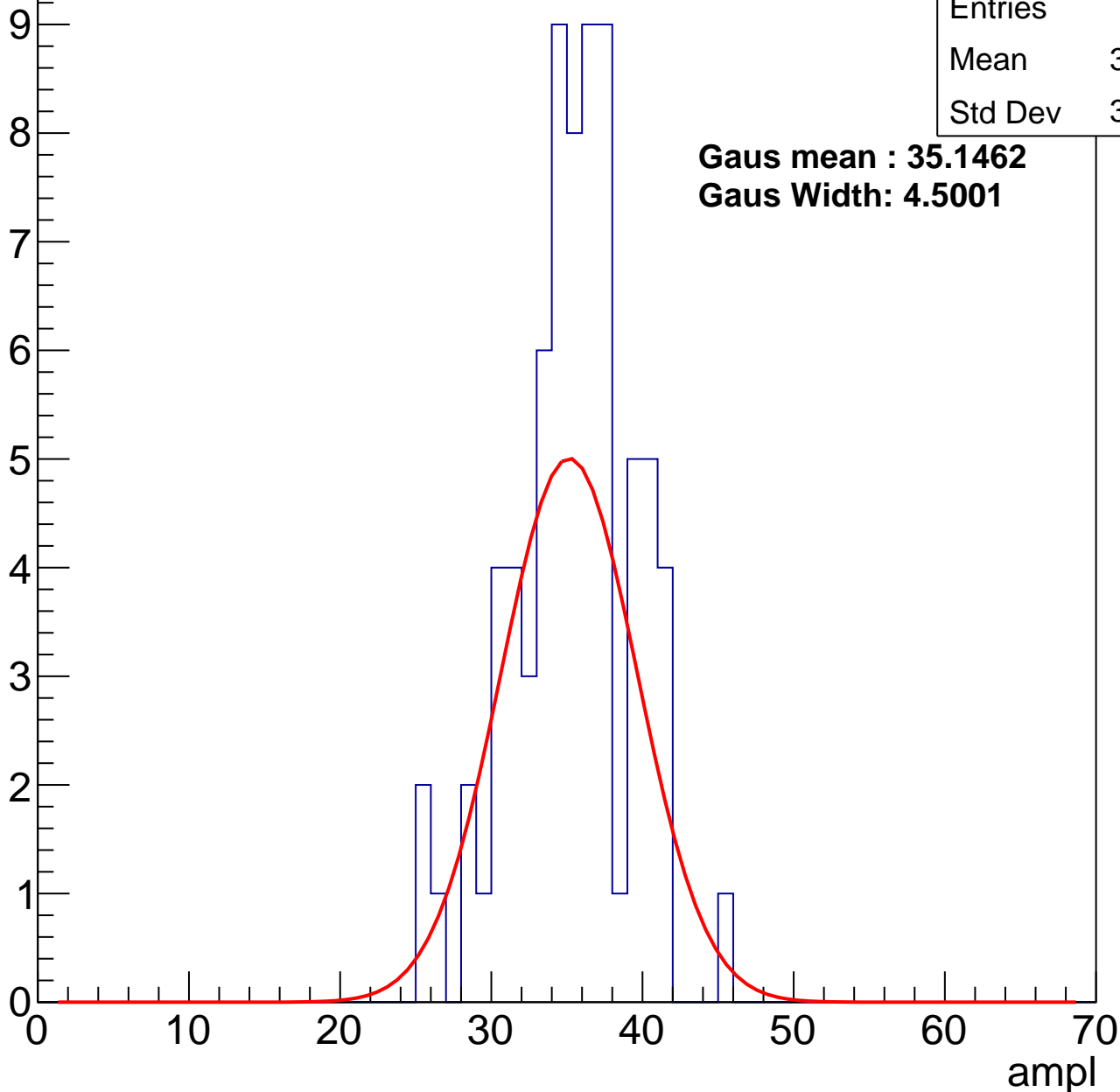
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.92
Std Dev	3.928

Gaus mean : 35.1462

Gaus Width: 4.5001



B1L102S, U14-ch126, adc2

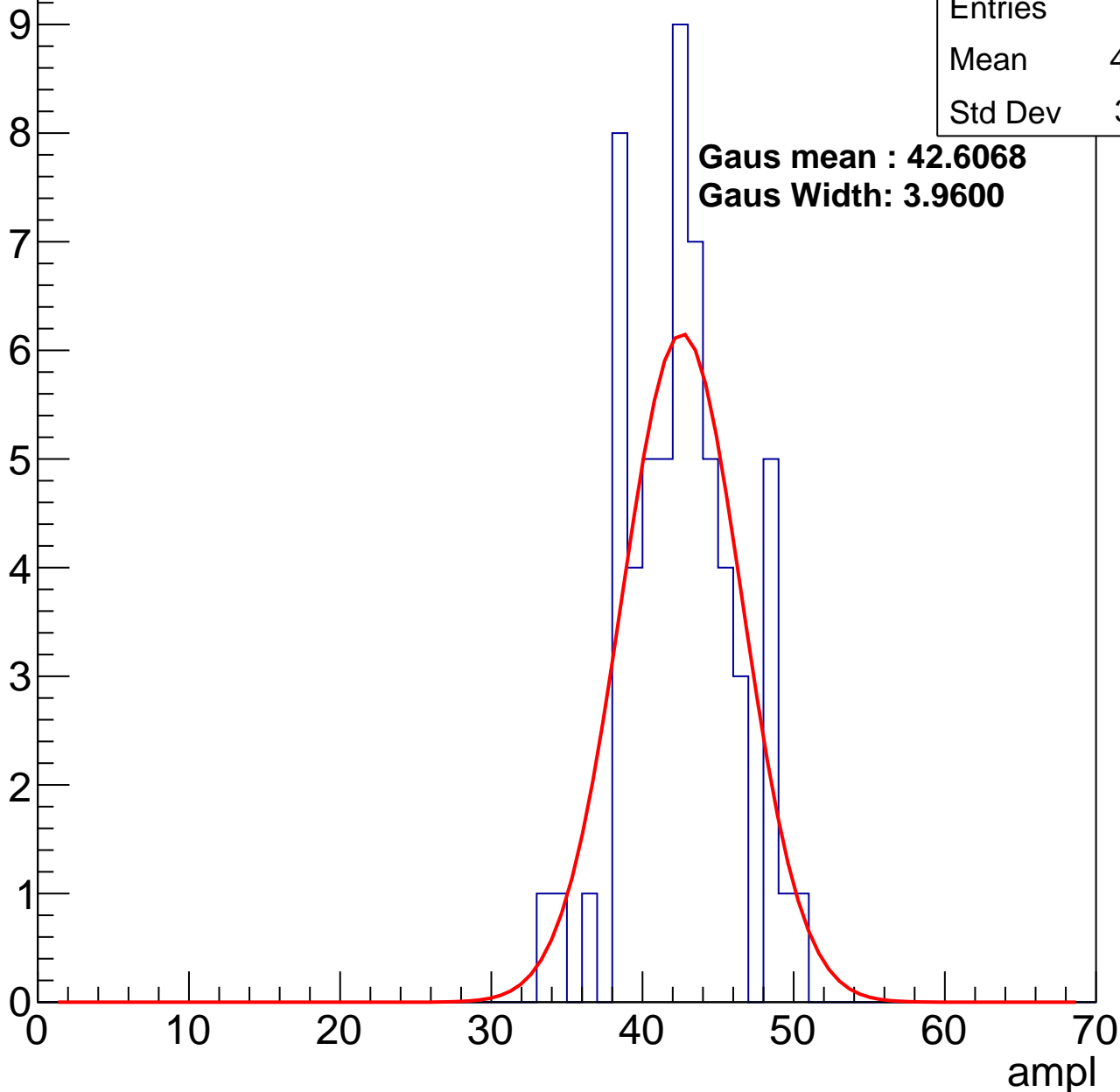
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.07
Std Dev	3.591

Gaus mean : 42.6068

Gaus Width: 3.9600

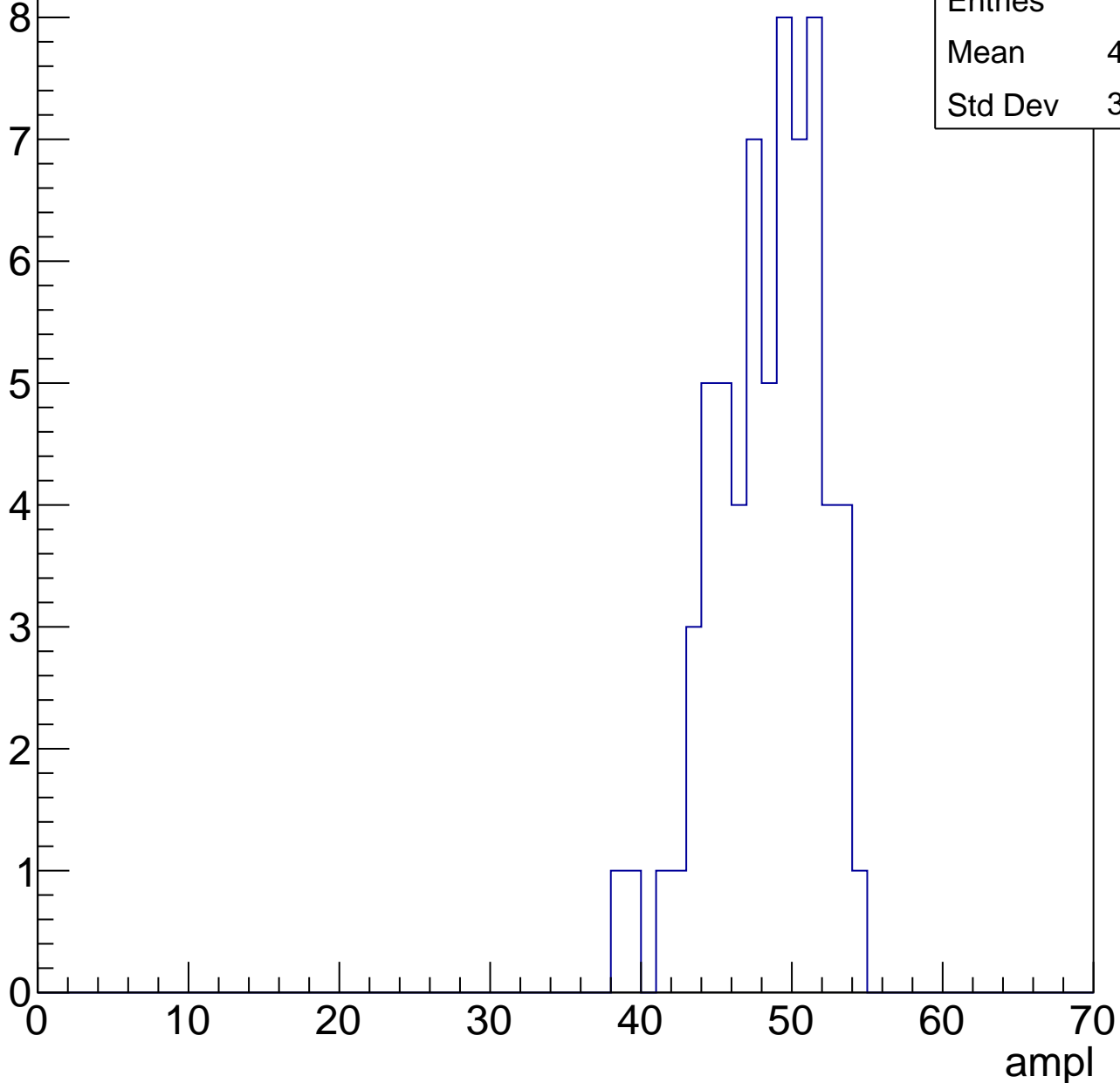


B1L102S, U14-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.86
Std Dev	3.499

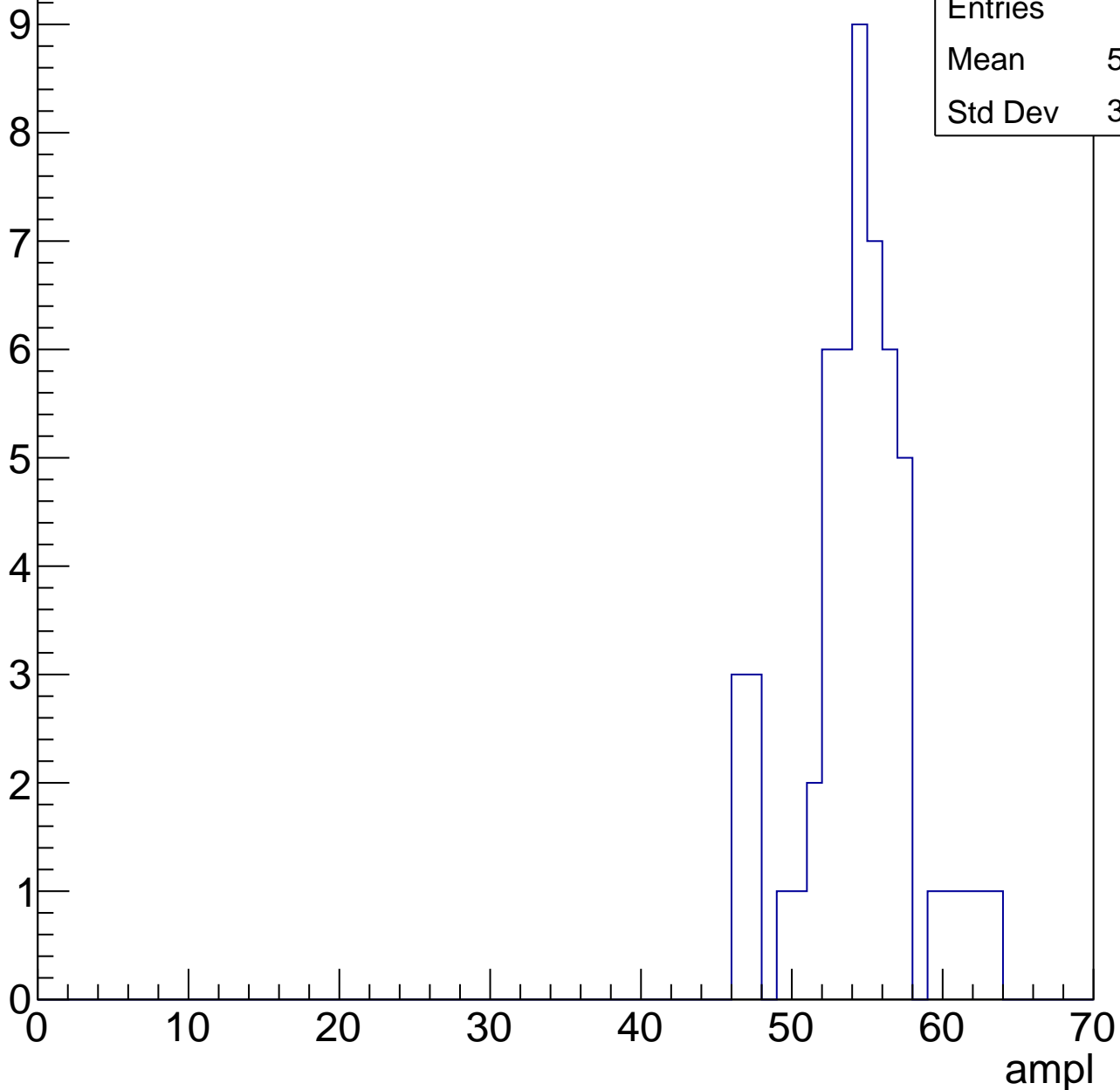


B1L102S, U14-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.83
Std Dev	3.745

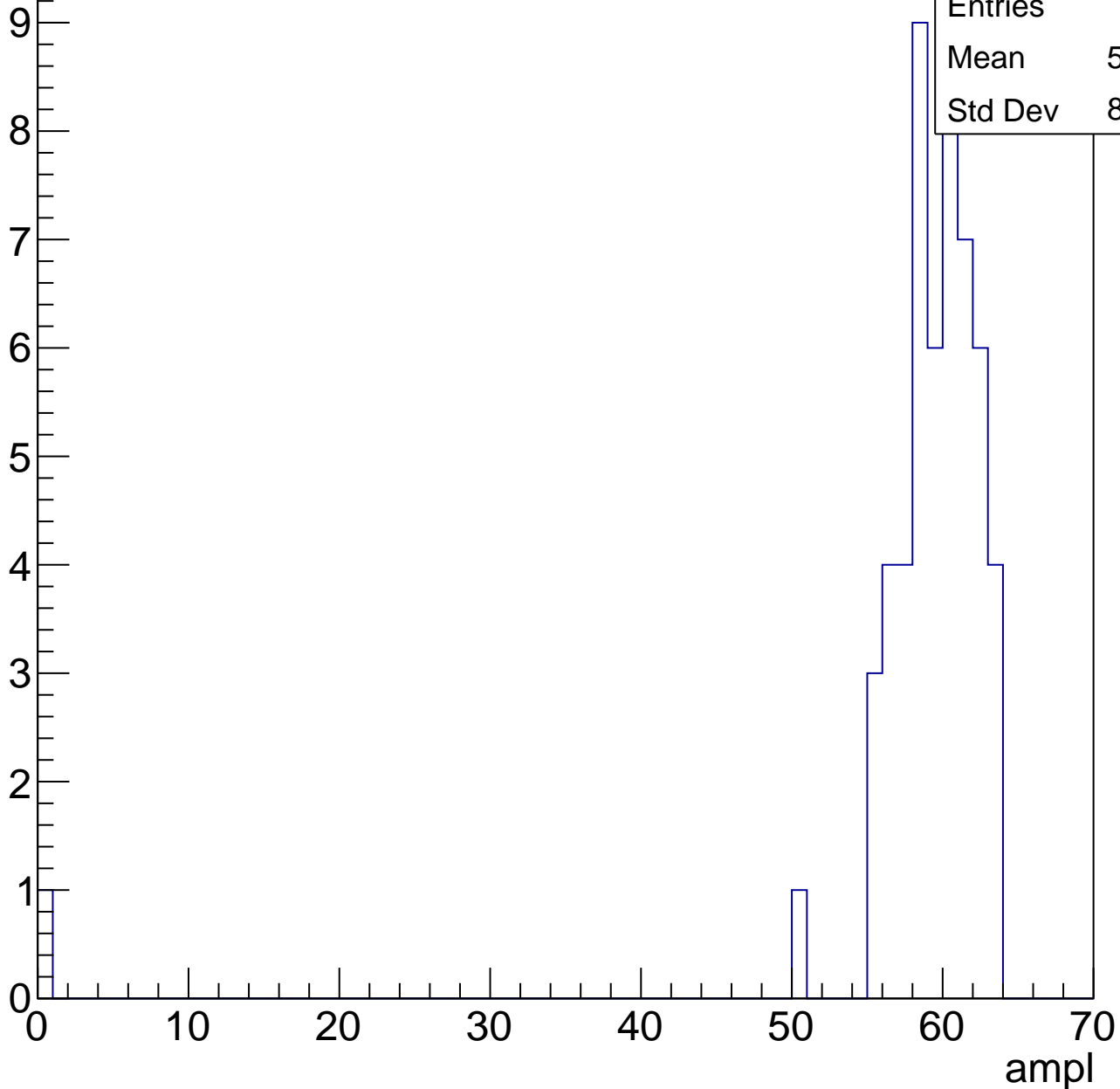


B1L102S, U14-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

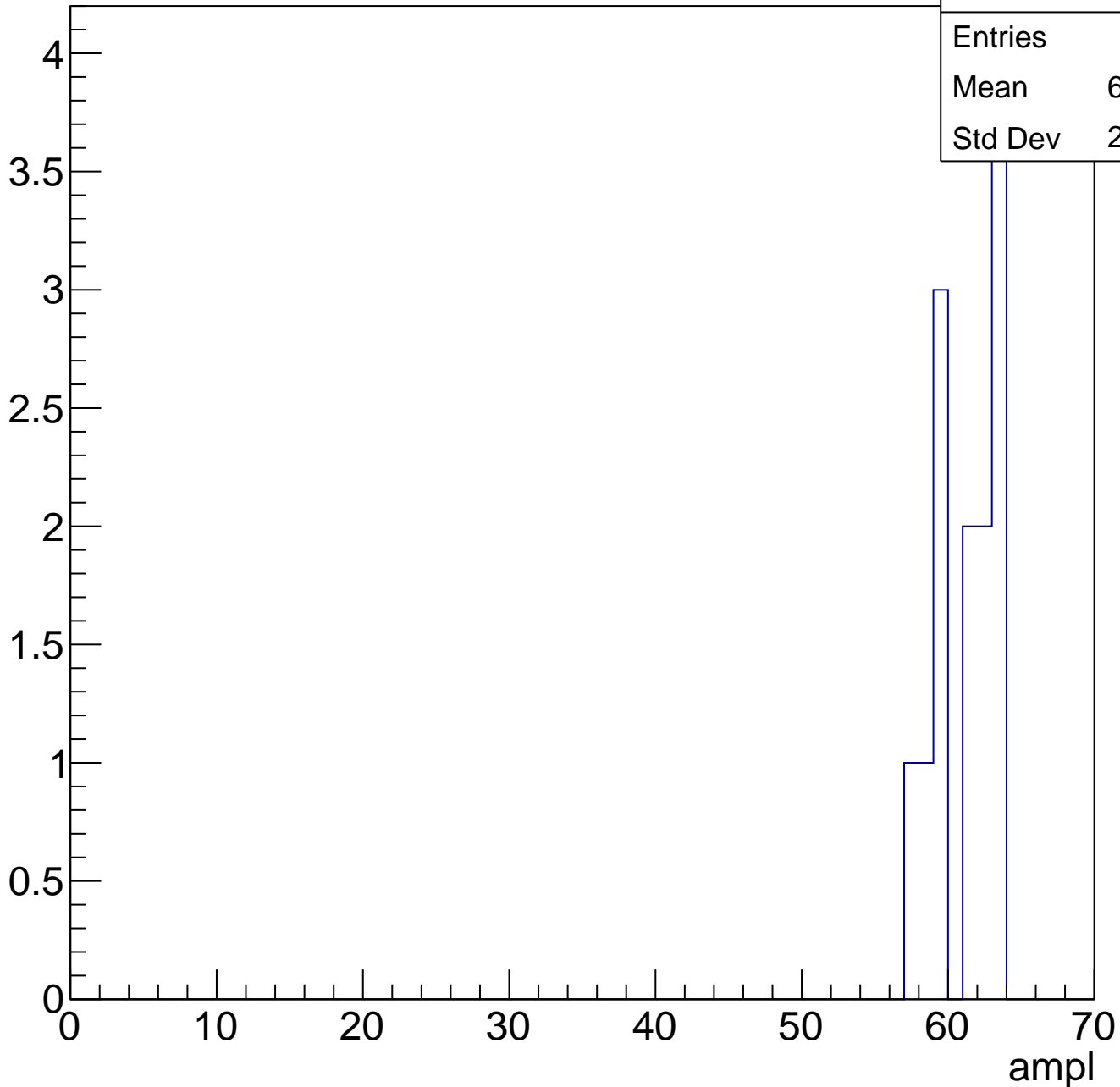
Entries	54
Mean	58.04
Std Dev	8.362



B1L102S, U14-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	13
Mean	60.77
Std Dev	2.044

B1L102S, U14-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch127, adc0

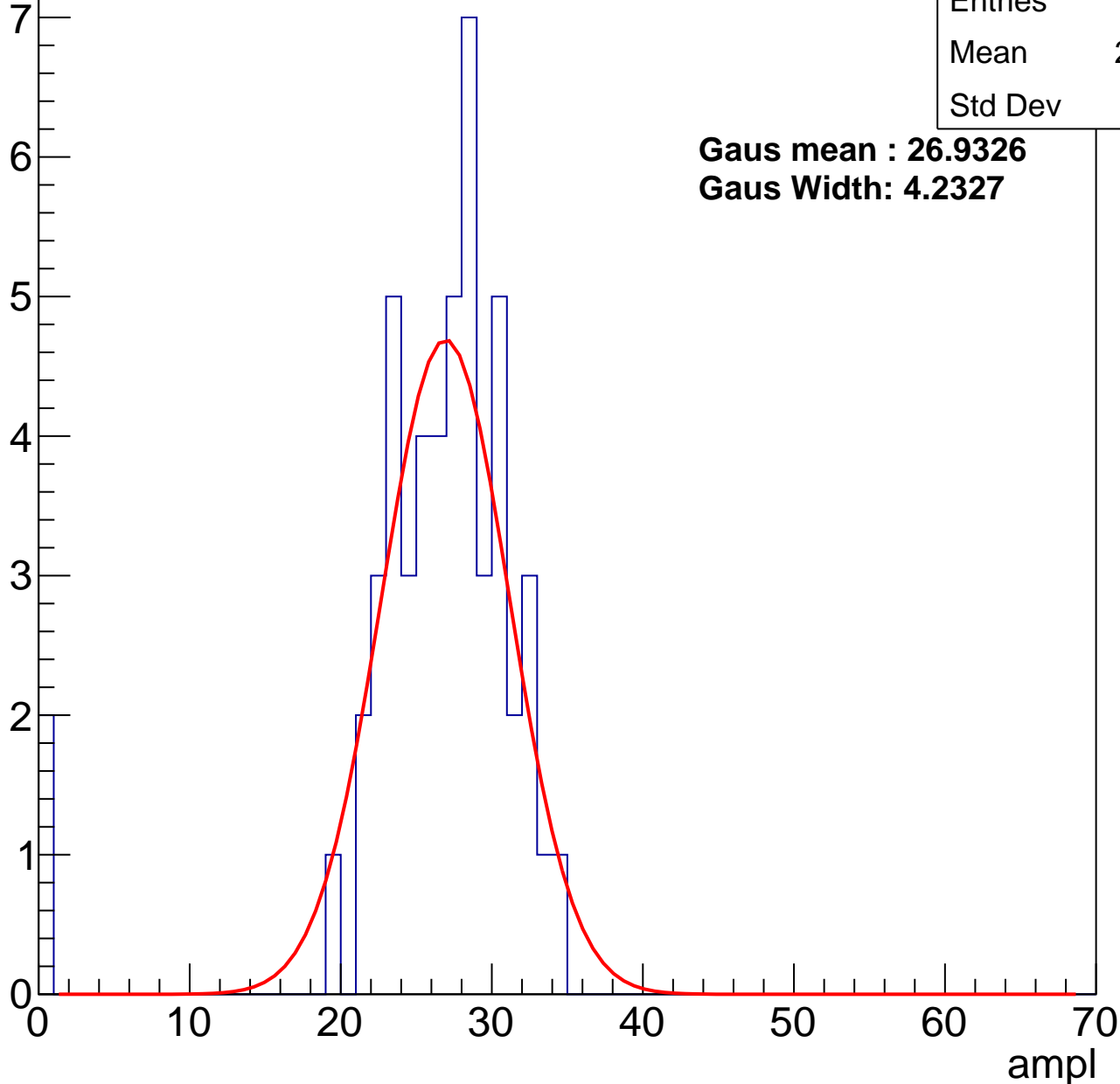
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	25.71
Std Dev	6.21

Gaus mean : 26.9326

Gaus Width: 4.2327



B1L102S, U14-ch127, adc1

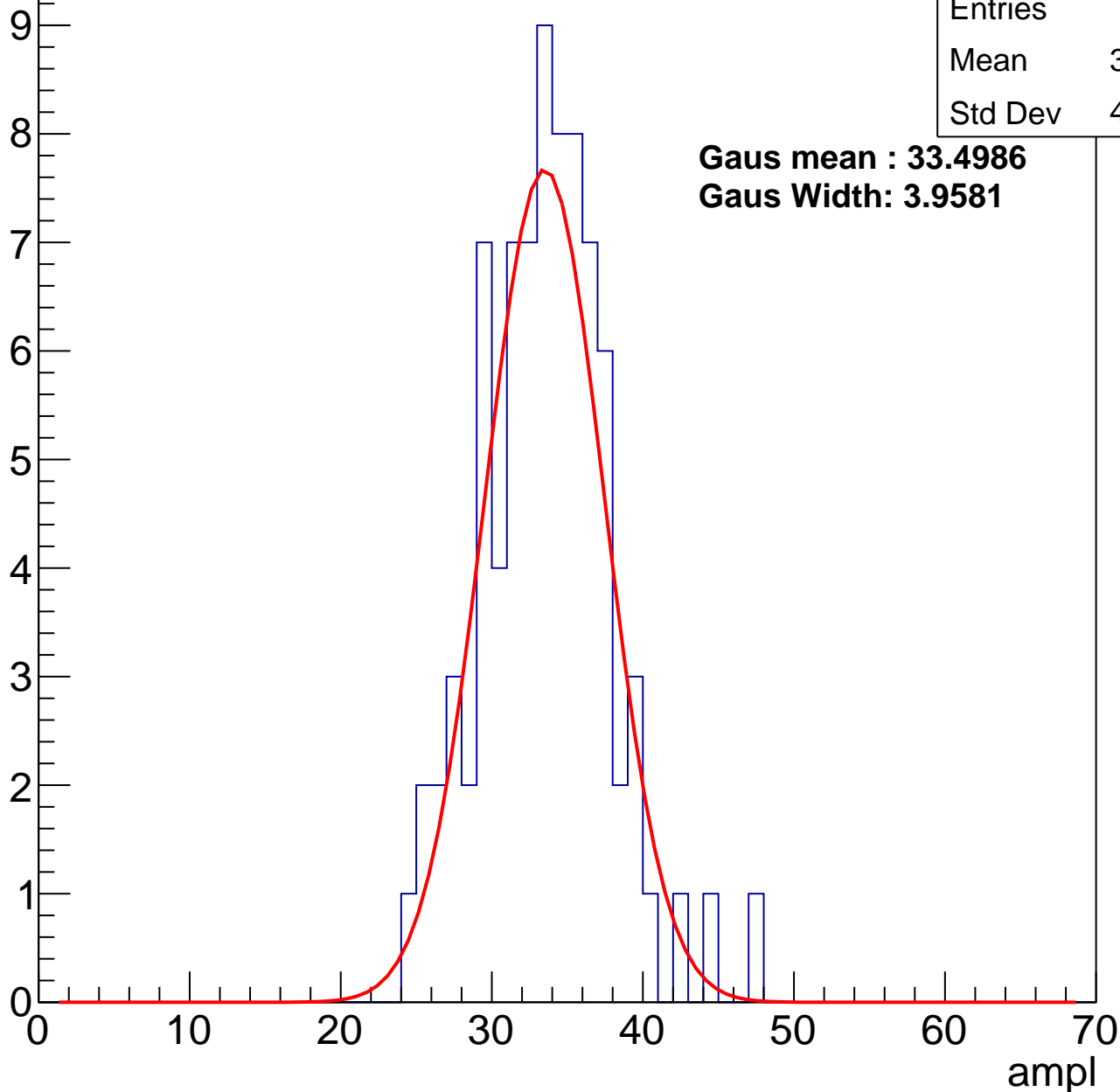
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	33.12
Std Dev	4.206

Gaus mean : 33.4986

Gaus Width: 3.9581



B1L102S, U14-ch127, adc2

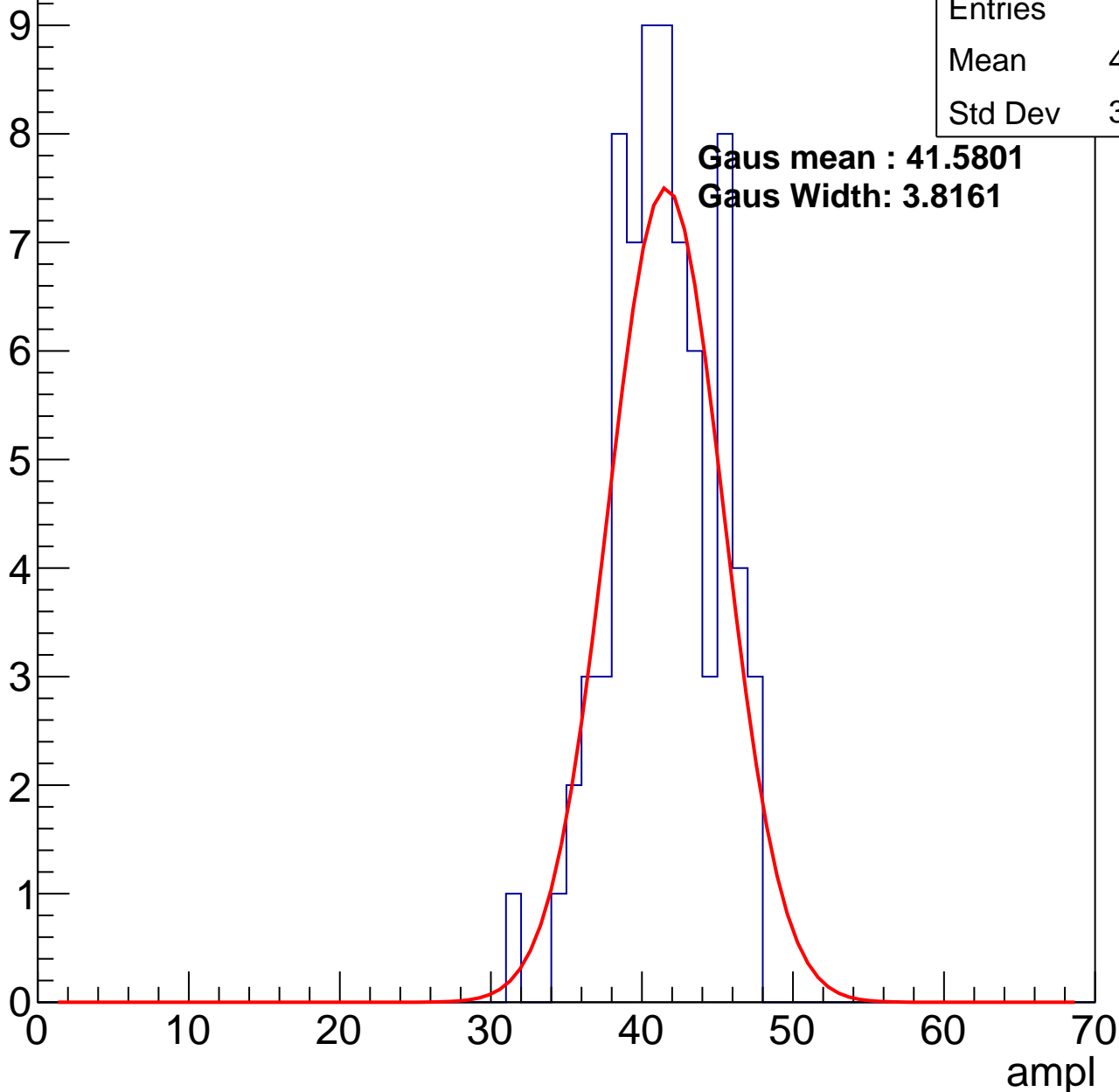
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	40.93
Std Dev	3.383

Gaus mean : 41.5801

Gaus Width: 3.8161

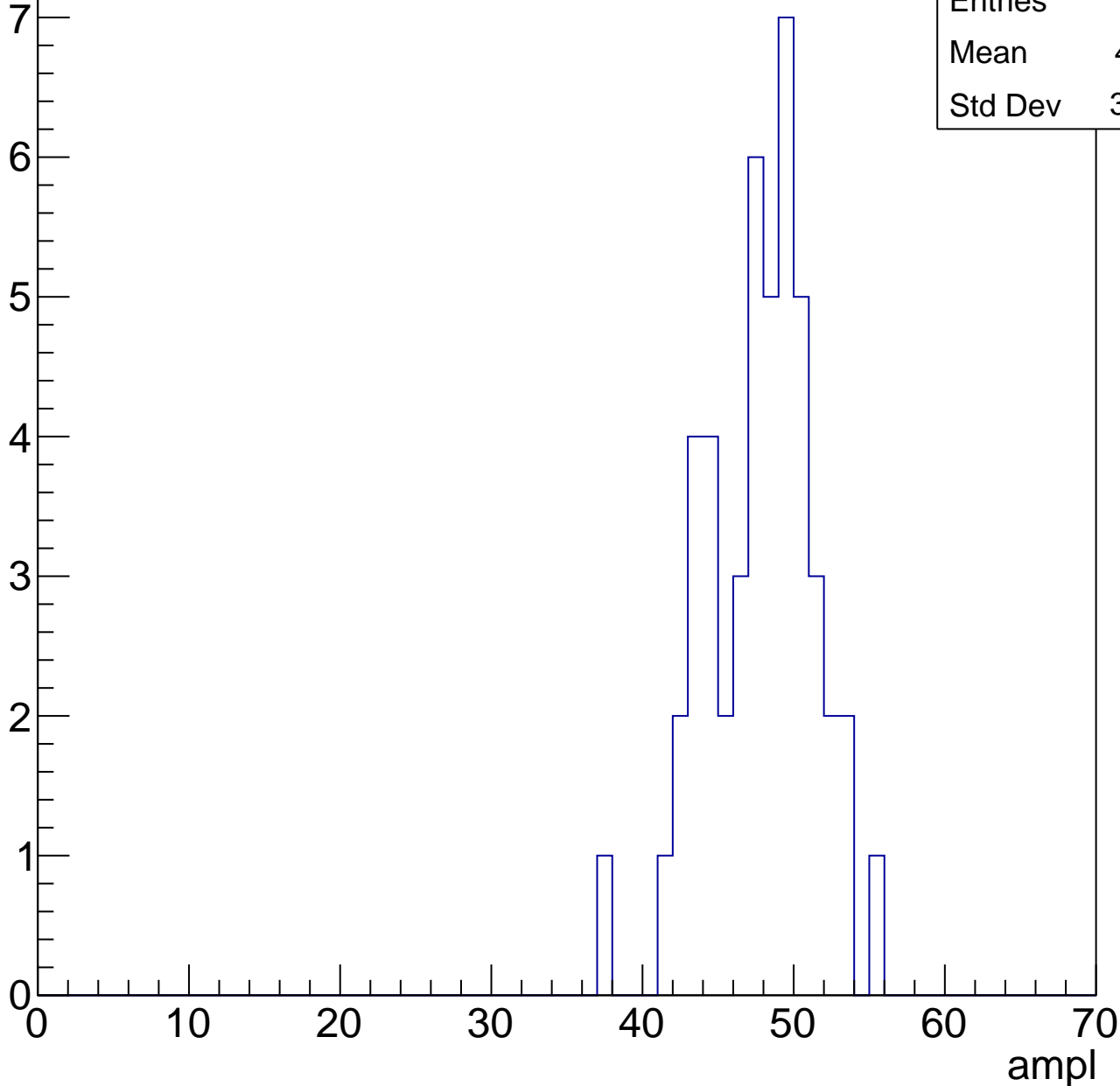


B1L102S, U14-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	47.31
Std Dev	3.548

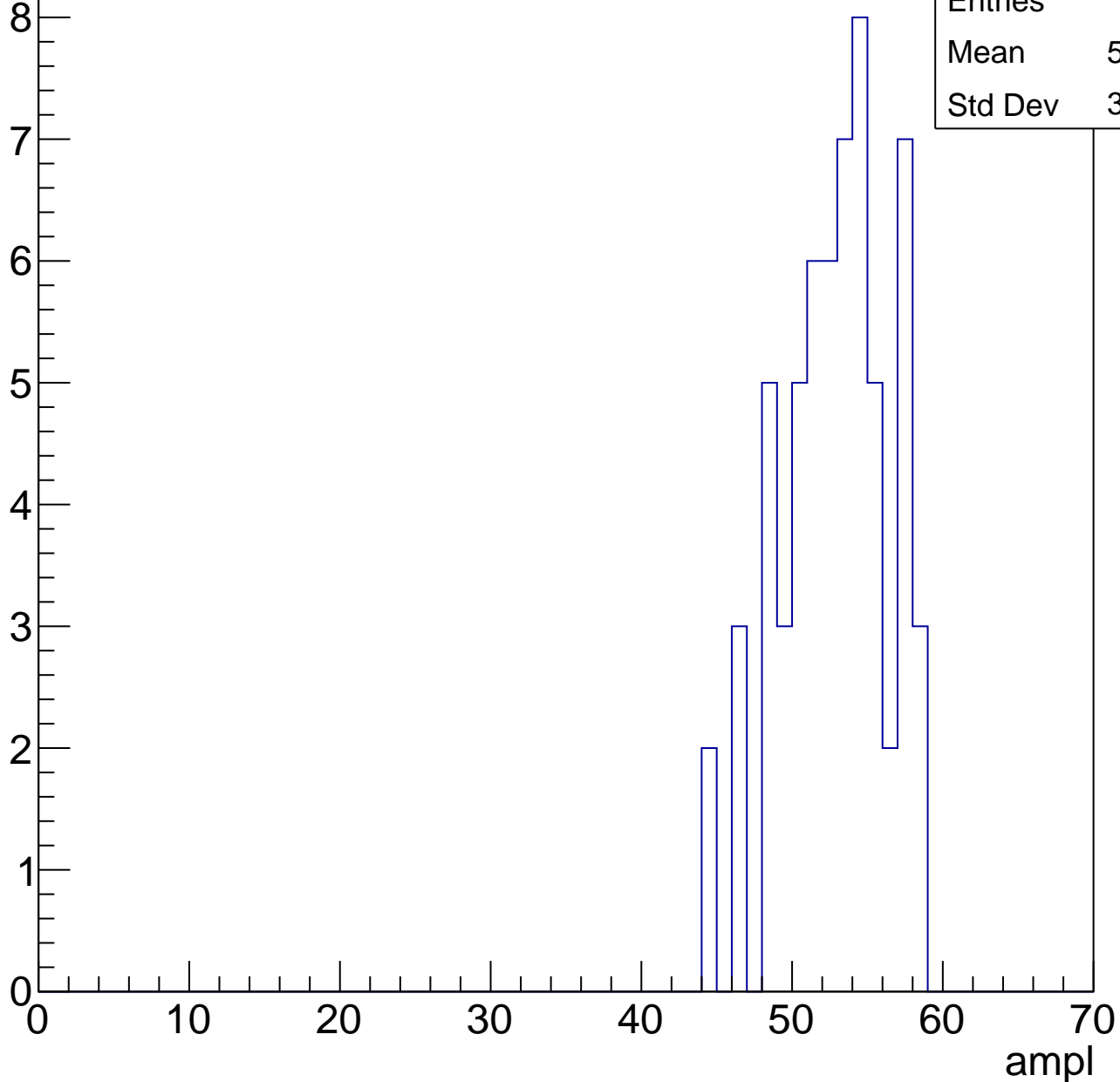


B1L102S, U14-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	52.32
Std Dev	3.505

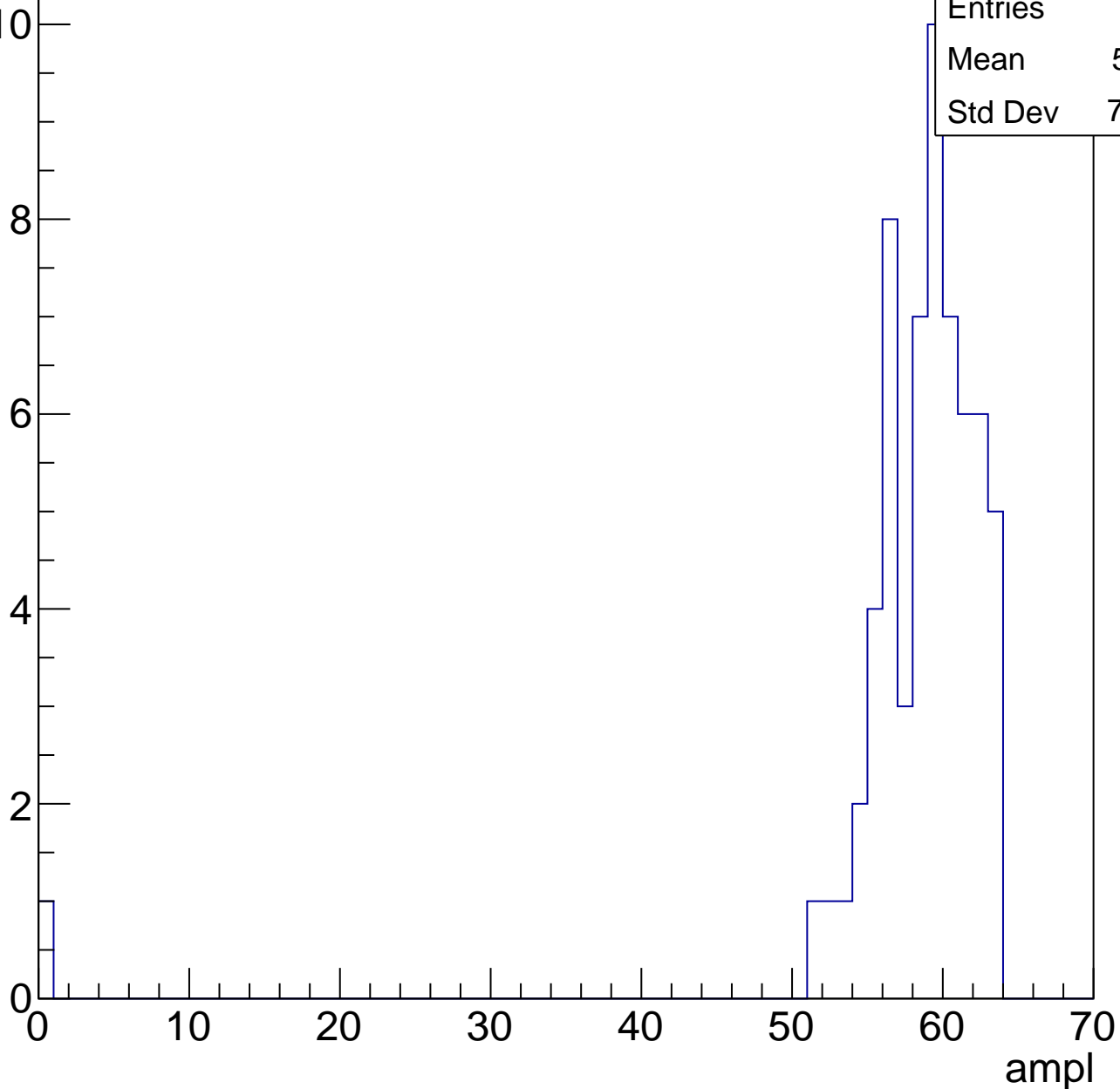


B1L102S, U14-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	57.61
Std Dev	7.912

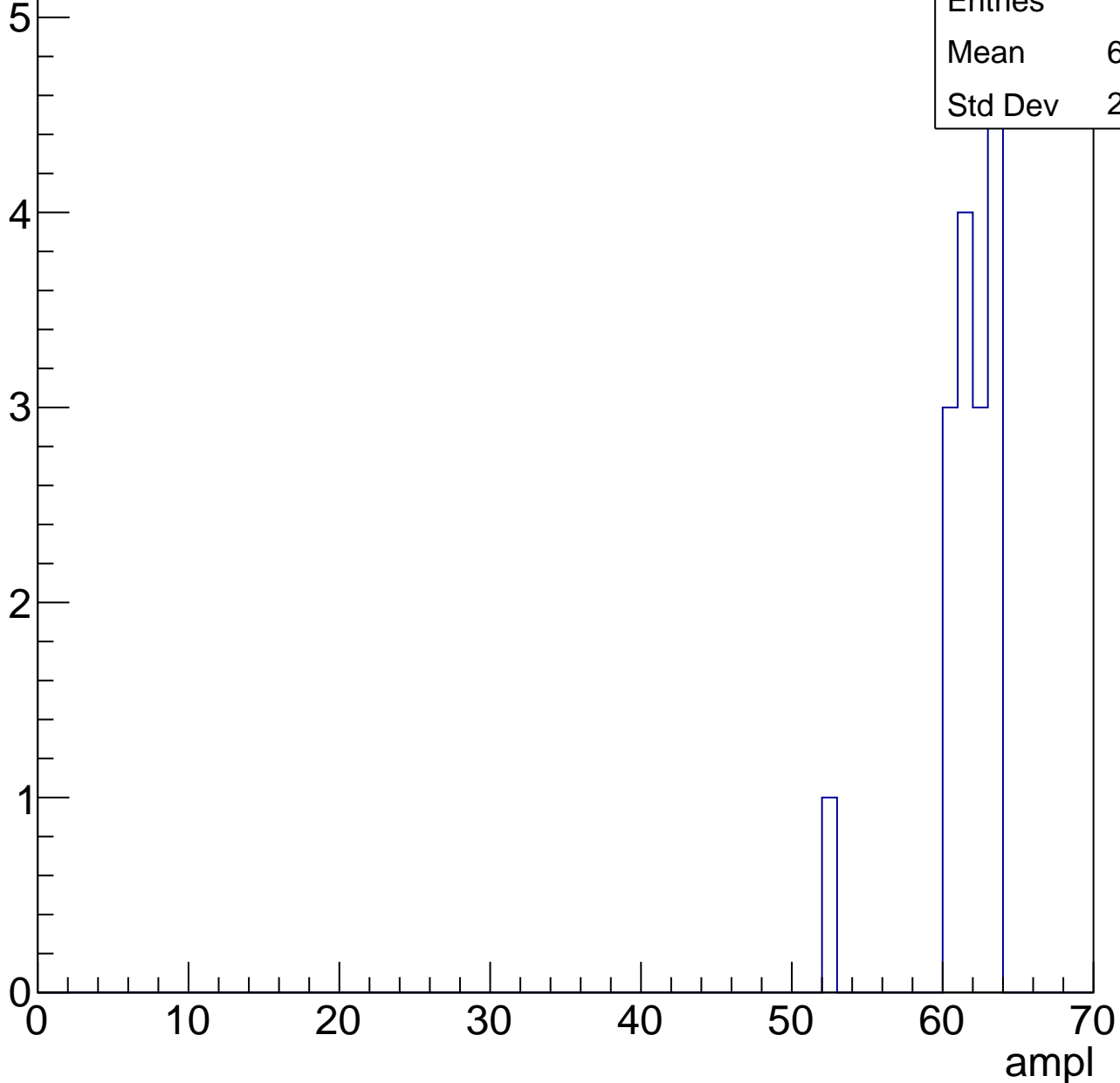


B1L102S, U14-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.06
Std Dev	2.585



B1L102S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

