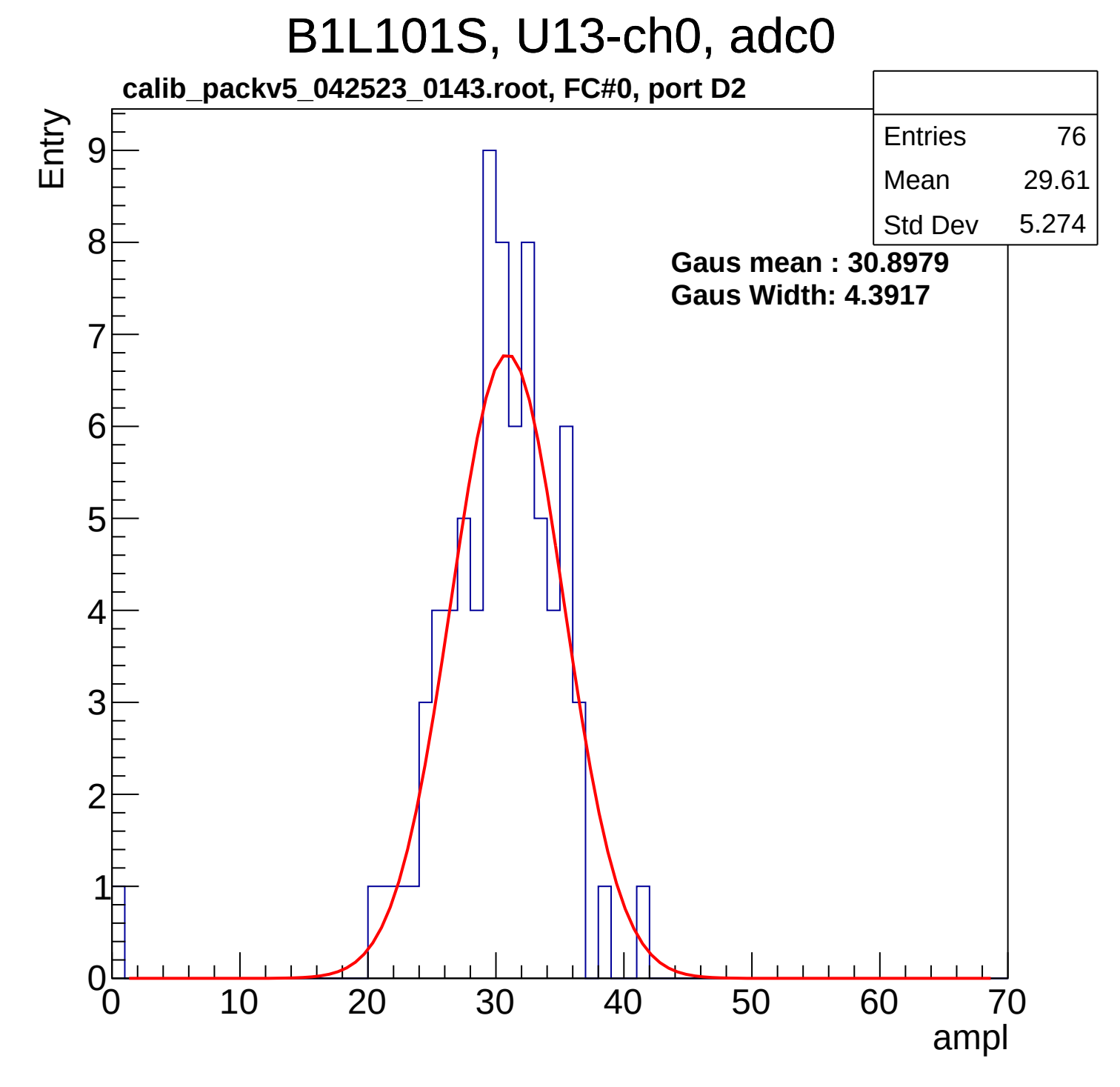
B1L101S, U13-ch0, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 29.61
Std Dev 5.274
Gaus mean : 30.8979
Gaus Width: 4.3917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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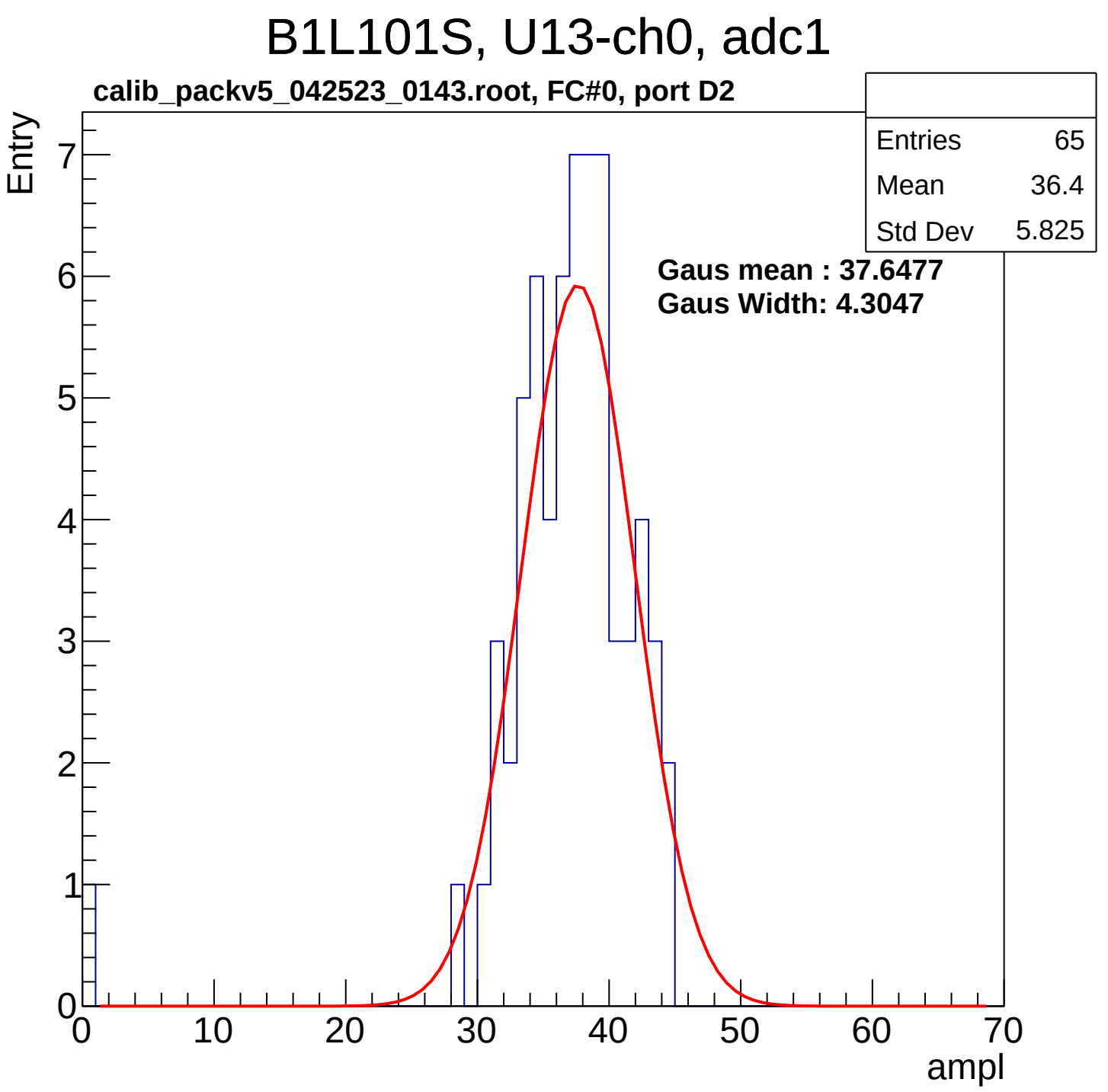
B1L101S, U13-ch0, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 36.4
Std Dev 5.825
Gaus mean : 37.6477
Gaus Width: 4.3047
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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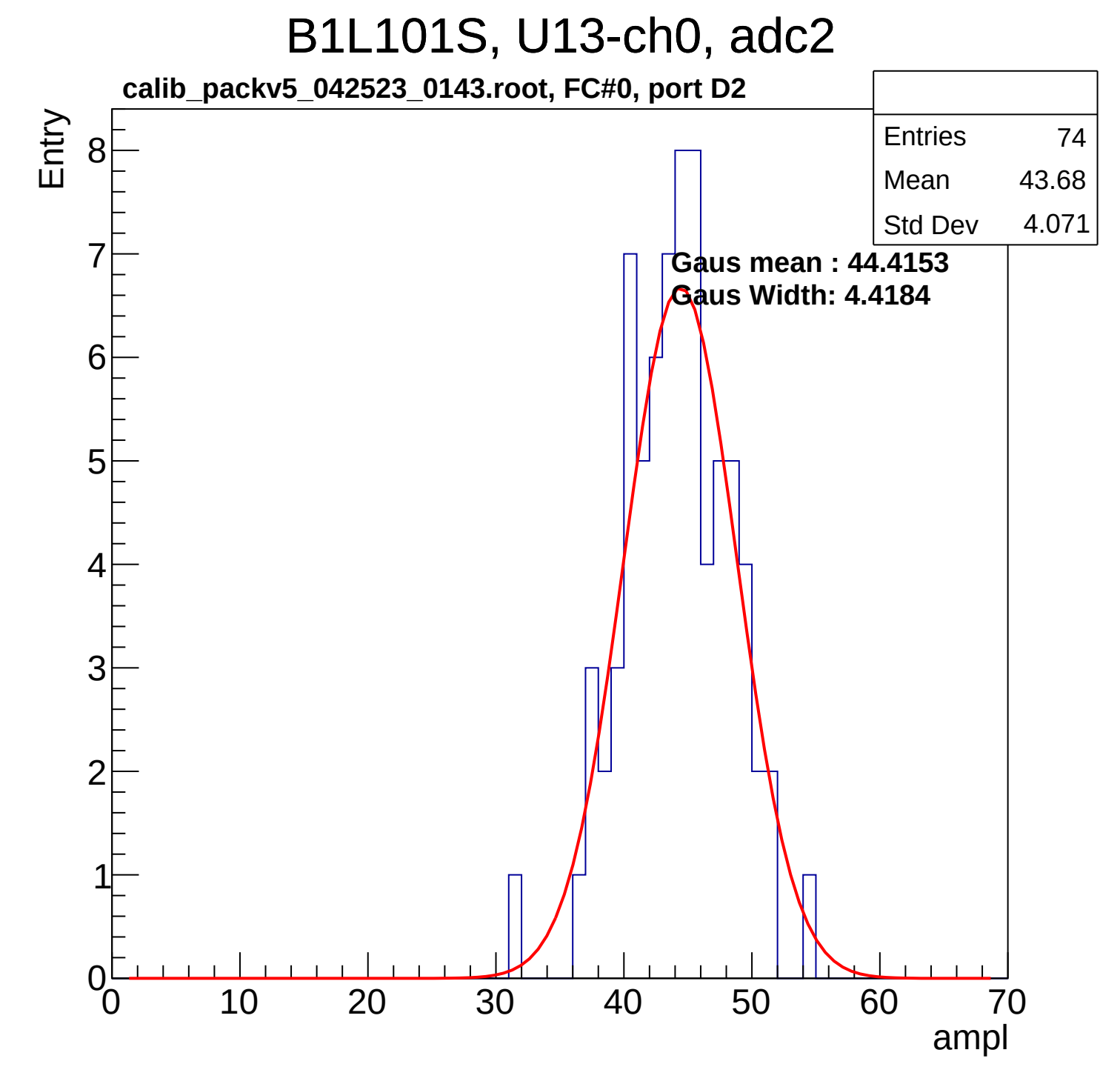
B1L101S, U13-ch0, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 43.68
Std Dev 4.071
Gaus mean : 44.4153
Gaus Width: 4.4184
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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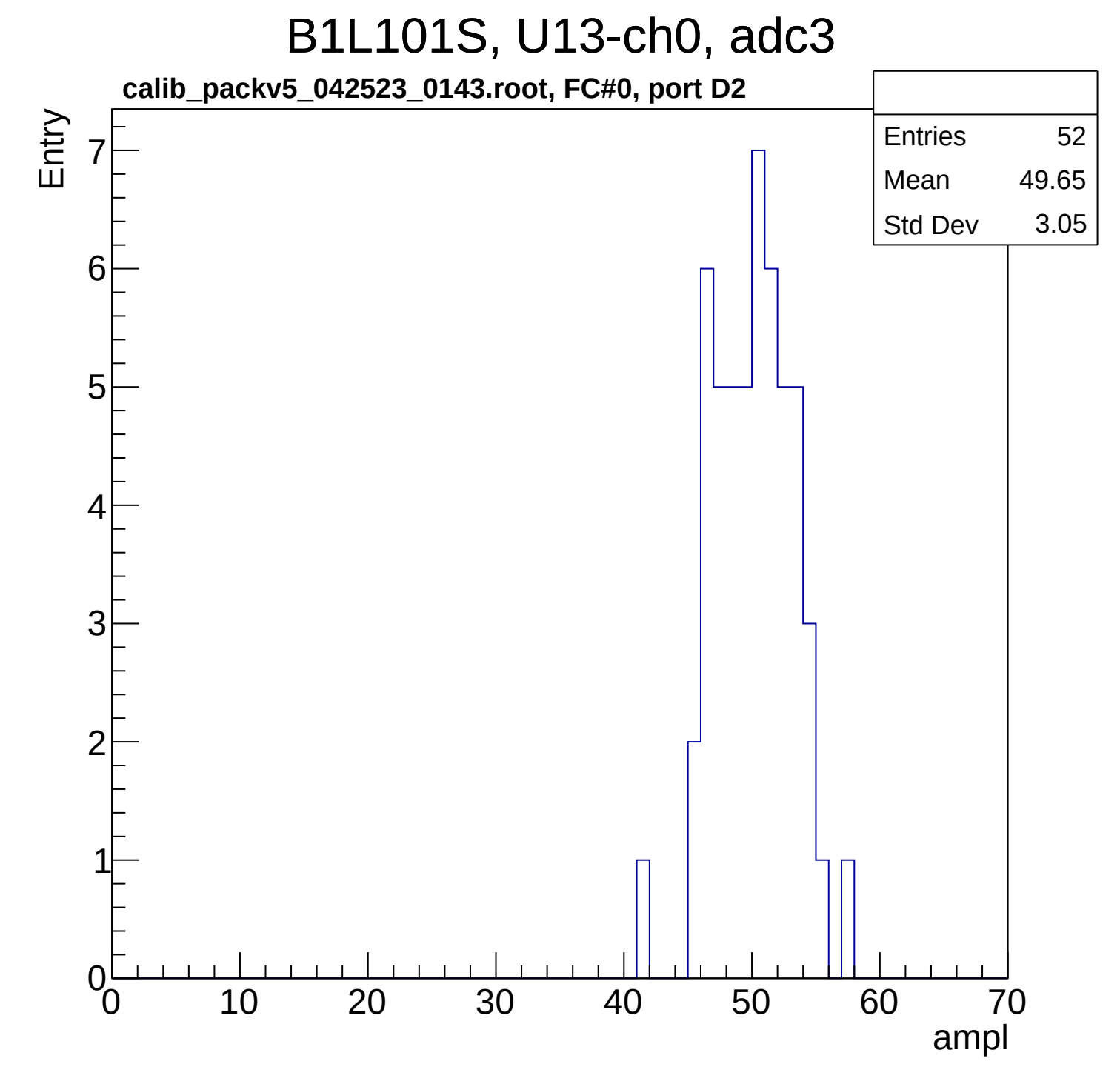
B1L101S, U13-ch0, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 52
Mean 49.65
Std Dev 3.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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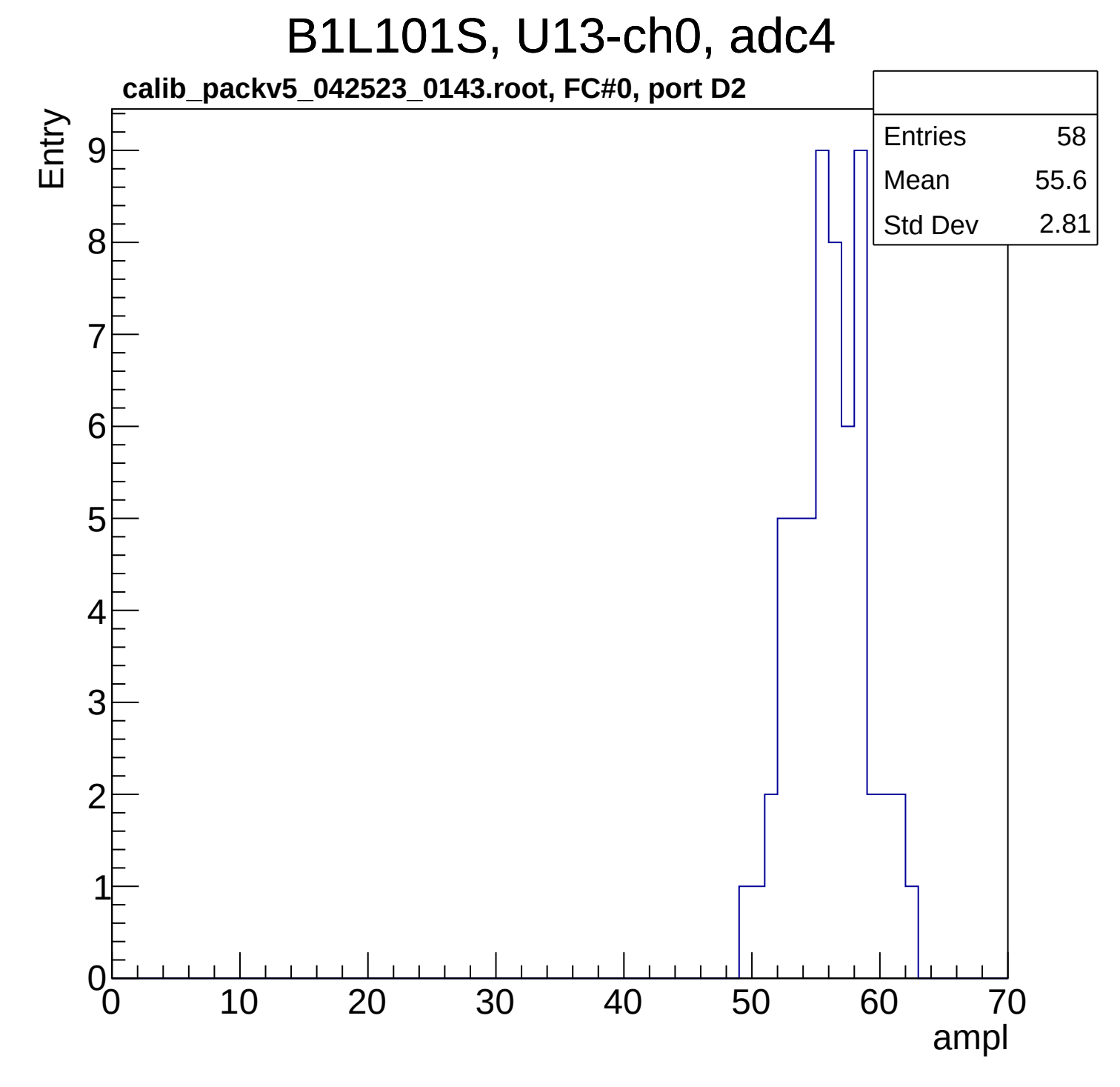
B1L101S, U13-ch0, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 55.6
Std Dev 2.81
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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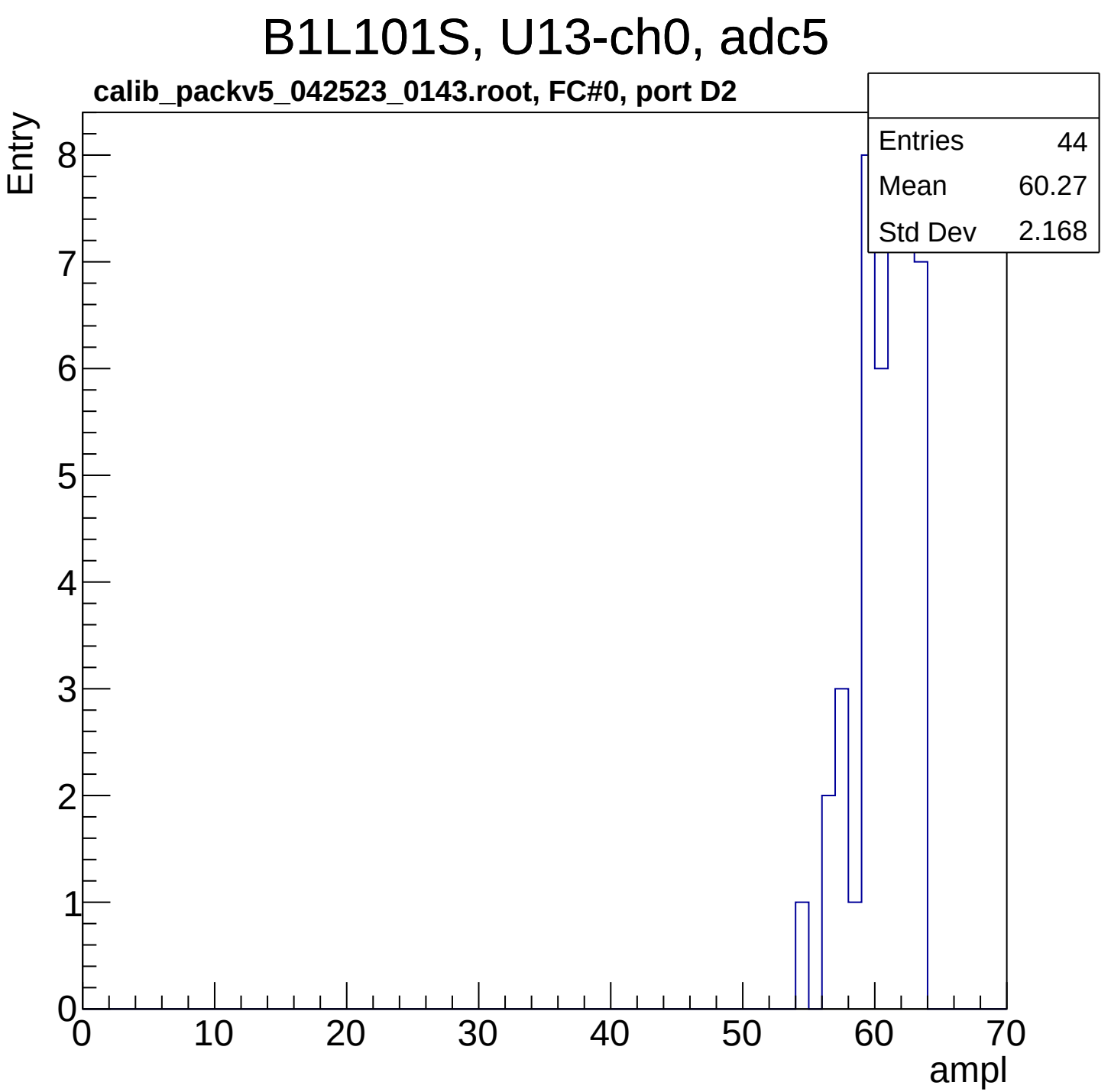
B1L101S, U13-ch0, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 60.27
Std Dev 2.168
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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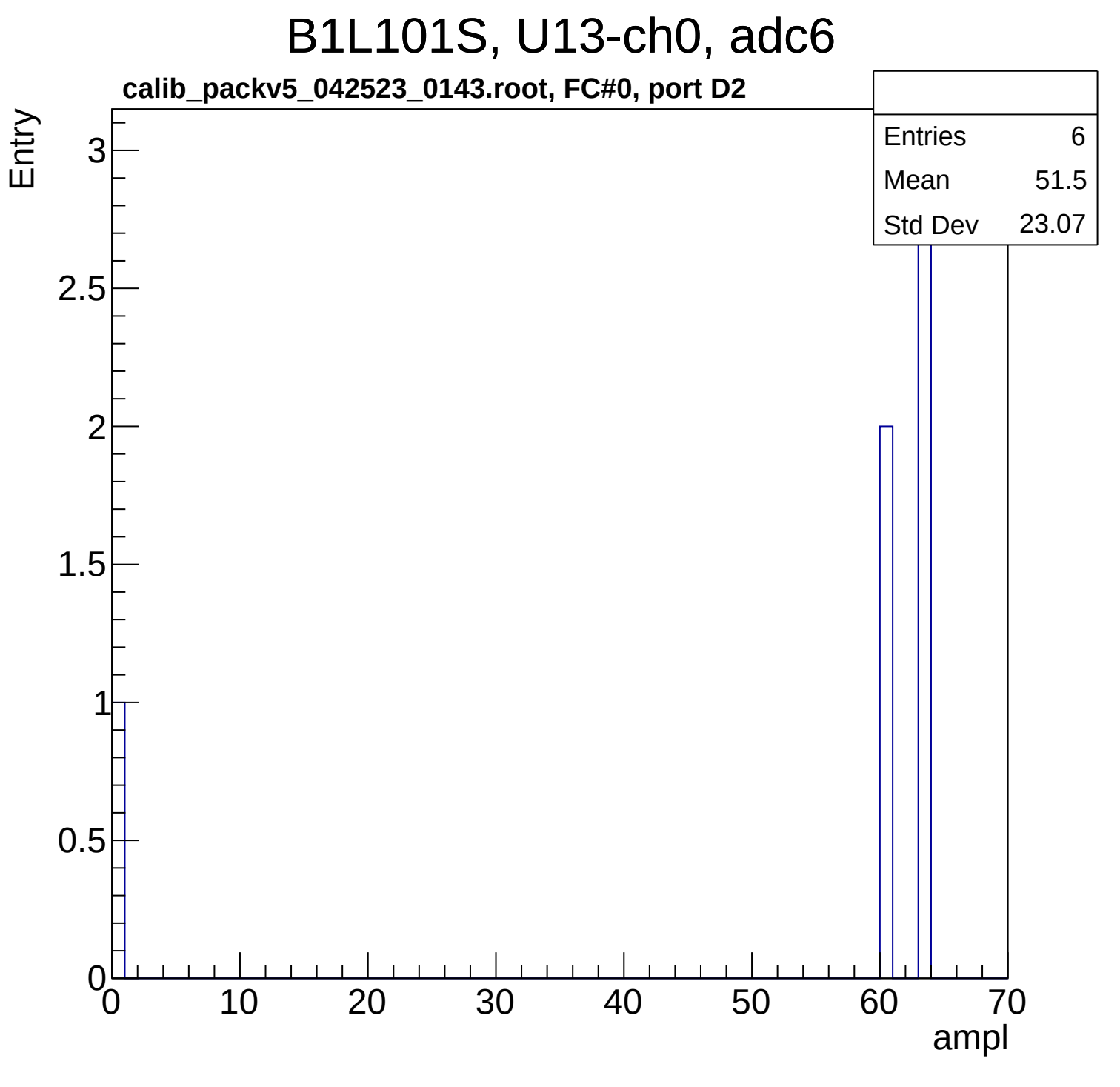
B1L101S, U13-ch0, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 6
Mean 51.5
Std Dev 23.07
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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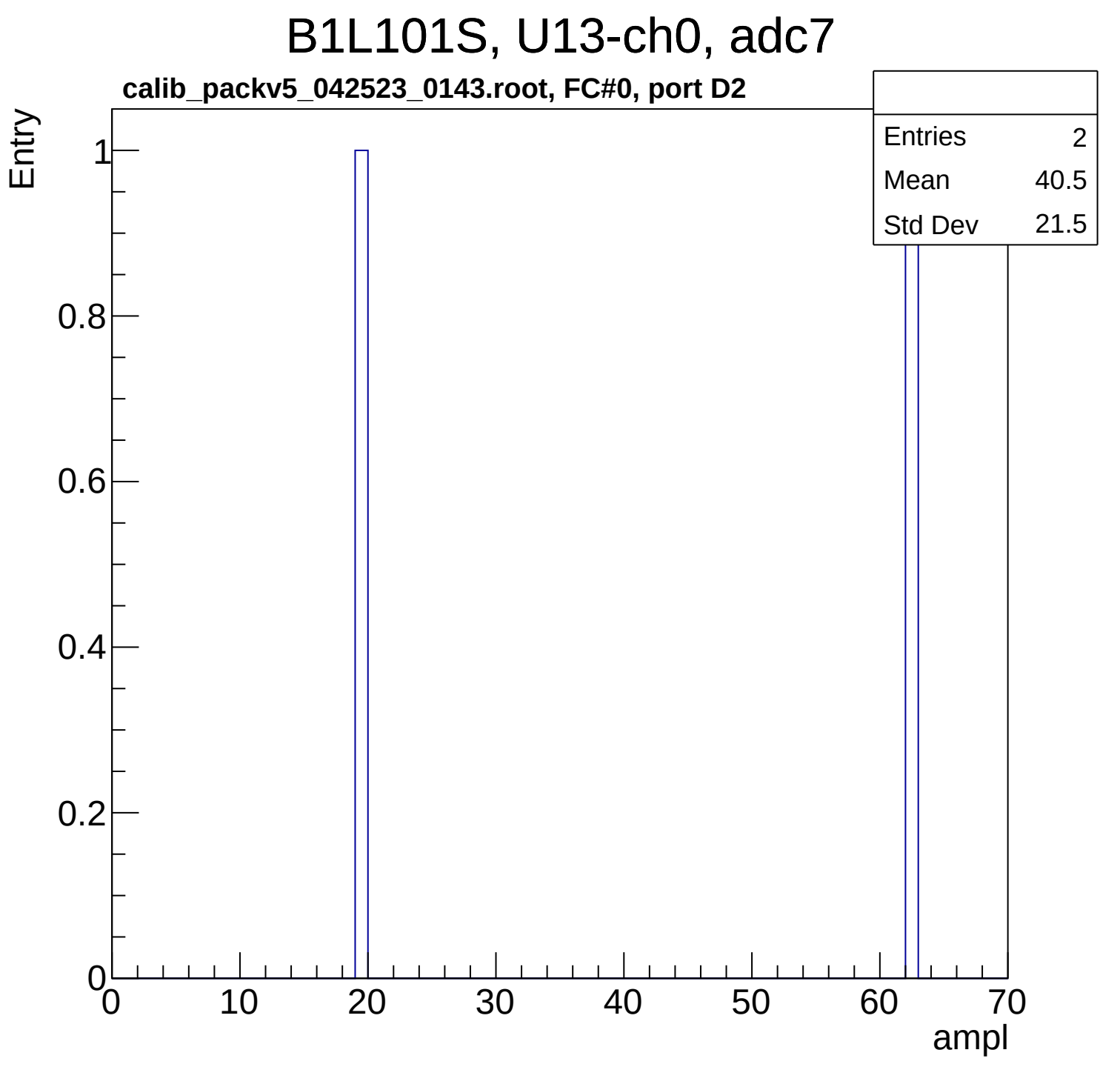
B1L101S, U13-ch0, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 40.5
Std Dev 21.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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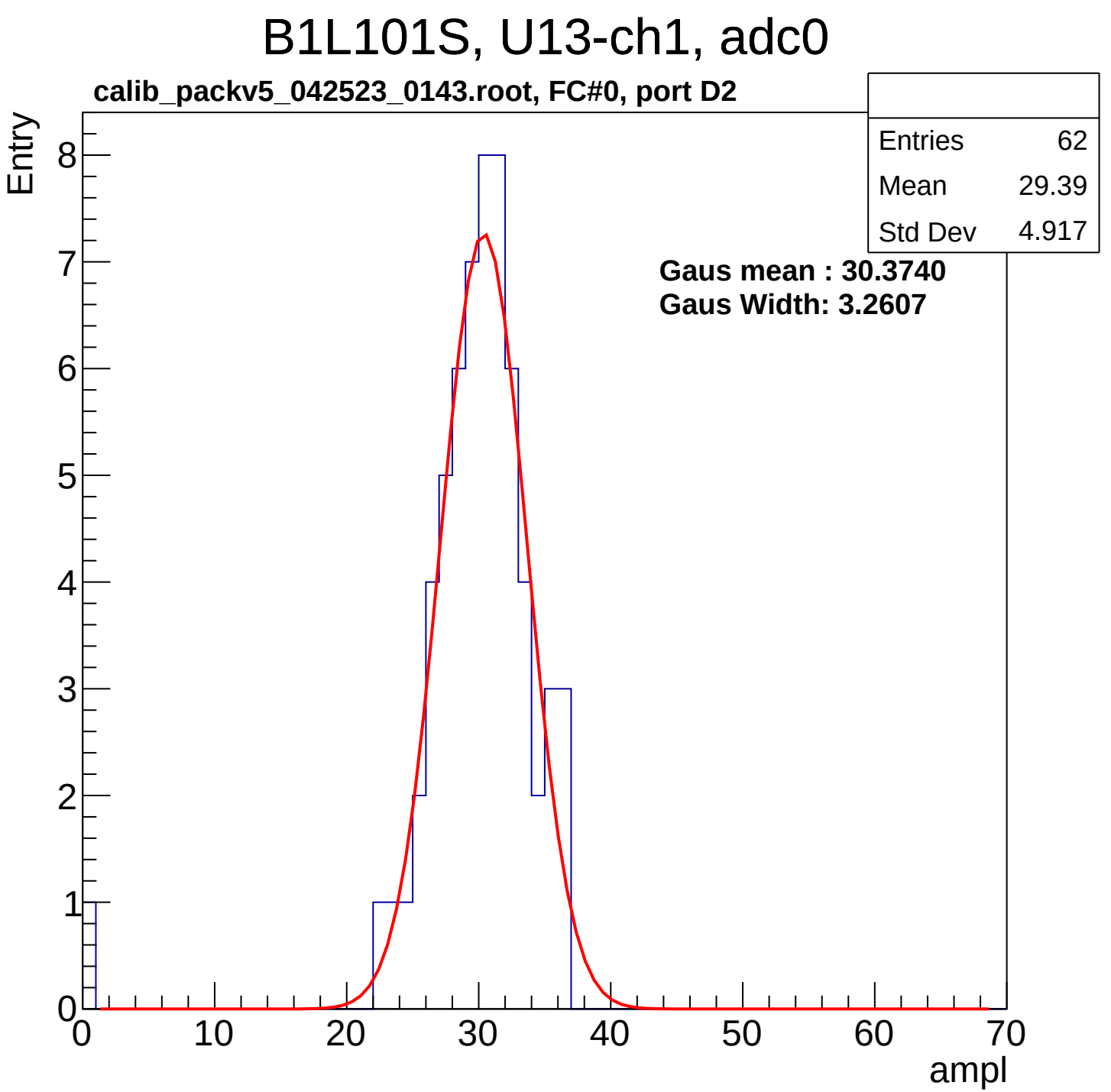
B1L101S, U13-ch1, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 29.39
Std Dev 4.917
Gaus mean : 30.3740
Gaus Width: 3.2607
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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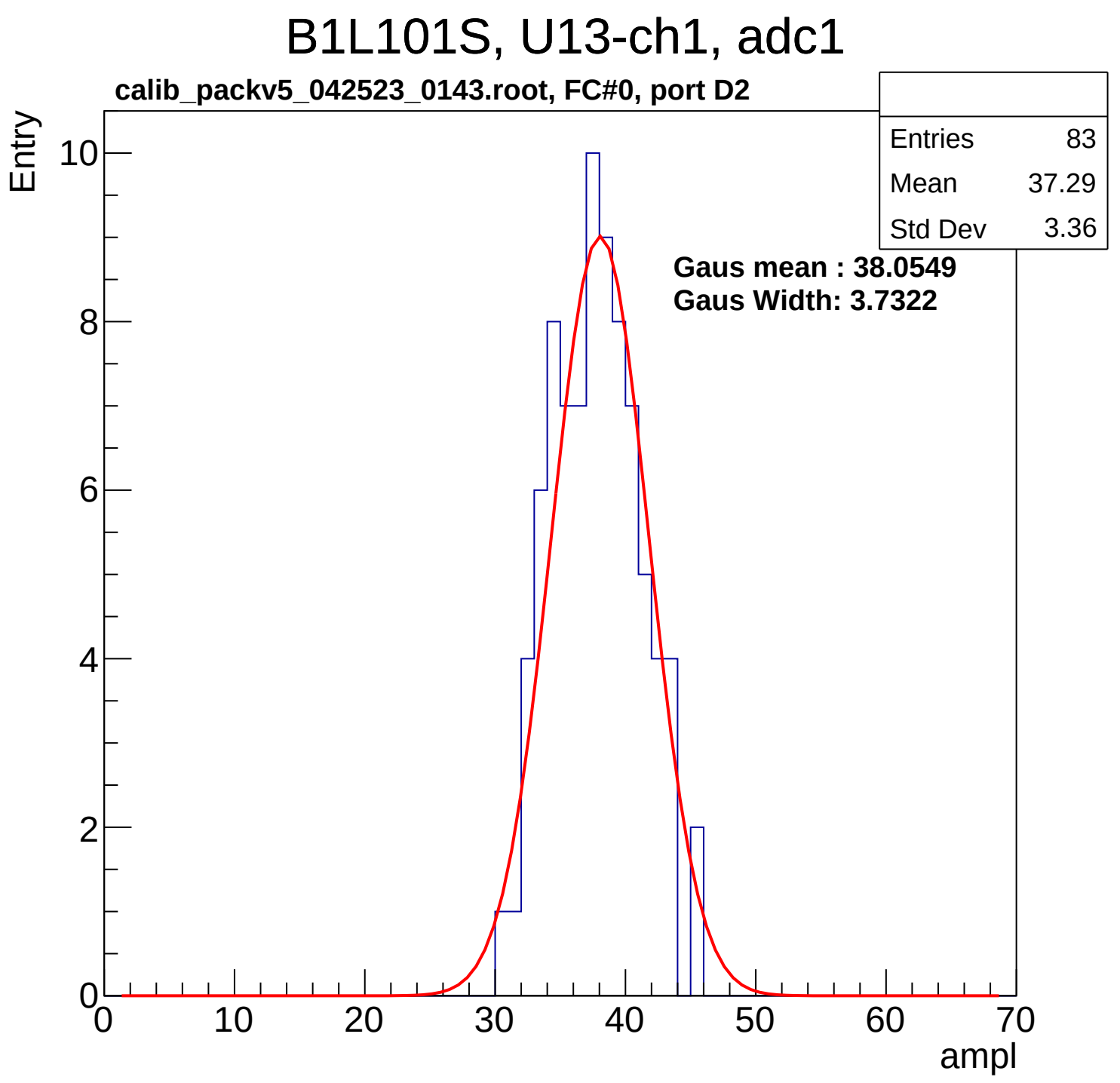
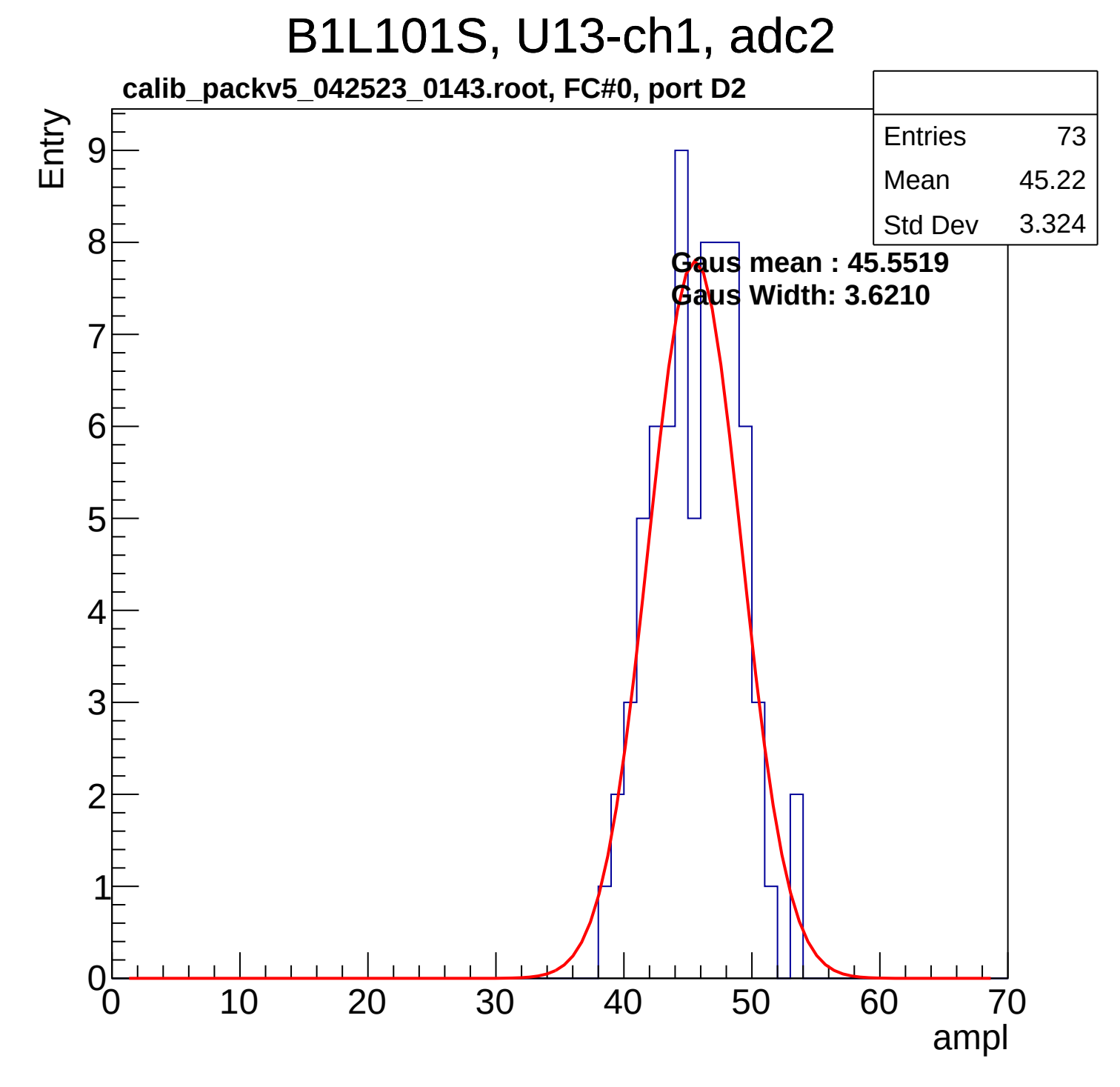

B1L101S, U13-ch1, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 83
Mean 37.29
Std Dev 3.36
Gaus mean : 38.0549
Gaus Width: 3.7322
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch1, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 45.22
Std Dev 3.324
Gaus mean : 45.5519
Gaus Width: 3.6210
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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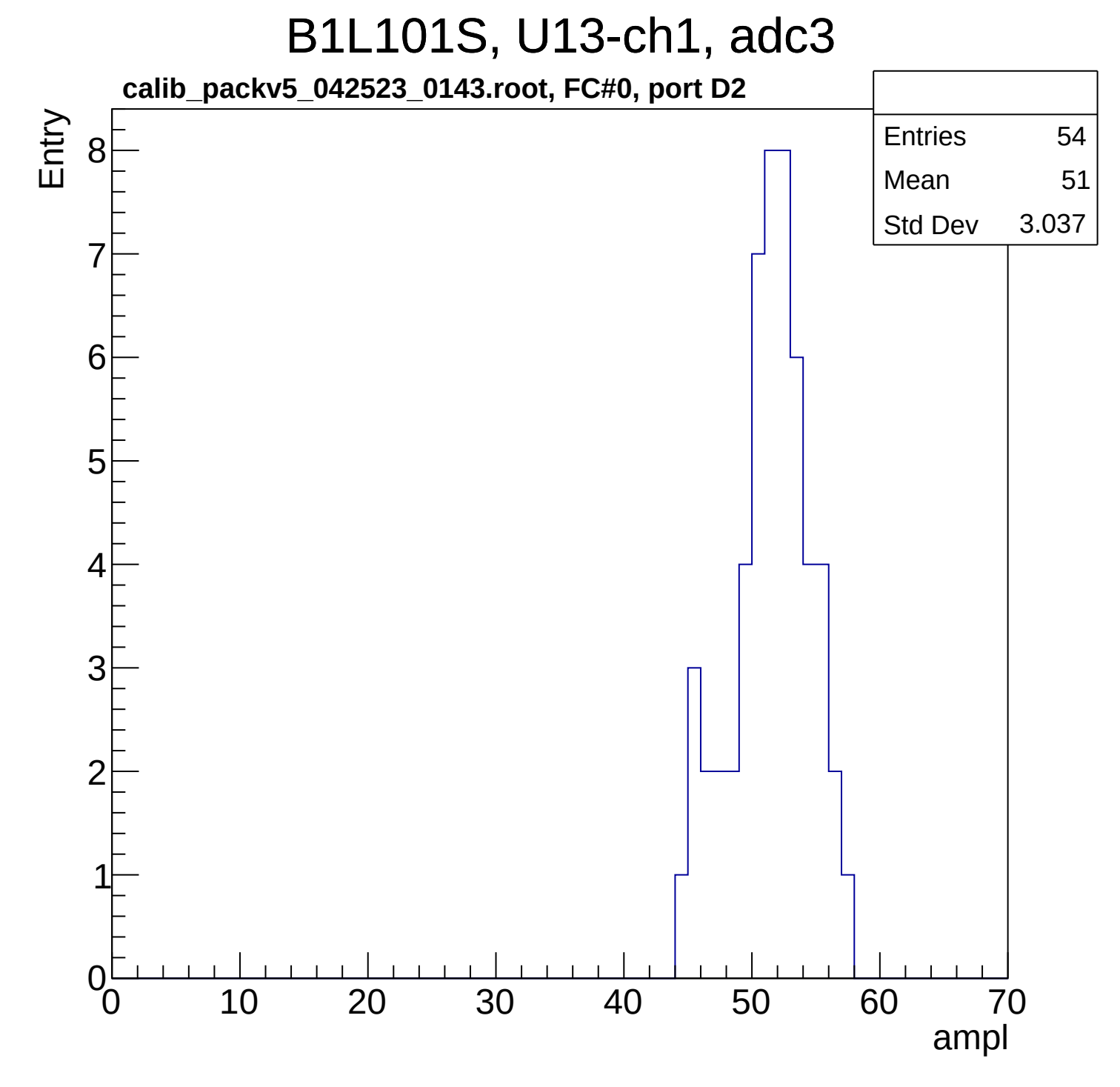
B1L101S, U13-ch1, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 51
Std Dev 3.037
Entry
ampl
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1
2
3
4
5
6
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0
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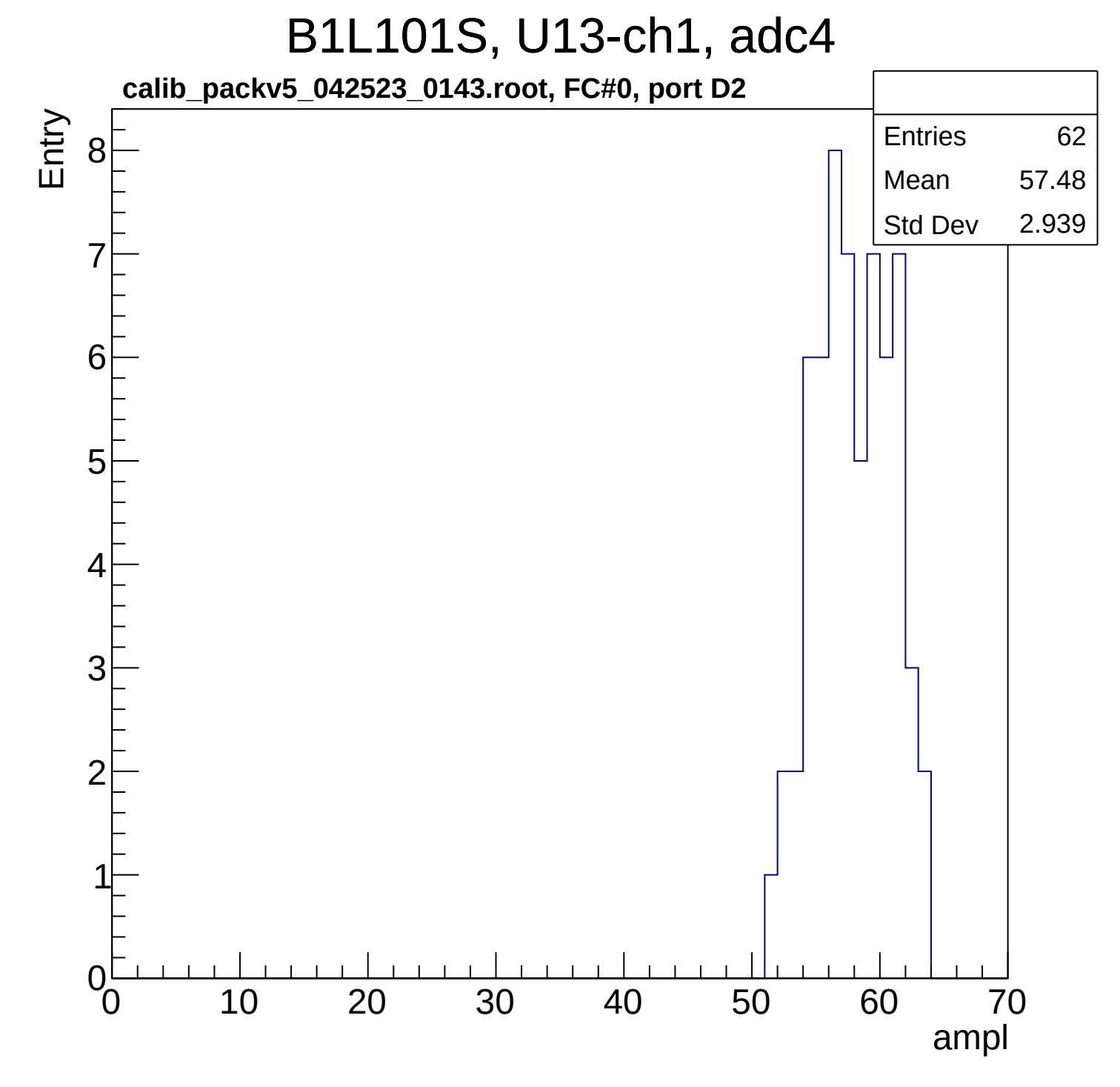
B1L101S, U13-ch1, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 57.48
Std Dev 2.939
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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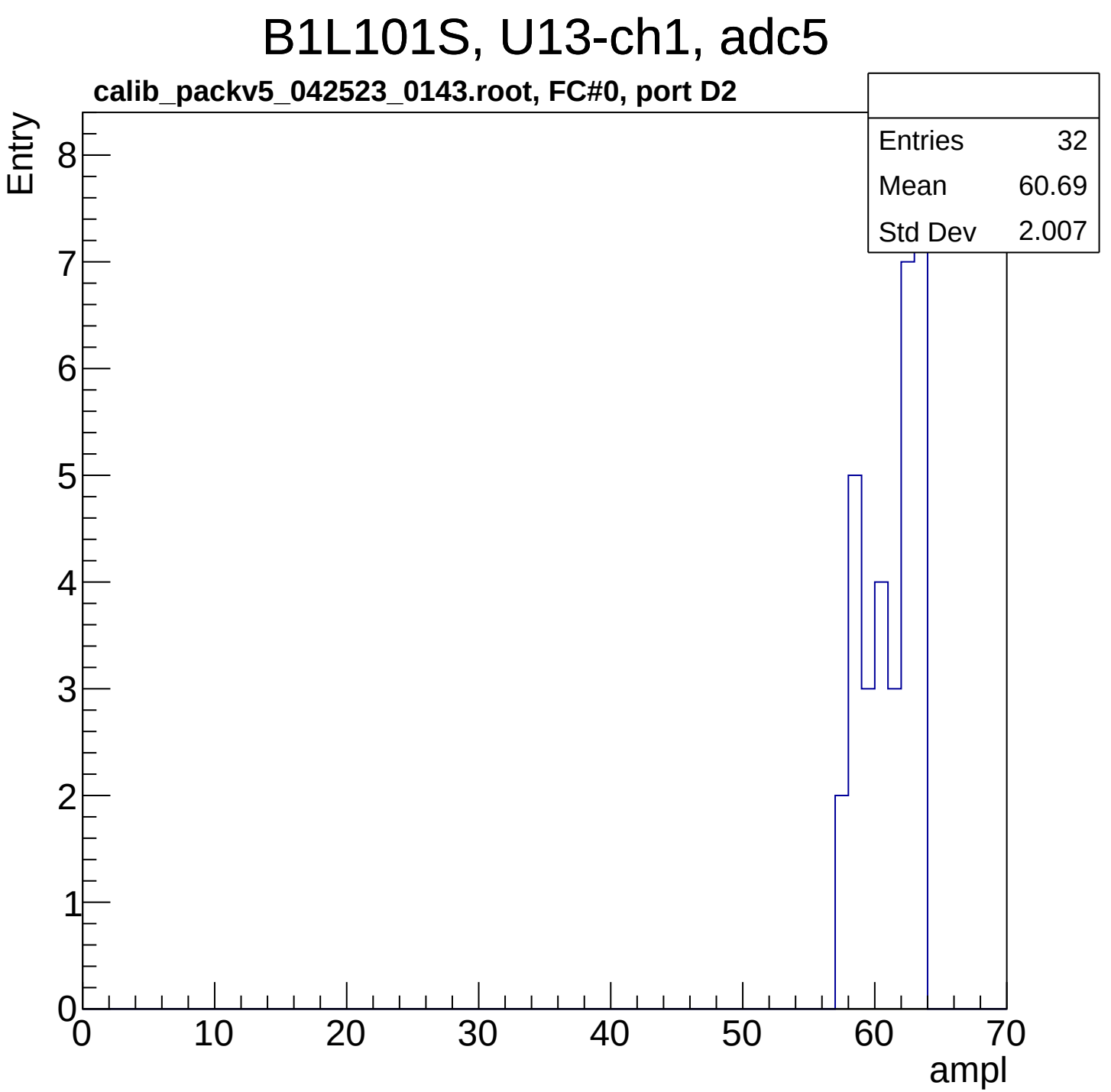
B1L101S, U13-ch1, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 32
Mean 60.69
Std Dev 2.007
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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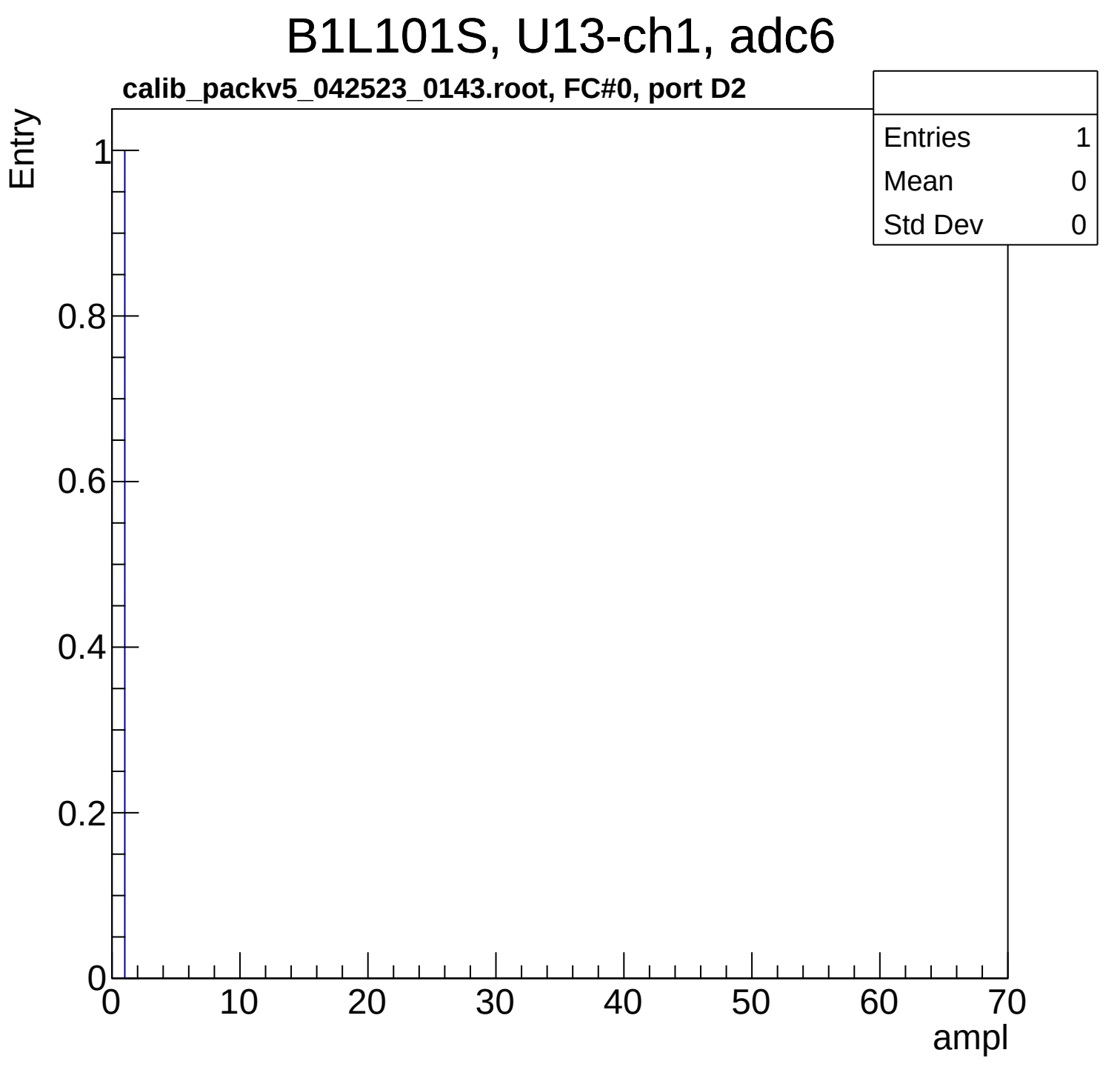
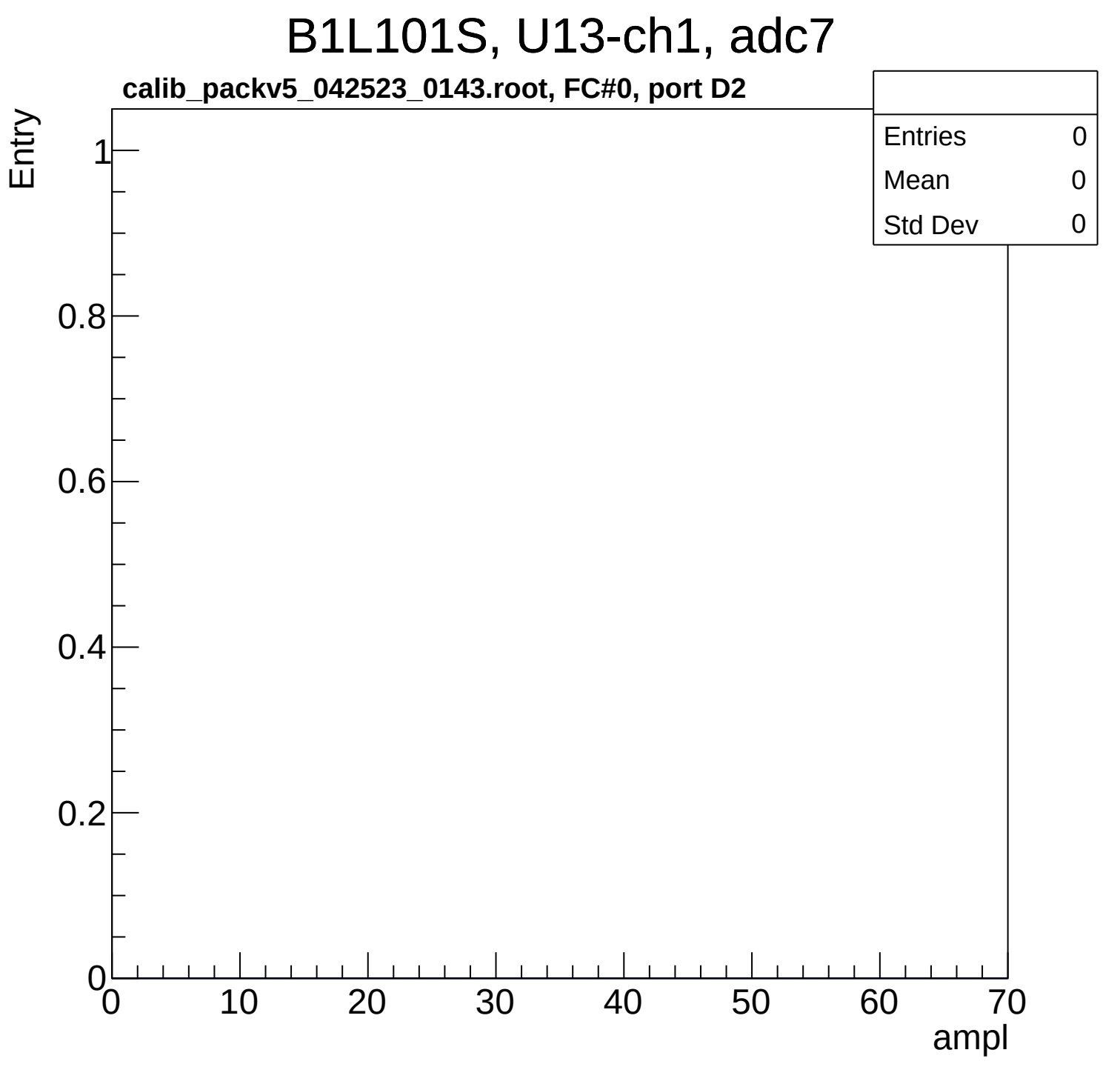
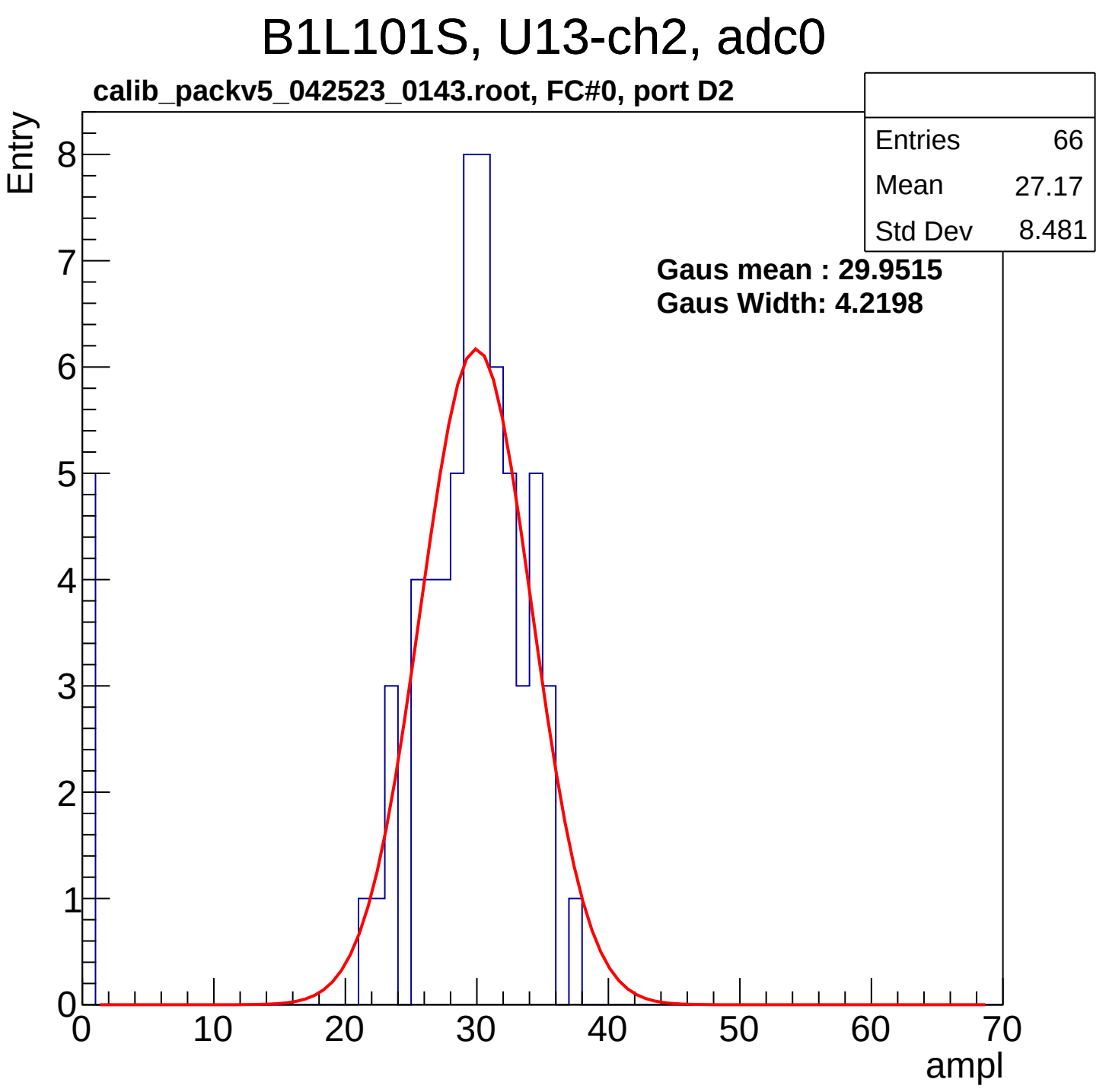

B1L101S, U13-ch1, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch1, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch2, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 27.17
Std Dev 8.481
Gaus mean : 29.9515
Gaus Width: 4.2198
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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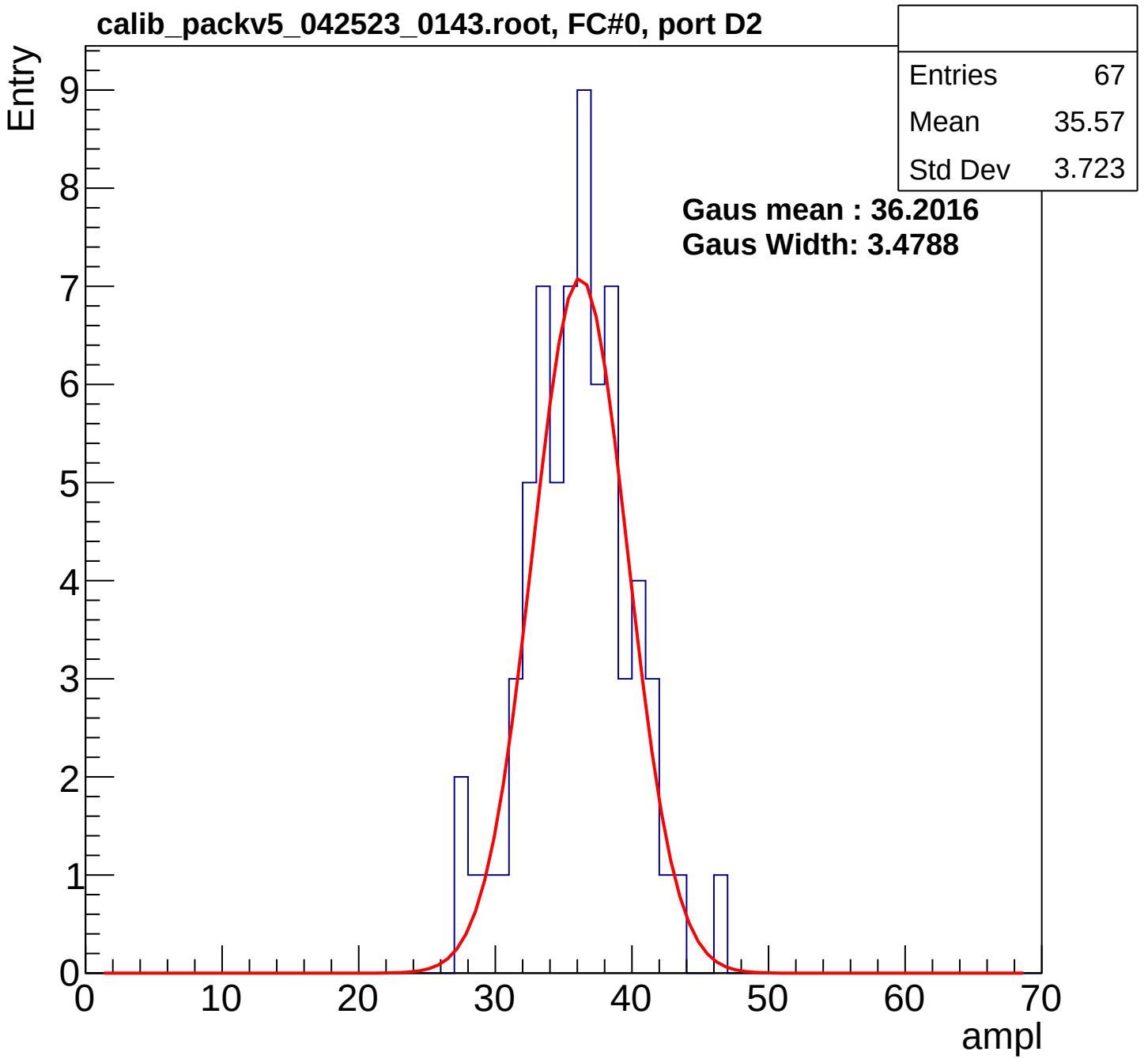
B1L101S, U13-ch2, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 35.57
Std Dev 3.723
Gaus mean : 36.2016
Gaus Width: 3.4788
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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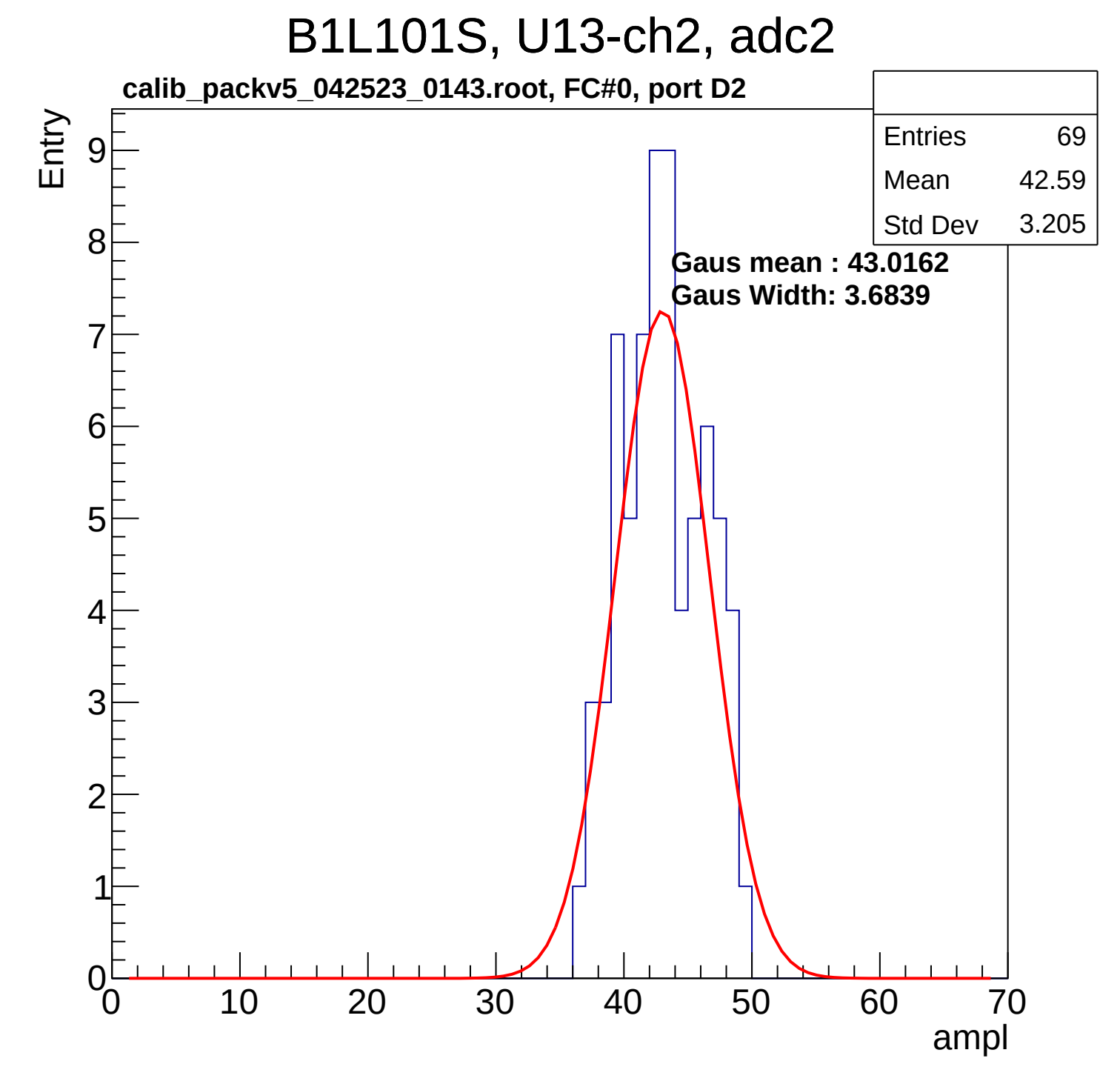
B1L101S, U13-ch2, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 42.59
Std Dev 3.205
Gaus mean : 43.0162
Gaus Width: 3.6839
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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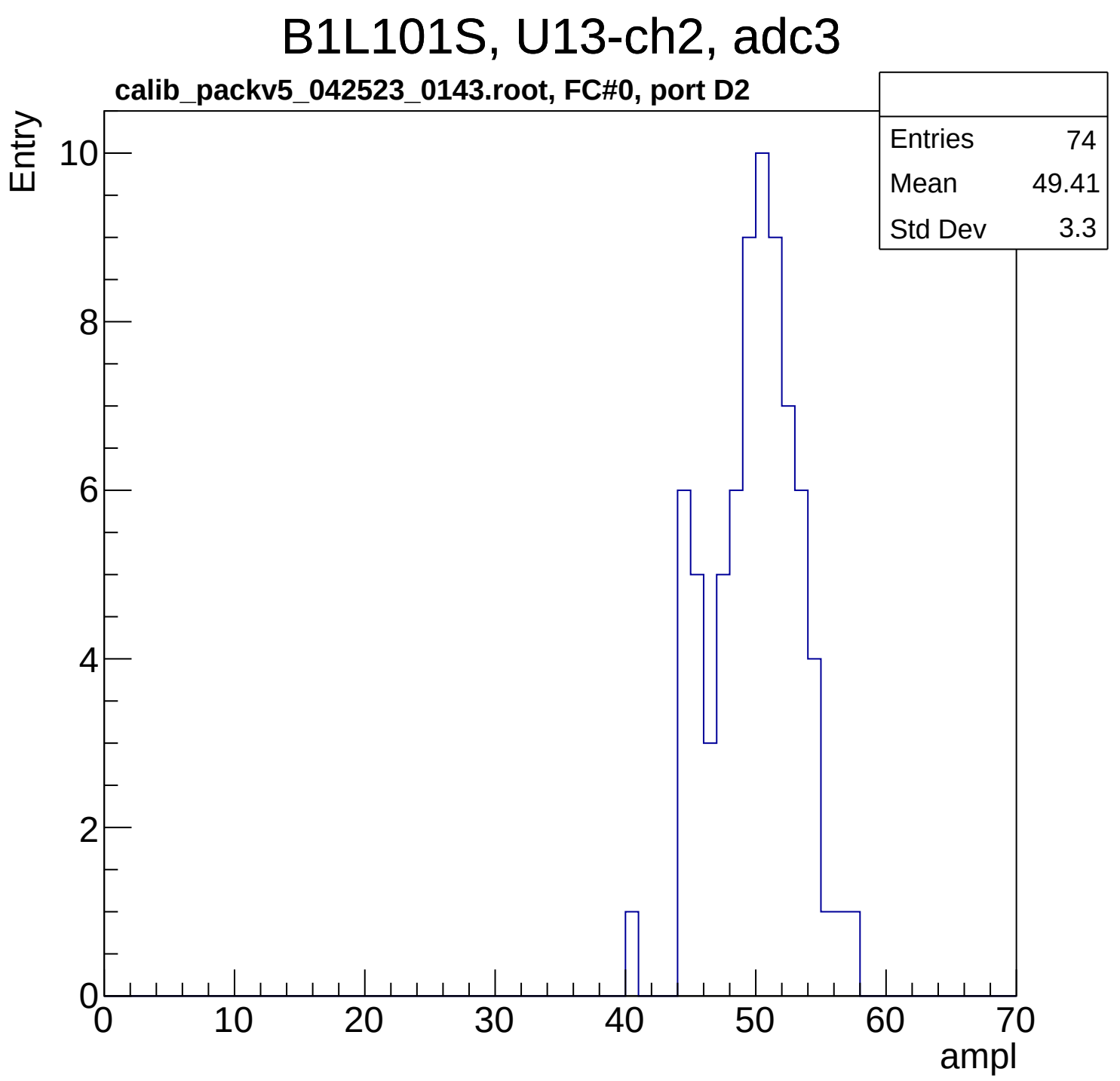
B1L101S, U13-ch2, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 49.41
Std Dev 3.3
Entry
0
2
4
6
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10
0
10
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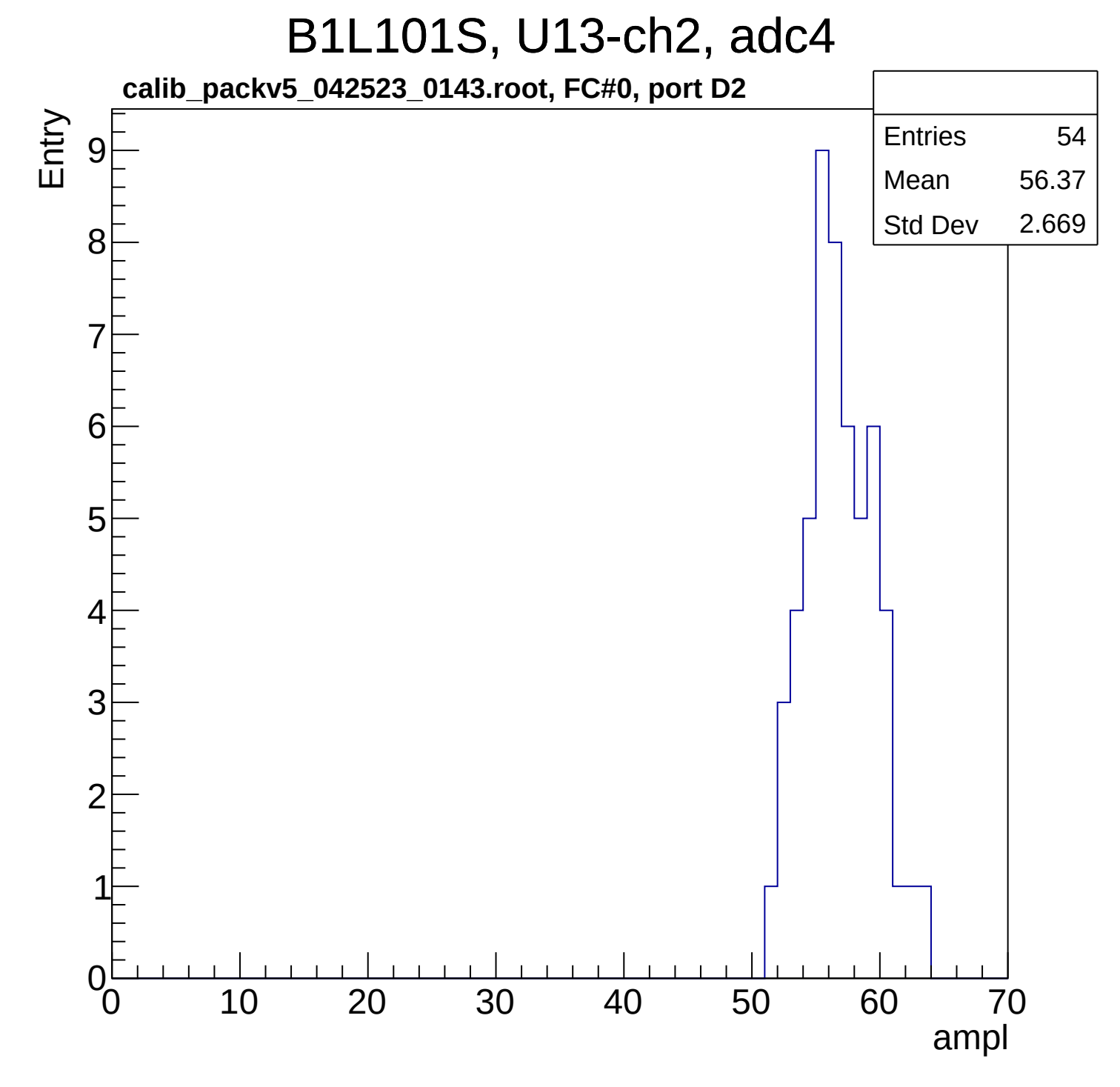
30
40
50
60
70
ampl


B1L101S, U13-ch2, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 56.37
Std Dev 2.669
Entry
ampl
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2
3
4
5
6
7
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9
0
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60
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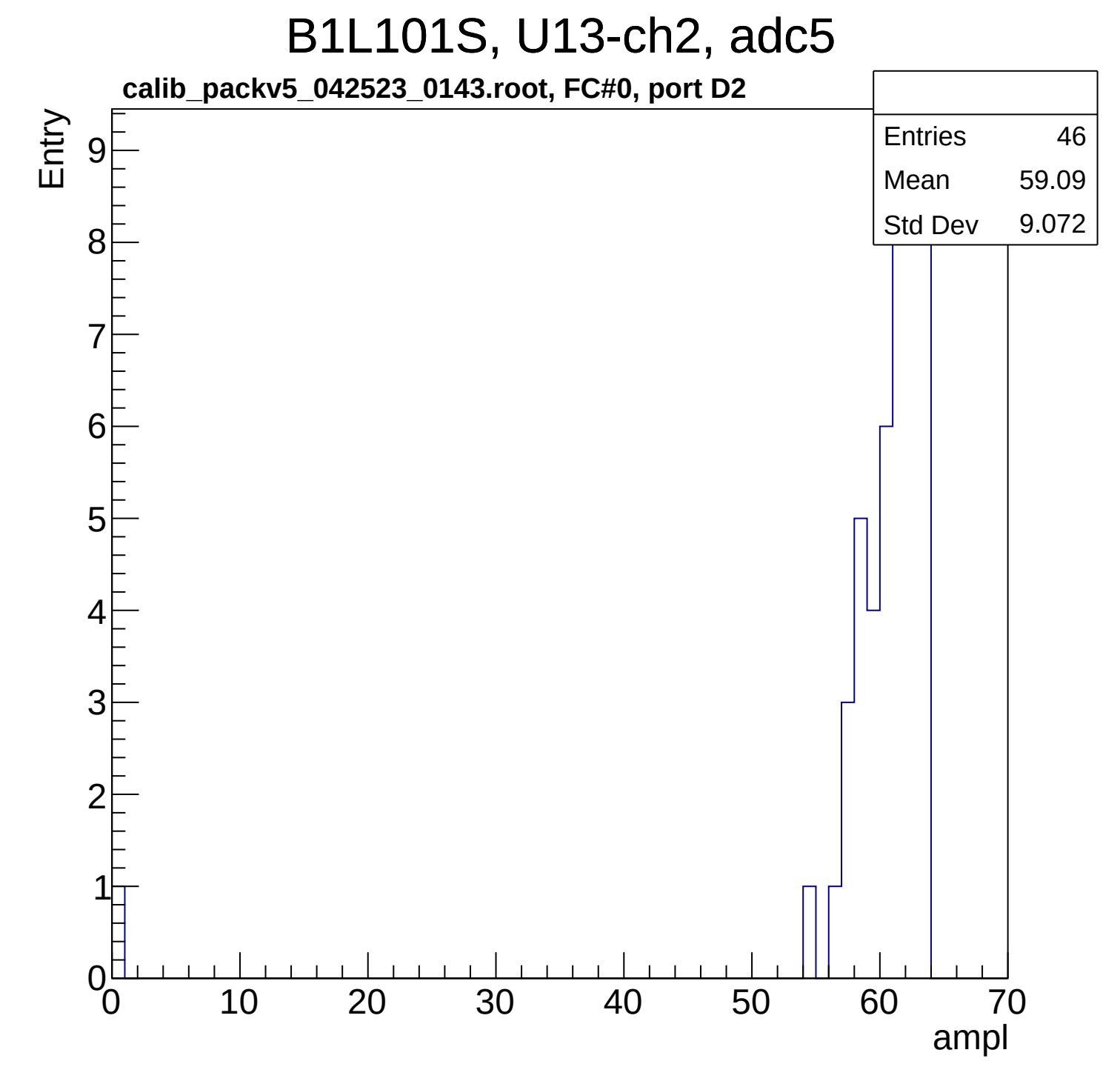
B1L101S, U13-ch2, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 59.09
Std Dev 9.072
Entry
ampl
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3
4
5
6
7
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9
0
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60
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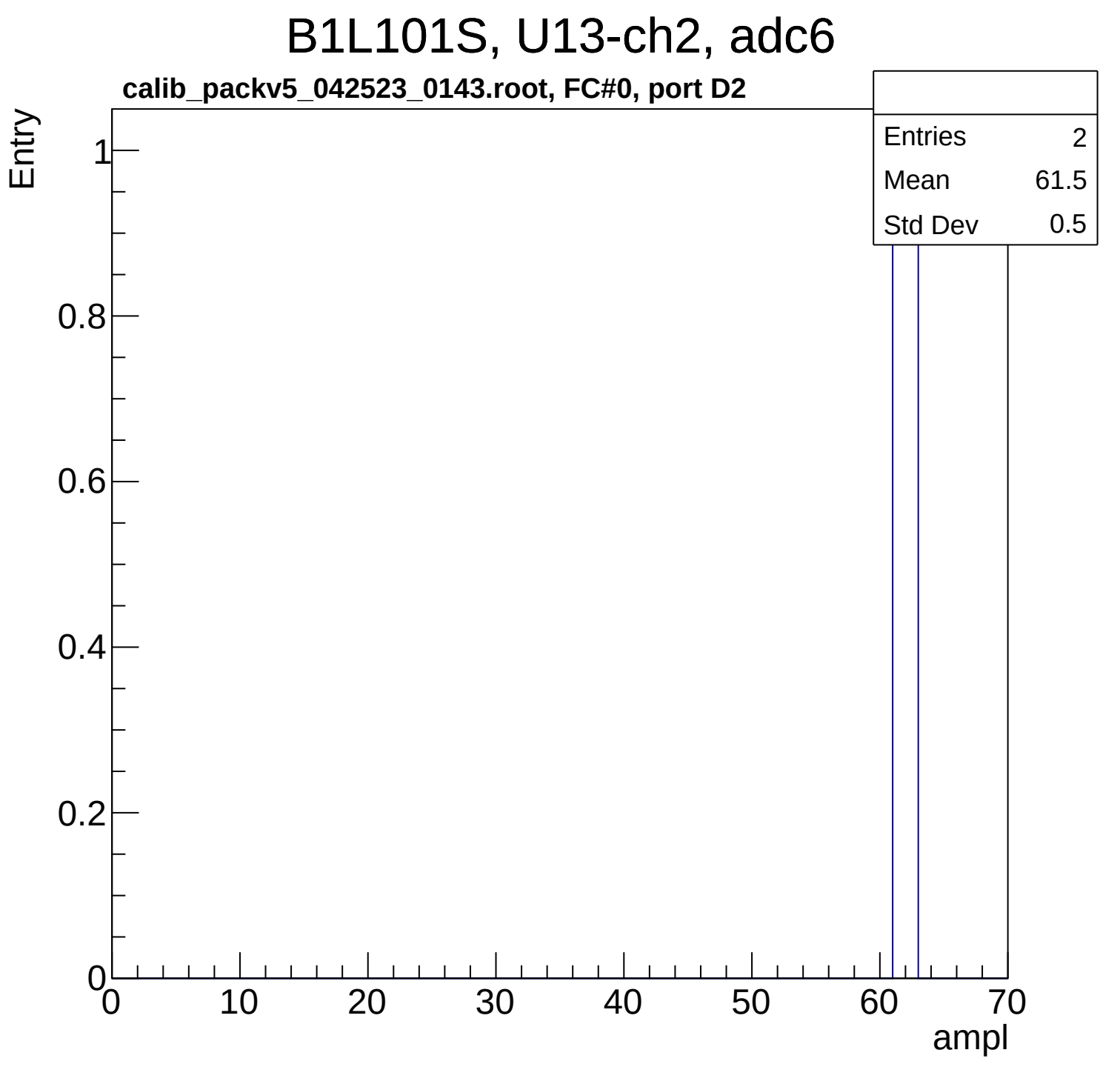
B1L101S, U13-ch2, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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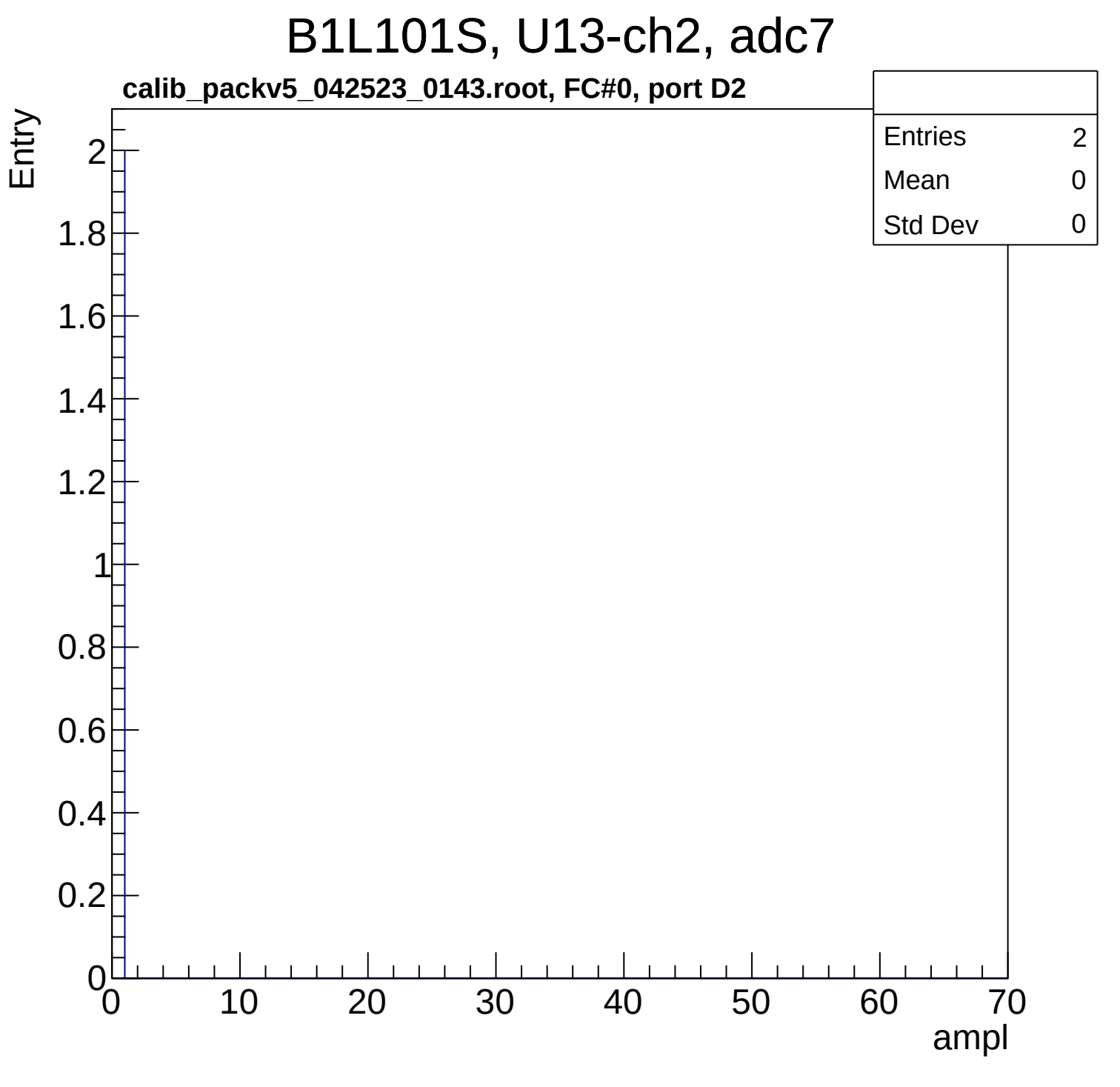
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch2, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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0
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60
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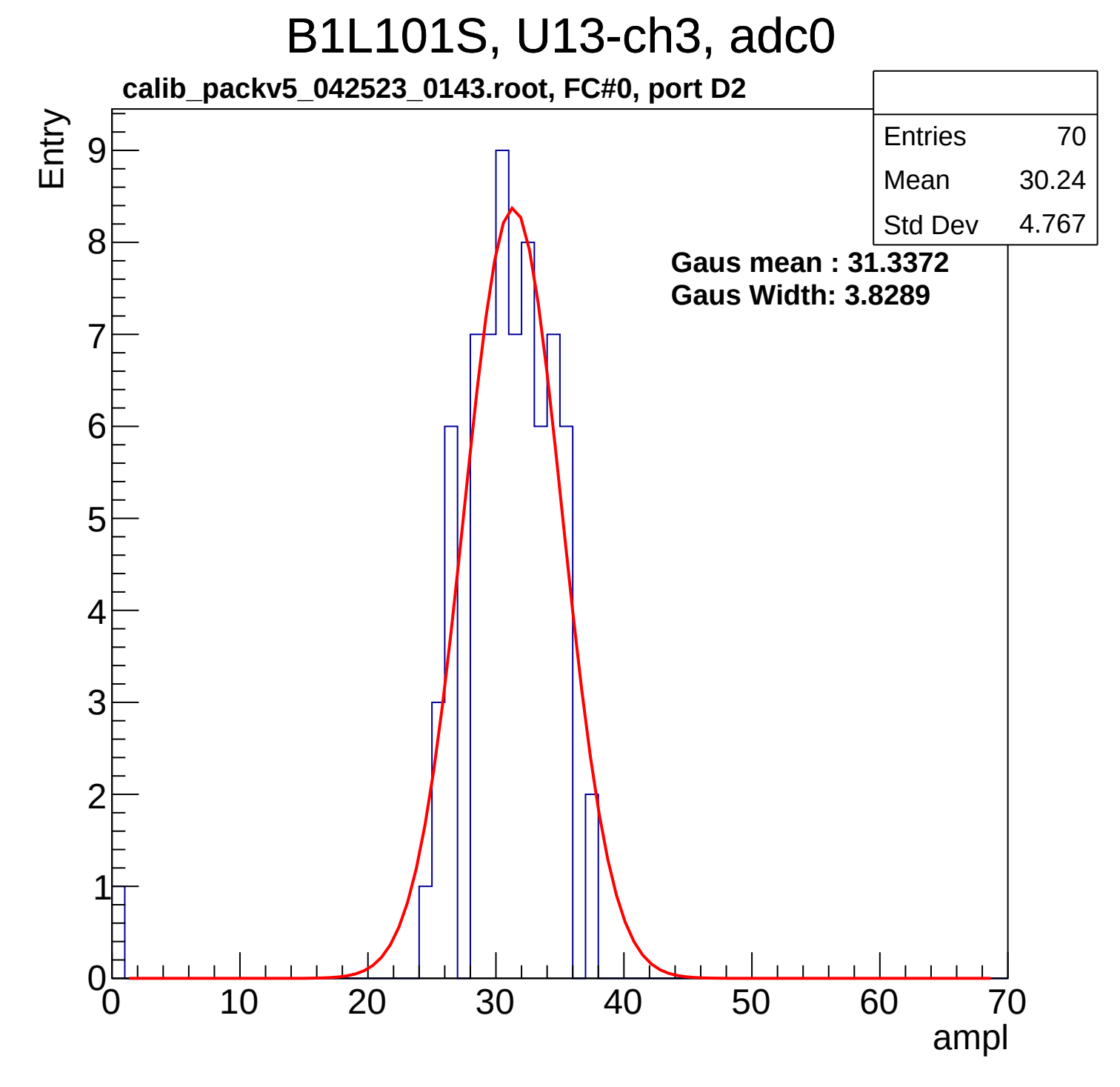
B1L101S, U13-ch3, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 30.24
Std Dev 4.767
Gaus mean : 31.3372
Gaus Width: 3.8289
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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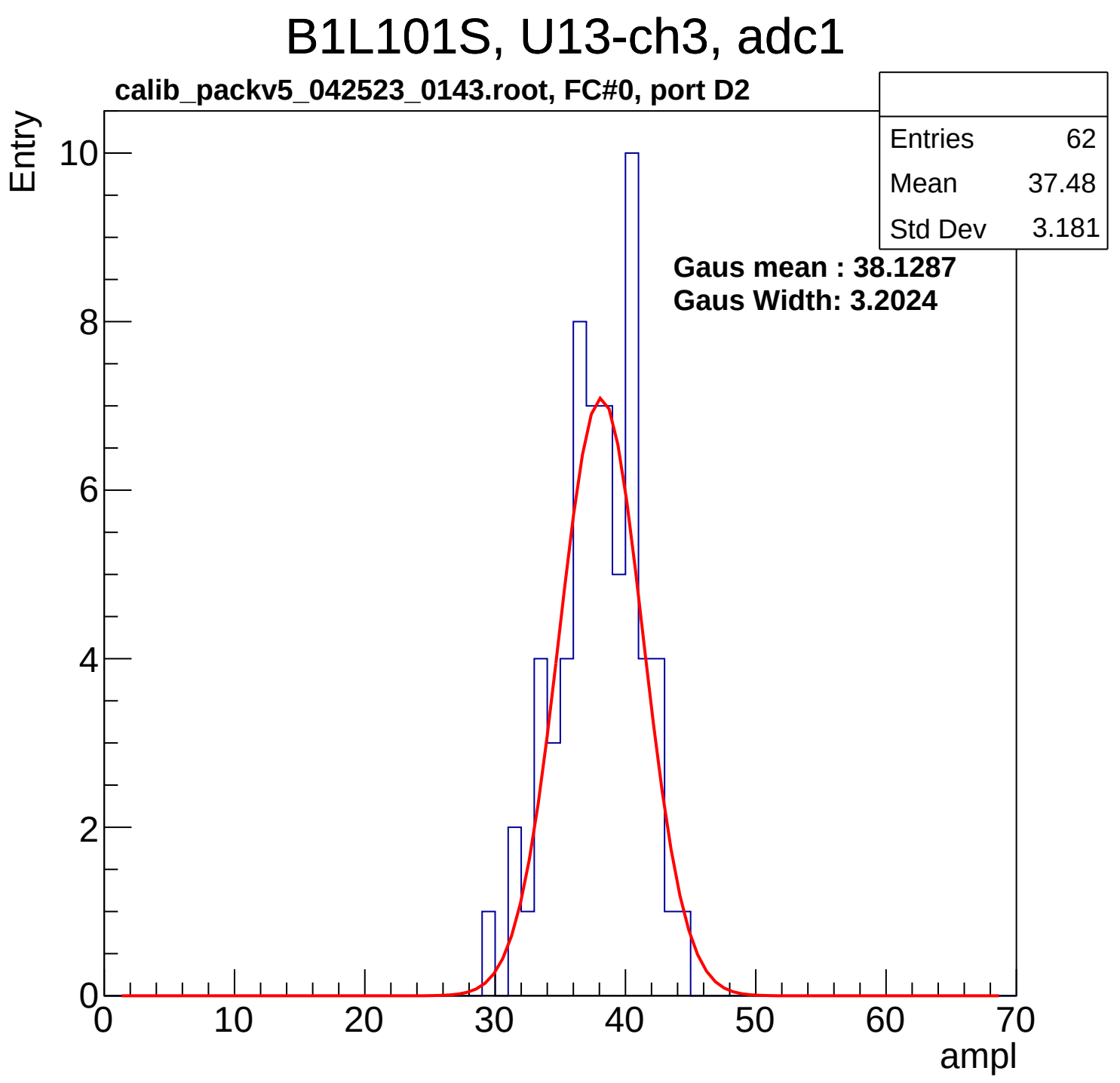
B1L101S, U13-ch3, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 37.48
Std Dev 3.181
Gaus mean : 38.1287
Gaus Width: 3.2024
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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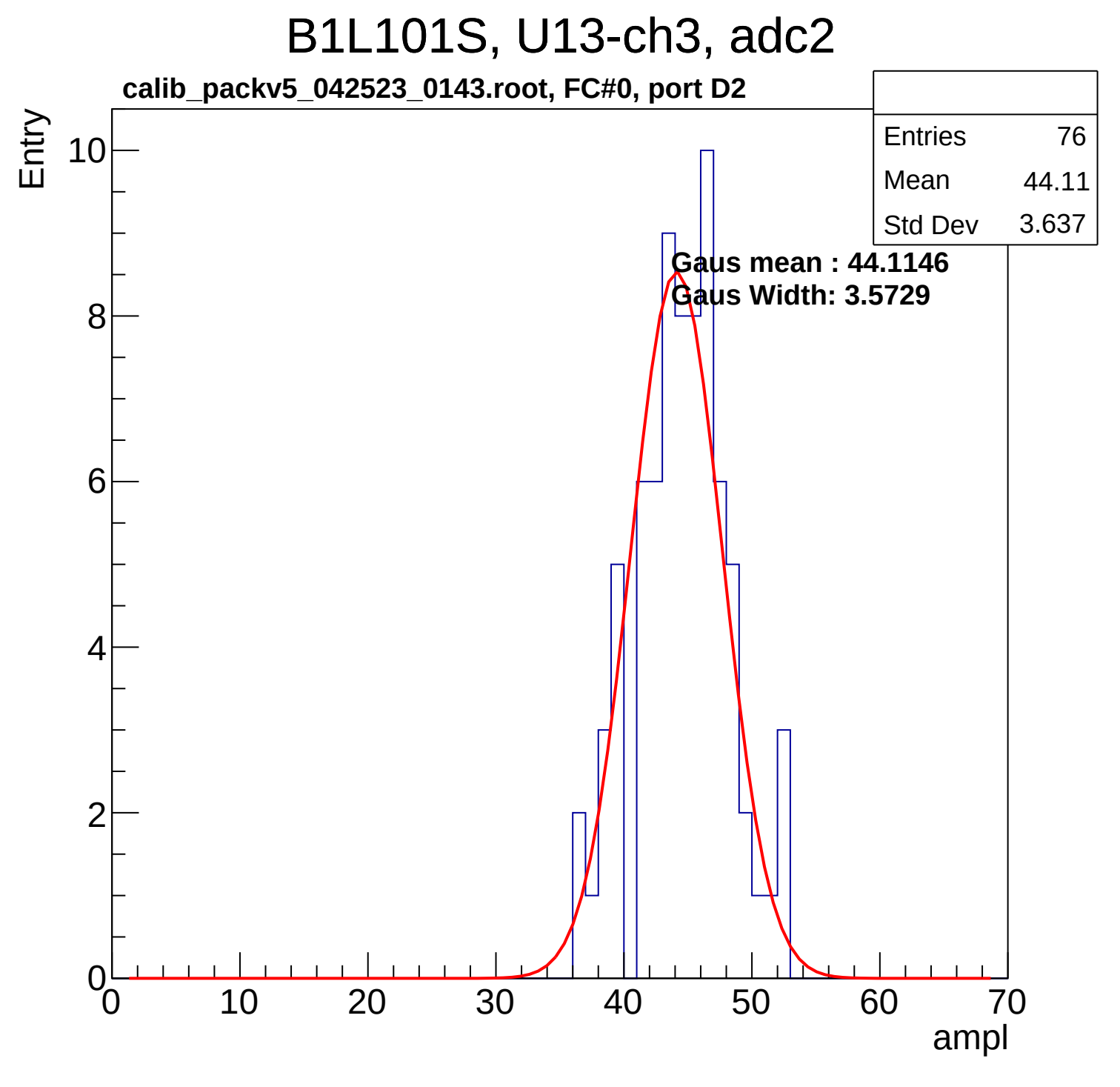
B1L101S, U13-ch3, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 44.11
Std Dev 3.637
Gaus mean : 44.1146
Gaus Width: 3.5729
Entry
ampl
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4
6
8
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0
10
20
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40
50
60
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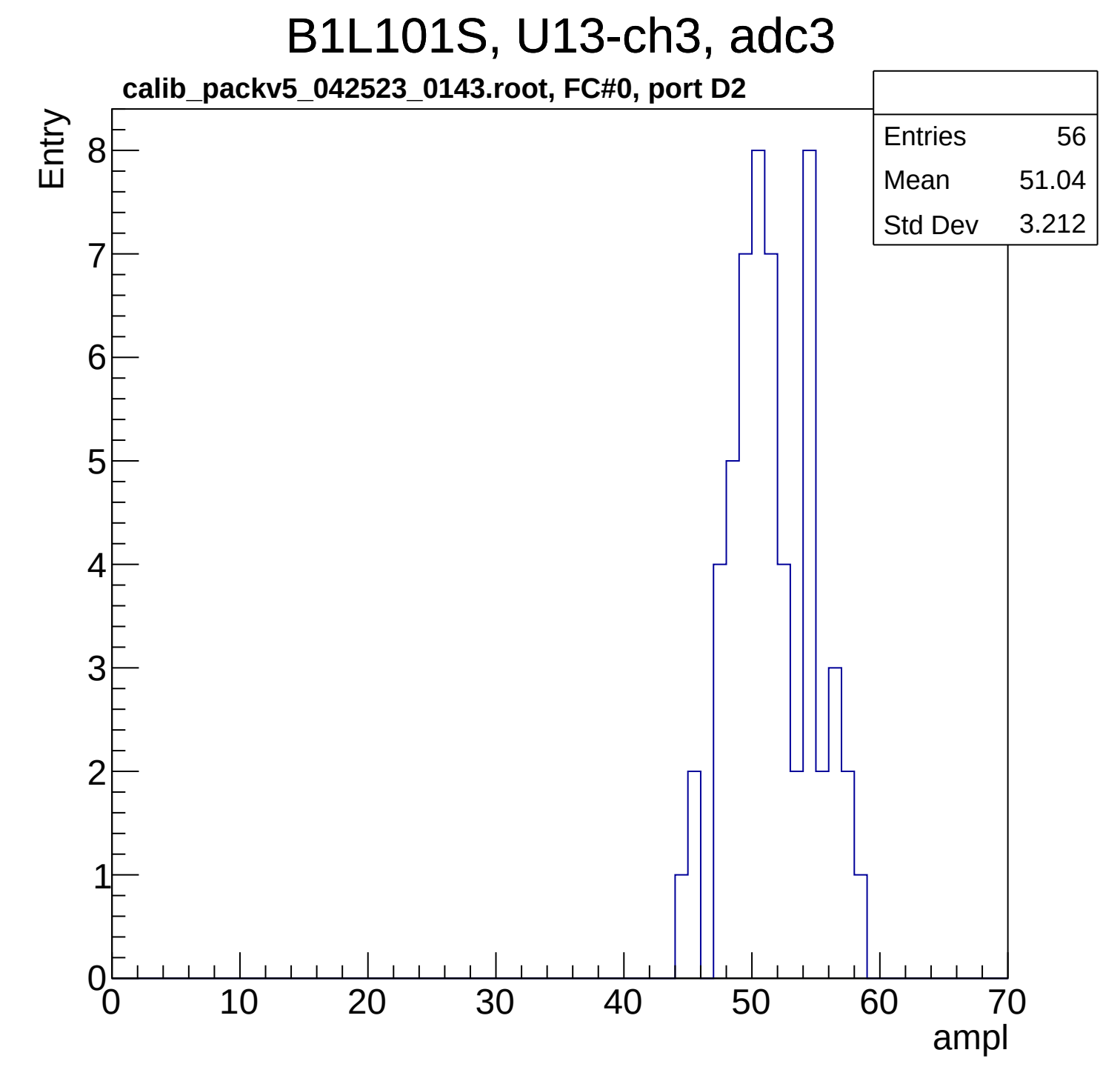
B1L101S, U13-ch3, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 51.04
Std Dev 3.212
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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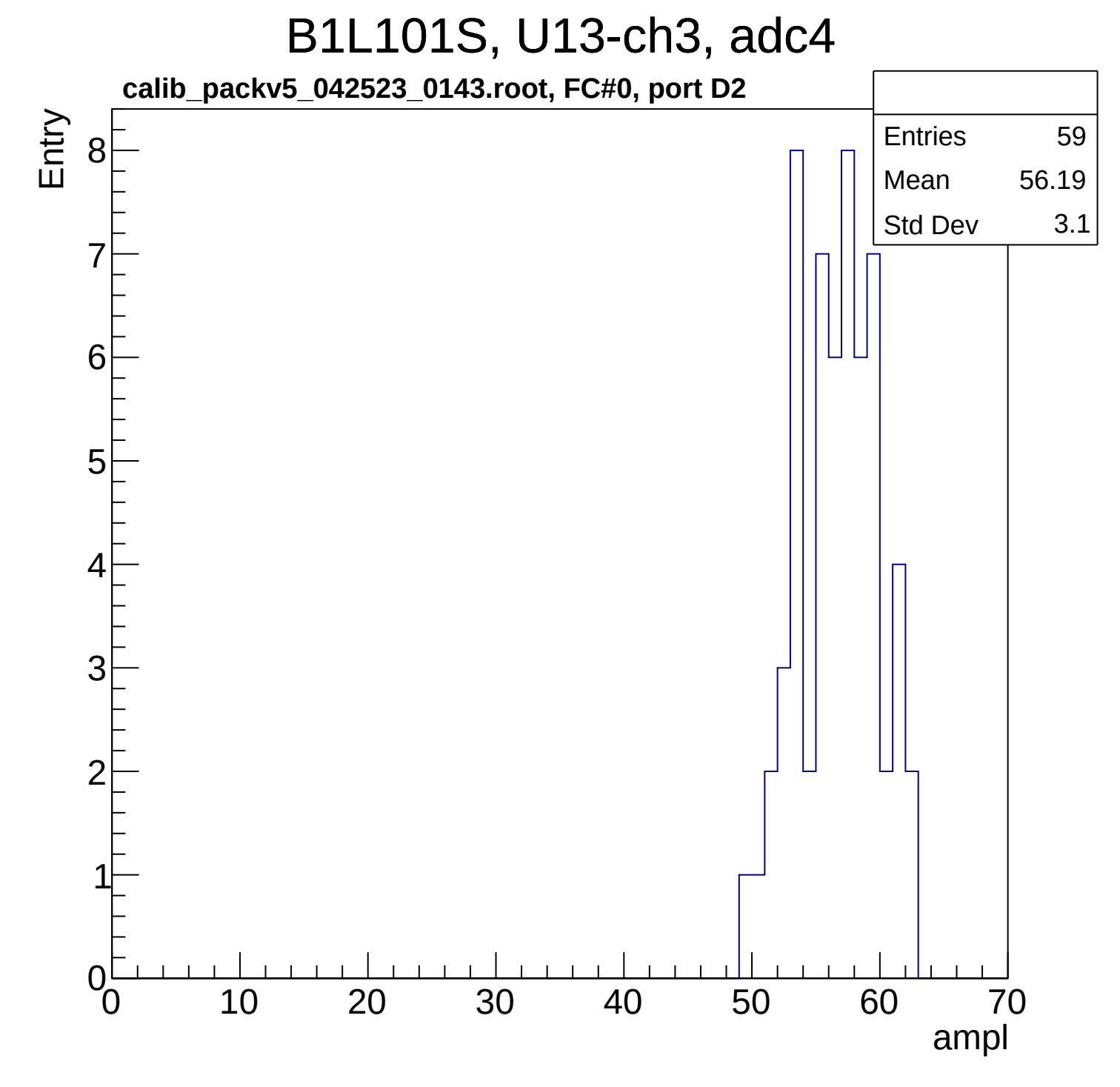
B1L101S, U13-ch3, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 56.19
Std Dev 3.1
Entry
ampl
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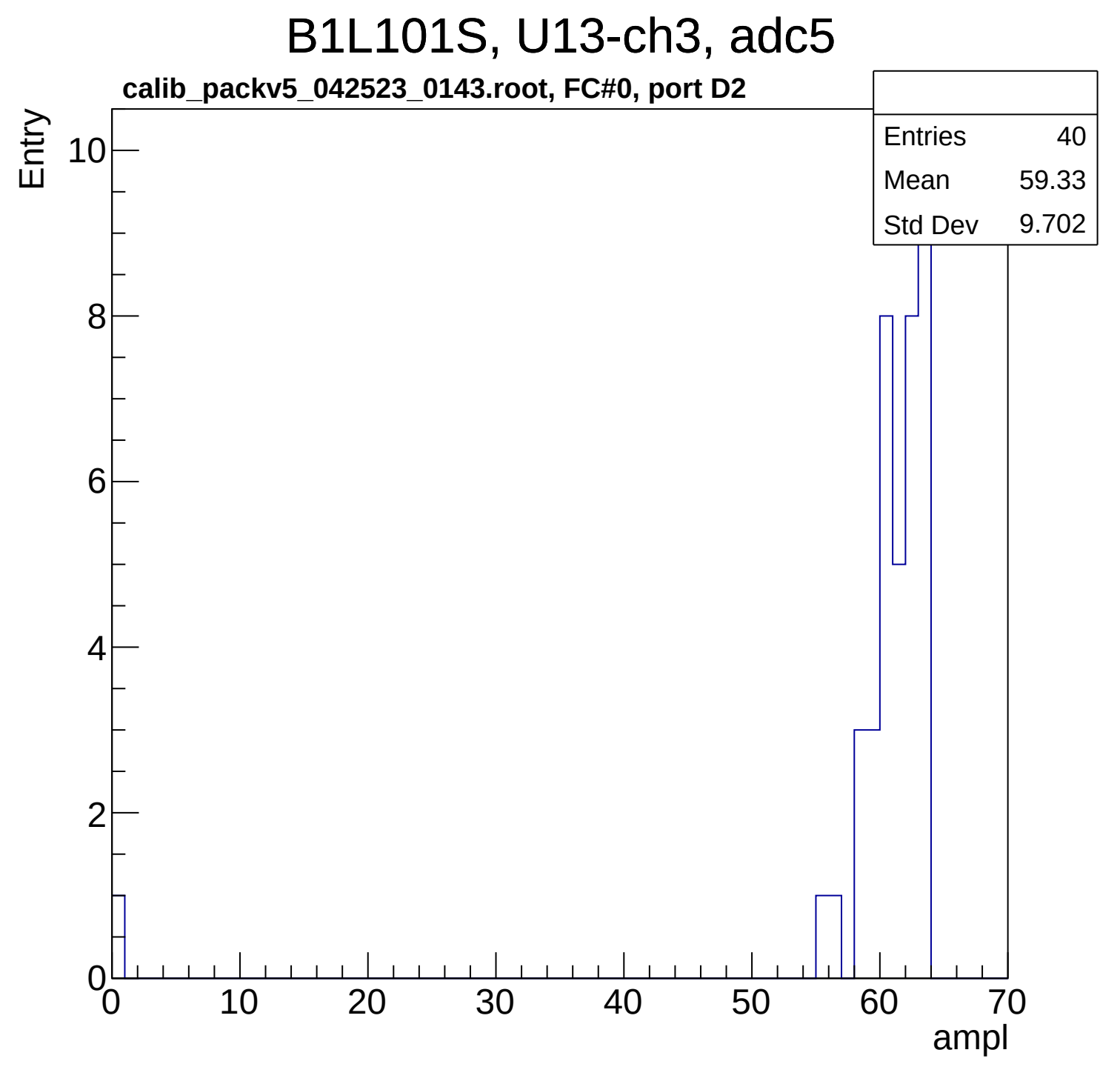
B1L101S, U13-ch3, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 59.33
Std Dev 9.702
Entry
ampl
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4
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0
10
20
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40
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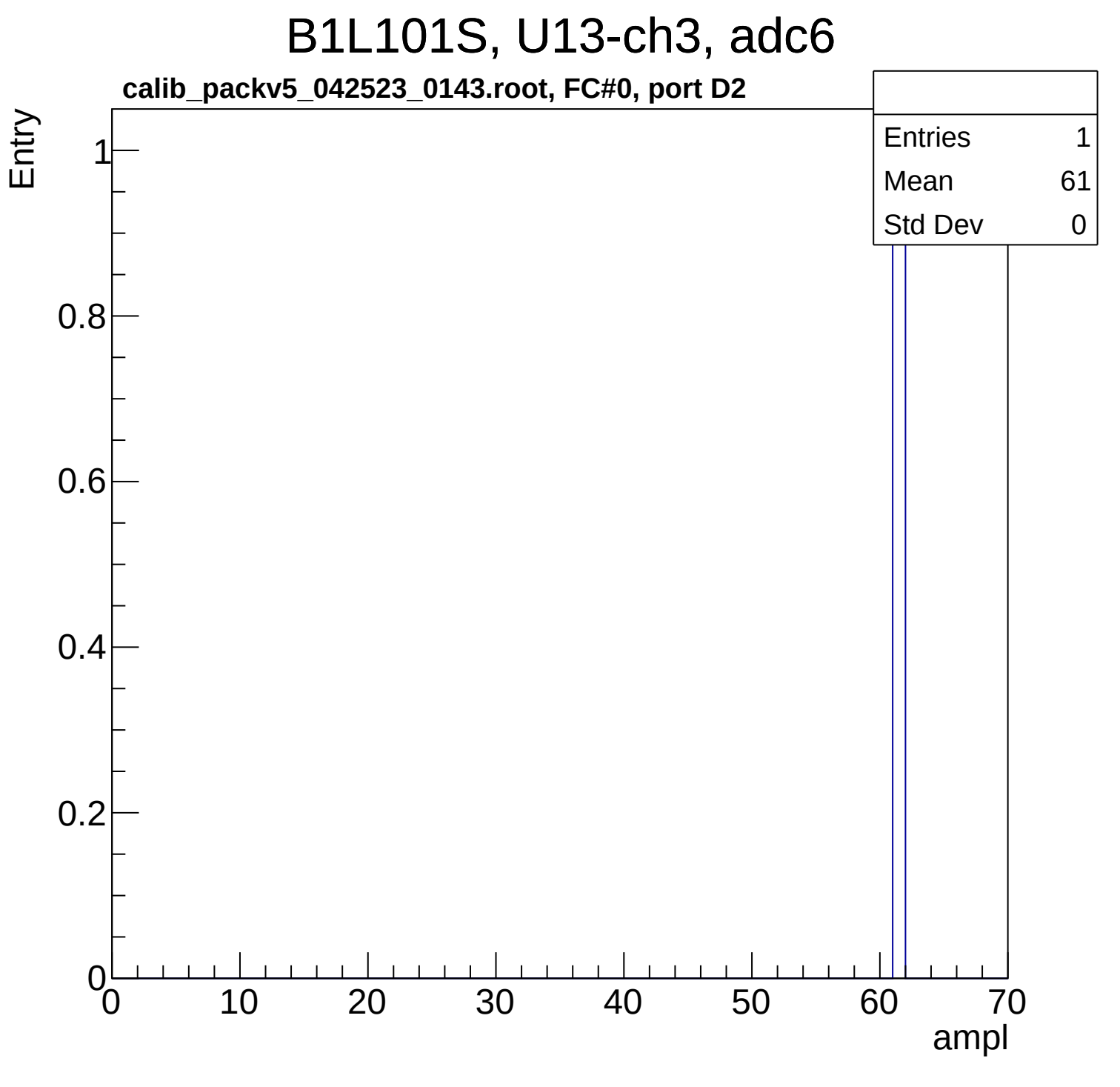
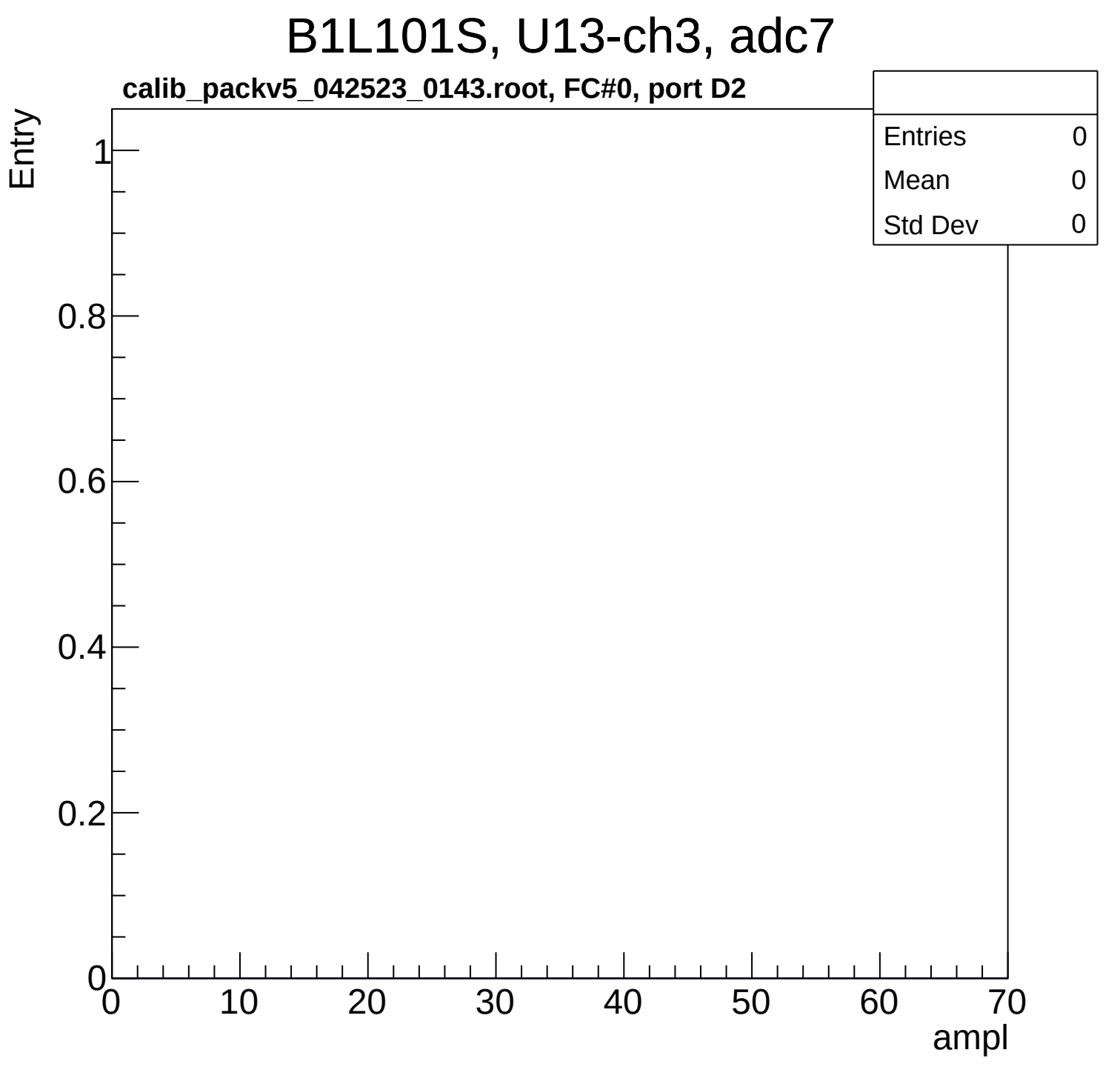
60
70


B1L101S, U13-ch3, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch3, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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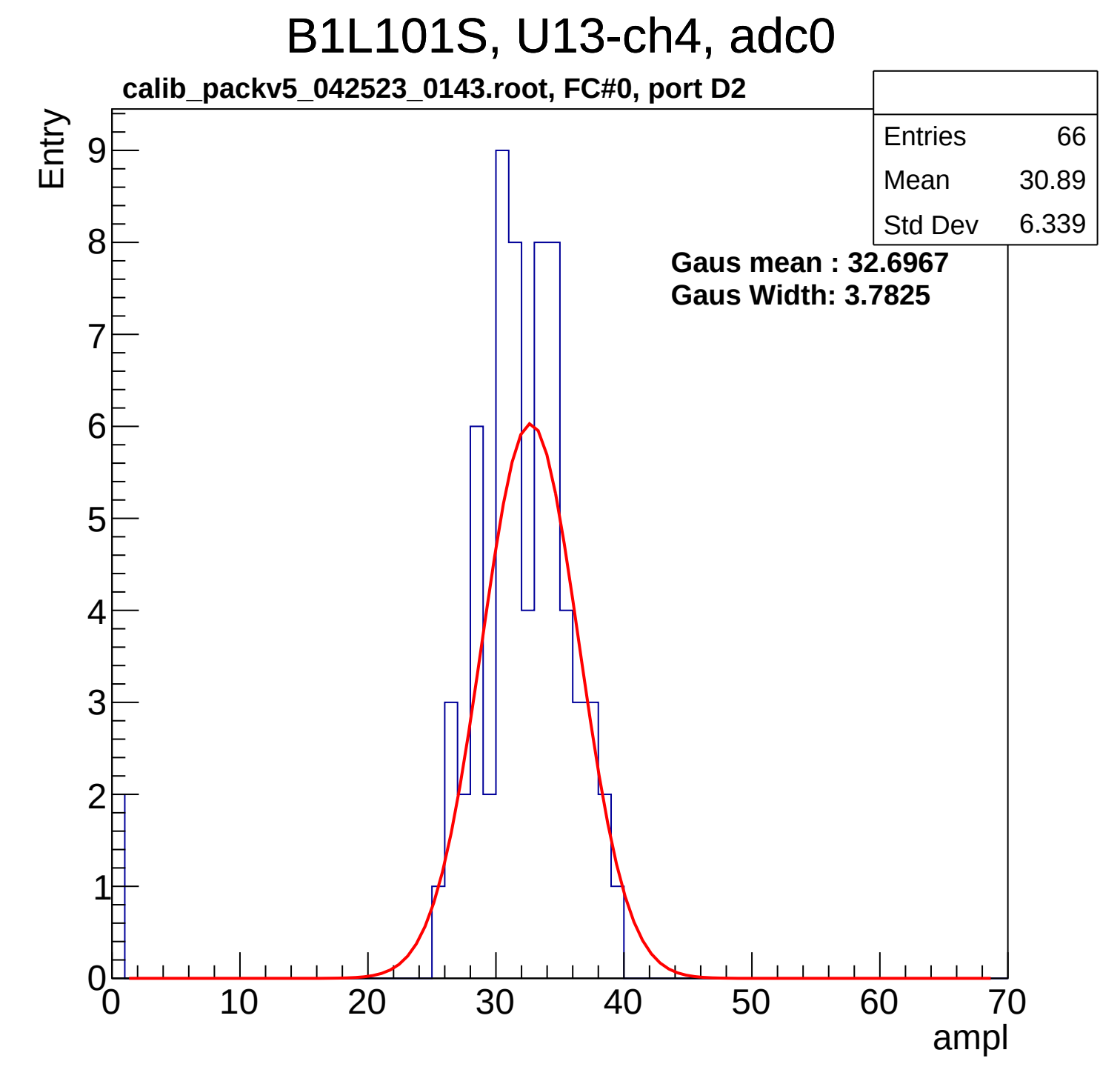
ampl


B1L101S, U13-ch4, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 30.89
Std Dev 6.339
Gaus mean : 32.6967
Gaus Width: 3.7825
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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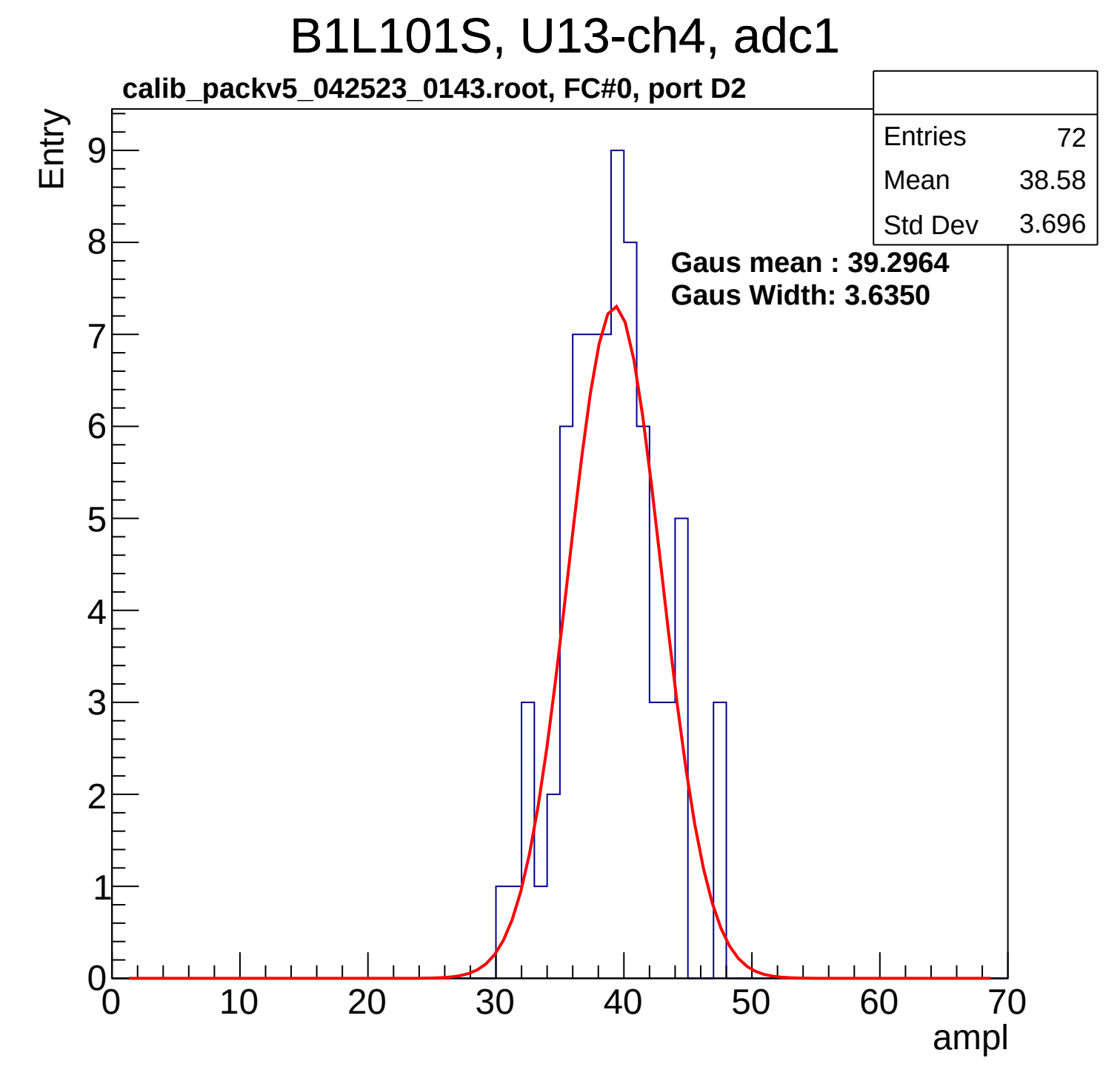
B1L101S, U13-ch4, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 38.58
Std Dev 3.696
Gaus mean : 39.2964
Gaus Width: 3.6350
Entry
ampl
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2
3
4
5
6
7
8
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0
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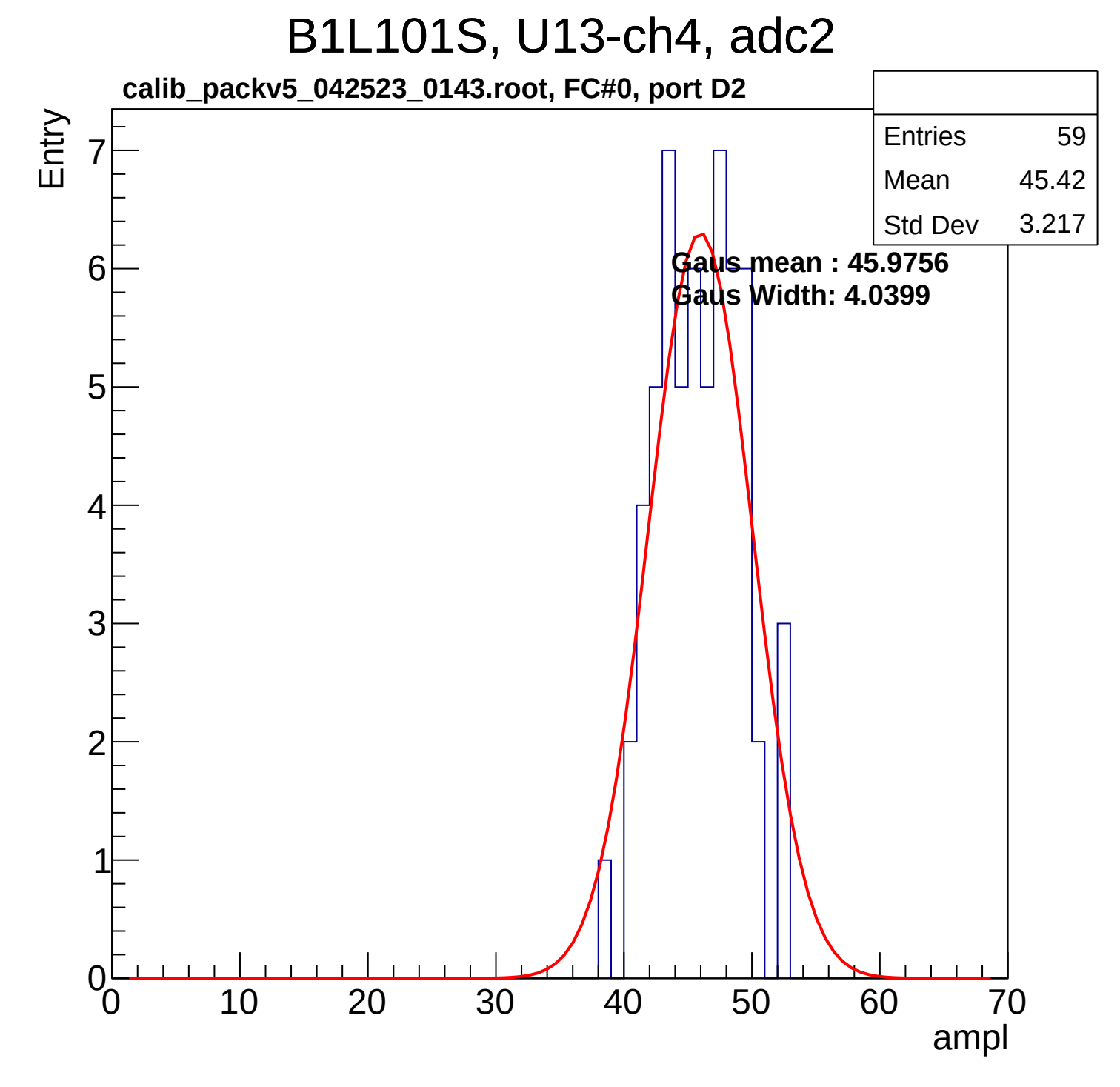
B1L101S, U13-ch4, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 45.42
Std Dev 3.217
Gaus mean : 45.9756
Gaus Width: 4.0399
Entry
ampl
0
1
2
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4
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6
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0
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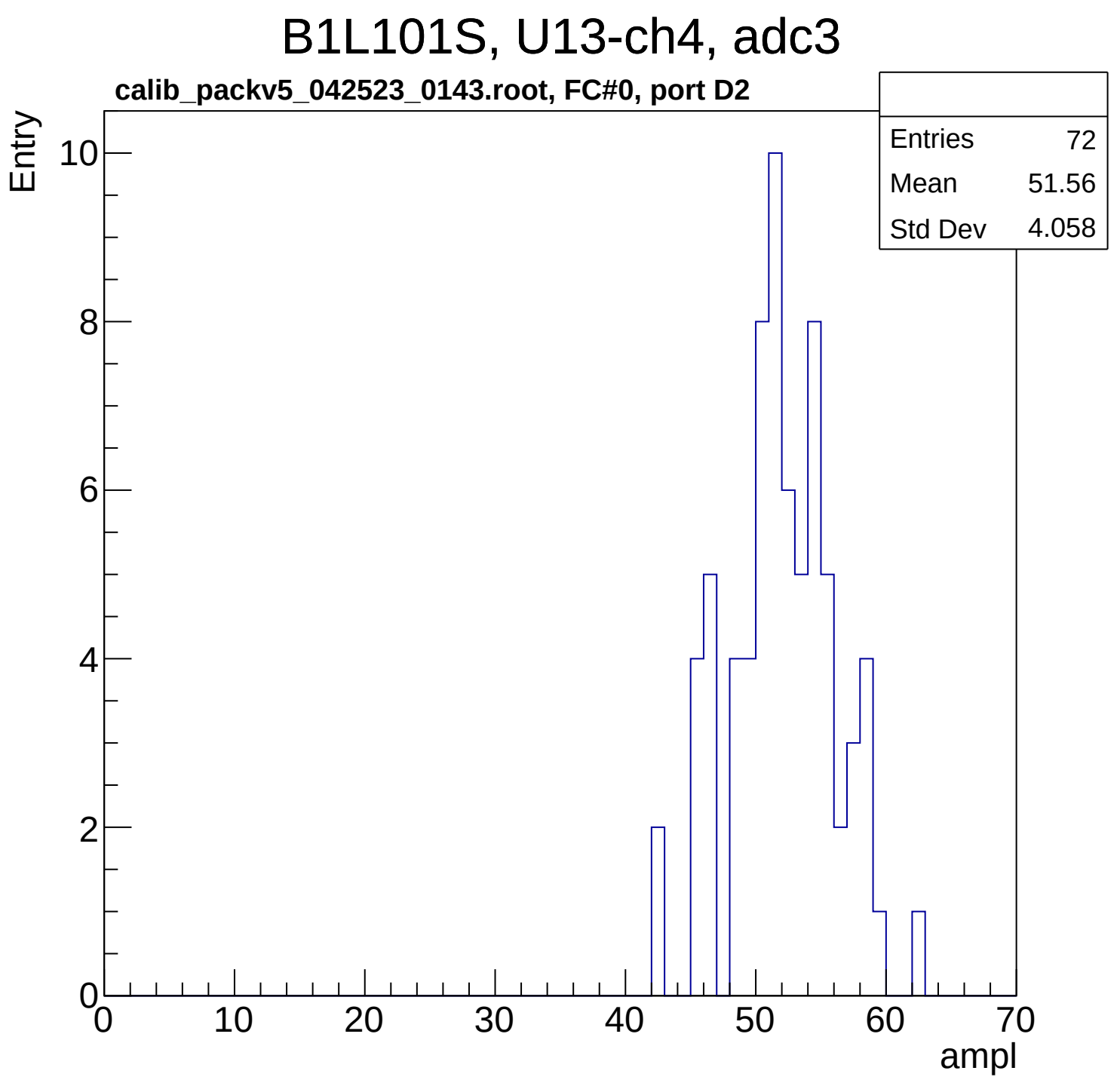
B1L101S, U13-ch4, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 51.56
Std Dev 4.058
Entry
ampl
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4
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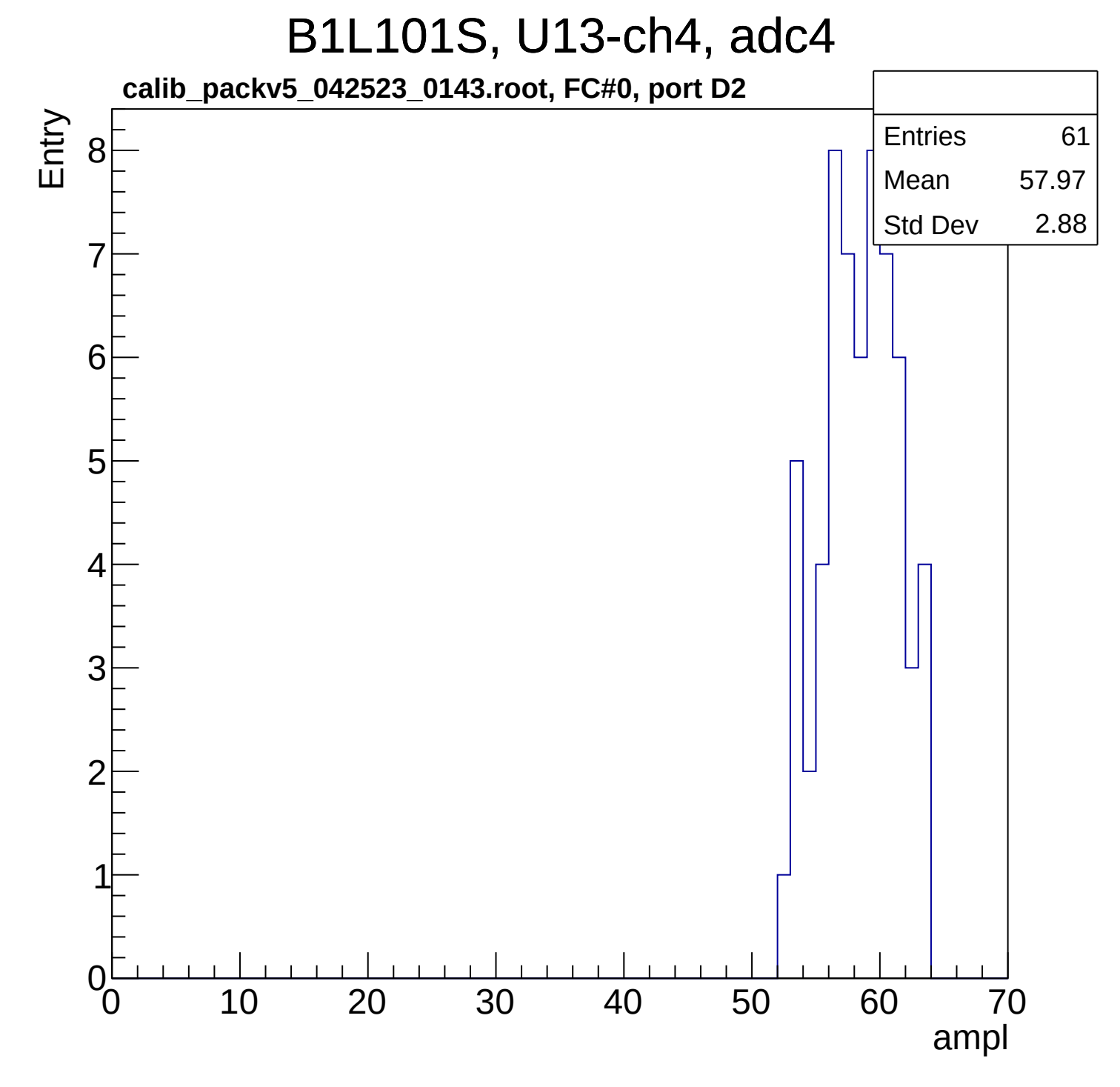
B1L101S, U13-ch4, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 57.97
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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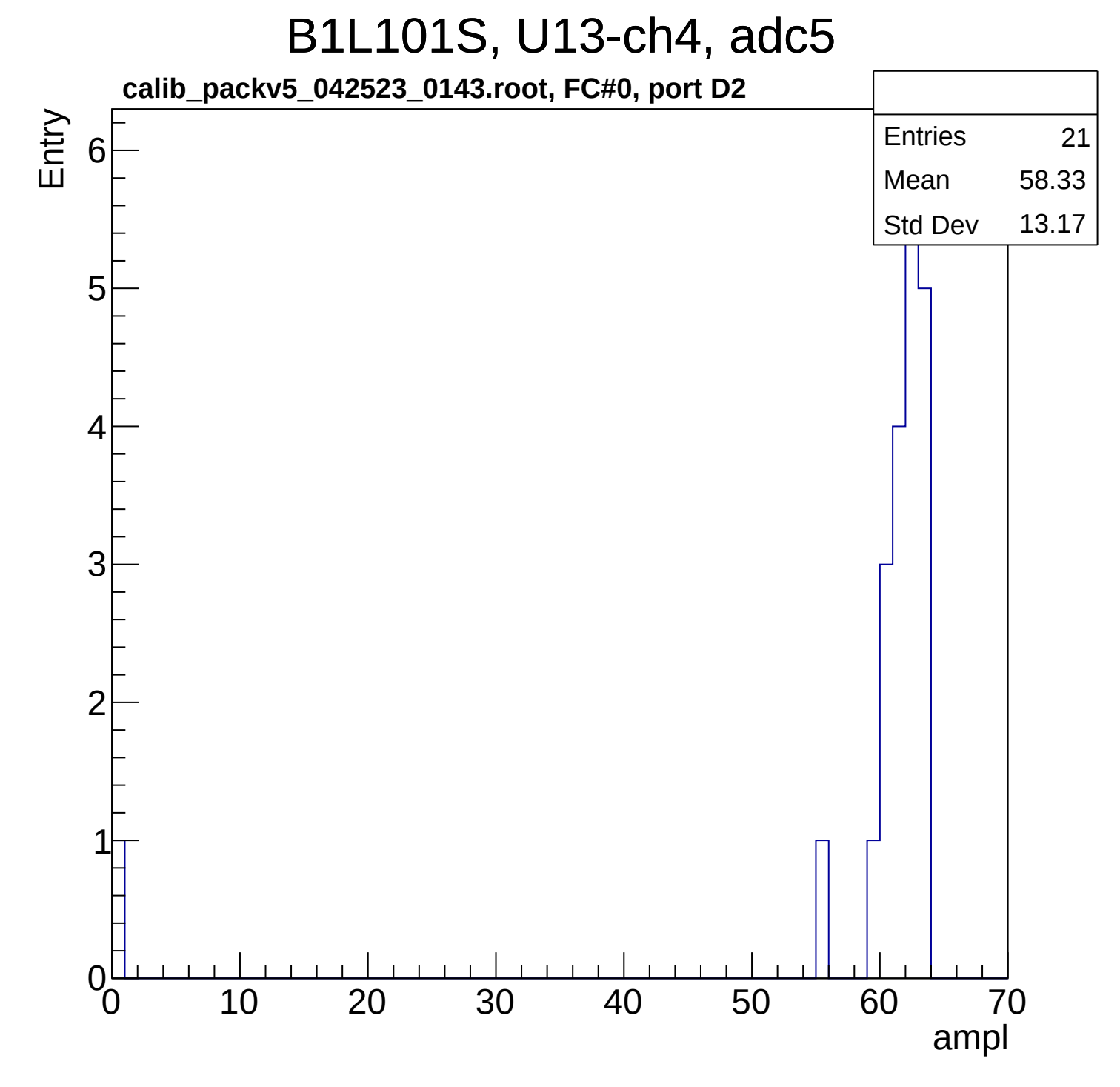
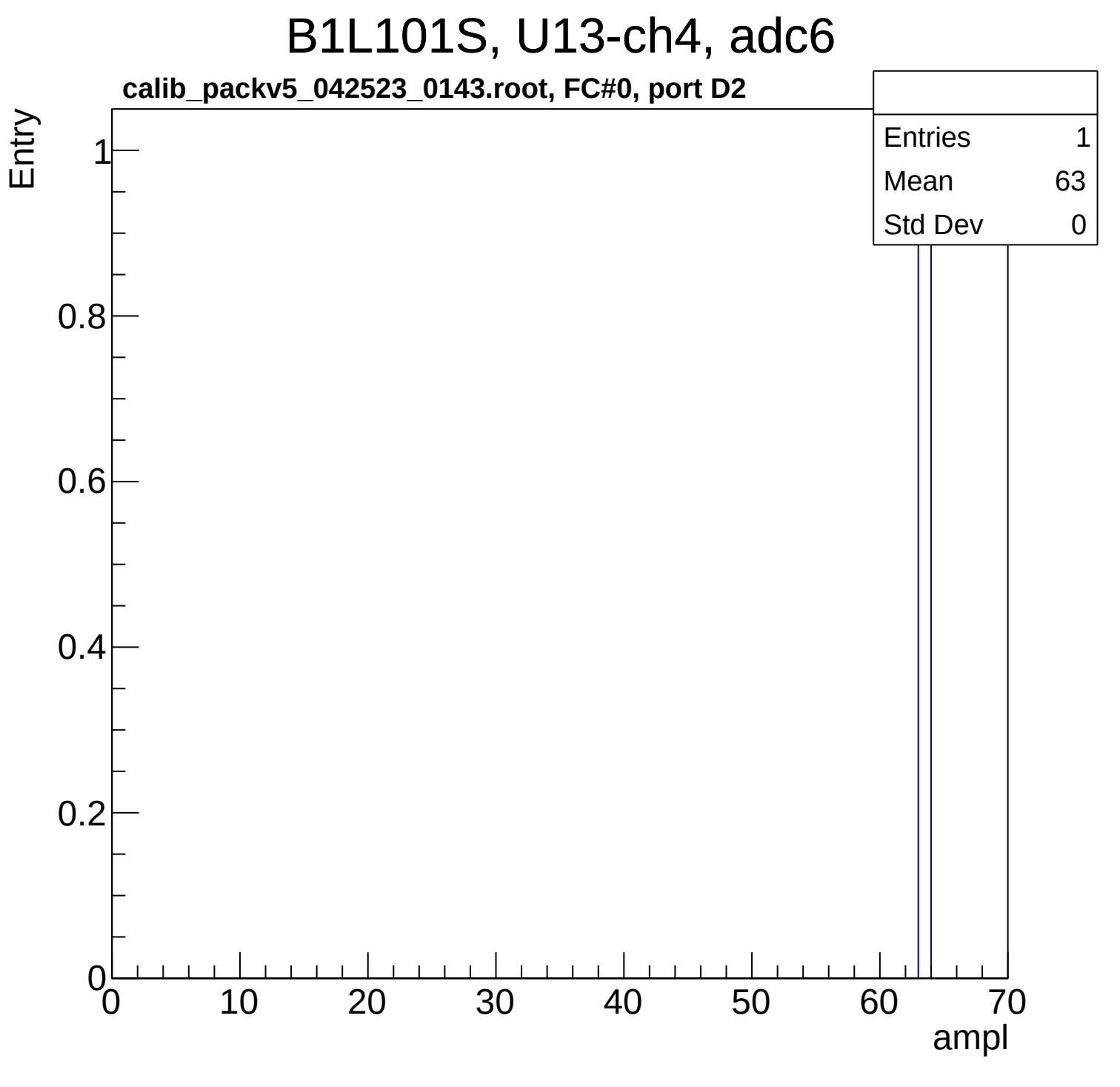

B1L101S, U13-ch4, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 21
Mean 58.33
Std Dev 13.17
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch4, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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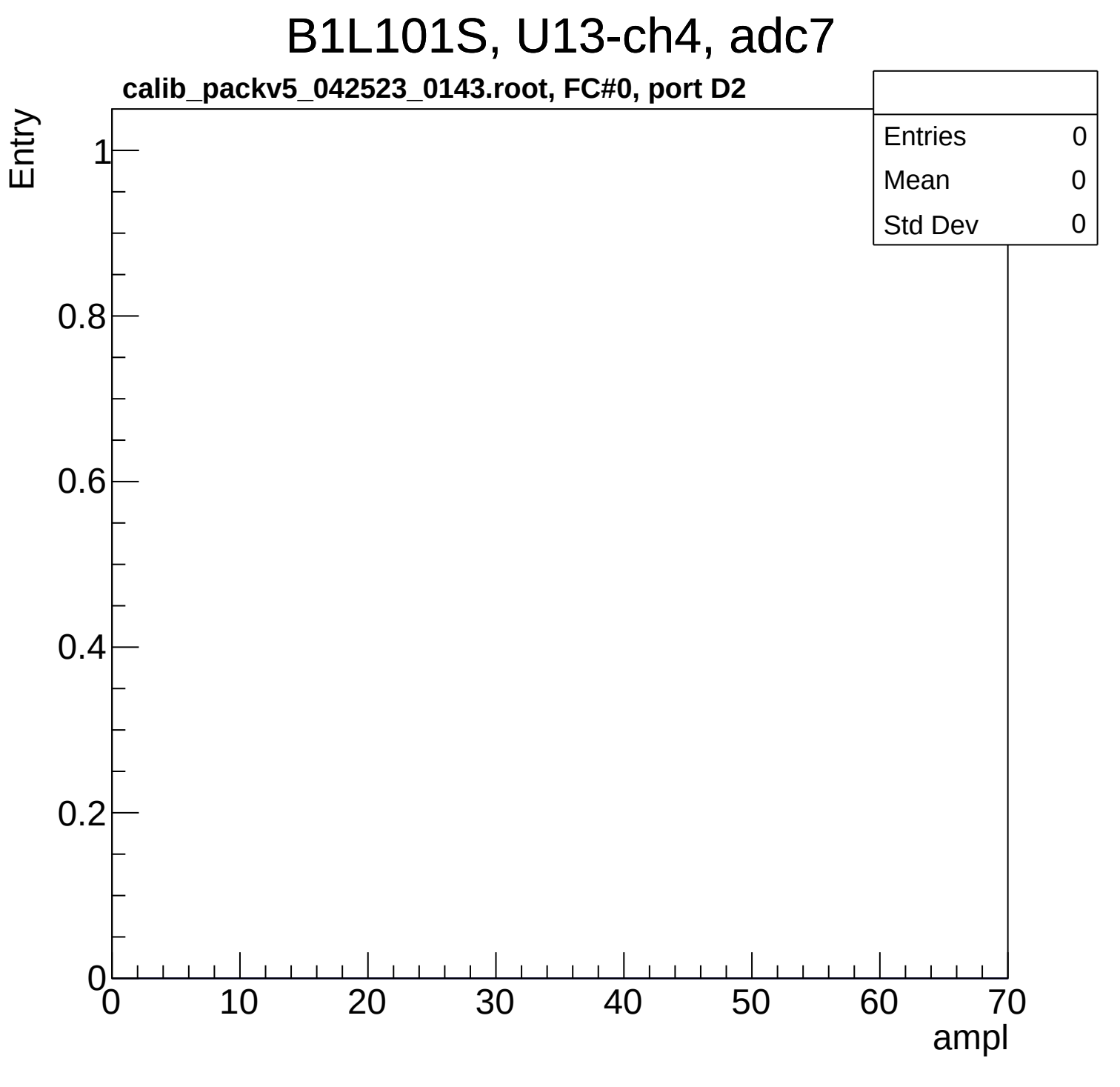
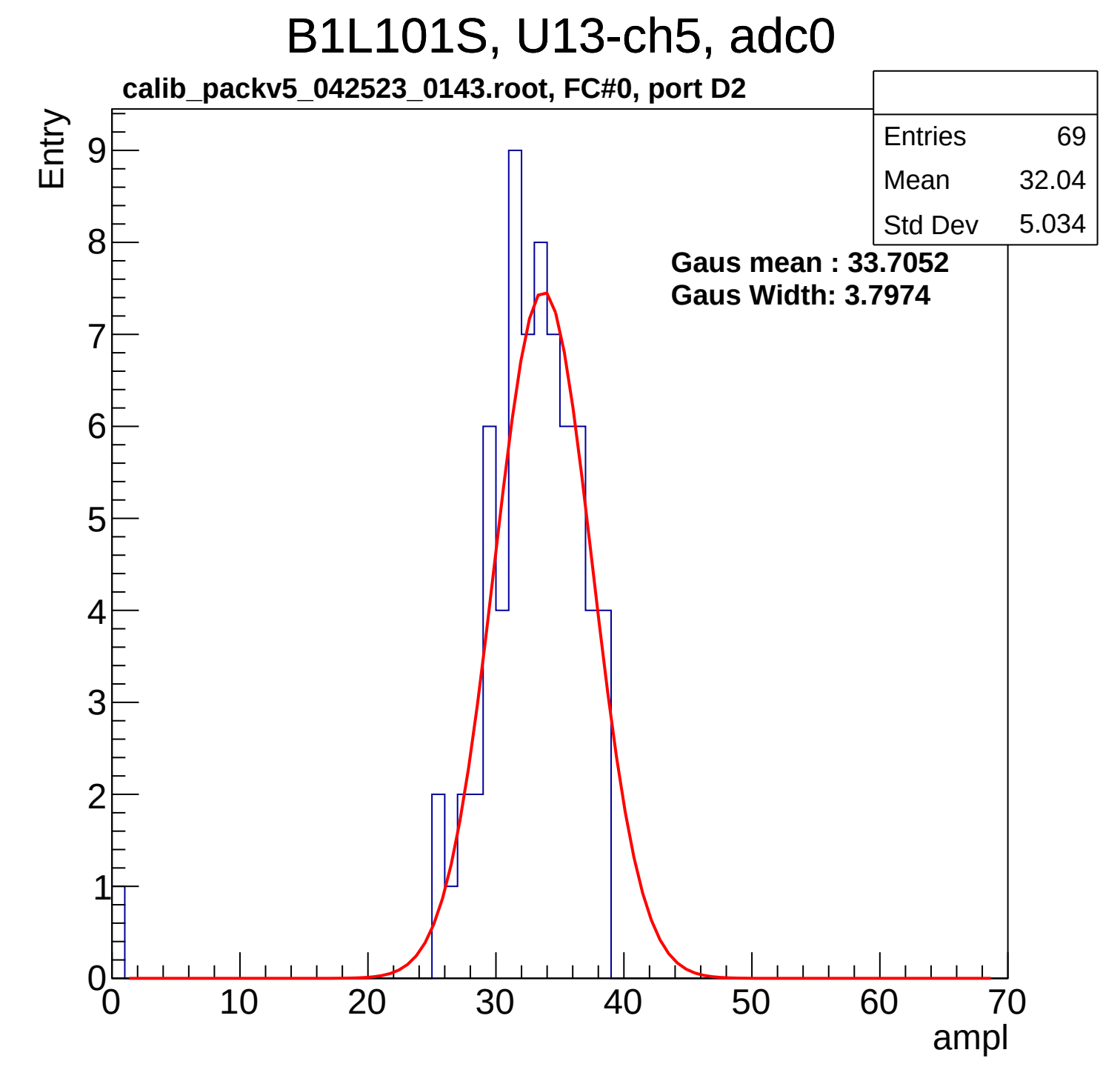
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch4, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch5, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 32.04
Std Dev 5.034
Gaus mean : 33.7052
Gaus Width: 3.7974
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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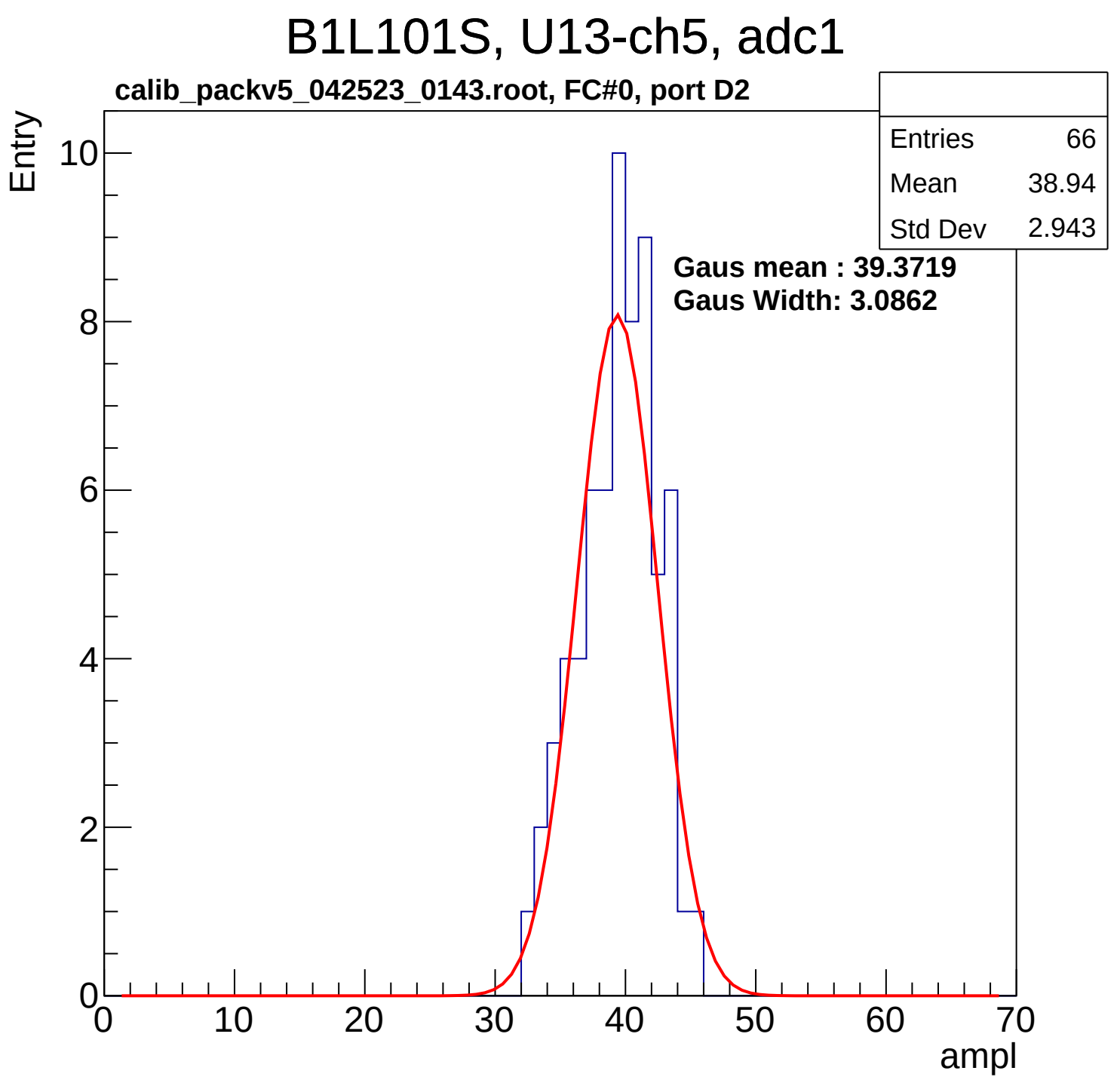
B1L101S, U13-ch5, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 38.94
Std Dev 2.943
Gaus mean : 39.3719
Gaus Width: 3.0862
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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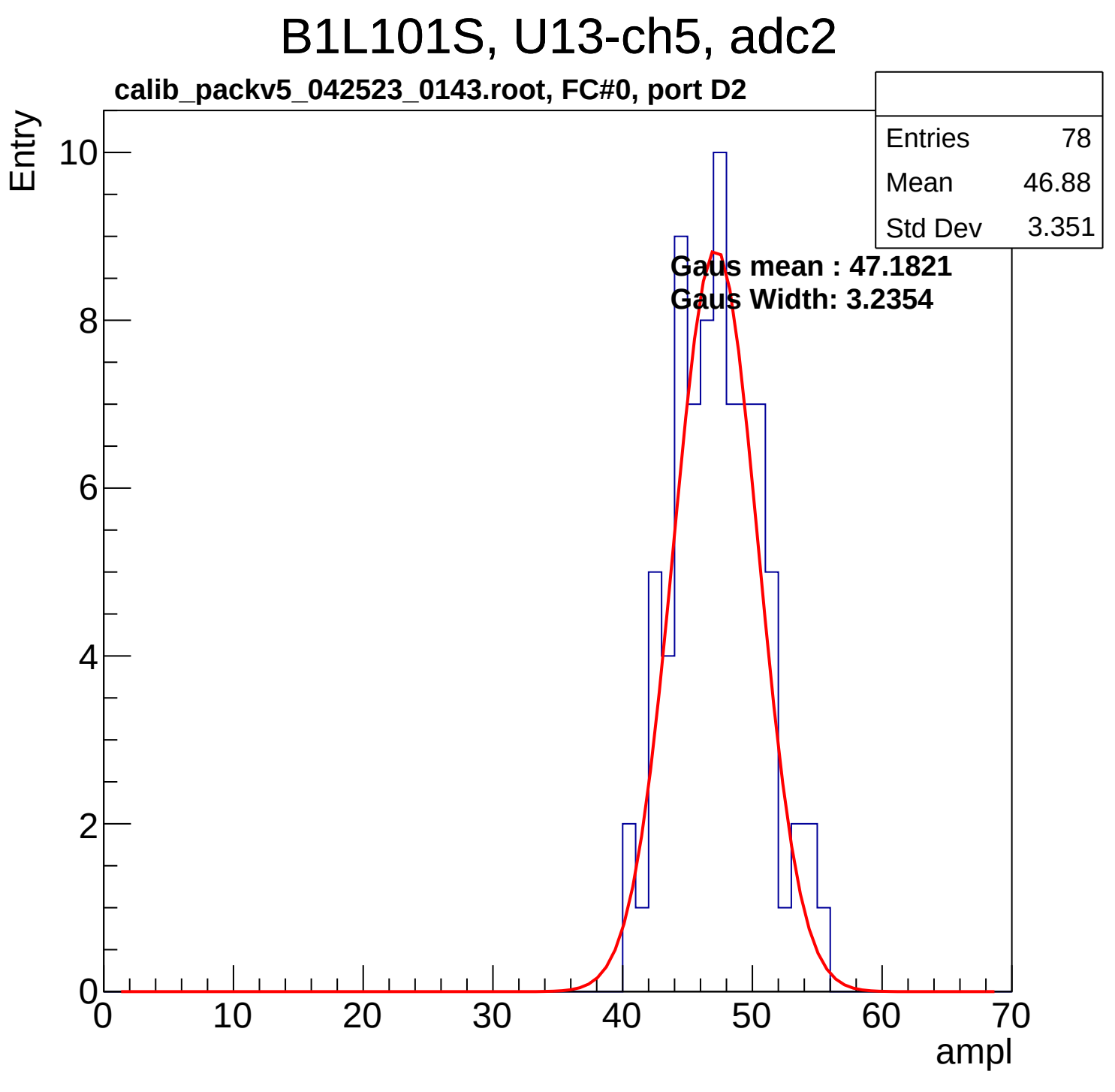
B1L101S, U13-ch5, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 46.88
Std Dev 3.351
Gaus mean : 47.1821
Gaus Width: 3.2354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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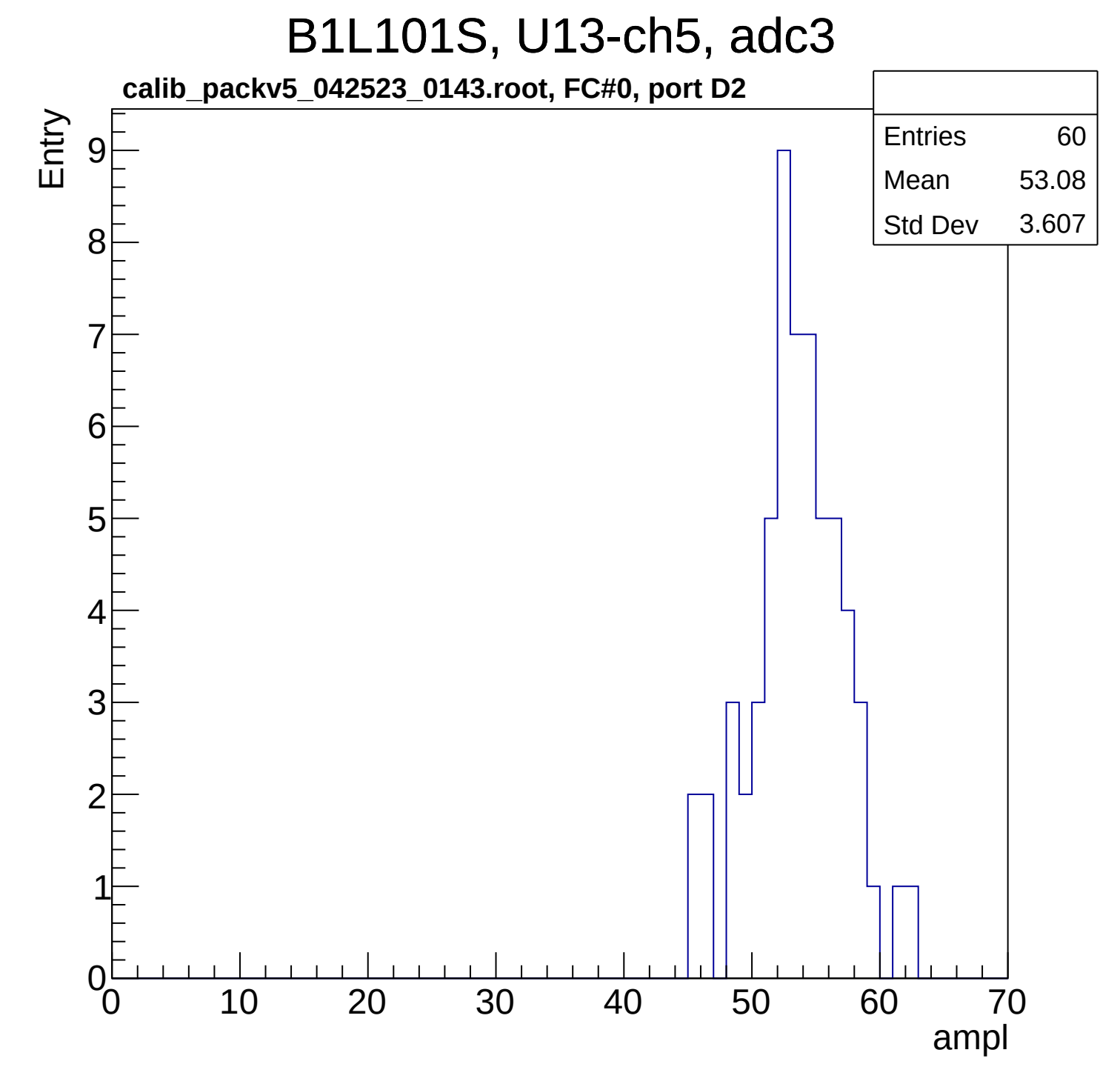
B1L101S, U13-ch5, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 53.08
Std Dev 3.607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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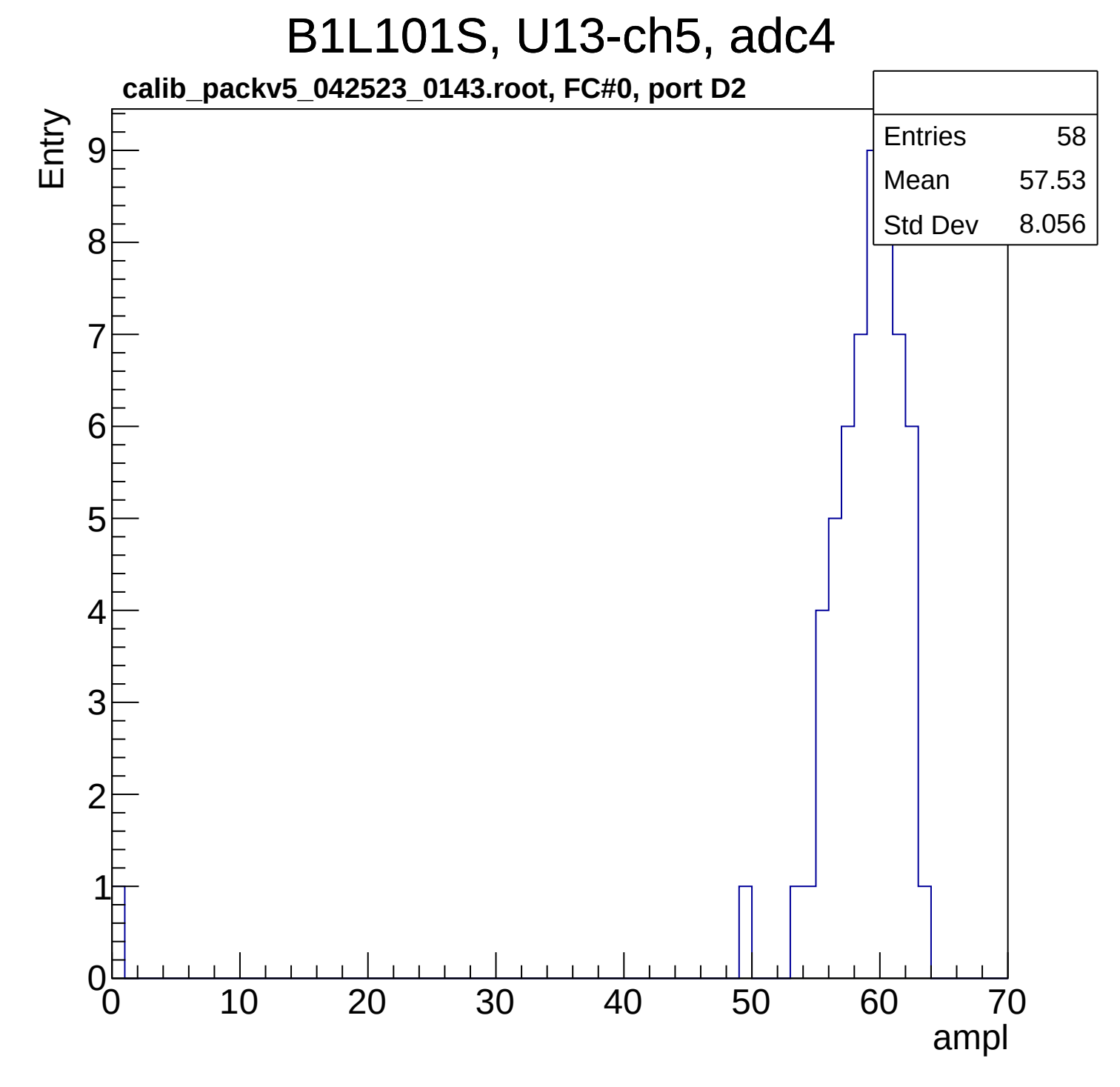
B1L101S, U13-ch5, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 57.53
Std Dev 8.056
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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60
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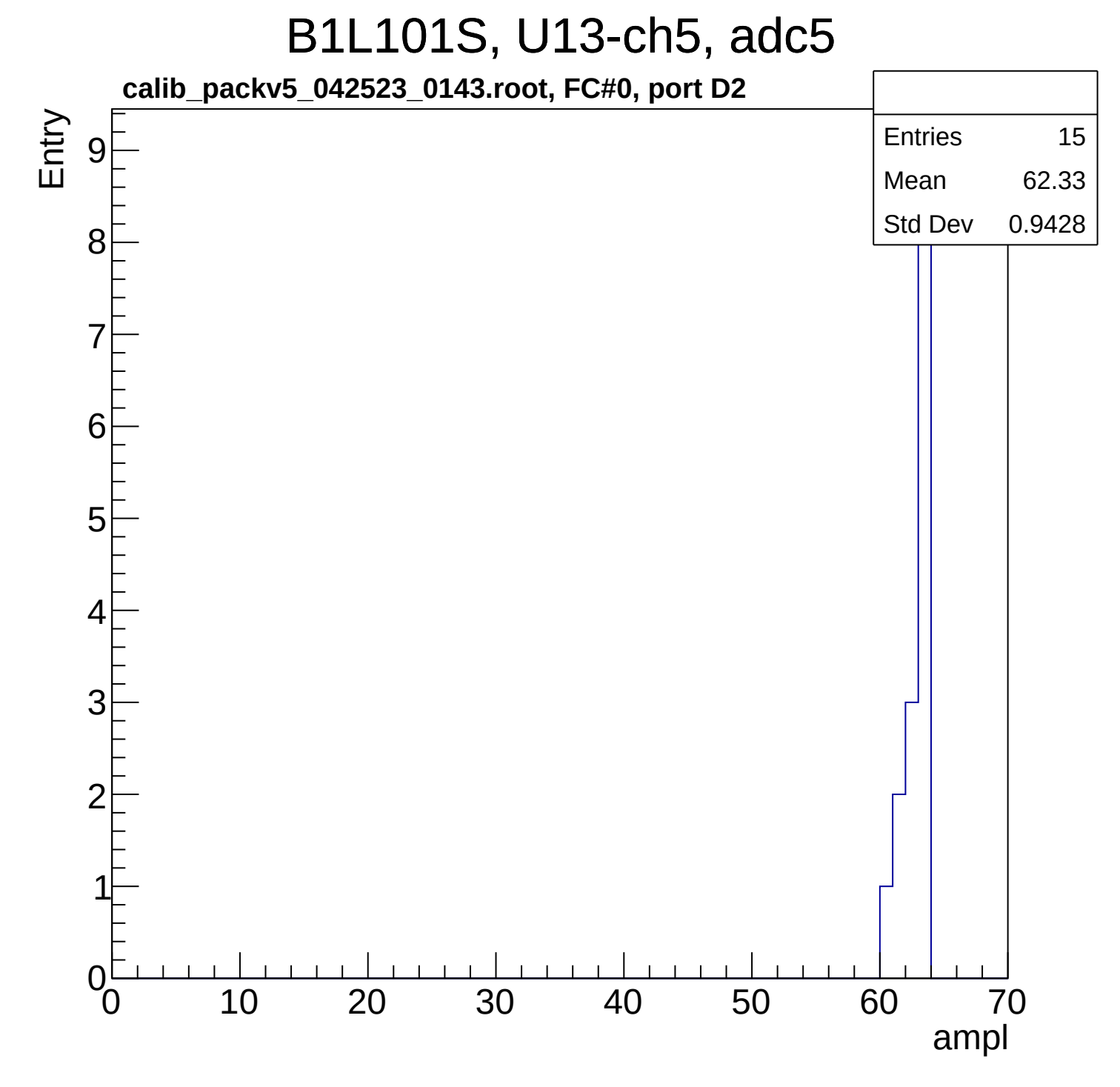
B1L101S, U13-ch5, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 15
Mean 62.33
Std Dev 0.9428
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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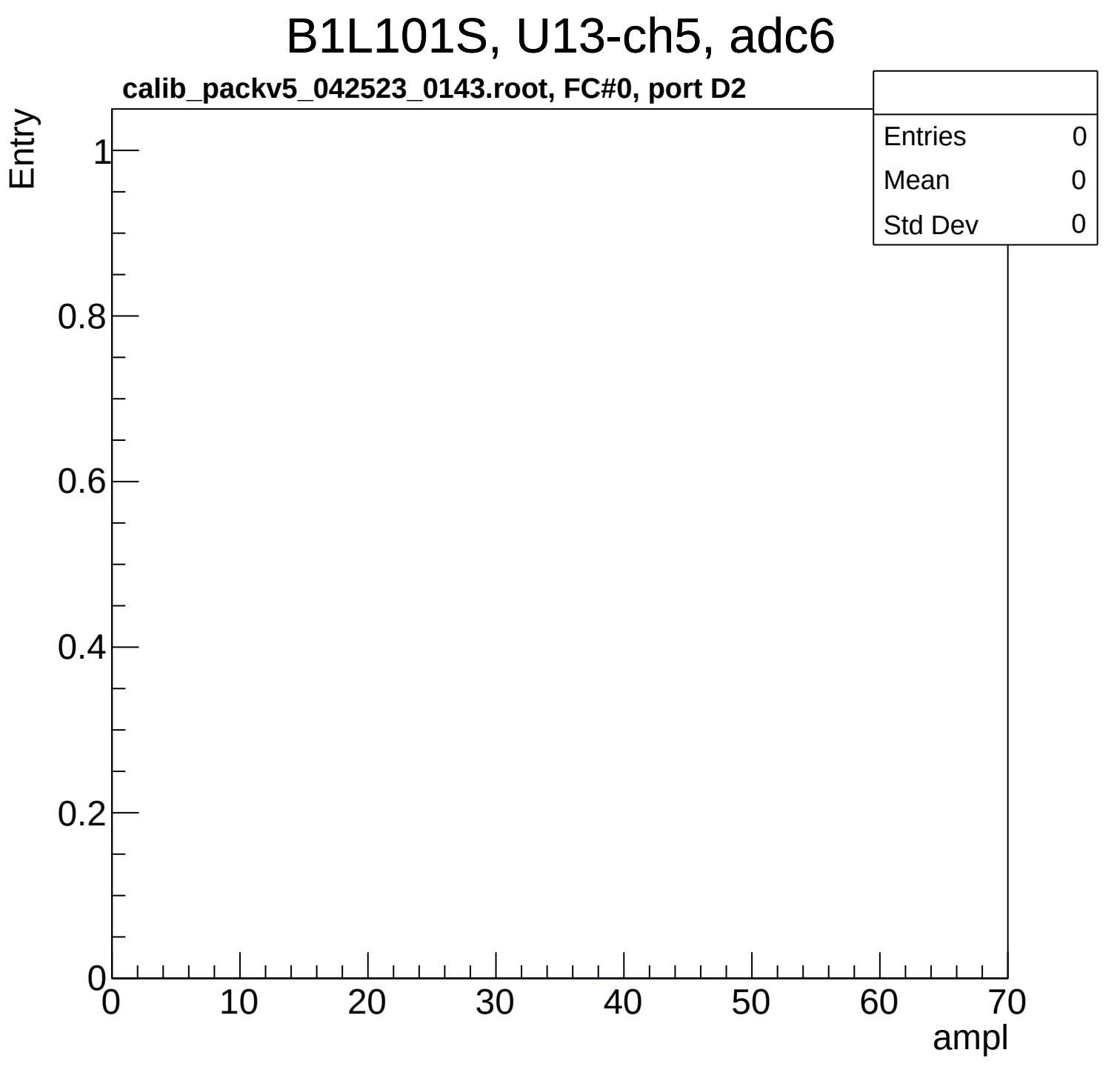
B1L101S, U13-ch5, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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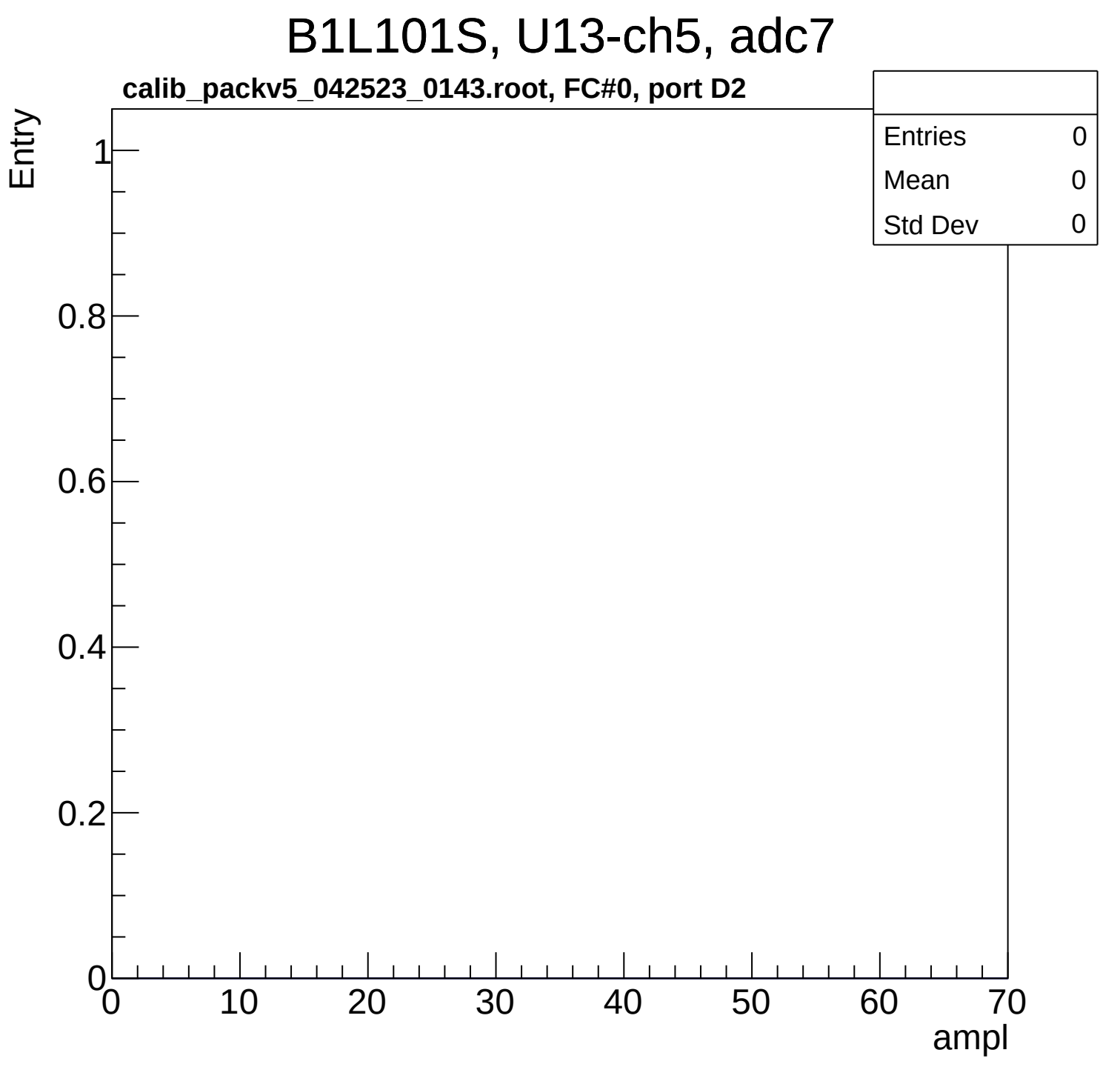
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch5, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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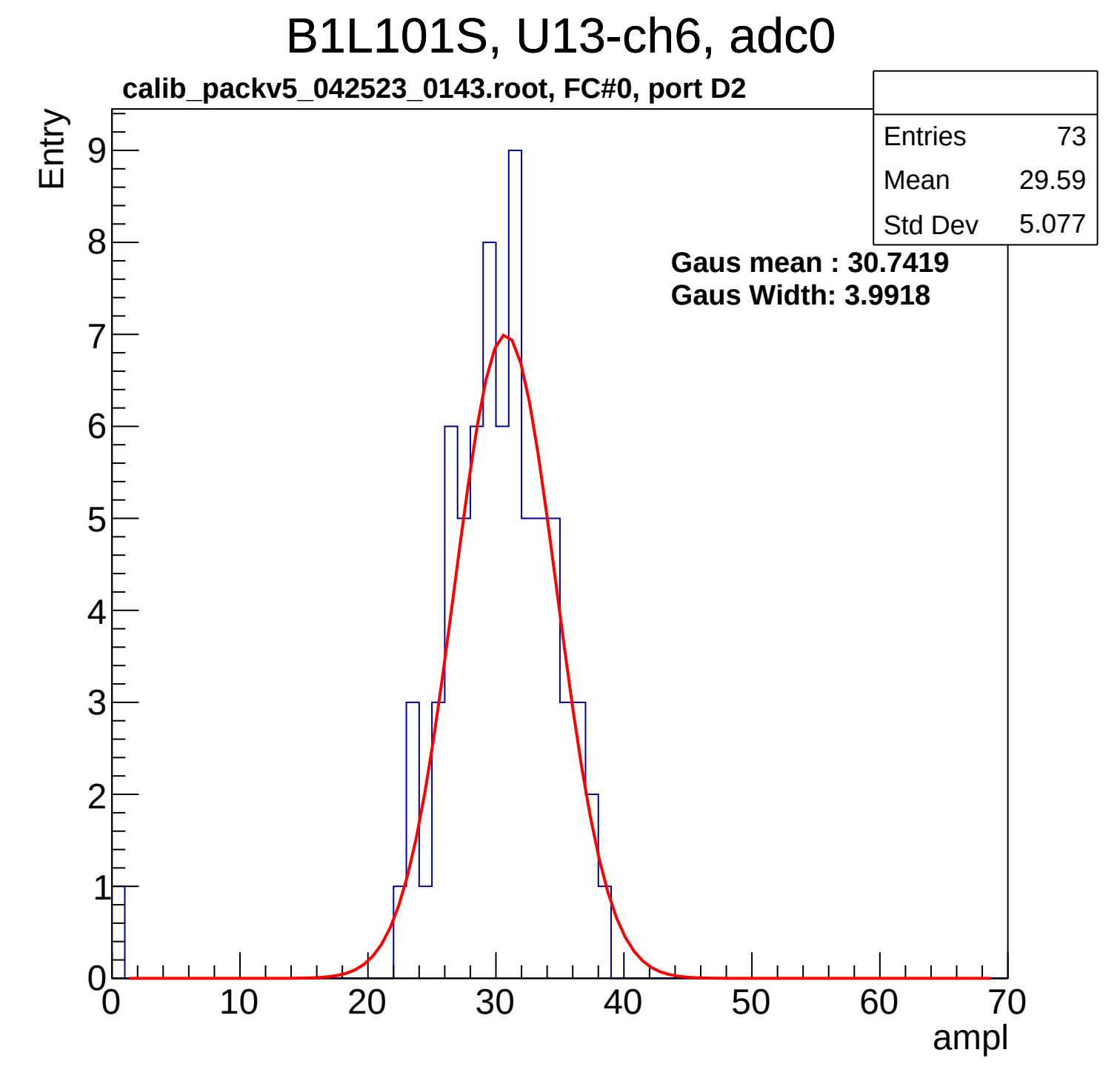
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch6, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 29.59
Std Dev 5.077
Gaus mean : 30.7419
Gaus Width: 3.9918
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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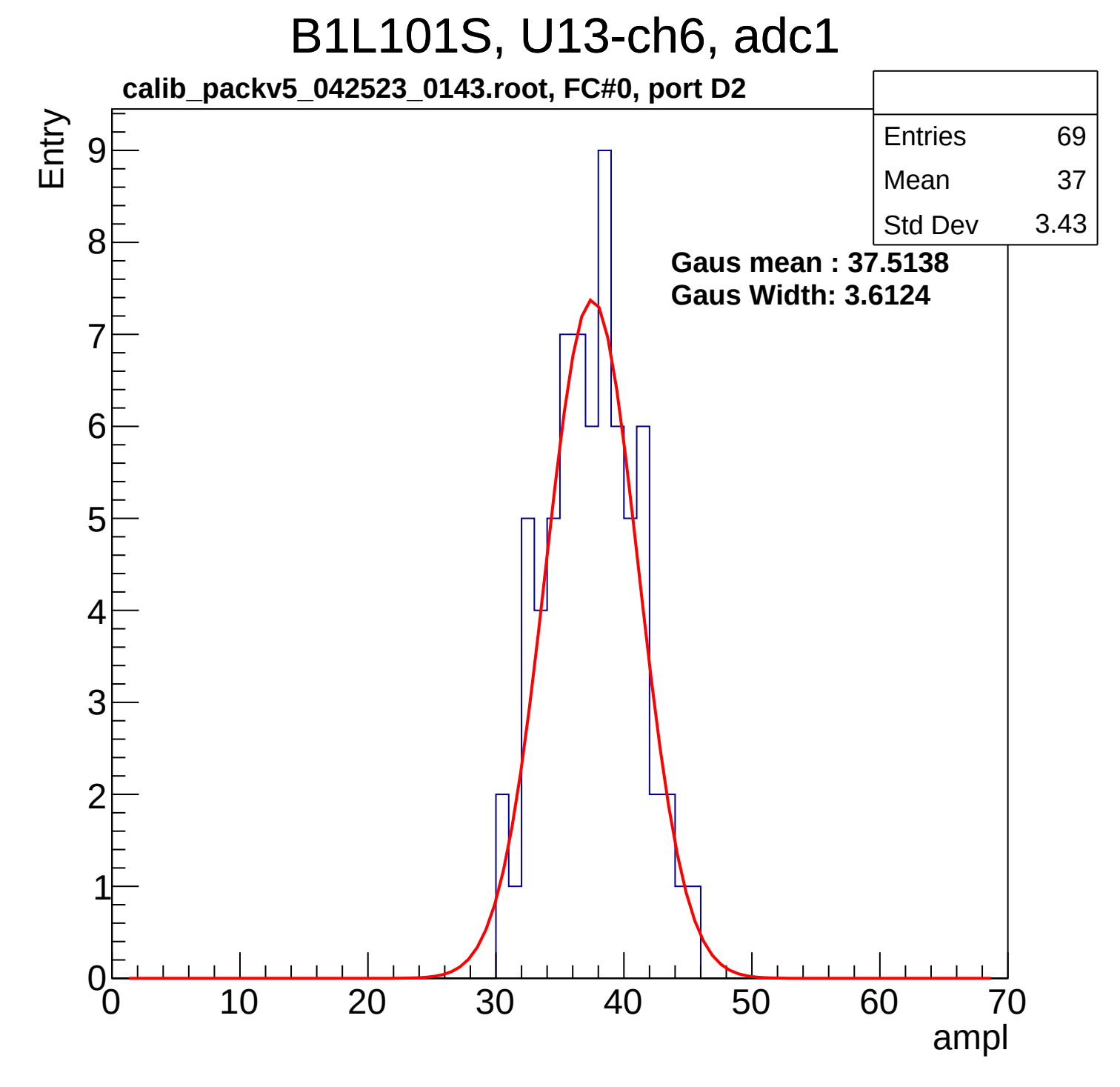
B1L101S, U13-ch6, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 37
Std Dev 3.43
Gaus mean : 37.5138
Gaus Width: 3.6124
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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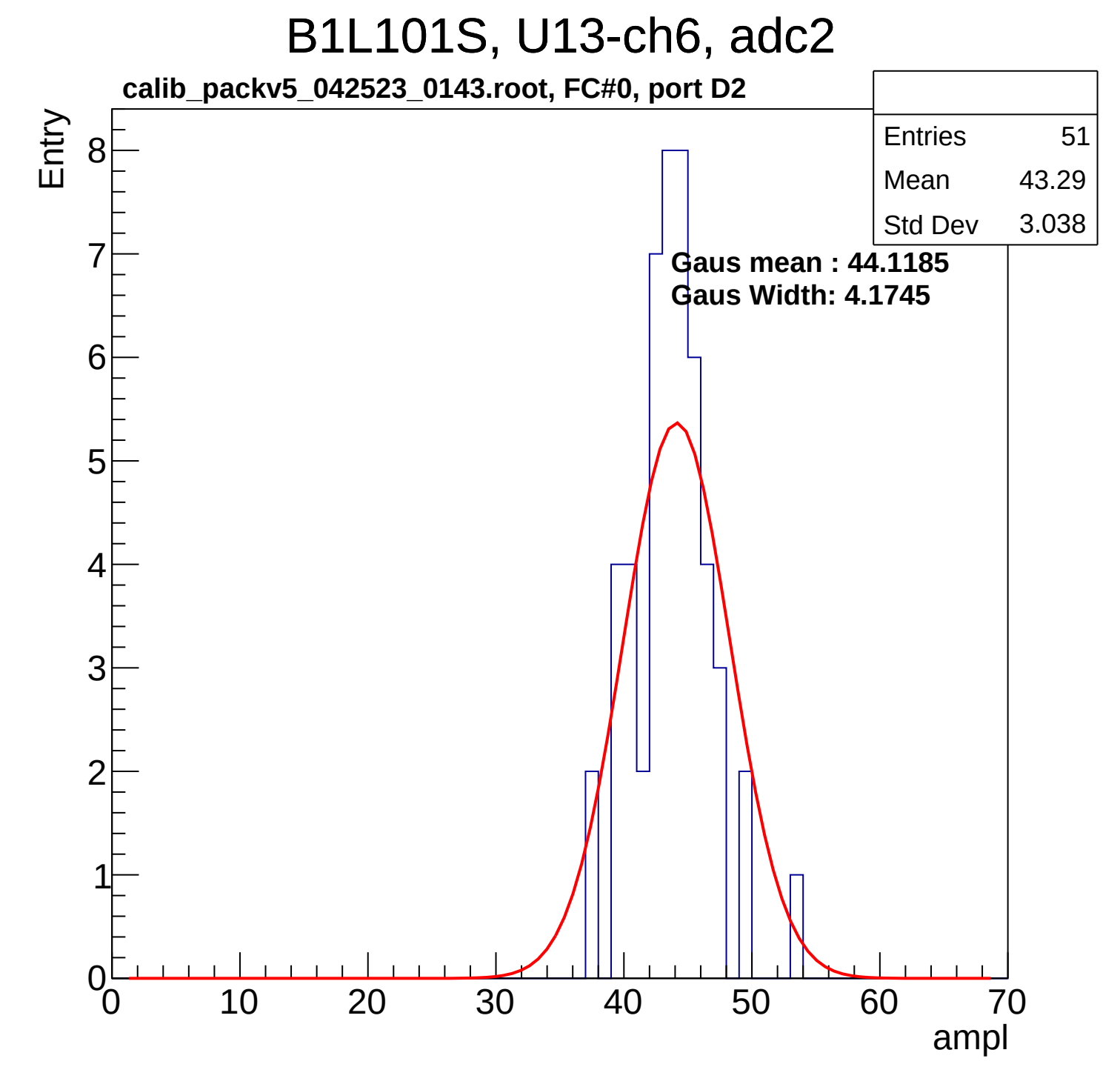
B1L101S, U13-ch6, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 43.29
Std Dev 3.038
Gaus mean : 44.1185
Gaus Width: 4.1745
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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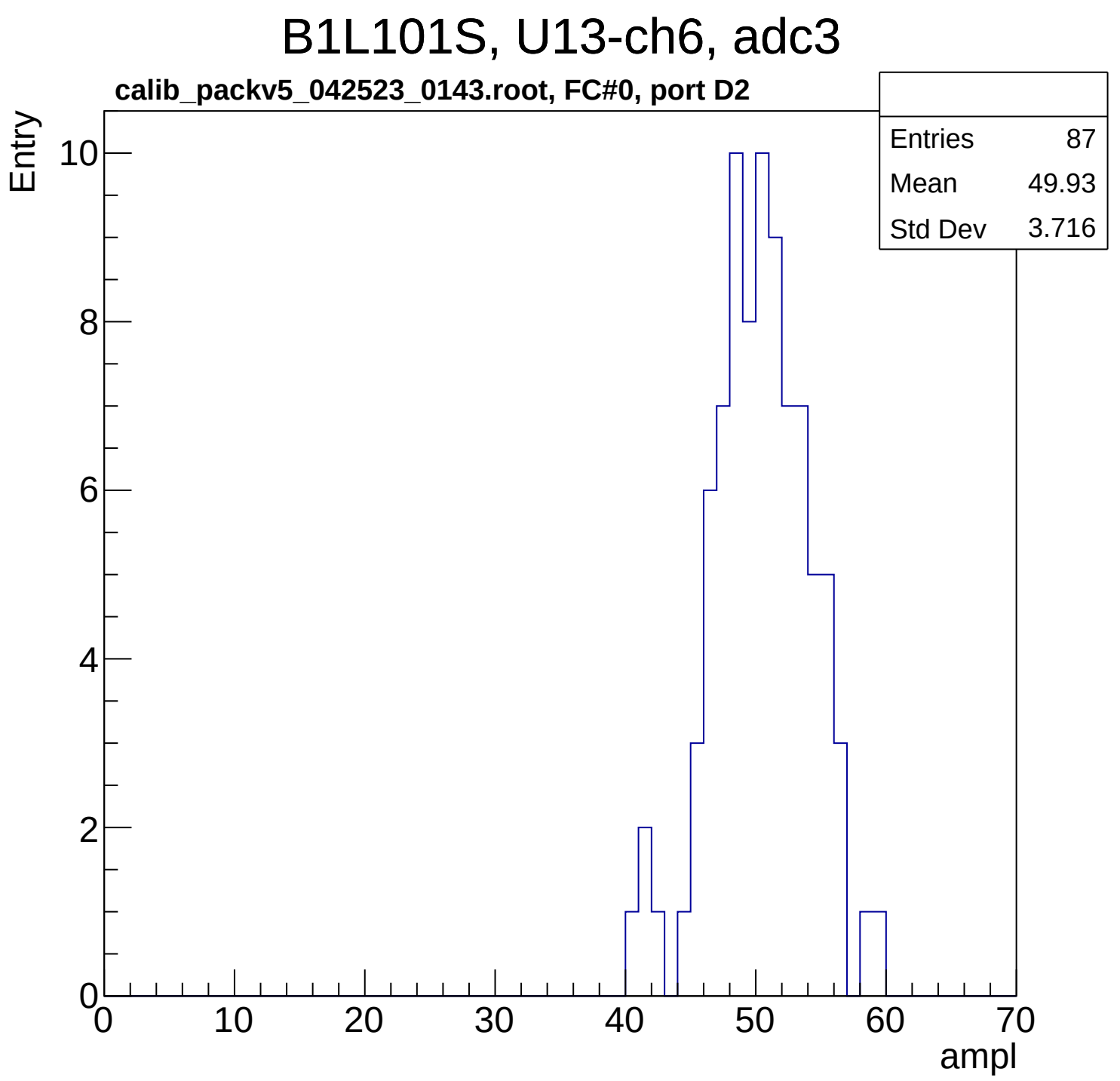
B1L101S, U13-ch6, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 87
Mean 49.93
Std Dev 3.716
Entry
ampl
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4
6
8
10
0
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60
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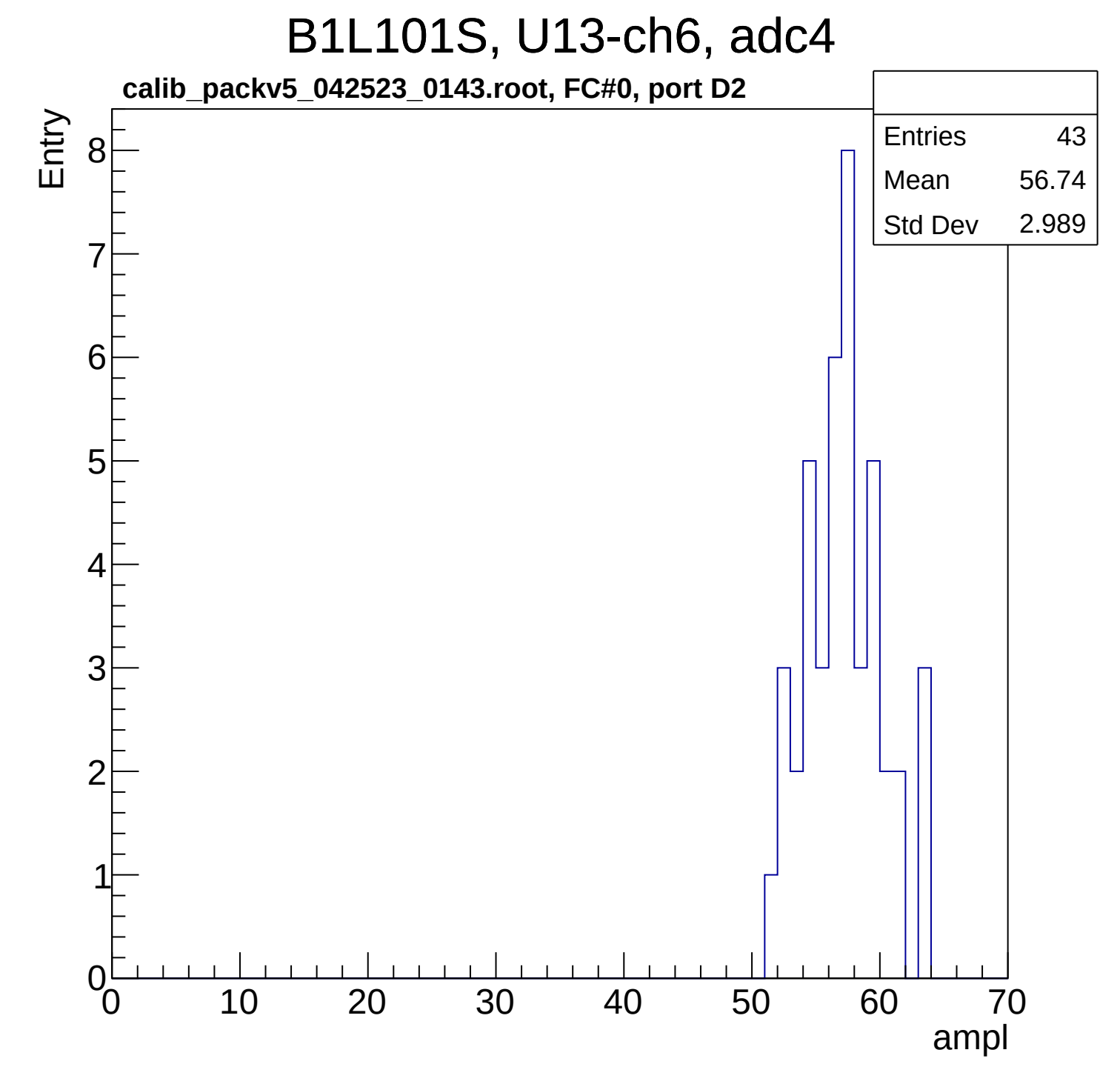
B1L101S, U13-ch6, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 43
Mean 56.74
Std Dev 2.989
Entry
ampl
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2
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4
5
6
7
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0
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40
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60
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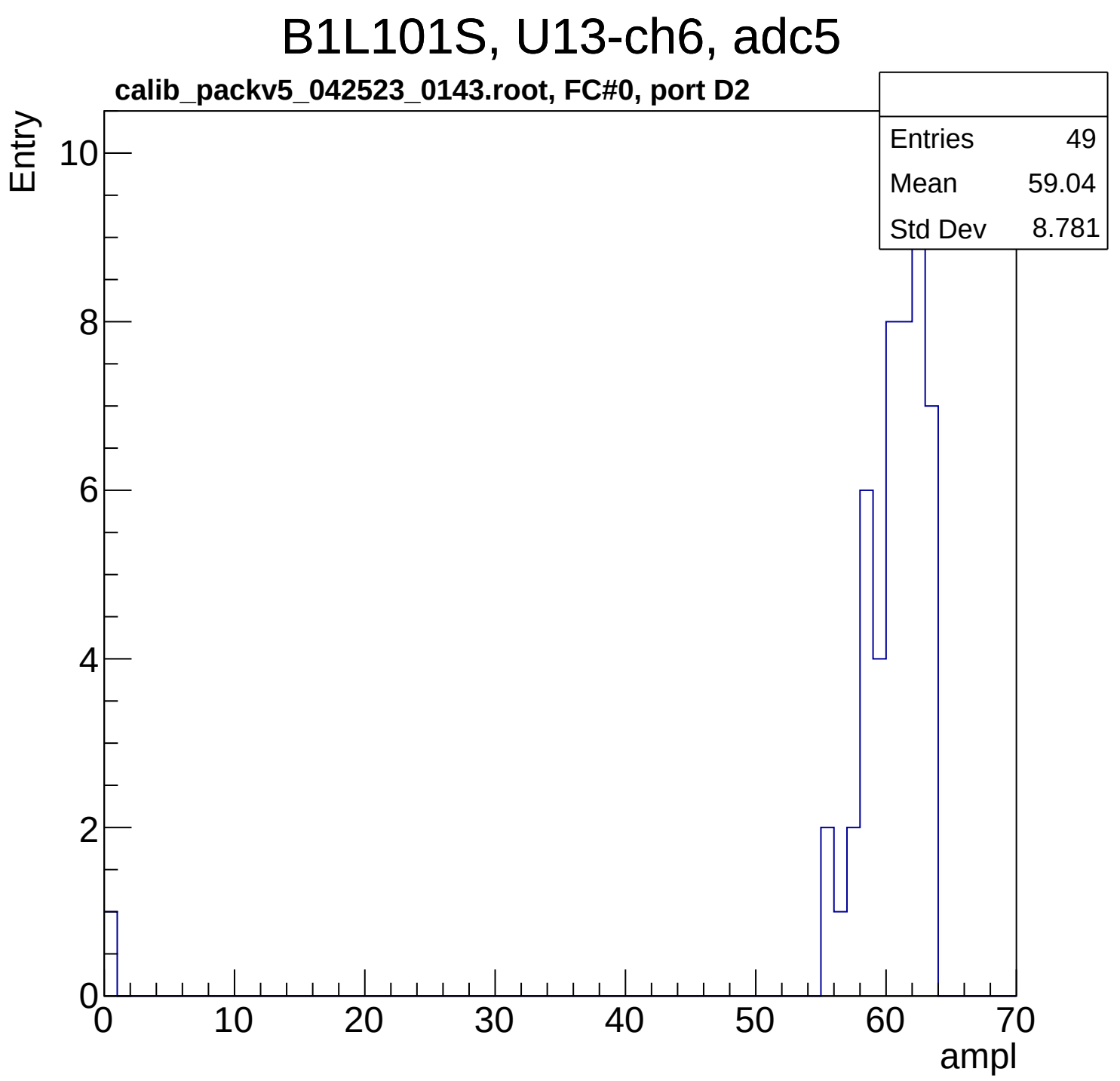
B1L101S, U13-ch6, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 59.04
Std Dev 8.781
Entry
ampl
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8
10
0
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20
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40
50
60
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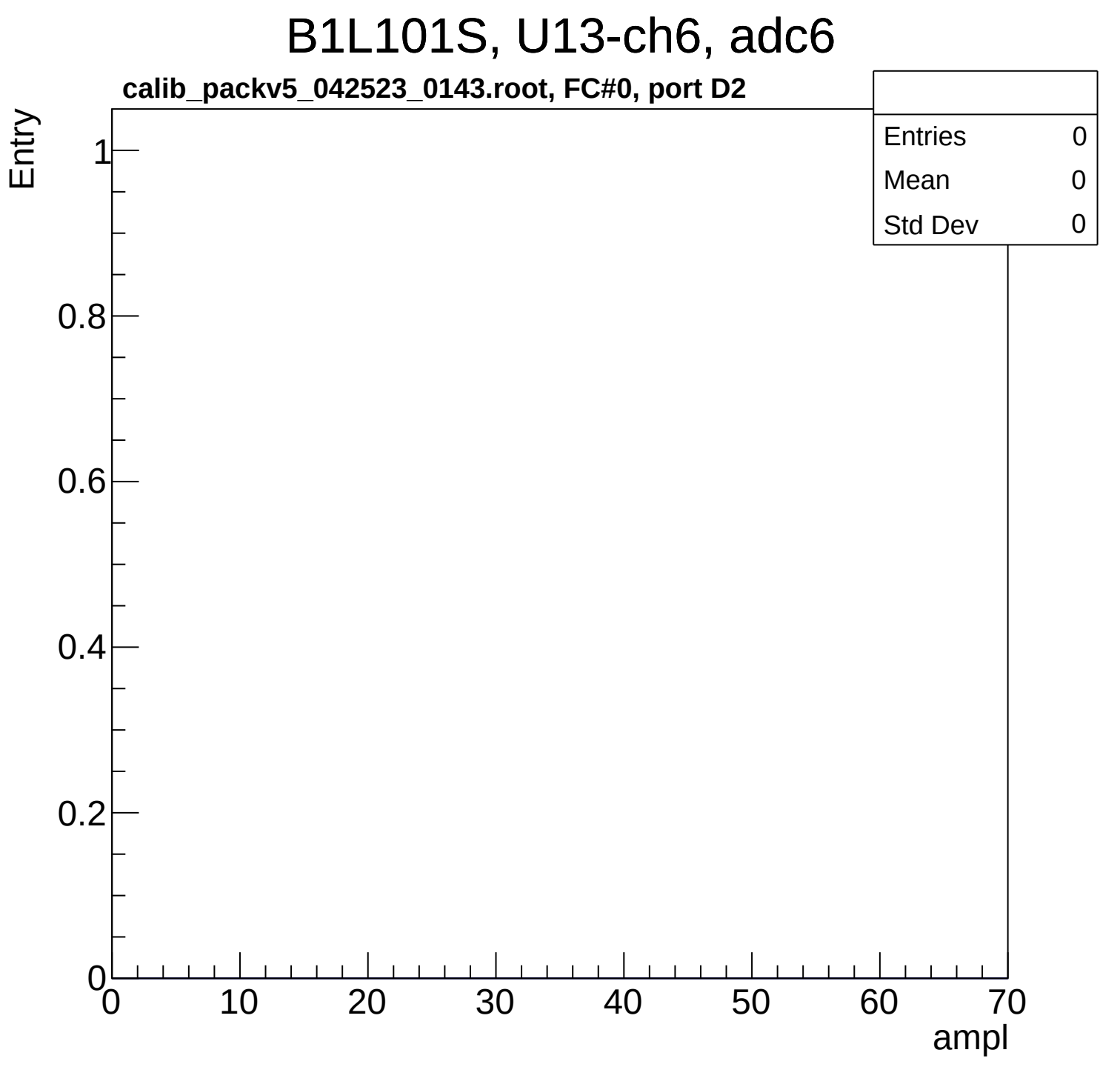
B1L101S, U13-ch6, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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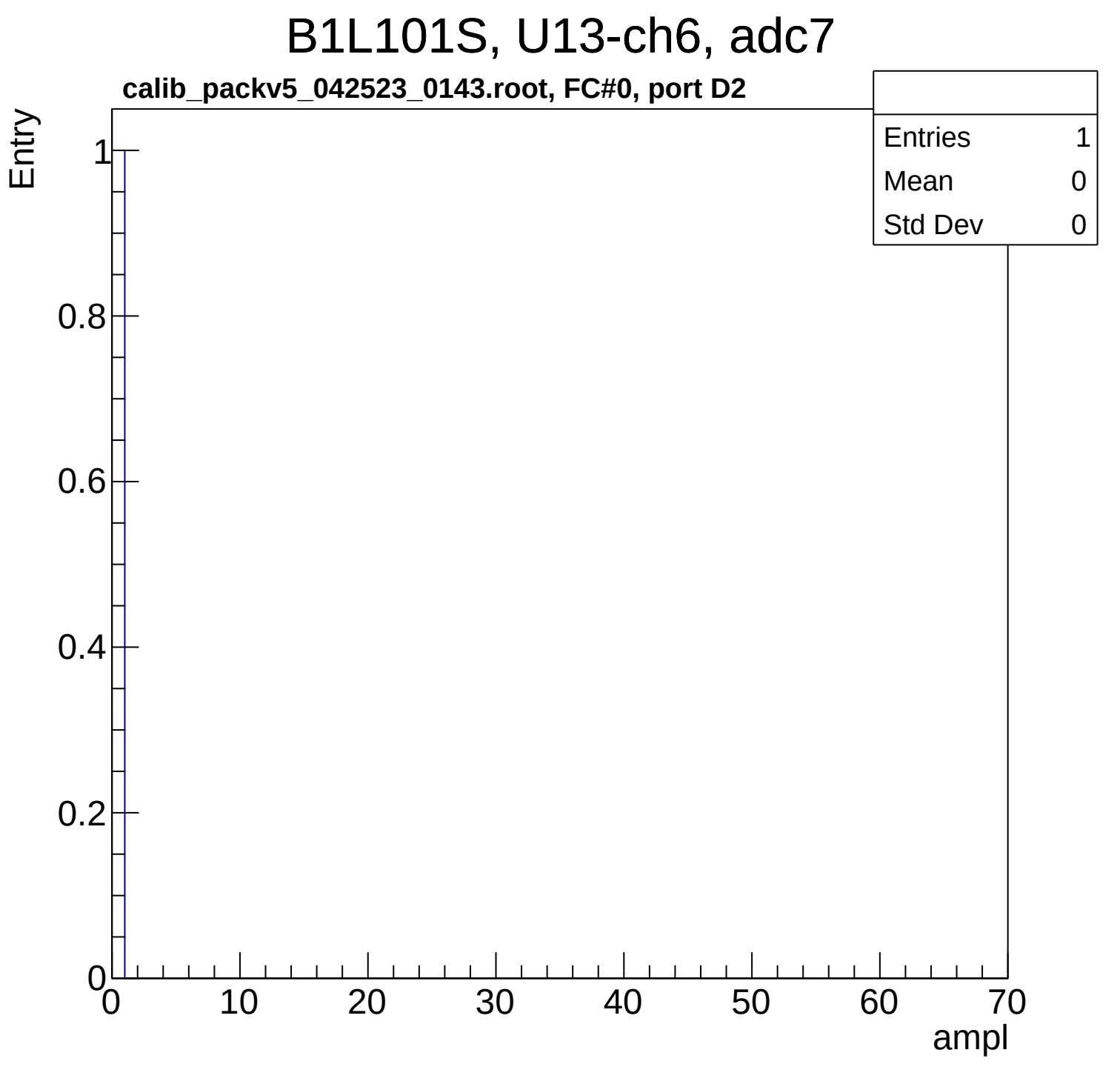
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch6, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
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0.6
0.4
0.2
0
0
10
20
30
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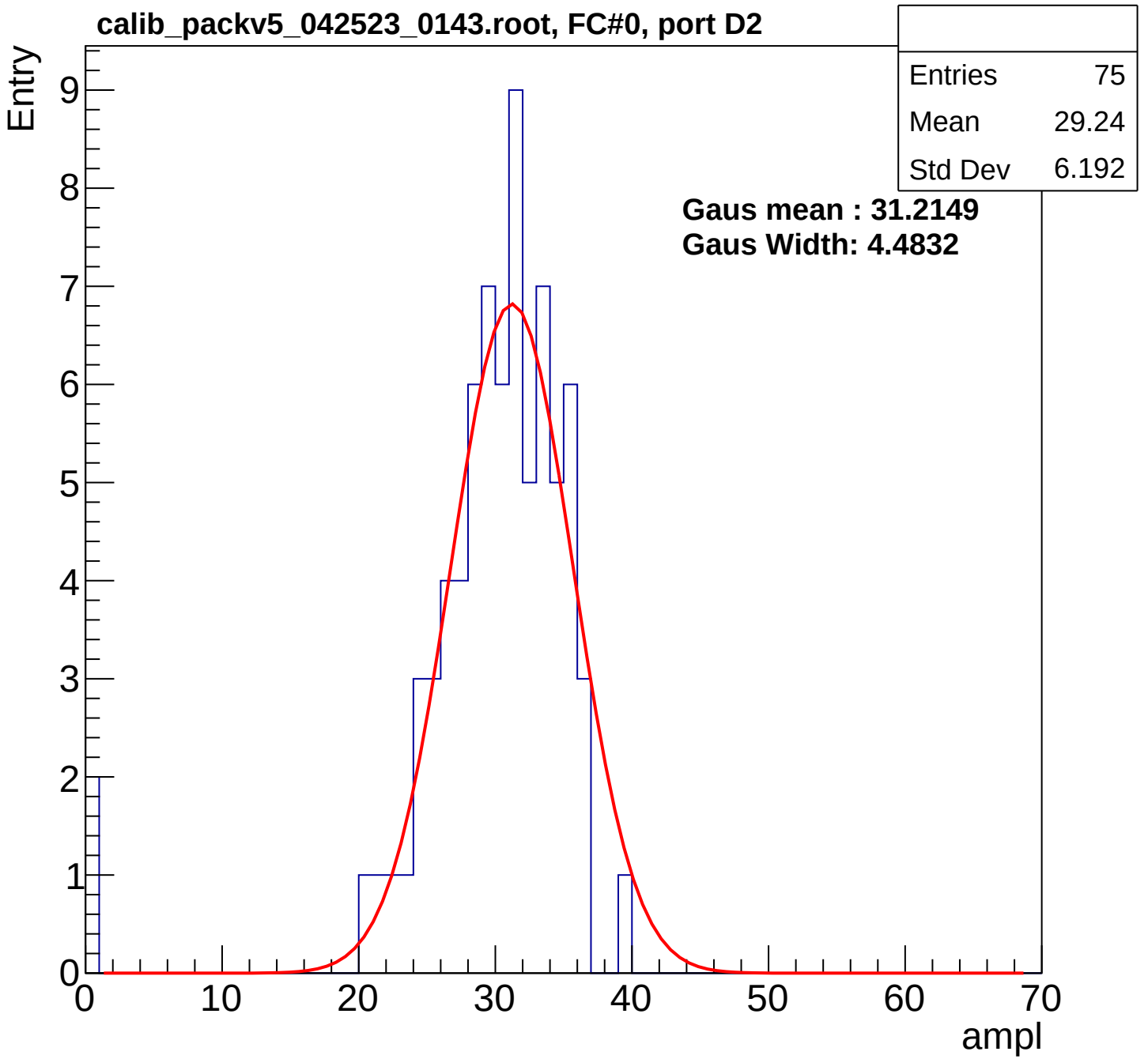
50
60
70
ampl


B1L101S, U13-ch7, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 29.24
Std Dev 6.192
Gaus mean : 31.2149
Gaus Width: 4.4832
Entry
ampl
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3
4
5
6
7
8
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0
10
20
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40
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60
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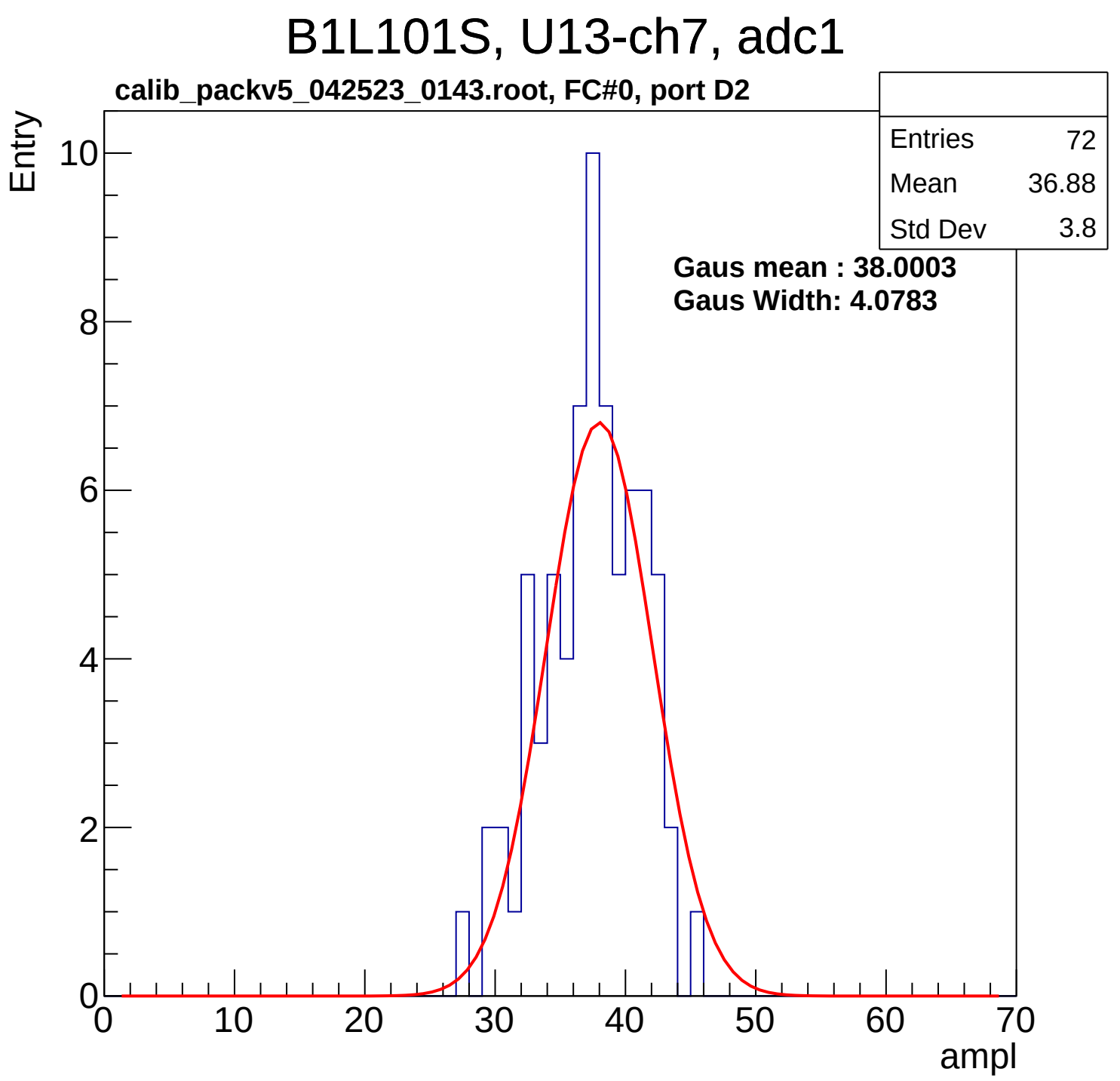
B1L101S, U13-ch7, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 36.88
Std Dev 3.8
Gaus mean : 38.0003
Gaus Width: 4.0783
Entry
ampl
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4
6
8
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0
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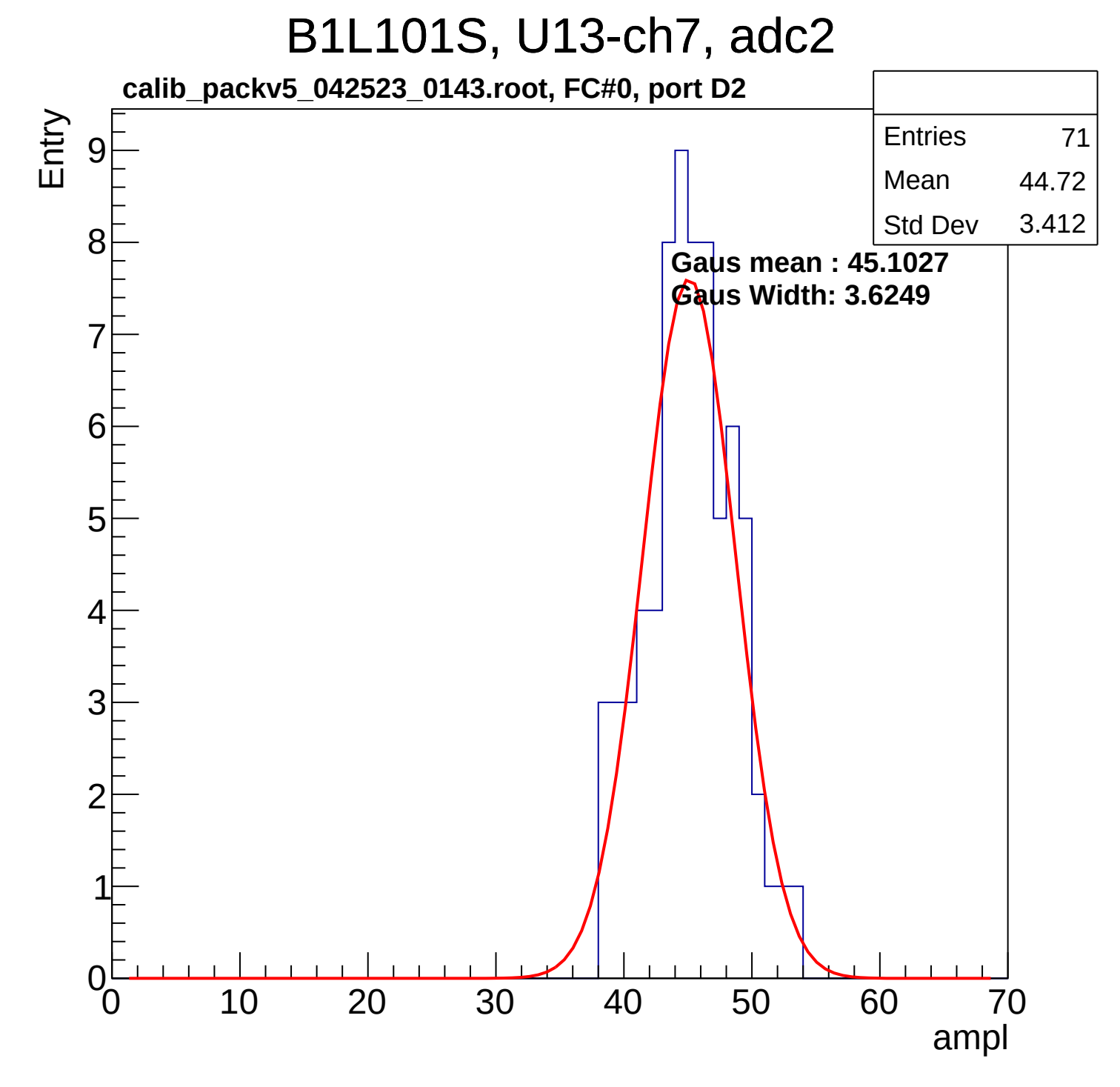
B1L101S, U13-ch7, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 44.72
Std Dev 3.412
Gaus mean : 45.1027
Gaus Width: 3.6249
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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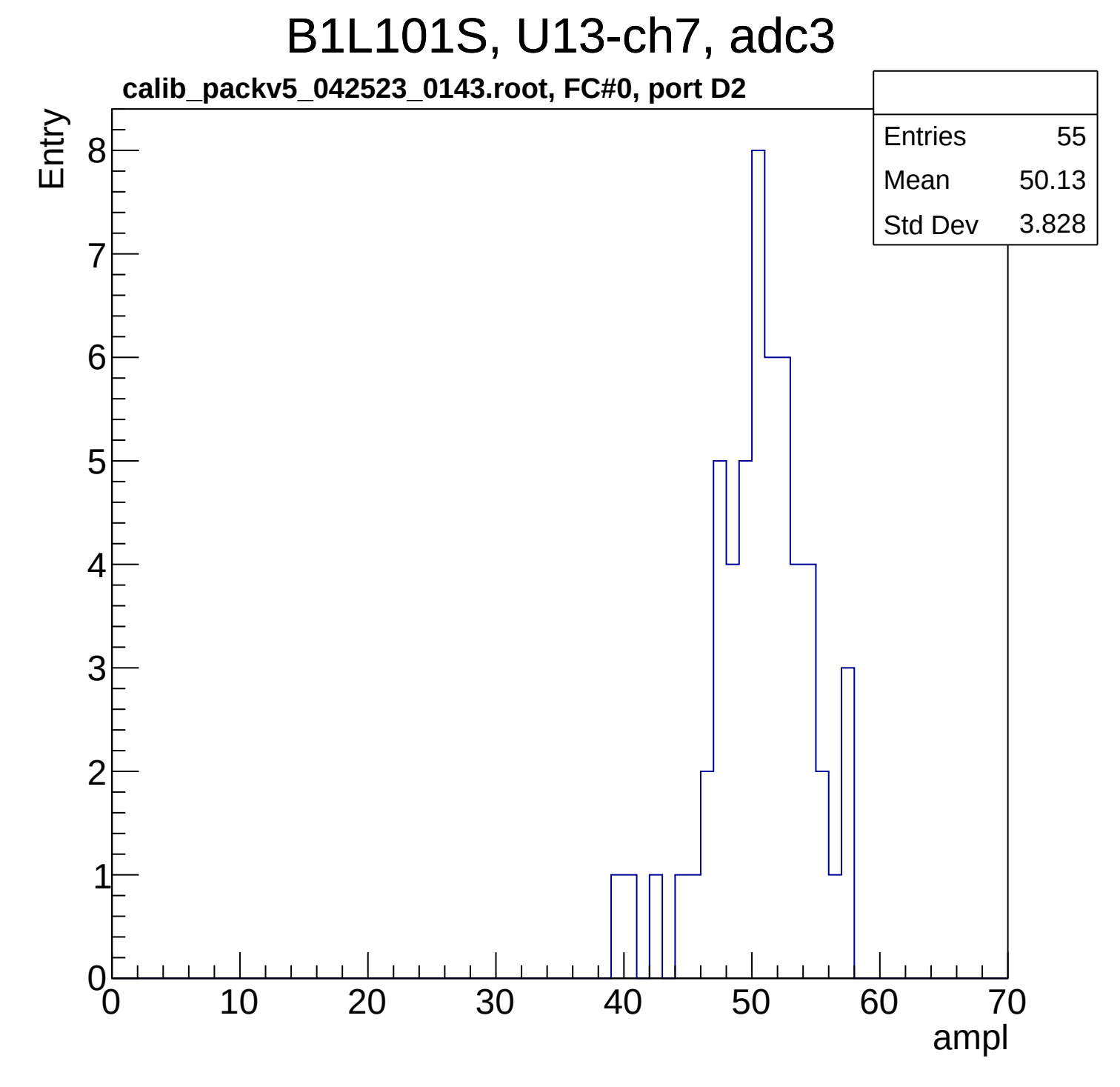
B1L101S, U13-ch7, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 50.13
Std Dev 3.828
Entry
ampl
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2
3
4
5
6
7
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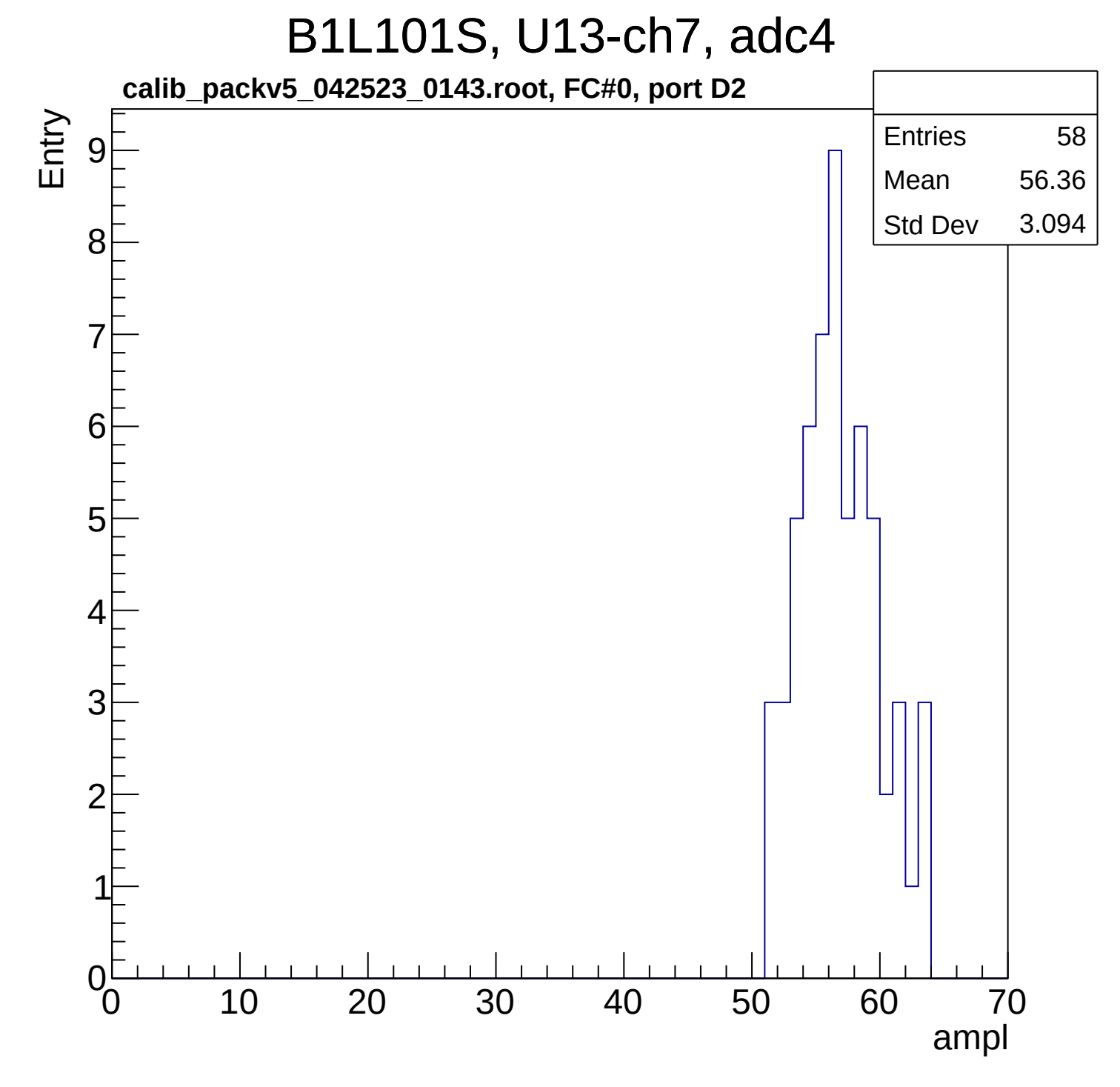
B1L101S, U13-ch7, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 56.36
Std Dev 3.094
Entry
ampl
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3
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5
6
7
8
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0
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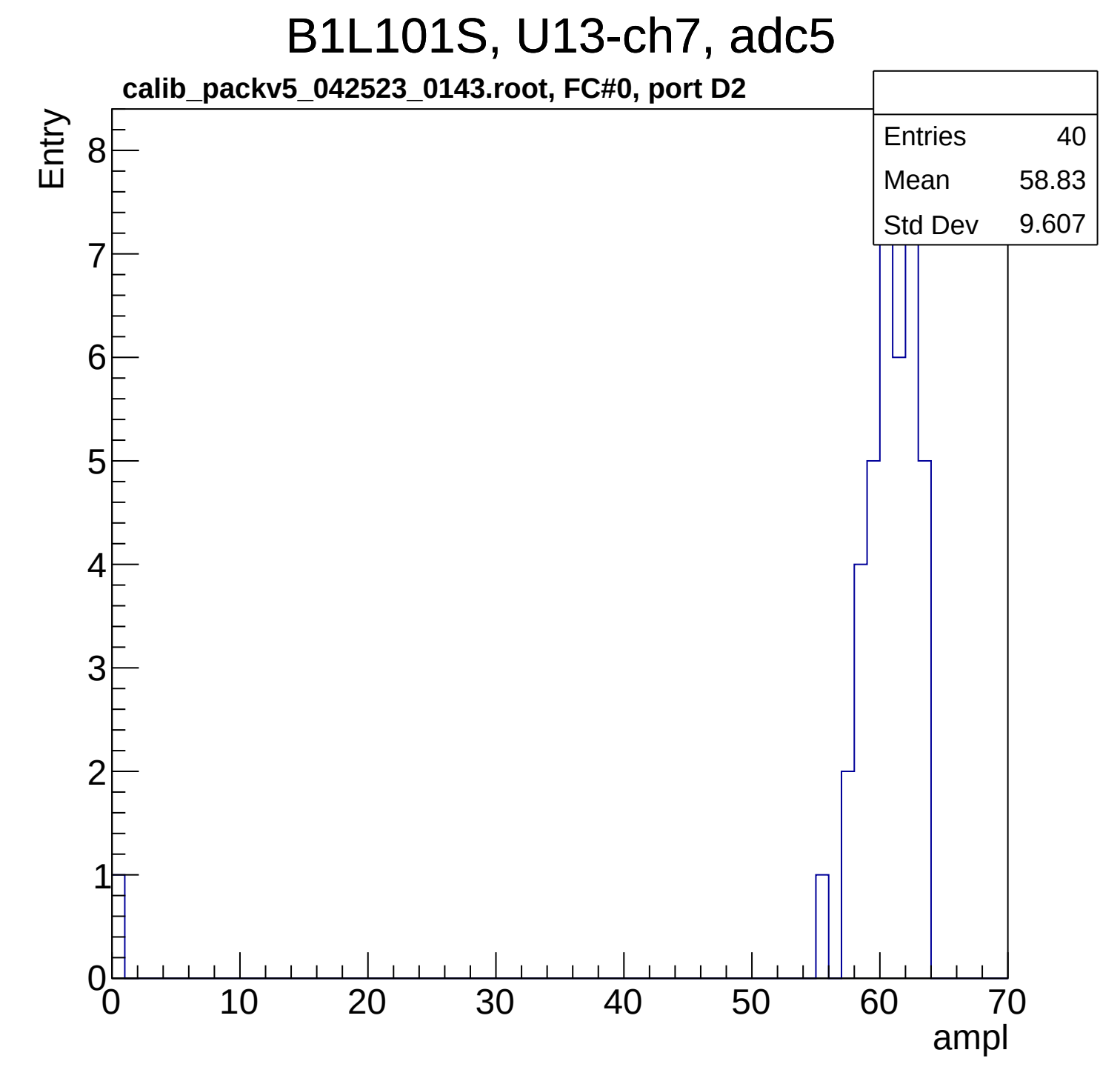
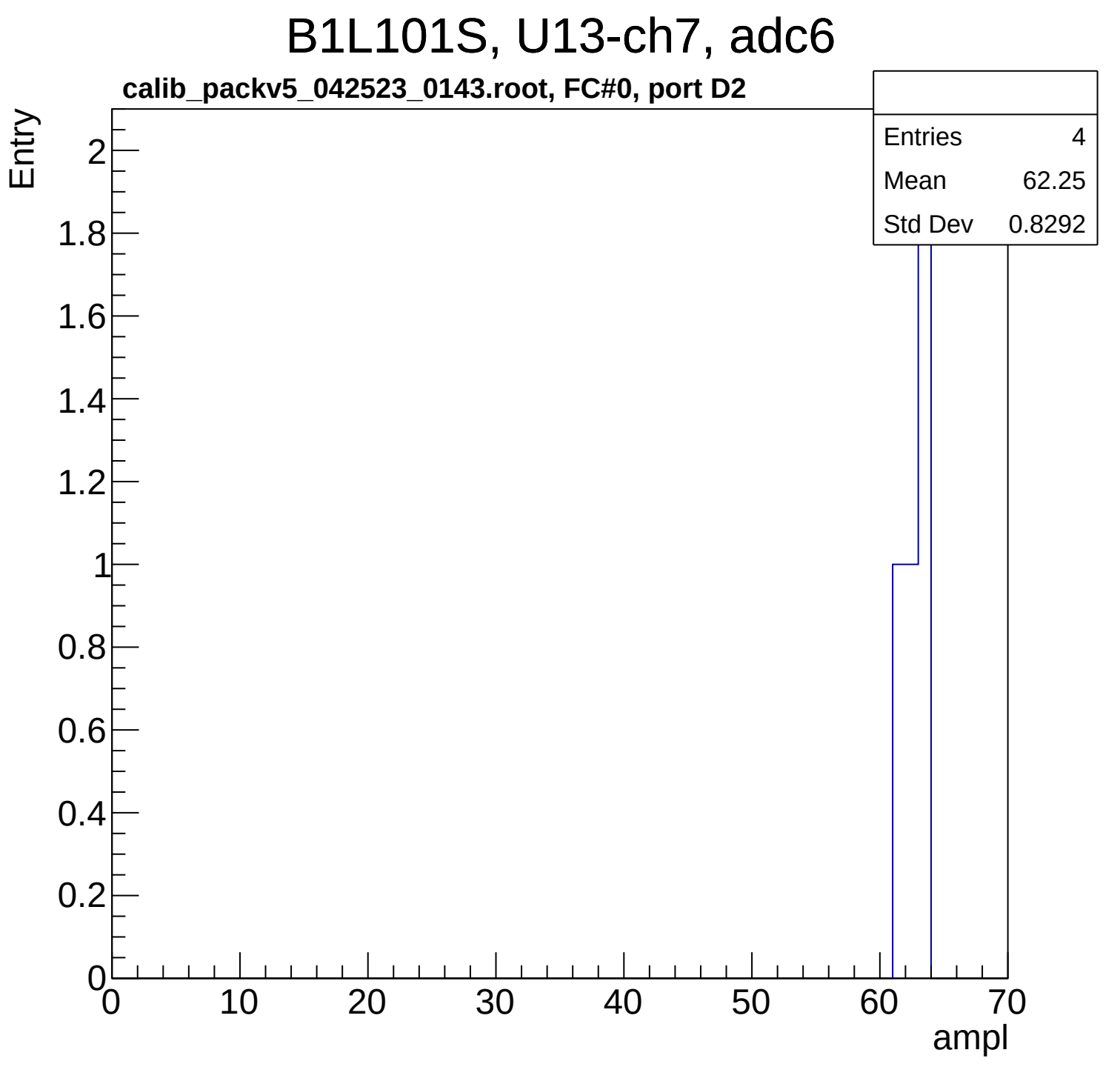

B1L101S, U13-ch7, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 58.83
Std Dev 9.607
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch7, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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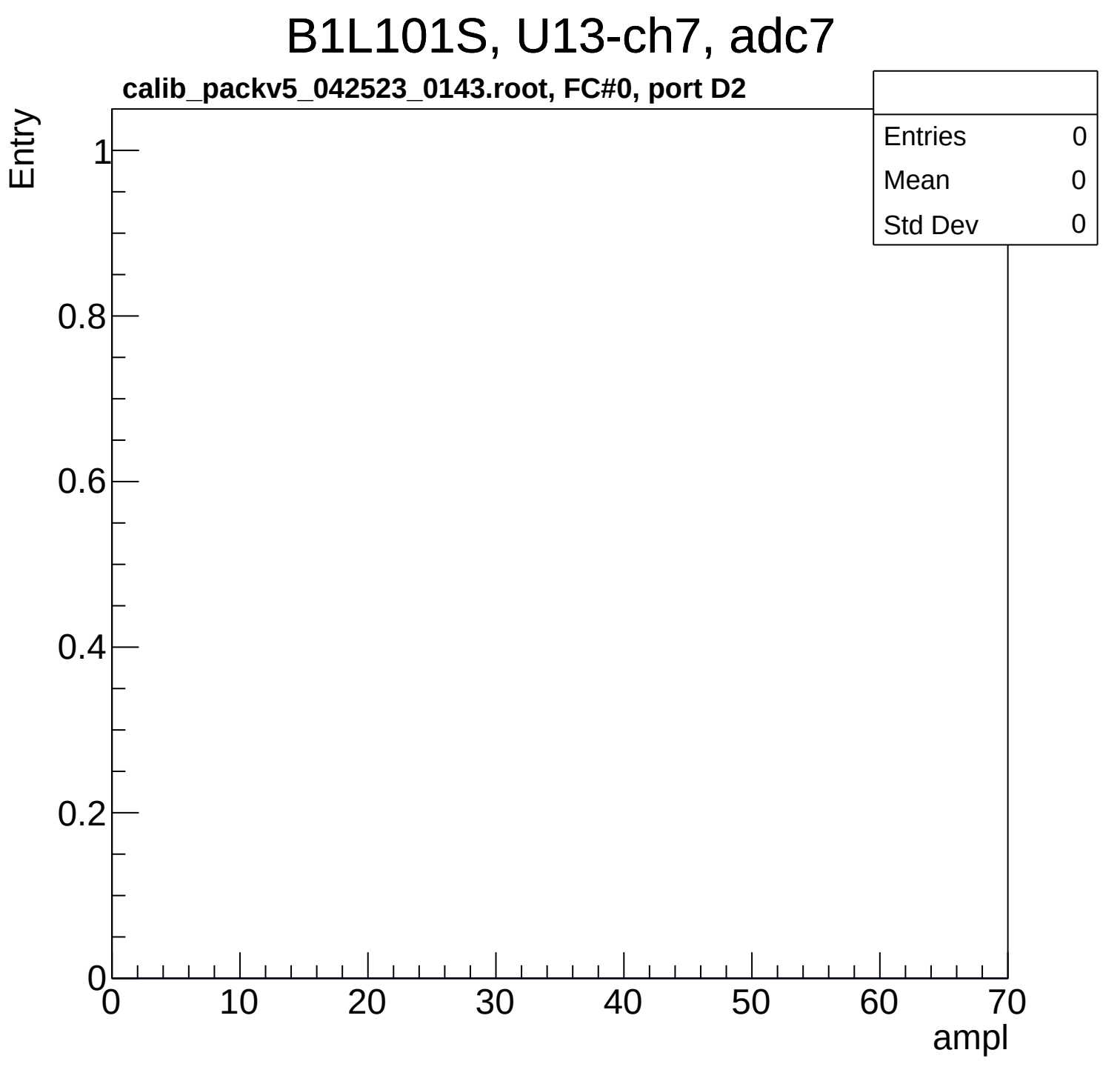
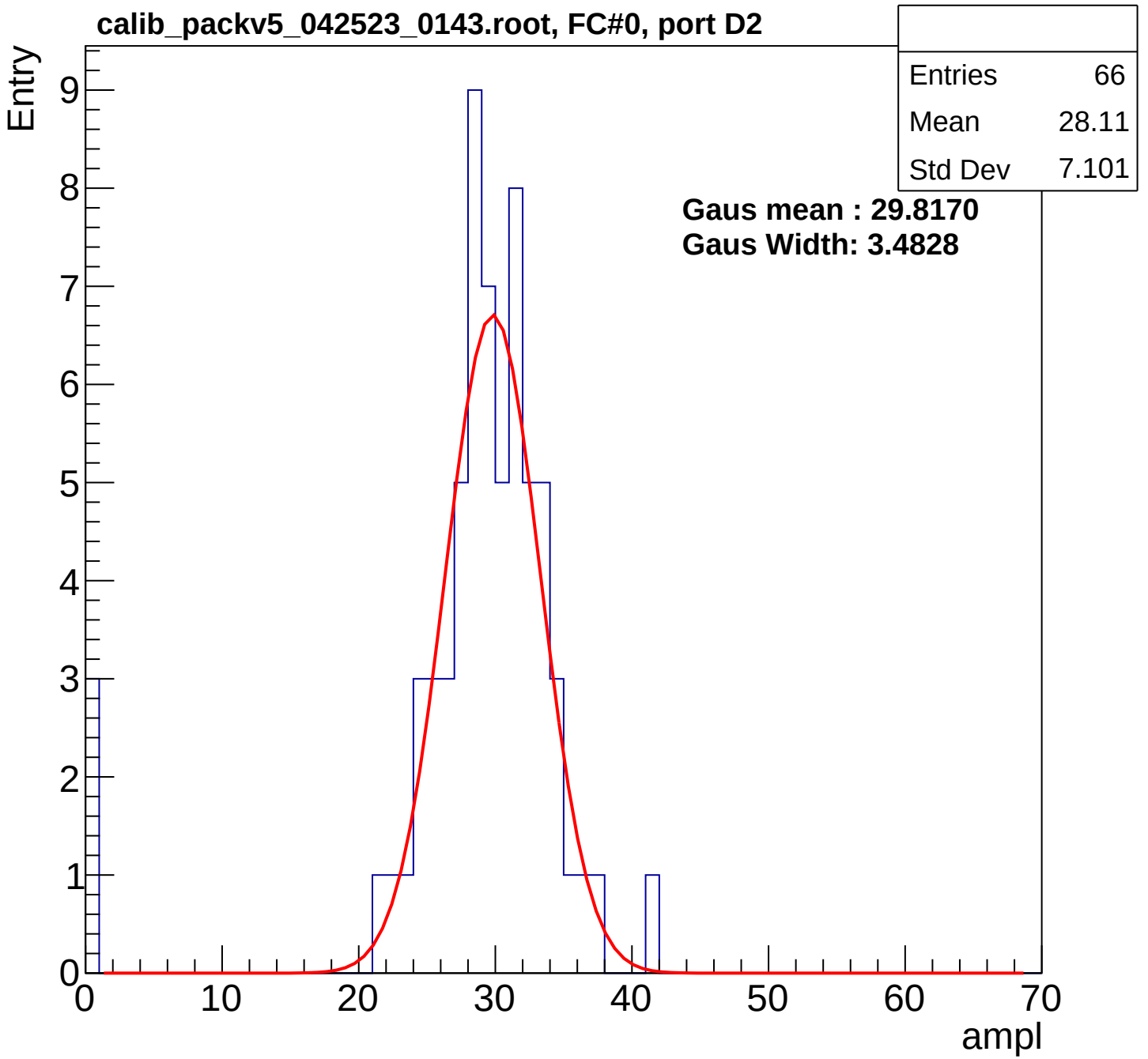
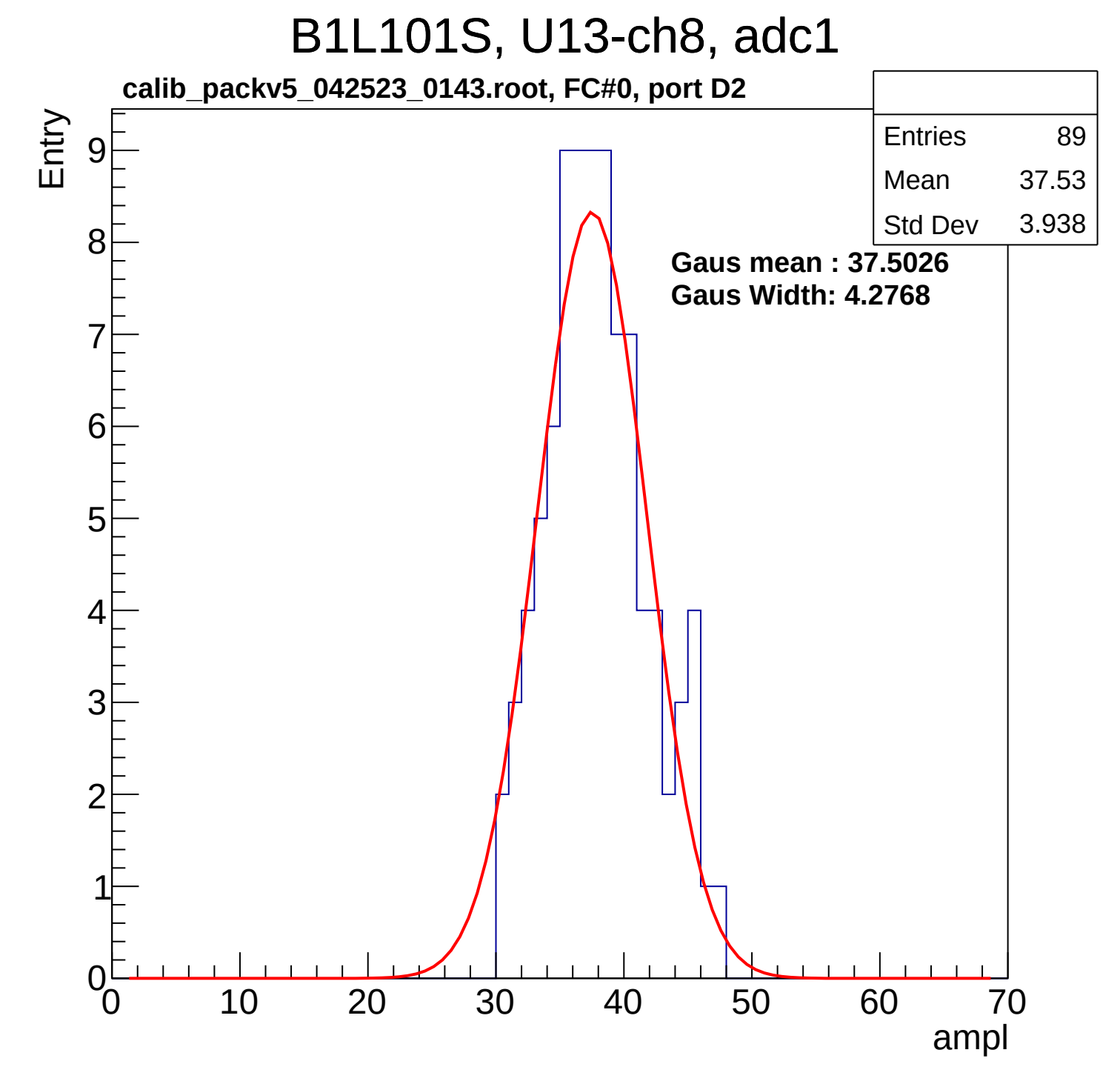

B1L101S, U13-ch7, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch8, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 28.11
Std Dev 7.101
Gaus mean : 29.8170
Gaus Width: 3.4828
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L101S, U13-ch8, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 89
Mean 37.53
Std Dev 3.938
Gaus mean : 37.5026
Gaus Width: 4.2768
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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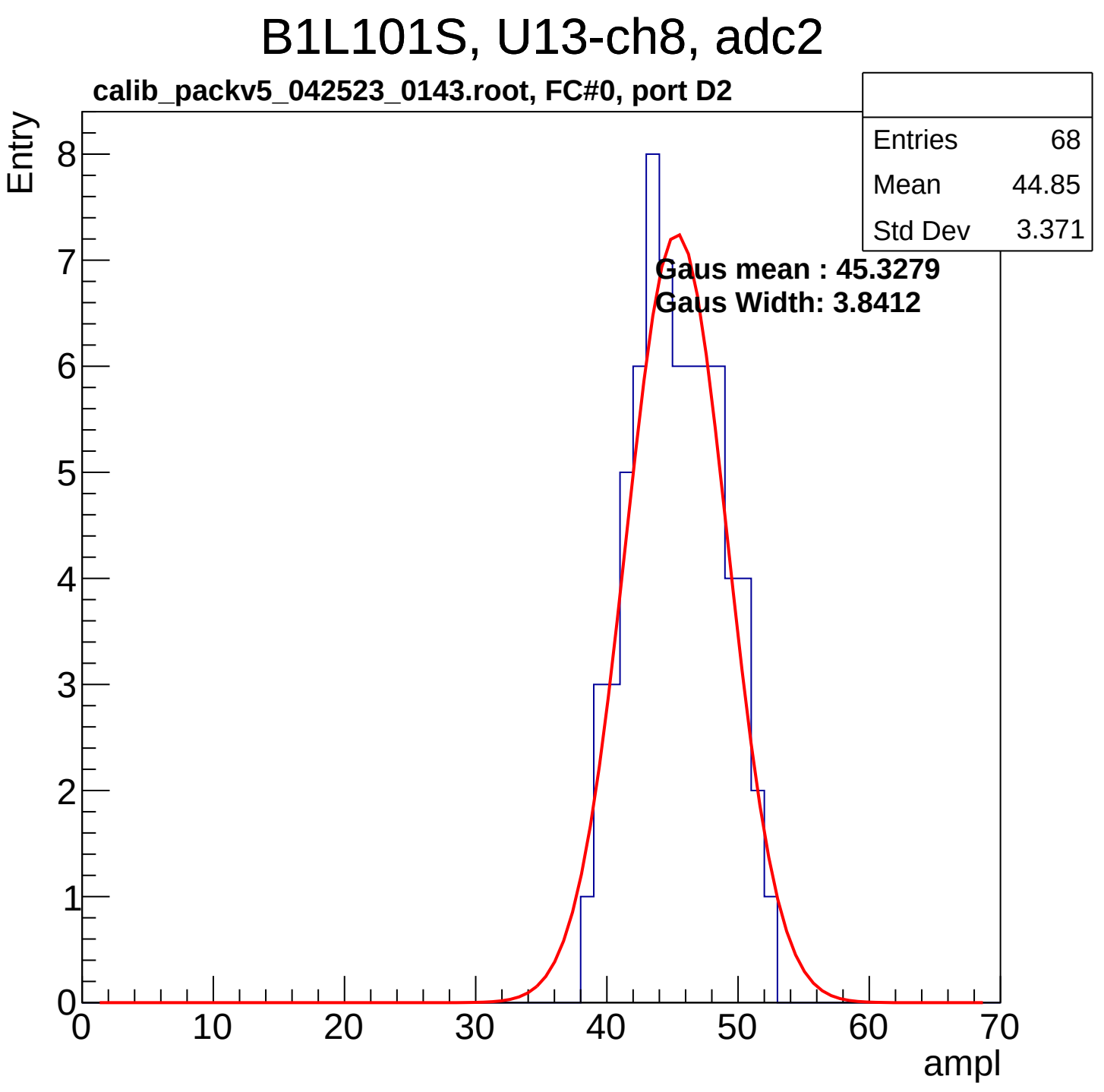
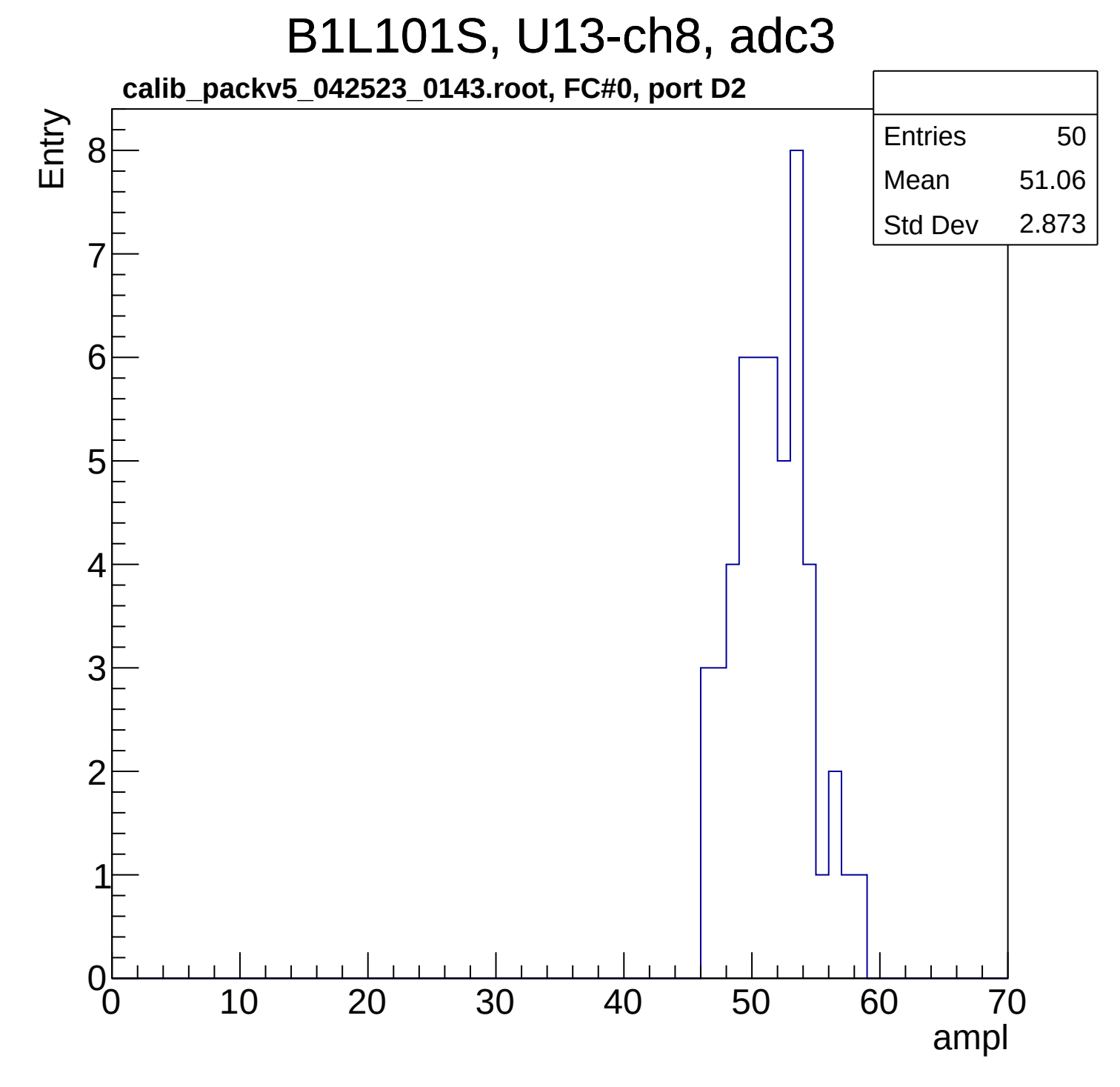
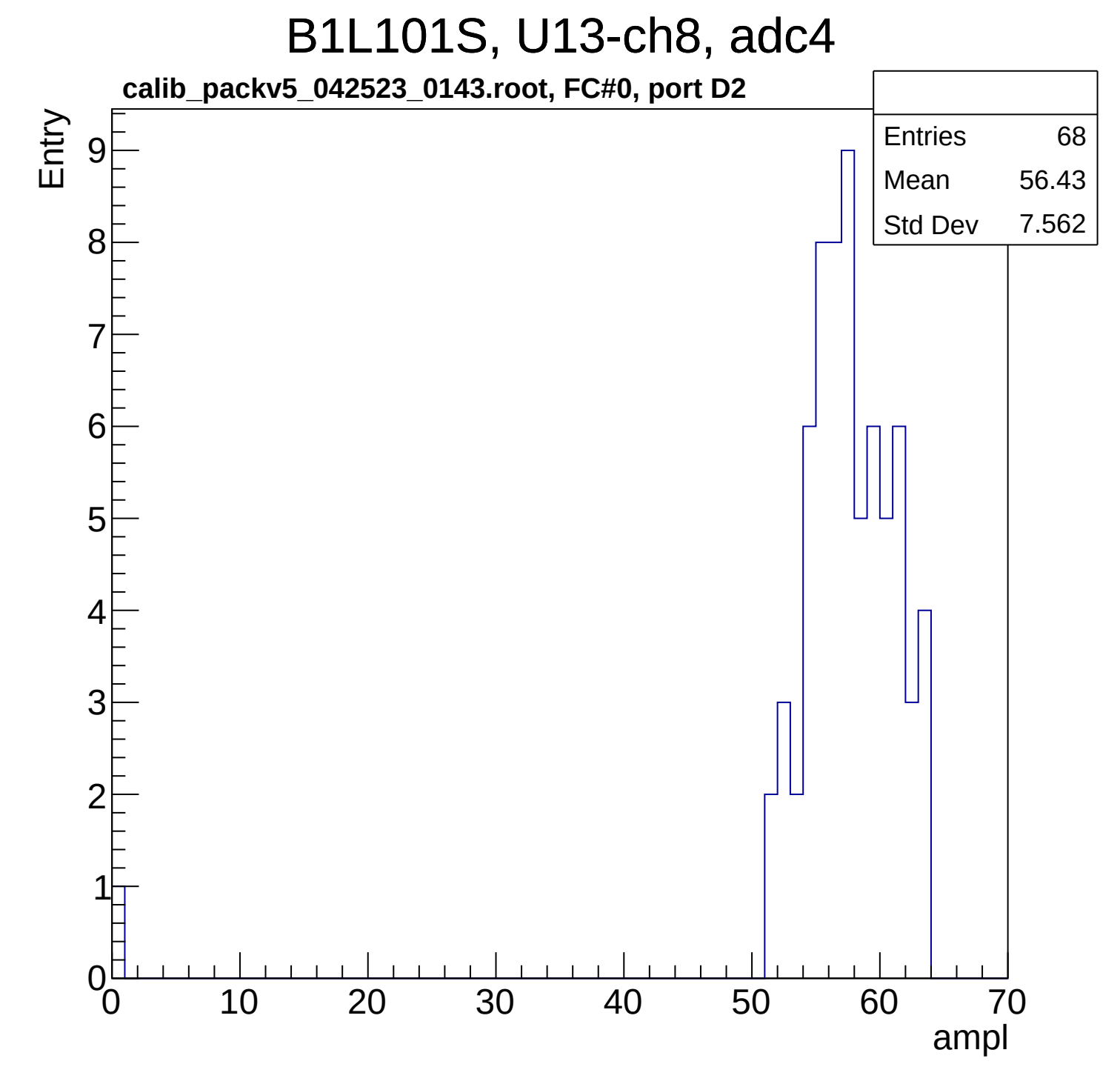
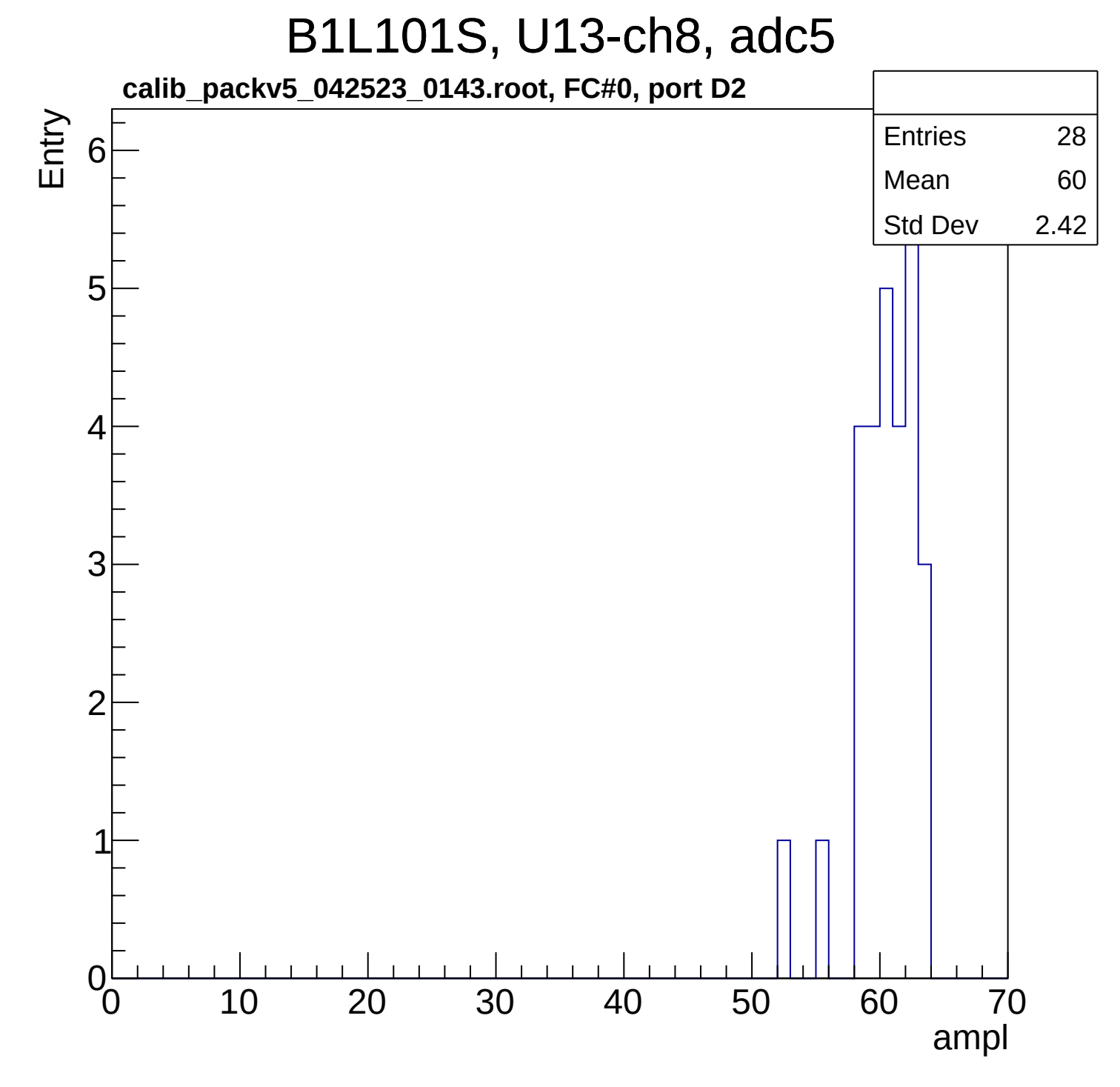
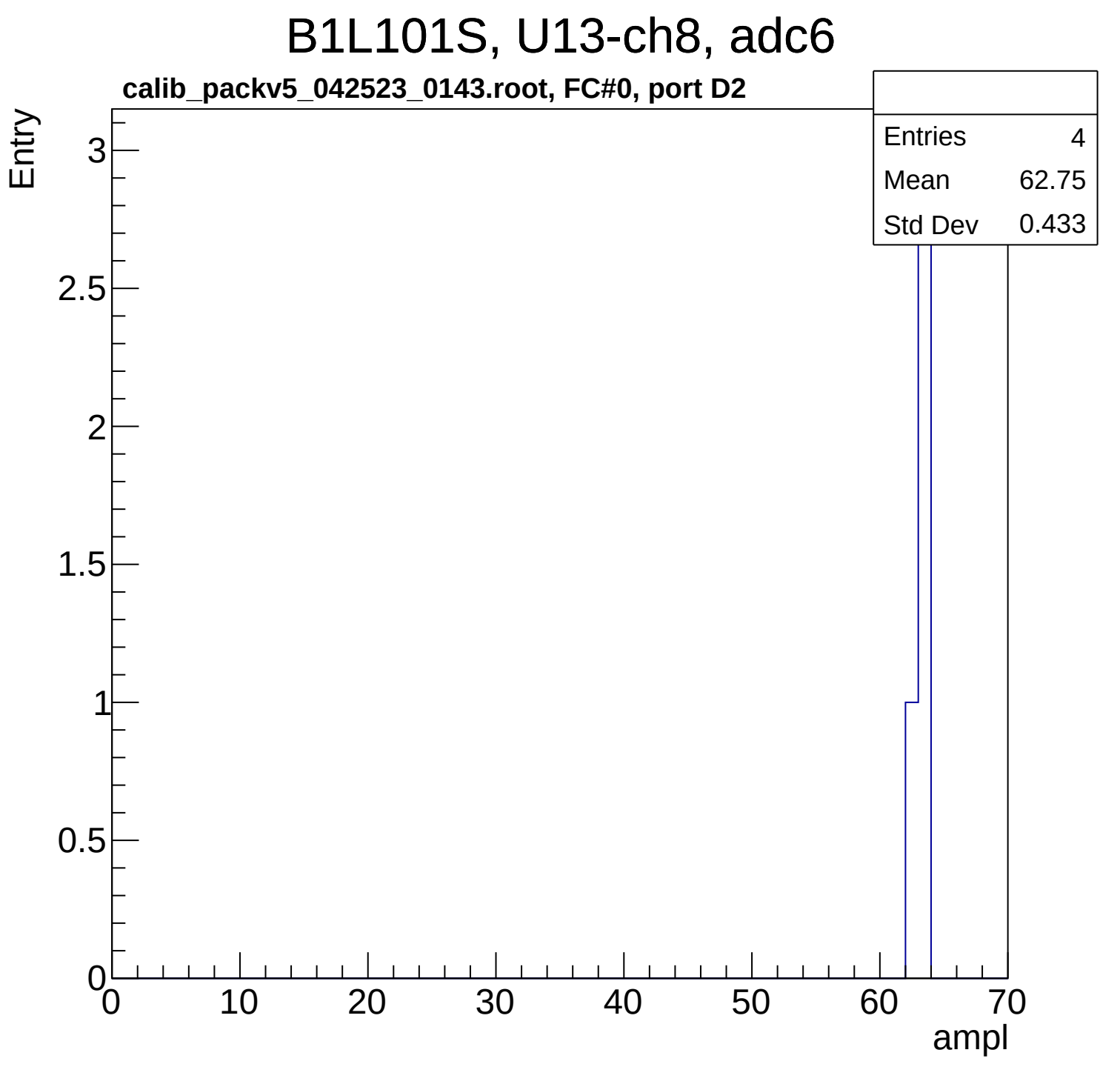

B1L101S, U13-ch8, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 44.85
Std Dev 3.371
Gaus mean : 45.3279
Gaus Width: 3.8412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch8, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 50
Mean 51.06
Std Dev 2.873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch8, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 56.43
Std Dev 7.562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch8, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 28
Mean 60
Std Dev 2.42
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch8, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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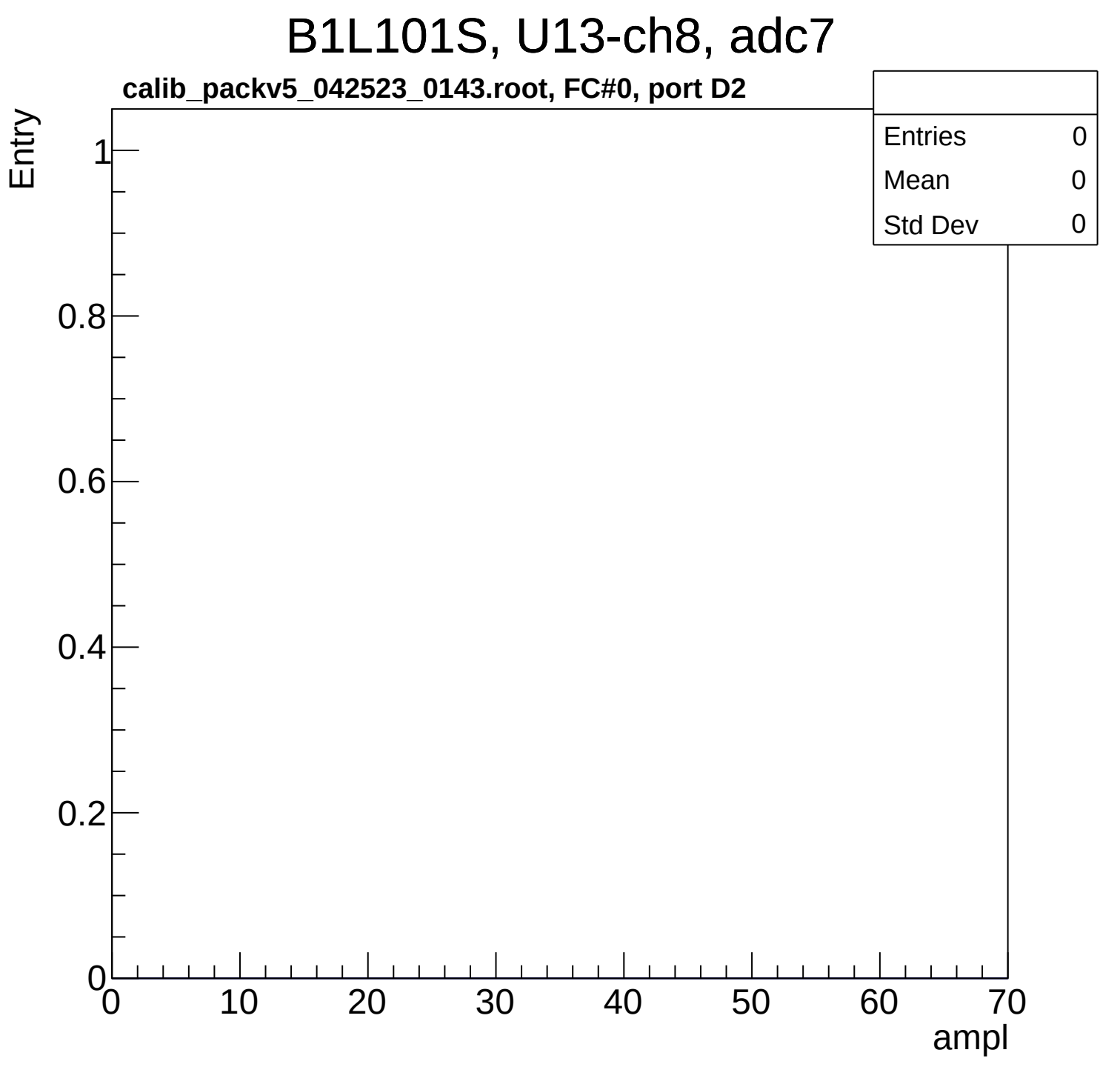
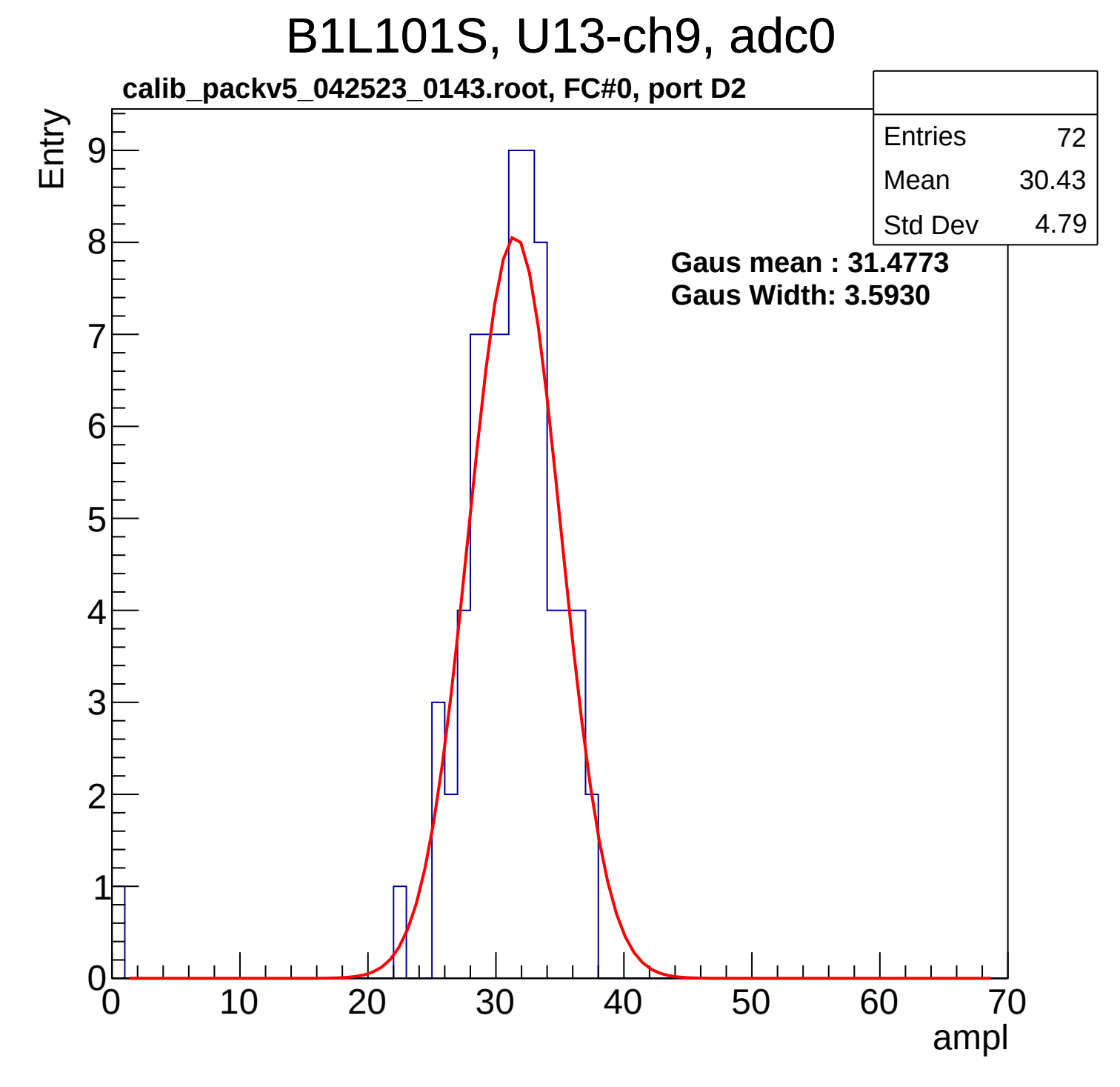
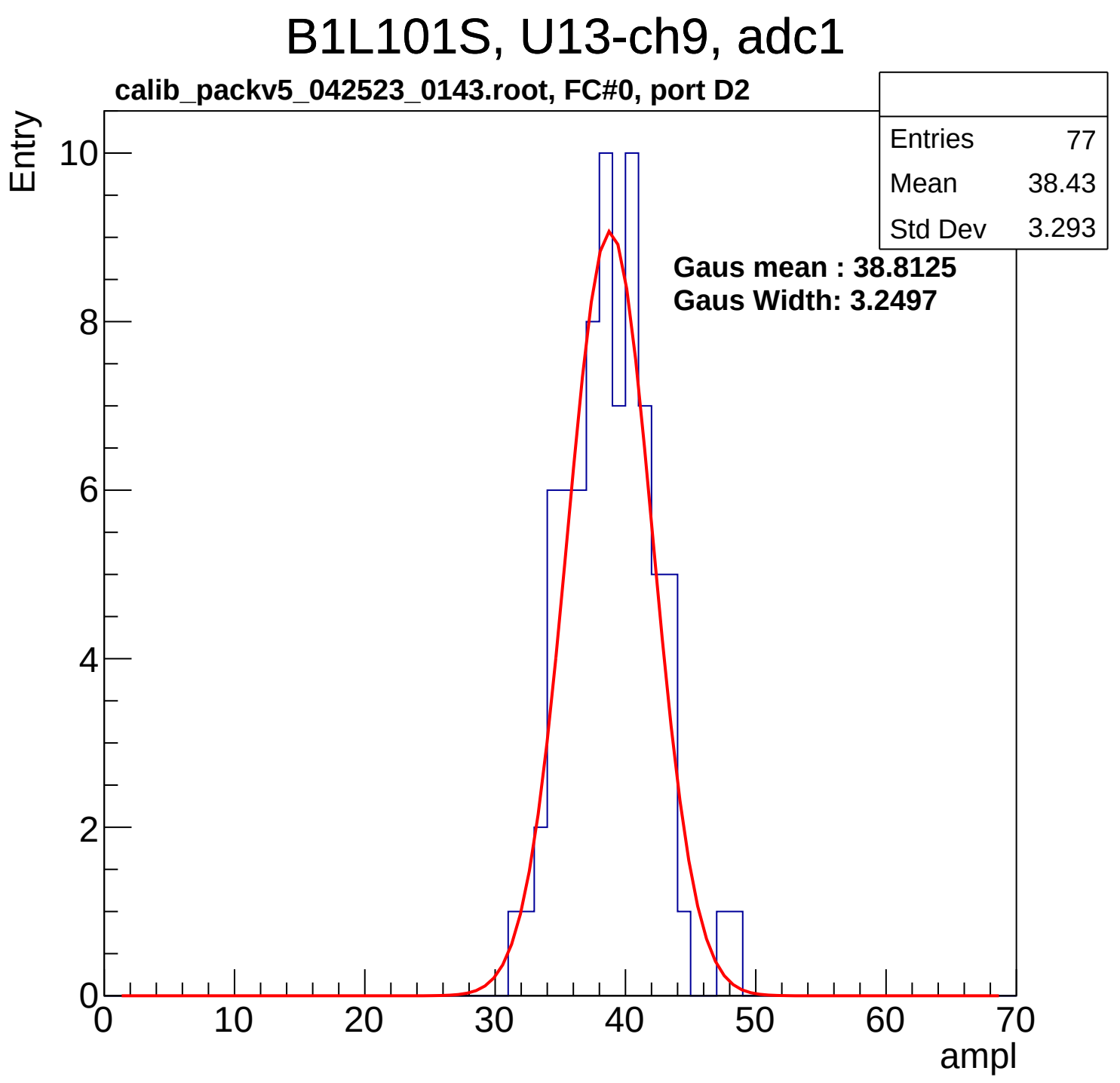
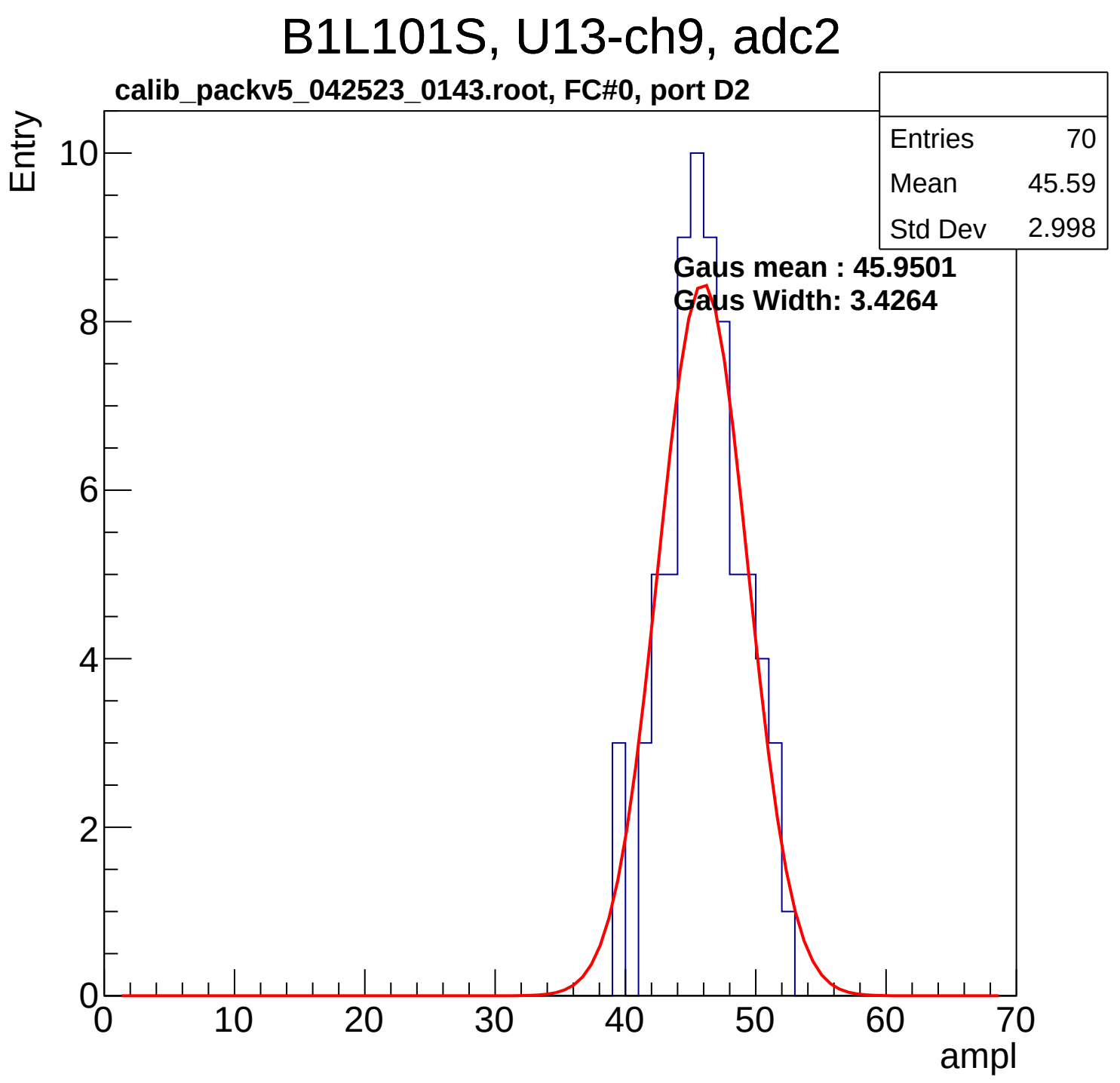
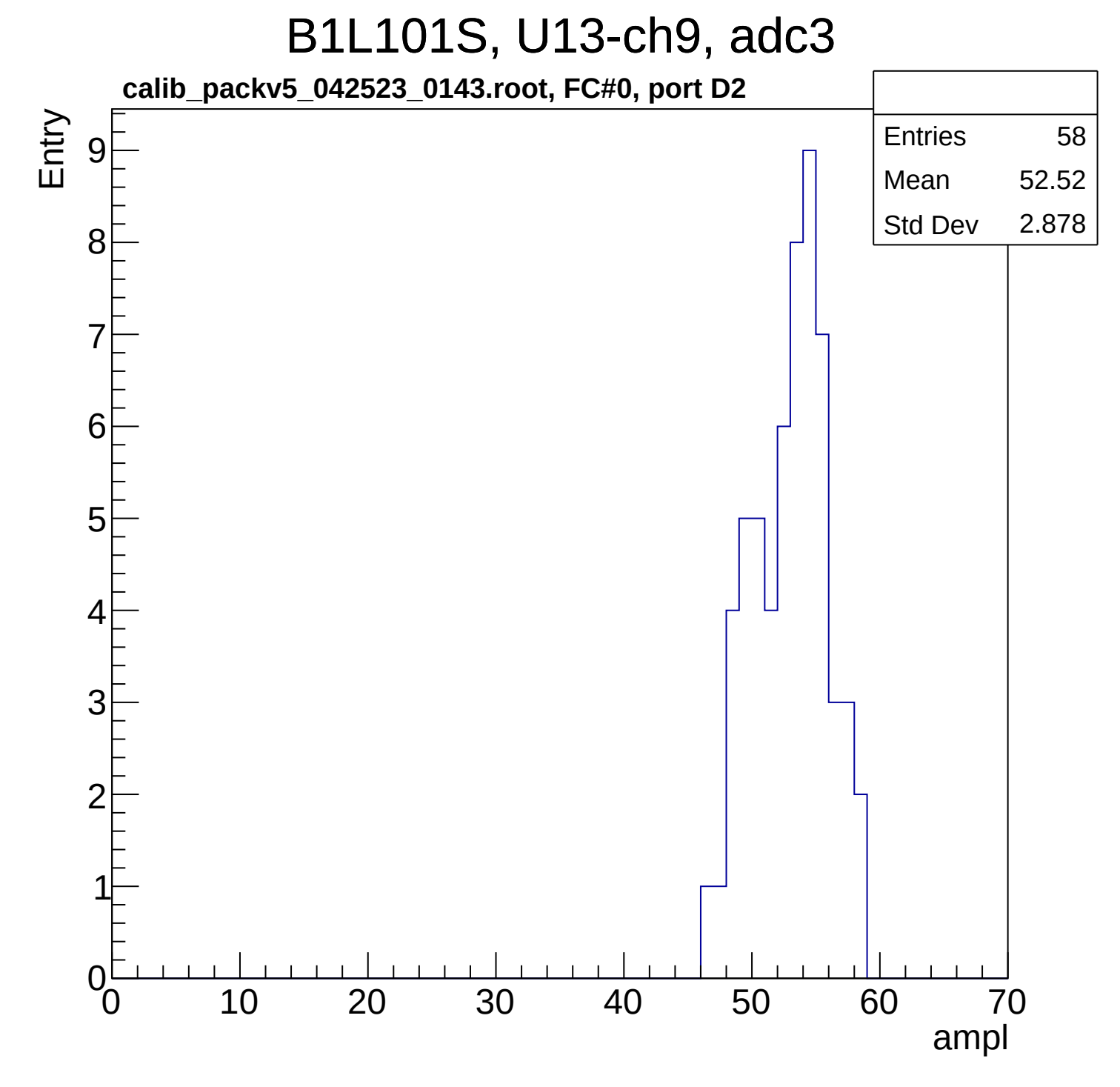
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

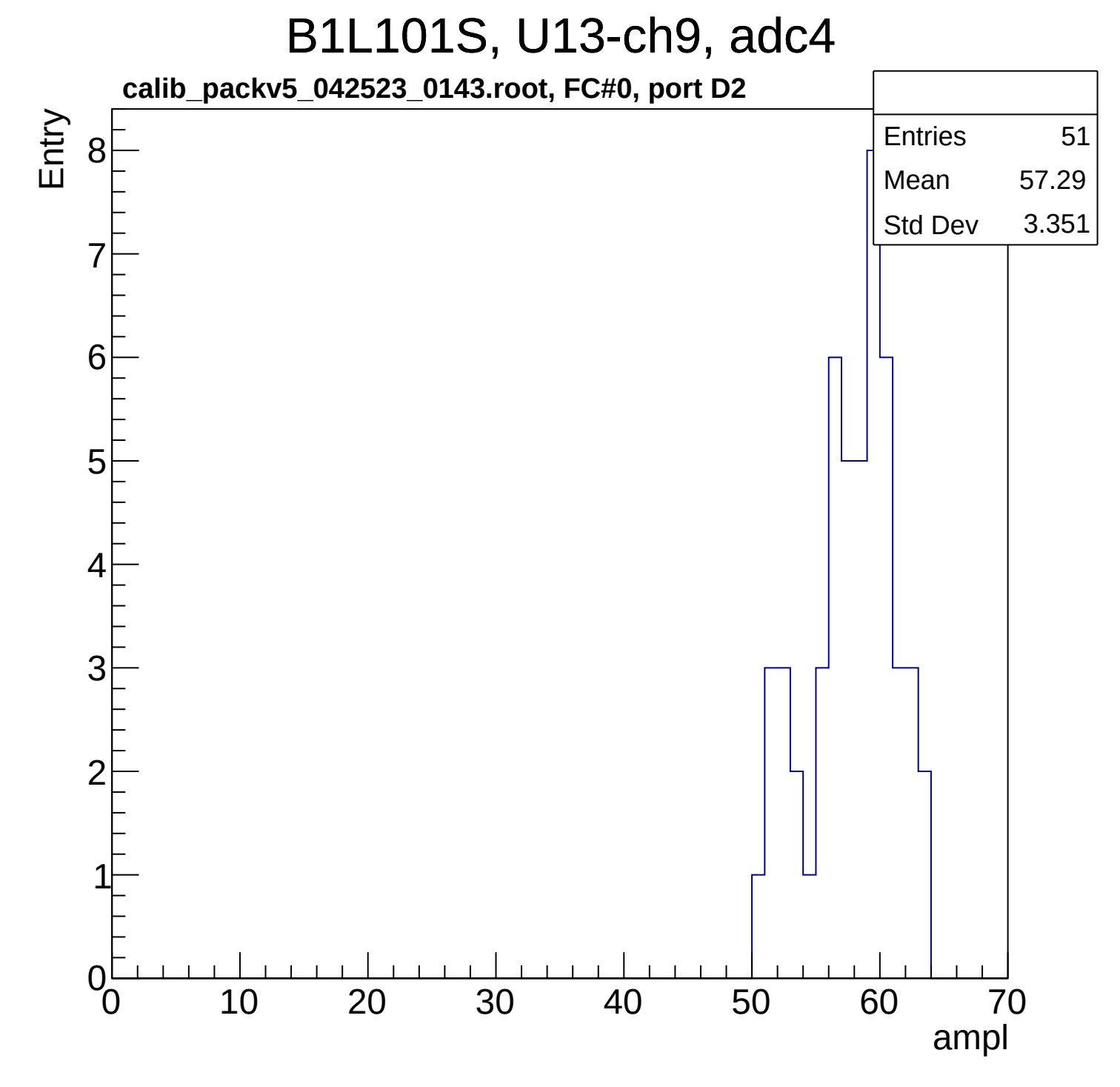

B1L101S, U13-ch8, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch9, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 30.43
Std Dev 4.79
Gaus mean : 31.4773
Gaus Width: 3.5930
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch9, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 38.43
Std Dev 3.293
Gaus mean : 38.8125
Gaus Width: 3.2497
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch9, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 45.59
Std Dev 2.998
Gaus mean : 45.9501
Gaus Width: 3.4264
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch9, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 52.52
Std Dev 2.878
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch9, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 57.29
Std Dev 3.351
Entry
ampl
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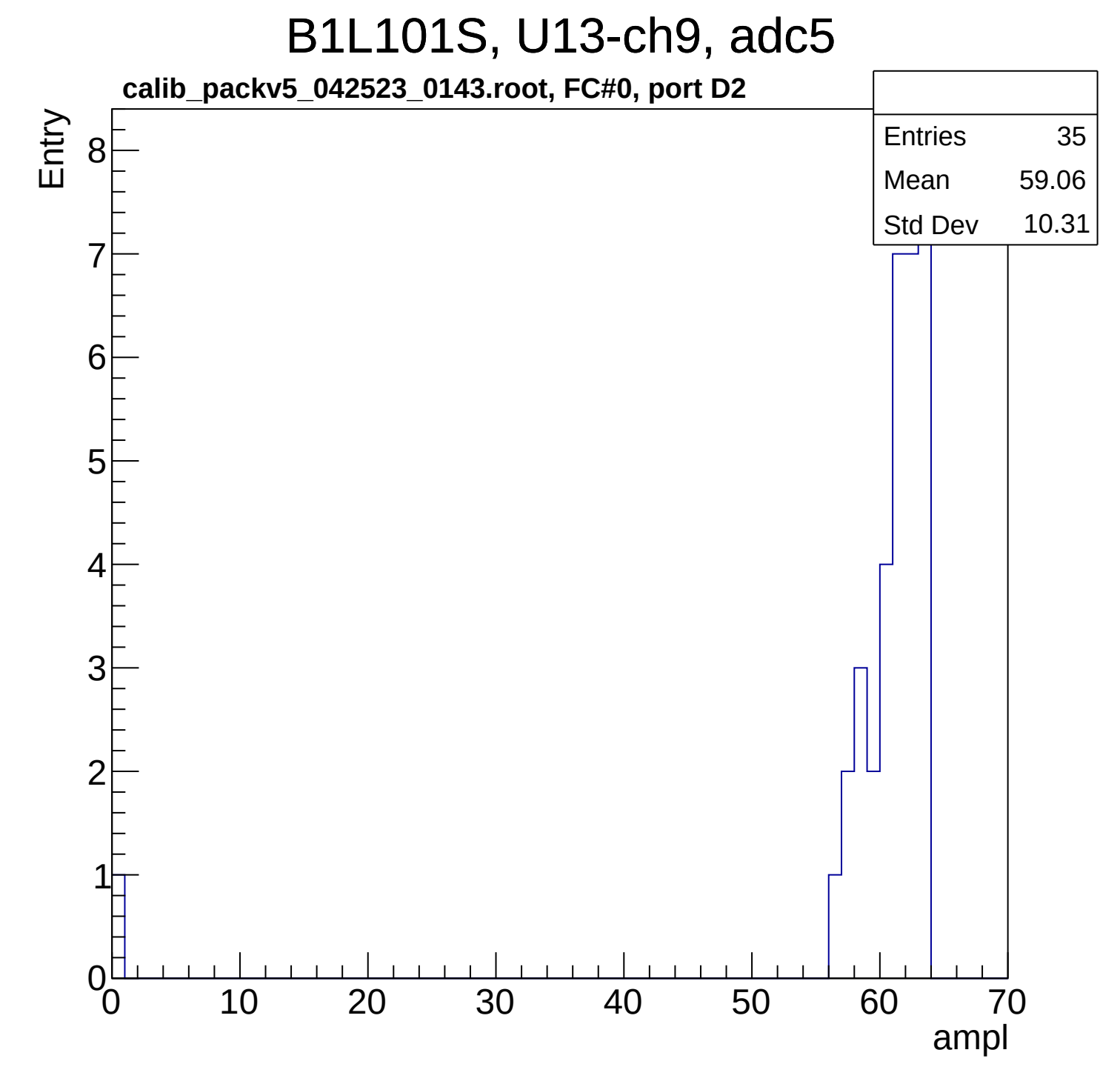
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch9, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 35
Mean 59.06
Std Dev 10.31
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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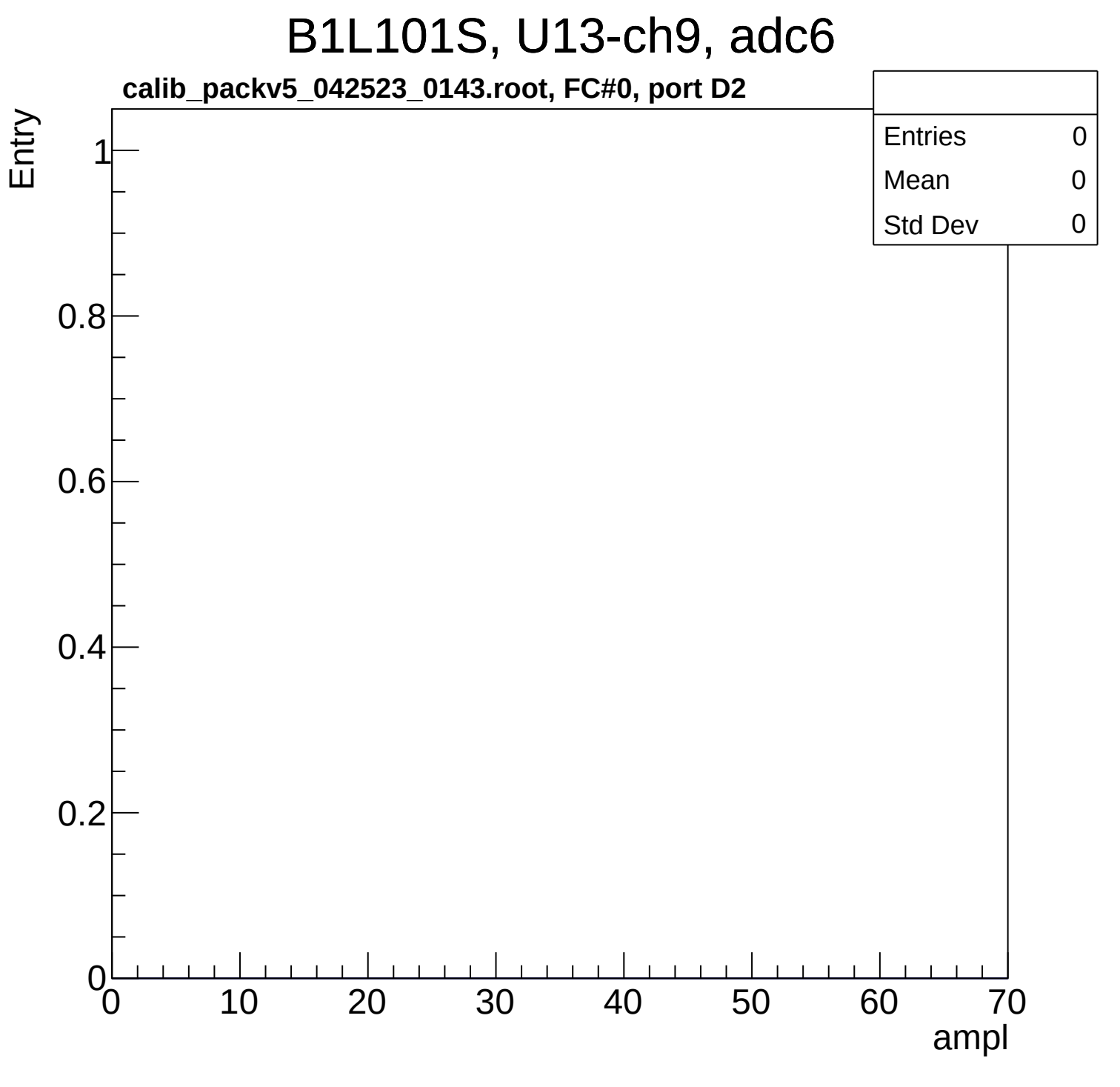
60
70


B1L101S, U13-ch9, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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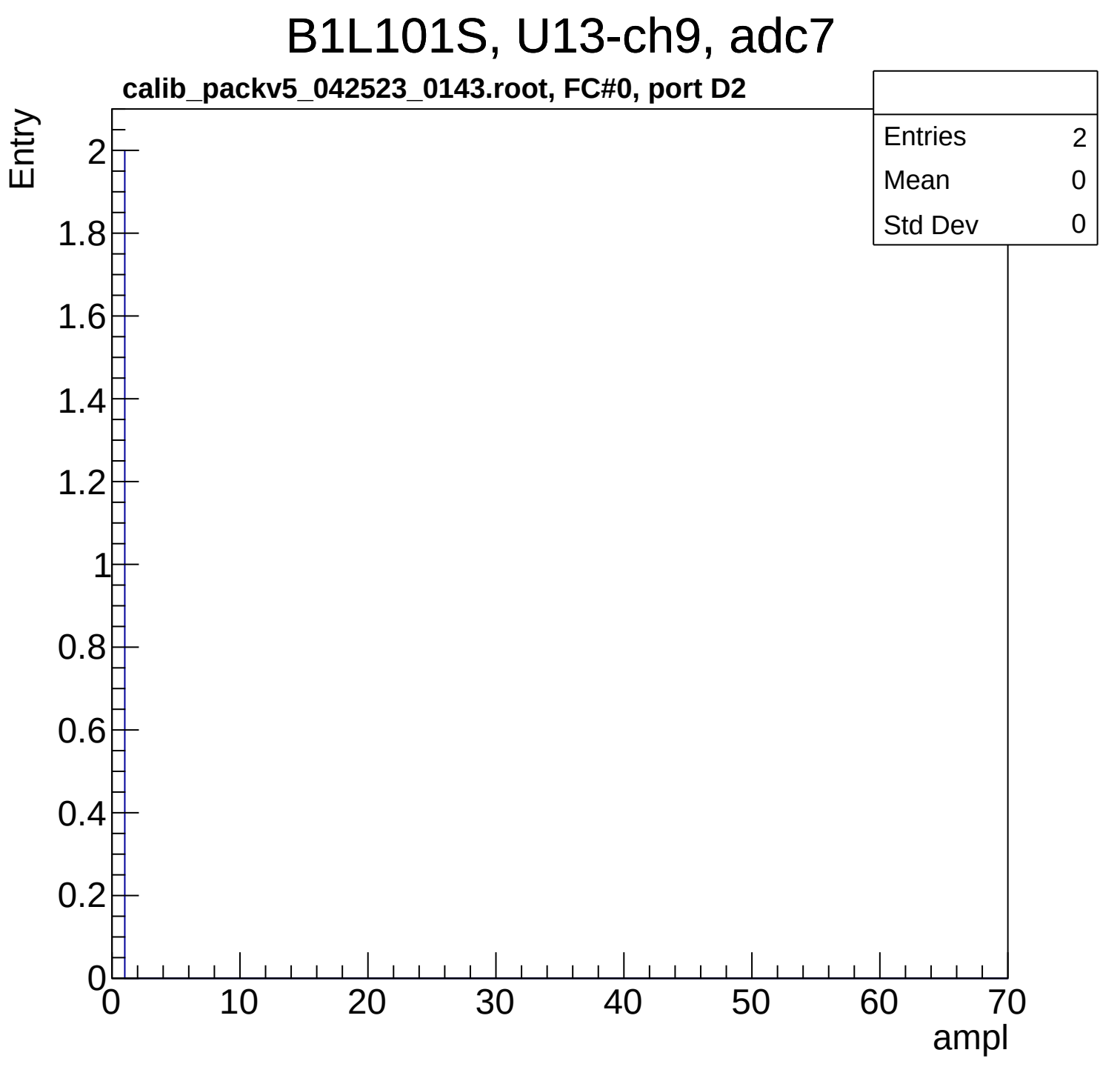
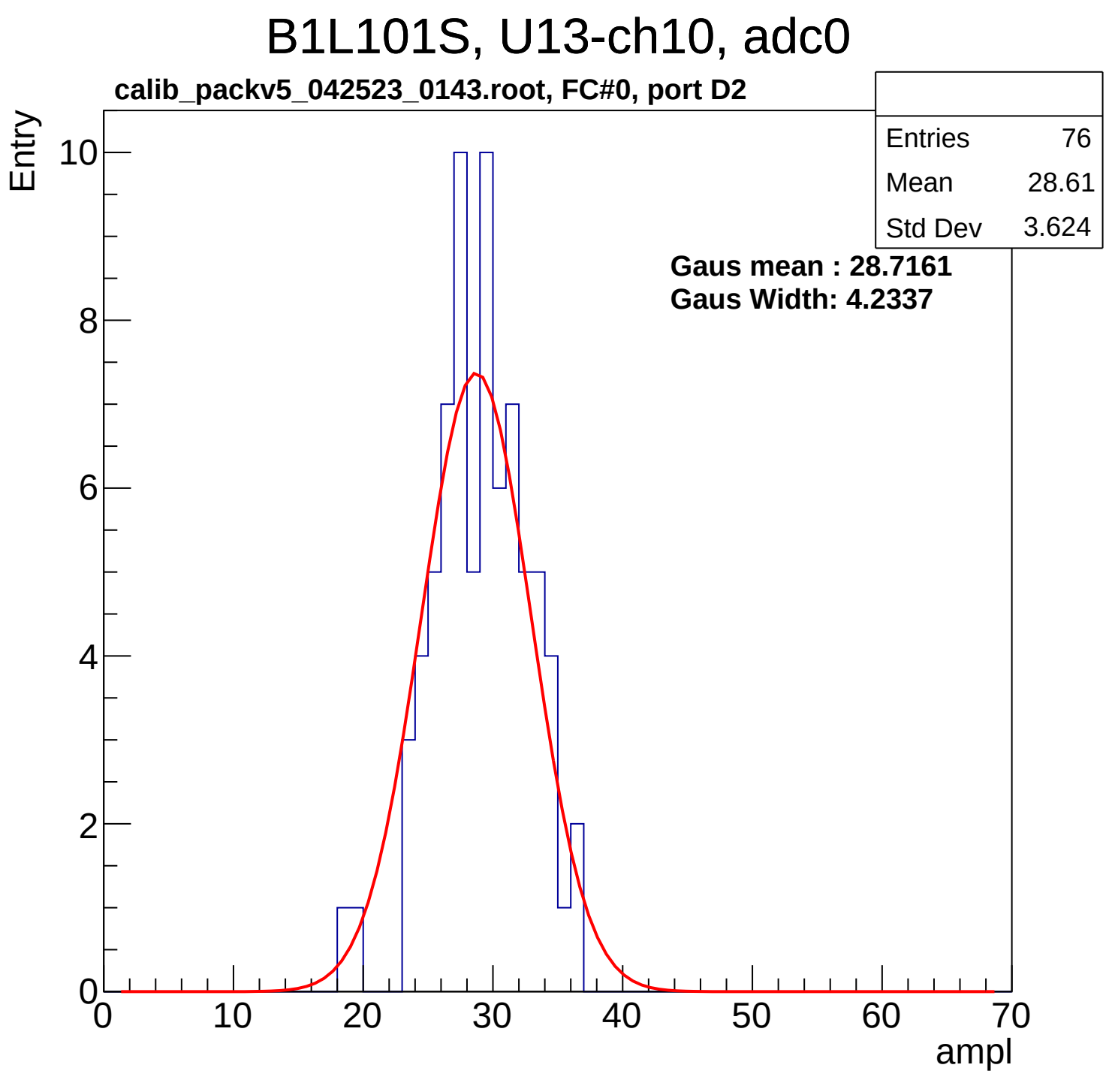
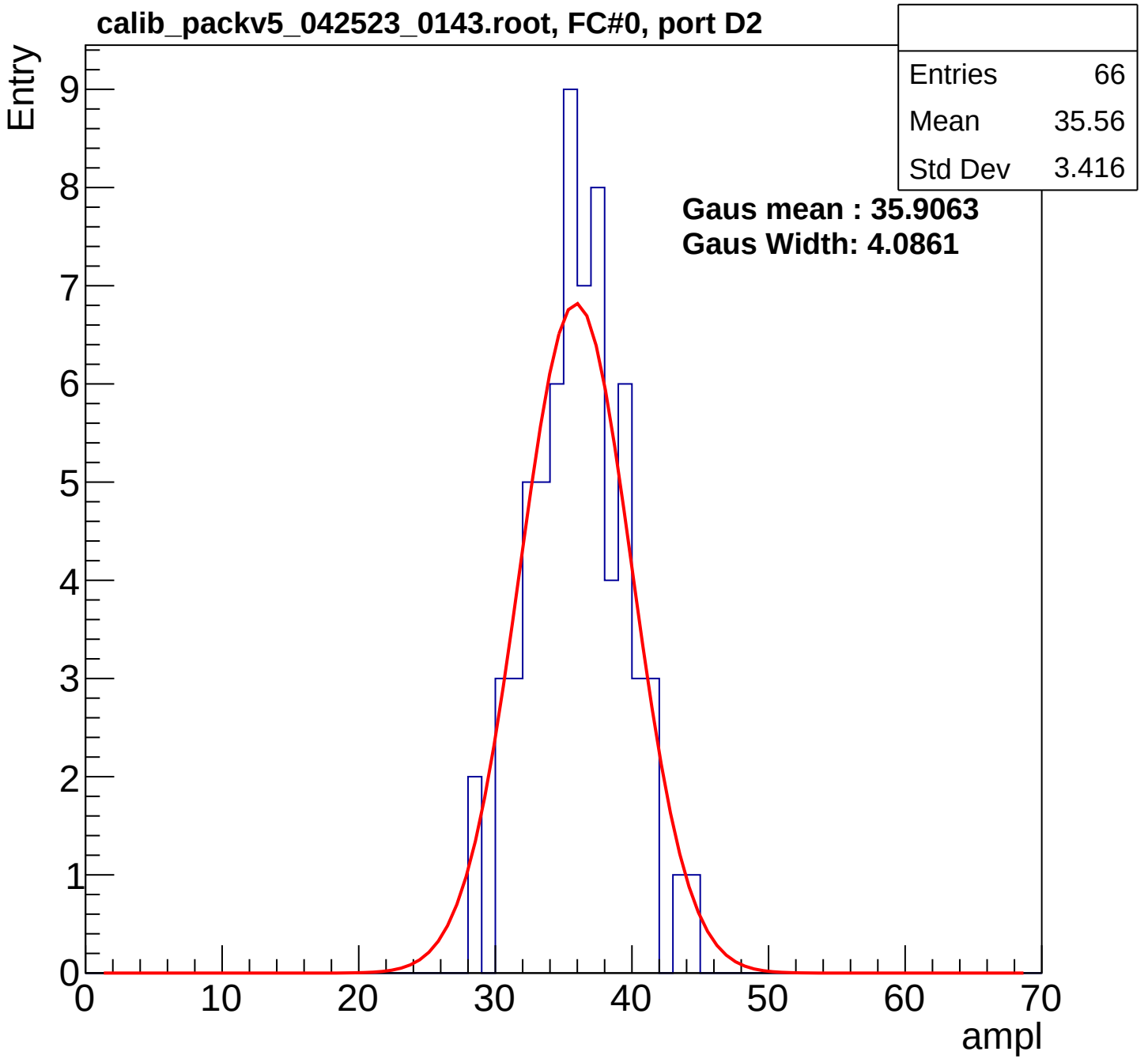
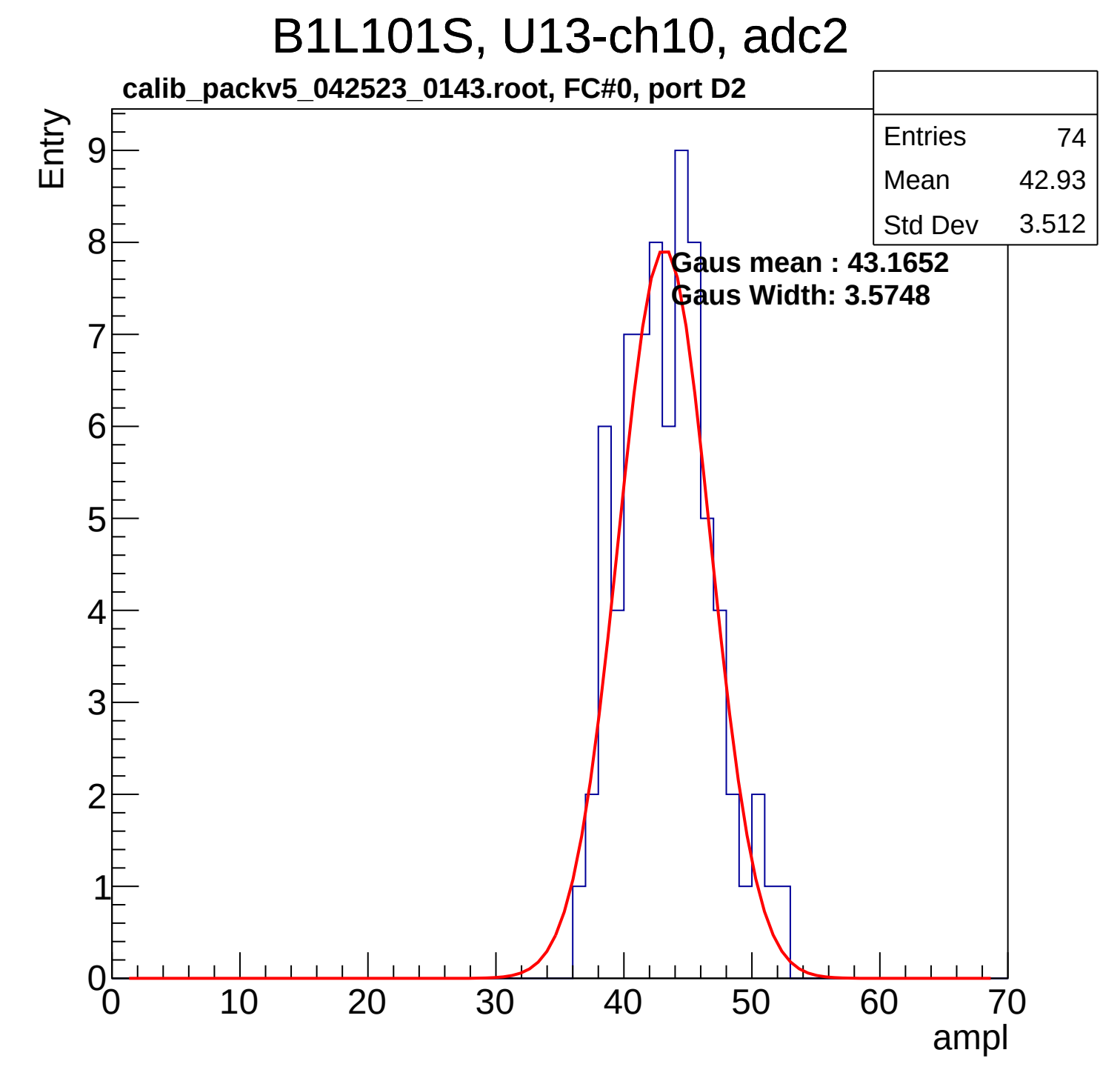
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch9, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch10, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 28.61
Std Dev 3.624
Gaus mean : 28.7161
Gaus Width: 4.2337
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch10, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 35.56
Std Dev 3.416
Gaus mean : 35.9063
Gaus Width: 4.0861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch10, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 42.93
Std Dev 3.512
Gaus mean : 43.1652
Gaus Width: 3.5748
Entry
ampl
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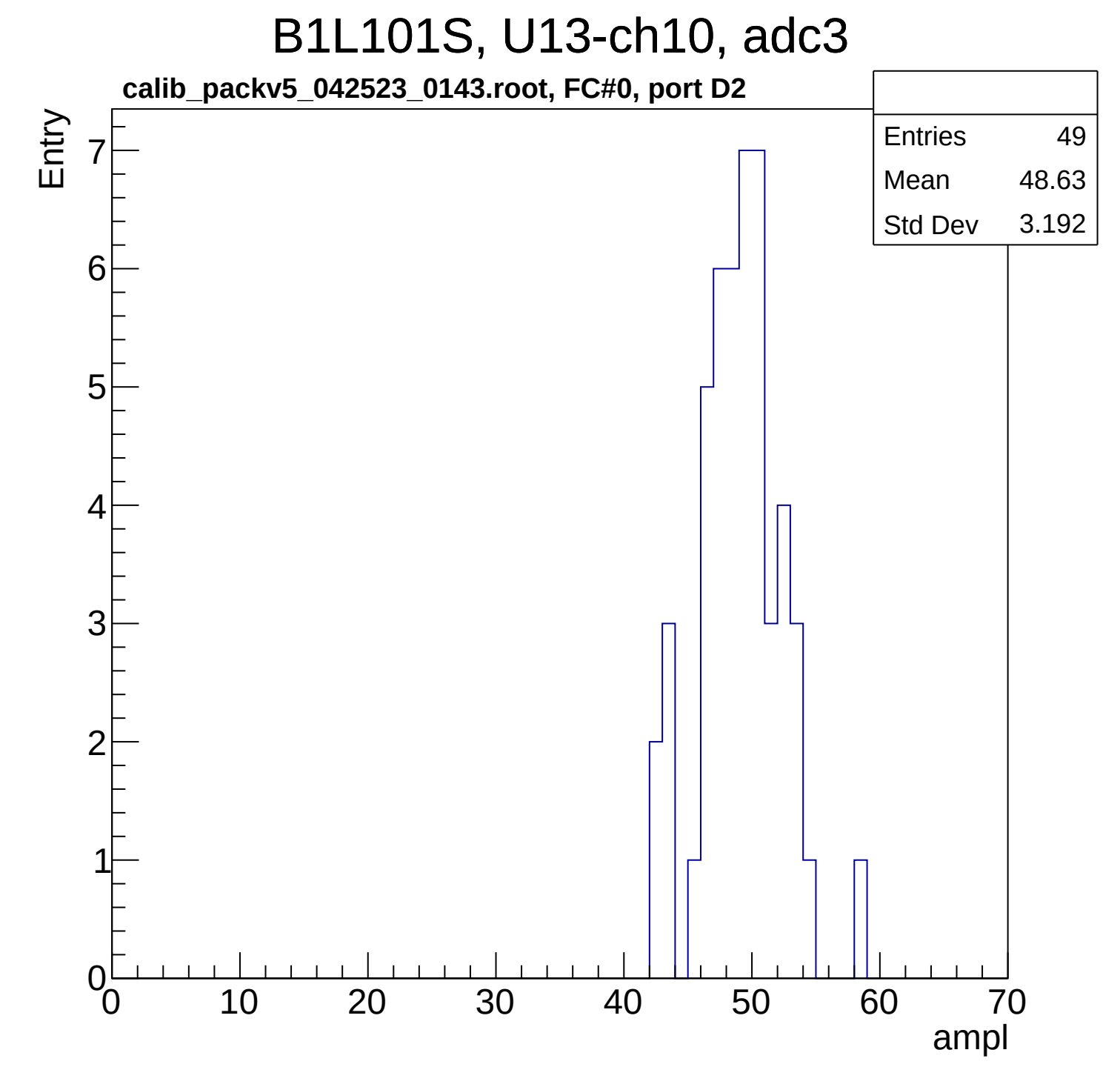
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch10, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 48.63
Std Dev 3.192
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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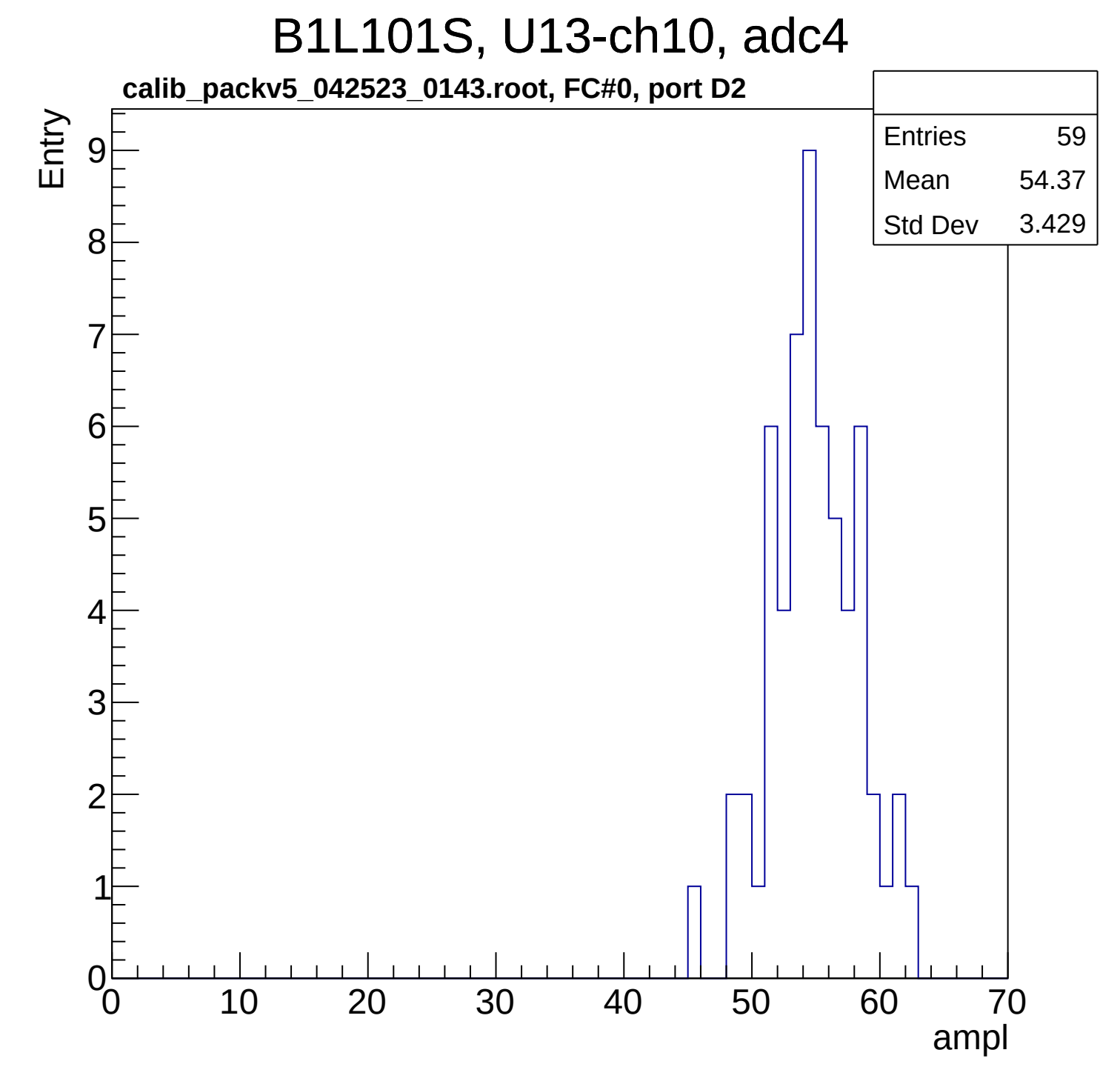
B1L101S, U13-ch10, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 54.37
Std Dev 3.429
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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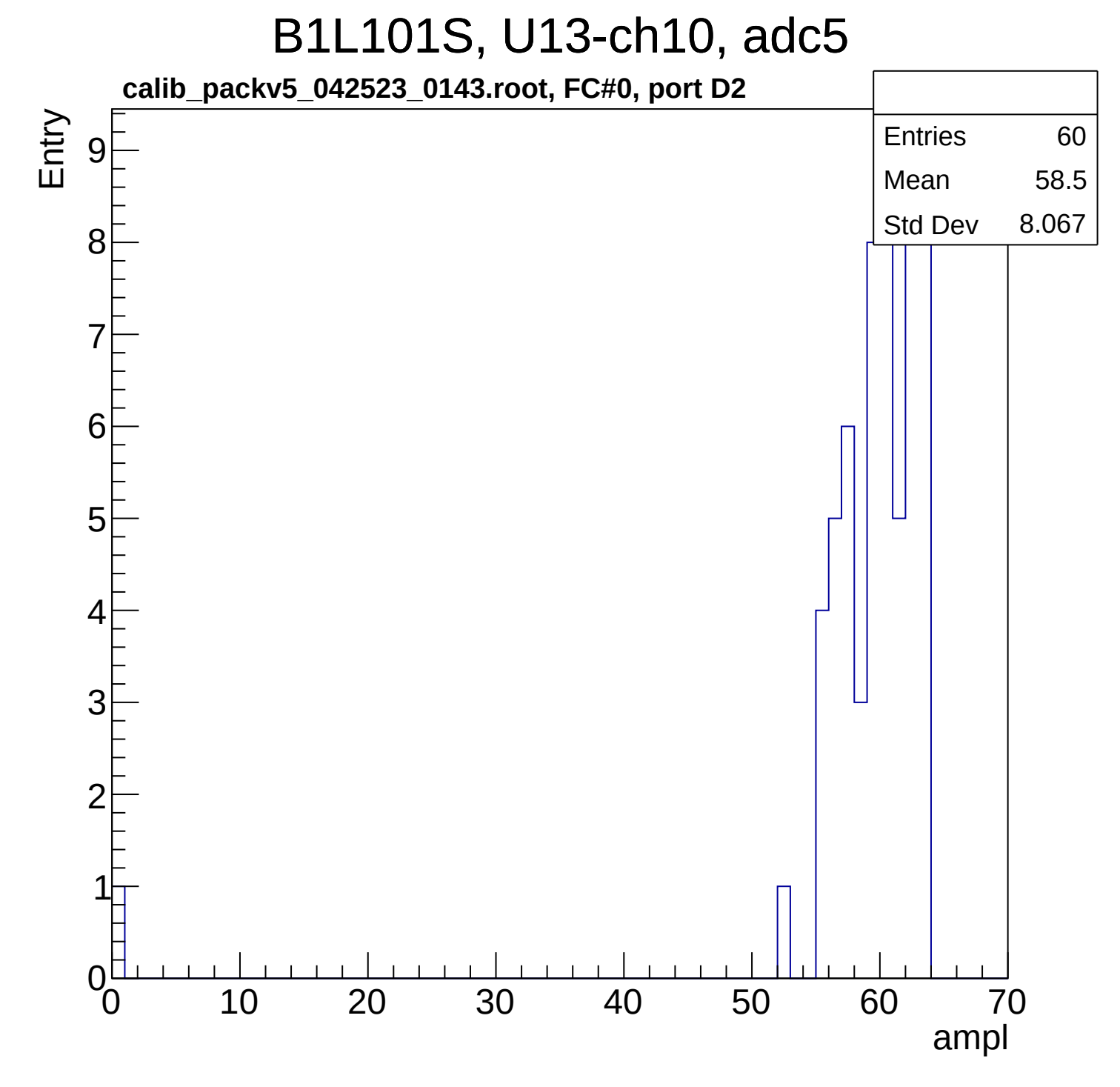
40
50
60
70


B1L101S, U13-ch10, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 58.5
Std Dev 8.067
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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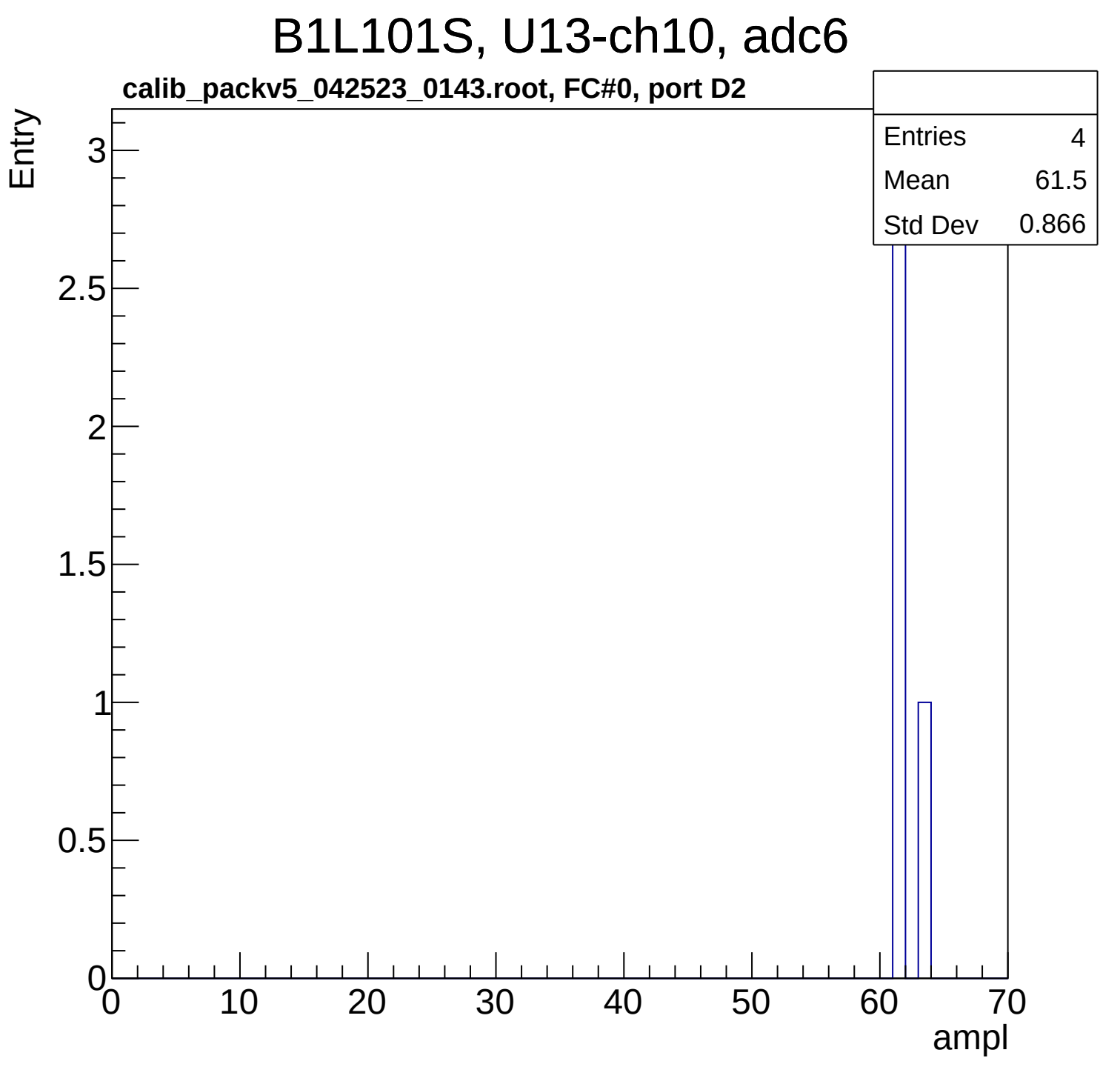
B1L101S, U13-ch10, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 61.5
Std Dev 0.866
Entry
ampl
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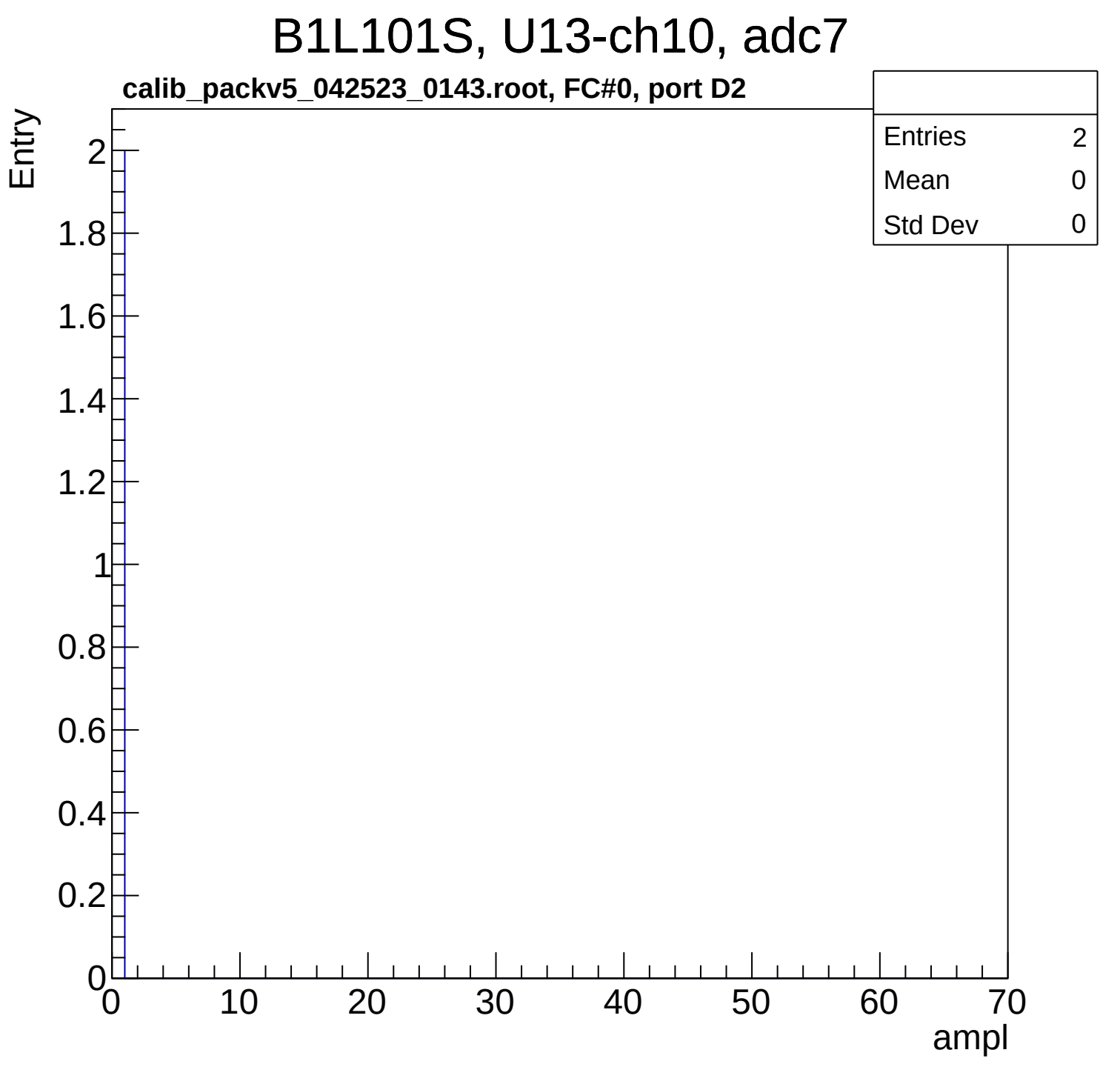
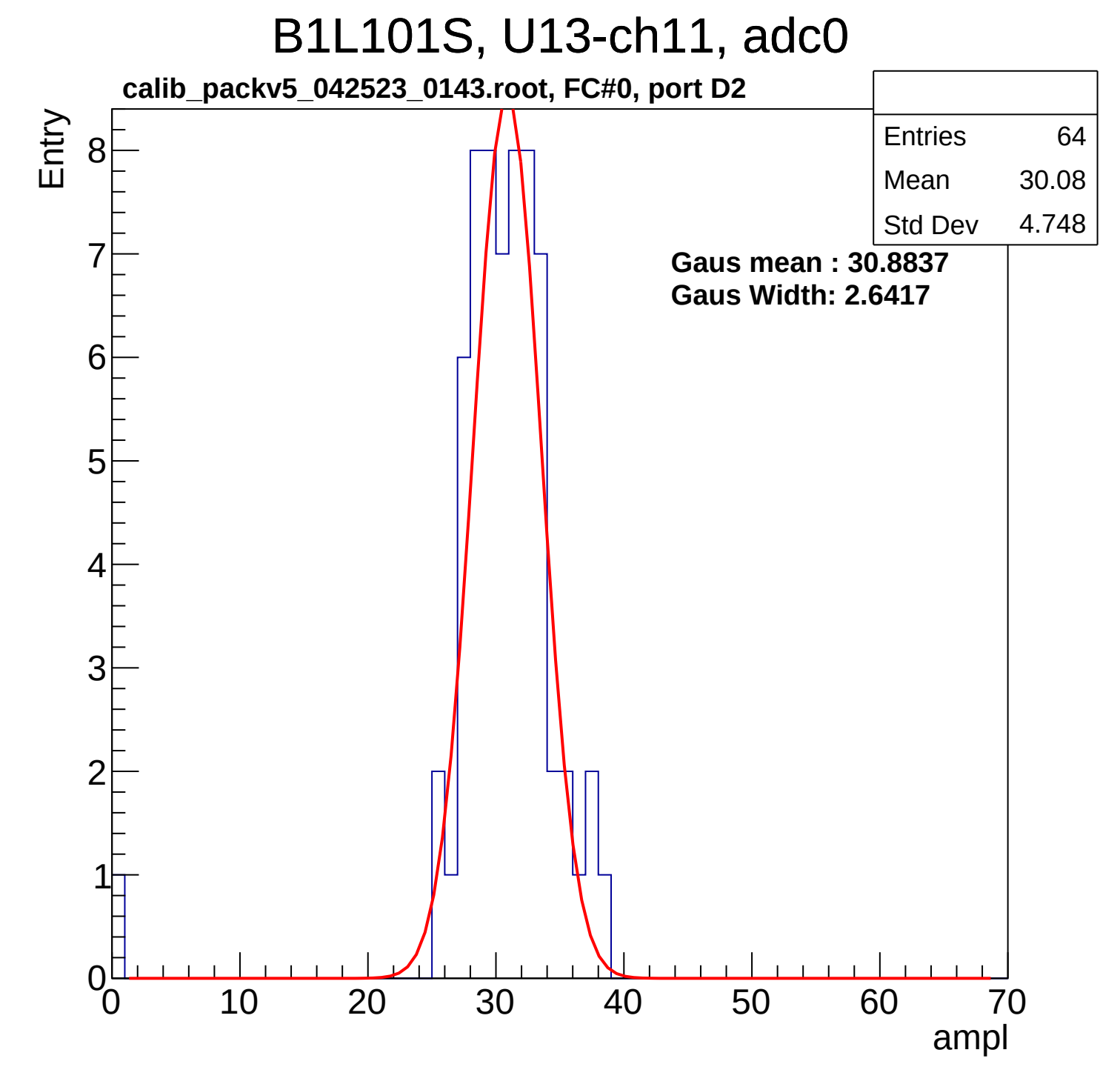
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch10, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch11, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 30.08
Std Dev 4.748
Gaus mean : 30.8837
Gaus Width: 2.6417
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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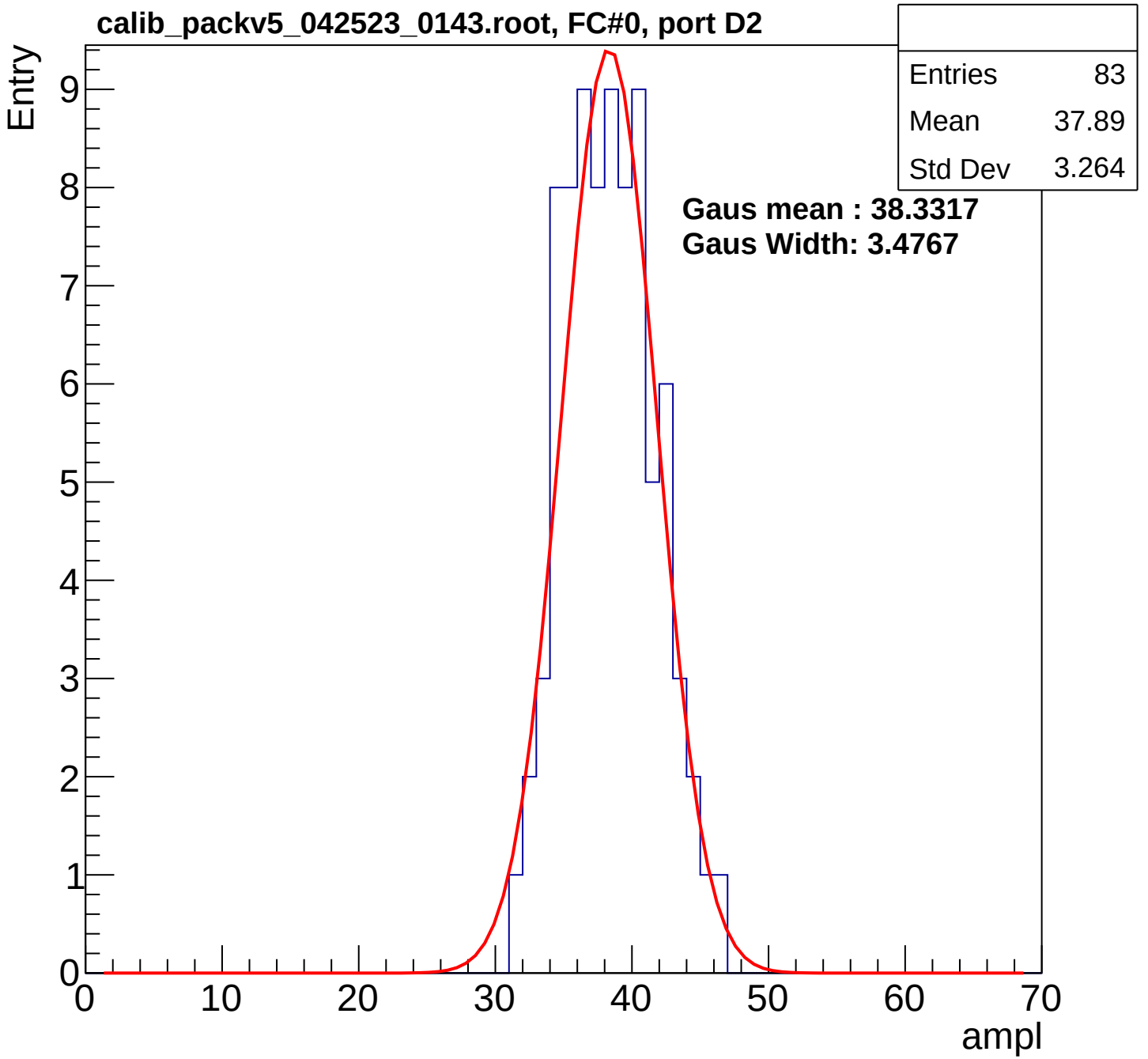
B1L101S, U13-ch11, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 83
Mean 37.89
Std Dev 3.264
Gaus mean : 38.3317
Gaus Width: 3.4767
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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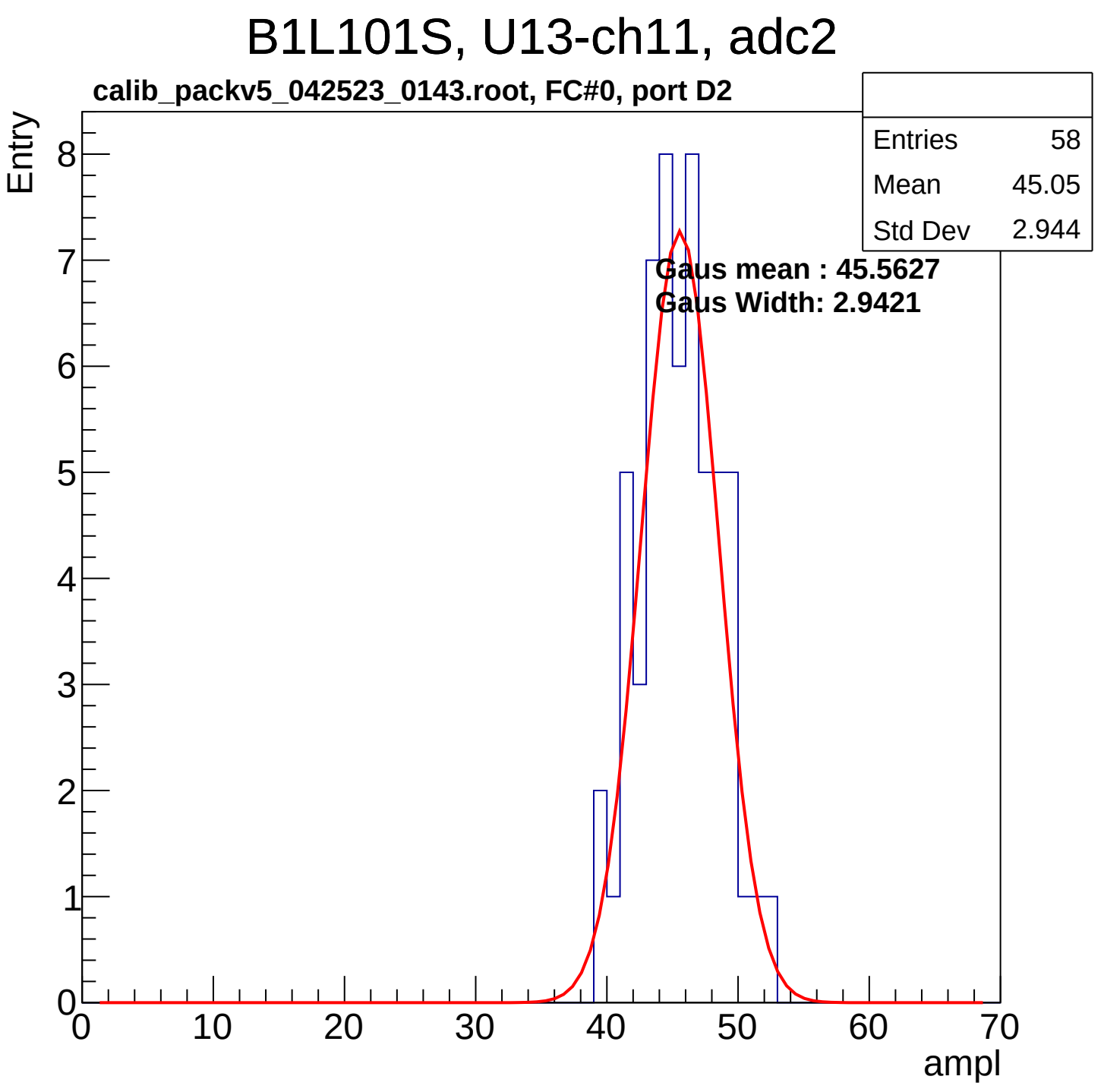
B1L101S, U13-ch11, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 45.05
Std Dev 2.944
Gaus mean : 45.5627
Gaus Width: 2.9421
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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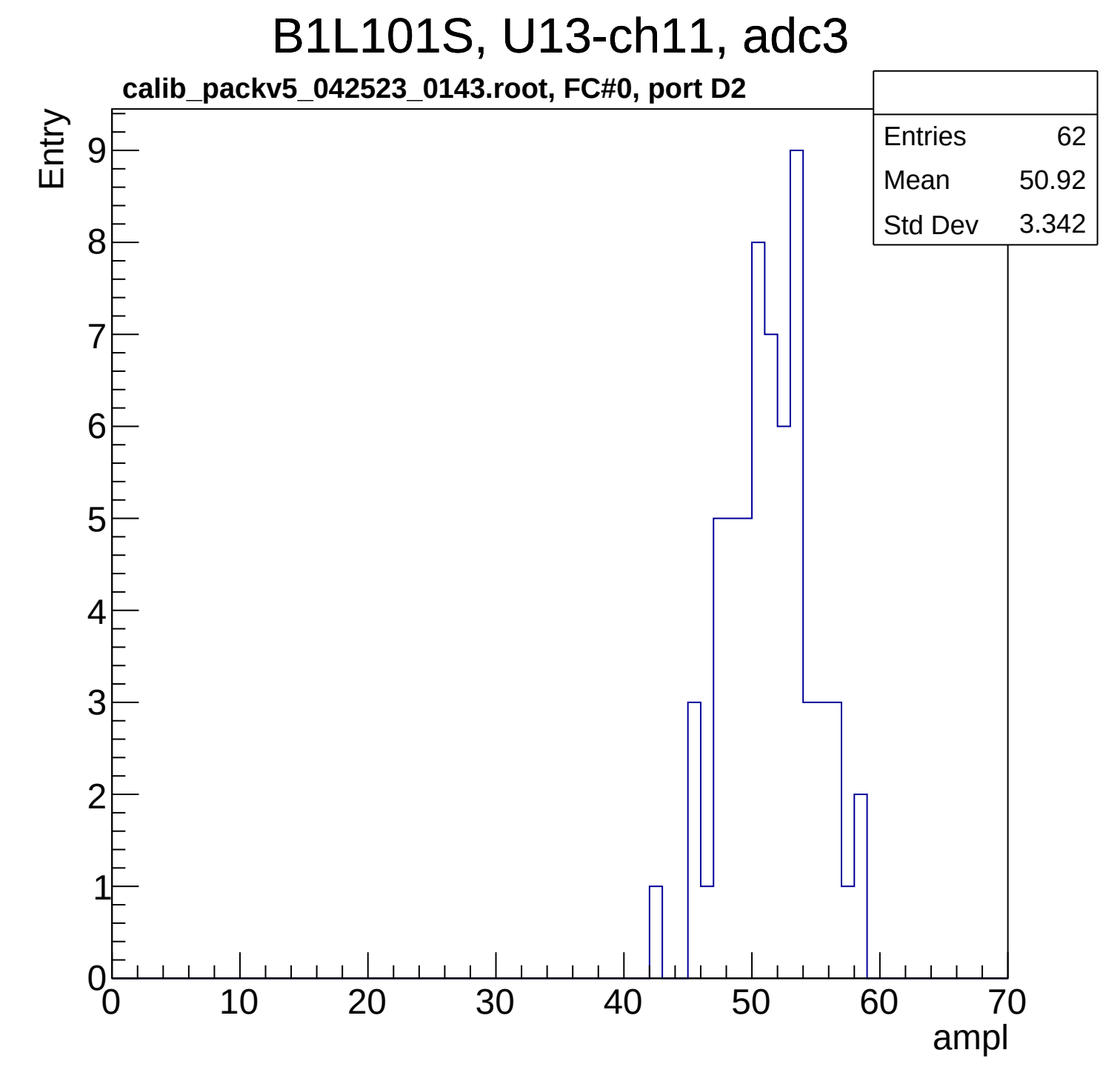
B1L101S, U13-ch11, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 50.92
Std Dev 3.342
Entry
ampl
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6
7
8
9
0
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40
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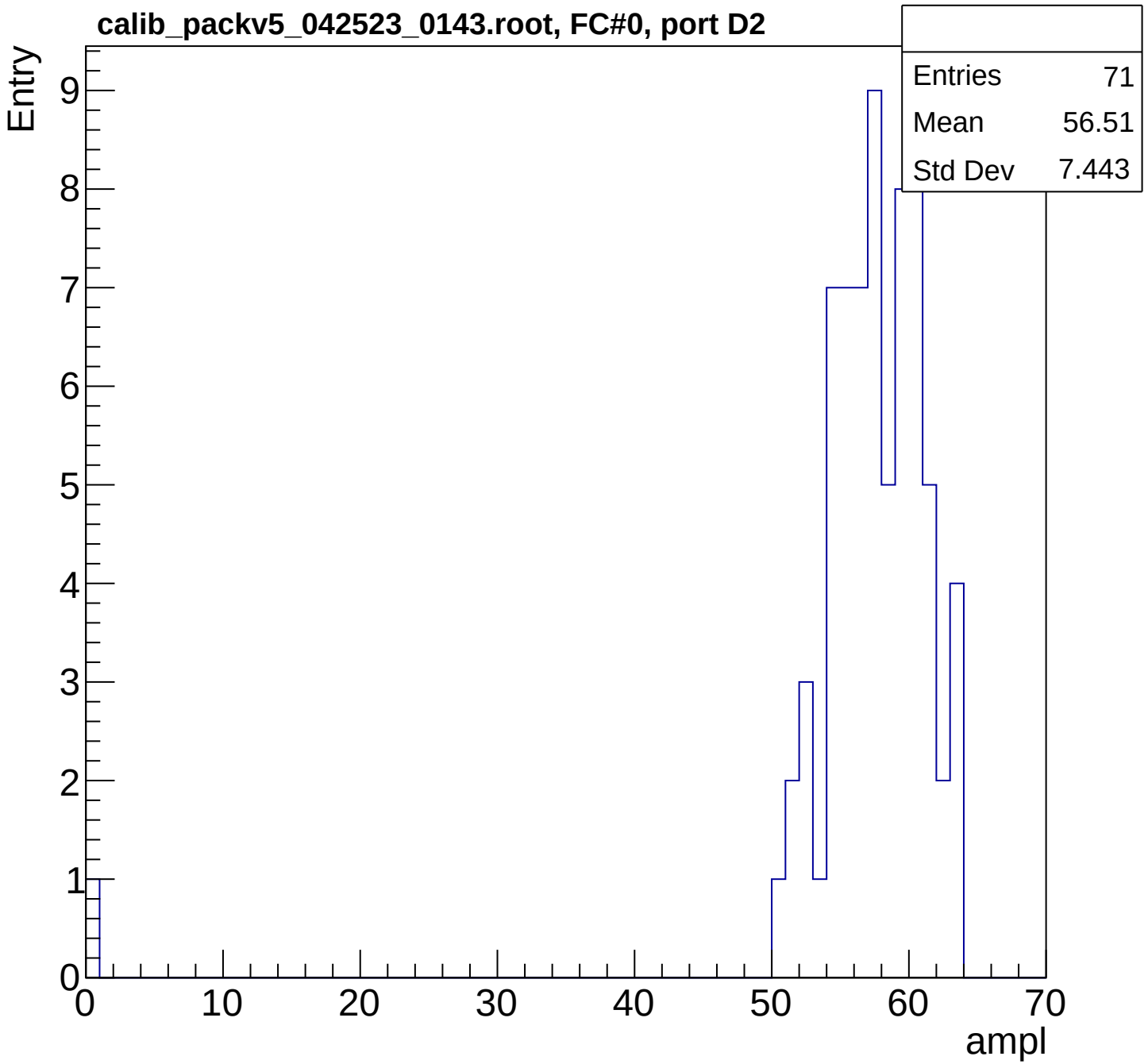
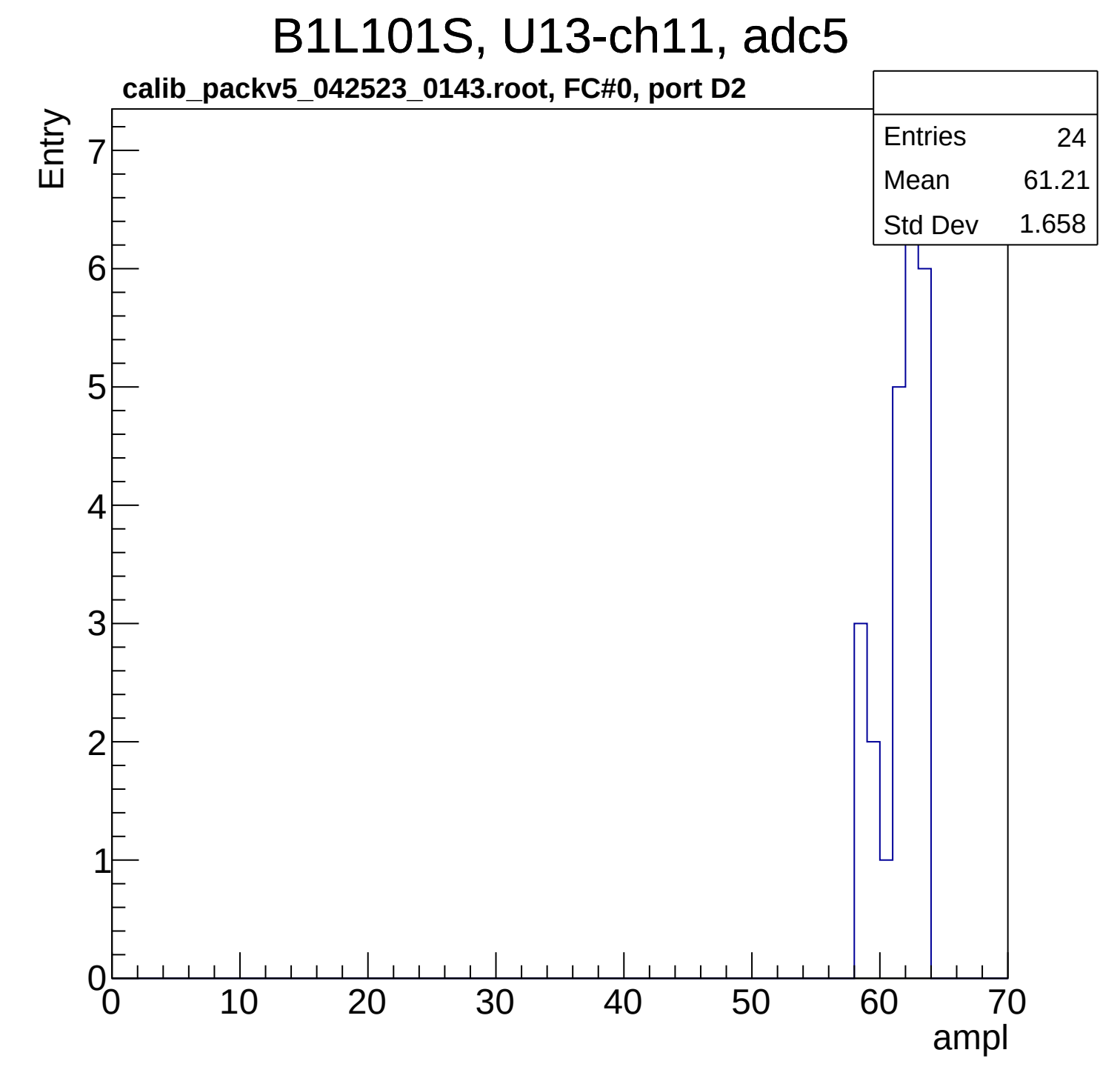

B1L101S, U13-ch11, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 56.51
Std Dev 7.443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch11, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 24
Mean 61.21
Std Dev 1.658
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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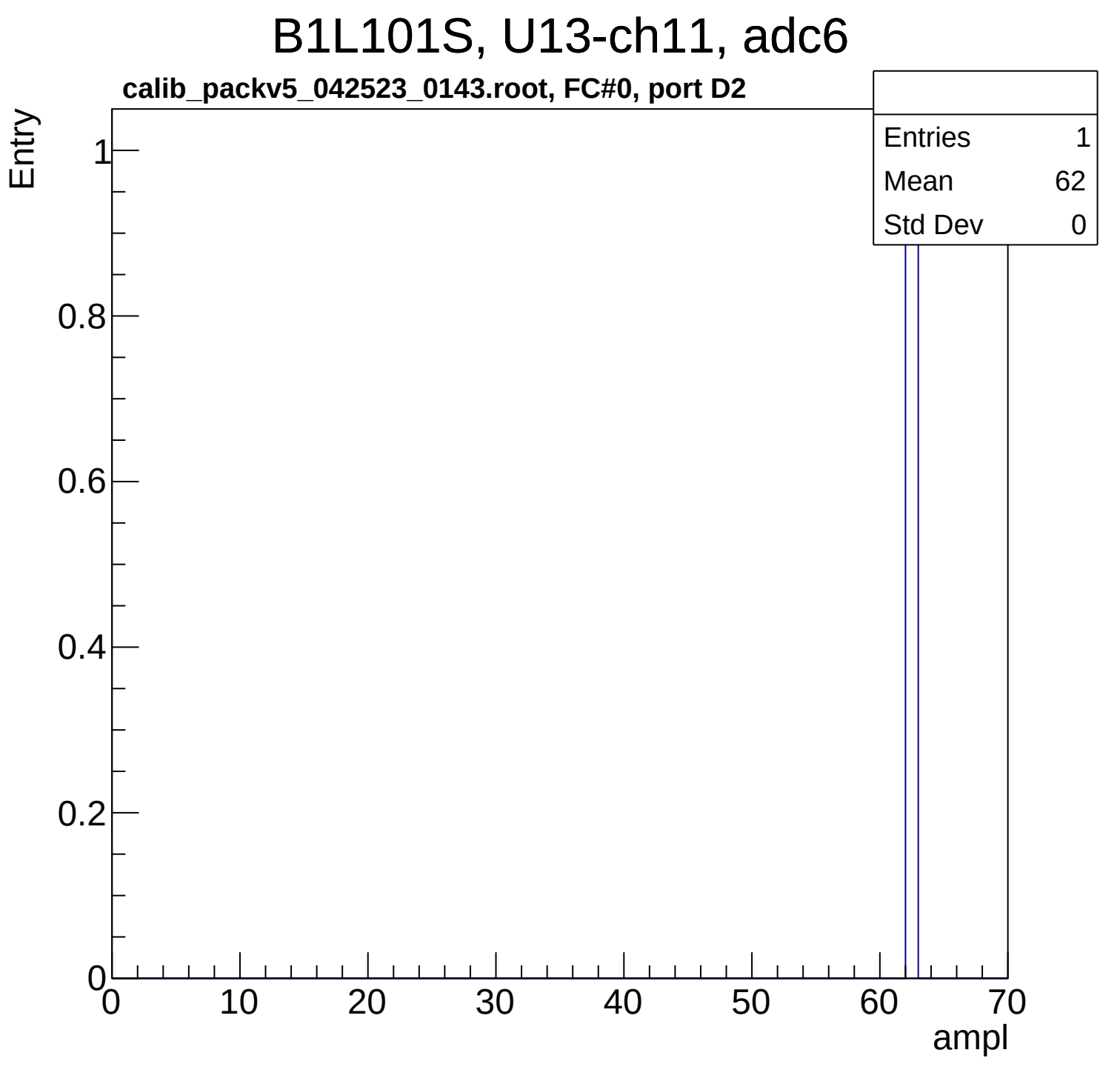
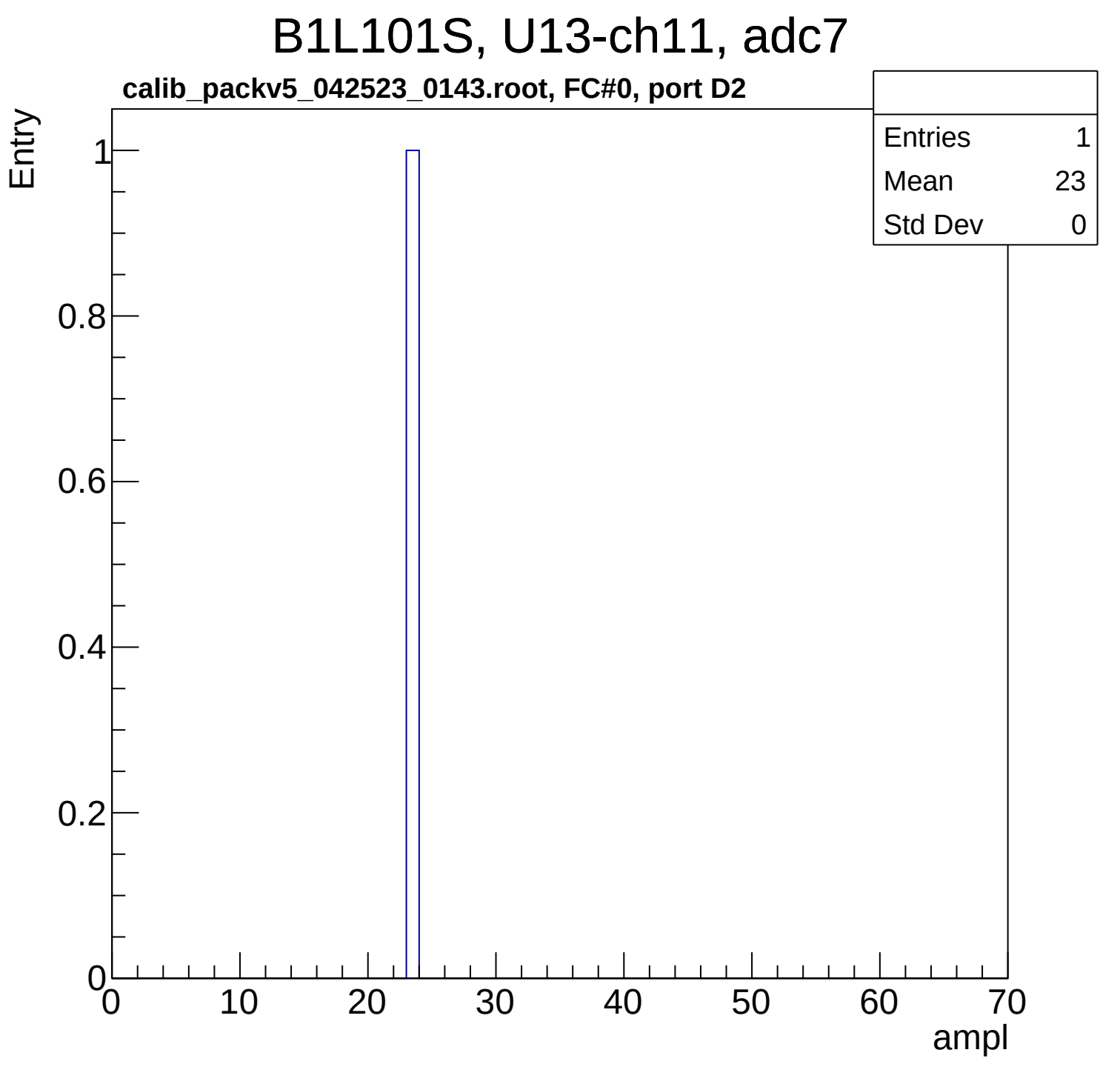
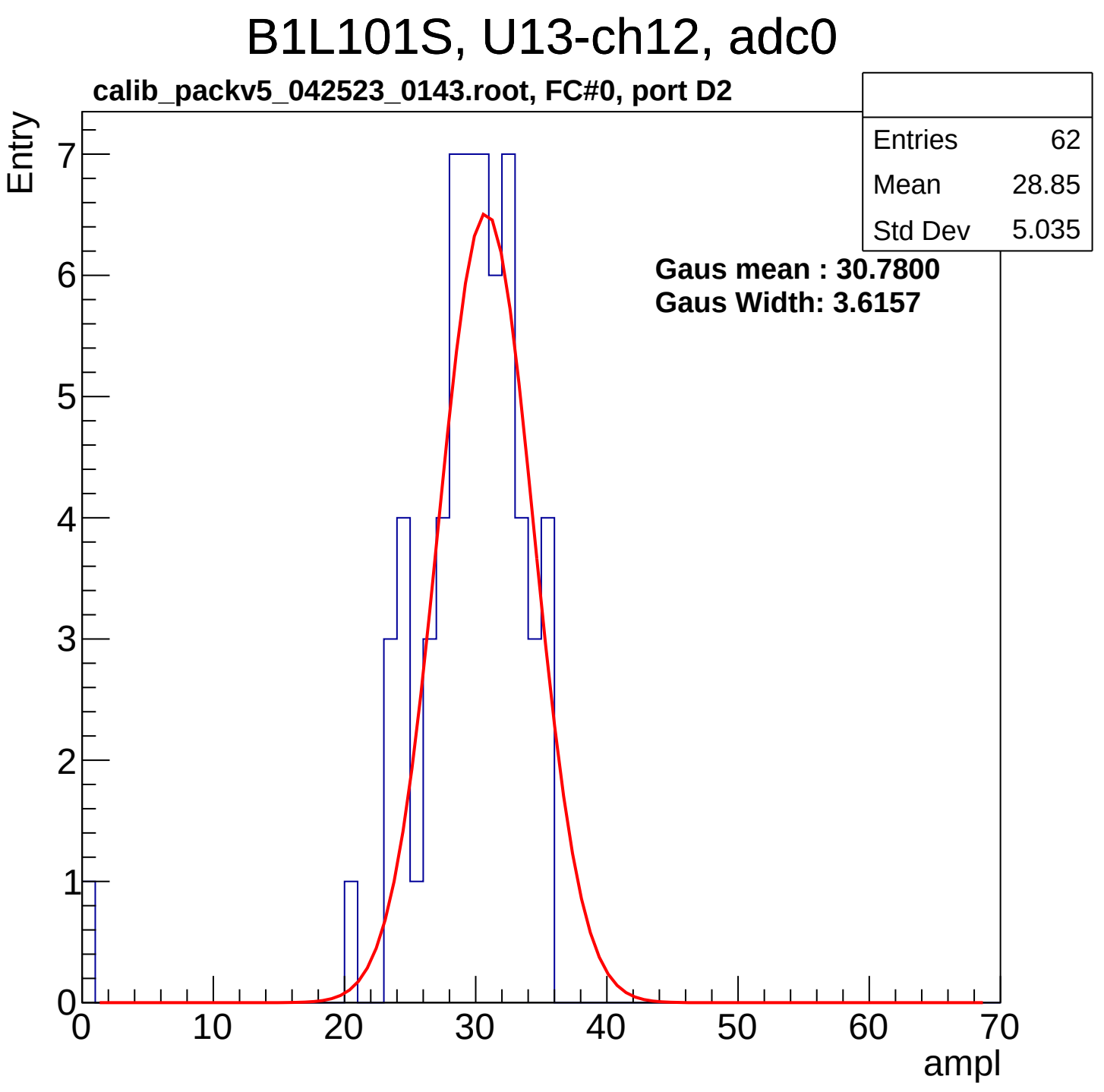
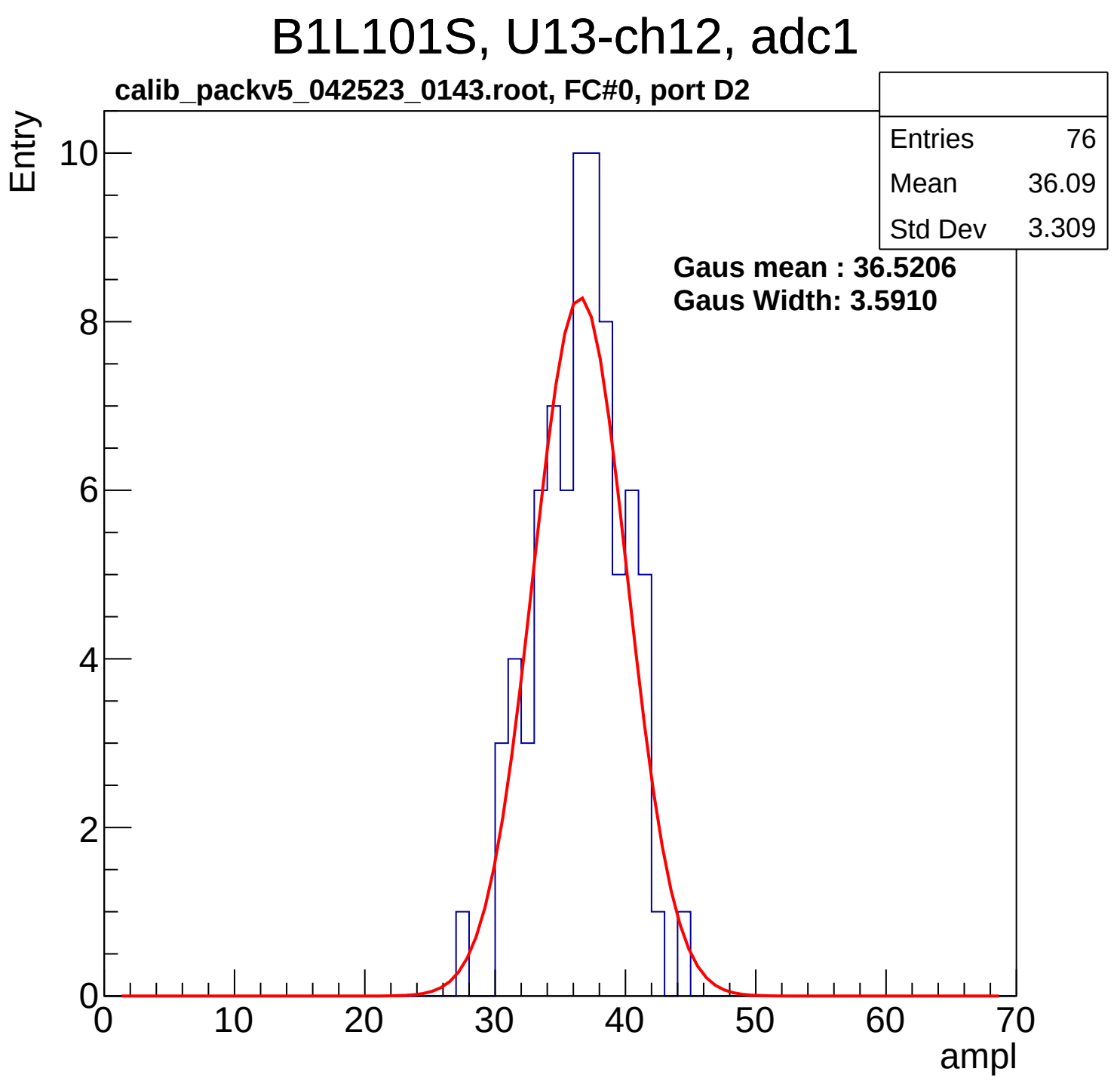
60
70


B1L101S, U13-ch11, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch11, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch12, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 28.85
Std Dev 5.035
Gaus mean : 30.7800
Gaus Width: 3.6157
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch12, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 36.09
Std Dev 3.309
Gaus mean : 36.5206
Gaus Width: 3.5910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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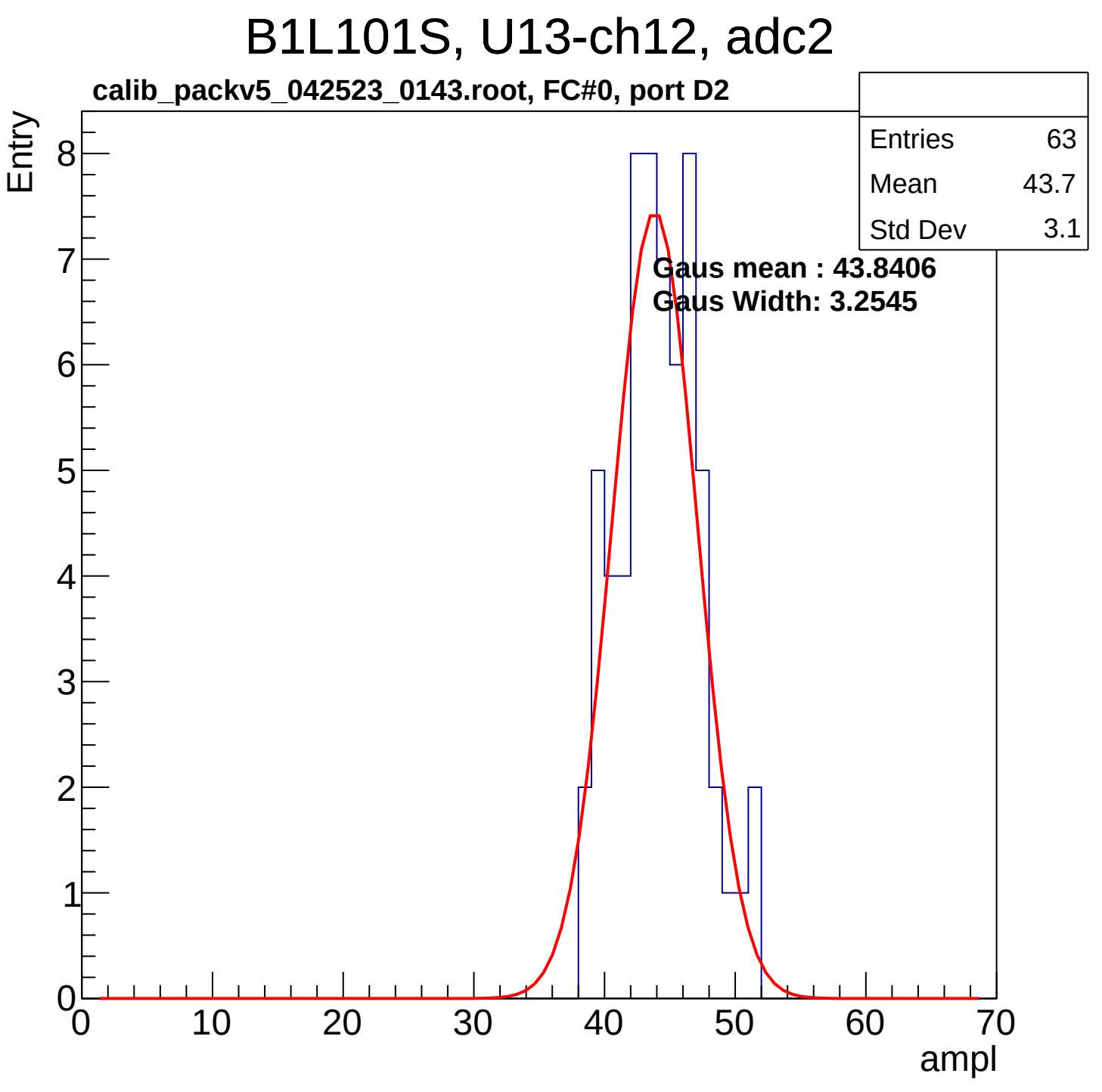
B1L101S, U13-ch12, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 43.7
Std Dev 3.1
Gaus mean : 43.8406
Gaus Width: 3.2545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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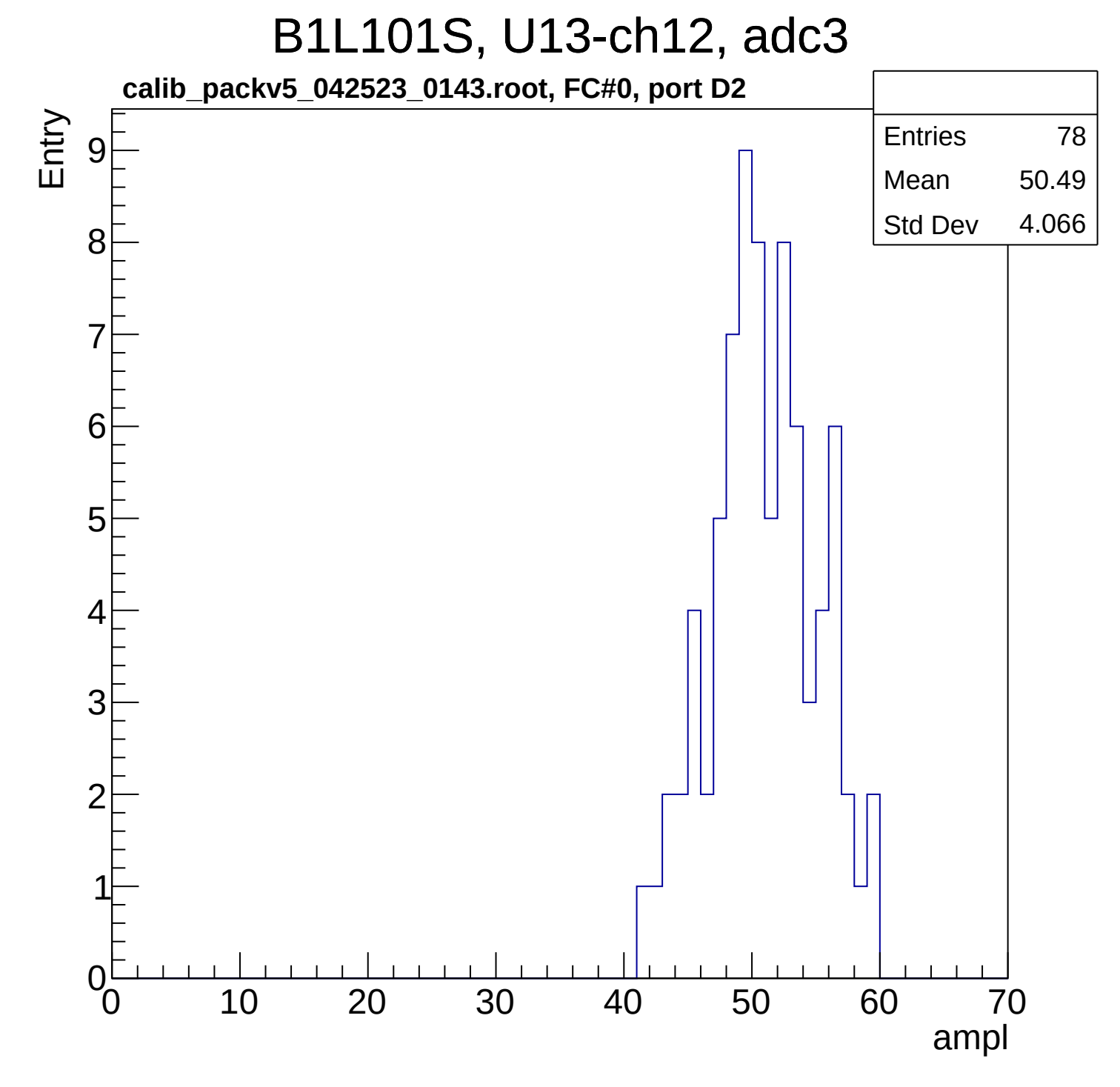
B1L101S, U13-ch12, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 50.49
Std Dev 4.066
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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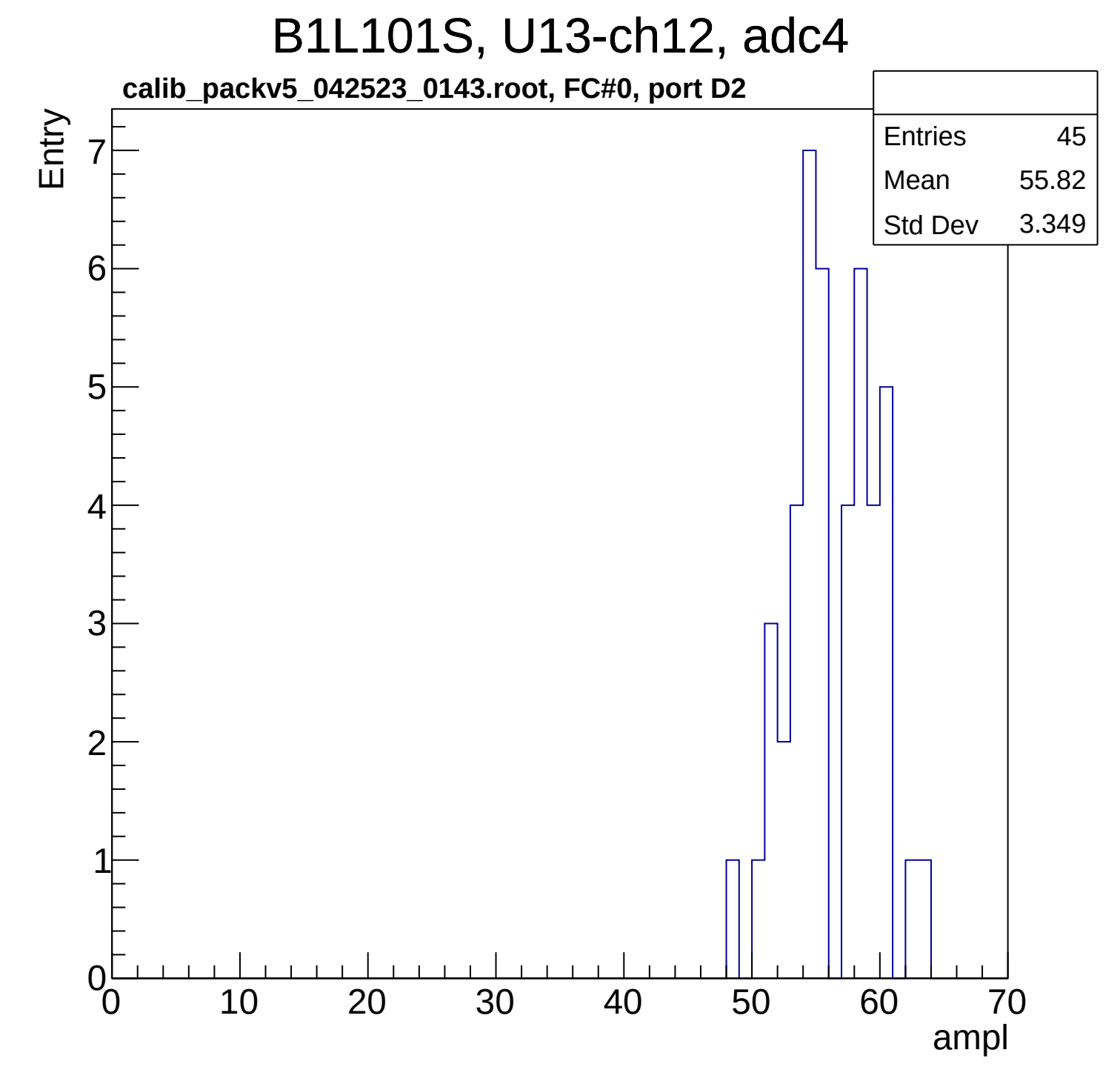
B1L101S, U13-ch12, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 55.82
Std Dev 3.349
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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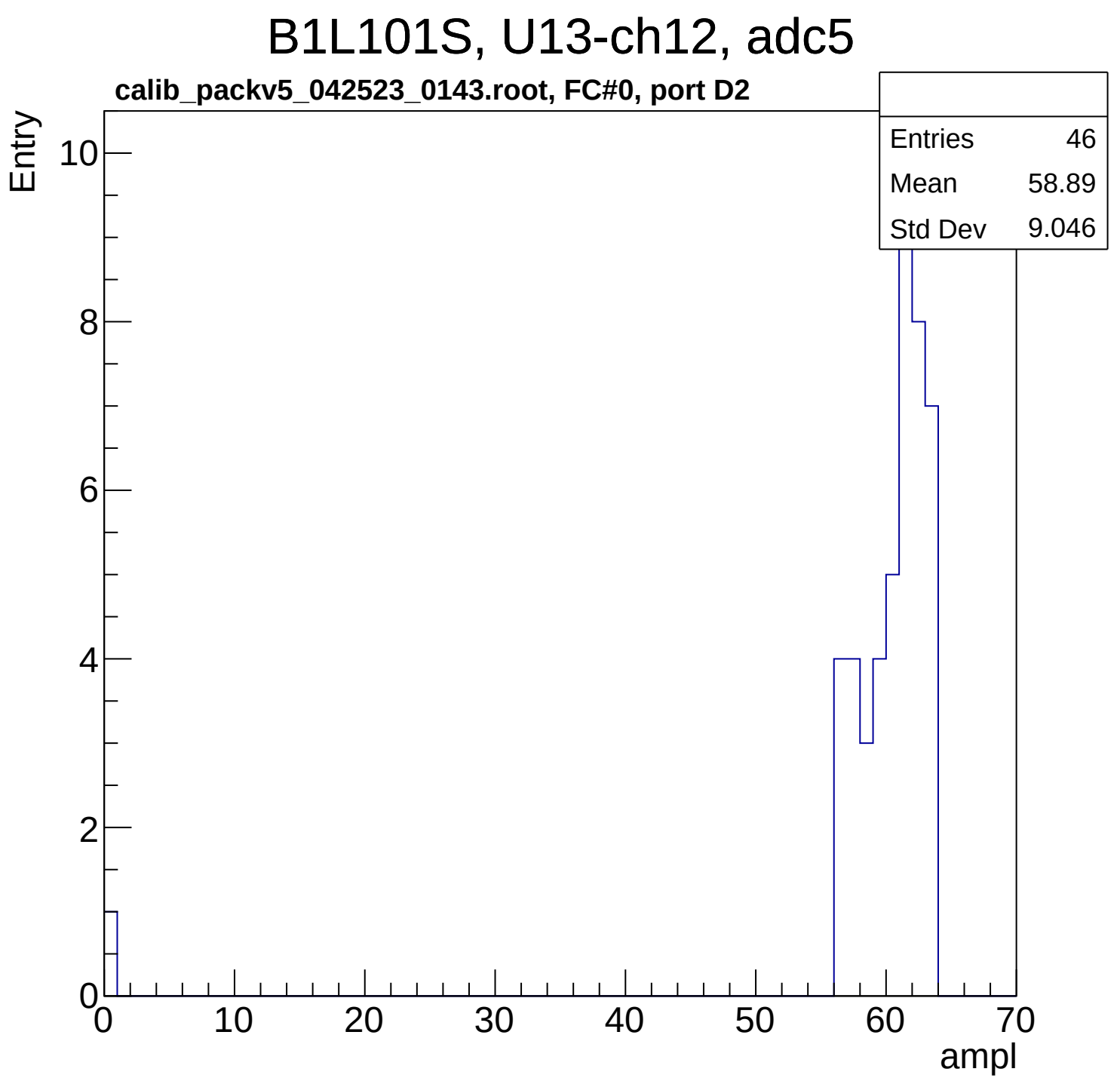
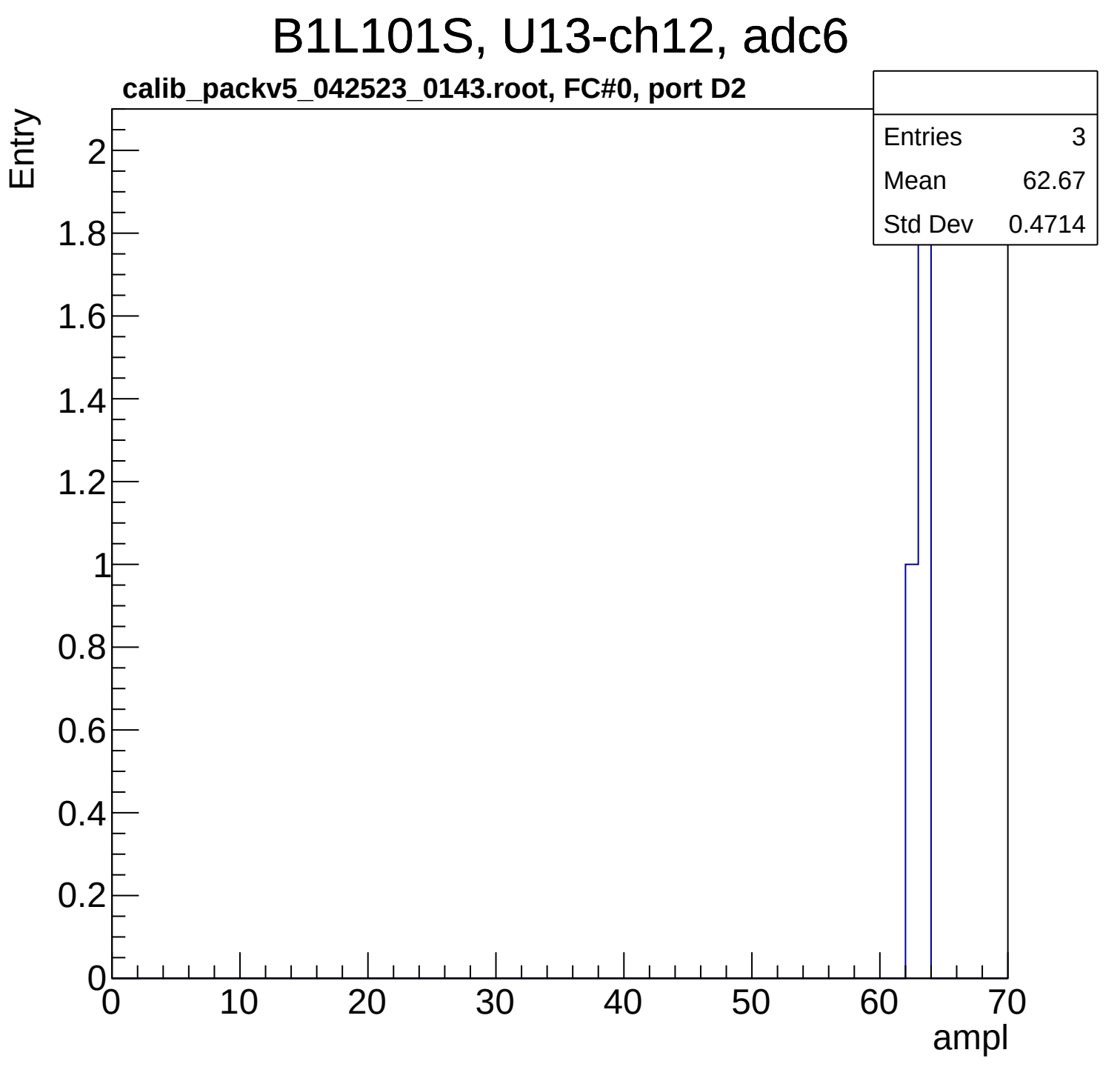
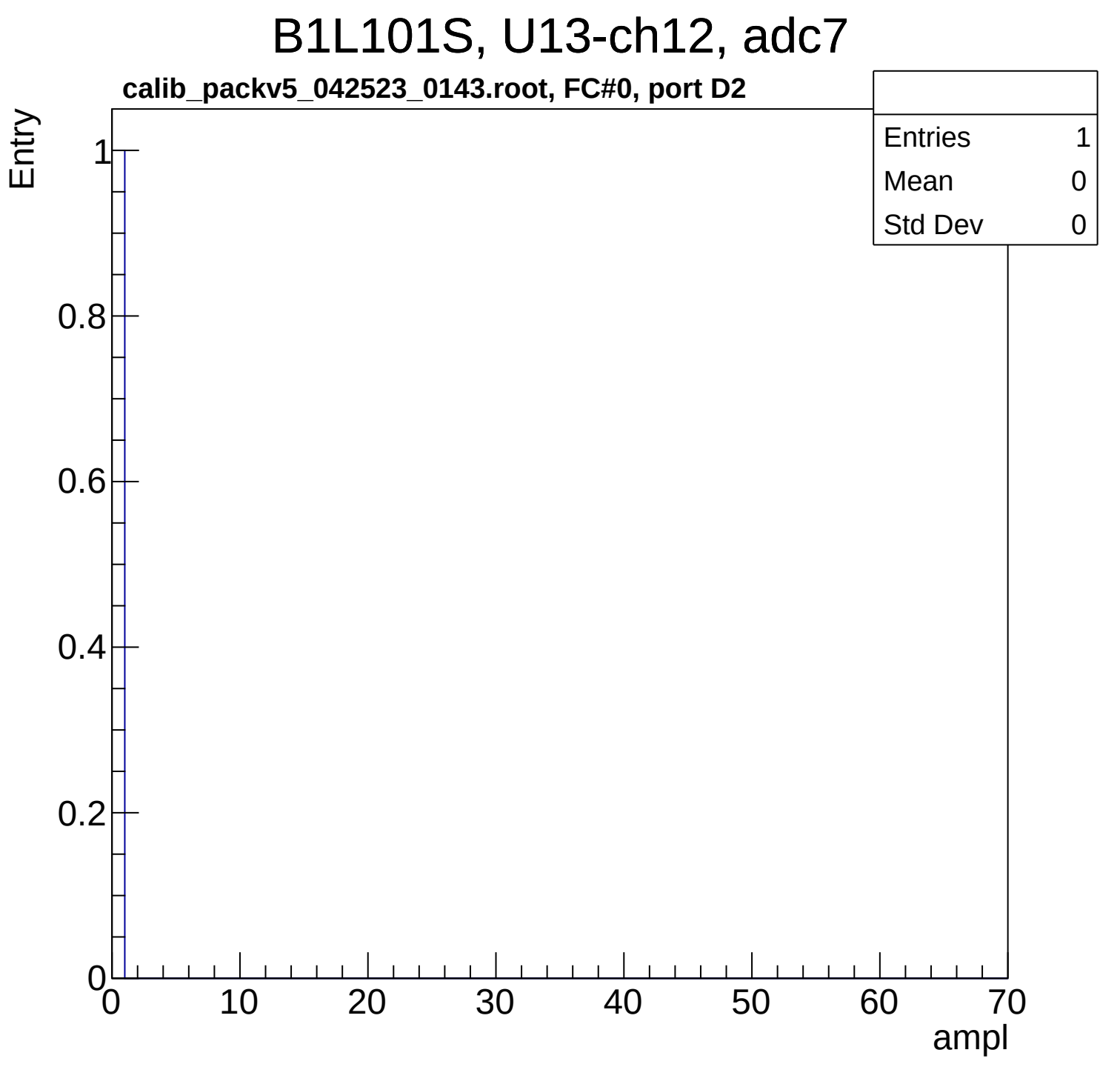
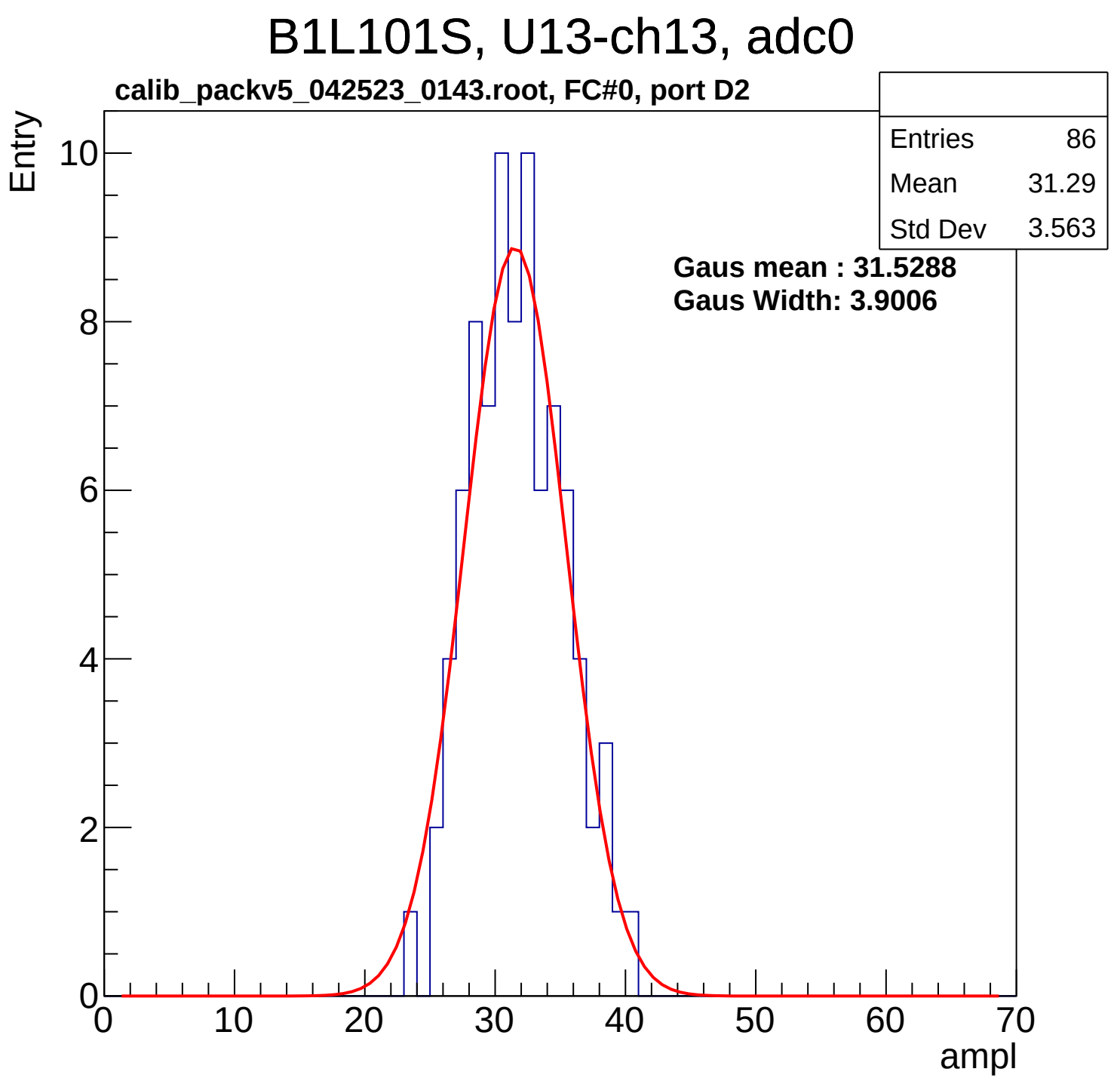
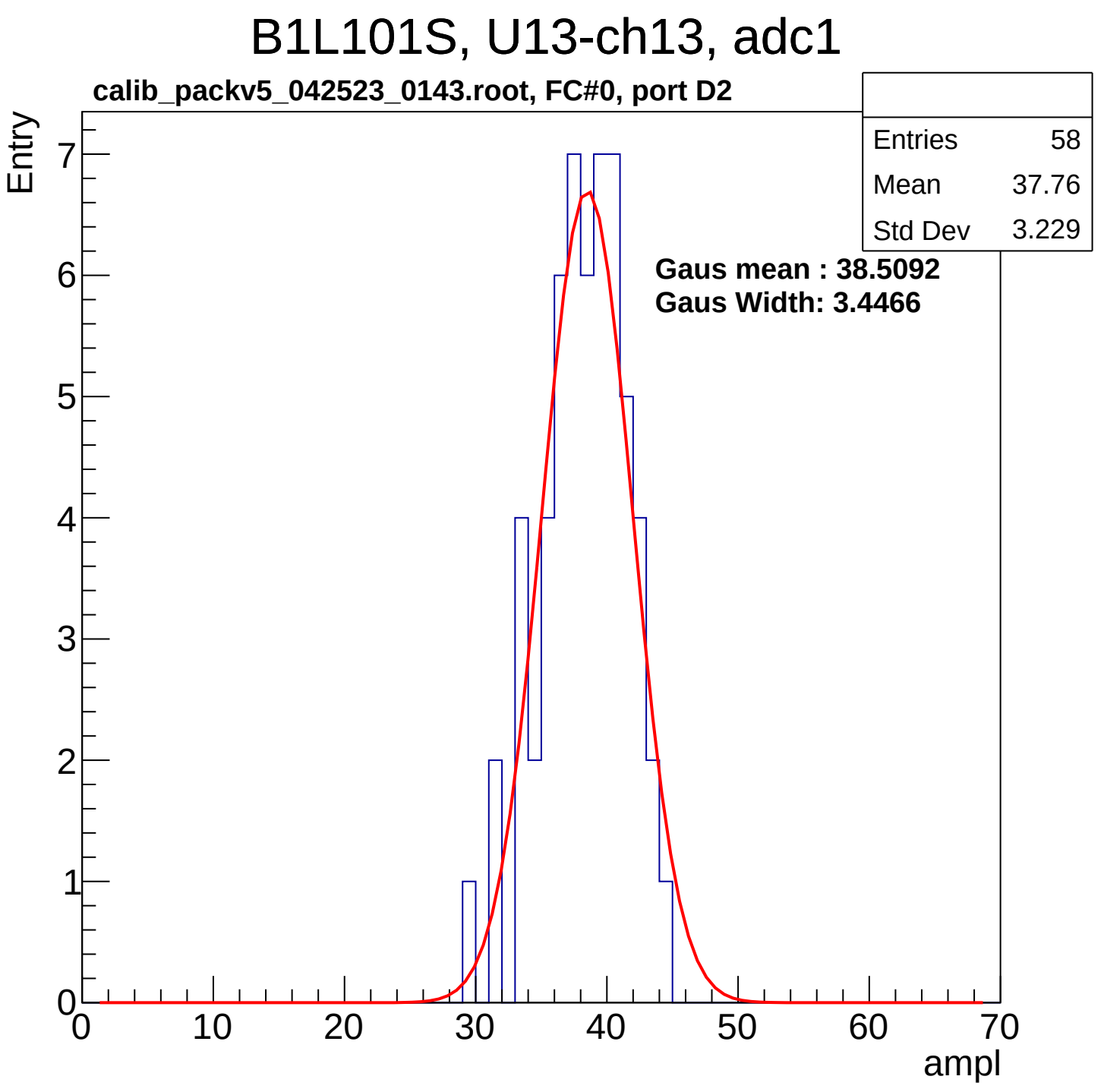
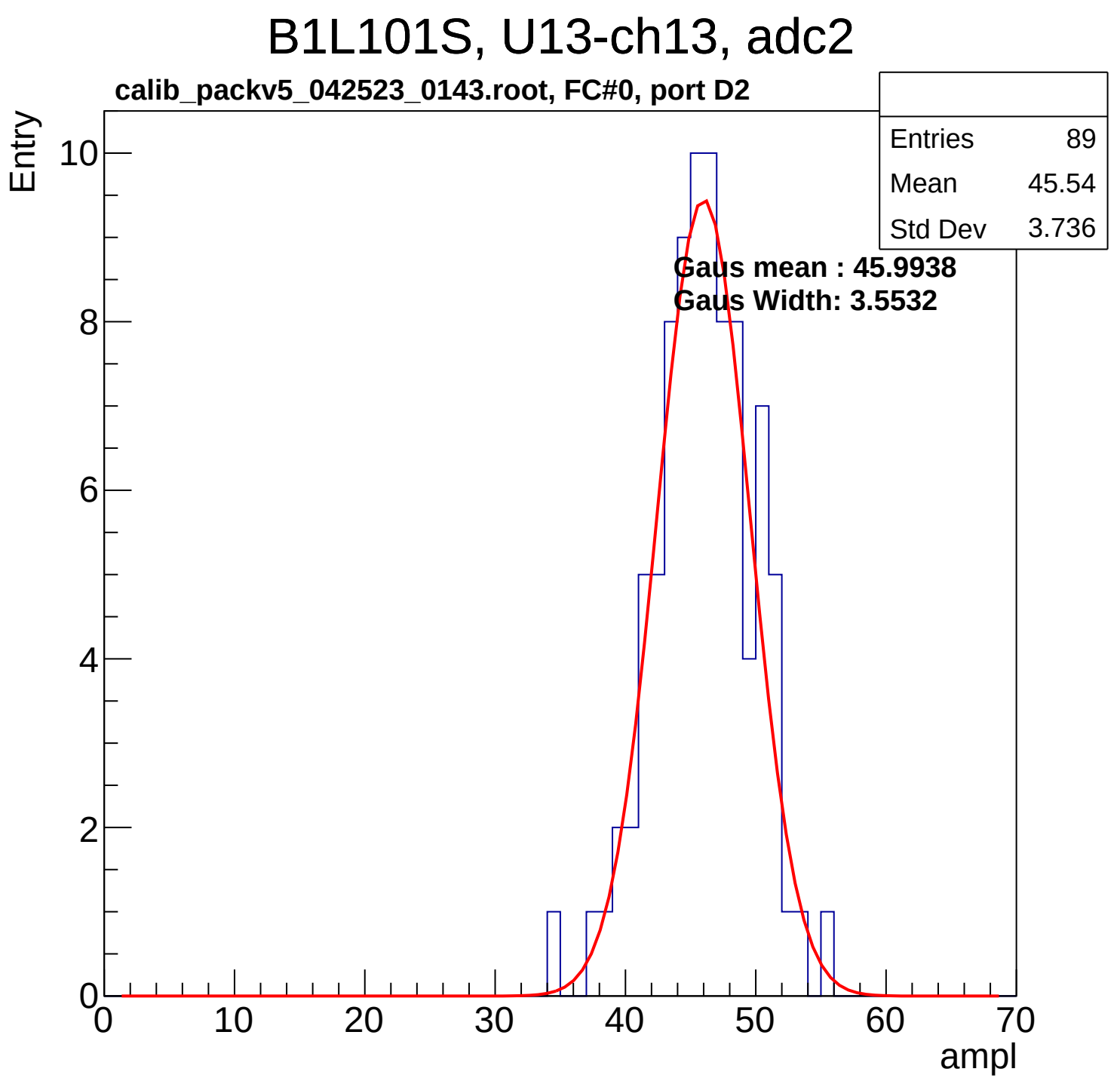

B1L101S, U13-ch12, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 58.89
Std Dev 9.046
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

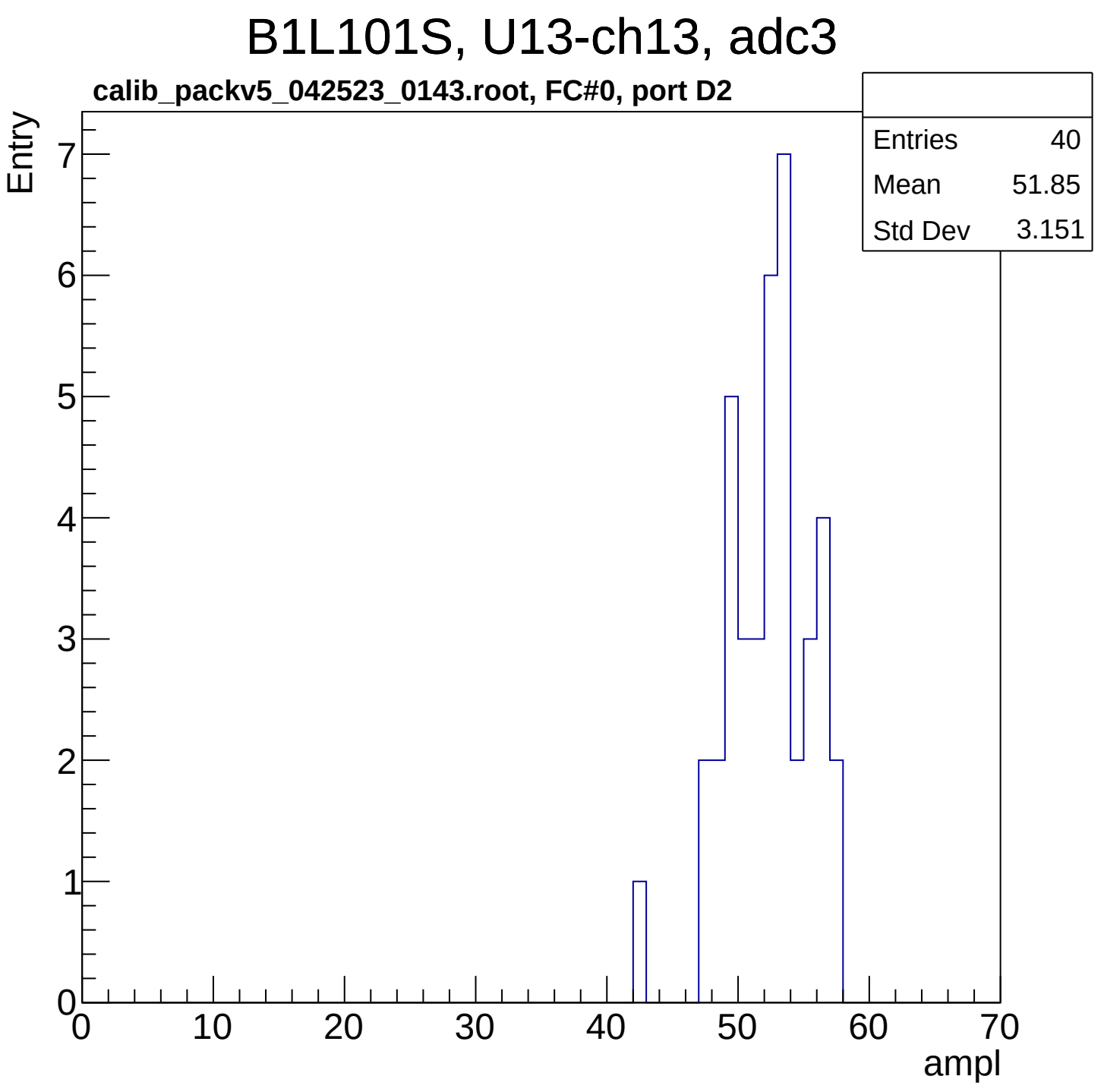

B1L101S, U13-ch12, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

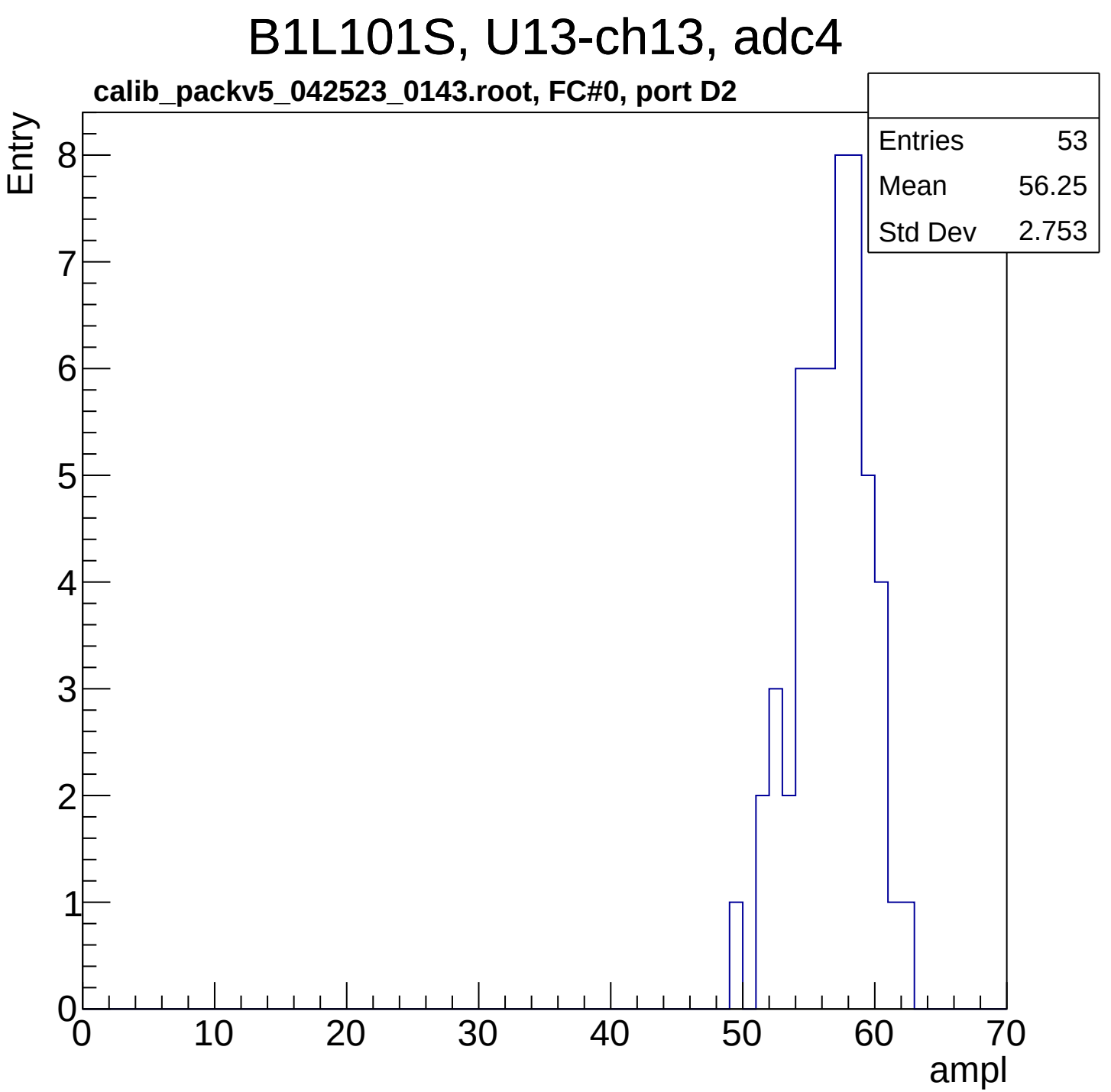

B1L101S, U13-ch12, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

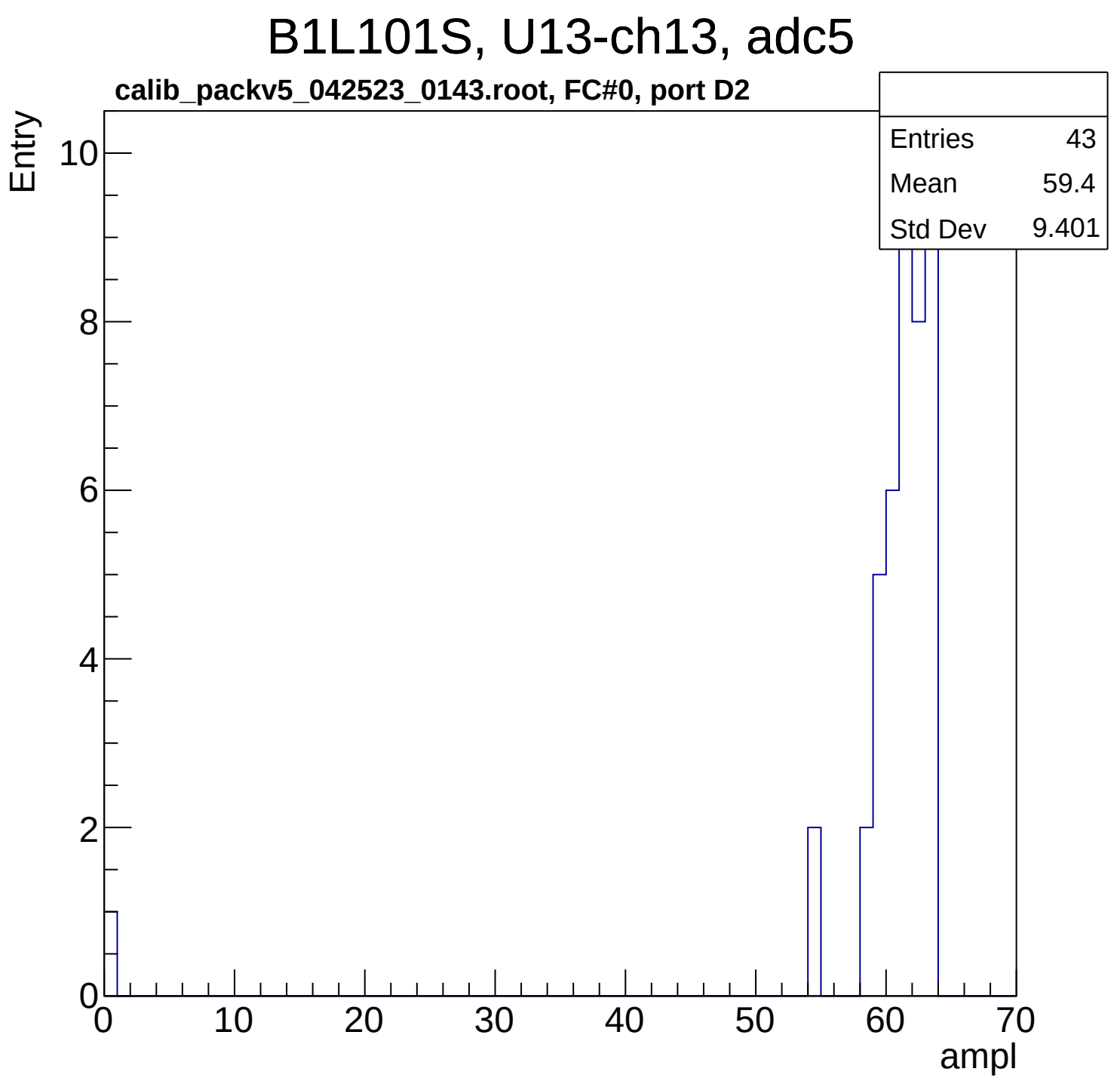

B1L101S, U13-ch13, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 86
Mean 31.29
Std Dev 3.563
Gaus mean : 31.5288
Gaus Width: 3.9006
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

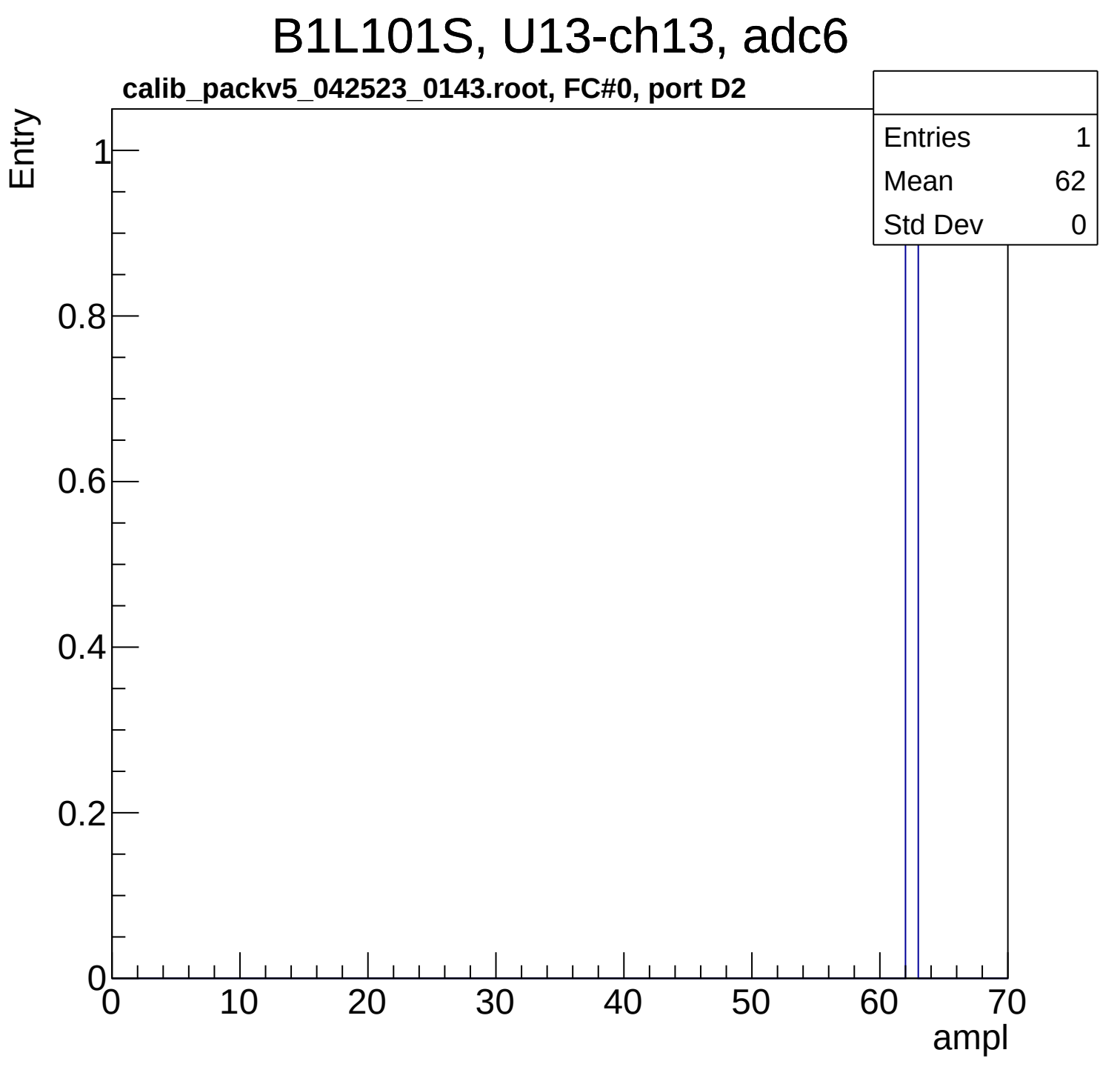

B1L101S, U13-ch13, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 37.76
Std Dev 3.229
Gaus mean : 38.5092
Gaus Width: 3.4466
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

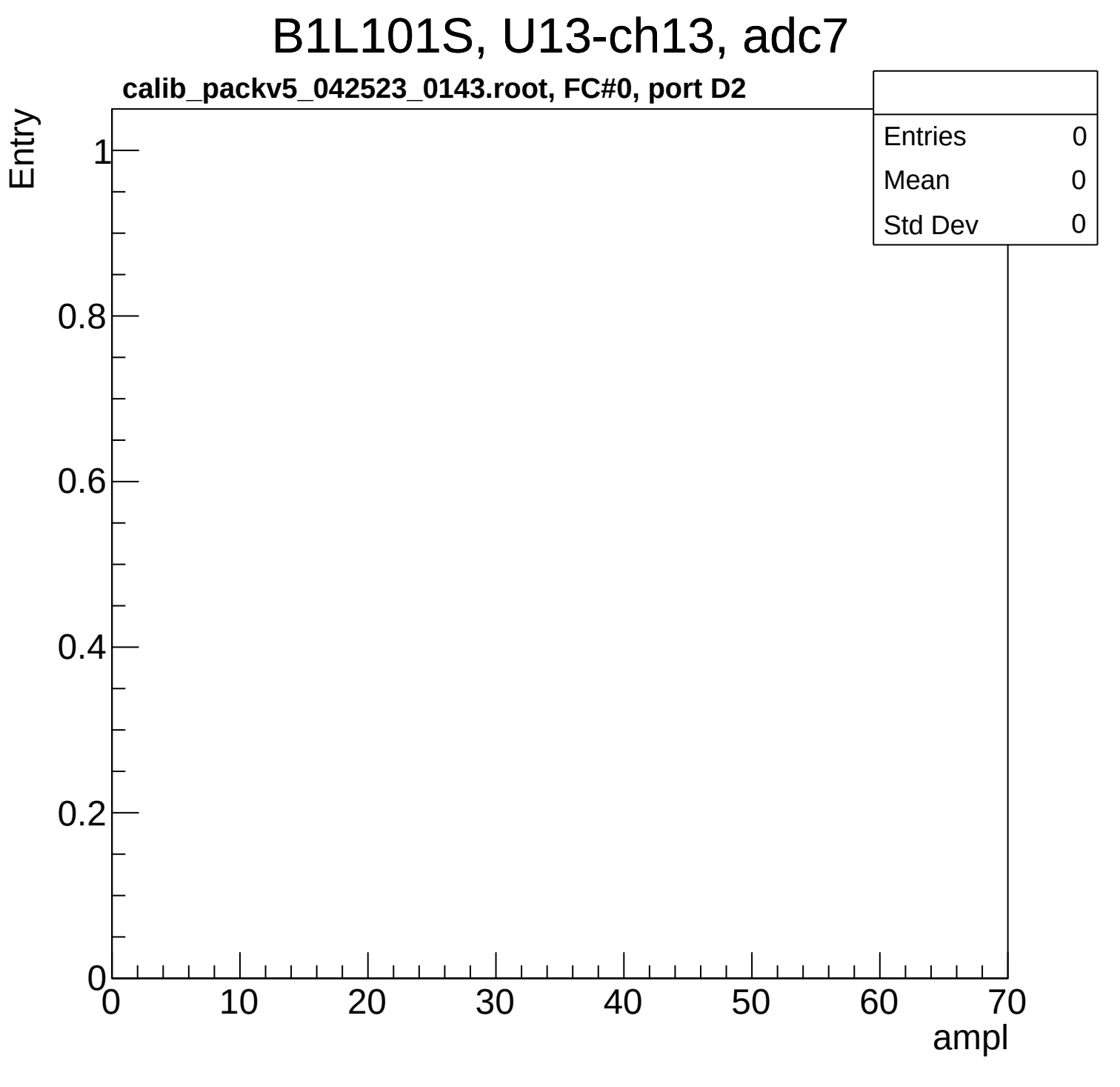

B1L101S, U13-ch13, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 89
Mean 45.54
Std Dev 3.736
Gaus mean : 45.9938
Gaus Width: 3.5532
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

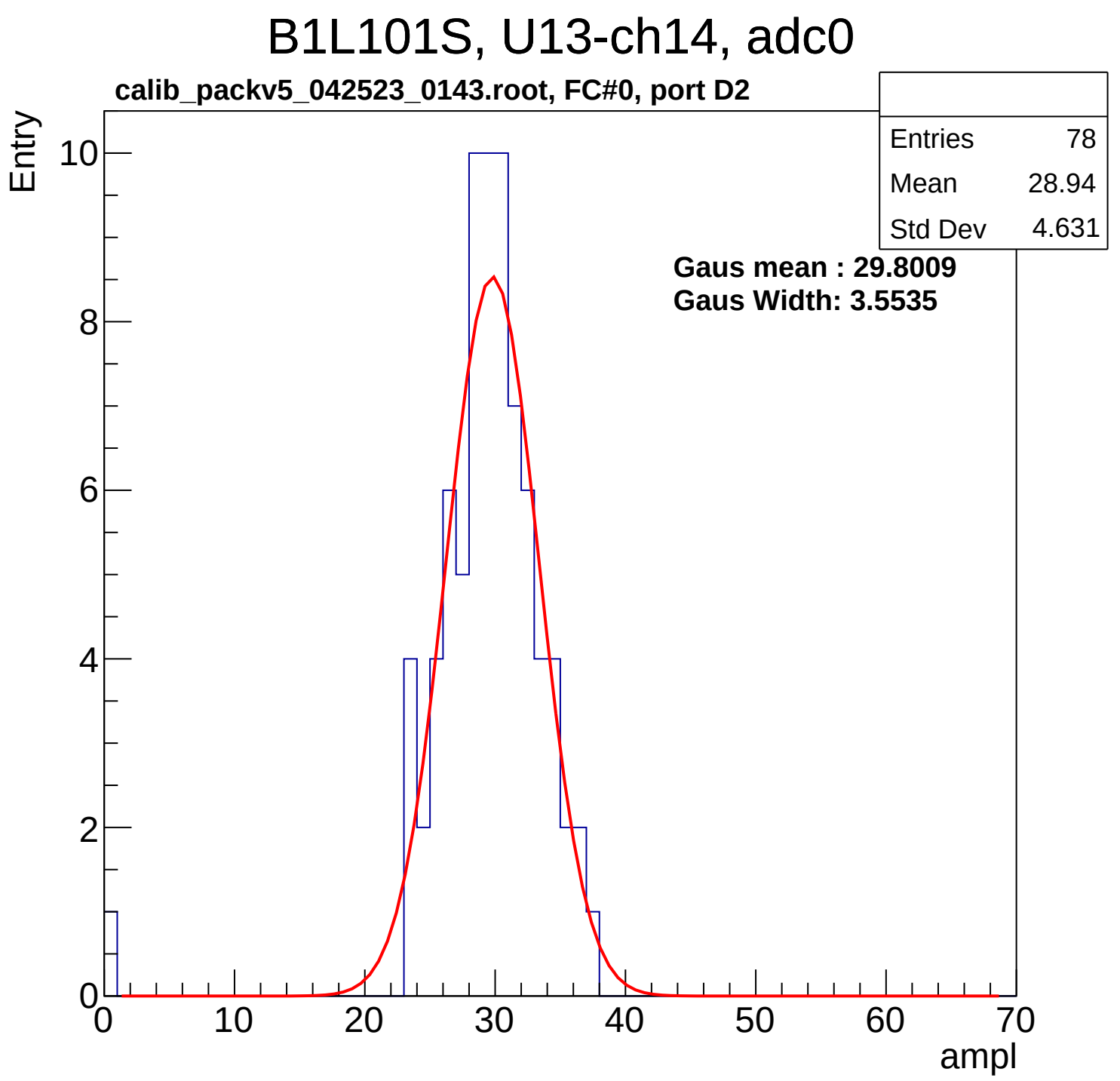

B1L101S, U13-ch13, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 51.85
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

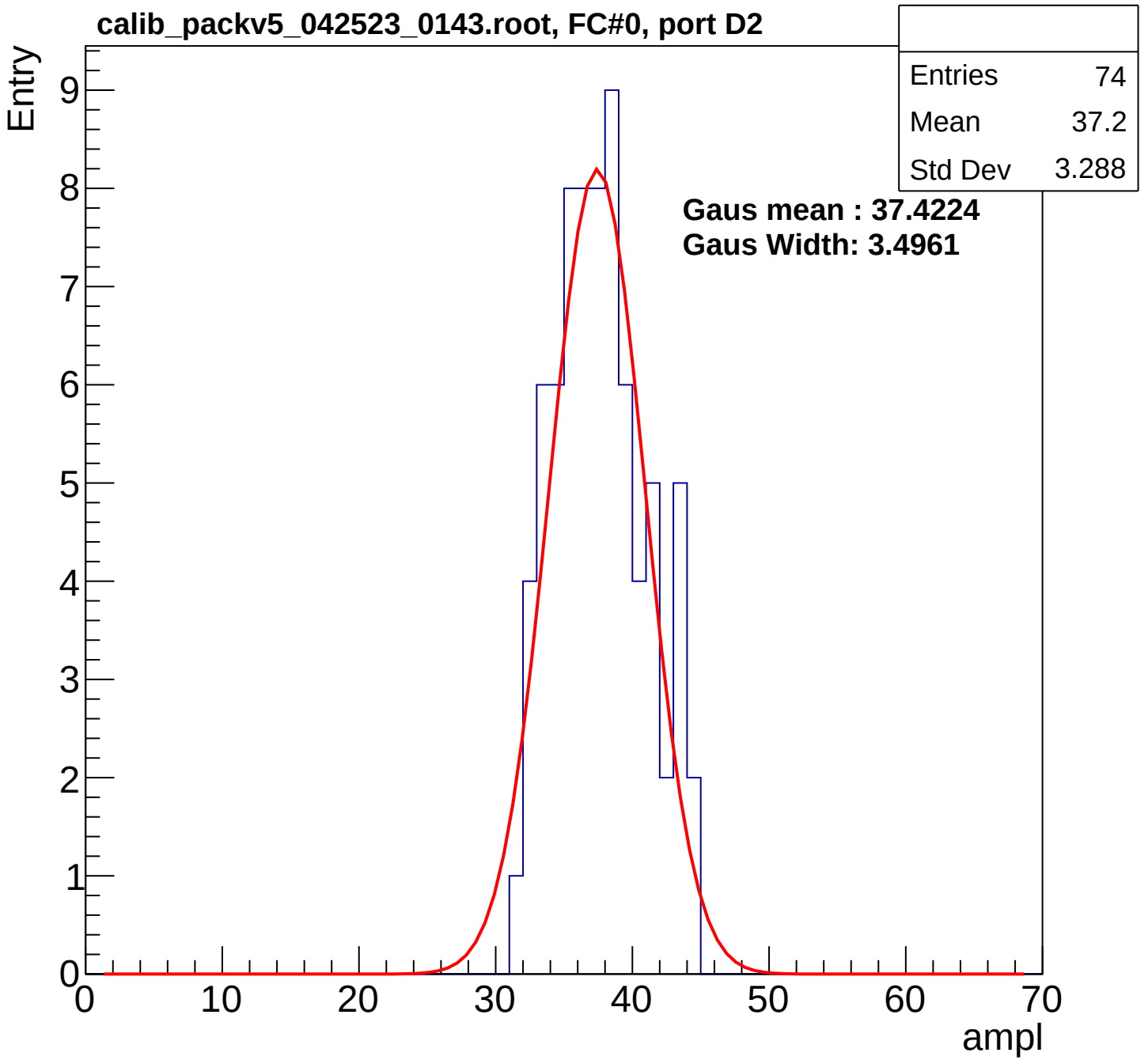

B1L101S, U13-ch13, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 56.25
Std Dev 2.753
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

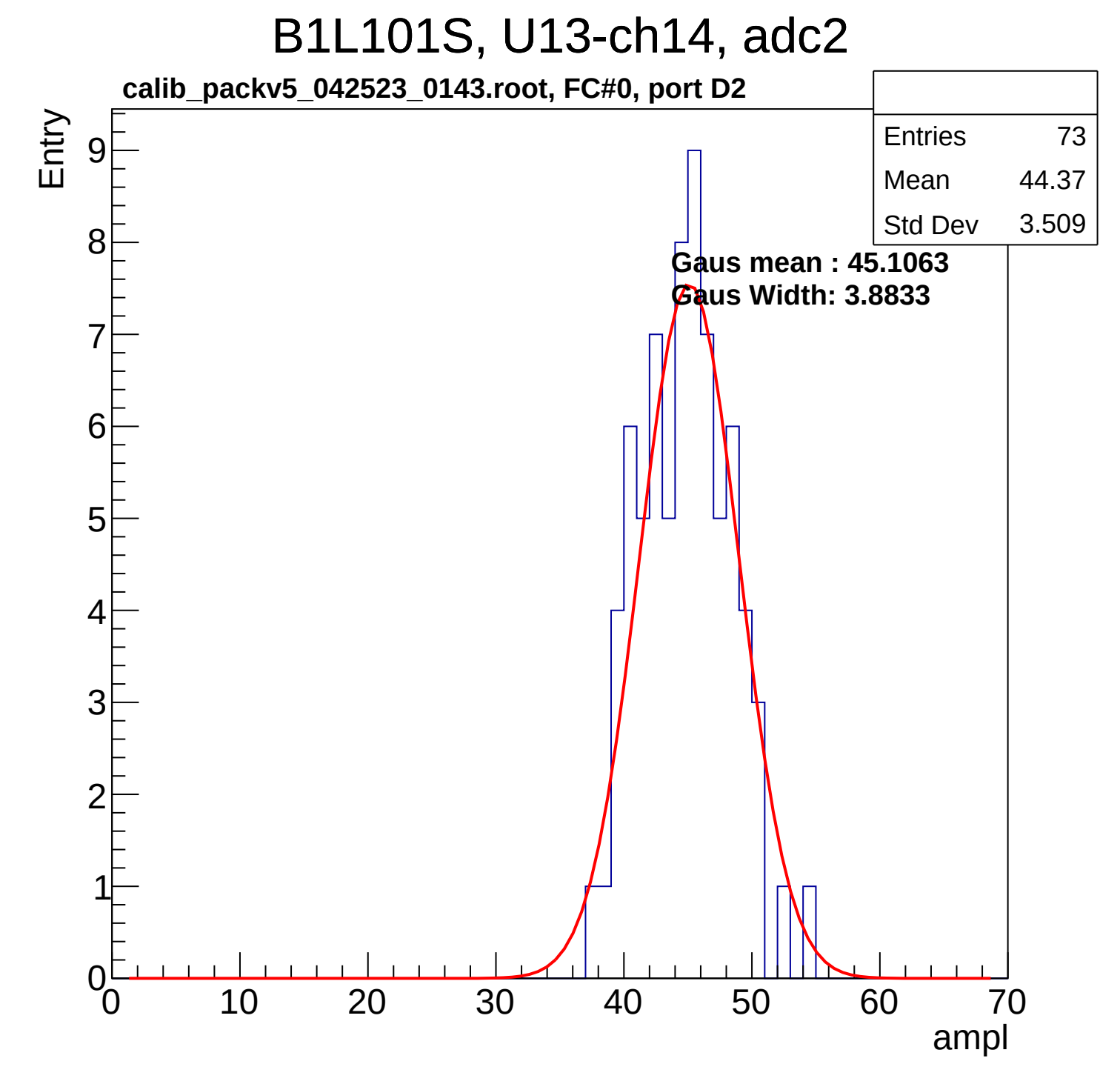

B1L101S, U13-ch13, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 43
Mean 59.4
Std Dev 9.401
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch13, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch13, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch14, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 28.94
Std Dev 4.631
Gaus mean : 29.8009
Gaus Width: 3.5535
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch14, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 37.2
Std Dev 3.288
Gaus mean : 37.4224
Gaus Width: 3.4961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch14, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 44.37
Std Dev 3.509
Gaus mean : 45.1063
Gaus Width: 3.8833
Entry
ampl
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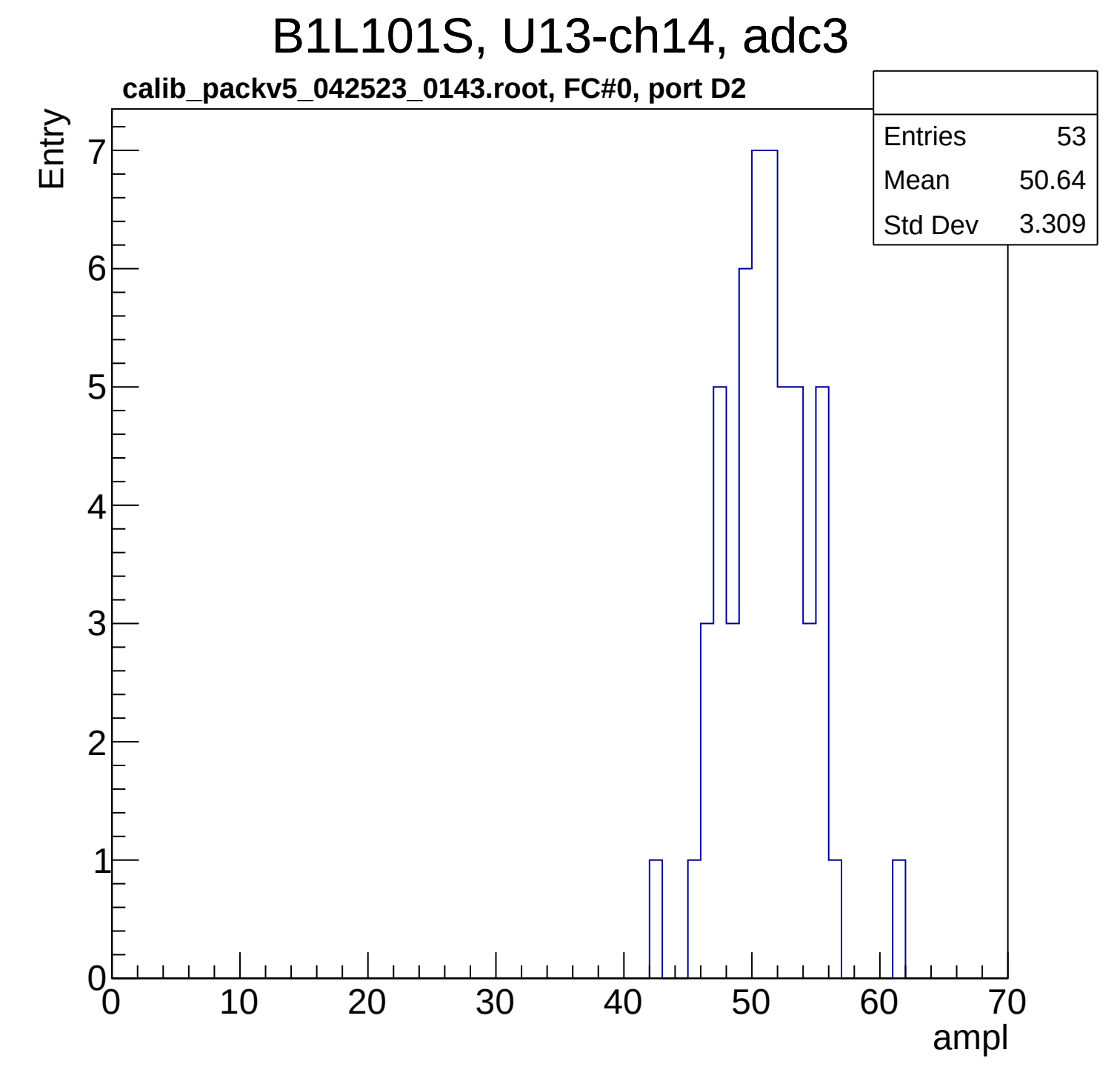
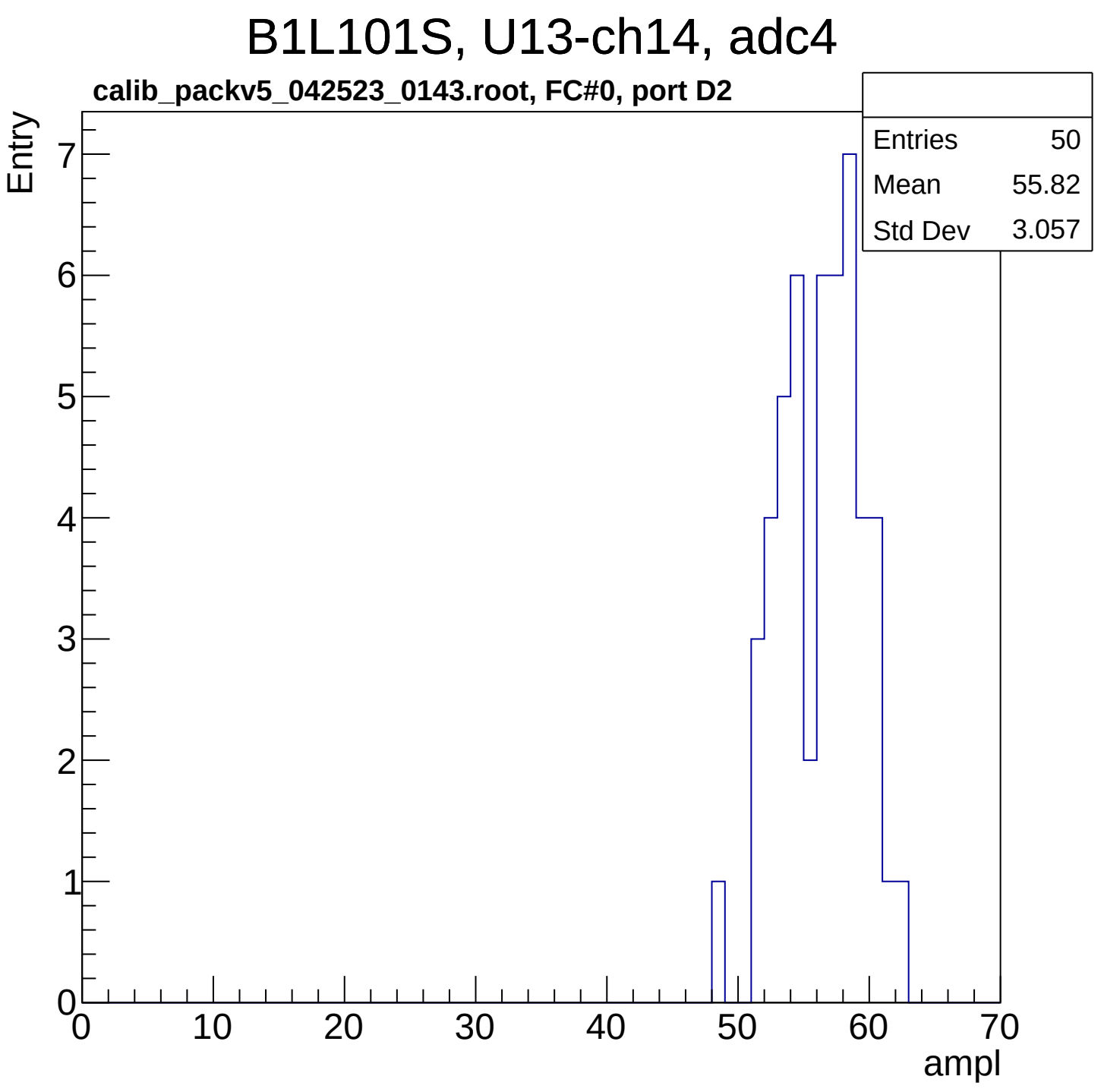
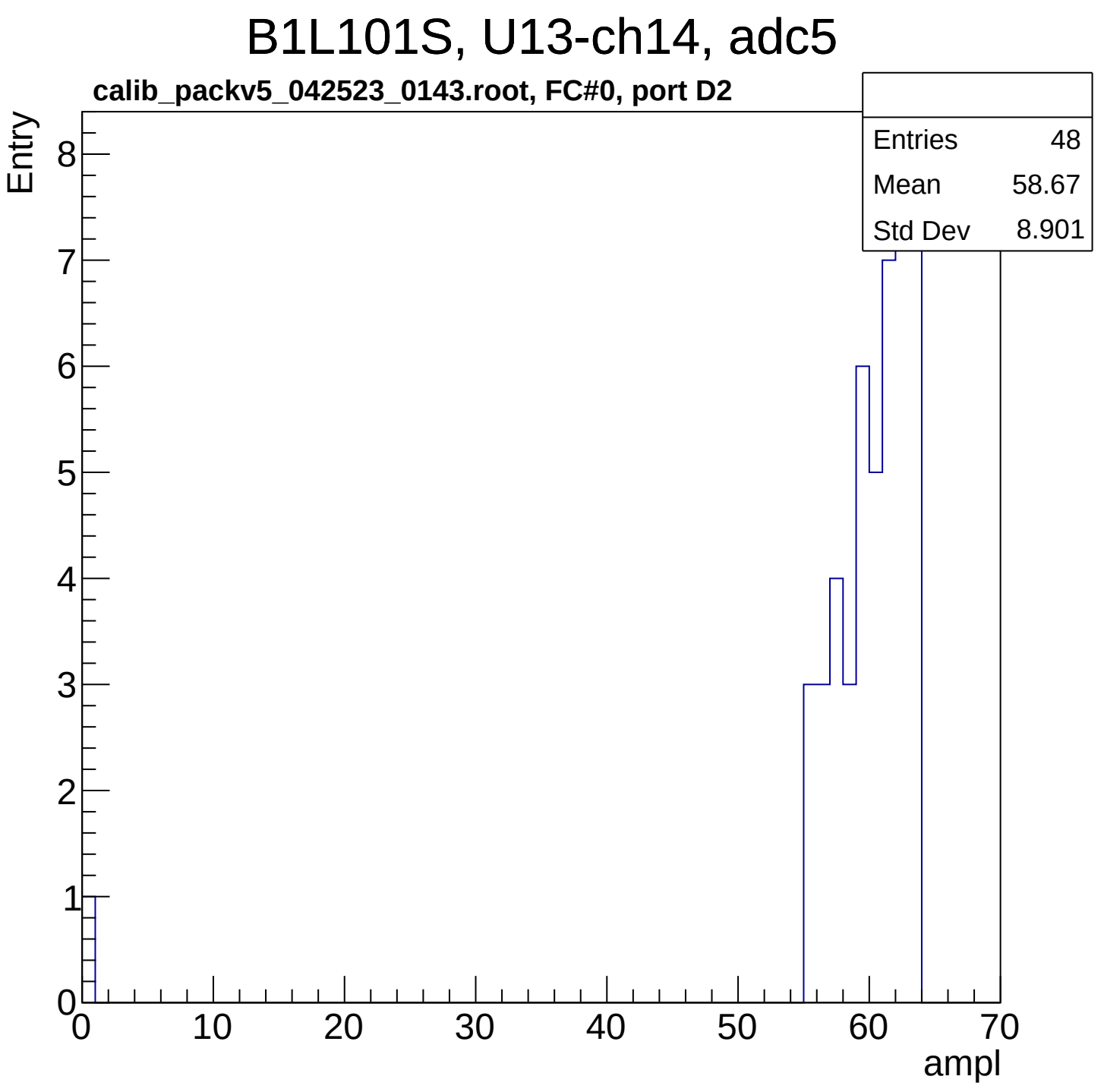
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch14, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 50.64
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch14, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 50
Mean 55.82
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch14, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 58.67
Std Dev 8.901
Entry
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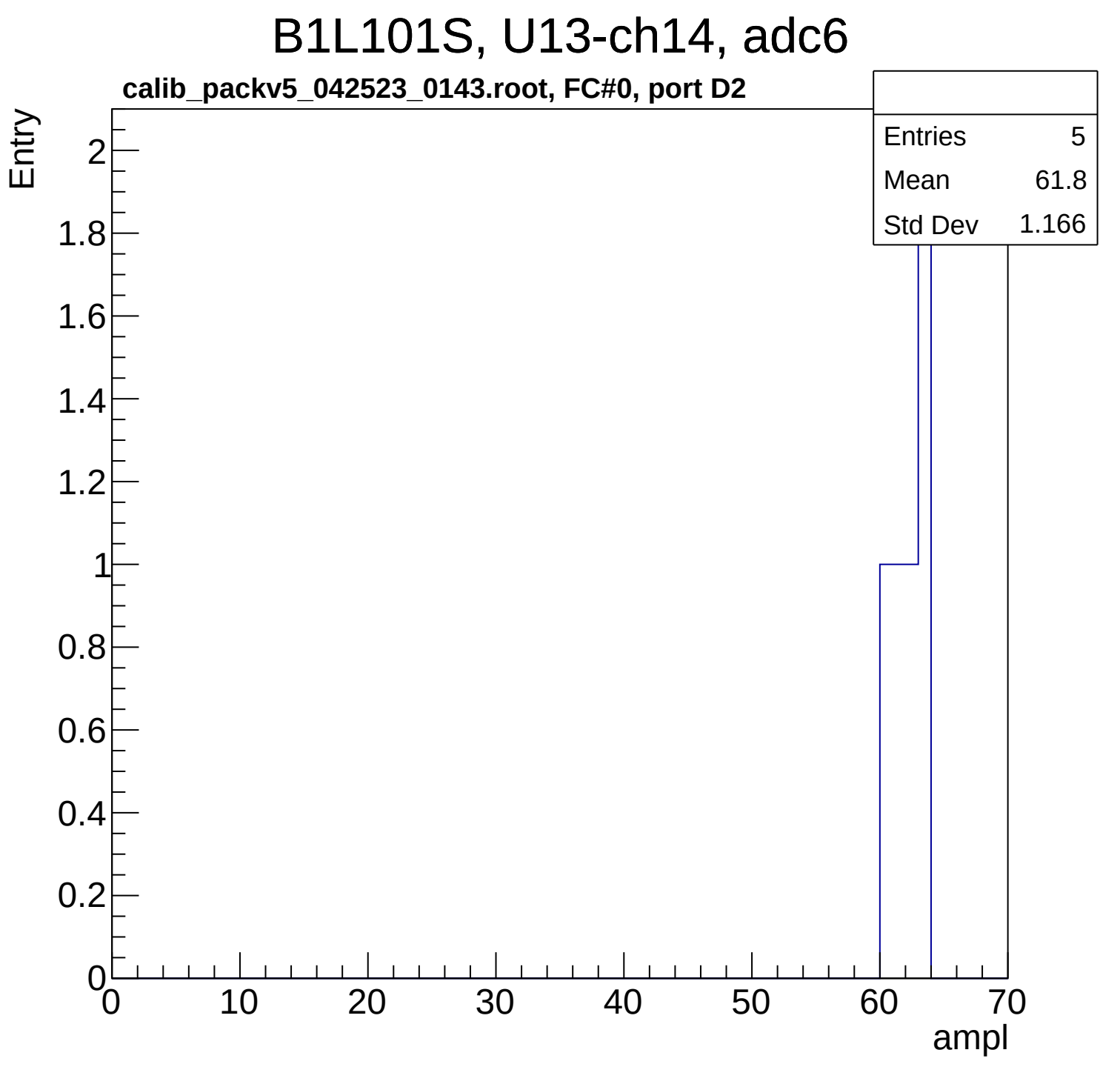
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch14, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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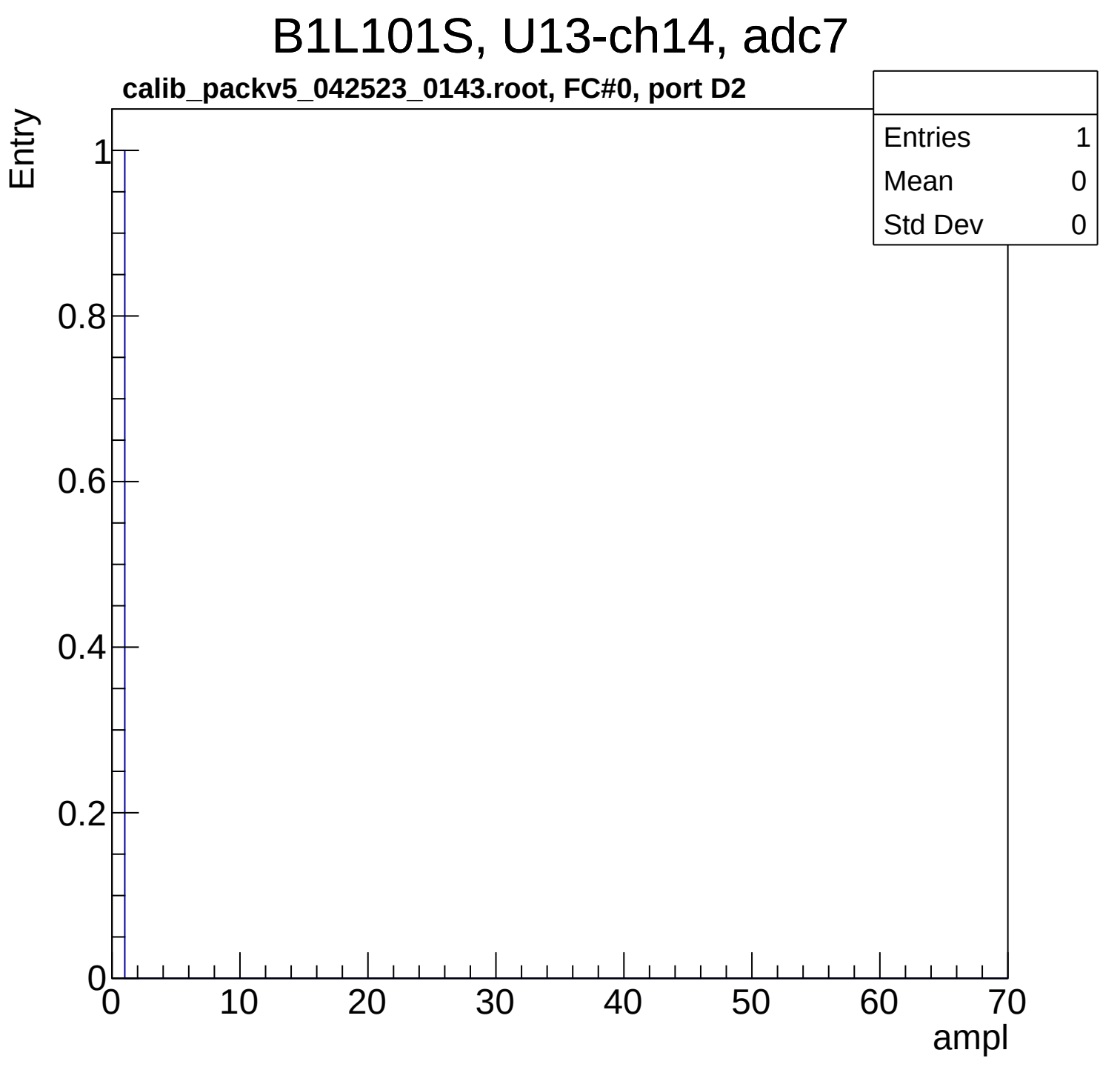
B1L101S, U13-ch14, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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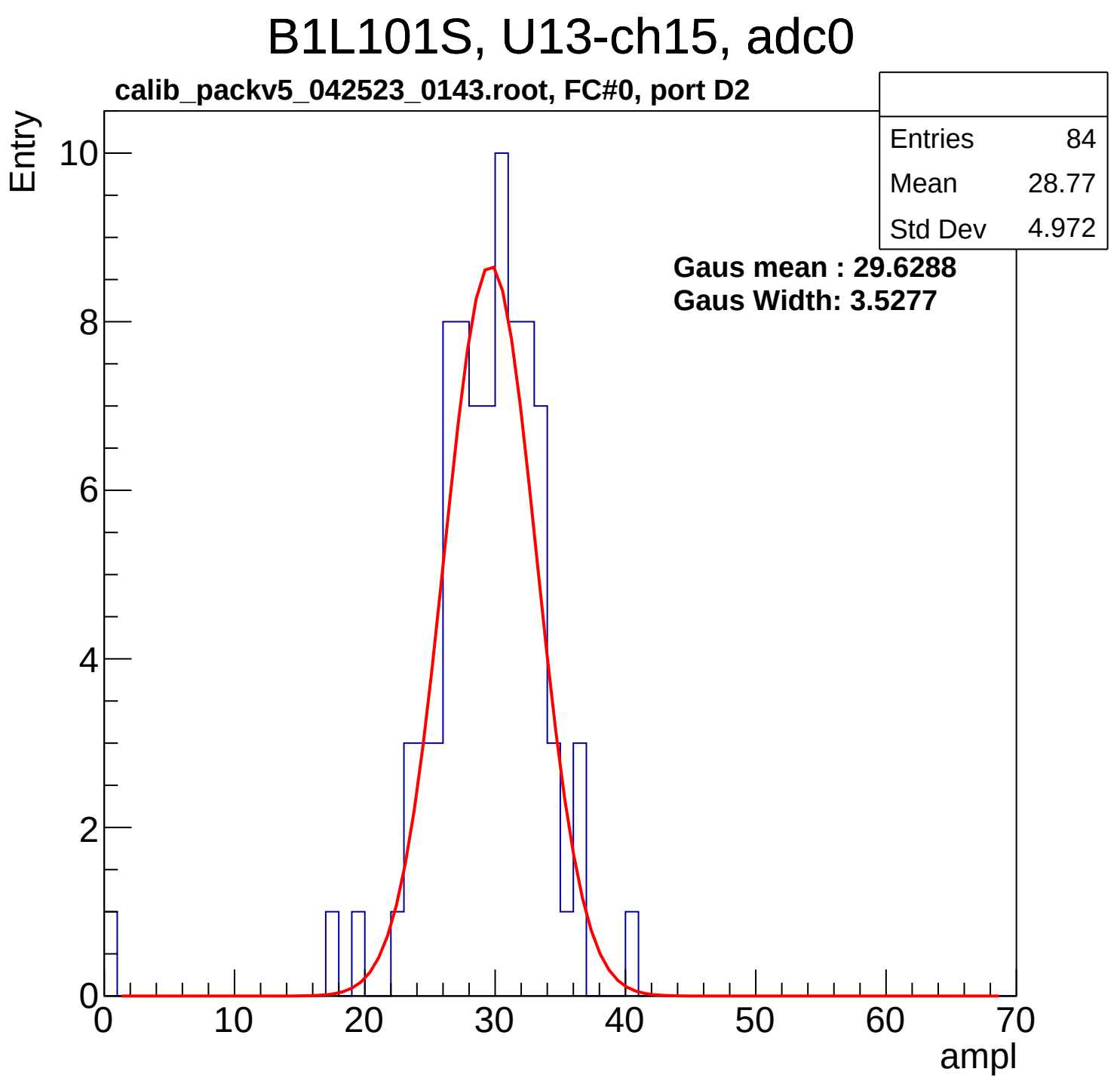
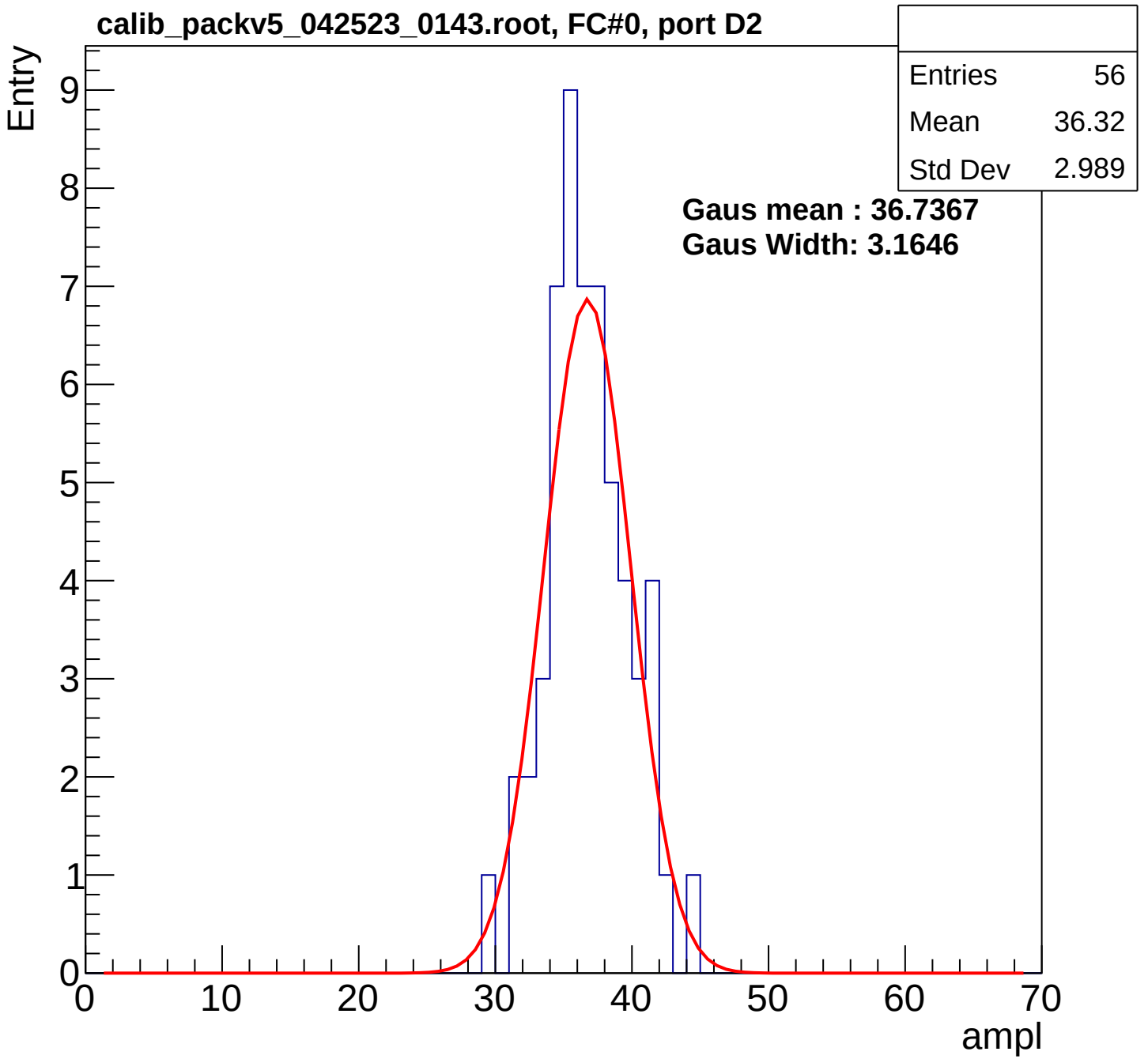
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch15, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 84
Mean 28.77
Std Dev 4.972
Gaus mean : 29.6288
Gaus Width: 3.5277
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch15, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 36.32
Std Dev 2.989
Gaus mean : 36.7367
Gaus Width: 3.1646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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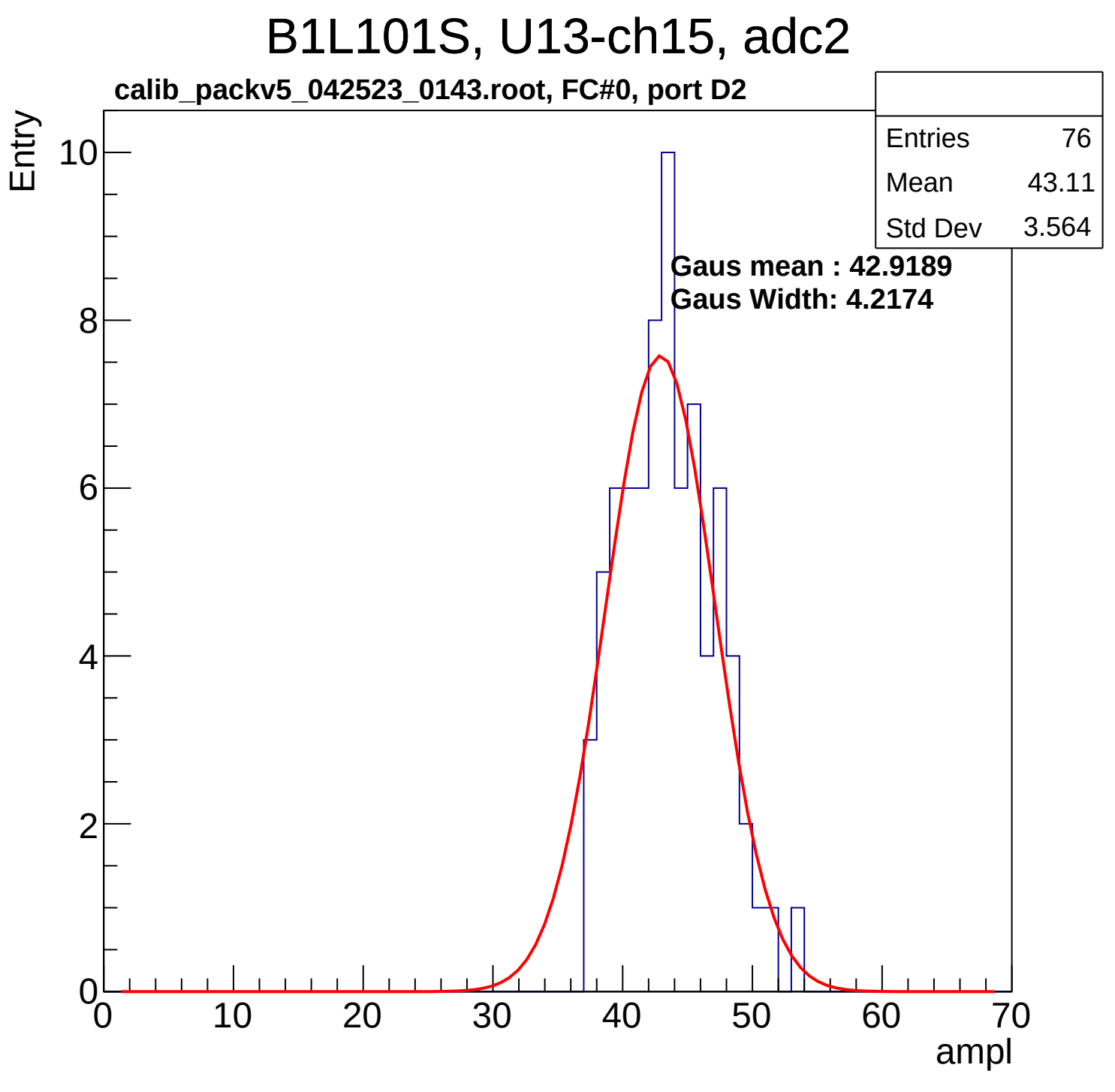
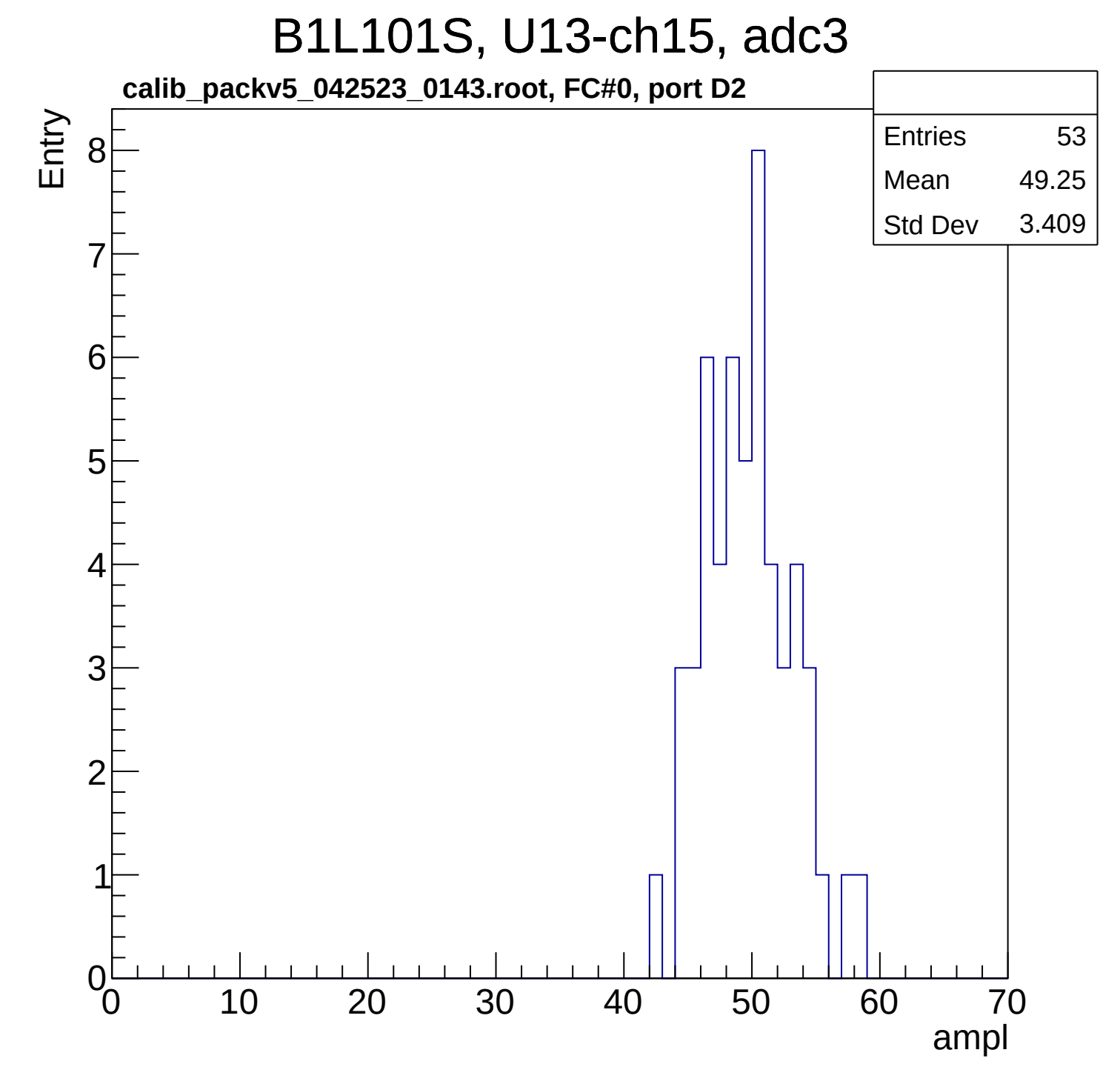

B1L101S, U13-ch15, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 43.11
Std Dev 3.564
Gaus mean : 42.9189
Gaus Width: 4.2174
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch15, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 49.25
Std Dev 3.409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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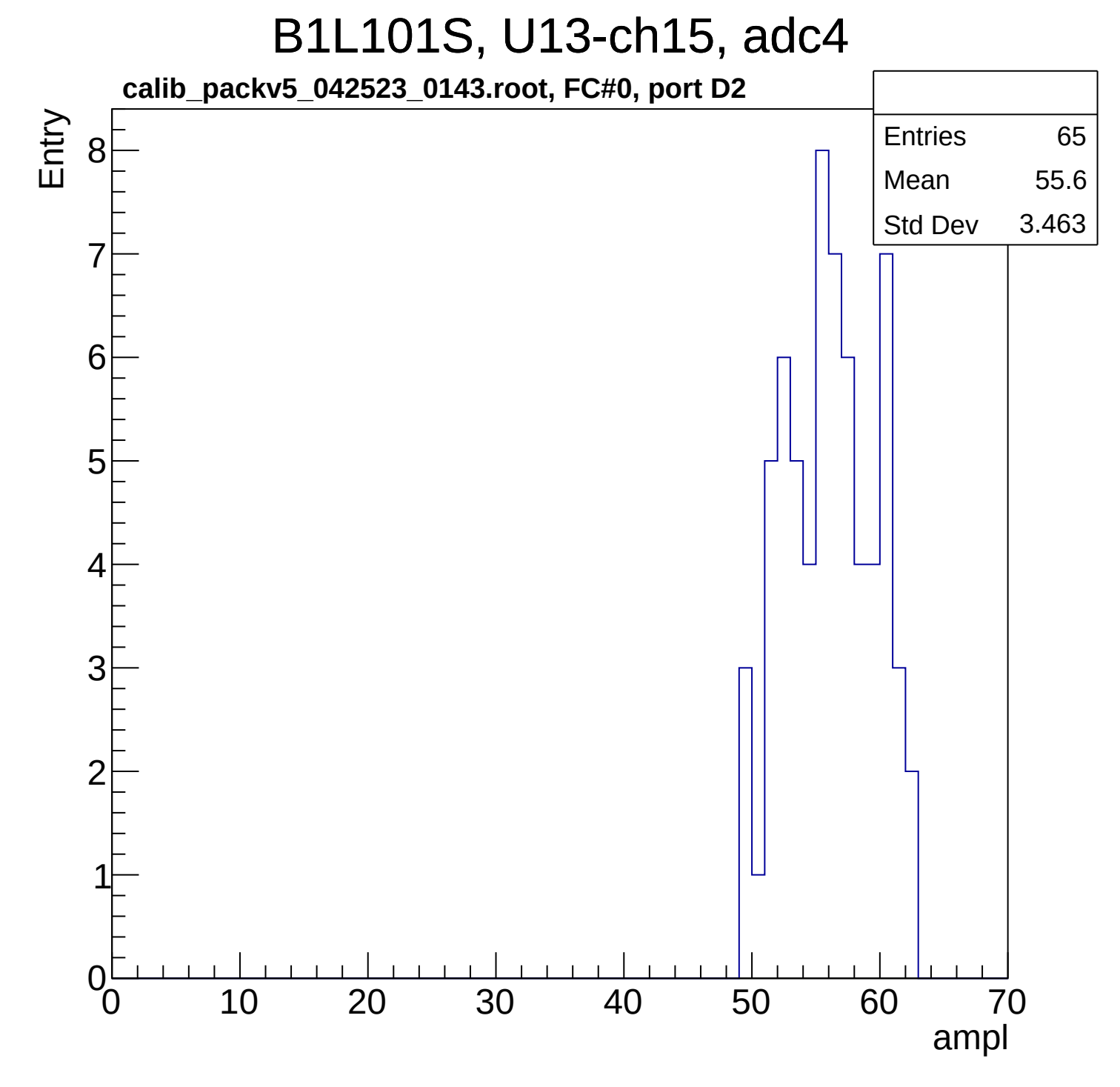
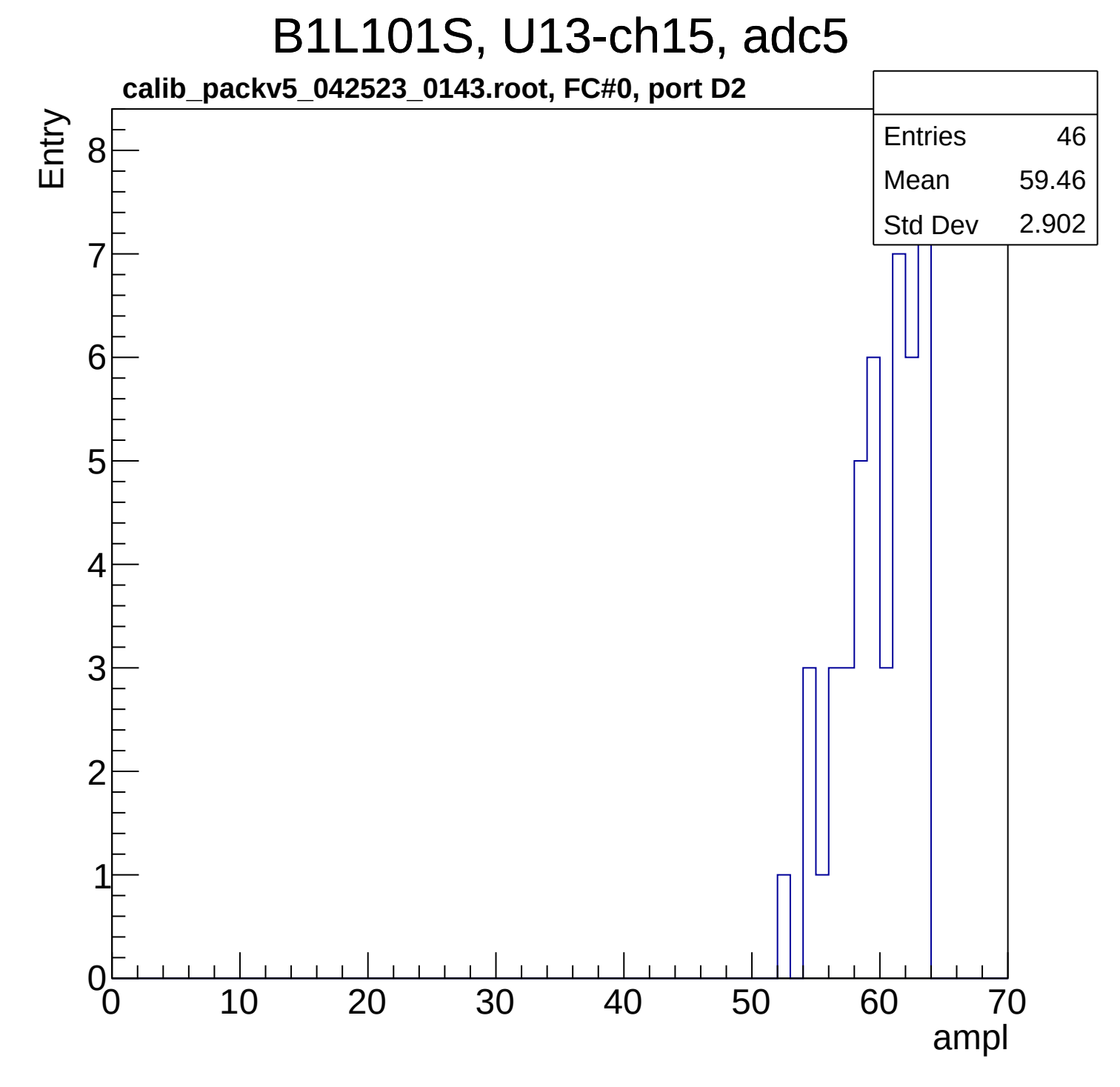

B1L101S, U13-ch15, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 55.6
Std Dev 3.463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch15, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 59.46
Std Dev 2.902
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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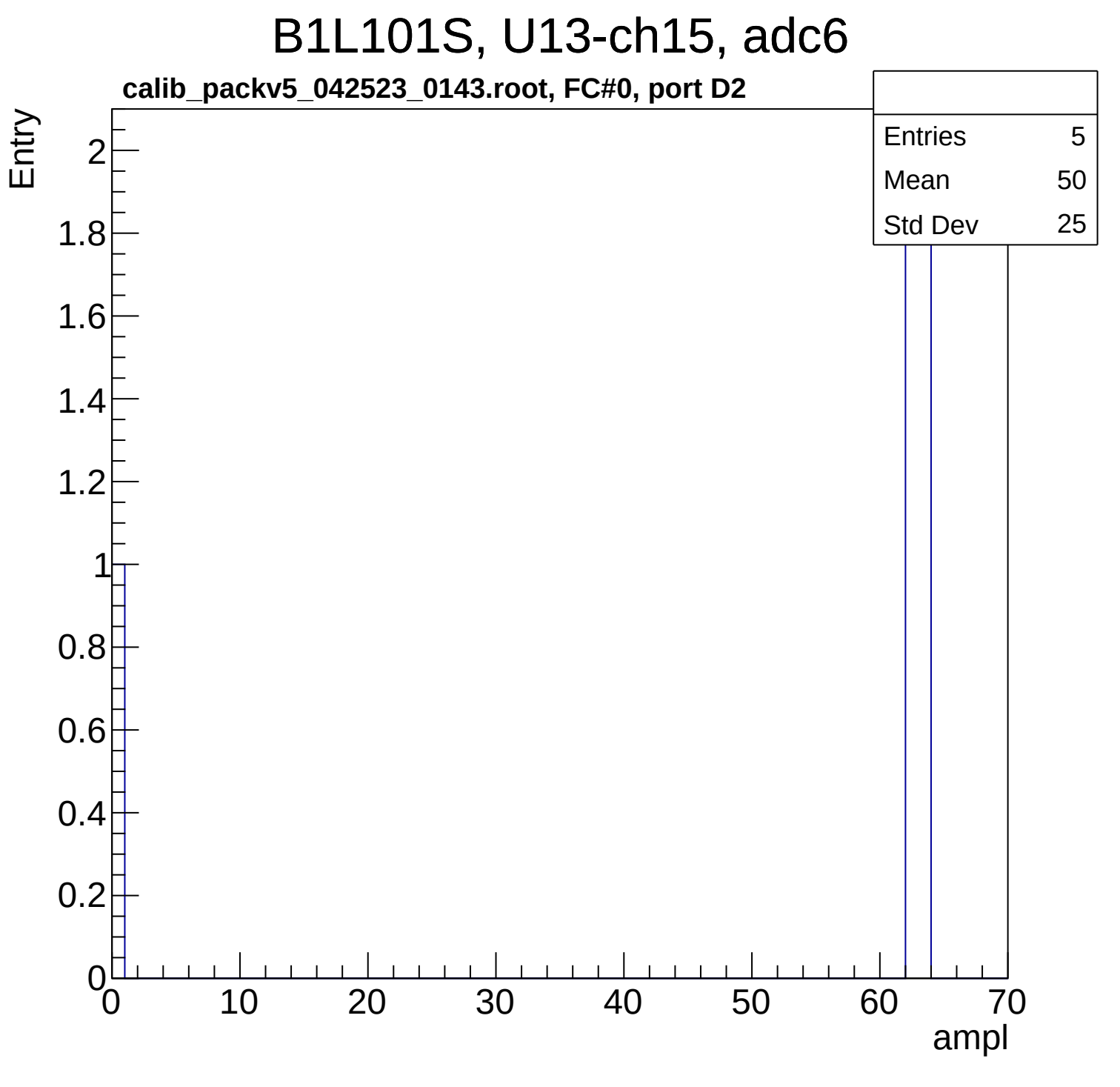
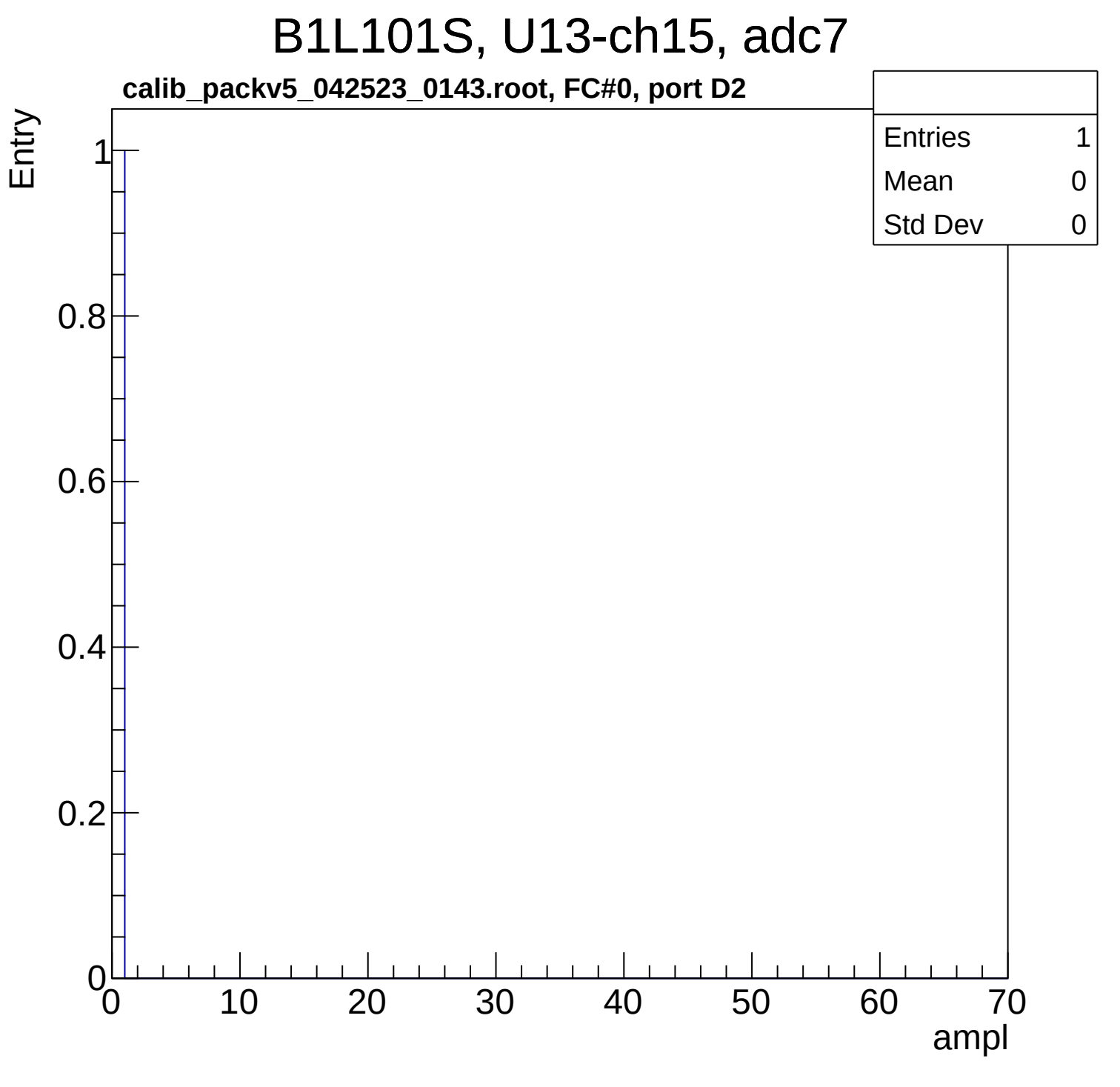
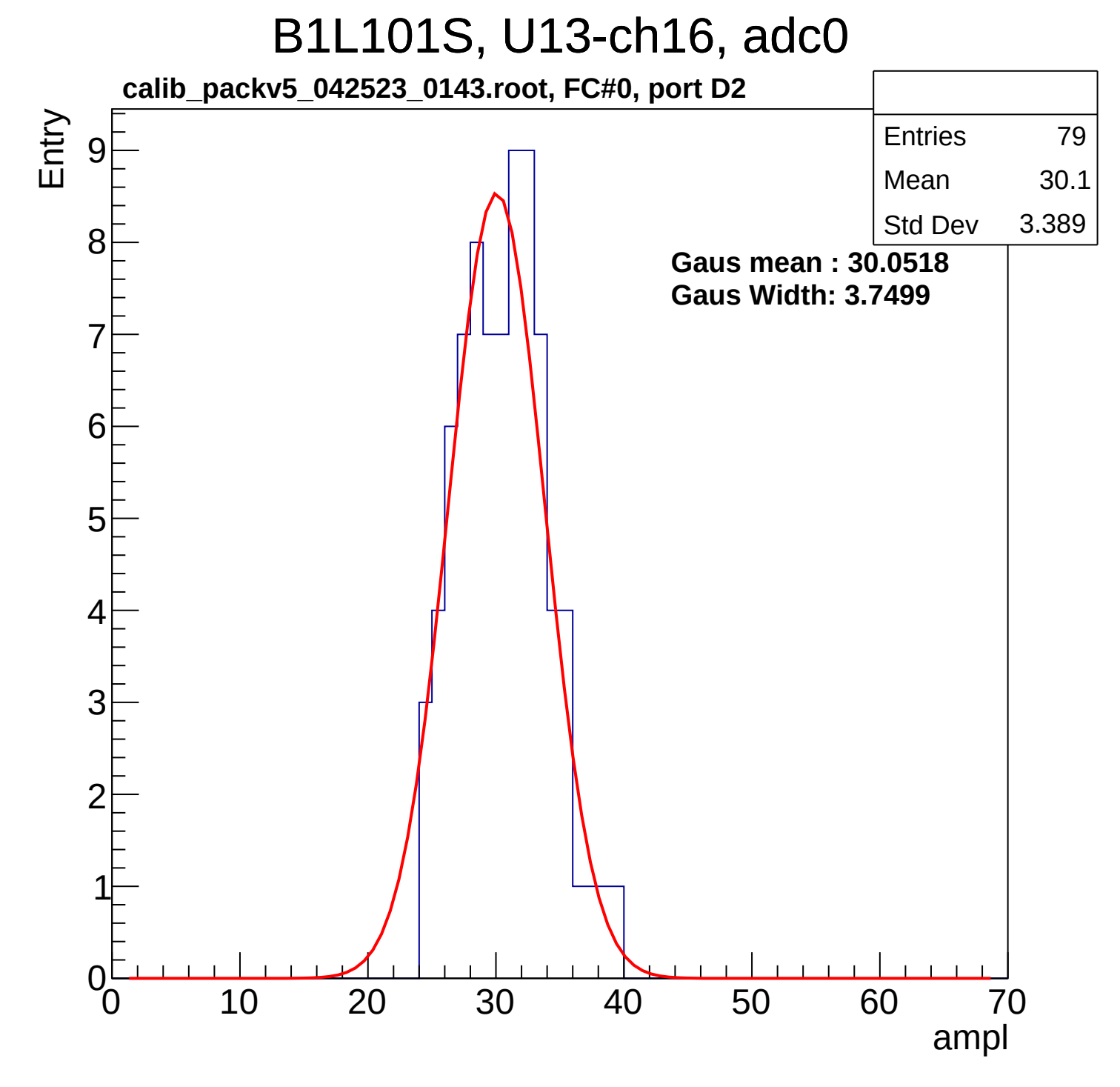
60
70


B1L101S, U13-ch15, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch15, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch16, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 79
Mean 30.1
Std Dev 3.389
Gaus mean : 30.0518
Gaus Width: 3.7499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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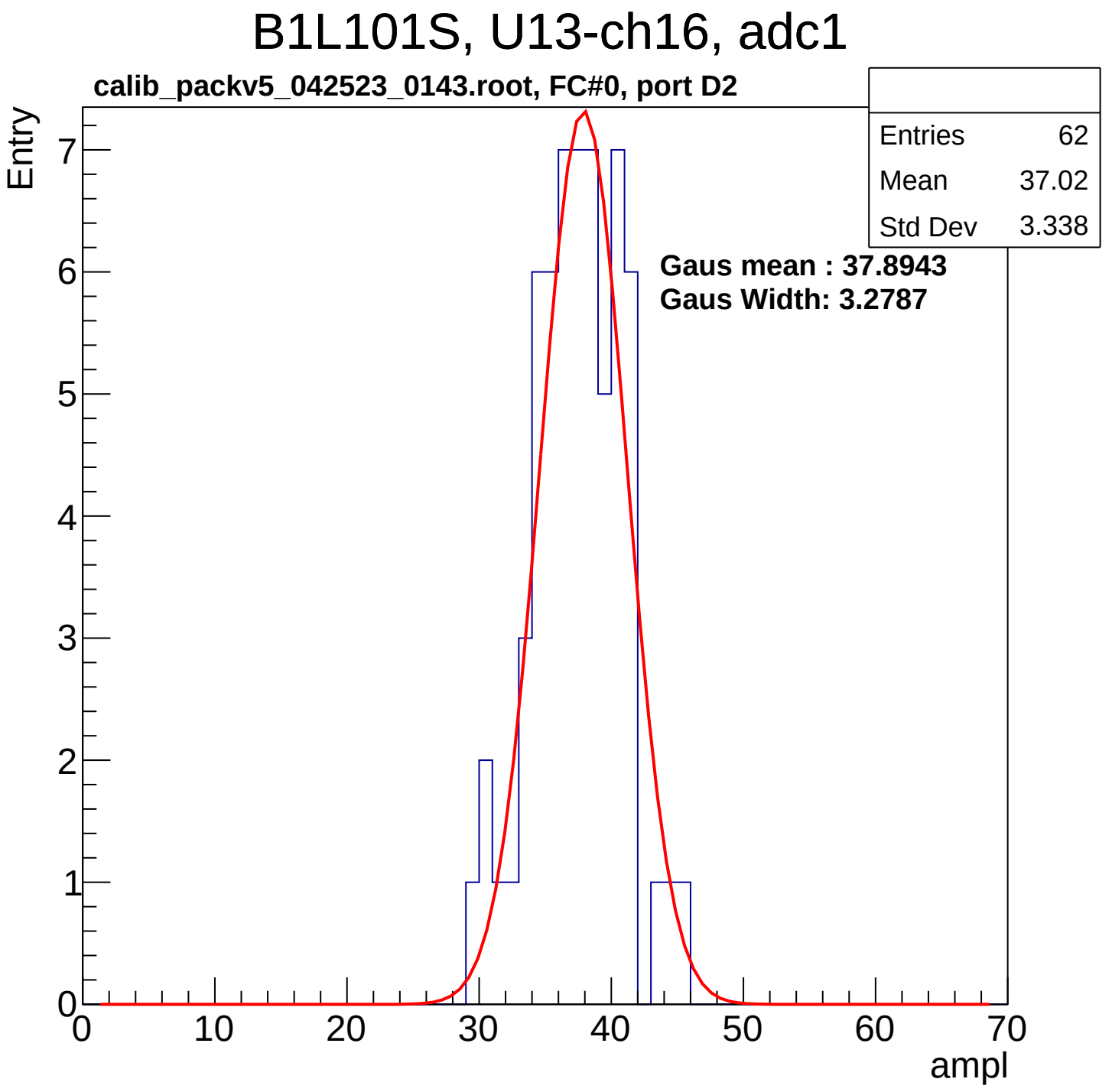
B1L101S, U13-ch16, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 37.02
Std Dev 3.338
Gaus mean : 37.8943
Gaus Width: 3.2787
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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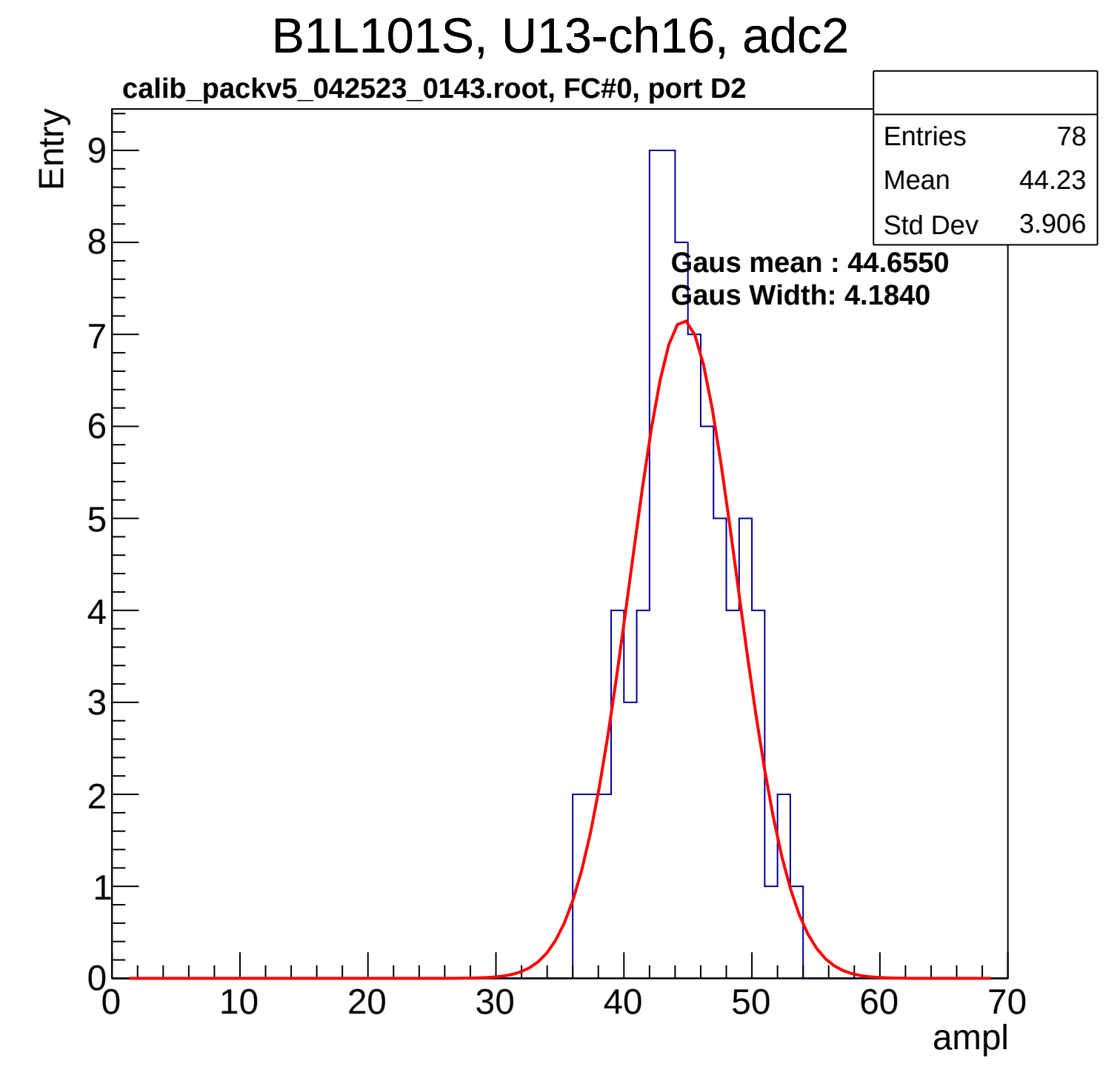
B1L101S, U13-ch16, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 44.23
Std Dev 3.906
Gaus mean : 44.6550
Gaus Width: 4.1840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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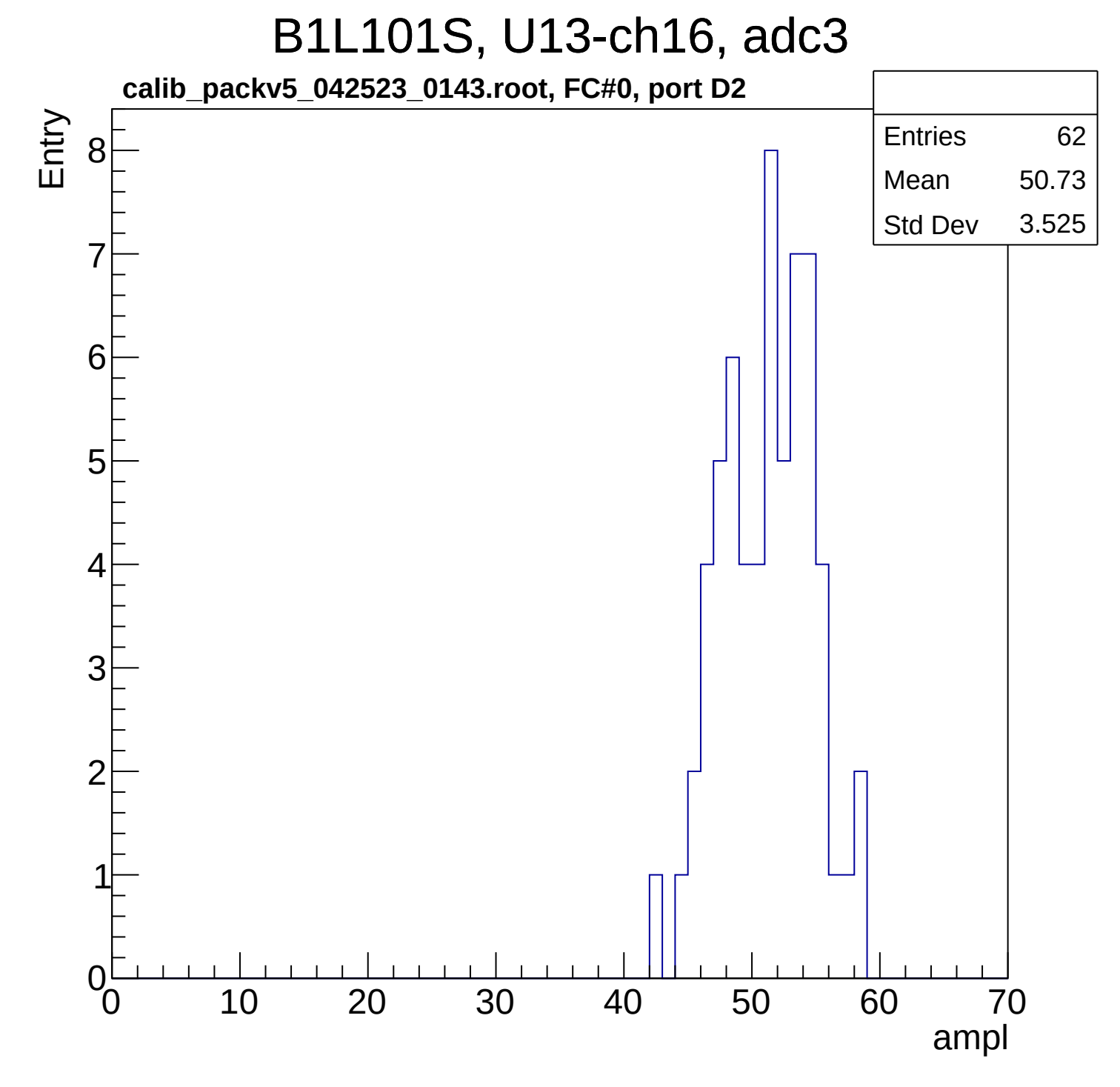
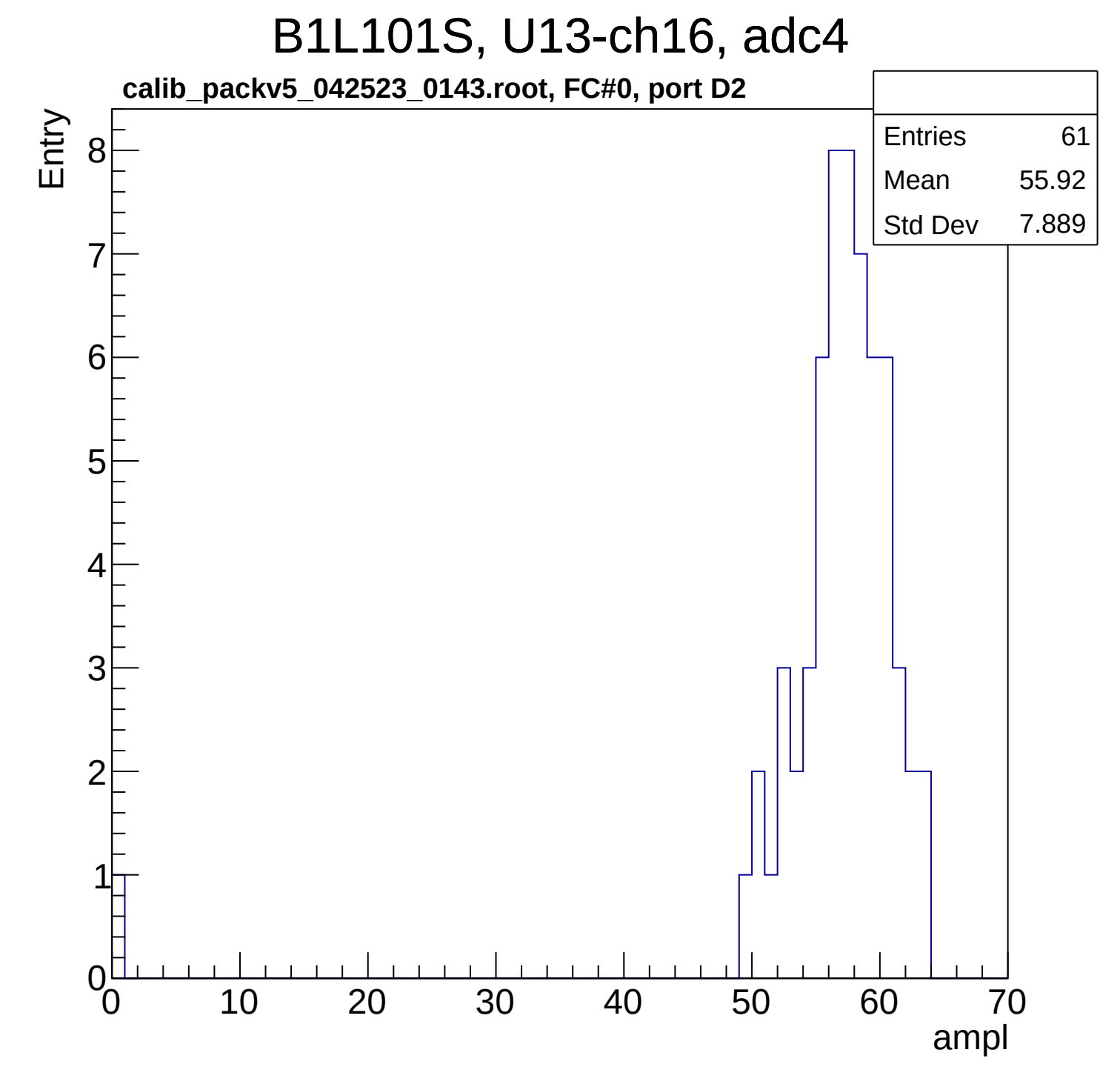
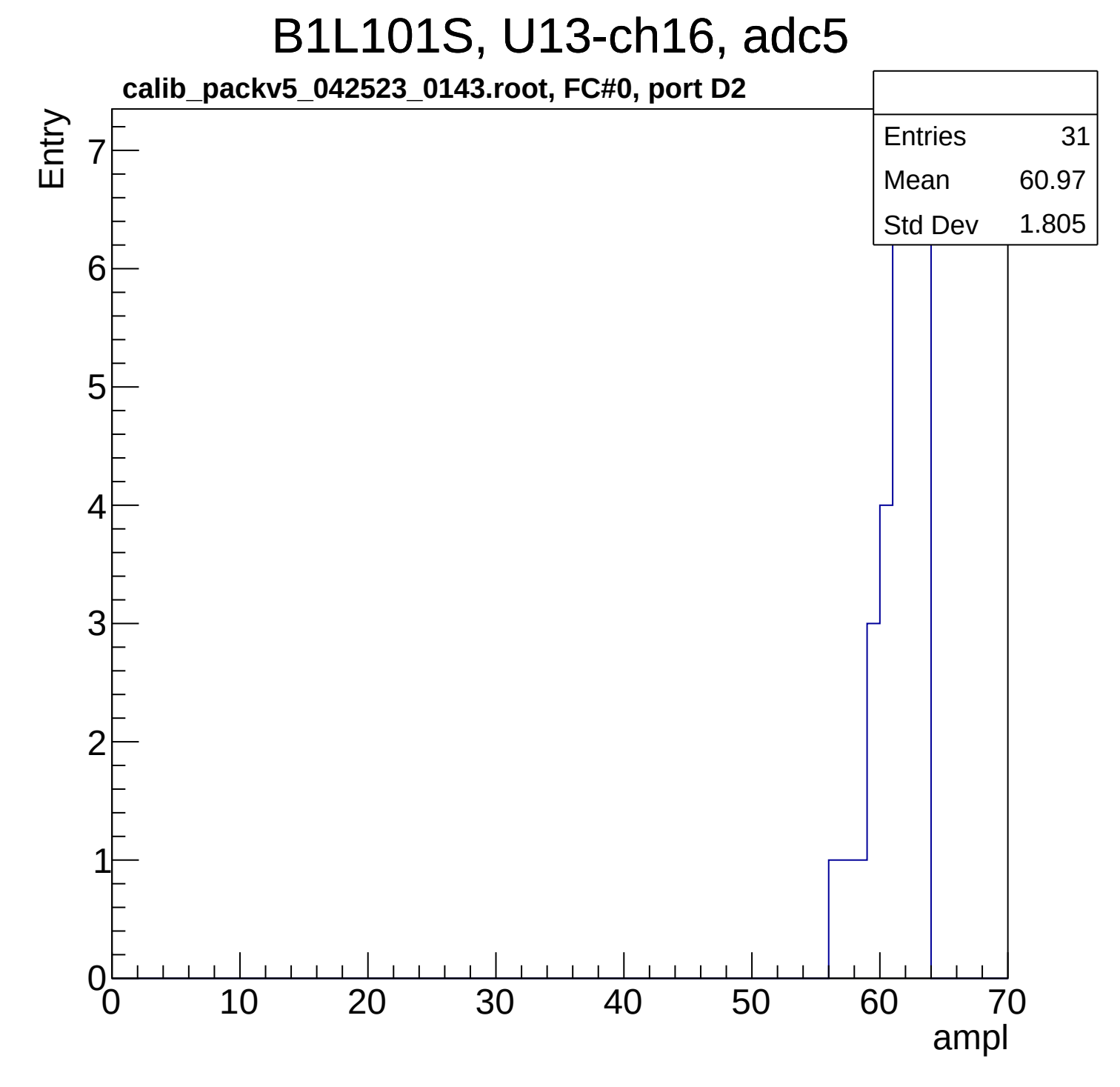
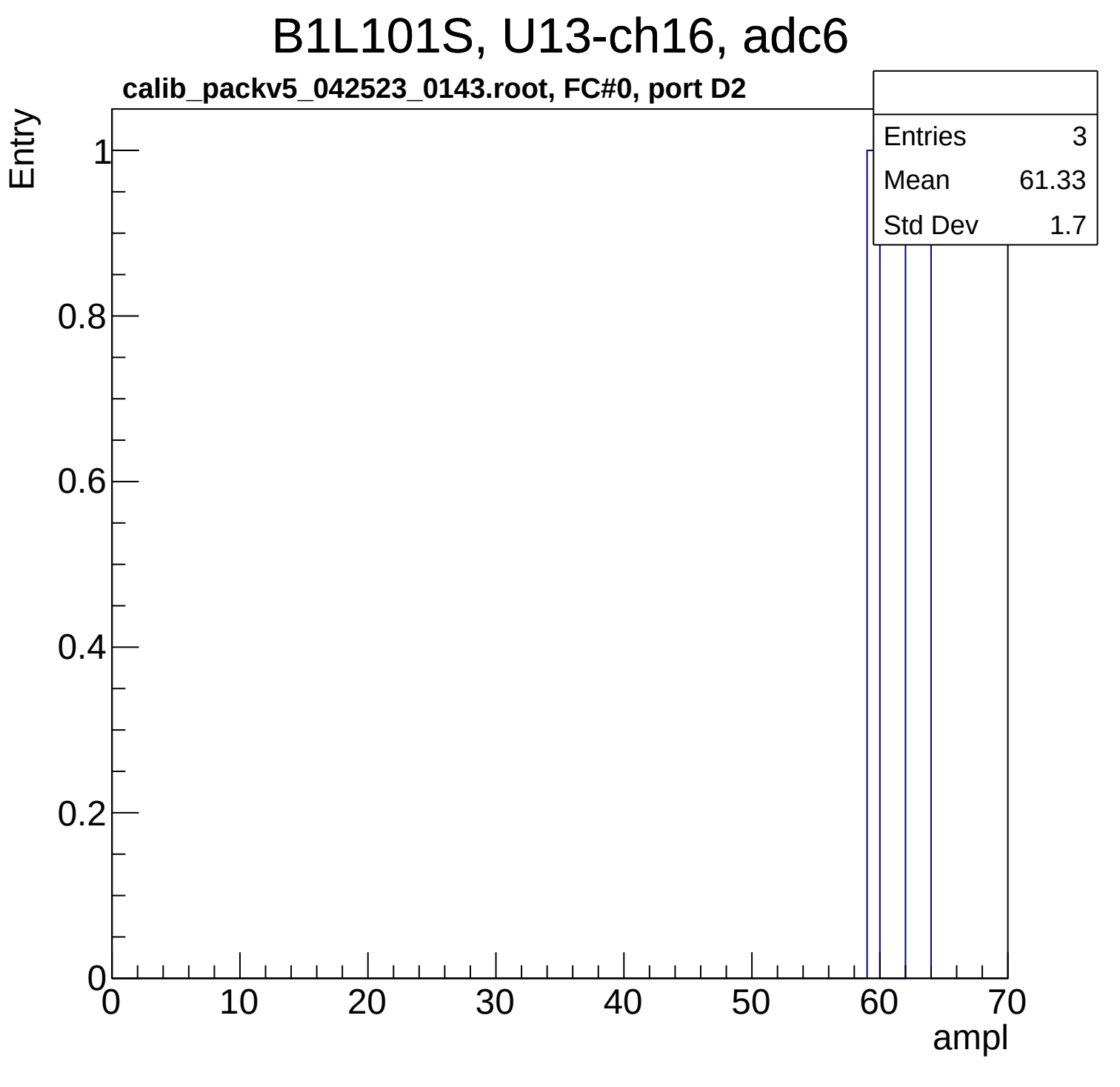
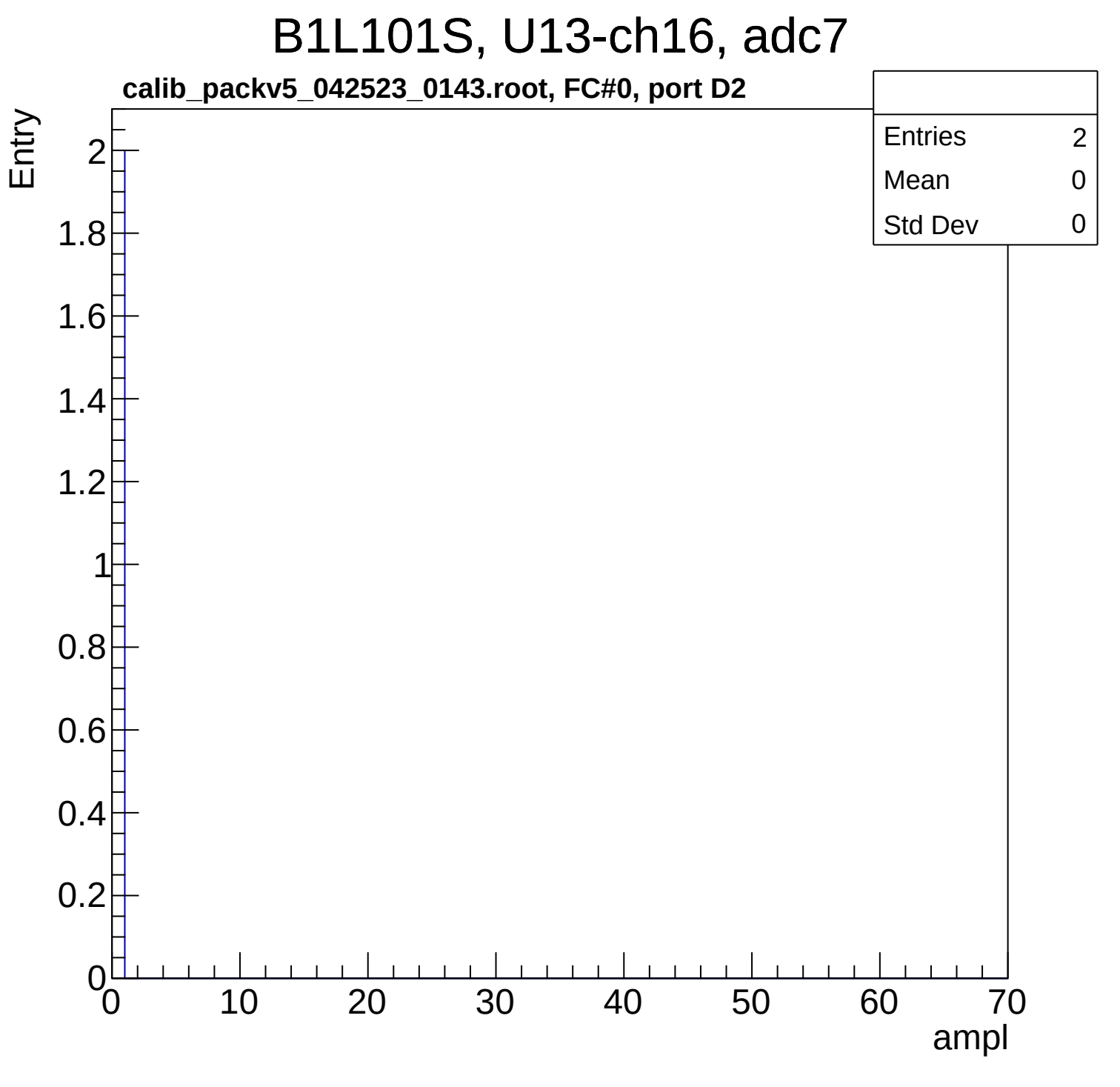
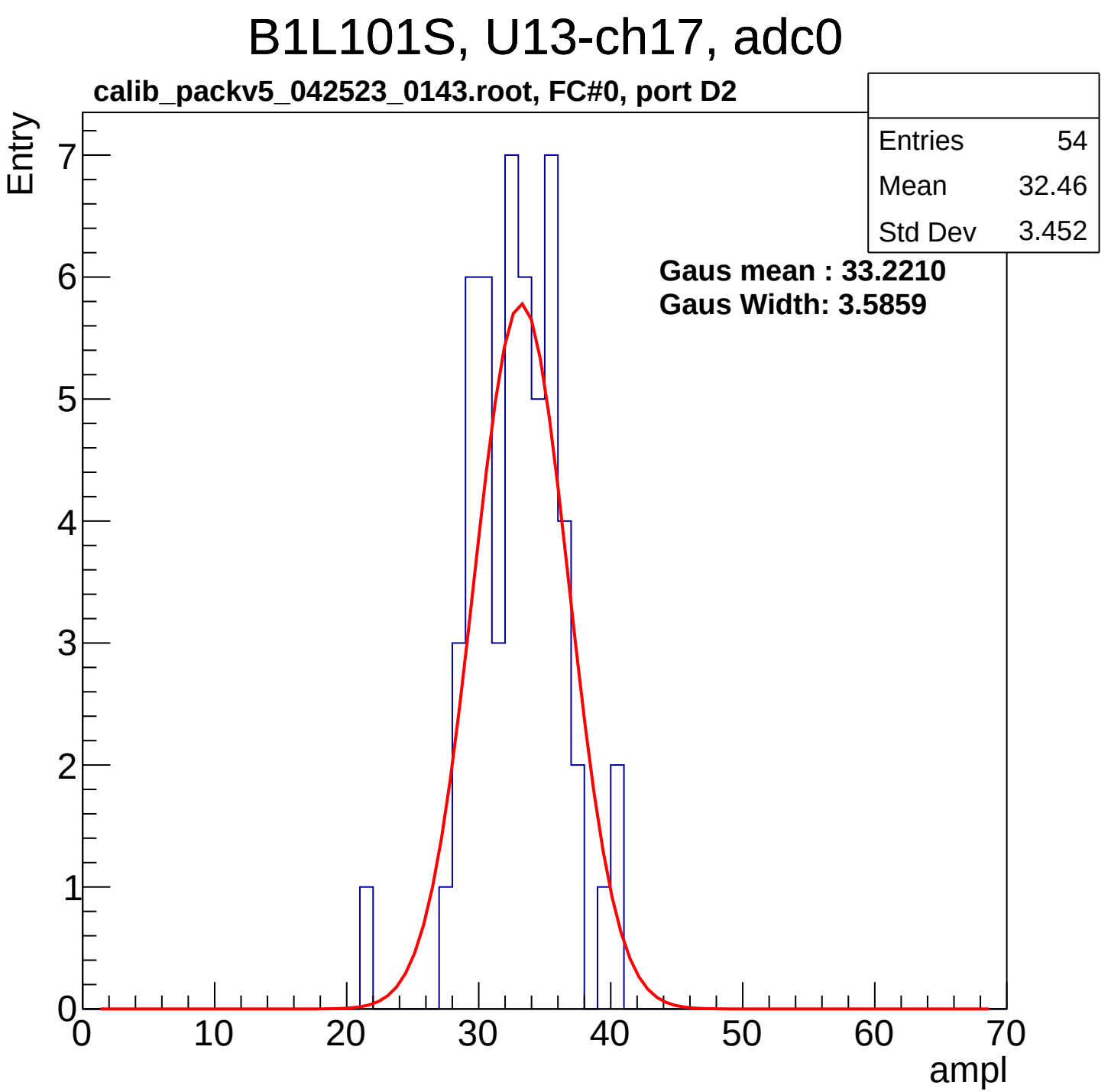

B1L101S, U13-ch16, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 50.73
Std Dev 3.525
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

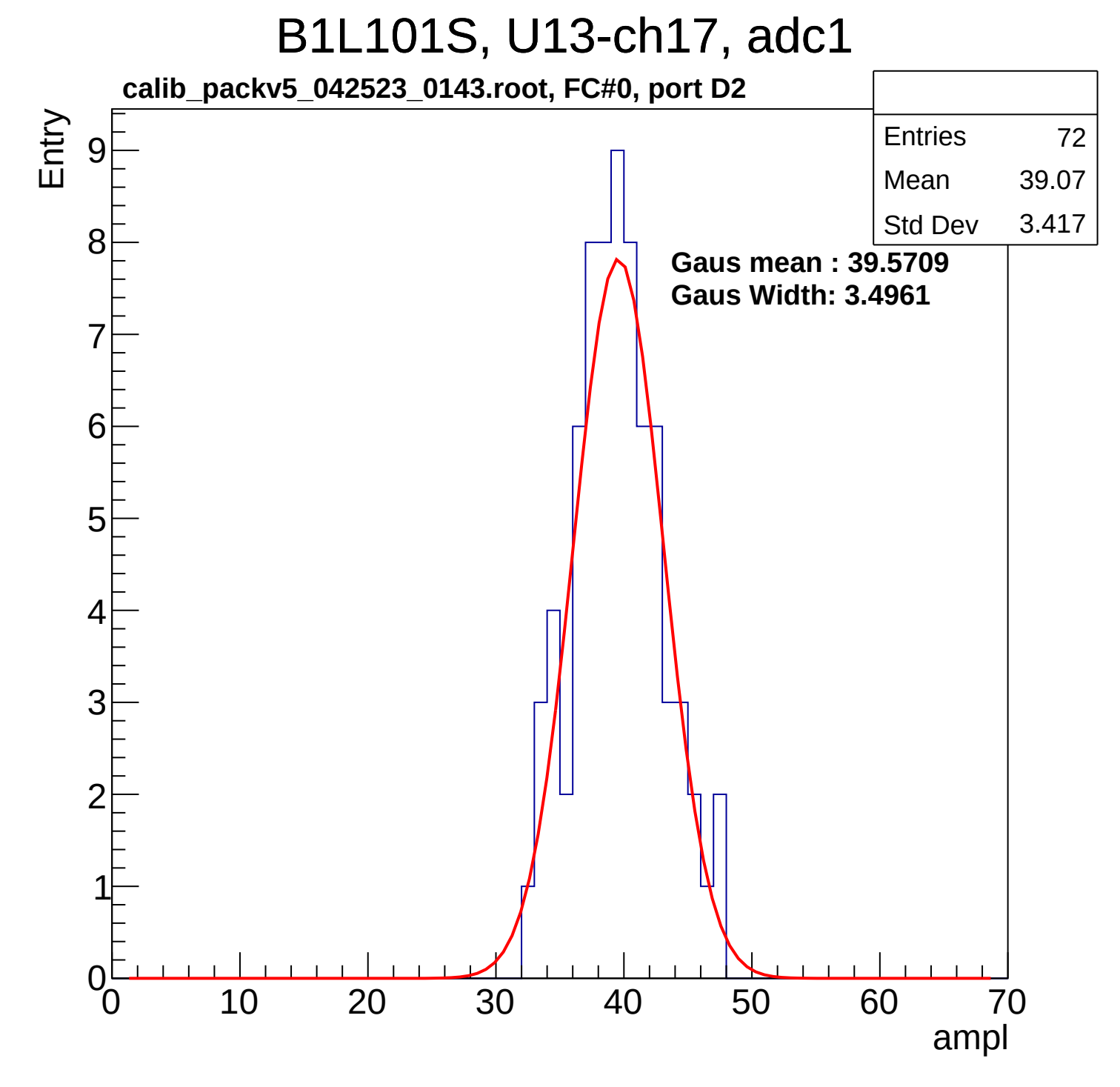

B1L101S, U13-ch16, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 55.92
Std Dev 7.889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

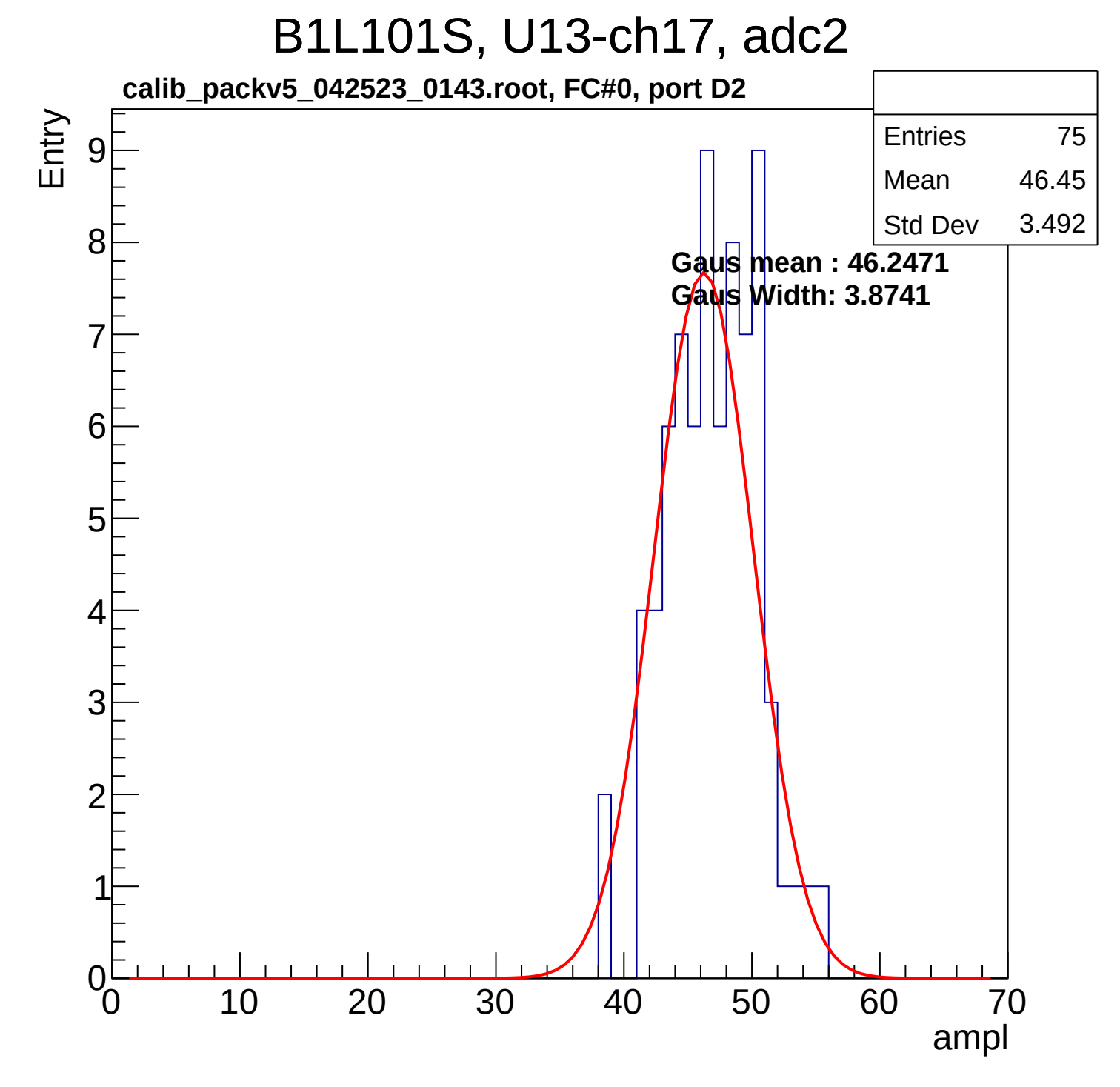

B1L101S, U13-ch16, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 31
Mean 60.97
Std Dev 1.805
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

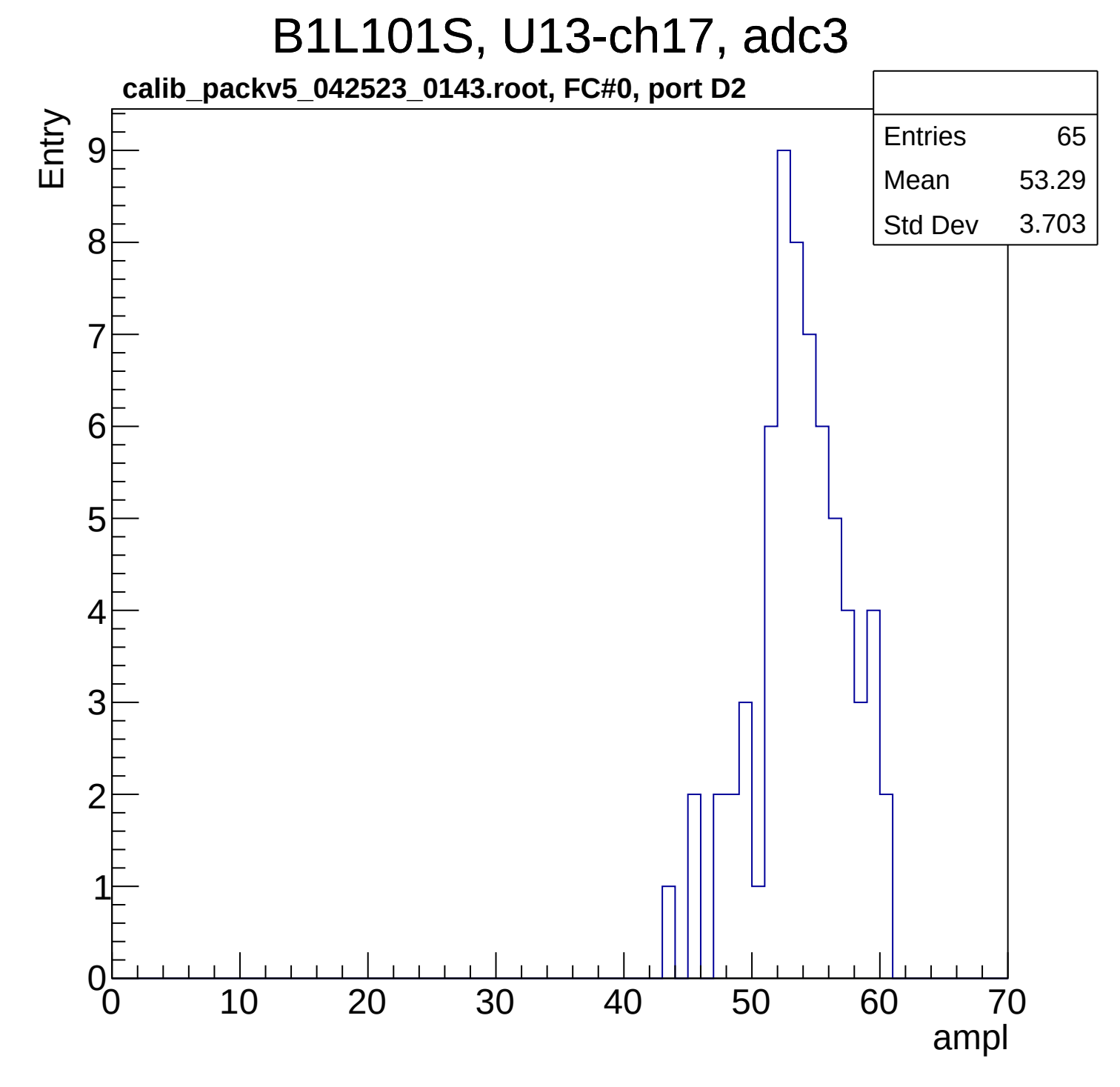

B1L101S, U13-ch16, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 61.33
Std Dev 1.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L101S, U13-ch16, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch17, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 32.46
Std Dev 3.452
Gaus mean : 33.2210
Gaus Width: 3.5859
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch17, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 39.07
Std Dev 3.417
Gaus mean : 39.5709
Gaus Width: 3.4961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch17, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 46.45
Std Dev 3.492
Gaus mean : 46.2471
Gaus Width: 3.8741
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L101S, U13-ch17, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 53.29
Std Dev 3.703
Entry
ampl
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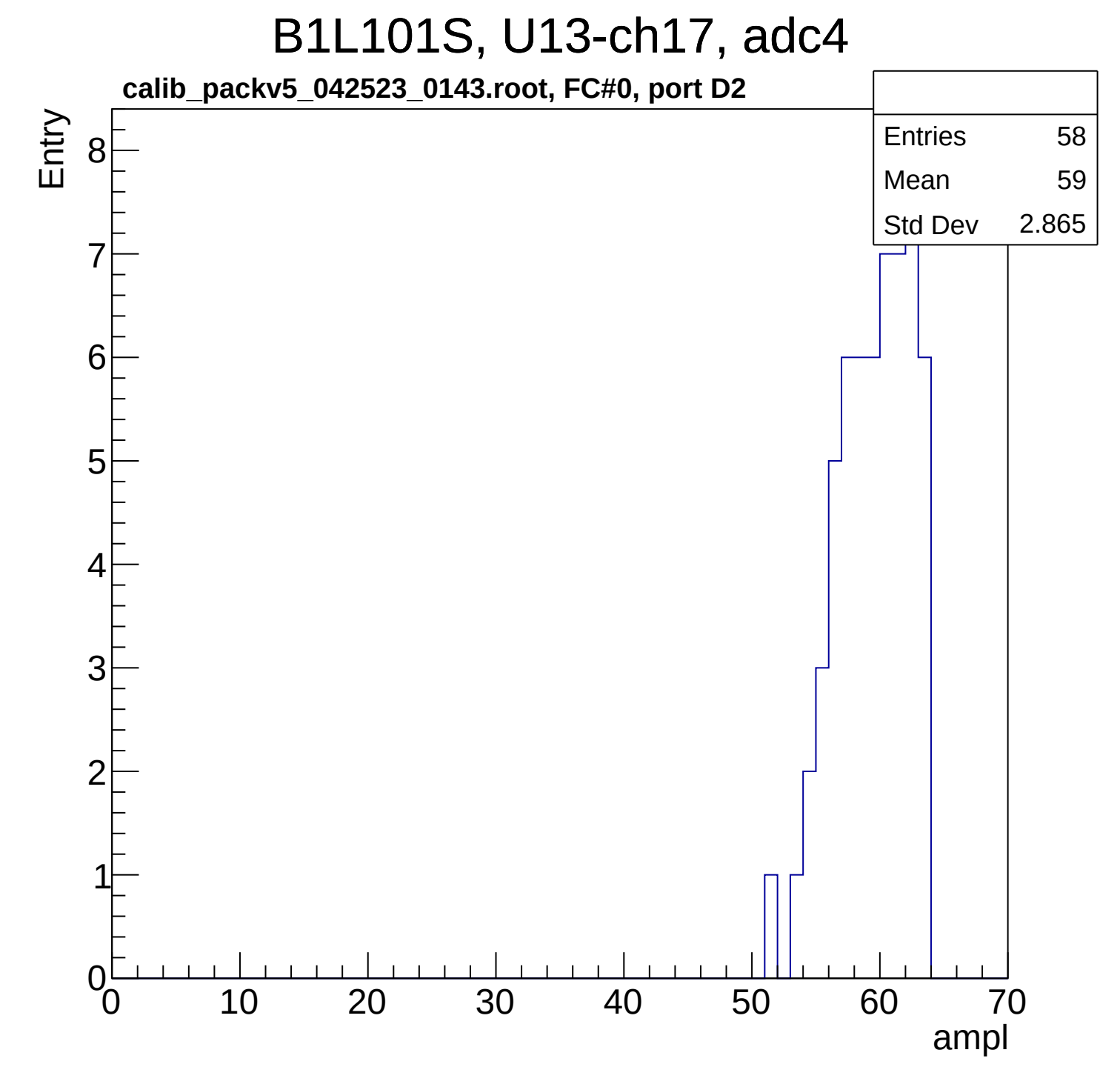
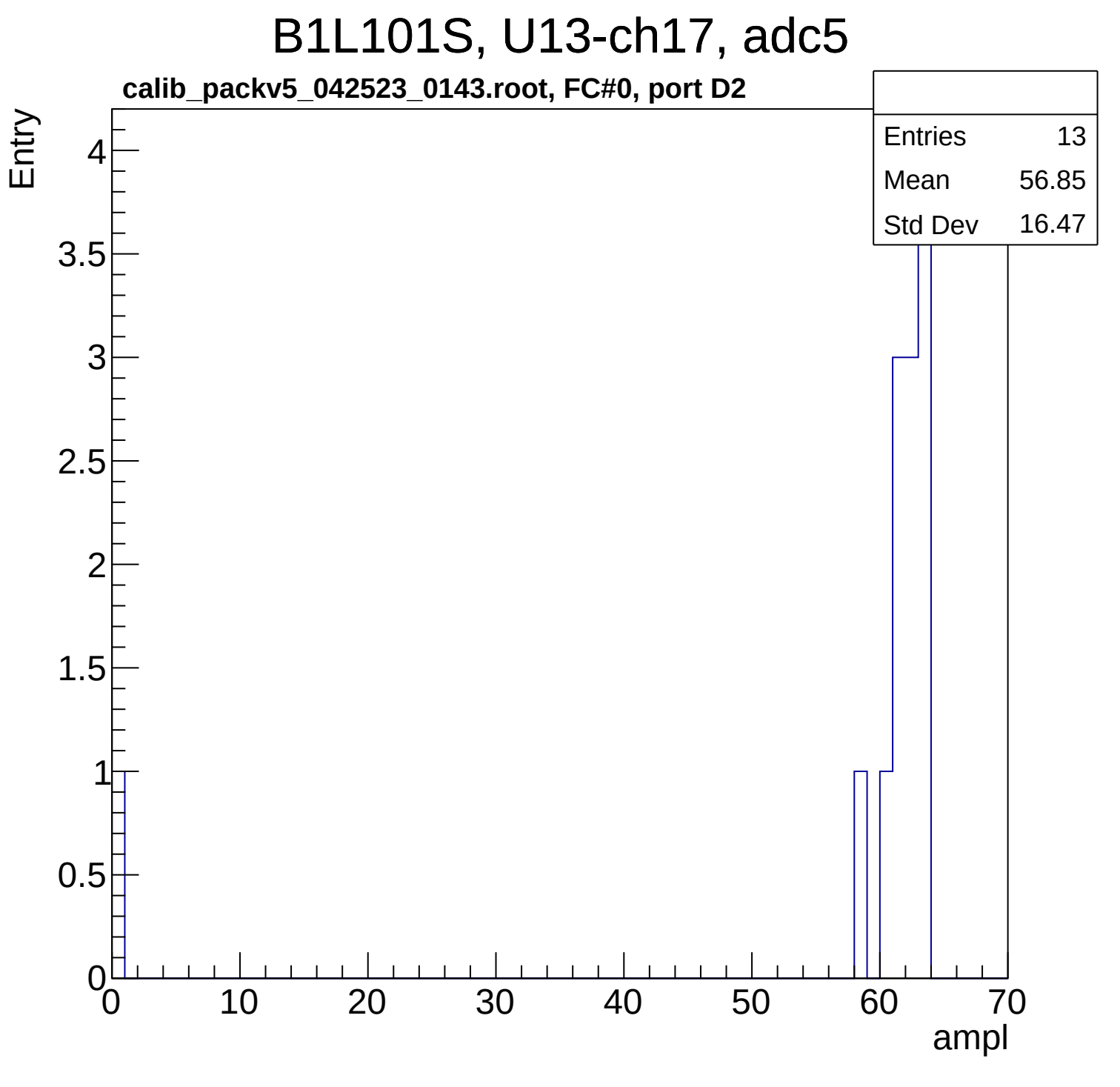
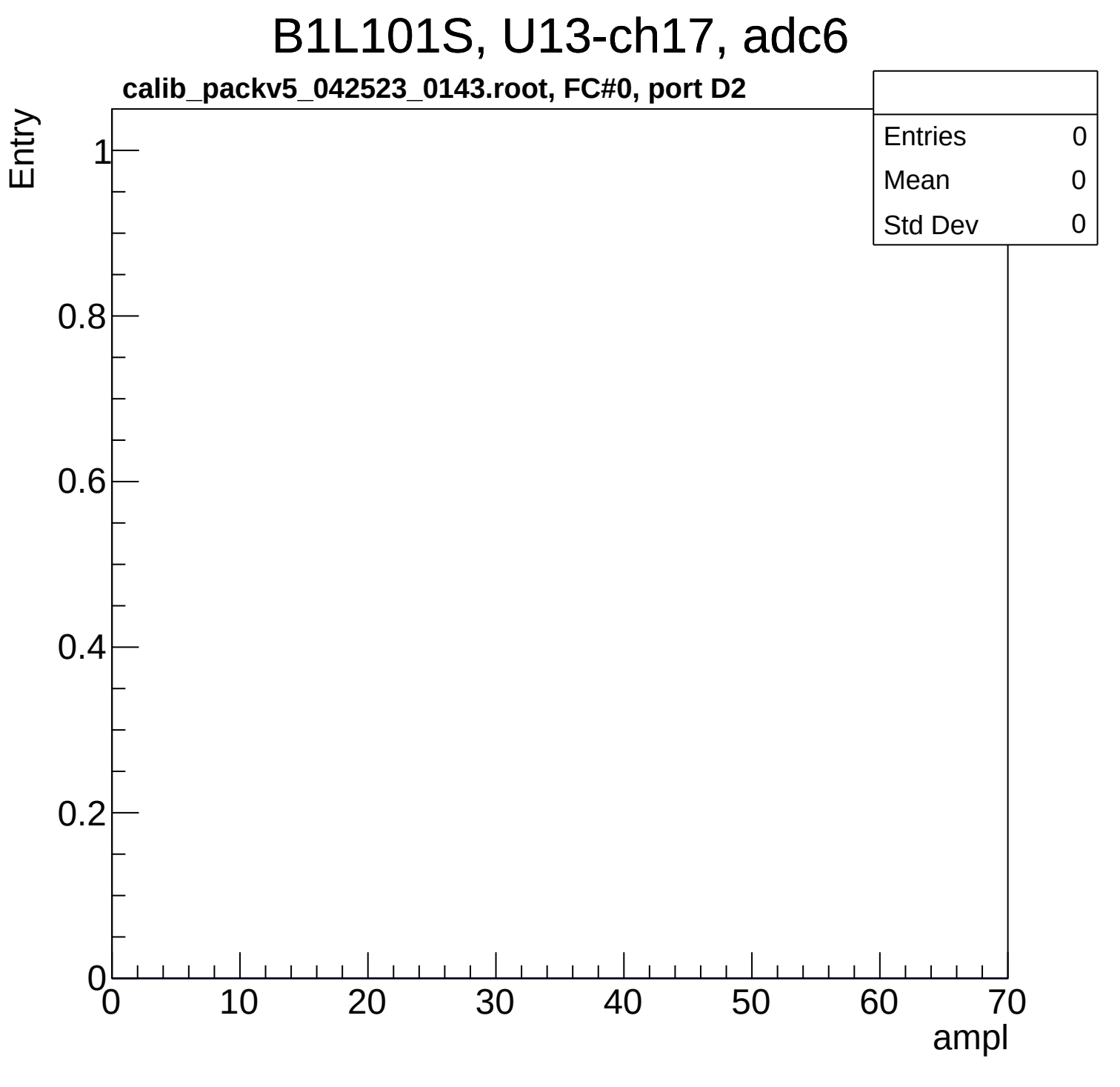
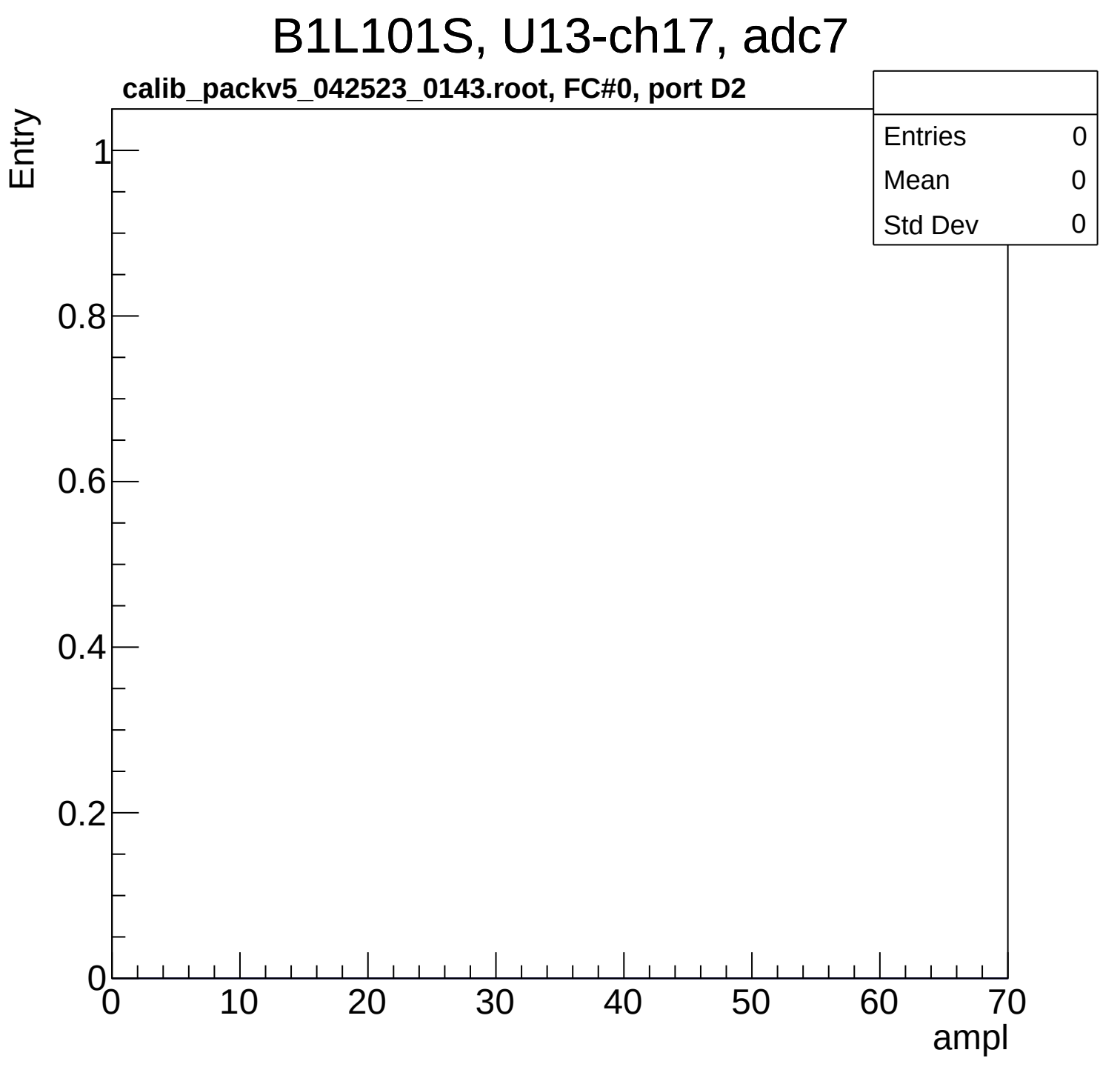
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch17, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 59
Std Dev 2.865
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch17, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 13
Mean 56.85
Std Dev 16.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L101S, U13-ch17, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch17, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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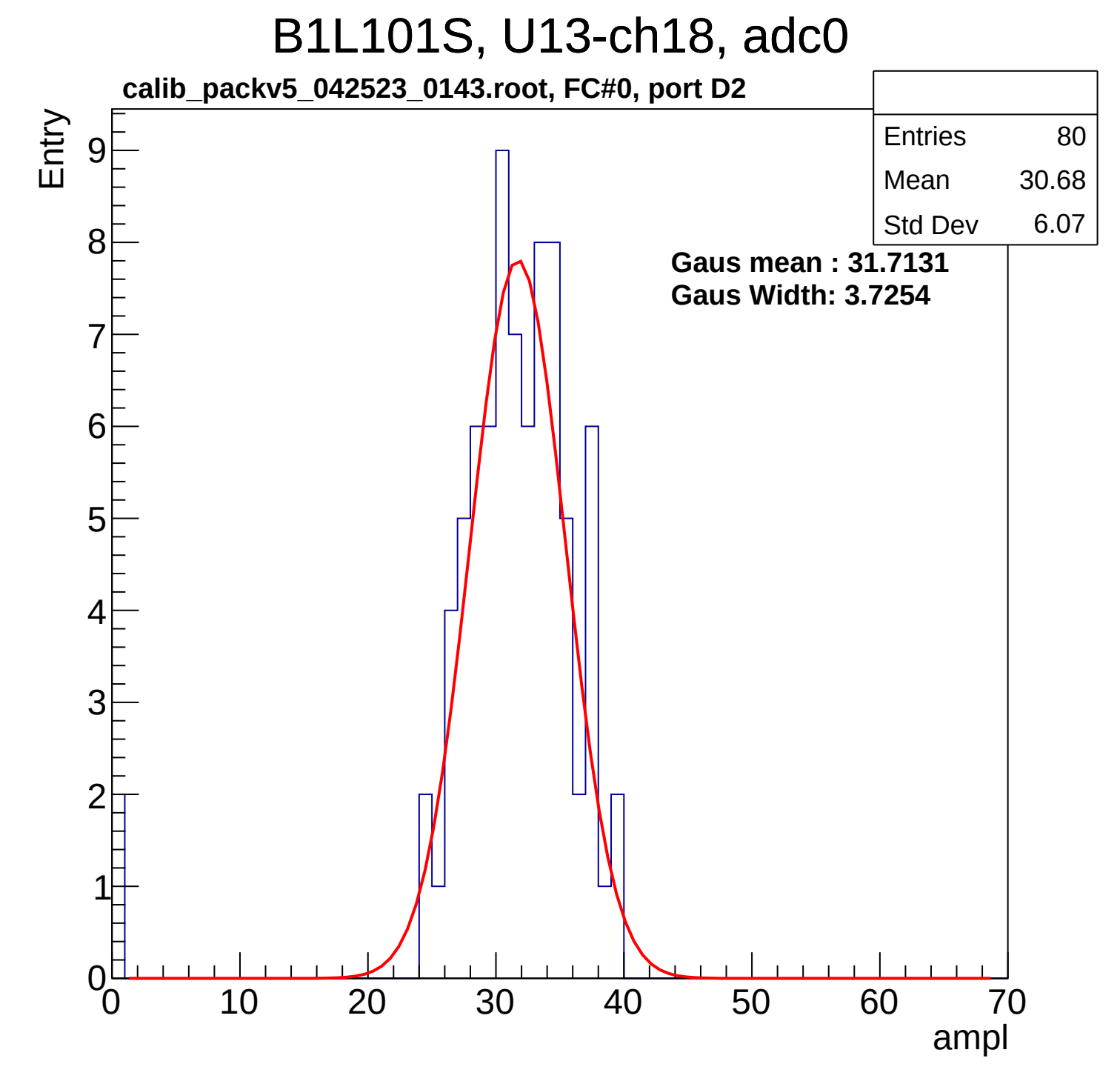
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch18, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 30.68
Std Dev 6.07
Gaus mean : 31.7131
Gaus Width: 3.7254
Entry
ampl
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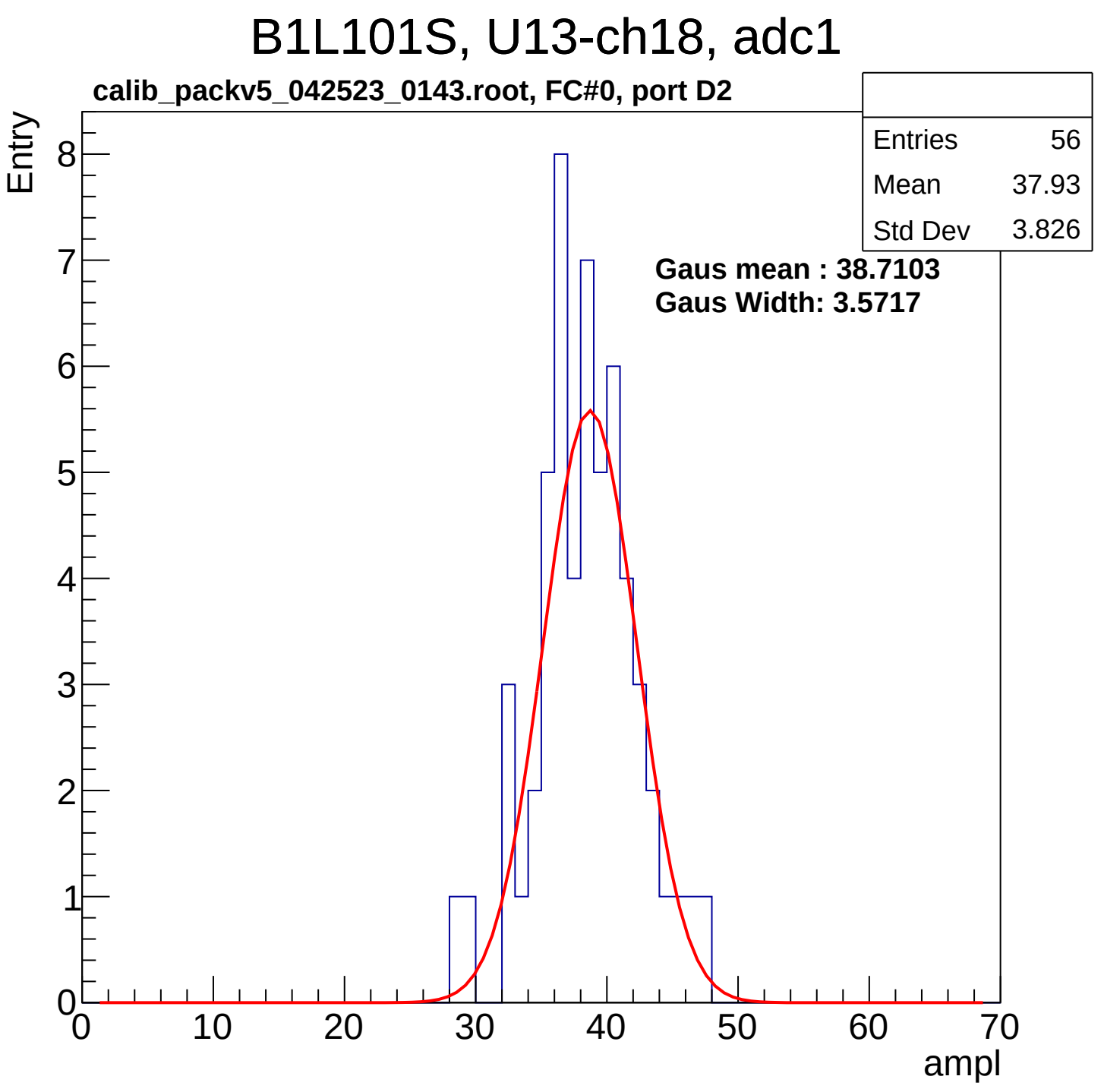
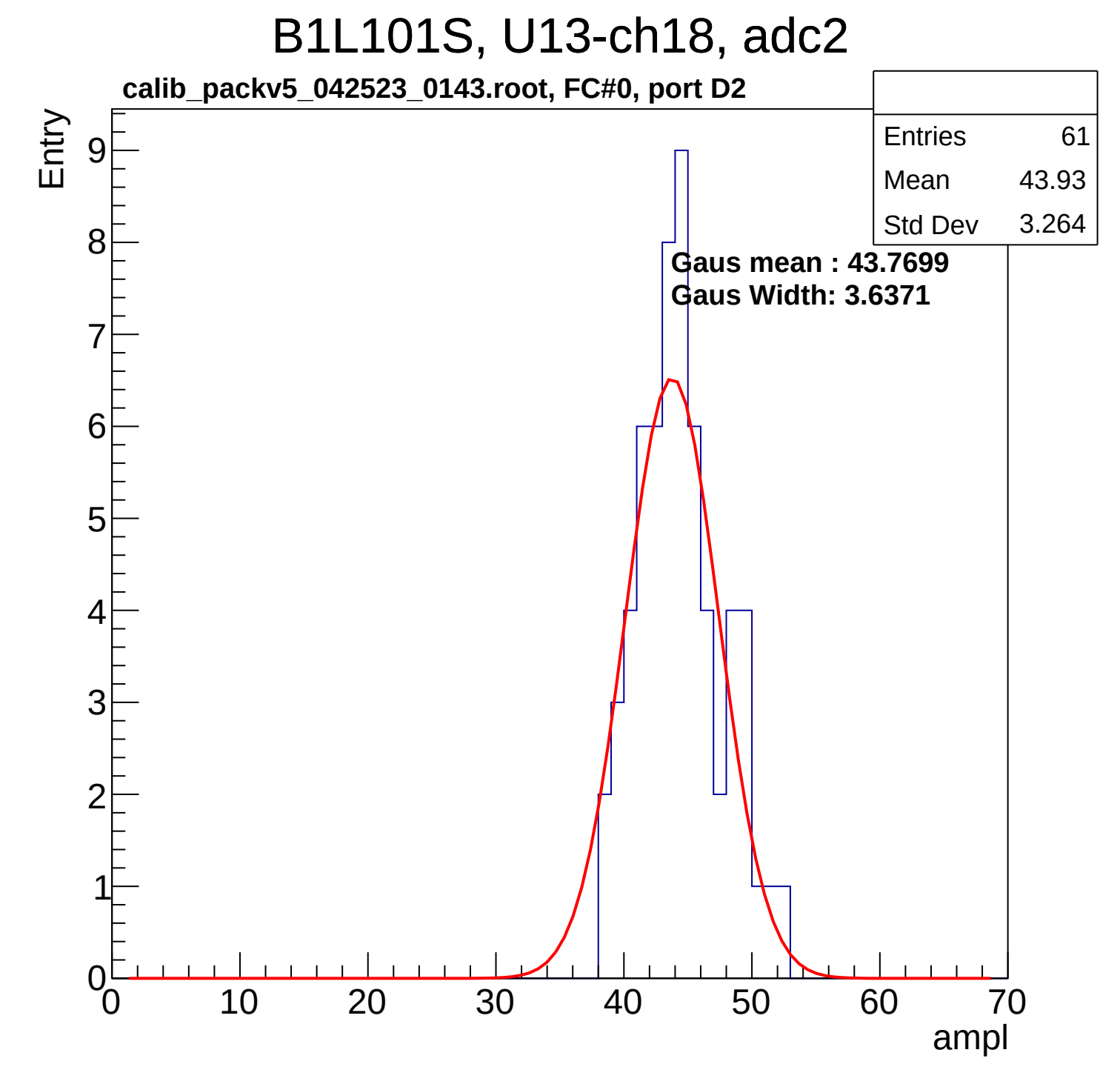
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch18, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 37.93
Std Dev 3.826
Gaus mean : 38.7103
Gaus Width: 3.5717
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch18, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 43.93
Std Dev 3.264
Gaus mean : 43.7699
Gaus Width: 3.6371
Entry
ampl
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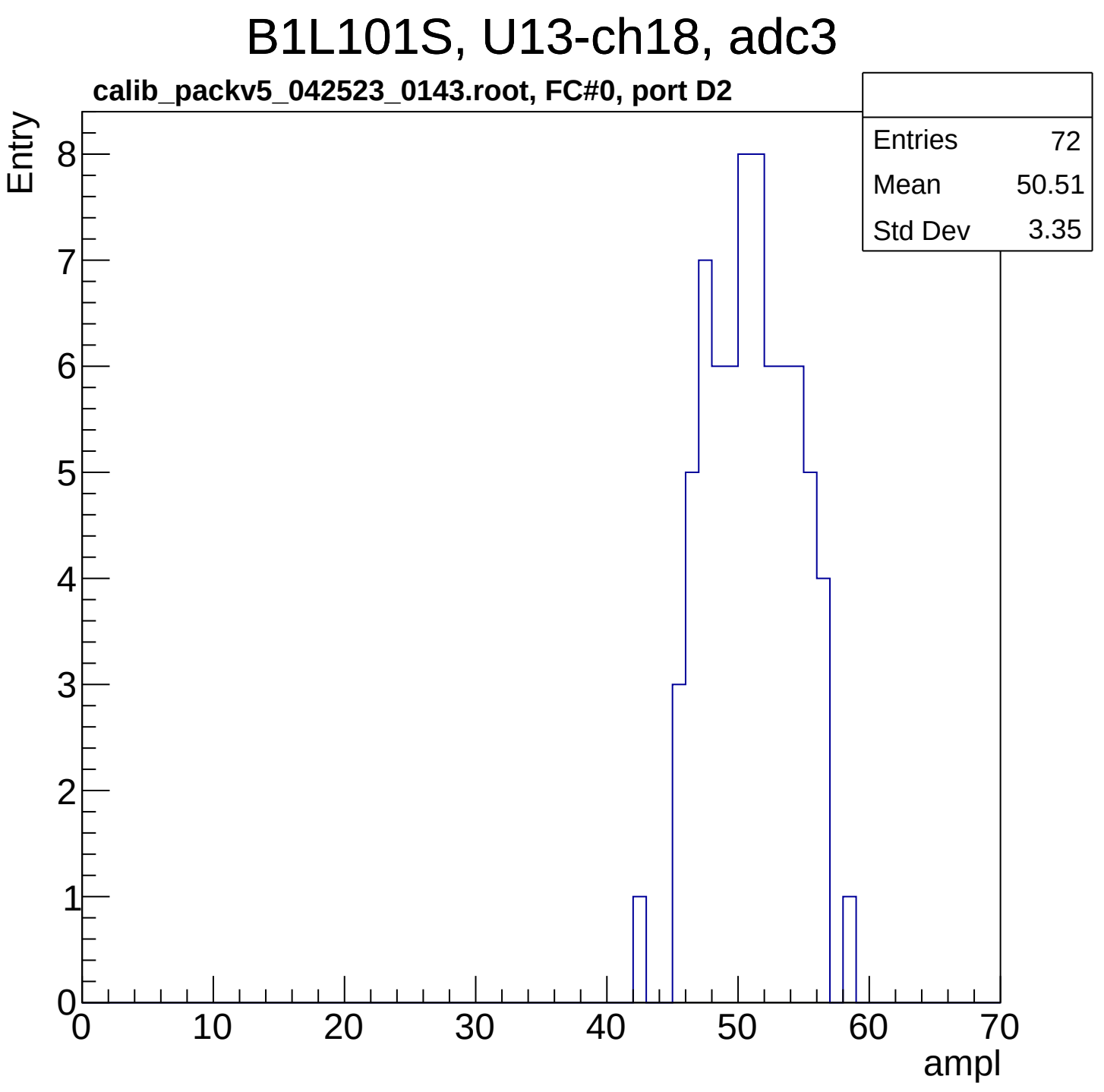
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch18, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 50.51
Std Dev 3.35
Entry
ampl
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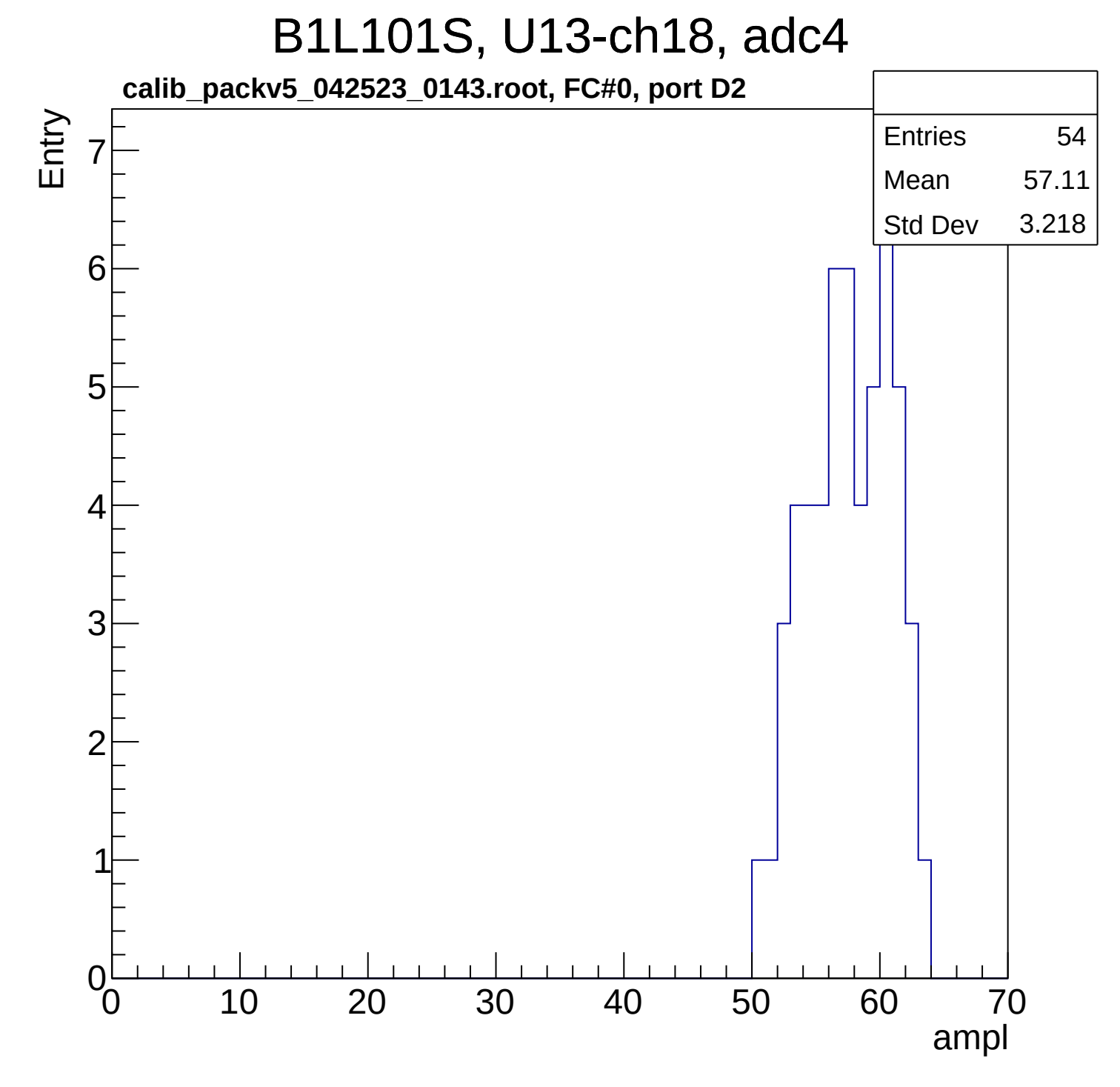
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch18, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 57.11
Std Dev 3.218
Entry
ampl
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1
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3
4
5
6
7
0
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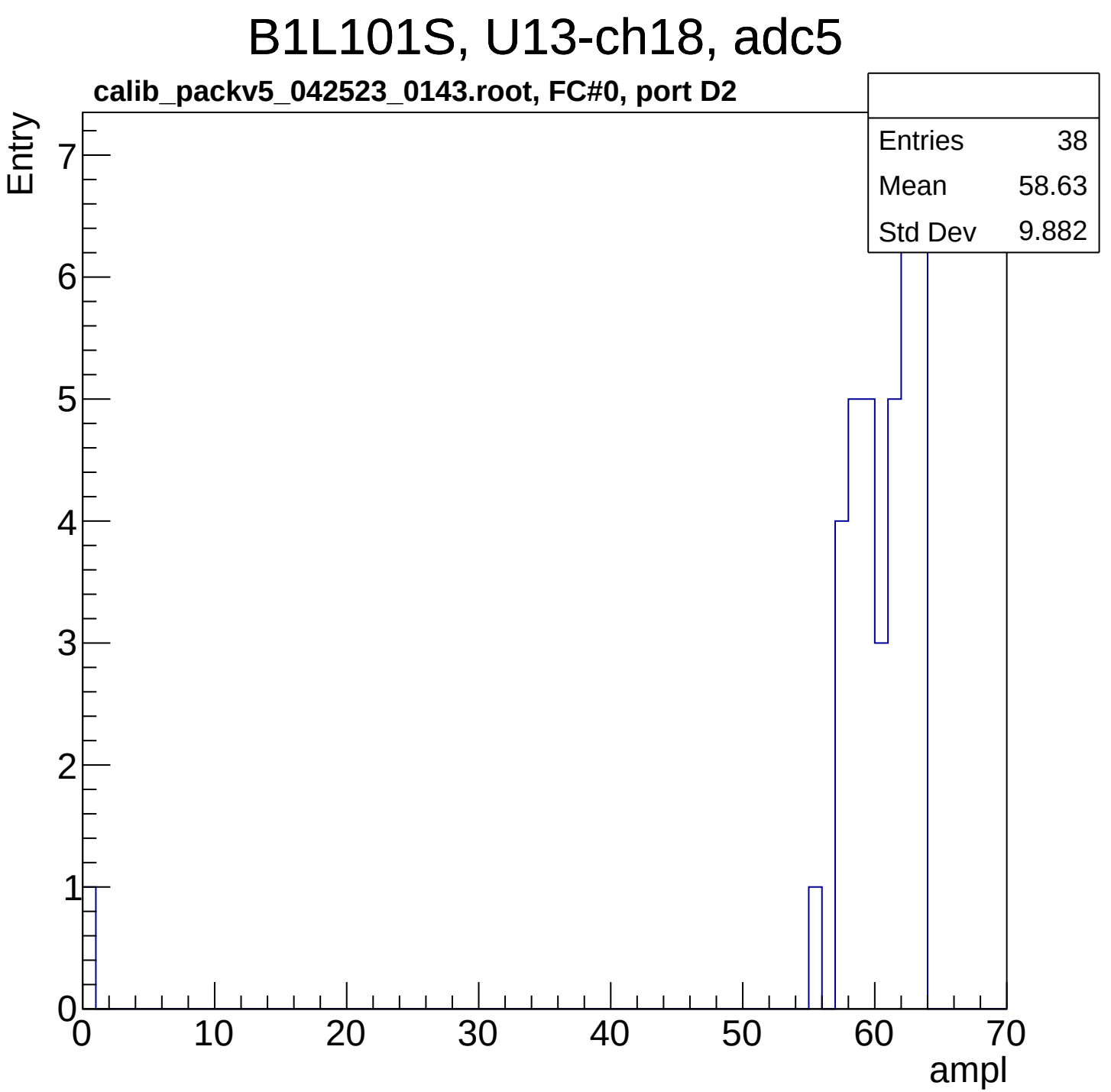
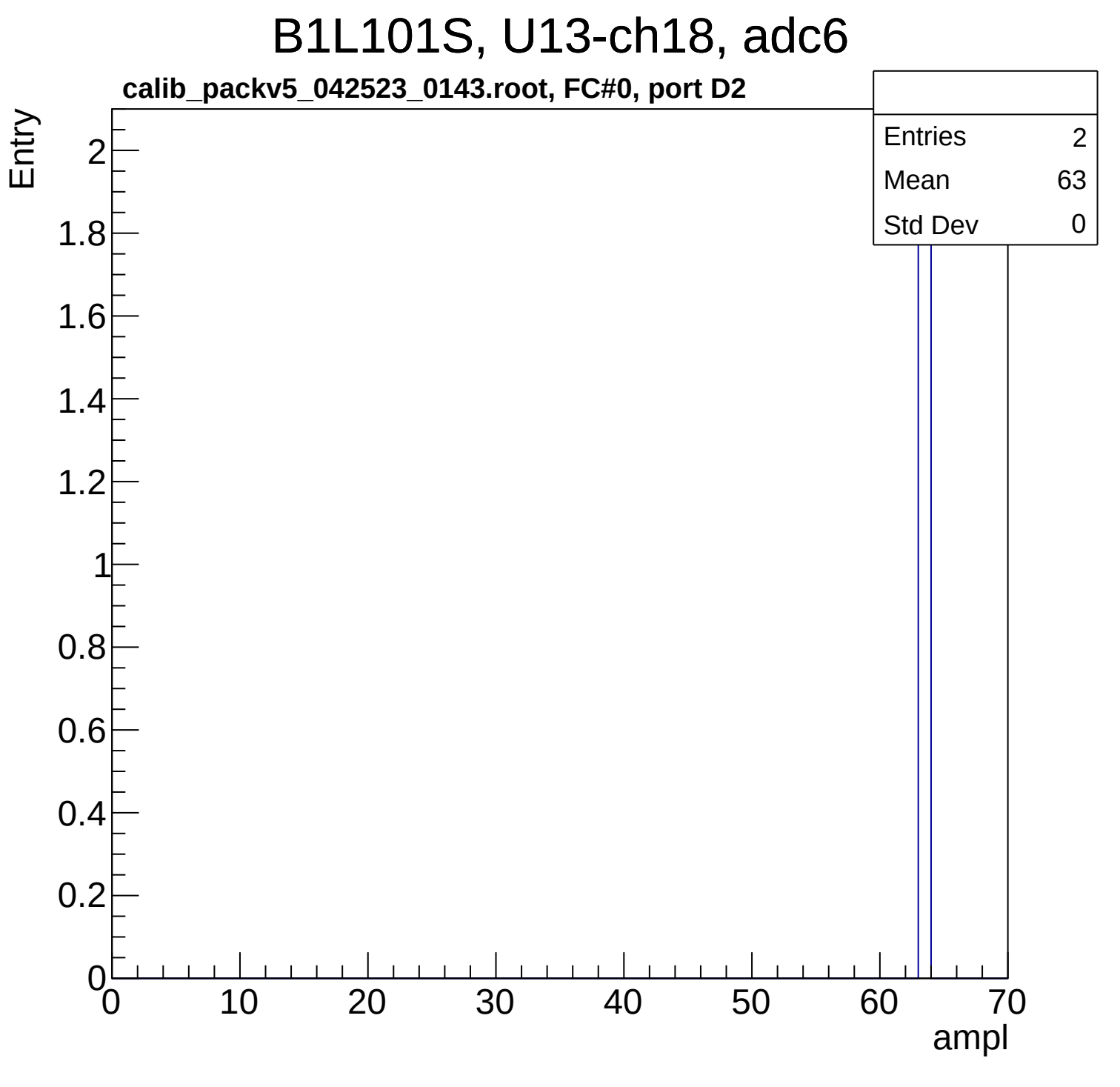
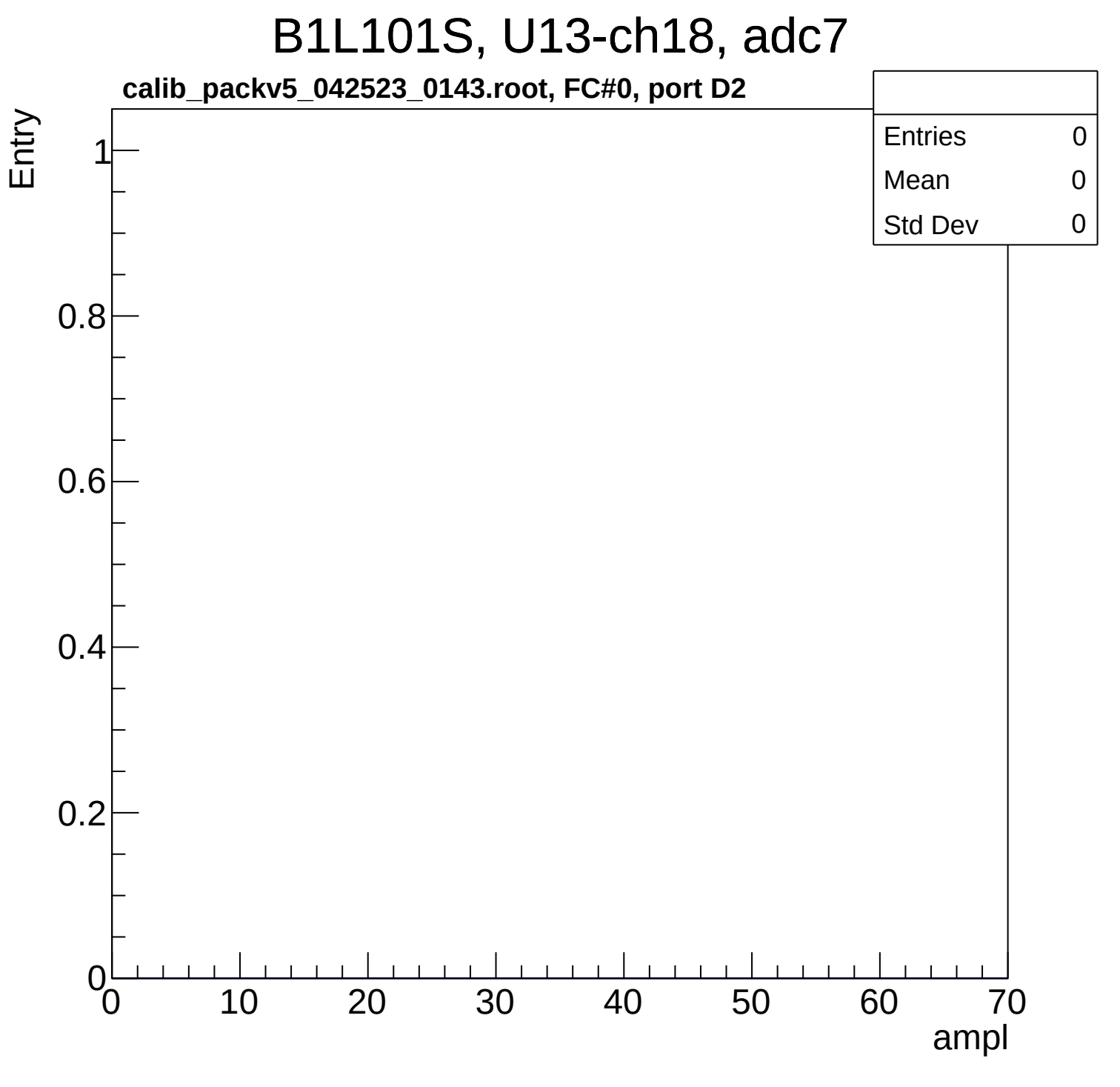
20
30
40
50
60
70


B1L101S, U13-ch18, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 38
Mean 58.63
Std Dev 9.882
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch18, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch18, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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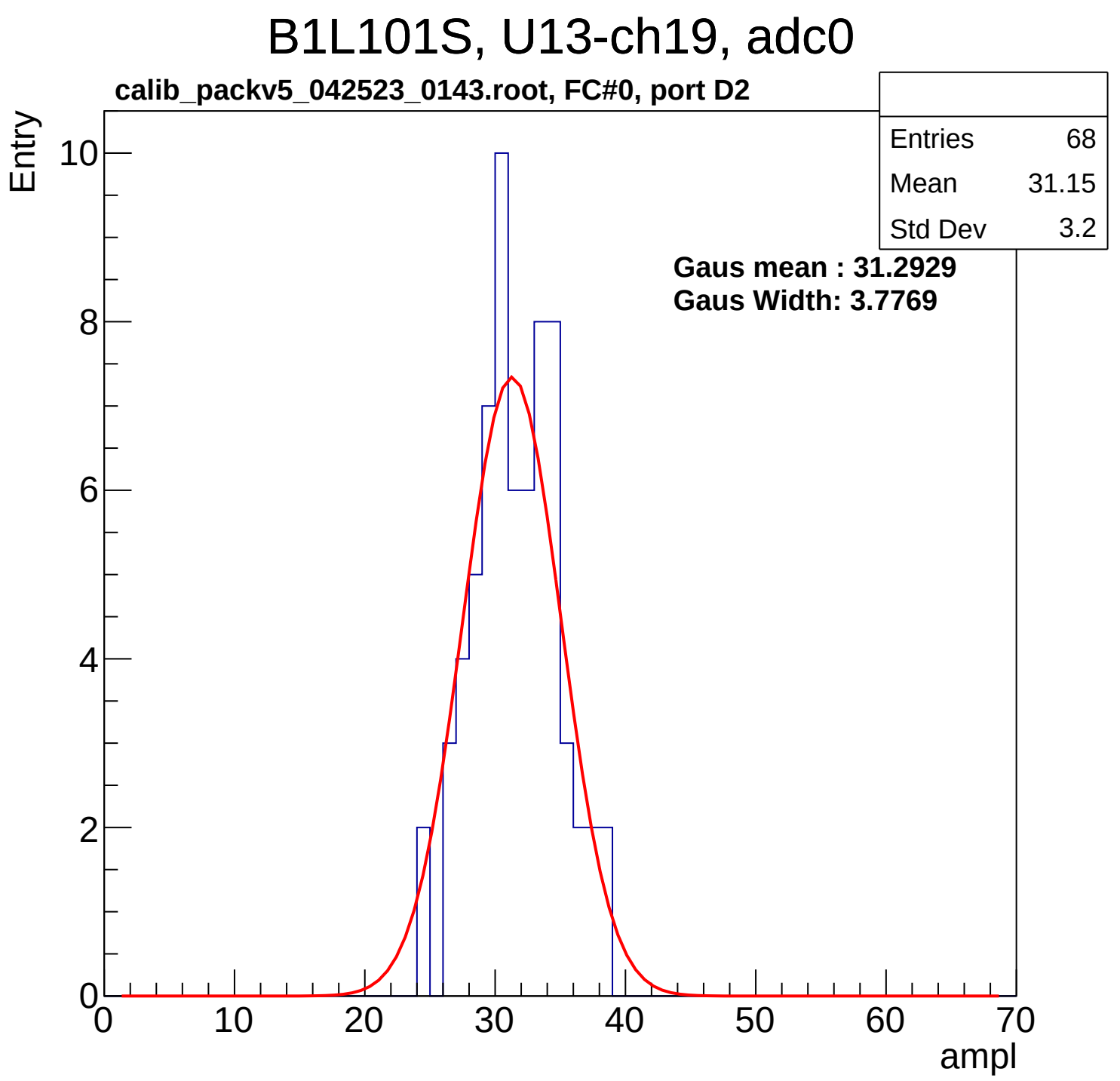
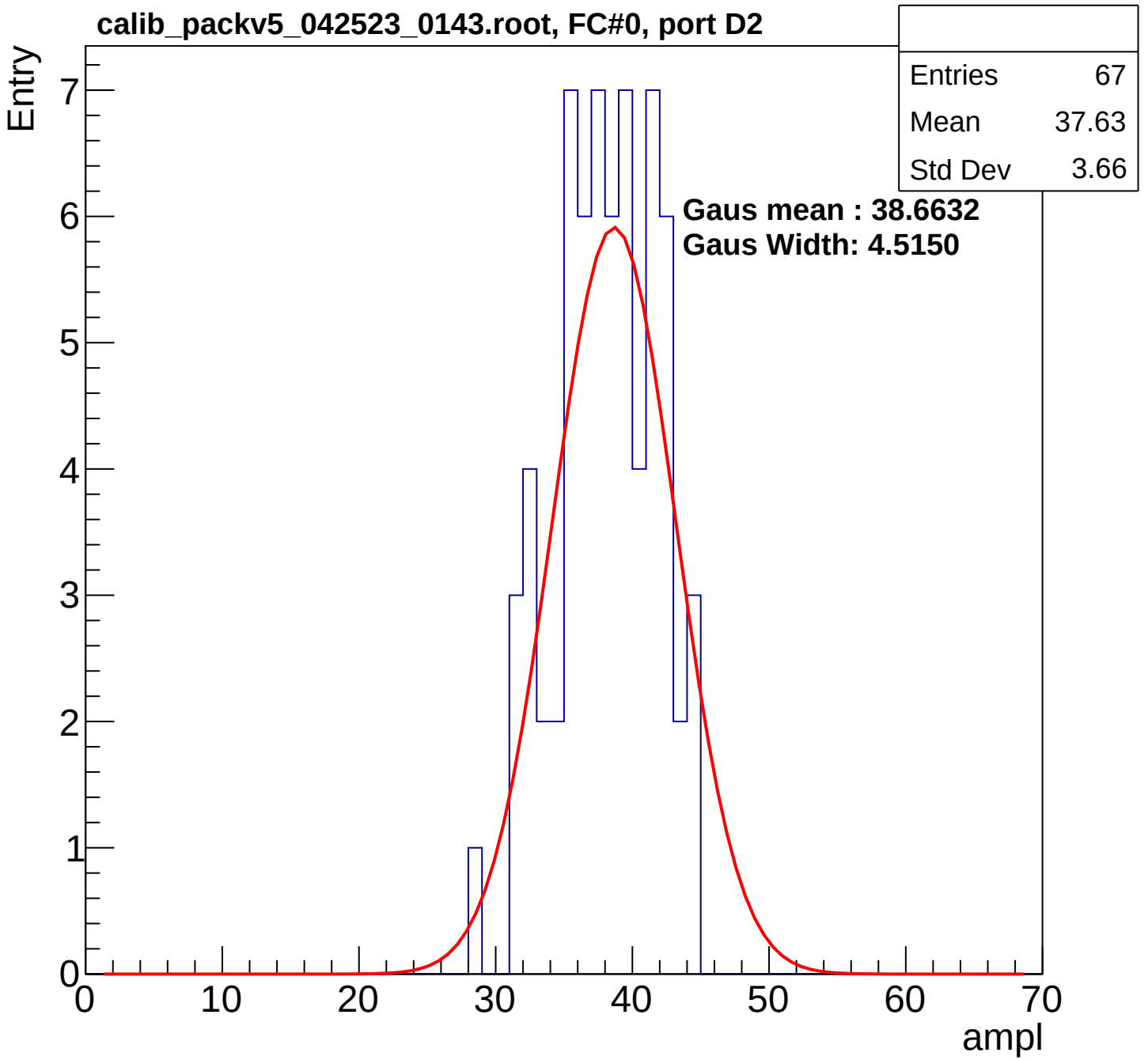
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch19, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 31.15
Std Dev 3.2
Gaus mean : 31.2929
Gaus Width: 3.7769
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch19, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 37.63
Std Dev 3.66
Gaus mean : 38.6632
Gaus Width: 4.5150
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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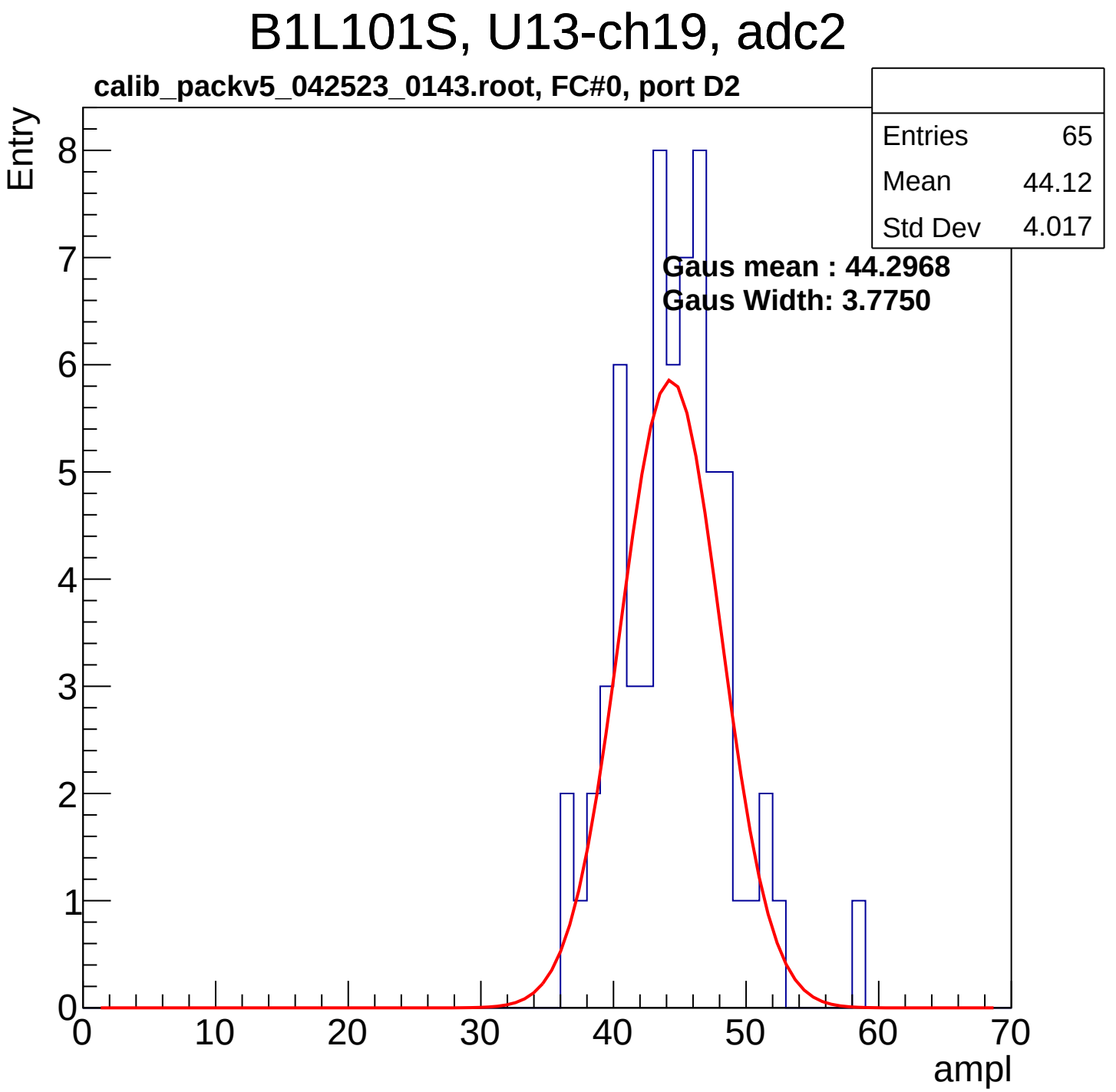
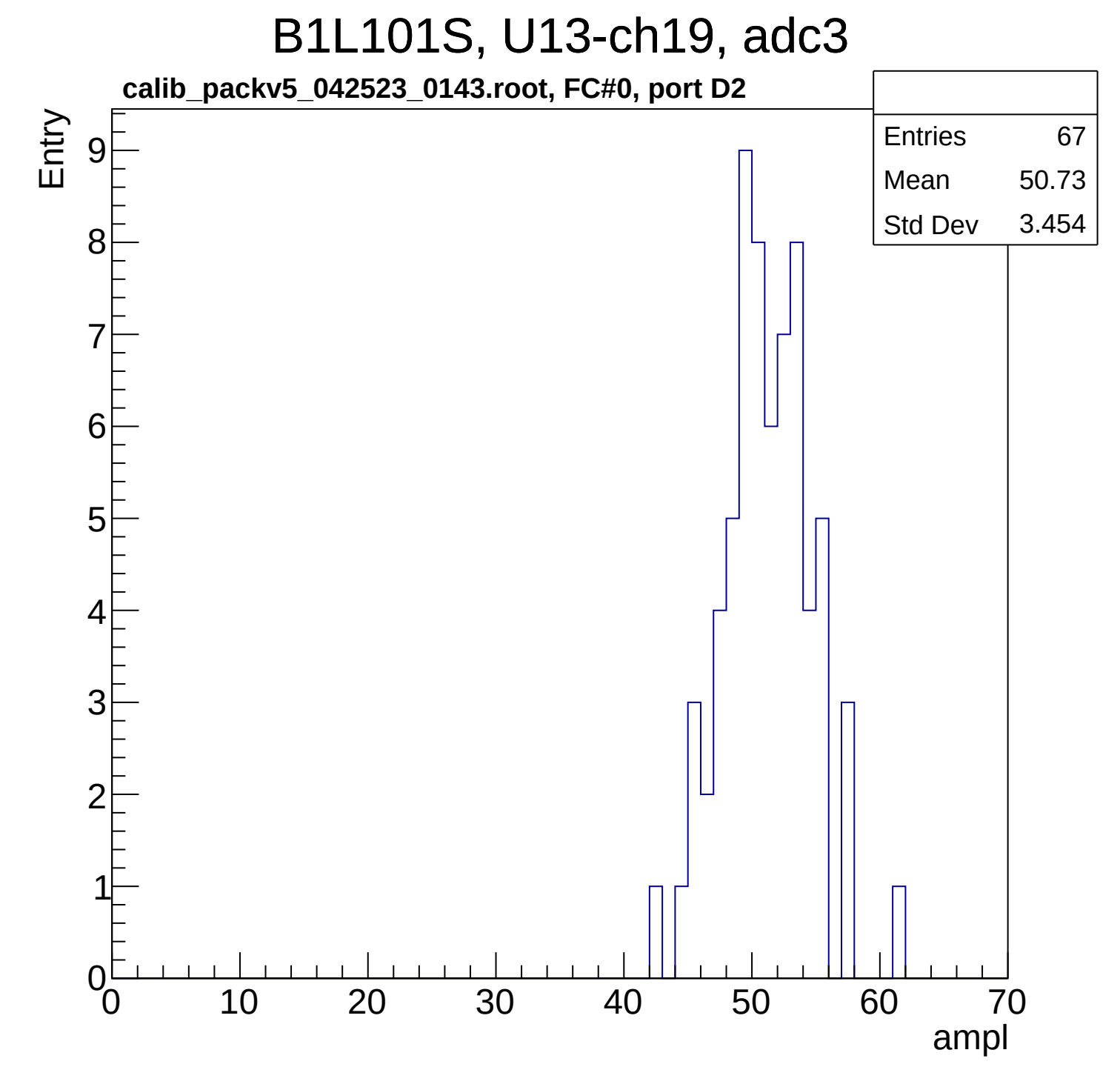
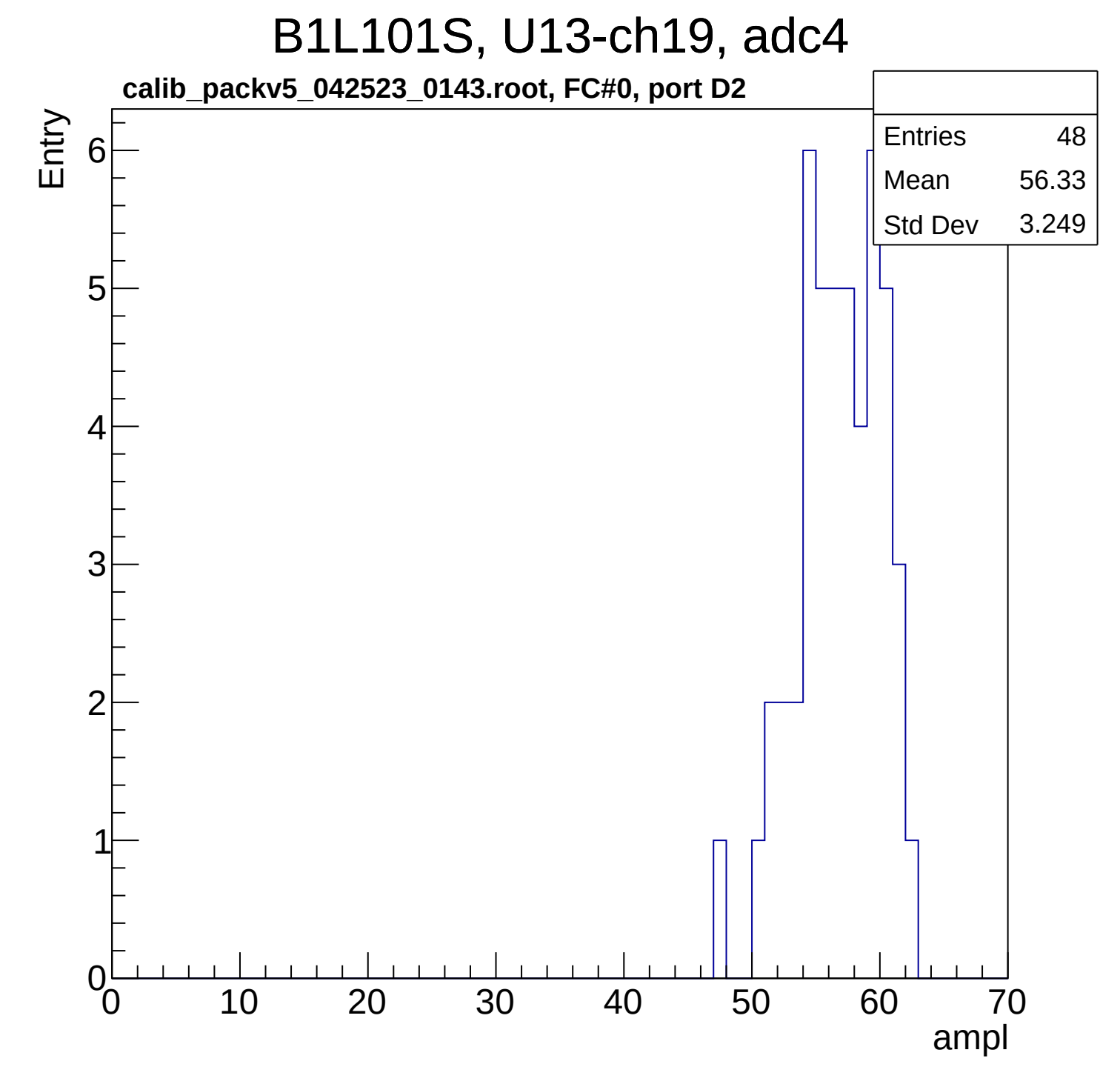
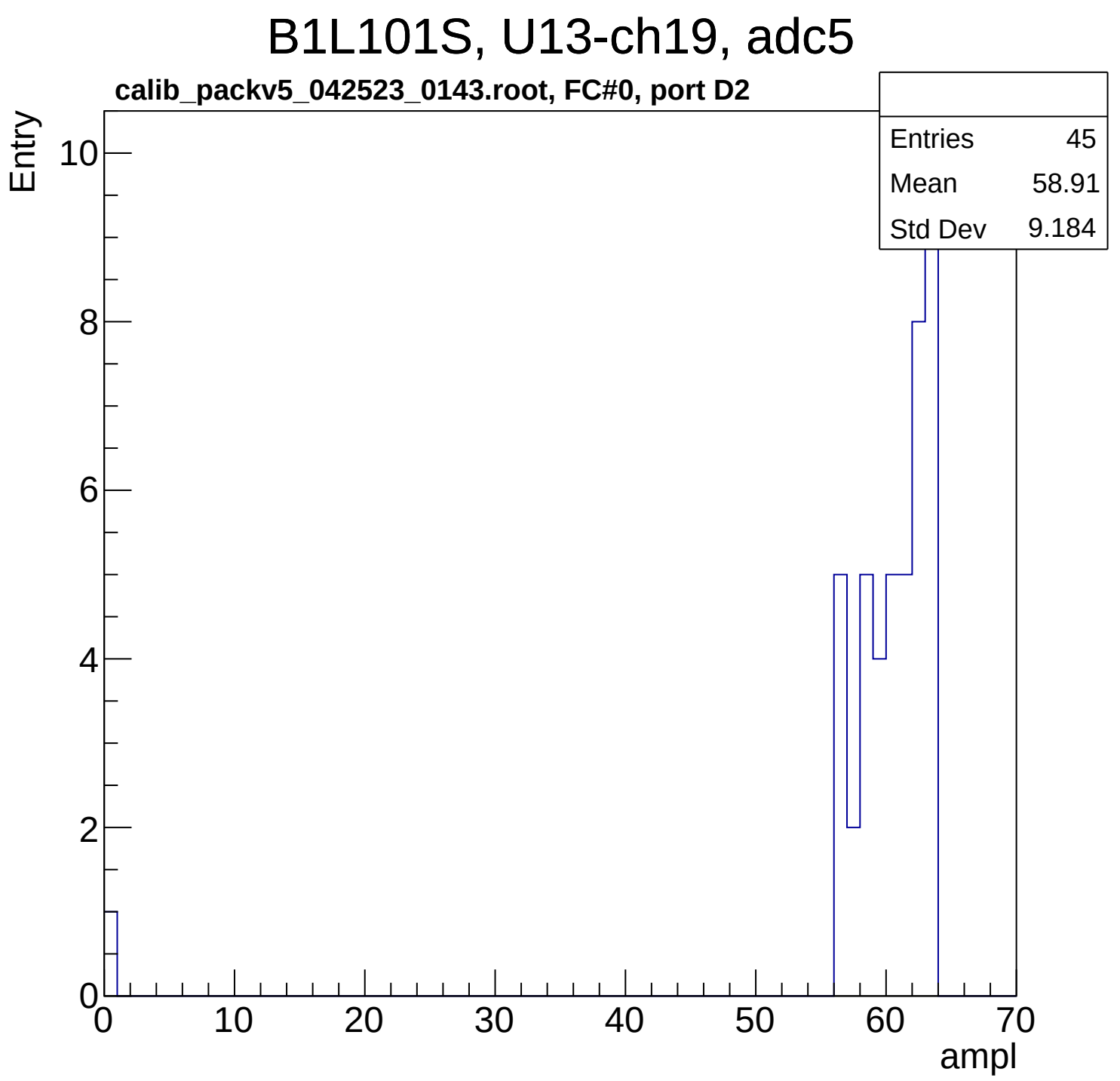
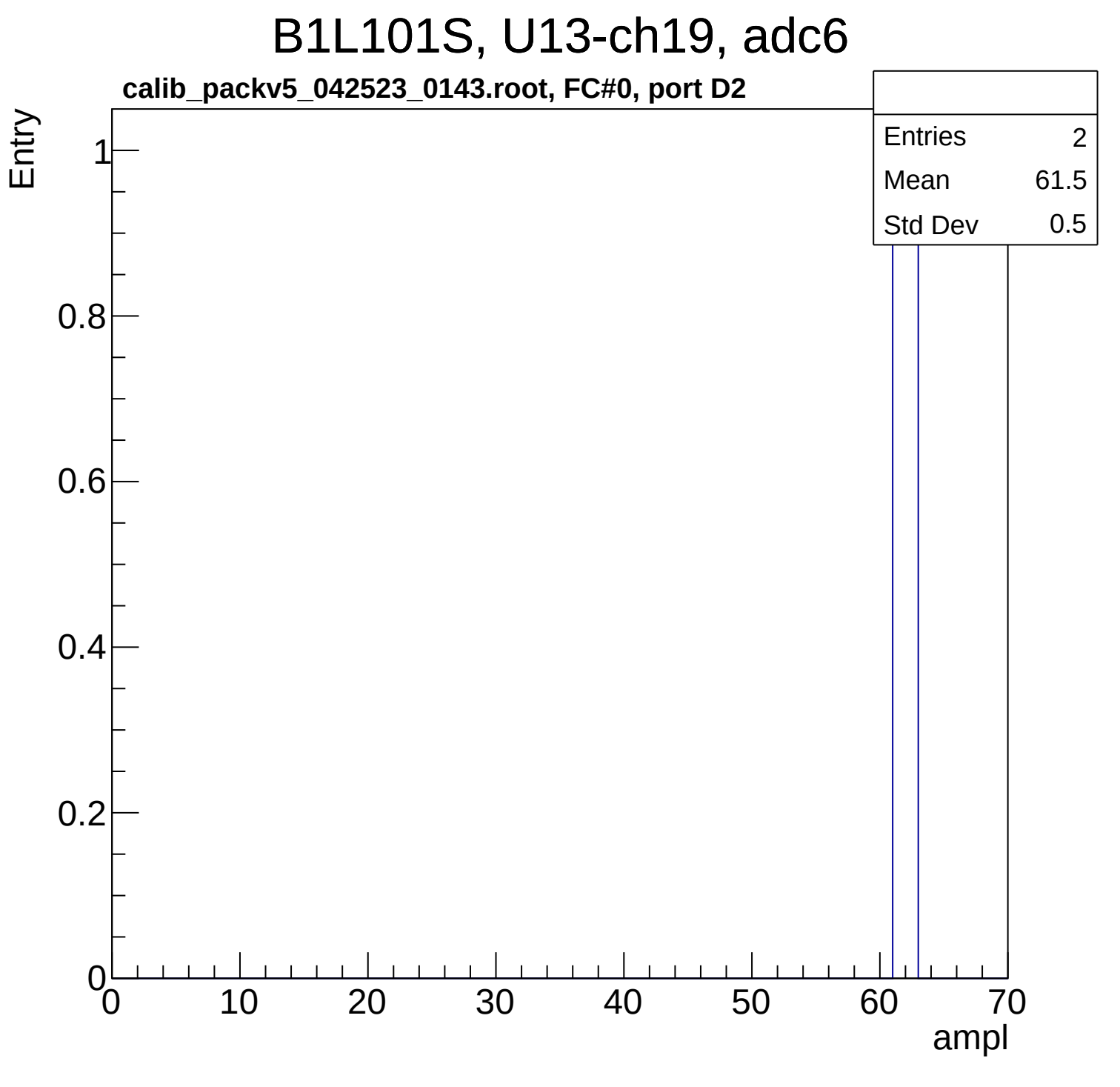
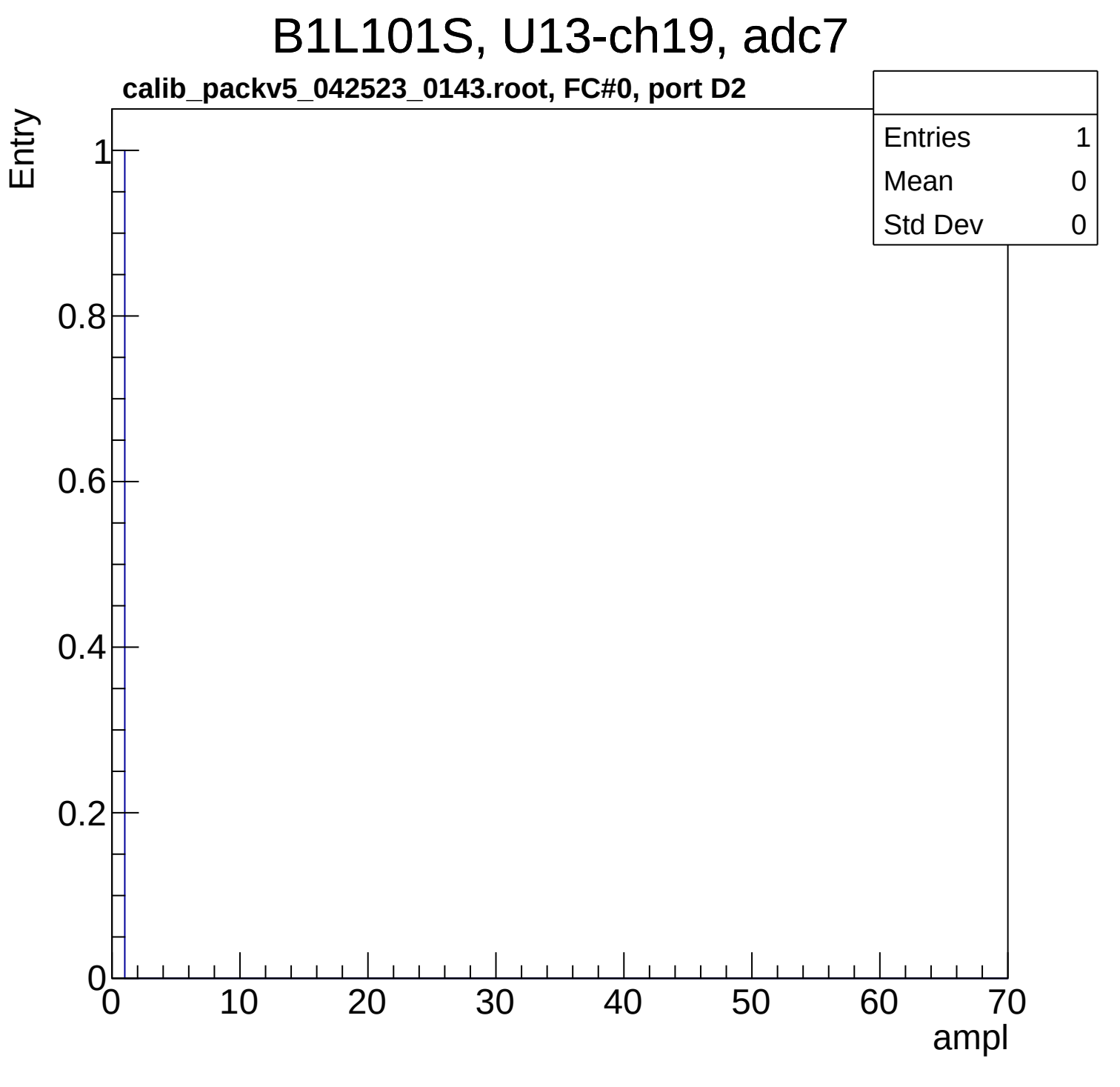

B1L101S, U13-ch19, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 44.12
Std Dev 4.017
Gaus mean : 44.2968
Gaus Width: 3.7750
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

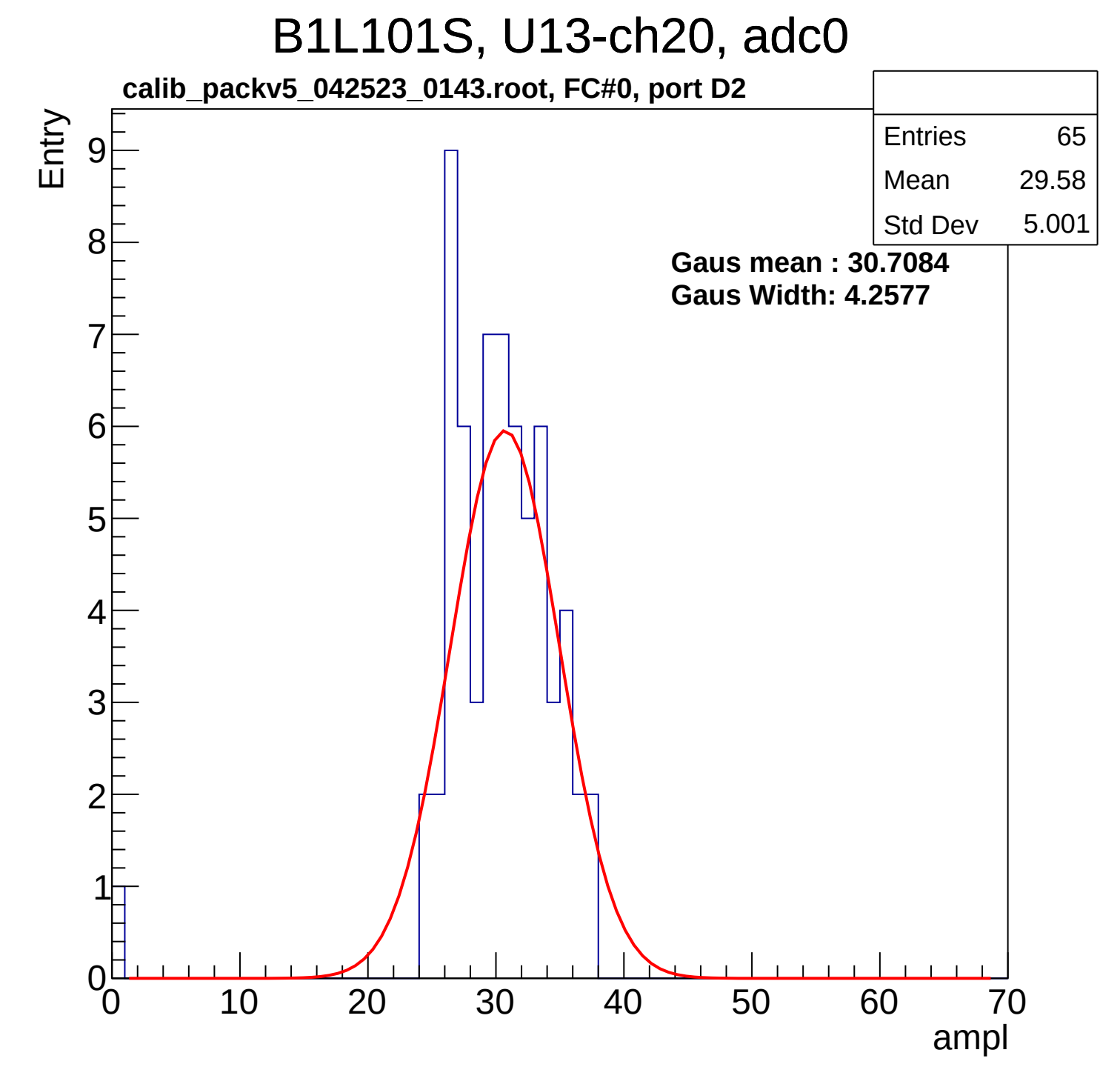

B1L101S, U13-ch19, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 50.73
Std Dev 3.454
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

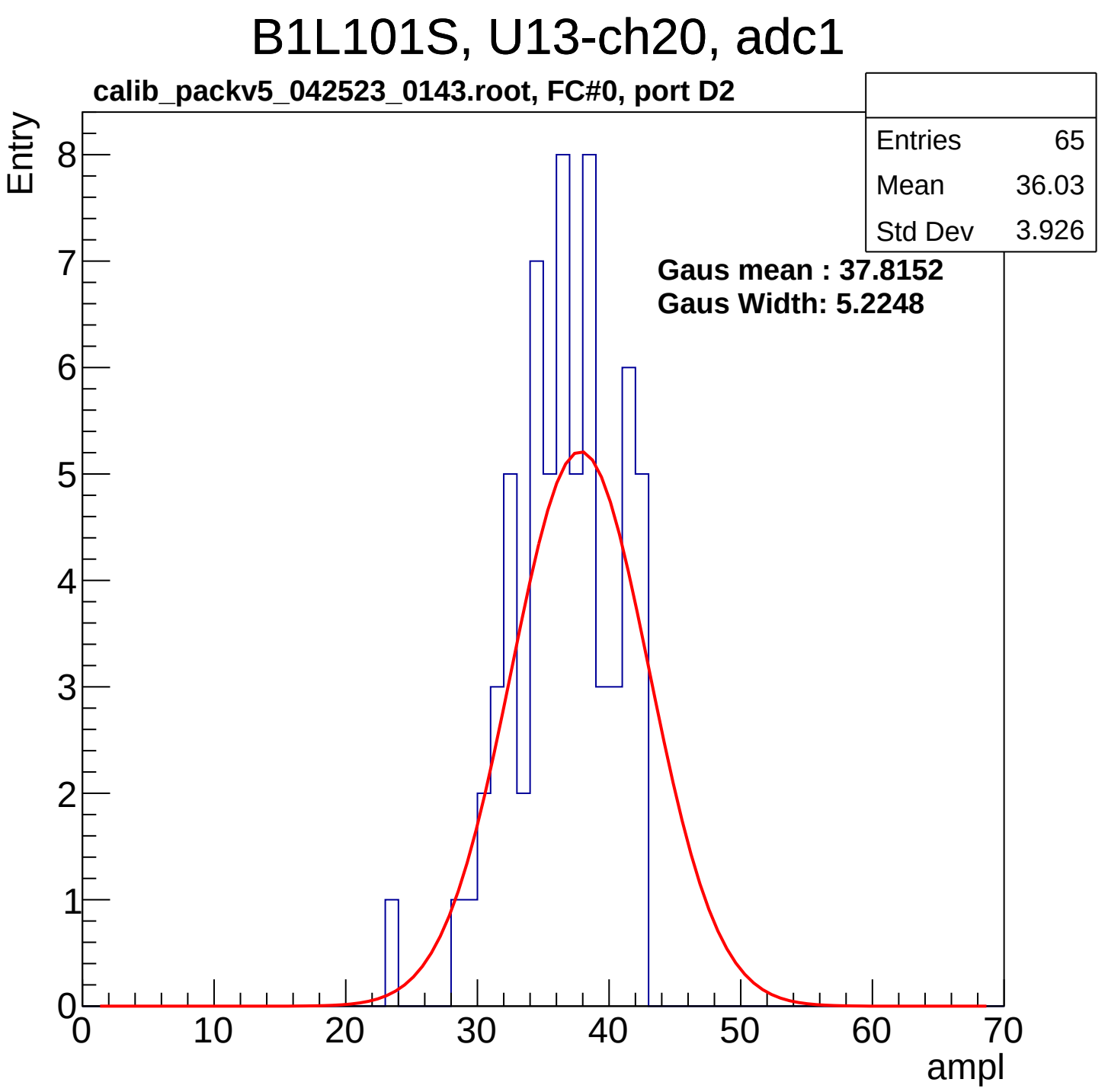

B1L101S, U13-ch19, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 56.33
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

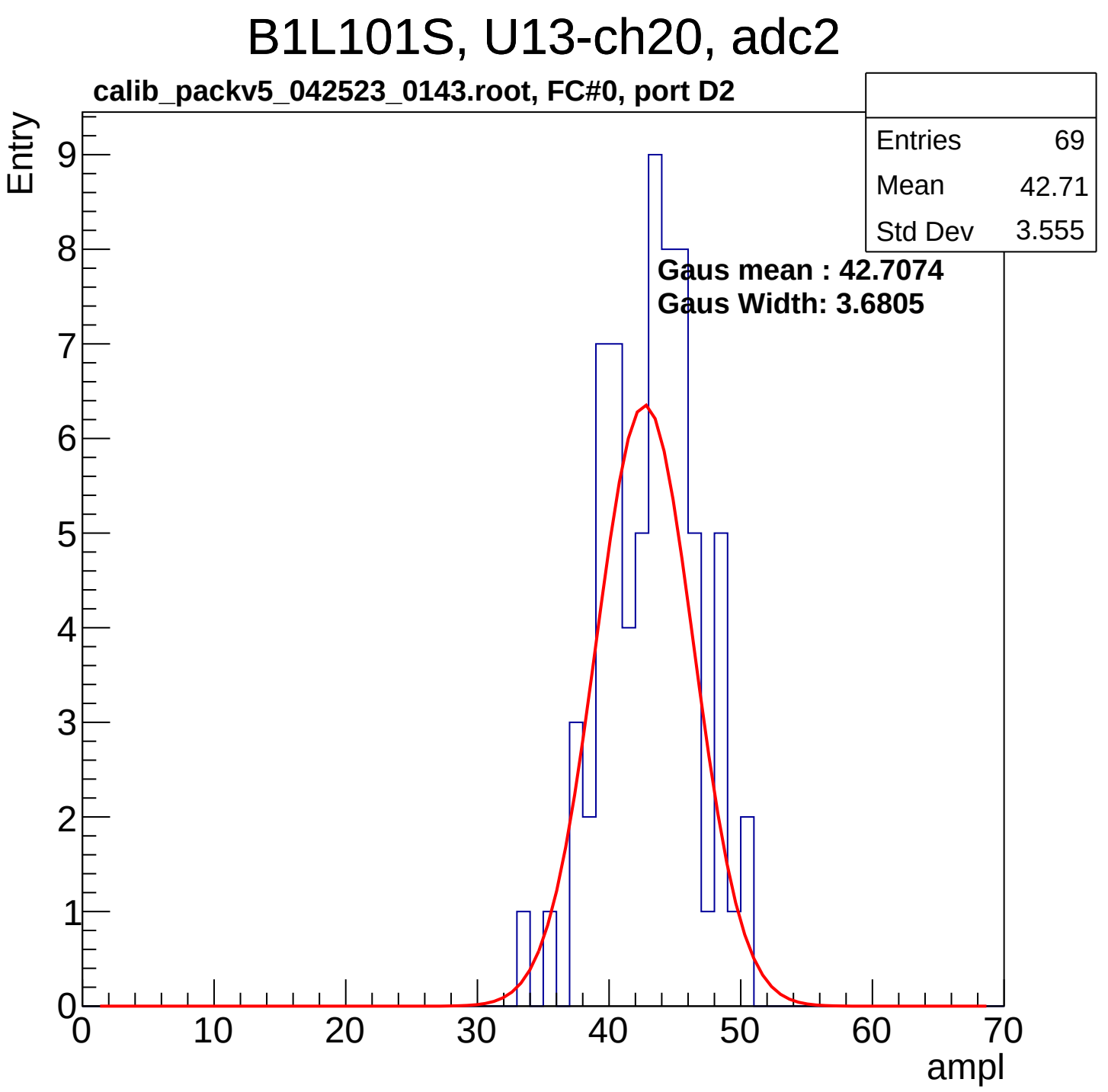

B1L101S, U13-ch19, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 58.91
Std Dev 9.184
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

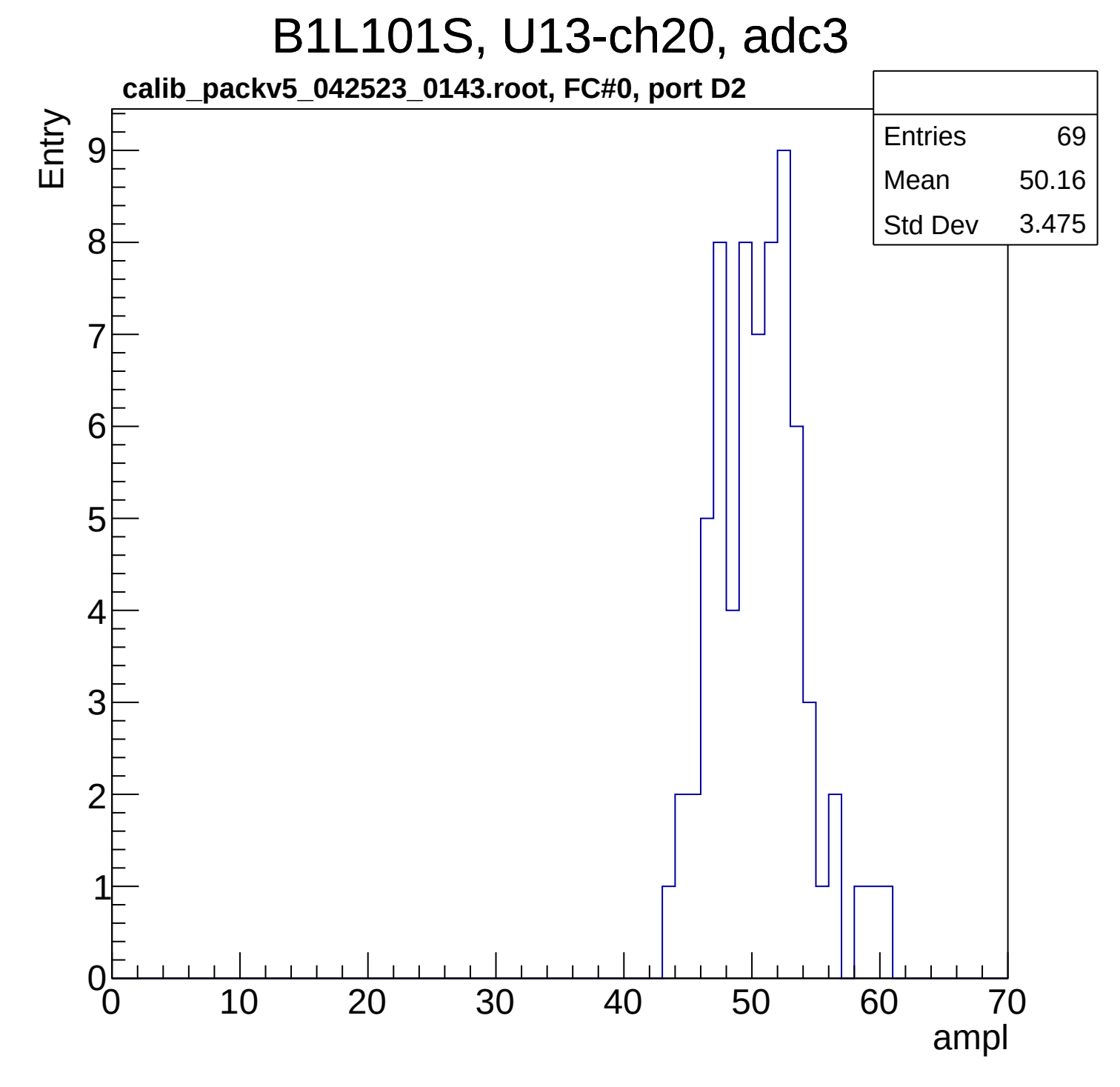

B1L101S, U13-ch19, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

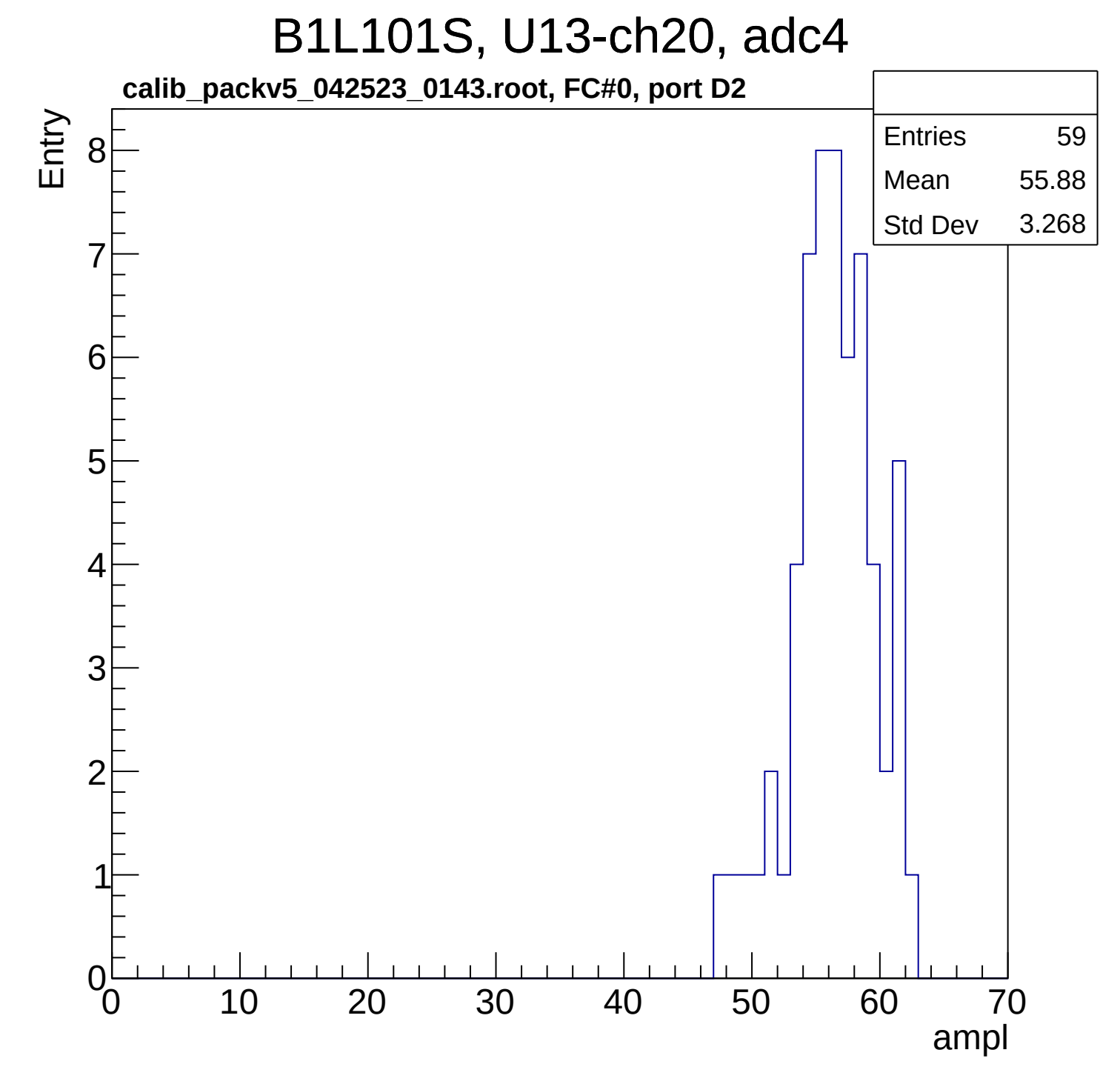

B1L101S, U13-ch19, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

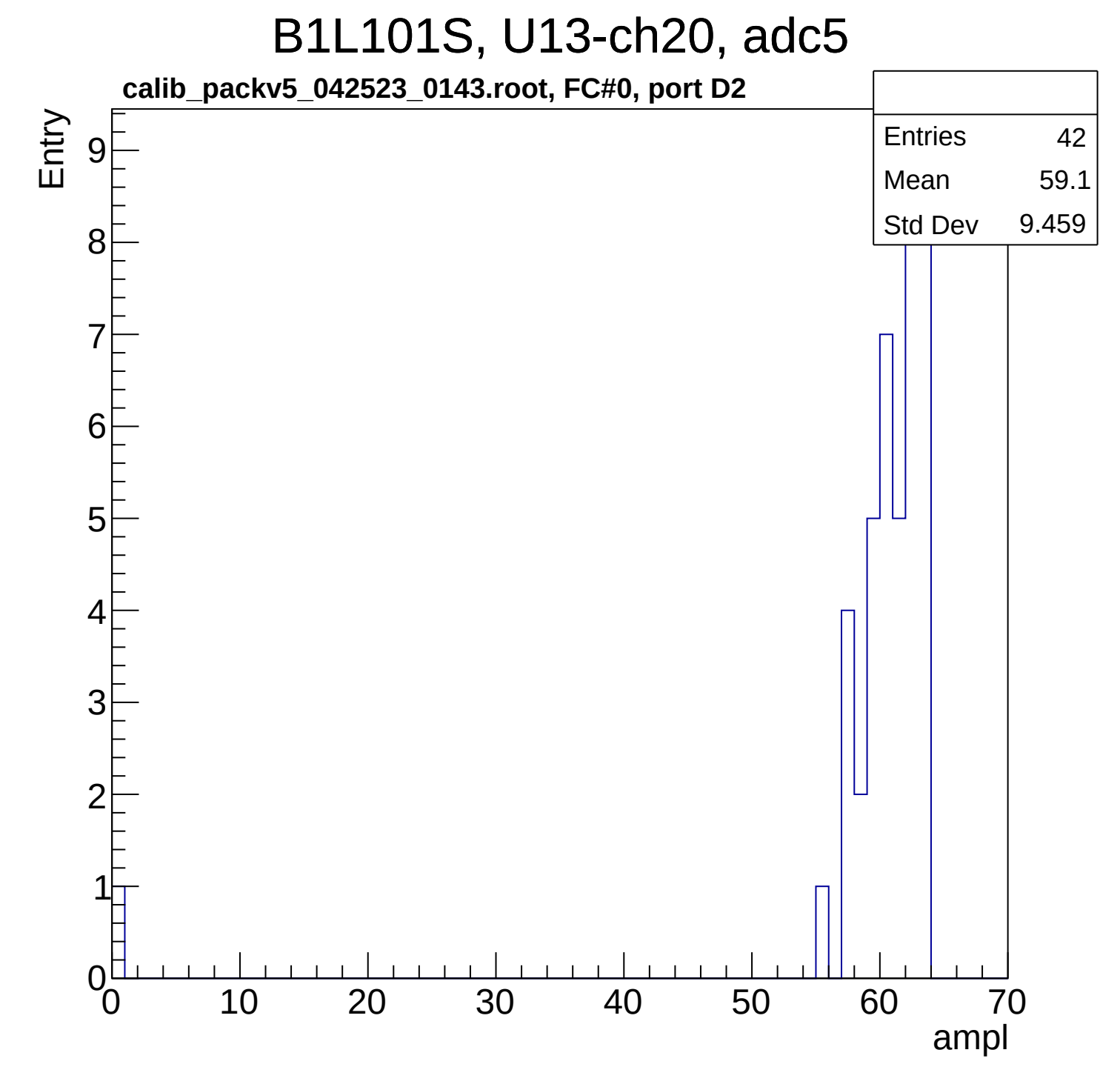

B1L101S, U13-ch20, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 29.58
Std Dev 5.001
Gaus mean : 30.7084
Gaus Width: 4.2577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

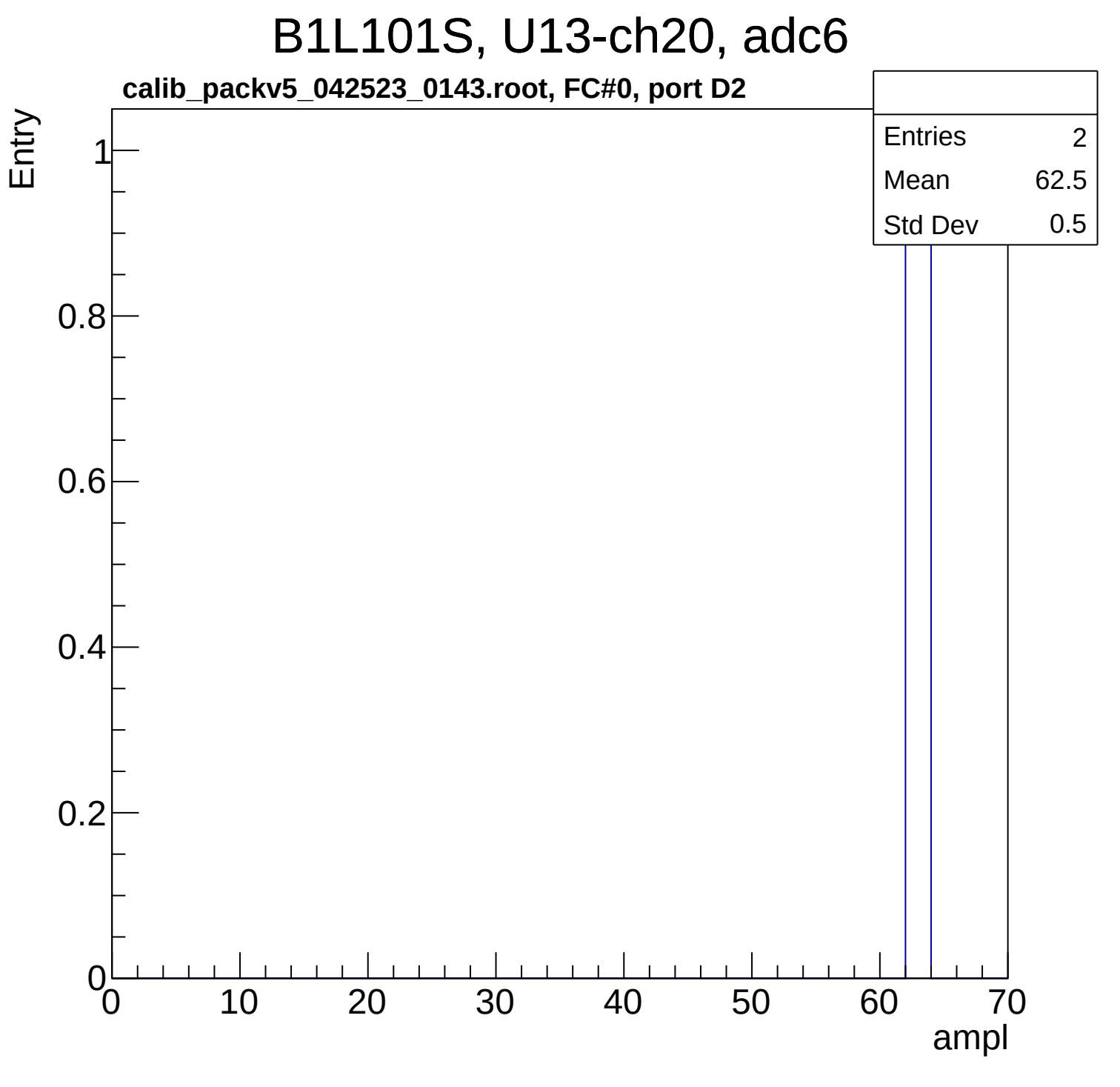

B1L101S, U13-ch20, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 36.03
Std Dev 3.926
Gaus mean : 37.8152
Gaus Width: 5.2248
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

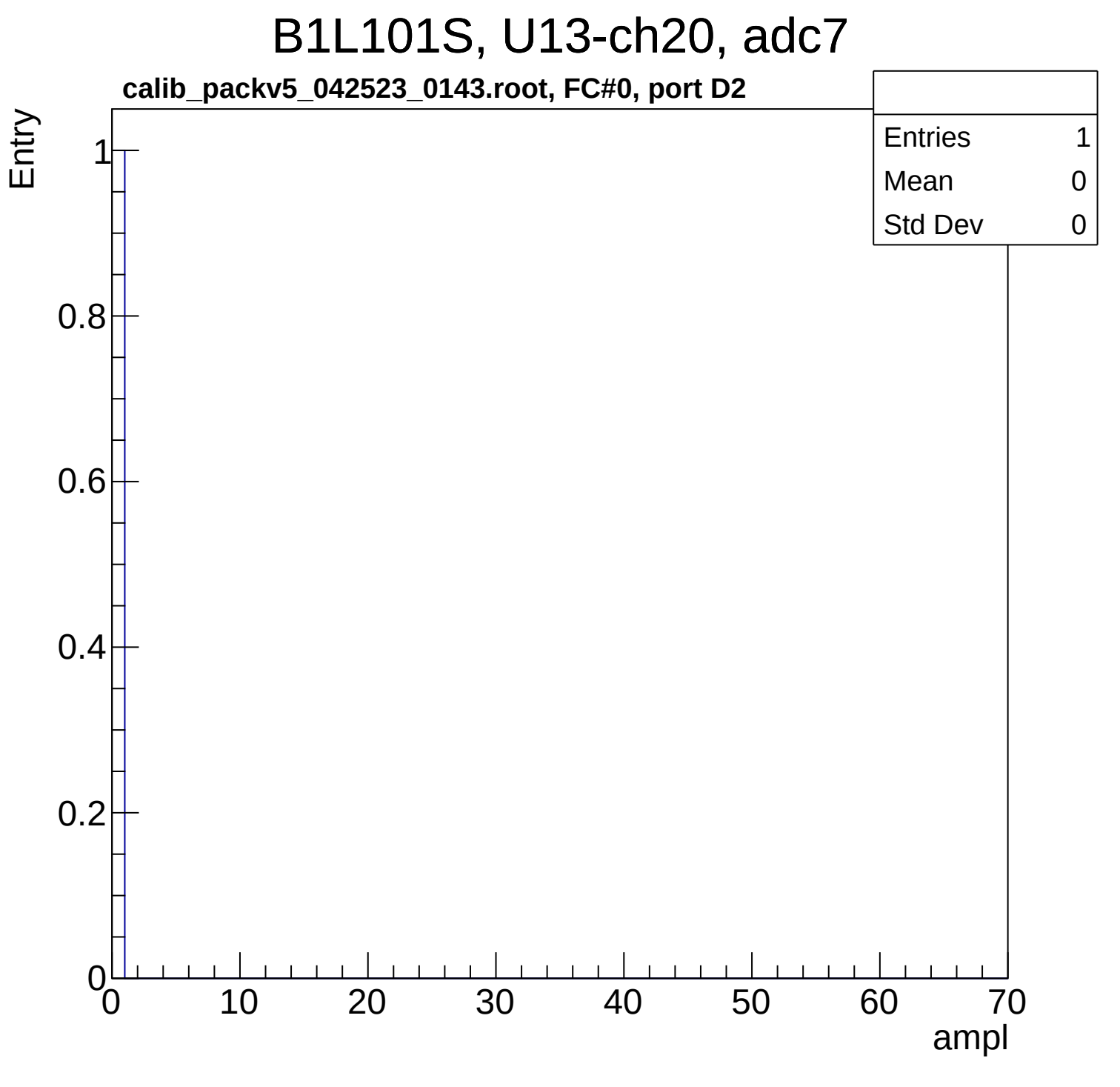

B1L101S, U13-ch20, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 42.71
Std Dev 3.555
Gaus mean : 42.7074
Gaus Width: 3.6805
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

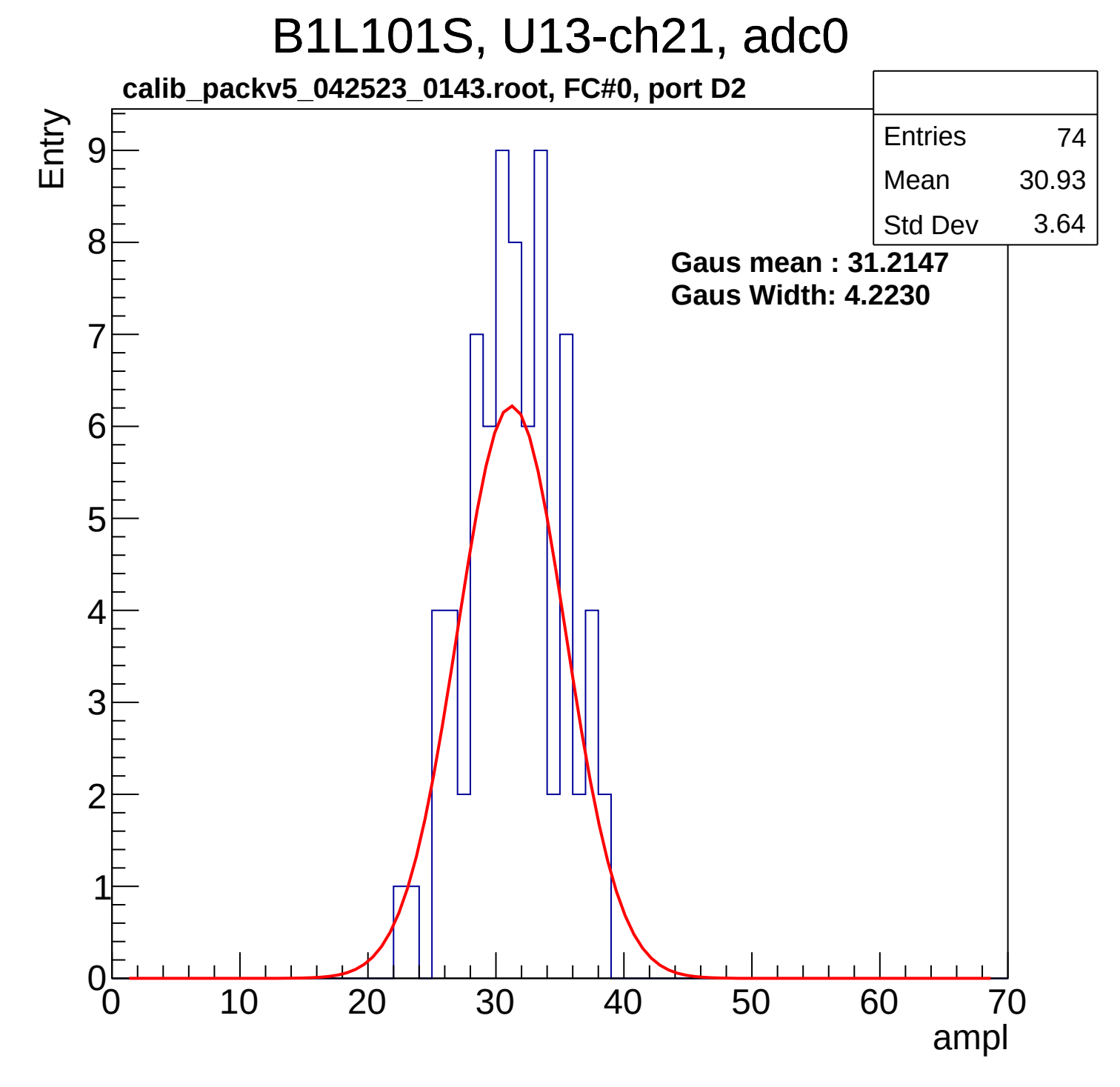

B1L101S, U13-ch20, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 50.16
Std Dev 3.475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

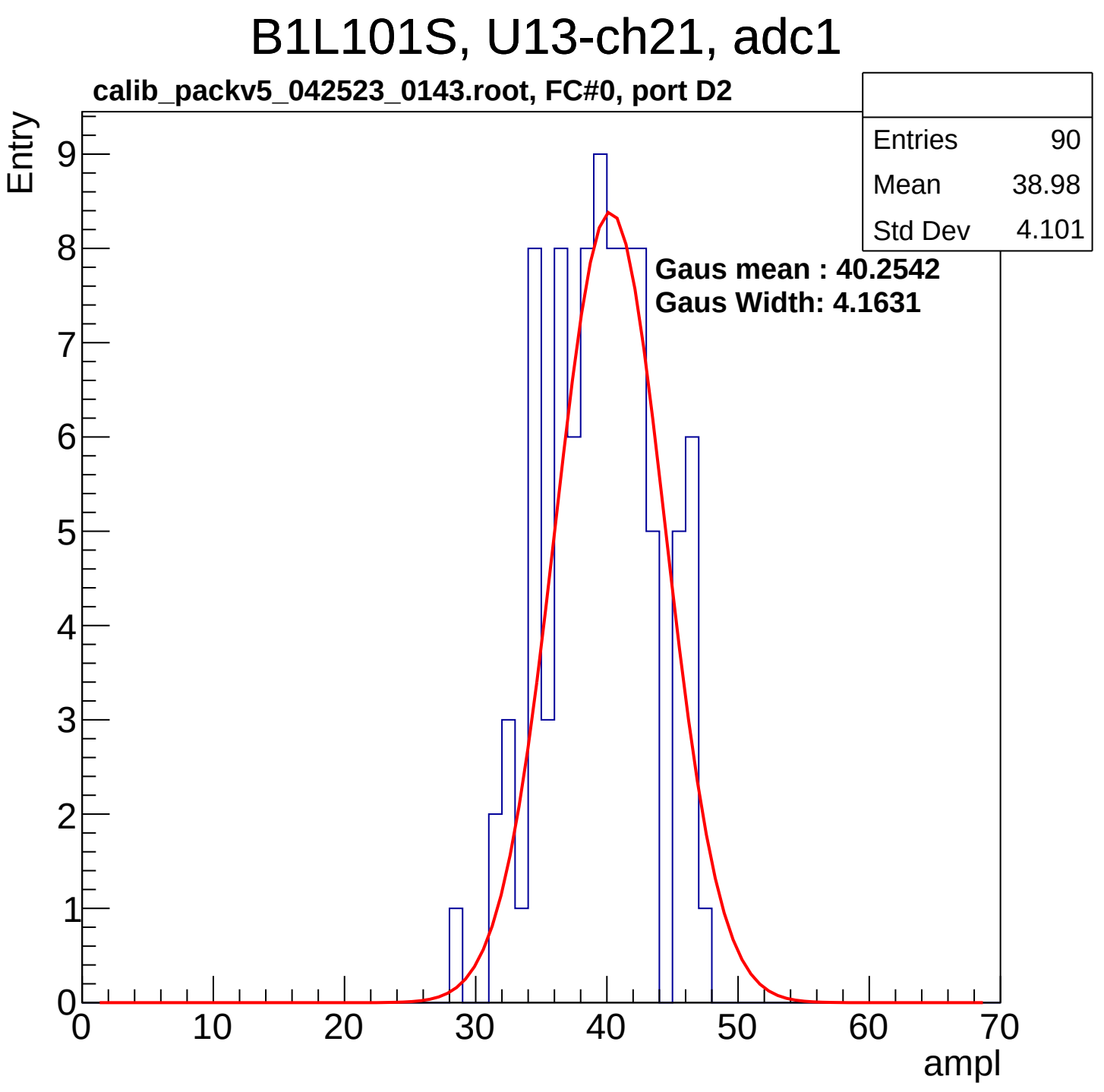

B1L101S, U13-ch20, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 55.88
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

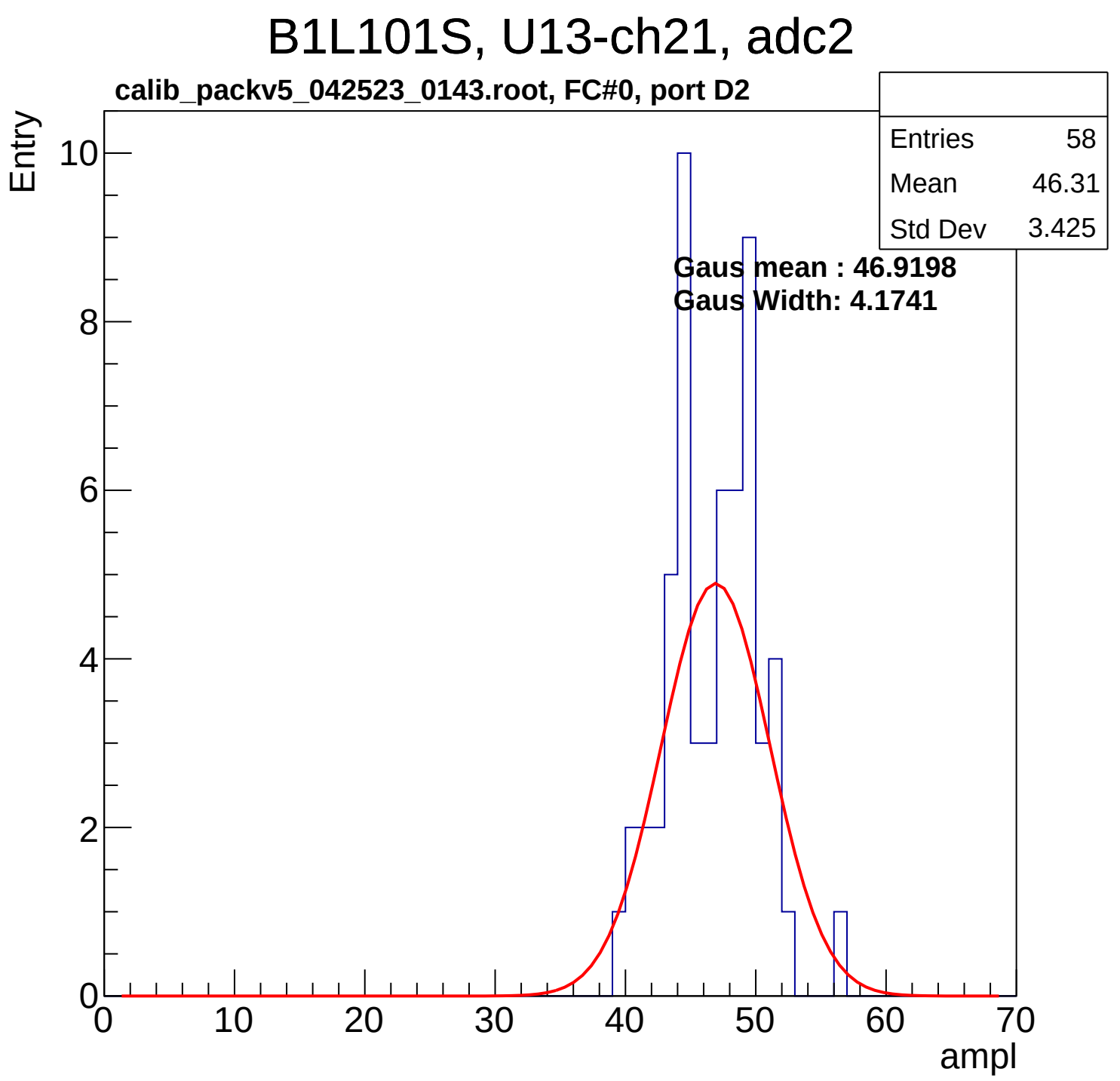

B1L101S, U13-ch20, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 59.1
Std Dev 9.459
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

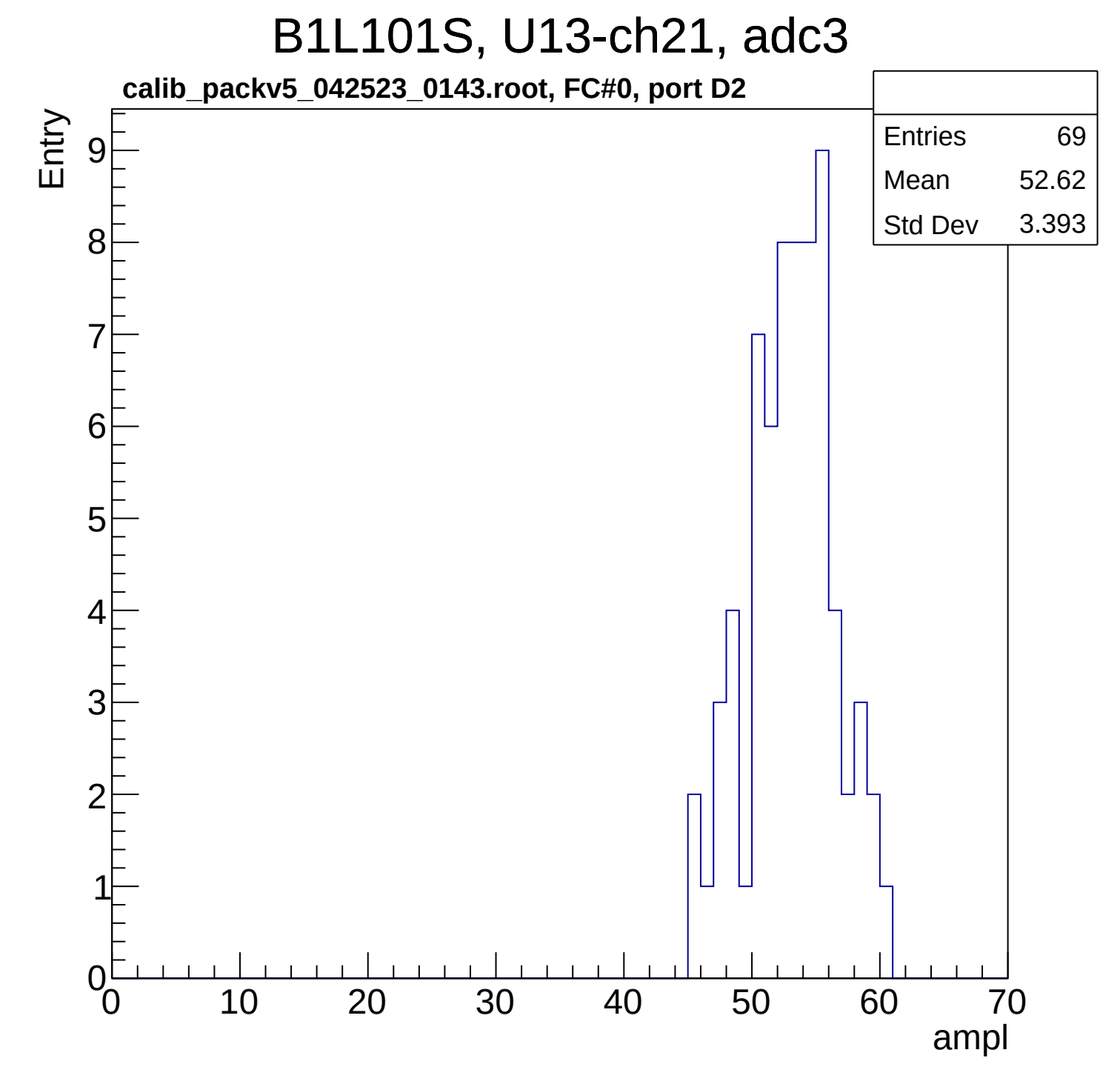

B1L101S, U13-ch20, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch20, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch21, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 30.93
Std Dev 3.64
Gaus mean : 31.2147
Gaus Width: 4.2230
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch21, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 90
Mean 38.98
Std Dev 4.101
Gaus mean : 40.2542
Gaus Width: 4.1631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch21, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 46.31
Std Dev 3.425
Gaus mean : 46.9198
Gaus Width: 4.1741
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch21, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 52.62
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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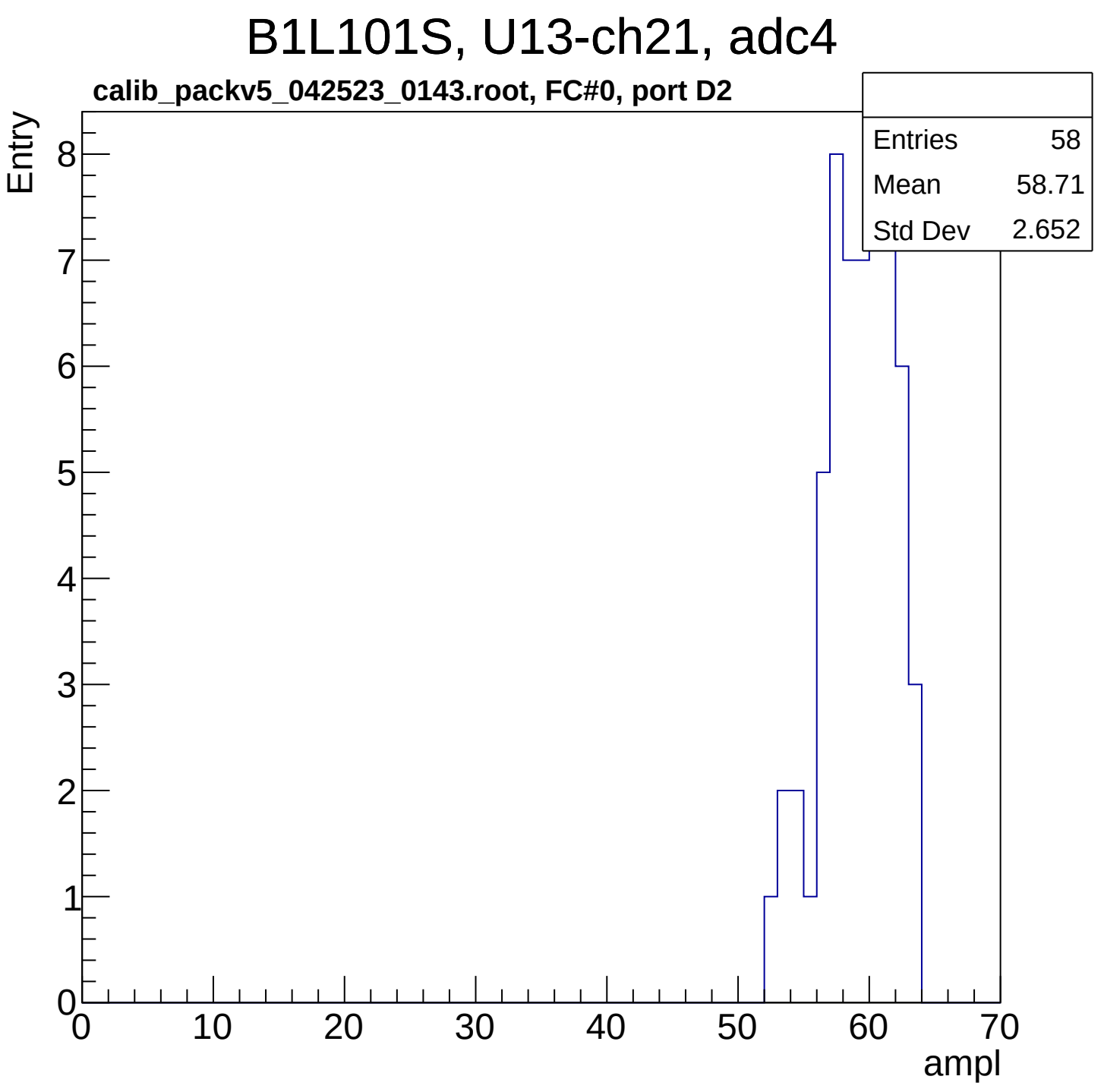
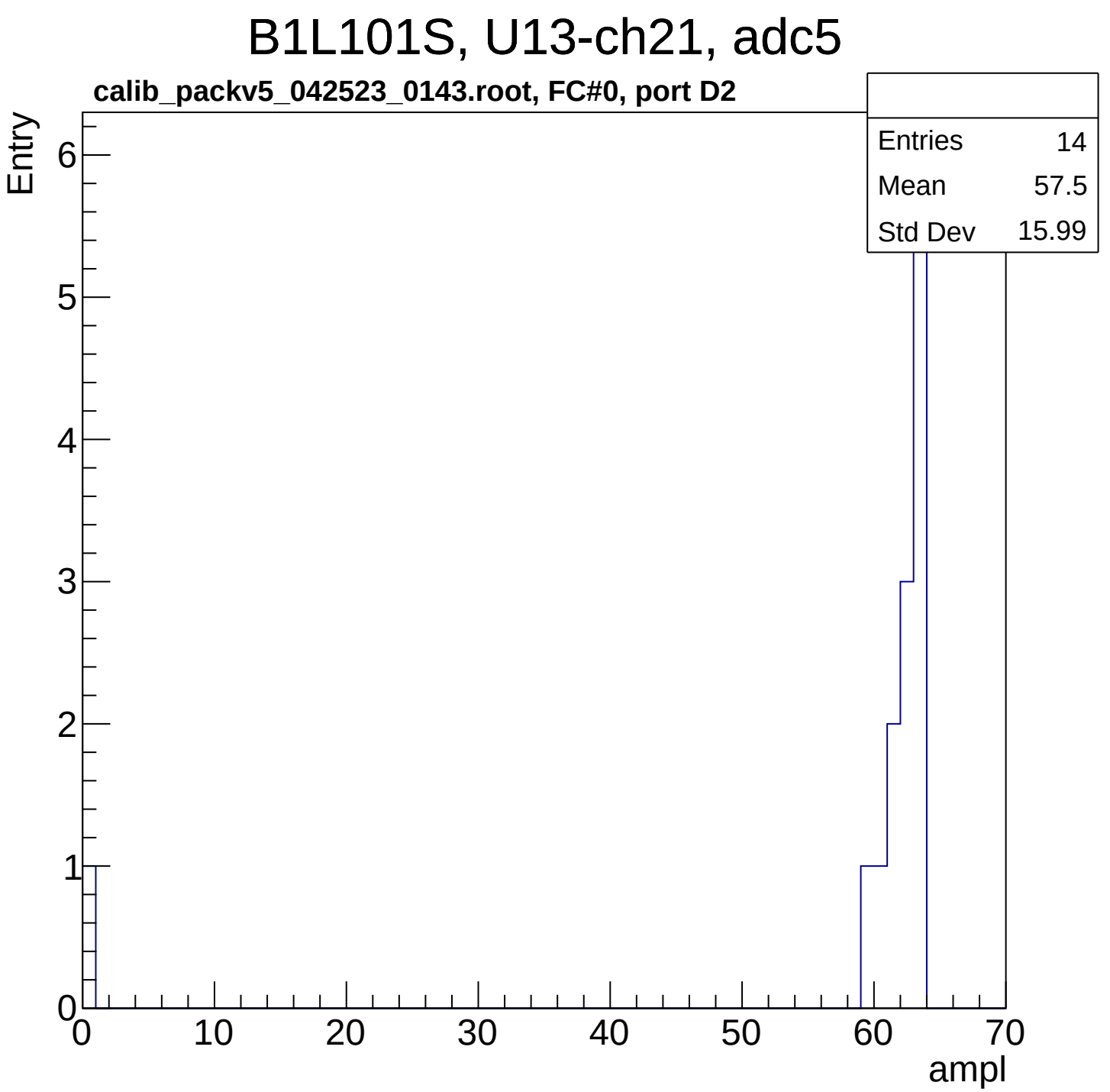
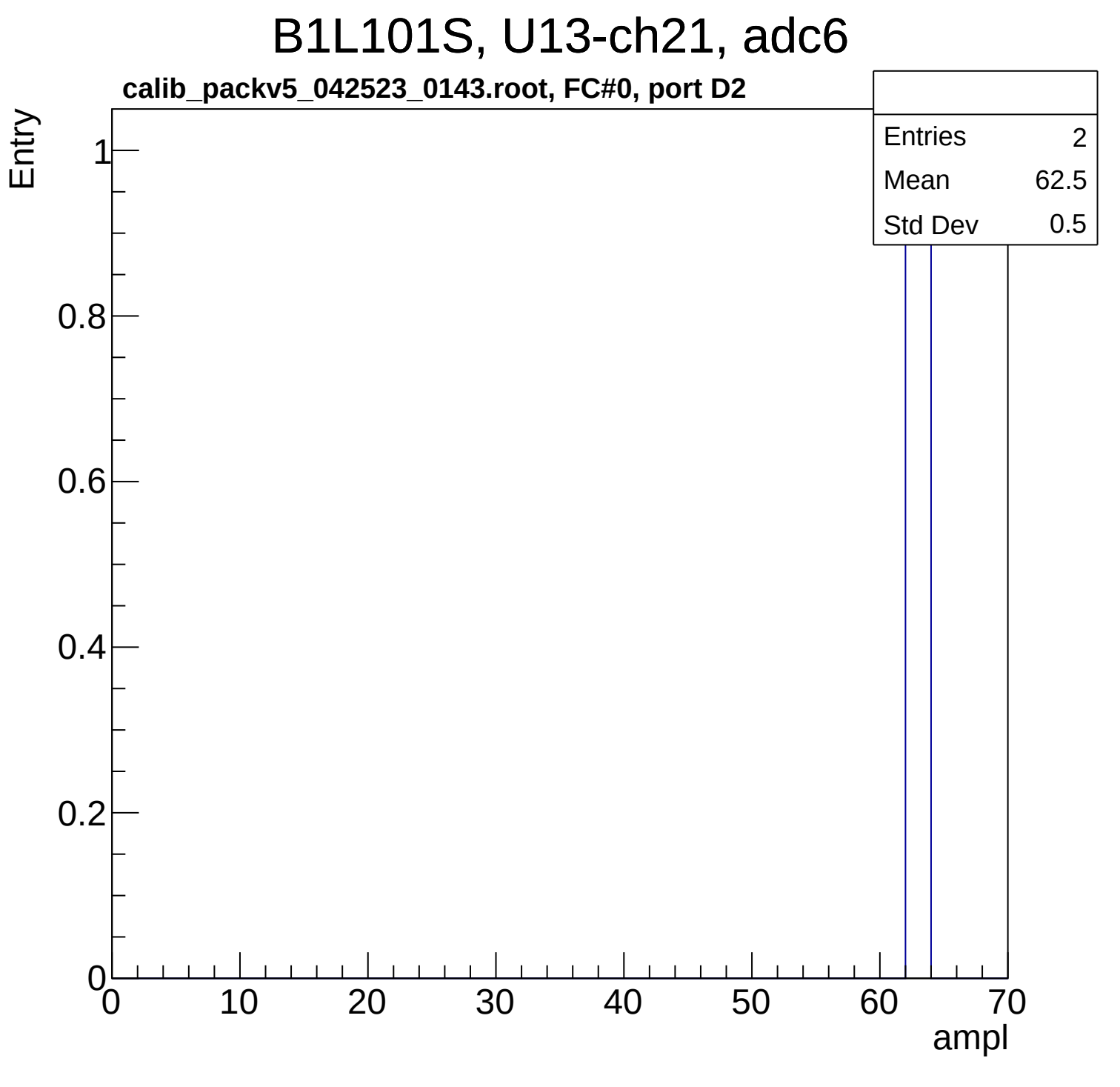
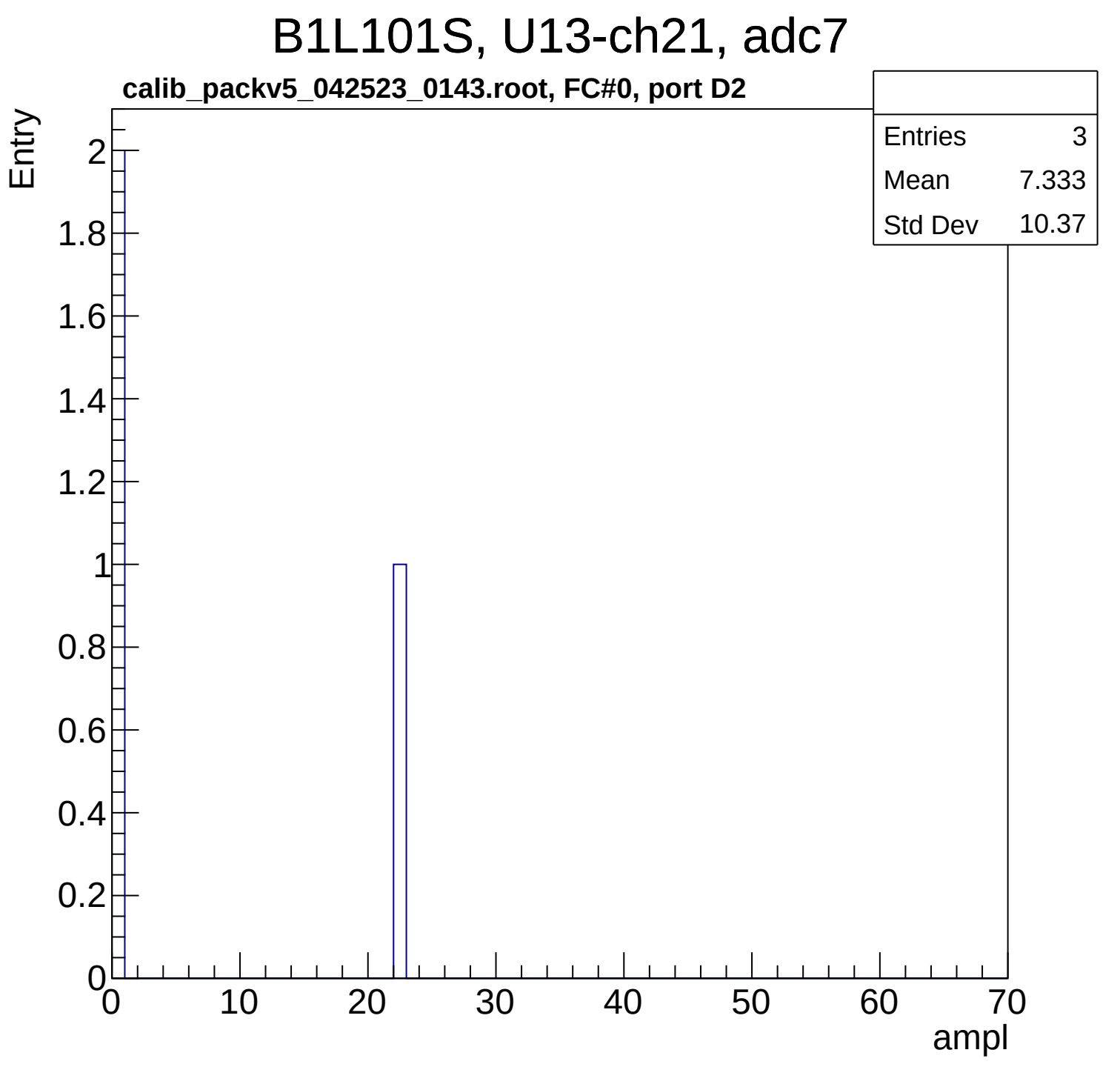
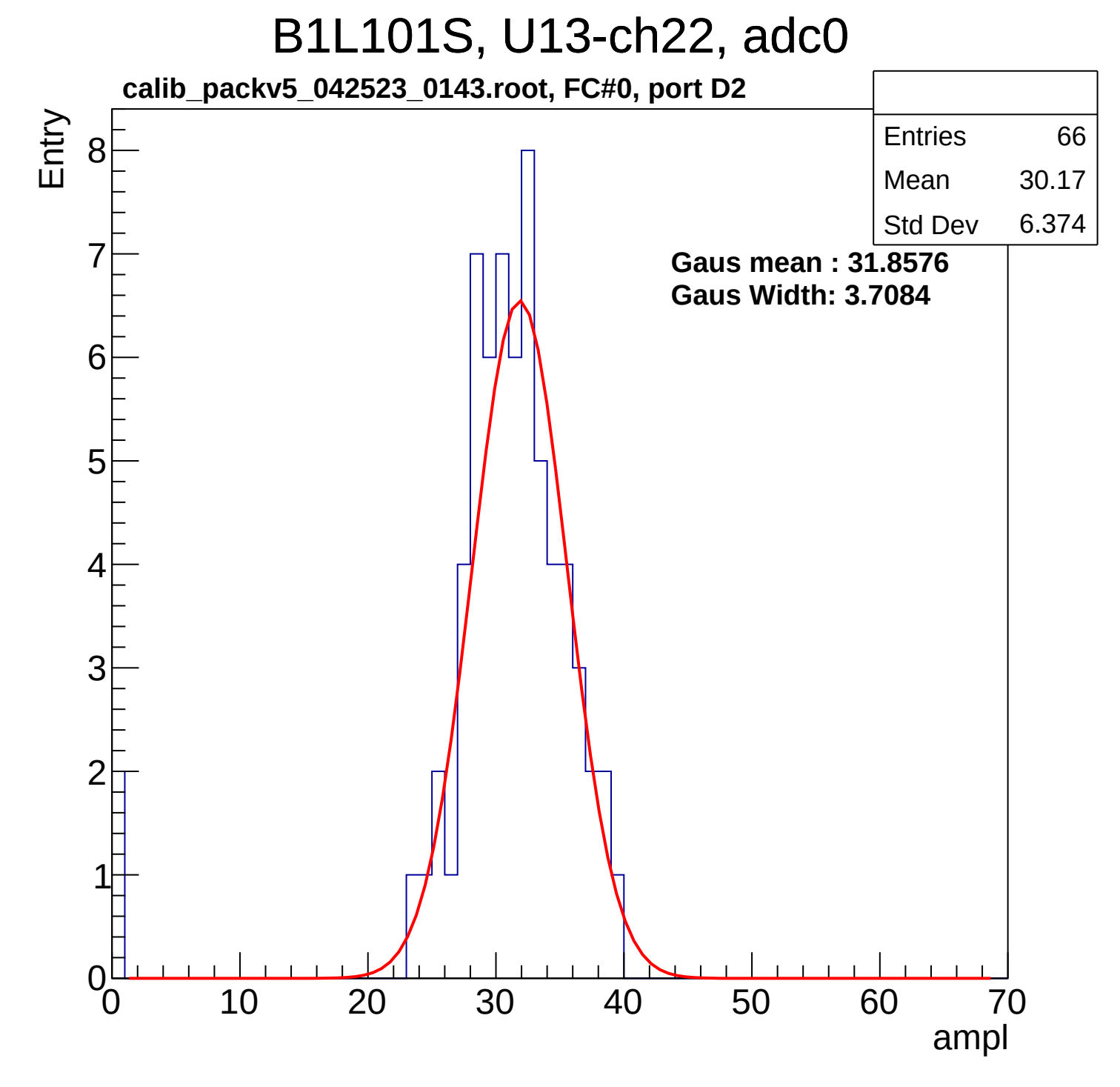
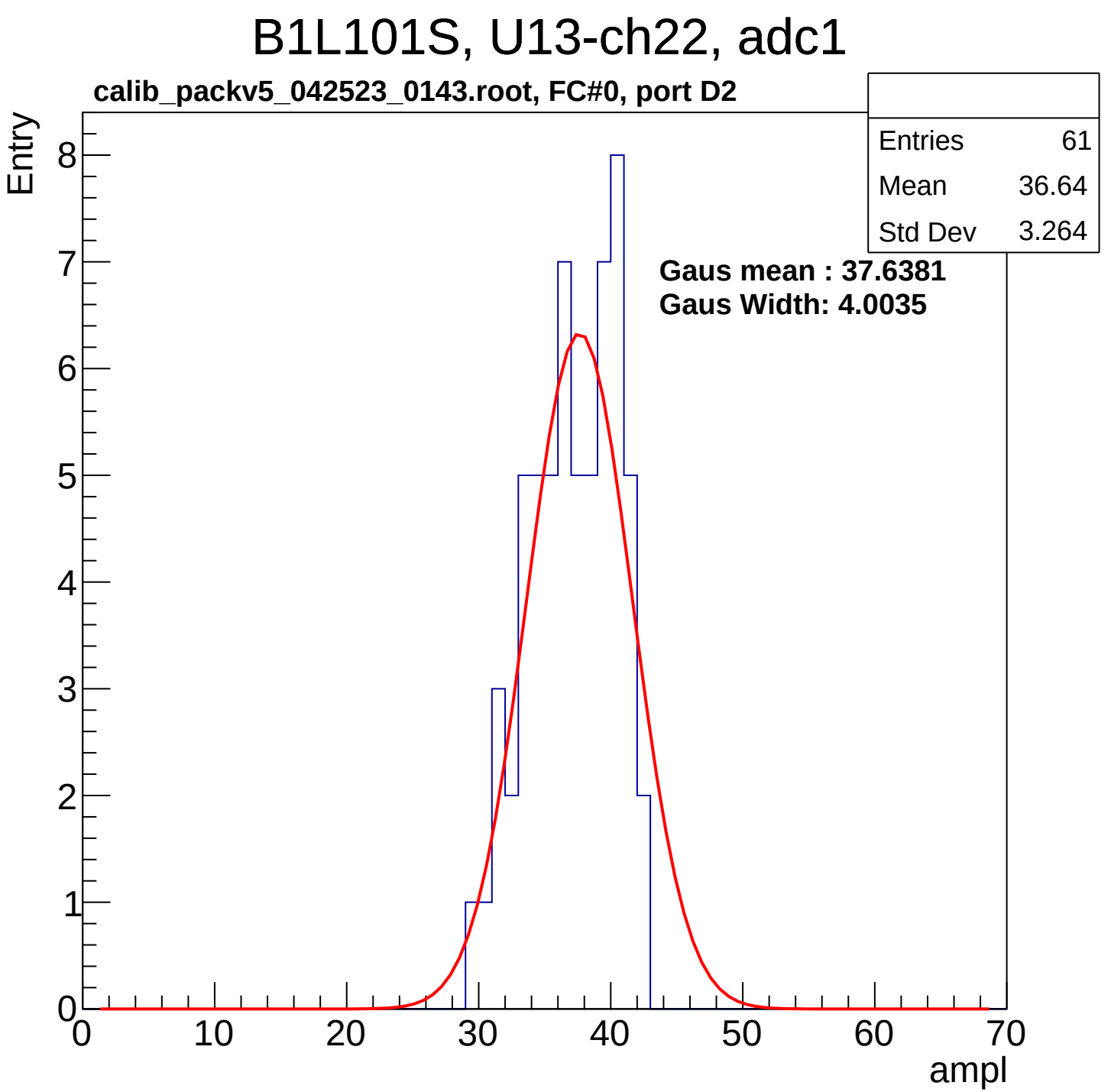

B1L101S, U13-ch21, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 58.71
Std Dev 2.652
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

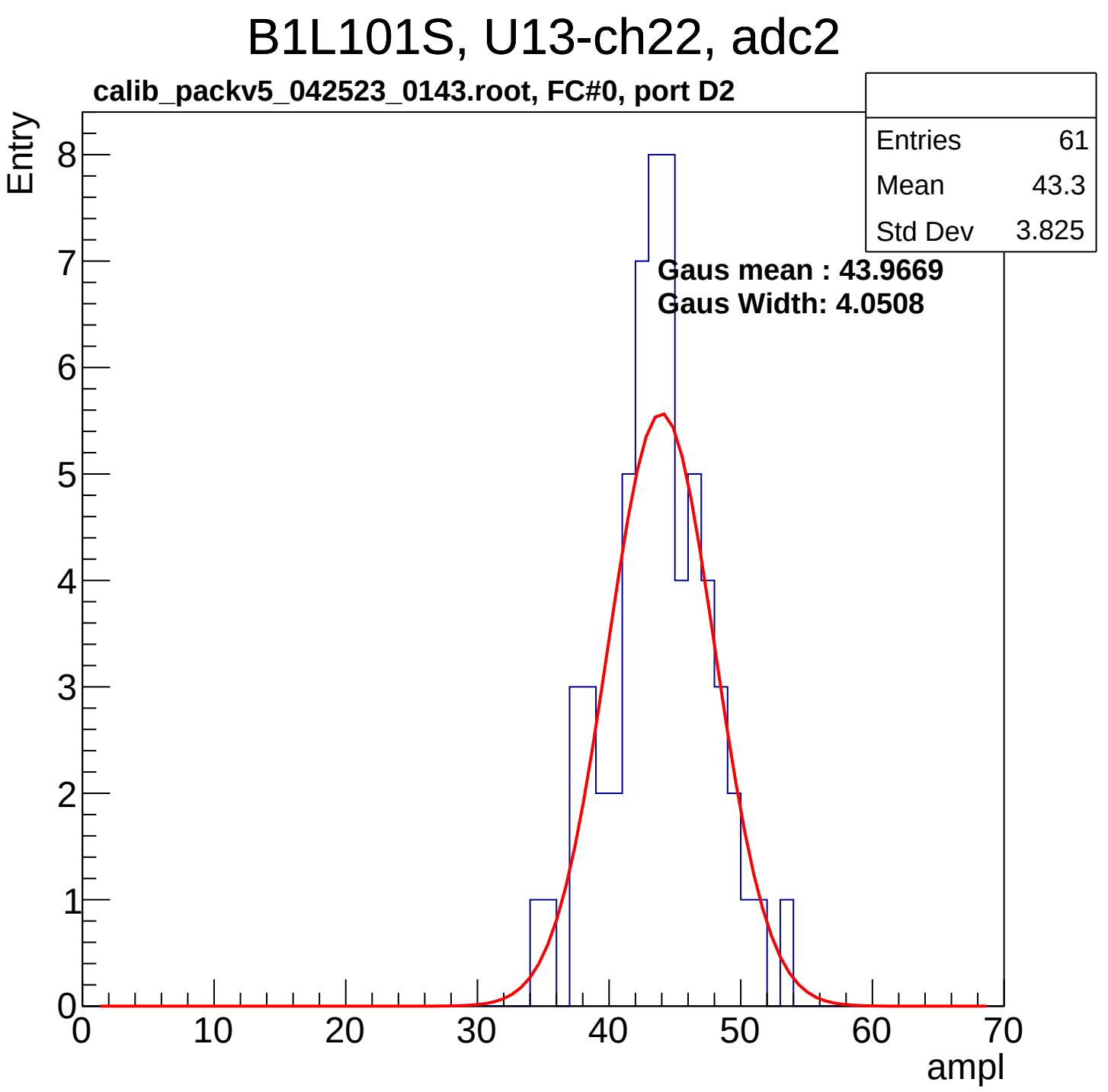

B1L101S, U13-ch21, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 14
Mean 57.5
Std Dev 15.99
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

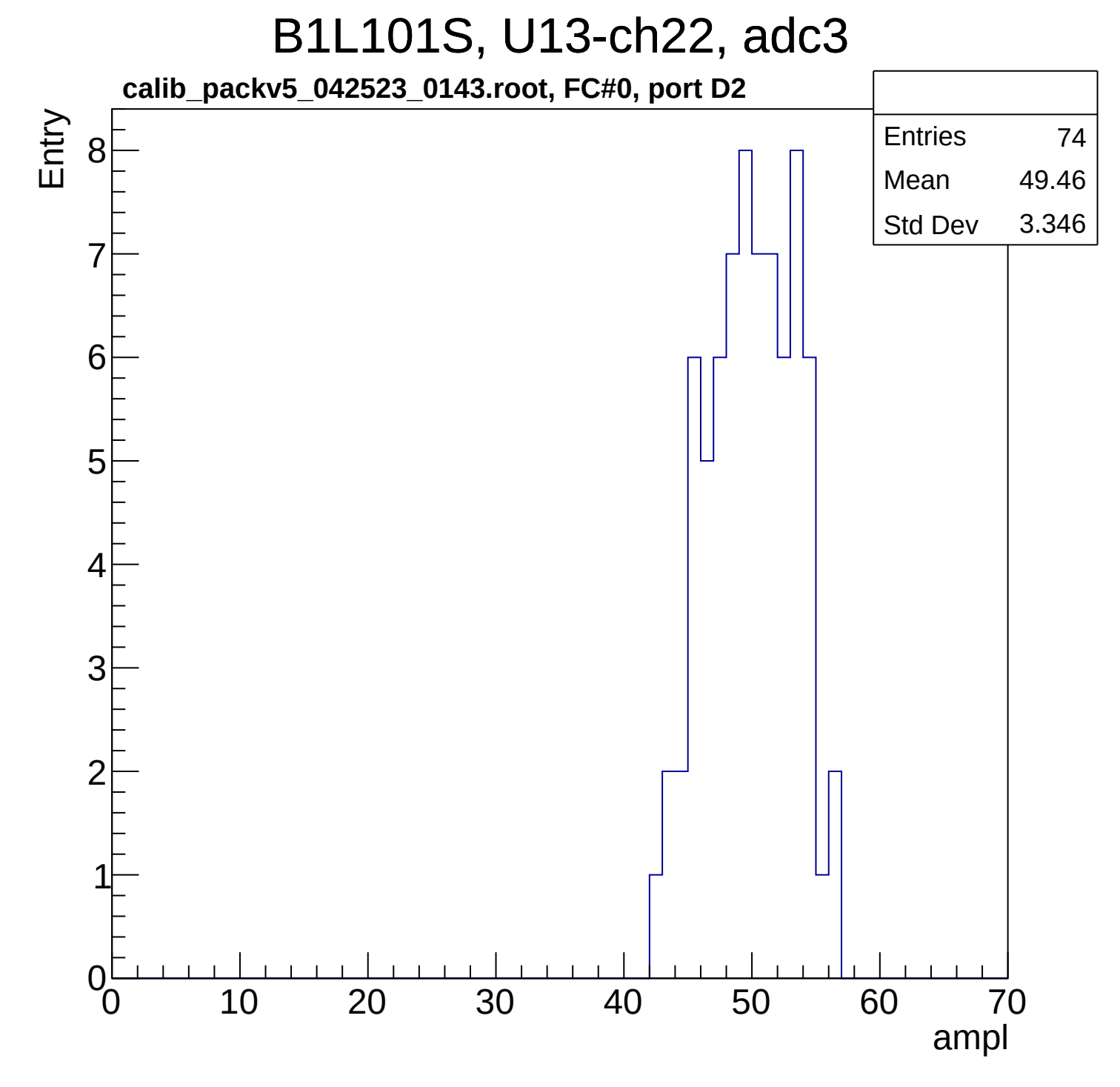

B1L101S, U13-ch21, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch21, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch22, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 30.17
Std Dev 6.374
Gaus mean : 31.8576
Gaus Width: 3.7084
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch22, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 36.64
Std Dev 3.264
Gaus mean : 37.6381
Gaus Width: 4.0035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch22, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 43.3
Std Dev 3.825
Gaus mean : 43.9669
Gaus Width: 4.0508
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch22, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 49.46
Std Dev 3.346
Entry
ampl
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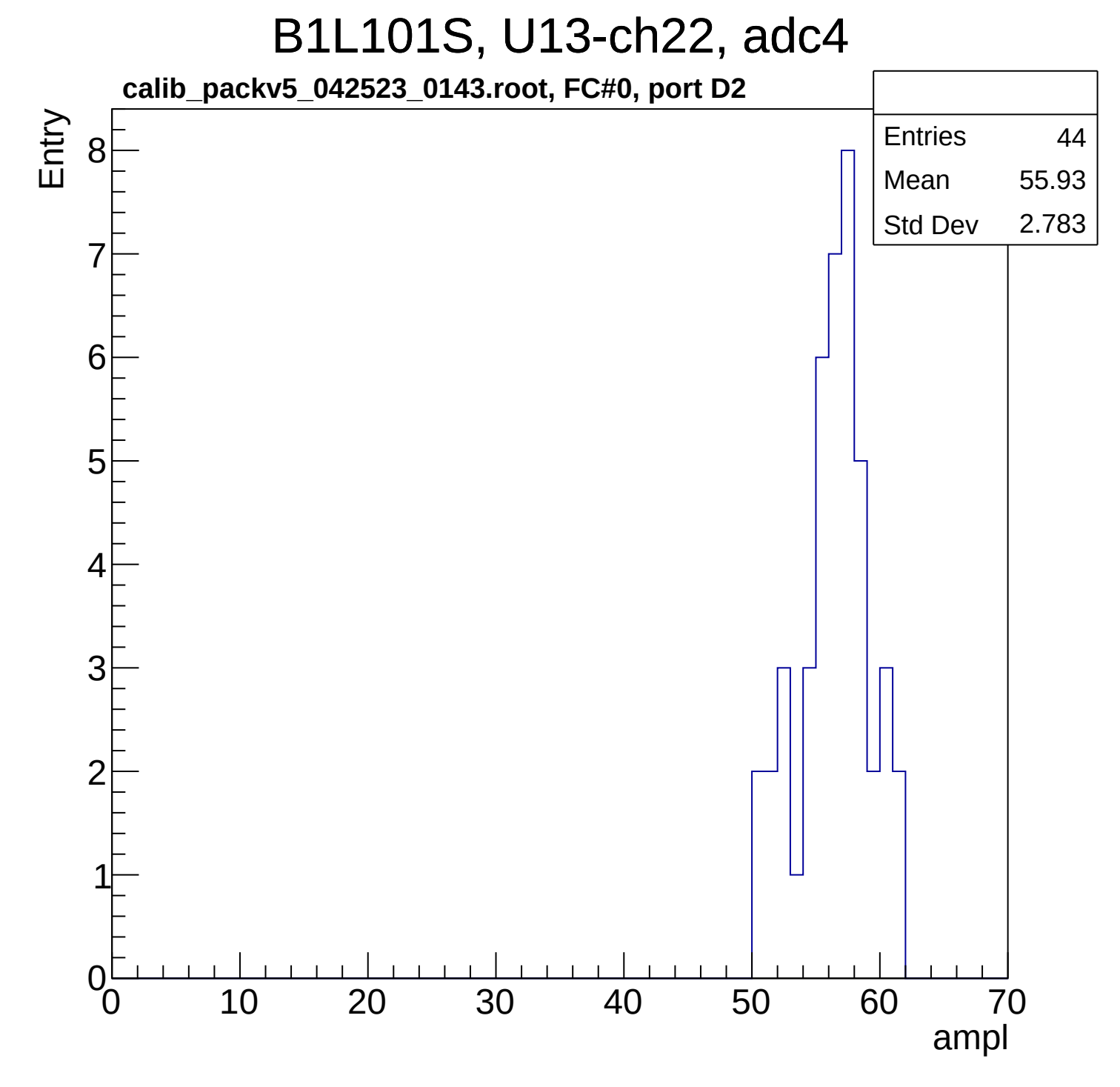
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch22, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 55.93
Std Dev 2.783
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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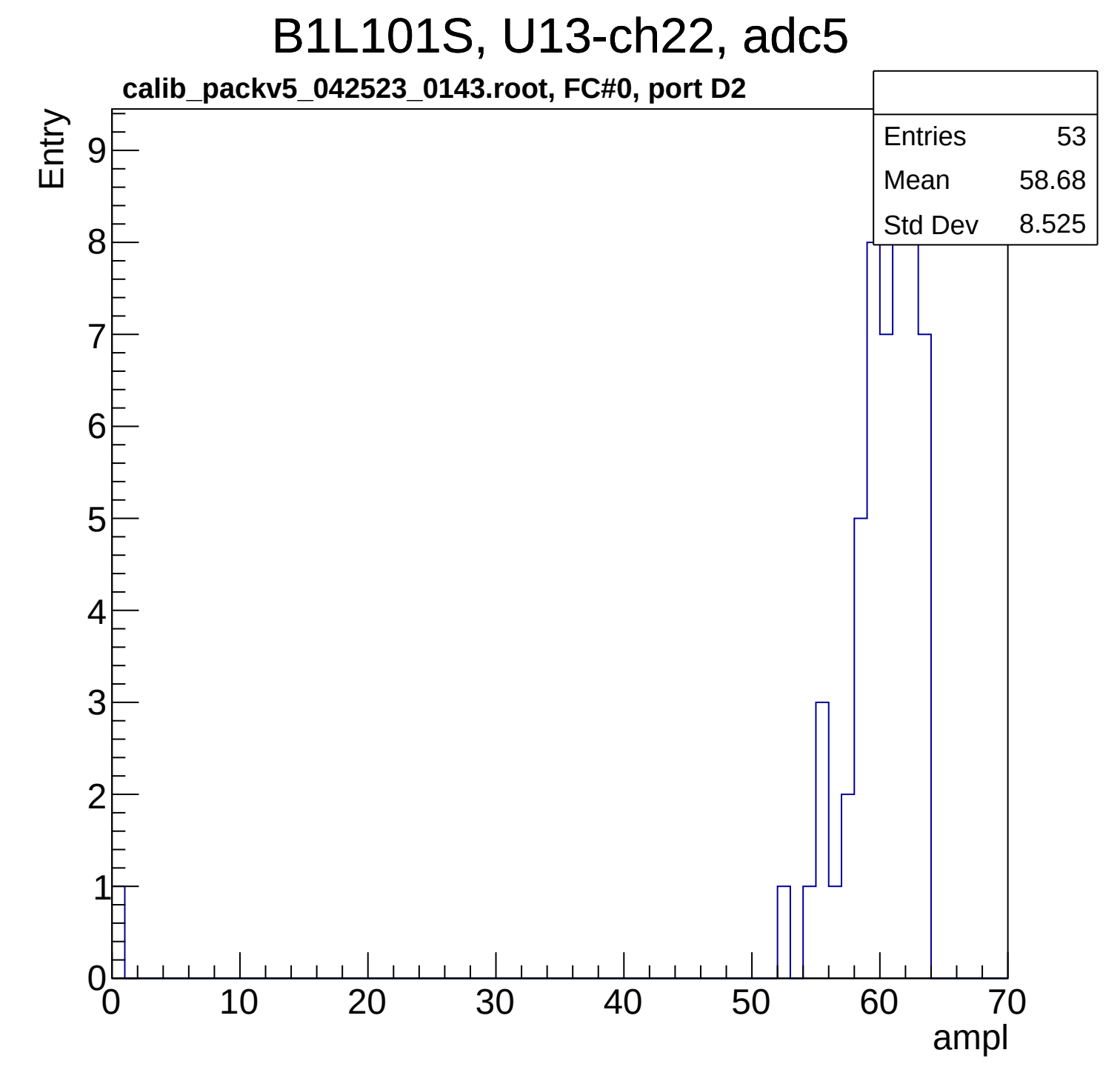
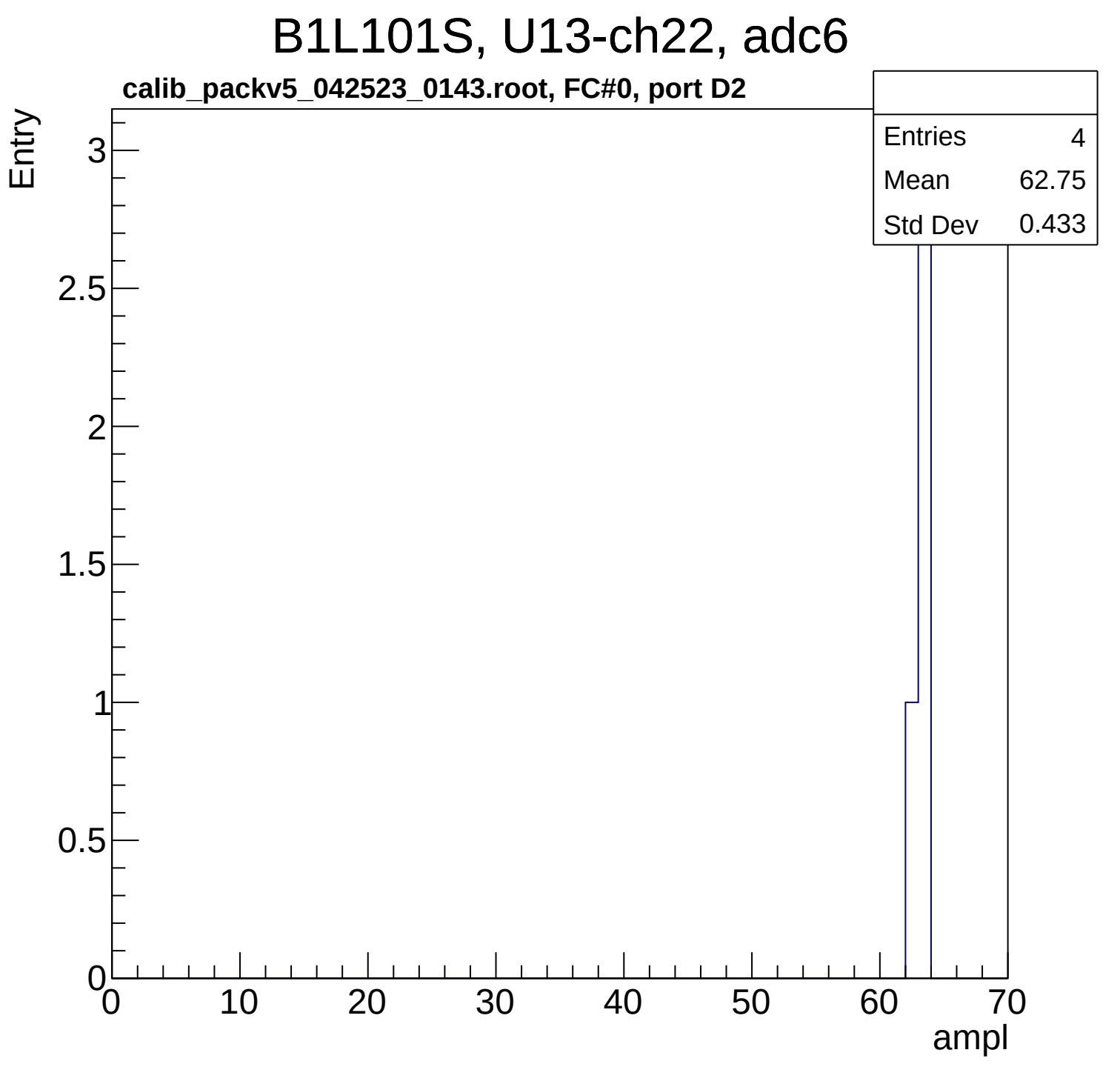

B1L101S, U13-ch22, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 58.68
Std Dev 8.525
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch22, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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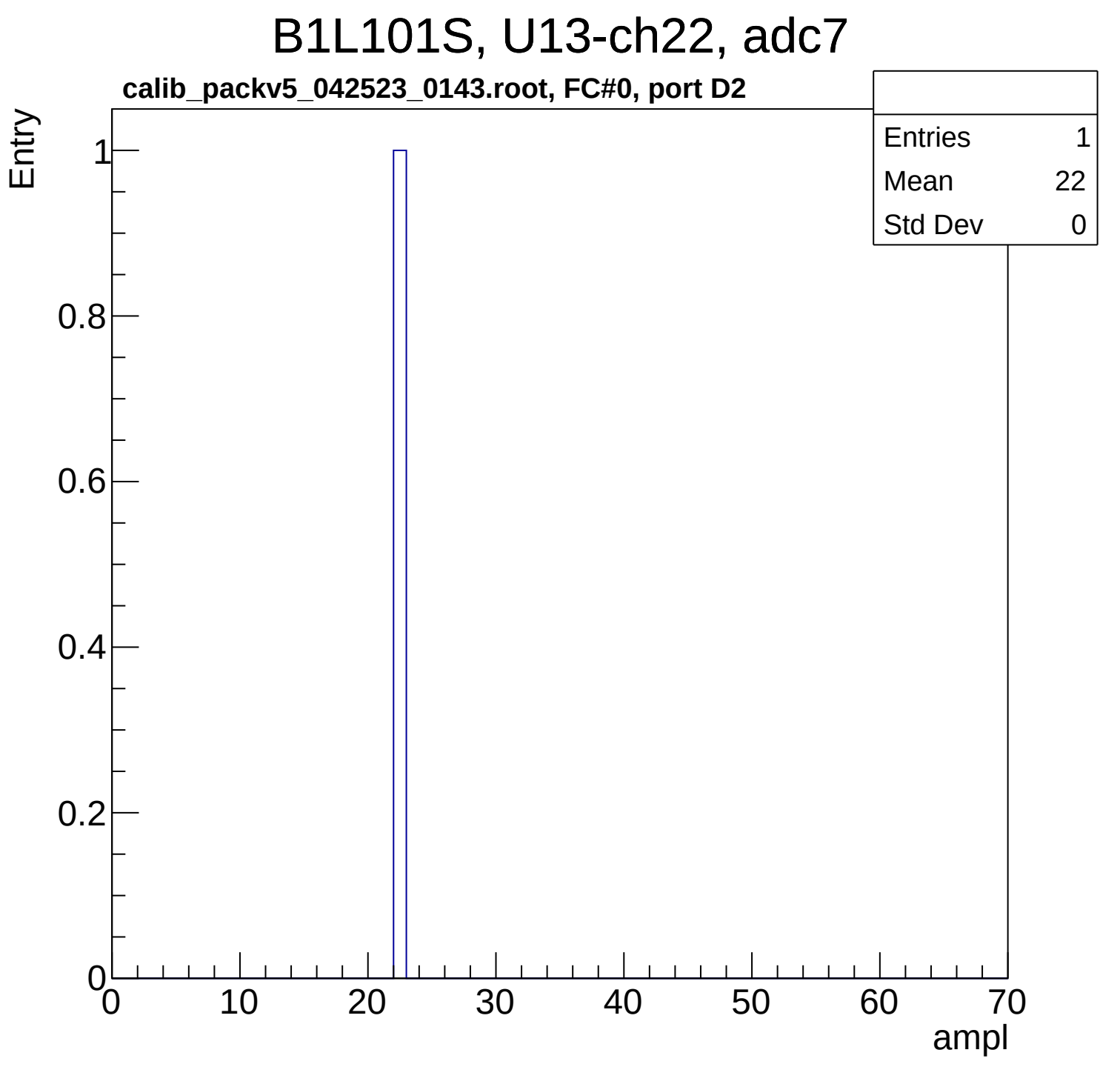
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch22, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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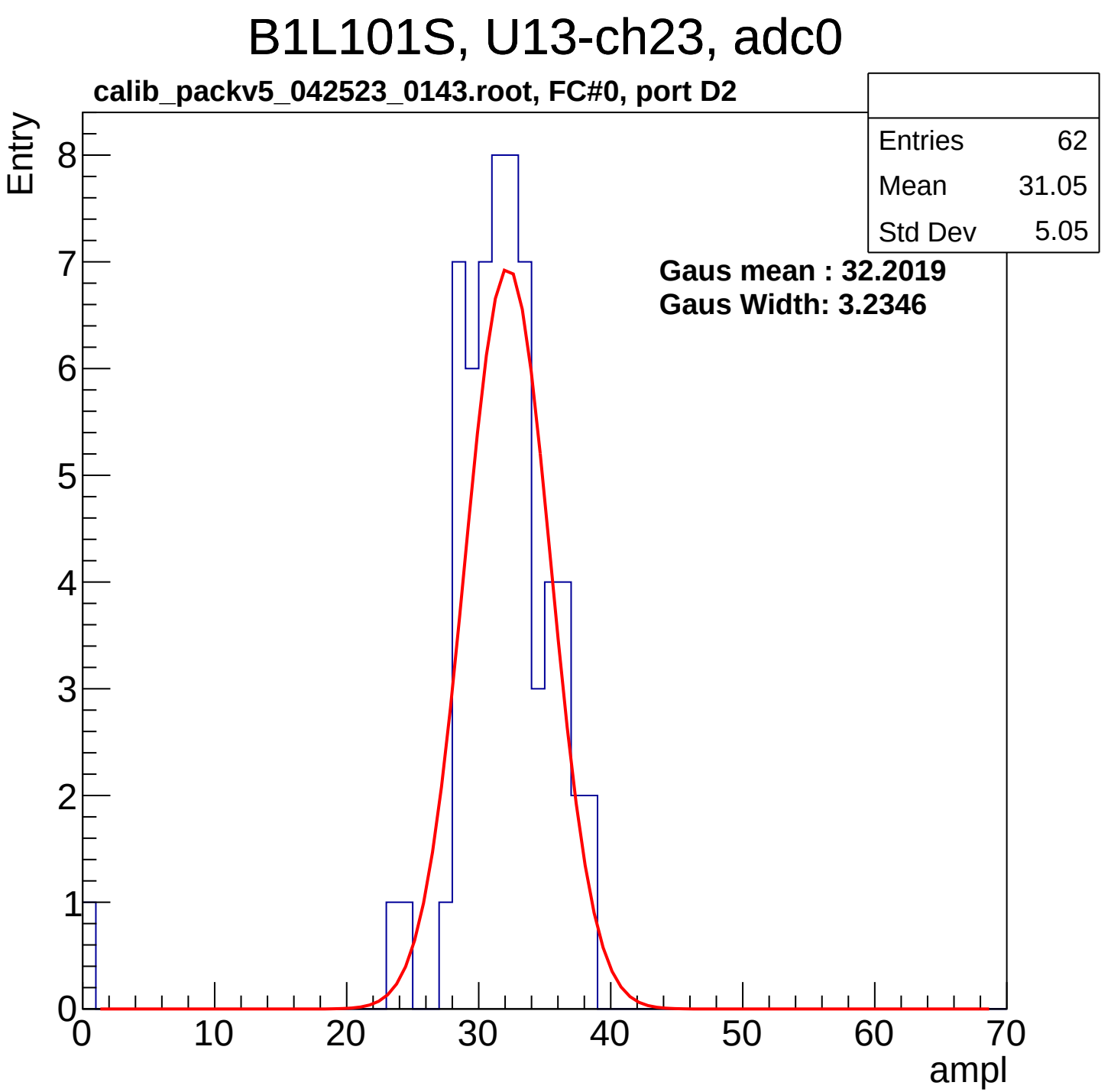
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch23, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 31.05
Std Dev 5.05
Gaus mean : 32.2019
Gaus Width: 3.2346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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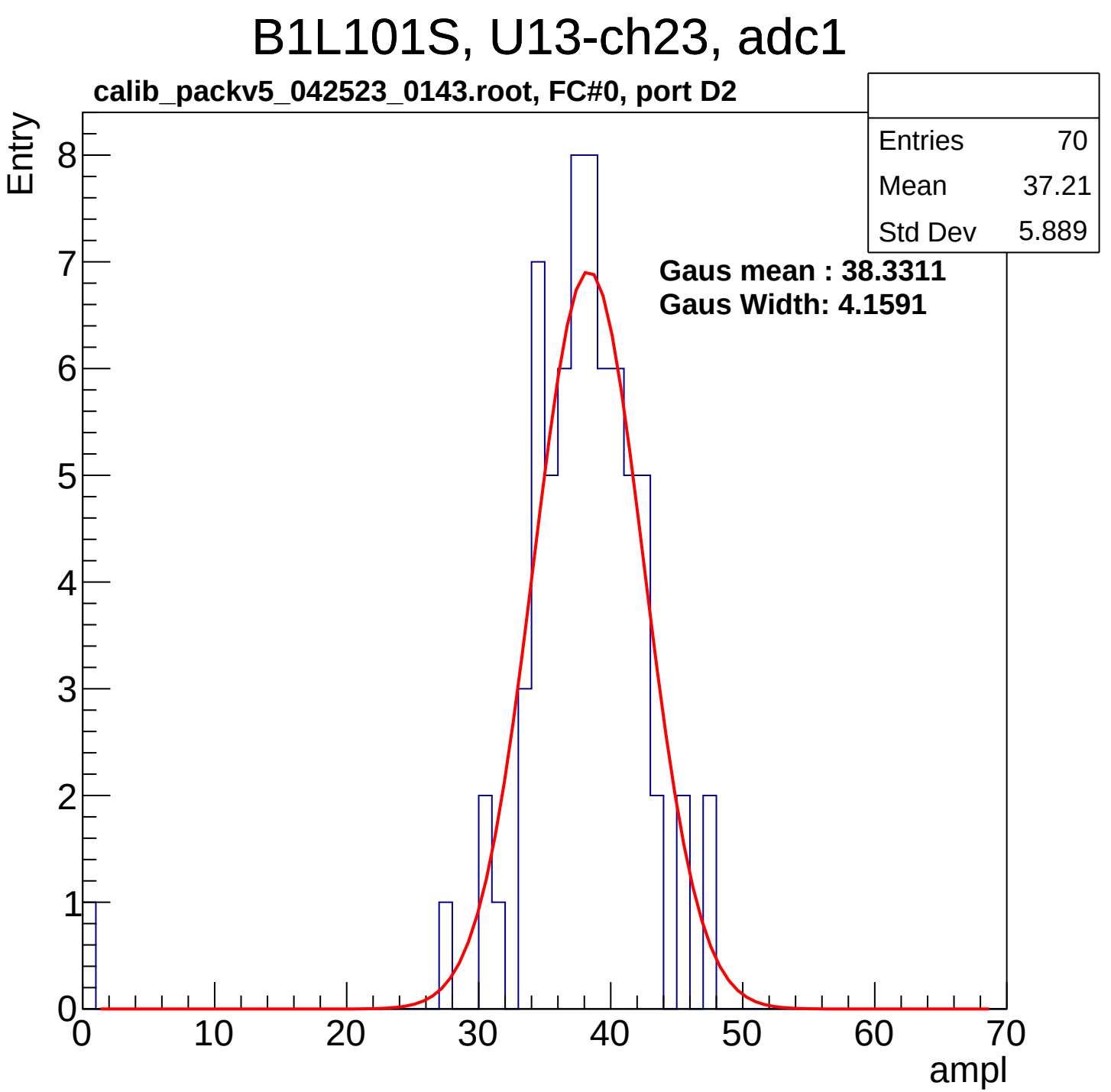
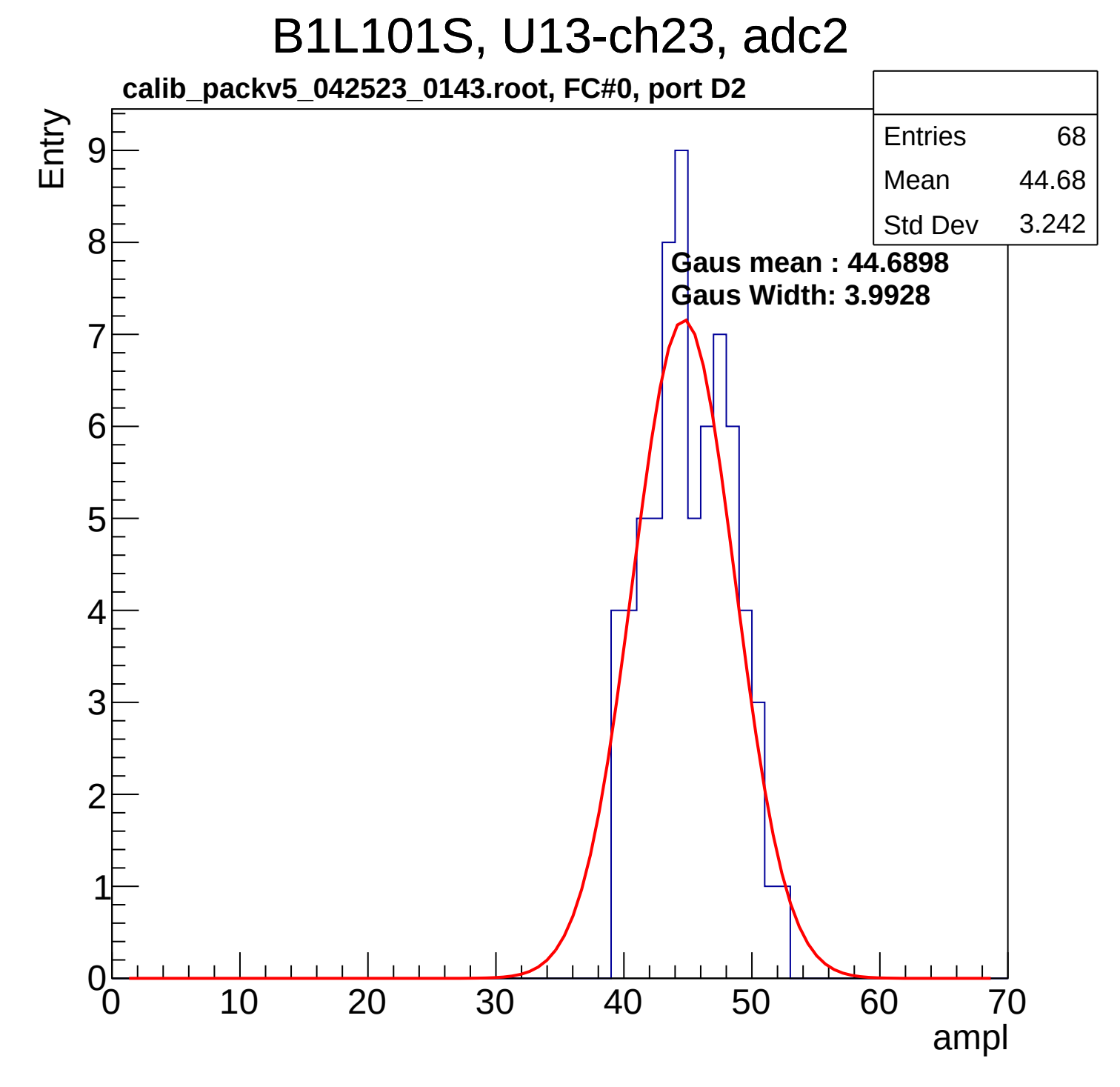
60
70


B1L101S, U13-ch23, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 37.21
Std Dev 5.889
Gaus mean : 38.3311
Gaus Width: 4.1591
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch23, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 44.68
Std Dev 3.242
Gaus mean : 44.6898
Gaus Width: 3.9928
Entry
ampl
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1
2
3
4
5
6
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8
9
0
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40
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60
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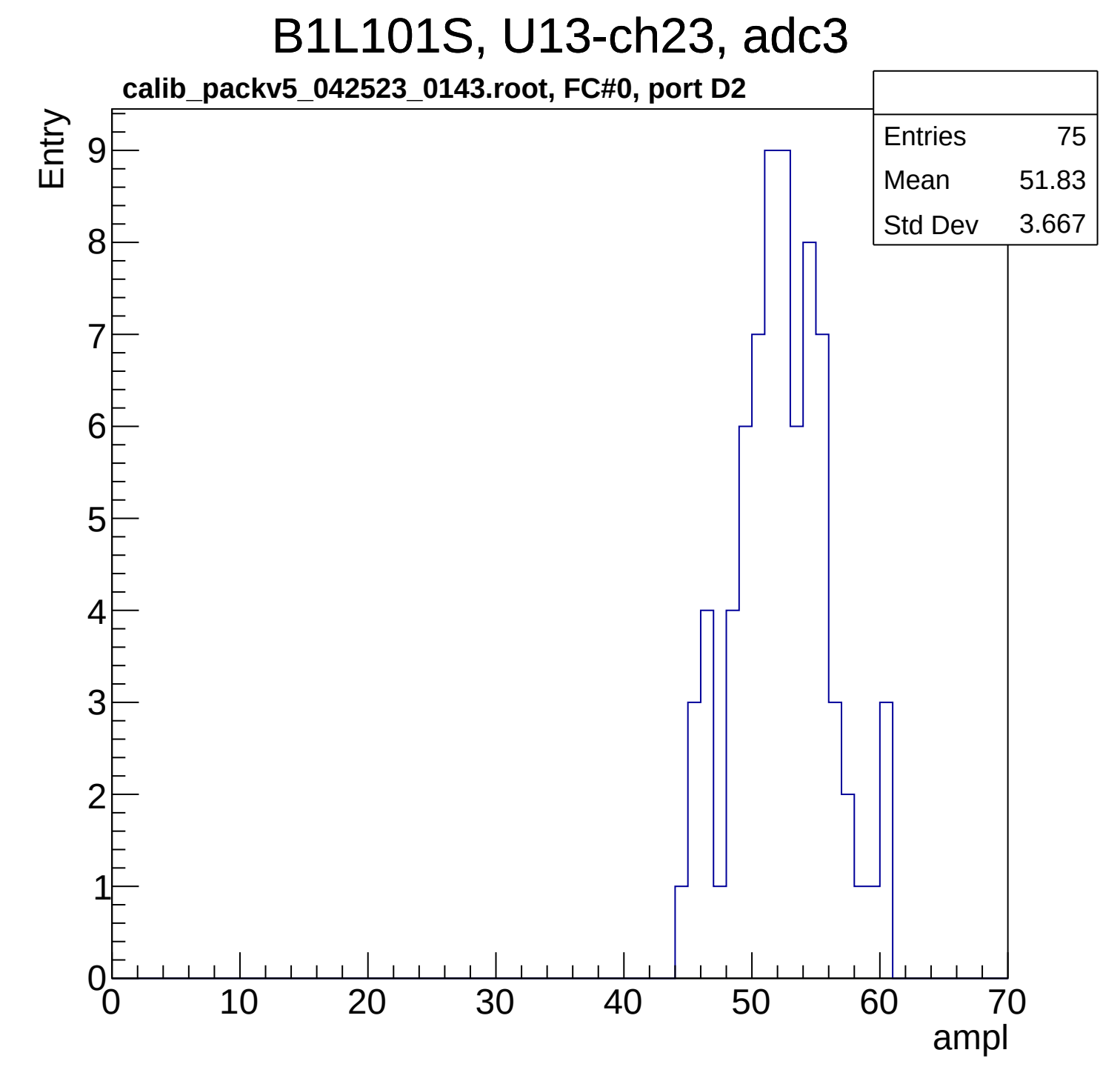
B1L101S, U13-ch23, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 51.83
Std Dev 3.667
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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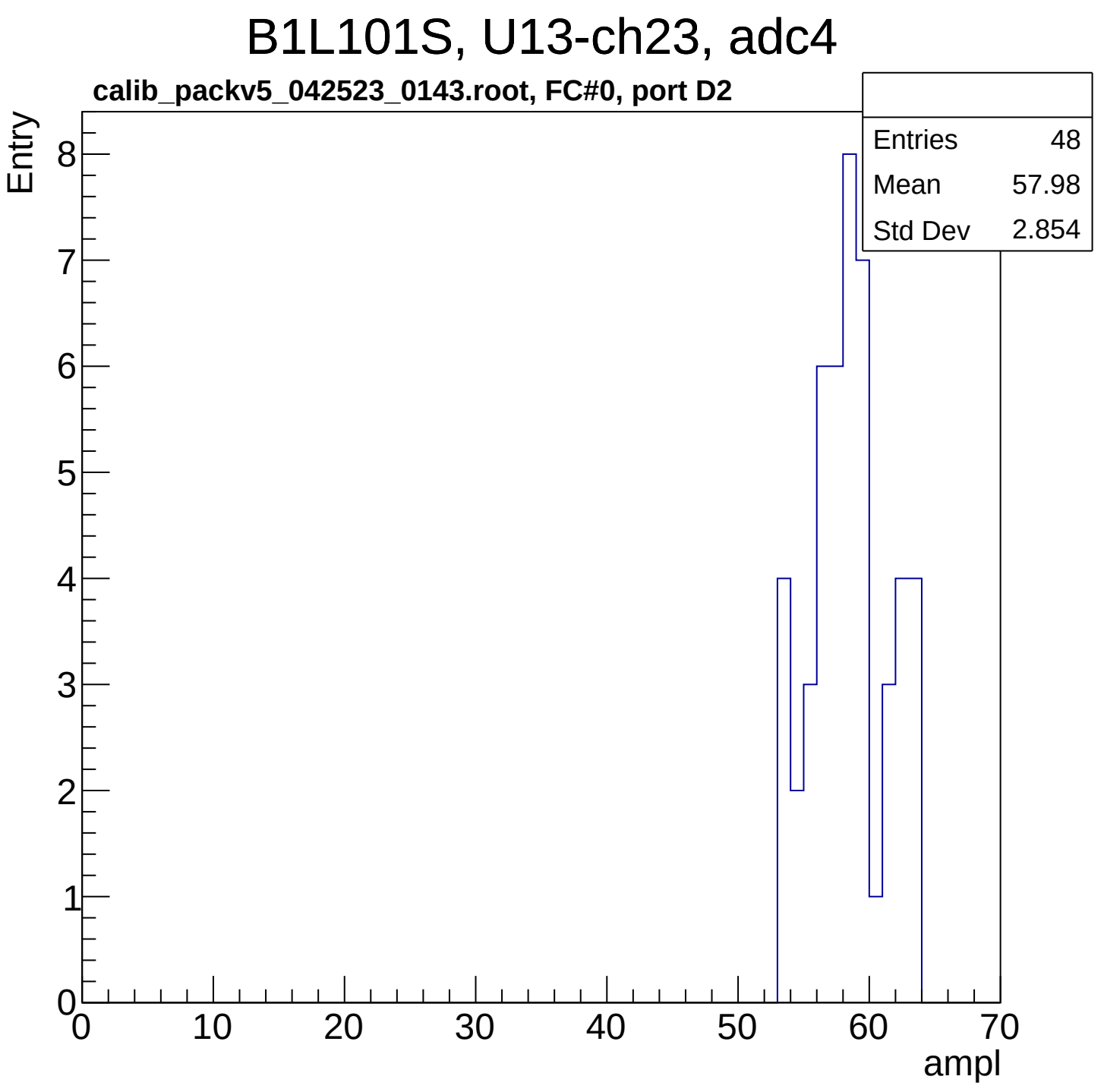
B1L101S, U13-ch23, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 57.98
Std Dev 2.854
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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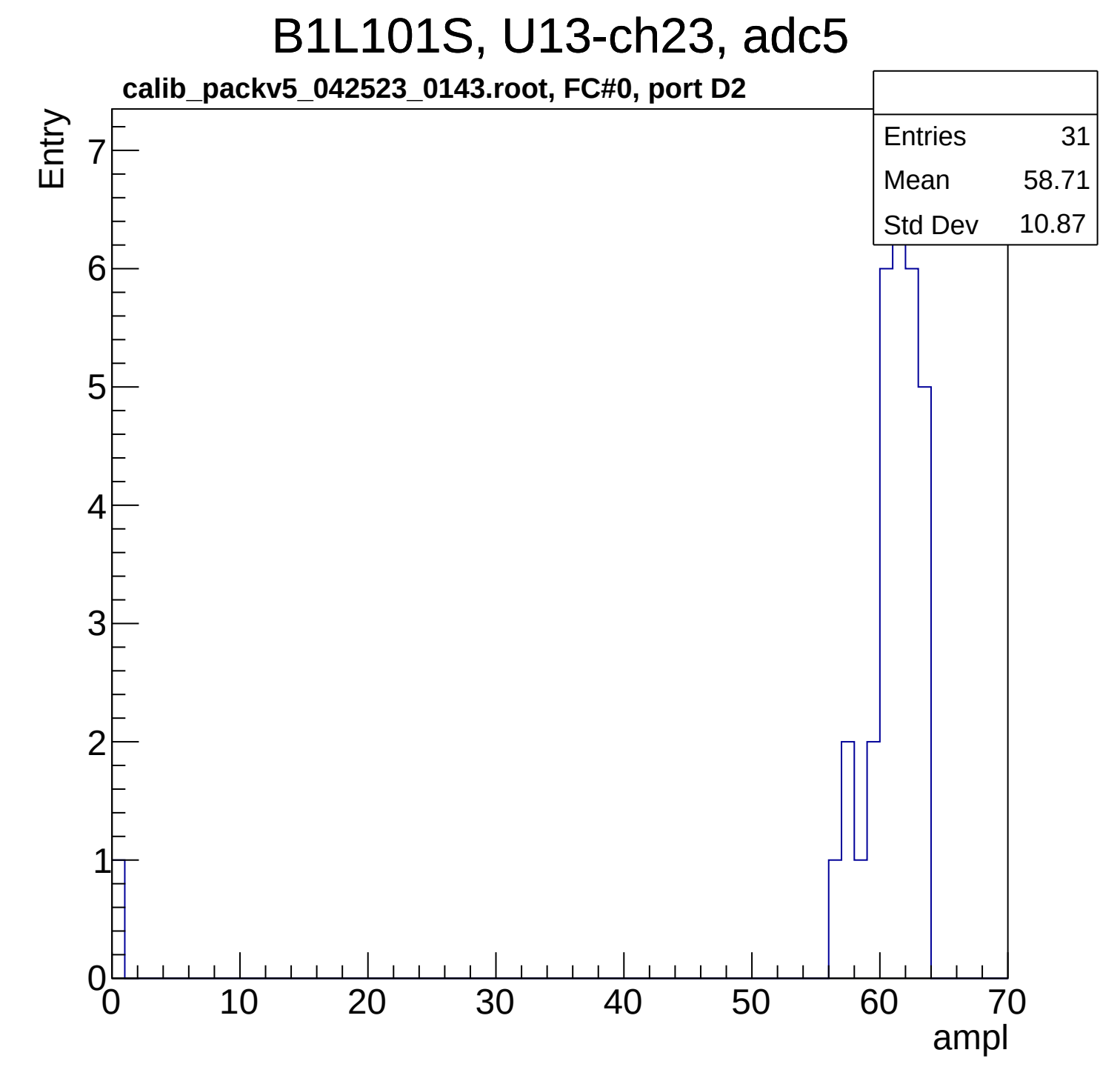
B1L101S, U13-ch23, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 31
Mean 58.71
Std Dev 10.87
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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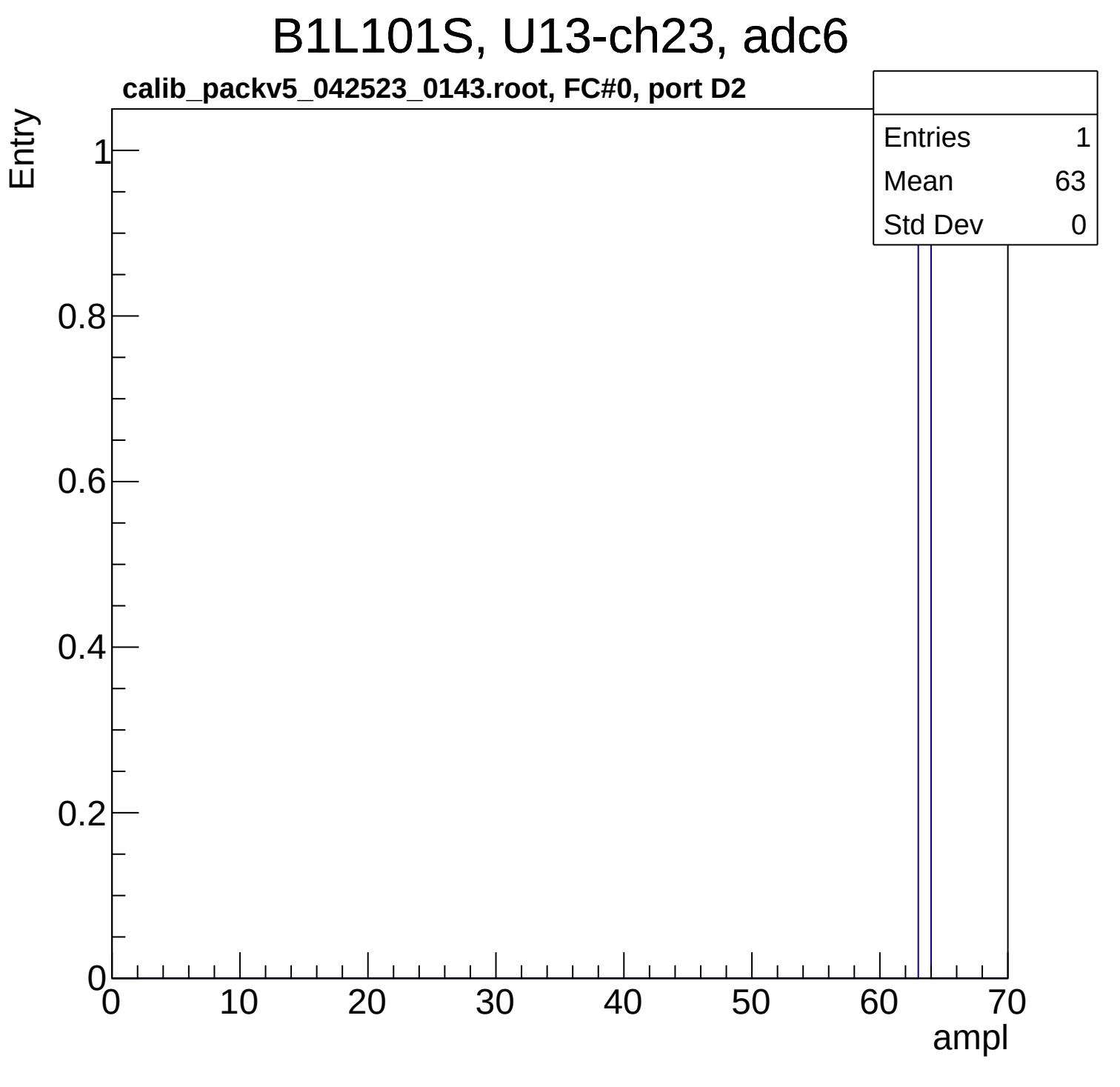
B1L101S, U13-ch23, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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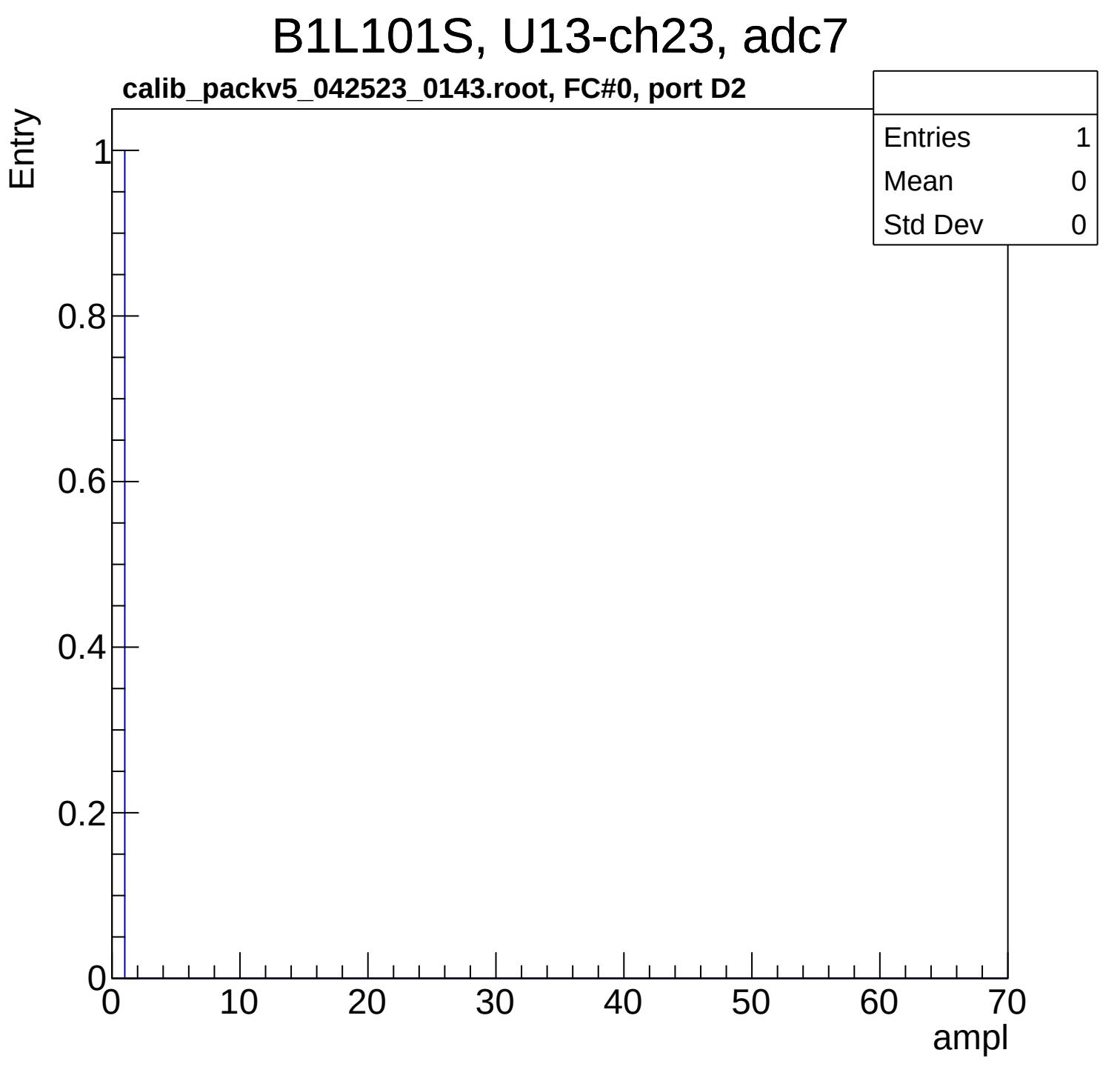
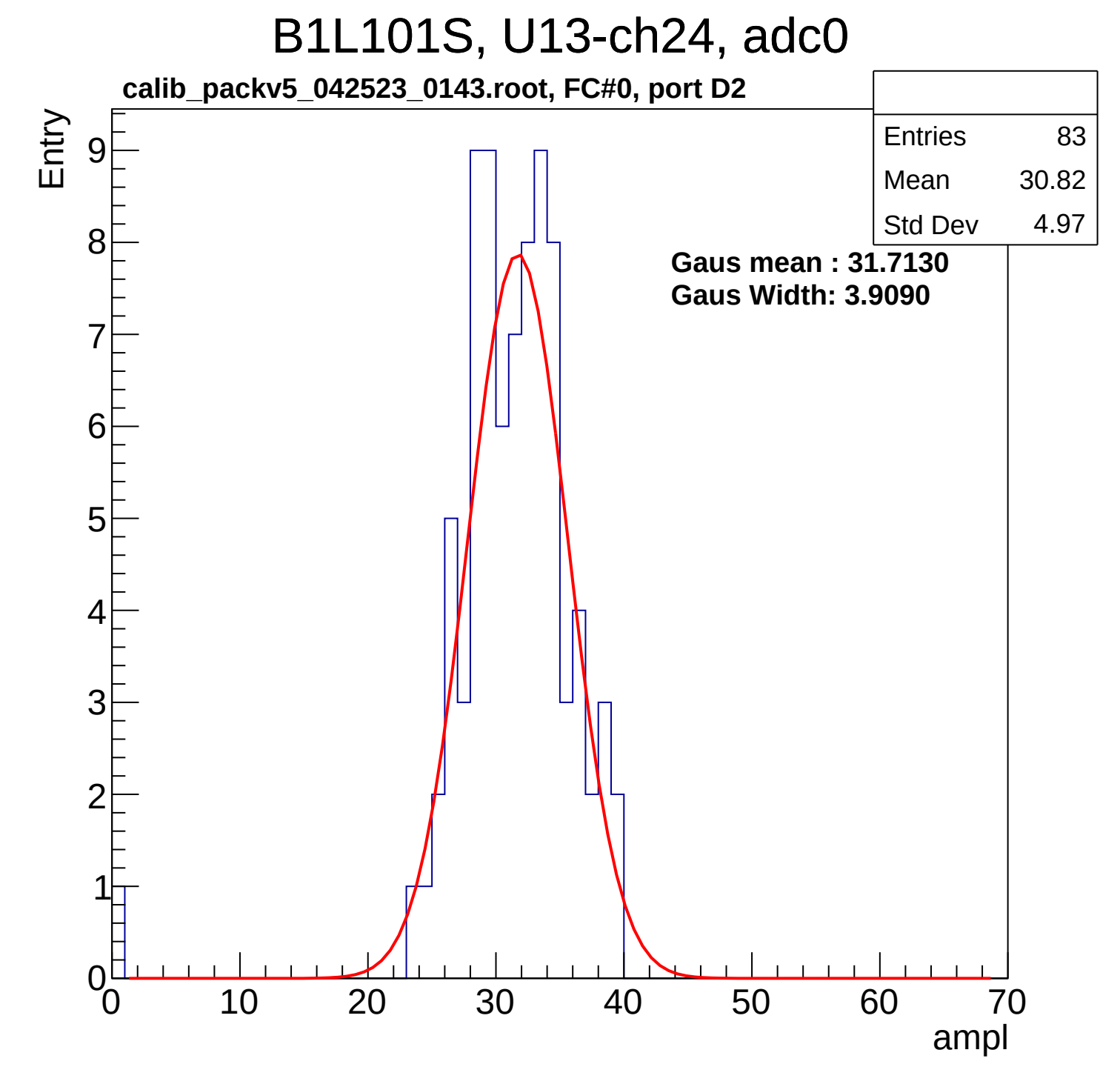
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch23, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch24, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 83
Mean 30.82
Std Dev 4.97
Gaus mean : 31.7130
Gaus Width: 3.9090
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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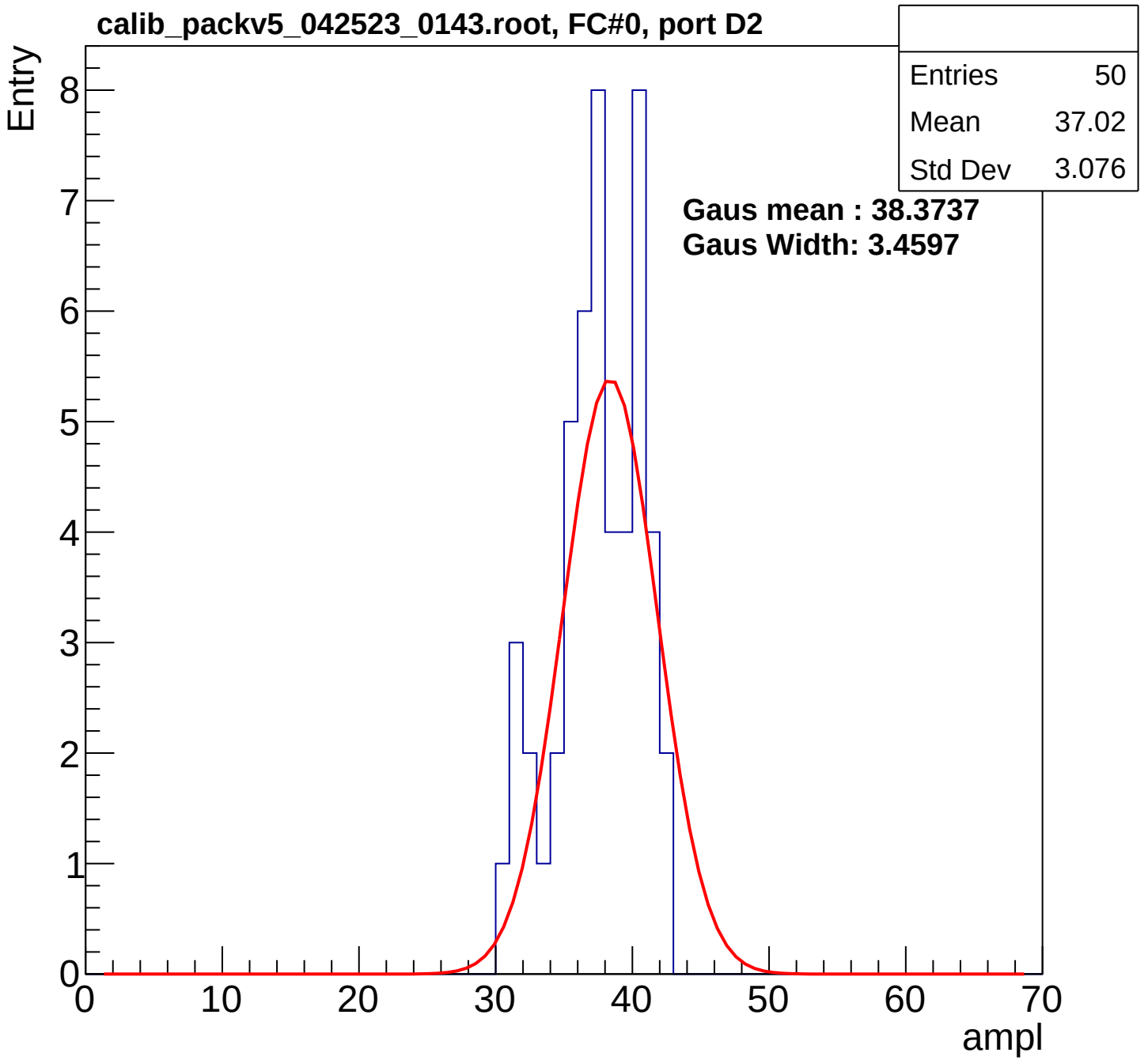
B1L101S, U13-ch24, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 50
Mean 37.02
Std Dev 3.076
Gaus mean : 38.3737
Gaus Width: 3.4597
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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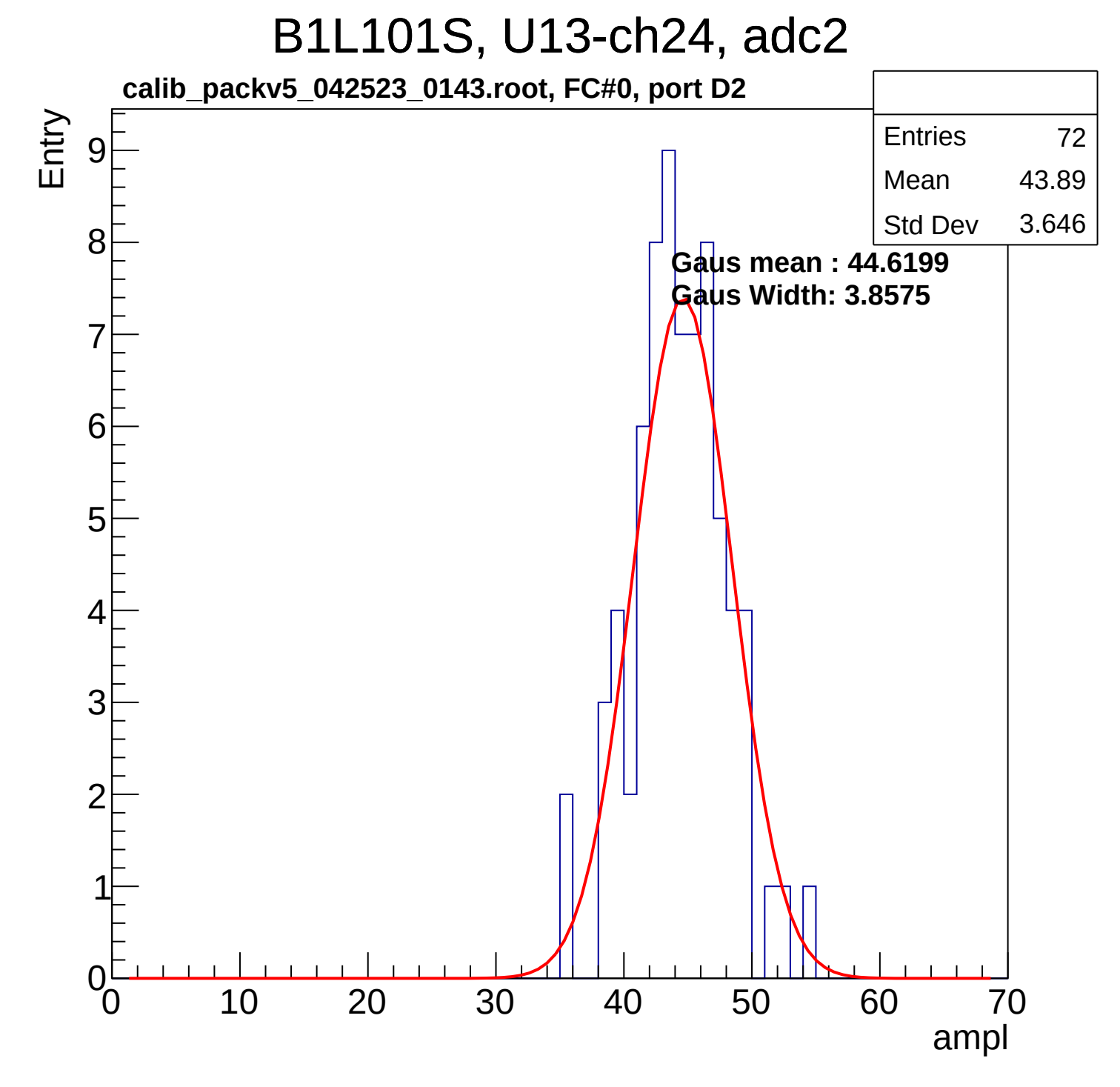
B1L101S, U13-ch24, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 43.89
Std Dev 3.646
Gaus mean : 44.6199
Gaus Width: 3.8575
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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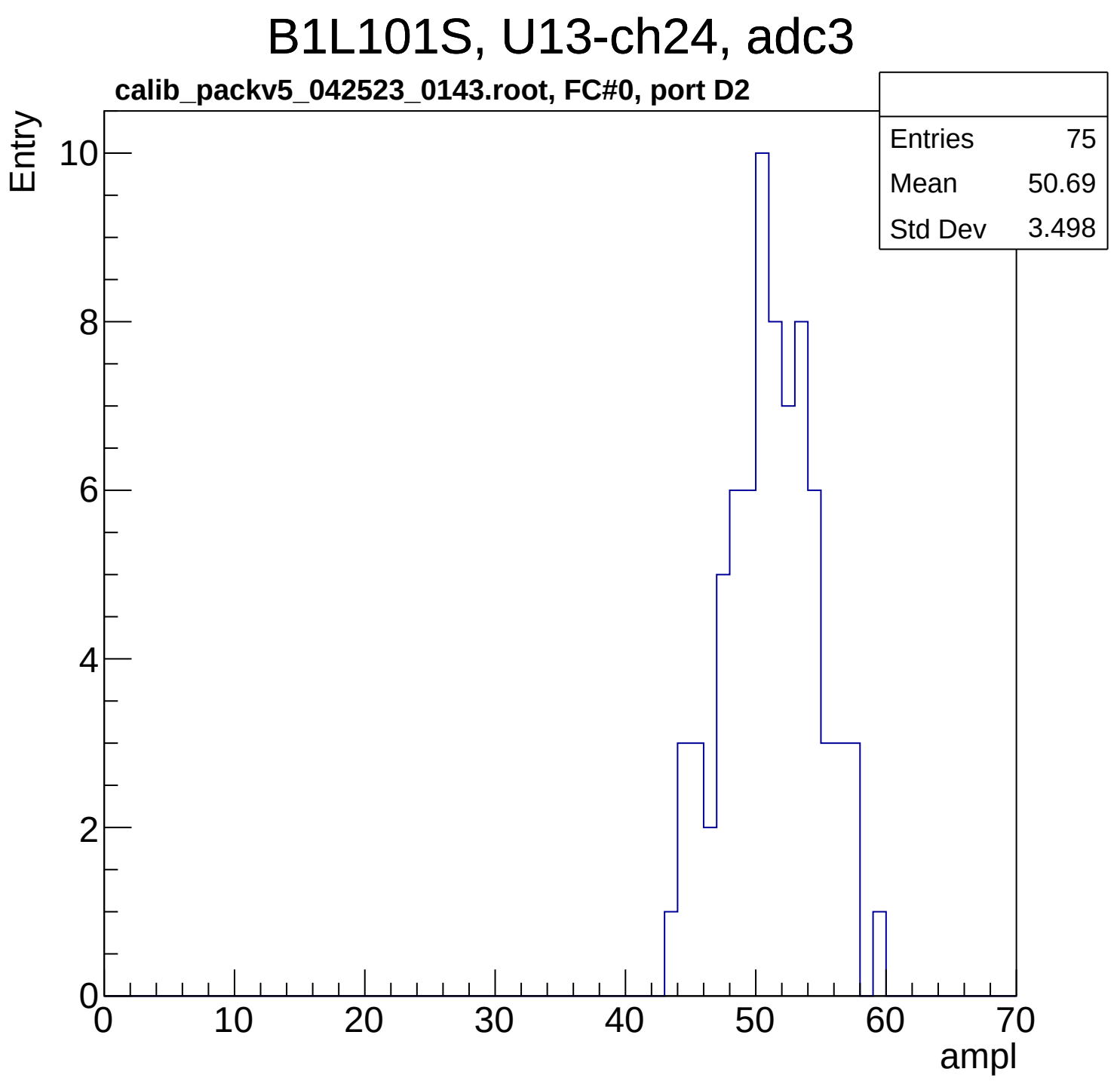
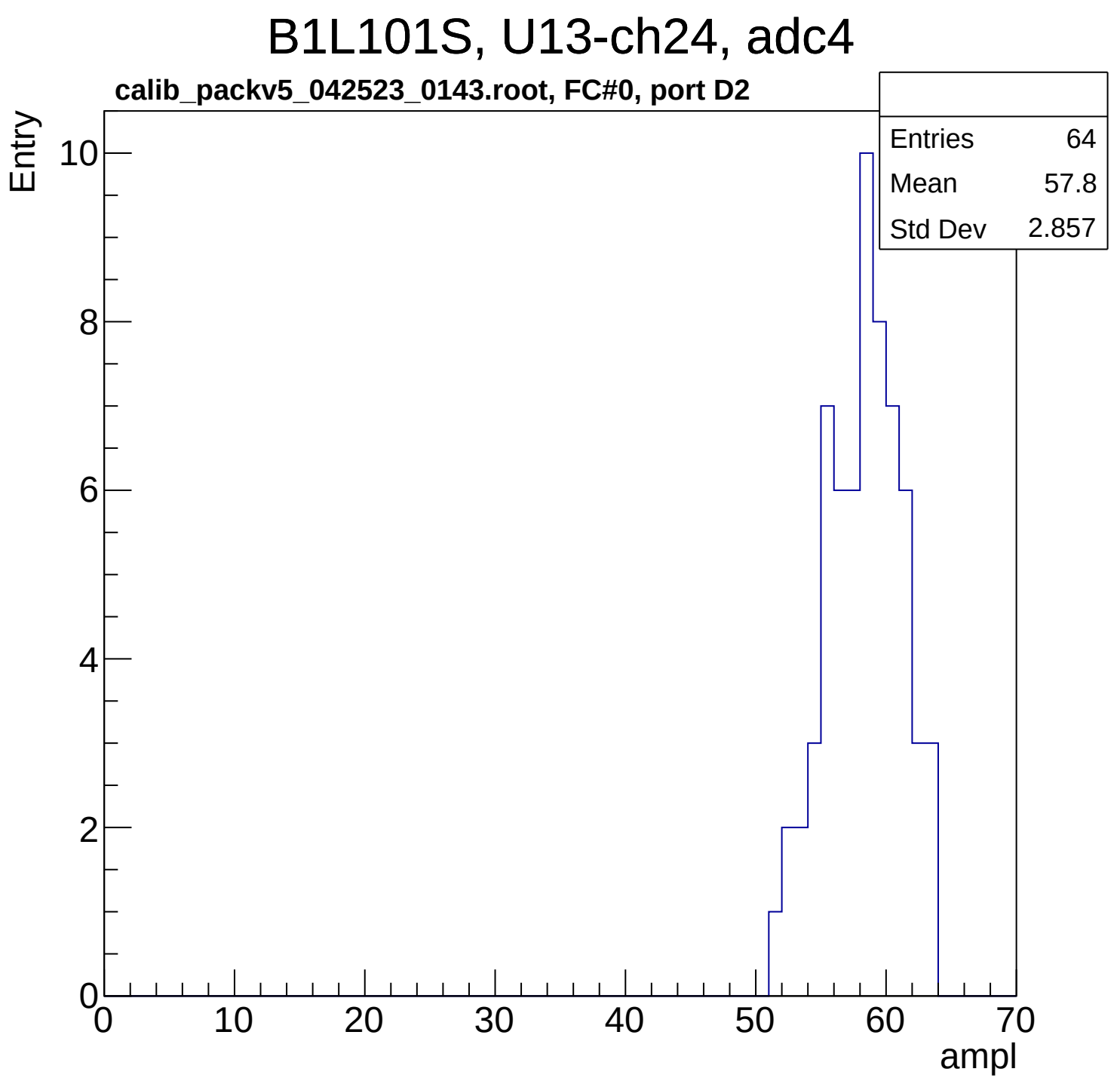

B1L101S, U13-ch24, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 50.69
Std Dev 3.498
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch24, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 57.8
Std Dev 2.857
Entry
0
2
4
6
8
10
0
10
20
30
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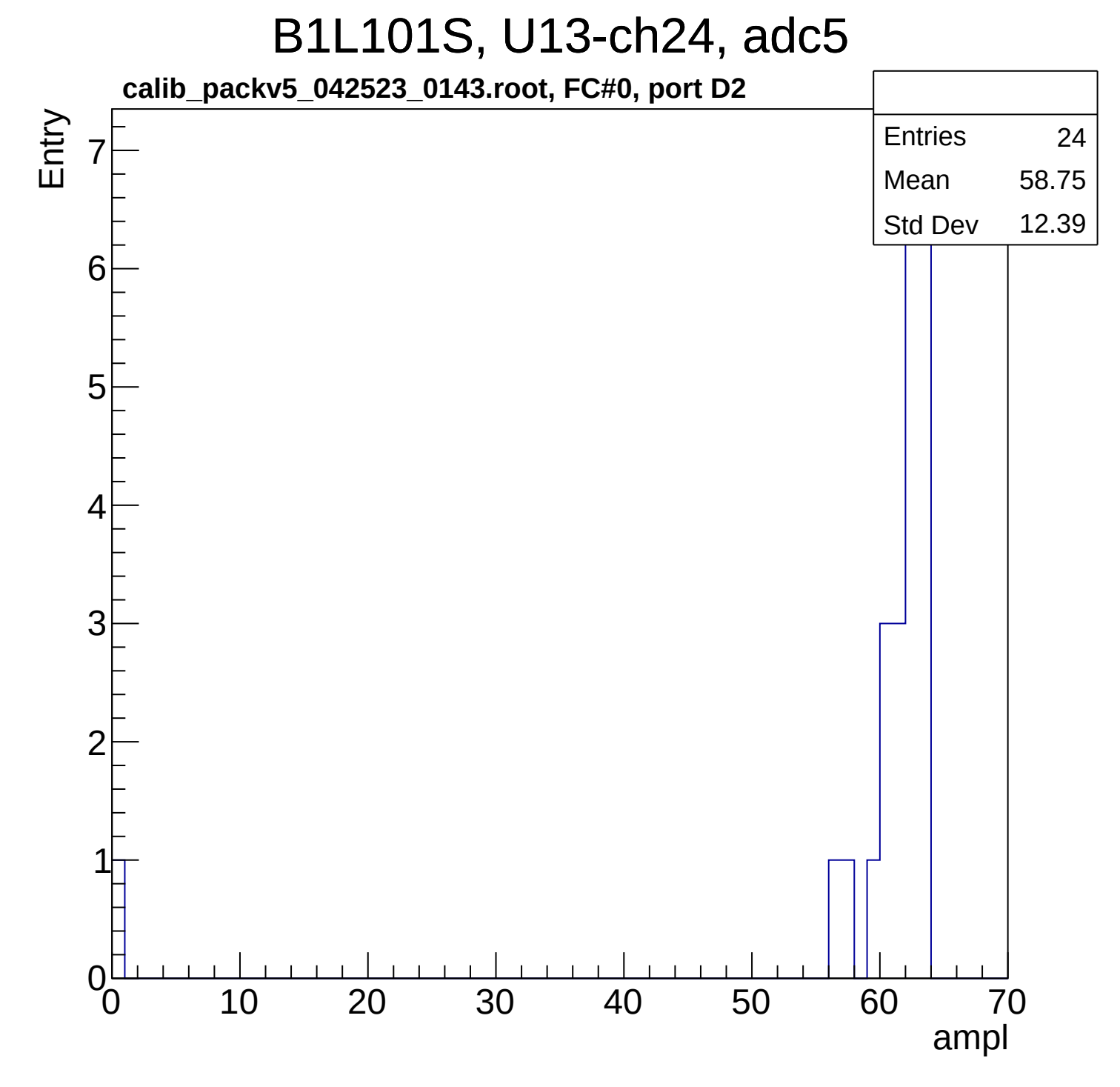
50
60
70
ampl


B1L101S, U13-ch24, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 24
Mean 58.75
Std Dev 12.39
Entry
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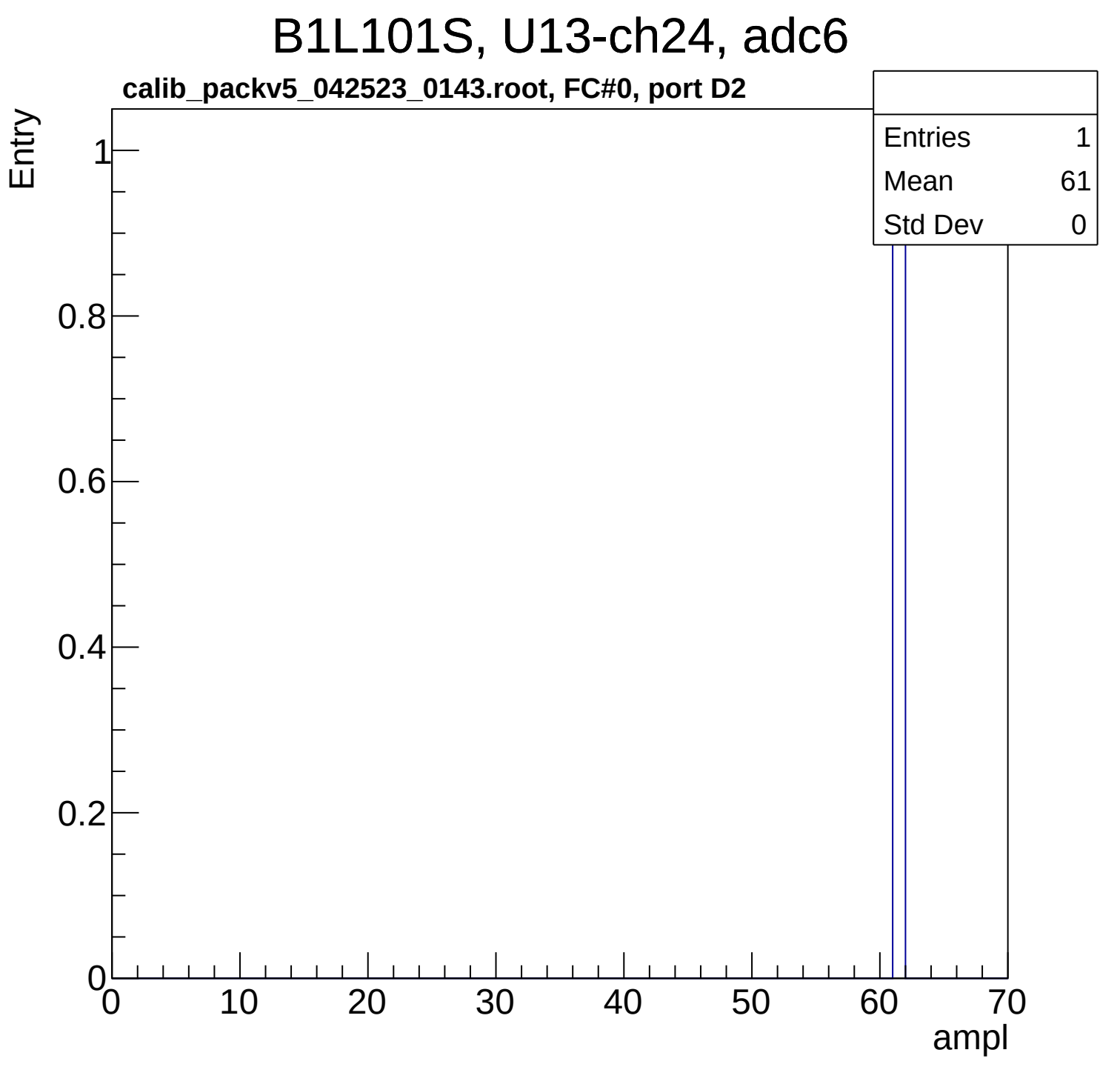
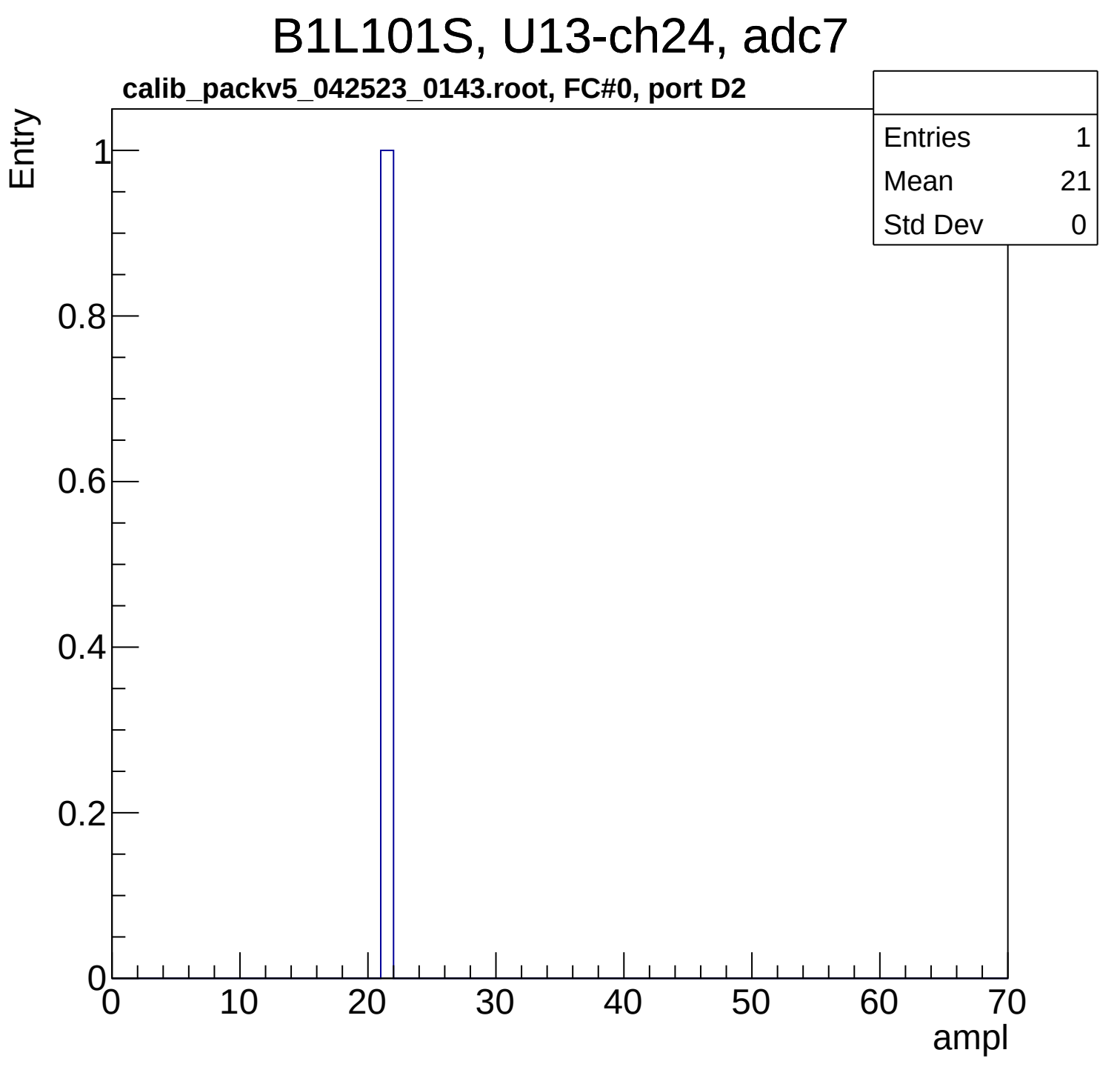
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch24, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch24, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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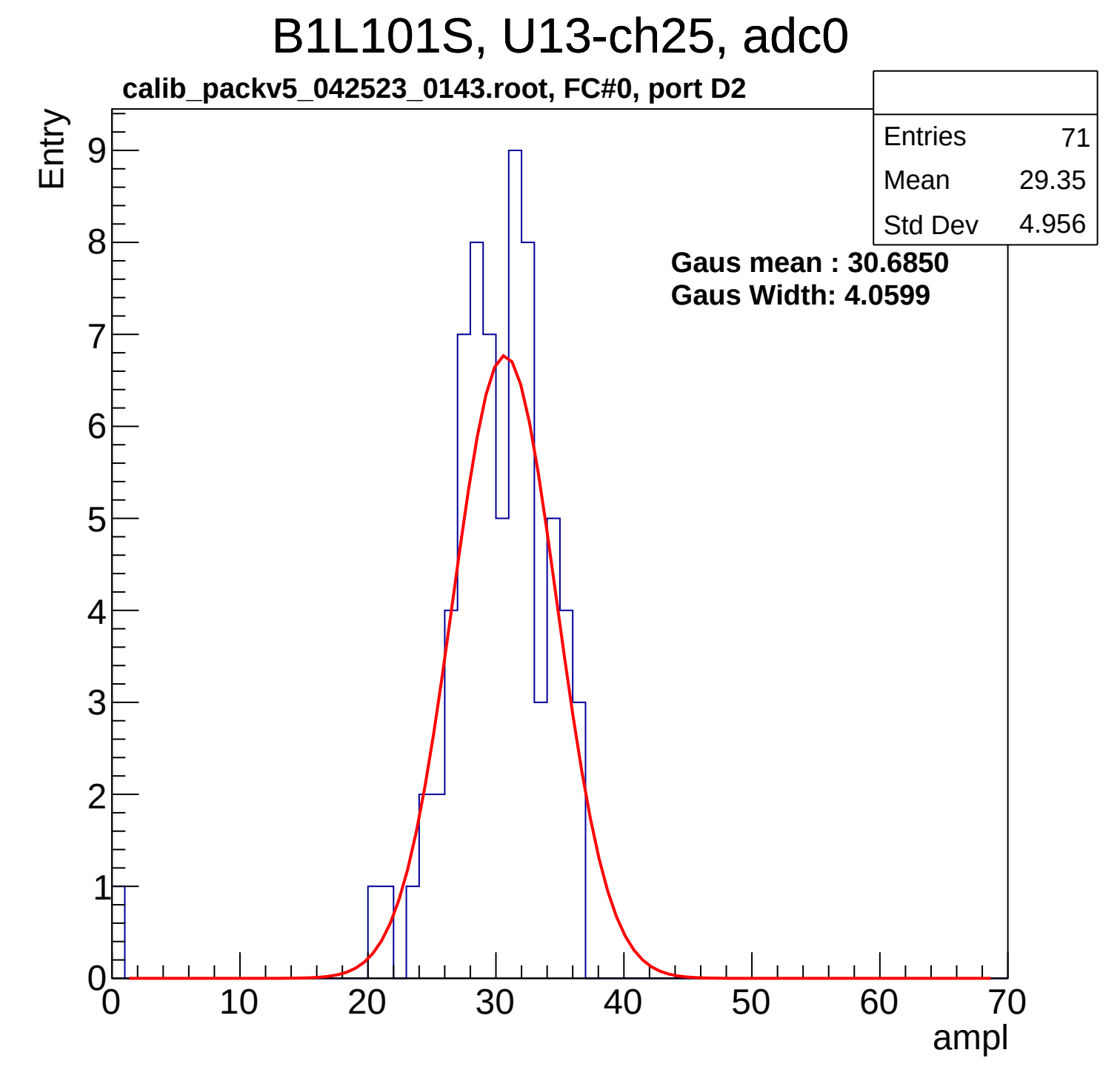
30
40
50
60
70
ampl


B1L101S, U13-ch25, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 29.35
Std Dev 4.956
Gaus mean : 30.6850
Gaus Width: 4.0599
Entry
ampl
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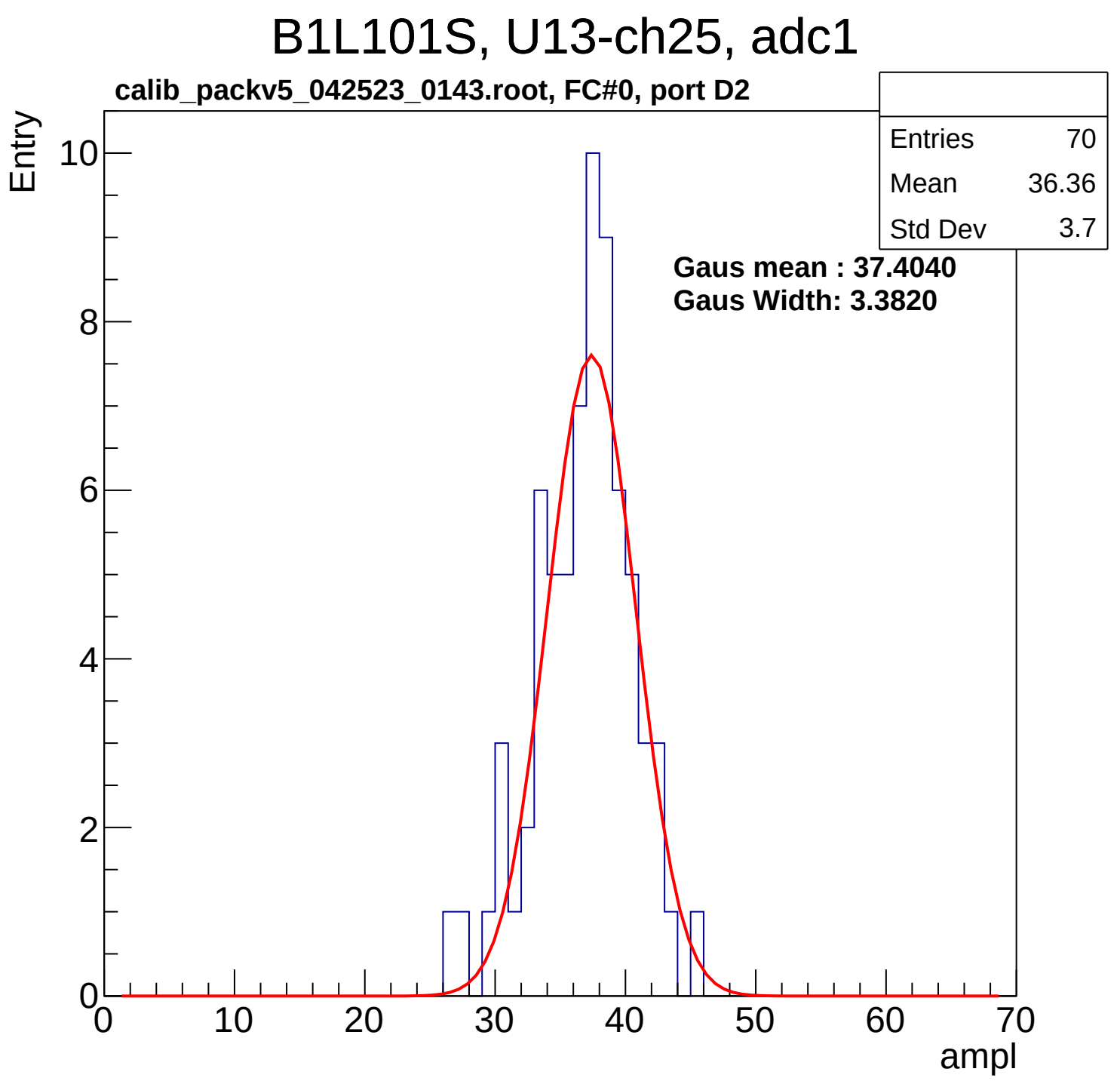
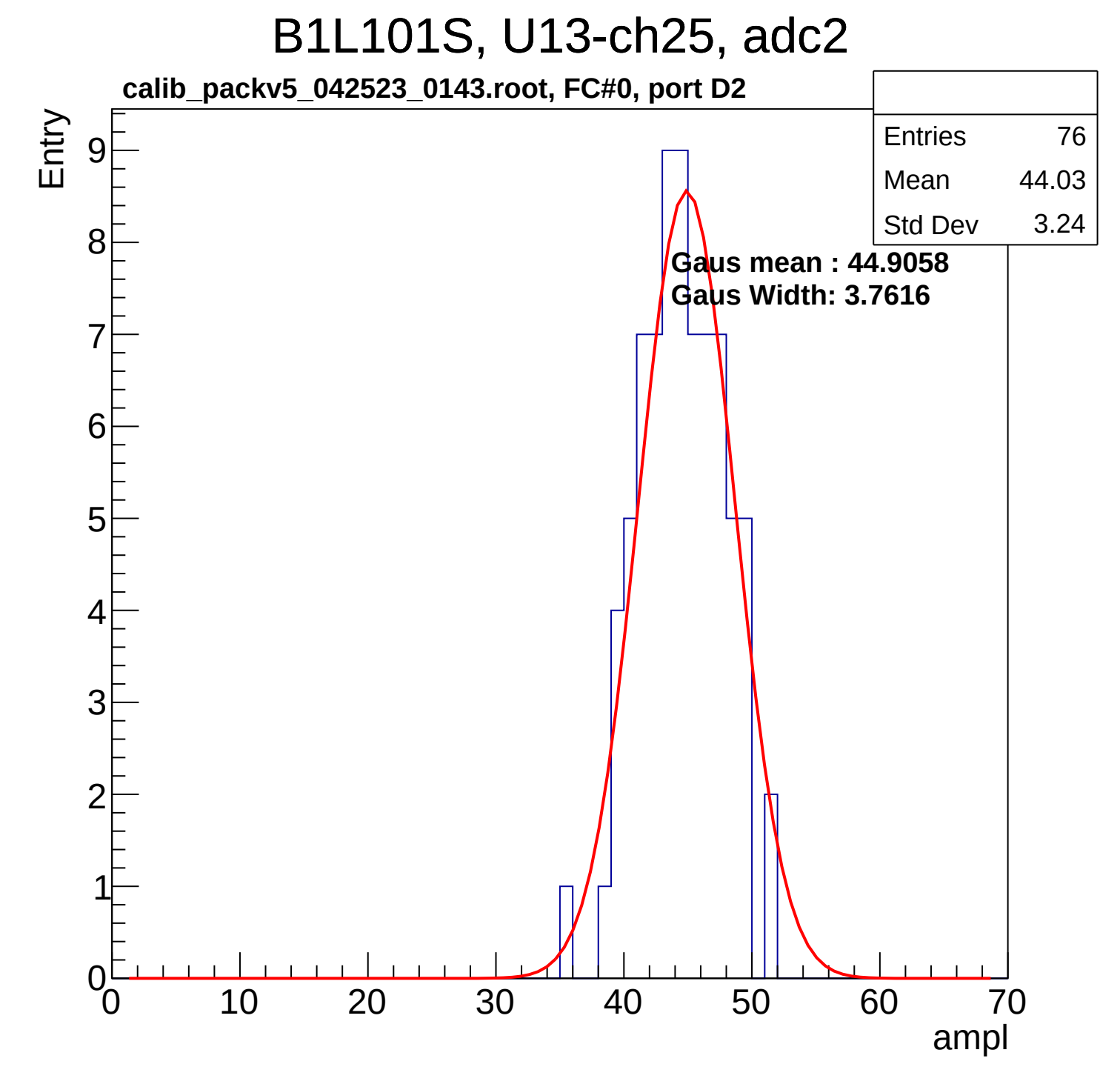
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch25, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 36.36
Std Dev 3.7
Gaus mean : 37.4040
Gaus Width: 3.3820
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch25, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 44.03
Std Dev 3.24
Gaus mean : 44.9058
Gaus Width: 3.7616
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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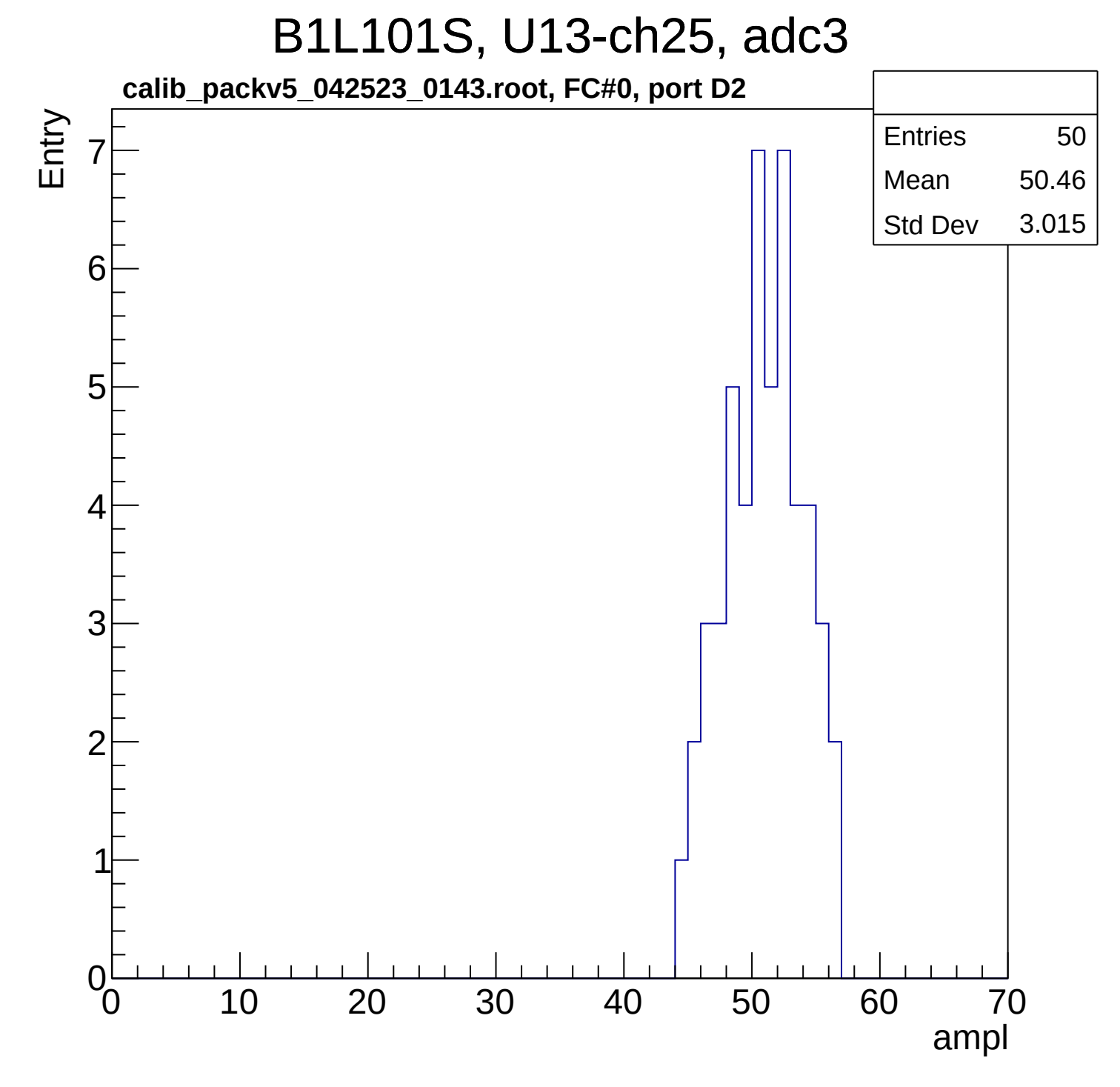
B1L101S, U13-ch25, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 50
Mean 50.46
Std Dev 3.015
Entry
ampl
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4
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0
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60
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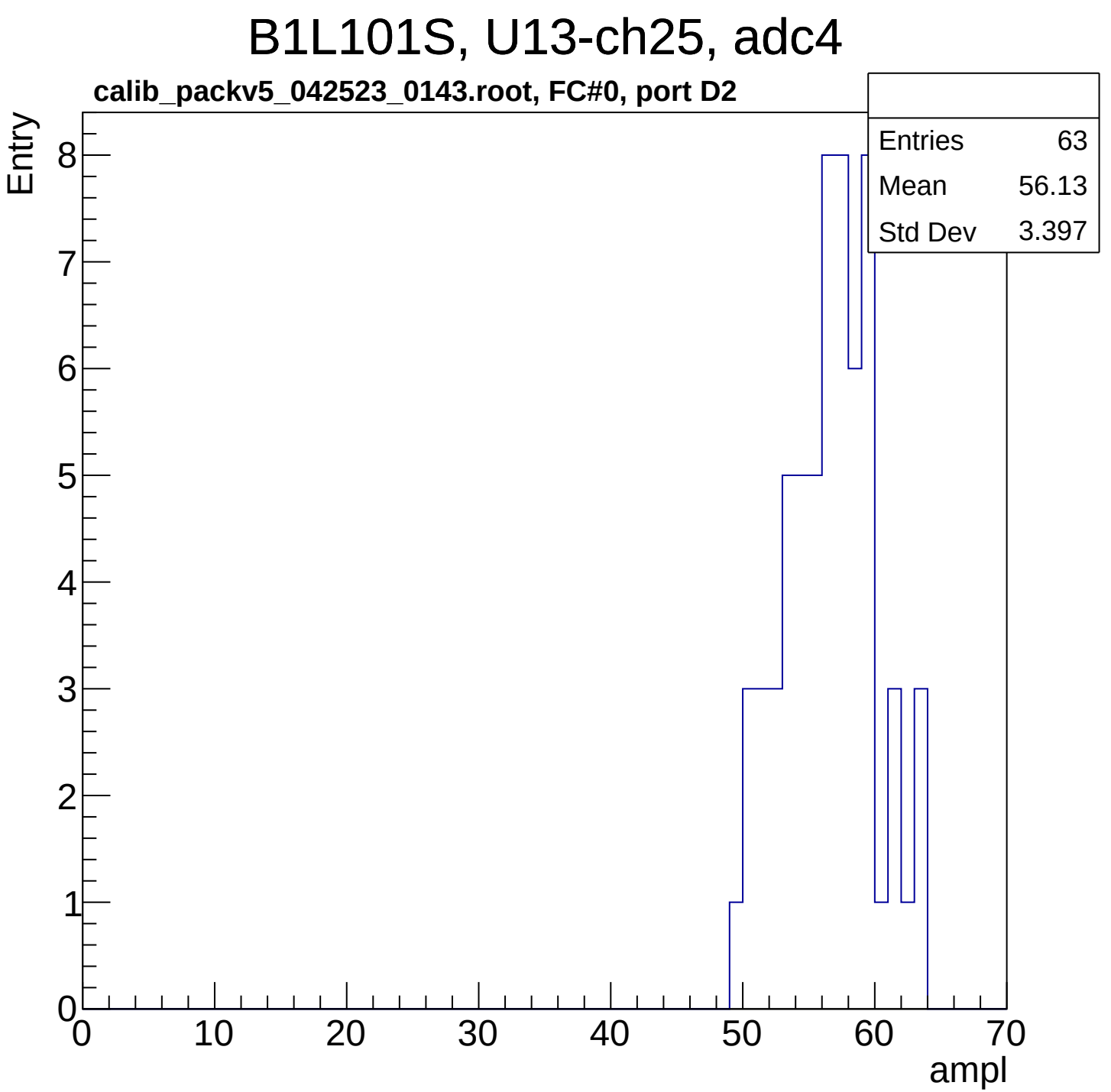
B1L101S, U13-ch25, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 56.13
Std Dev 3.397
Entry
ampl
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0
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60
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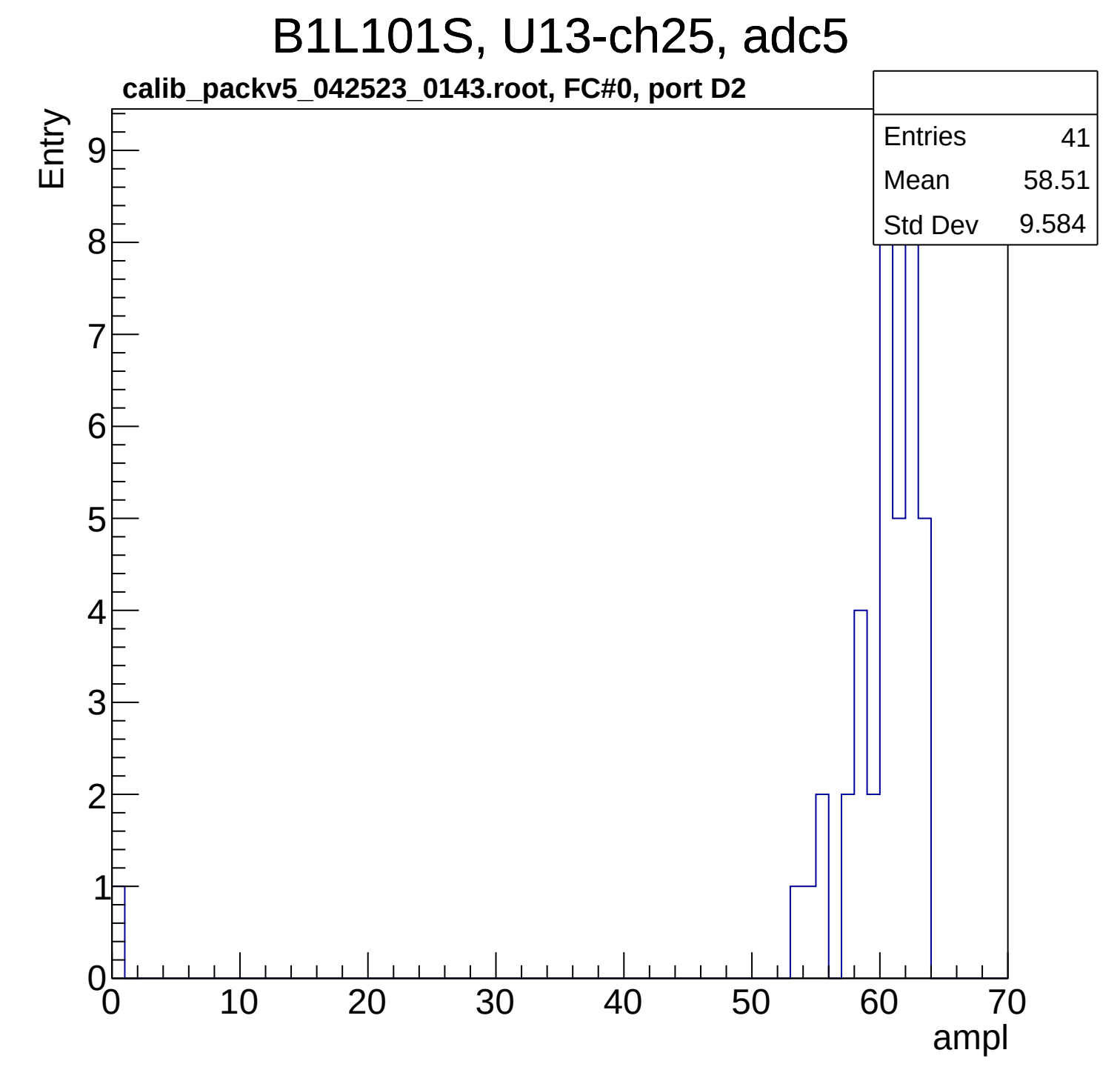
B1L101S, U13-ch25, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 58.51
Std Dev 9.584
Entry
ampl
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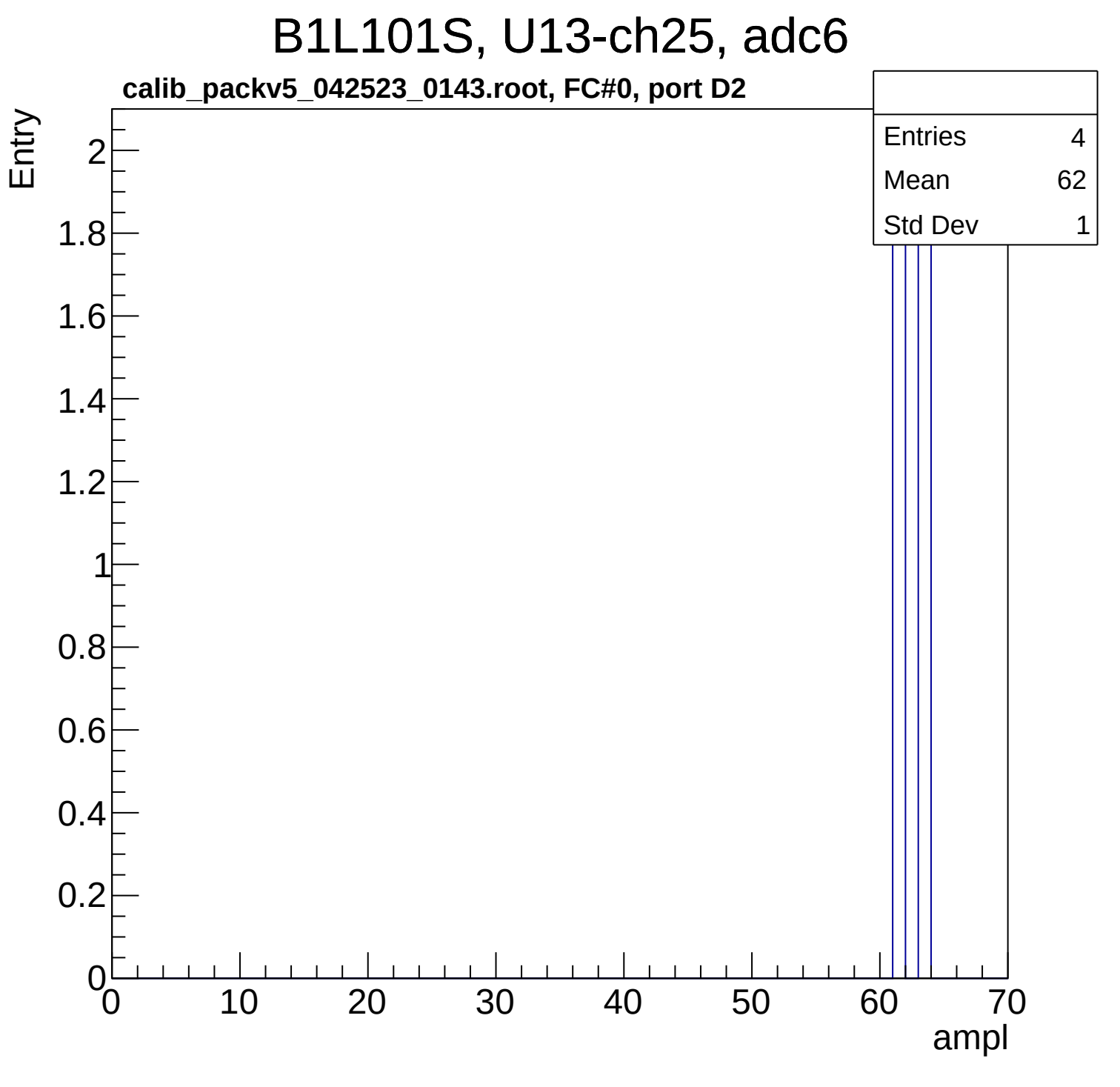
B1L101S, U13-ch25, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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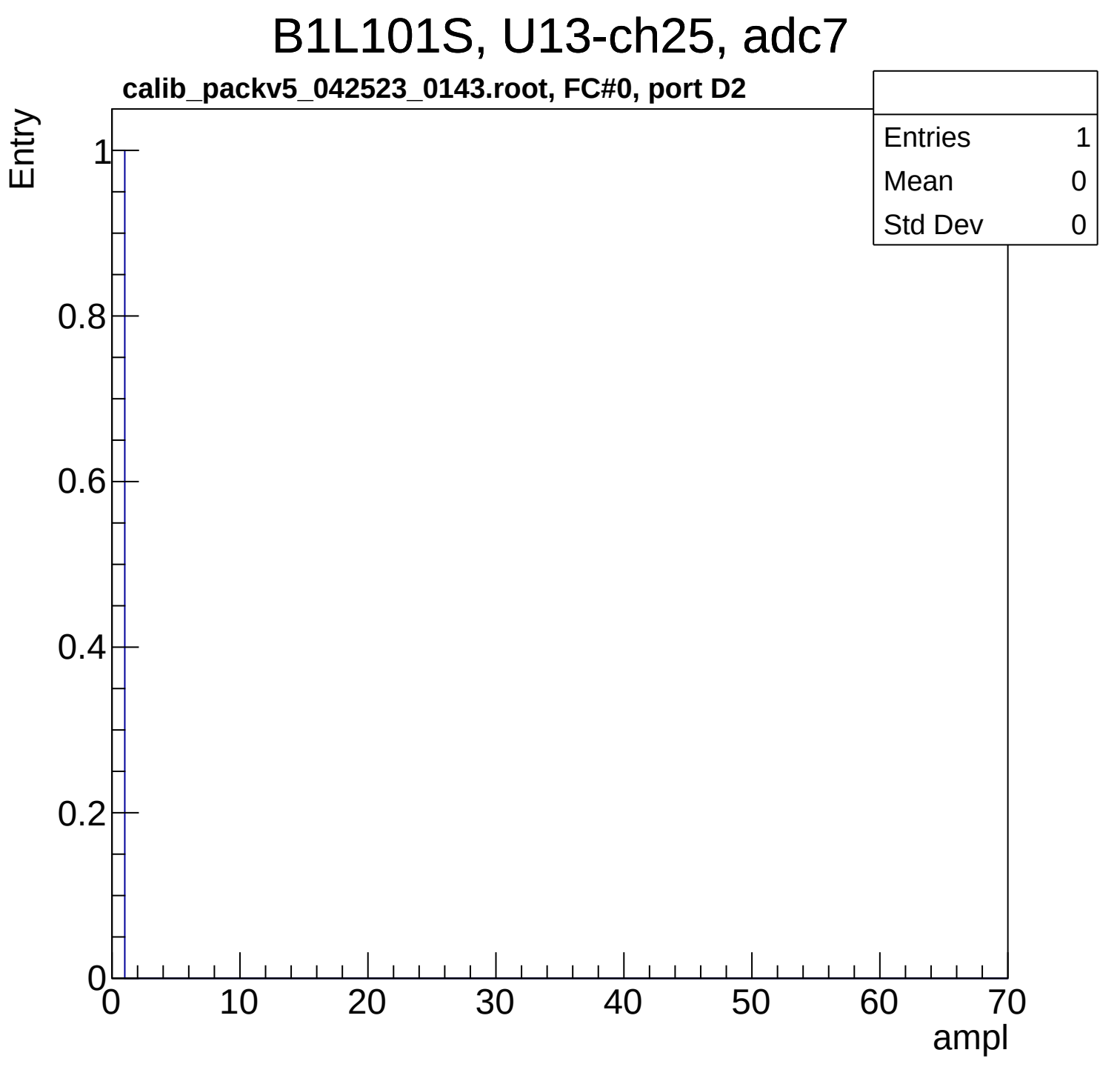
B1L101S, U13-ch25, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
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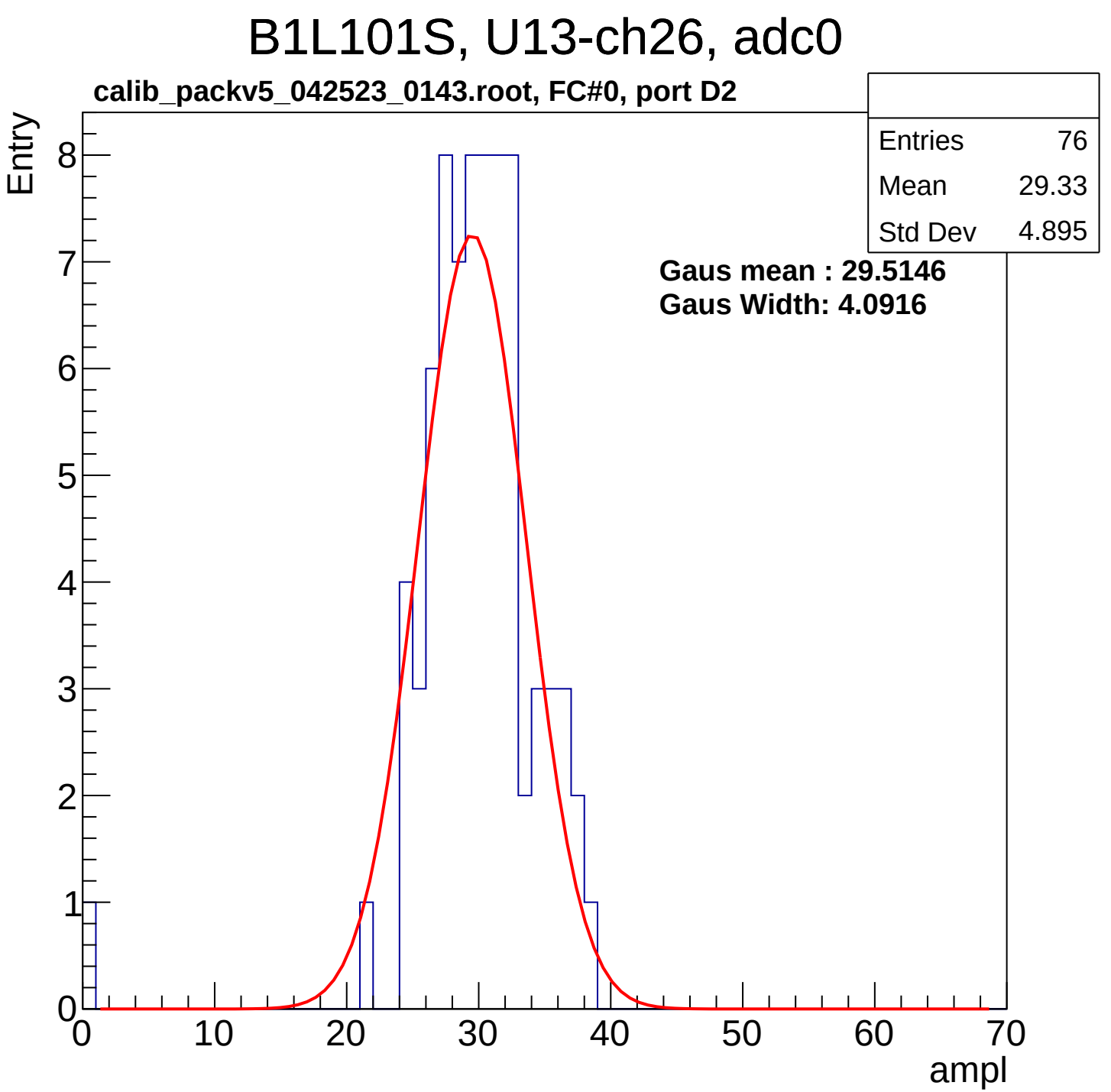
30
40
50
60
70
ampl


B1L101S, U13-ch26, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 29.33
Std Dev 4.895
Gaus mean : 29.5146
Gaus Width: 4.0916
Entry
ampl
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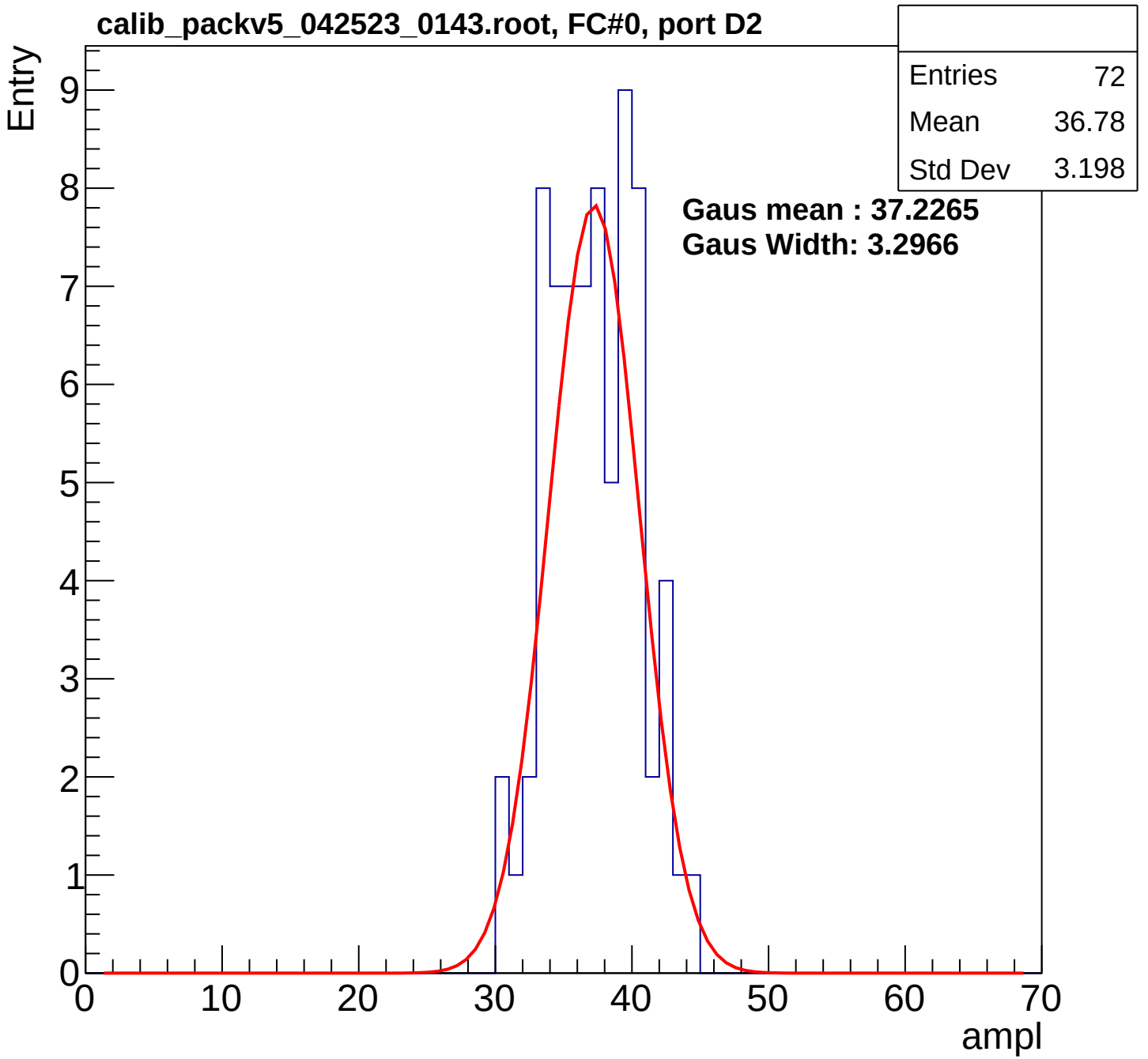
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch26, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 36.78
Std Dev 3.198
Gaus mean : 37.2265
Gaus Width: 3.2966
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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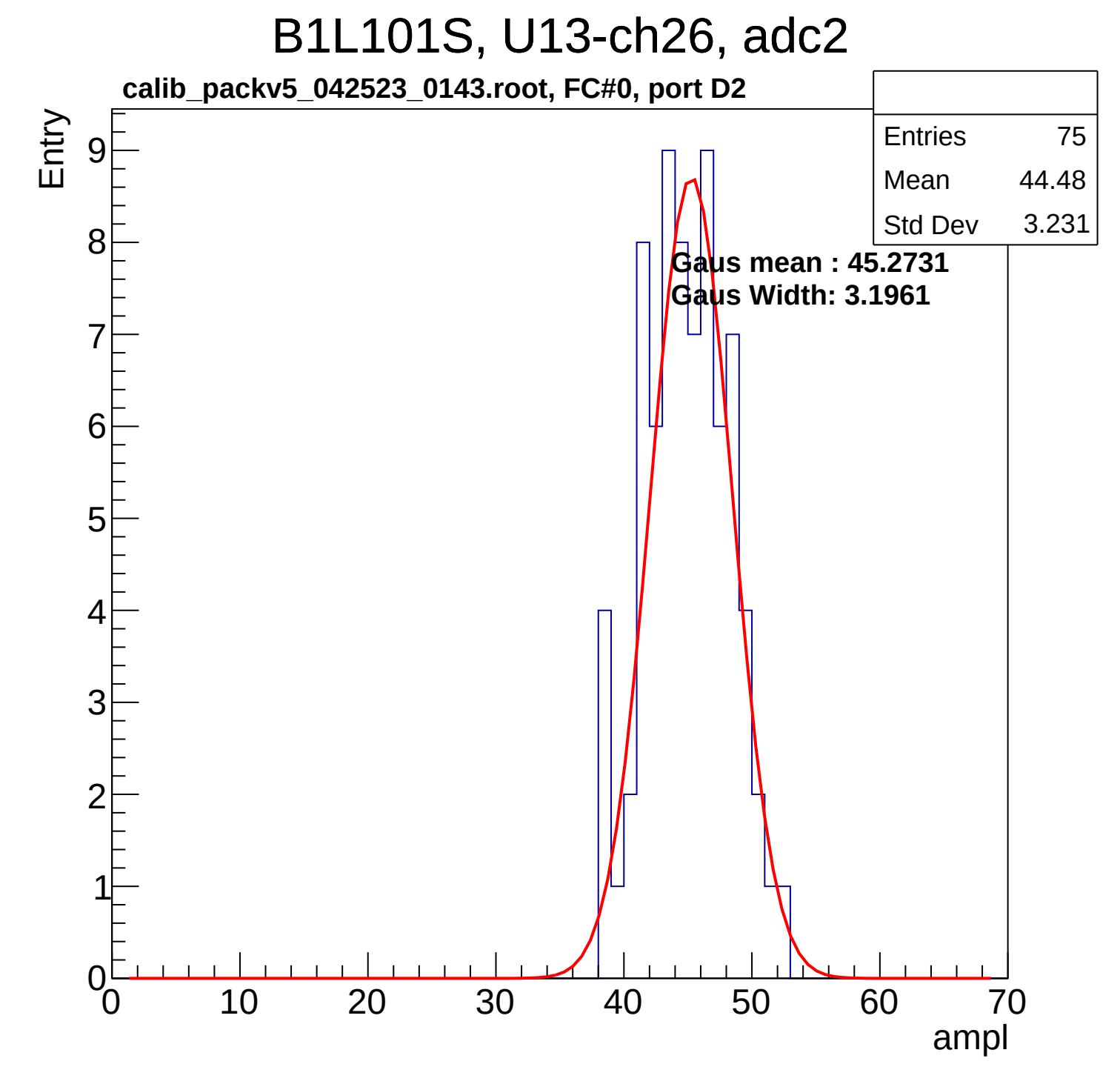
B1L101S, U13-ch26, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 44.48
Std Dev 3.231
Gaus mean : 45.2731
Gaus Width: 3.1961
Entry
ampl
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2
3
4
5
6
7
8
9
0
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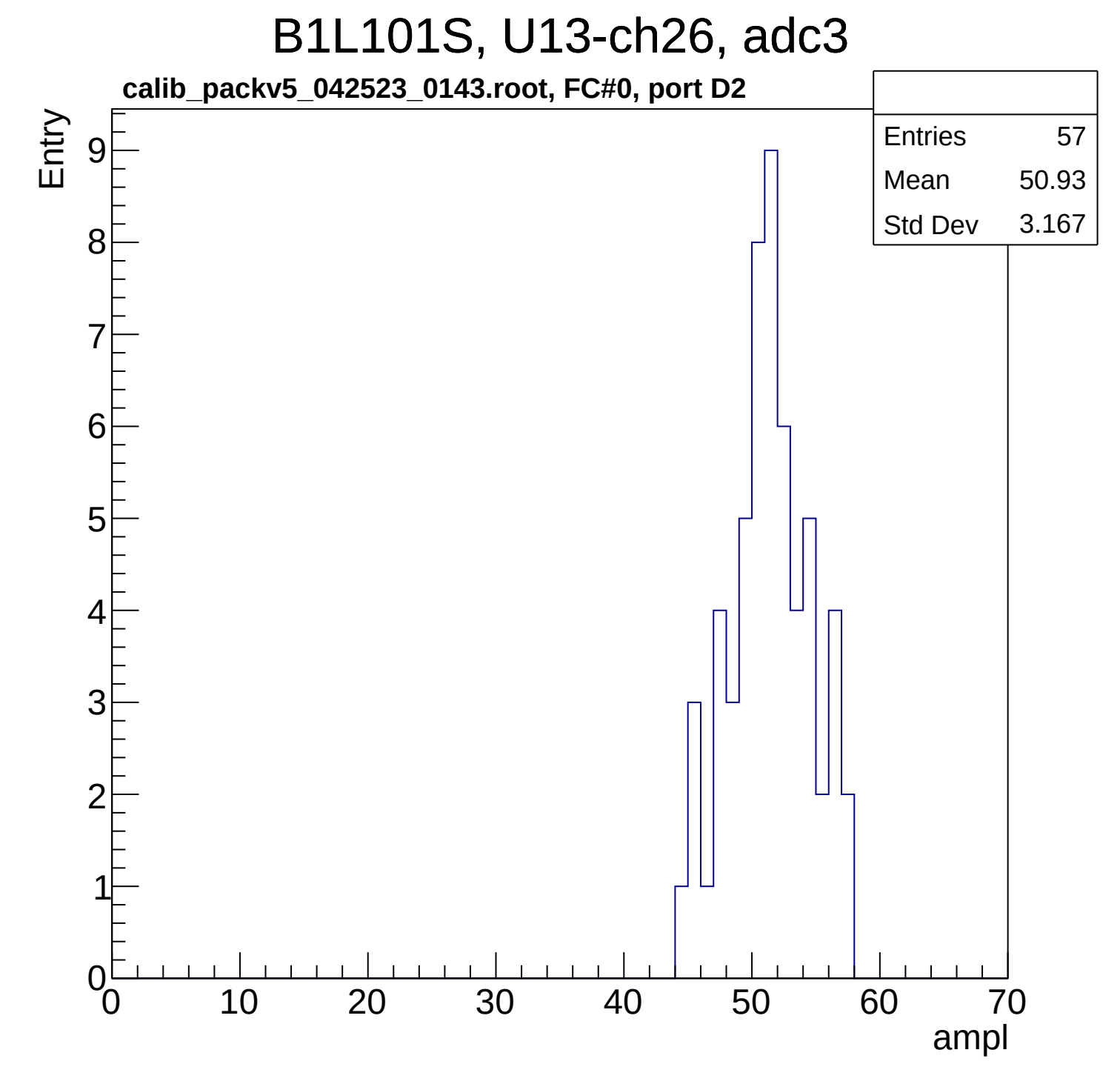
B1L101S, U13-ch26, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 50.93
Std Dev 3.167
Entry
ampl
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3
4
5
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0
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60
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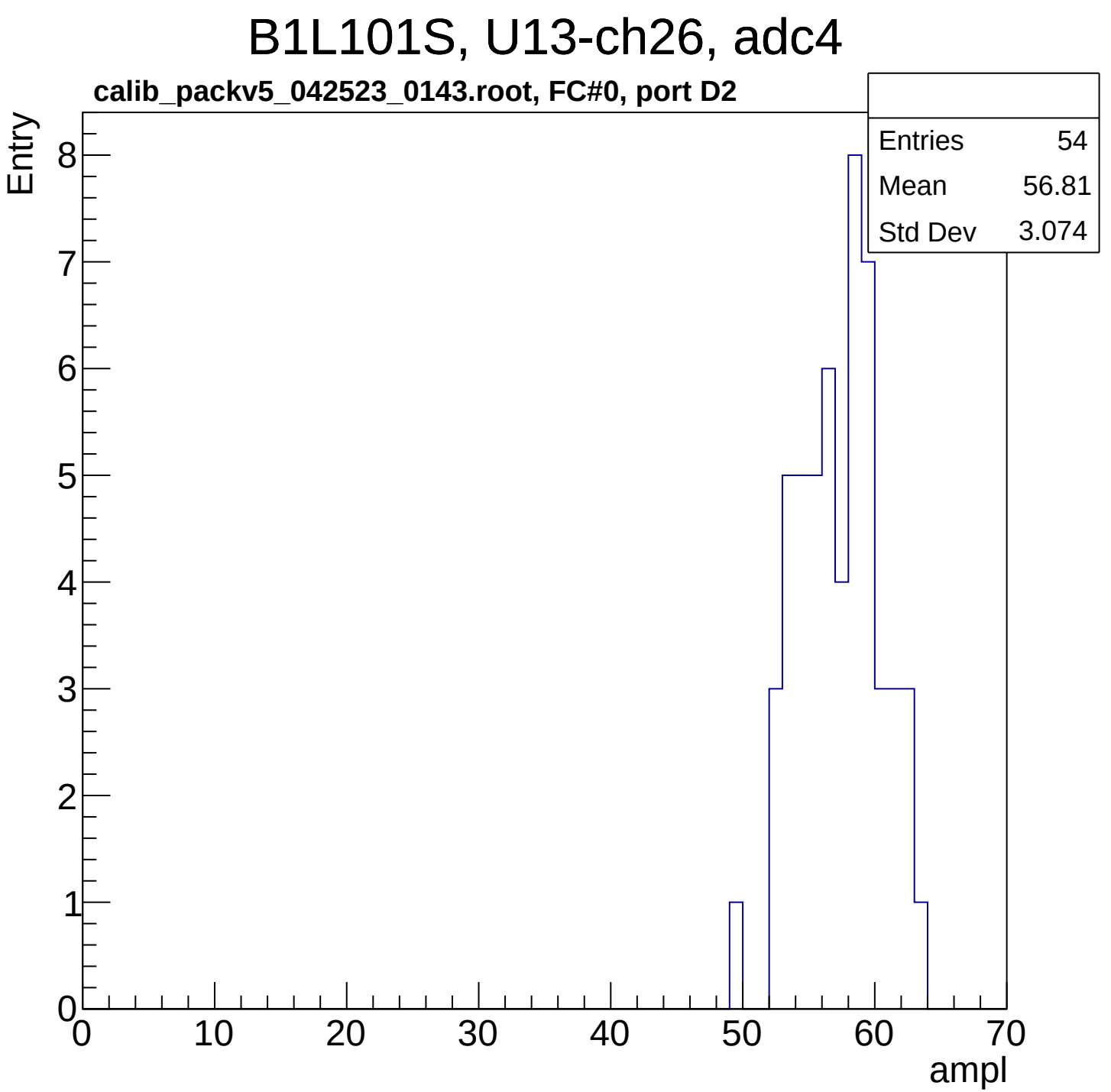
B1L101S, U13-ch26, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 56.81
Std Dev 3.074
Entry
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2
3
4
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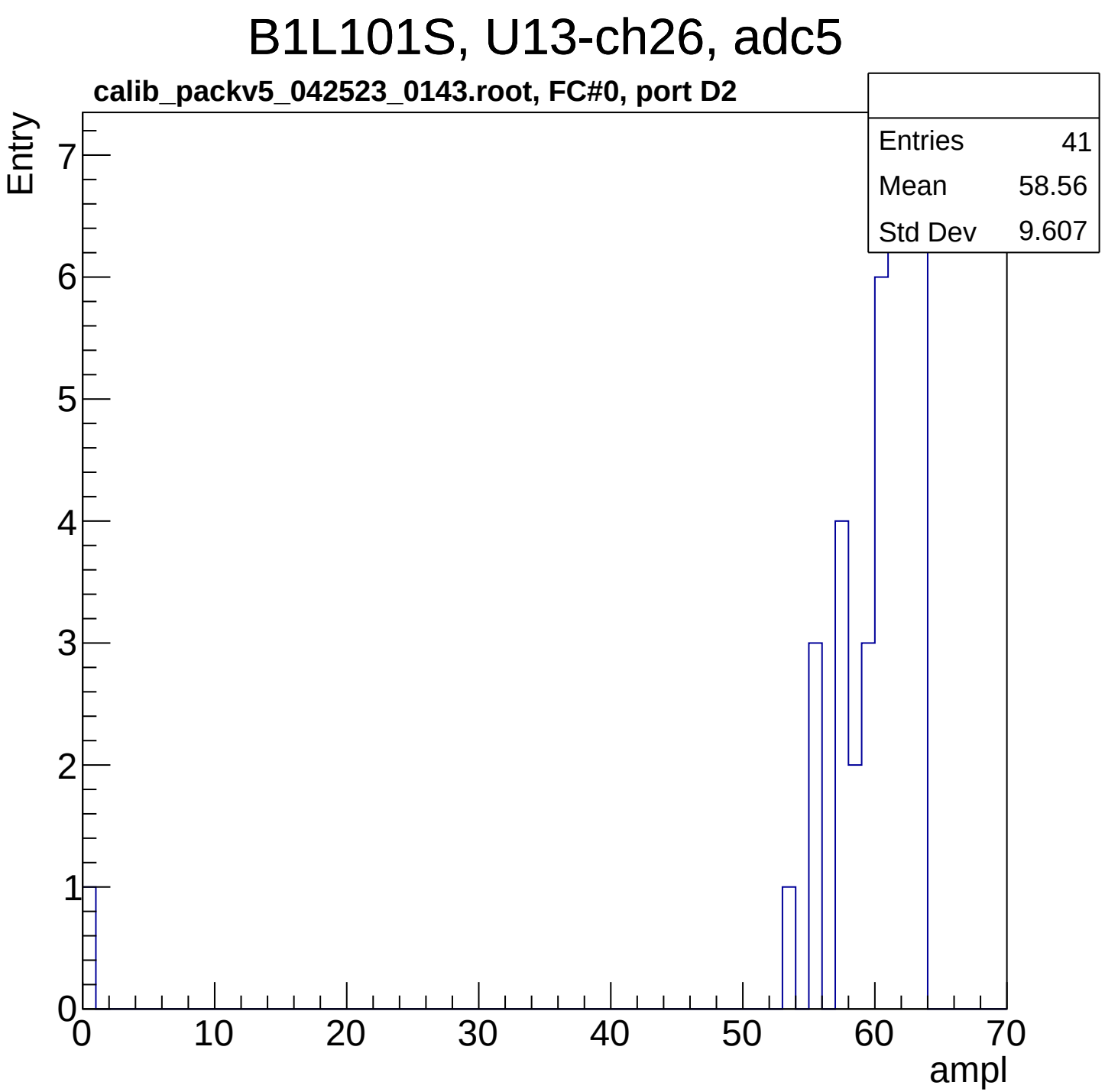
7
8
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch26, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 58.56
Std Dev 9.607
Entry
ampl
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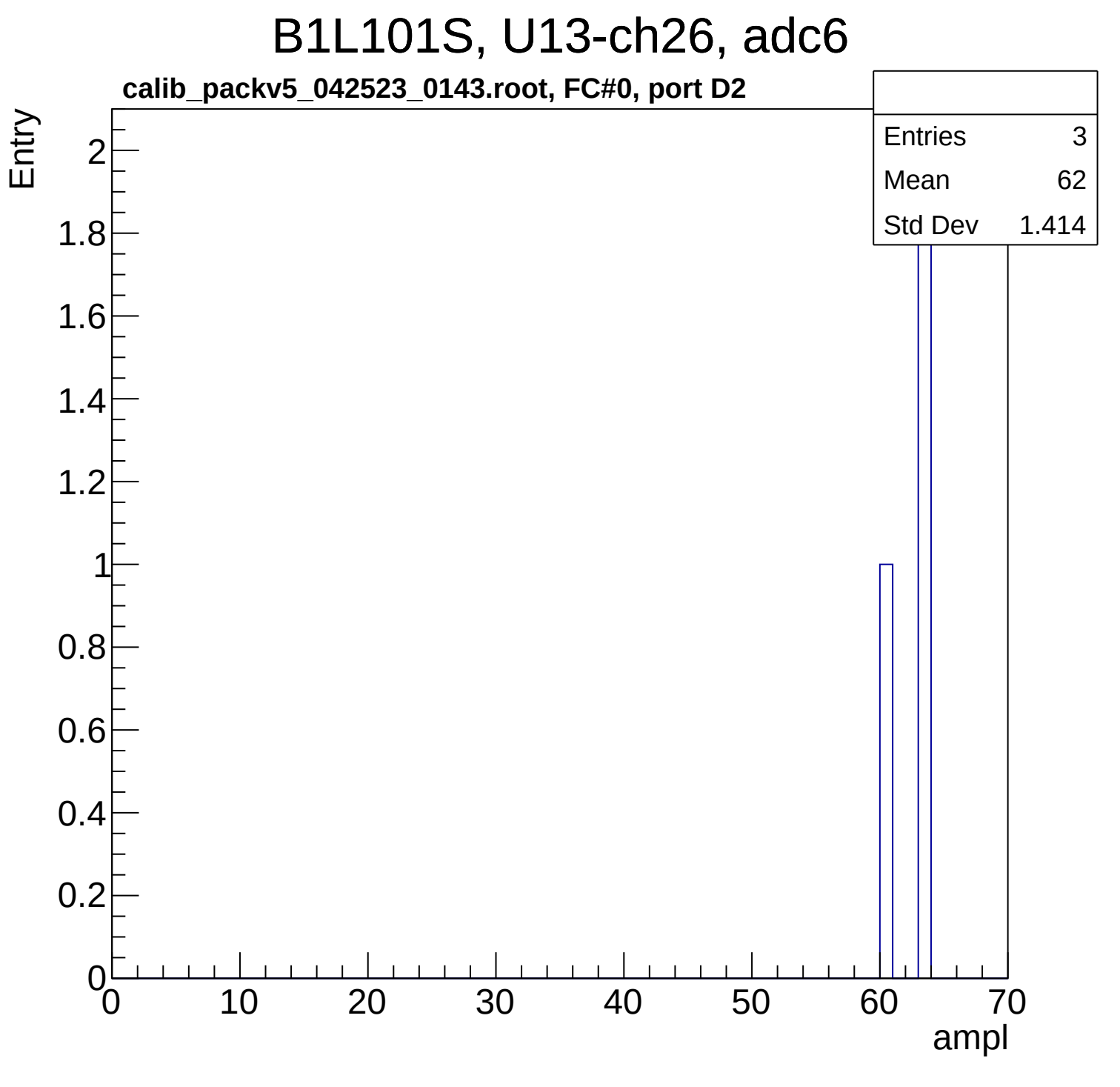
B1L101S, U13-ch26, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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40
50
60
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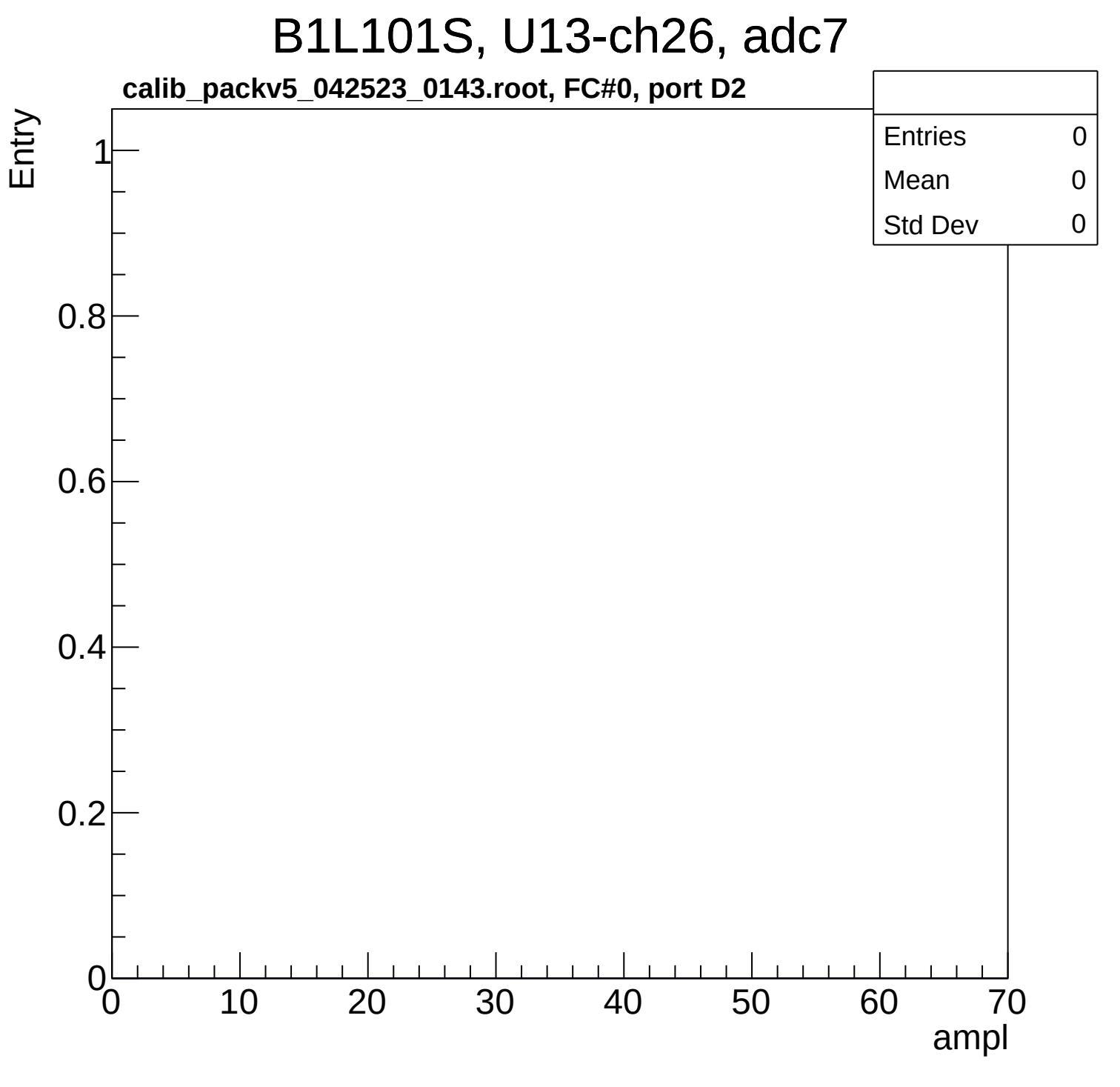
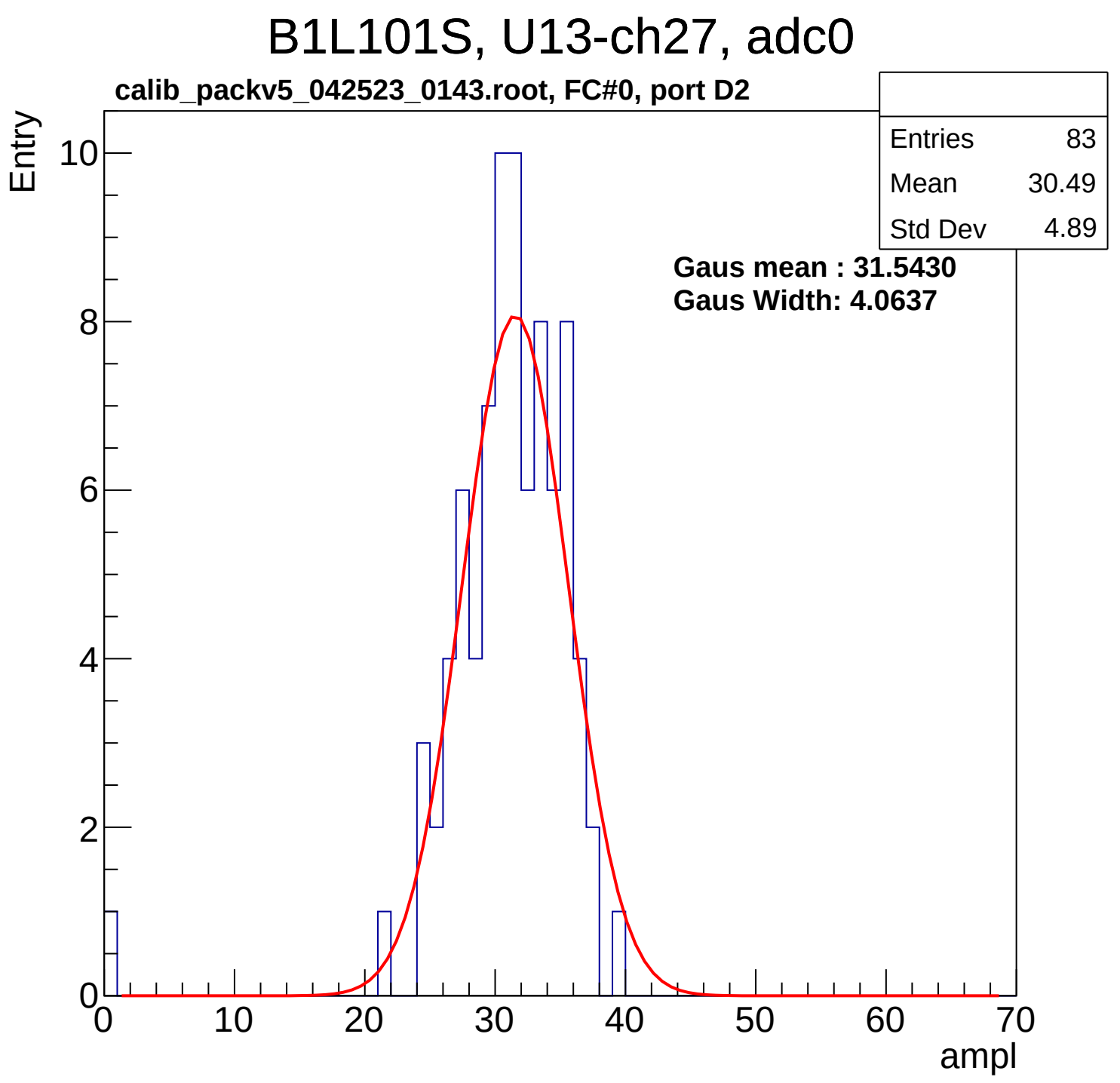
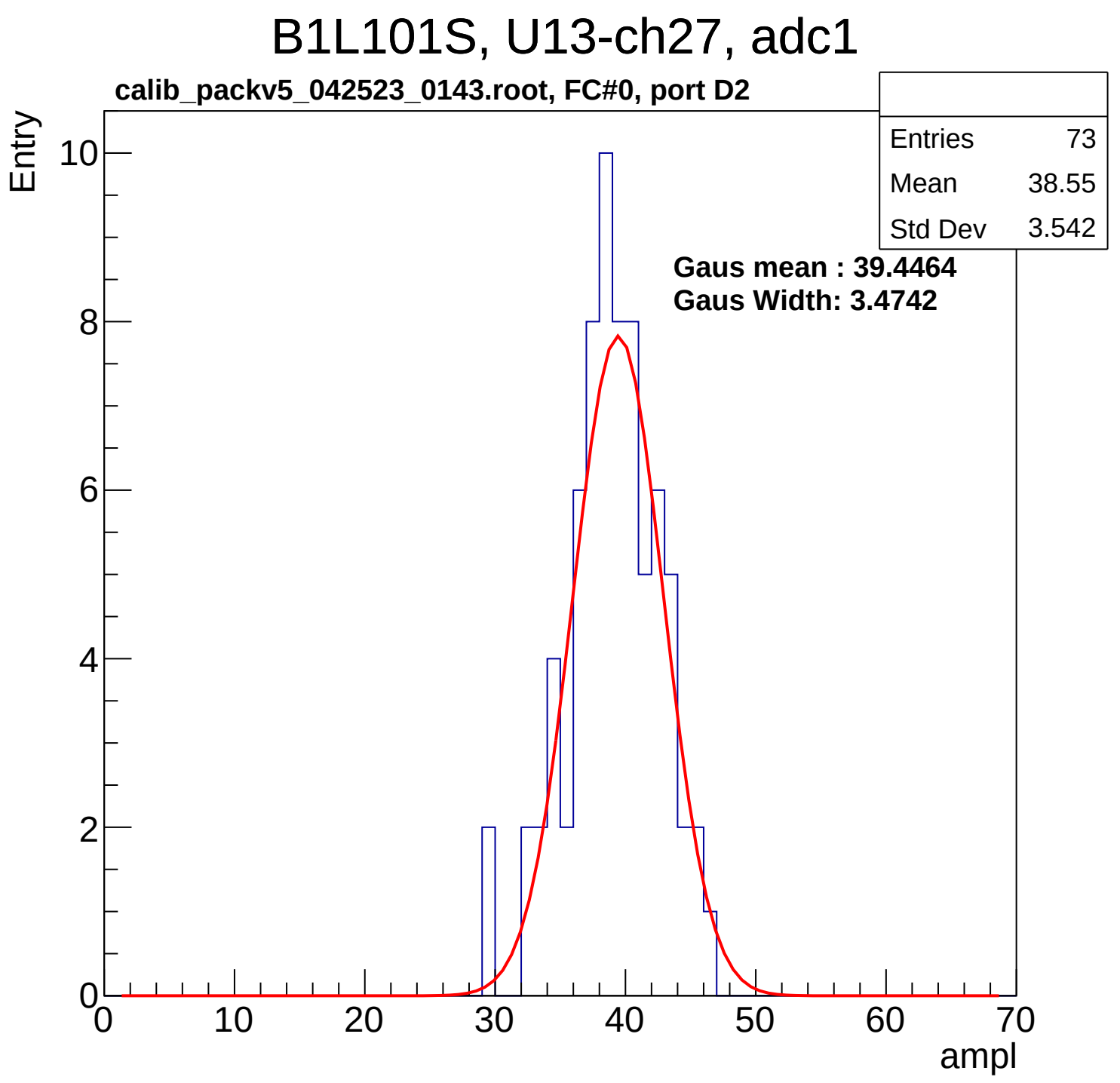

B1L101S, U13-ch26, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch27, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 83
Mean 30.49
Std Dev 4.89
Gaus mean : 31.5430
Gaus Width: 4.0637
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch27, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 38.55
Std Dev 3.542
Gaus mean : 39.4464
Gaus Width: 3.4742
Entry
ampl
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2
4
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8
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0
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60
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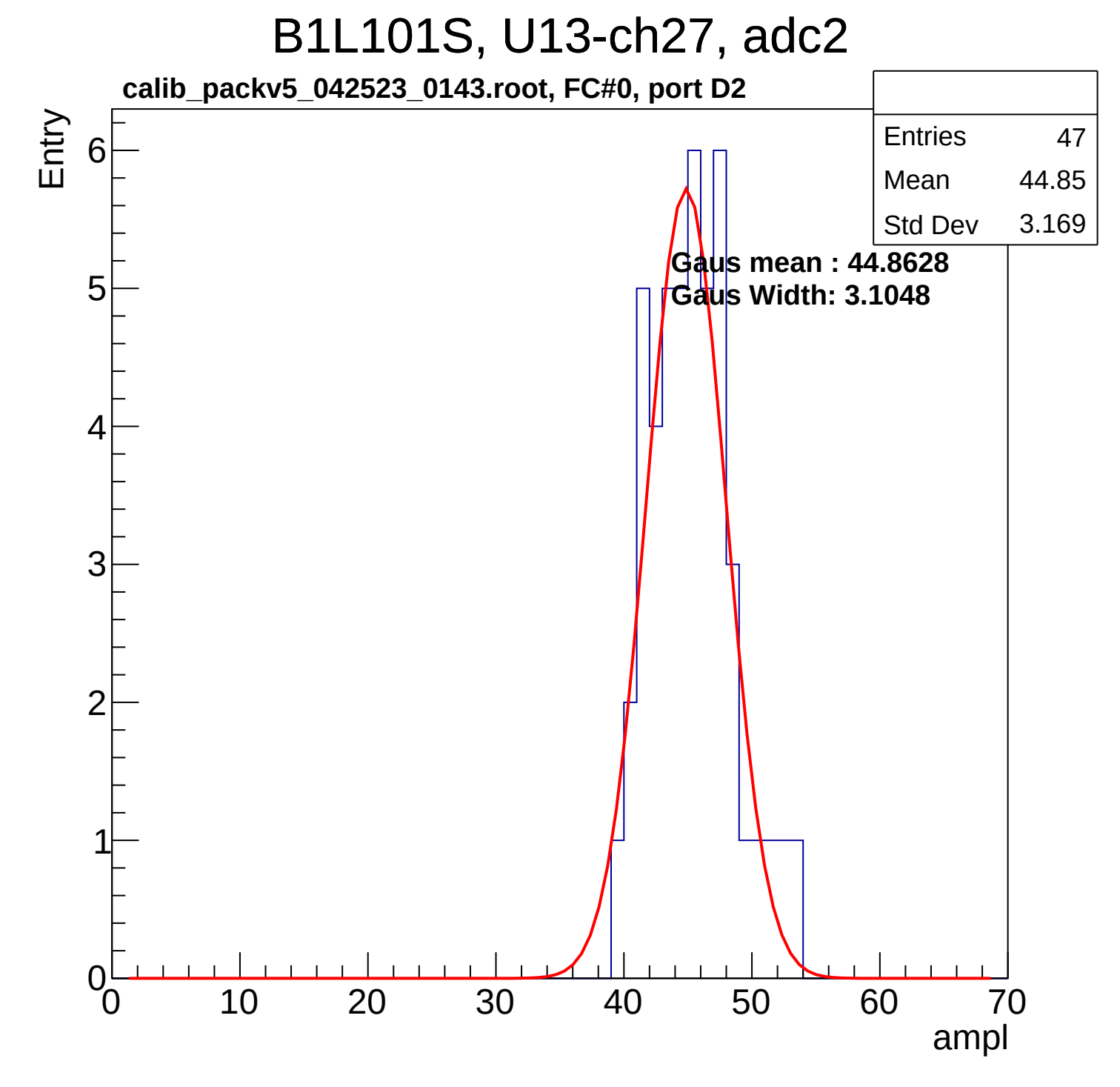
B1L101S, U13-ch27, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 47
Mean 44.85
Std Dev 3.169
Gaus mean : 44.8628
Gaus Width: 3.1048
Entry
ampl
0
1
2
3
4
5
6
0
10
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30
40
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60
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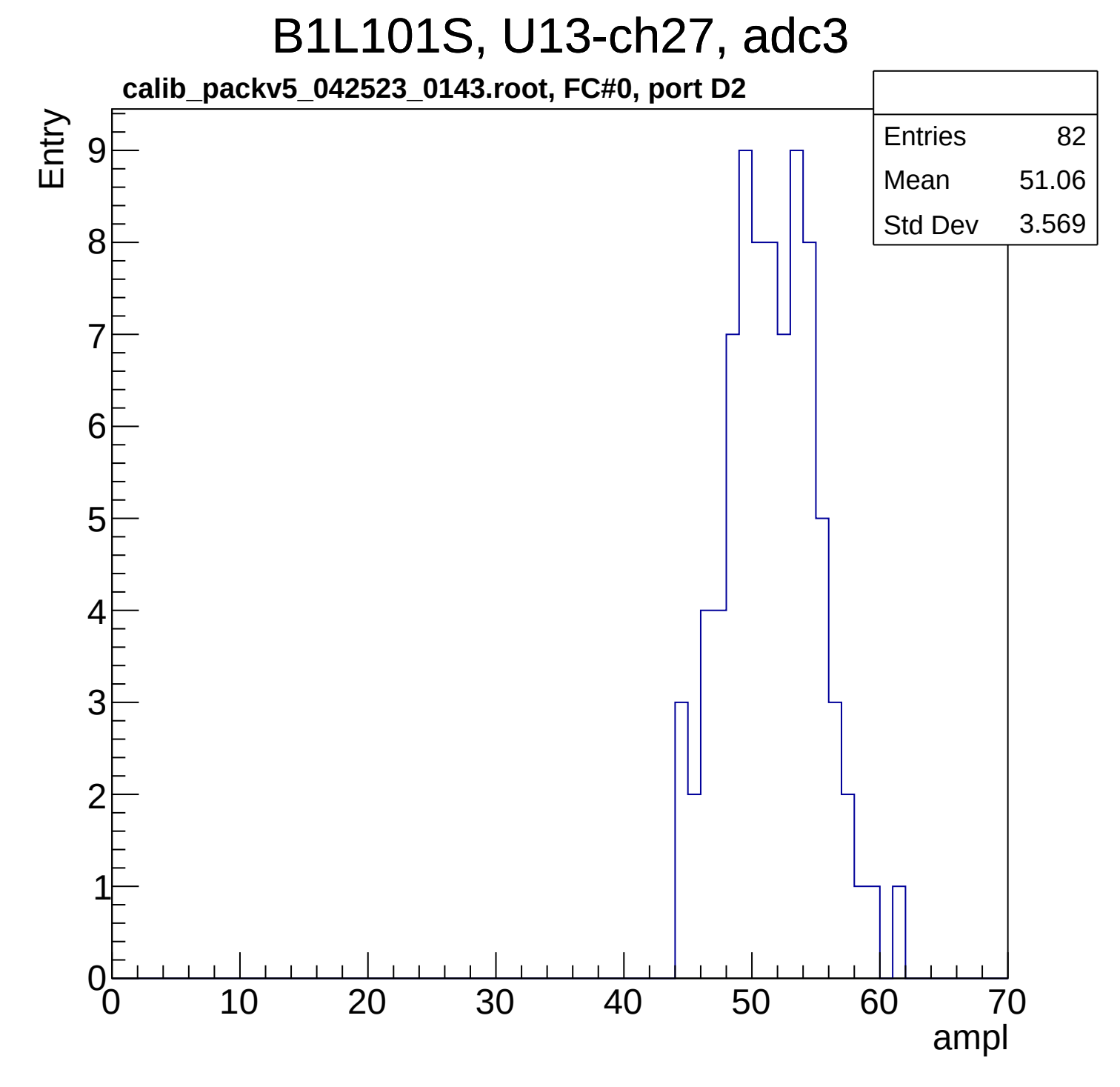
B1L101S, U13-ch27, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 82
Mean 51.06
Std Dev 3.569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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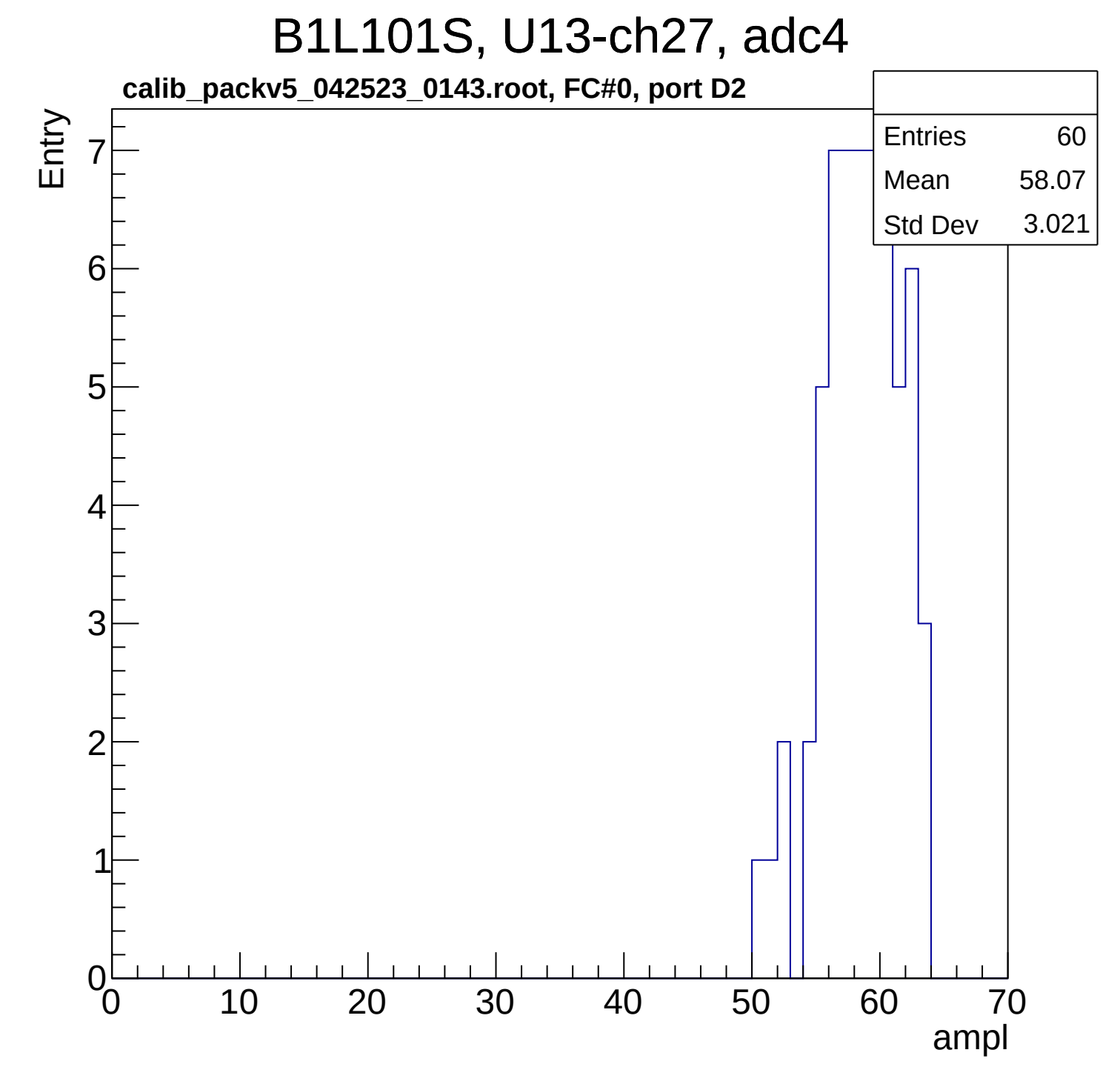
B1L101S, U13-ch27, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 58.07
Std Dev 3.021
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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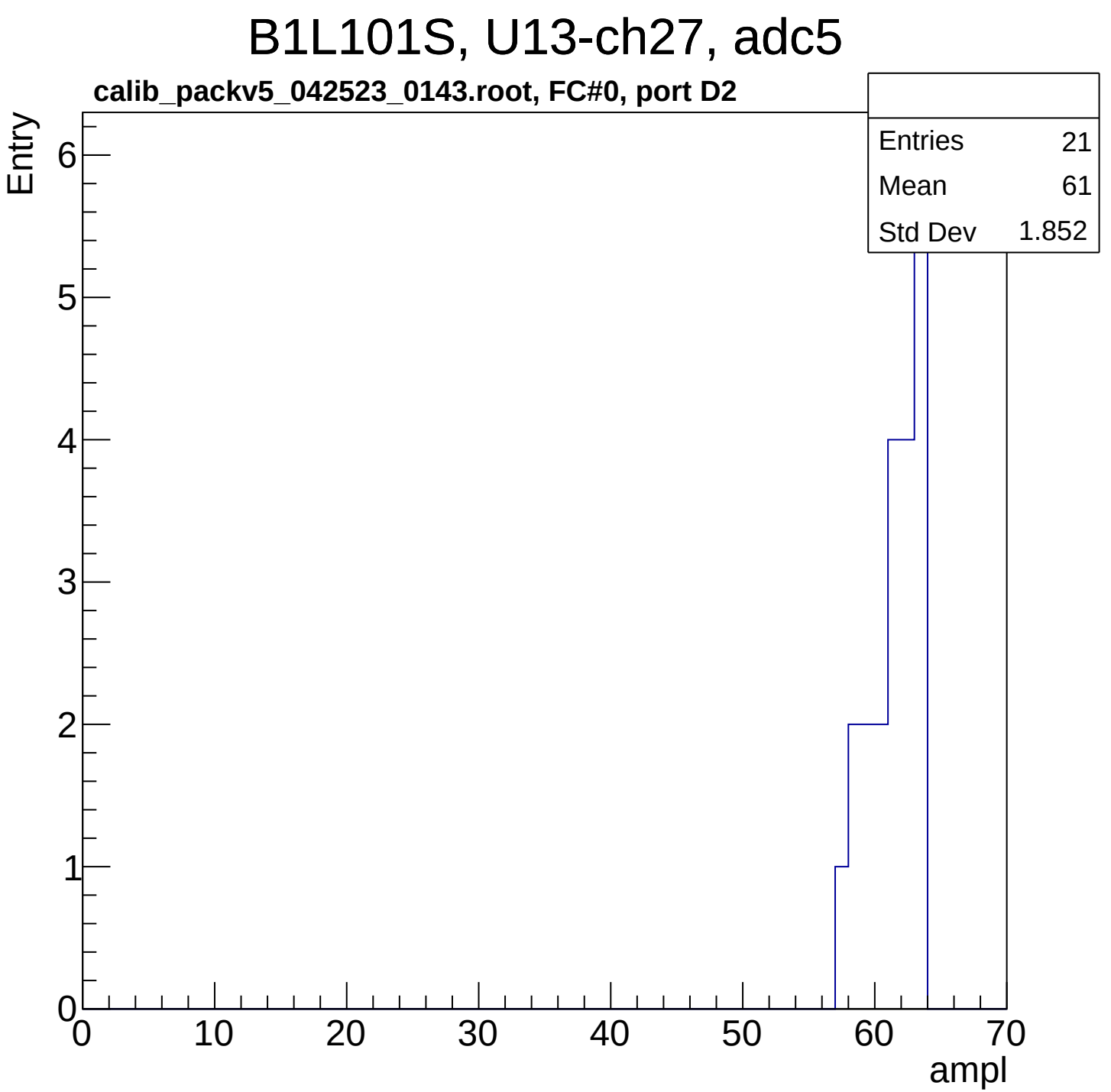
B1L101S, U13-ch27, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 21
Mean 61
Std Dev 1.852
Entry
0
1
2
3
4
5
6
0
10
20
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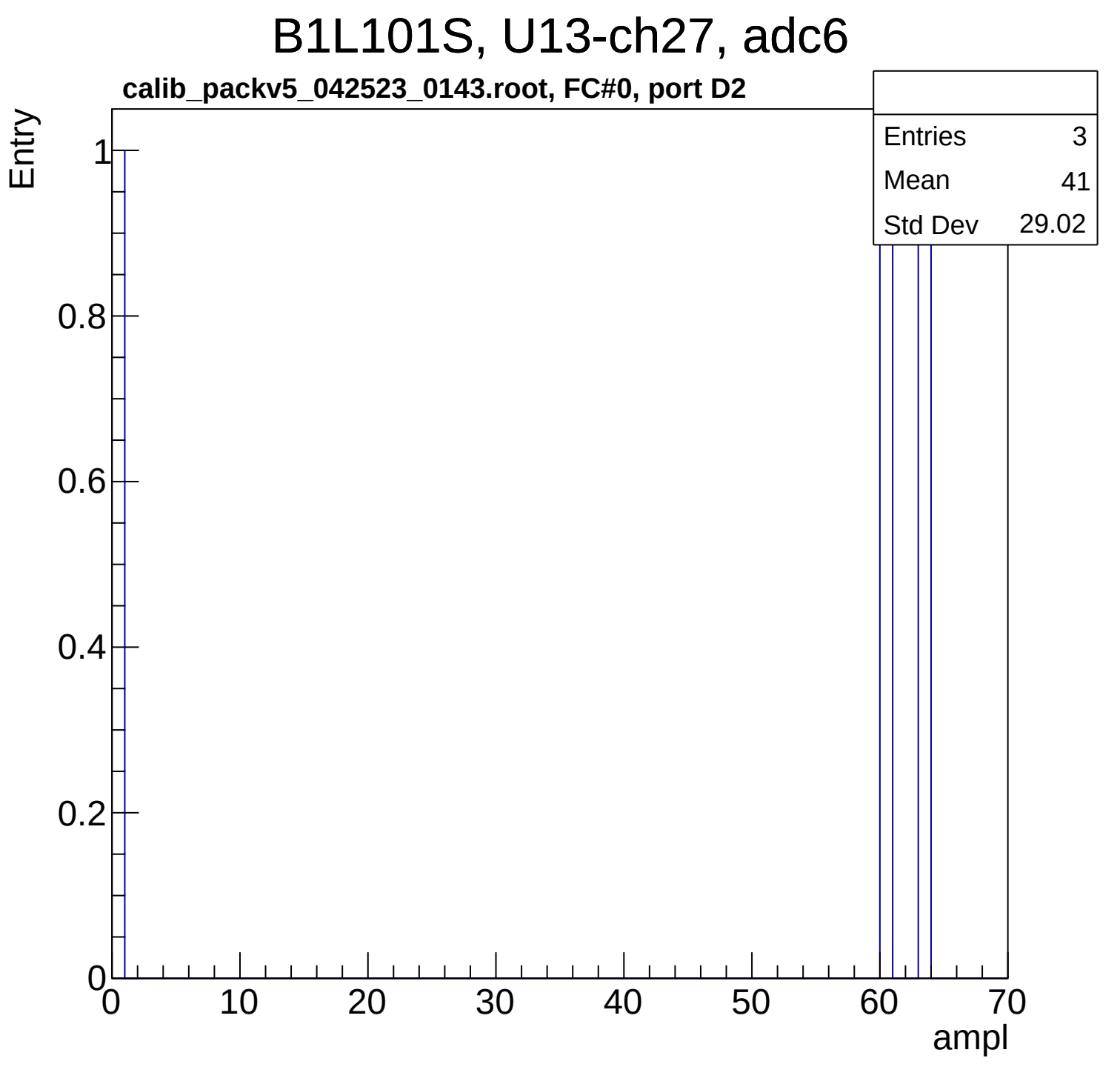
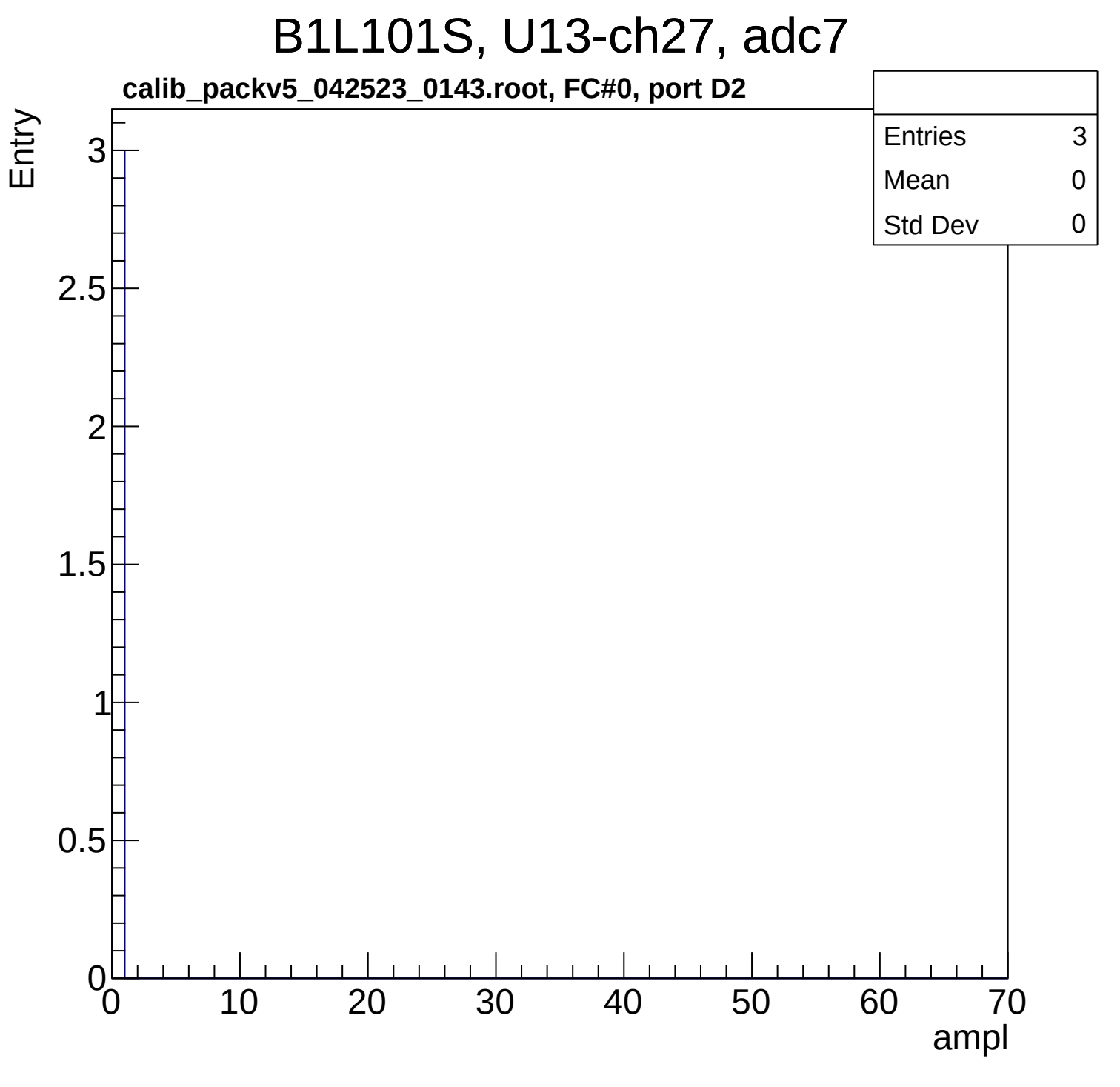
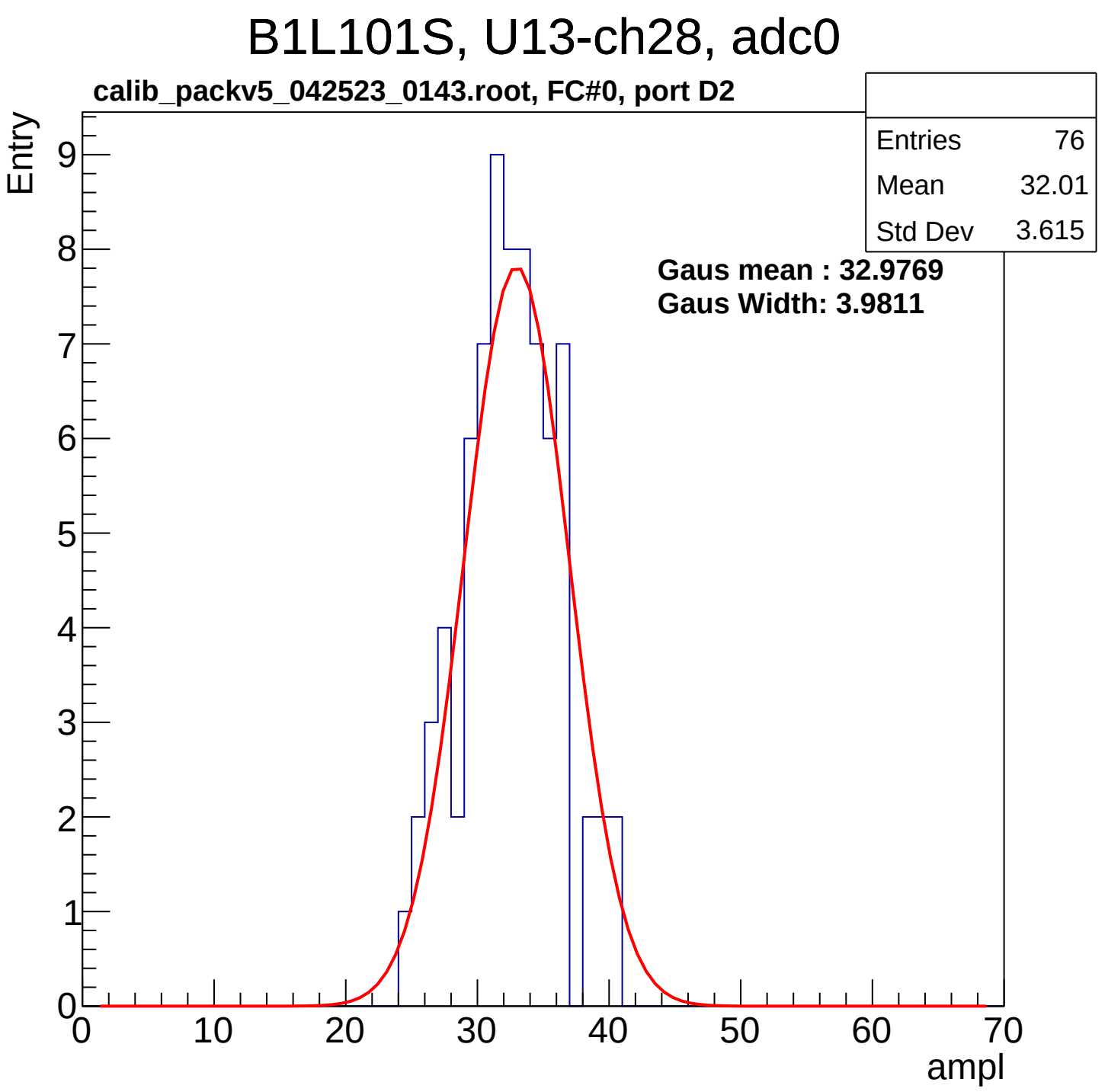
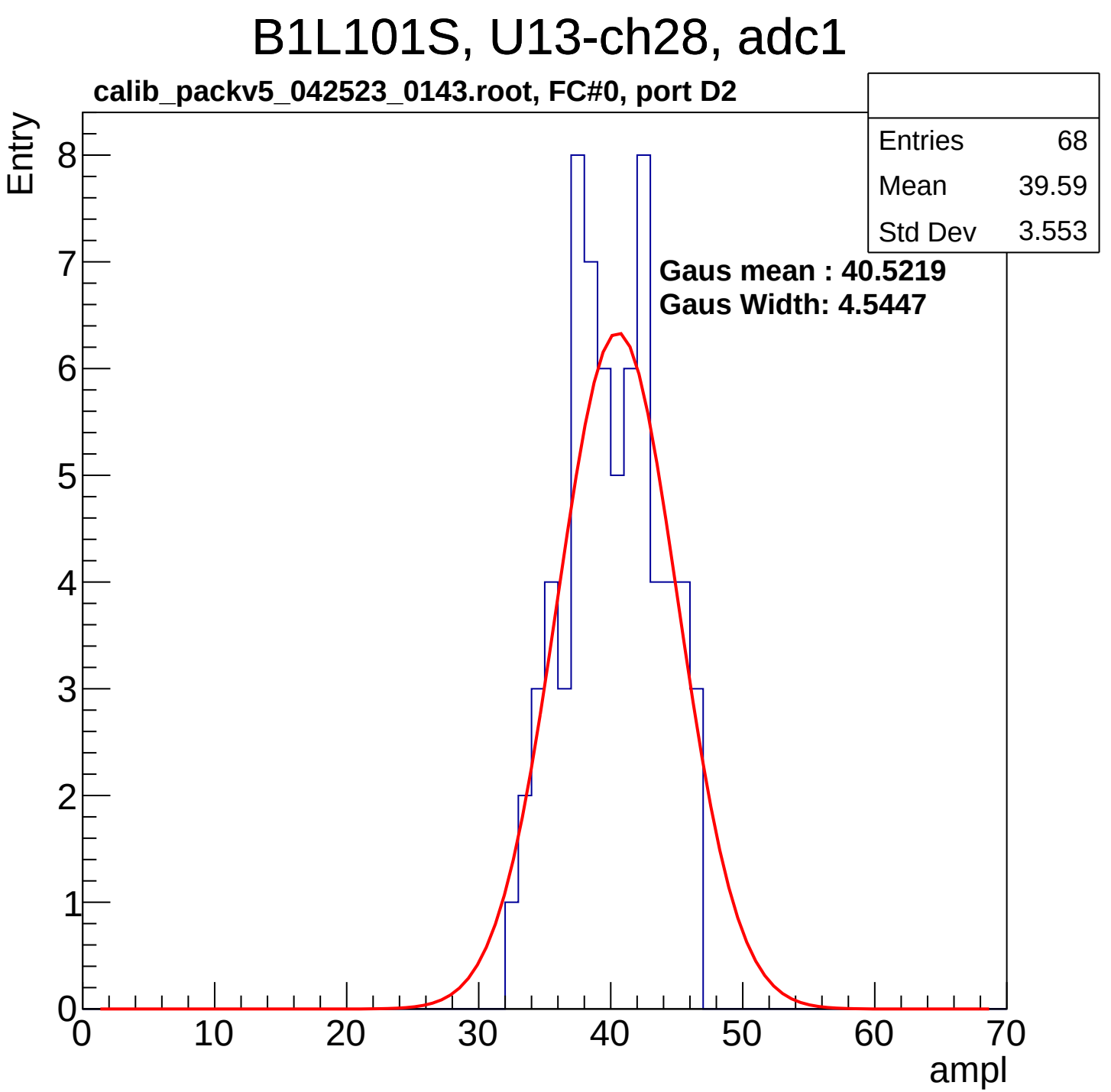
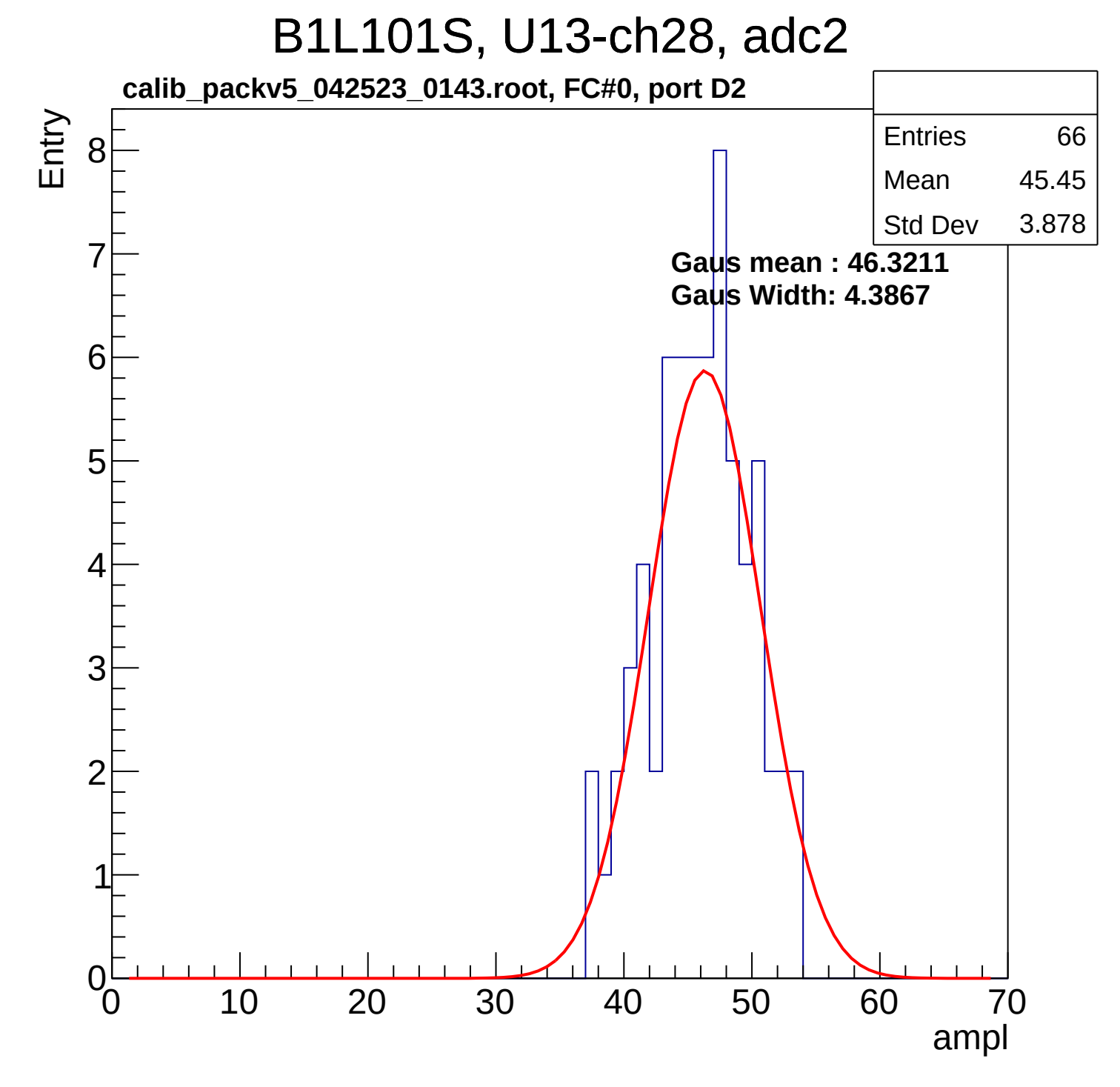
50
60
70
ampl


B1L101S, U13-ch27, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch27, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch28, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 32.01
Std Dev 3.615
Gaus mean : 32.9769
Gaus Width: 3.9811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch28, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 39.59
Std Dev 3.553
Gaus mean : 40.5219
Gaus Width: 4.5447
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch28, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 45.45
Std Dev 3.878
Gaus mean : 46.3211
Gaus Width: 4.3867
Entry
ampl
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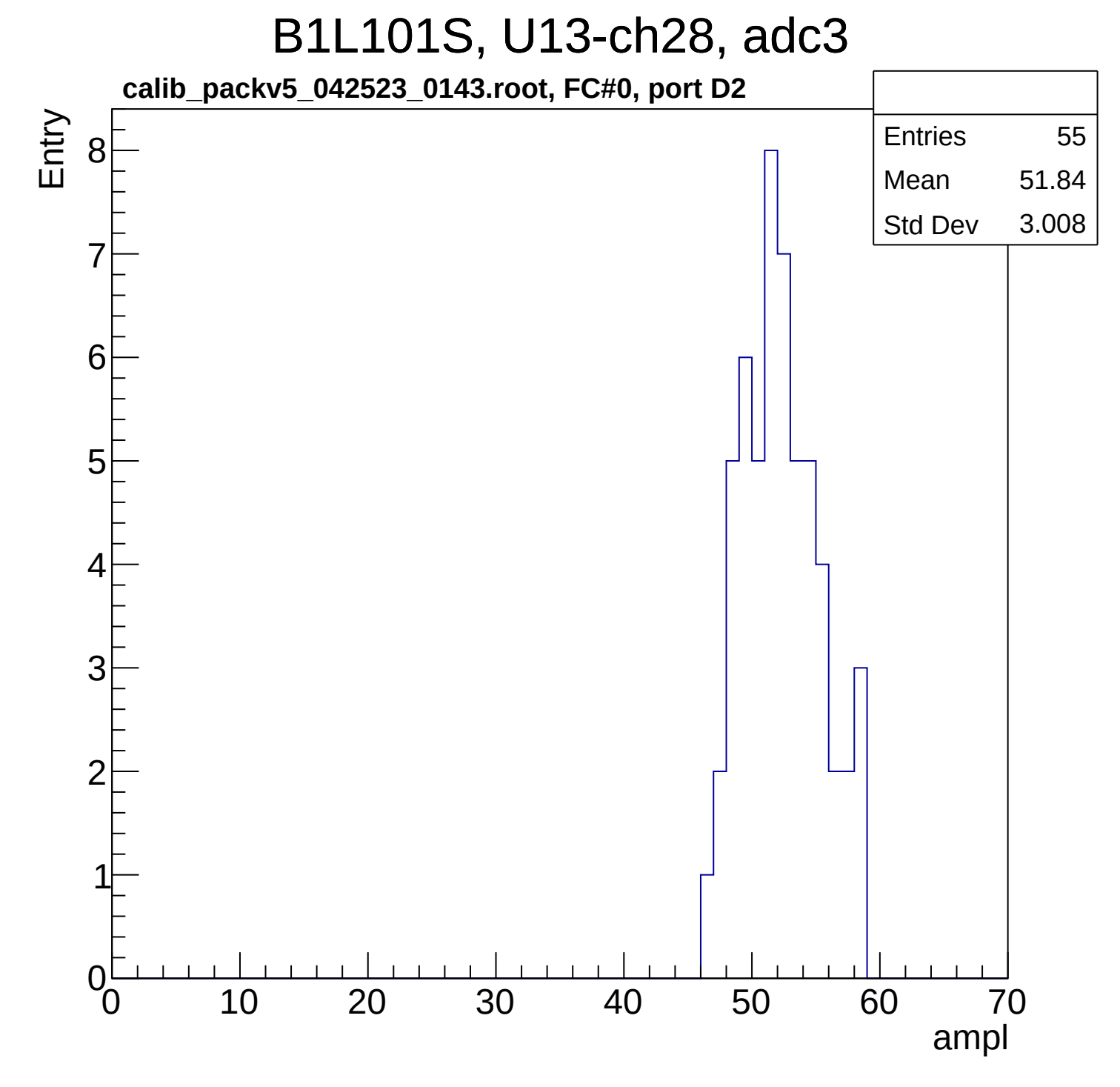
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch28, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 51.84
Std Dev 3.008
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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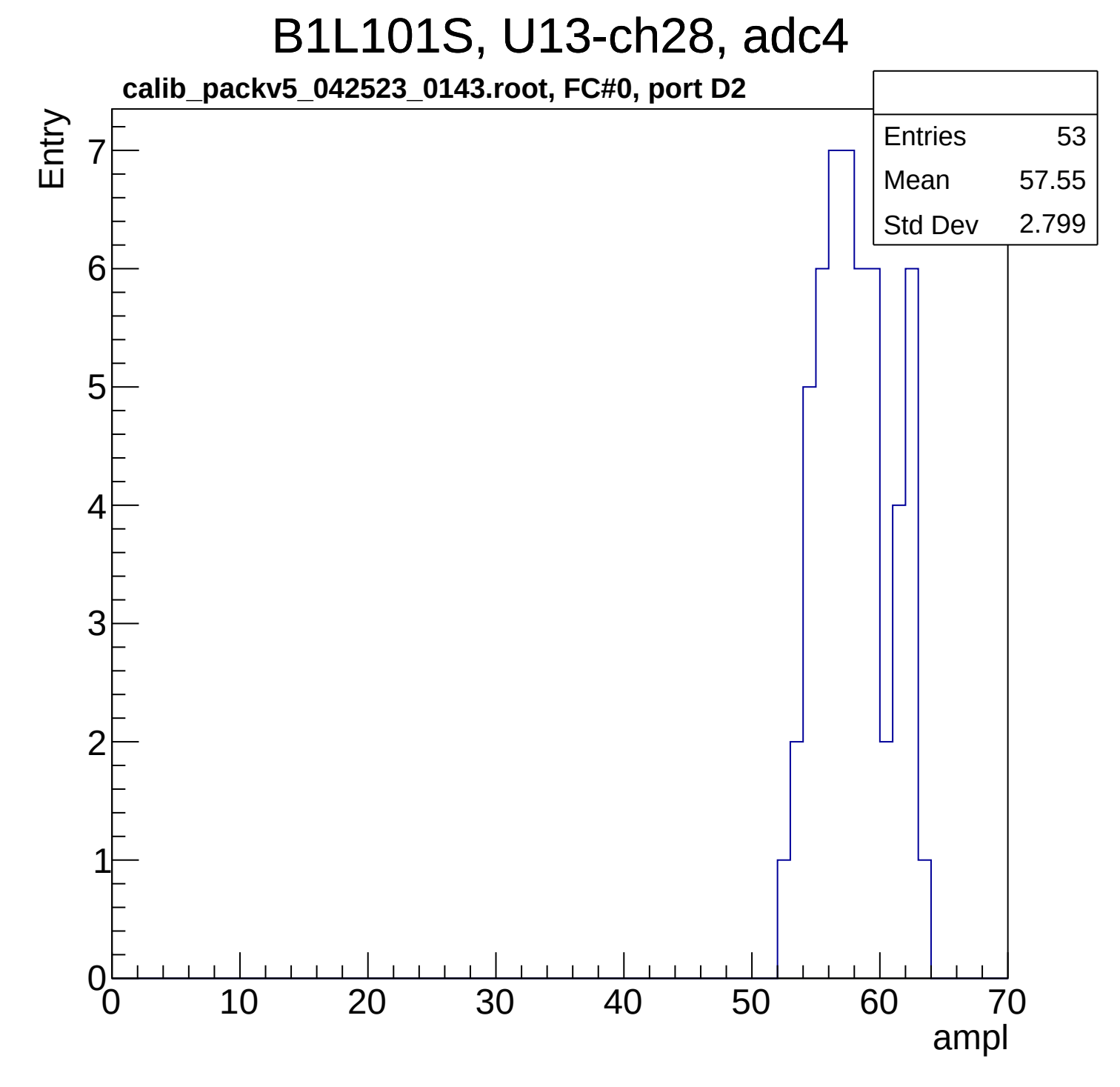
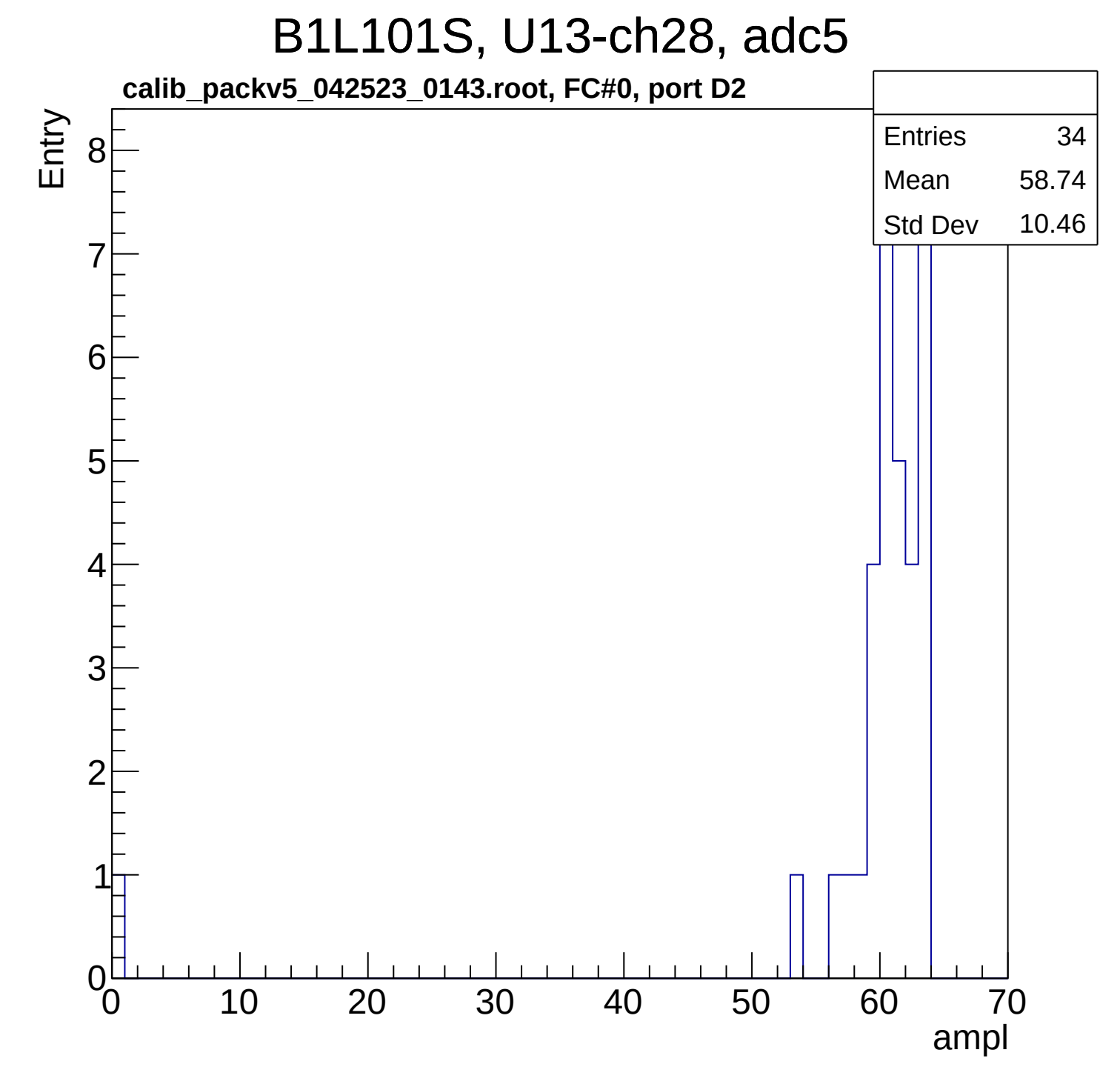

B1L101S, U13-ch28, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 57.55
Std Dev 2.799
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch28, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 34
Mean 58.74
Std Dev 10.46
Entry
ampl
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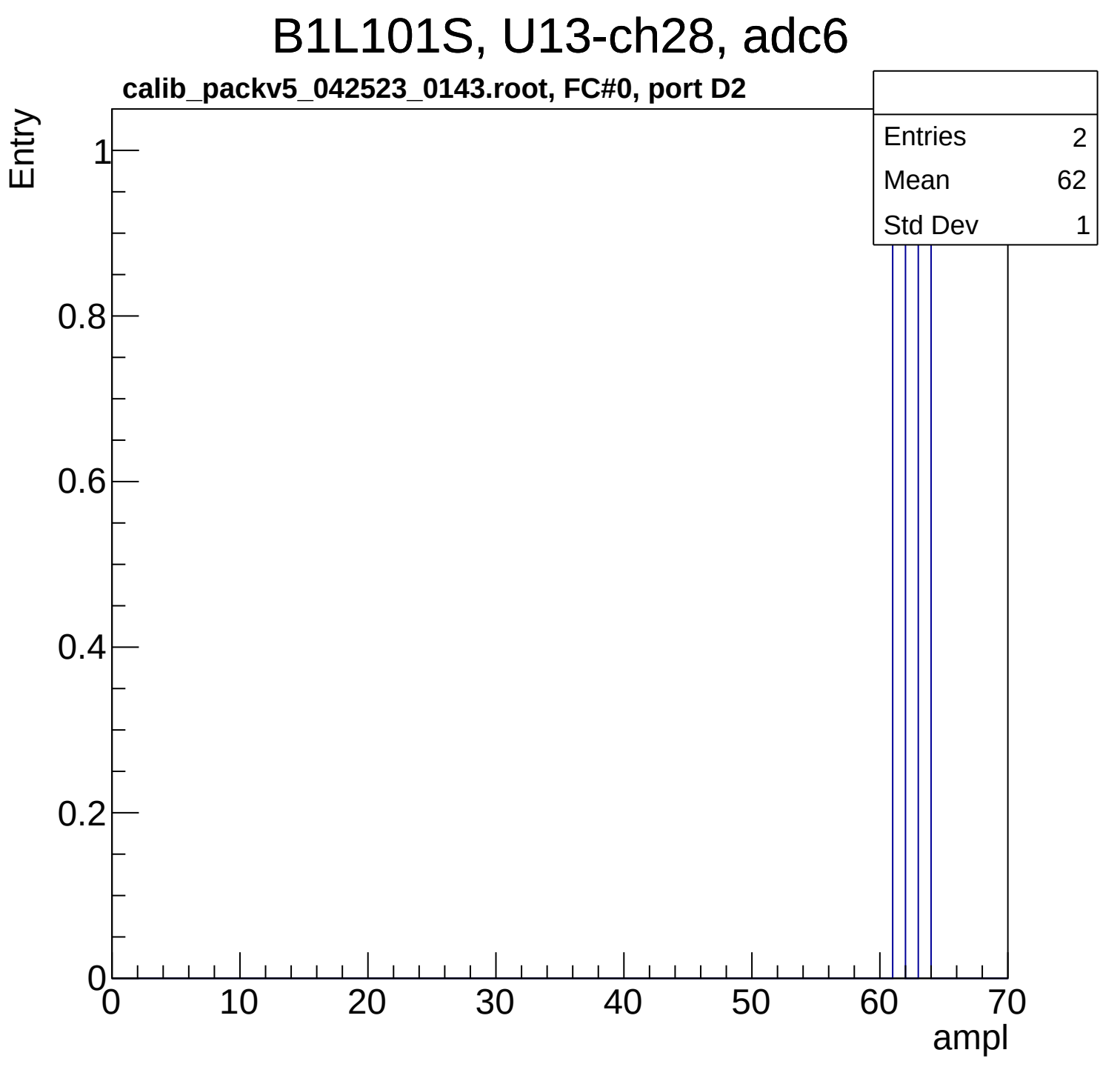
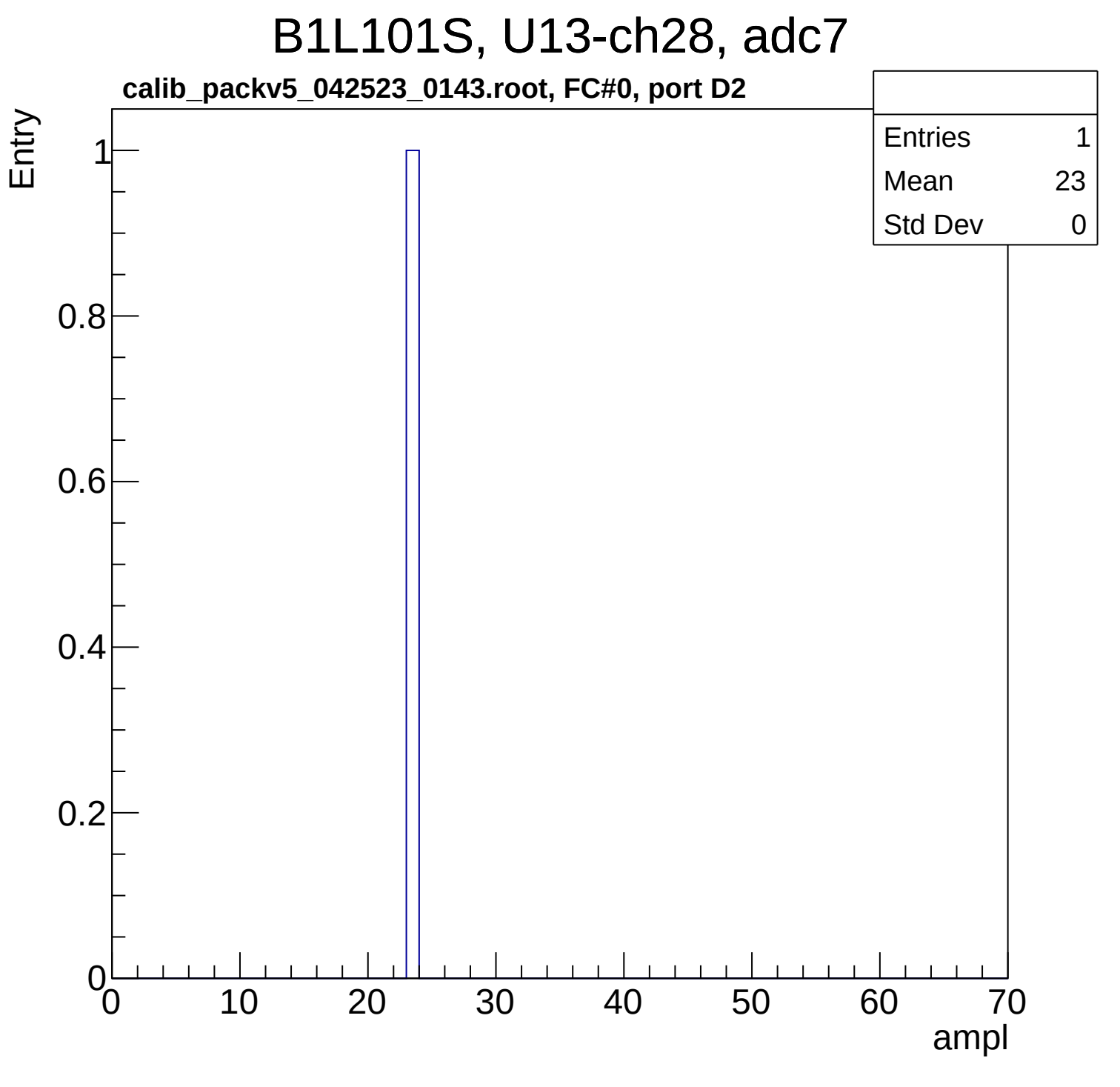
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch28, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch28, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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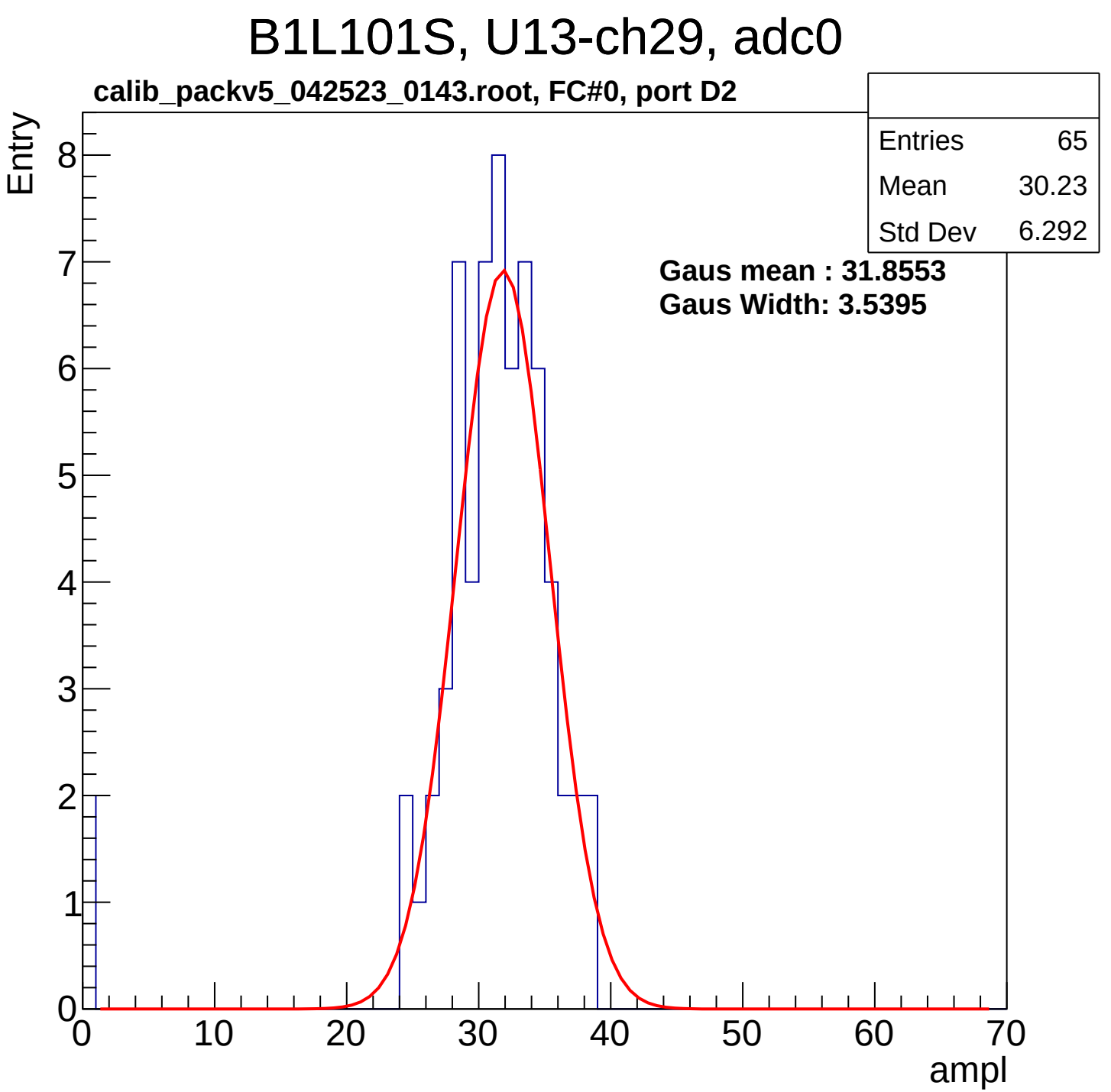
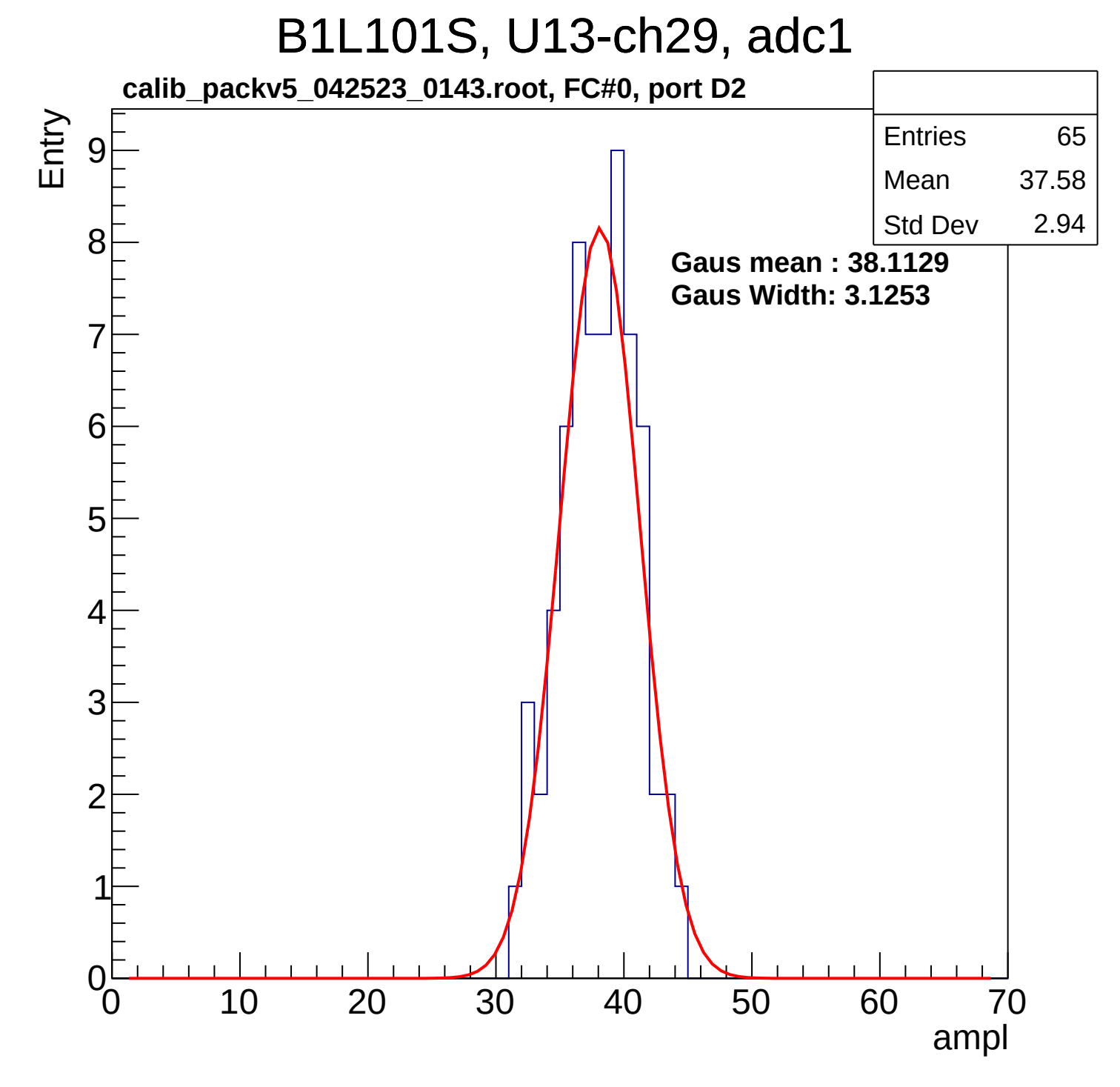
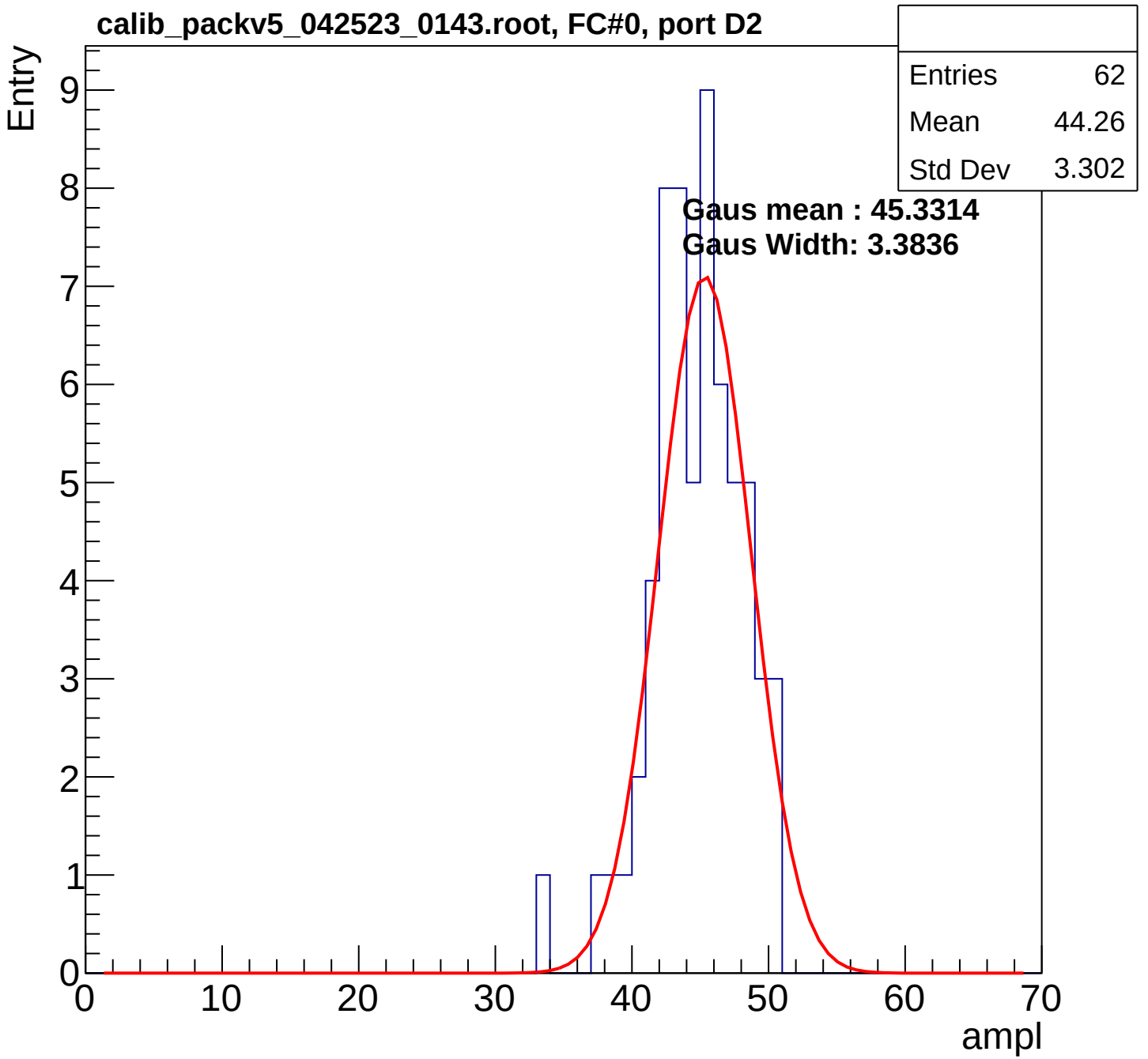
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch29, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 30.23
Std Dev 6.292
Gaus mean : 31.8553
Gaus Width: 3.5395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch29, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 37.58
Std Dev 2.94
Gaus mean : 38.1129
Gaus Width: 3.1253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch29, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 44.26
Std Dev 3.302
Gaus mean : 45.3314
Gaus Width: 3.3836
Entry
ampl
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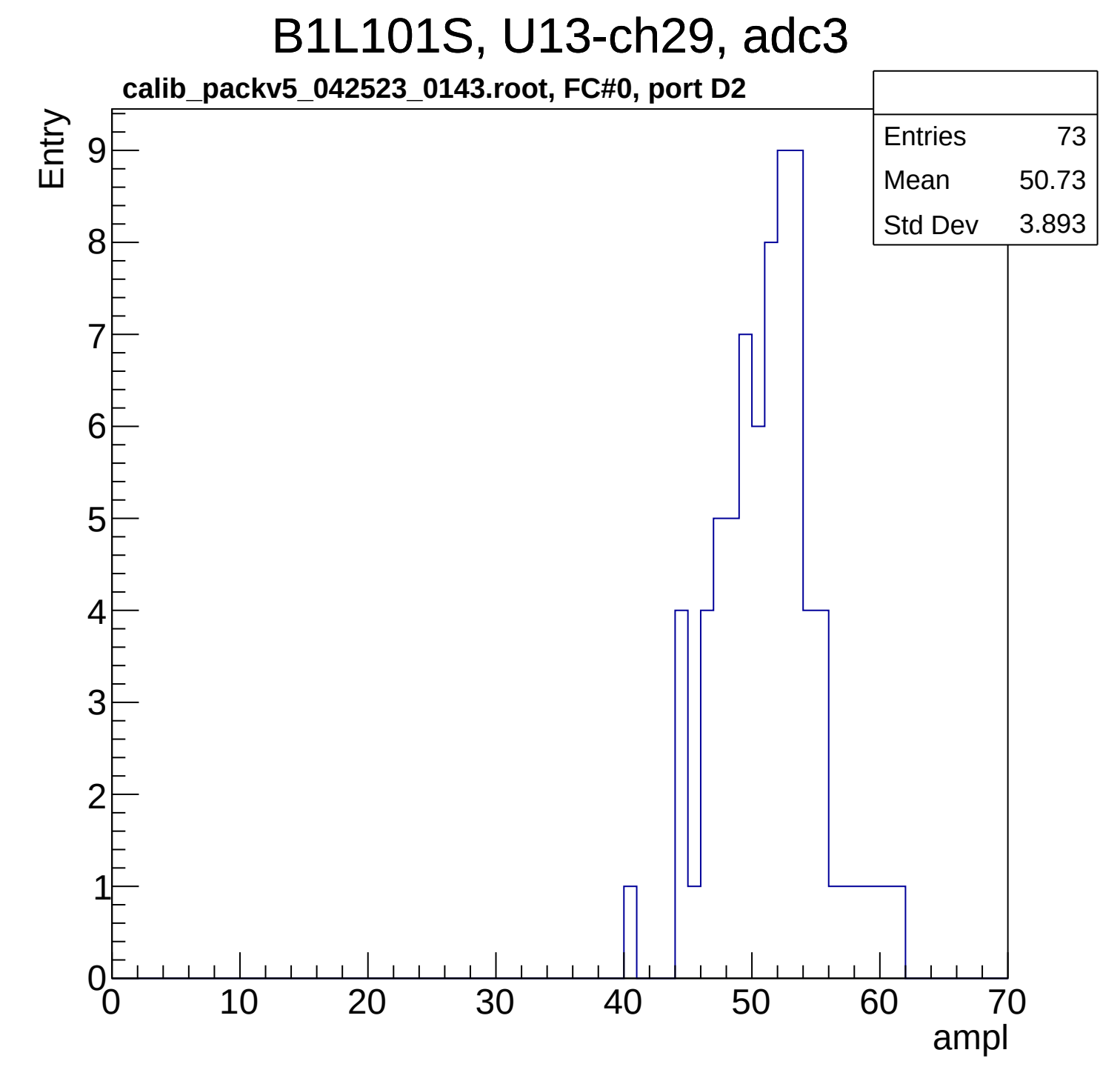
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L101S, U13-ch29, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 50.73
Std Dev 3.893
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
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60
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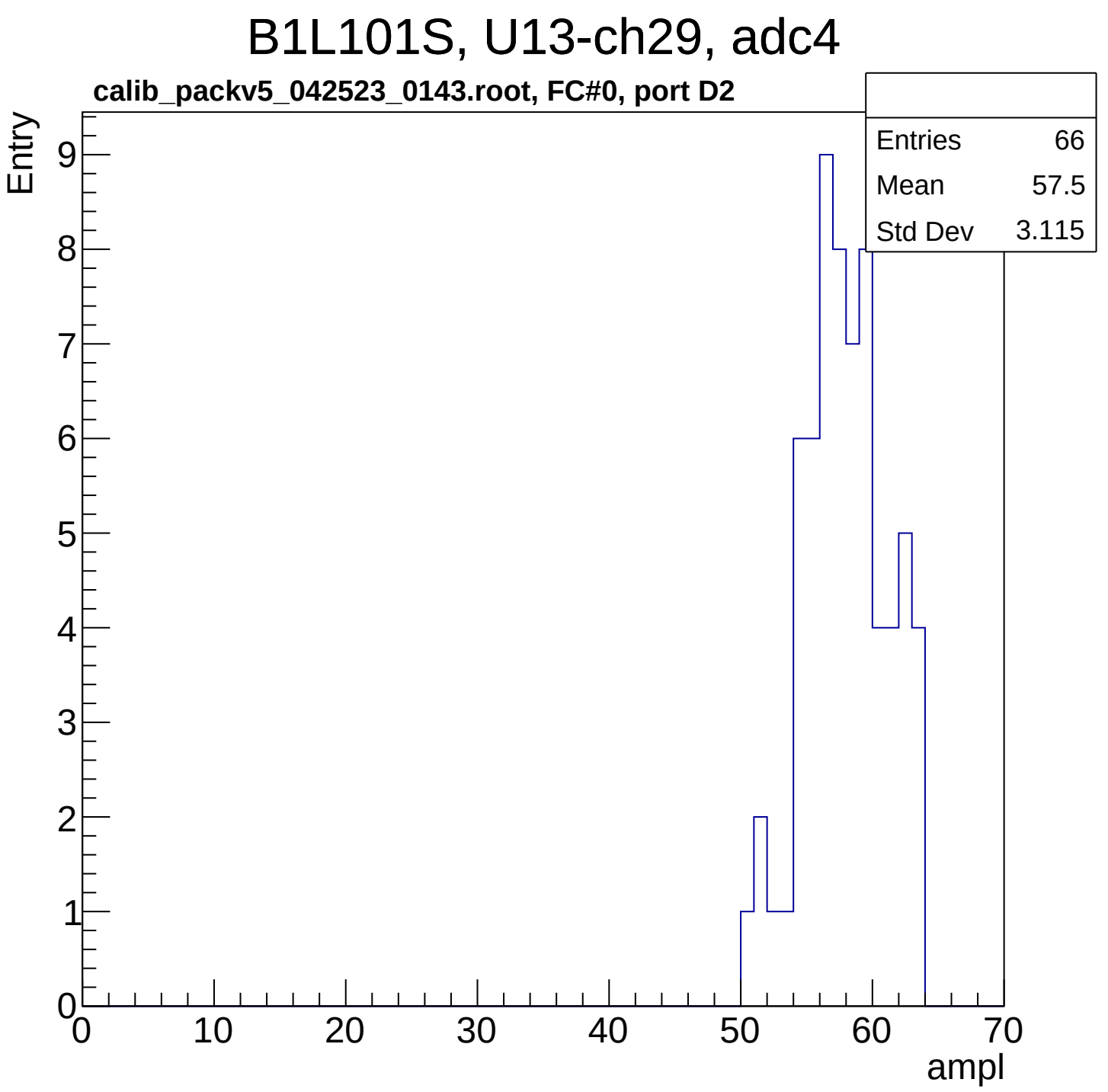
B1L101S, U13-ch29, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 57.5
Std Dev 3.115
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
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60
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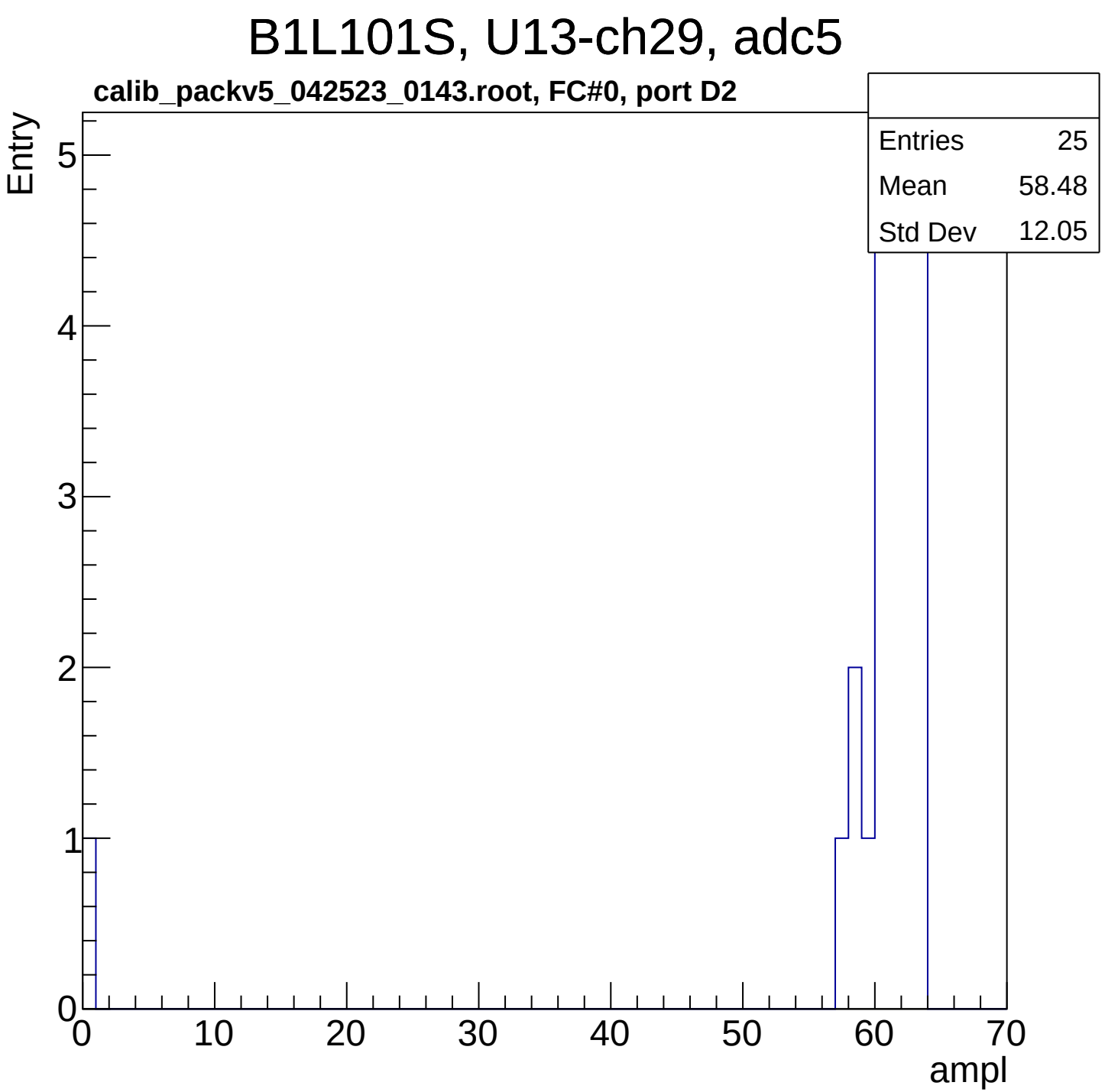
B1L101S, U13-ch29, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 25
Mean 58.48
Std Dev 12.05
Entry
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1
2
3
4
5
0
10
20
30
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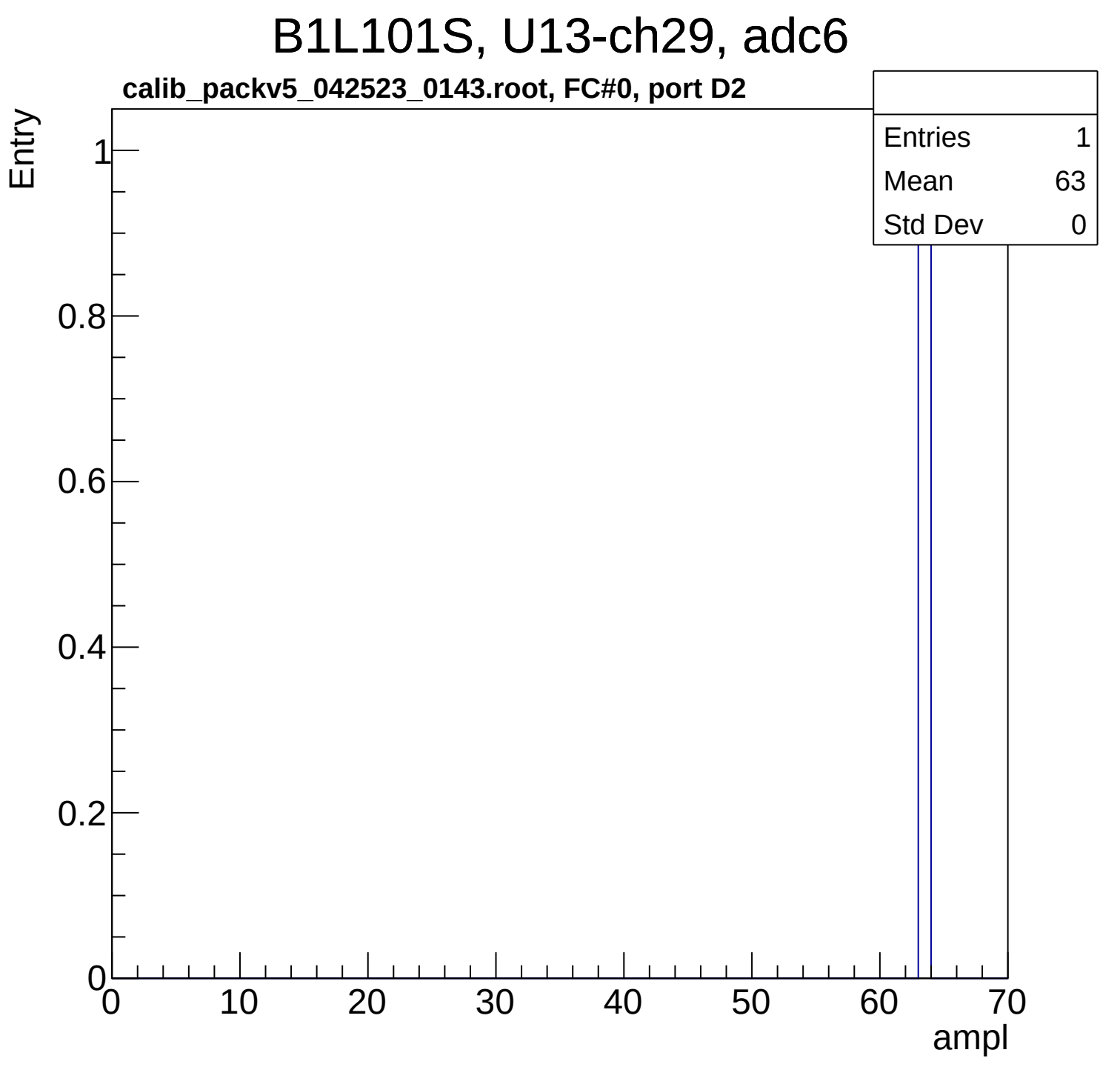
50
60
70
ampl


B1L101S, U13-ch29, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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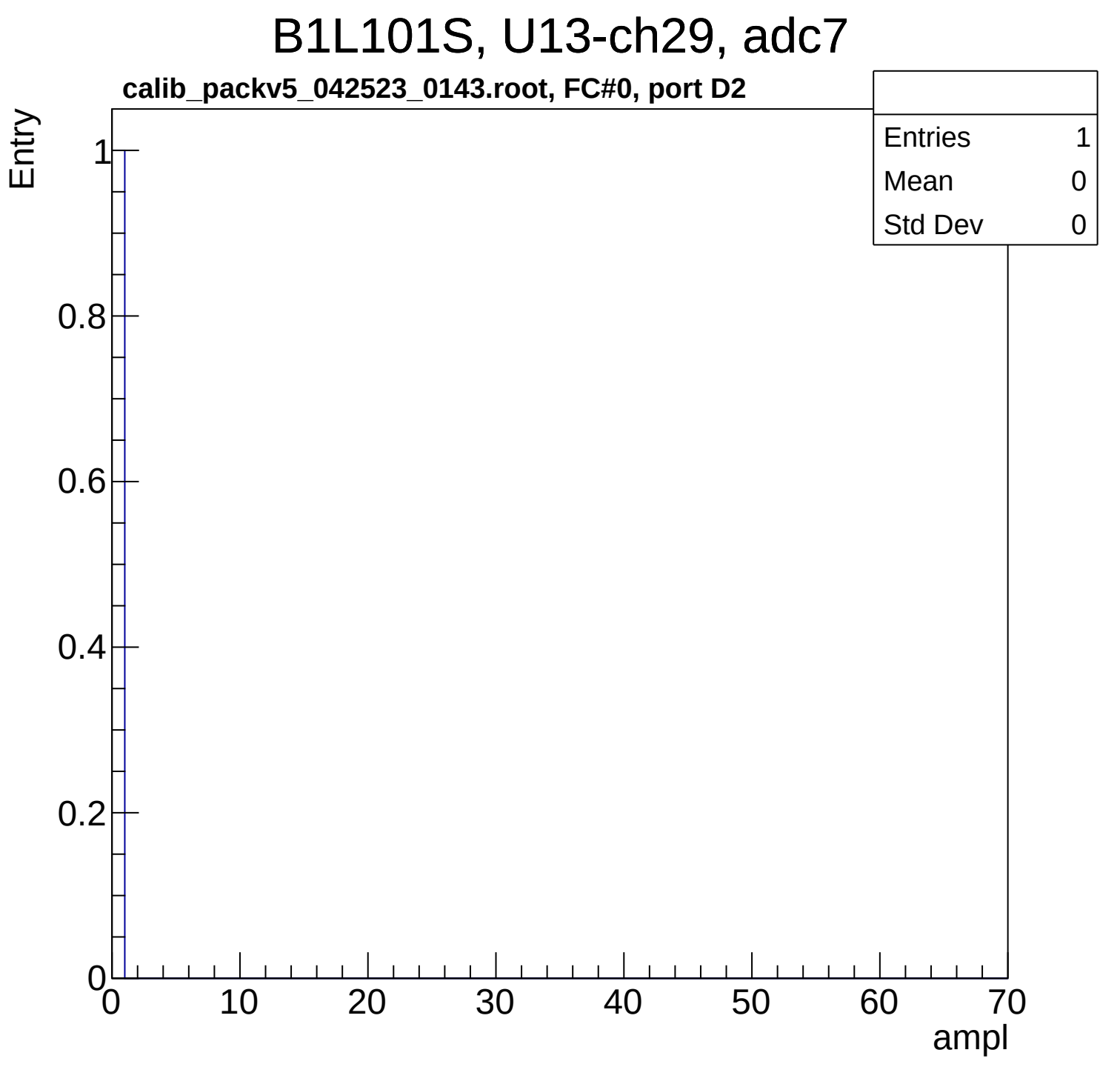
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch29, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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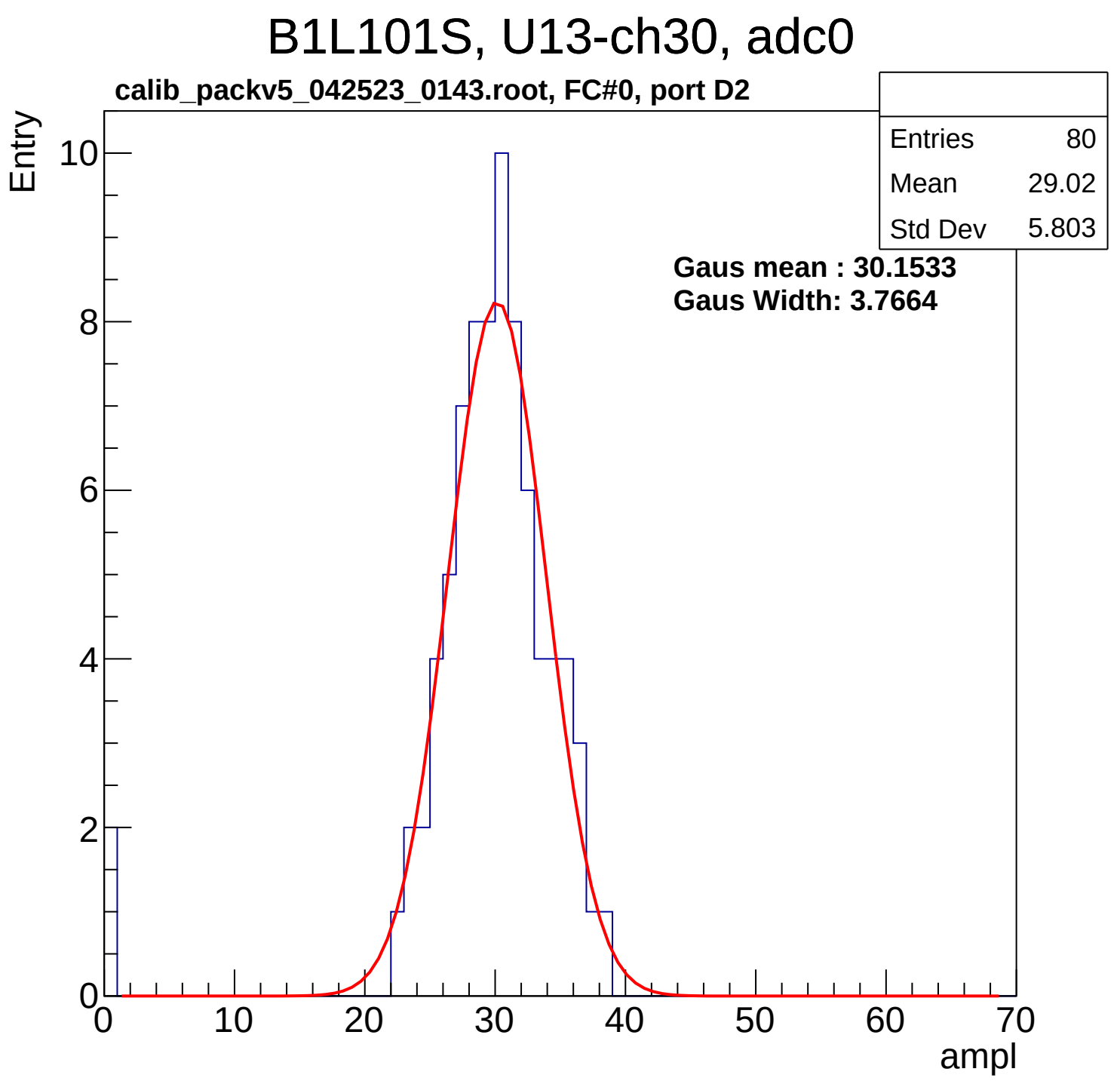
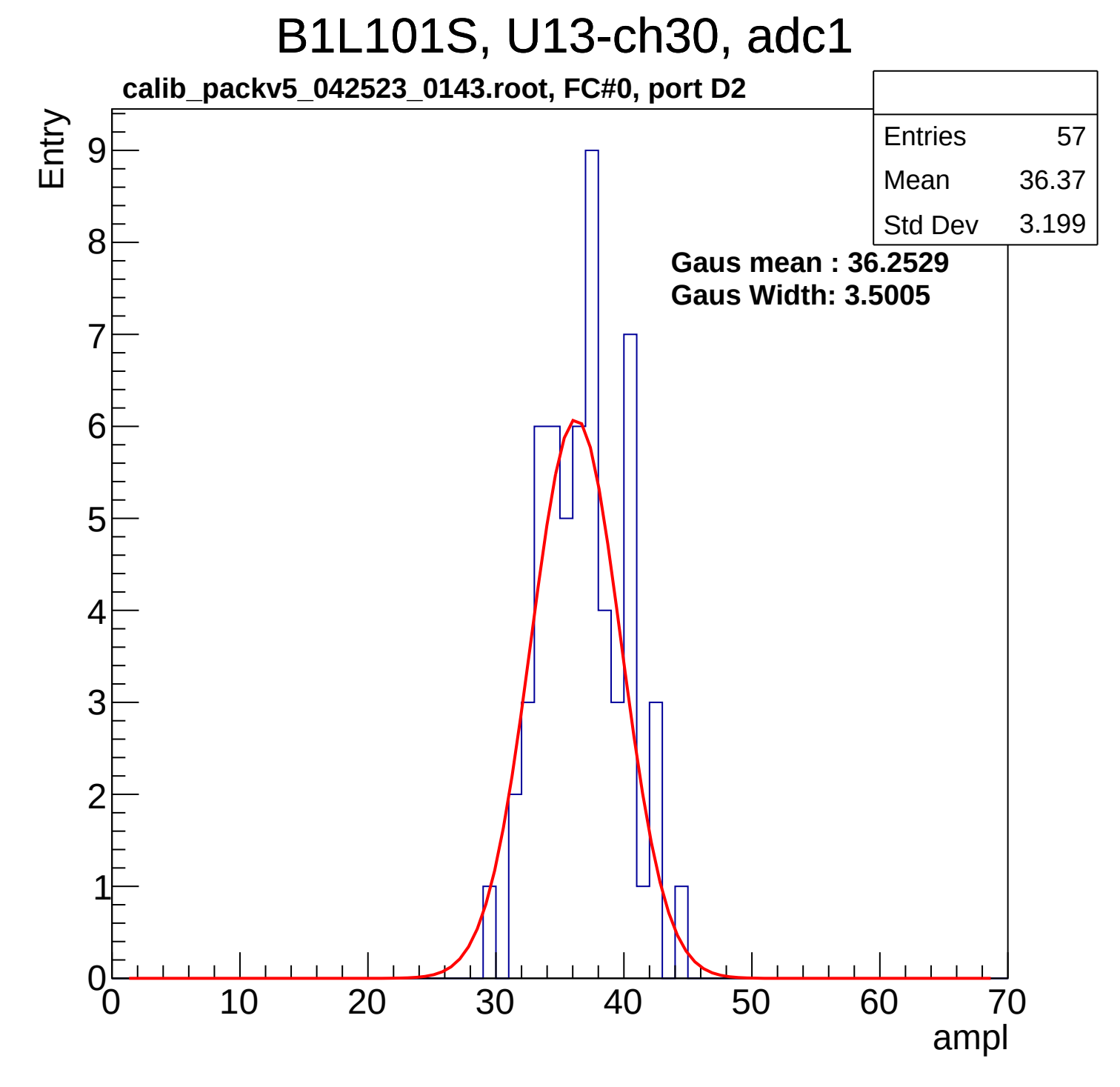
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch30, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 29.02
Std Dev 5.803
Gaus mean : 30.1533
Gaus Width: 3.7664
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch30, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 36.37
Std Dev 3.199
Gaus mean : 36.2529
Gaus Width: 3.5005
Entry
ampl
0
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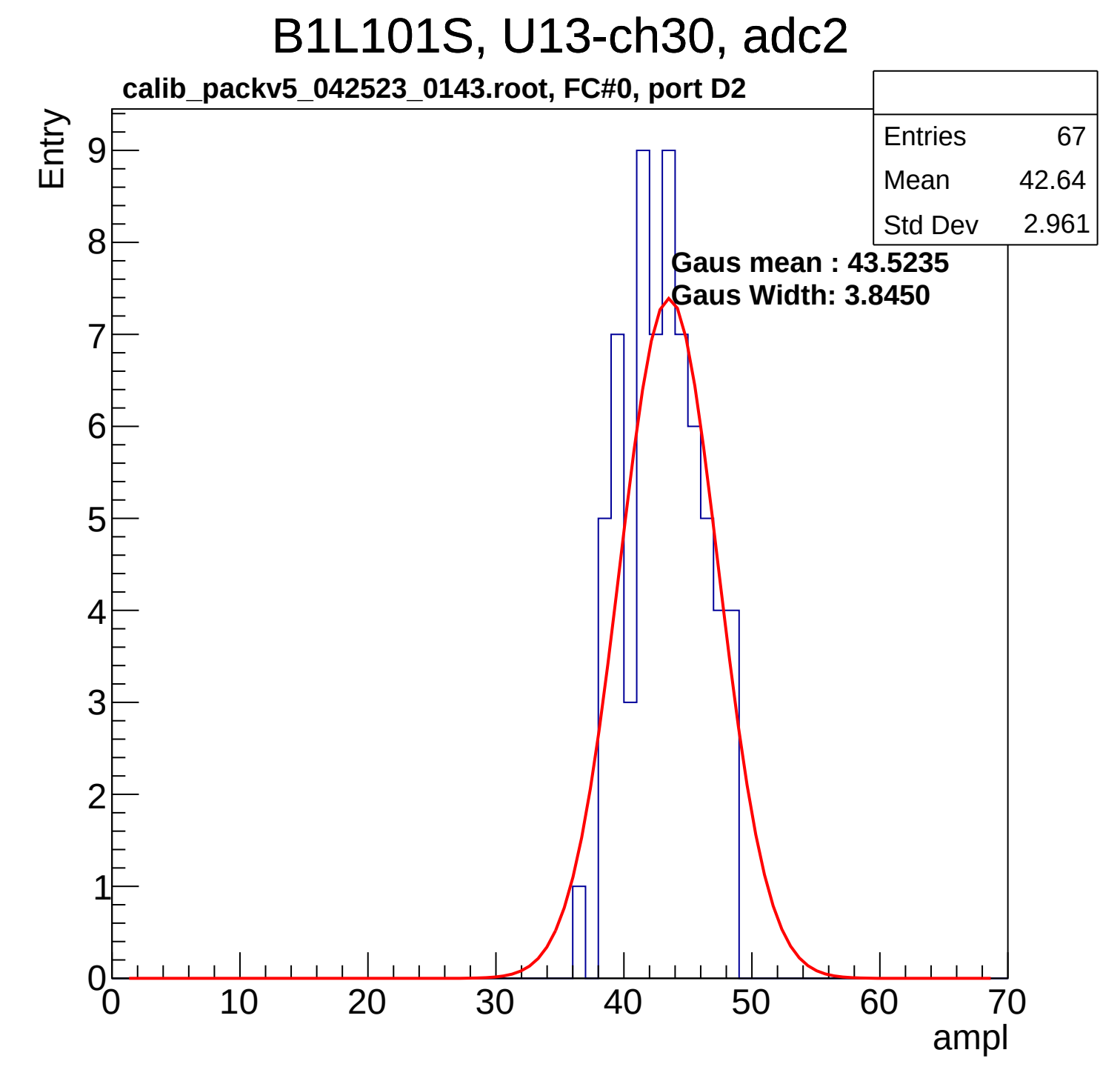
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch30, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 42.64
Std Dev 2.961
Gaus mean : 43.5235
Gaus Width: 3.8450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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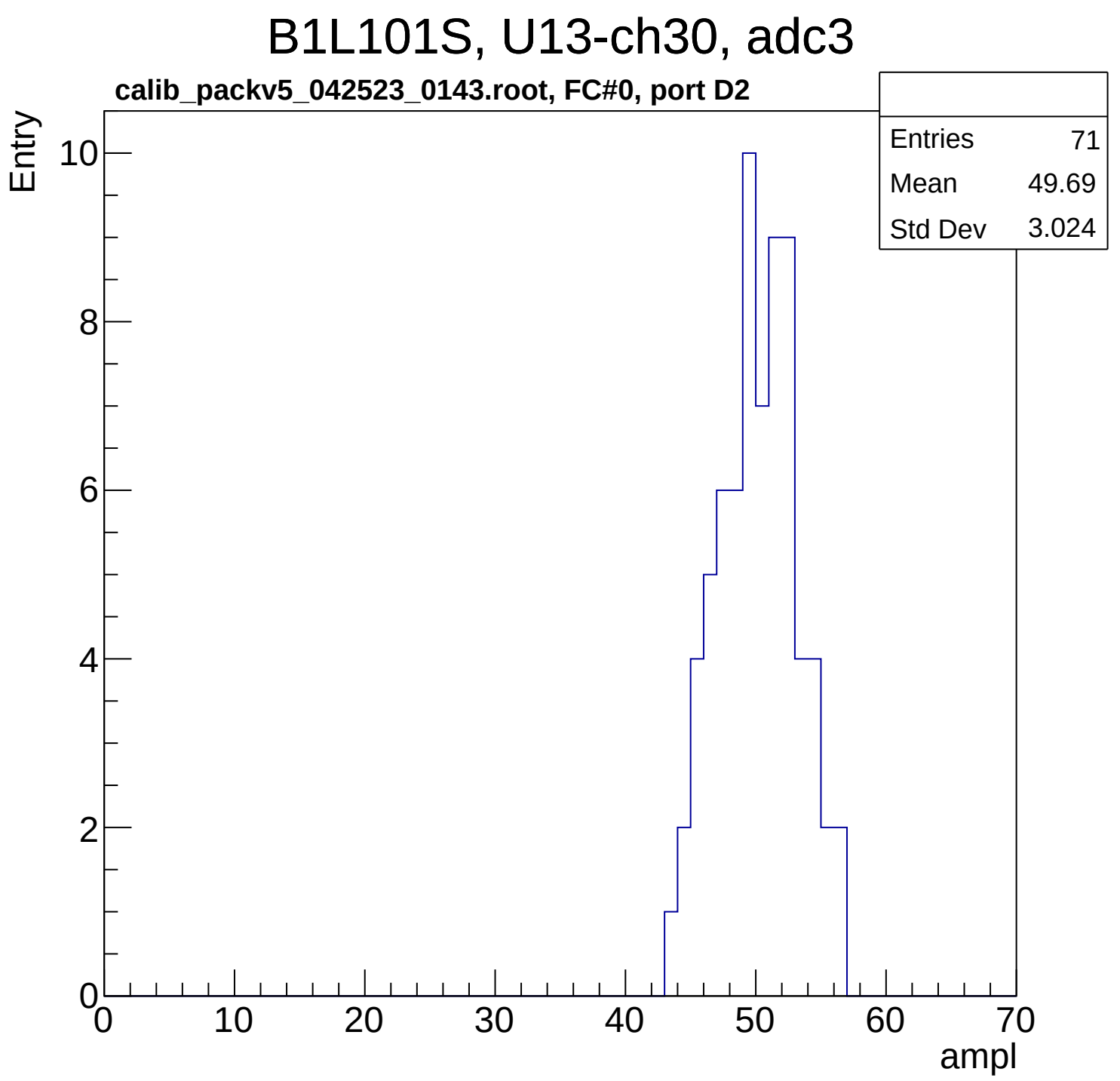
B1L101S, U13-ch30, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 49.69
Std Dev 3.024
Entry
0
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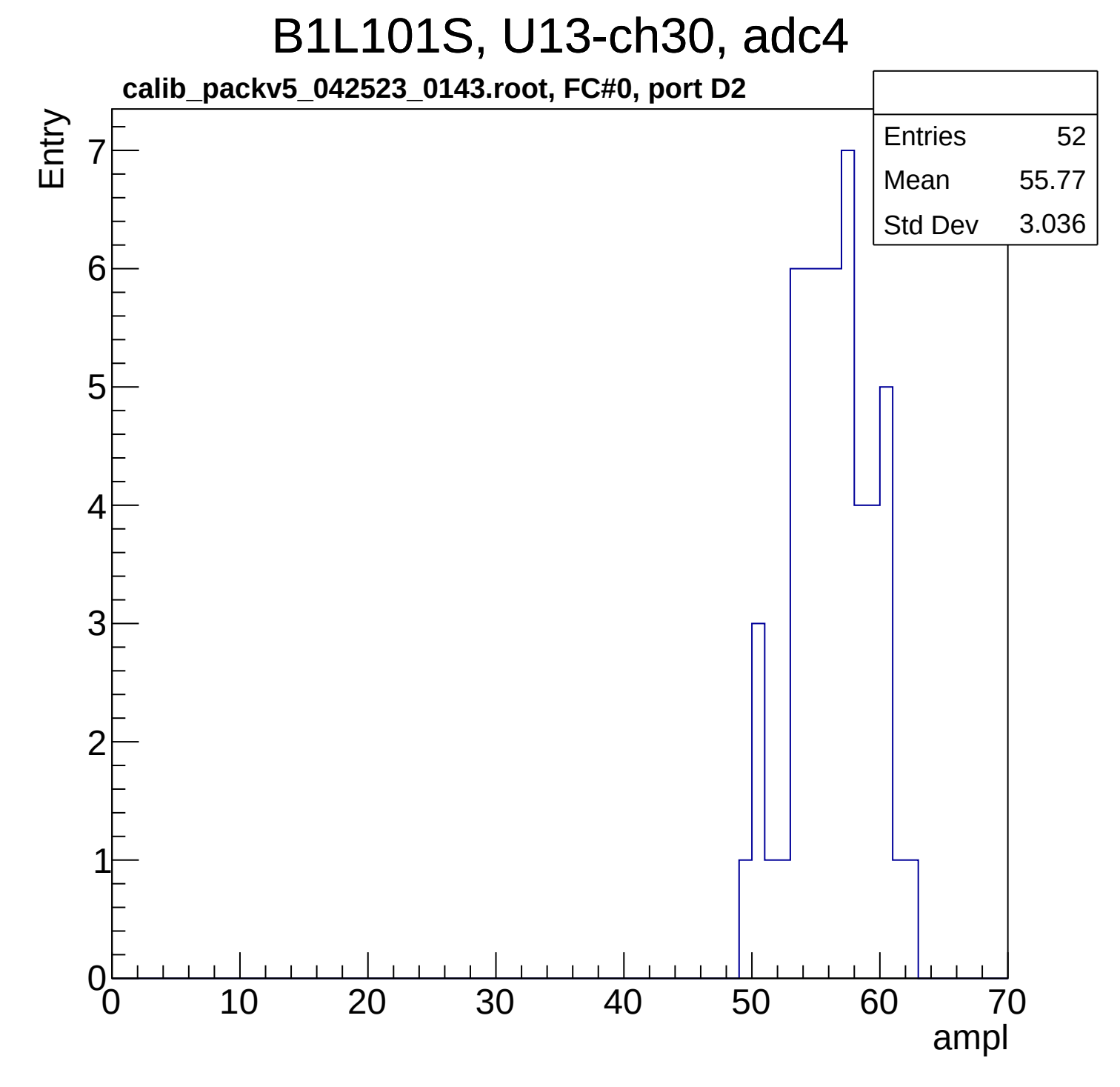
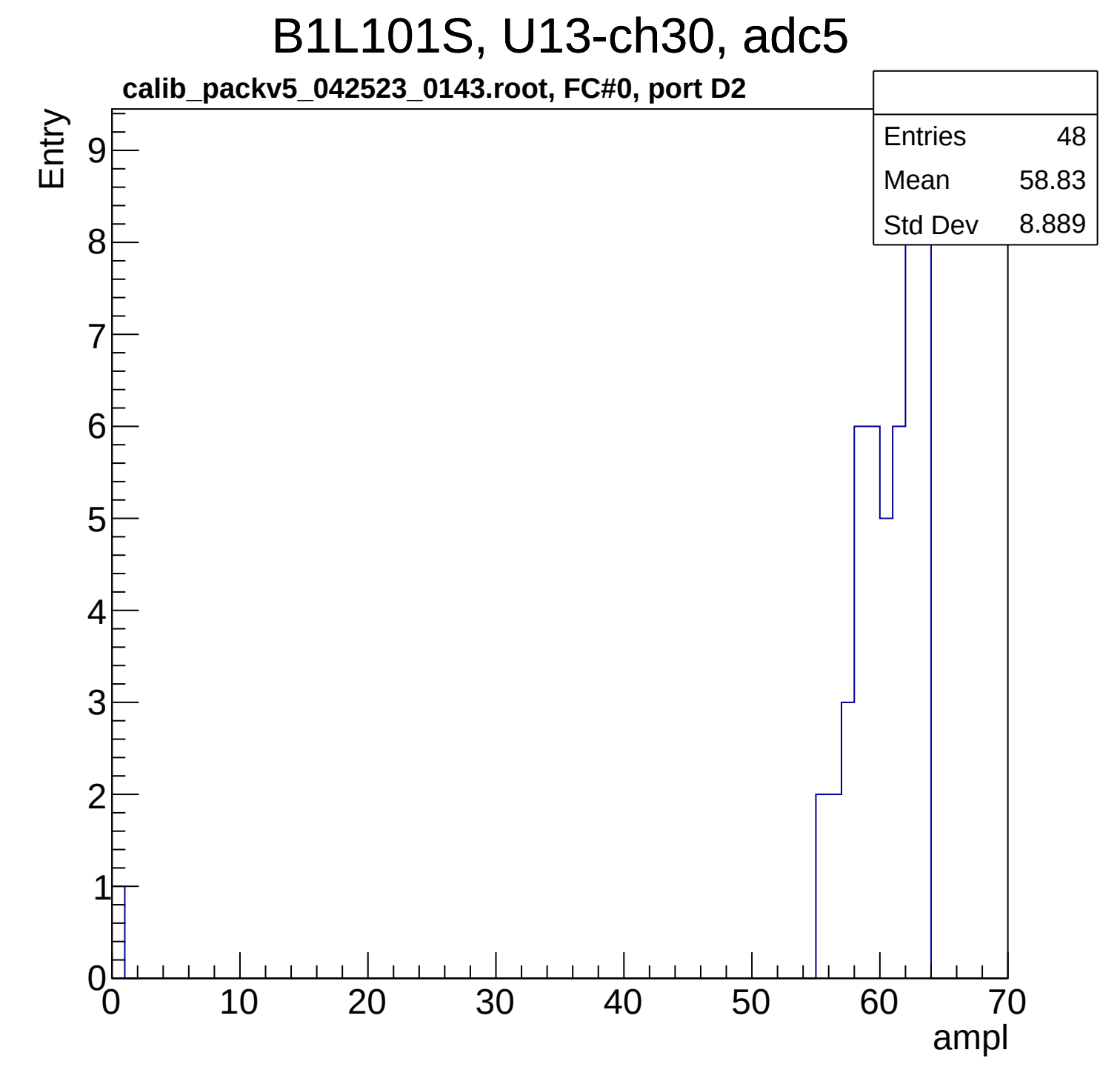
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch30, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 52
Mean 55.77
Std Dev 3.036
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch30, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 58.83
Std Dev 8.889
Entry
ampl
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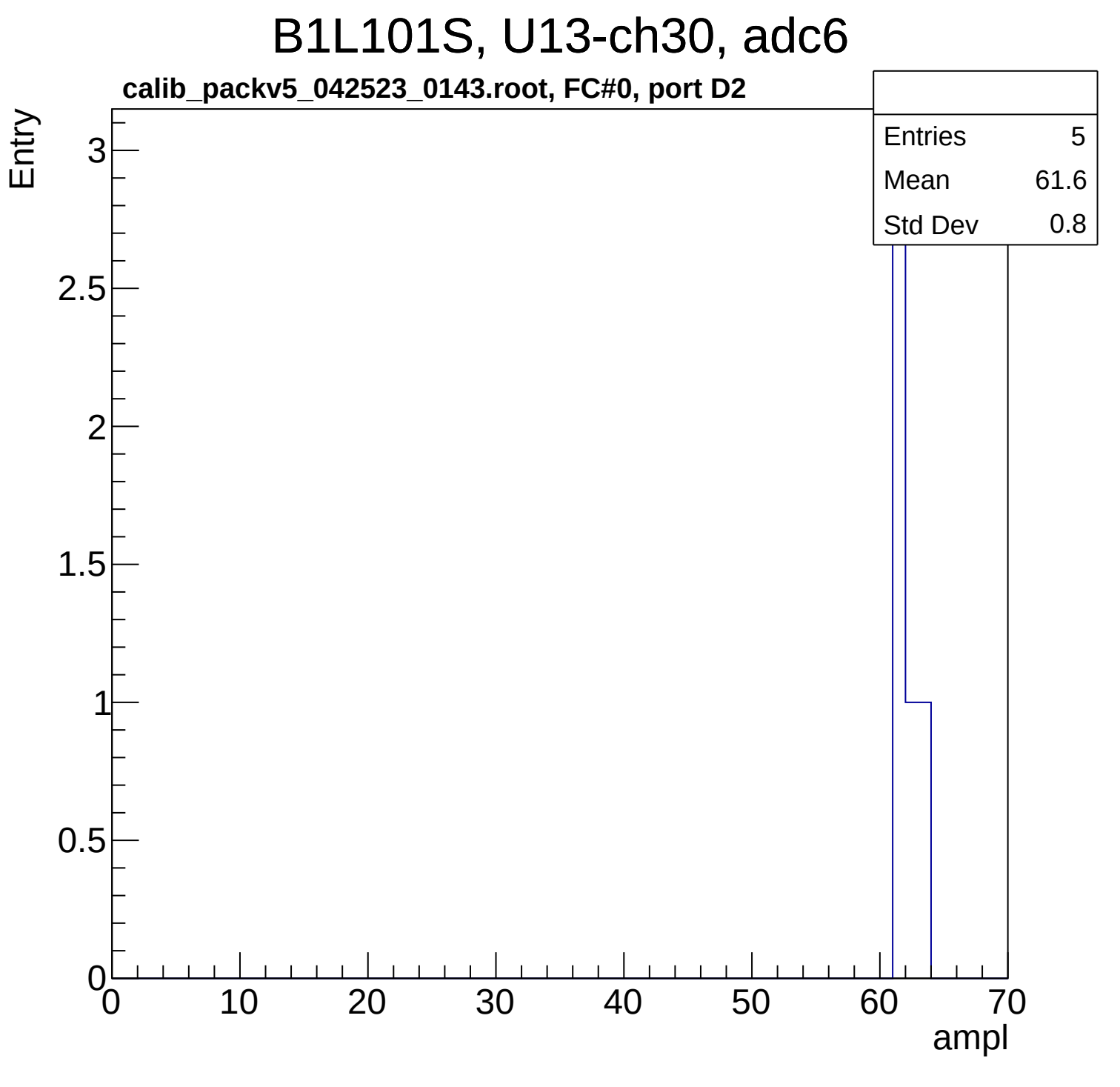
B1L101S, U13-ch30, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 61.6
Std Dev 0.8
Entry
ampl
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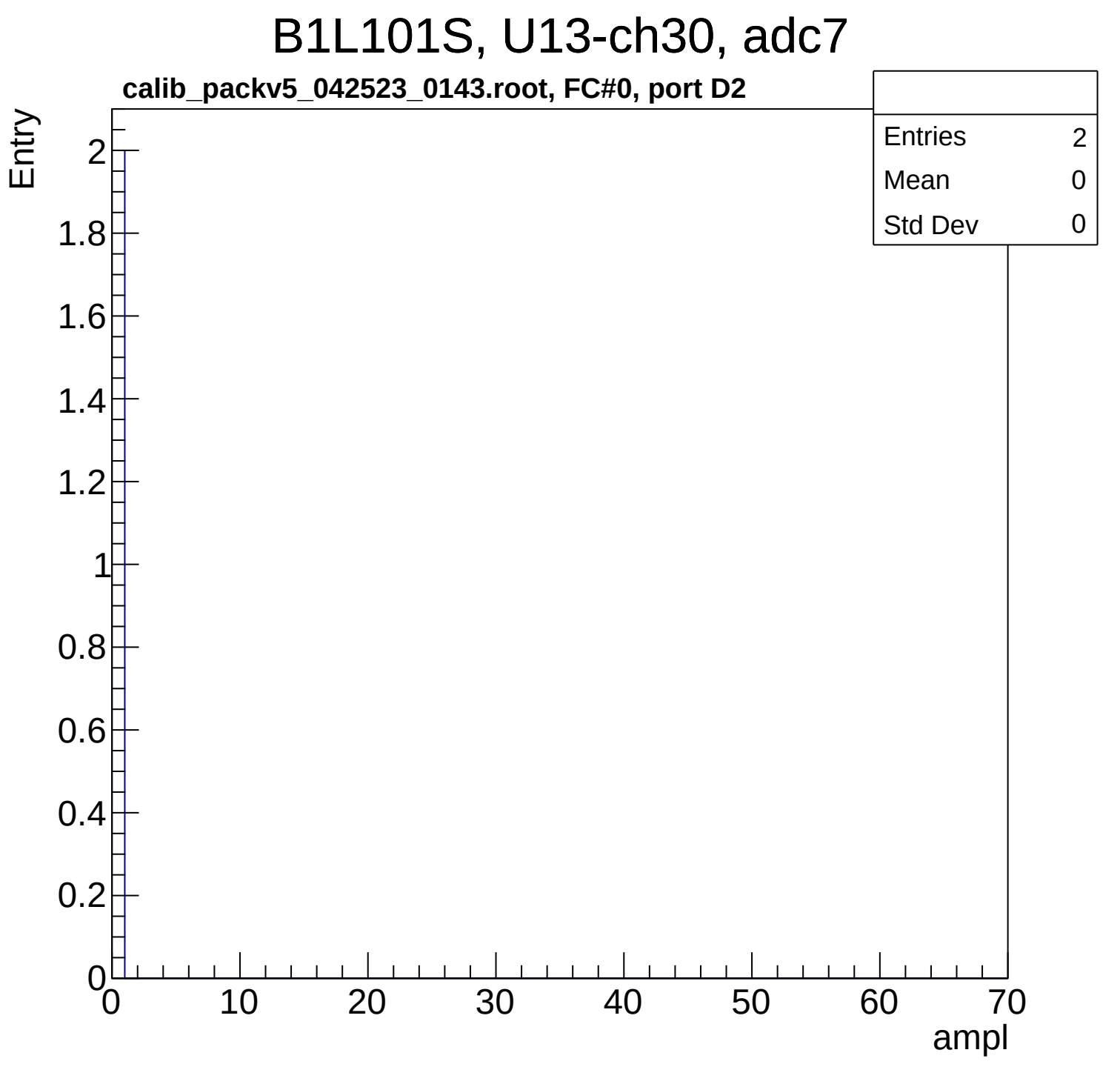
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch30, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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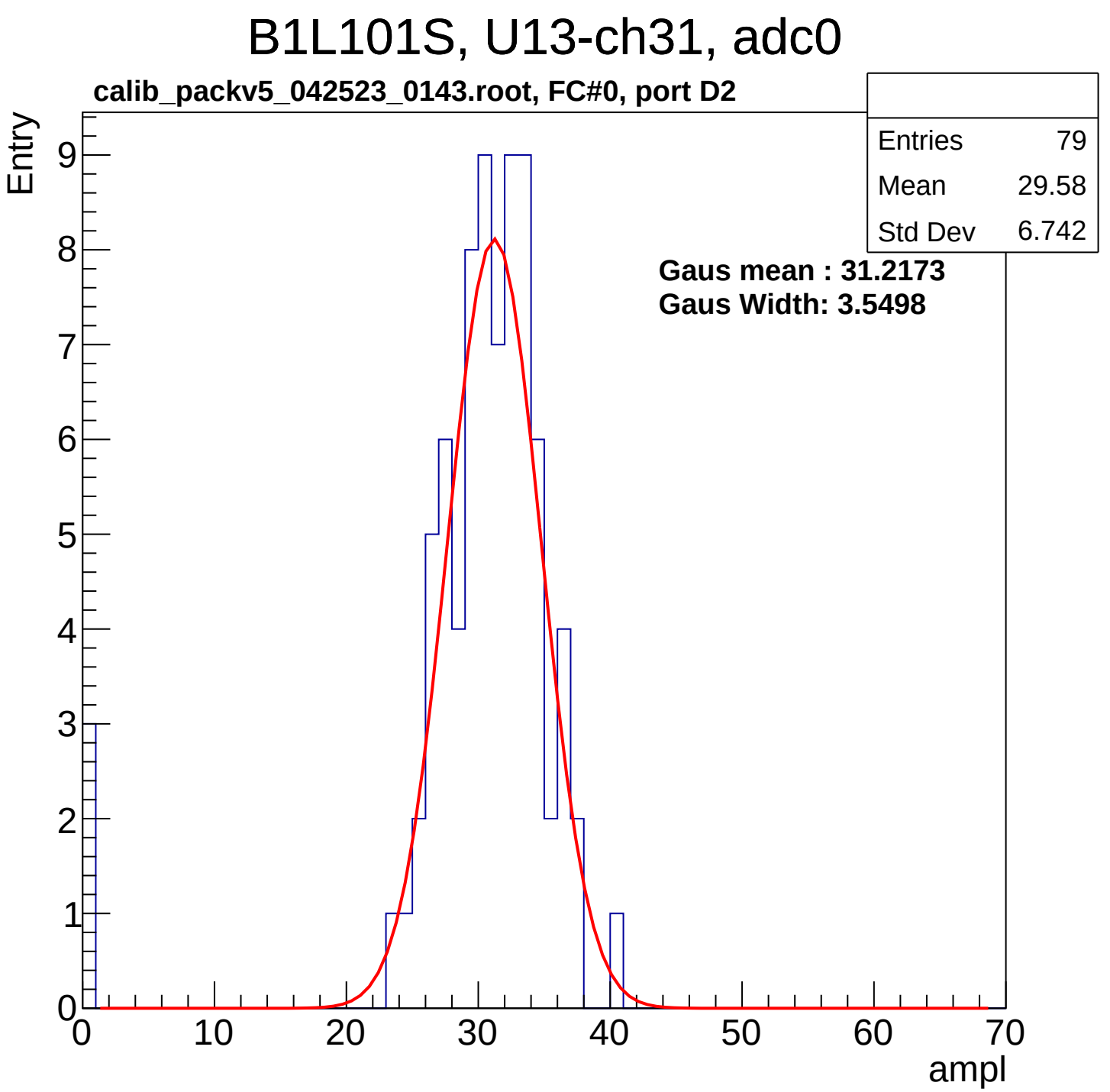
B1L101S, U13-ch31, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 79
Mean 29.58
Std Dev 6.742
Gaus mean : 31.2173
Gaus Width: 3.5498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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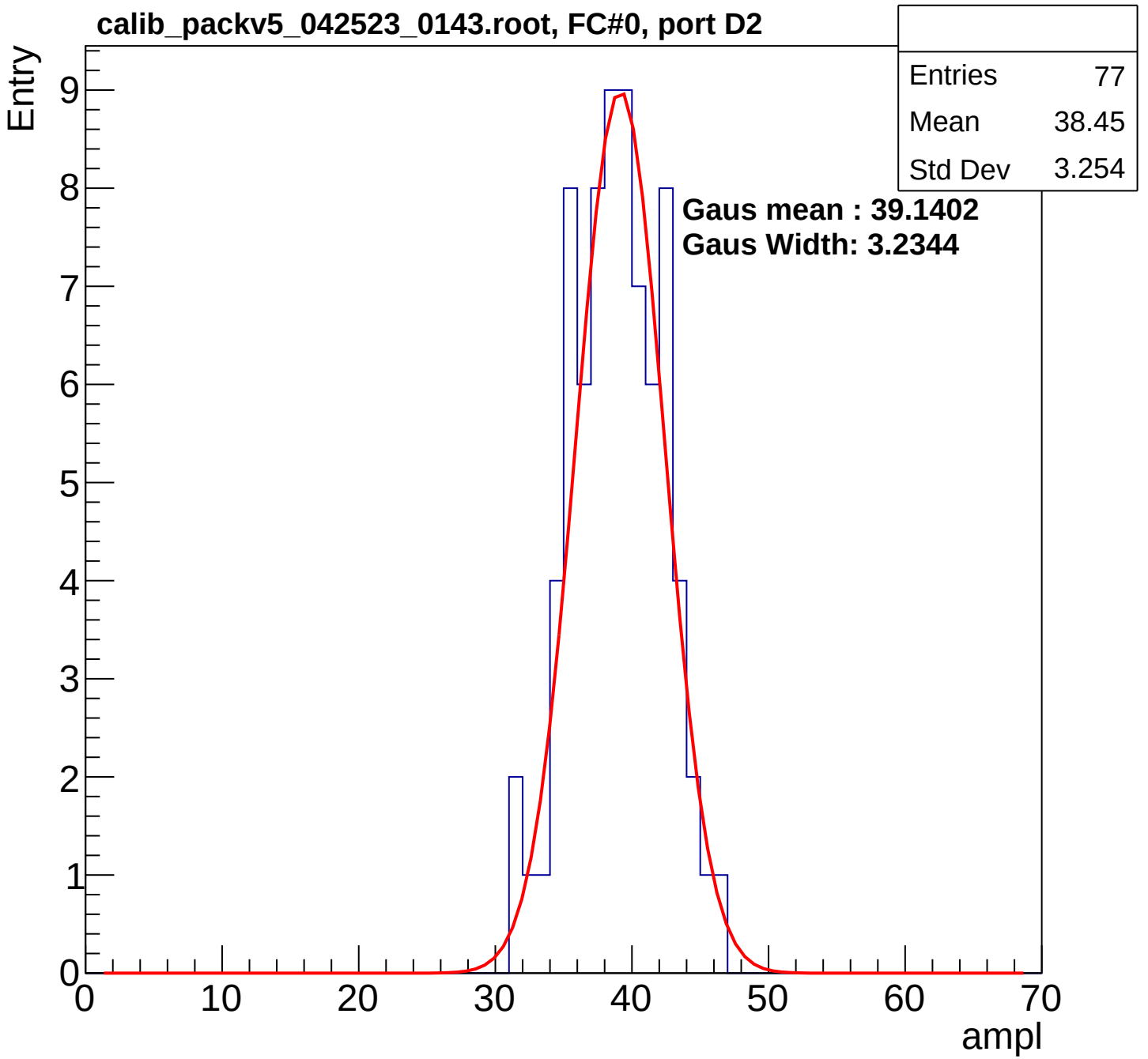
B1L101S, U13-ch31, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 38.45
Std Dev 3.254
Gaus mean : 39.1402
Gaus Width: 3.2344
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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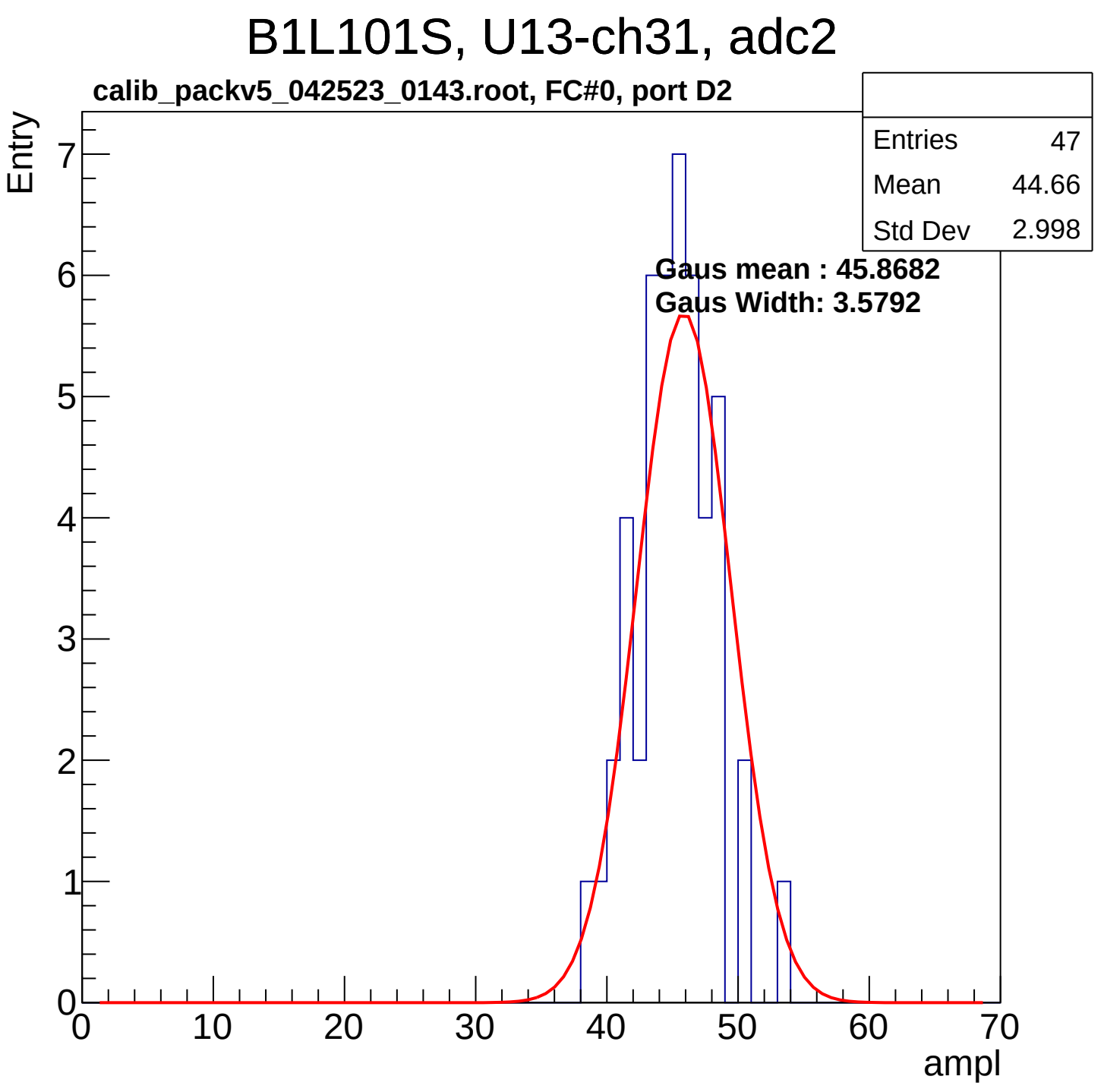
B1L101S, U13-ch31, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 47
Mean 44.66
Std Dev 2.998
Gaus mean : 45.8682
Gaus Width: 3.5792
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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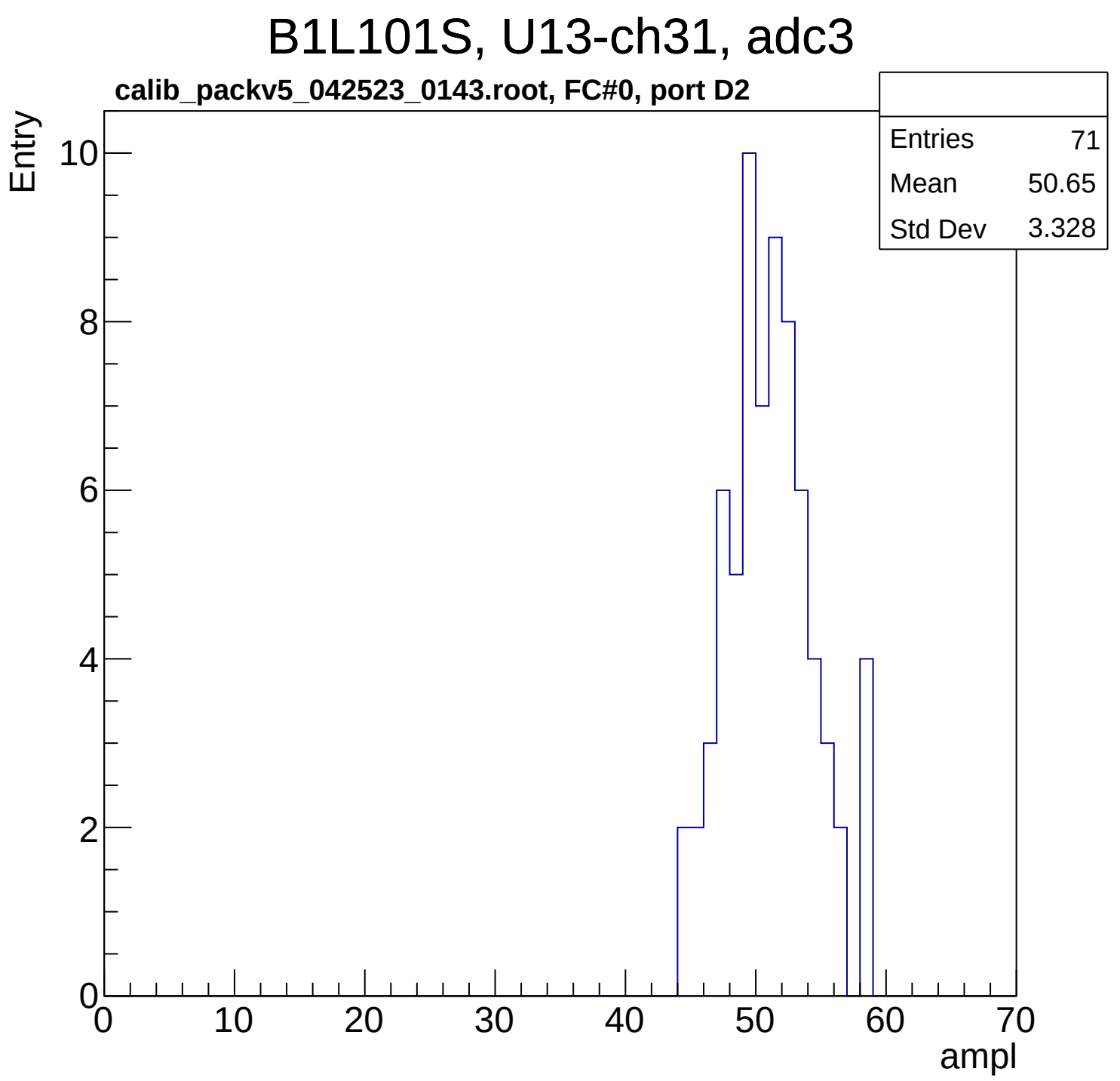
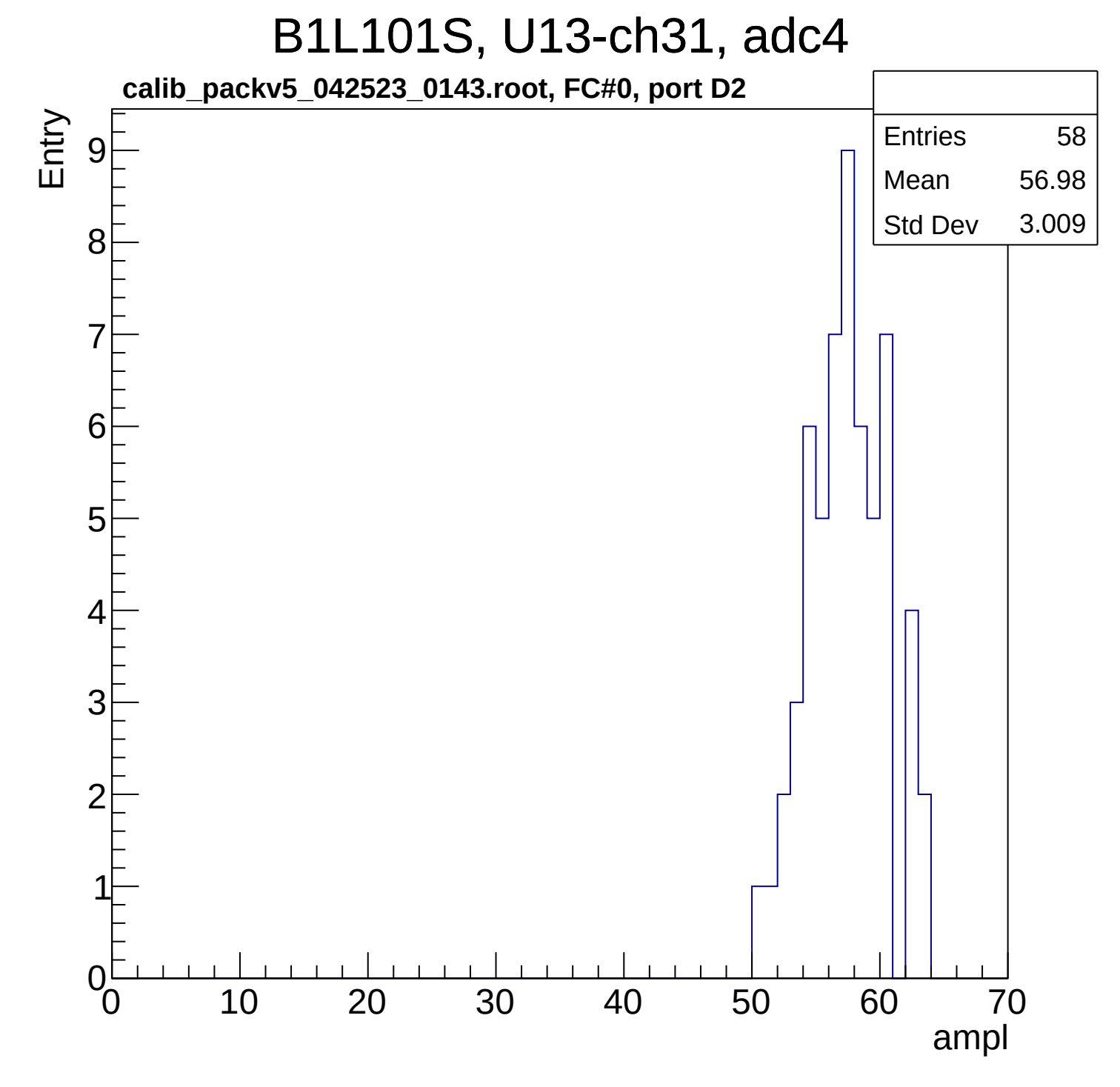

B1L101S, U13-ch31, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 50.65
Std Dev 3.328
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch31, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 56.98
Std Dev 3.009
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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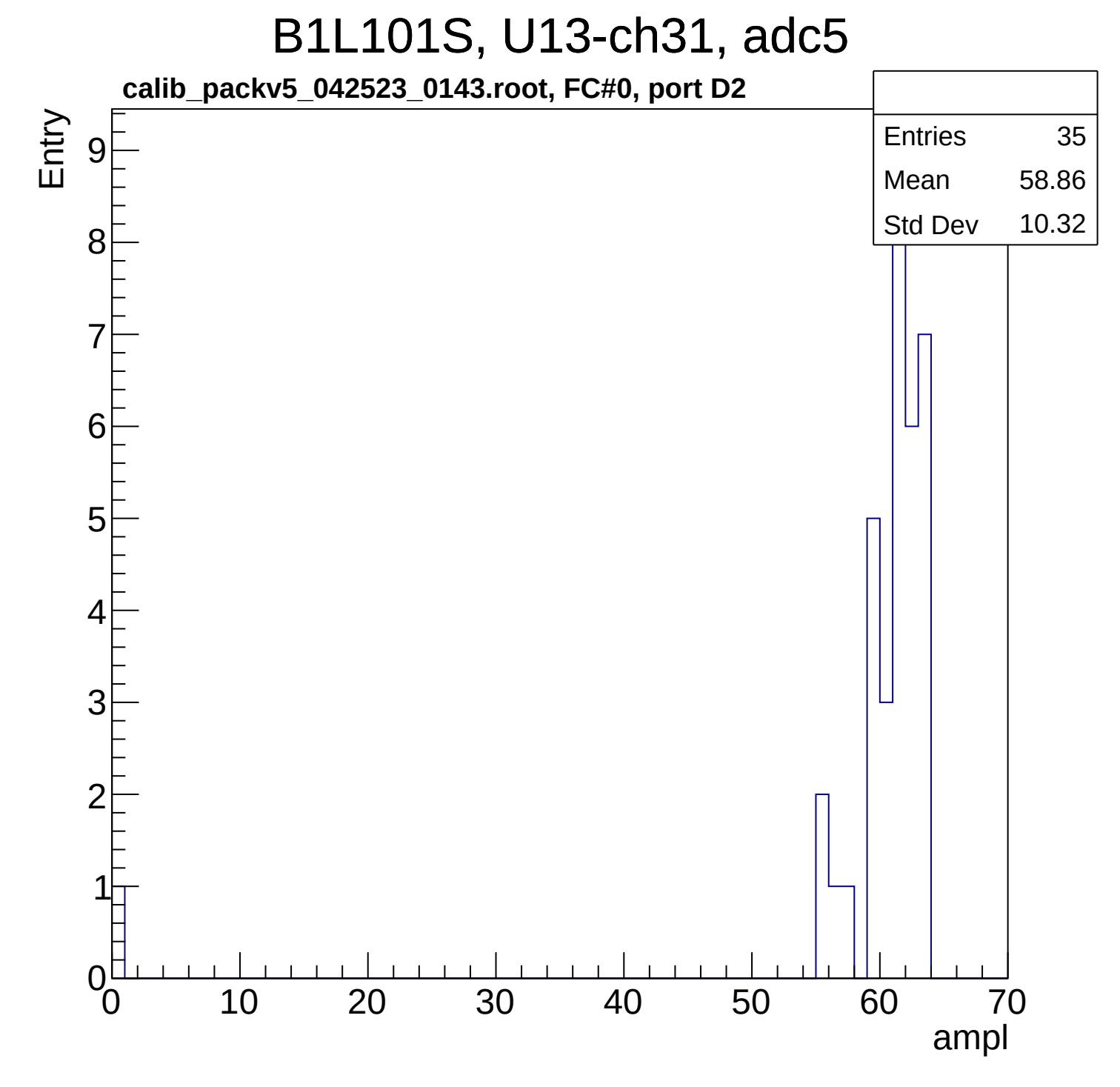
B1L101S, U13-ch31, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 35
Mean 58.86
Std Dev 10.32
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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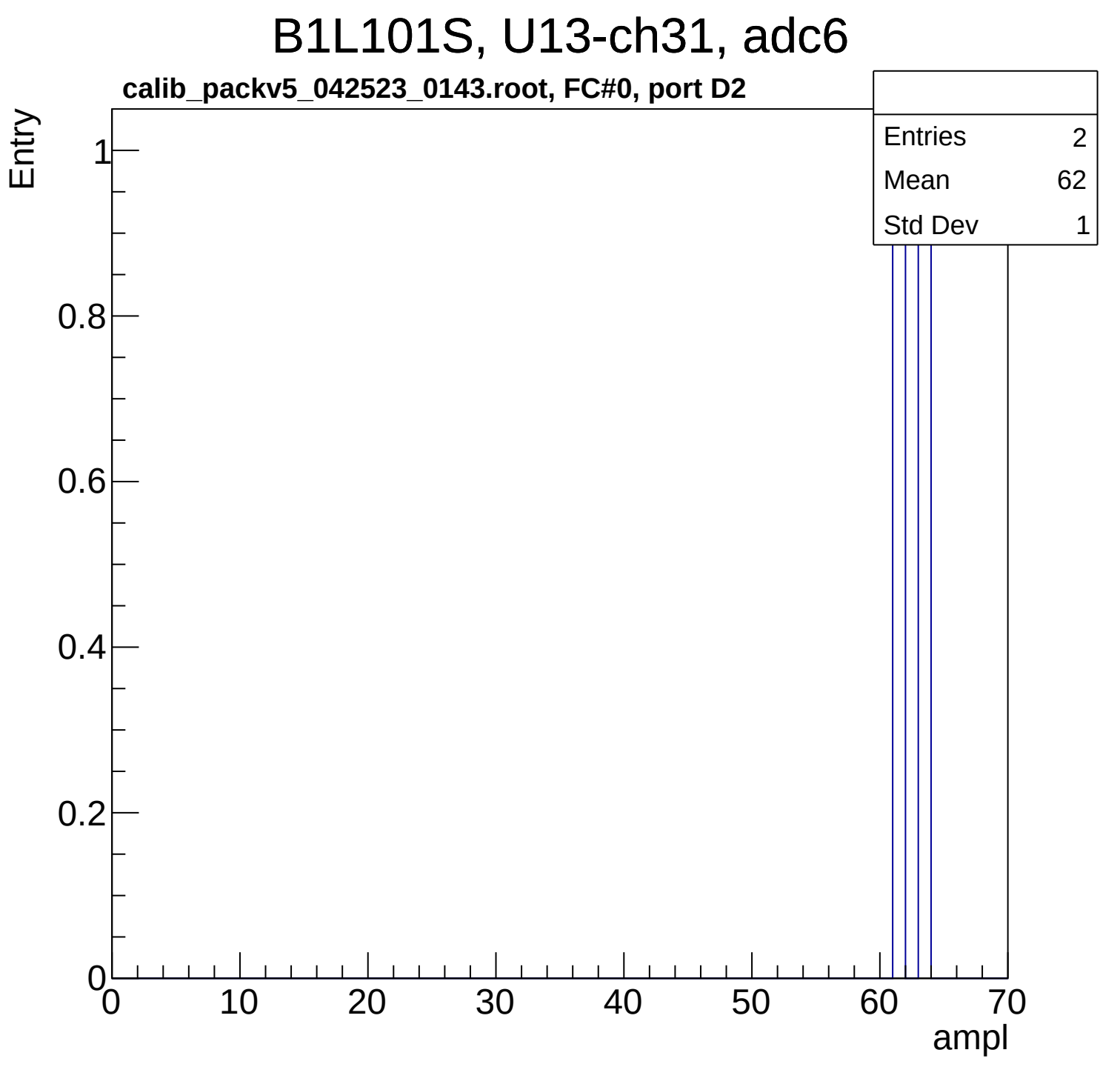
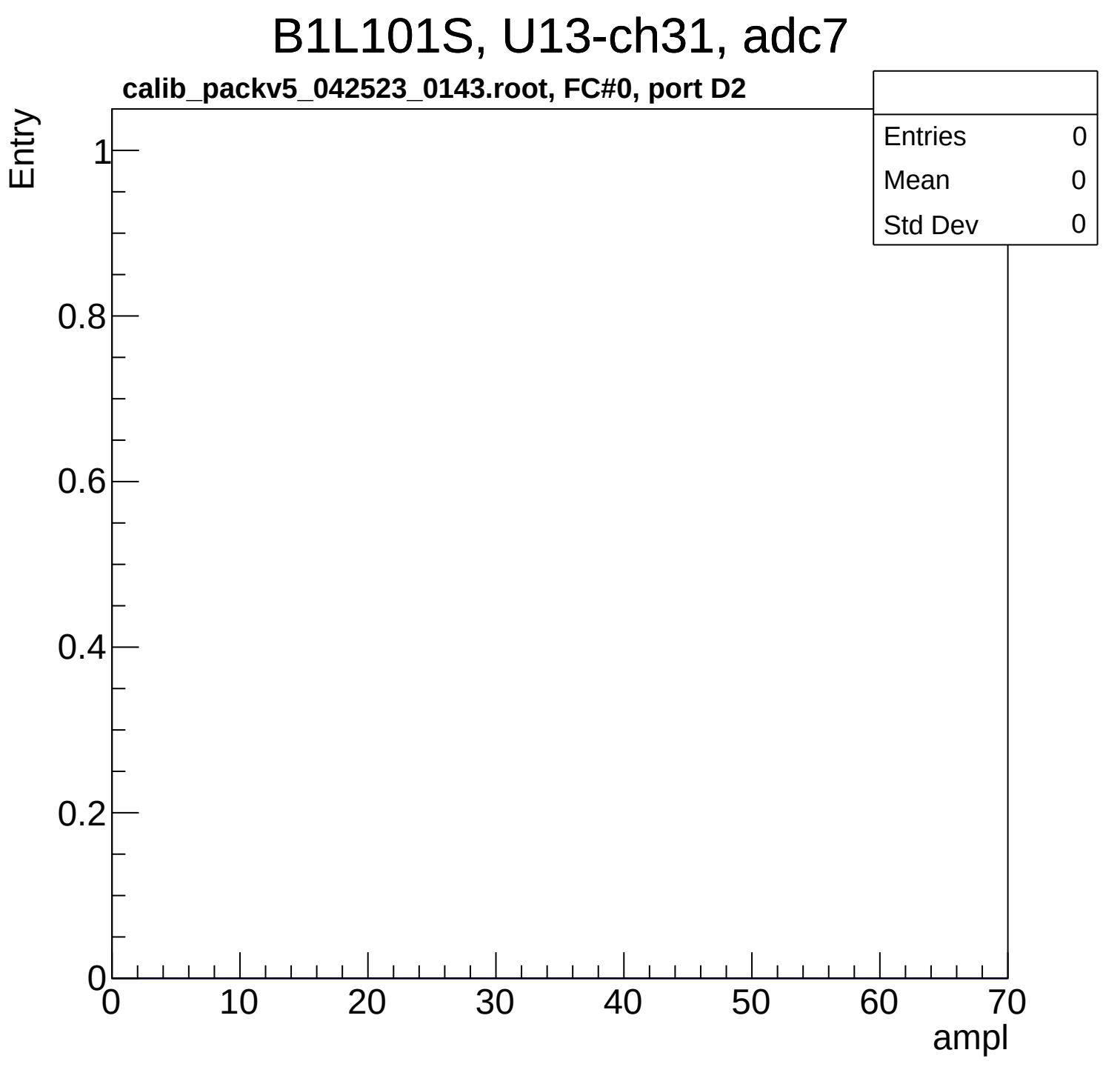
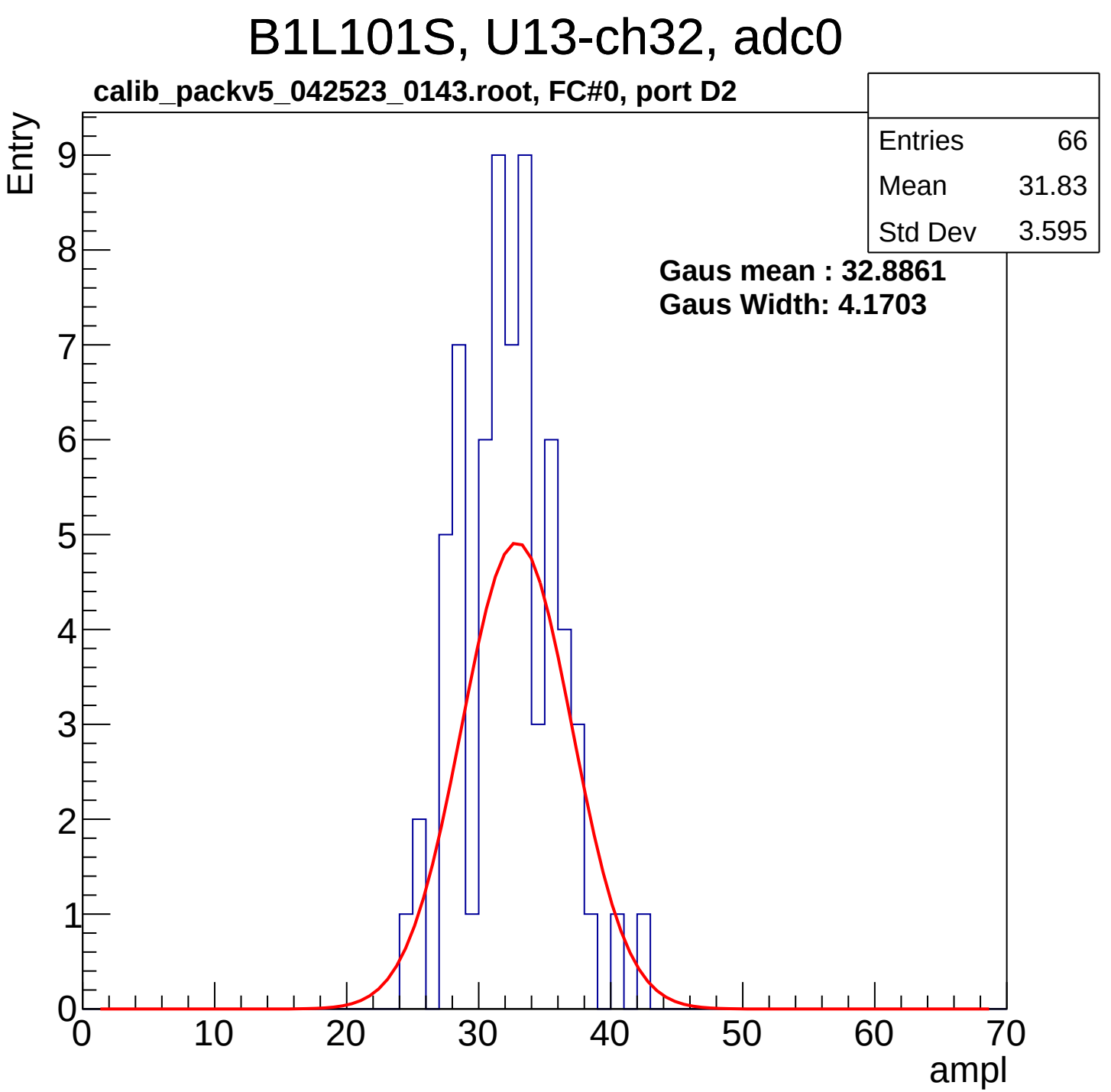
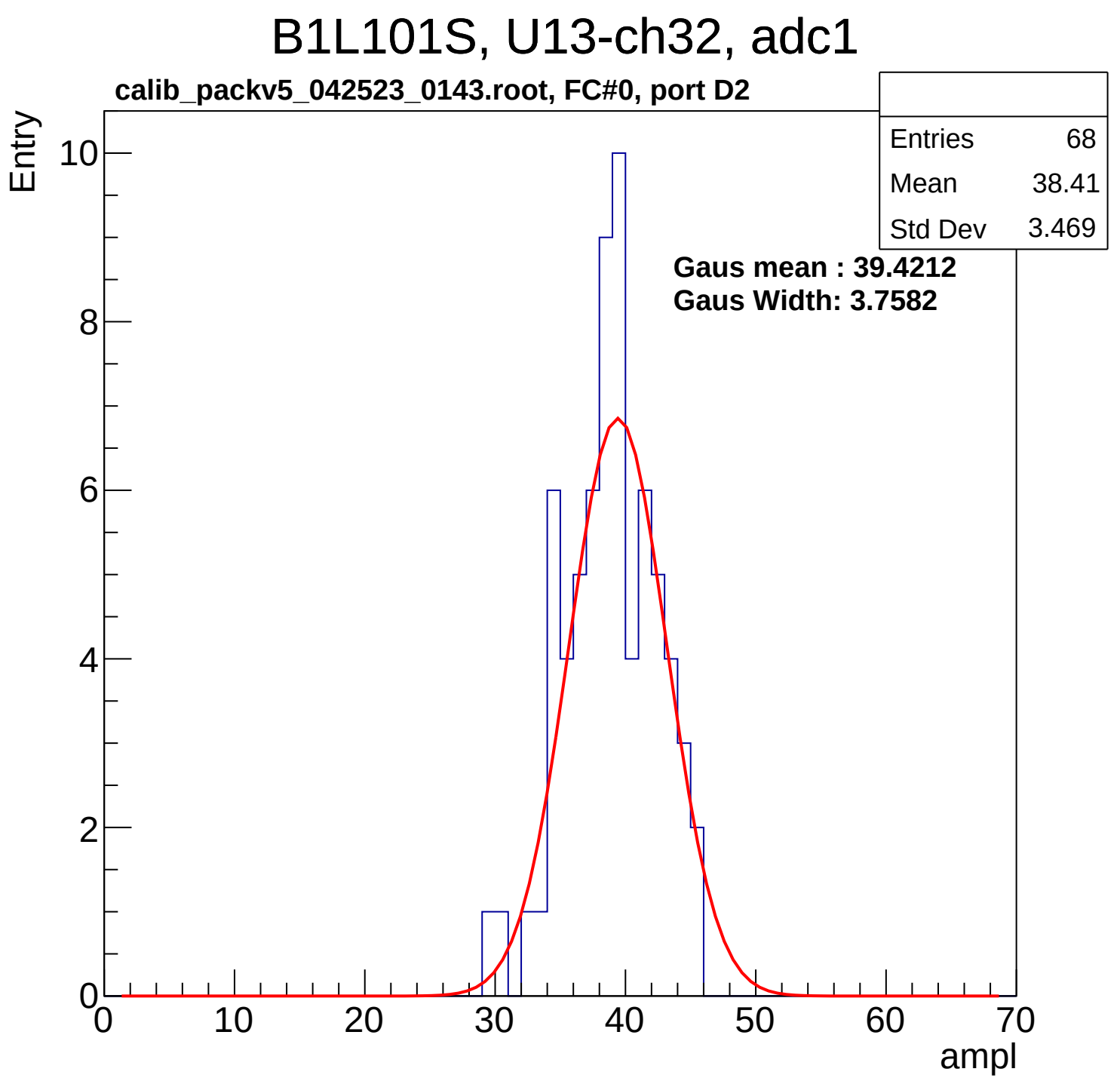
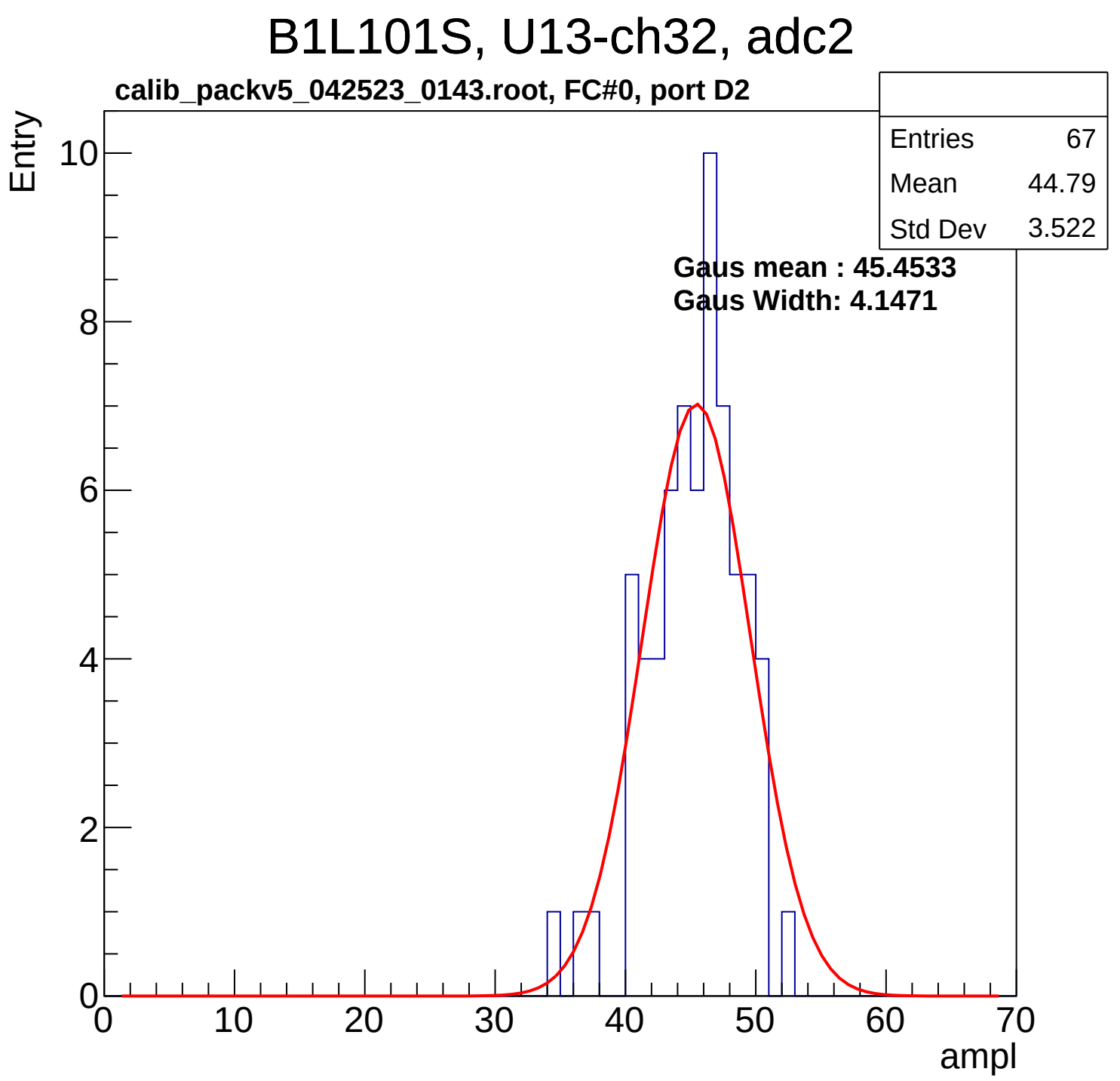
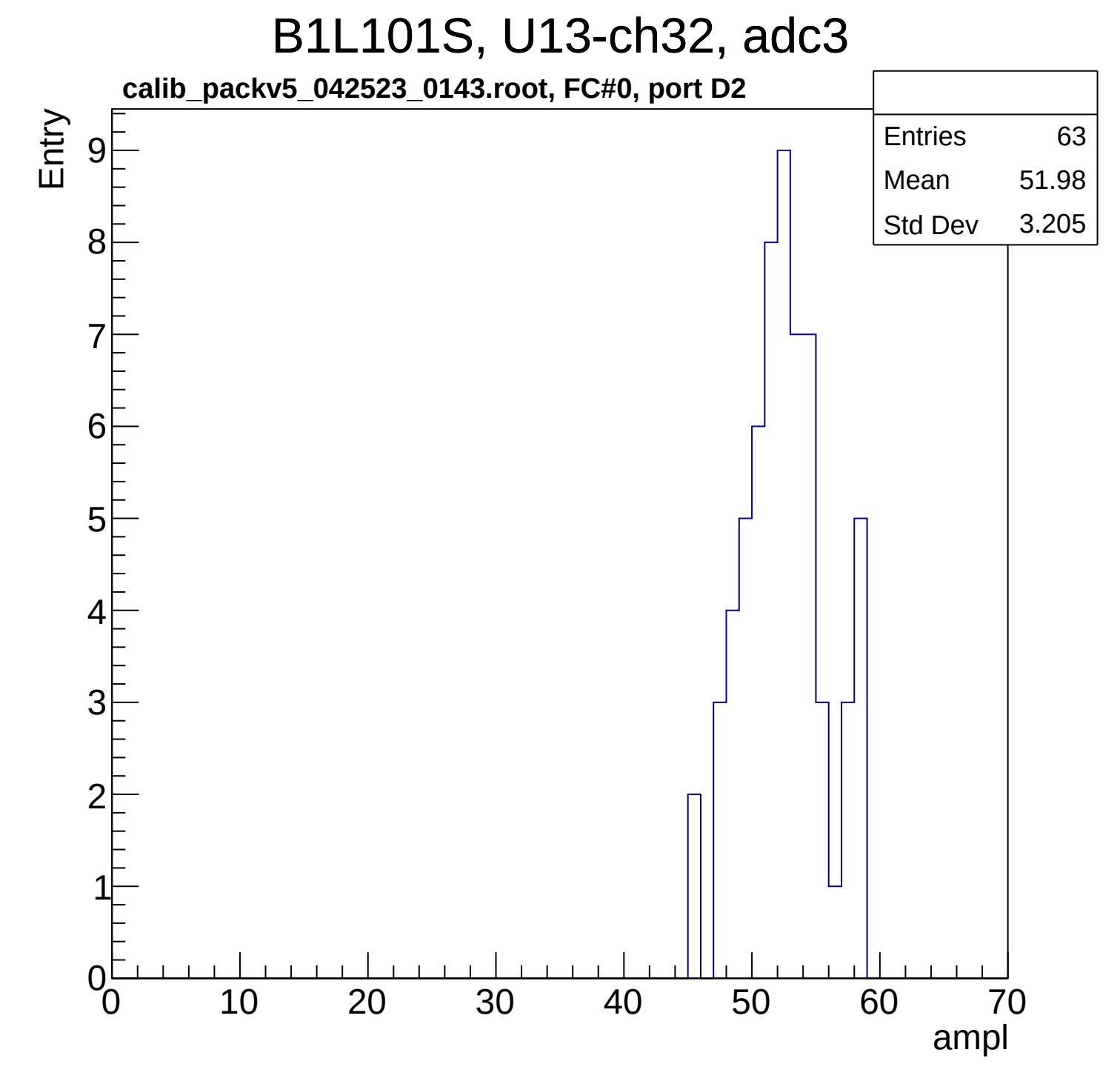

B1L101S, U13-ch31, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch31, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch32, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 31.83
Std Dev 3.595
Gaus mean : 32.8861
Gaus Width: 4.1703
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch32, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 38.41
Std Dev 3.469
Gaus mean : 39.4212
Gaus Width: 3.7582
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch32, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 44.79
Std Dev 3.522
Gaus mean : 45.4533
Gaus Width: 4.1471
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch32, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 51.98
Std Dev 3.205
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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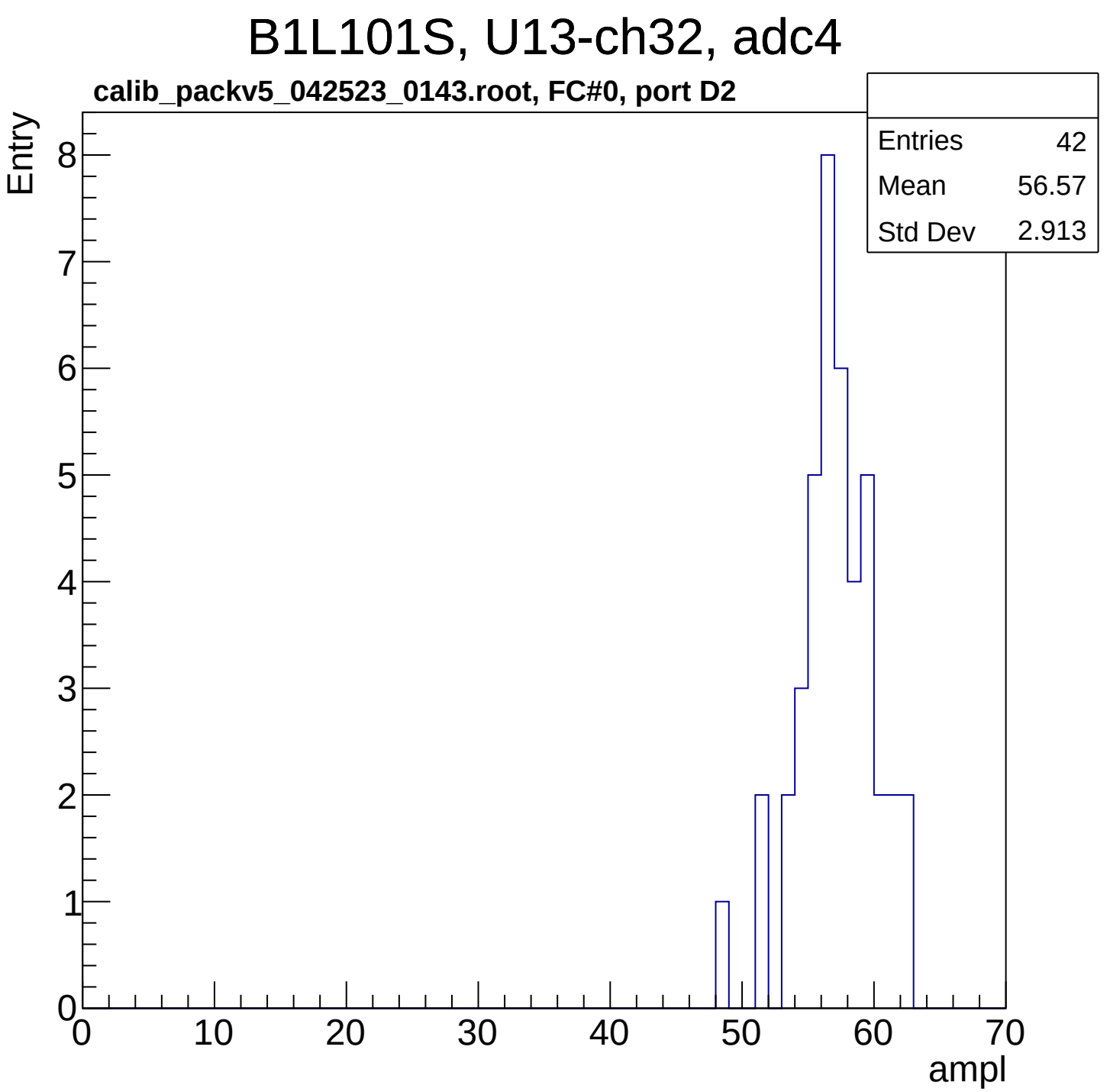
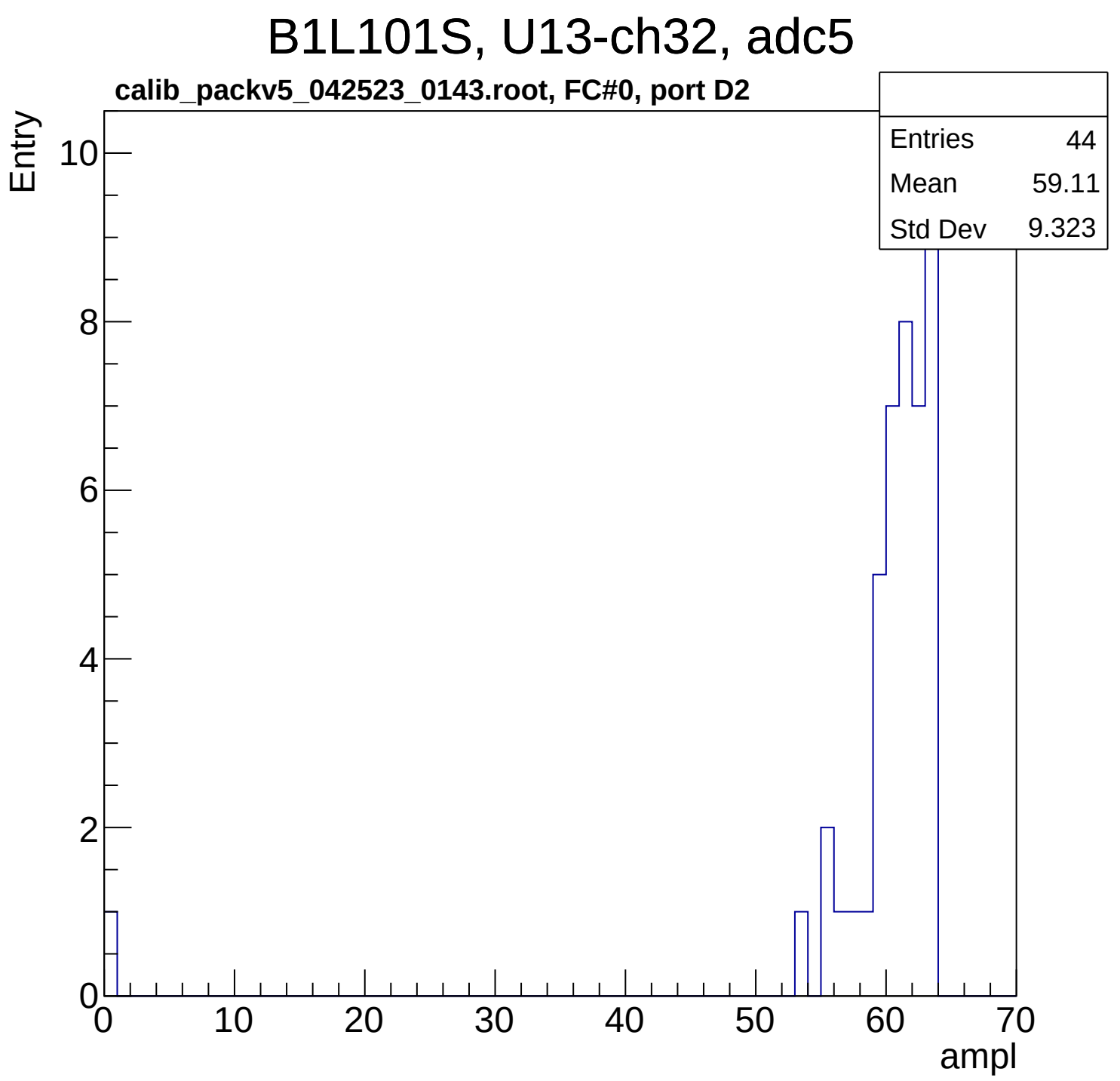
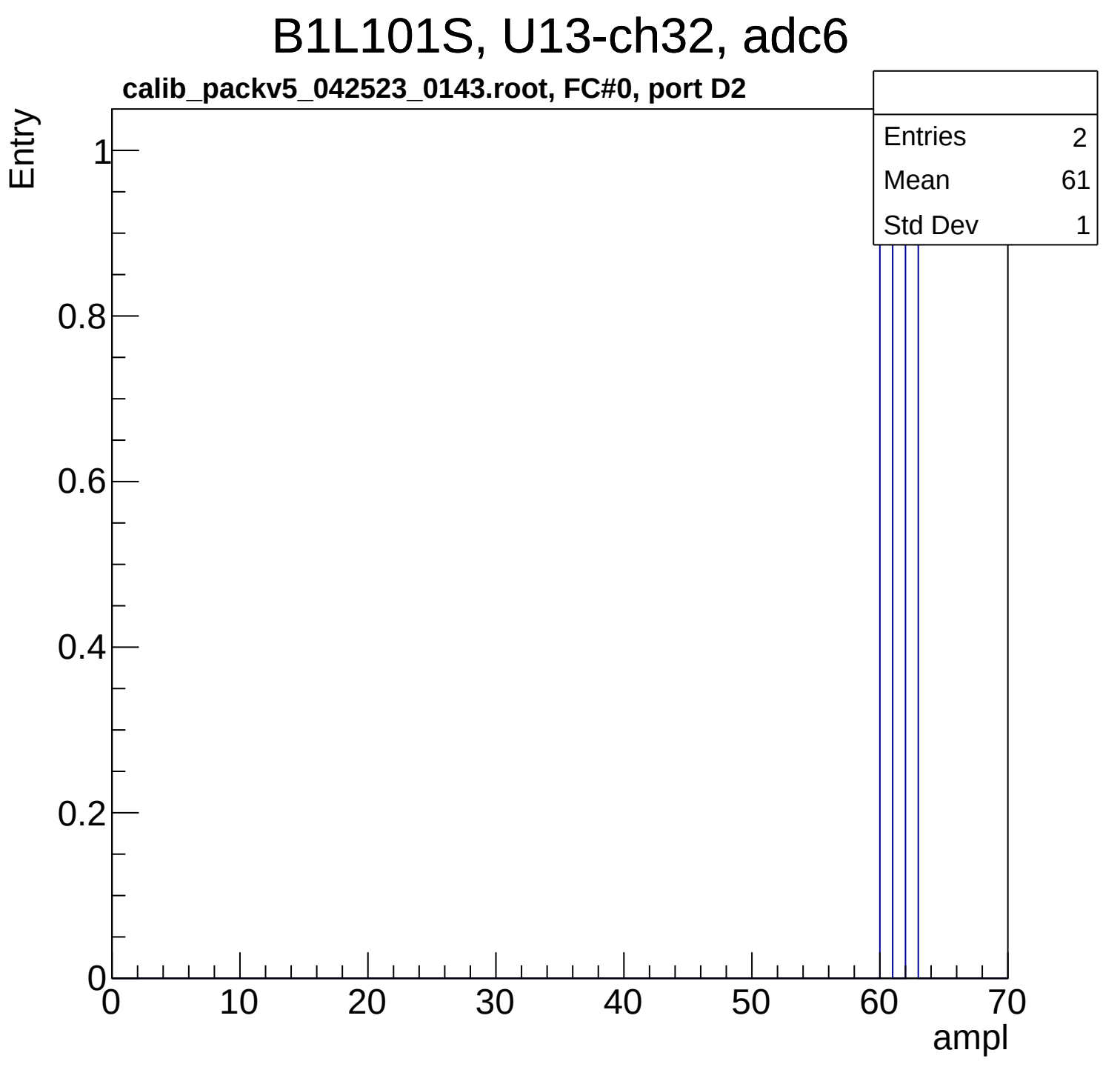
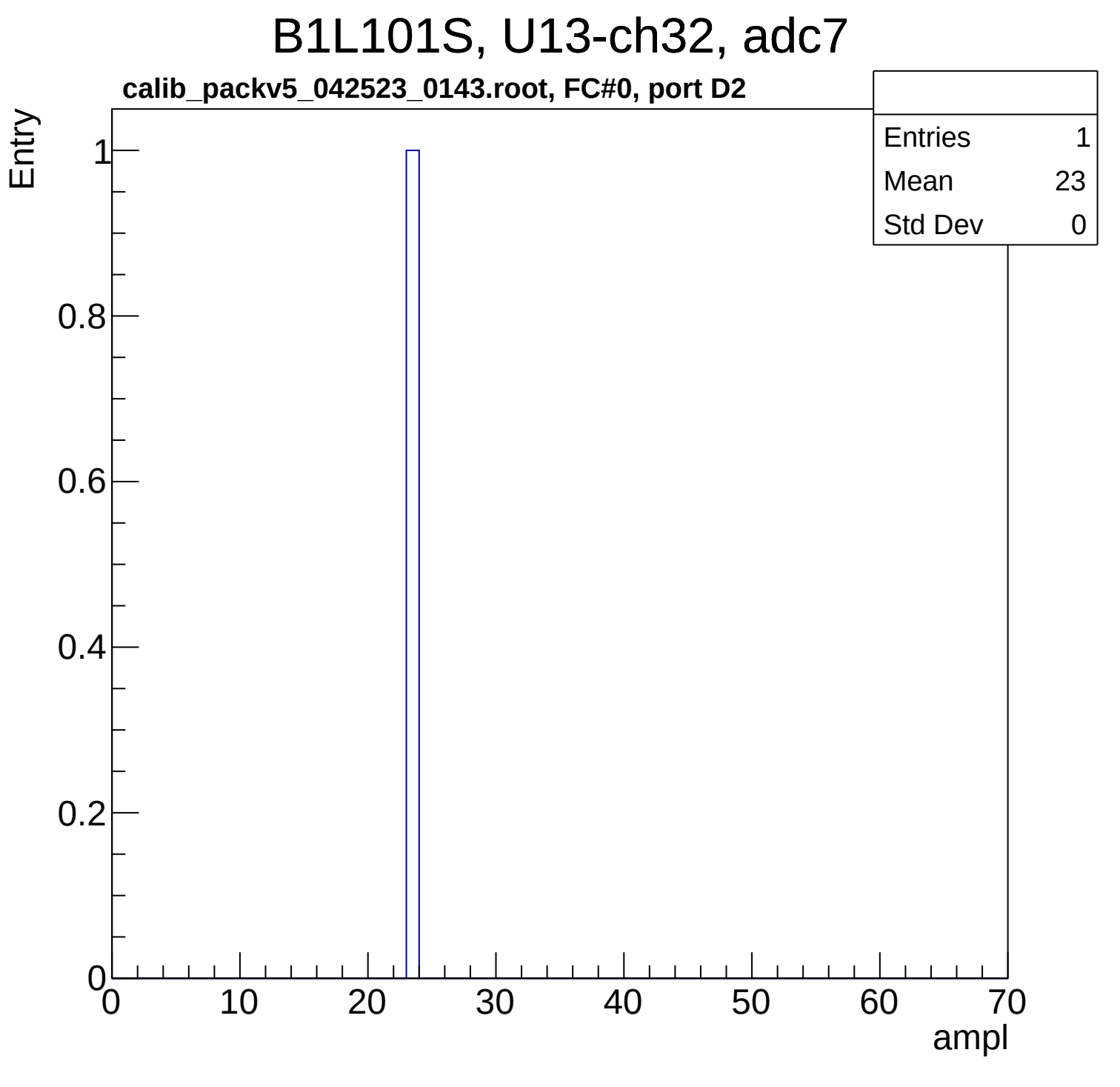
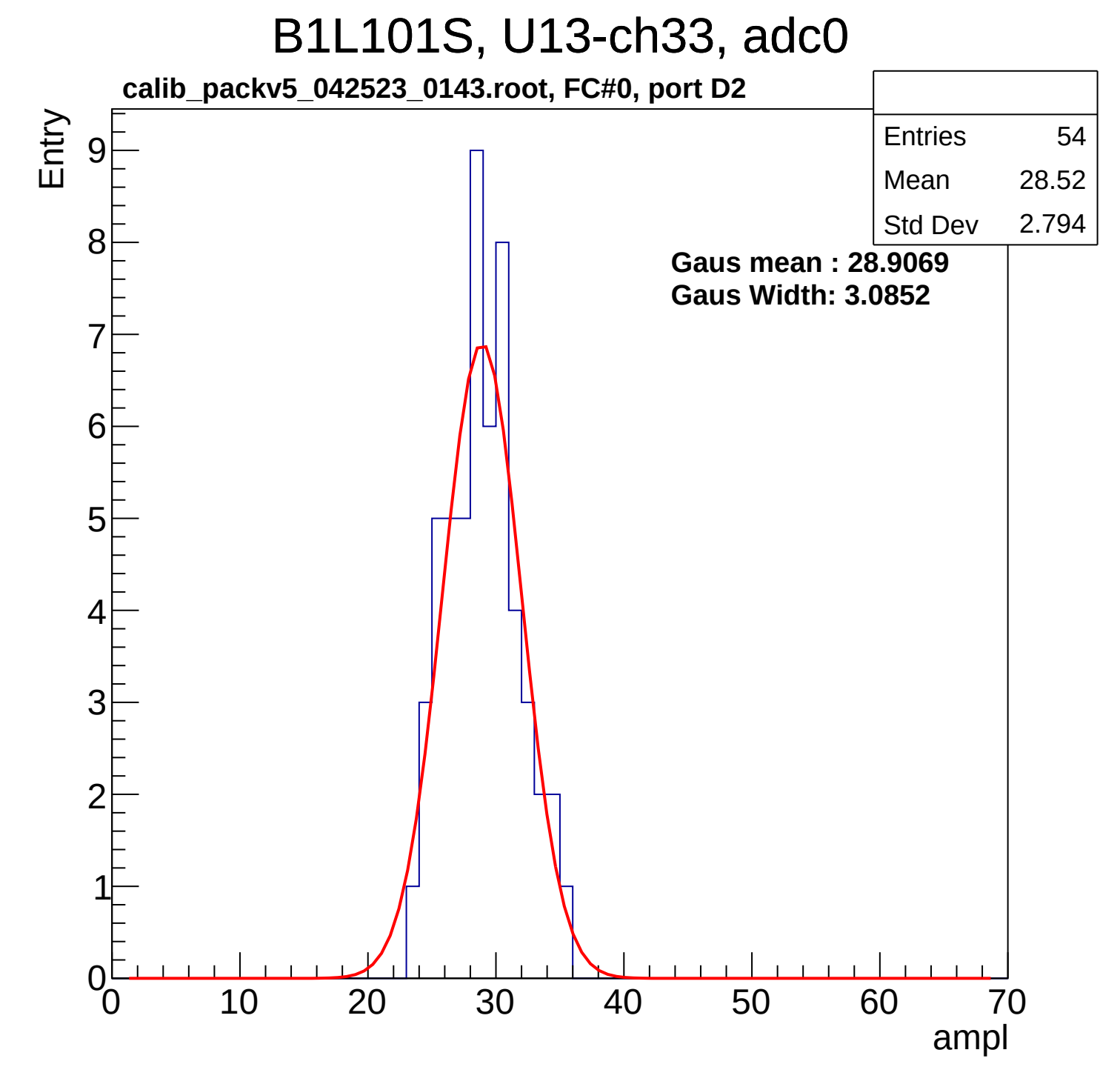
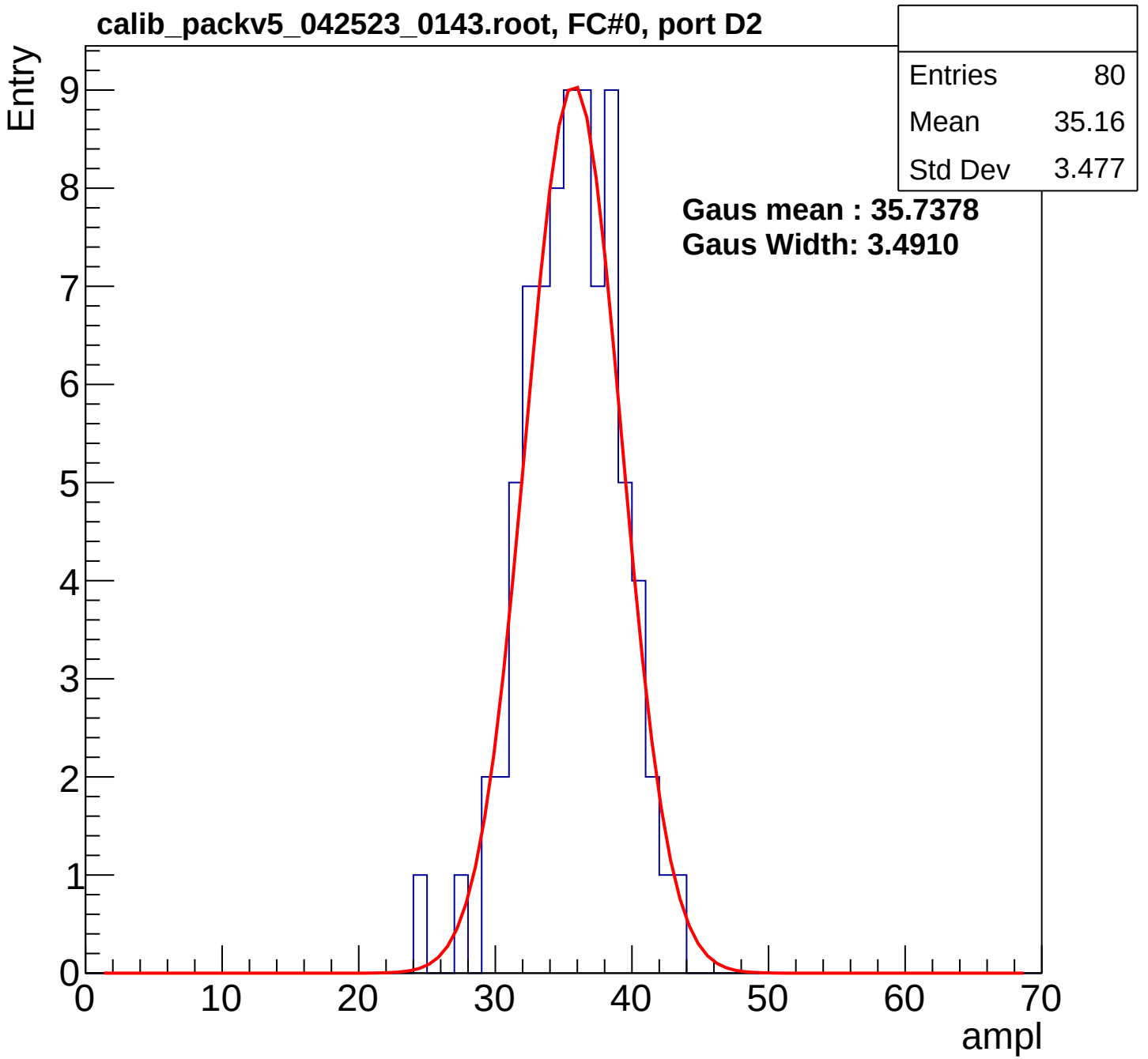

B1L101S, U13-ch32, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 56.57
Std Dev 2.913
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

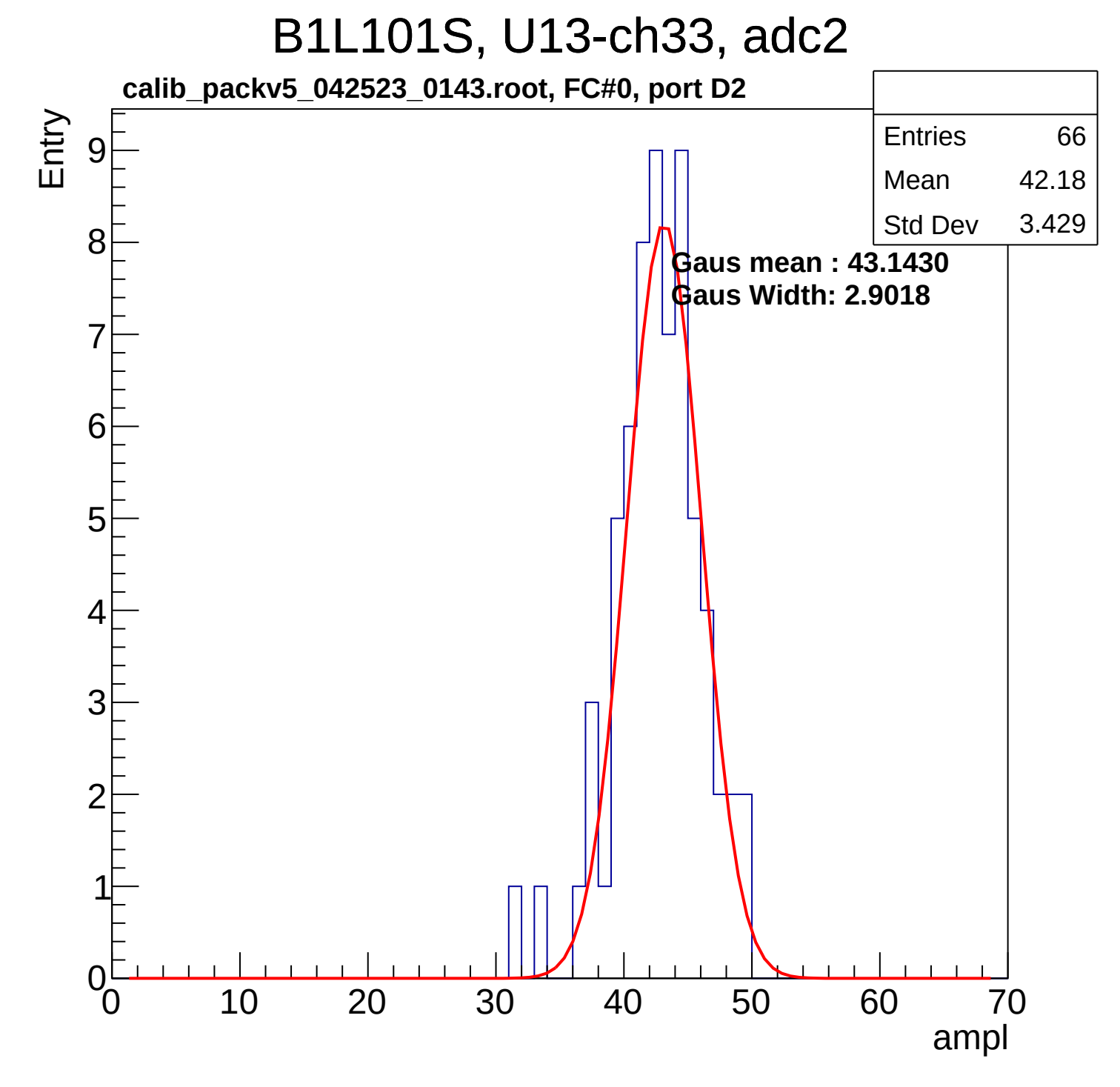

B1L101S, U13-ch32, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 59.11
Std Dev 9.323
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch32, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch32, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch33, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 28.52
Std Dev 2.794
Gaus mean : 28.9069
Gaus Width: 3.0852
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch33, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 35.16
Std Dev 3.477
Gaus mean : 35.7378
Gaus Width: 3.4910
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch33, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 42.18
Std Dev 3.429
Gaus mean : 43.1430
Gaus Width: 2.9018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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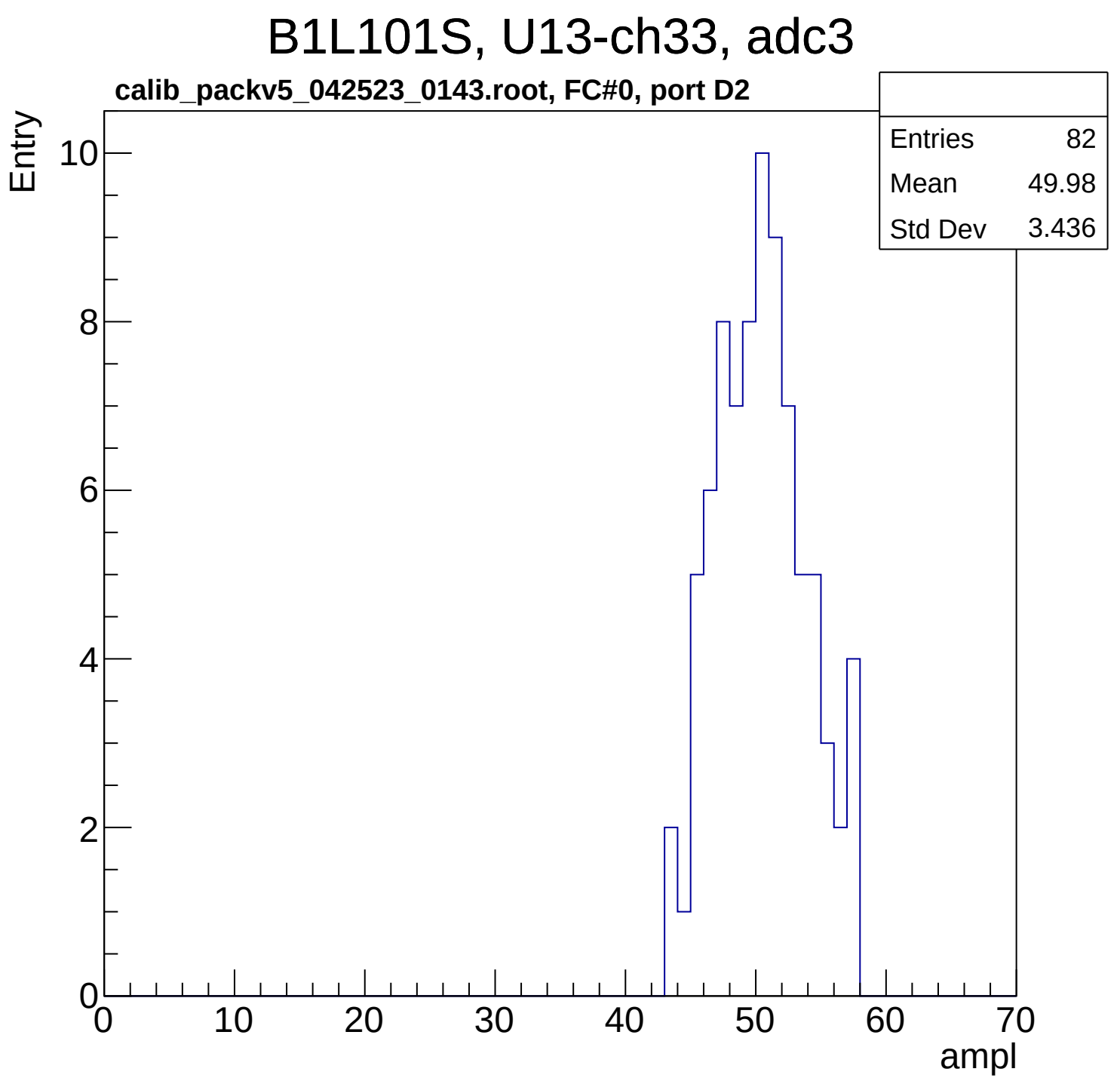
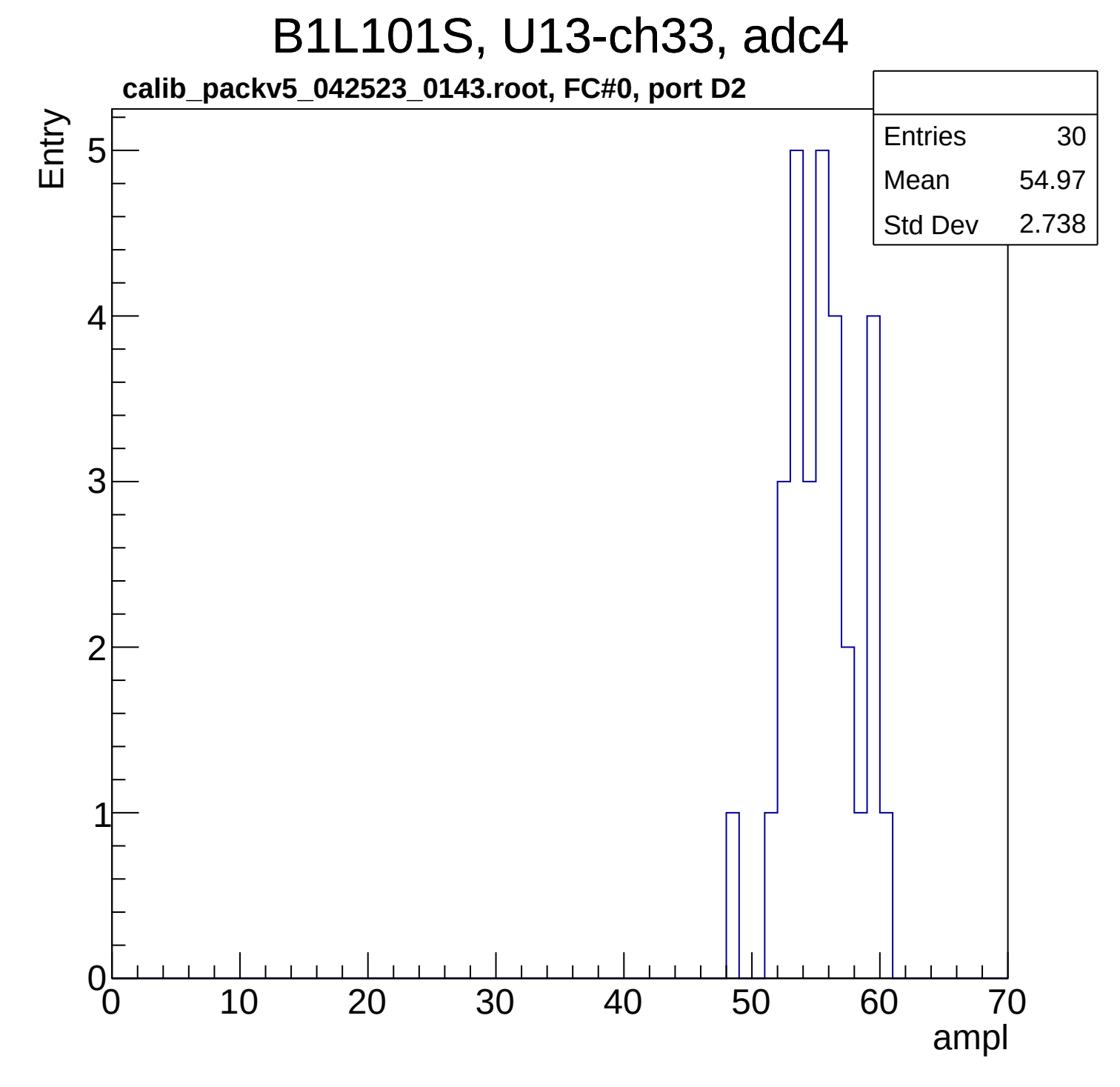
40
50
60
70


B1L101S, U13-ch33, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 82
Mean 49.98
Std Dev 3.436
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch33, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 30
Mean 54.97
Std Dev 2.738
Entry
ampl
0
1
2
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4
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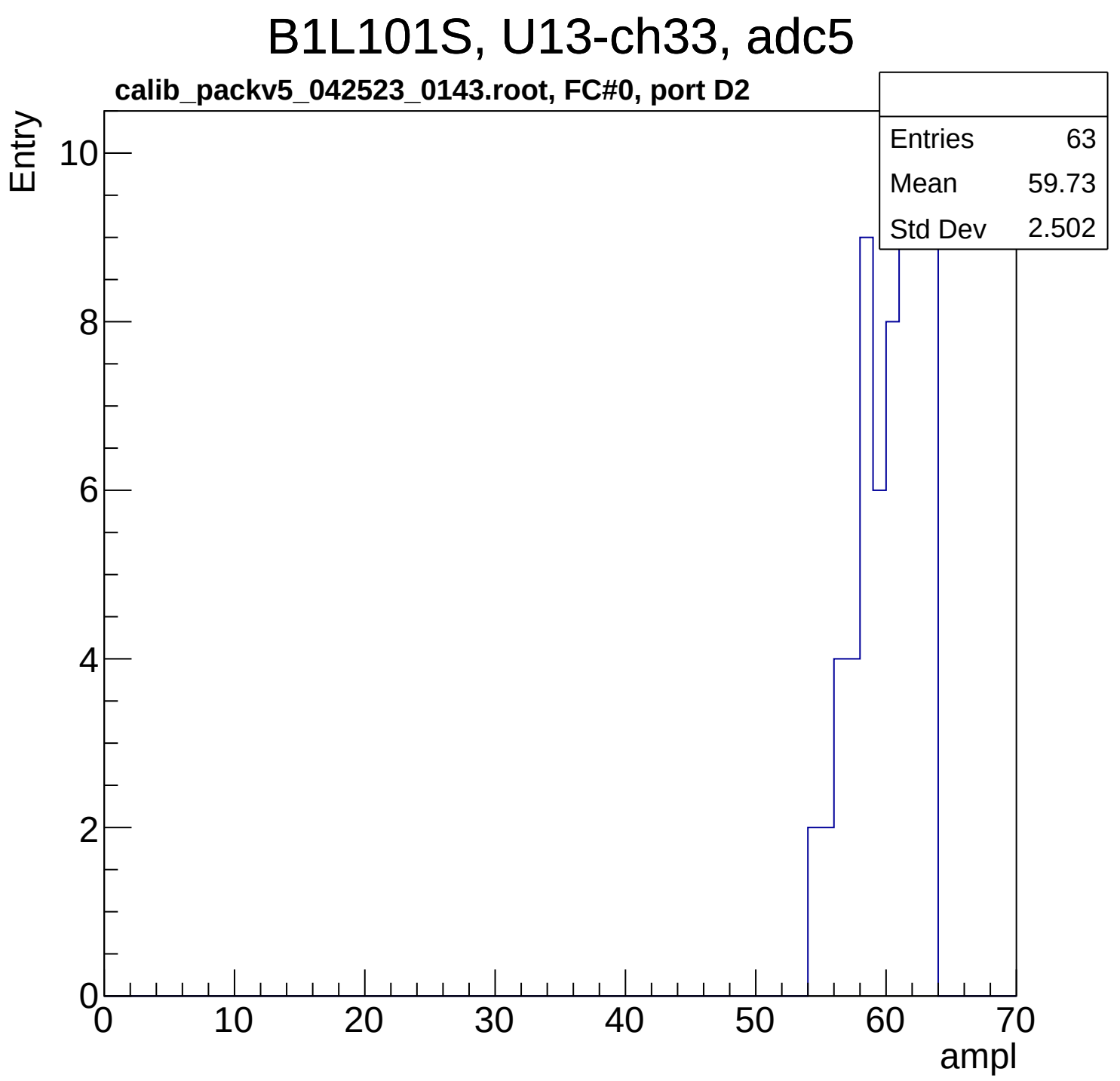
0
10
20
30
40
50
60
70


B1L101S, U13-ch33, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 59.73
Std Dev 2.502
Entry
0
2
4
6
8
10
0
10
20
30
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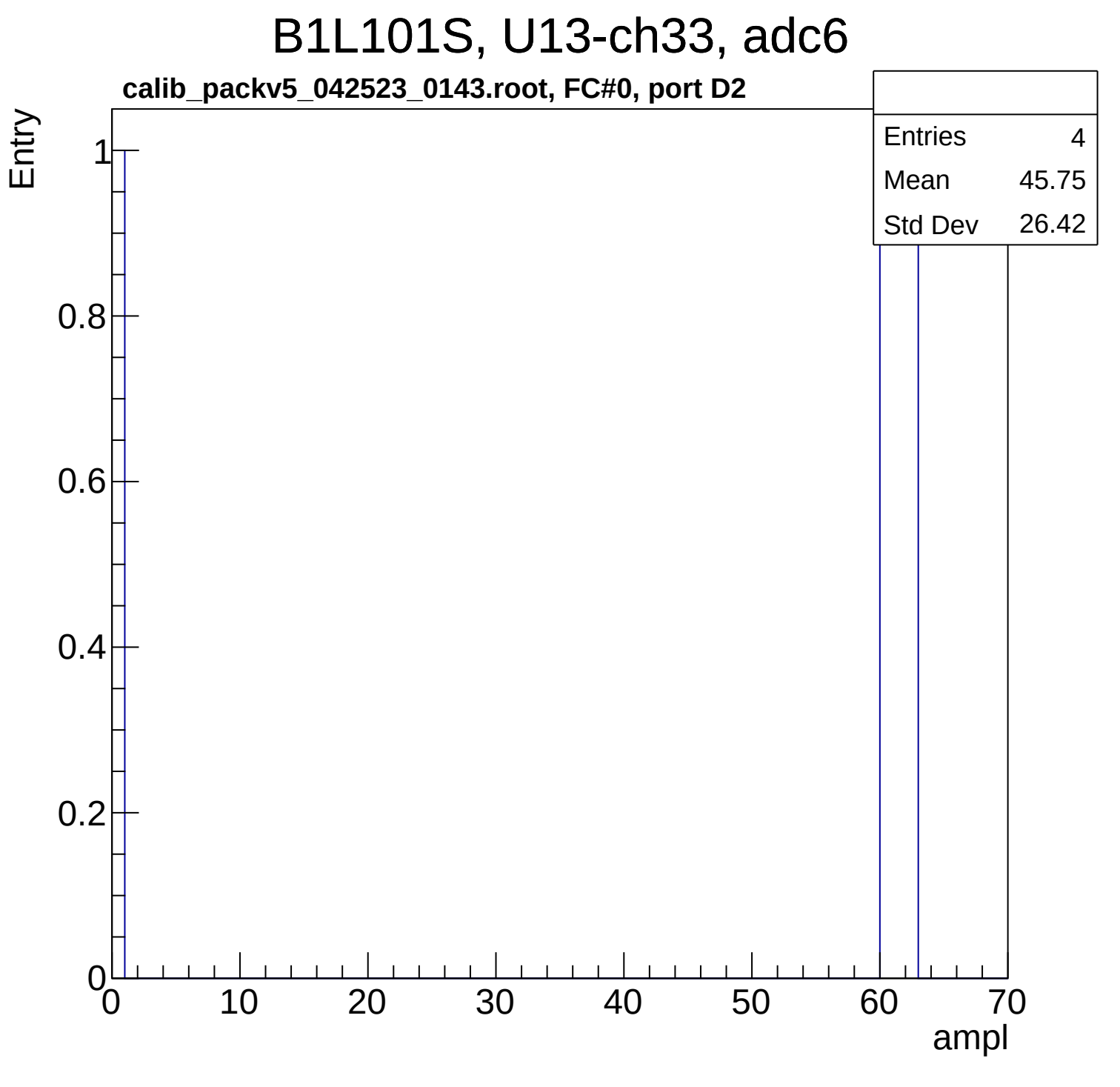
50
60
70
ampl


B1L101S, U13-ch33, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 45.75
Std Dev 26.42
Entry
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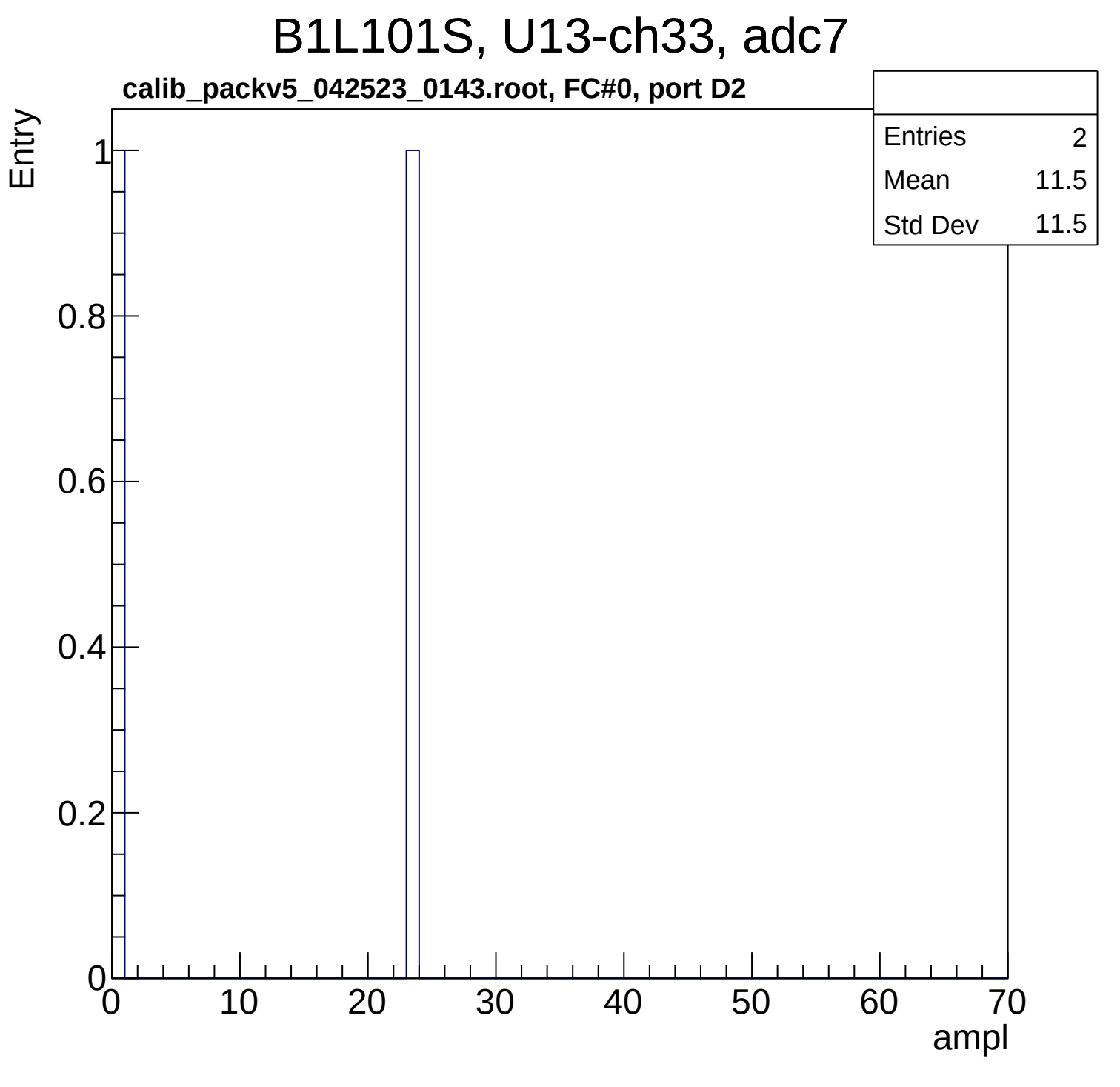
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch33, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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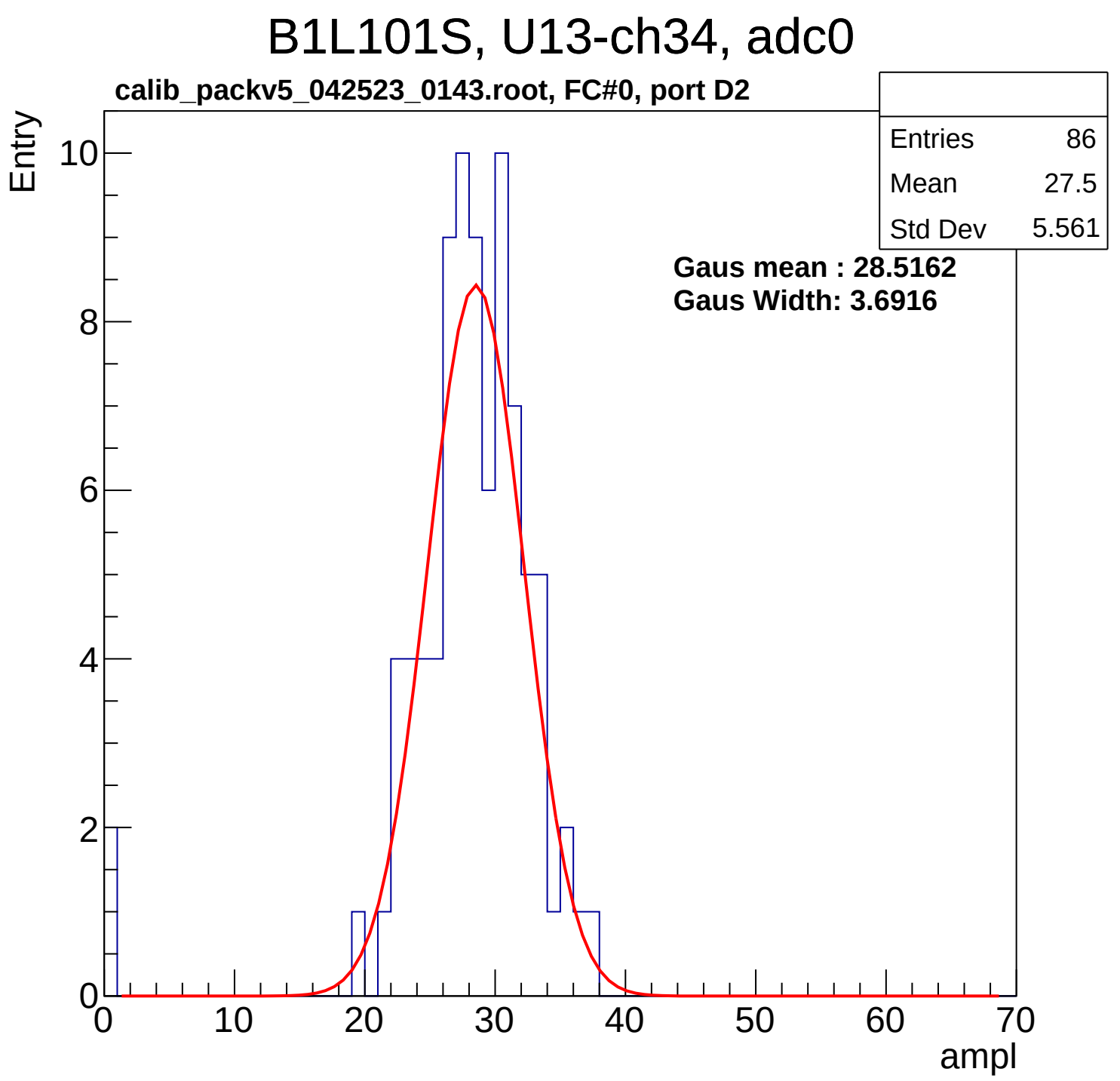
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch34, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 86
Mean 27.5
Std Dev 5.561
Gaus mean : 28.5162
Gaus Width: 3.6916
Entry
ampl
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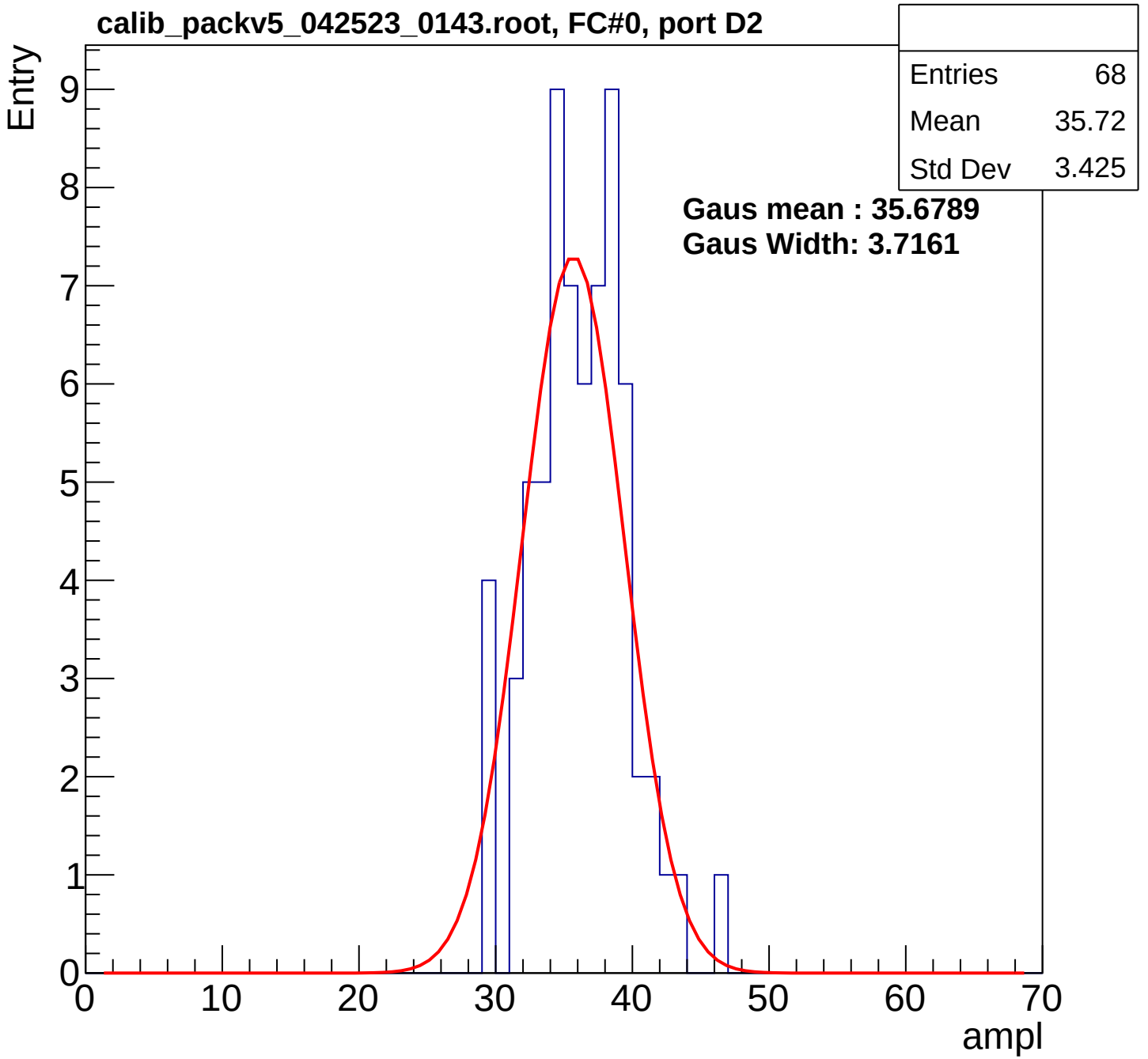
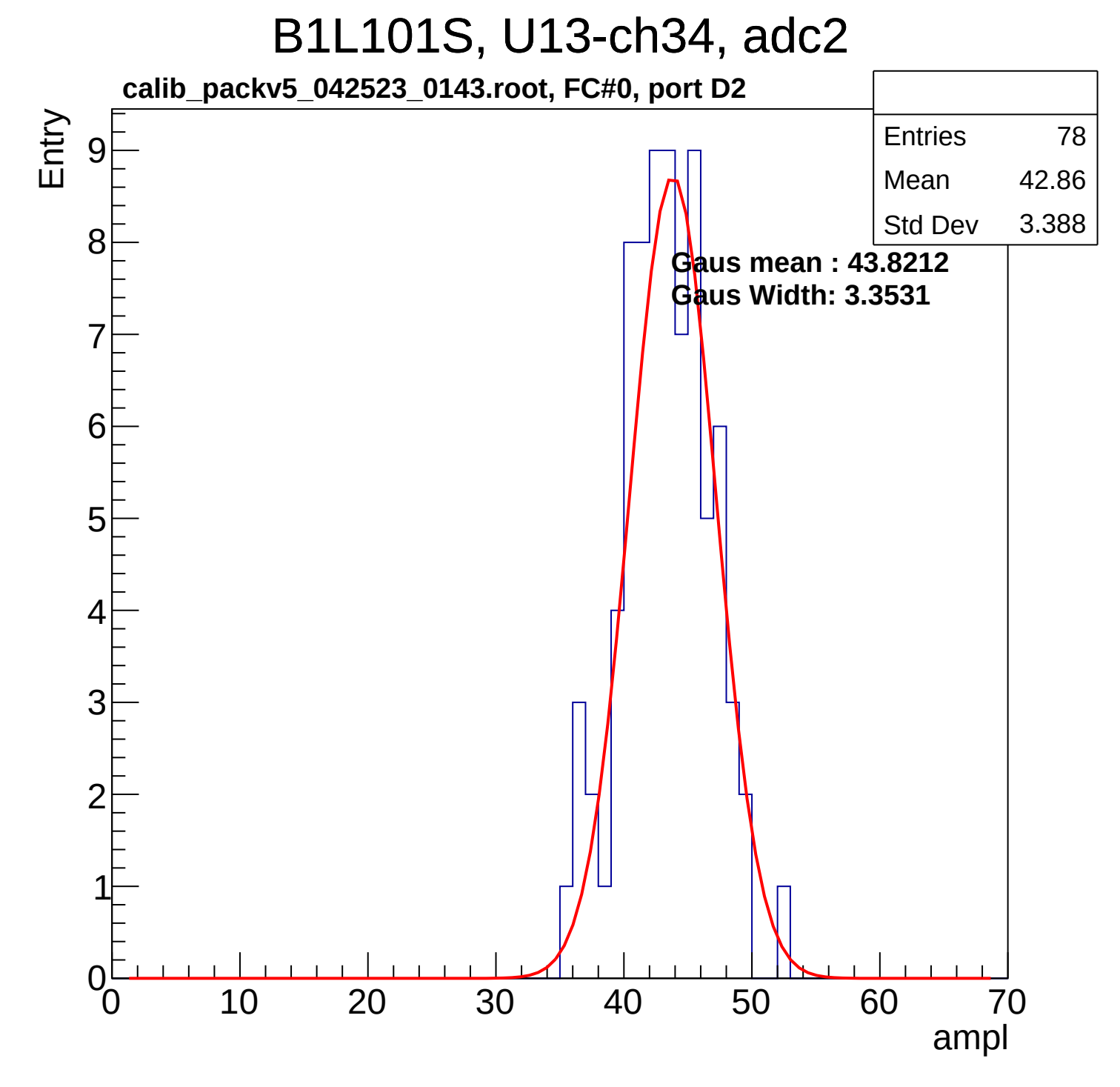
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch34, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 35.72
Std Dev 3.425
Gaus mean : 35.6789
Gaus Width: 3.7161
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L101S, U13-ch34, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 42.86
Std Dev 3.388
Gaus mean : 43.8212
Gaus Width: 3.3531
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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60
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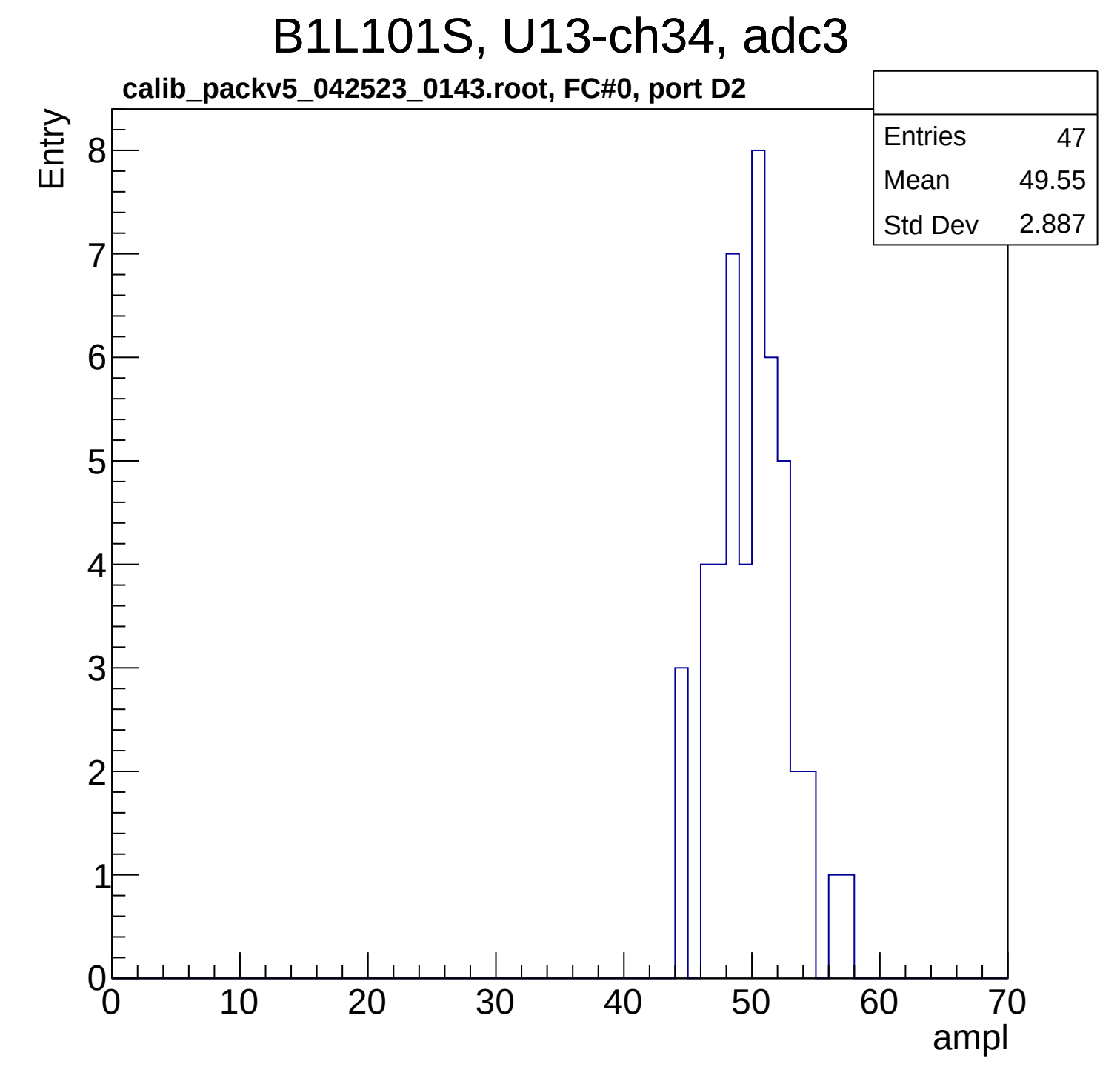
B1L101S, U13-ch34, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 47
Mean 49.55
Std Dev 2.887
Entry
ampl
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2
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4
5
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7
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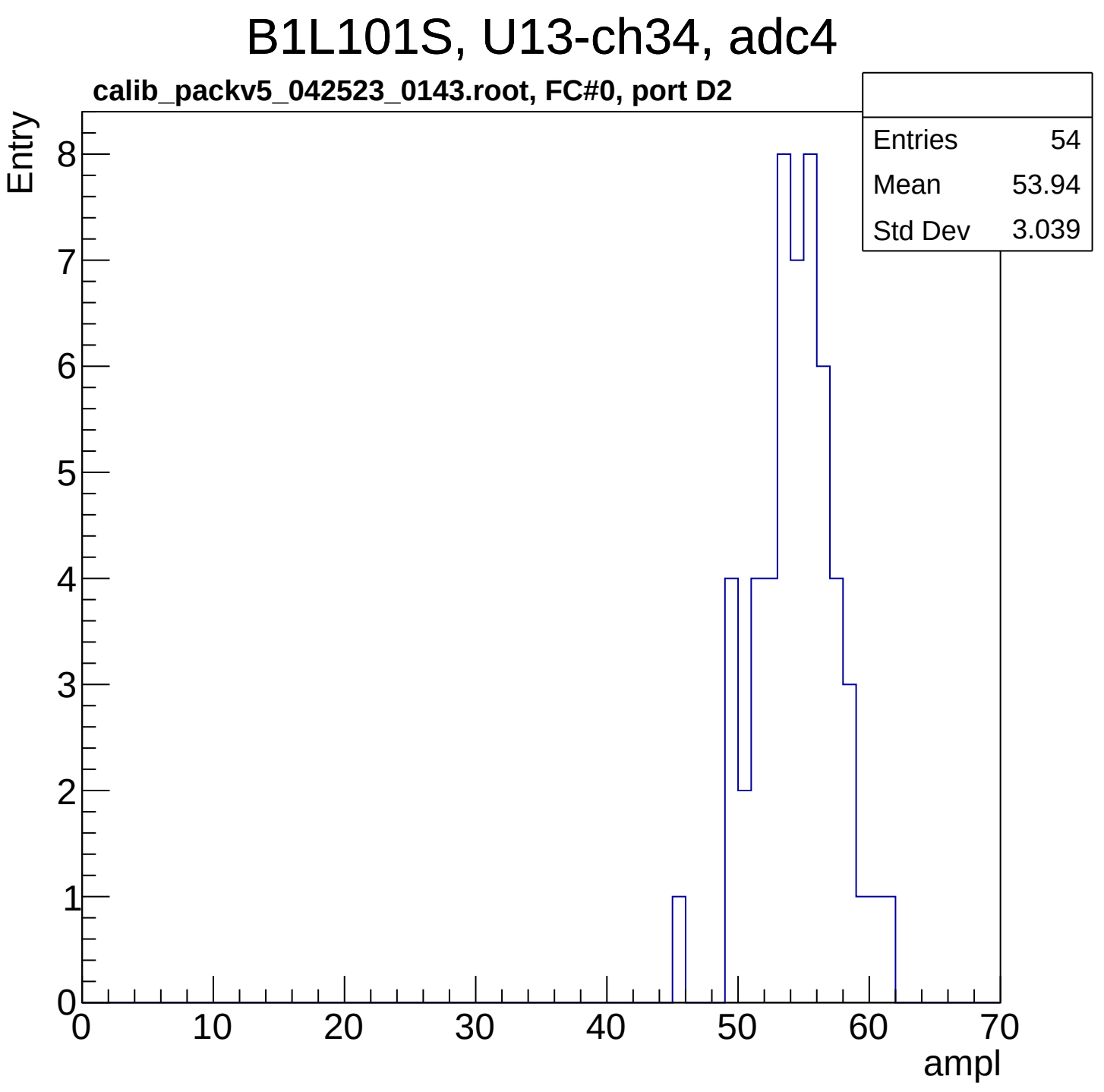
B1L101S, U13-ch34, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 53.94
Std Dev 3.039
Entry
ampl
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4
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60
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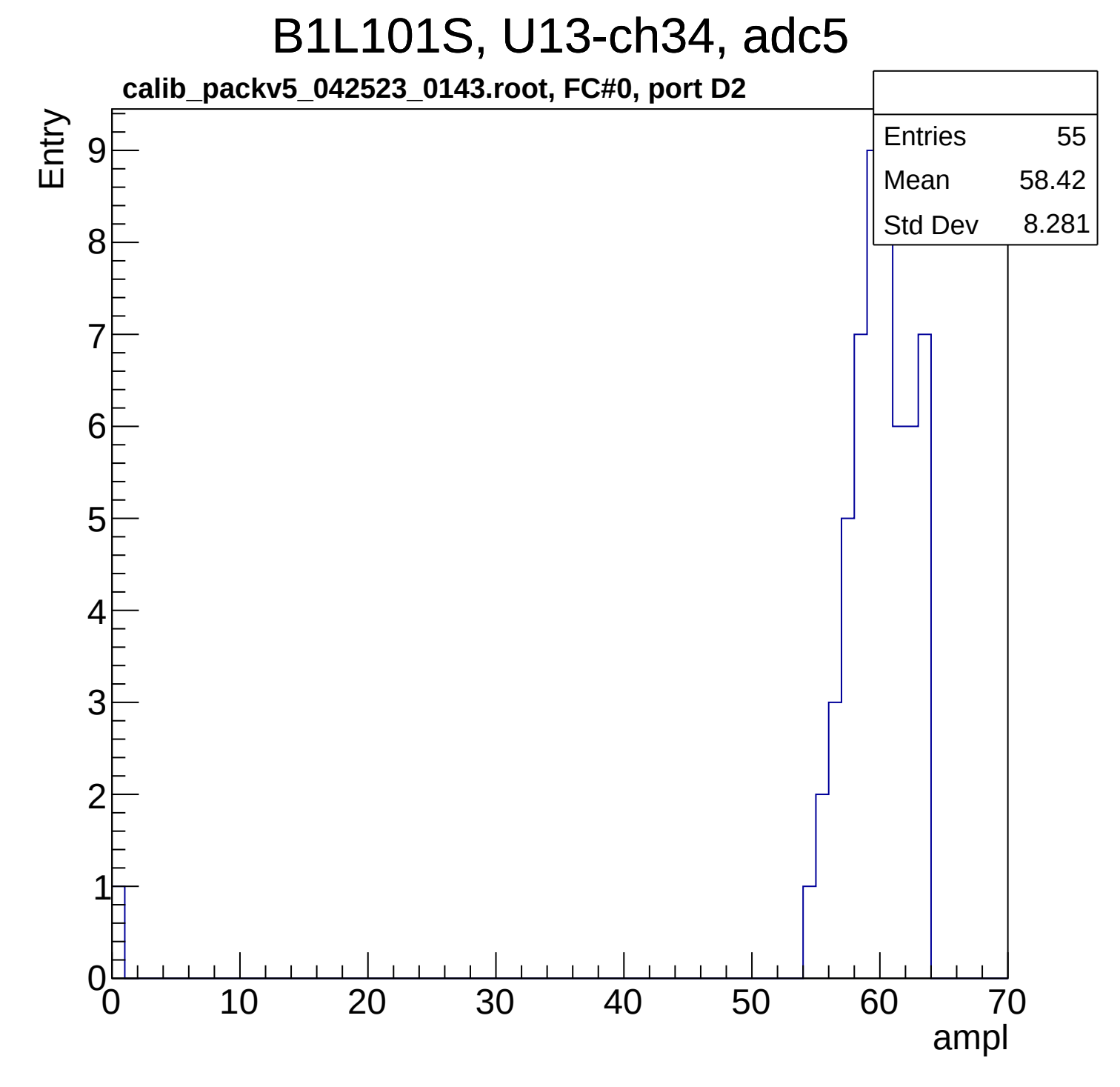
B1L101S, U13-ch34, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 58.42
Std Dev 8.281
Entry
ampl
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3
4
5
6
7
8
9
0
10
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60
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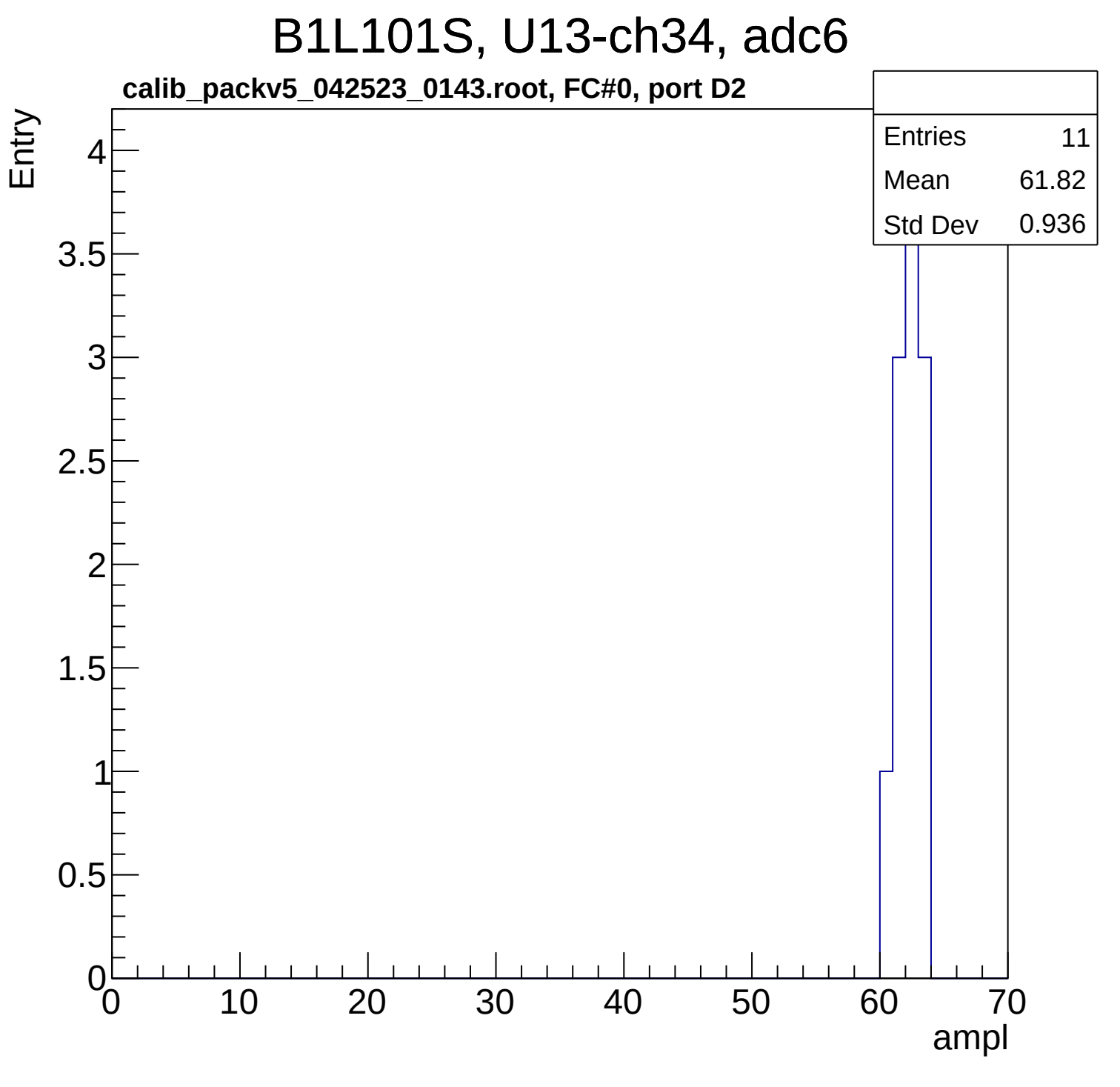
B1L101S, U13-ch34, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 11
Mean 61.82
Std Dev 0.936
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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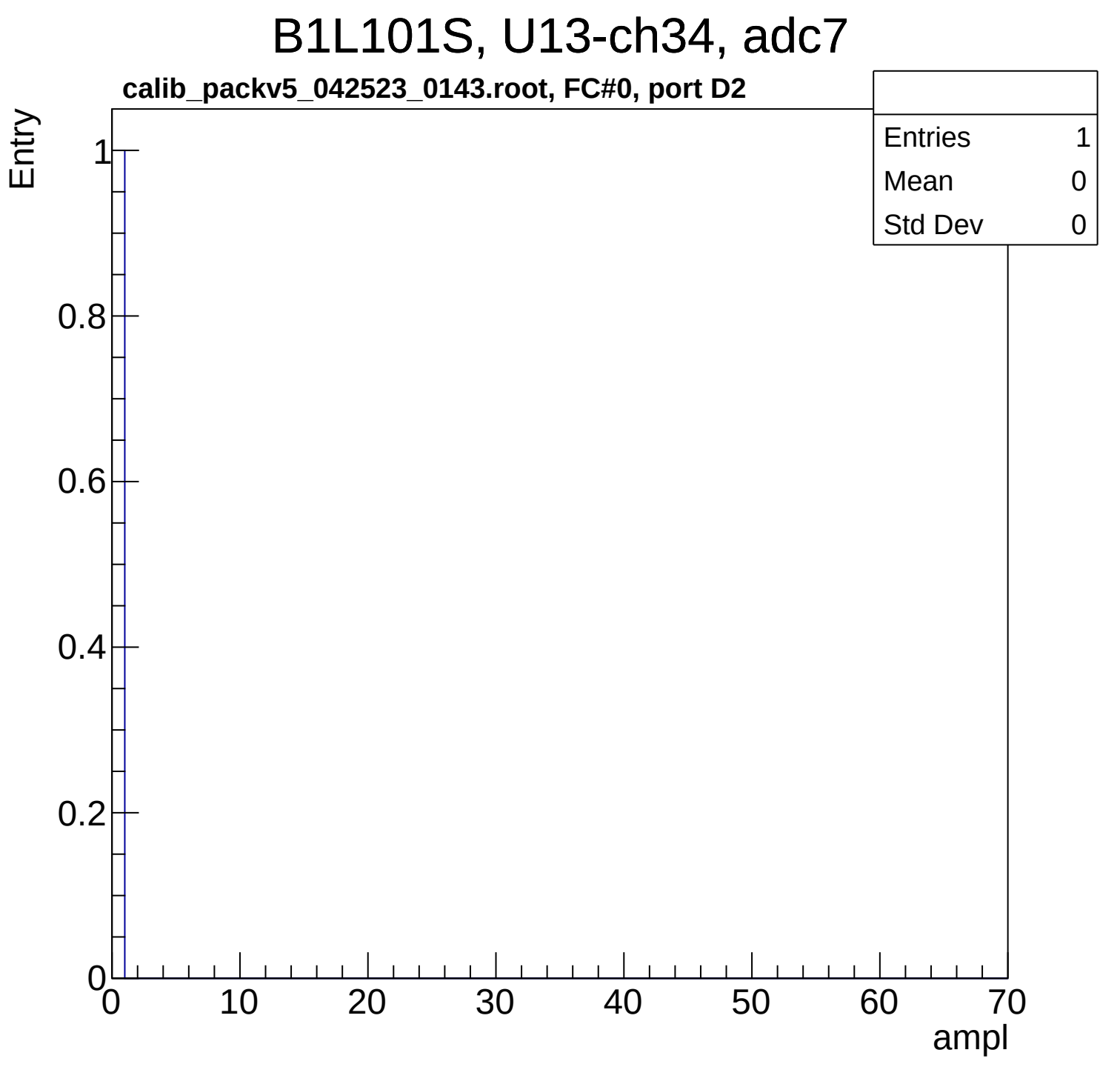
60
70


B1L101S, U13-ch34, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
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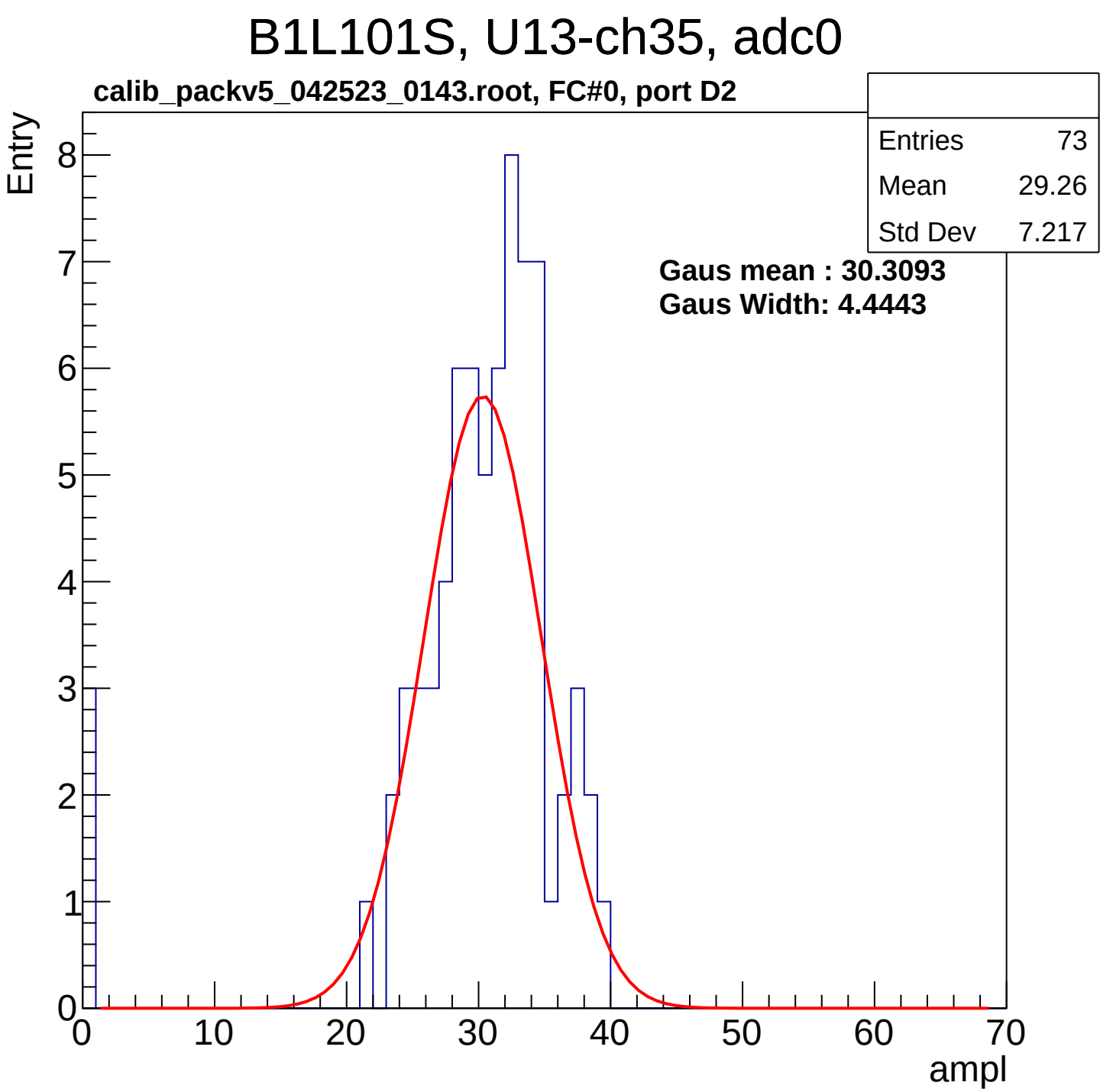
40
50
60
70
ampl


B1L101S, U13-ch35, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 29.26
Std Dev 7.217
Gaus mean : 30.3093
Gaus Width: 4.4443
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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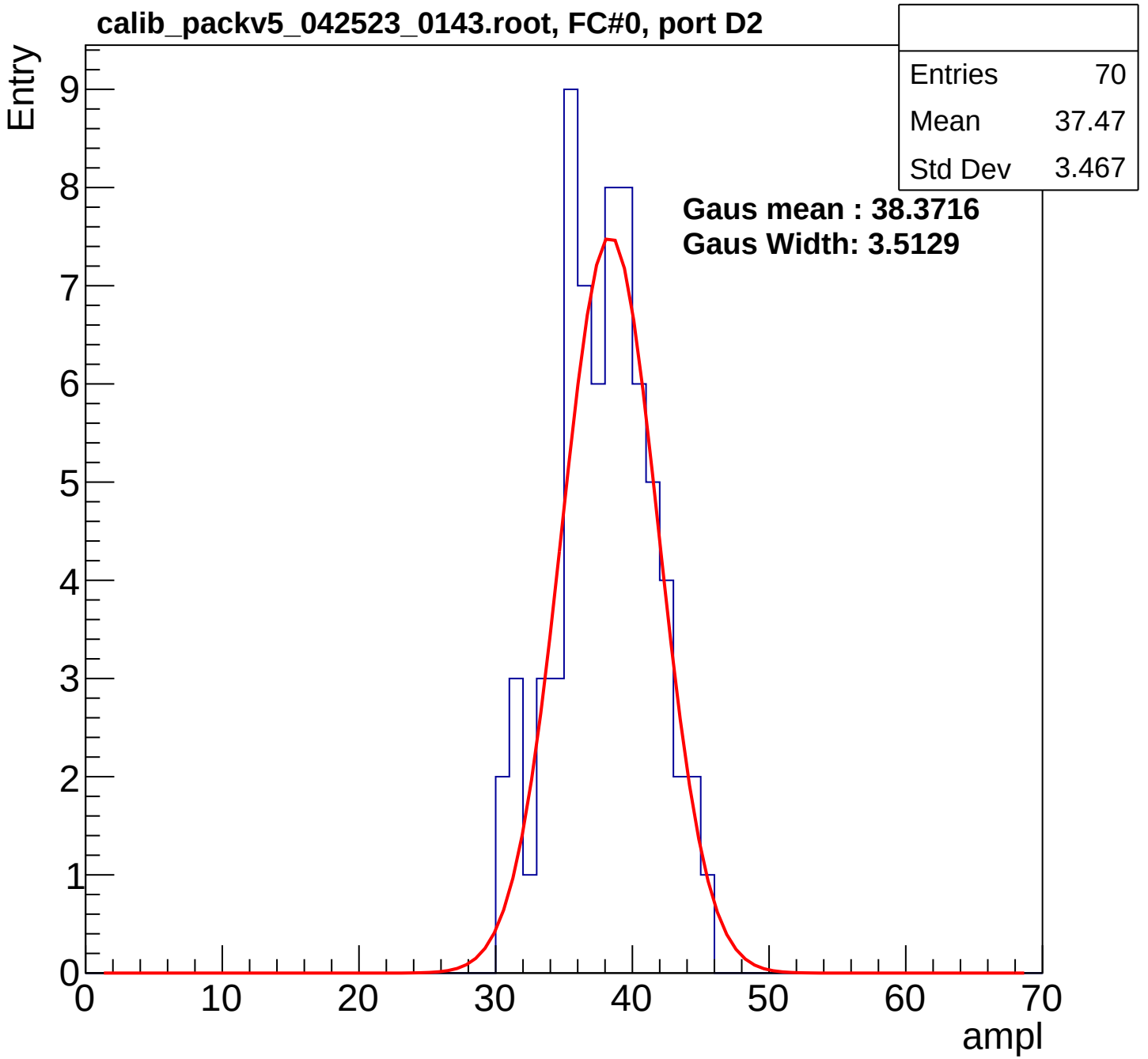
B1L101S, U13-ch35, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 37.47
Std Dev 3.467
Gaus mean : 38.3716
Gaus Width: 3.5129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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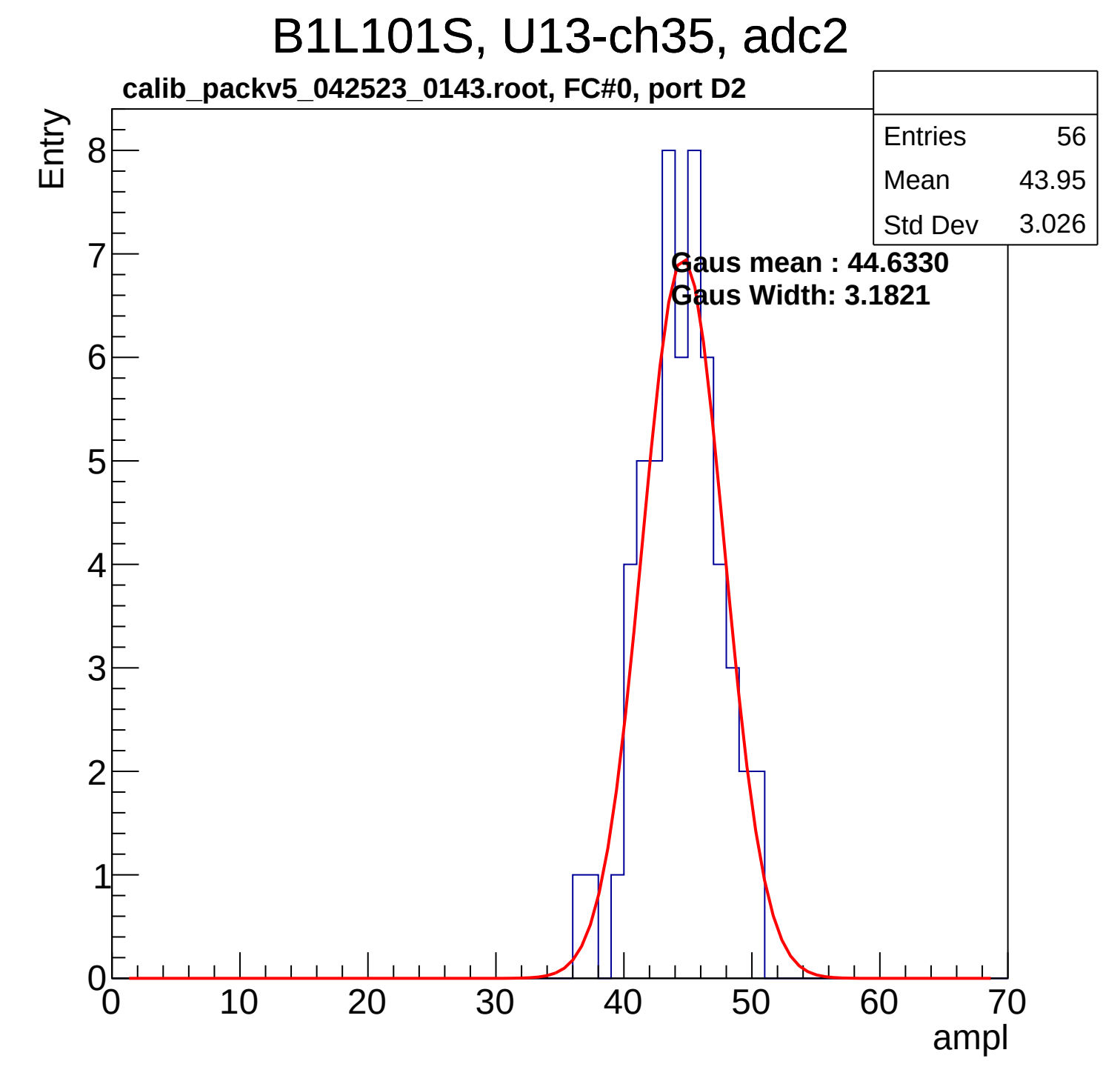
B1L101S, U13-ch35, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 43.95
Std Dev 3.026
Gaus mean : 44.6330
Gaus Width: 3.1821
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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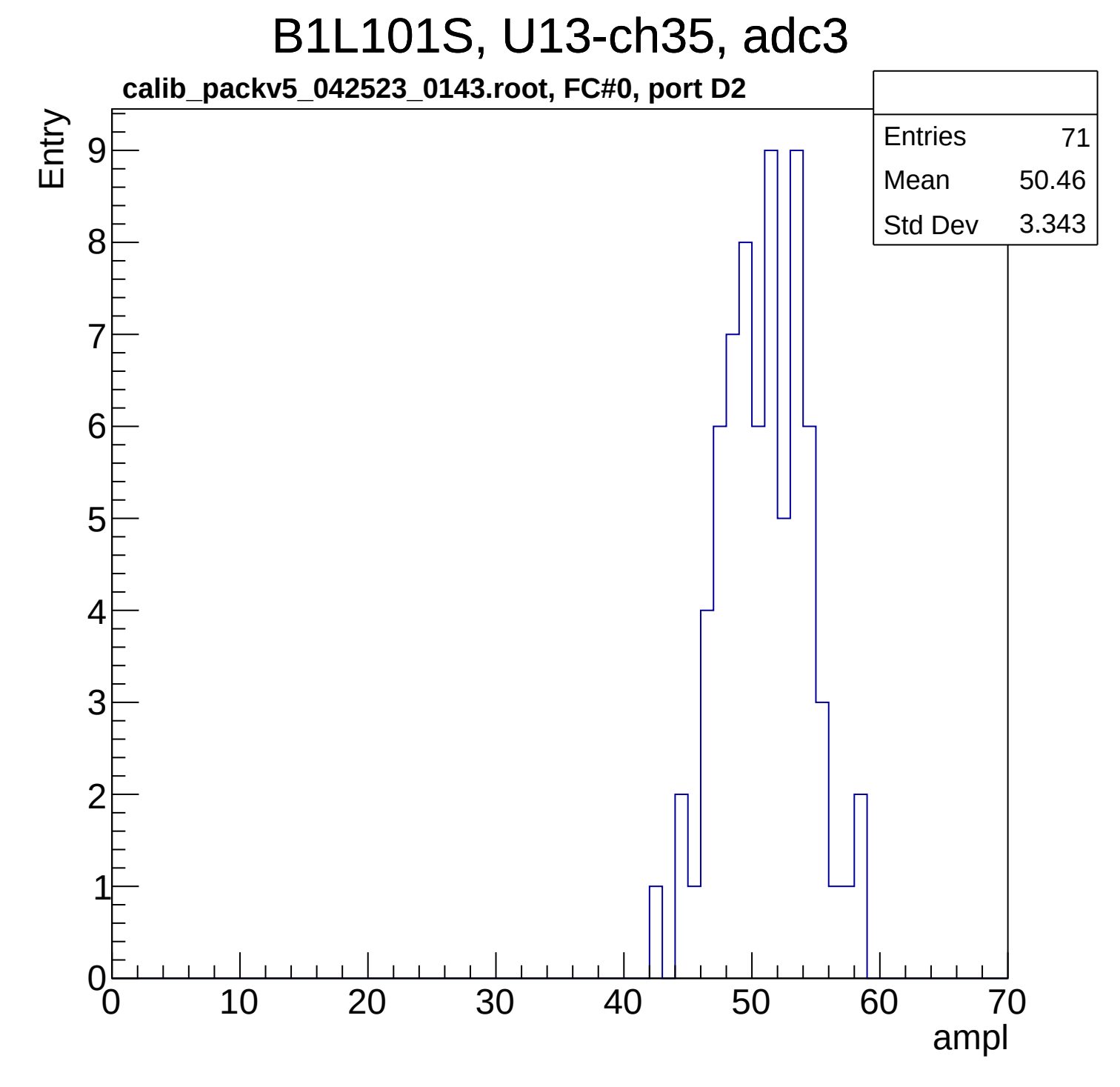
B1L101S, U13-ch35, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 50.46
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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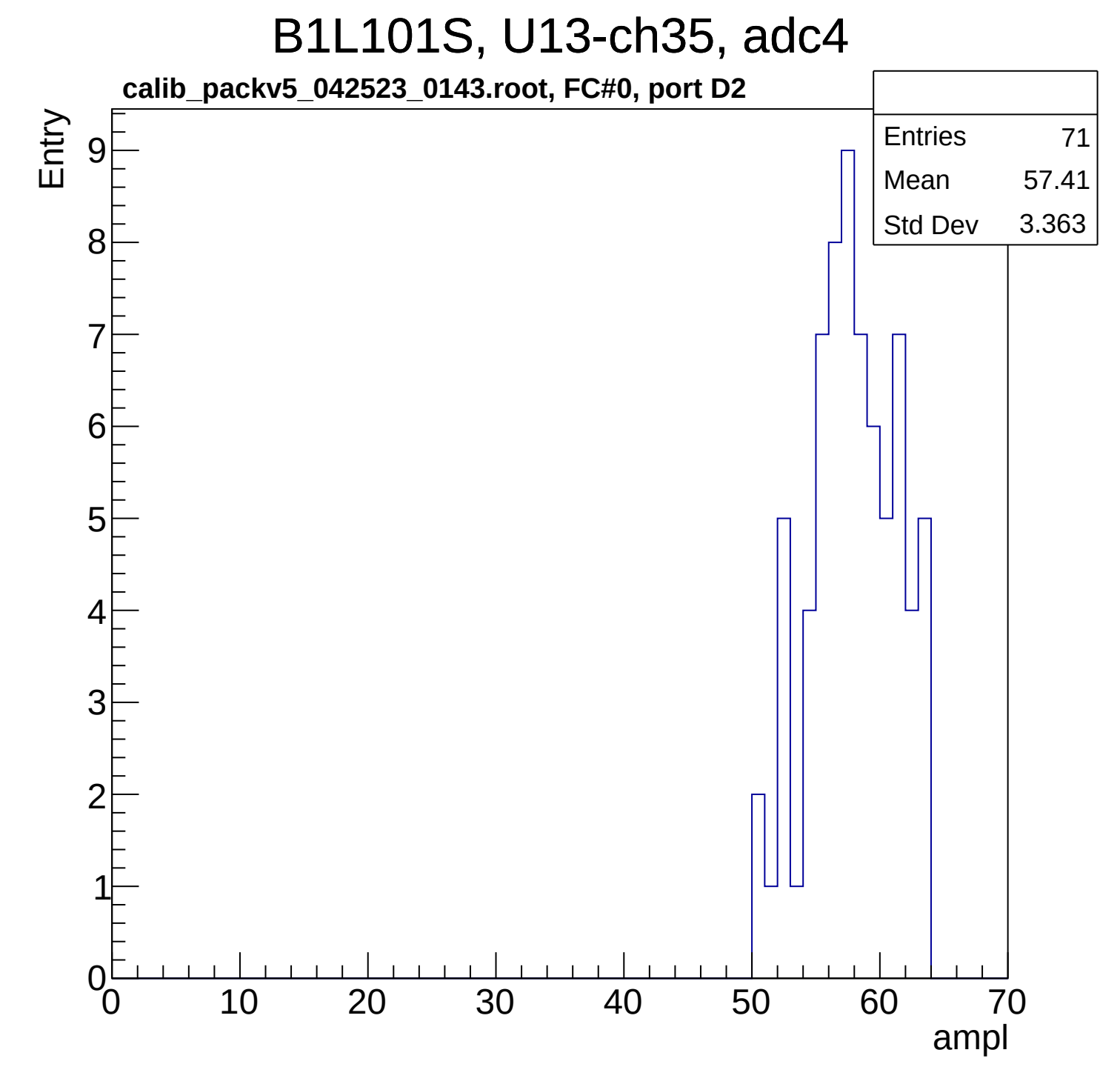
B1L101S, U13-ch35, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 57.41
Std Dev 3.363
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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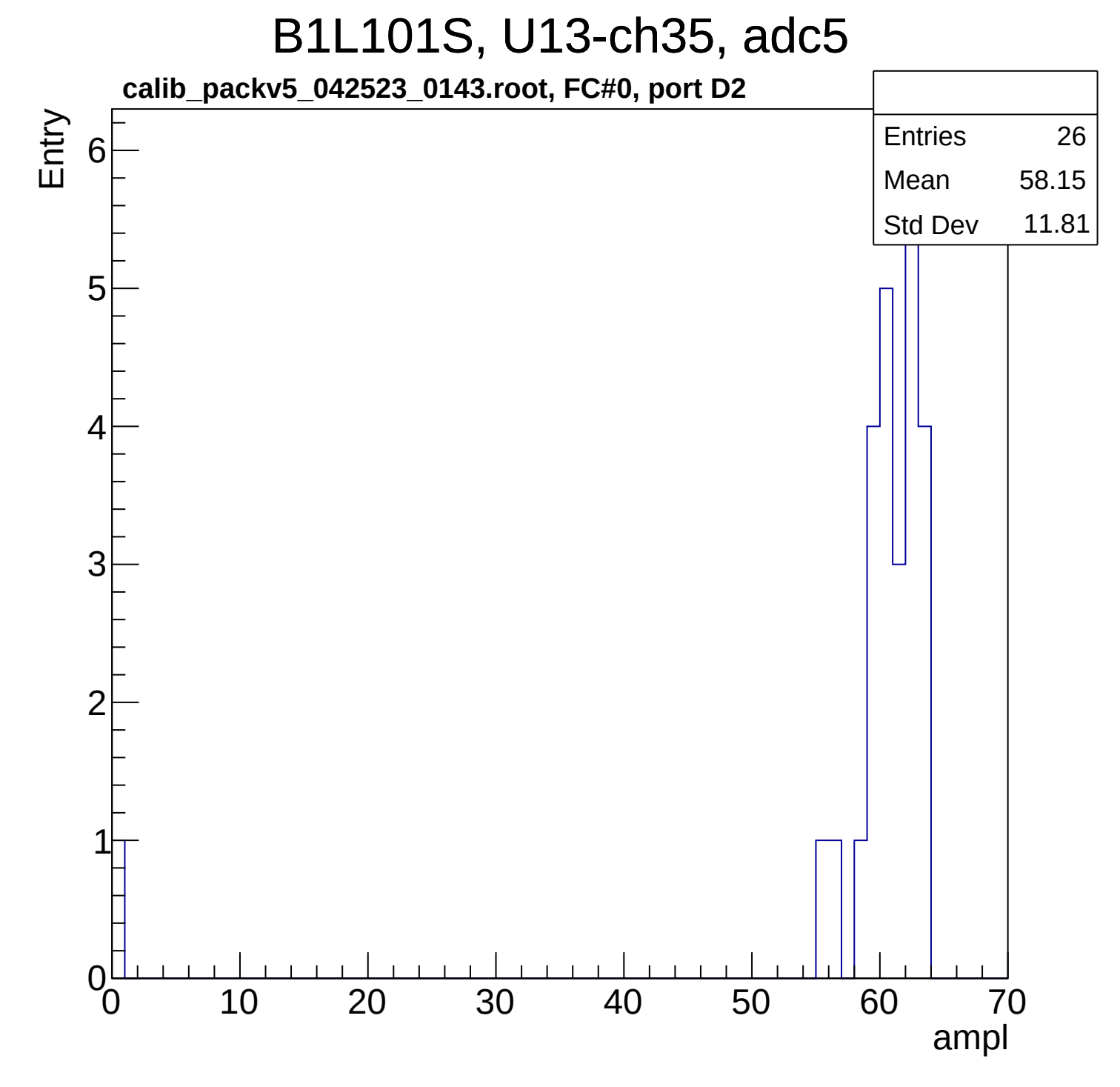
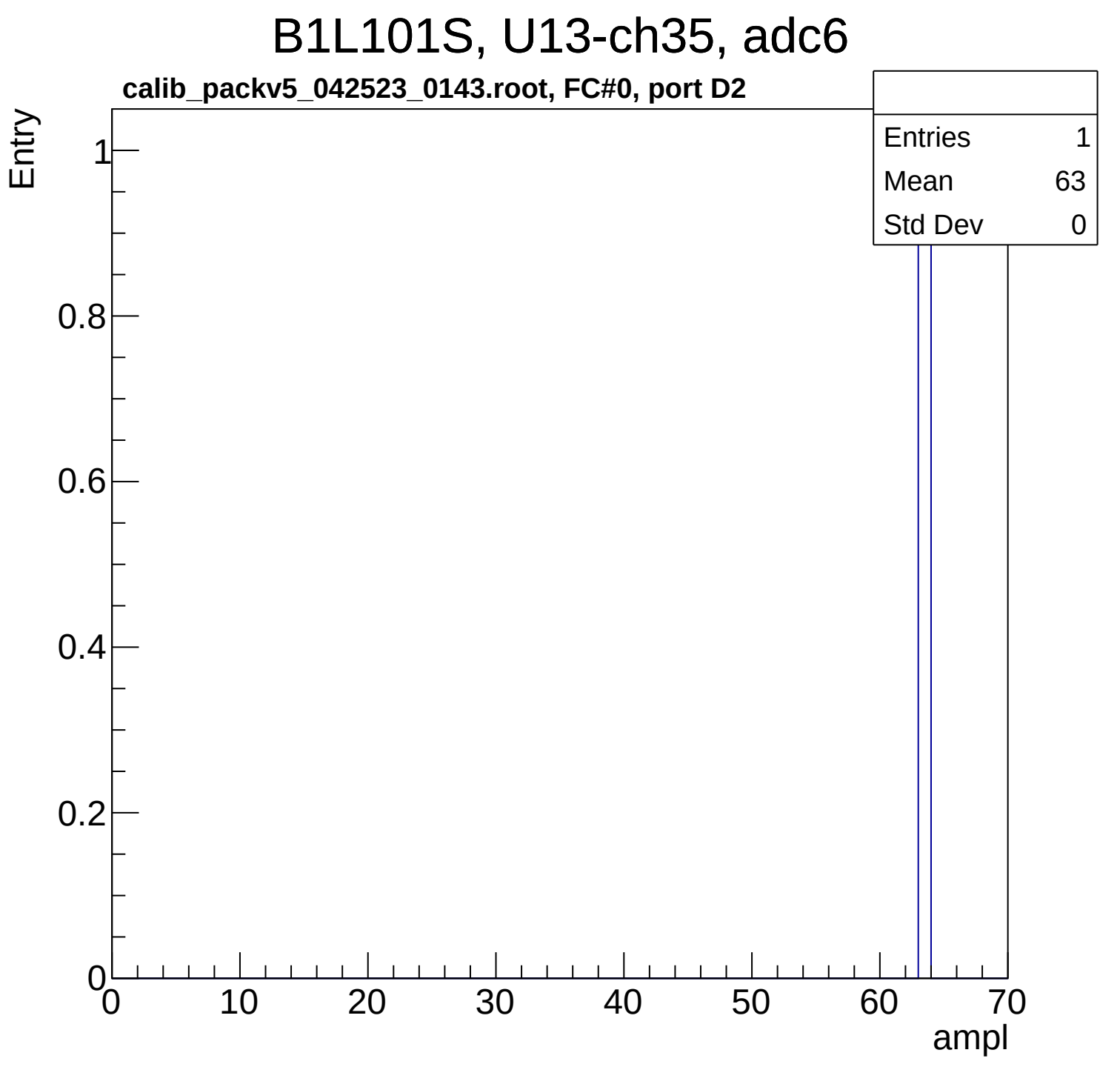

B1L101S, U13-ch35, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 26
Mean 58.15
Std Dev 11.81
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch35, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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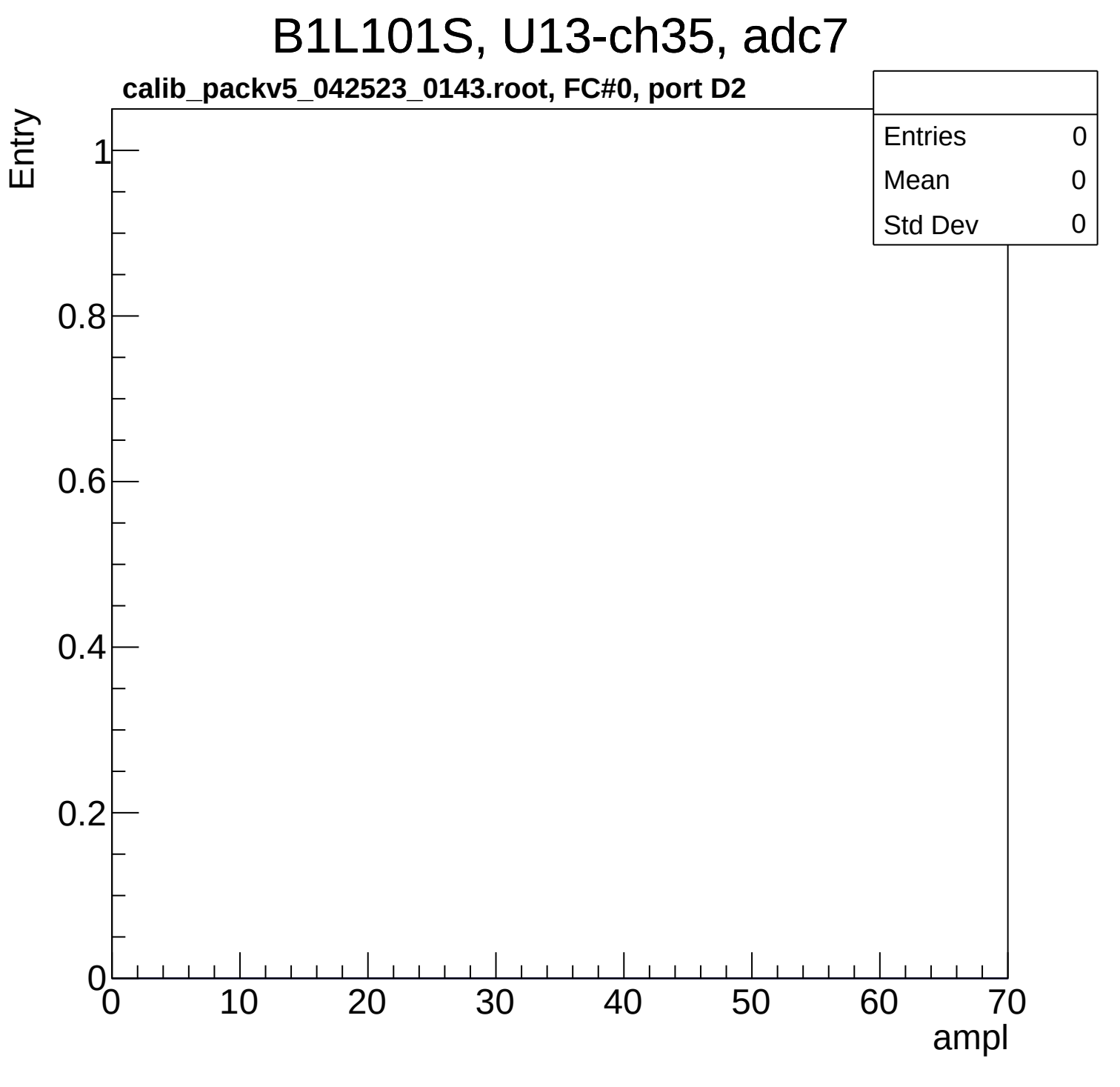
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch35, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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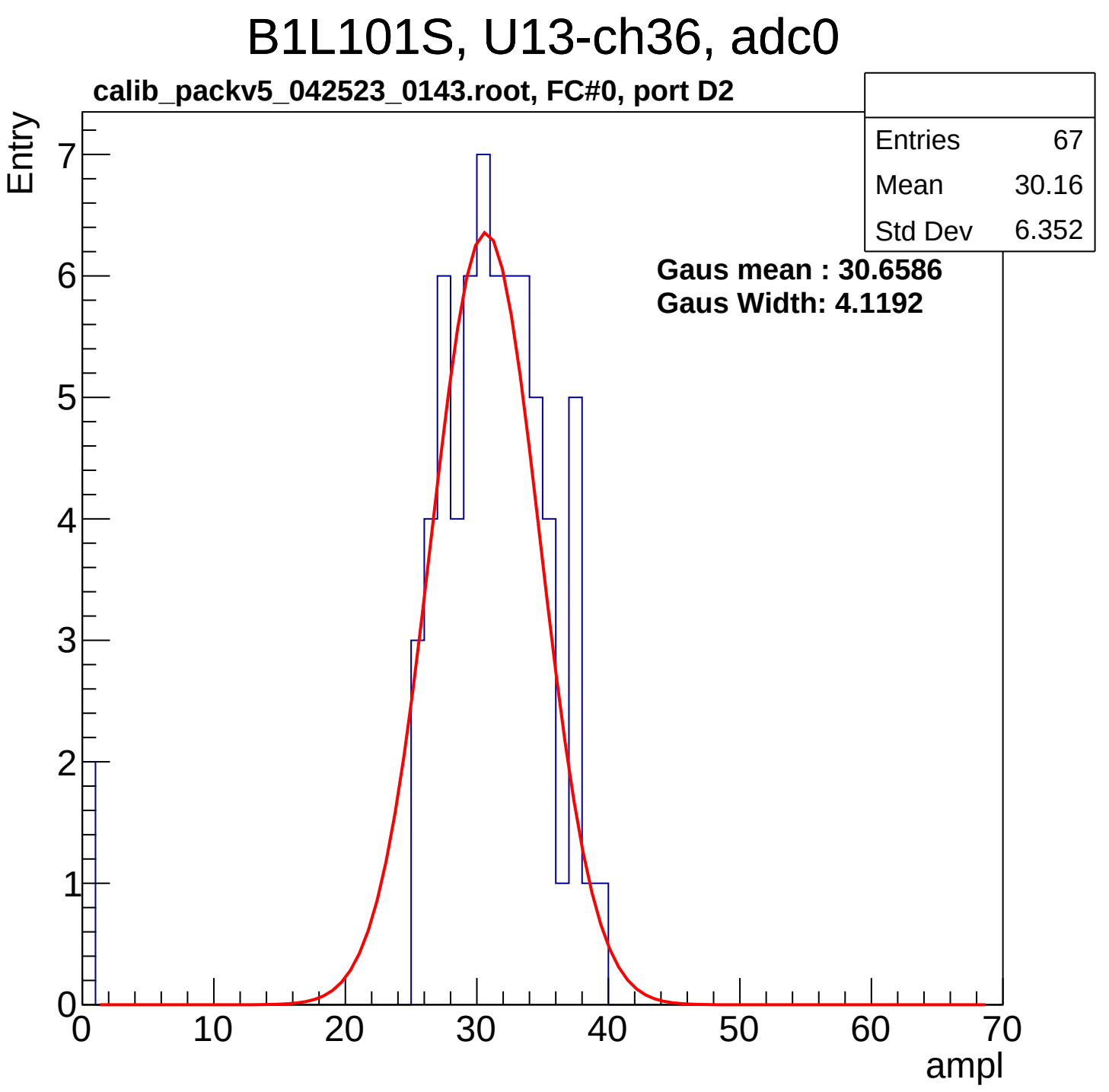
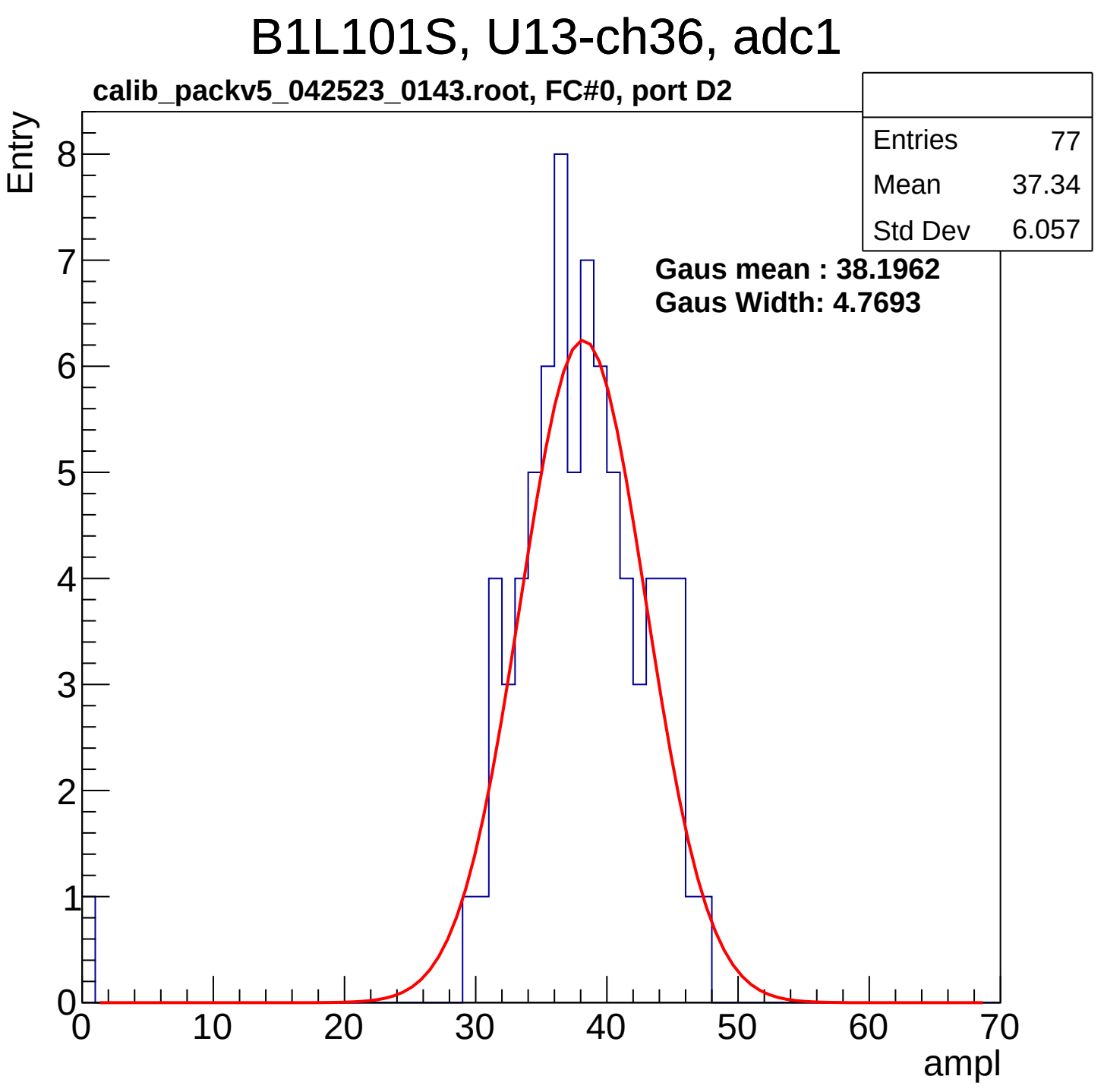
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch36, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 30.16
Std Dev 6.352
Gaus mean : 30.6586
Gaus Width: 4.1192
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch36, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 37.34
Std Dev 6.057
Gaus mean : 38.1962
Gaus Width: 4.7693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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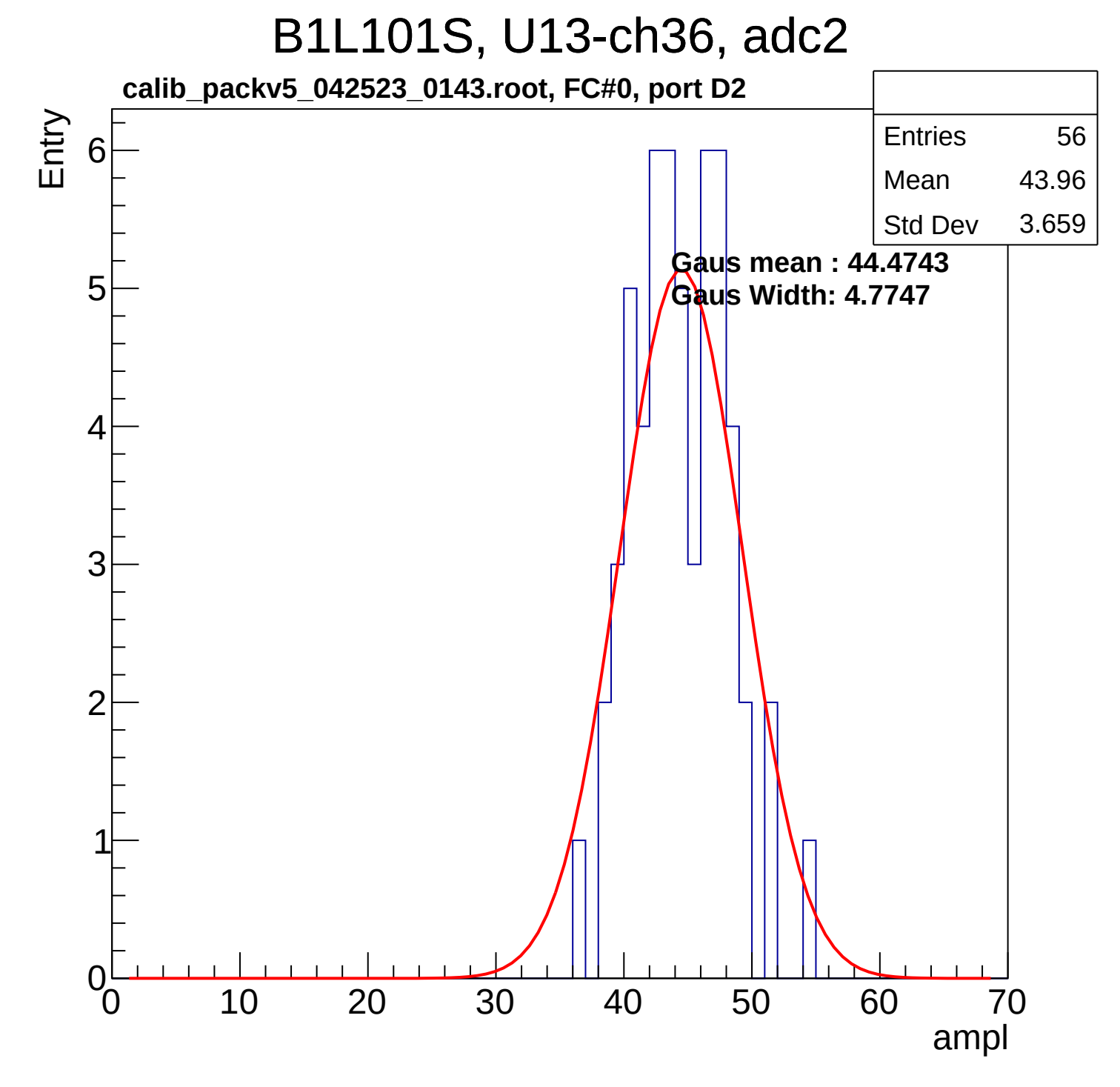
B1L101S, U13-ch36, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 43.96
Std Dev 3.659
Gaus mean : 44.4743
Gaus Width: 4.7747
Entry
ampl
0
1
2
3
4
5
6
0
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60
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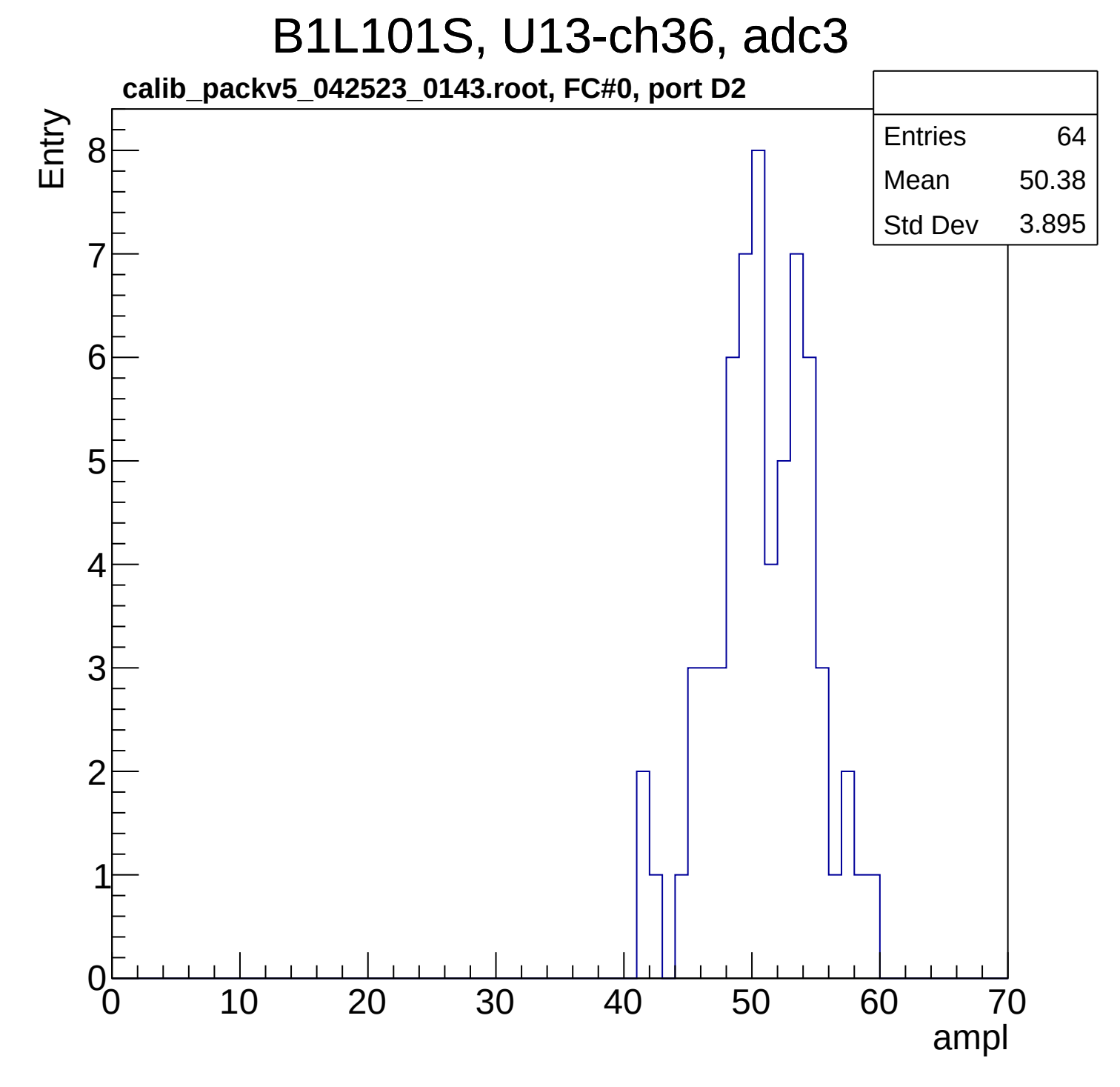
B1L101S, U13-ch36, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 50.38
Std Dev 3.895
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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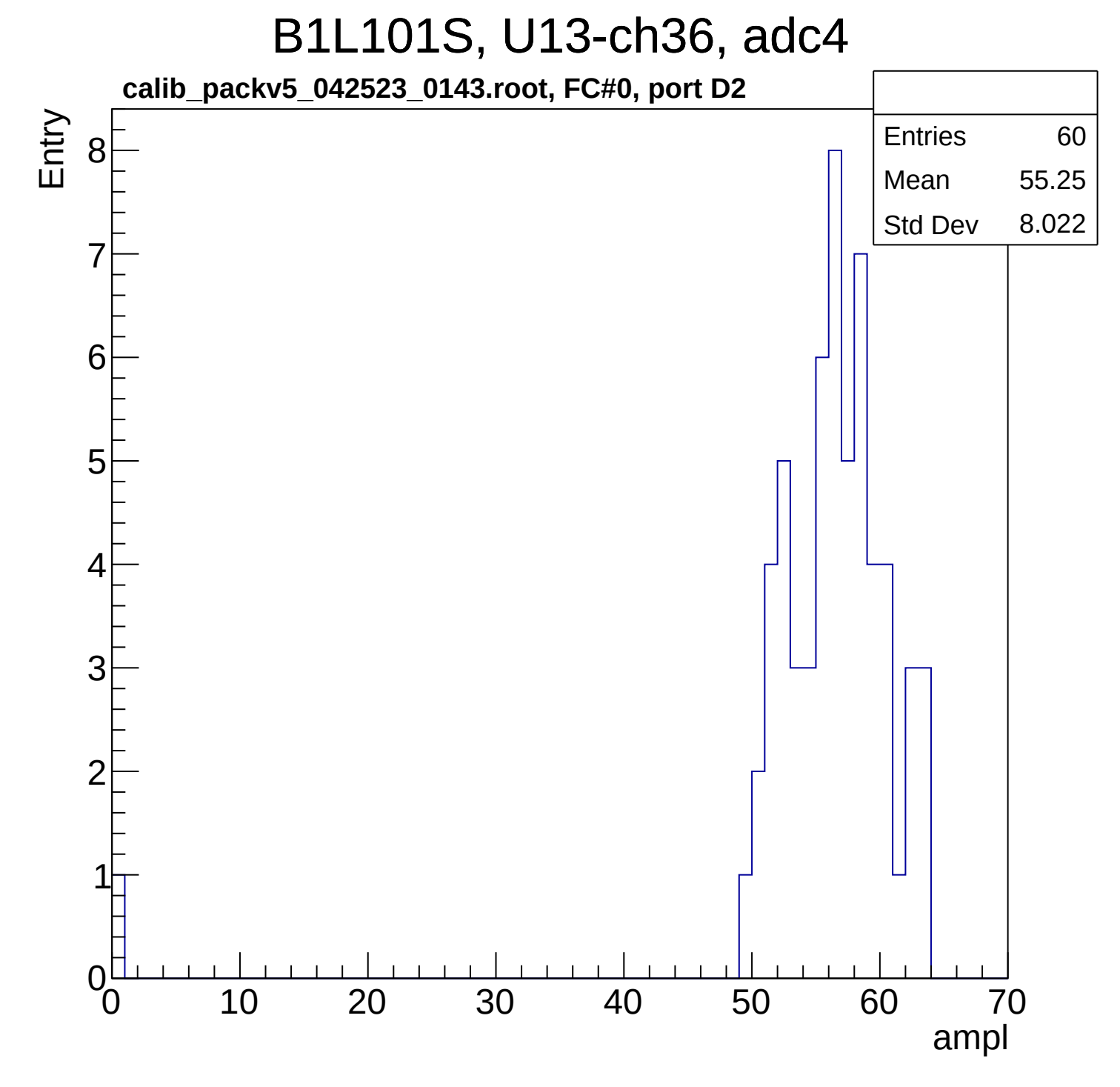
B1L101S, U13-ch36, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 55.25
Std Dev 8.022
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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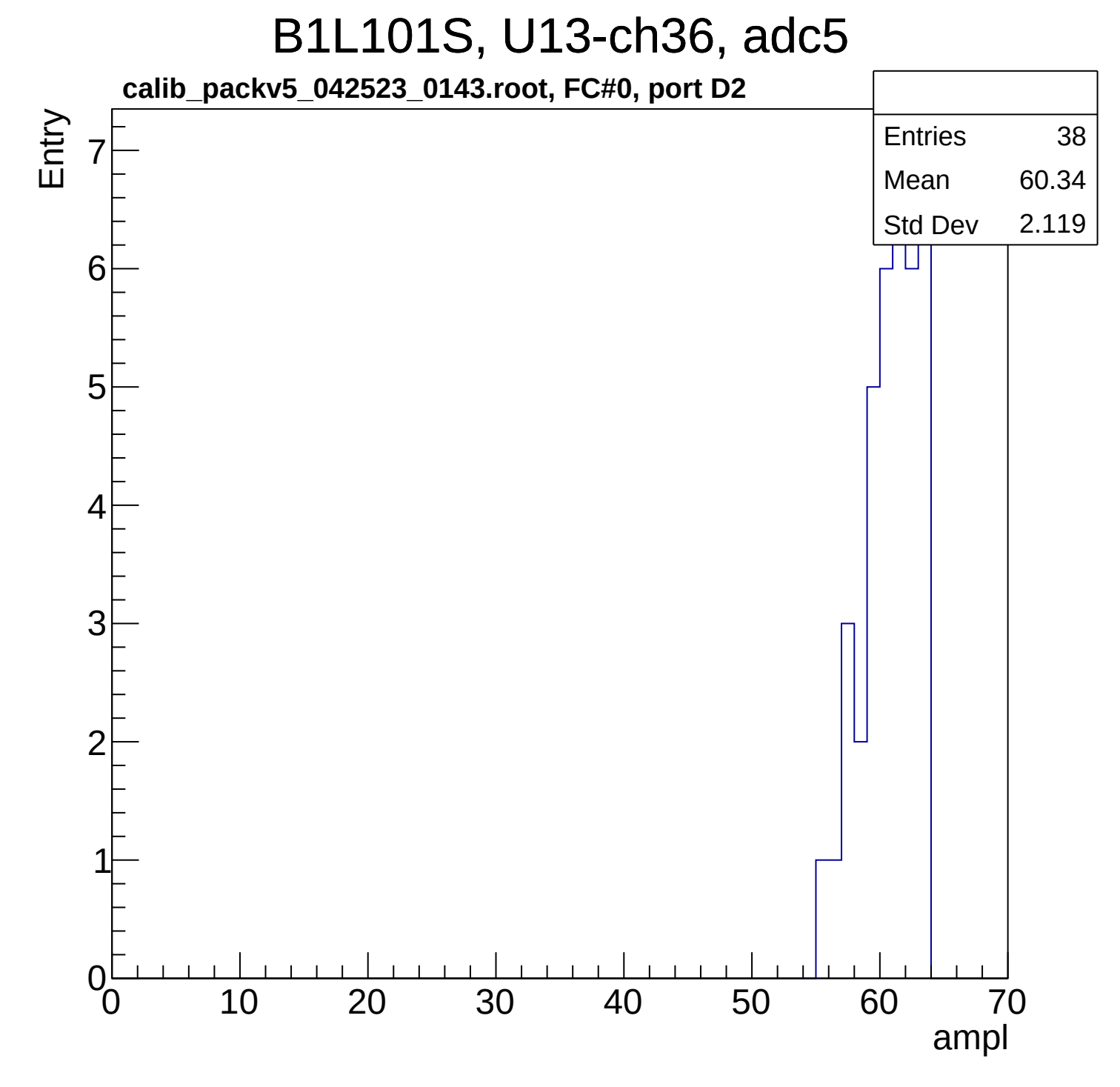
B1L101S, U13-ch36, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 38
Mean 60.34
Std Dev 2.119
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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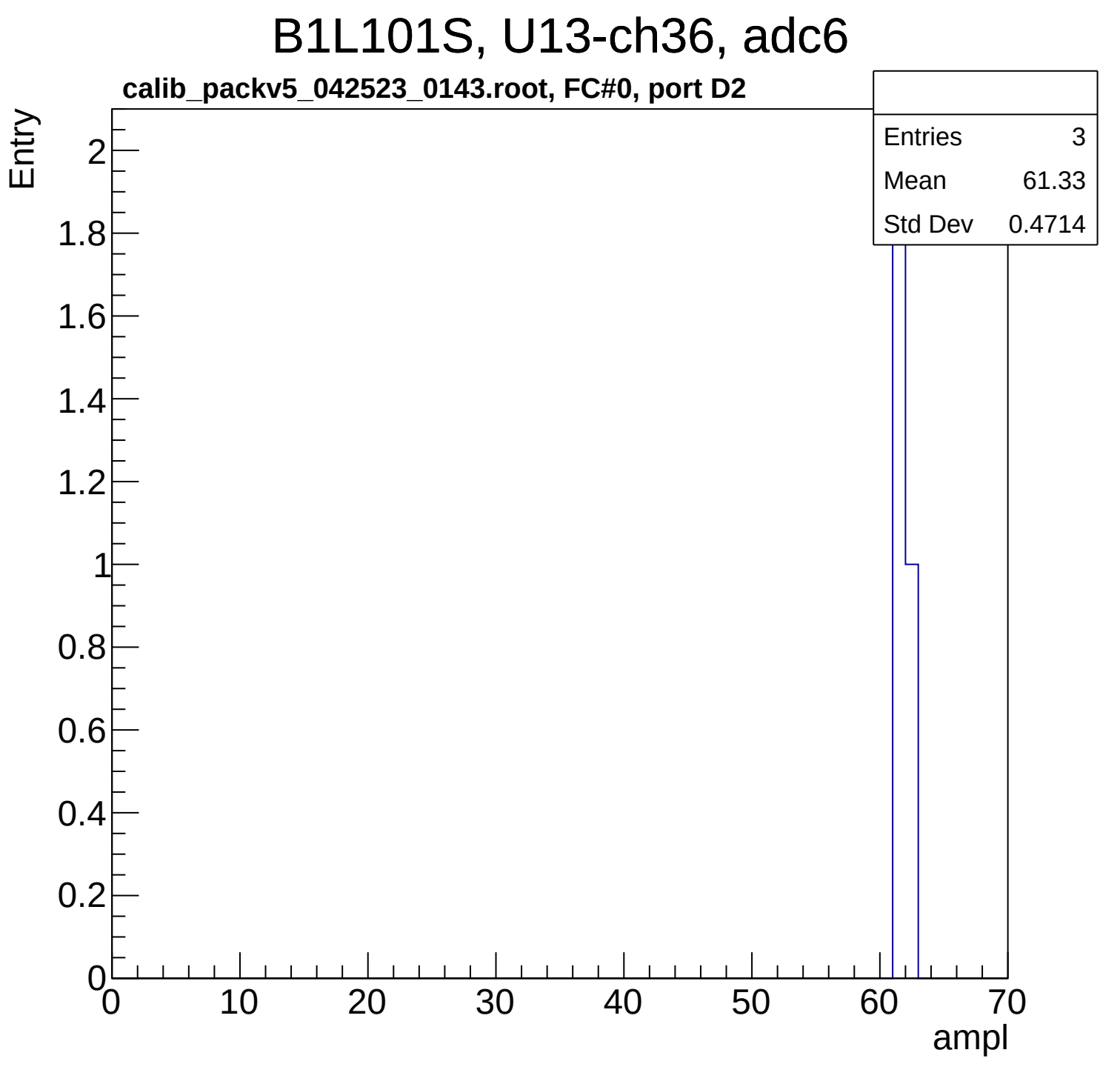
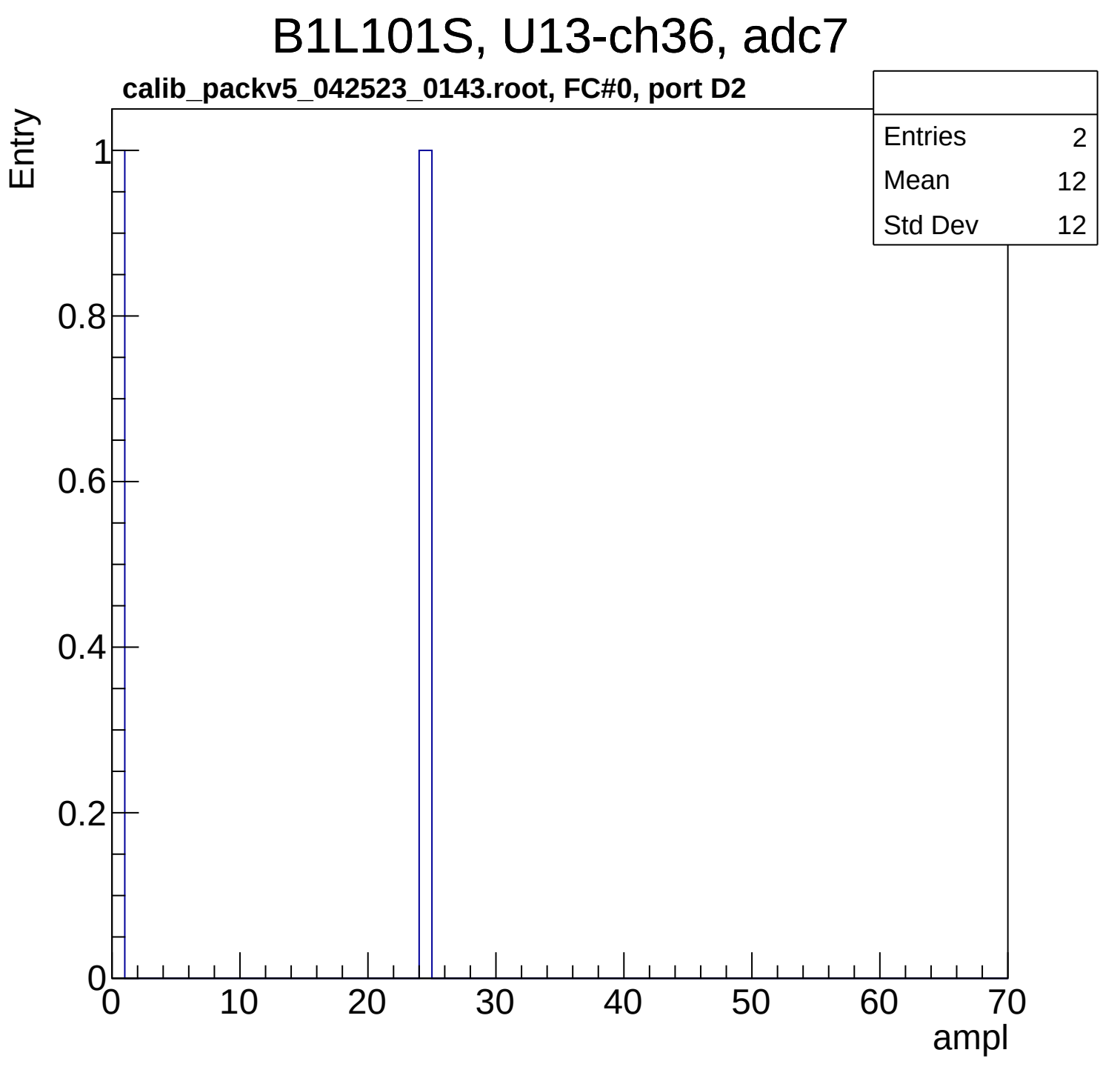
50
60
70


B1L101S, U13-ch36, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch36, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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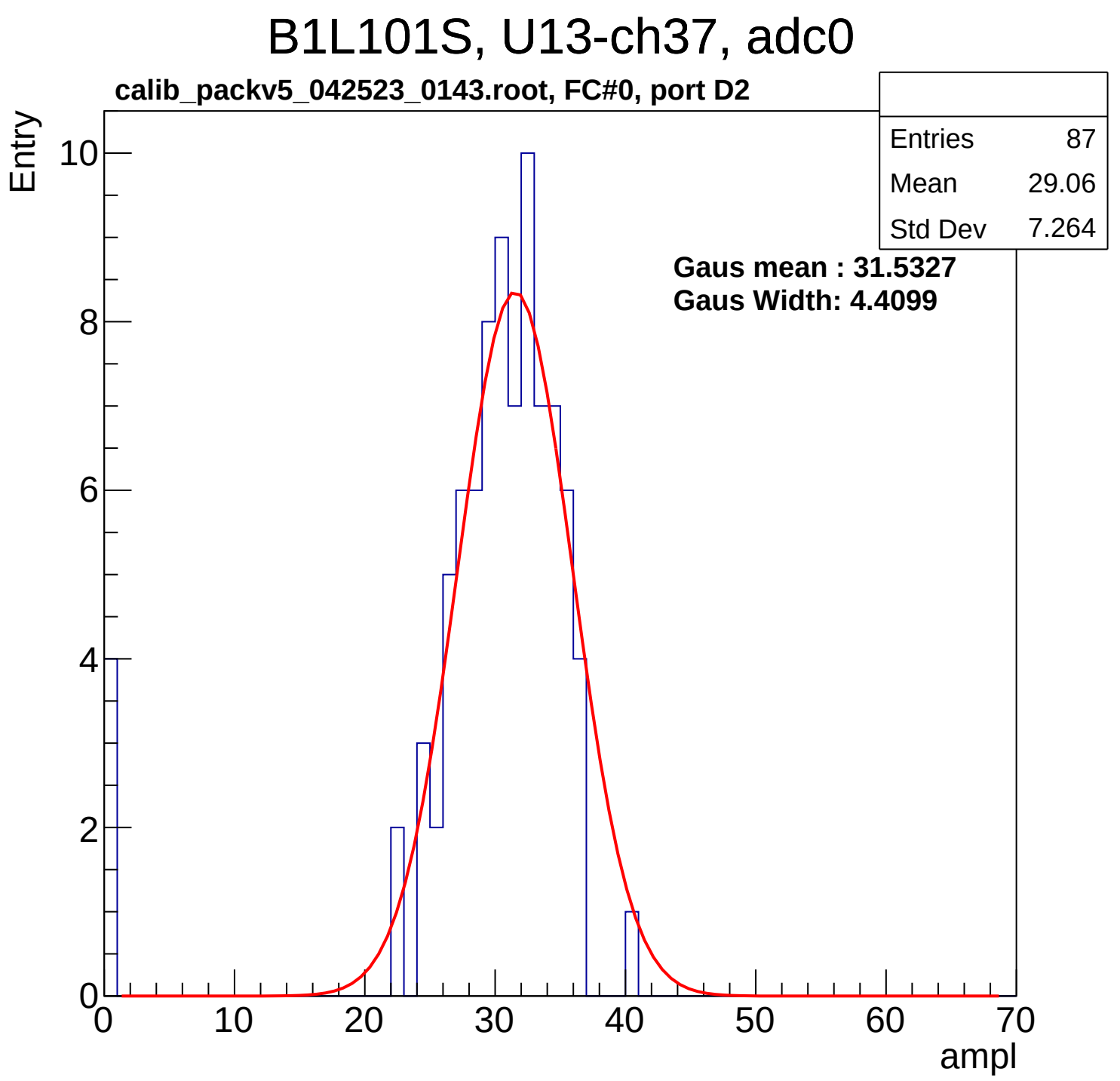
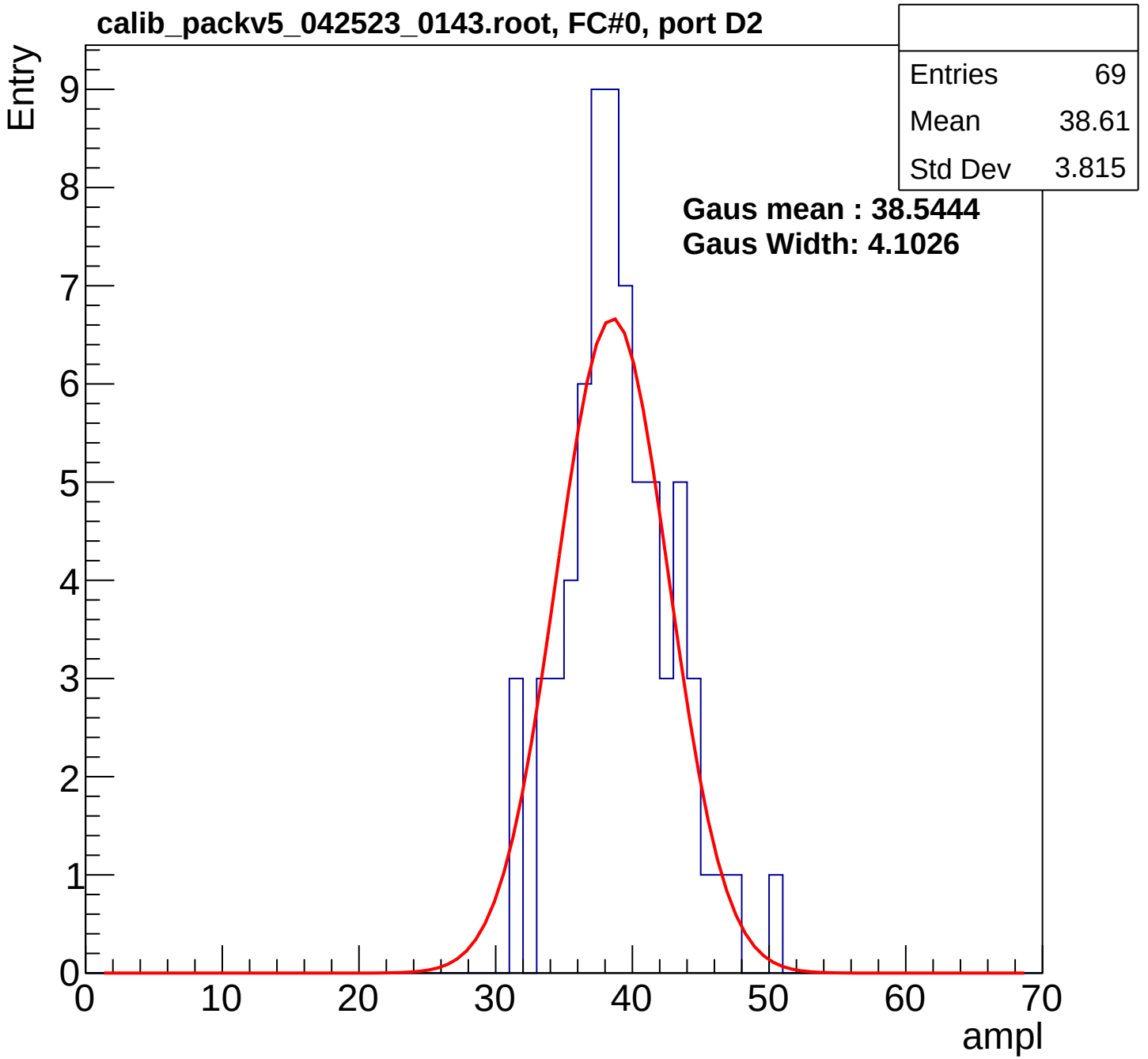
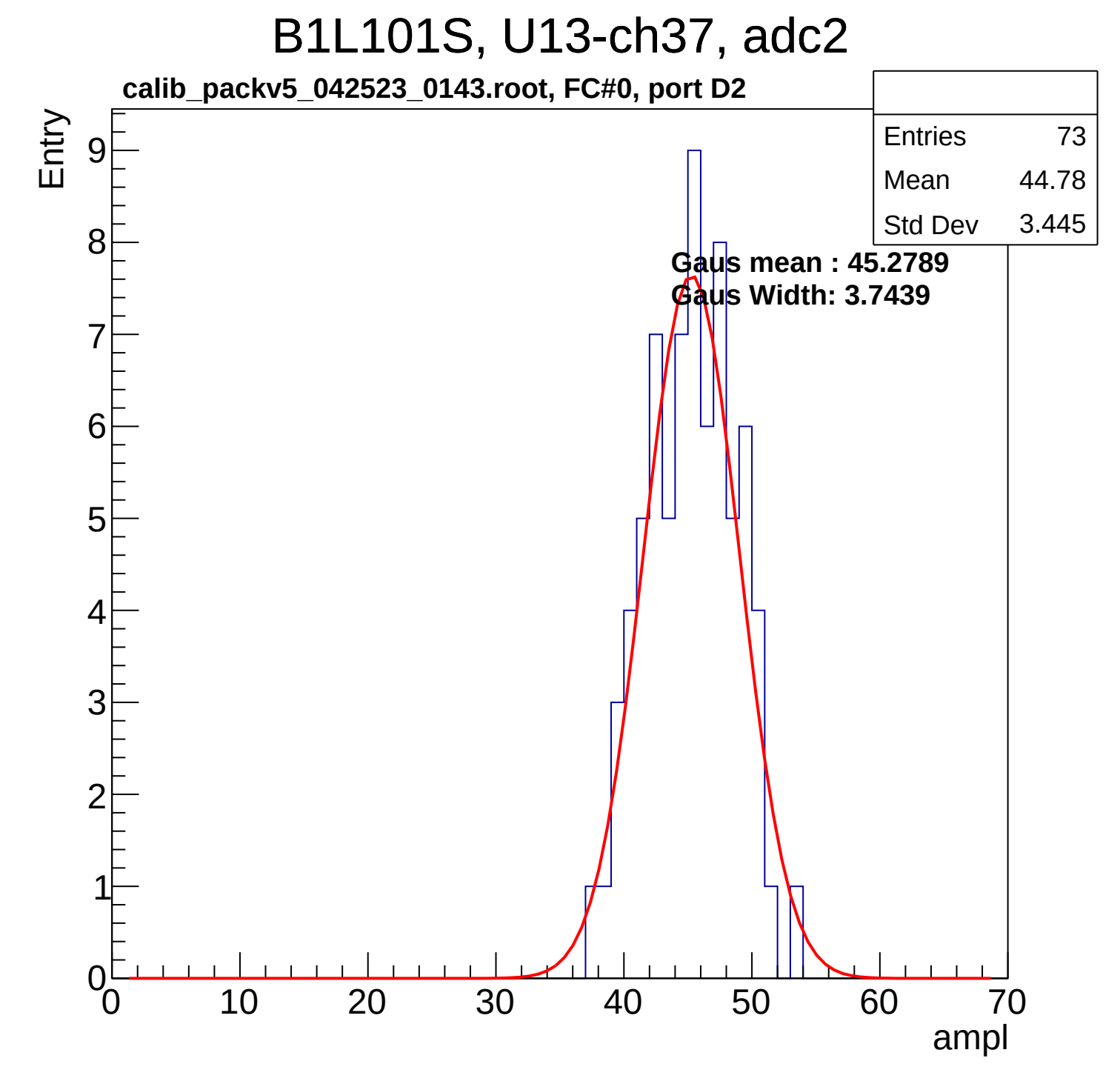
40
50
60
70
ampl


B1L101S, U13-ch37, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 87
Mean 29.06
Std Dev 7.264
Gaus mean : 31.5327
Gaus Width: 4.4099
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch37, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 38.61
Std Dev 3.815
Gaus mean : 38.5444
Gaus Width: 4.1026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch37, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 44.78
Std Dev 3.445
Gaus mean : 45.2789
Gaus Width: 3.7439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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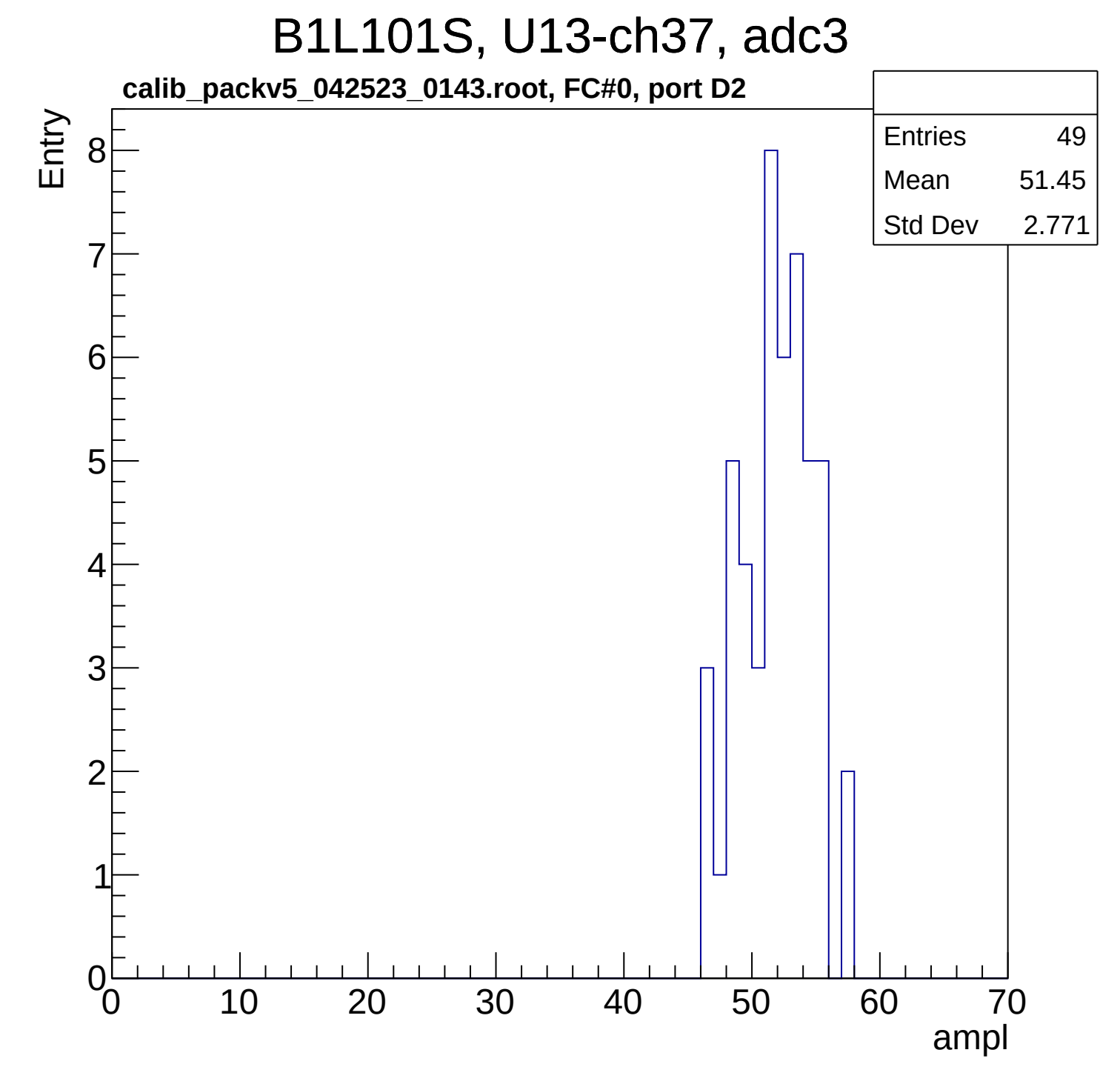
B1L101S, U13-ch37, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 51.45
Std Dev 2.771
Entry
ampl
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3
4
5
6
7
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0
10
20
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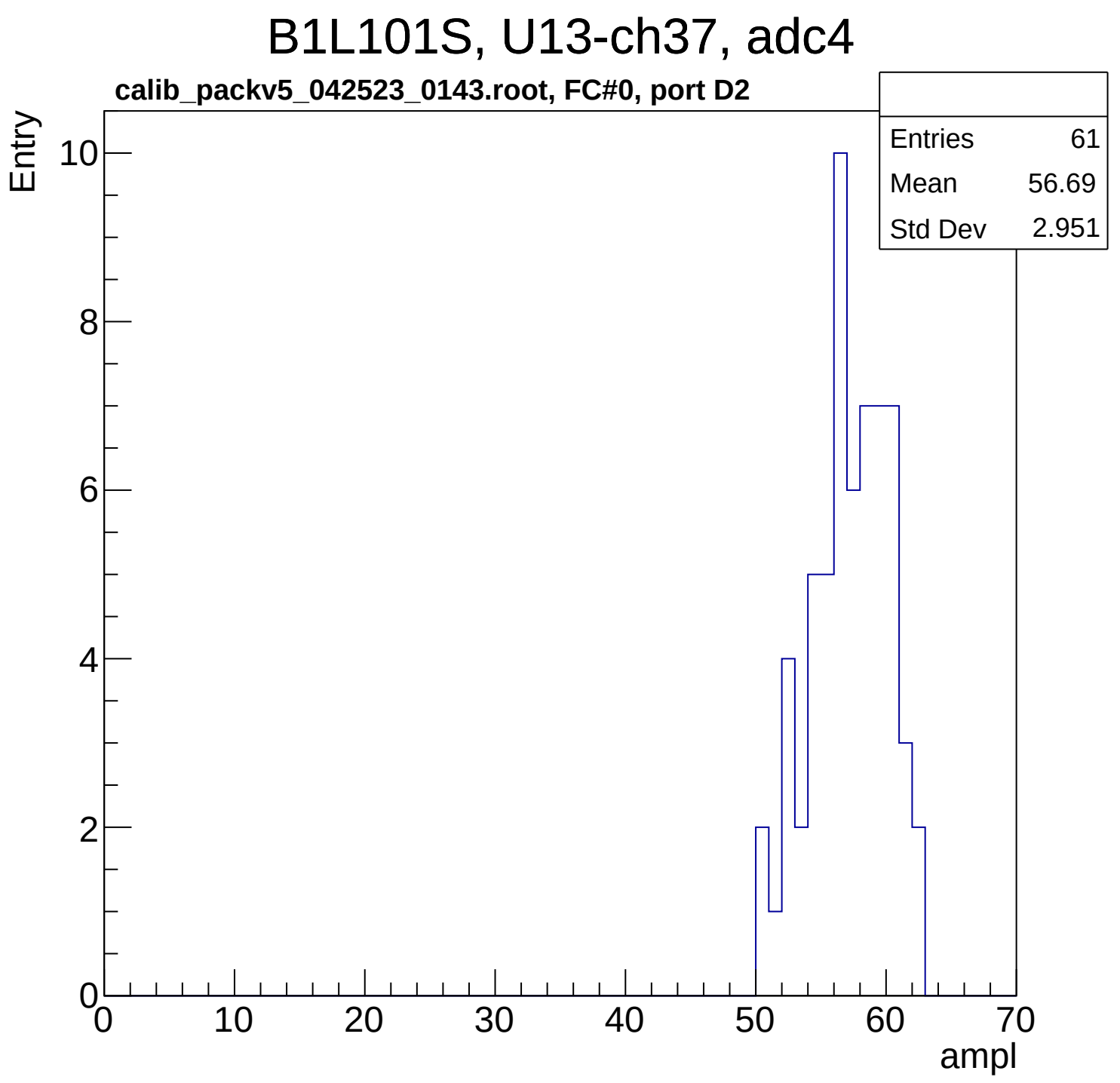
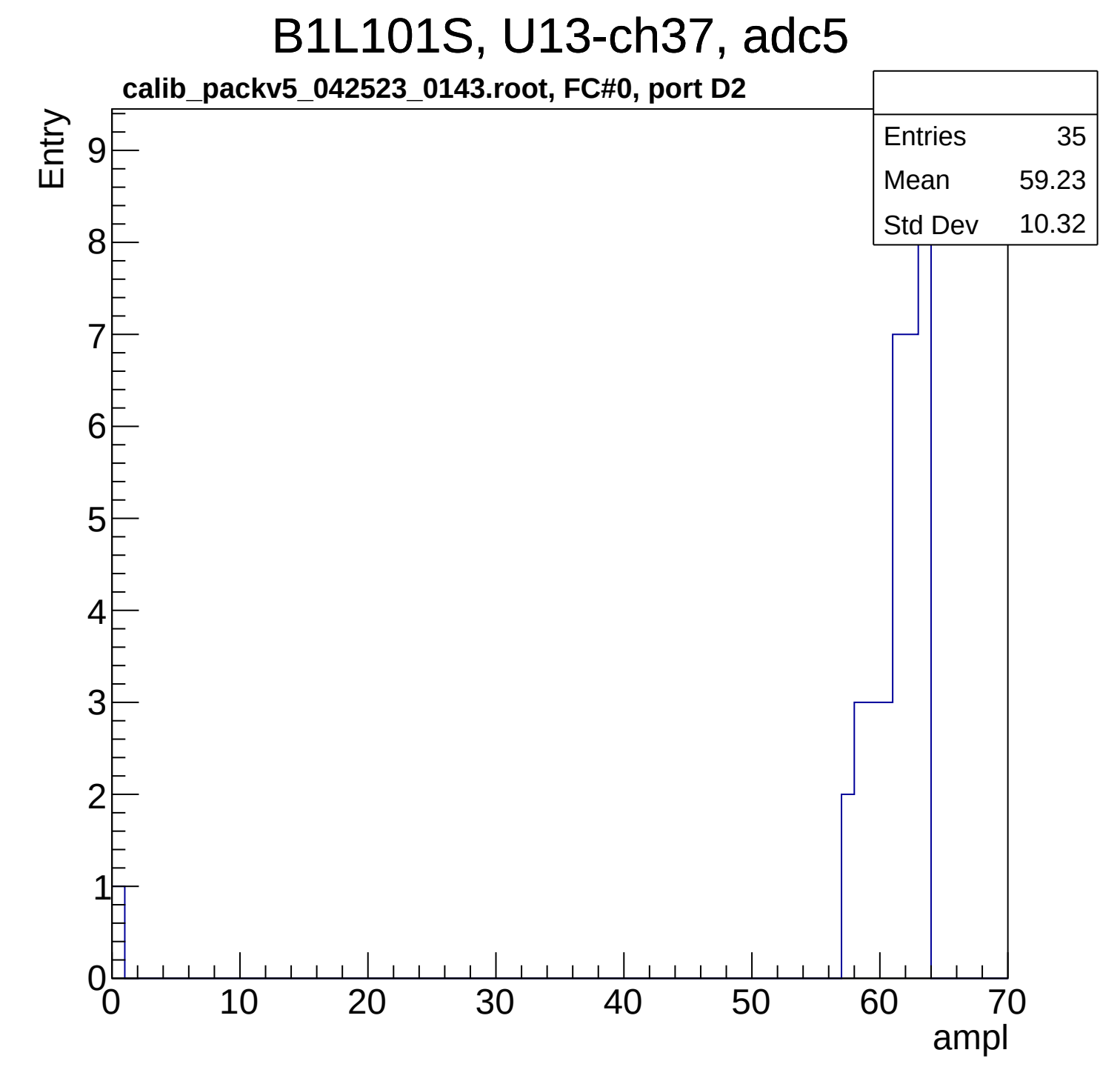
40
50
60
70


B1L101S, U13-ch37, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 56.69
Std Dev 2.951
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch37, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 35
Mean 59.23
Std Dev 10.32
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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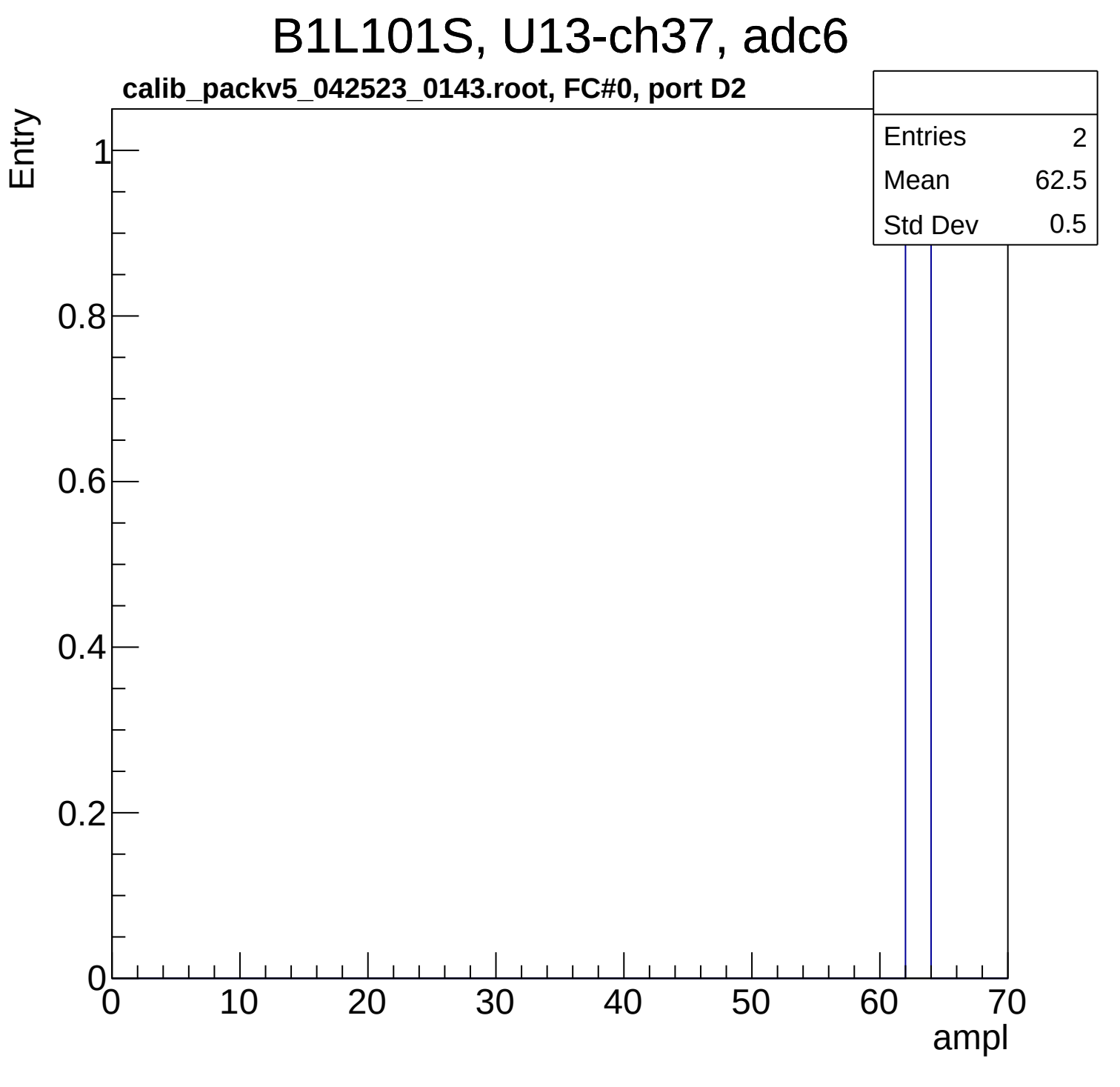
B1L101S, U13-ch37, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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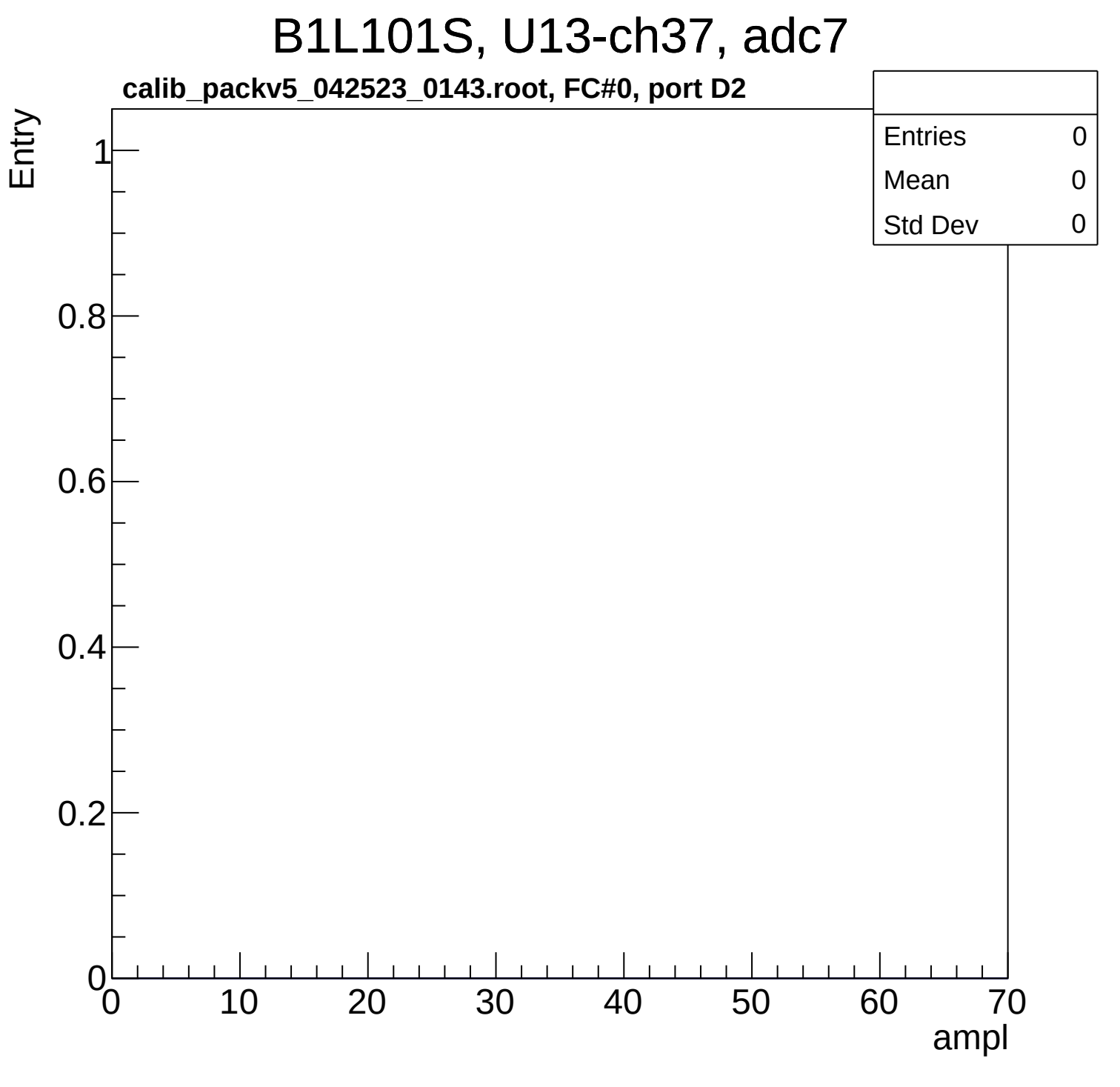
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch37, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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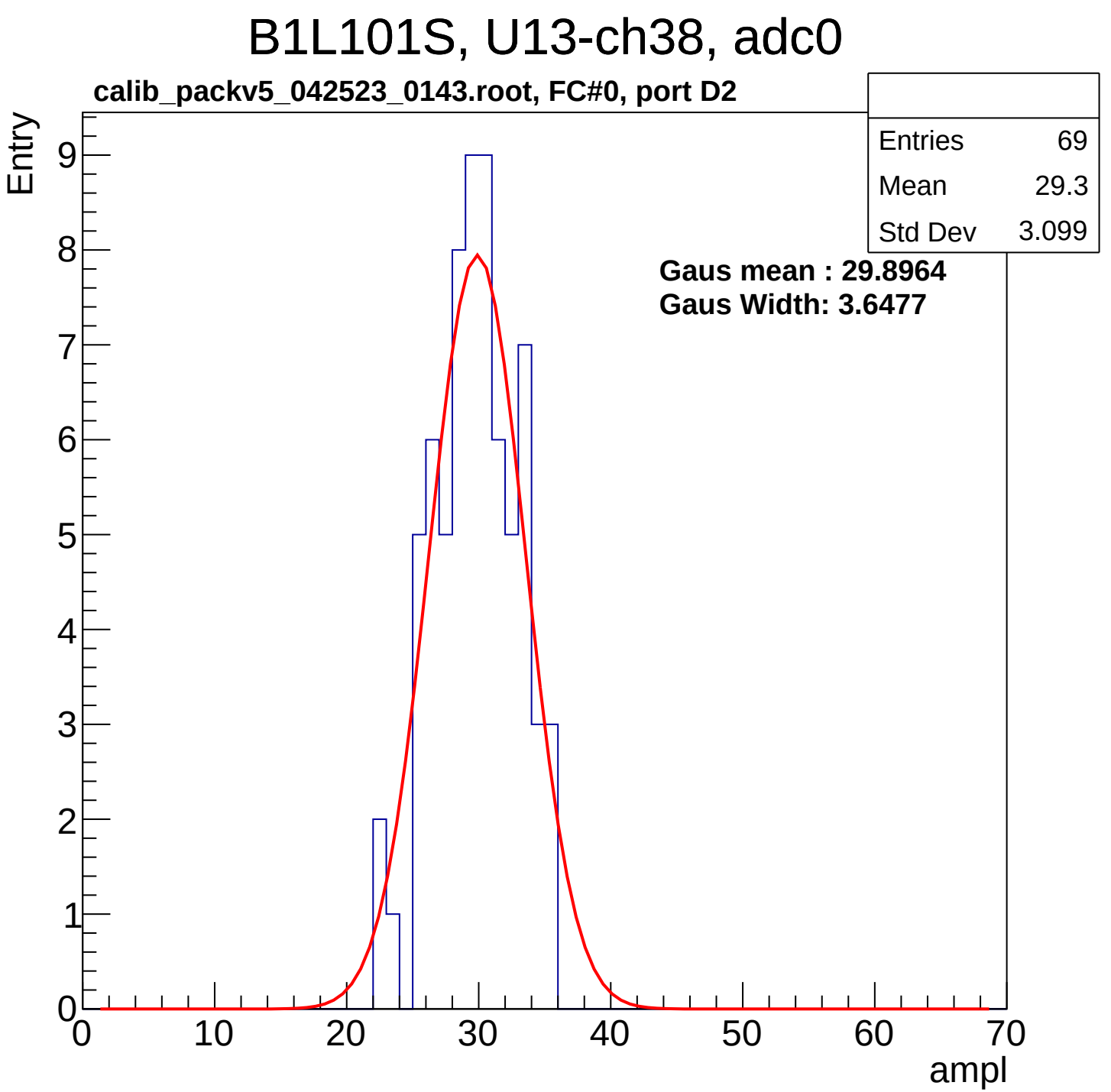
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch38, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 29.3
Std Dev 3.099
Gaus mean : 29.8964
Gaus Width: 3.6477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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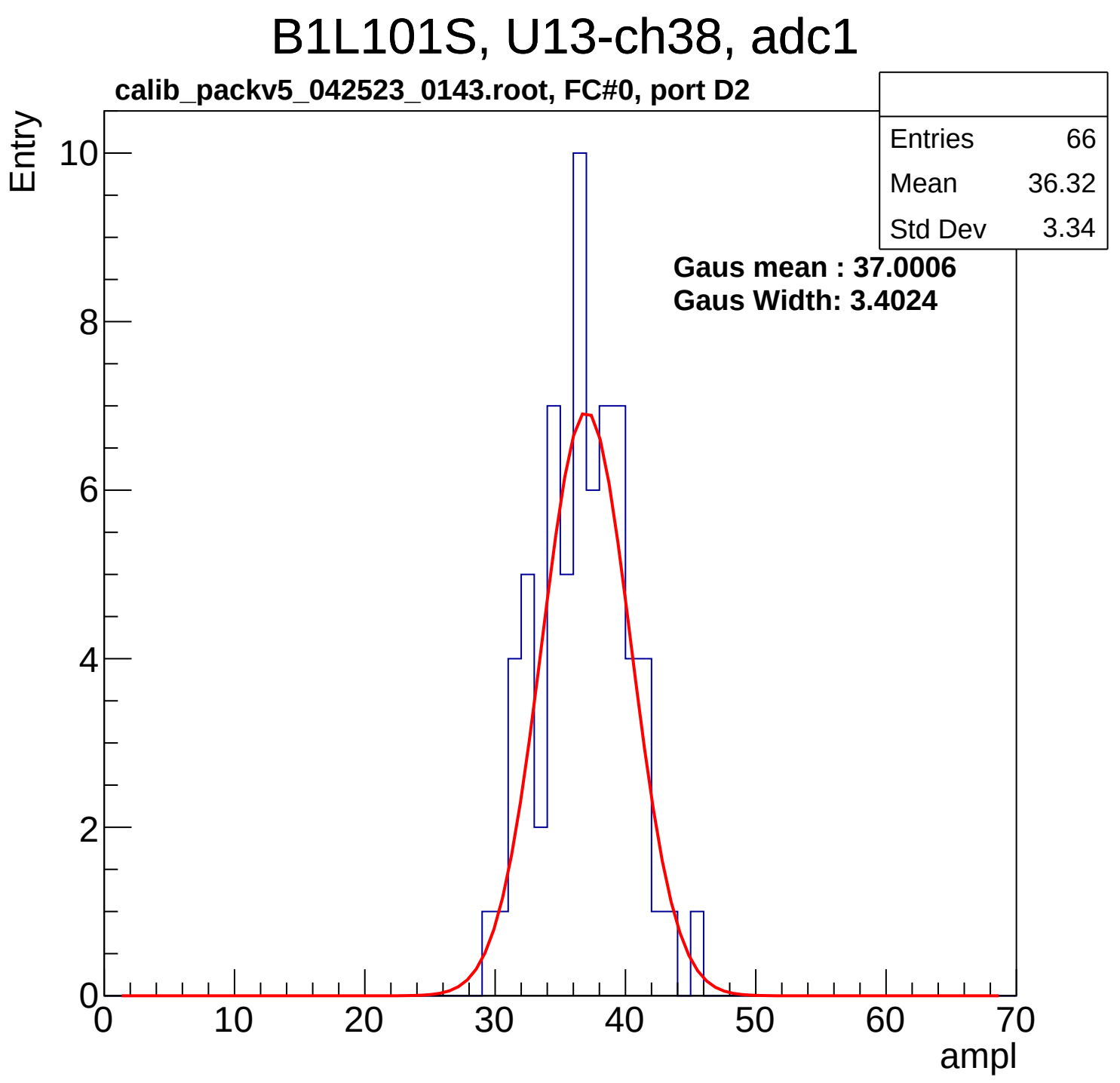
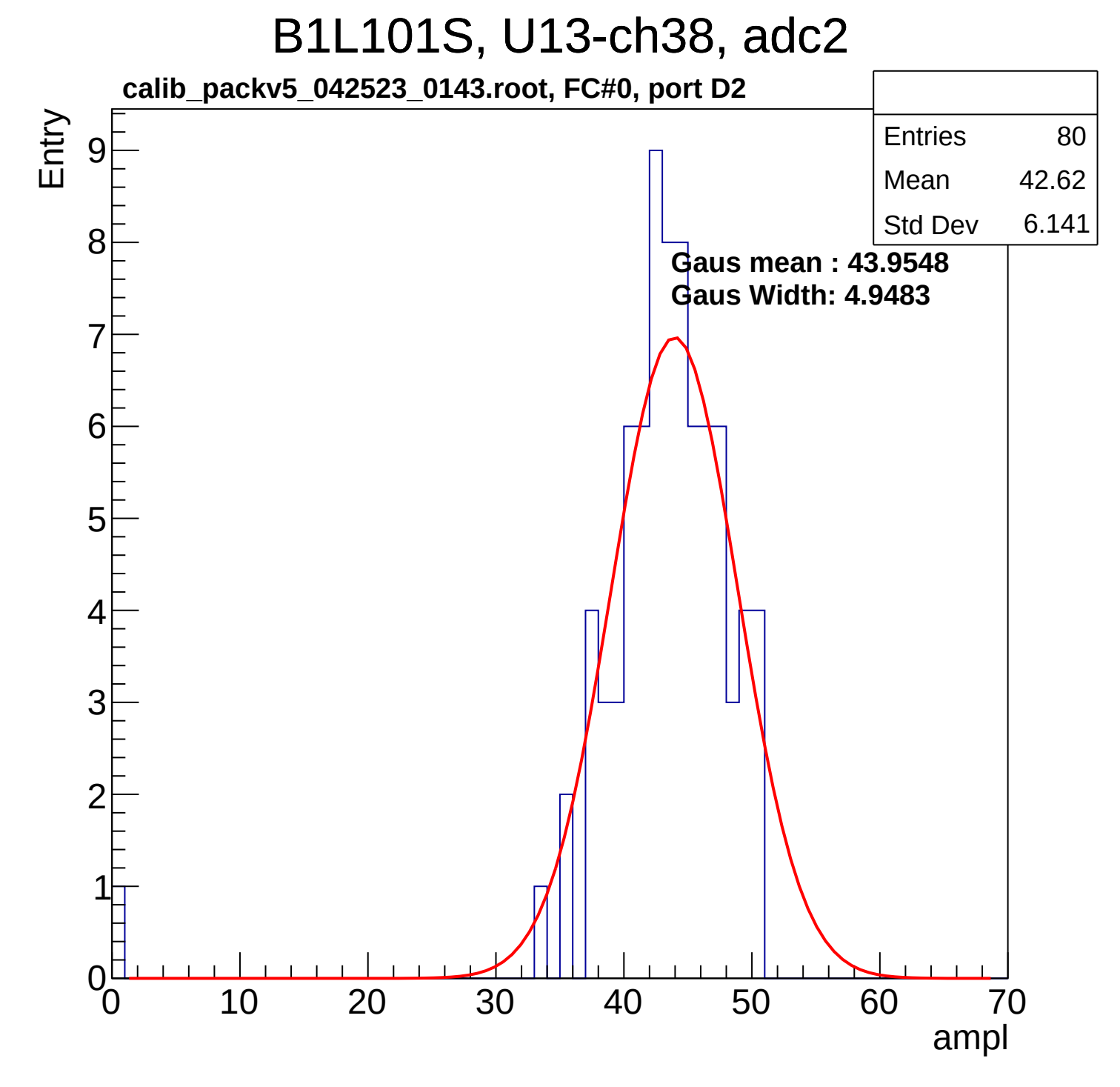

B1L101S, U13-ch38, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 36.32
Std Dev 3.34
Gaus mean : 37.0006
Gaus Width: 3.4024
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch38, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 42.62
Std Dev 6.141
Gaus mean : 43.9548
Gaus Width: 4.9483
Entry
ampl
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2
3
4
5
6
7
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0
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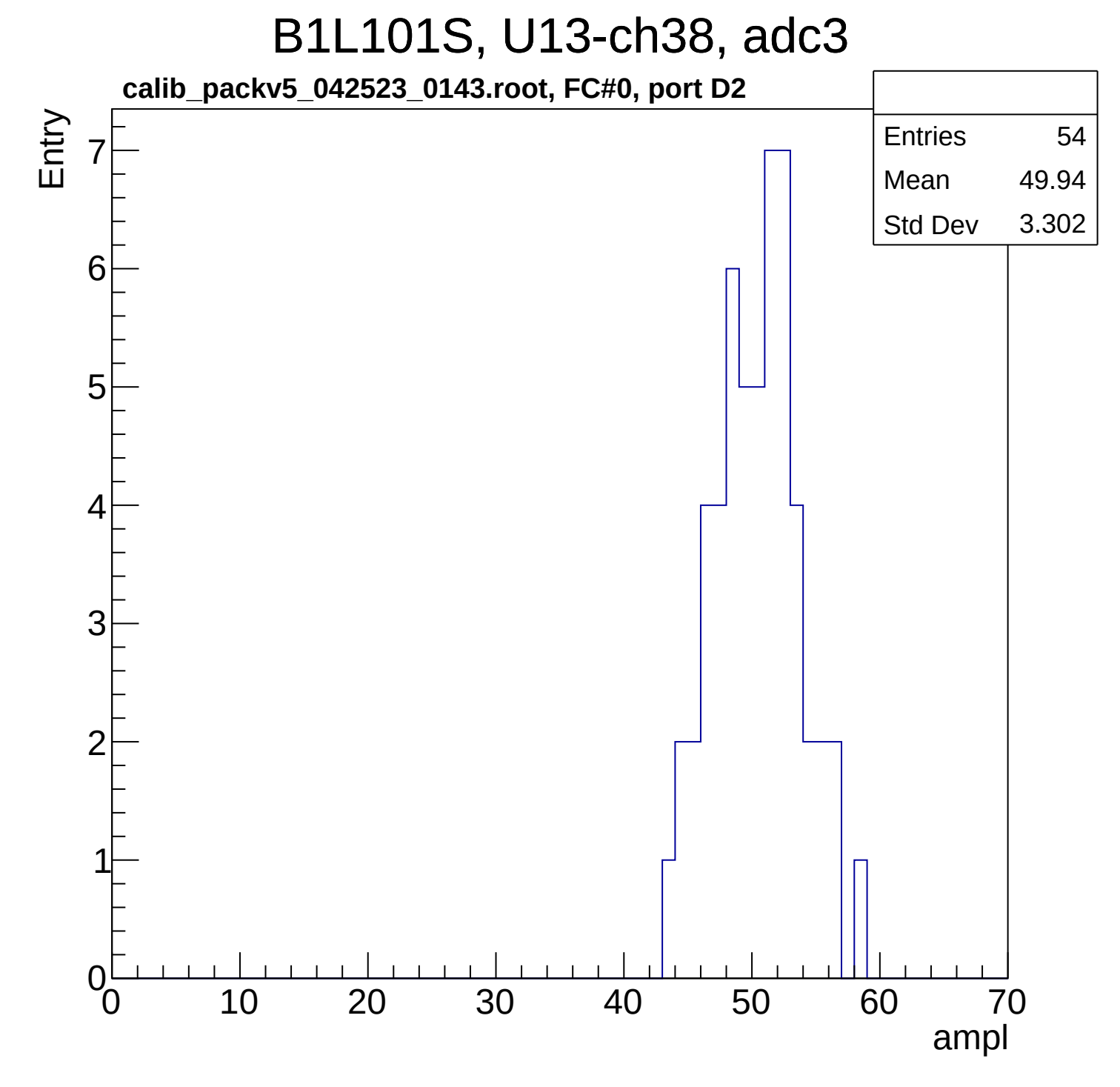
B1L101S, U13-ch38, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 49.94
Std Dev 3.302
Entry
ampl
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1
2
3
4
5
6
7
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60
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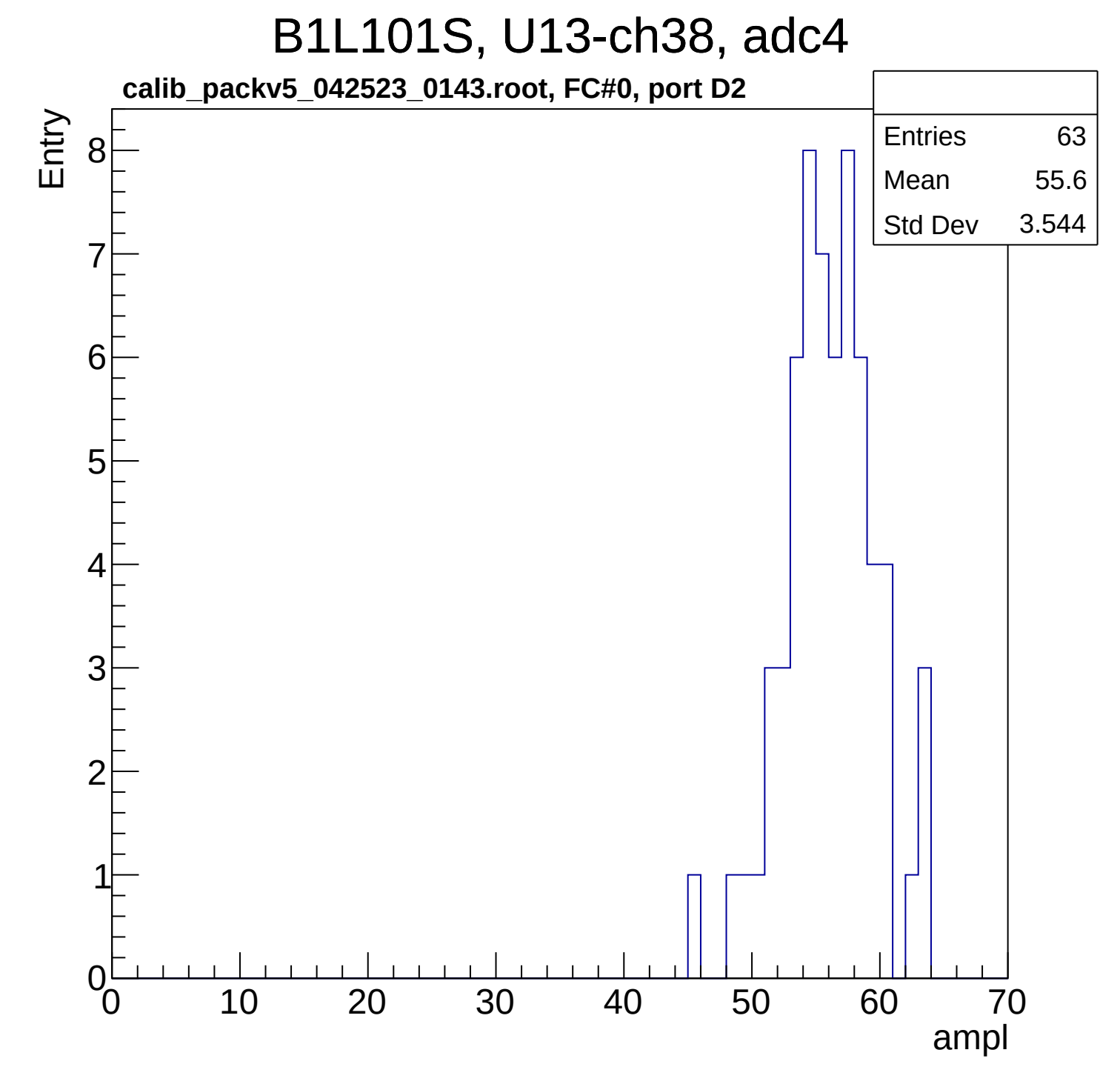
B1L101S, U13-ch38, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 55.6
Std Dev 3.544
Entry
ampl
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2
3
4
5
6
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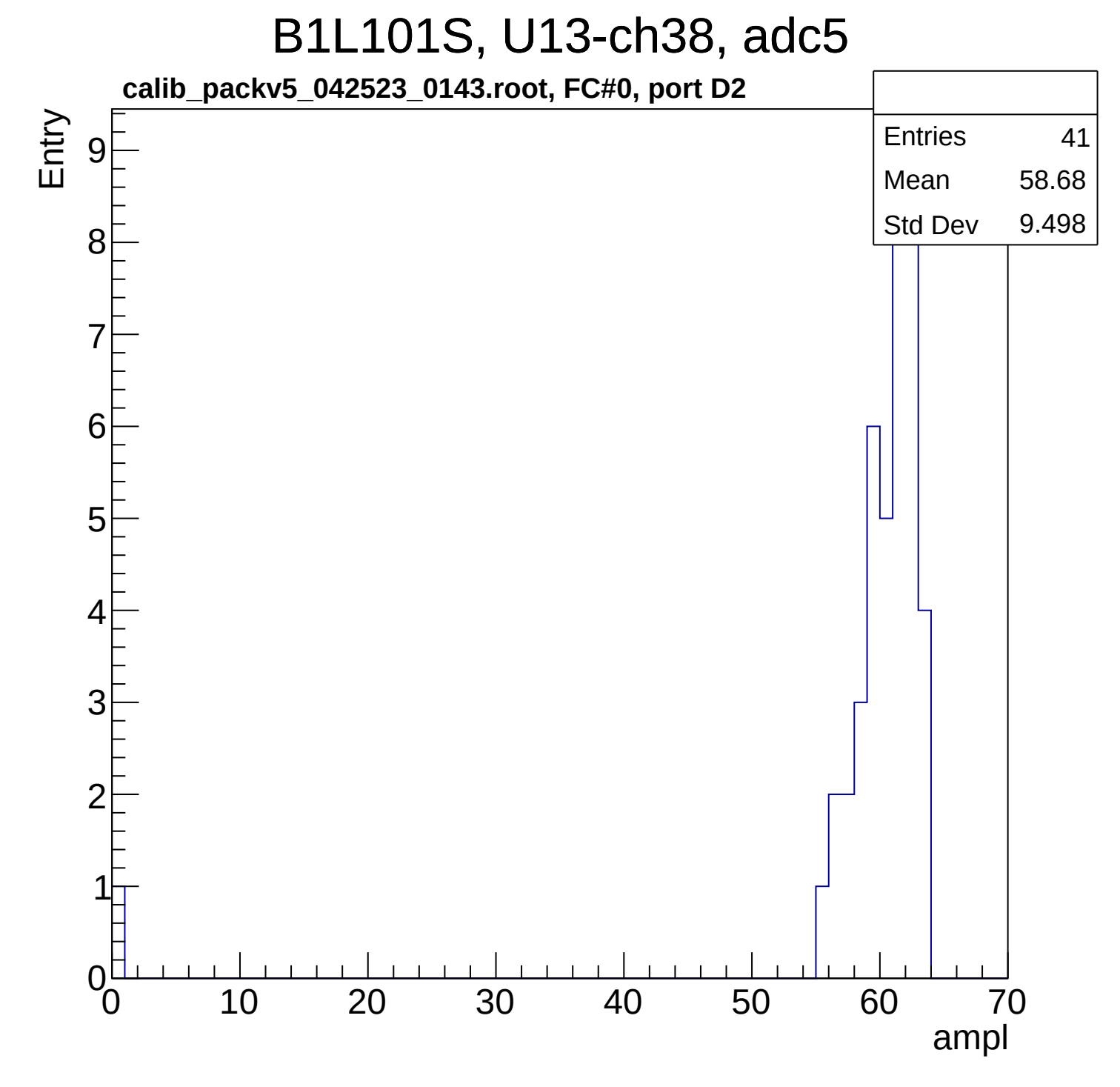
B1L101S, U13-ch38, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 58.68
Std Dev 9.498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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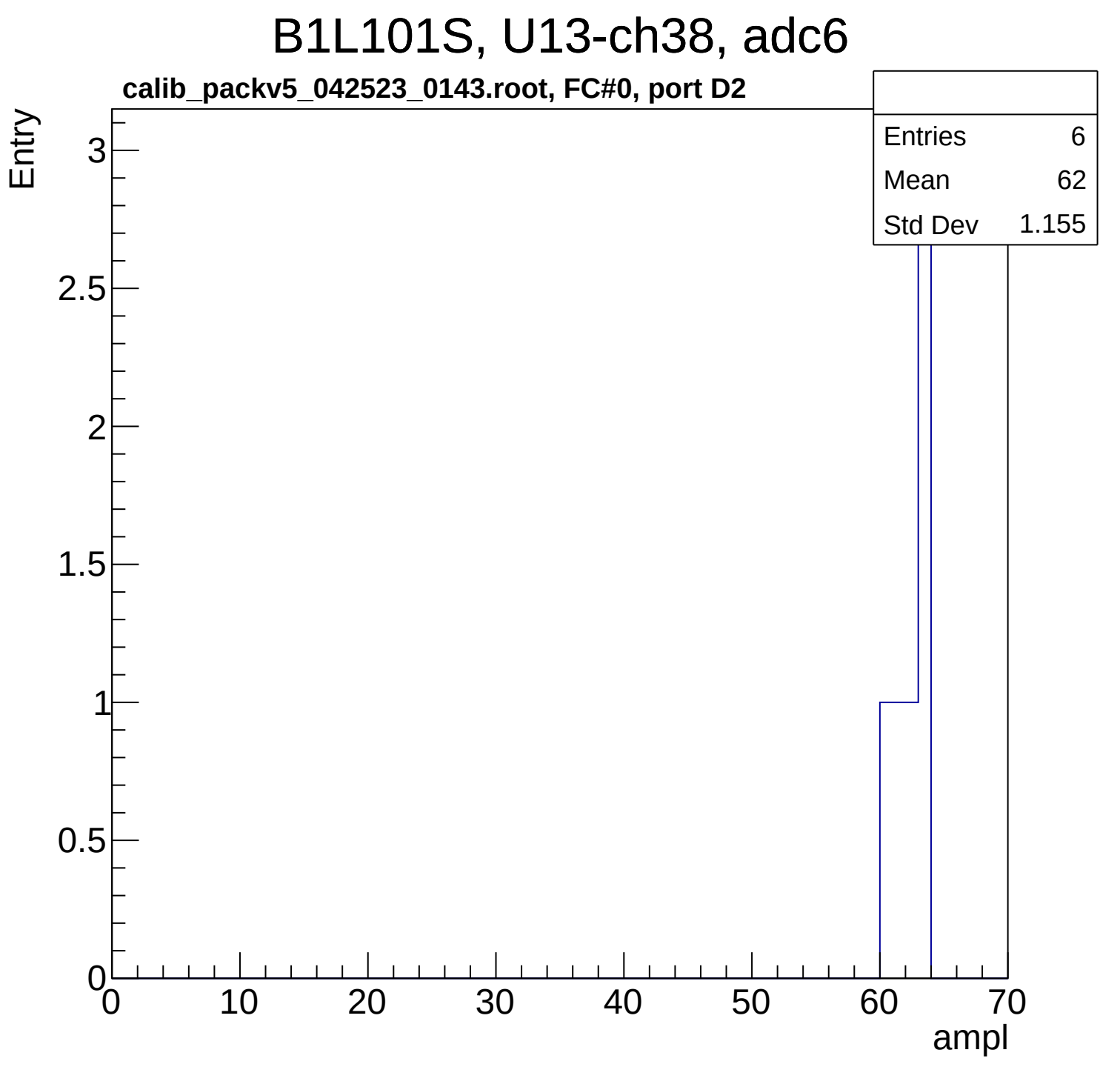
40
50
60
70


B1L101S, U13-ch38, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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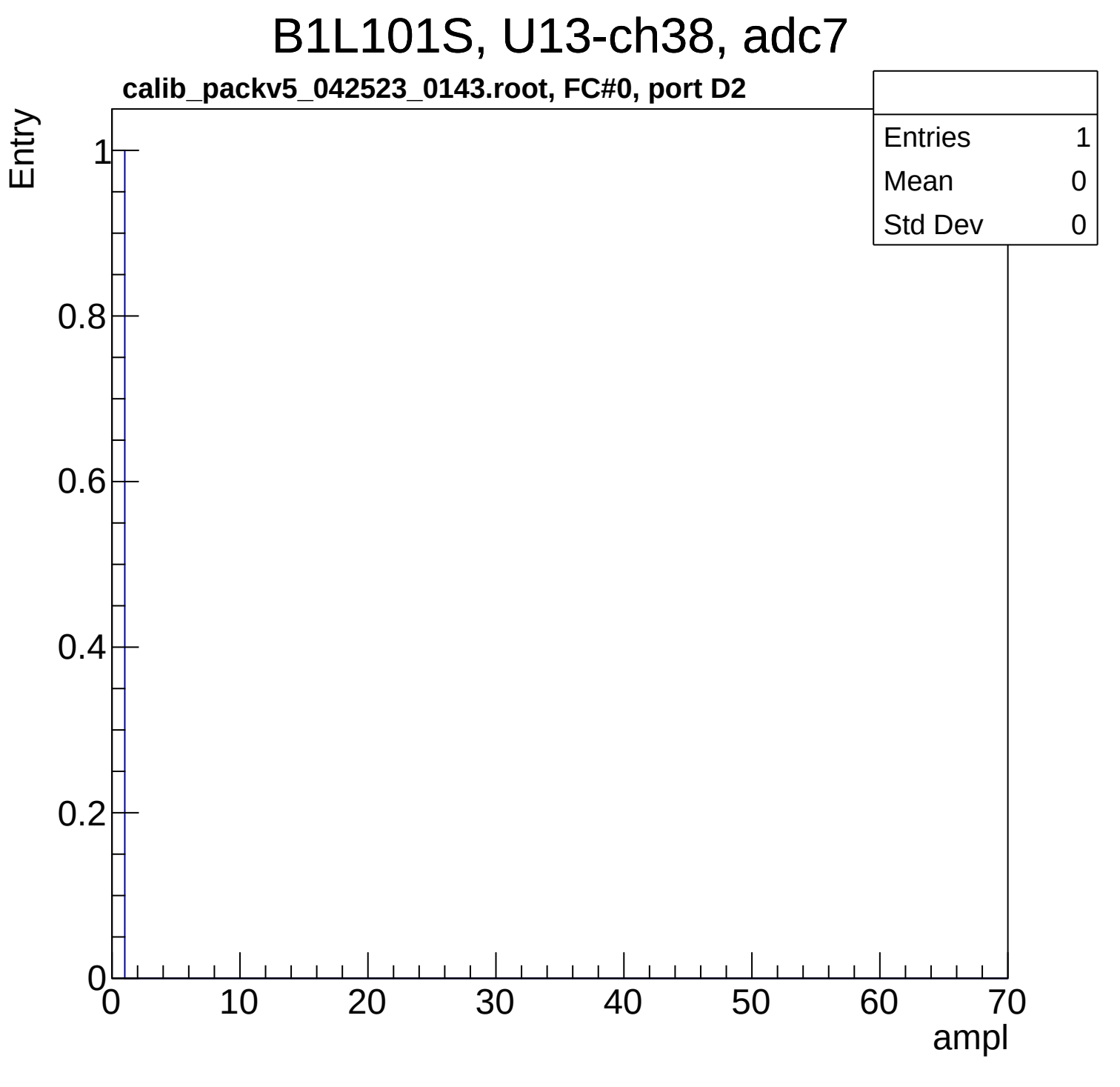
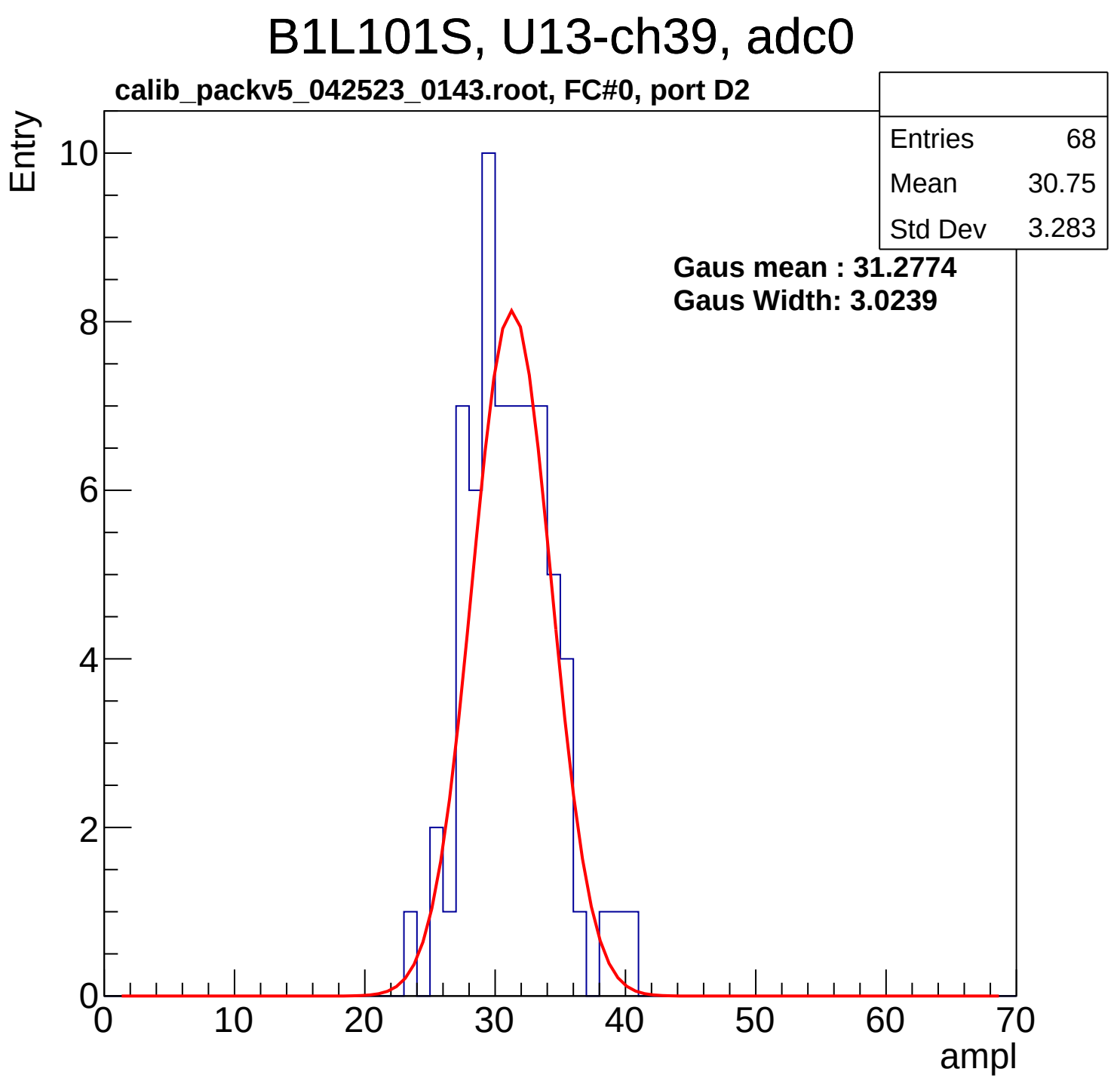
40
50
60
70


B1L101S, U13-ch38, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch39, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 30.75
Std Dev 3.283
Gaus mean : 31.2774
Gaus Width: 3.0239
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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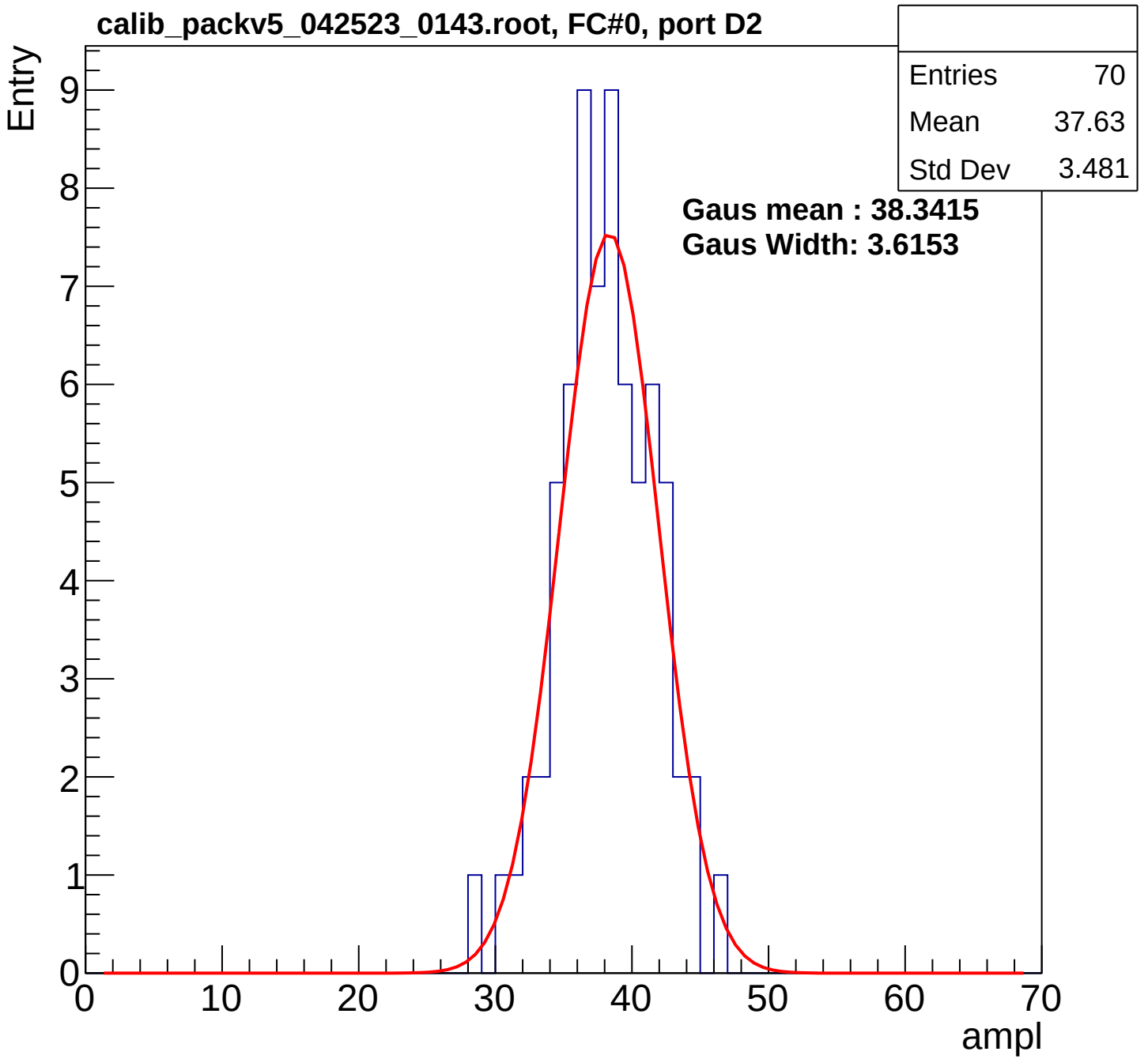
B1L101S, U13-ch39, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 37.63
Std Dev 3.481
Gaus mean : 38.3415
Gaus Width: 3.6153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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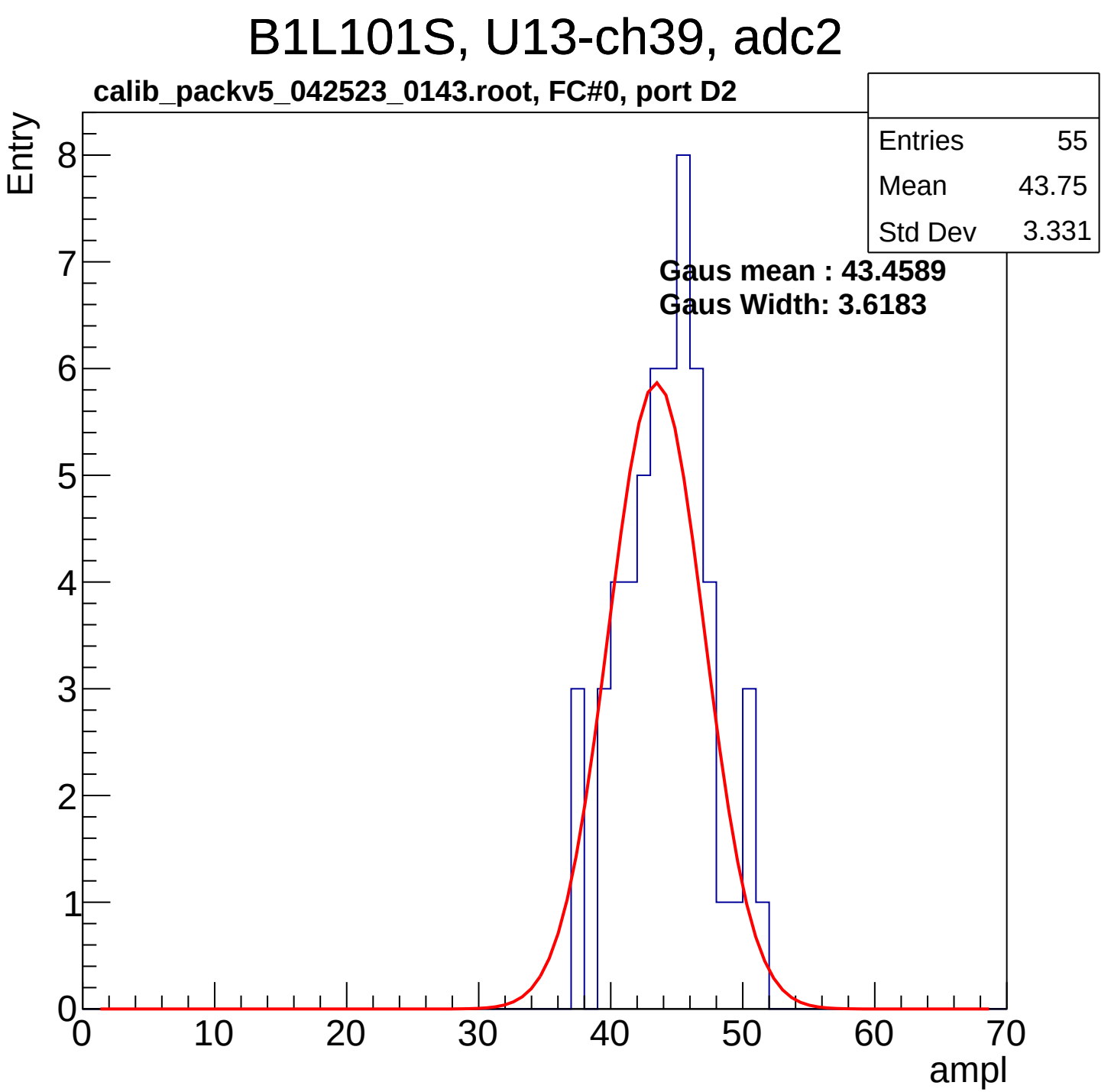
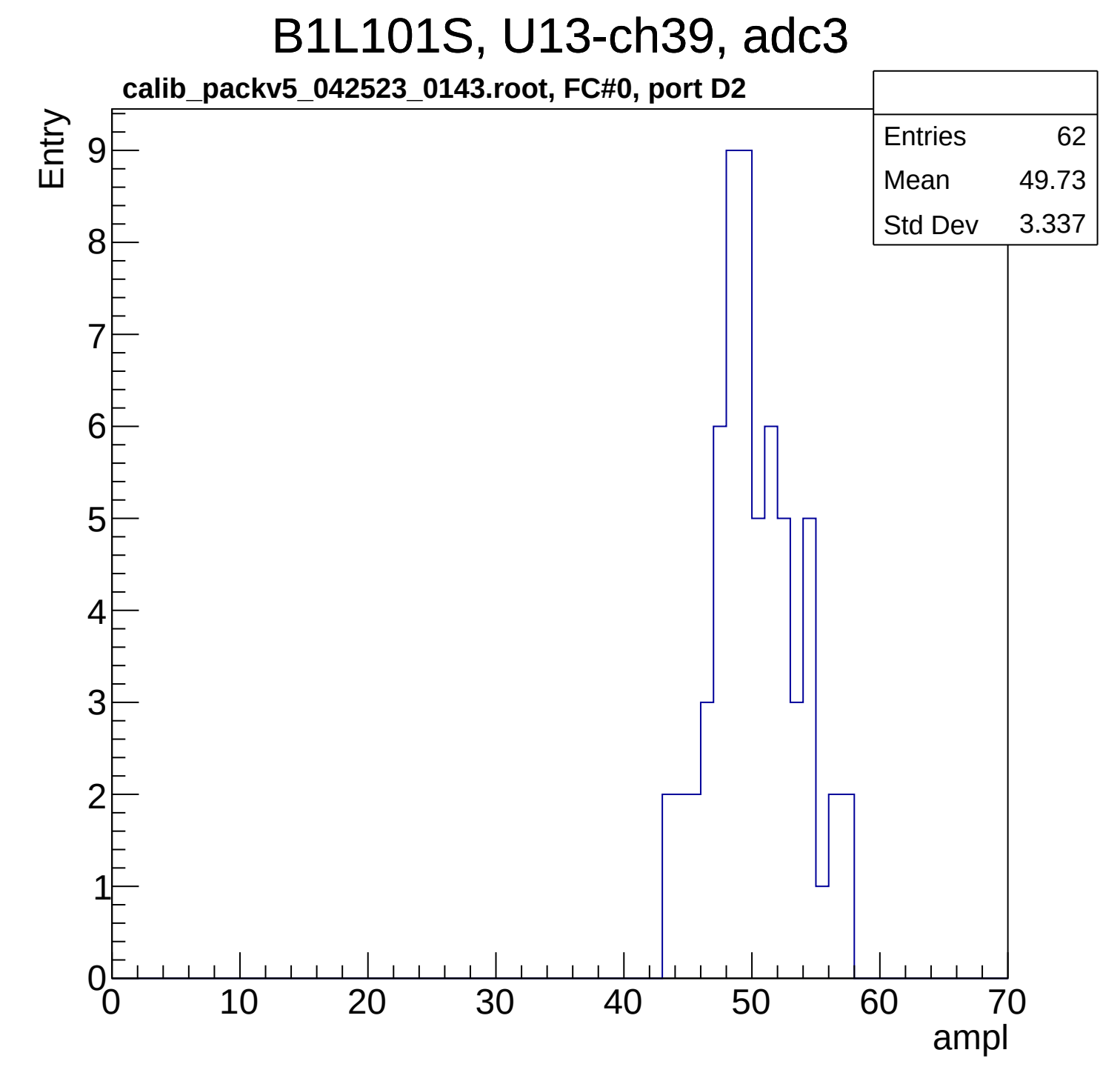

B1L101S, U13-ch39, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 43.75
Std Dev 3.331
Gaus mean : 43.4589
Gaus Width: 3.6183
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch39, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 49.73
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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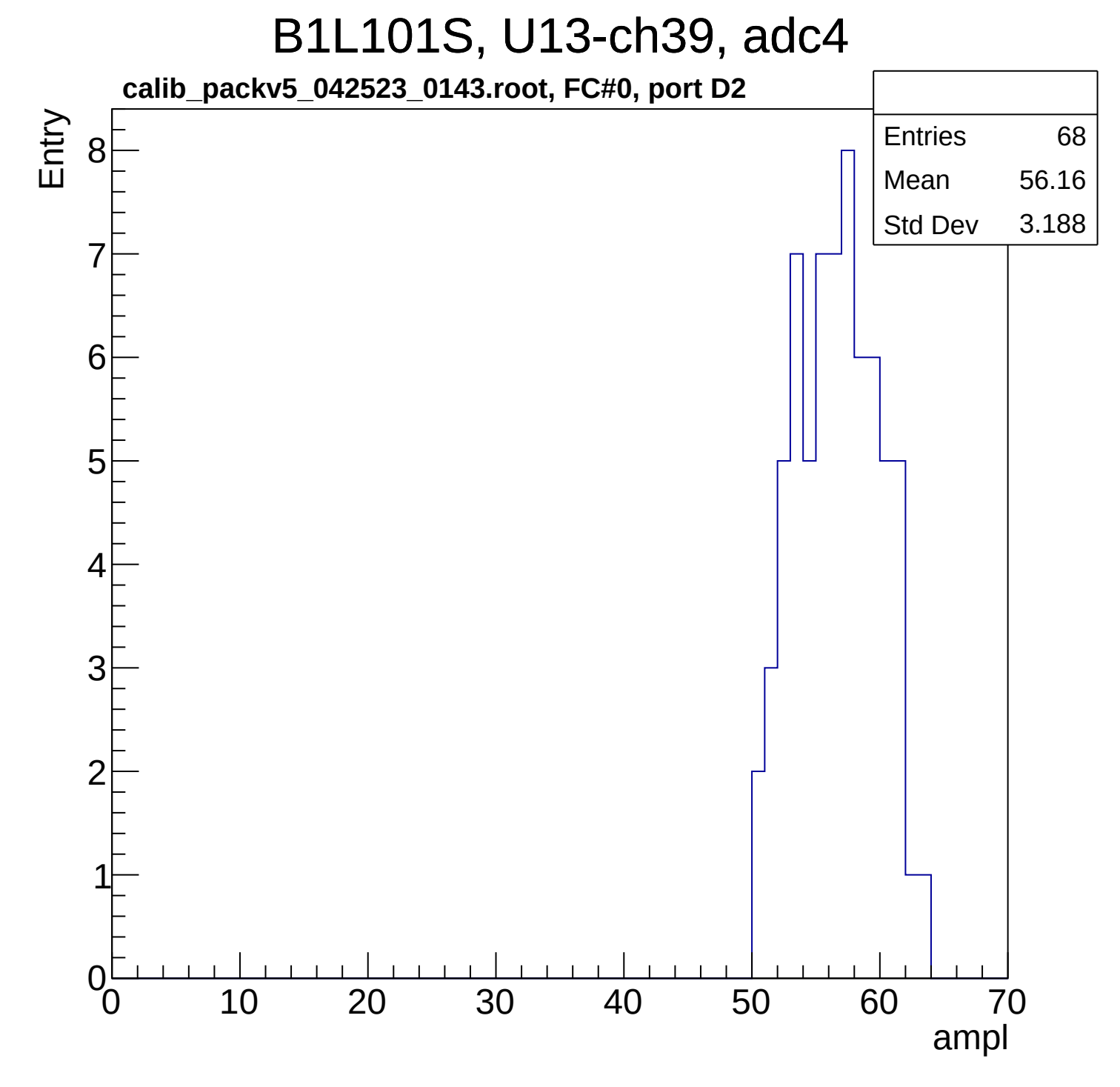
B1L101S, U13-ch39, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 56.16
Std Dev 3.188
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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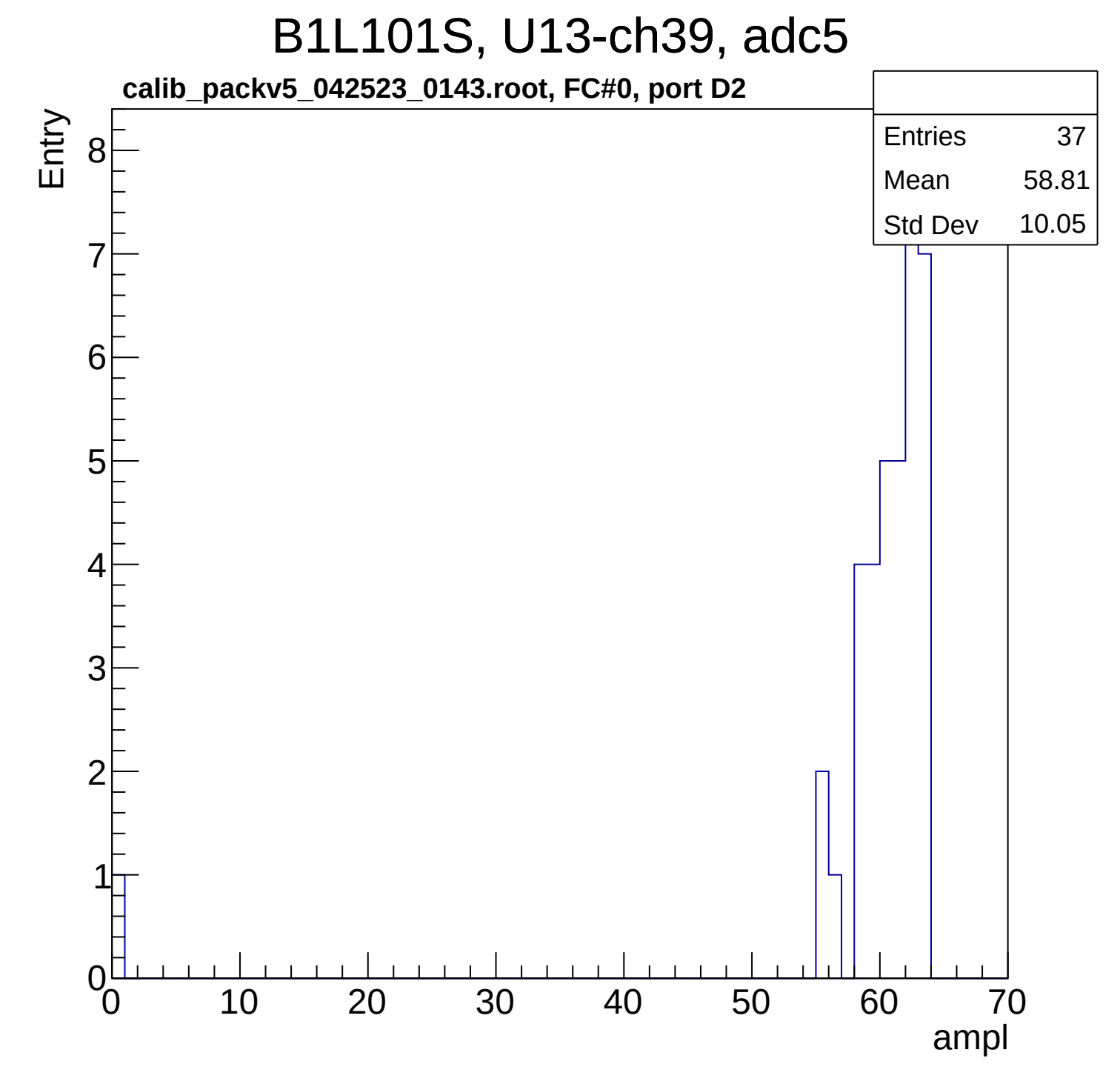
B1L101S, U13-ch39, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 37
Mean 58.81
Std Dev 10.05
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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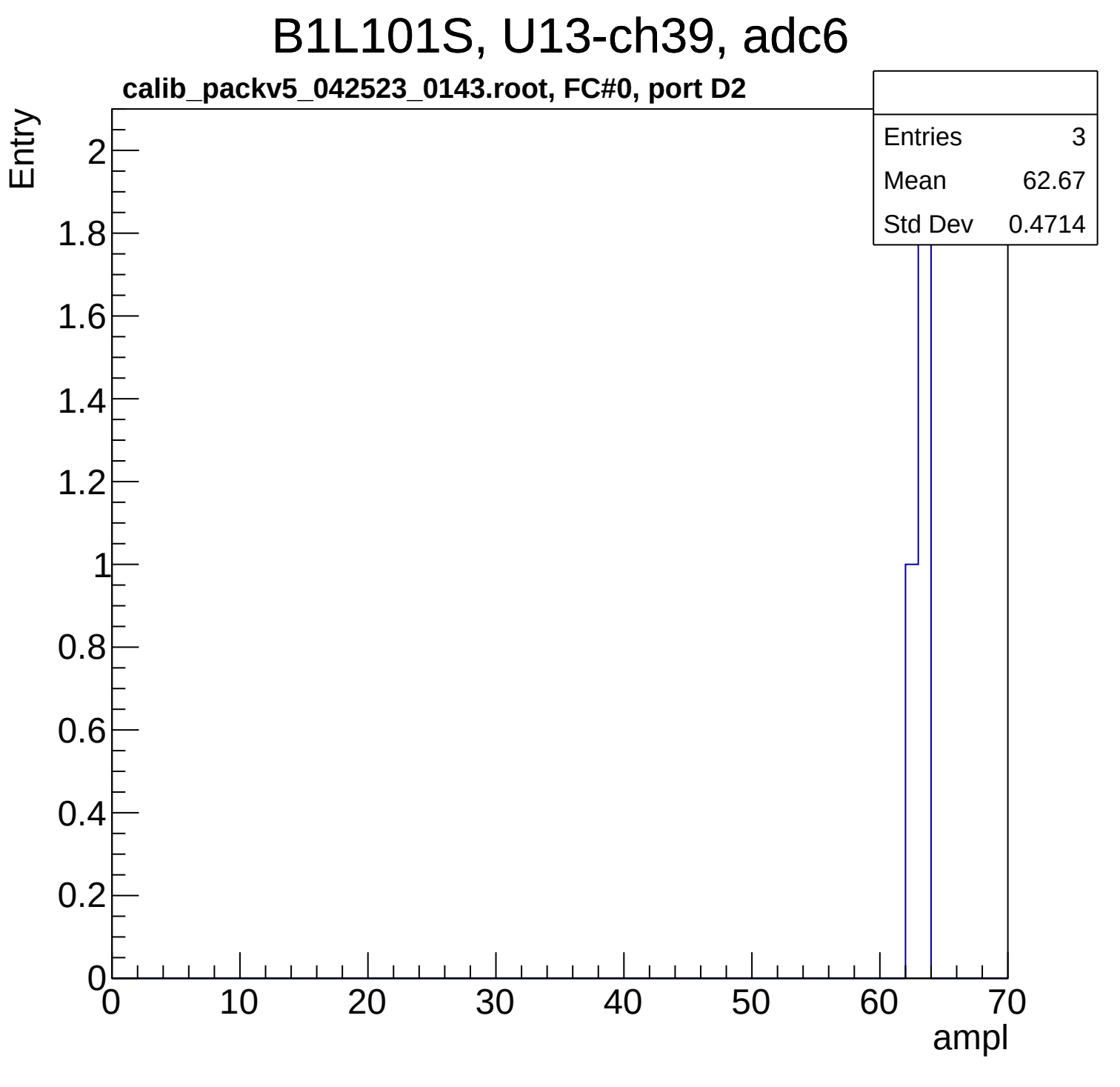
B1L101S, U13-ch39, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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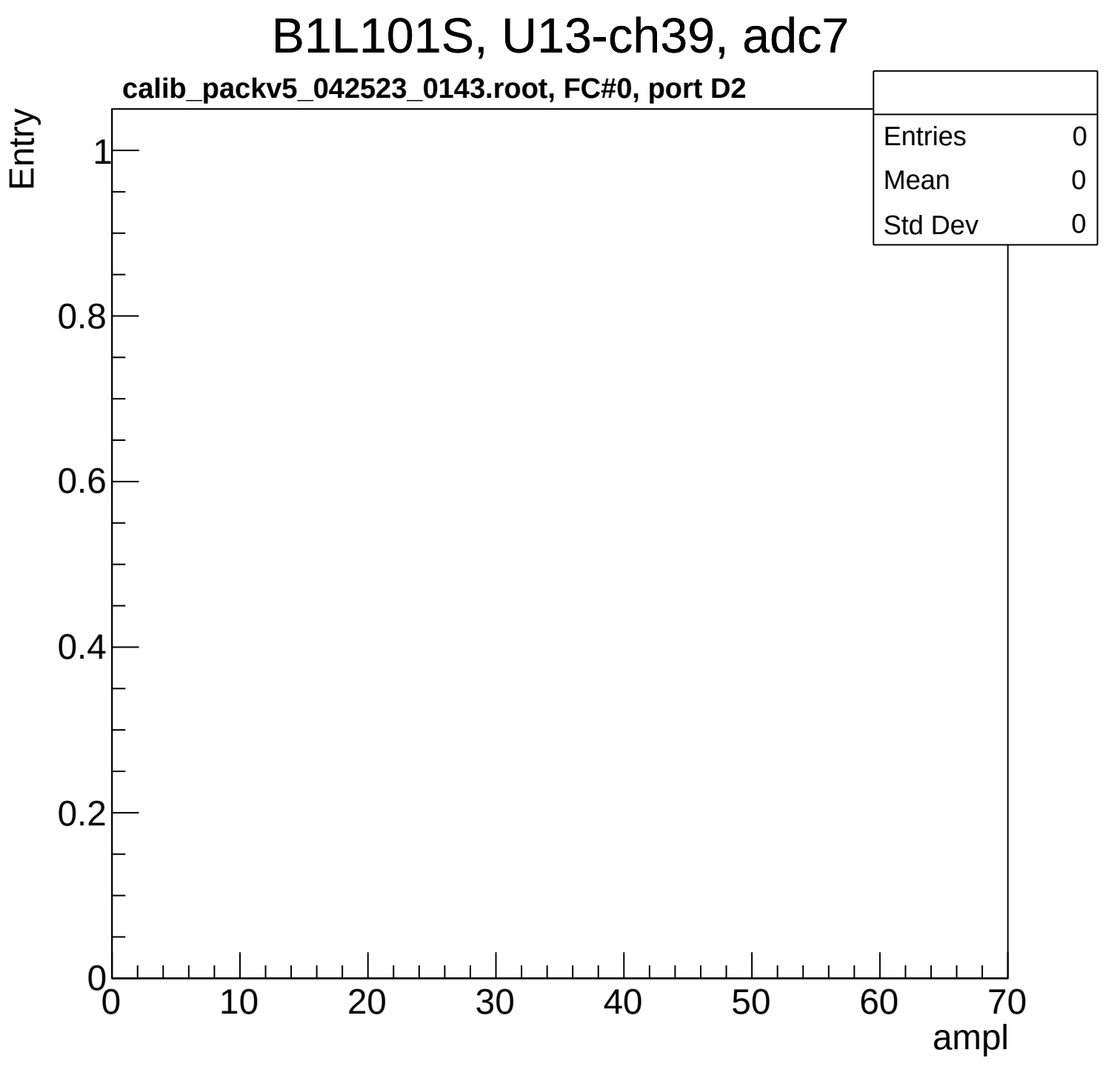
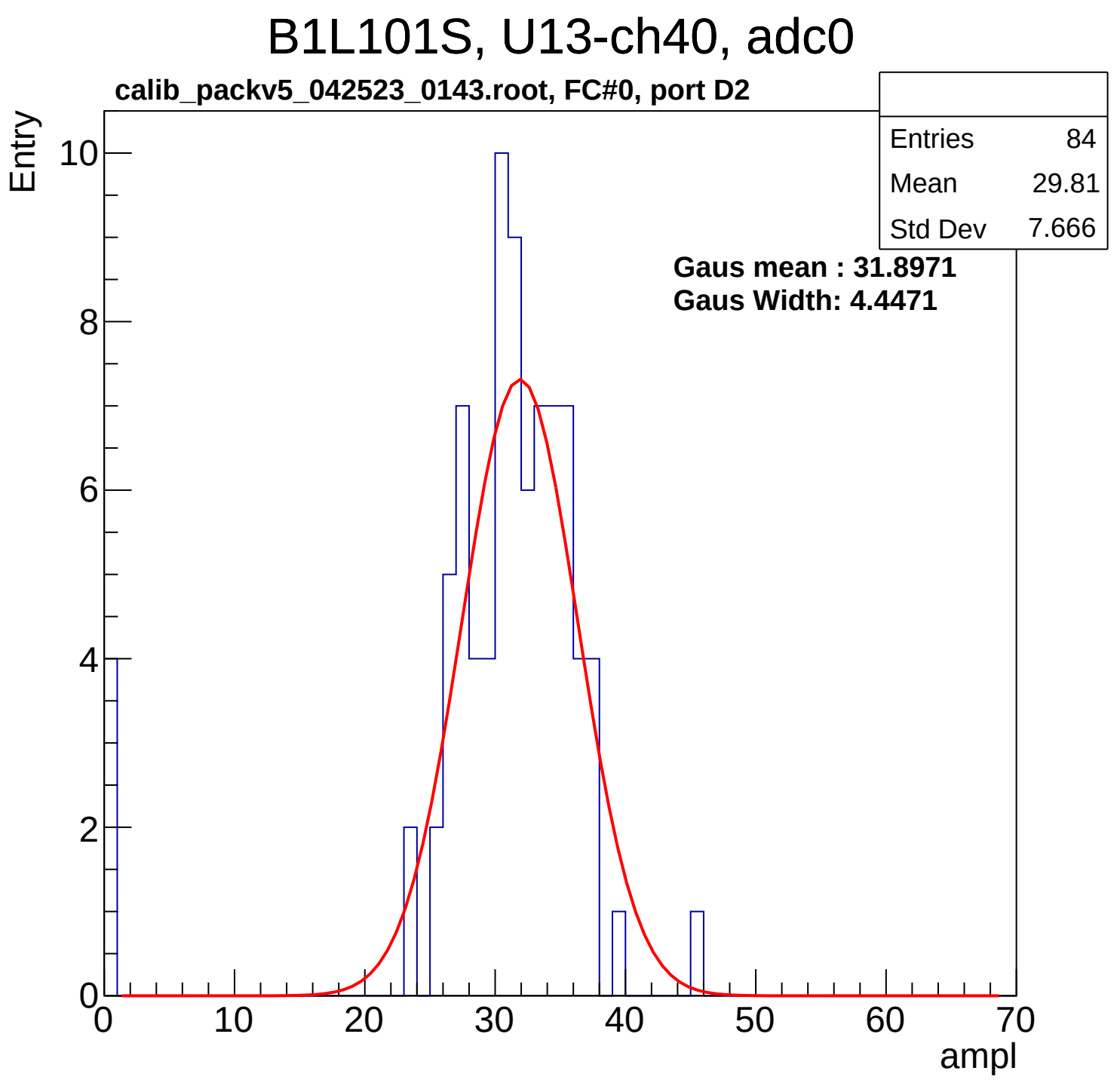

B1L101S, U13-ch39, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch40, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 84
Mean 29.81
Std Dev 7.666
Gaus mean : 31.8971
Gaus Width: 4.4471
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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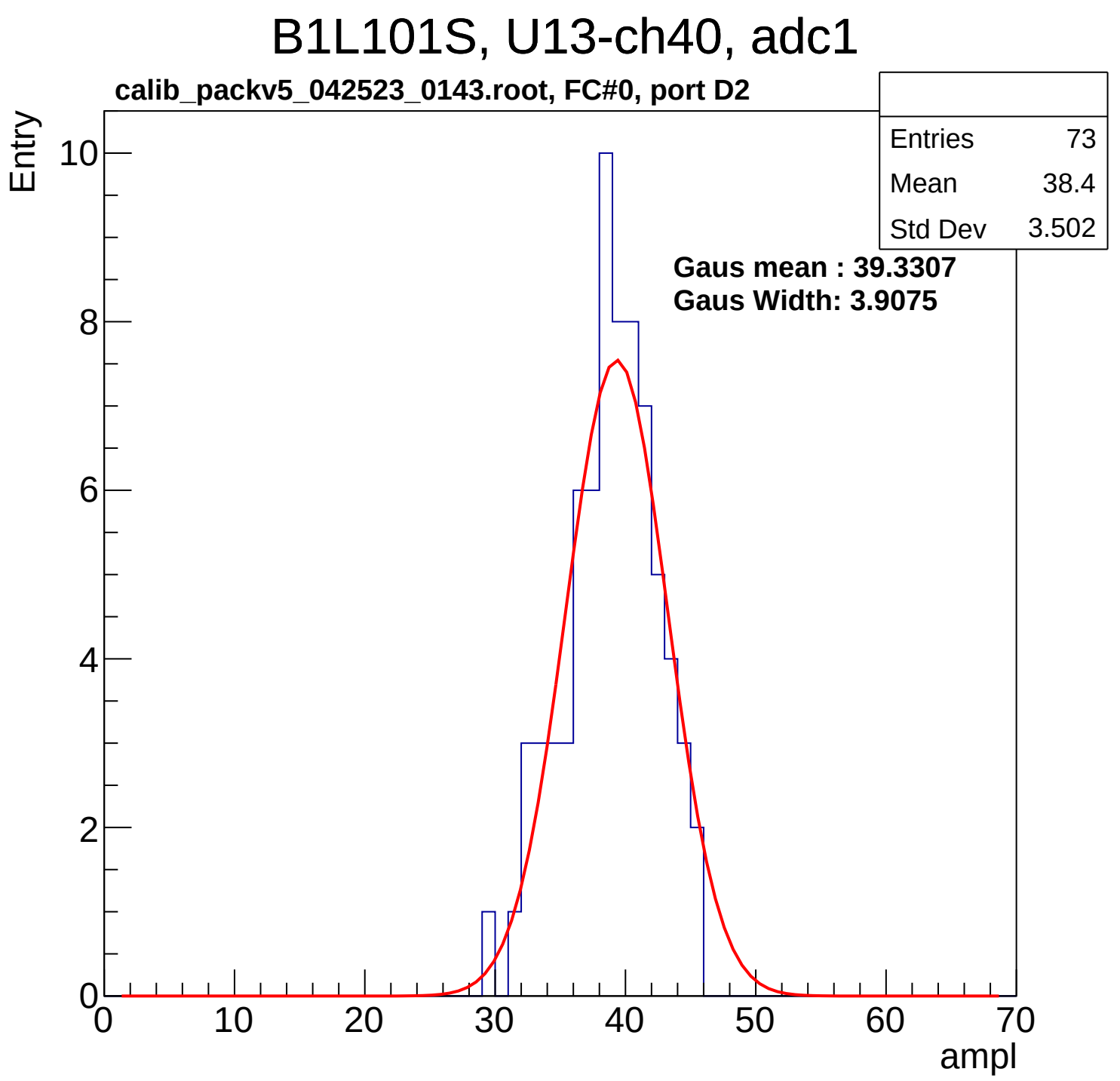
B1L101S, U13-ch40, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 38.4
Std Dev 3.502
Gaus mean : 39.3307
Gaus Width: 3.9075
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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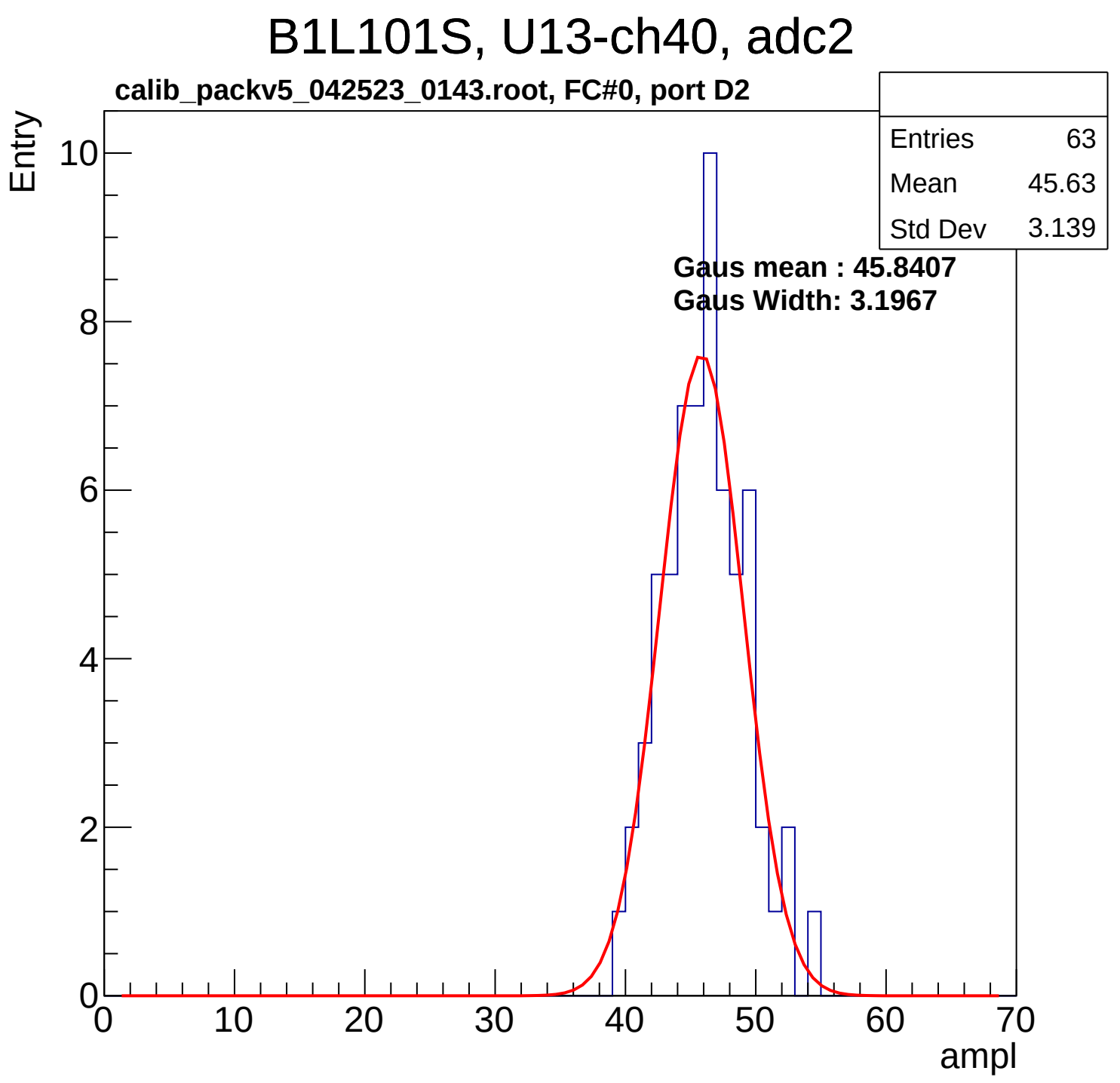
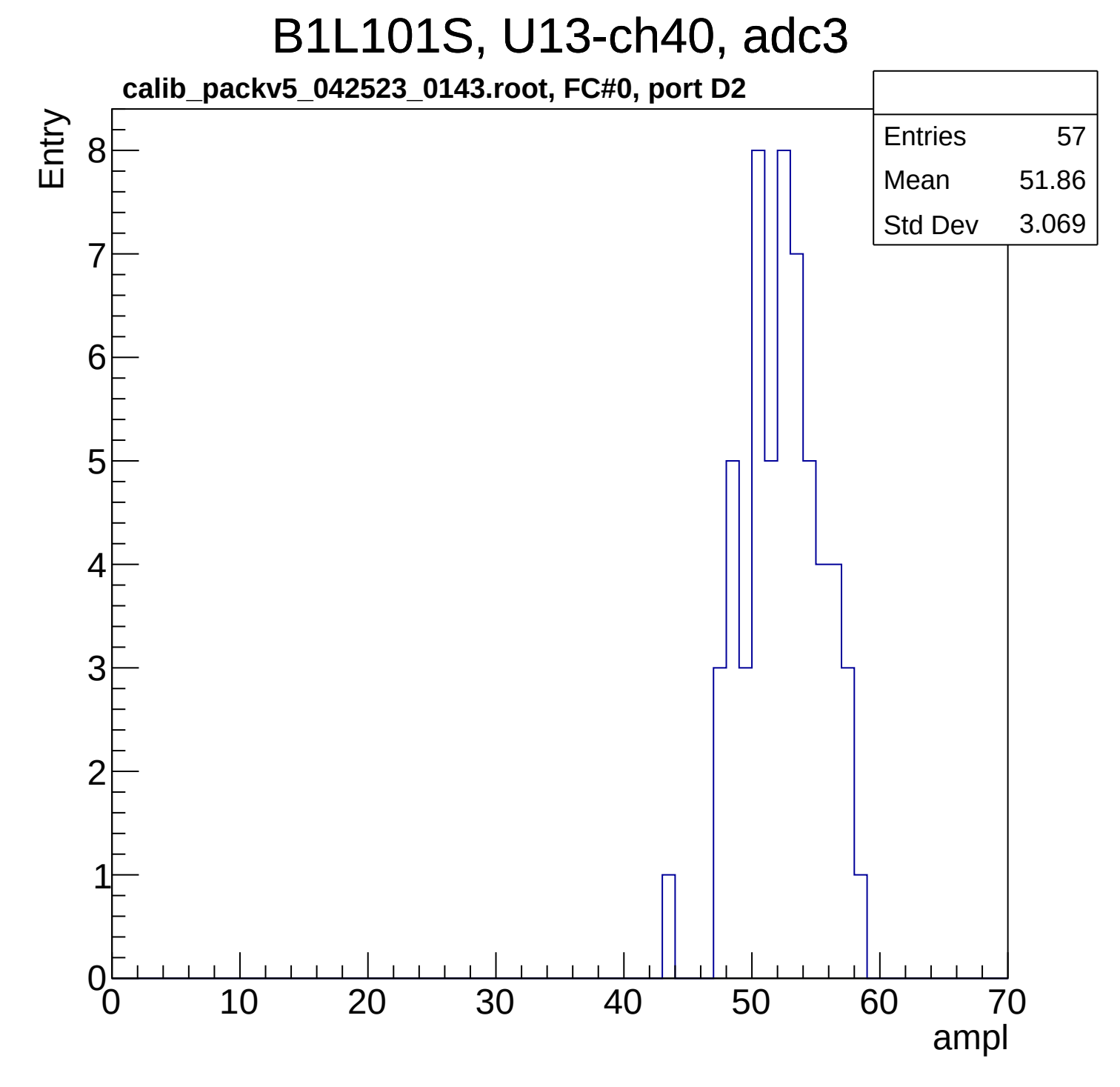
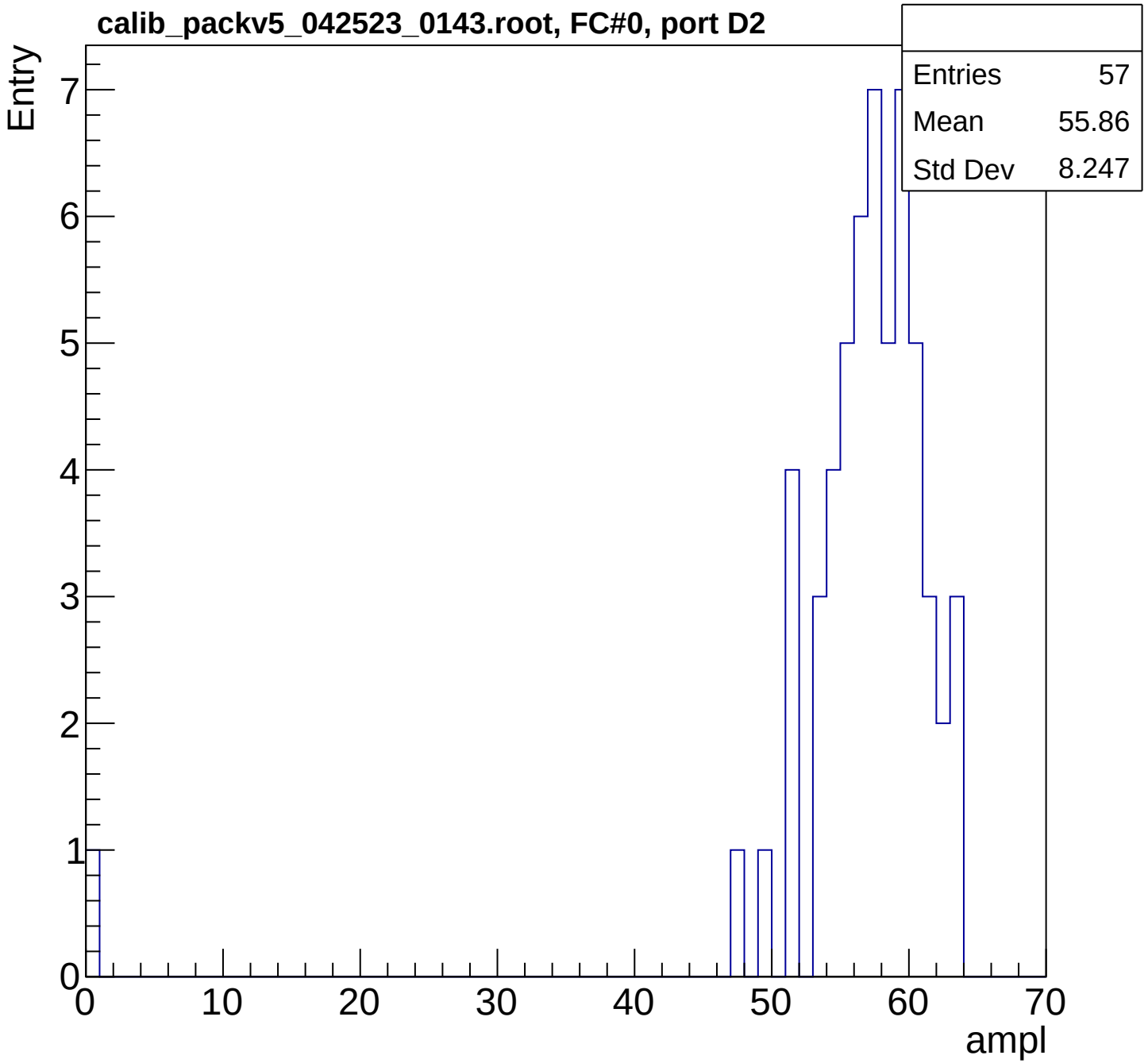
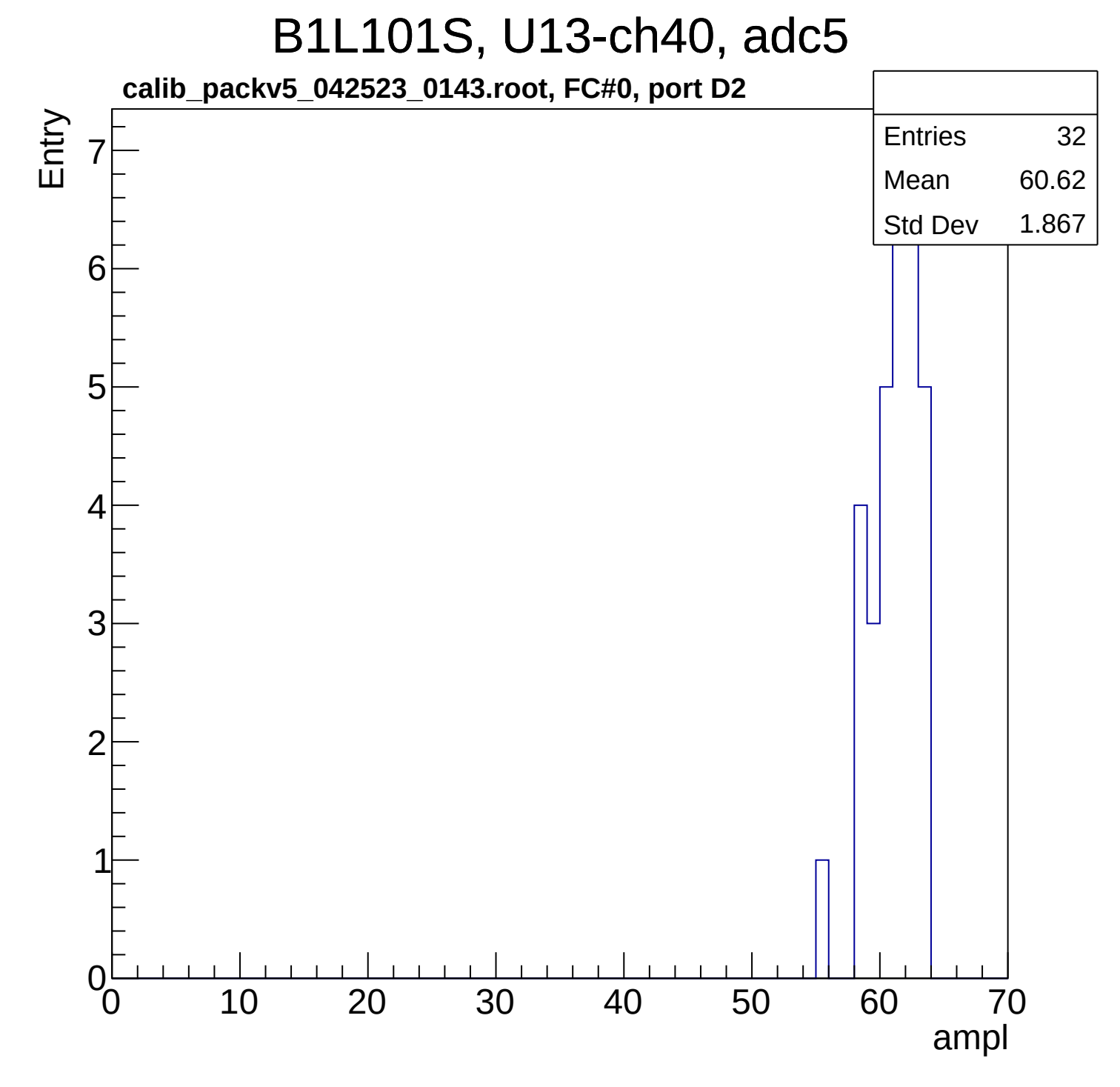
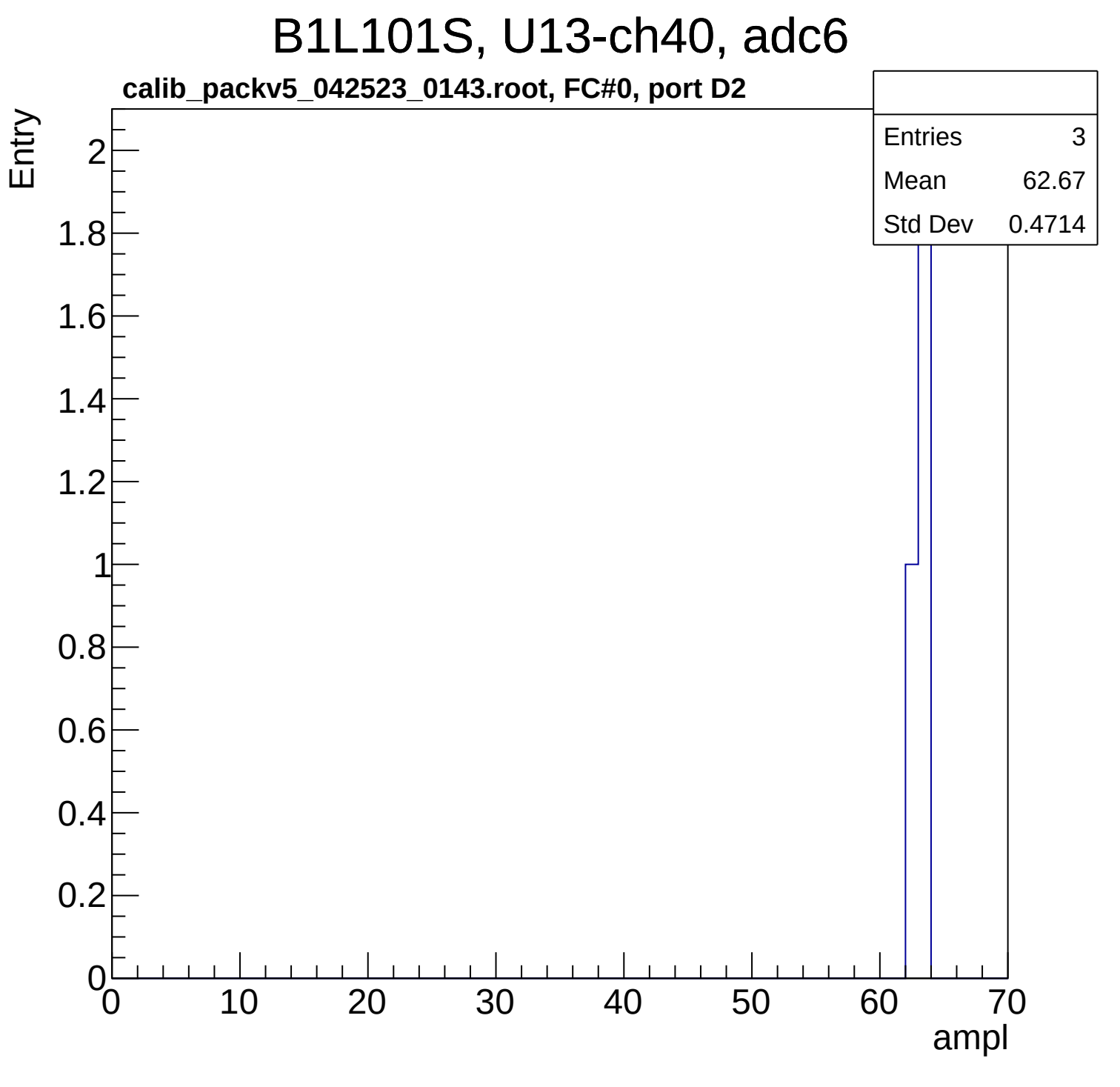
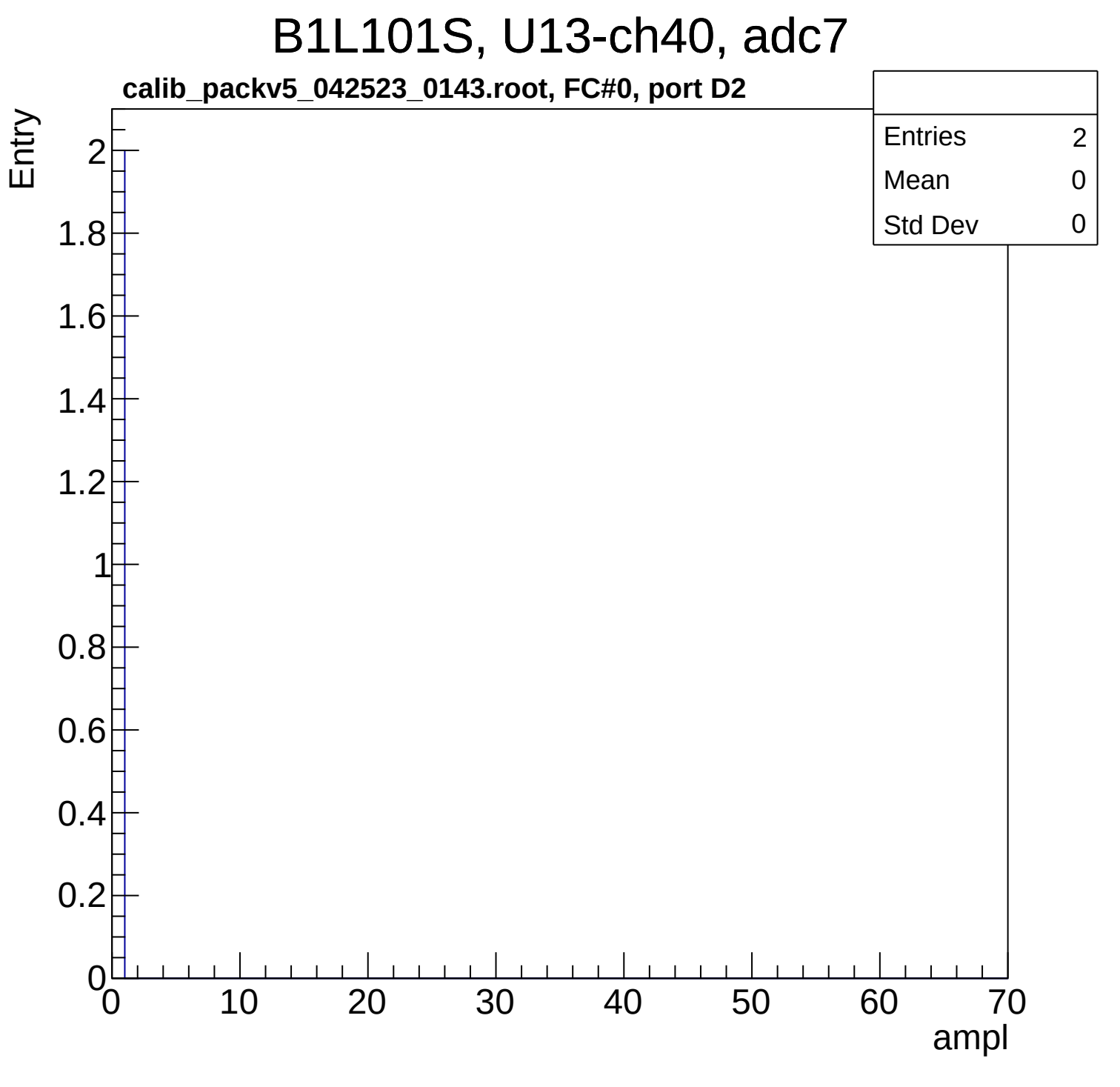

B1L101S, U13-ch40, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 45.63
Std Dev 3.139
Gaus mean : 45.8407
Gaus Width: 3.1967
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

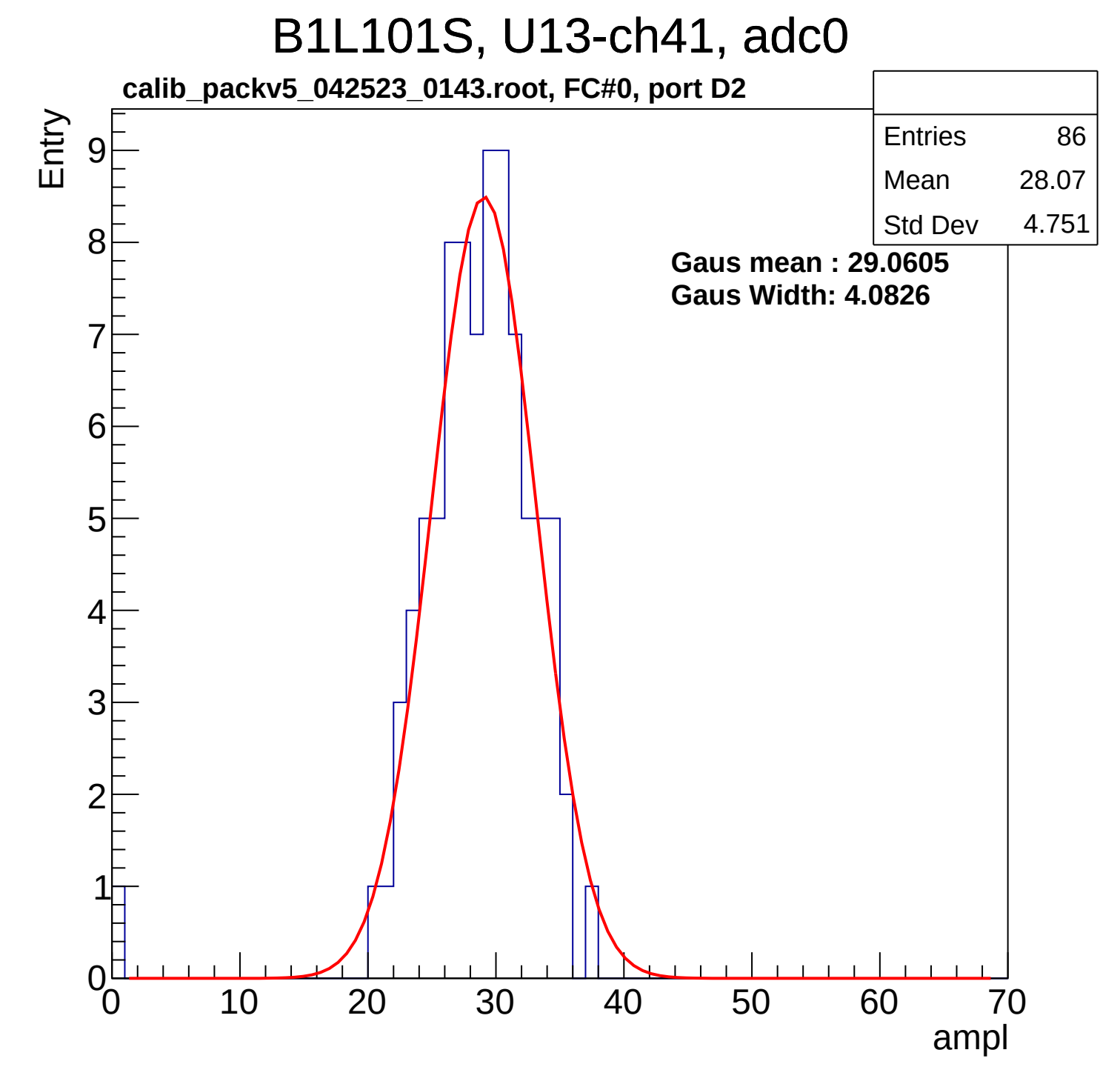

B1L101S, U13-ch40, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 51.86
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch40, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 55.86
Std Dev 8.247
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch40, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 32
Mean 60.62
Std Dev 1.867
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch40, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch40, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch41, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 86
Mean 28.07
Std Dev 4.751
Gaus mean : 29.0605
Gaus Width: 4.0826
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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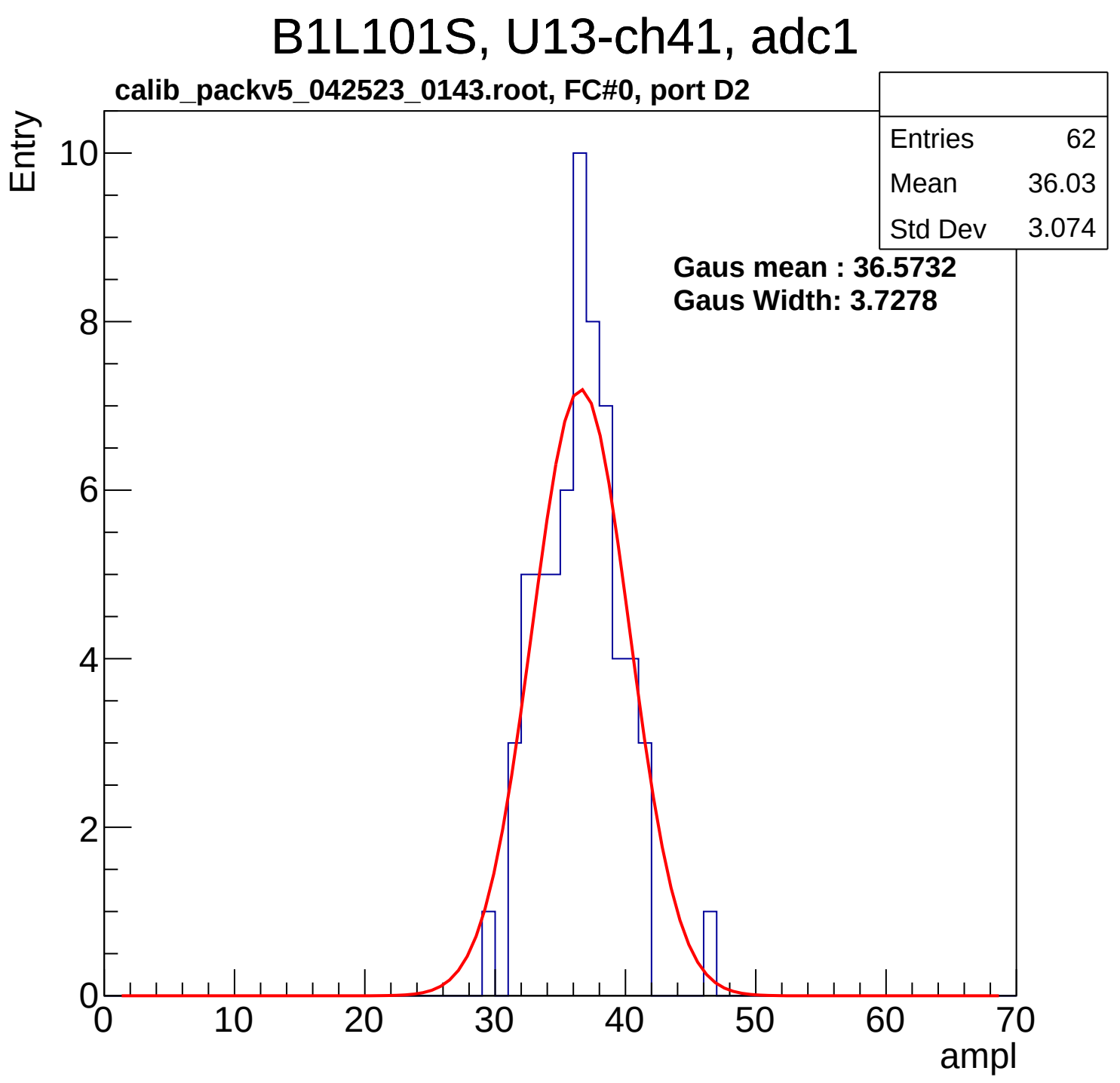
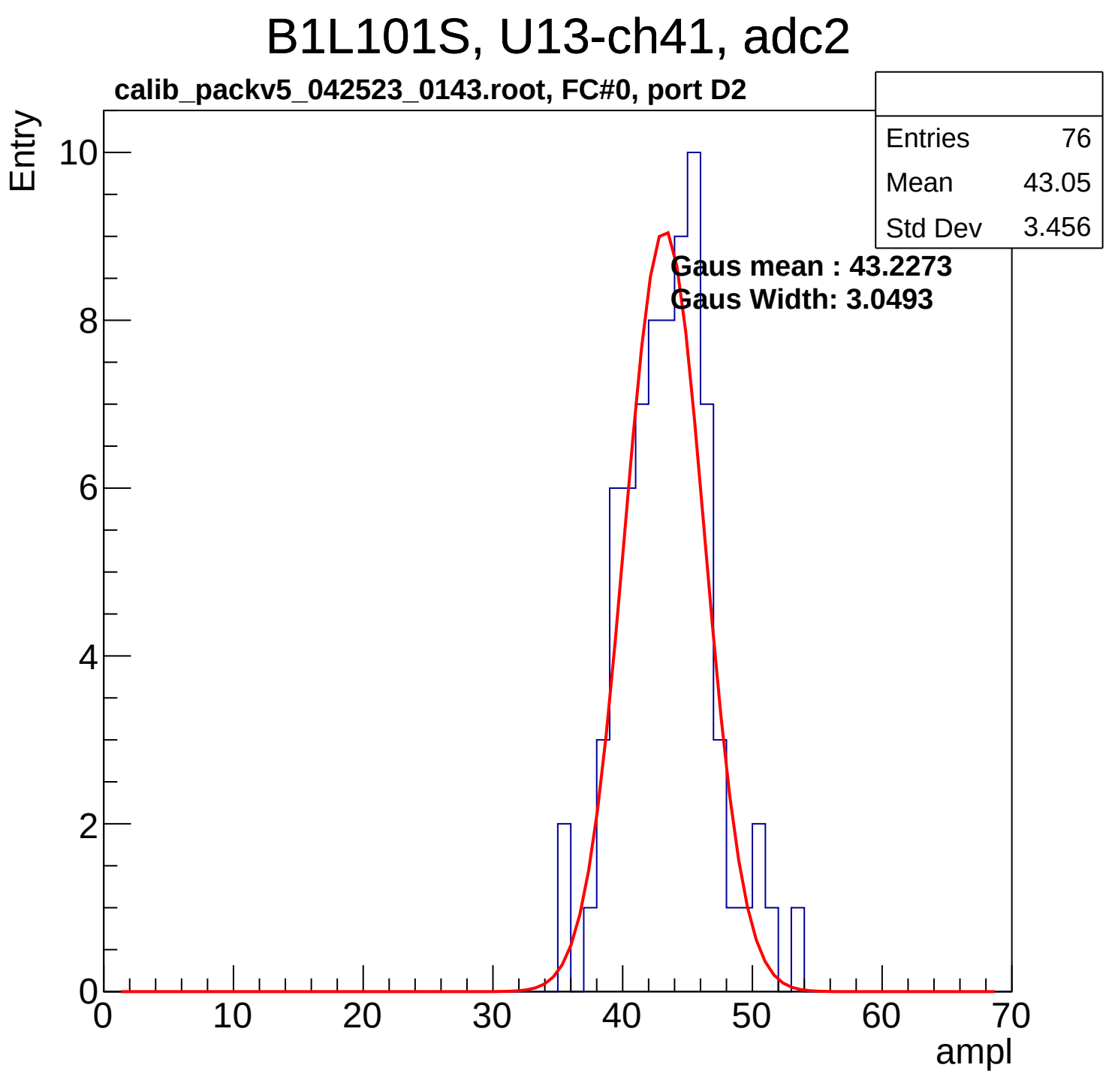
60
70


B1L101S, U13-ch41, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 36.03
Std Dev 3.074
Gaus mean : 36.5732
Gaus Width: 3.7278
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch41, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 43.05
Std Dev 3.456
Gaus mean : 43.2273
Gaus Width: 3.0493
Entry
ampl
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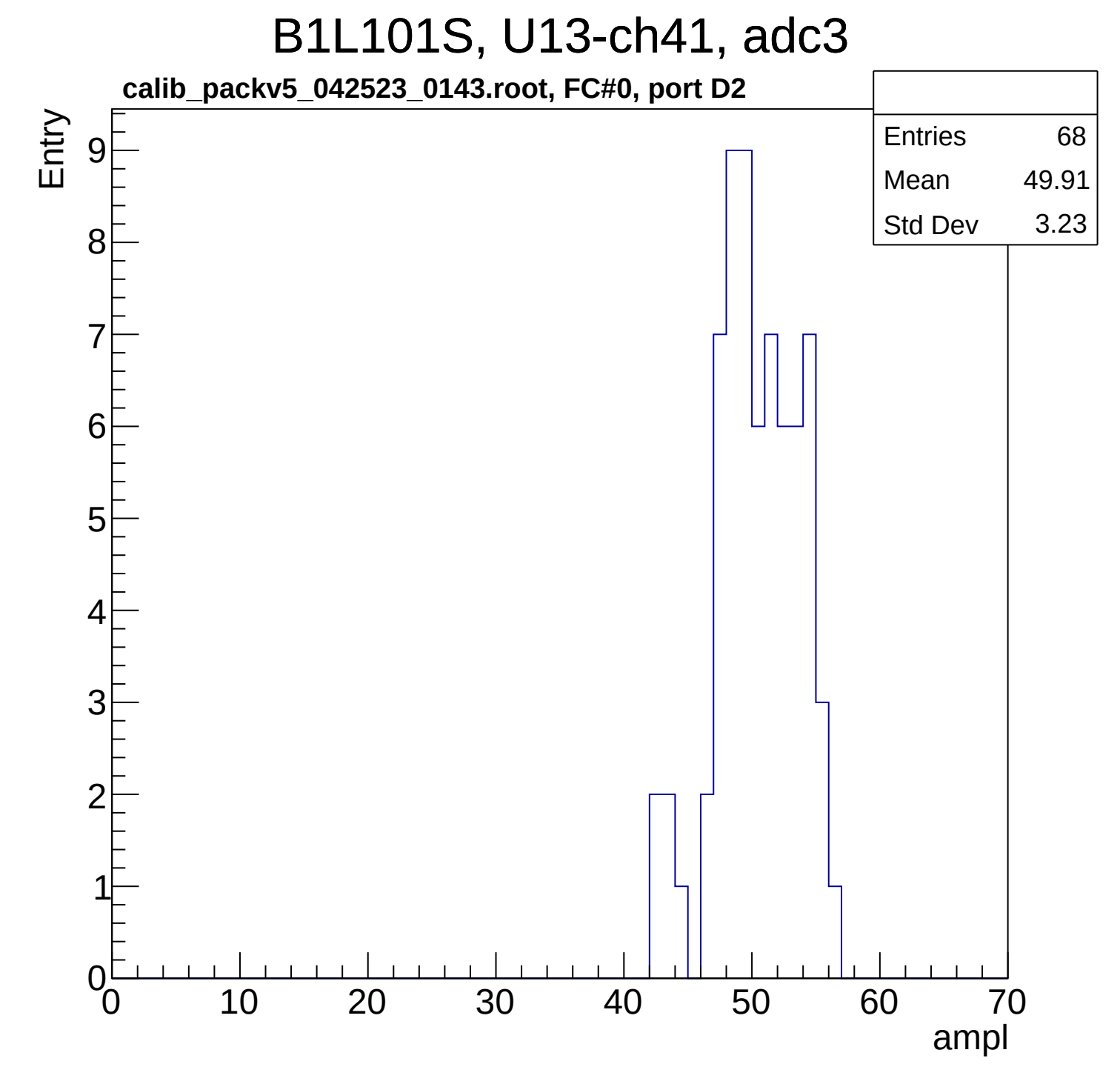
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch41, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 49.91
Std Dev 3.23
Entry
ampl
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2
3
4
5
6
7
8
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0
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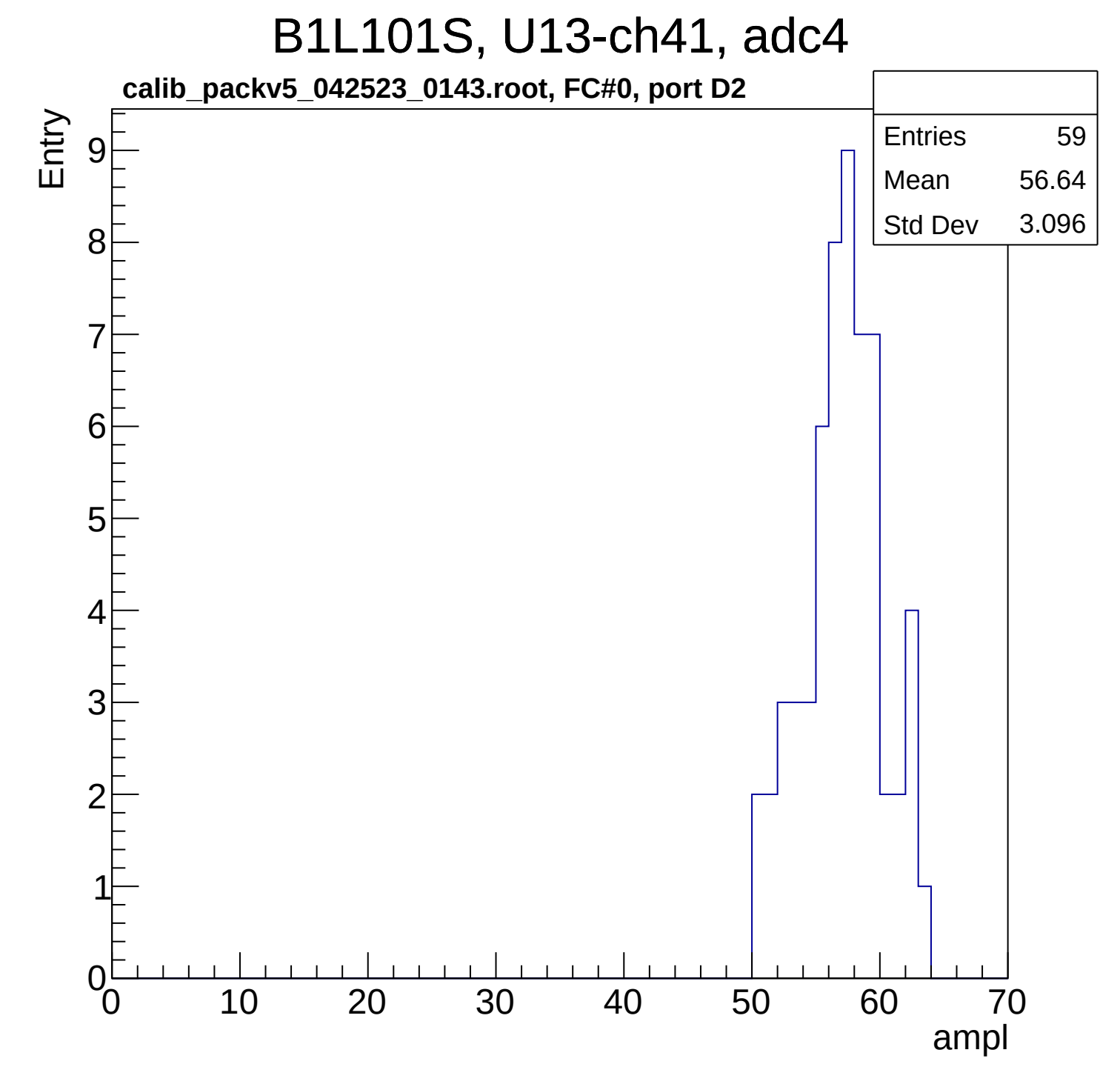
B1L101S, U13-ch41, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 56.64
Std Dev 3.096
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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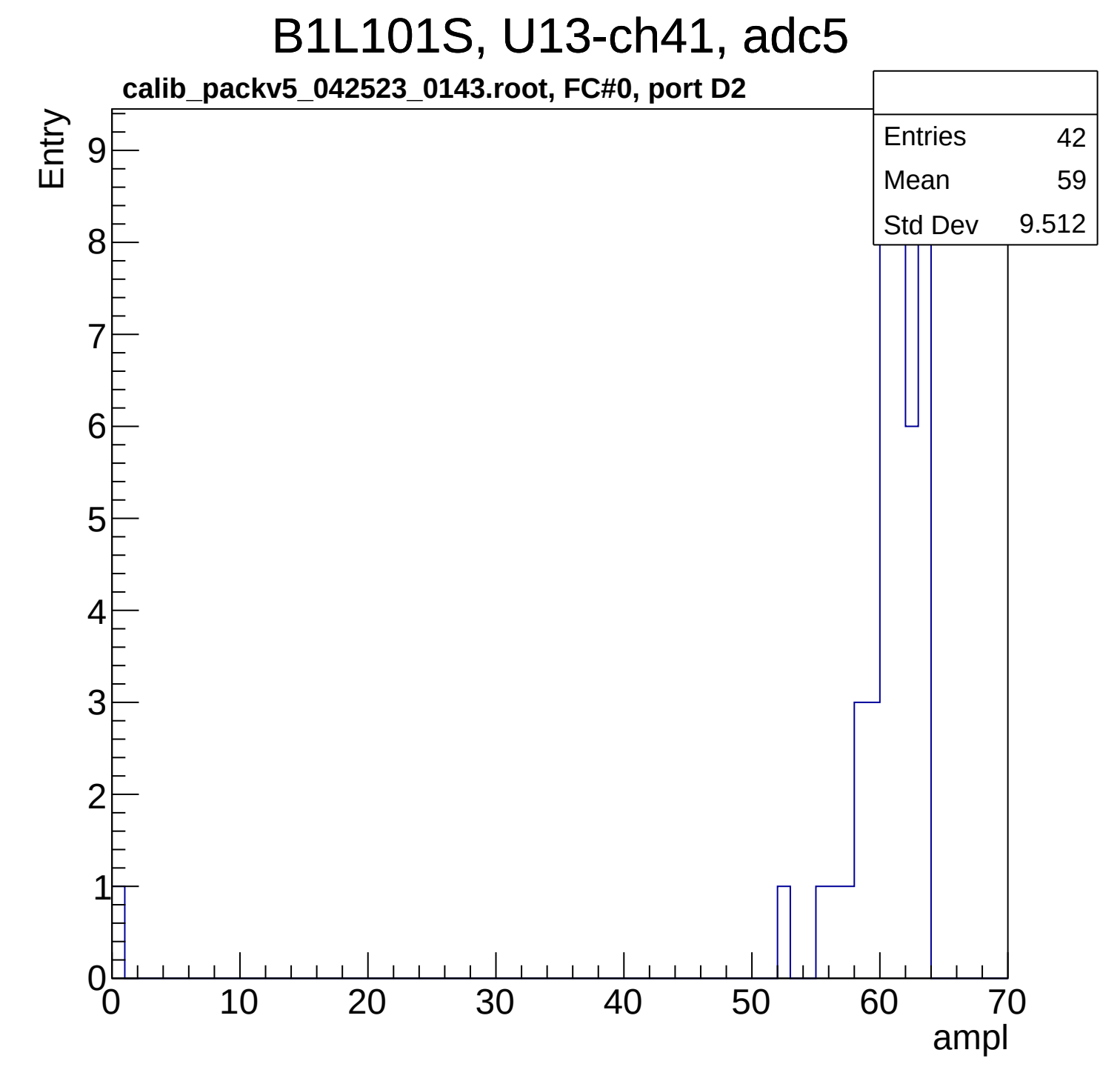
B1L101S, U13-ch41, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 59
Std Dev 9.512
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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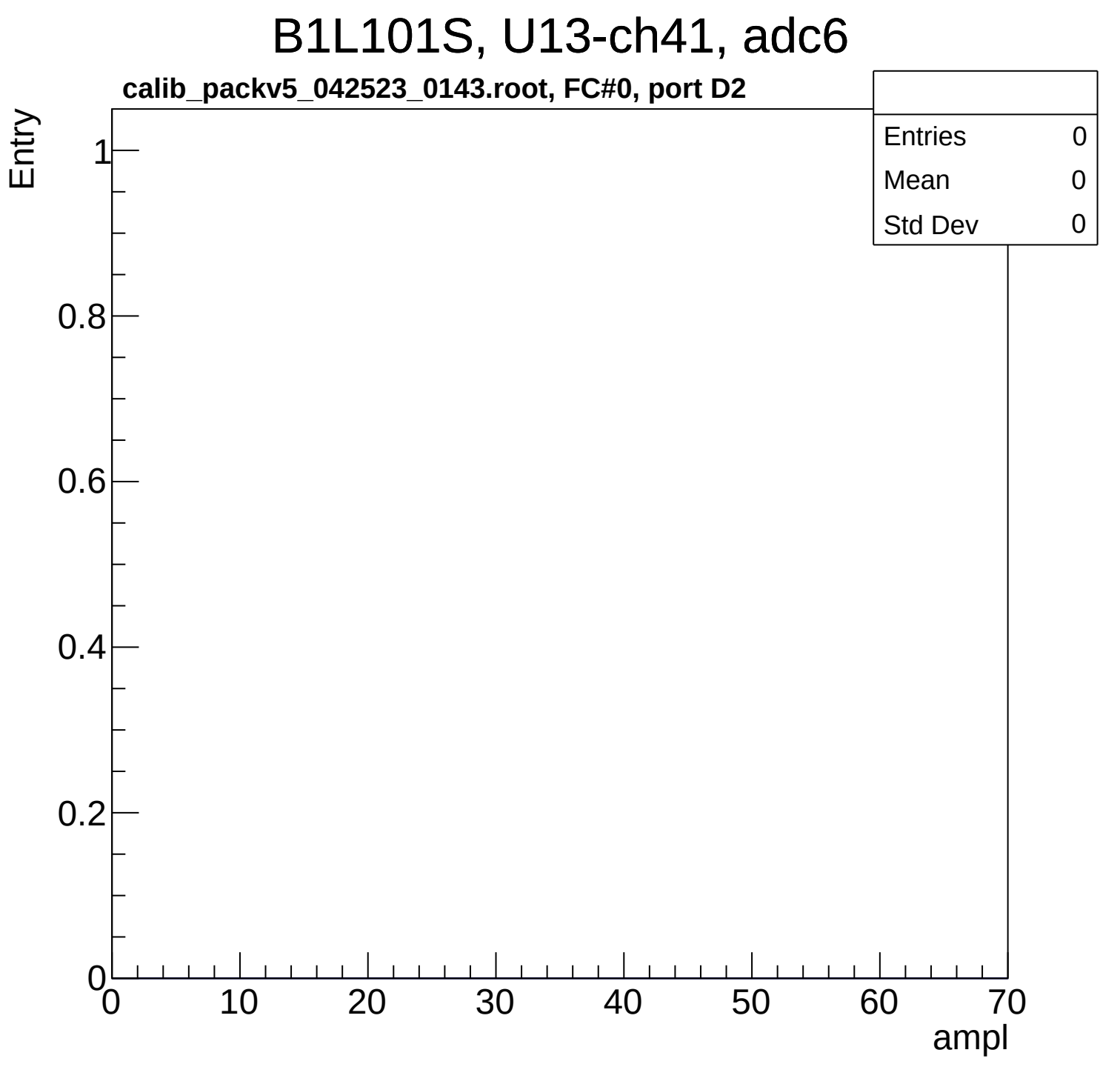
60
70


B1L101S, U13-ch41, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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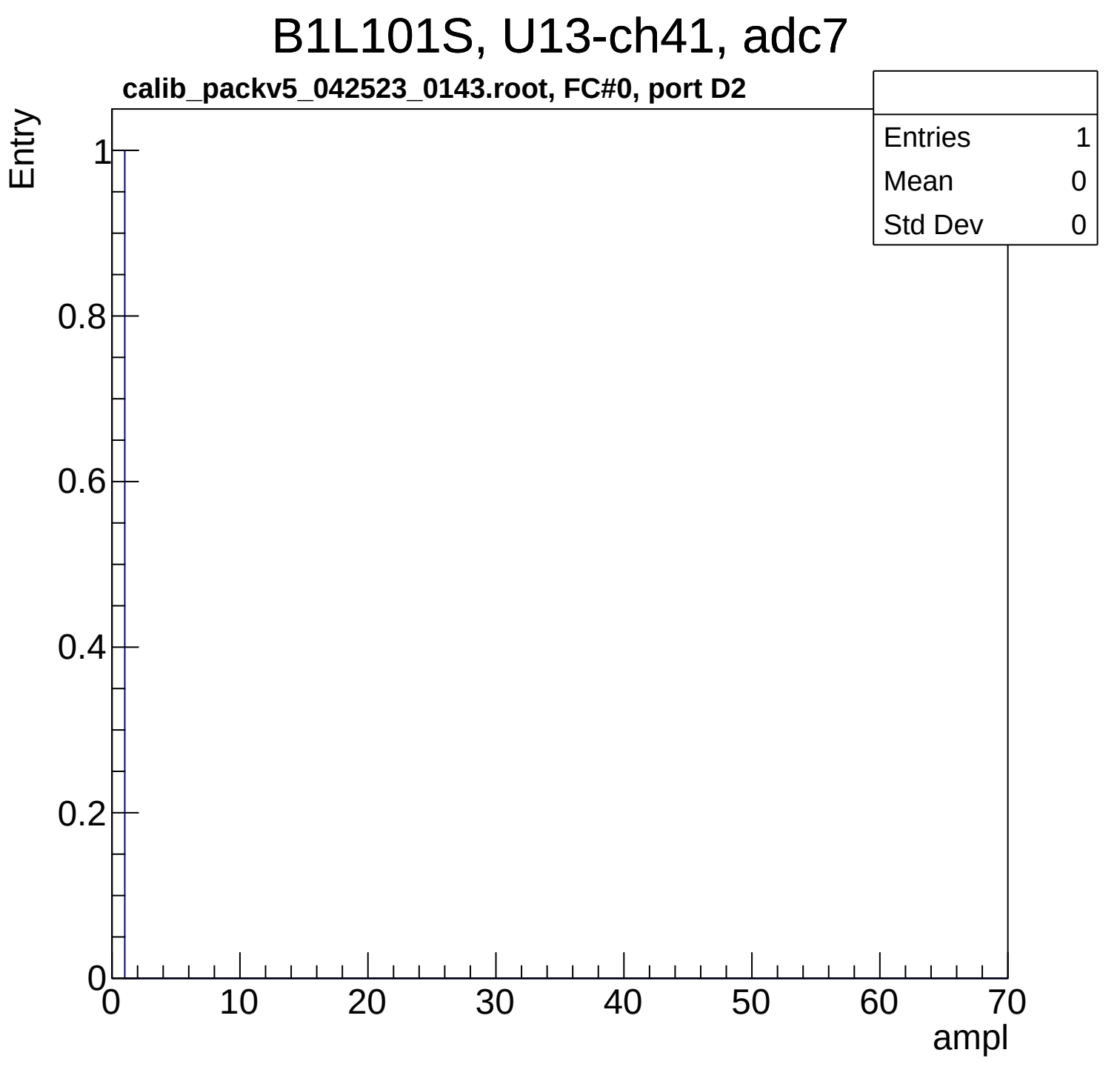
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch41, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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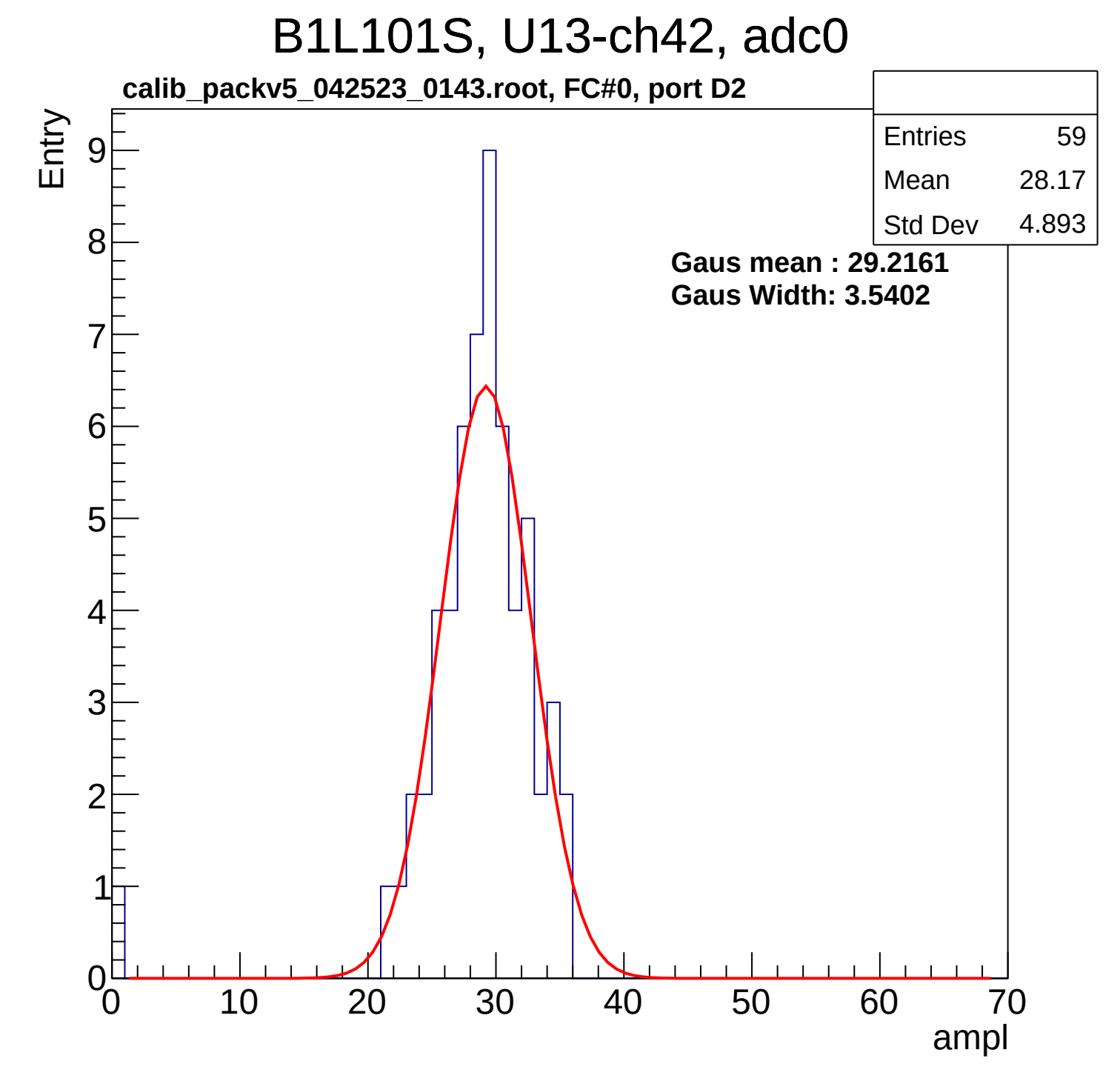
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch42, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 28.17
Std Dev 4.893
Gaus mean : 29.2161
Gaus Width: 3.5402
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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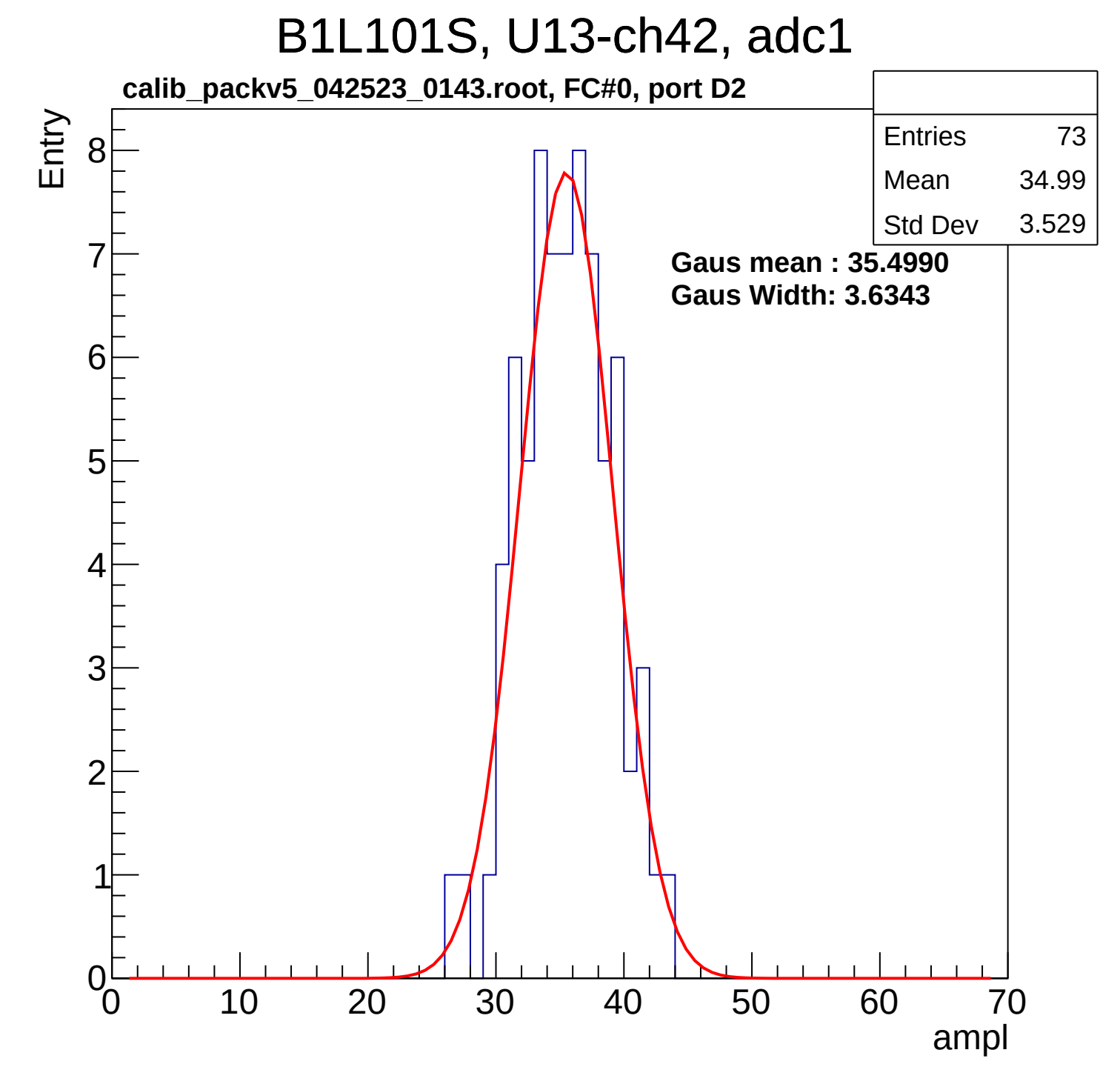
60
70


B1L101S, U13-ch42, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 34.99
Std Dev 3.529
Gaus mean : 35.4990
Gaus Width: 3.6343
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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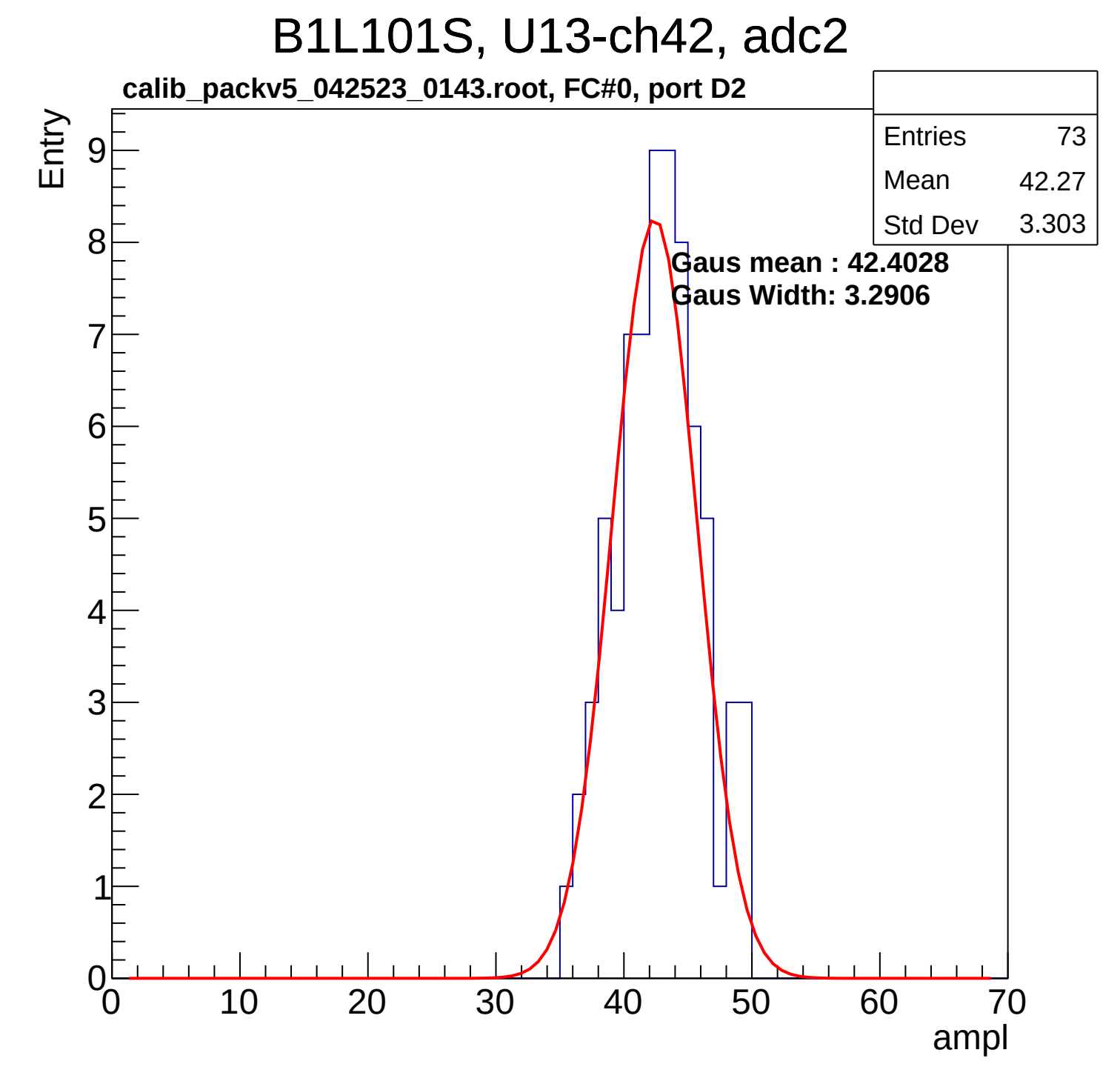
B1L101S, U13-ch42, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 42.27
Std Dev 3.303
Gaus mean : 42.4028
Gaus Width: 3.2906
Entry
ampl
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10
20
30
40
50
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1
2
3
4
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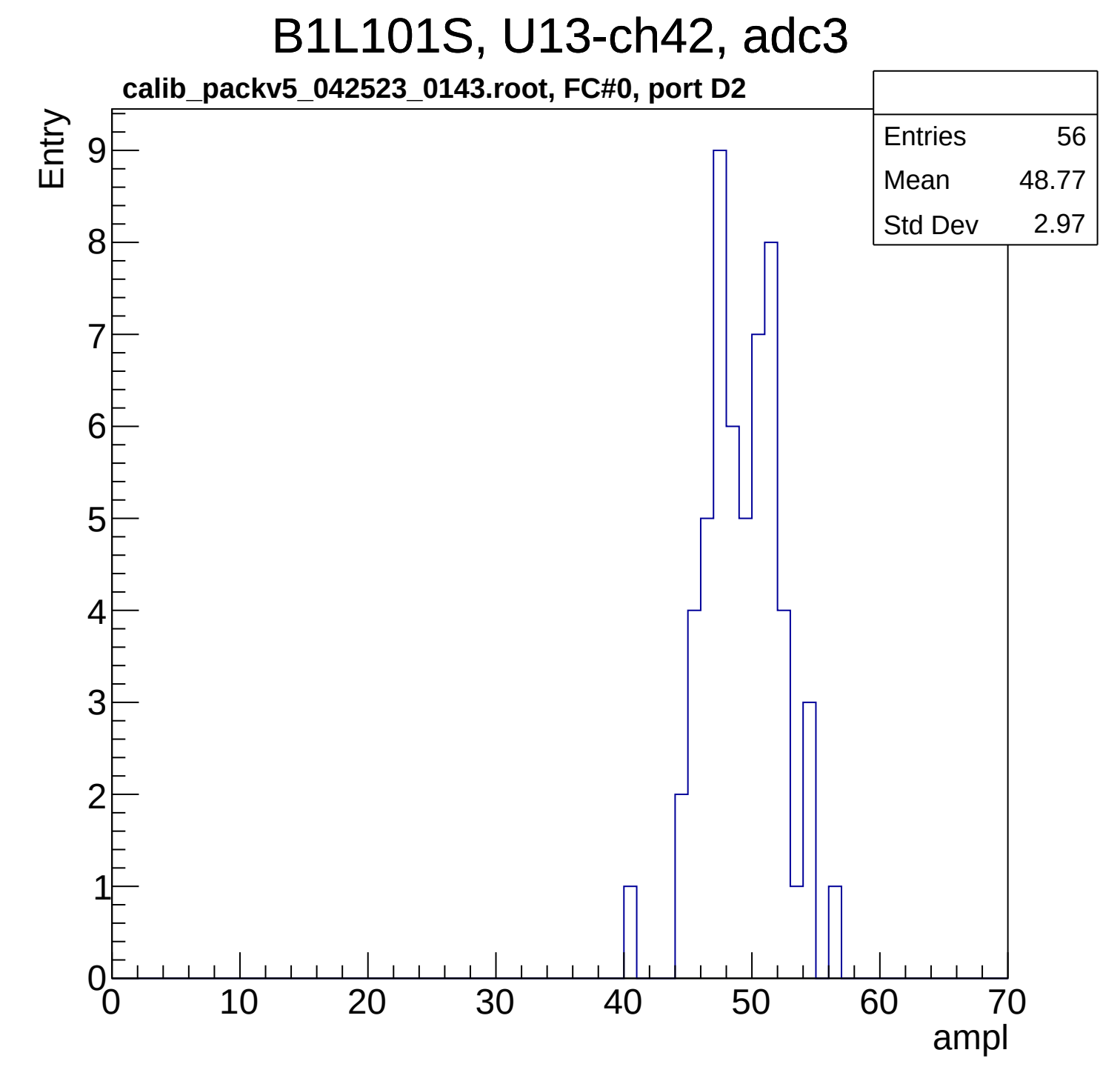
6
7
8
9


B1L101S, U13-ch42, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 48.77
Std Dev 2.97
Entry
ampl
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3
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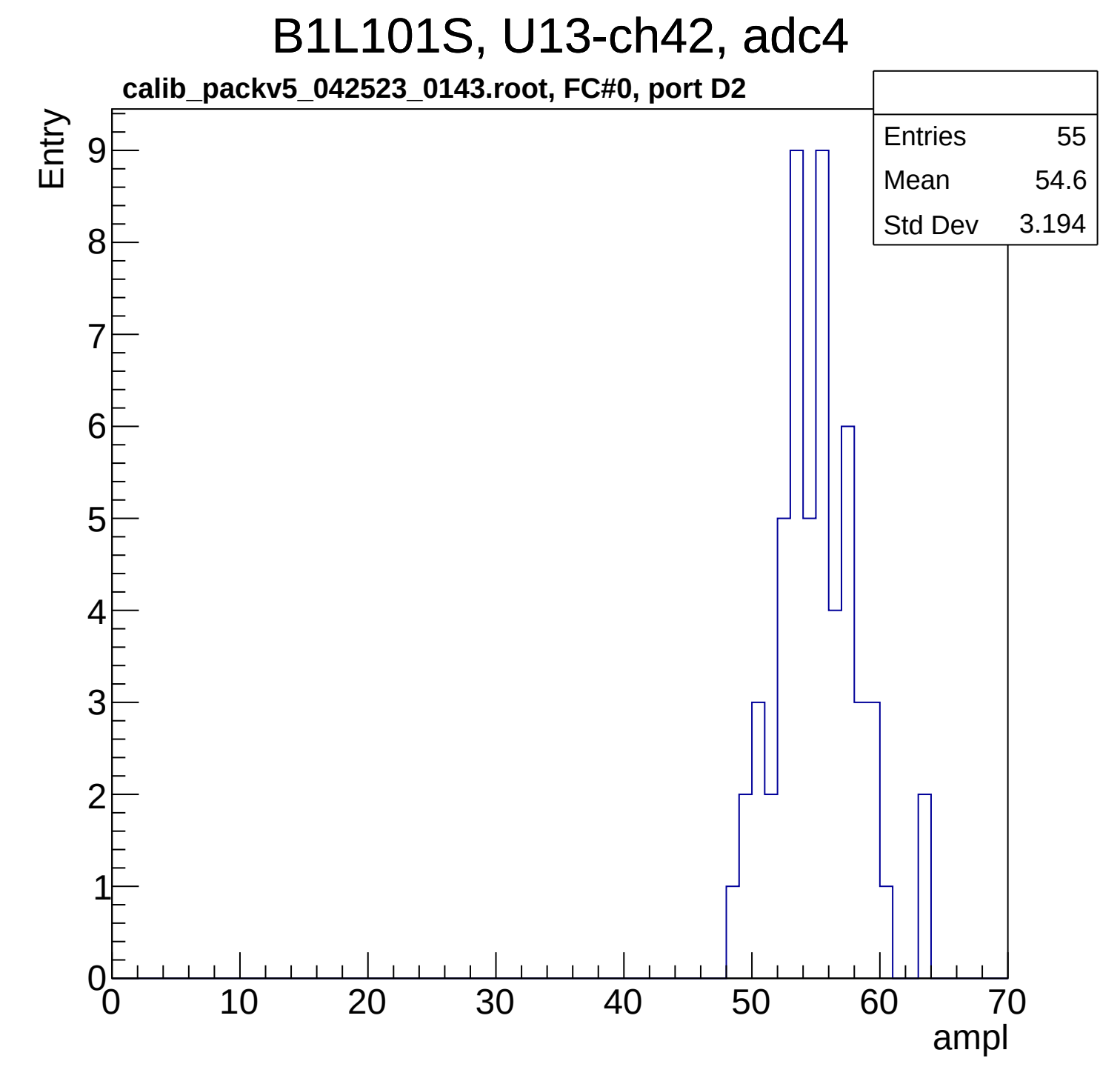
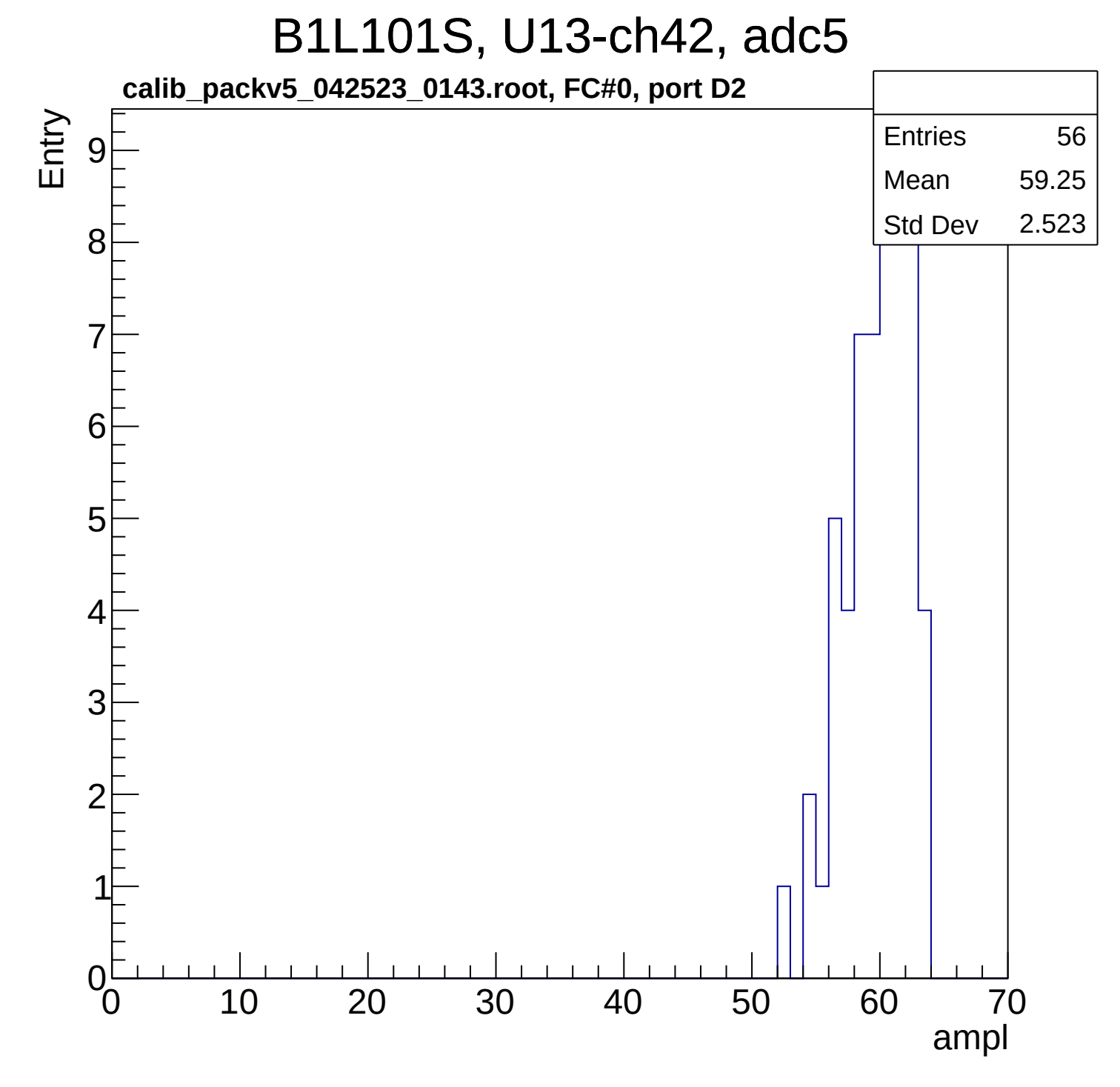

B1L101S, U13-ch42, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 54.6
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch42, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 59.25
Std Dev 2.523
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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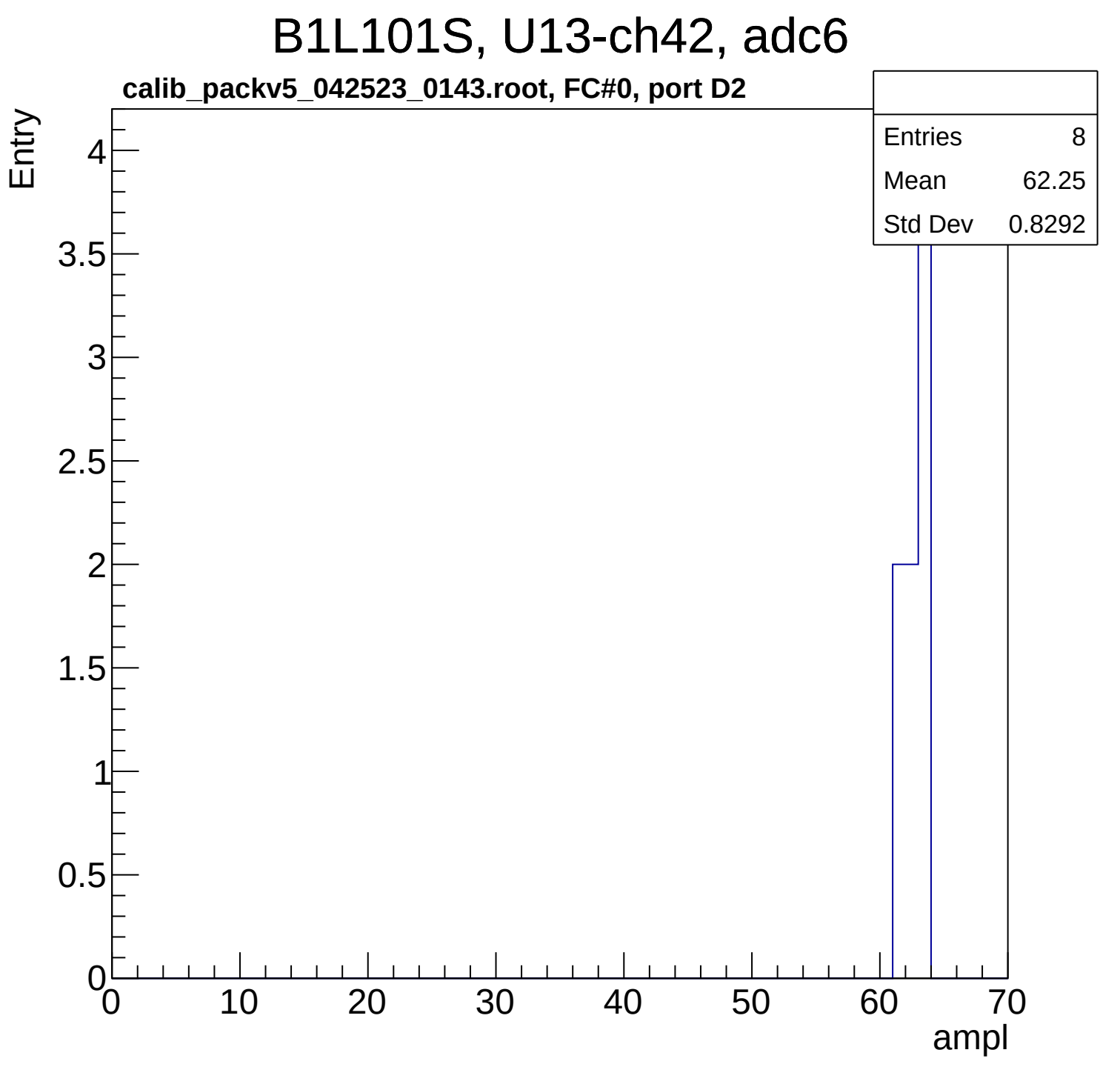
B1L101S, U13-ch42, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 8
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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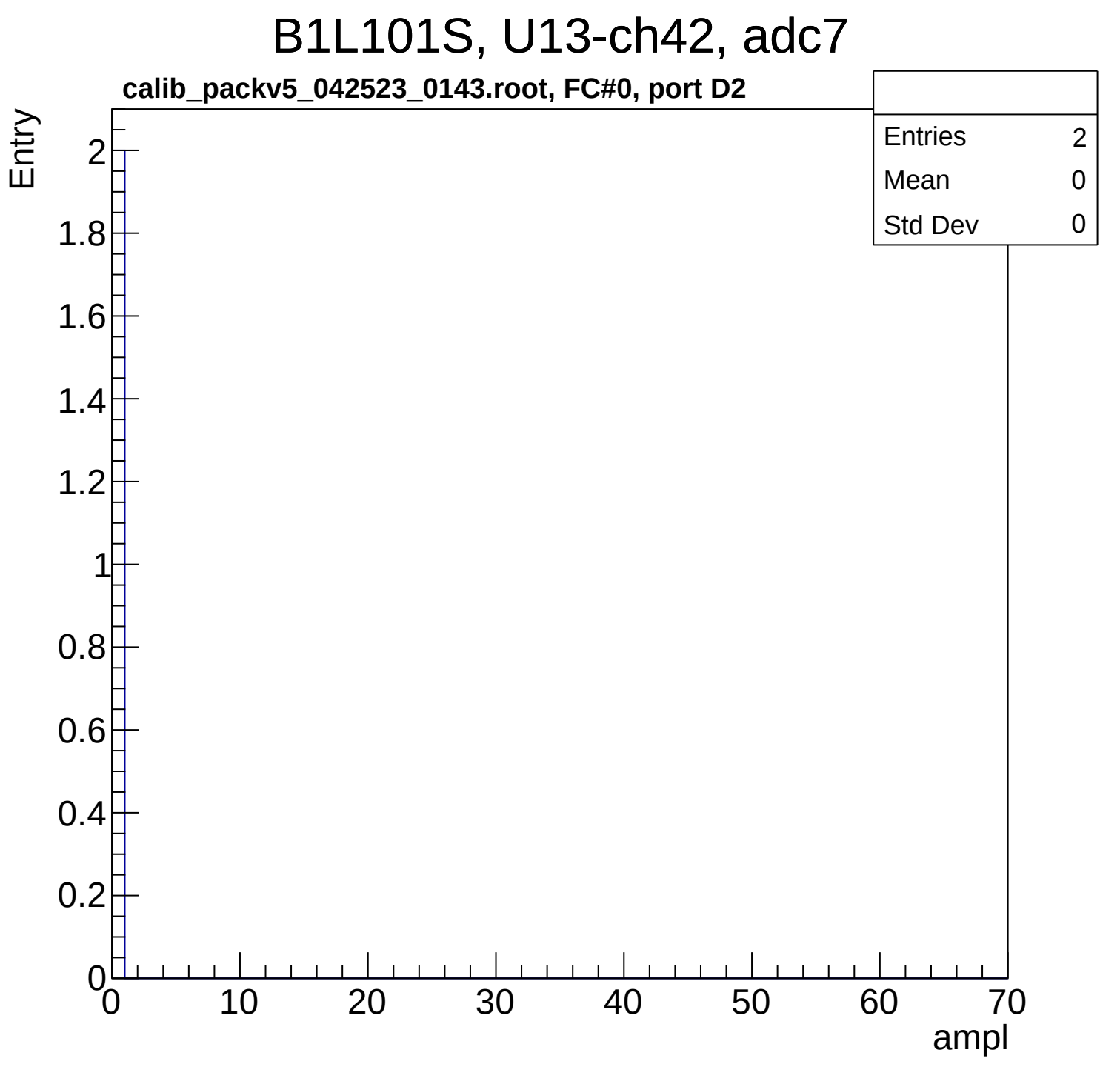
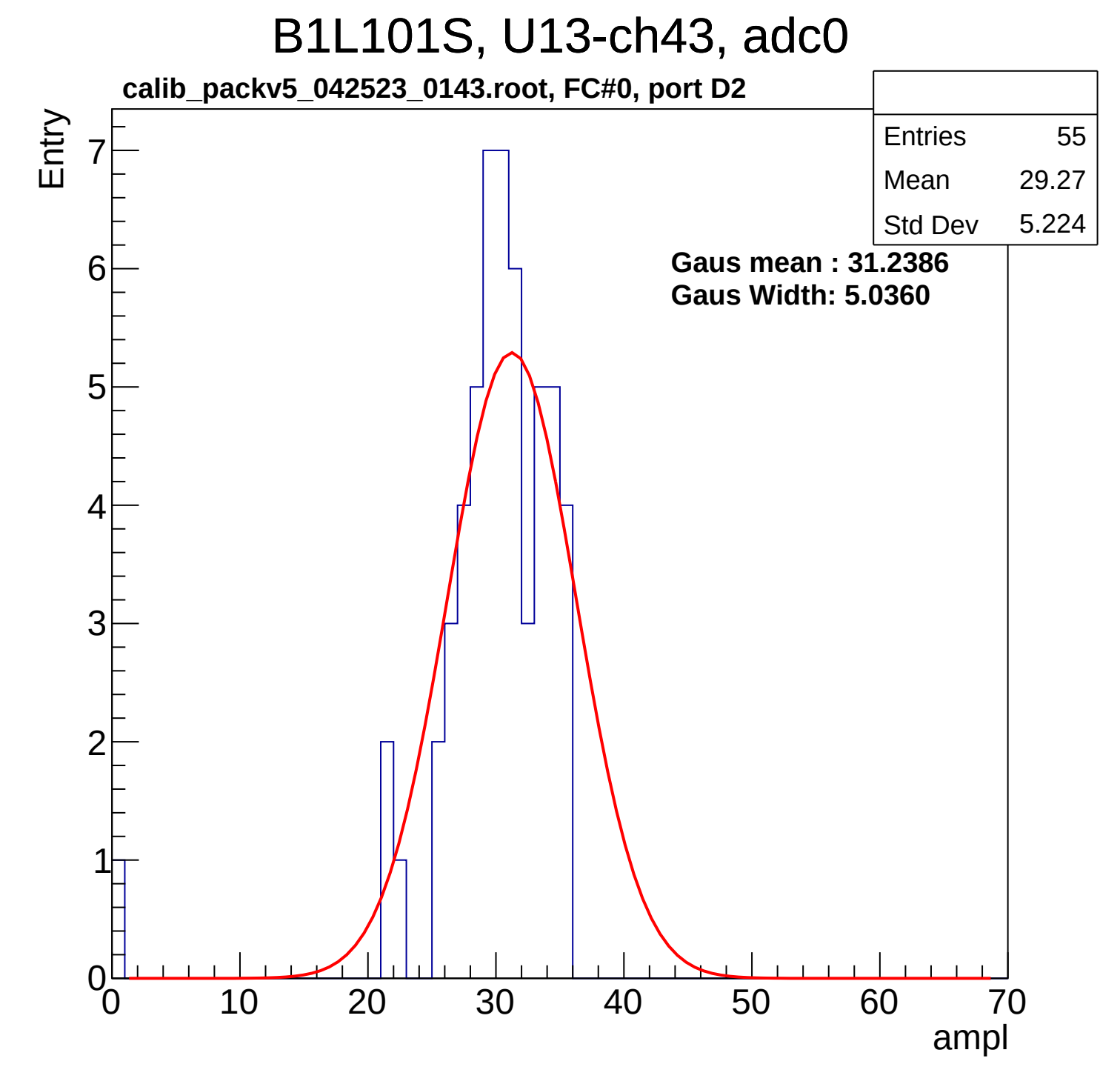
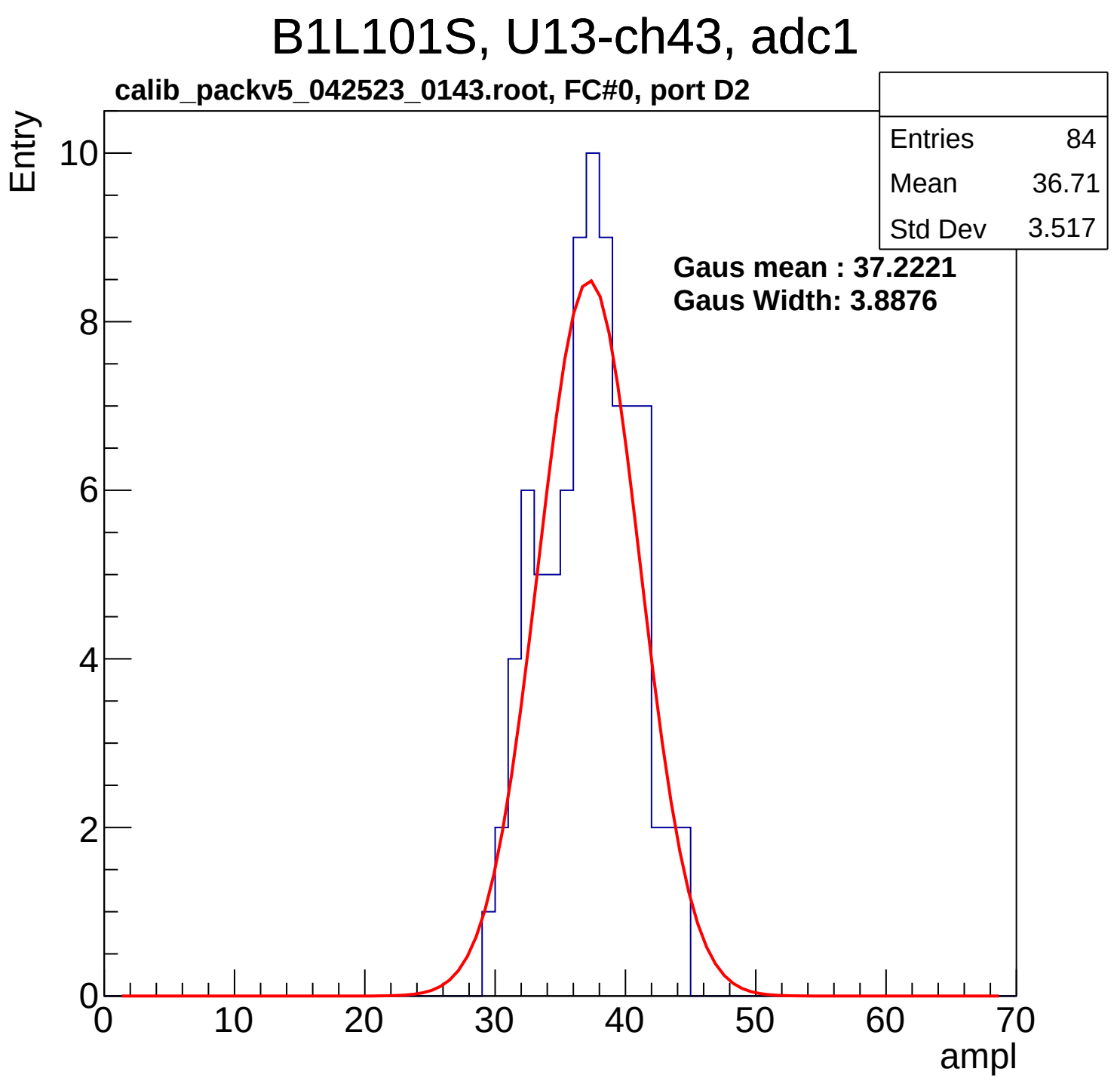

B1L101S, U13-ch42, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch43, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 29.27
Std Dev 5.224
Gaus mean : 31.2386
Gaus Width: 5.0360
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch43, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 84
Mean 36.71
Std Dev 3.517
Gaus mean : 37.2221
Gaus Width: 3.8876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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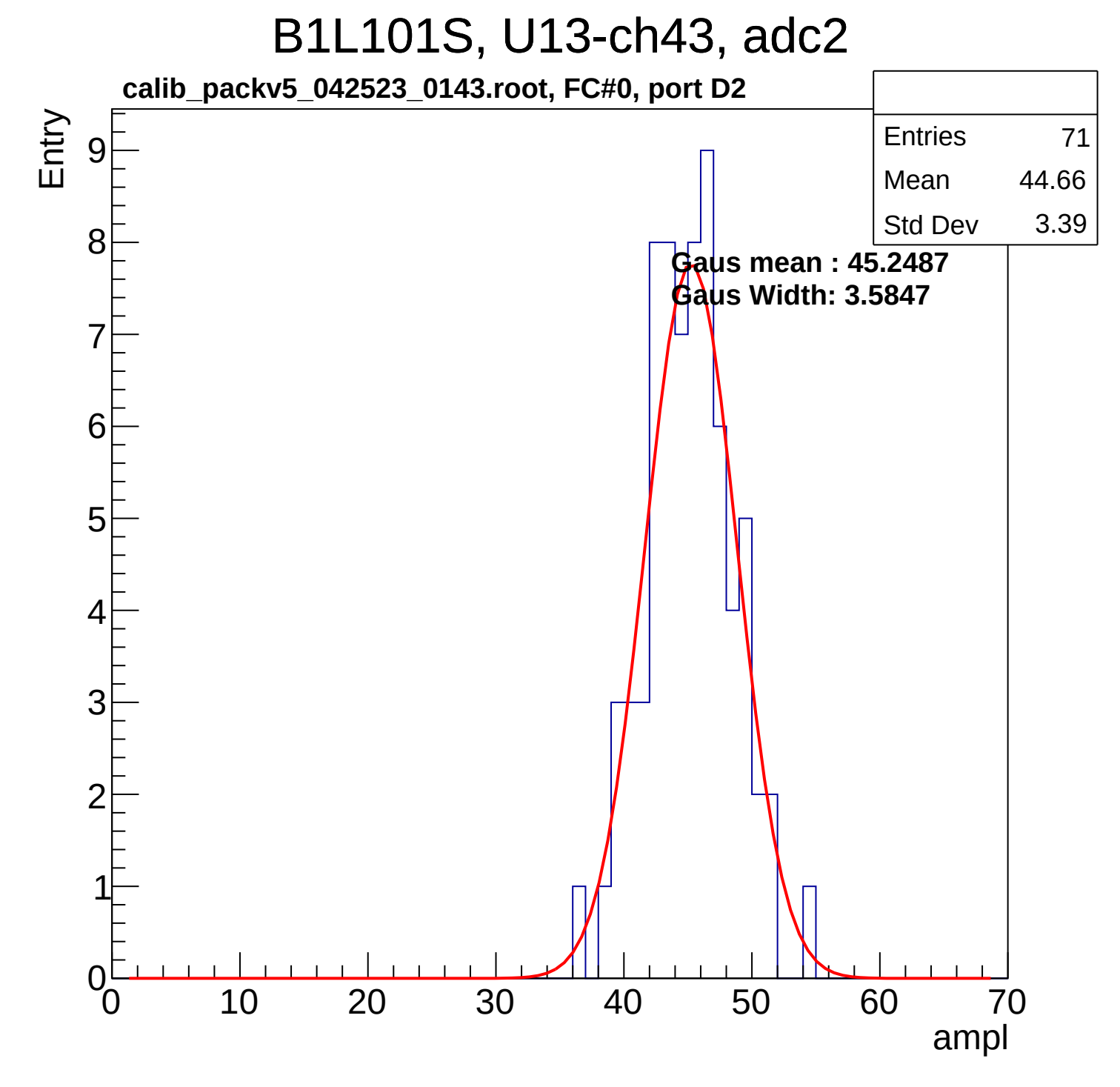
B1L101S, U13-ch43, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 44.66
Std Dev 3.39
Gaus mean : 45.2487
Gaus Width: 3.5847
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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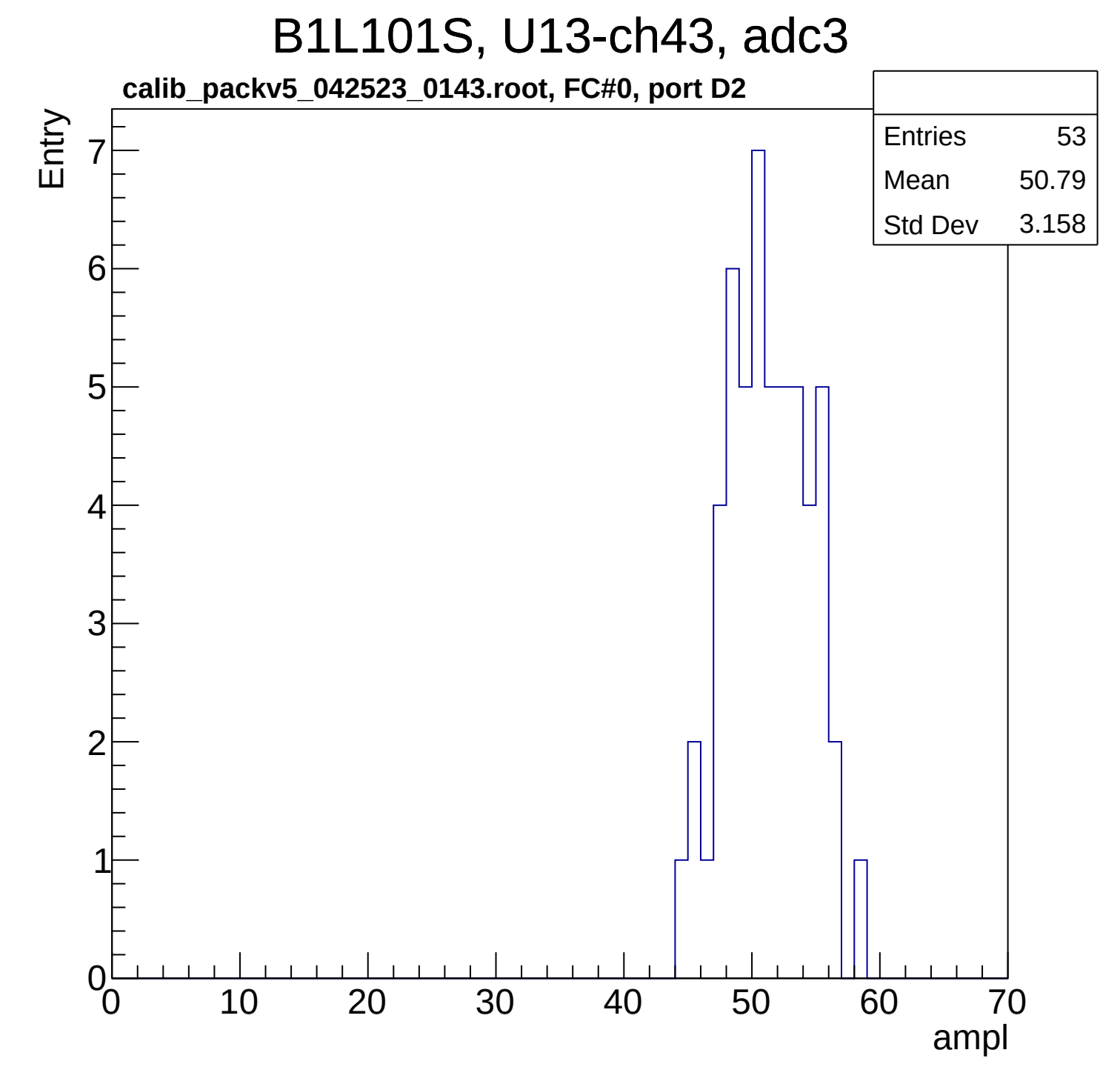
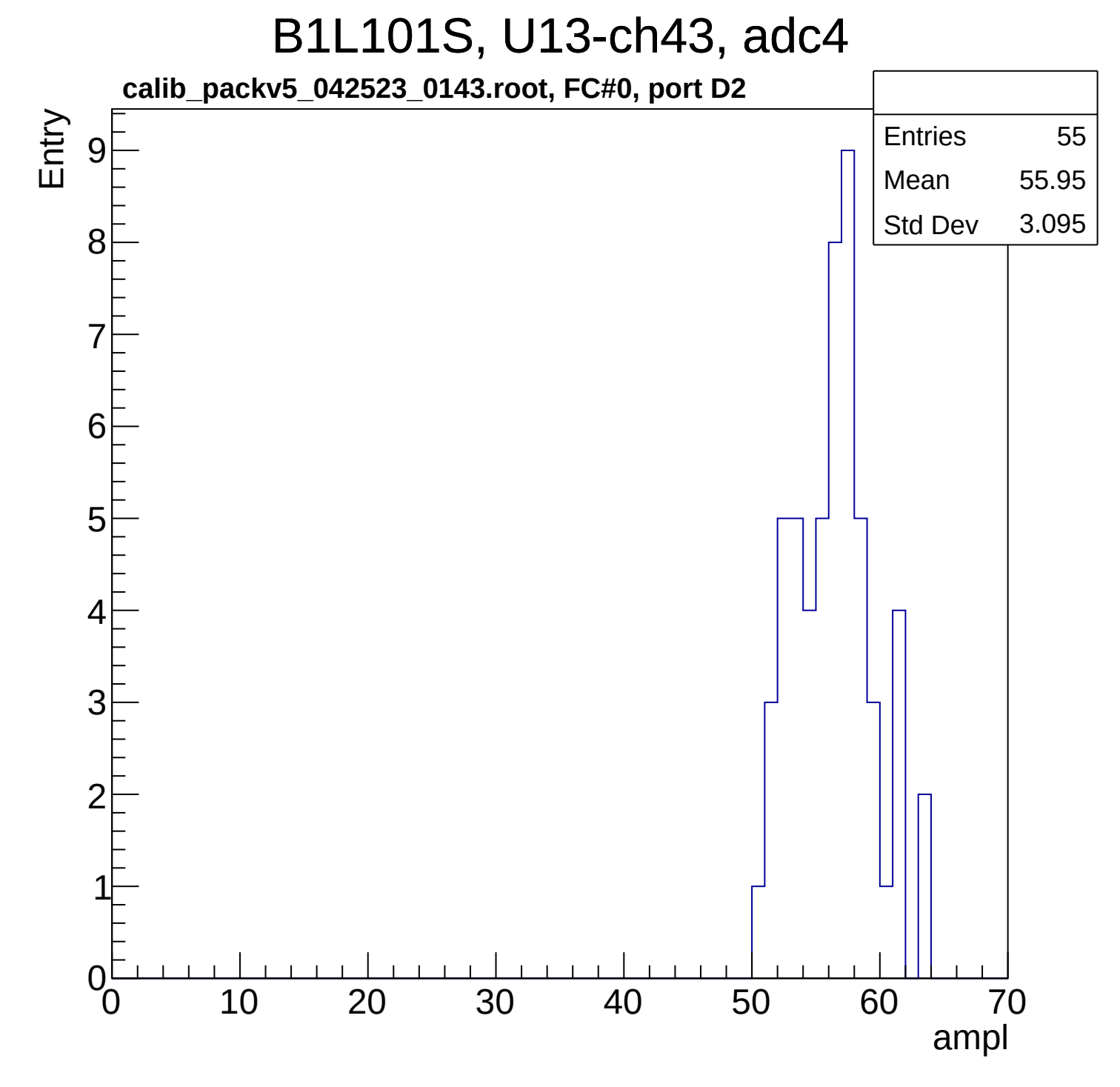
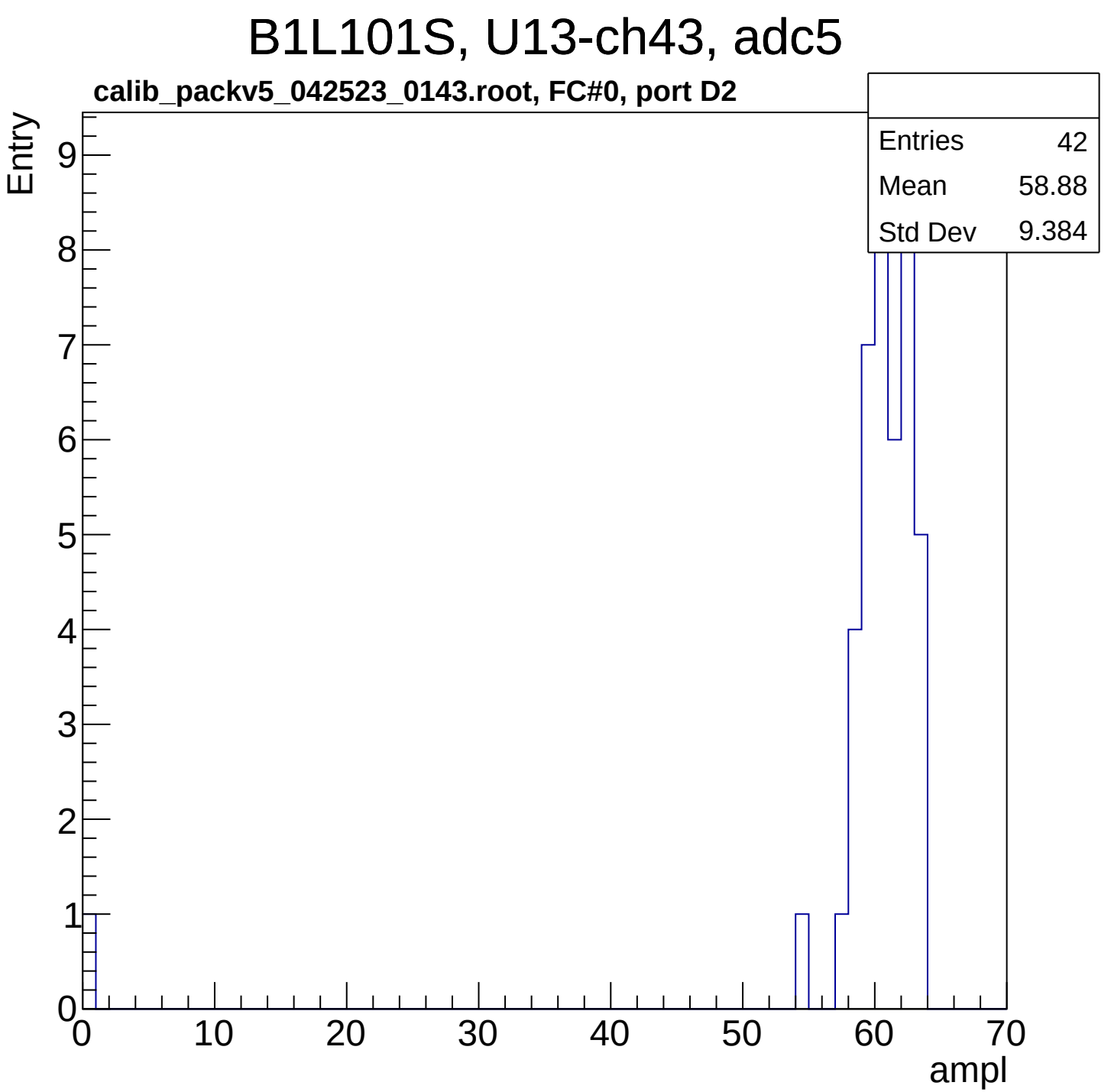
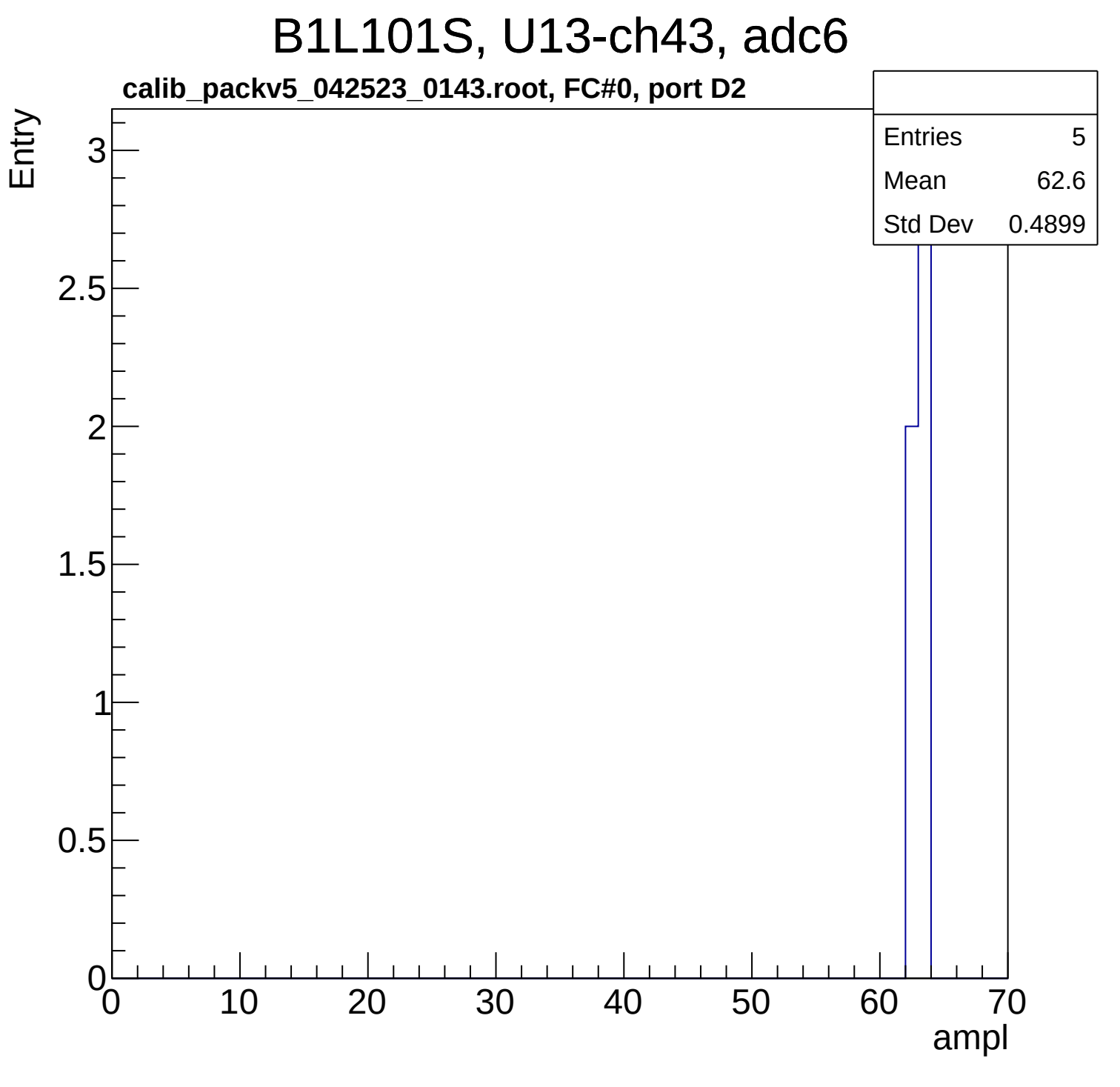

B1L101S, U13-ch43, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 50.79
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch43, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 55.95
Std Dev 3.095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch43, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 58.88
Std Dev 9.384
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch43, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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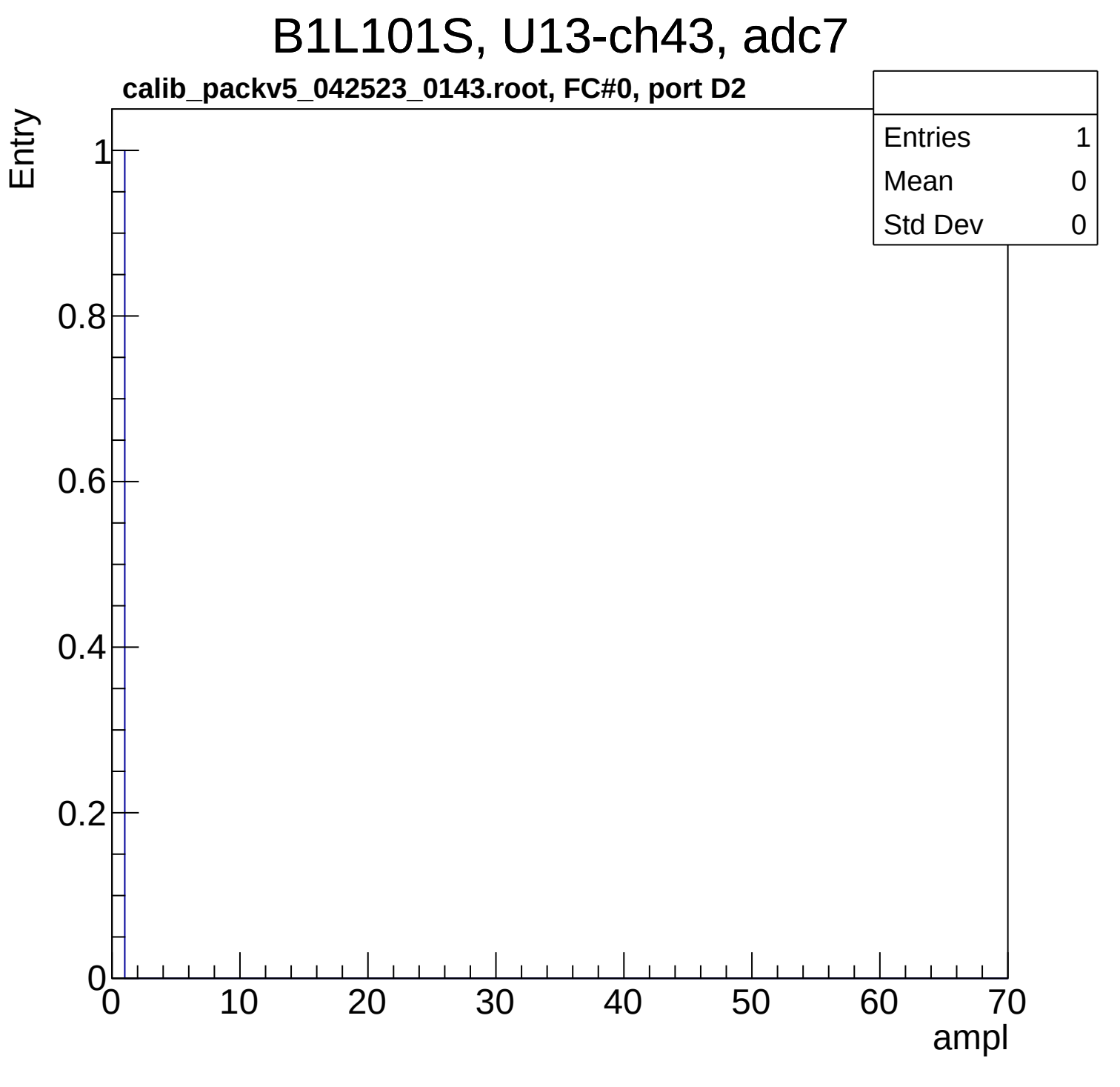
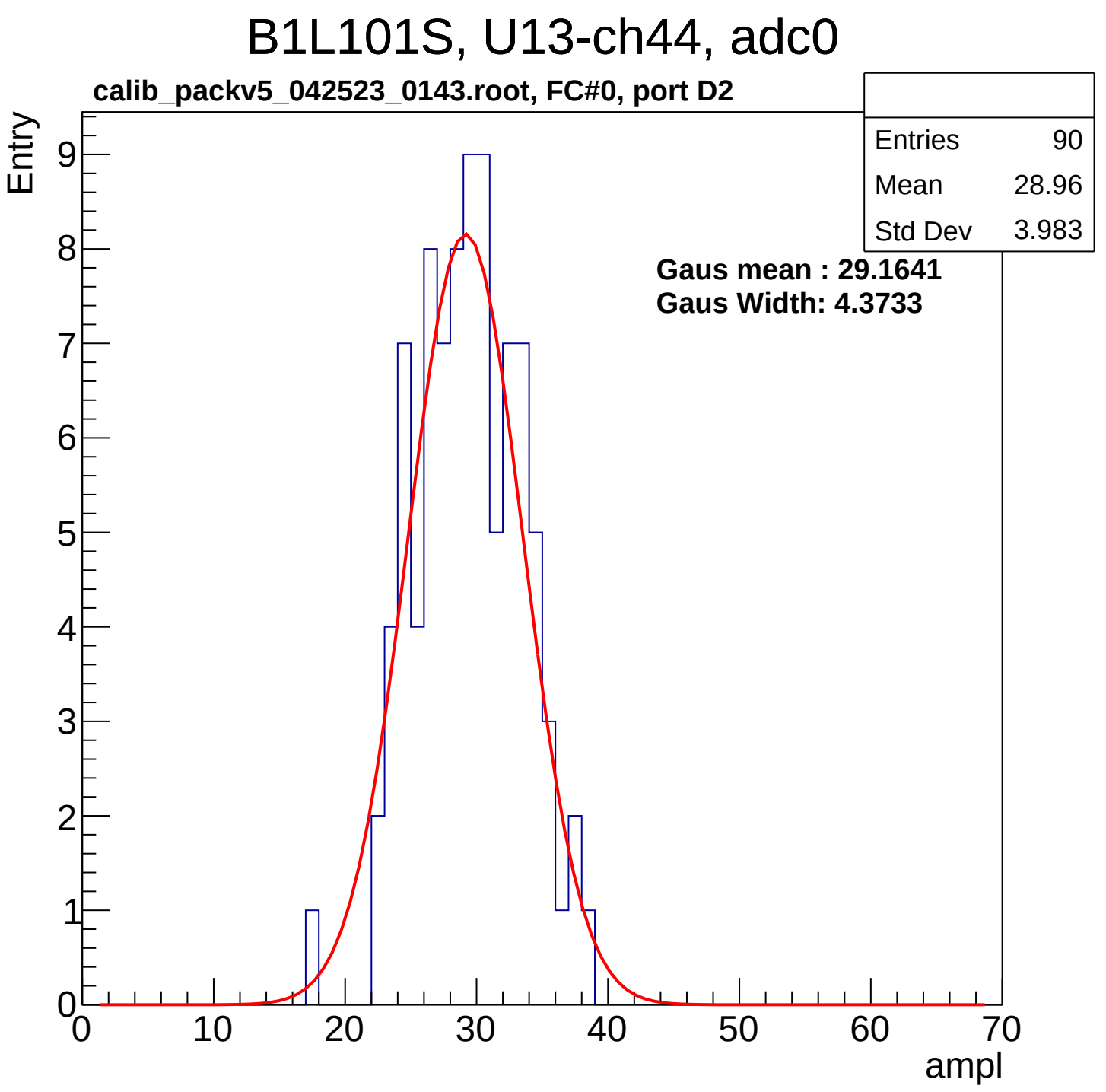
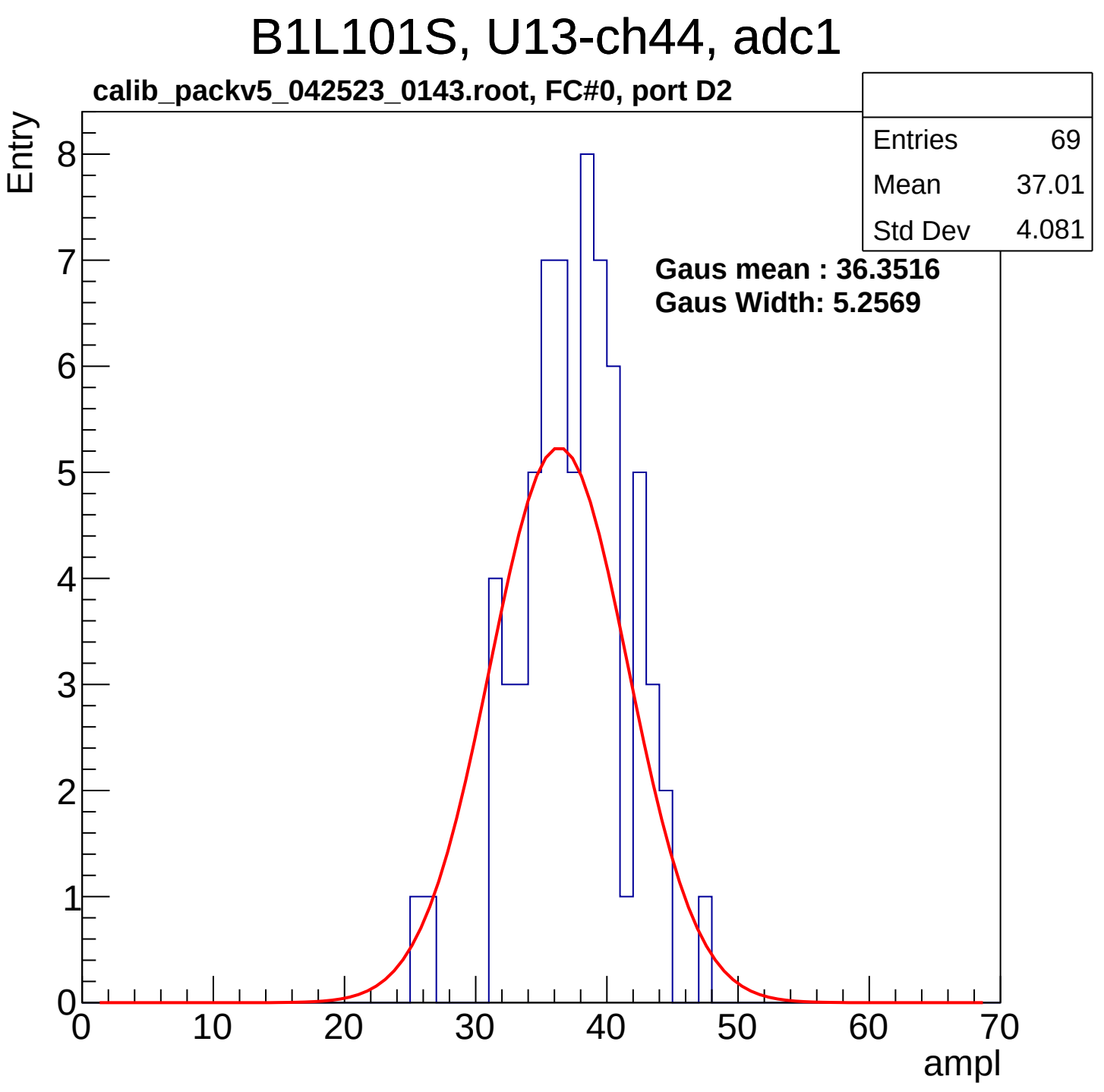
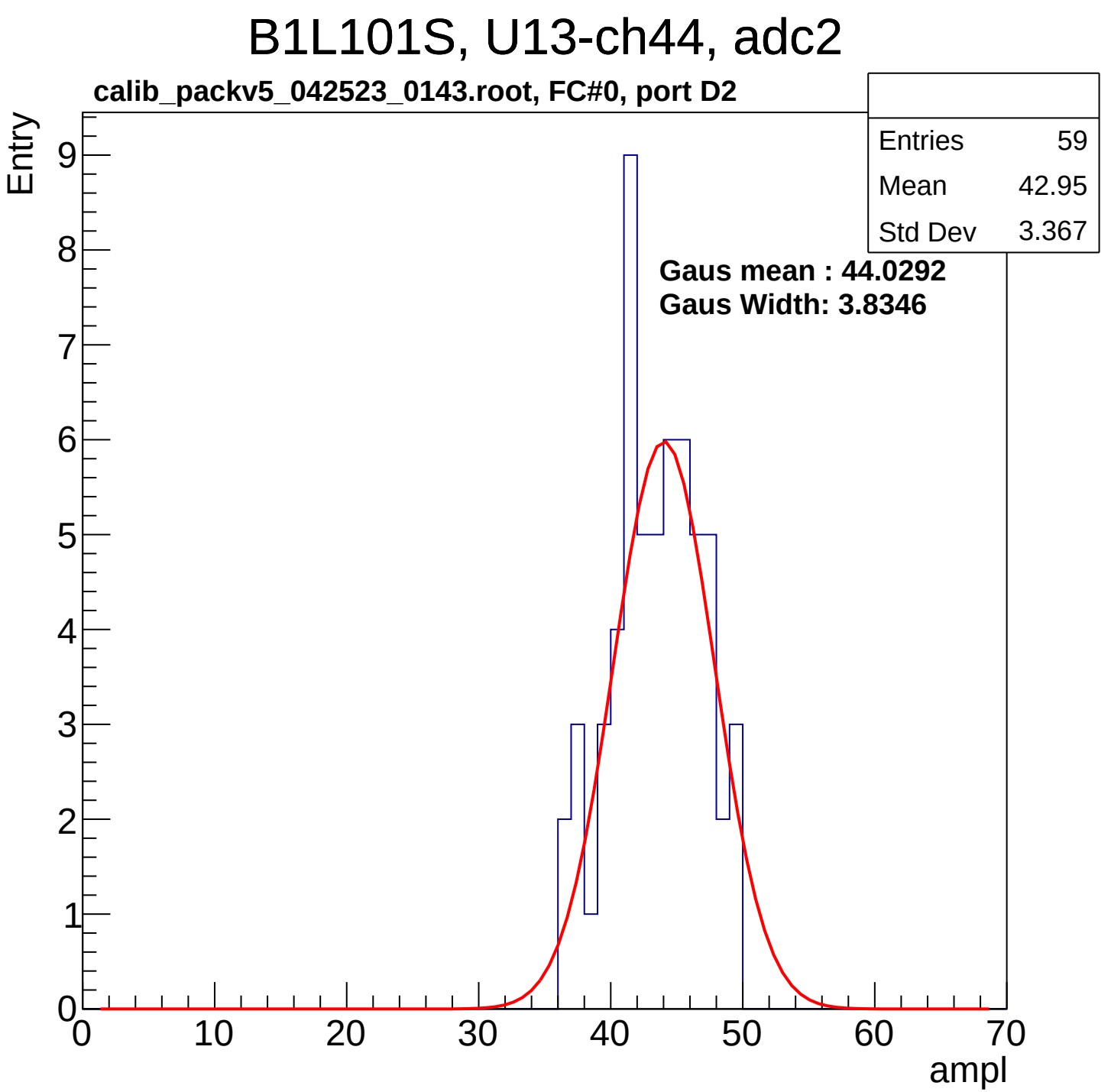
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch43, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch44, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 90
Mean 28.96
Std Dev 3.983
Gaus mean : 29.1641
Gaus Width: 4.3733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch44, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 37.01
Std Dev 4.081
Gaus mean : 36.3516
Gaus Width: 5.2569
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch44, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 42.95
Std Dev 3.367
Gaus mean : 44.0292
Gaus Width: 3.8346
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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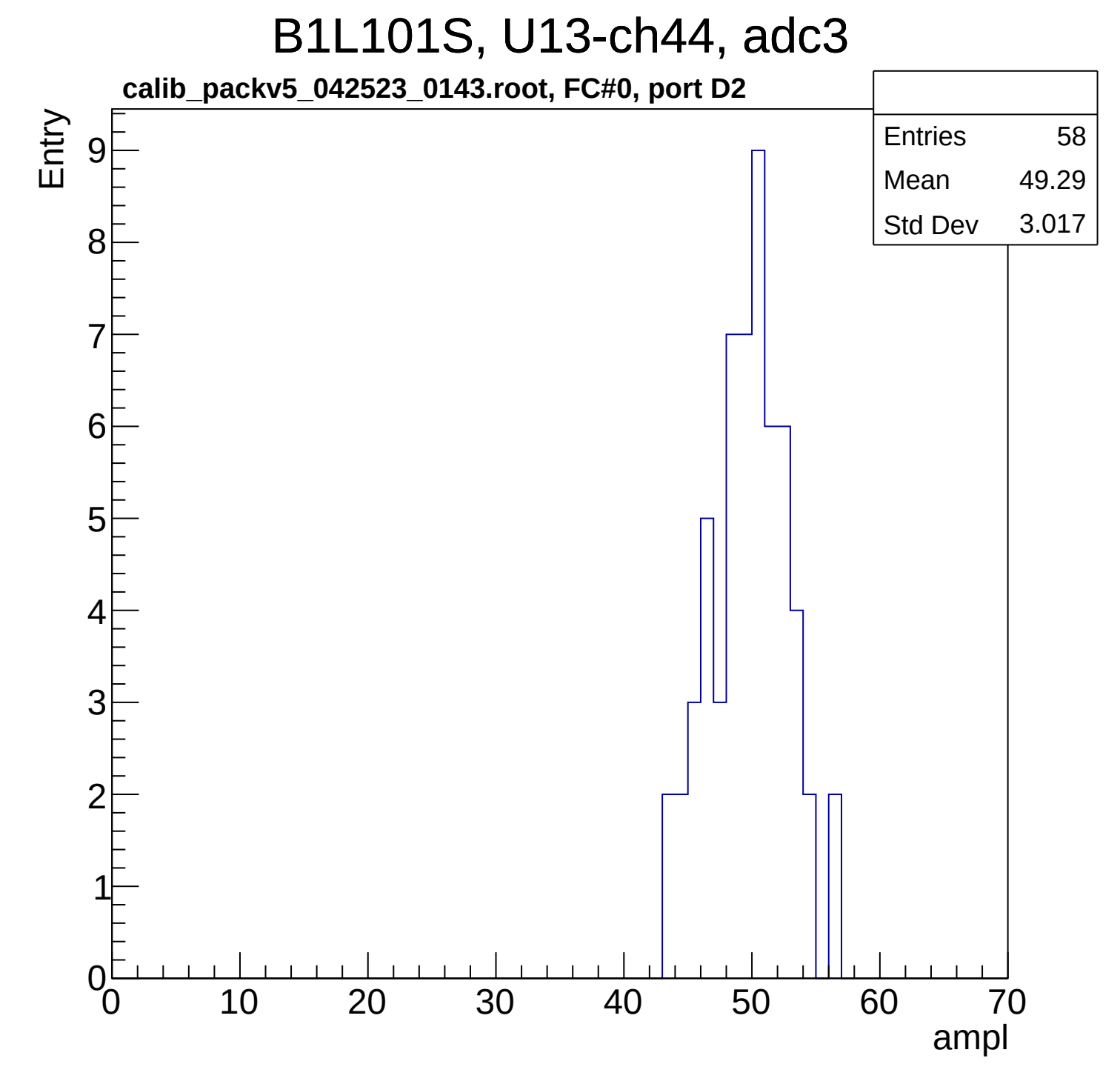
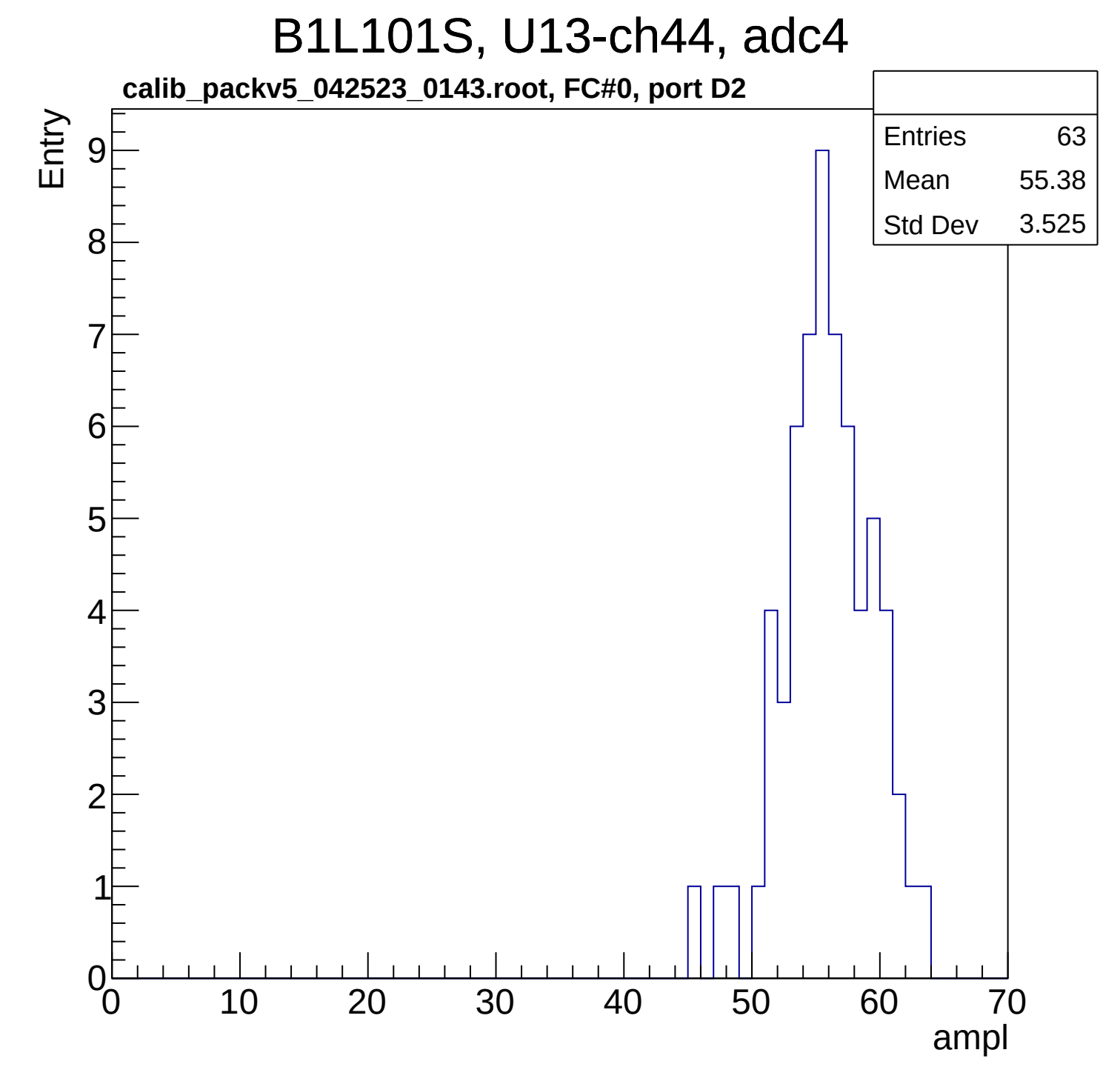

B1L101S, U13-ch44, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 49.29
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch44, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 55.38
Std Dev 3.525
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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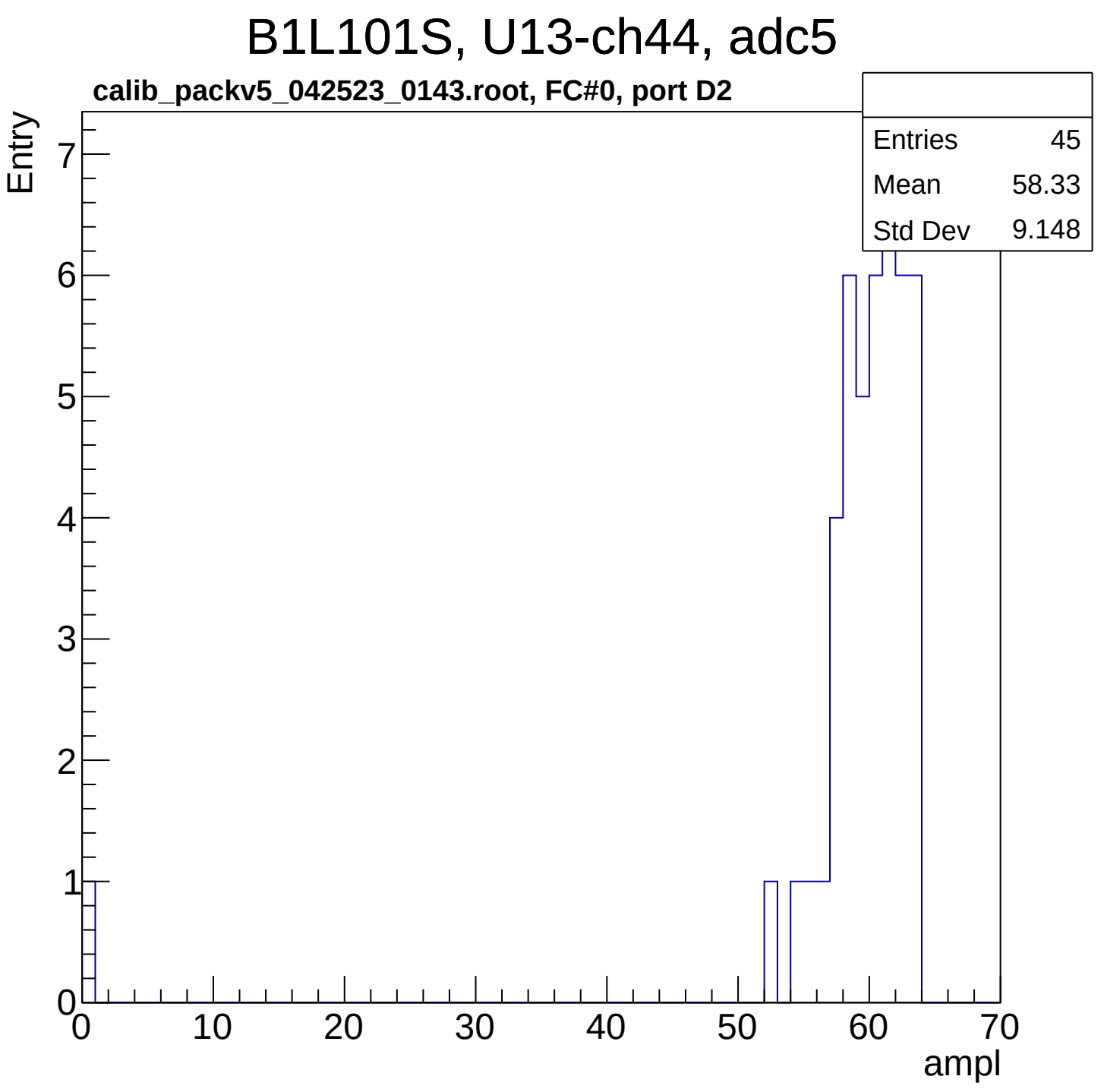
B1L101S, U13-ch44, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 58.33
Std Dev 9.148
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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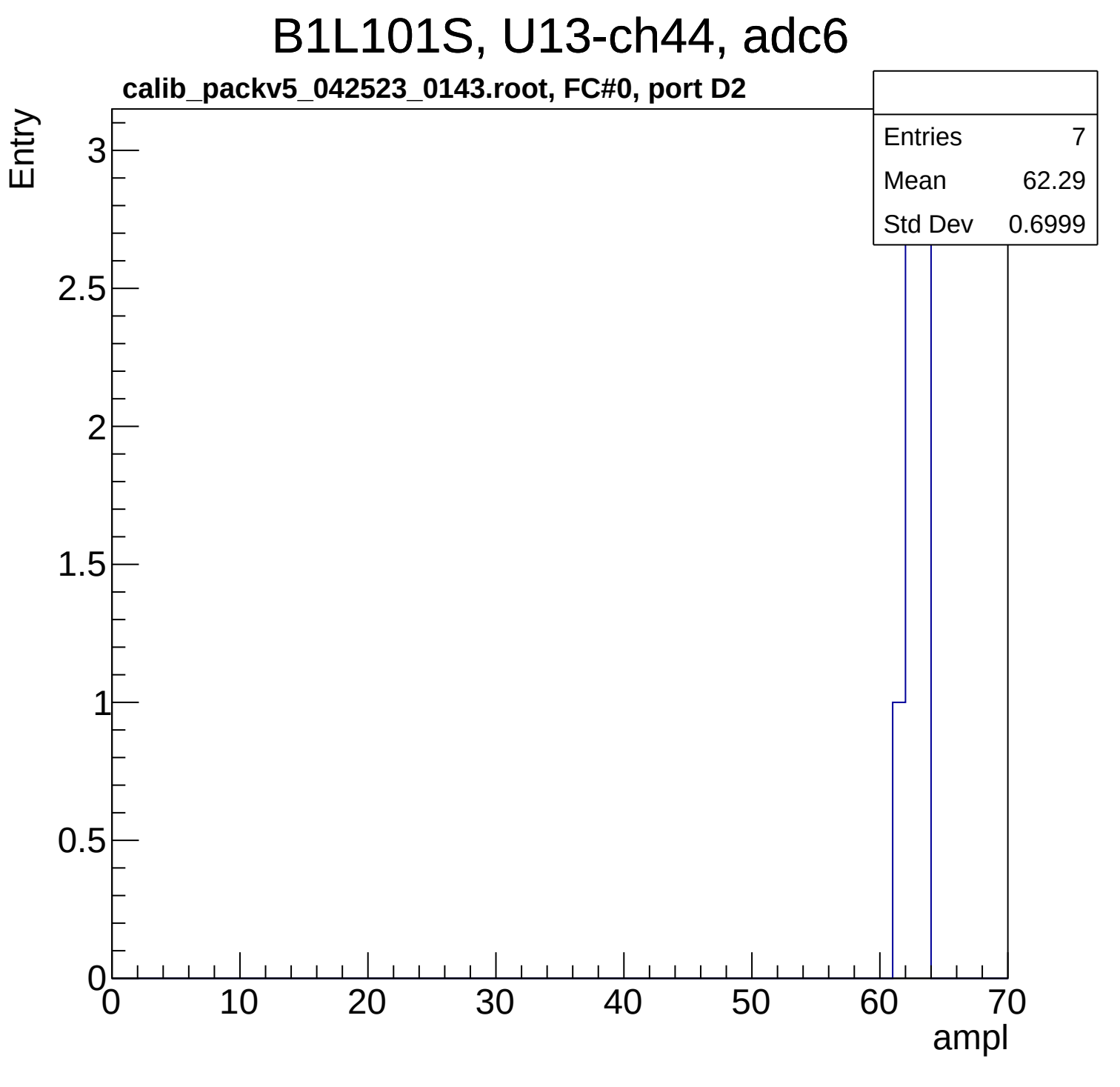
B1L101S, U13-ch44, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
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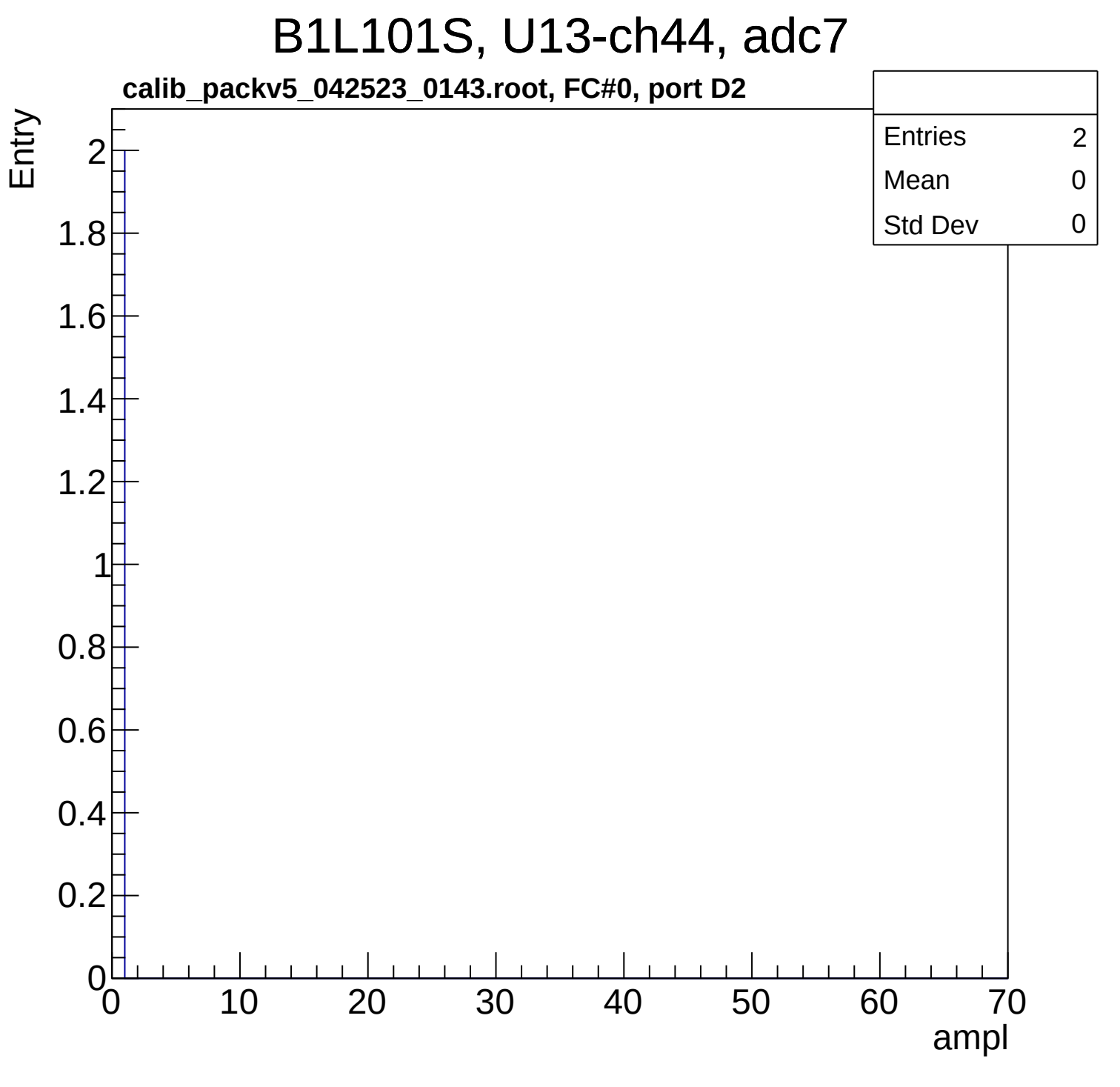
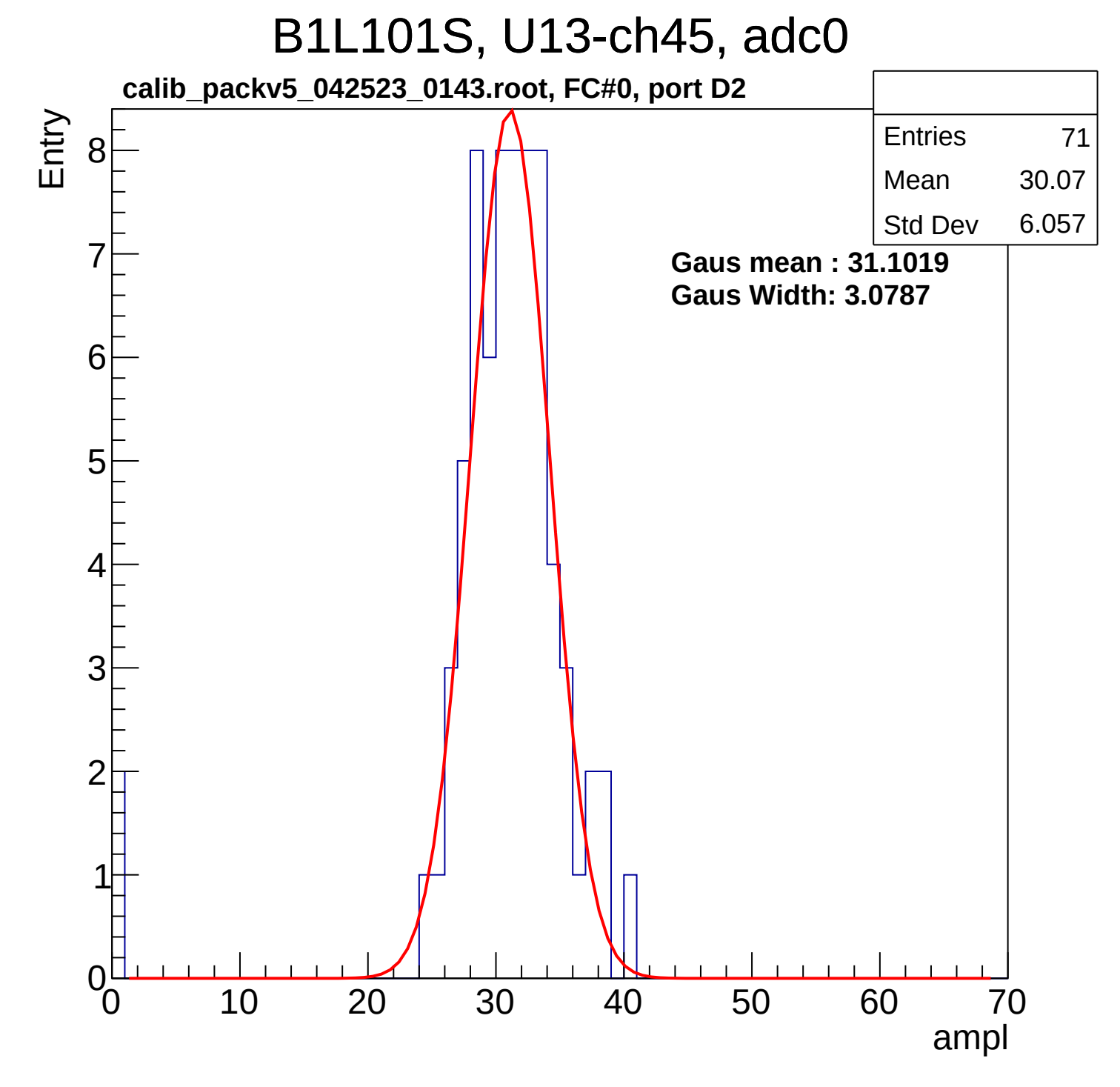
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch44, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch45, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 30.07
Std Dev 6.057
Gaus mean : 31.1019
Gaus Width: 3.0787
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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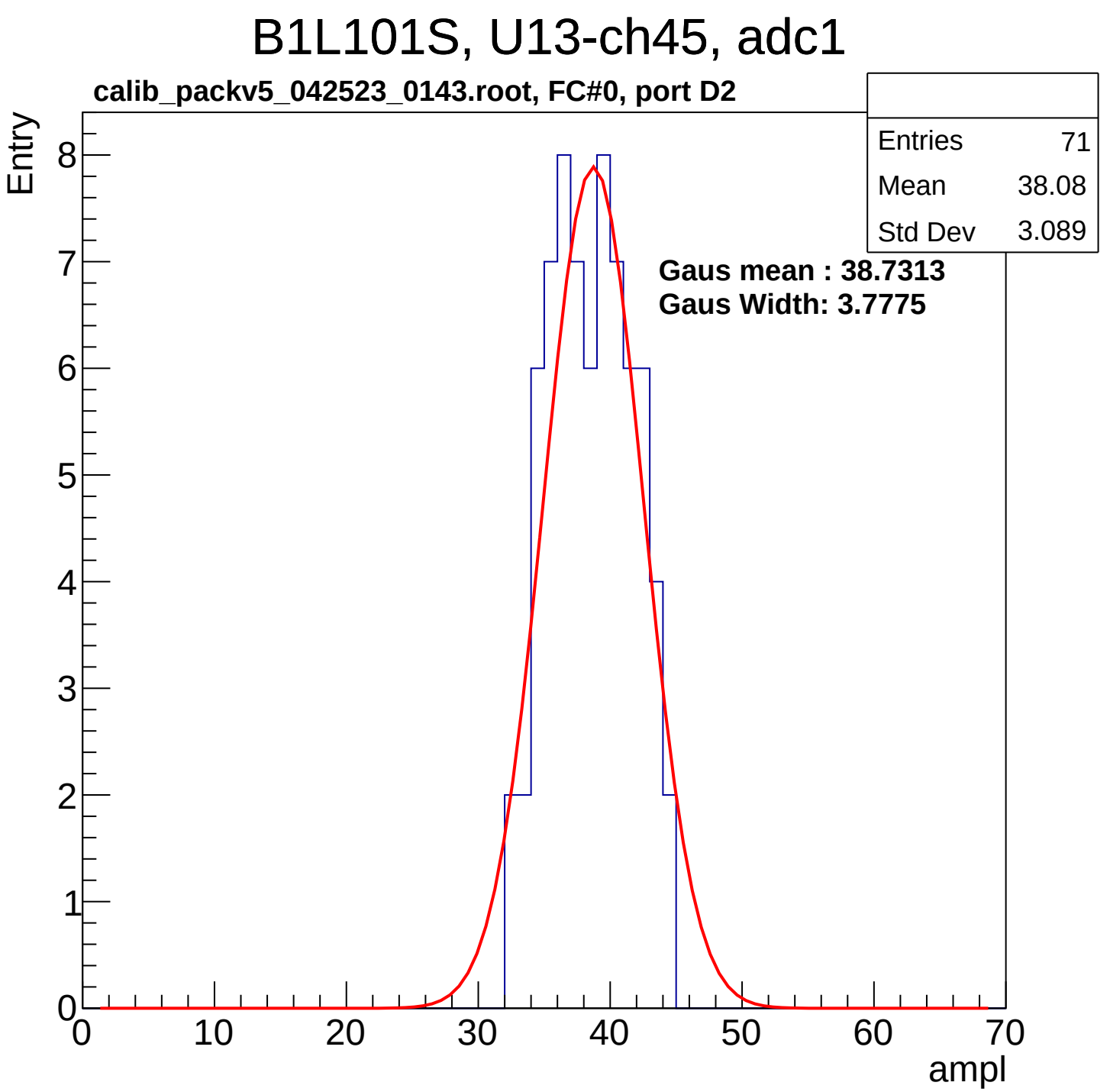
40
50
60
70


B1L101S, U13-ch45, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 38.08
Std Dev 3.089
Gaus mean : 38.7313
Gaus Width: 3.7775
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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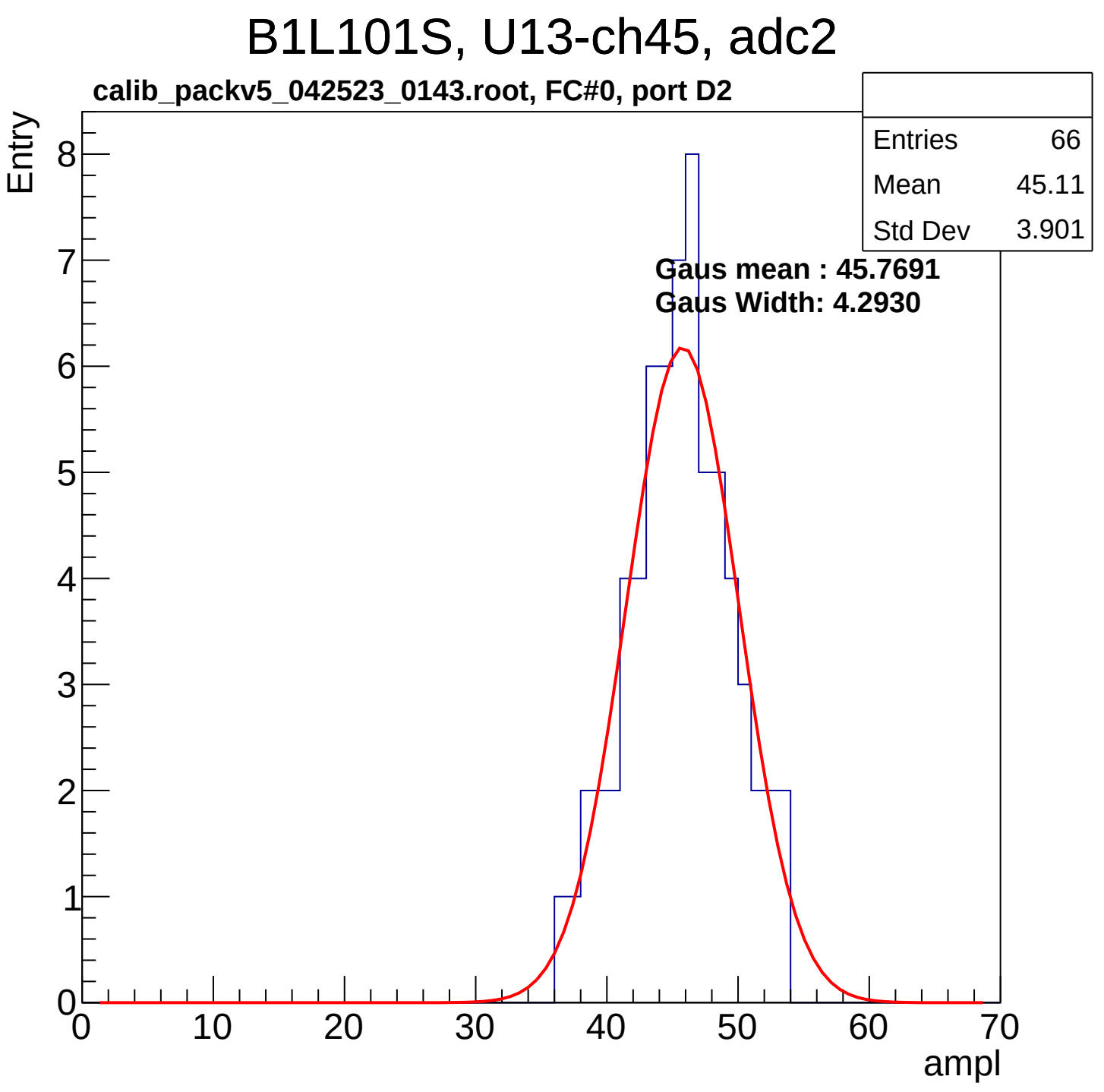
40
50
60
70


B1L101S, U13-ch45, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 45.11
Std Dev 3.901
Gaus mean : 45.7691
Gaus Width: 4.2930
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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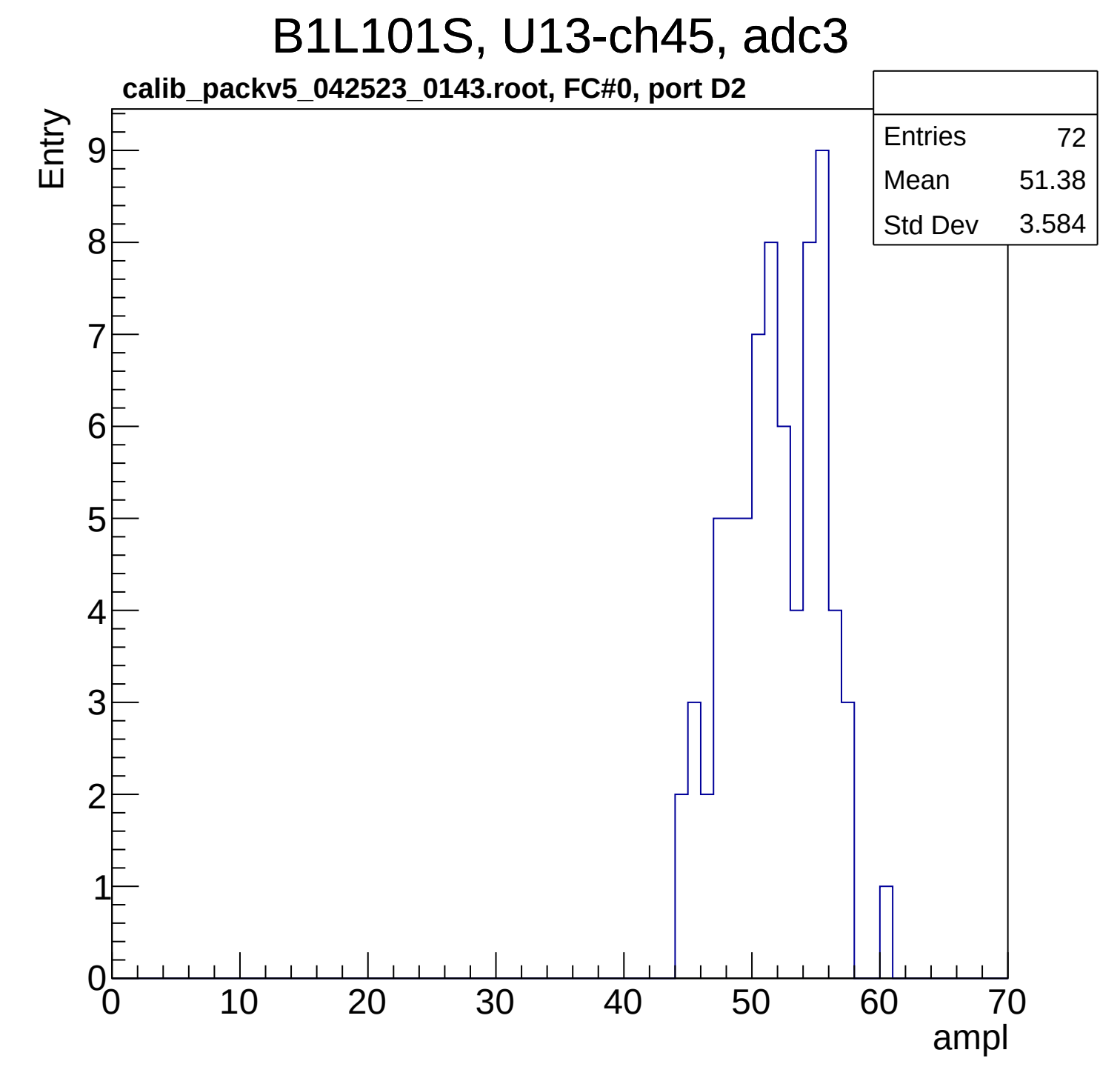
B1L101S, U13-ch45, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 51.38
Std Dev 3.584
Entry
ampl
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7
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0
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60
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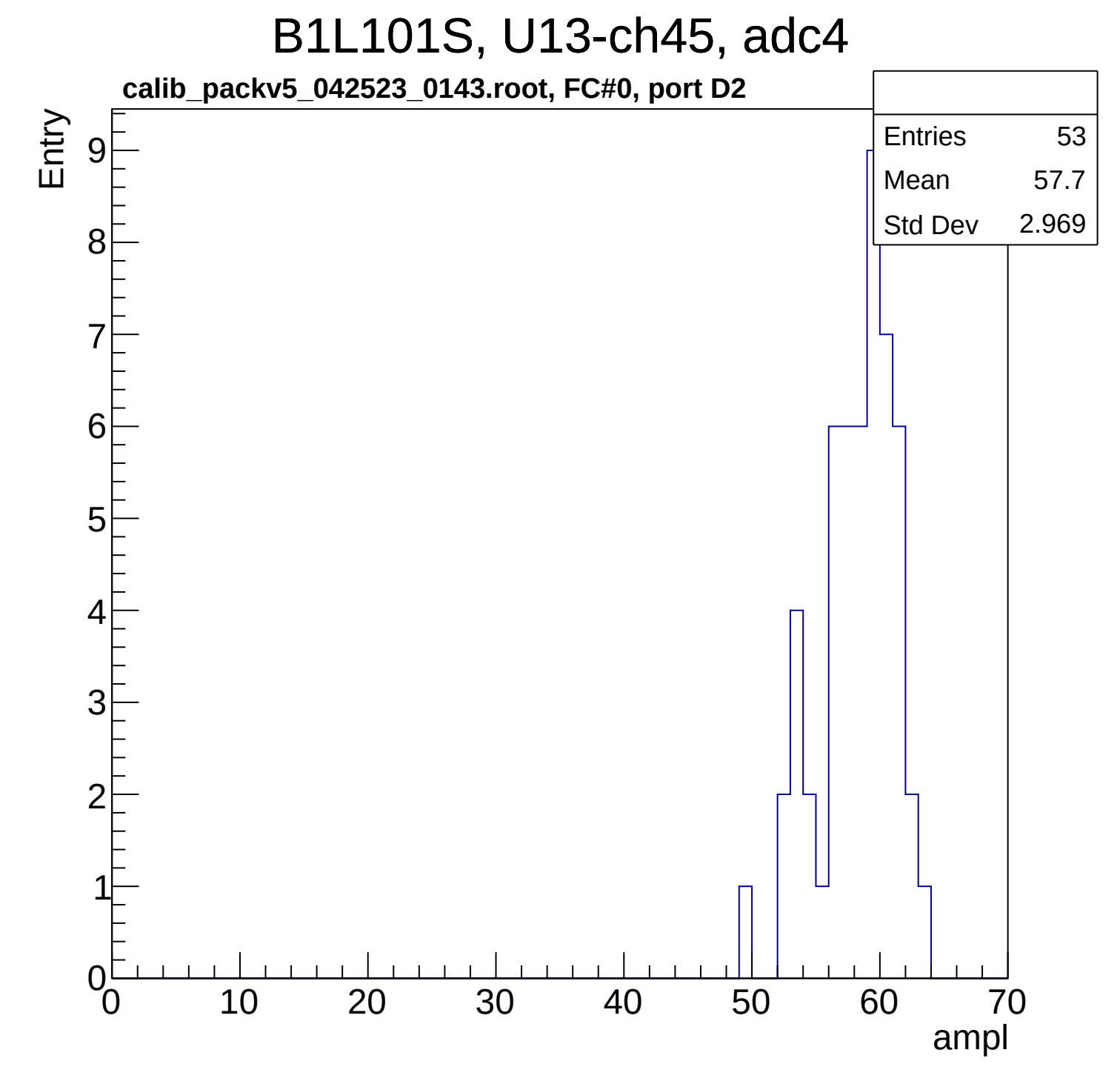
B1L101S, U13-ch45, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 57.7
Std Dev 2.969
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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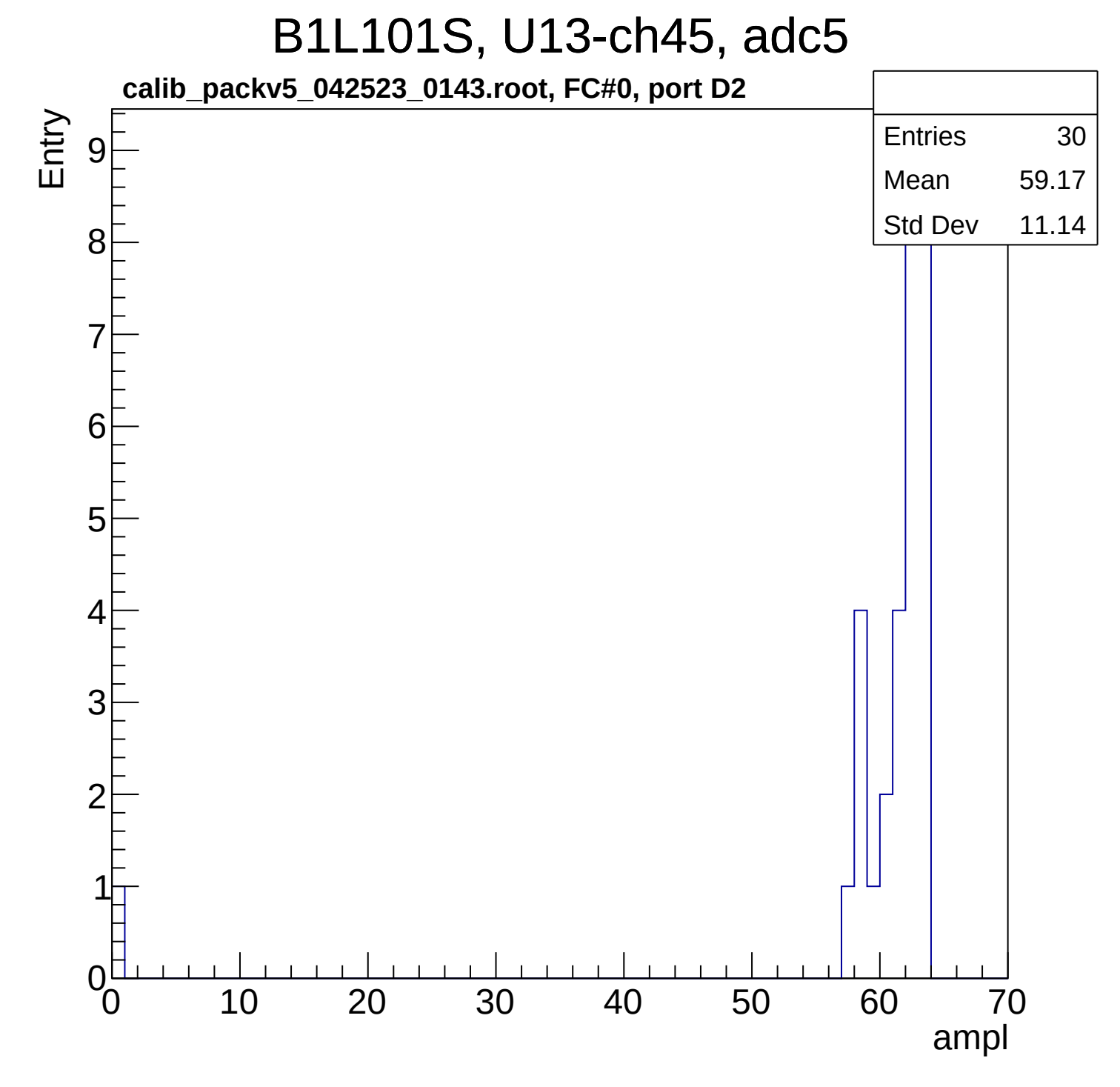
B1L101S, U13-ch45, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 30
Mean 59.17
Std Dev 11.14
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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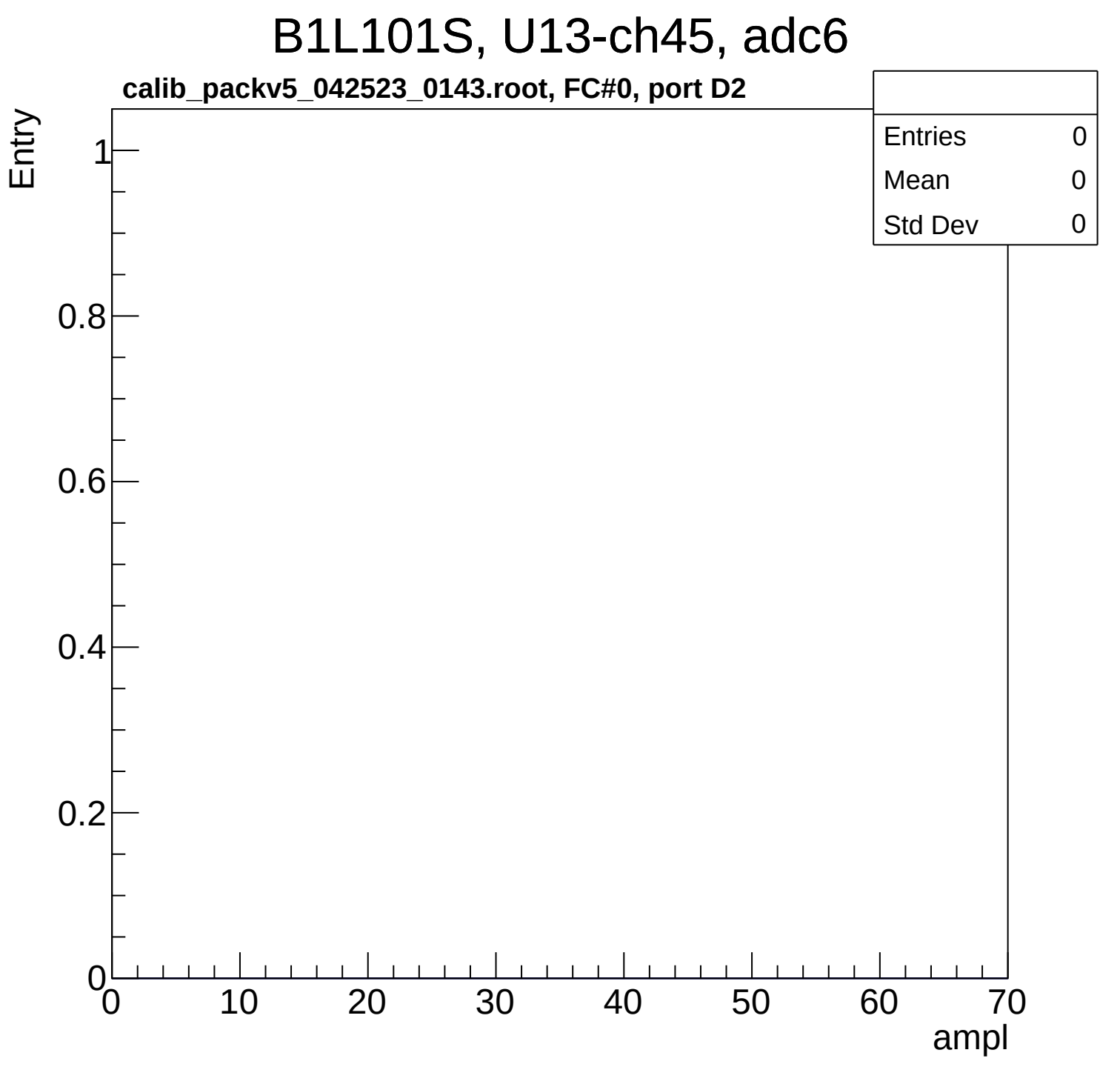
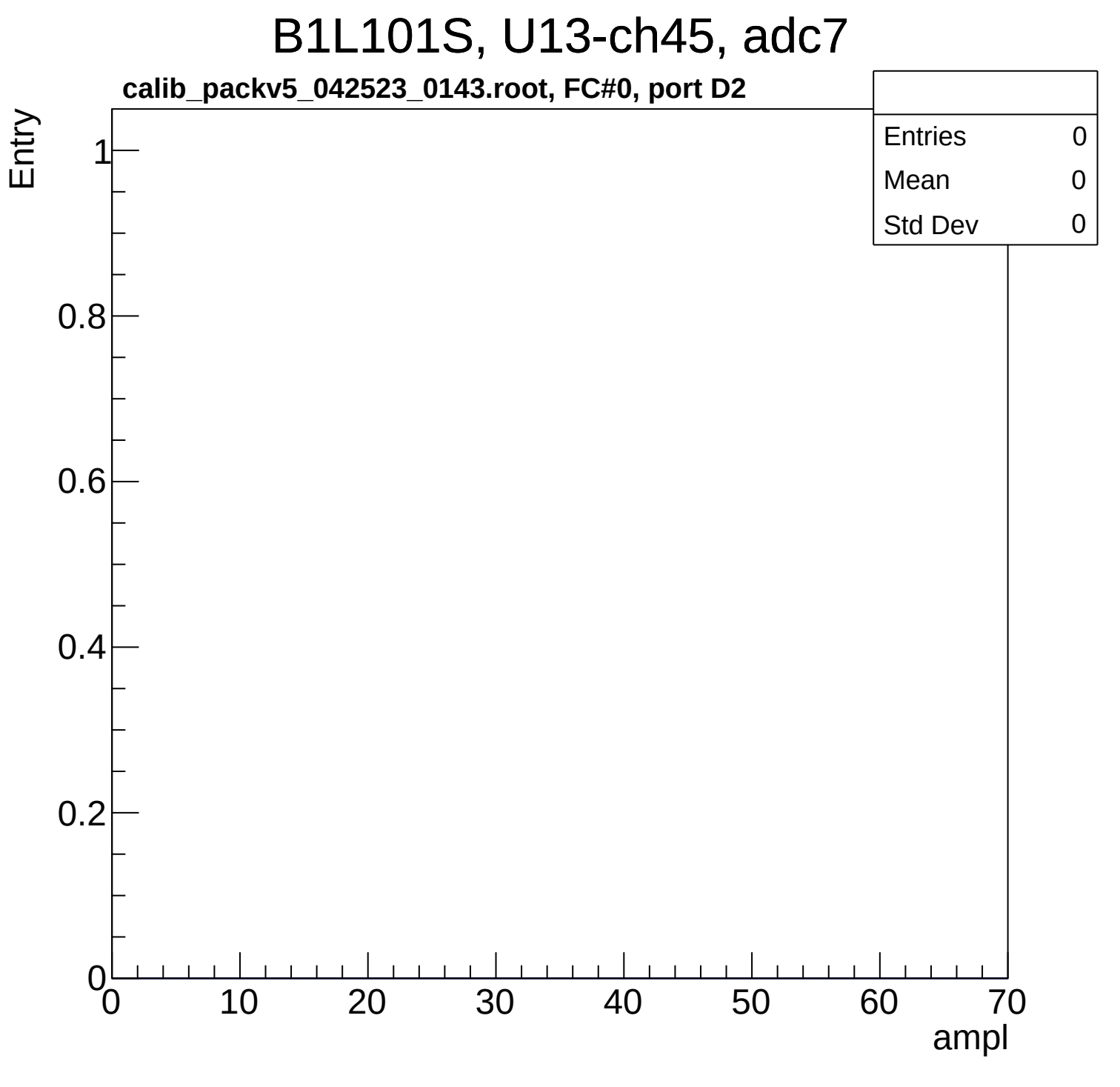

B1L101S, U13-ch45, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch45, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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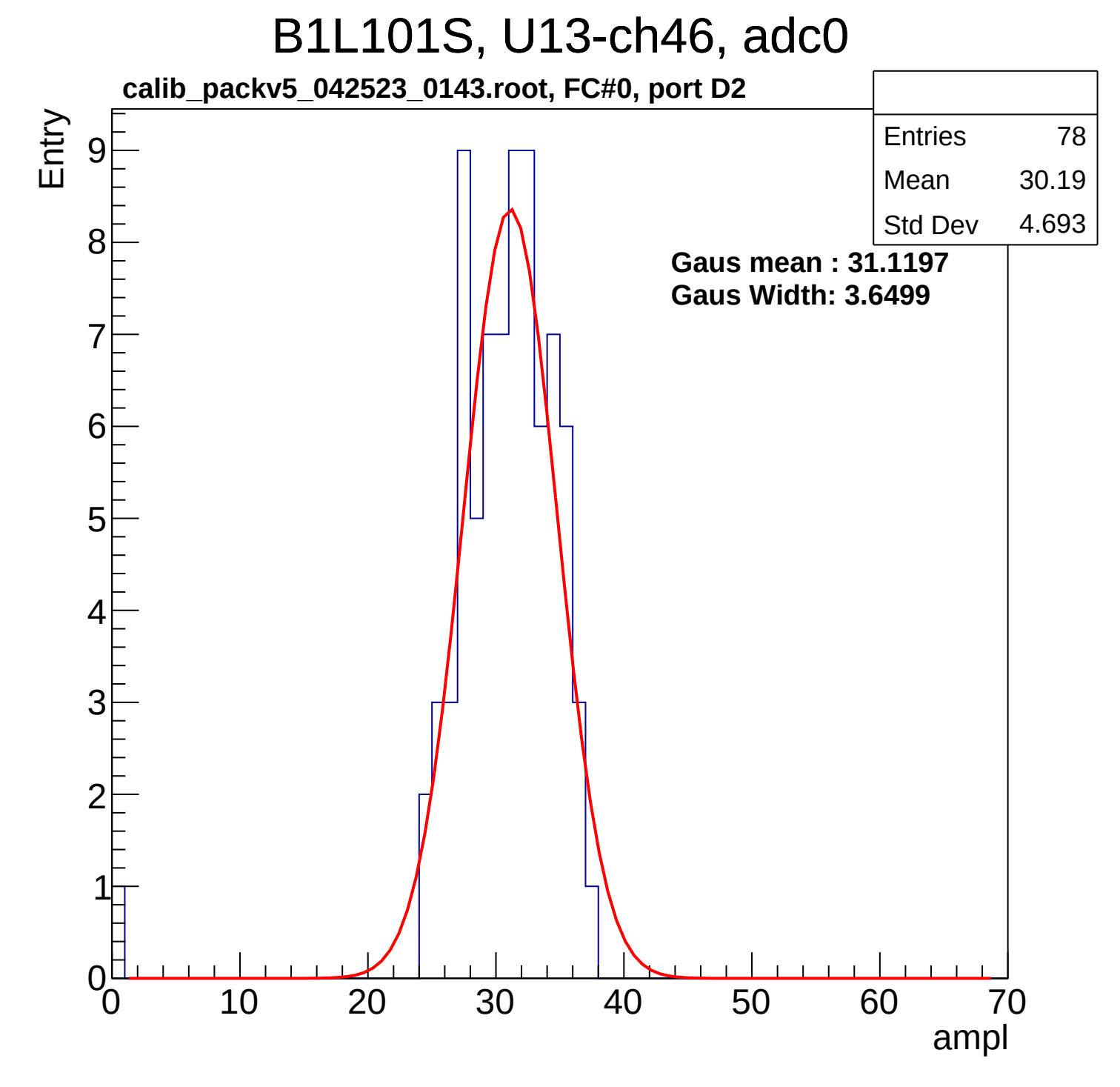
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch46, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 30.19
Std Dev 4.693
Gaus mean : 31.1197
Gaus Width: 3.6499
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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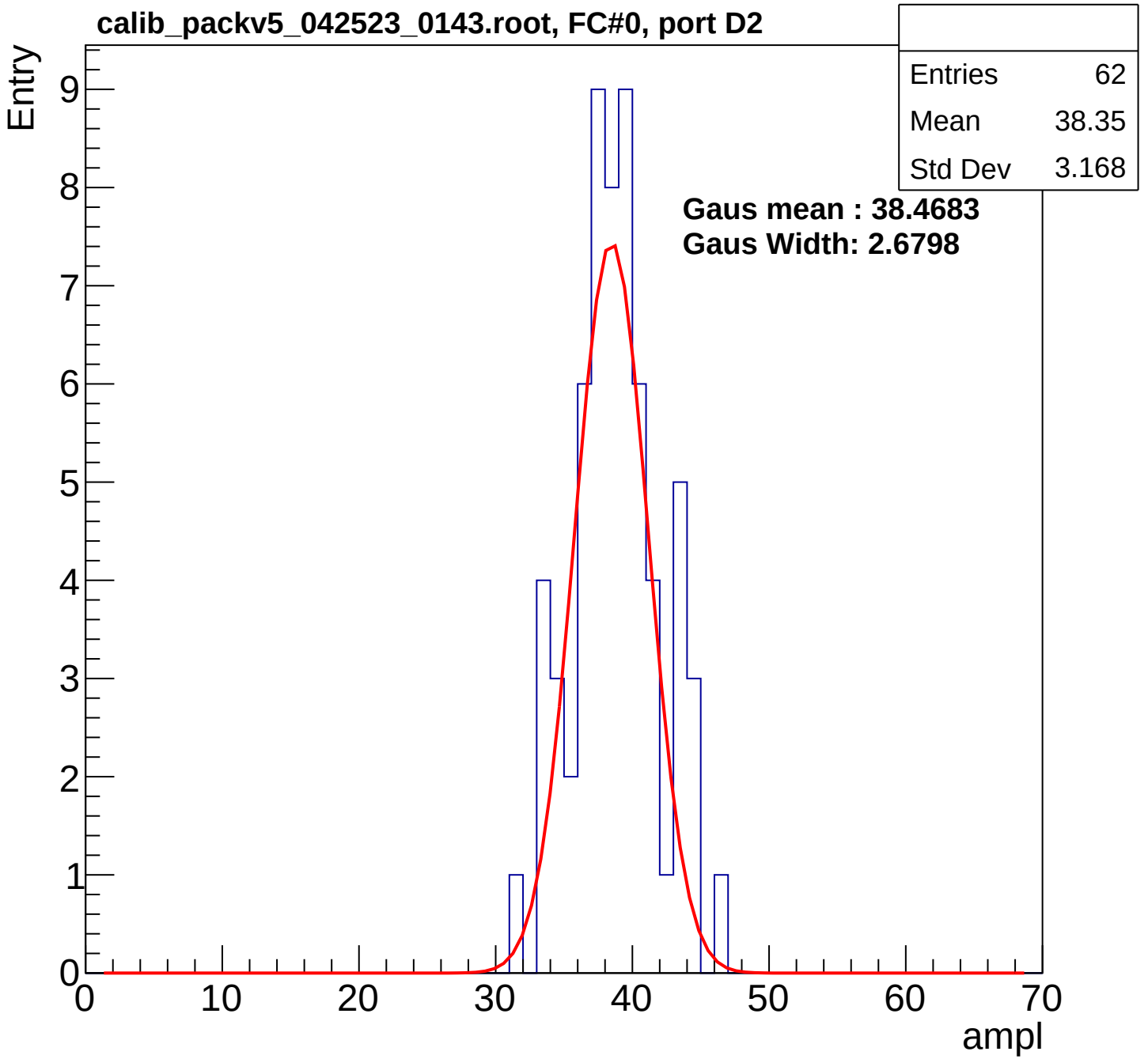
B1L101S, U13-ch46, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 38.35
Std Dev 3.168
Gaus mean : 38.4683
Gaus Width: 2.6798
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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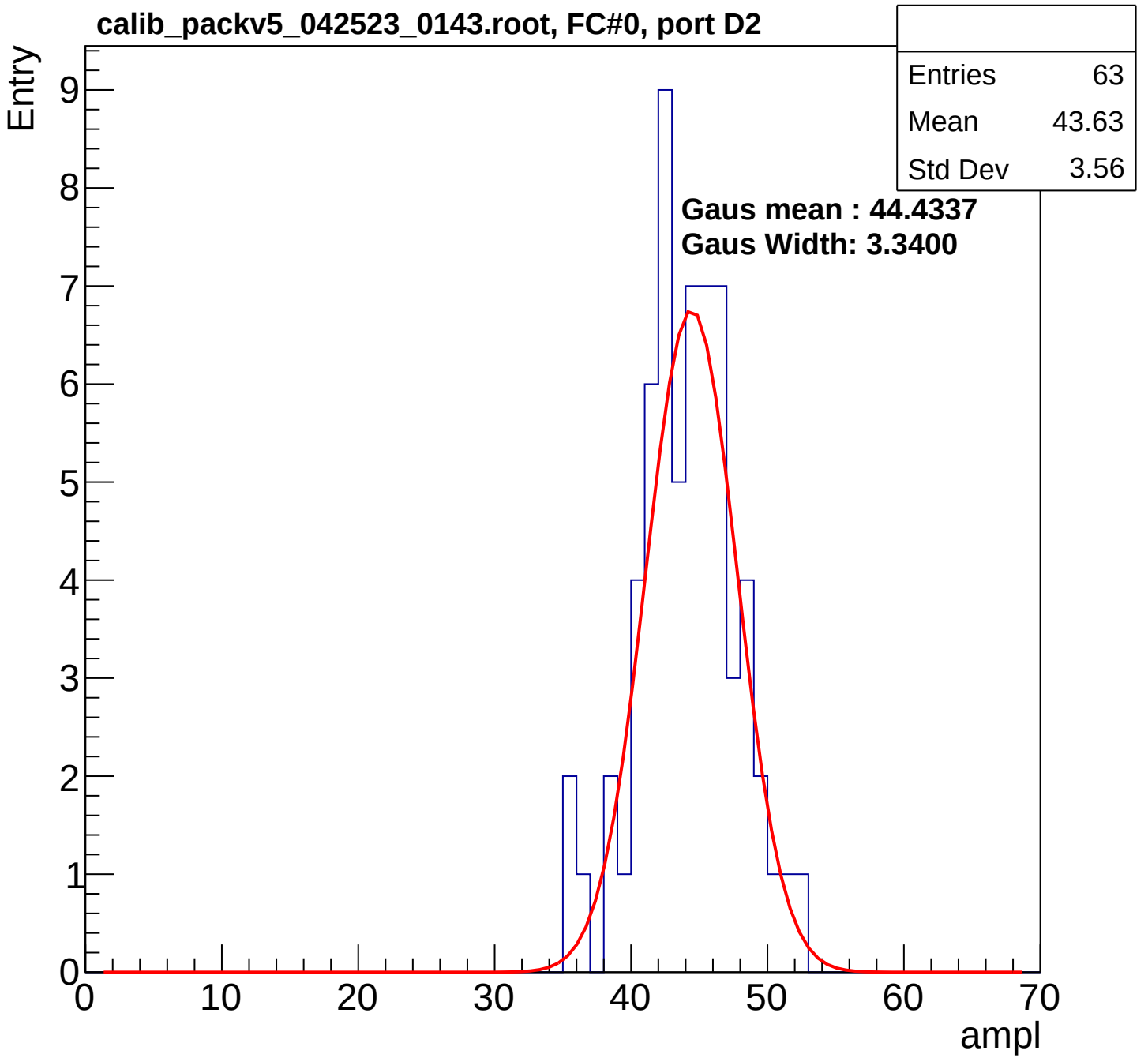
B1L101S, U13-ch46, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 43.63
Std Dev 3.56
Gaus mean : 44.4337
Gaus Width: 3.3400
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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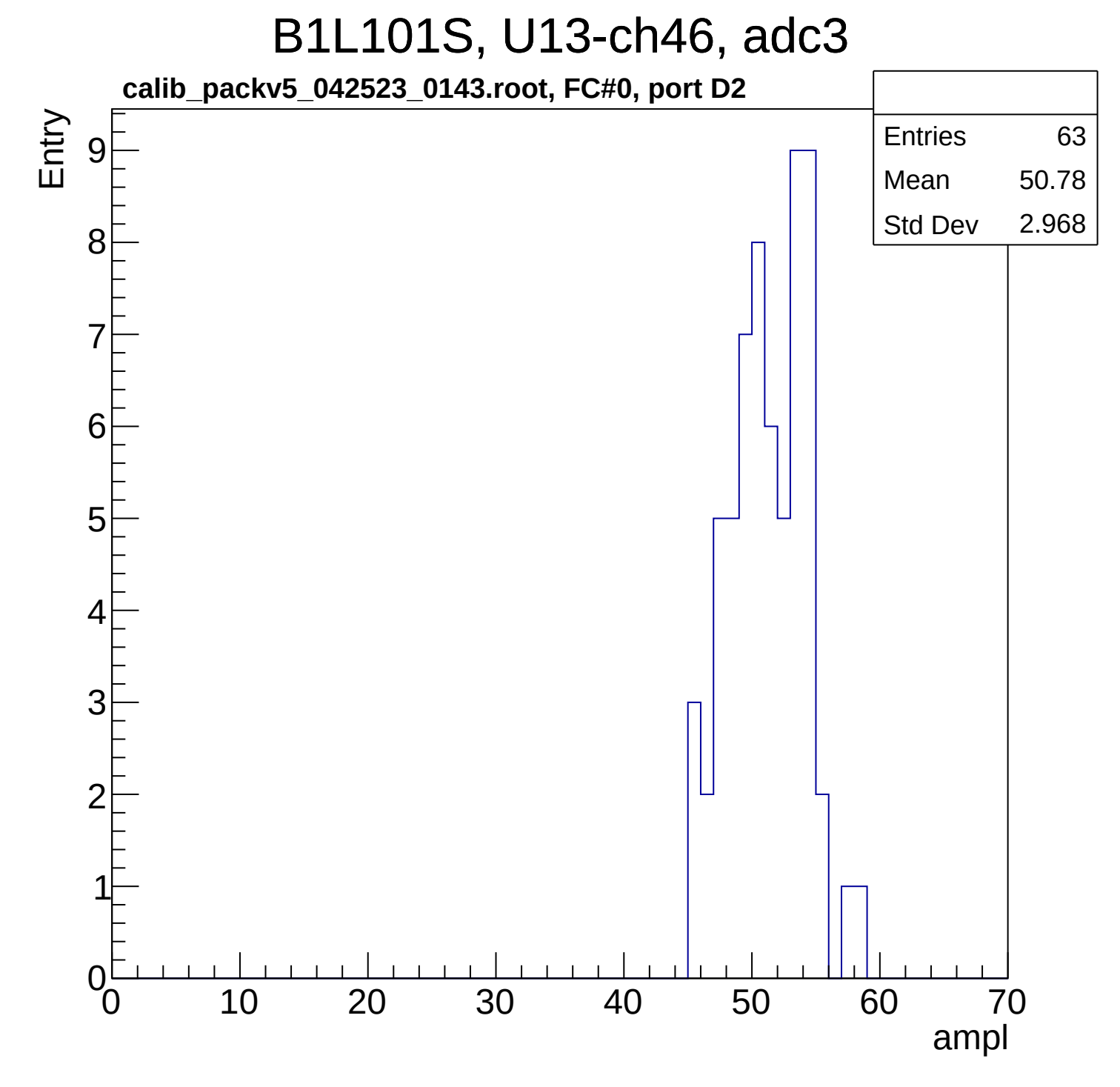
B1L101S, U13-ch46, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 50.78
Std Dev 2.968
Entry
ampl
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6
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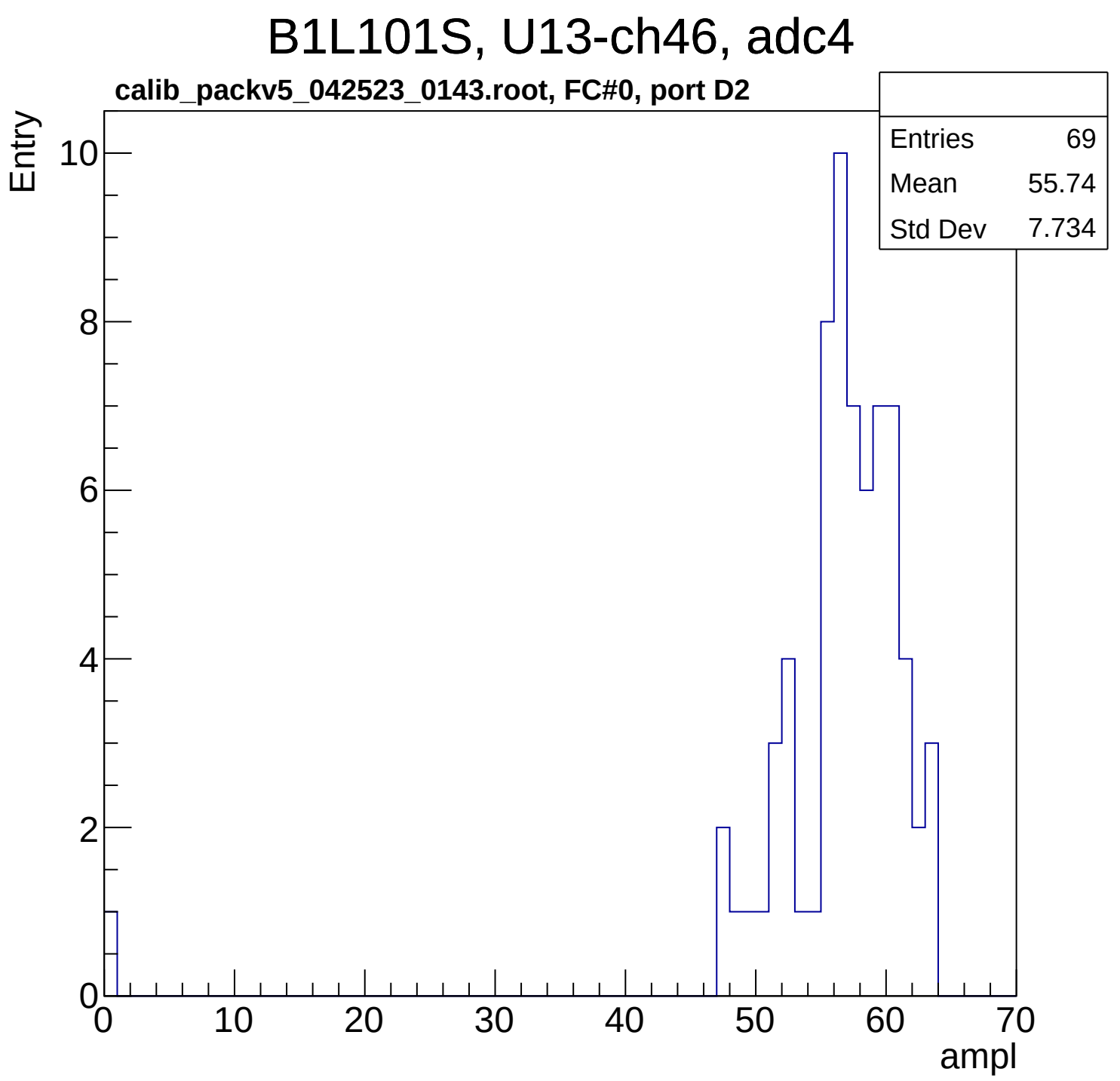
B1L101S, U13-ch46, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 55.74
Std Dev 7.734
Entry
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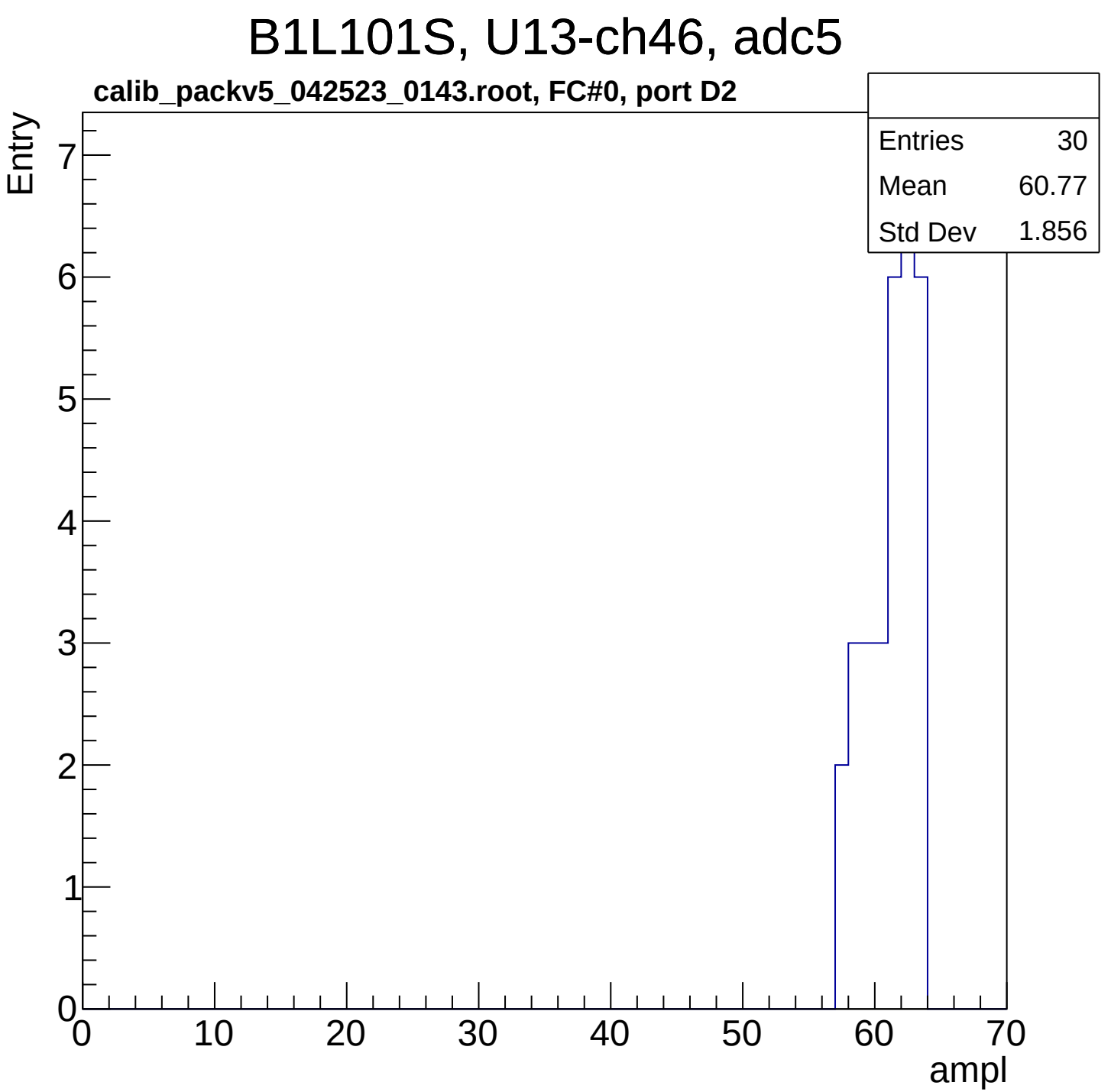
ampl


B1L101S, U13-ch46, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 30
Mean 60.77
Std Dev 1.856
Entry
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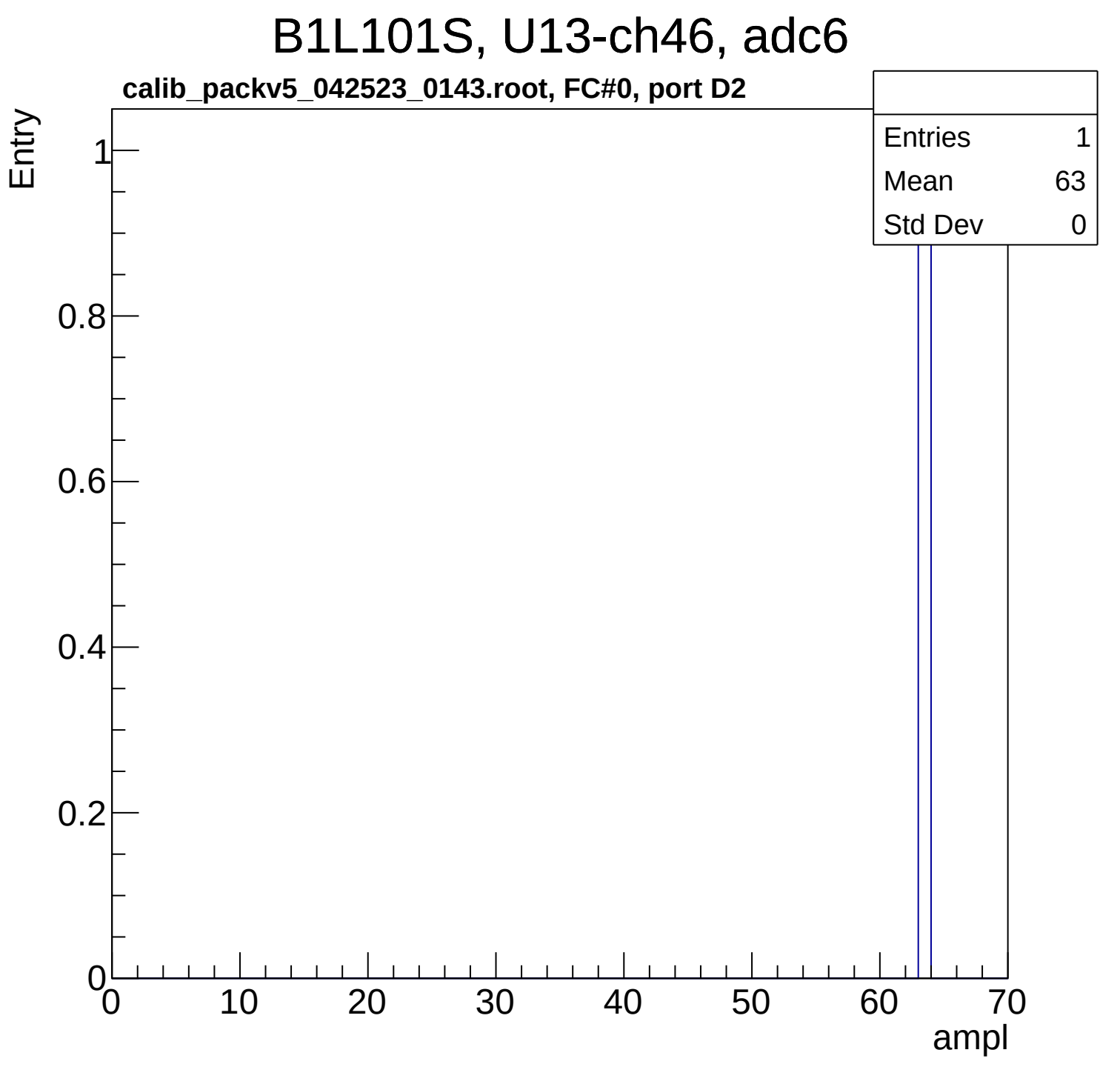
70
ampl


B1L101S, U13-ch46, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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0.8
0.6
0.4
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10
20
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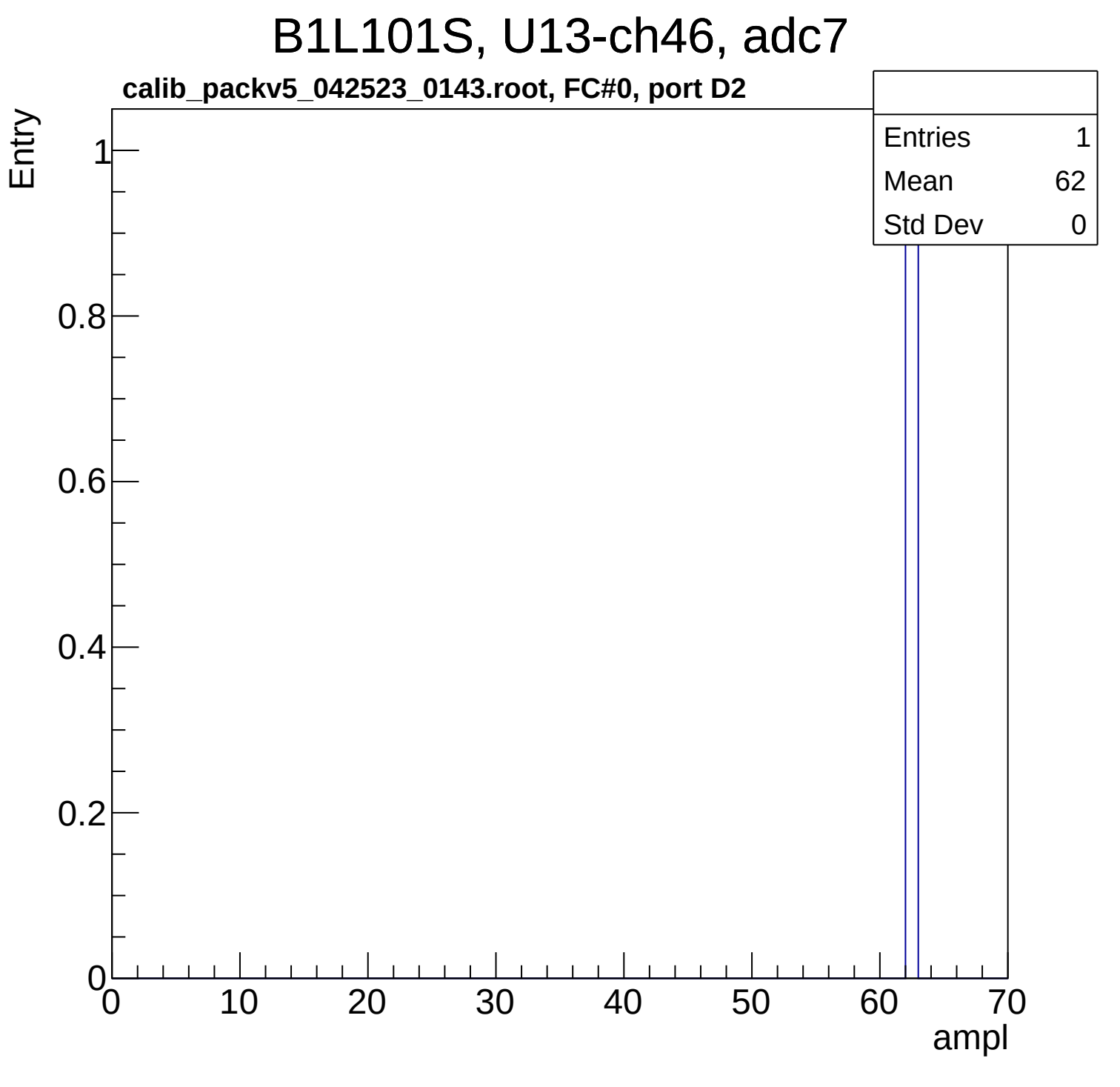
50
60
70
ampl


B1L101S, U13-ch46, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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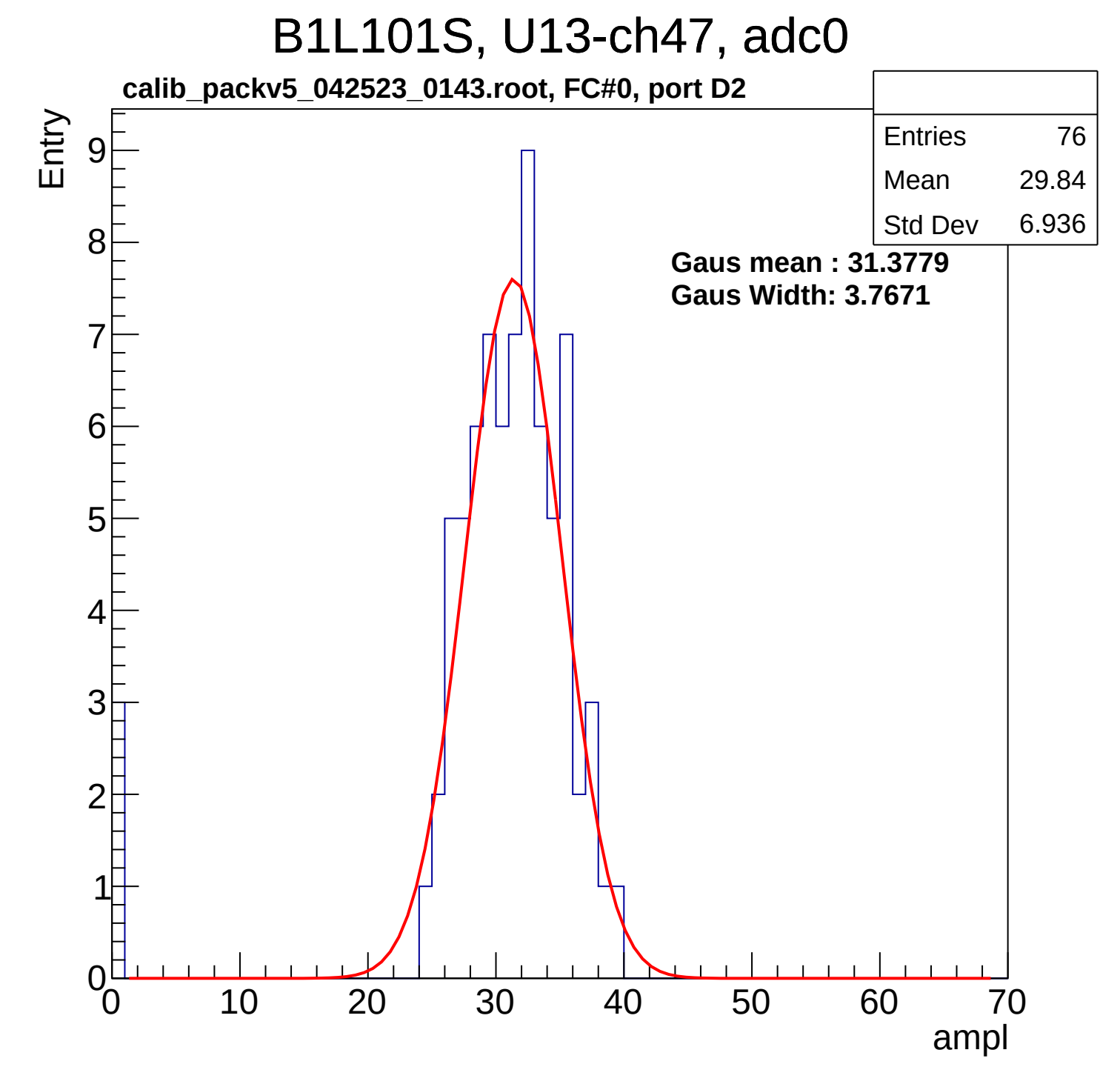
40
50
60
70
ampl


B1L101S, U13-ch47, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 29.84
Std Dev 6.936
Gaus mean : 31.3779
Gaus Width: 3.7671
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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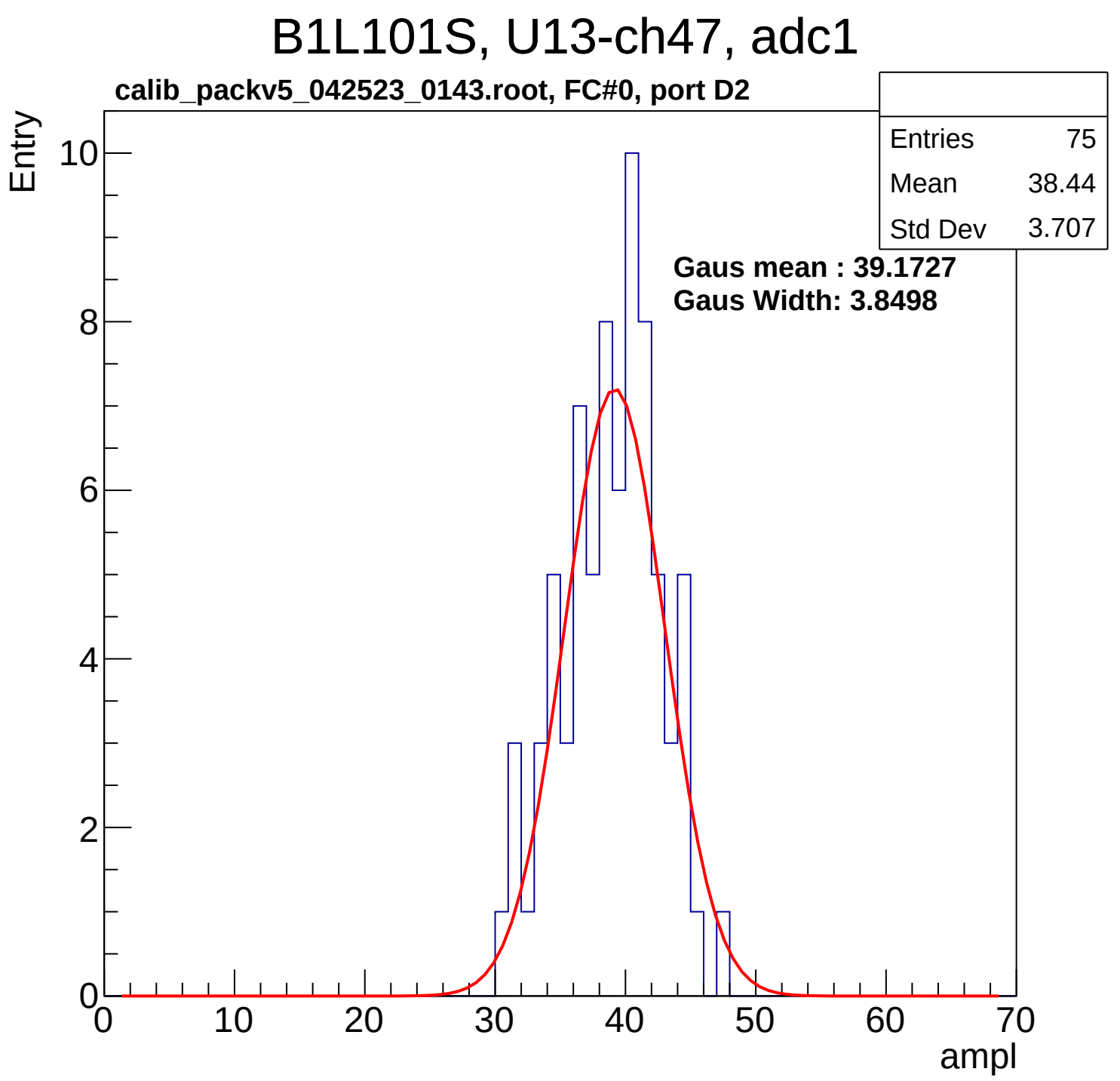
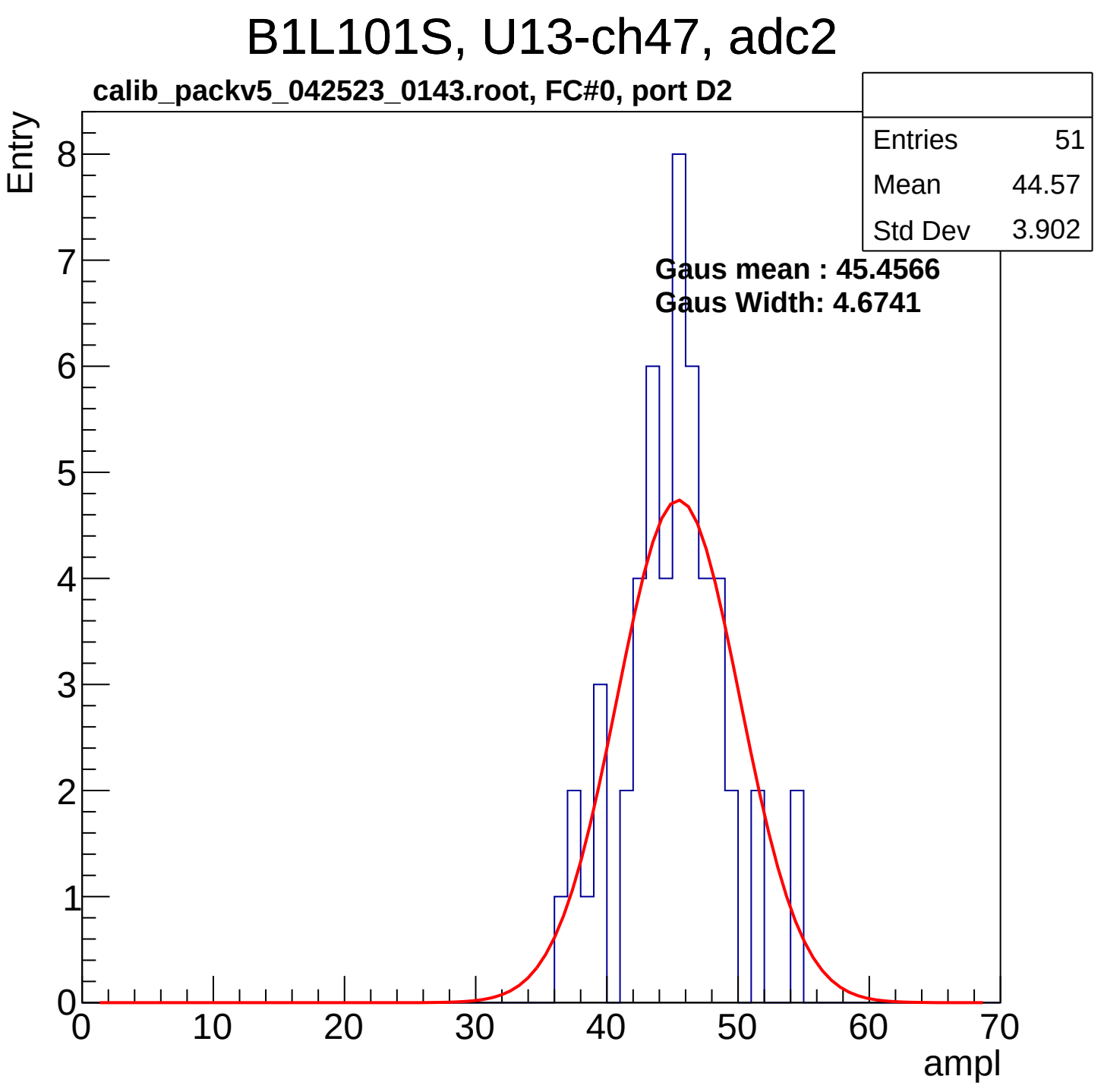

B1L101S, U13-ch47, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 38.44
Std Dev 3.707
Gaus mean : 39.1727
Gaus Width: 3.8498
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch47, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 44.57
Std Dev 3.902
Gaus mean : 45.4566
Gaus Width: 4.6741
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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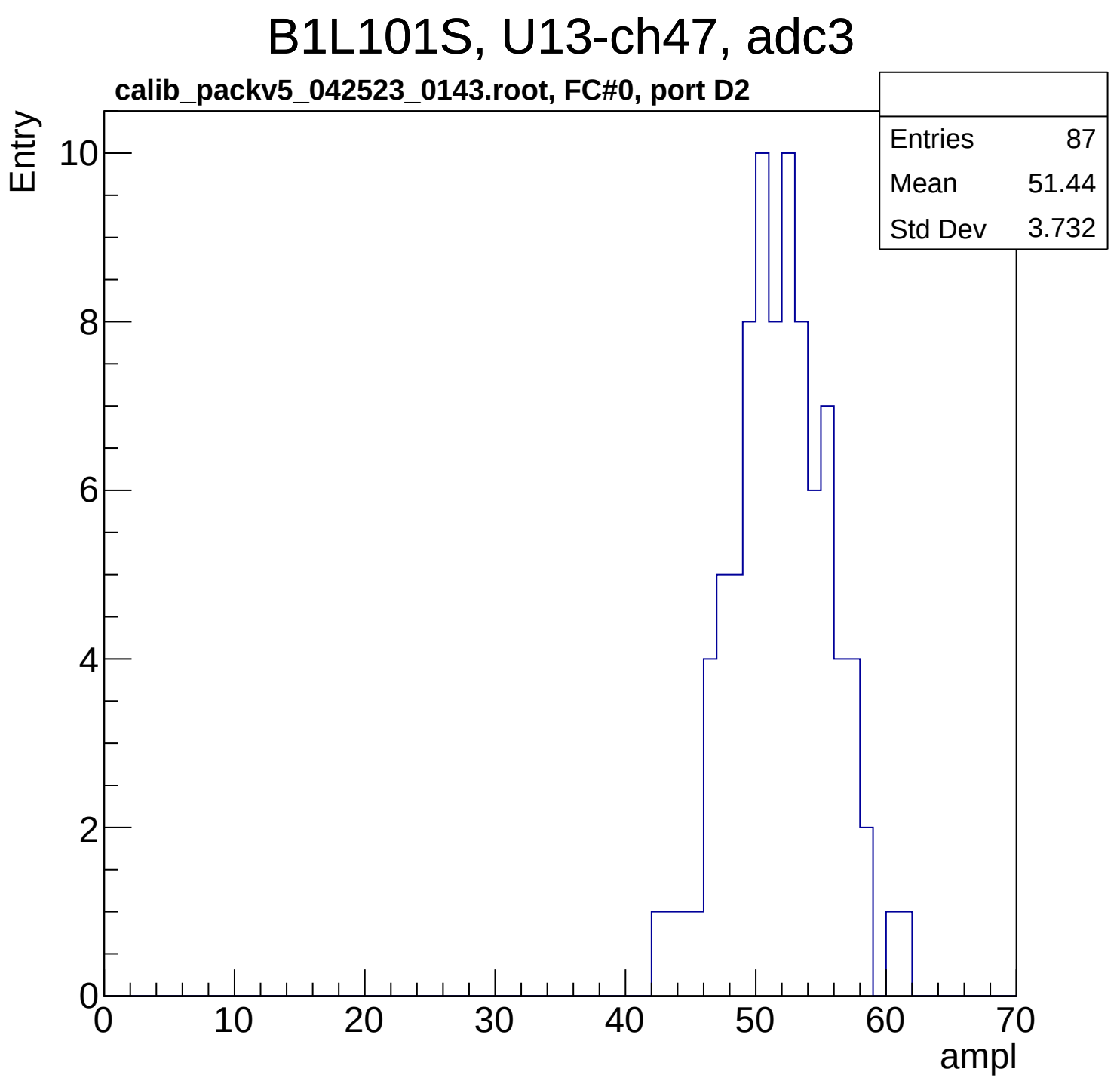
B1L101S, U13-ch47, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 87
Mean 51.44
Std Dev 3.732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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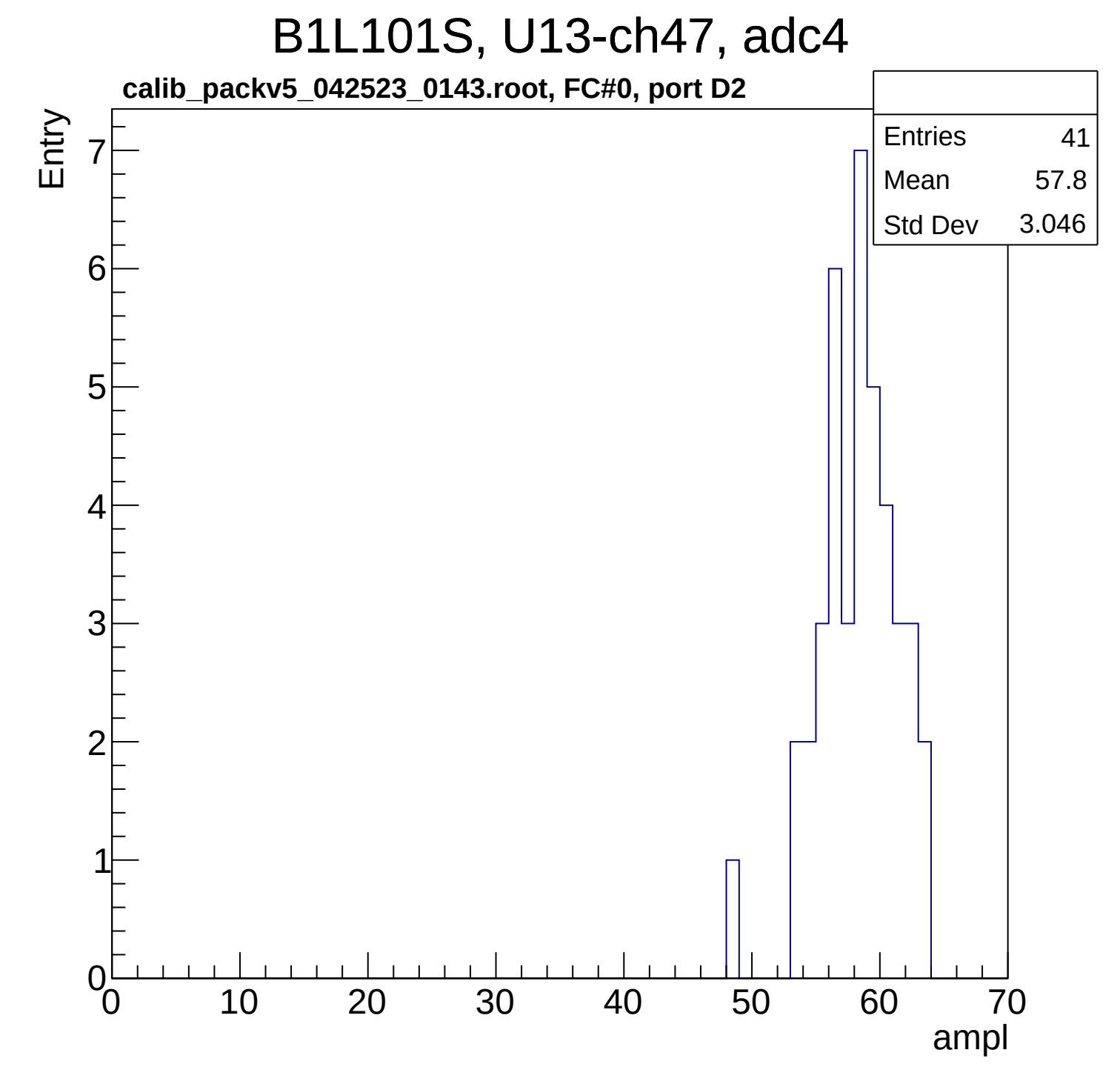
B1L101S, U13-ch47, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 57.8
Std Dev 3.046
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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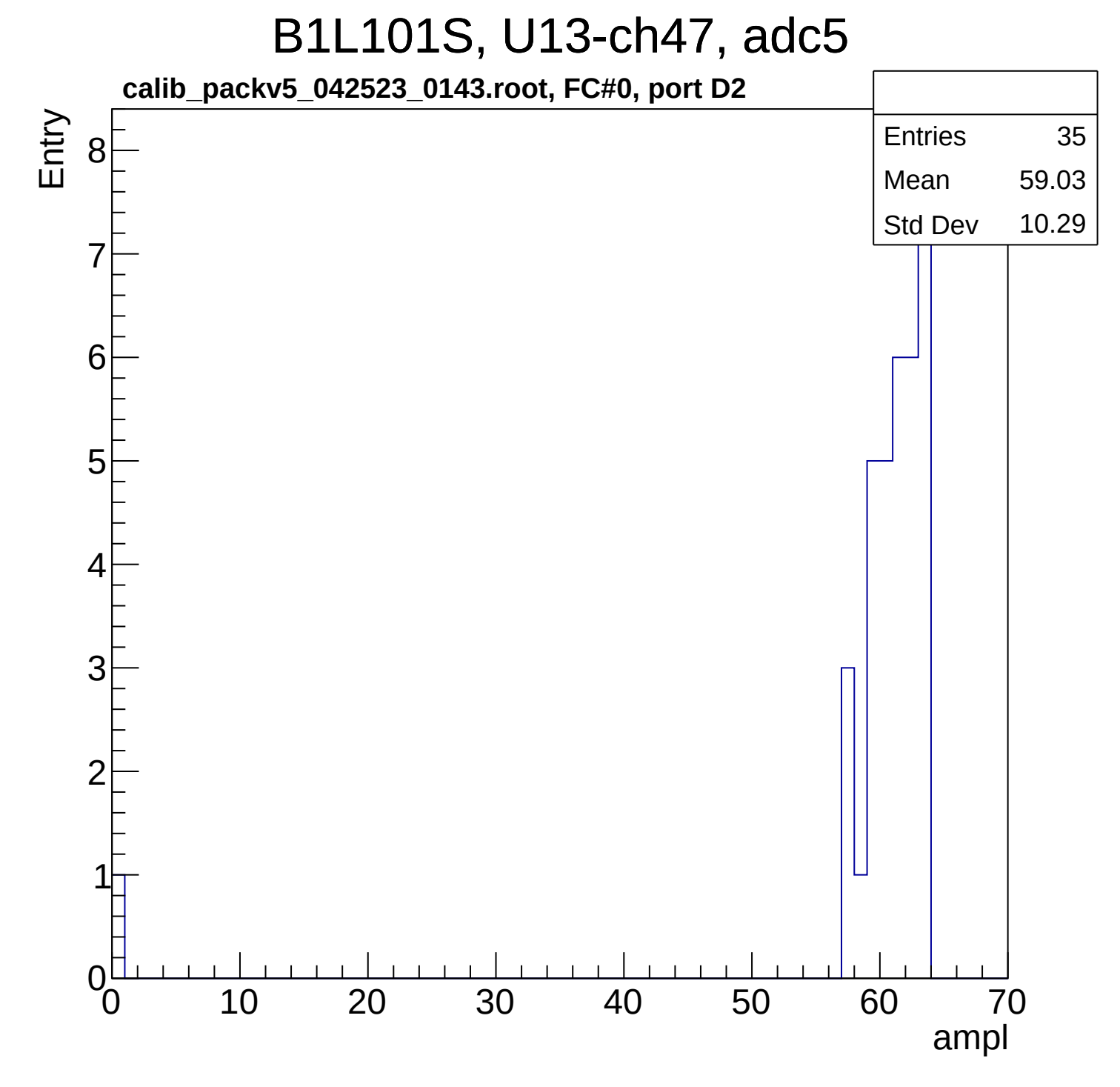
B1L101S, U13-ch47, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 35
Mean 59.03
Std Dev 10.29
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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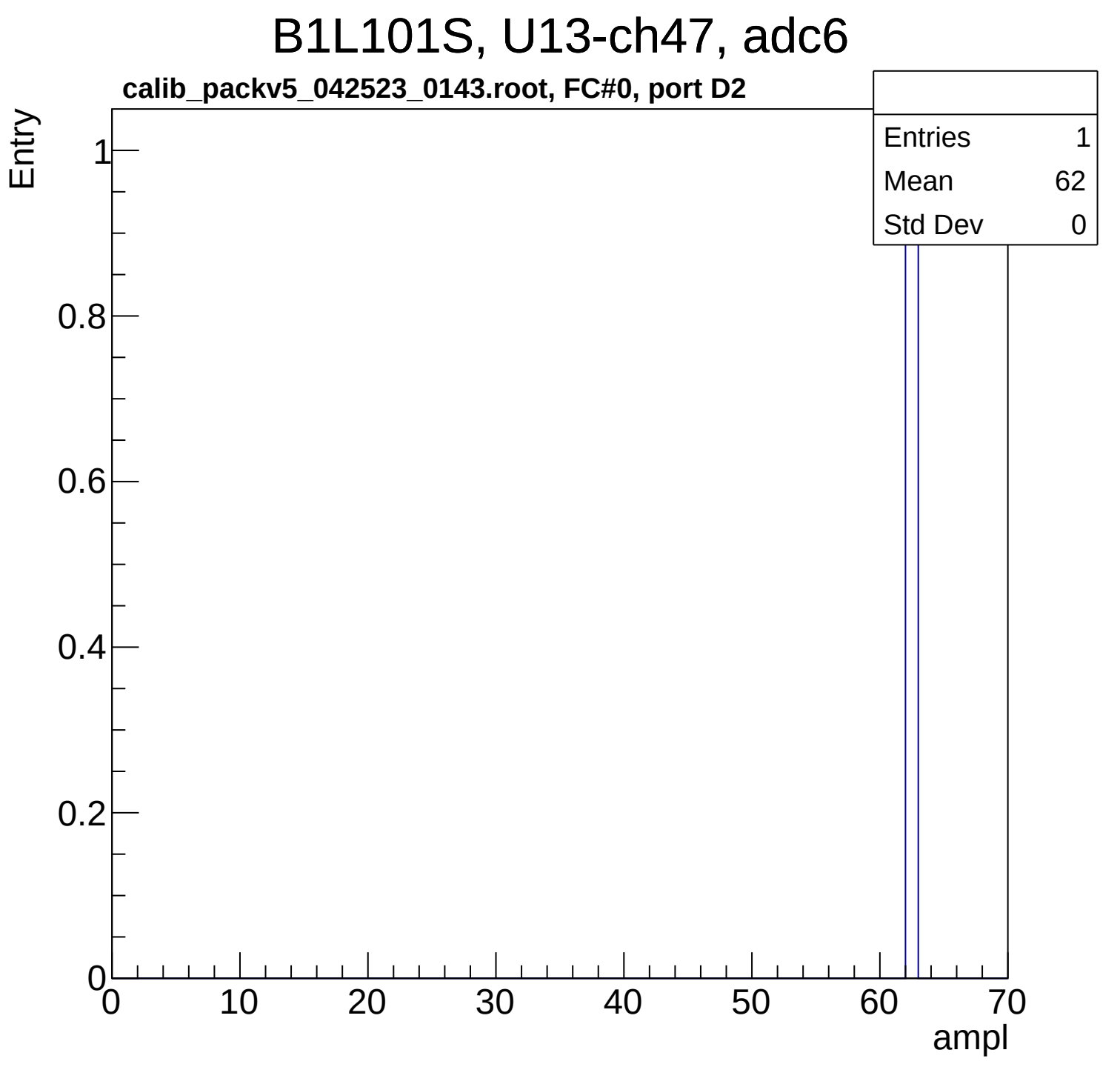
B1L101S, U13-ch47, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 62
Std Dev 0
Entry
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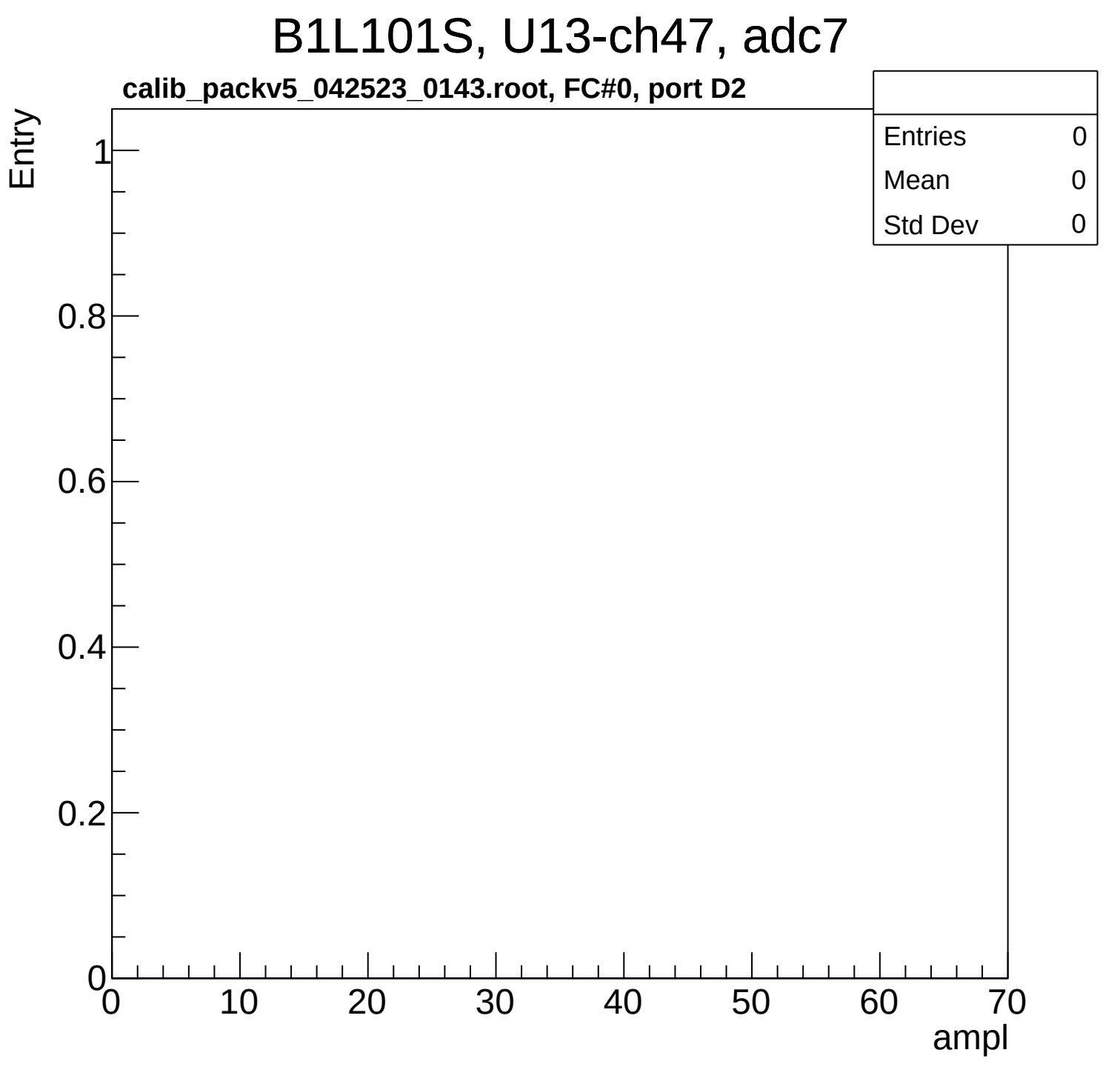
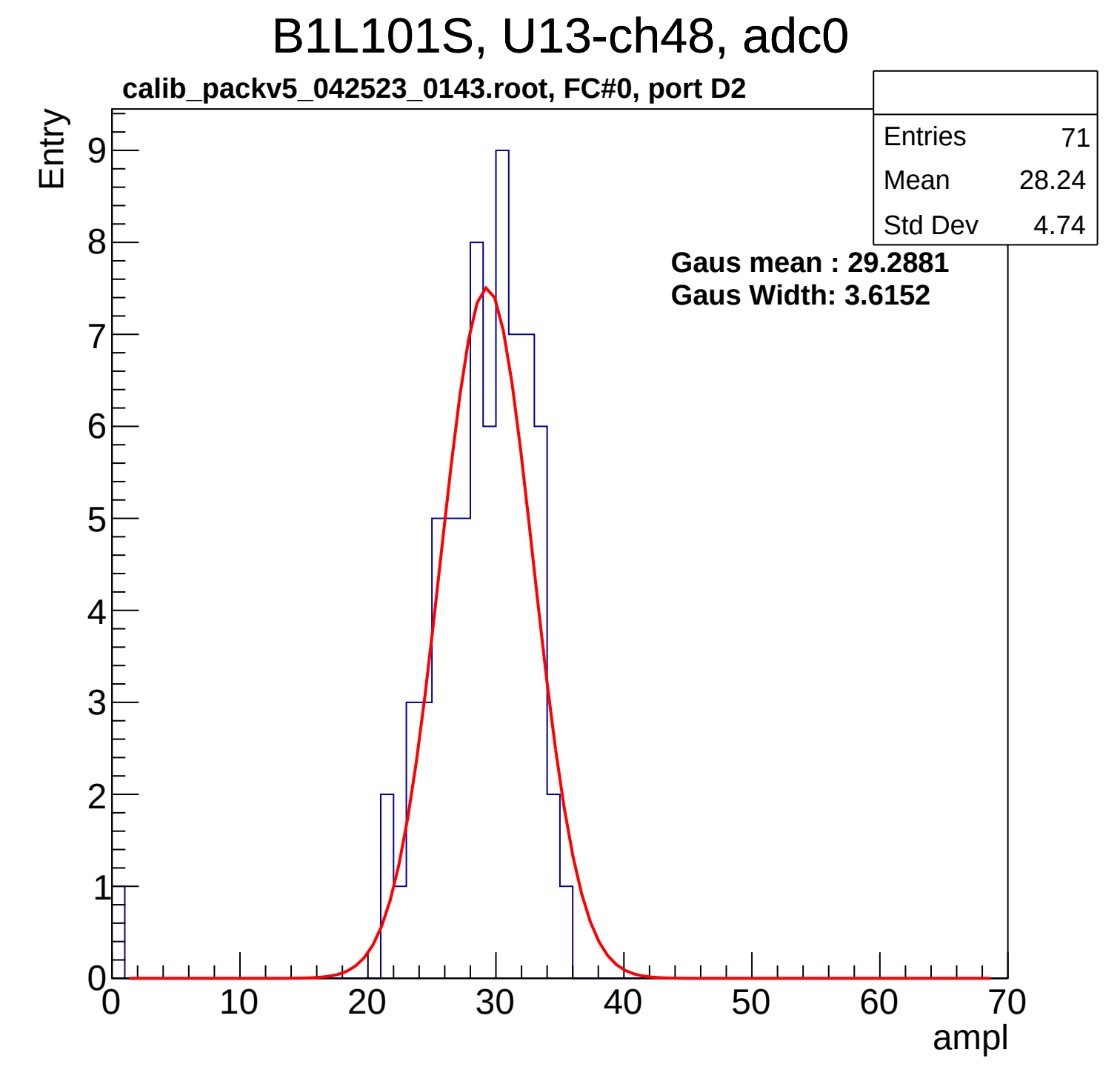
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch47, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch48, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 28.24
Std Dev 4.74
Gaus mean : 29.2881
Gaus Width: 3.6152
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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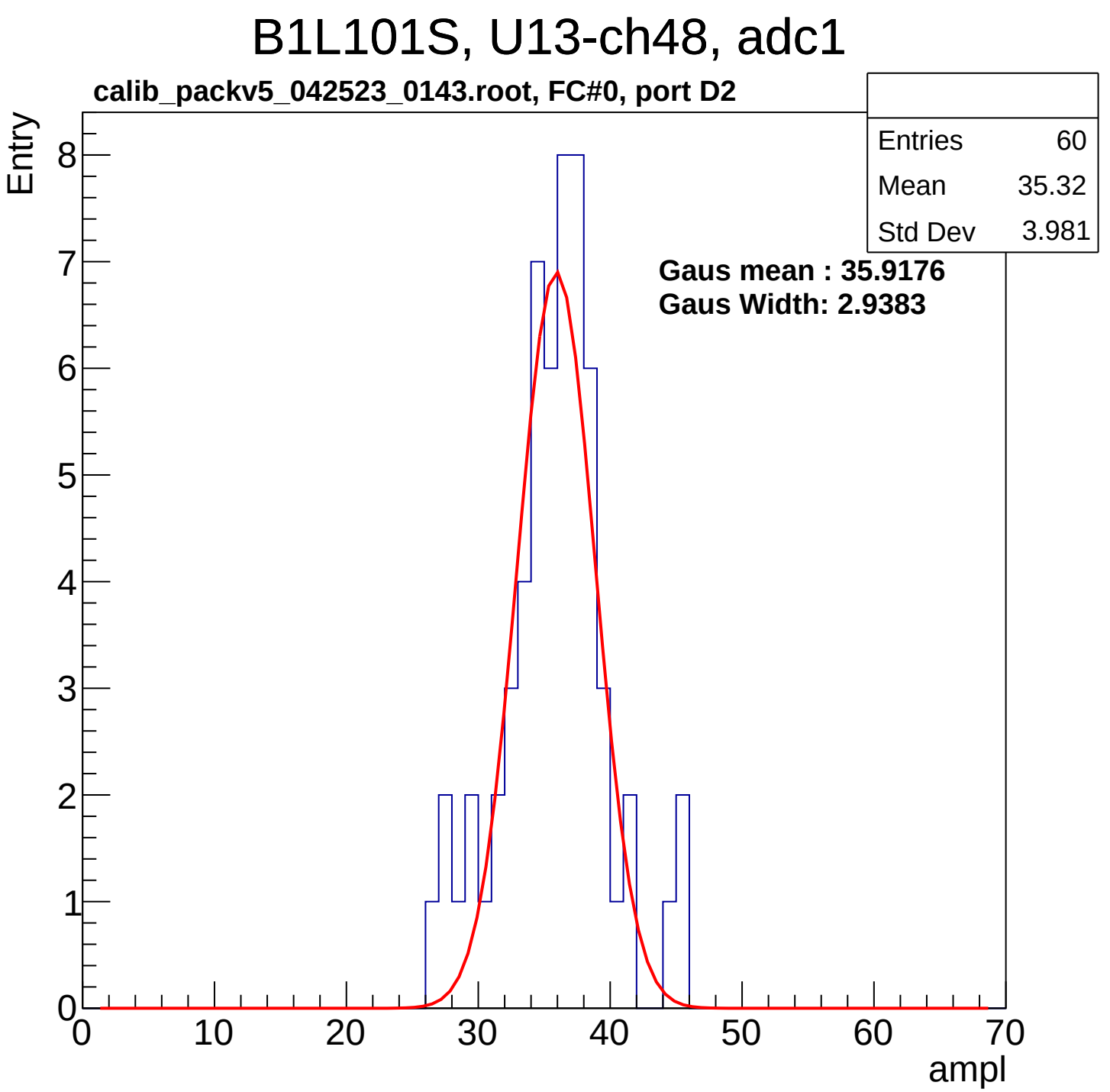
B1L101S, U13-ch48, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 35.32
Std Dev 3.981
Gaus mean : 35.9176
Gaus Width: 2.9383
Entry
ampl
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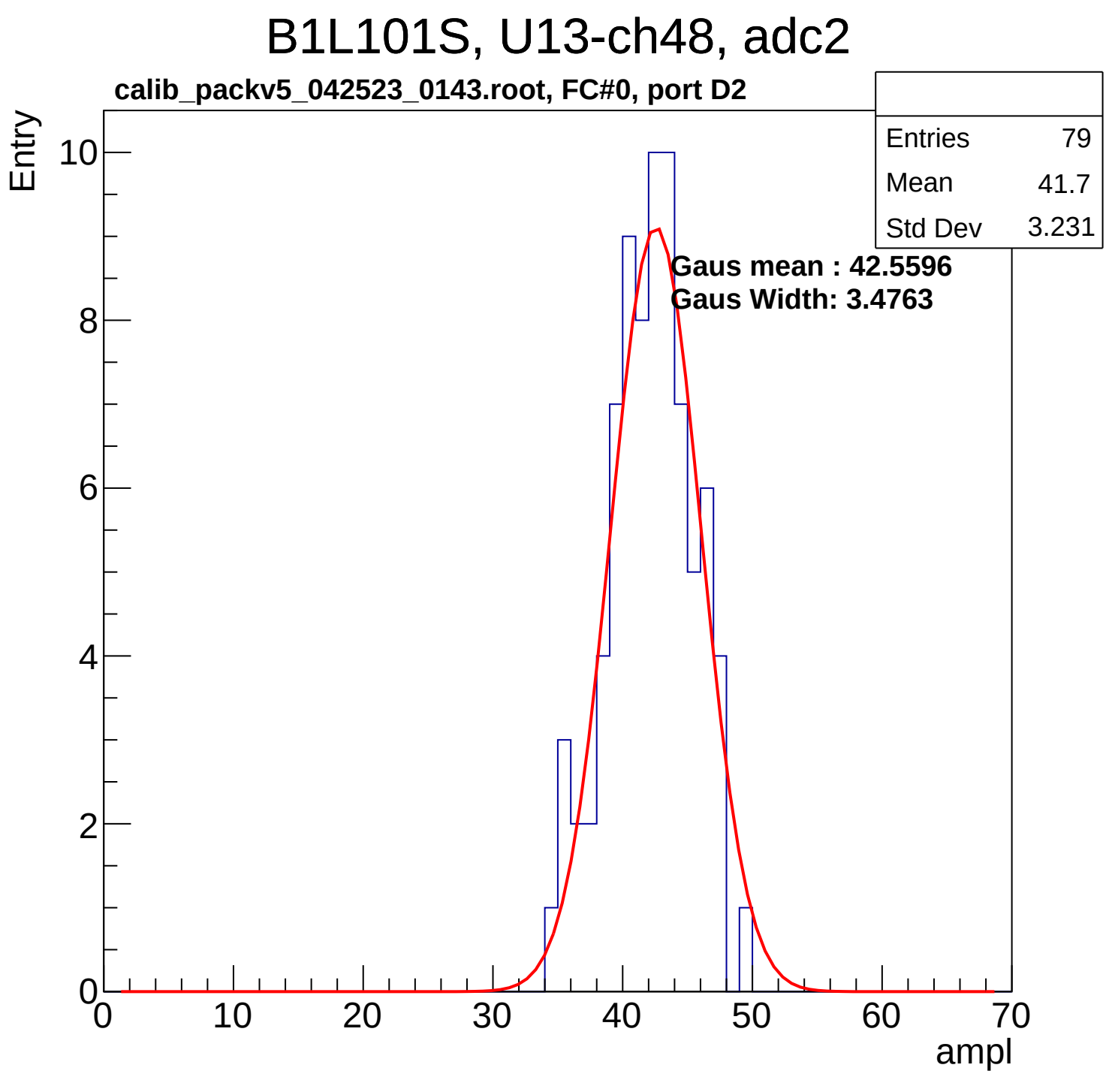
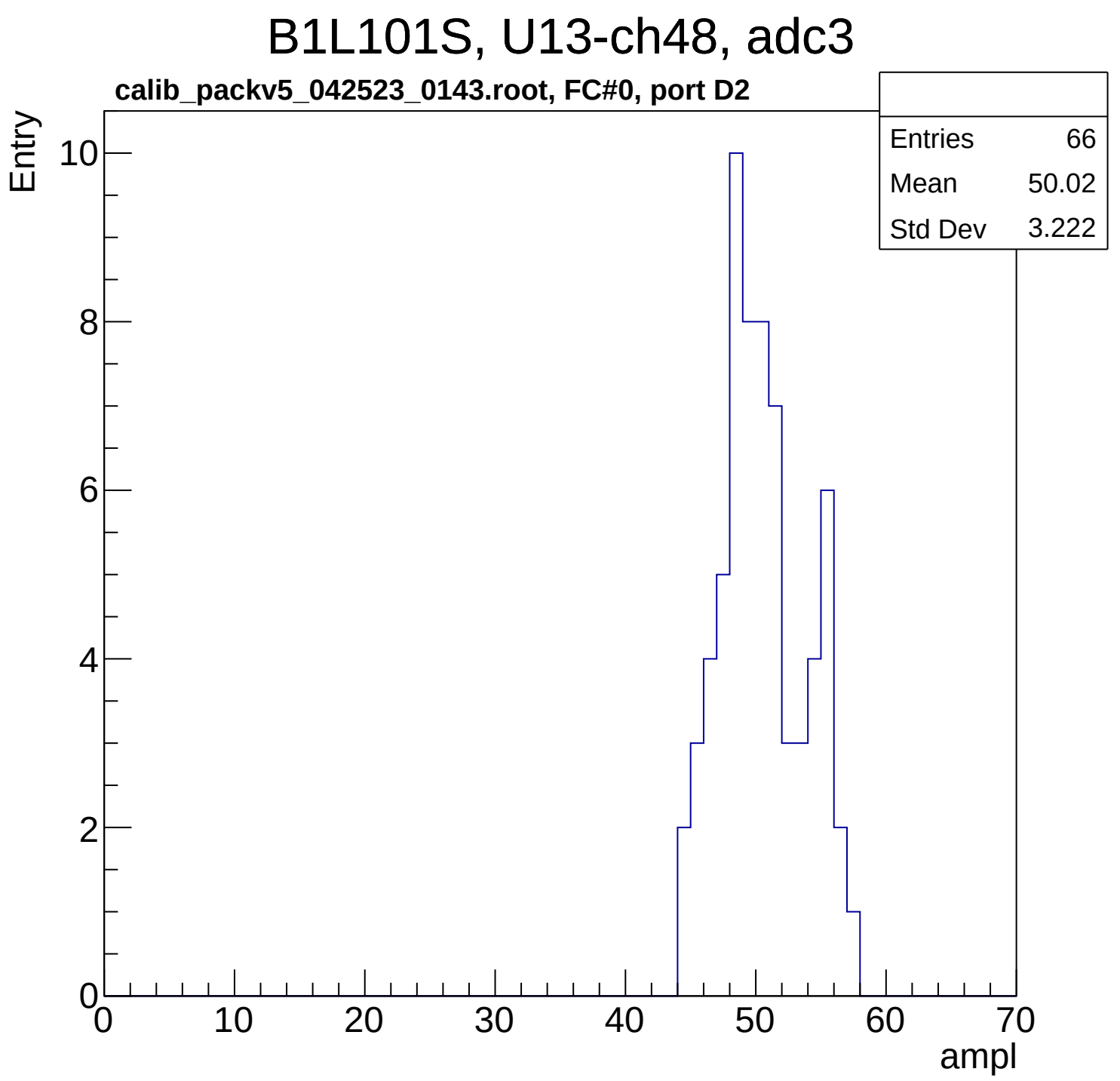
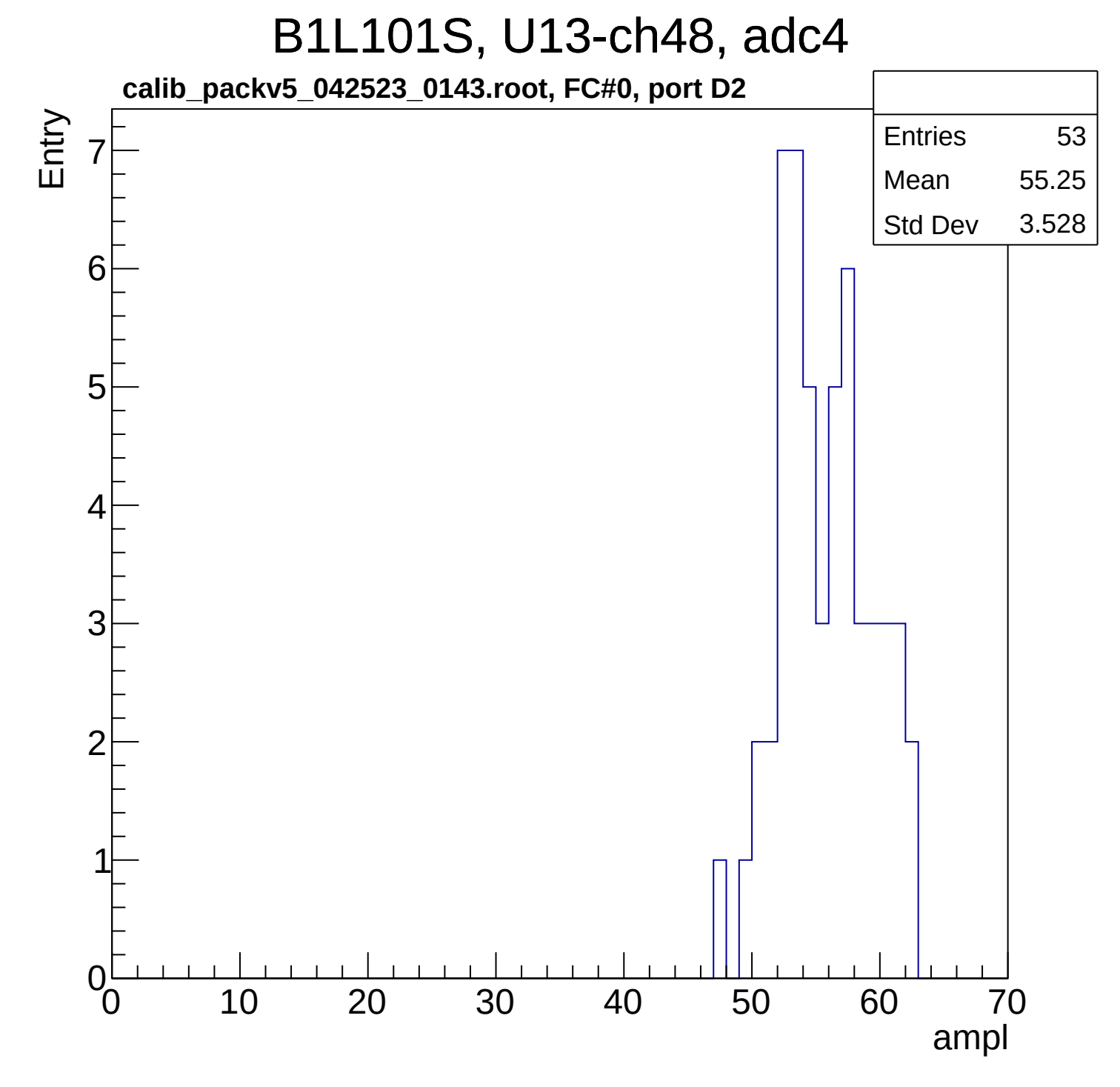
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch48, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 79
Mean 41.7
Std Dev 3.231
Gaus mean : 42.5596
Gaus Width: 3.4763
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch48, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 50.02
Std Dev 3.222
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch48, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 55.25
Std Dev 3.528
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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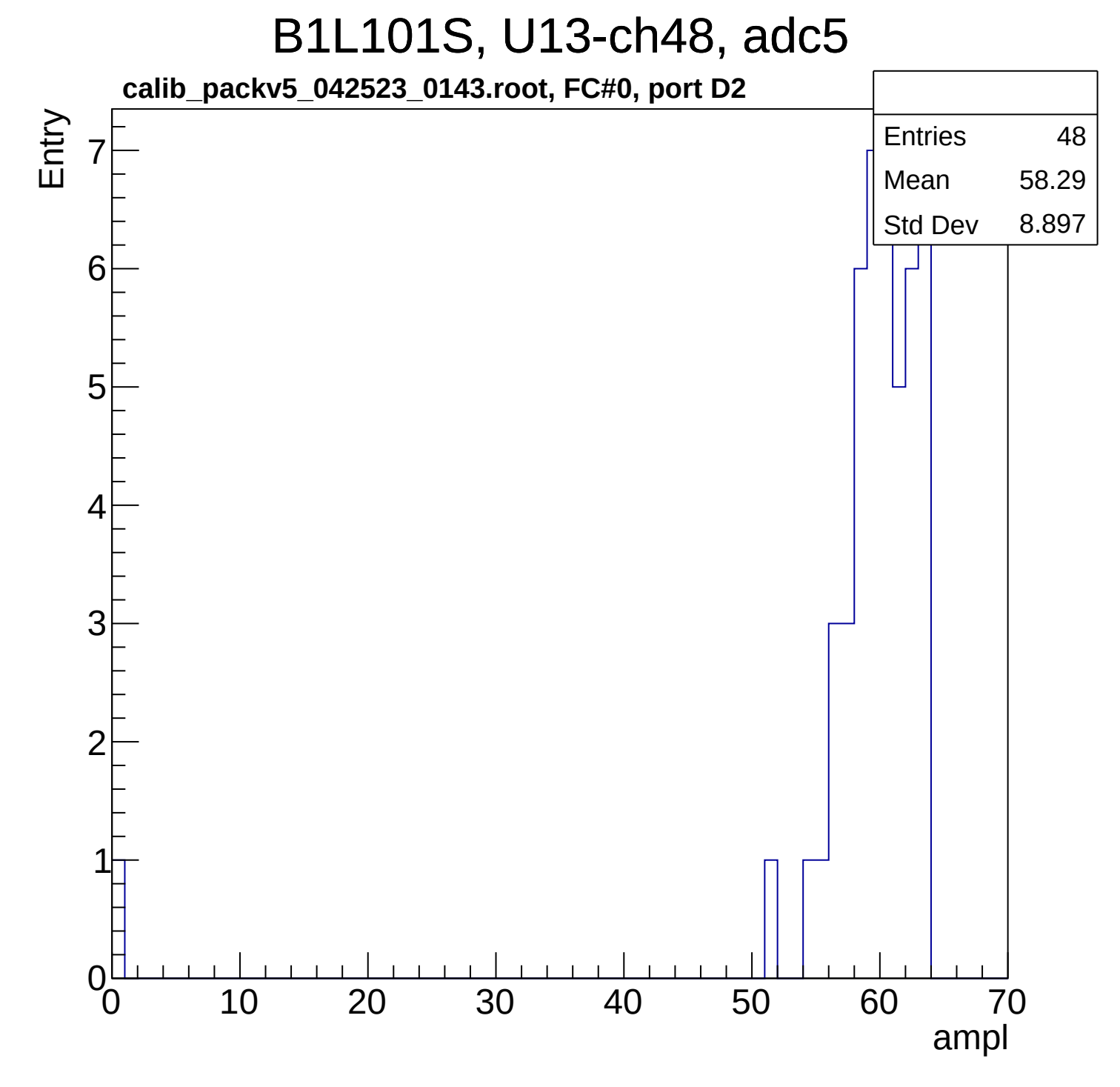
B1L101S, U13-ch48, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 58.29
Std Dev 8.897
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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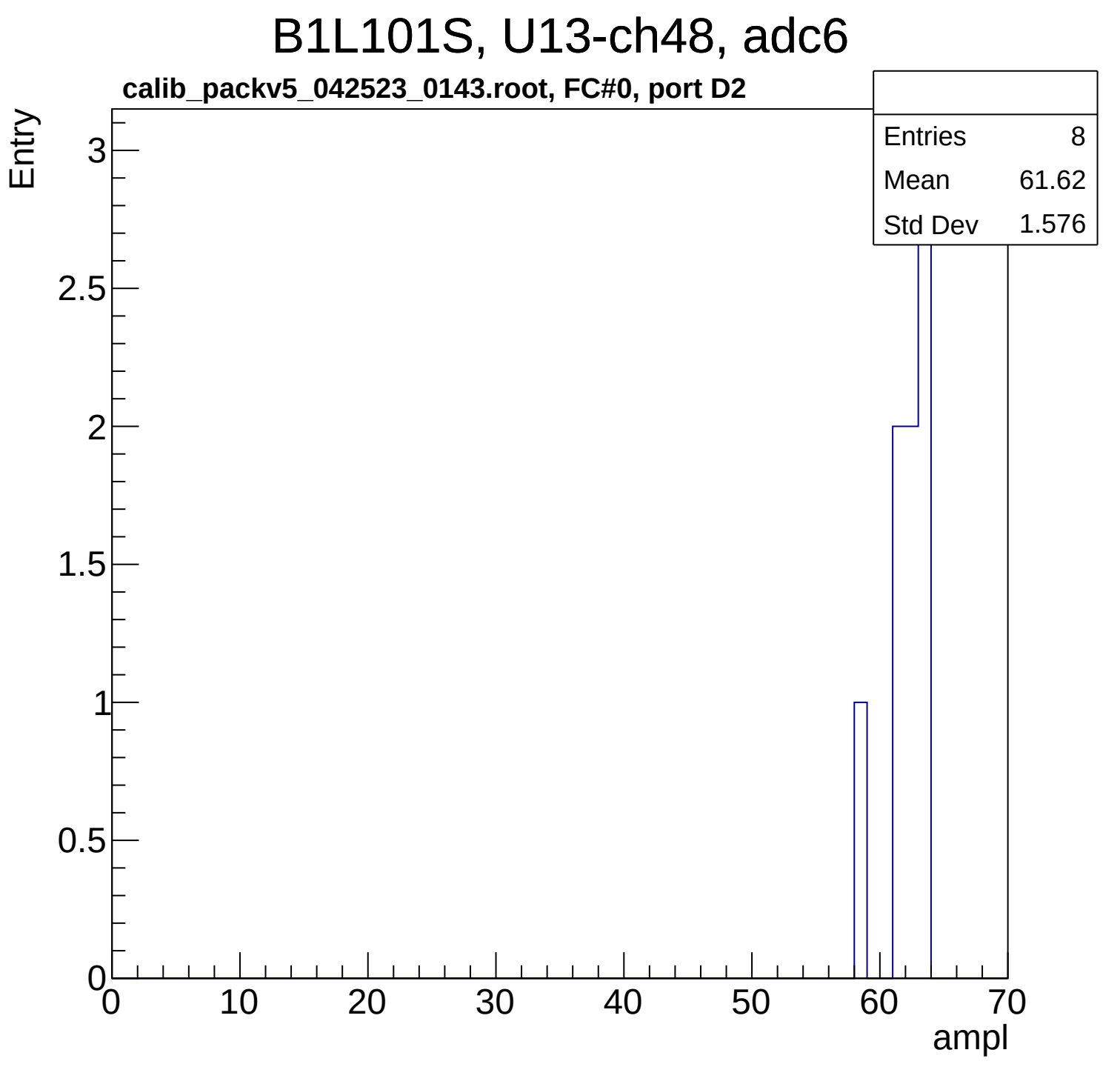
B1L101S, U13-ch48, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 8
Mean 61.62
Std Dev 1.576
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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60
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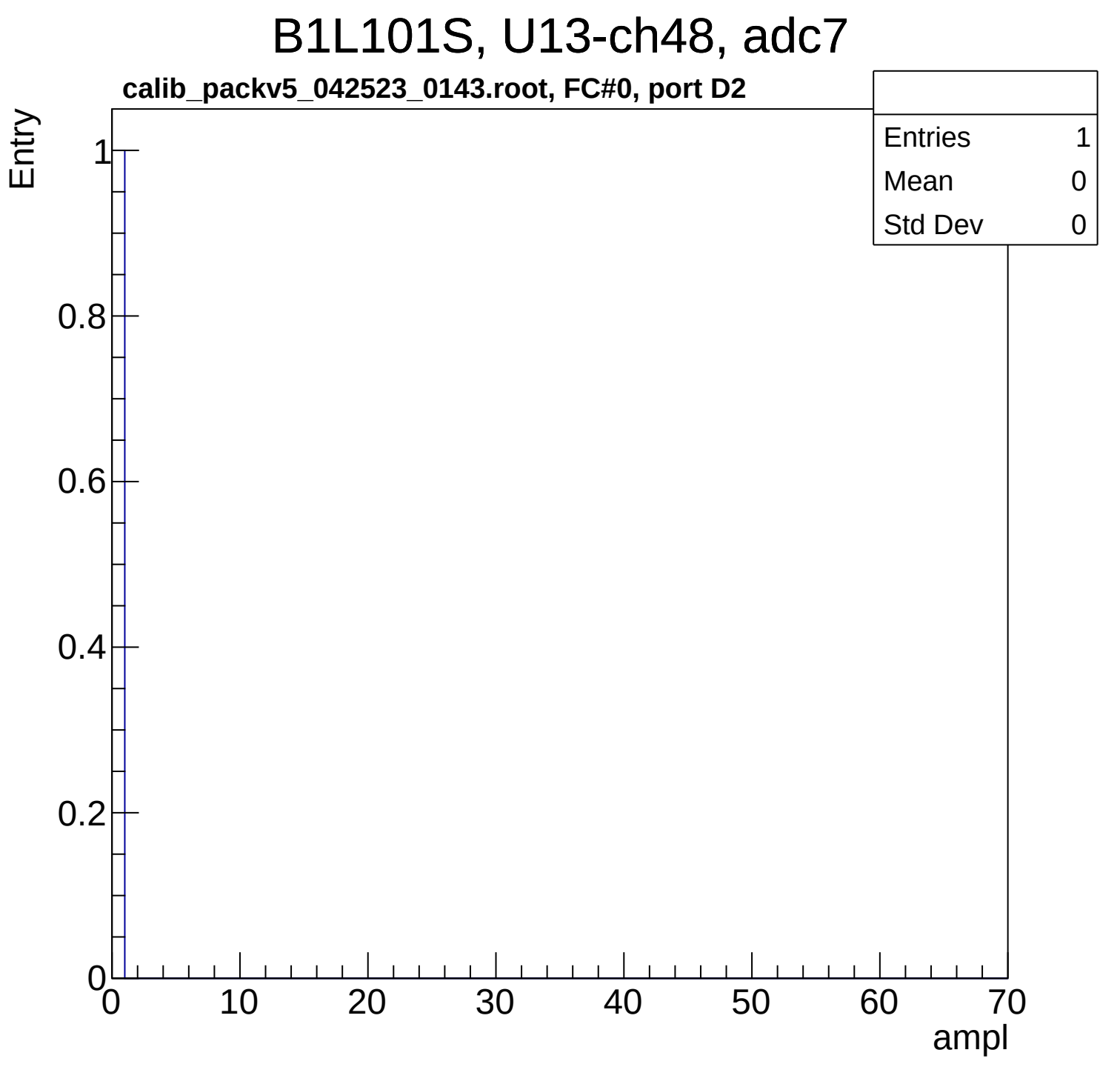
B1L101S, U13-ch48, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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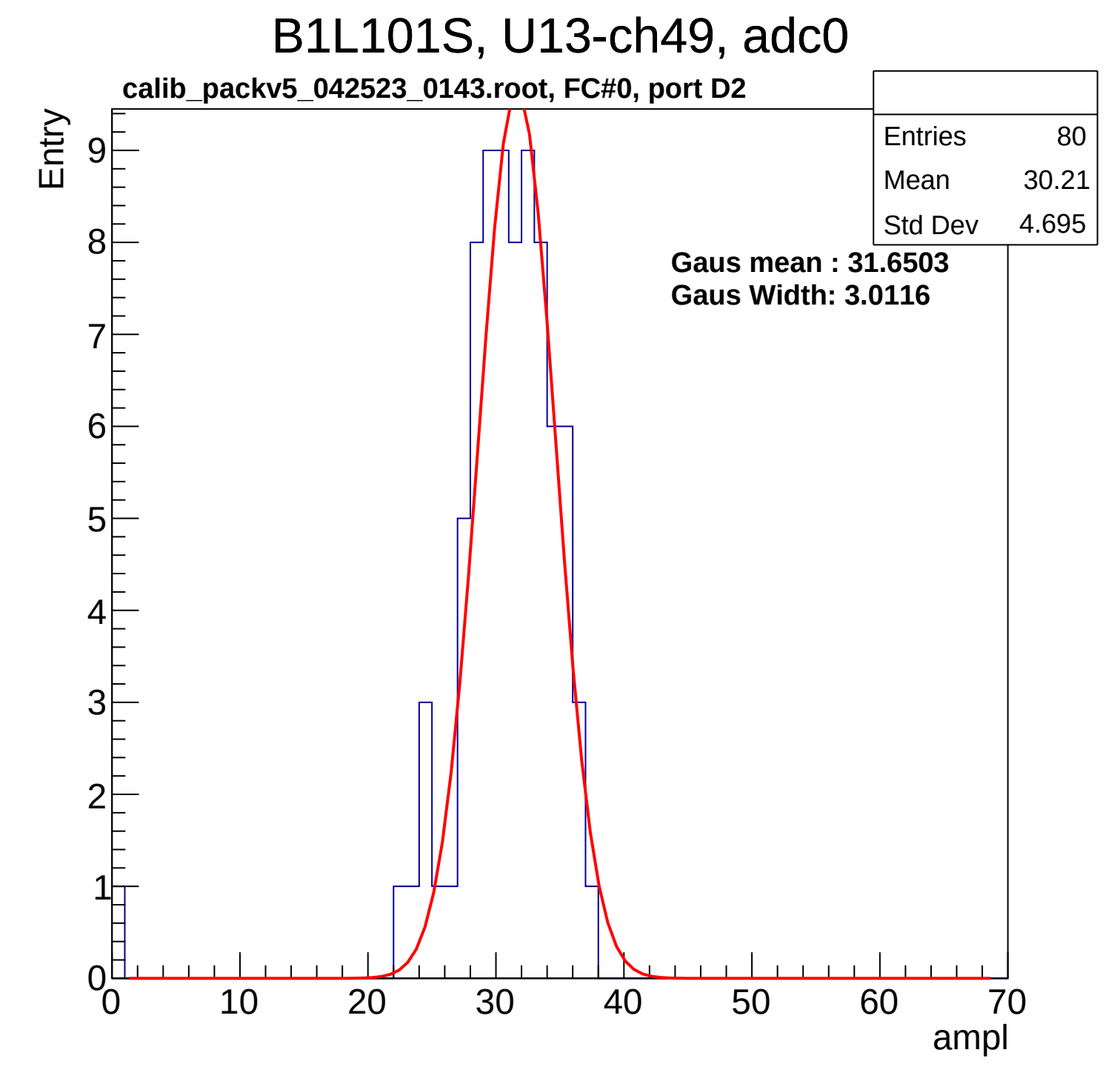
60
70
ampl


B1L101S, U13-ch49, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 30.21
Std Dev 4.695
Gaus mean : 31.6503
Gaus Width: 3.0116
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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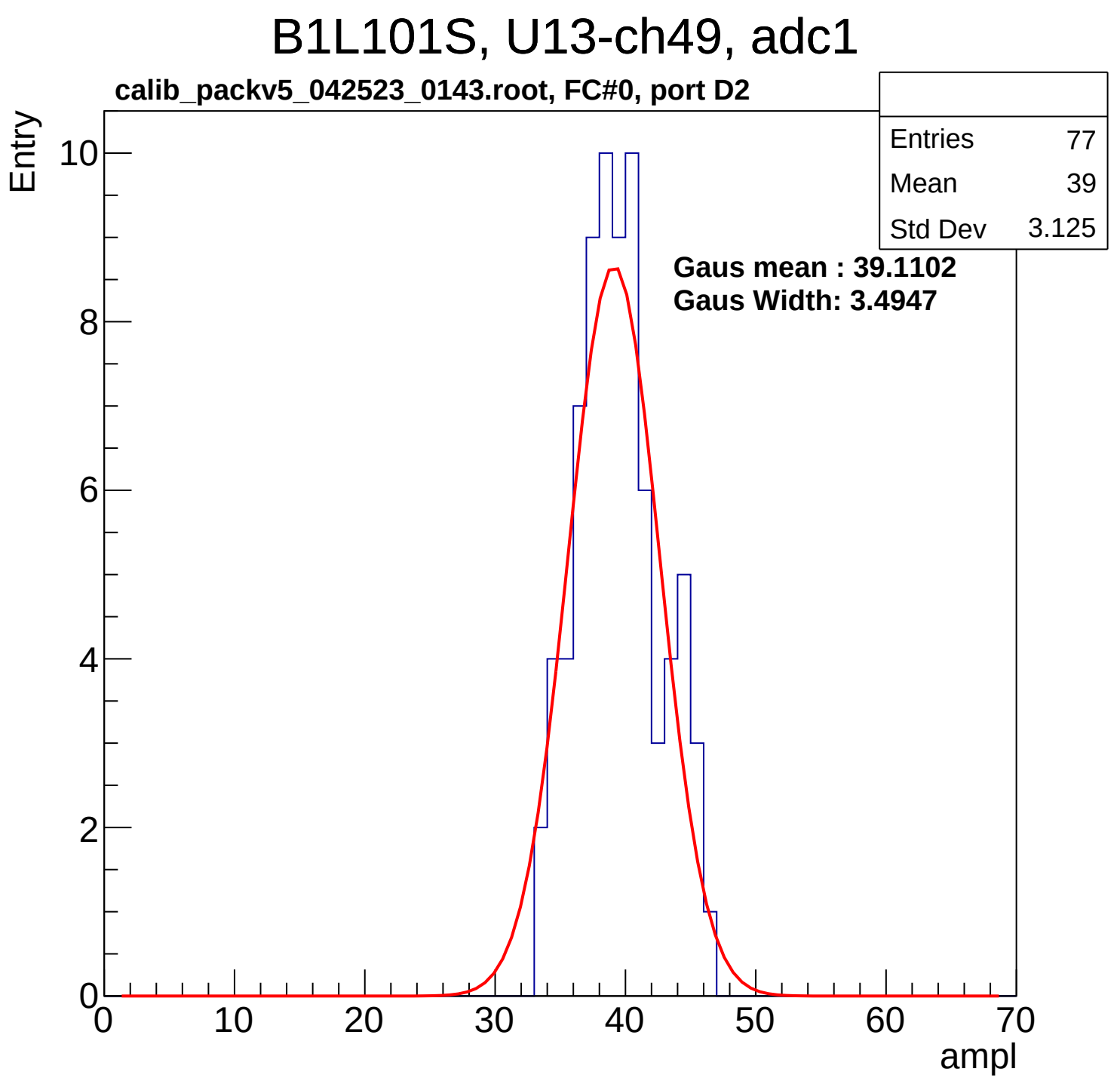
B1L101S, U13-ch49, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 39
Std Dev 3.125
Gaus mean : 39.1102
Gaus Width: 3.4947
Entry
ampl
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4
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8
10
0
10
20
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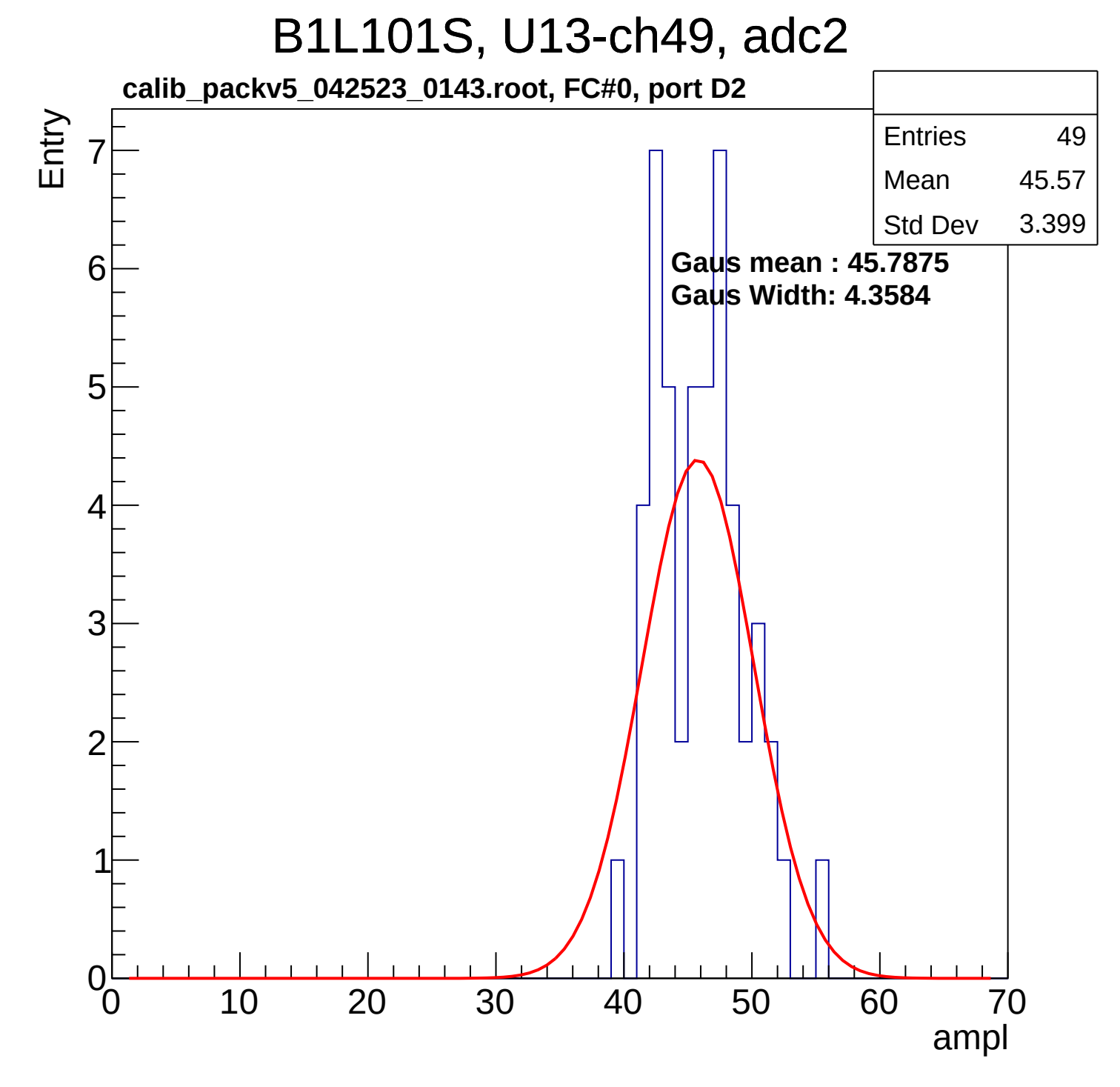
40
50
60
70


B1L101S, U13-ch49, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 45.57
Std Dev 3.399
Gaus mean : 45.7875
Gaus Width: 4.3584
Entry
ampl
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2
3
4
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6
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0
10
20
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40
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60
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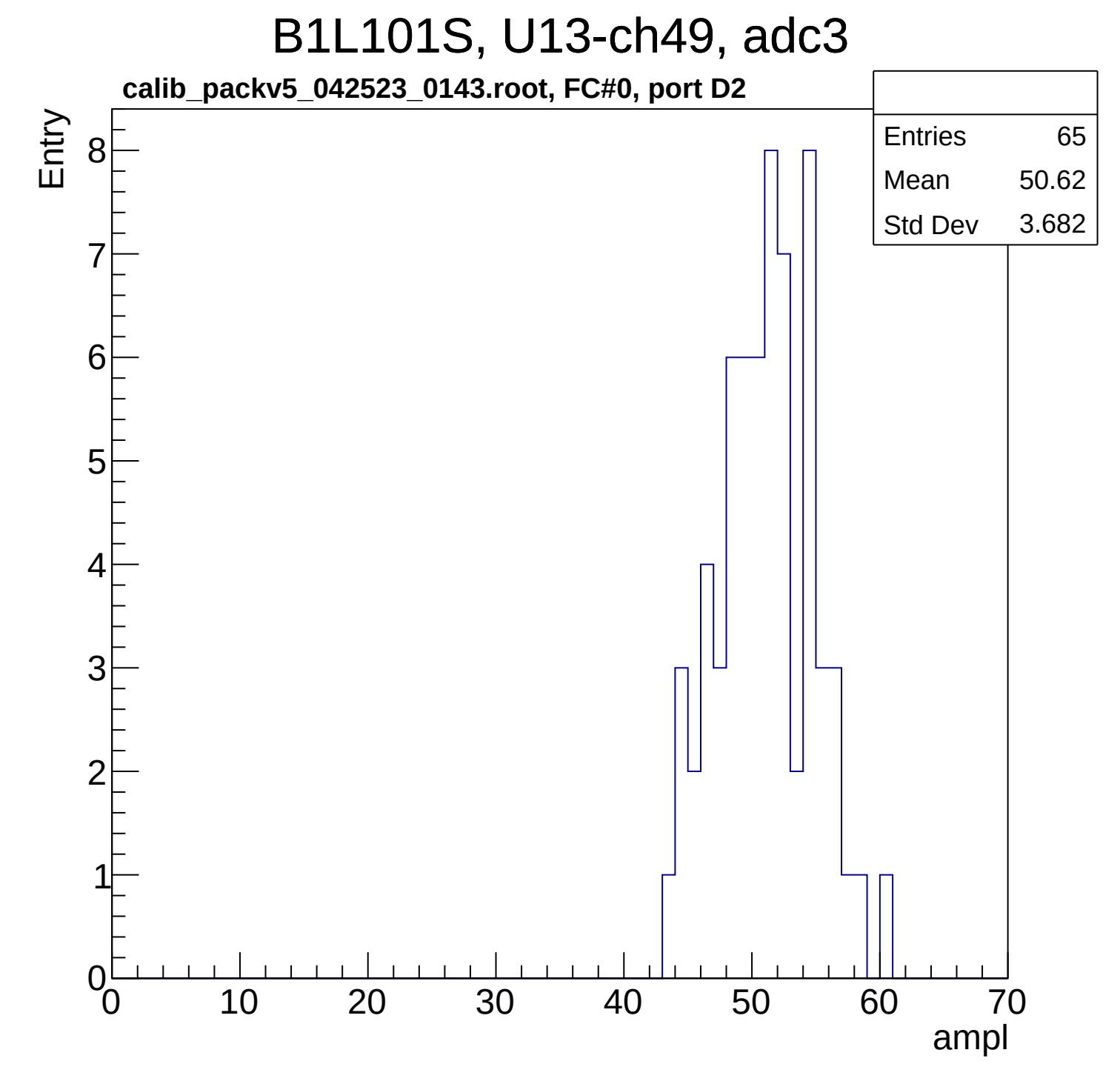
B1L101S, U13-ch49, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 50.62
Std Dev 3.682
Entry
ampl
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3
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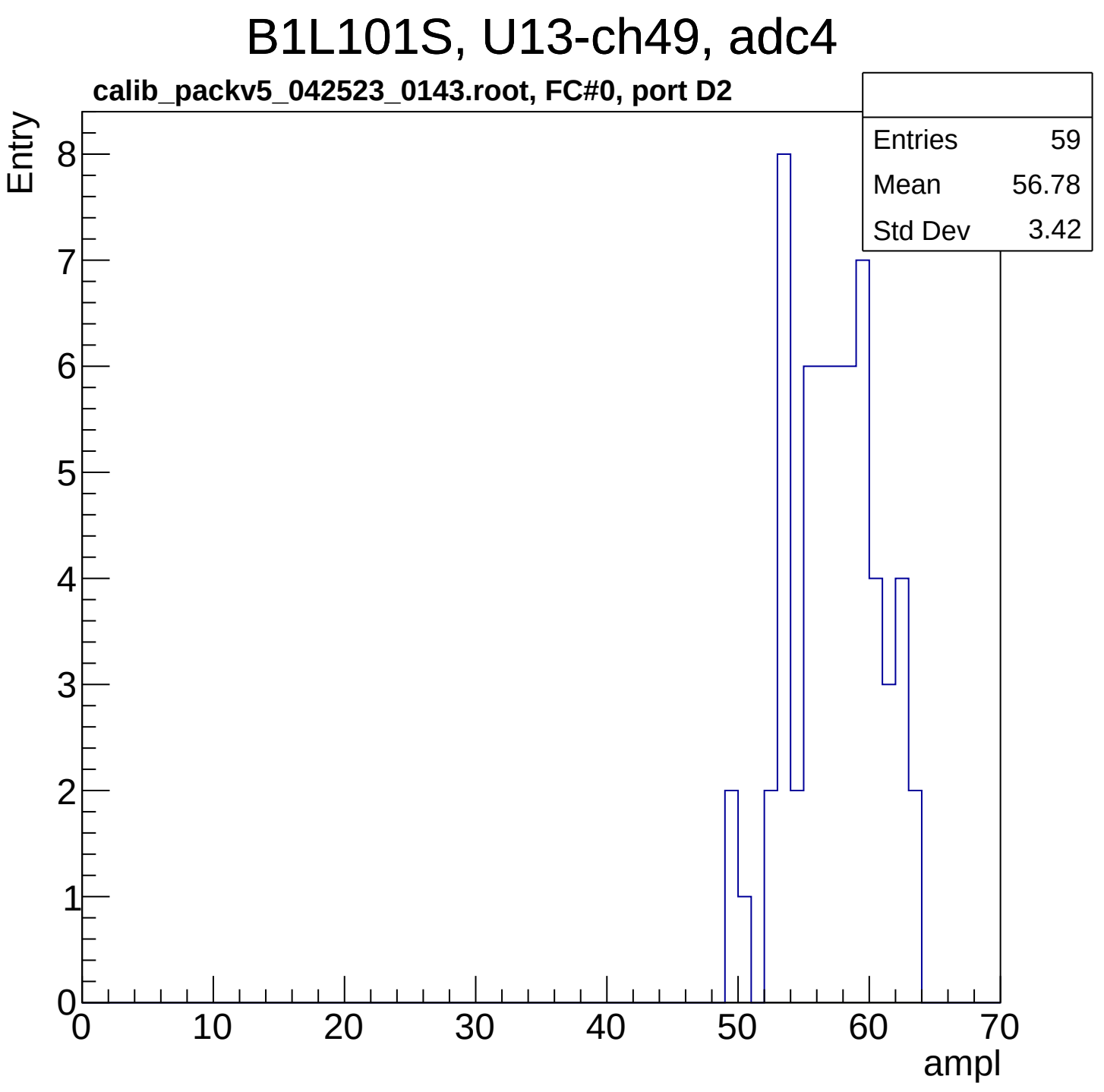
B1L101S, U13-ch49, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 56.78
Std Dev 3.42
Entry
ampl
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1
2
3
4
5
6
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0
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60
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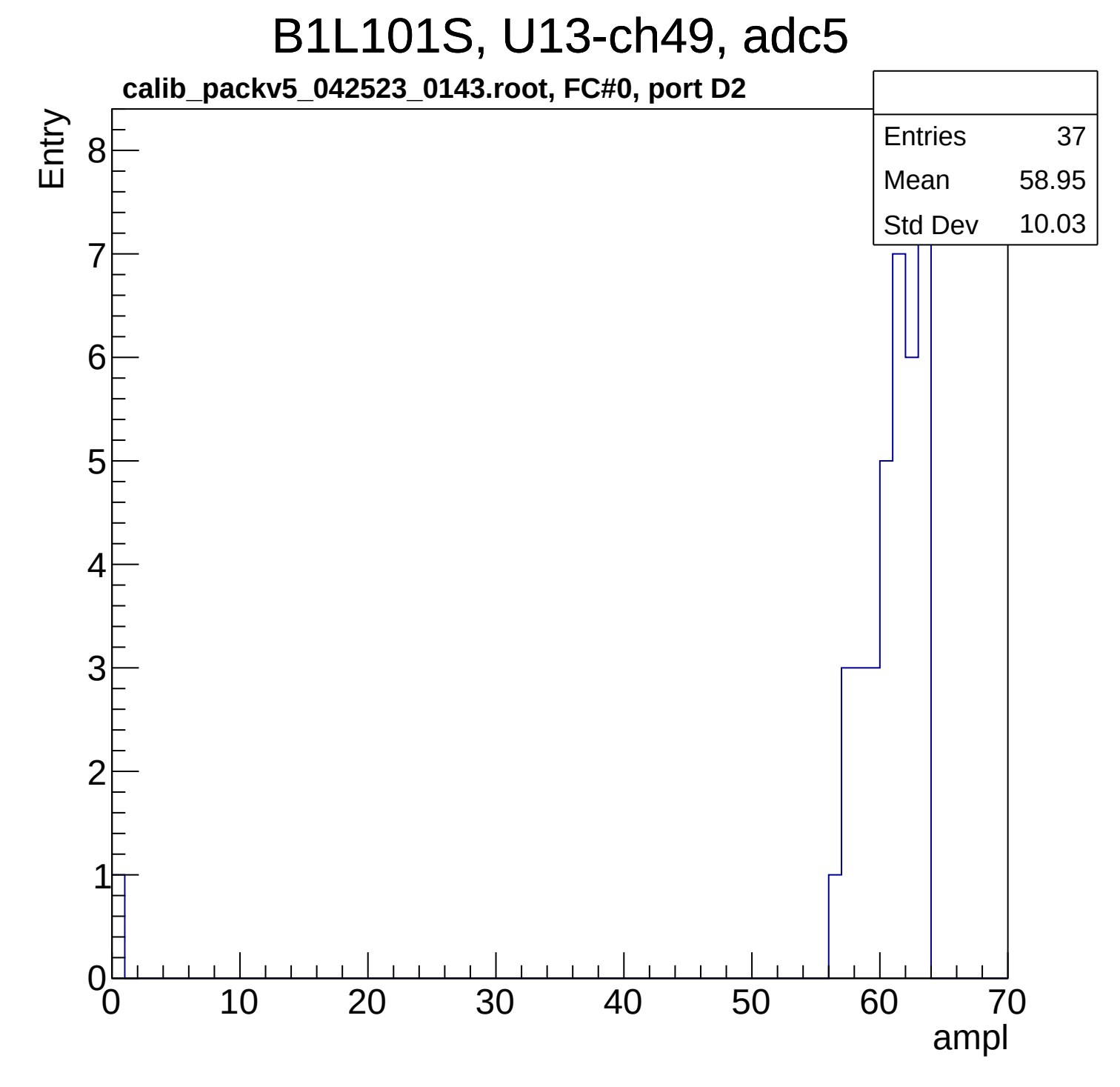
B1L101S, U13-ch49, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 37
Mean 58.95
Std Dev 10.03
Entry
ampl
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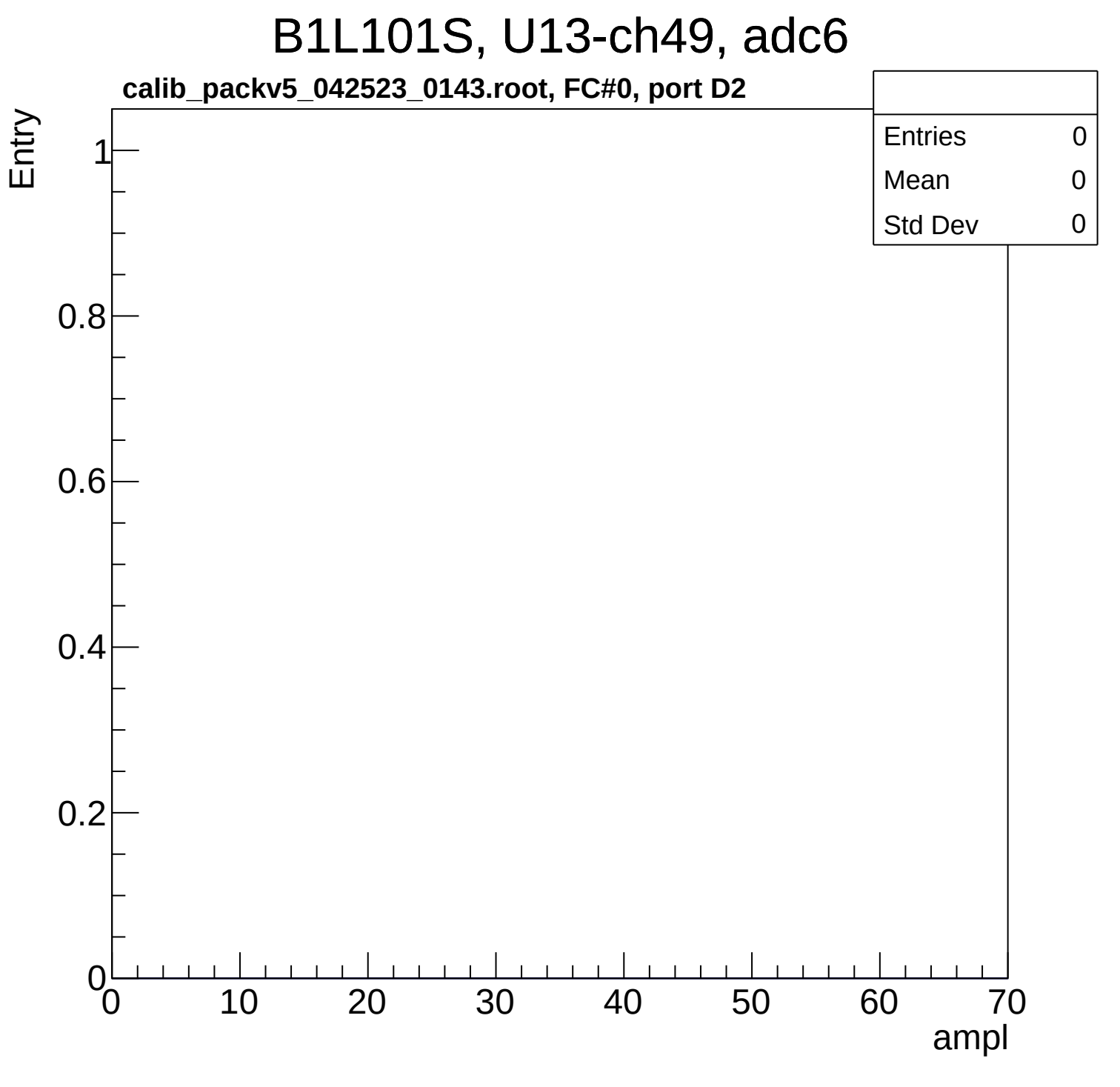
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch49, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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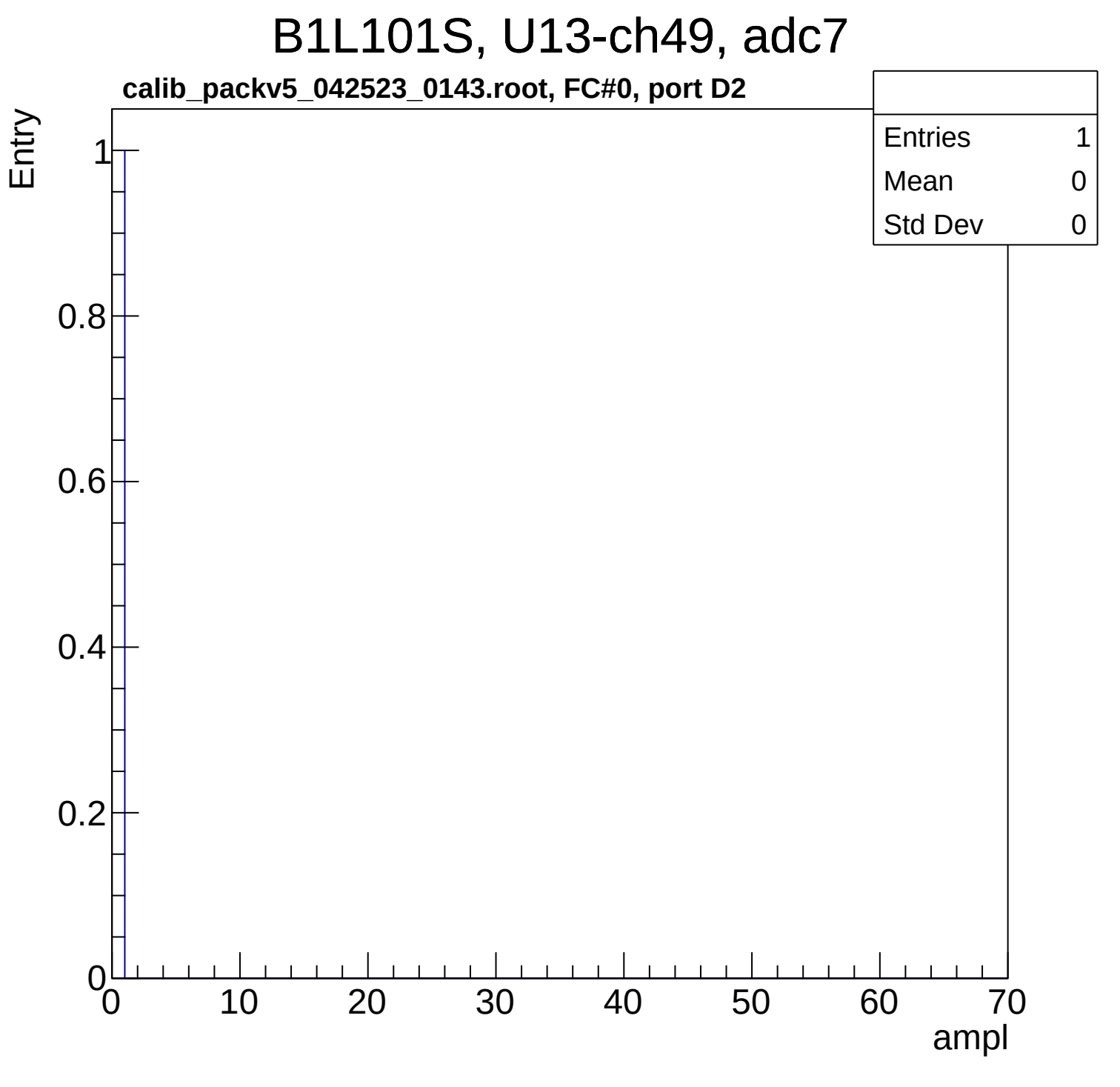
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch49, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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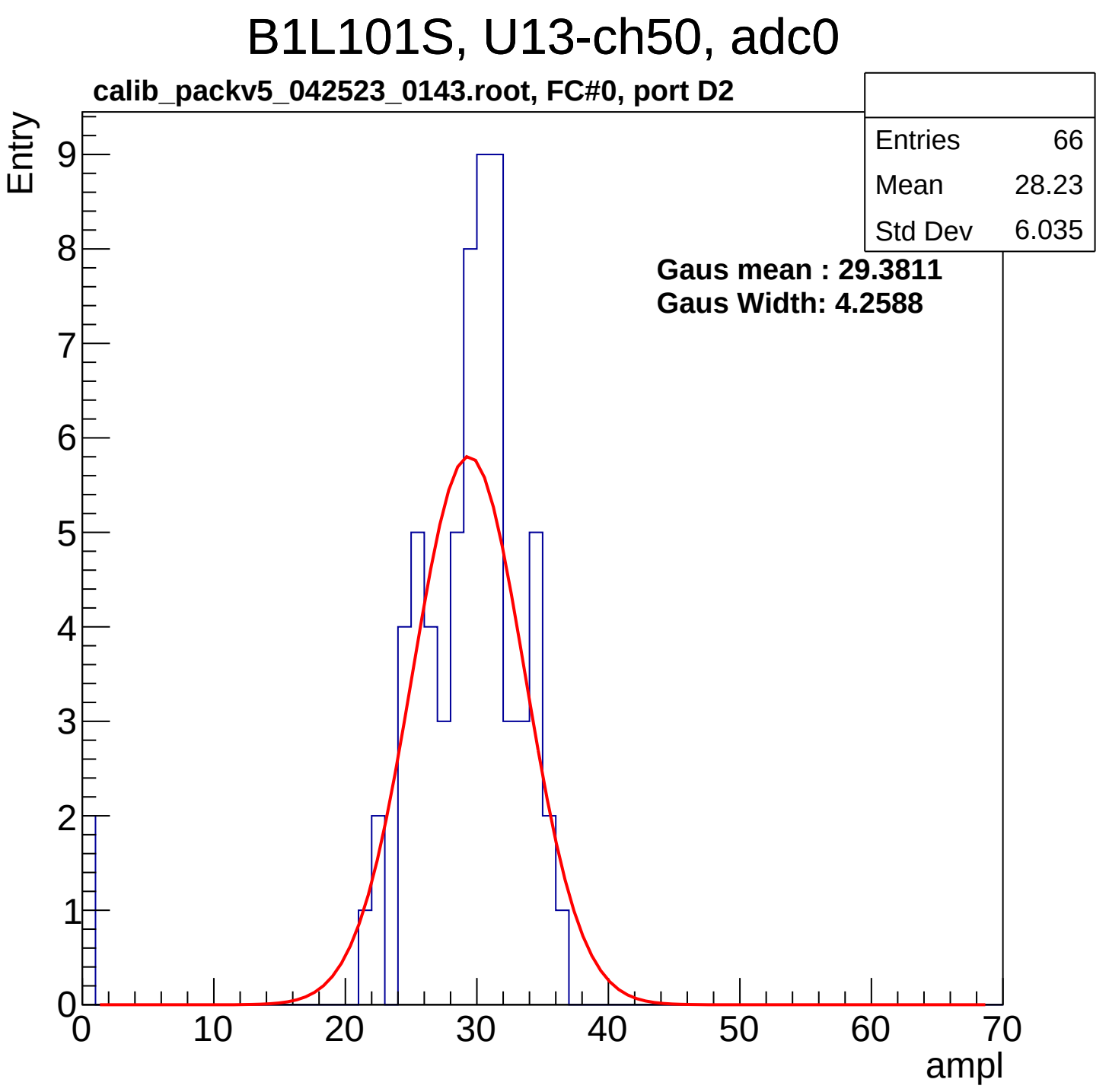
50
60
70
ampl


B1L101S, U13-ch50, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 28.23
Std Dev 6.035
Gaus mean : 29.3811
Gaus Width: 4.2588
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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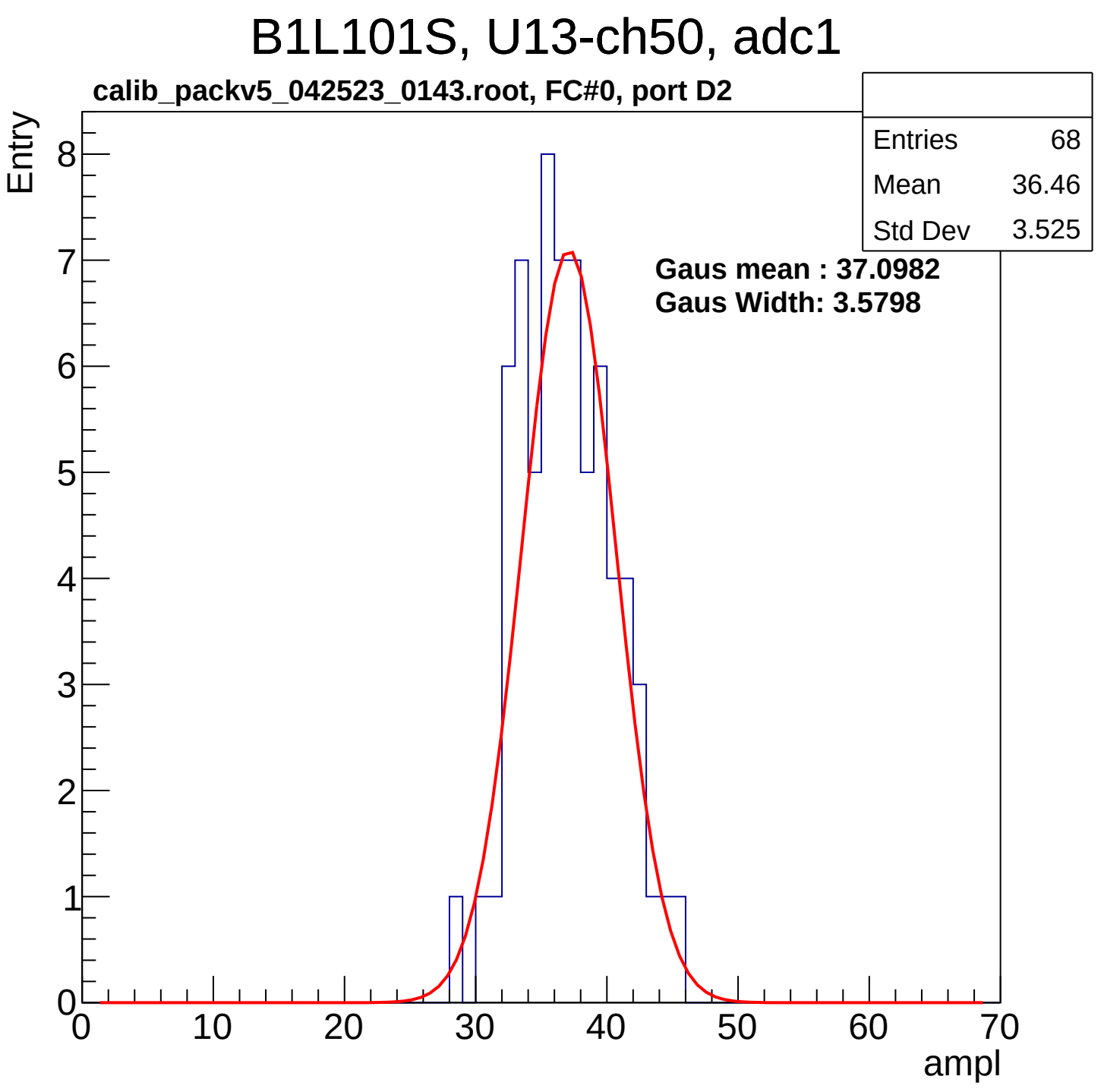
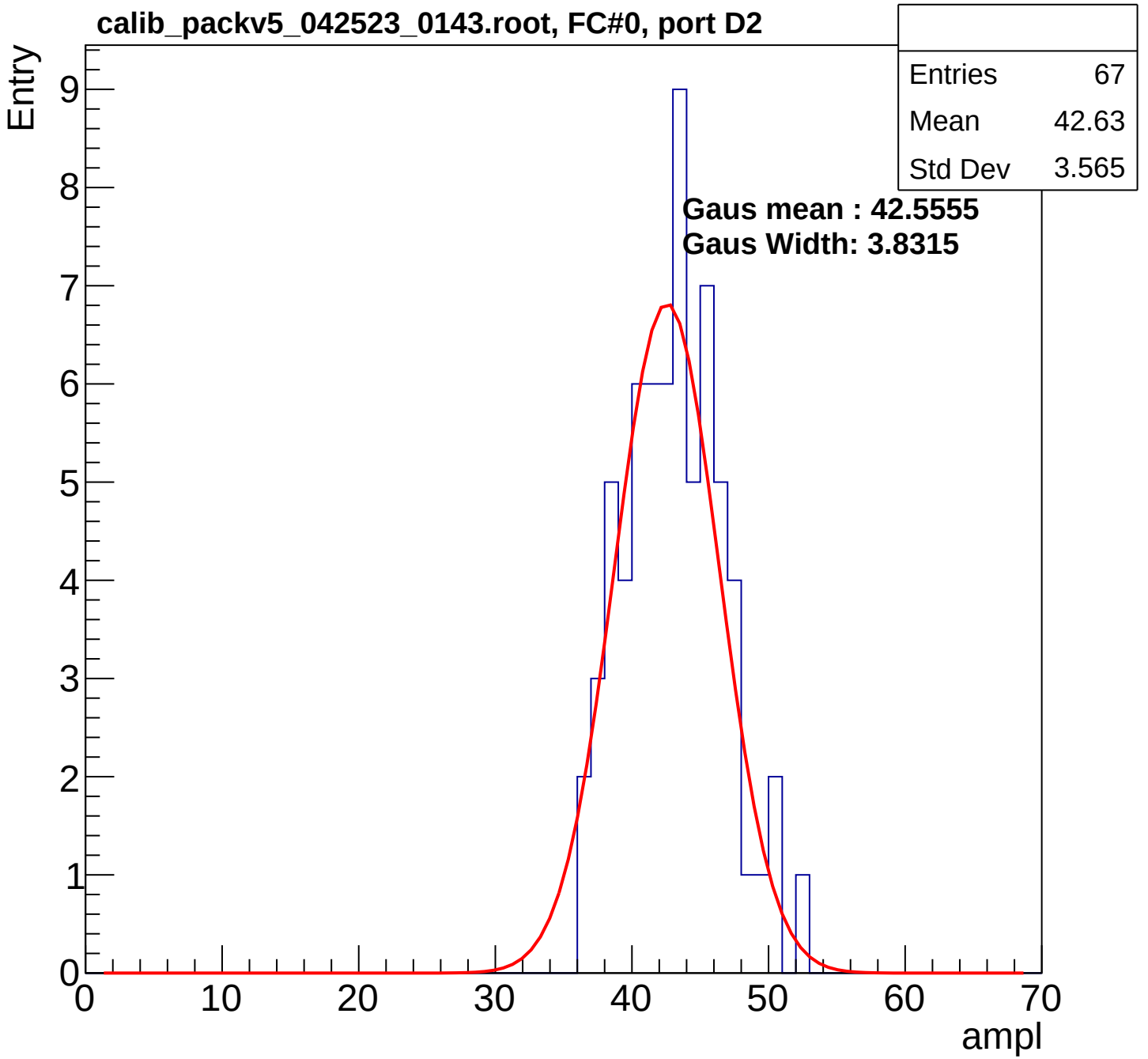

B1L101S, U13-ch50, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 36.46
Std Dev 3.525
Gaus mean : 37.0982
Gaus Width: 3.5798
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch50, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 42.63
Std Dev 3.565
Gaus mean : 42.5555
Gaus Width: 3.8315
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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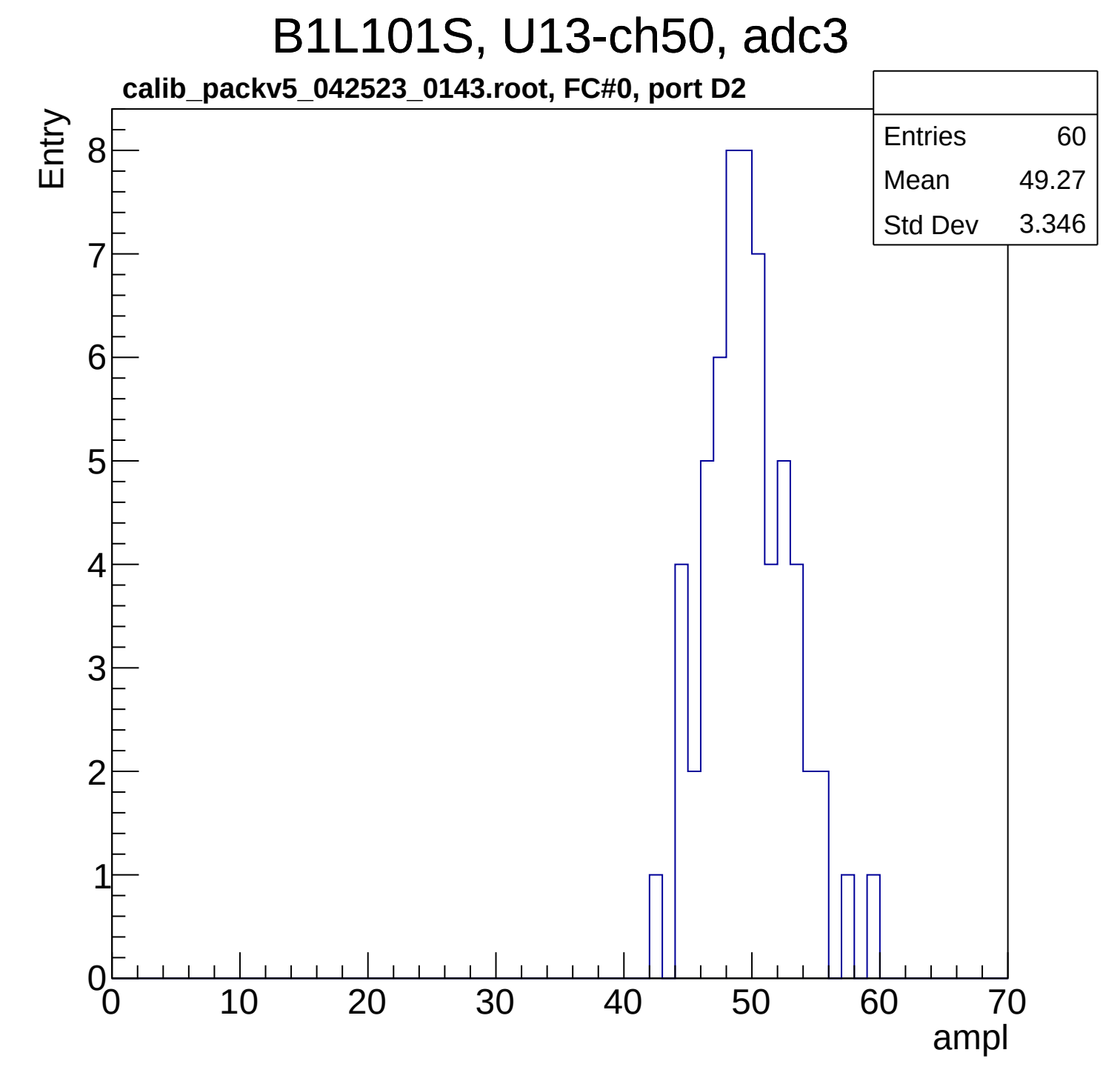
B1L101S, U13-ch50, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 49.27
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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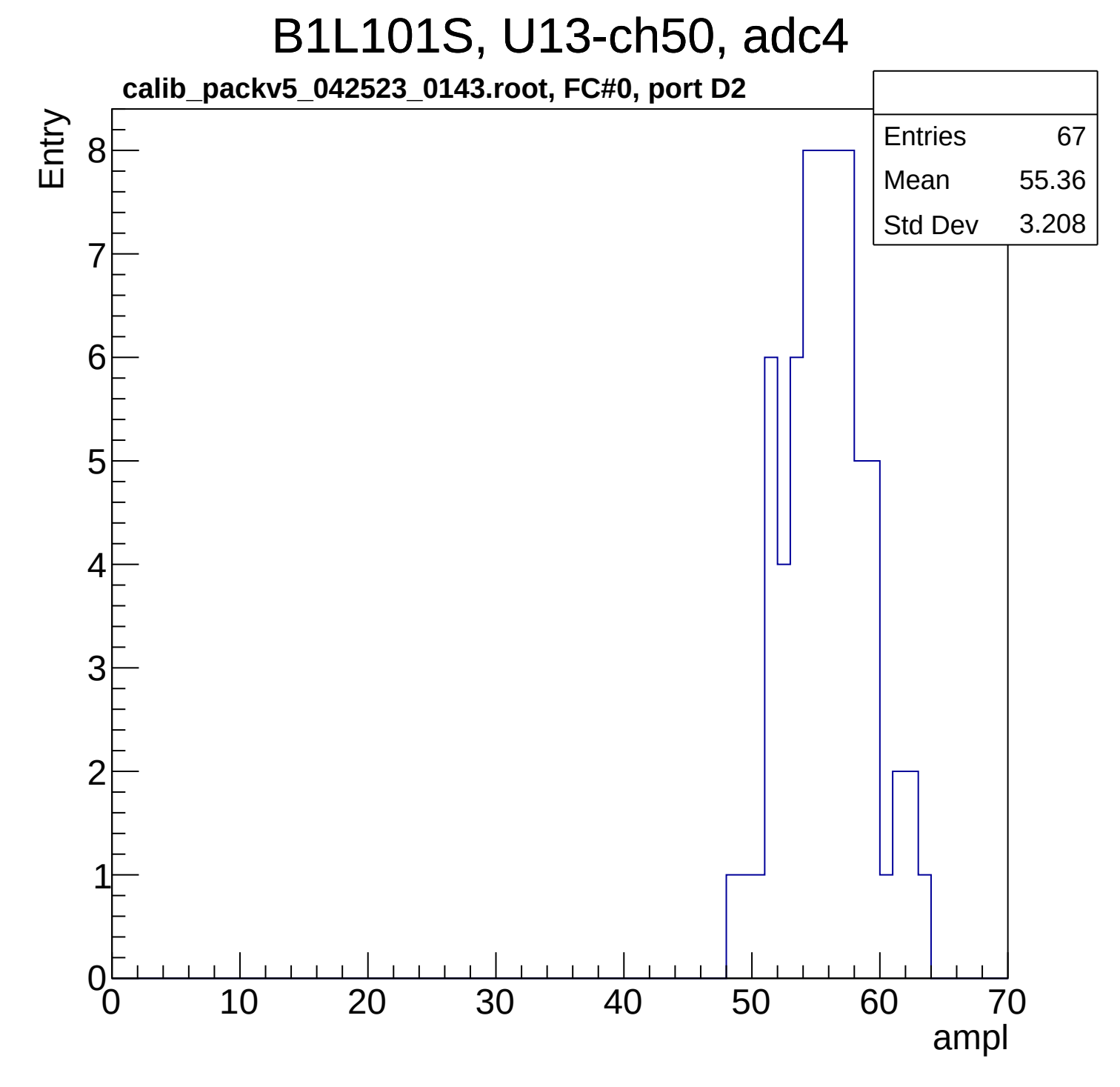
B1L101S, U13-ch50, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 55.36
Std Dev 3.208
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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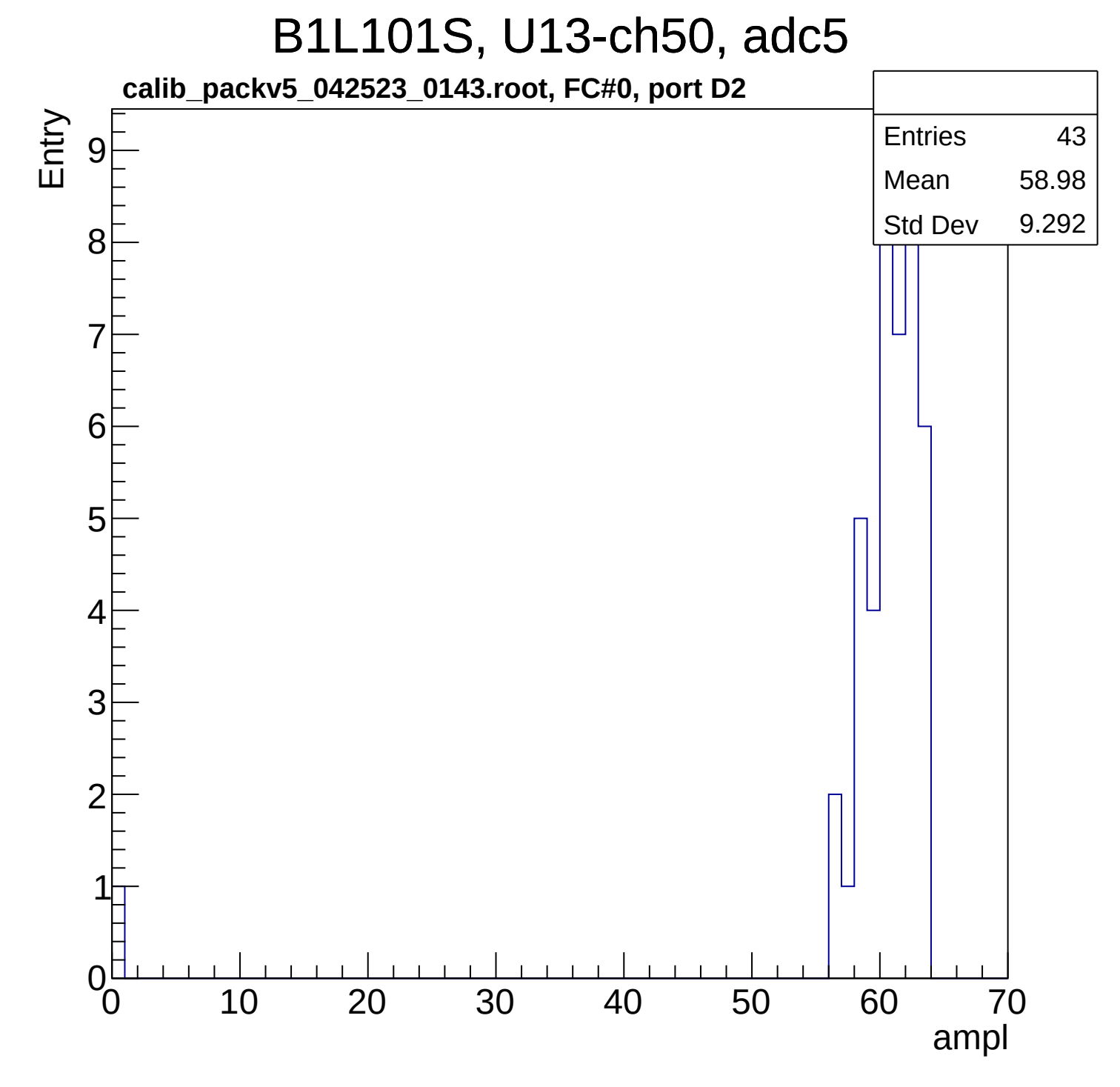
B1L101S, U13-ch50, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 43
Mean 58.98
Std Dev 9.292
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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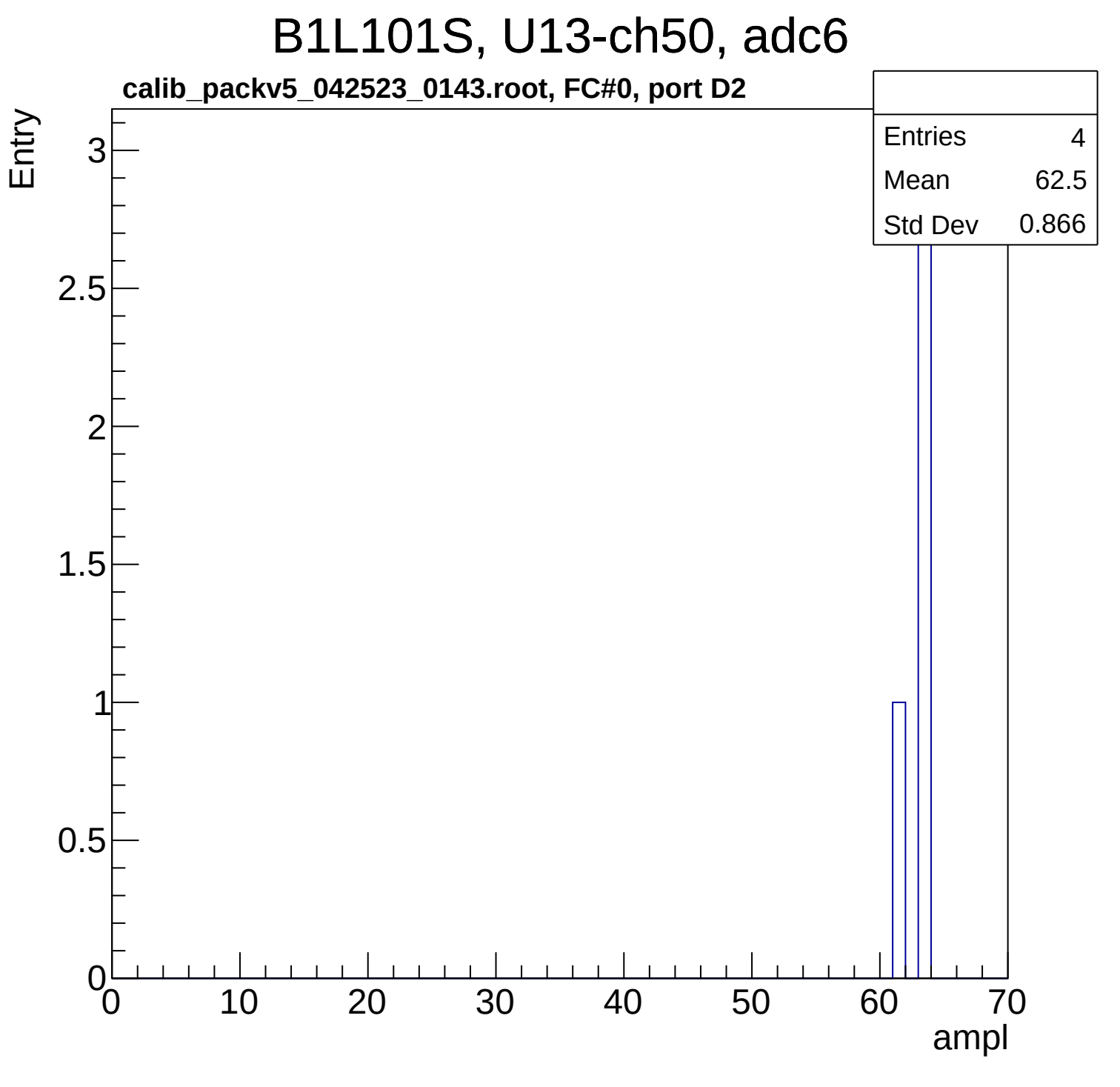
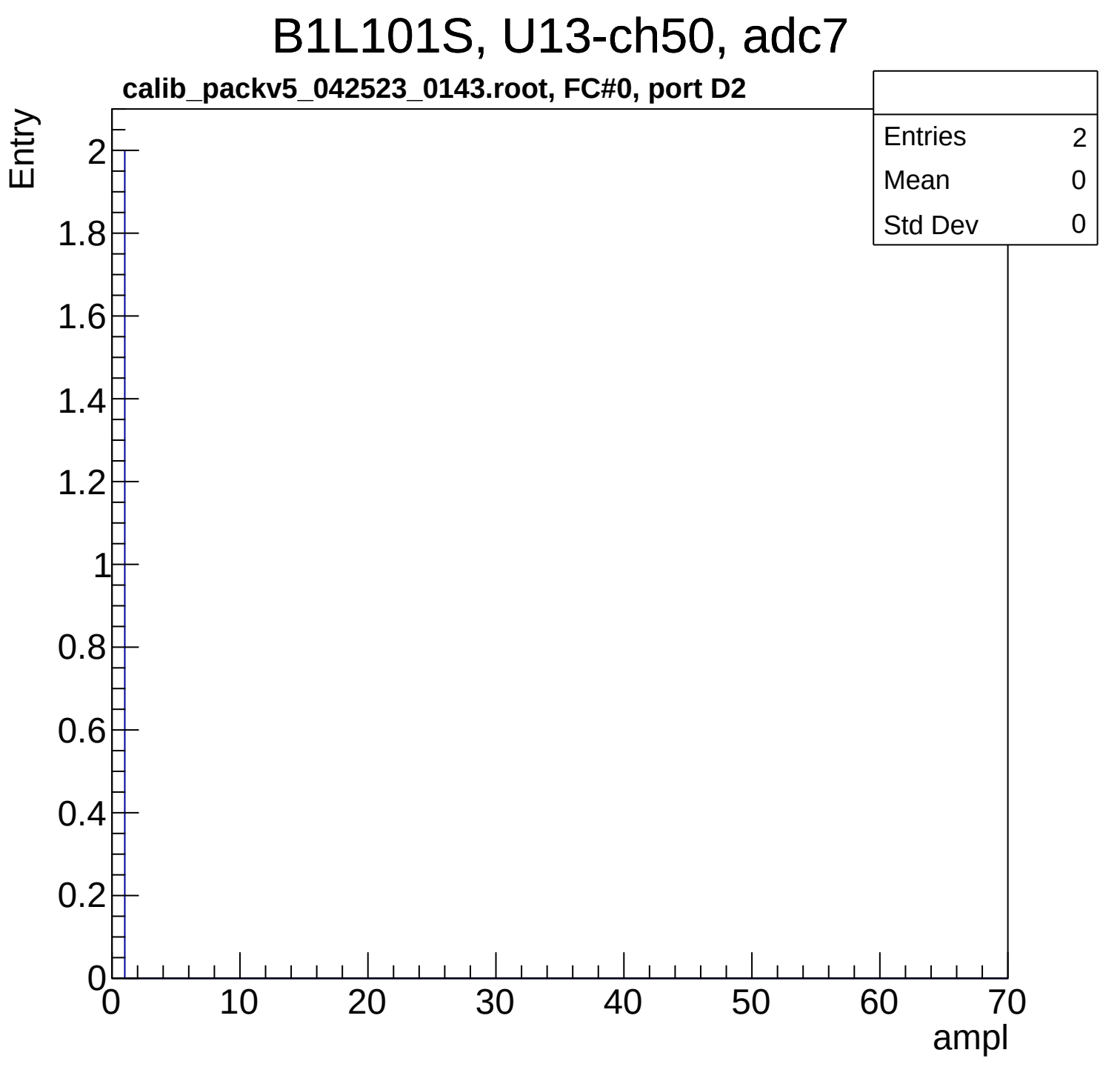

B1L101S, U13-ch50, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch50, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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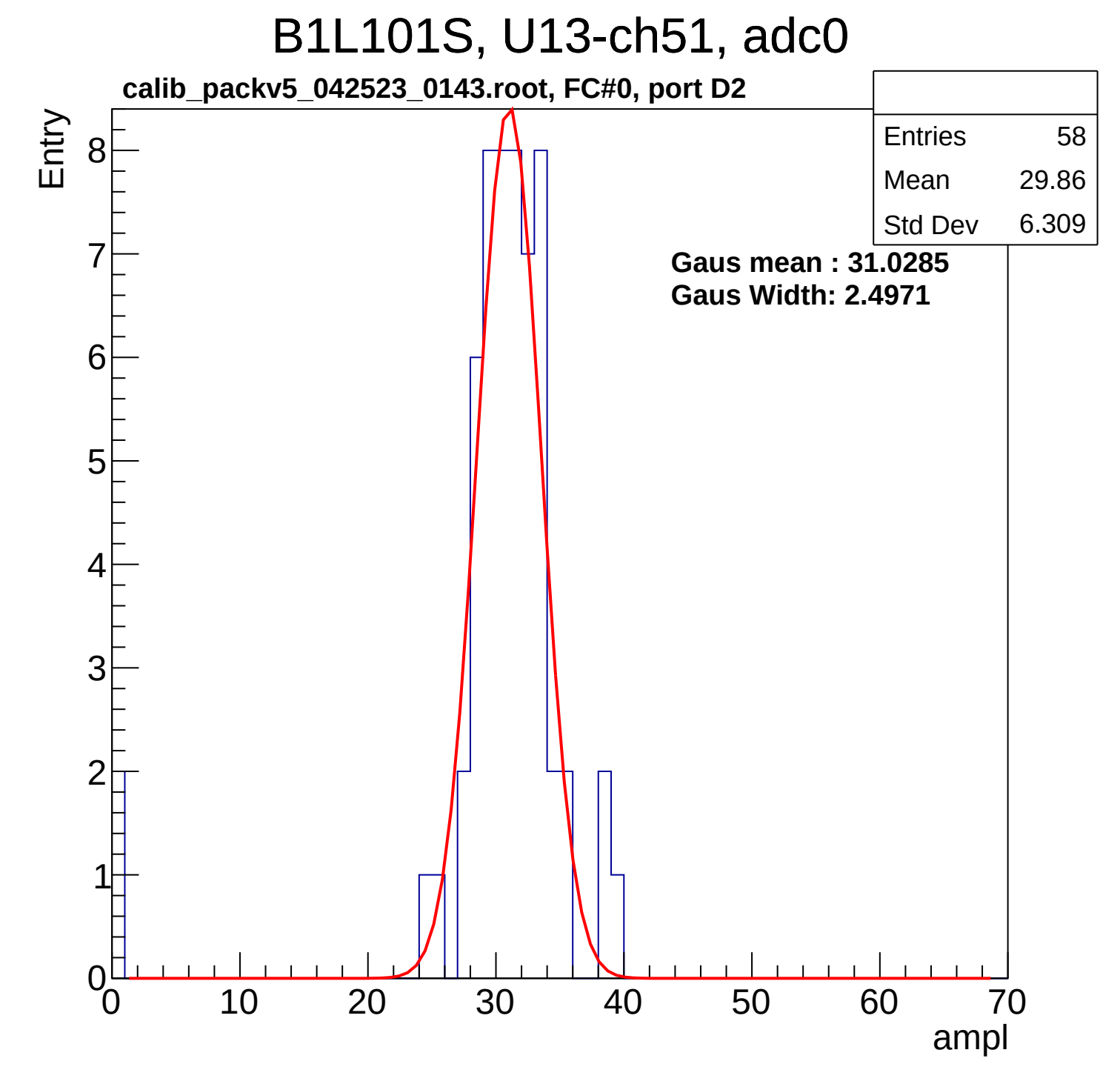
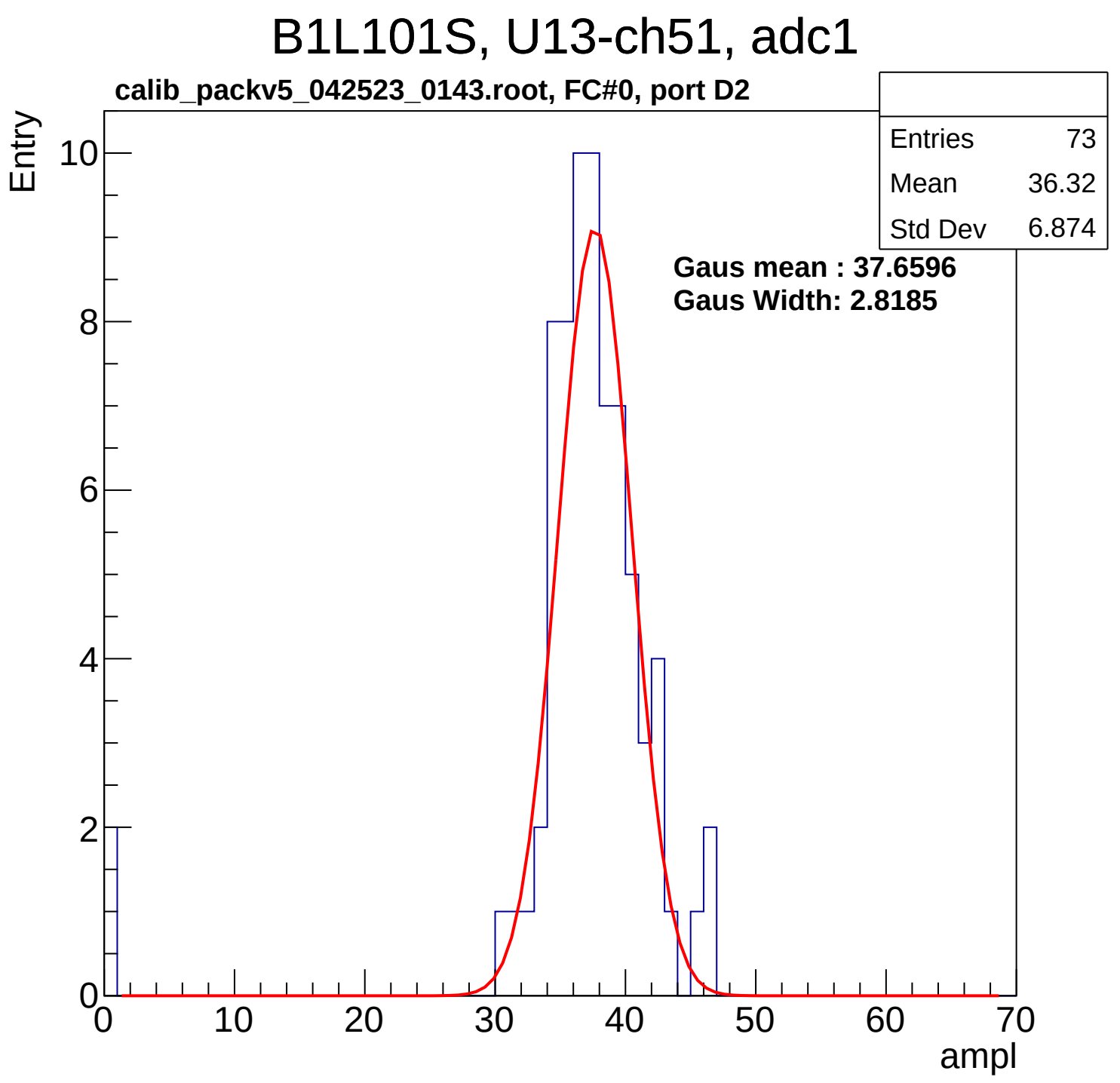
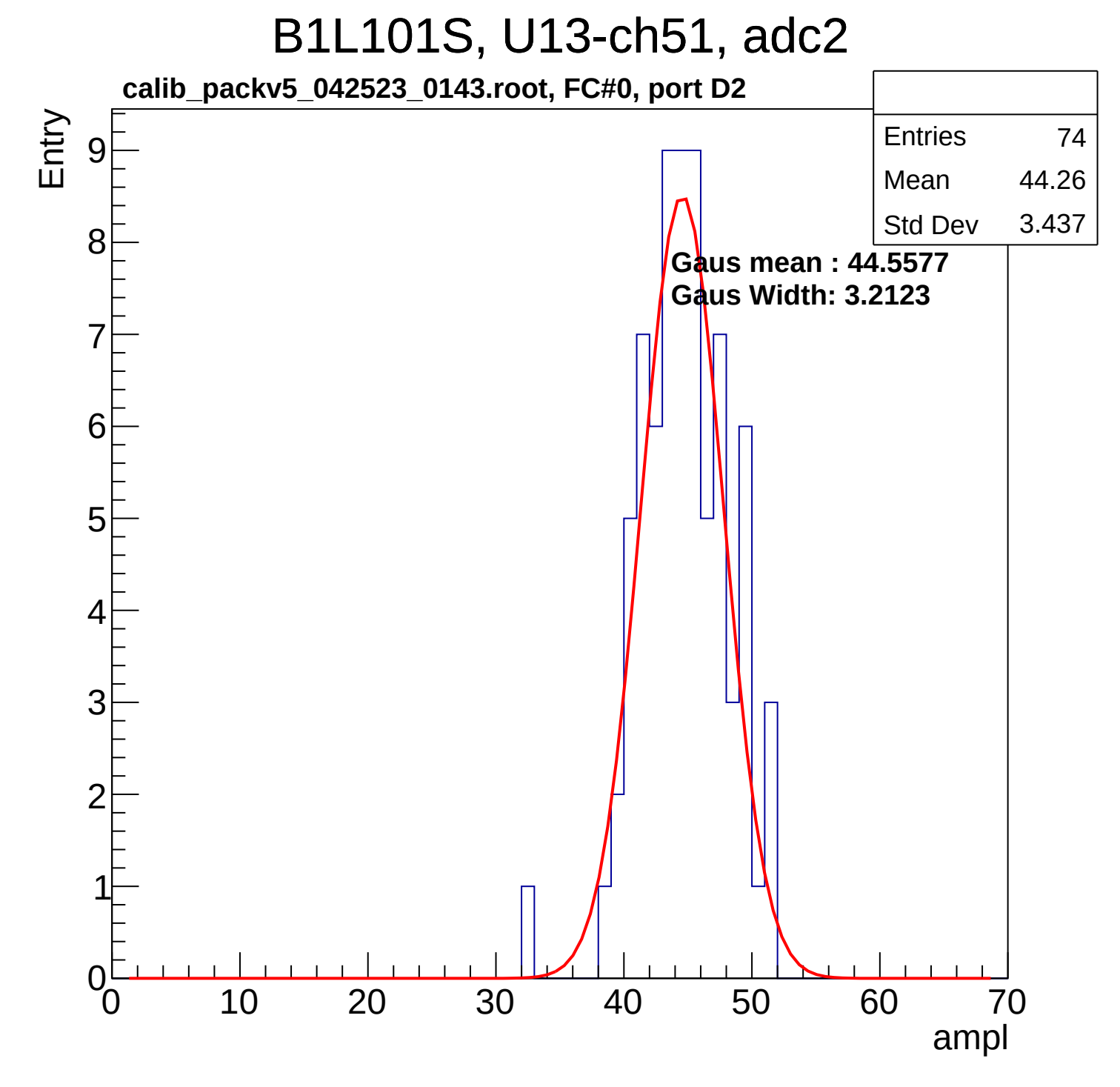
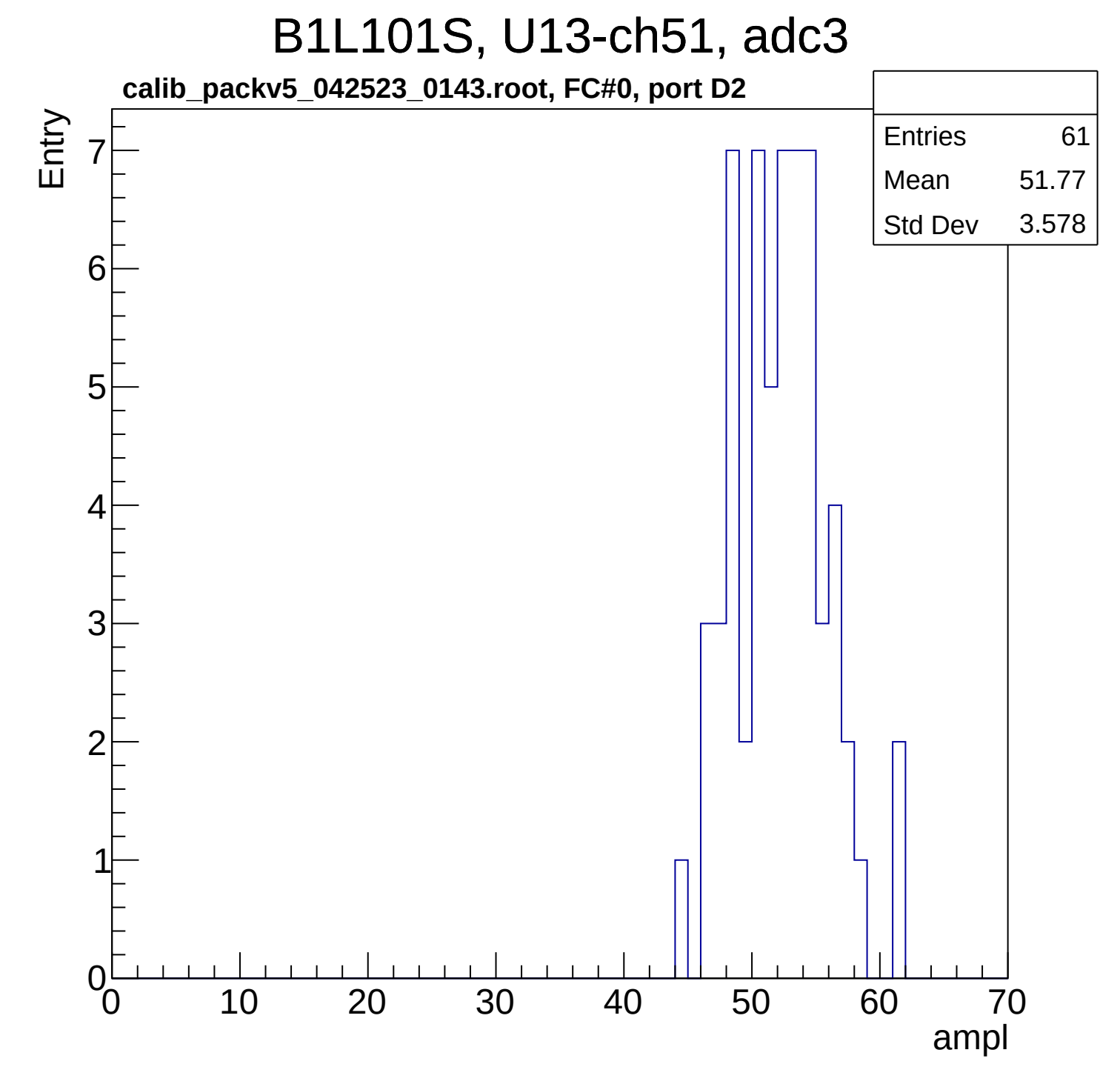
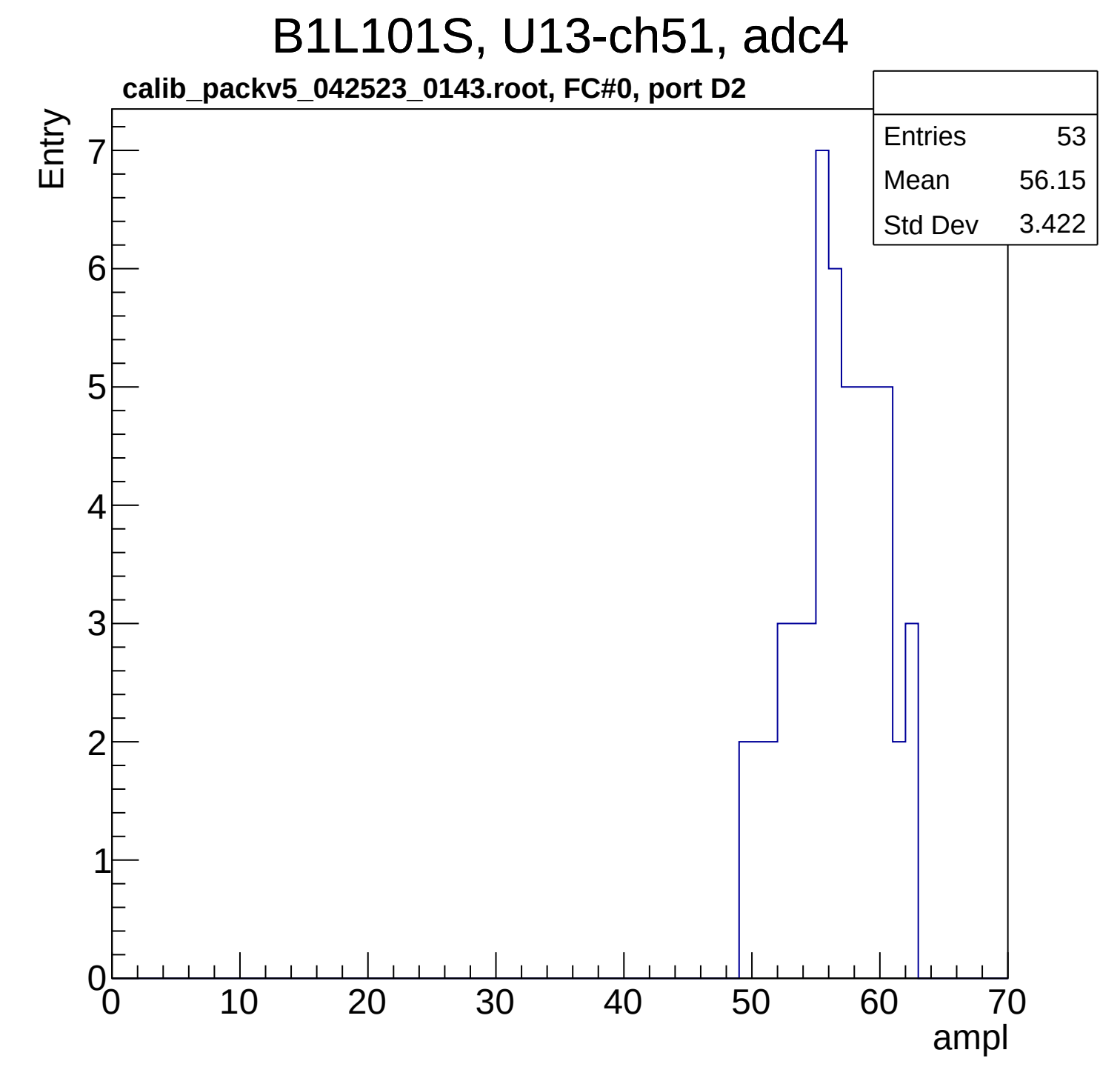
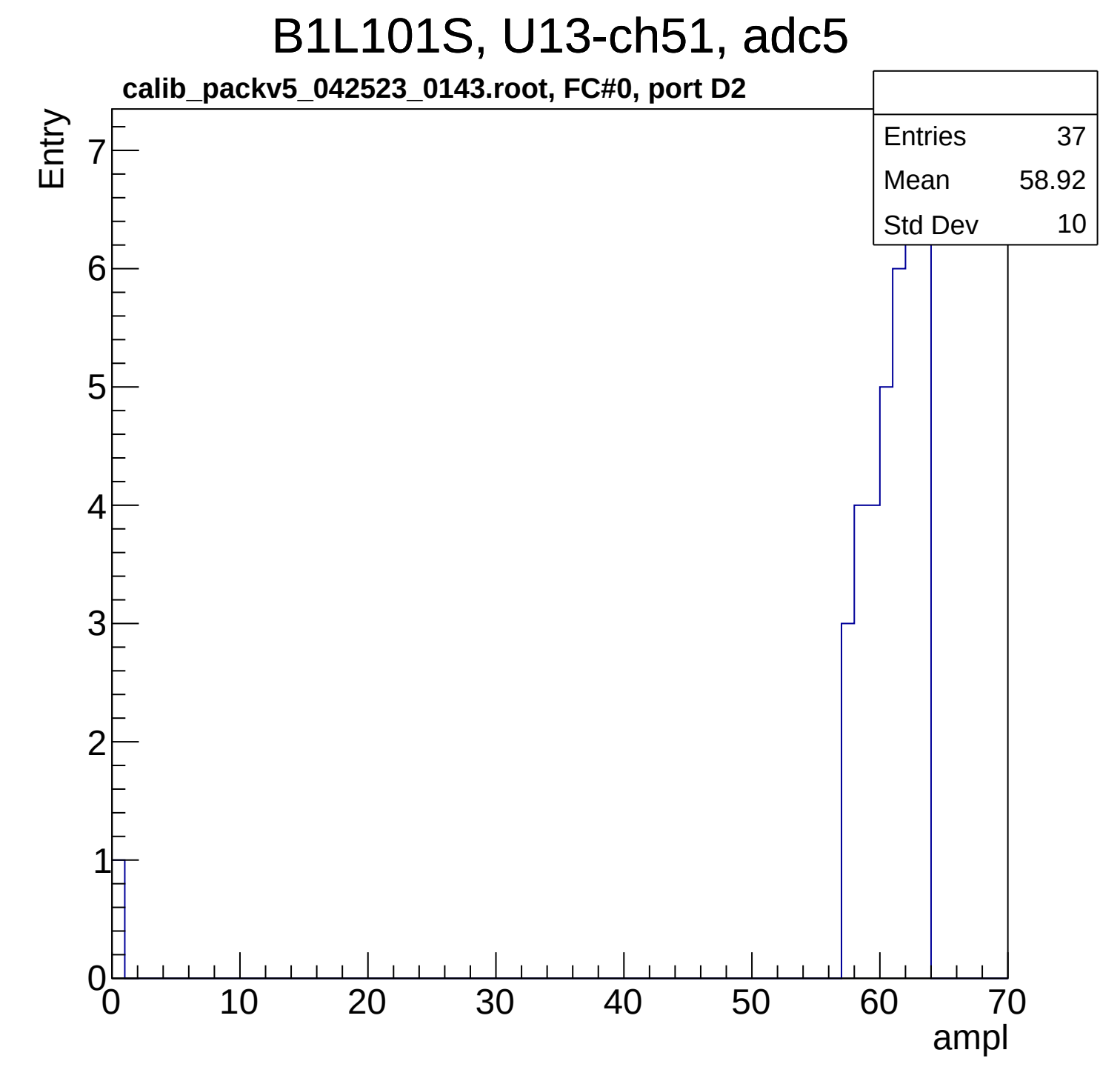

B1L101S, U13-ch51, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 29.86
Std Dev 6.309
Gaus mean : 31.0285
Gaus Width: 2.4971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

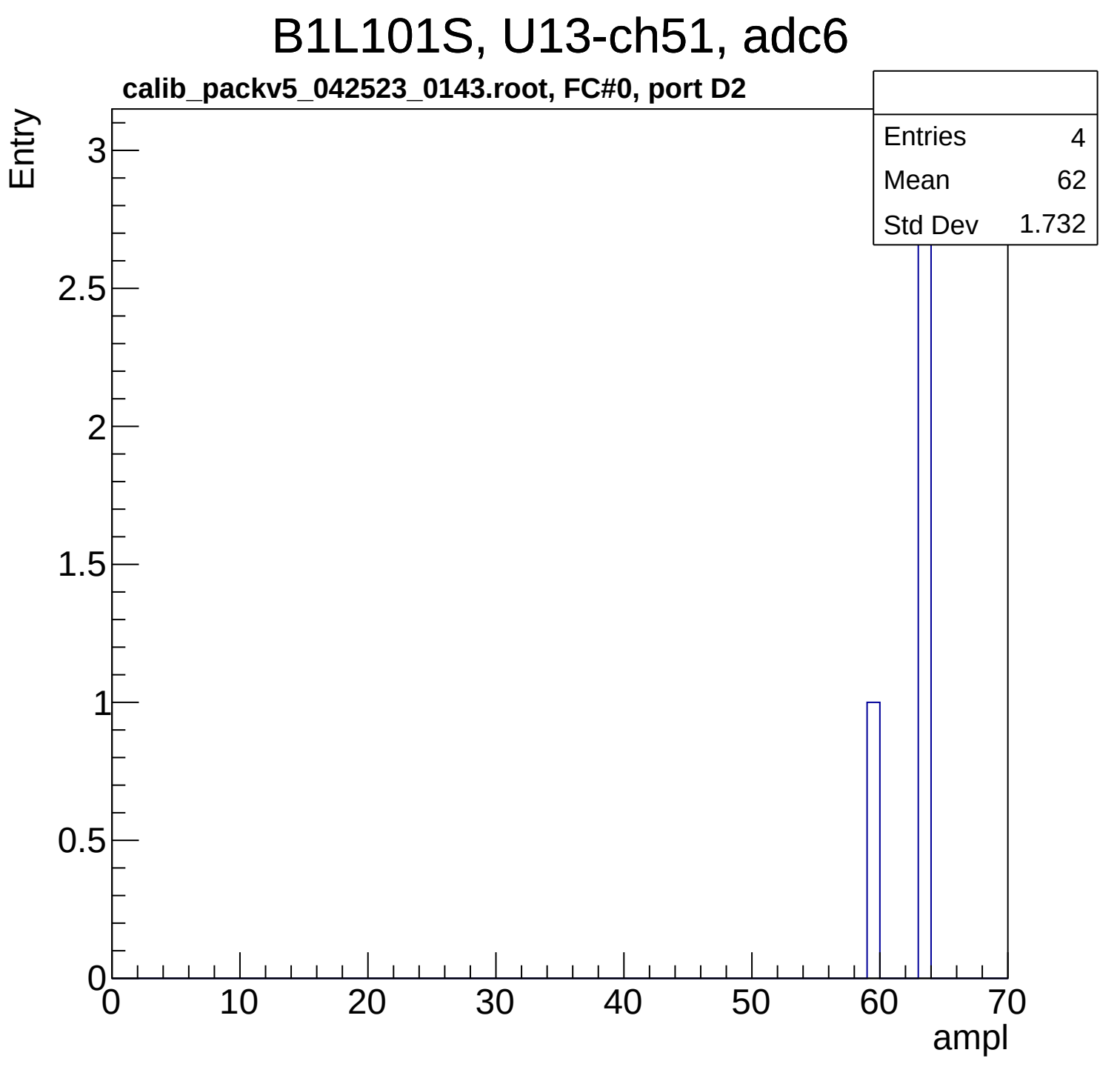

B1L101S, U13-ch51, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 36.32
Std Dev 6.874
Gaus mean : 37.6596
Gaus Width: 2.8185
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

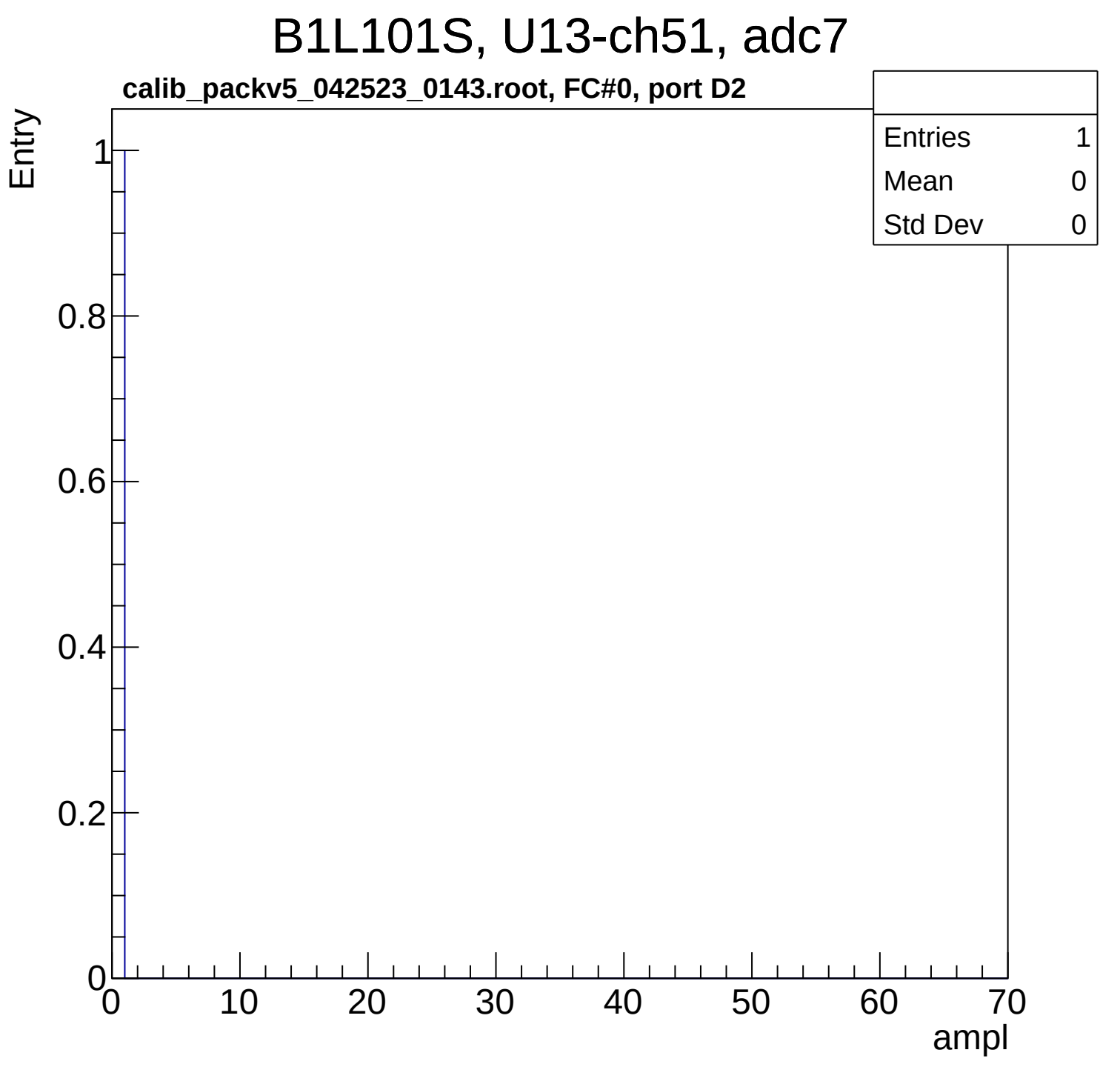

B1L101S, U13-ch51, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 44.26
Std Dev 3.437
Gaus mean : 44.5577
Gaus Width: 3.2123
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

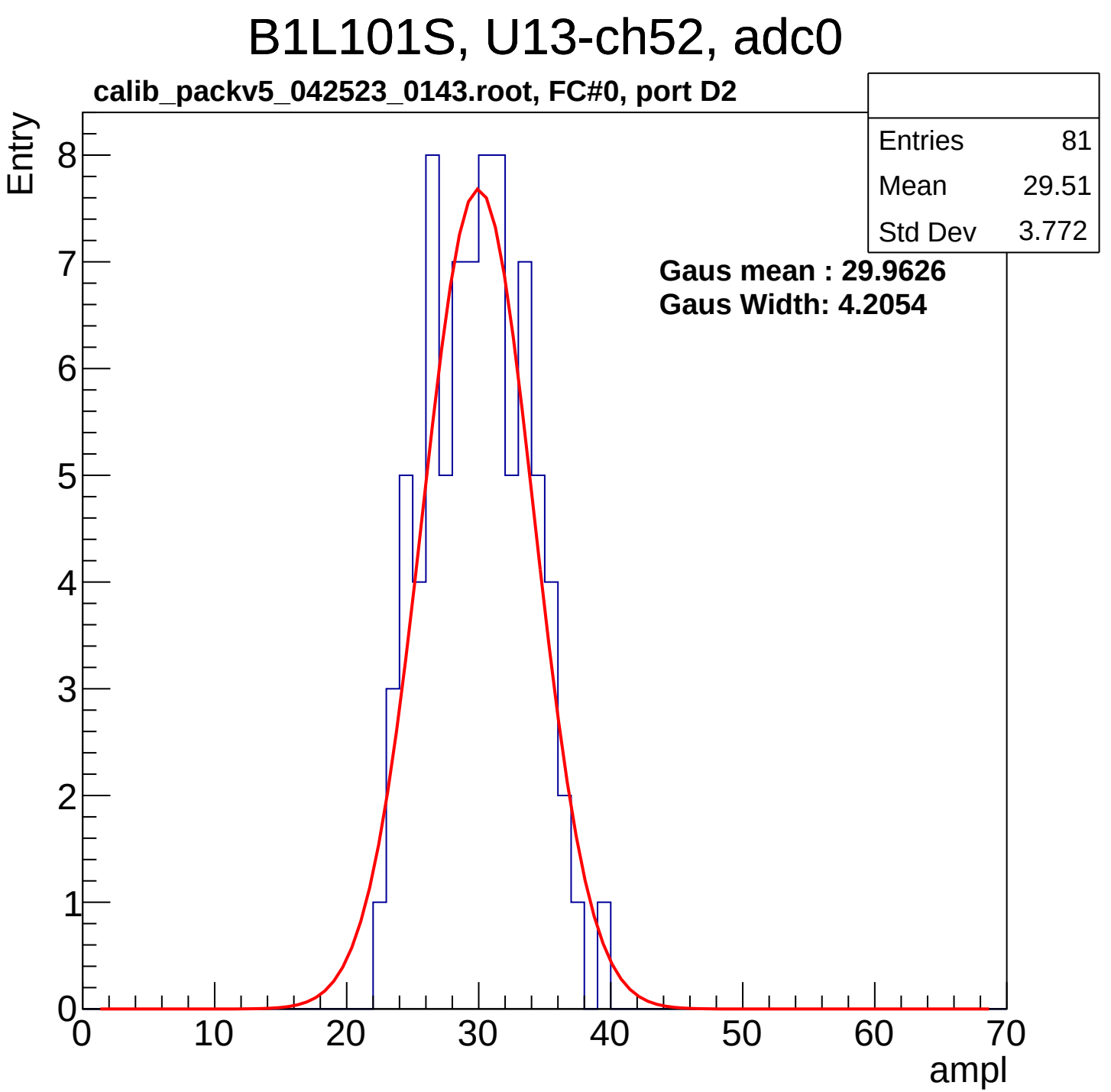

B1L101S, U13-ch51, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 51.77
Std Dev 3.578
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

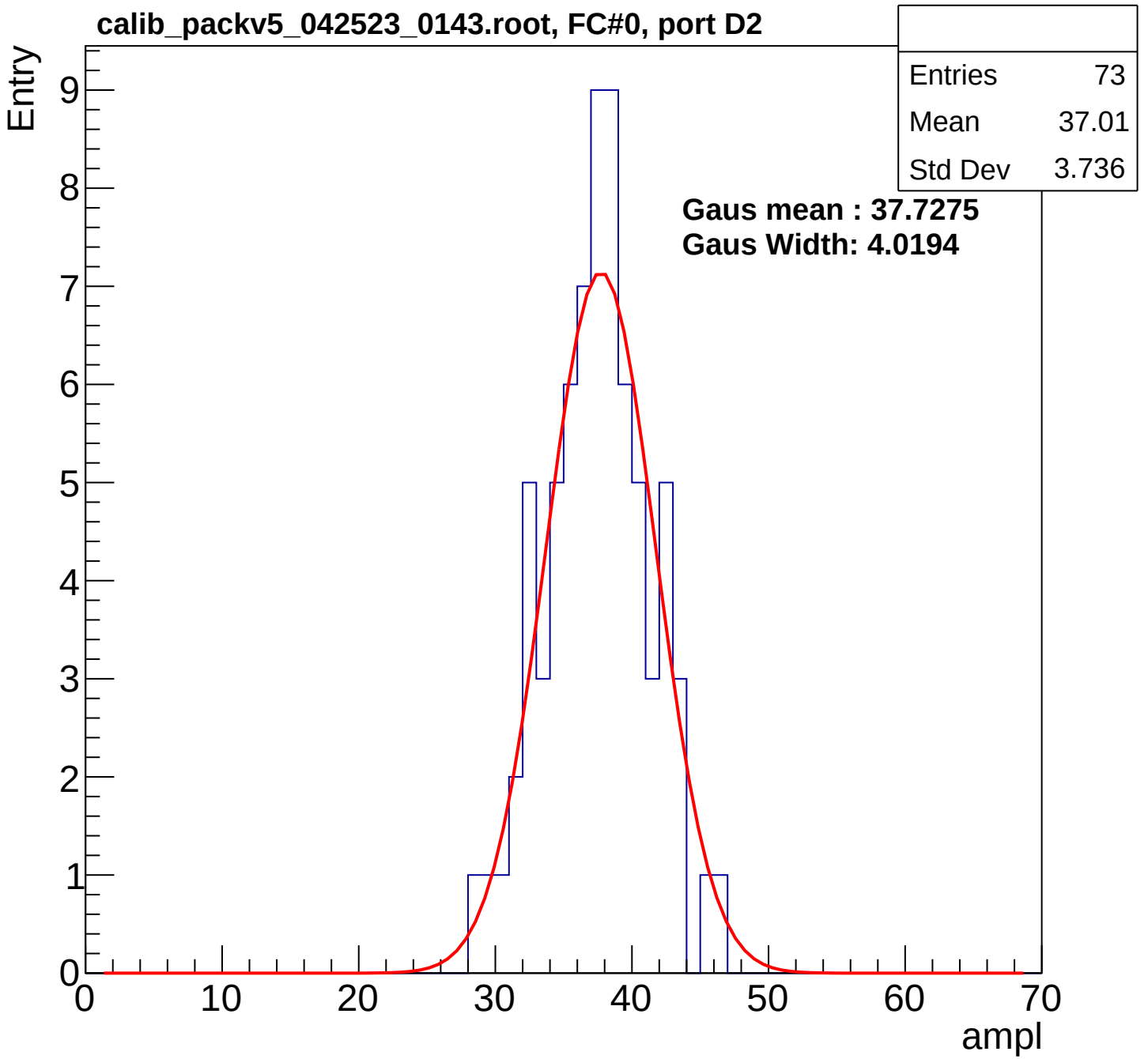

B1L101S, U13-ch51, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 56.15
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

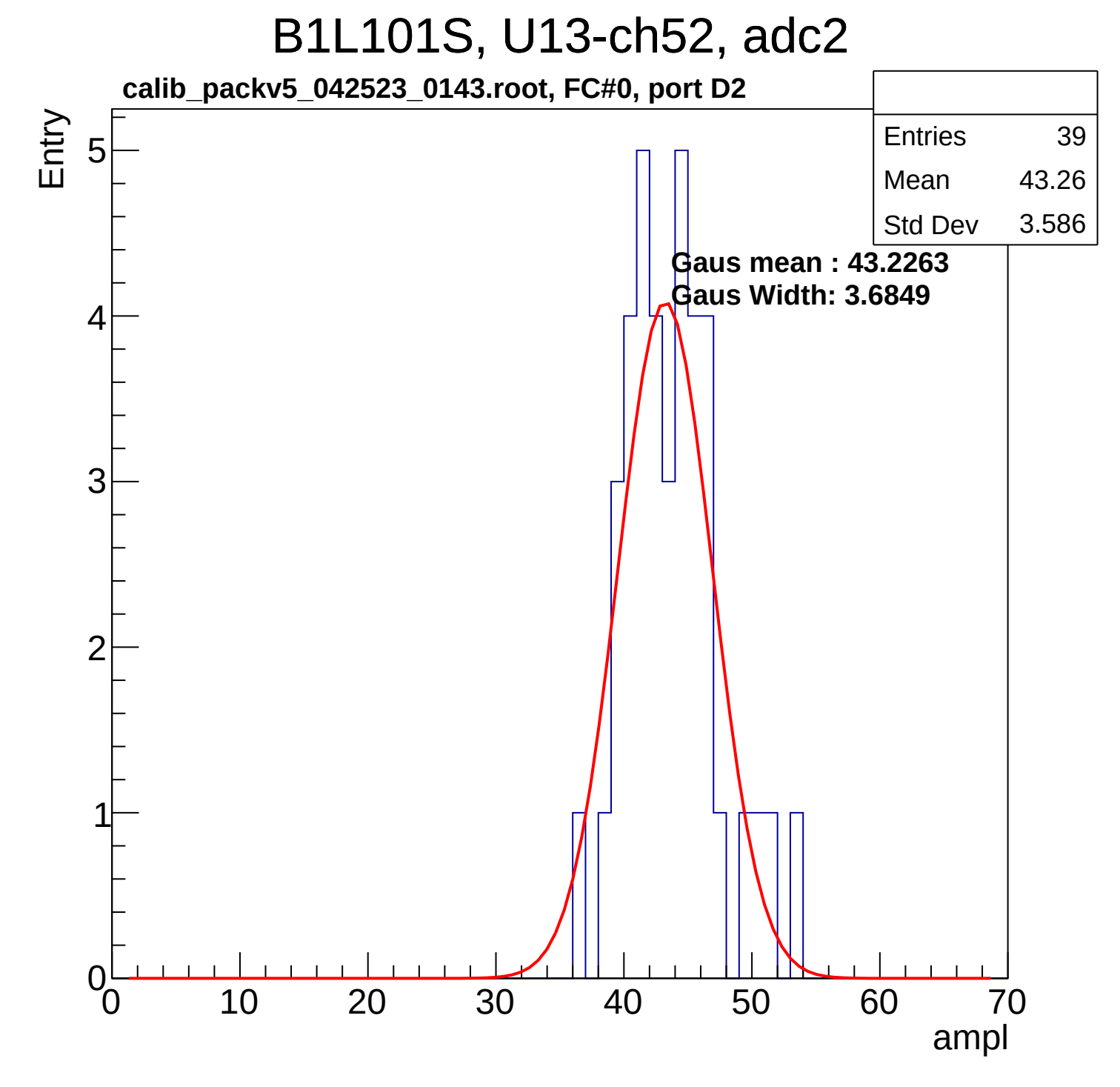

B1L101S, U13-ch51, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 37
Mean 58.92
Std Dev 10
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

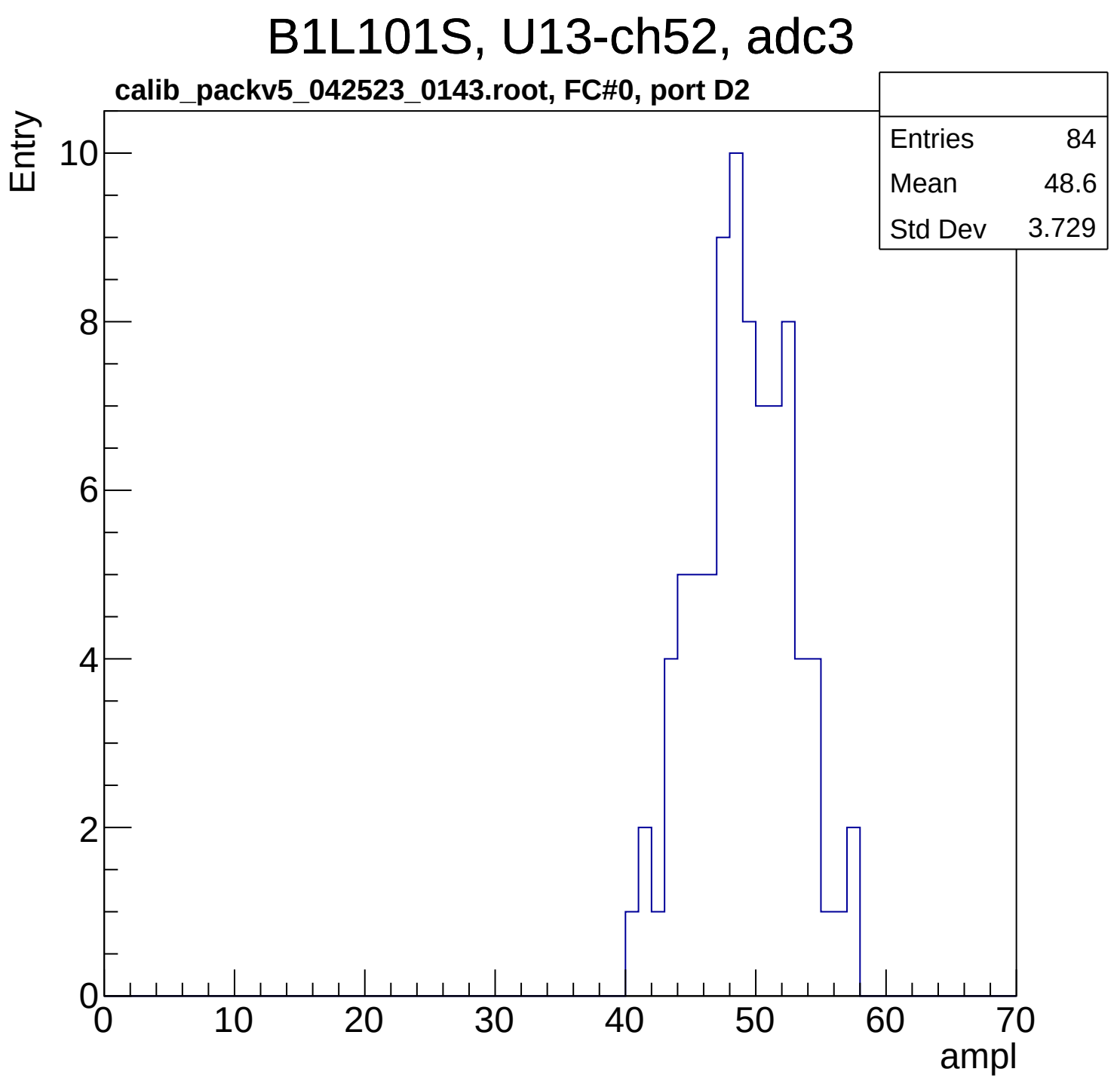

B1L101S, U13-ch51, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62
Std Dev 1.732
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

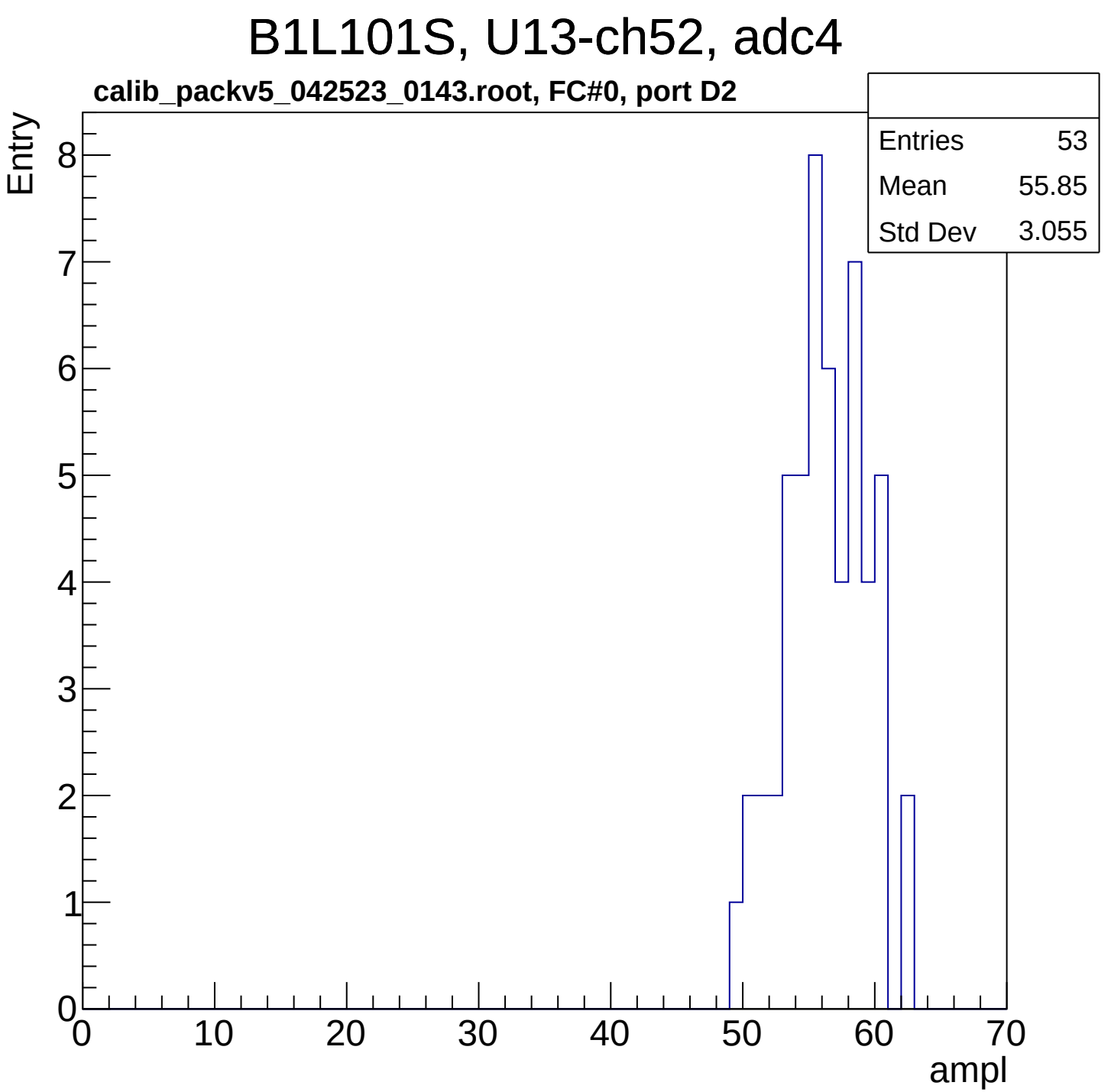

B1L101S, U13-ch51, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

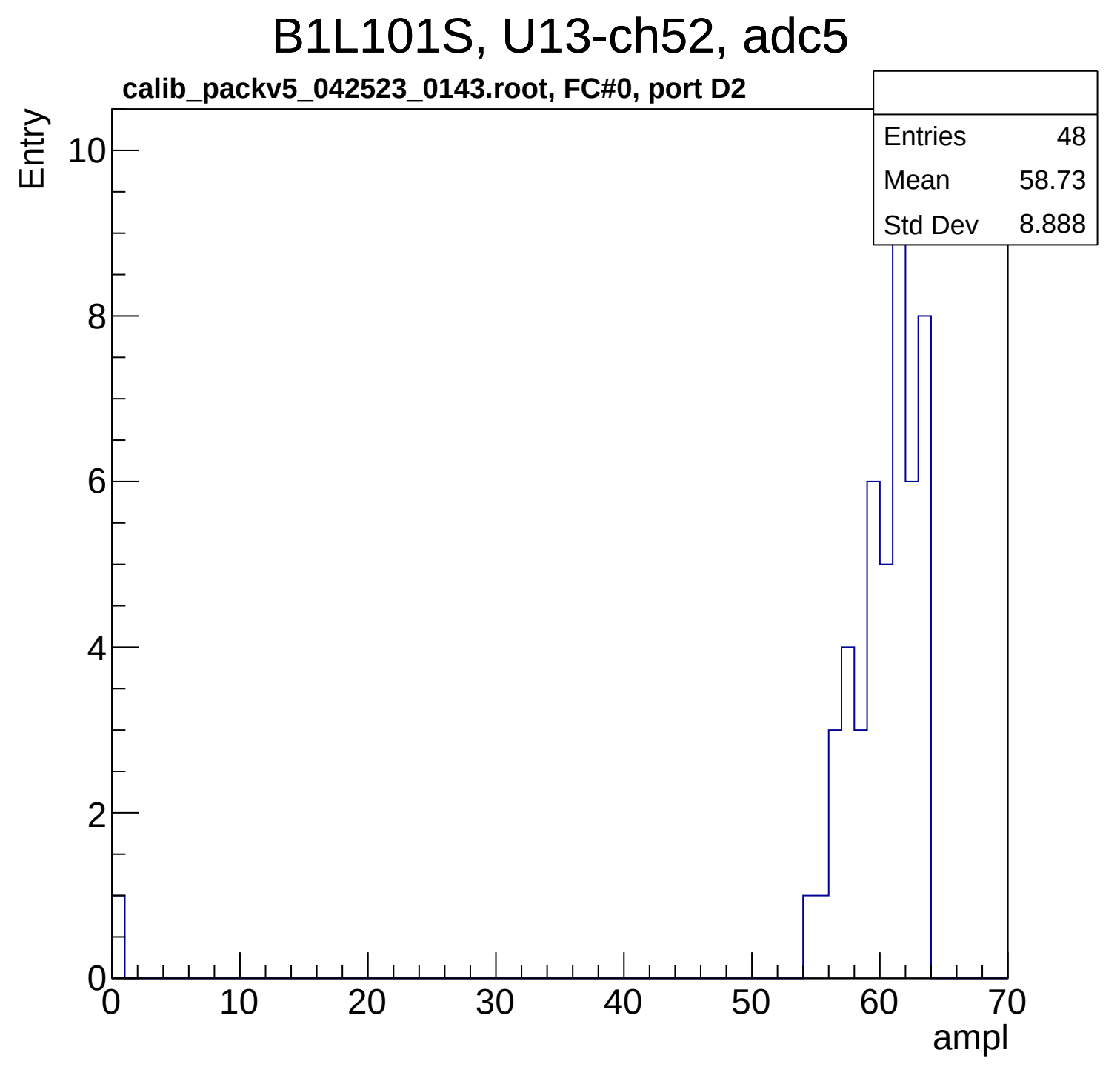

B1L101S, U13-ch52, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 81
Mean 29.51
Std Dev 3.772
Gaus mean : 29.9626
Gaus Width: 4.2054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

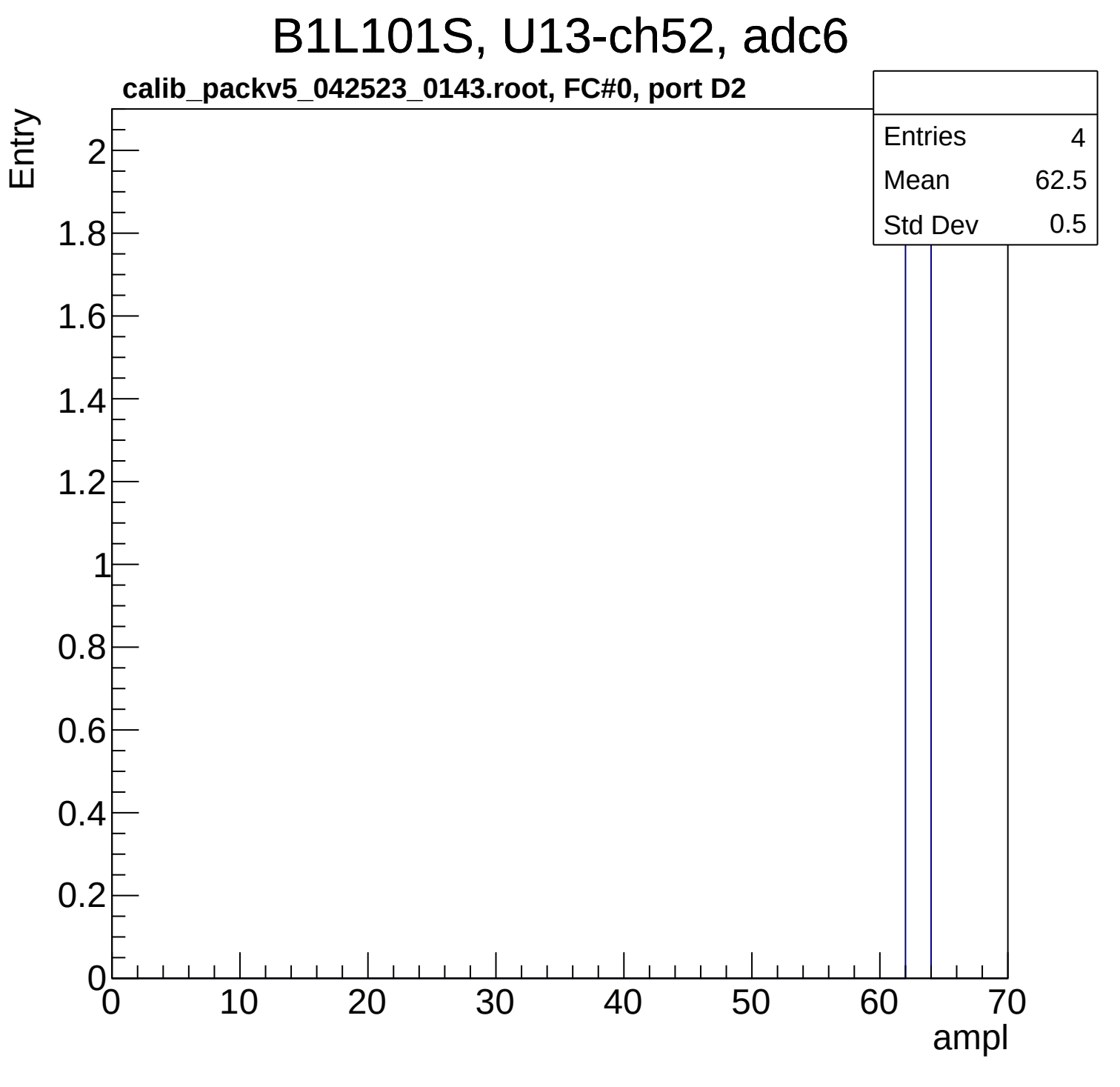

B1L101S, U13-ch52, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 37.01
Std Dev 3.736
Gaus mean : 37.7275
Gaus Width: 4.0194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

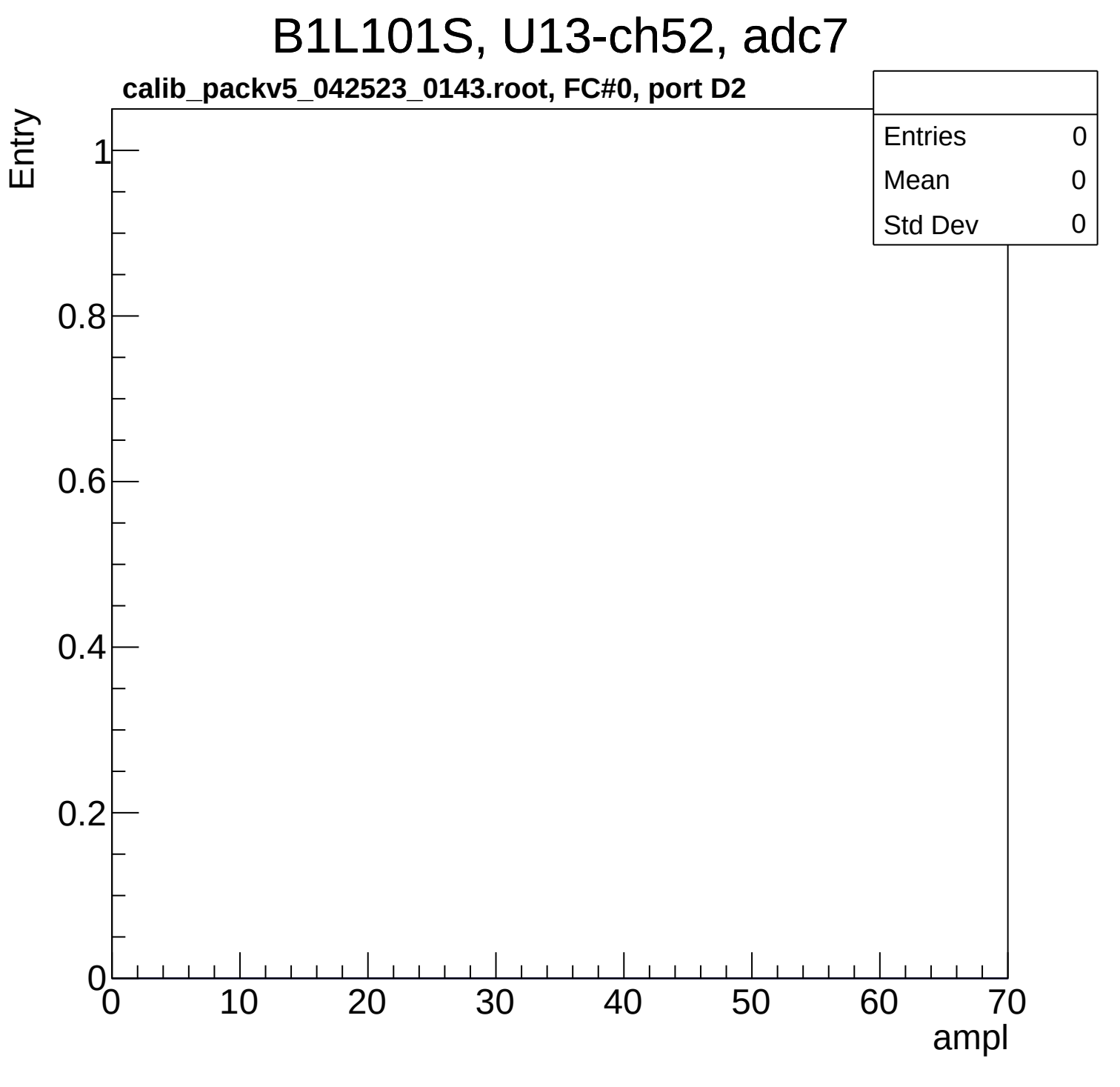

B1L101S, U13-ch52, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 39
Mean 43.26
Std Dev 3.586
Gaus mean : 43.2263
Gaus Width: 3.6849
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

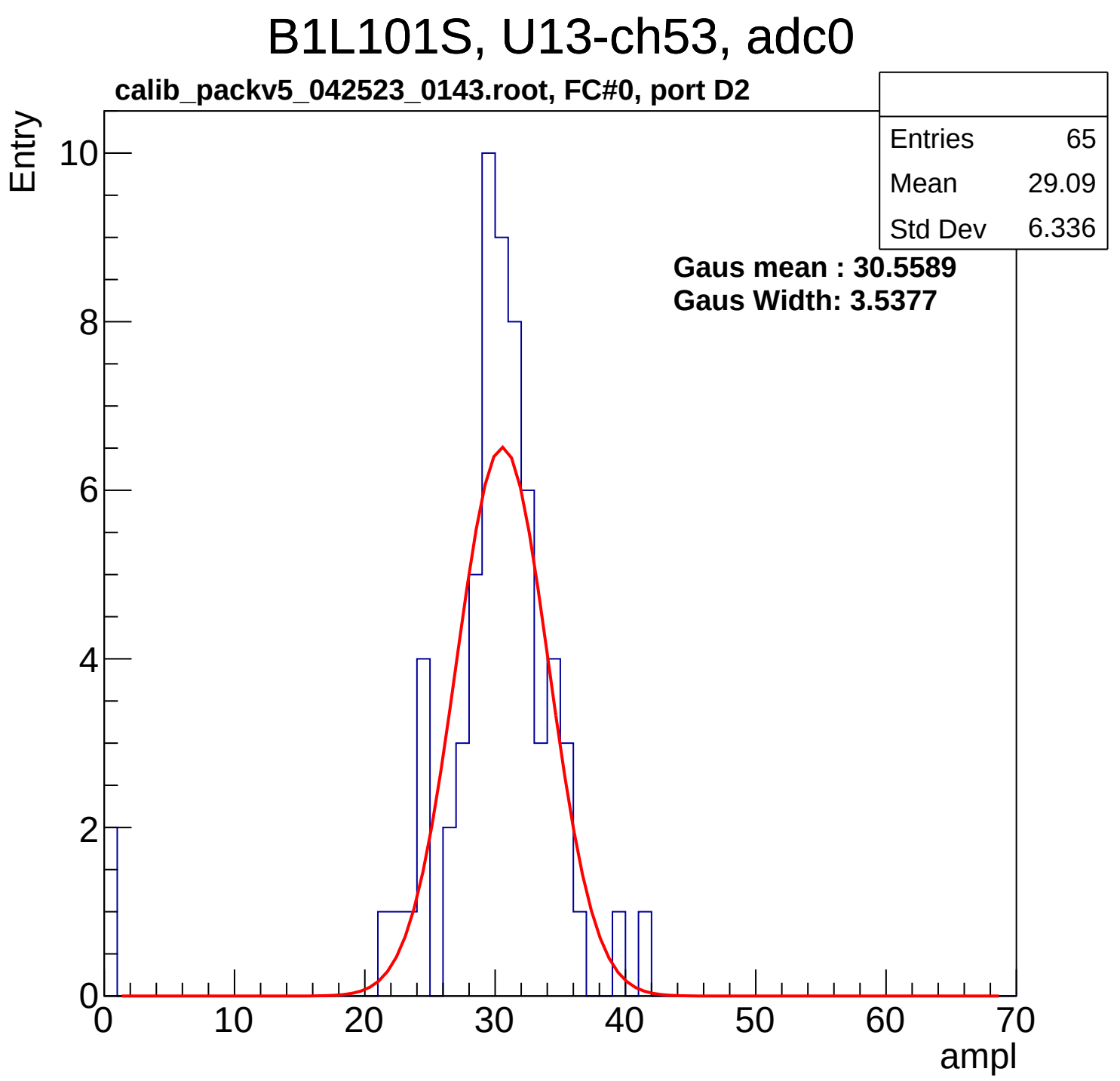

B1L101S, U13-ch52, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 84
Mean 48.6
Std Dev 3.729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

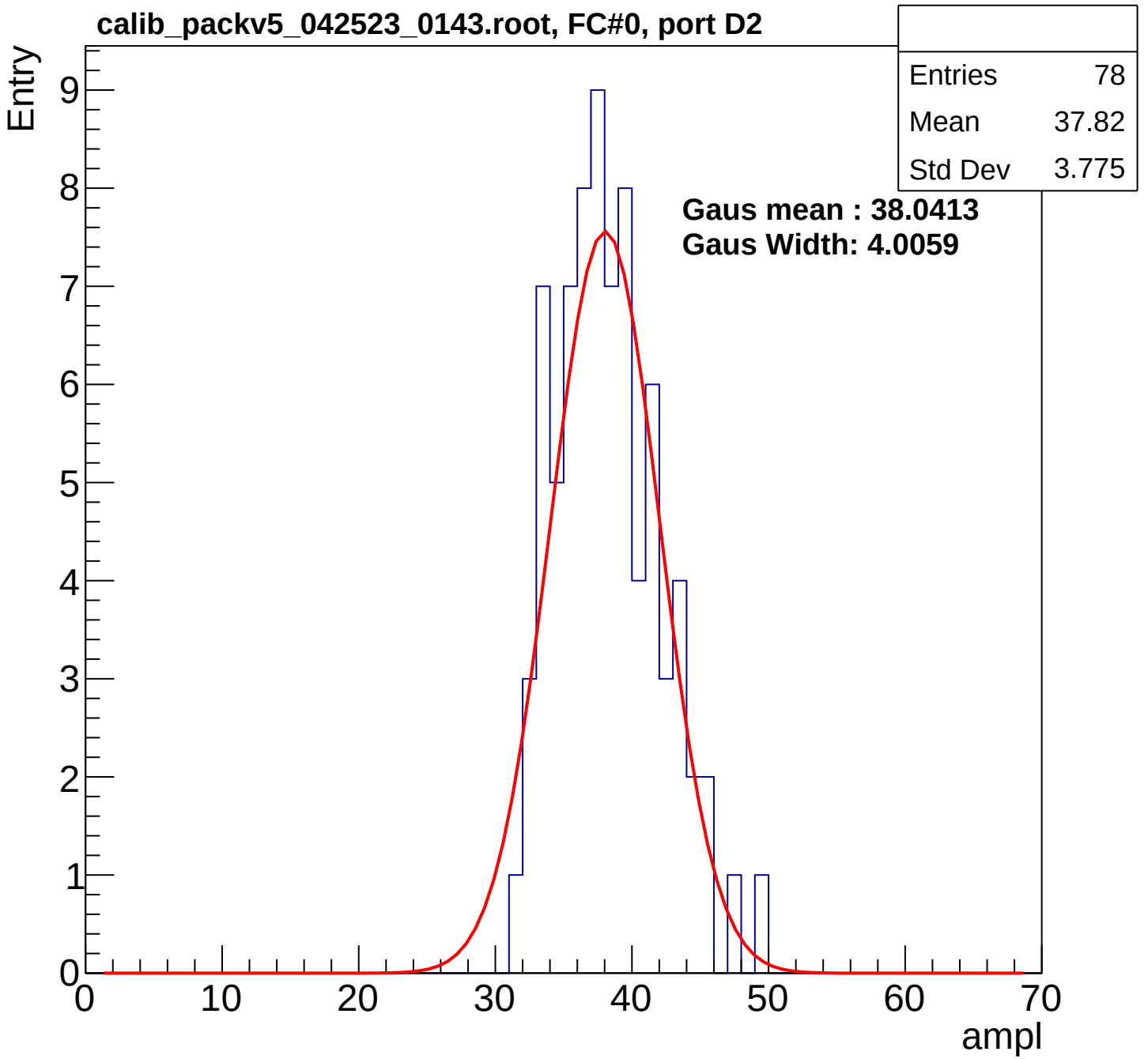

B1L101S, U13-ch52, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 55.85
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

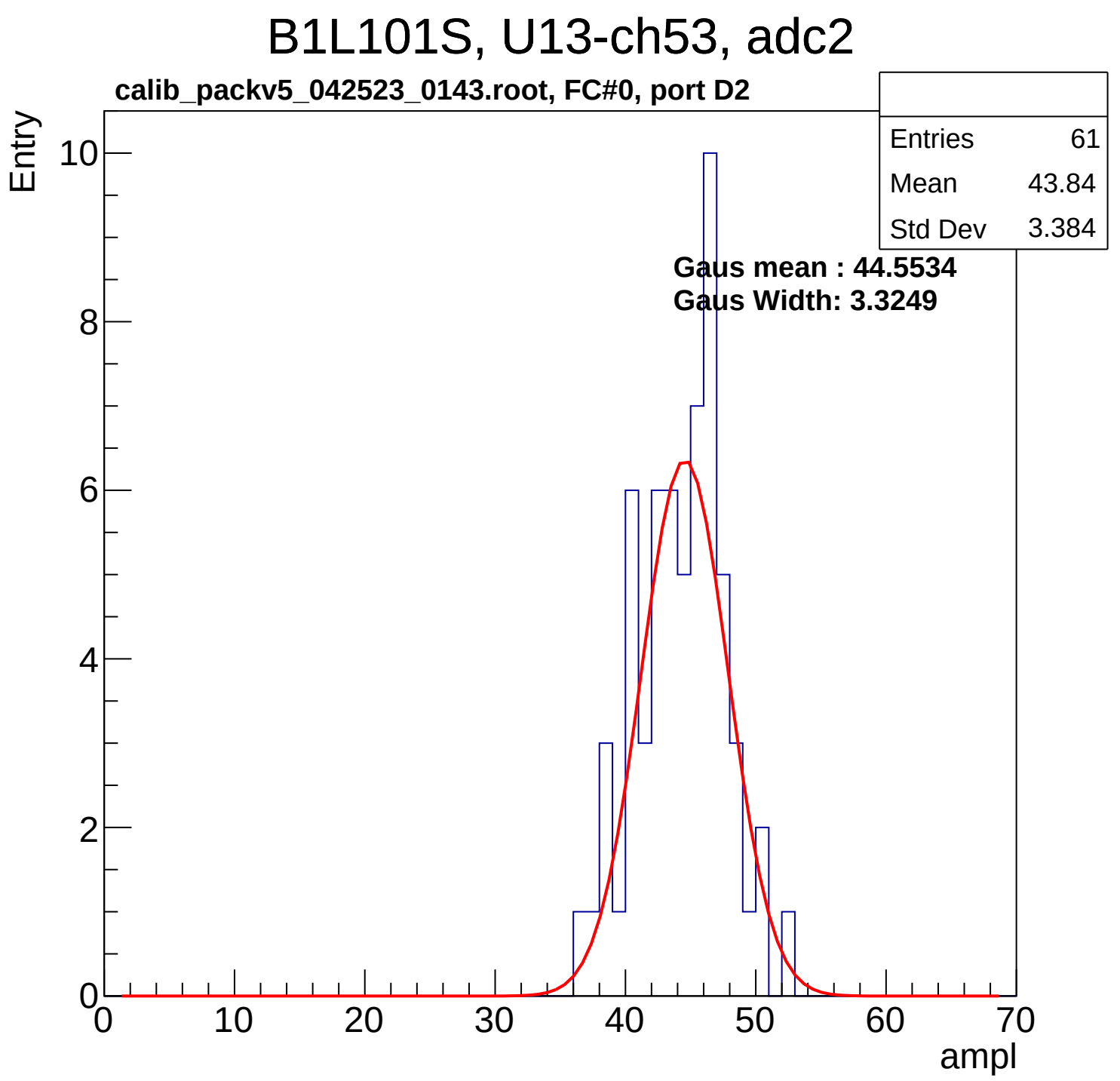

B1L101S, U13-ch52, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 58.73
Std Dev 8.888
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

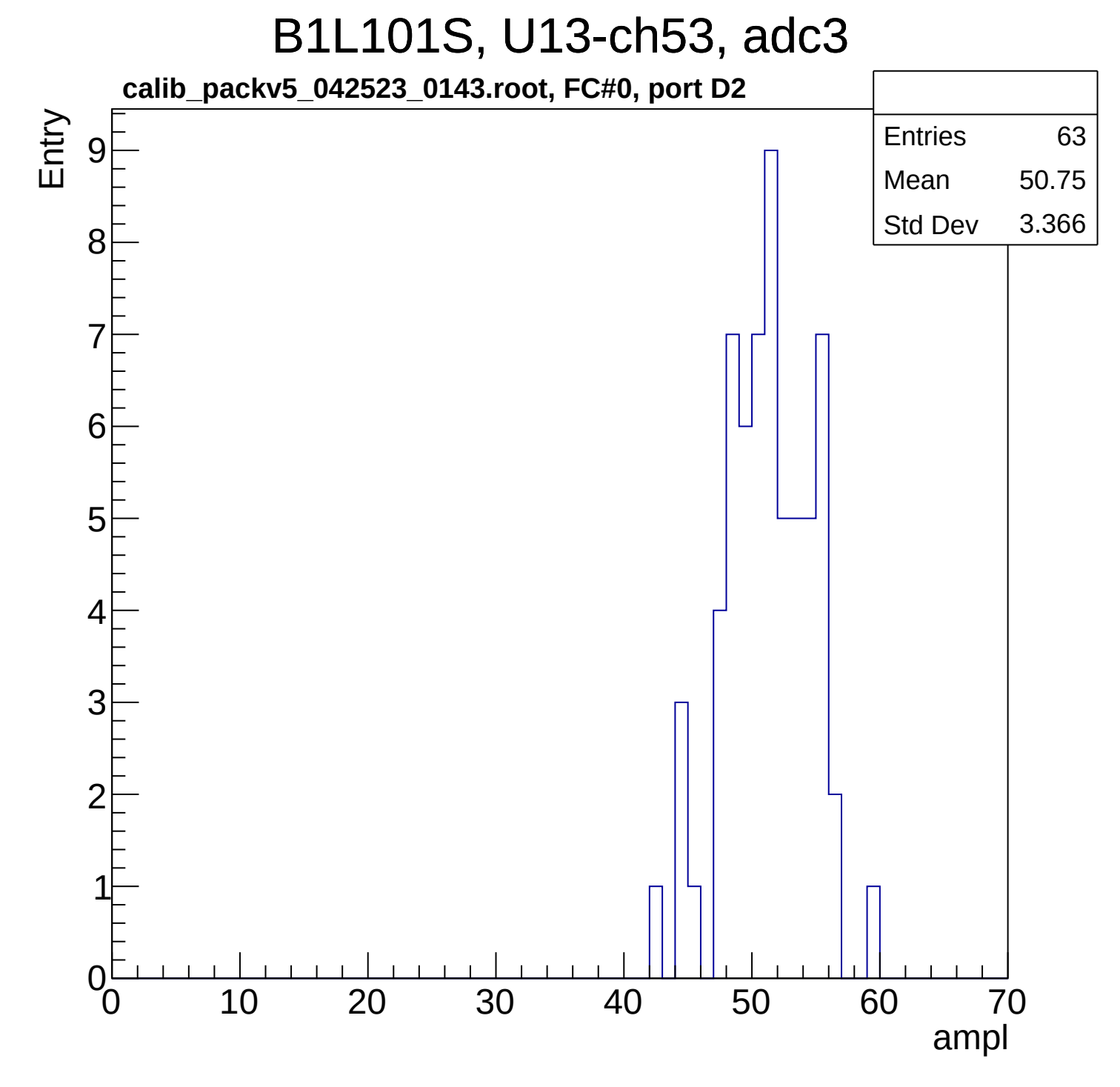

B1L101S, U13-ch52, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch52, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch53, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 29.09
Std Dev 6.336
Gaus mean : 30.5589
Gaus Width: 3.5377
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch53, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 37.82
Std Dev 3.775
Gaus mean : 38.0413
Gaus Width: 4.0059
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch53, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 43.84
Std Dev 3.384
Gaus mean : 44.5534
Gaus Width: 3.3249
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch53, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 50.75
Std Dev 3.366
Entry
ampl
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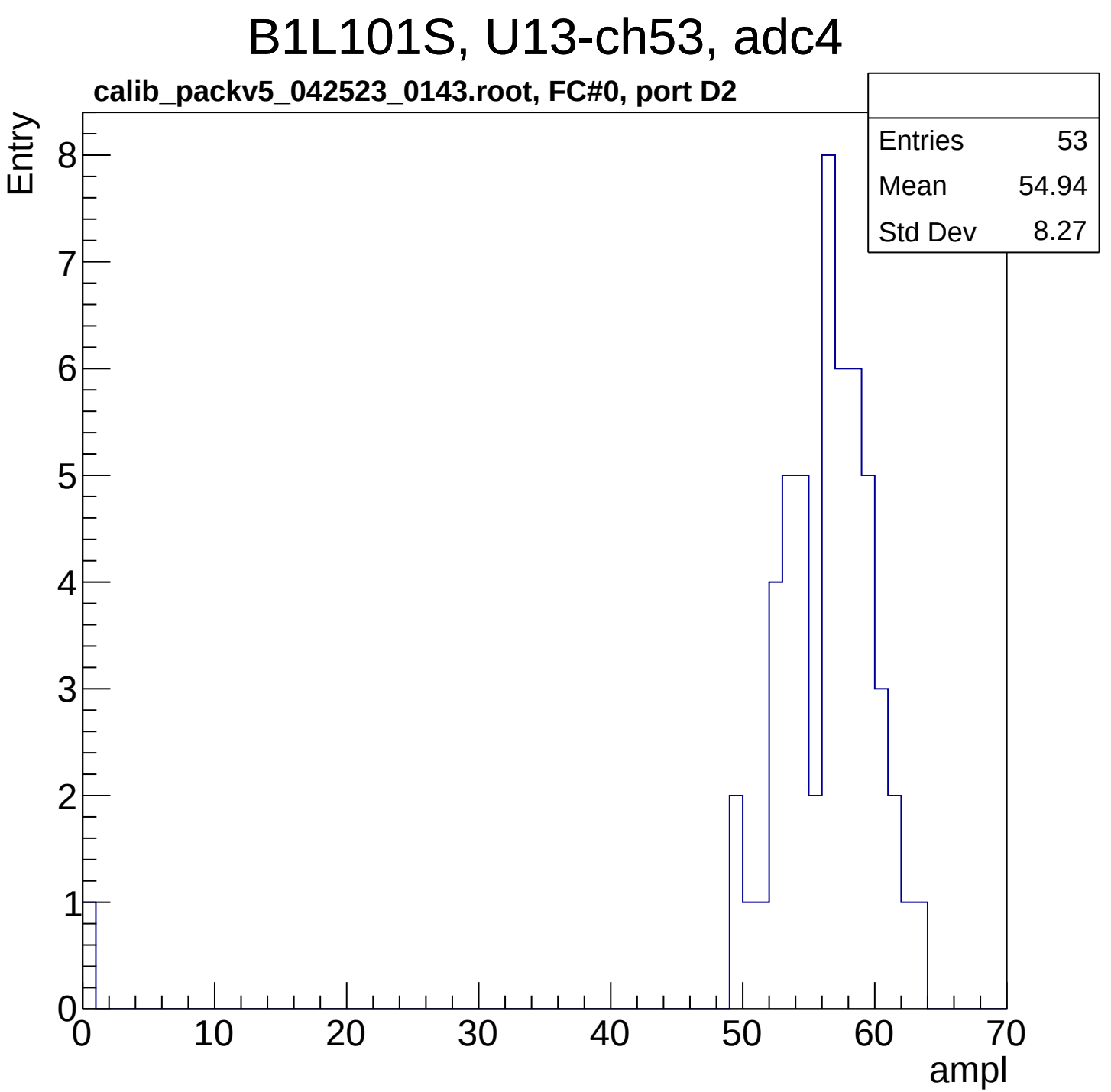
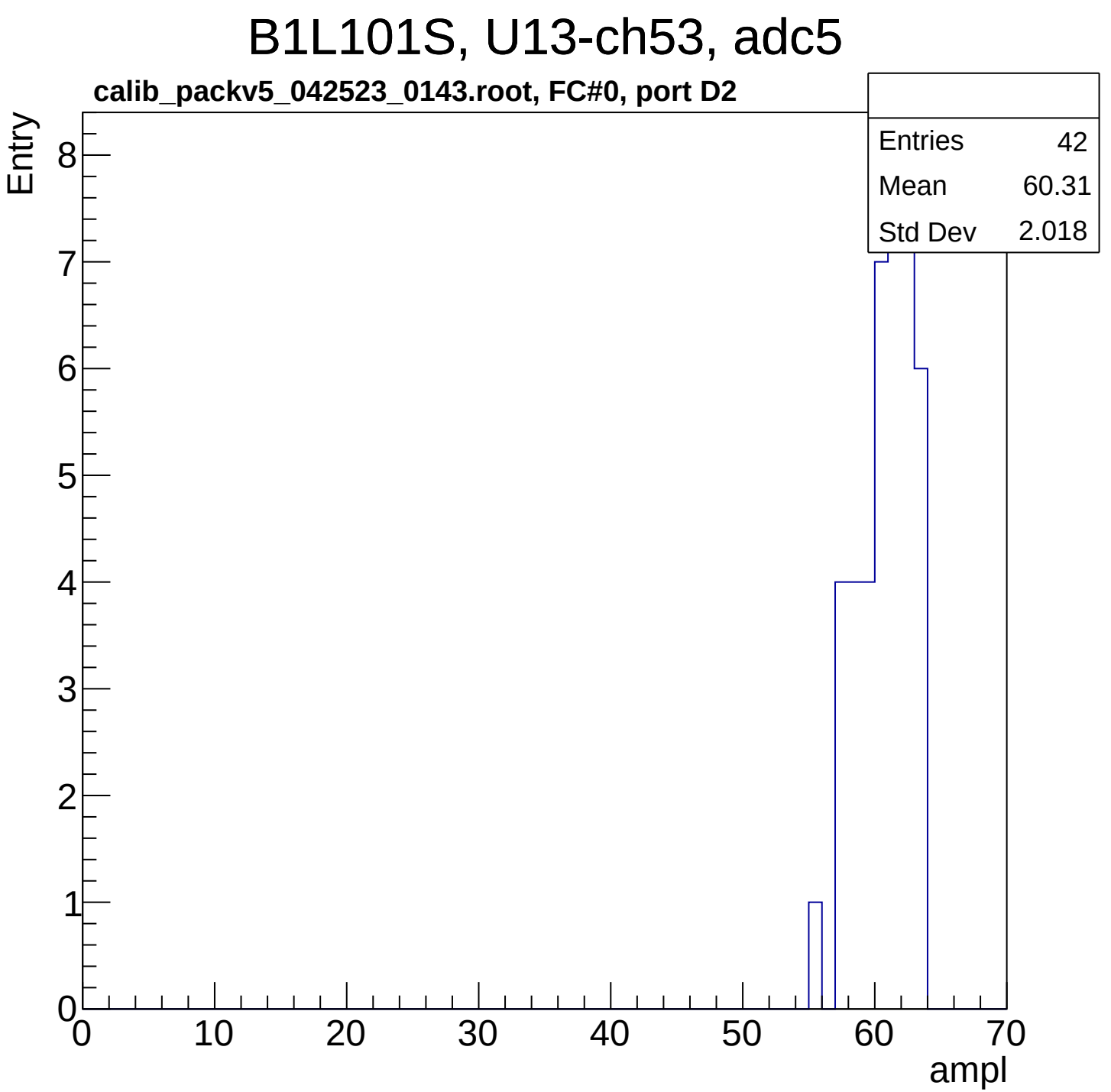
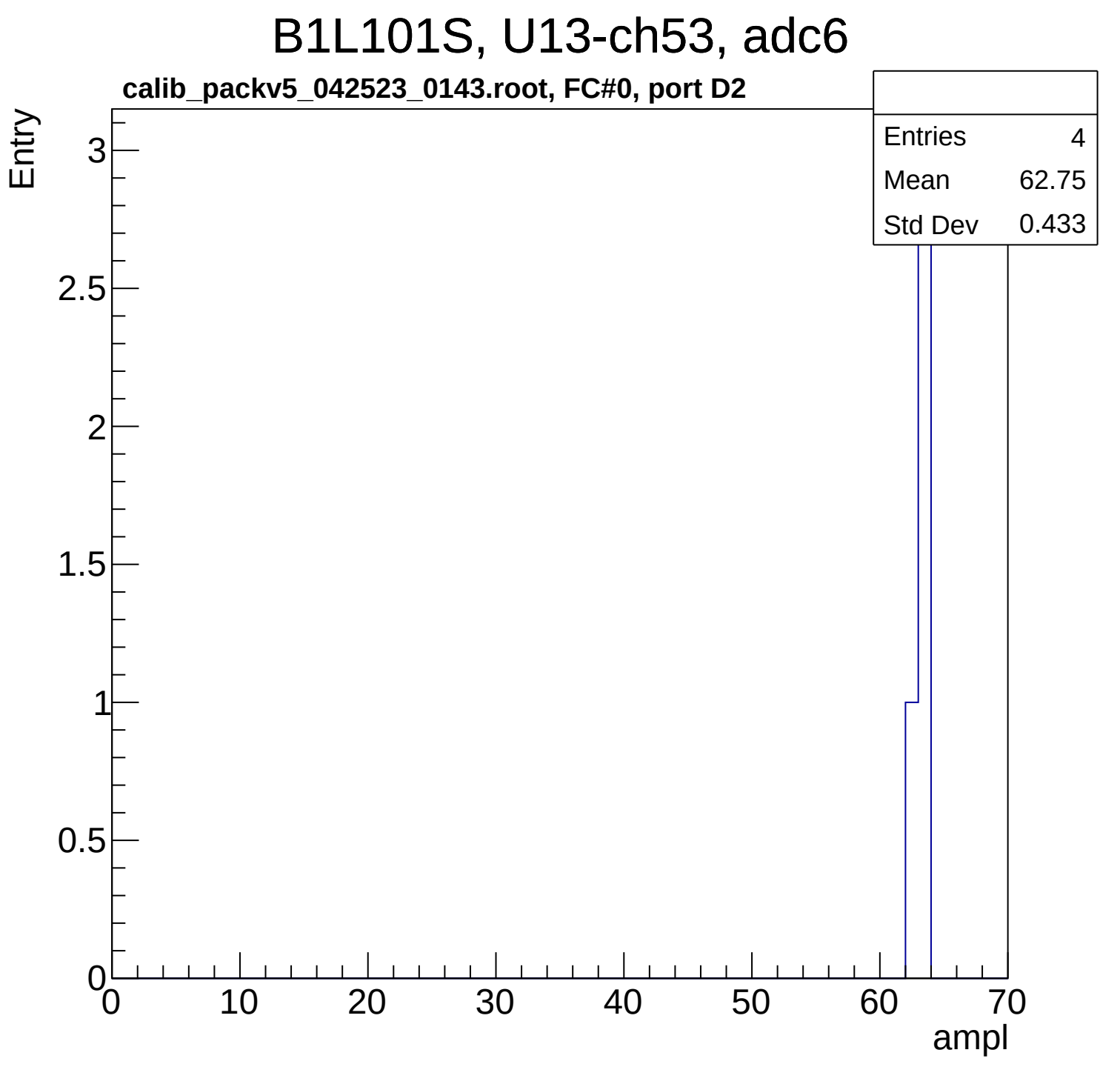
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch53, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 54.94
Std Dev 8.27
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch53, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 60.31
Std Dev 2.018
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch53, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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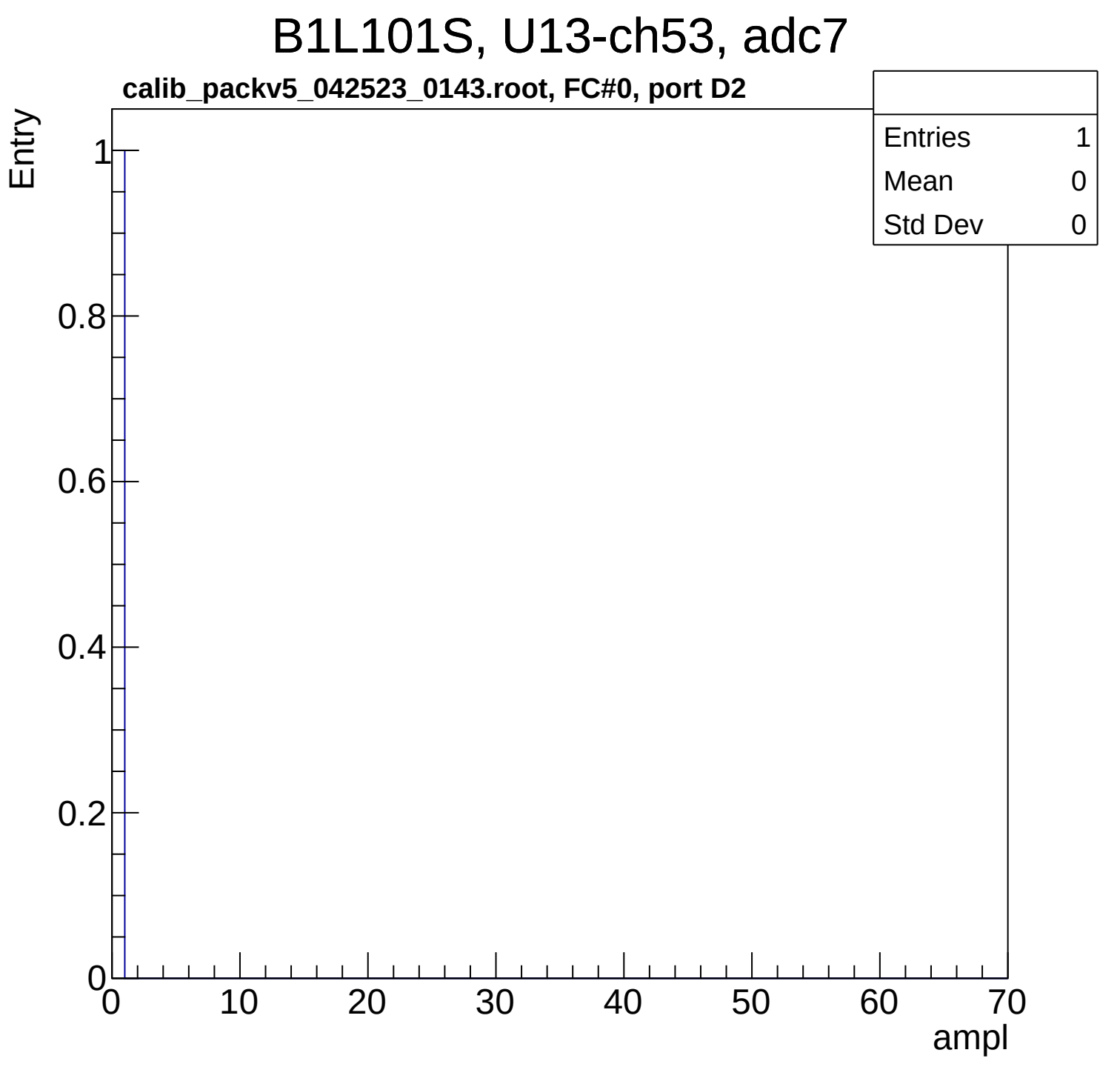
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch53, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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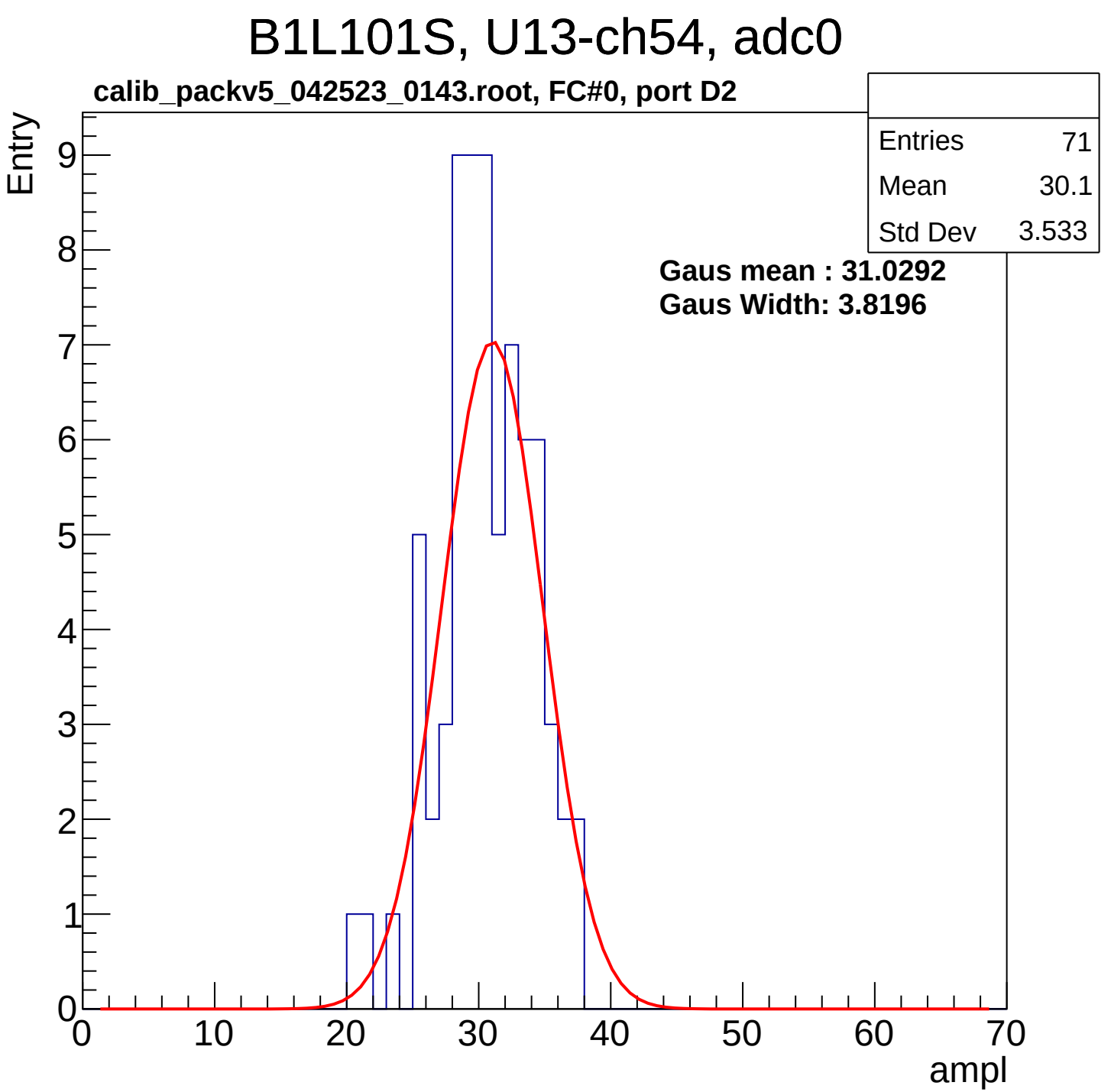
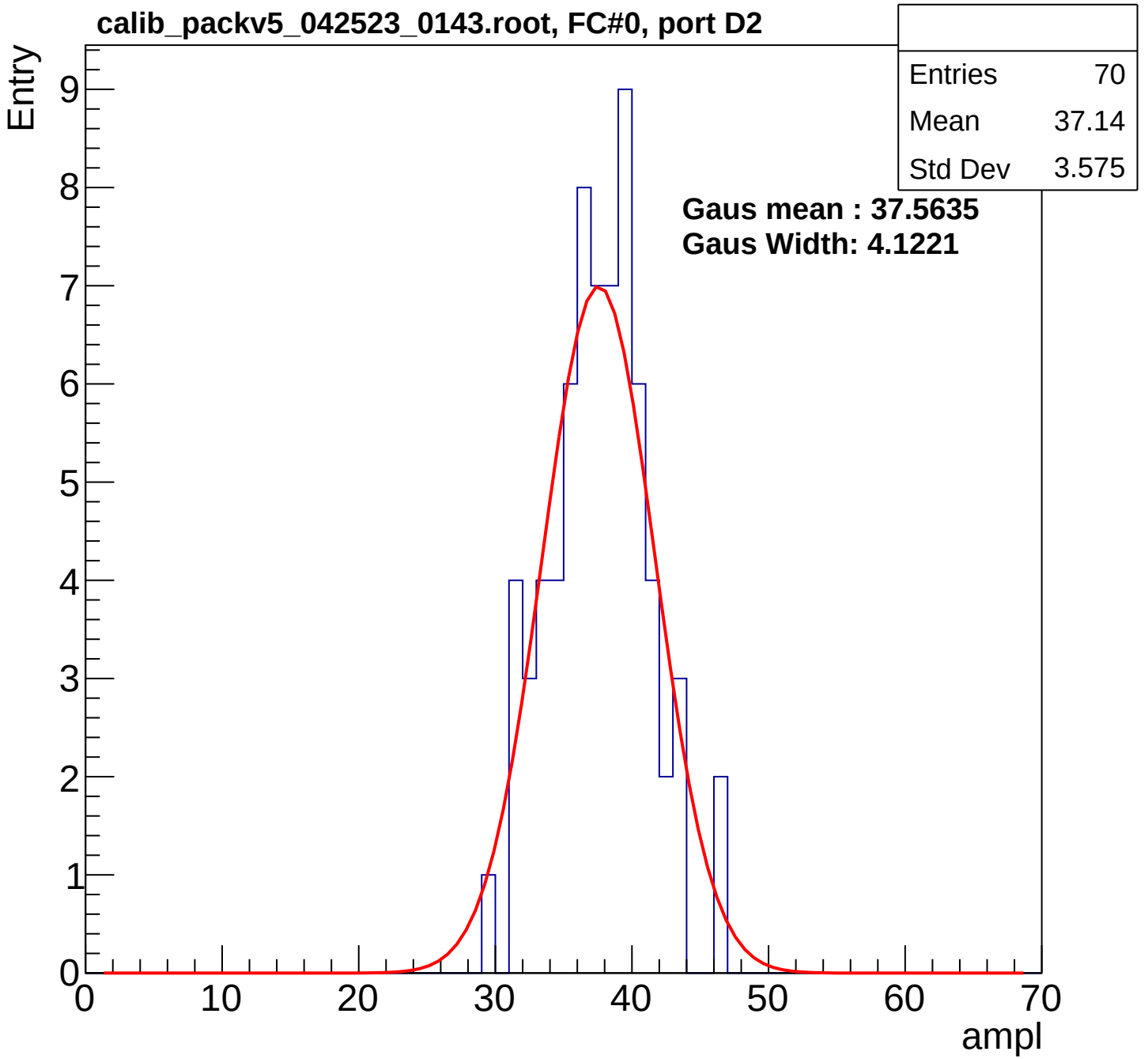
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch54, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 30.1
Std Dev 3.533
Gaus mean : 31.0292
Gaus Width: 3.8196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch54, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 37.14
Std Dev 3.575
Gaus mean : 37.5635
Gaus Width: 4.1221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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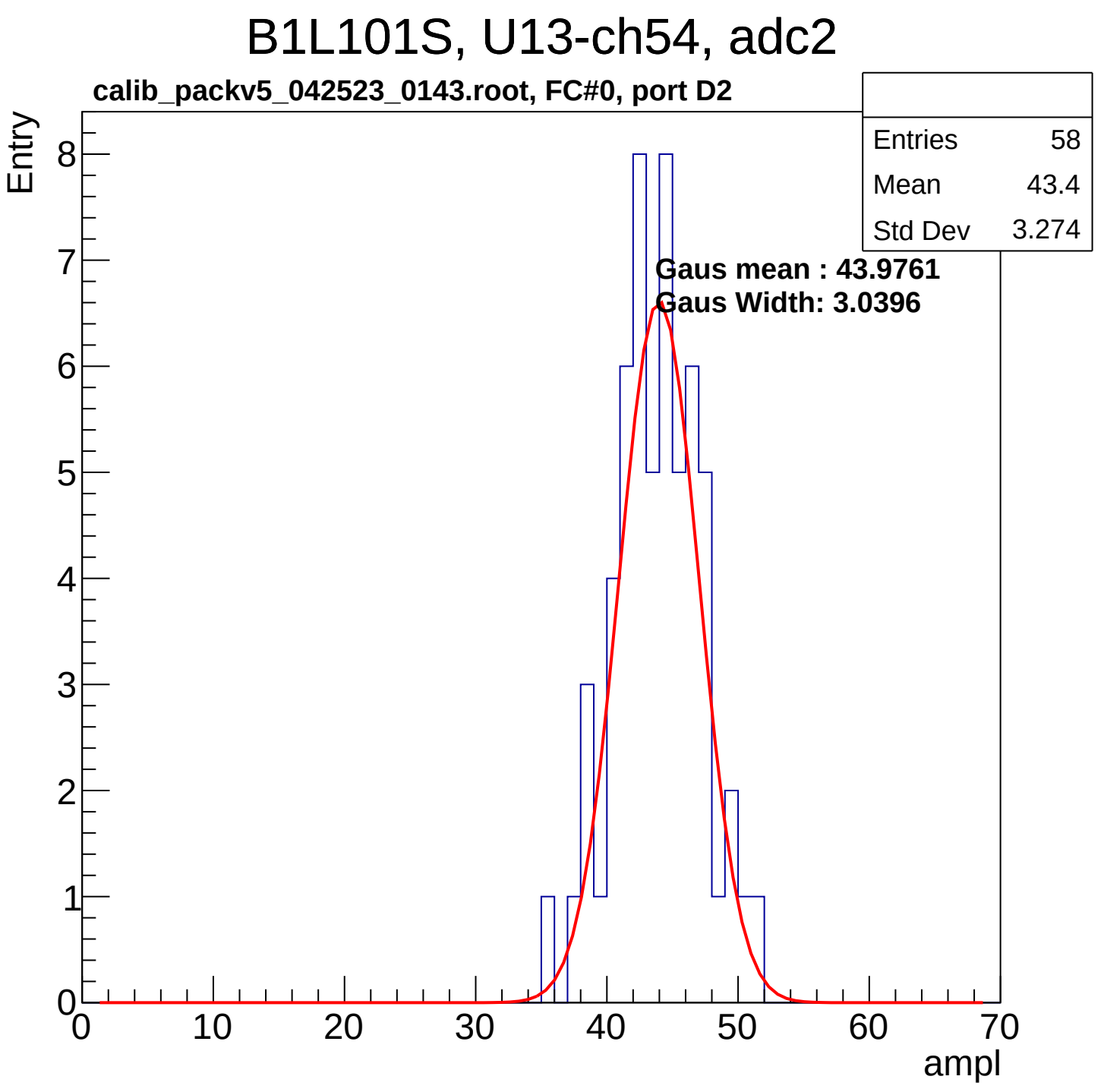
B1L101S, U13-ch54, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 43.4
Std Dev 3.274
Gaus mean : 43.9761
Gaus Width: 3.0396
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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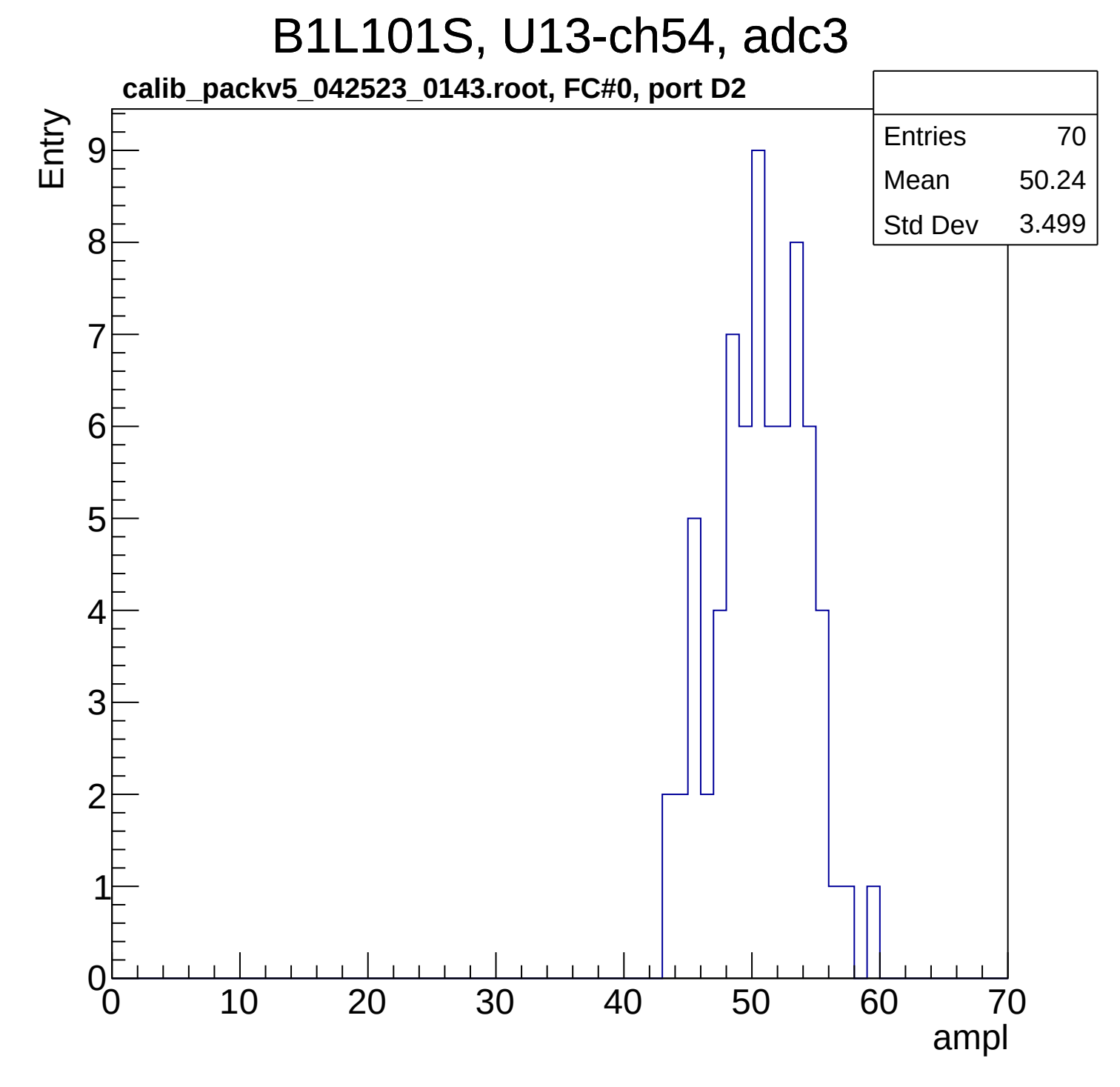
B1L101S, U13-ch54, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 50.24
Std Dev 3.499
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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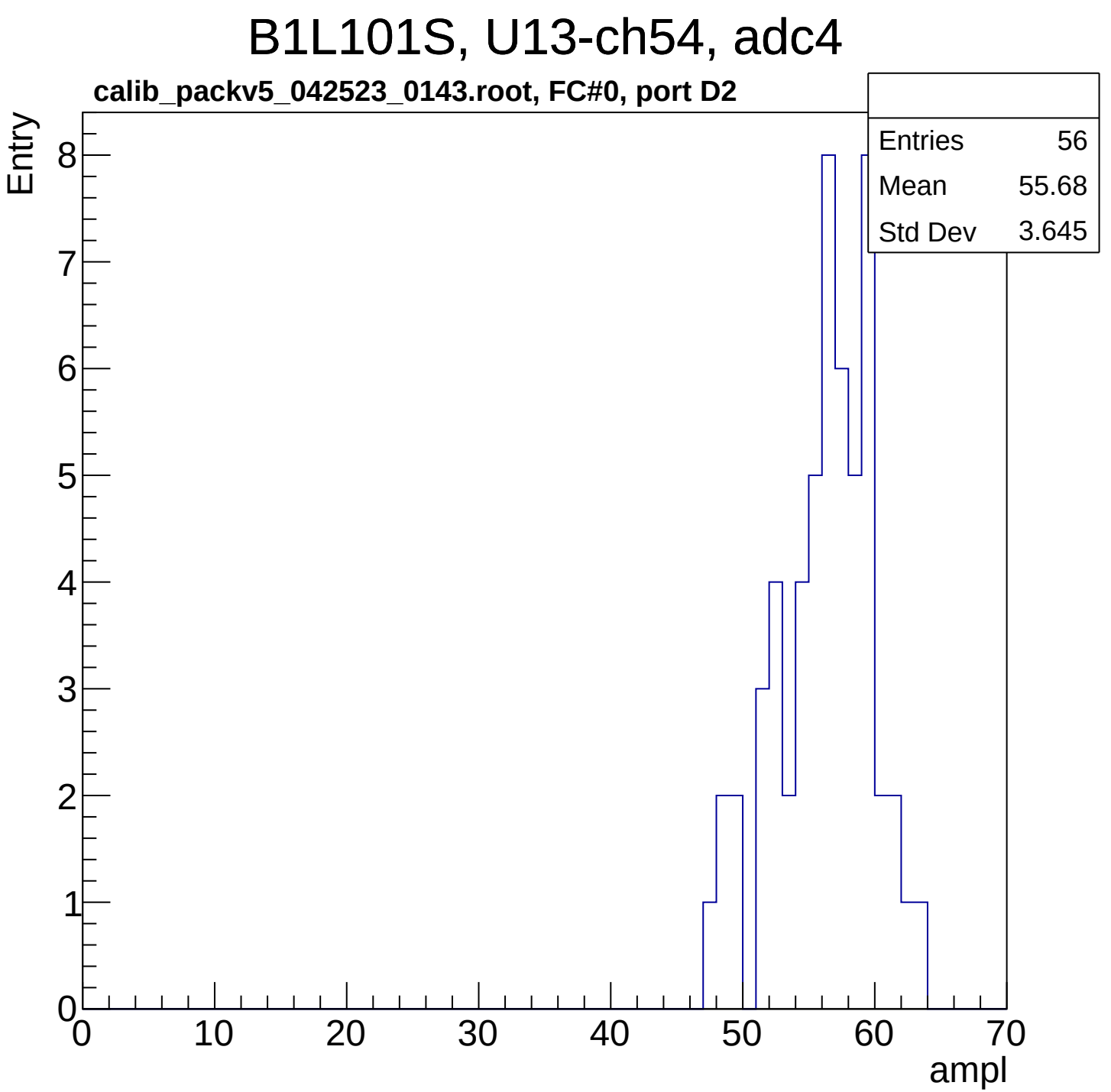
B1L101S, U13-ch54, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 55.68
Std Dev 3.645
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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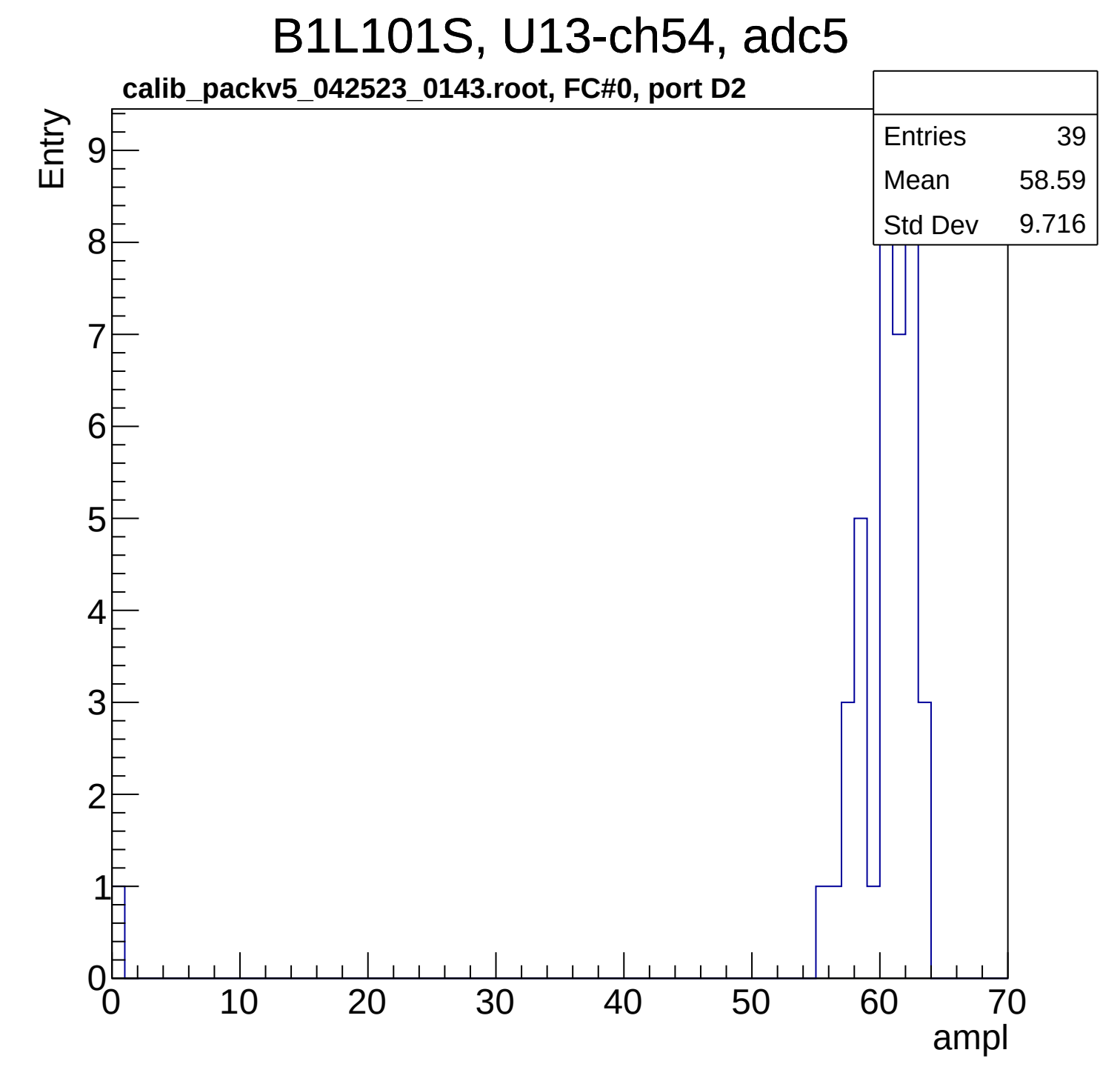
B1L101S, U13-ch54, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 39
Mean 58.59
Std Dev 9.716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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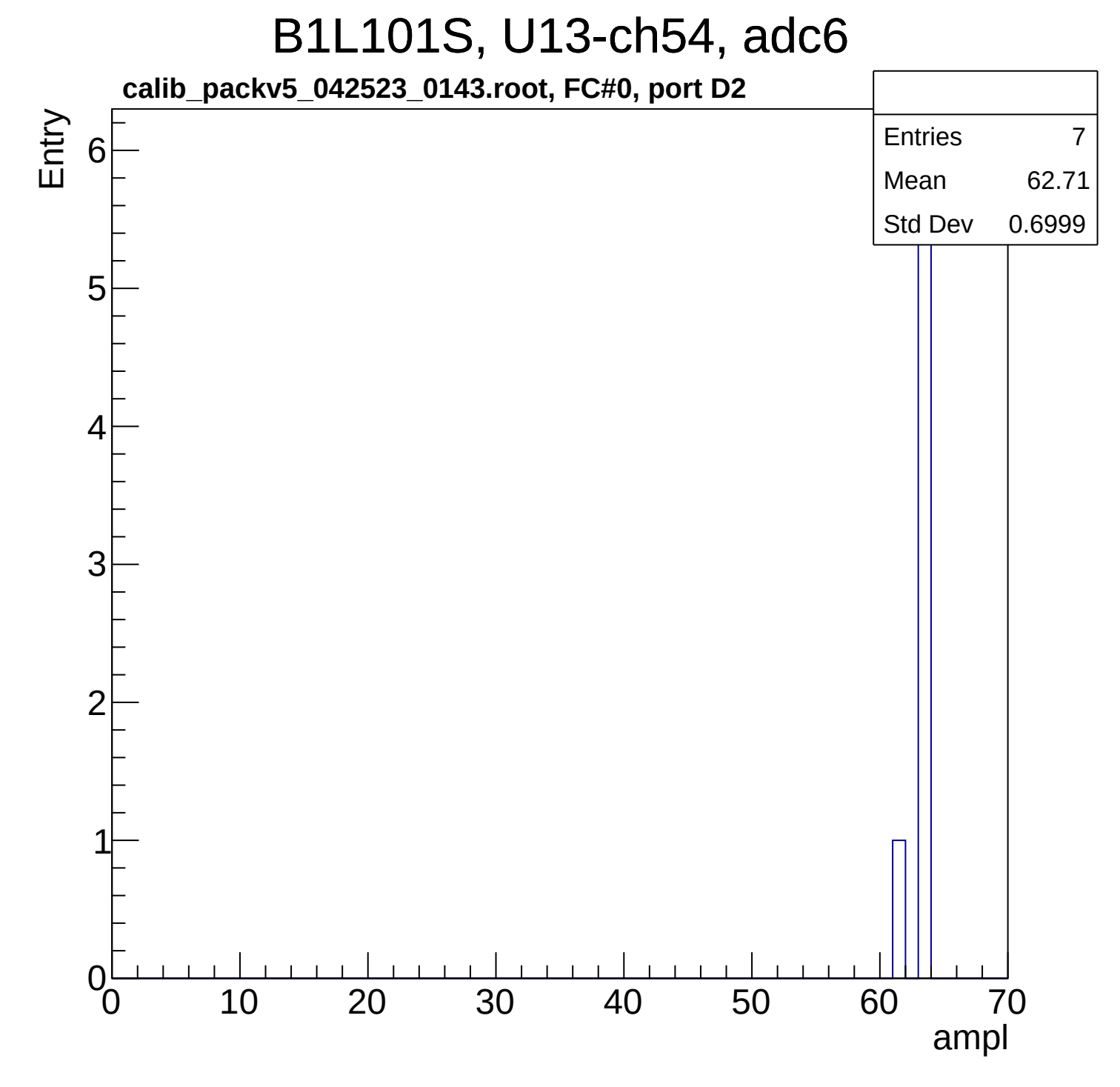
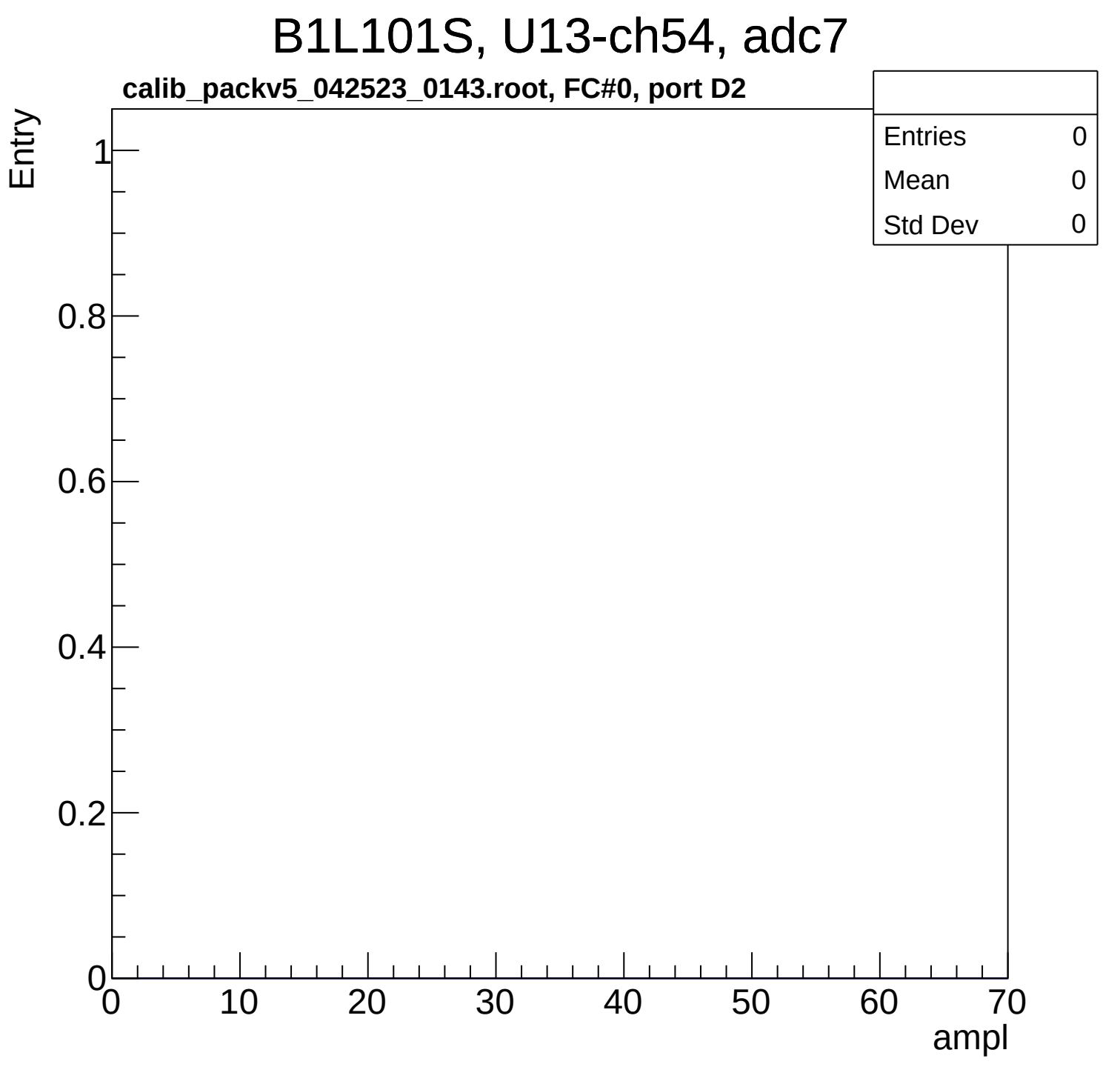
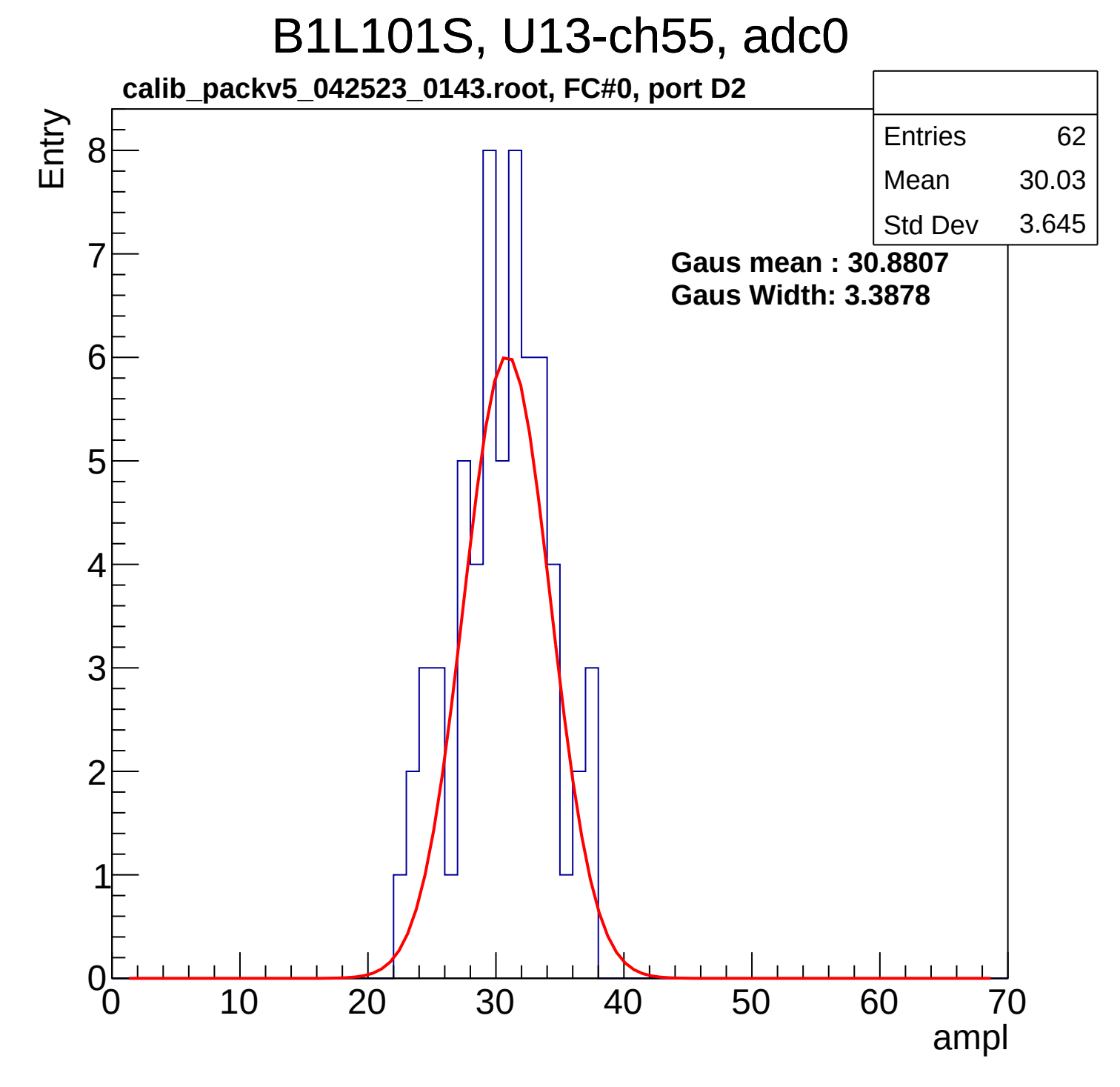

B1L101S, U13-ch54, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 7
Mean 62.71
Std Dev 0.6999
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch54, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch55, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 30.03
Std Dev 3.645
Gaus mean : 30.8807
Gaus Width: 3.3878
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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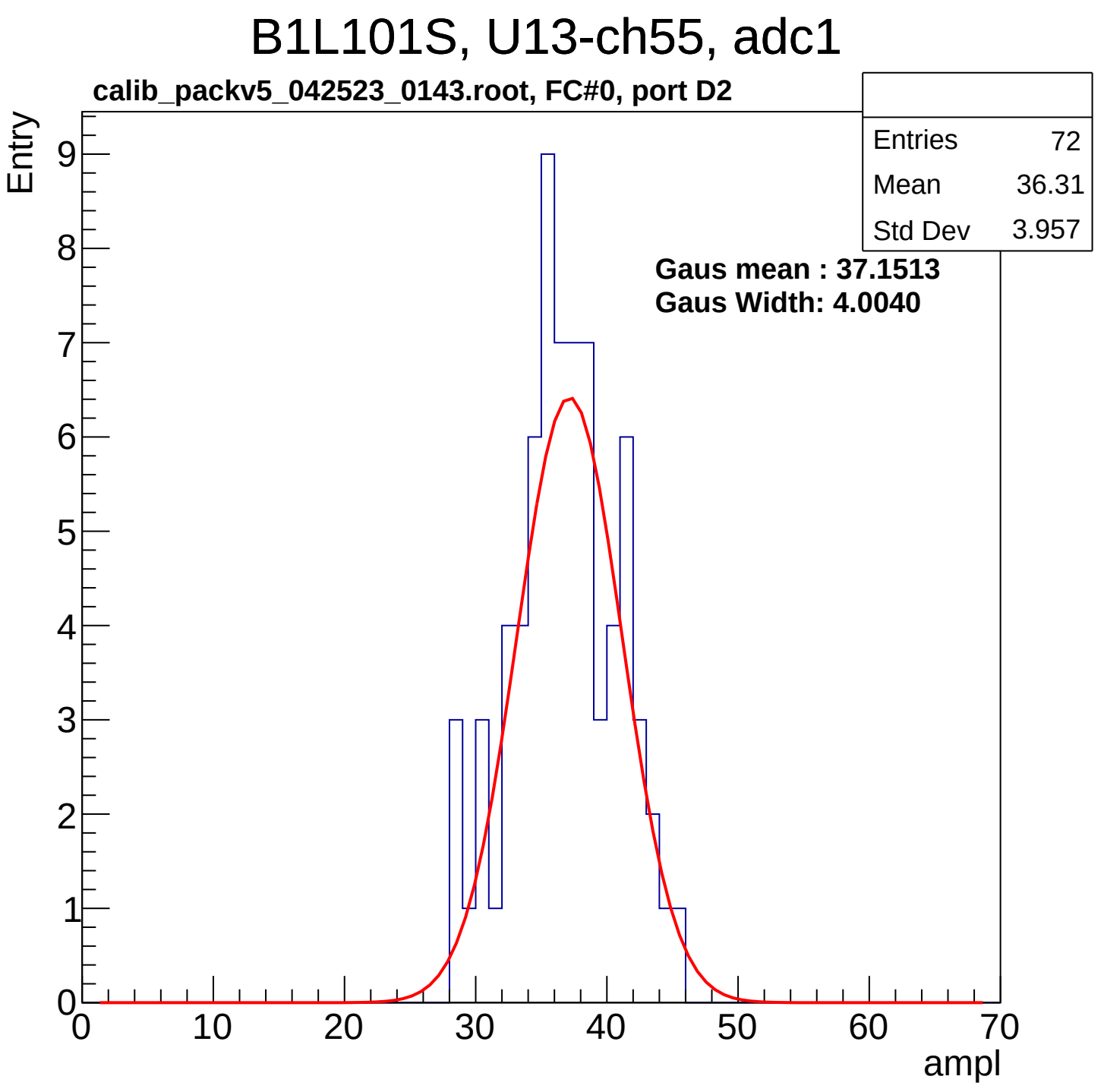
B1L101S, U13-ch55, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 36.31
Std Dev 3.957
Gaus mean : 37.1513
Gaus Width: 4.0040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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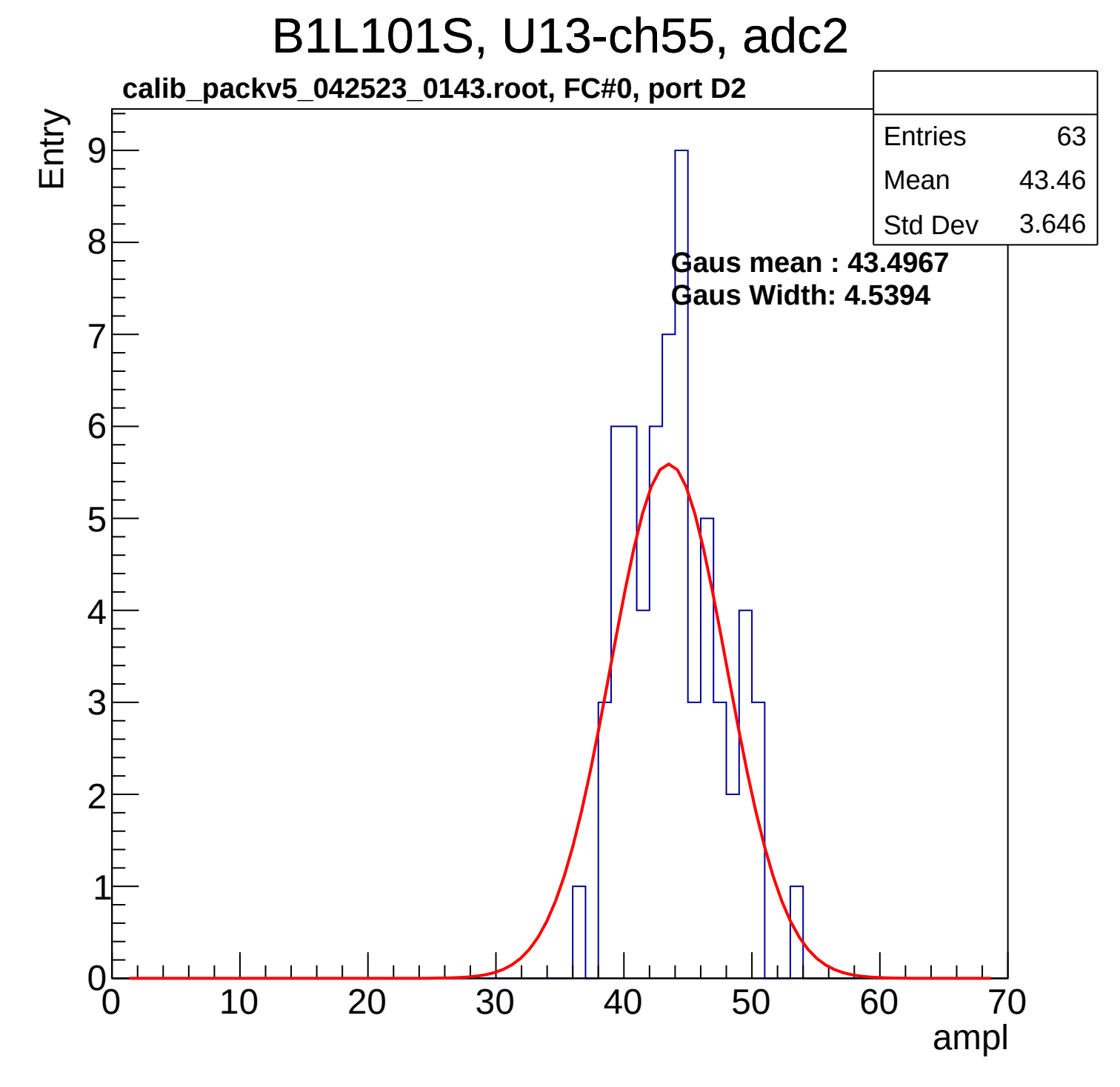
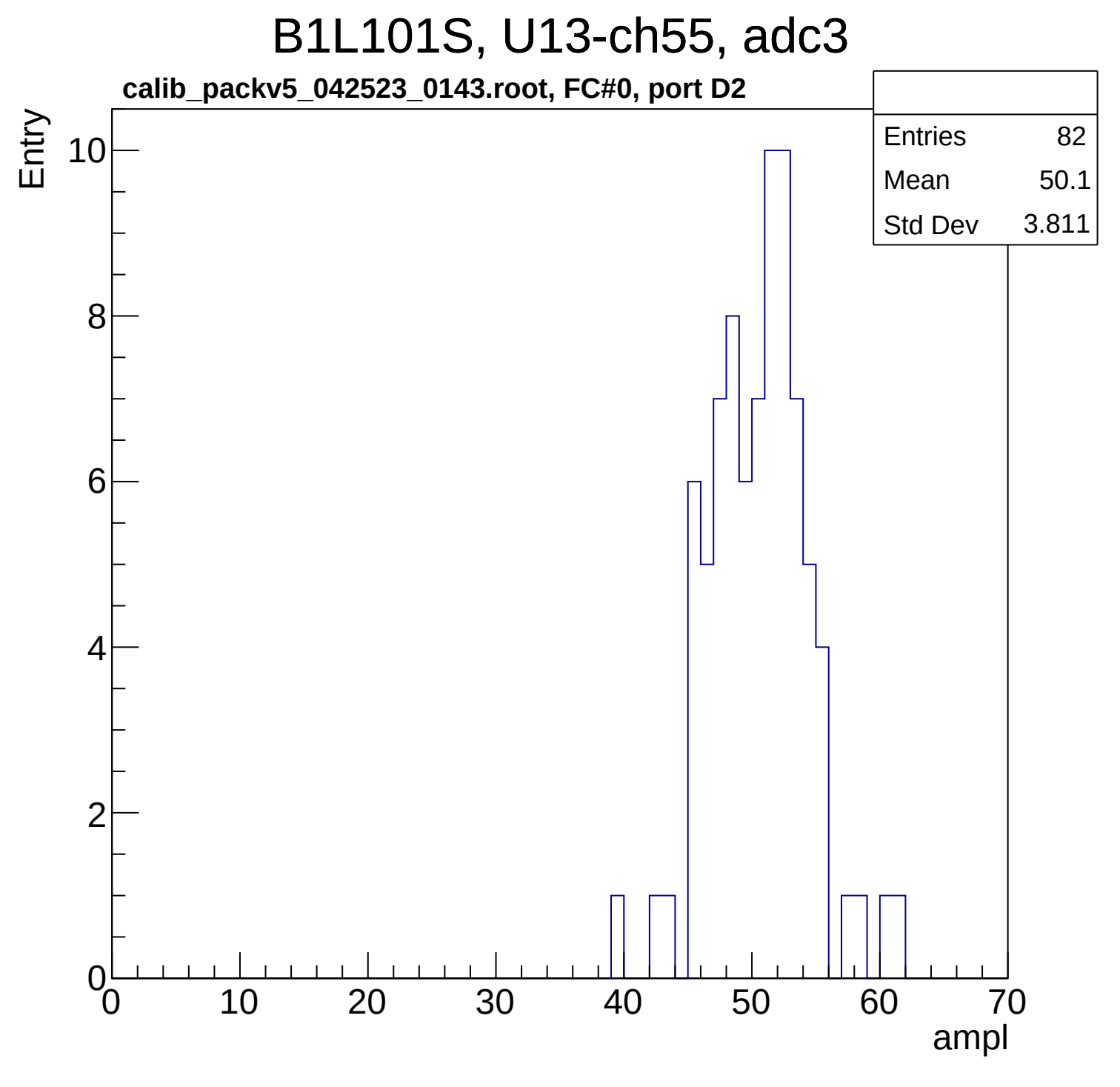
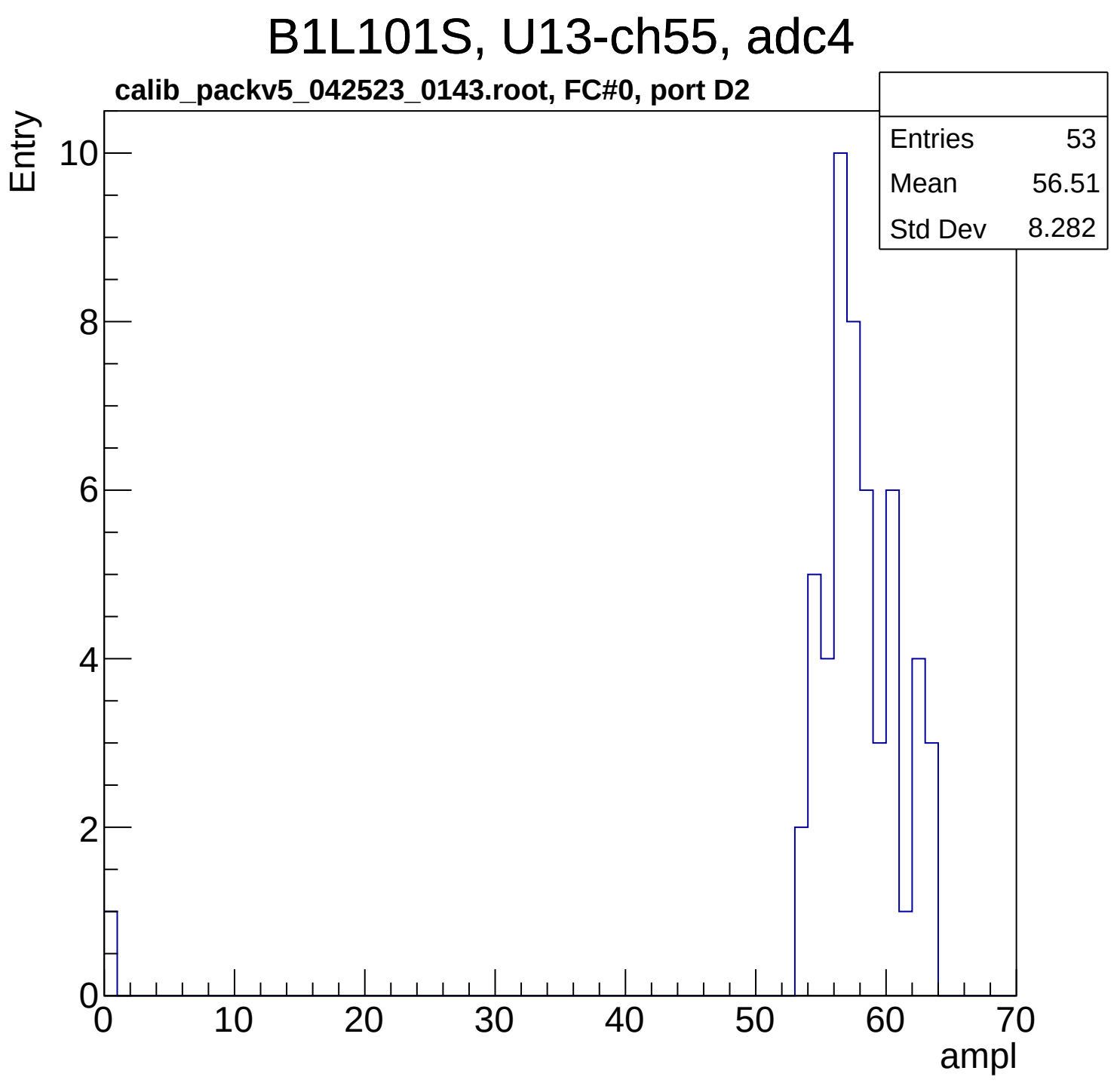
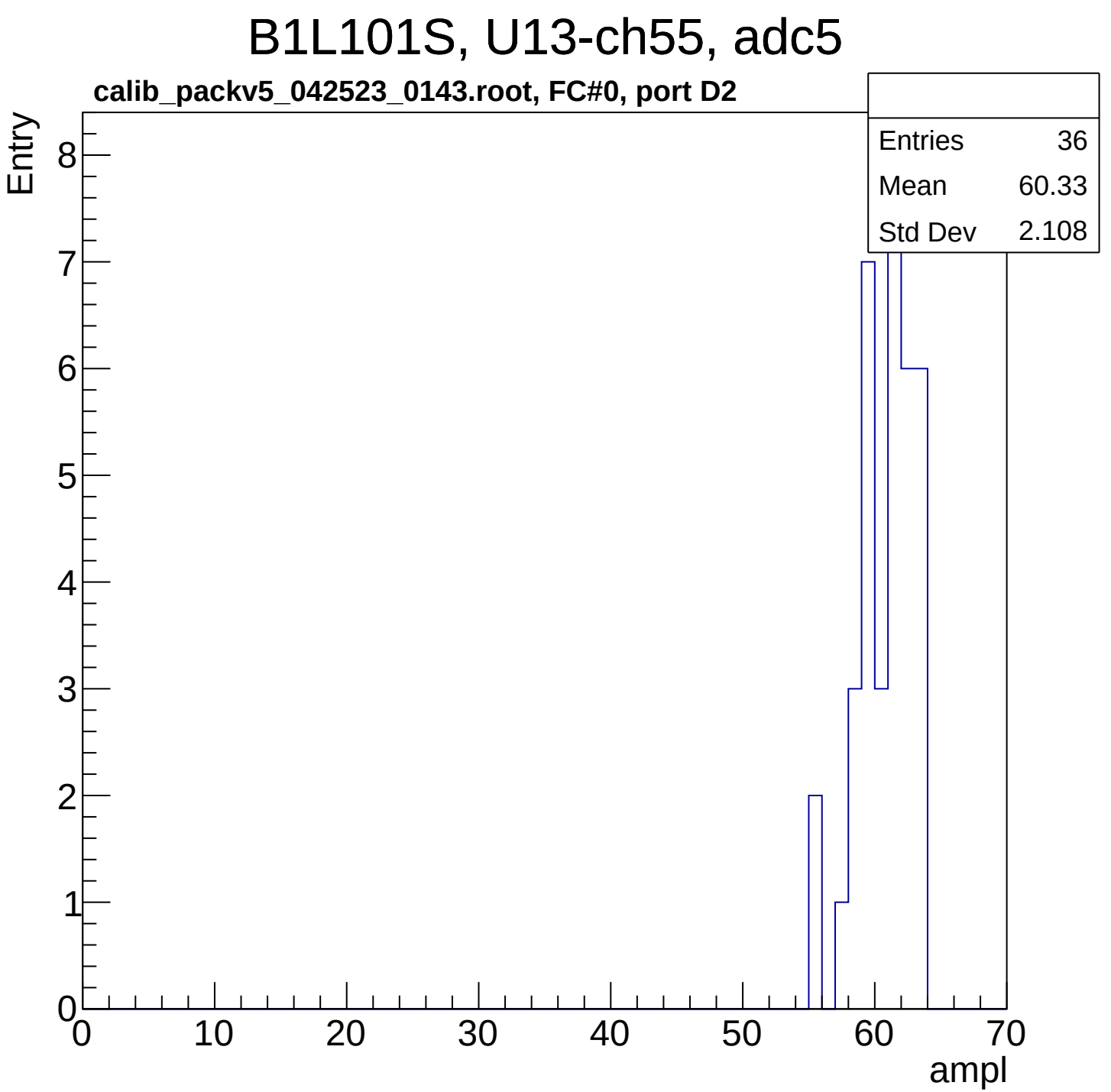

B1L101S, U13-ch55, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 43.46
Std Dev 3.646
Gaus mean : 43.4967
Gaus Width: 4.5394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch55, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 82
Mean 50.1
Std Dev 3.811
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch55, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 56.51
Std Dev 8.282
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch55, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 36
Mean 60.33
Std Dev 2.108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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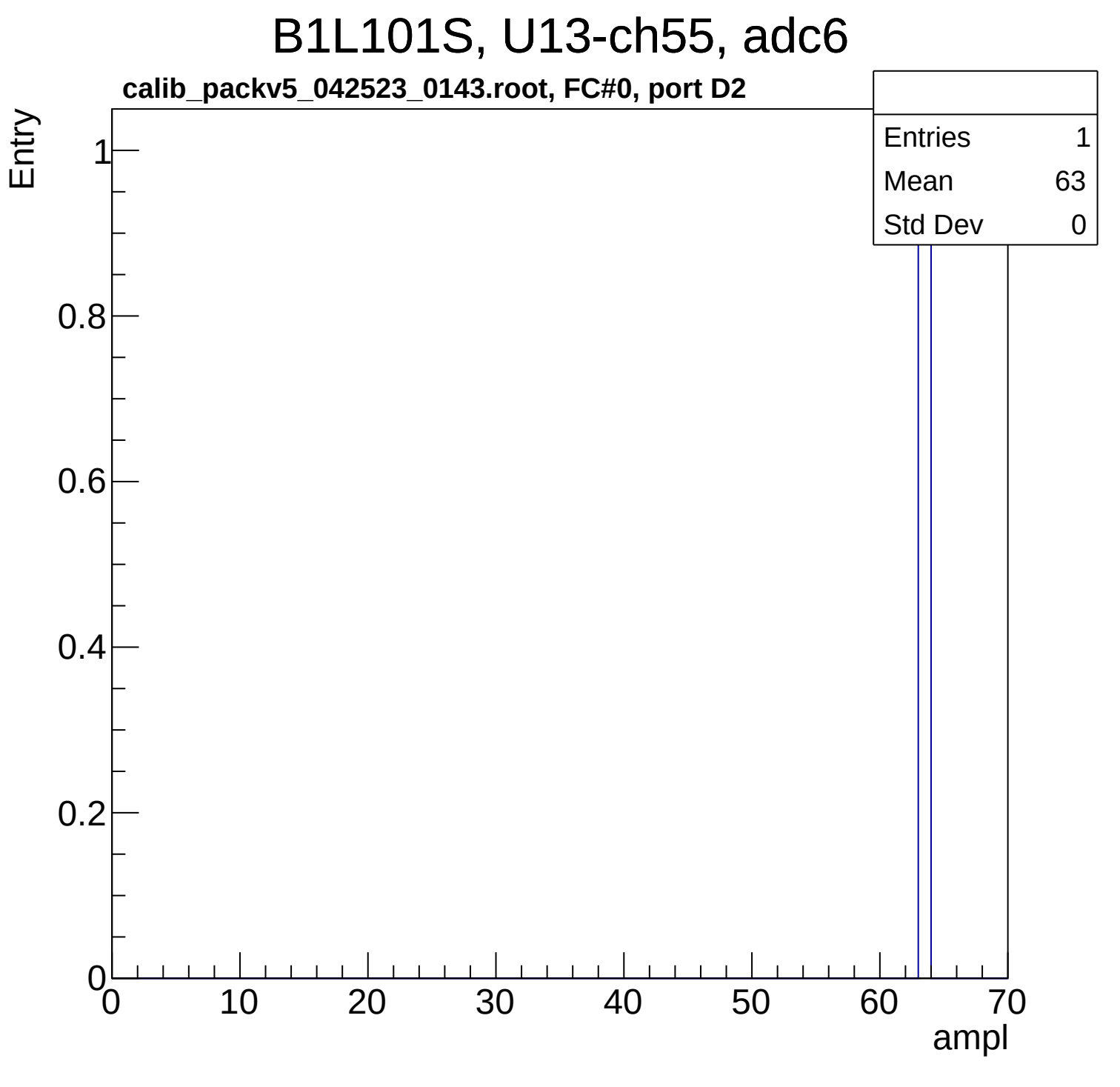
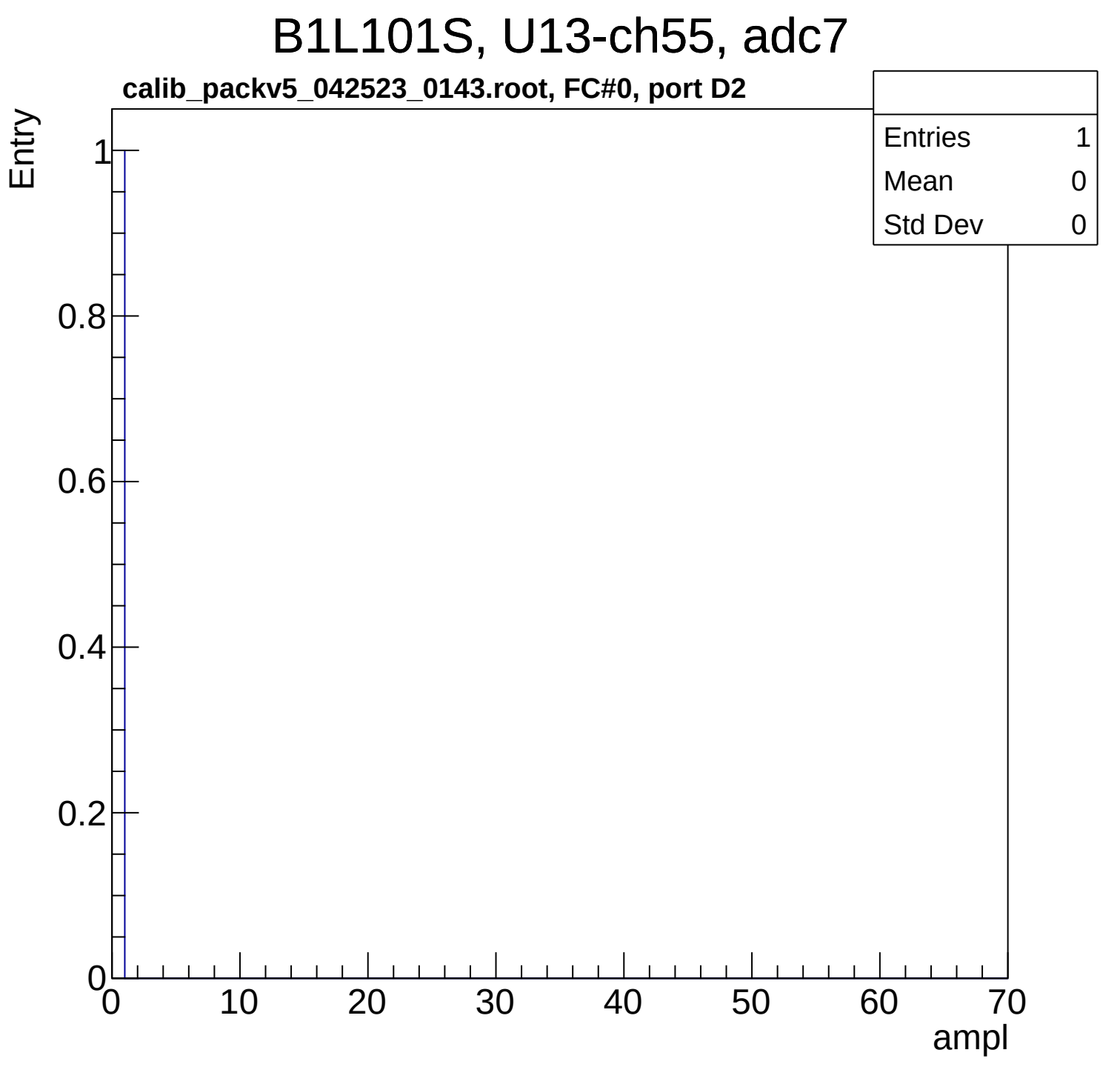
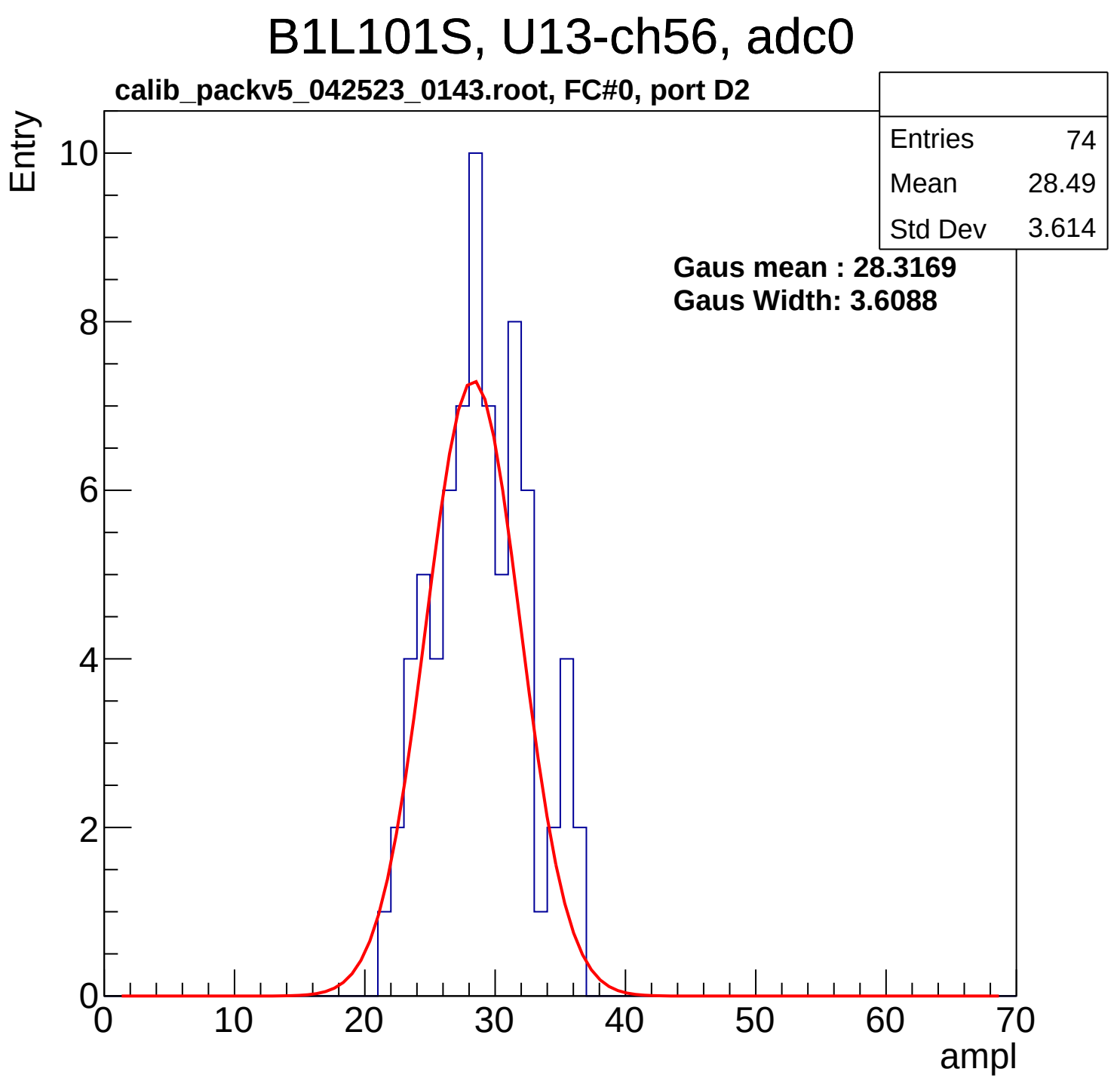
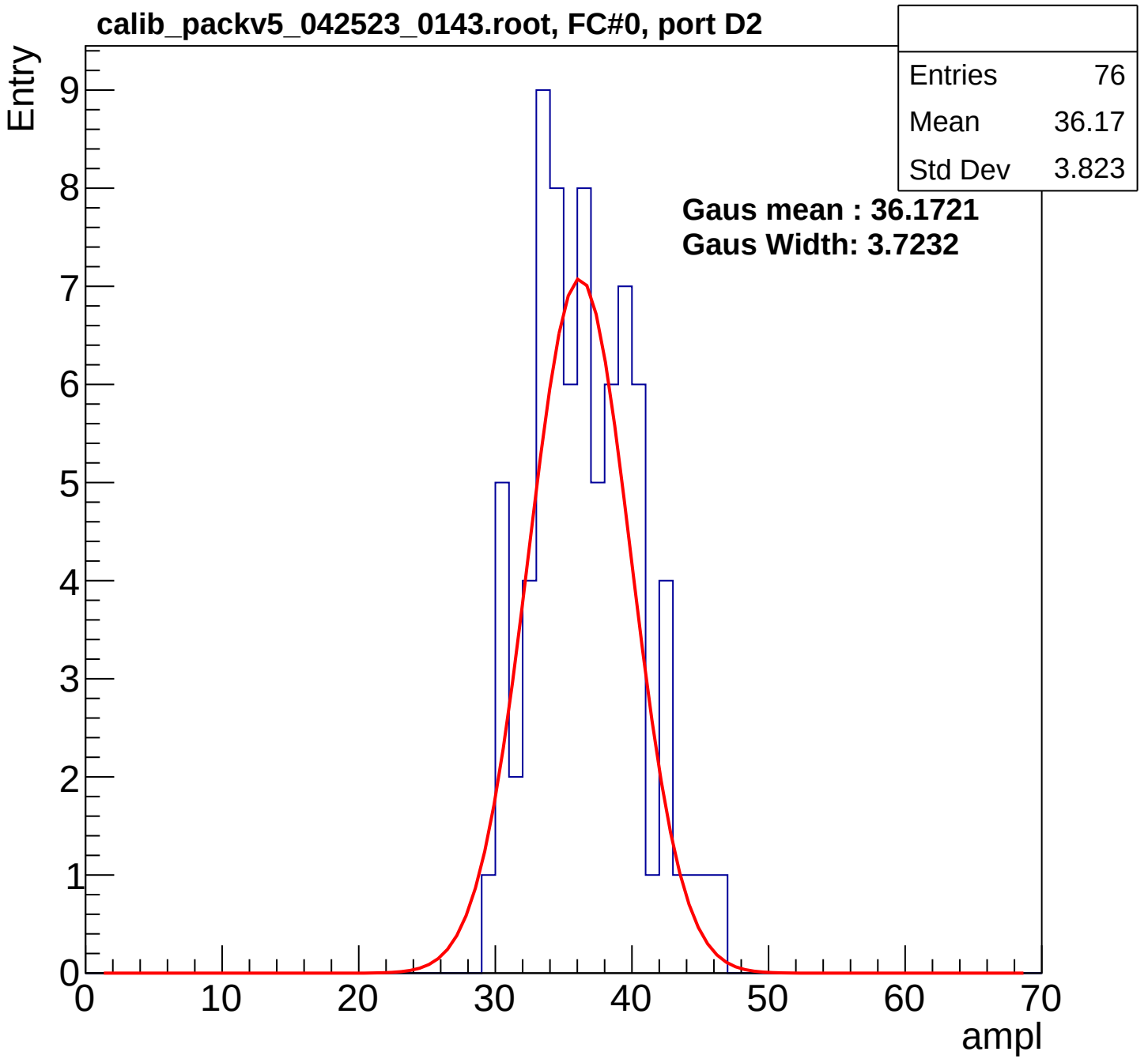

B1L101S, U13-ch55, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch55, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch56, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 28.49
Std Dev 3.614
Gaus mean : 28.3169
Gaus Width: 3.6088
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch56, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 36.17
Std Dev 3.823
Gaus mean : 36.1721
Gaus Width: 3.7232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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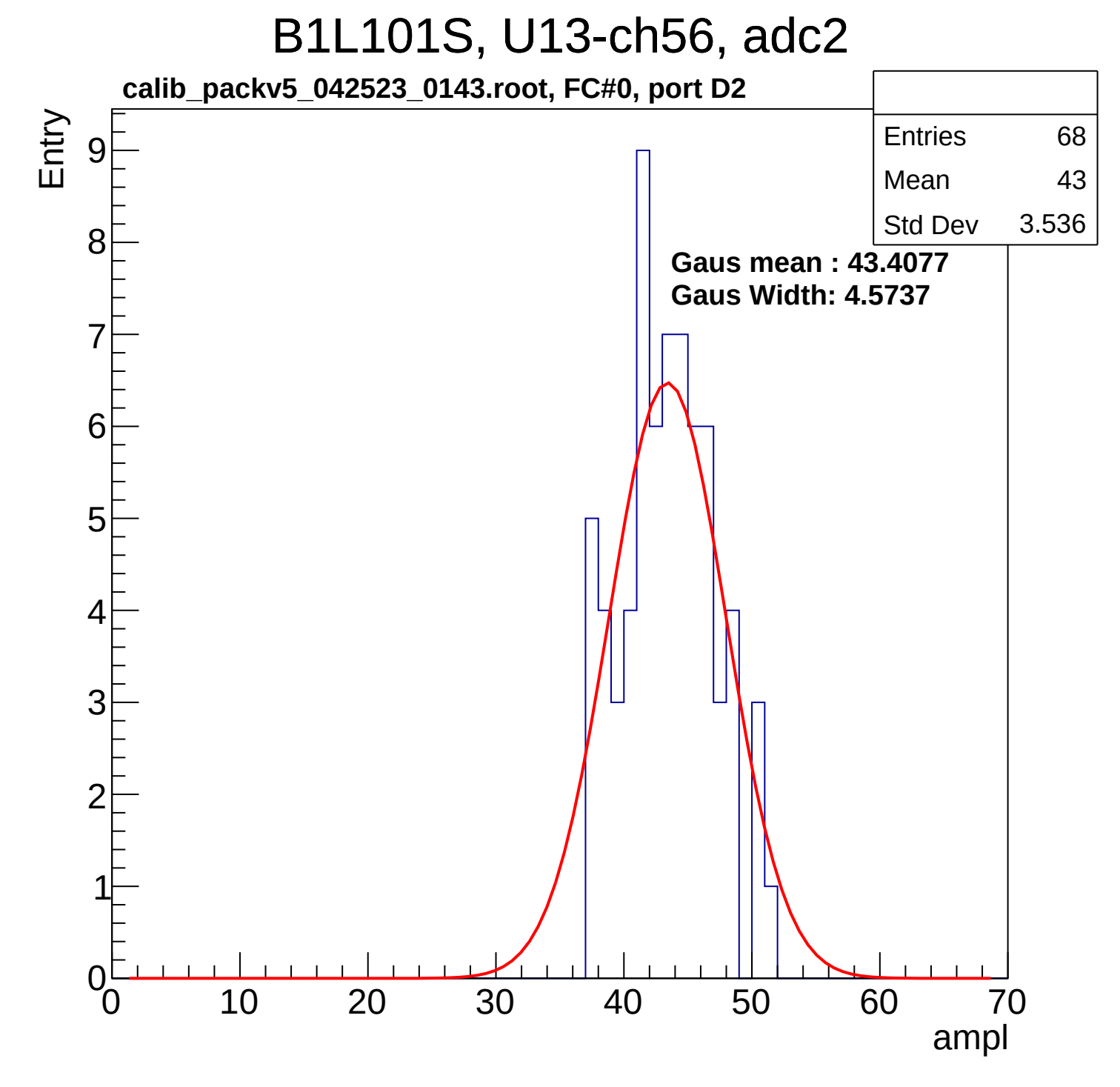
B1L101S, U13-ch56, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 43
Std Dev 3.536
Gaus mean : 43.4077
Gaus Width: 4.5737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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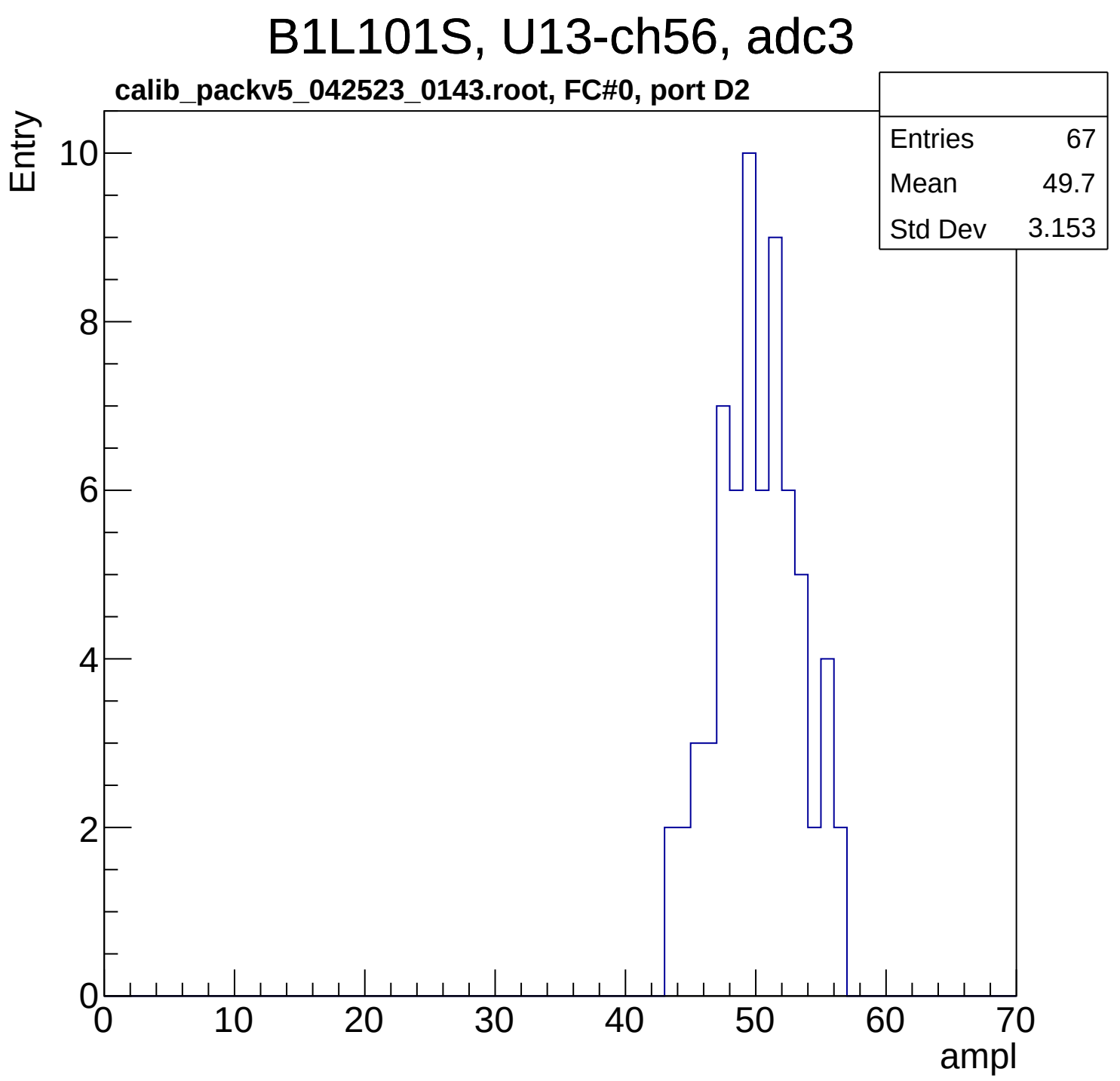
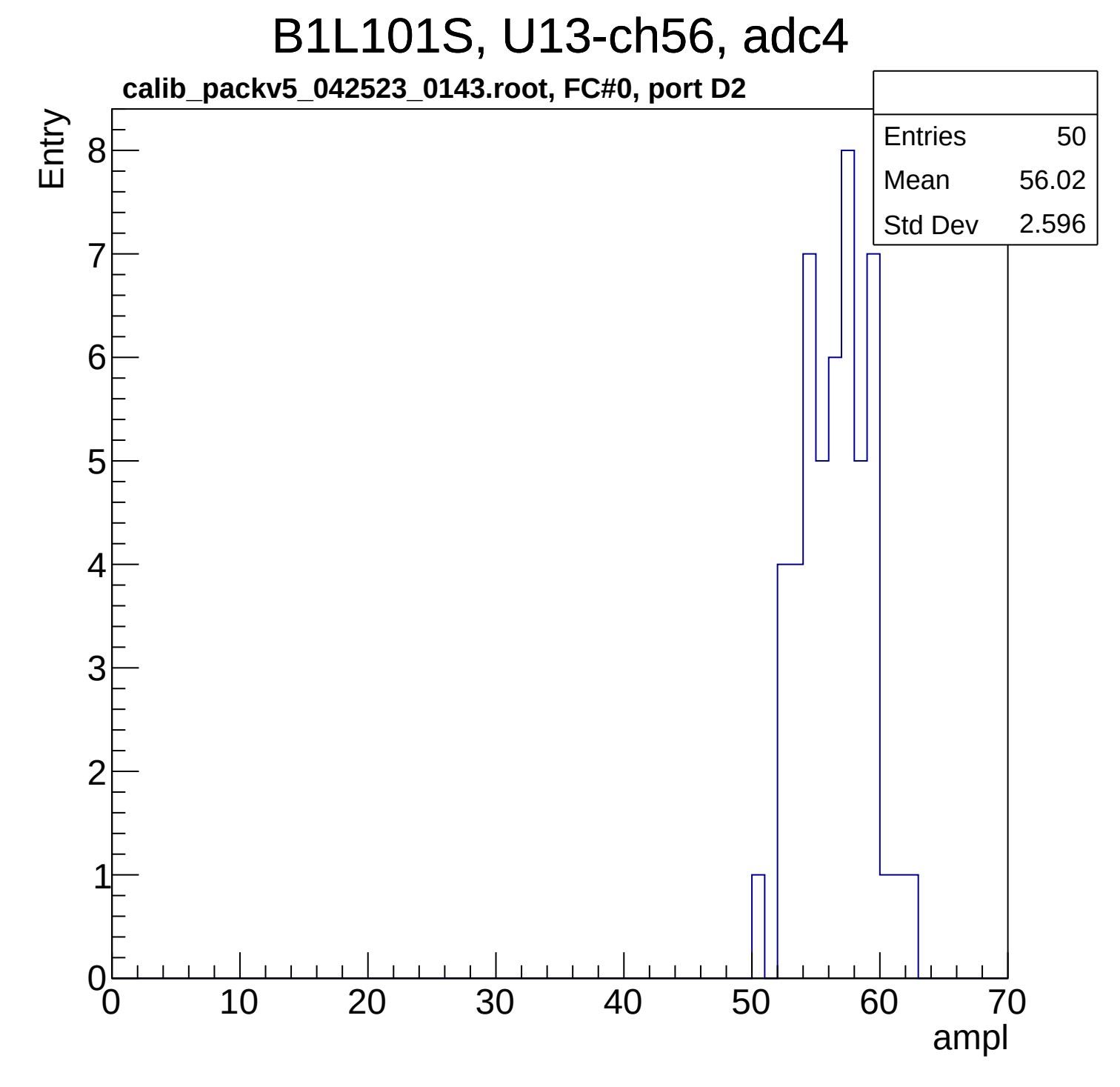
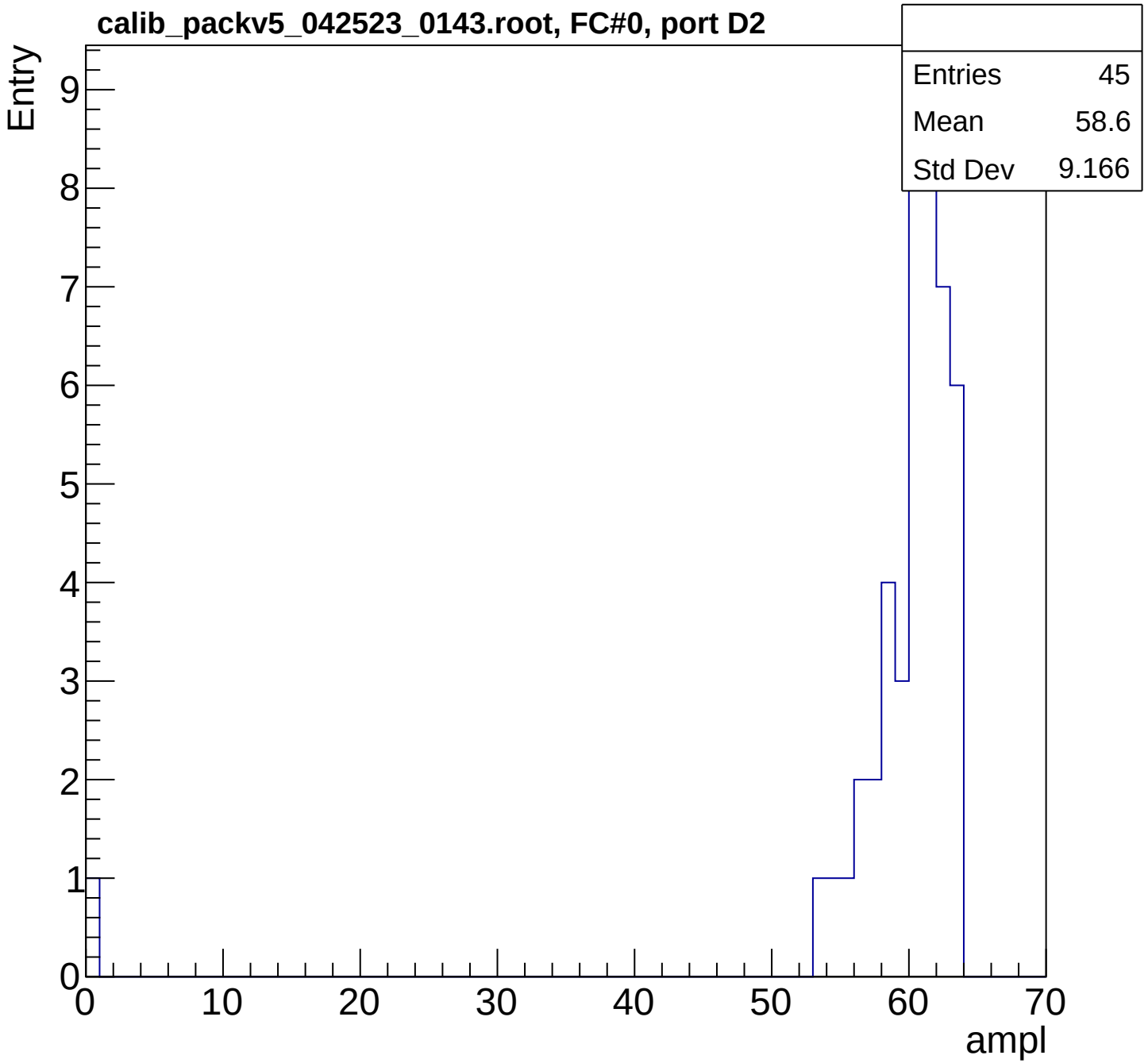
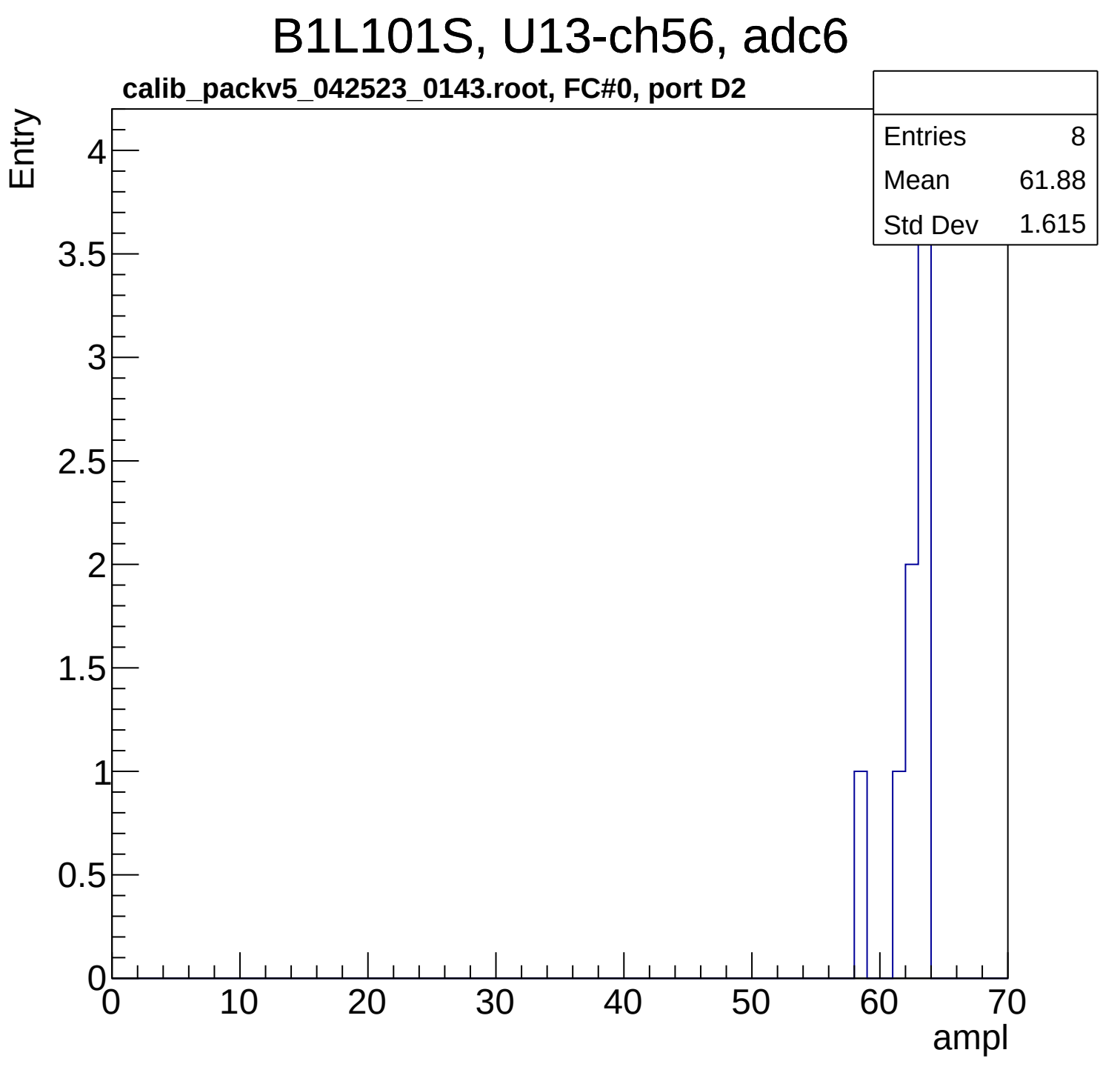

B1L101S, U13-ch56, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 49.7
Std Dev 3.153
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch56, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 50
Mean 56.02
Std Dev 2.596
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch56, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 58.6
Std Dev 9.166
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L101S, U13-ch56, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 8
Mean 61.88
Std Dev 1.615
Entry
ampl
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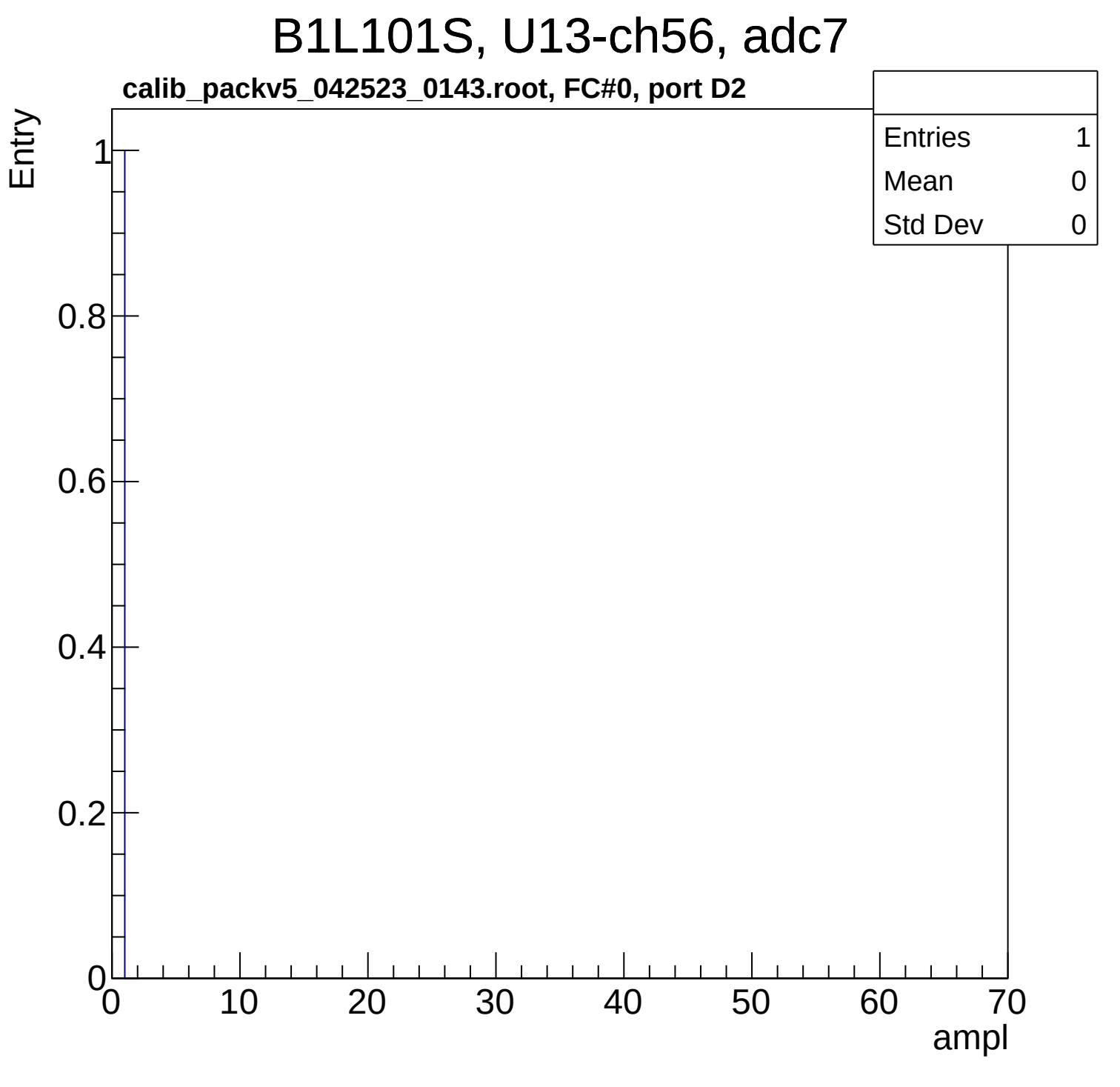
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L101S, U13-ch56, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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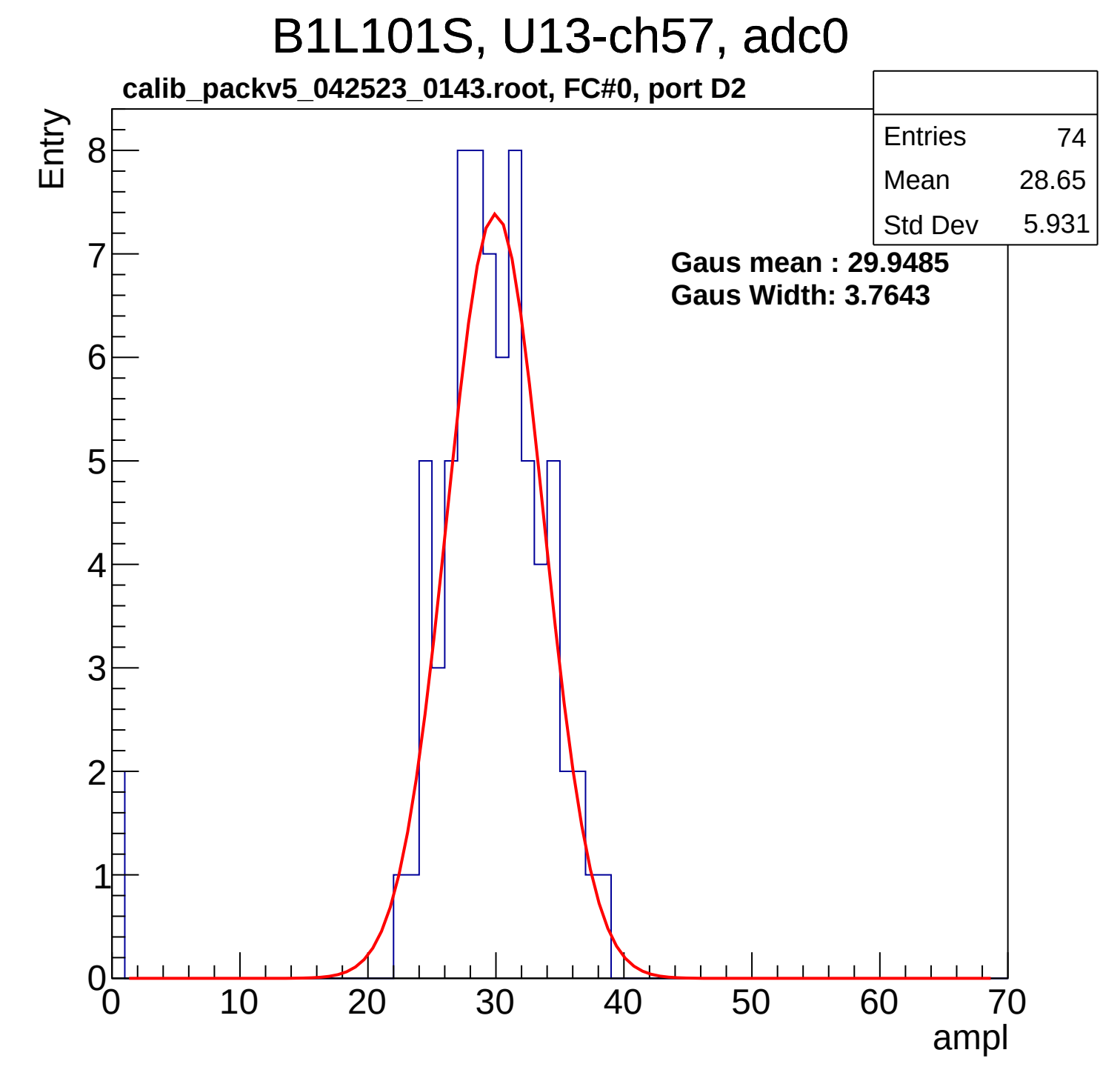
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch57, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 28.65
Std Dev 5.931
Gaus mean : 29.9485
Gaus Width: 3.7643
Entry
ampl
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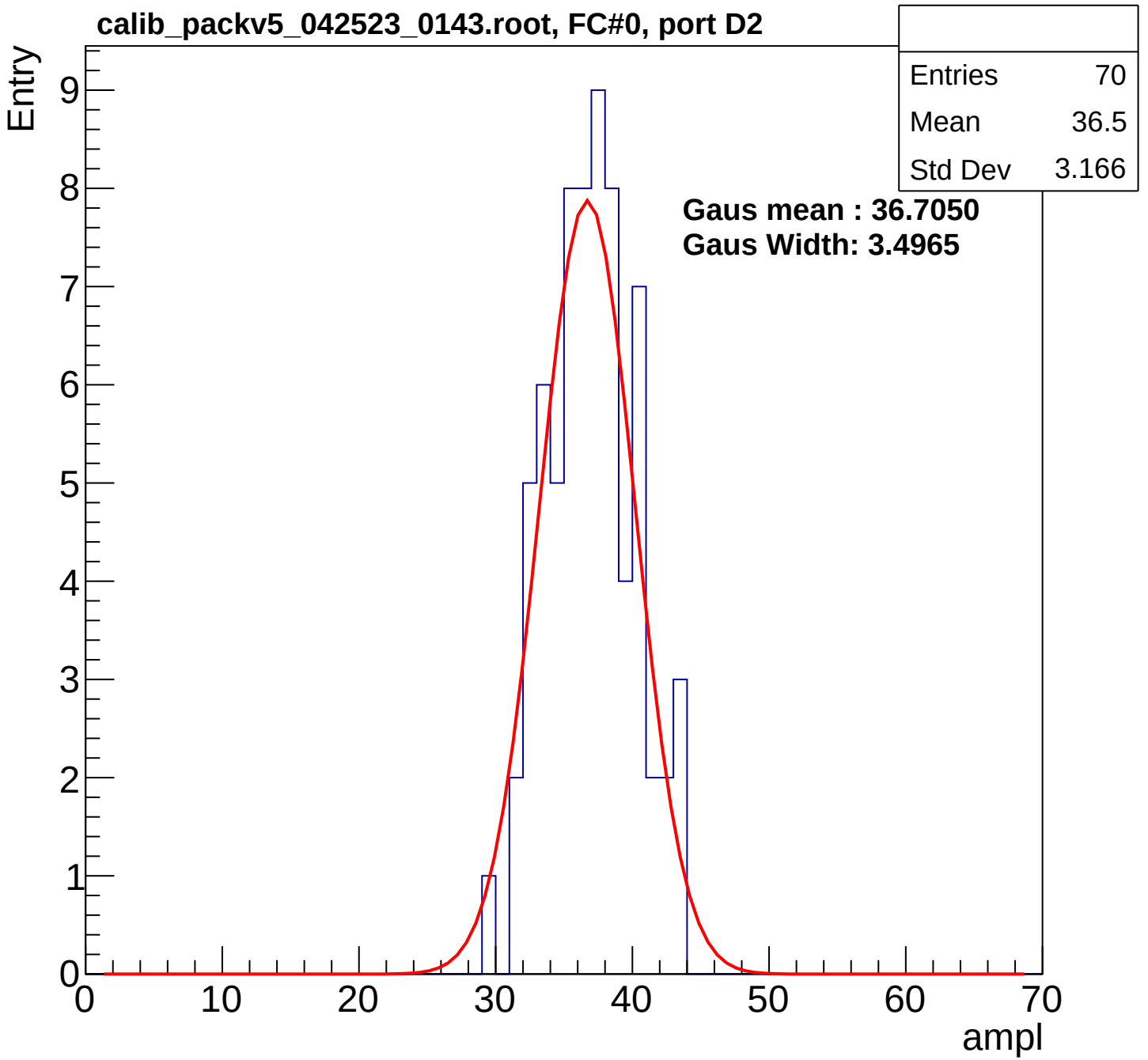
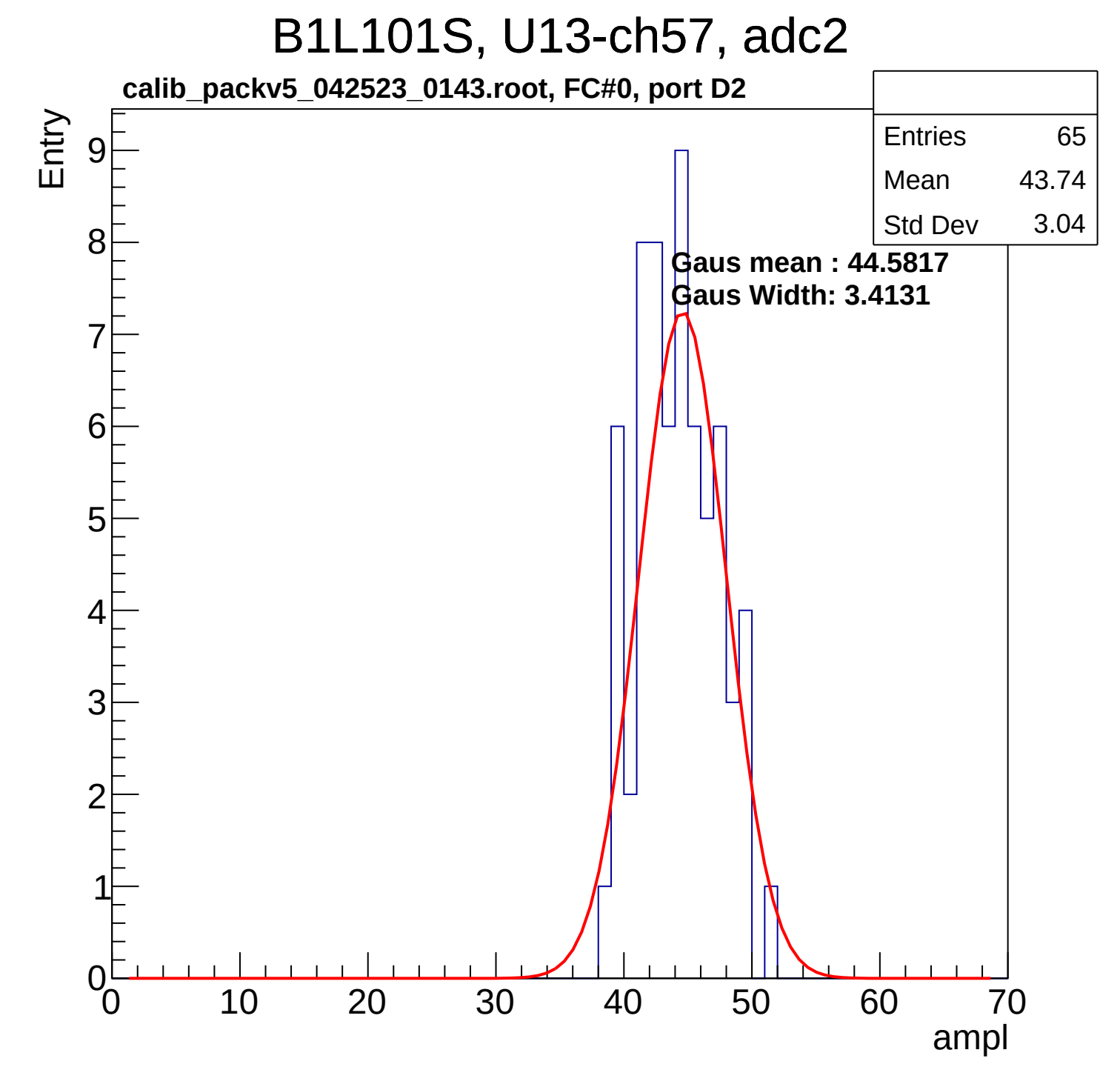
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch57, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 36.5
Std Dev 3.166
Gaus mean : 36.7050
Gaus Width: 3.4965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch57, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 43.74
Std Dev 3.04
Gaus mean : 44.5817
Gaus Width: 3.4131
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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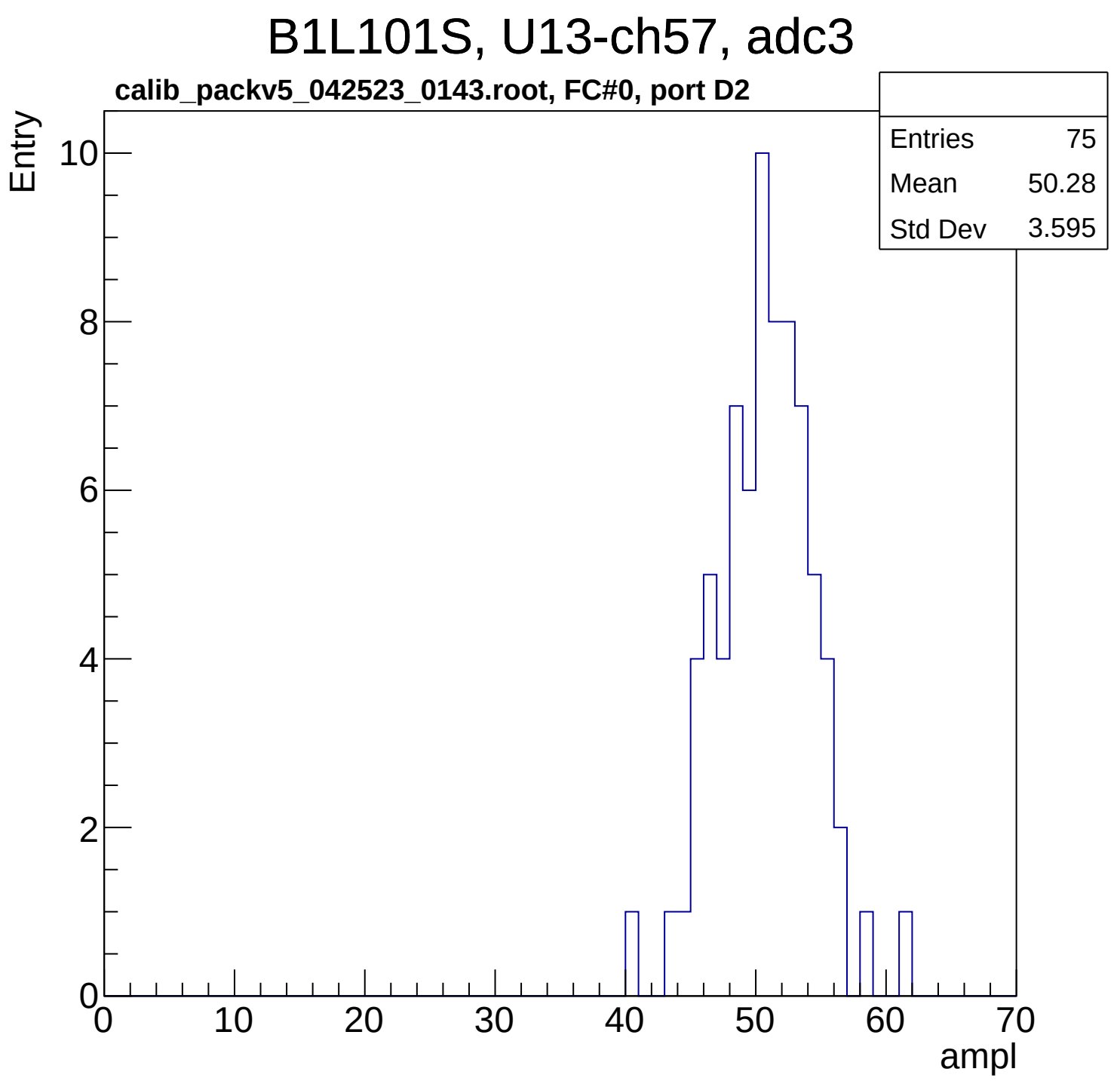
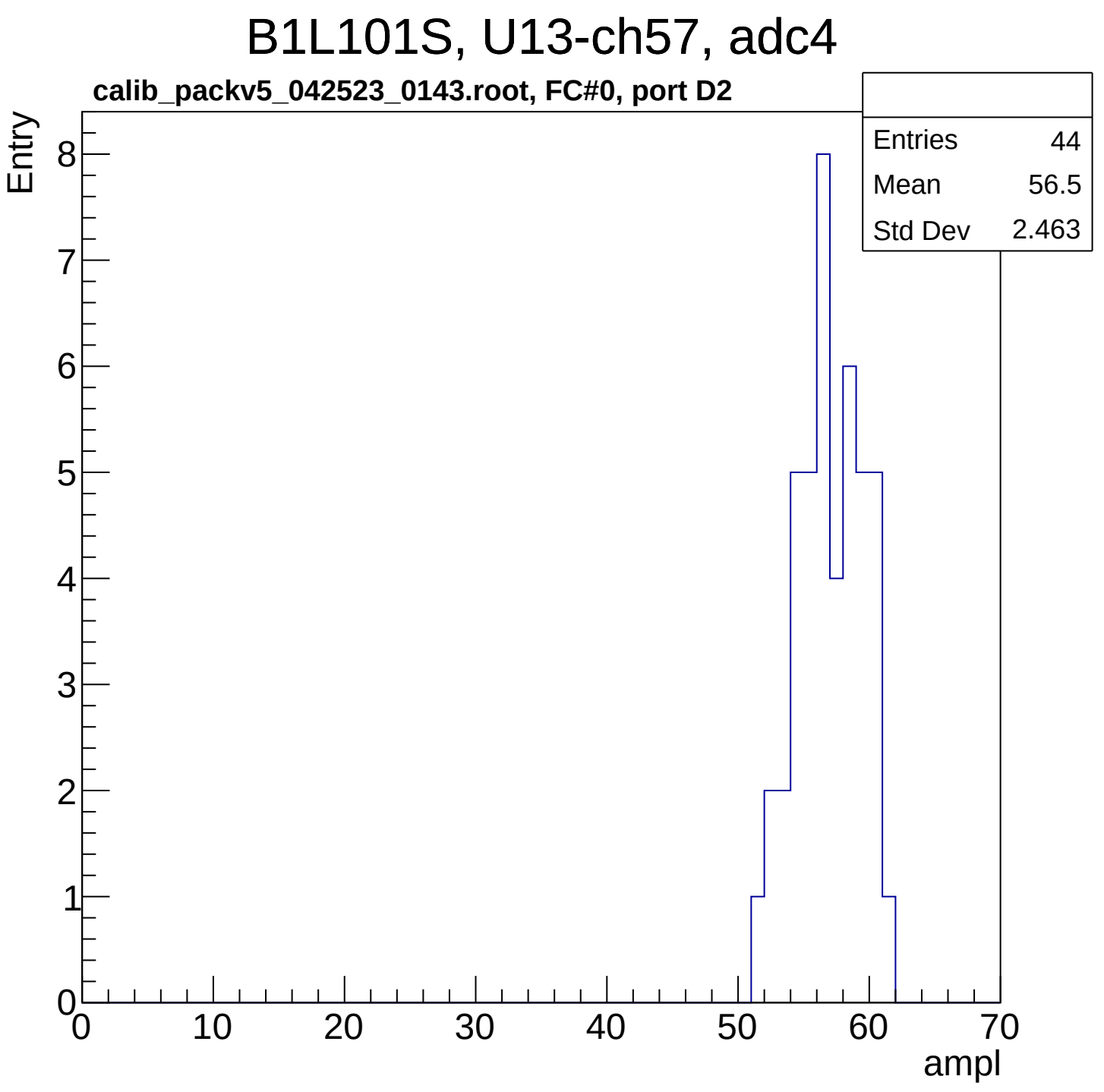
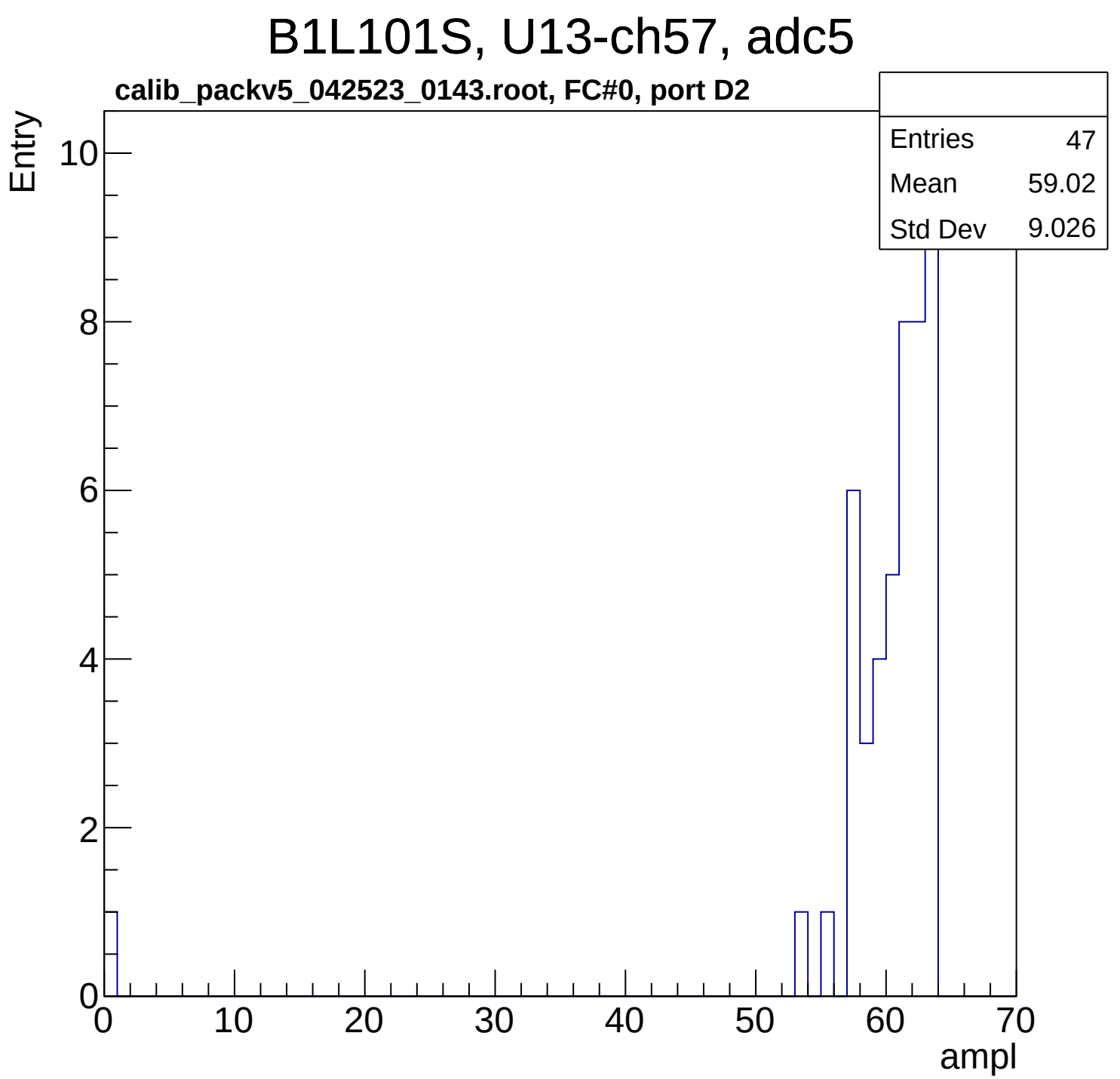
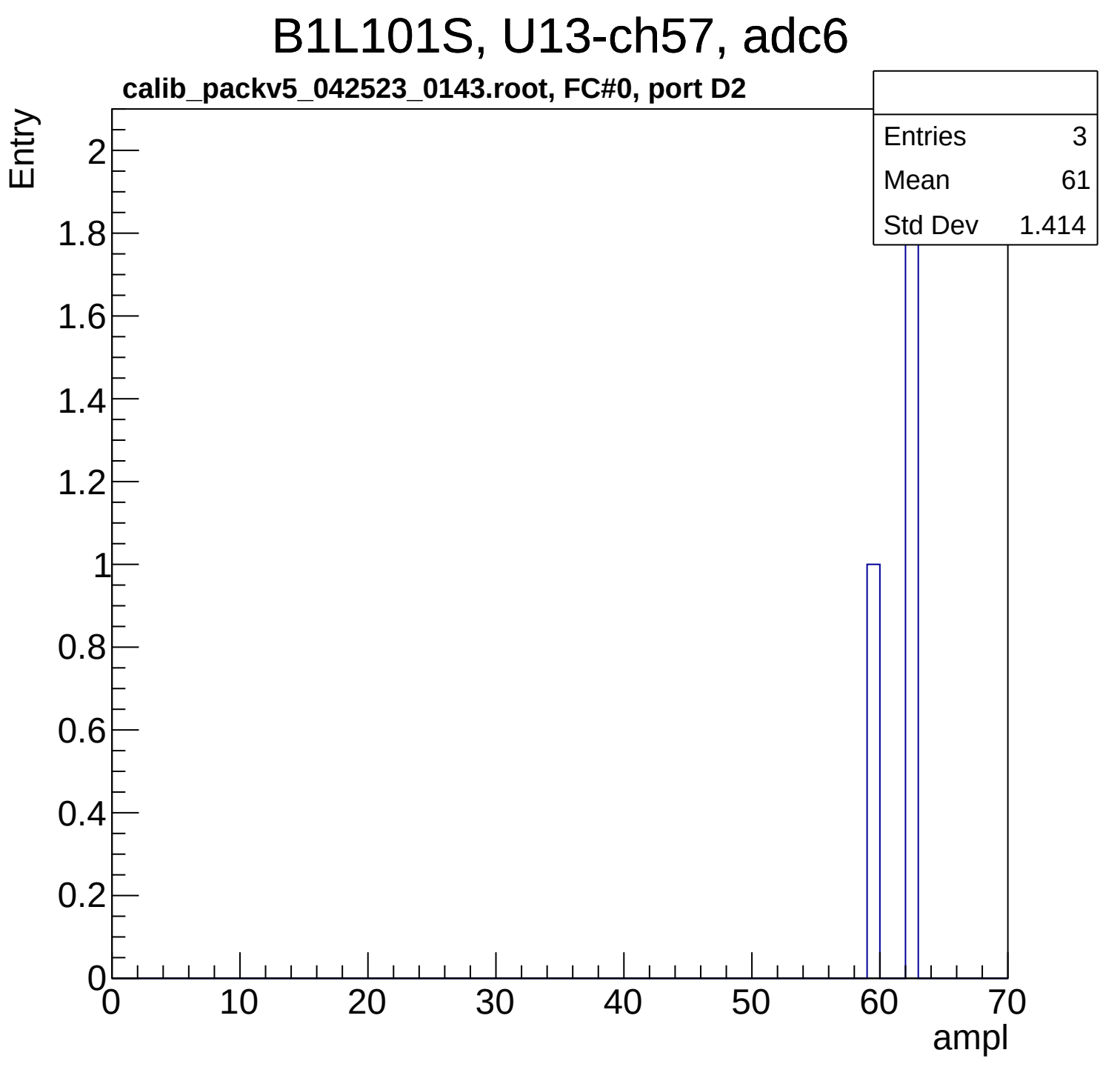
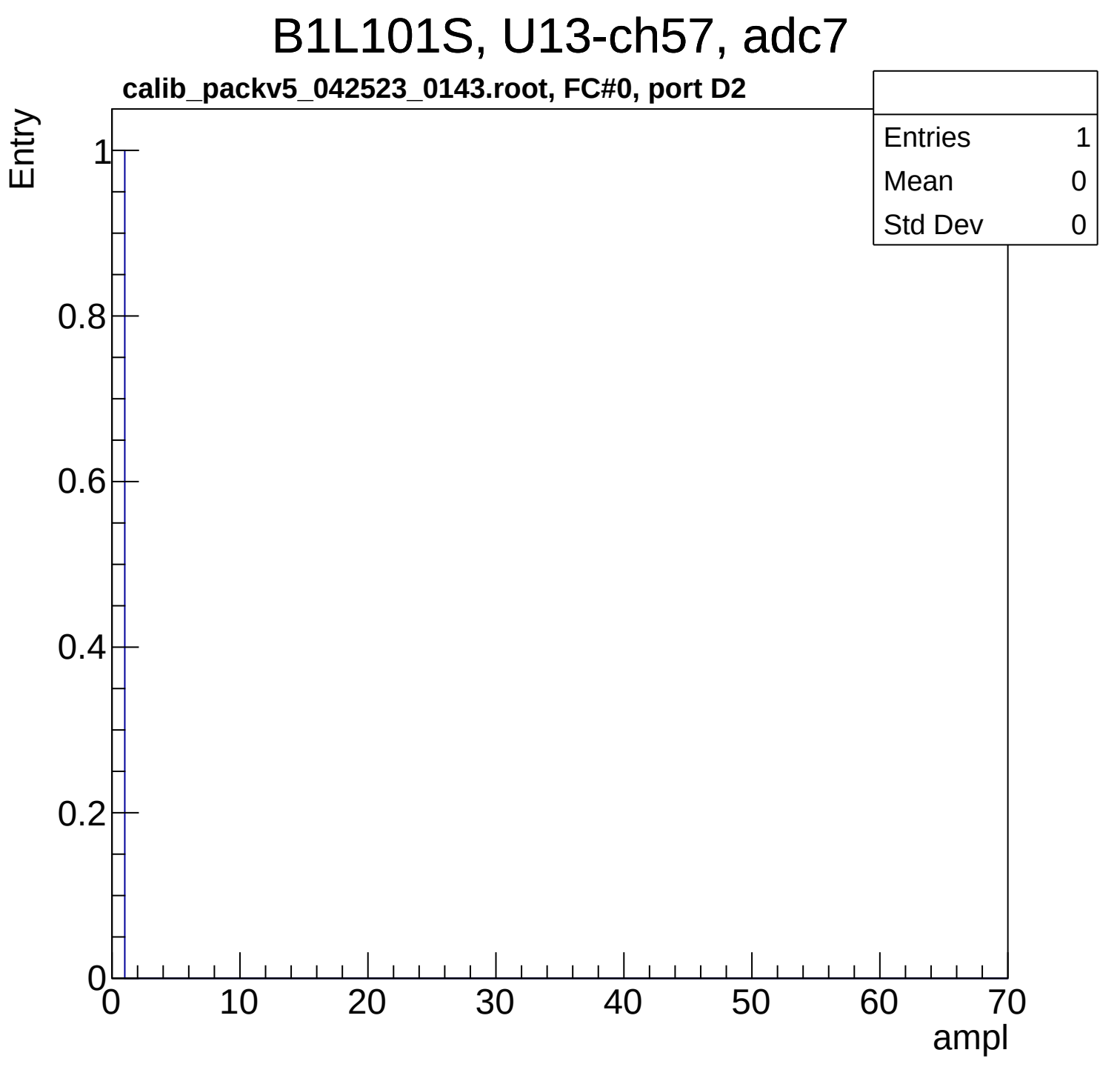
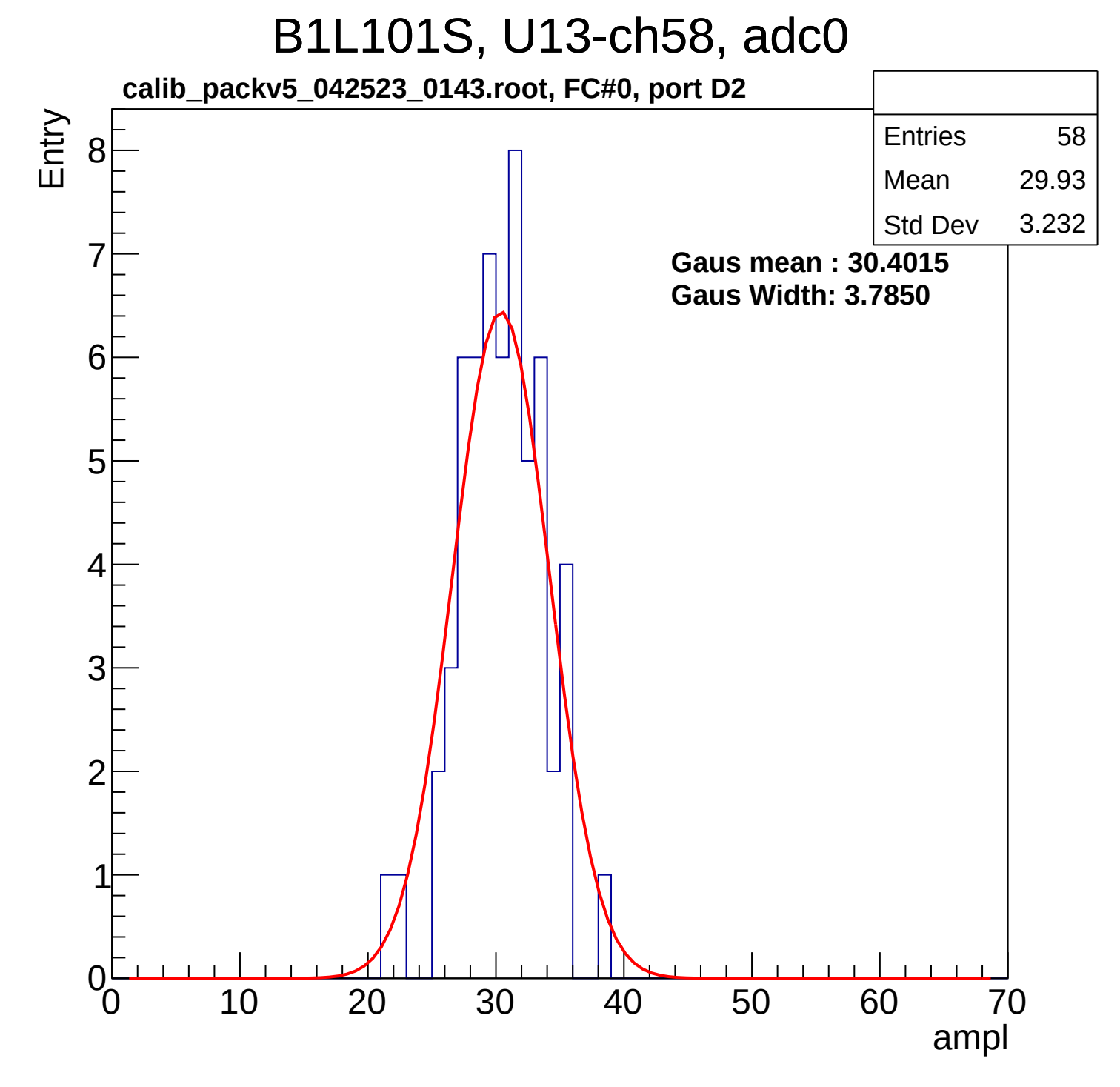

B1L101S, U13-ch57, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 50.28
Std Dev 3.595
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch57, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 56.5
Std Dev 2.463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch57, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 47
Mean 59.02
Std Dev 9.026
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch57, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 61
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch57, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch58, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 29.93
Std Dev 3.232
Gaus mean : 30.4015
Gaus Width: 3.7850
Entry
ampl
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1
2
3
4
5
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7
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0
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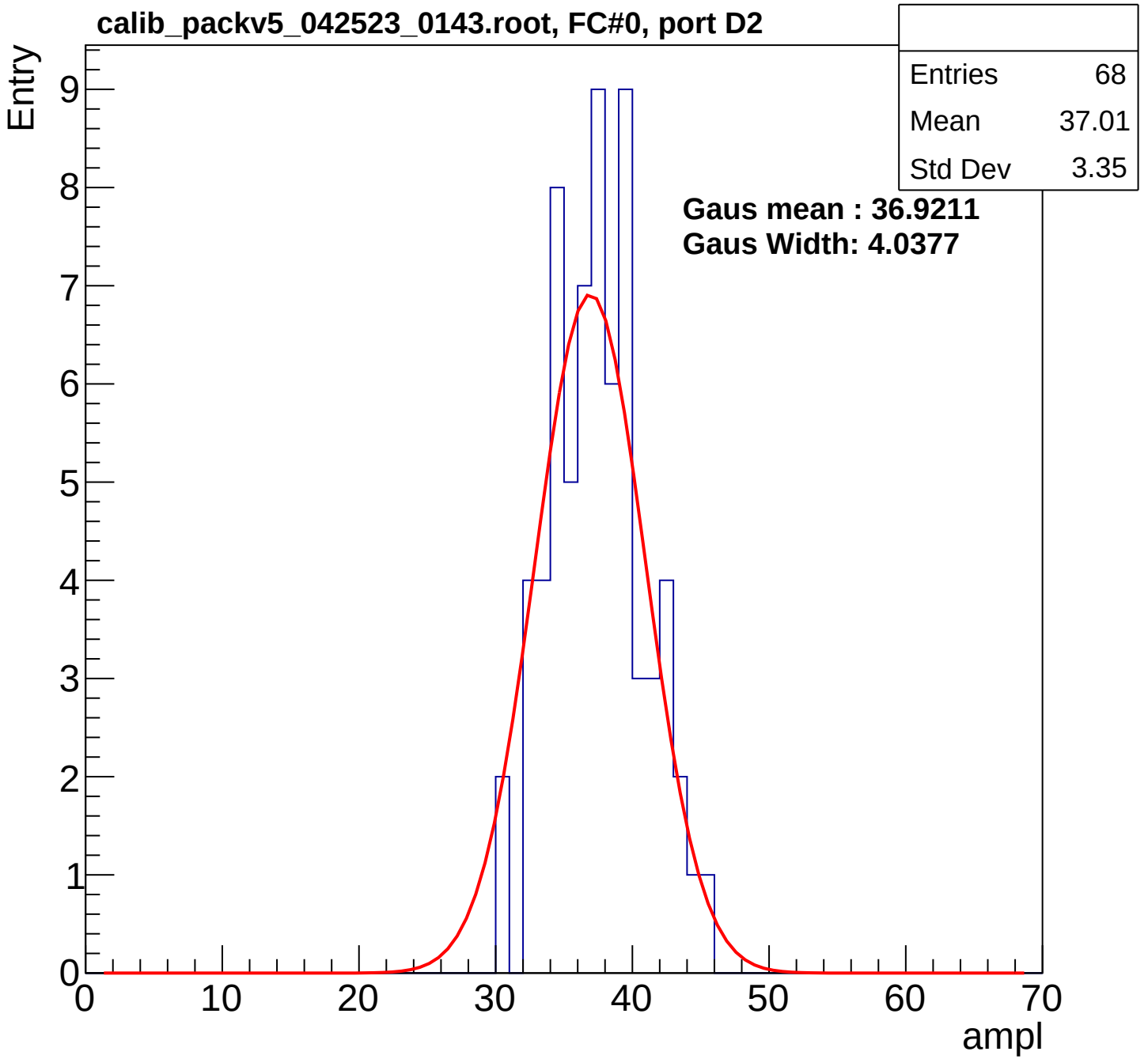
B1L101S, U13-ch58, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 37.01
Std Dev 3.35
Gaus mean : 36.9211
Gaus Width: 4.0377
Entry
ampl
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3
4
5
6
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0
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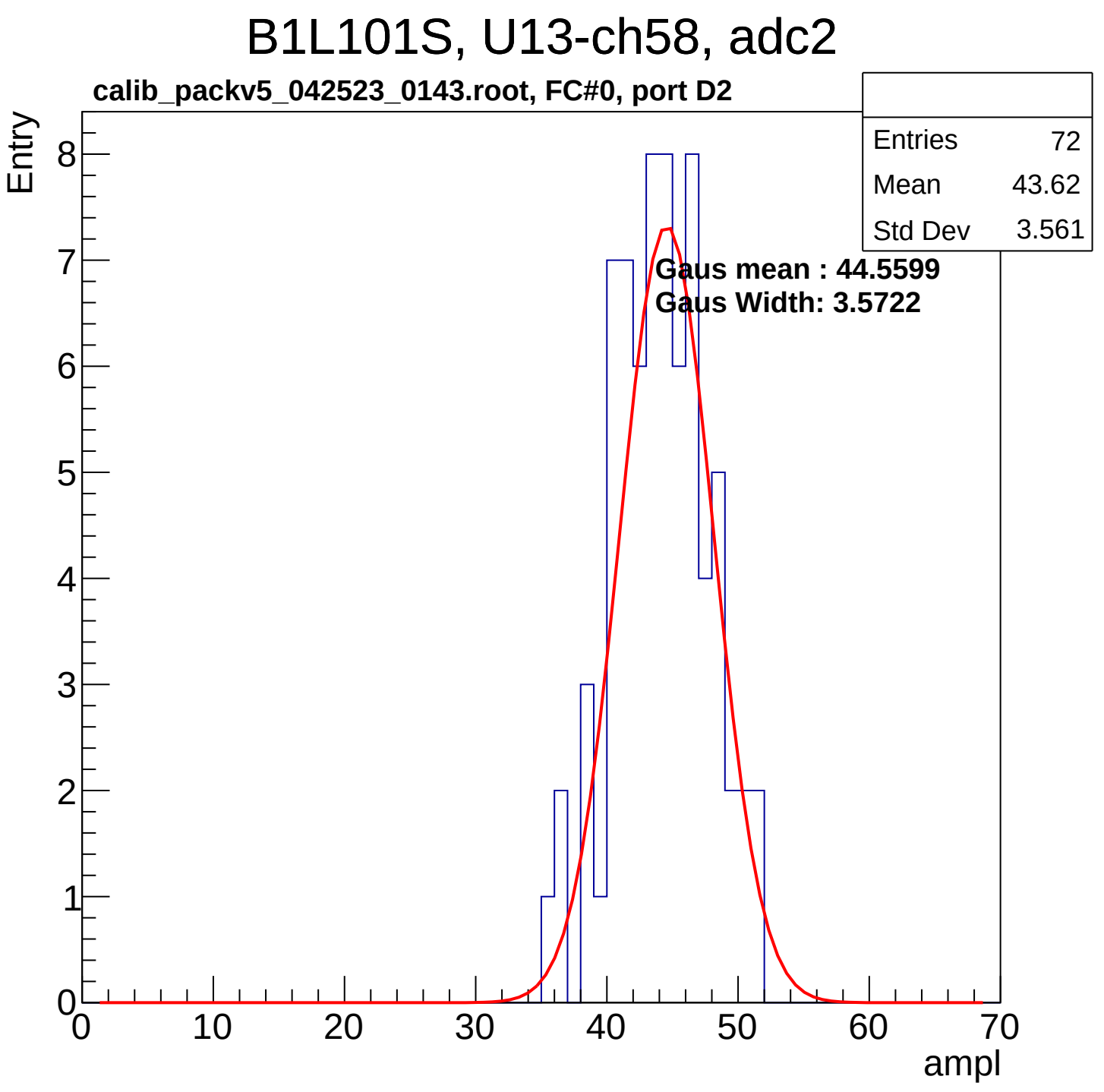
B1L101S, U13-ch58, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 43.62
Std Dev 3.561
Gaus mean : 44.5599
Gaus Width: 3.5722
Entry
ampl
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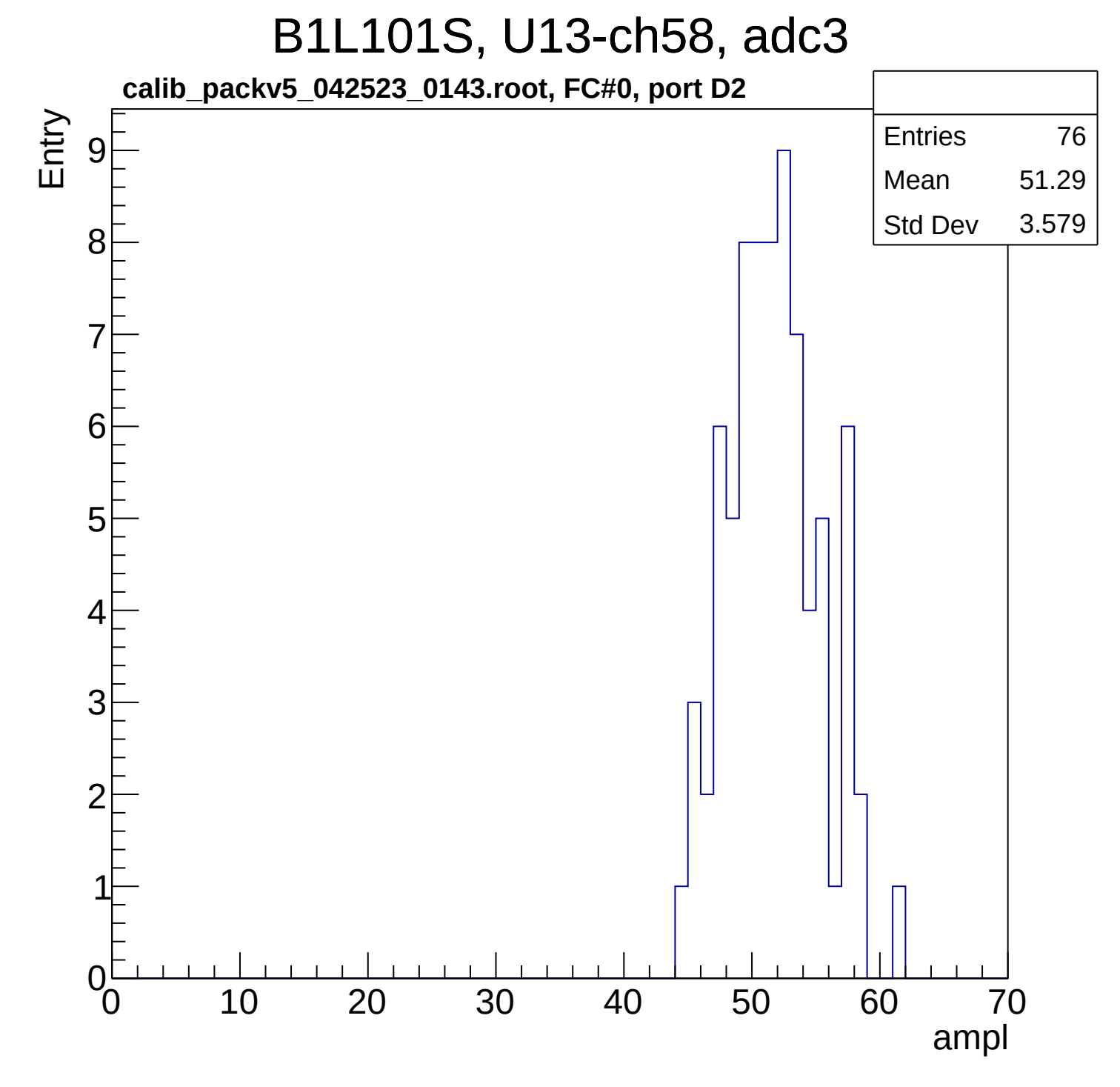
B1L101S, U13-ch58, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 51.29
Std Dev 3.579
Entry
ampl
0
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3
4
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6
7
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0
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60
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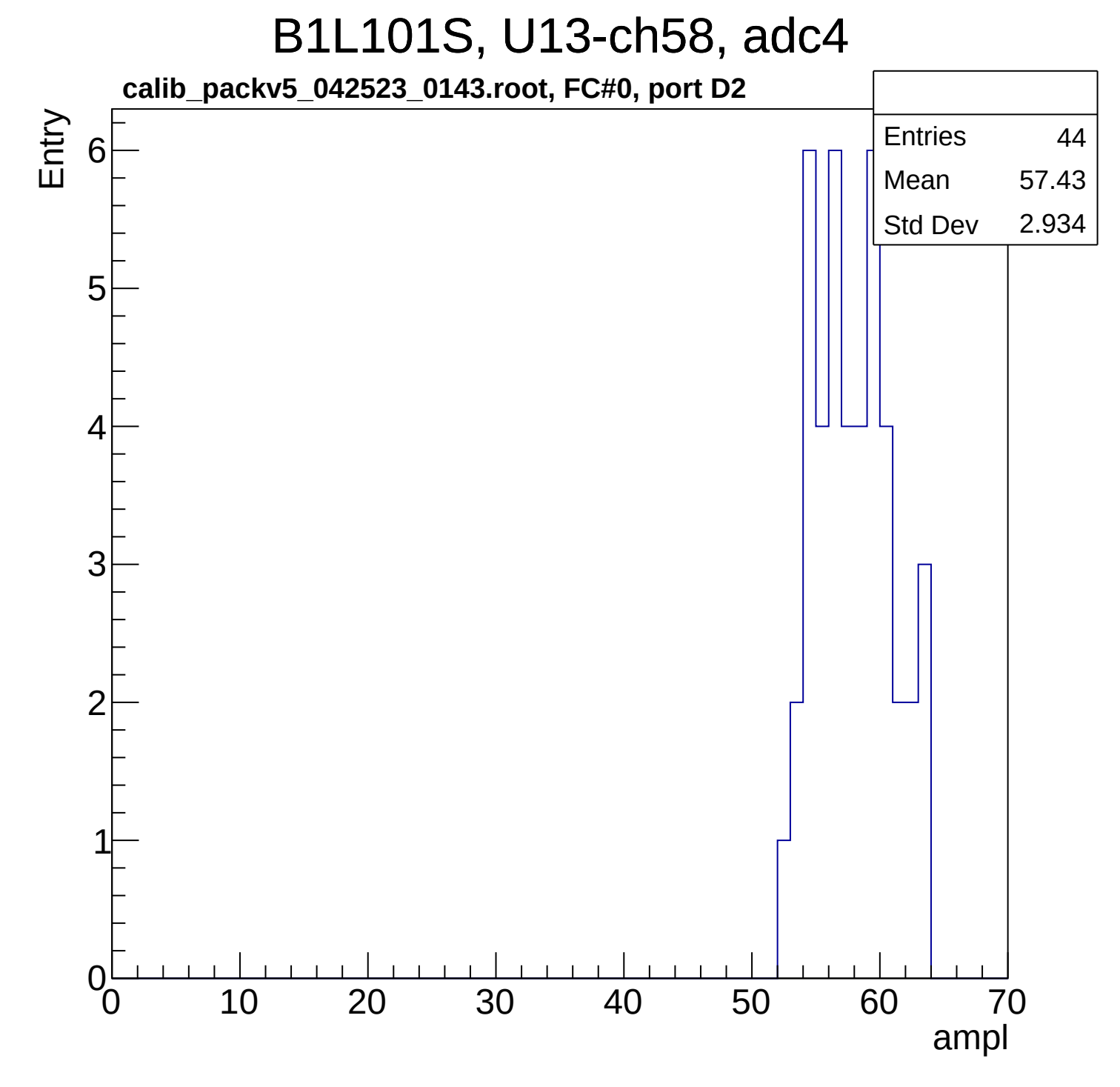
B1L101S, U13-ch58, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 57.43
Std Dev 2.934
Entry
ampl
0
1
2
3
4
5
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0
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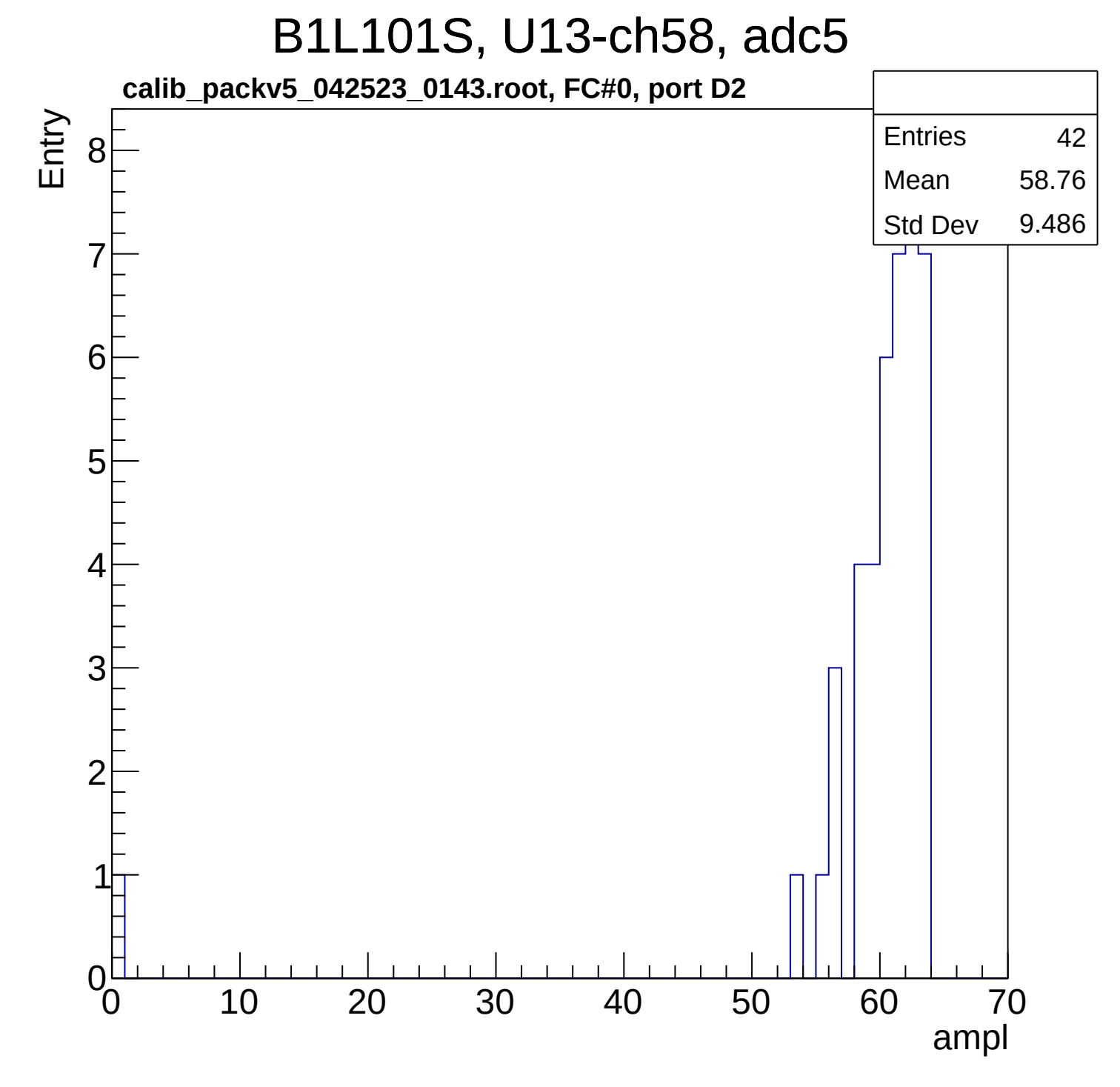
B1L101S, U13-ch58, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 58.76
Std Dev 9.486
Entry
ampl
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1
2
3
4
5
6
7
8
0
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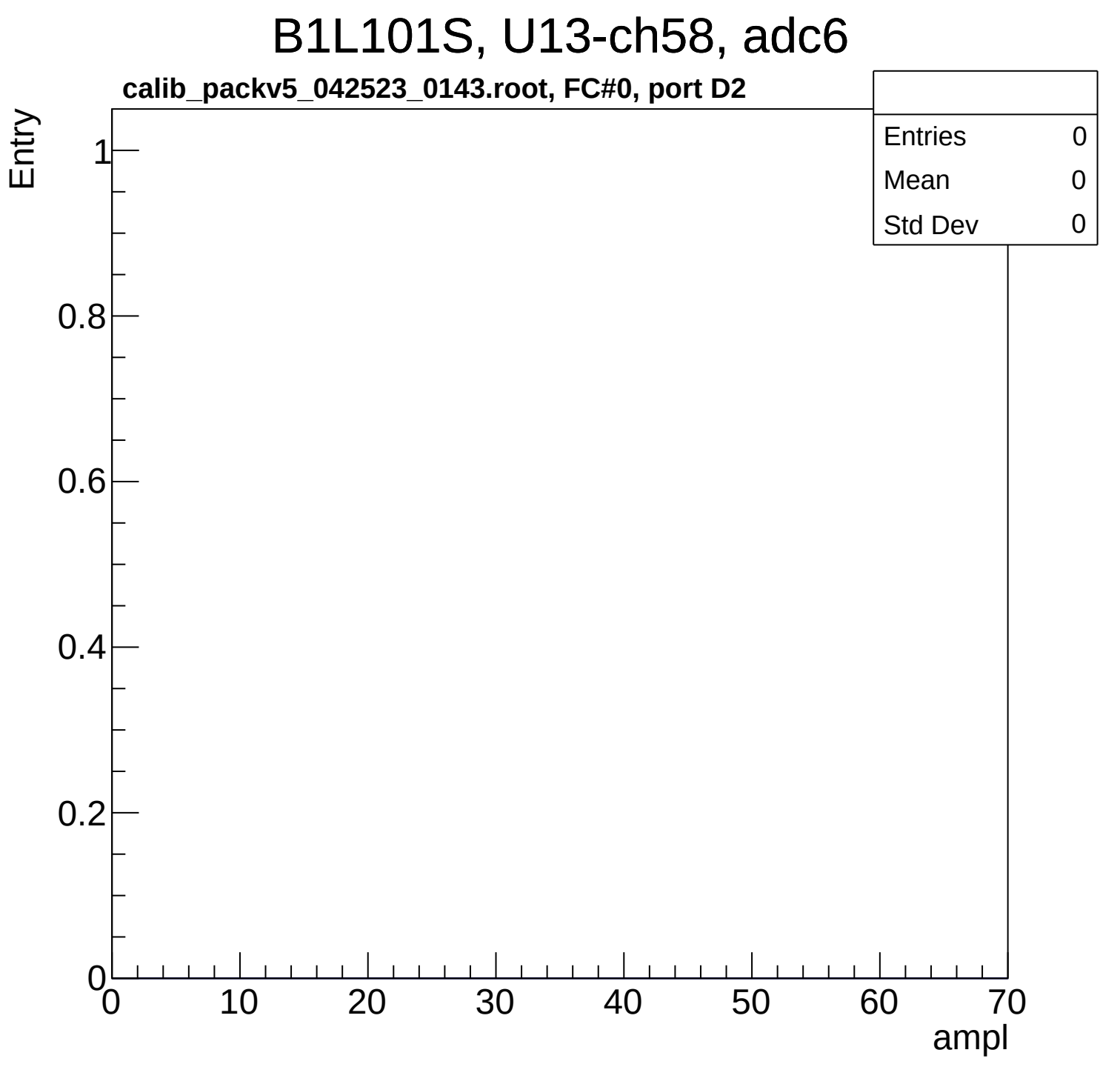
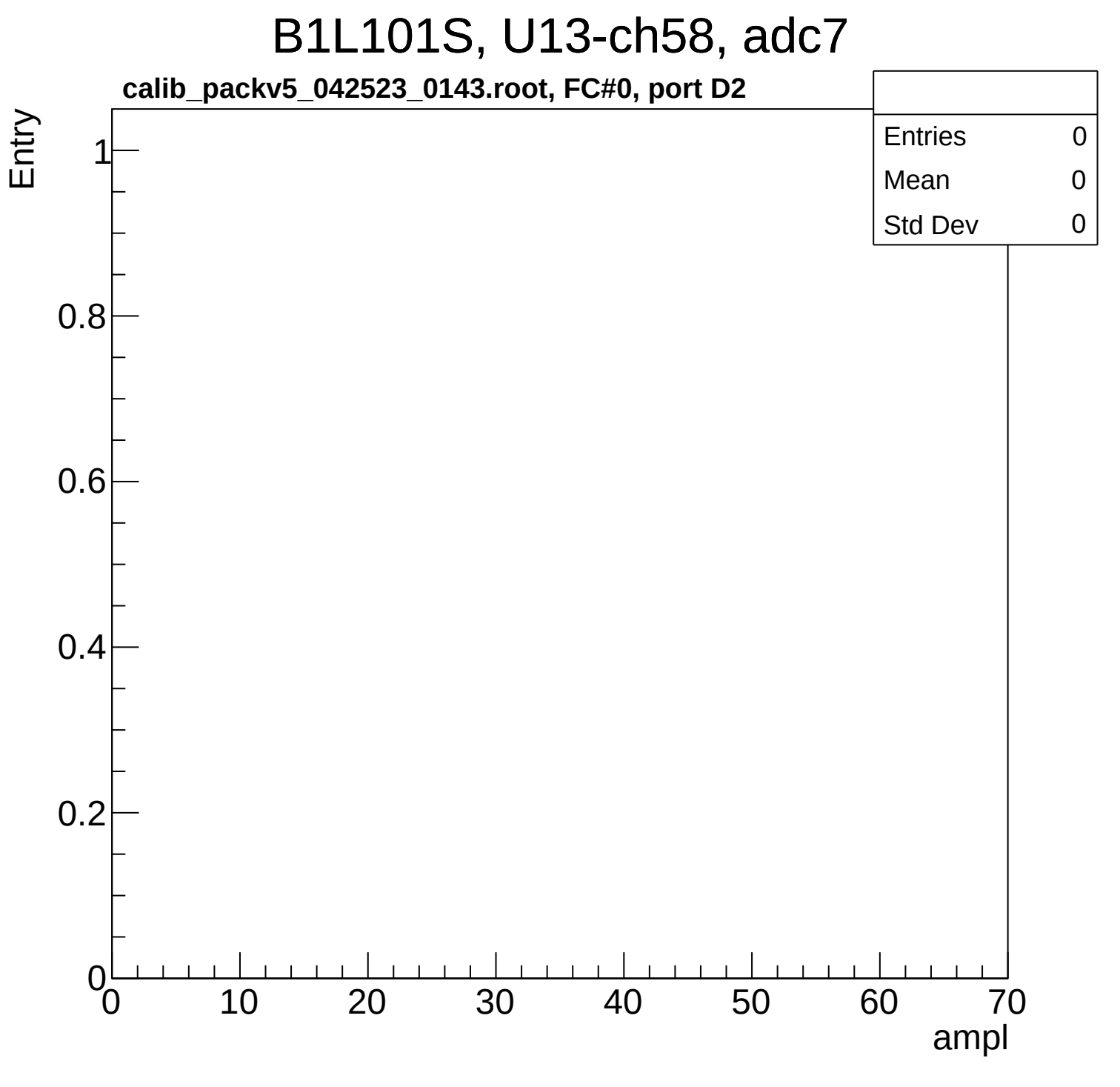

B1L101S, U13-ch58, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch58, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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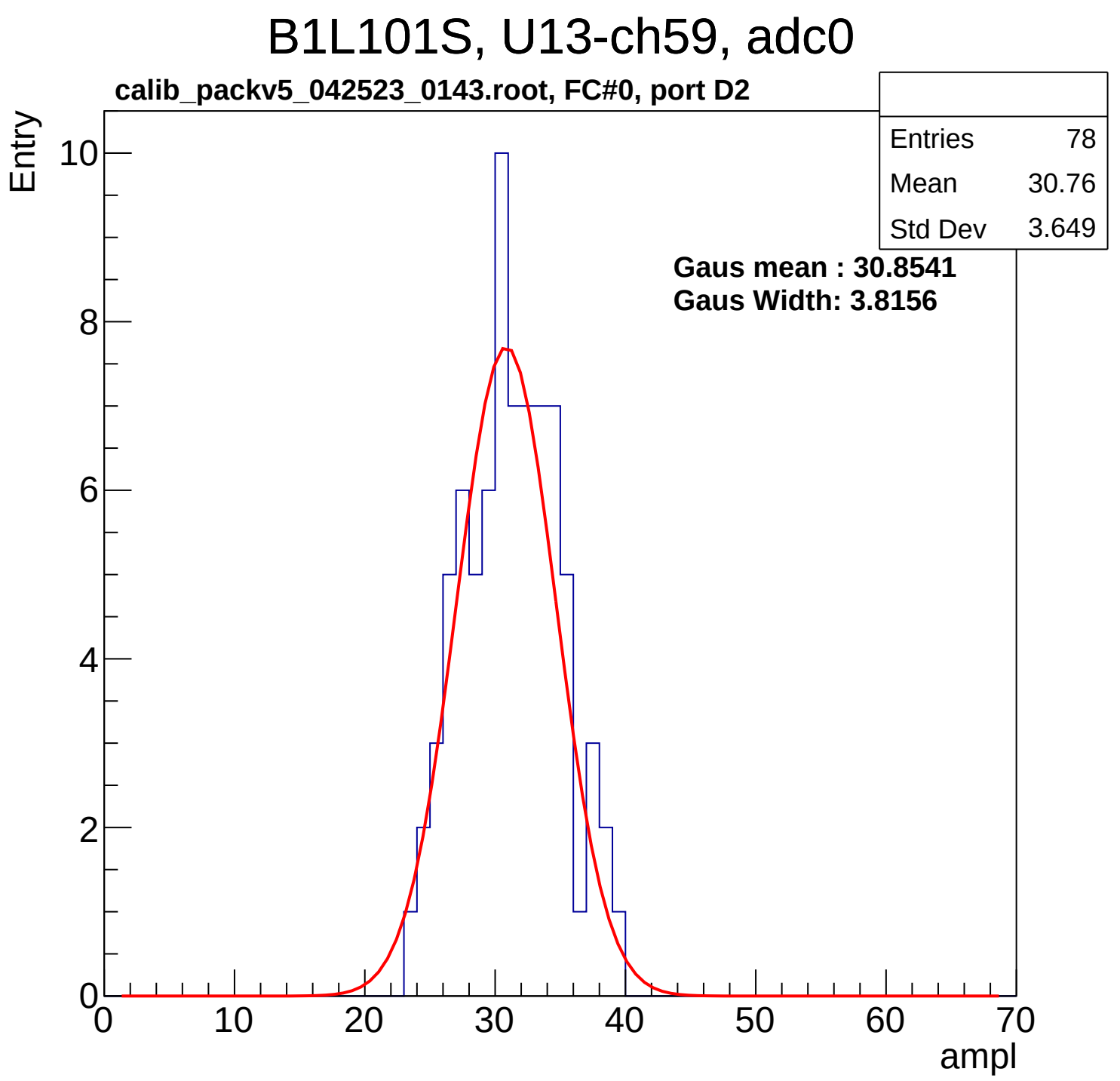
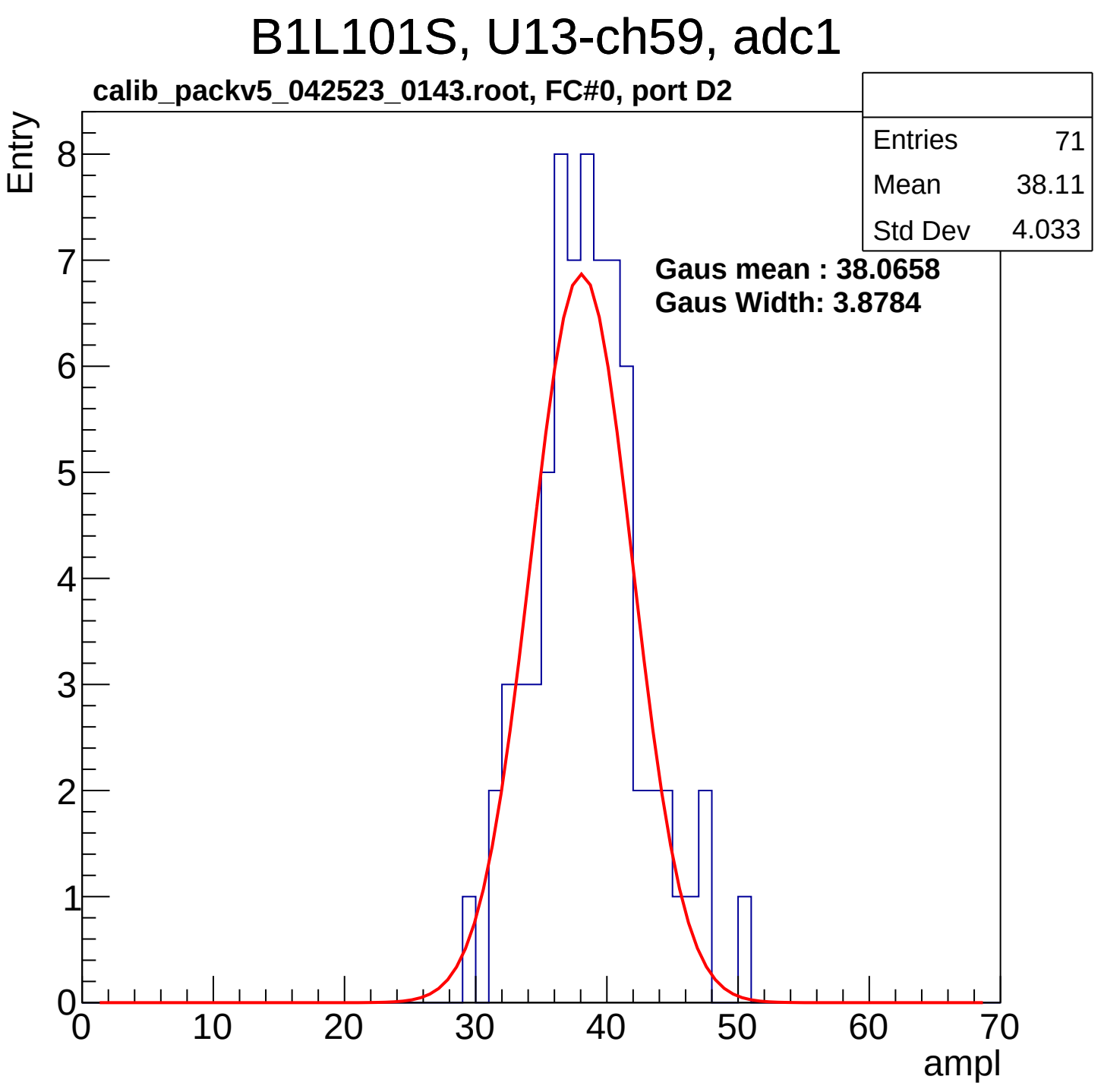
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch59, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 30.76
Std Dev 3.649
Gaus mean : 30.8541
Gaus Width: 3.8156
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch59, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 38.11
Std Dev 4.033
Gaus mean : 38.0658
Gaus Width: 3.8784
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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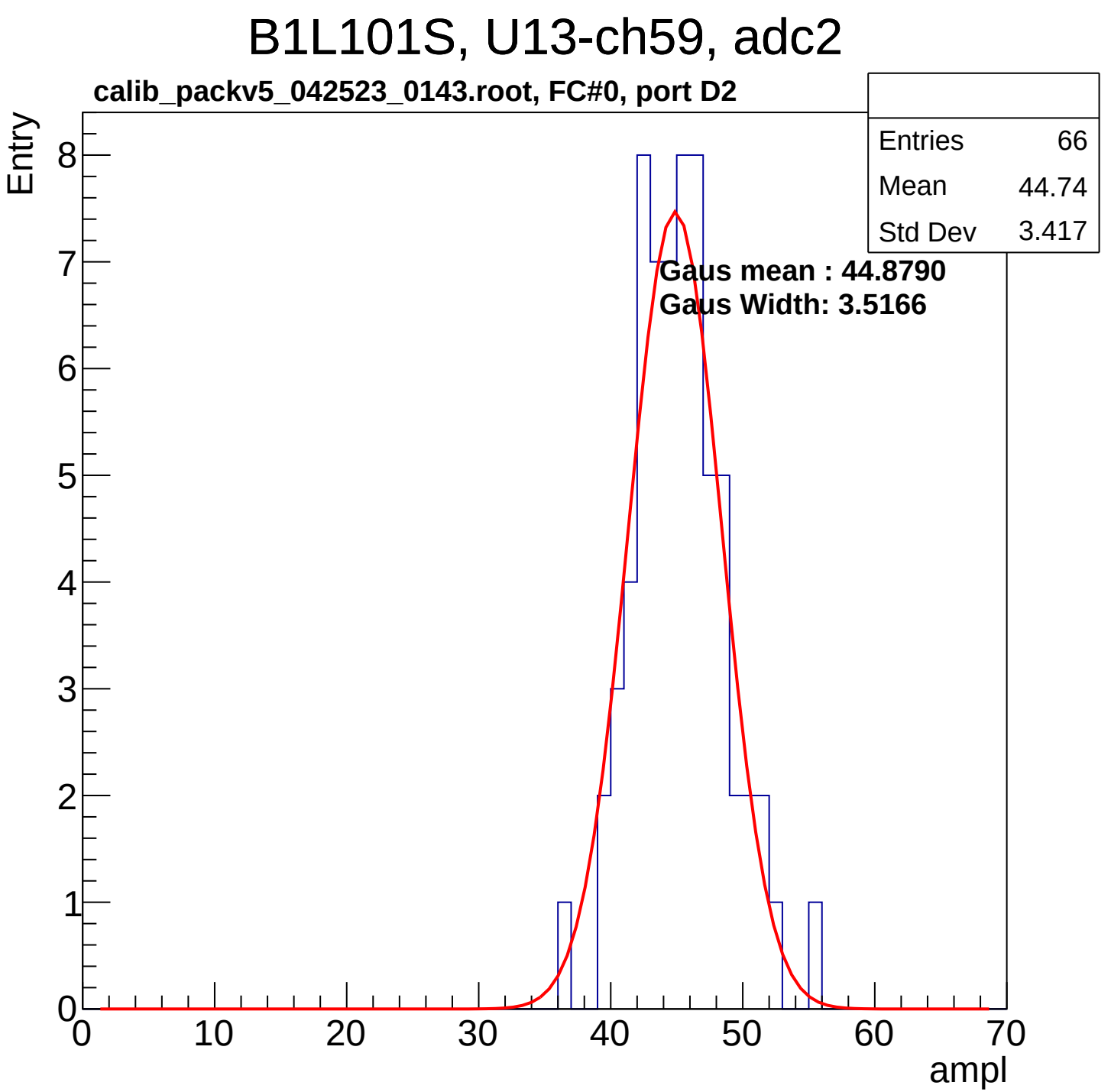
B1L101S, U13-ch59, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 44.74
Std Dev 3.417
Gaus mean : 44.8790
Gaus Width: 3.5166
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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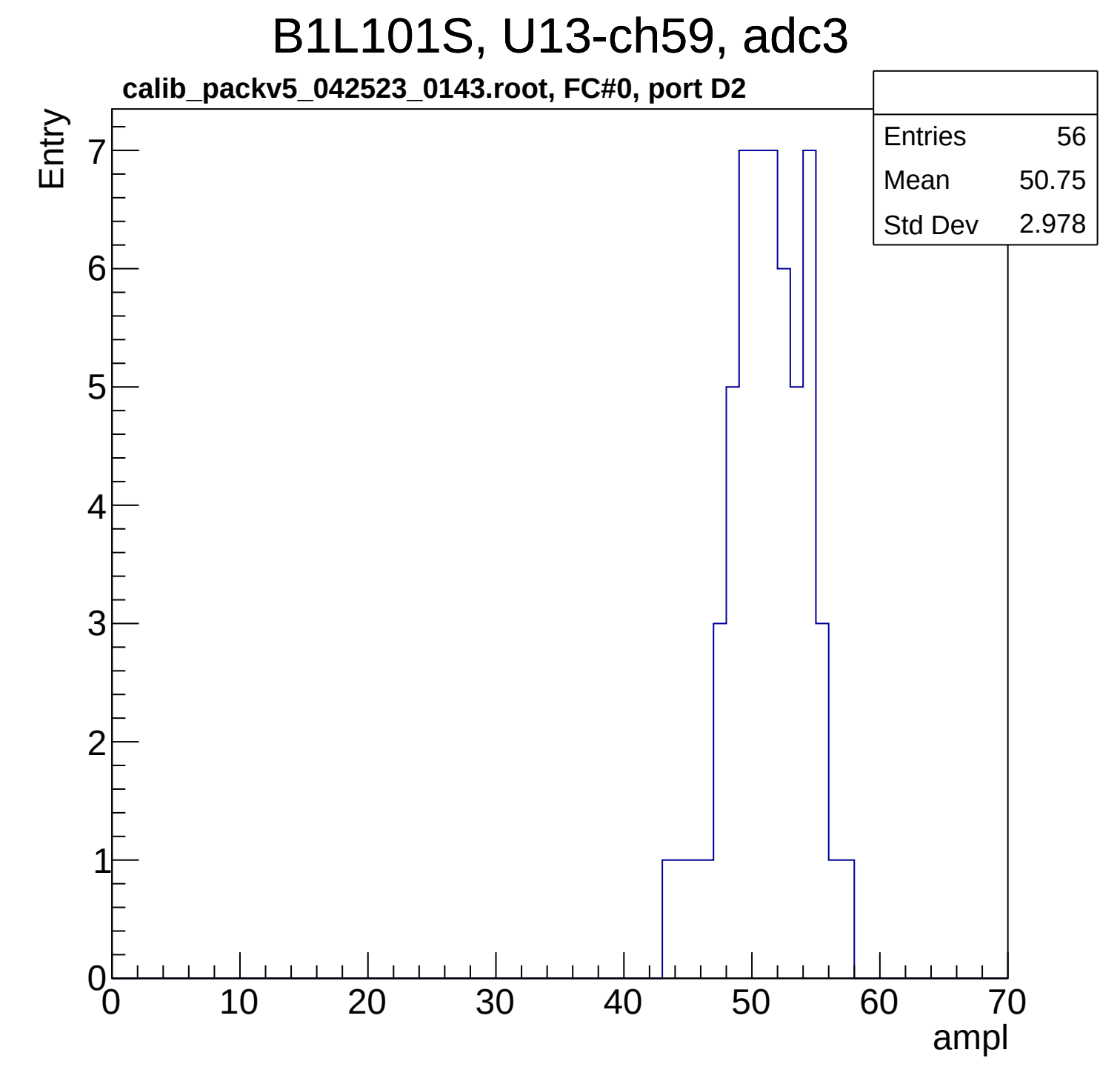
B1L101S, U13-ch59, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 50.75
Std Dev 2.978
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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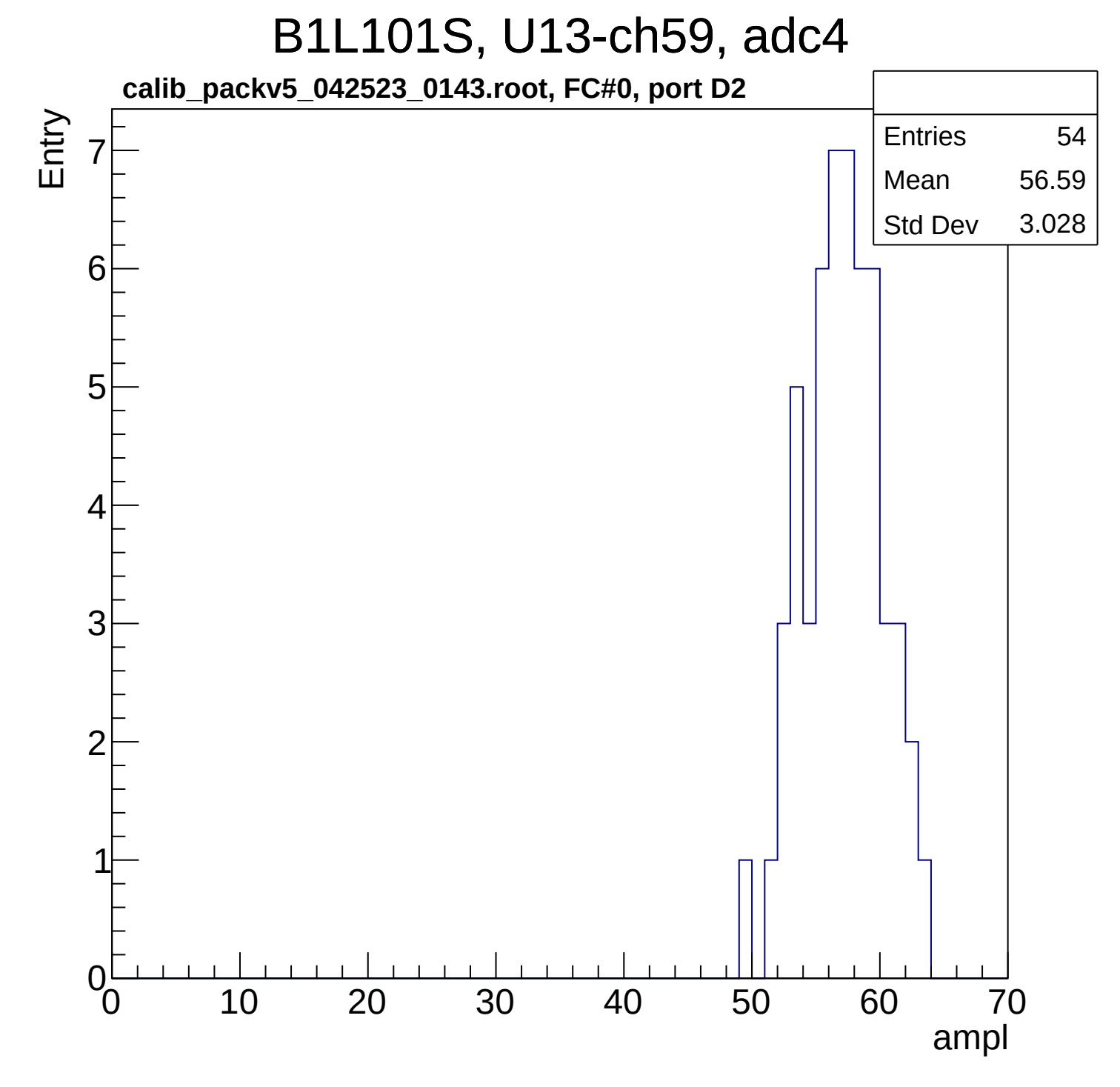
B1L101S, U13-ch59, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 56.59
Std Dev 3.028
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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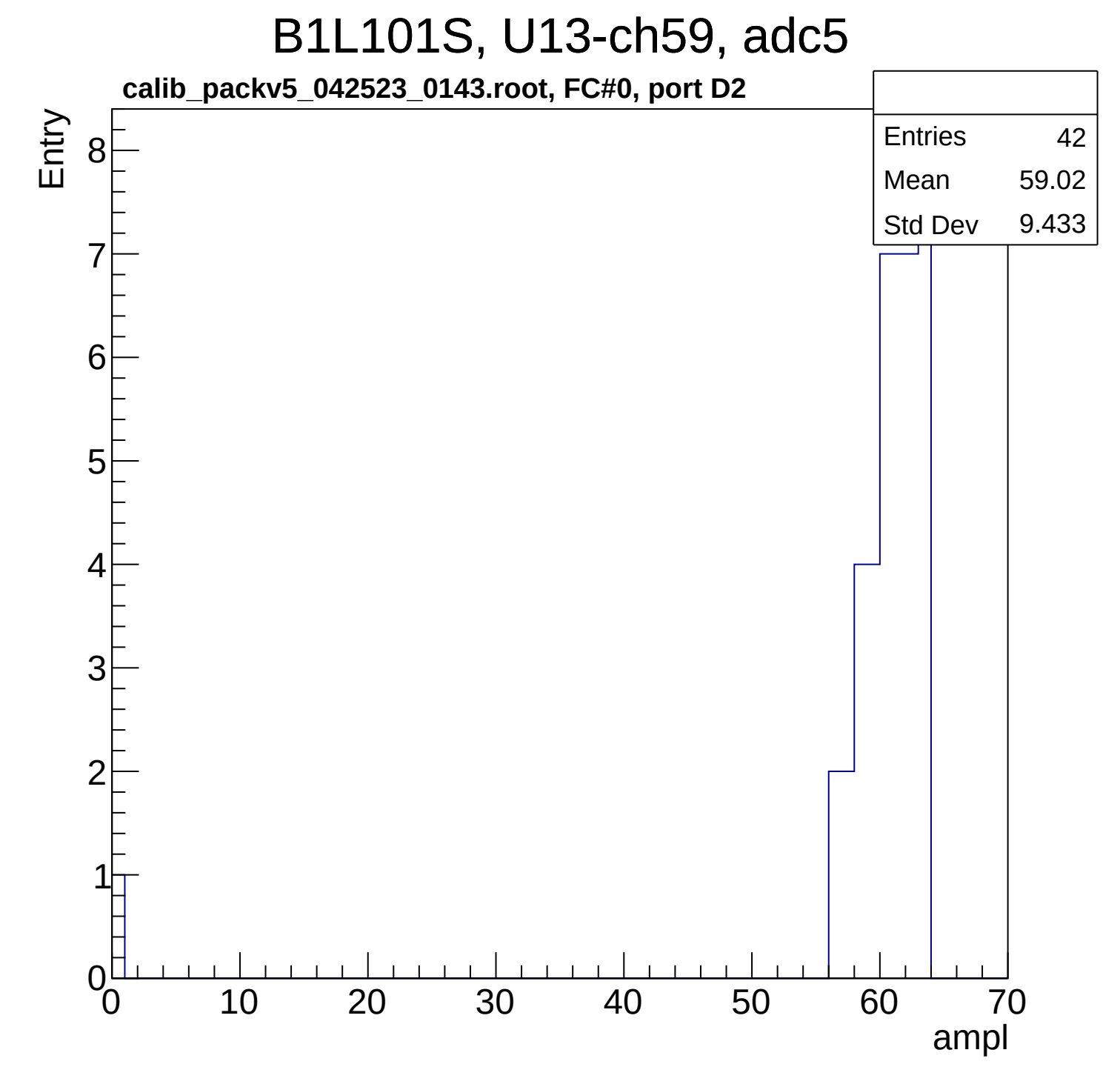
B1L101S, U13-ch59, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 59.02
Std Dev 9.433
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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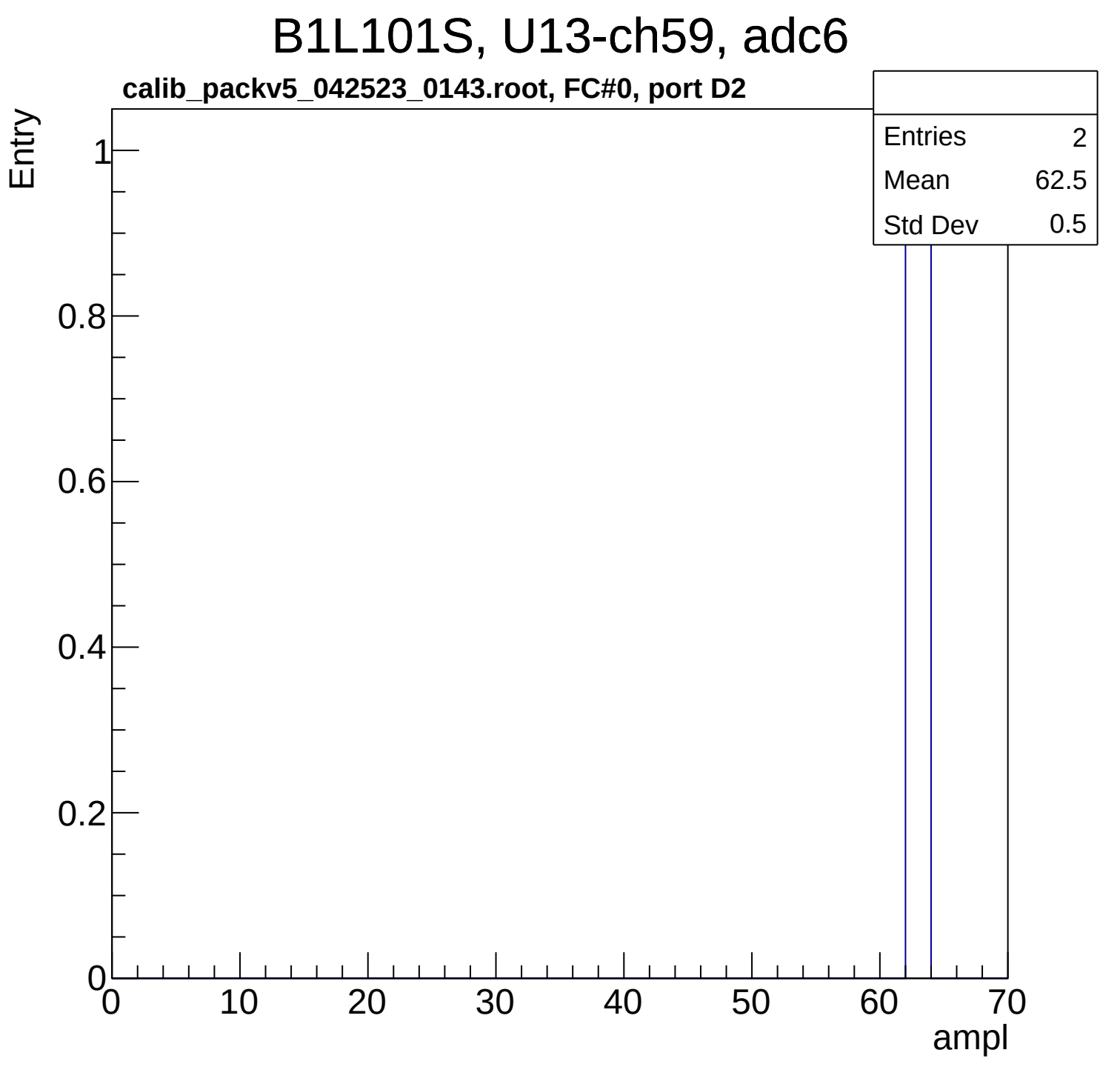
B1L101S, U13-ch59, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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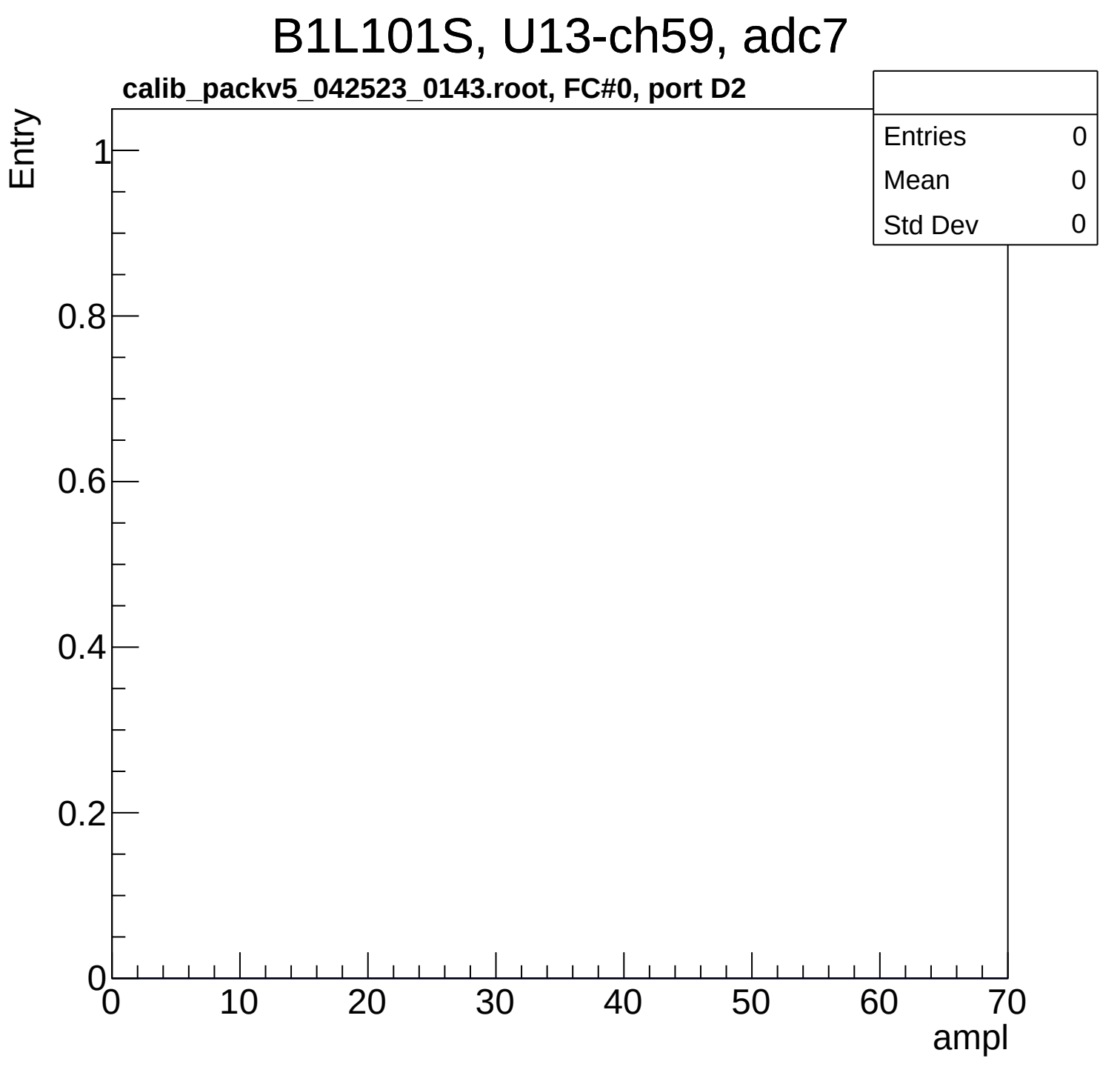
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch59, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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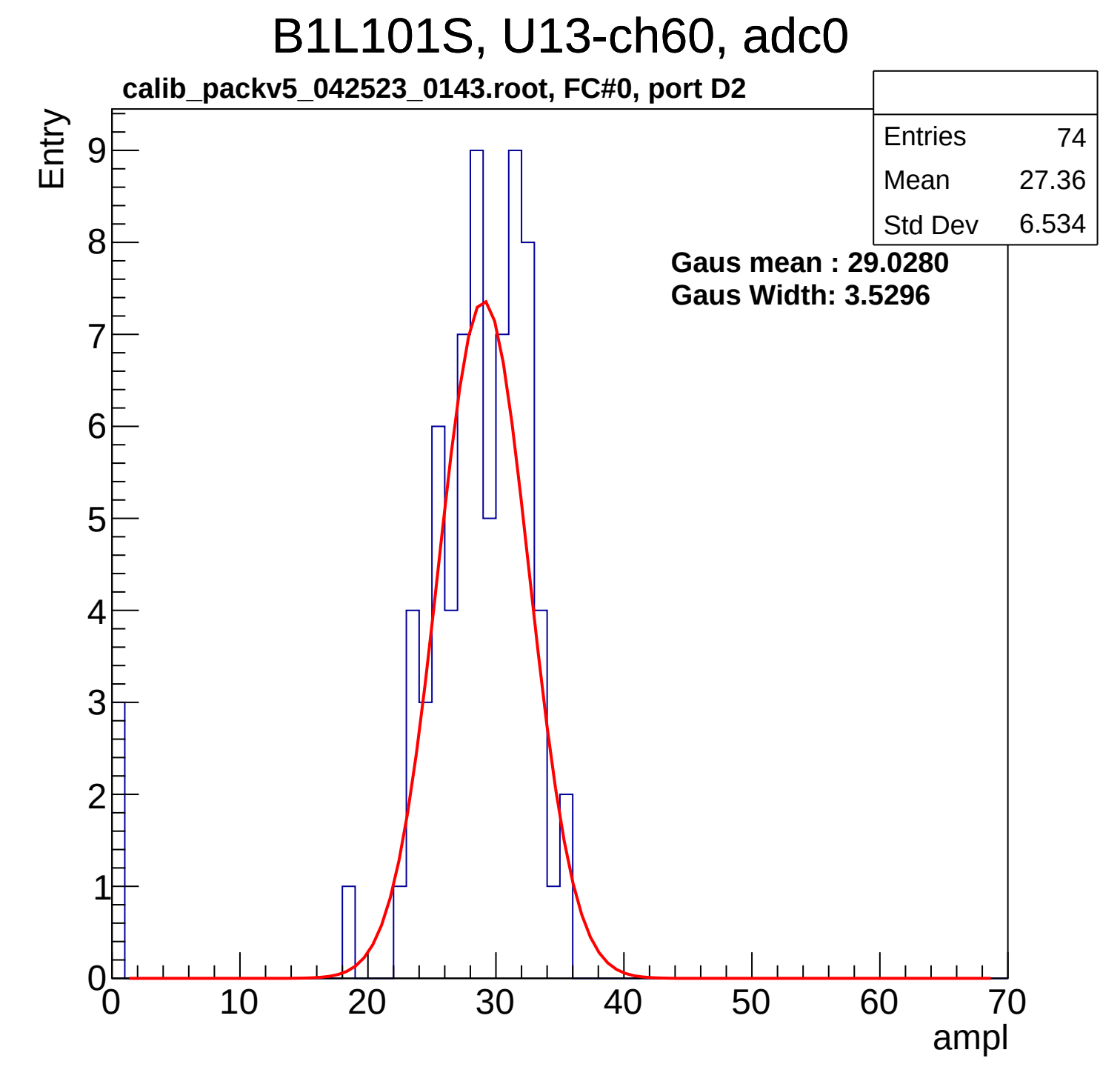
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch60, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 27.36
Std Dev 6.534
Gaus mean : 29.0280
Gaus Width: 3.5296
Entry
ampl
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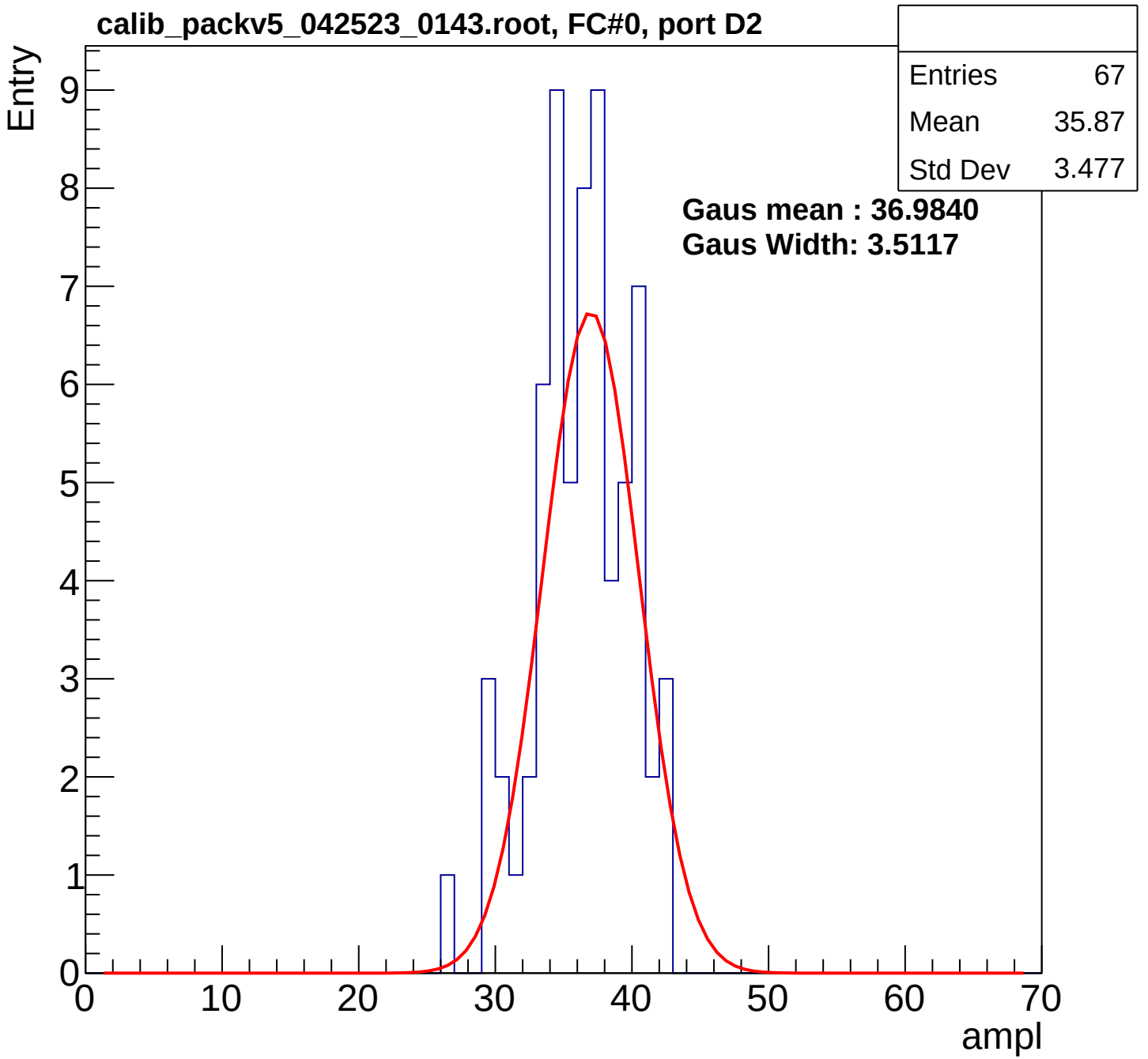
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch60, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 35.87
Std Dev 3.477
Gaus mean : 36.9840
Gaus Width: 3.5117
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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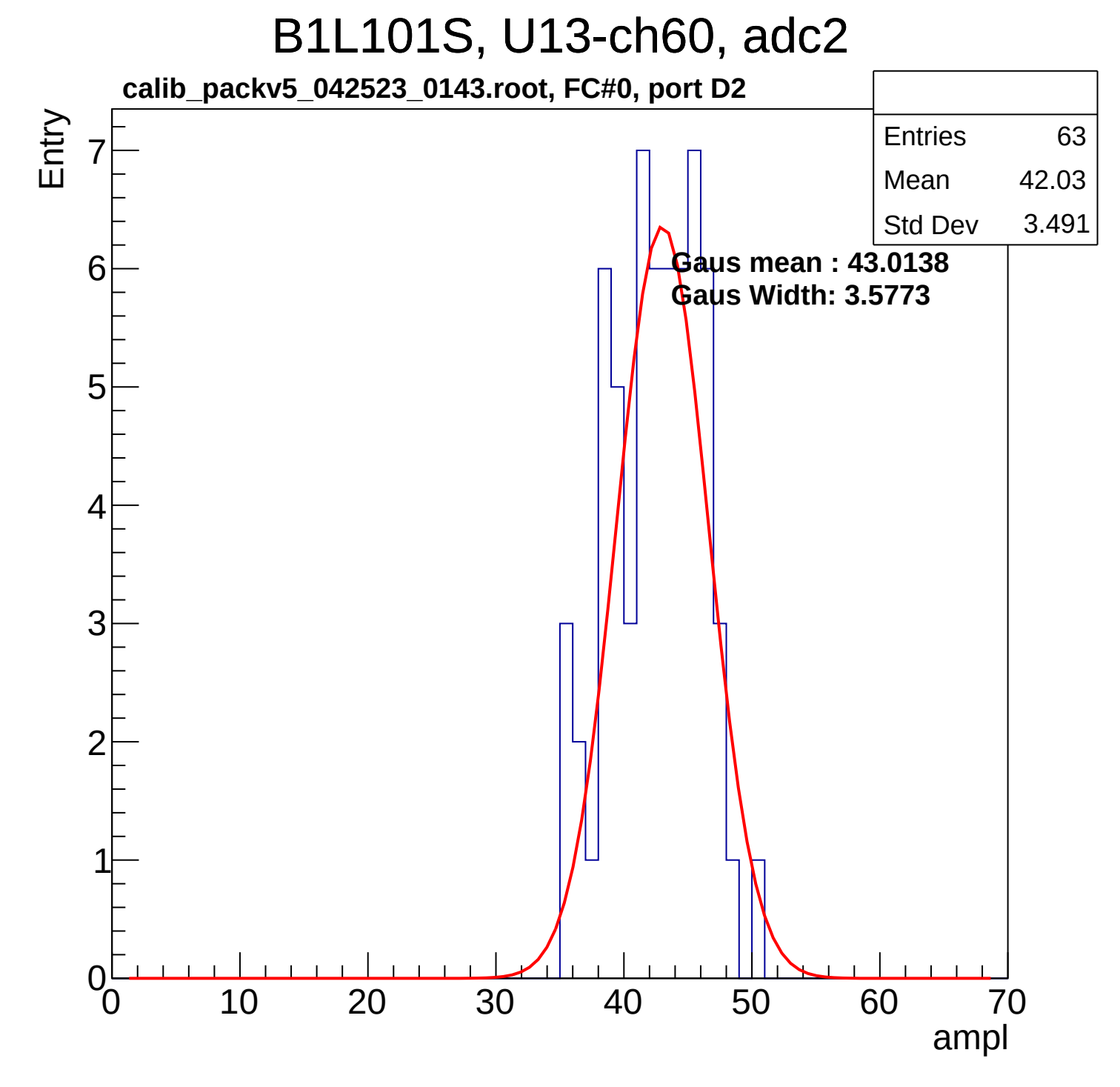
B1L101S, U13-ch60, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 42.03
Std Dev 3.491
Gaus mean : 43.0138
Gaus Width: 3.5773
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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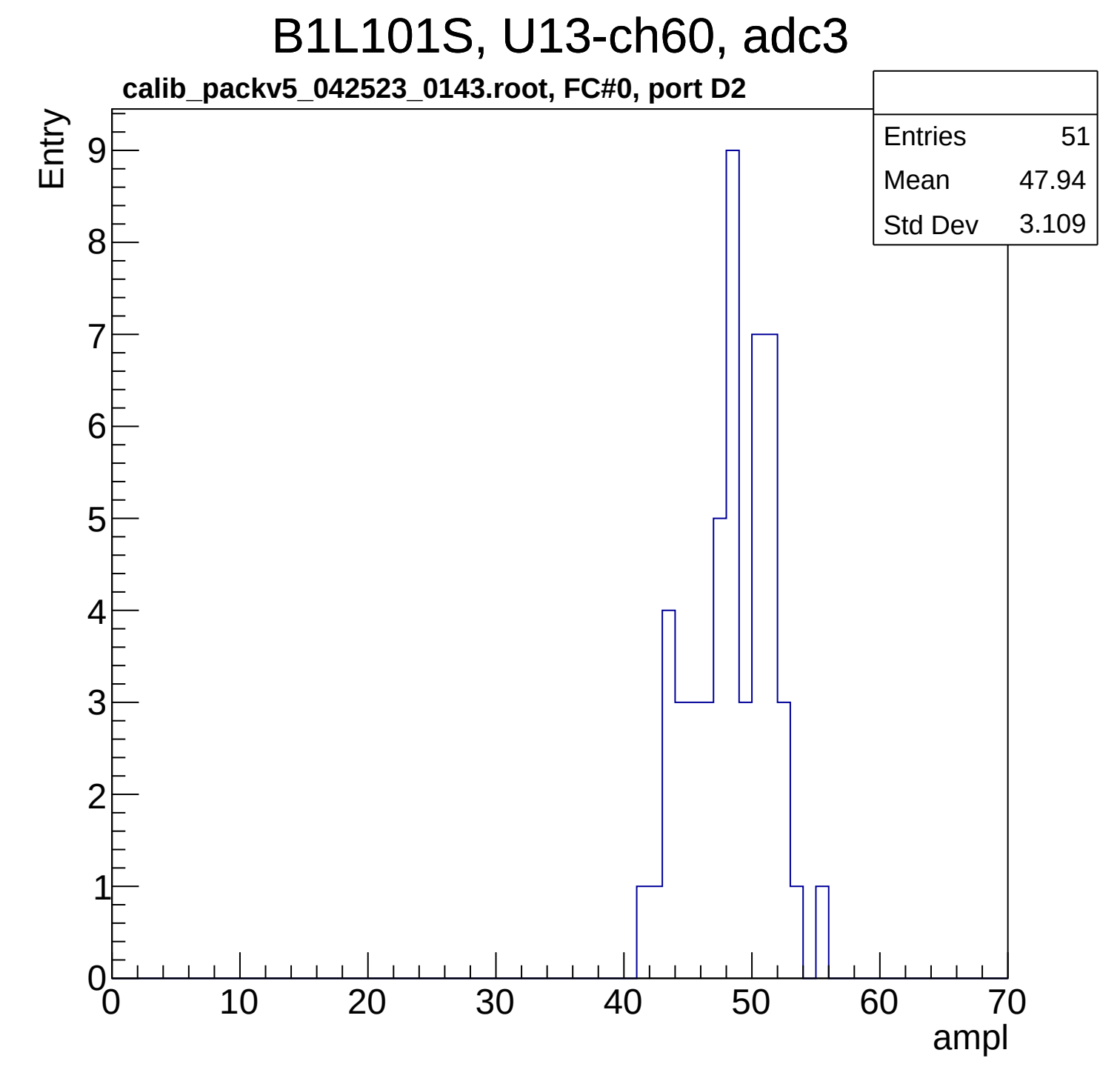
B1L101S, U13-ch60, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 47.94
Std Dev 3.109
Entry
ampl
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4
5
6
7
8
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0
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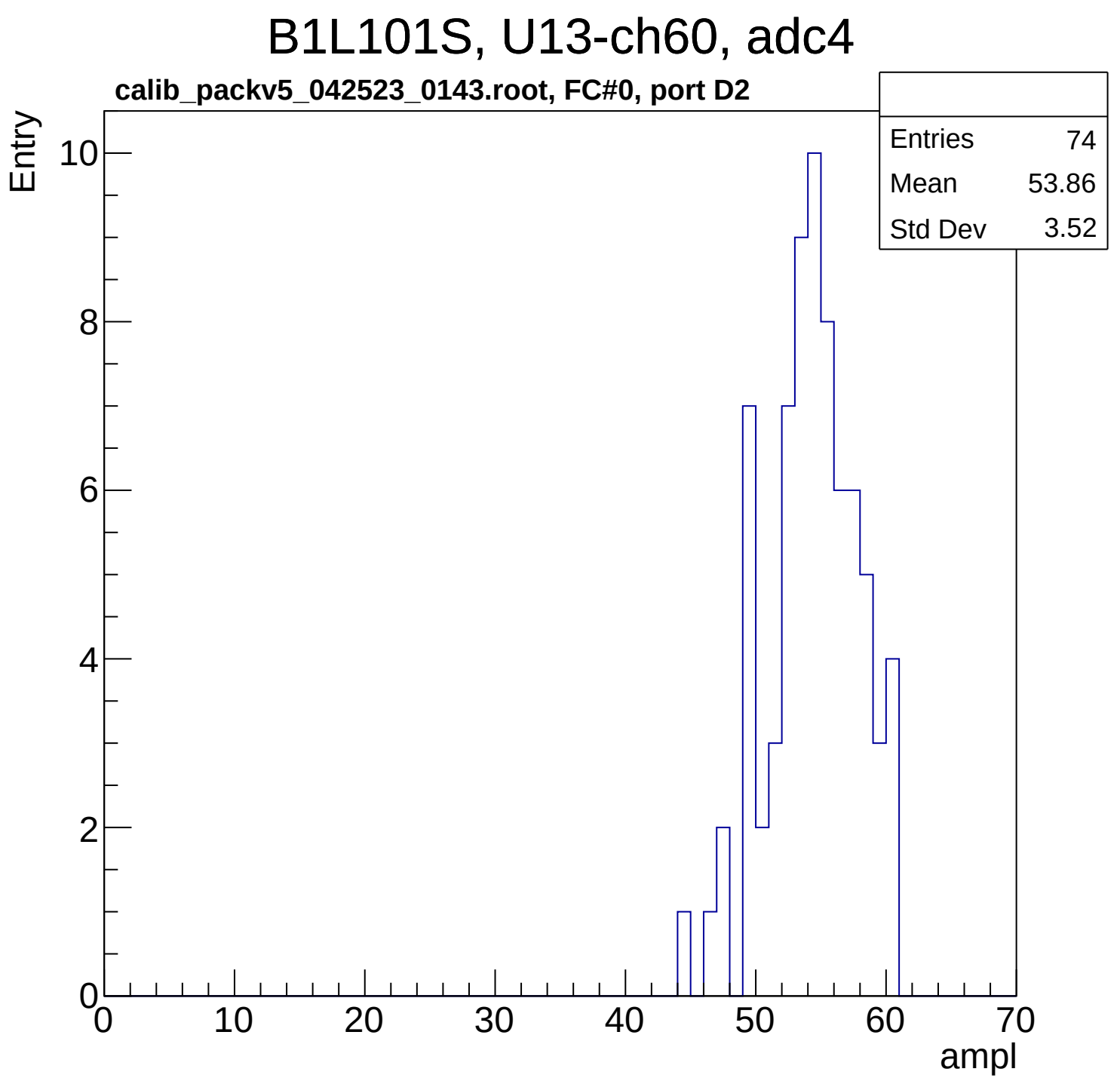
B1L101S, U13-ch60, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 53.86
Std Dev 3.52
Entry
ampl
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0
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60
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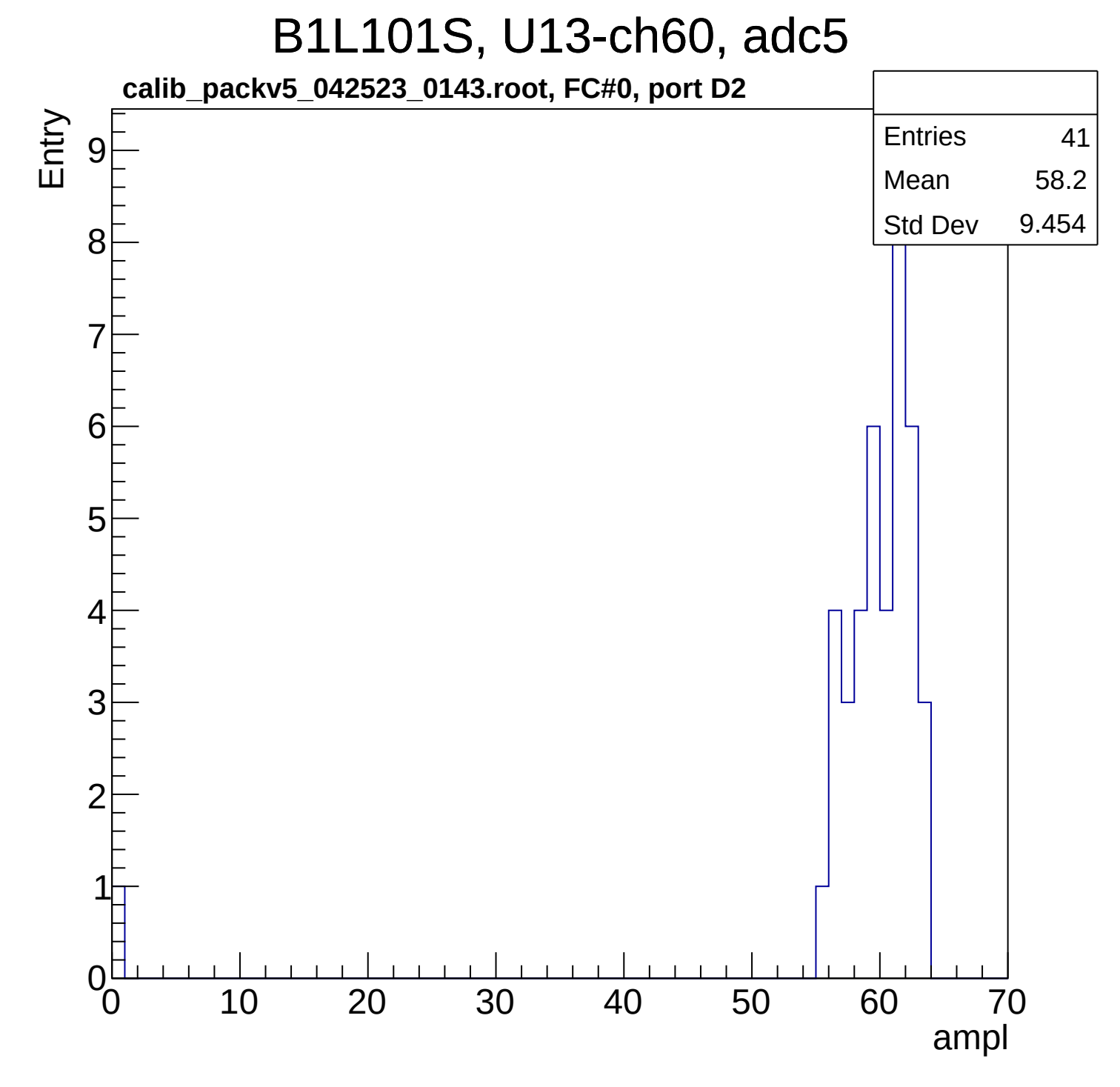
B1L101S, U13-ch60, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 58.2
Std Dev 9.454
Entry
ampl
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3
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6
7
8
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0
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60
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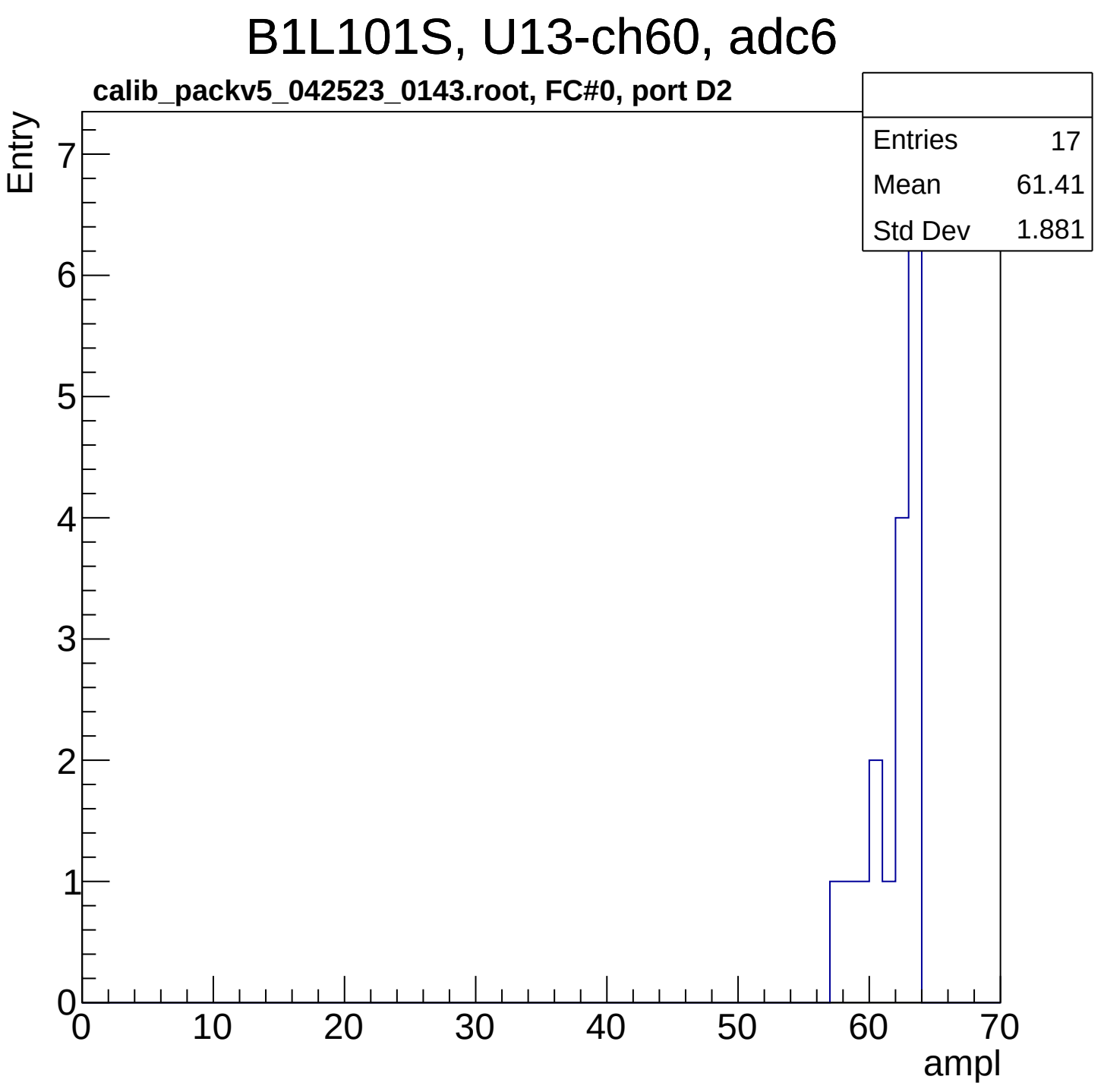
B1L101S, U13-ch60, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 17
Mean 61.41
Std Dev 1.881
Entry
ampl
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4
5
6
7
0
10
20
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40
50
60
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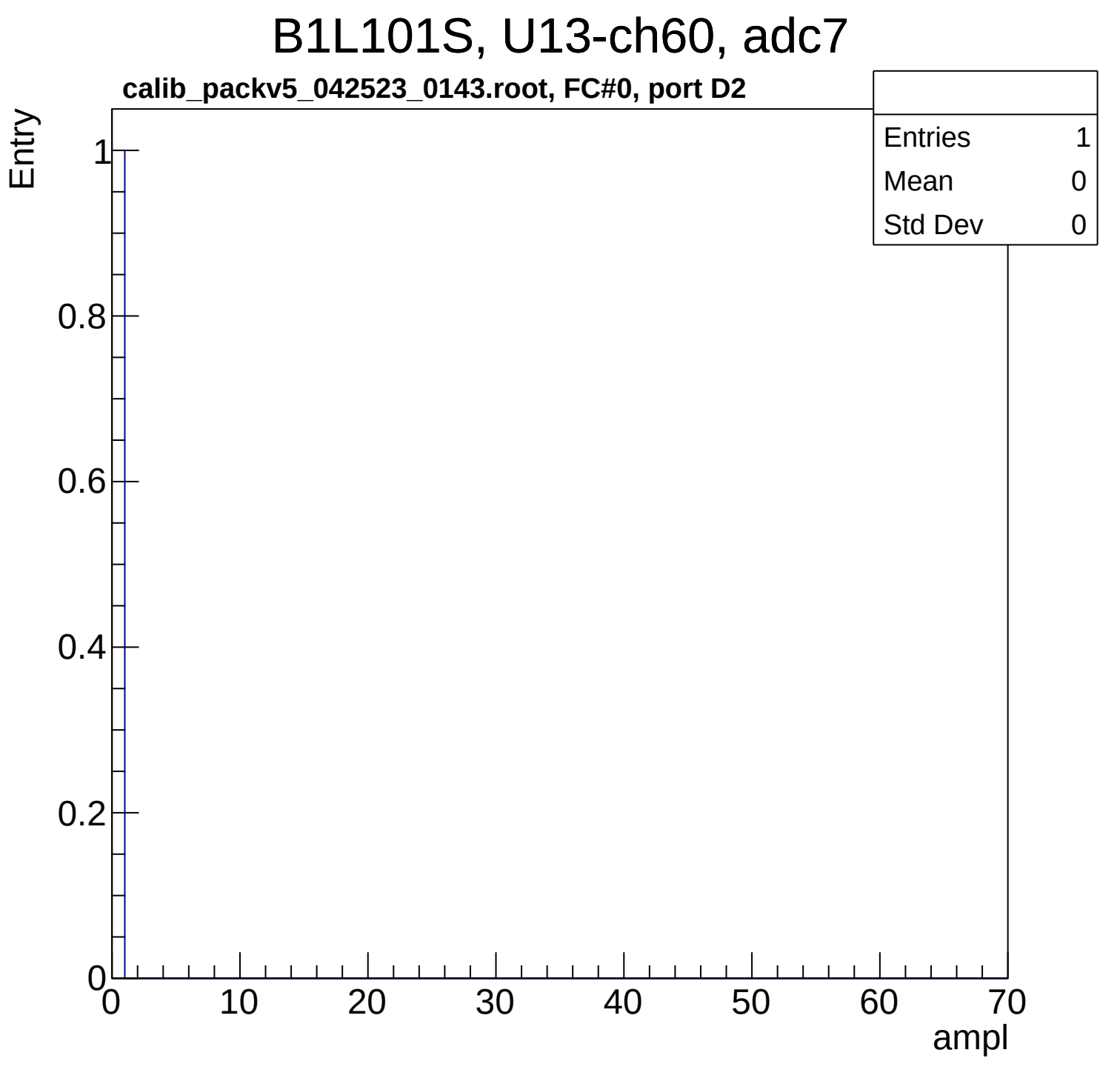
B1L101S, U13-ch60, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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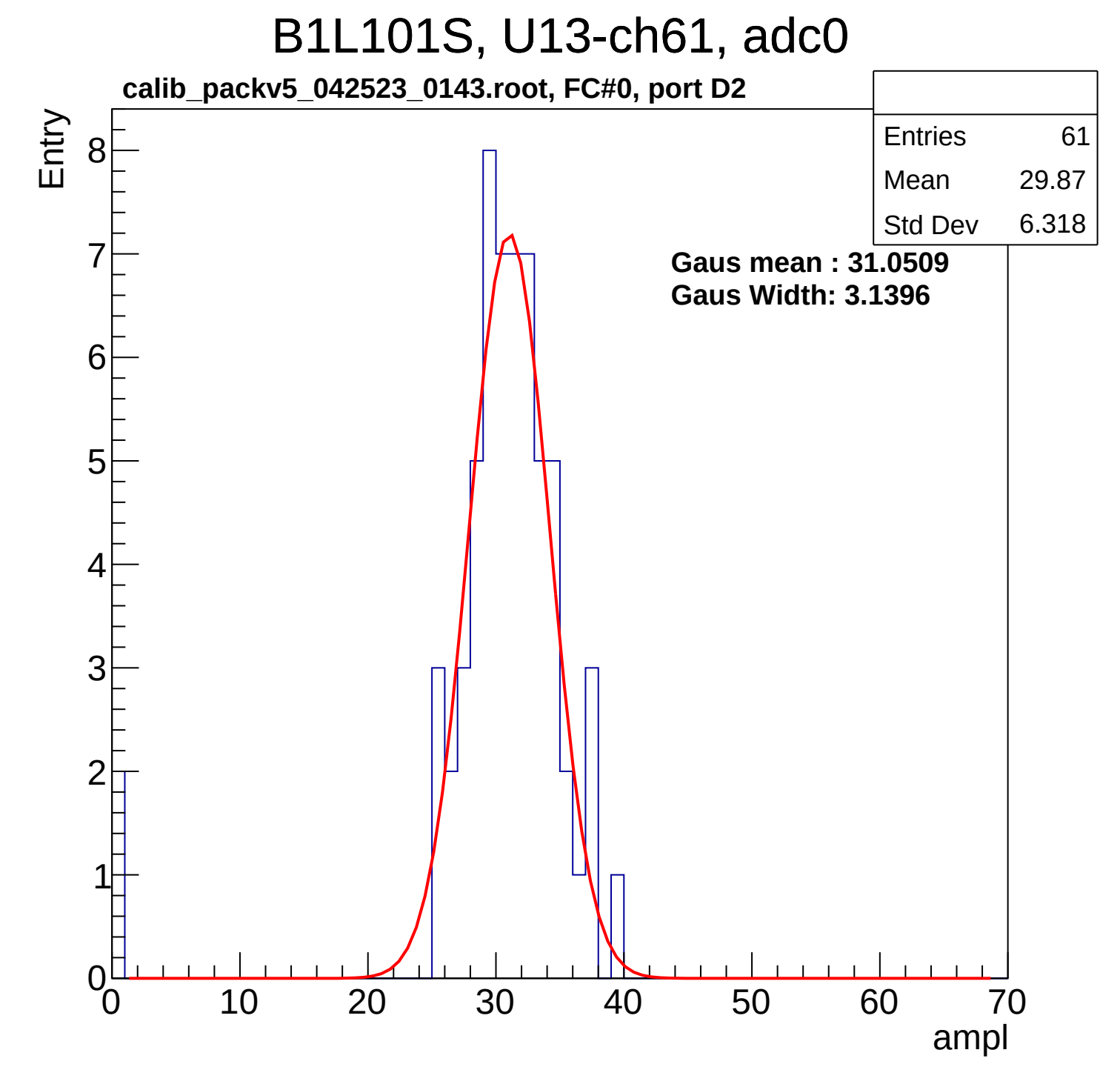
40
50
60
70
ampl


B1L101S, U13-ch61, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 29.87
Std Dev 6.318
Gaus mean : 31.0509
Gaus Width: 3.1396
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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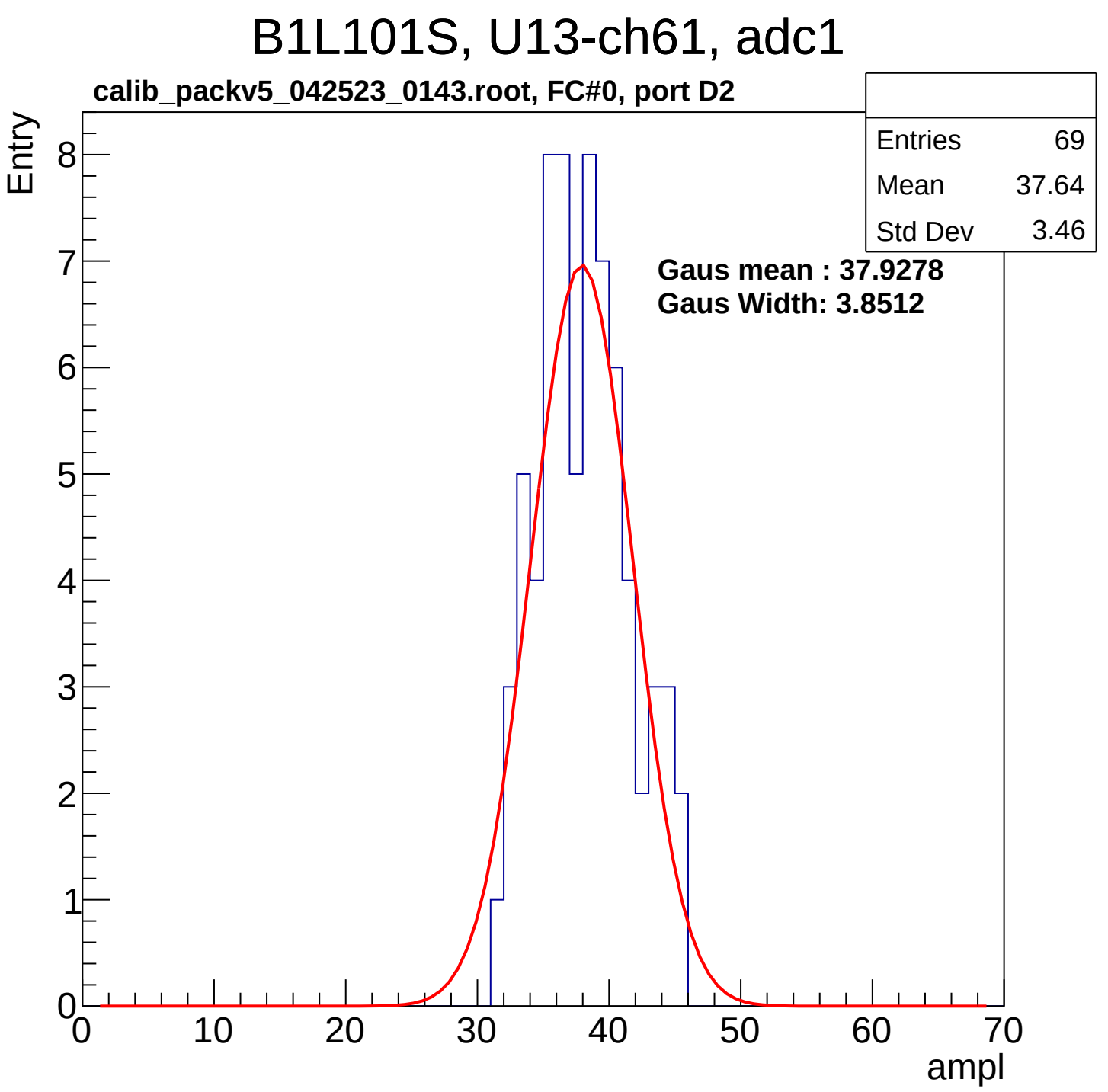
B1L101S, U13-ch61, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 37.64
Std Dev 3.46
Gaus mean : 37.9278
Gaus Width: 3.8512
Entry
ampl
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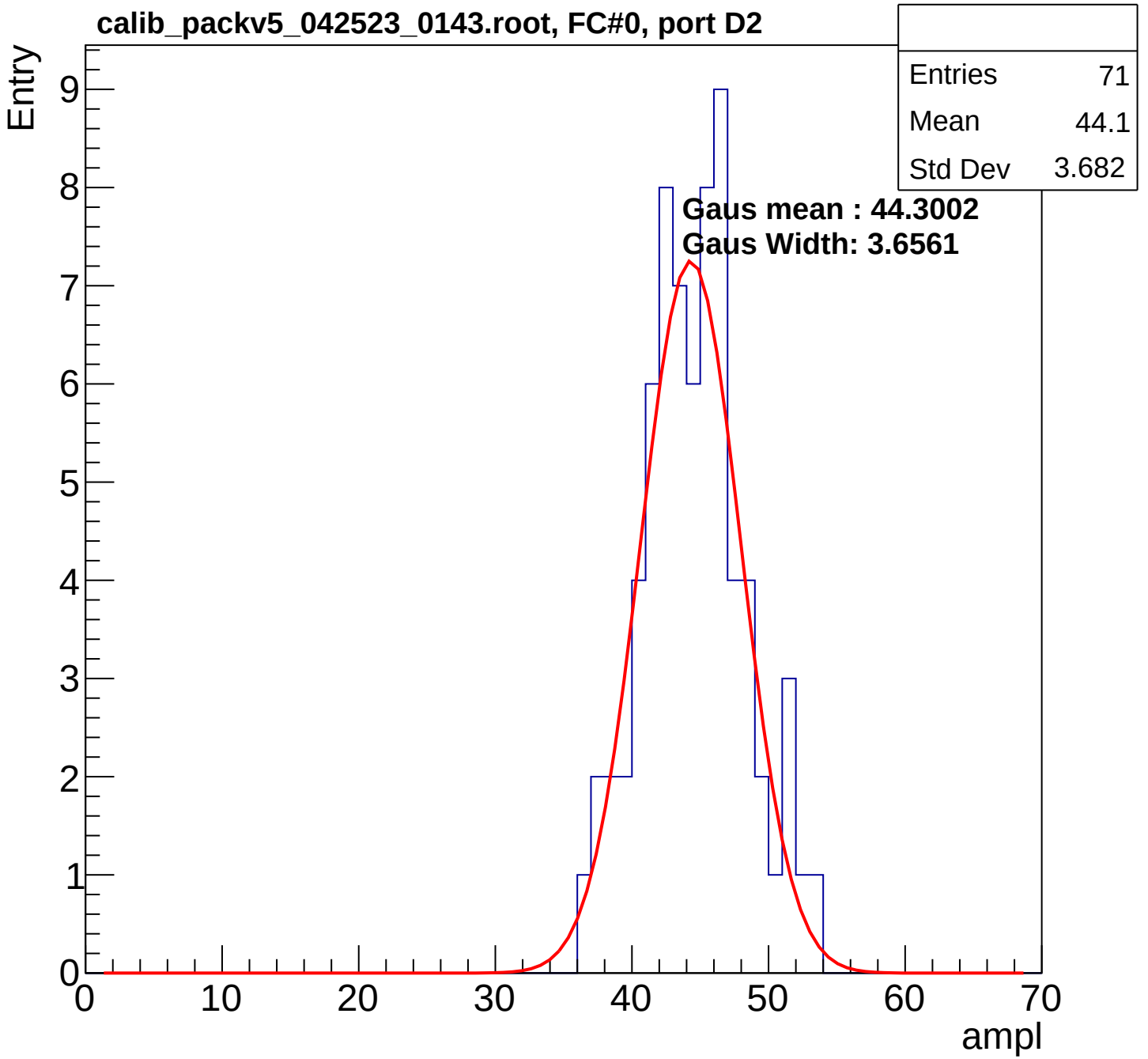
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch61, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 44.1
Std Dev 3.682
Gaus mean : 44.3002
Gaus Width: 3.6561
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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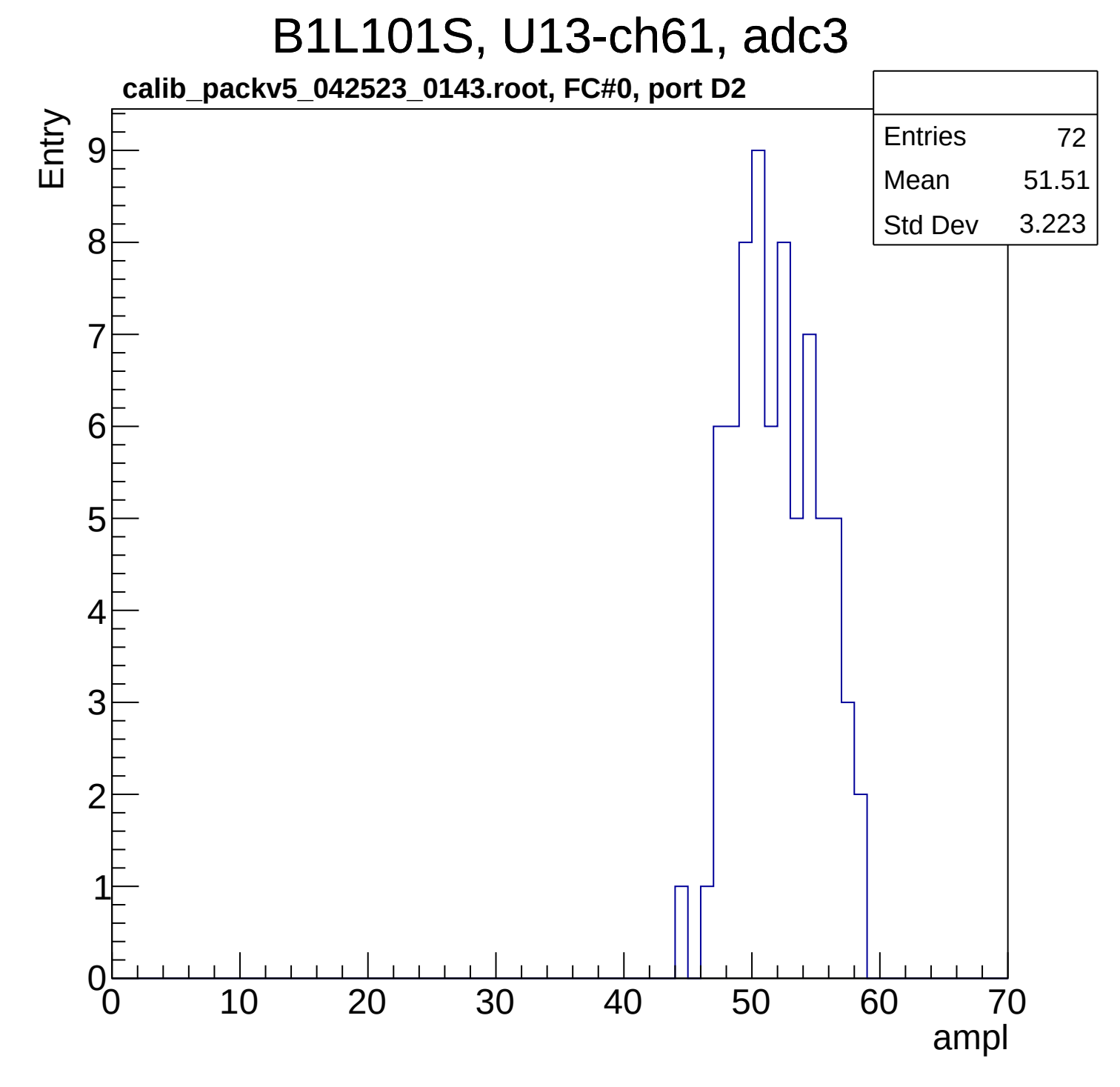
B1L101S, U13-ch61, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 51.51
Std Dev 3.223
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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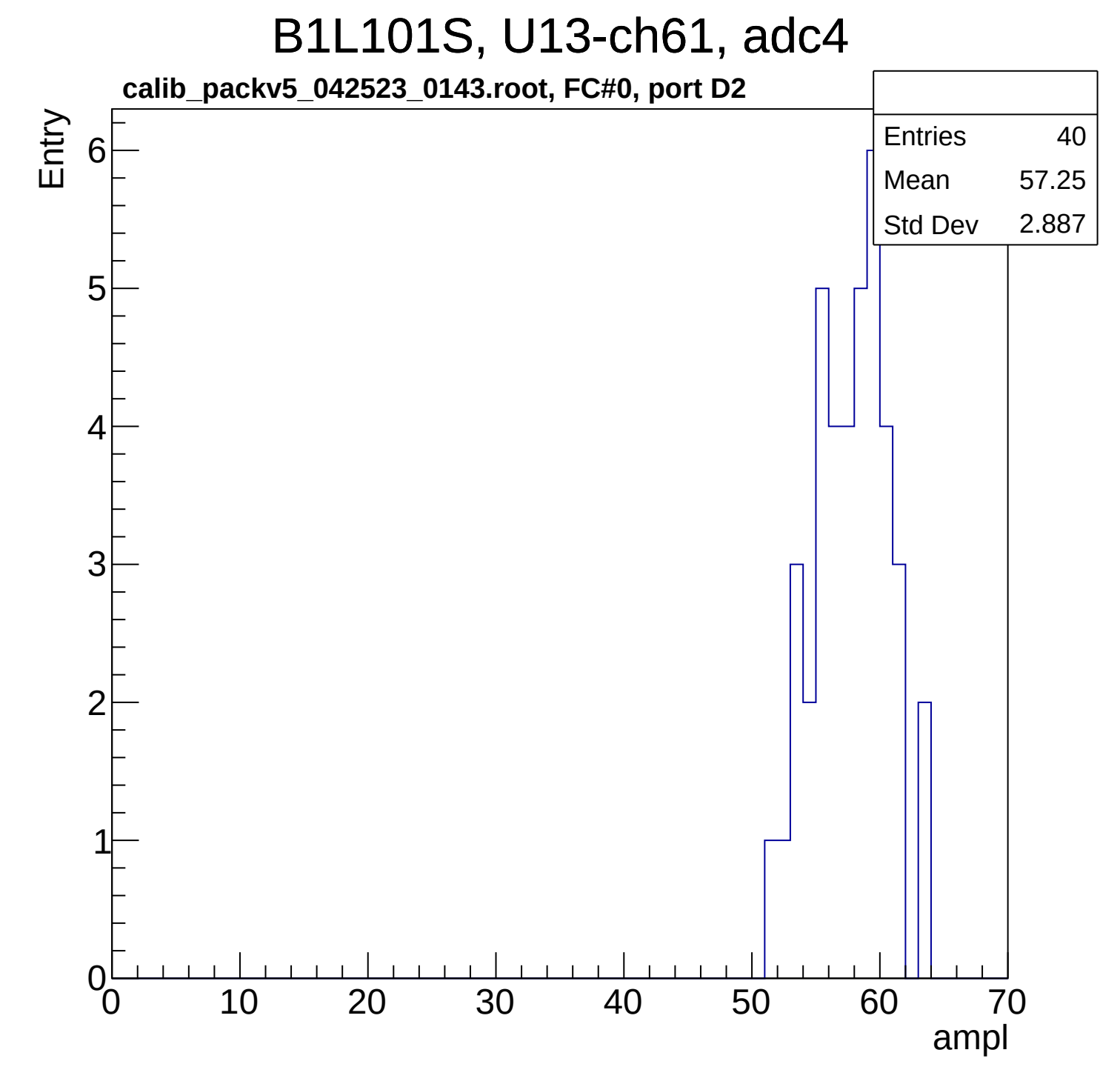
B1L101S, U13-ch61, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 57.25
Std Dev 2.887
Entry
ampl
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3
4
5
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0
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40
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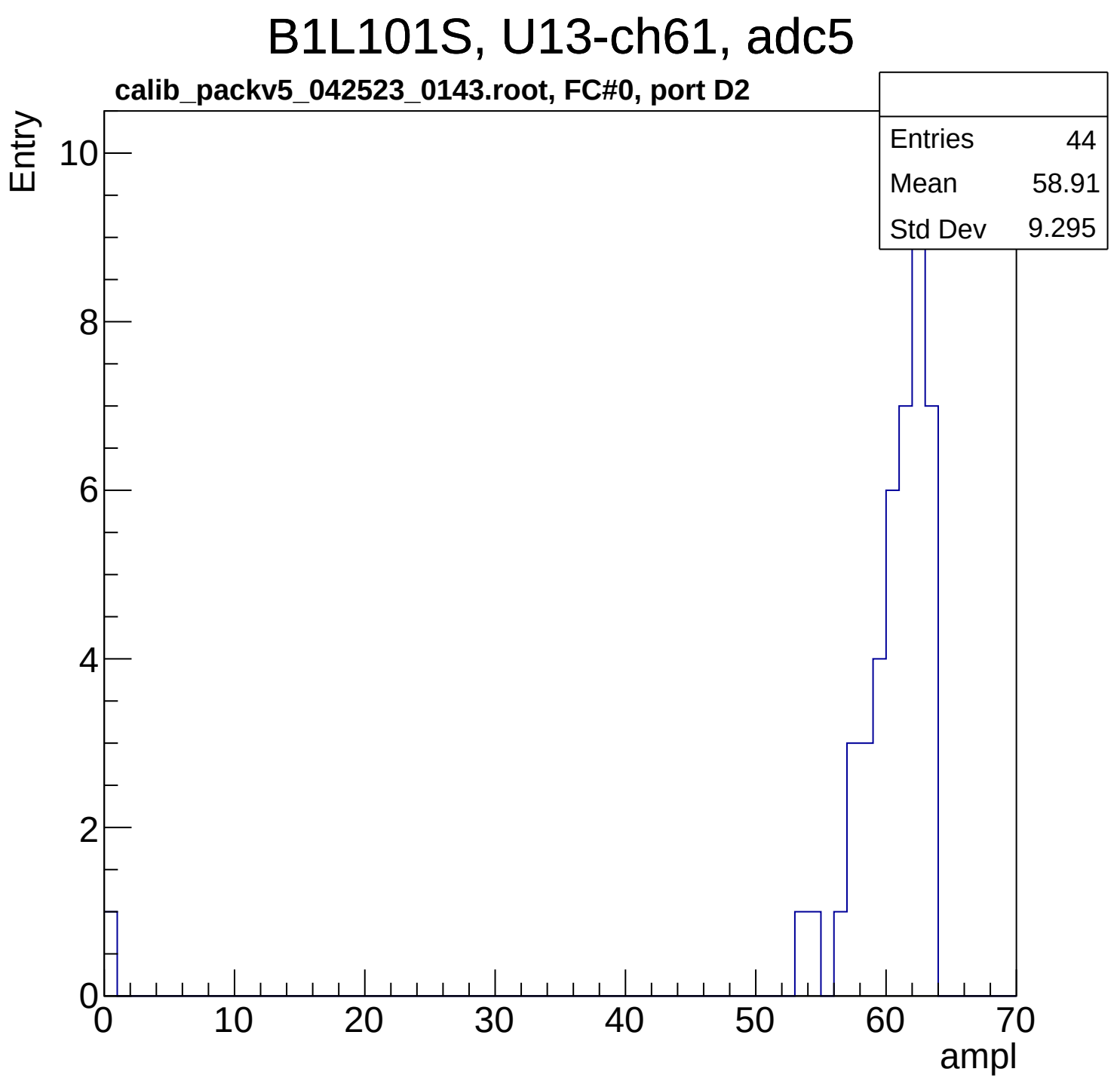
B1L101S, U13-ch61, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 58.91
Std Dev 9.295
Entry
ampl
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4
6
8
10
0
10
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40
50
60
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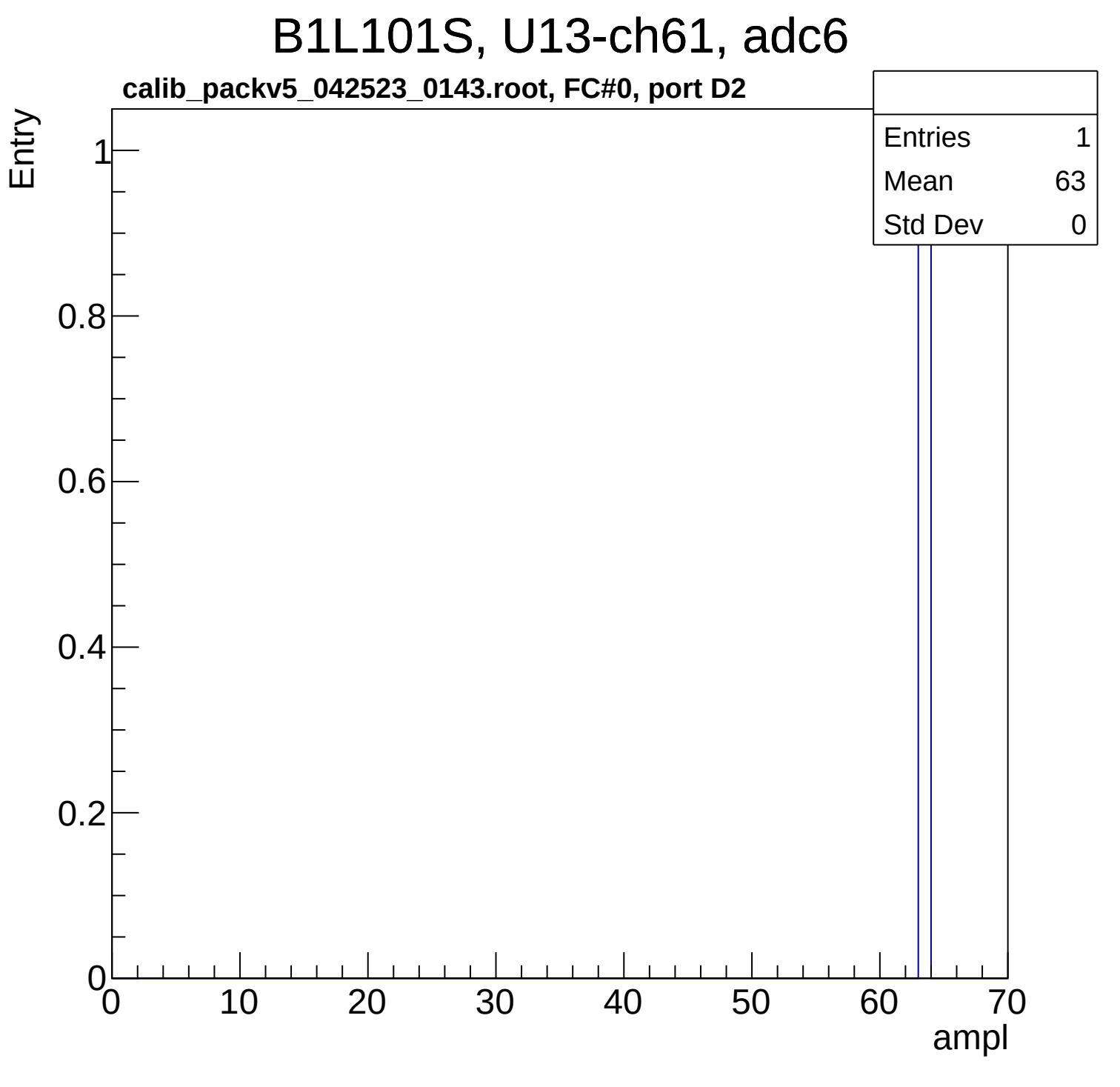
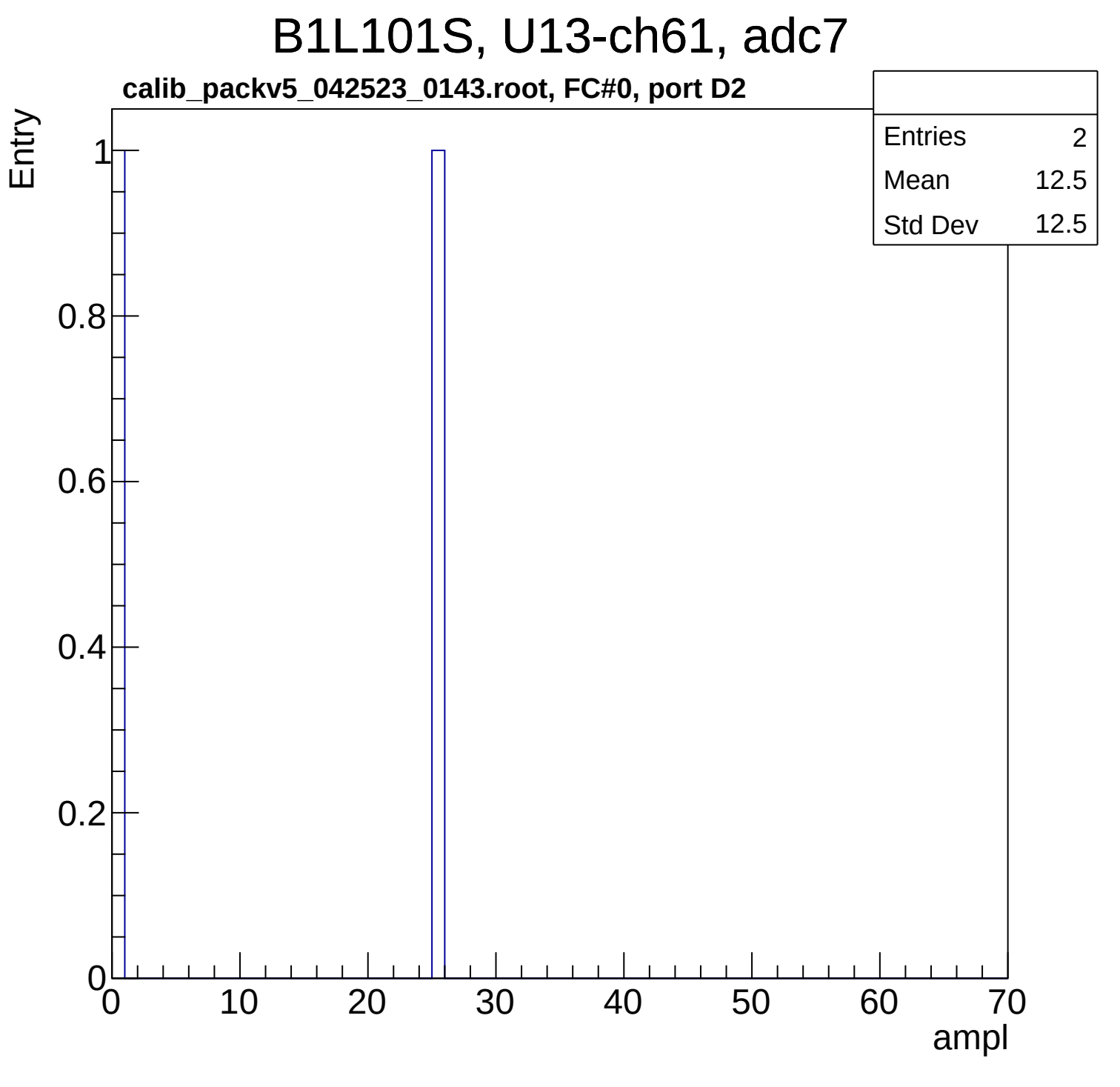

B1L101S, U13-ch61, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch61, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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40
50
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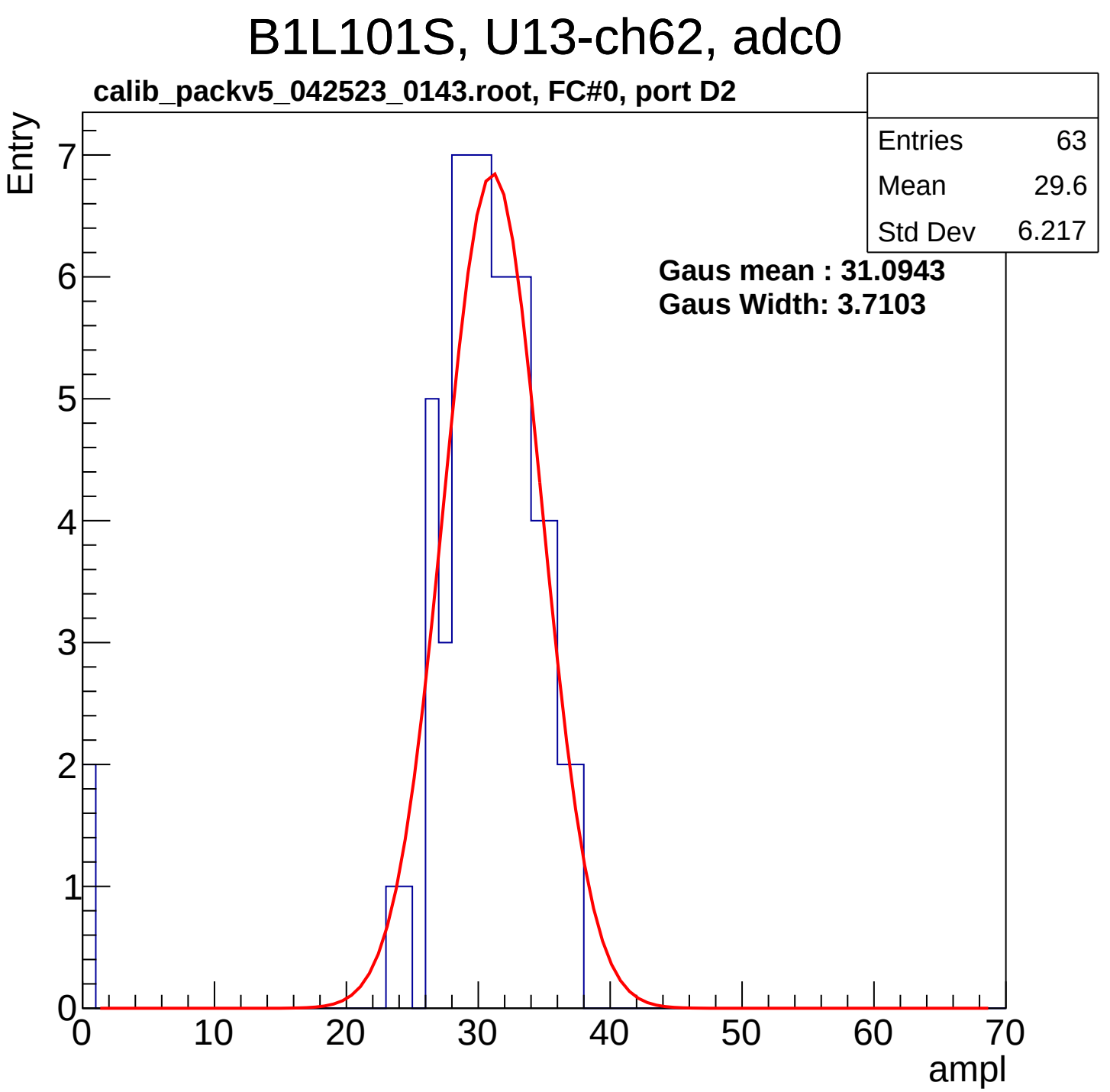
70
ampl


B1L101S, U13-ch62, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 29.6
Std Dev 6.217
Gaus mean : 31.0943
Gaus Width: 3.7103
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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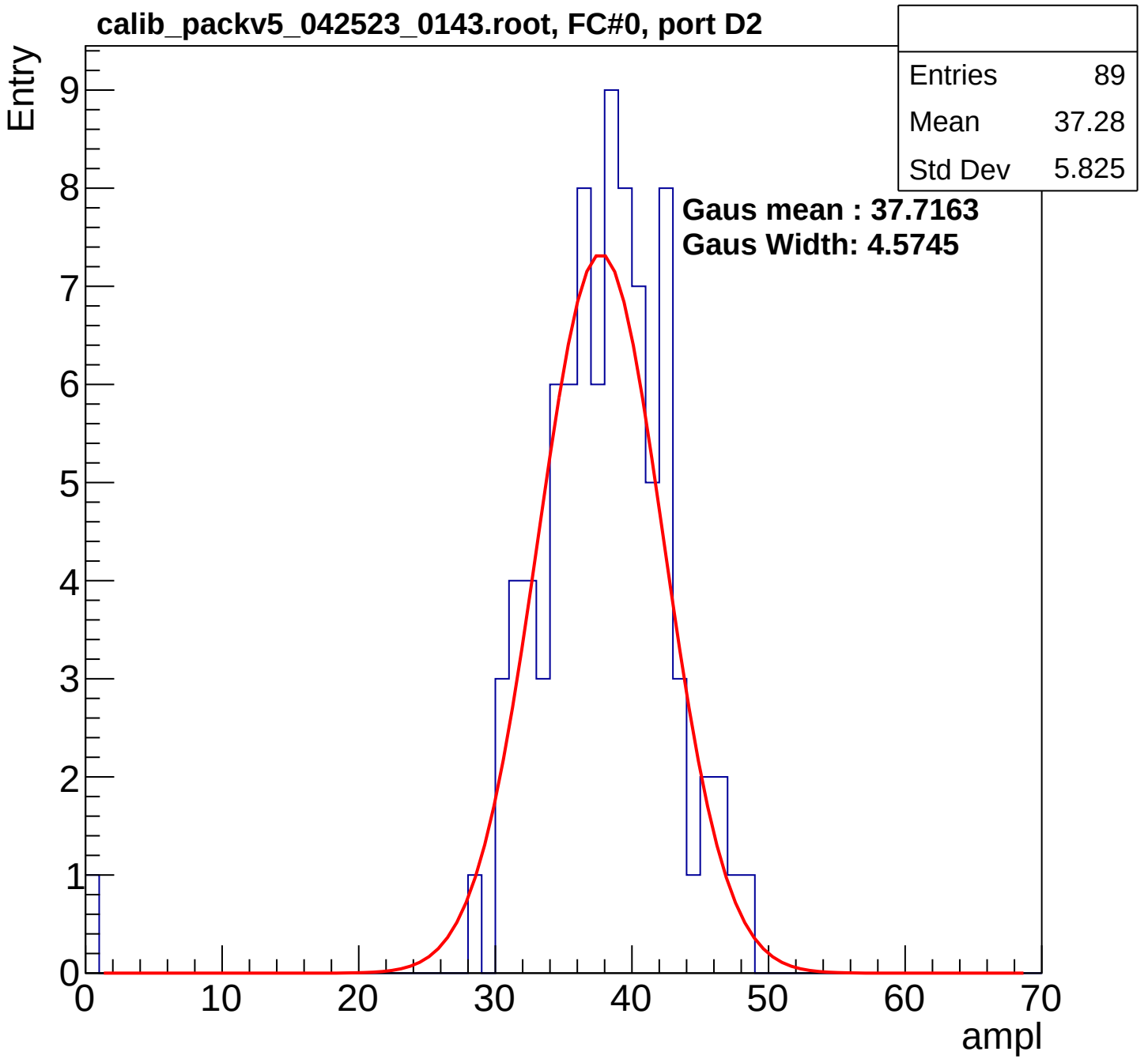
B1L101S, U13-ch62, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 89
Mean 37.28
Std Dev 5.825
Gaus mean : 37.7163
Gaus Width: 4.5745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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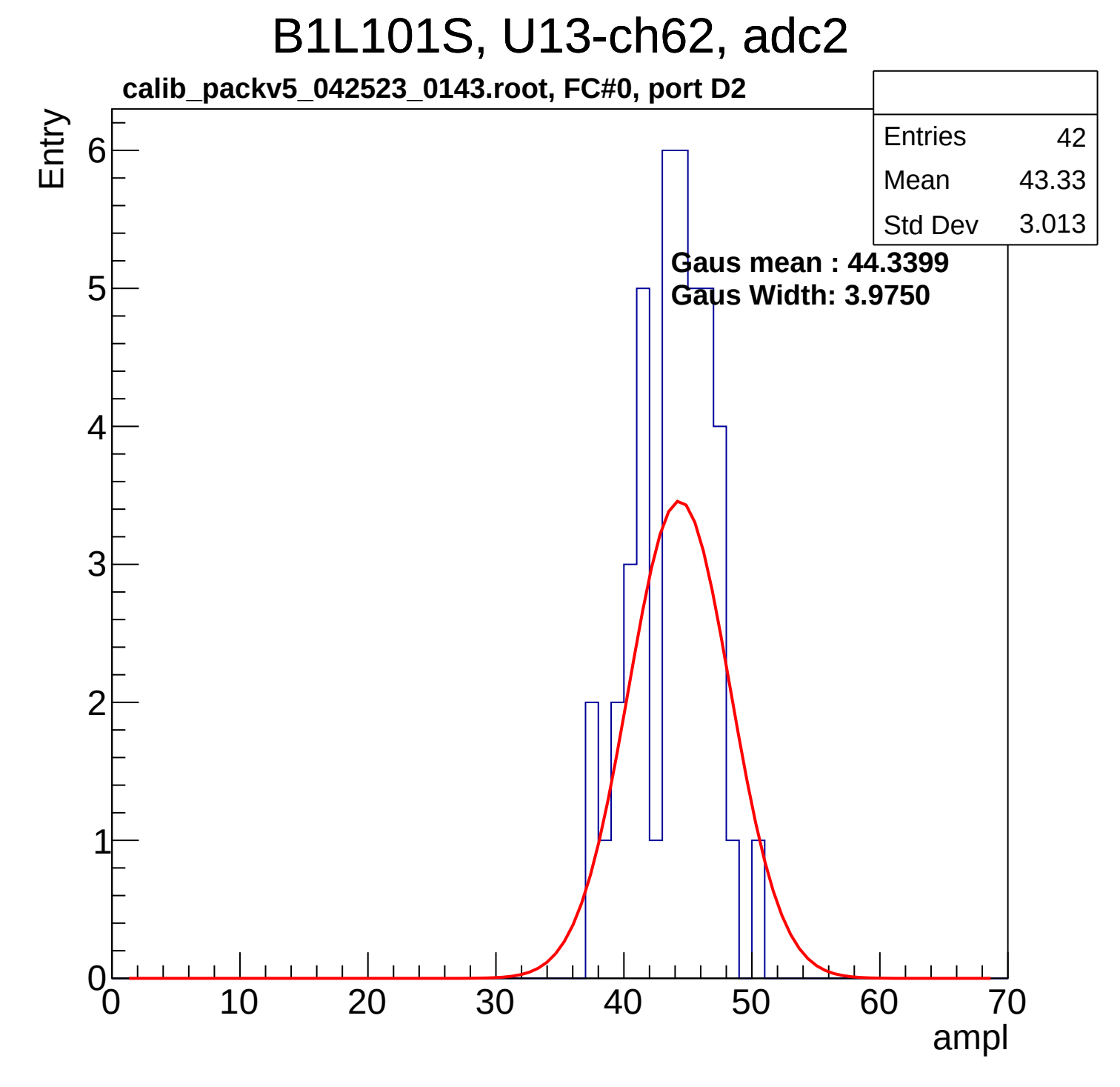
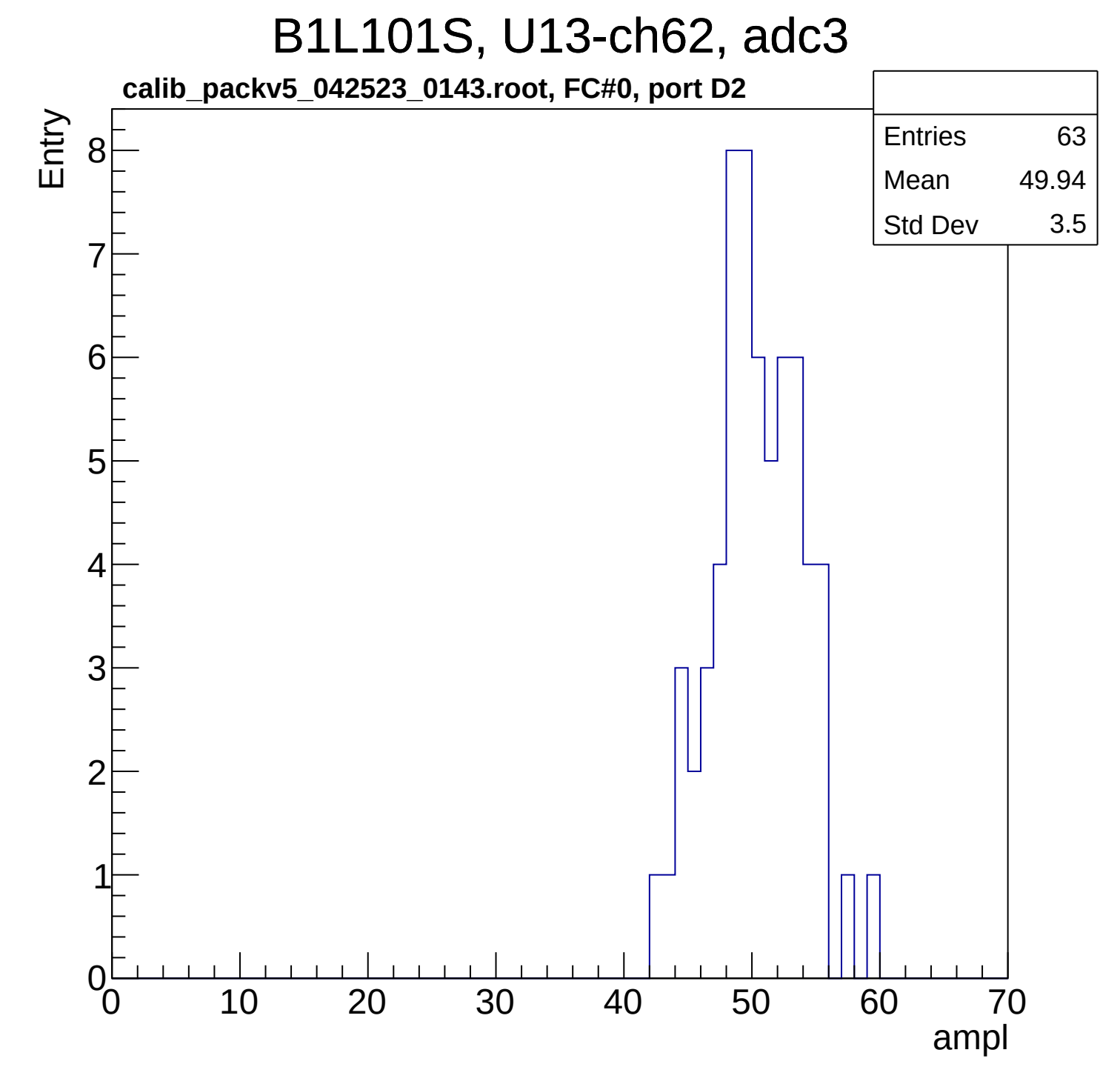

B1L101S, U13-ch62, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 43.33
Std Dev 3.013
Gaus mean : 44.3399
Gaus Width: 3.9750
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch62, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 49.94
Std Dev 3.5
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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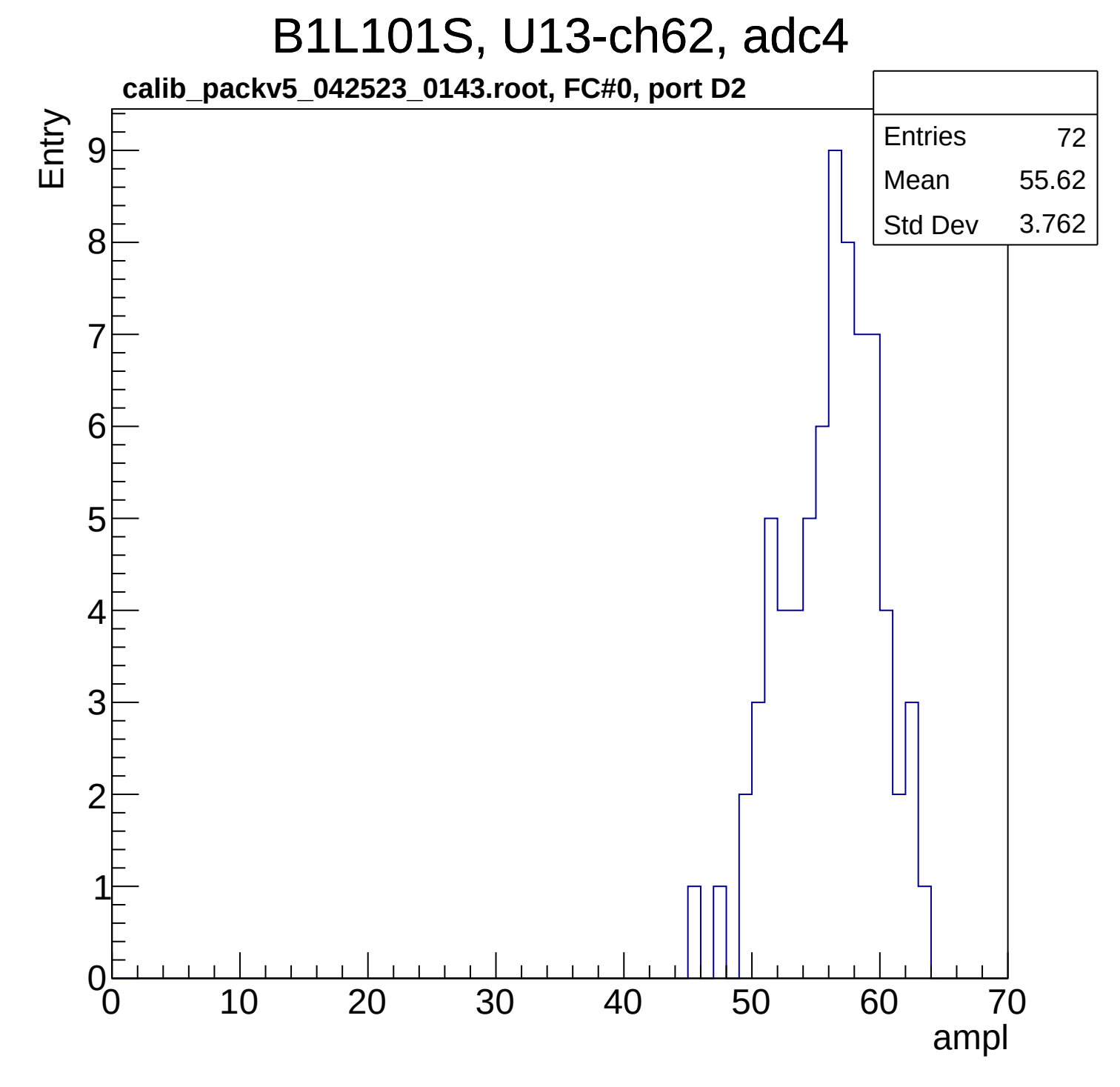
B1L101S, U13-ch62, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 55.62
Std Dev 3.762
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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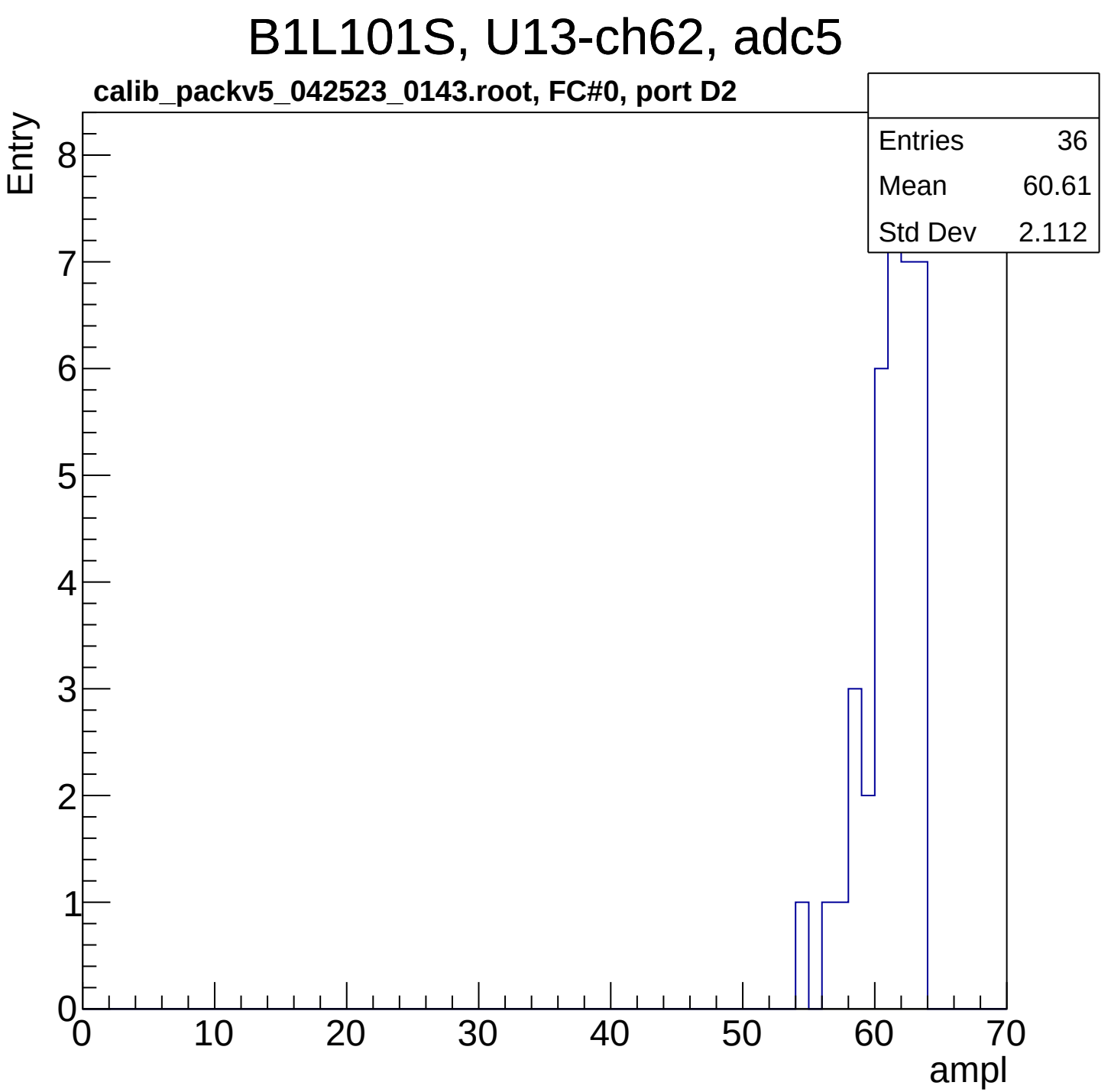
B1L101S, U13-ch62, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 36
Mean 60.61
Std Dev 2.112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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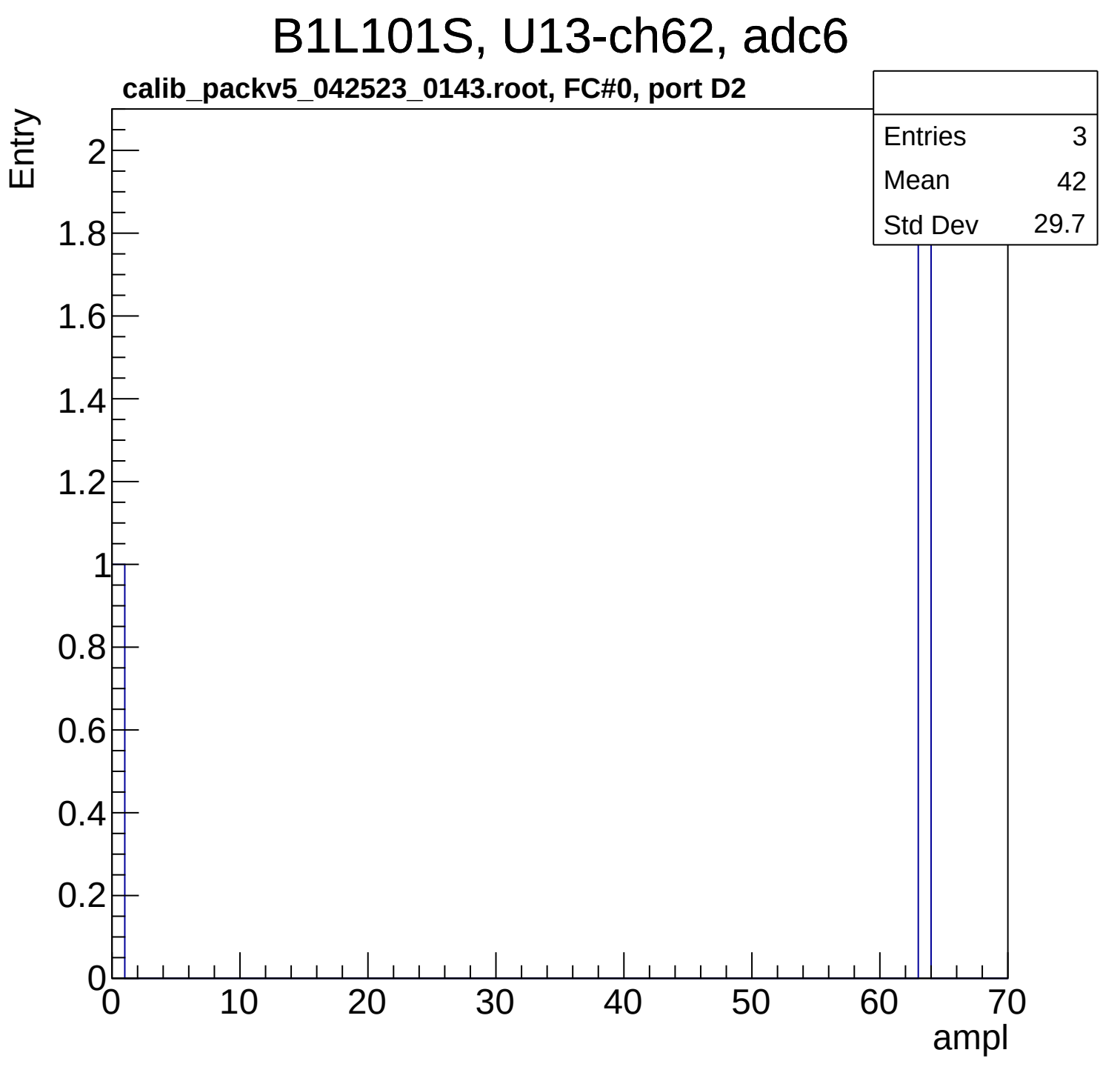
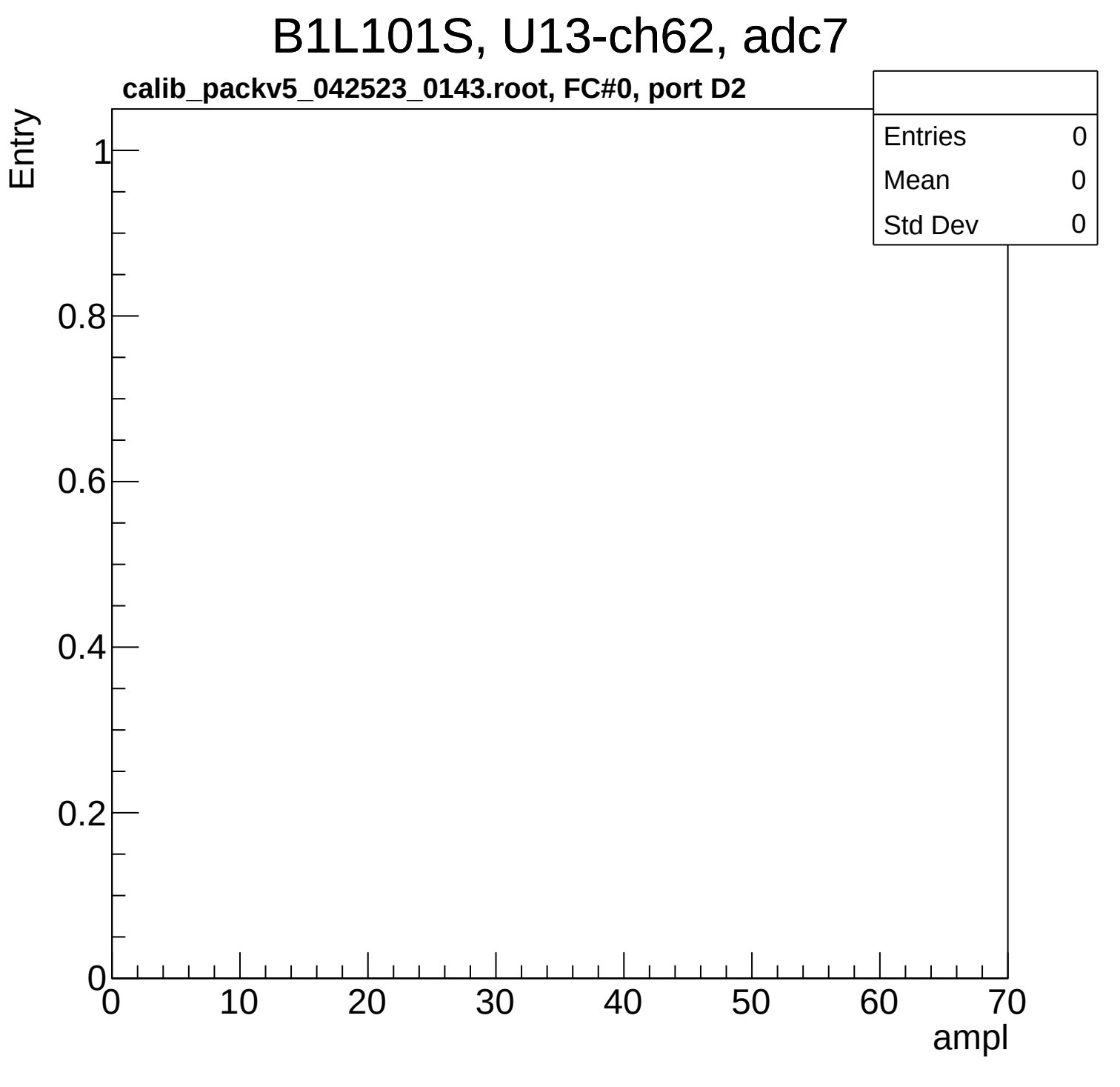
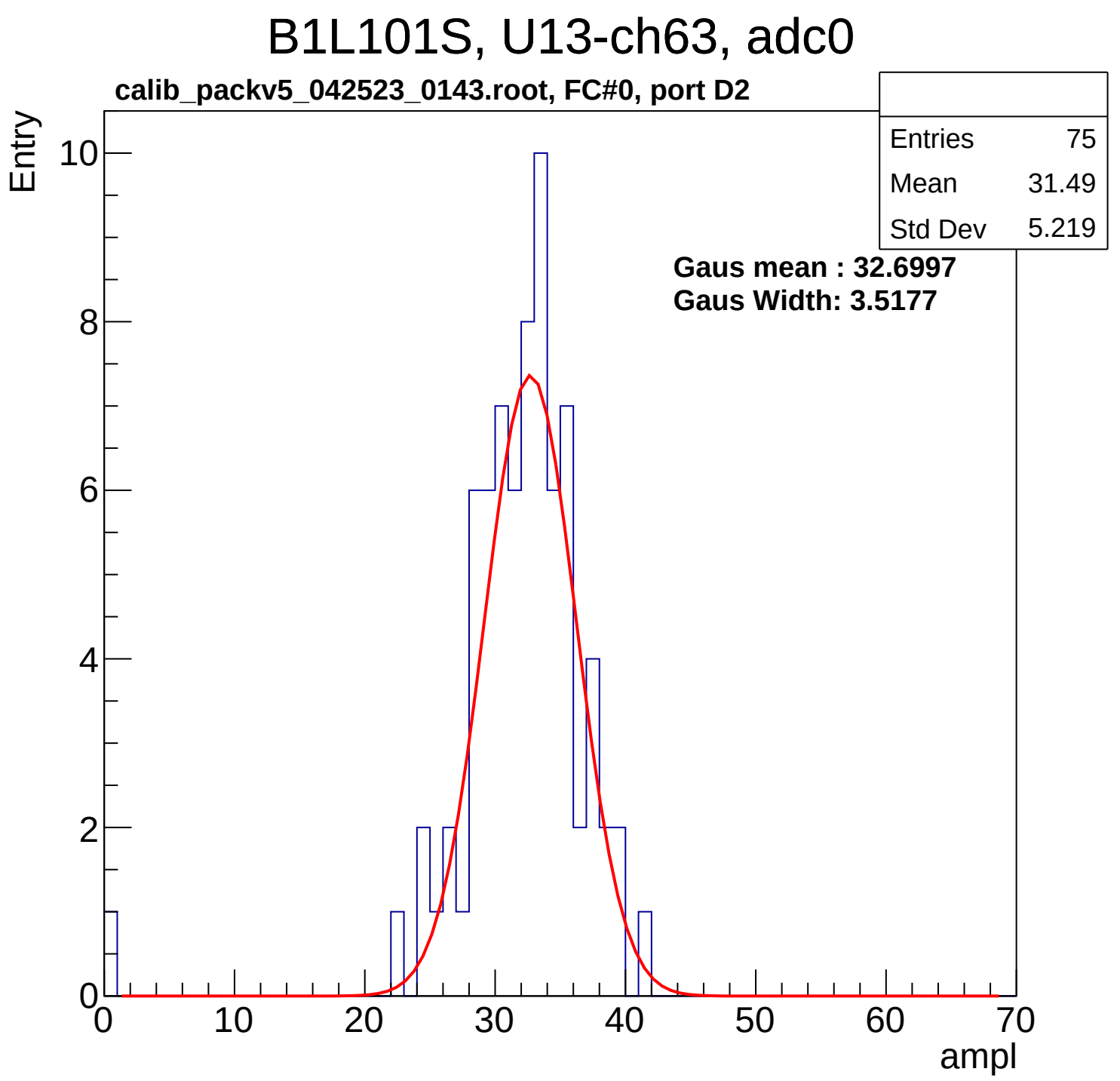
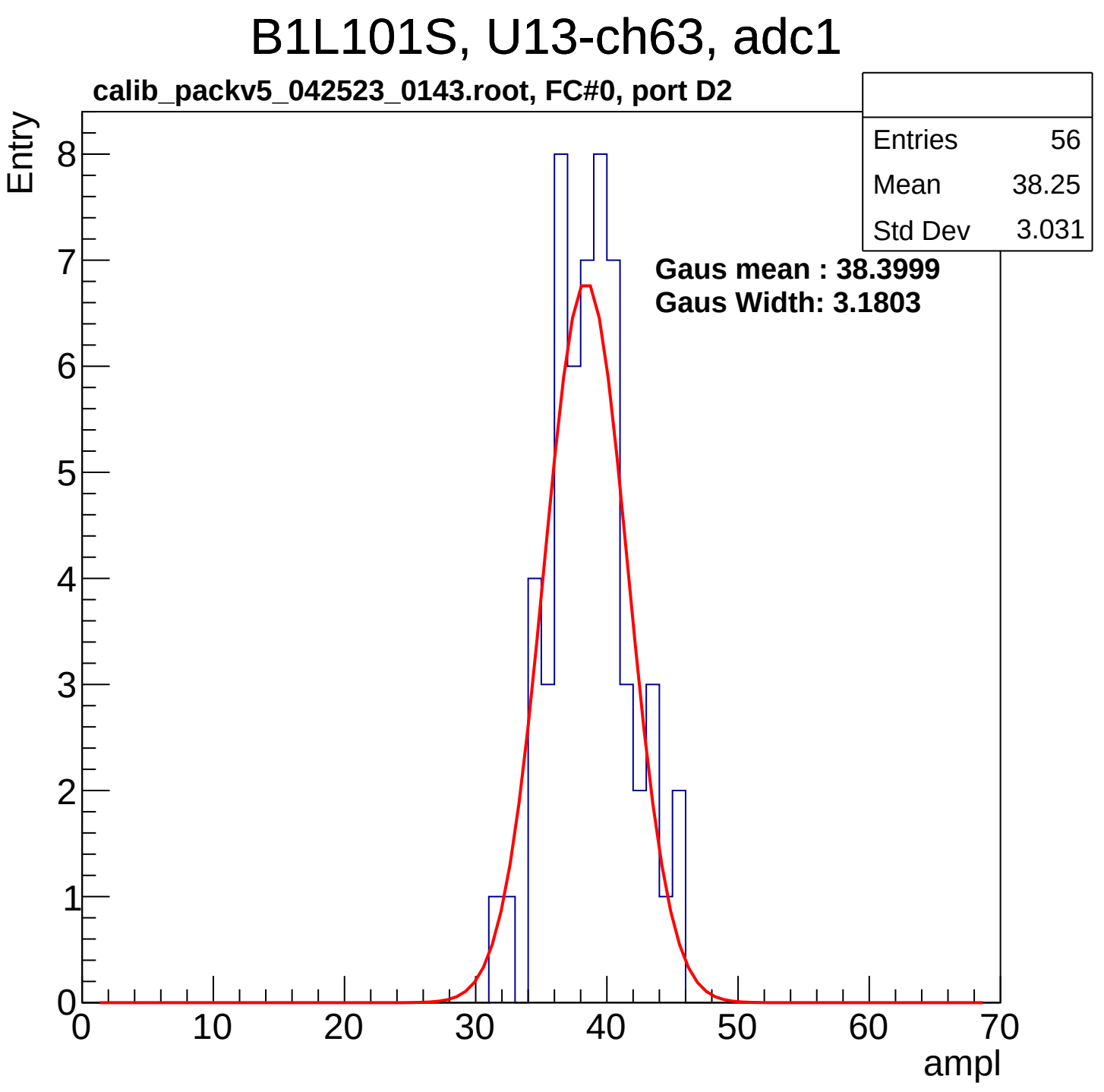
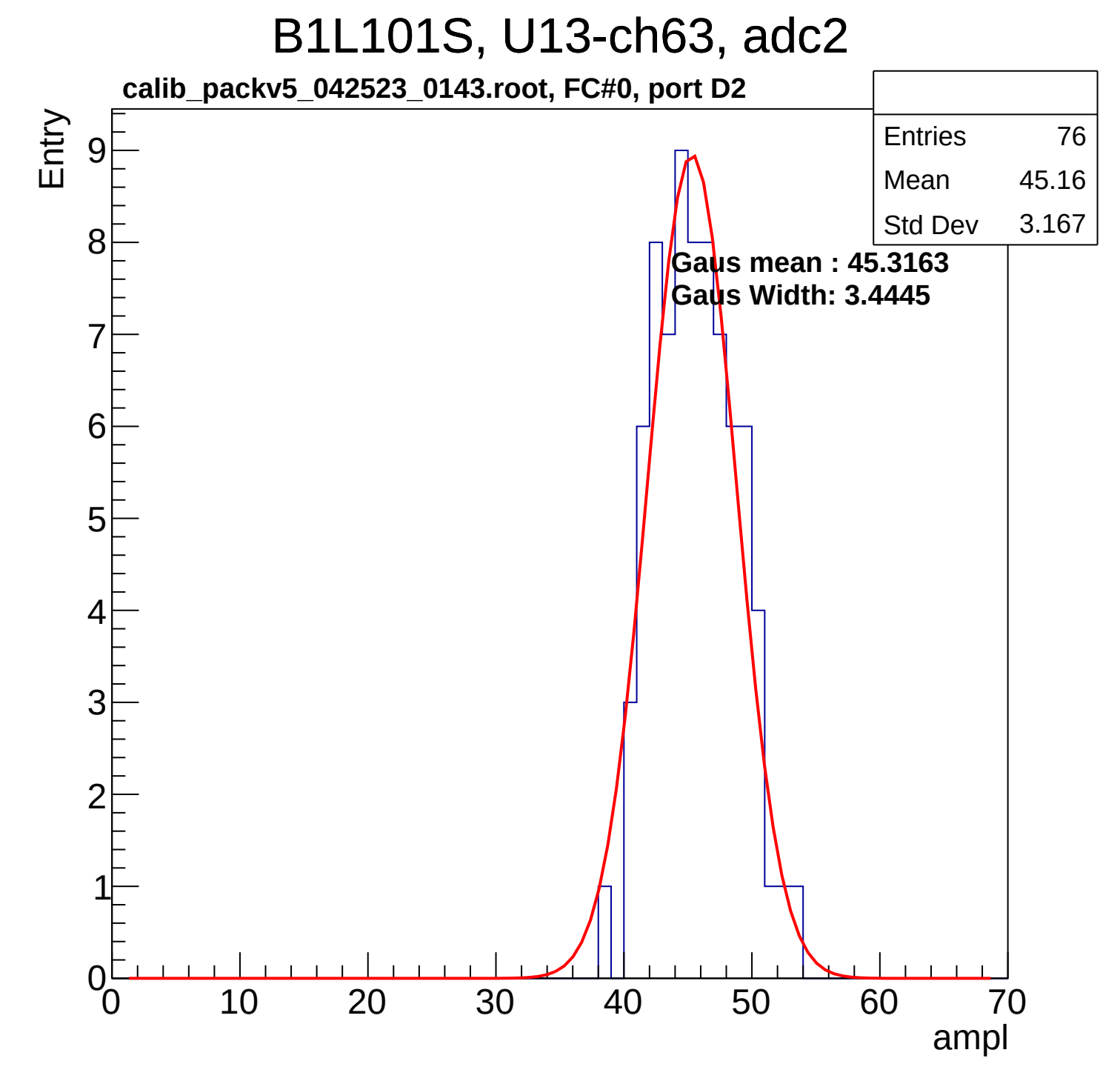

B1L101S, U13-ch62, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch62, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch63, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 31.49
Std Dev 5.219
Gaus mean : 32.6997
Gaus Width: 3.5177
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch63, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 38.25
Std Dev 3.031
Gaus mean : 38.3999
Gaus Width: 3.1803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch63, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 45.16
Std Dev 3.167
Gaus mean : 45.3163
Gaus Width: 3.4445
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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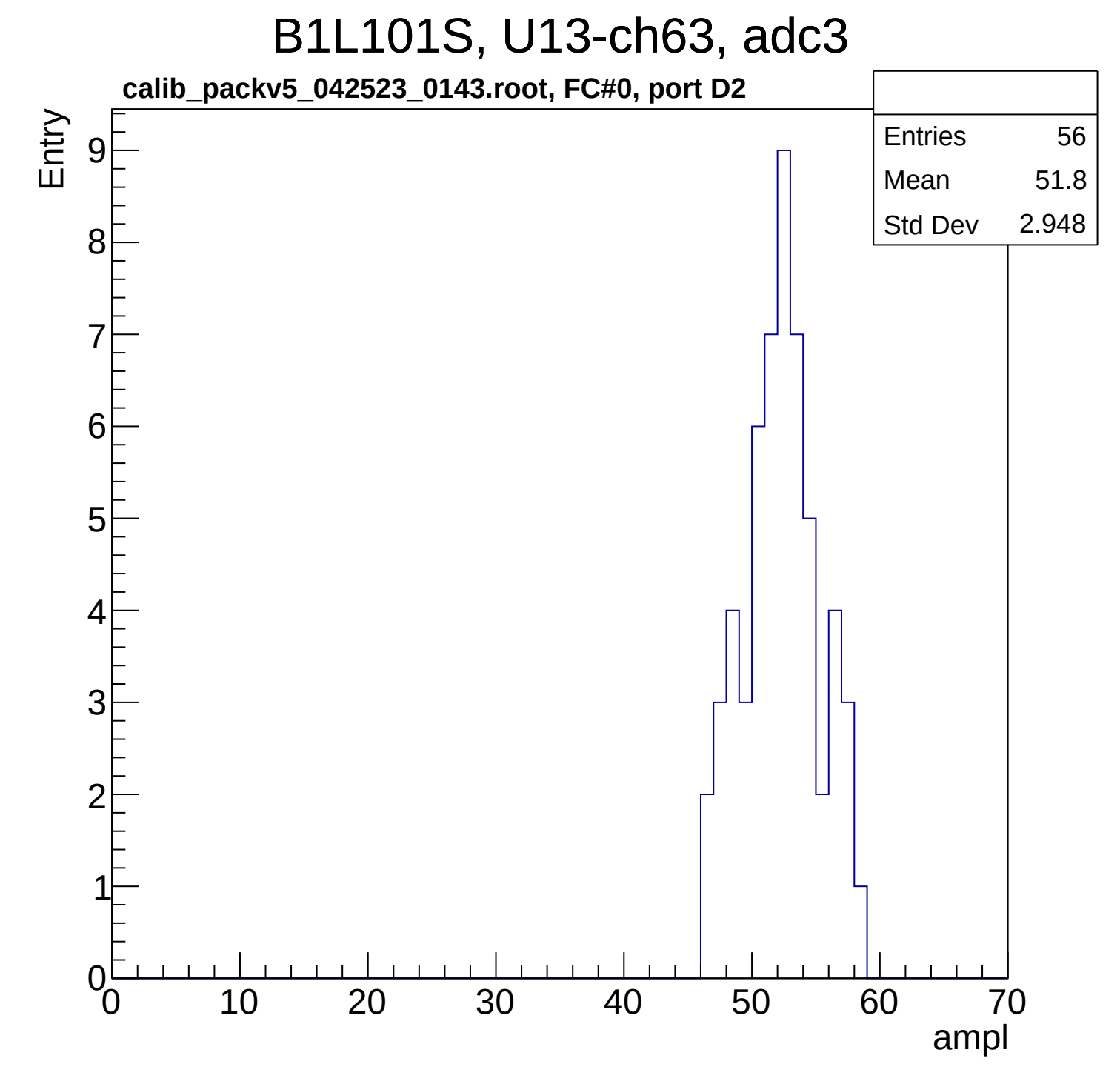
B1L101S, U13-ch63, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 51.8
Std Dev 2.948
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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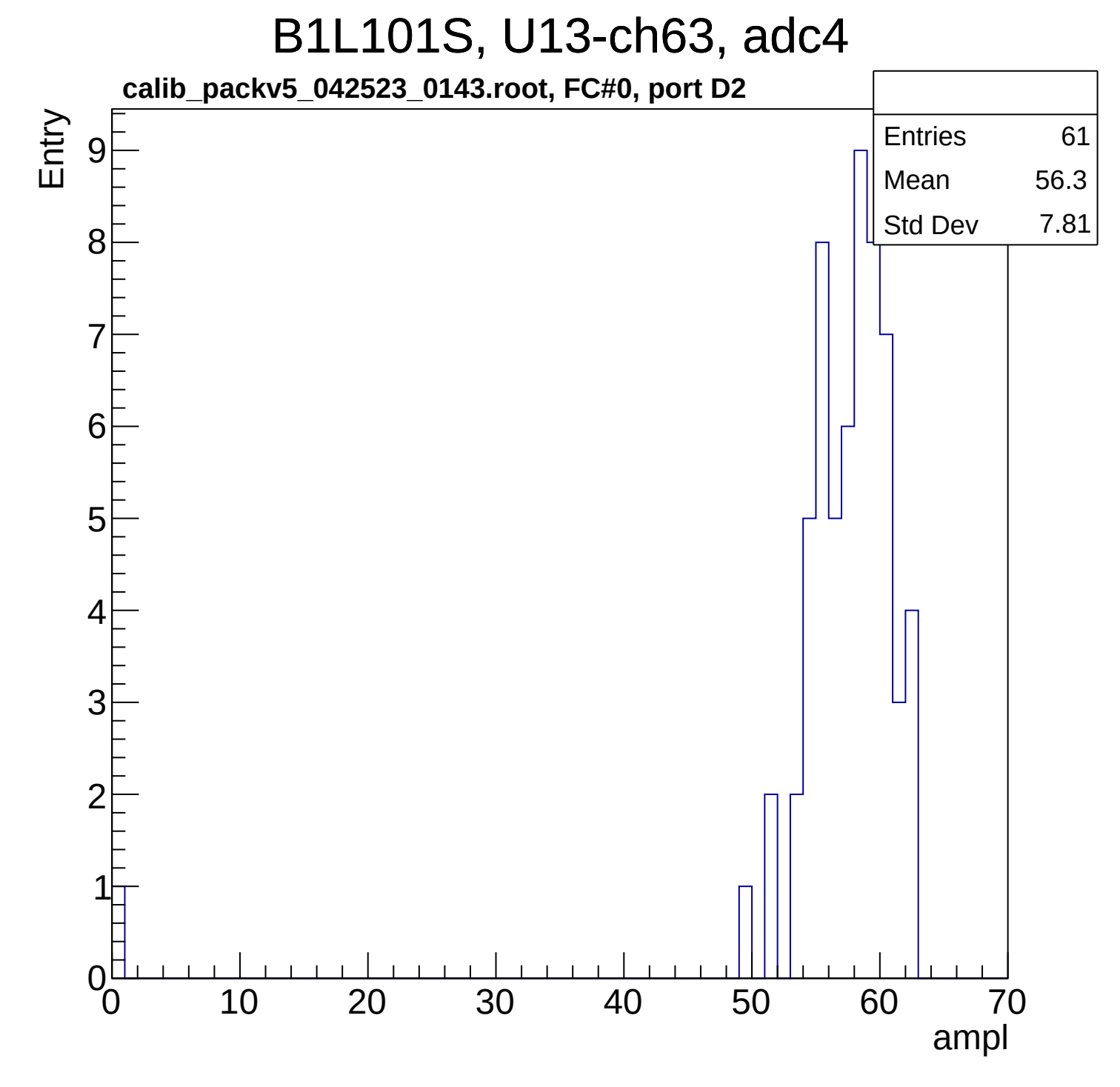
B1L101S, U13-ch63, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 56.3
Std Dev 7.81
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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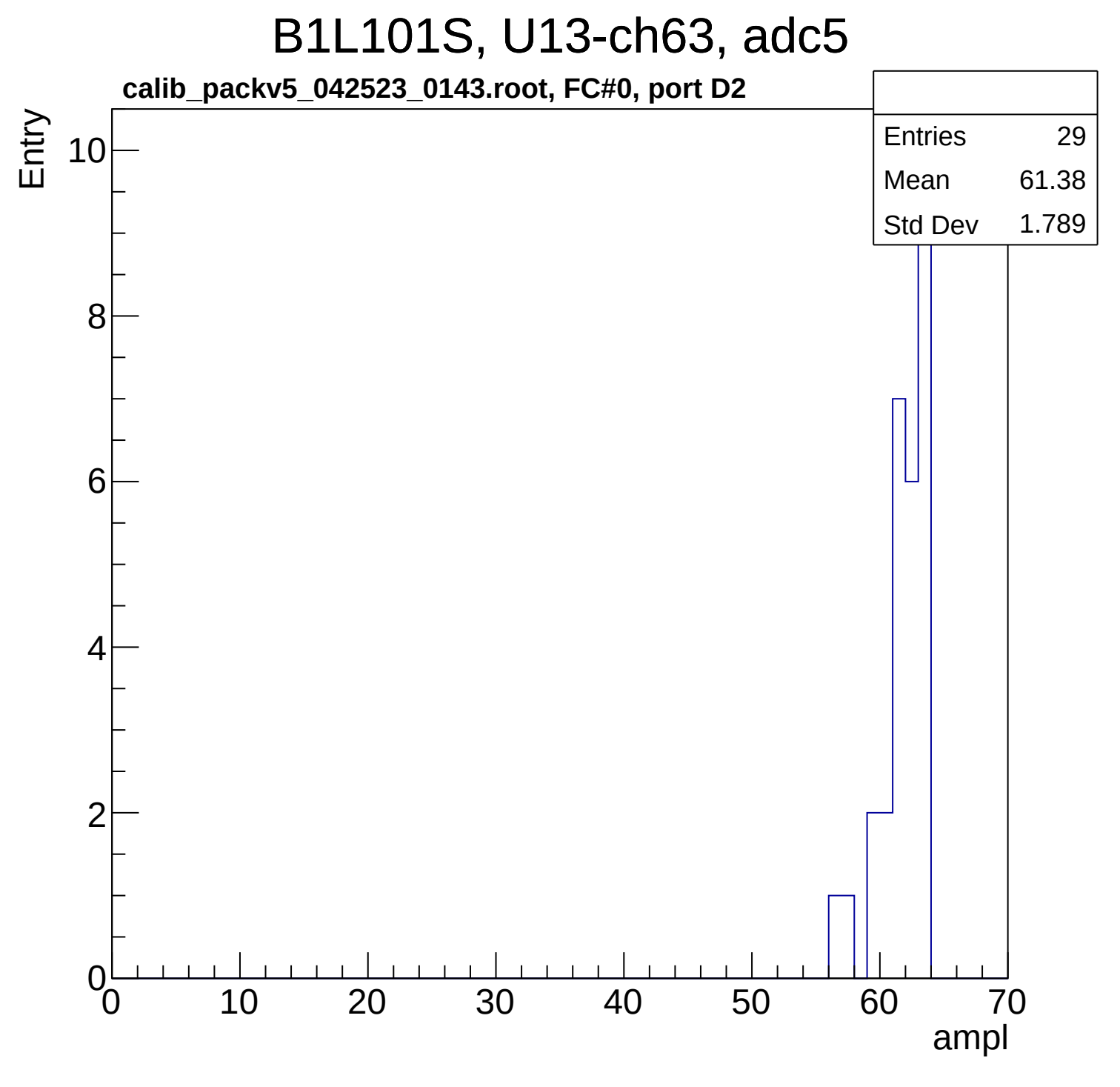
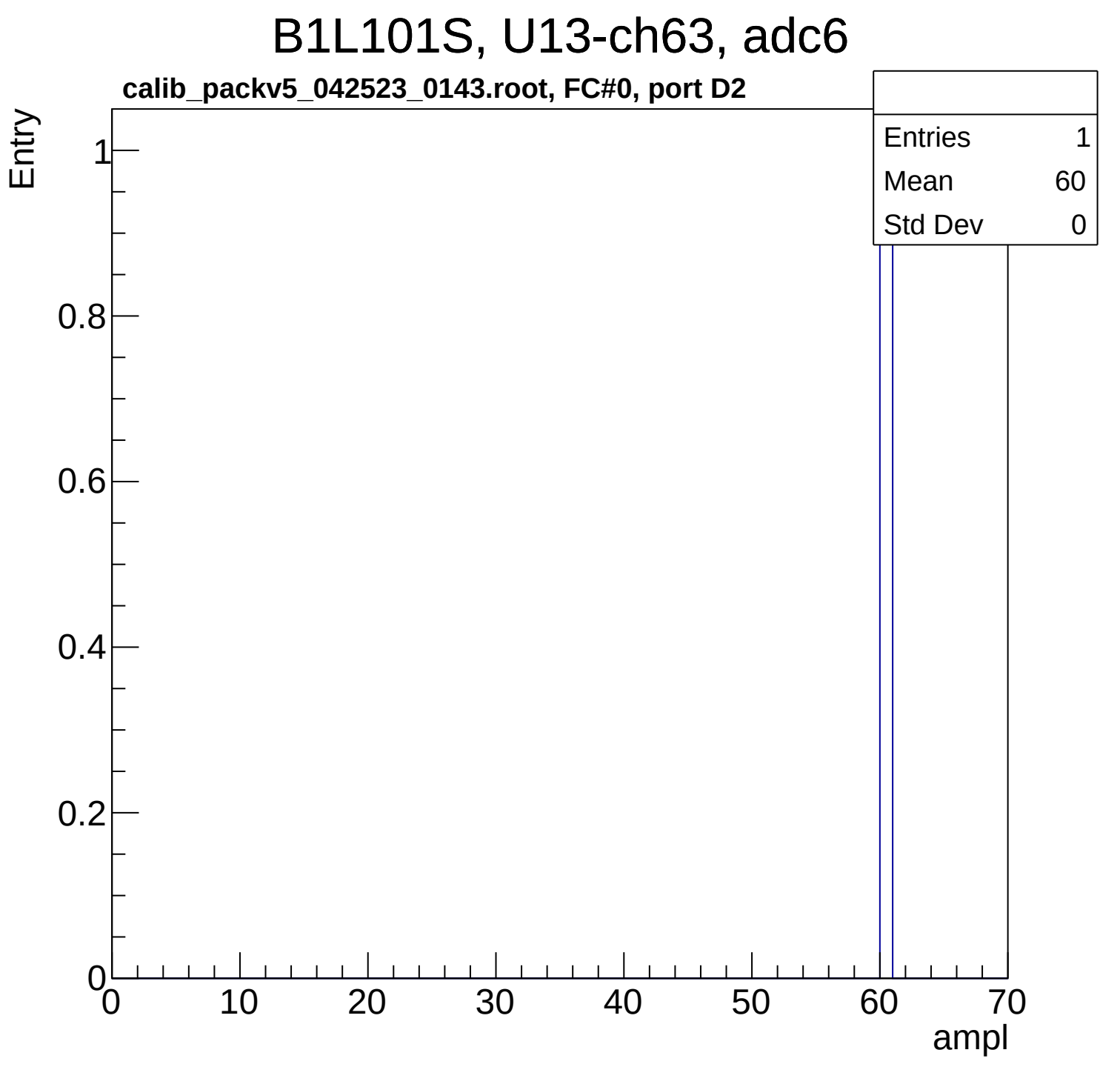
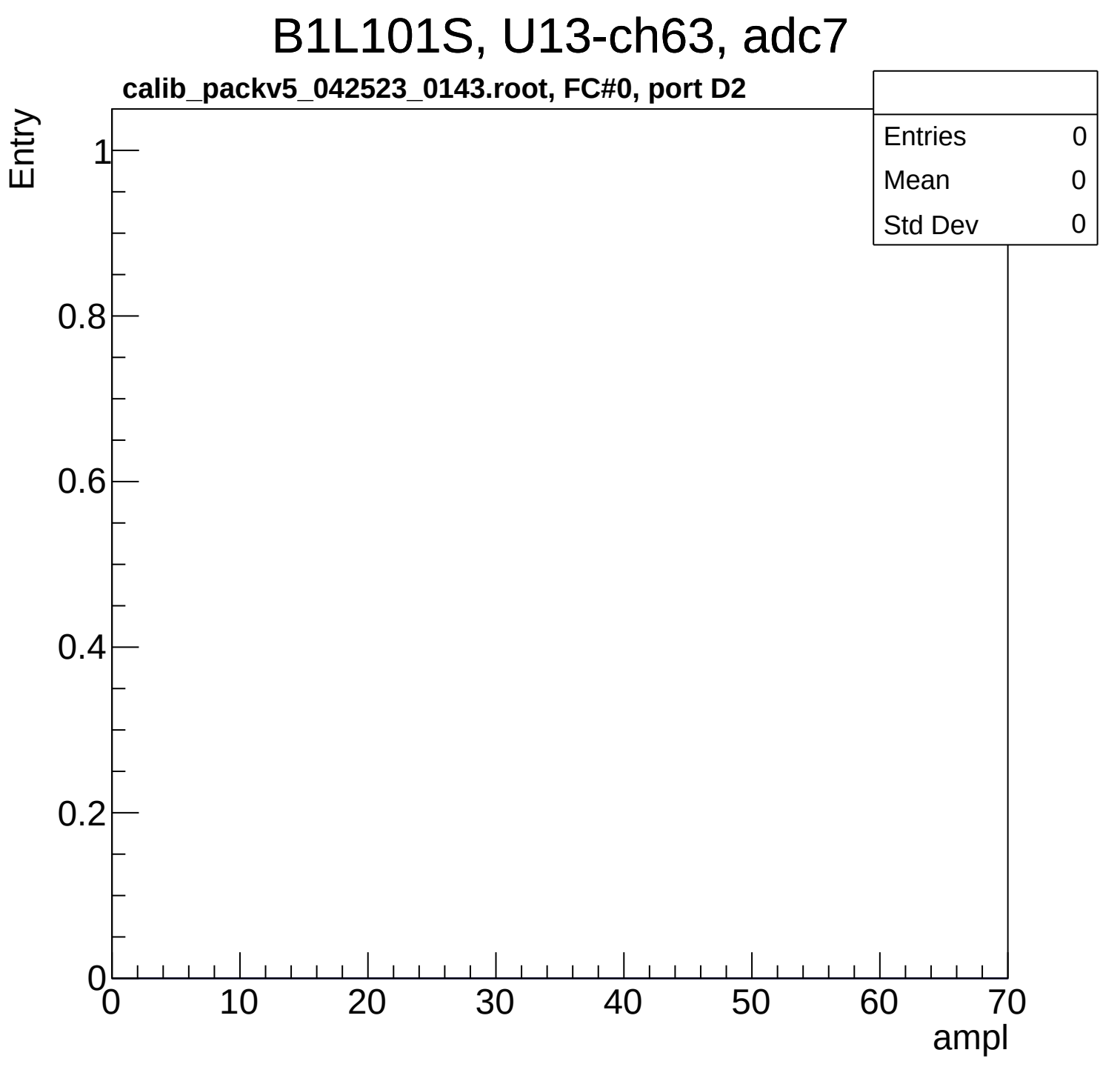

B1L101S, U13-ch63, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 29
Mean 61.38
Std Dev 1.789
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch63, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch63, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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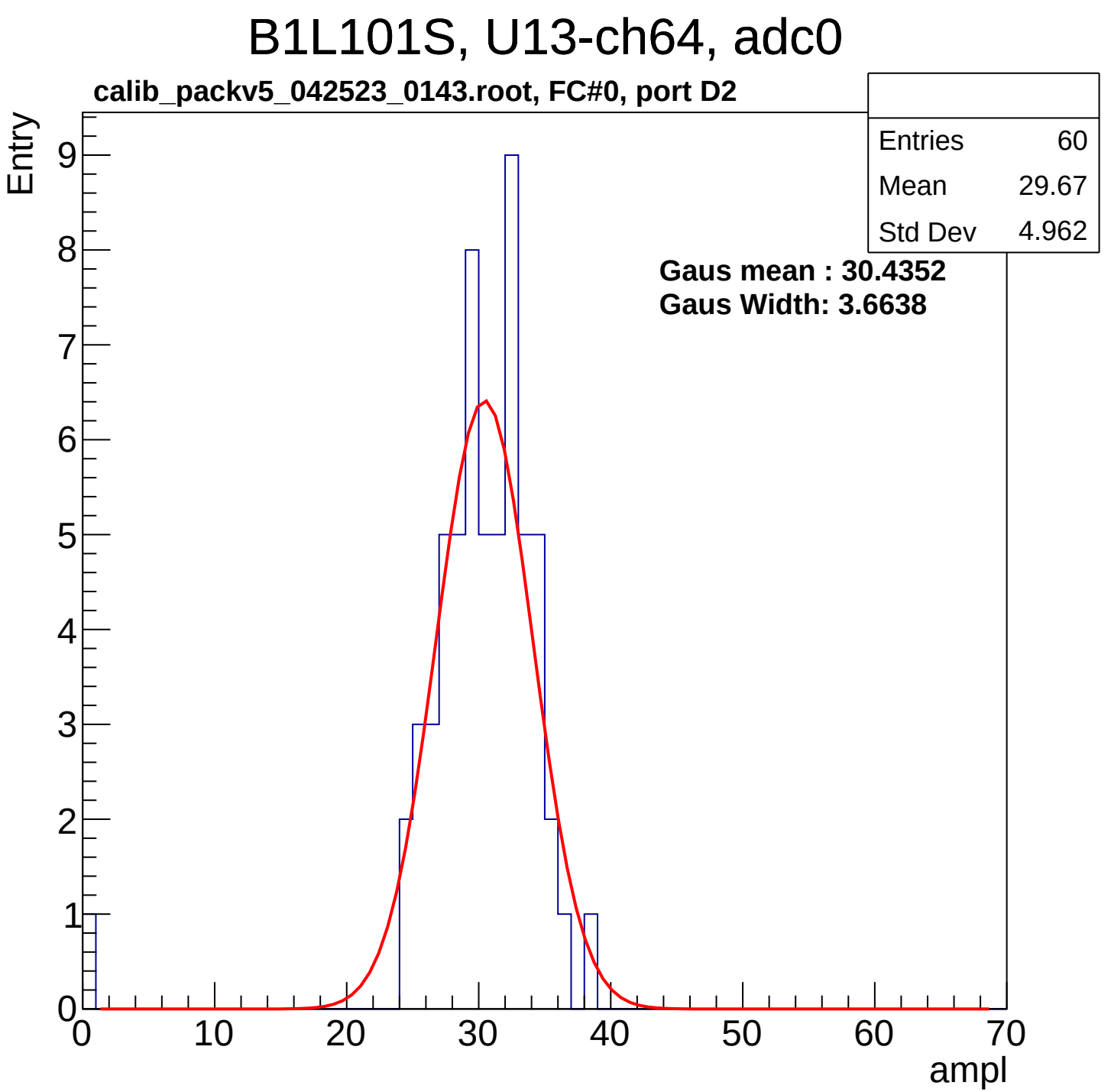
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch64, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 29.67
Std Dev 4.962
Gaus mean : 30.4352
Gaus Width: 3.6638
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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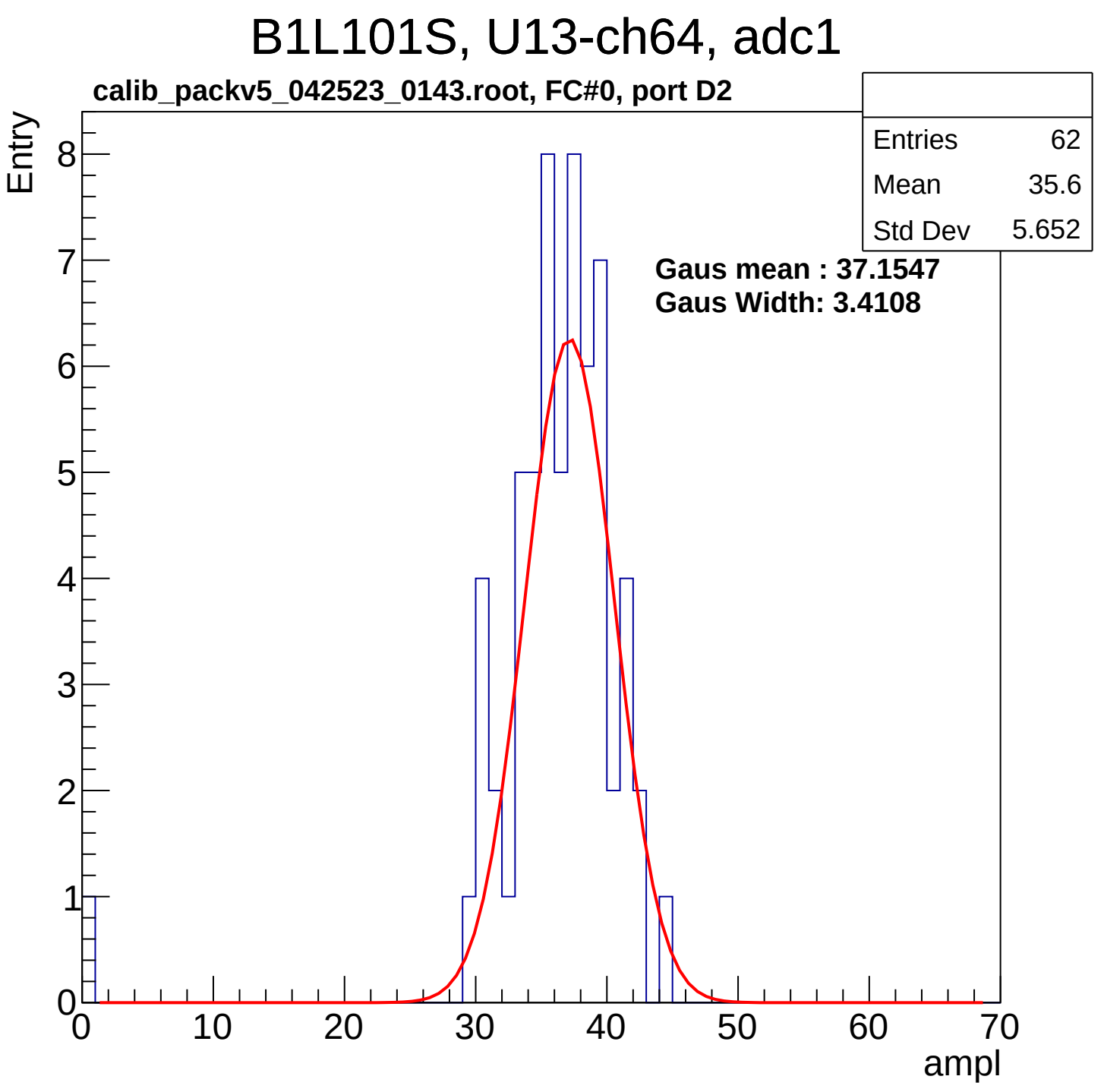
B1L101S, U13-ch64, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 35.6
Std Dev 5.652
Gaus mean : 37.1547
Gaus Width: 3.4108
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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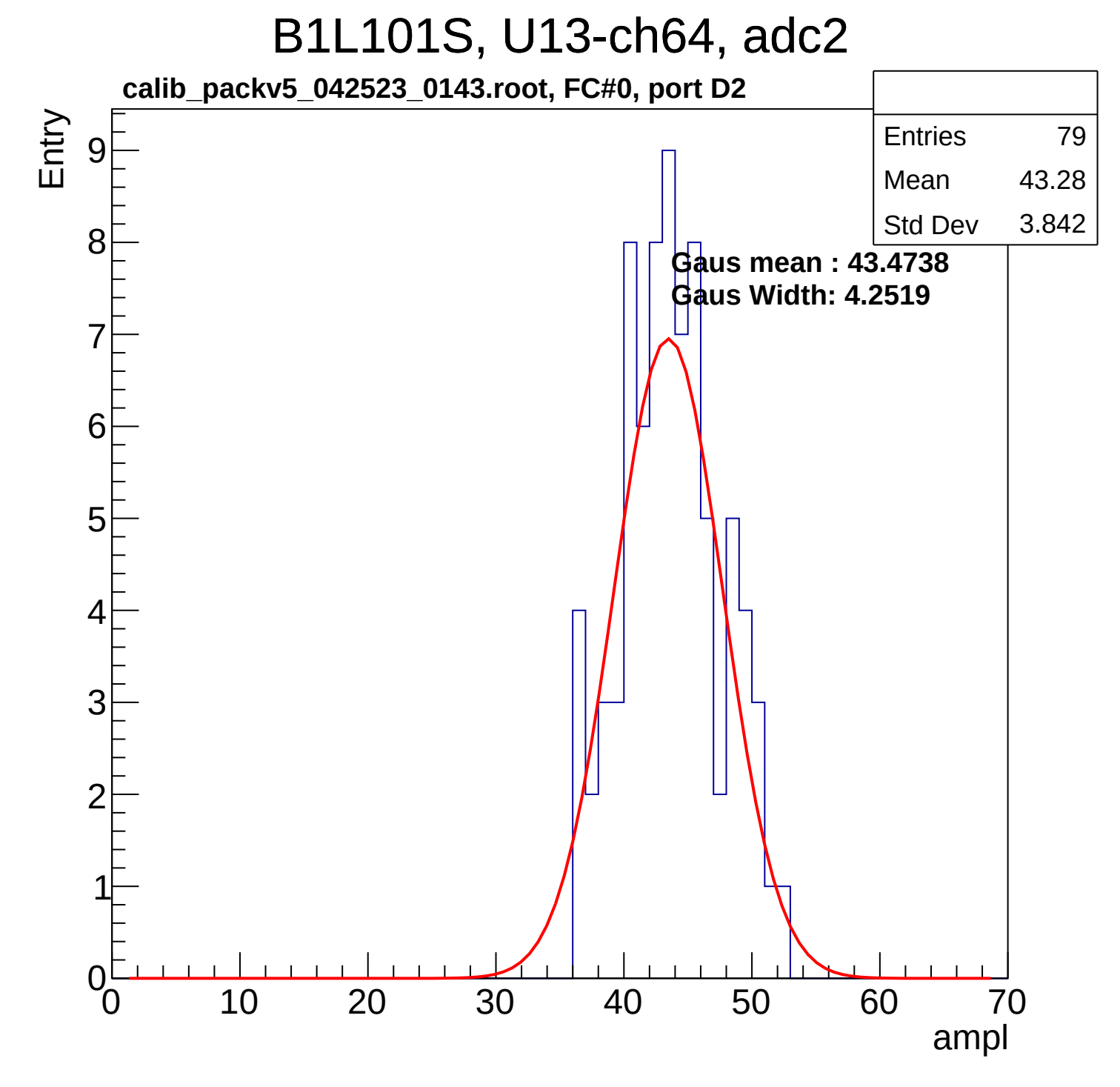
B1L101S, U13-ch64, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 79
Mean 43.28
Std Dev 3.842
Gaus mean : 43.4738
Gaus Width: 4.2519
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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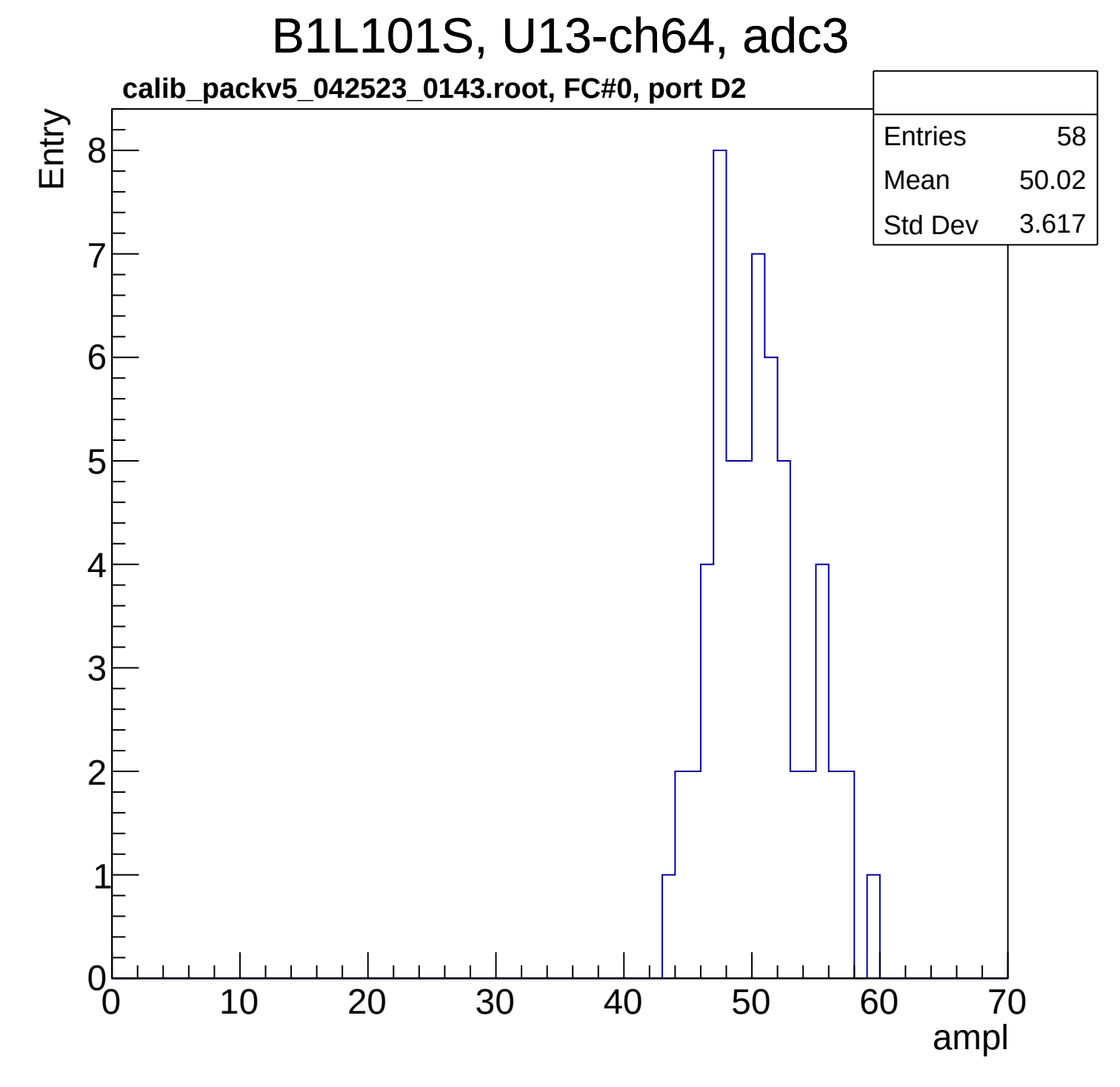
B1L101S, U13-ch64, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 50.02
Std Dev 3.617
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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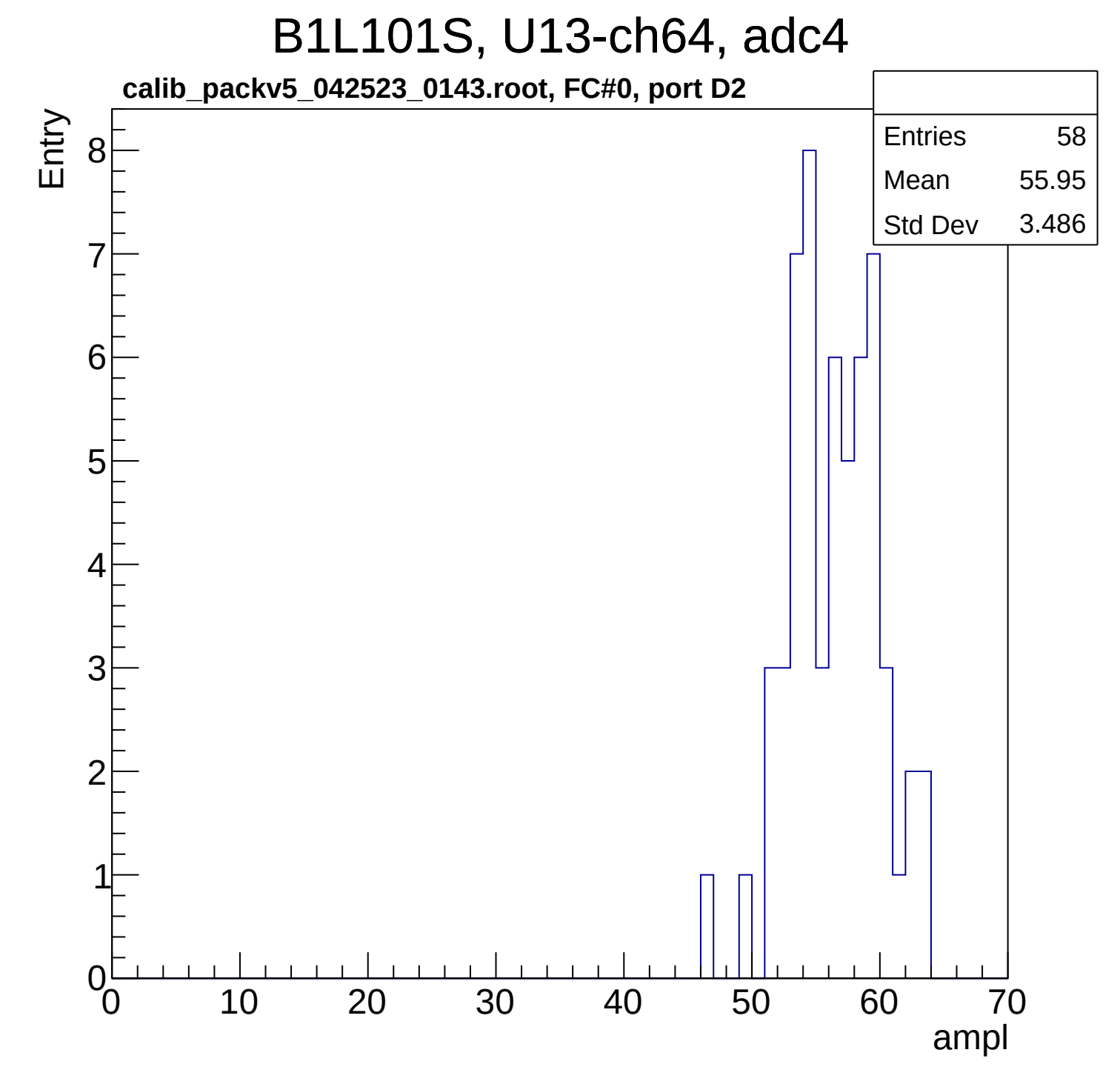
B1L101S, U13-ch64, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 55.95
Std Dev 3.486
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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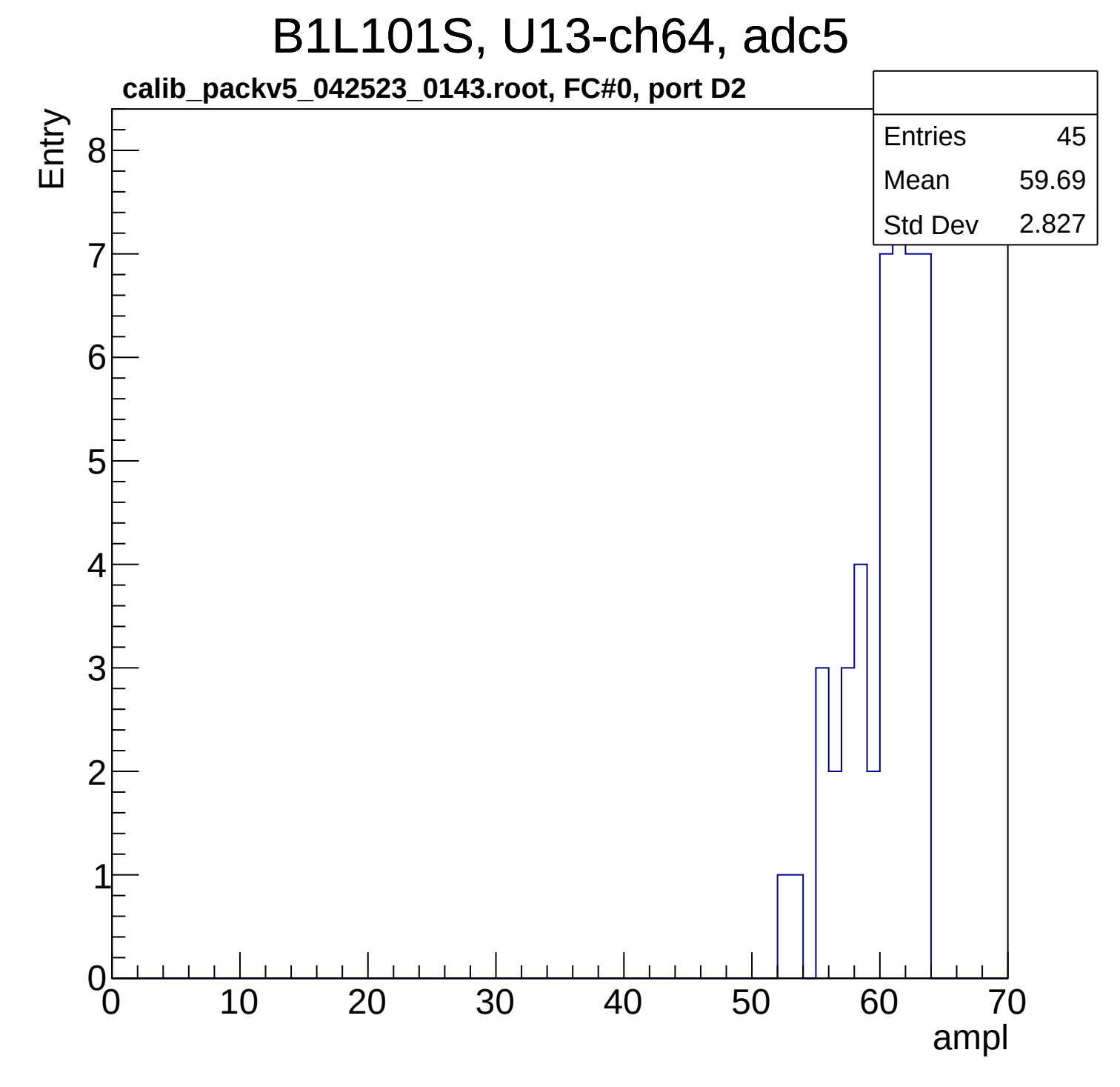
B1L101S, U13-ch64, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 59.69
Std Dev 2.827
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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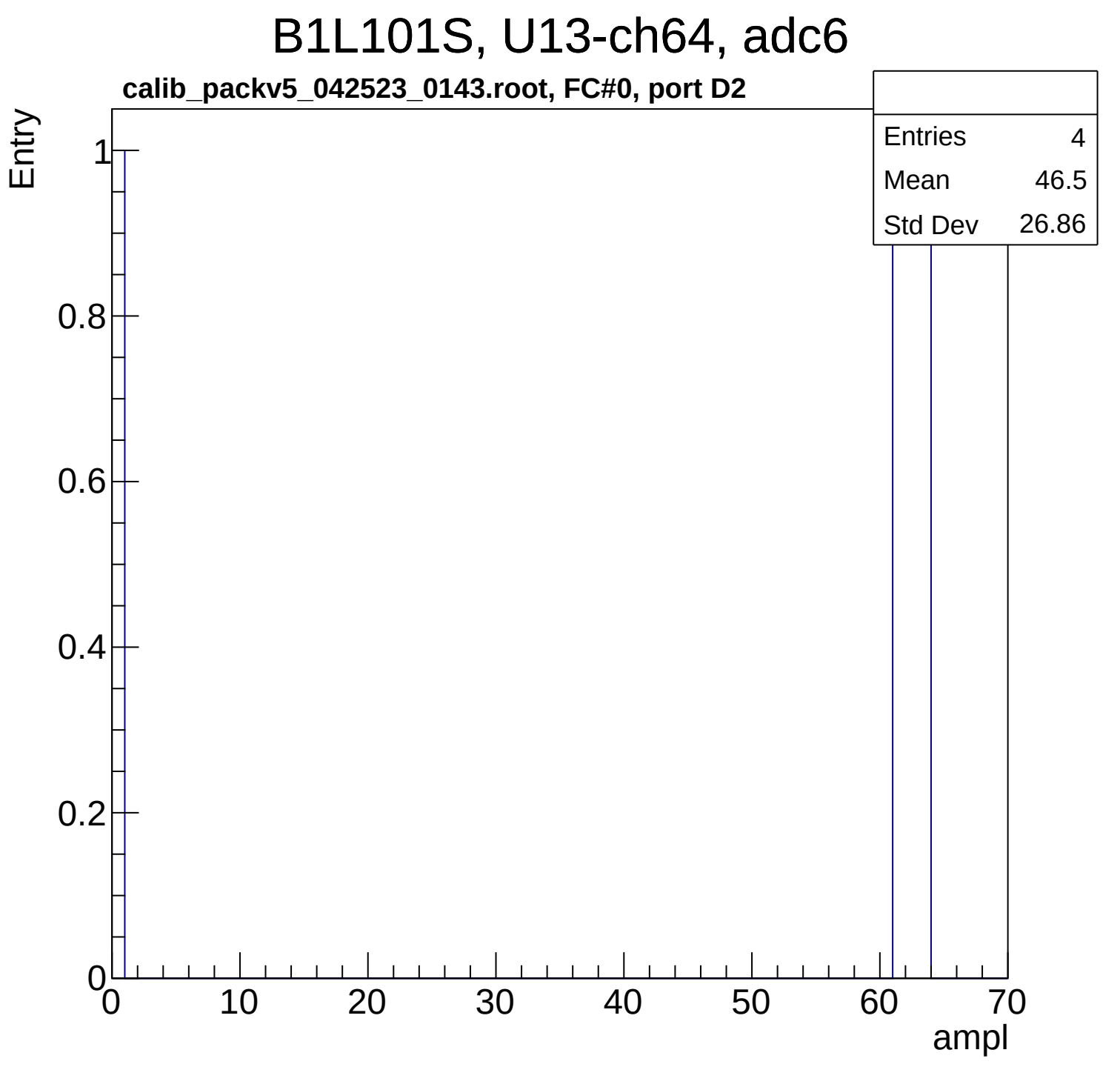
B1L101S, U13-ch64, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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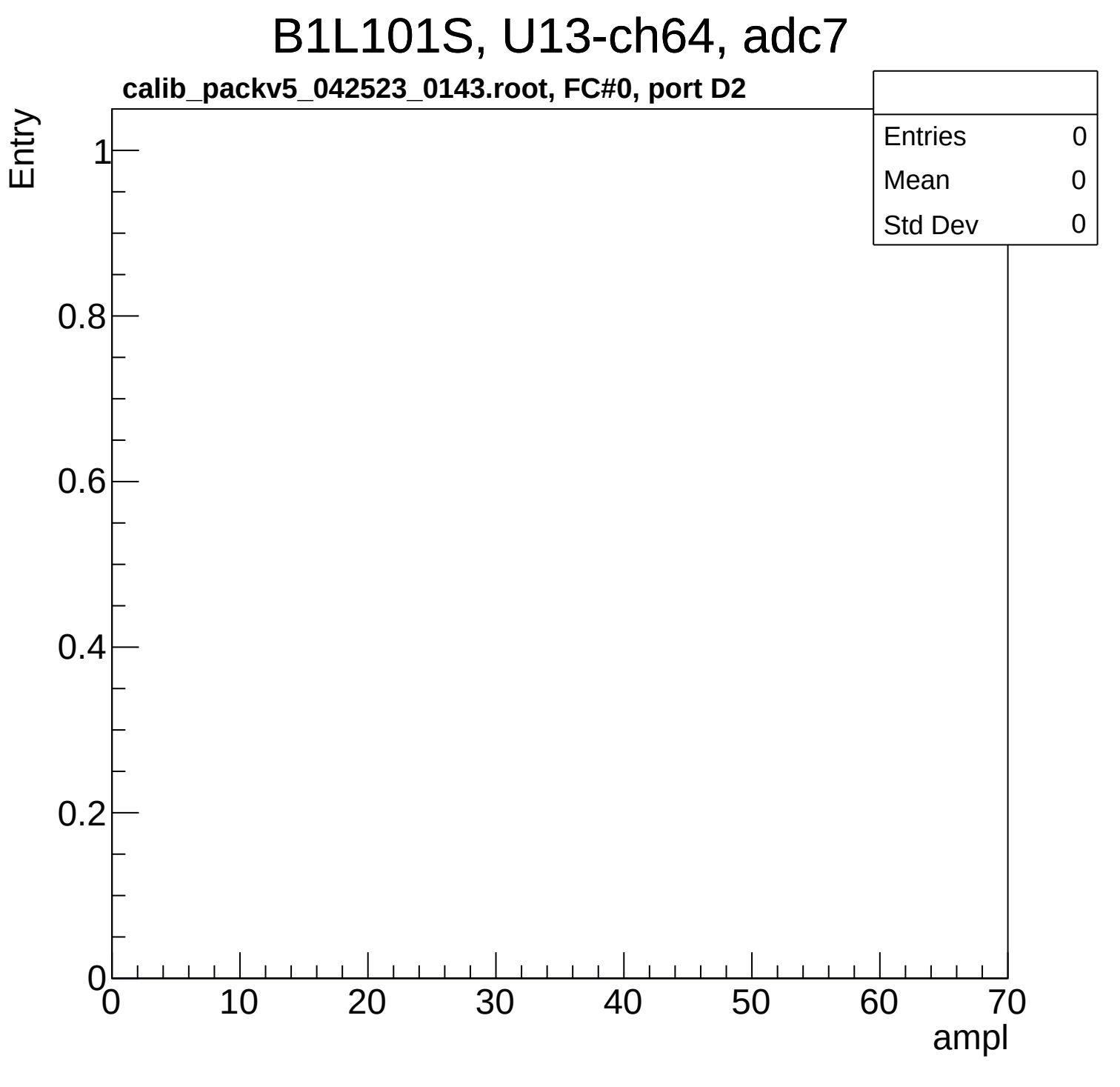
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch64, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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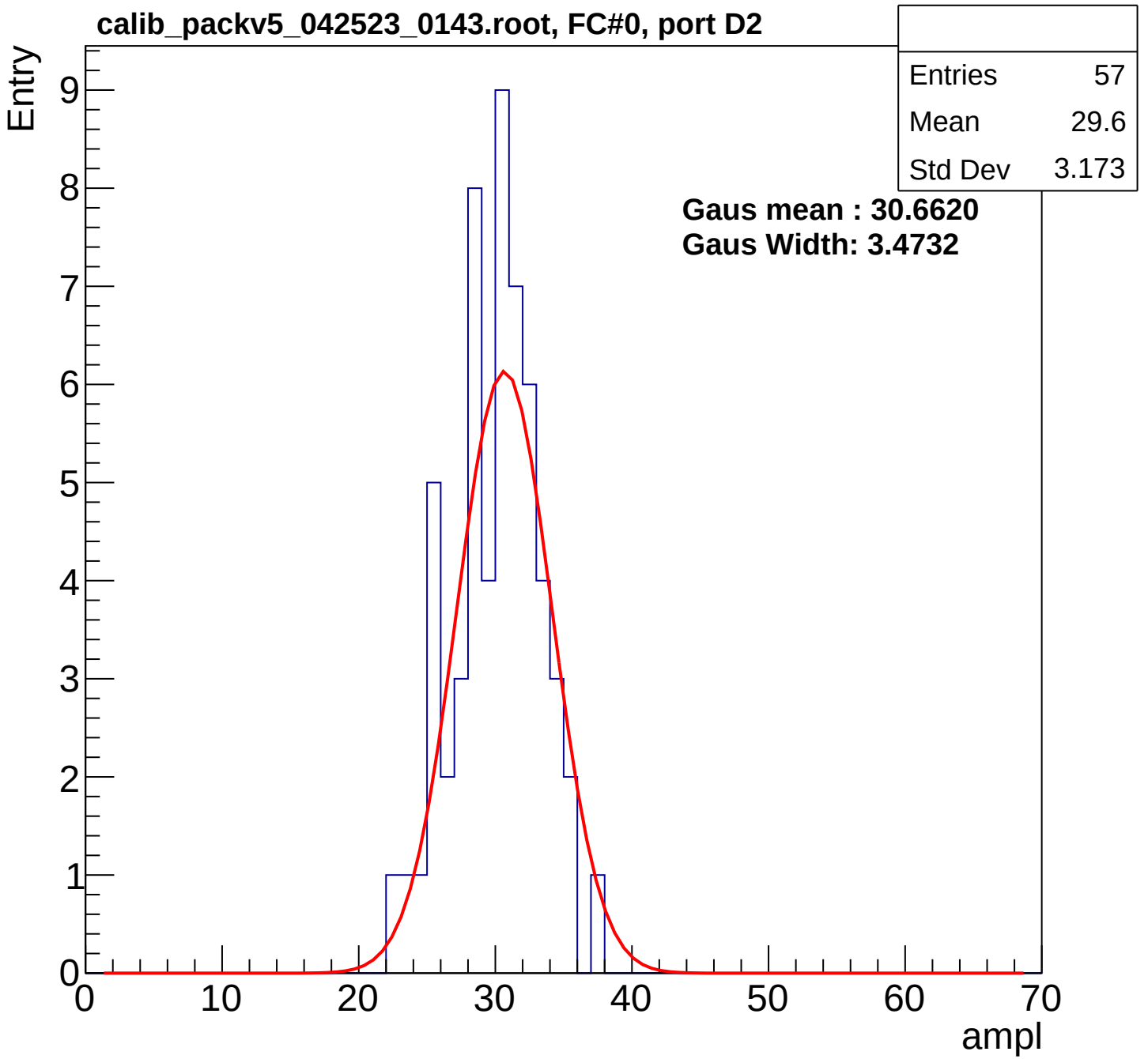
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch65, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 29.6
Std Dev 3.173
Gaus mean : 30.6620
Gaus Width: 3.4732
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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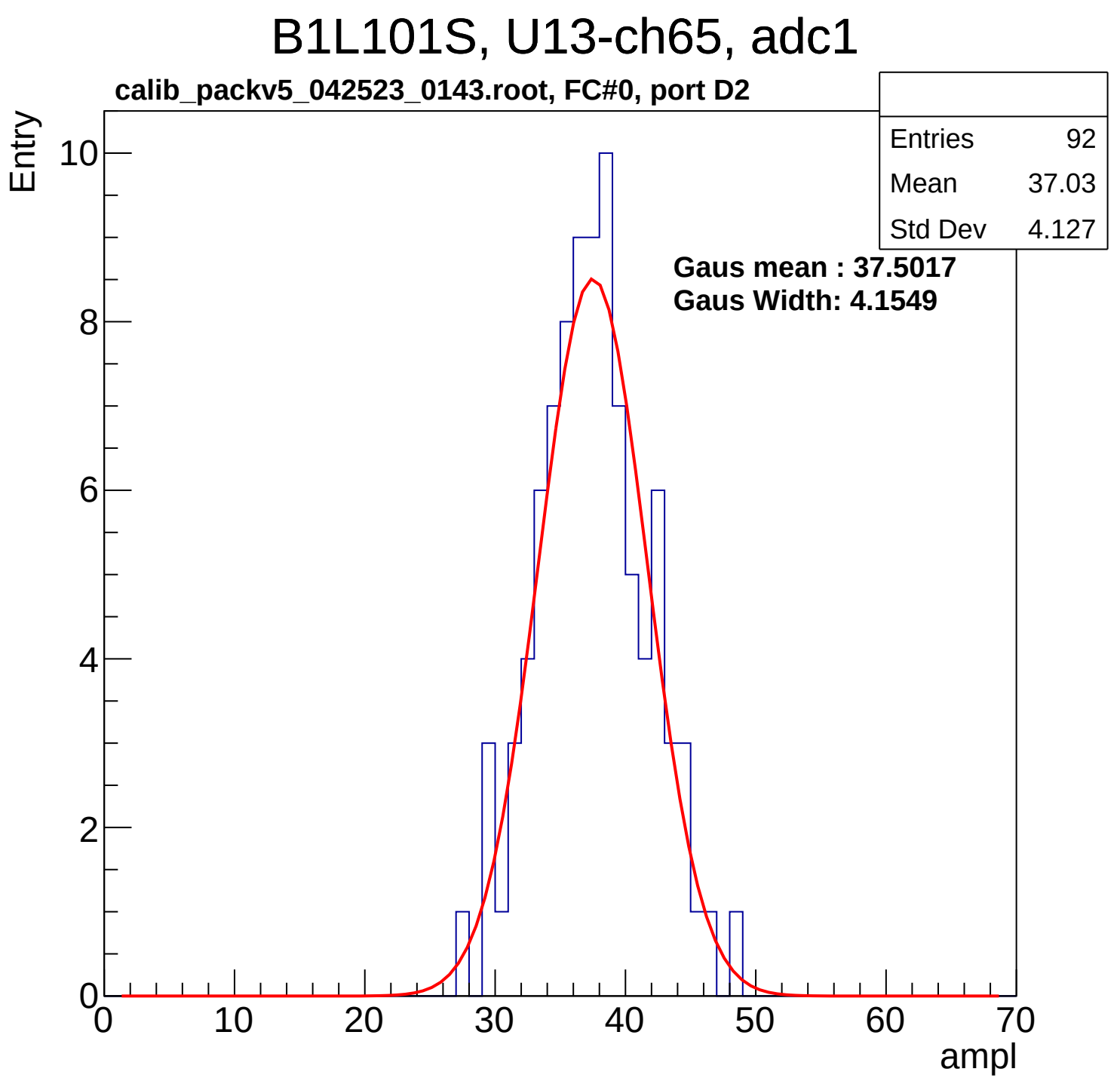
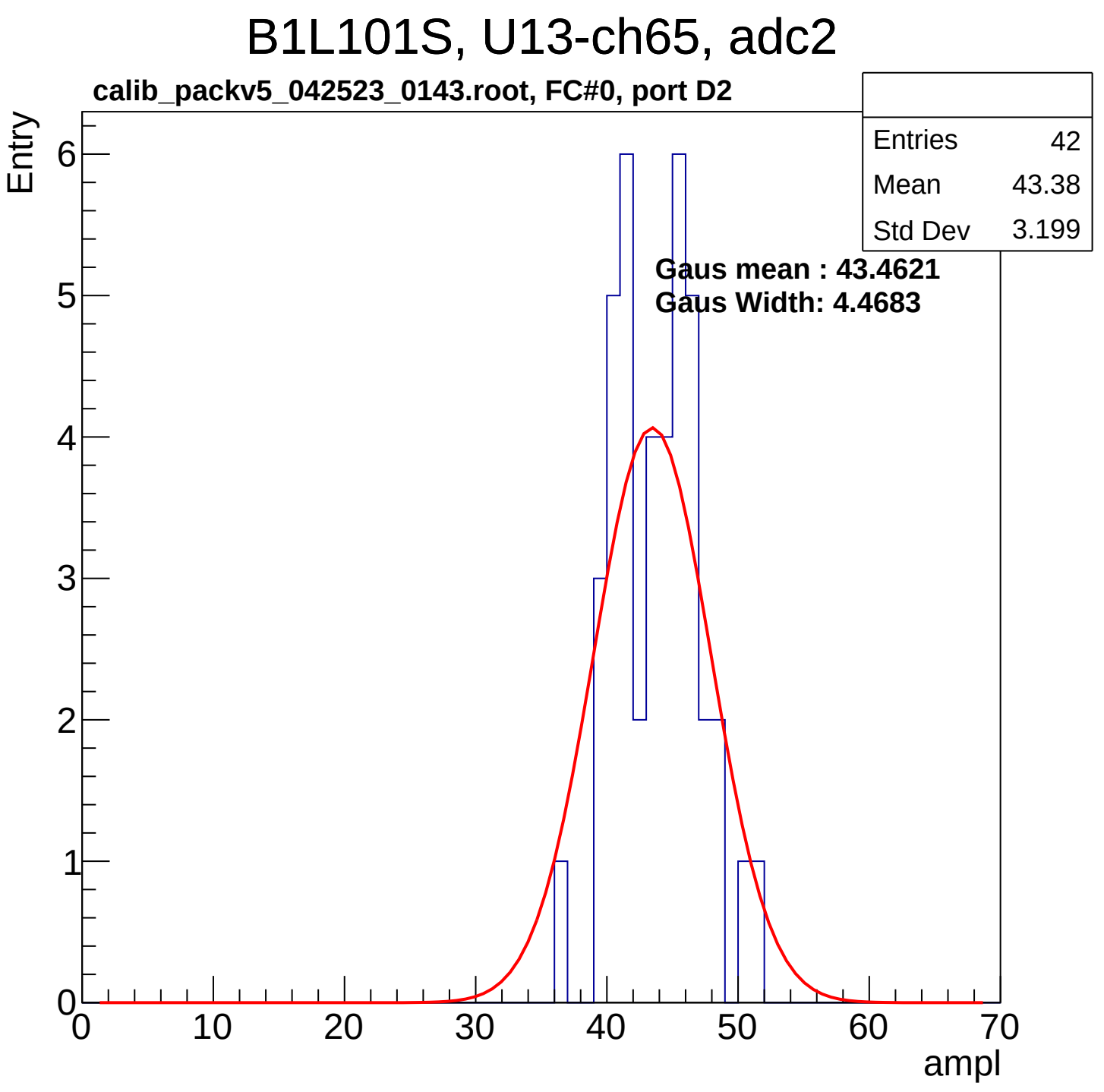
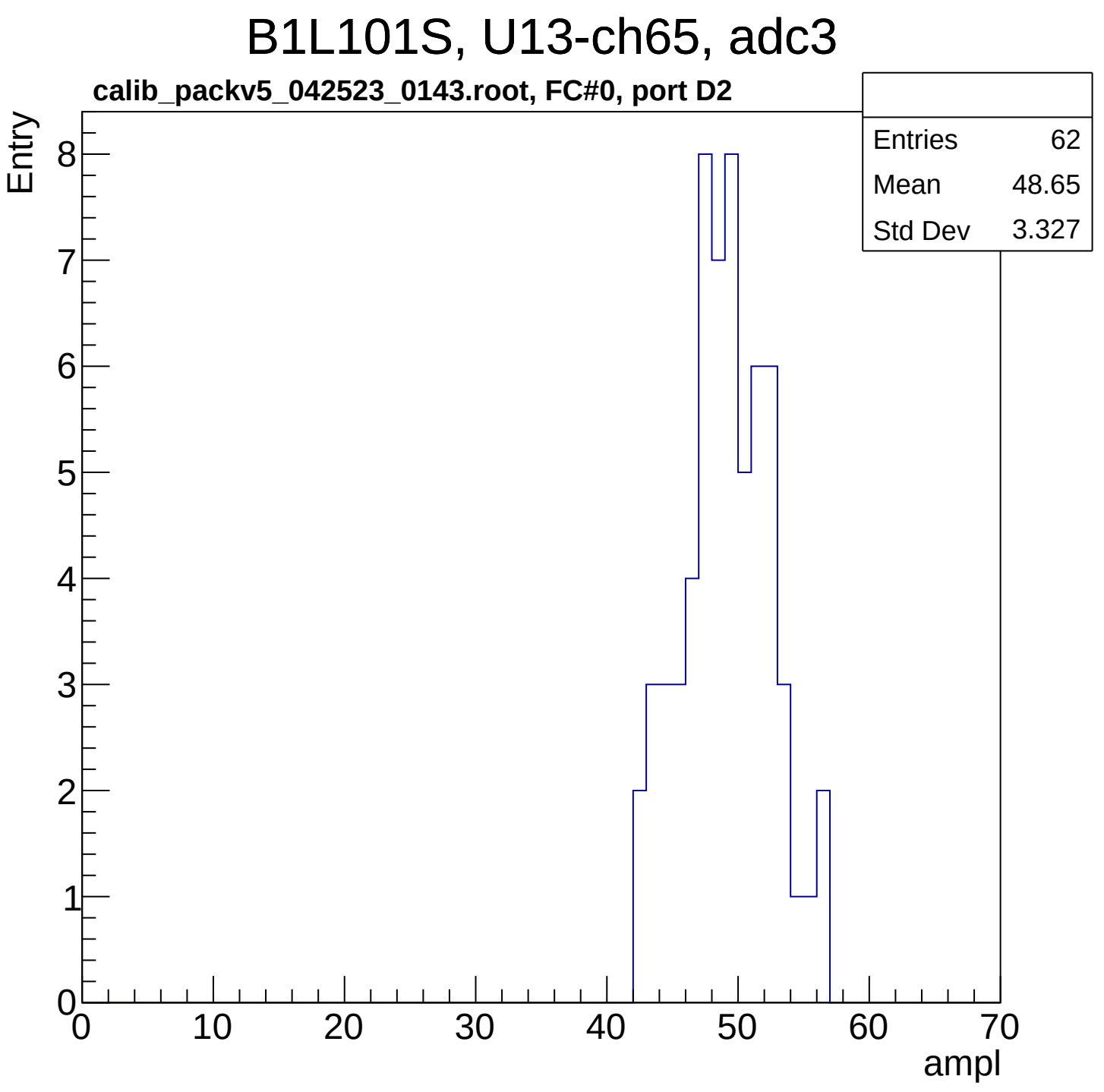

B1L101S, U13-ch65, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 92
Mean 37.03
Std Dev 4.127
Gaus mean : 37.5017
Gaus Width: 4.1549
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch65, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 43.38
Std Dev 3.199
Gaus mean : 43.4621
Gaus Width: 4.4683
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch65, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 48.65
Std Dev 3.327
Entry
ampl
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2
3
4
5
6
7
8
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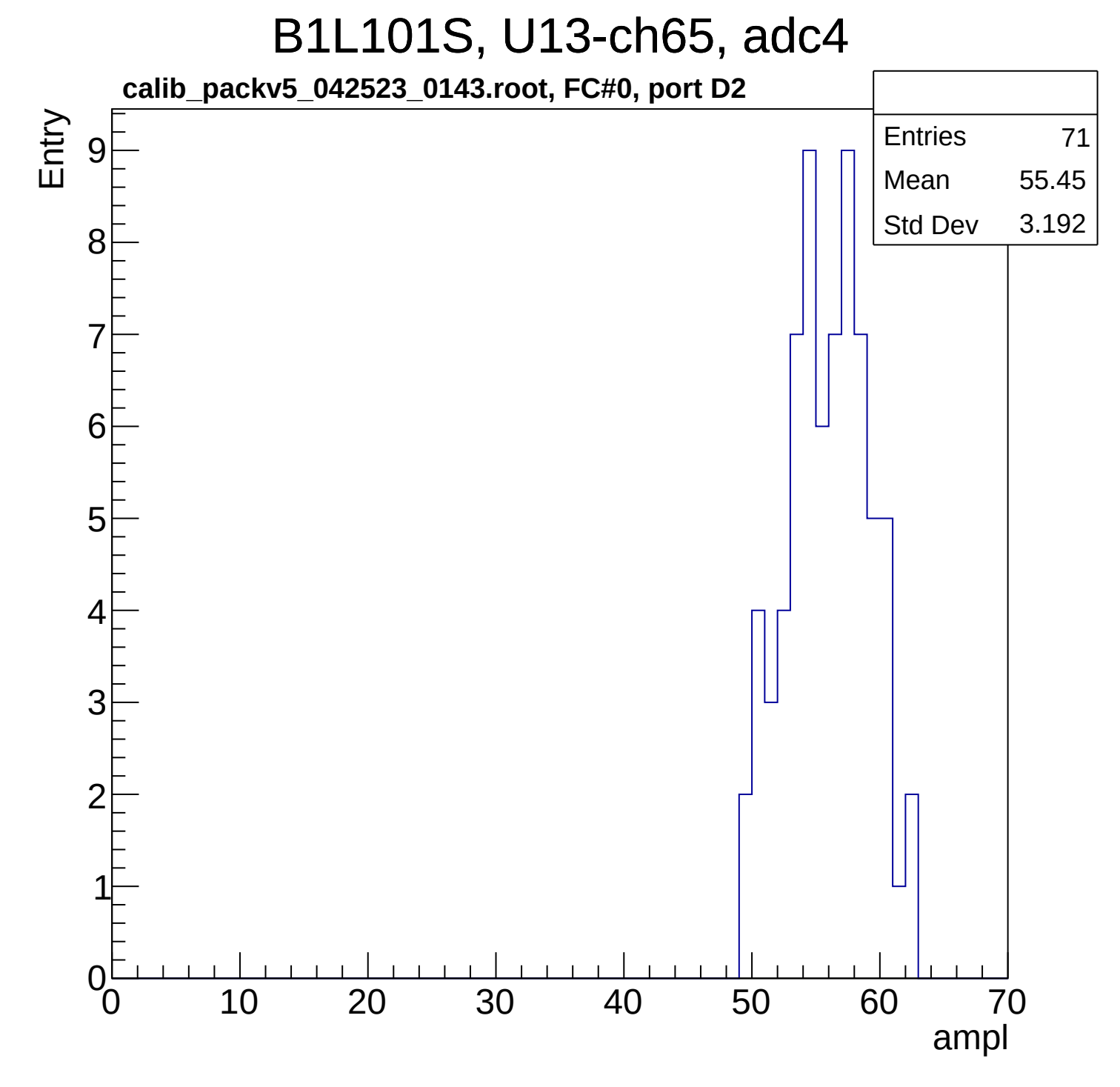
B1L101S, U13-ch65, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 55.45
Std Dev 3.192
Entry
ampl
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2
3
4
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7
8
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0
10
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40
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60
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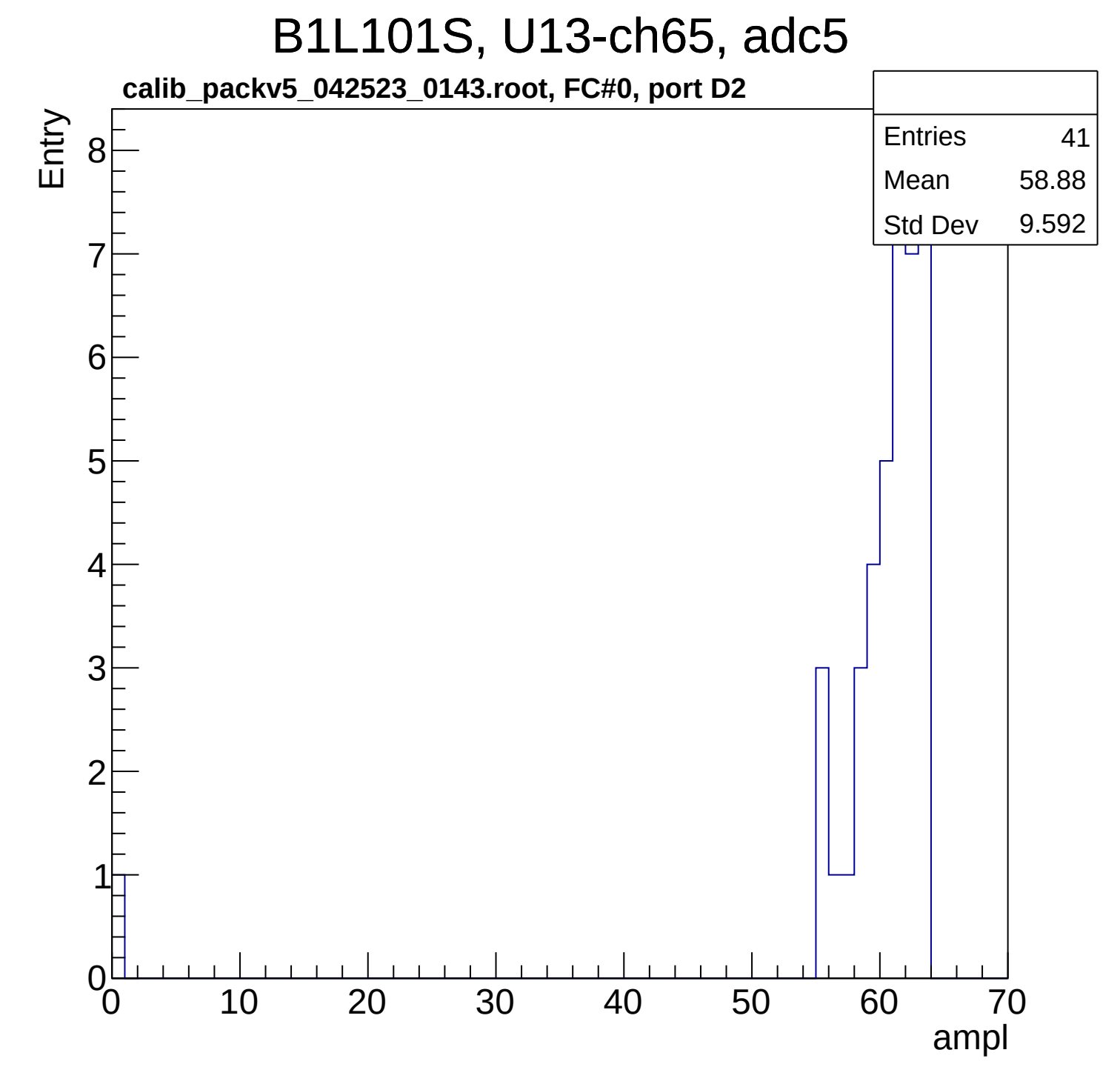
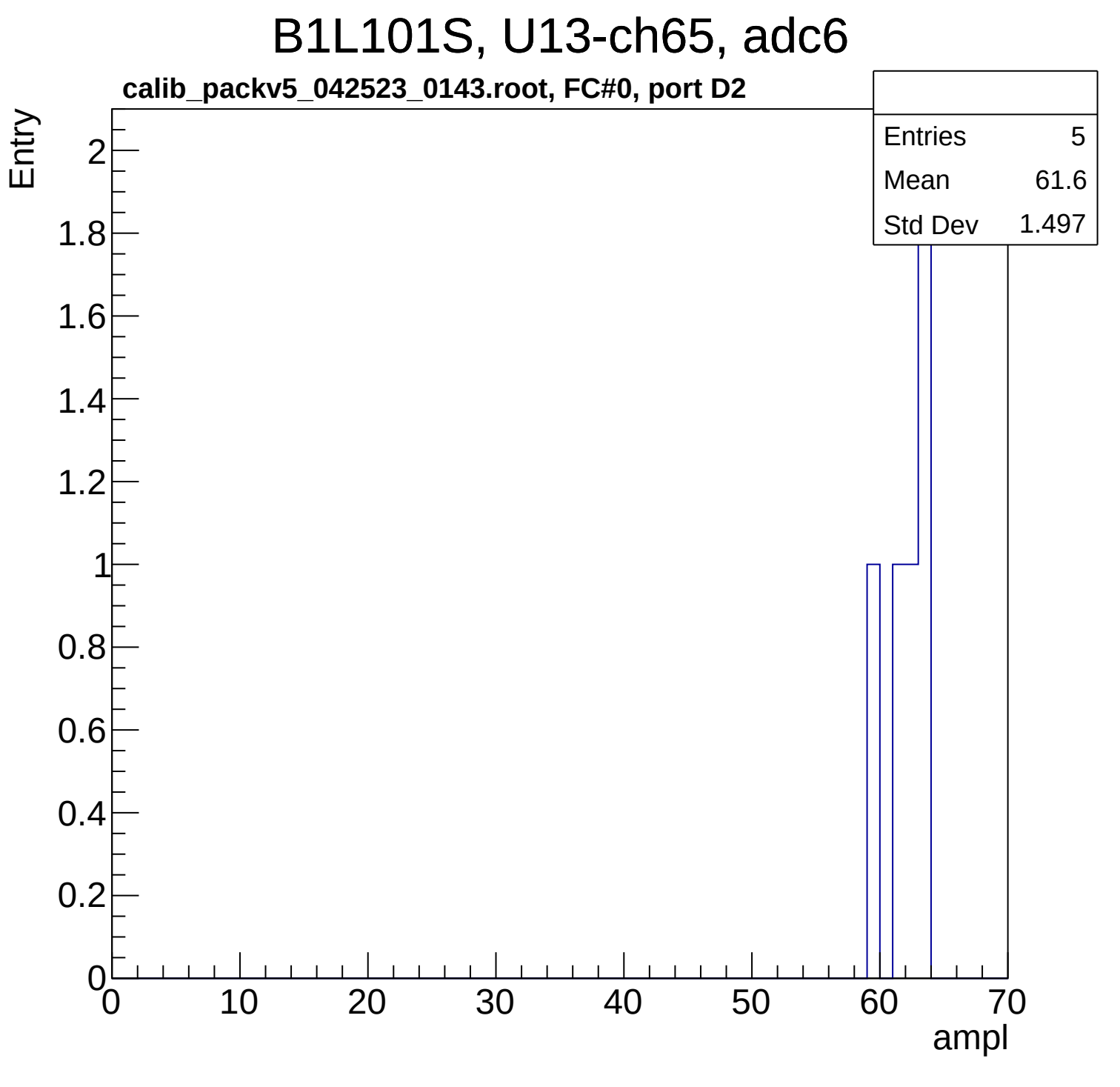

B1L101S, U13-ch65, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 58.88
Std Dev 9.592
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch65, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 61.6
Std Dev 1.497
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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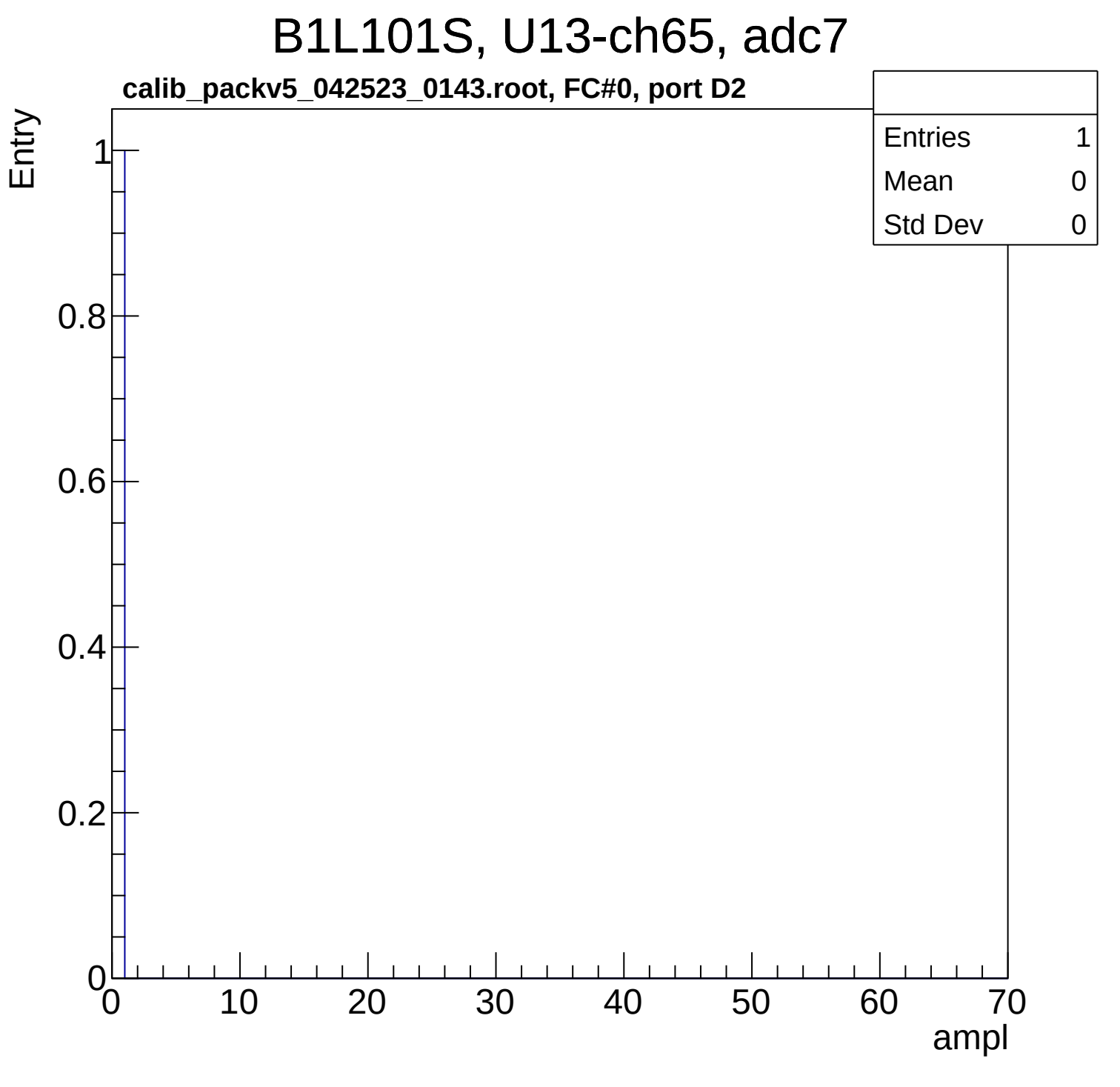
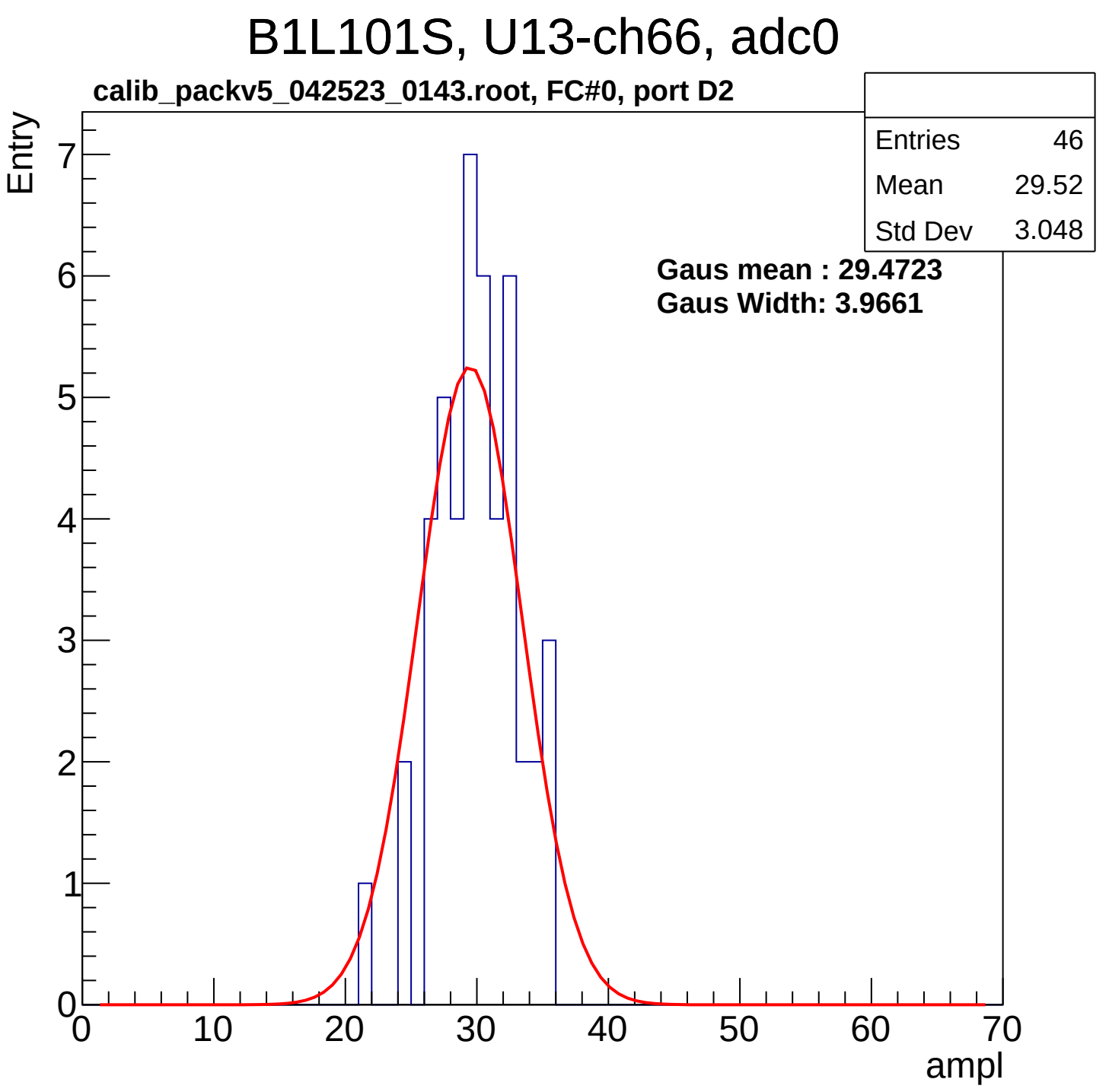
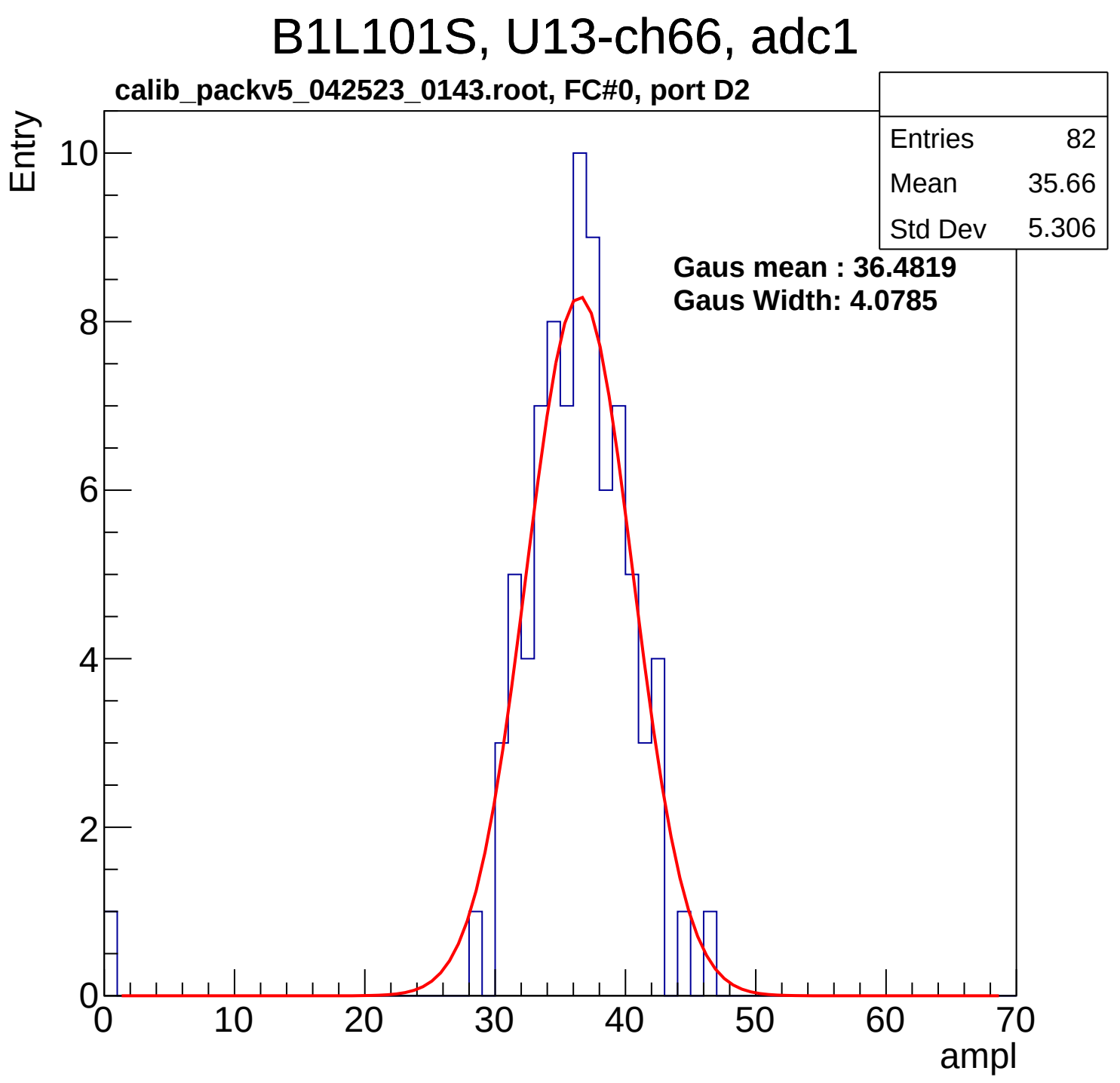
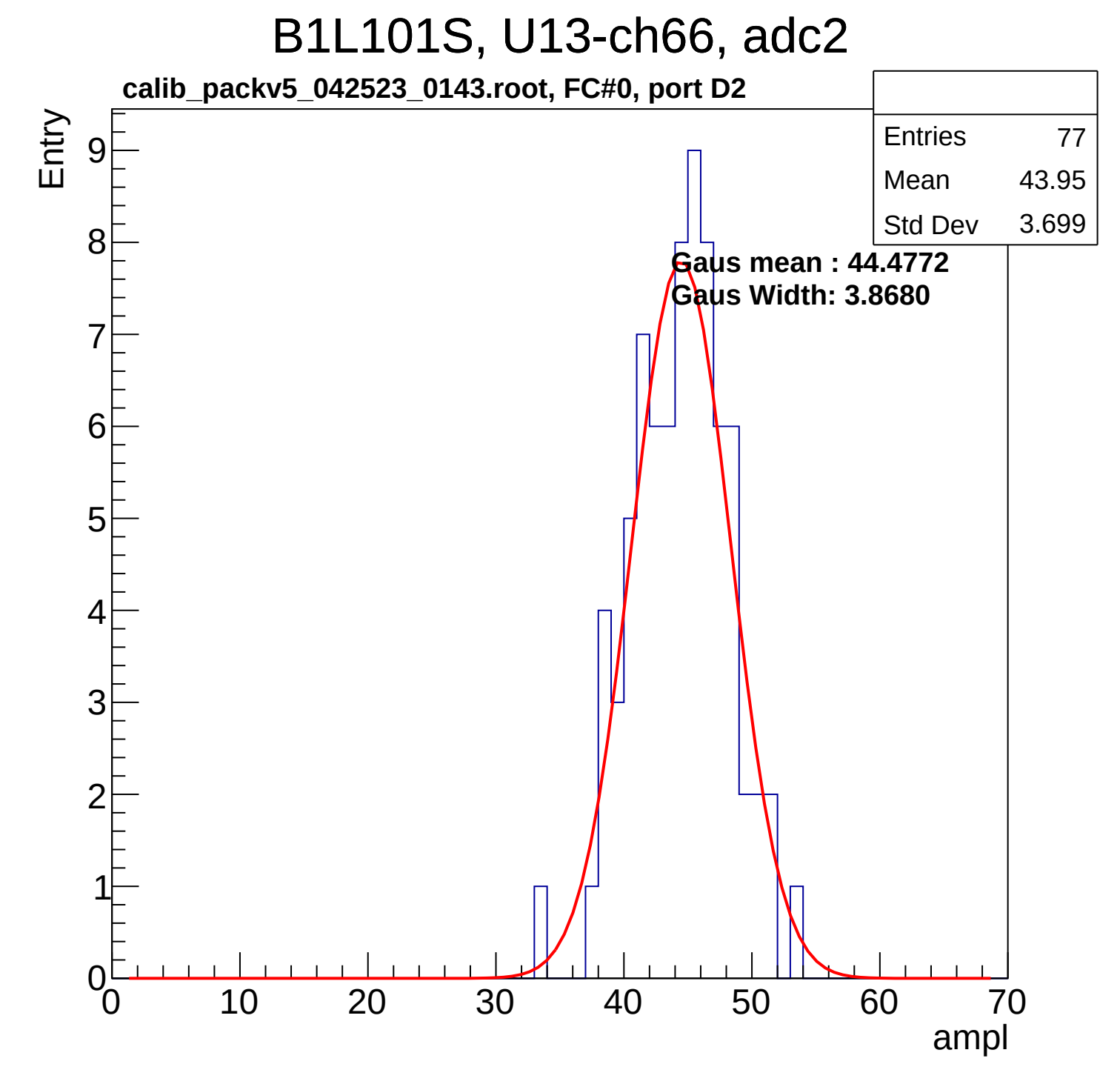

B1L101S, U13-ch65, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch66, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 29.52
Std Dev 3.048
Gaus mean : 29.4723
Gaus Width: 3.9661
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch66, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 82
Mean 35.66
Std Dev 5.306
Gaus mean : 36.4819
Gaus Width: 4.0785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch66, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 43.95
Std Dev 3.699
Gaus mean : 44.4772
Gaus Width: 3.8680
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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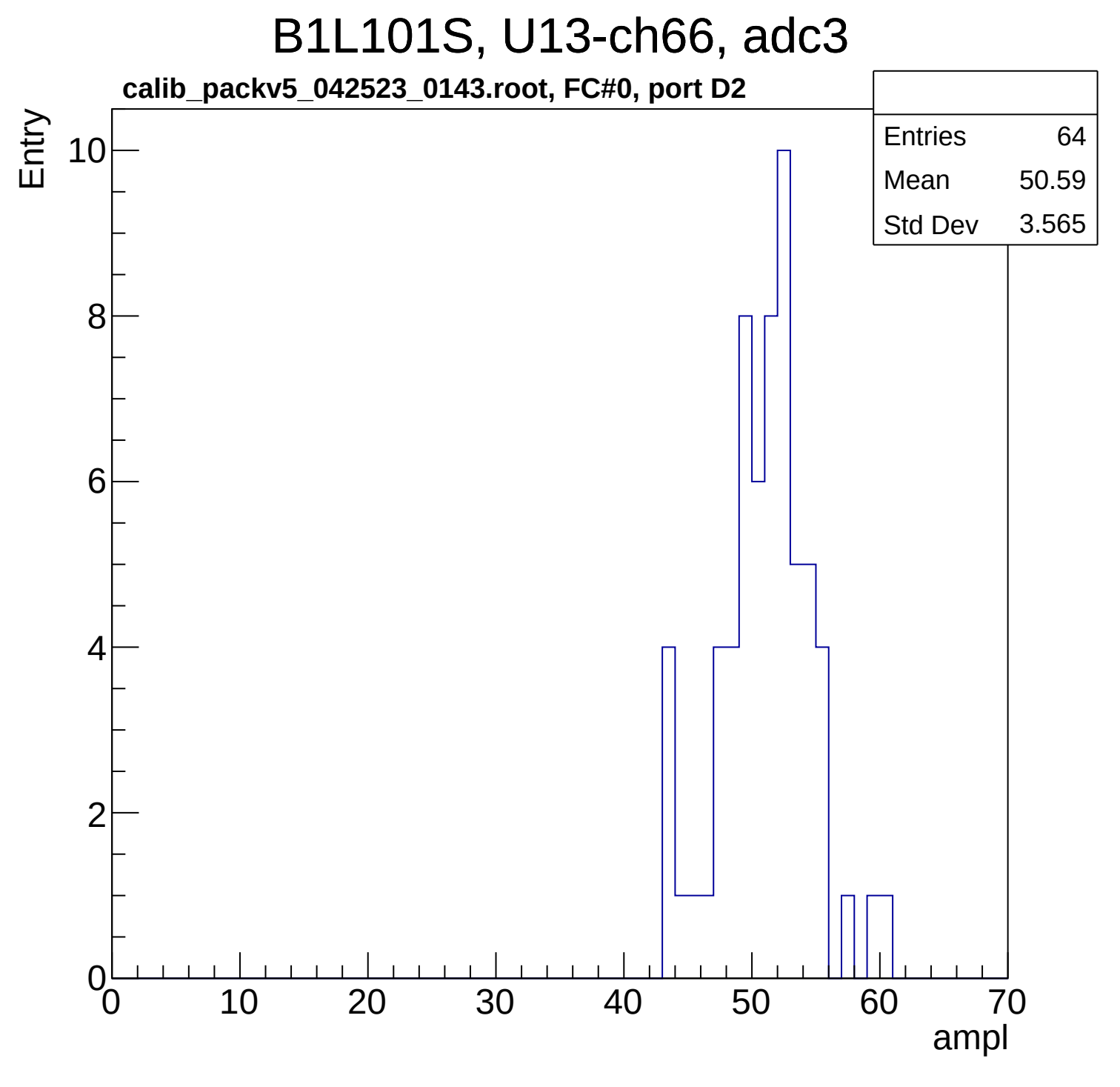
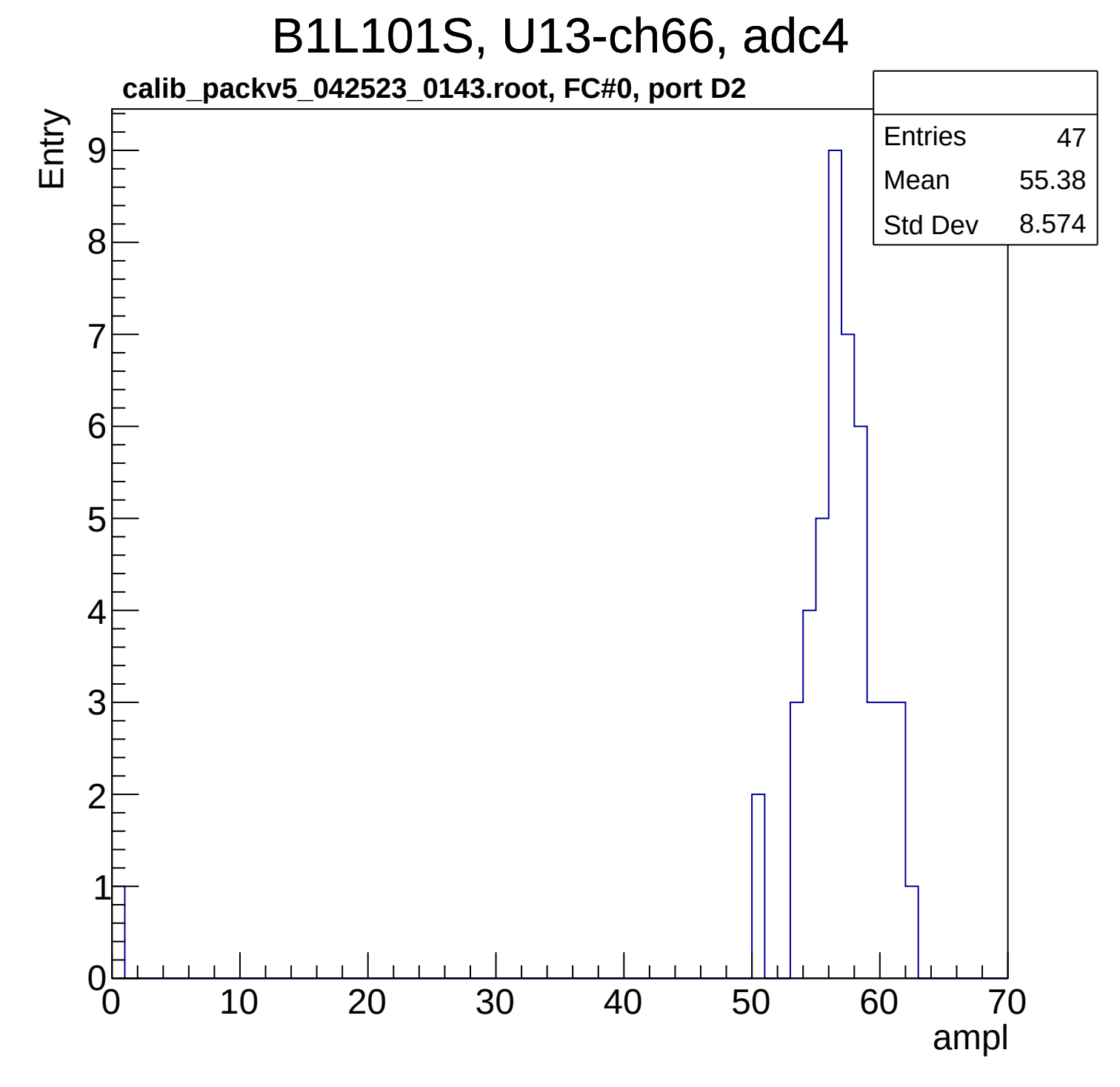

B1L101S, U13-ch66, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 50.59
Std Dev 3.565
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch66, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 47
Mean 55.38
Std Dev 8.574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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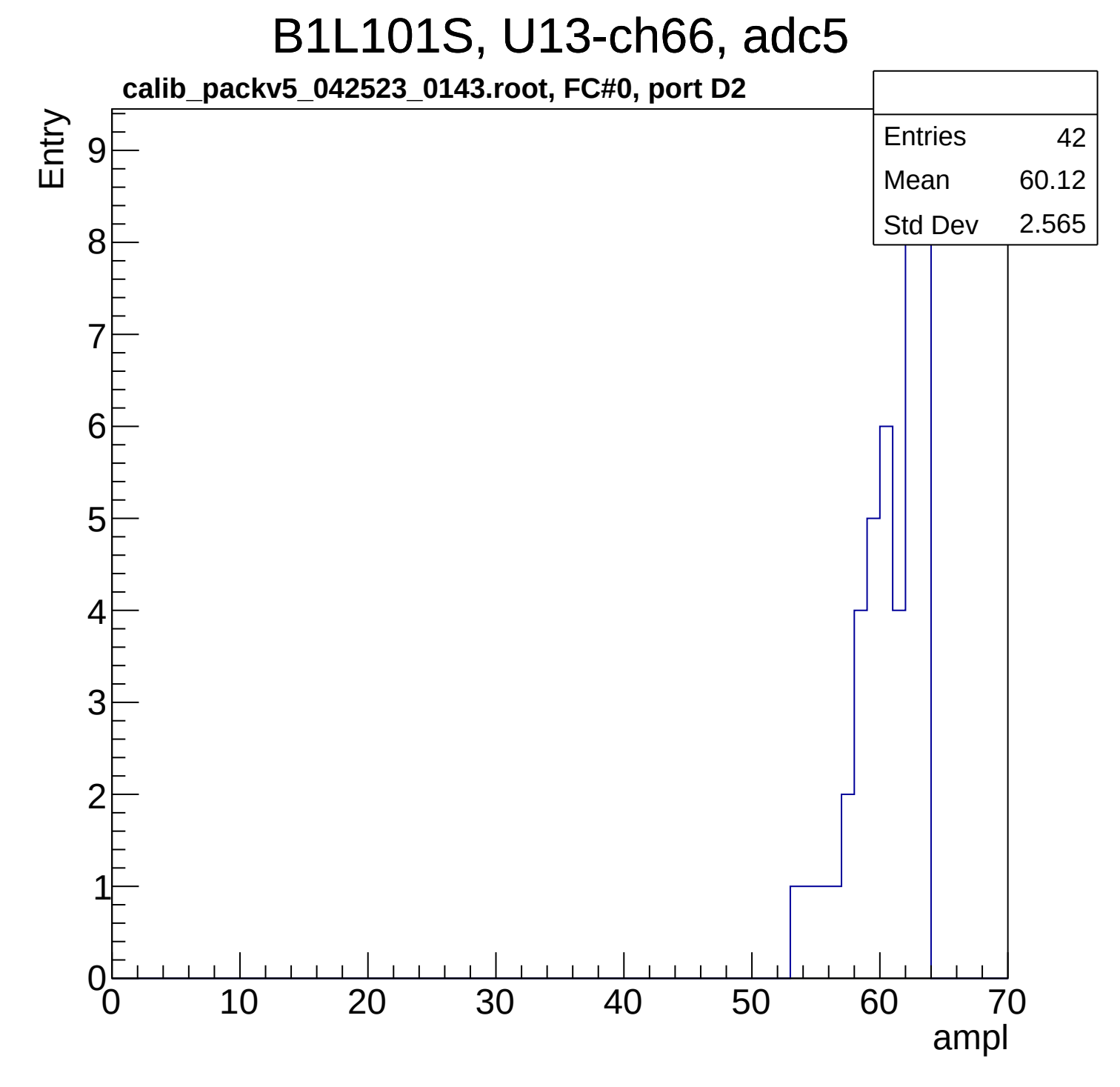
B1L101S, U13-ch66, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 60.12
Std Dev 2.565
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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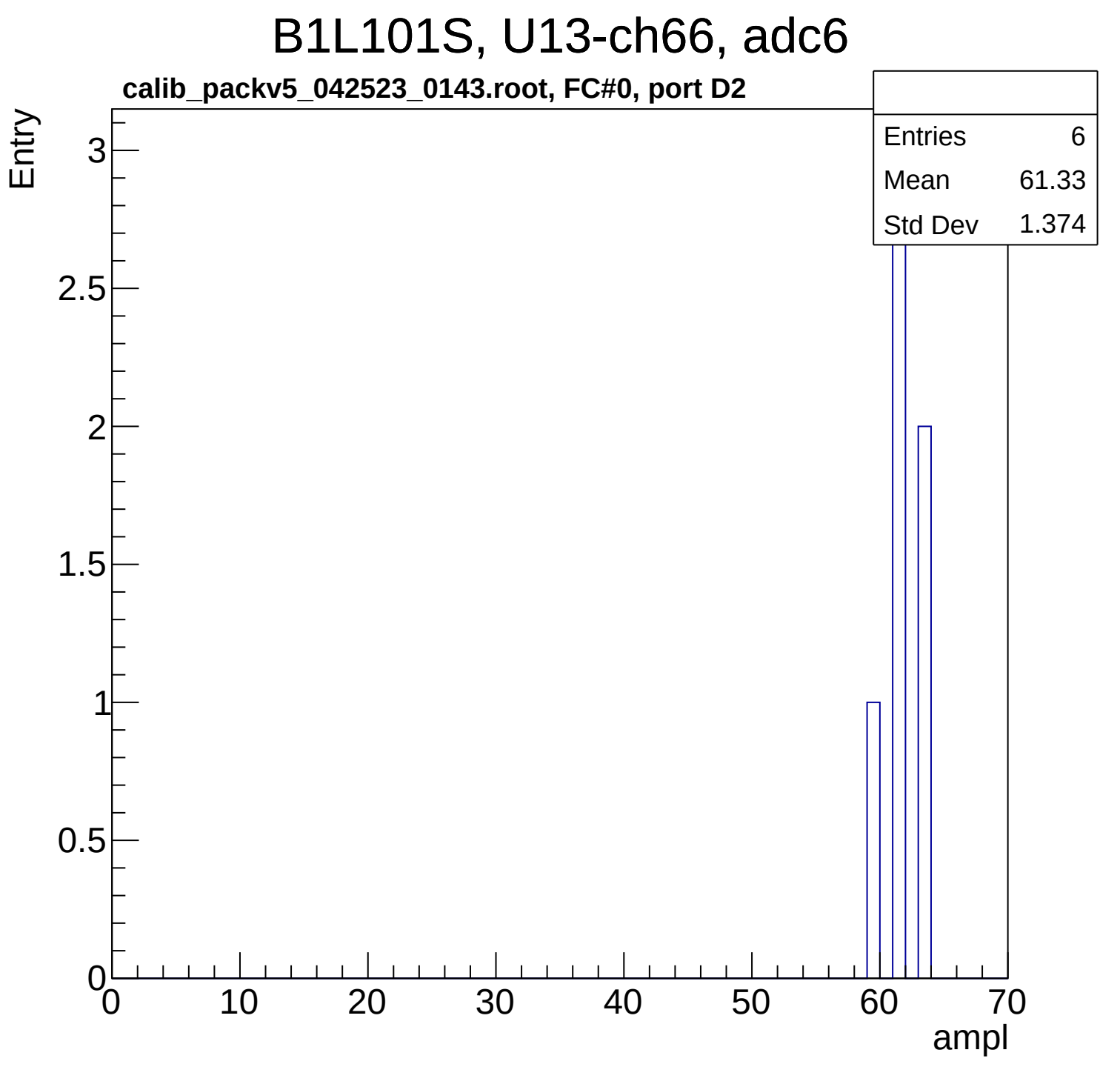
B1L101S, U13-ch66, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 6
Mean 61.33
Std Dev 1.374
Entry
ampl
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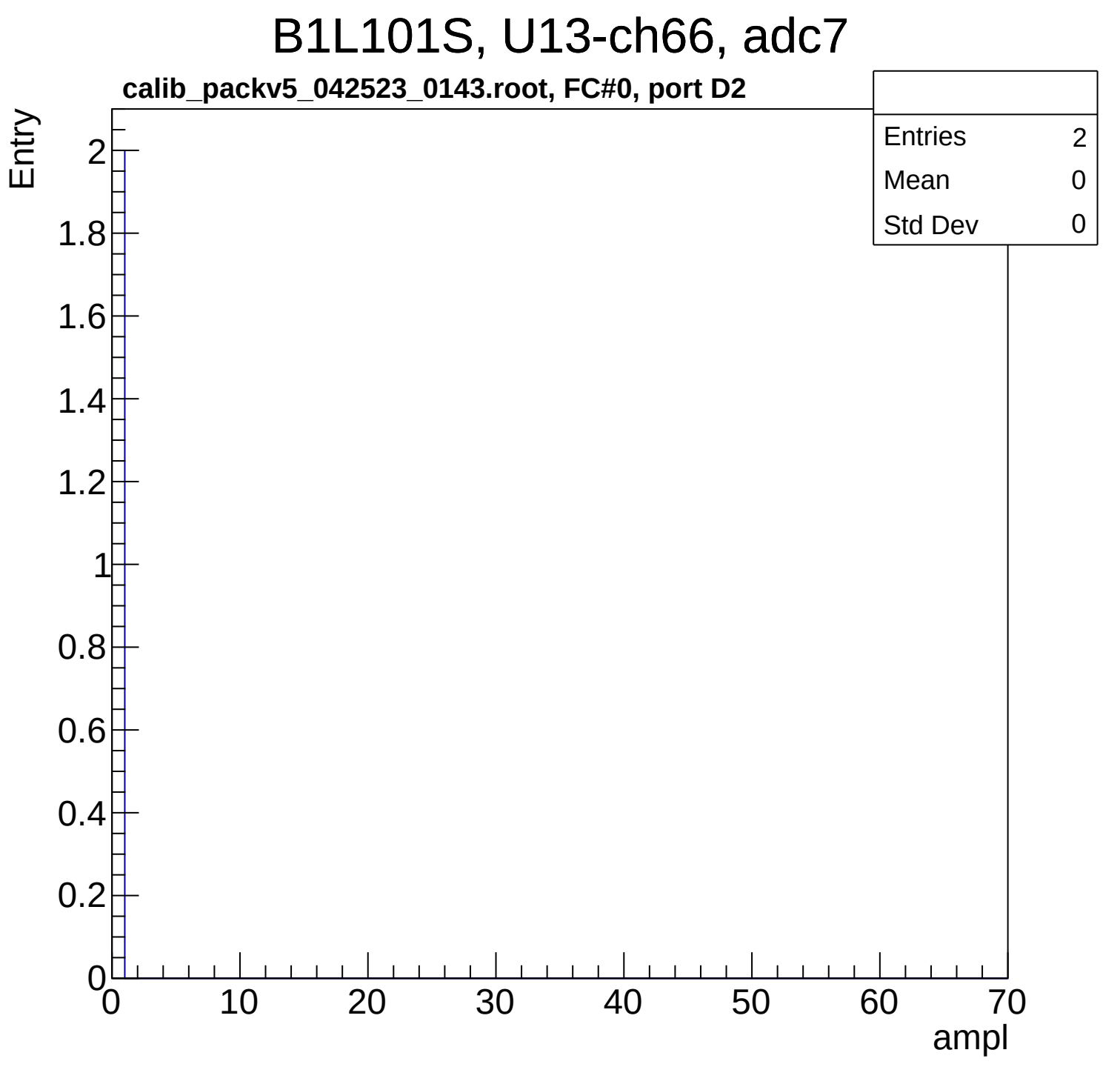
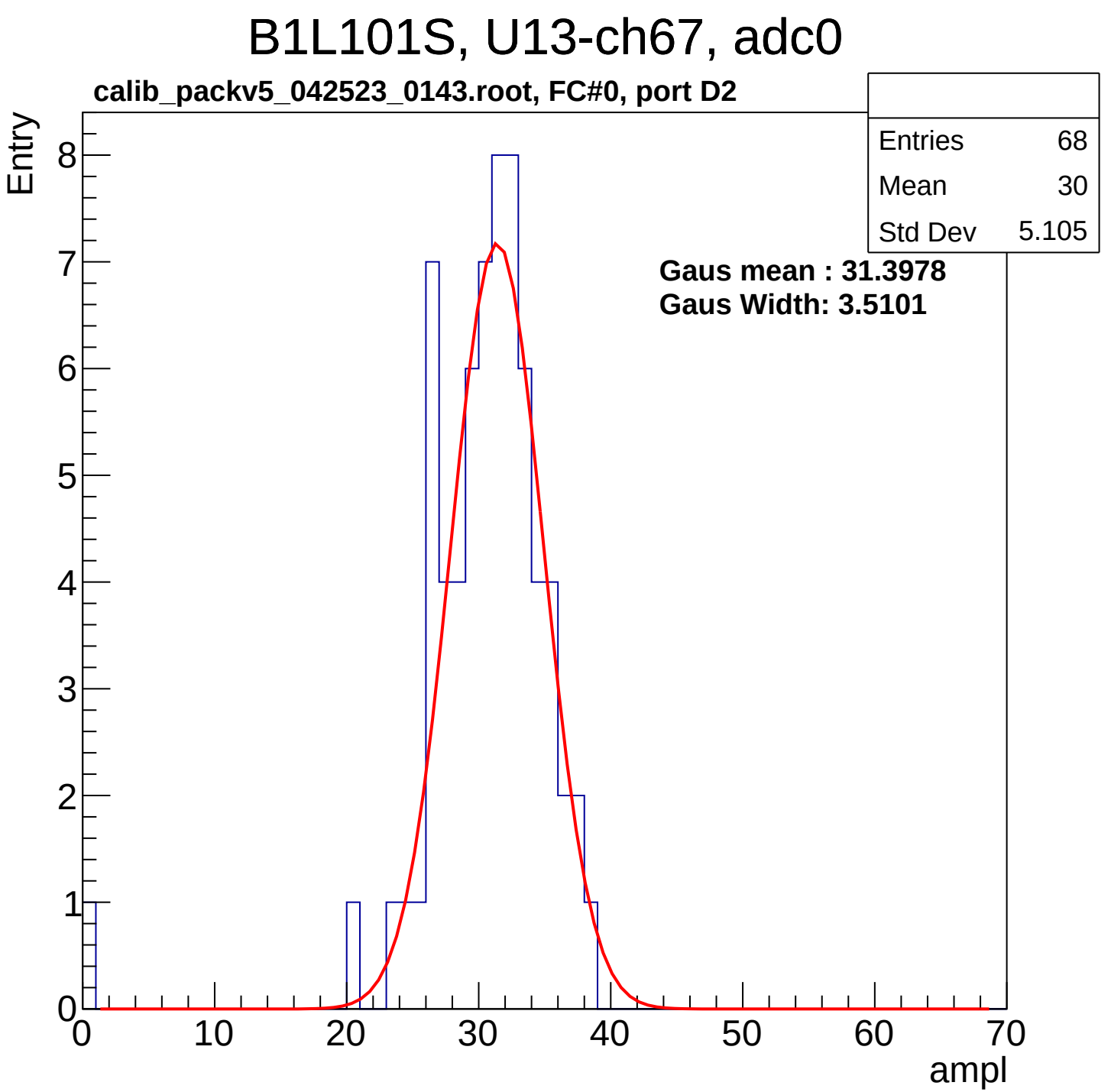
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch66, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch67, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 30
Std Dev 5.105
Gaus mean : 31.3978
Gaus Width: 3.5101
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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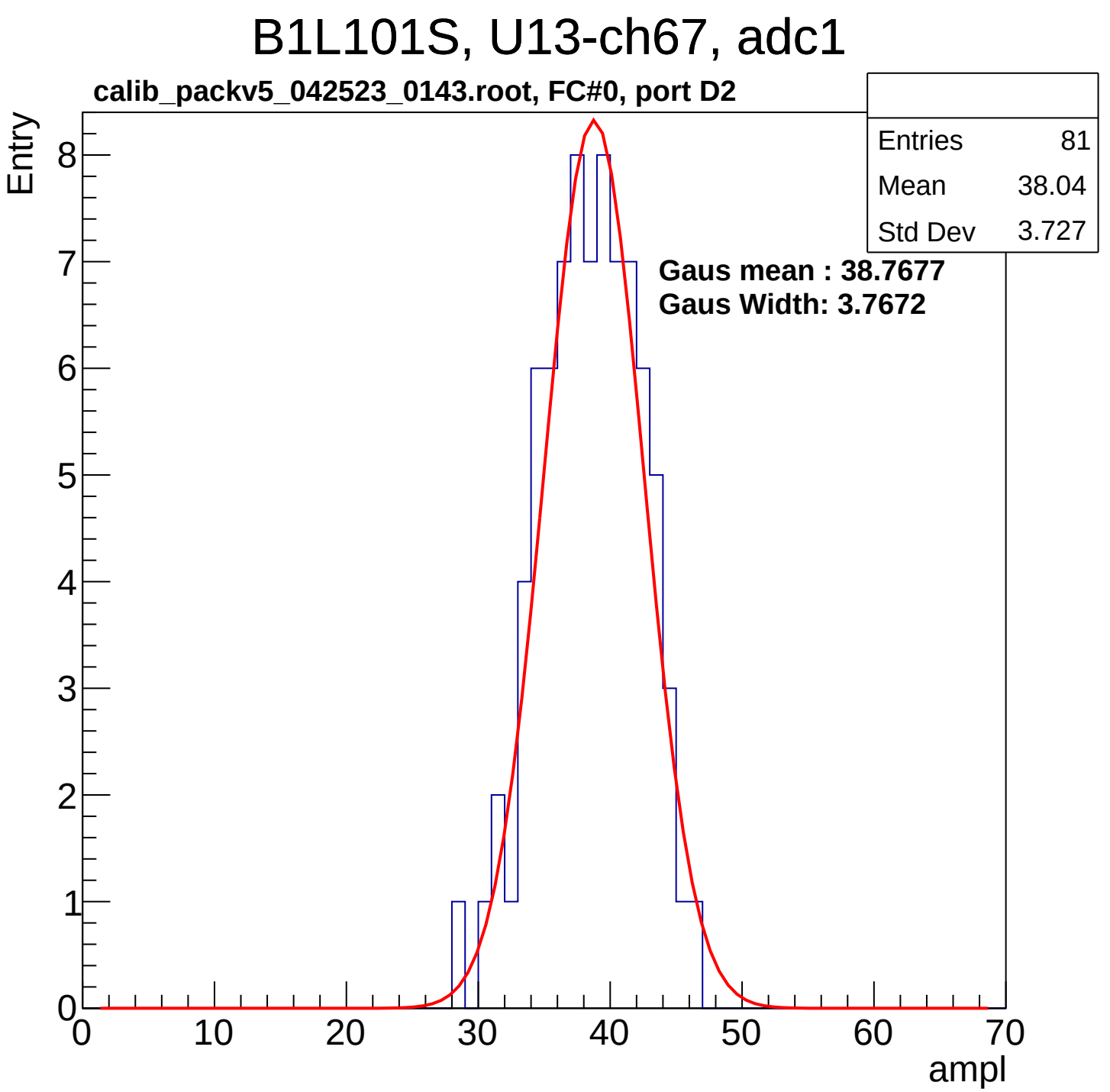
B1L101S, U13-ch67, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 81
Mean 38.04
Std Dev 3.727
Gaus mean : 38.7677
Gaus Width: 3.7672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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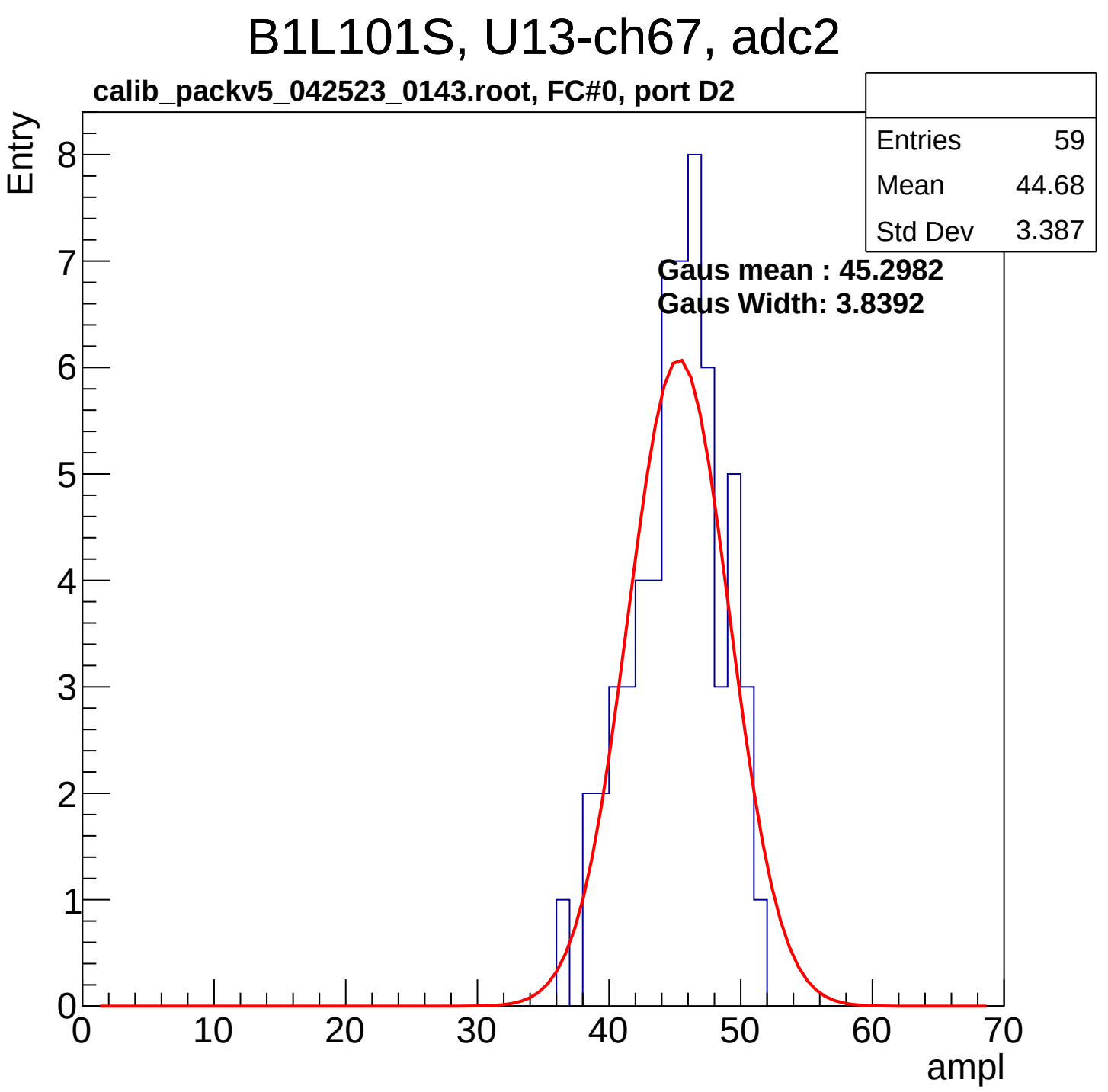
B1L101S, U13-ch67, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 44.68
Std Dev 3.387
Gaus mean : 45.2982
Gaus Width: 3.8392
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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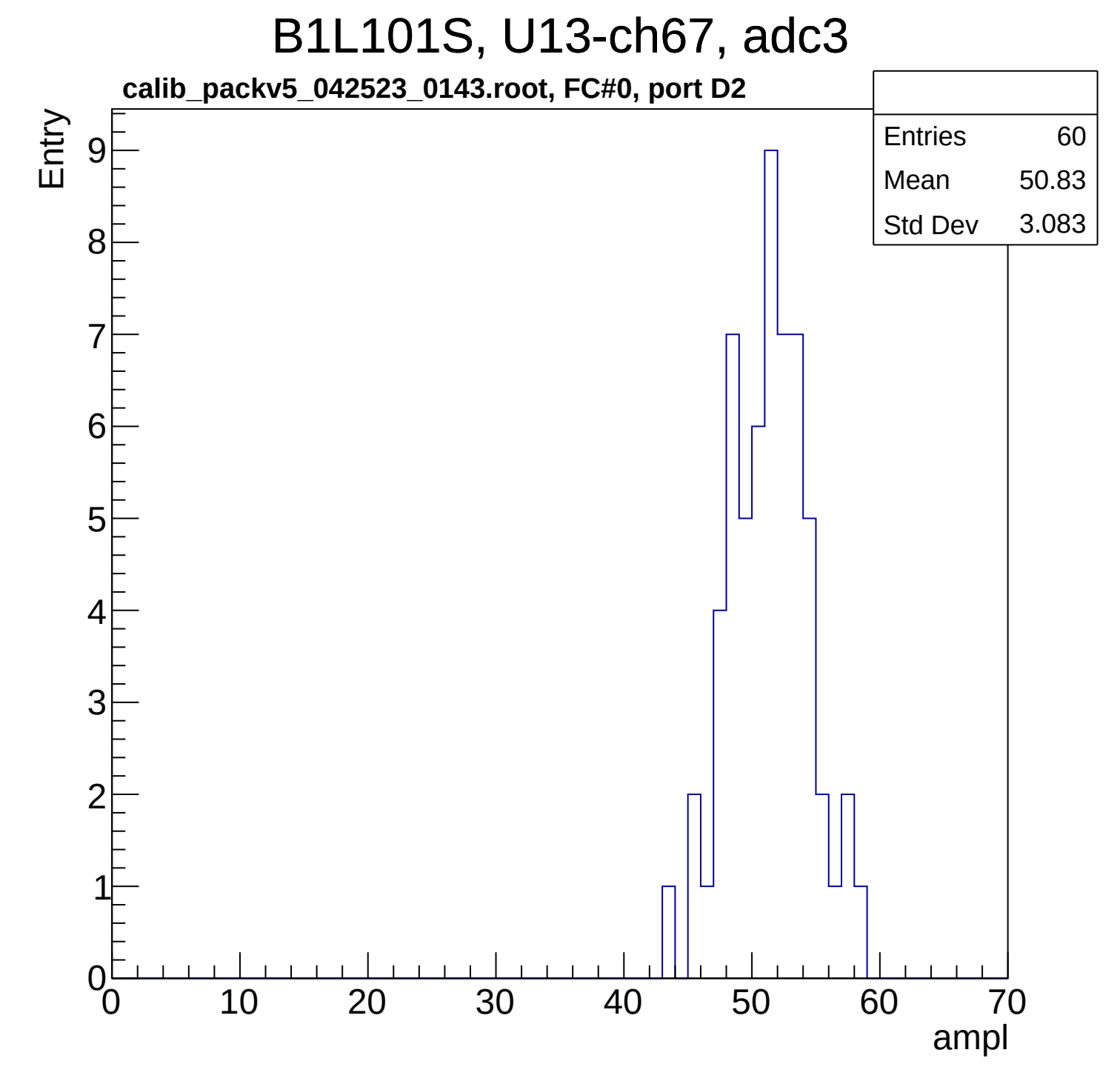
B1L101S, U13-ch67, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 50.83
Std Dev 3.083
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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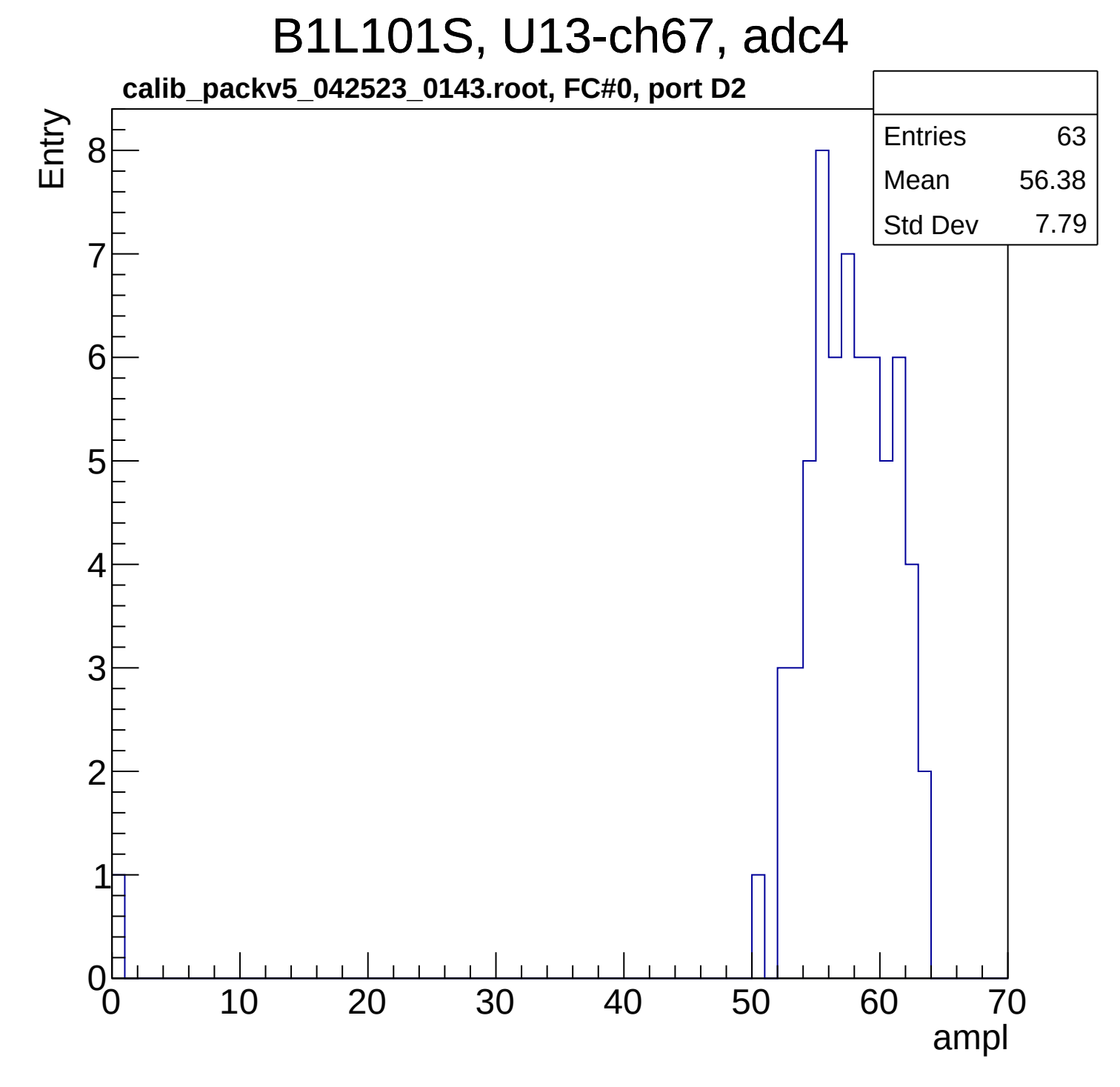
B1L101S, U13-ch67, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 56.38
Std Dev 7.79
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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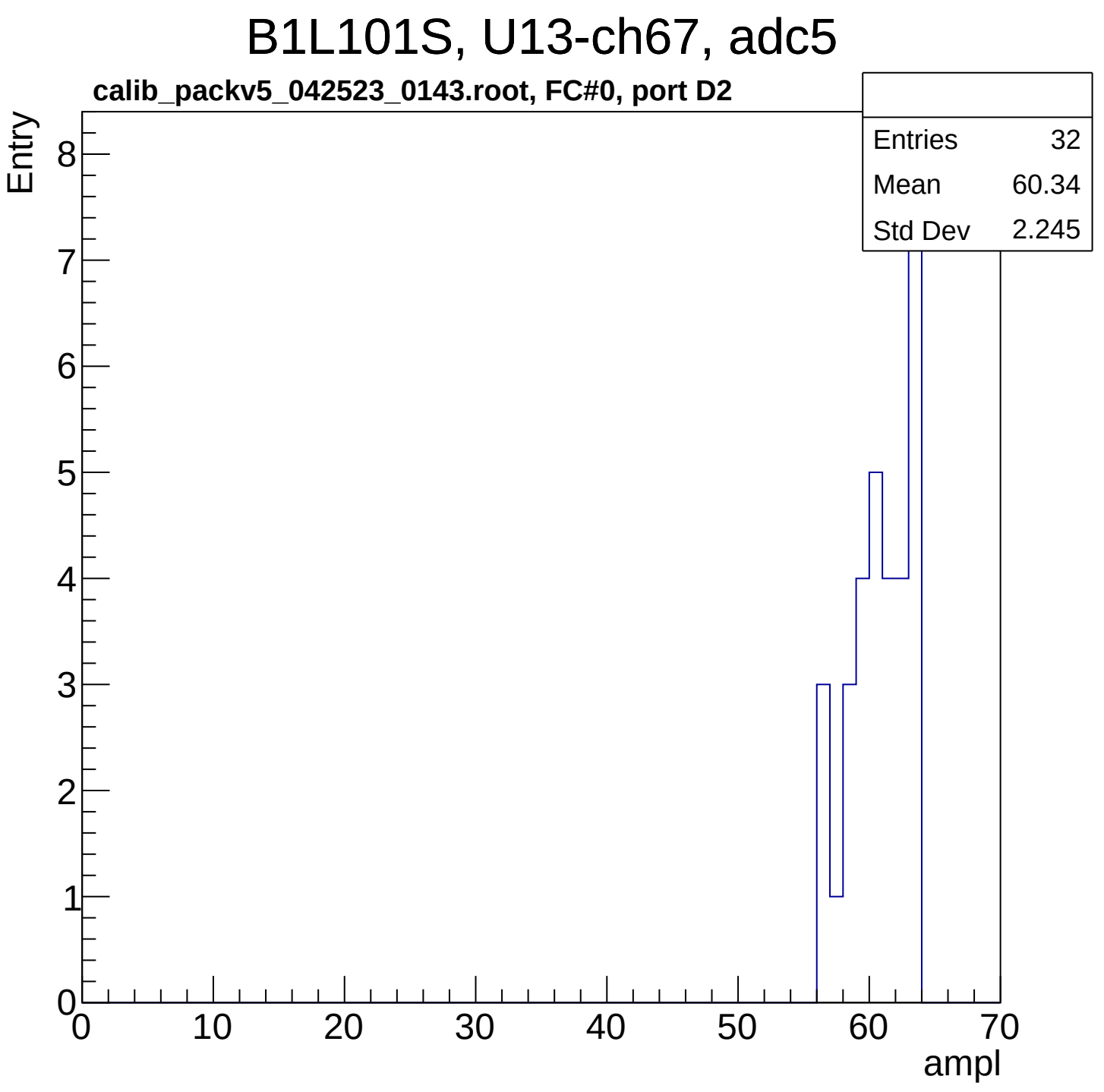
60
70


B1L101S, U13-ch67, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 32
Mean 60.34
Std Dev 2.245
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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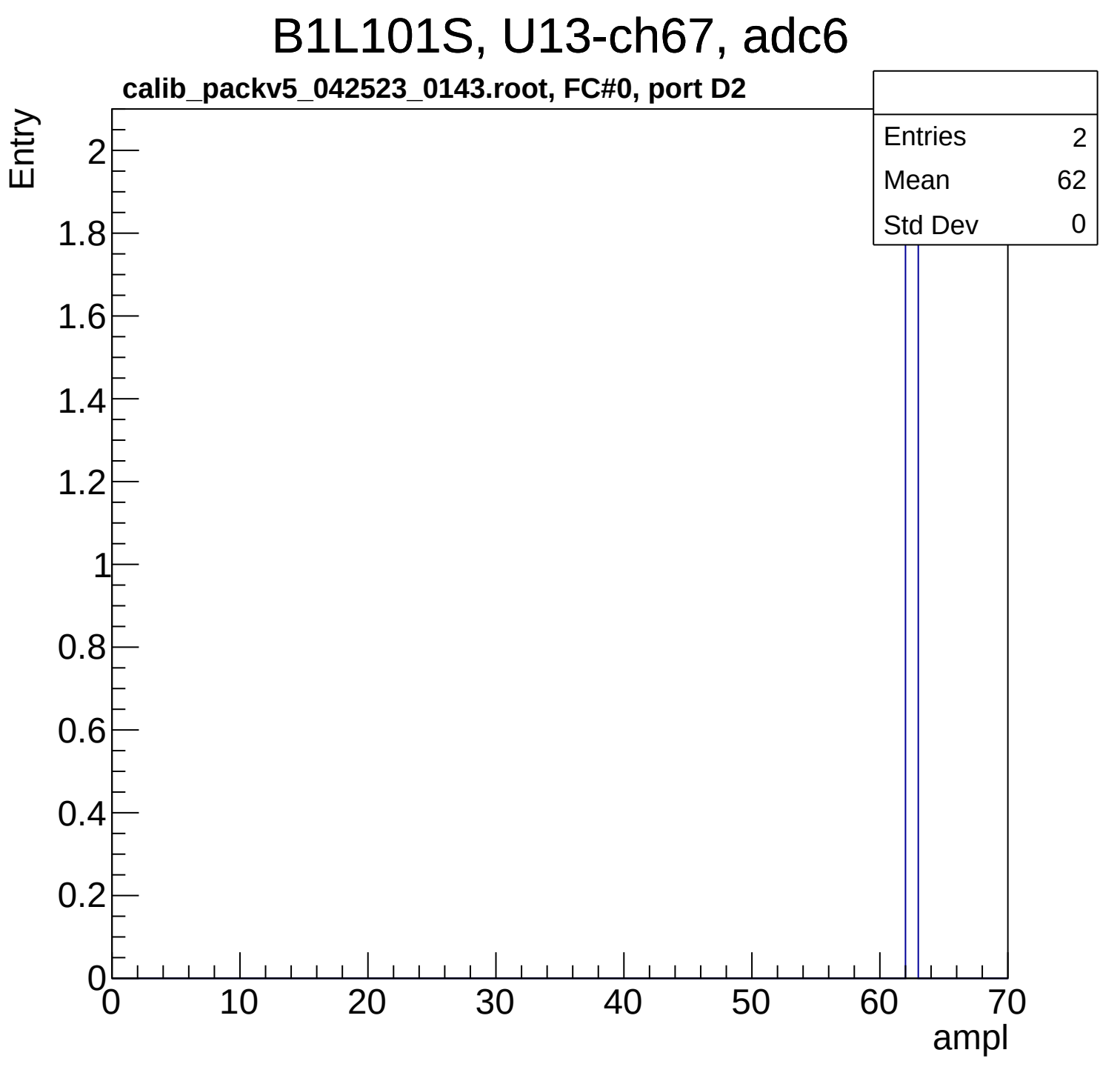
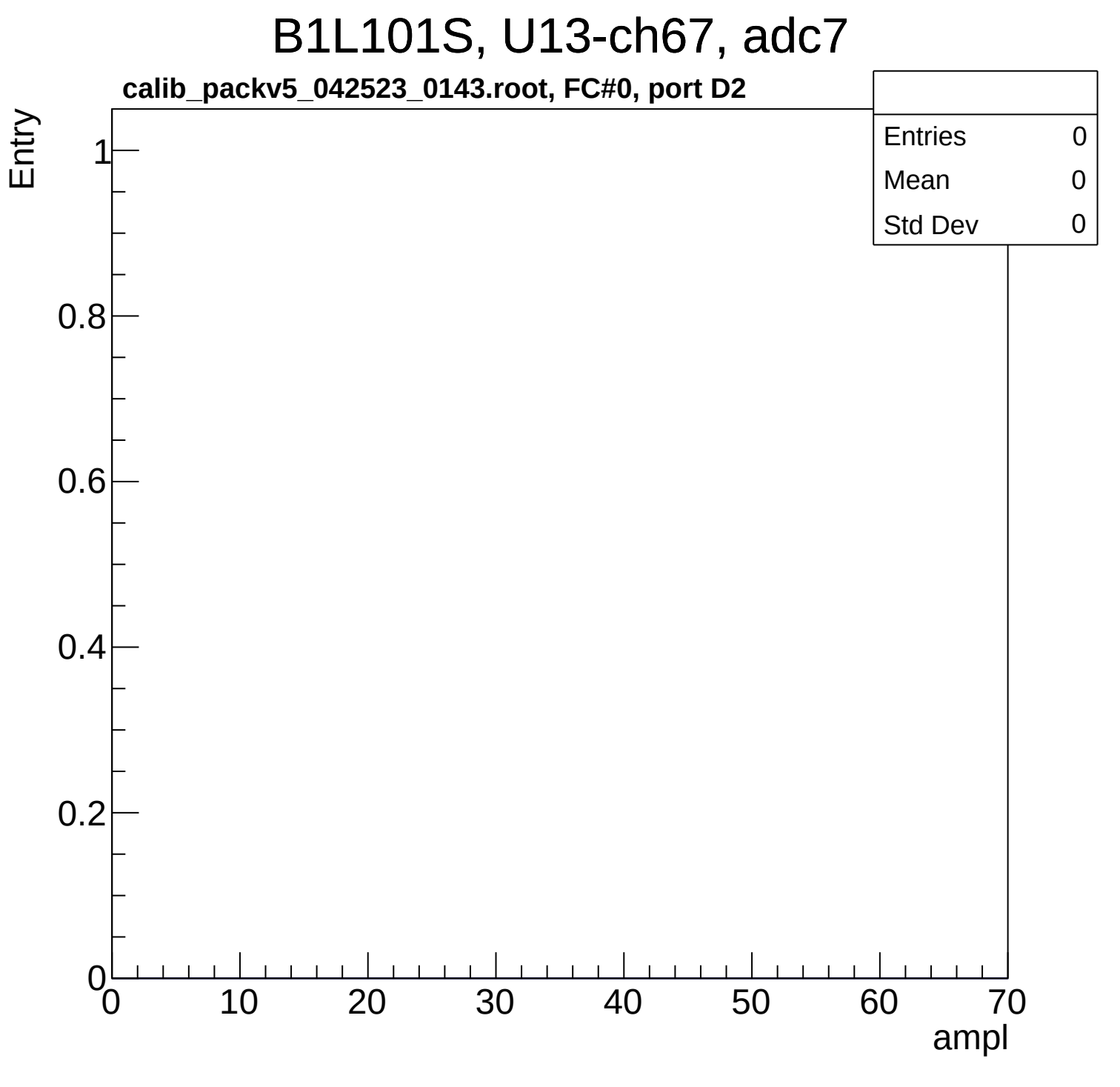

B1L101S, U13-ch67, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch67, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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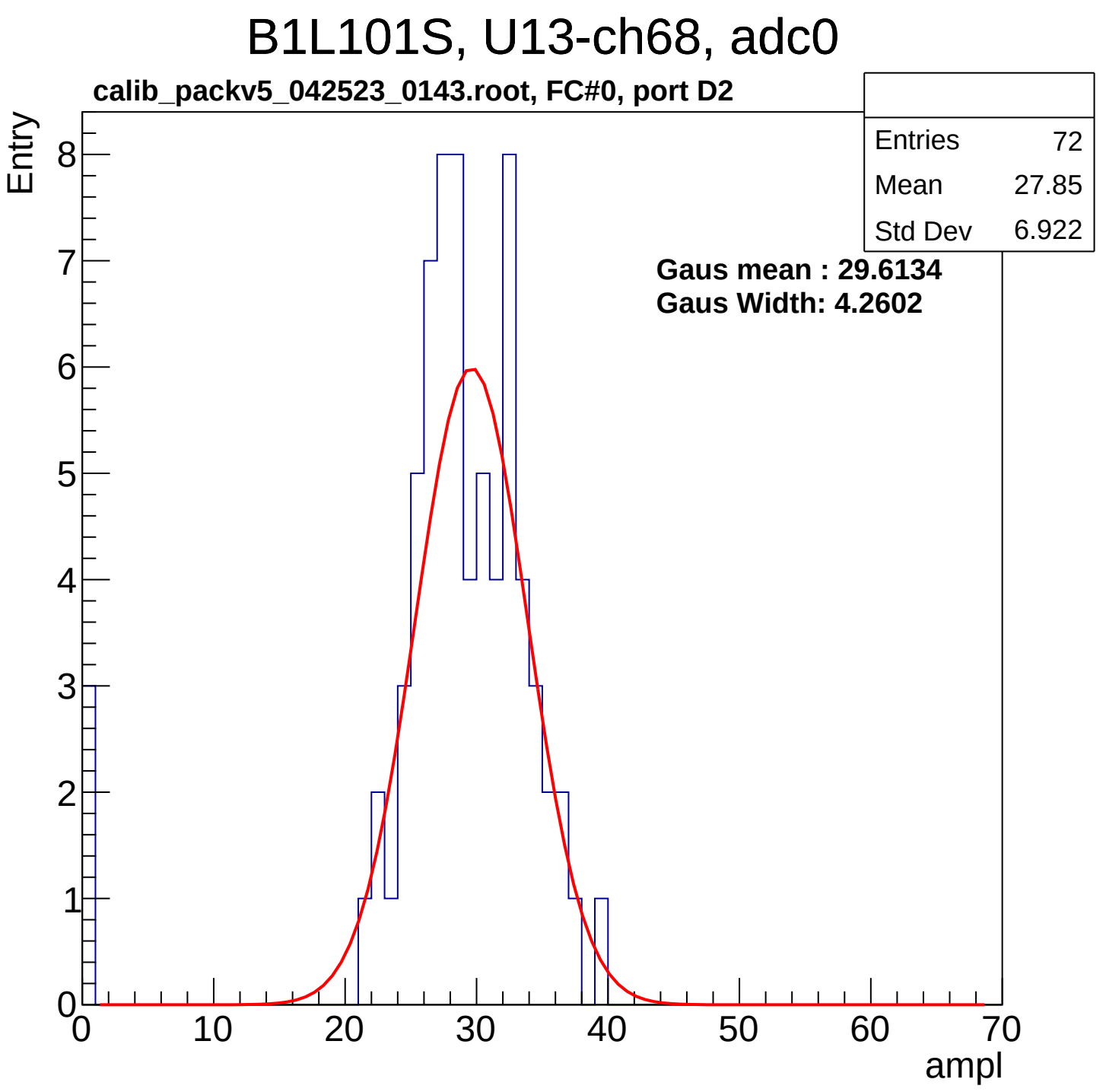
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch68, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 27.85
Std Dev 6.922
Gaus mean : 29.6134
Gaus Width: 4.2602
Entry
ampl
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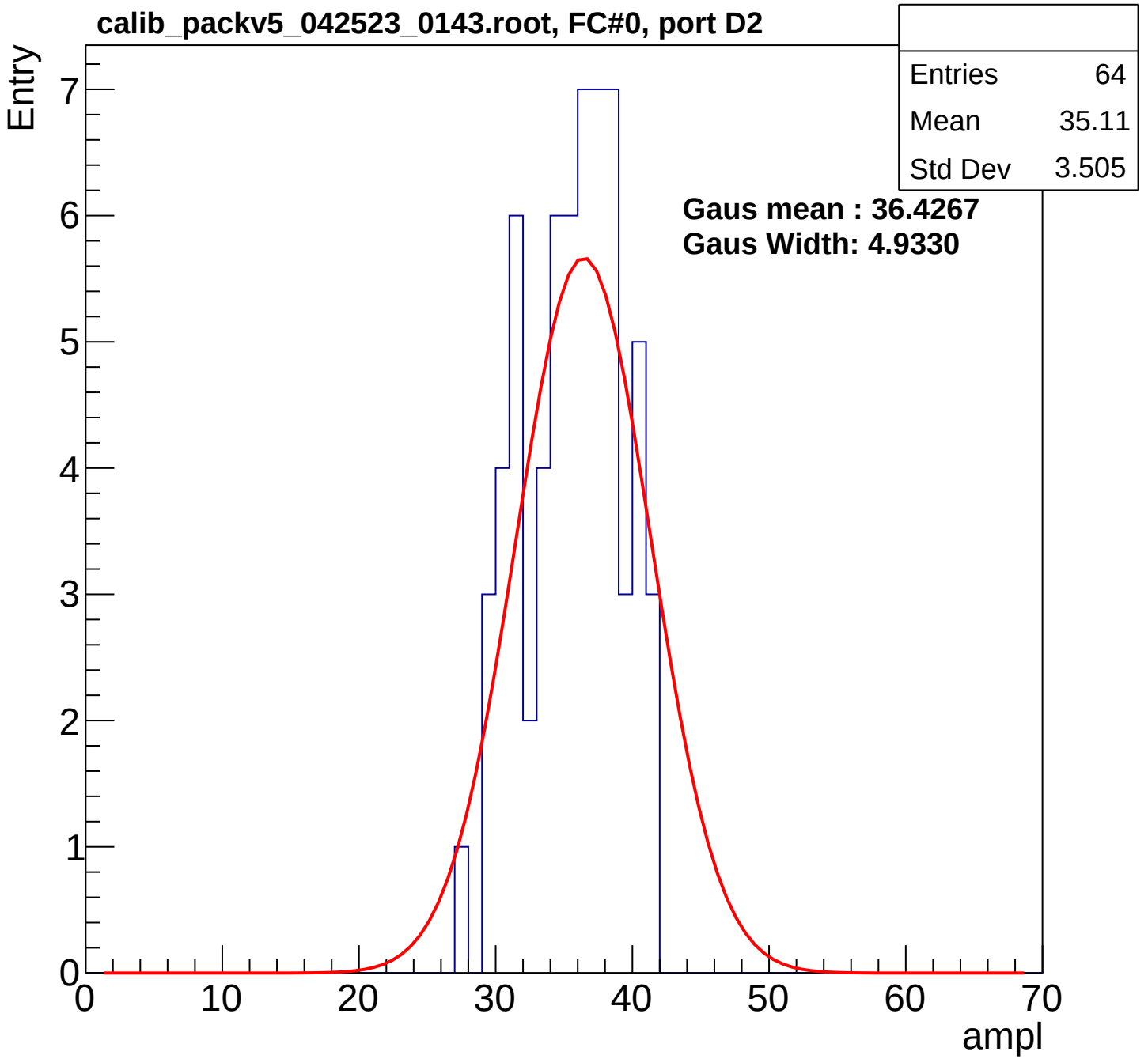
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch68, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 35.11
Std Dev 3.505
Gaus mean : 36.4267
Gaus Width: 4.9330
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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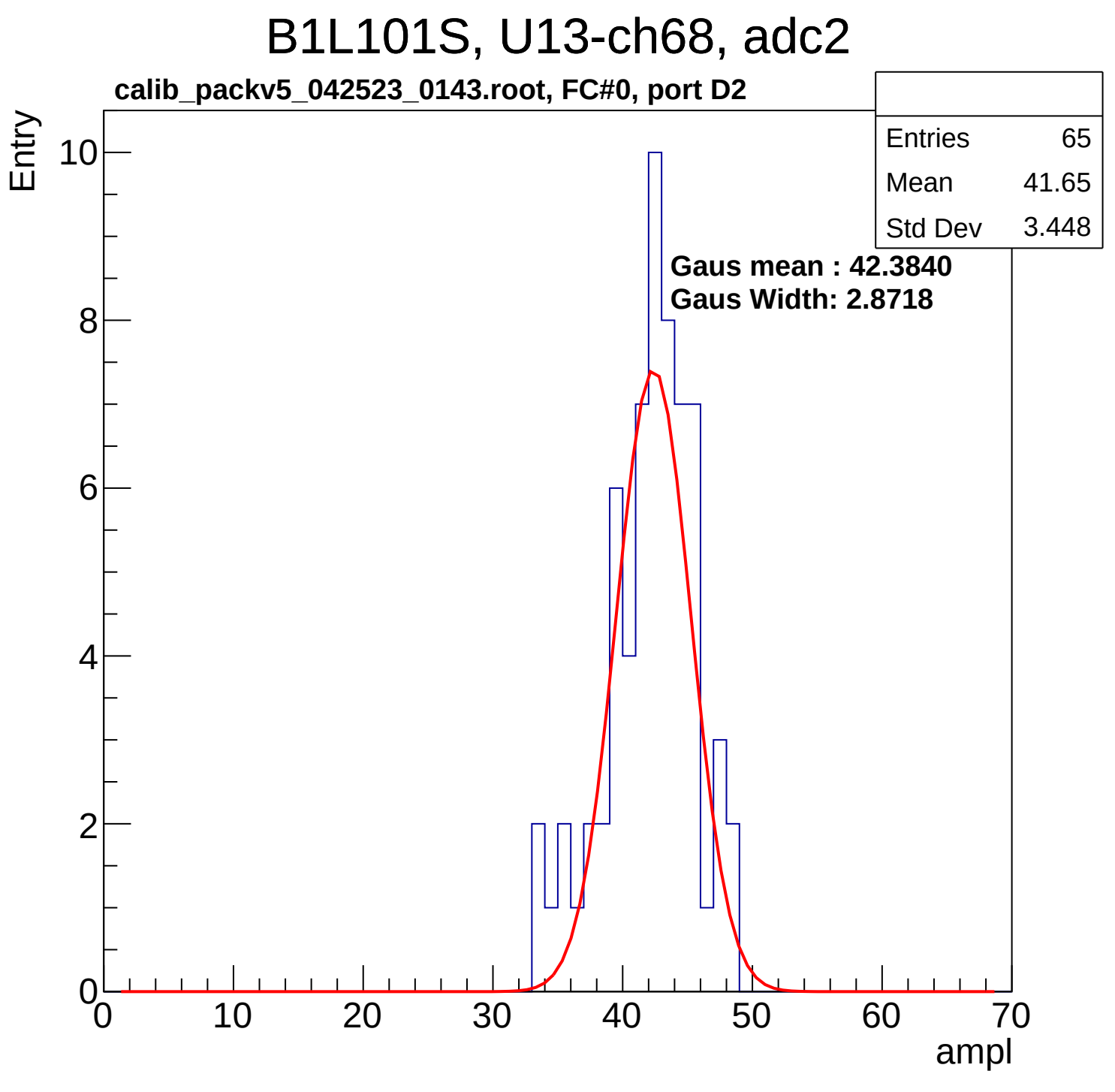
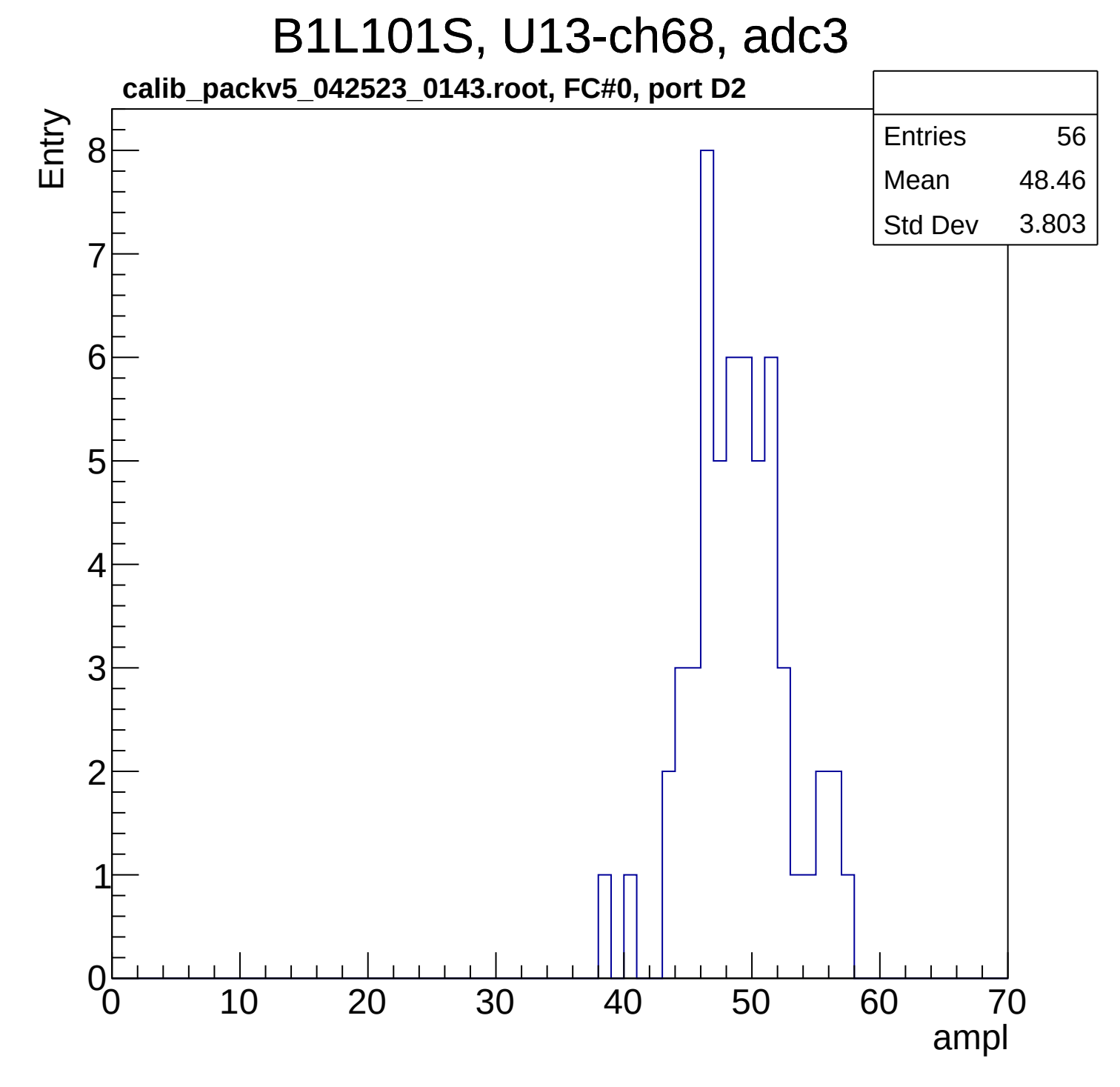

B1L101S, U13-ch68, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 41.65
Std Dev 3.448
Gaus mean : 42.3840
Gaus Width: 2.8718
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch68, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 48.46
Std Dev 3.803
Entry
ampl
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3
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5
6
7
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0
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60
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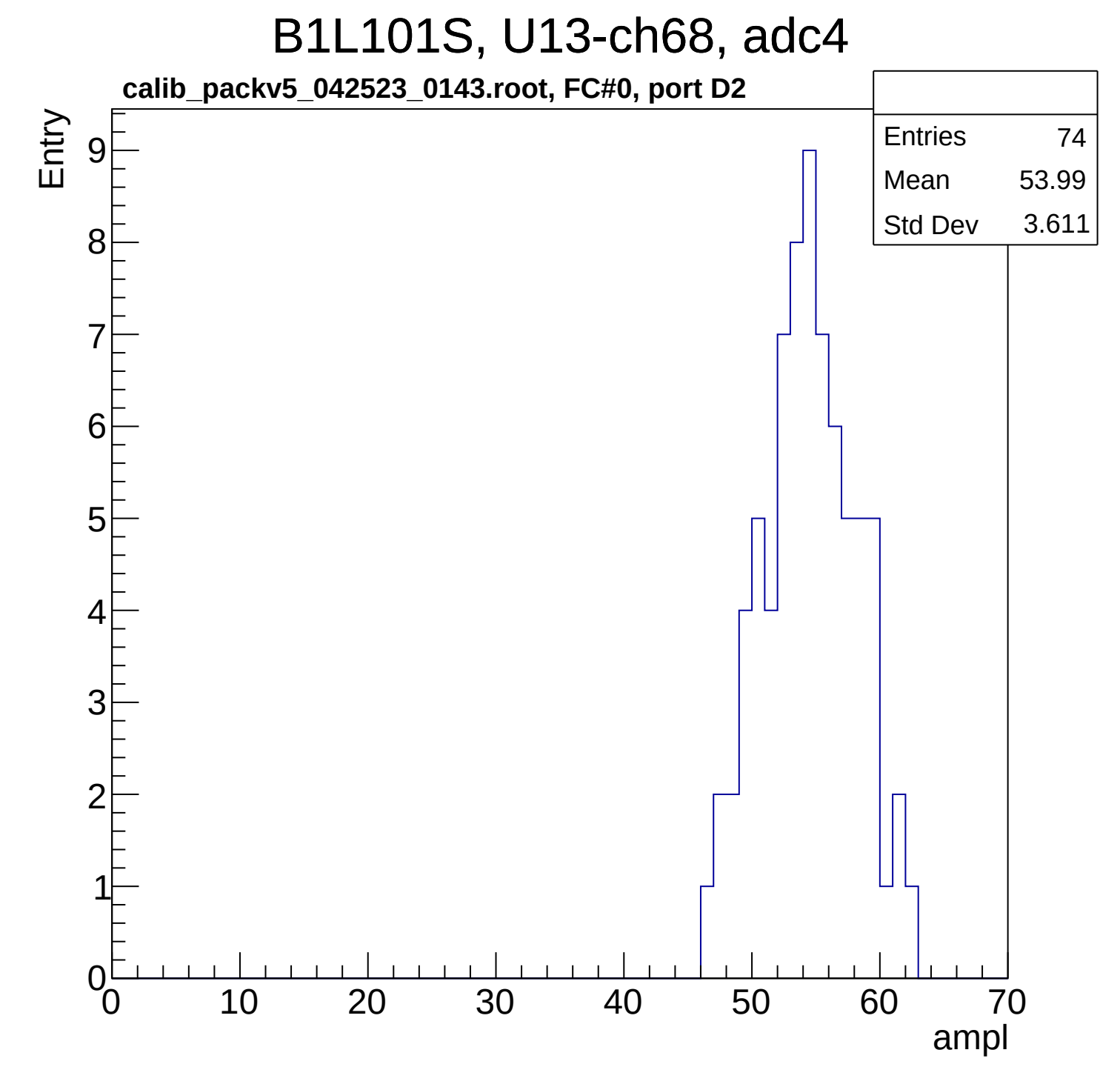
B1L101S, U13-ch68, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 53.99
Std Dev 3.611
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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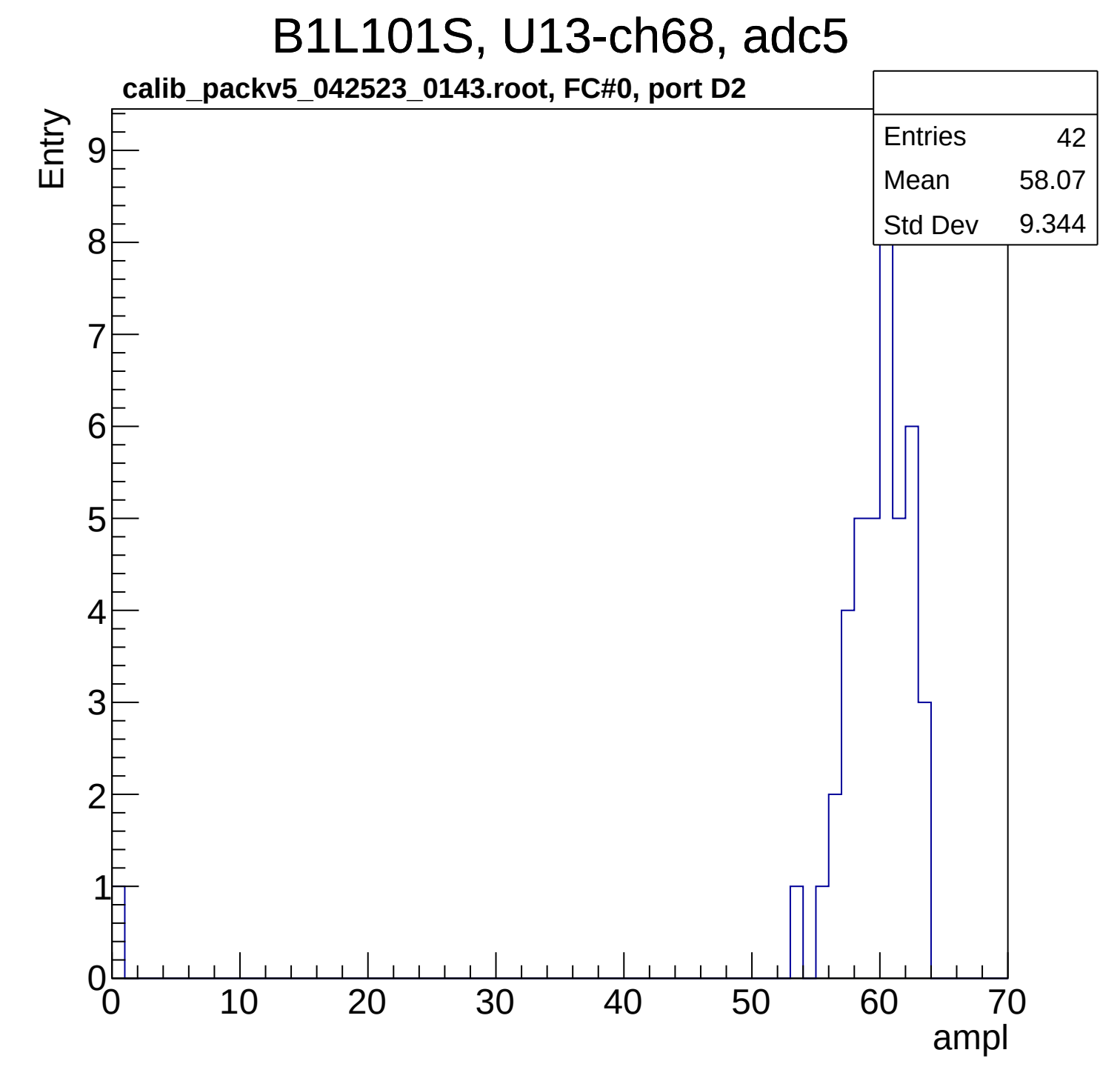
B1L101S, U13-ch68, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 58.07
Std Dev 9.344
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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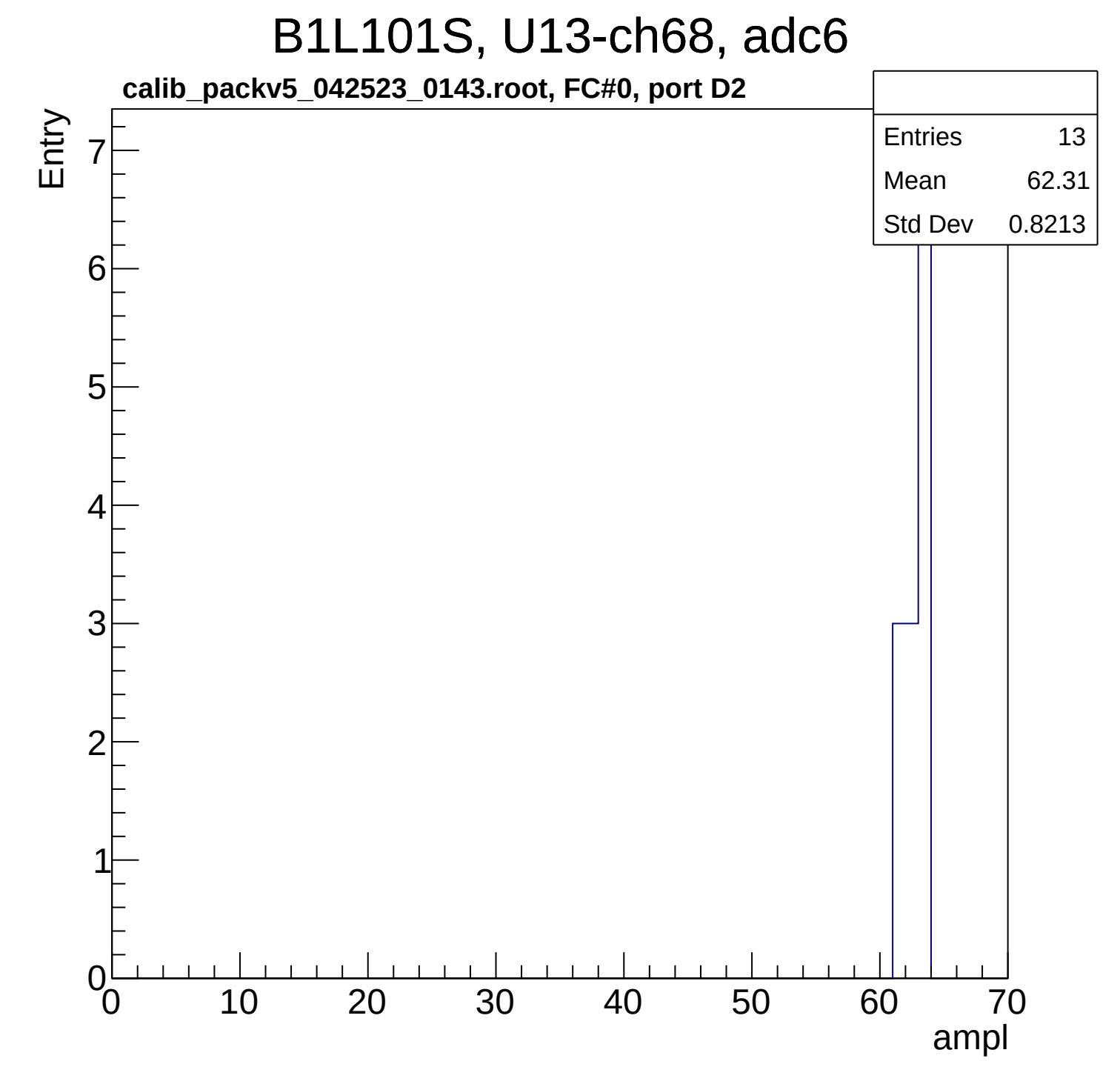
B1L101S, U13-ch68, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 13
Mean 62.31
Std Dev 0.8213
Entry
ampl
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1
2
3
4
5
6
7
0
10
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40
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60
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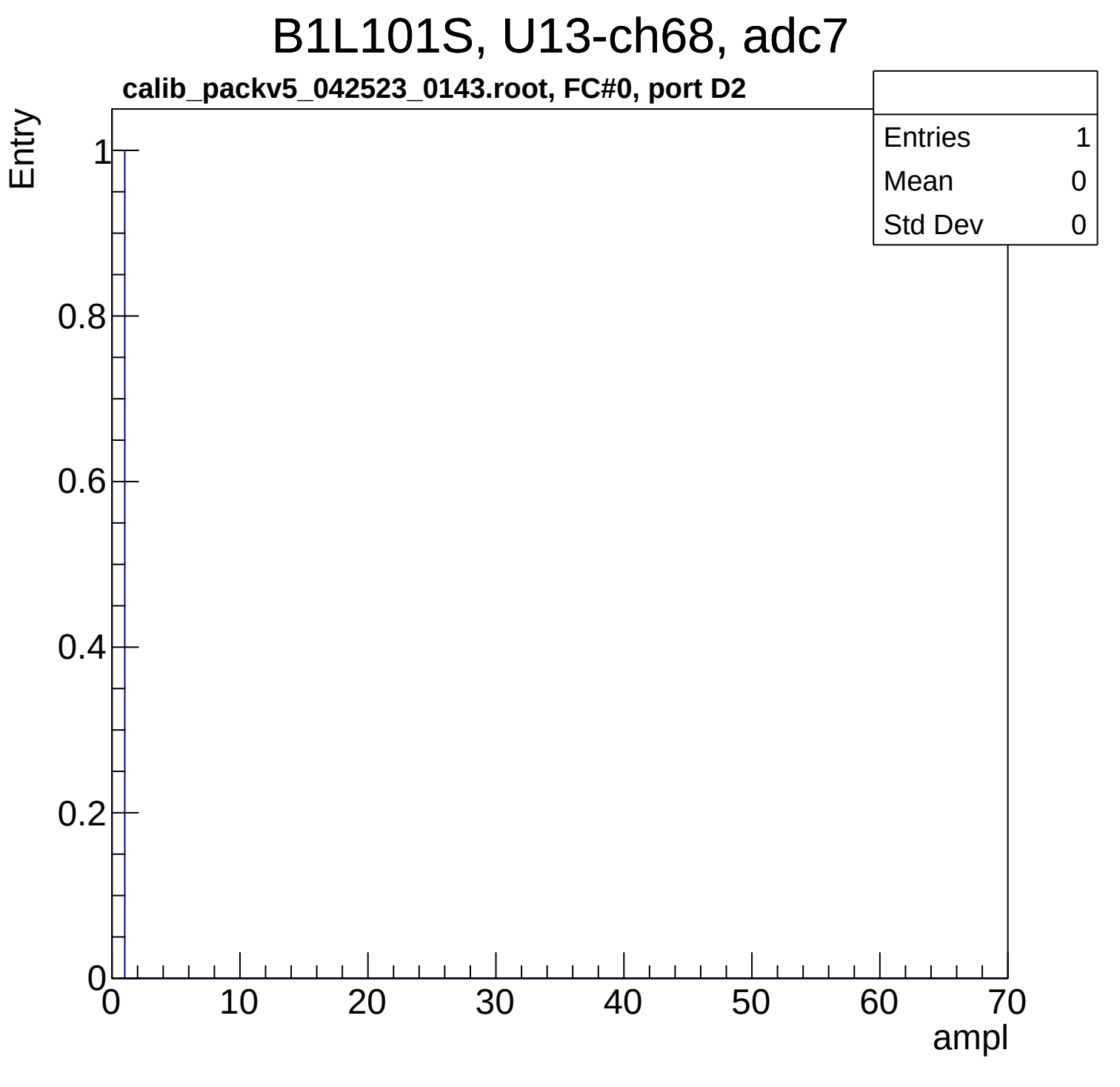
B1L101S, U13-ch68, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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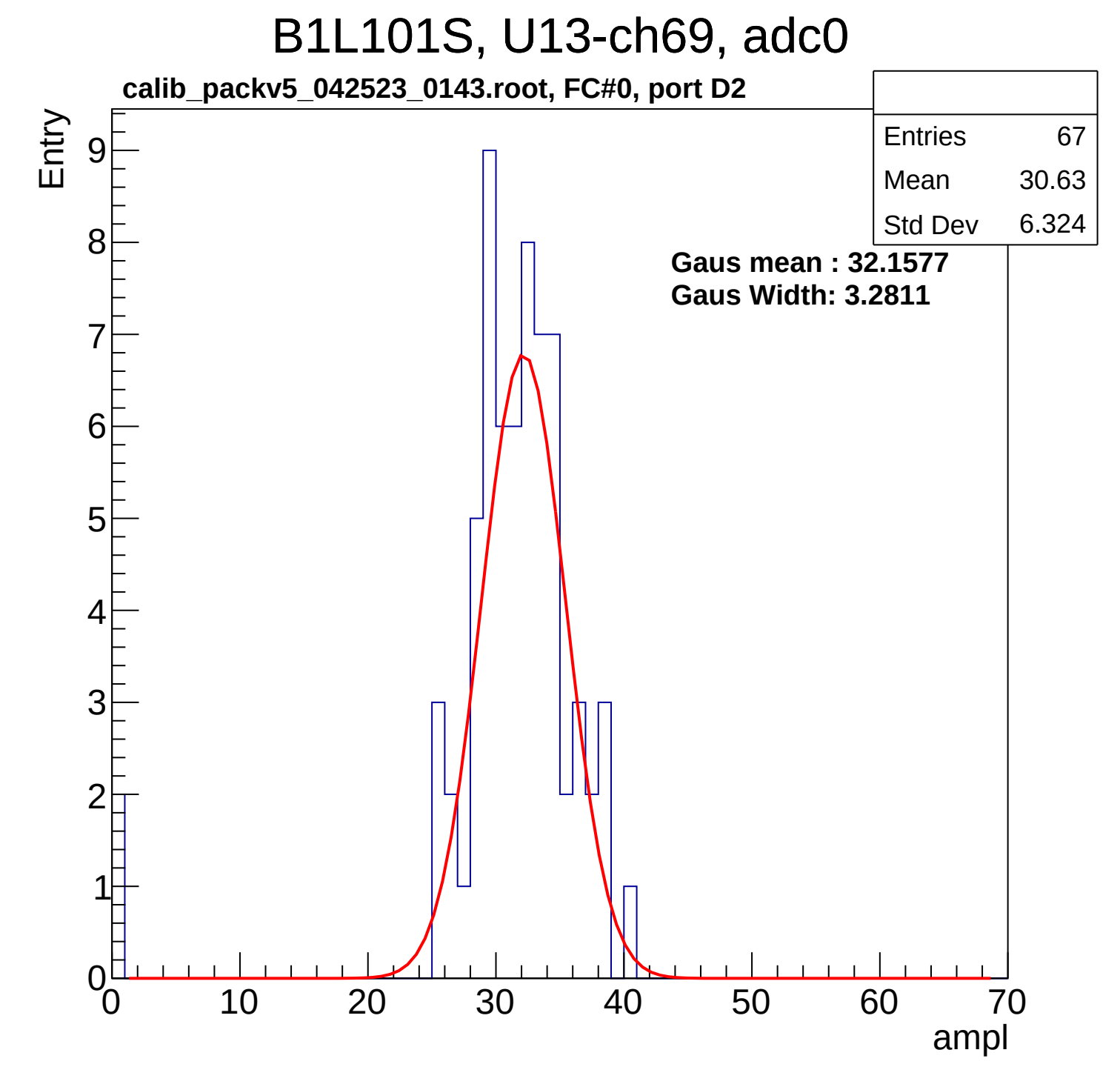
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch69, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 30.63
Std Dev 6.324
Gaus mean : 32.1577
Gaus Width: 3.2811
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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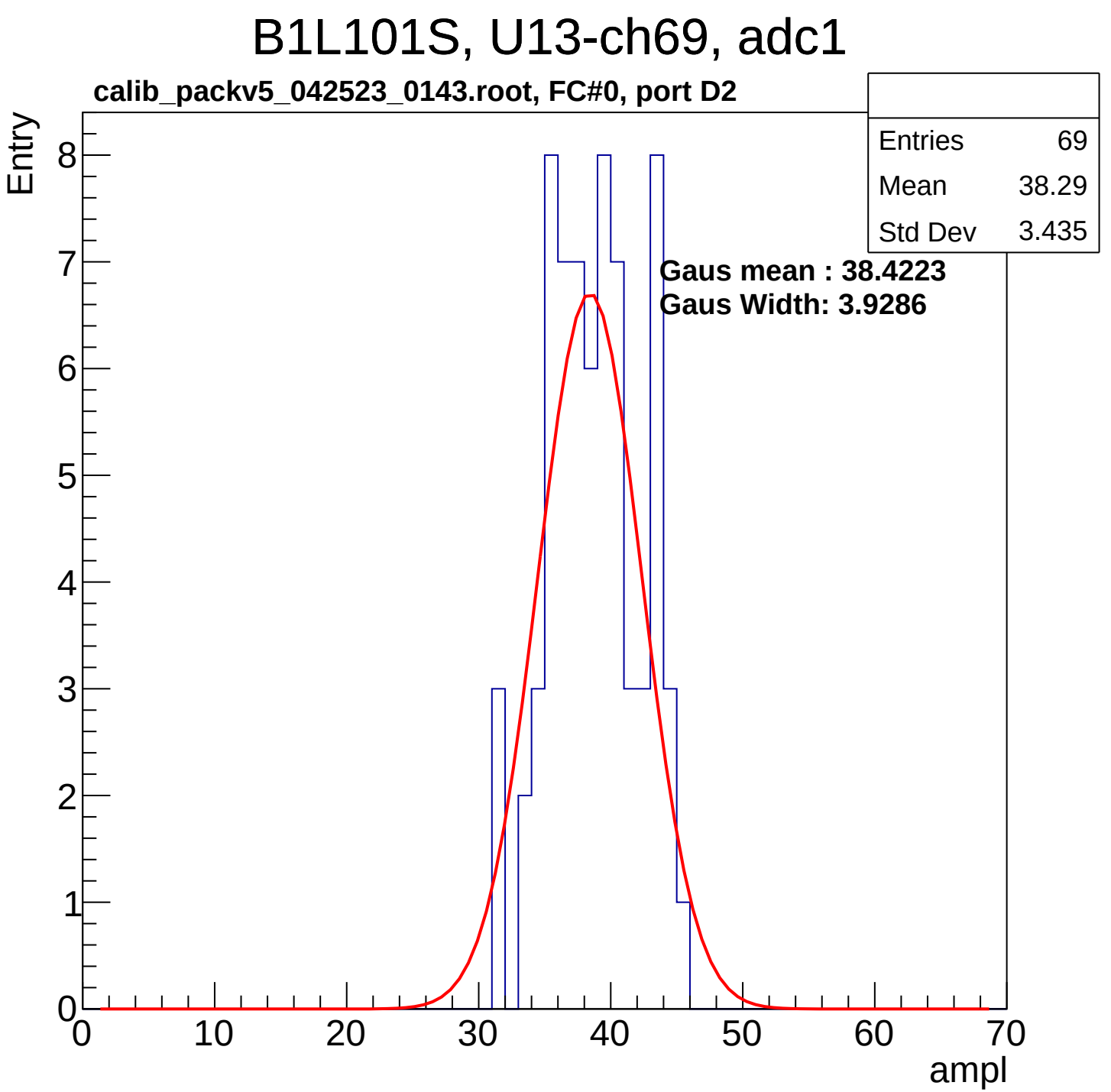
B1L101S, U13-ch69, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 38.29
Std Dev 3.435
Gaus mean : 38.4223
Gaus Width: 3.9286
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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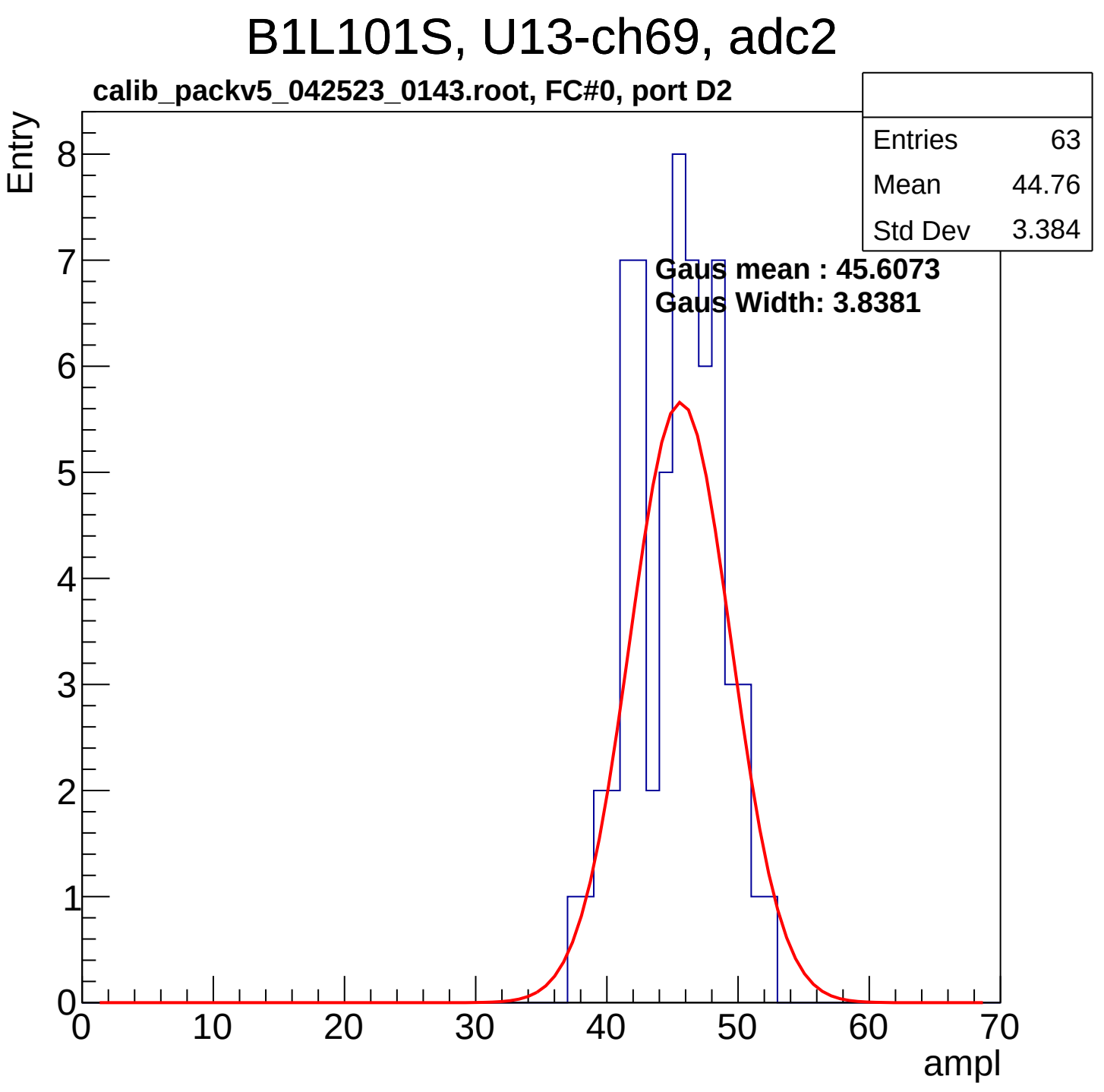
B1L101S, U13-ch69, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 44.76
Std Dev 3.384
Gaus mean : 45.6073
Gaus Width: 3.8381
Entry
ampl
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1
2
3
4
5
6
7
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0
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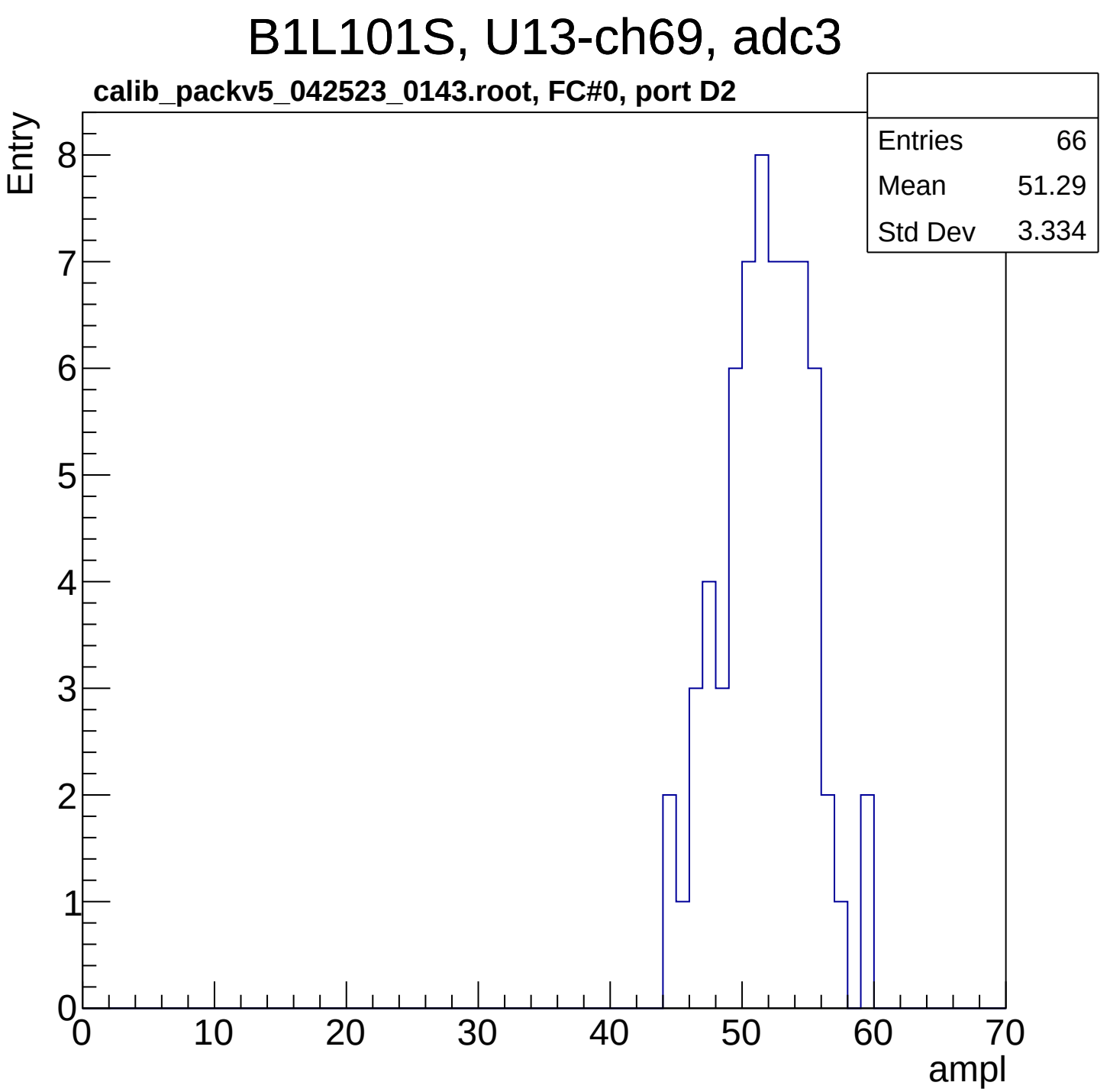
B1L101S, U13-ch69, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 51.29
Std Dev 3.334
Entry
ampl
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4
5
6
7
8
0
10
20
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40
50
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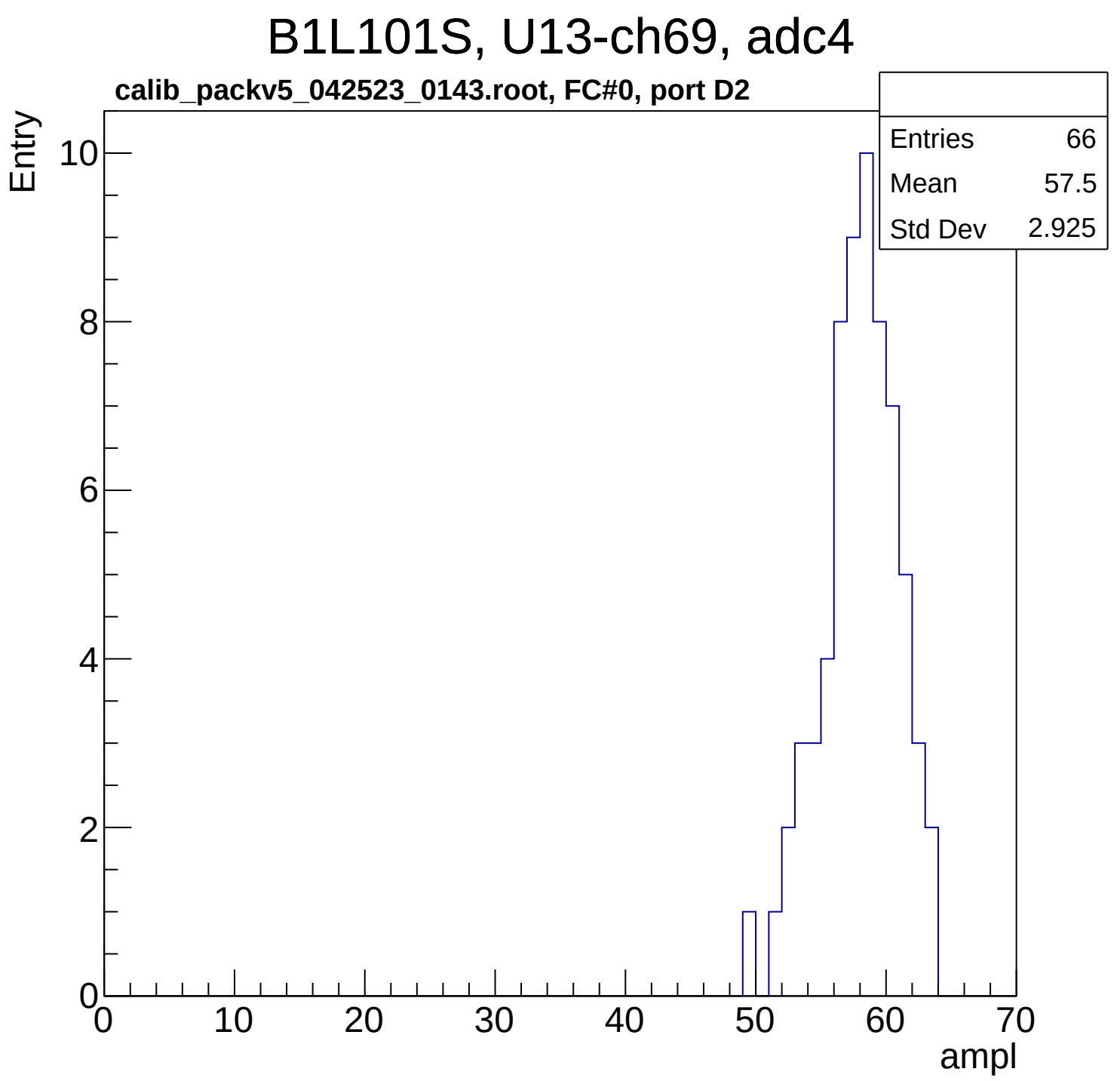
B1L101S, U13-ch69, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 57.5
Std Dev 2.925
Entry
ampl
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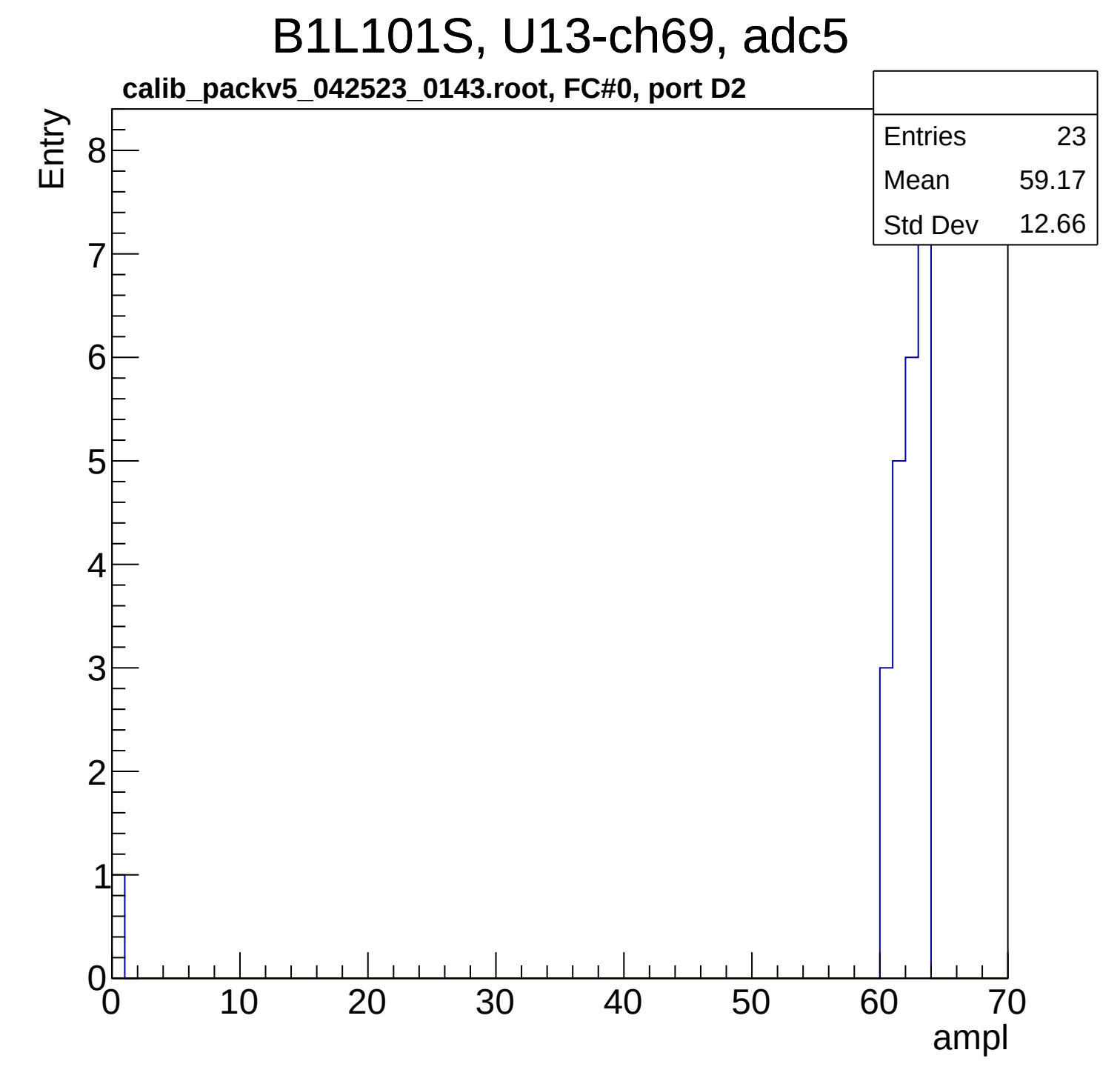
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch69, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 23
Mean 59.17
Std Dev 12.66
Entry
ampl
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1
2
3
4
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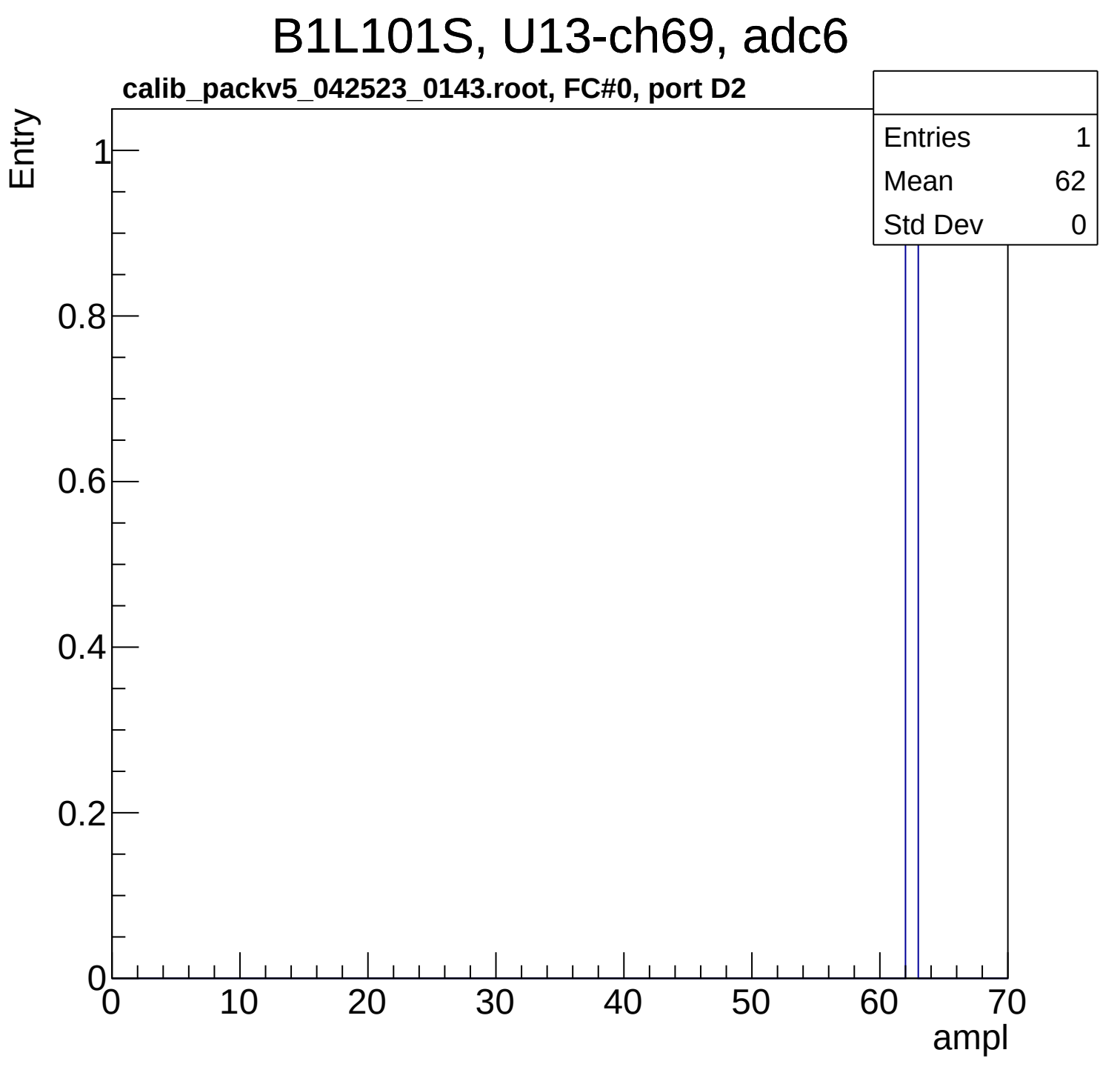
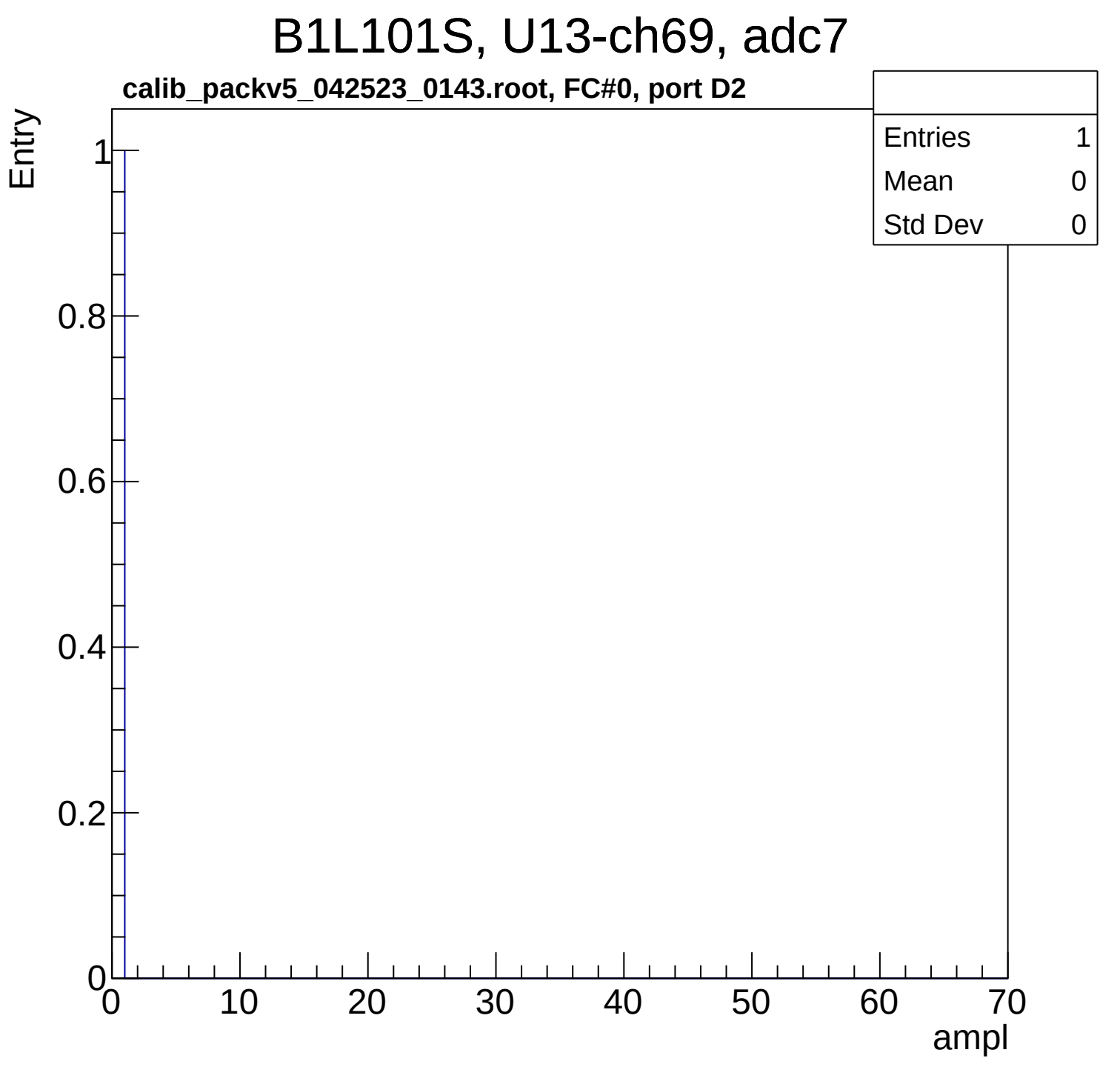

B1L101S, U13-ch69, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch69, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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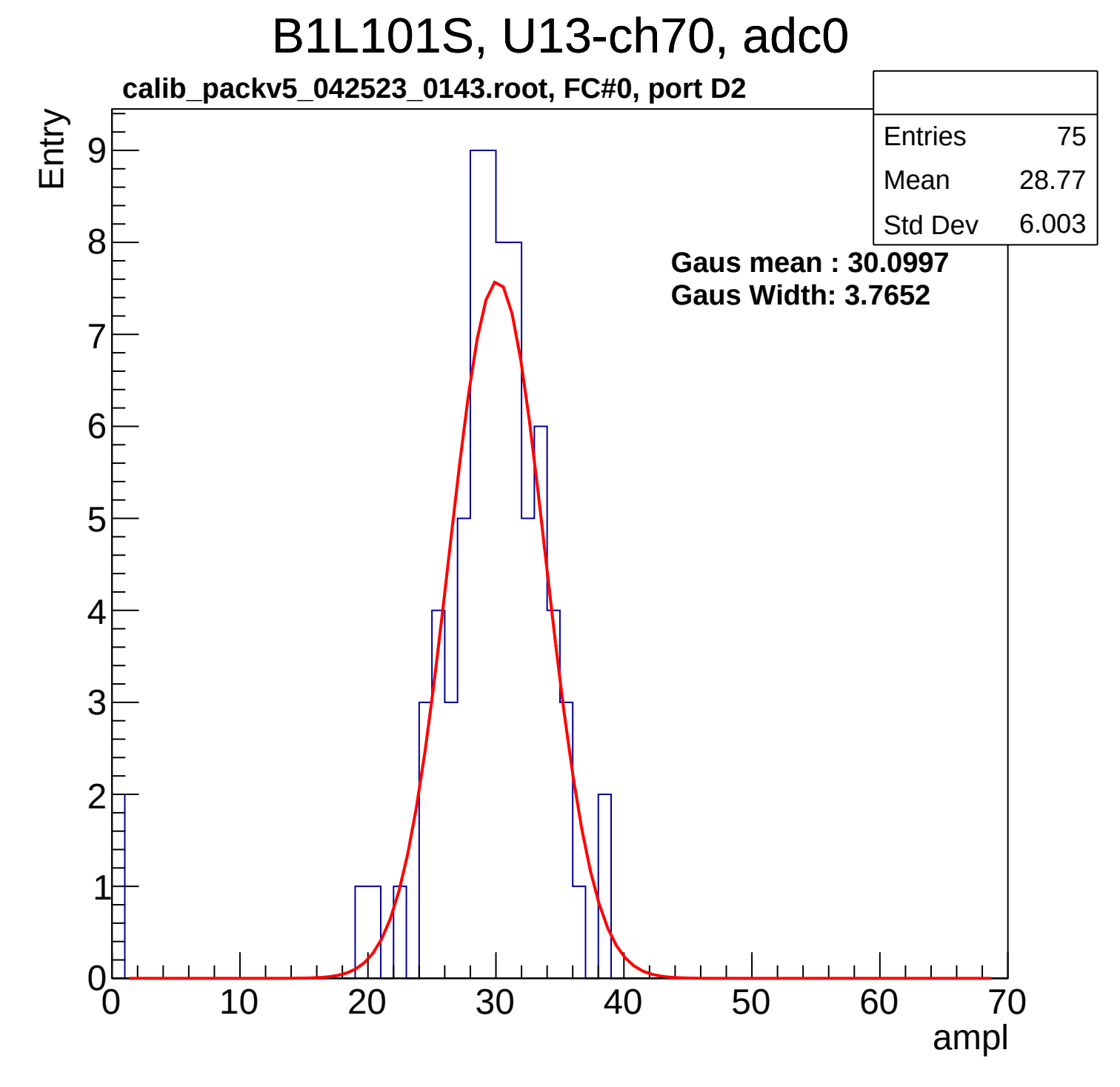
50
60
70
ampl


B1L101S, U13-ch70, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 28.77
Std Dev 6.003
Gaus mean : 30.0997
Gaus Width: 3.7652
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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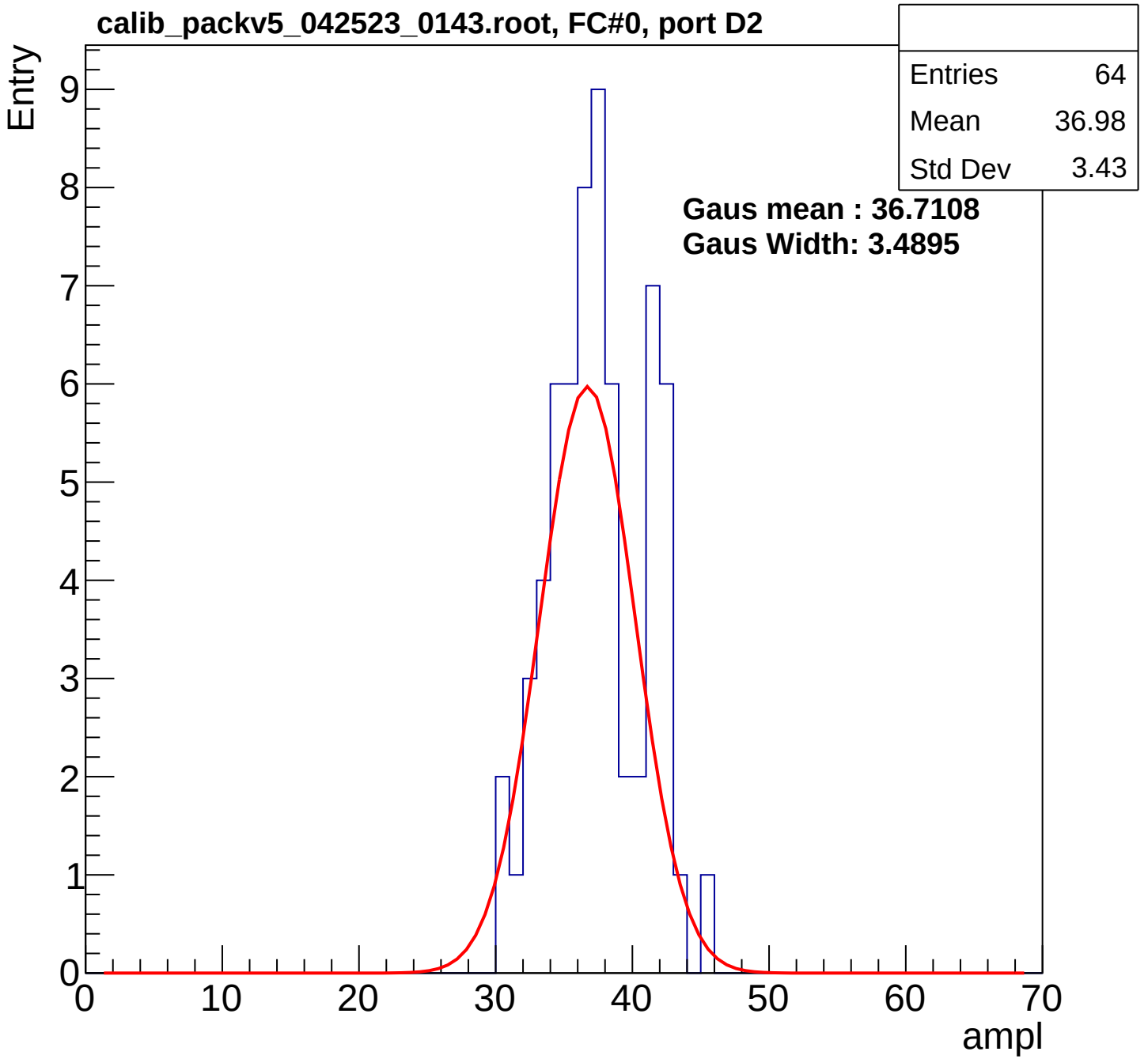
B1L101S, U13-ch70, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 36.98
Std Dev 3.43
Gaus mean : 36.7108
Gaus Width: 3.4895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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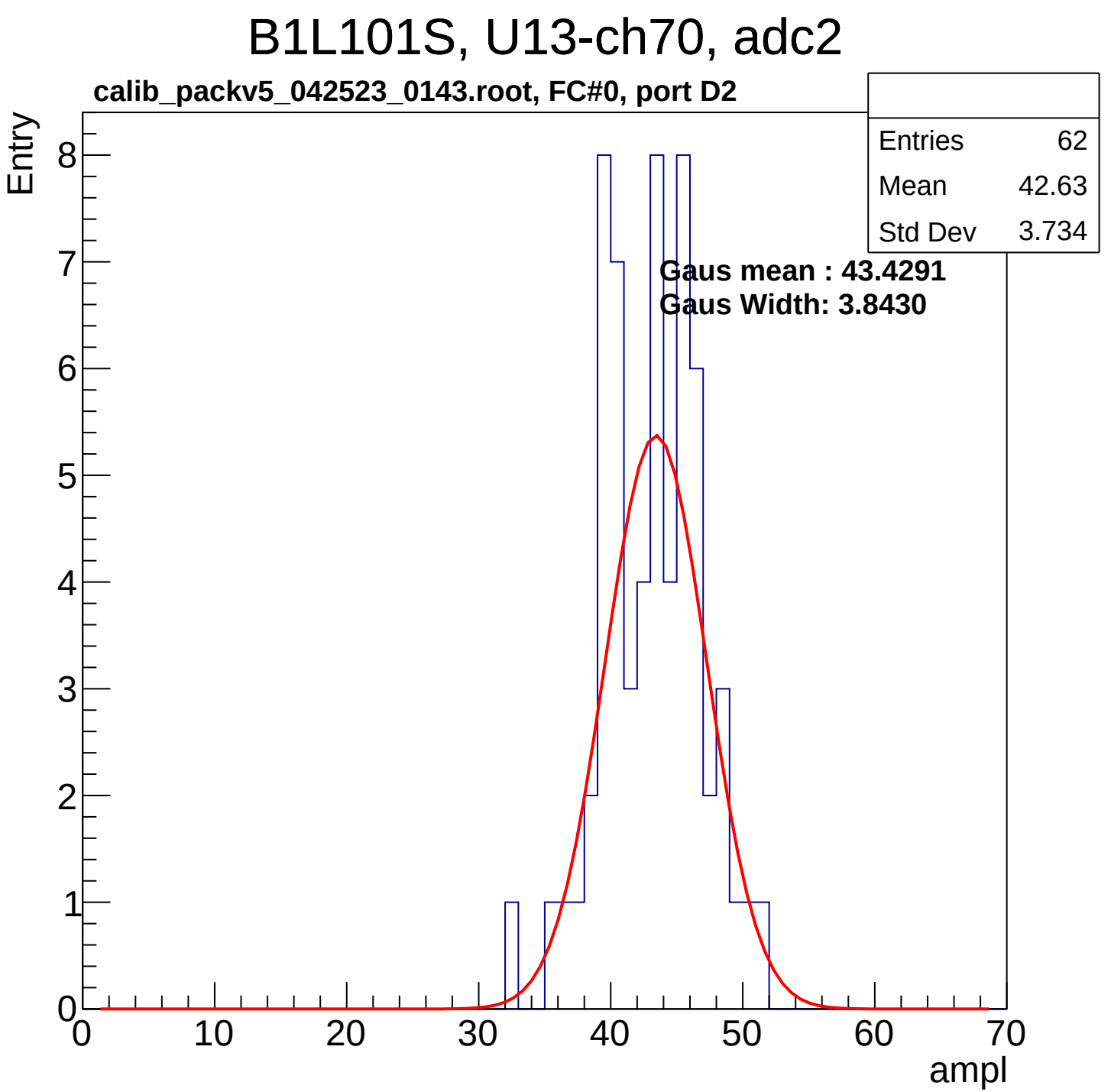
B1L101S, U13-ch70, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 42.63
Std Dev 3.734
Gaus mean : 43.4291
Gaus Width: 3.8430
Entry
ampl
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1
2
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4
5
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7
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0
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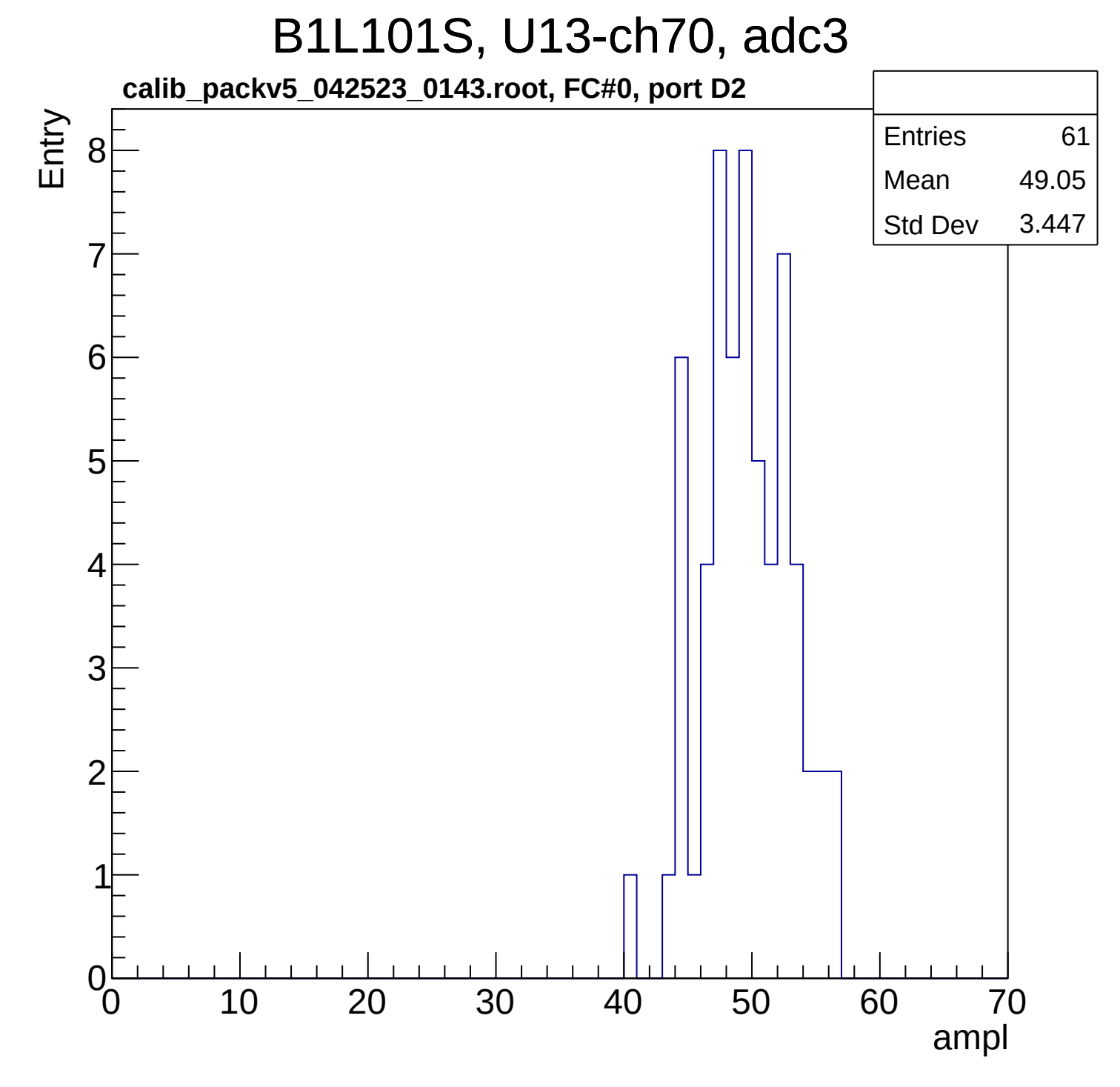
B1L101S, U13-ch70, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 49.05
Std Dev 3.447
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
50
60
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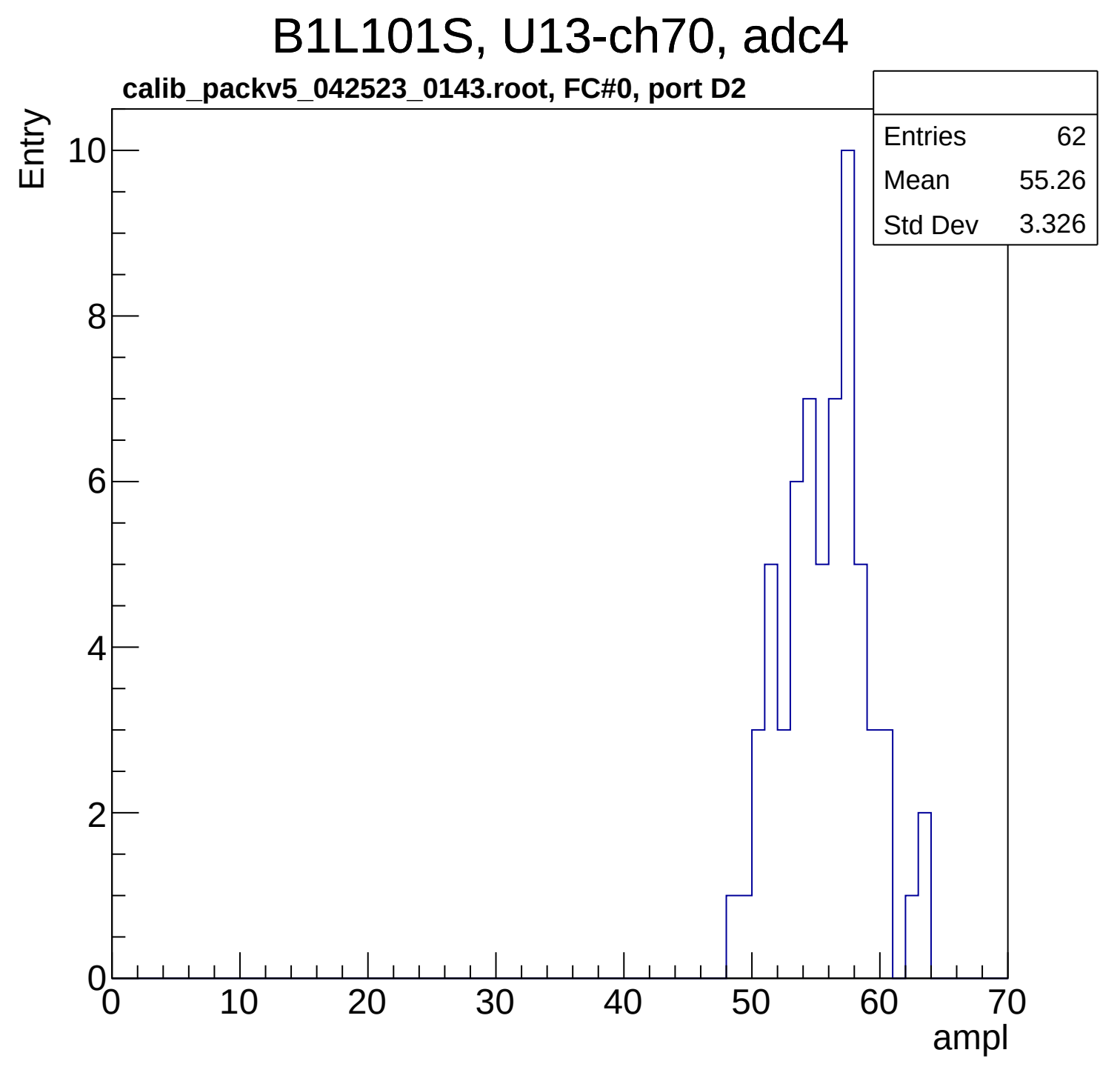
B1L101S, U13-ch70, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 55.26
Std Dev 3.326
Entry
ampl
0
2
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6
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0
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40
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60
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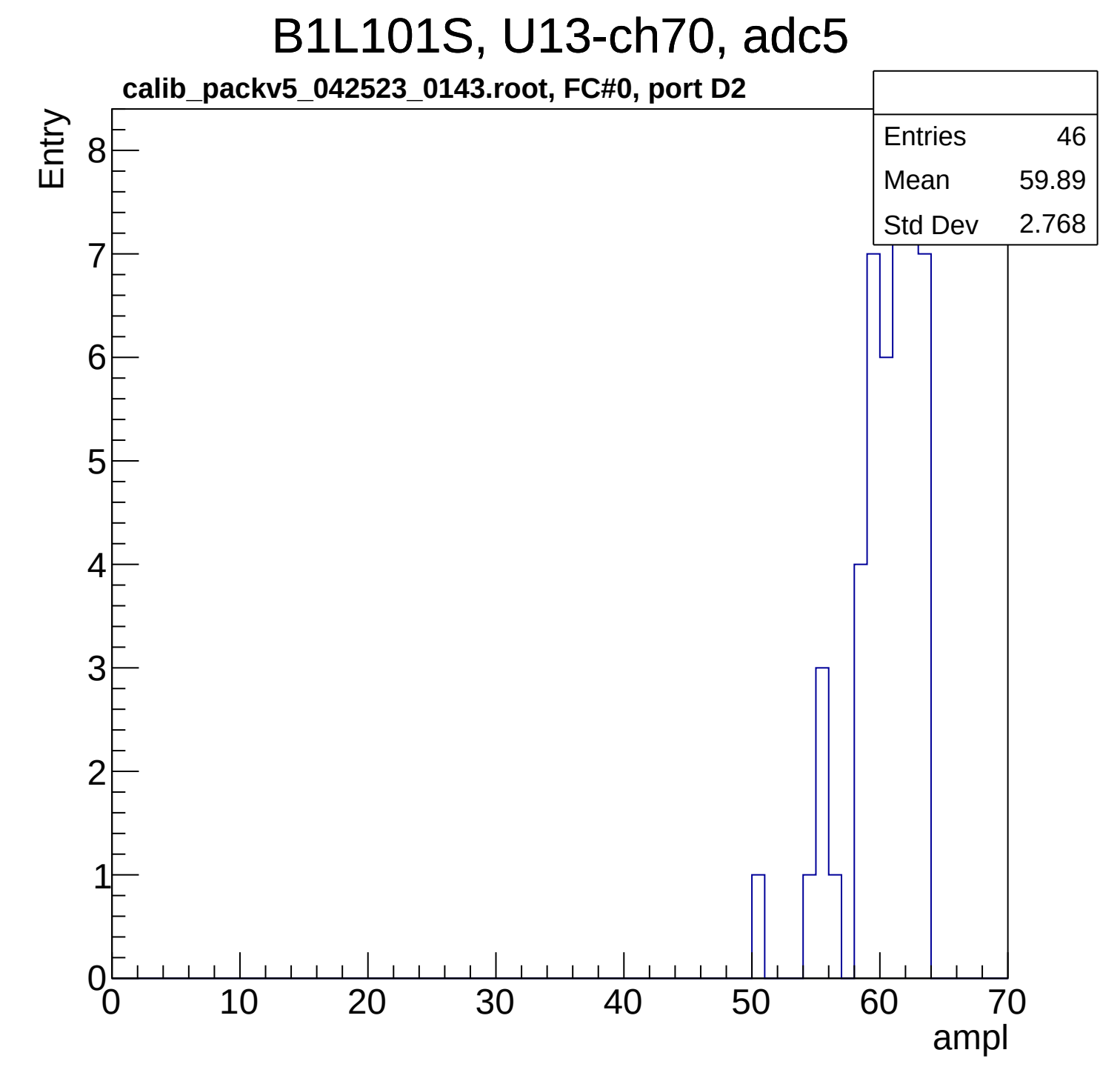
B1L101S, U13-ch70, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 59.89
Std Dev 2.768
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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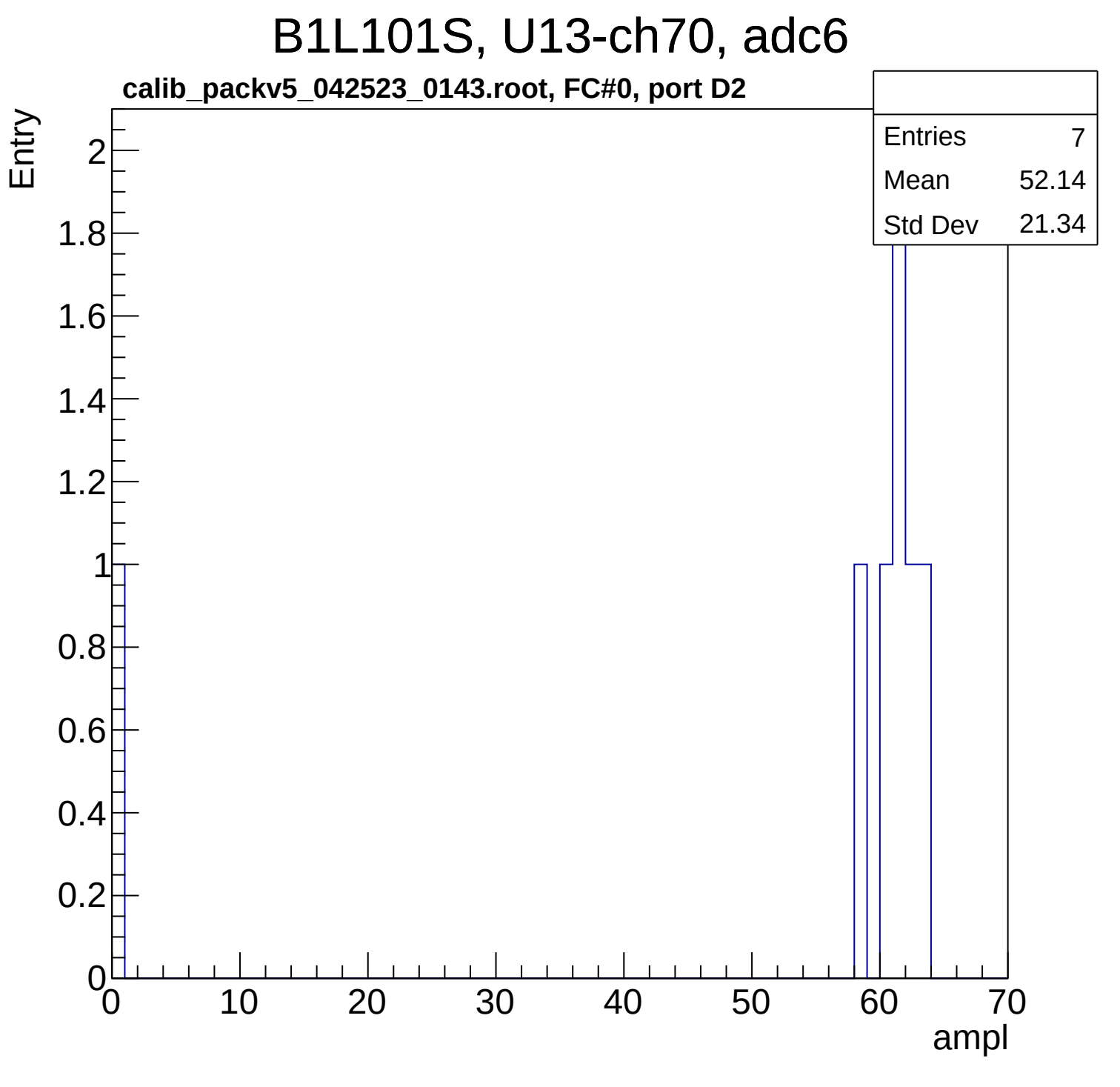
B1L101S, U13-ch70, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 7
Mean 52.14
Std Dev 21.34
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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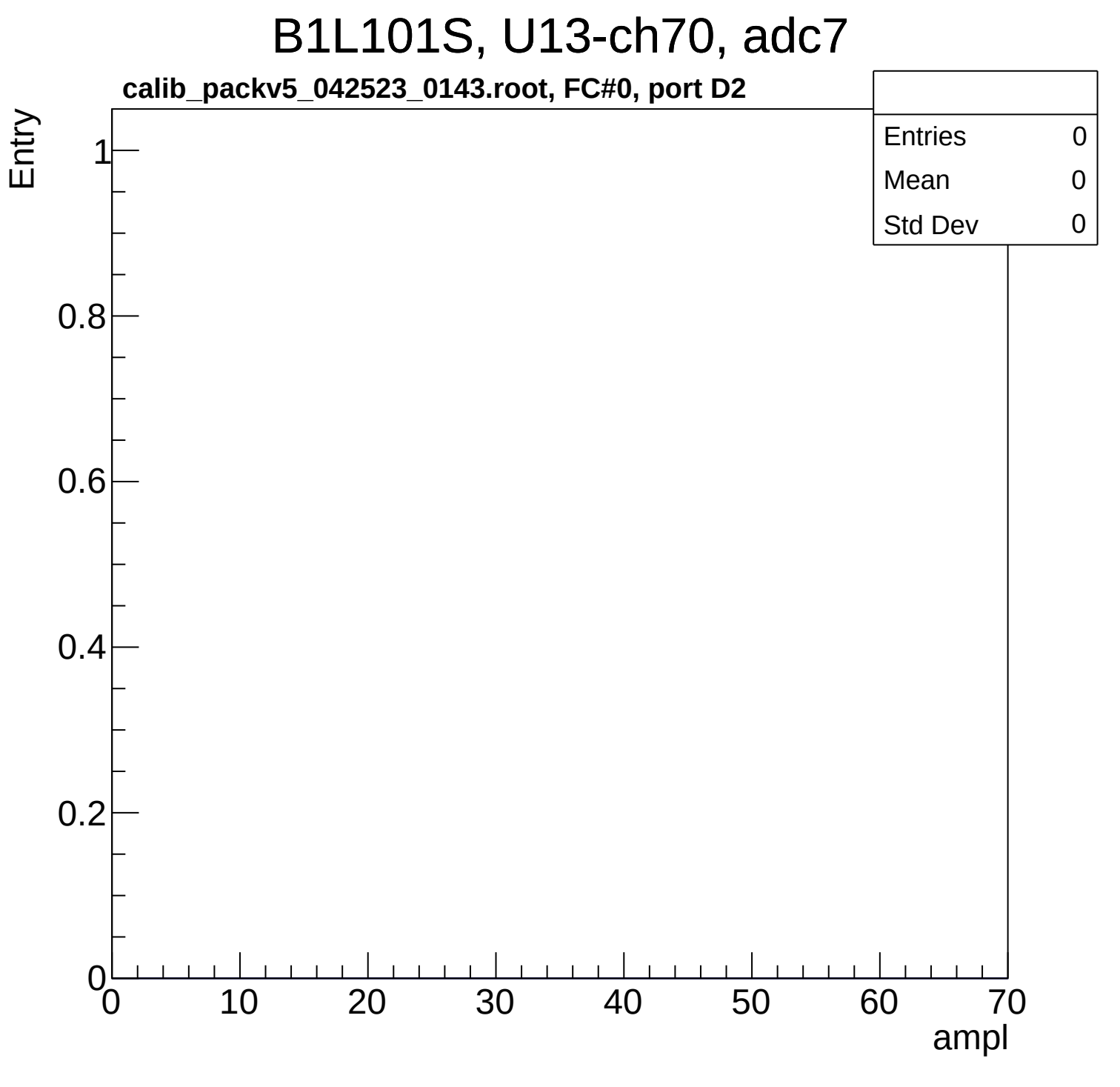
B1L101S, U13-ch70, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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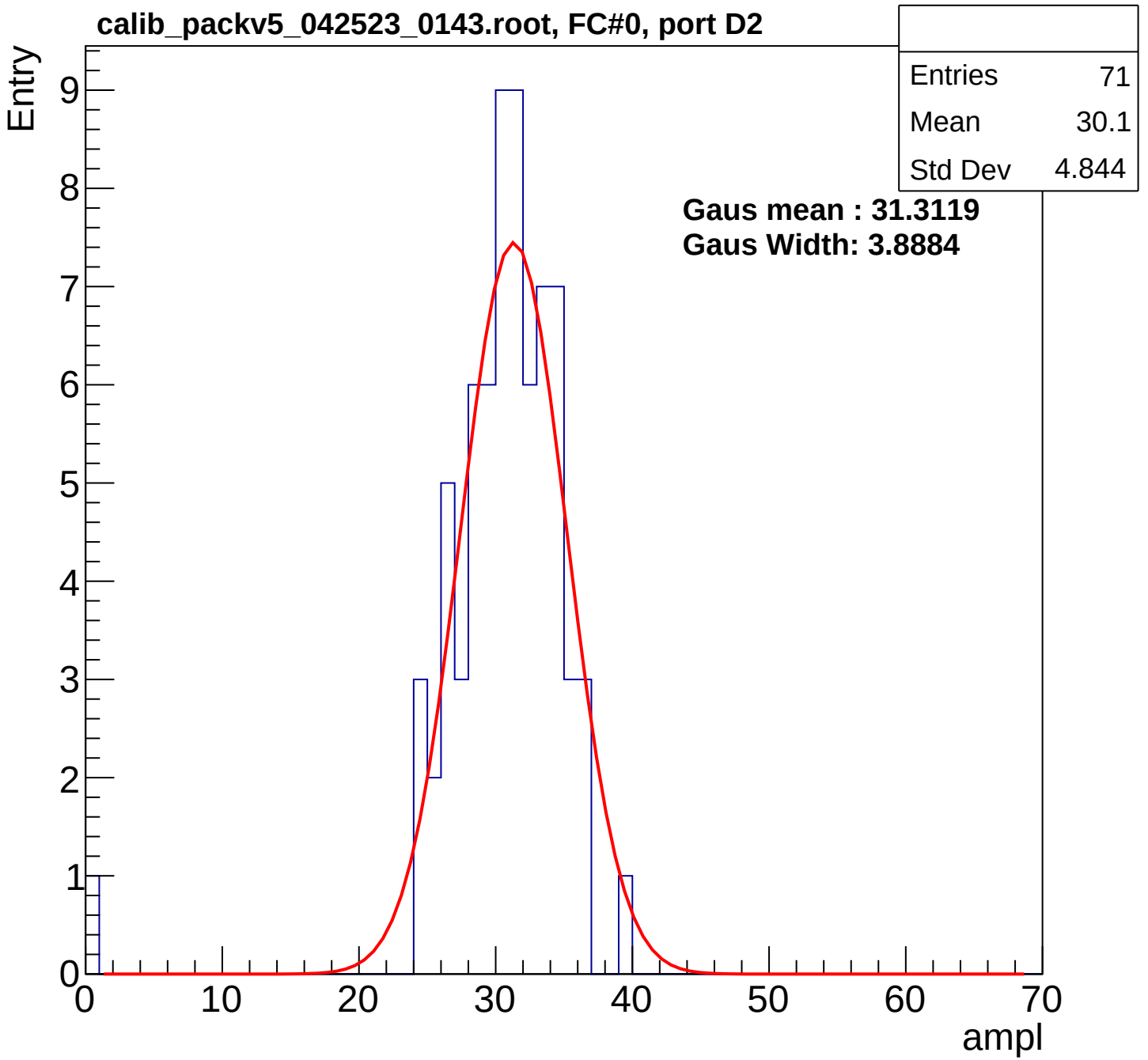
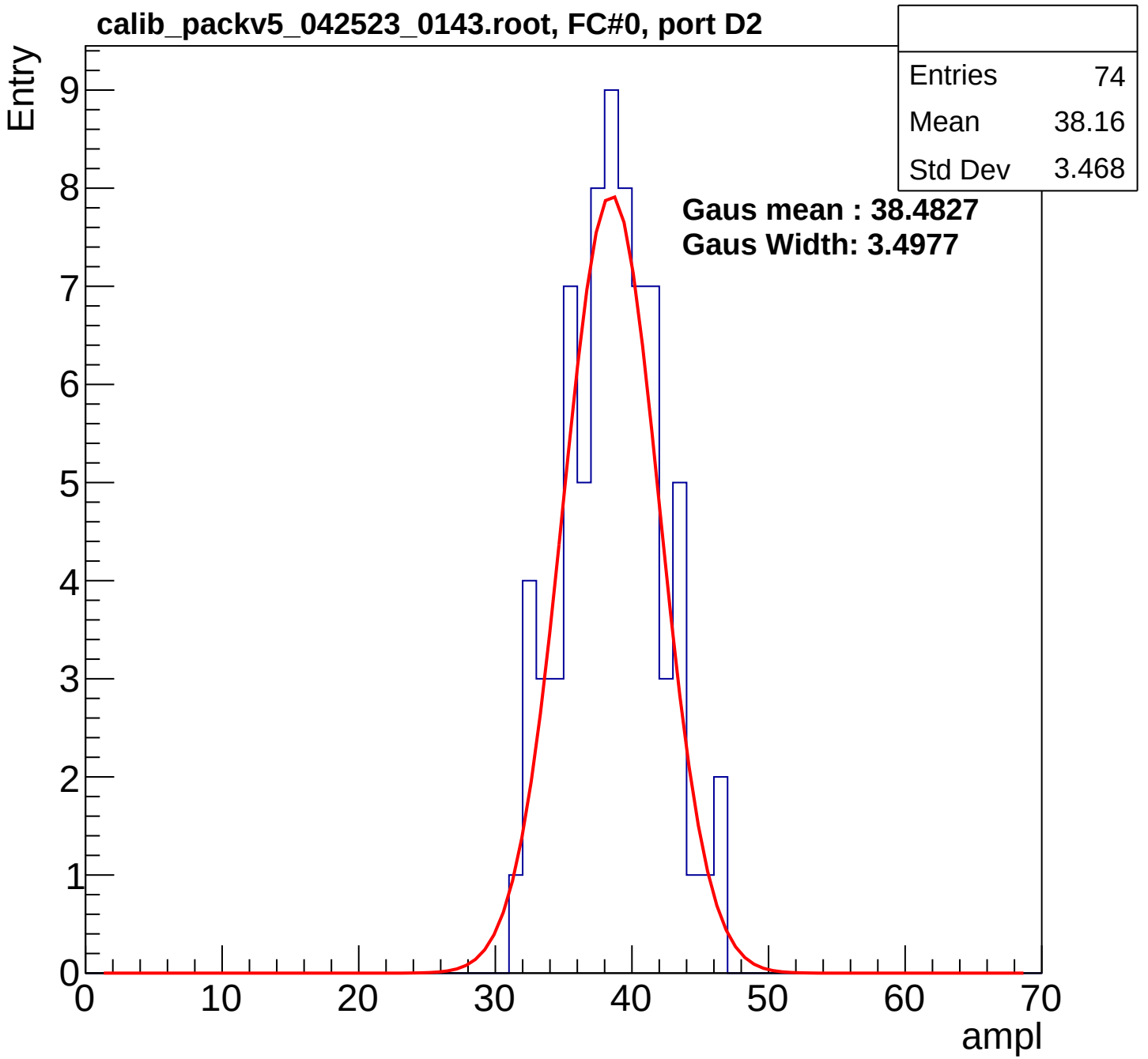
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch71, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 30.1
Std Dev 4.844
Gaus mean : 31.3119
Gaus Width: 3.8884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch71, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 38.16
Std Dev 3.468
Gaus mean : 38.4827
Gaus Width: 3.4977
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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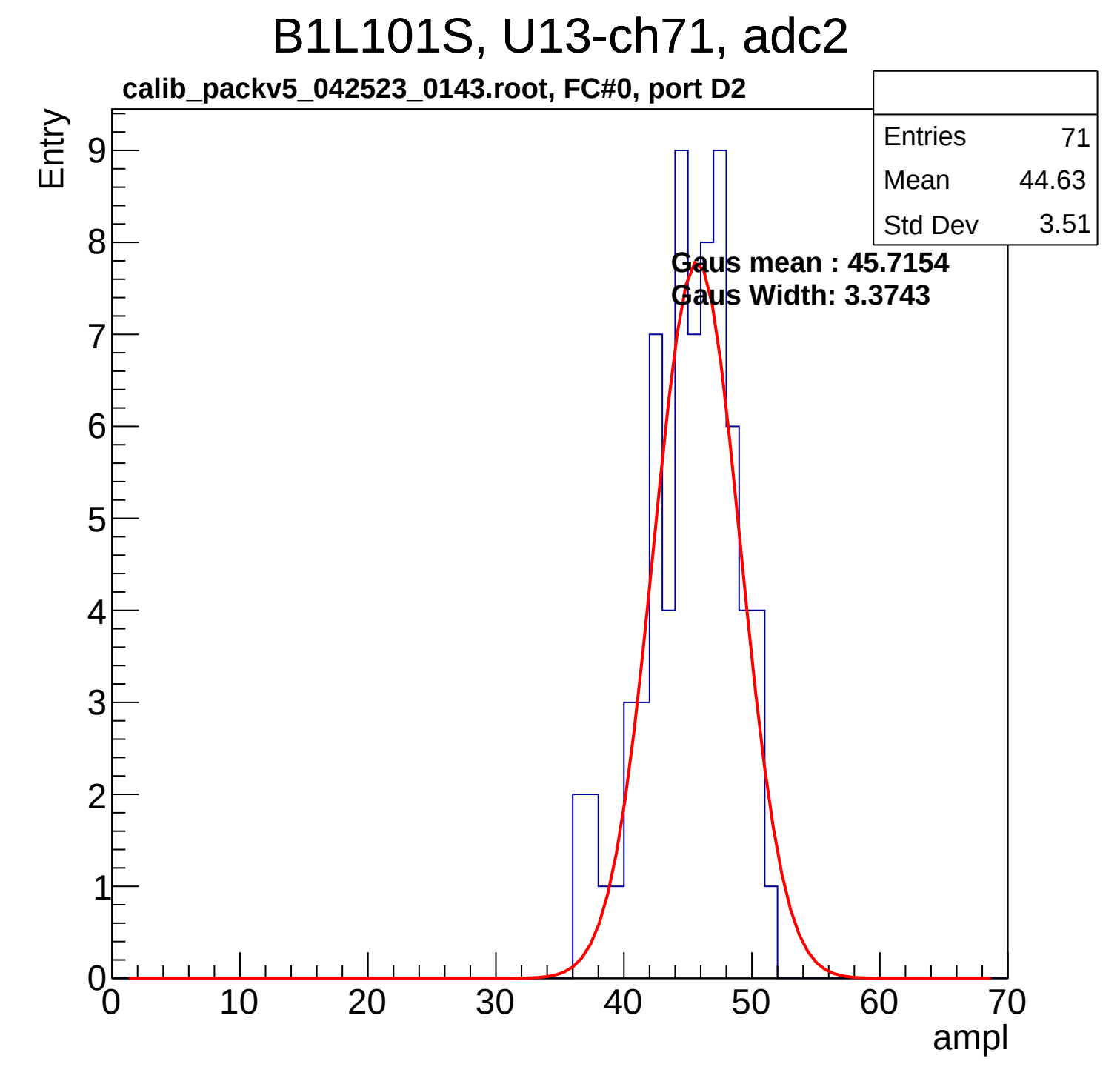
B1L101S, U13-ch71, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 44.63
Std Dev 3.51
Gaus mean : 45.7154
Gaus Width: 3.3743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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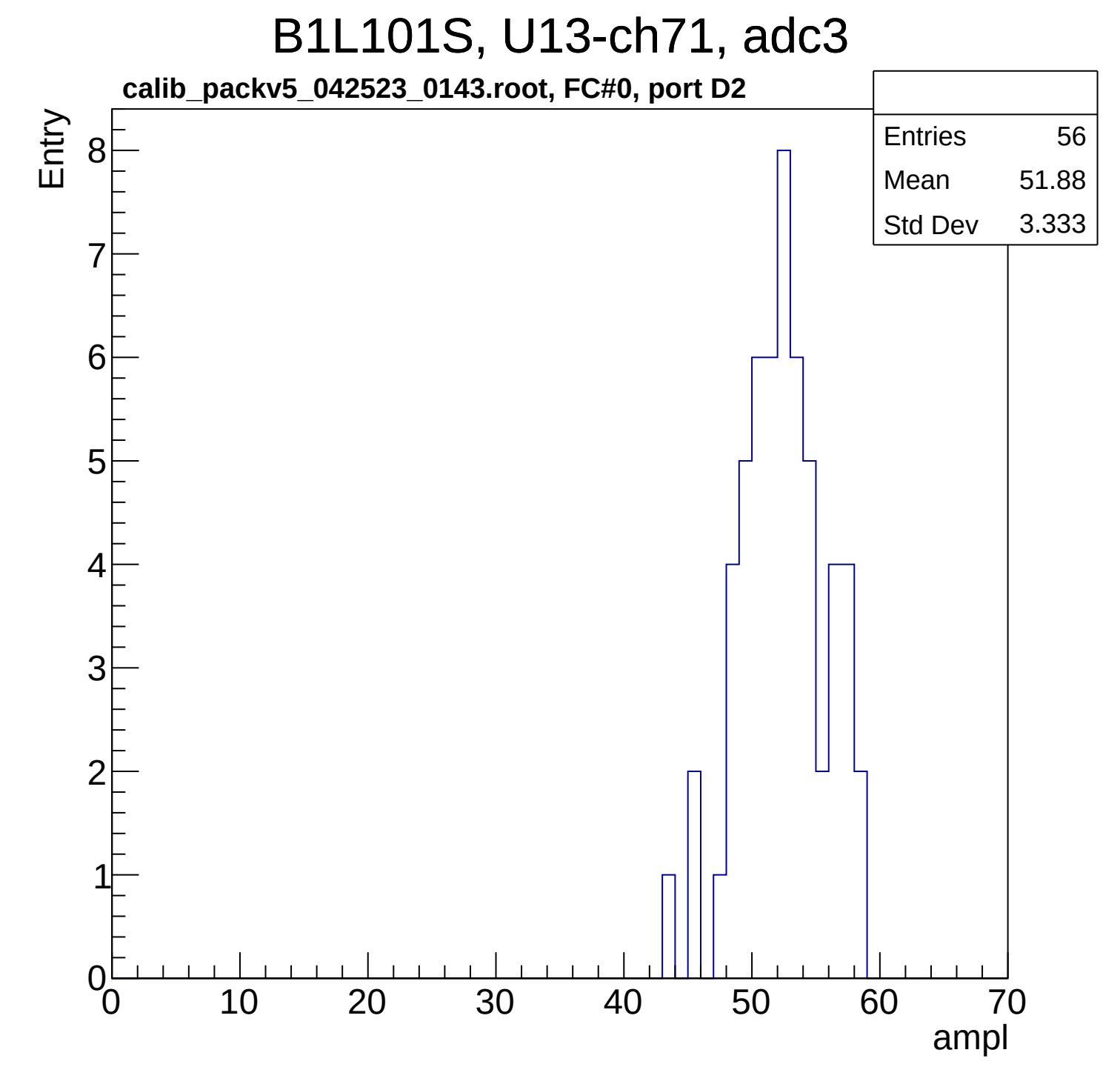
B1L101S, U13-ch71, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 51.88
Std Dev 3.333
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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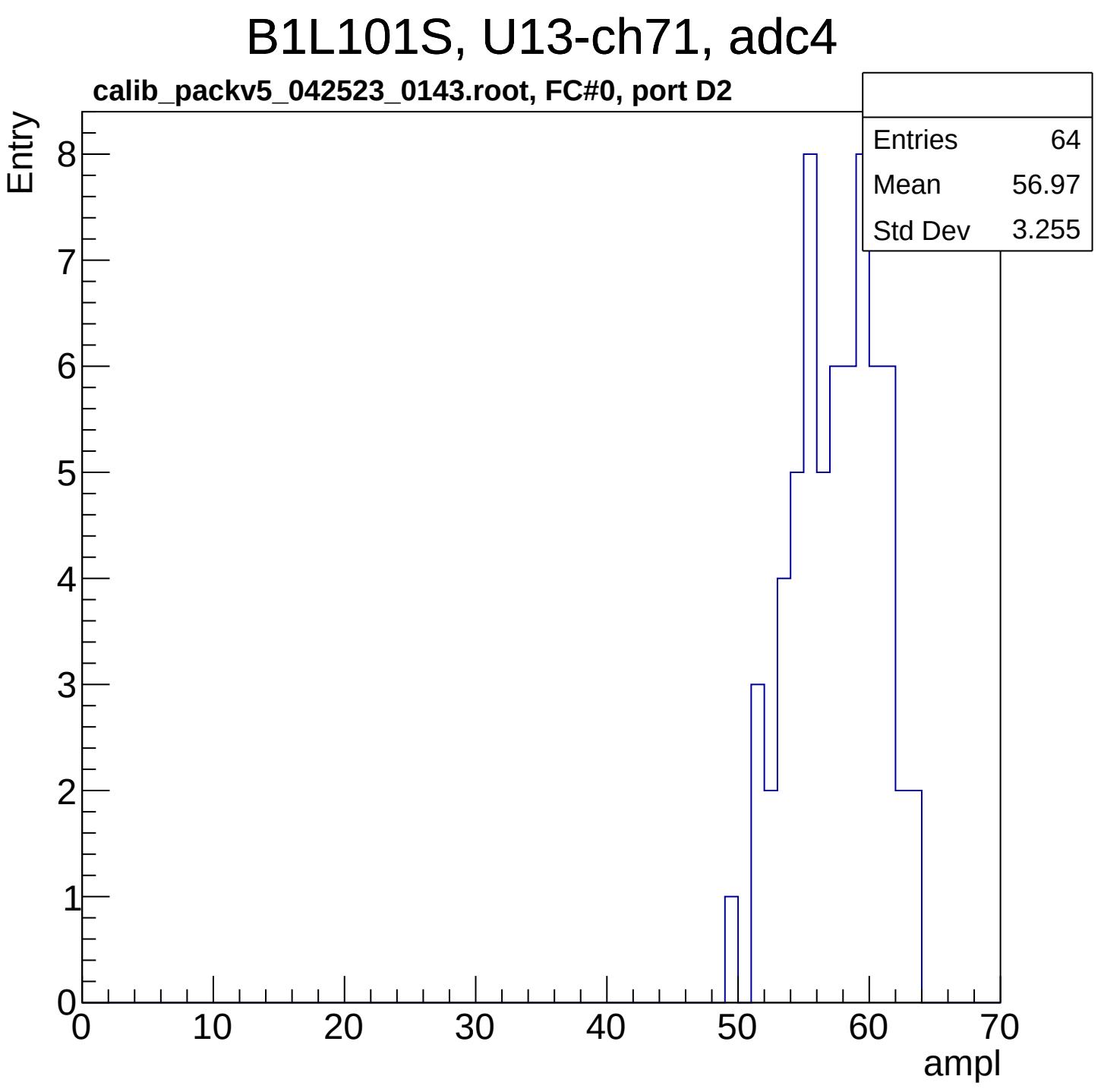
B1L101S, U13-ch71, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 56.97
Std Dev 3.255
Entry
ampl
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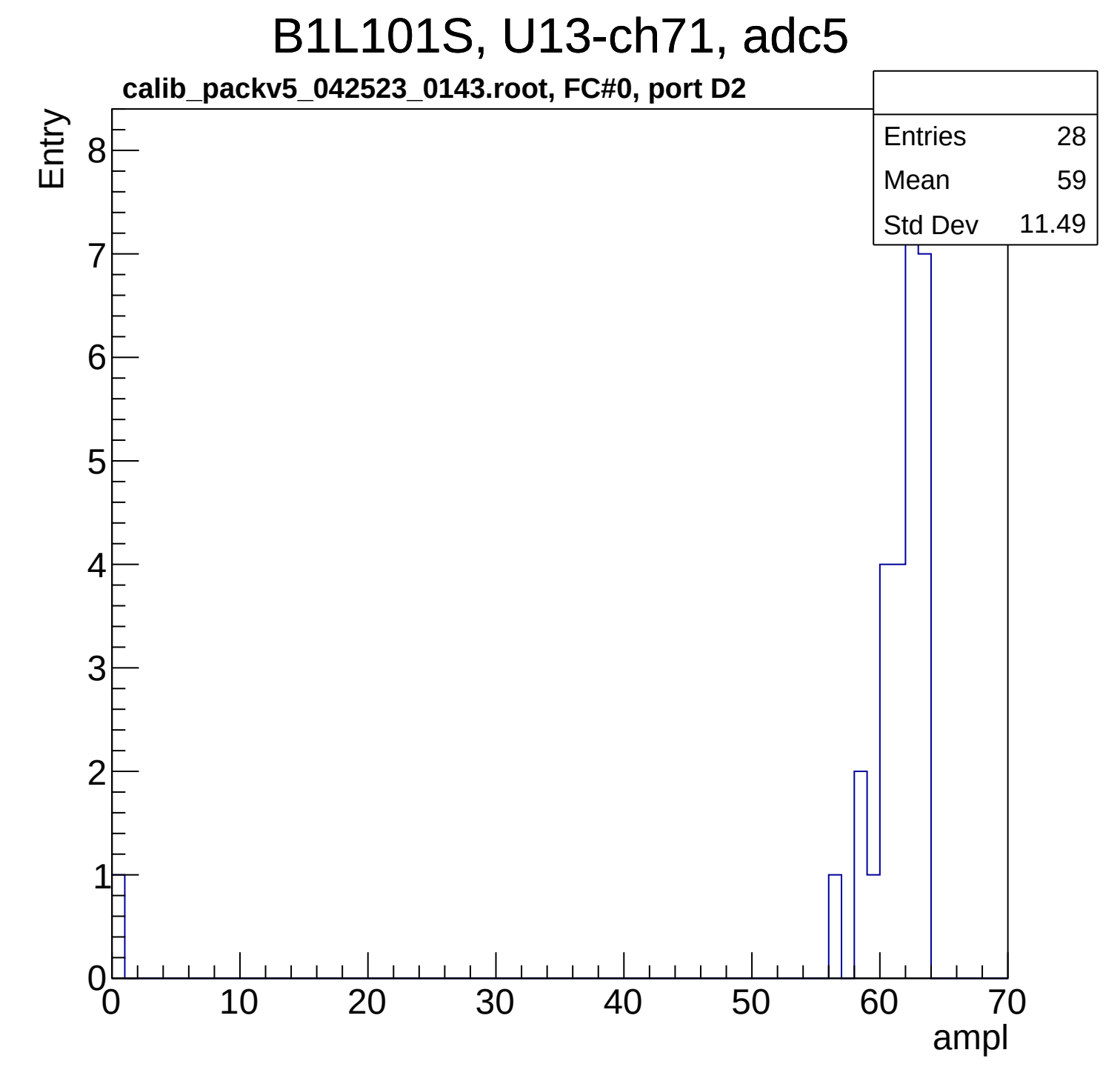
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch71, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 28
Mean 59
Std Dev 11.49
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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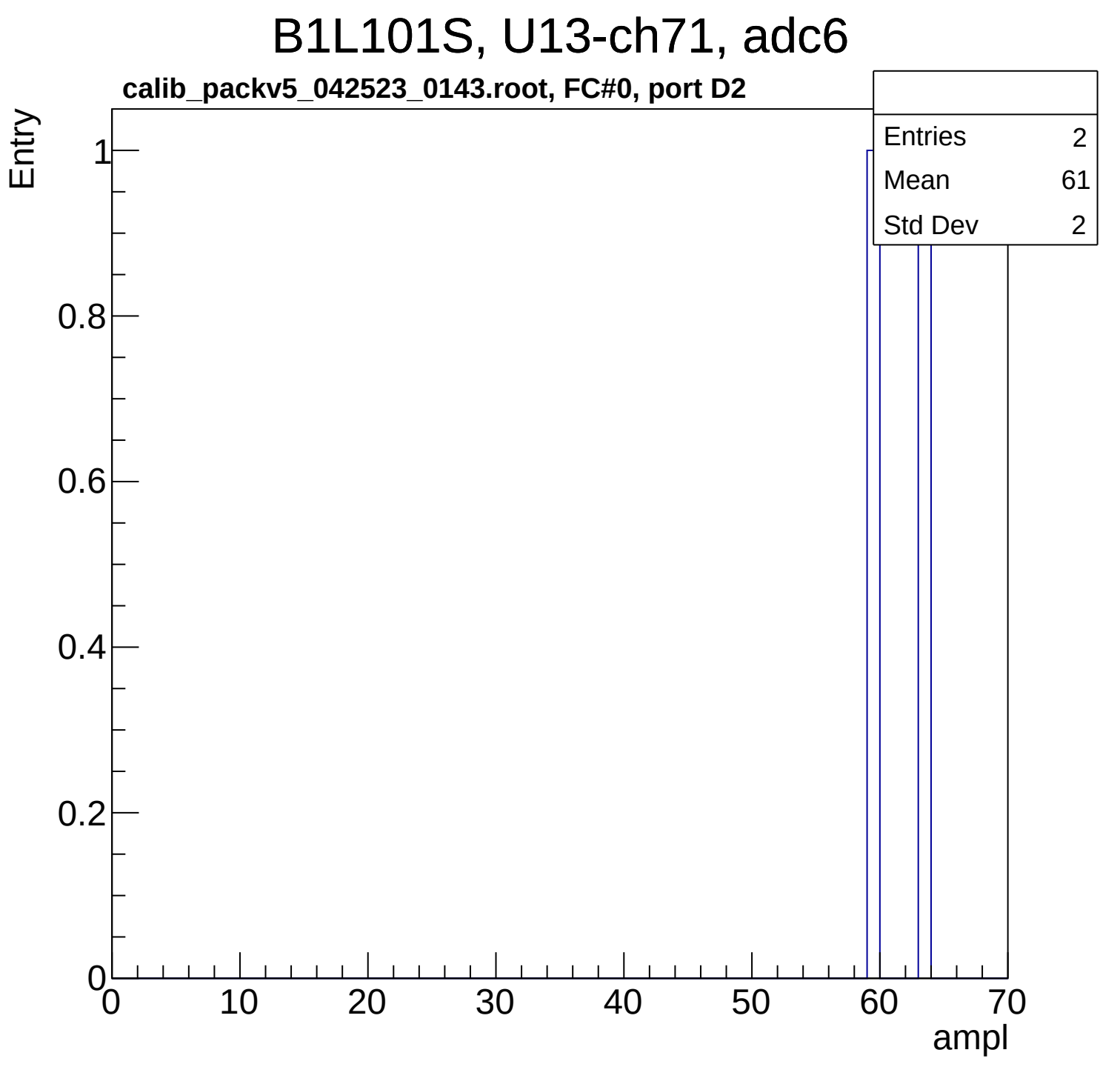
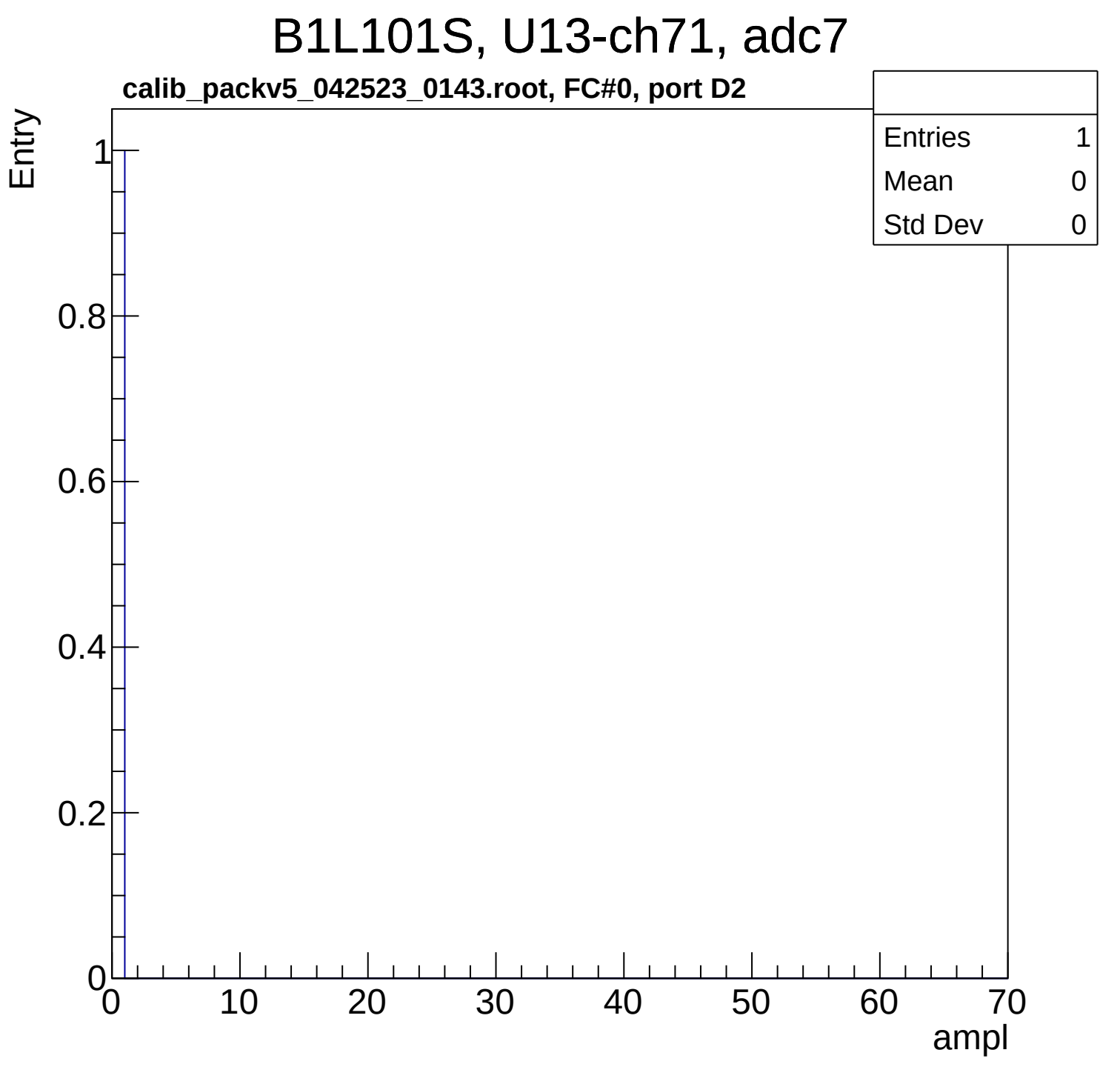

B1L101S, U13-ch71, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch71, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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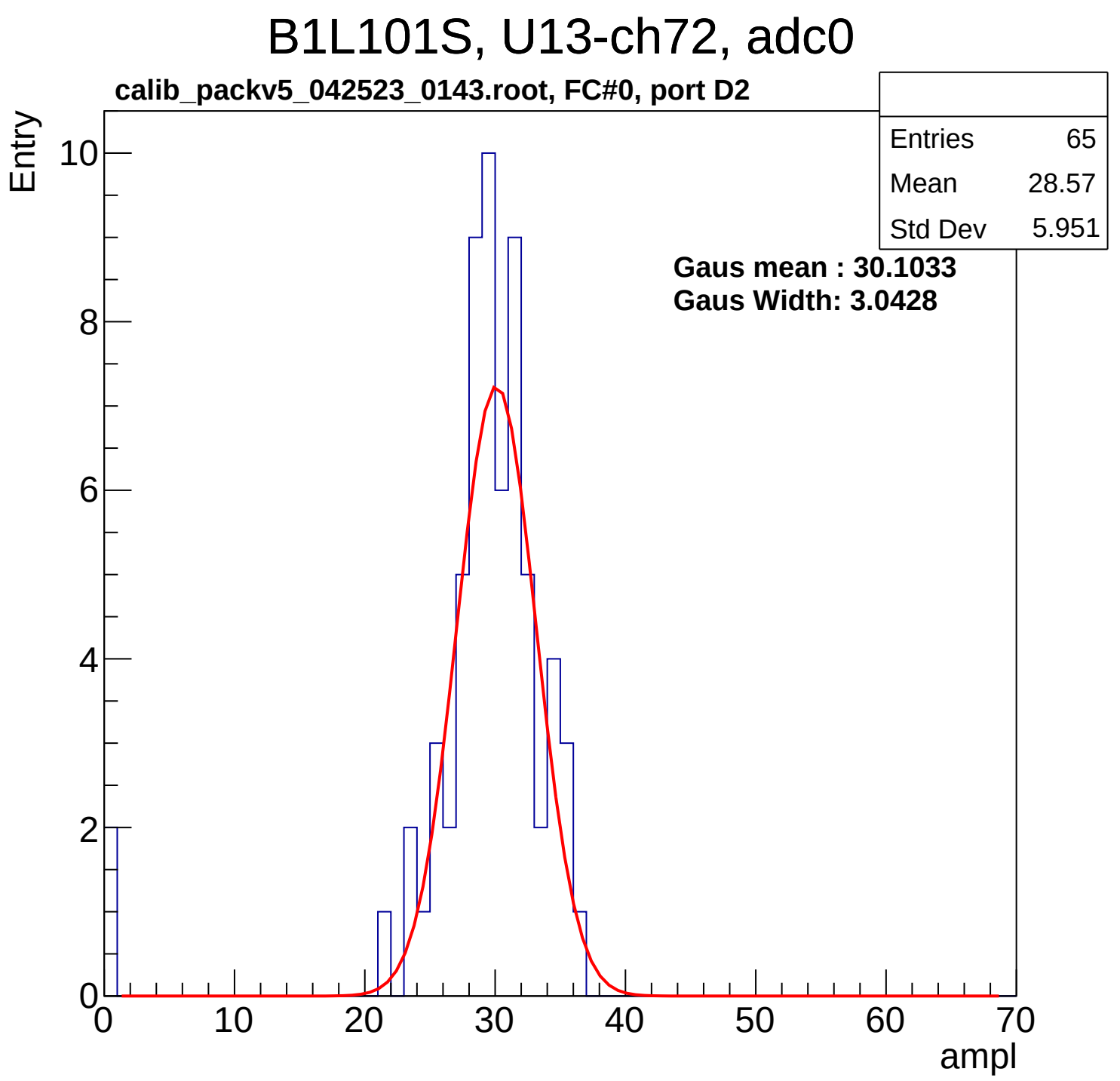
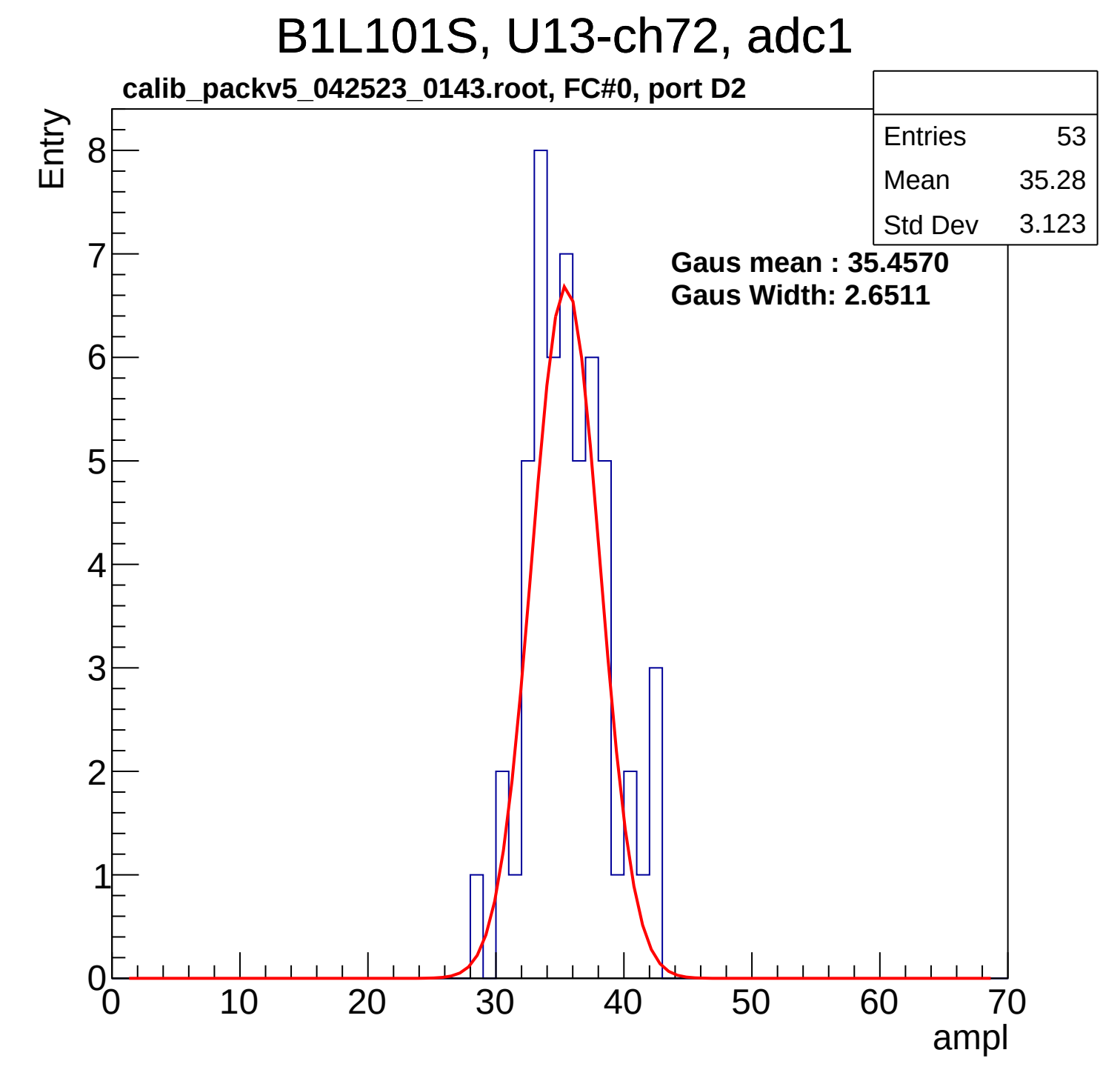
50
60
70
ampl


B1L101S, U13-ch72, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 28.57
Std Dev 5.951
Gaus mean : 30.1033
Gaus Width: 3.0428
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch72, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 35.28
Std Dev 3.123
Gaus mean : 35.4570
Gaus Width: 2.6511
Entry
ampl
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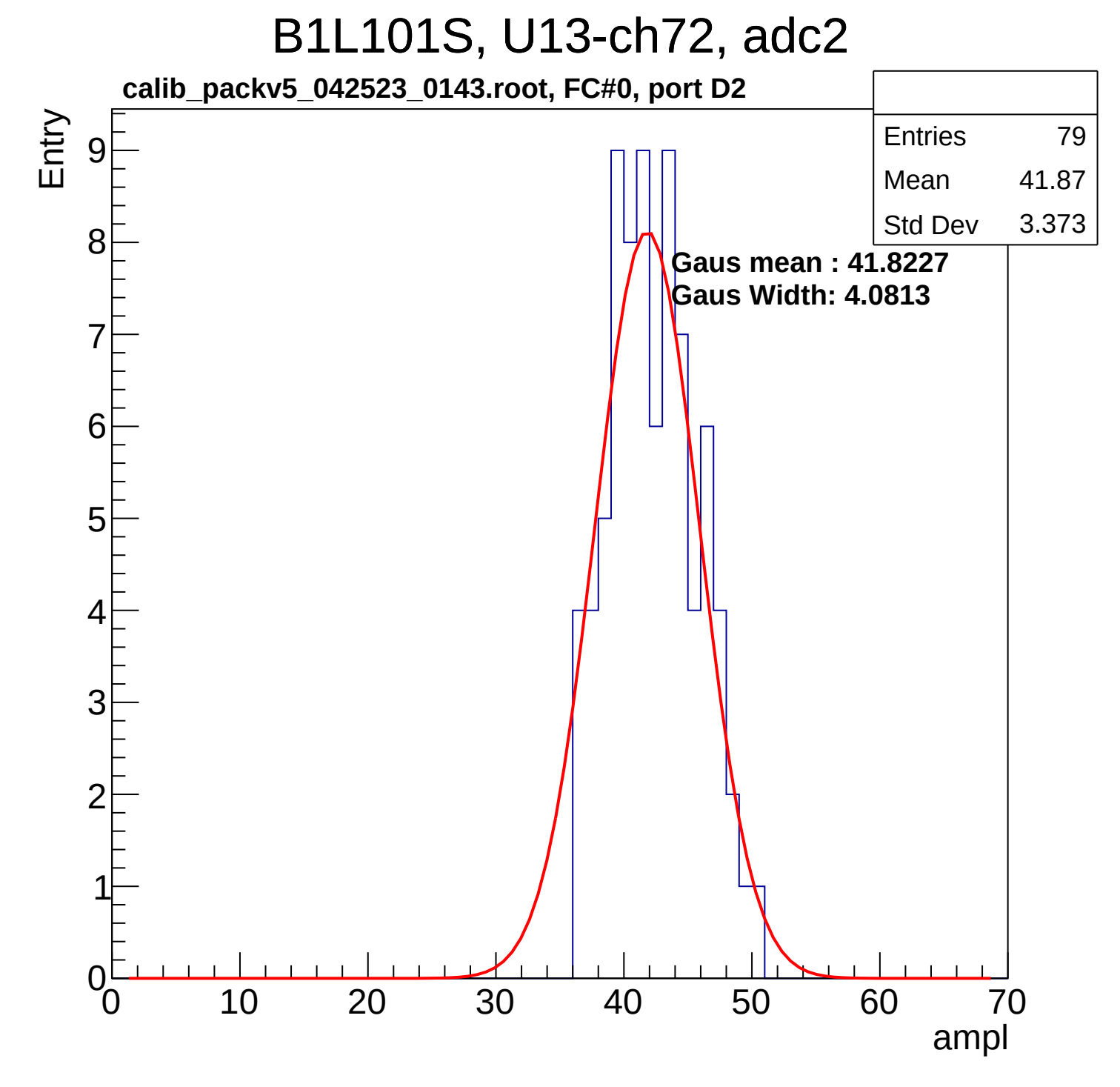
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch72, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 79
Mean 41.87
Std Dev 3.373
Gaus mean : 41.8227
Gaus Width: 4.0813
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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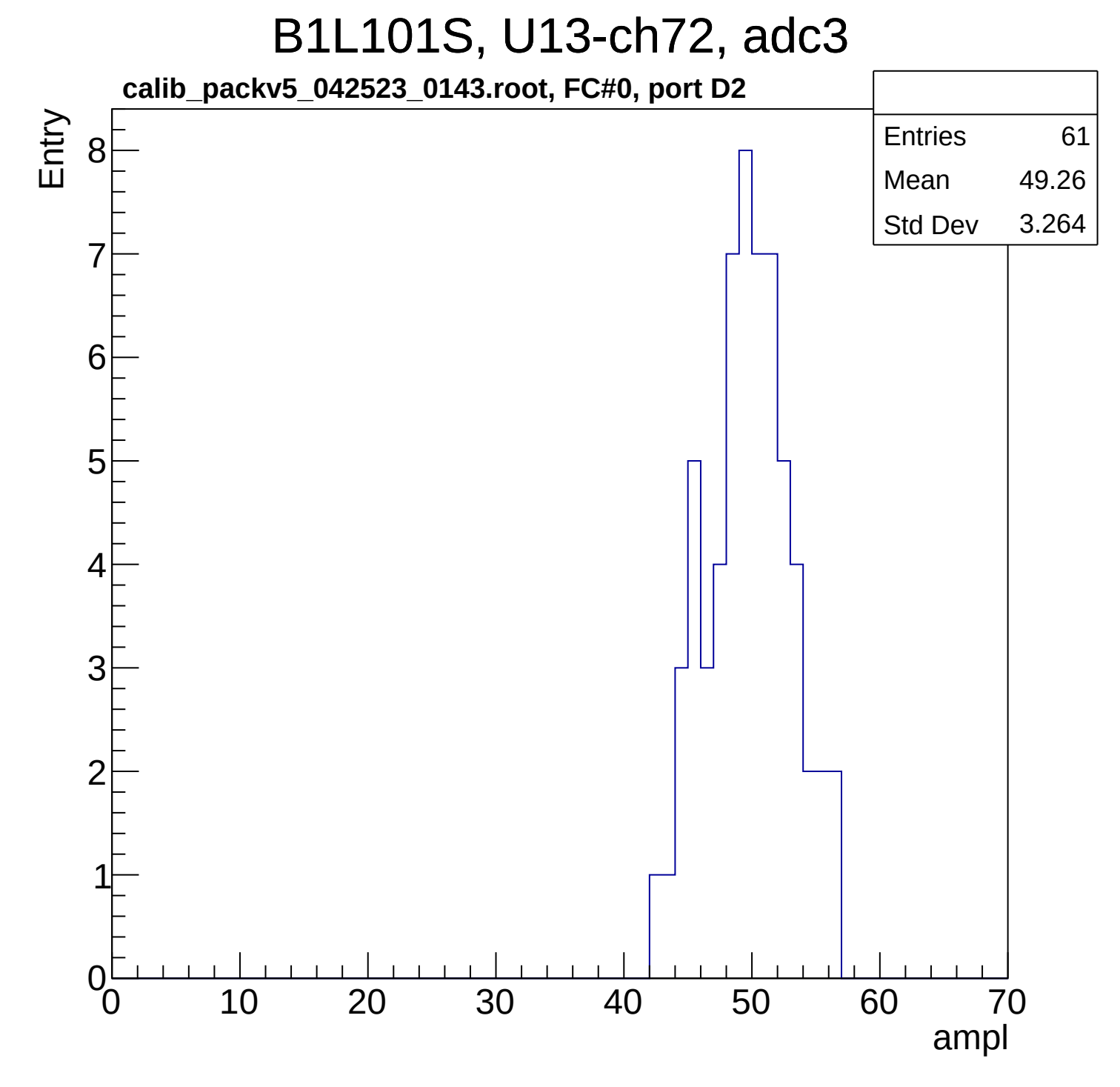
B1L101S, U13-ch72, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 49.26
Std Dev 3.264
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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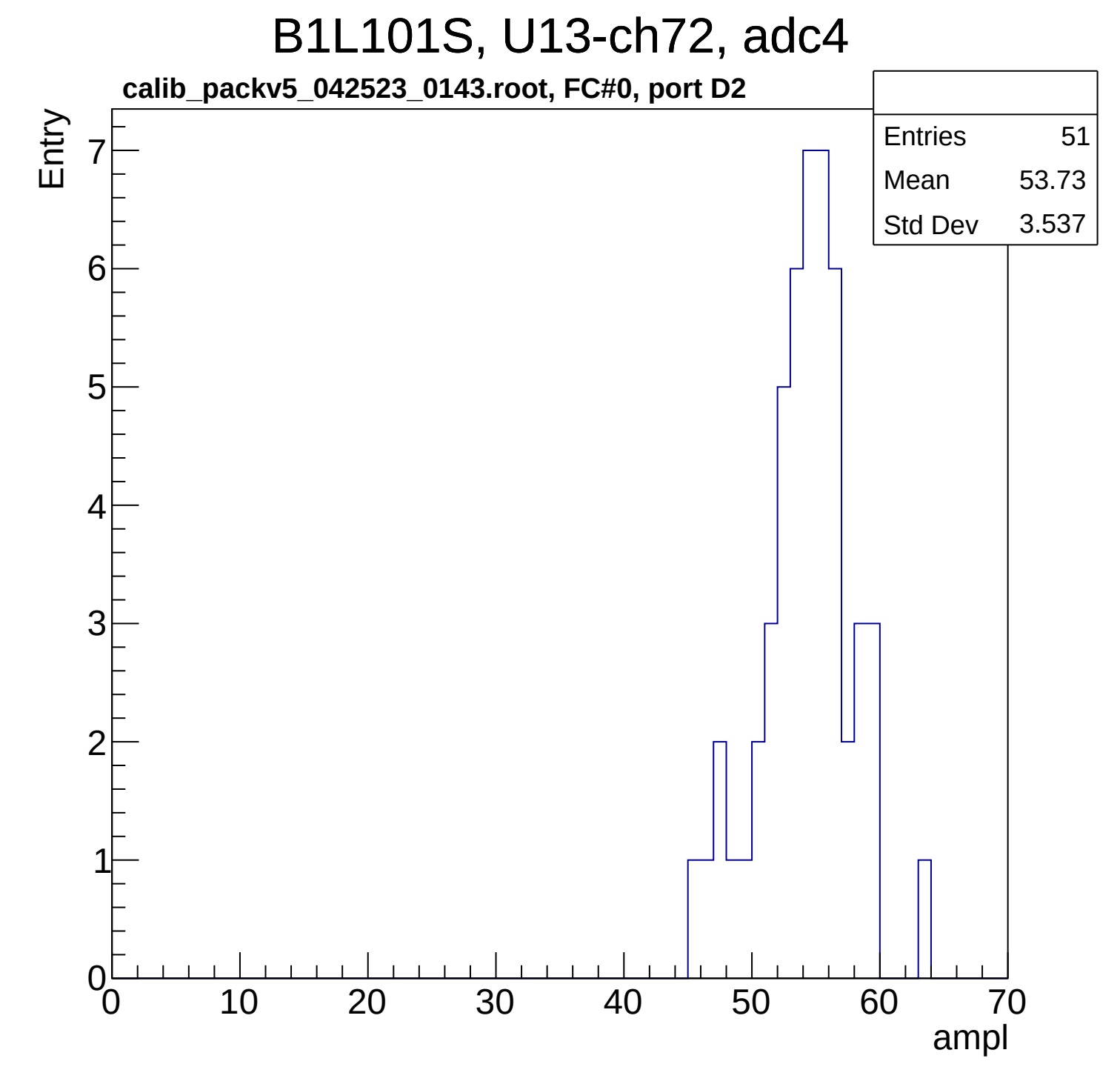
B1L101S, U13-ch72, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 53.73
Std Dev 3.537
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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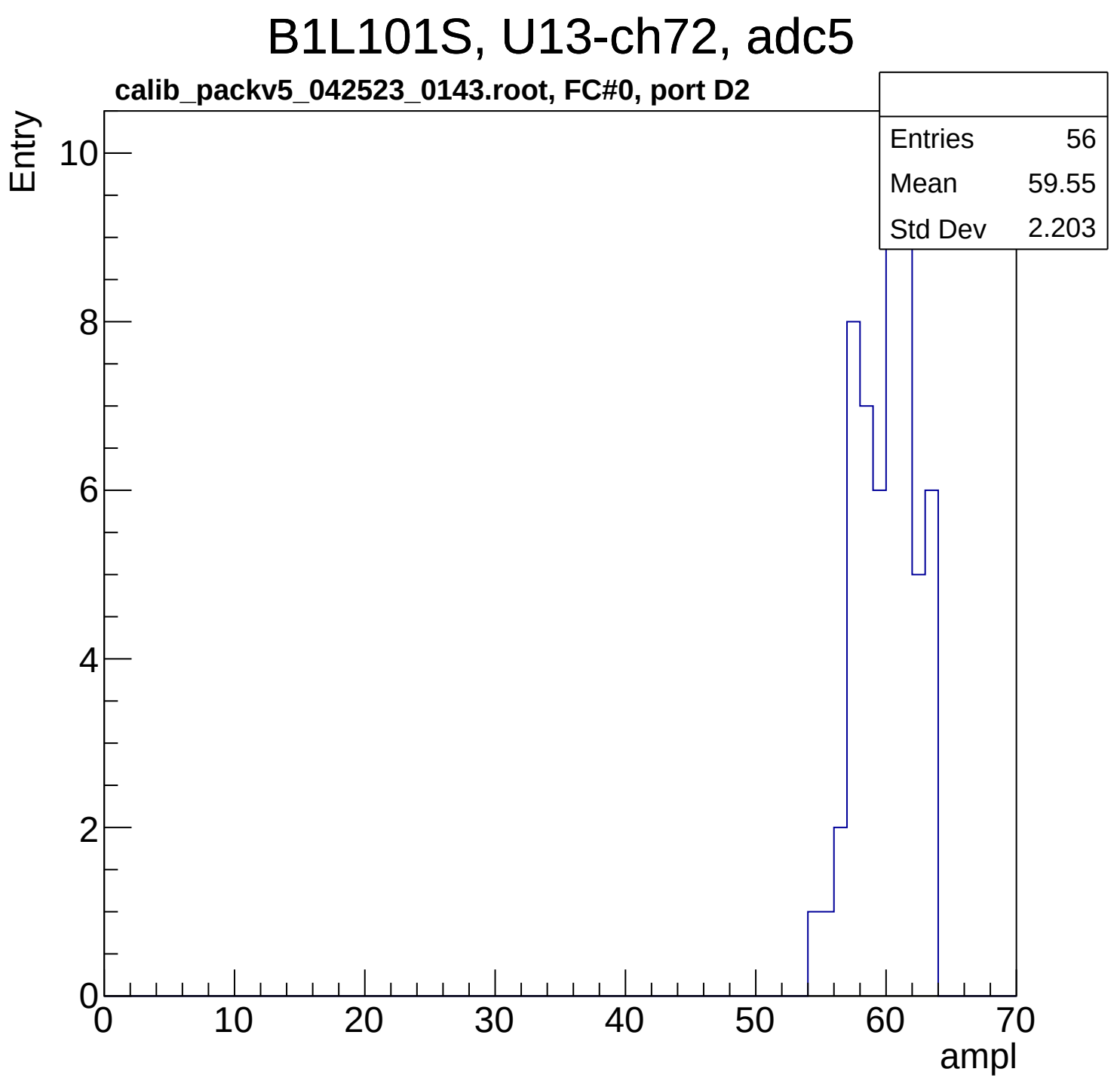
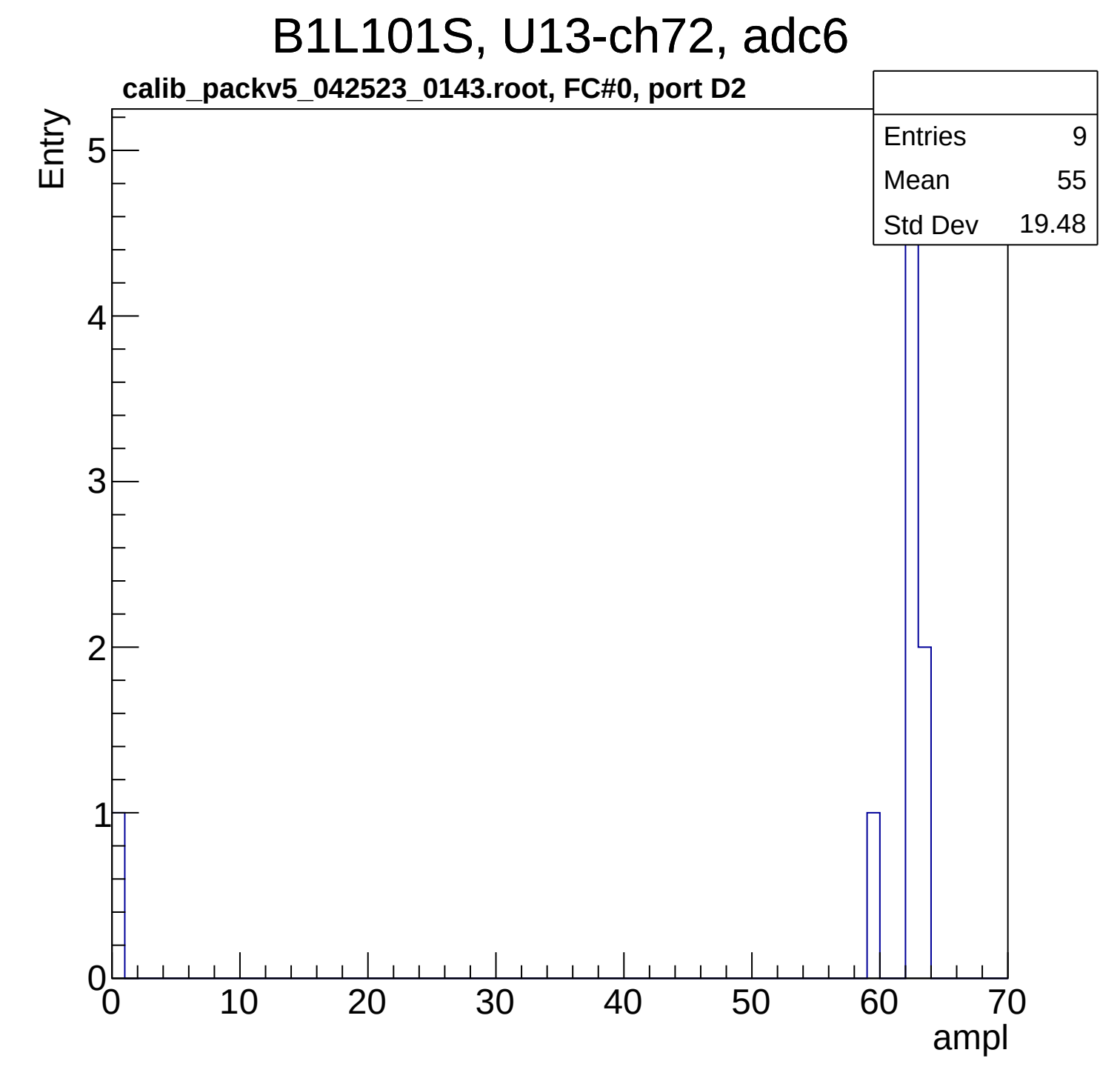
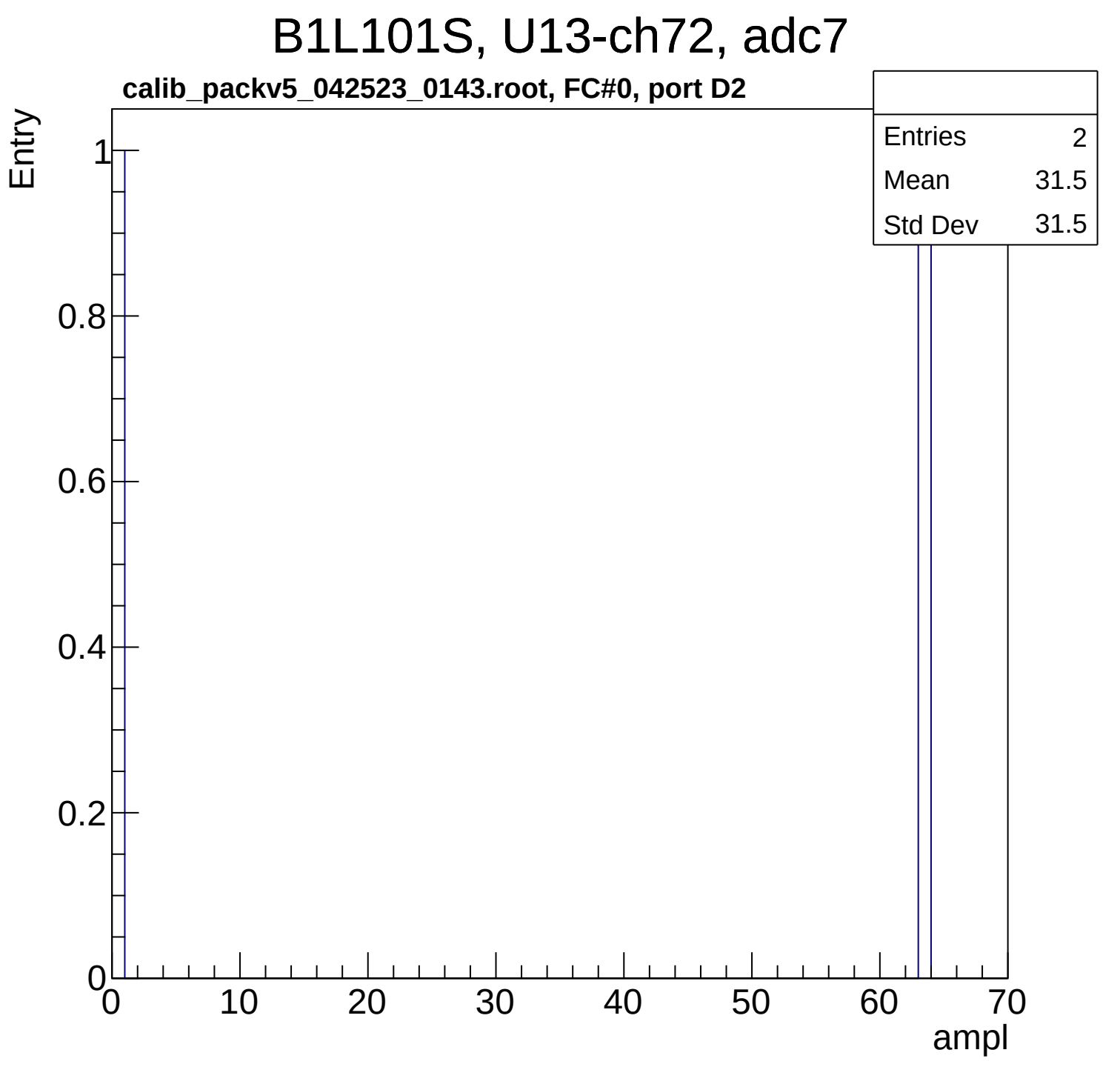

B1L101S, U13-ch72, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 59.55
Std Dev 2.203
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch72, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 9
Mean 55
Std Dev 19.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch72, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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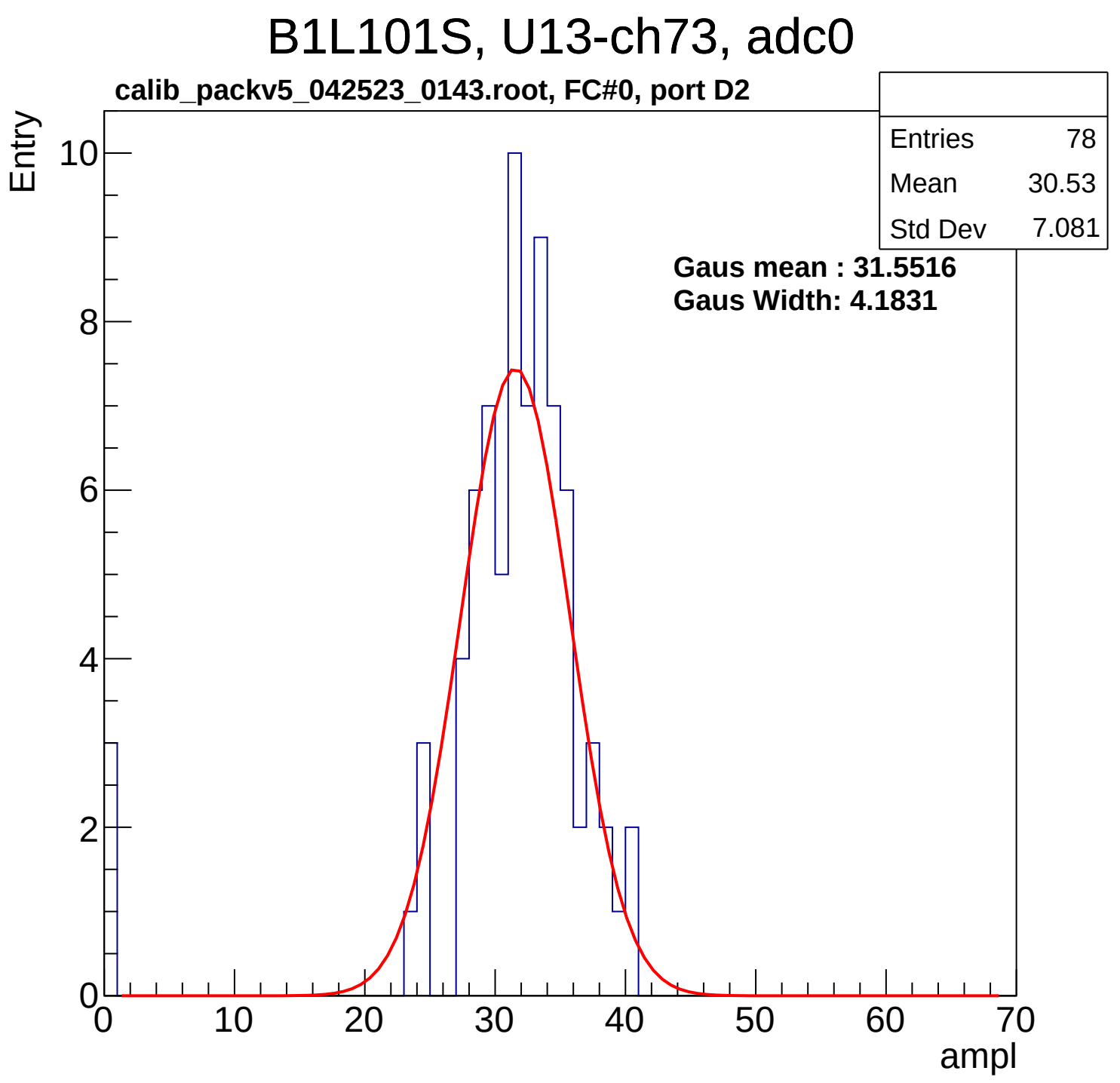
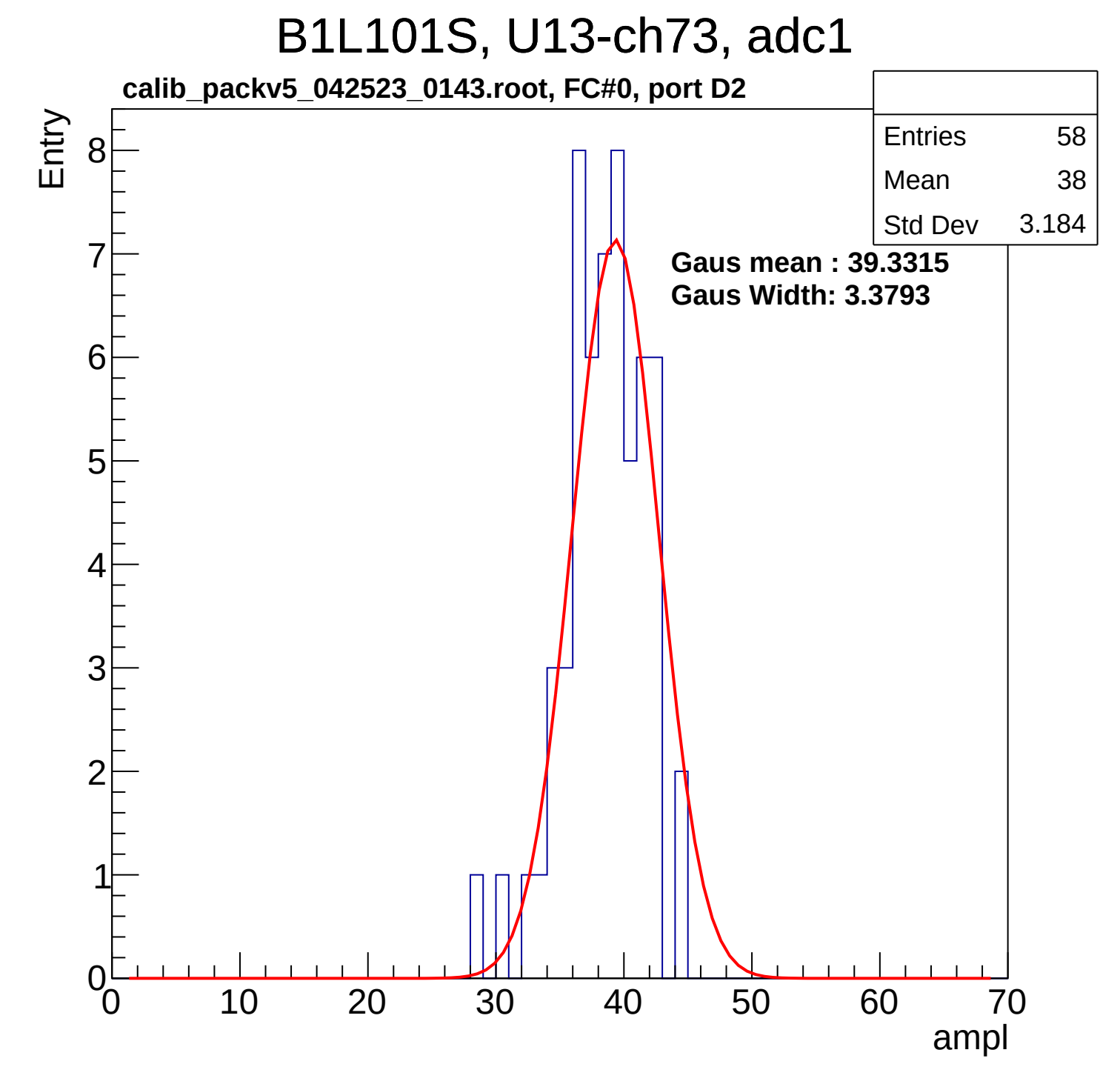
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch73, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 30.53
Std Dev 7.081
Gaus mean : 31.5516
Gaus Width: 4.1831
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch73, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 38
Std Dev 3.184
Gaus mean : 39.3315
Gaus Width: 3.3793
Entry
ampl
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2
3
4
5
6
7
8
0
10
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40
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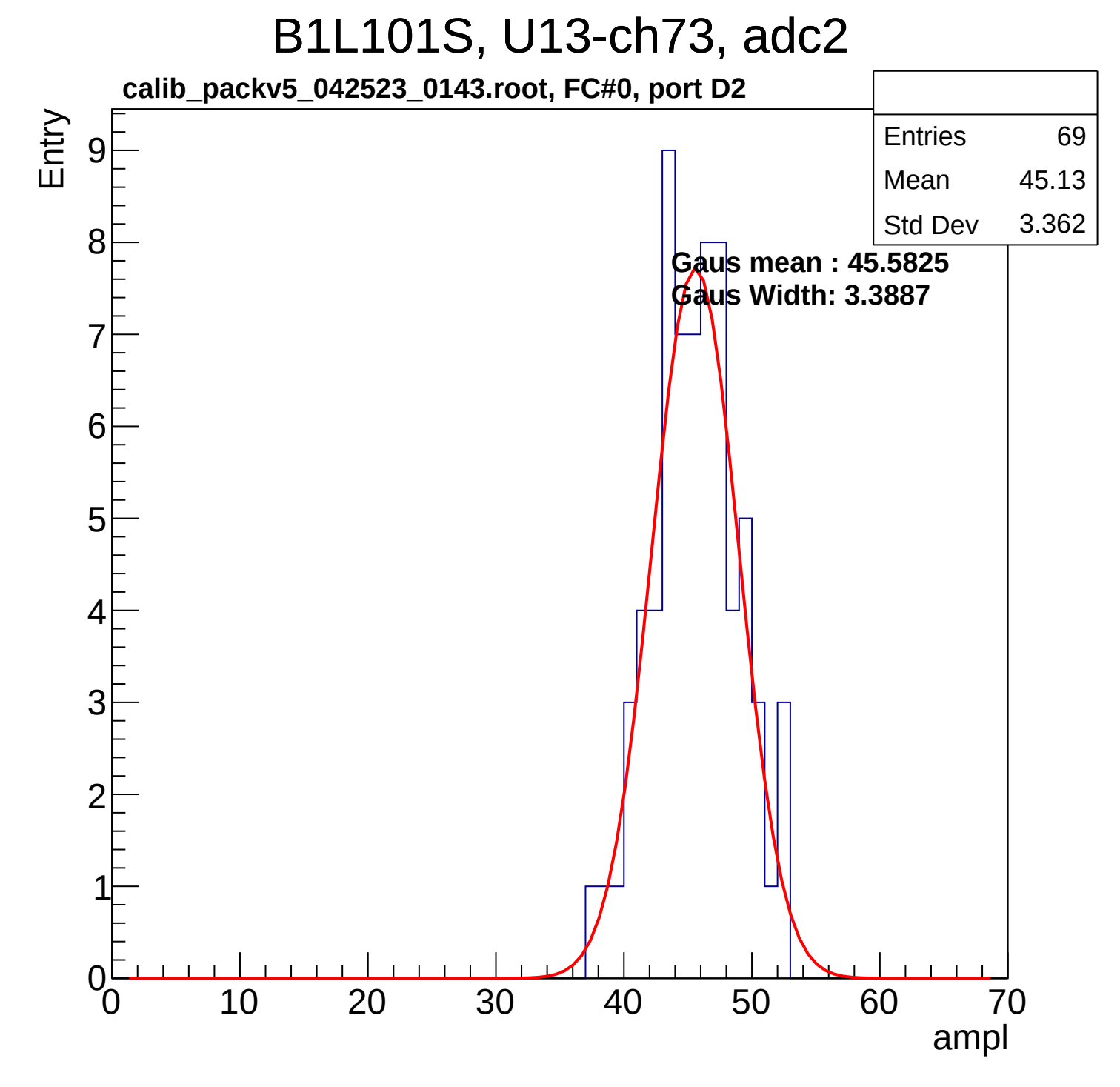
B1L101S, U13-ch73, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 45.13
Std Dev 3.362
Gaus mean : 45.5825
Gaus Width: 3.3887
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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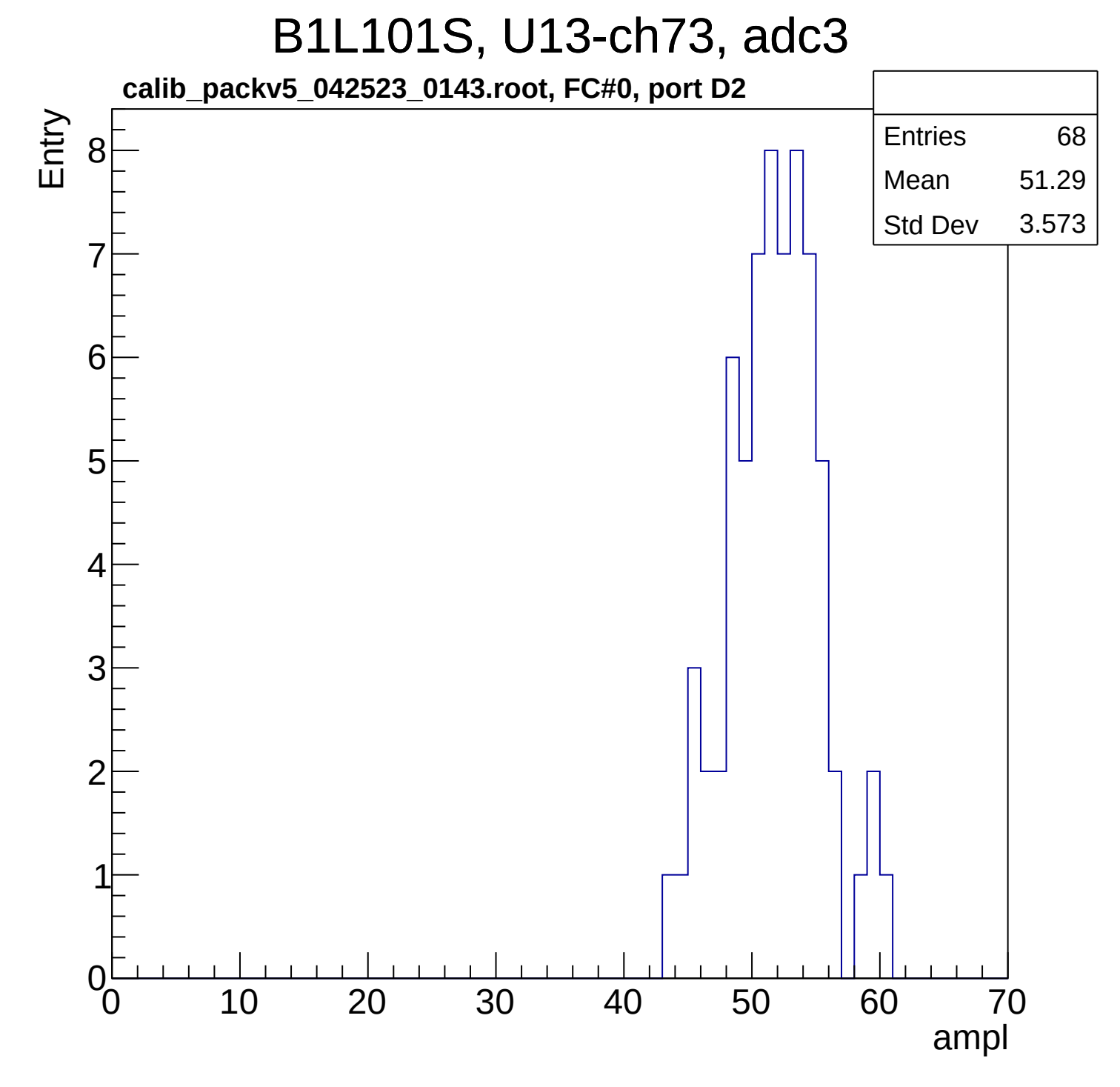
B1L101S, U13-ch73, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 51.29
Std Dev 3.573
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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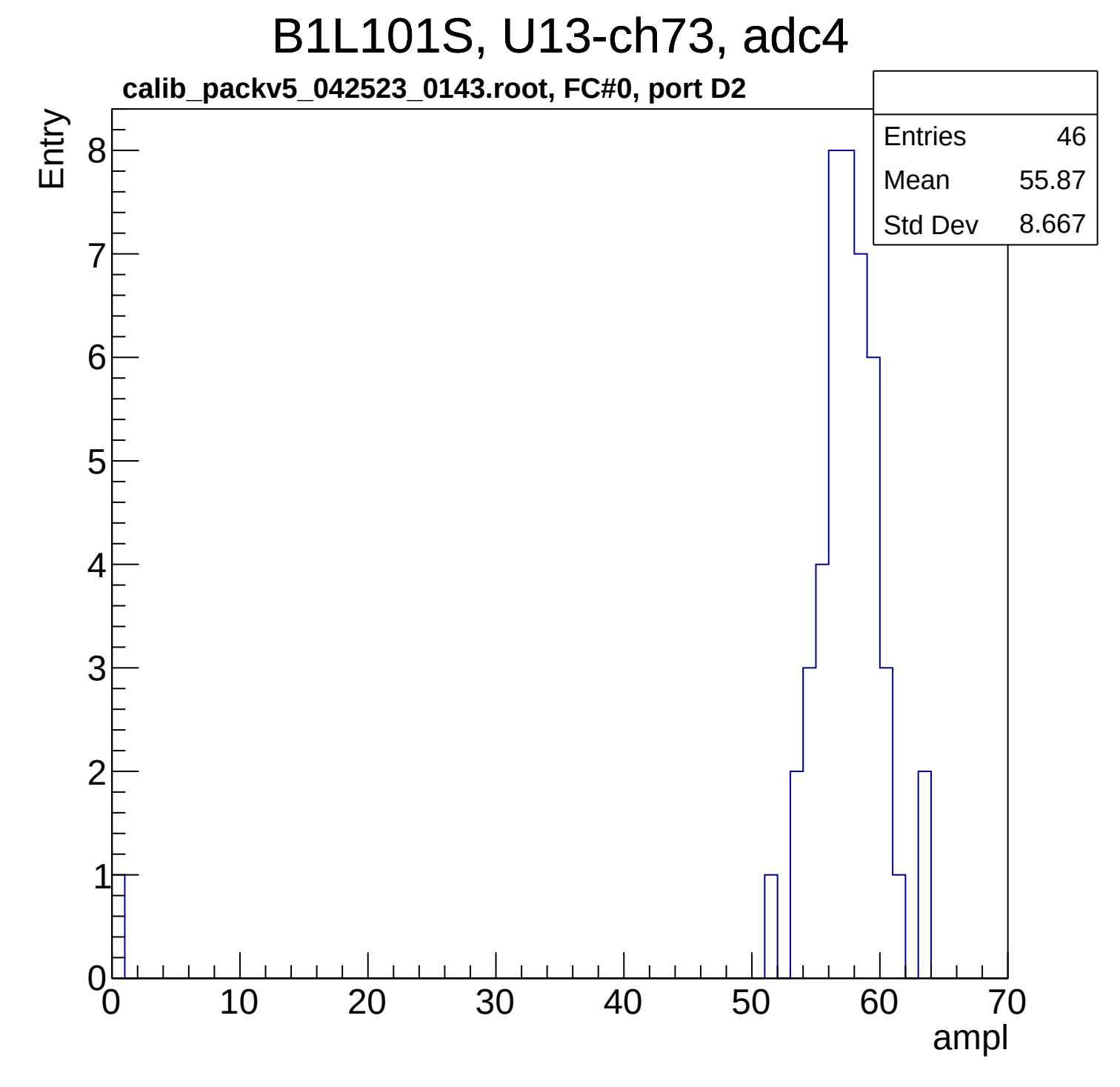
B1L101S, U13-ch73, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 55.87
Std Dev 8.667
Entry
ampl
0
1
2
3
4
5
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0
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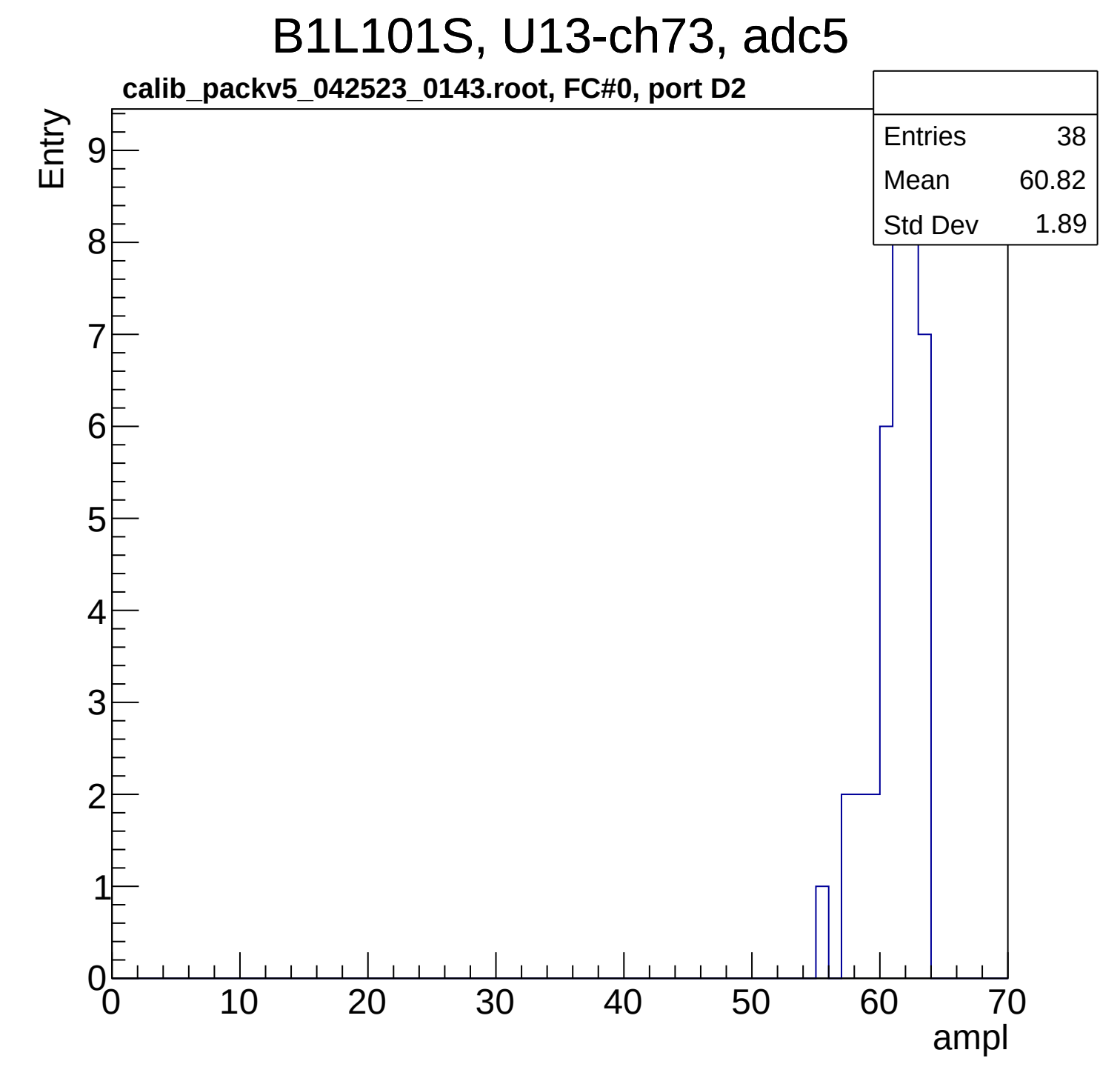
B1L101S, U13-ch73, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 38
Mean 60.82
Std Dev 1.89
Entry
ampl
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1
2
3
4
5
6
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0
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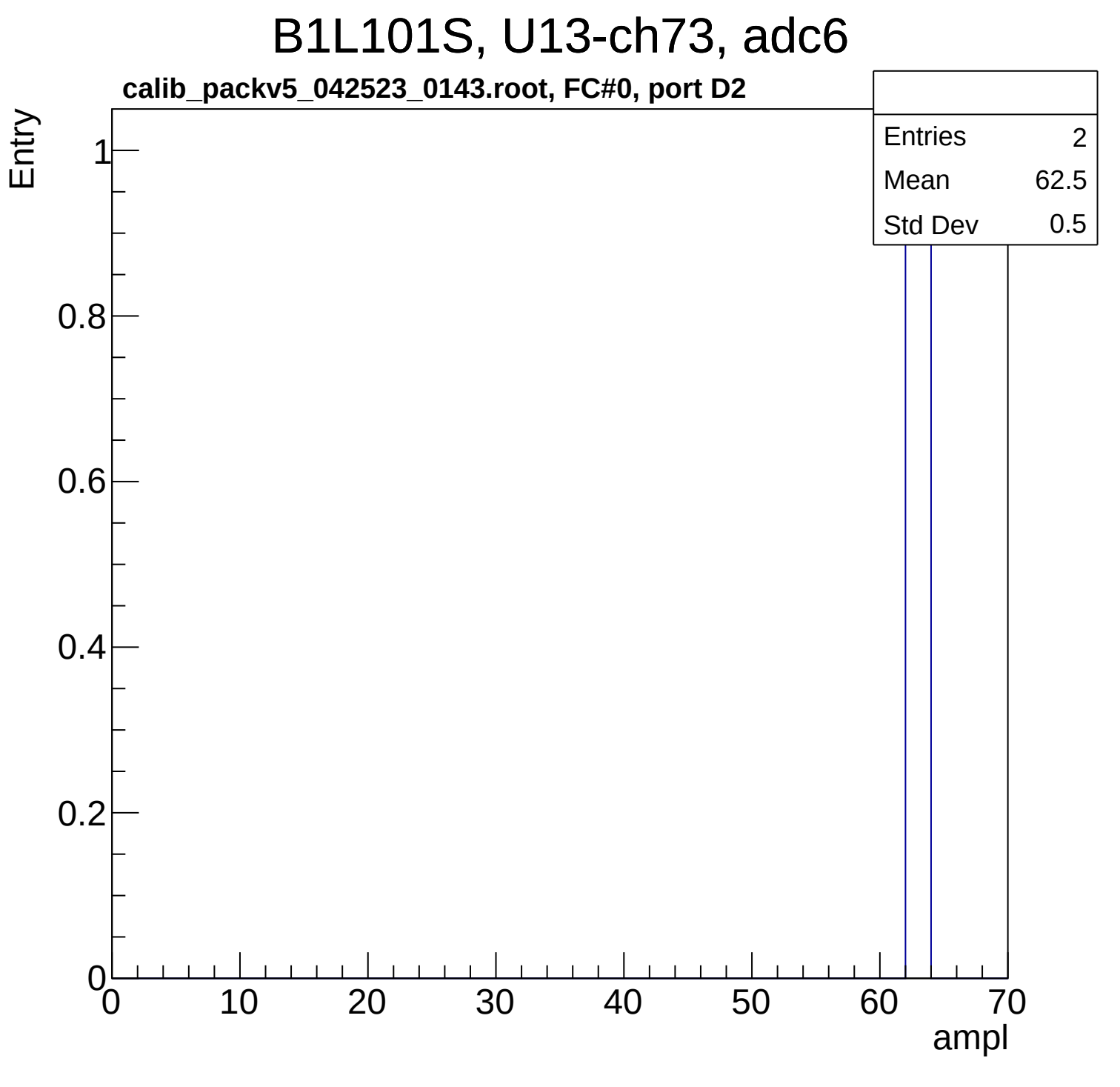
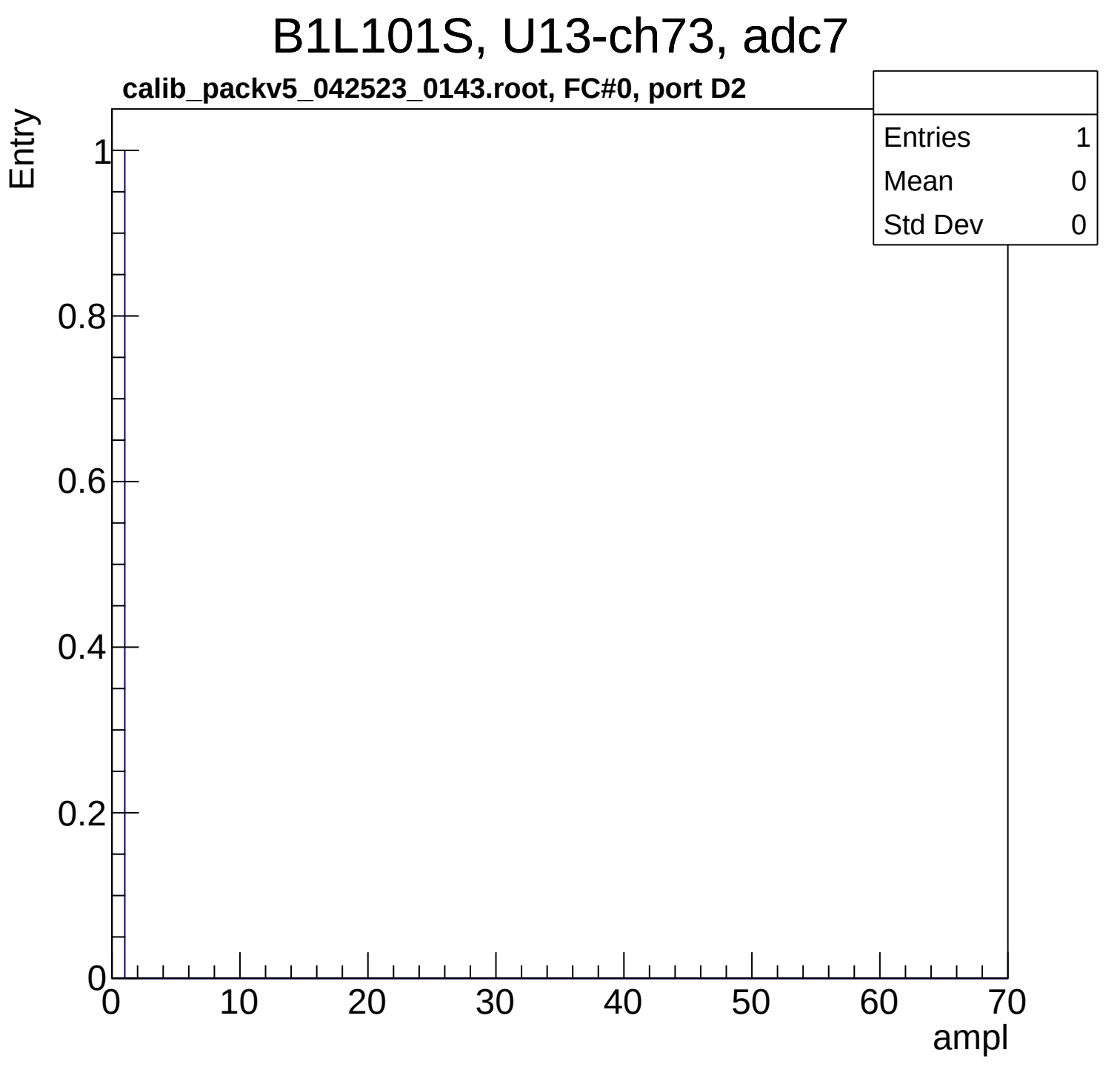

B1L101S, U13-ch73, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch73, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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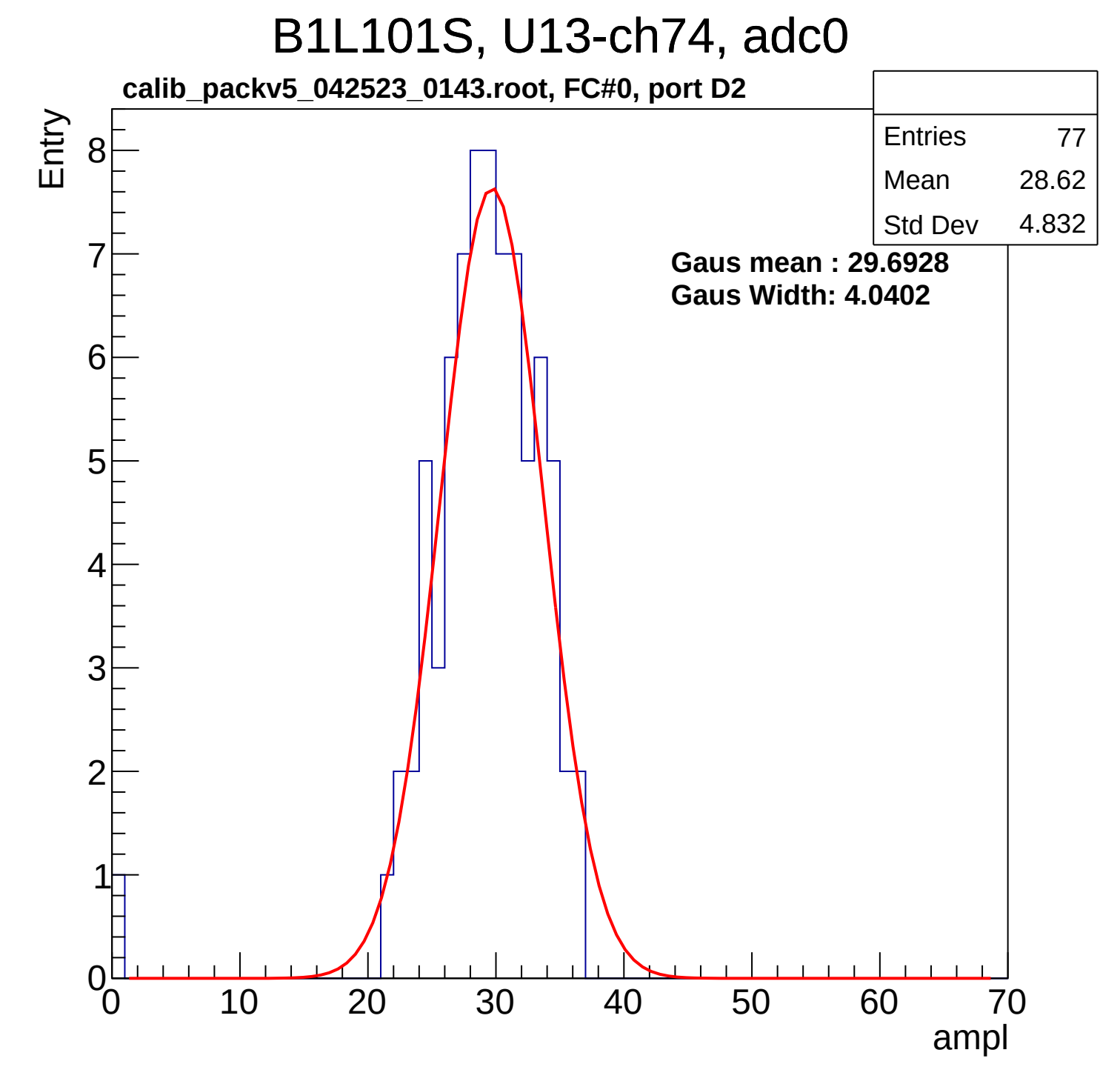
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch74, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 28.62
Std Dev 4.832
Gaus mean : 29.6928
Gaus Width: 4.0402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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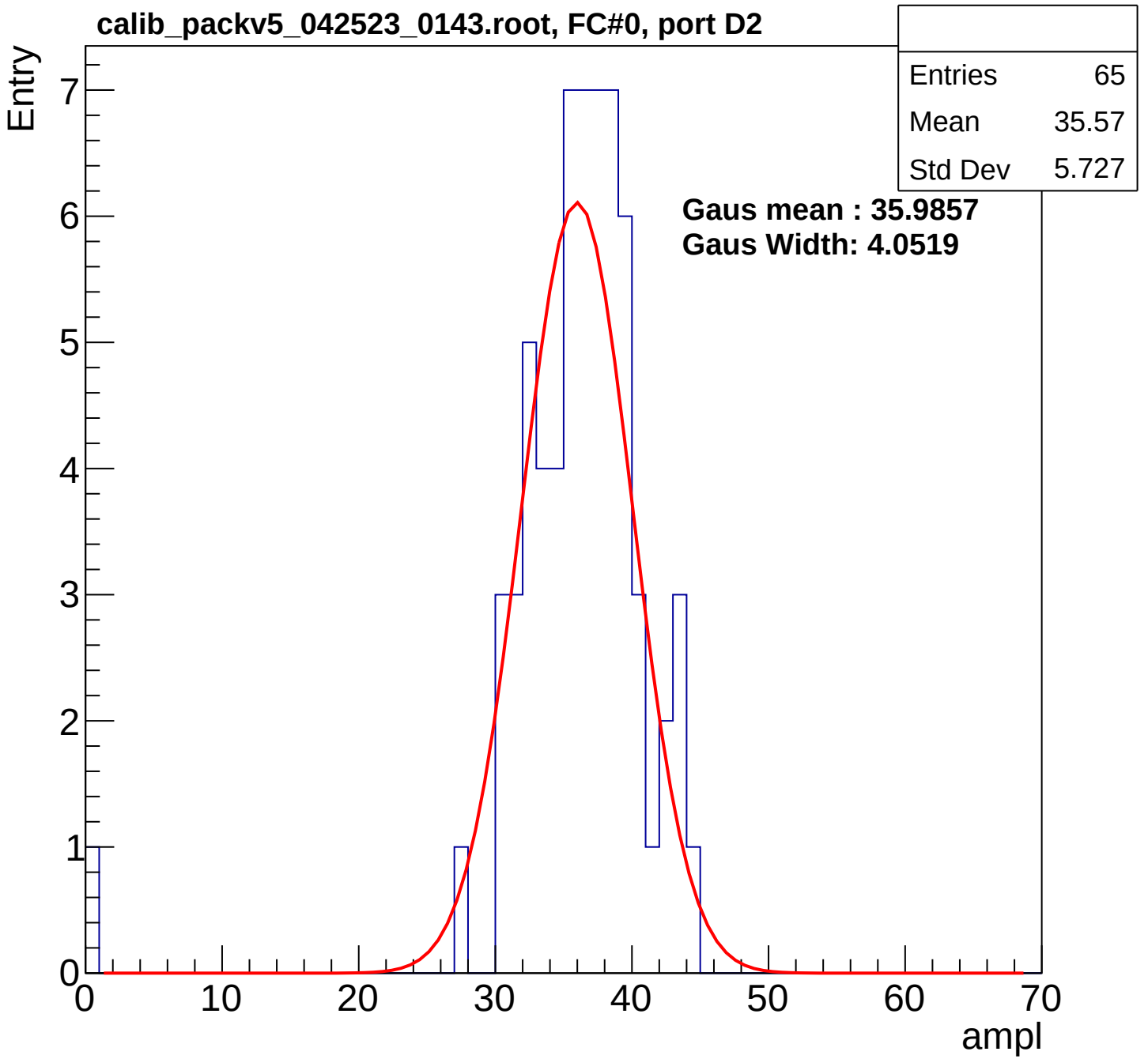
B1L101S, U13-ch74, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 35.57
Std Dev 5.727
Gaus mean : 35.9857
Gaus Width: 4.0519
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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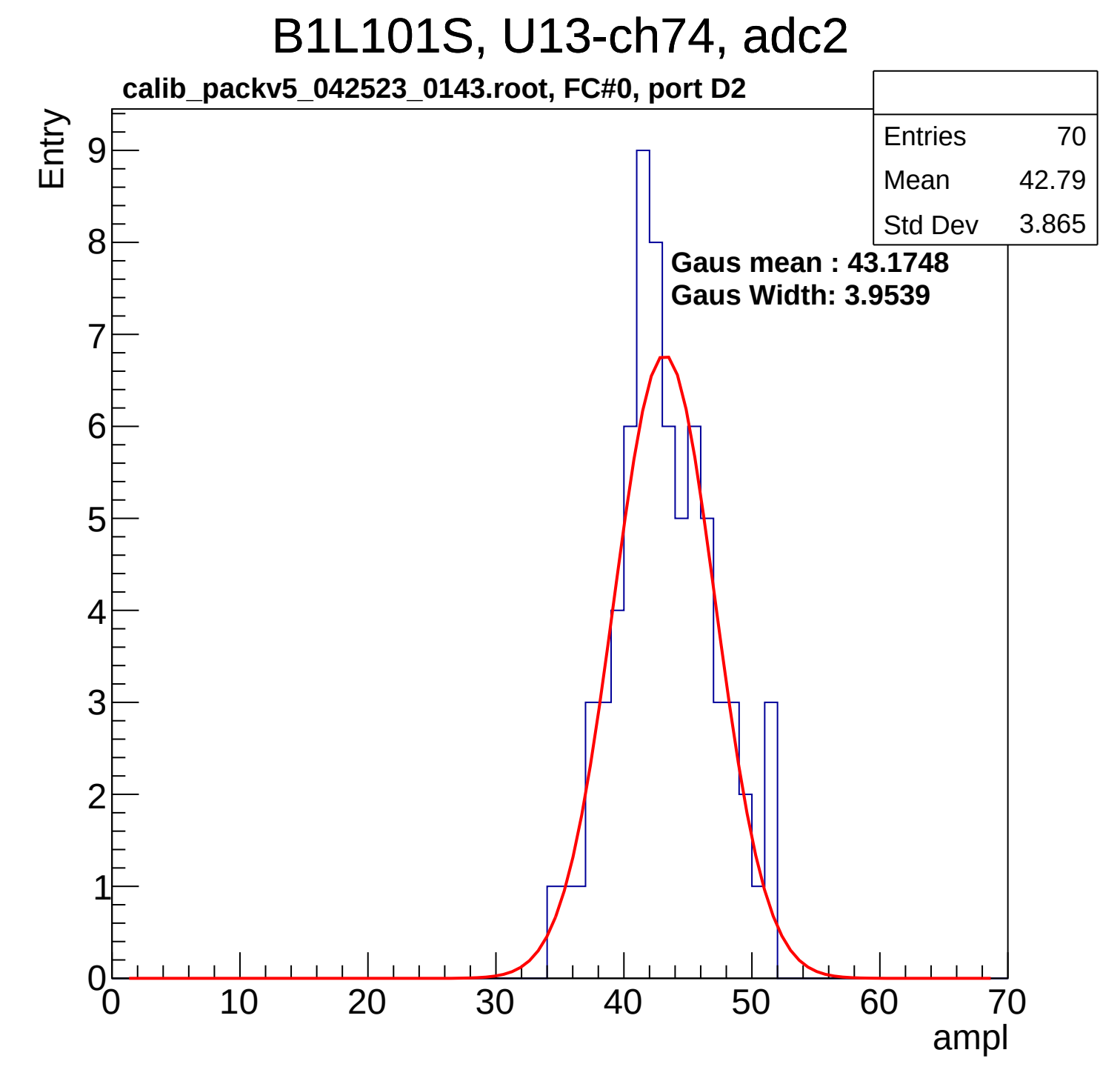
B1L101S, U13-ch74, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 42.79
Std Dev 3.865
Gaus mean : 43.1748
Gaus Width: 3.9539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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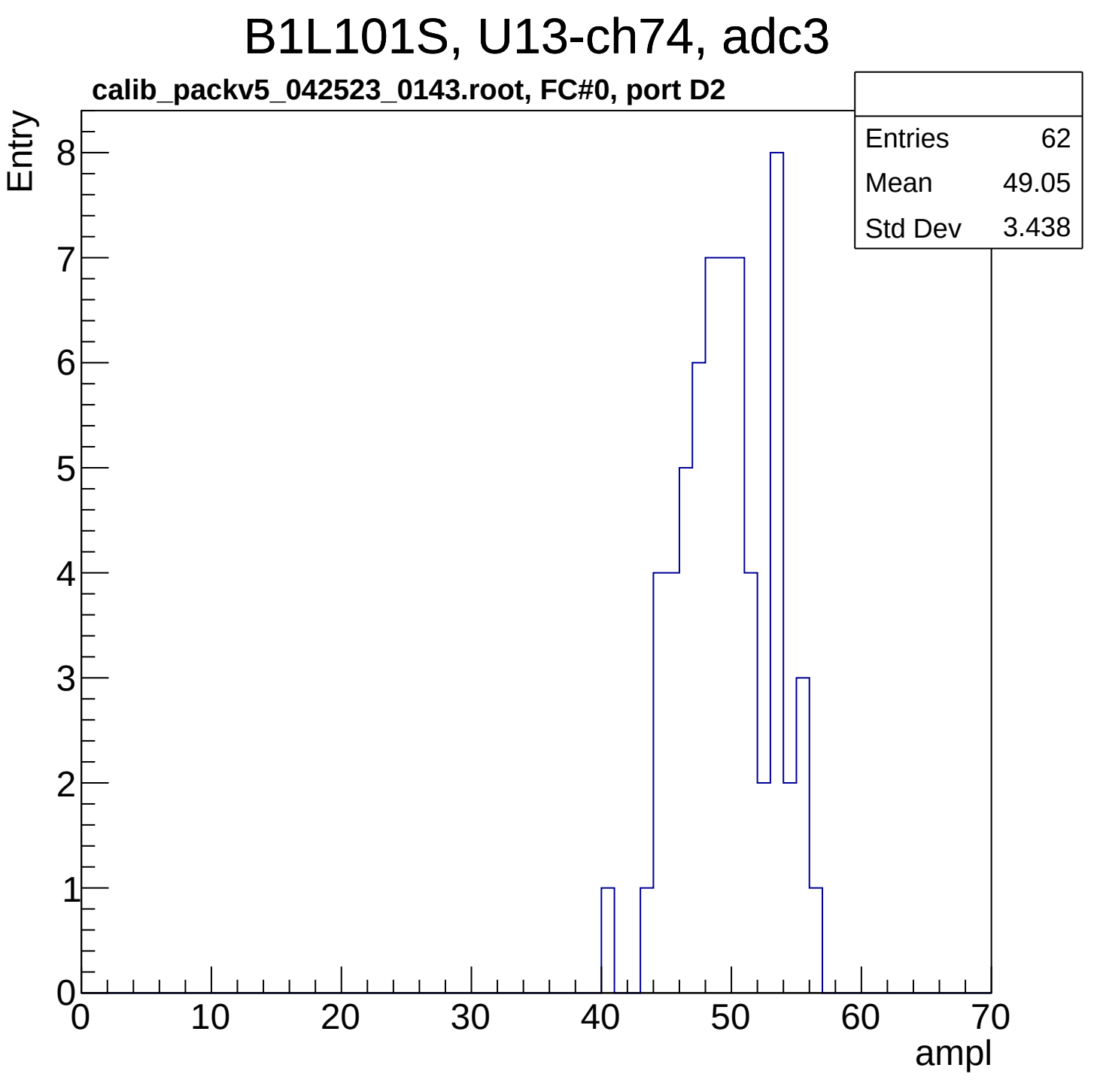
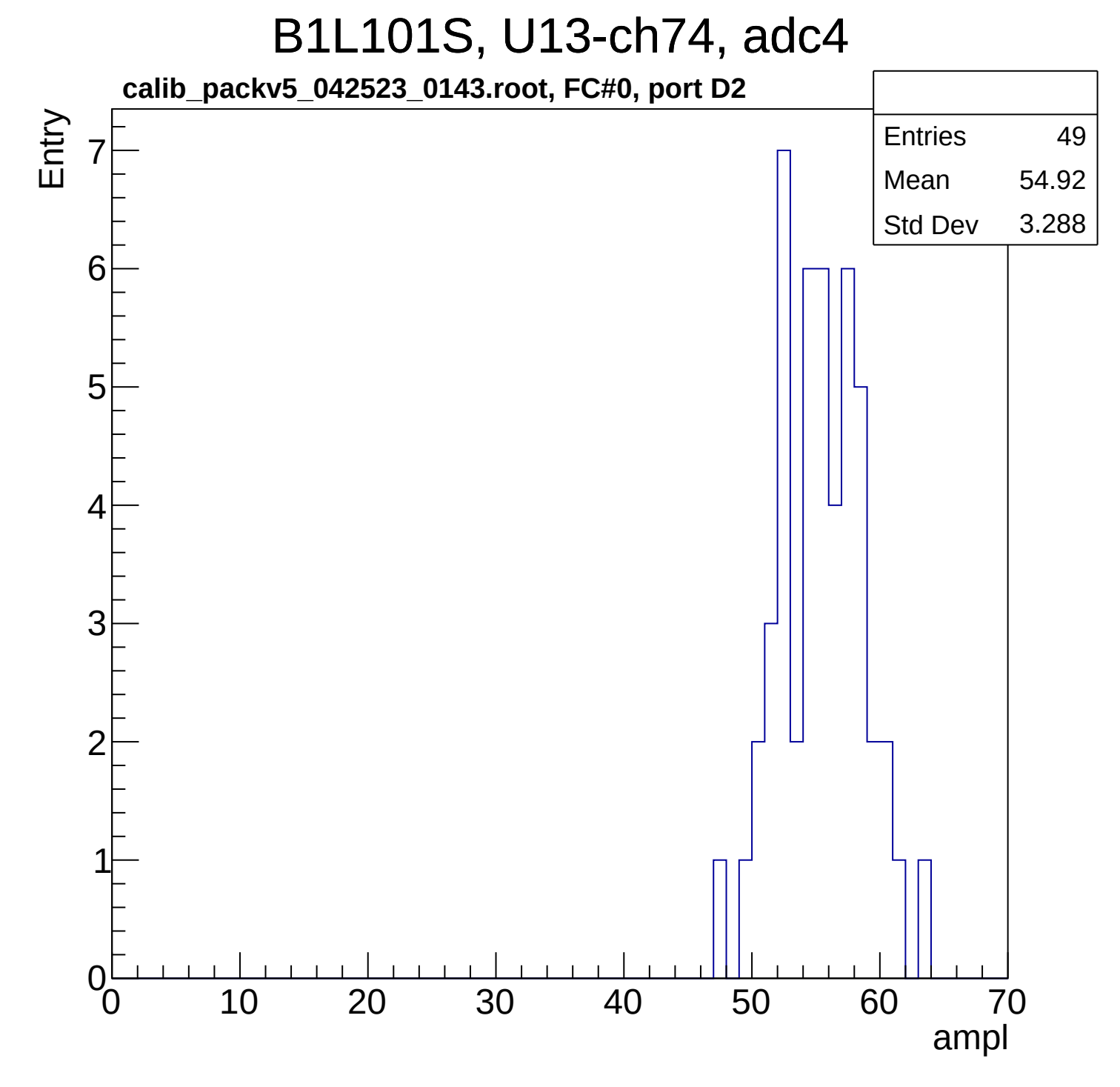
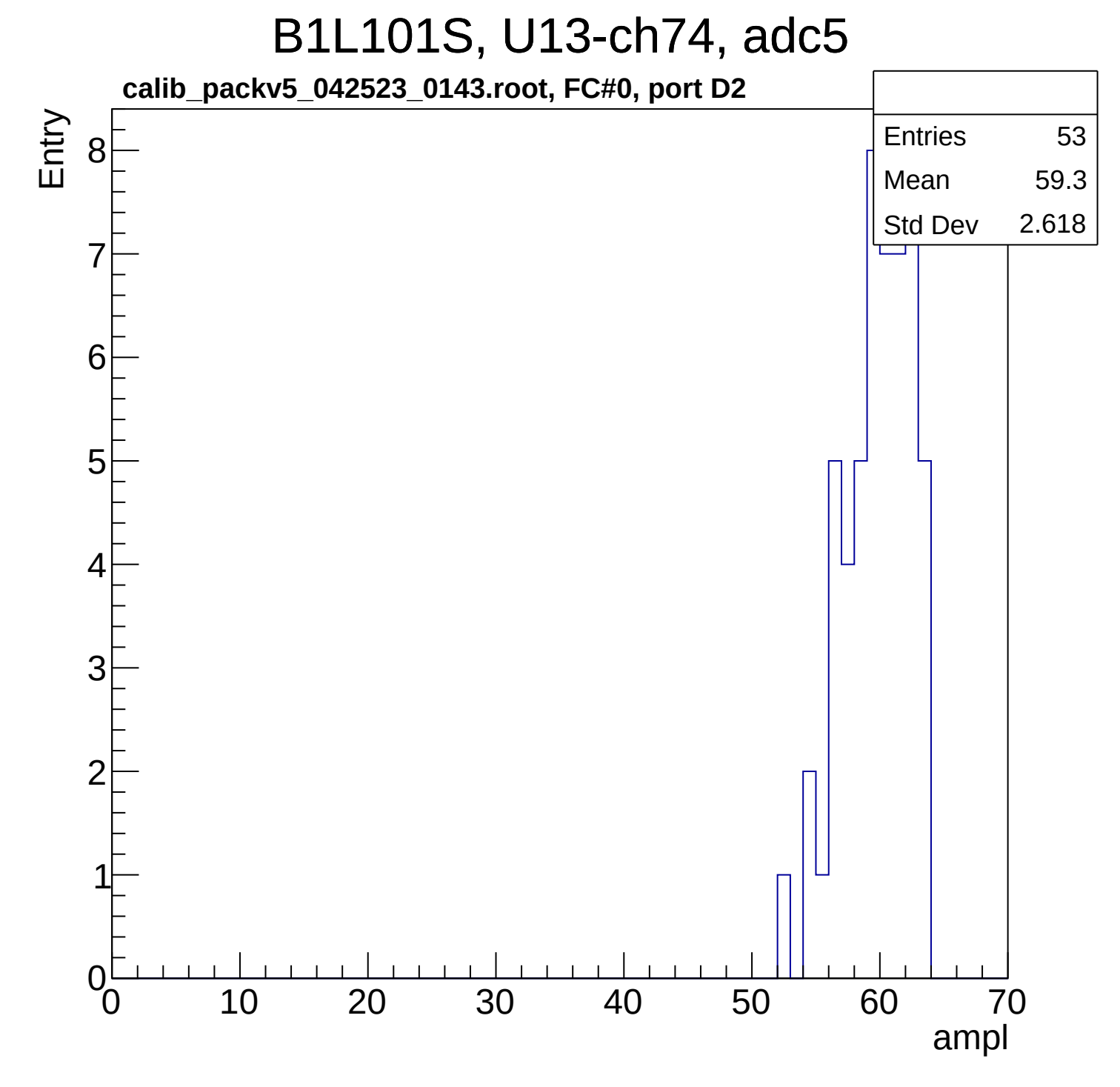

B1L101S, U13-ch74, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 49.05
Std Dev 3.438
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch74, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 54.92
Std Dev 3.288
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch74, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 59.3
Std Dev 2.618
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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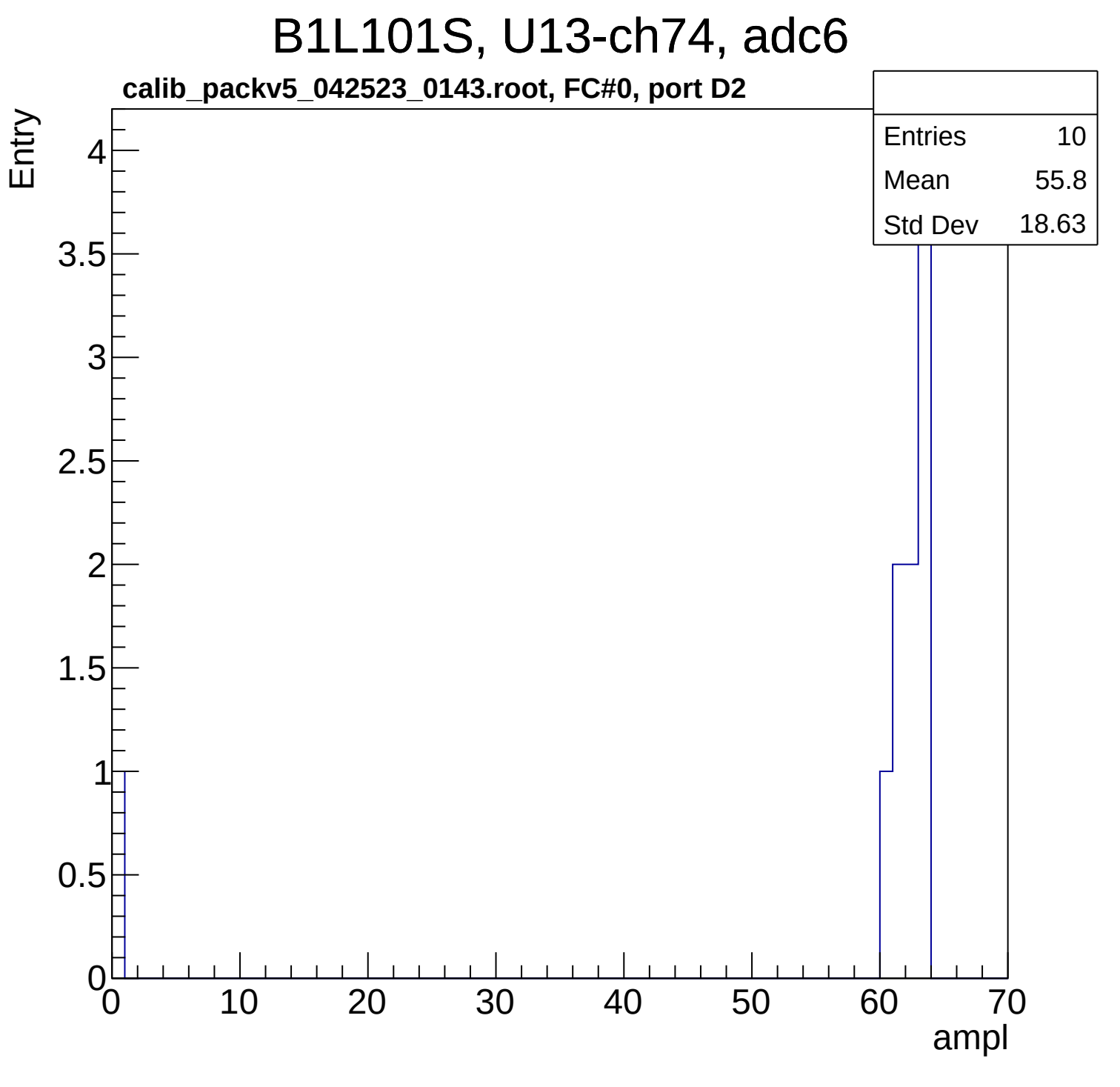
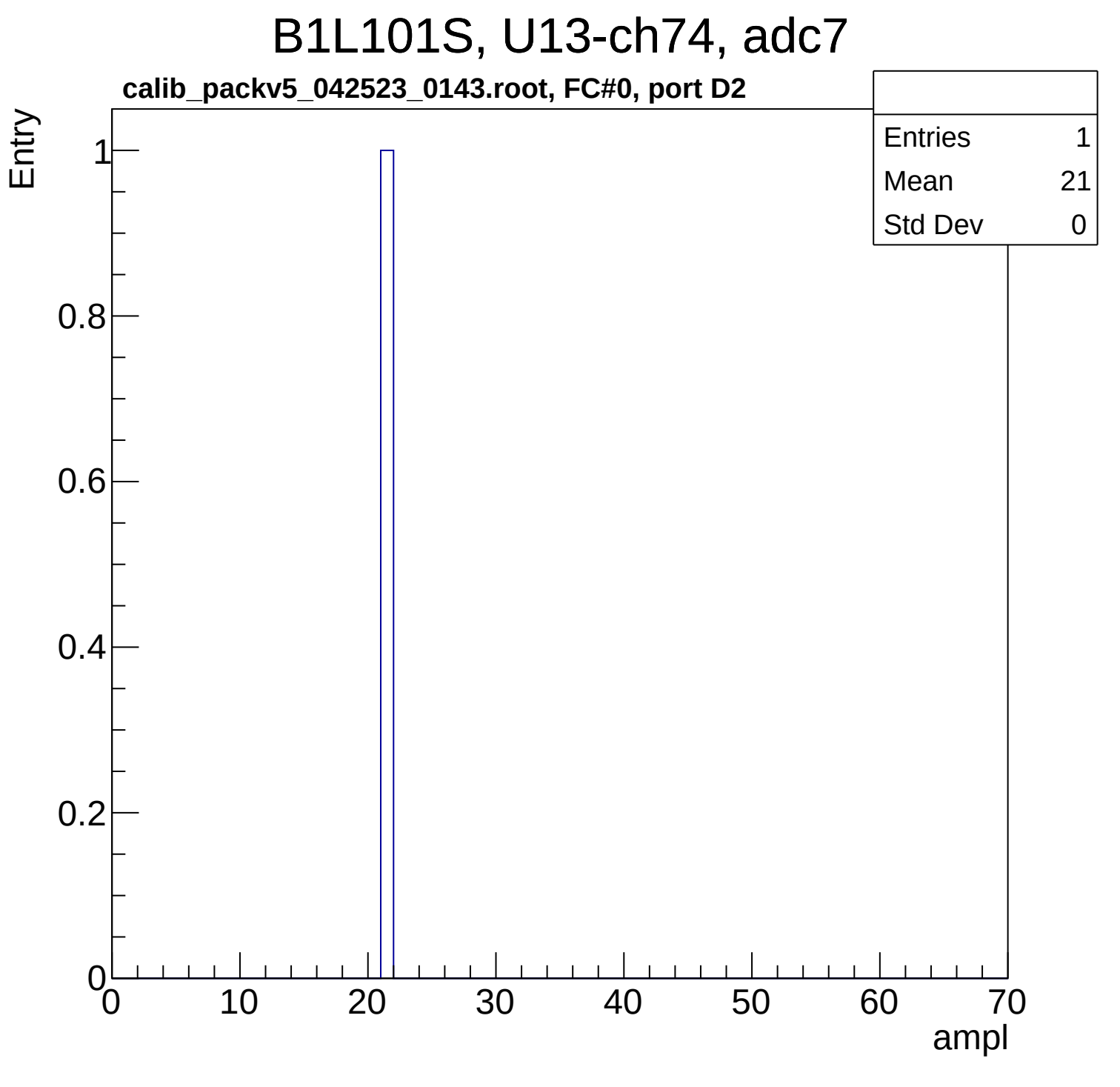

B1L101S, U13-ch74, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 10
Mean 55.8
Std Dev 18.63
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L101S, U13-ch74, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 21
Std Dev 0
Entry
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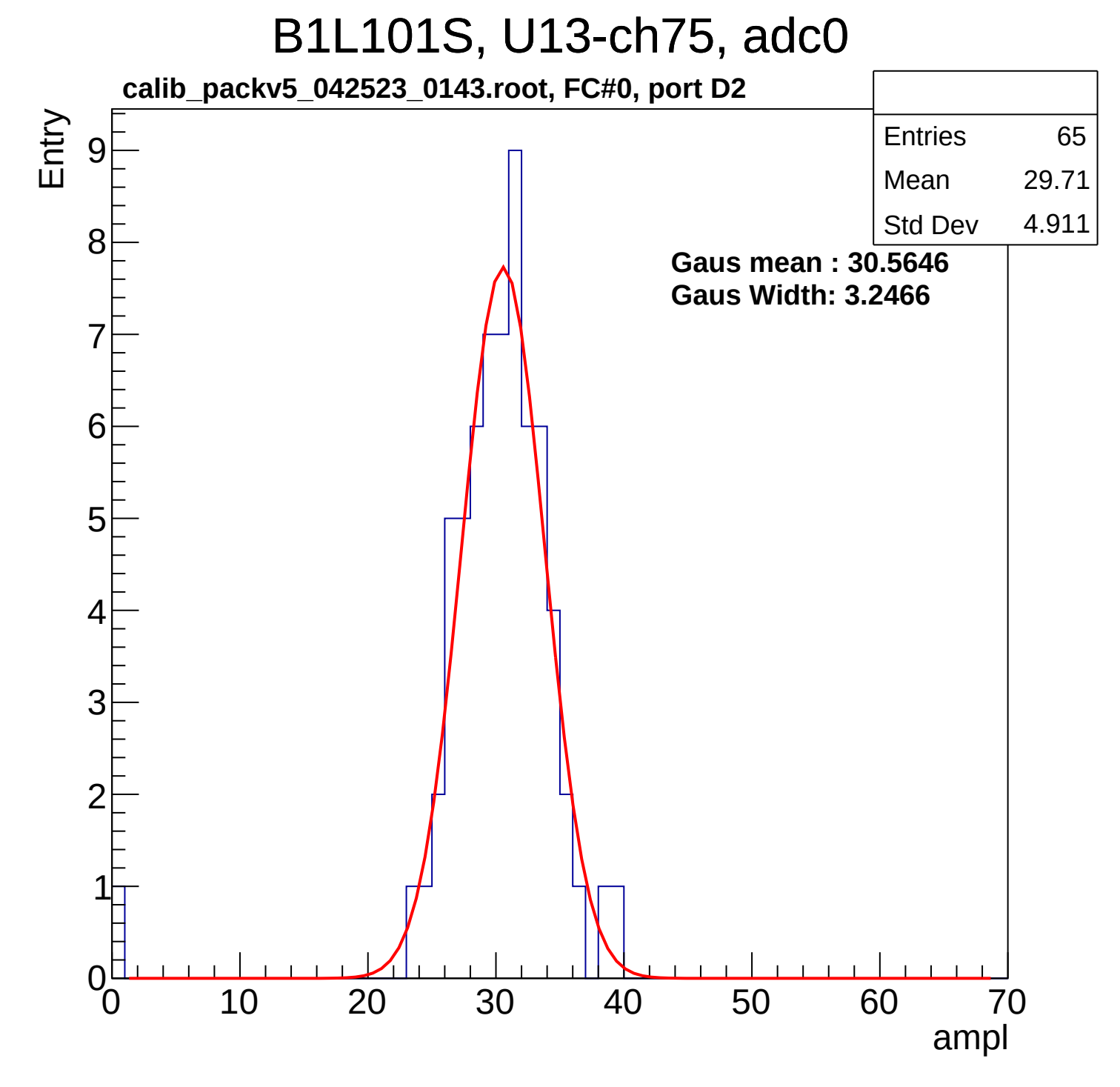
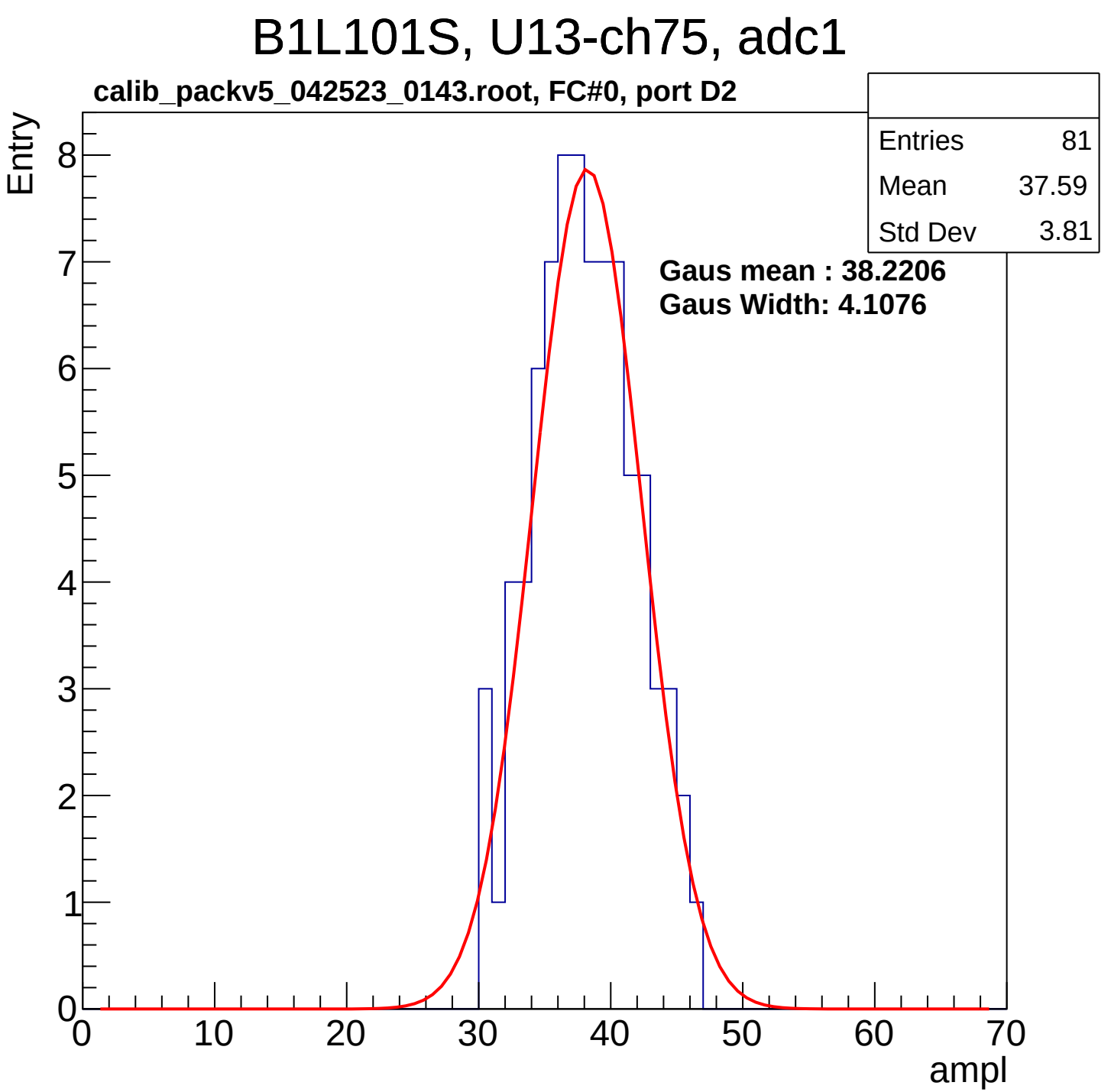
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch75, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 29.71
Std Dev 4.911
Gaus mean : 30.5646
Gaus Width: 3.2466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch75, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 81
Mean 37.59
Std Dev 3.81
Gaus mean : 38.2206
Gaus Width: 4.1076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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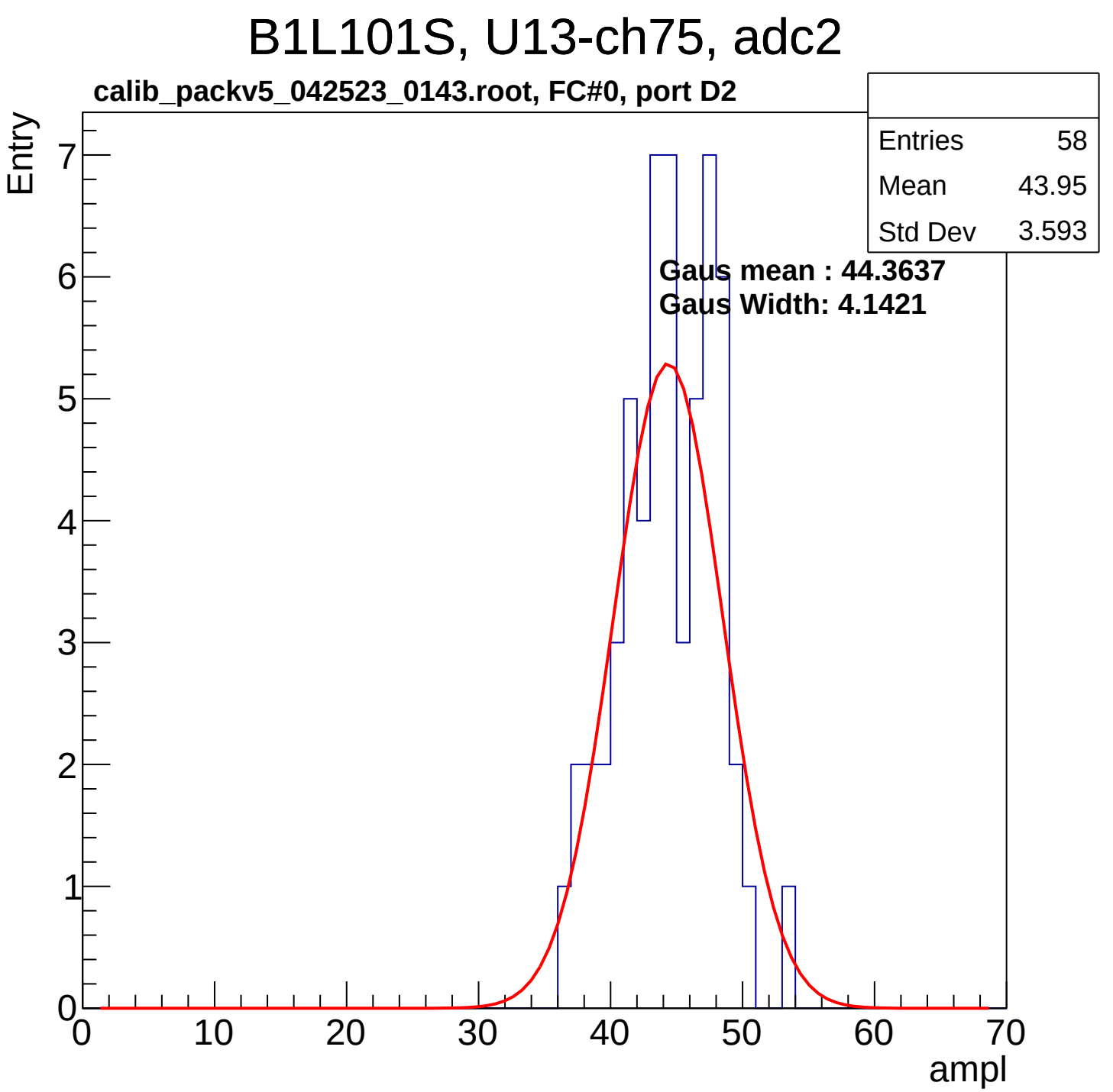
B1L101S, U13-ch75, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 43.95
Std Dev 3.593
Gaus mean : 44.3637
Gaus Width: 4.1421
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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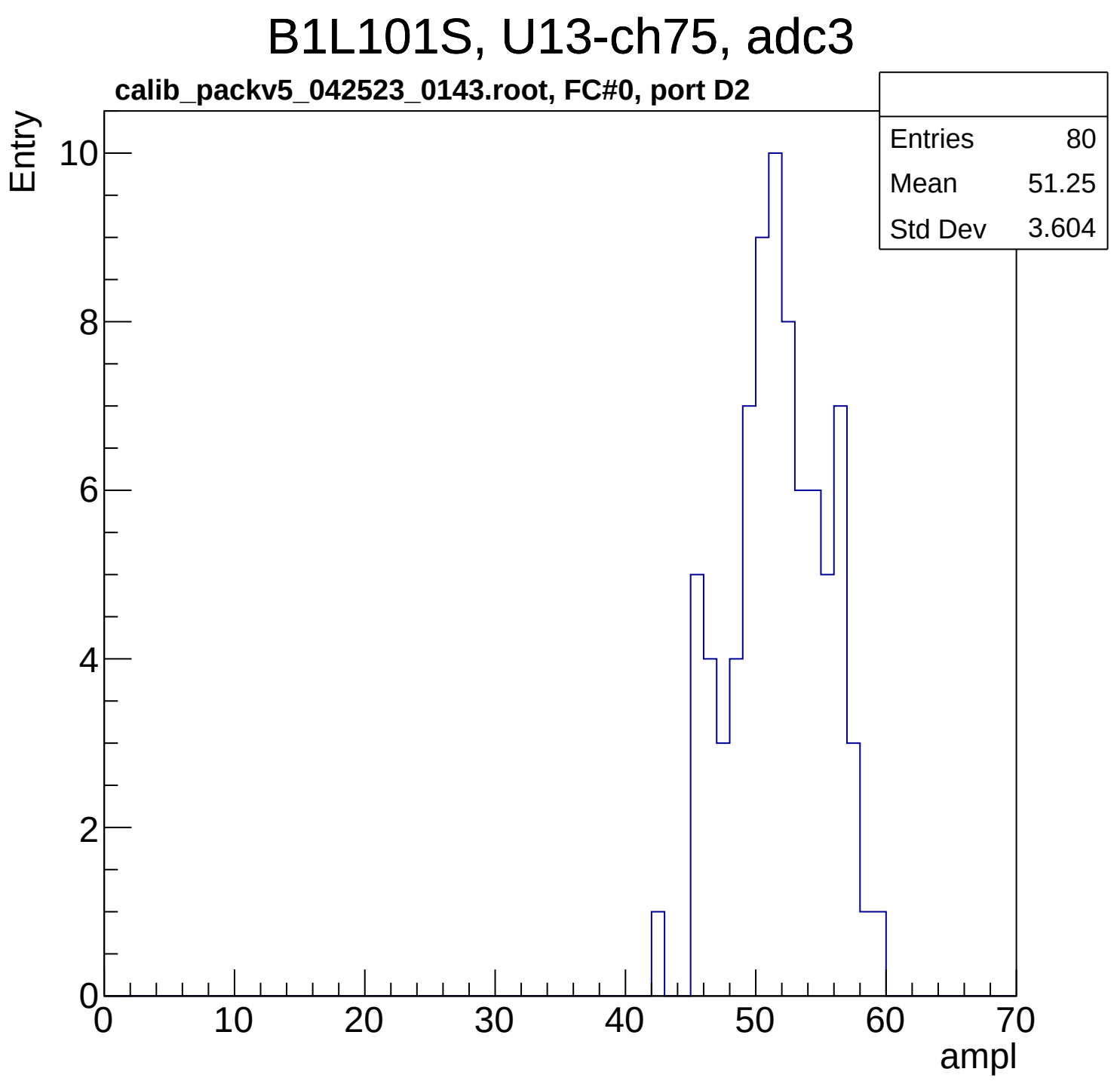
B1L101S, U13-ch75, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 51.25
Std Dev 3.604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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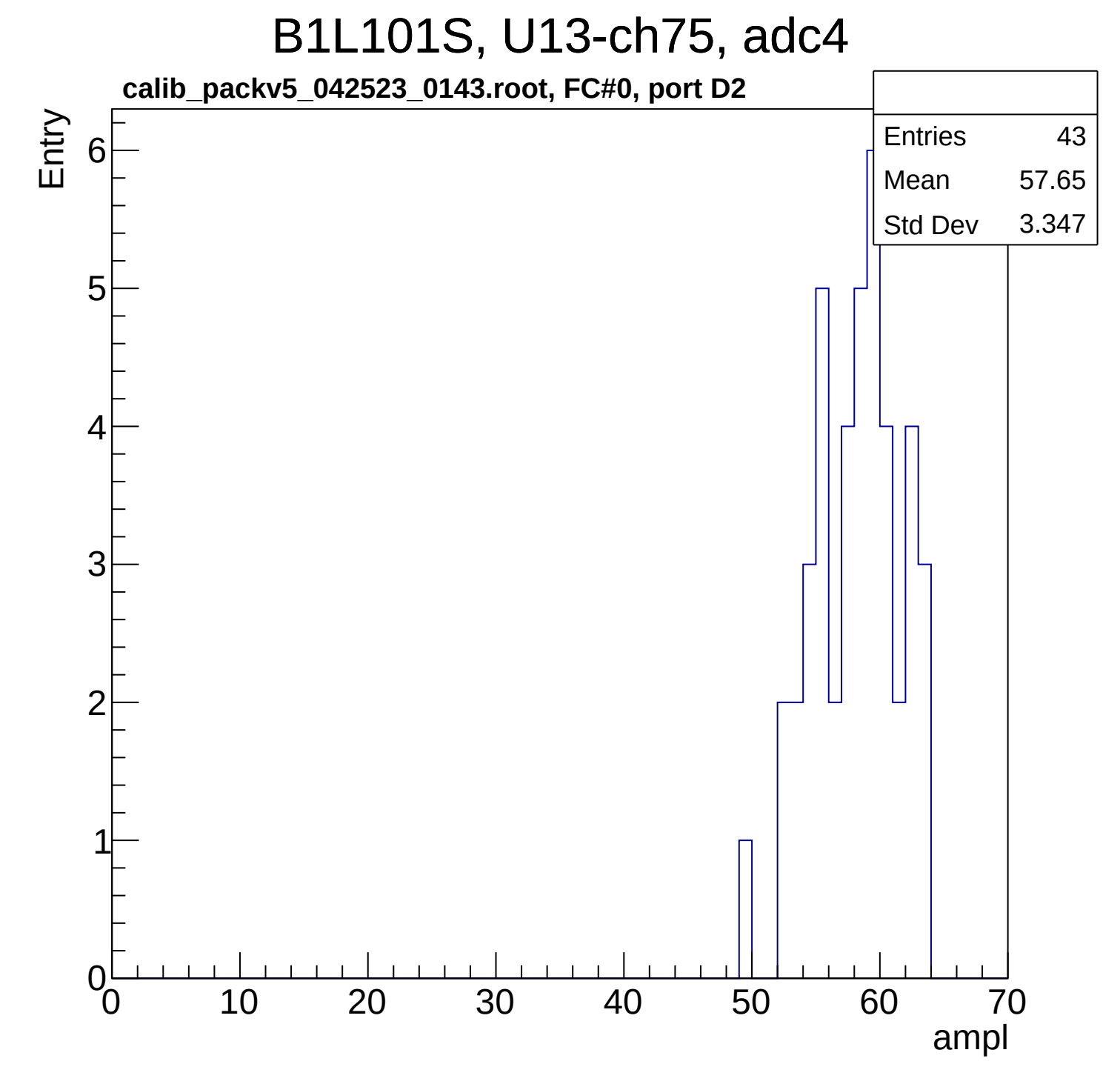
B1L101S, U13-ch75, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 43
Mean 57.65
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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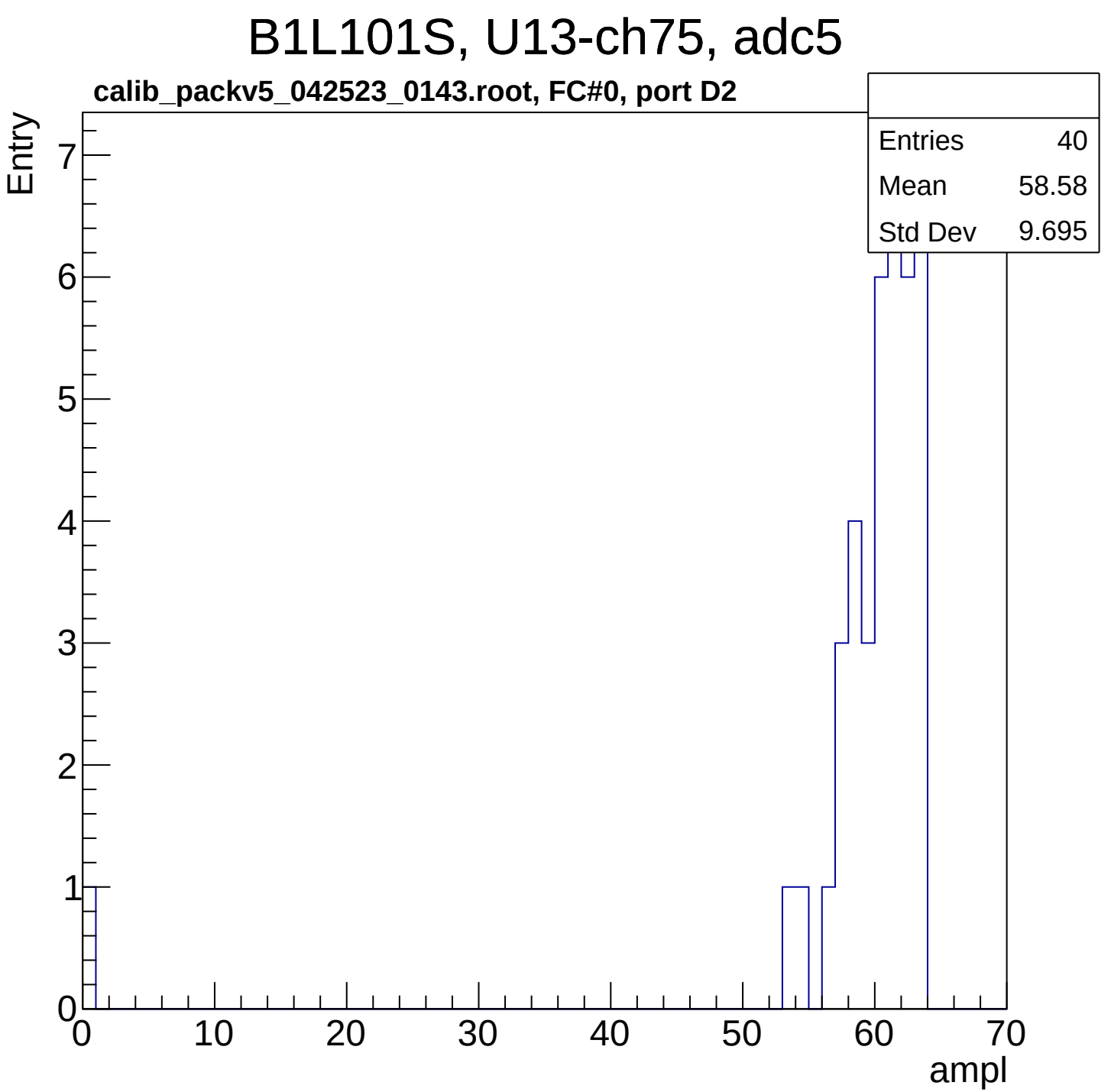
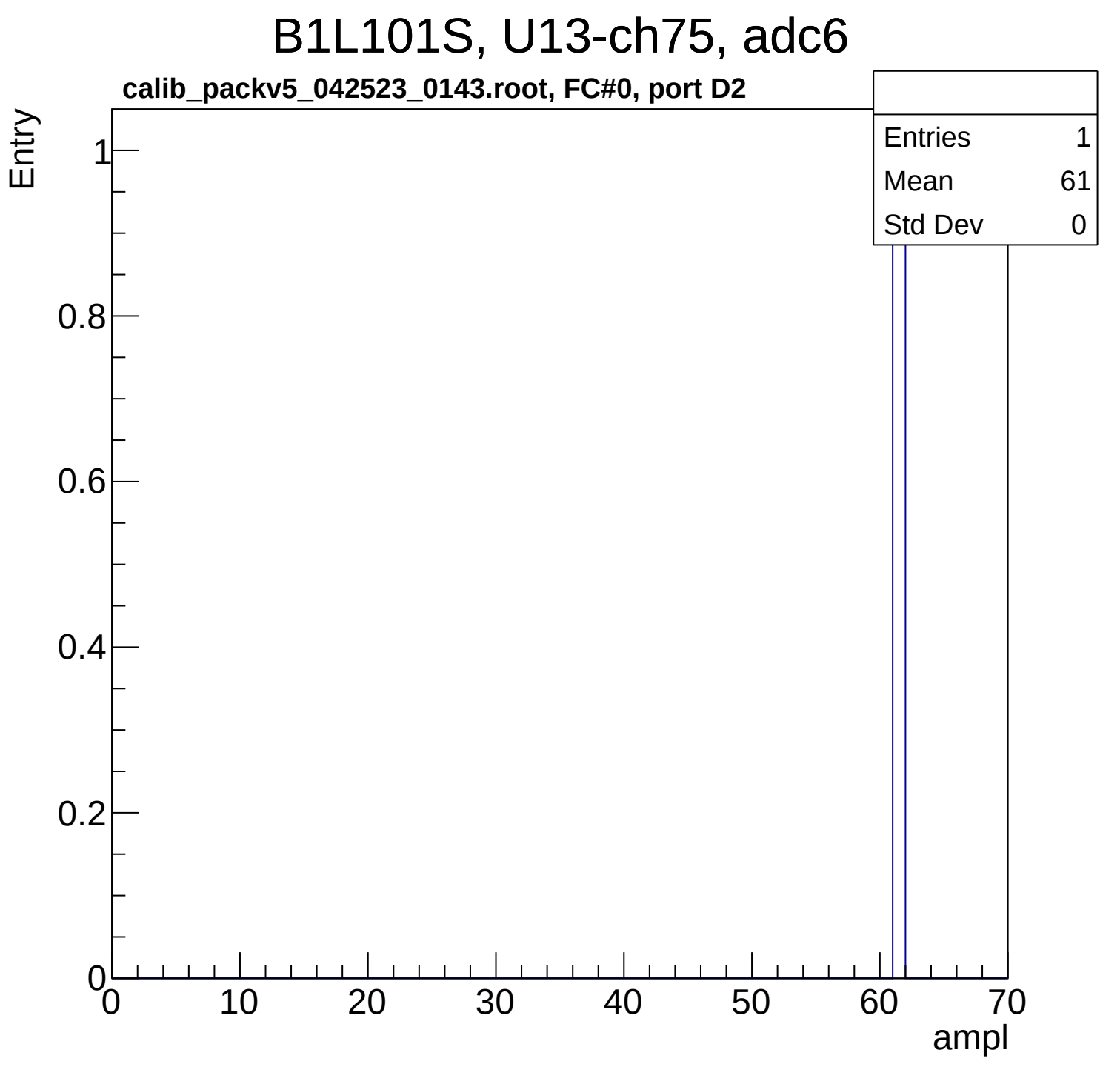
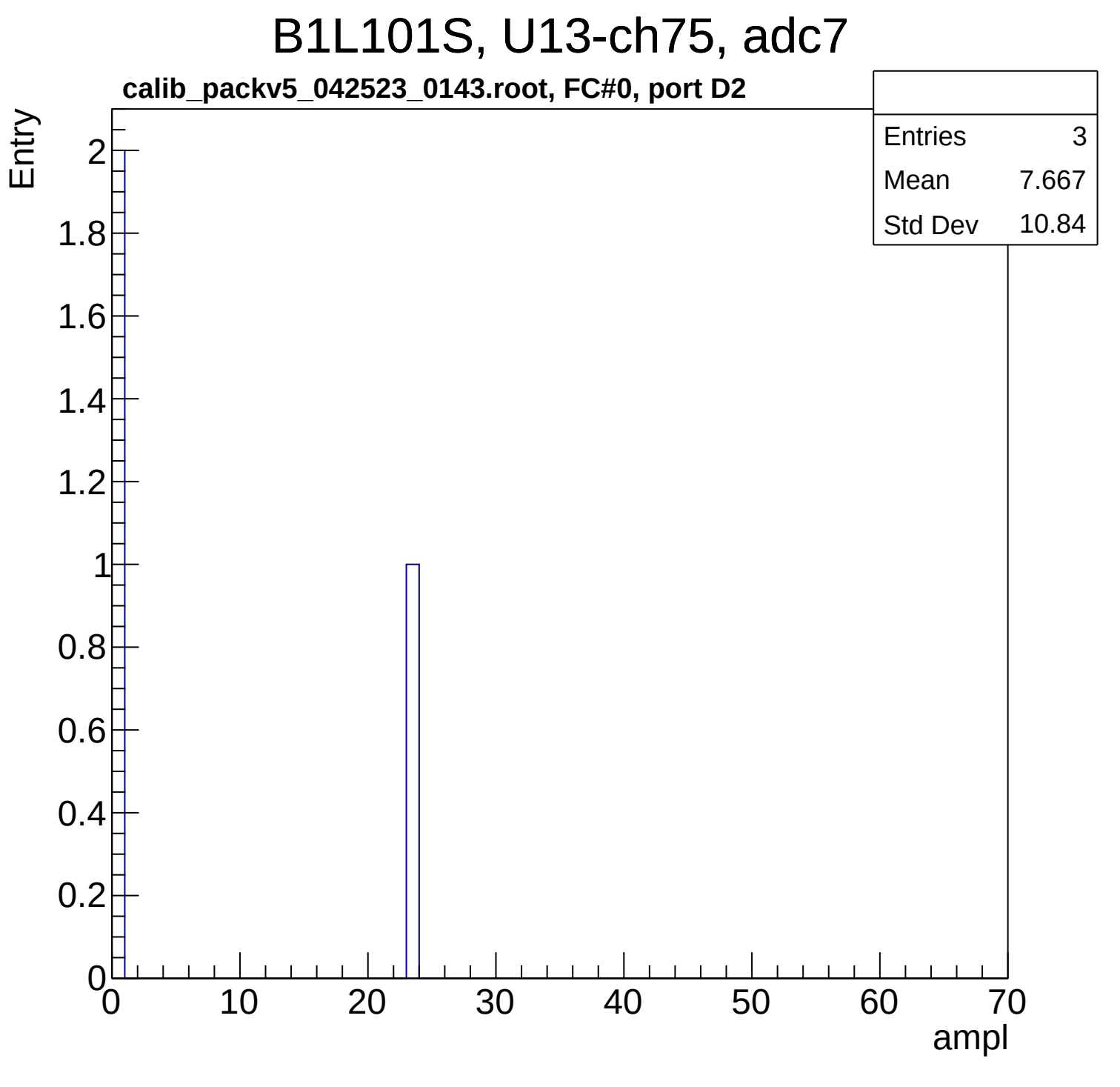

B1L101S, U13-ch75, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 58.58
Std Dev 9.695
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch75, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch75, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
30
40
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60
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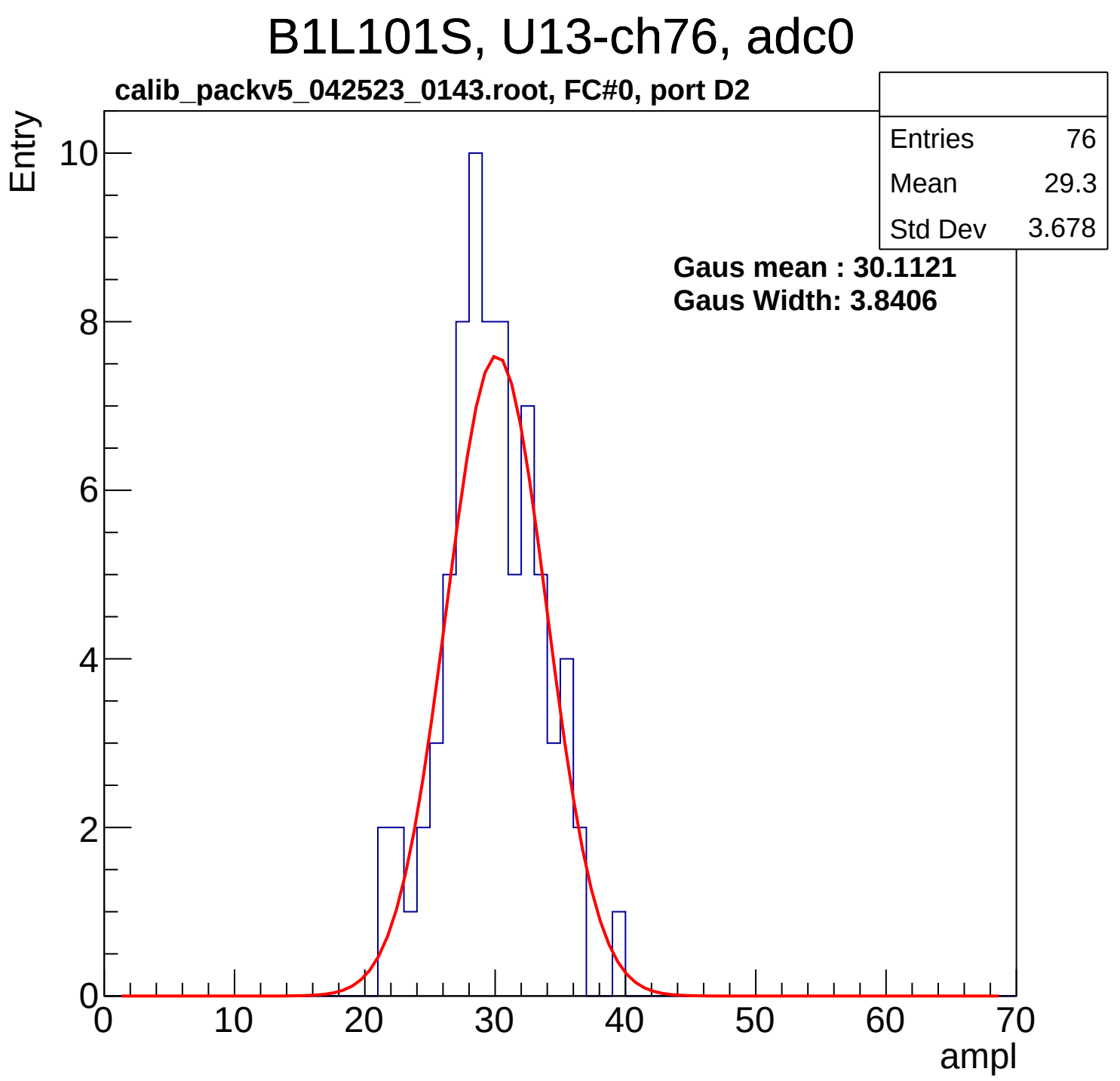
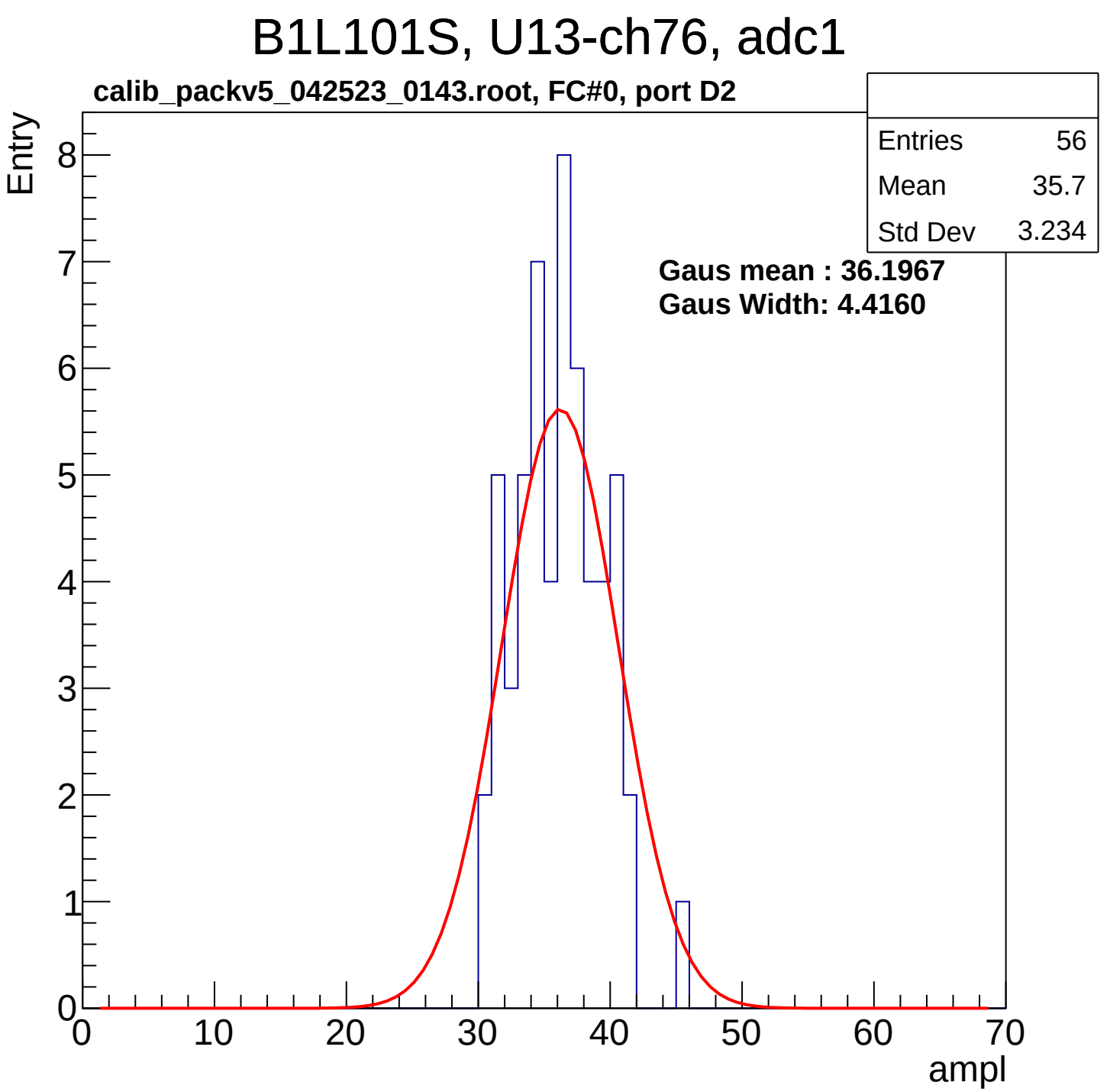
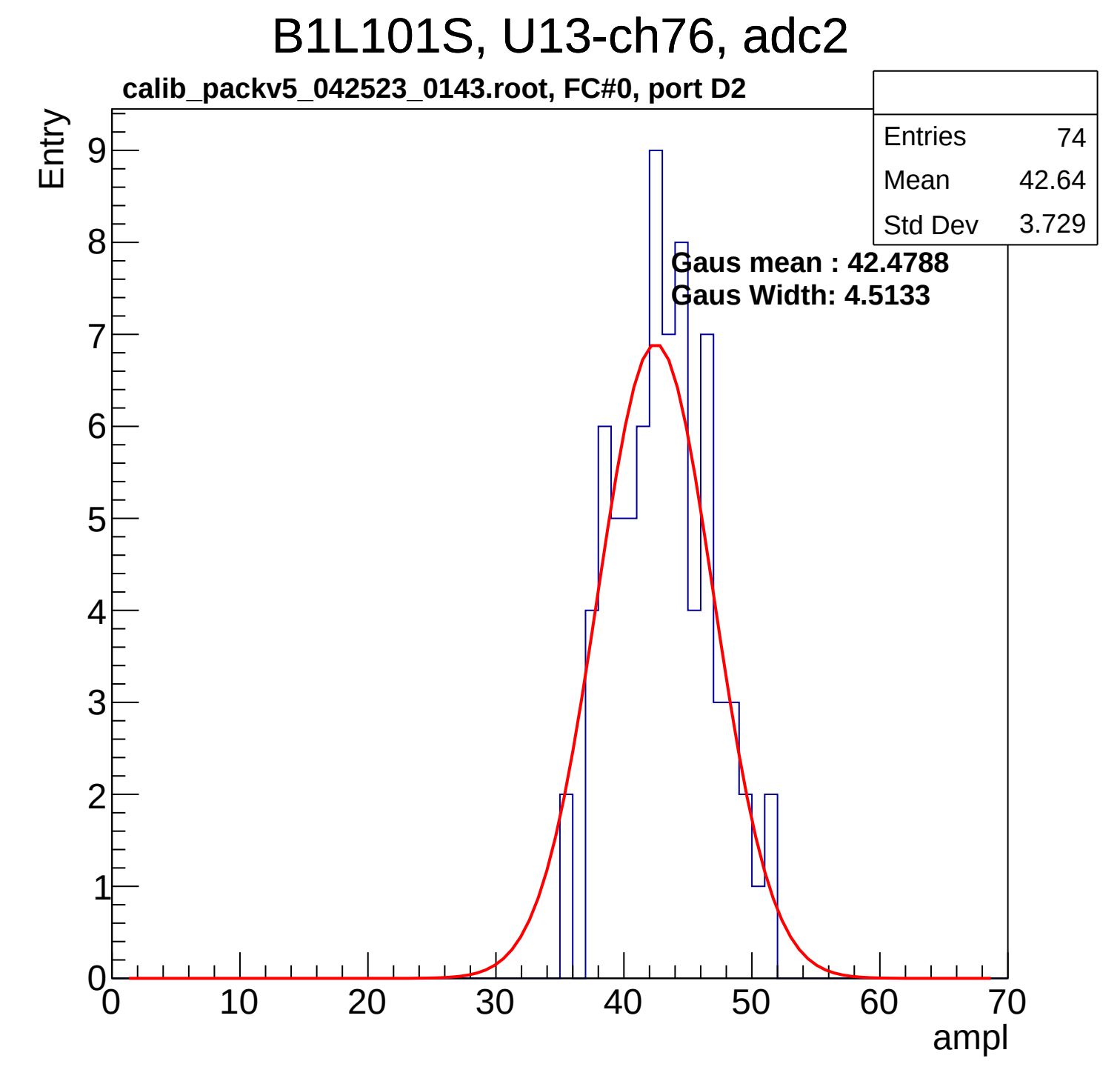

B1L101S, U13-ch76, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 29.3
Std Dev 3.678
Gaus mean : 30.1121
Gaus Width: 3.8406
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch76, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 35.7
Std Dev 3.234
Gaus mean : 36.1967
Gaus Width: 4.4160
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch76, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 42.64
Std Dev 3.729
Gaus mean : 42.4788
Gaus Width: 4.5133
Entry
ampl
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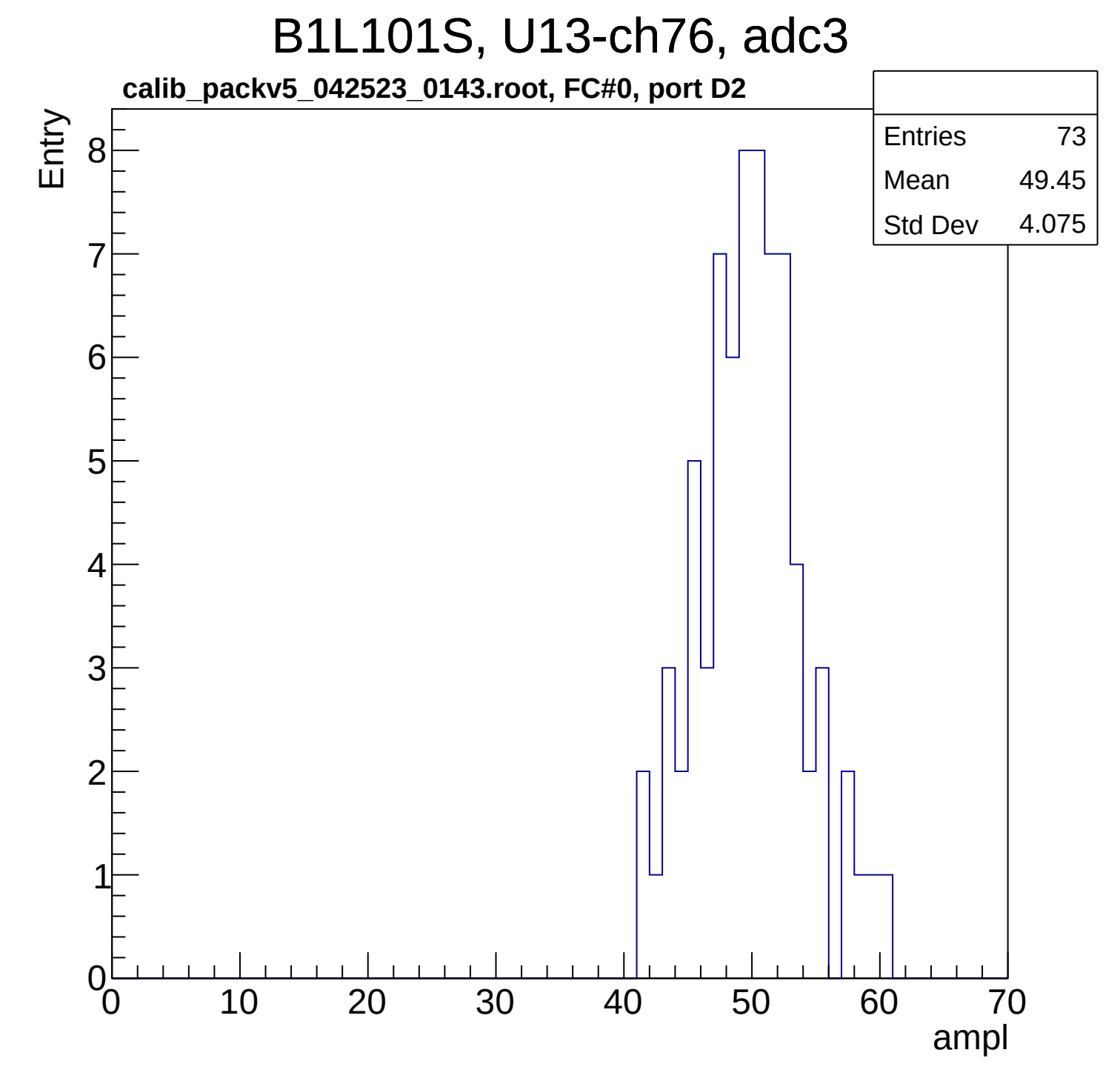
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch76, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 49.45
Std Dev 4.075
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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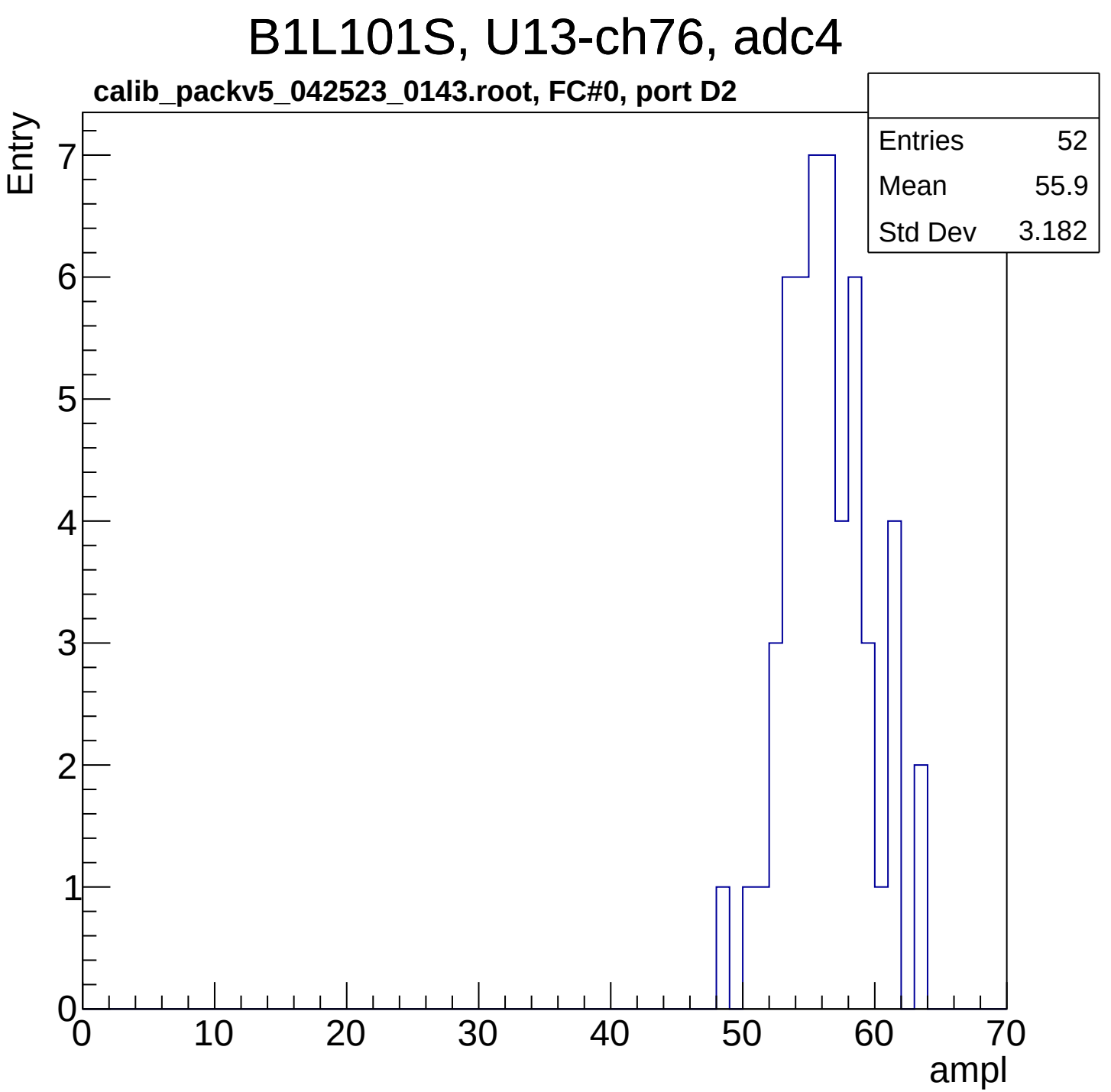
40
50
60
70


B1L101S, U13-ch76, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 52
Mean 55.9
Std Dev 3.182
Entry
ampl
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3
4
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6
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0
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40
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60
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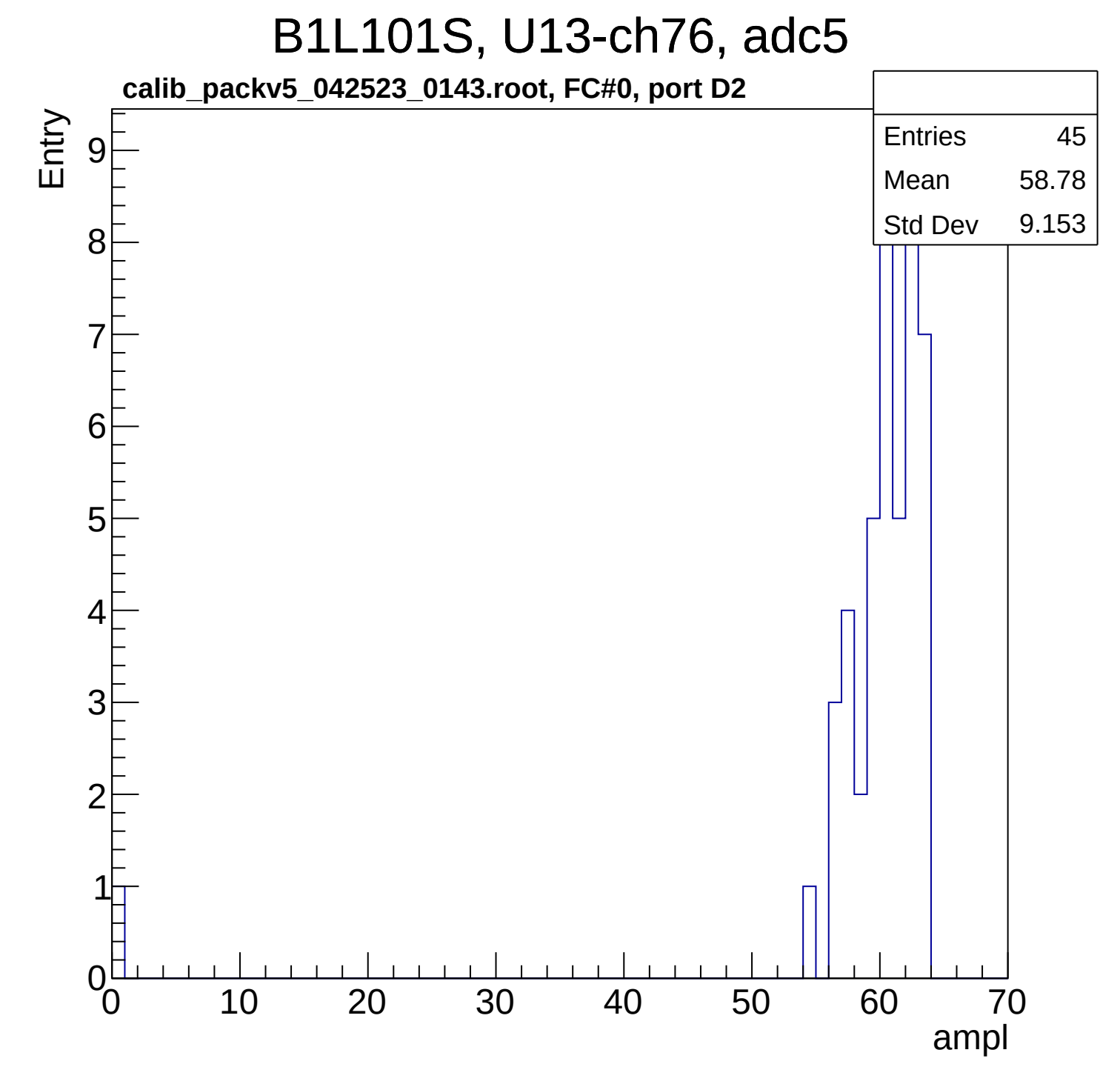
B1L101S, U13-ch76, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 58.78
Std Dev 9.153
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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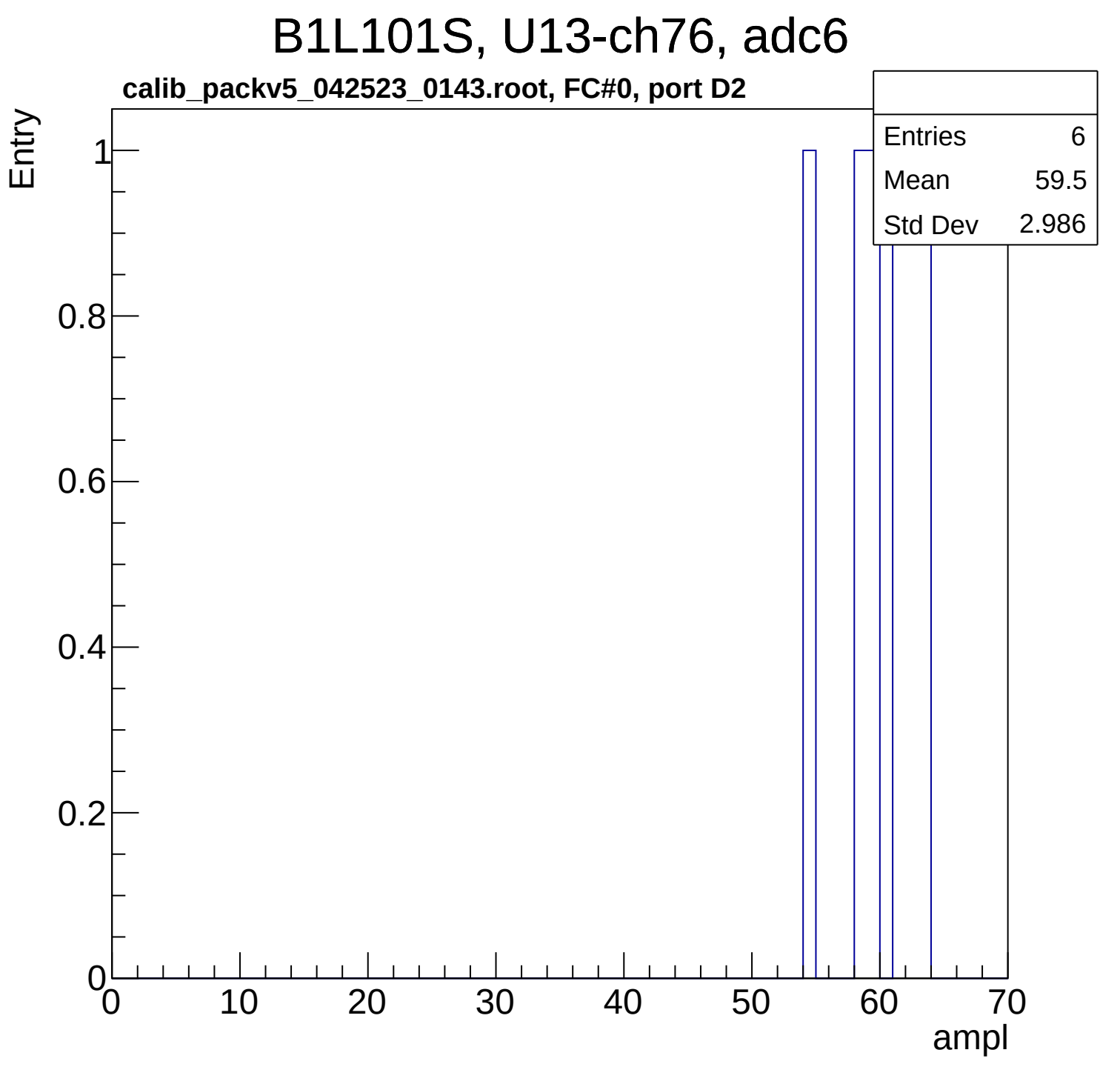
B1L101S, U13-ch76, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 6
Mean 59.5
Std Dev 2.986
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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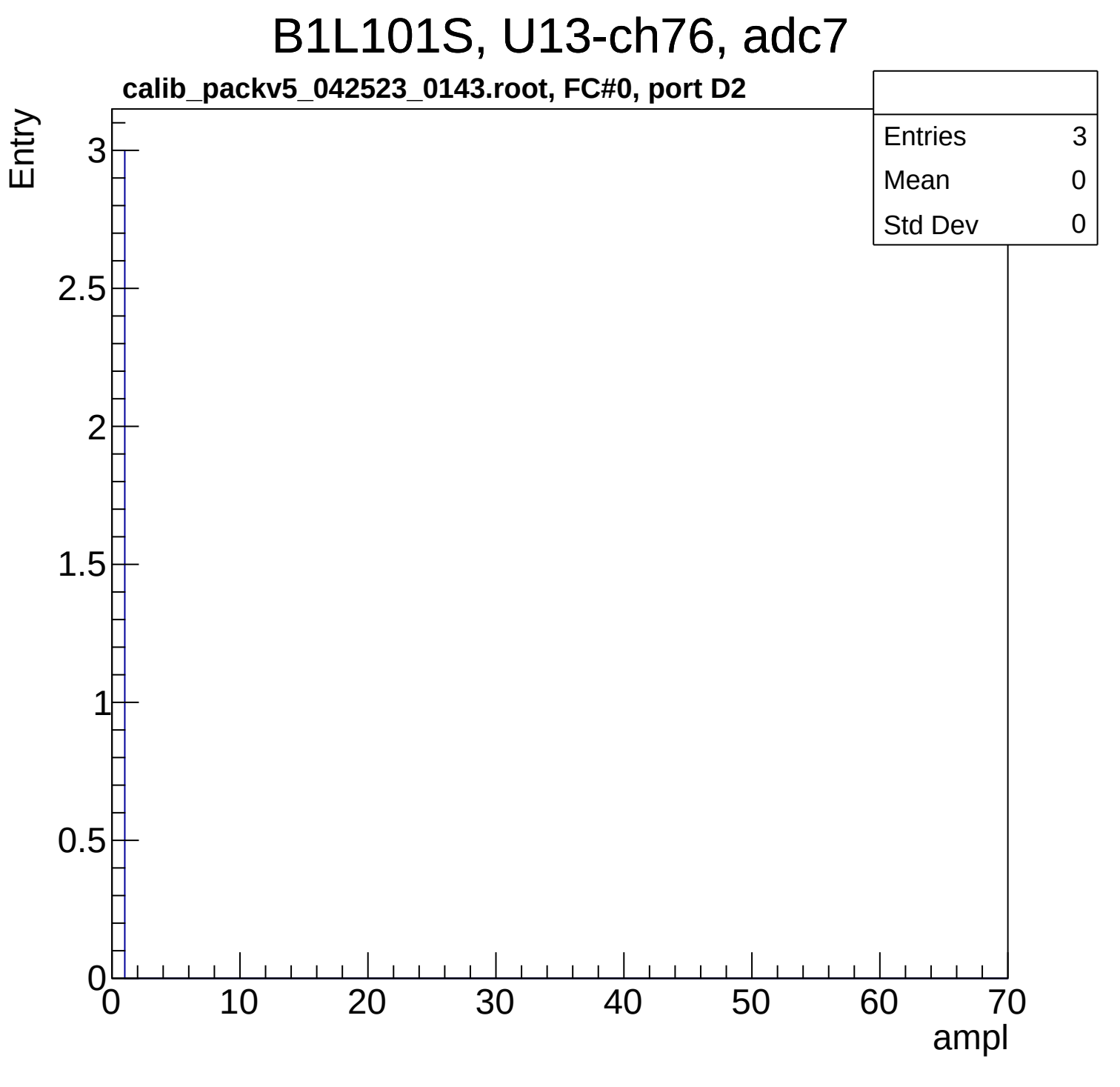
40
50
60
70


B1L101S, U13-ch76, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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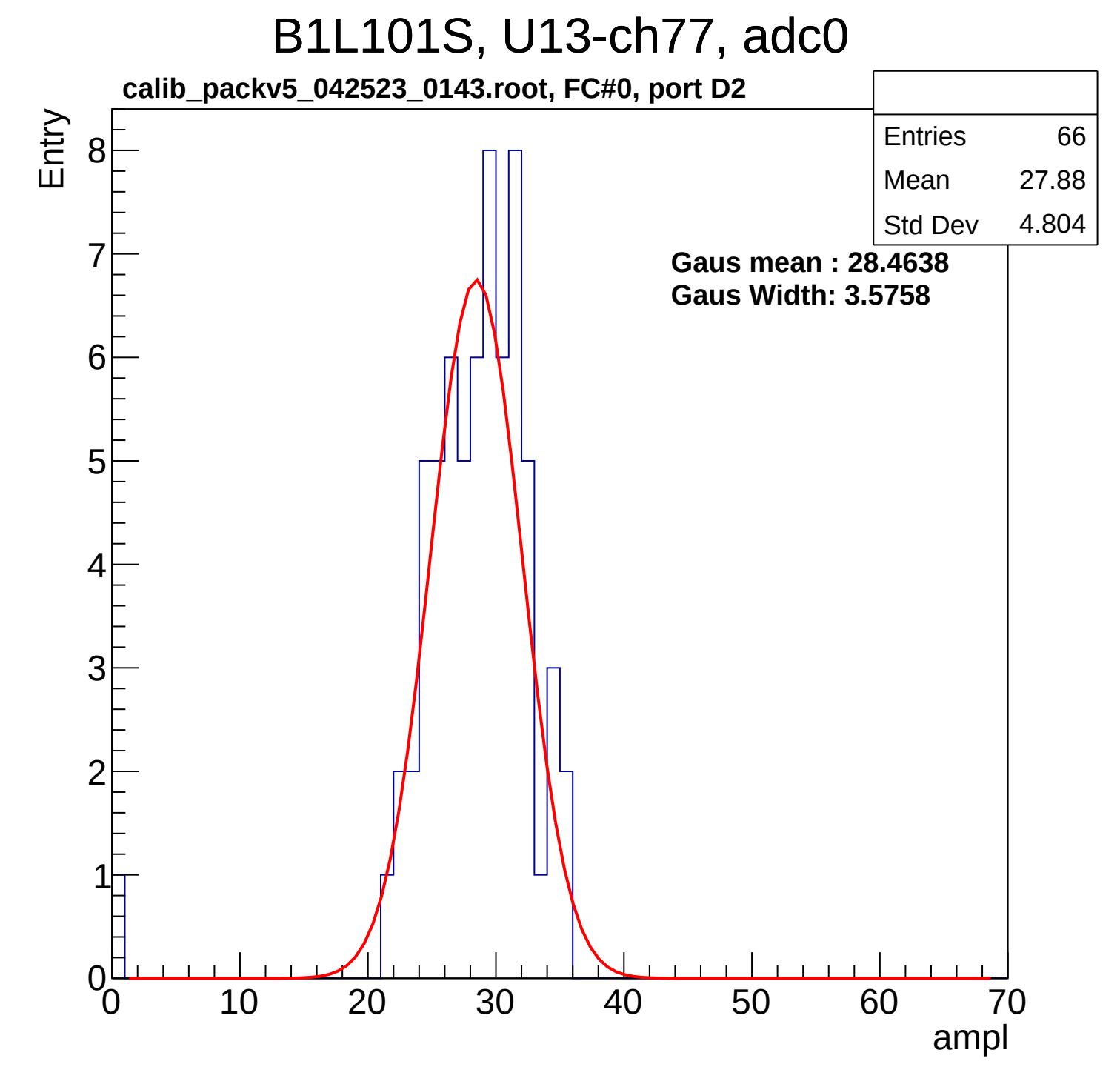
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch77, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 27.88
Std Dev 4.804
Gaus mean : 28.4638
Gaus Width: 3.5758
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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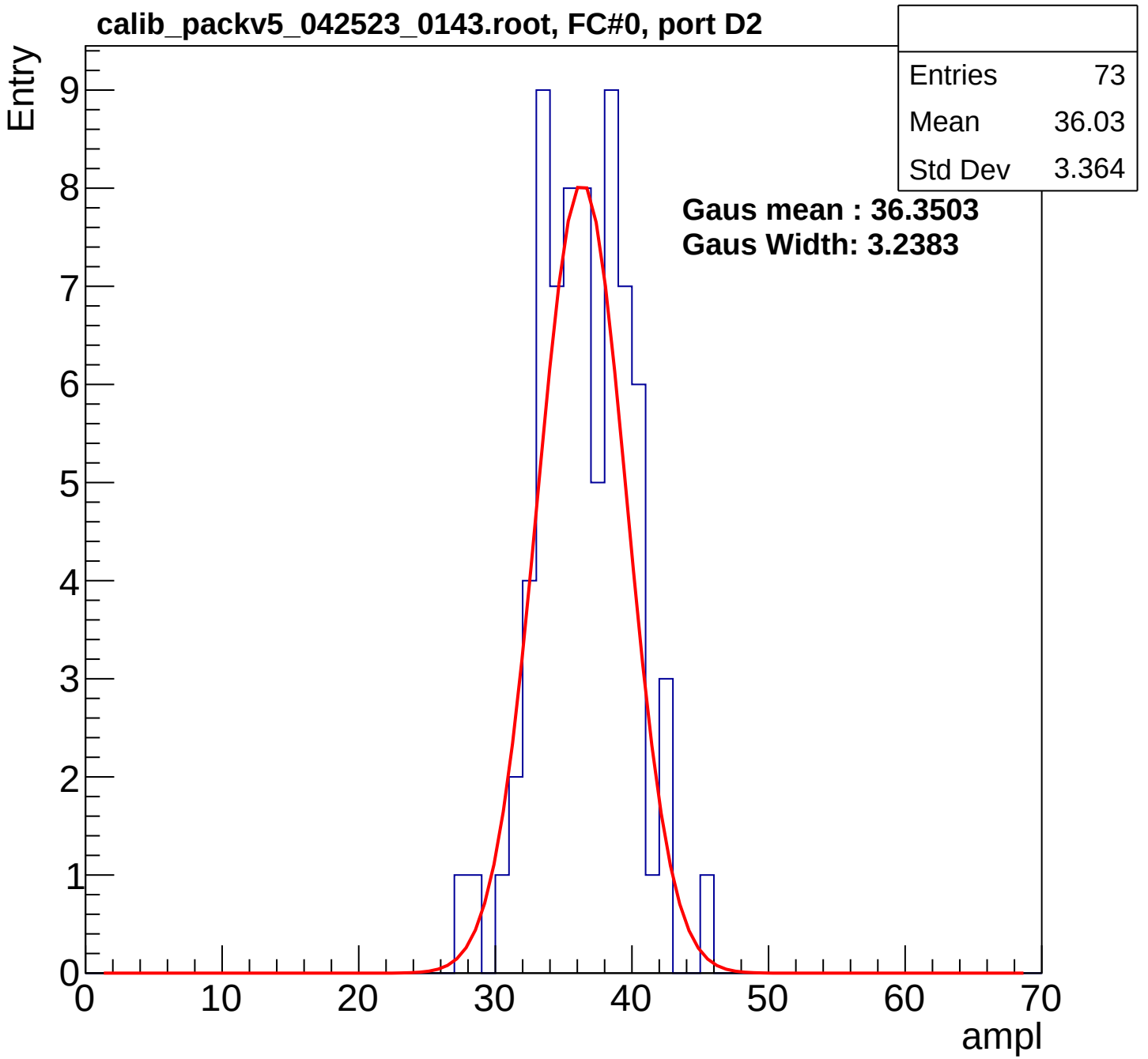
B1L101S, U13-ch77, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 36.03
Std Dev 3.364
Gaus mean : 36.3503
Gaus Width: 3.2383
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
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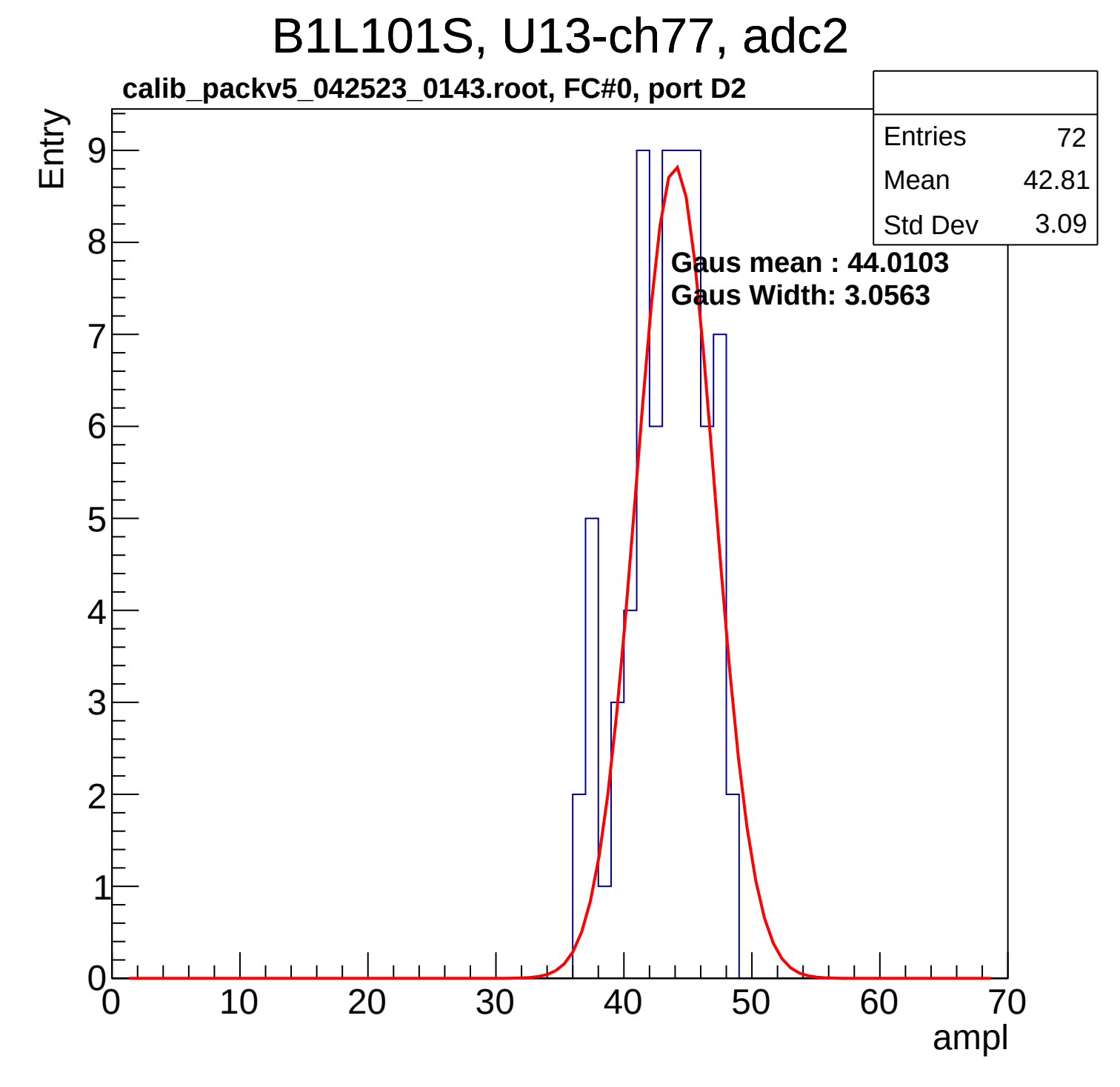
B1L101S, U13-ch77, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 42.81
Std Dev 3.09
Gaus mean : 44.0103
Gaus Width: 3.0563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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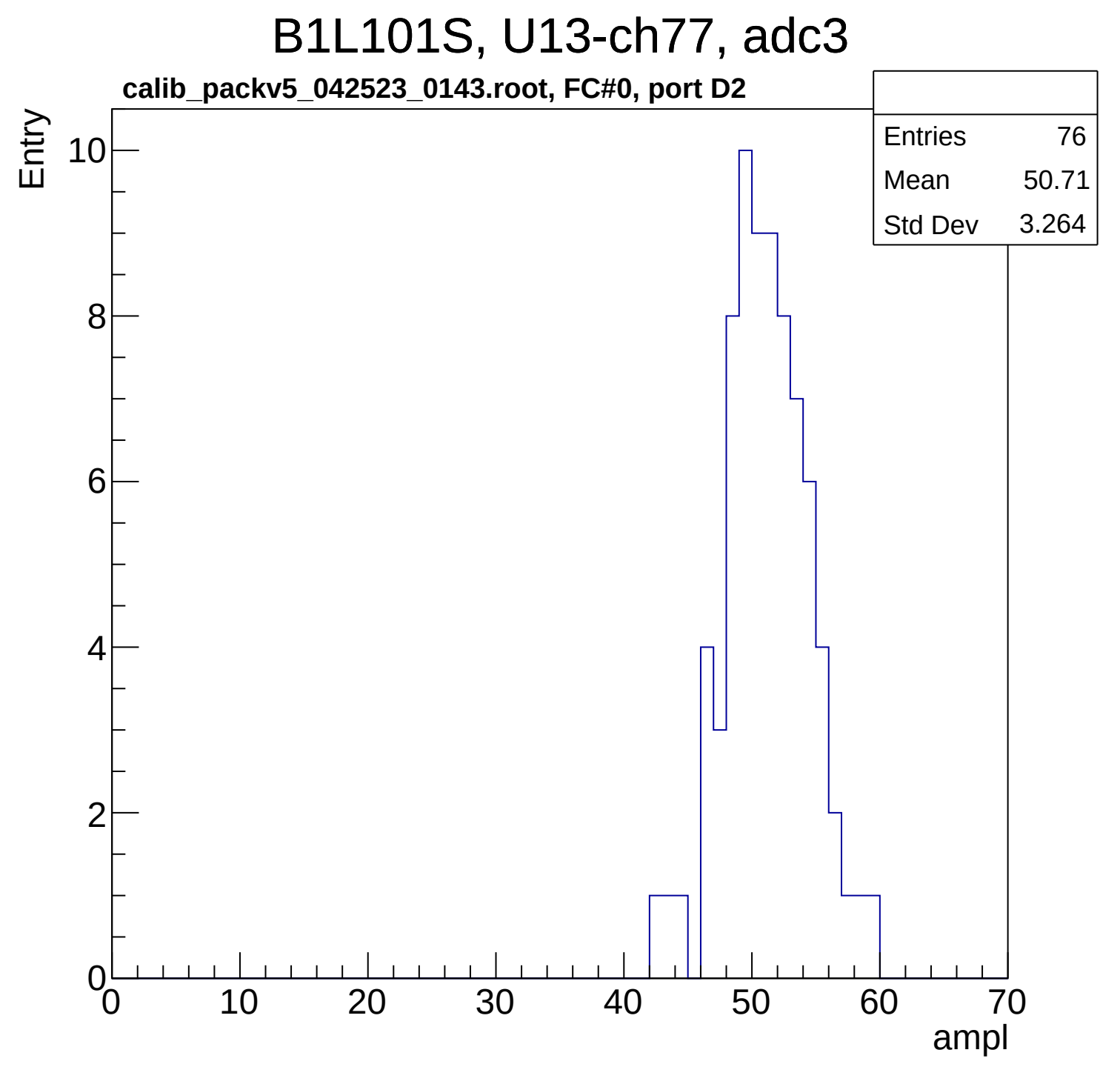
B1L101S, U13-ch77, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 50.71
Std Dev 3.264
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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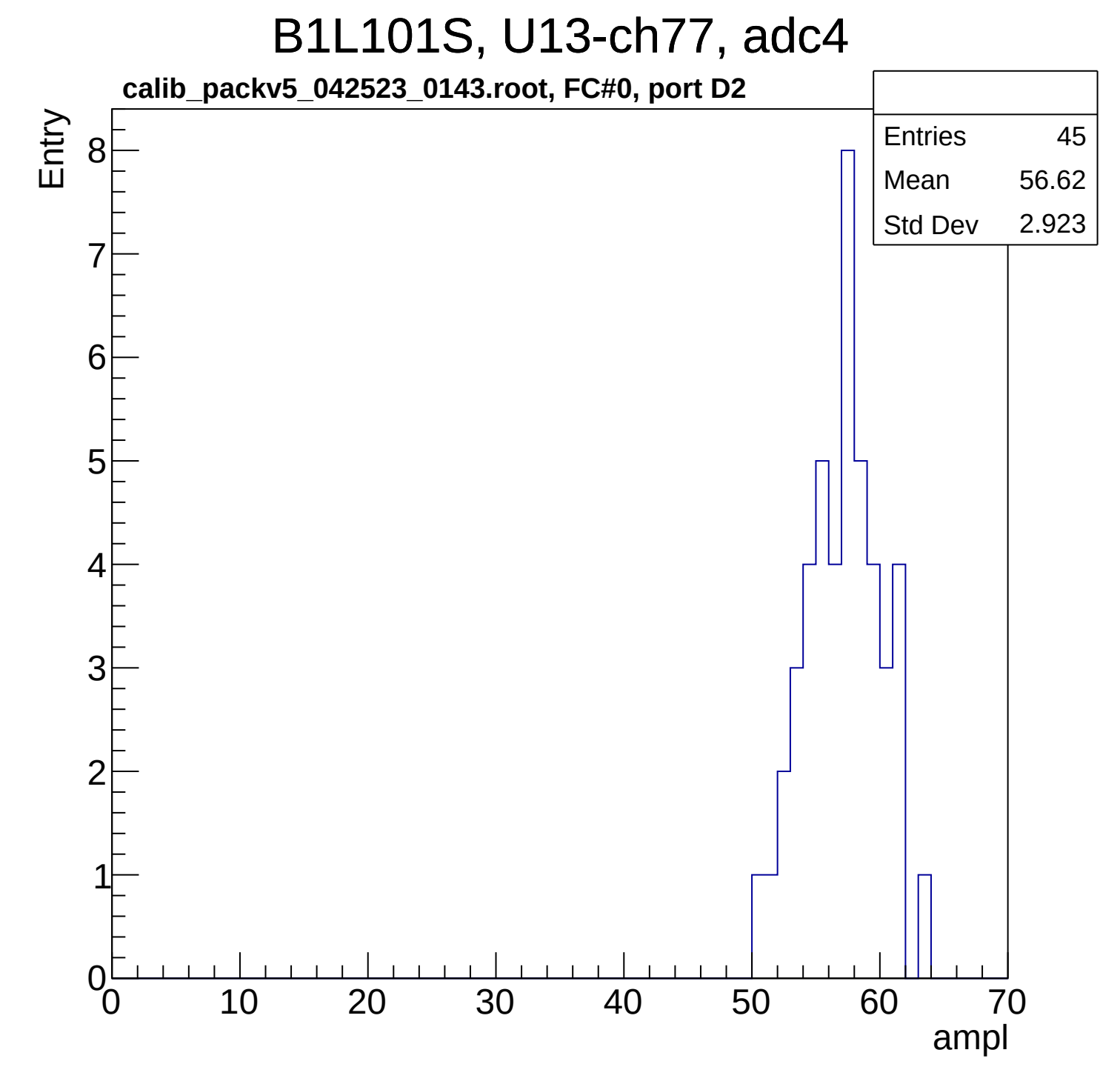
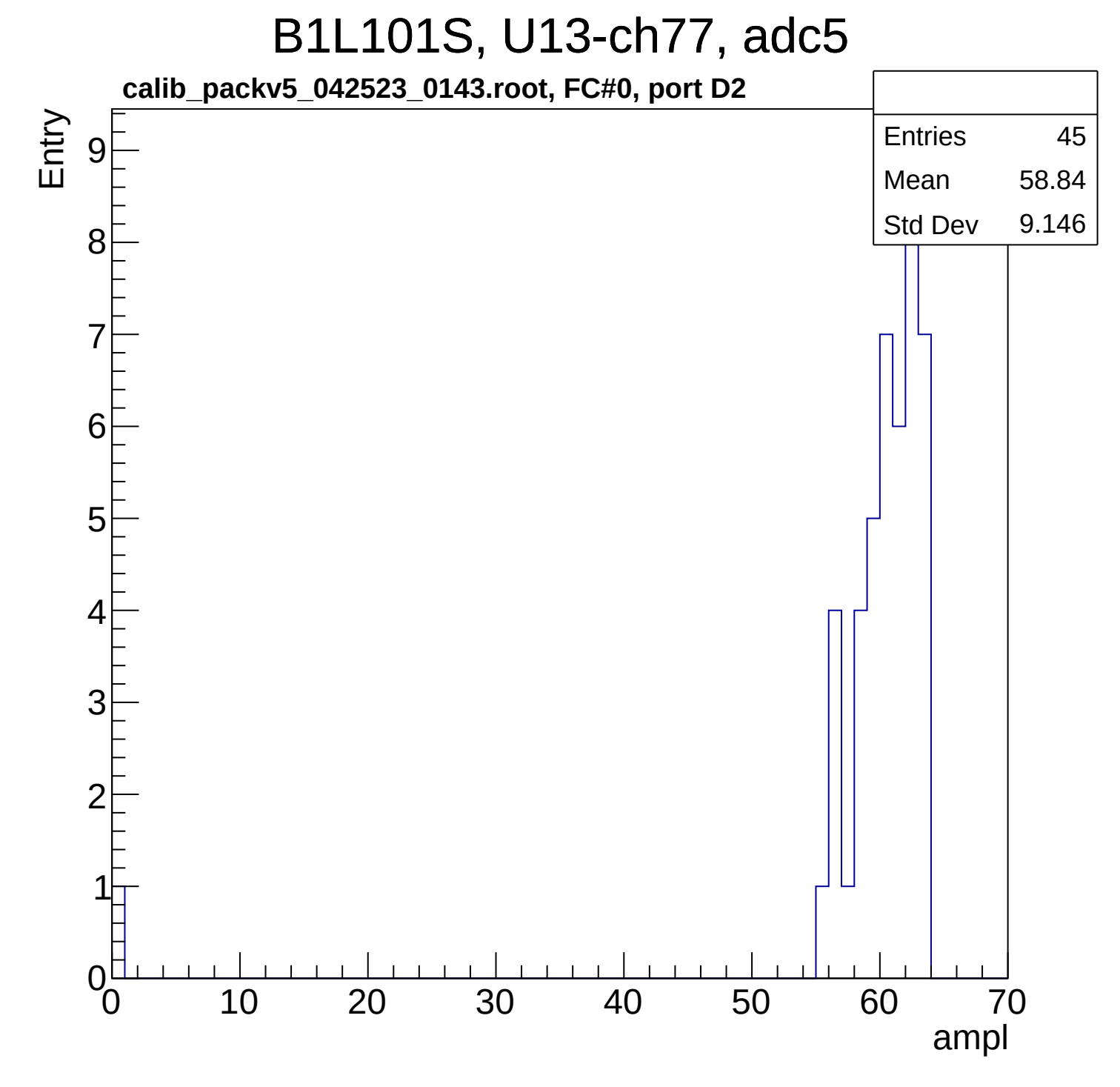

B1L101S, U13-ch77, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 56.62
Std Dev 2.923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch77, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 58.84
Std Dev 9.146
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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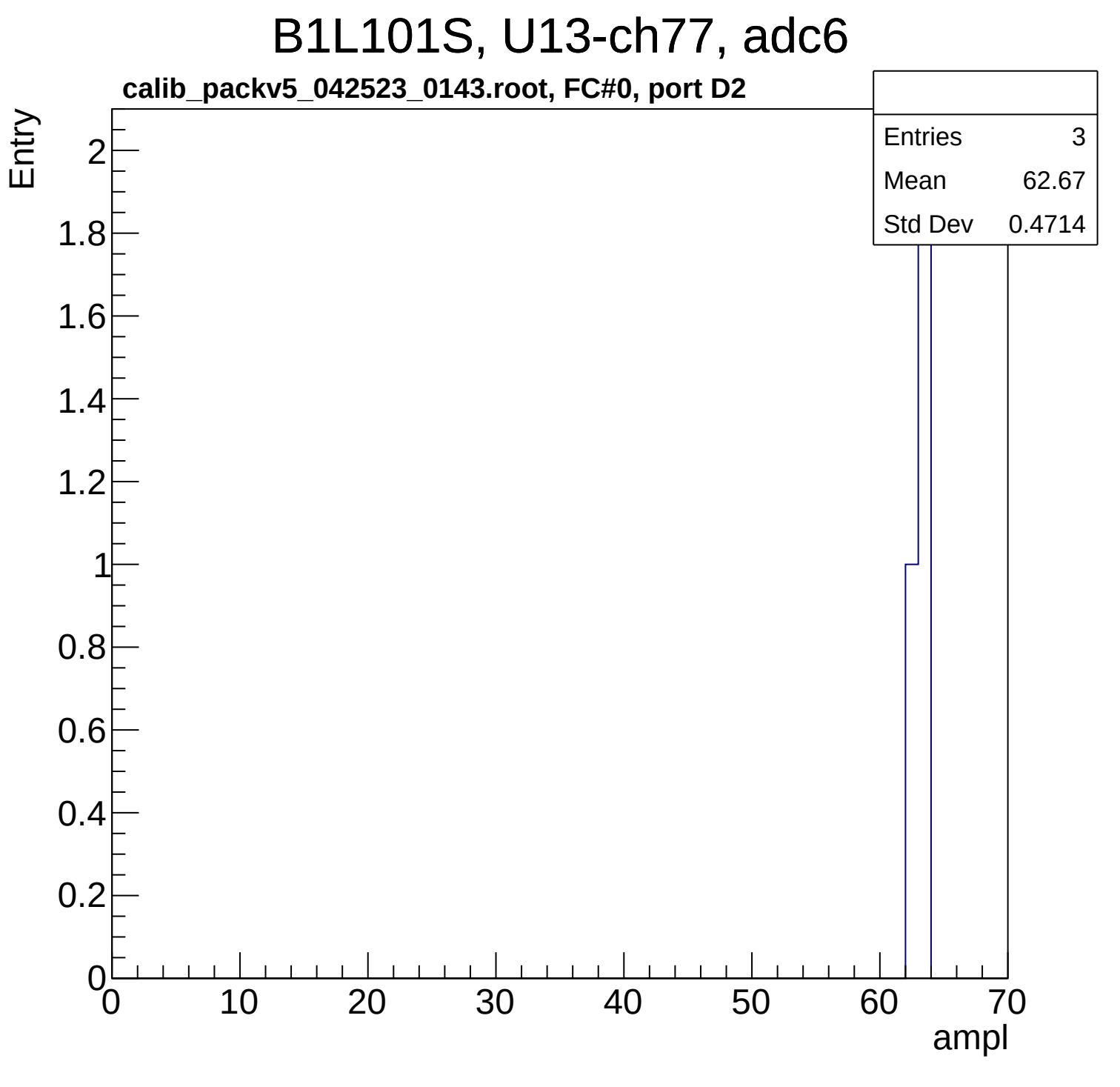
B1L101S, U13-ch77, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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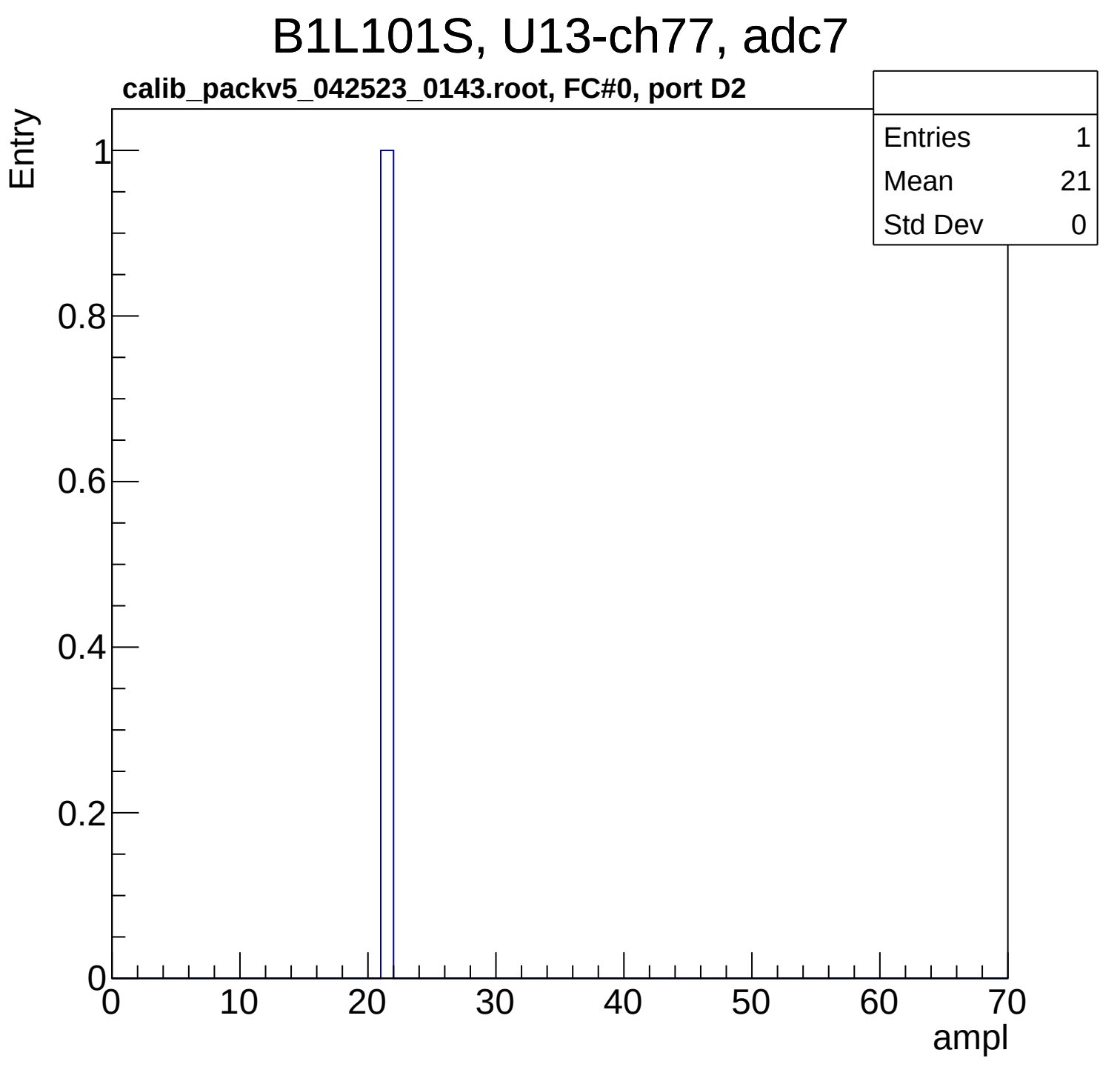
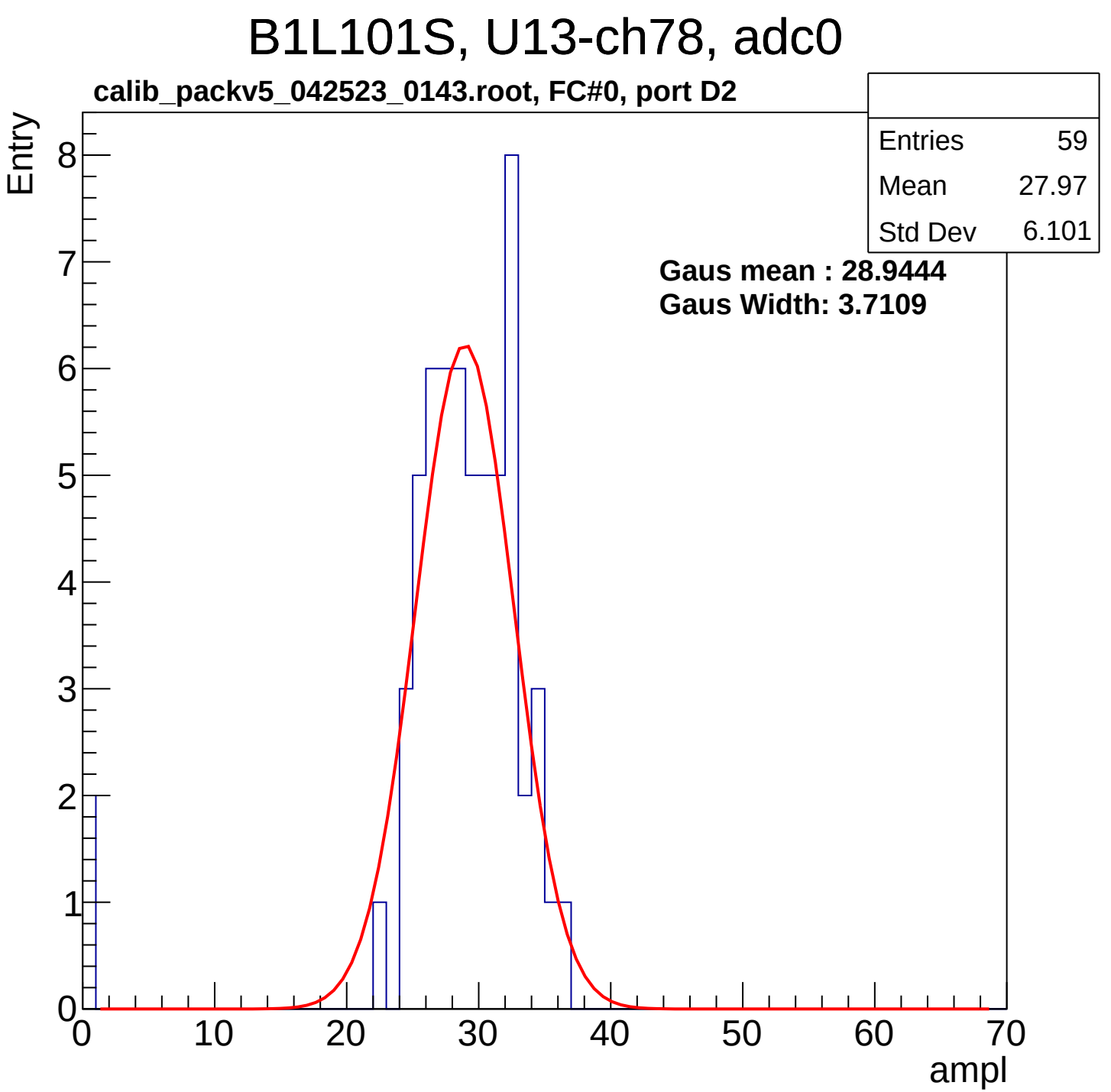
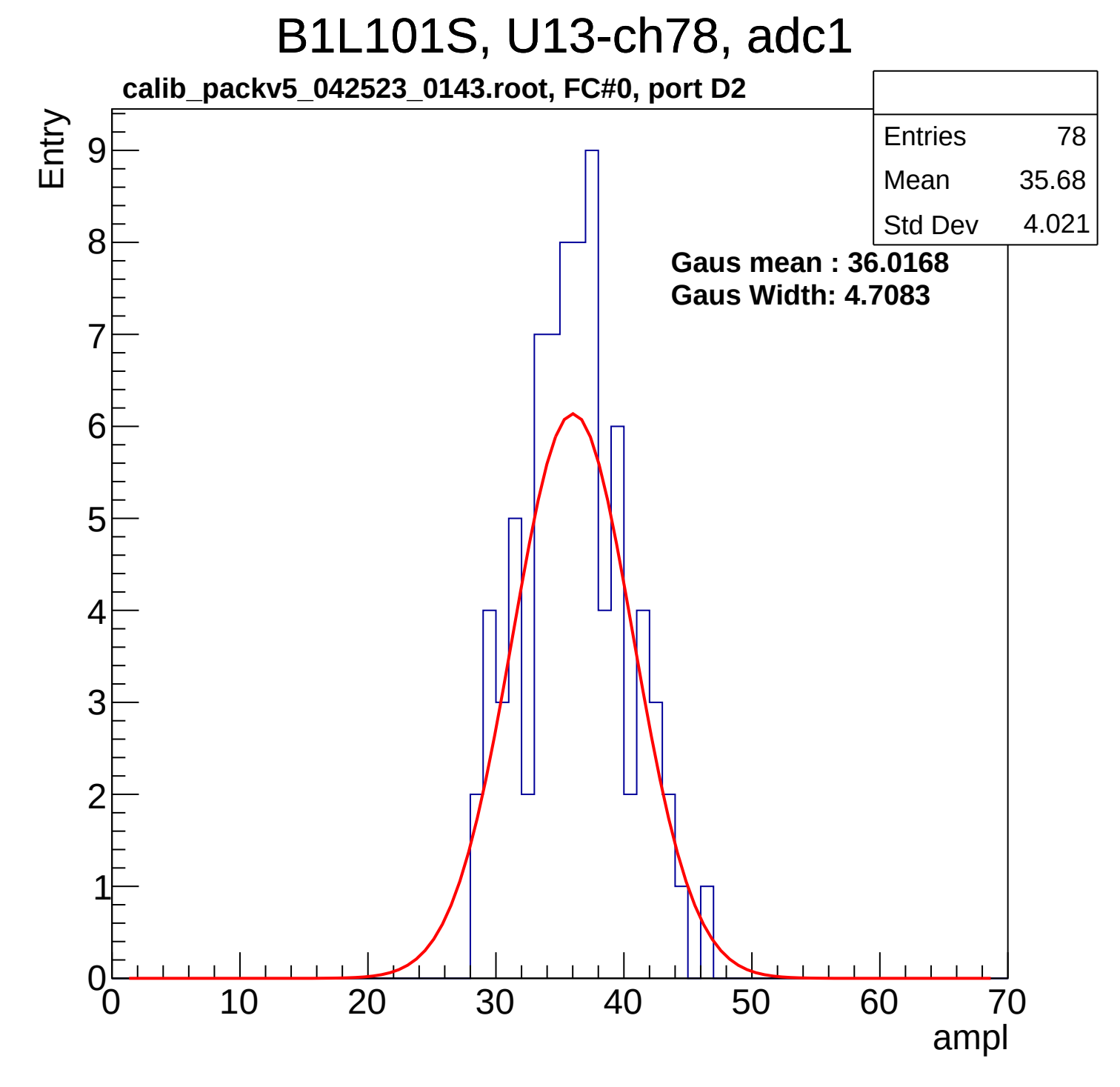
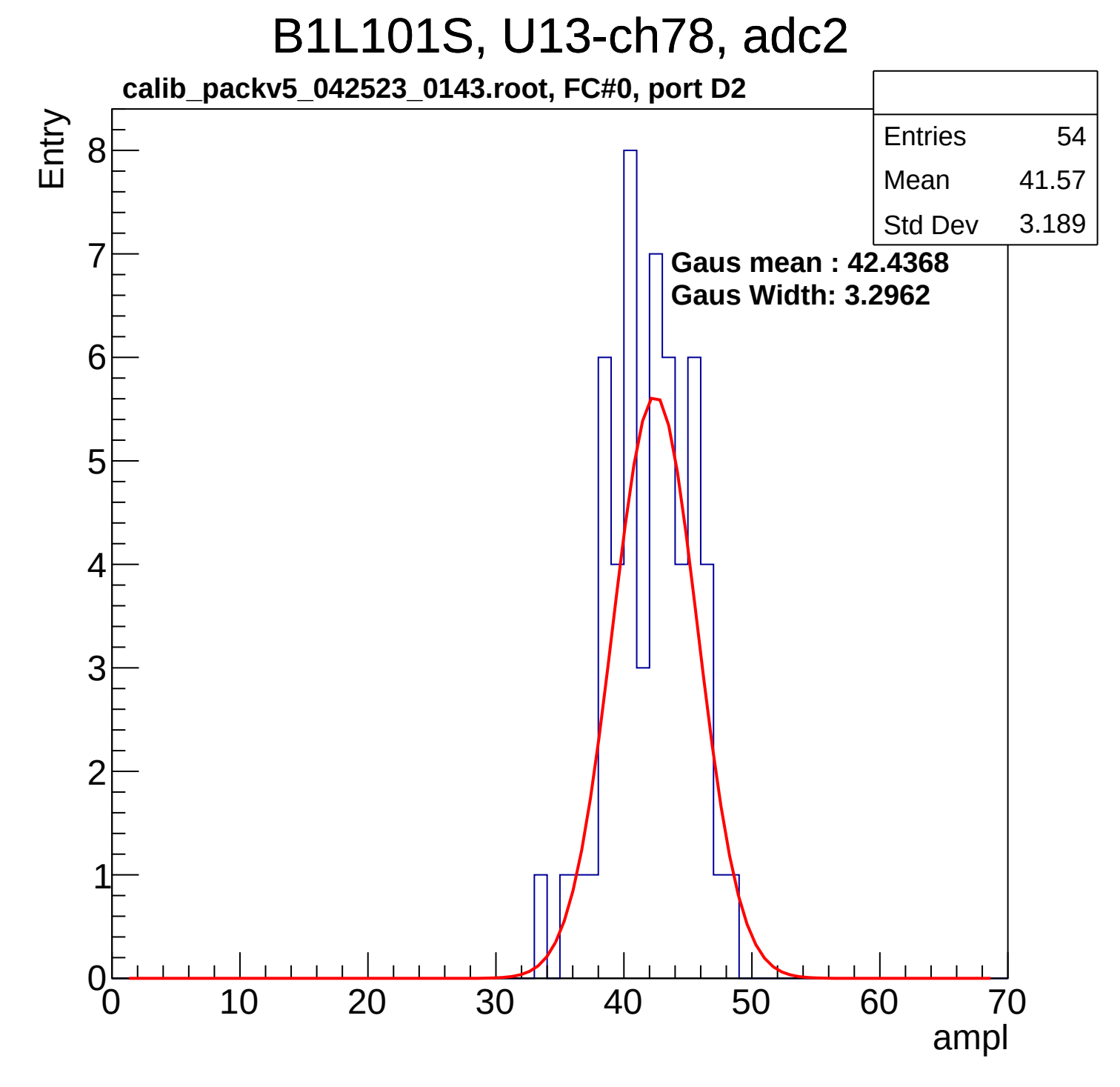
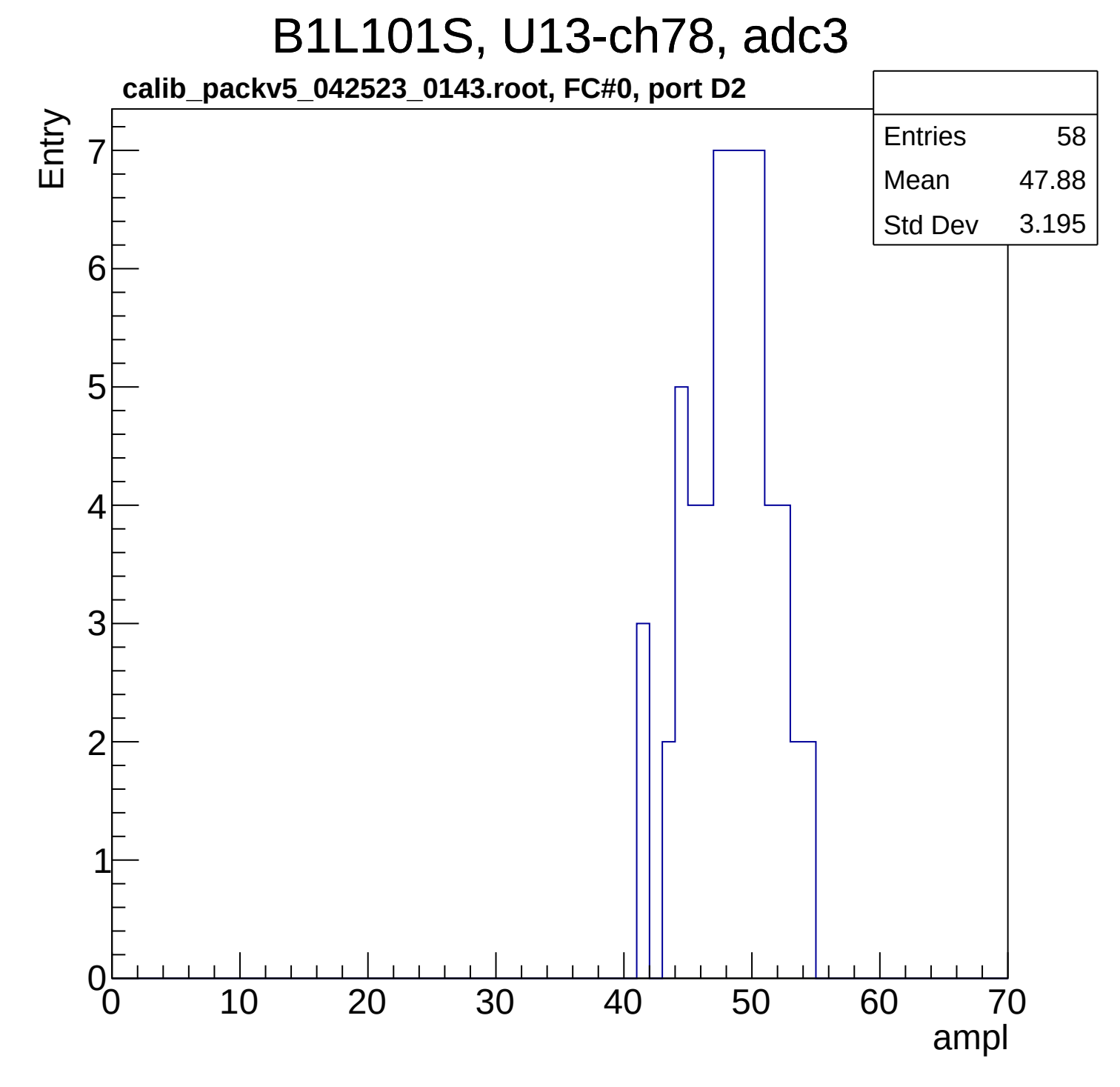
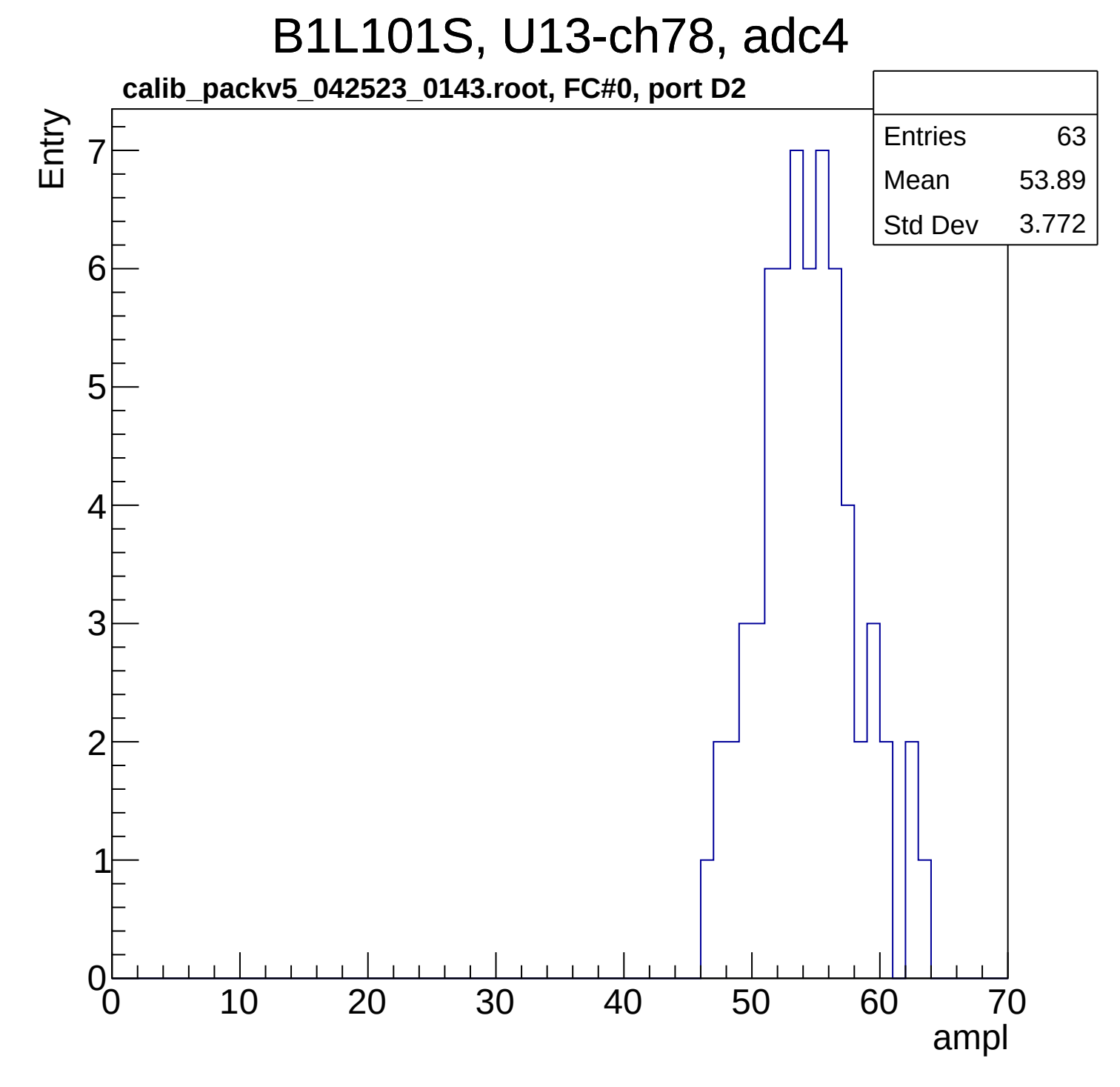

B1L101S, U13-ch77, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

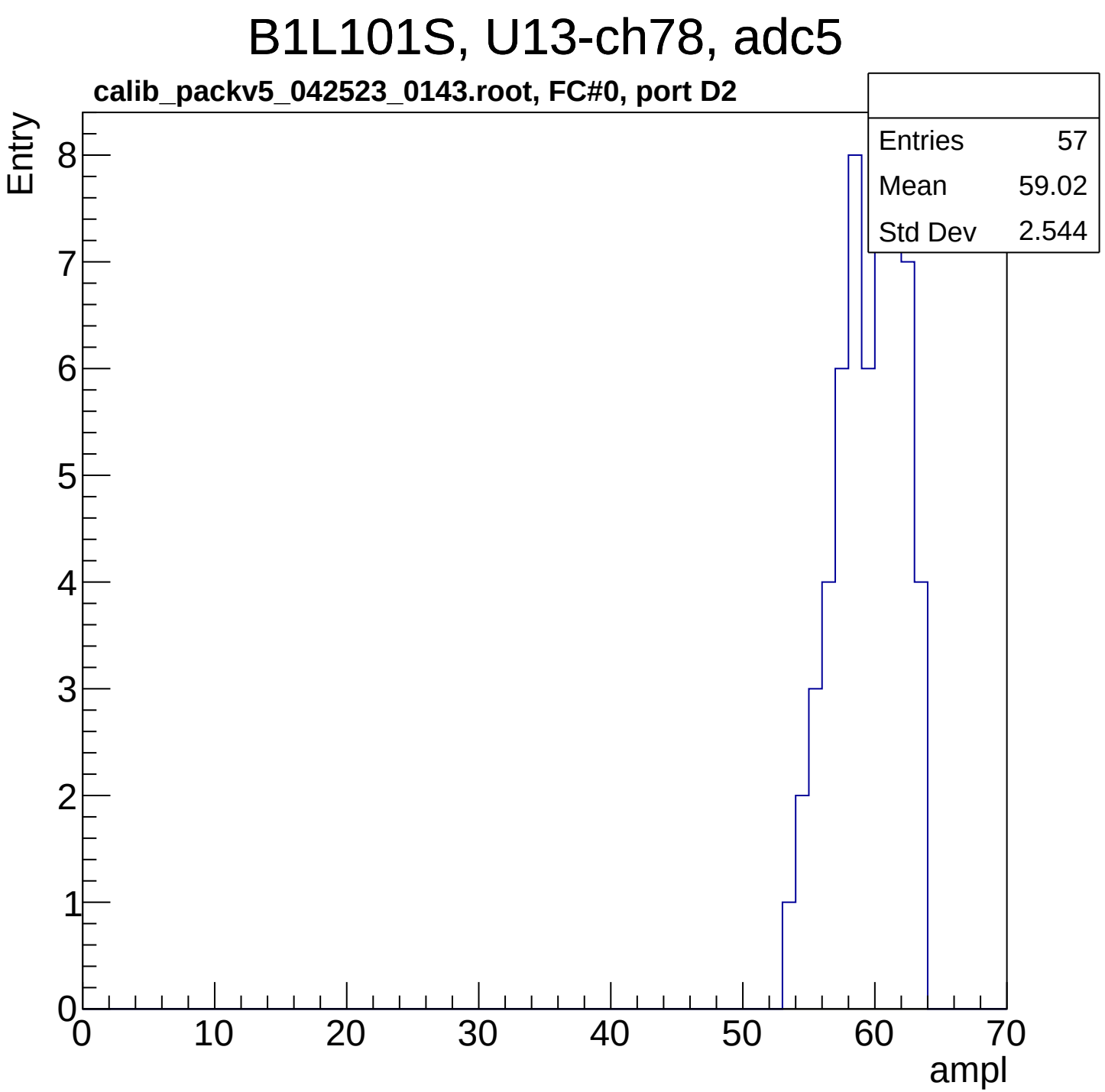

B1L101S, U13-ch78, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 27.97
Std Dev 6.101
Gaus mean : 28.9444
Gaus Width: 3.7109
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch78, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 35.68
Std Dev 4.021
Gaus mean : 36.0168
Gaus Width: 4.7083
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch78, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 41.57
Std Dev 3.189
Gaus mean : 42.4368
Gaus Width: 3.2962
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch78, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 47.88
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch78, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 53.89
Std Dev 3.772
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch78, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 59.02
Std Dev 2.544
Entry
ampl
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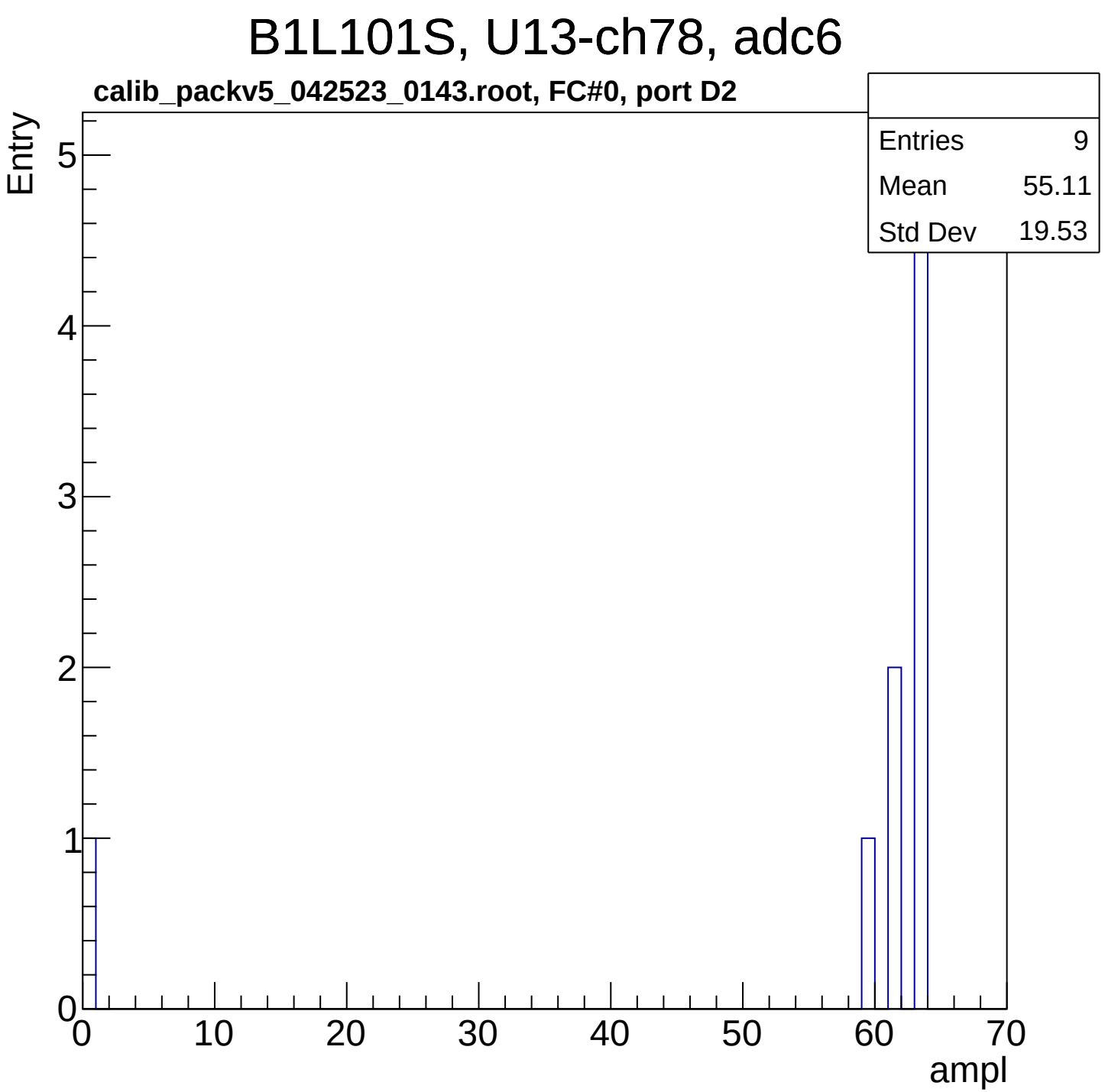
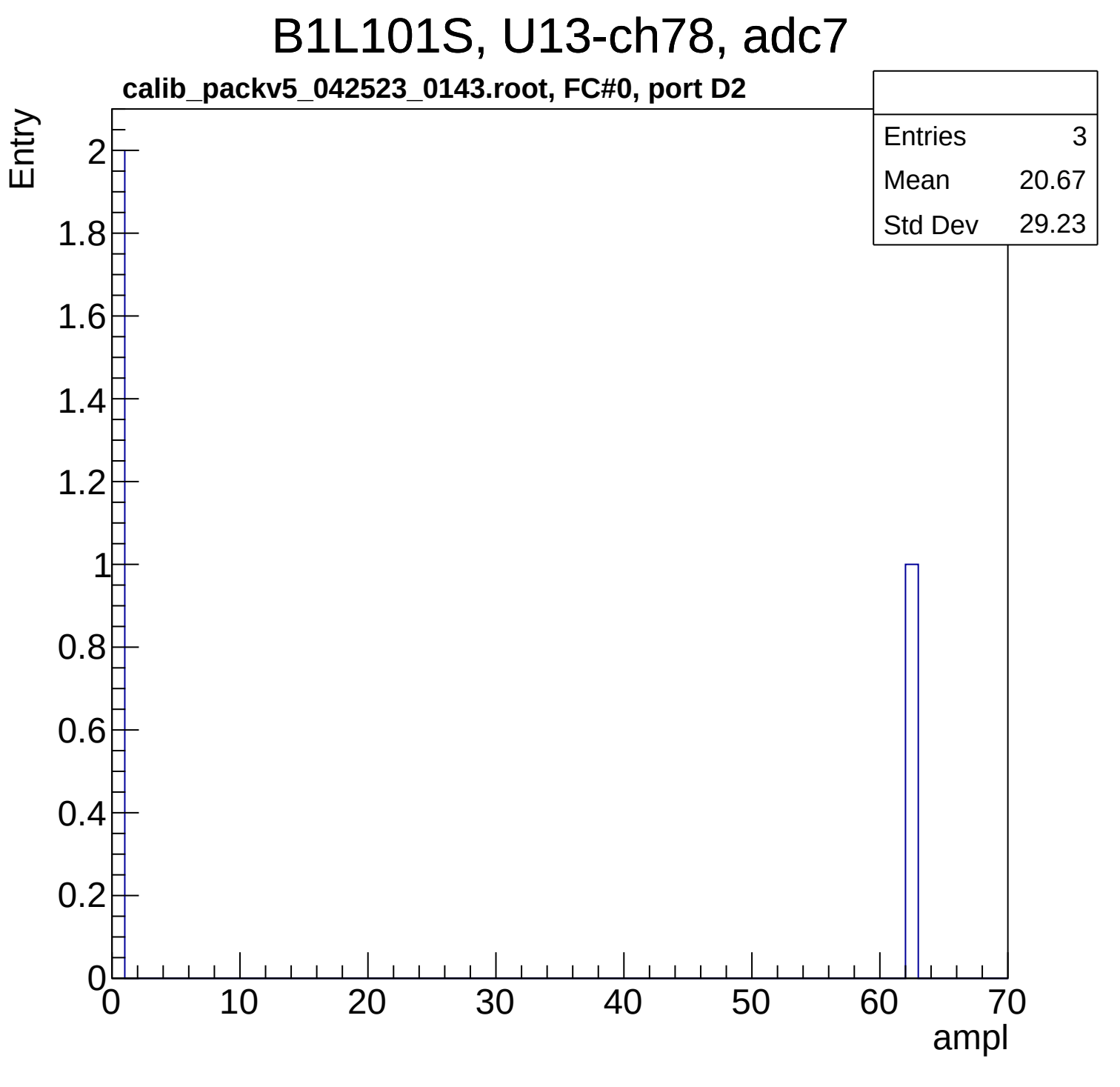
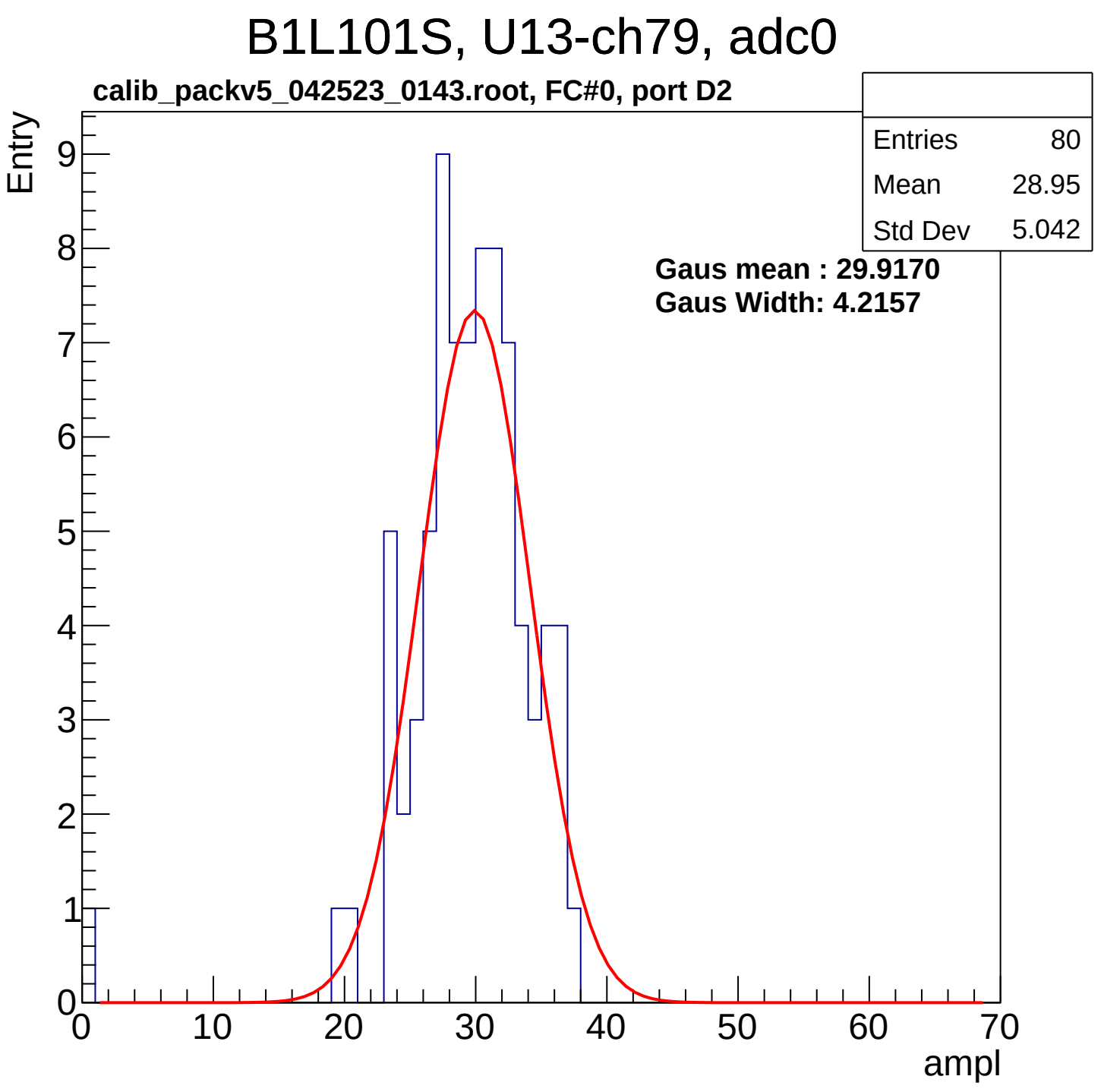
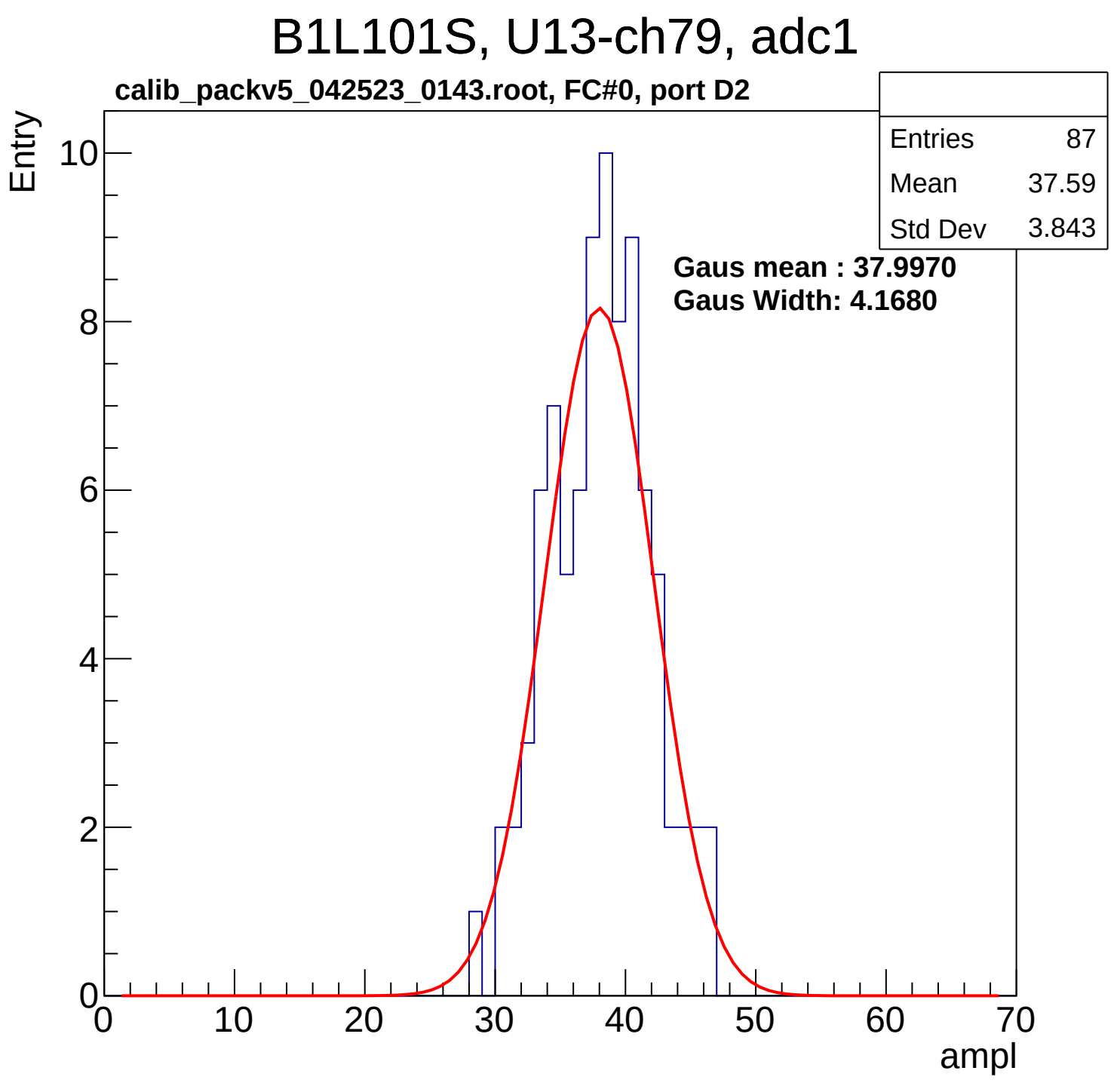
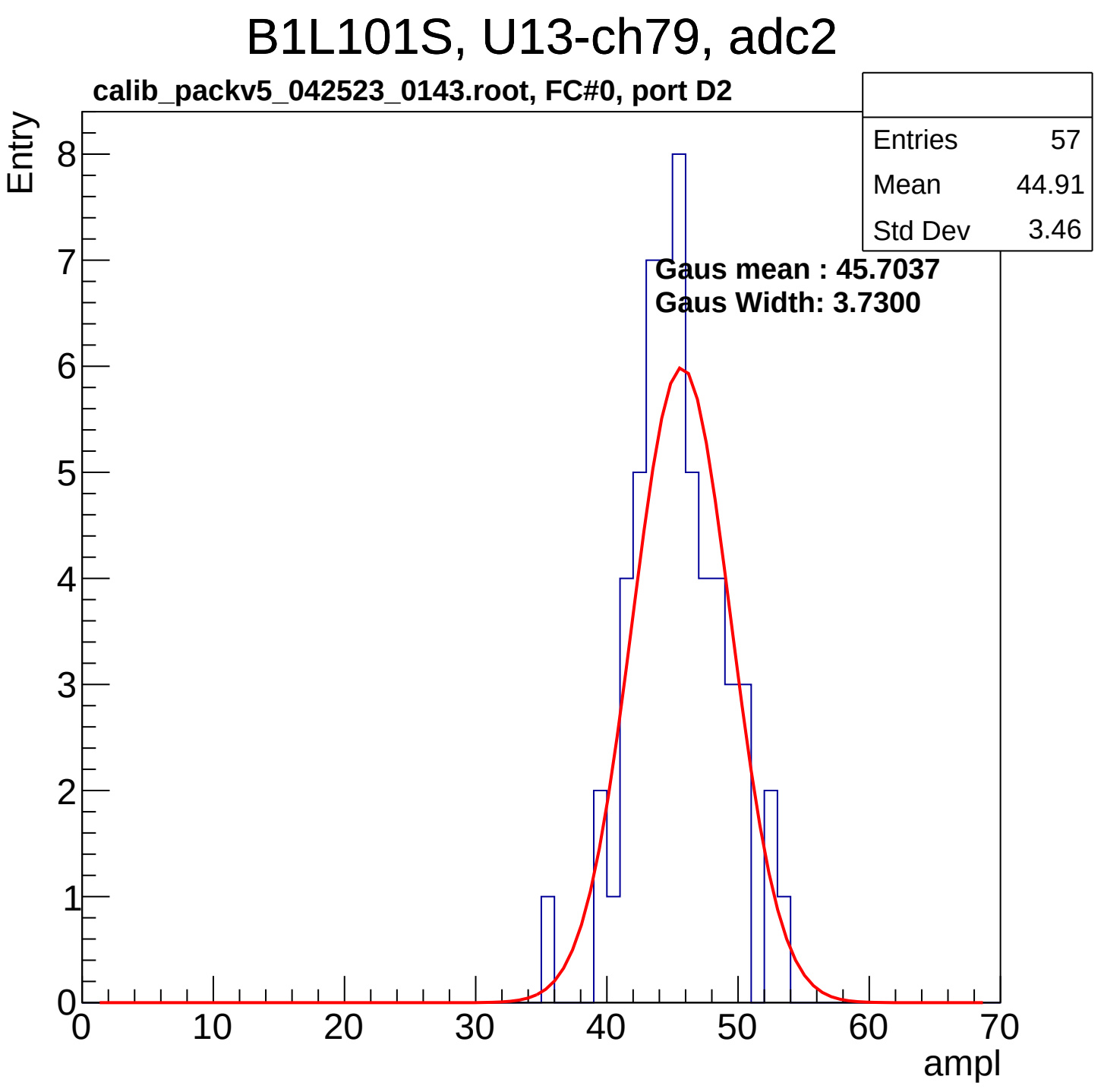
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

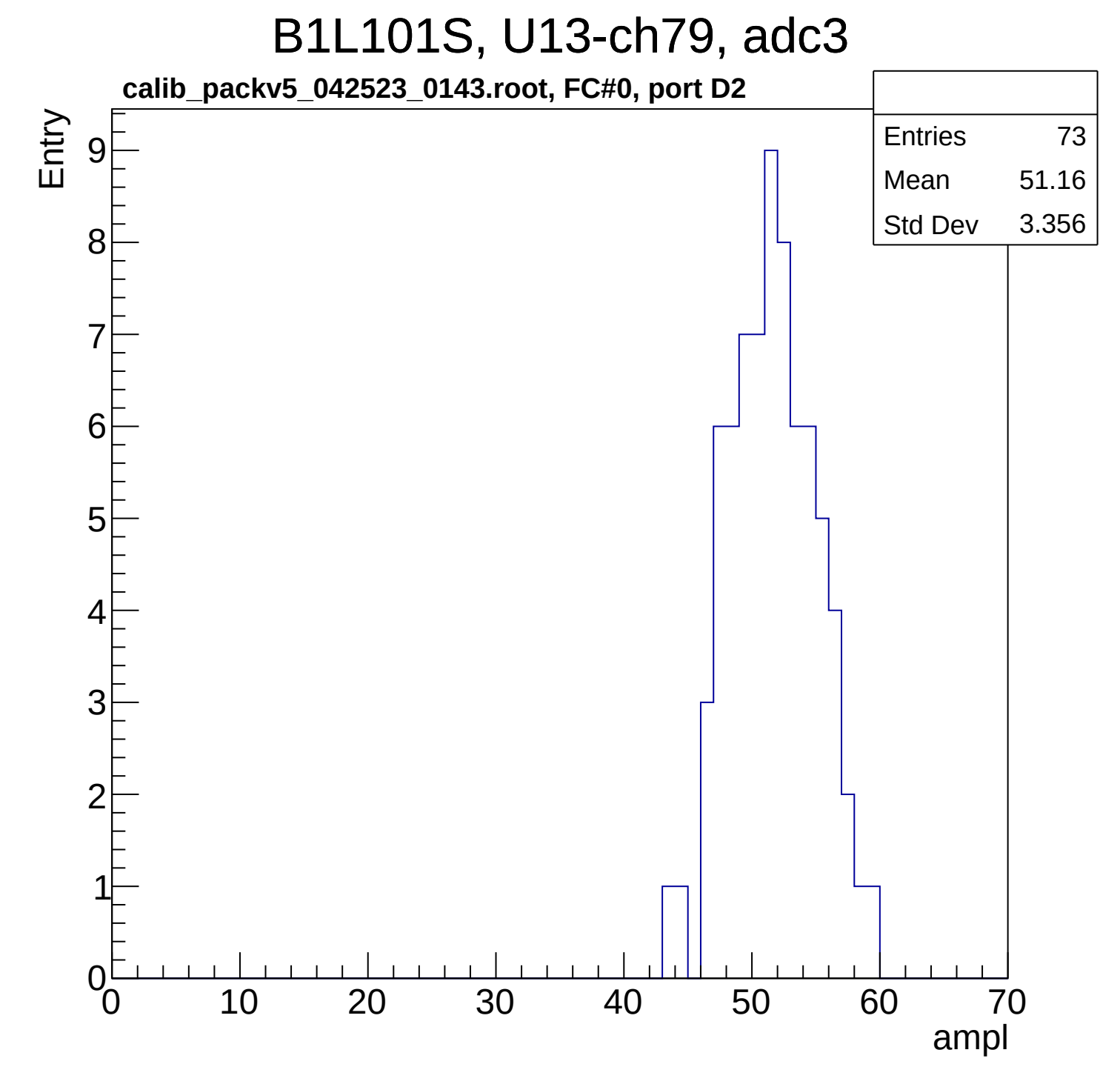

B1L101S, U13-ch78, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 9
Mean 55.11
Std Dev 19.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

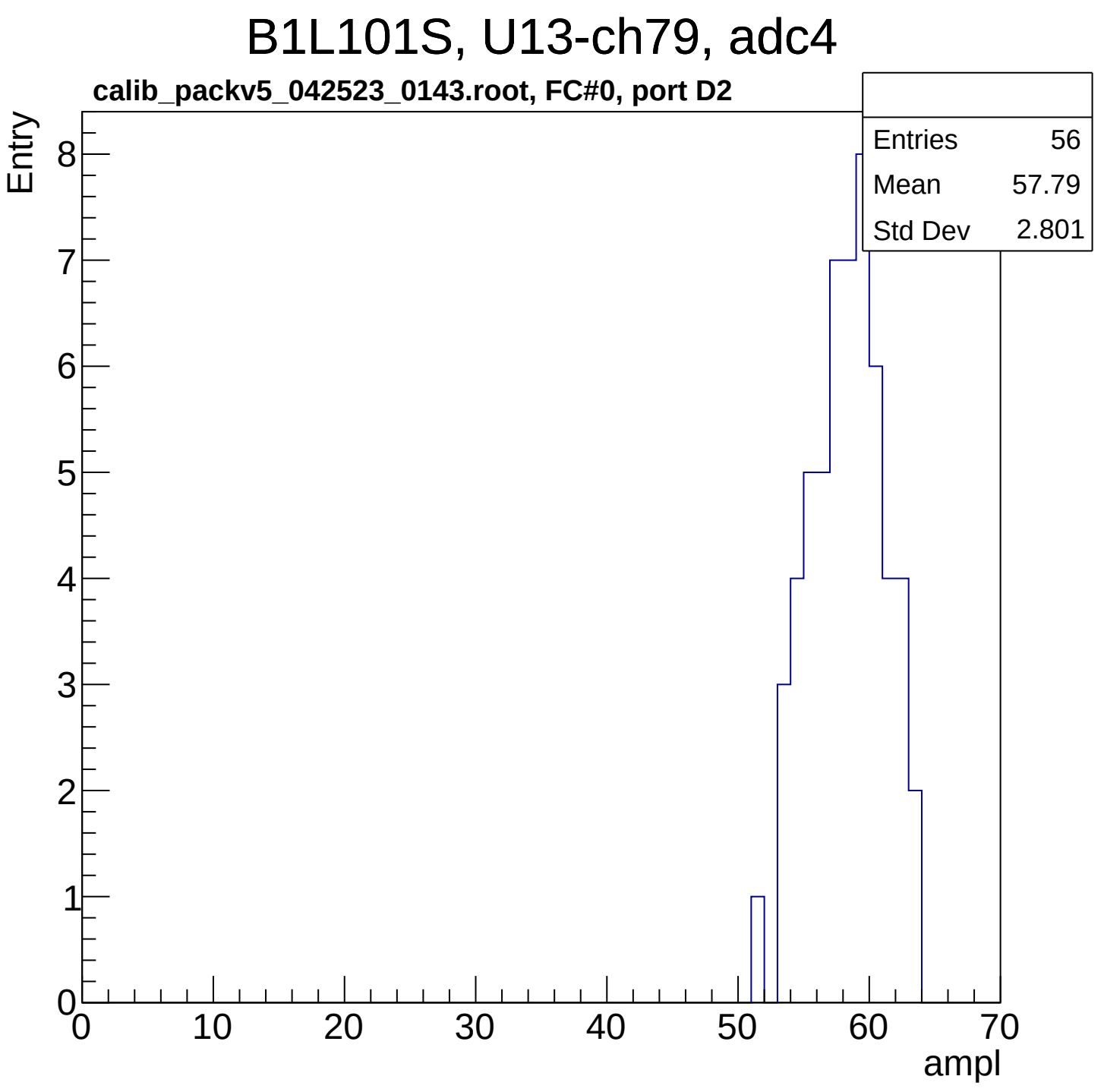

B1L101S, U13-ch78, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

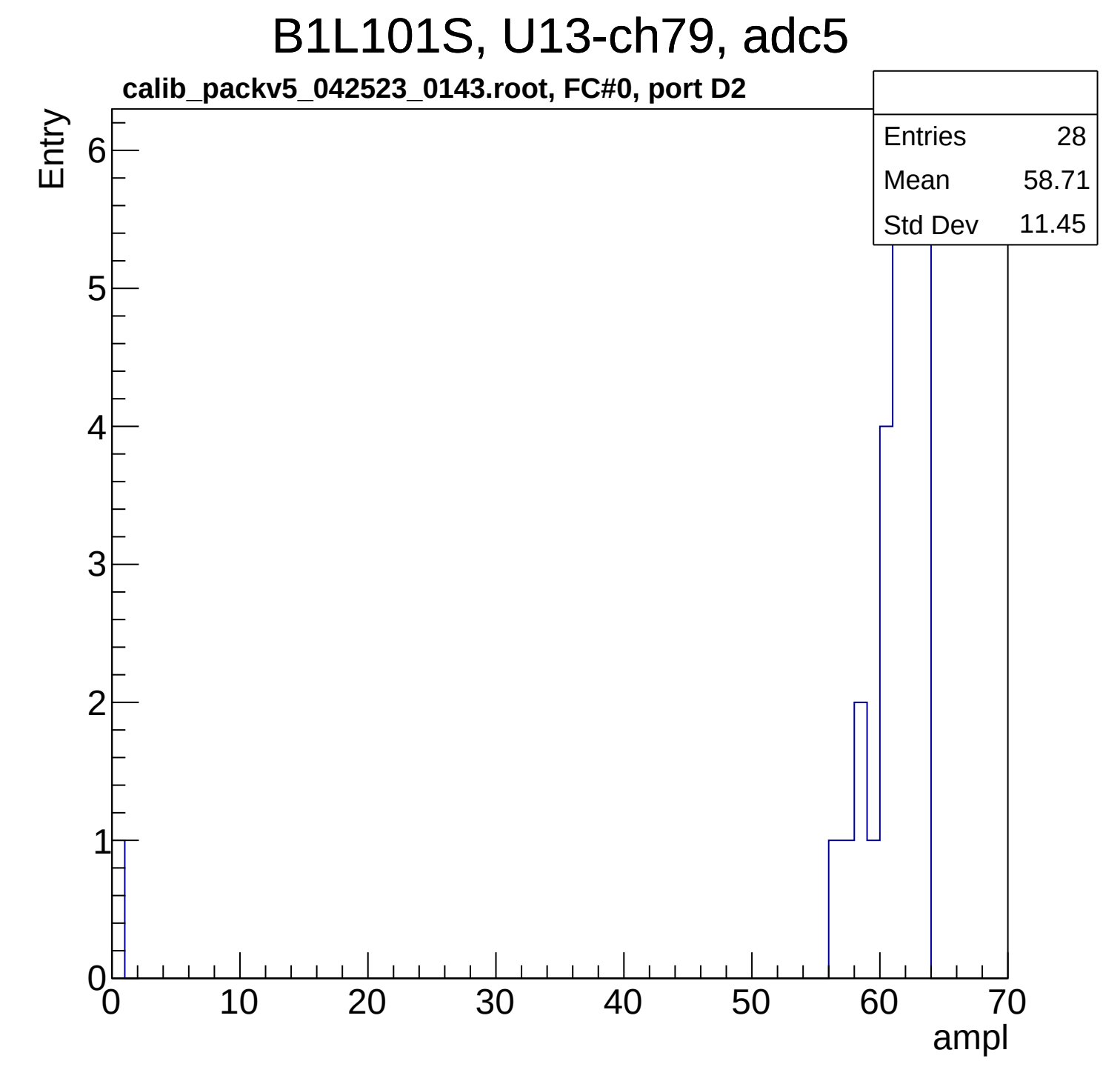

B1L101S, U13-ch79, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 28.95
Std Dev 5.042
Gaus mean : 29.9170
Gaus Width: 4.2157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

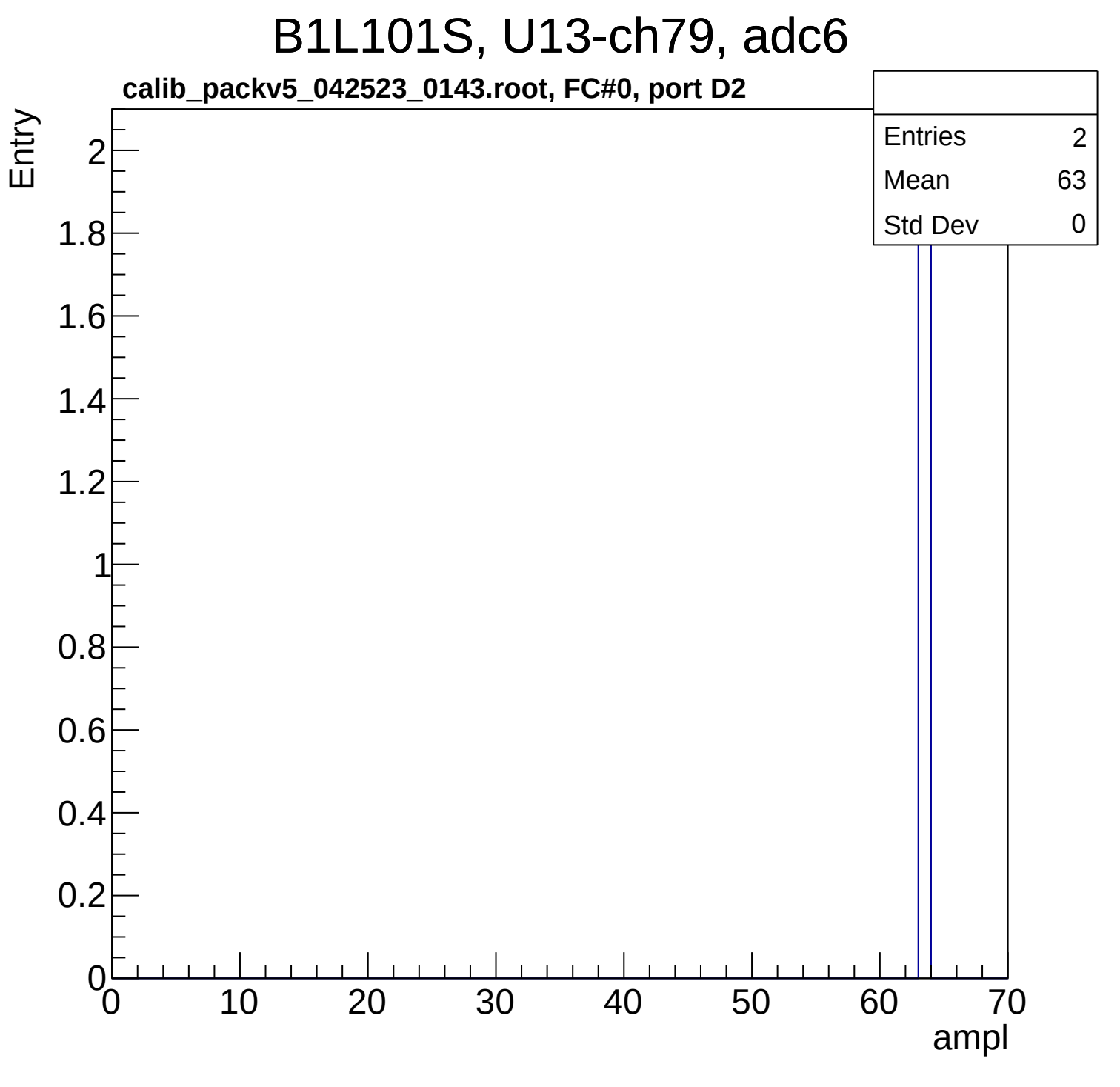

B1L101S, U13-ch79, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 87
Mean 37.59
Std Dev 3.843
Gaus mean : 37.9970
Gaus Width: 4.1680
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

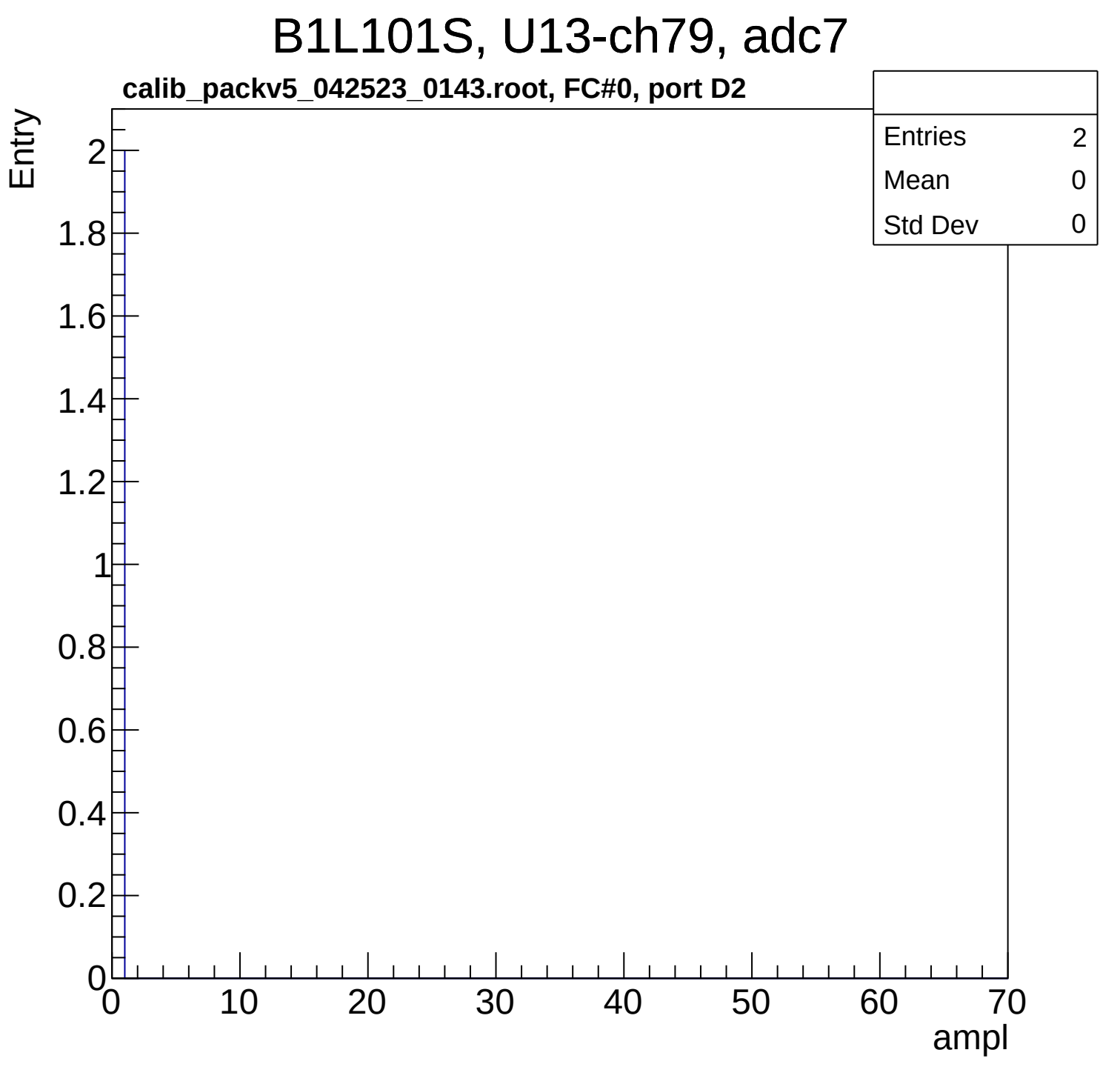

B1L101S, U13-ch79, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 44.91
Std Dev 3.46
Gaus mean : 45.7037
Gaus Width: 3.7300
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

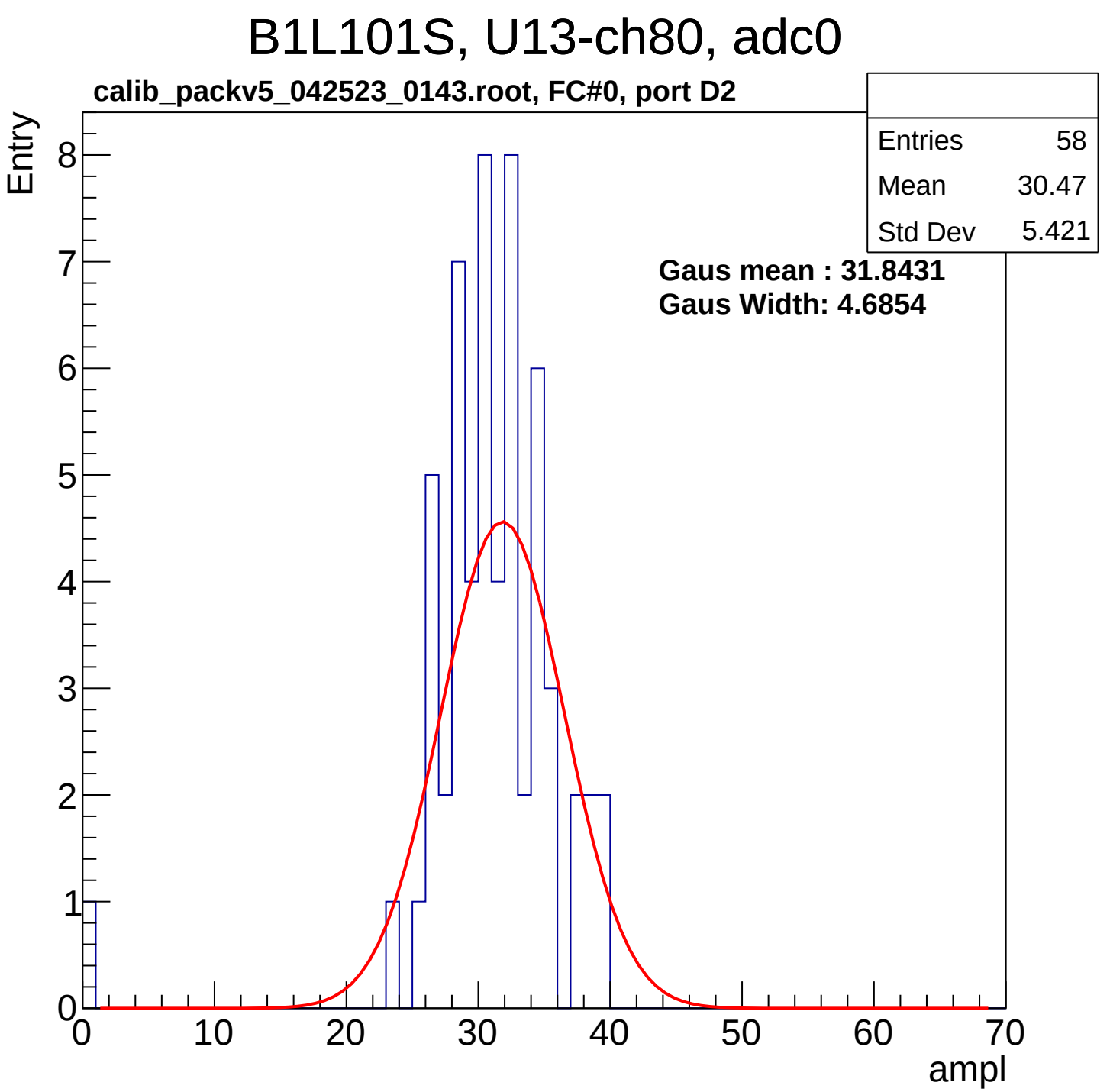

B1L101S, U13-ch79, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 51.16
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch79, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 57.79
Std Dev 2.801
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch79, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 28
Mean 58.71
Std Dev 11.45
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch79, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch79, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch80, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 30.47
Std Dev 5.421
Gaus mean : 31.8431
Gaus Width: 4.6854
Entry
ampl
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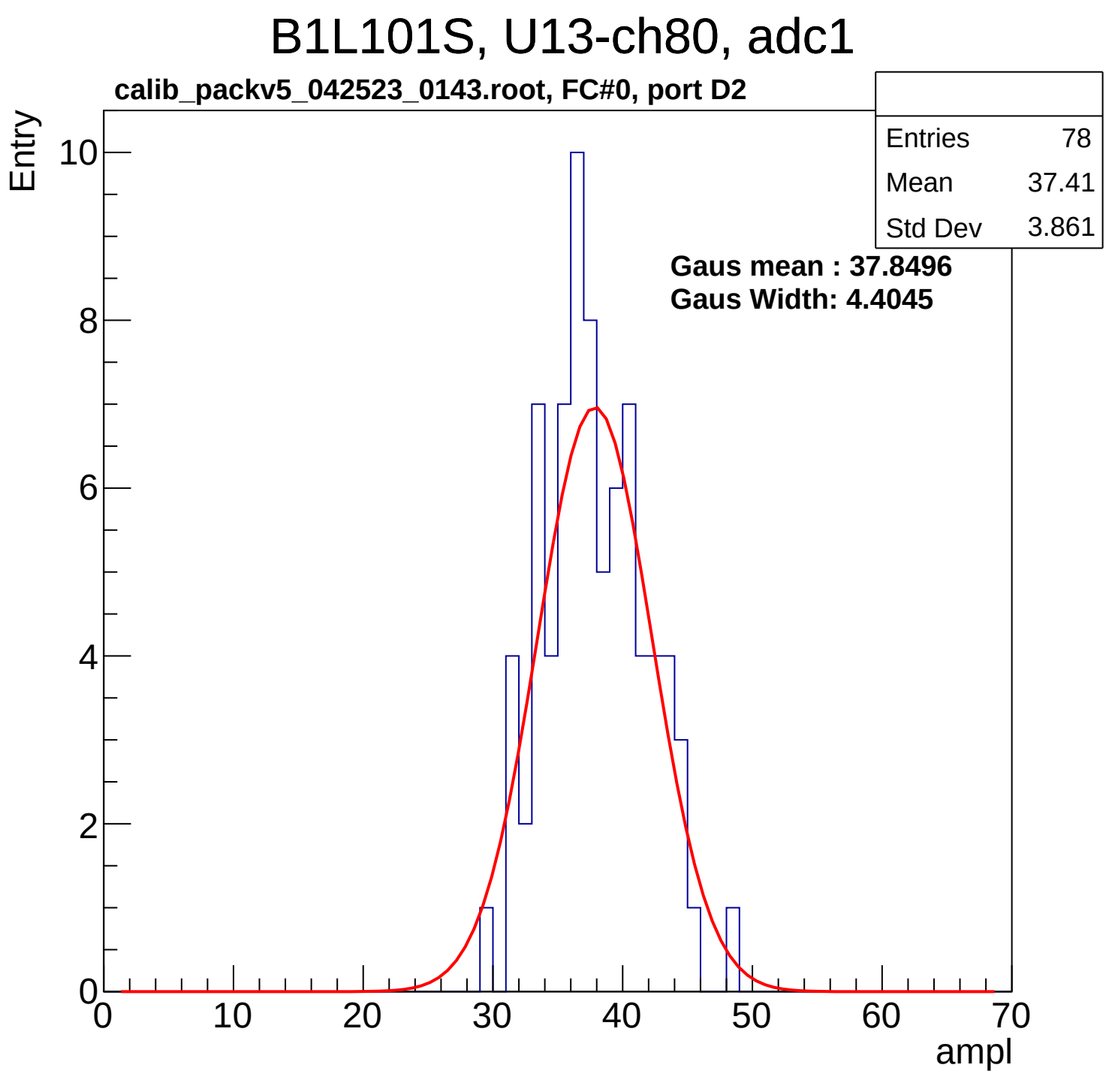
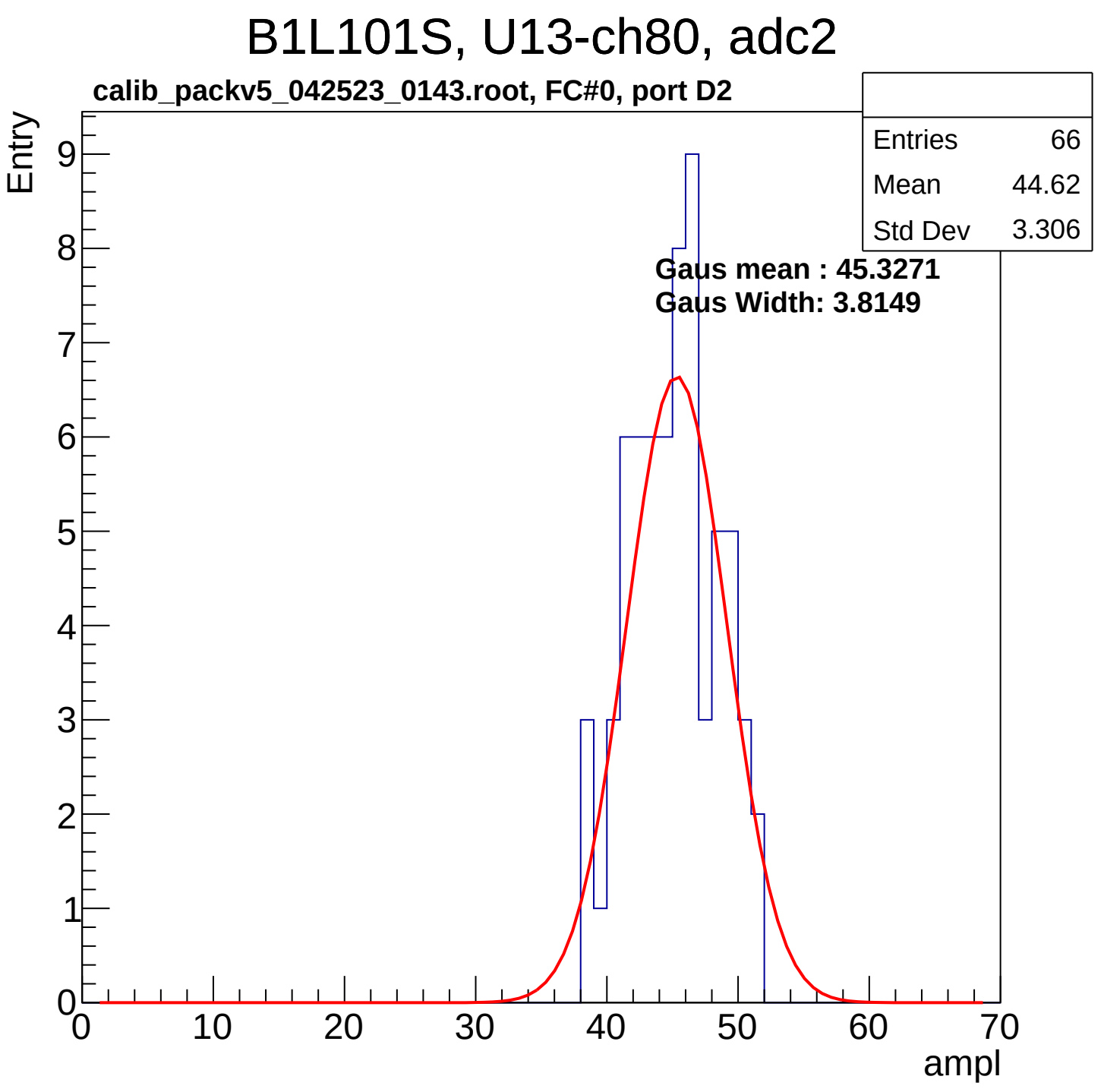
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch80, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 37.41
Std Dev 3.861
Gaus mean : 37.8496
Gaus Width: 4.4045
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch80, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 44.62
Std Dev 3.306
Gaus mean : 45.3271
Gaus Width: 3.8149
Entry
ampl
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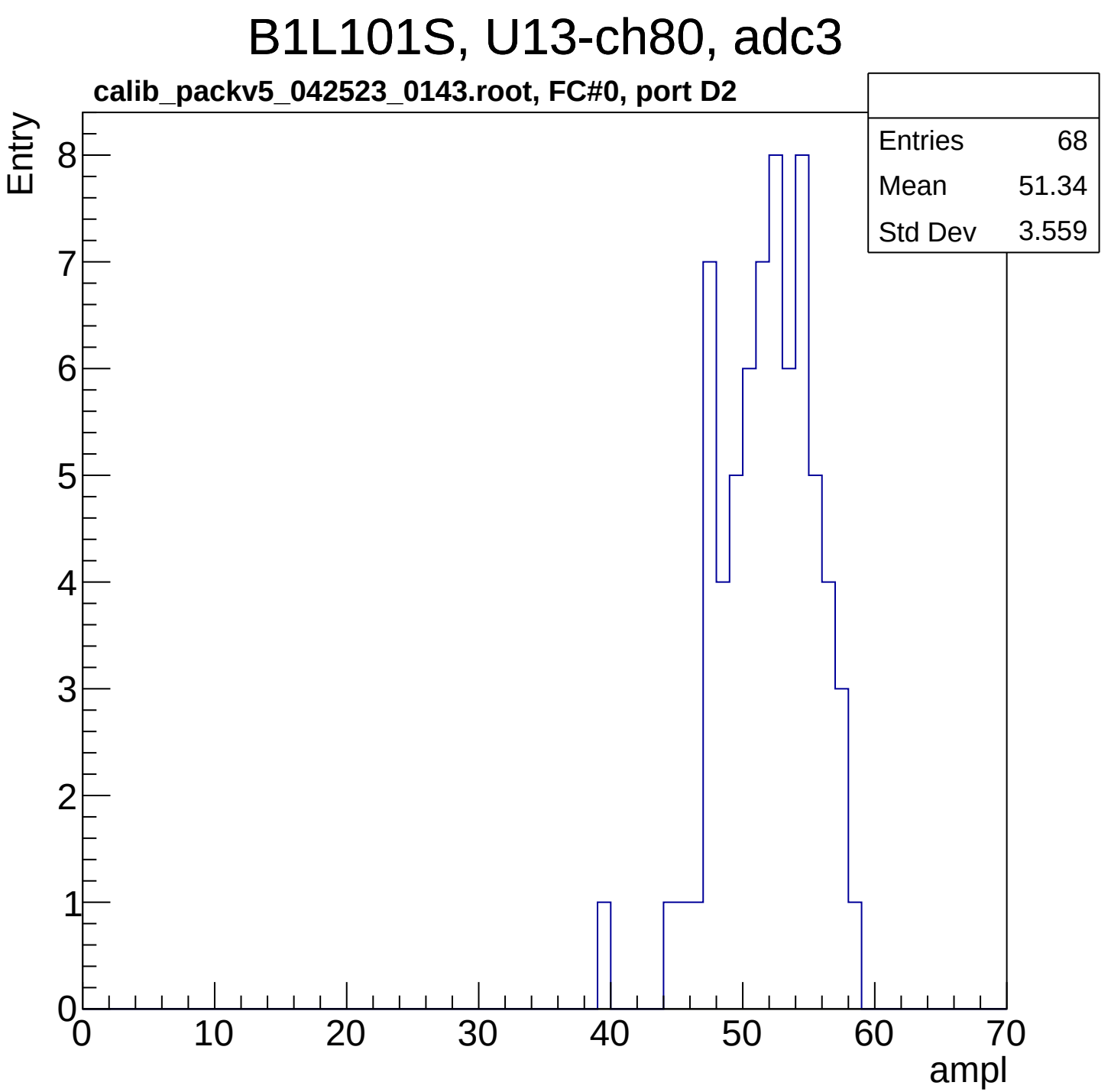
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch80, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 51.34
Std Dev 3.559
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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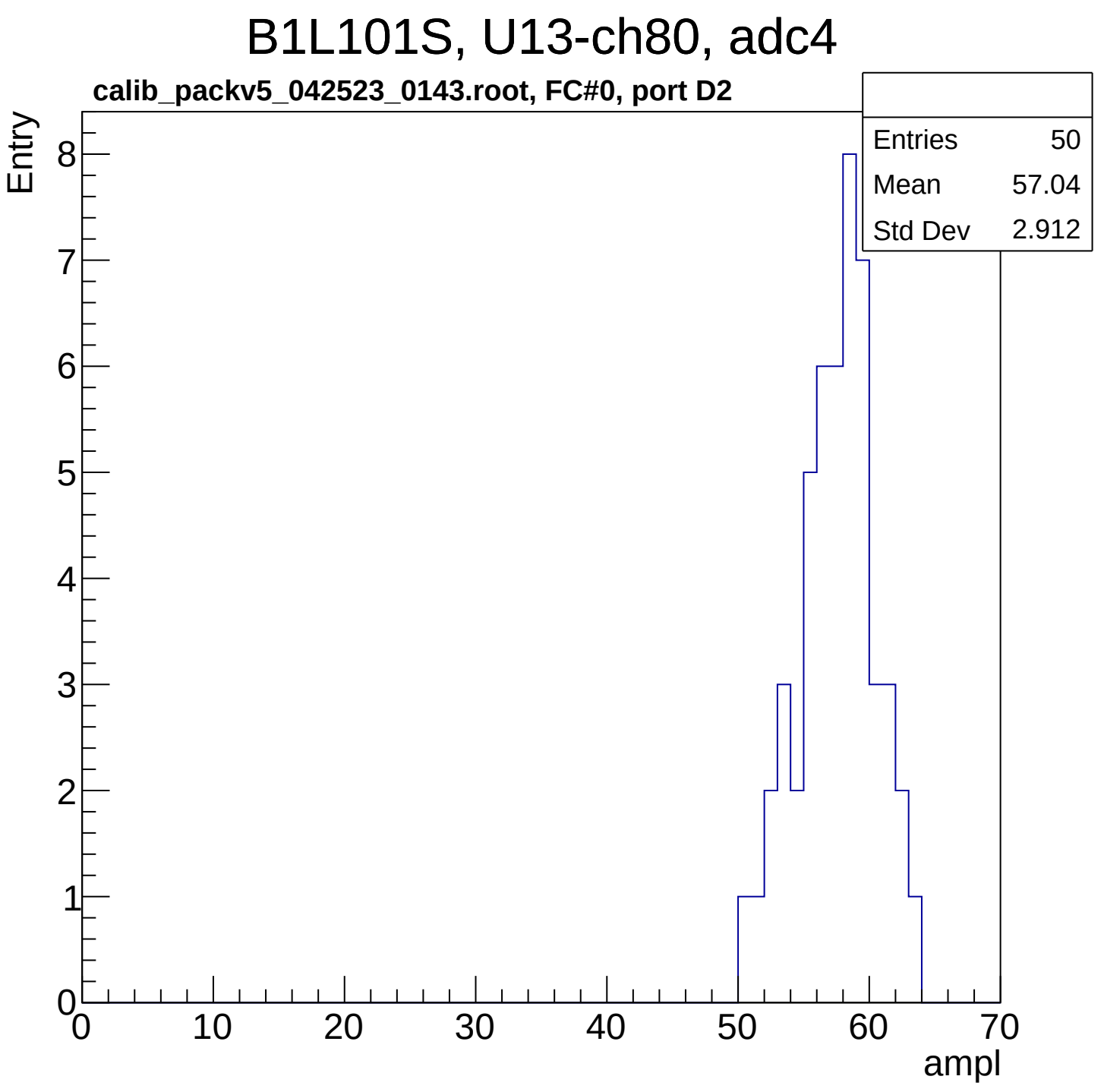
B1L101S, U13-ch80, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 50
Mean 57.04
Std Dev 2.912
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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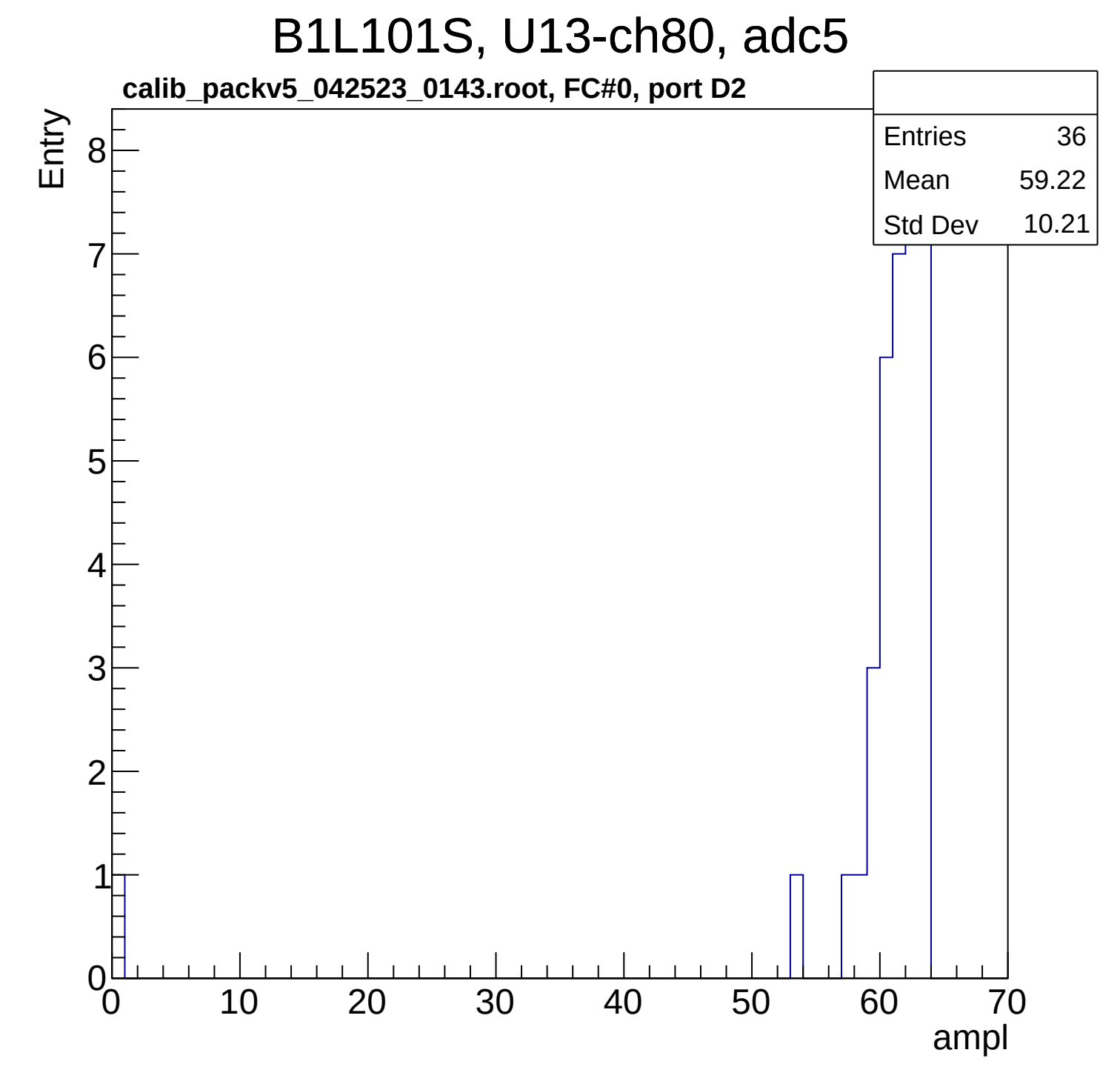
40
50
60
70


B1L101S, U13-ch80, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 36
Mean 59.22
Std Dev 10.21
Entry
ampl
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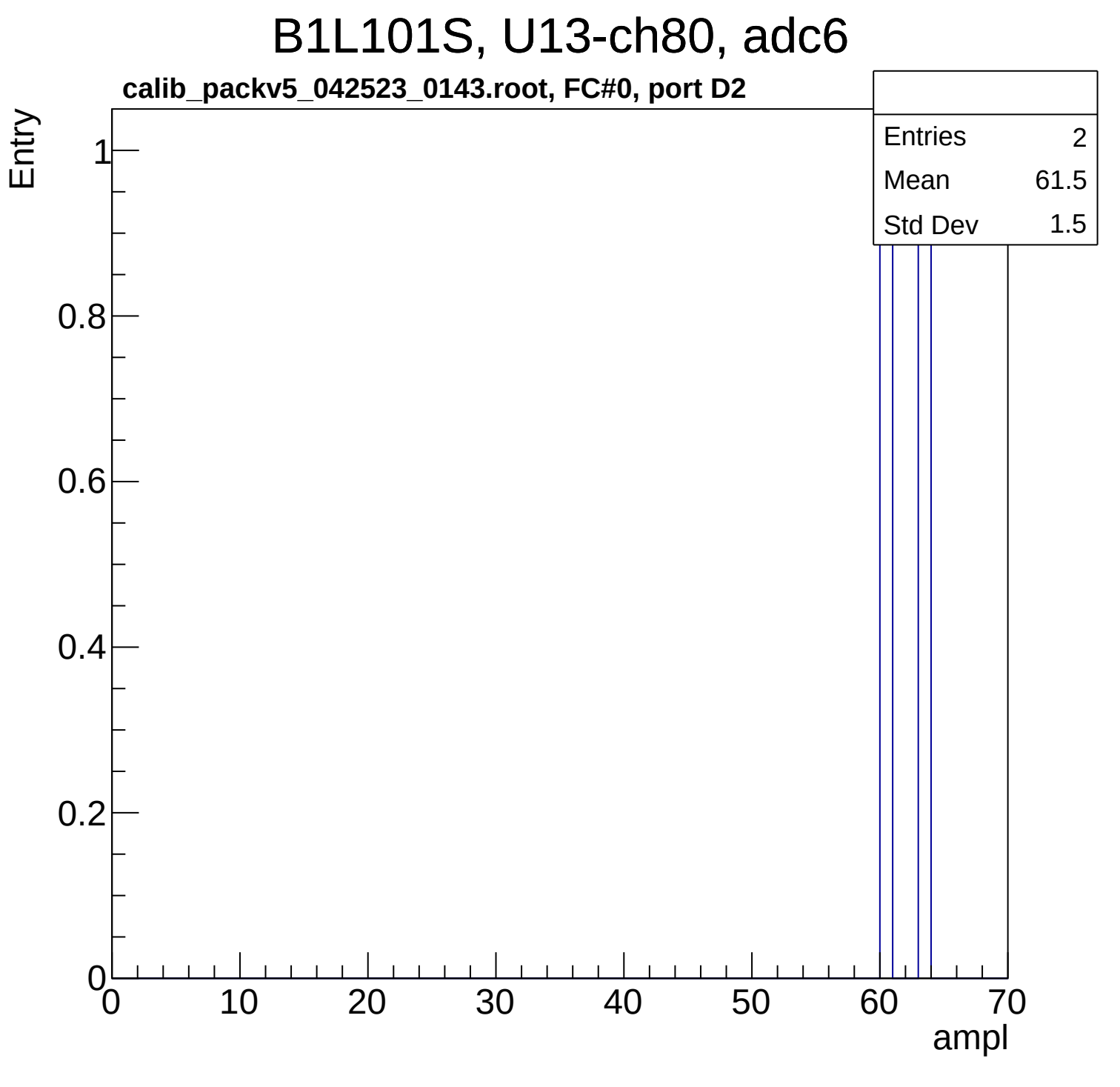
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch80, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
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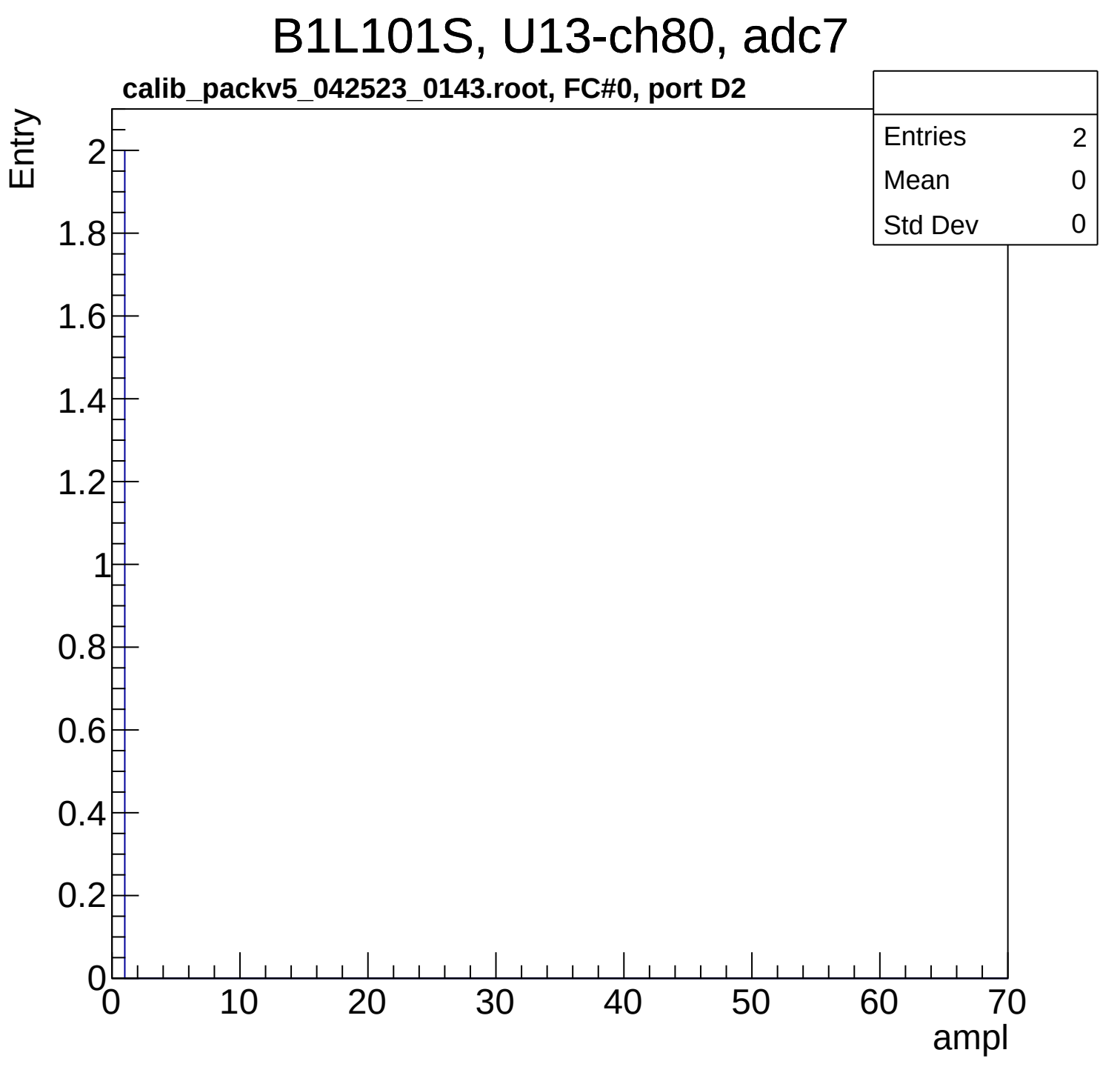
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch80, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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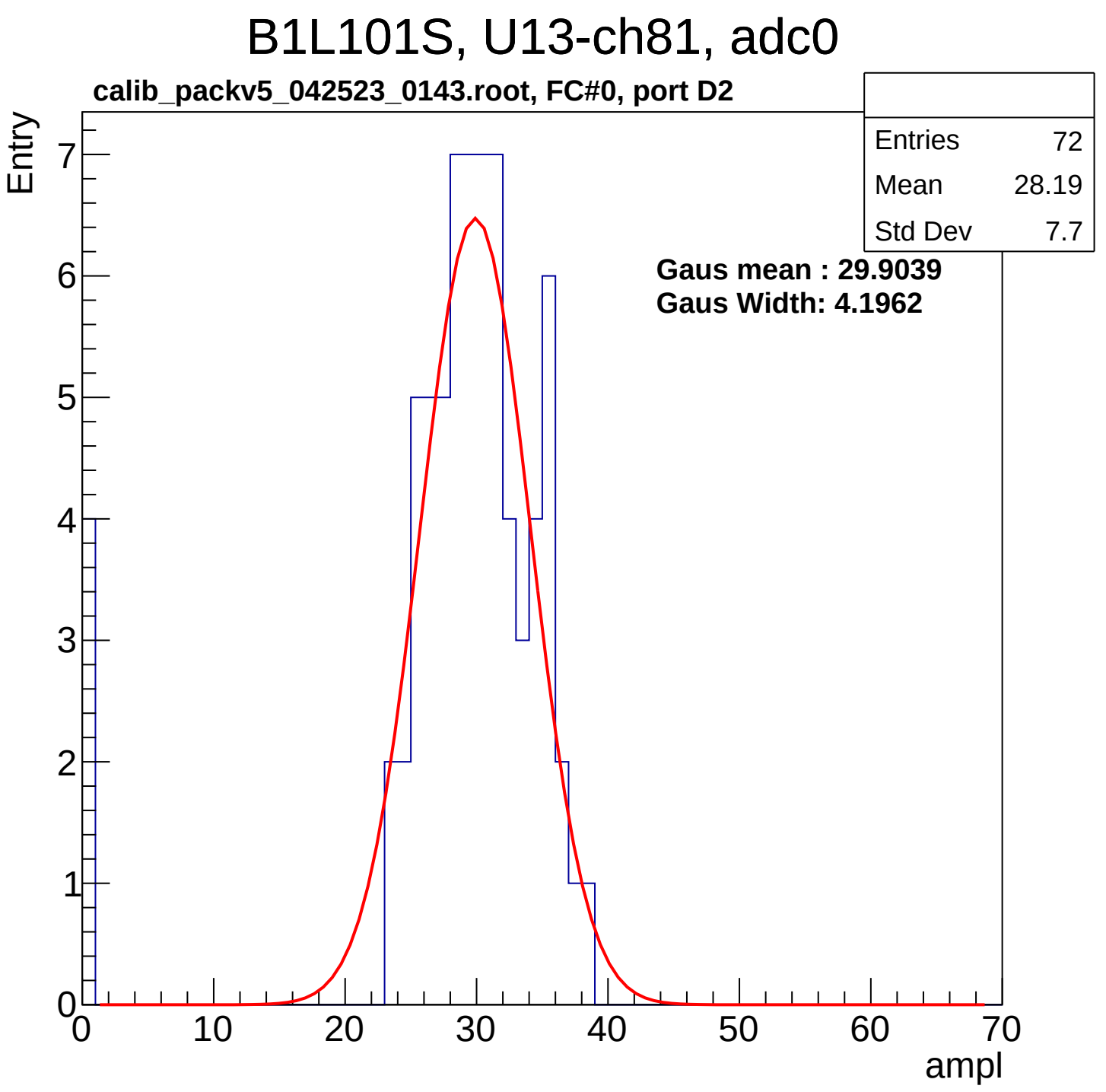
B1L101S, U13-ch81, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 28.19
Std Dev 7.7
Gaus mean : 29.9039
Gaus Width: 4.1962
Entry
ampl
0
1
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3
4
5
6
7
0
10
20
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60
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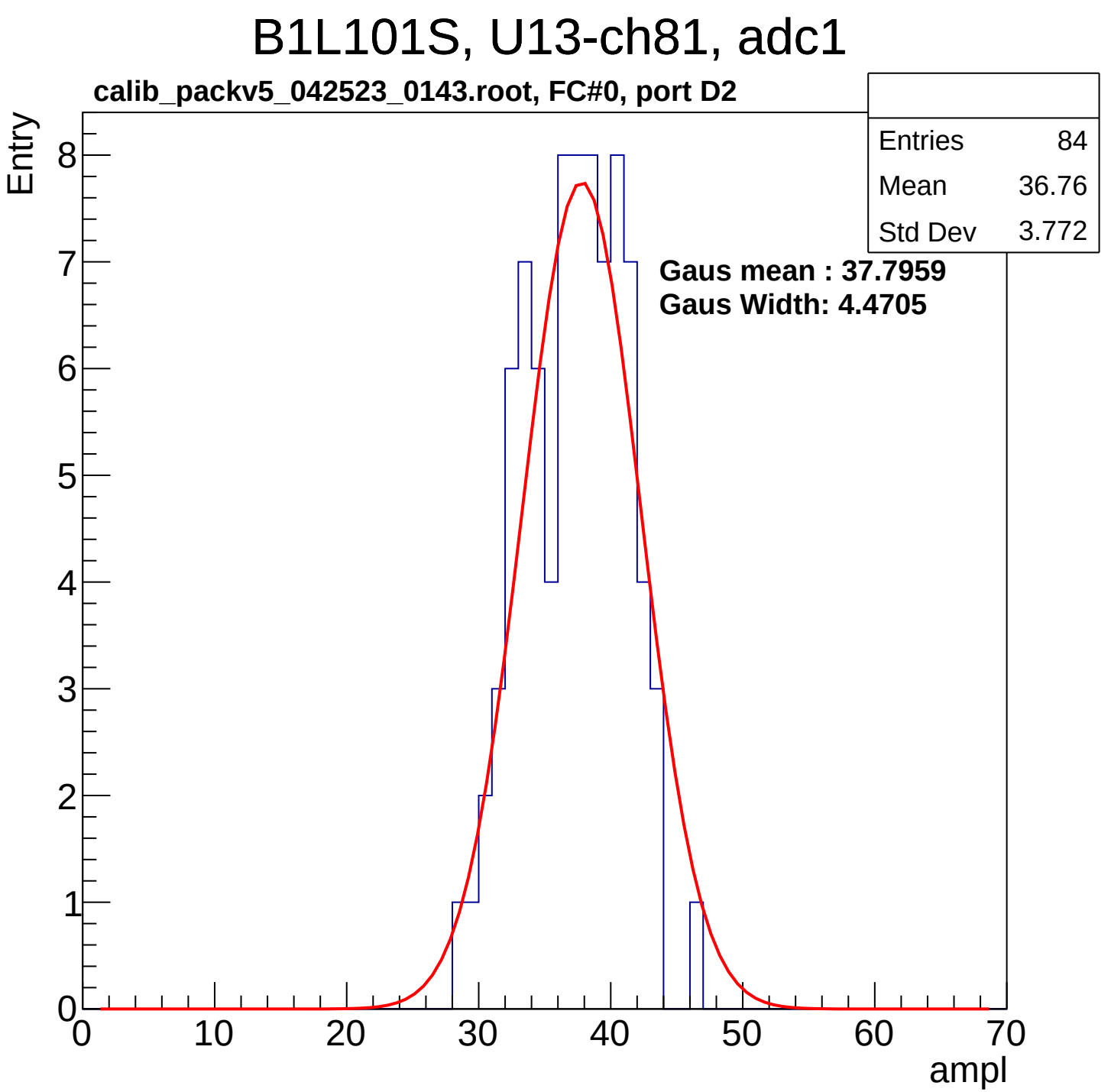
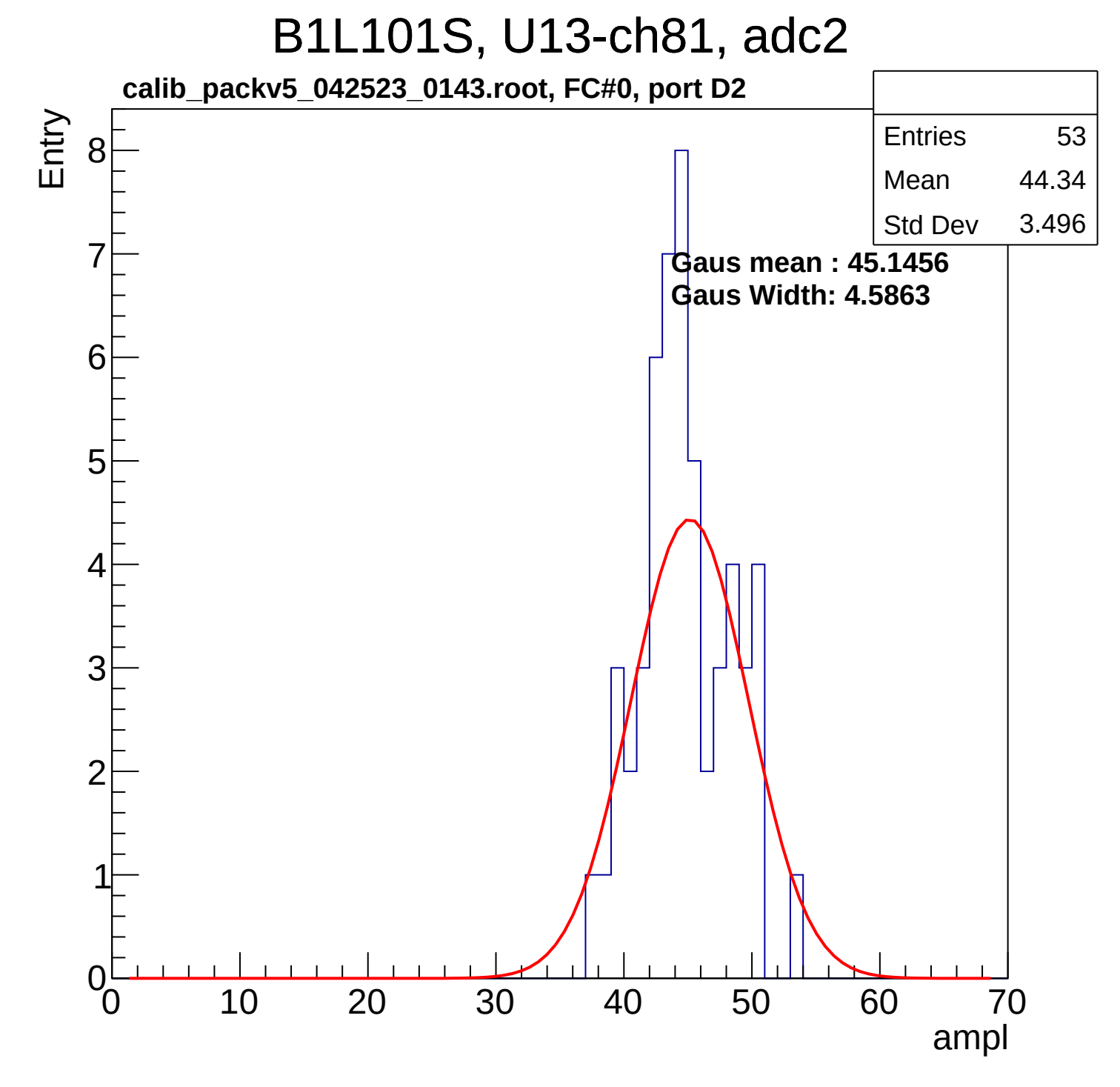

B1L101S, U13-ch81, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 84
Mean 36.76
Std Dev 3.772
Gaus mean : 37.7959
Gaus Width: 4.4705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch81, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 44.34
Std Dev 3.496
Gaus mean : 45.1456
Gaus Width: 4.5863
Entry
ampl
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1
2
3
4
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6
7
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0
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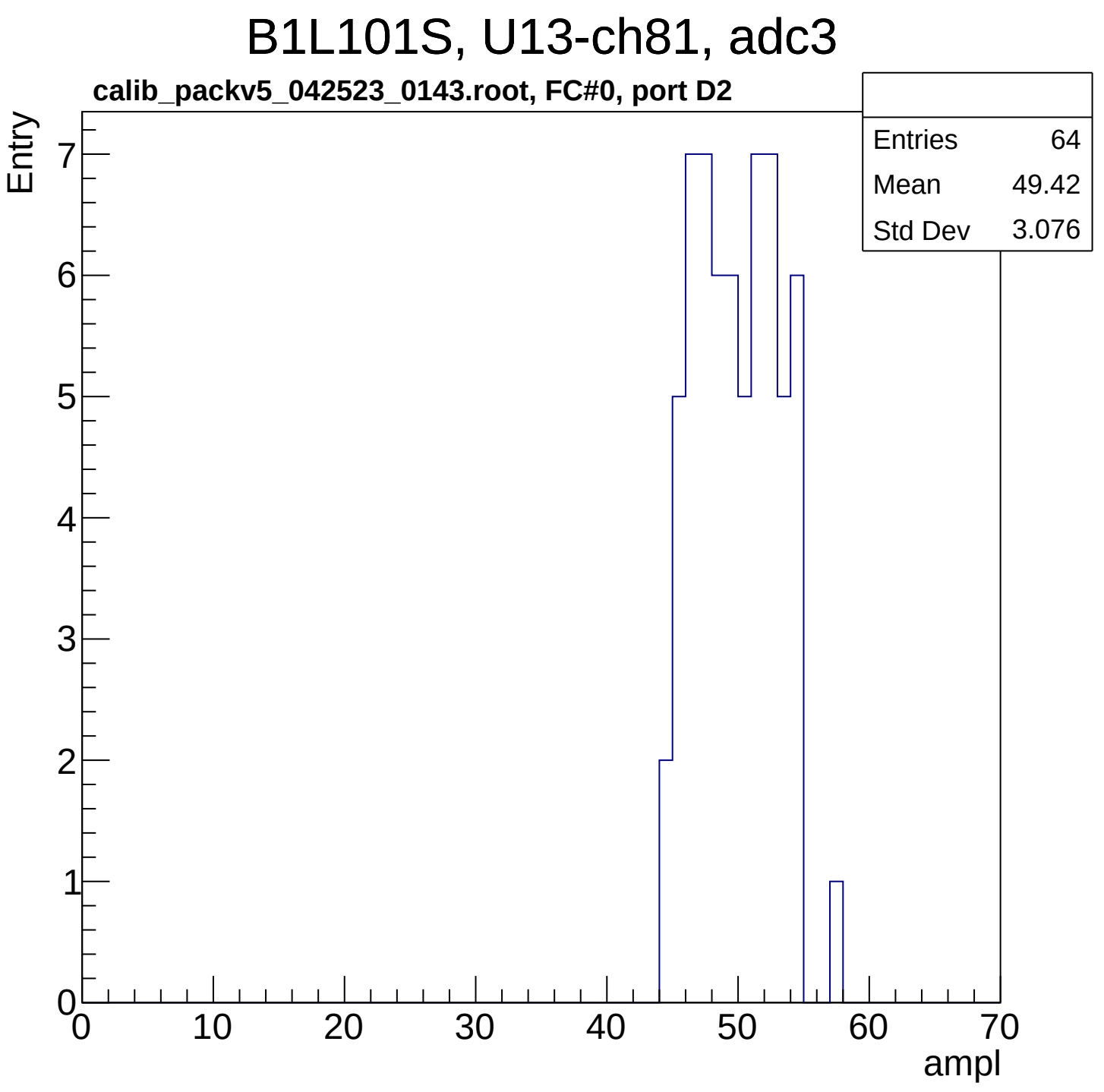
B1L101S, U13-ch81, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 49.42
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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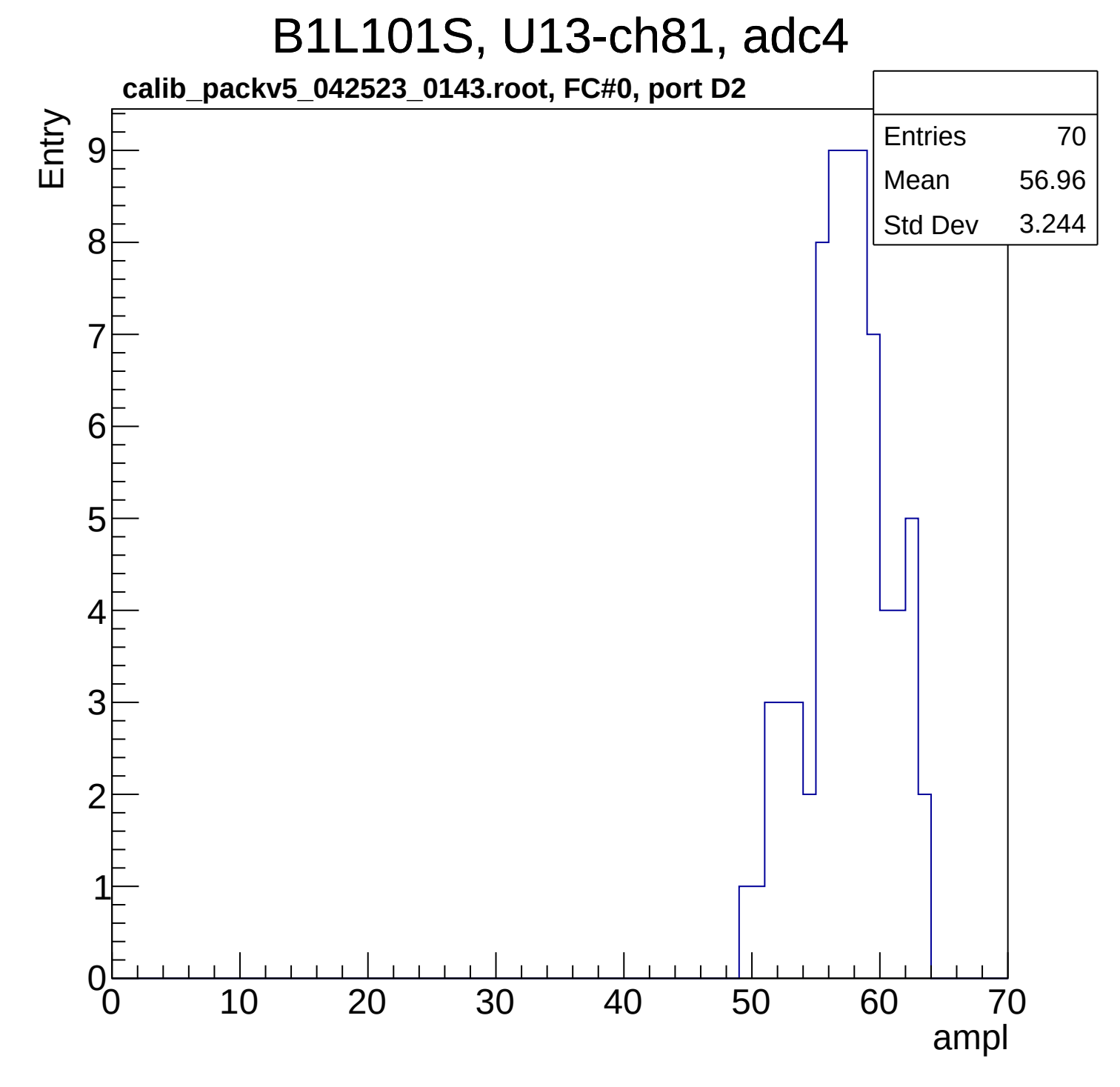
B1L101S, U13-ch81, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 56.96
Std Dev 3.244
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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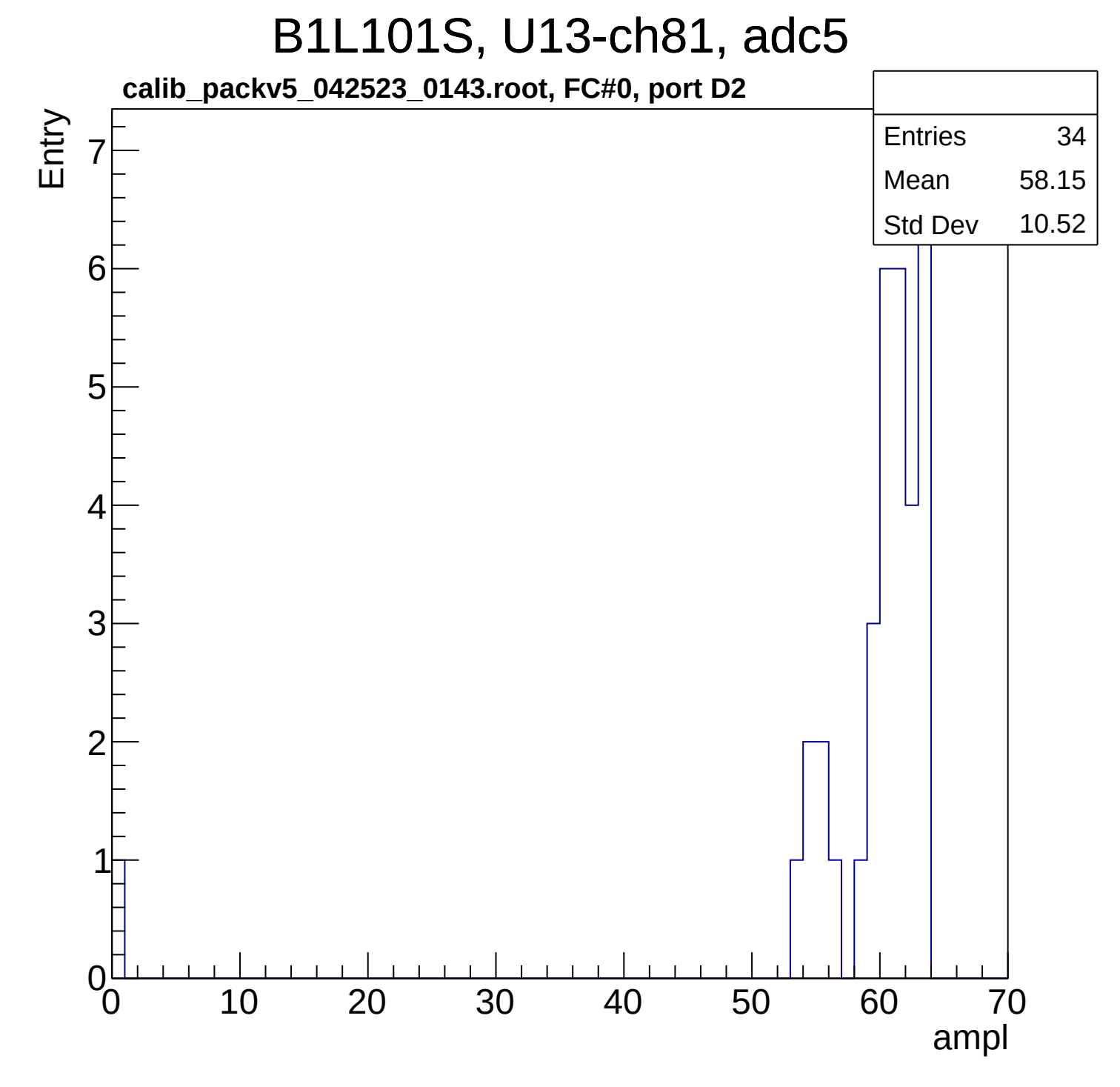
B1L101S, U13-ch81, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 34
Mean 58.15
Std Dev 10.52
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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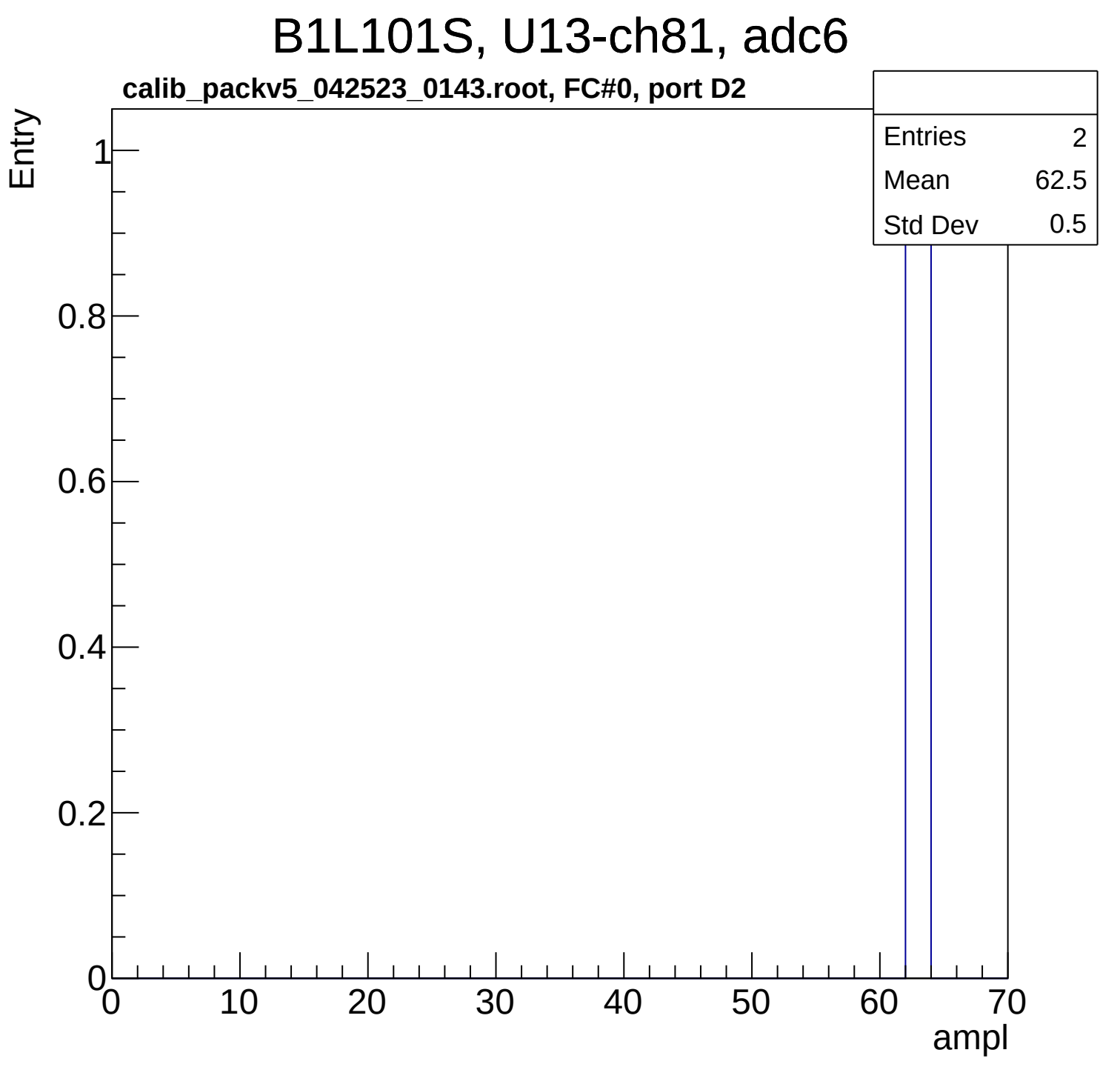
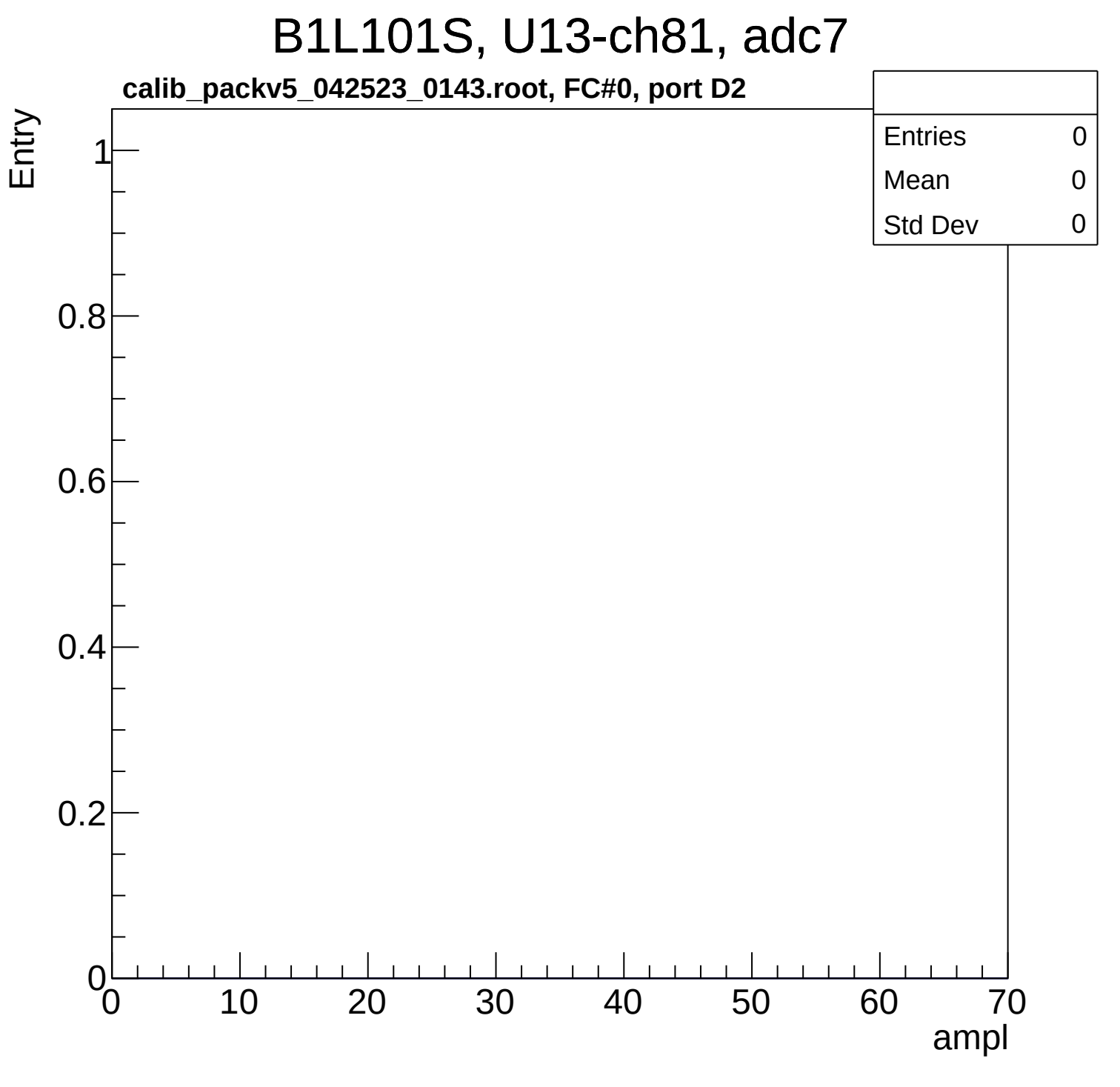
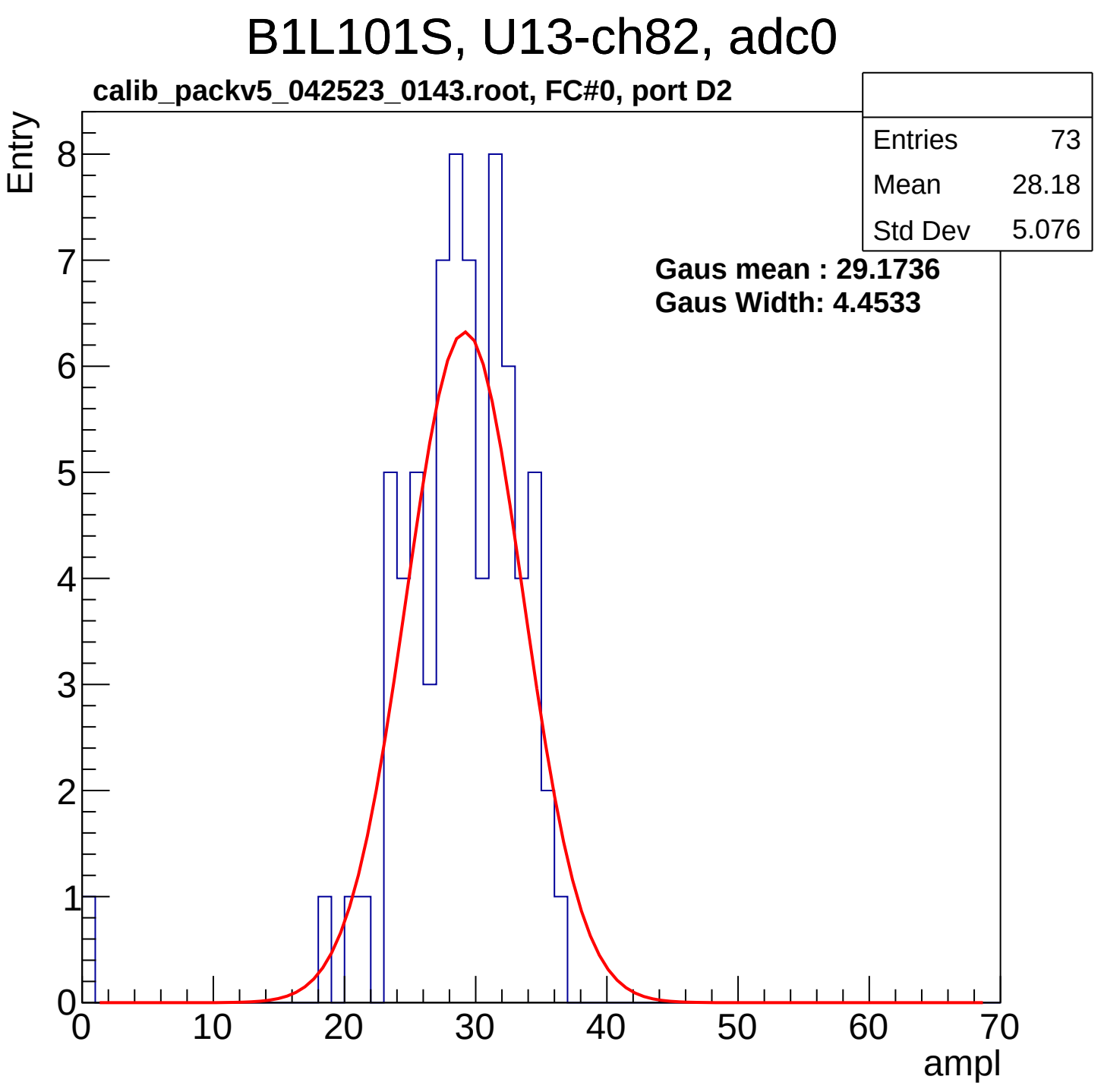
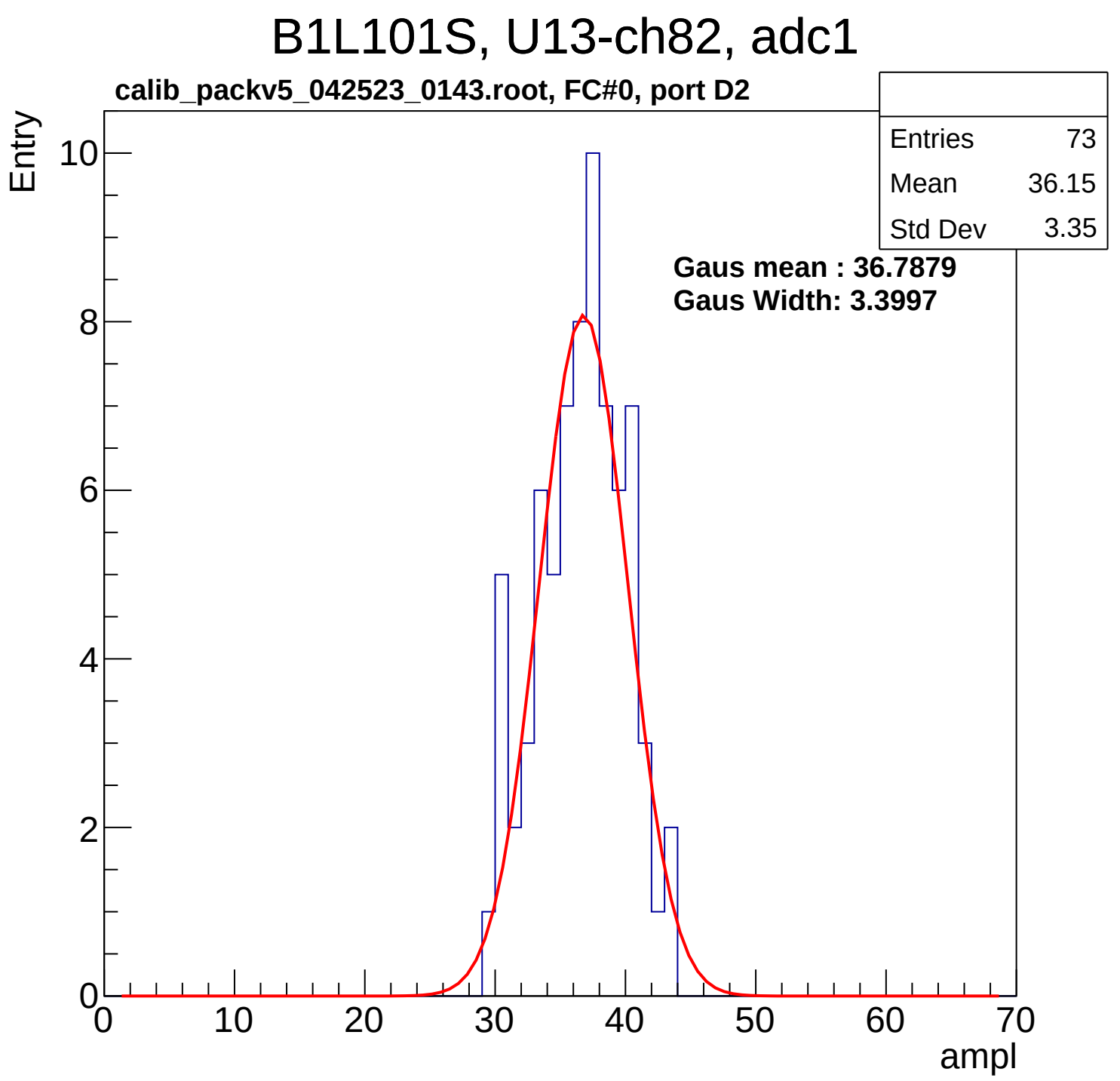

B1L101S, U13-ch81, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch81, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch82, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 28.18
Std Dev 5.076
Gaus mean : 29.1736
Gaus Width: 4.4533
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch82, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 36.15
Std Dev 3.35
Gaus mean : 36.7879
Gaus Width: 3.3997
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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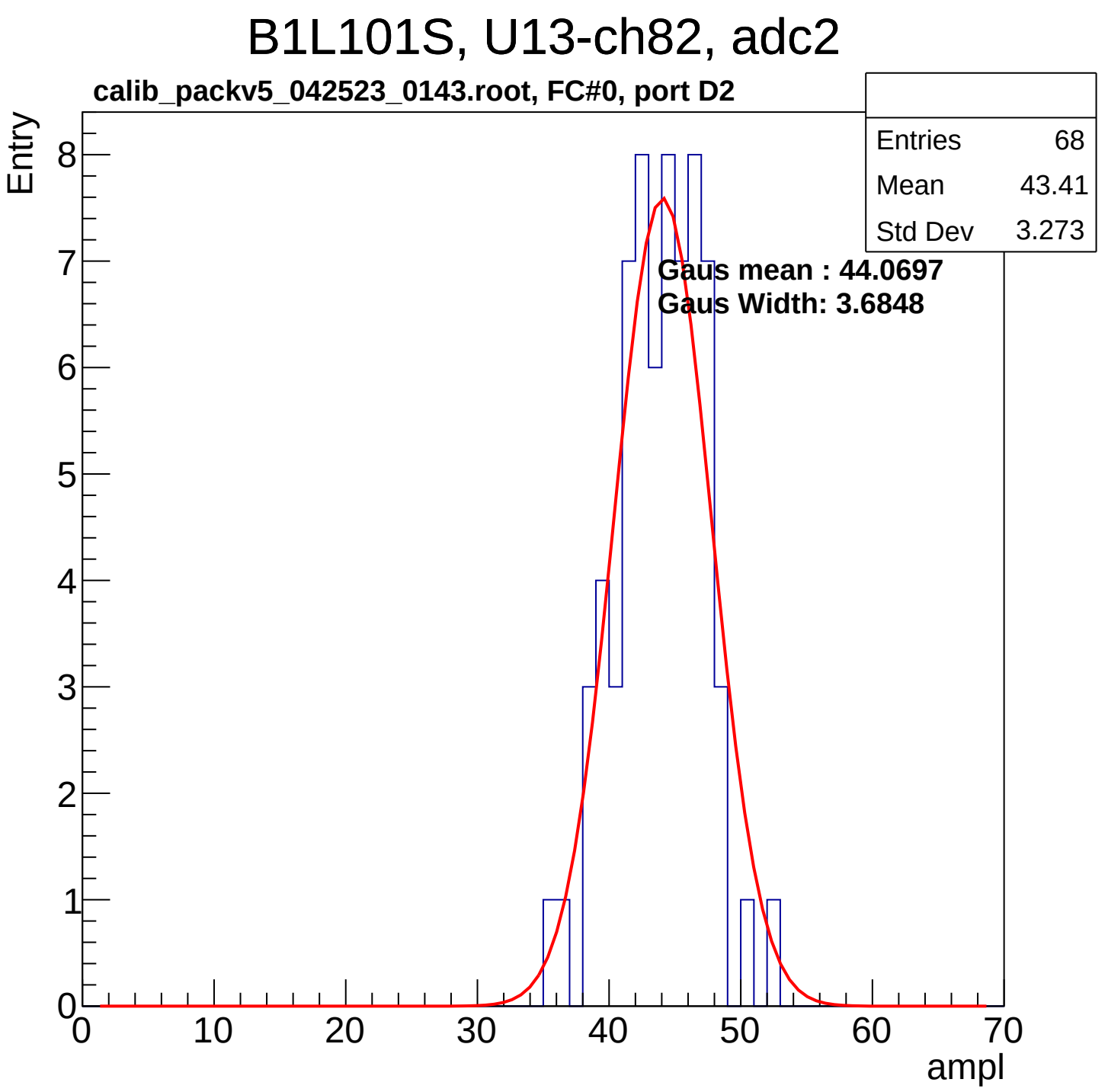
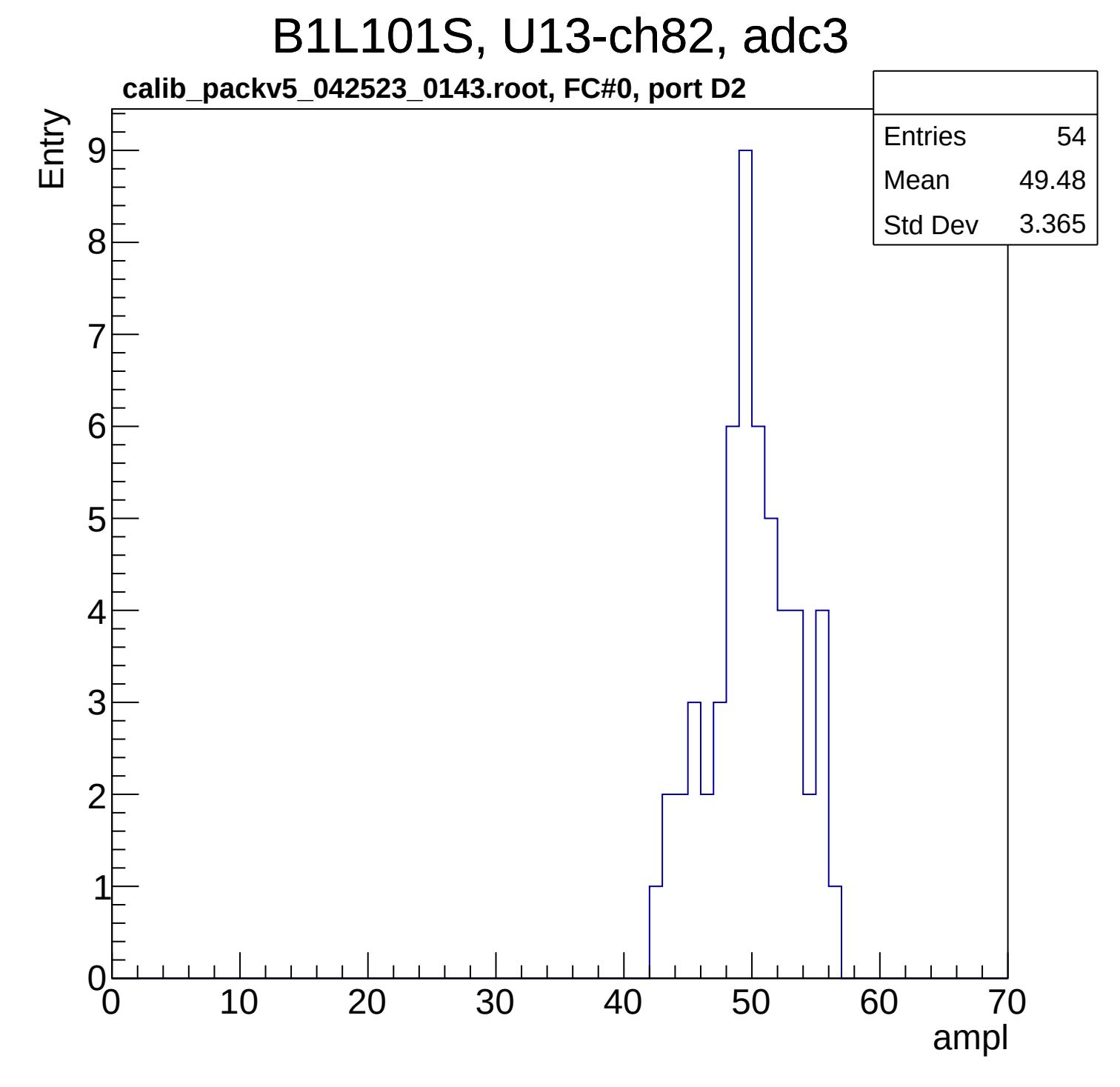

B1L101S, U13-ch82, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 43.41
Std Dev 3.273
Gaus mean : 44.0697
Gaus Width: 3.6848
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch82, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 49.48
Std Dev 3.365
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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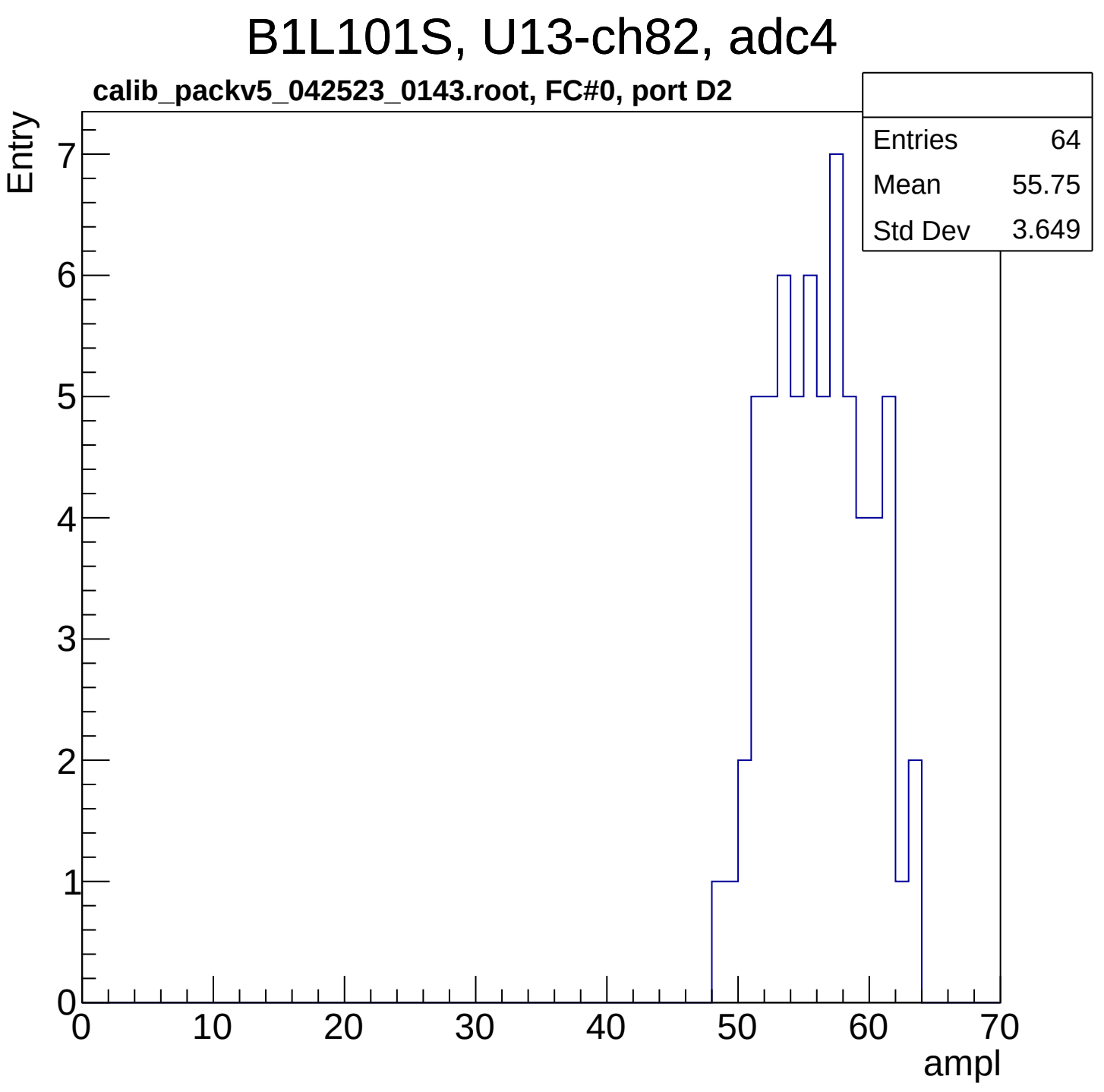
B1L101S, U13-ch82, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 55.75
Std Dev 3.649
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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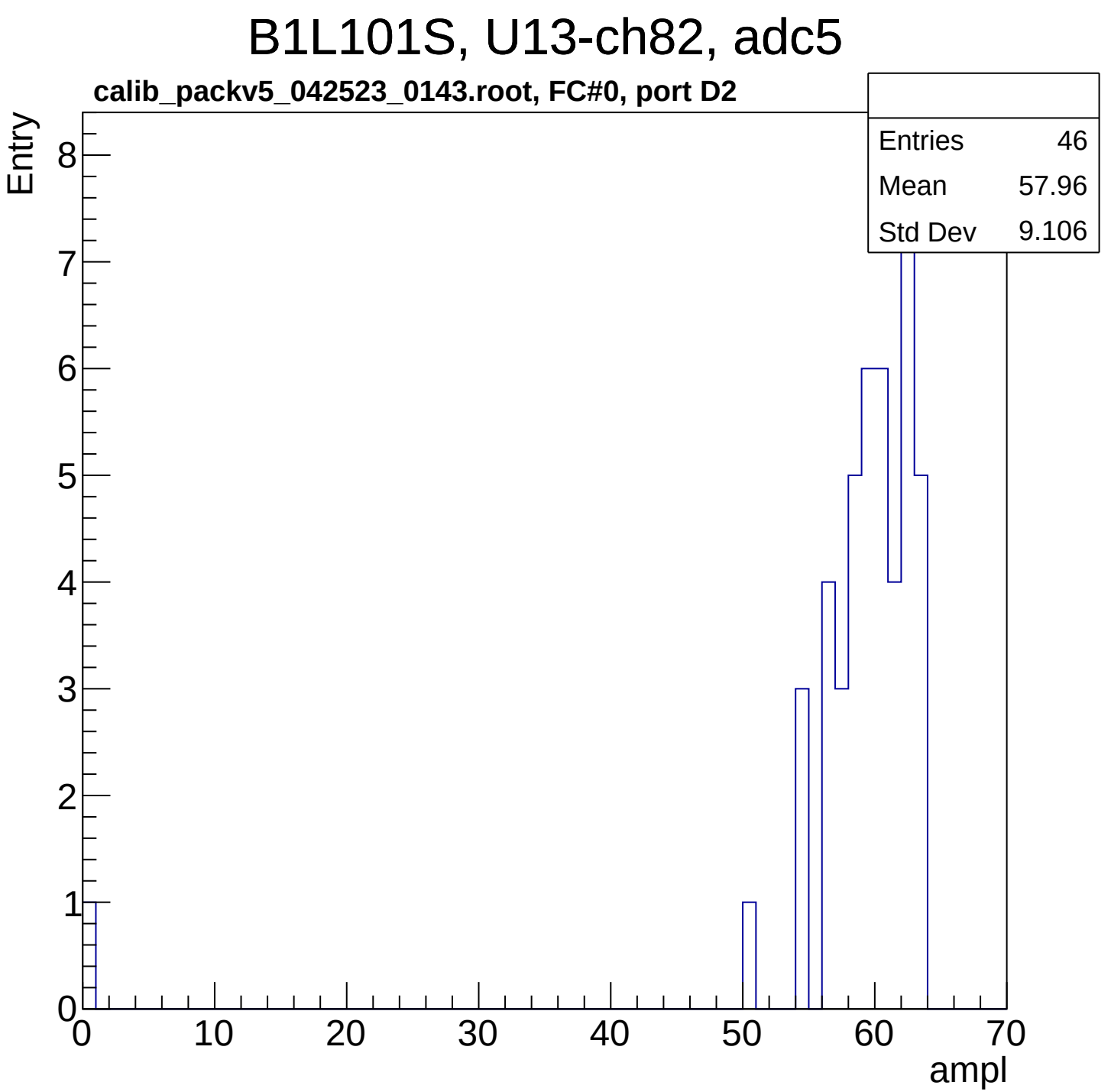
B1L101S, U13-ch82, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 57.96
Std Dev 9.106
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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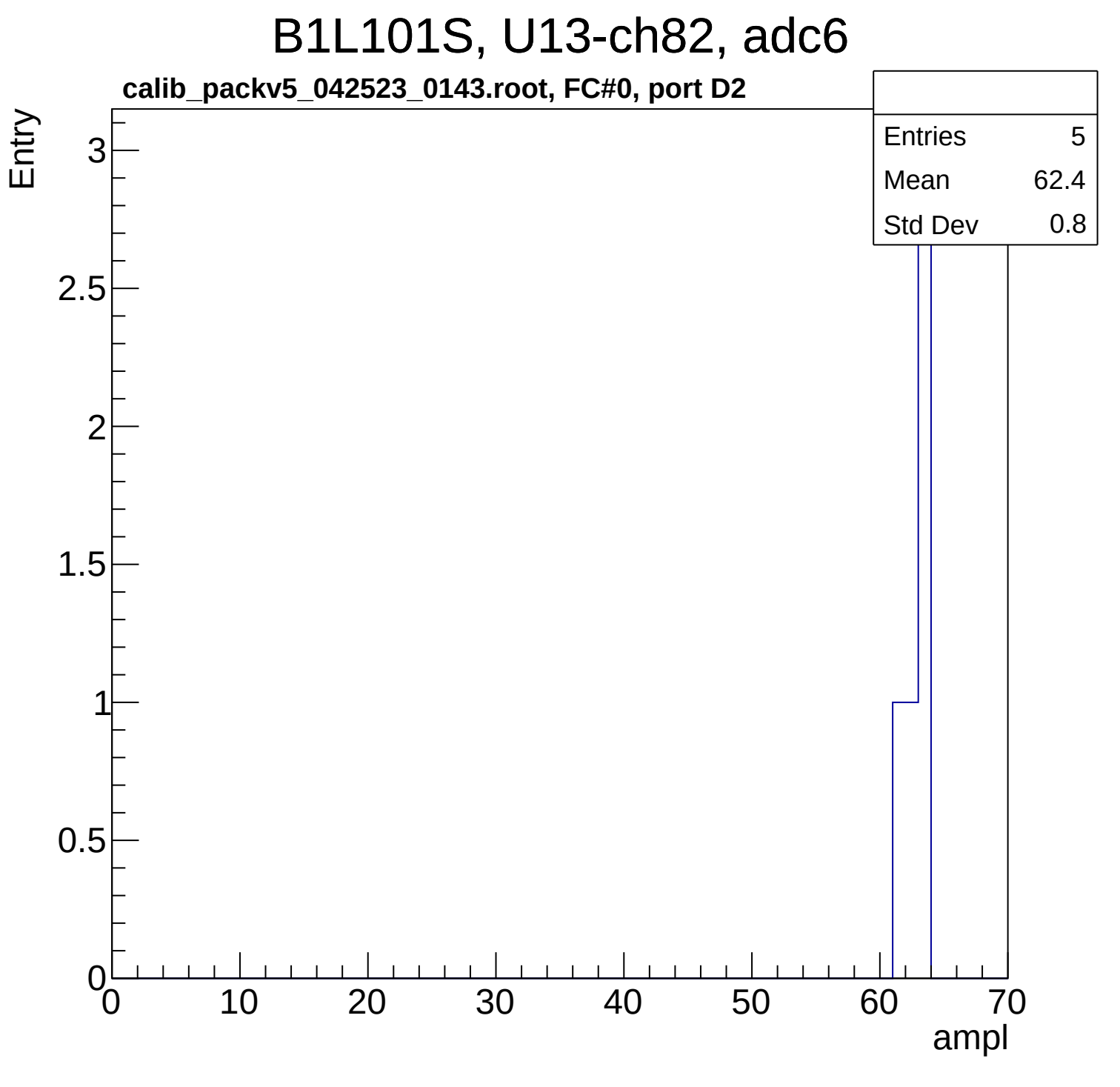
B1L101S, U13-ch82, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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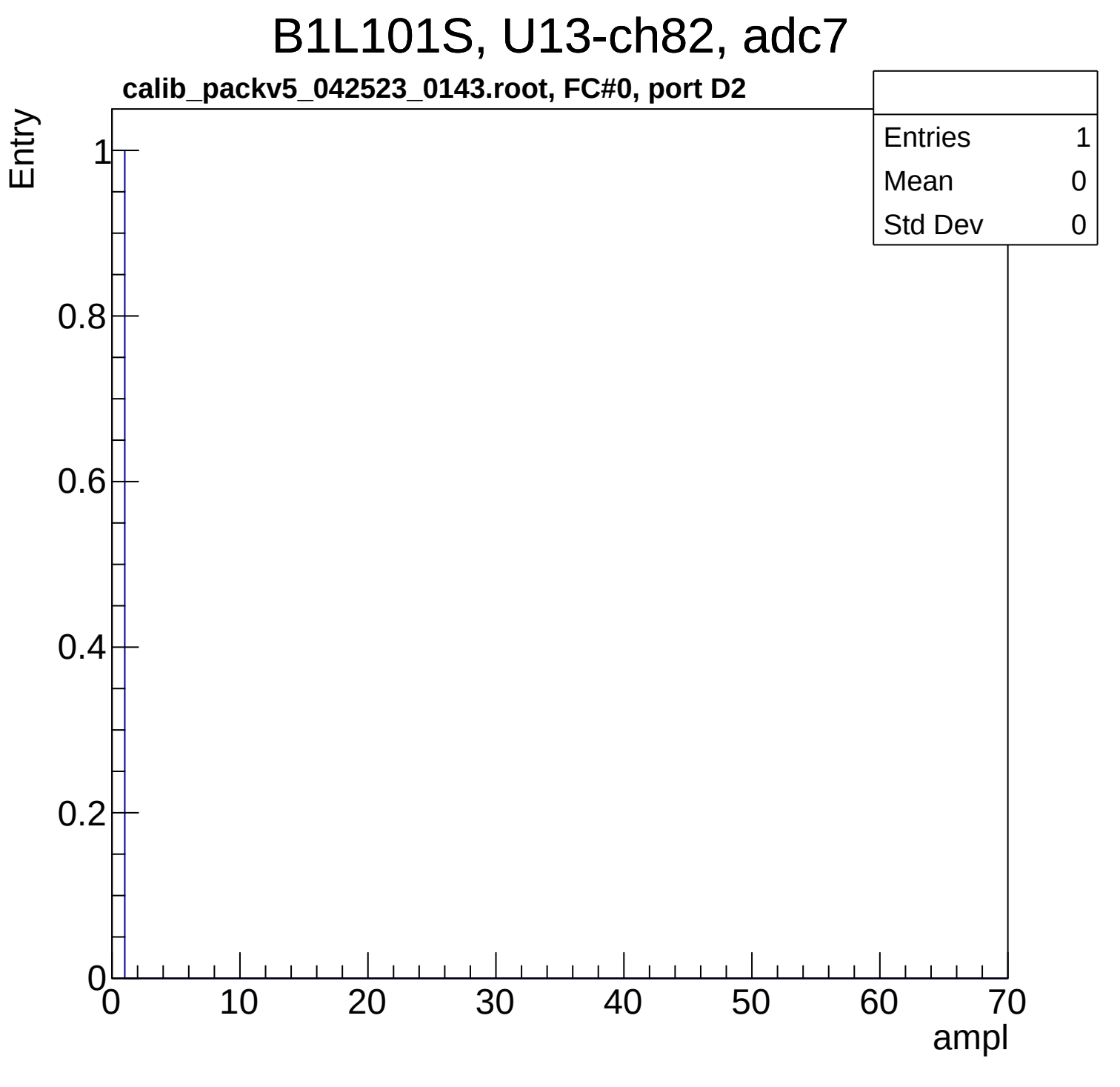
B1L101S, U13-ch82, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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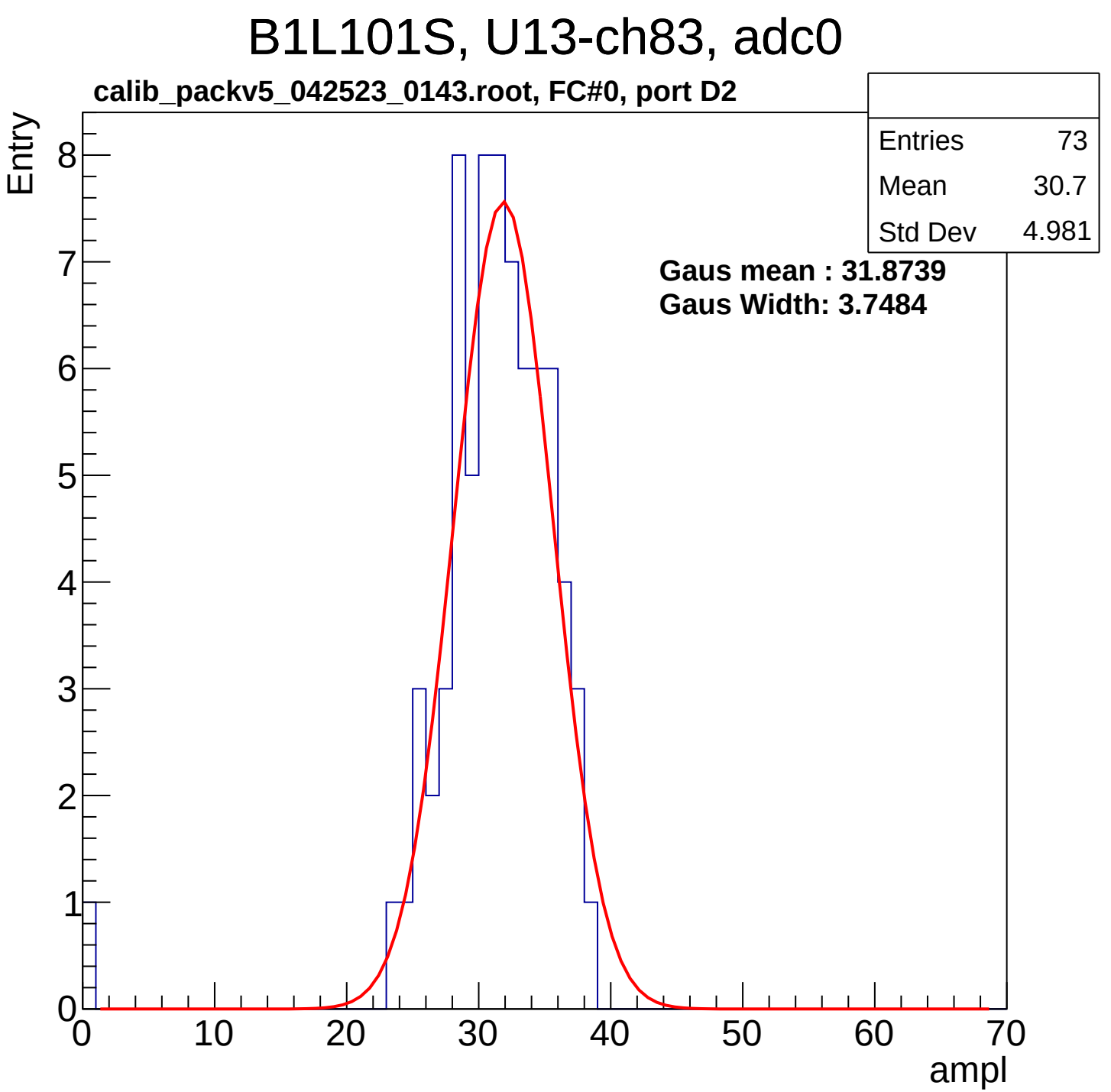
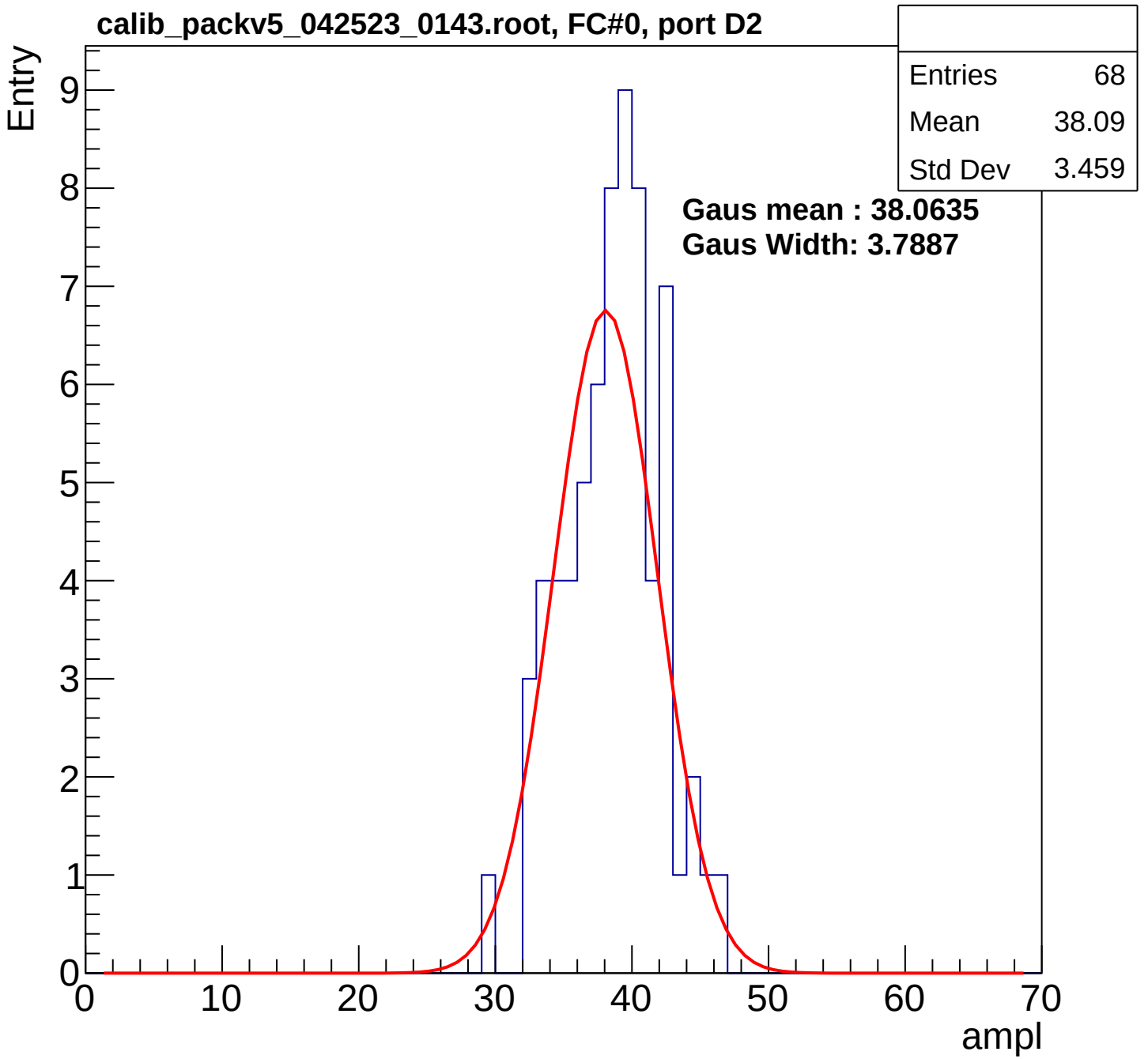
30
40
50
60
70
ampl


B1L101S, U13-ch83, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 30.7
Std Dev 4.981
Gaus mean : 31.8739
Gaus Width: 3.7484
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch83, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 38.09
Std Dev 3.459
Gaus mean : 38.0635
Gaus Width: 3.7887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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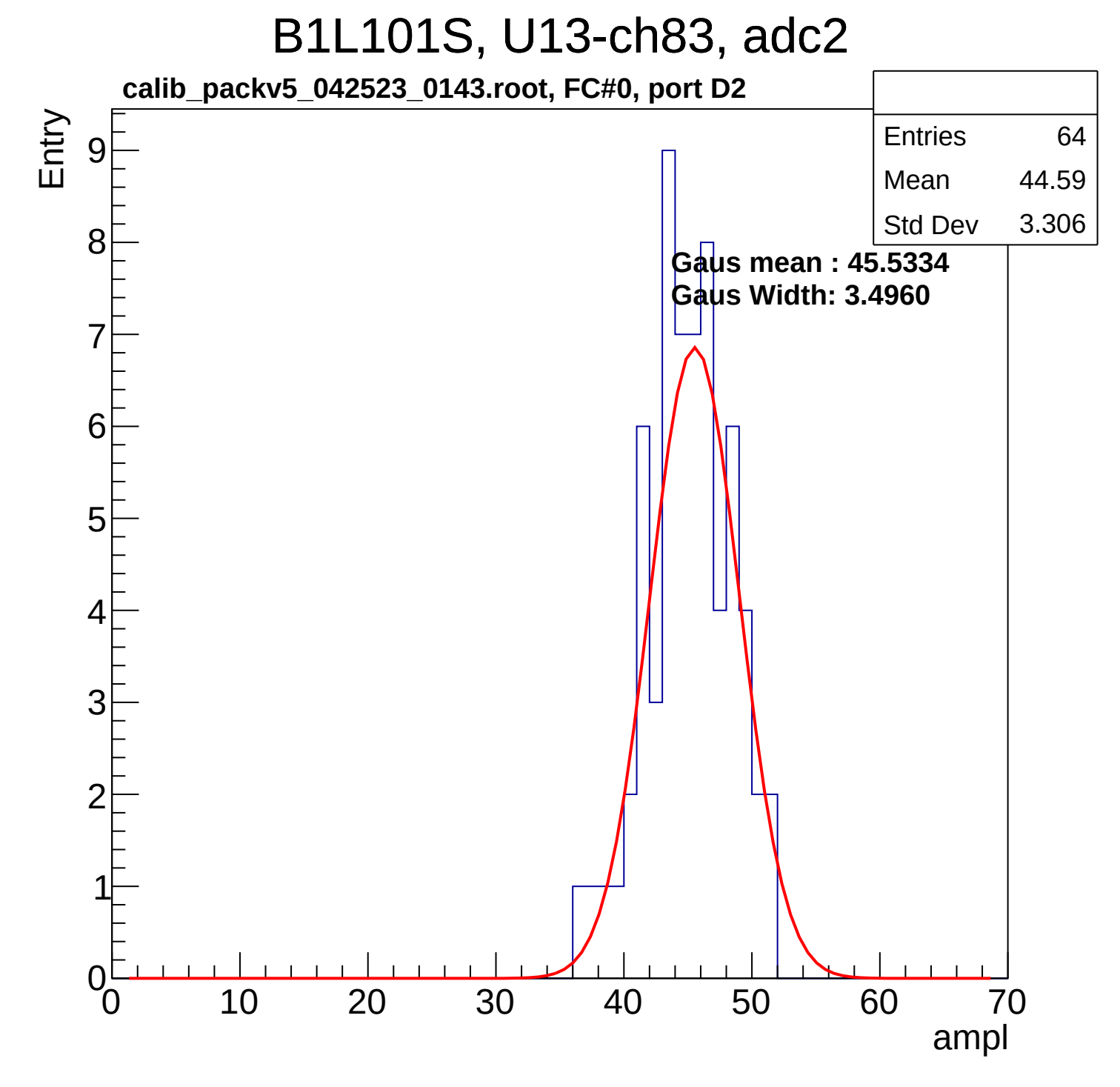
60
70


B1L101S, U13-ch83, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 44.59
Std Dev 3.306
Gaus mean : 45.5334
Gaus Width: 3.4960
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
50
60
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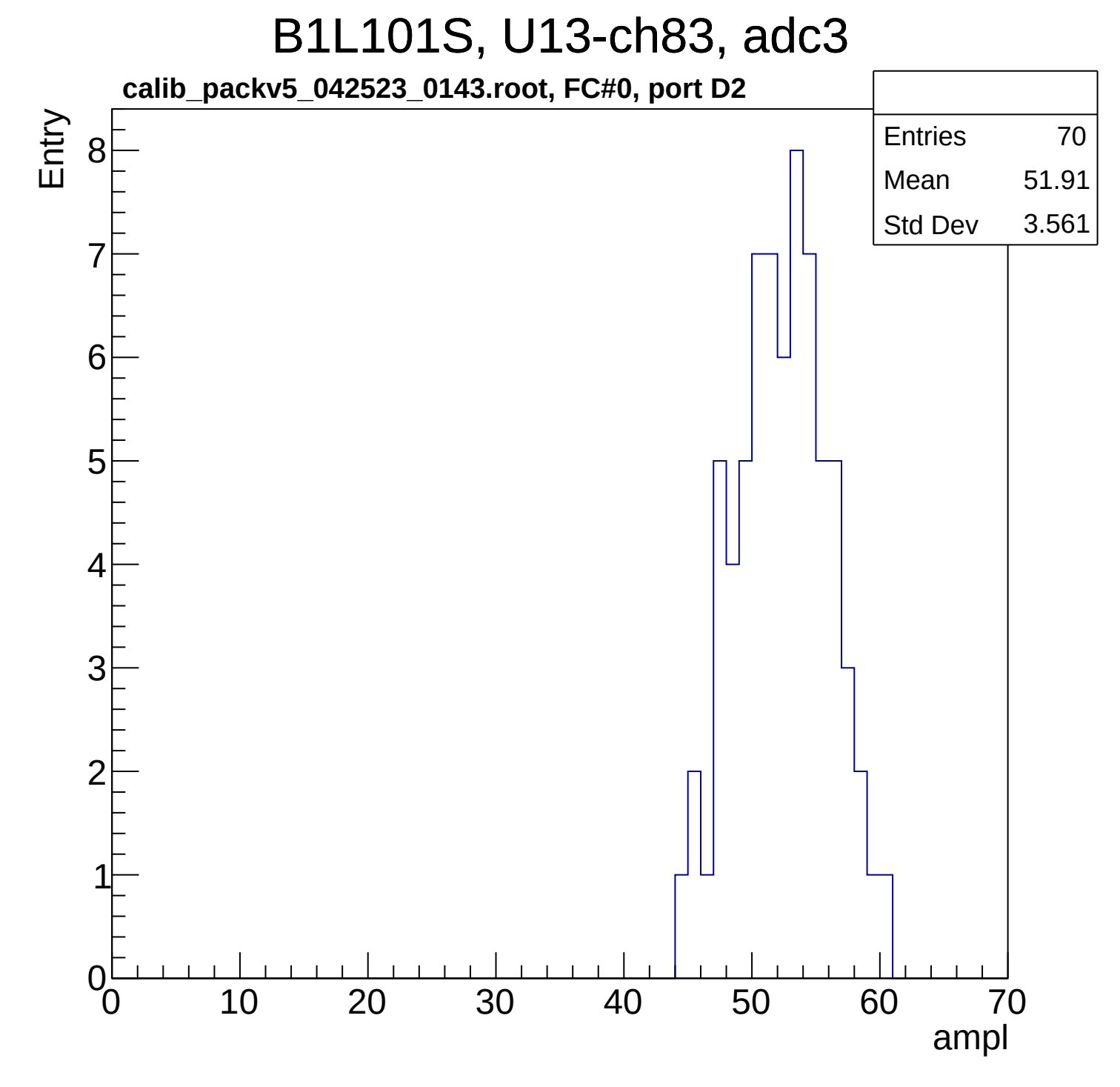
B1L101S, U13-ch83, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 51.91
Std Dev 3.561
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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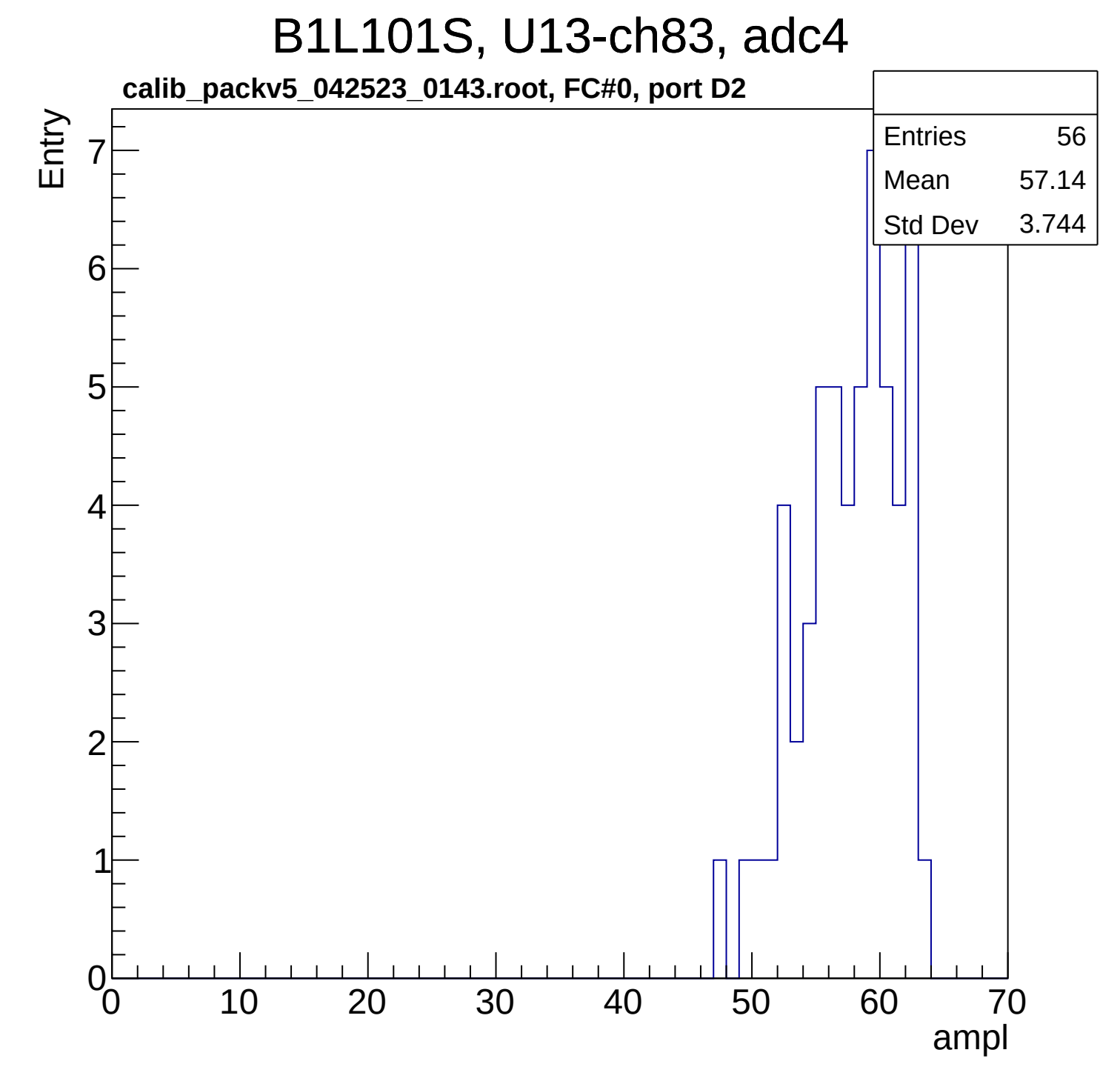
B1L101S, U13-ch83, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 57.14
Std Dev 3.744
Entry
ampl
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0
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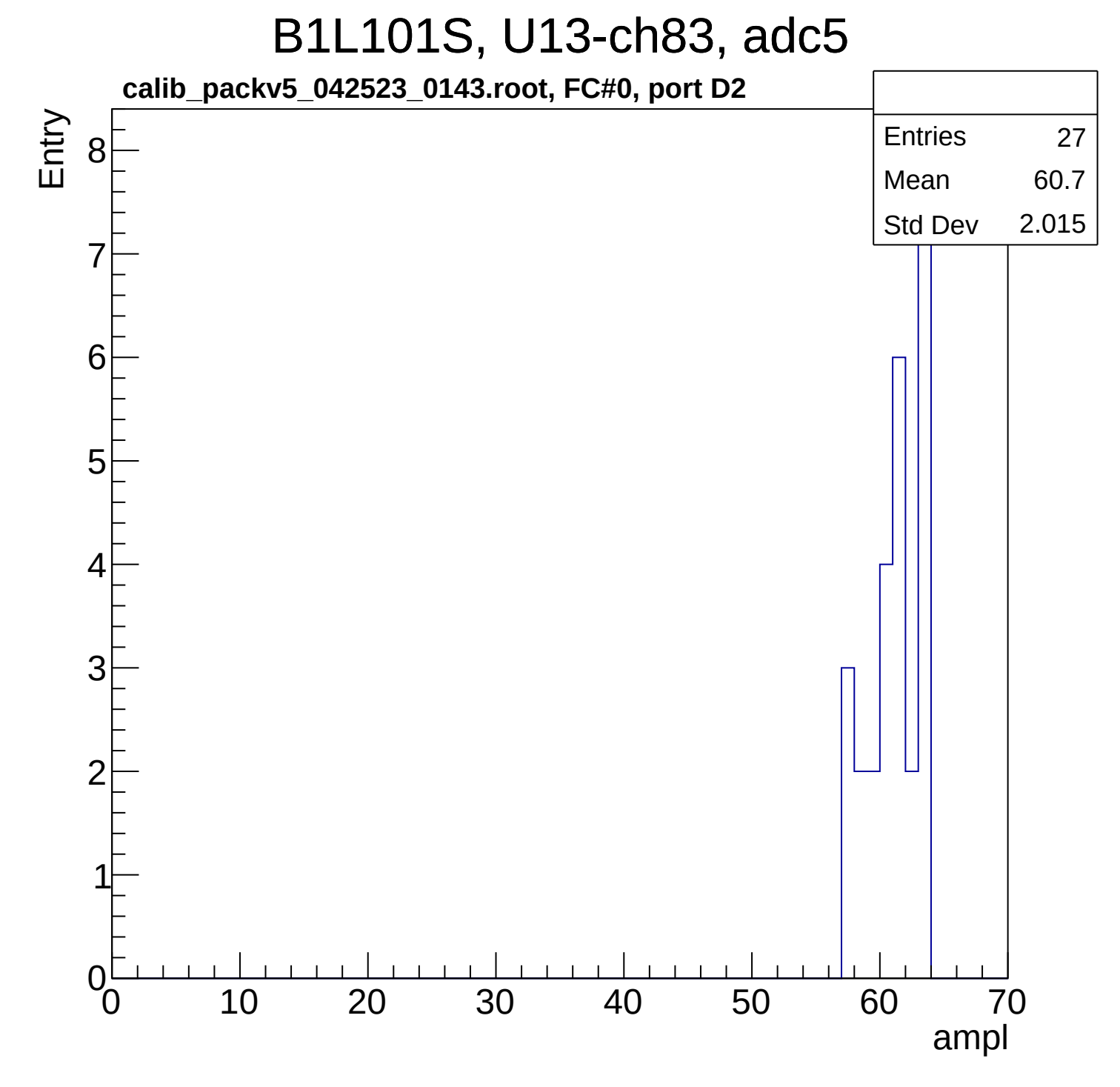
B1L101S, U13-ch83, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 27
Mean 60.7
Std Dev 2.015
Entry
ampl
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1
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0
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60
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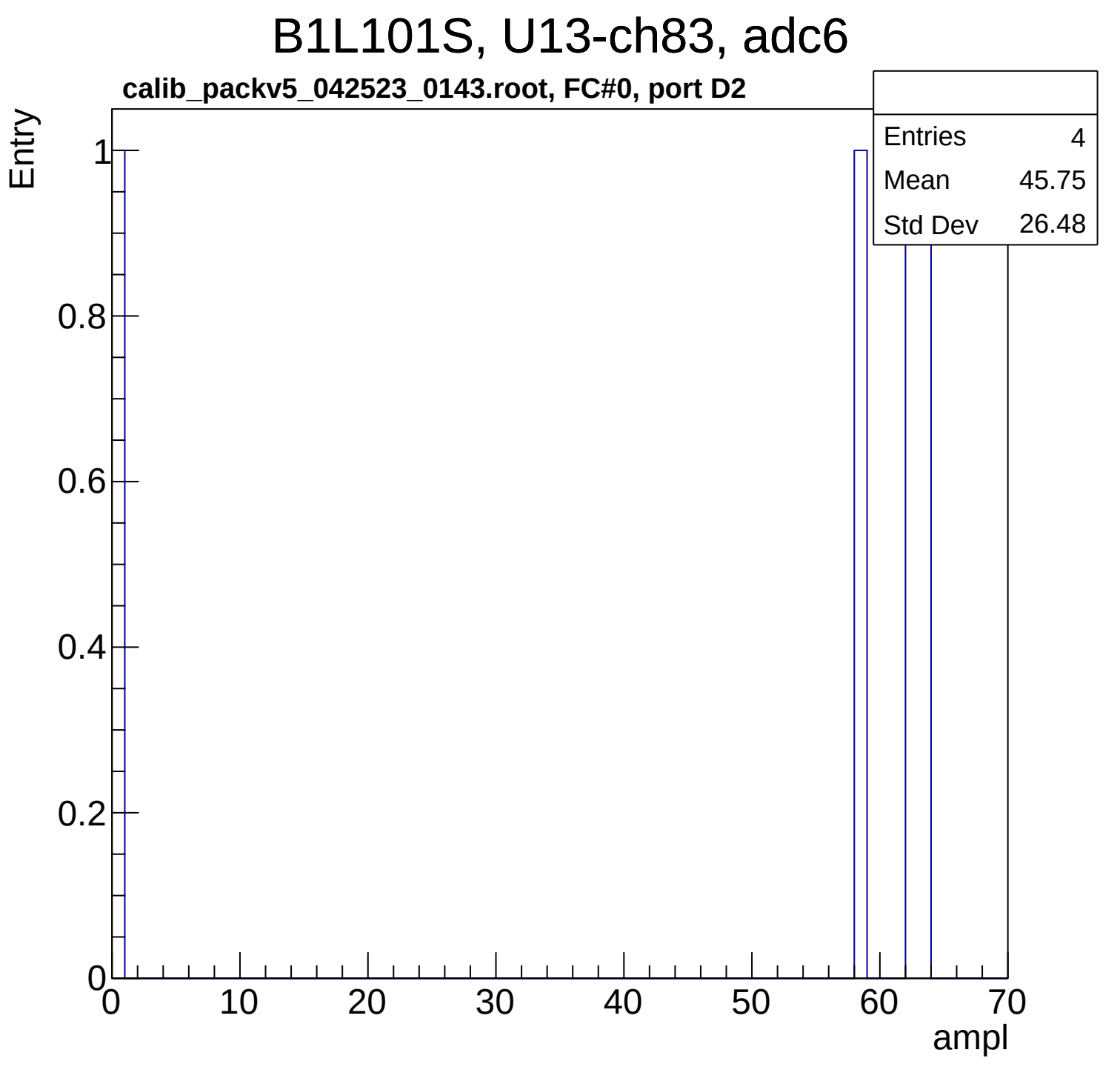
B1L101S, U13-ch83, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 45.75
Std Dev 26.48
Entry
ampl
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0.4
0.6
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1
0
10
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60
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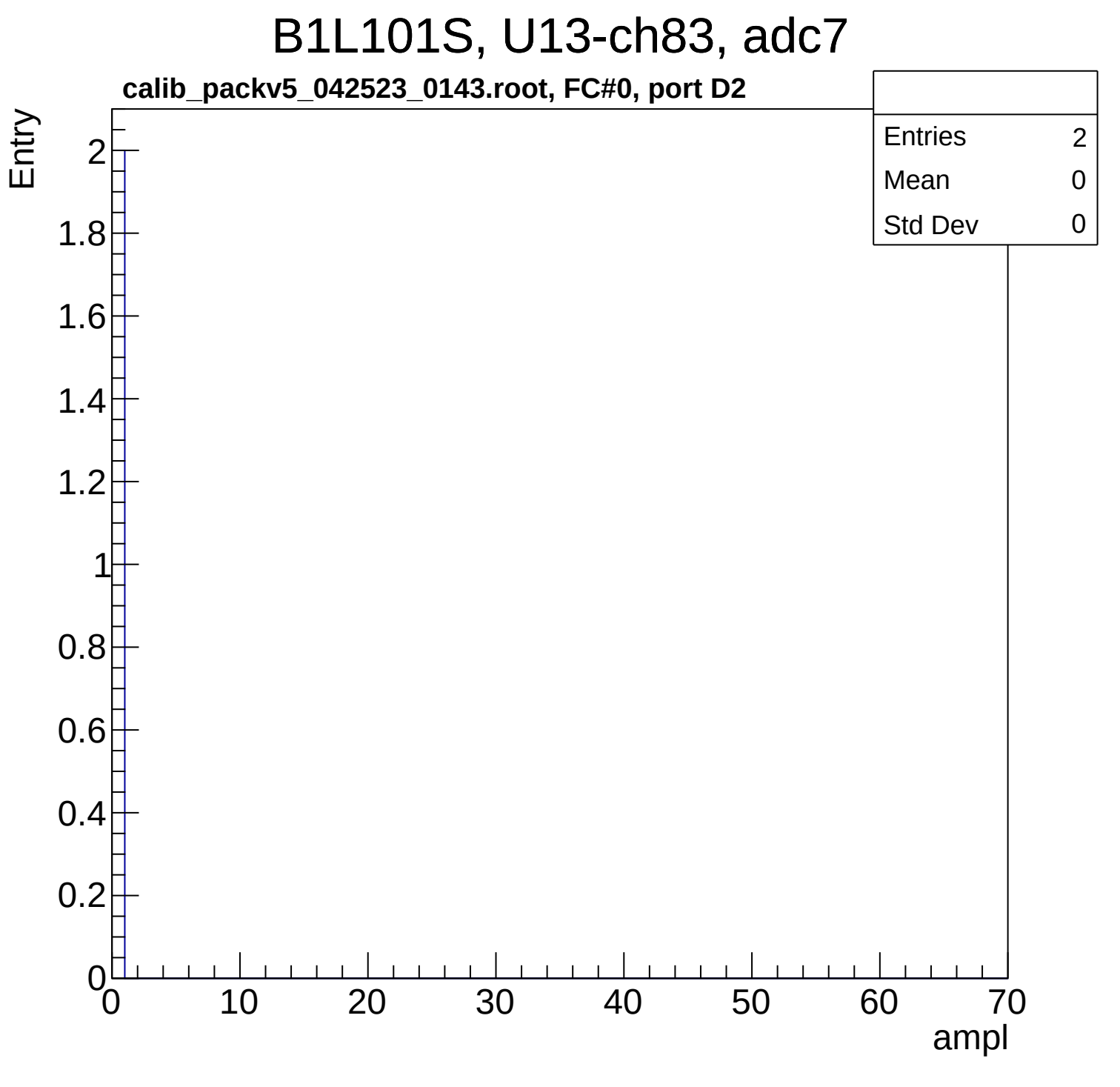
B1L101S, U13-ch83, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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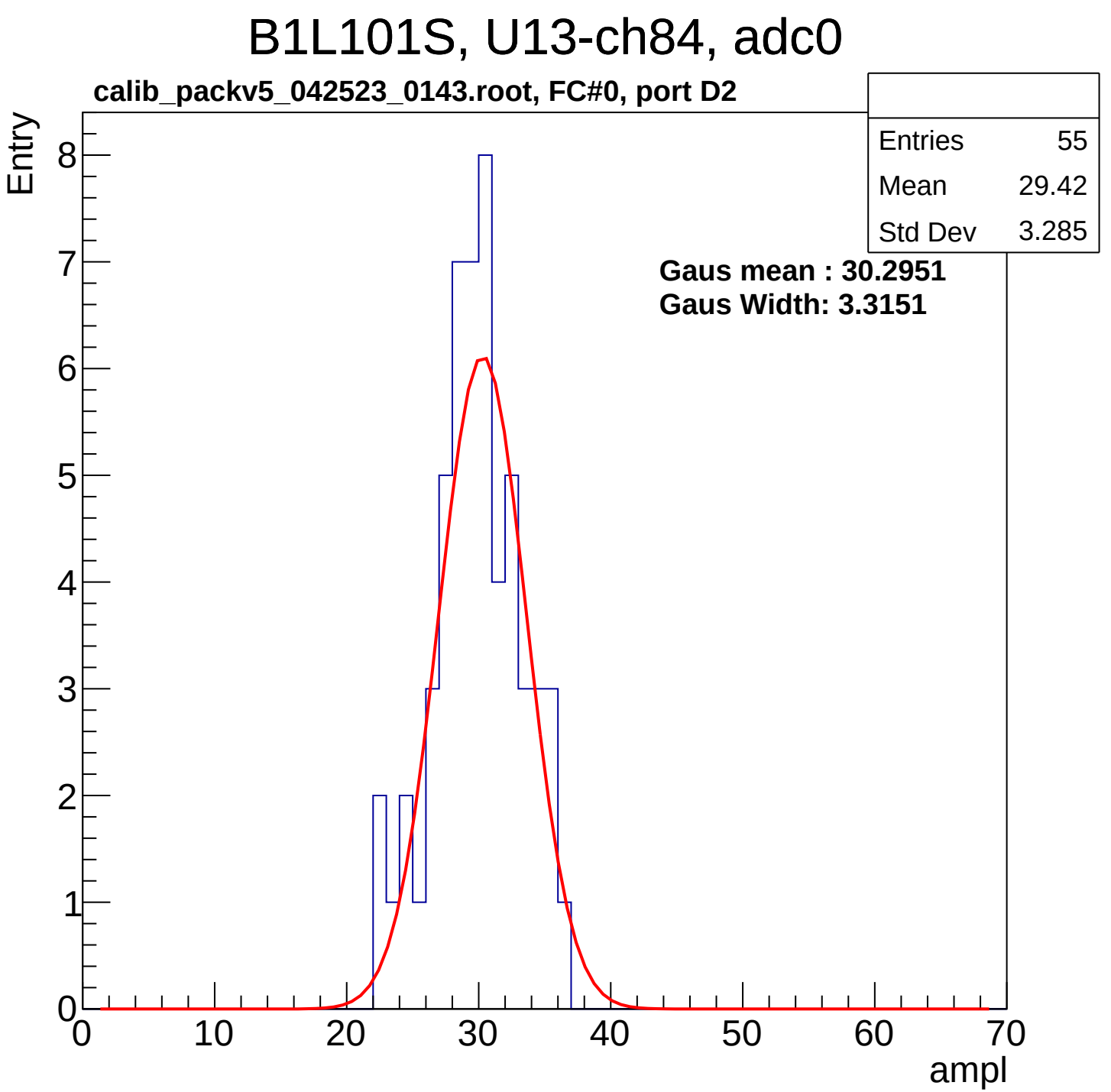
B1L101S, U13-ch84, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 29.42
Std Dev 3.285
Gaus mean : 30.2951
Gaus Width: 3.3151
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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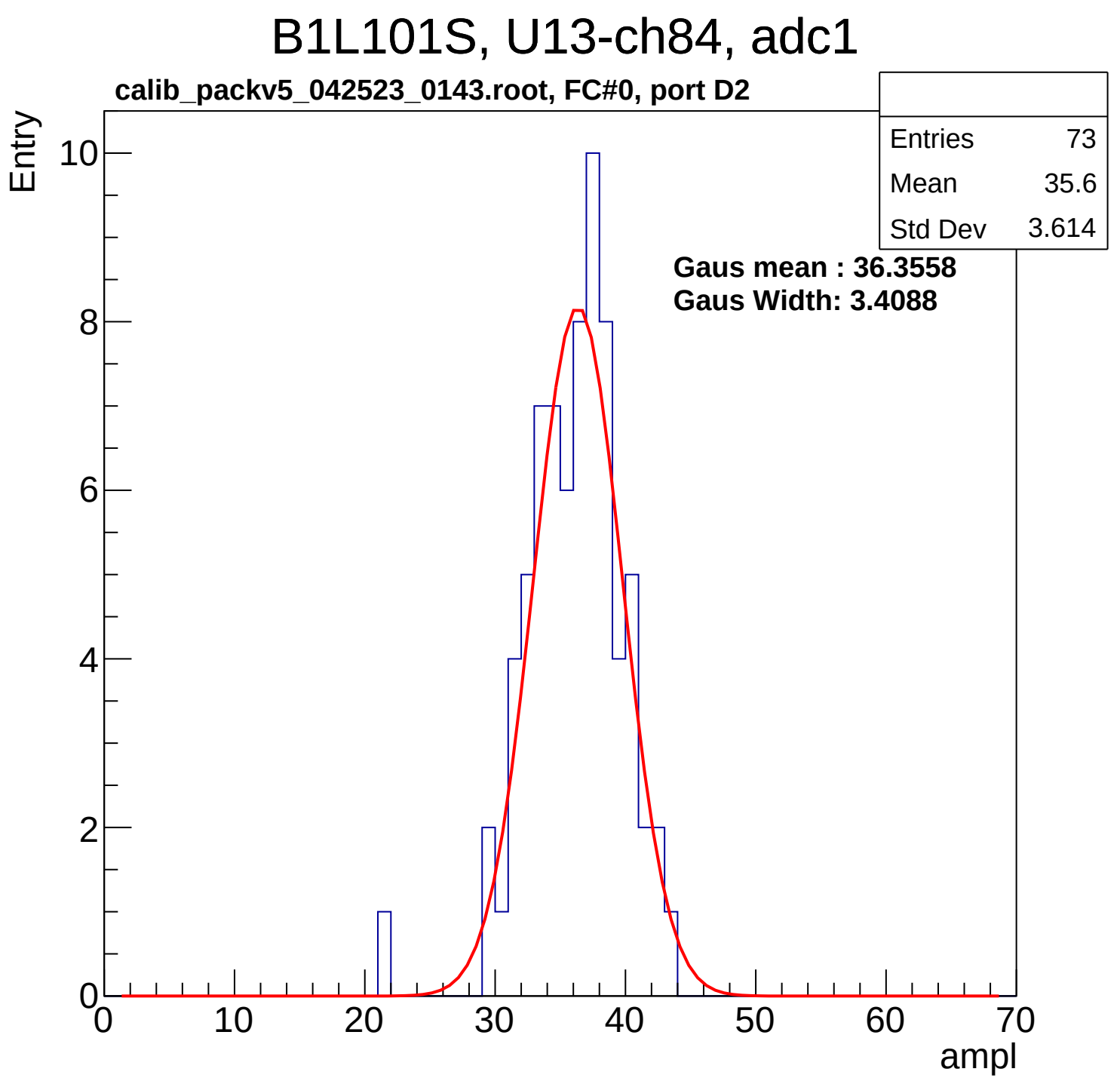
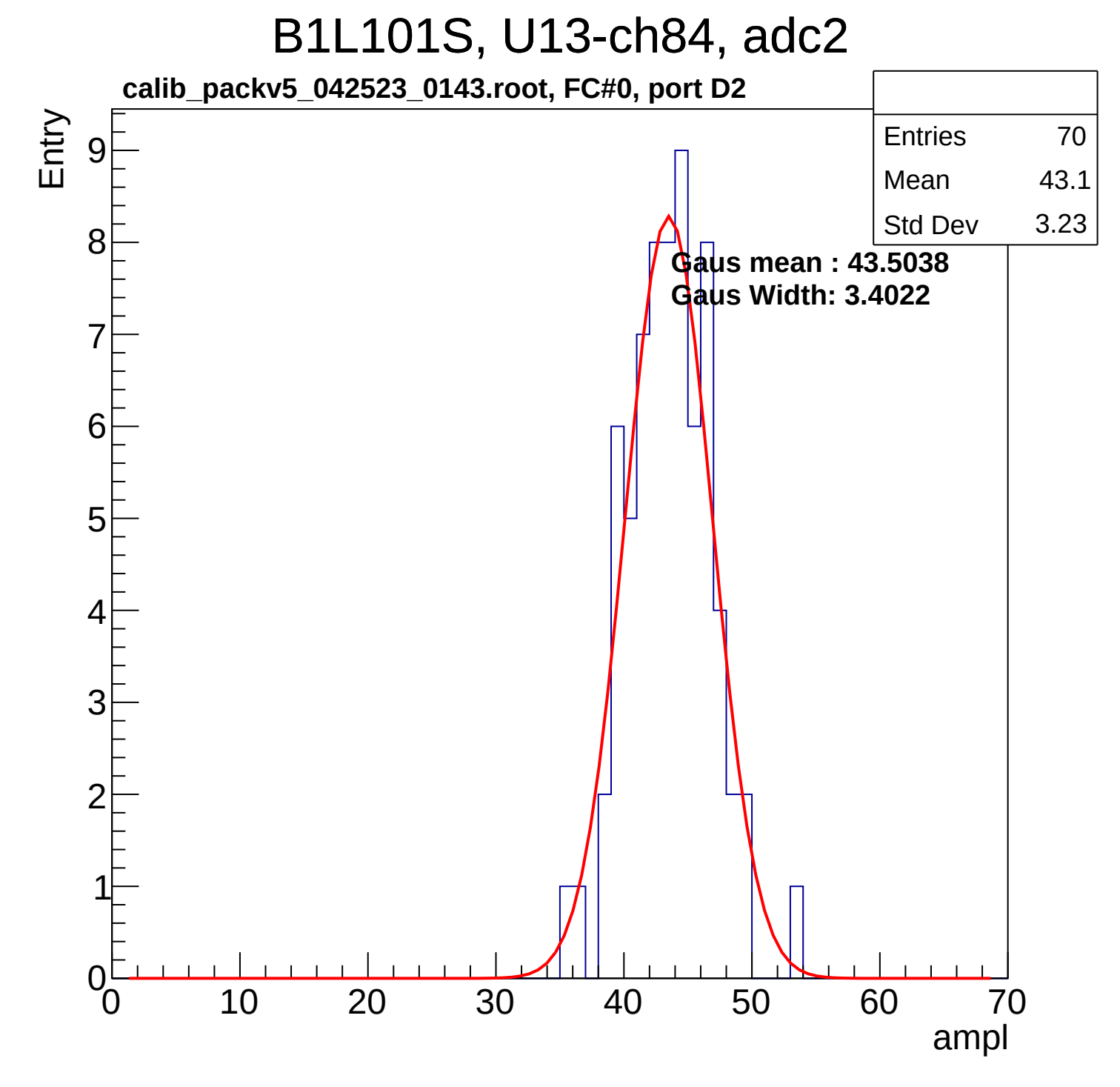
20
30
40
50
60
70


B1L101S, U13-ch84, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 35.6
Std Dev 3.614
Gaus mean : 36.3558
Gaus Width: 3.4088
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch84, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 43.1
Std Dev 3.23
Gaus mean : 43.5038
Gaus Width: 3.4022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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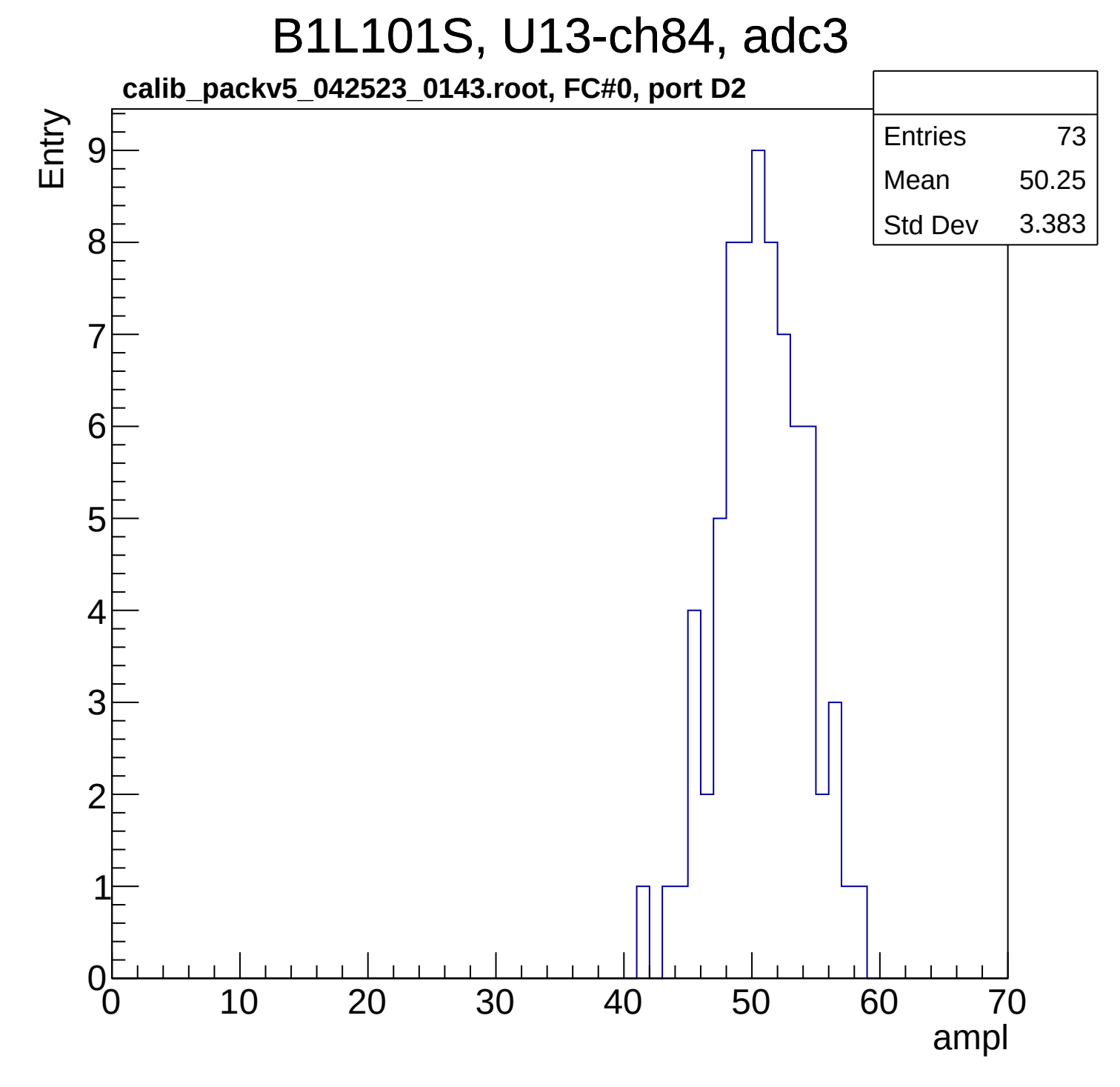
B1L101S, U13-ch84, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 50.25
Std Dev 3.383
Entry
ampl
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3
4
5
6
7
8
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0
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60
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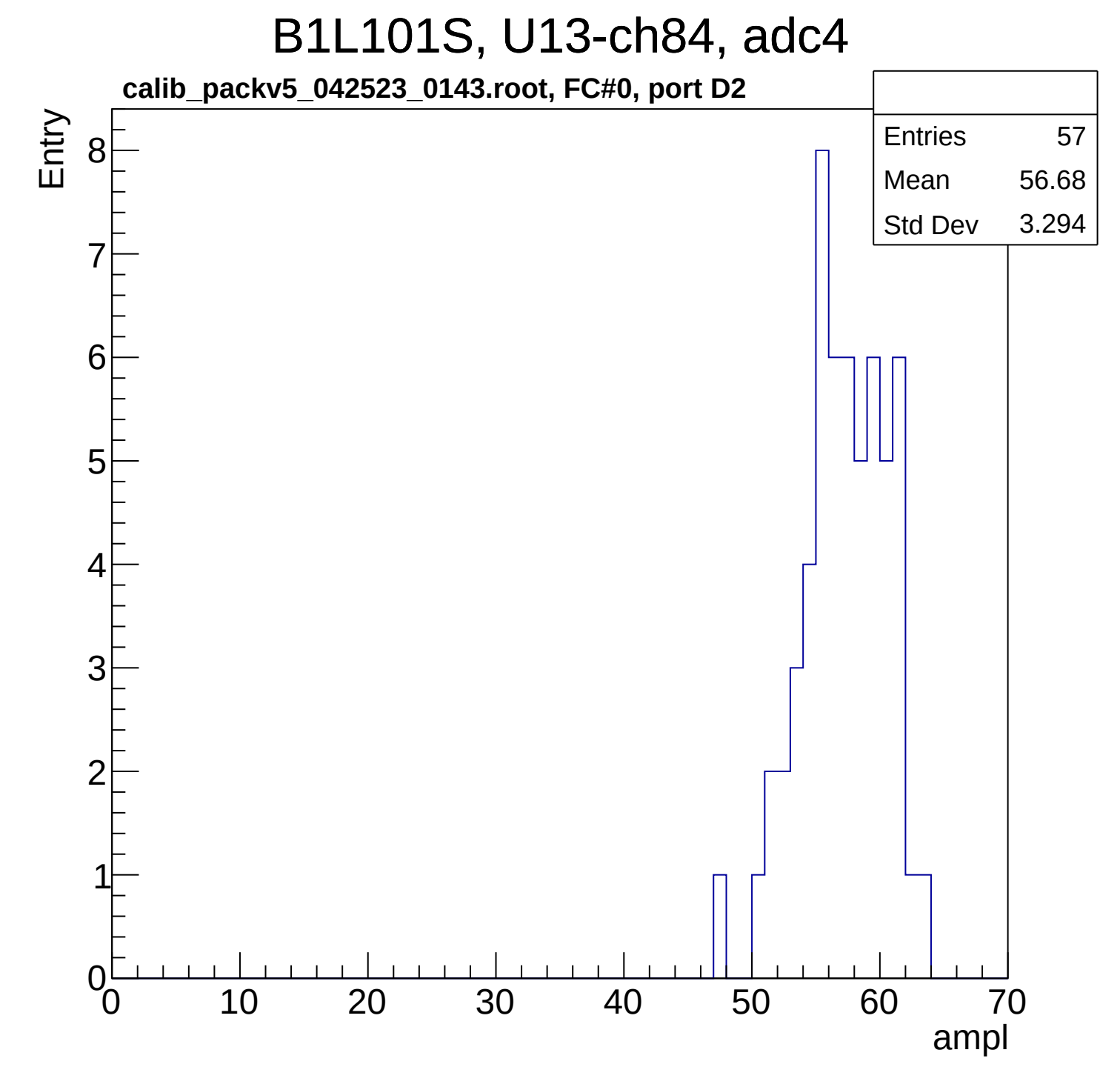
B1L101S, U13-ch84, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 56.68
Std Dev 3.294
Entry
ampl
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4
5
6
7
8
0
10
20
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40
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60
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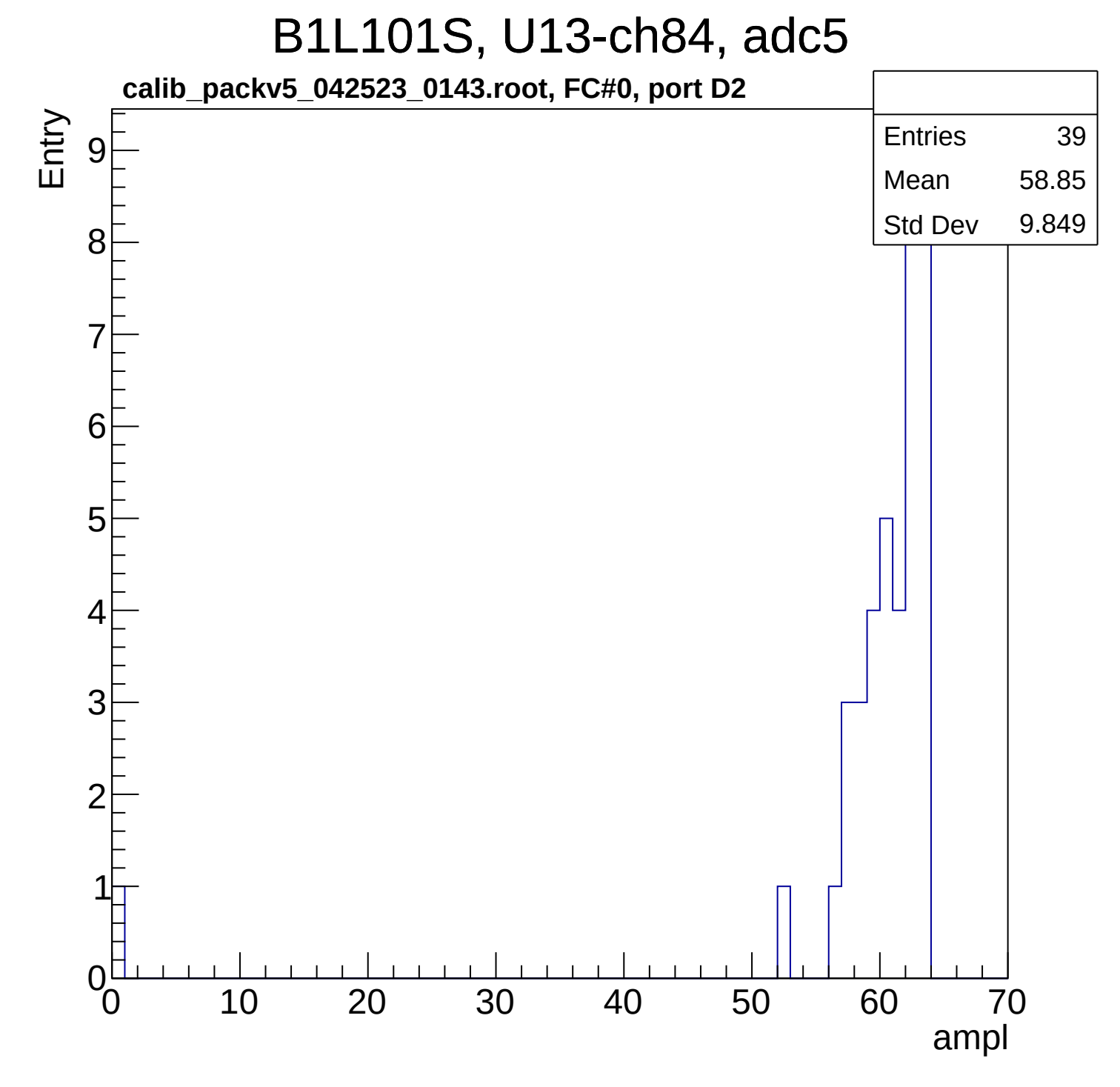
B1L101S, U13-ch84, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 39
Mean 58.85
Std Dev 9.849
Entry
ampl
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3
4
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7
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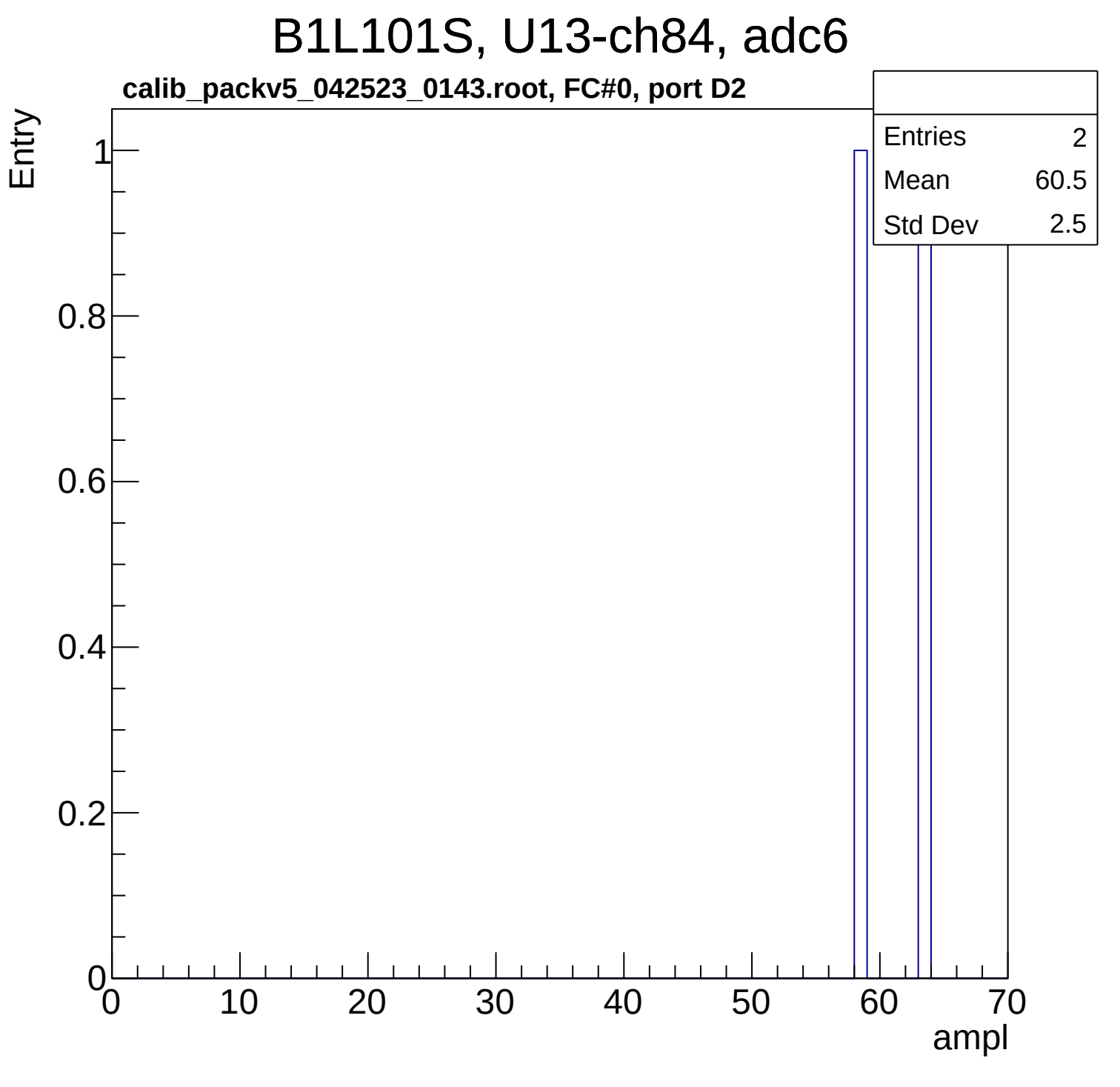
B1L101S, U13-ch84, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 60.5
Std Dev 2.5
Entry
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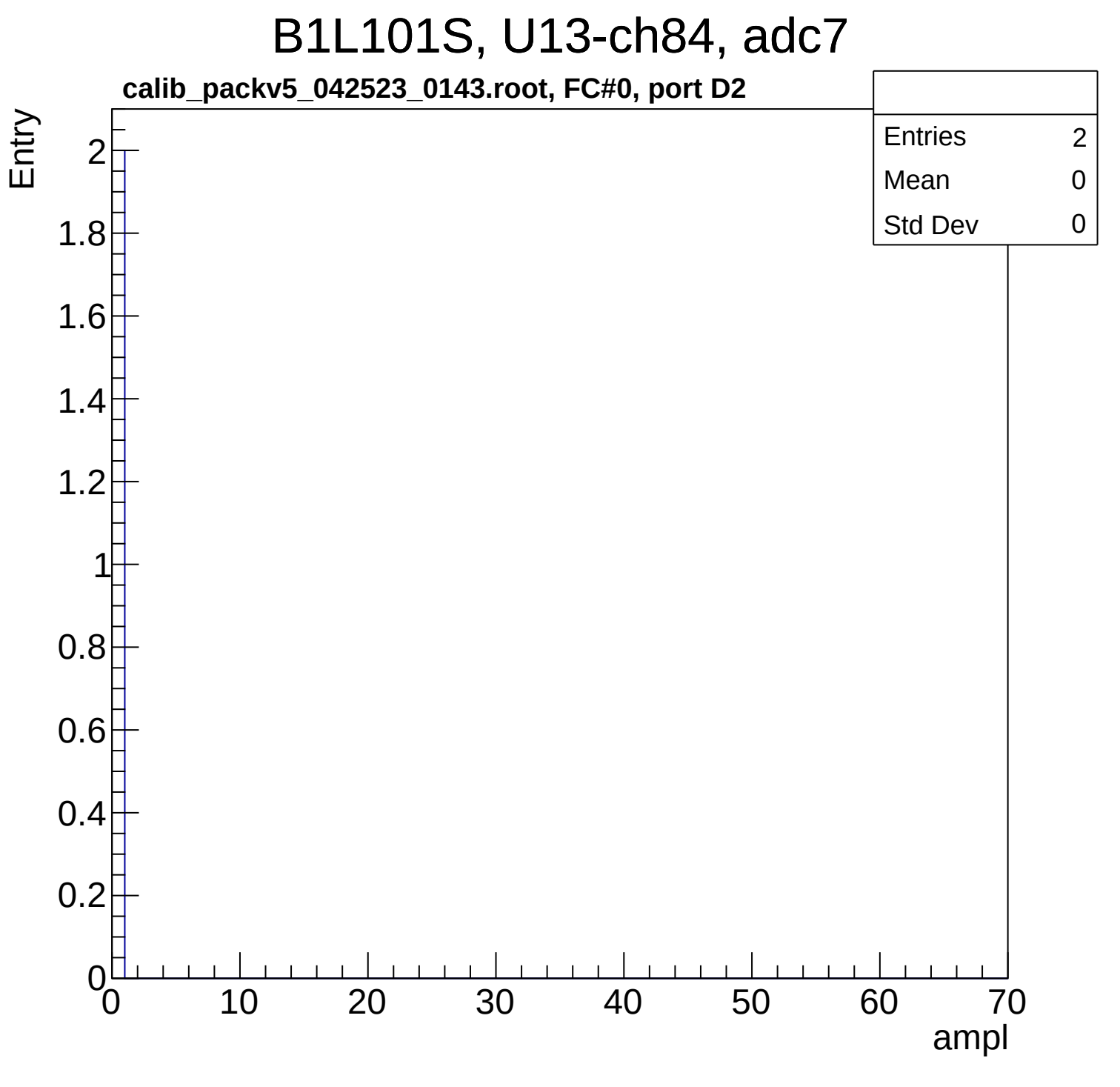
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch84, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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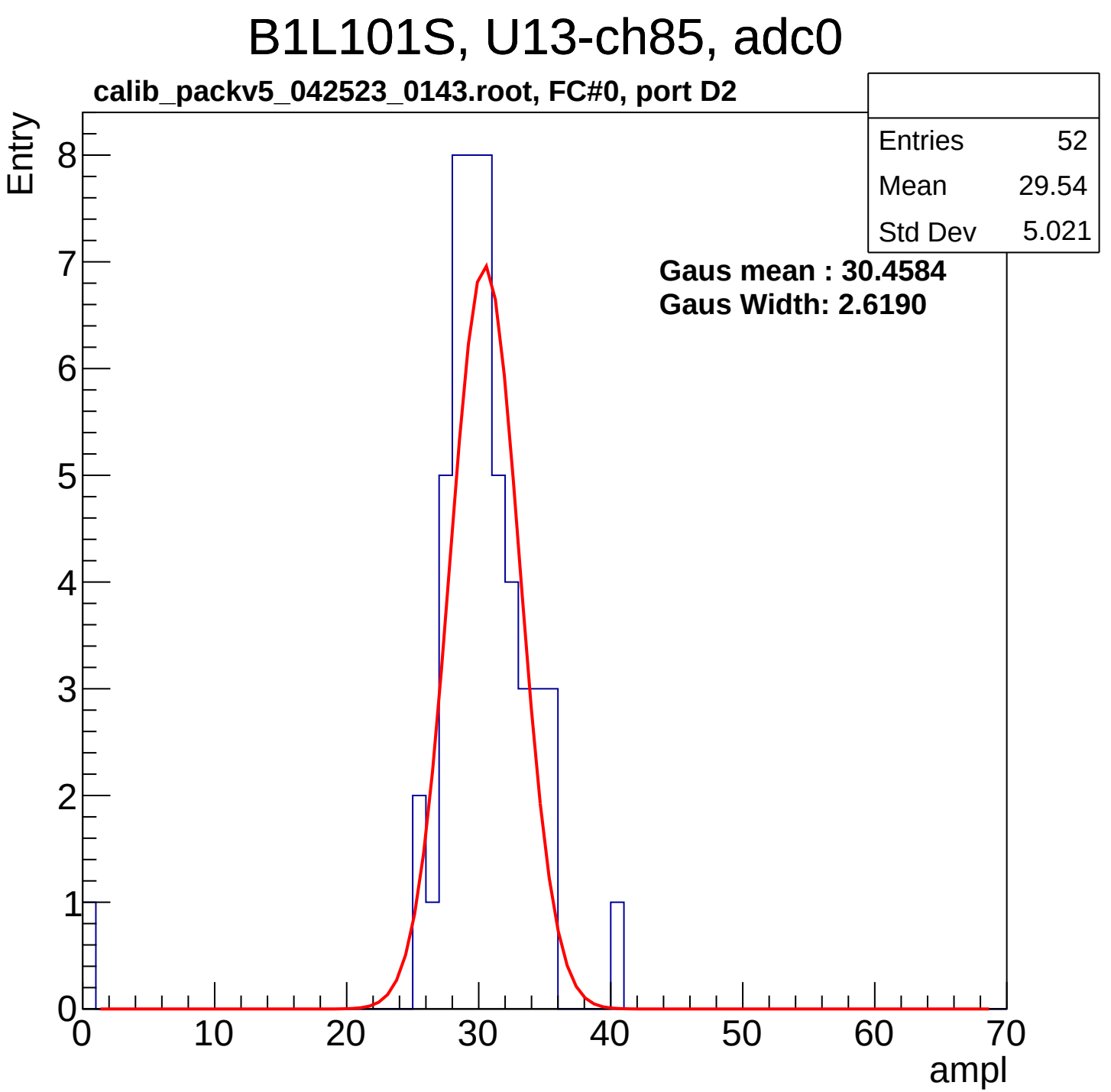
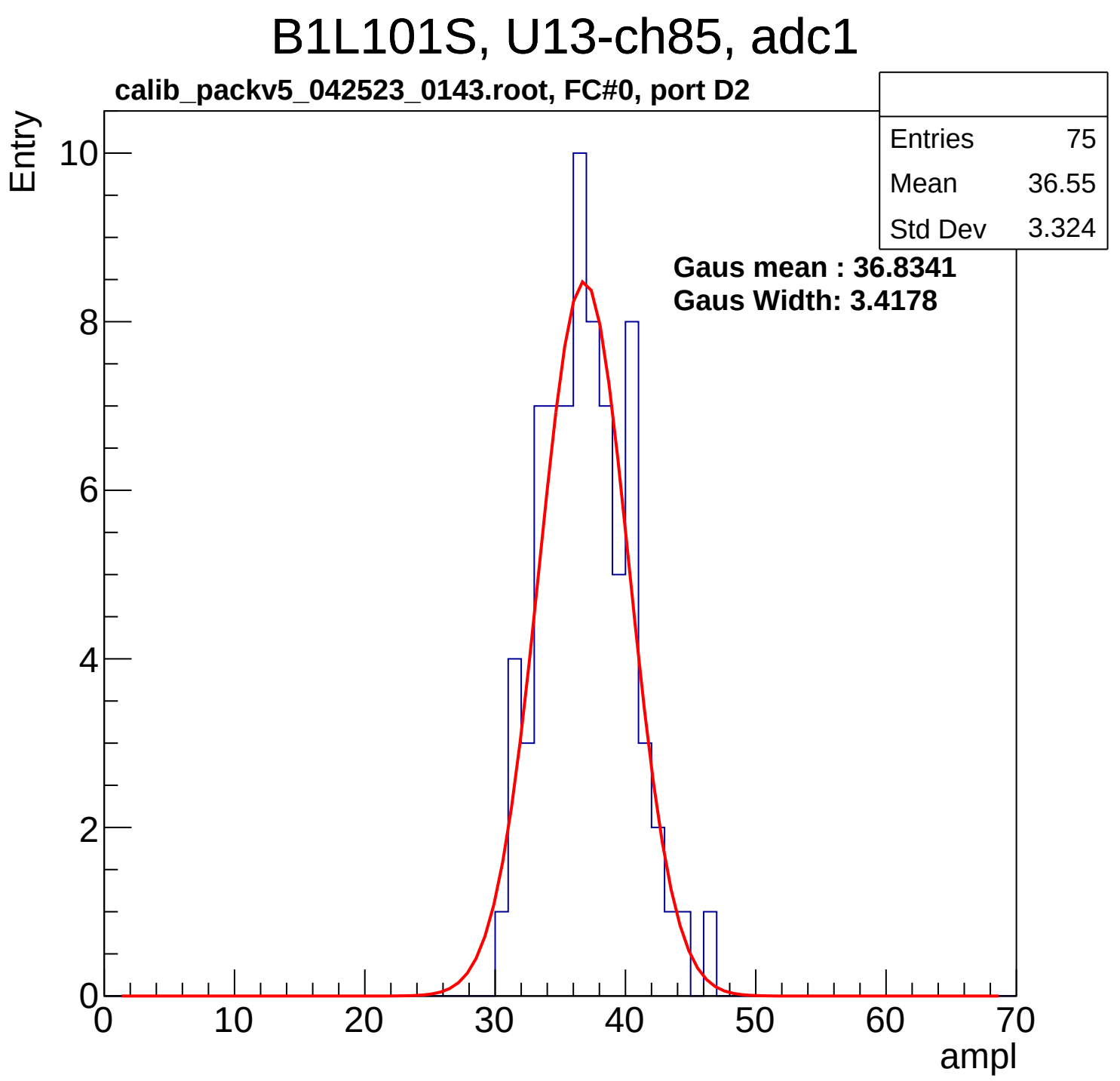
50
60
70
ampl


B1L101S, U13-ch85, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 52
Mean 29.54
Std Dev 5.021
Gaus mean : 30.4584
Gaus Width: 2.6190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch85, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 36.55
Std Dev 3.324
Gaus mean : 36.8341
Gaus Width: 3.4178
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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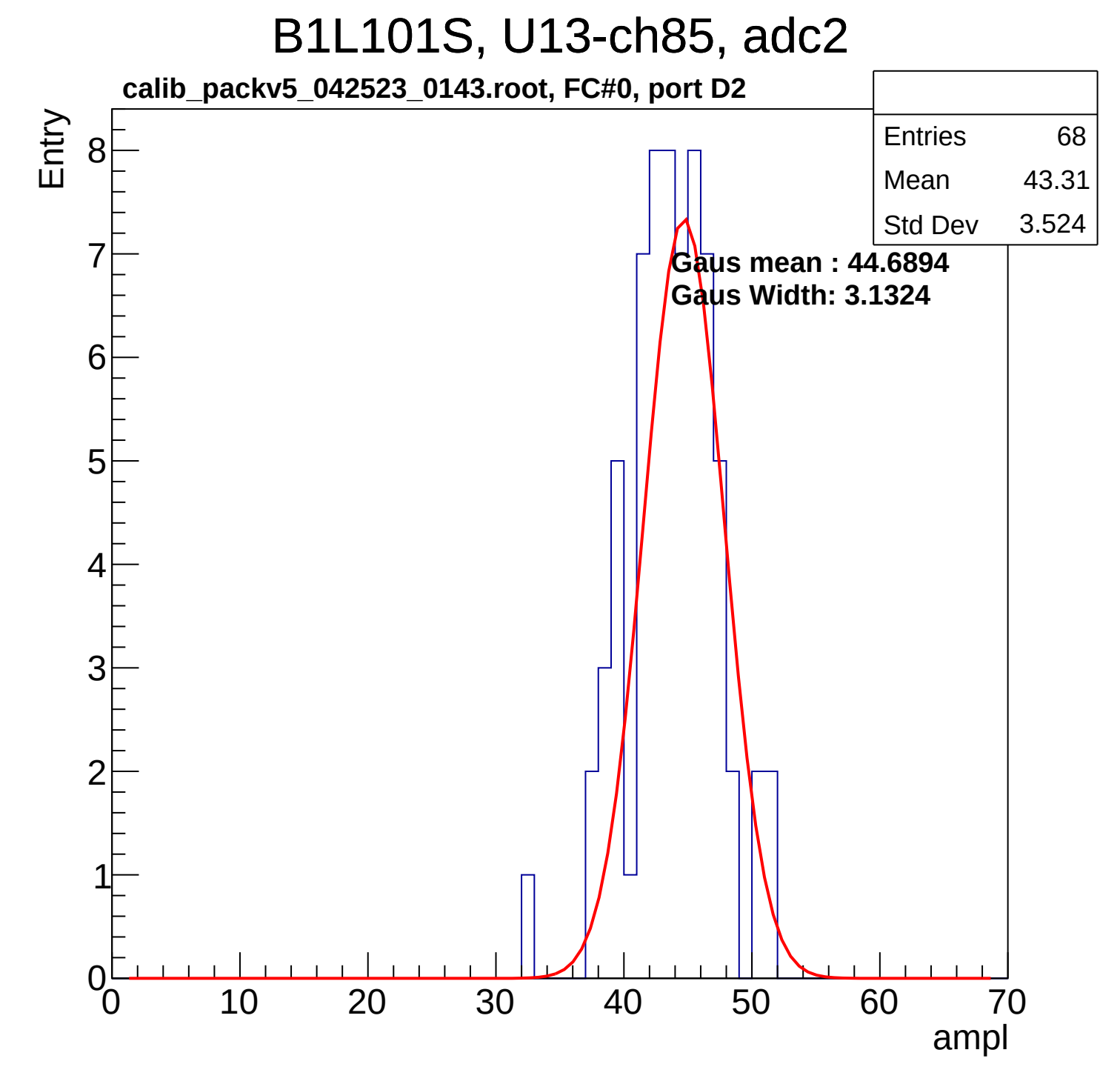
B1L101S, U13-ch85, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 43.31
Std Dev 3.524
Gaus mean : 44.6894
Gaus Width: 3.1324
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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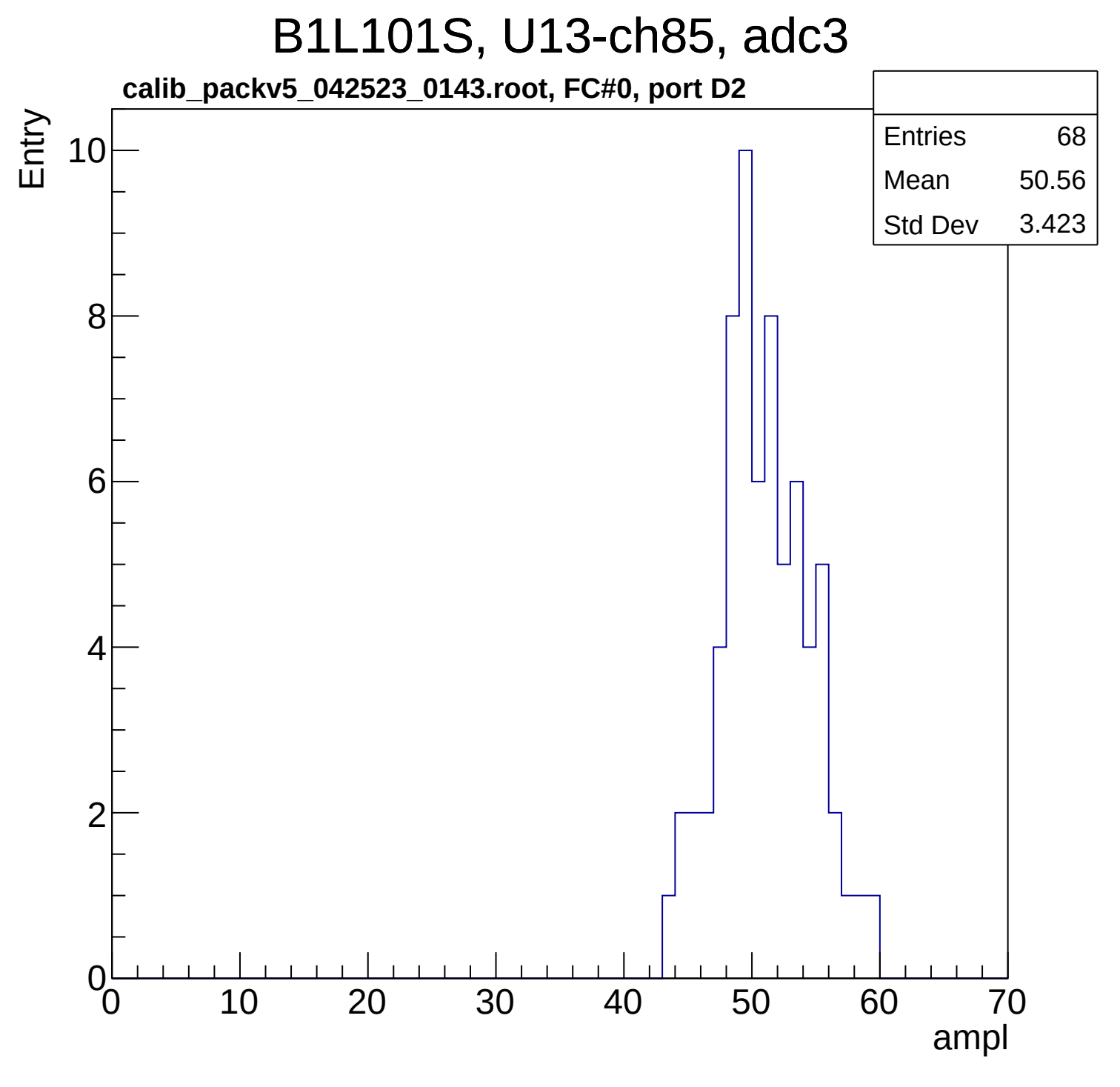
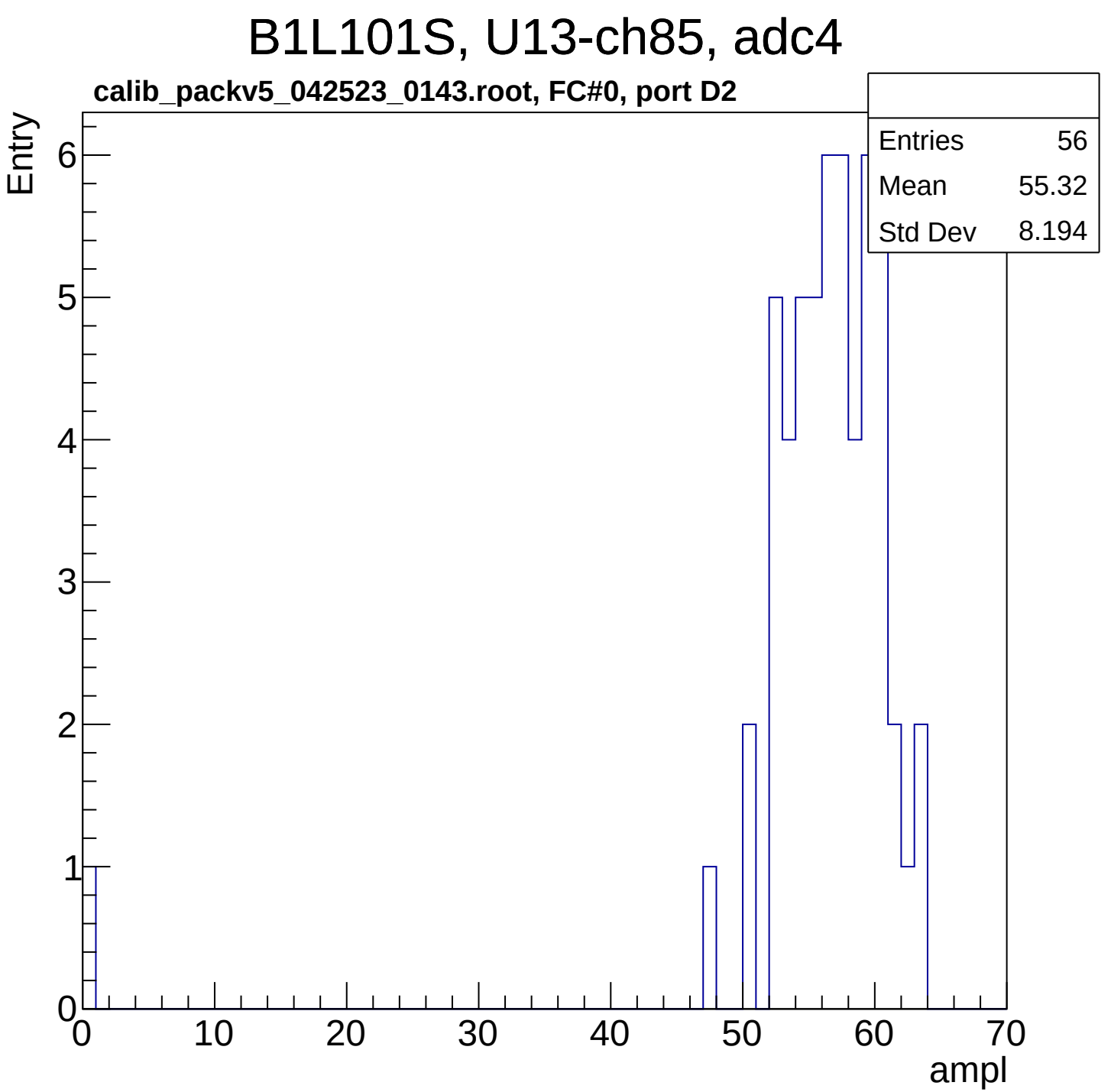
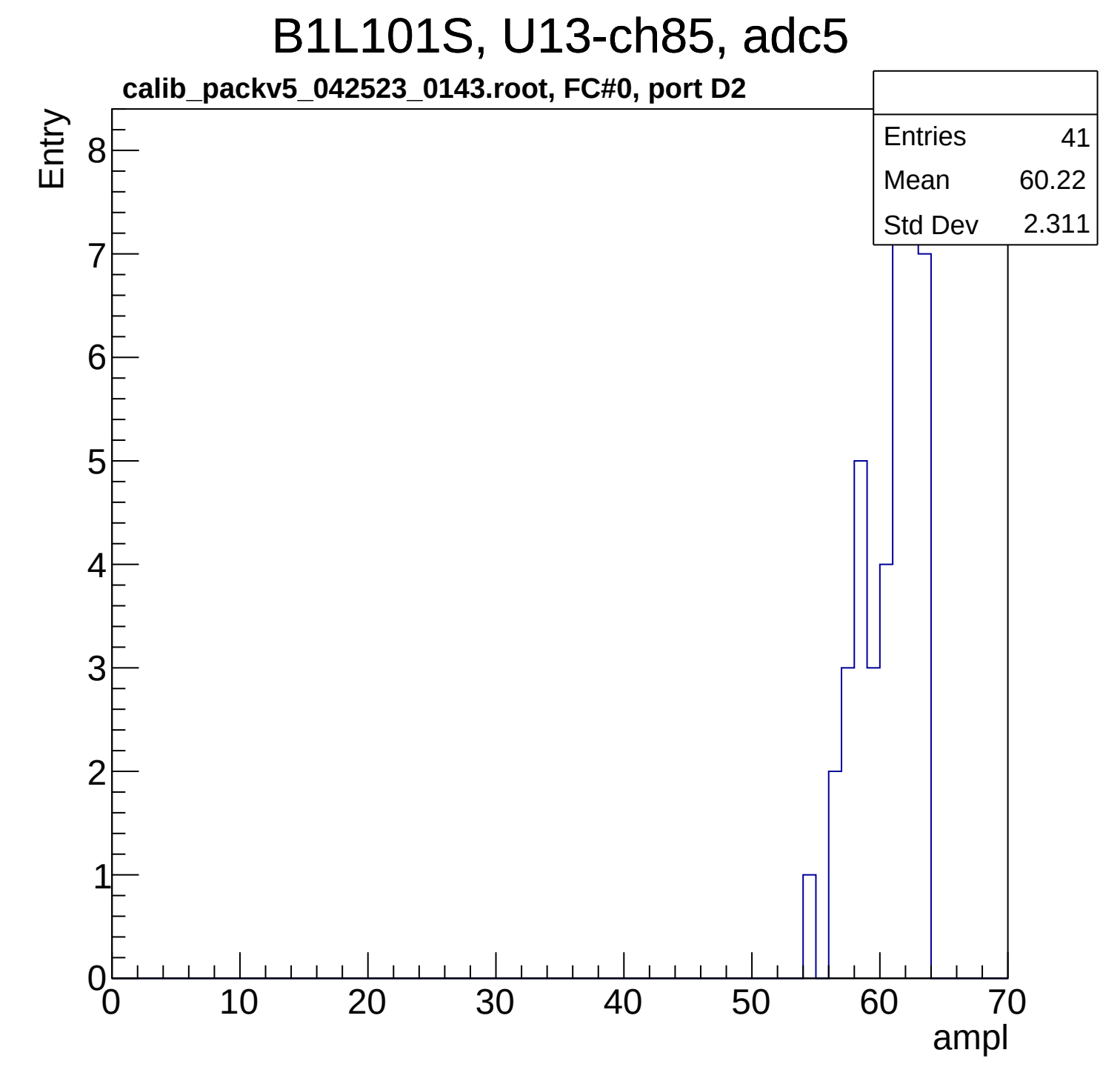

B1L101S, U13-ch85, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 50.56
Std Dev 3.423
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch85, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 55.32
Std Dev 8.194
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch85, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 60.22
Std Dev 2.311
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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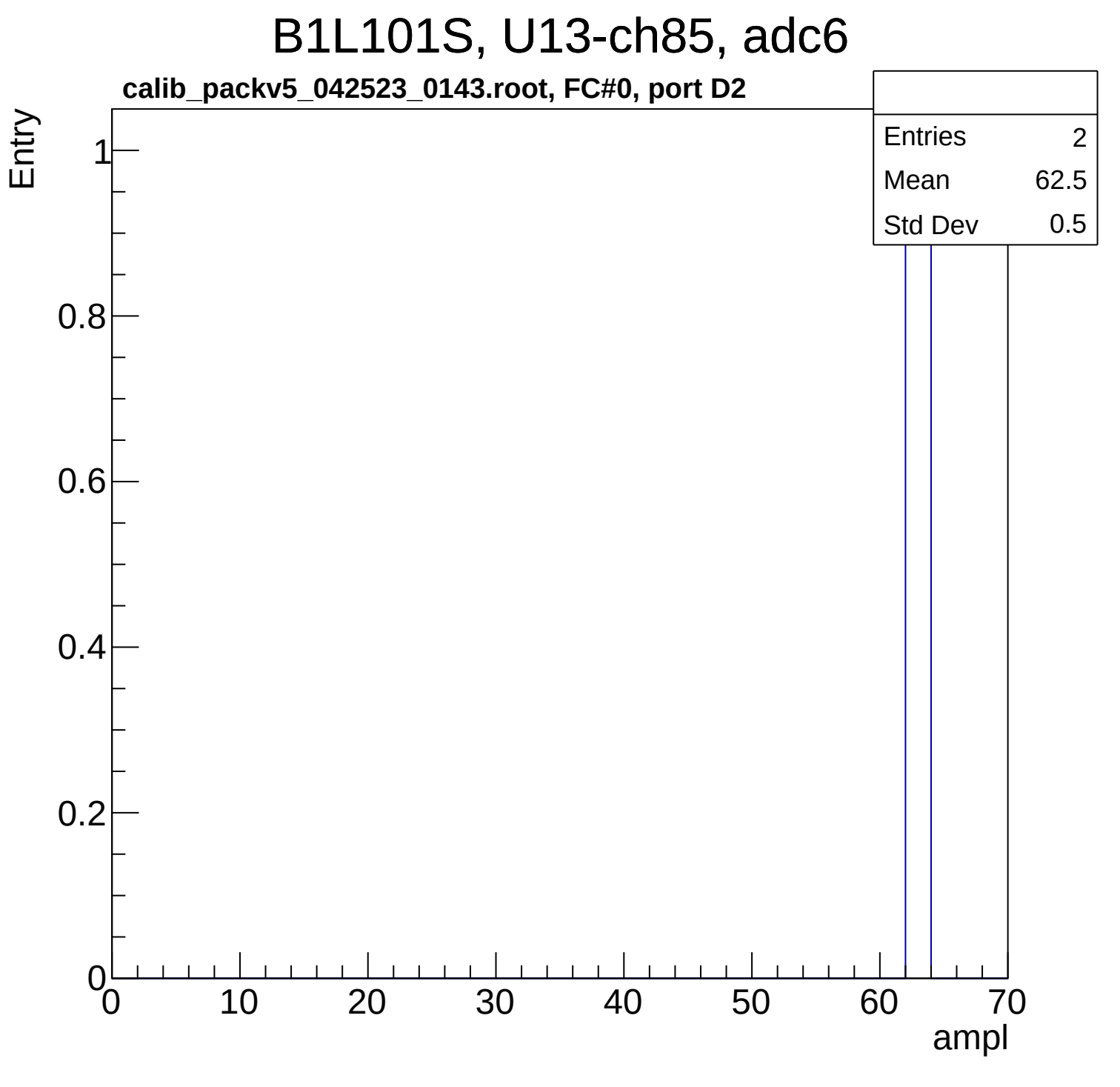
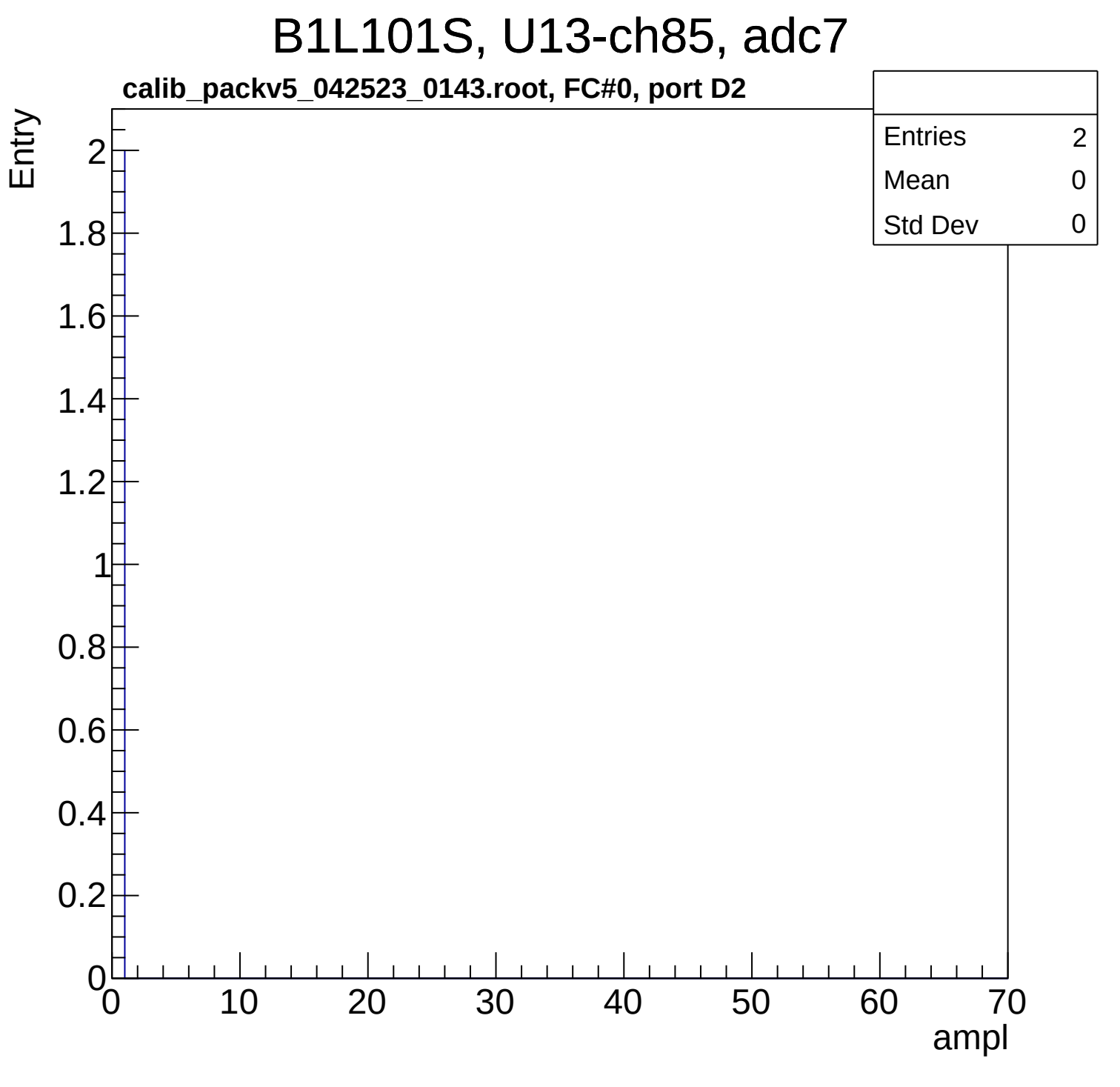
60
70


B1L101S, U13-ch85, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch85, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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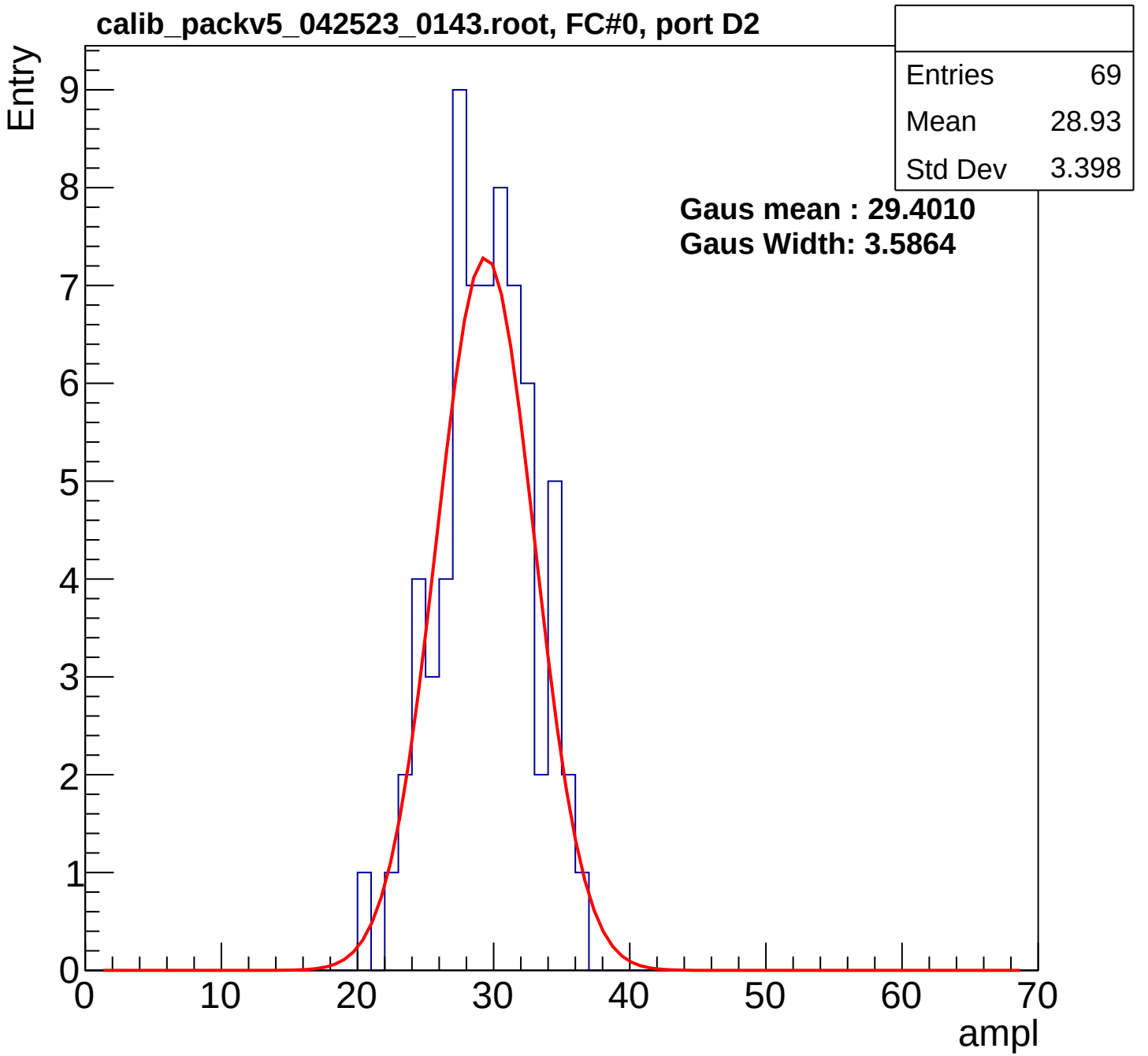
B1L101S, U13-ch86, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 28.93
Std Dev 3.398
Gaus mean : 29.4010
Gaus Width: 3.5864
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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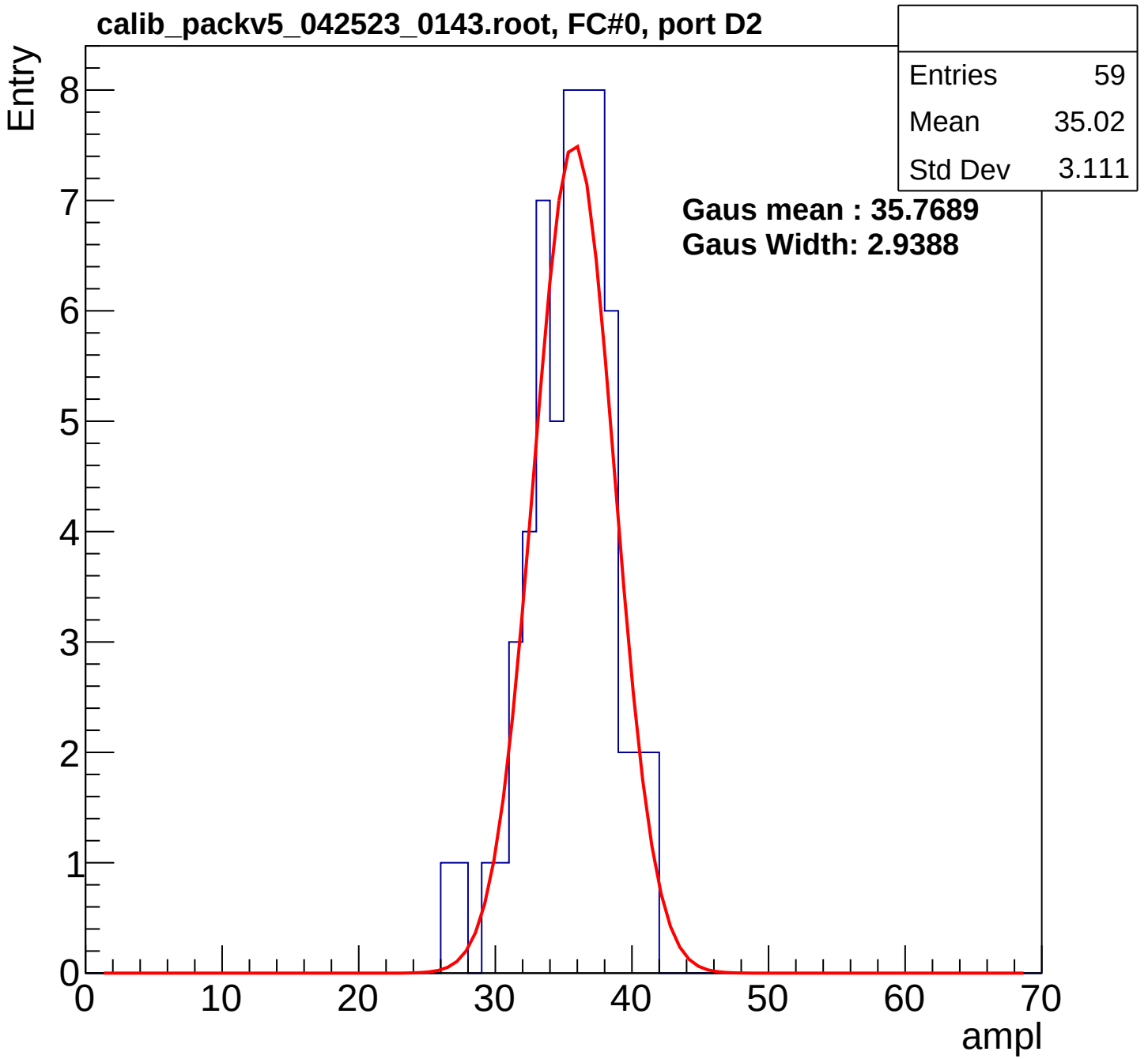
B1L101S, U13-ch86, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 35.02
Std Dev 3.111
Gaus mean : 35.7689
Gaus Width: 2.9388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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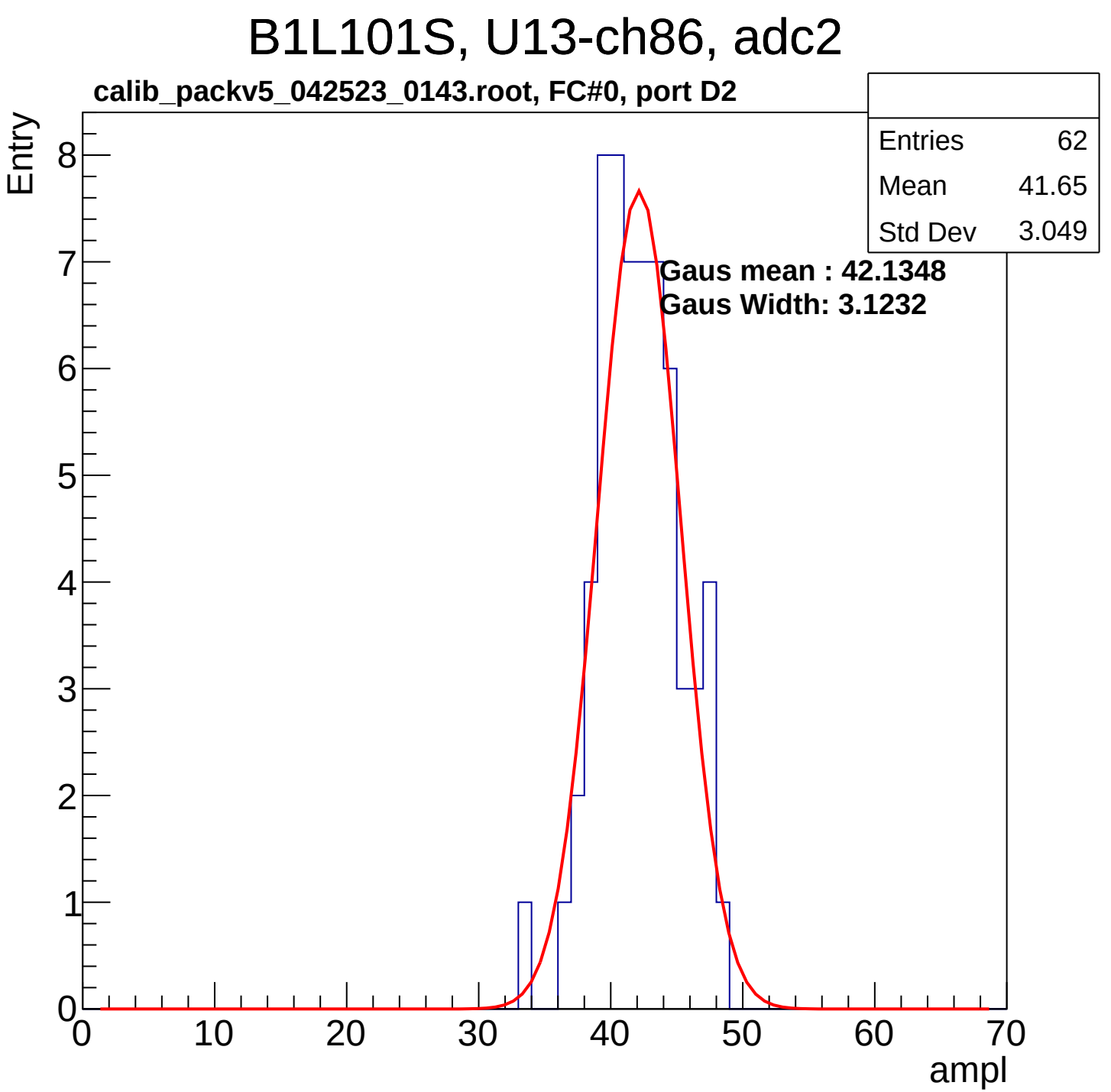
B1L101S, U13-ch86, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 41.65
Std Dev 3.049
Gaus mean : 42.1348
Gaus Width: 3.1232
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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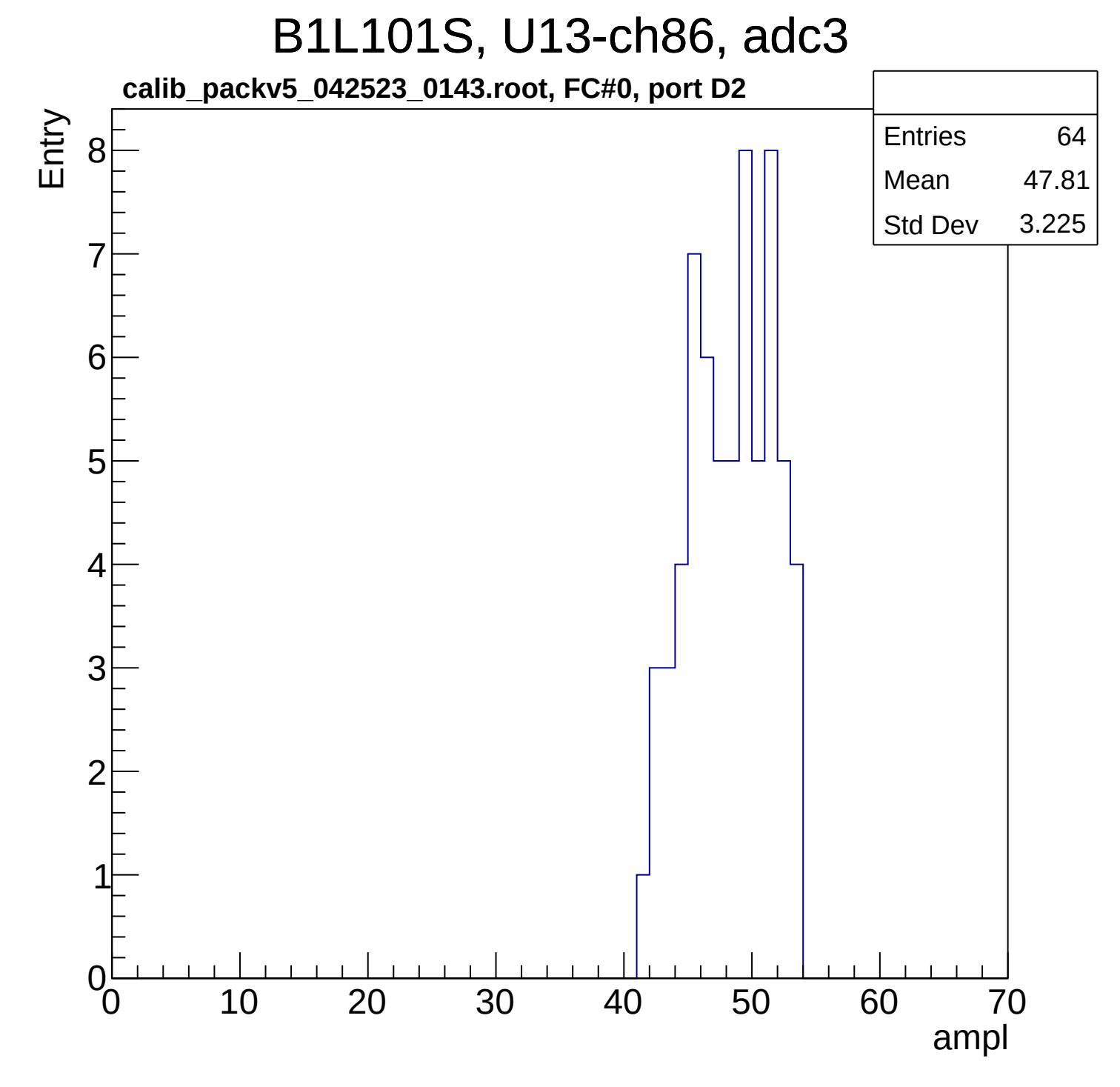
B1L101S, U13-ch86, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 47.81
Std Dev 3.225
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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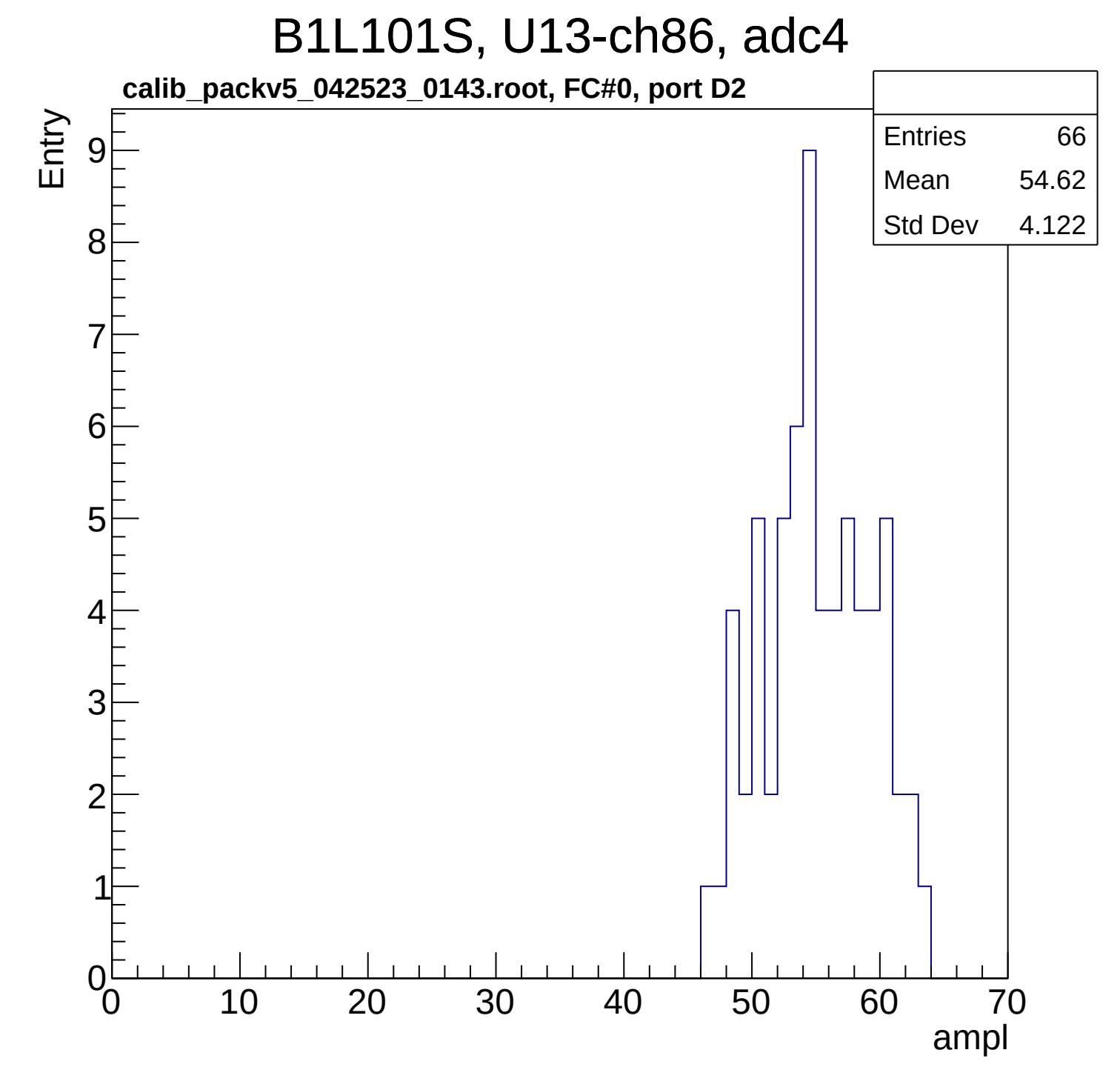
B1L101S, U13-ch86, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 54.62
Std Dev 4.122
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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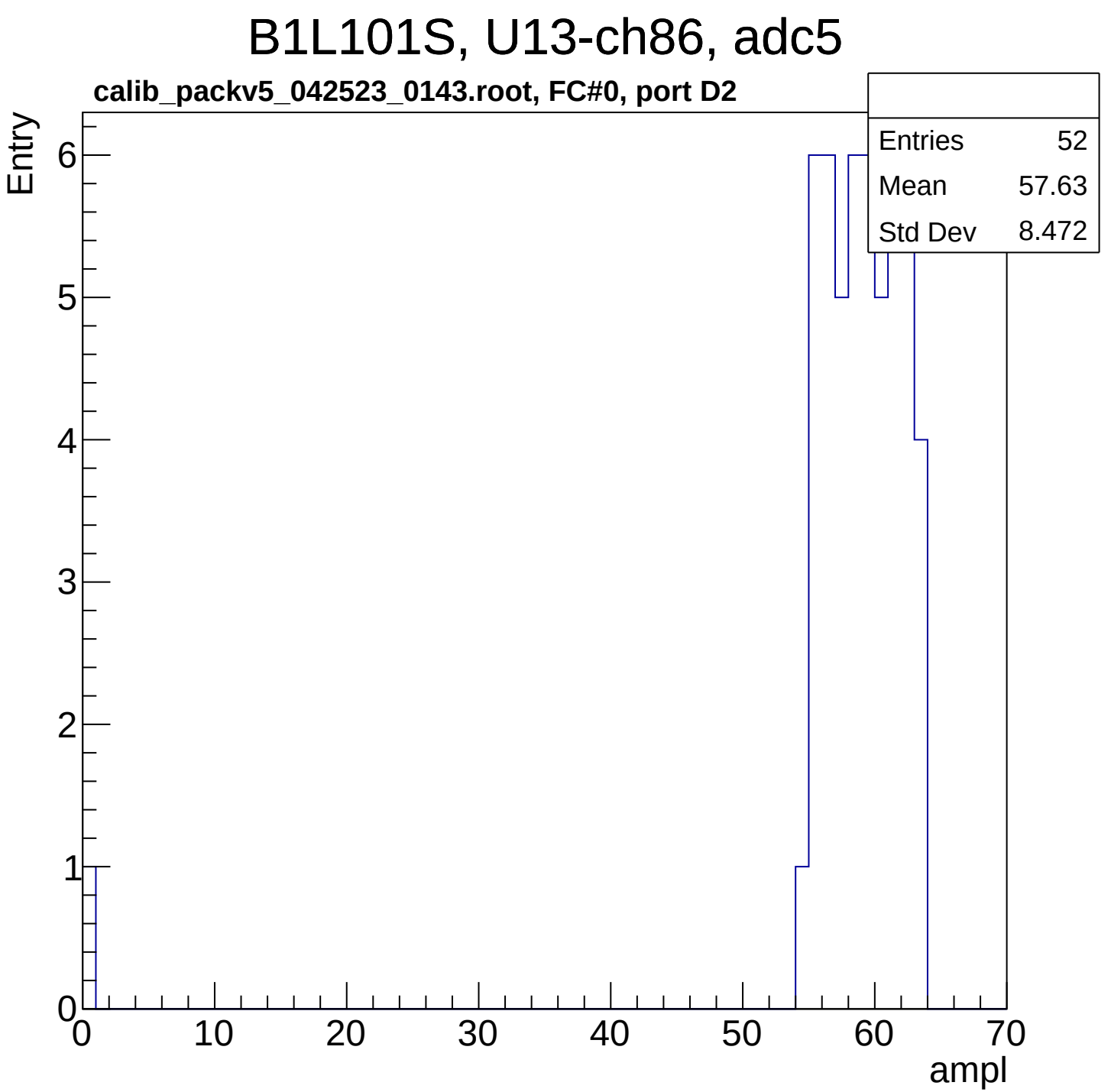
B1L101S, U13-ch86, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 52
Mean 57.63
Std Dev 8.472
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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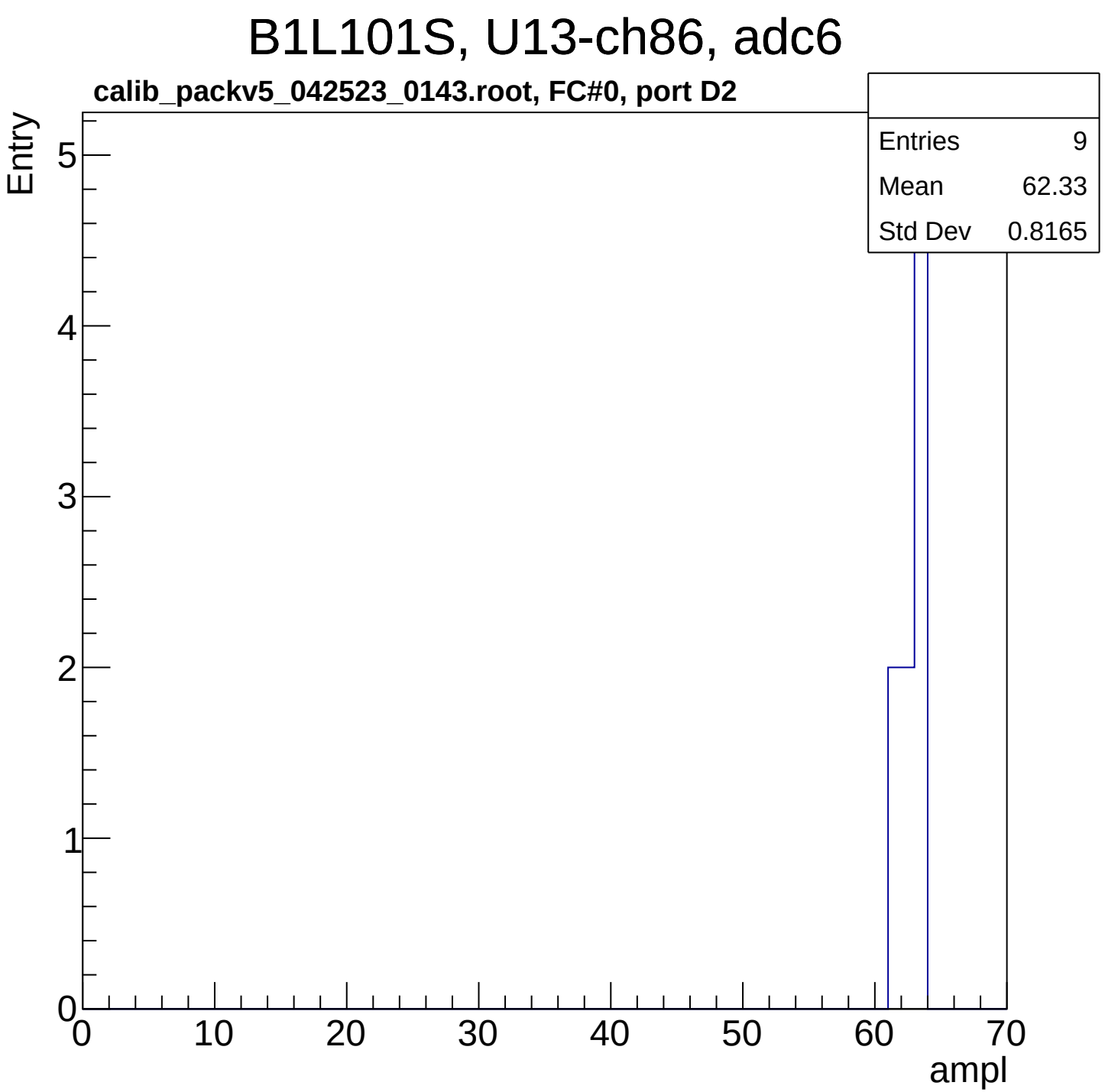
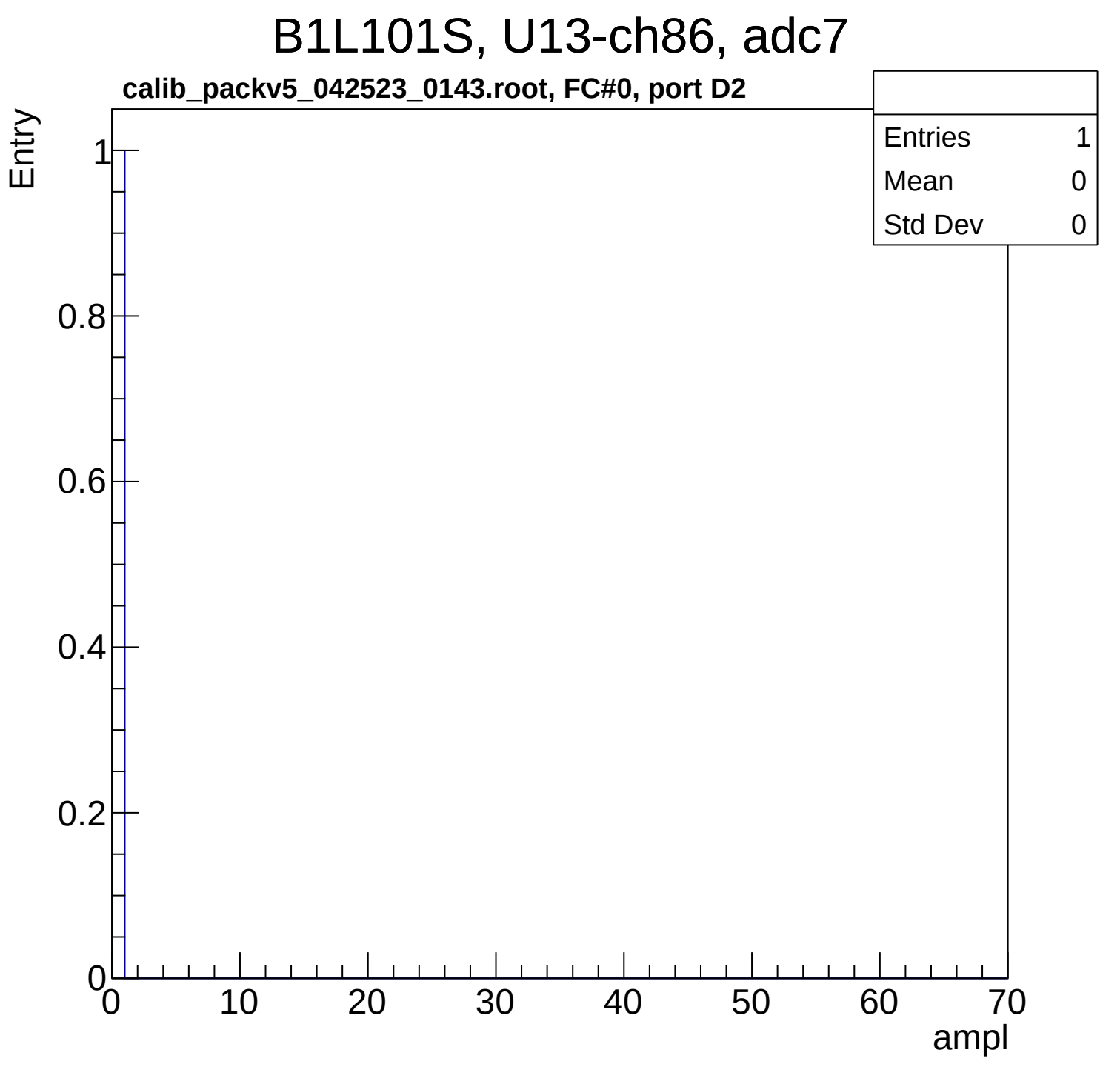

B1L101S, U13-ch86, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 9
Mean 62.33
Std Dev 0.8165
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch86, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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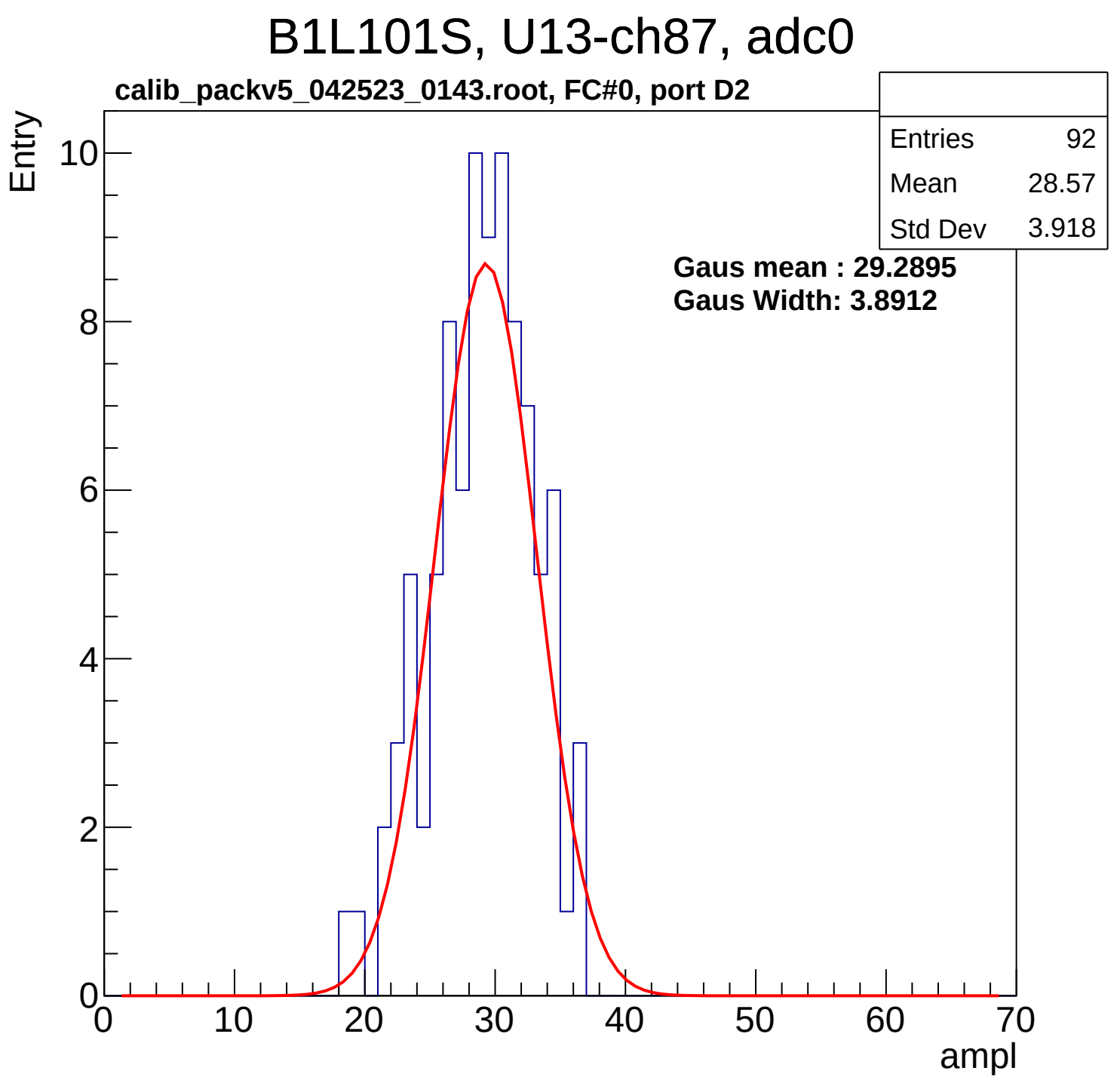
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch87, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 92
Mean 28.57
Std Dev 3.918
Gaus mean : 29.2895
Gaus Width: 3.8912
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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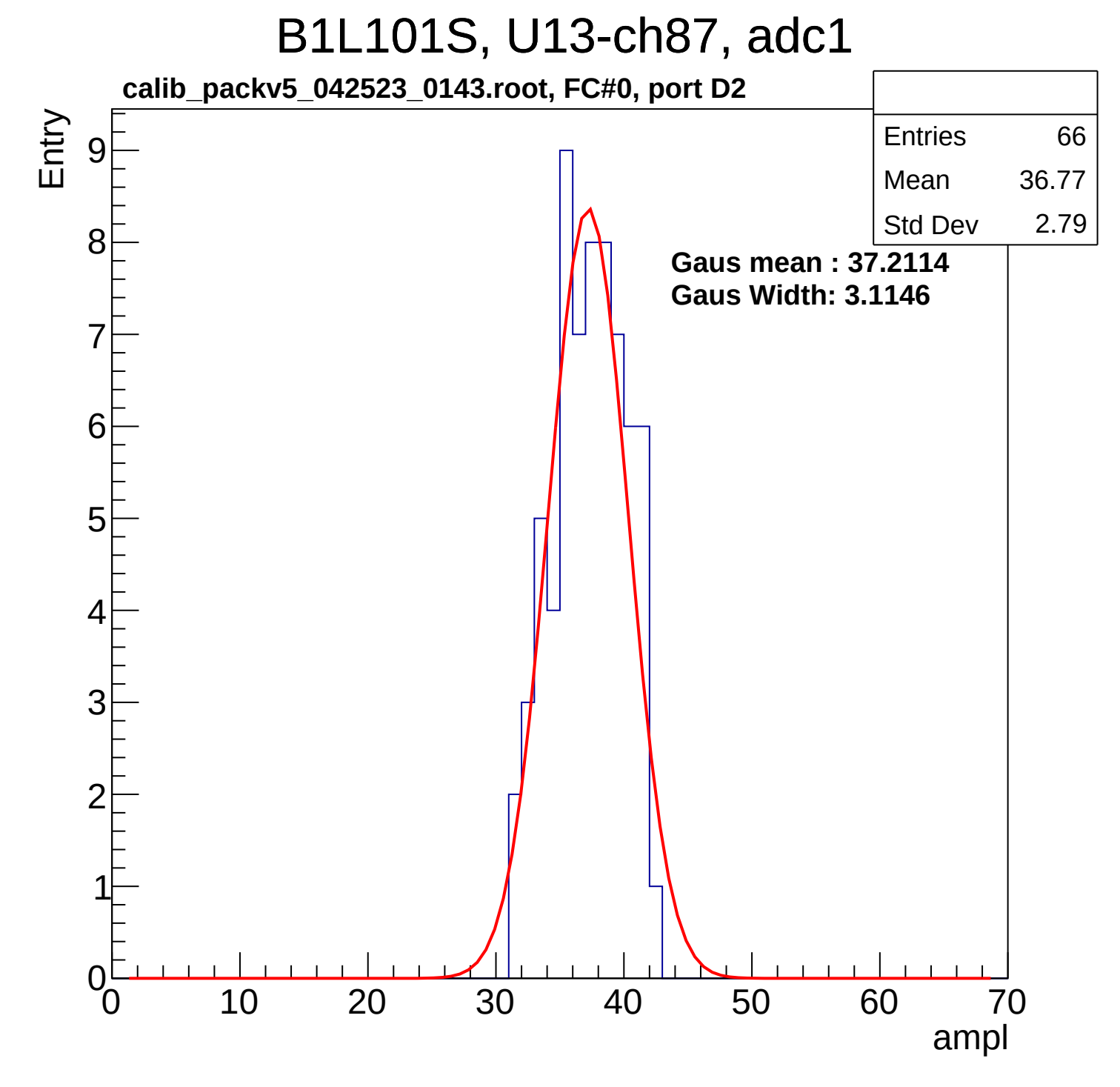
B1L101S, U13-ch87, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 36.77
Std Dev 2.79
Gaus mean : 37.2114
Gaus Width: 3.1146
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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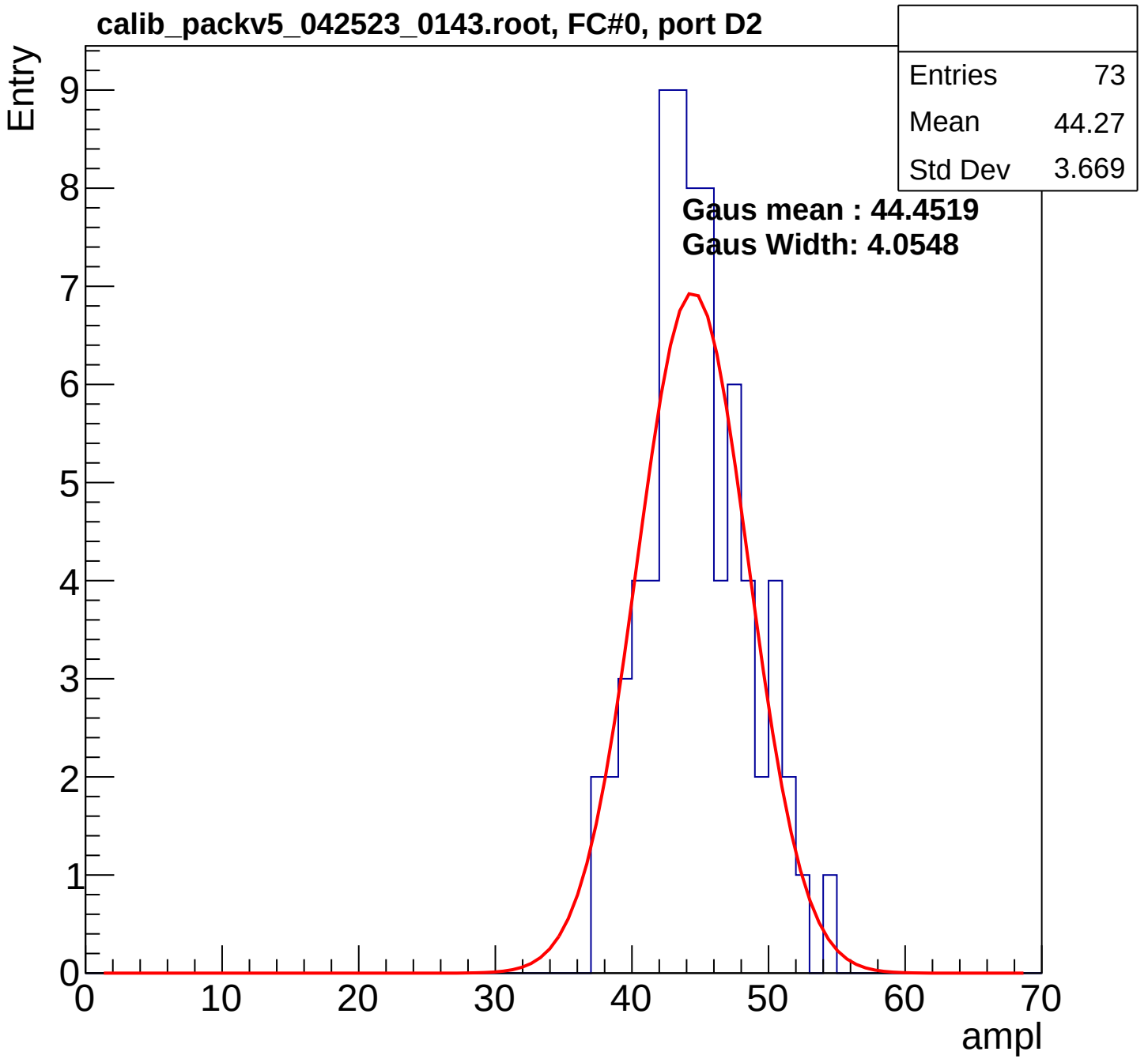
B1L101S, U13-ch87, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 44.27
Std Dev 3.669
Gaus mean : 44.4519
Gaus Width: 4.0548
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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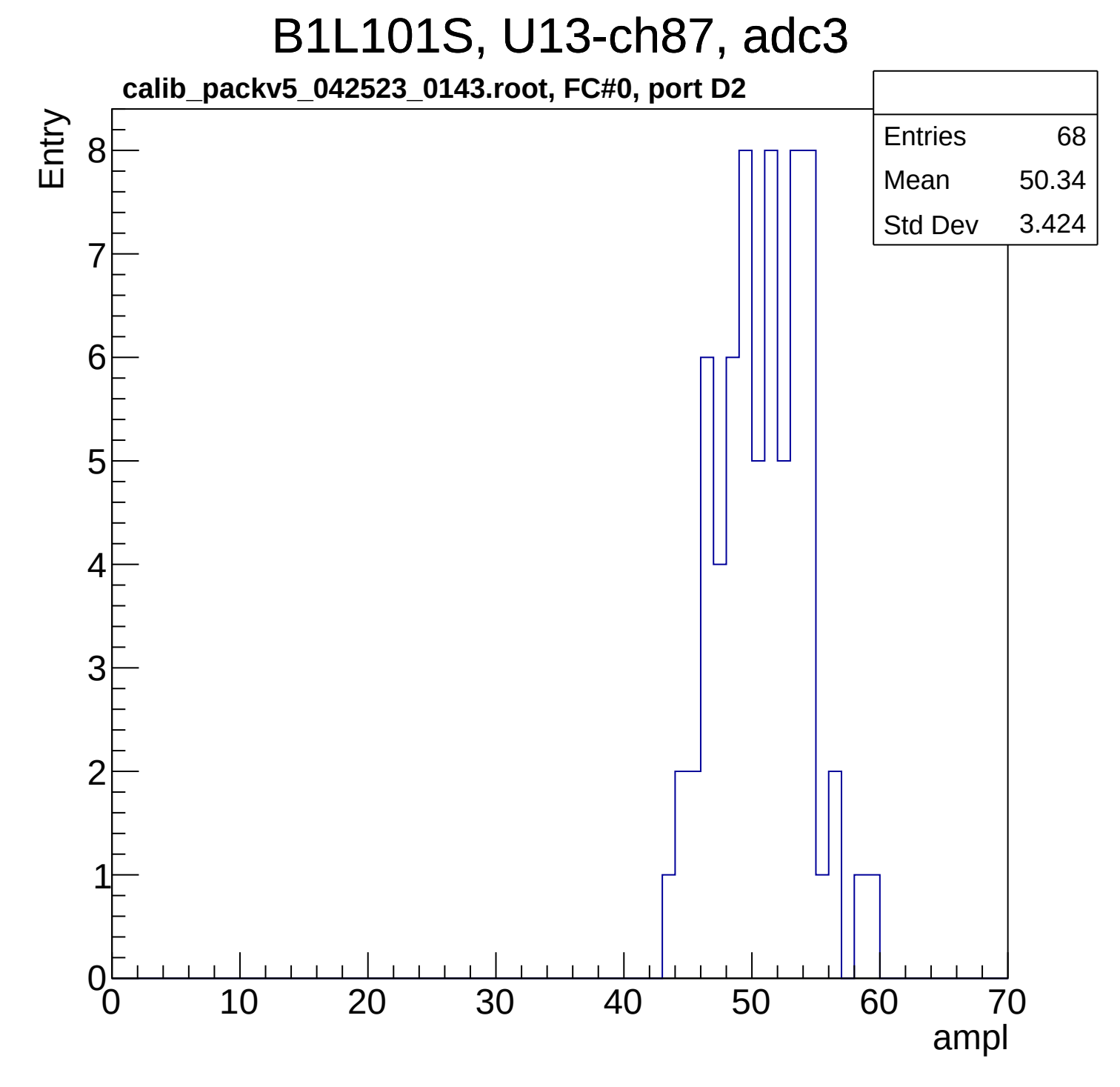
B1L101S, U13-ch87, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 50.34
Std Dev 3.424
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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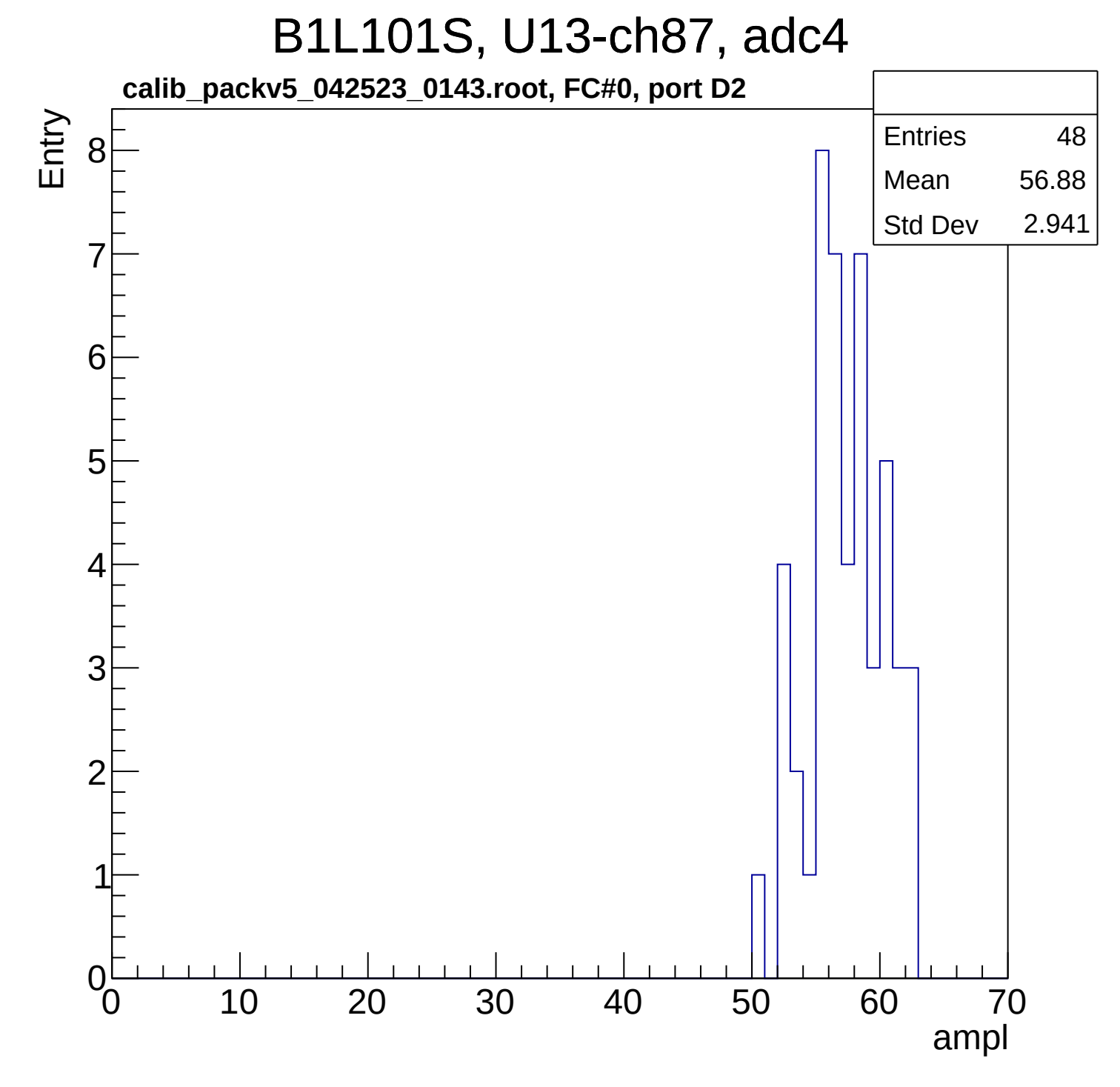
B1L101S, U13-ch87, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 56.88
Std Dev 2.941
Entry
ampl
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1
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3
4
5
6
7
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0
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60
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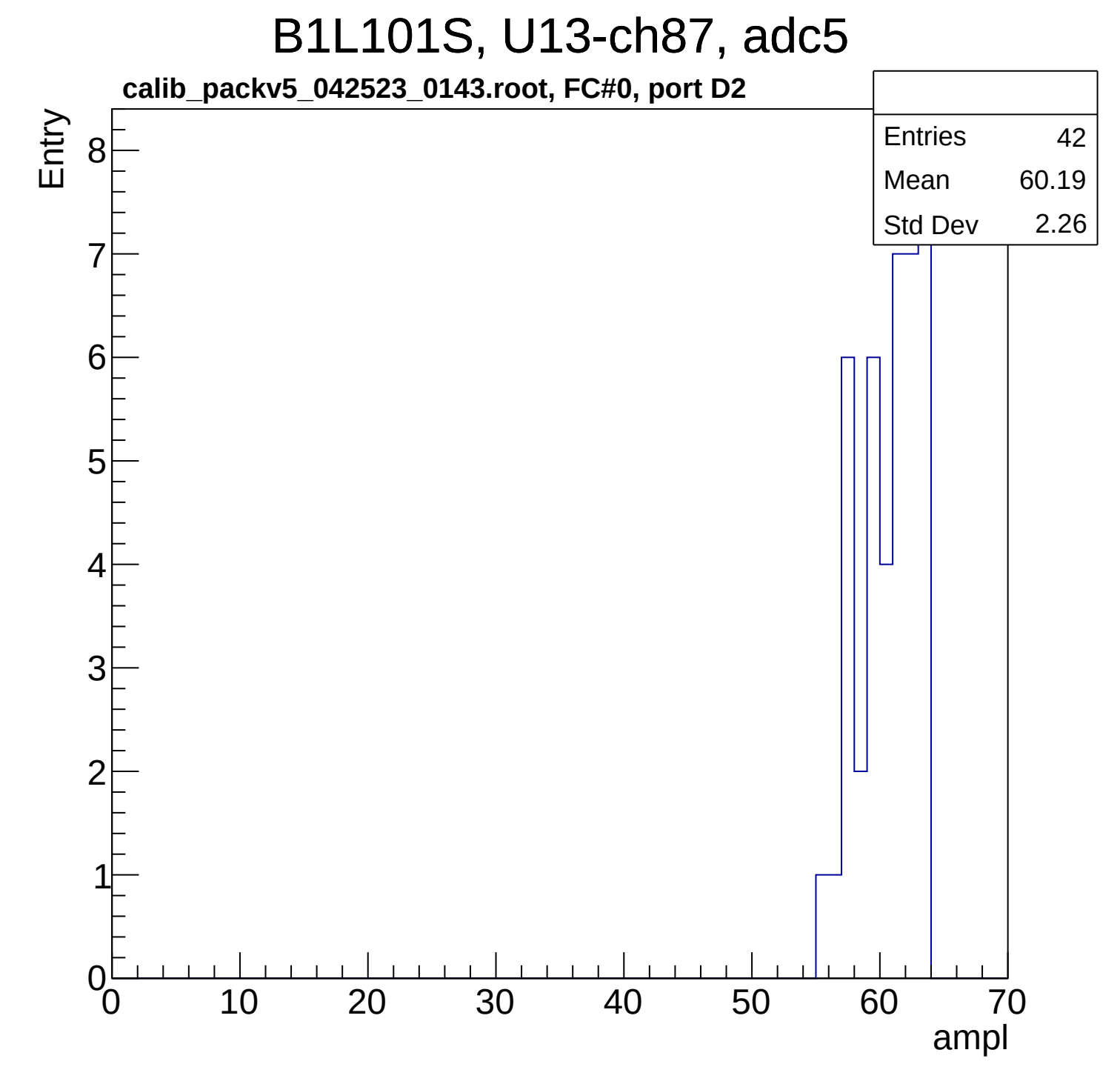
B1L101S, U13-ch87, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 60.19
Std Dev 2.26
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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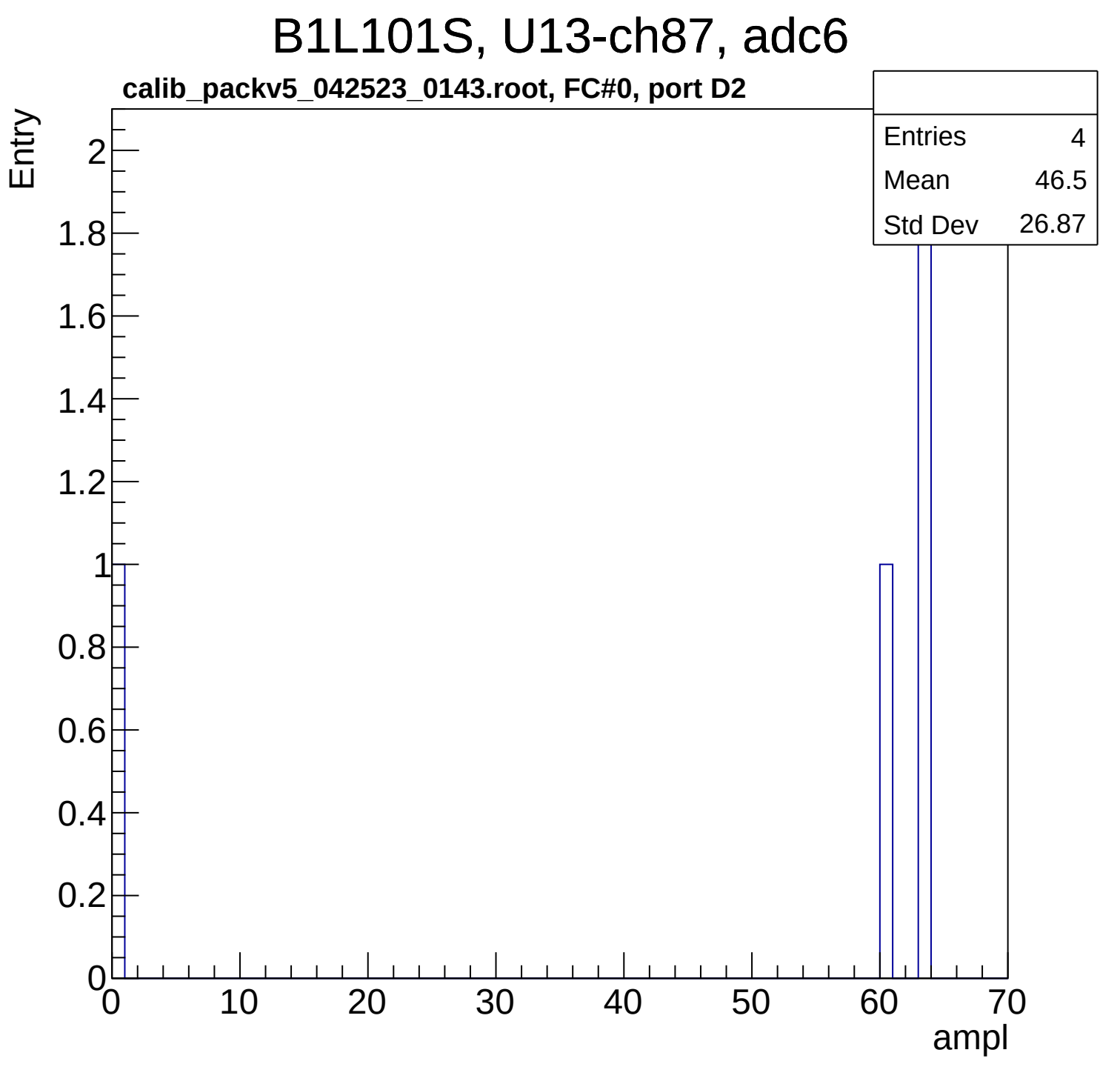
B1L101S, U13-ch87, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 46.5
Std Dev 26.87
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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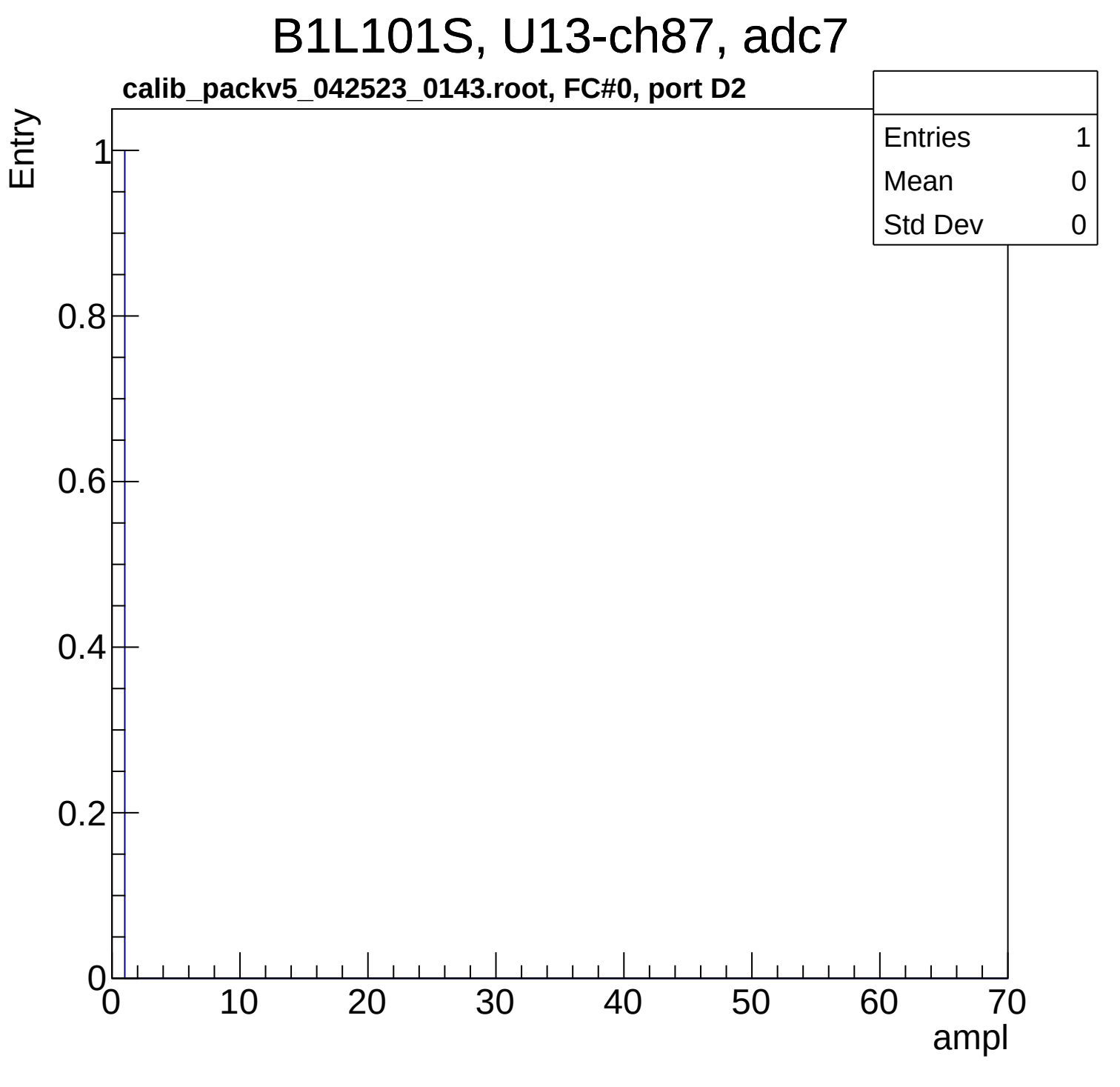
B1L101S, U13-ch87, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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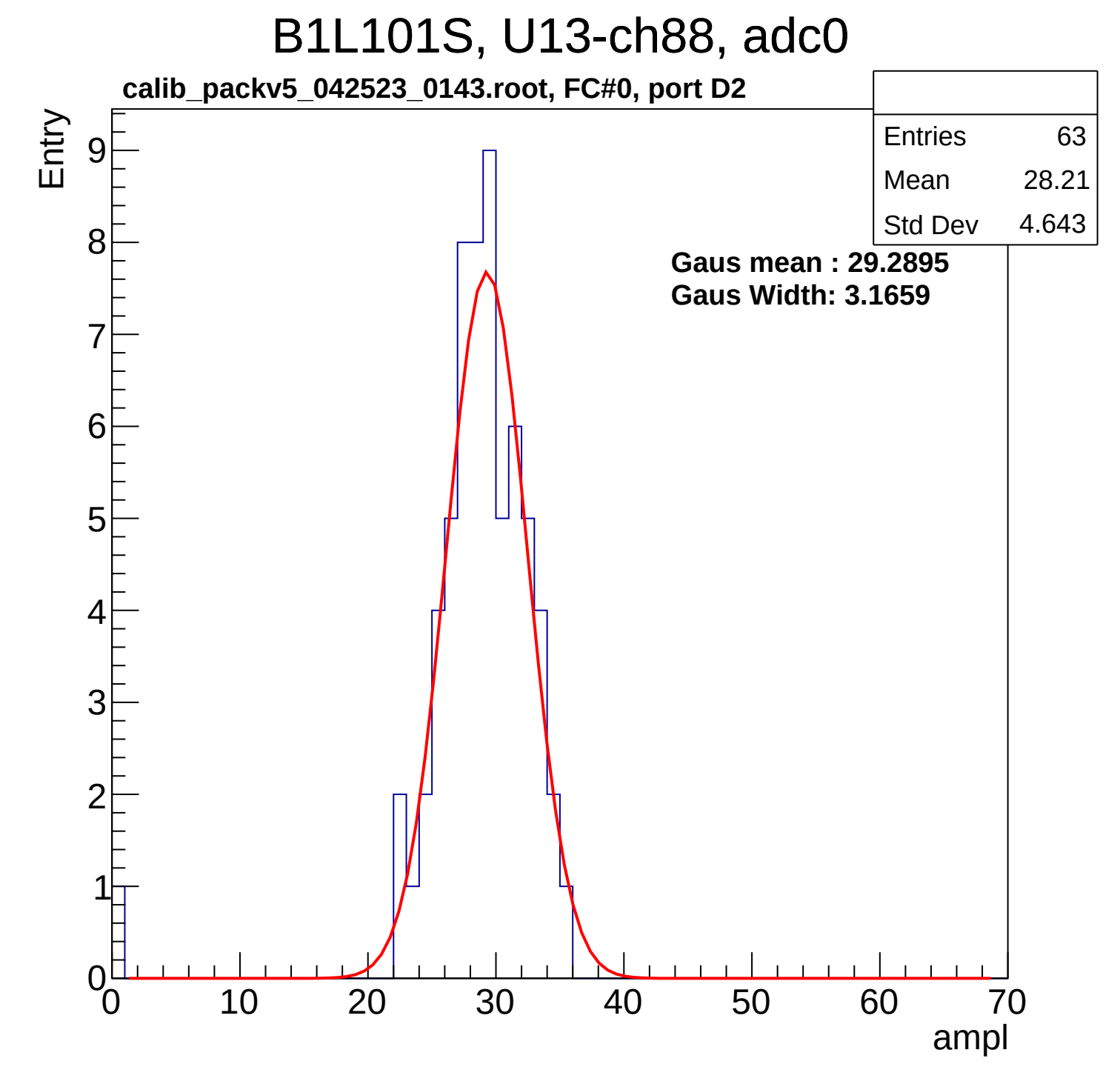
50
60
70
ampl


B1L101S, U13-ch88, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 28.21
Std Dev 4.643
Gaus mean : 29.2895
Gaus Width: 3.1659
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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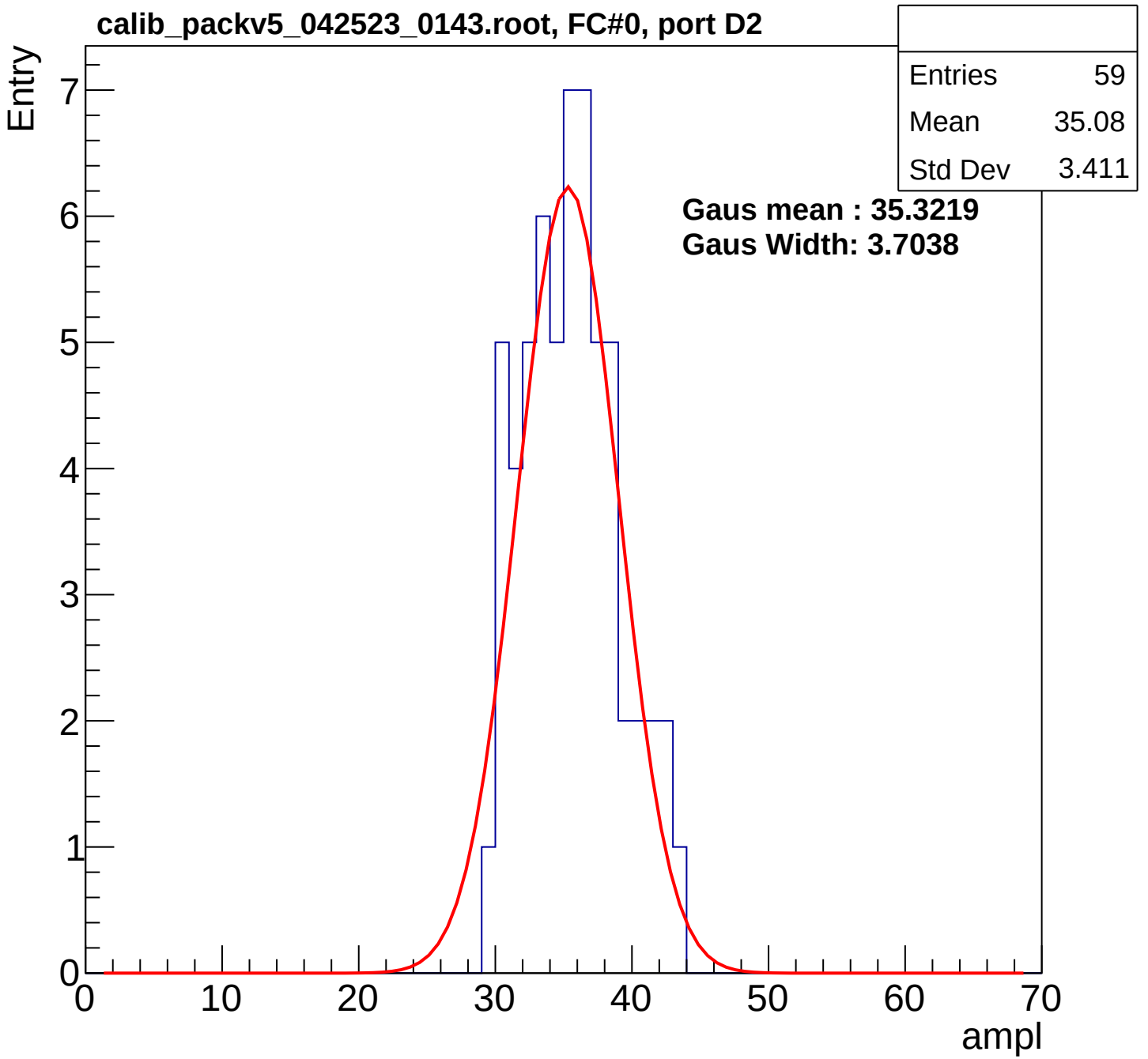
B1L101S, U13-ch88, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 35.08
Std Dev 3.411
Gaus mean : 35.3219
Gaus Width: 3.7038
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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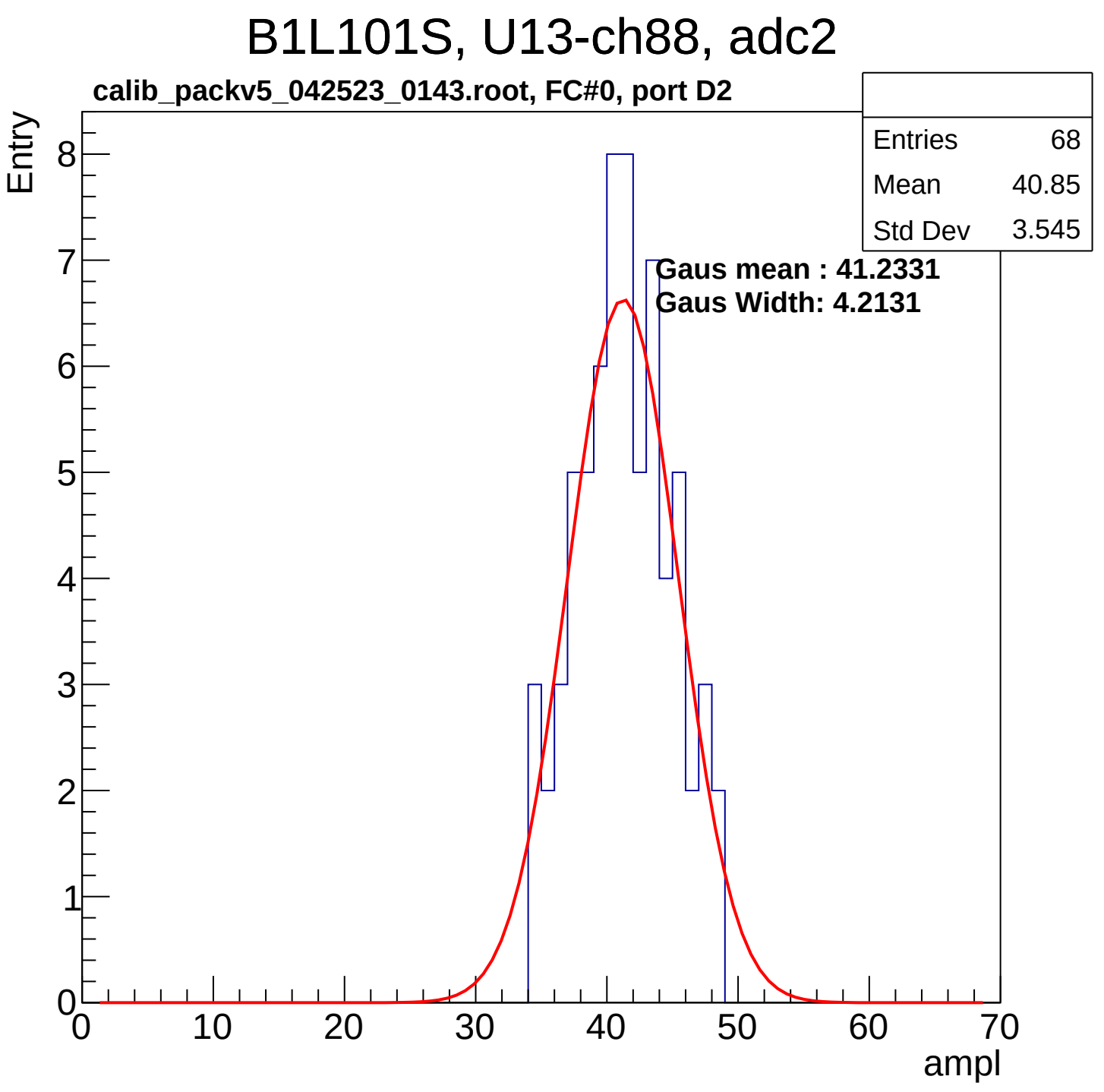
B1L101S, U13-ch88, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 40.85
Std Dev 3.545
Gaus mean : 41.2331
Gaus Width: 4.2131
Entry
ampl
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2
3
4
5
6
7
8
0
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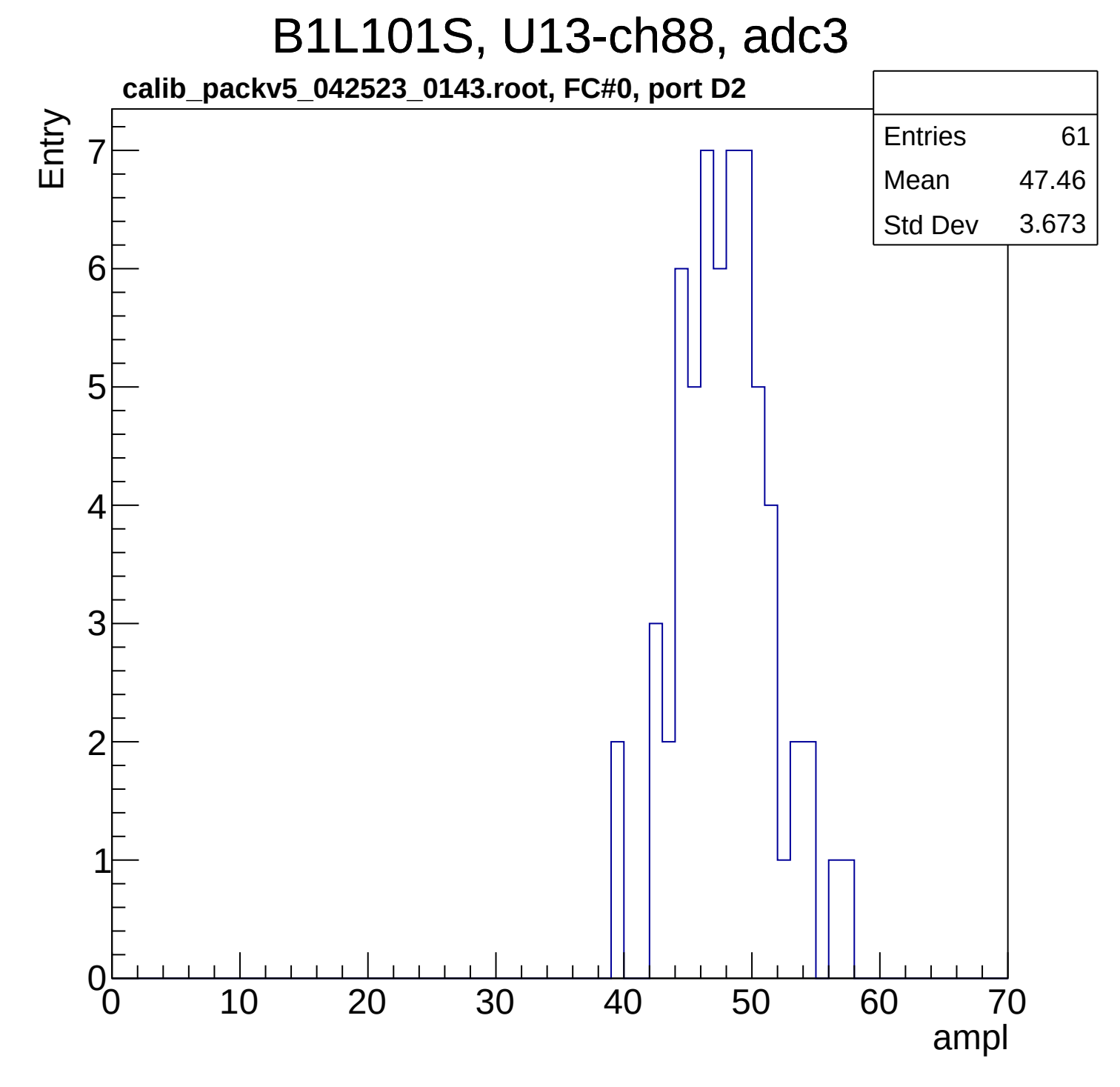
B1L101S, U13-ch88, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 47.46
Std Dev 3.673
Entry
ampl
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3
4
5
6
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0
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60
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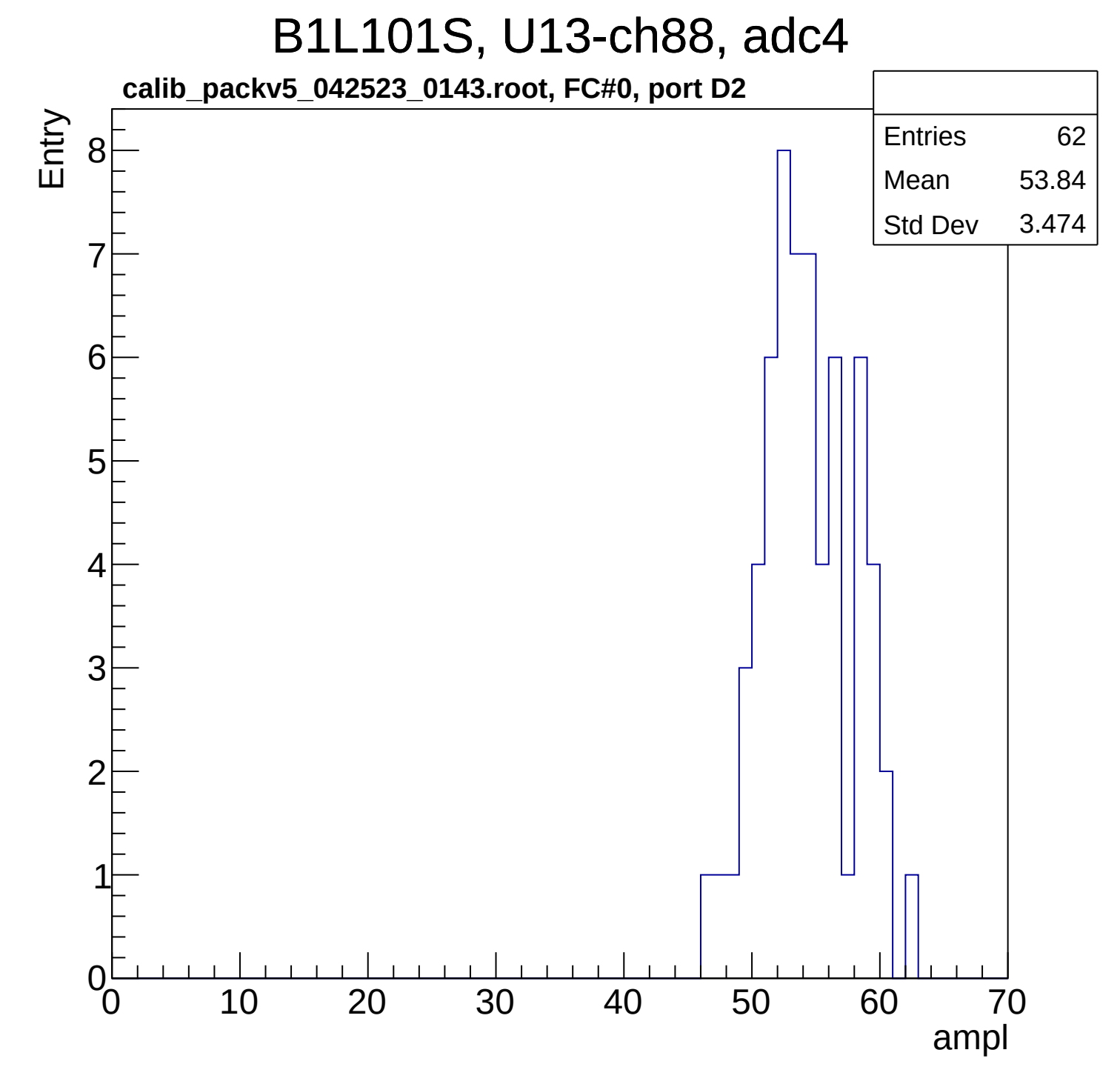
B1L101S, U13-ch88, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 53.84
Std Dev 3.474
Entry
ampl
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4
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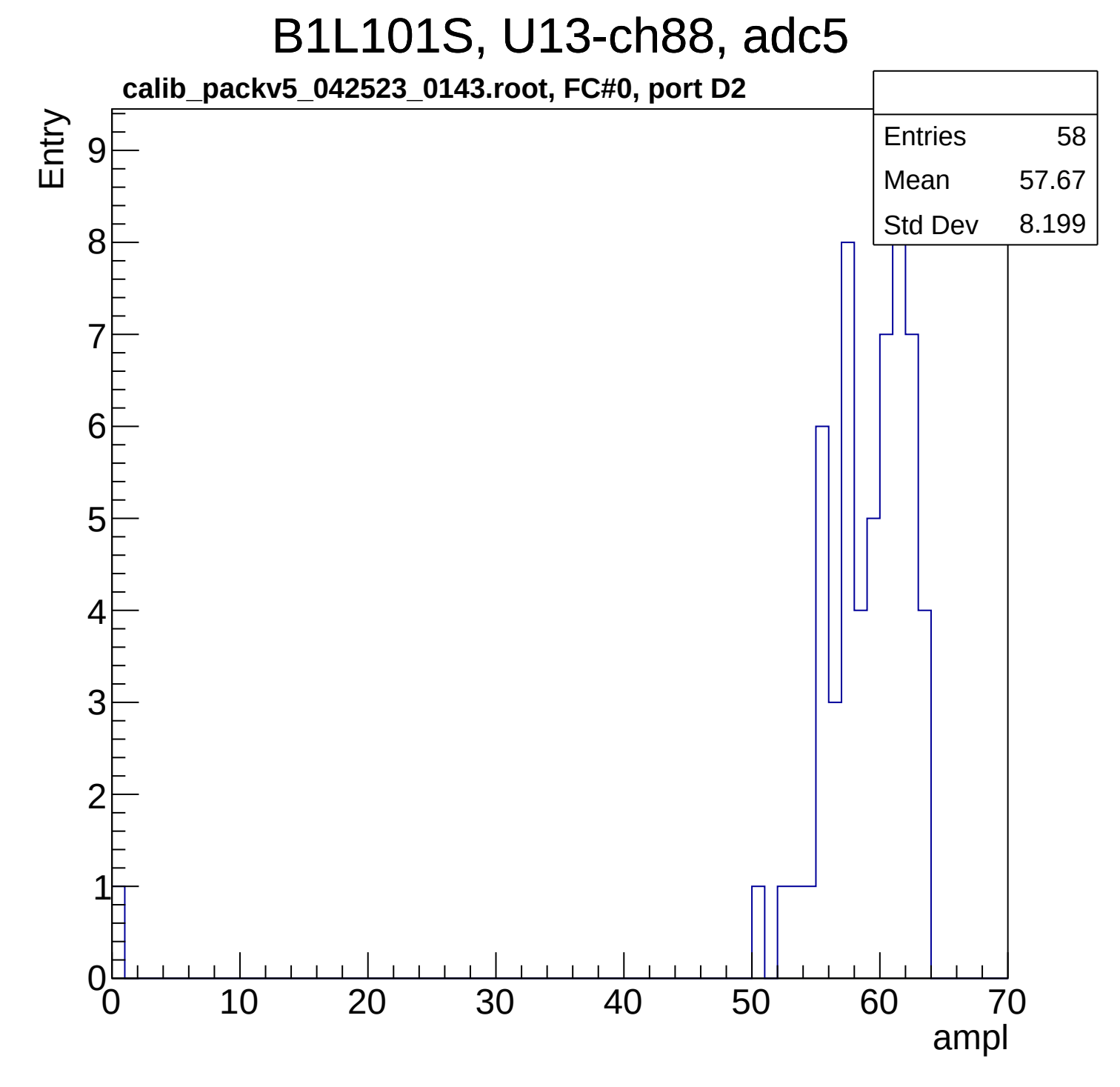
B1L101S, U13-ch88, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 57.67
Std Dev 8.199
Entry
ampl
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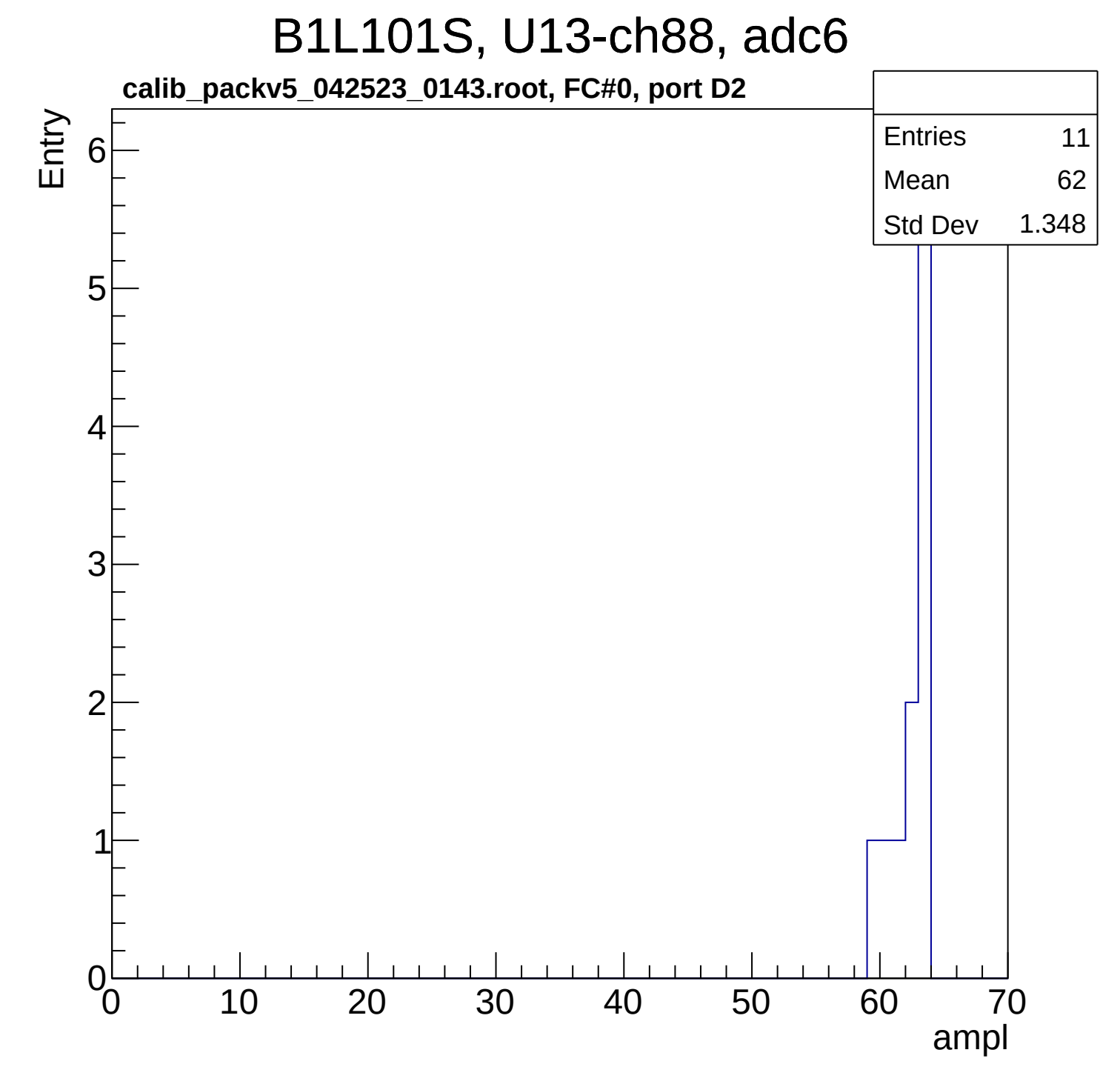
B1L101S, U13-ch88, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 11
Mean 62
Std Dev 1.348
Entry
ampl
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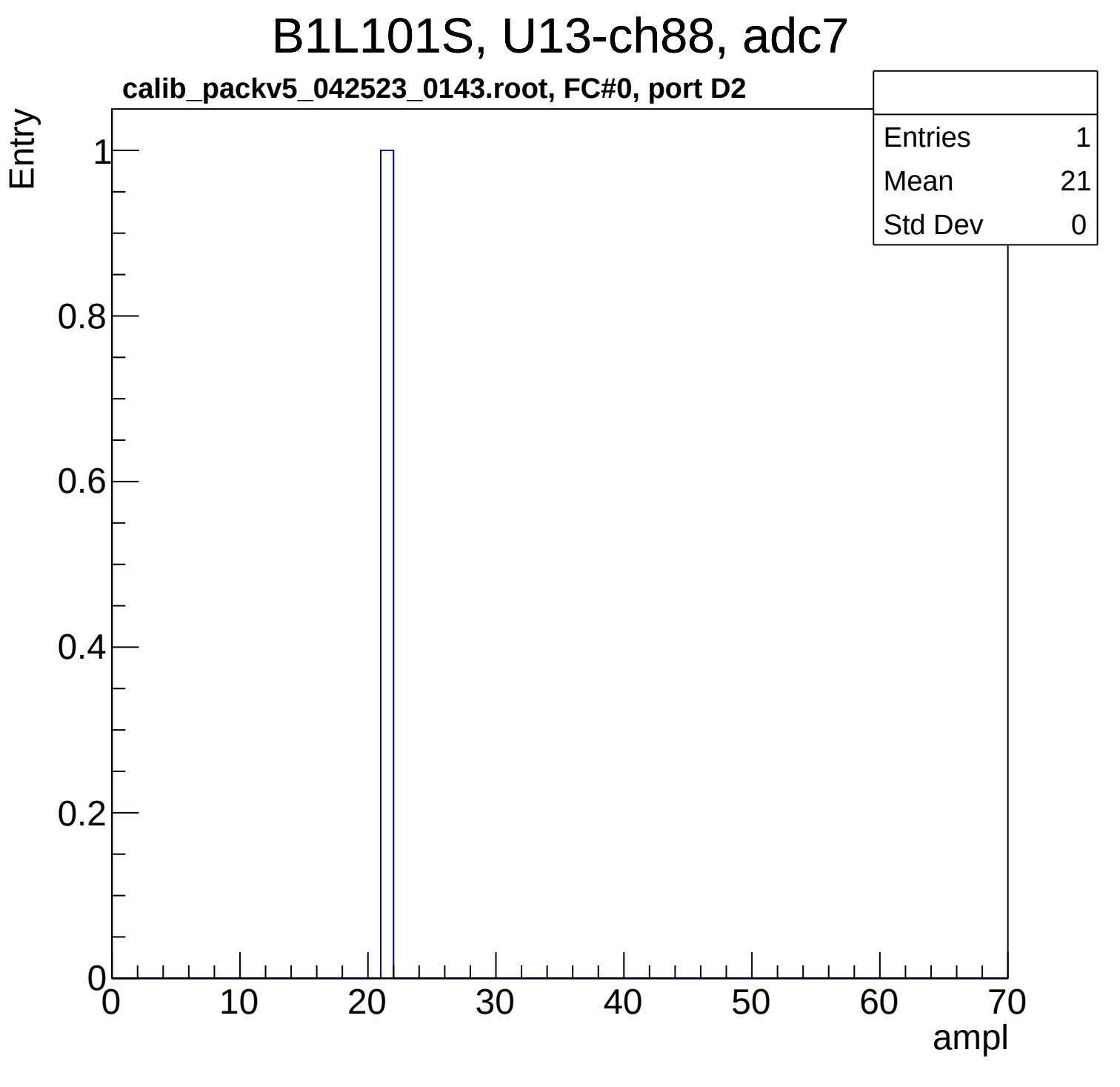
B1L101S, U13-ch88, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 21
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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50
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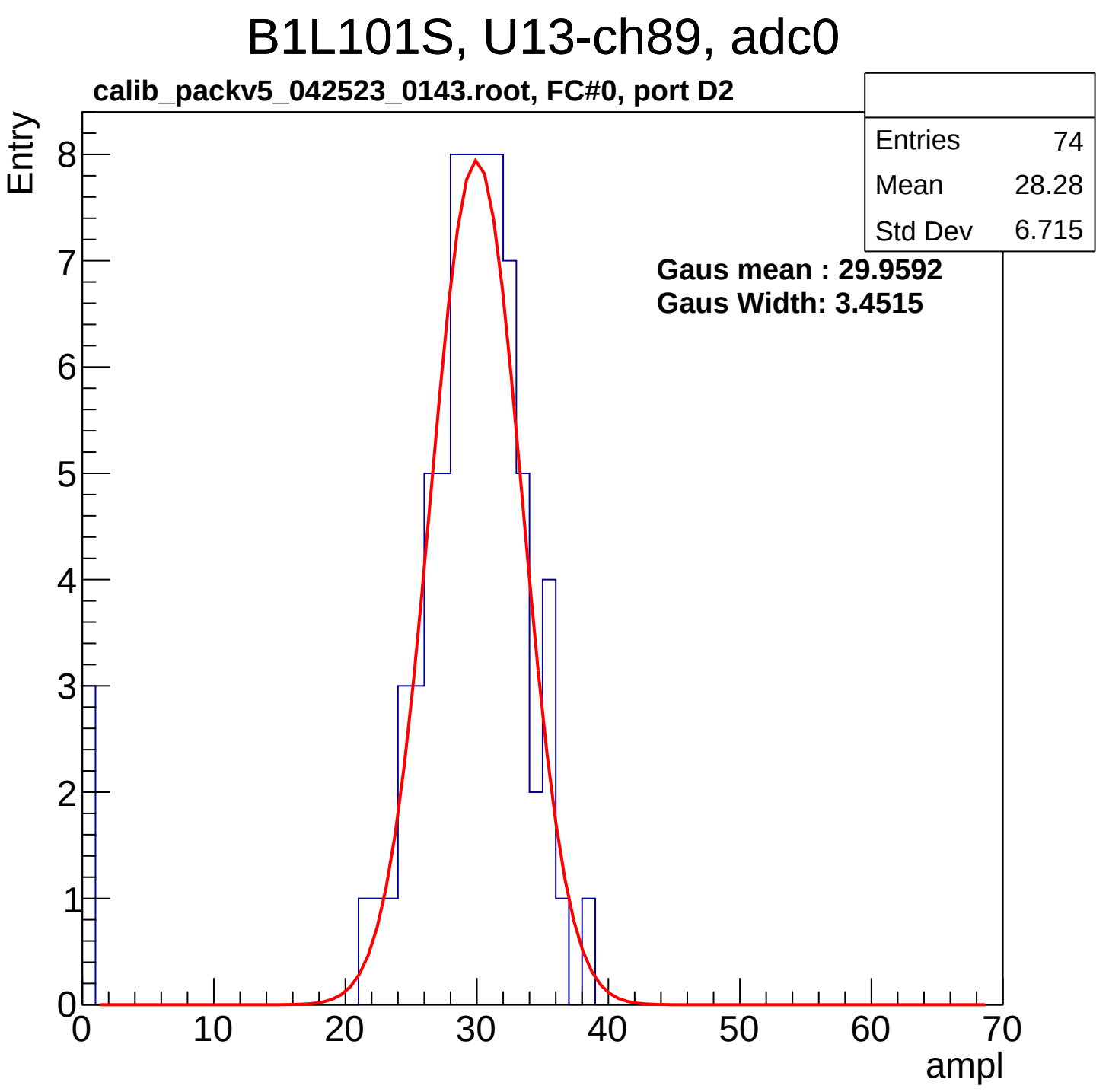
70
ampl


B1L101S, U13-ch89, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 28.28
Std Dev 6.715
Gaus mean : 29.9592
Gaus Width: 3.4515
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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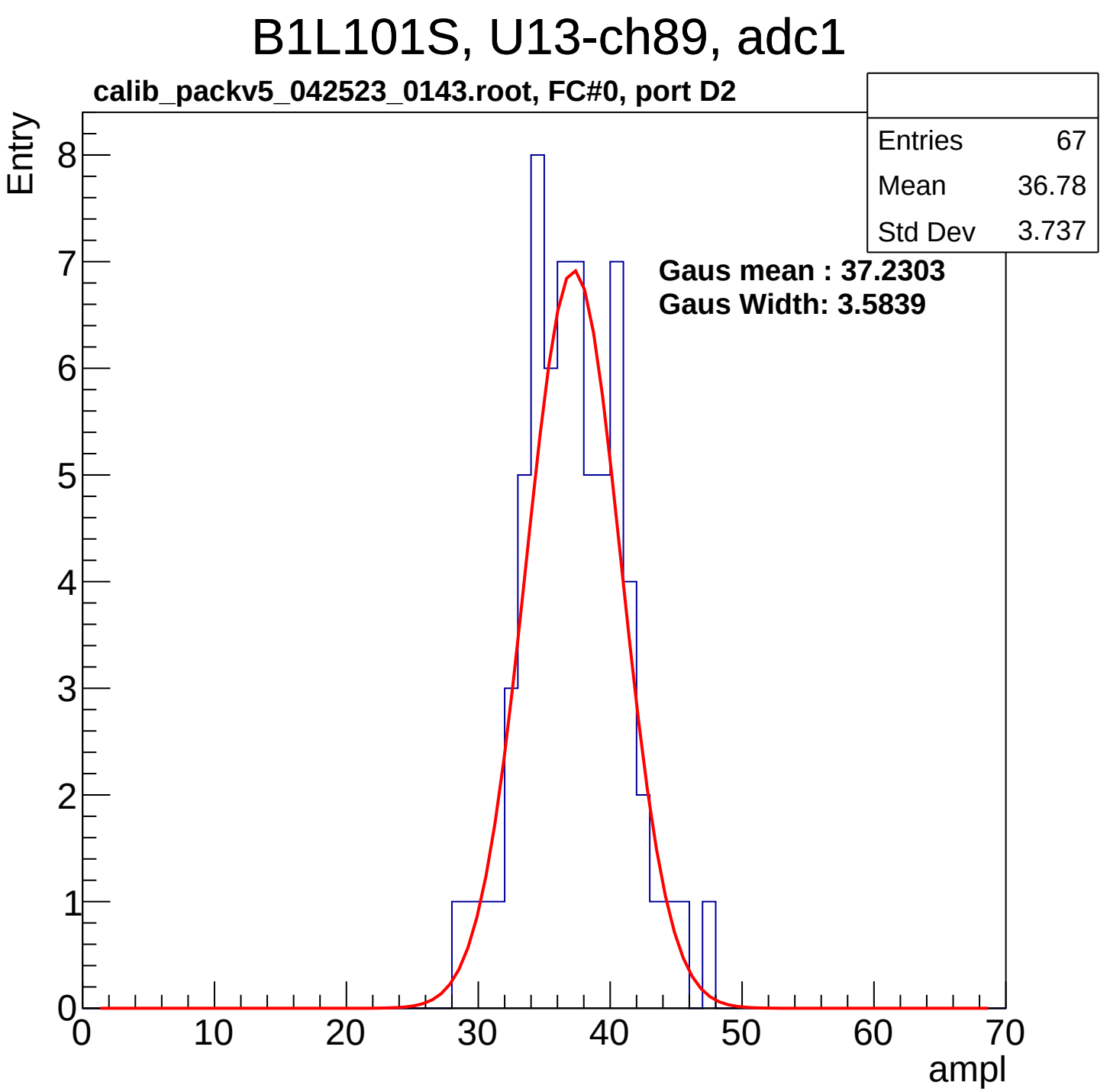
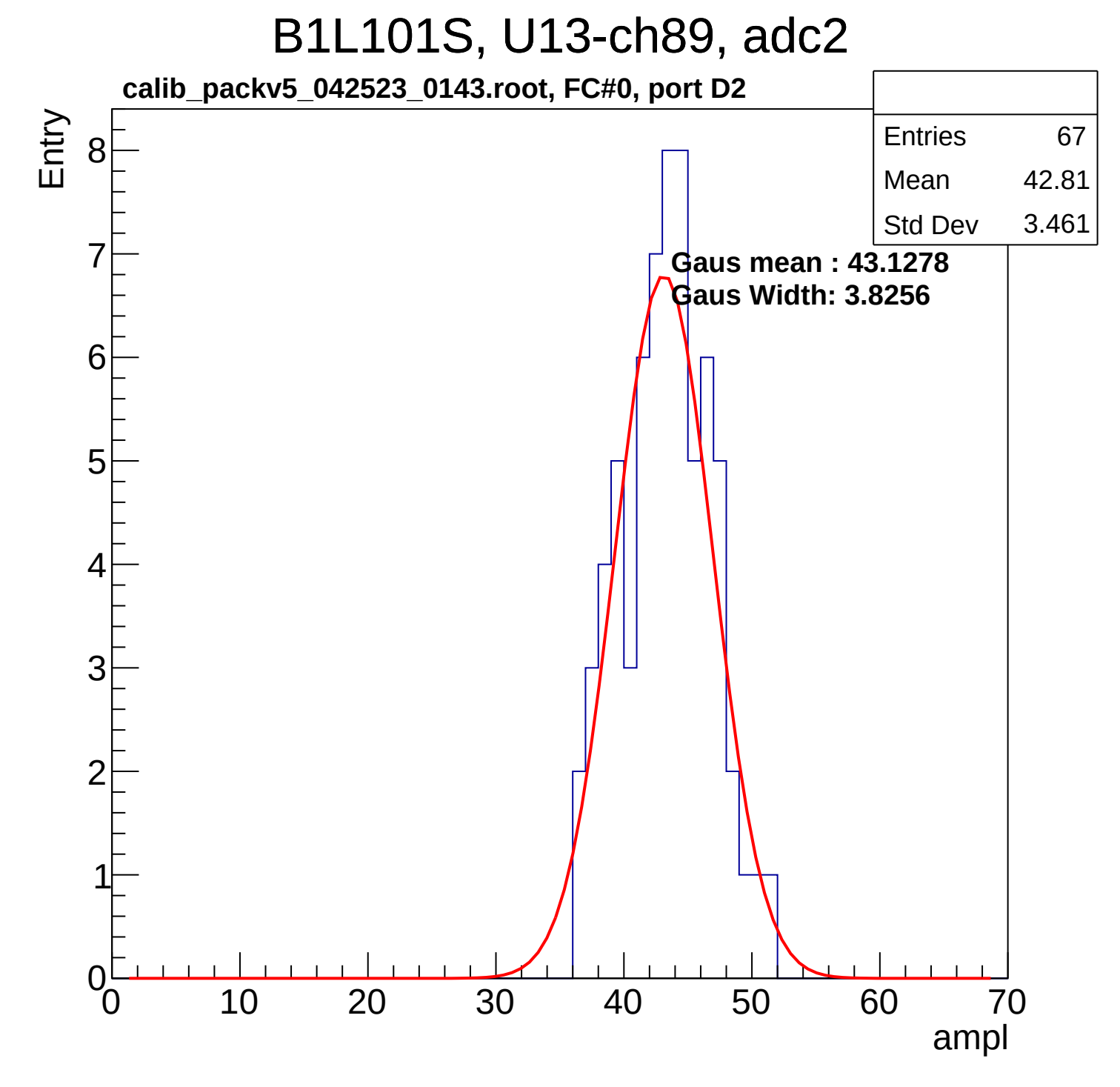

B1L101S, U13-ch89, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 36.78
Std Dev 3.737
Gaus mean : 37.2303
Gaus Width: 3.5839
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch89, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 42.81
Std Dev 3.461
Gaus mean : 43.1278
Gaus Width: 3.8256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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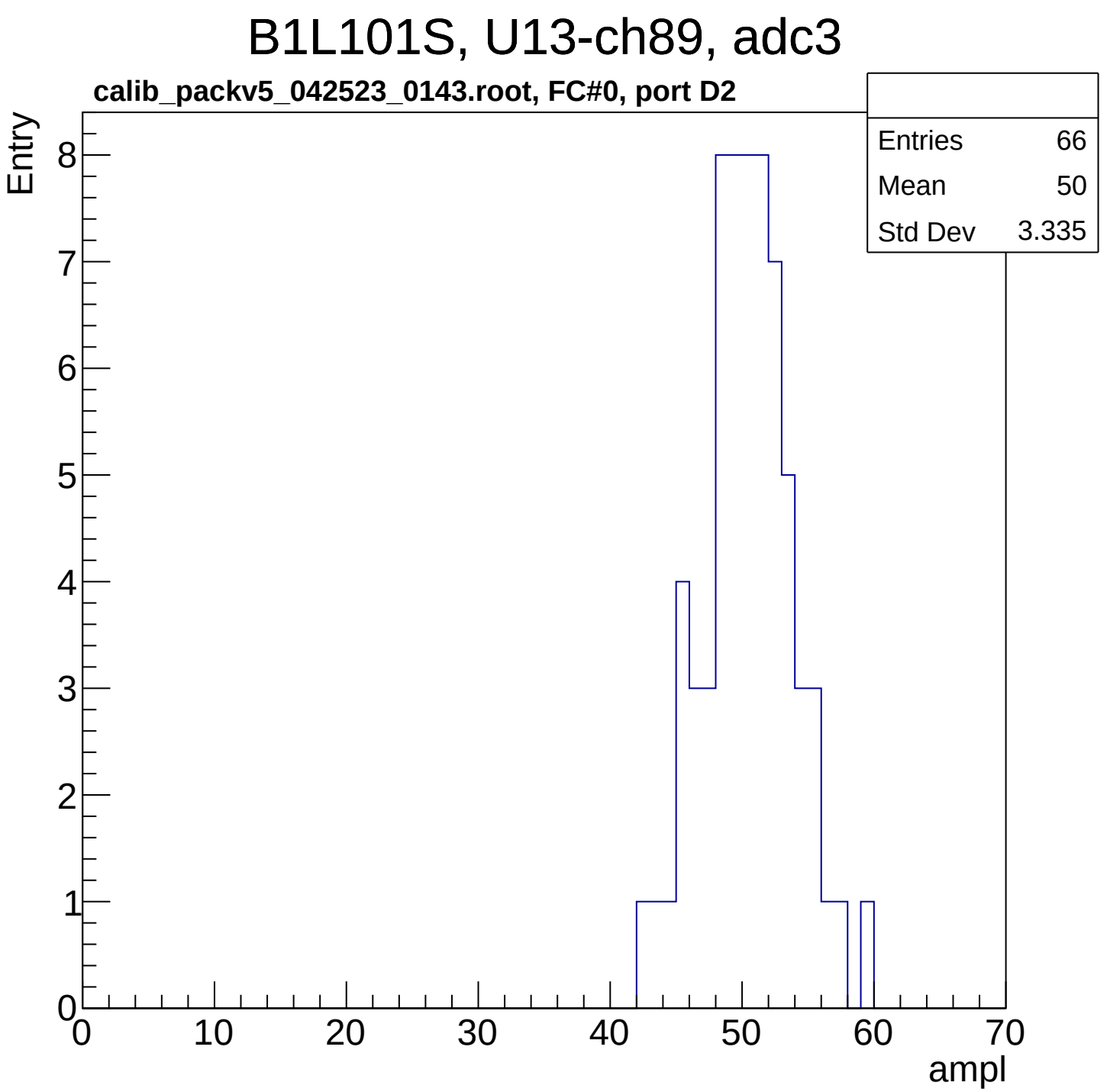
B1L101S, U13-ch89, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 50
Std Dev 3.335
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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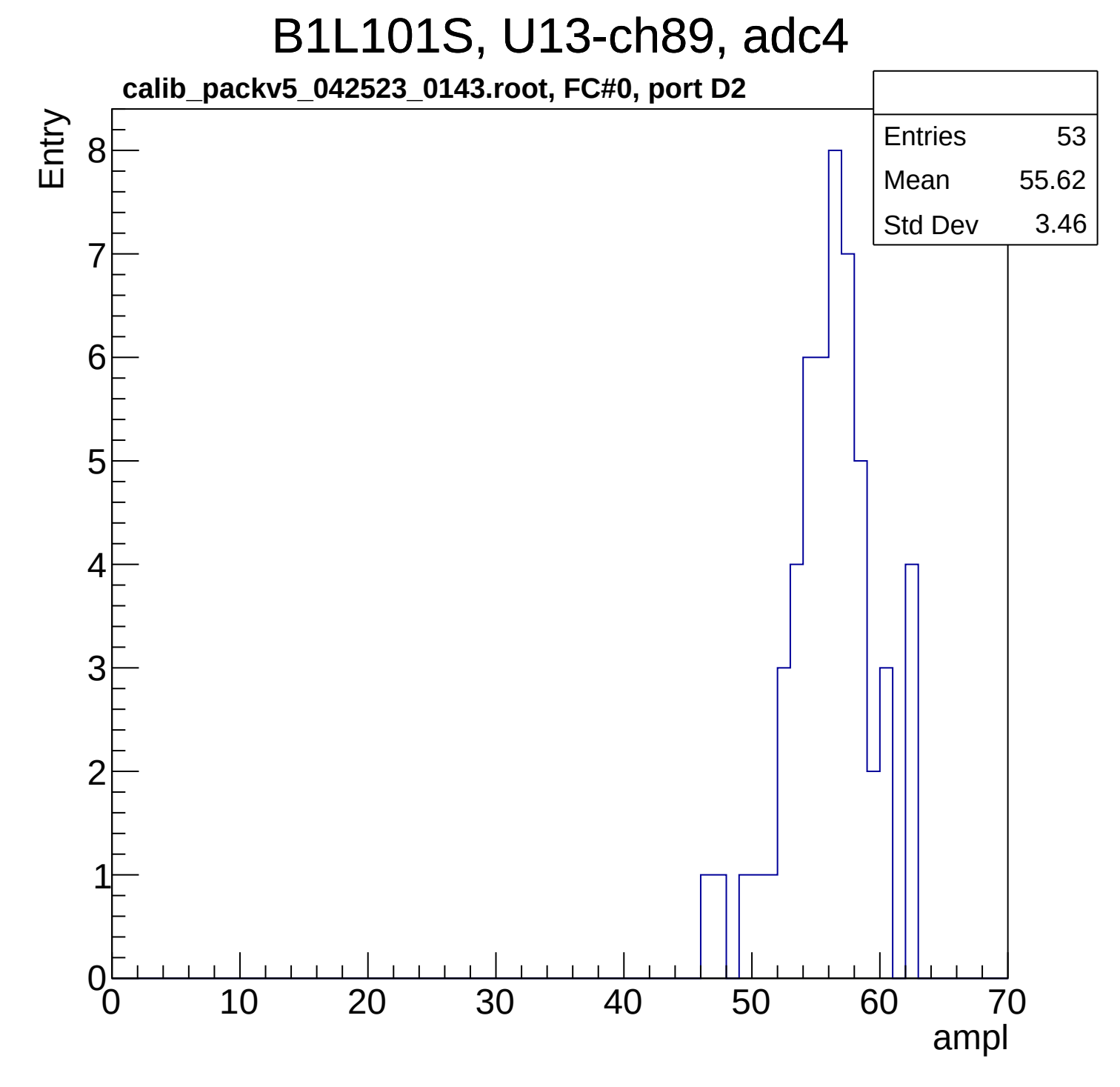
B1L101S, U13-ch89, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 55.62
Std Dev 3.46
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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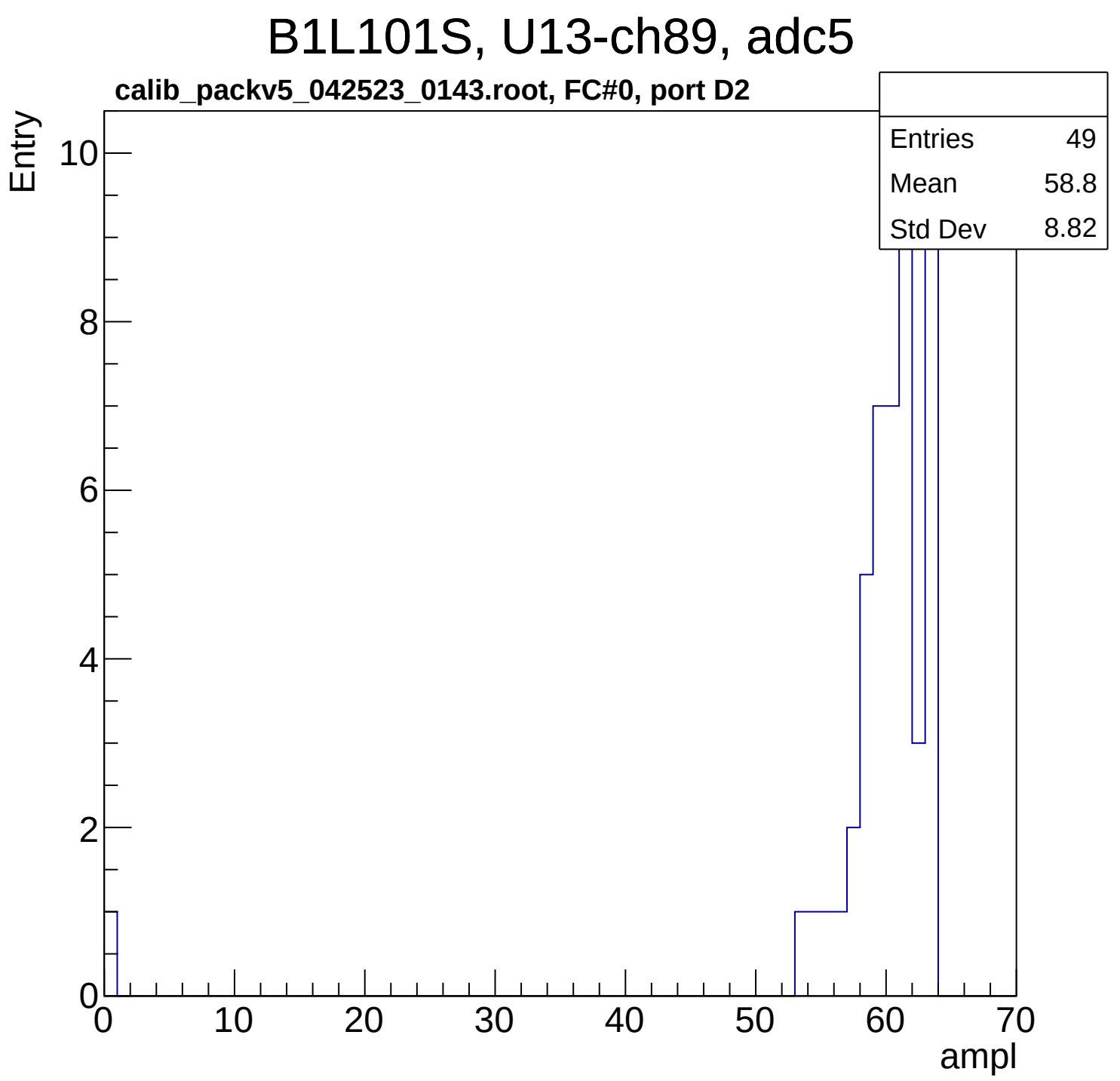
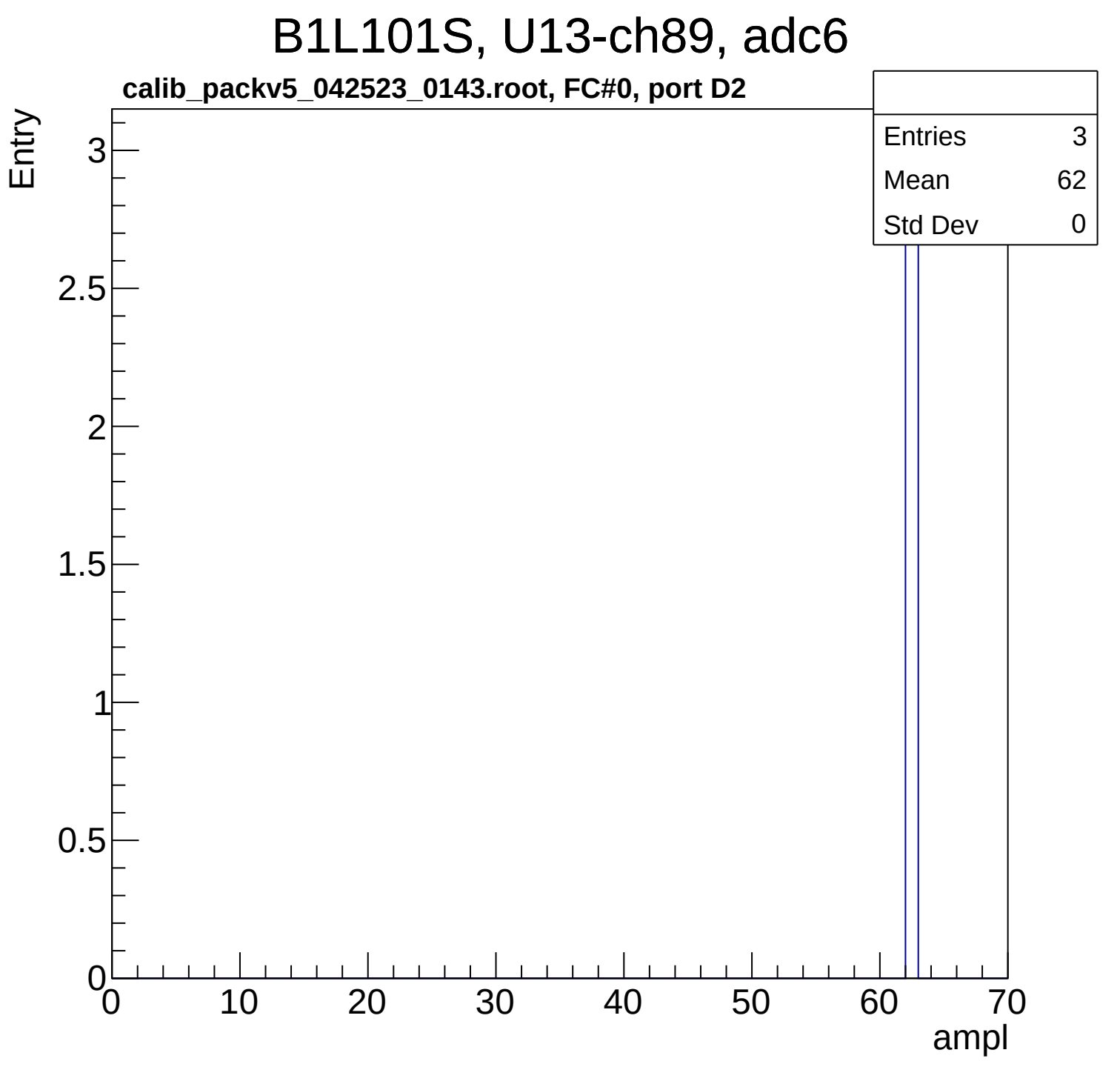
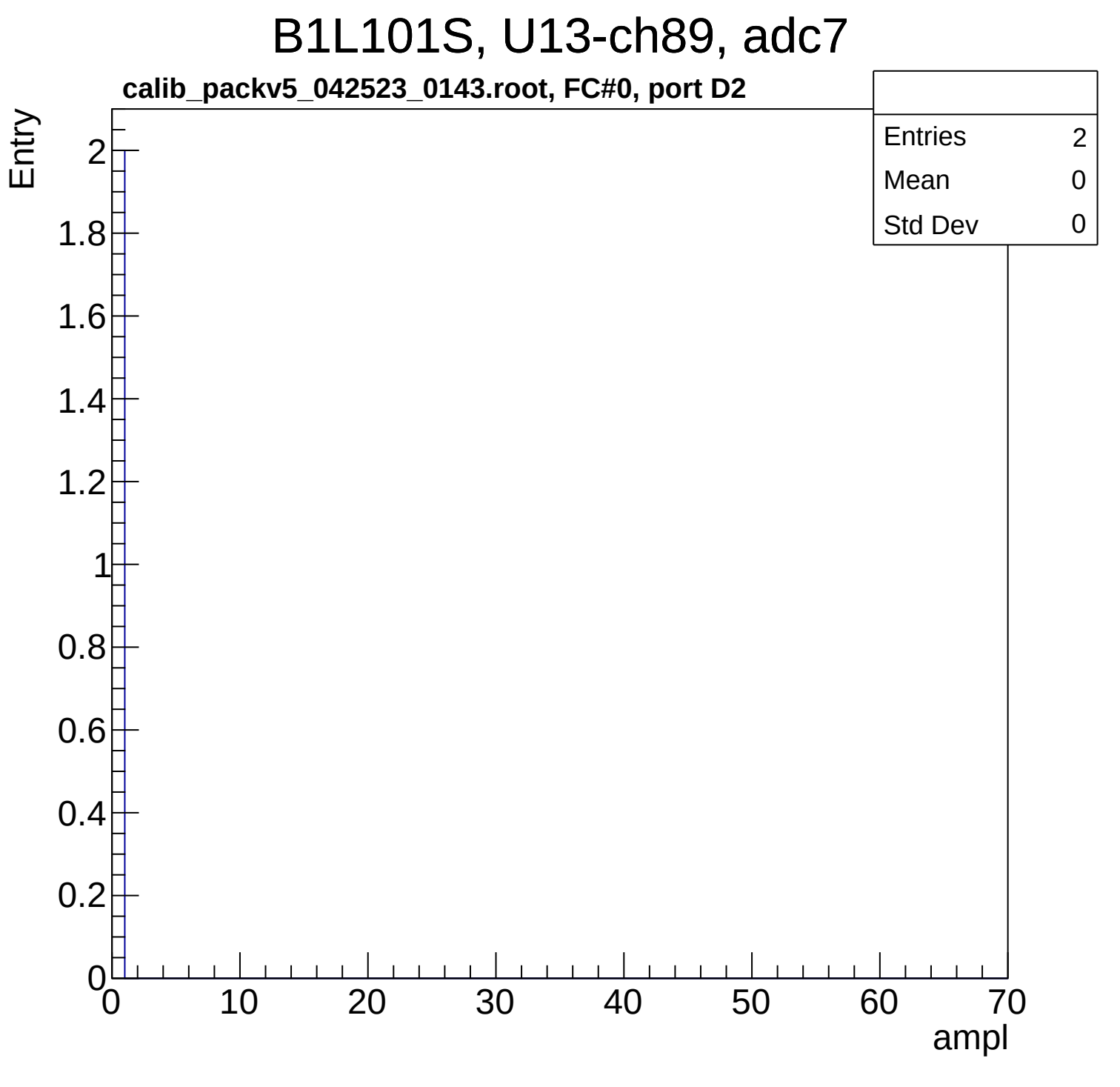
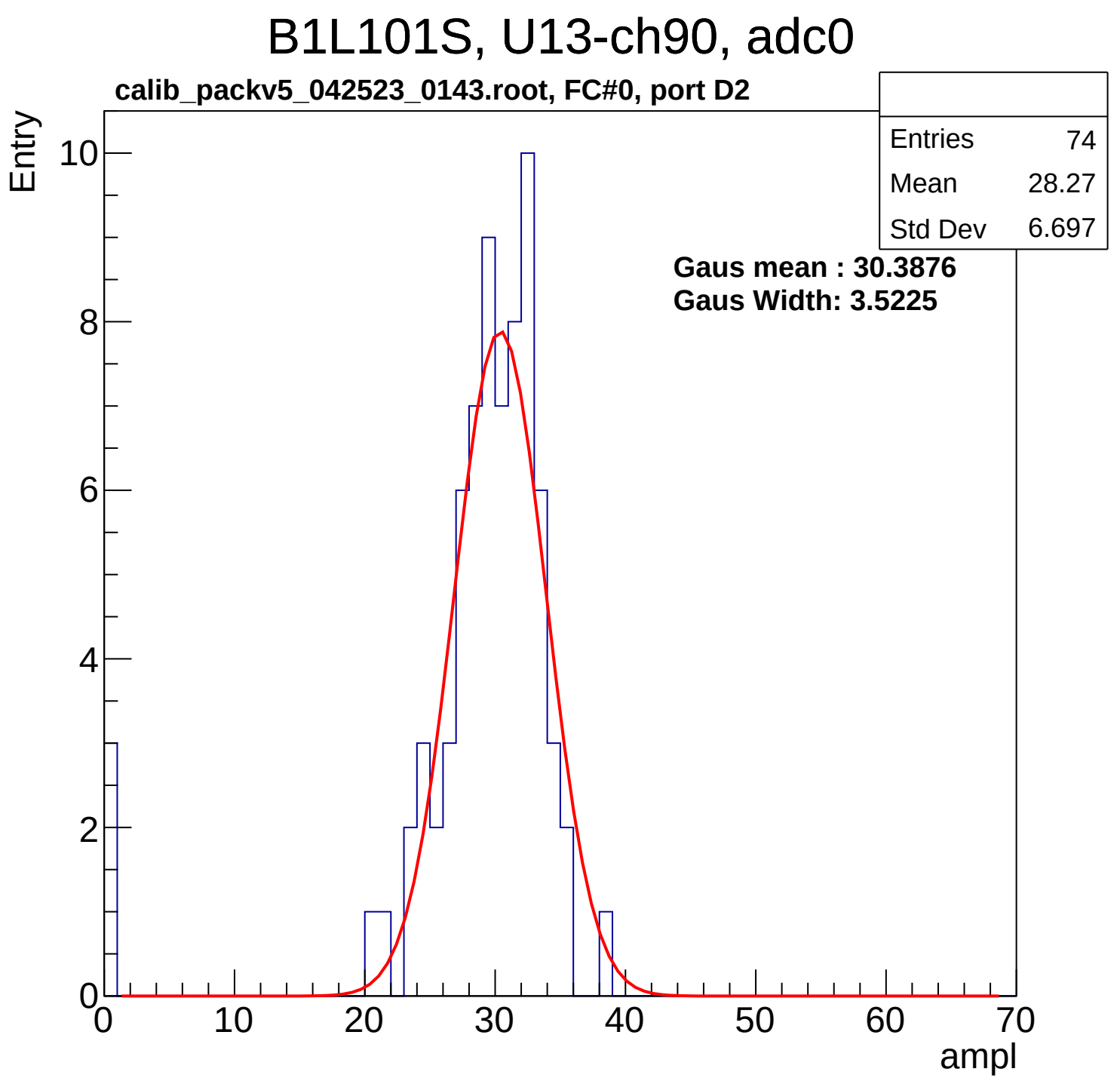
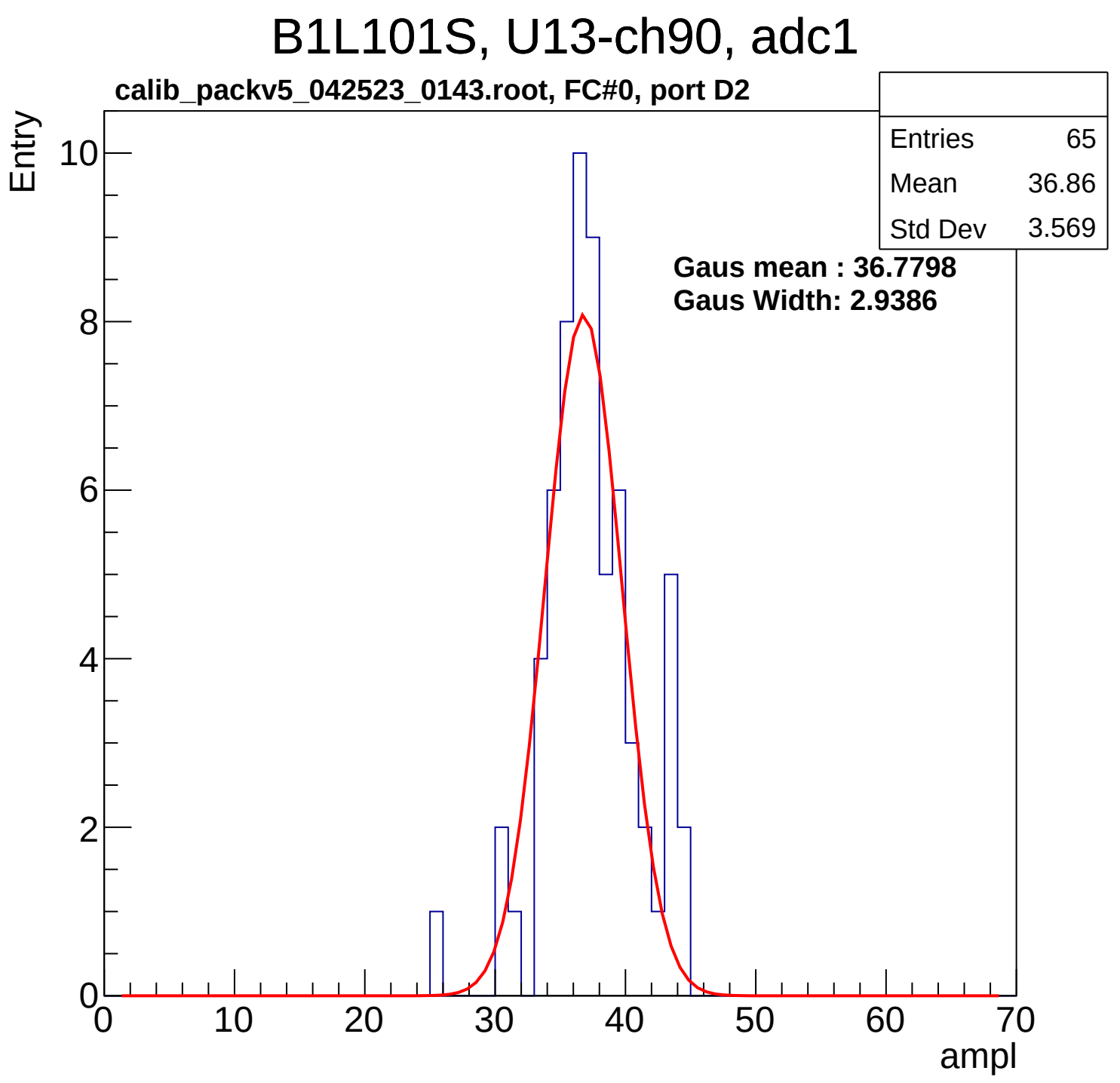
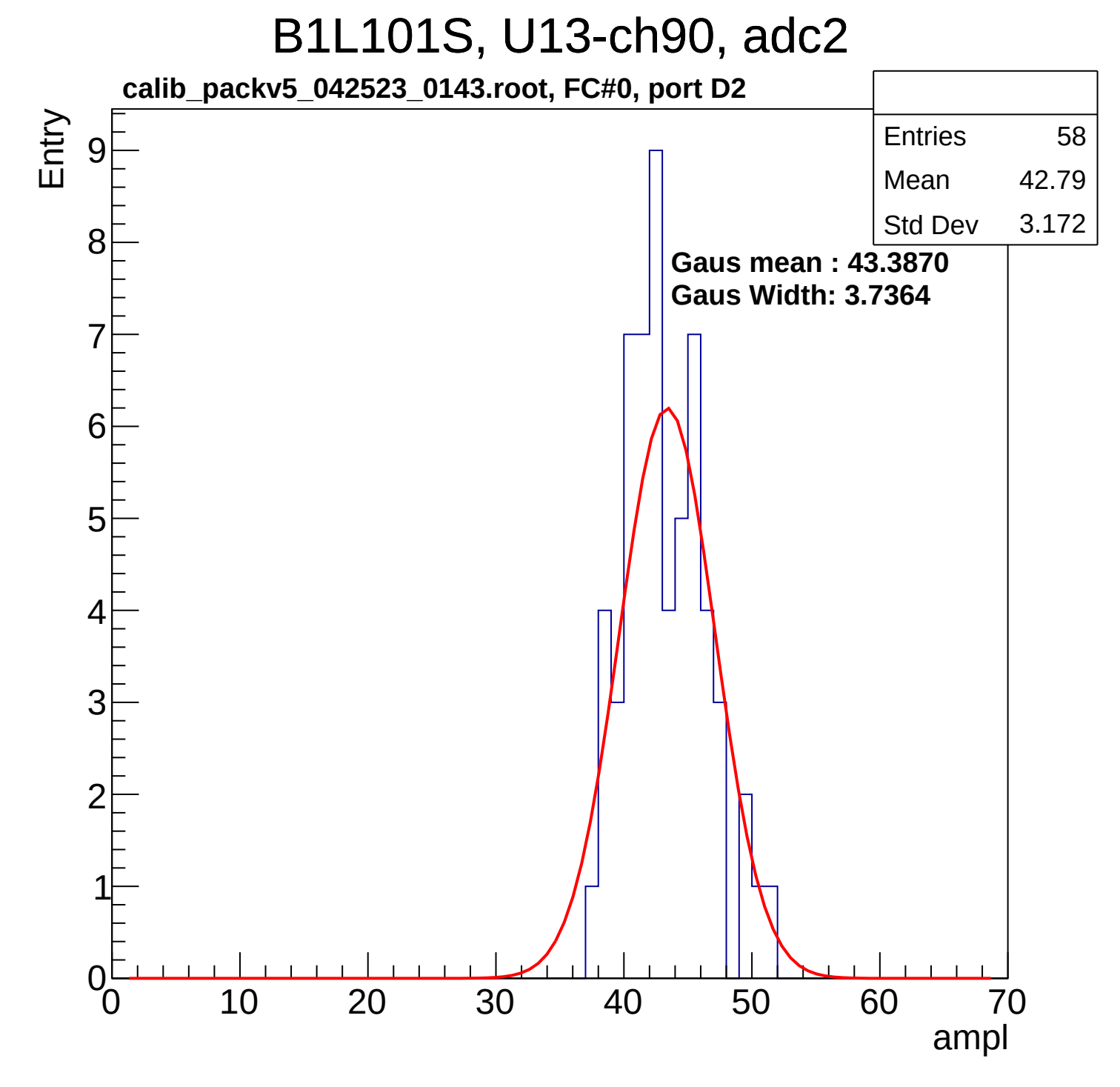

B1L101S, U13-ch89, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 58.8
Std Dev 8.82
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

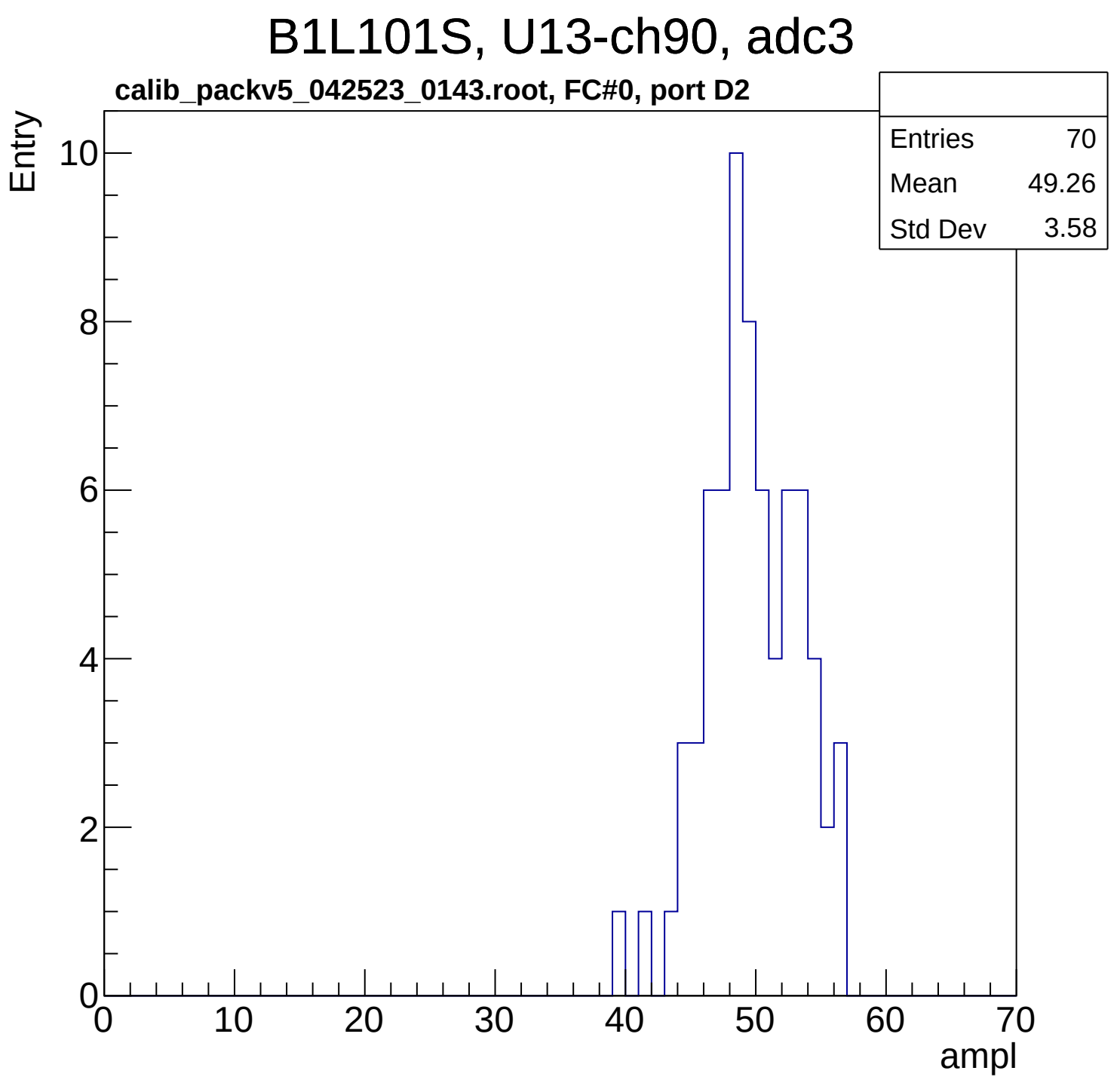

B1L101S, U13-ch89, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch89, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch90, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 28.27
Std Dev 6.697
Gaus mean : 30.3876
Gaus Width: 3.5225
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch90, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 36.86
Std Dev 3.569
Gaus mean : 36.7798
Gaus Width: 2.9386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch90, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 42.79
Std Dev 3.172
Gaus mean : 43.3870
Gaus Width: 3.7364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch90, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 49.26
Std Dev 3.58
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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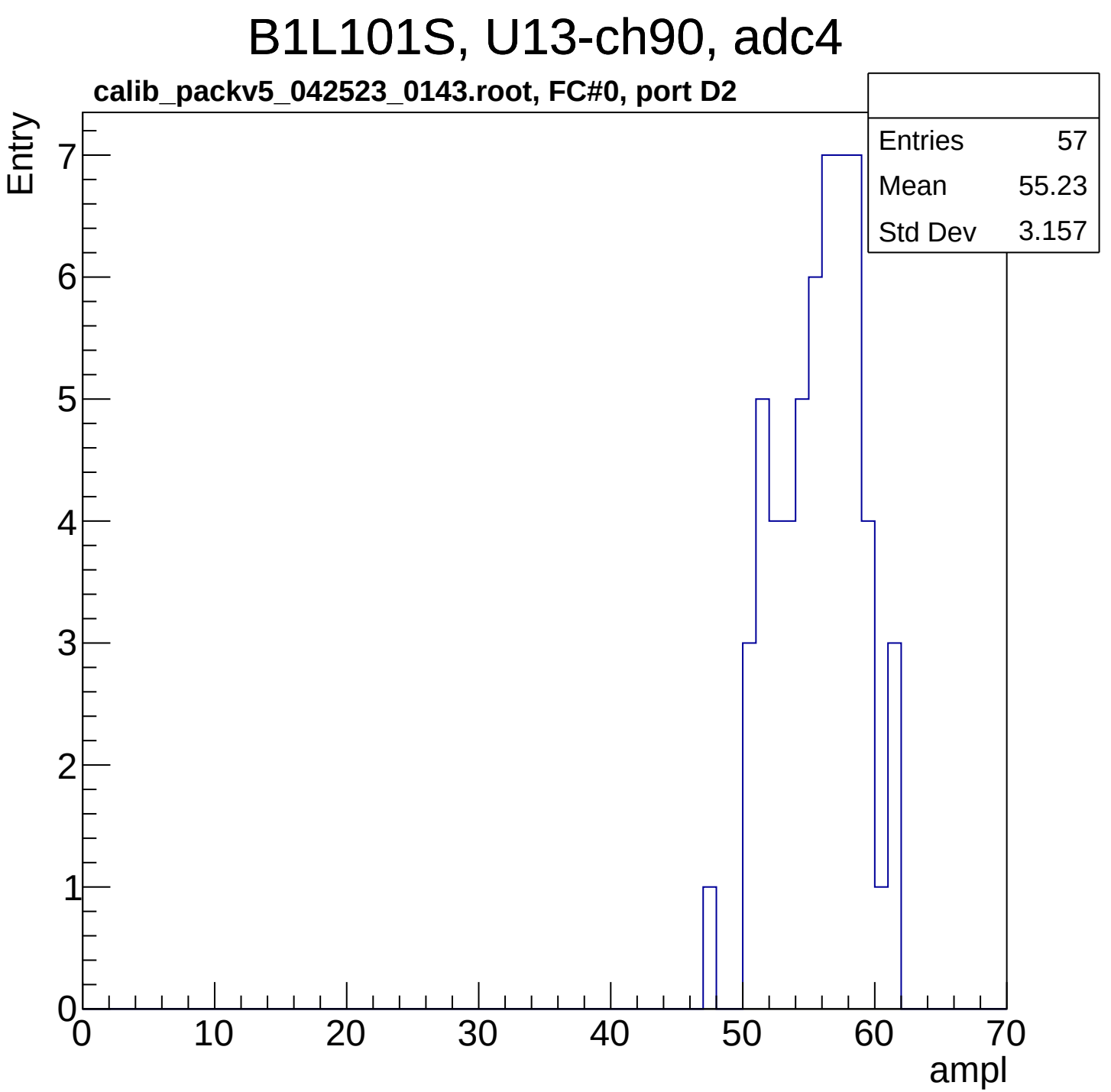
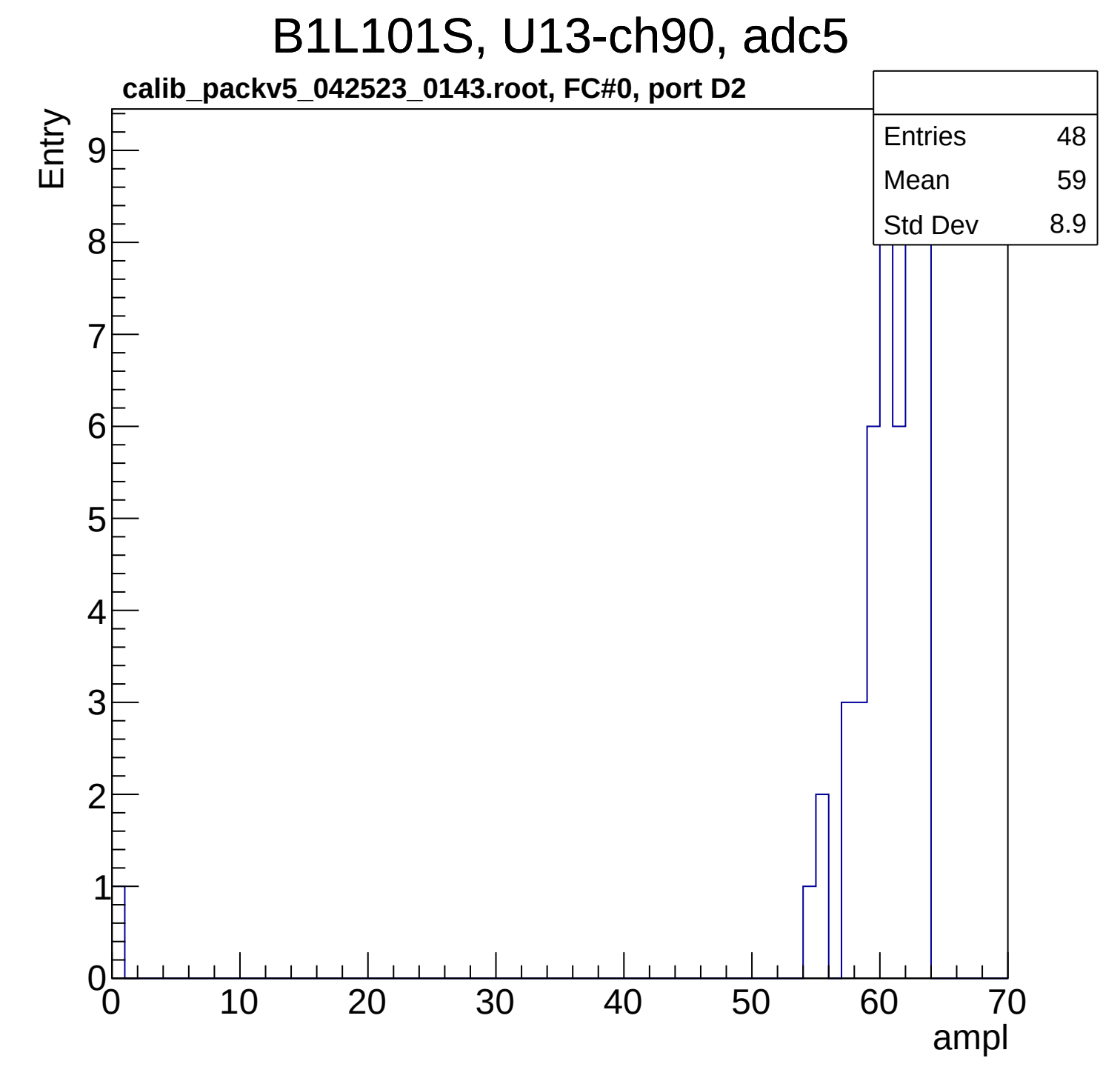

B1L101S, U13-ch90, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 55.23
Std Dev 3.157
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch90, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 59
Std Dev 8.9
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
50
60
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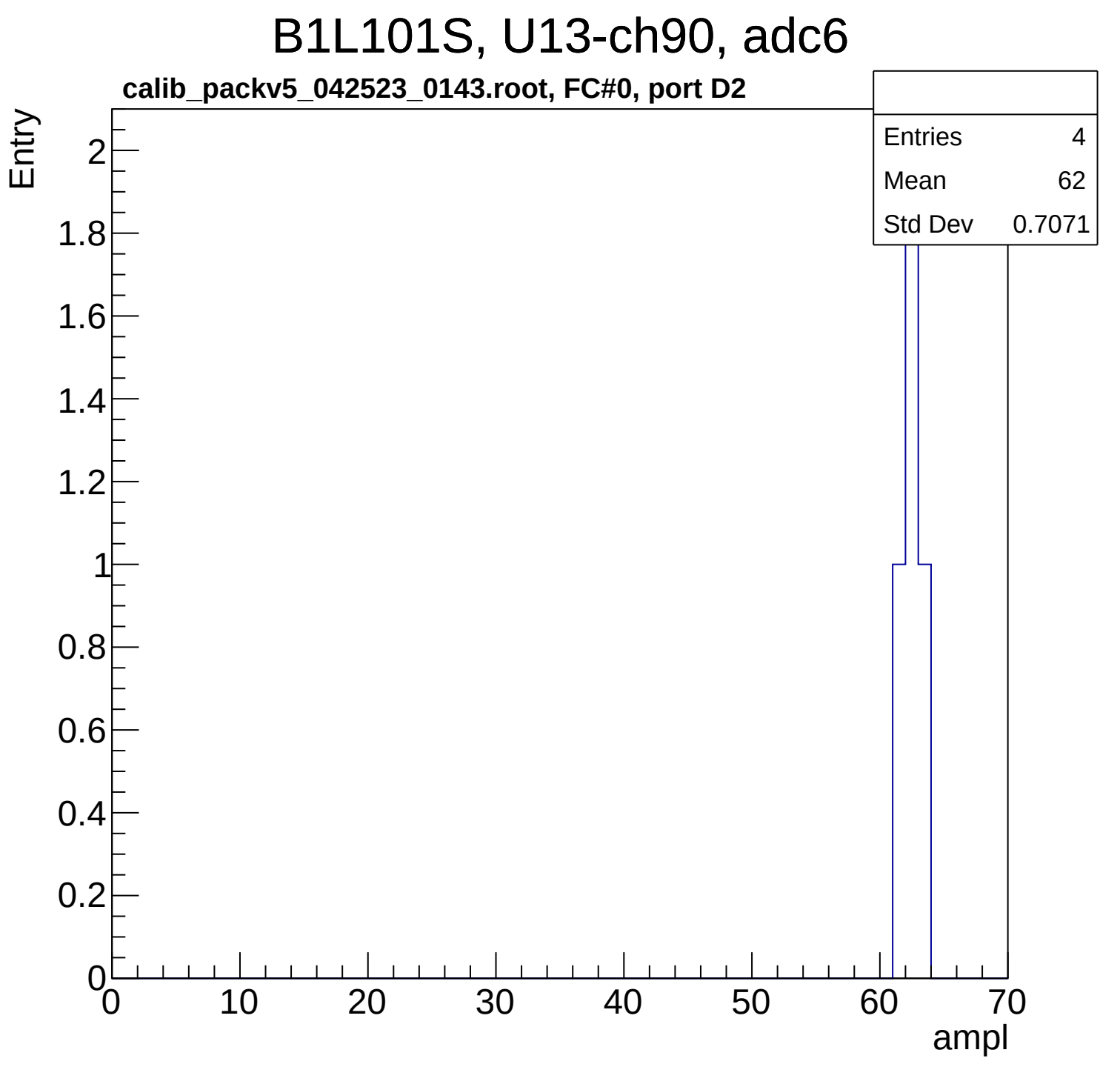
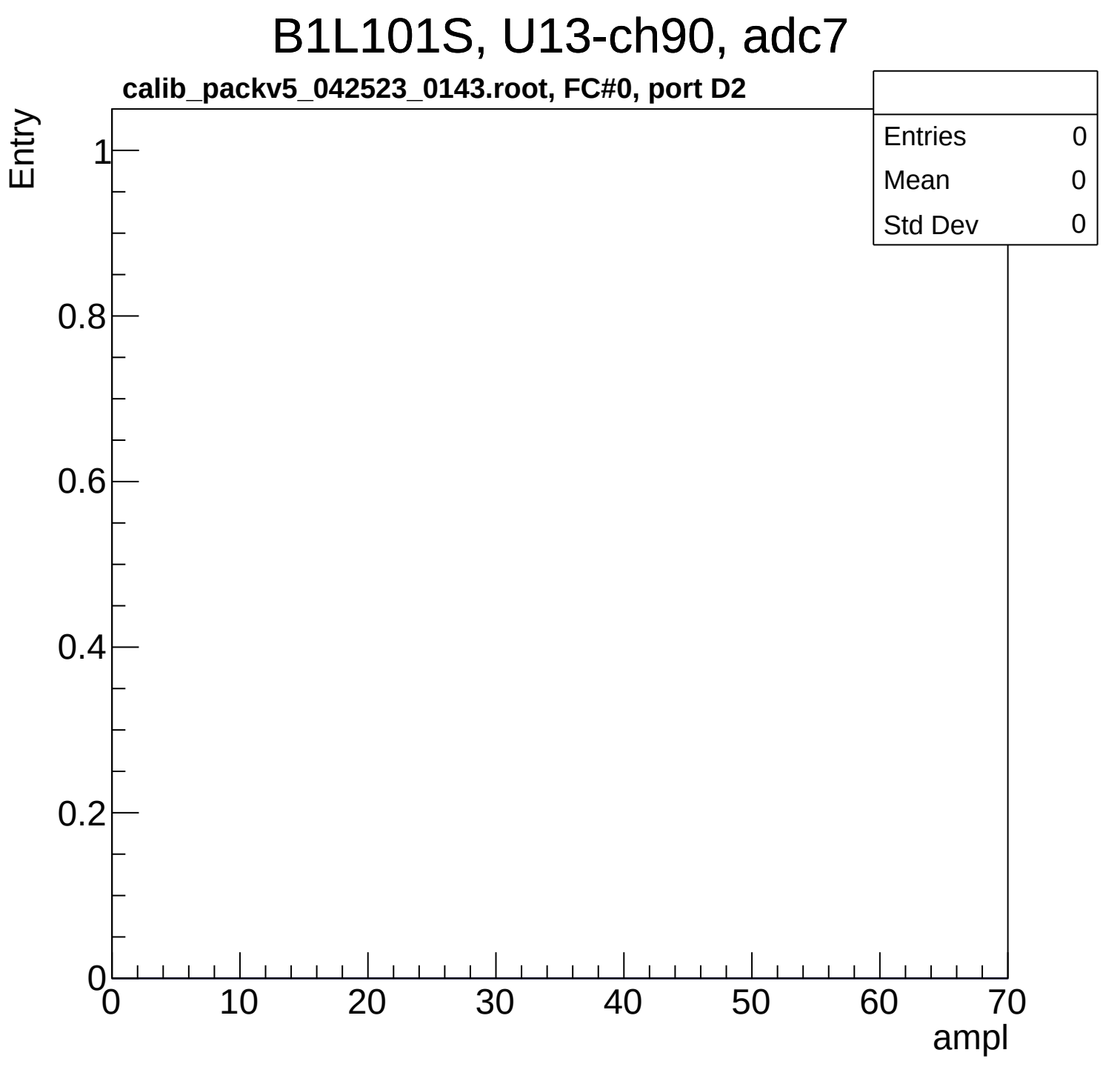

B1L101S, U13-ch90, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch90, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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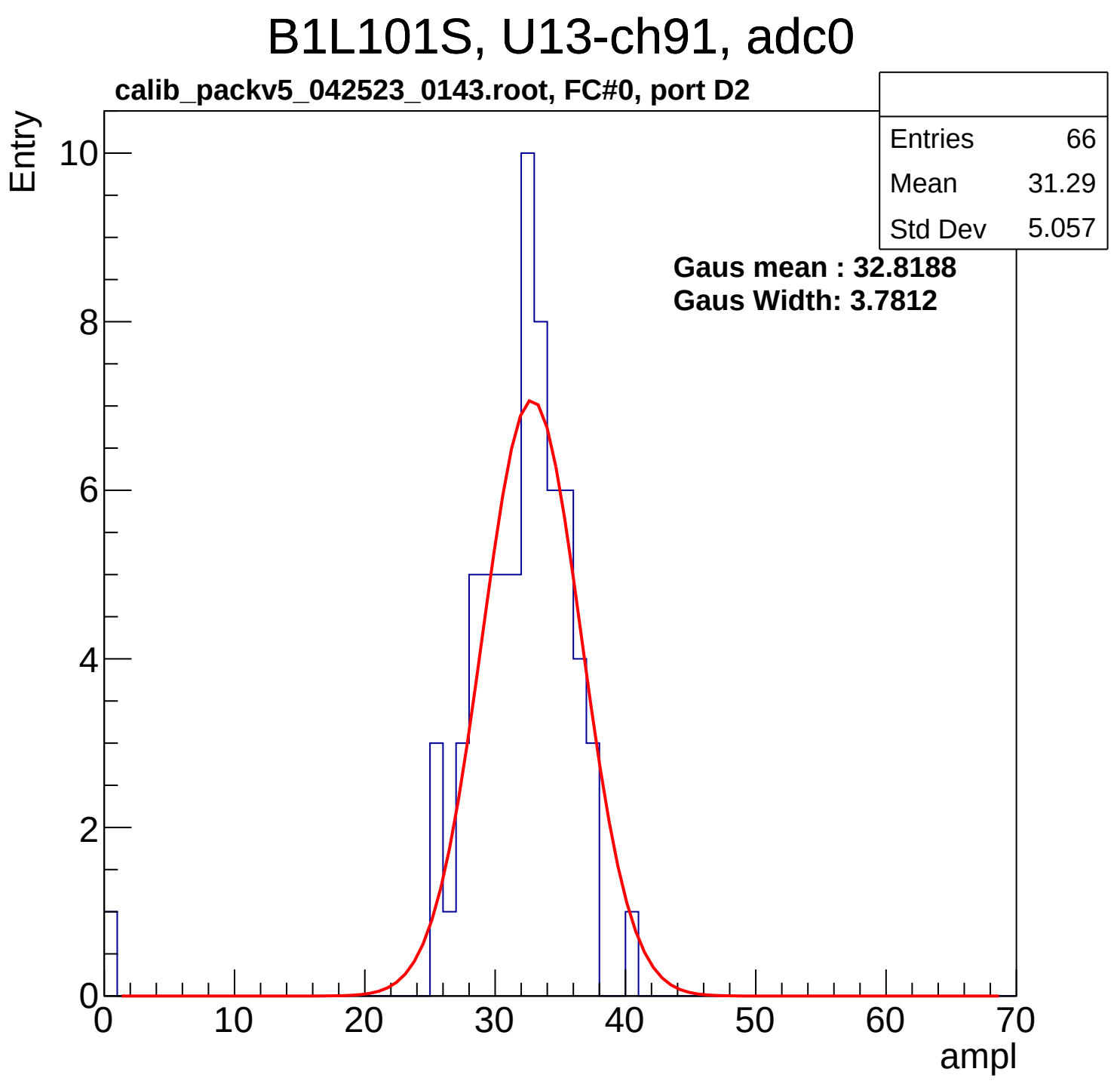
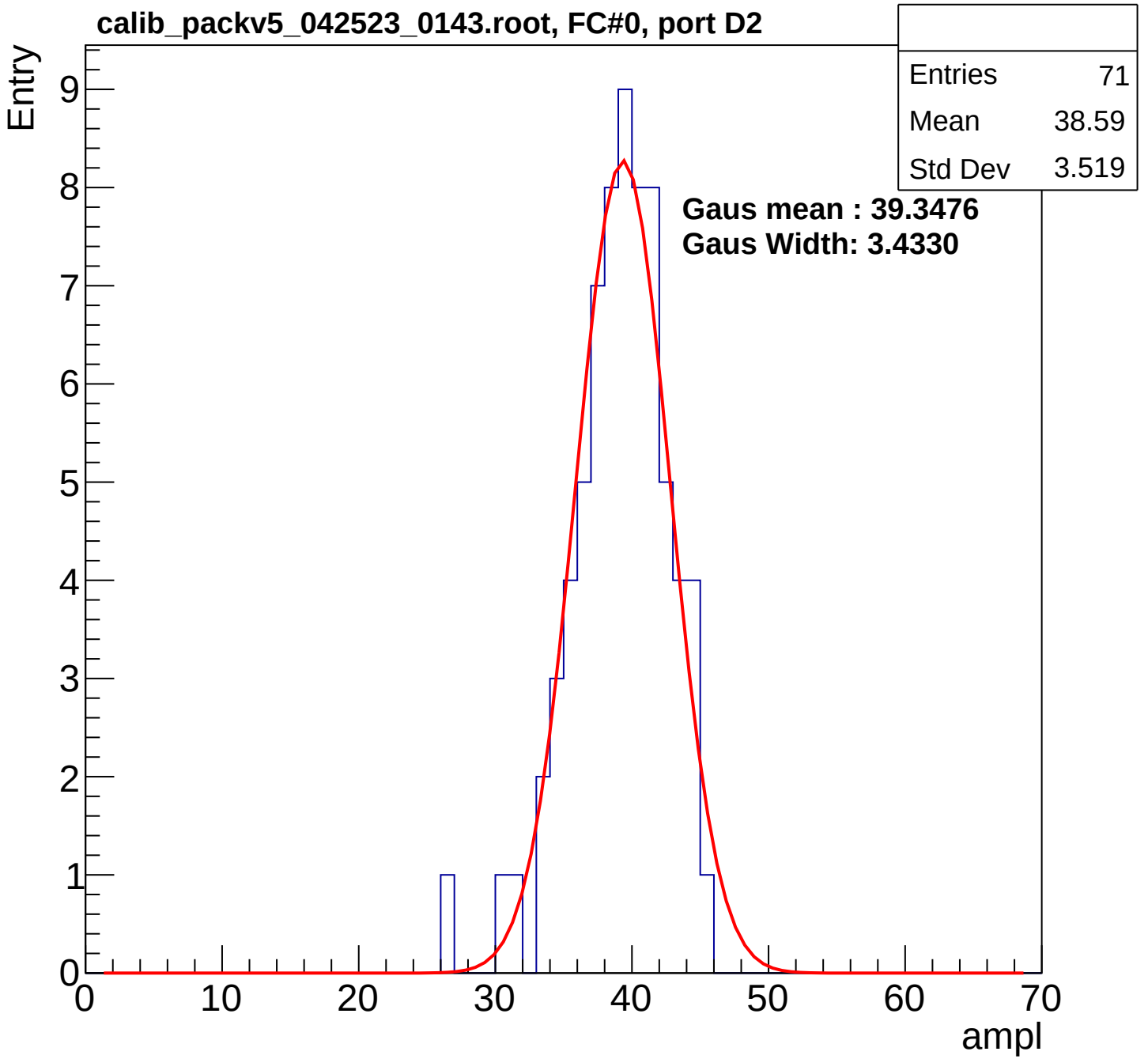
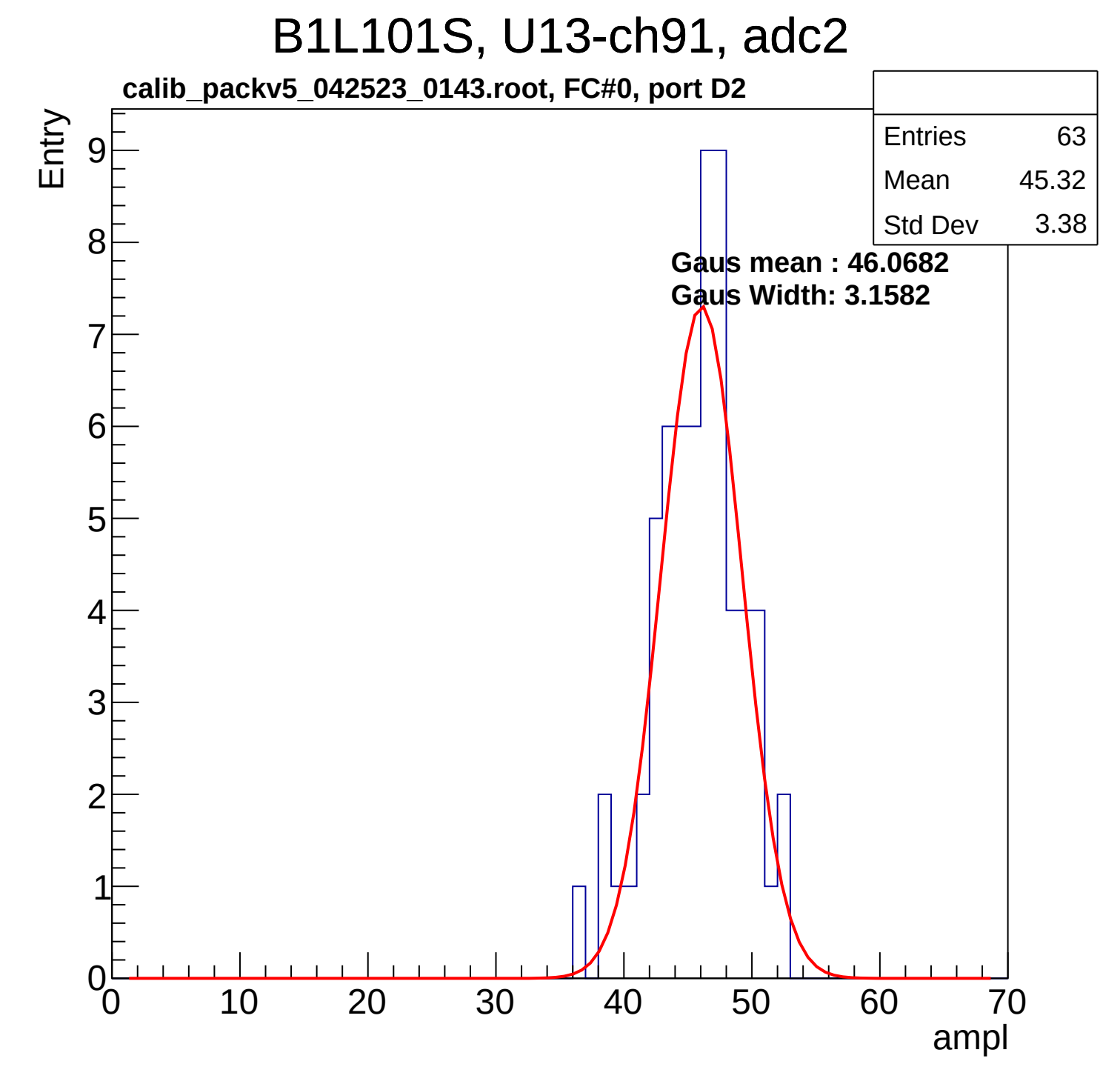
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch91, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 31.29
Std Dev 5.057
Gaus mean : 32.8188
Gaus Width: 3.7812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch91, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 38.59
Std Dev 3.519
Gaus mean : 39.3476
Gaus Width: 3.4330
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch91, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 45.32
Std Dev 3.38
Gaus mean : 46.0682
Gaus Width: 3.1582
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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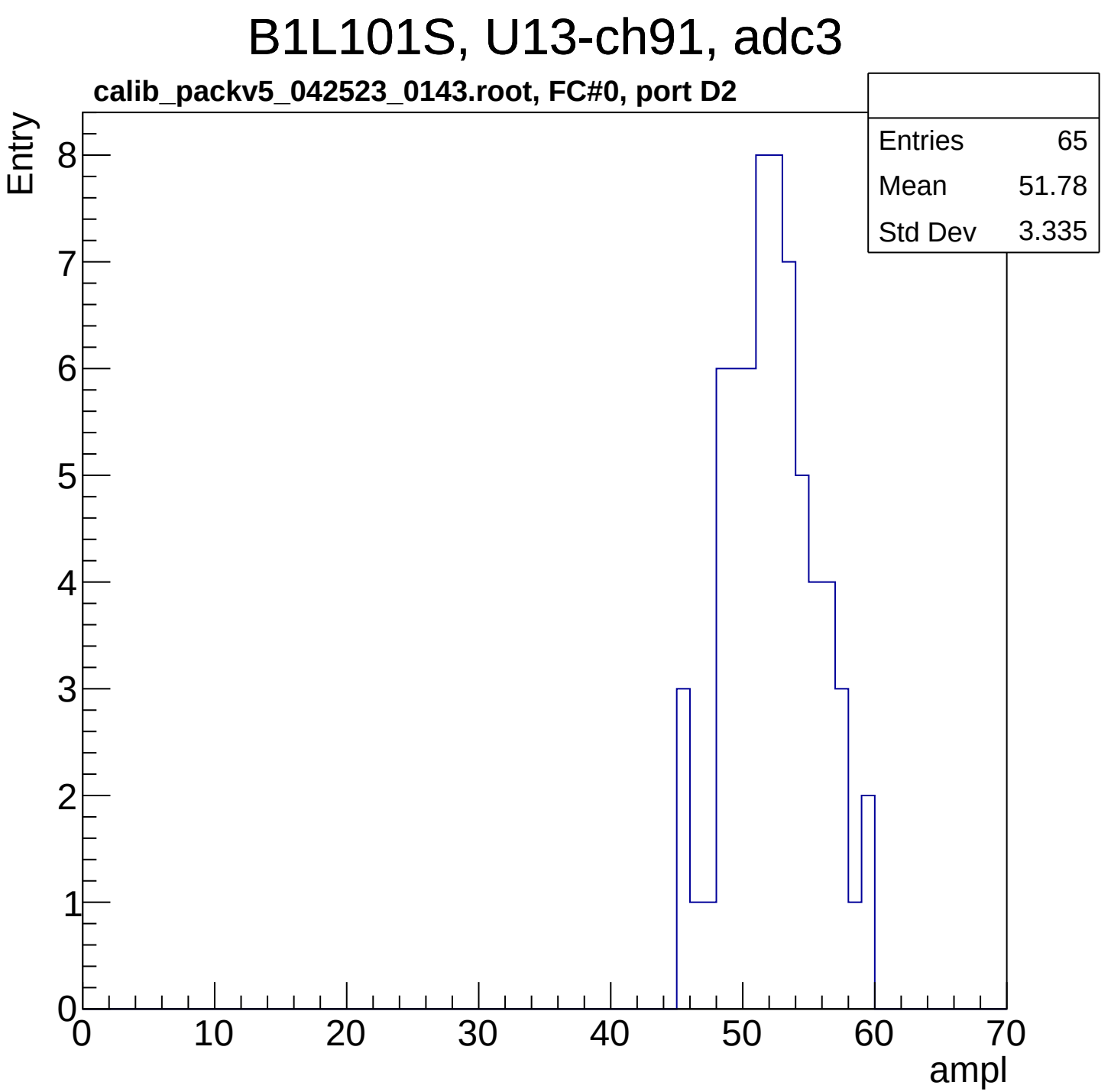
B1L101S, U13-ch91, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 51.78
Std Dev 3.335
Entry
ampl
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1
2
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4
5
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0
10
20
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40
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60
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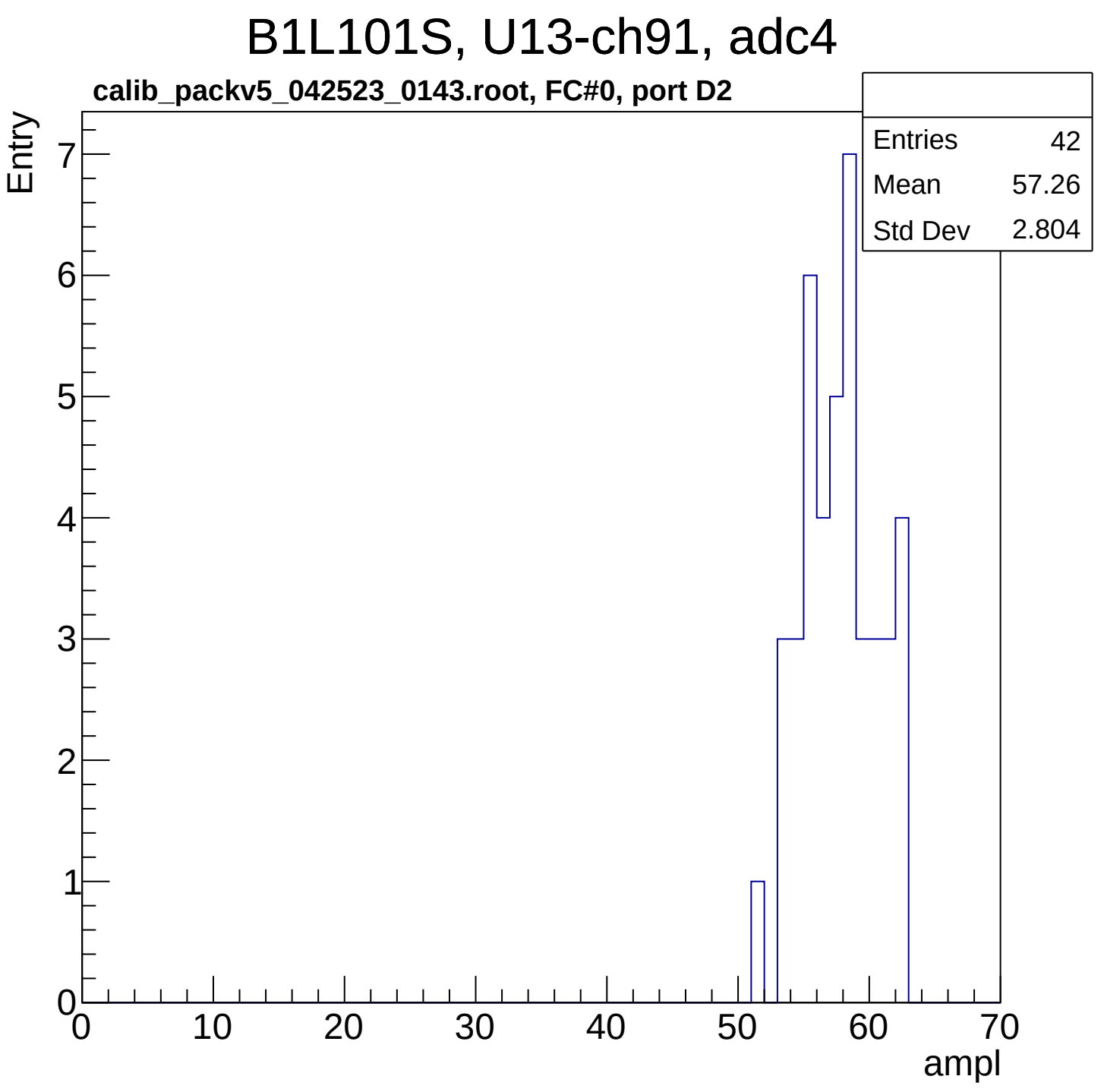
B1L101S, U13-ch91, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 57.26
Std Dev 2.804
Entry
ampl
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1
2
3
4
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6
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0
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60
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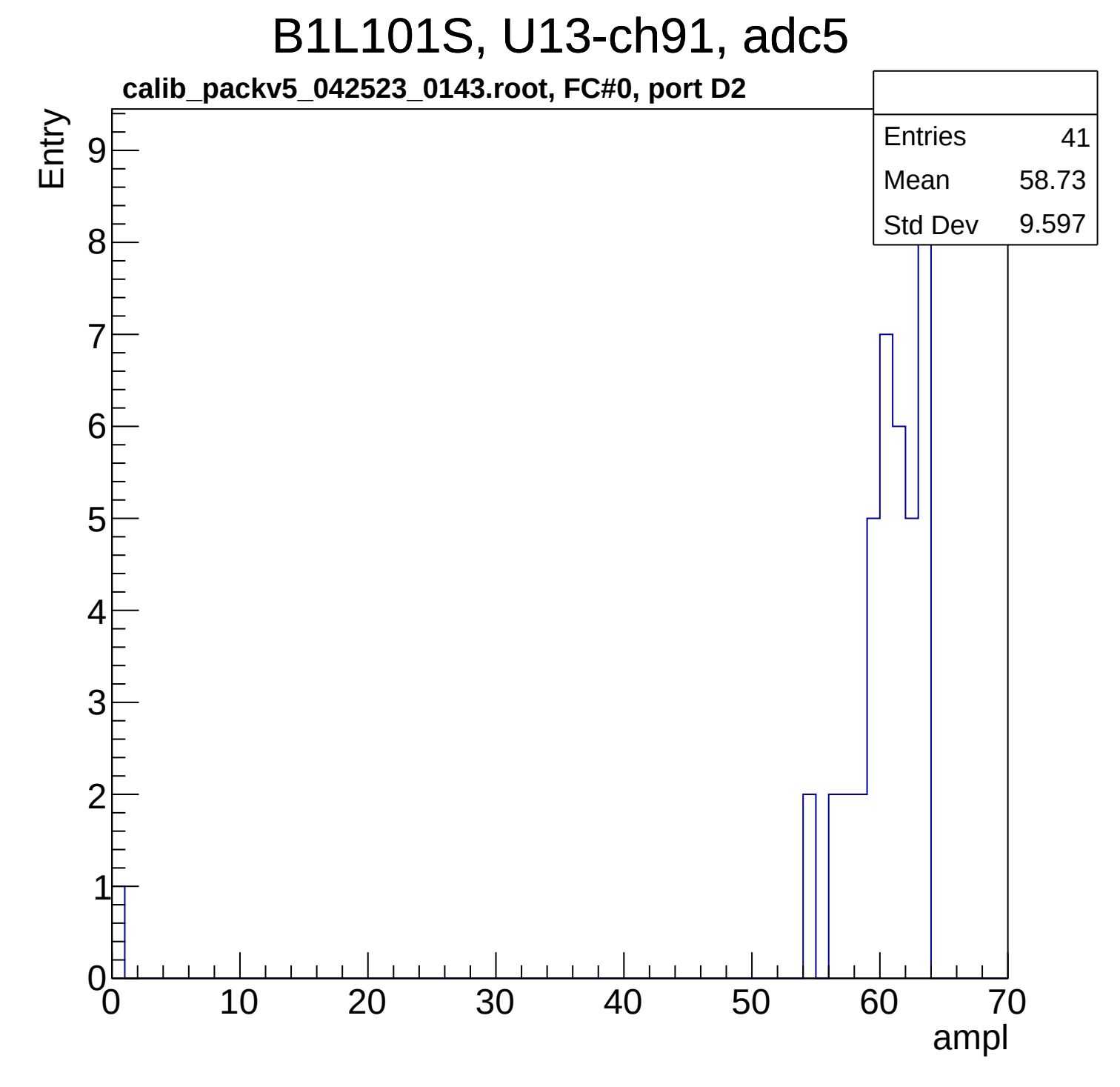
B1L101S, U13-ch91, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 58.73
Std Dev 9.597
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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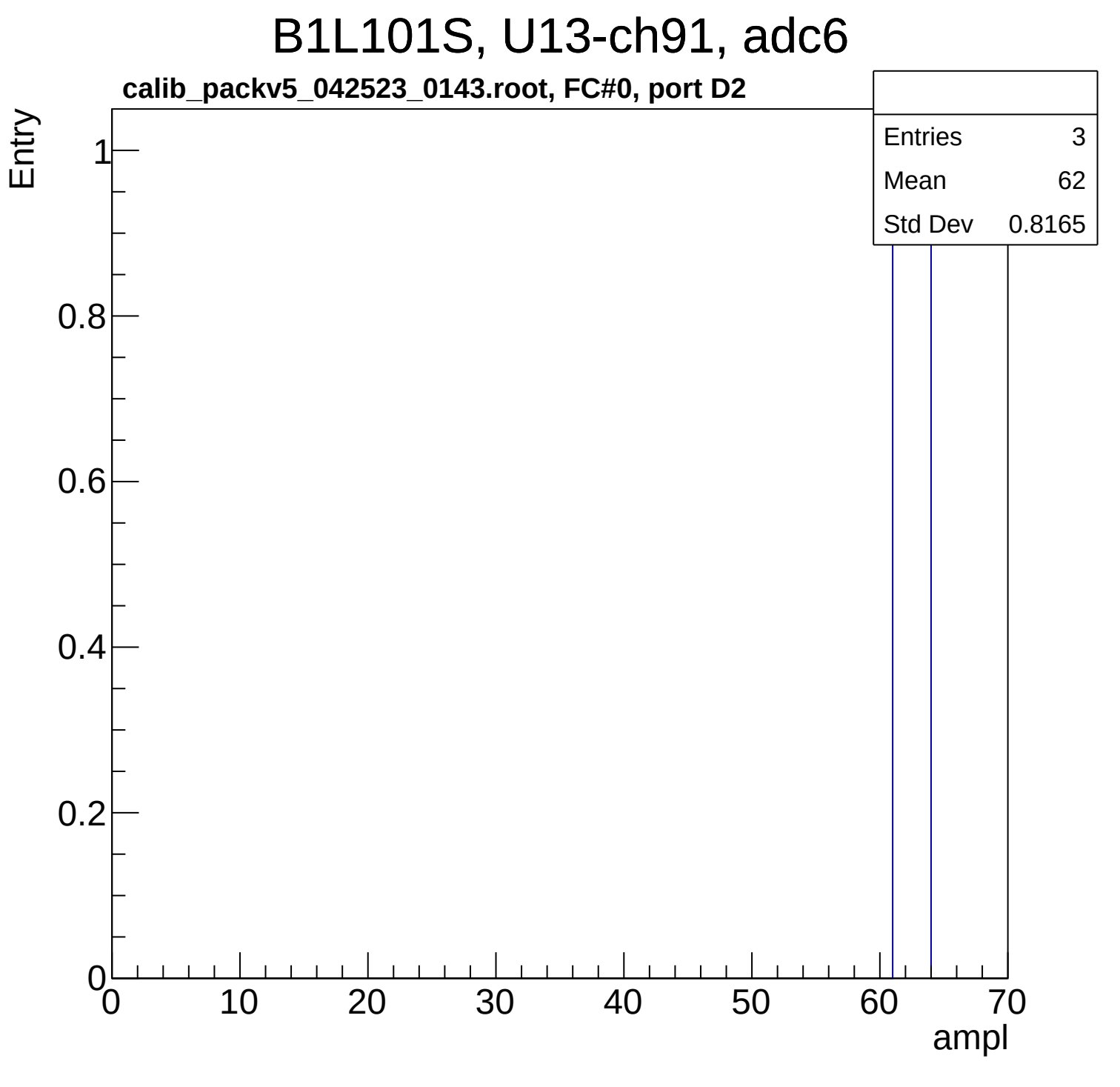
B1L101S, U13-ch91, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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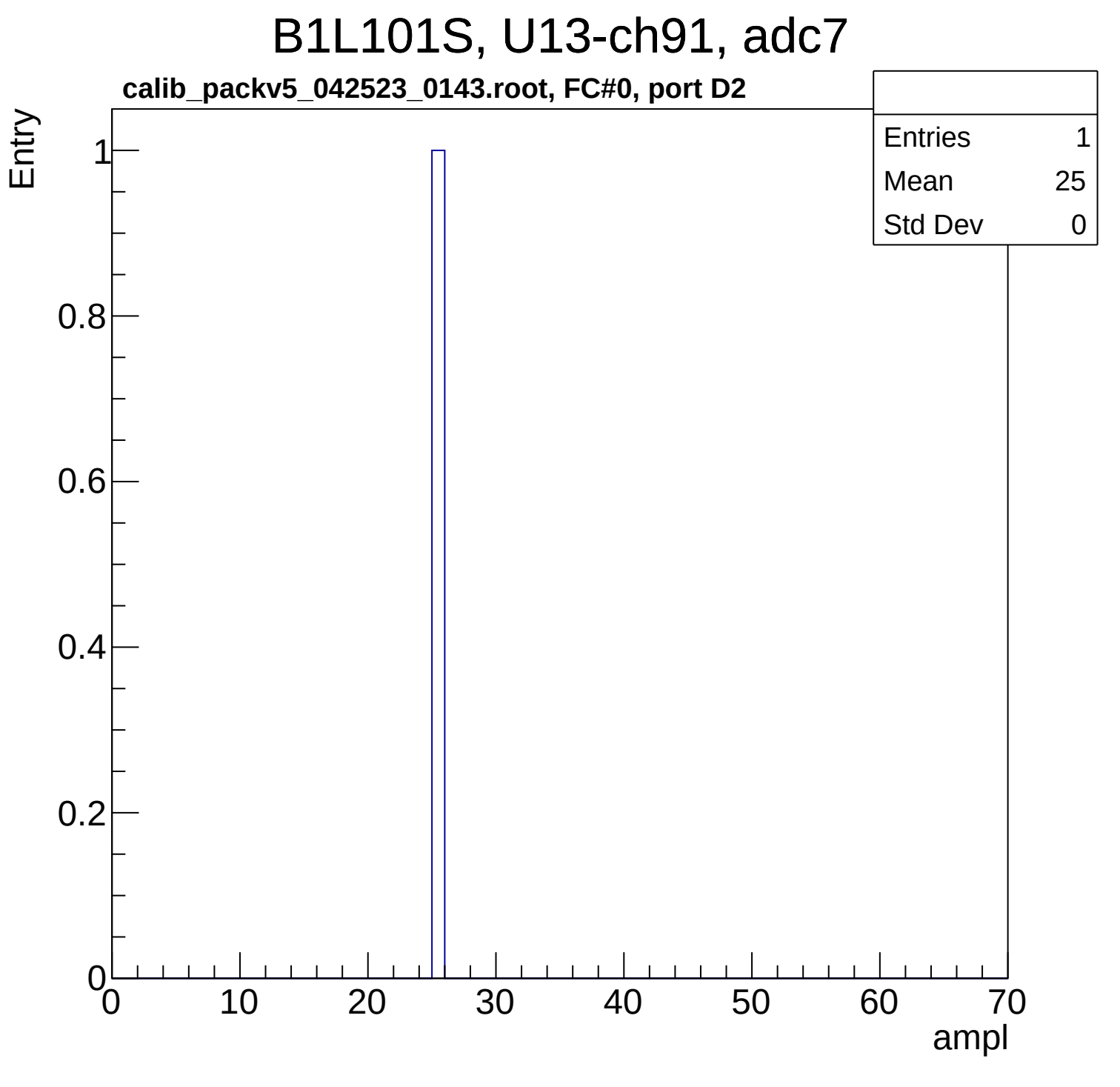
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch91, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 25
Std Dev 0
Entry
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0.6
0.4
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10
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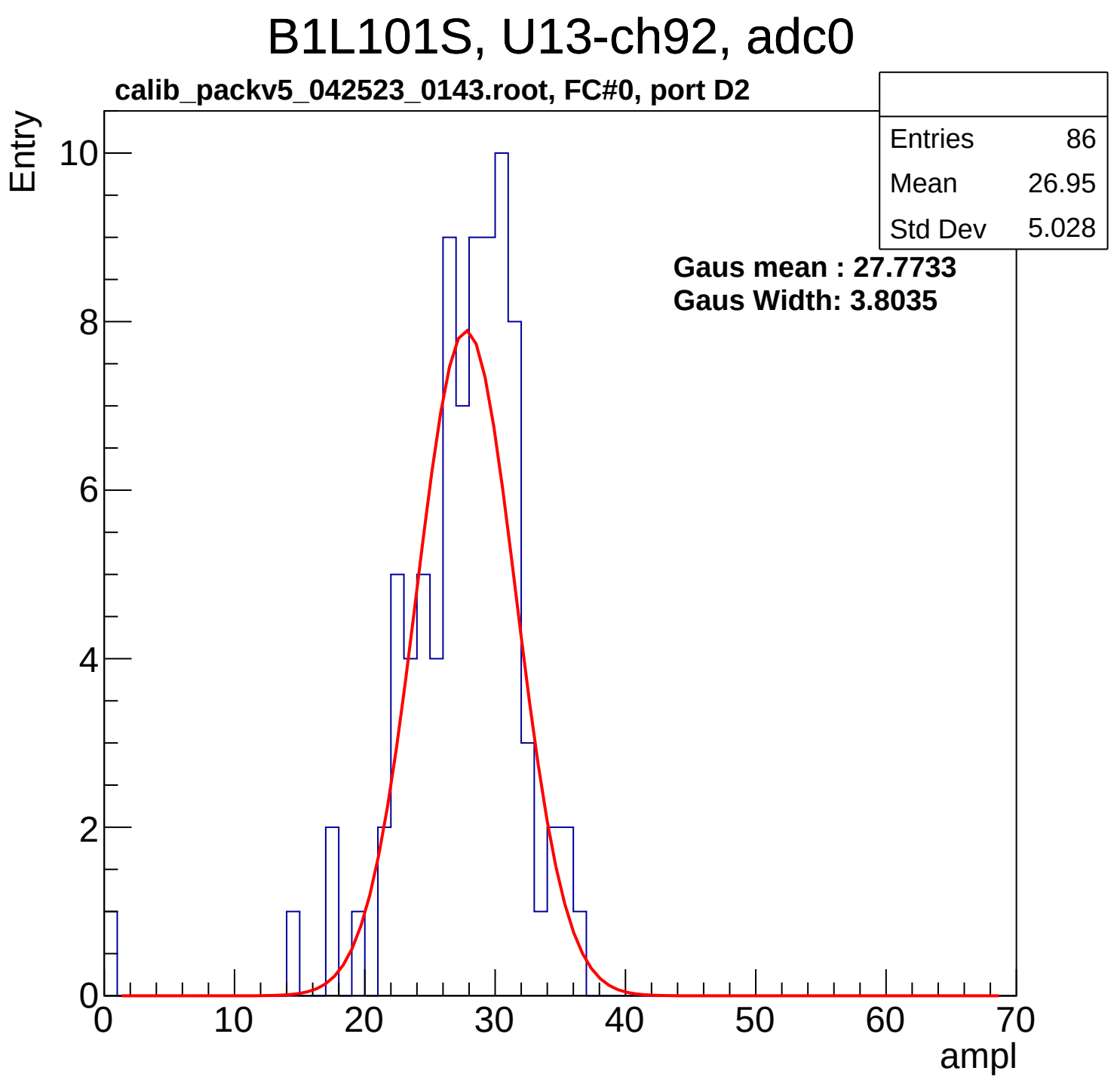
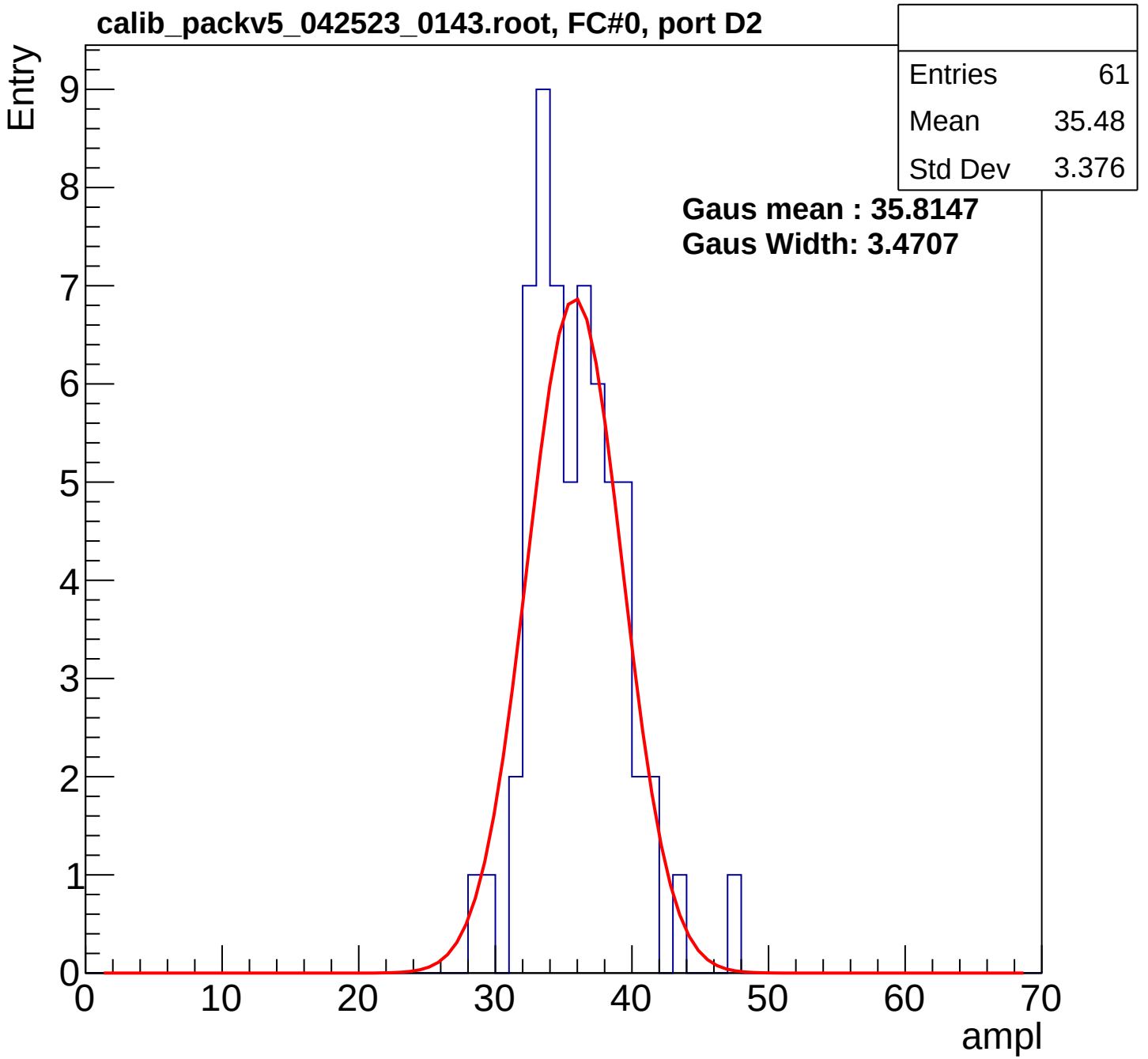
50
60
70
ampl


B1L101S, U13-ch92, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 86
Mean 26.95
Std Dev 5.028
Gaus mean : 27.7733
Gaus Width: 3.8035
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch92, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 35.48
Std Dev 3.376
Gaus mean : 35.8147
Gaus Width: 3.4707
Entry
ampl
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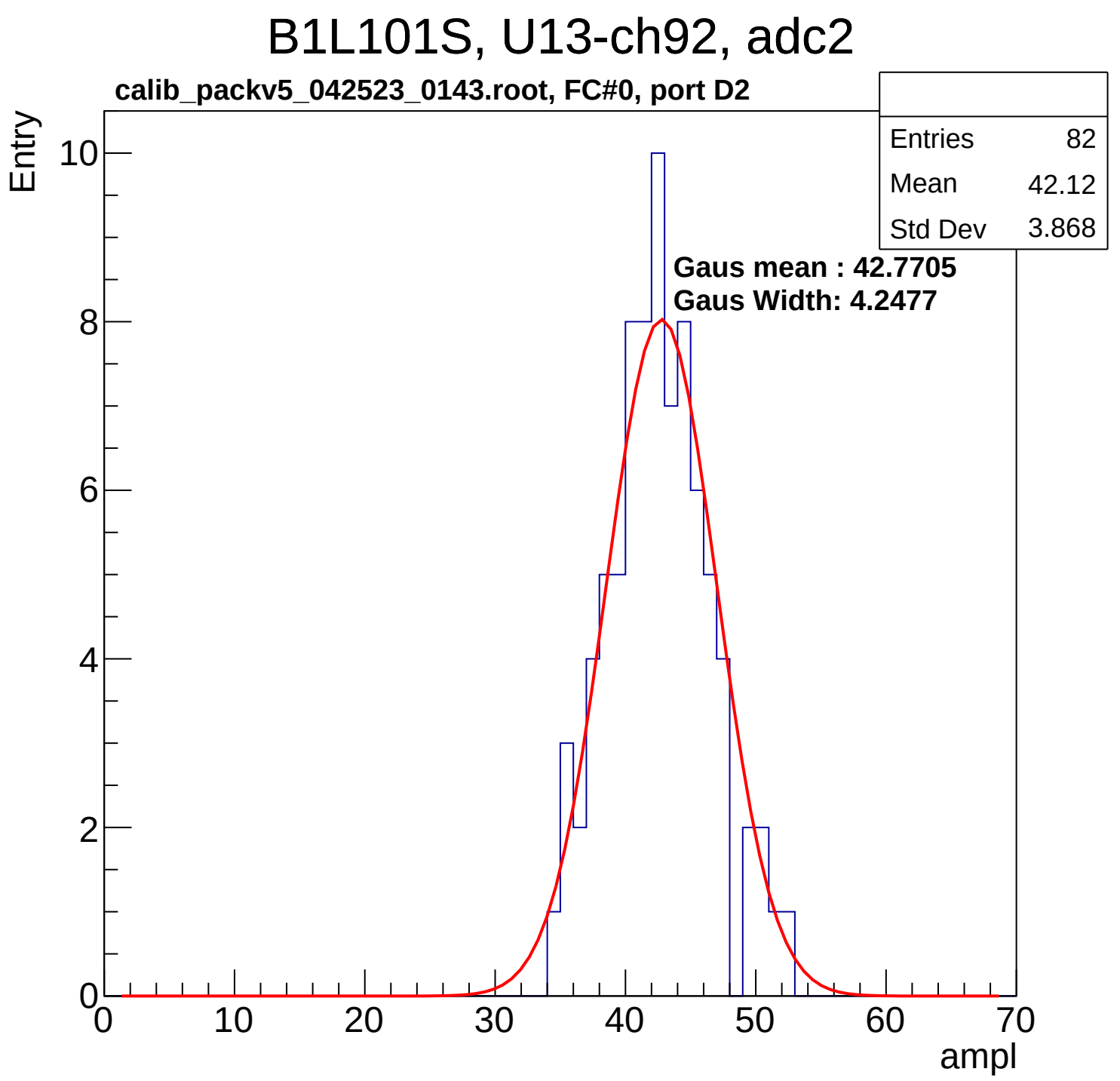
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L101S, U13-ch92, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 82
Mean 42.12
Std Dev 3.868
Gaus mean : 42.7705
Gaus Width: 4.2477
Entry
ampl
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2
4
6
8
10
0
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20
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40
50
60
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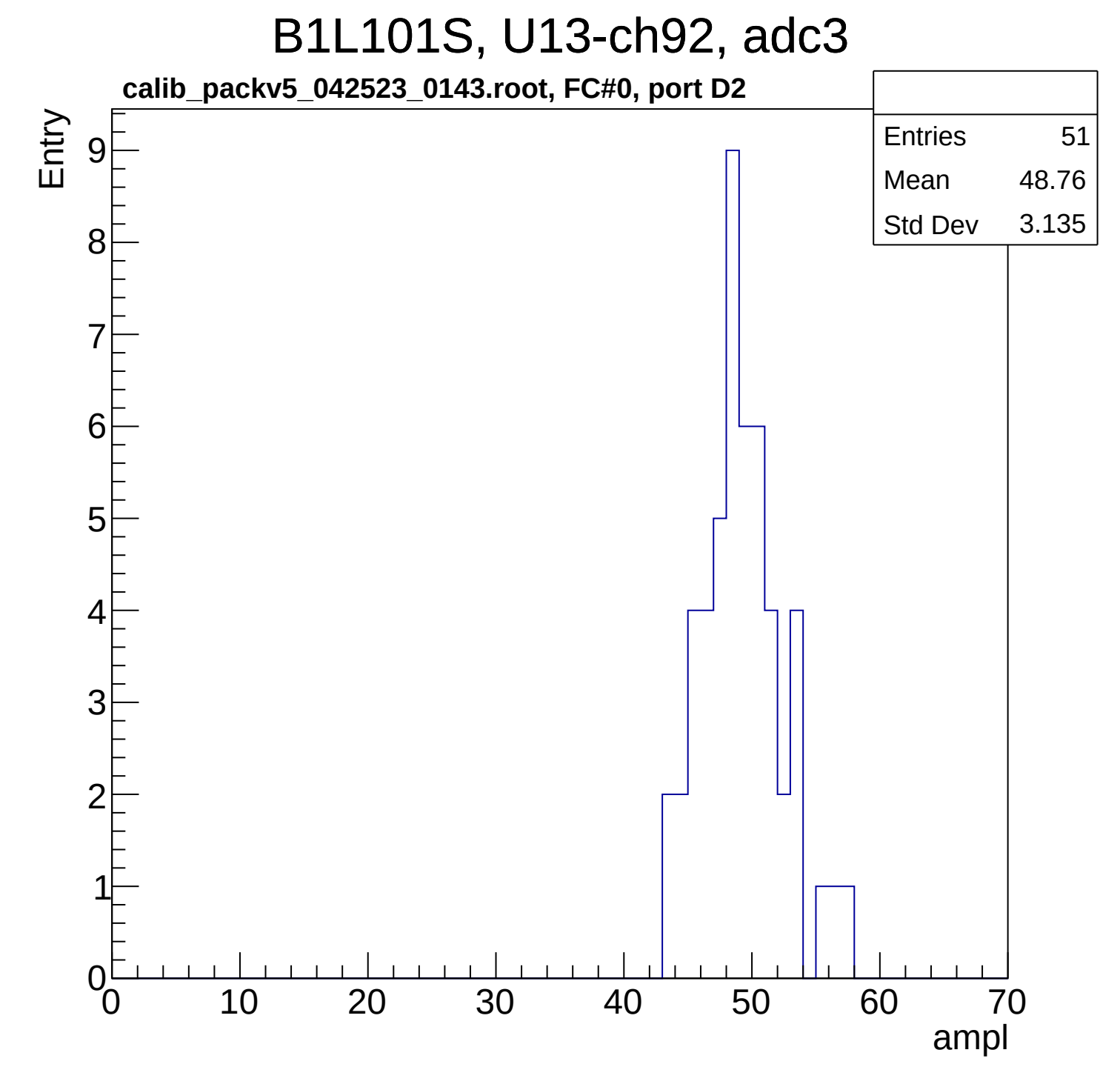
B1L101S, U13-ch92, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 48.76
Std Dev 3.135
Entry
ampl
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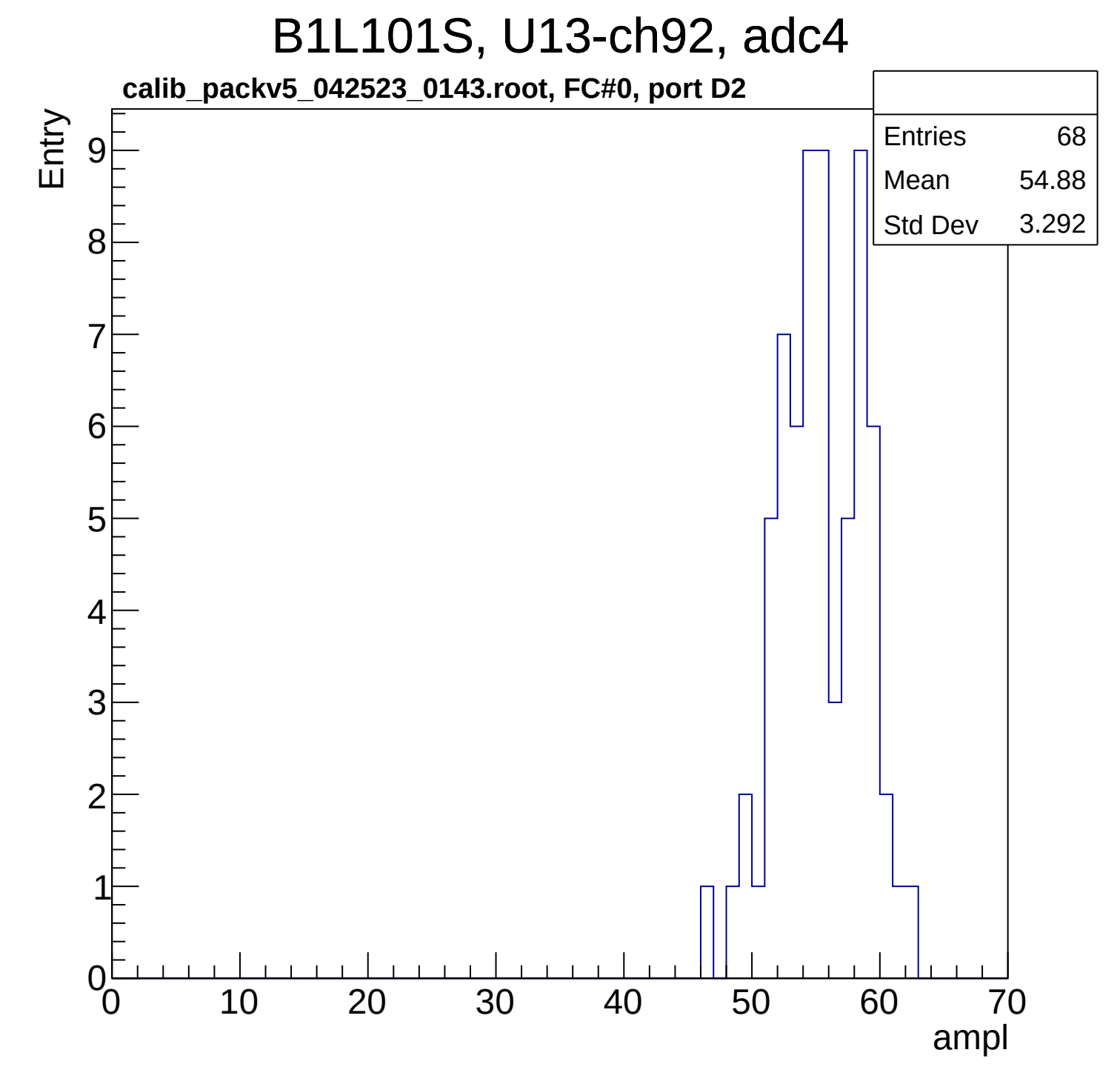
B1L101S, U13-ch92, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 54.88
Std Dev 3.292
Entry
ampl
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2
3
4
5
6
7
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40
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60
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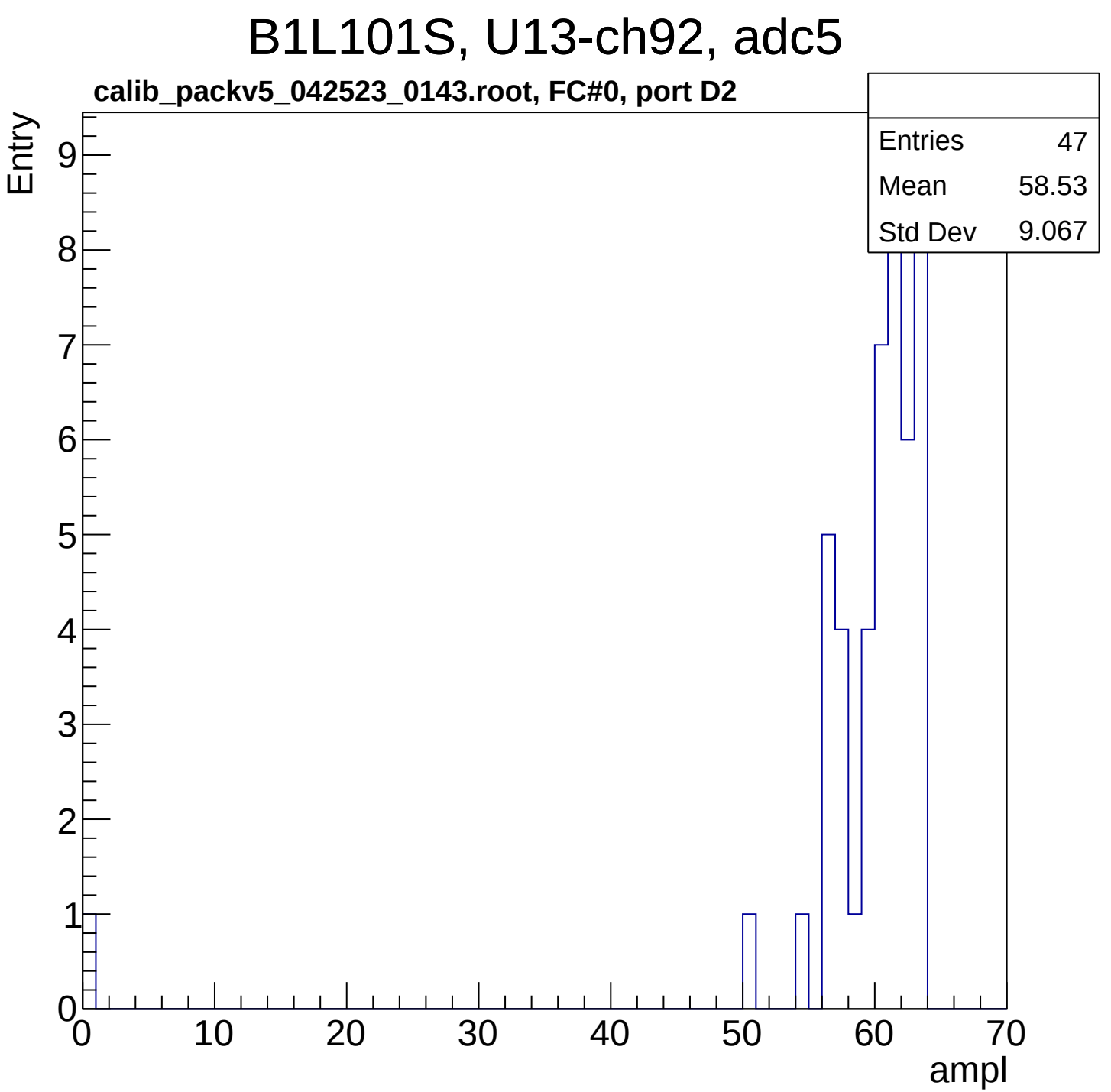
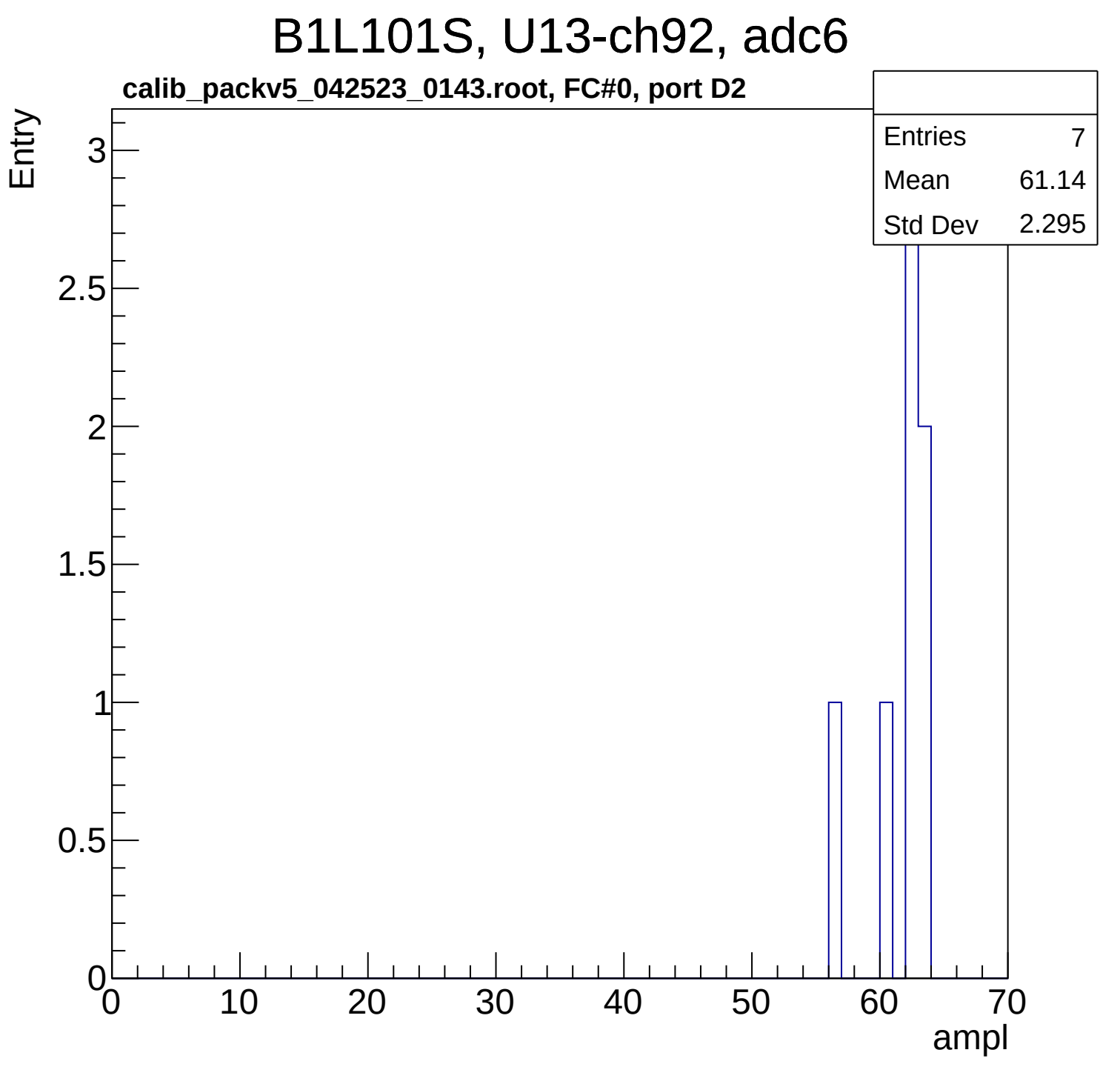
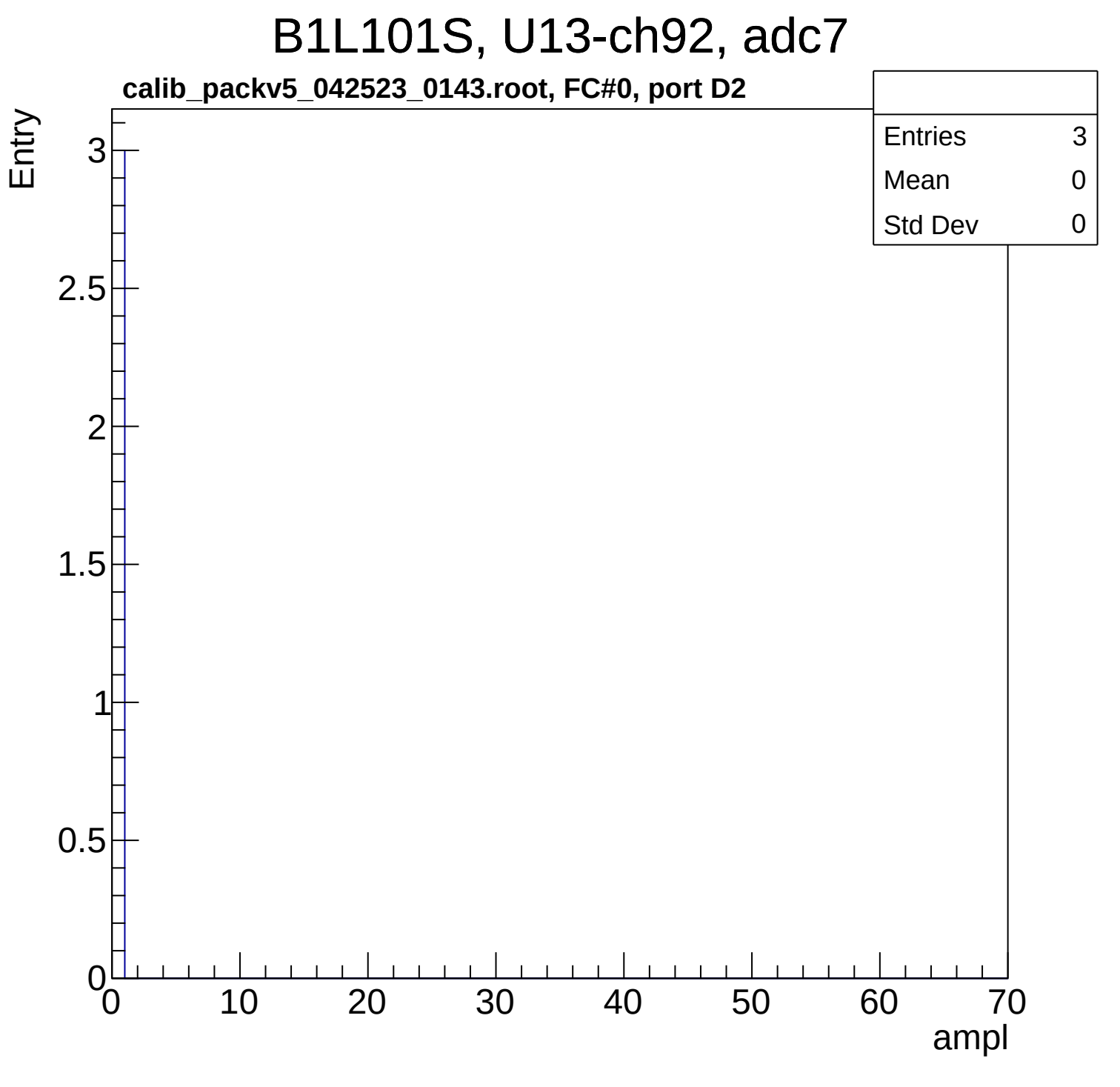
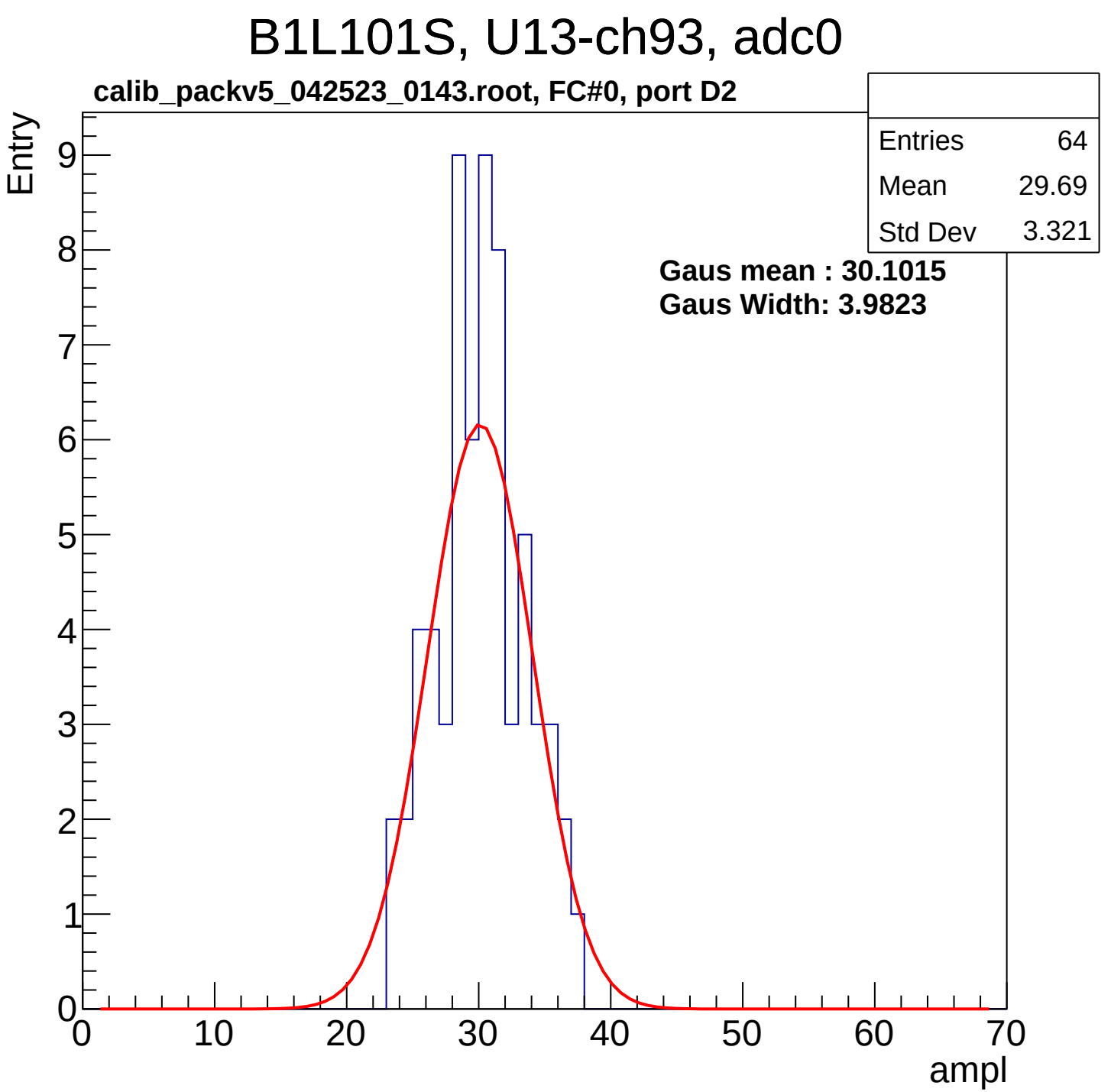
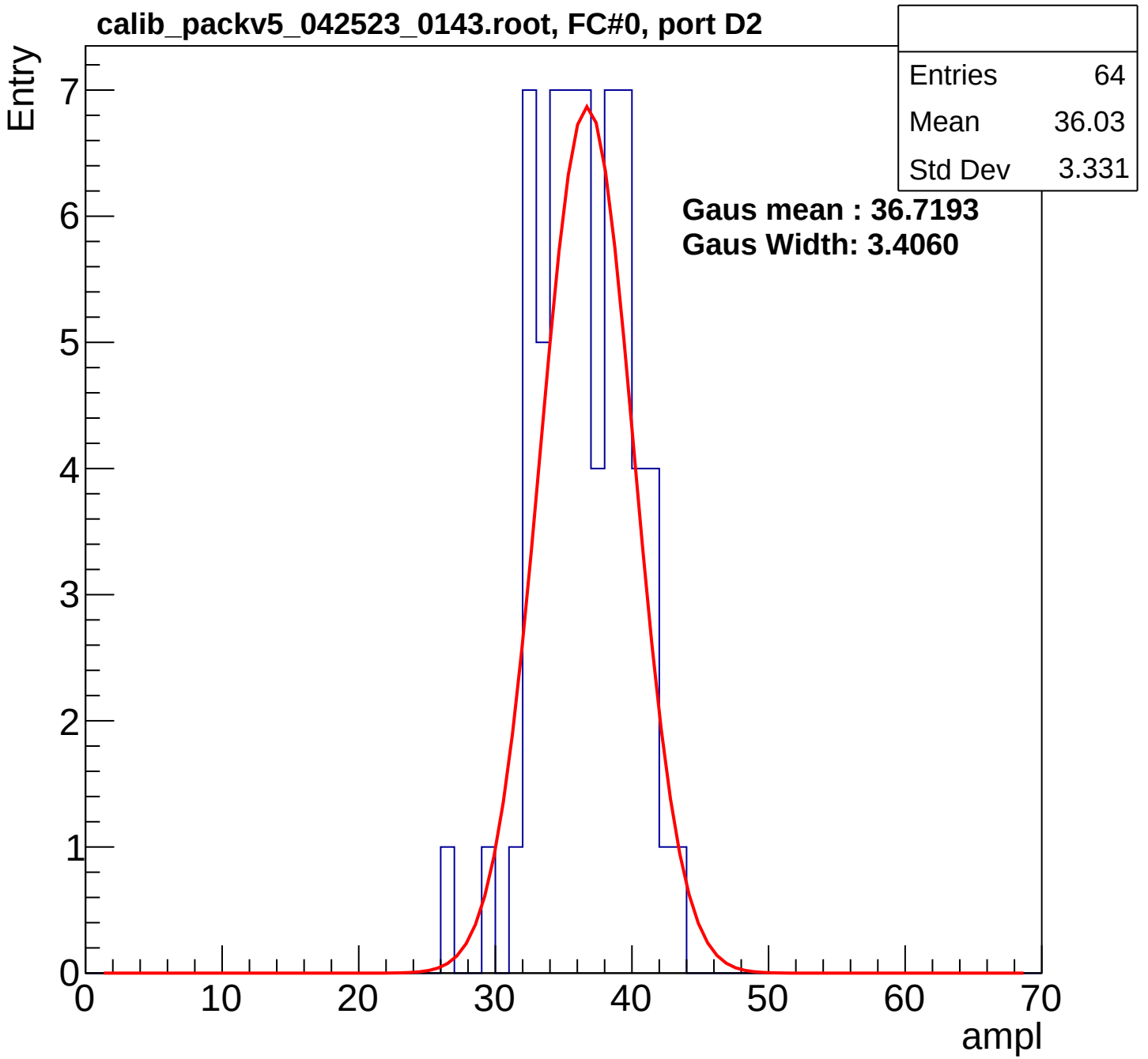
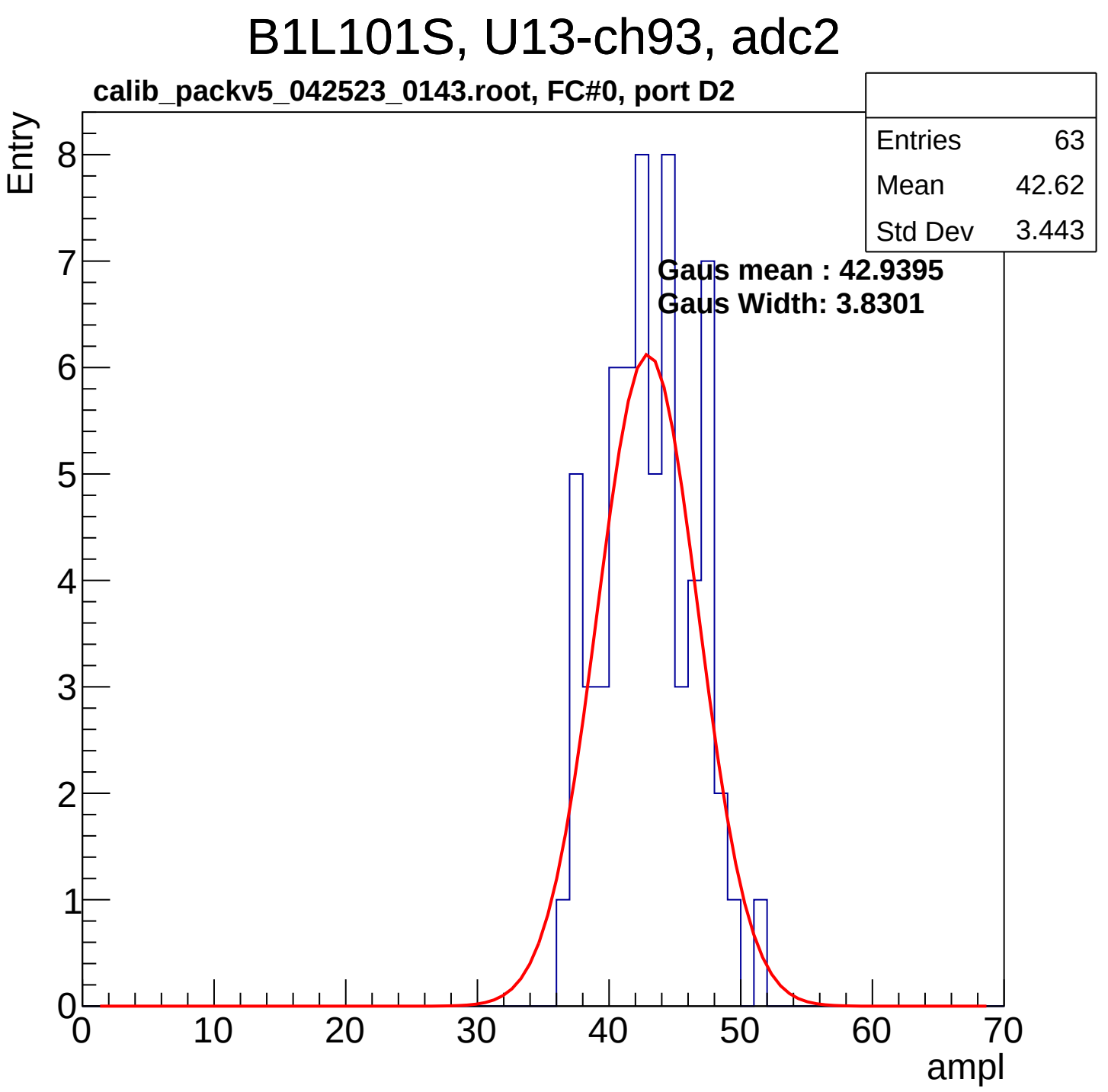

B1L101S, U13-ch92, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 47
Mean 58.53
Std Dev 9.067
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

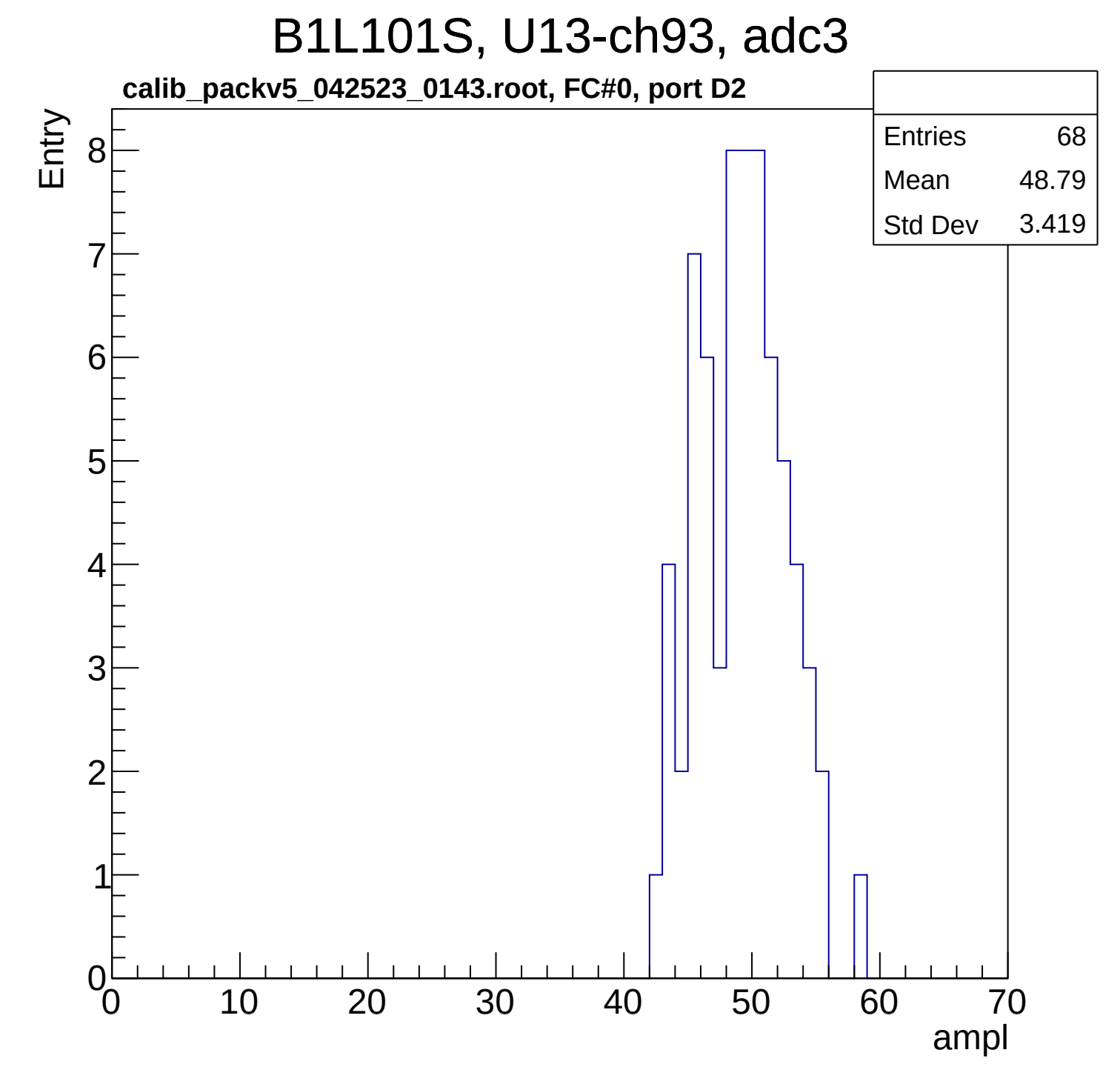

B1L101S, U13-ch92, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 7
Mean 61.14
Std Dev 2.295
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

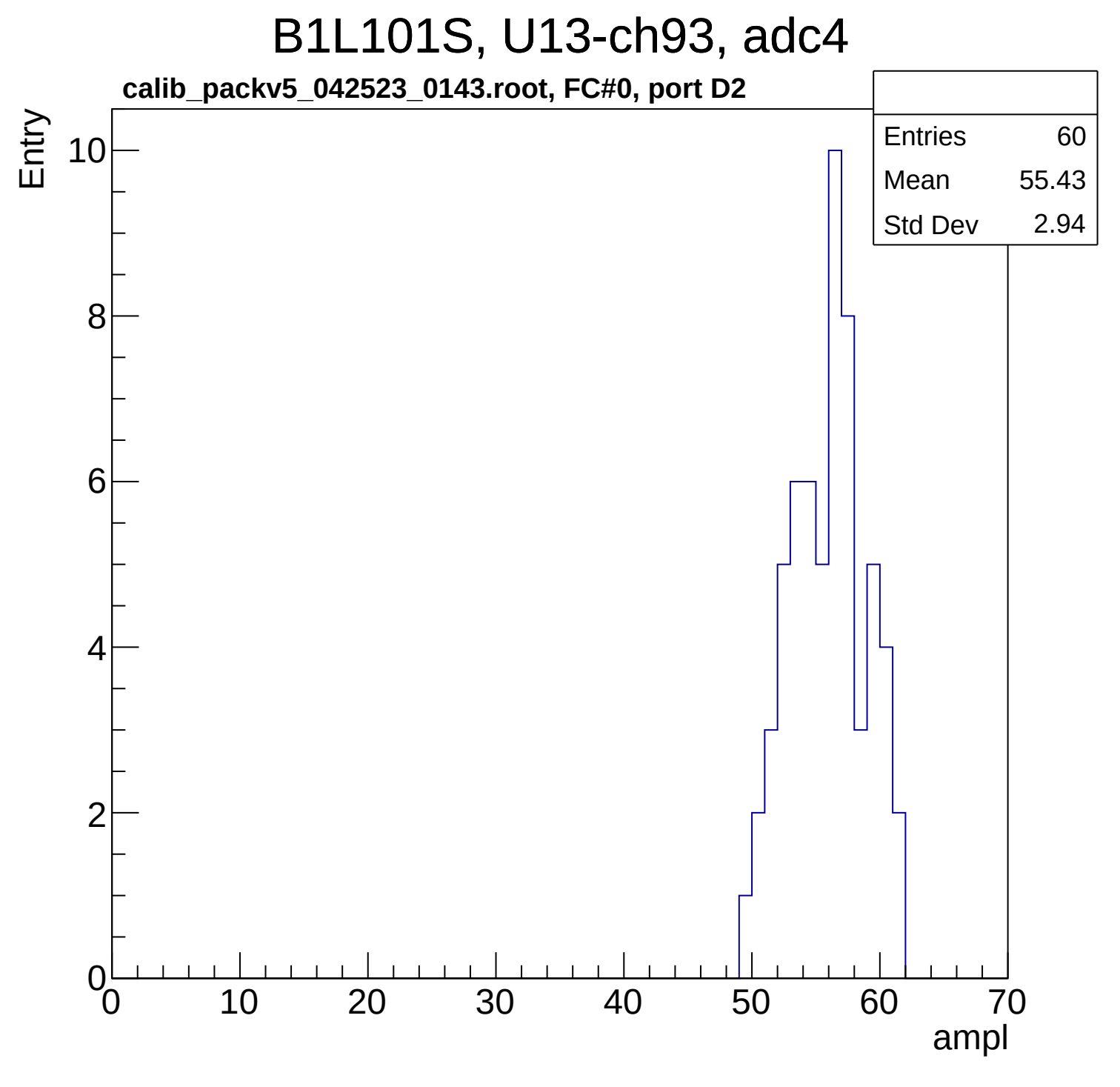

B1L101S, U13-ch92, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

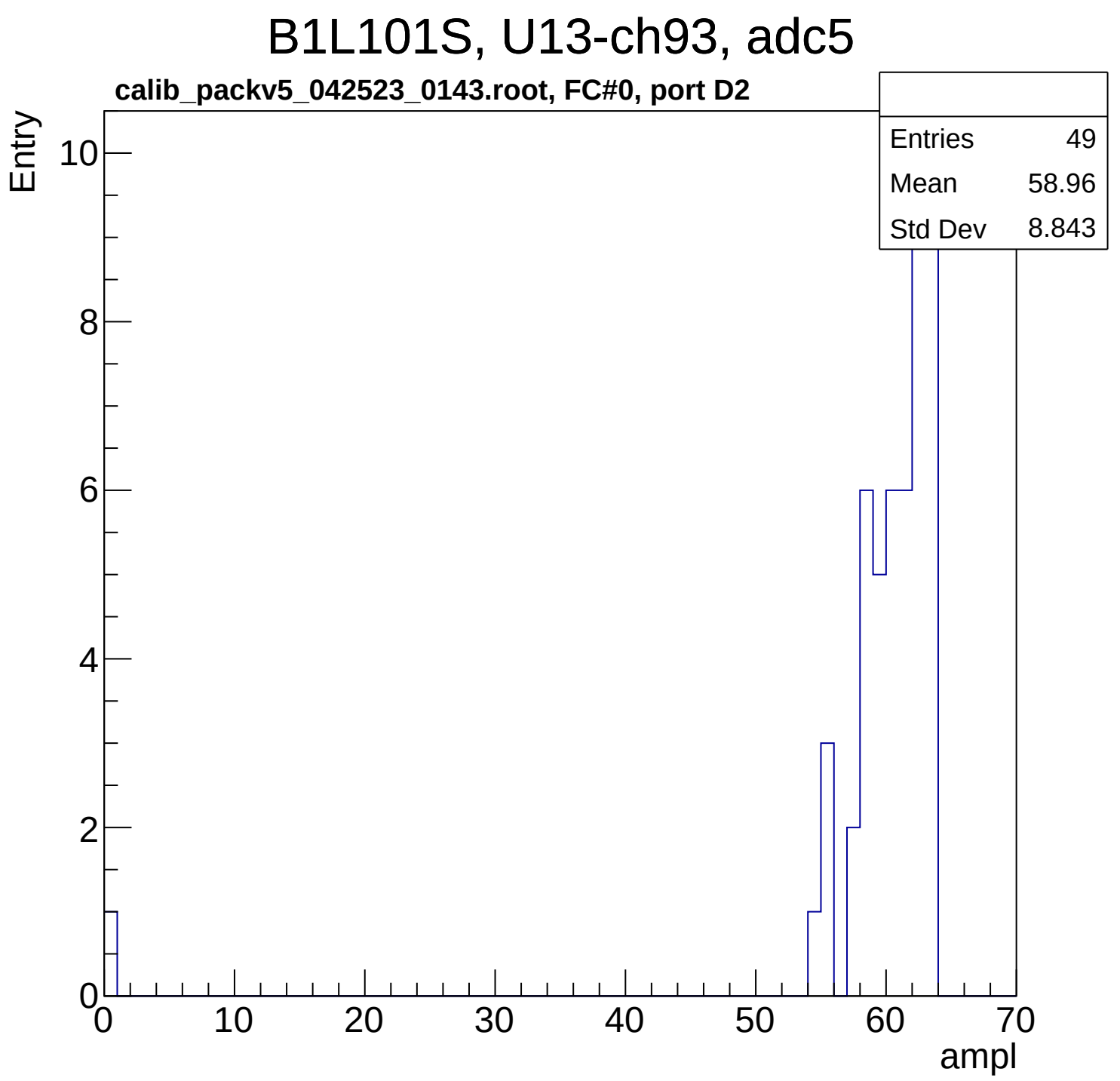

B1L101S, U13-ch93, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 29.69
Std Dev 3.321
Gaus mean : 30.1015
Gaus Width: 3.9823
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

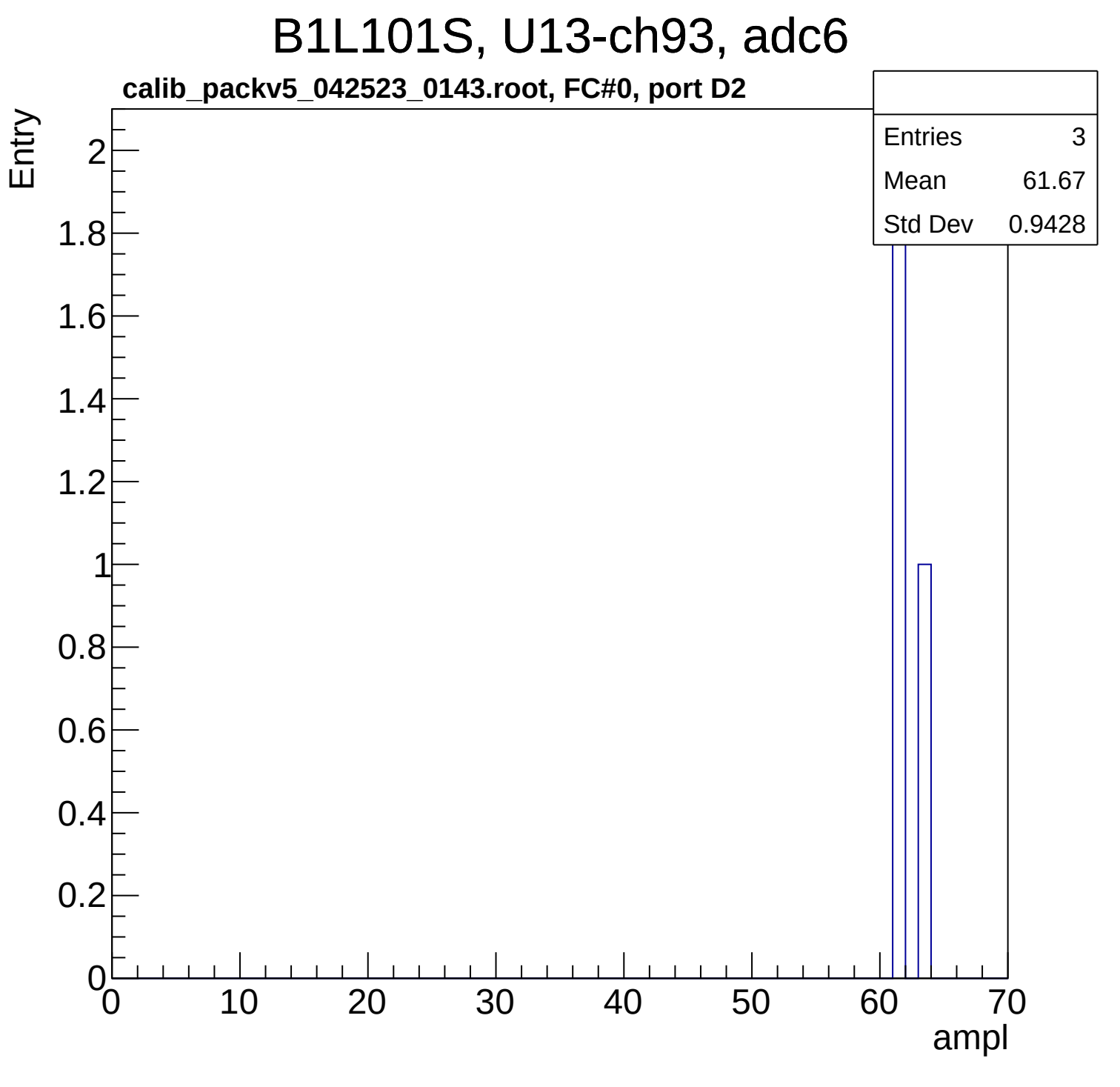

B1L101S, U13-ch93, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 36.03
Std Dev 3.331
Gaus mean : 36.7193
Gaus Width: 3.4060
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

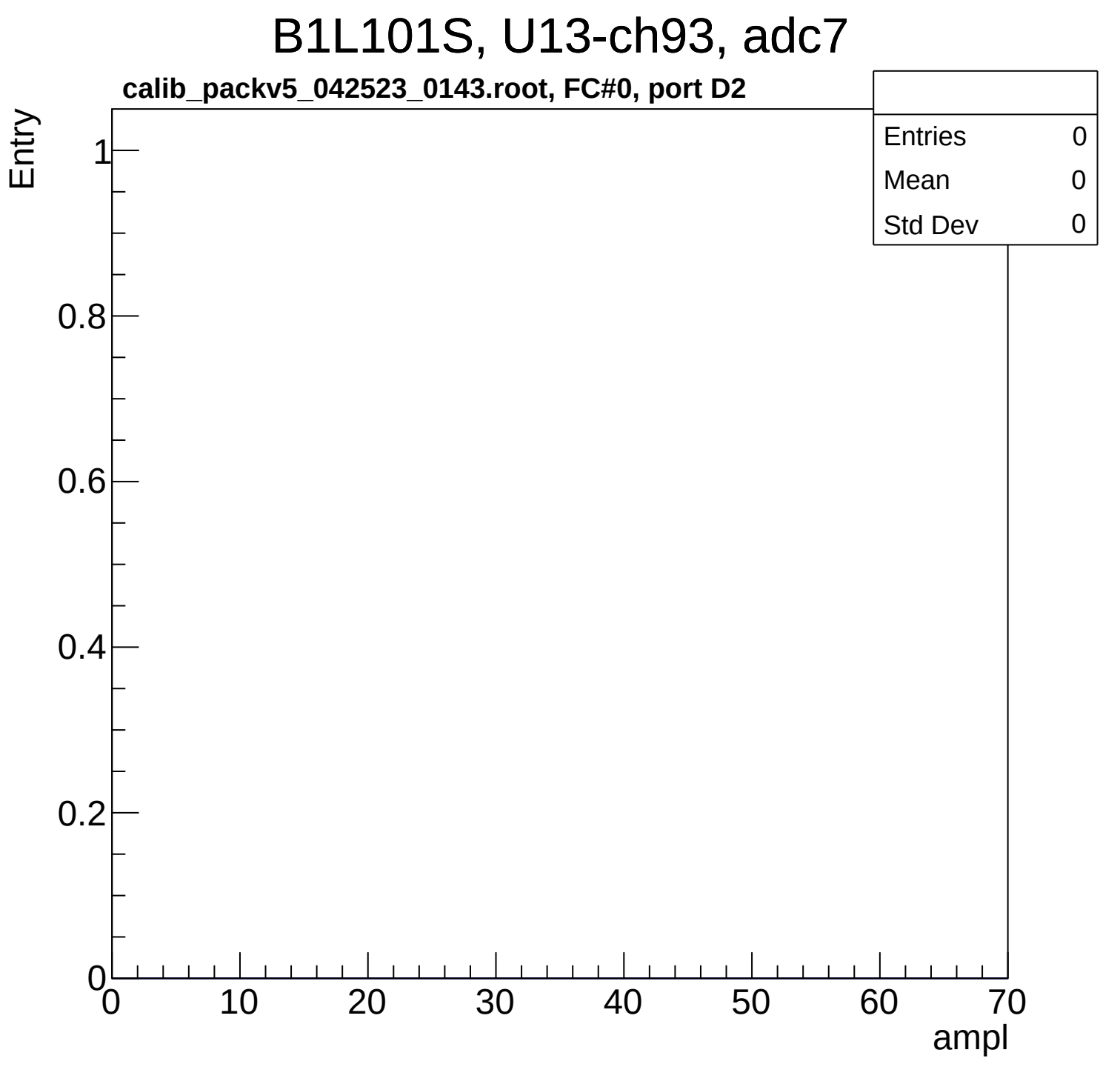

B1L101S, U13-ch93, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 42.62
Std Dev 3.443
Gaus mean : 42.9395
Gaus Width: 3.8301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

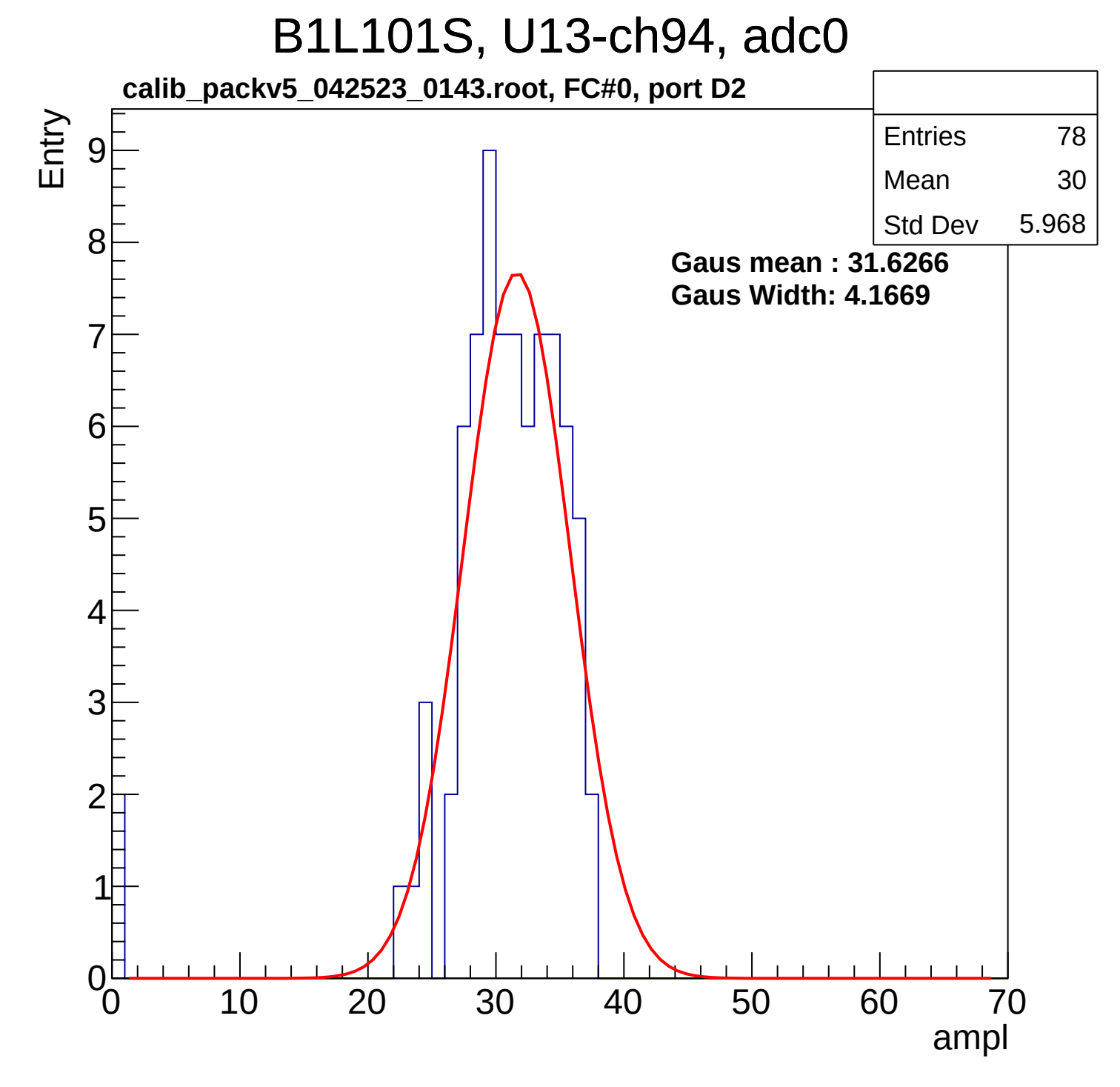

B1L101S, U13-ch93, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 48.79
Std Dev 3.419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

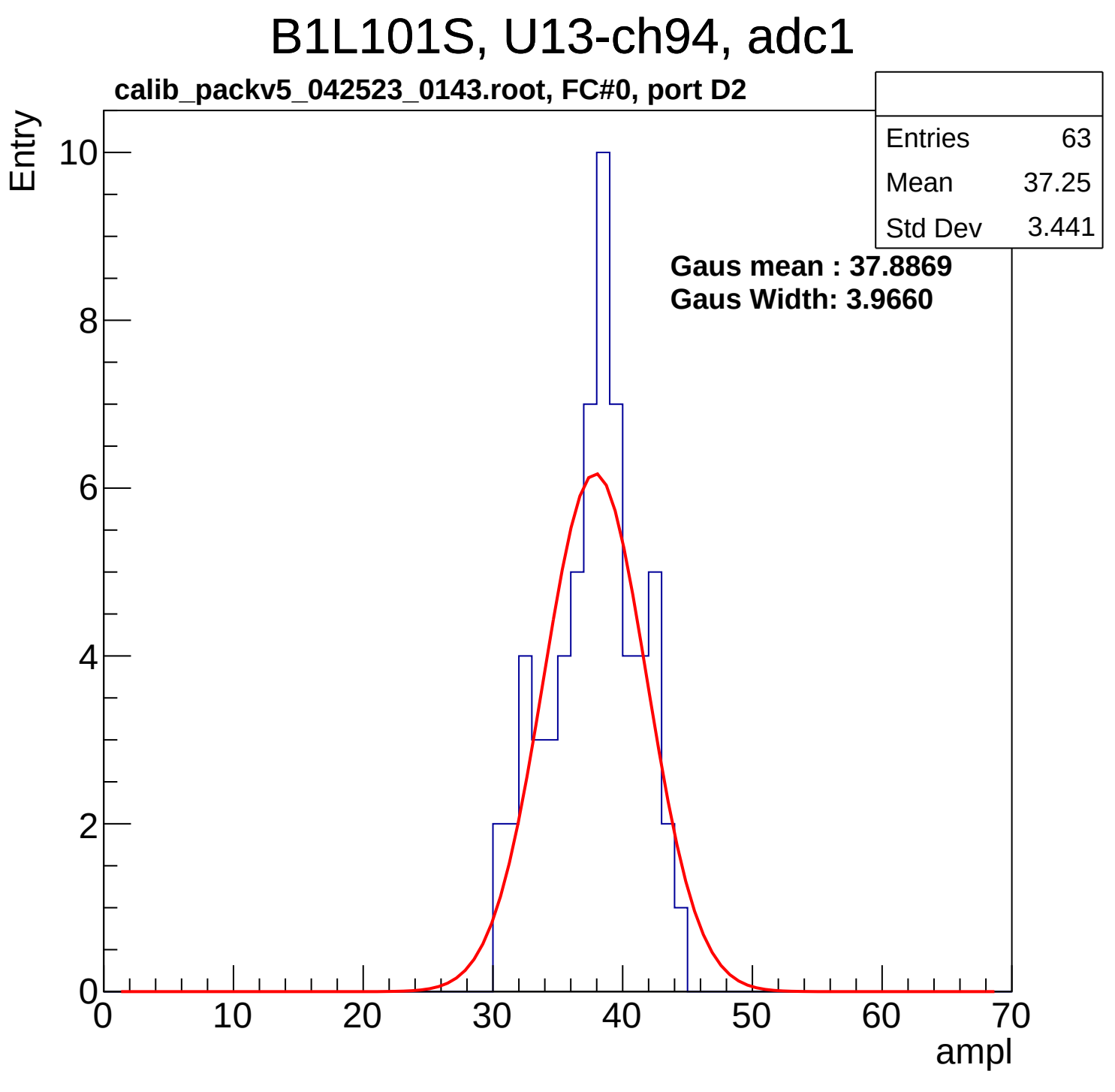

B1L101S, U13-ch93, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 55.43
Std Dev 2.94
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

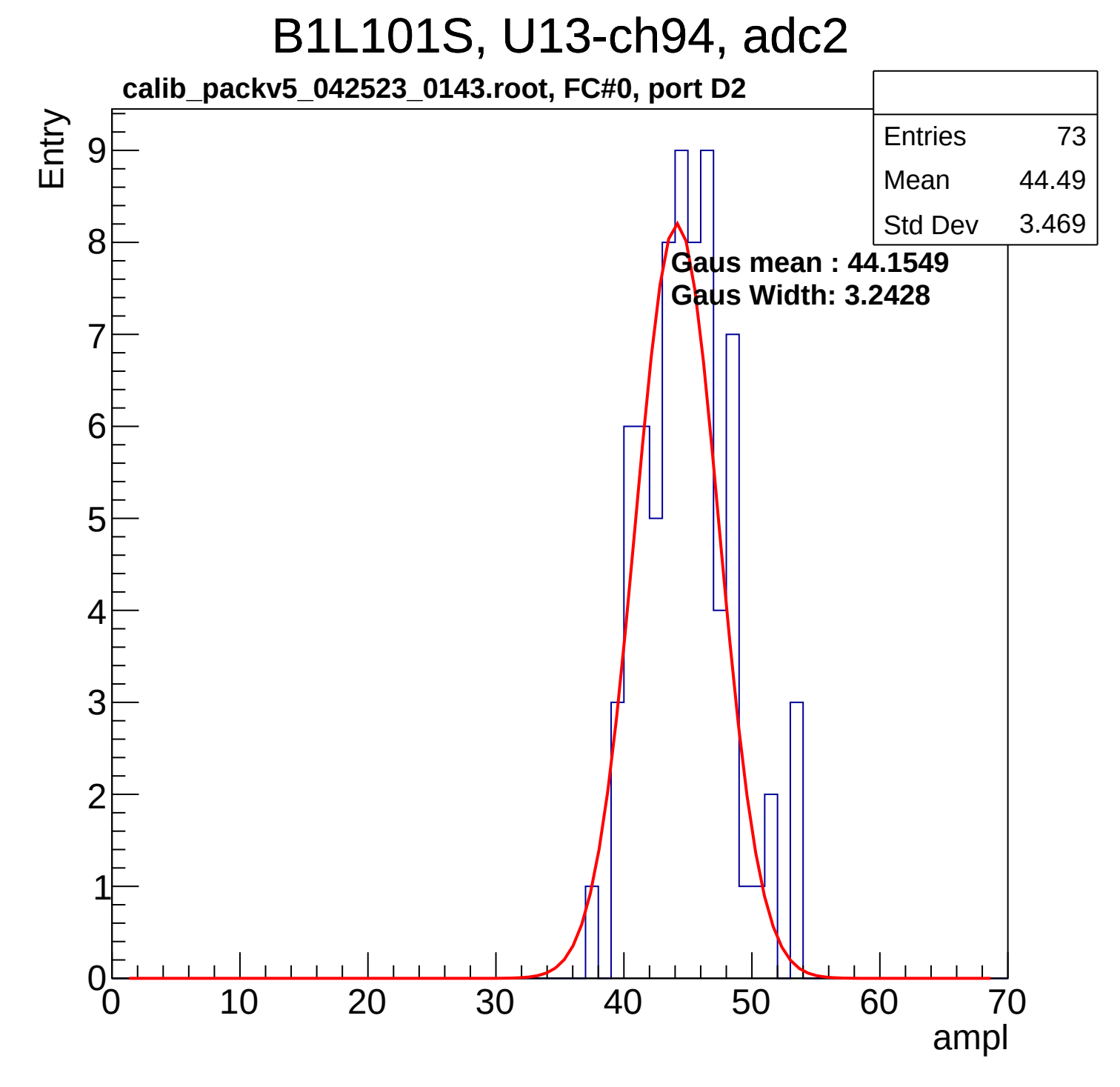

B1L101S, U13-ch93, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 58.96
Std Dev 8.843
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

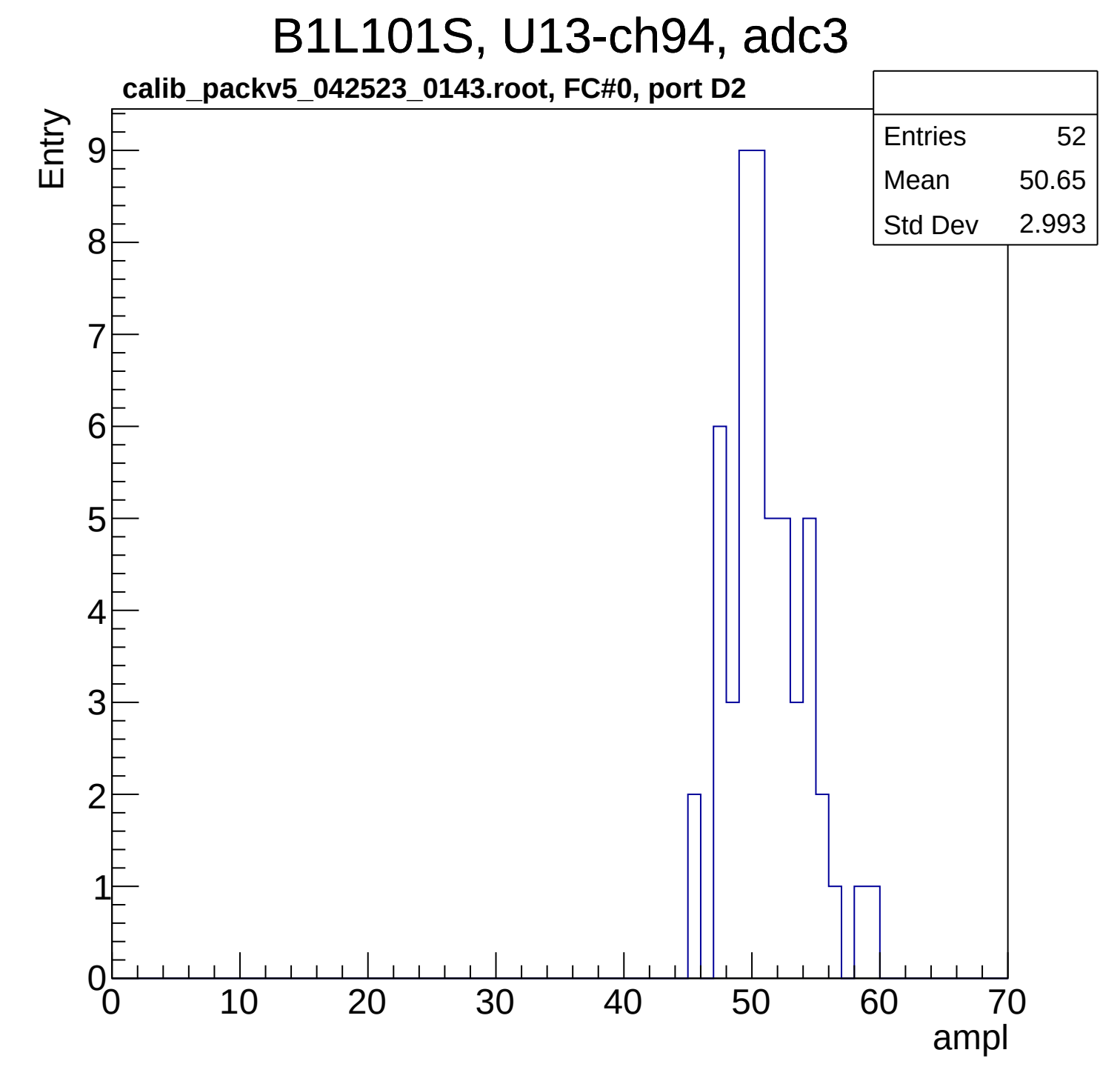

B1L101S, U13-ch93, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

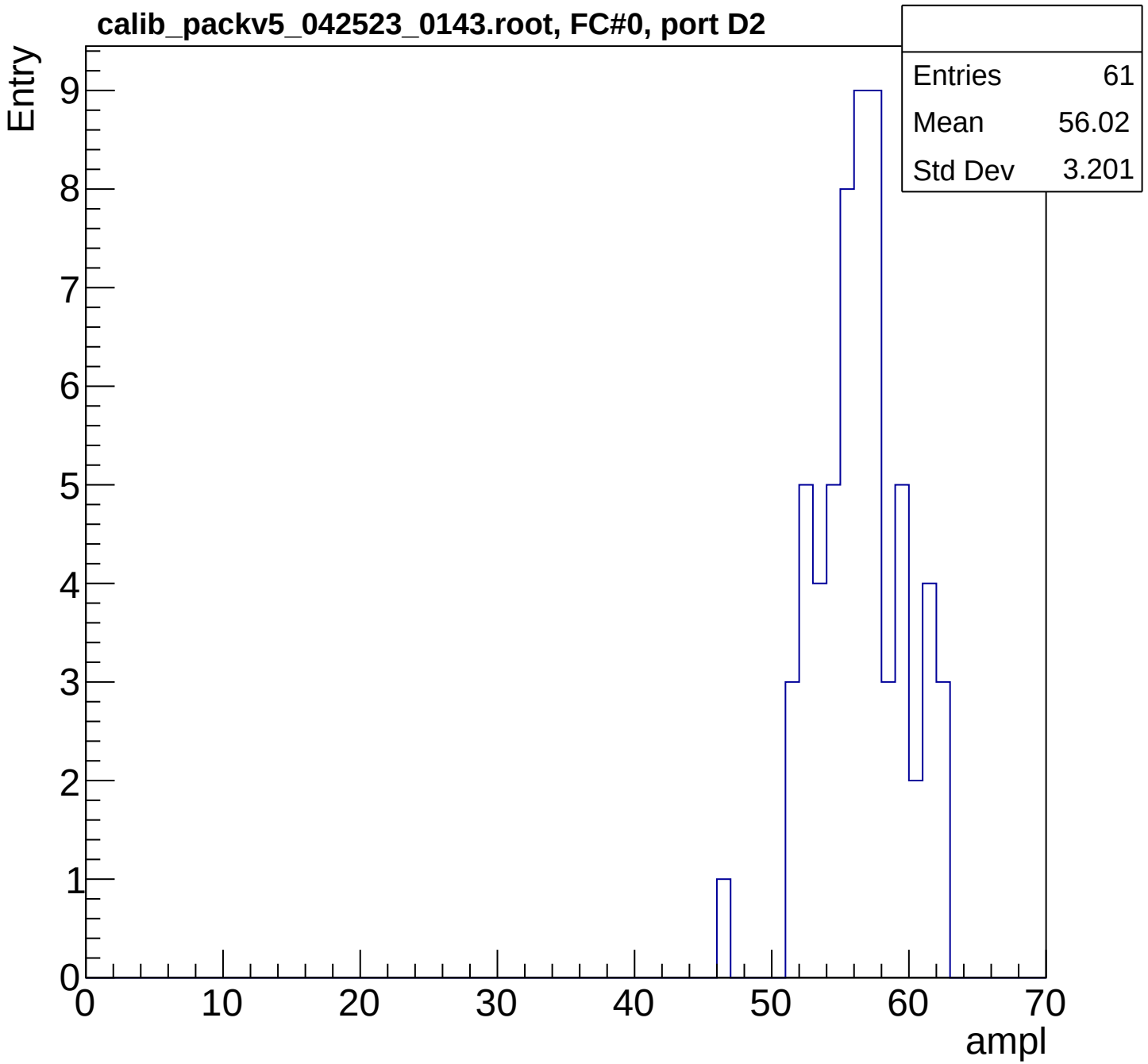

B1L101S, U13-ch93, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

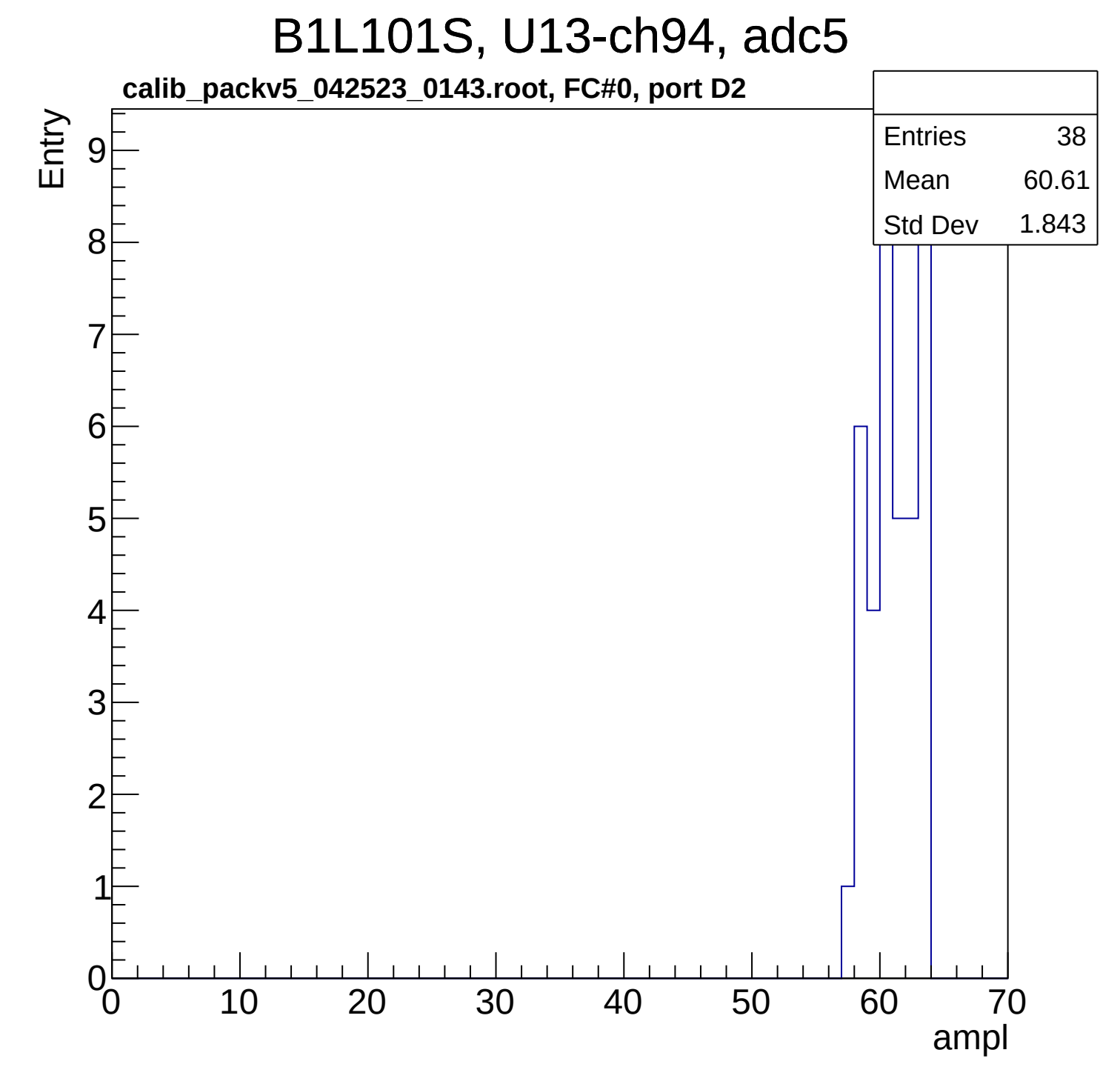

B1L101S, U13-ch94, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 30
Std Dev 5.968
Gaus mean : 31.6266
Gaus Width: 4.1669
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

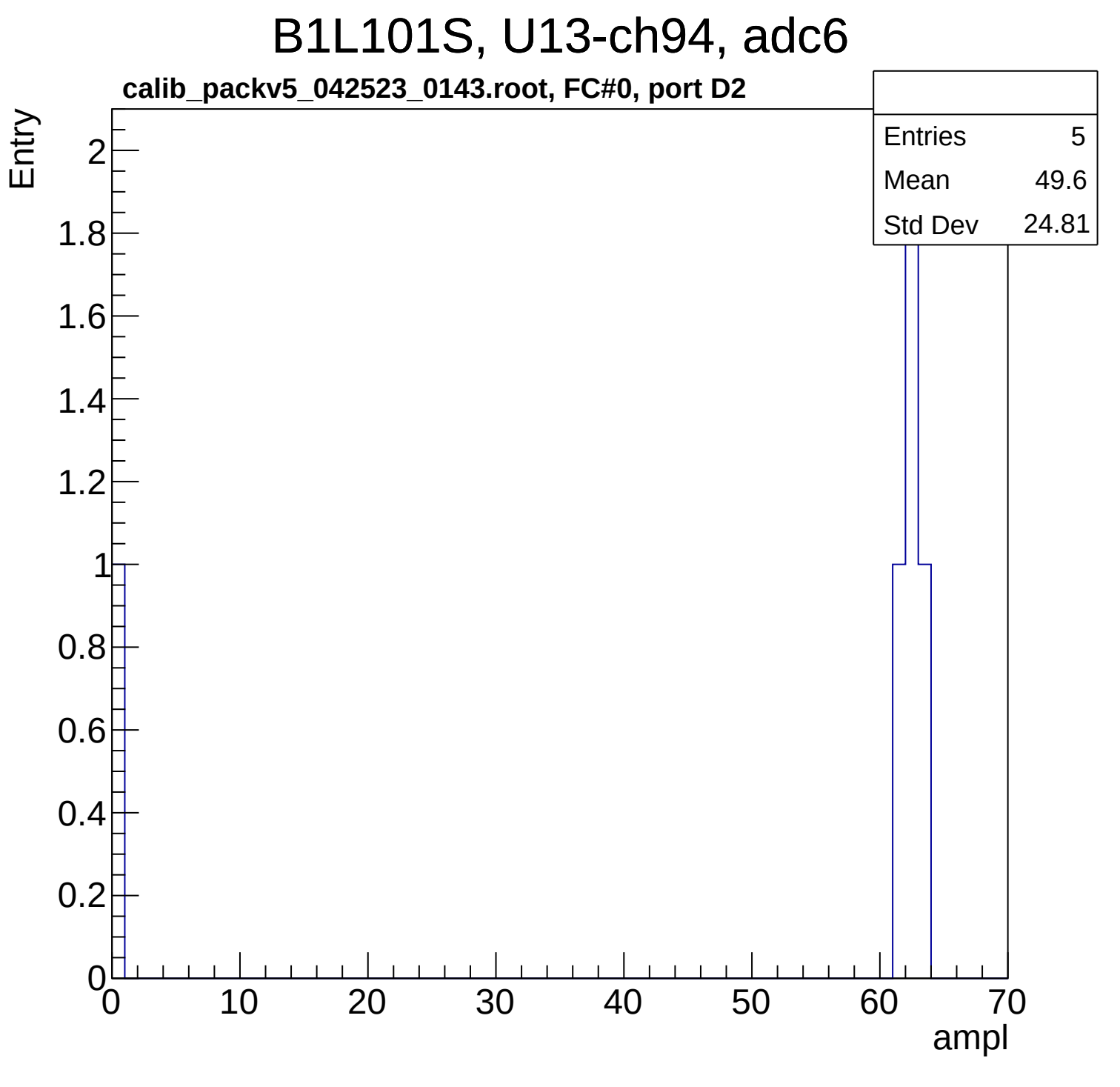

B1L101S, U13-ch94, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 37.25
Std Dev 3.441
Gaus mean : 37.8869
Gaus Width: 3.9660
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

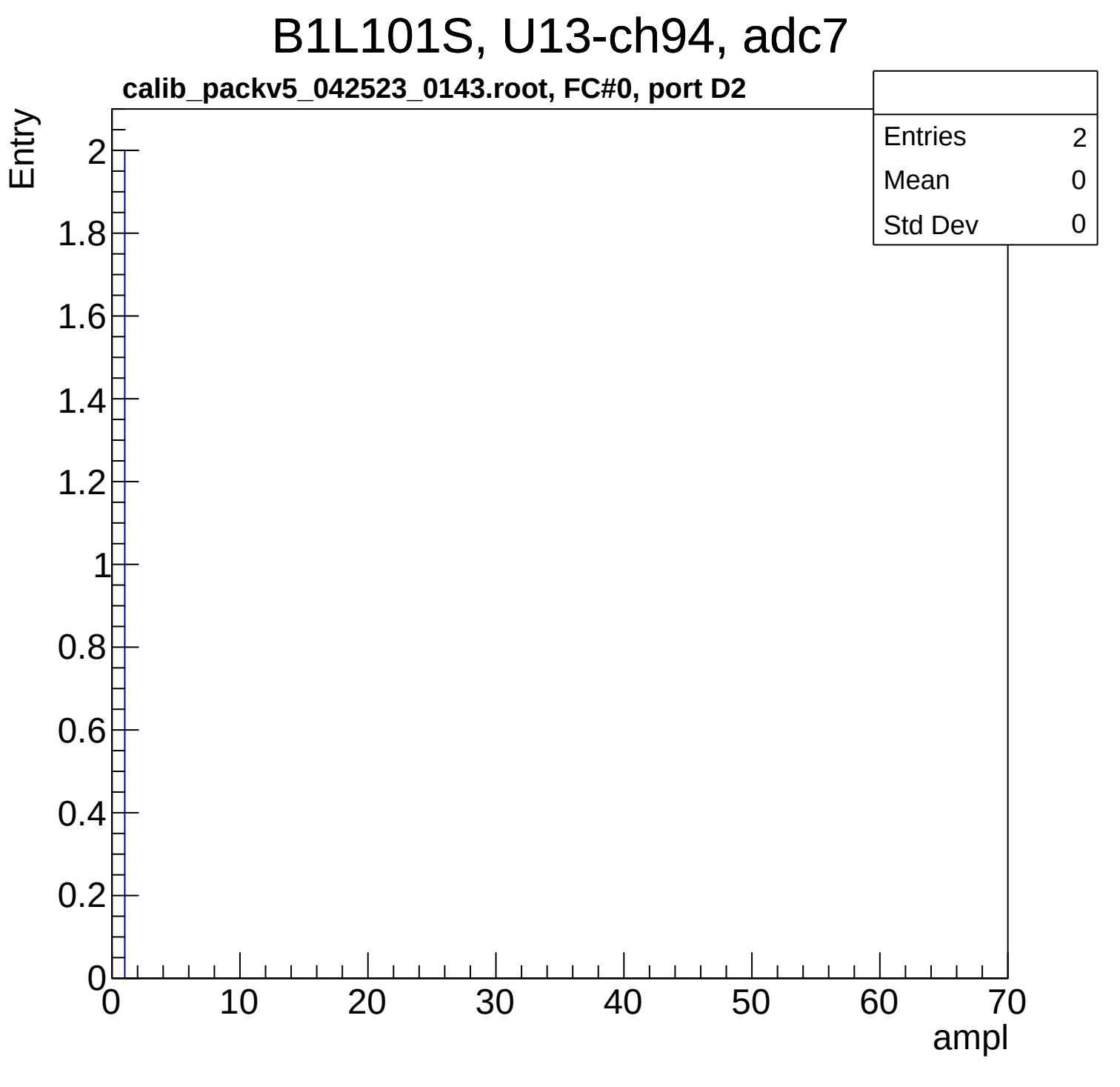

B1L101S, U13-ch94, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 44.49
Std Dev 3.469
Gaus mean : 44.1549
Gaus Width: 3.2428
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

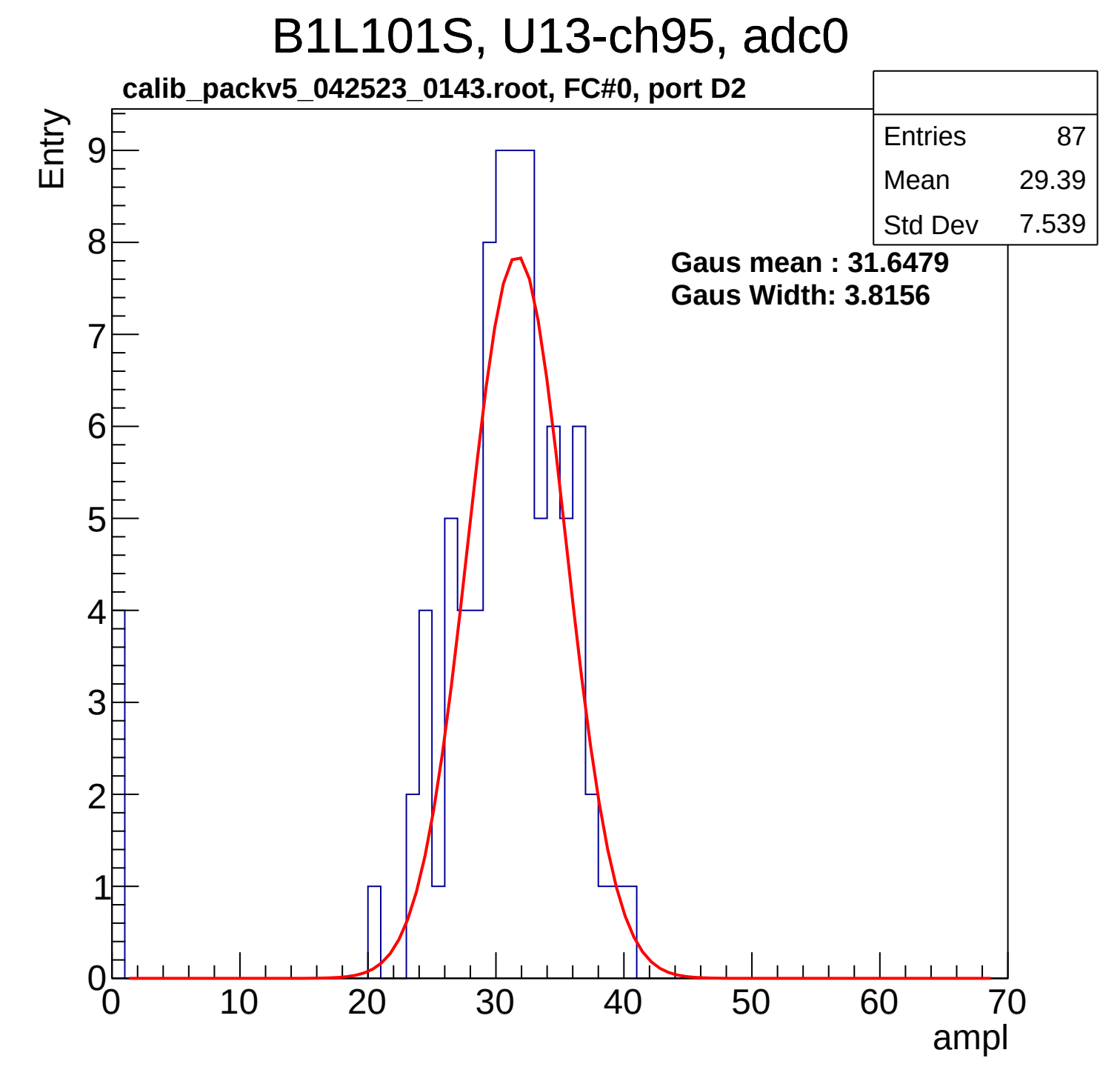

B1L101S, U13-ch94, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 52
Mean 50.65
Std Dev 2.993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

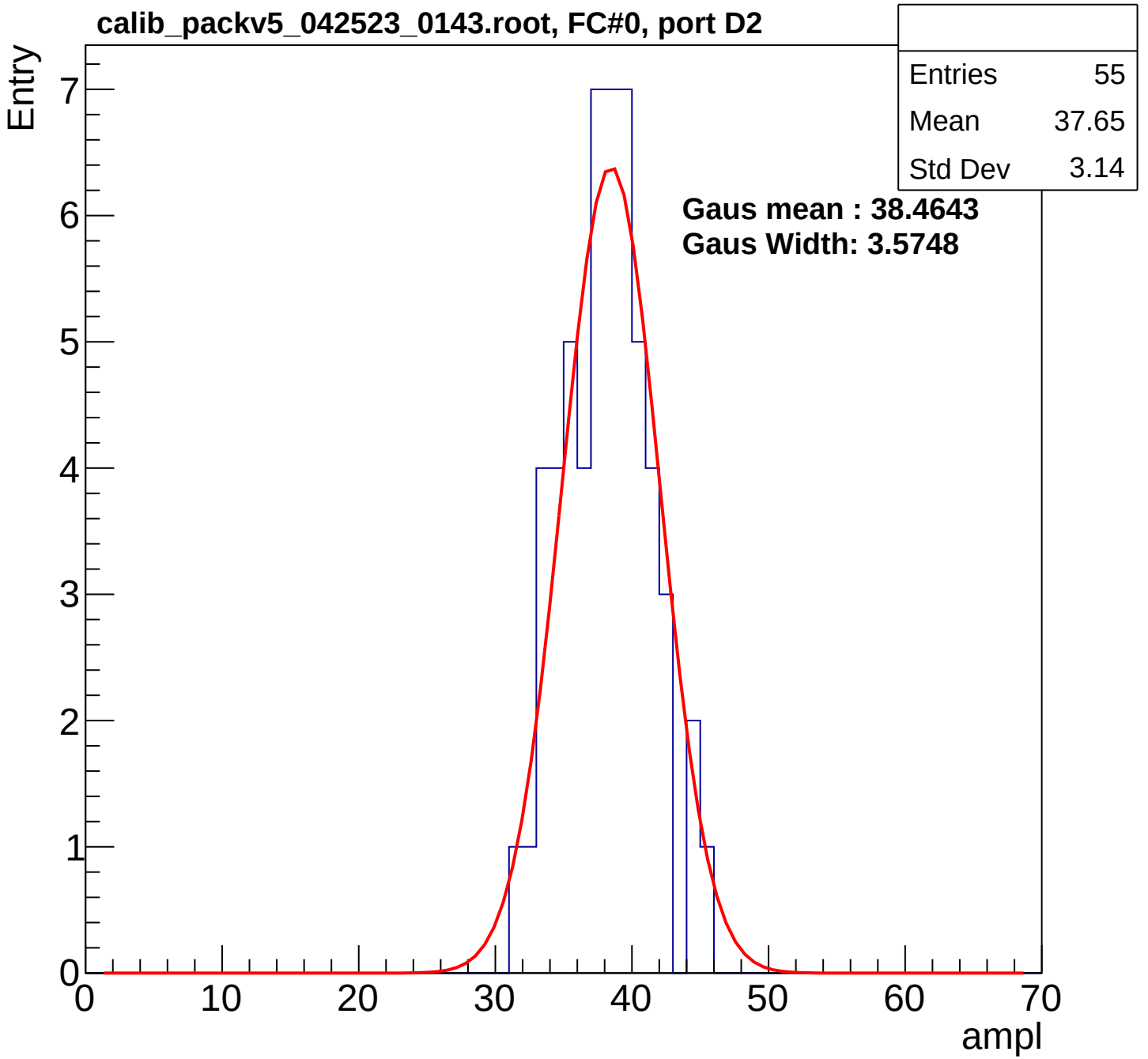

B1L101S, U13-ch94, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 56.02
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

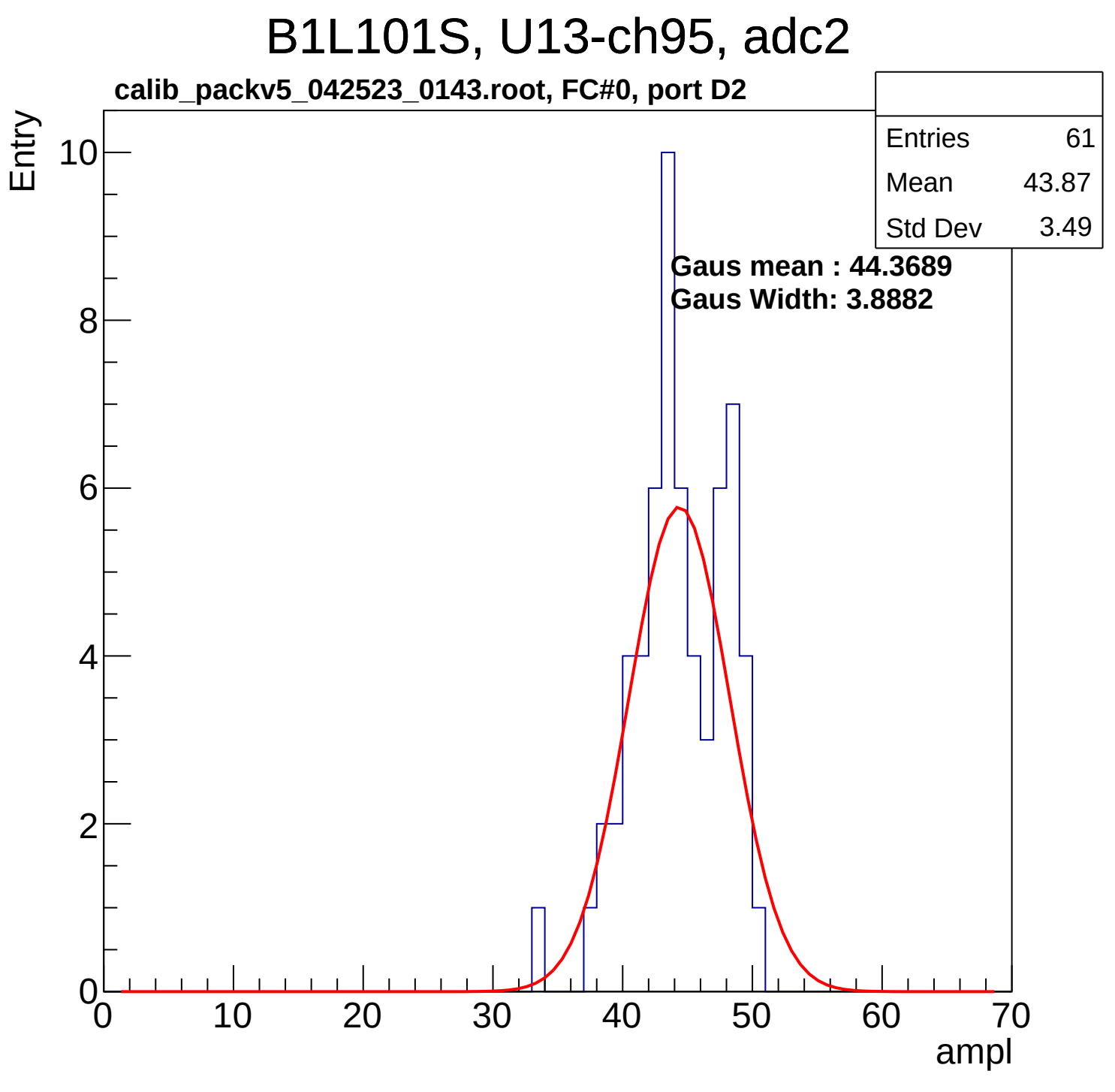

B1L101S, U13-ch94, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 38
Mean 60.61
Std Dev 1.843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

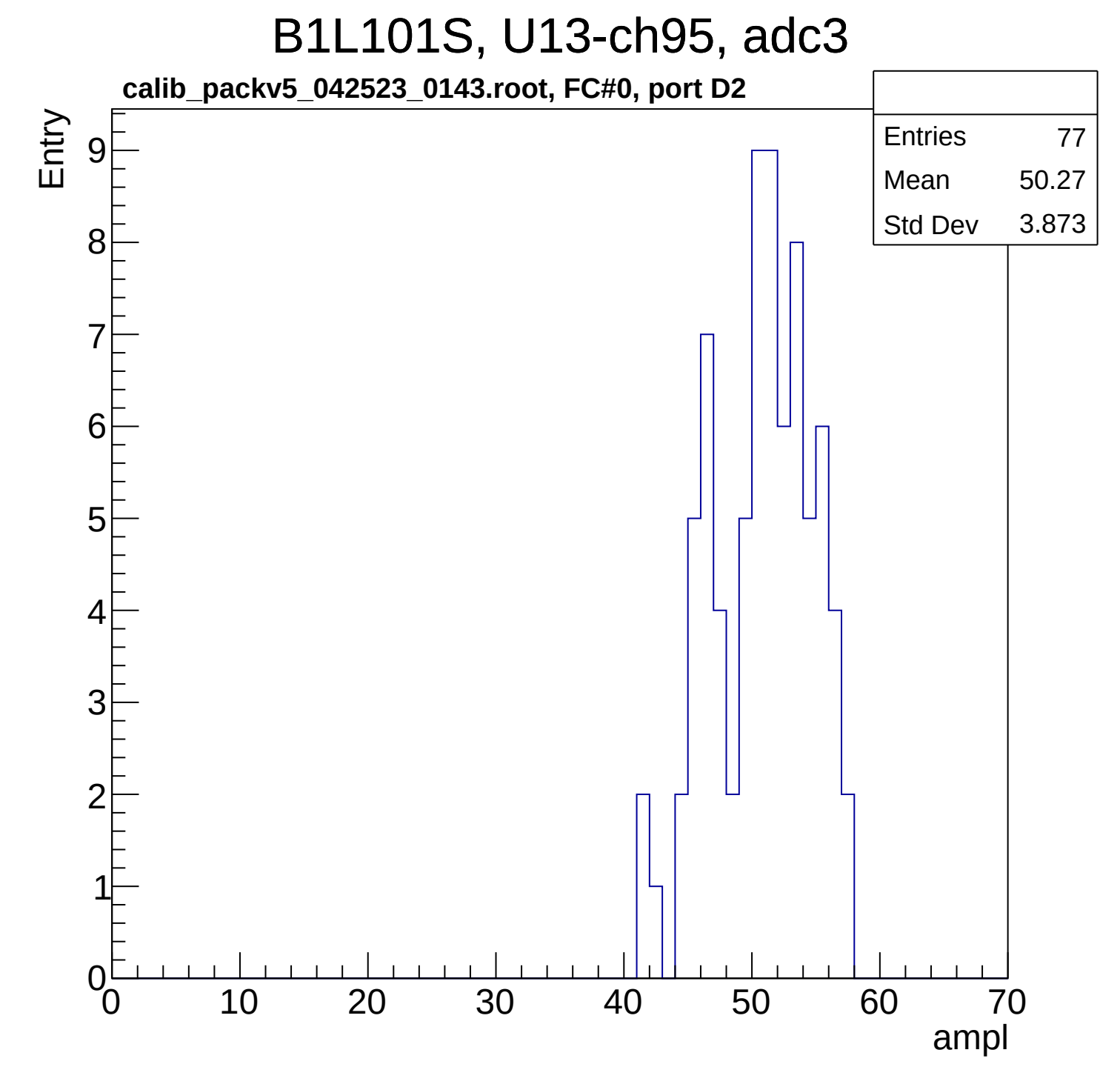

B1L101S, U13-ch94, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 49.6
Std Dev 24.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

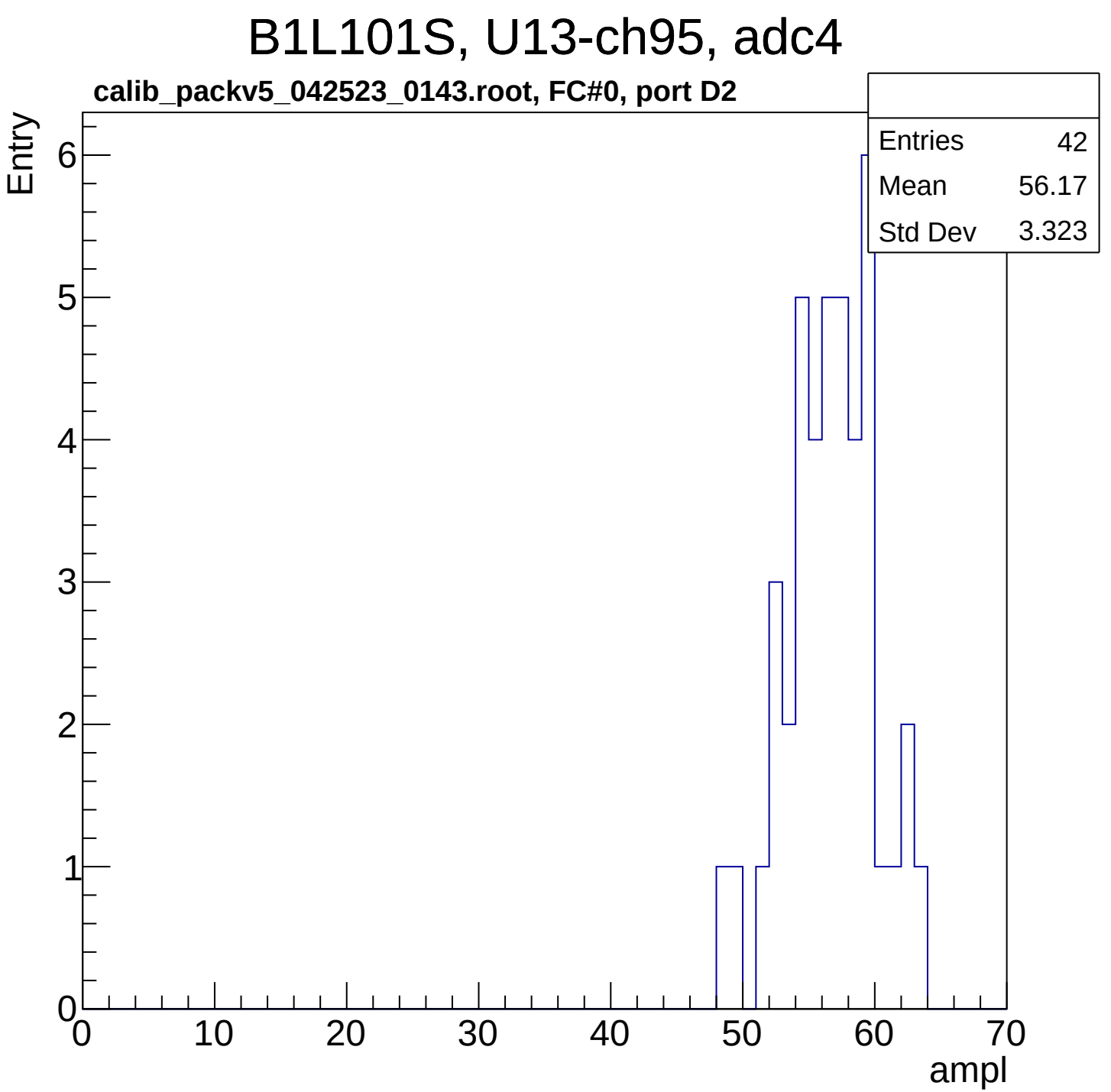

B1L101S, U13-ch94, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

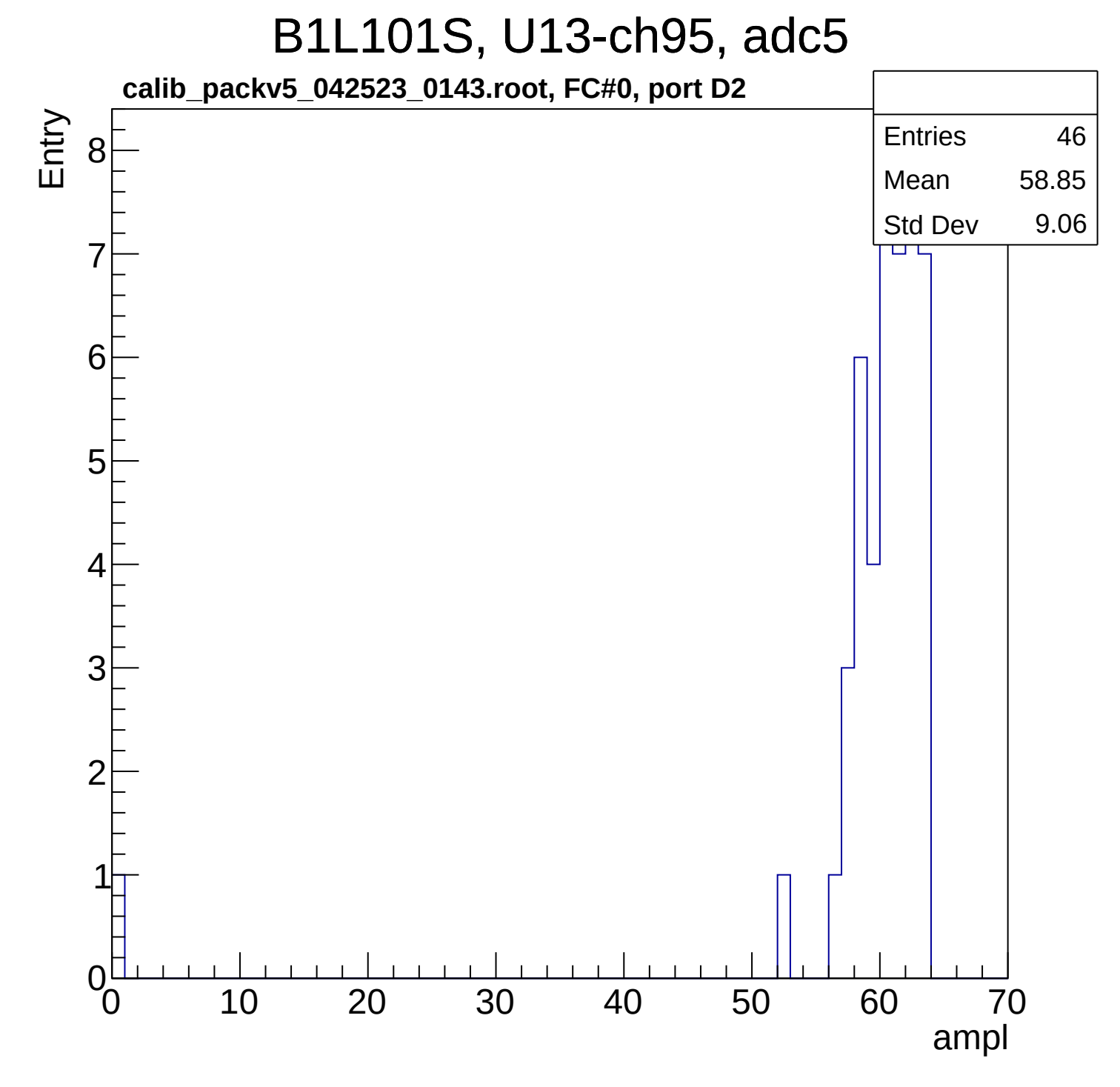

B1L101S, U13-ch95, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 87
Mean 29.39
Std Dev 7.539
Gaus mean : 31.6479
Gaus Width: 3.8156
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

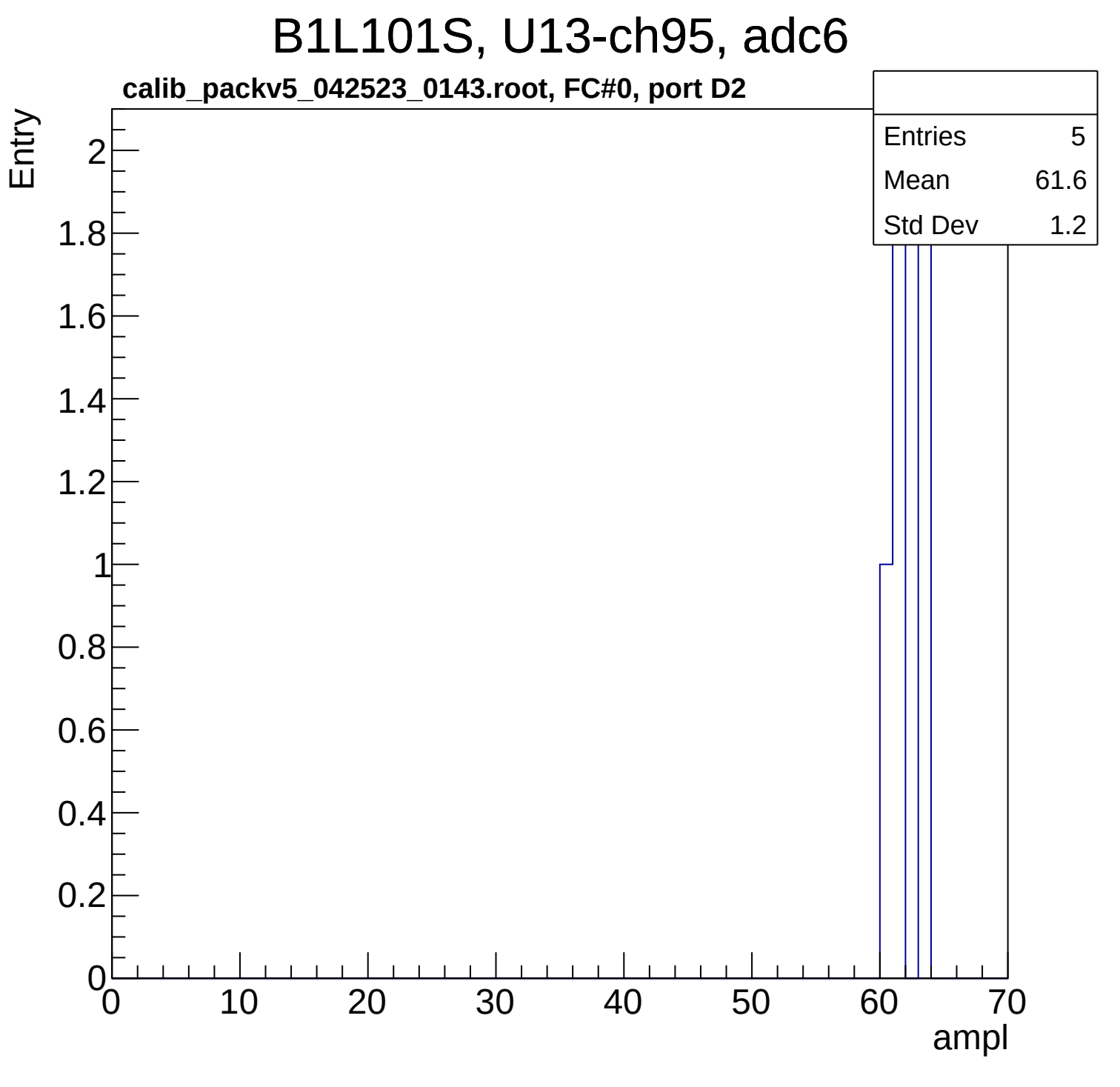

B1L101S, U13-ch95, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 37.65
Std Dev 3.14
Gaus mean : 38.4643
Gaus Width: 3.5748
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch95, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 43.87
Std Dev 3.49
Gaus mean : 44.3689
Gaus Width: 3.8882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch95, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 50.27
Std Dev 3.873
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch95, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 56.17
Std Dev 3.323
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch95, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 58.85
Std Dev 9.06
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch95, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 61.6
Std Dev 1.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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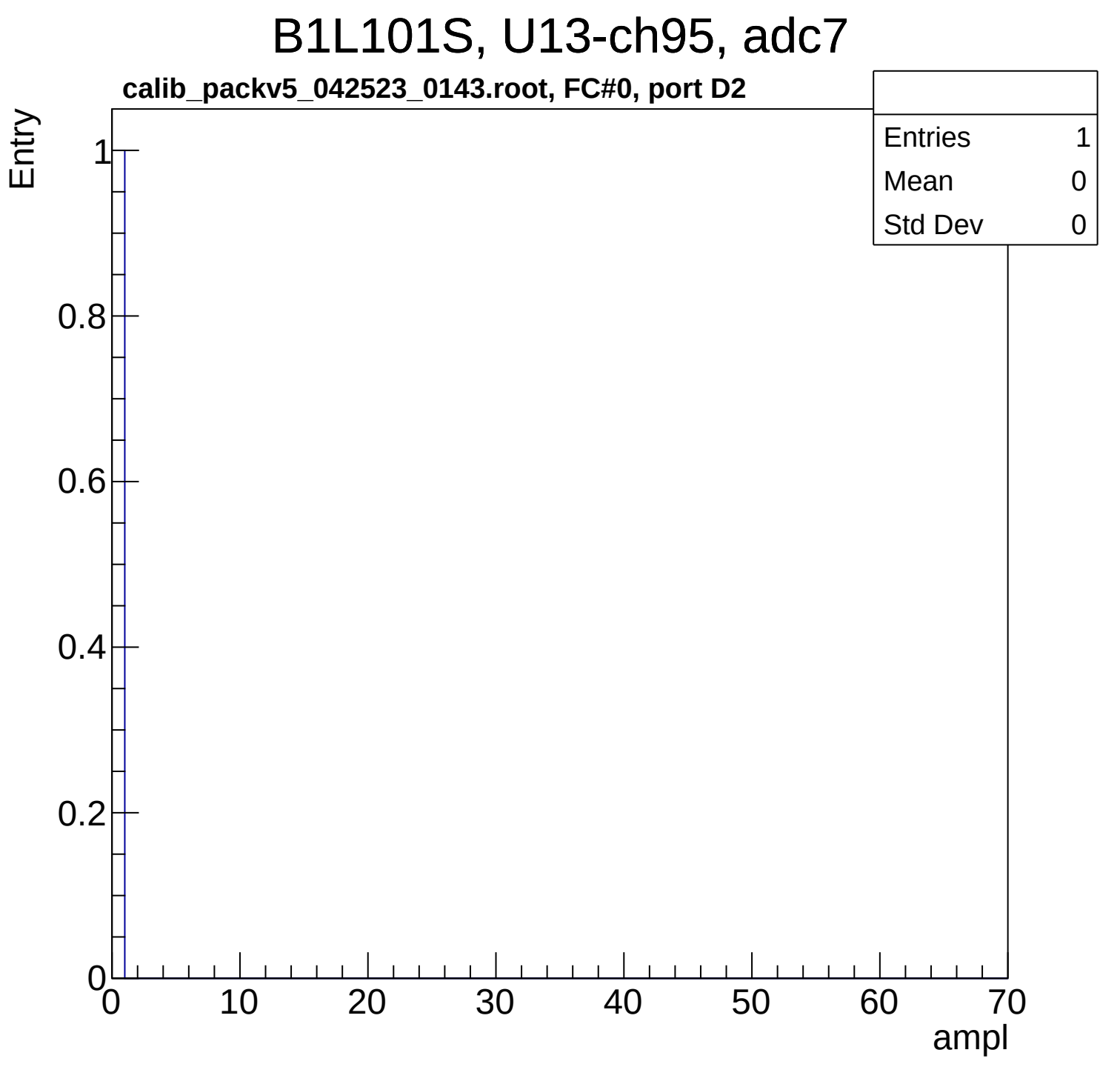
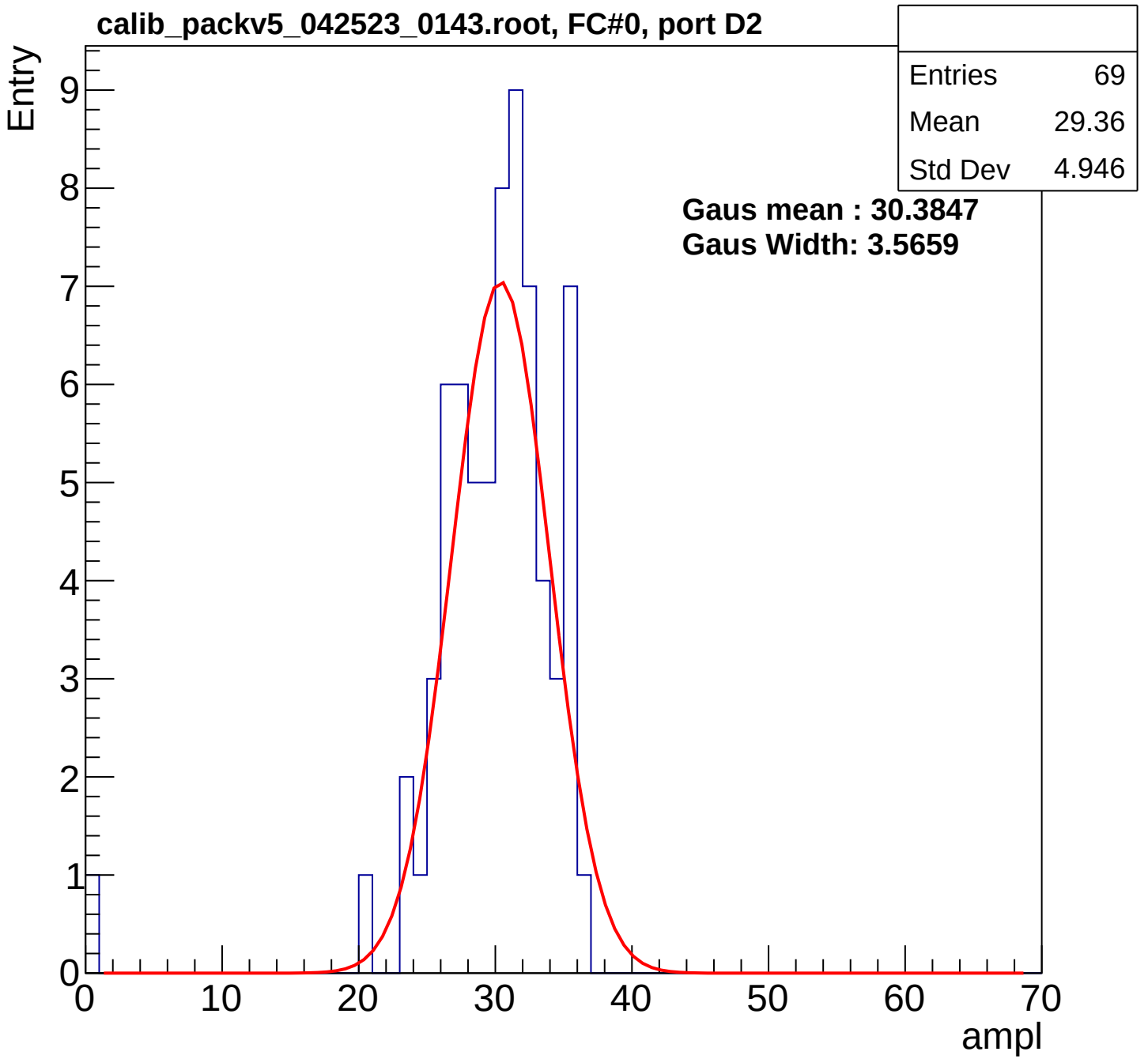
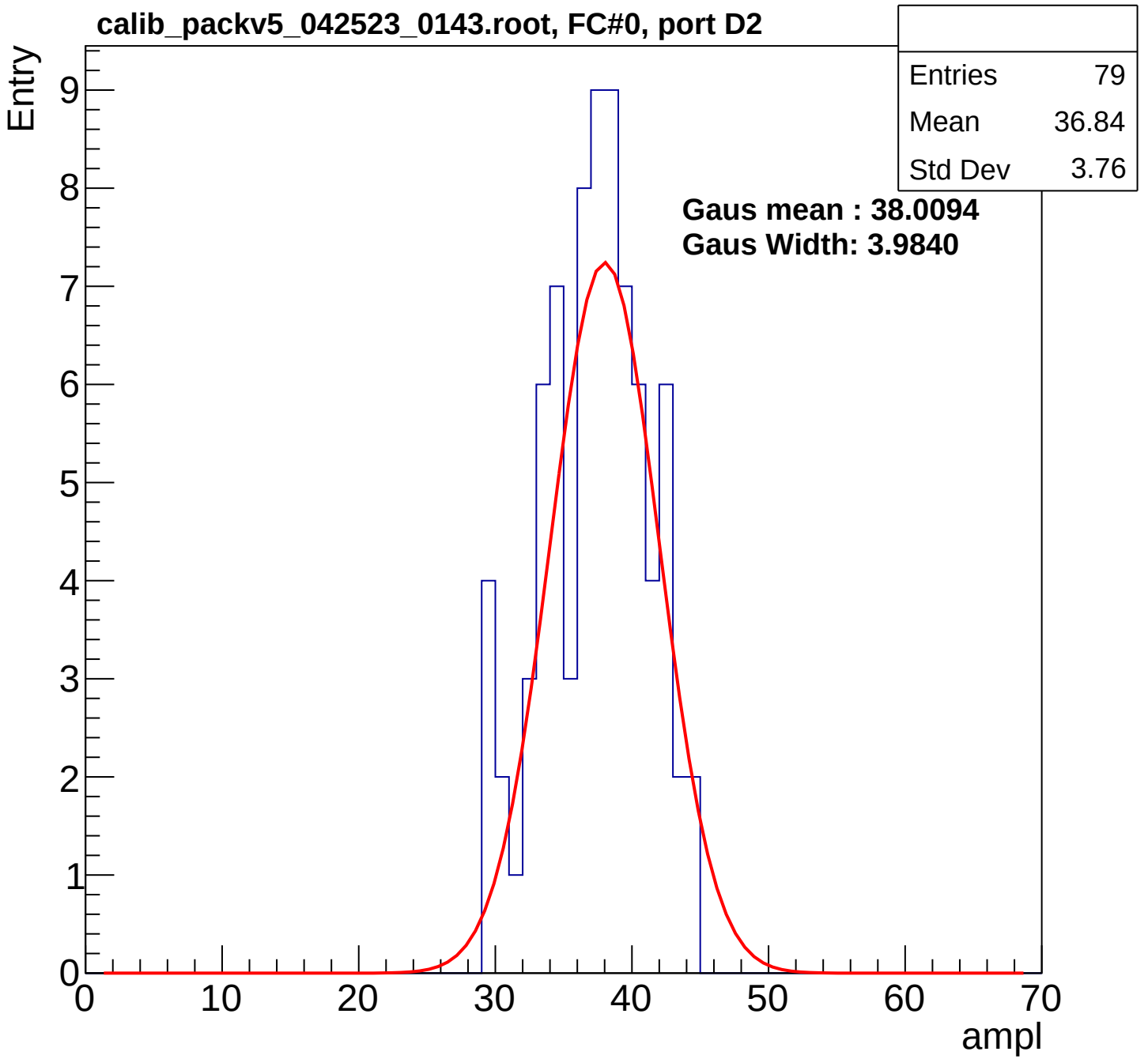
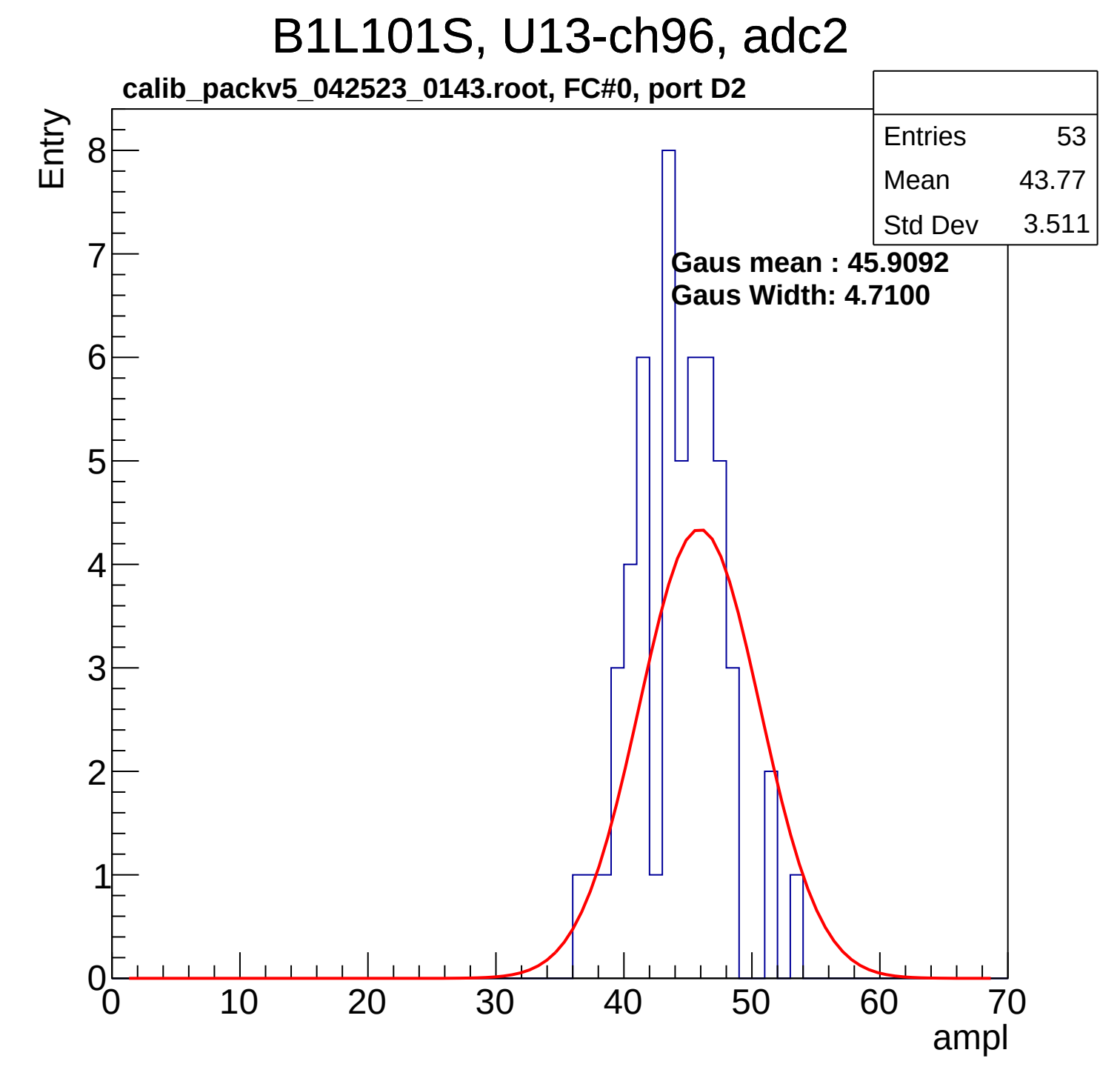
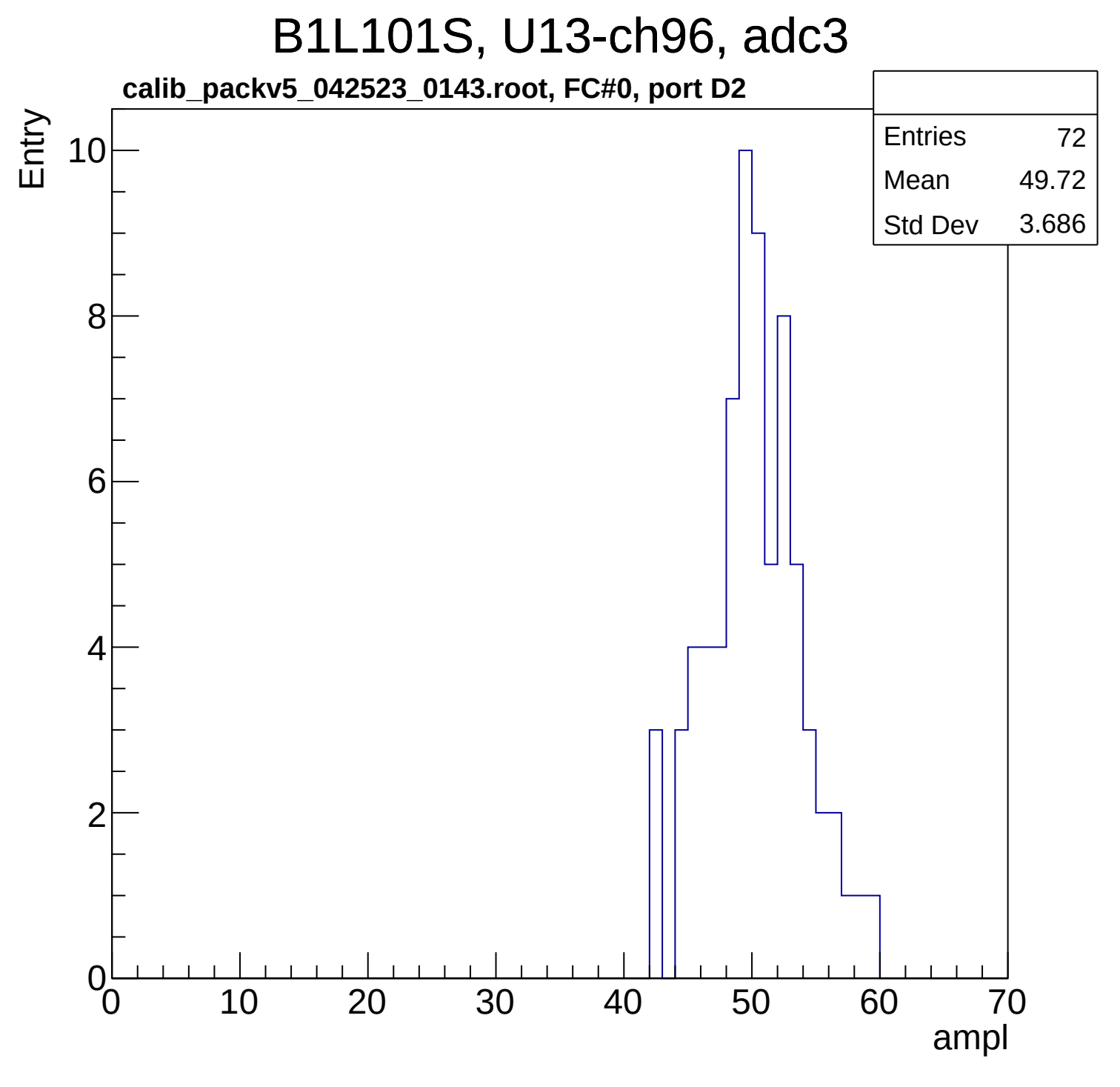

B1L101S, U13-ch95, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch96, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 29.36
Std Dev 4.946
Gaus mean : 30.3847
Gaus Width: 3.5659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch96, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 79
Mean 36.84
Std Dev 3.76
Gaus mean : 38.0094
Gaus Width: 3.9840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch96, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 43.77
Std Dev 3.511
Gaus mean : 45.9092
Gaus Width: 4.7100
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch96, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 49.72
Std Dev 3.686
Entry
ampl
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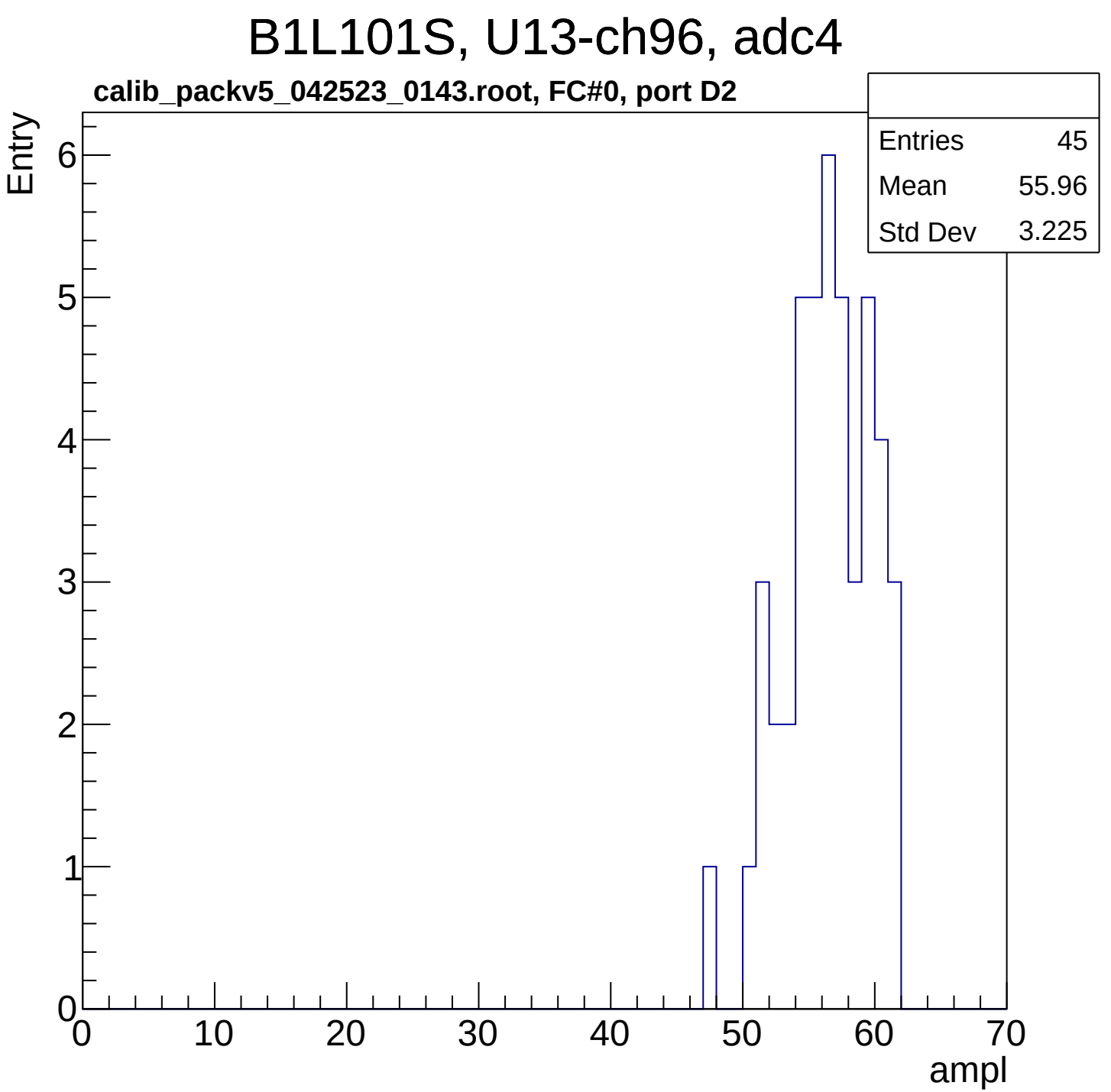
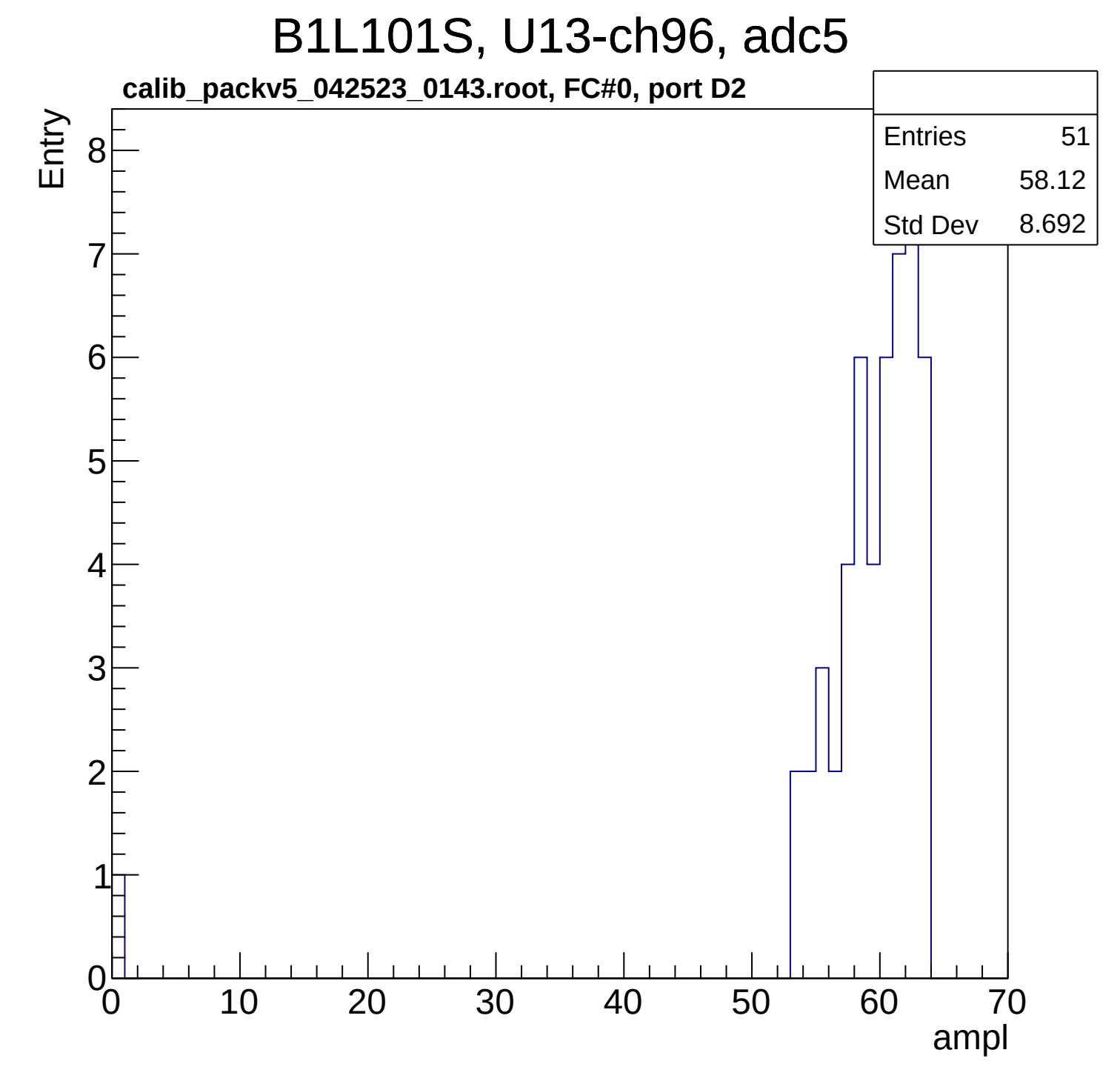
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch96, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 55.96
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch96, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 58.12
Std Dev 8.692
Entry
ampl
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1
2
3
4
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7
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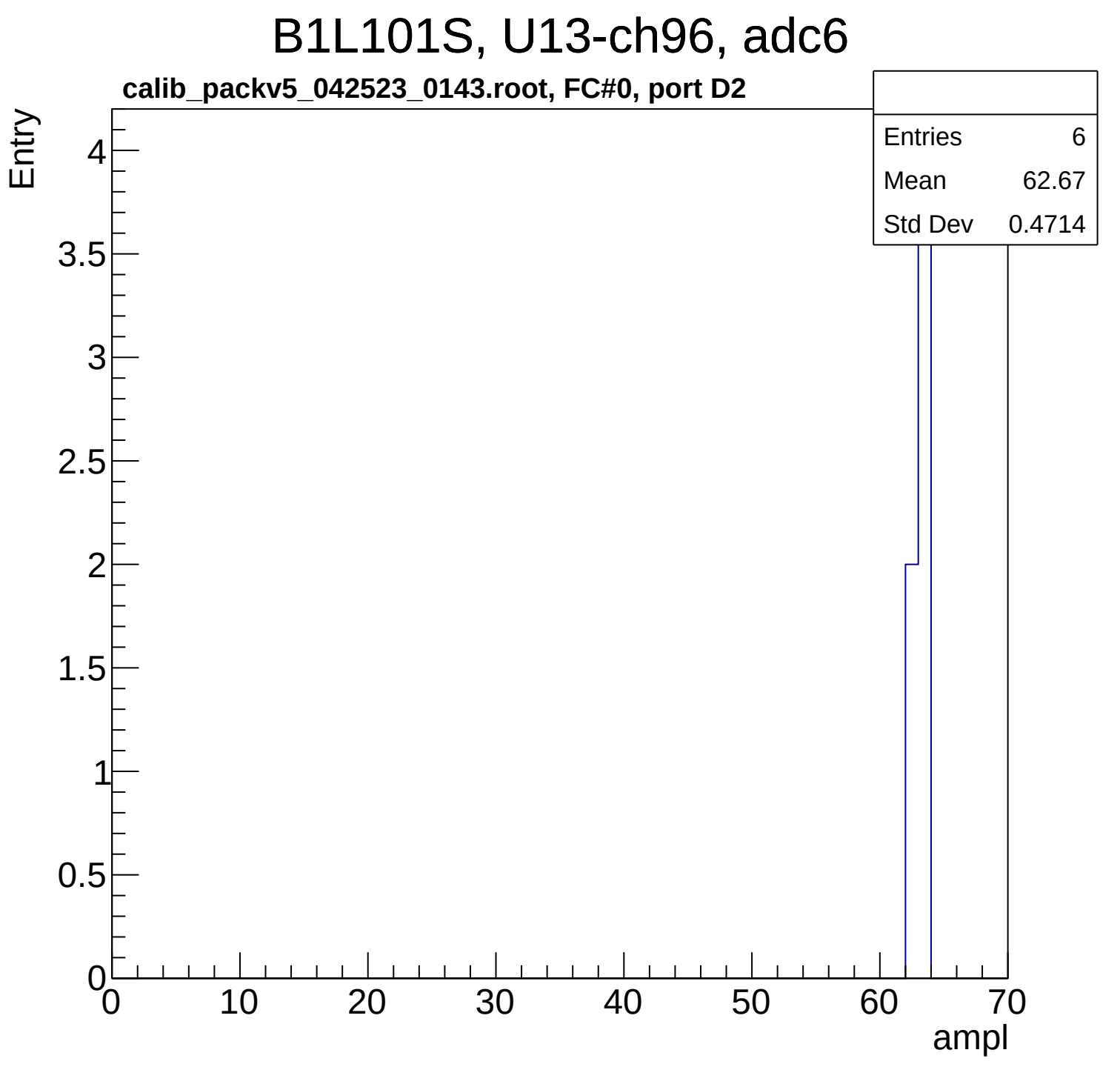
B1L101S, U13-ch96, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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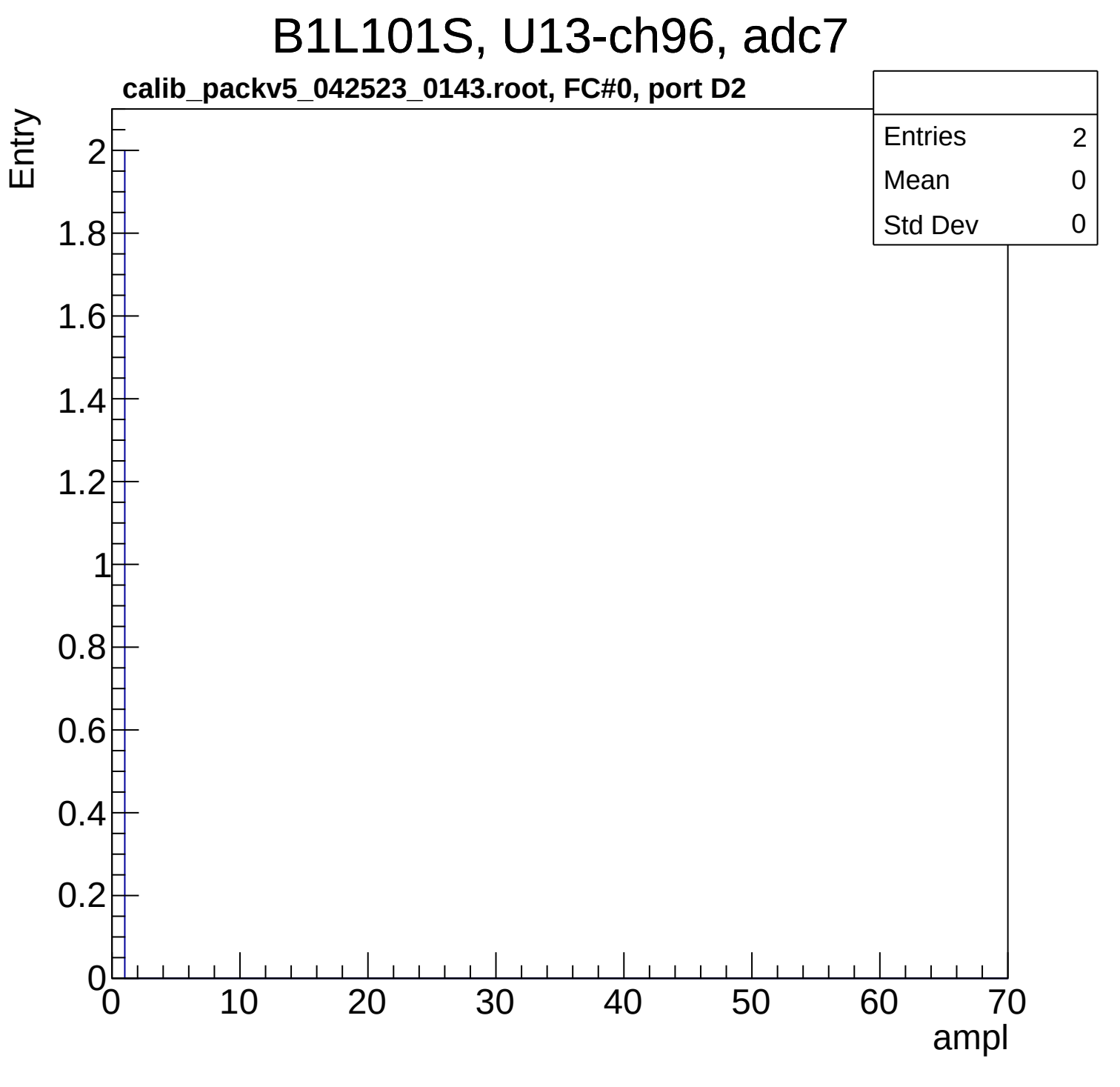
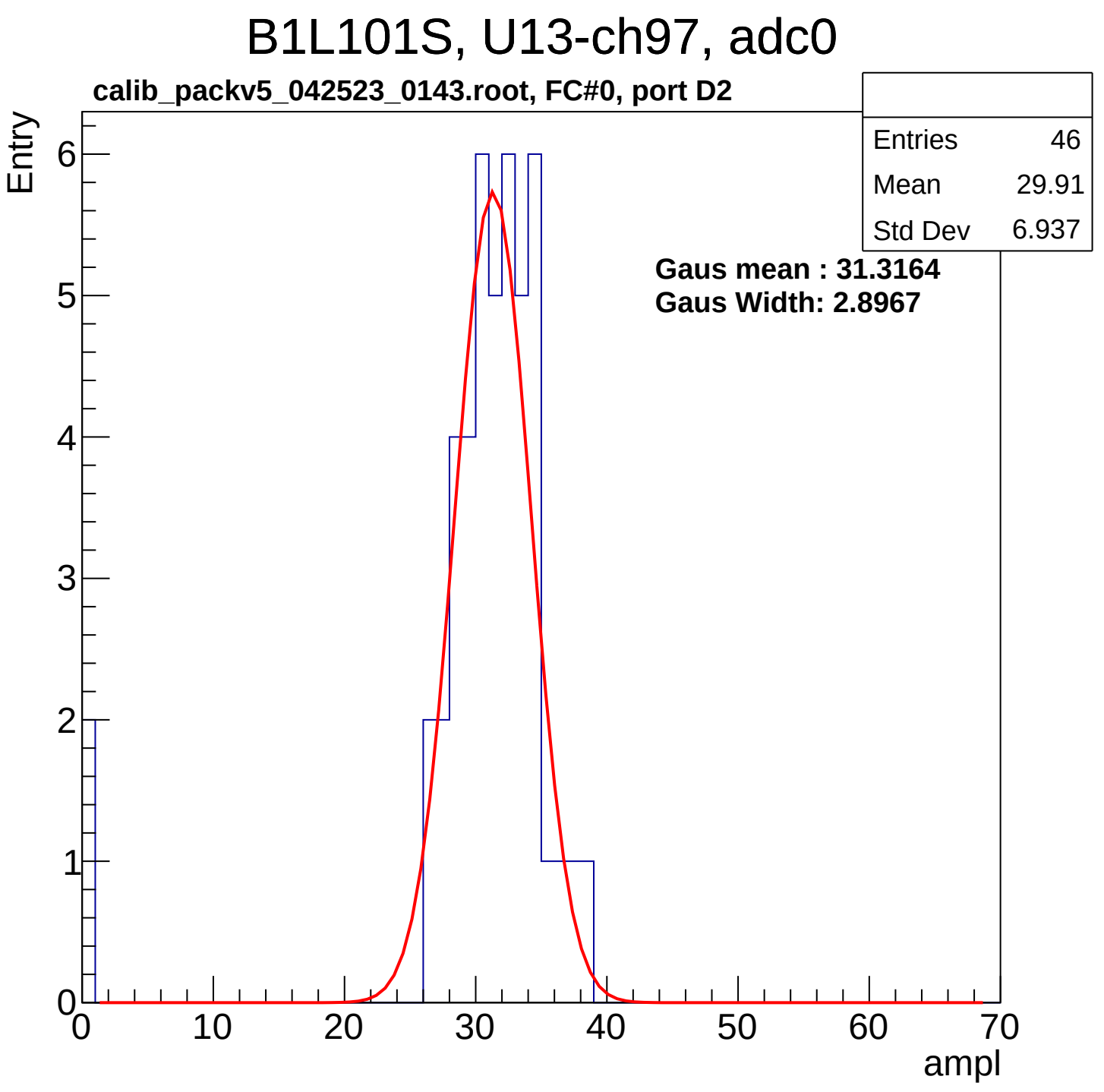
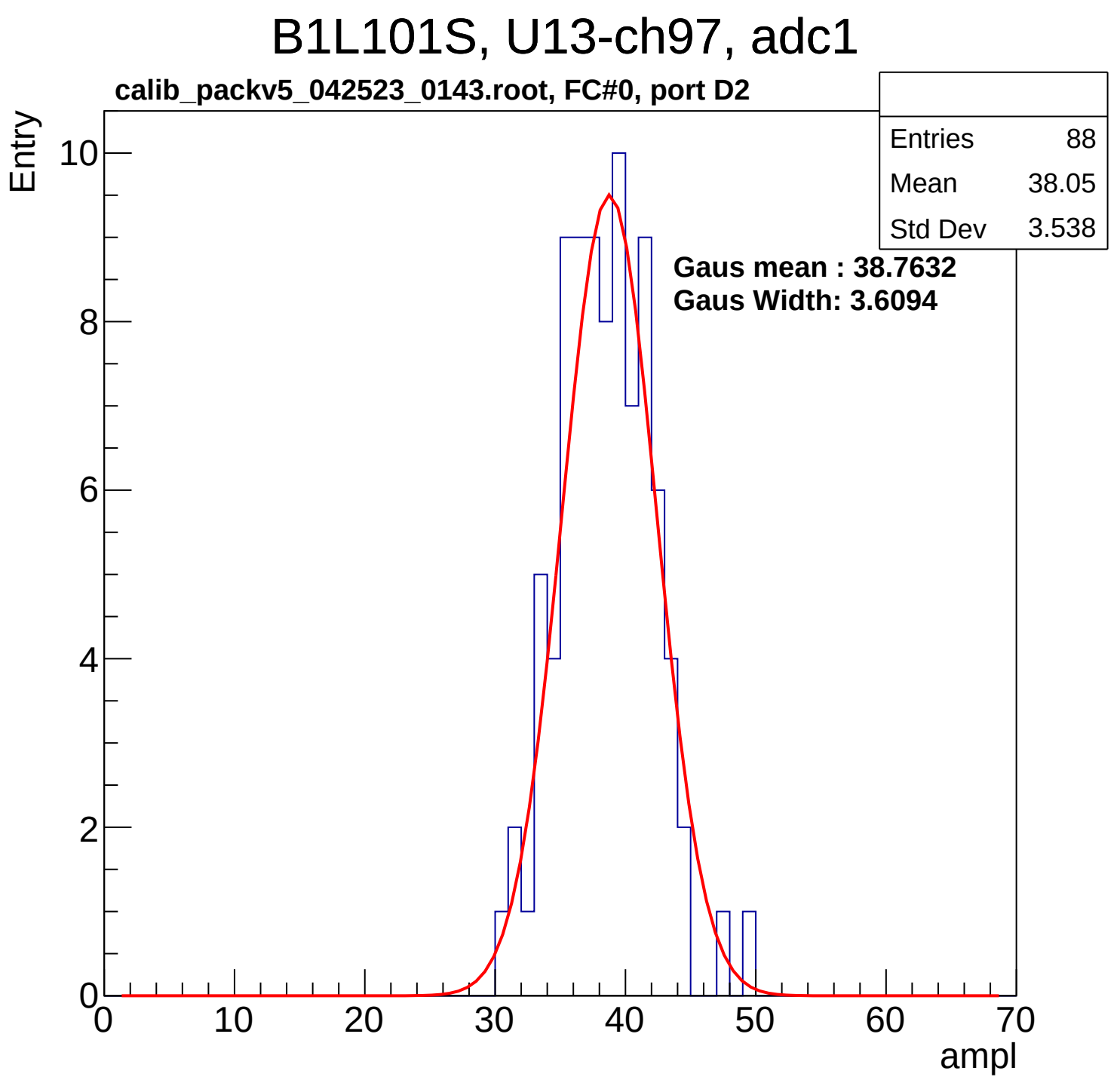

B1L101S, U13-ch96, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch97, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 29.91
Std Dev 6.937
Gaus mean : 31.3164
Gaus Width: 2.8967
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch97, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 88
Mean 38.05
Std Dev 3.538
Gaus mean : 38.7632
Gaus Width: 3.6094
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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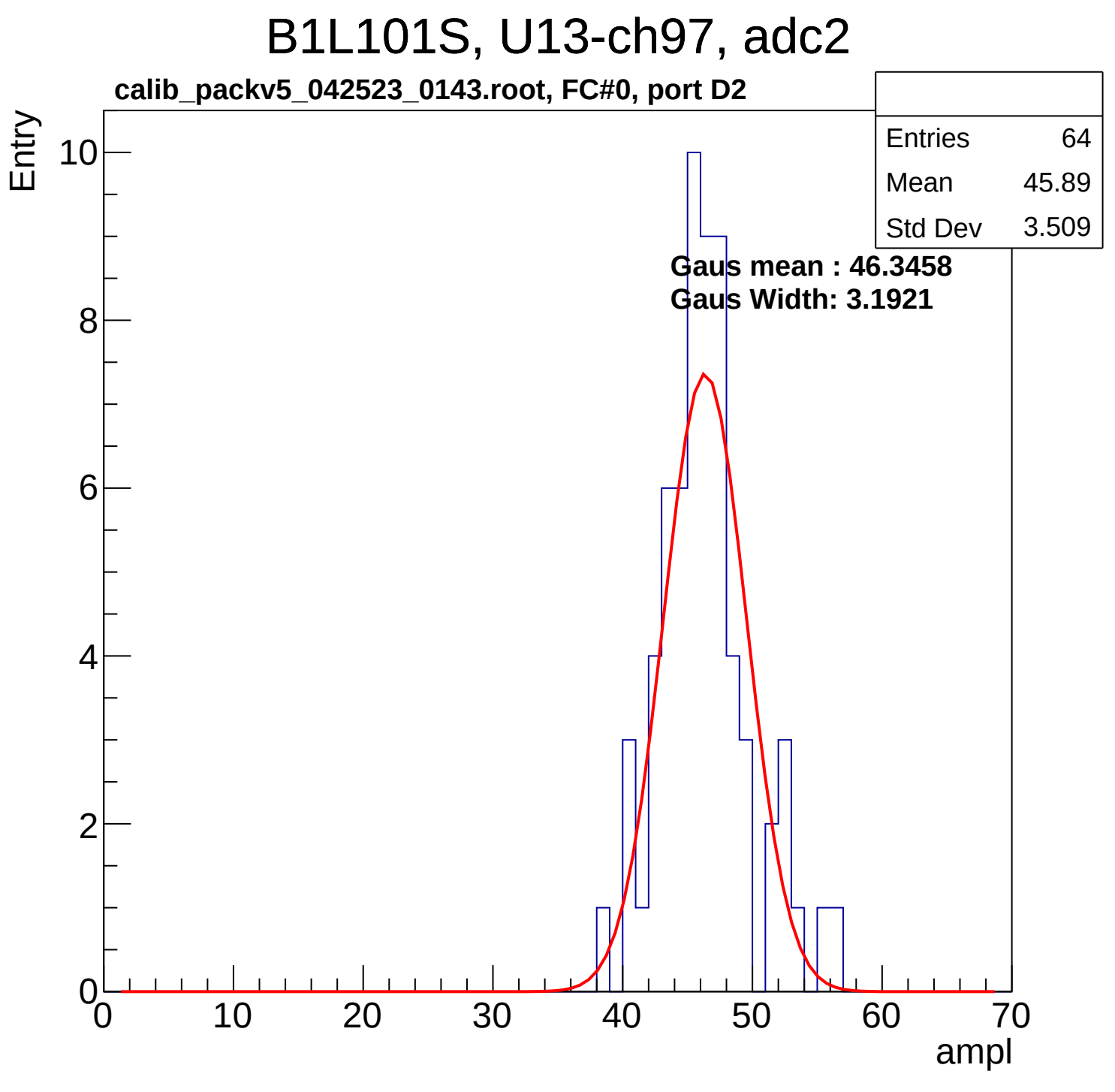
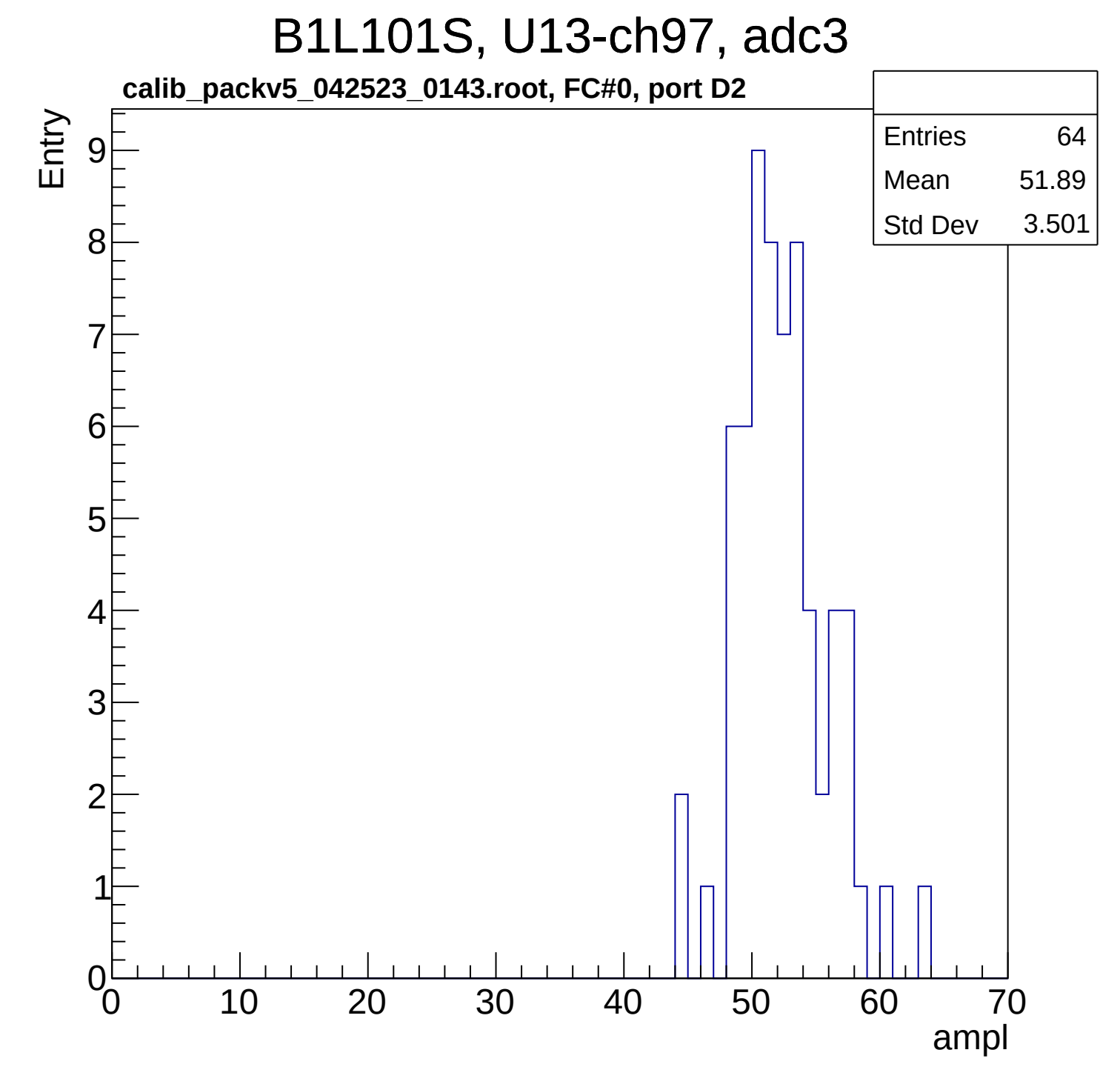

B1L101S, U13-ch97, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 45.89
Std Dev 3.509
Gaus mean : 46.3458
Gaus Width: 3.1921
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch97, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 51.89
Std Dev 3.501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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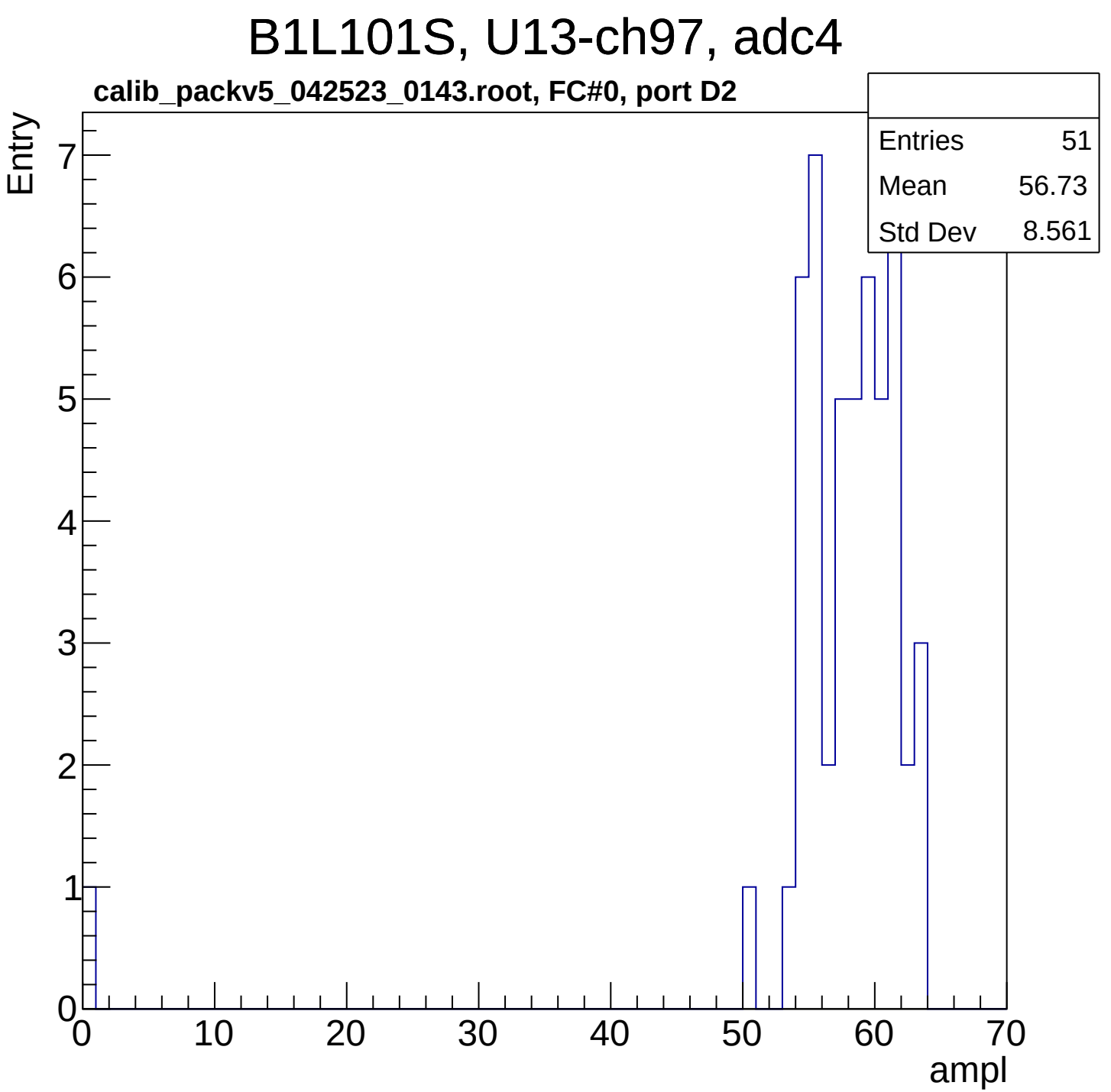
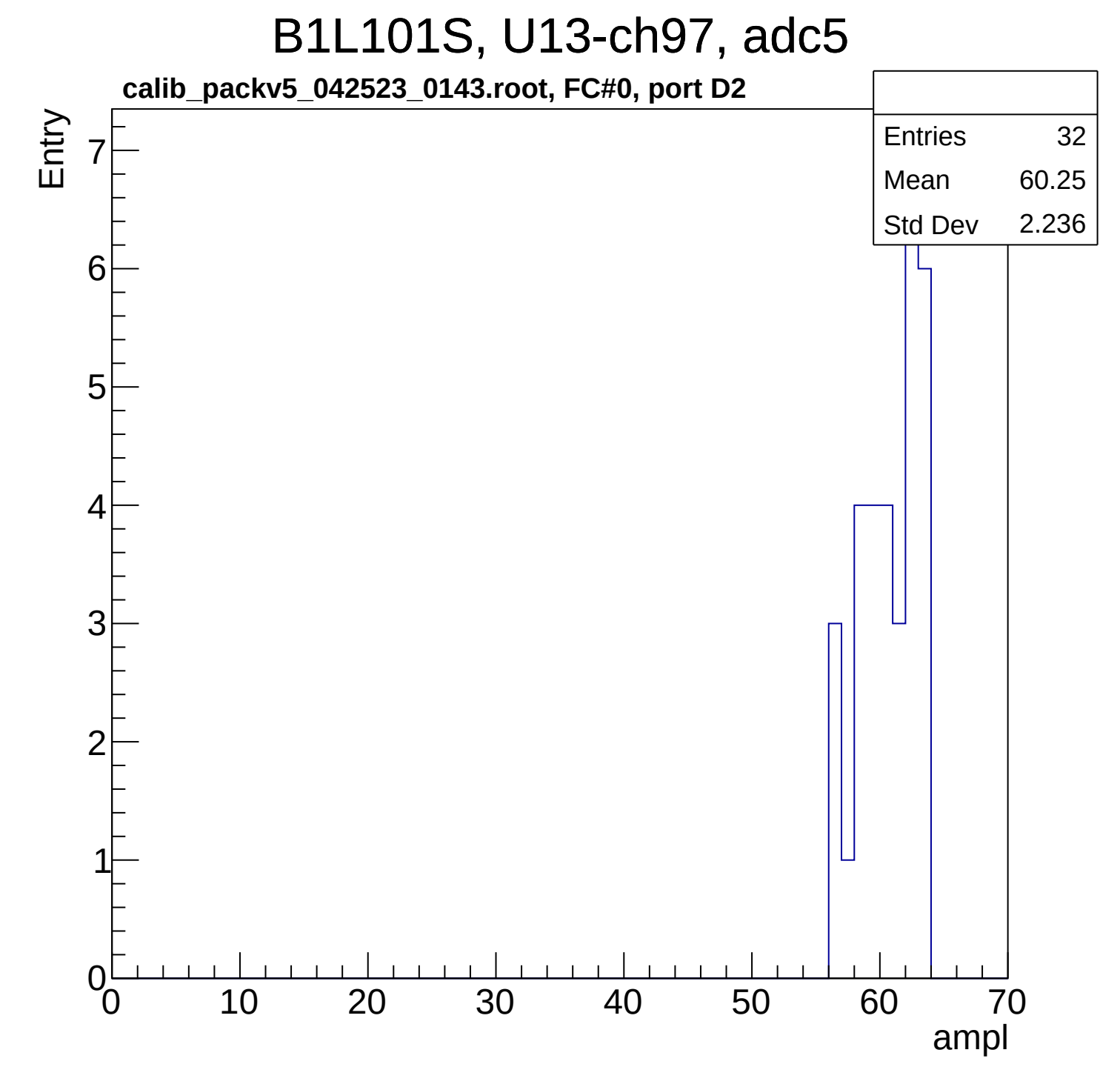
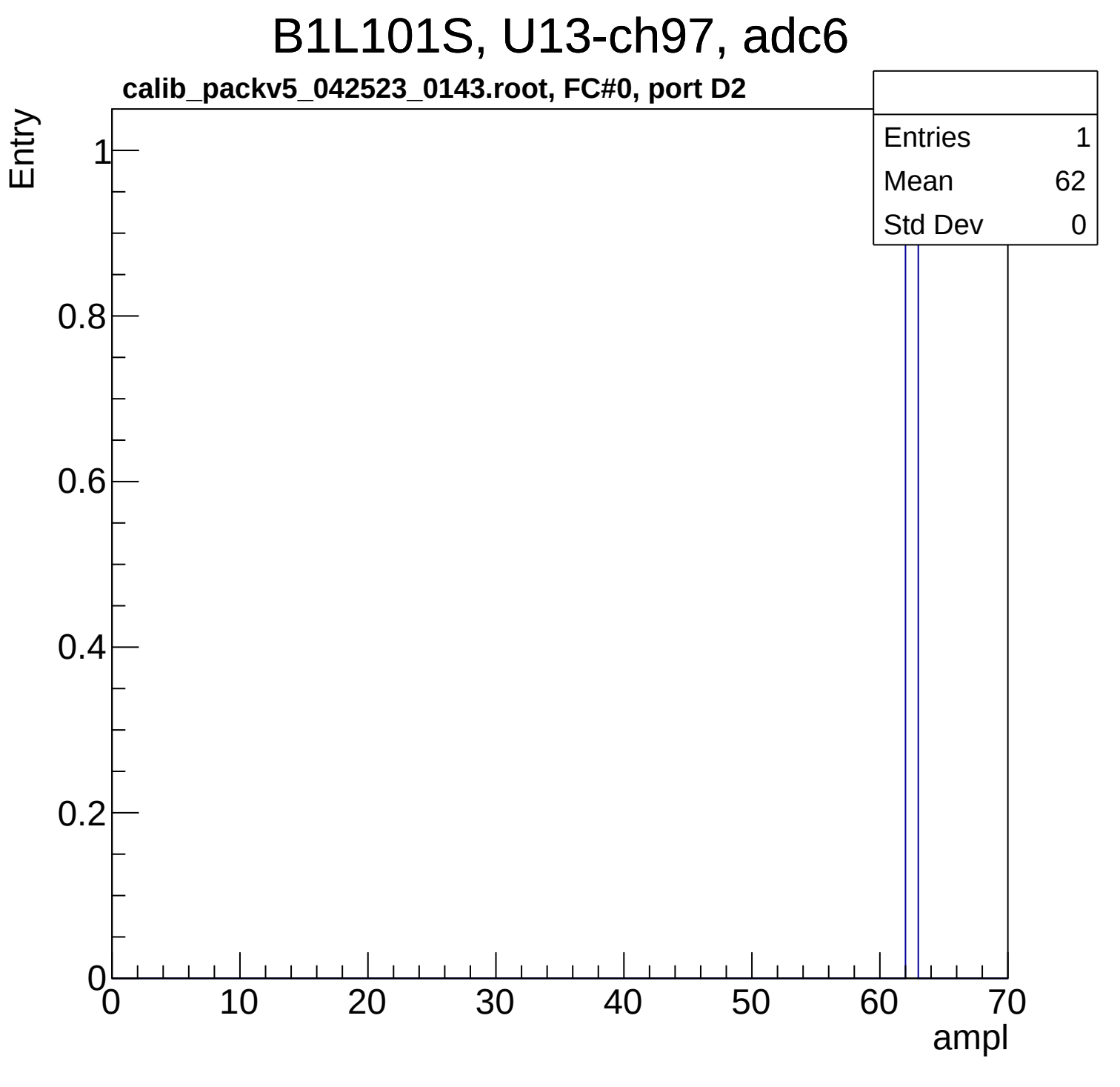
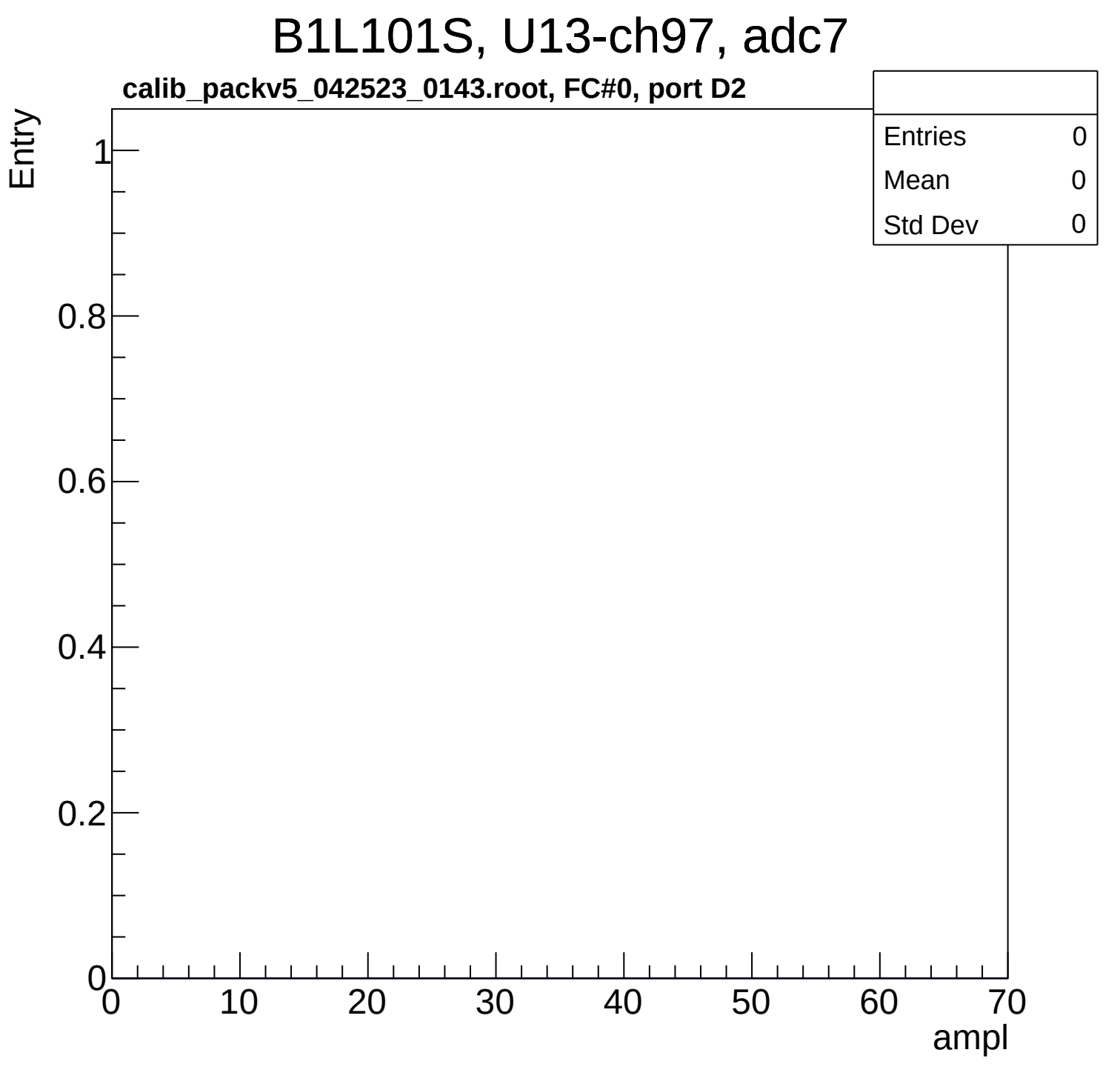
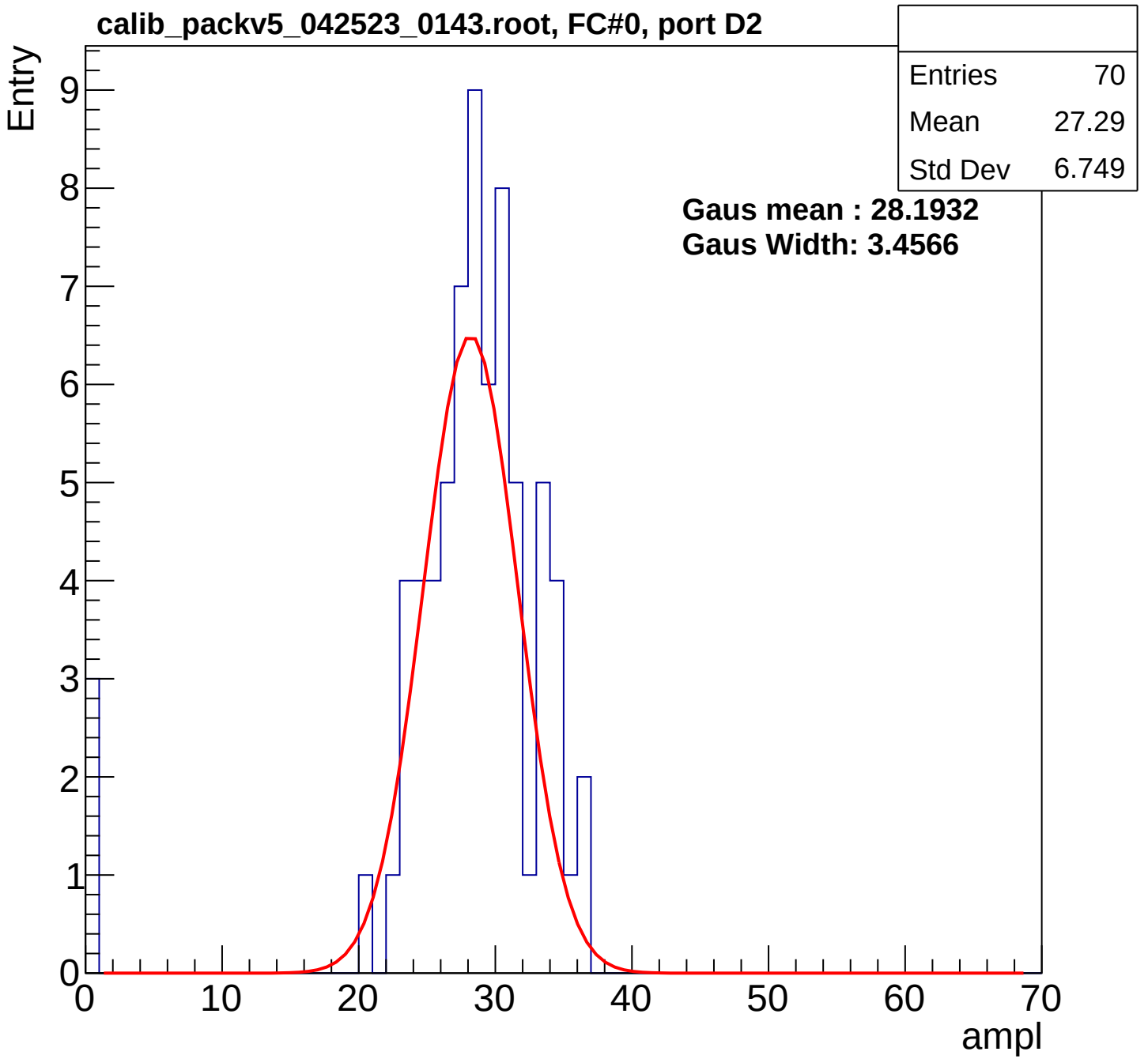
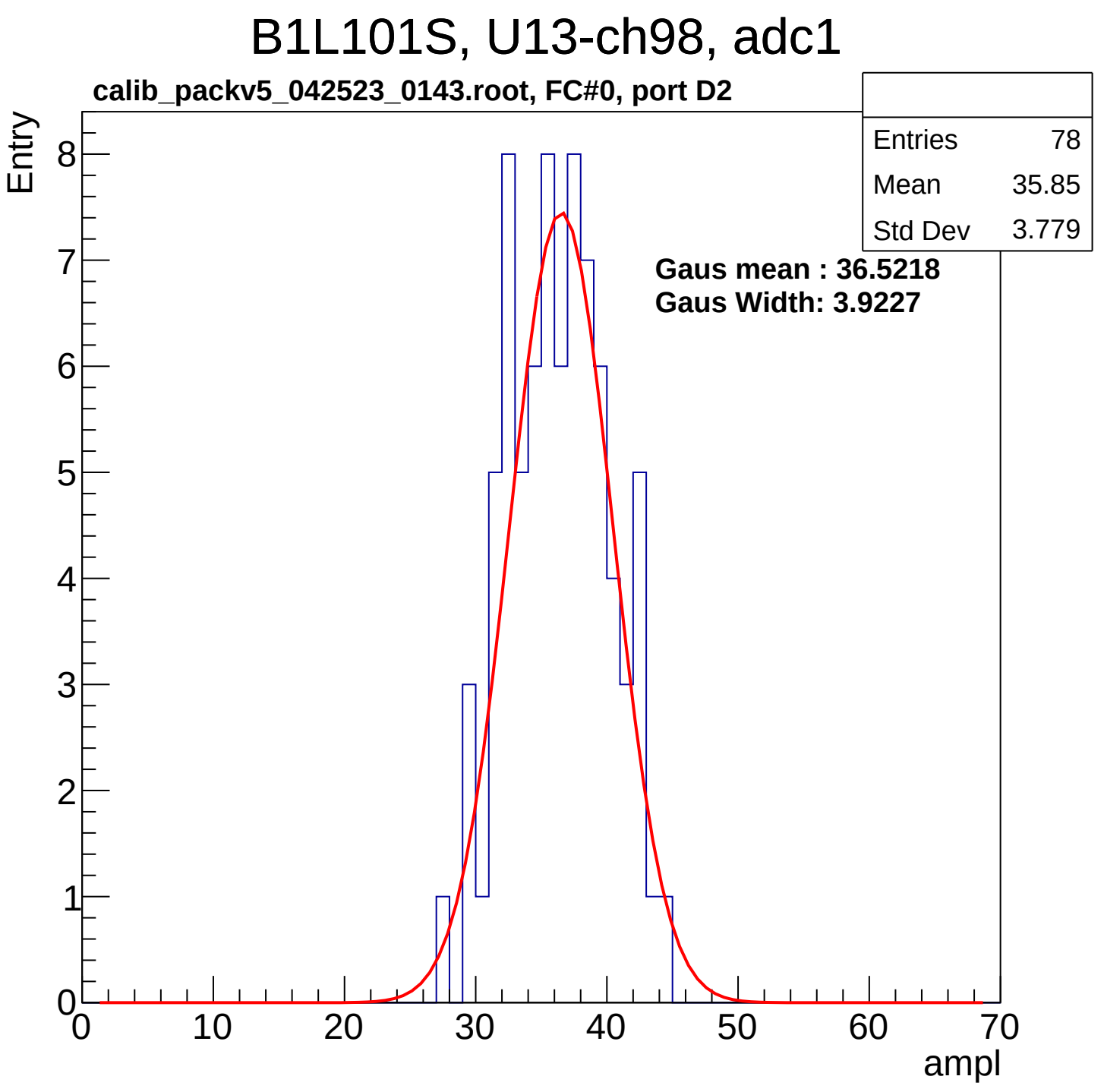

B1L101S, U13-ch97, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 56.73
Std Dev 8.561
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

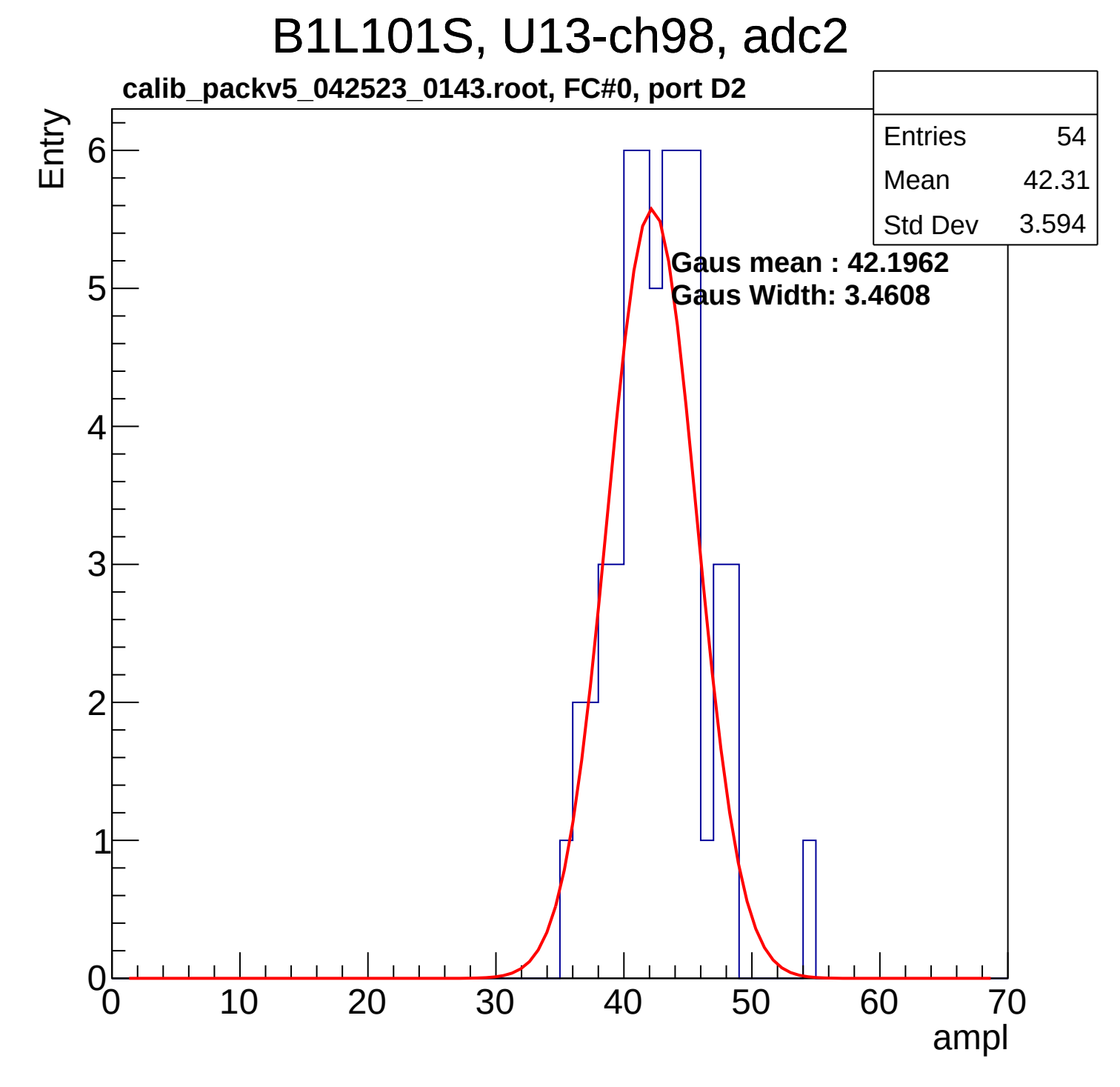

B1L101S, U13-ch97, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 32
Mean 60.25
Std Dev 2.236
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch97, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch97, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch98, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 27.29
Std Dev 6.749
Gaus mean : 28.1932
Gaus Width: 3.4566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch98, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 35.85
Std Dev 3.779
Gaus mean : 36.5218
Gaus Width: 3.9227
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch98, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 42.31
Std Dev 3.594
Gaus mean : 42.1962
Gaus Width: 3.4608
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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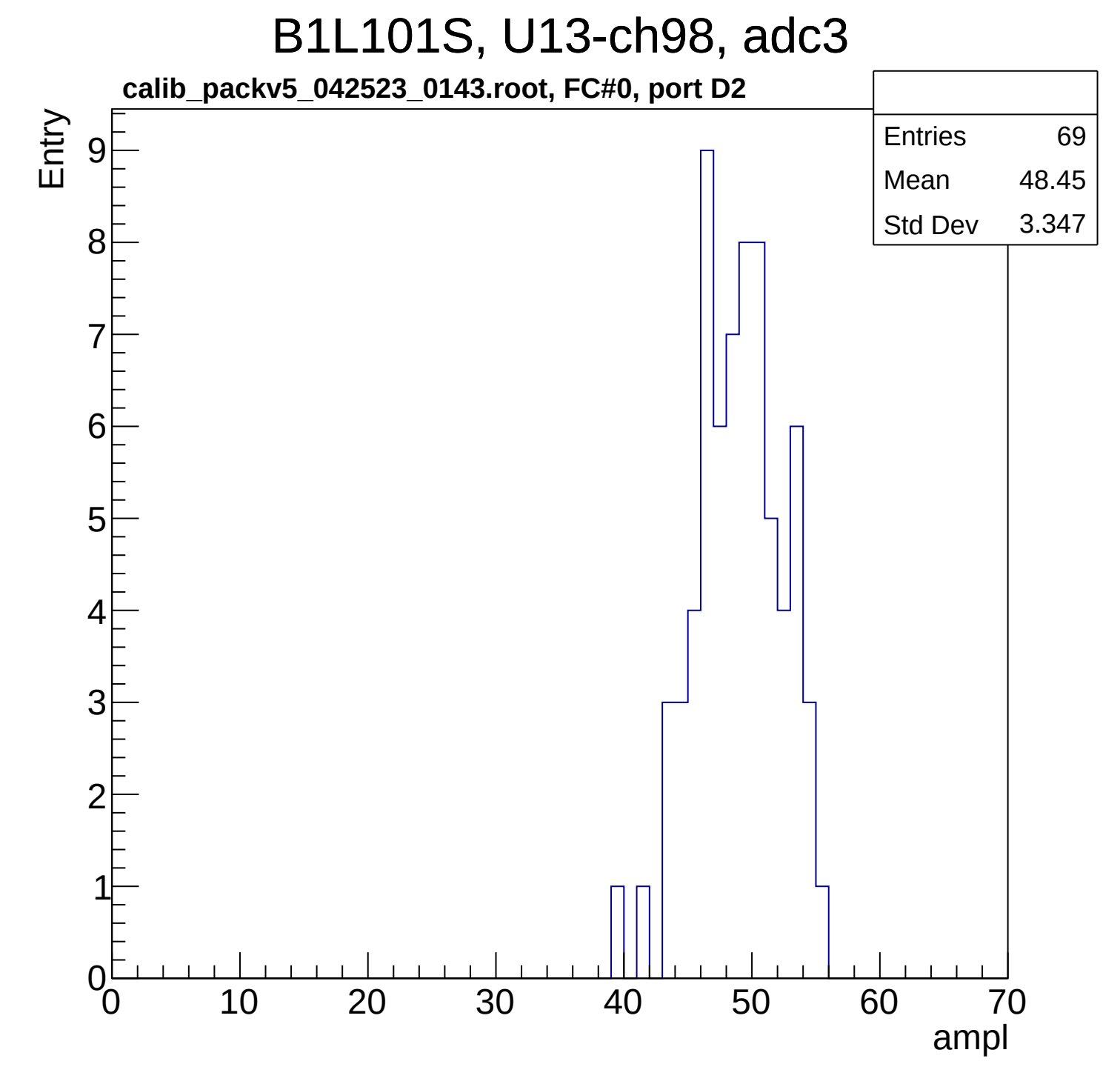
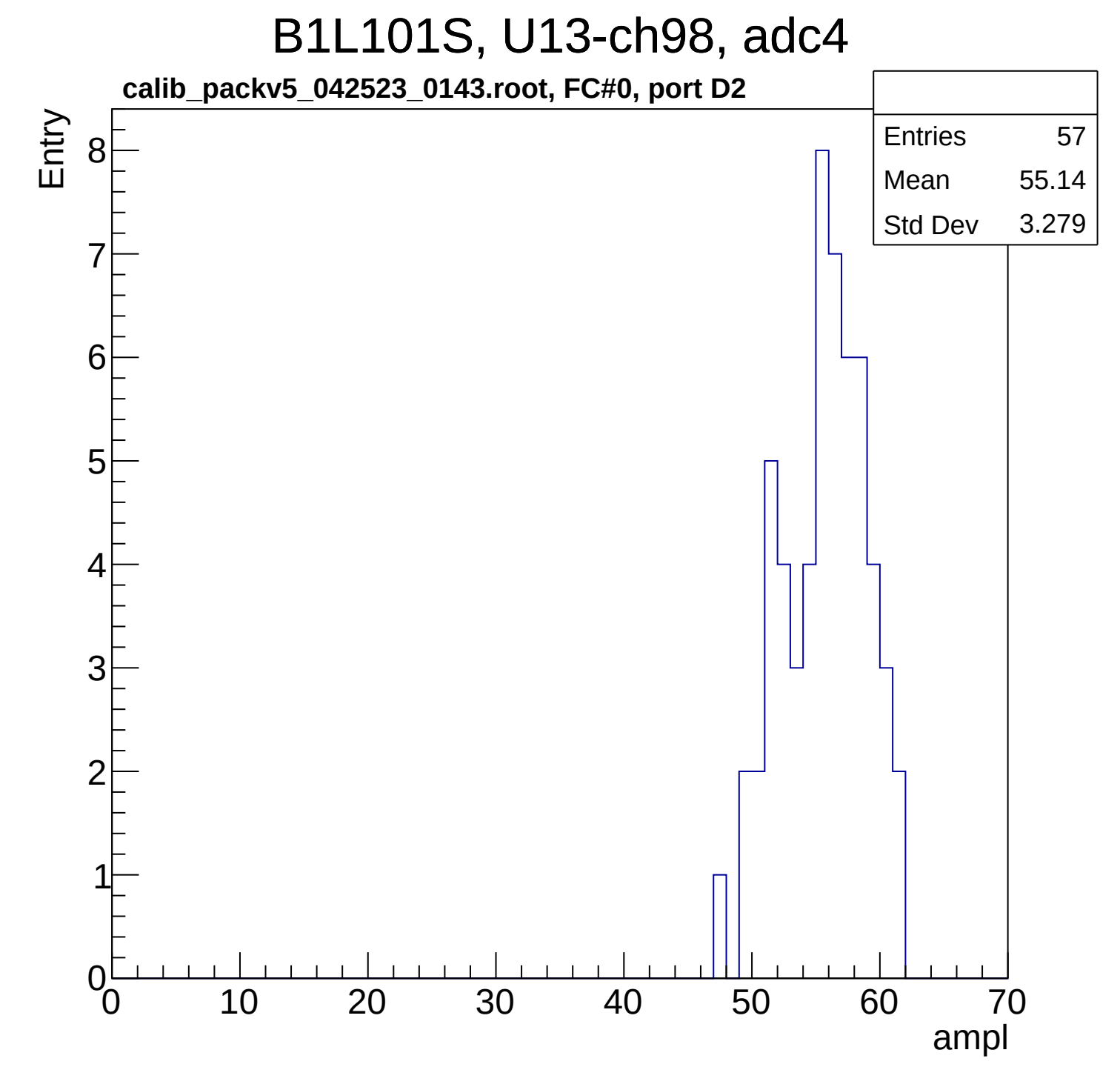
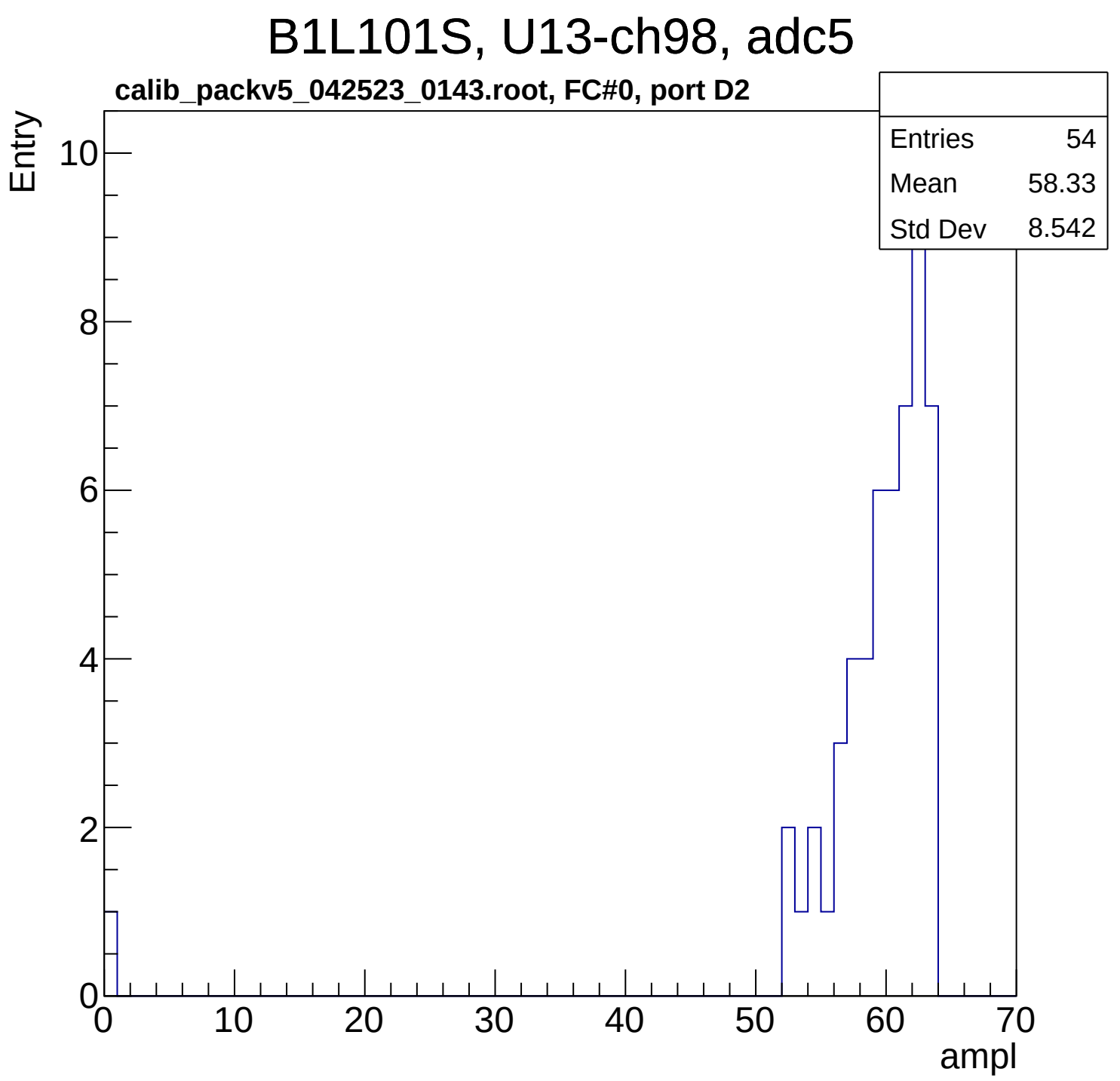
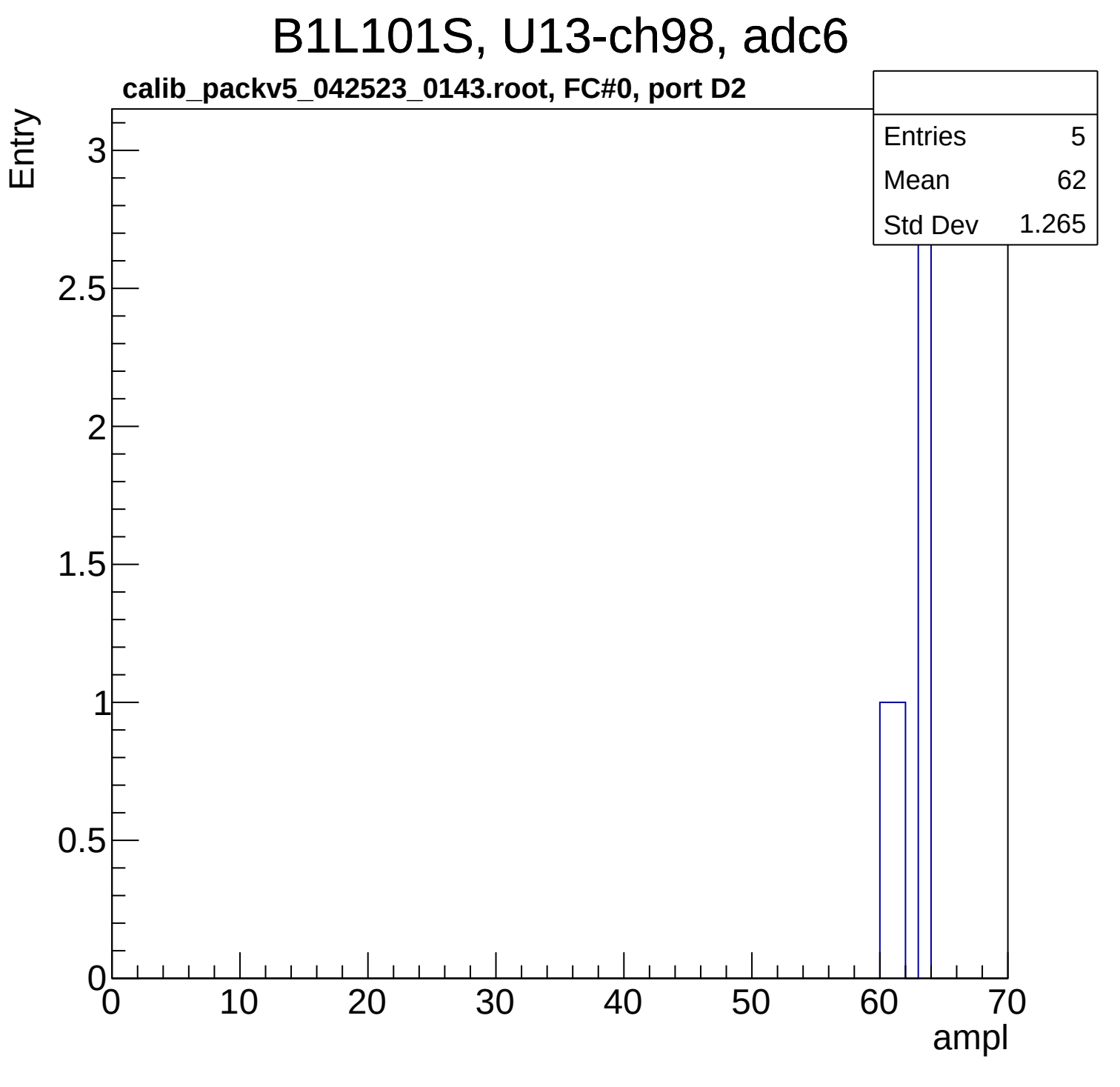
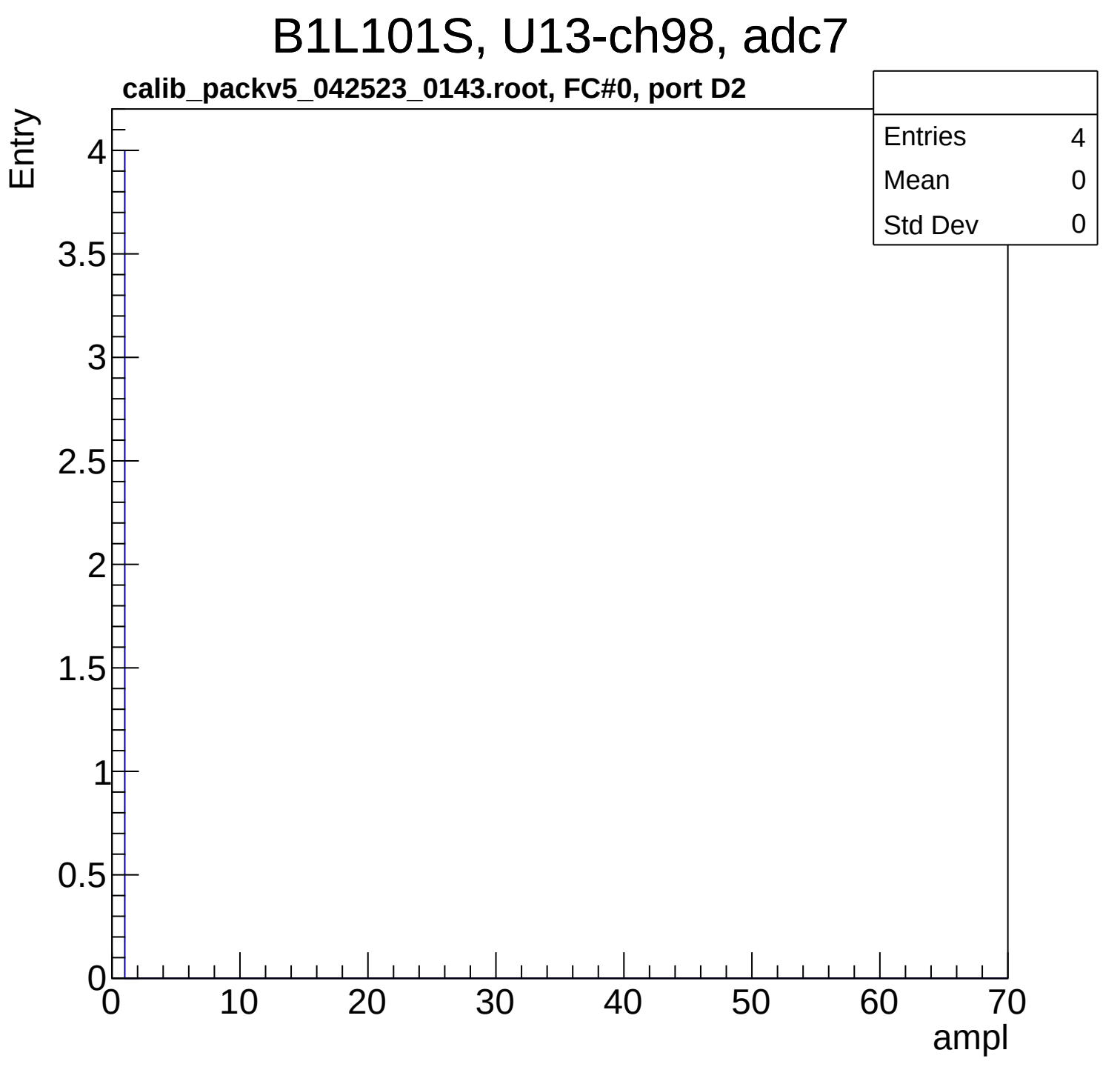
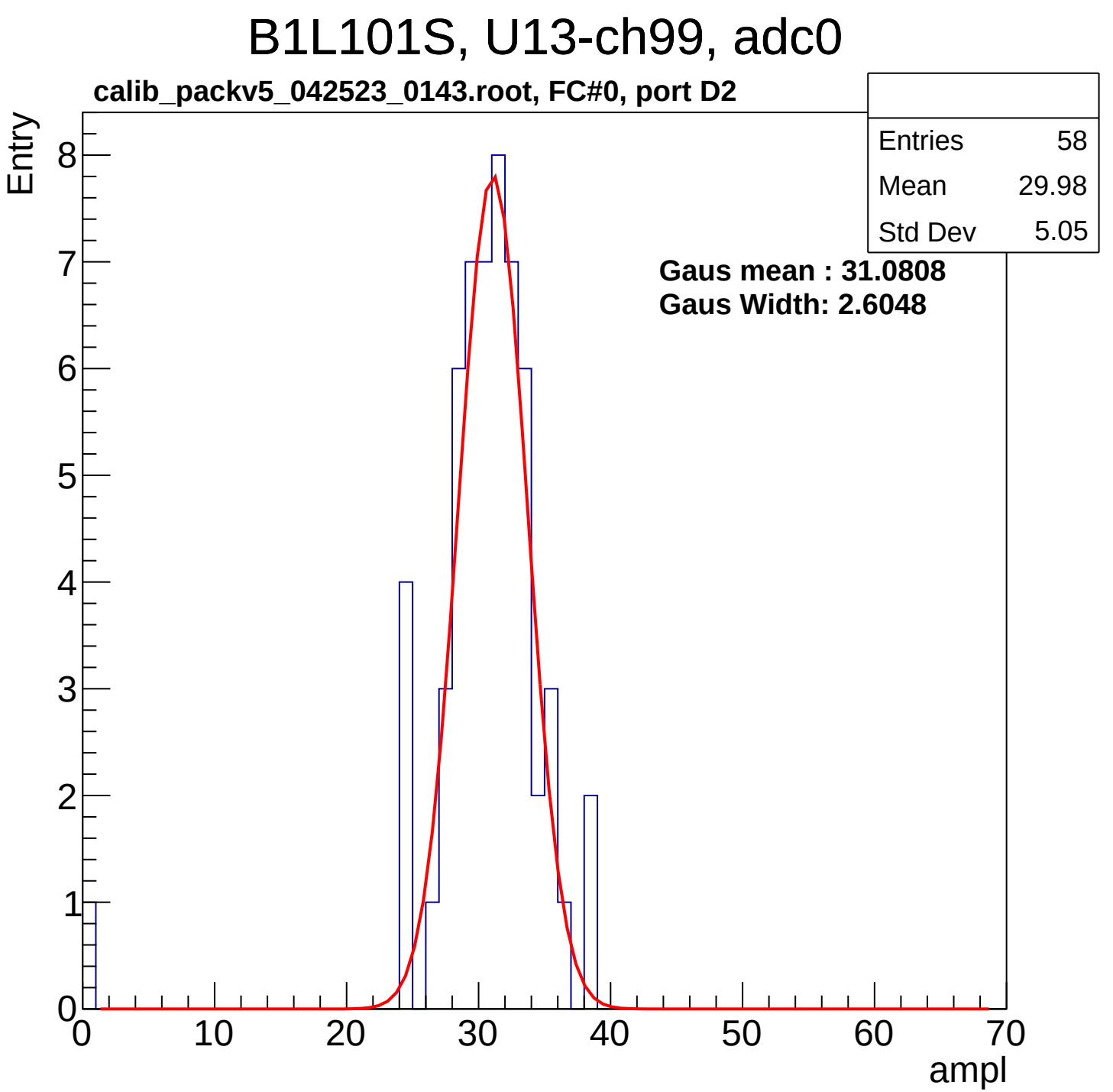

B1L101S, U13-ch98, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 48.45
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch98, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 55.14
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch98, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 58.33
Std Dev 8.542
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch98, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch98, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch99, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 29.98
Std Dev 5.05
Gaus mean : 31.0808
Gaus Width: 2.6048
Entry
ampl
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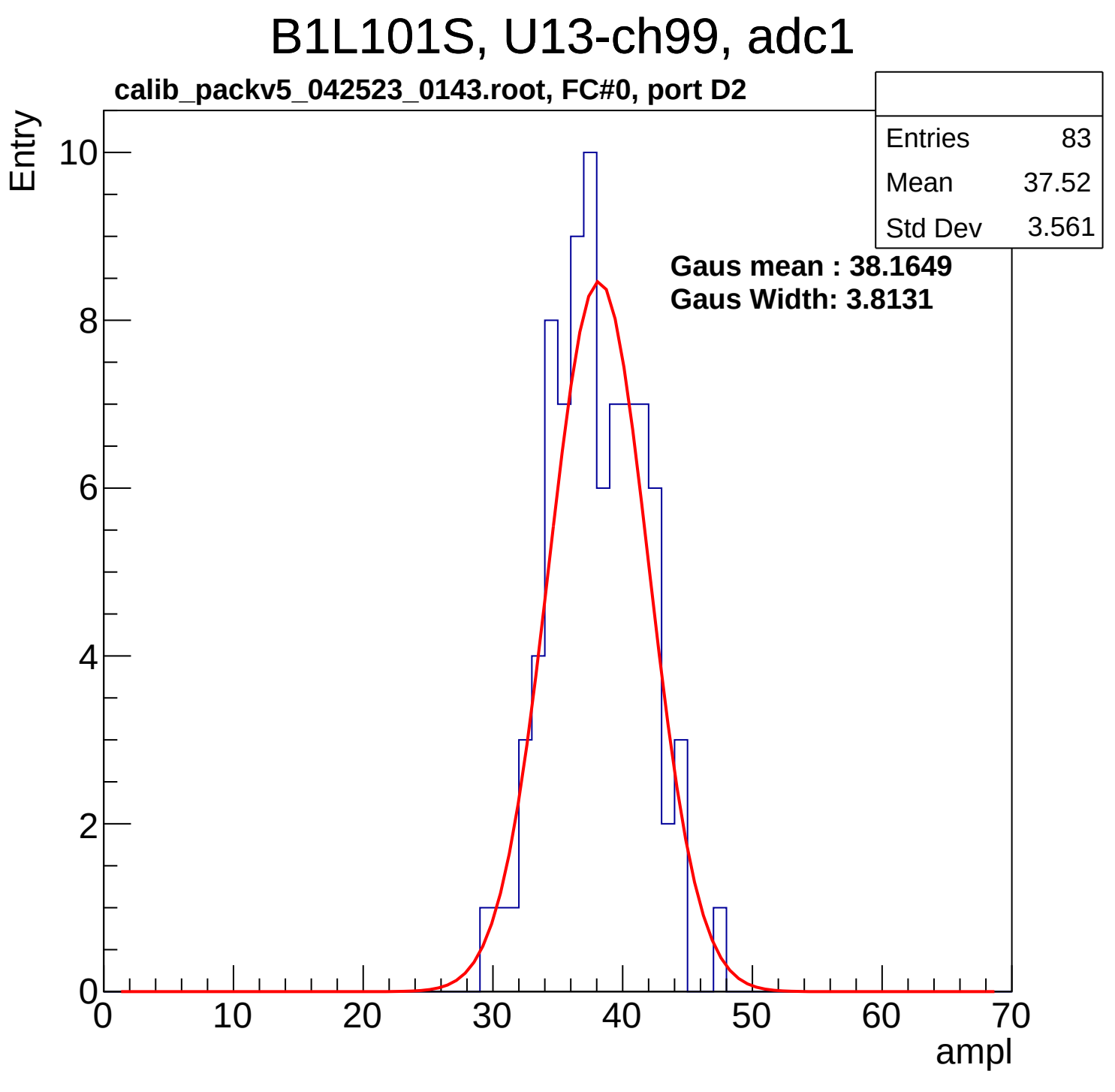
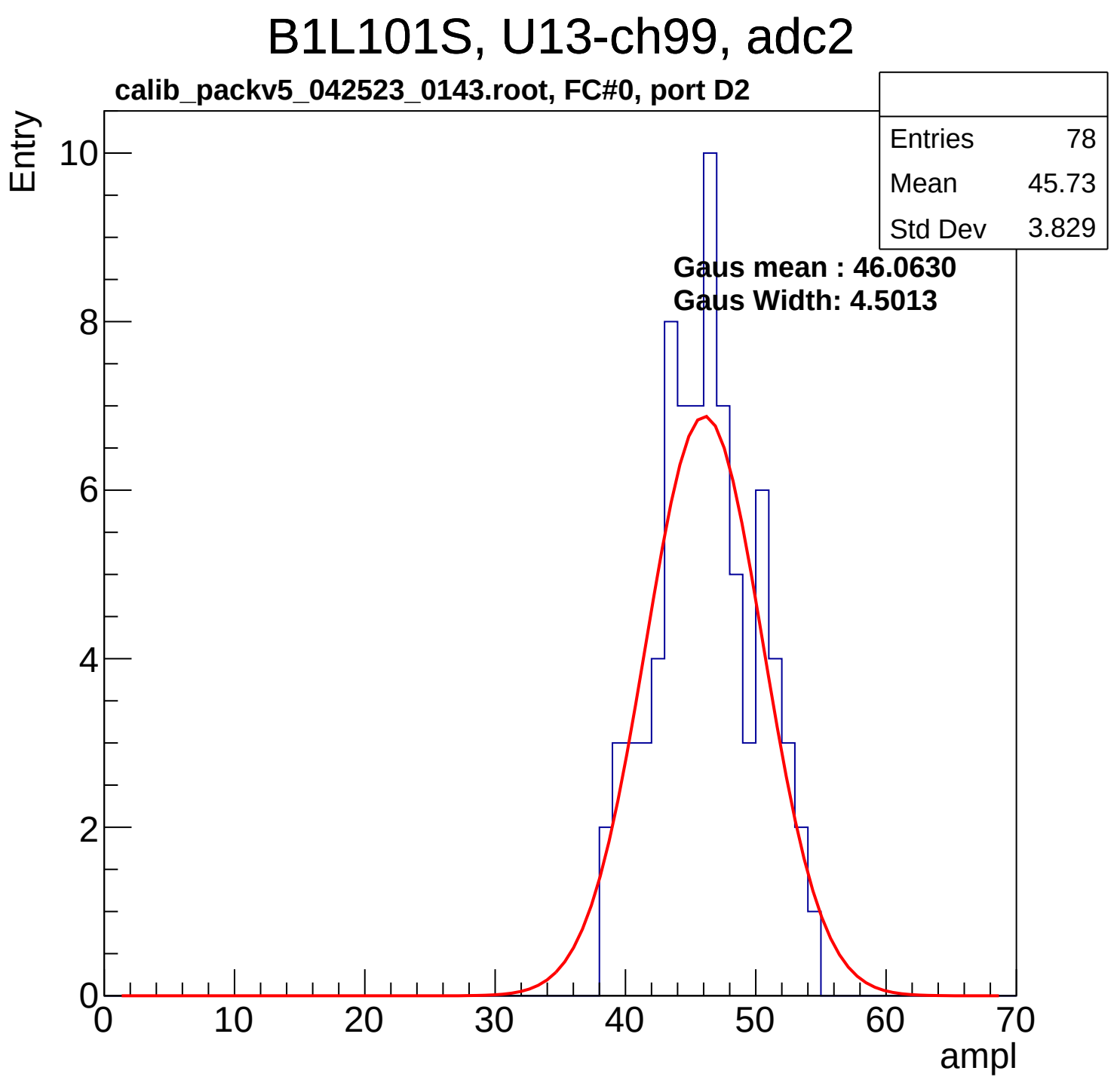
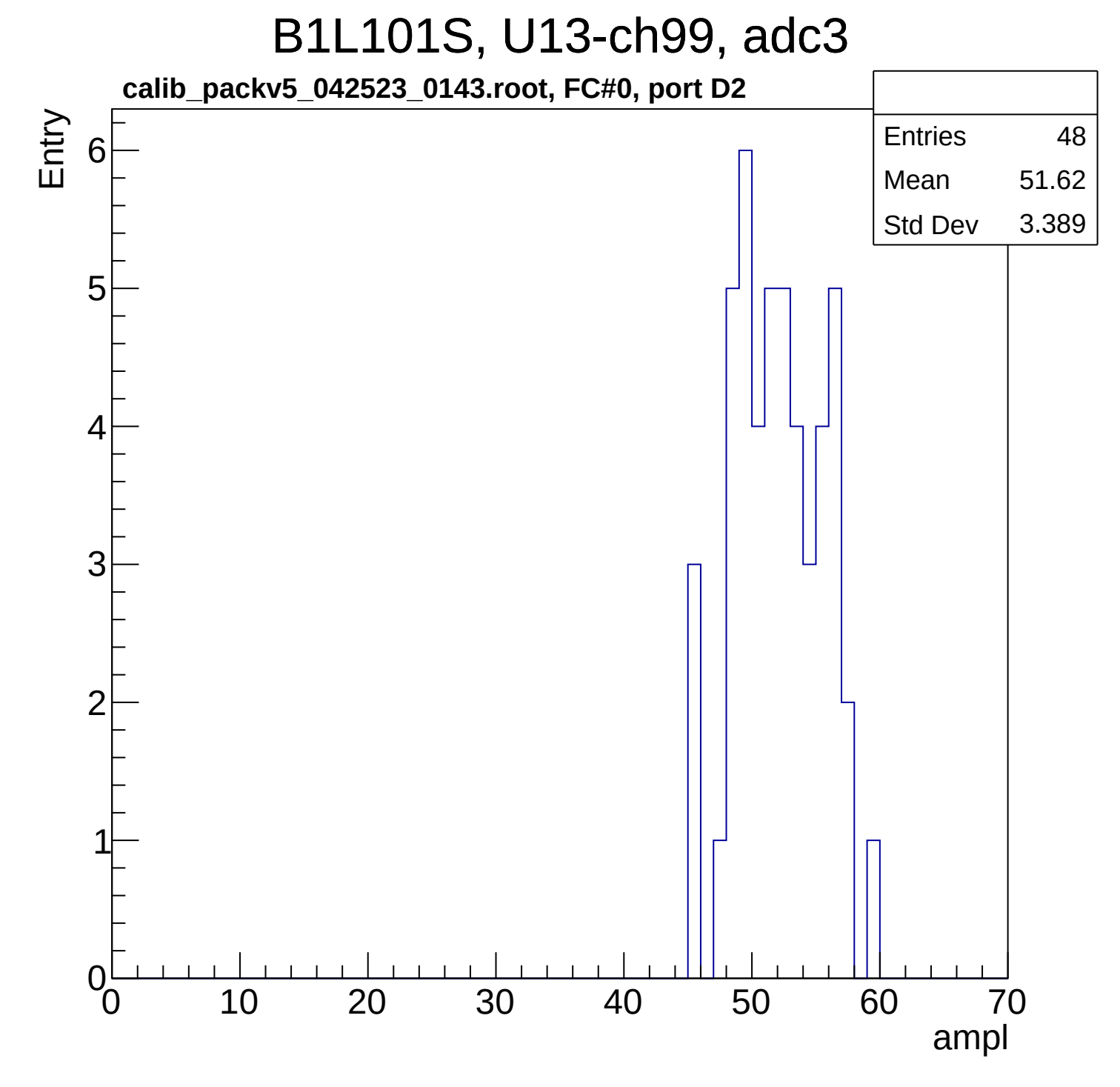
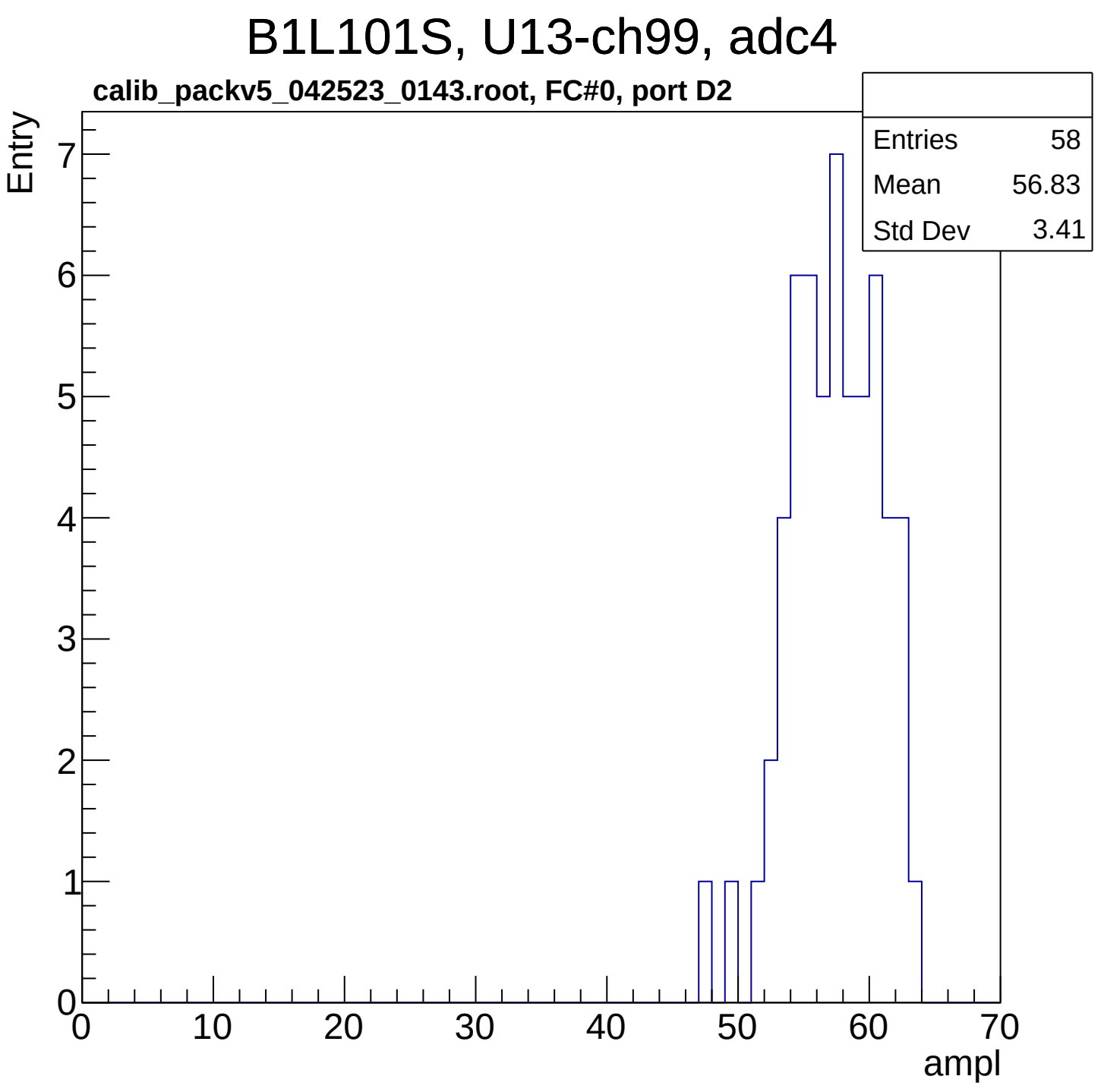
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch99, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 83
Mean 37.52
Std Dev 3.561
Gaus mean : 38.1649
Gaus Width: 3.8131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch99, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 45.73
Std Dev 3.829
Gaus mean : 46.0630
Gaus Width: 4.5013
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch99, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 51.62
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch99, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 56.83
Std Dev 3.41
Entry
ampl
0
1
2
3
4
5
6
7
0
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50
60
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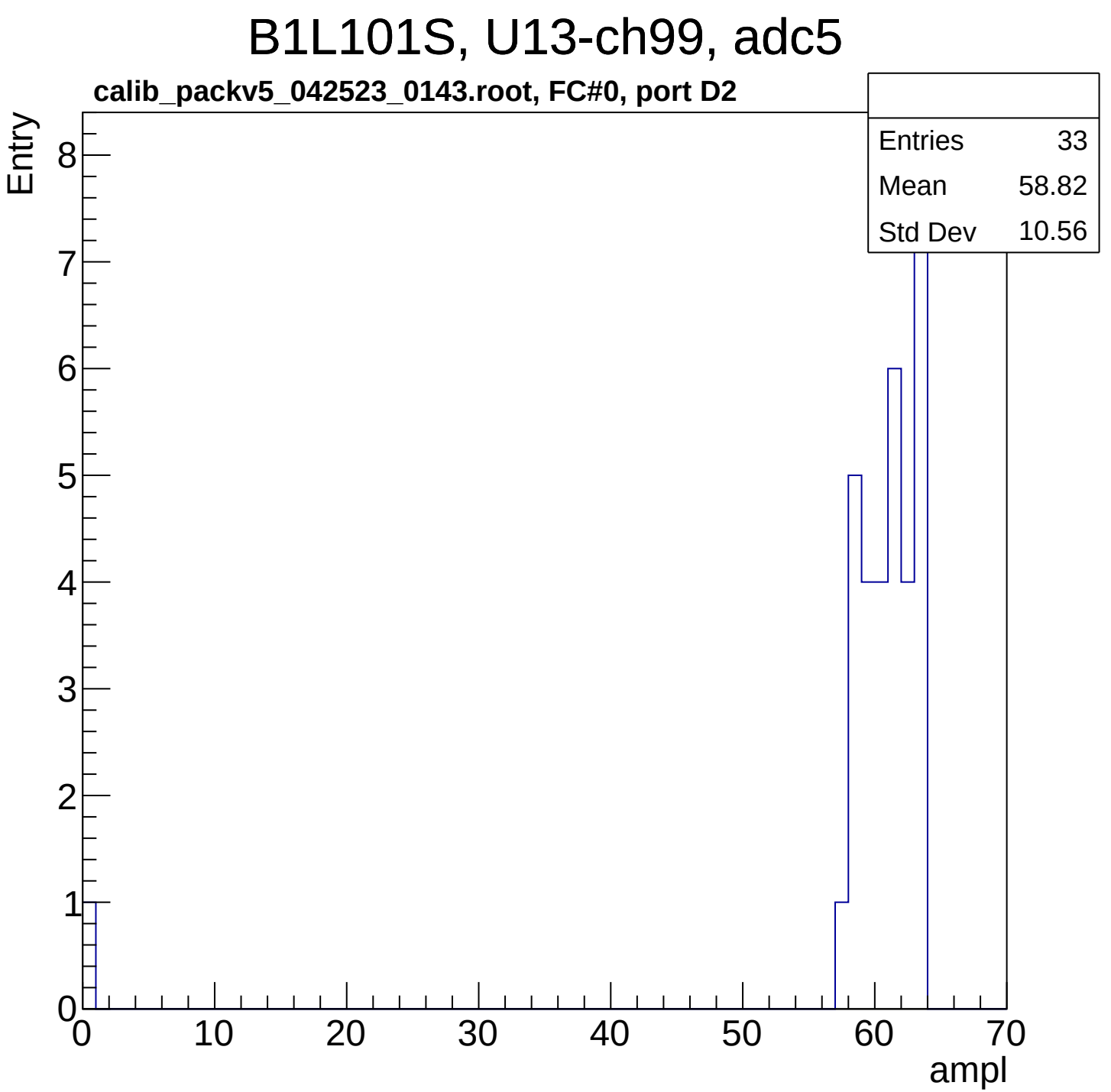
B1L101S, U13-ch99, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 33
Mean 58.82
Std Dev 10.56
Entry
ampl
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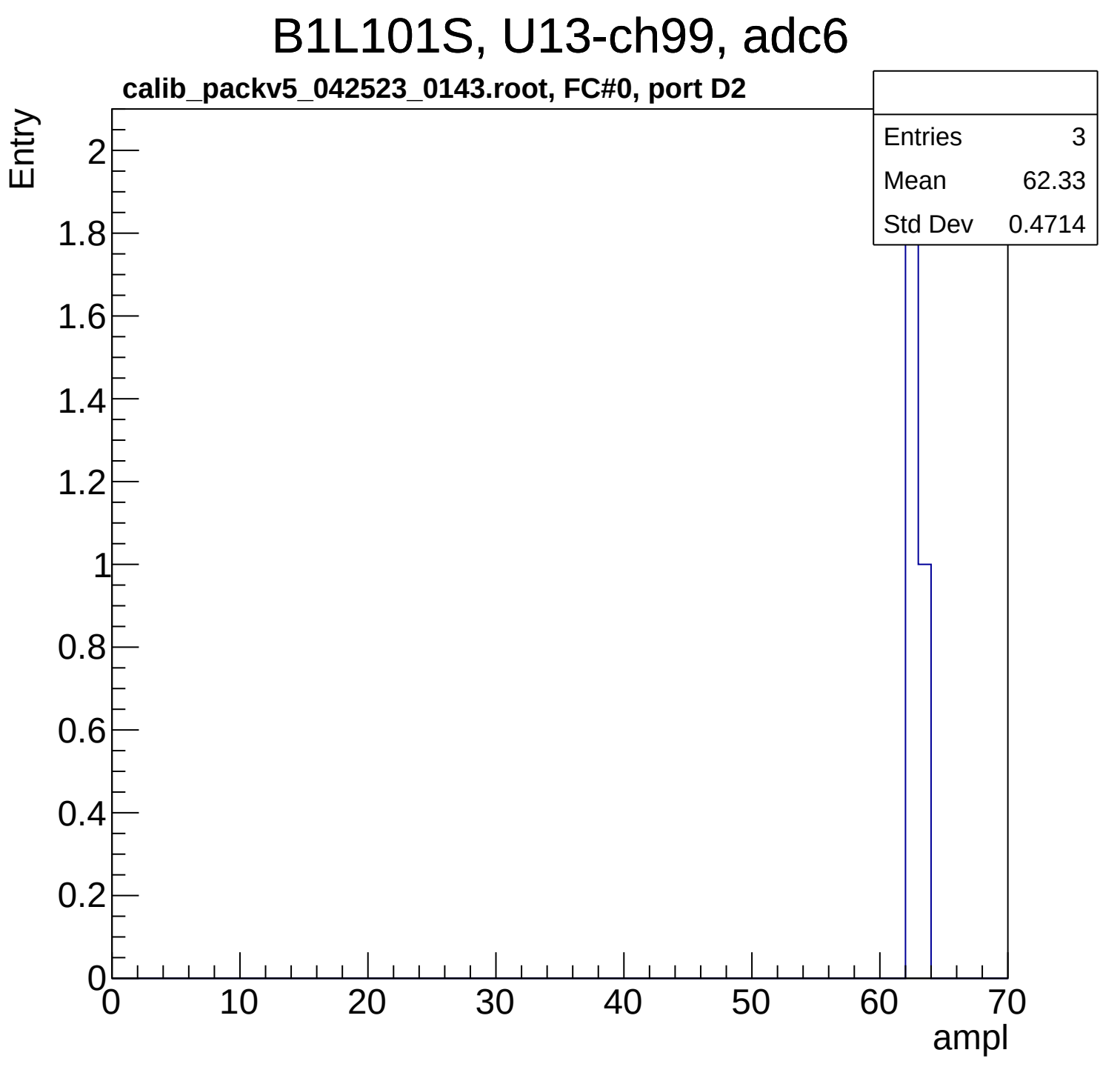
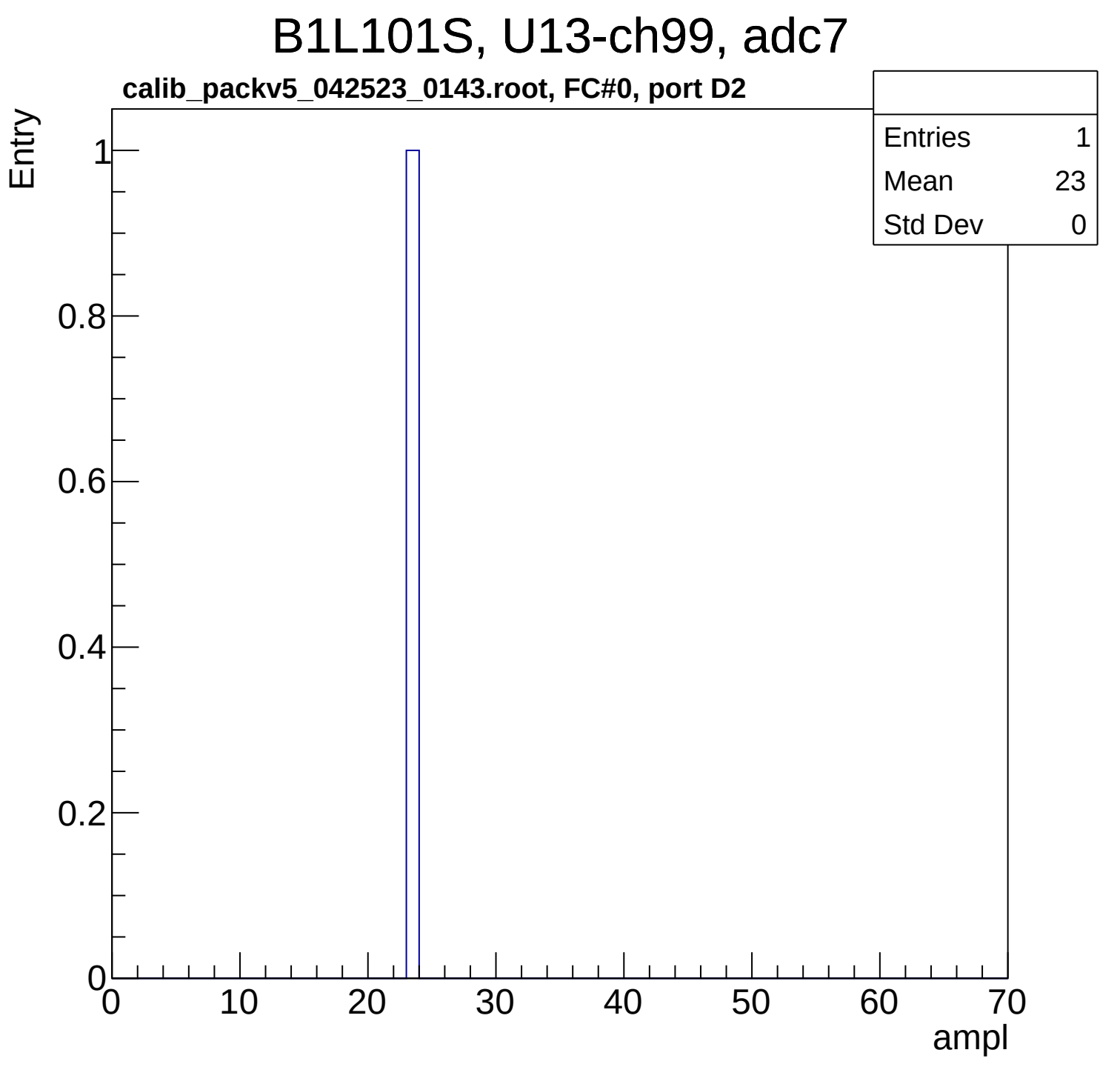
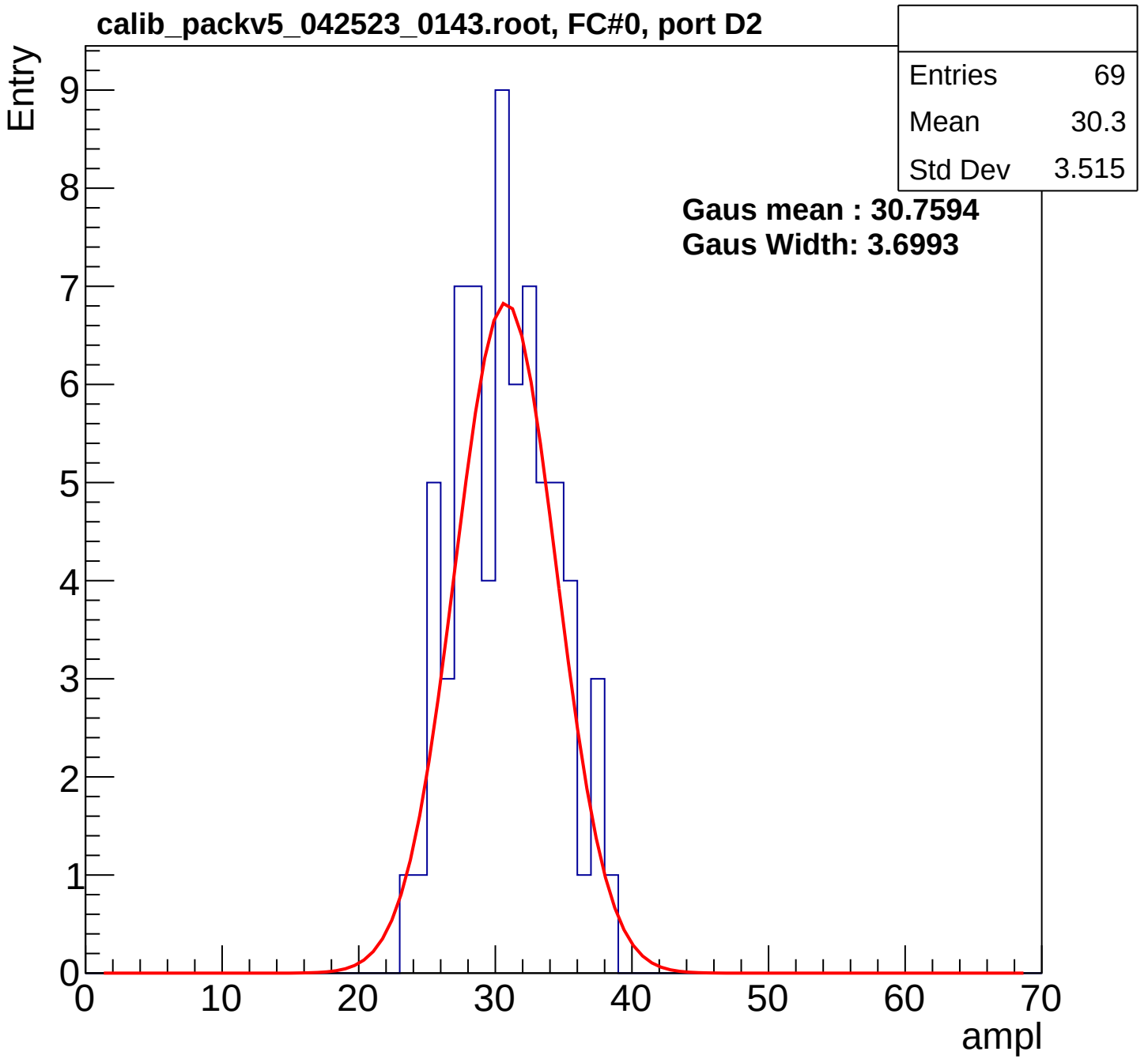
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch99, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch99, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch100, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 30.3
Std Dev 3.515
Gaus mean : 30.7594
Gaus Width: 3.6993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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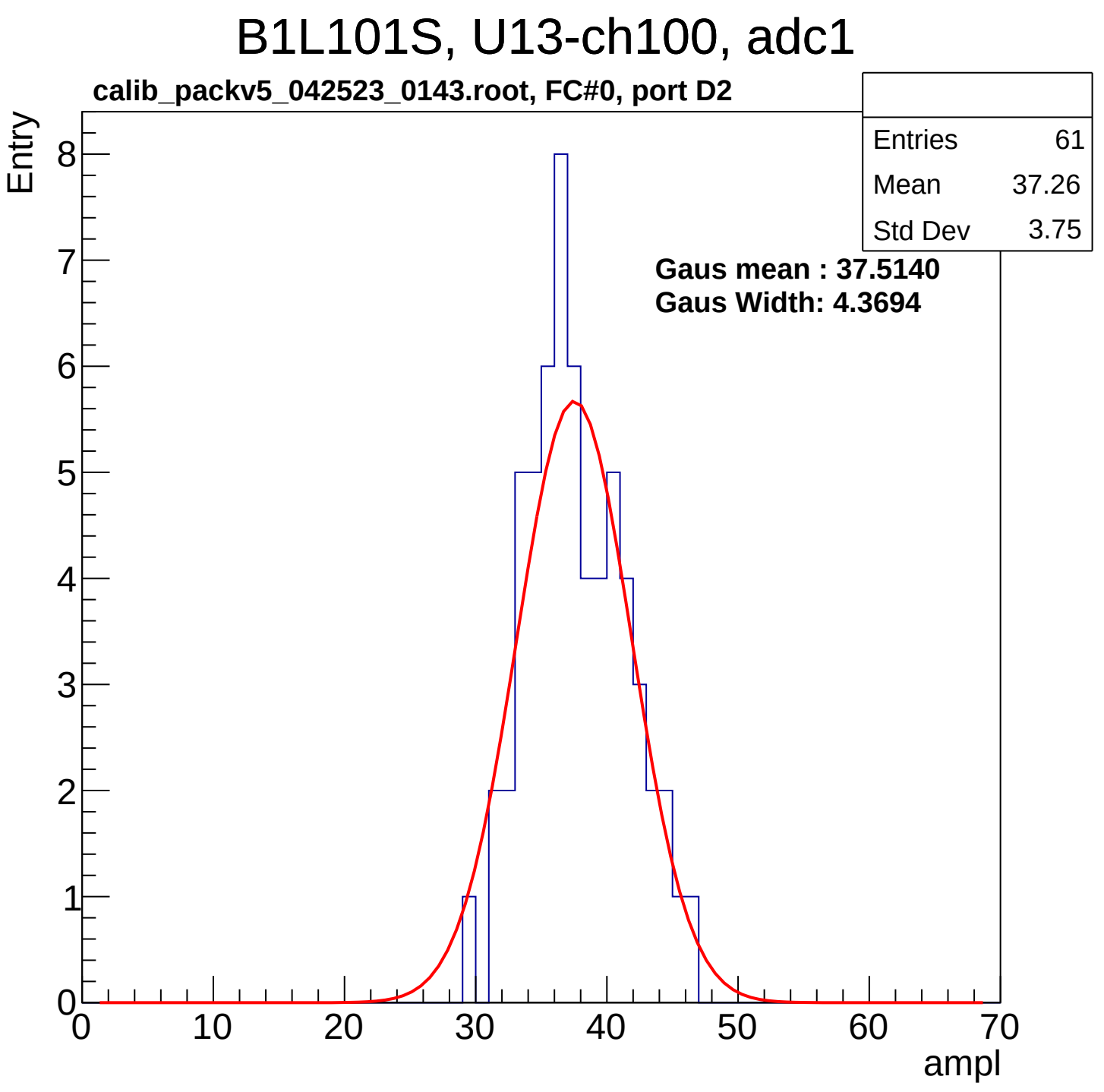
B1L101S, U13-ch100, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 37.26
Std Dev 3.75
Gaus mean : 37.5140
Gaus Width: 4.3694
Entry
ampl
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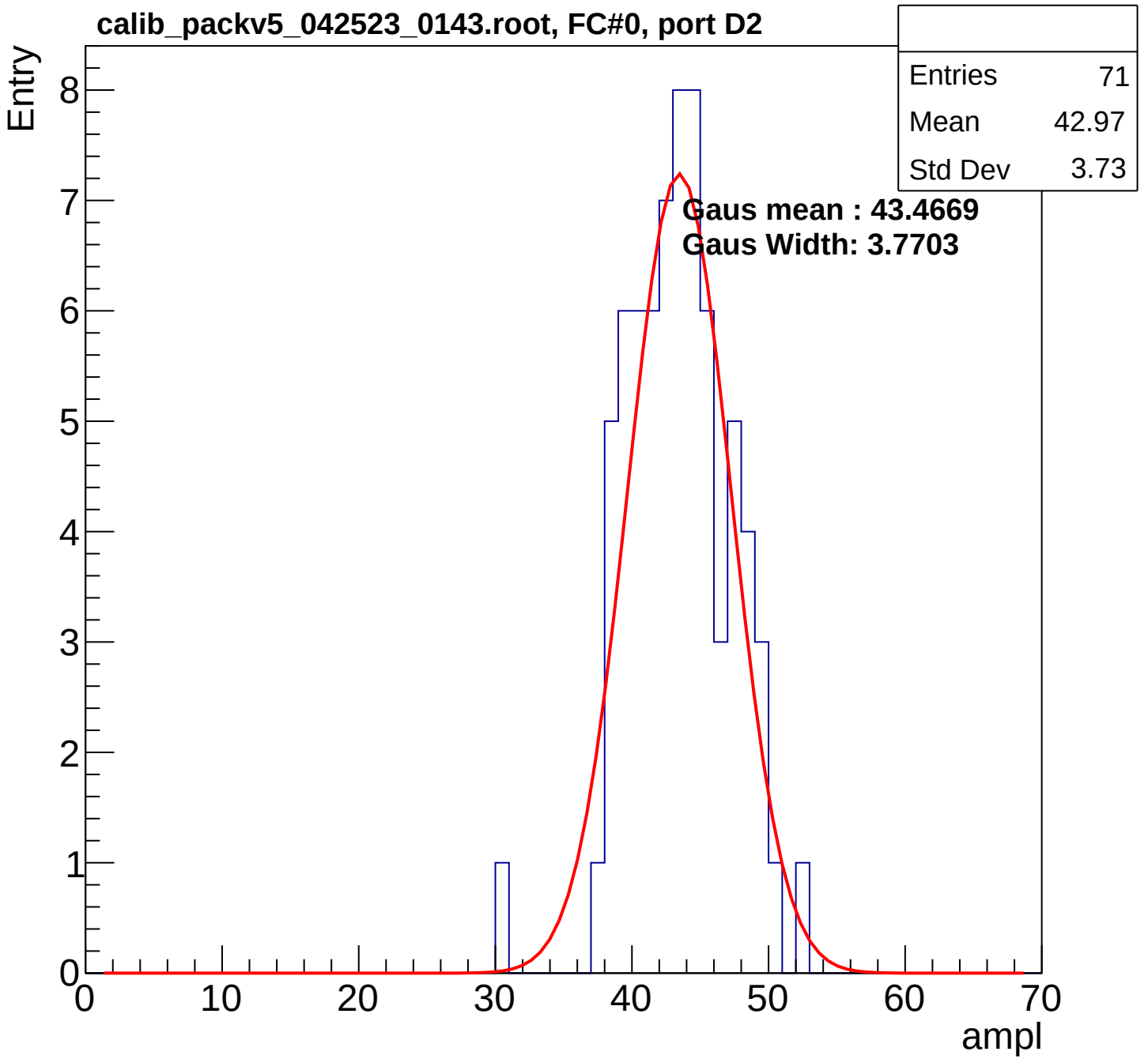
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch100, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 42.97
Std Dev 3.73
Gaus mean : 43.4669
Gaus Width: 3.7703
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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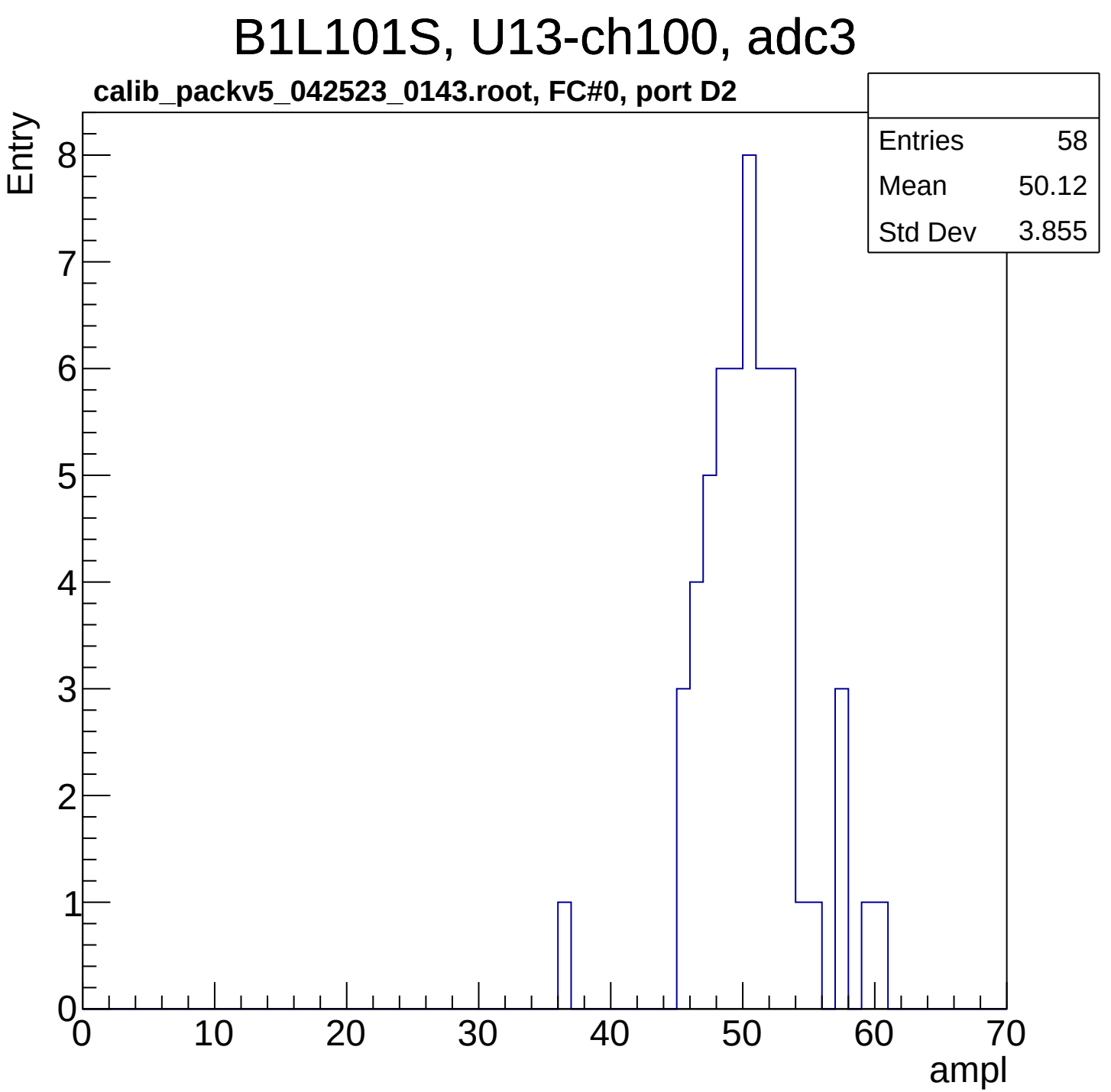
B1L101S, U13-ch100, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 50.12
Std Dev 3.855
Entry
ampl
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1
2
3
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5
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7
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0
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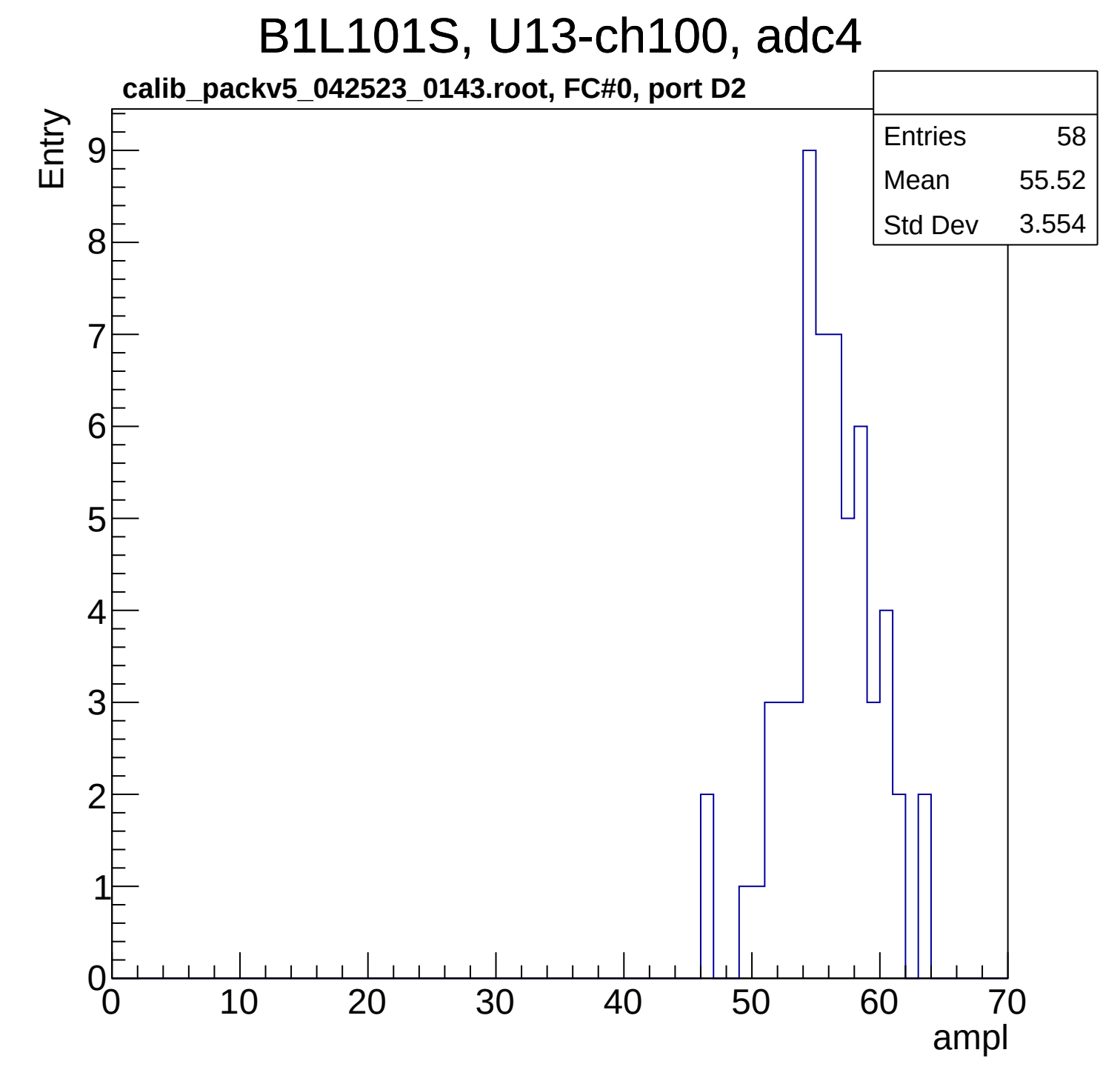
B1L101S, U13-ch100, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 55.52
Std Dev 3.554
Entry
ampl
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2
3
4
5
6
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0
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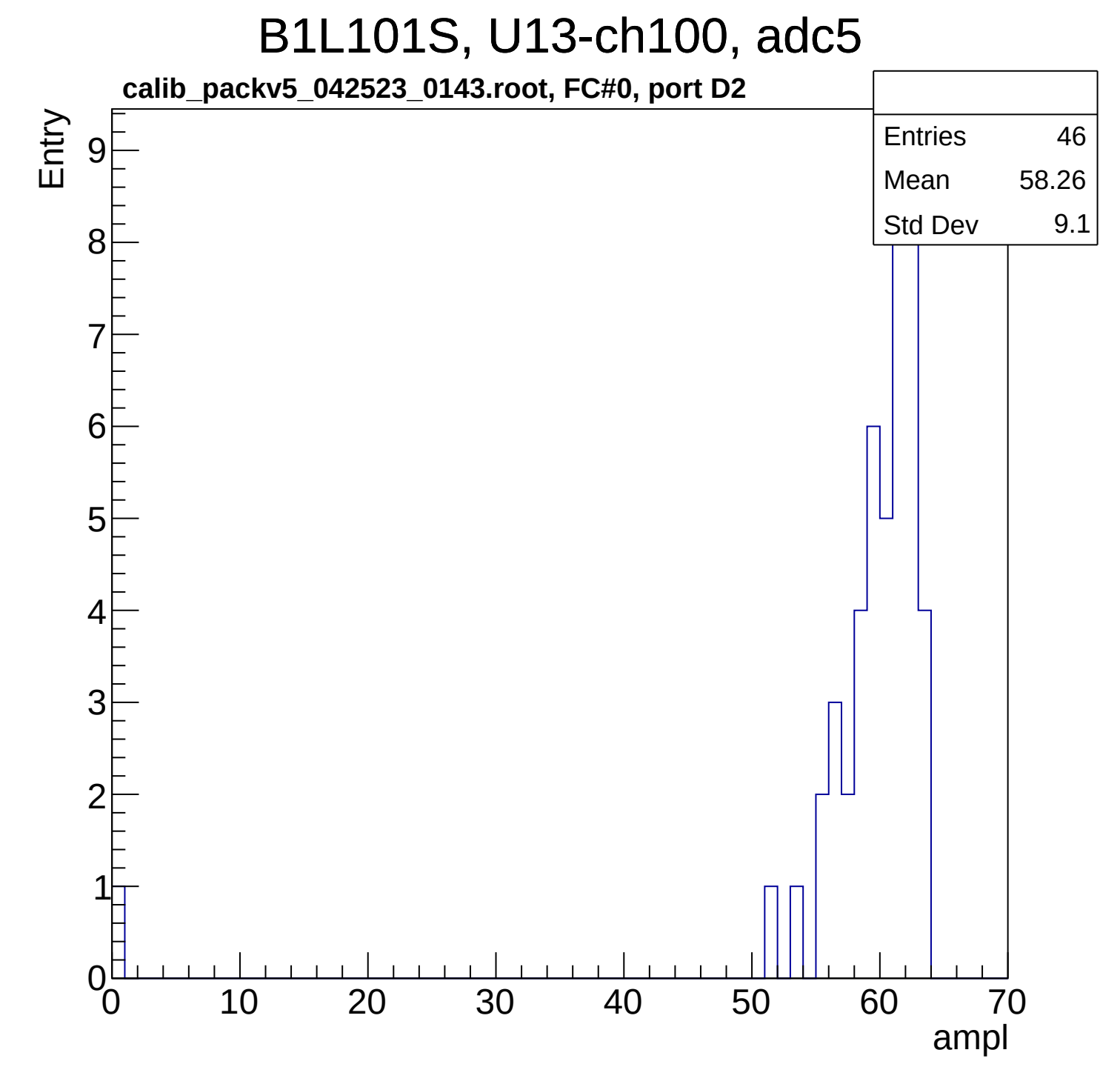
B1L101S, U13-ch100, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 58.26
Std Dev 9.1
Entry
ampl
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2
3
4
5
6
7
8
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0
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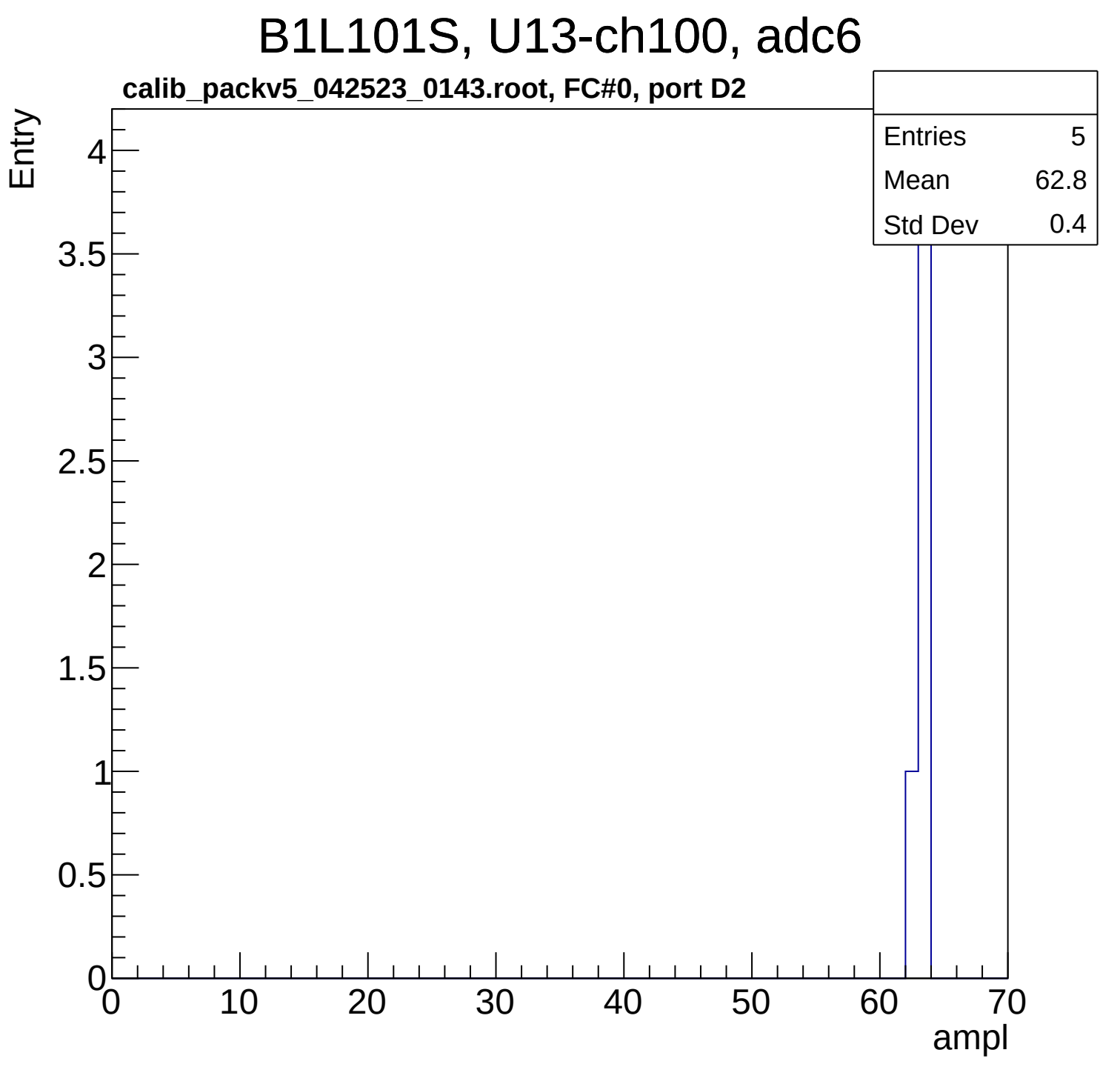
B1L101S, U13-ch100, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
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0
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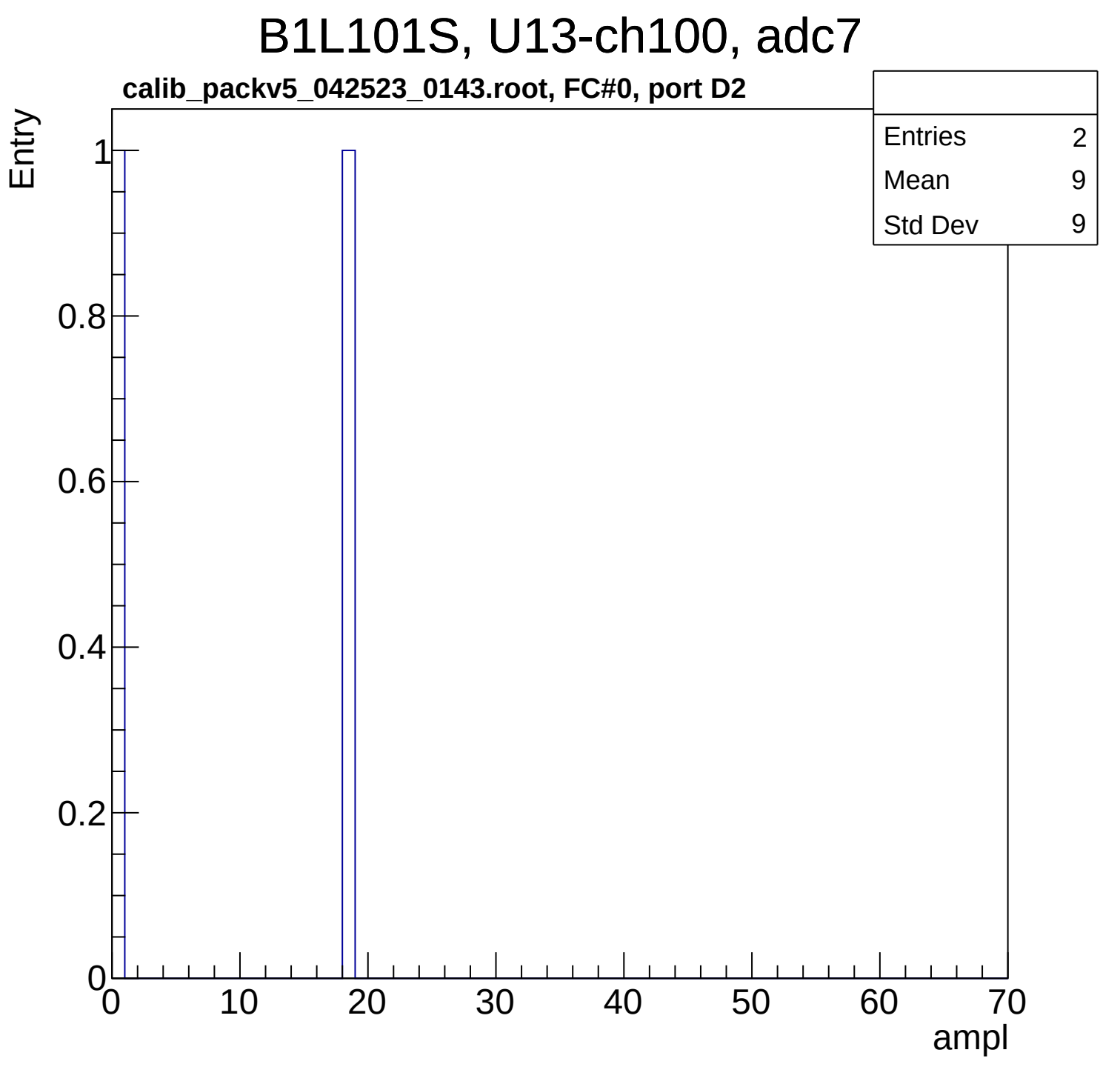
B1L101S, U13-ch100, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 9
Std Dev 9
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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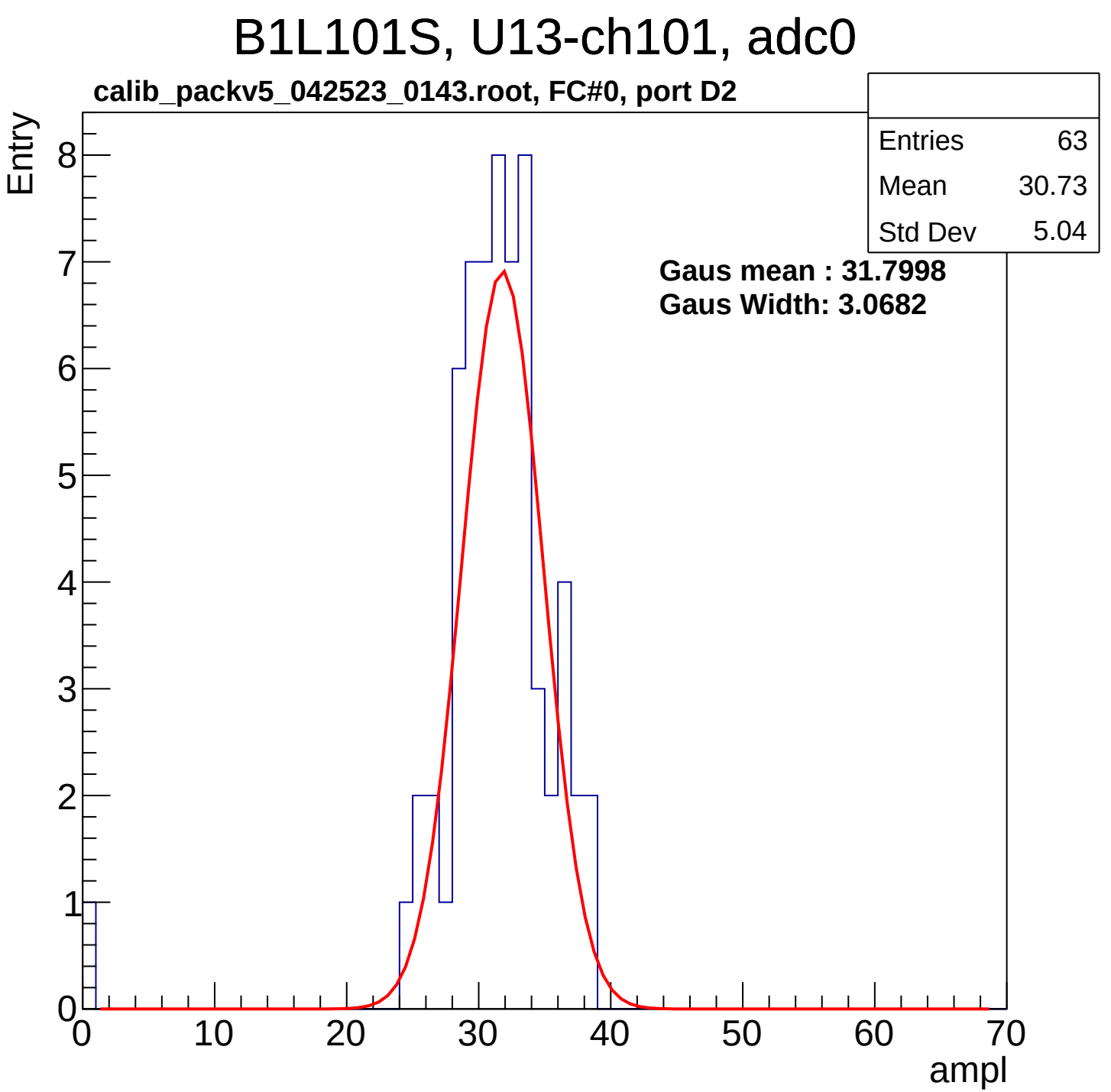
40
50
60
70
ampl


B1L101S, U13-ch101, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 30.73
Std Dev 5.04
Gaus mean : 31.7998
Gaus Width: 3.0682
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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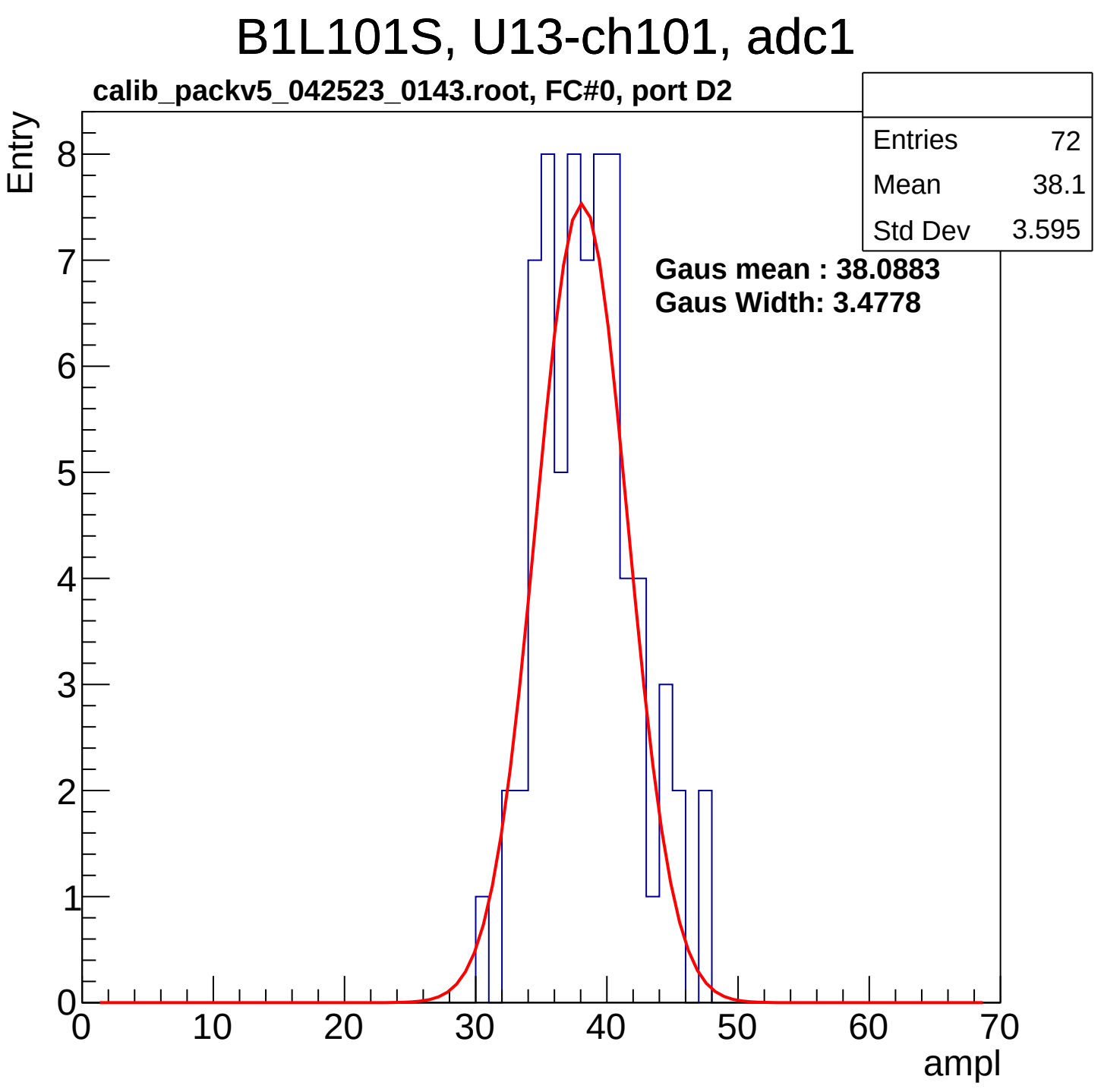
B1L101S, U13-ch101, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 38.1
Std Dev 3.595
Gaus mean : 38.0883
Gaus Width: 3.4778
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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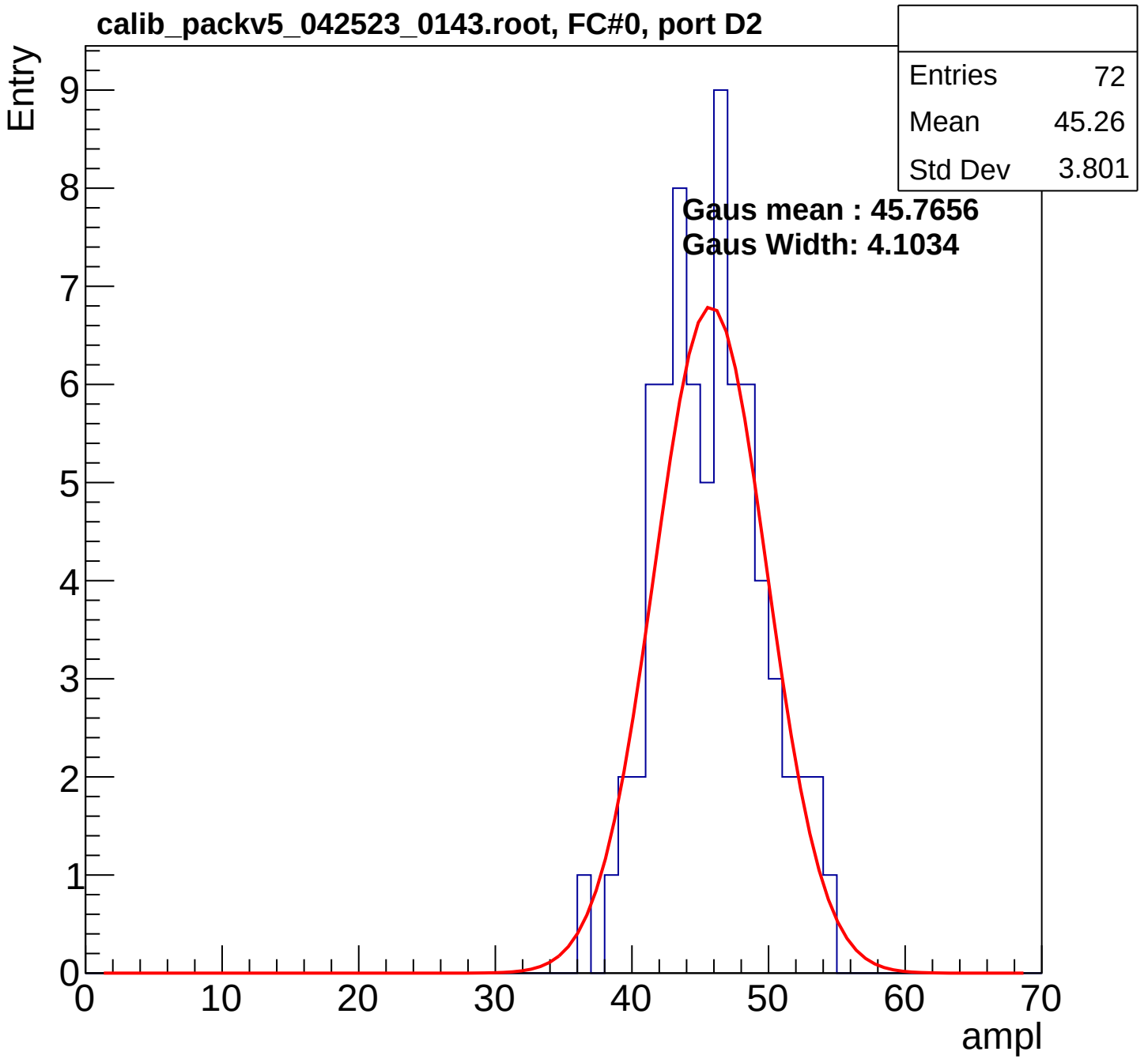
B1L101S, U13-ch101, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 45.26
Std Dev 3.801
Gaus mean : 45.7656
Gaus Width: 4.1034
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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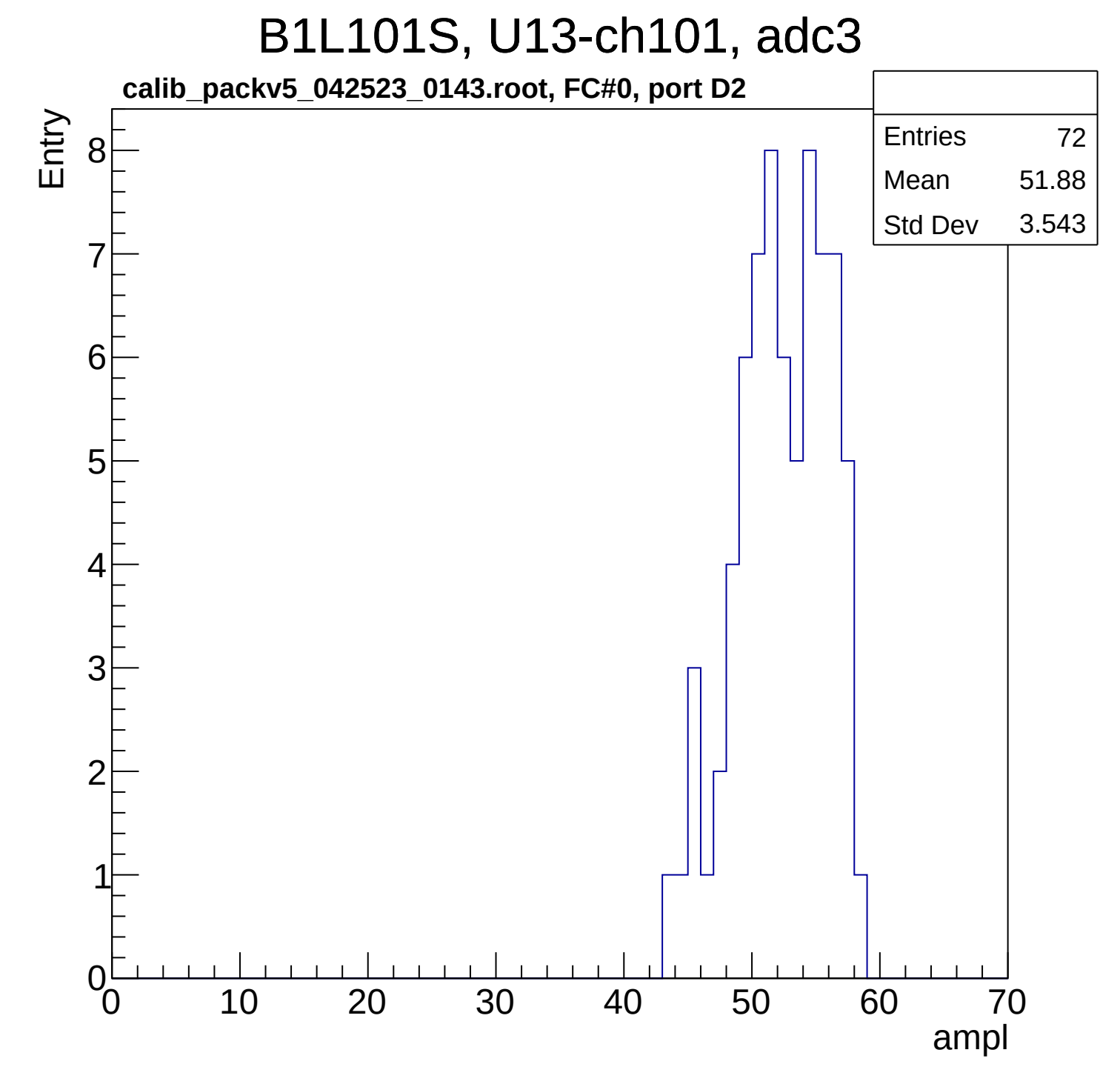
B1L101S, U13-ch101, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 51.88
Std Dev 3.543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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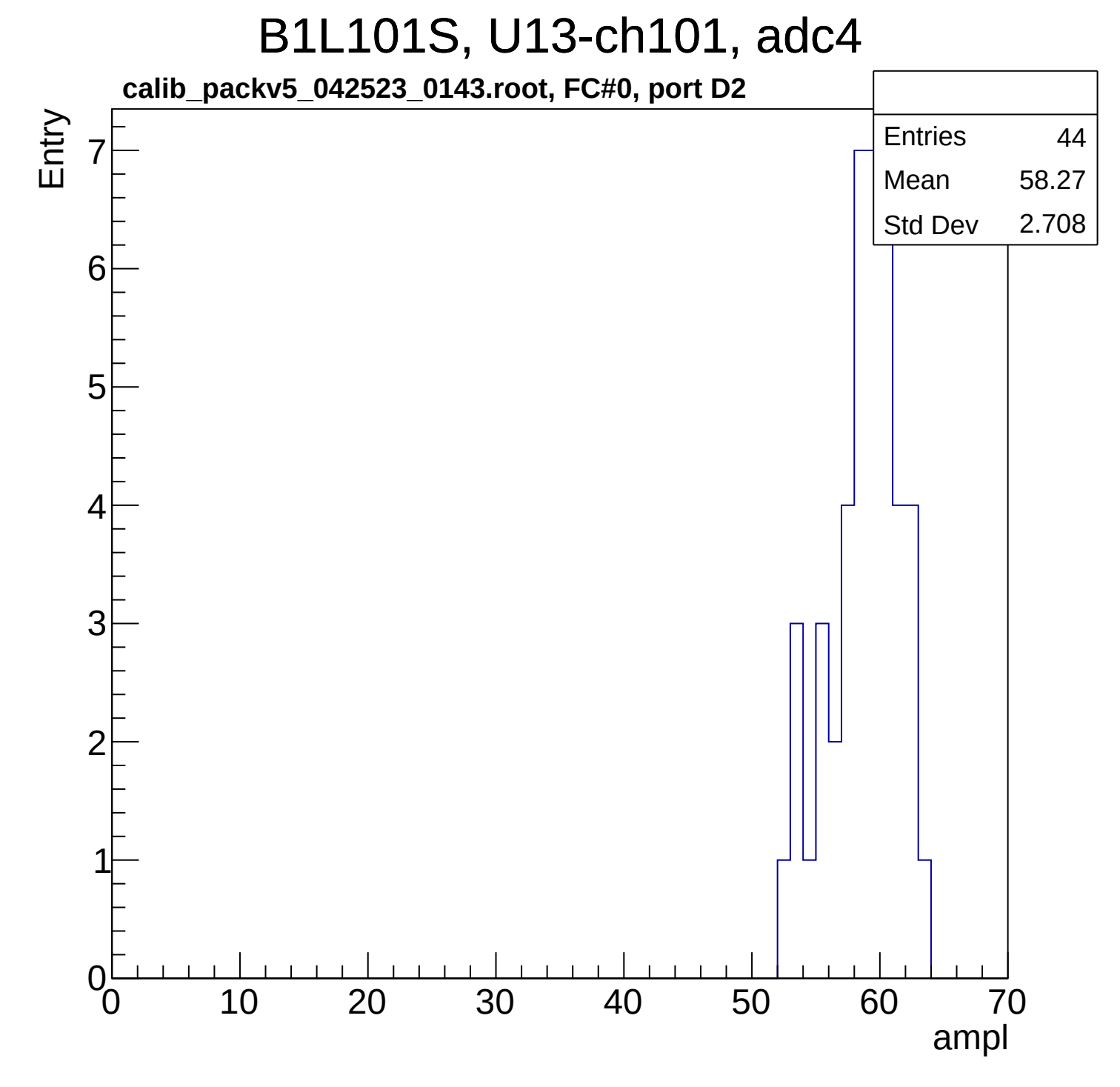
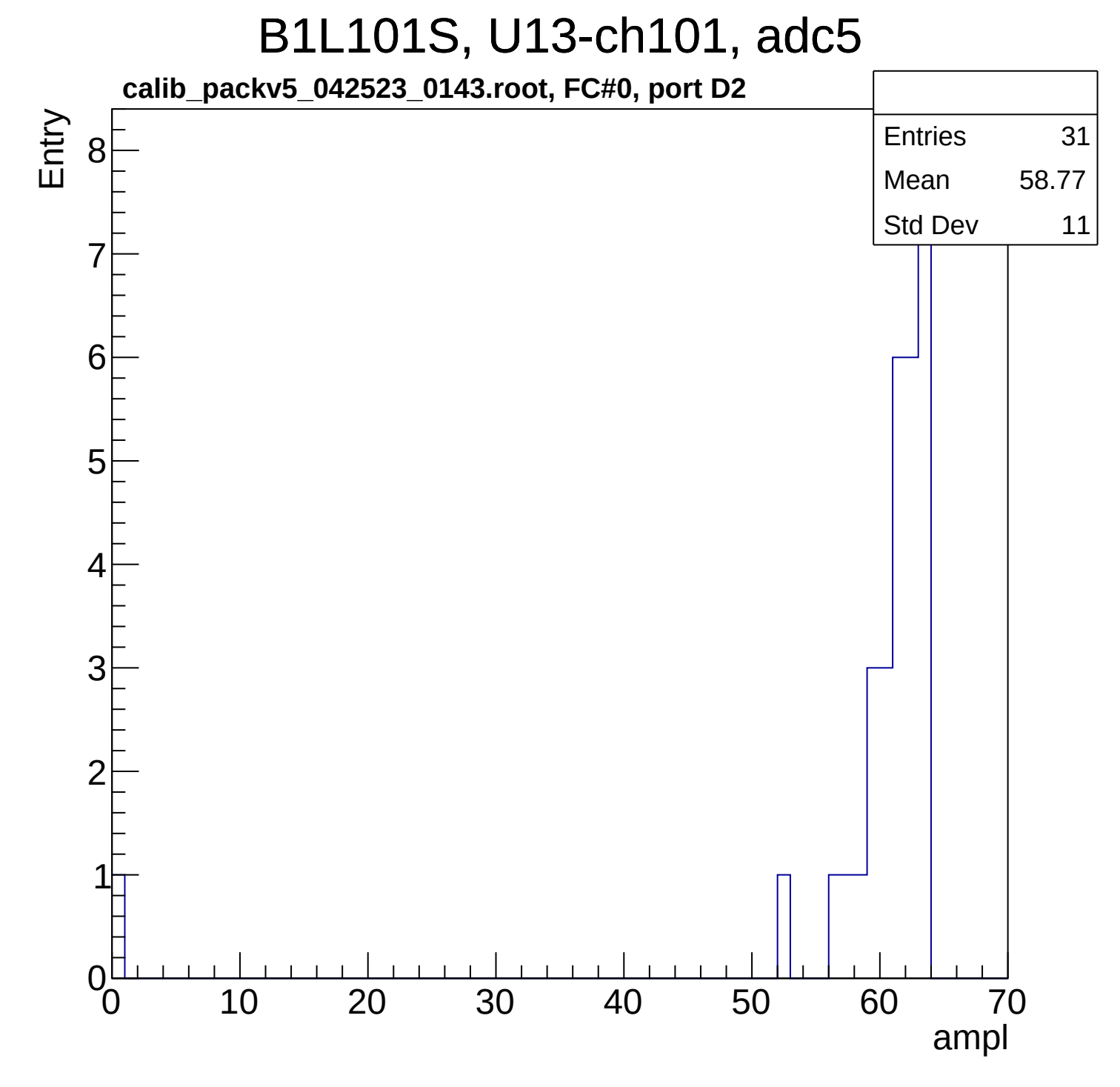

B1L101S, U13-ch101, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 58.27
Std Dev 2.708
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch101, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 31
Mean 58.77
Std Dev 11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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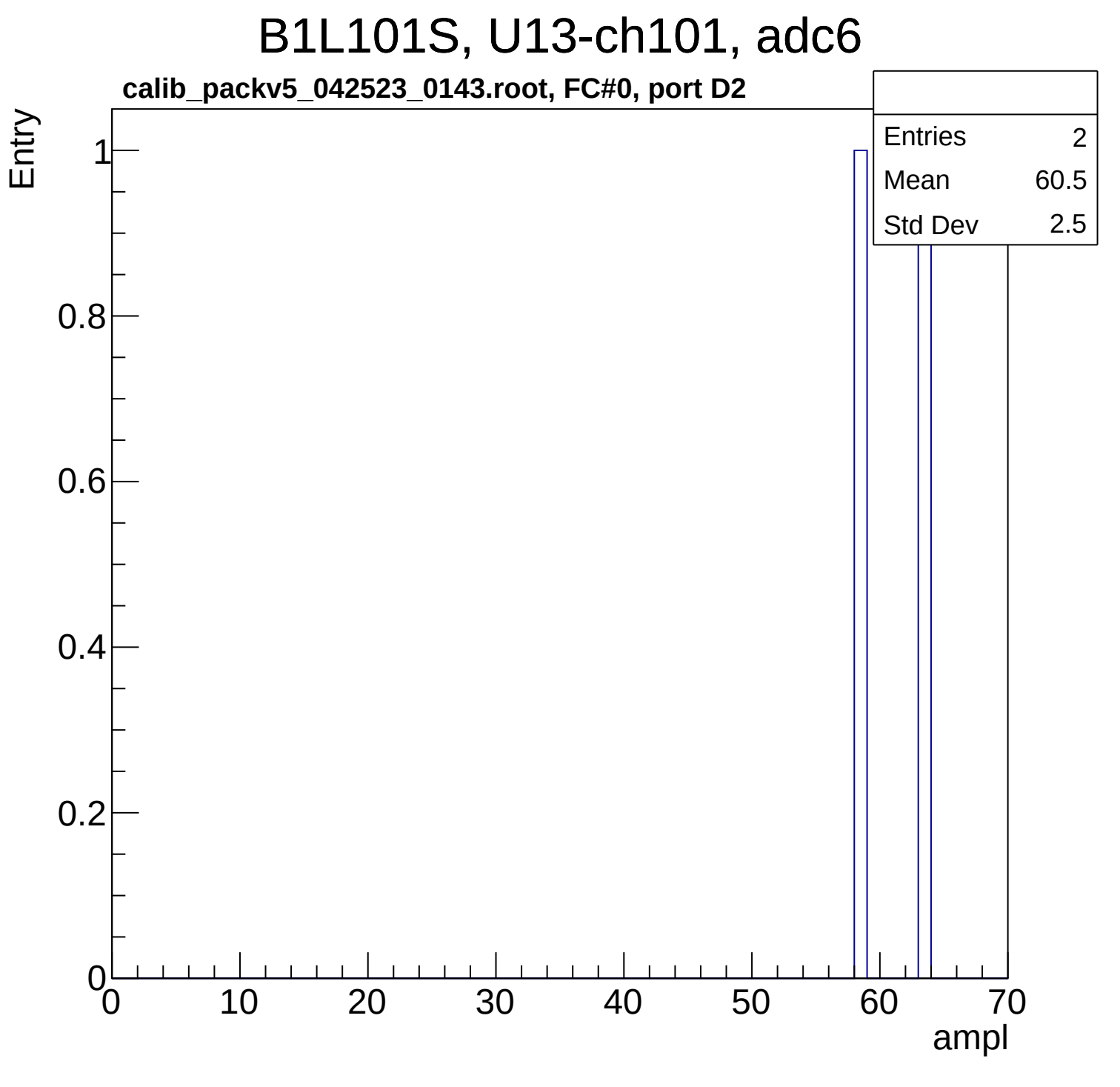
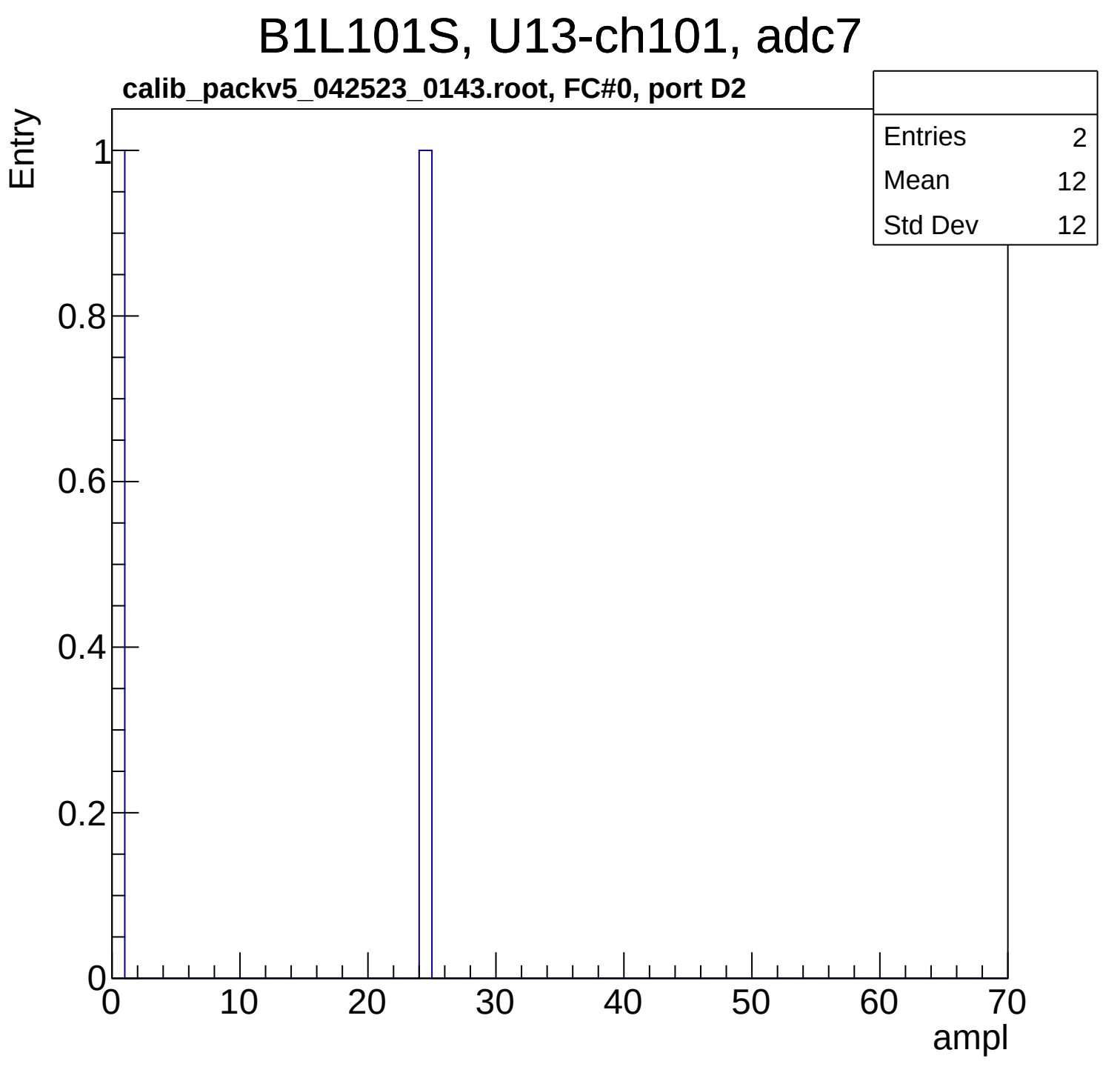

B1L101S, U13-ch101, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 60.5
Std Dev 2.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch101, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 12
Std Dev 12
Entry
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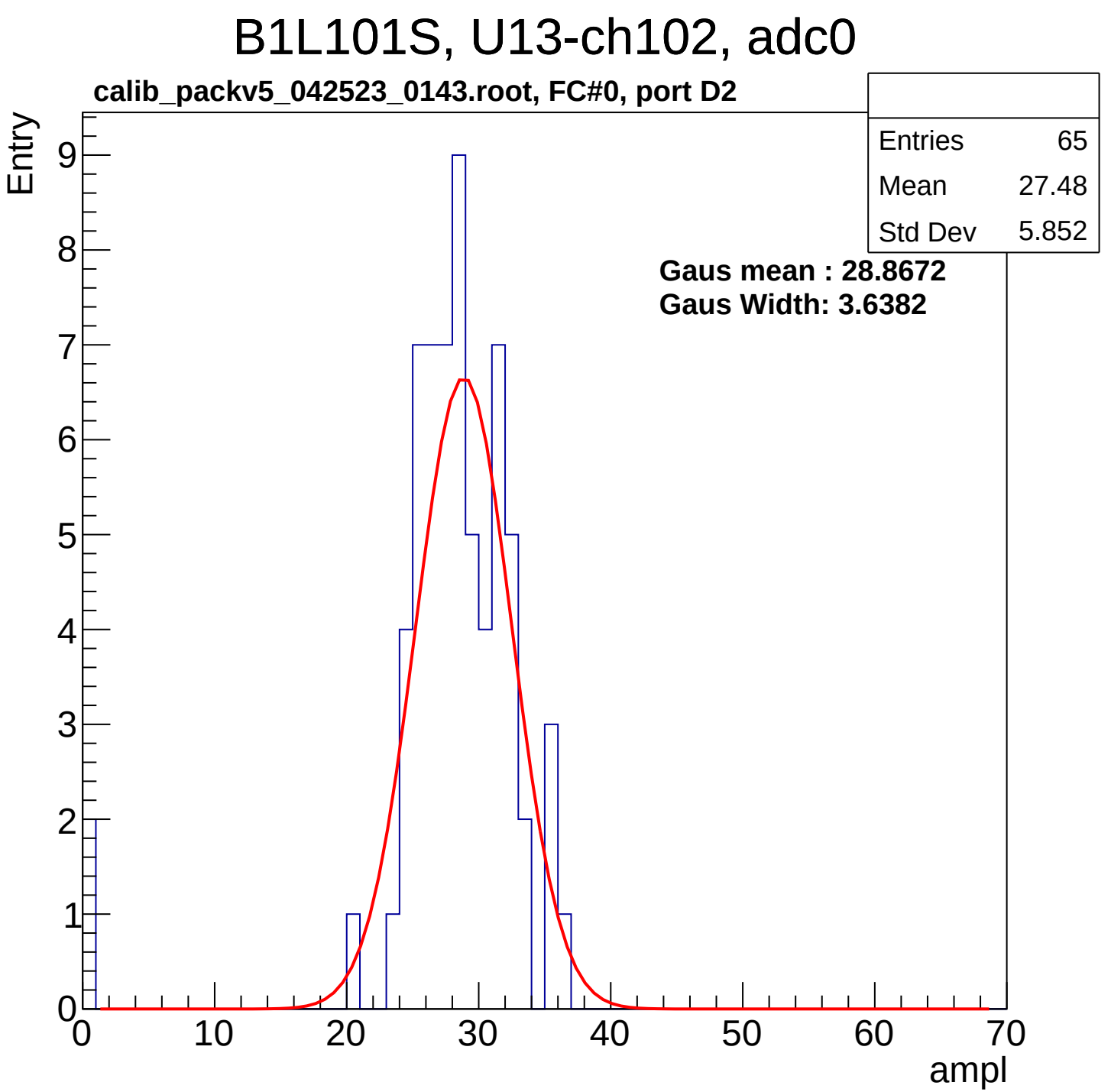
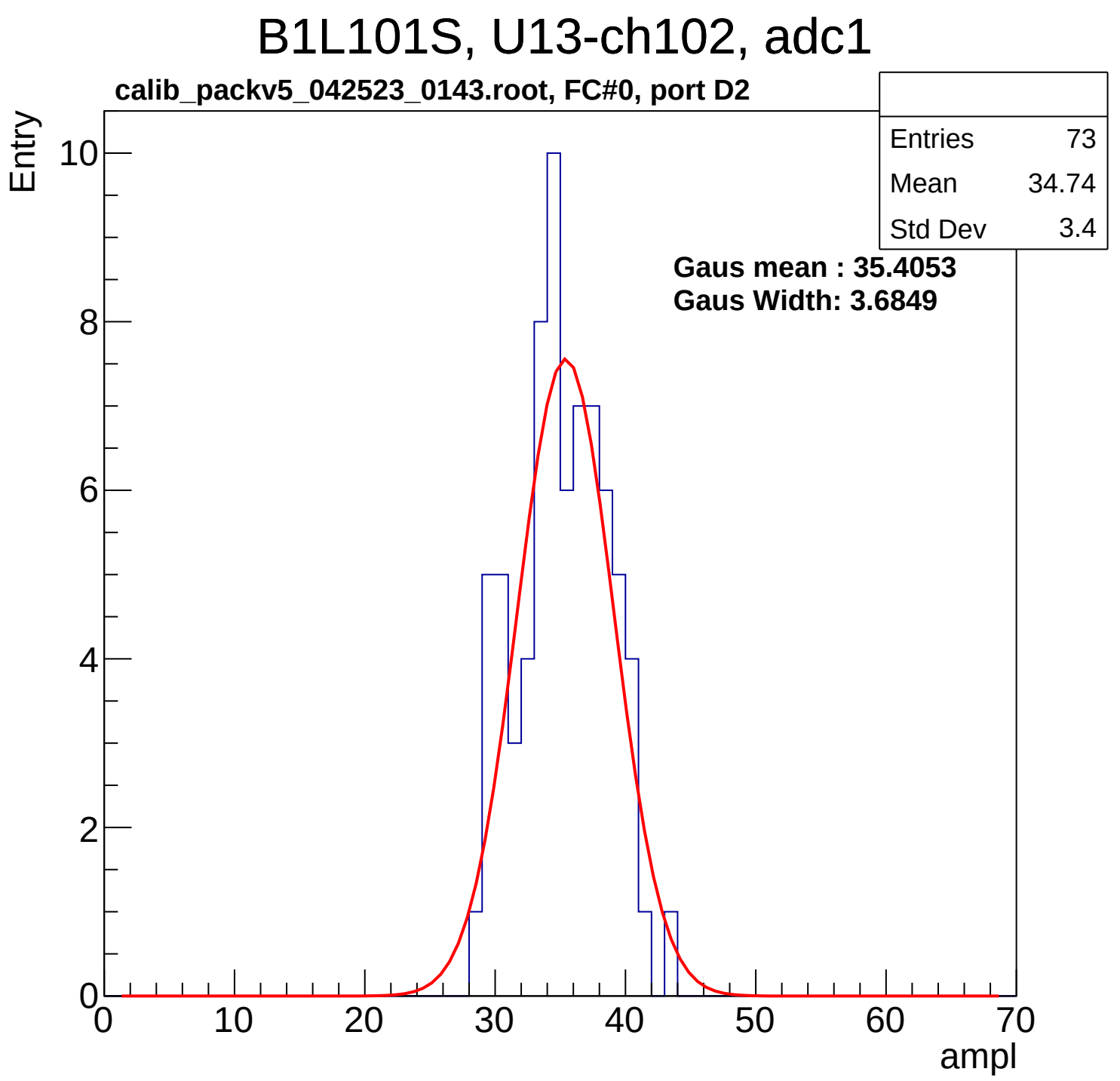
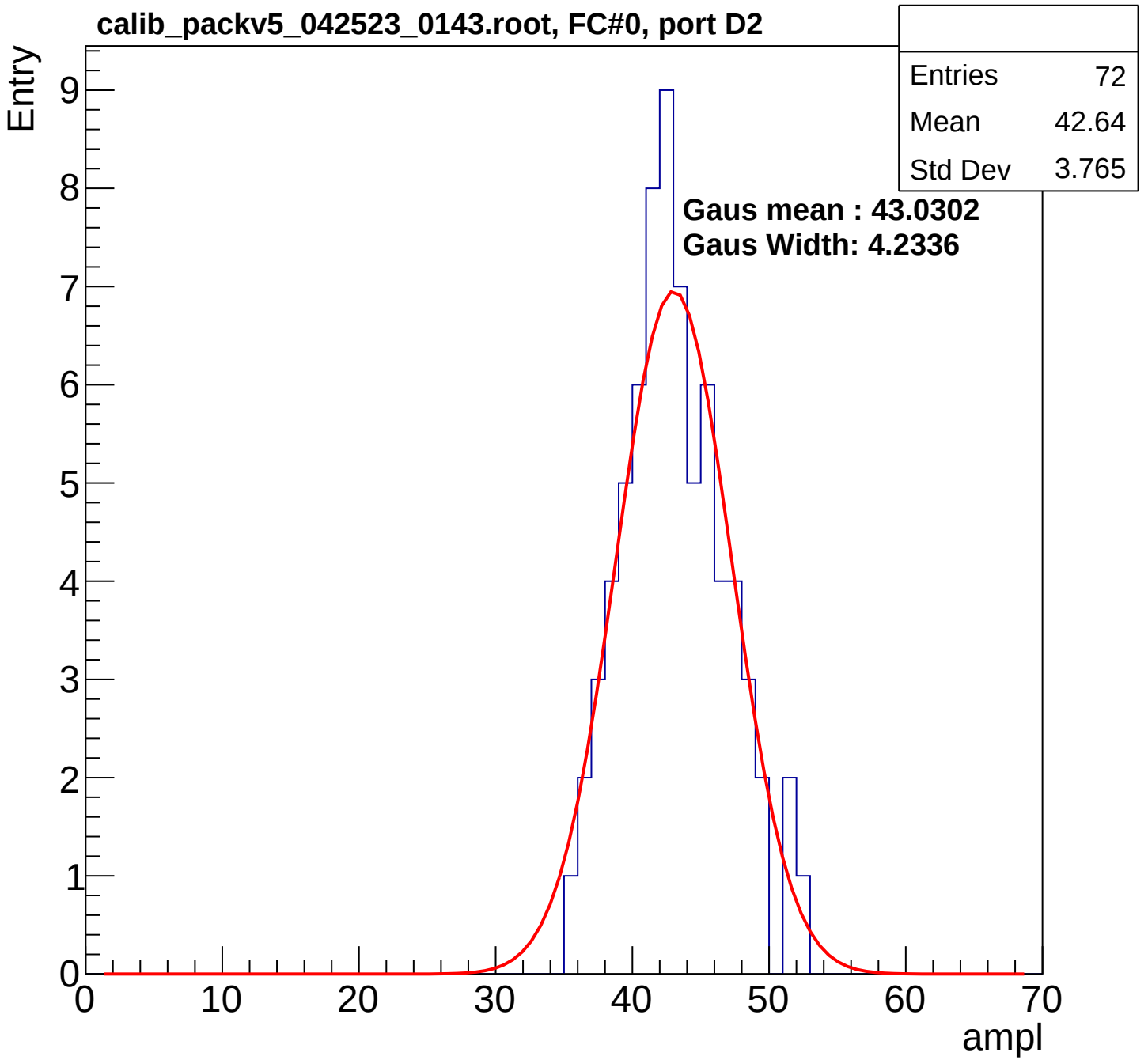
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch102, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 27.48
Std Dev 5.852
Gaus mean : 28.8672
Gaus Width: 3.6382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch102, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 34.74
Std Dev 3.4
Gaus mean : 35.4053
Gaus Width: 3.6849
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch102, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 42.64
Std Dev 3.765
Gaus mean : 43.0302
Gaus Width: 4.2336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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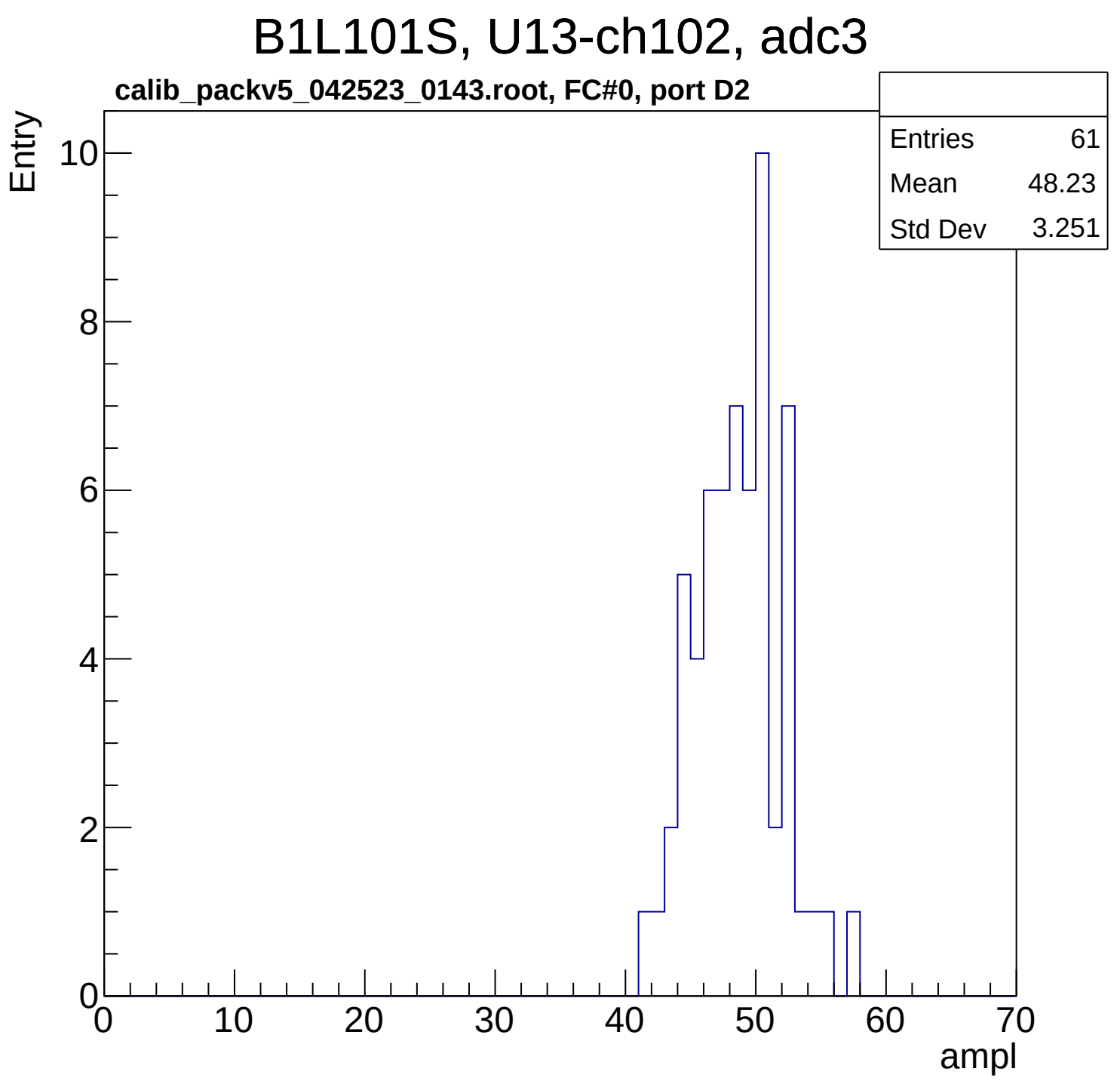
B1L101S, U13-ch102, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 48.23
Std Dev 3.251
Entry
ampl
0
2
4
6
8
10
0
10
20
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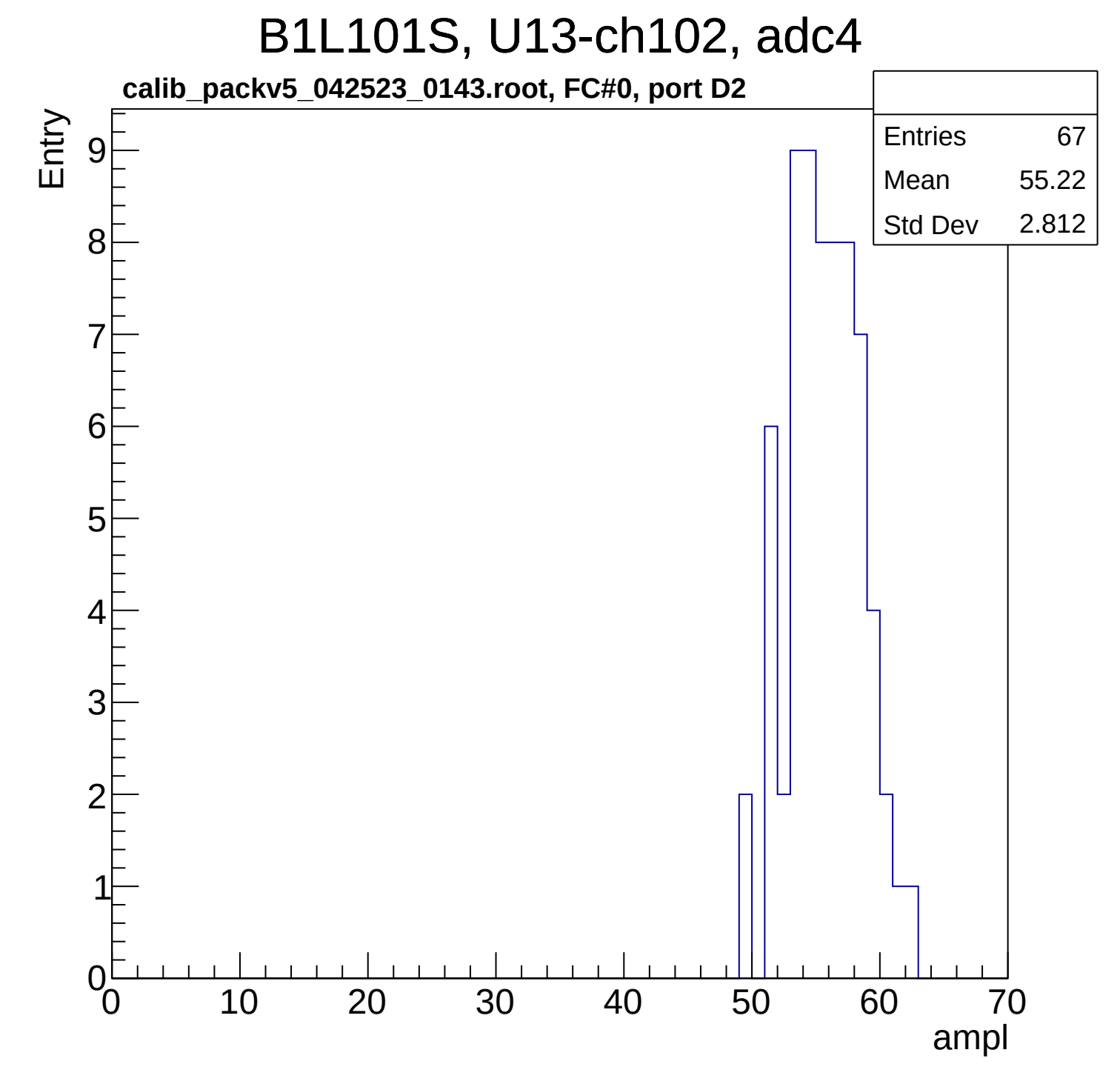
40
50
60
70


B1L101S, U13-ch102, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 55.22
Std Dev 2.812
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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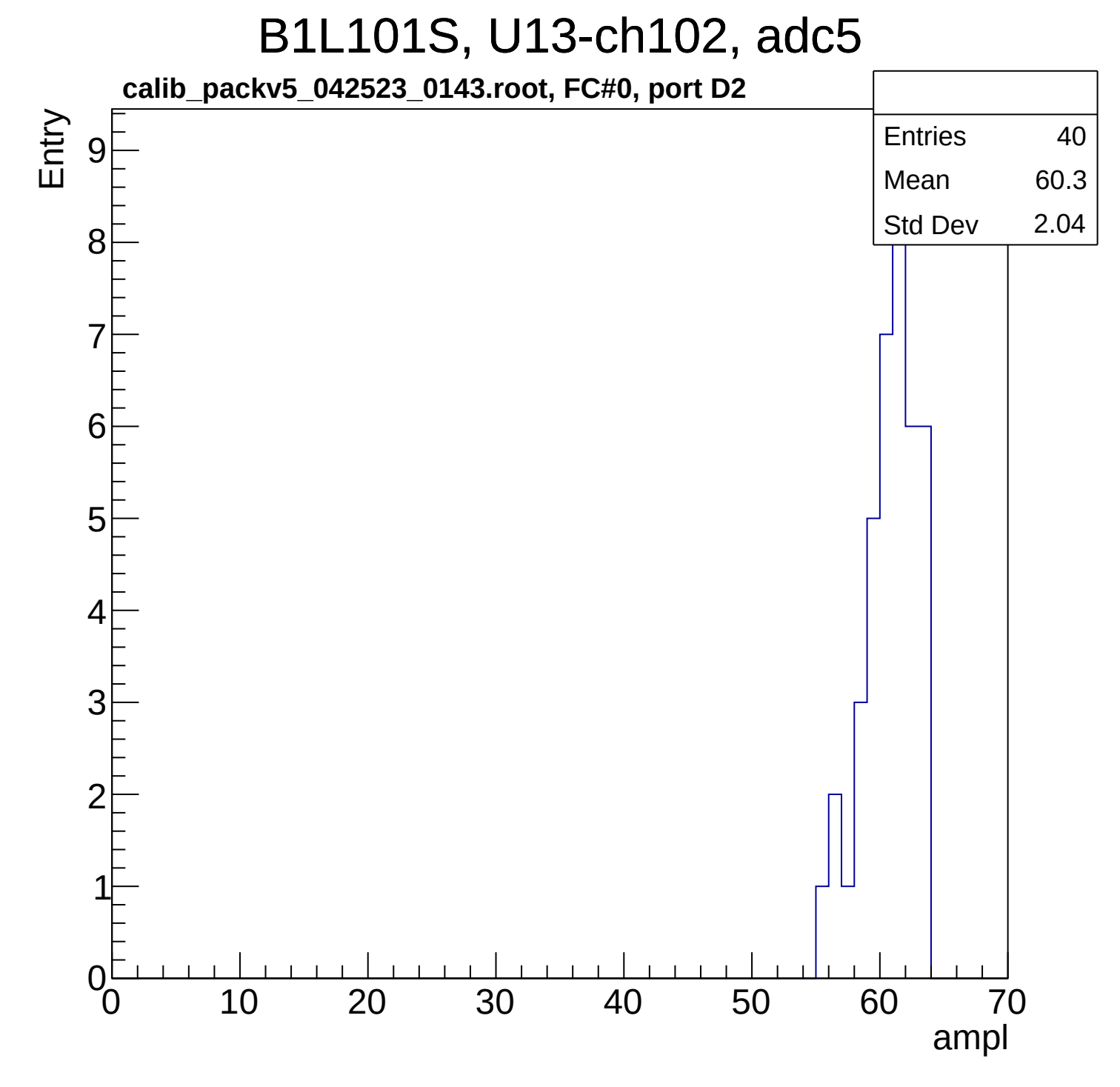
B1L101S, U13-ch102, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 60.3
Std Dev 2.04
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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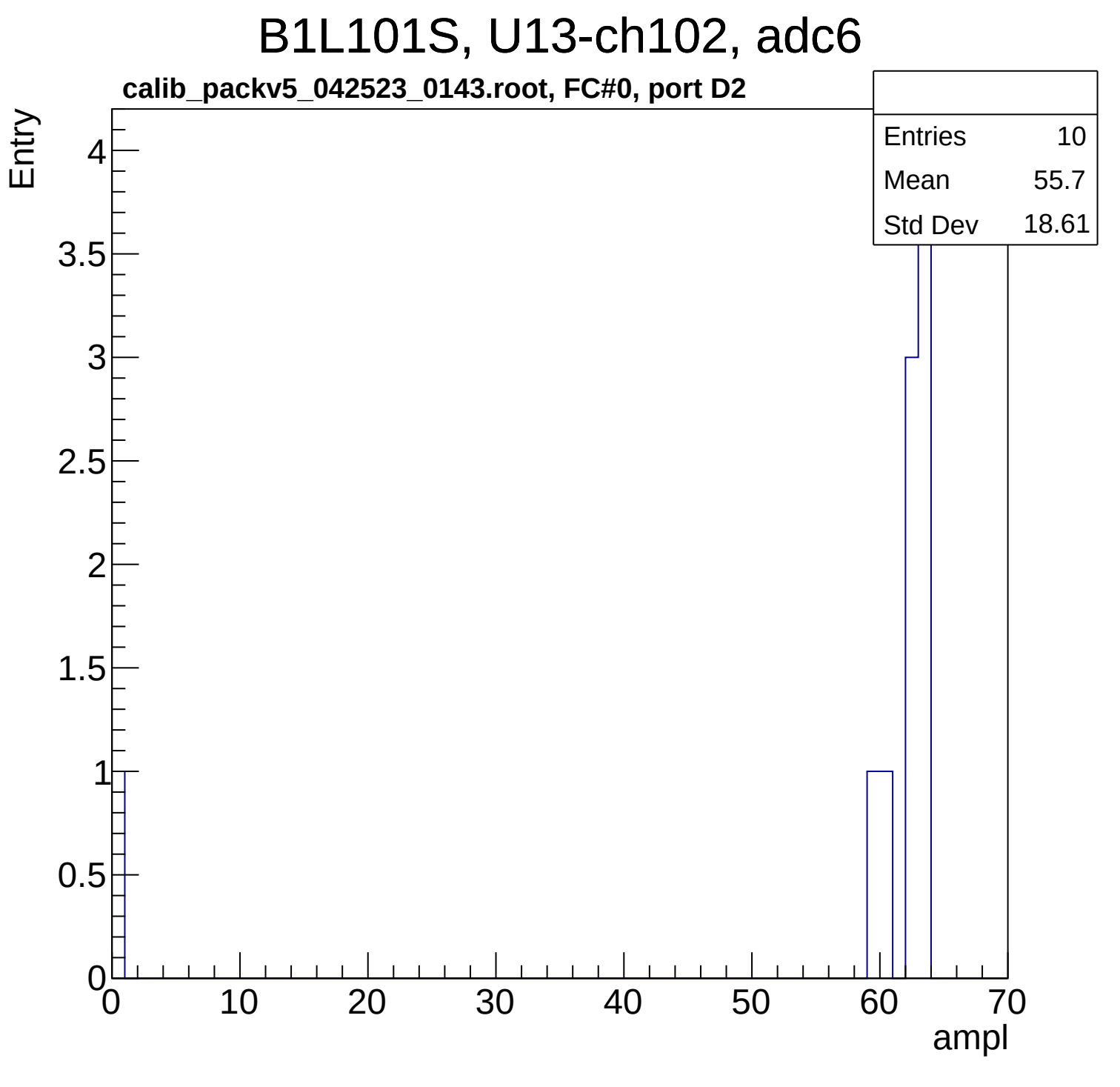
B1L101S, U13-ch102, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 10
Mean 55.7
Std Dev 18.61
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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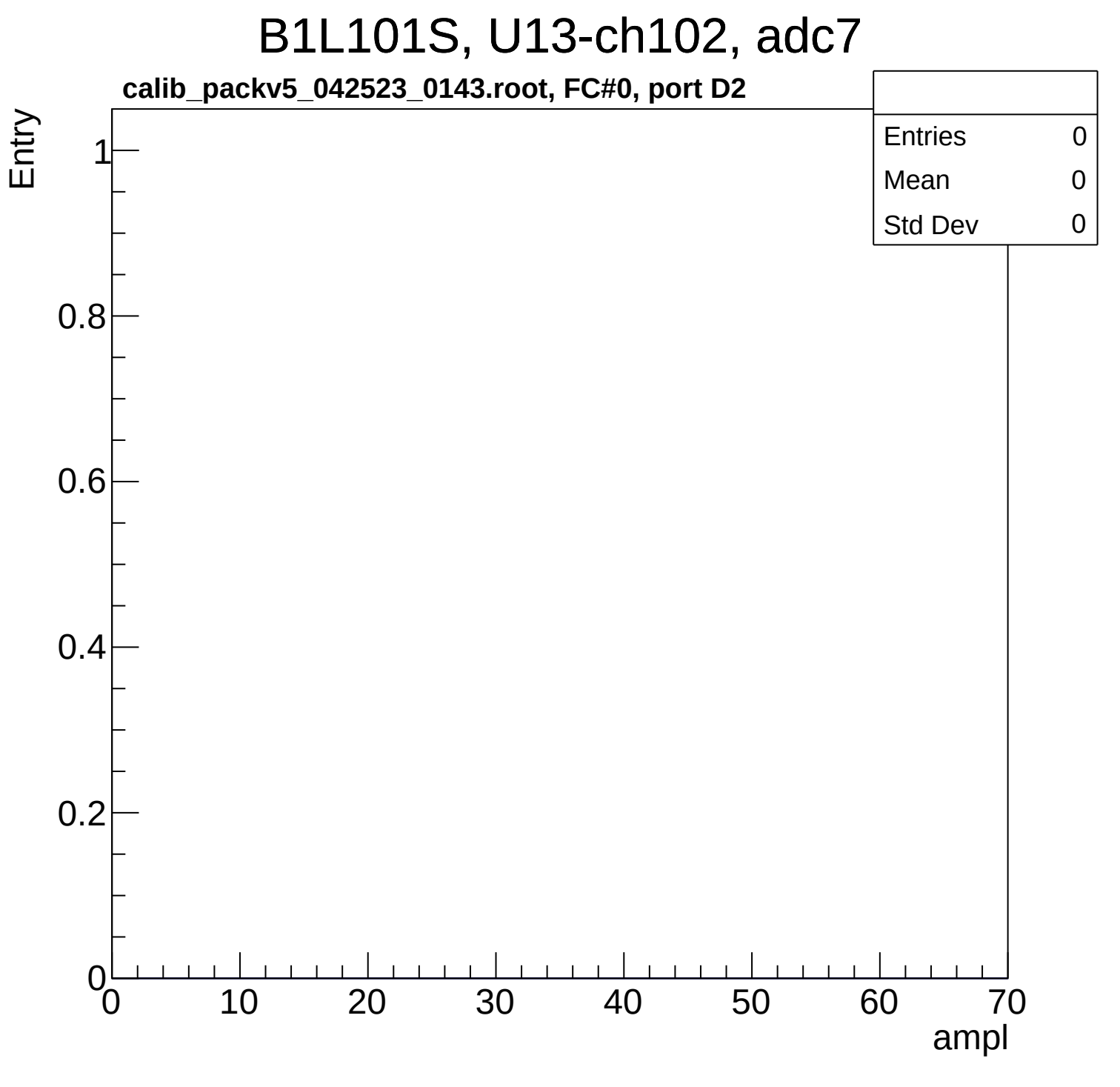
40
50
60
70


B1L101S, U13-ch102, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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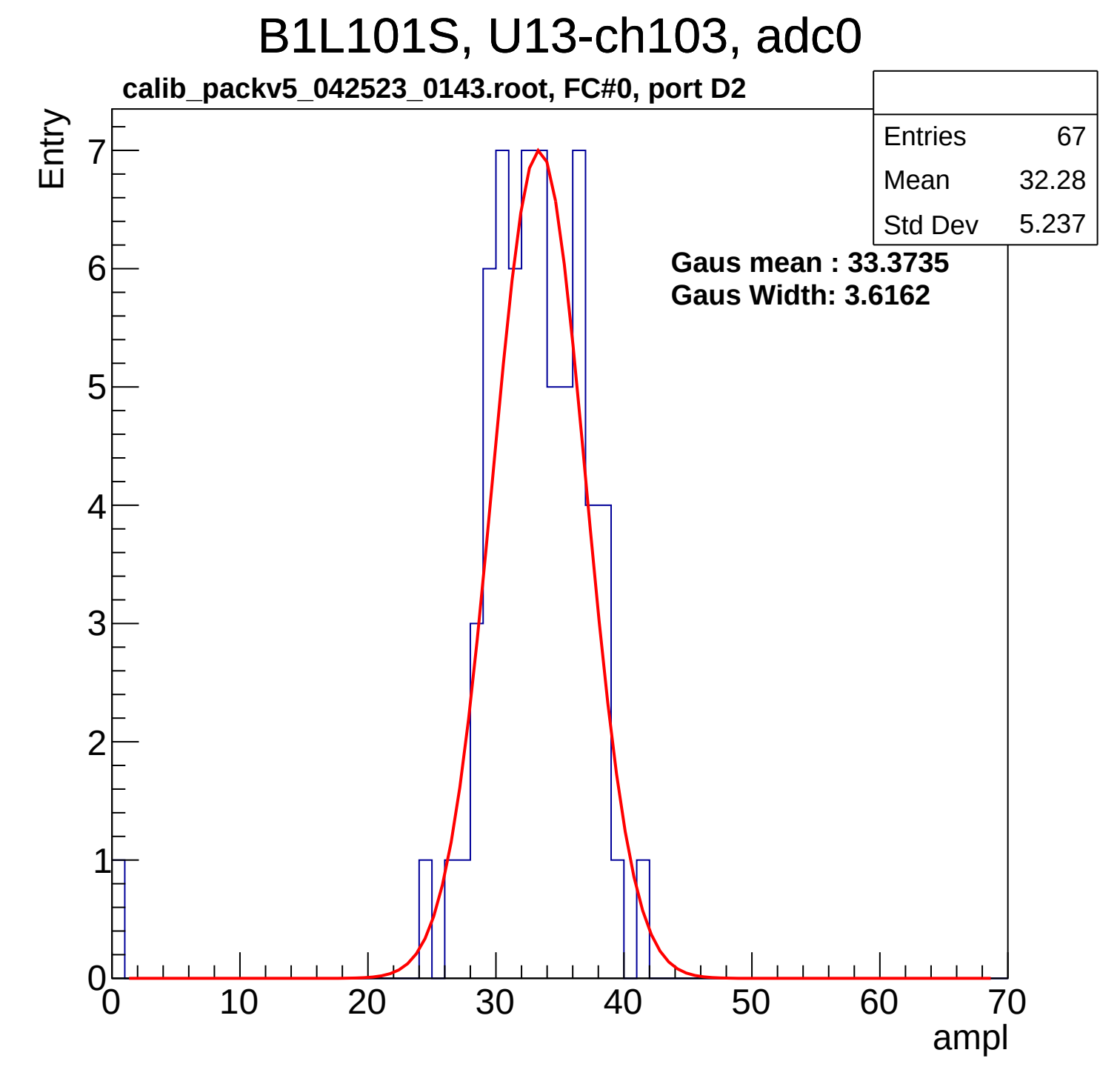
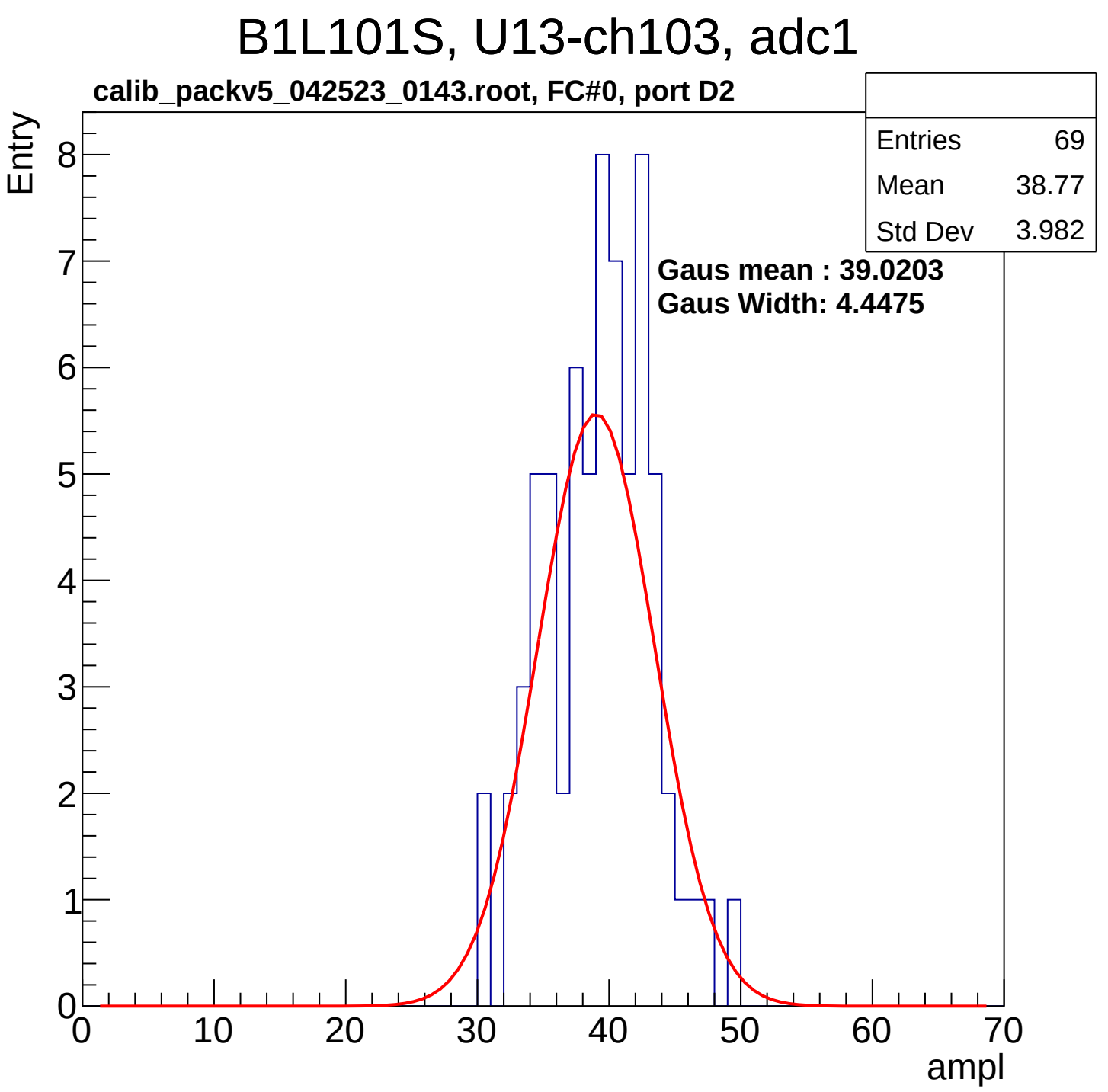
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch103, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 32.28
Std Dev 5.237
Gaus mean : 33.3735
Gaus Width: 3.6162
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch103, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 38.77
Std Dev 3.982
Gaus mean : 39.0203
Gaus Width: 4.4475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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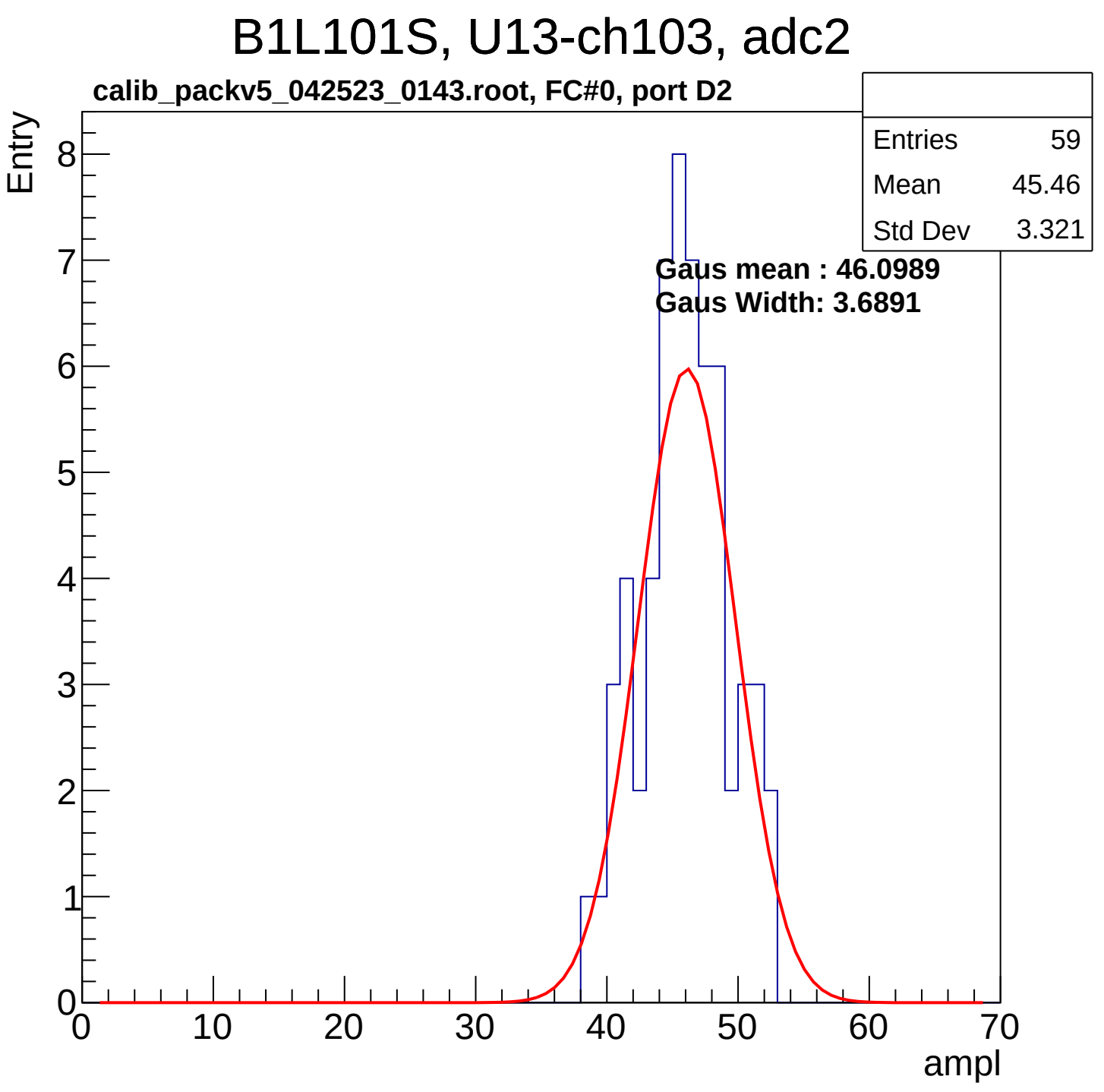
B1L101S, U13-ch103, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 45.46
Std Dev 3.321
Gaus mean : 46.0989
Gaus Width: 3.6891
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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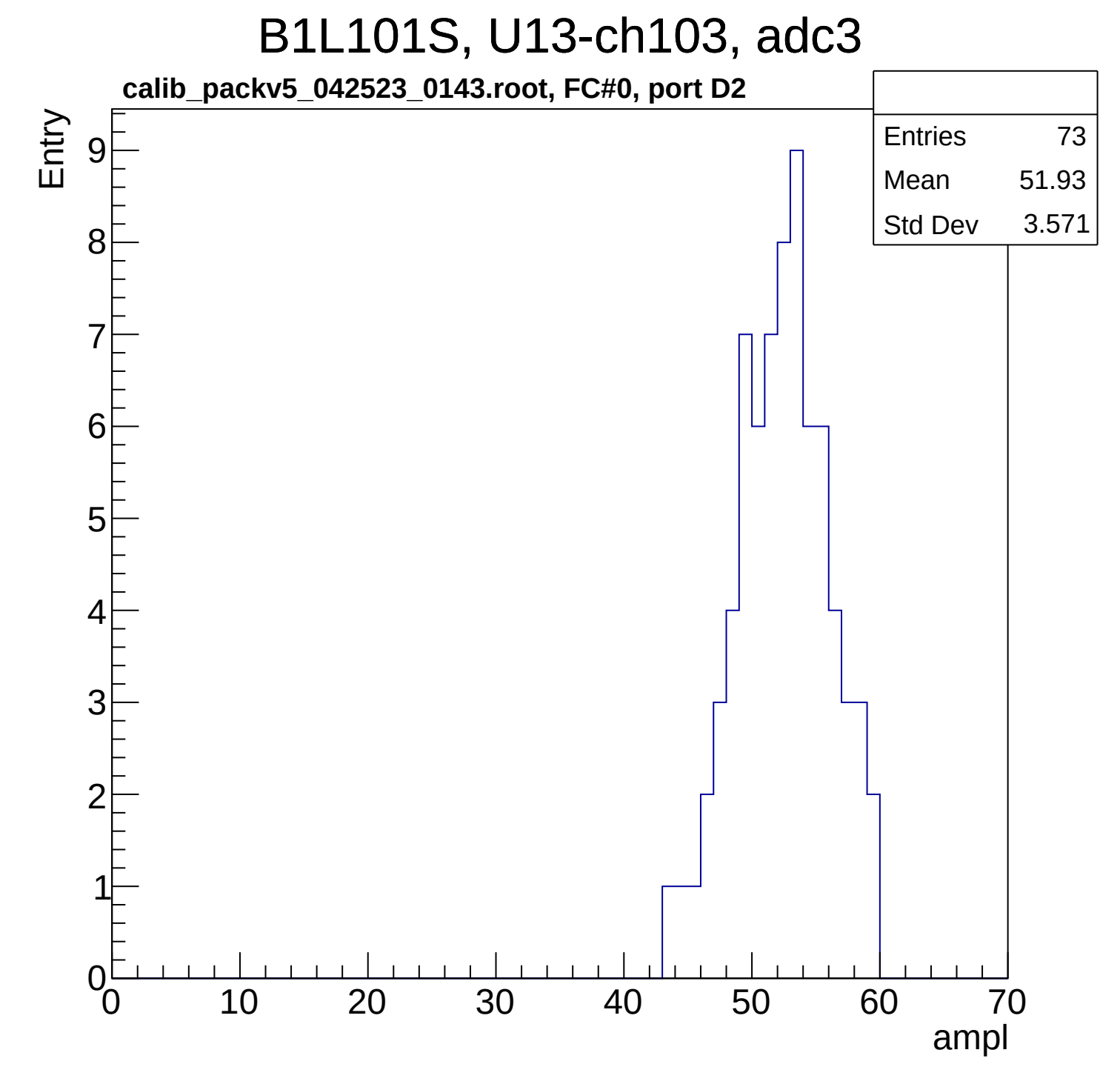
B1L101S, U13-ch103, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 51.93
Std Dev 3.571
Entry
ampl
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2
3
4
5
6
7
8
9
0
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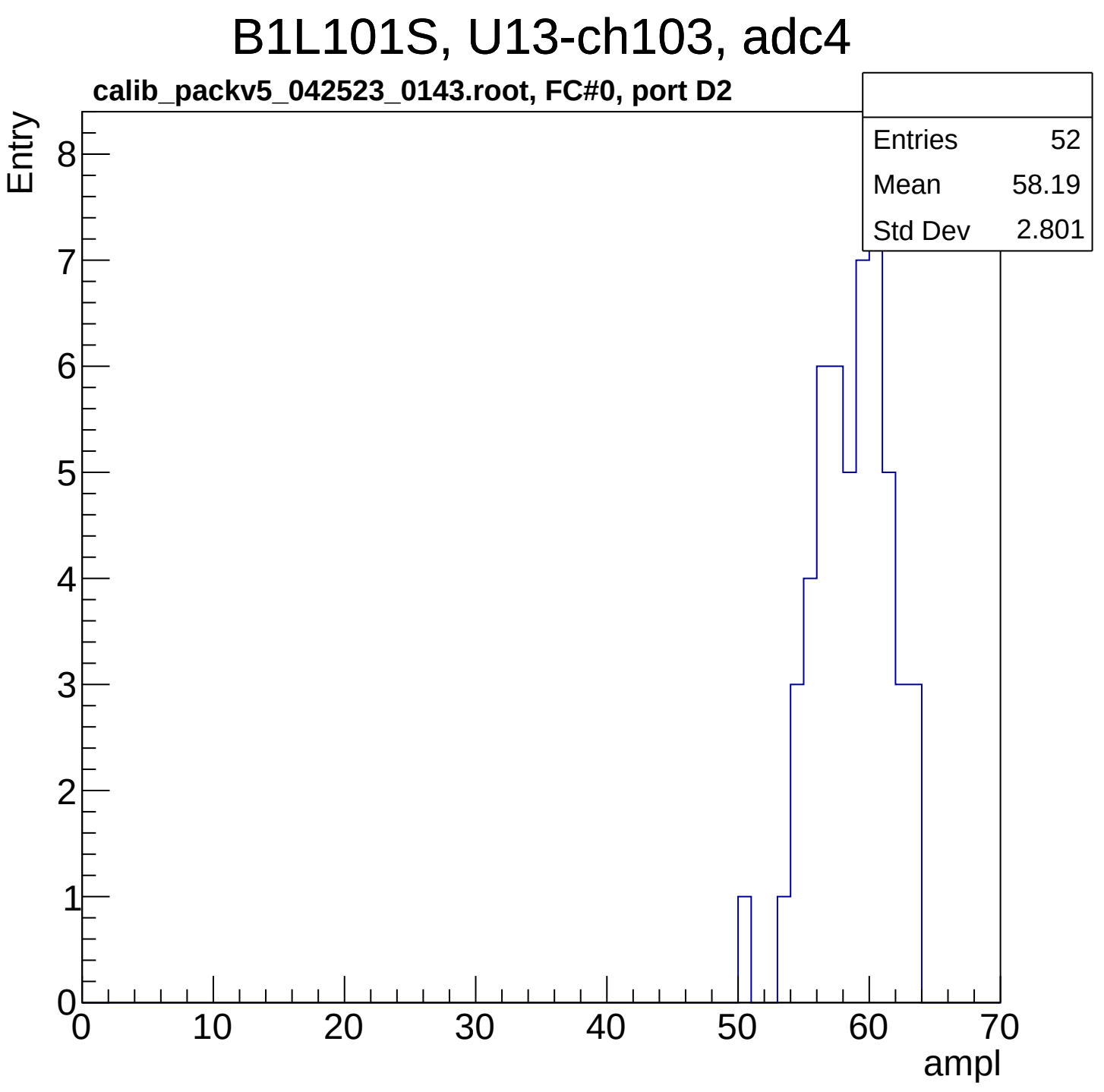
B1L101S, U13-ch103, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 52
Mean 58.19
Std Dev 2.801
Entry
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2
3
4
5
6
7
8
0
10
20
30
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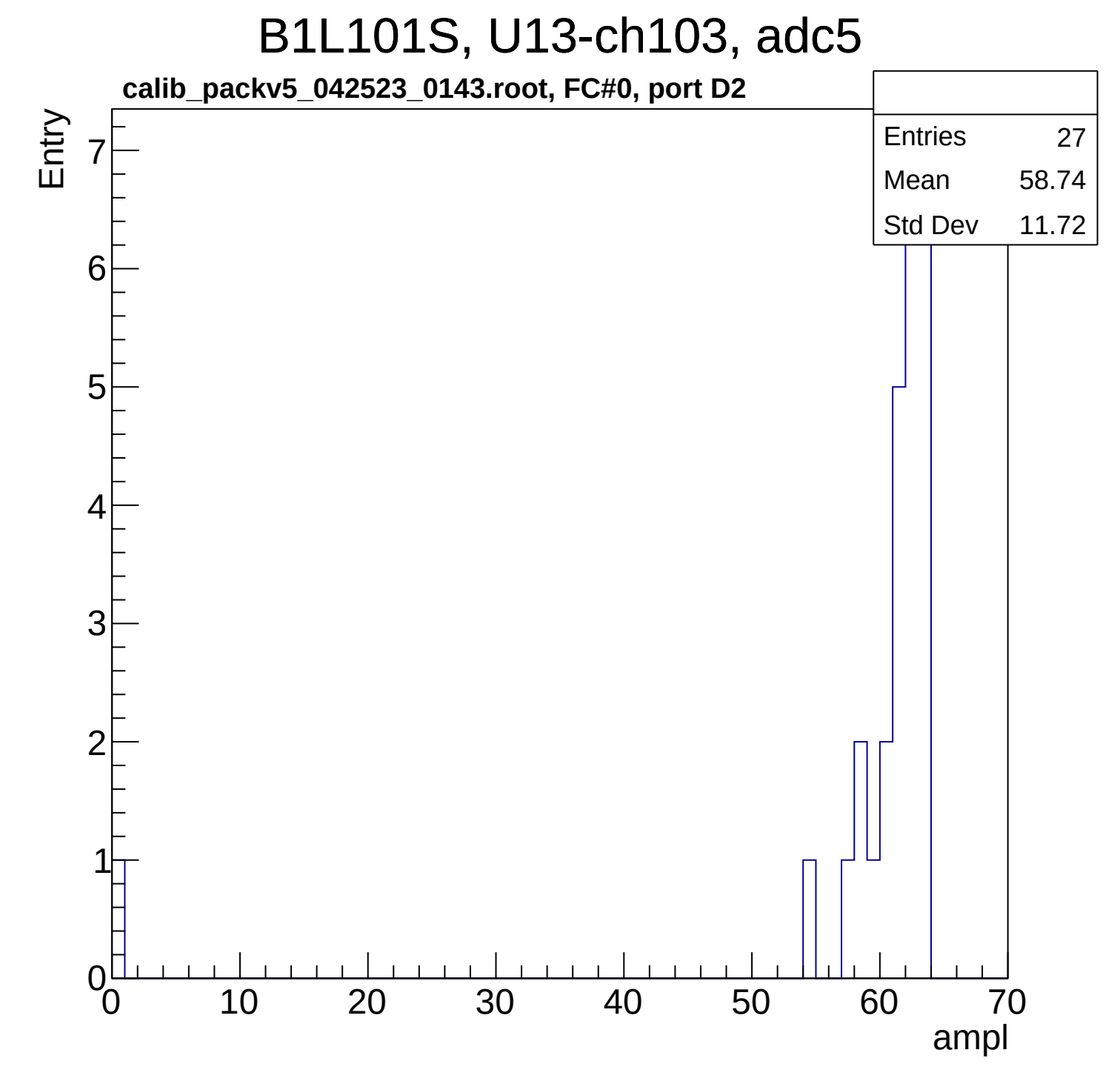
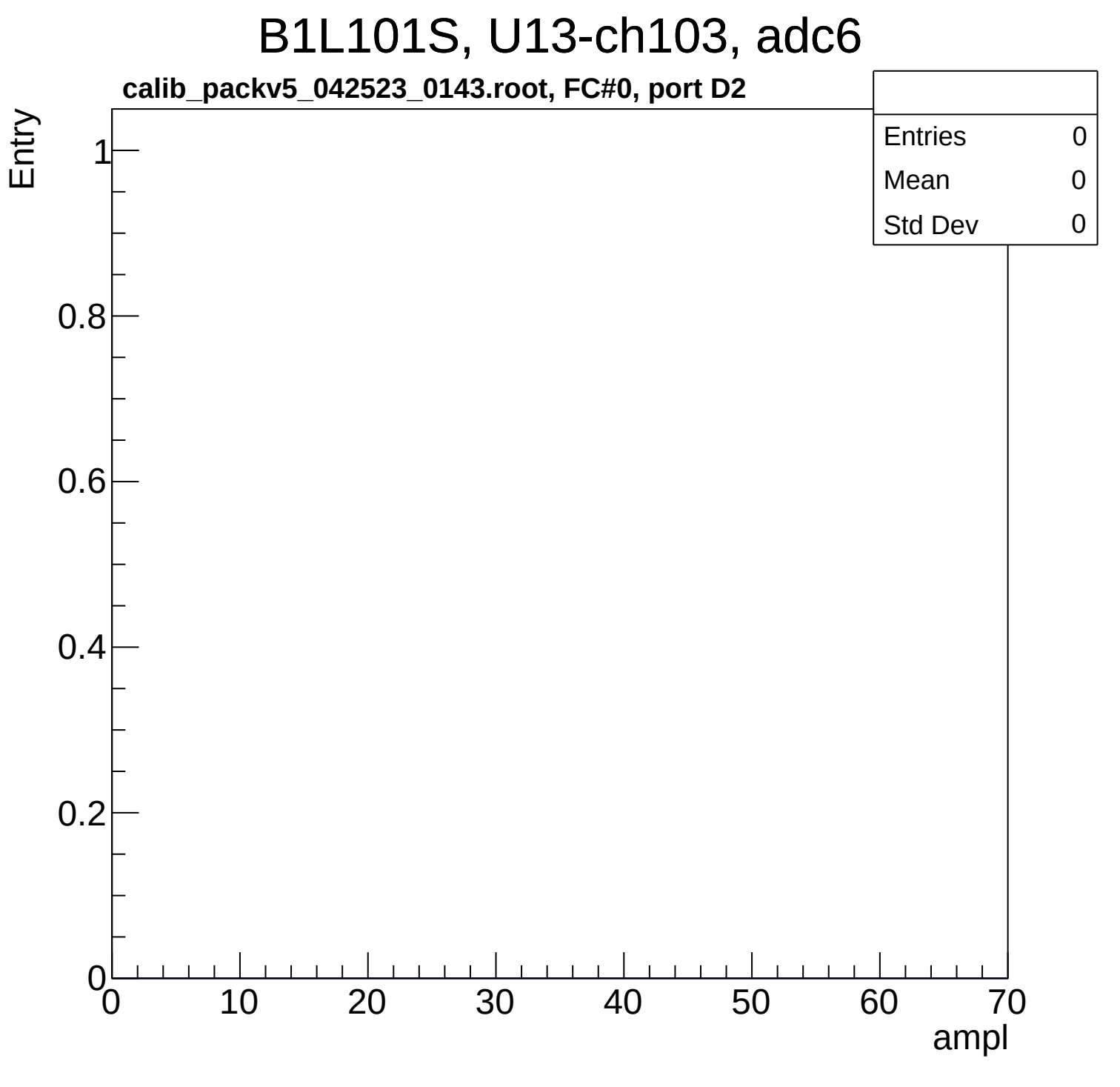
50
60
70
ampl


B1L101S, U13-ch103, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 27
Mean 58.74
Std Dev 11.72
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch103, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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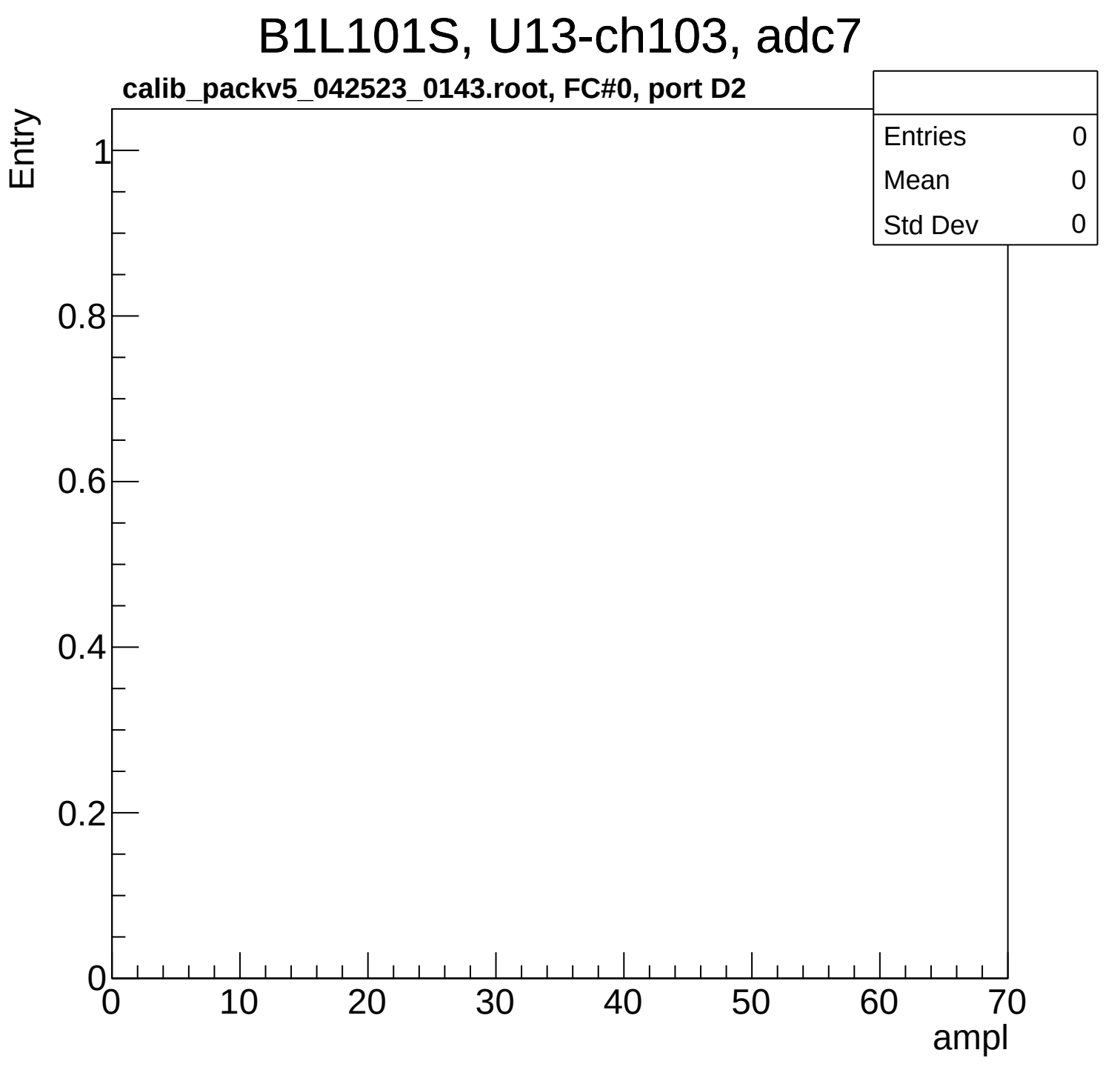
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch103, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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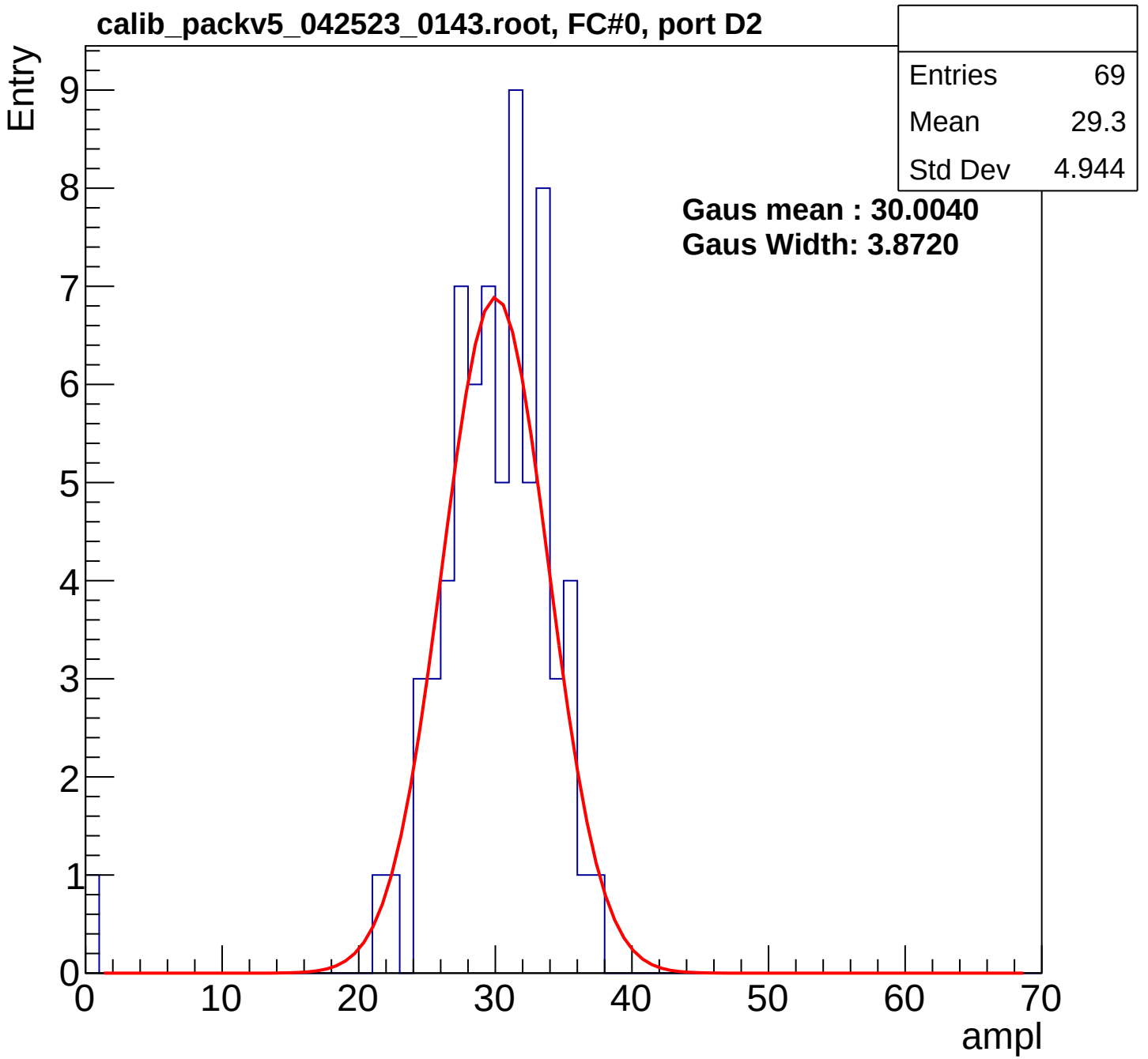
50
60
70
ampl


B1L101S, U13-ch104, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 29.3
Std Dev 4.944
Gaus mean : 30.0040
Gaus Width: 3.8720
Entry
ampl
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2
3
4
5
6
7
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0
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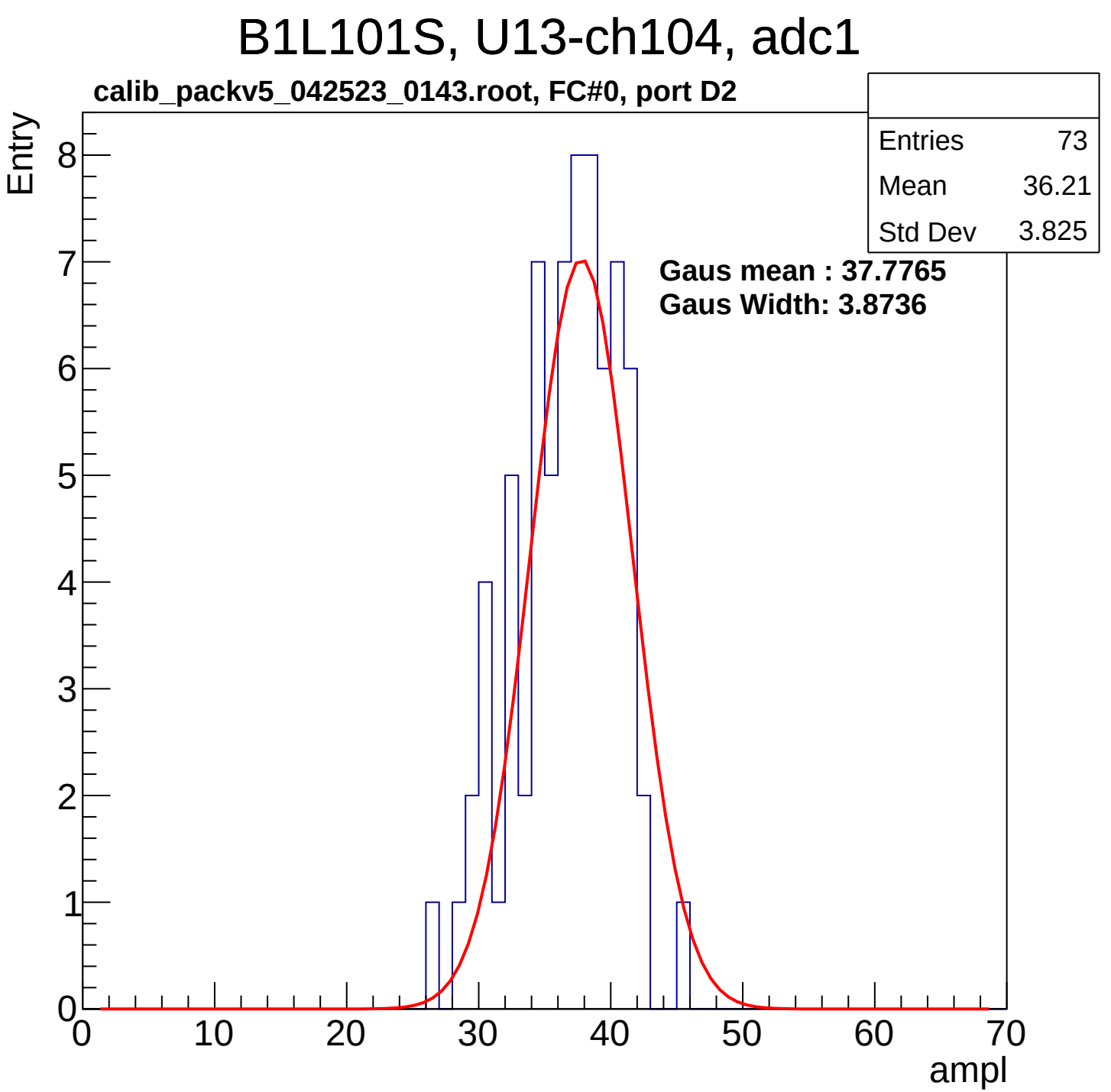
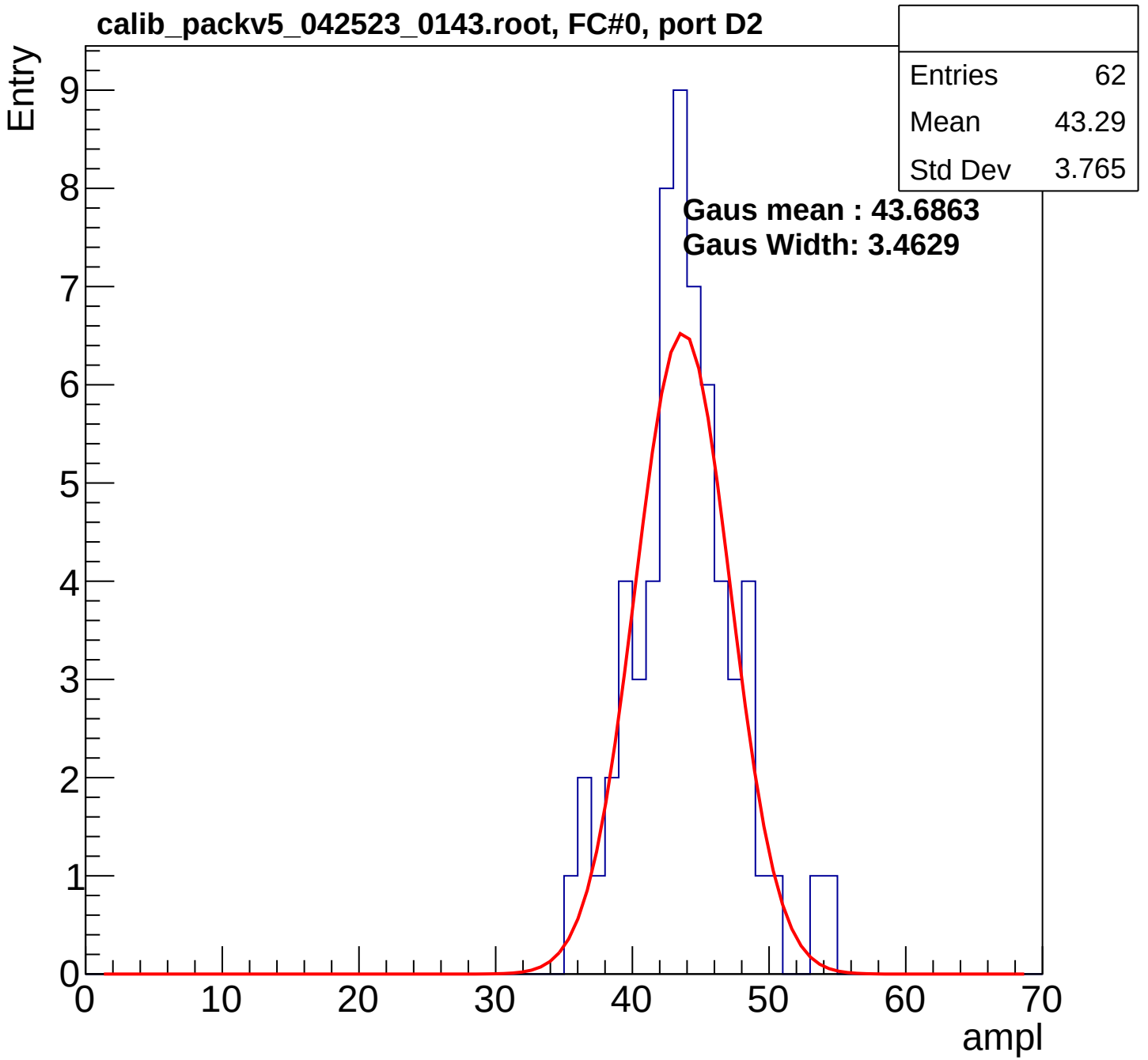
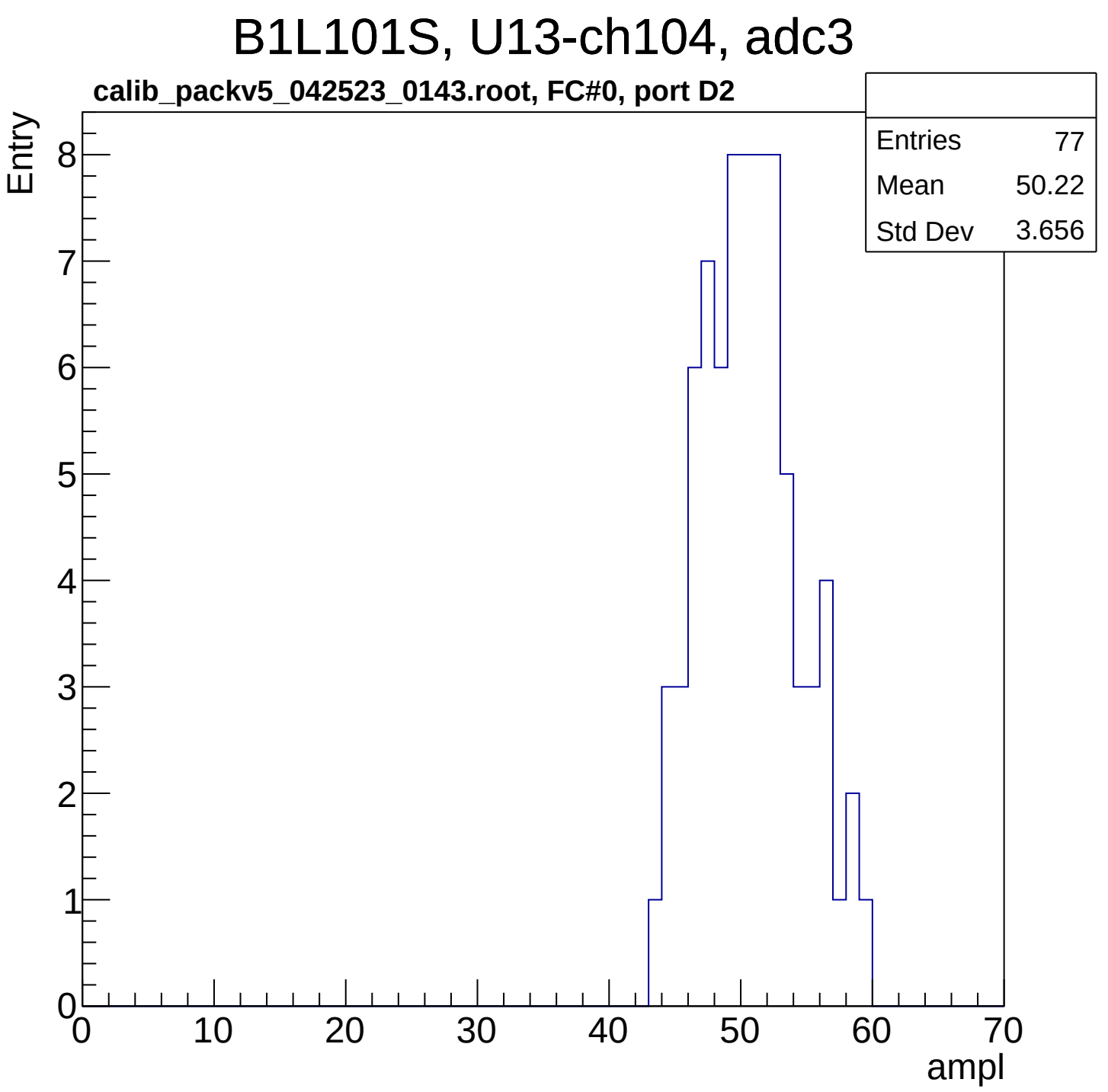
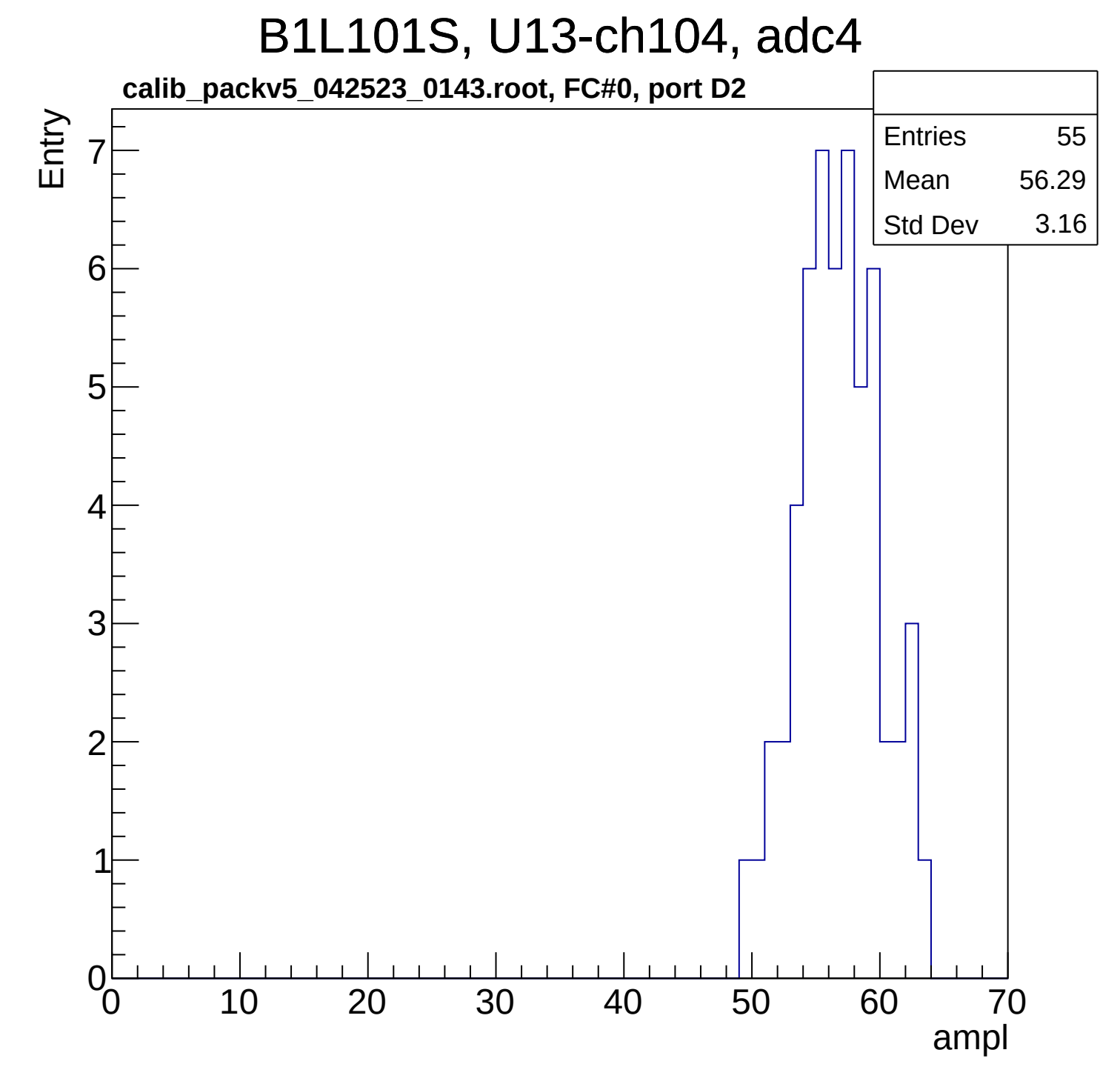
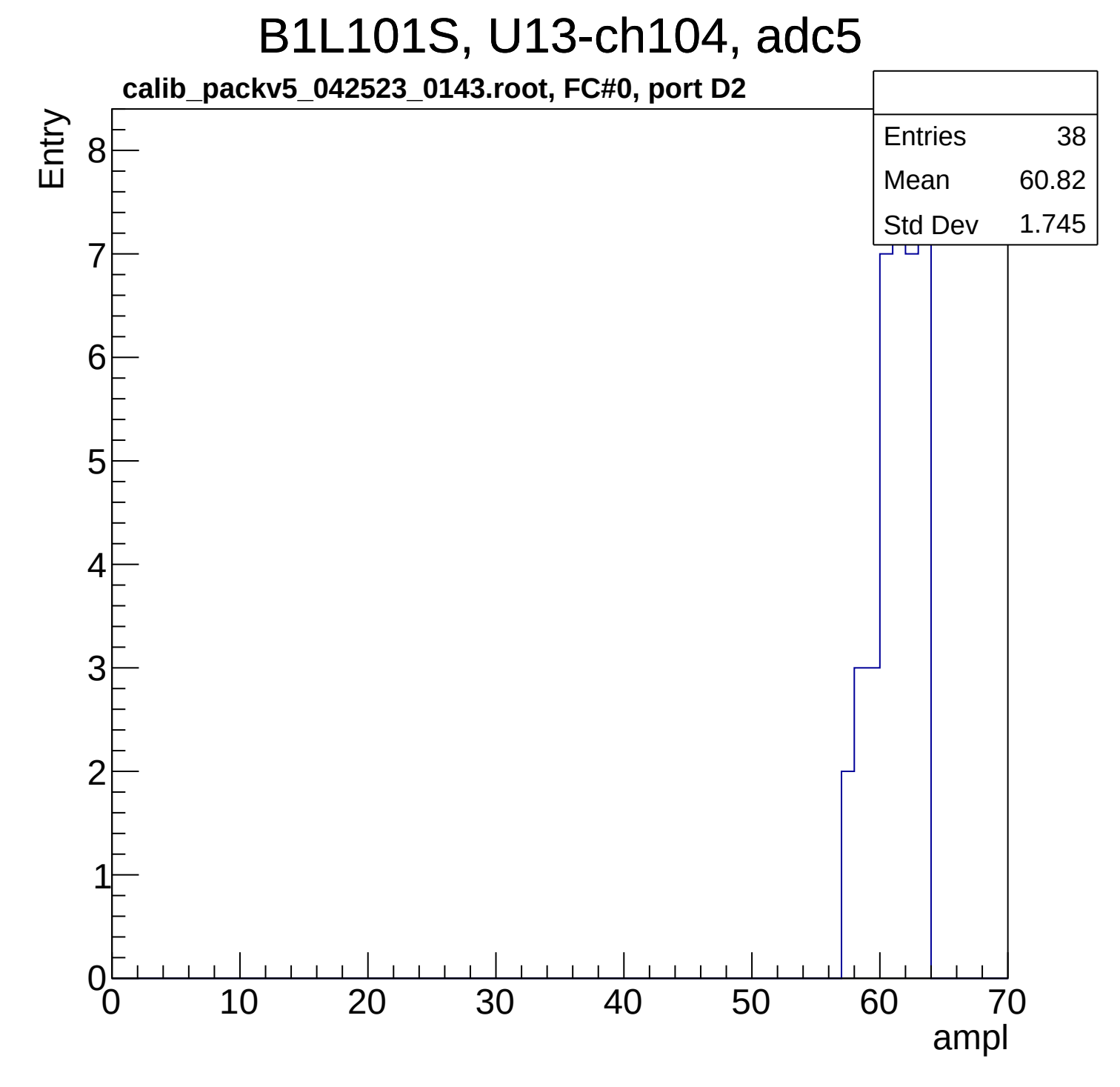
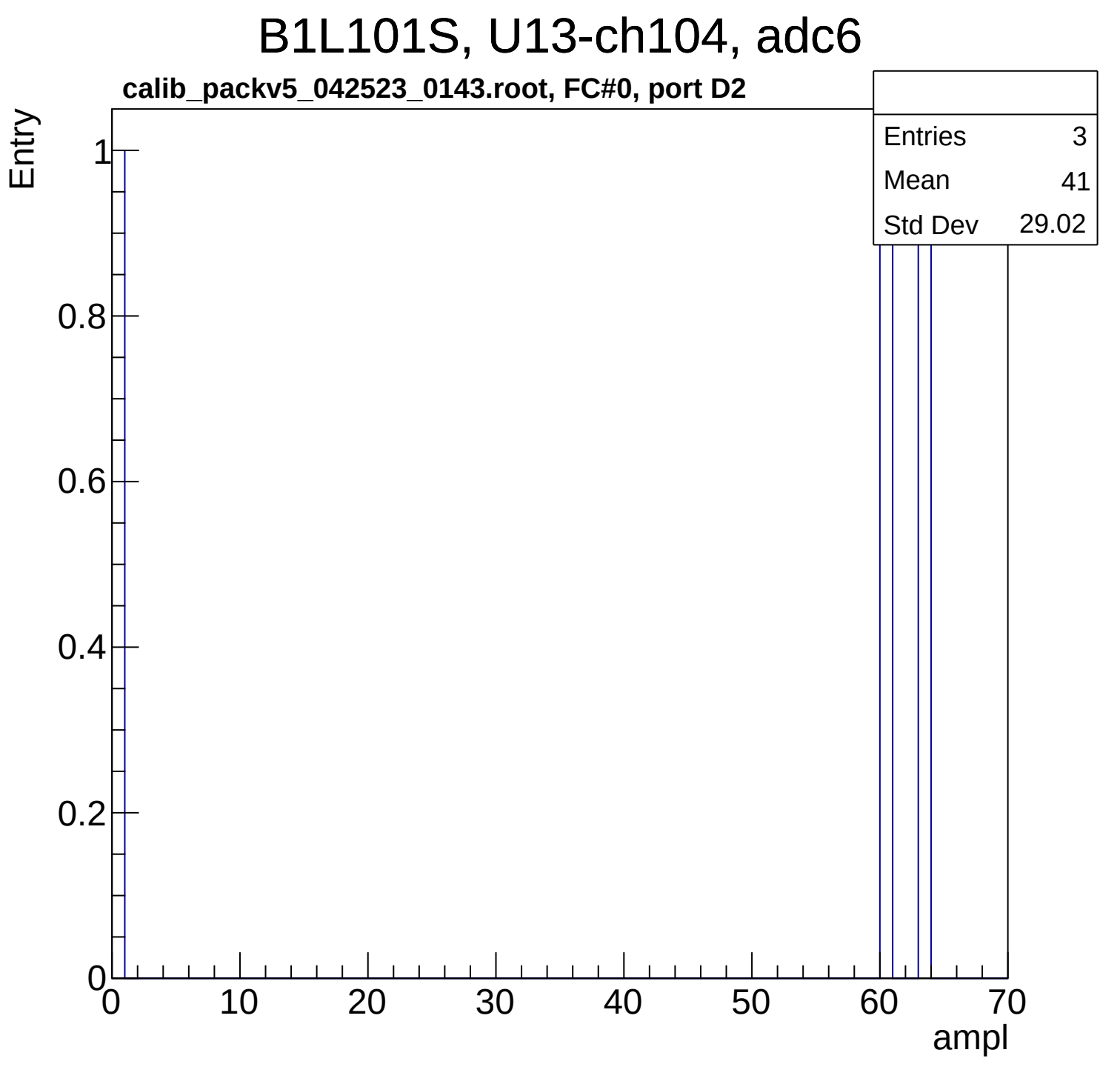

B1L101S, U13-ch104, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 36.21
Std Dev 3.825
Gaus mean : 37.7765
Gaus Width: 3.8736
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

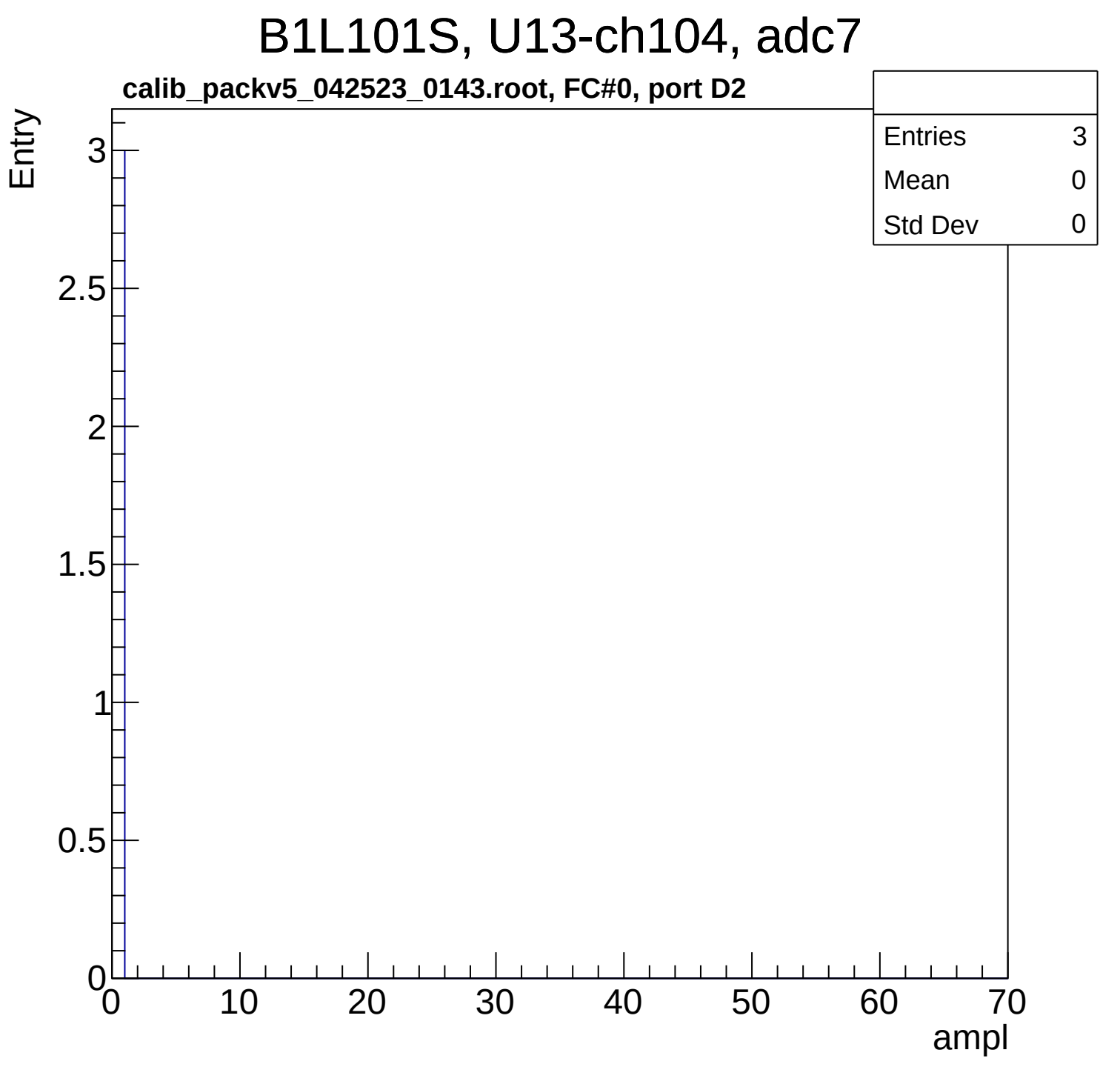

B1L101S, U13-ch104, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 43.29
Std Dev 3.765
Gaus mean : 43.6863
Gaus Width: 3.4629
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

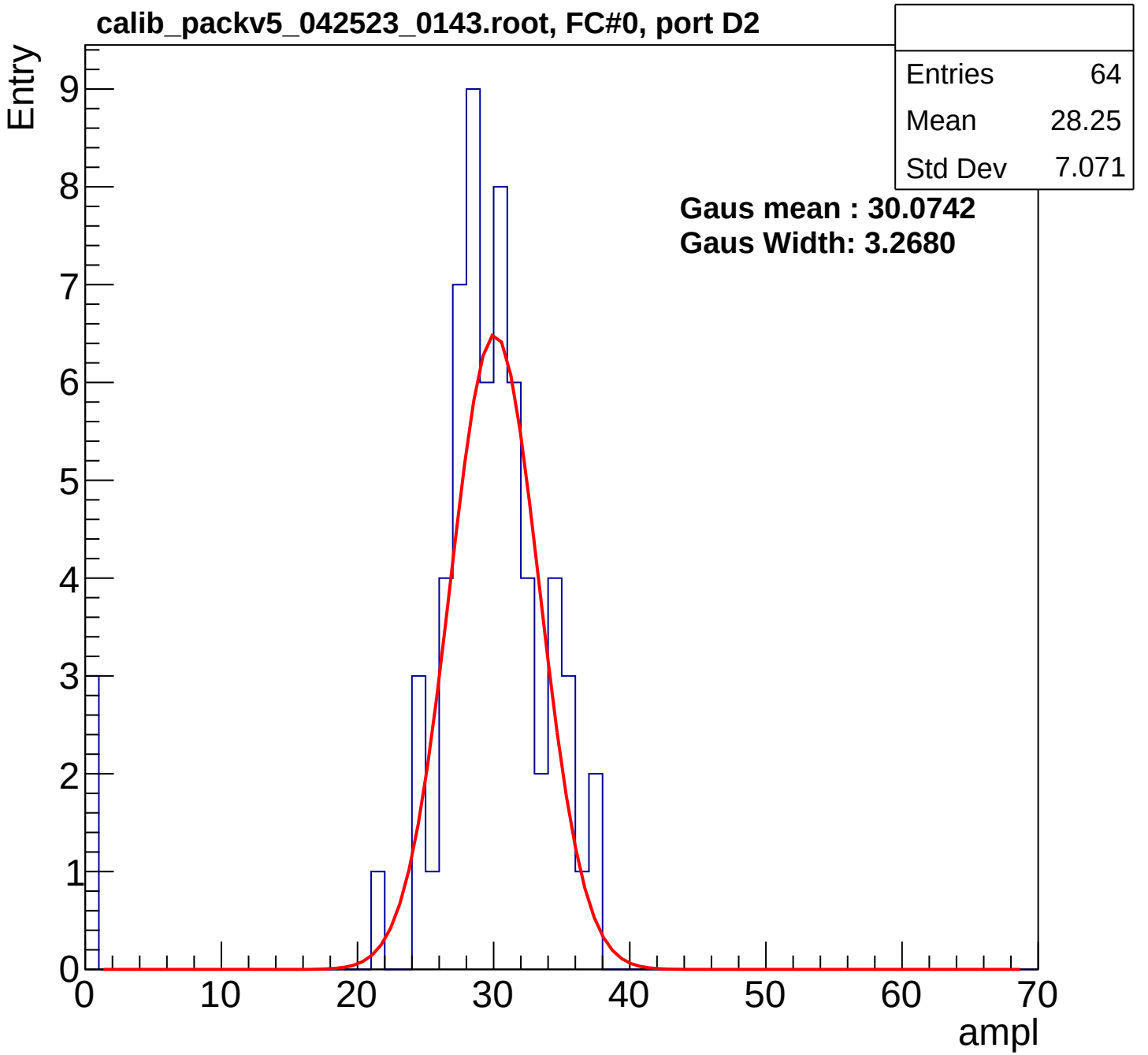

B1L101S, U13-ch104, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 50.22
Std Dev 3.656
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

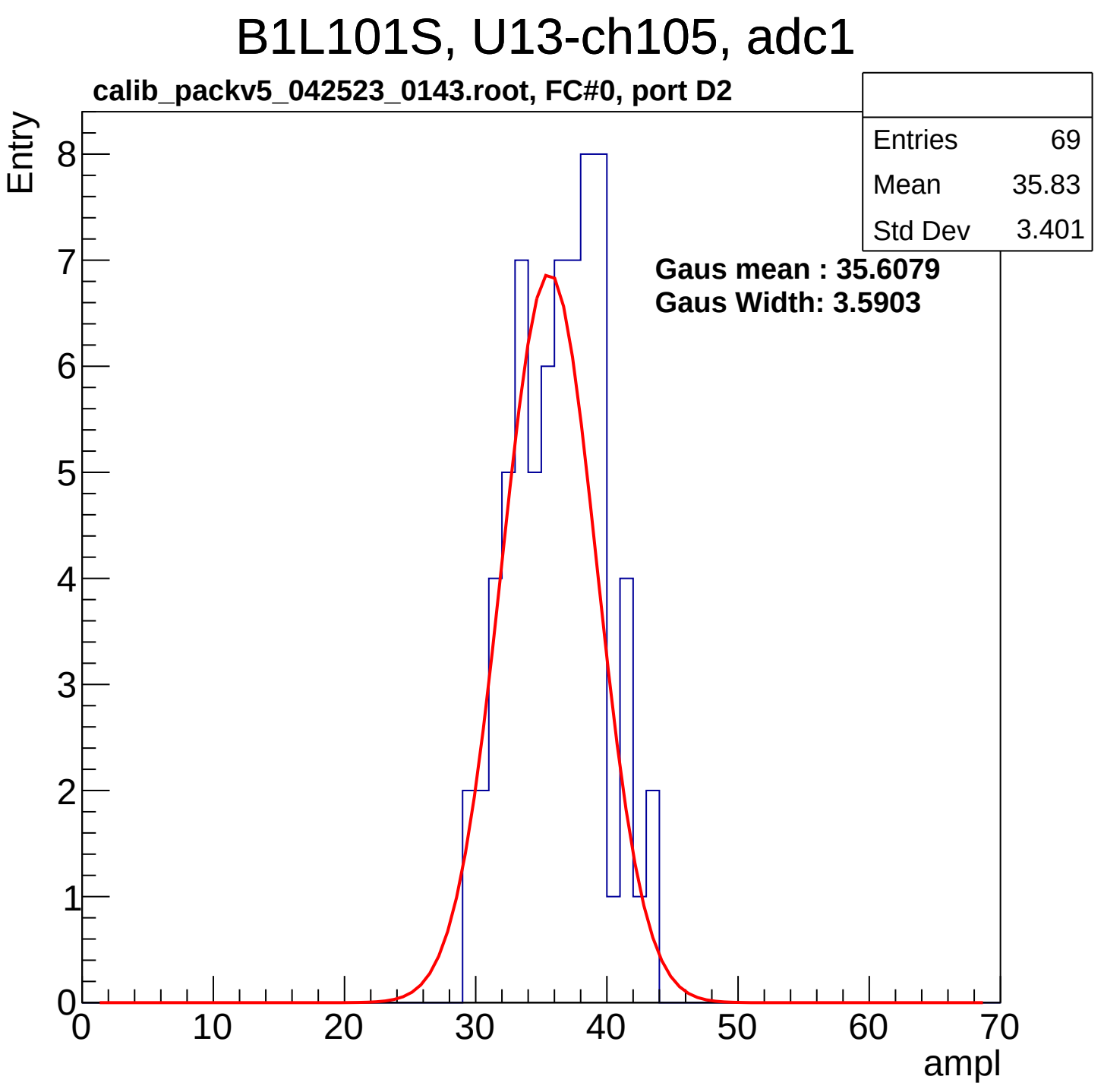

B1L101S, U13-ch104, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 56.29
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

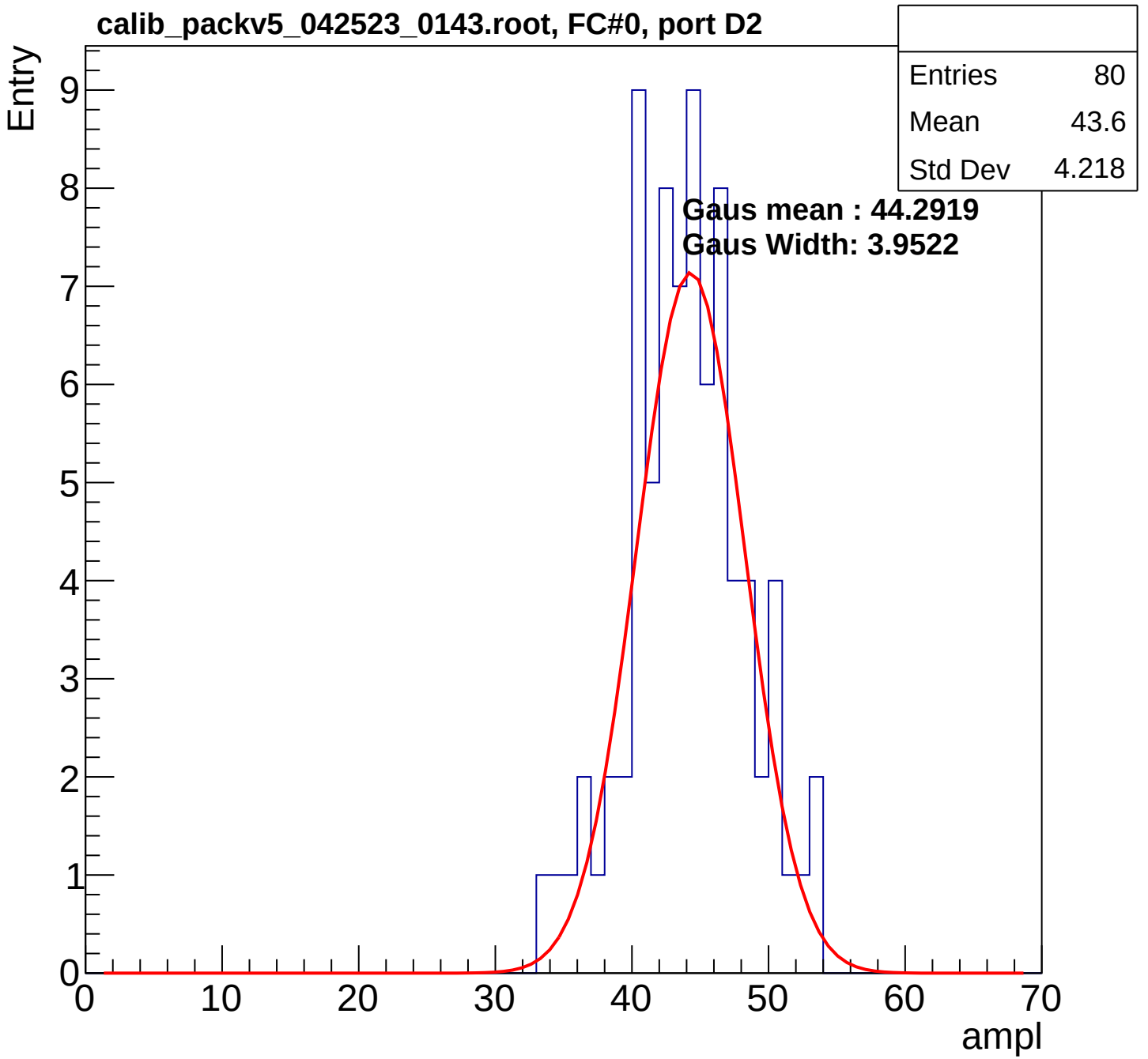

B1L101S, U13-ch104, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 38
Mean 60.82
Std Dev 1.745
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

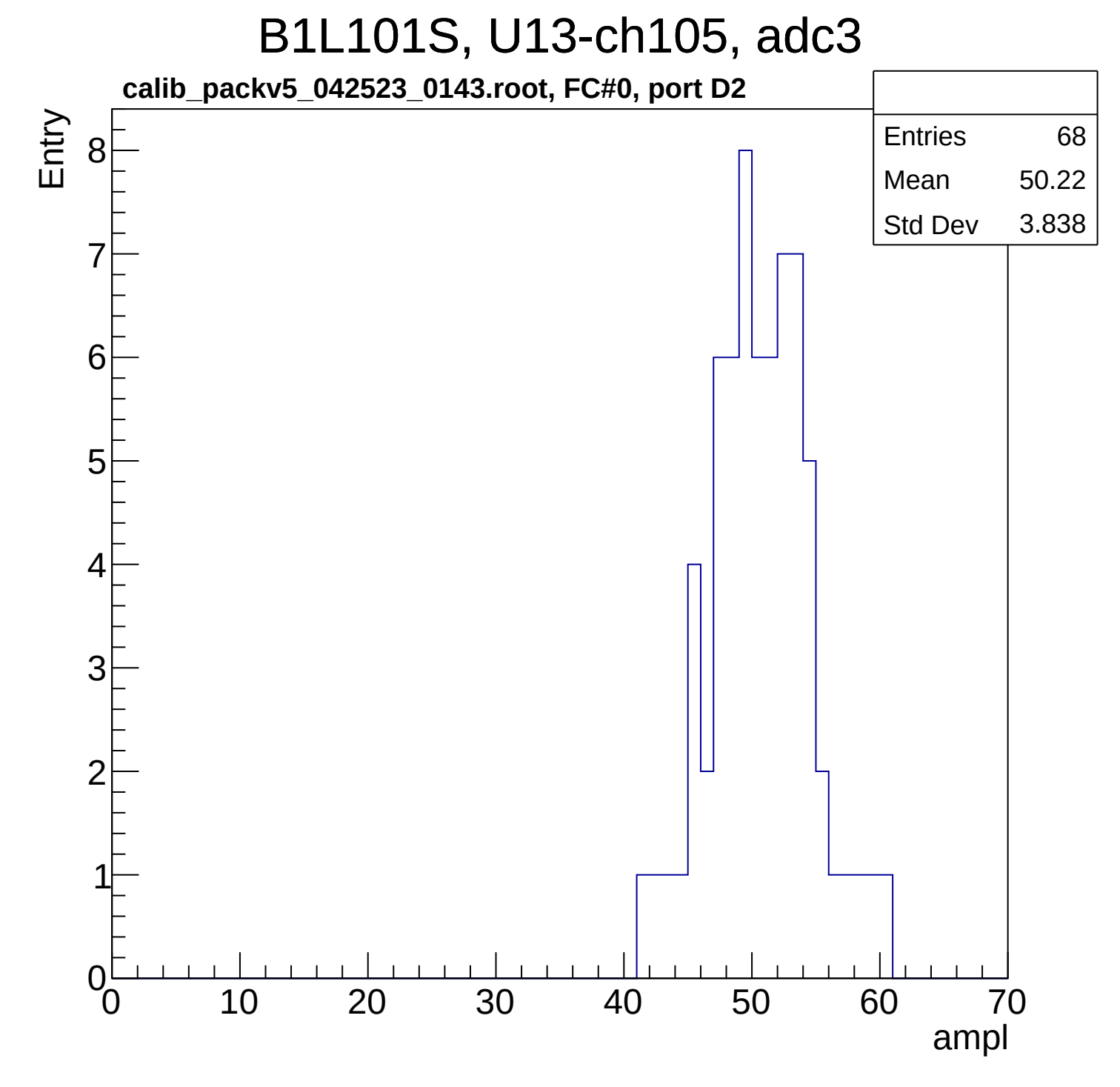

B1L101S, U13-ch104, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

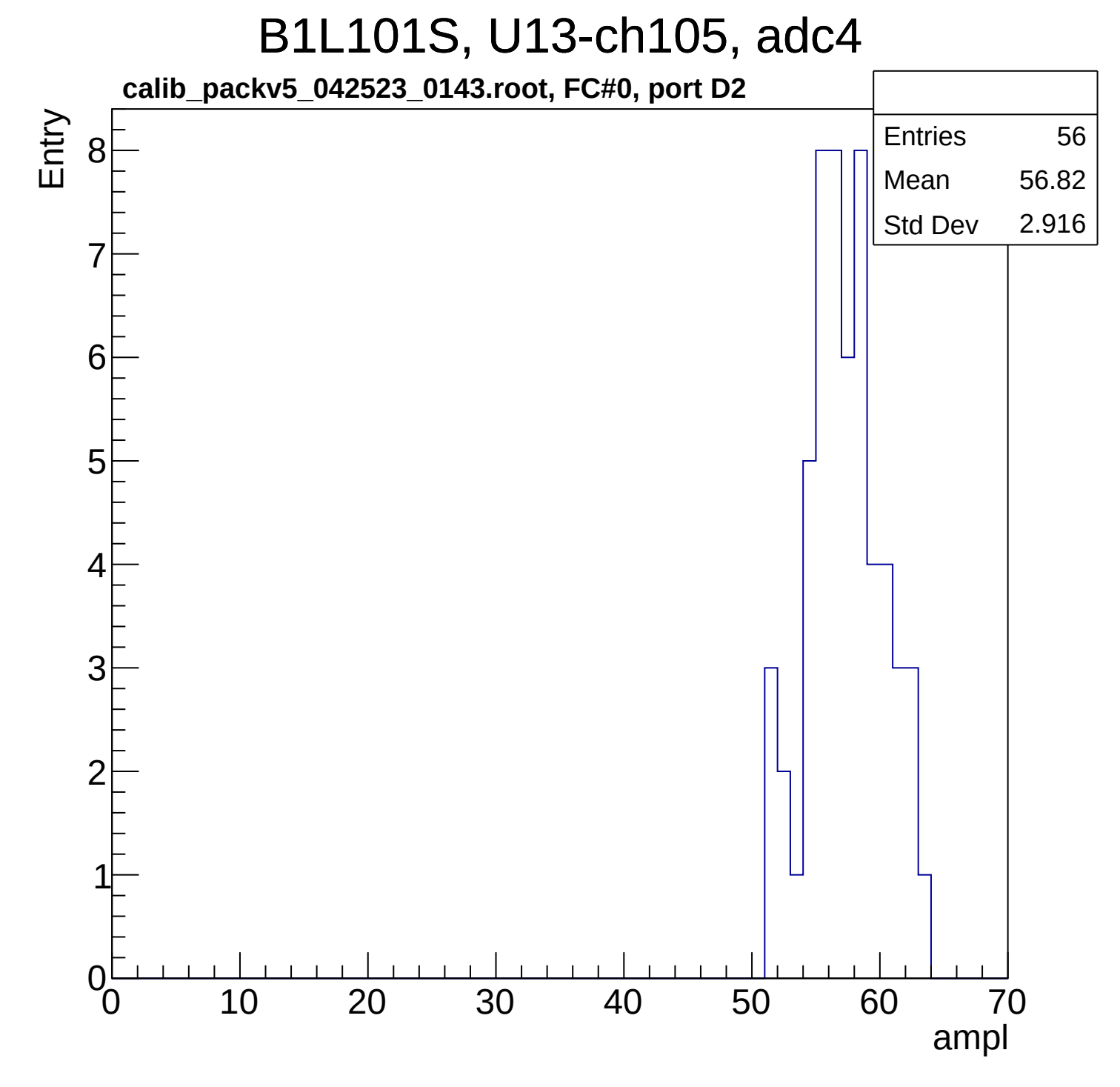

B1L101S, U13-ch104, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

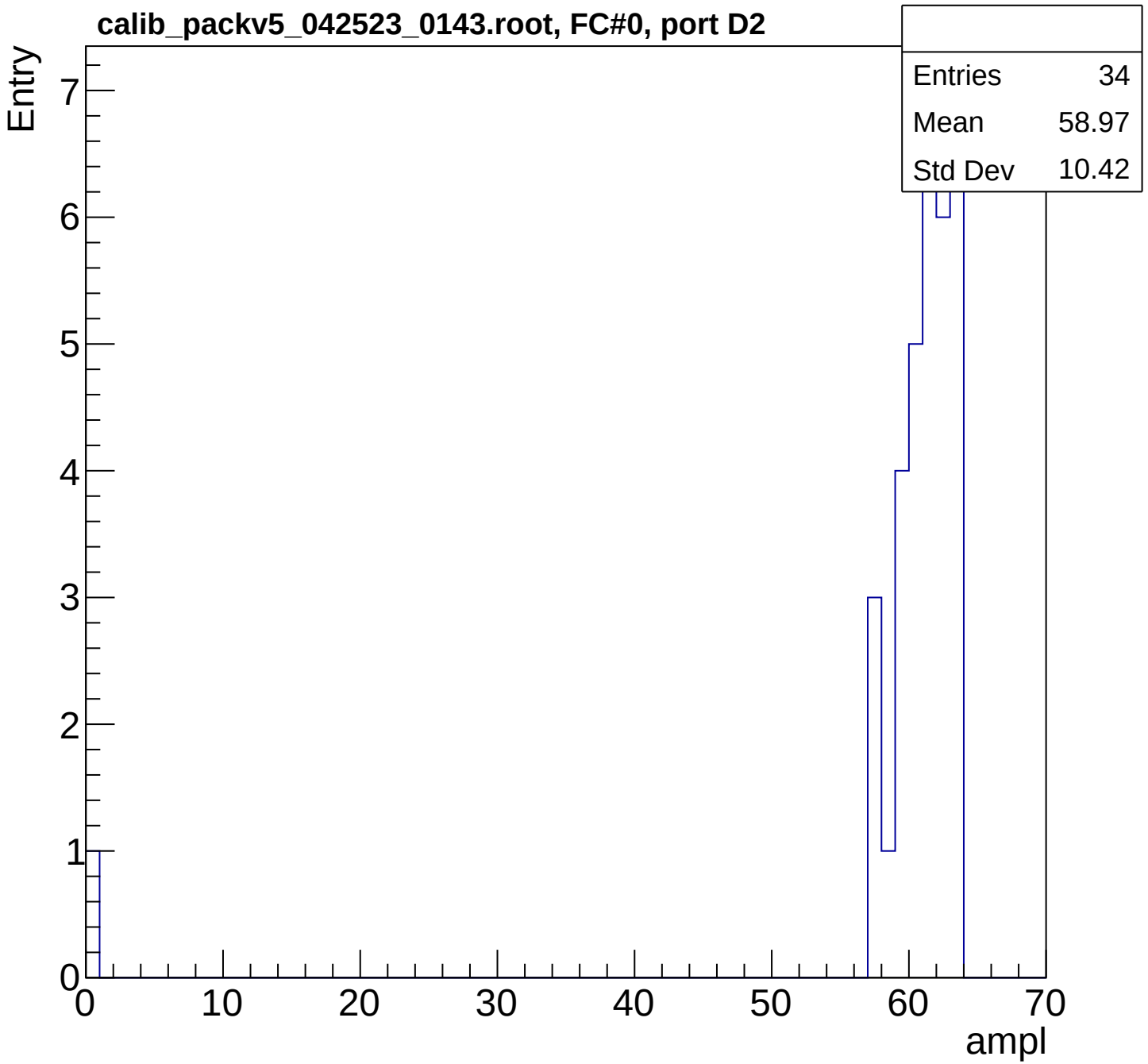

B1L101S, U13-ch105, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 64
Mean 28.25
Std Dev 7.071
Gaus mean : 30.0742
Gaus Width: 3.2680
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

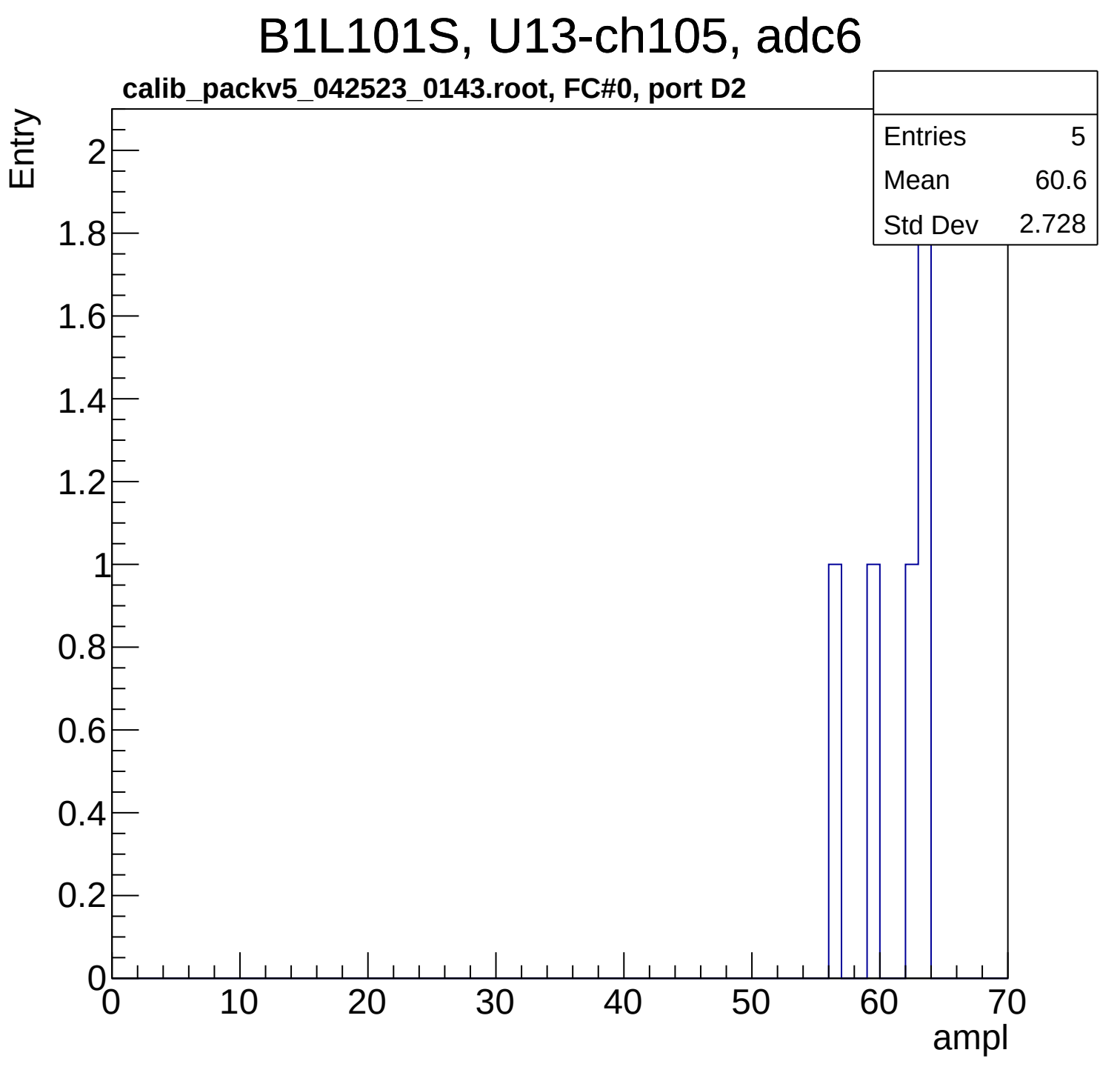

B1L101S, U13-ch105, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 35.83
Std Dev 3.401
Gaus mean : 35.6079
Gaus Width: 3.5903
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

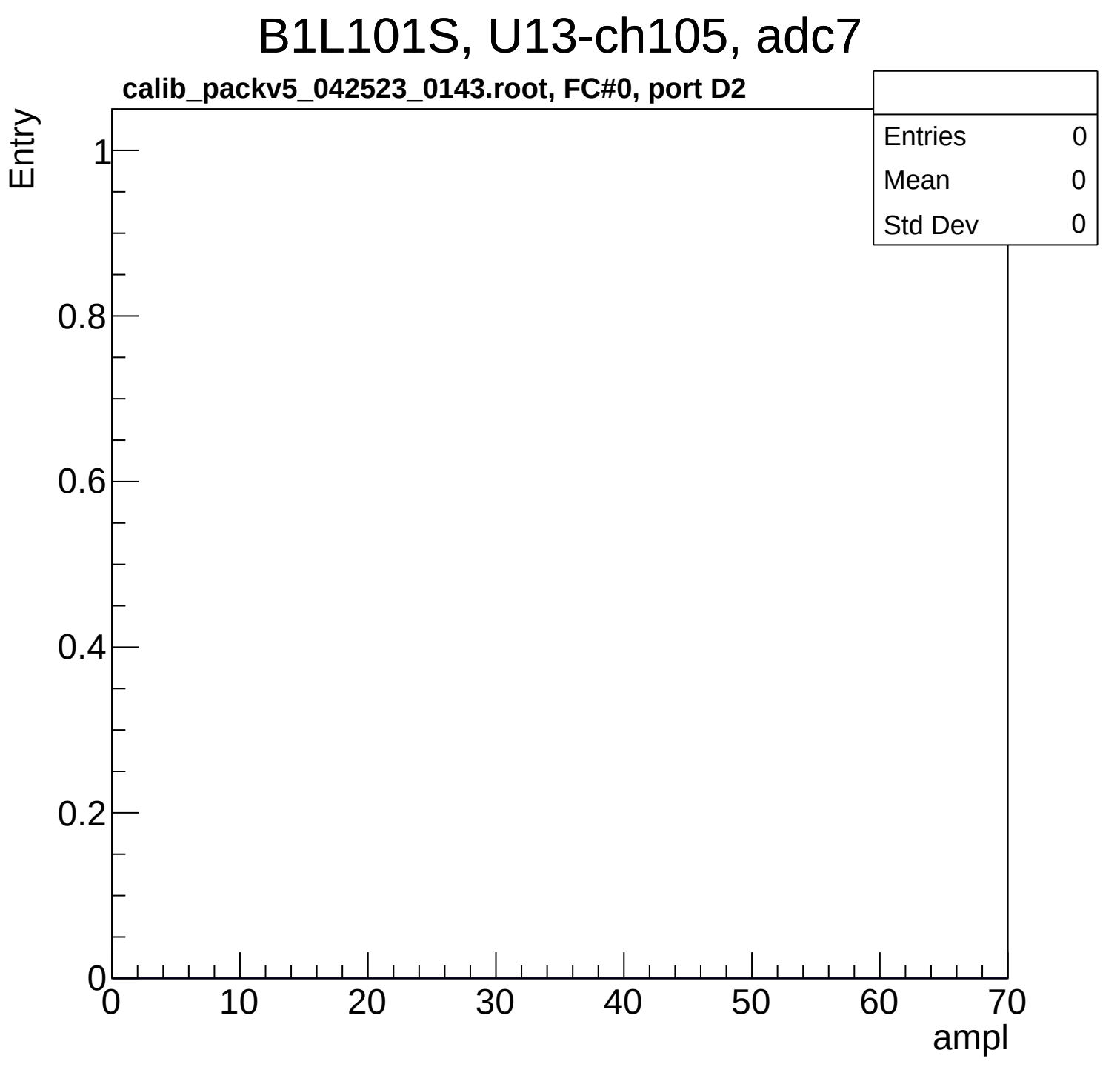

B1L101S, U13-ch105, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 43.6
Std Dev 4.218
Gaus mean : 44.2919
Gaus Width: 3.9522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

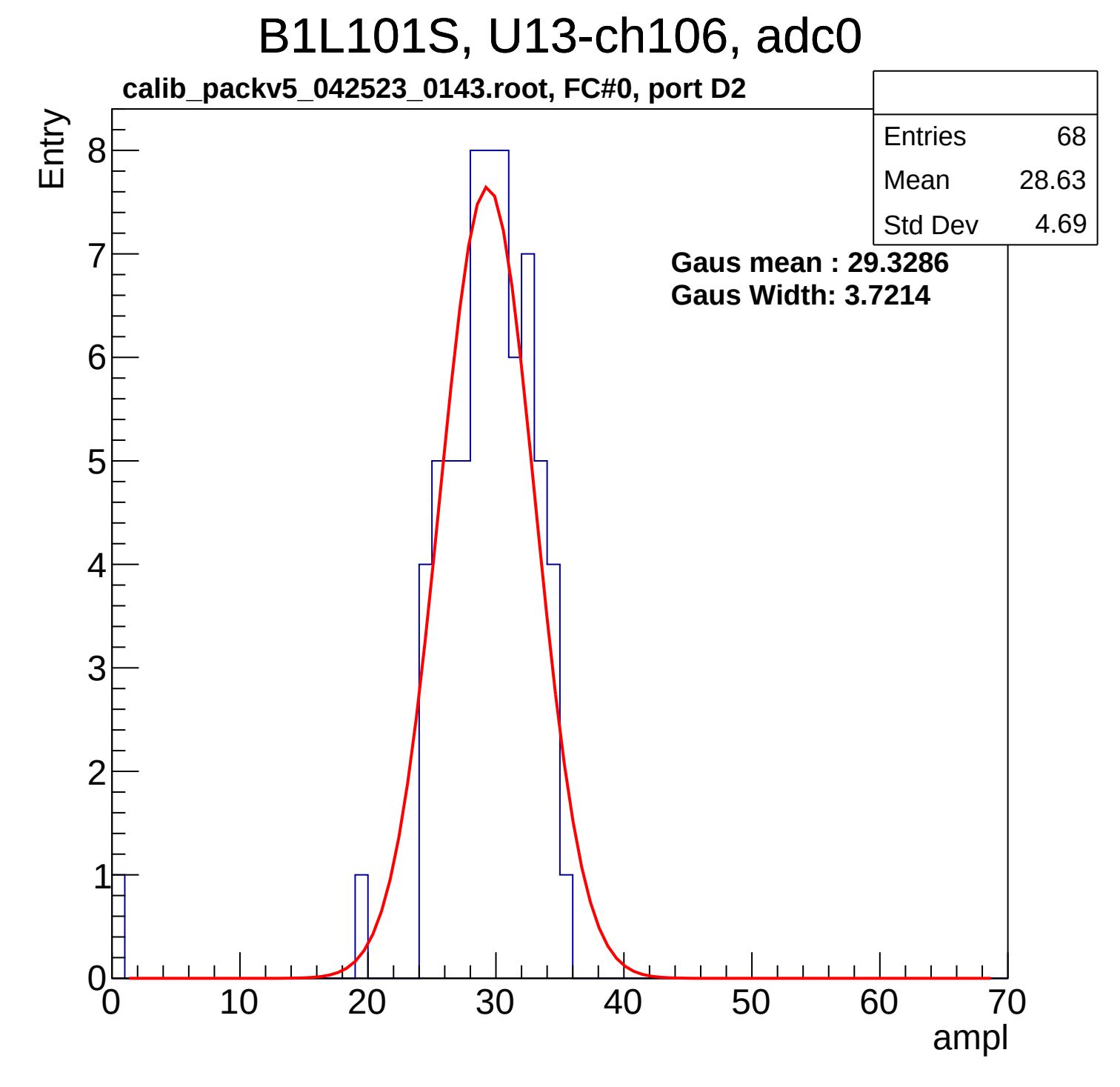

B1L101S, U13-ch105, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 50.22
Std Dev 3.838
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

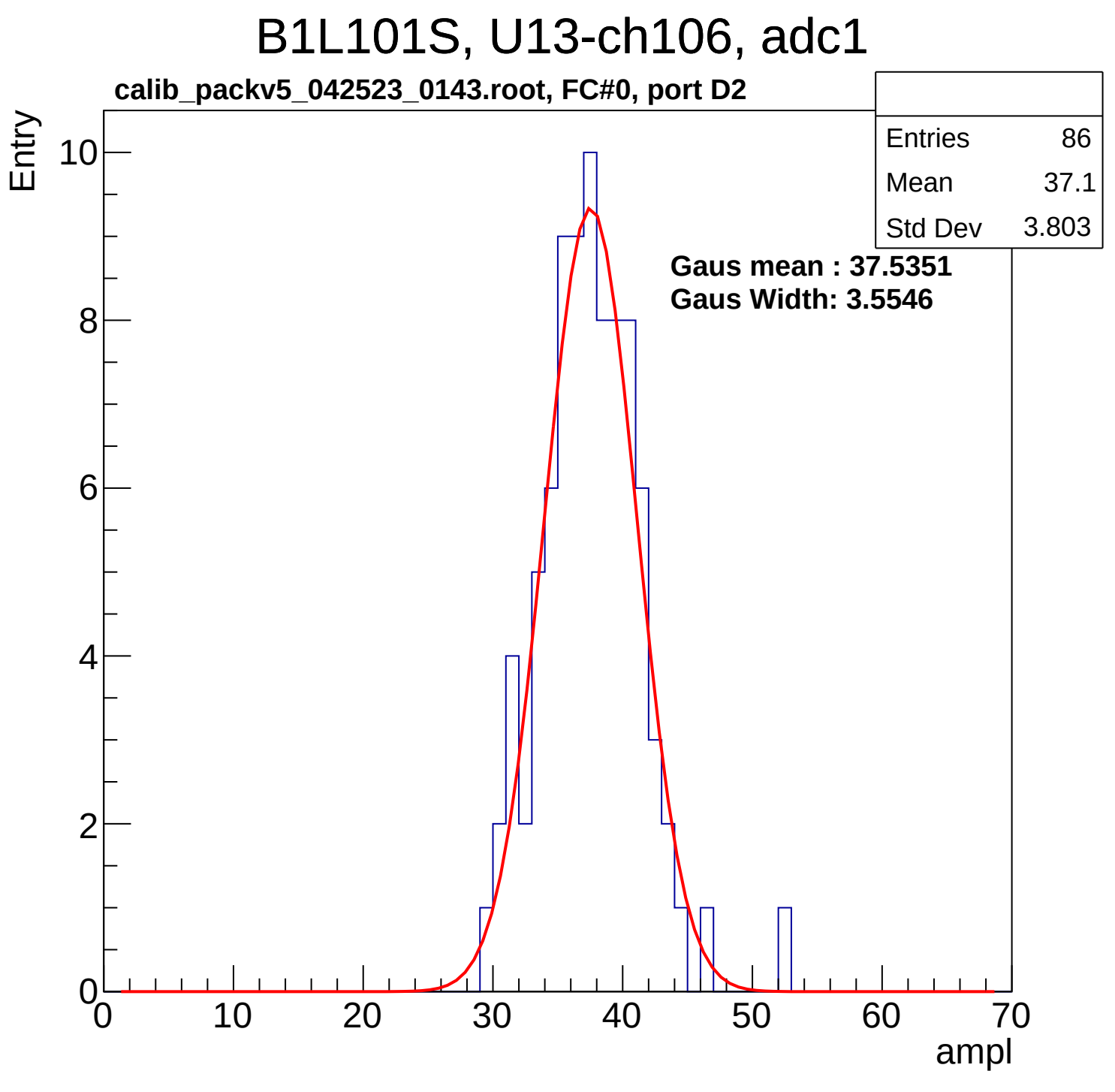

B1L101S, U13-ch105, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 56.82
Std Dev 2.916
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

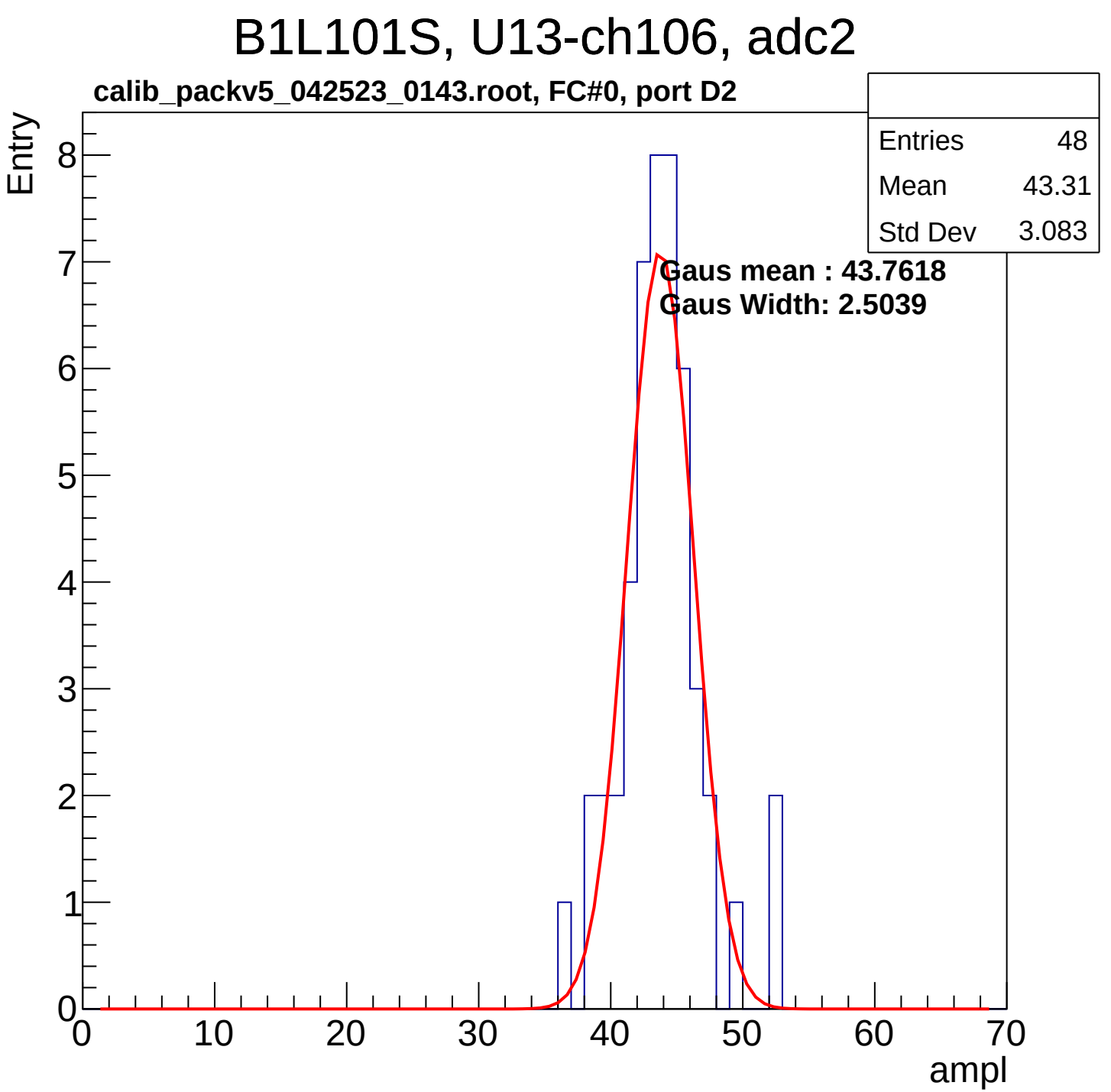

B1L101S, U13-ch105, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 34
Mean 58.97
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

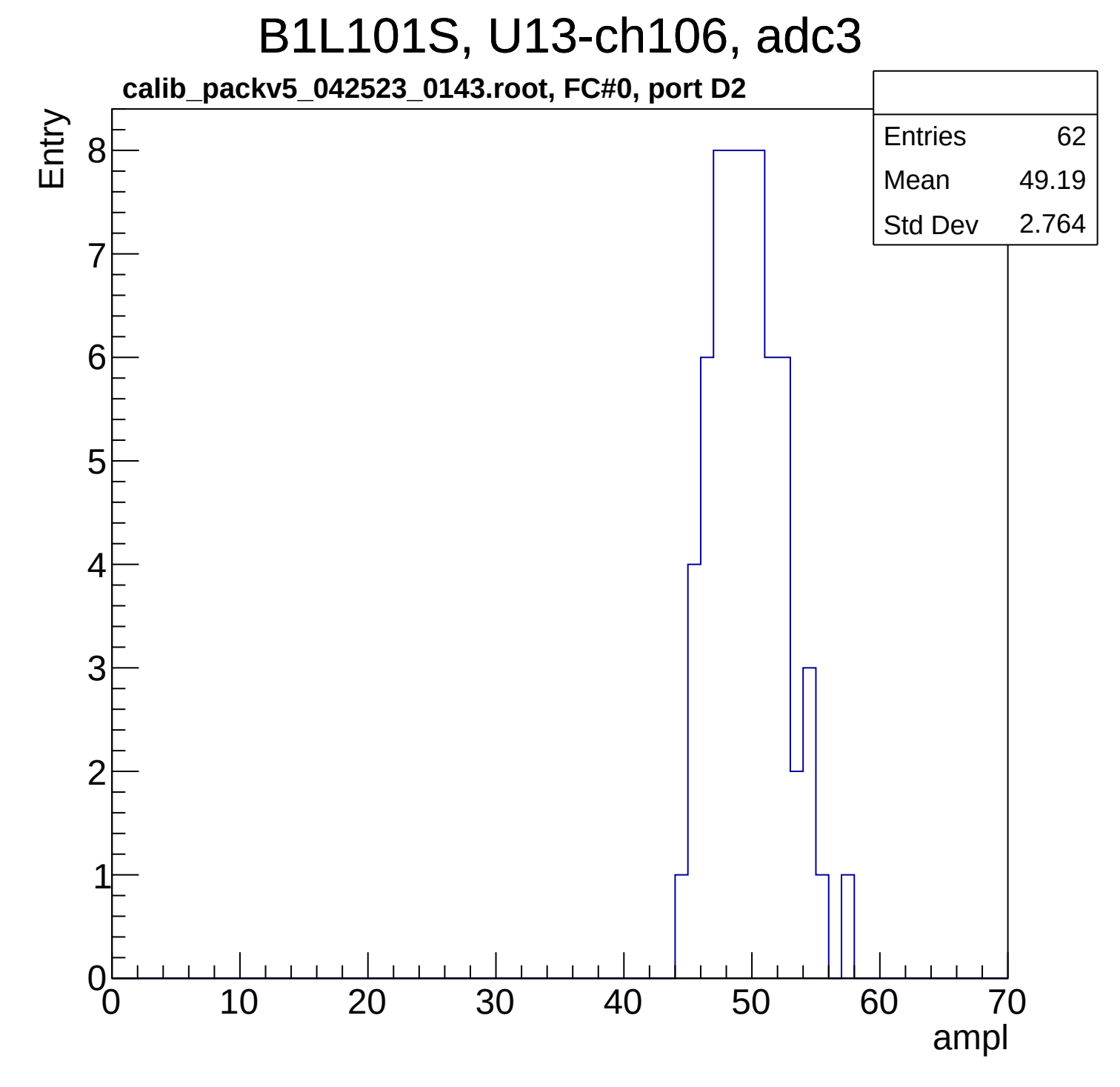

B1L101S, U13-ch105, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 60.6
Std Dev 2.728
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

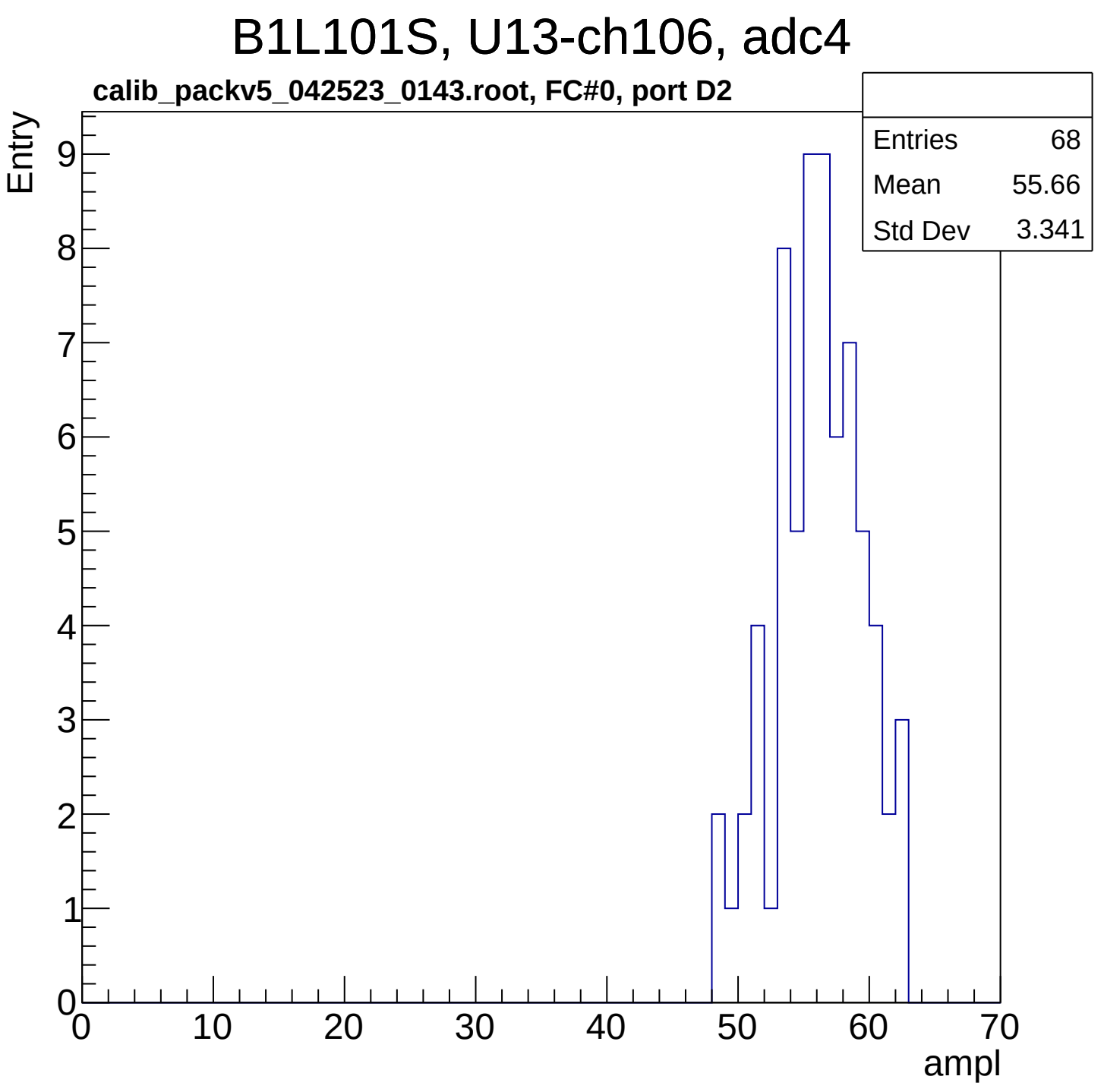

B1L101S, U13-ch105, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

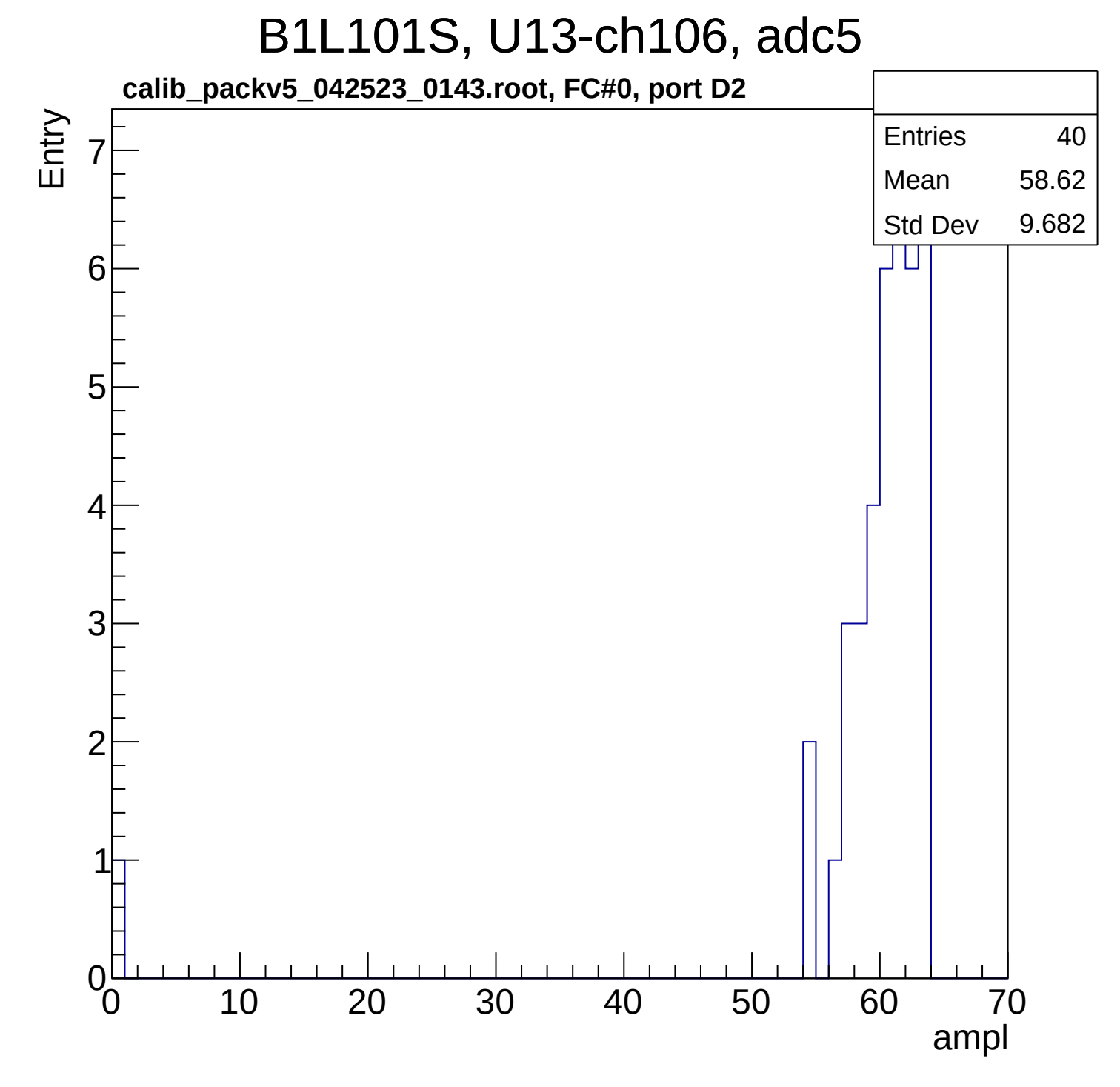

B1L101S, U13-ch106, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 28.63
Std Dev 4.69
Gaus mean : 29.3286
Gaus Width: 3.7214
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch106, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 86
Mean 37.1
Std Dev 3.803
Gaus mean : 37.5351
Gaus Width: 3.5546
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch106, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 43.31
Std Dev 3.083
Gaus mean : 43.7618
Gaus Width: 2.5039
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch106, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 49.19
Std Dev 2.764
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch106, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 68
Mean 55.66
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch106, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 58.62
Std Dev 9.682
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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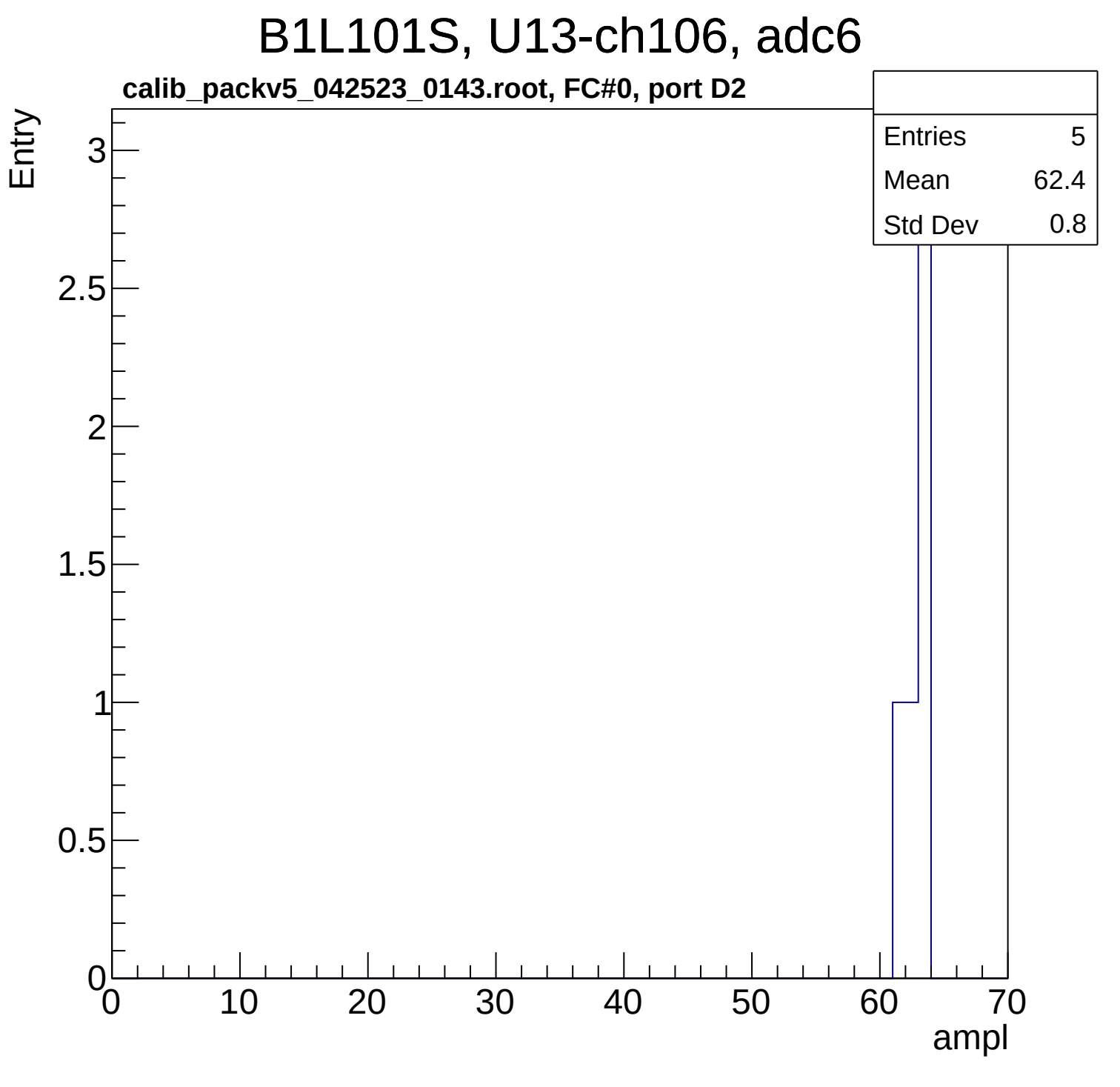
60
70


B1L101S, U13-ch106, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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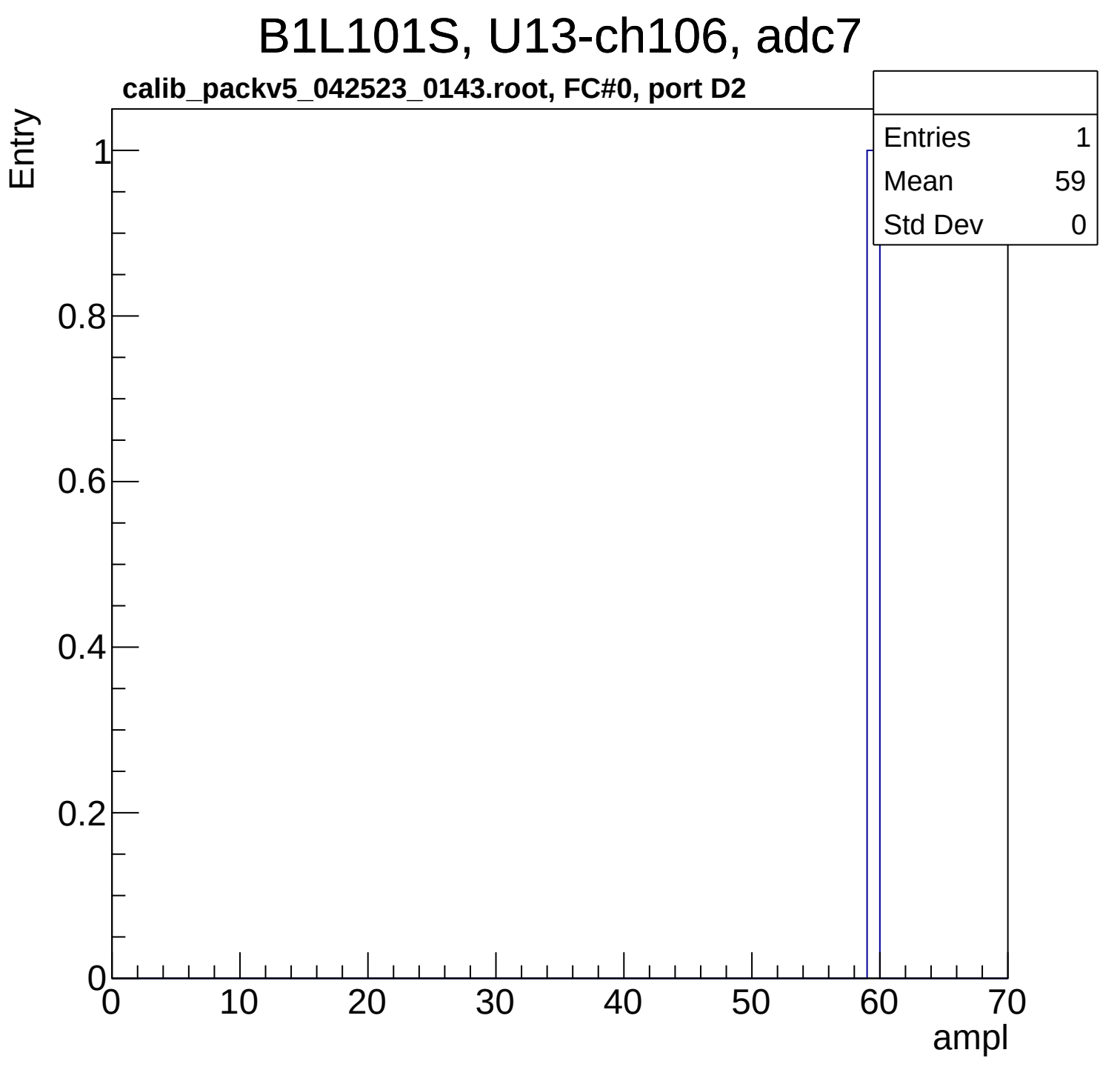
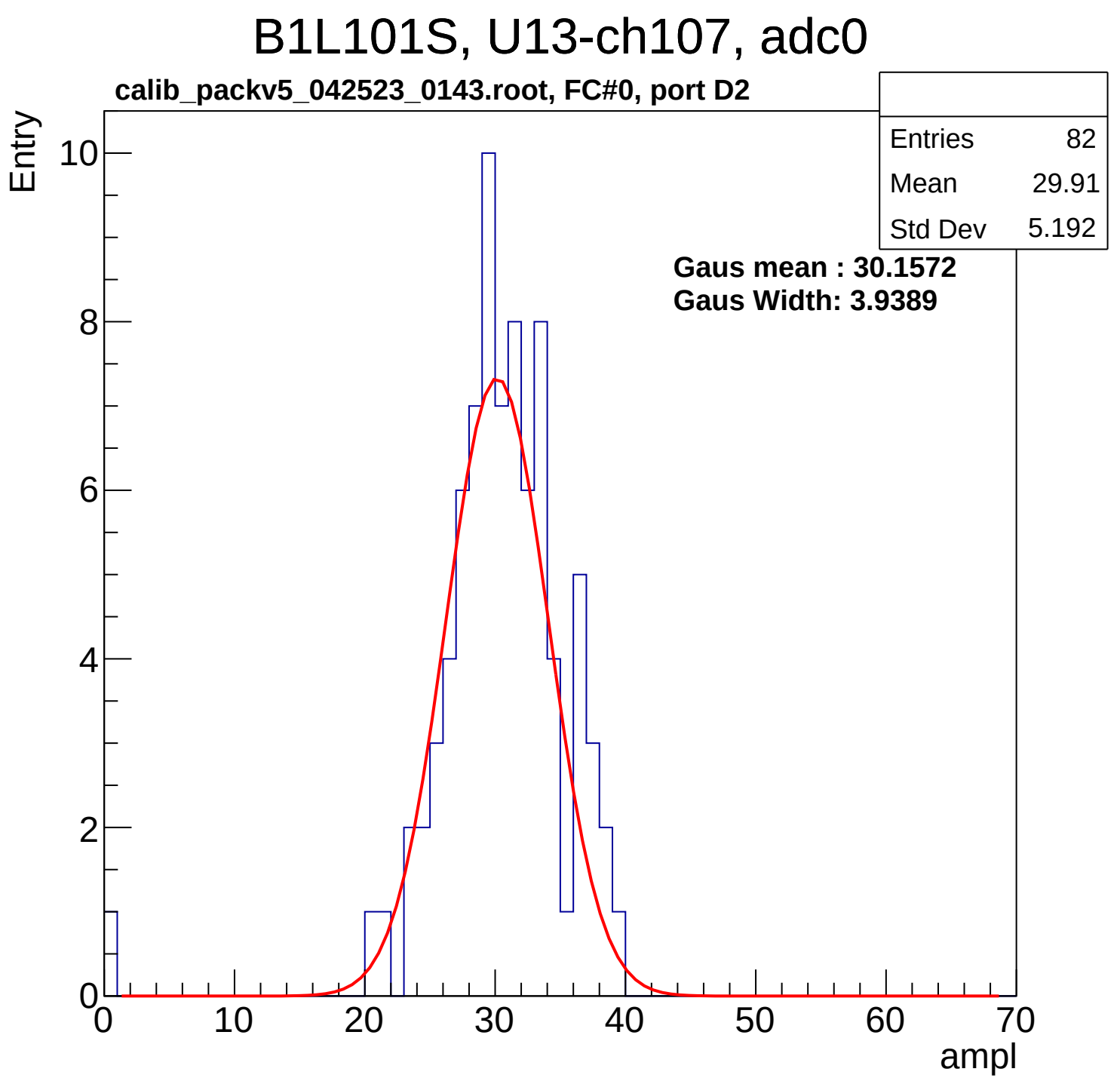
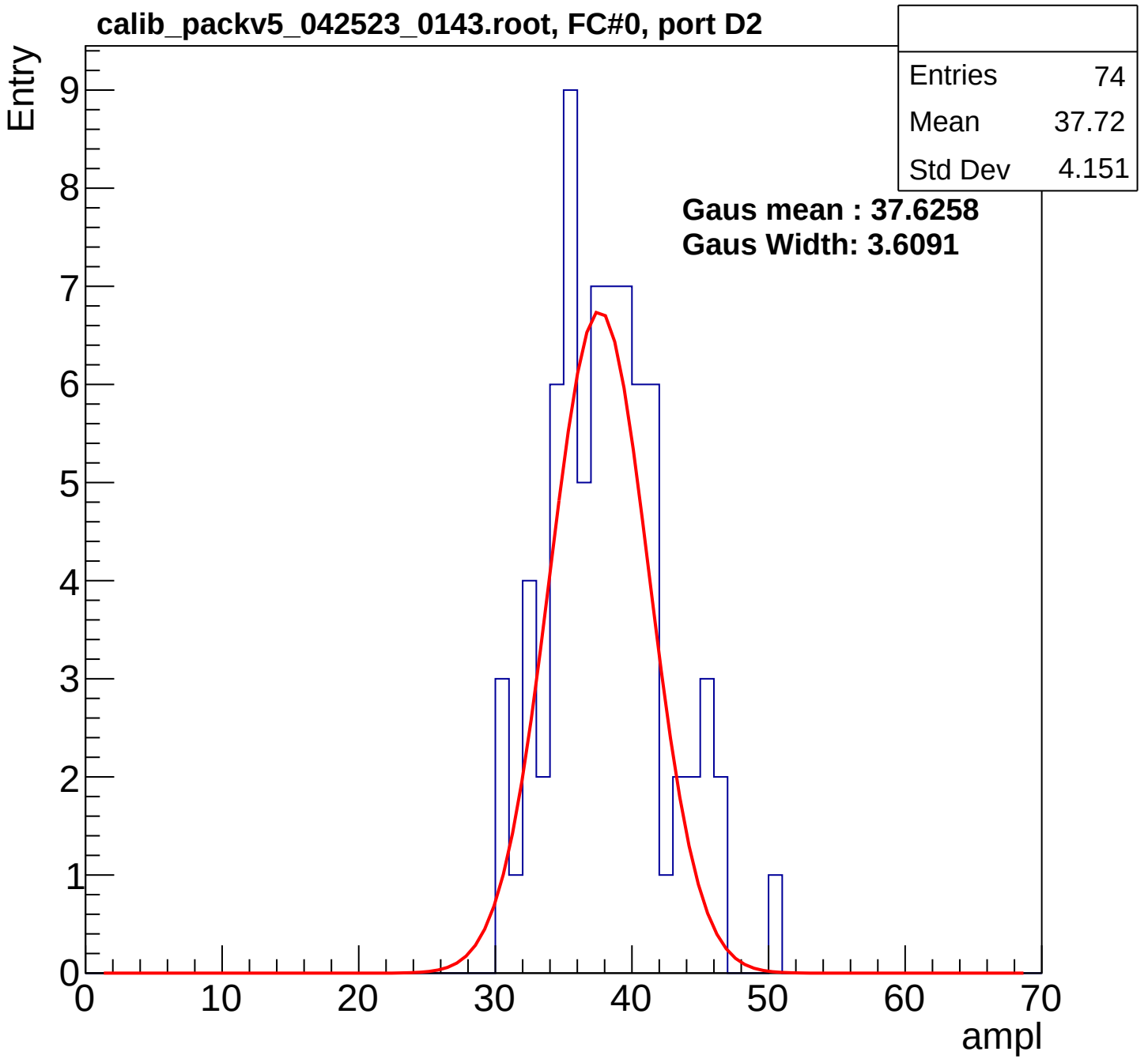
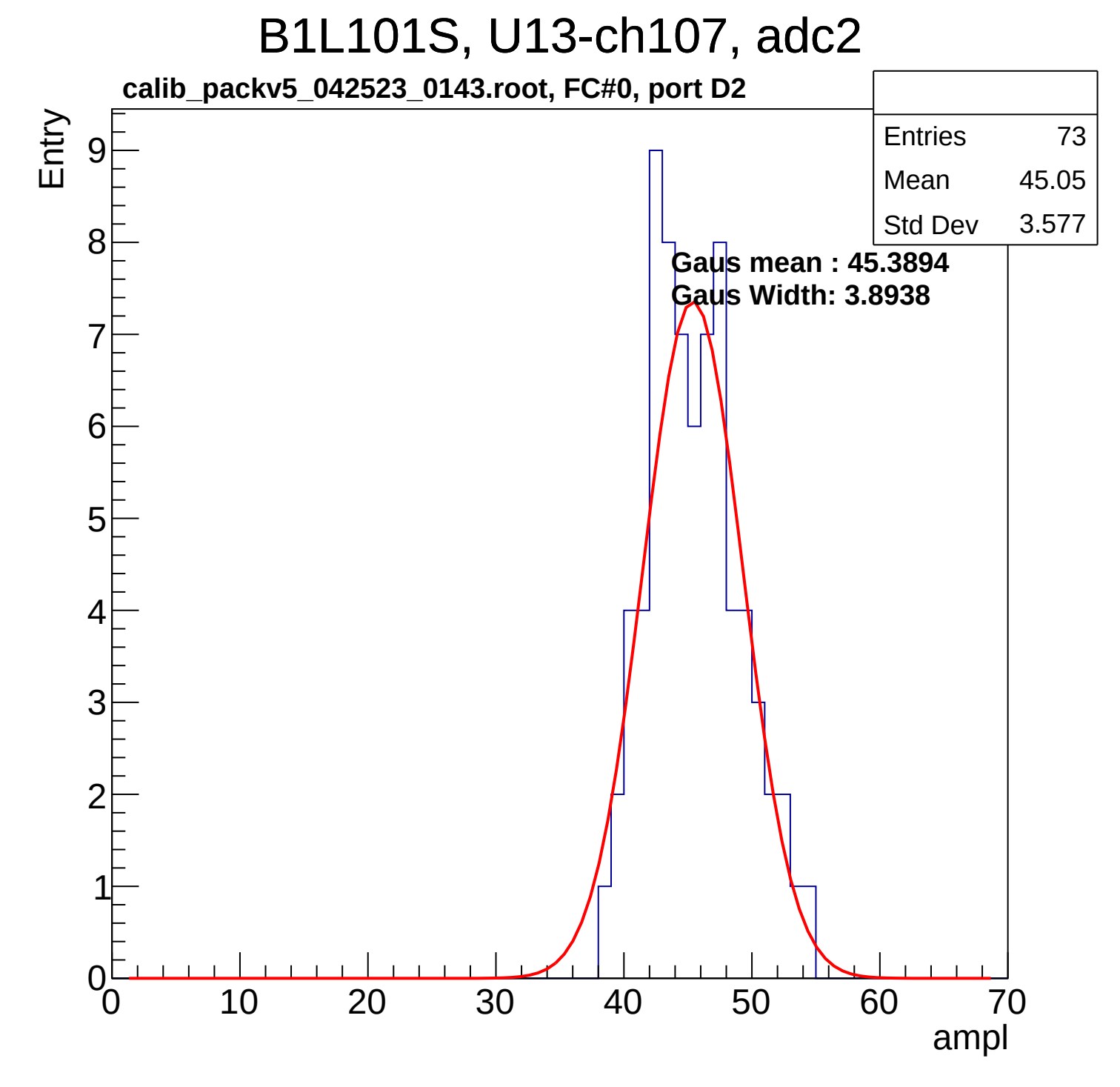
40
50
60
70


B1L101S, U13-ch106, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch107, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 82
Mean 29.91
Std Dev 5.192
Gaus mean : 30.1572
Gaus Width: 3.9389
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch107, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 37.72
Std Dev 4.151
Gaus mean : 37.6258
Gaus Width: 3.6091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch107, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 45.05
Std Dev 3.577
Gaus mean : 45.3894
Gaus Width: 3.8938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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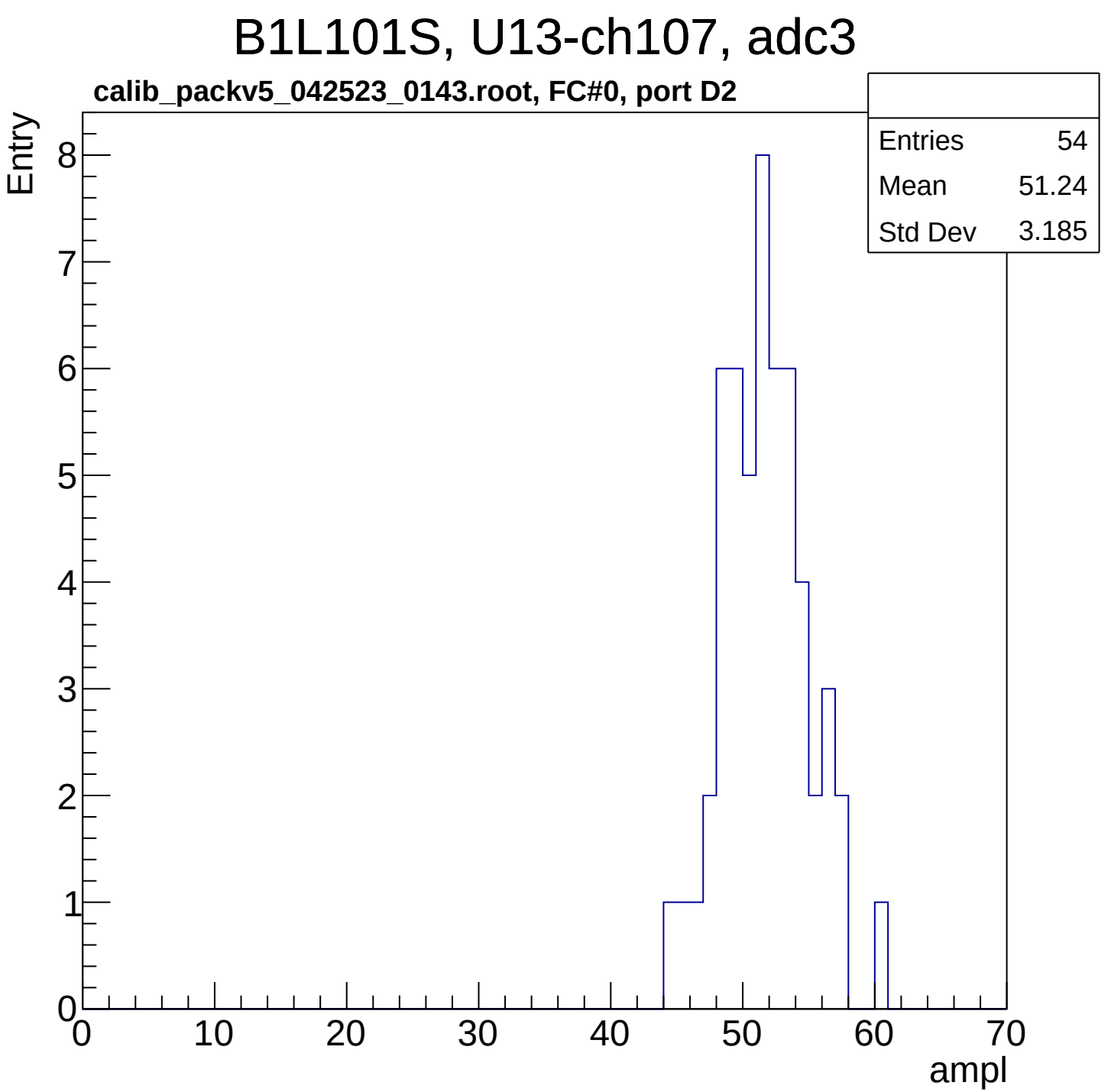
B1L101S, U13-ch107, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 51.24
Std Dev 3.185
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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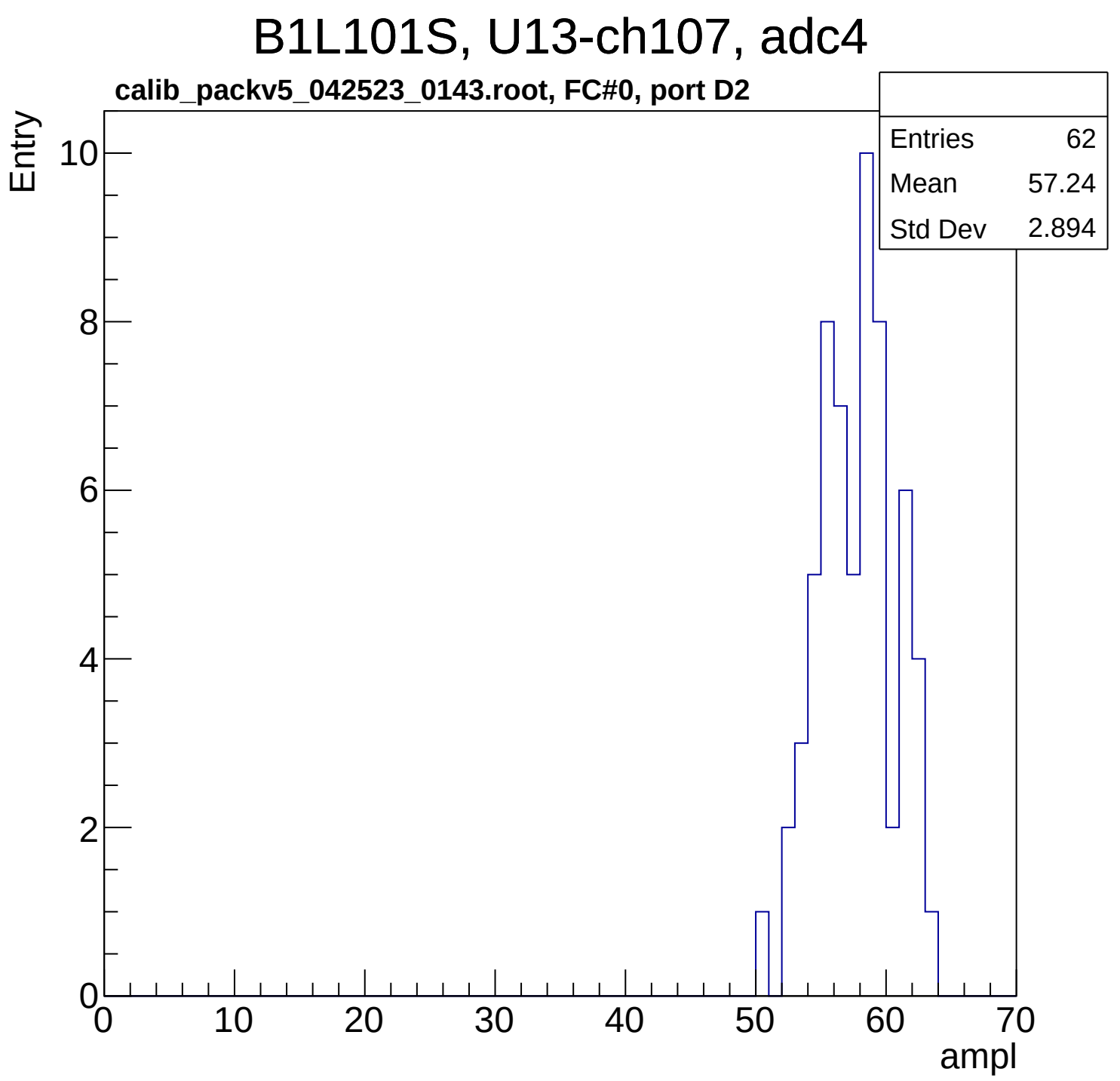
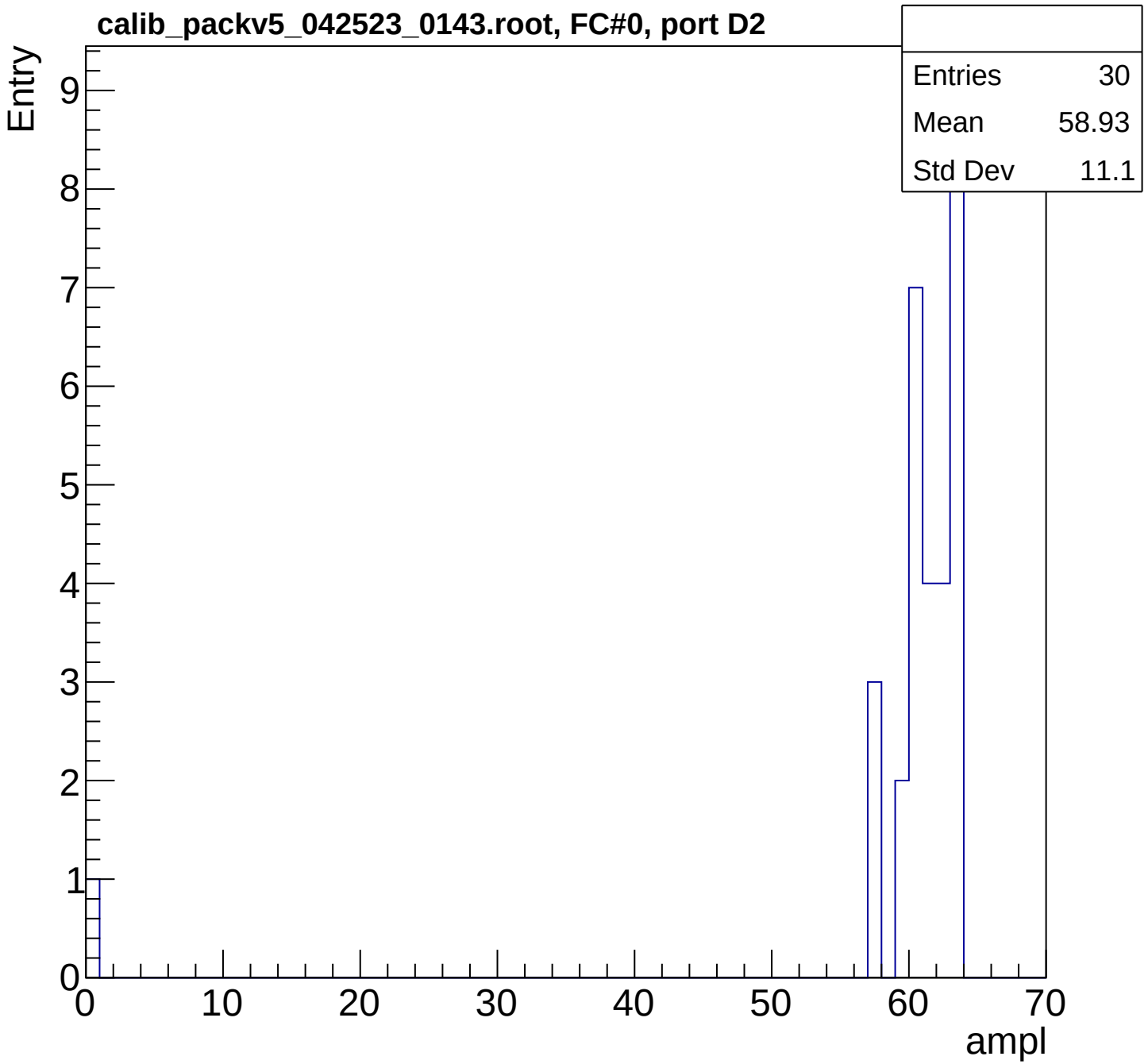

B1L101S, U13-ch107, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 57.24
Std Dev 2.894
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch107, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 30
Mean 58.93
Std Dev 11.1
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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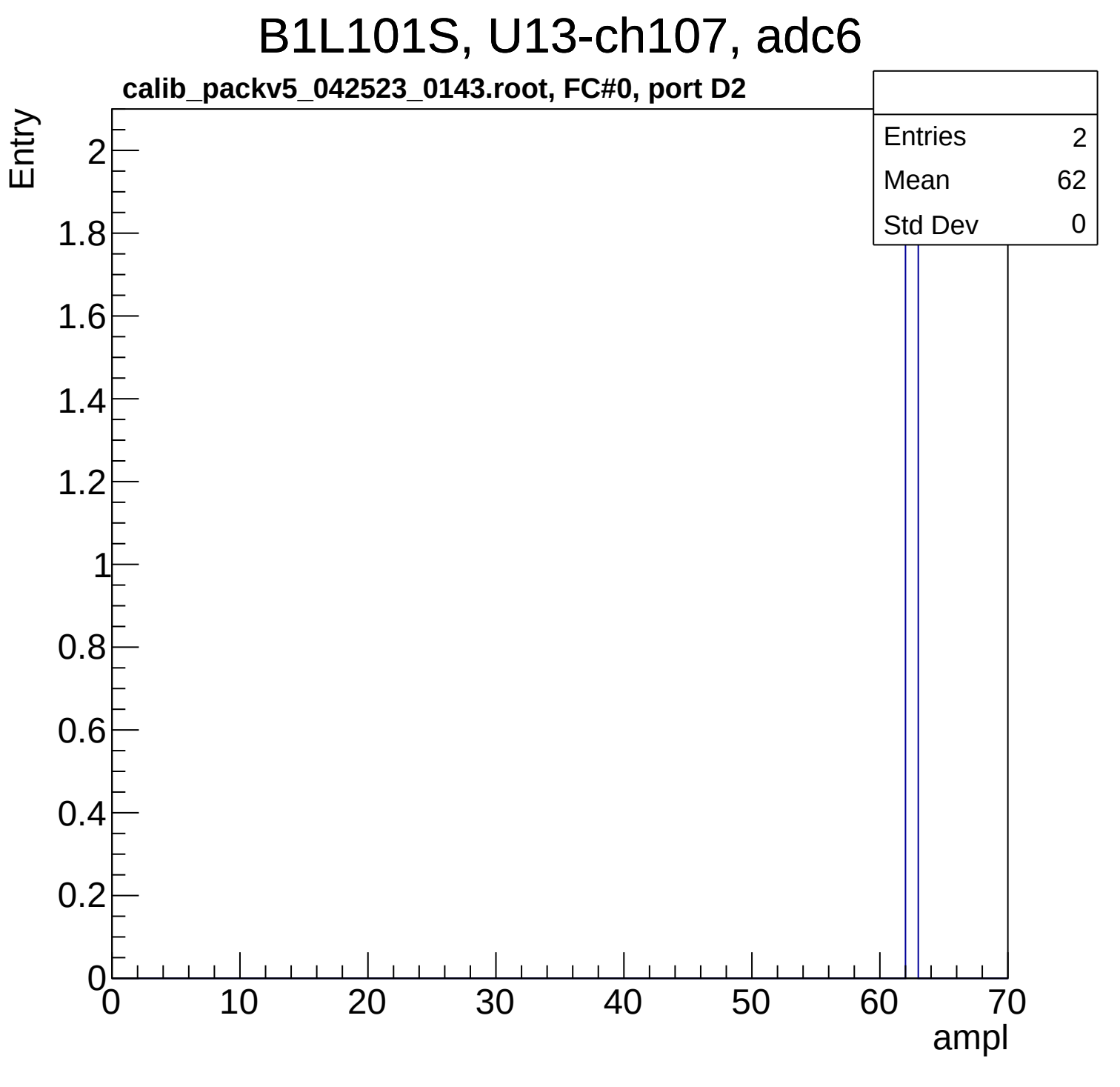
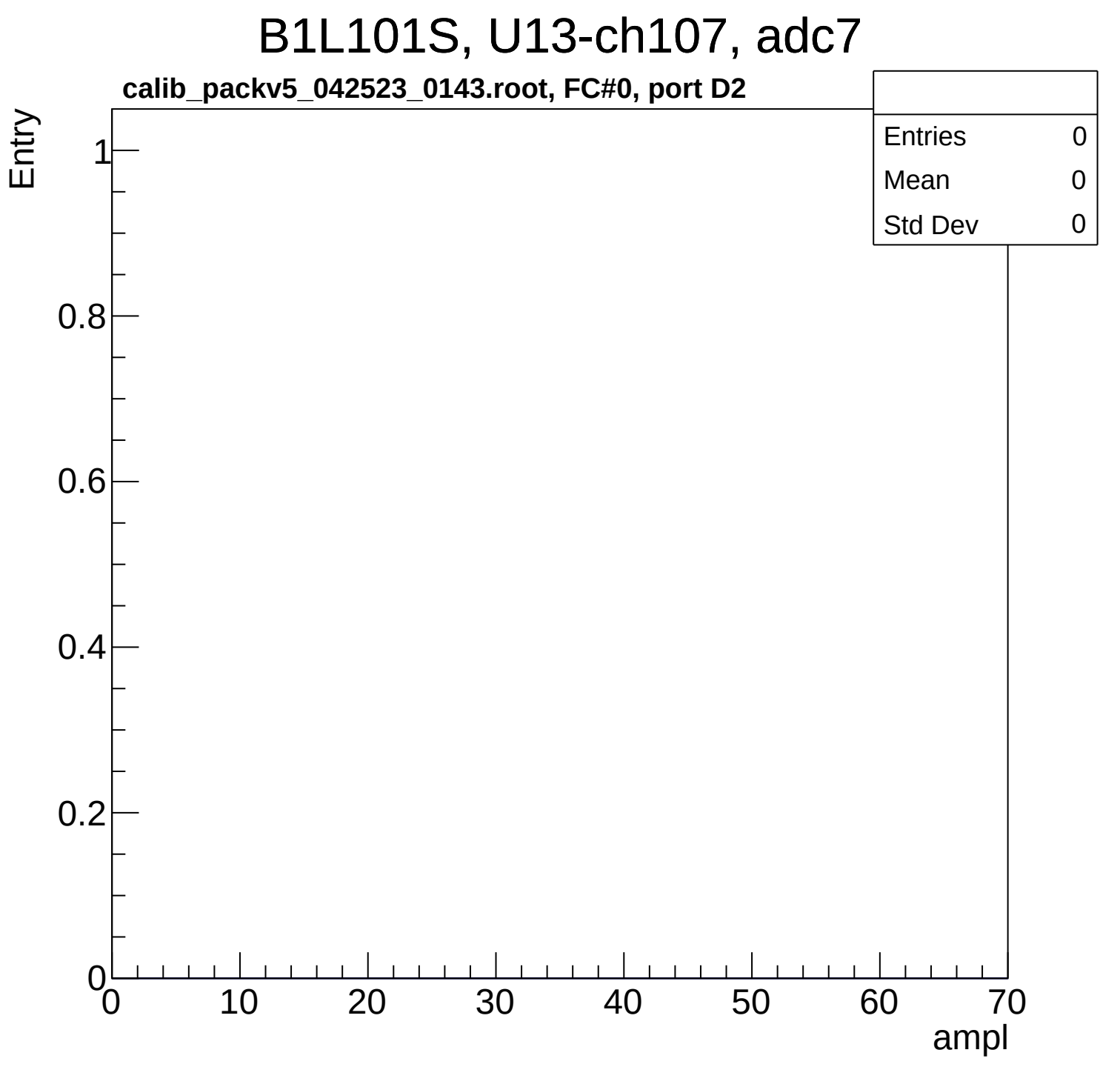

B1L101S, U13-ch107, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch107, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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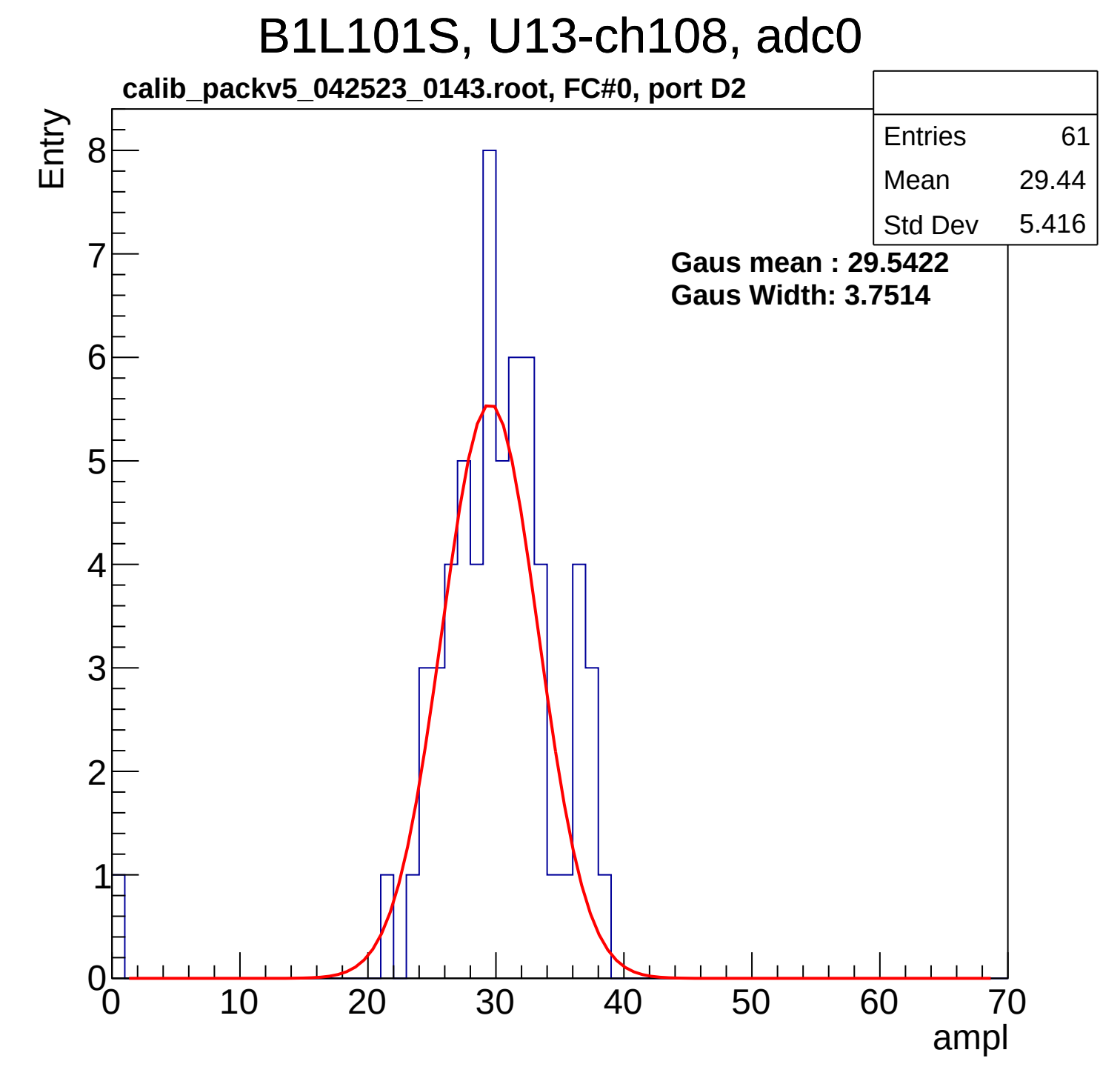
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch108, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 29.44
Std Dev 5.416
Gaus mean : 29.5422
Gaus Width: 3.7514
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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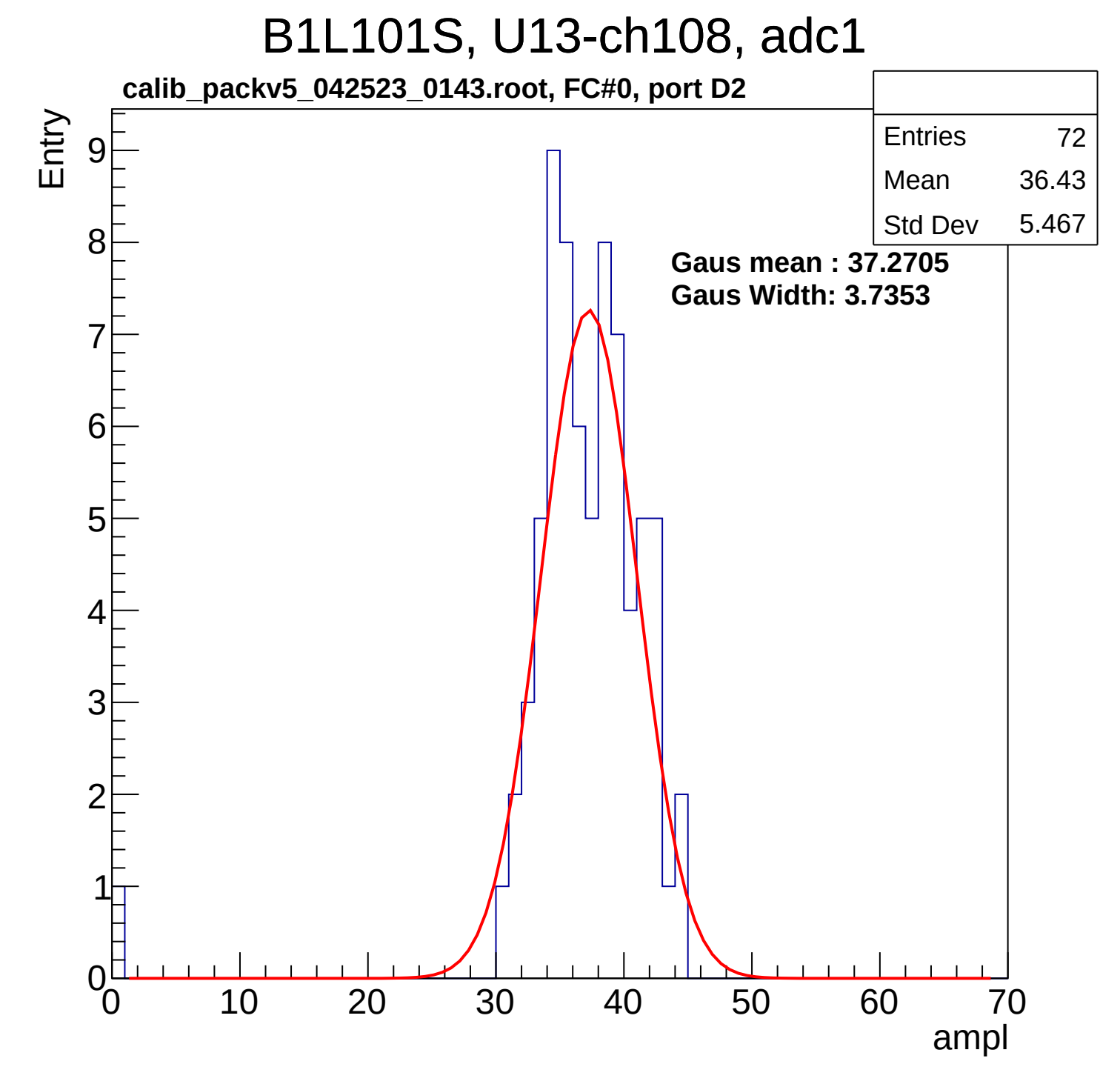
B1L101S, U13-ch108, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 36.43
Std Dev 5.467
Gaus mean : 37.2705
Gaus Width: 3.7353
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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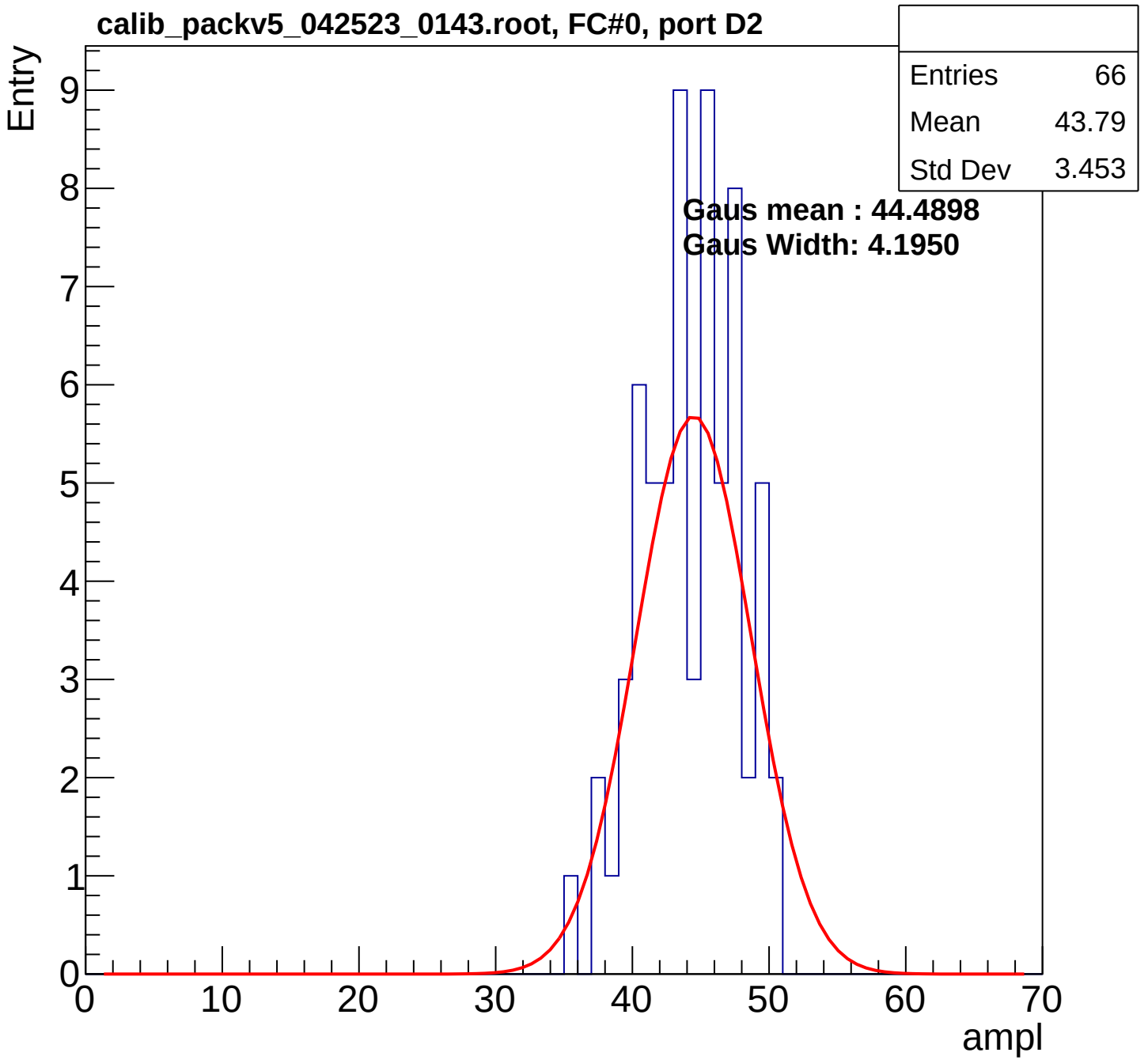
B1L101S, U13-ch108, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 43.79
Std Dev 3.453
Gaus mean : 44.4898
Gaus Width: 4.1950
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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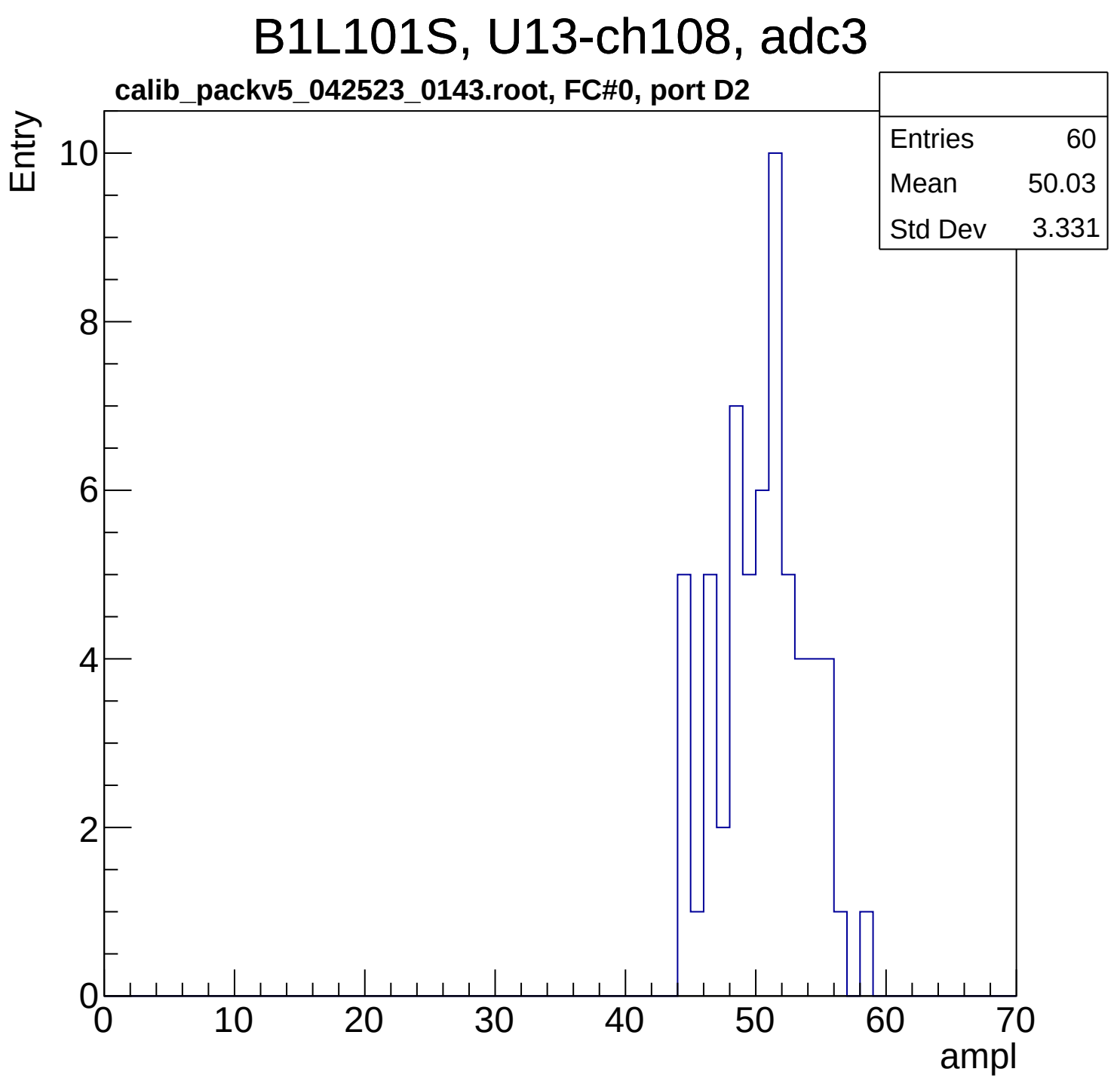
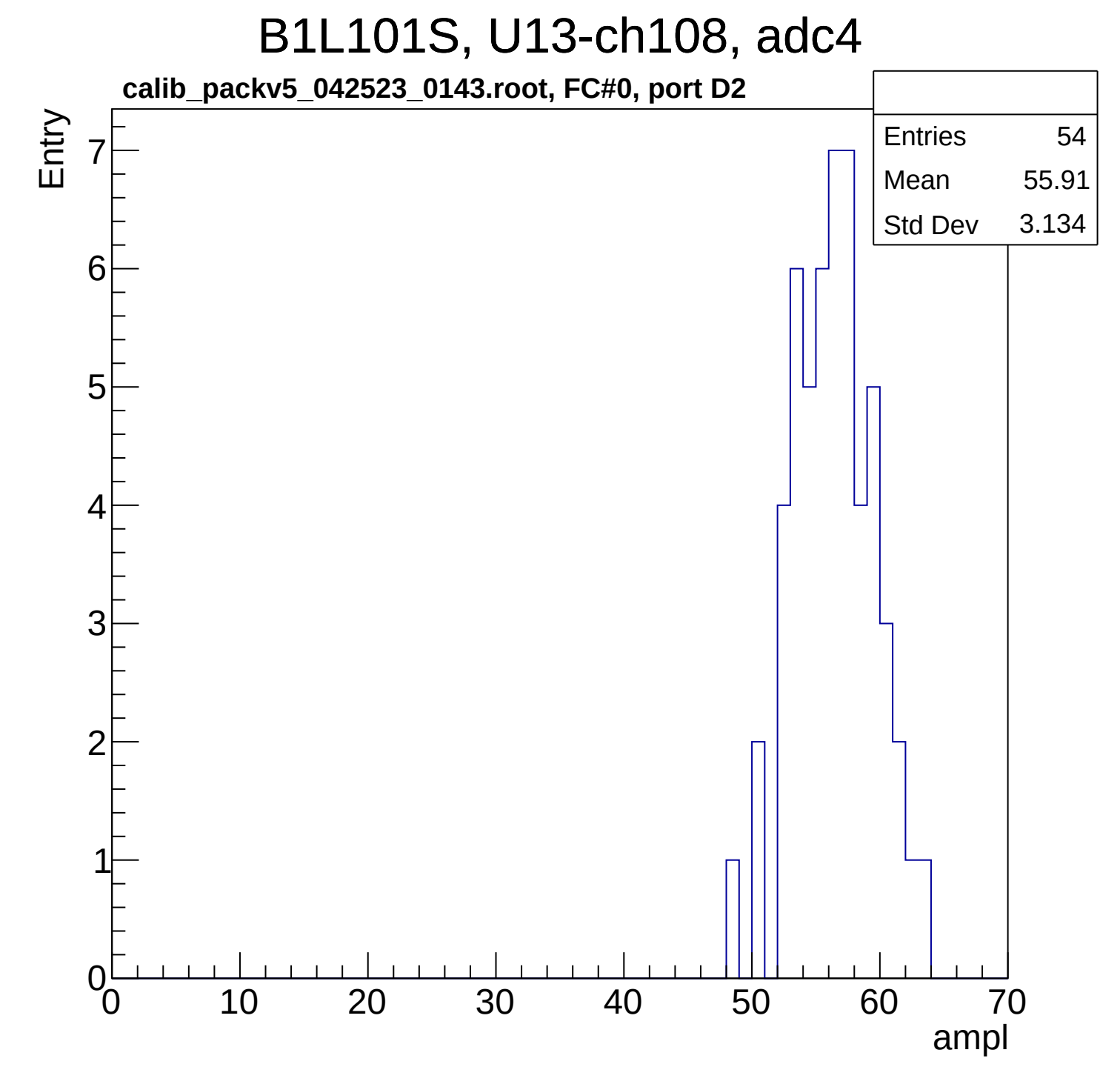
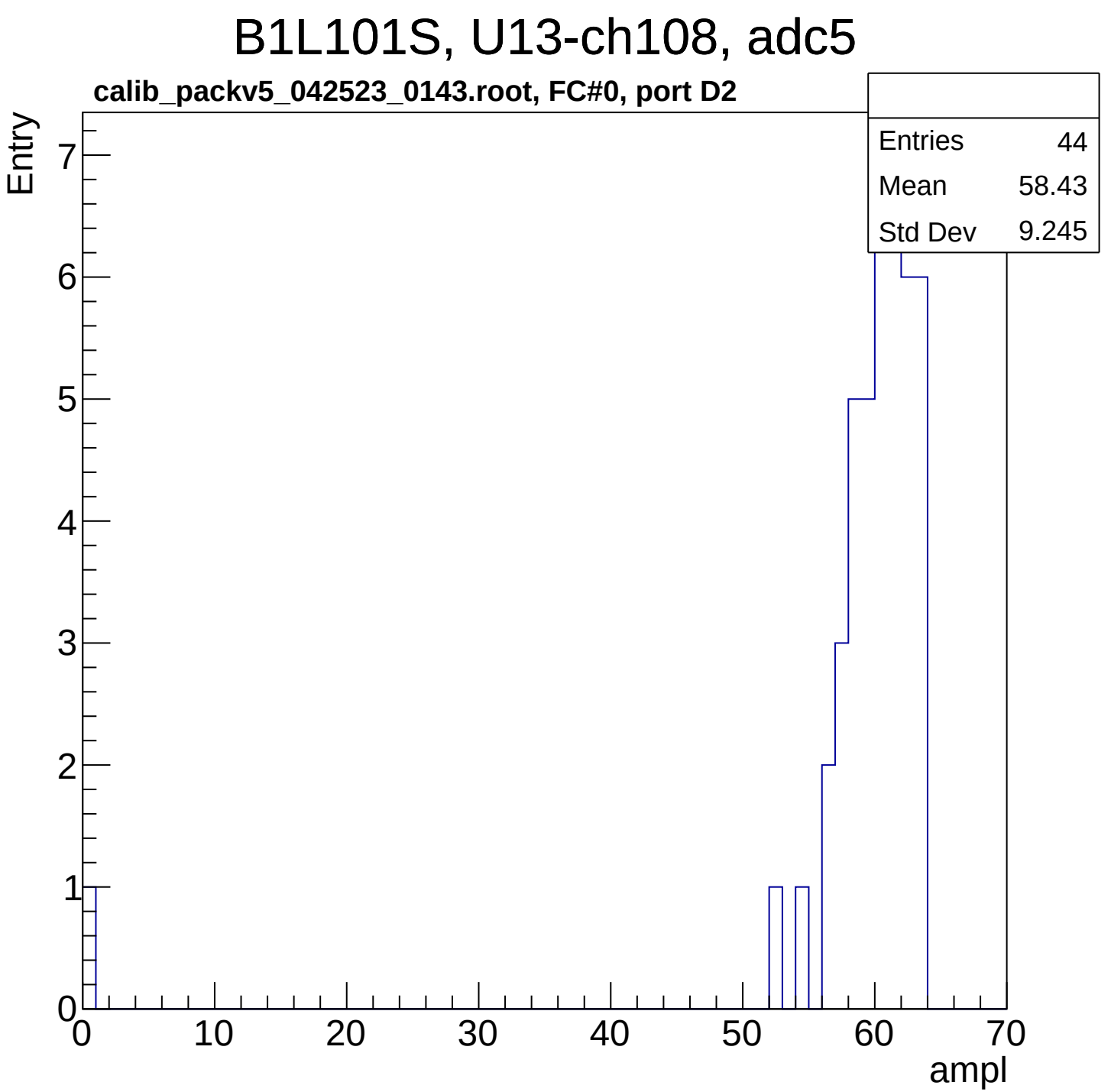

B1L101S, U13-ch108, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 50.03
Std Dev 3.331
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch108, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 55.91
Std Dev 3.134
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch108, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 58.43
Std Dev 9.245
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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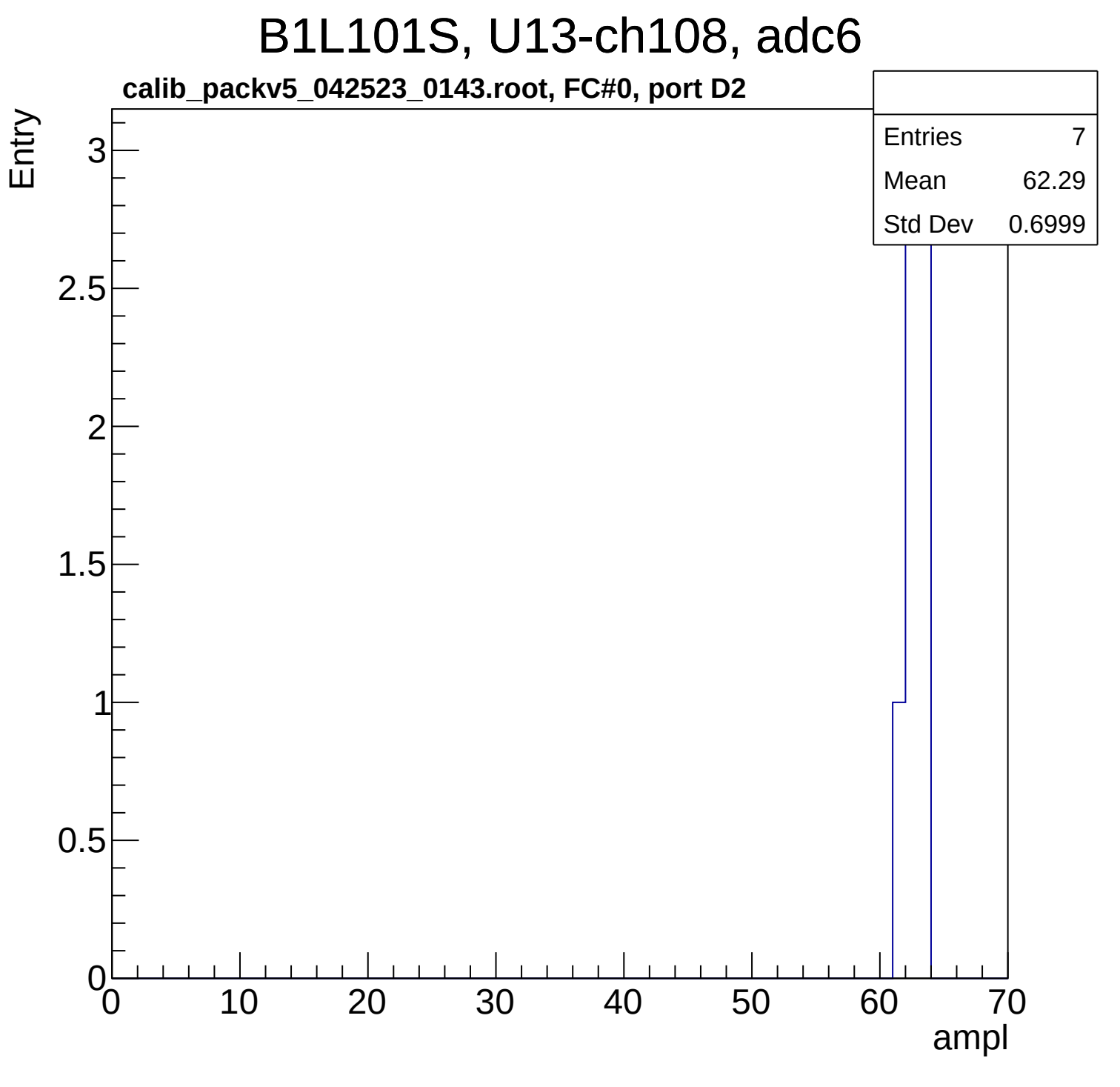
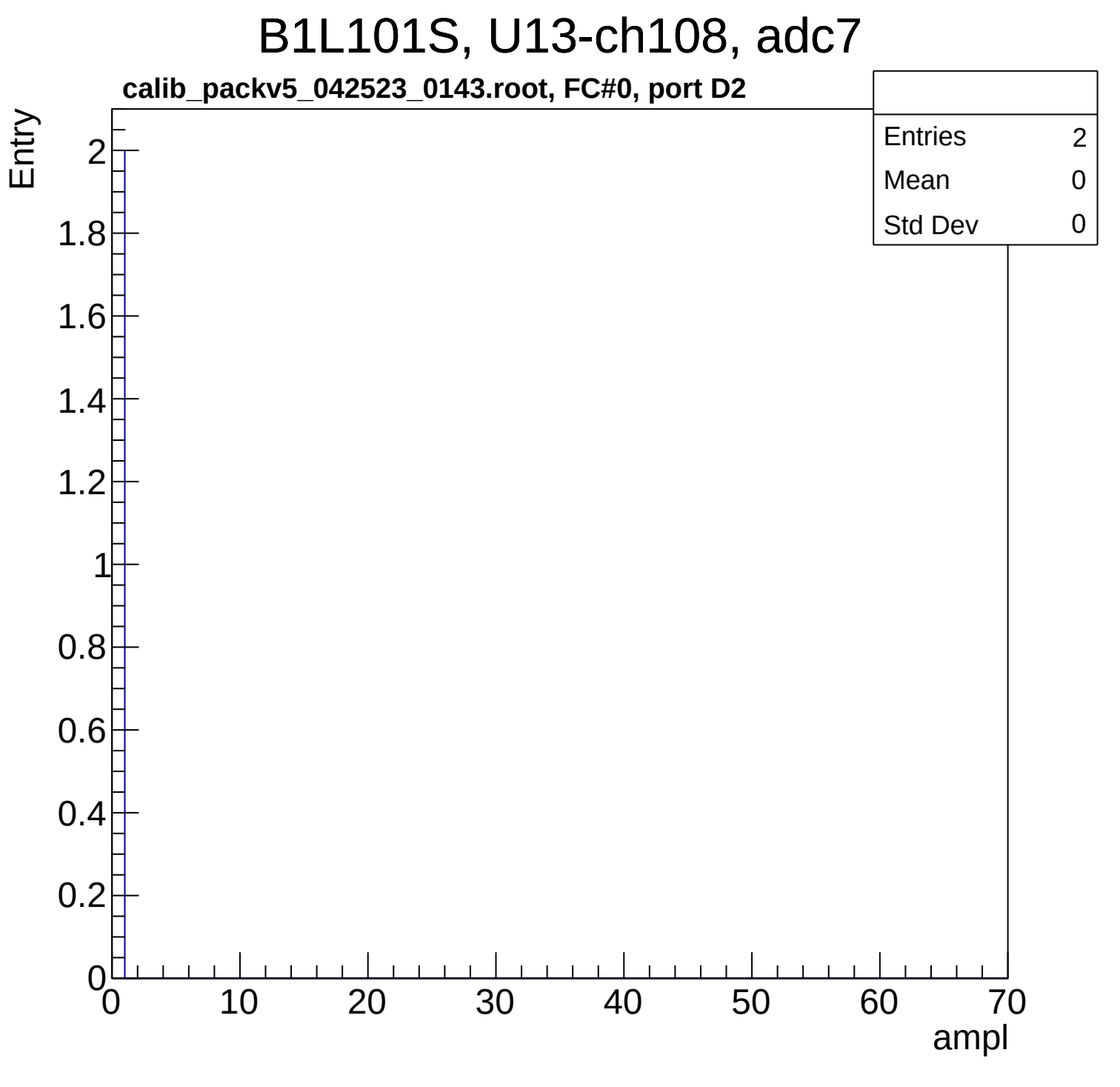

B1L101S, U13-ch108, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch108, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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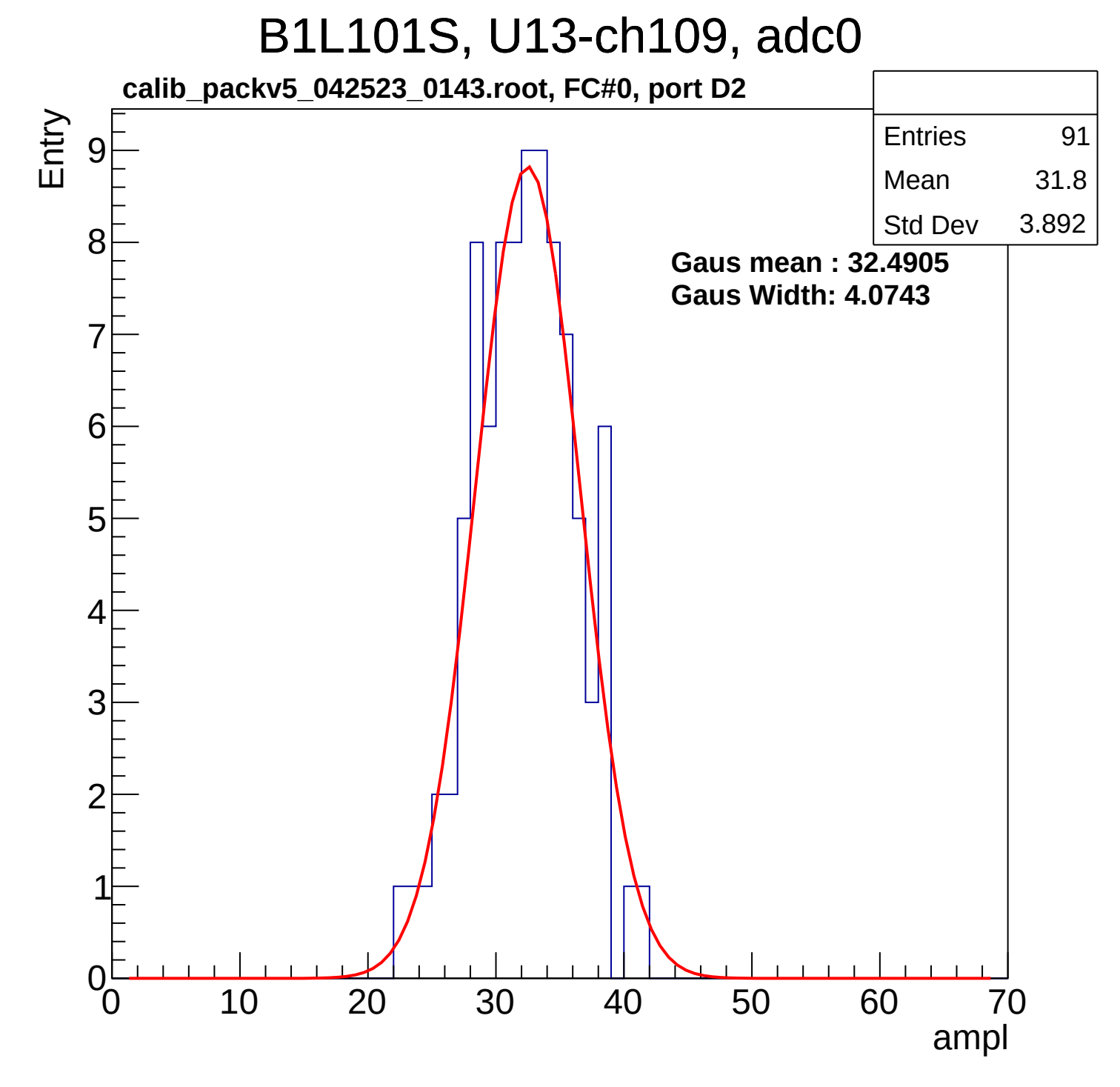
B1L101S, U13-ch109, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 91
Mean 31.8
Std Dev 3.892
Gaus mean : 32.4905
Gaus Width: 4.0743
Entry
ampl
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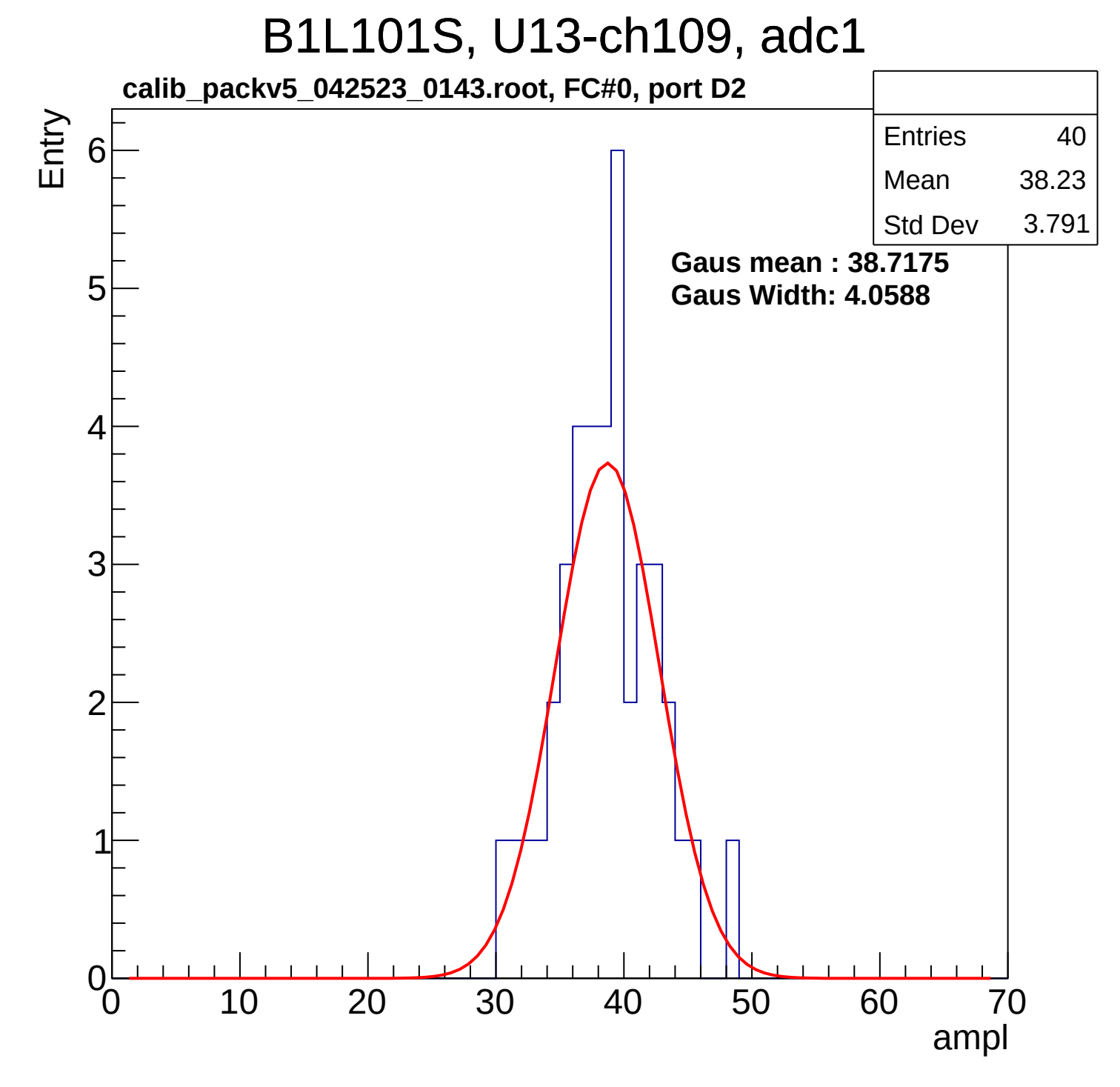
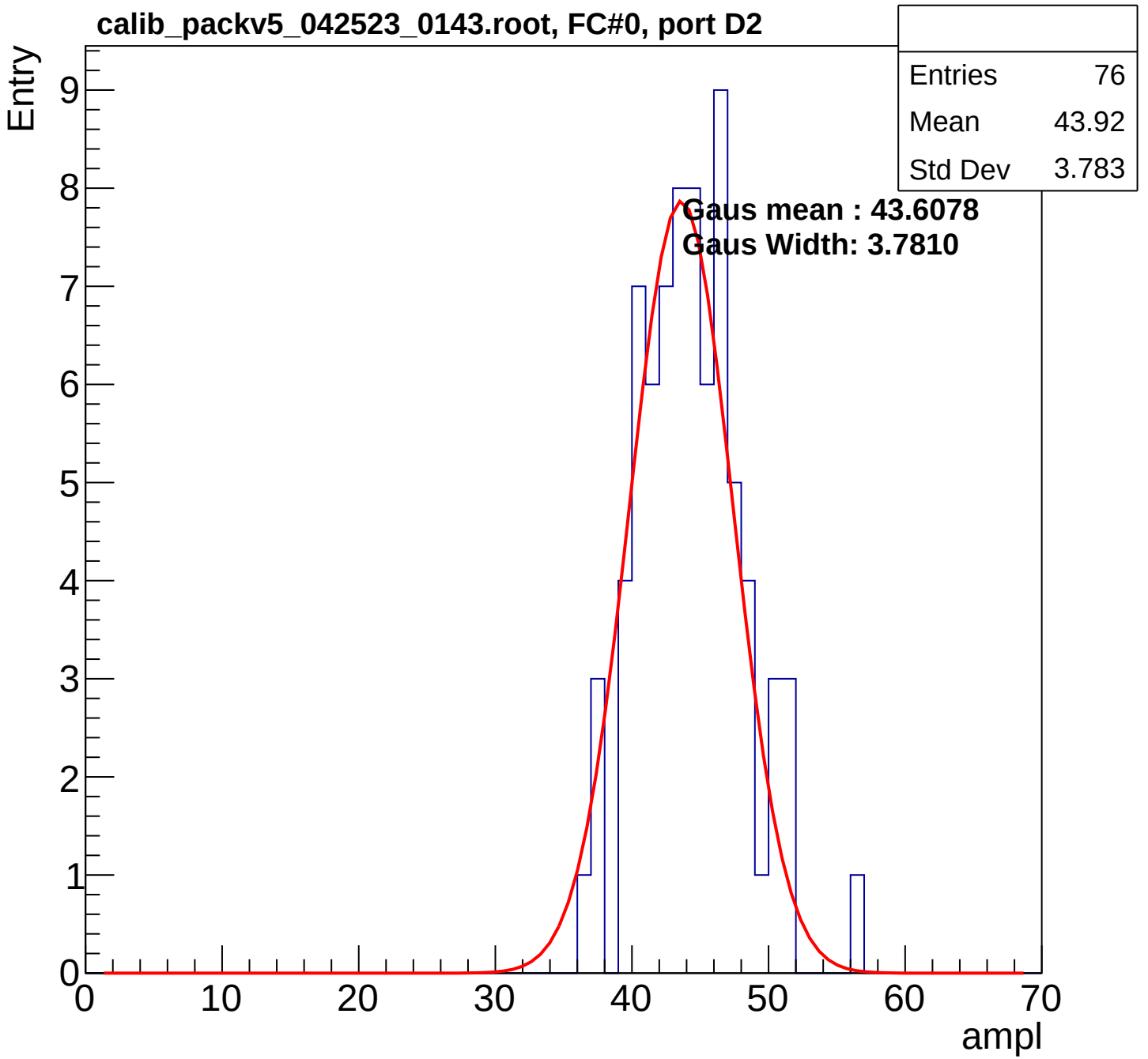
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch109, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 38.23
Std Dev 3.791
Gaus mean : 38.7175
Gaus Width: 4.0588
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch109, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 43.92
Std Dev 3.783
Gaus mean : 43.6078
Gaus Width: 3.7810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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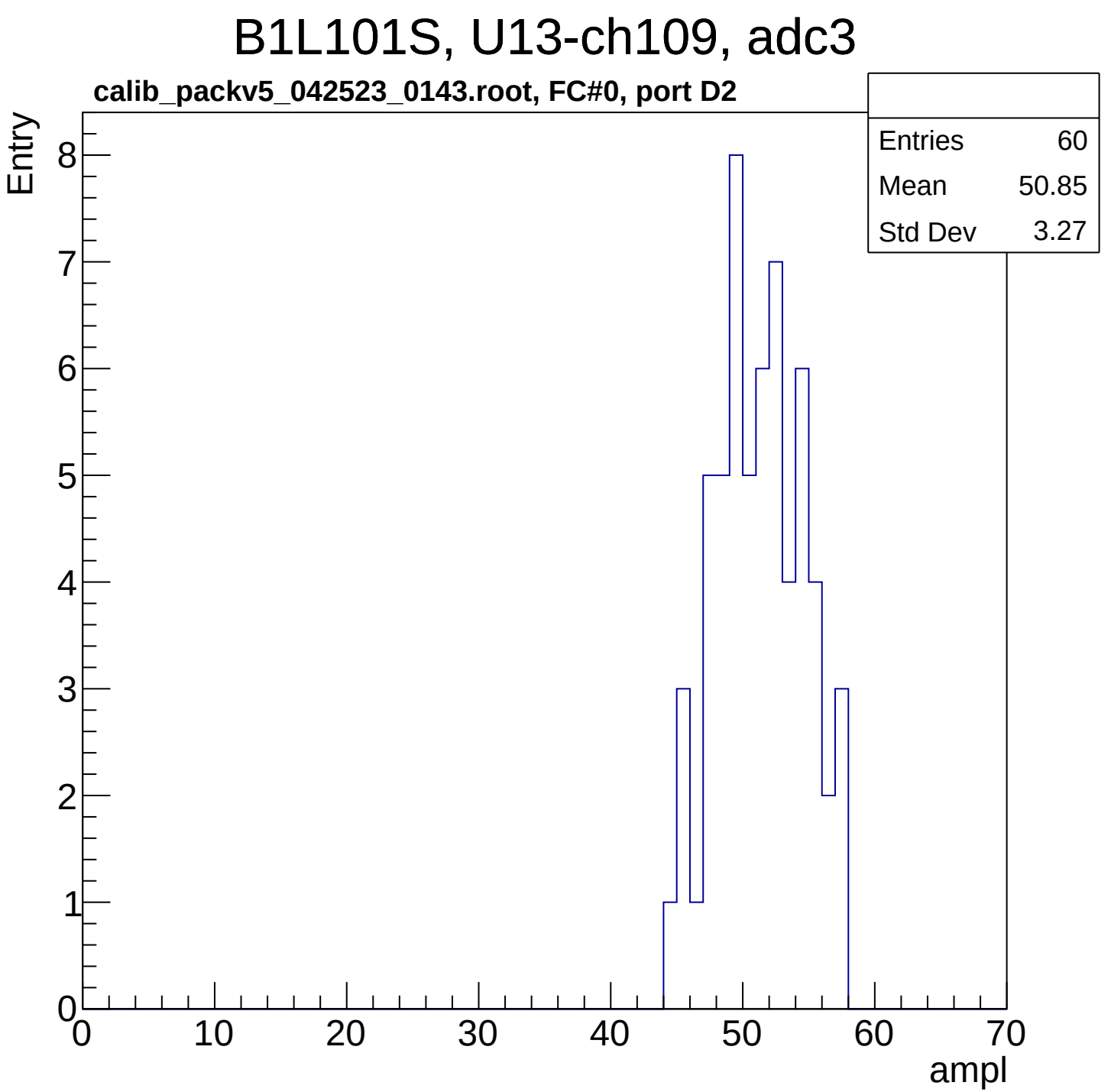
B1L101S, U13-ch109, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 50.85
Std Dev 3.27
Entry
ampl
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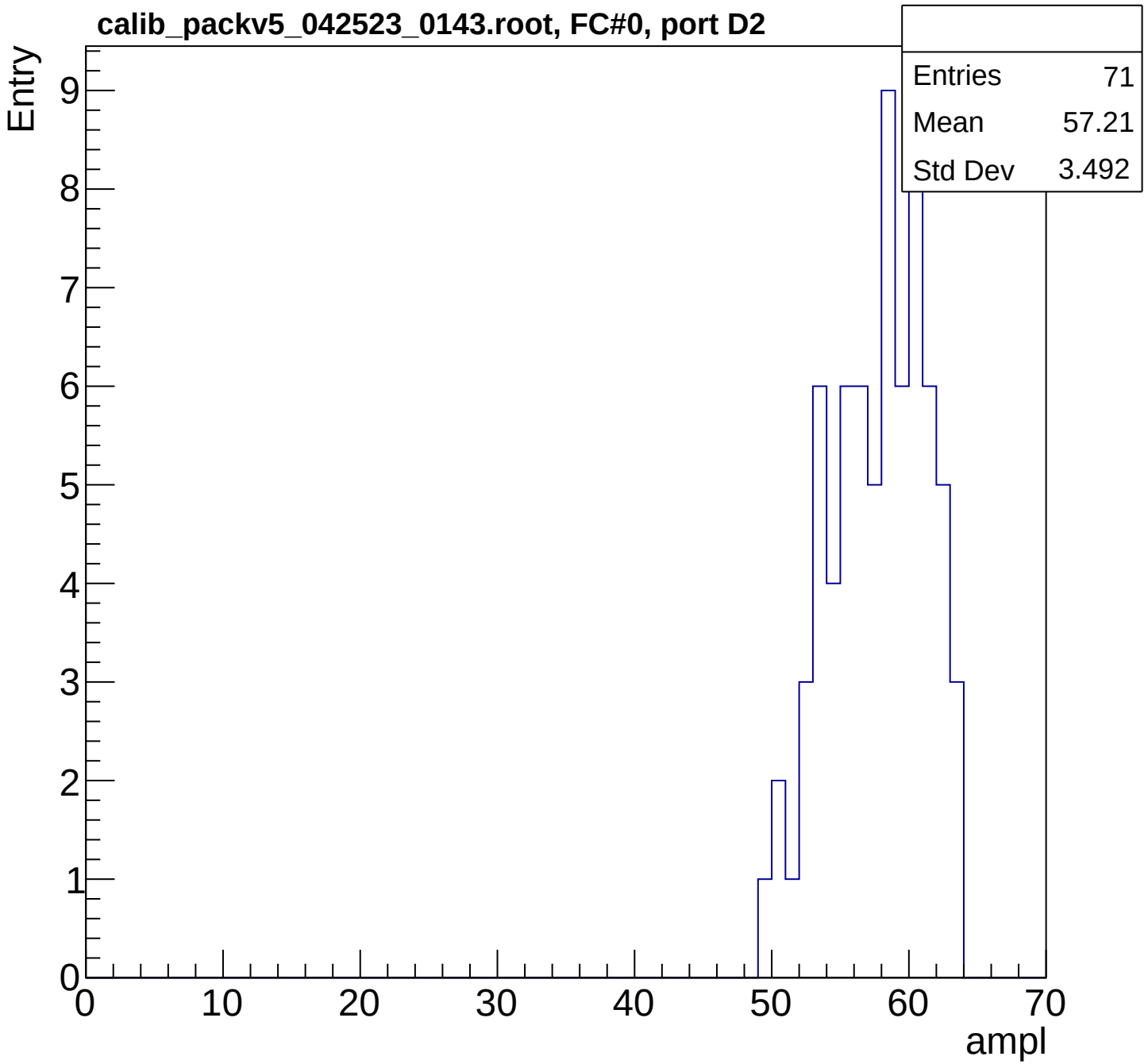
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch109, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 57.21
Std Dev 3.492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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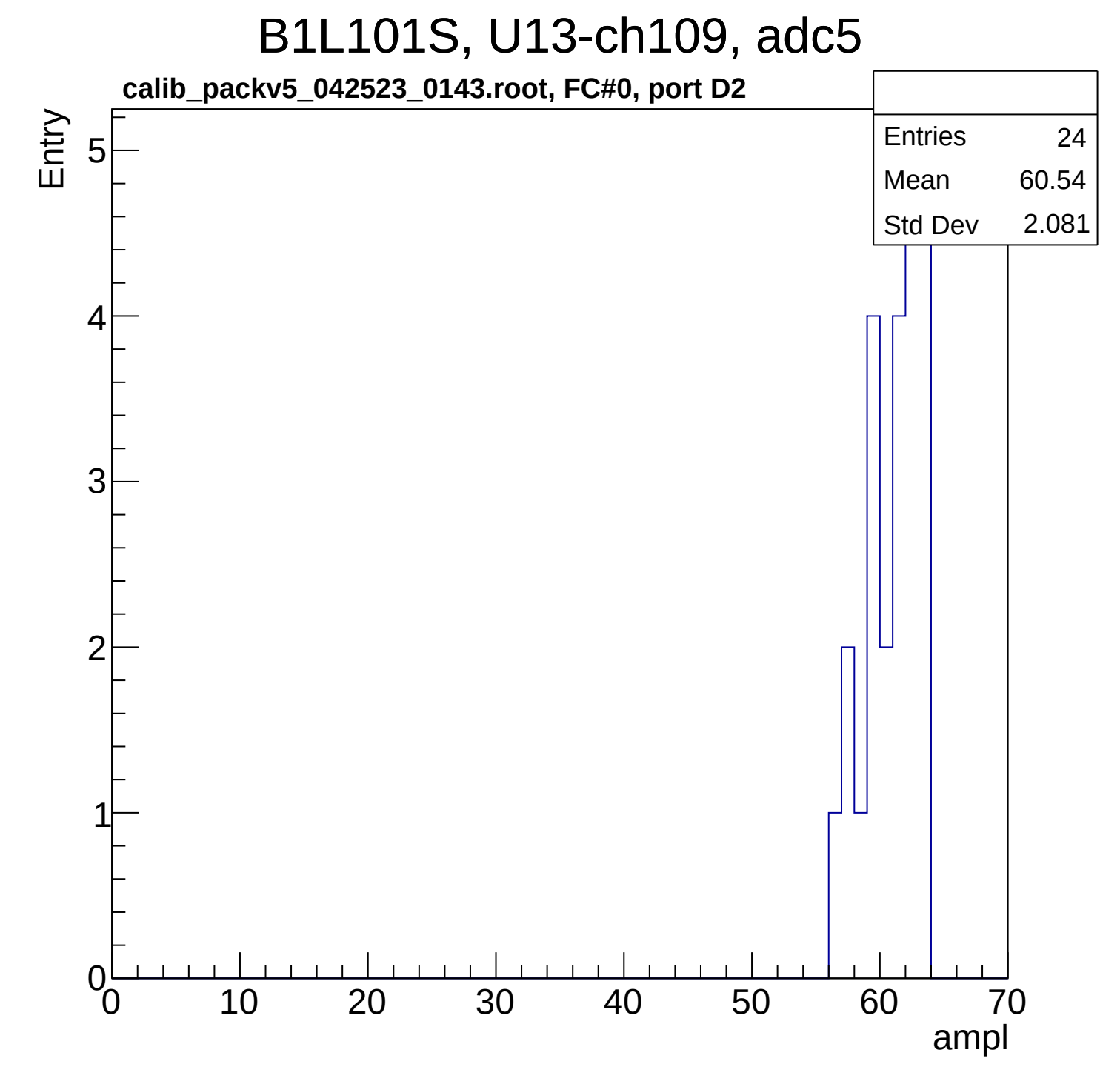
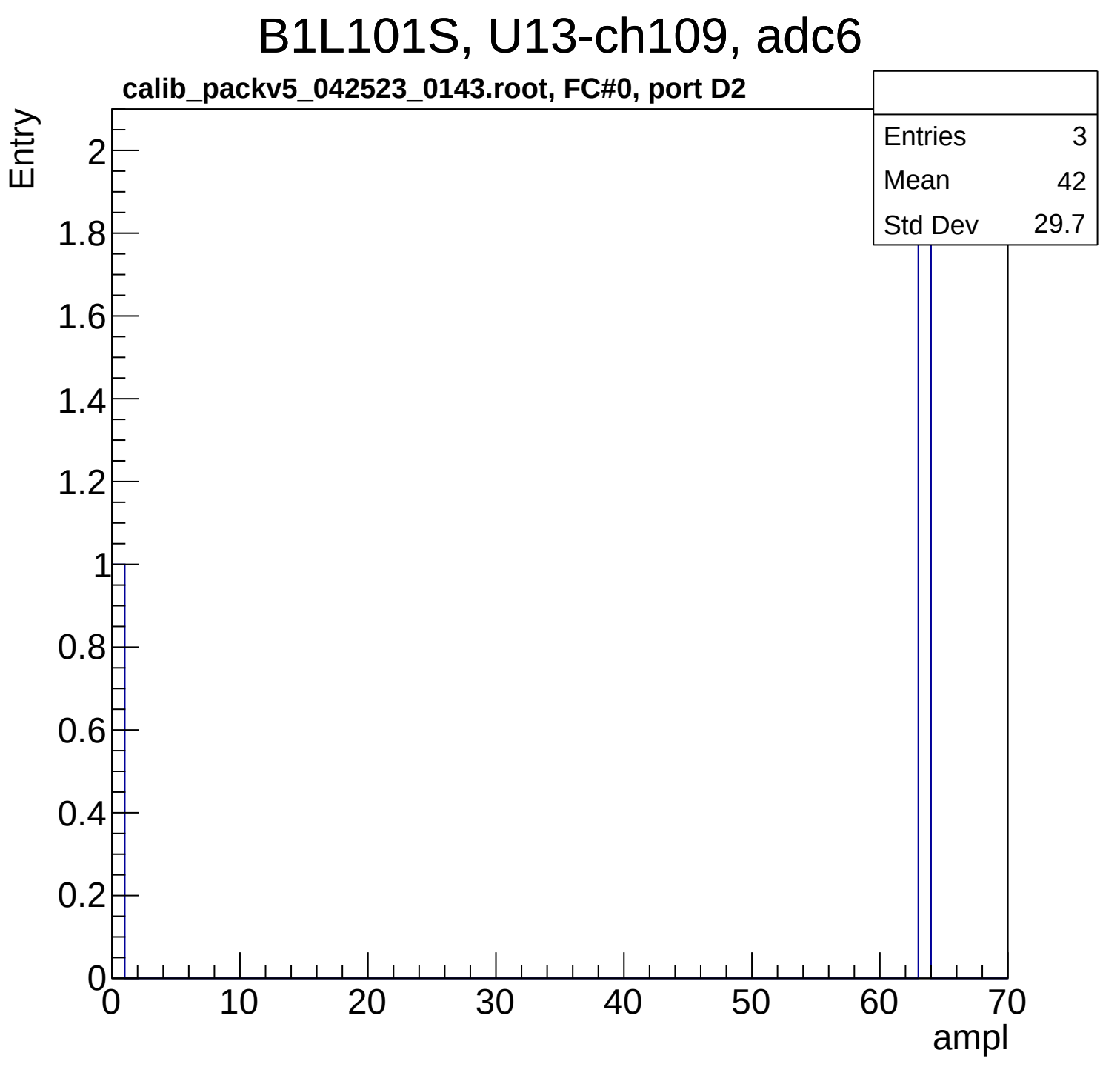
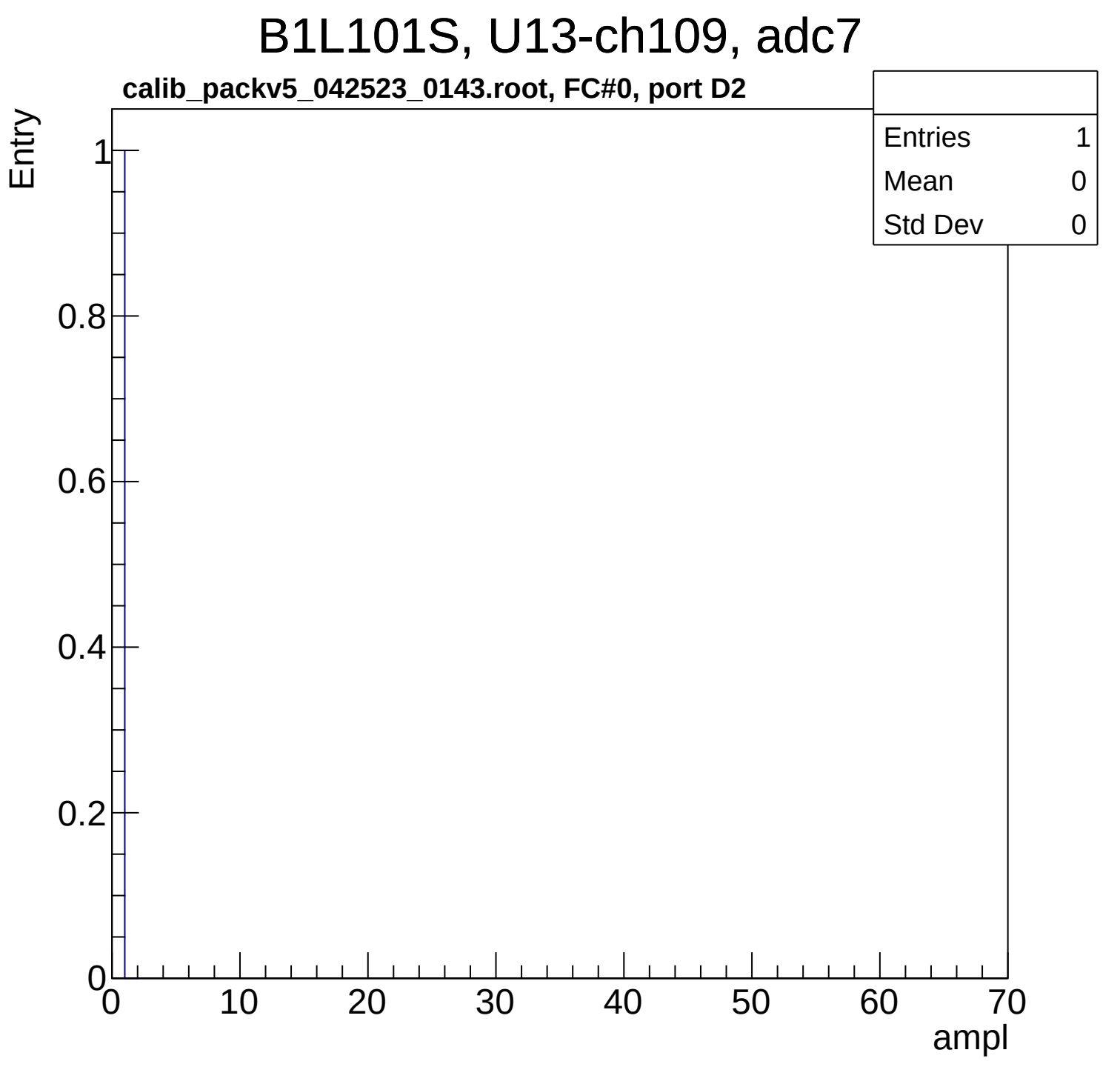
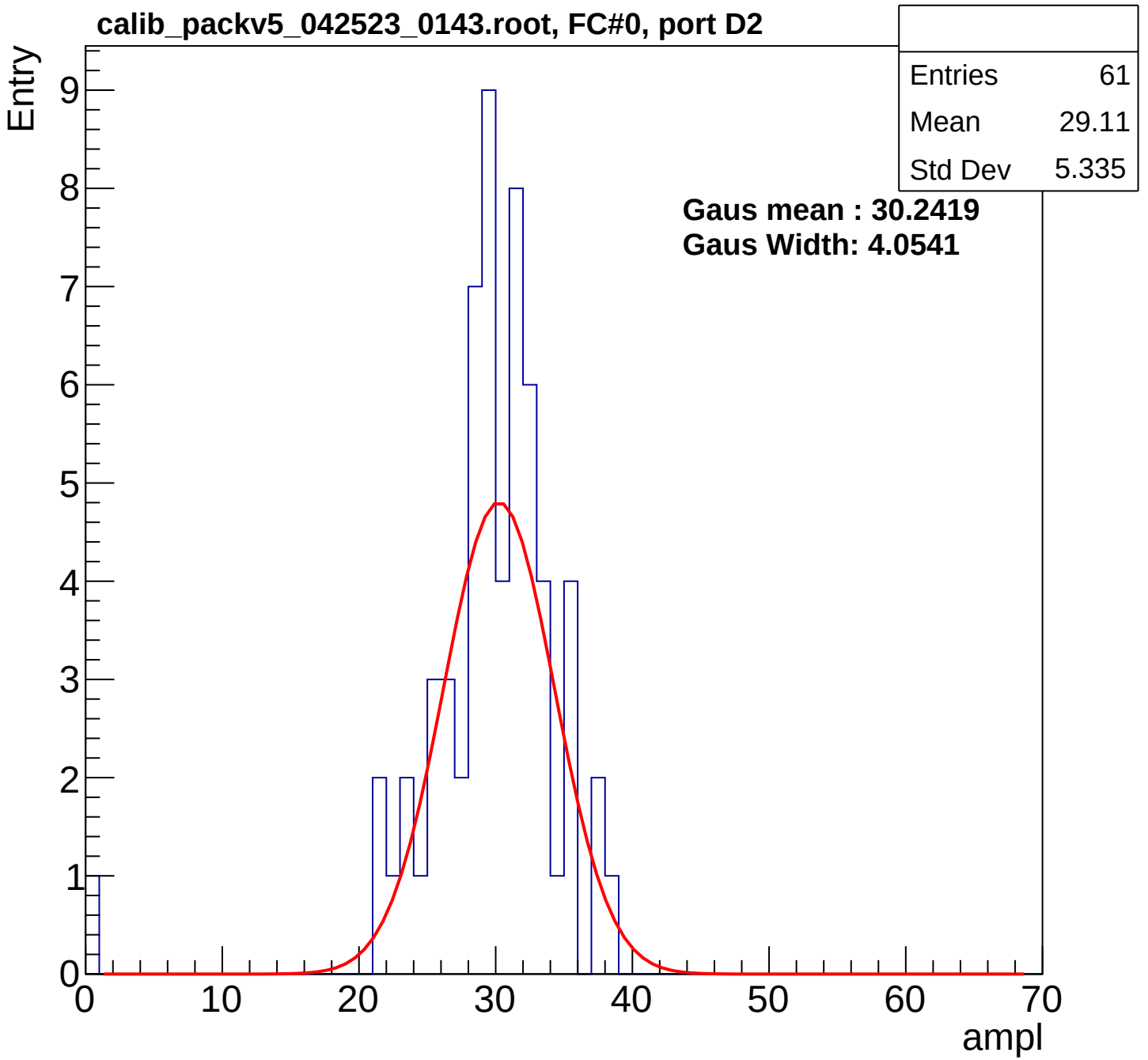
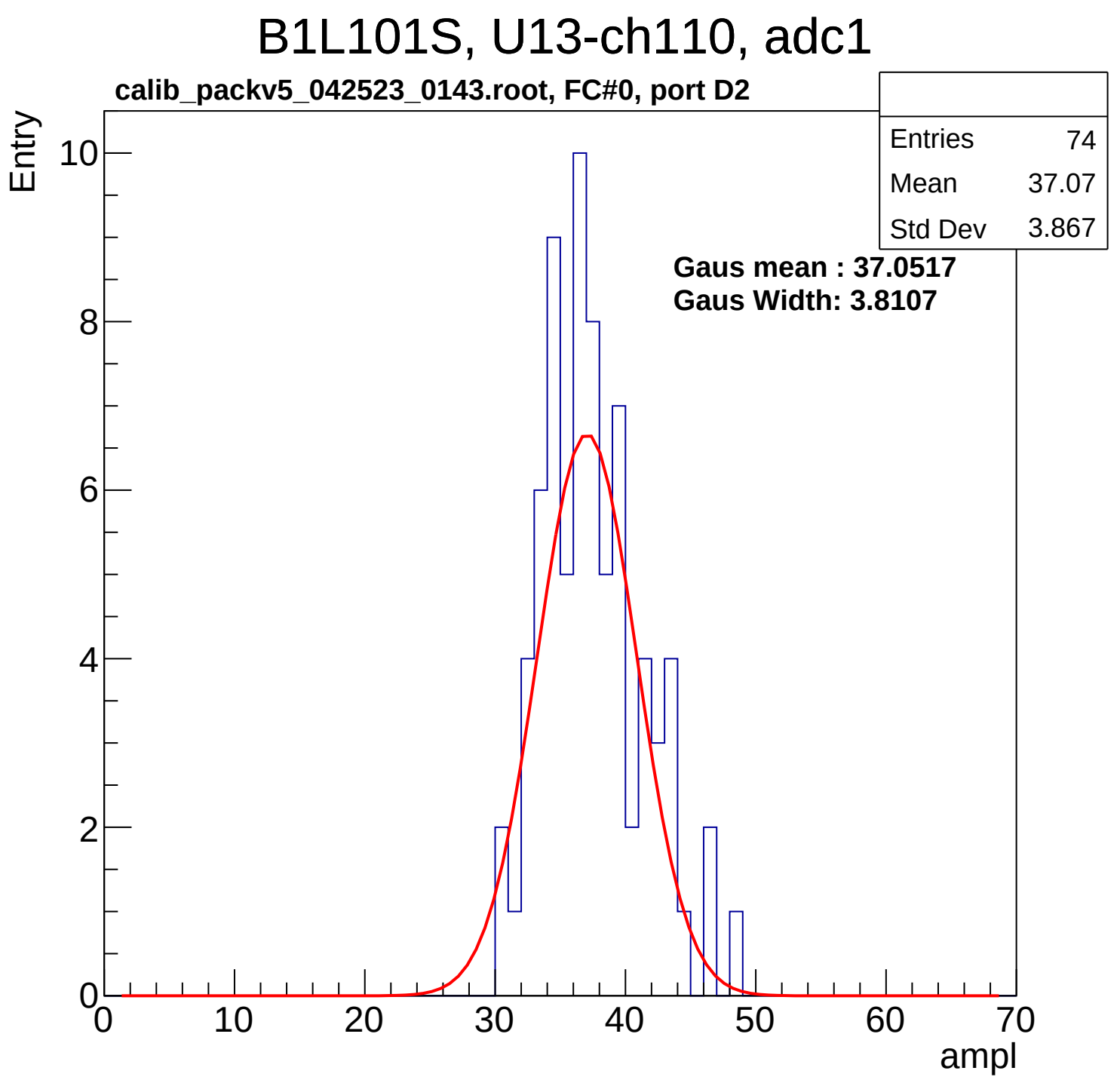
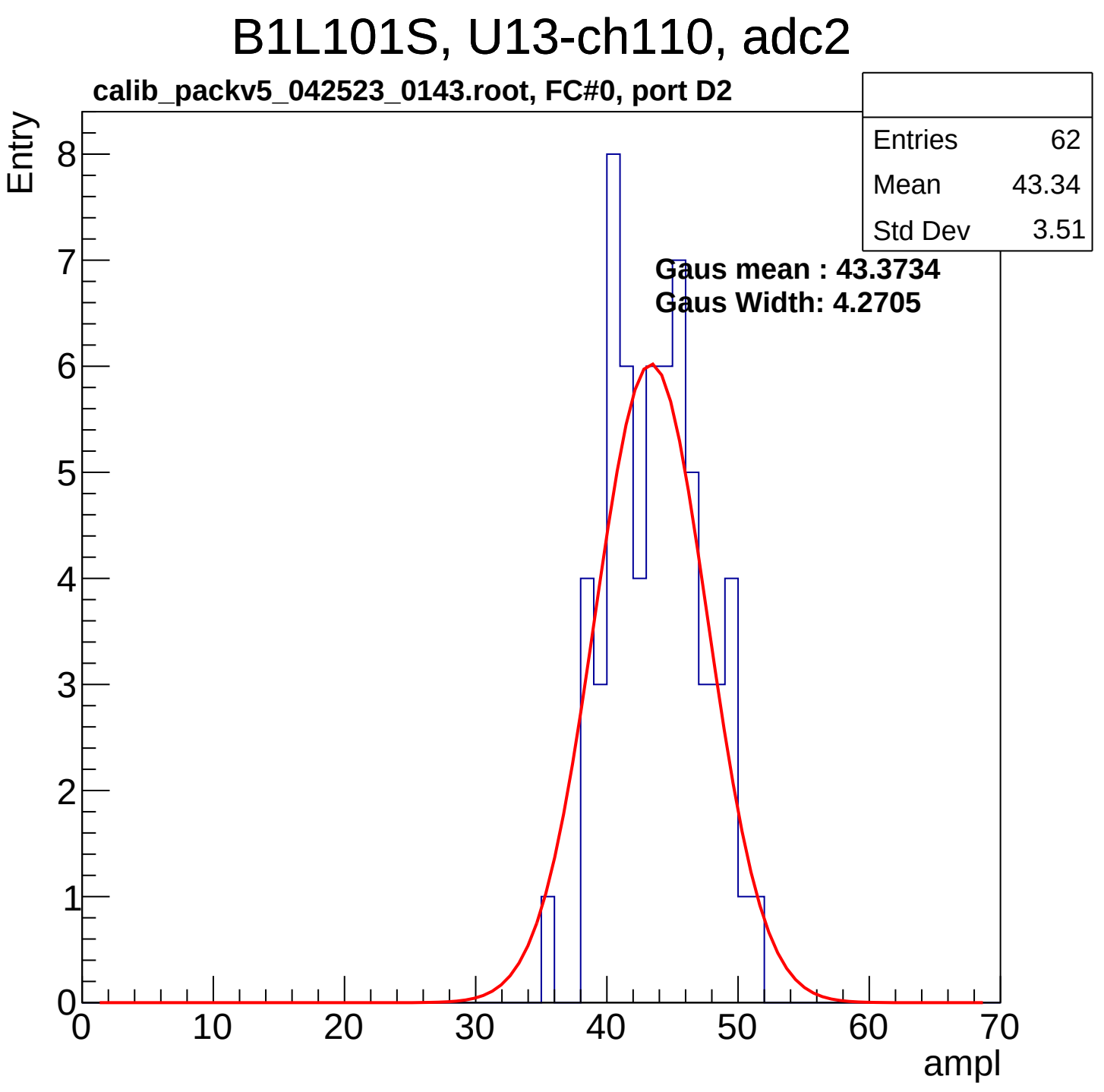

B1L101S, U13-ch109, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 24
Mean 60.54
Std Dev 2.081
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L101S, U13-ch109, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch109, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch110, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 29.11
Std Dev 5.335
Gaus mean : 30.2419
Gaus Width: 4.0541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch110, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 37.07
Std Dev 3.867
Gaus mean : 37.0517
Gaus Width: 3.8107
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch110, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 43.34
Std Dev 3.51
Gaus mean : 43.3734
Gaus Width: 4.2705
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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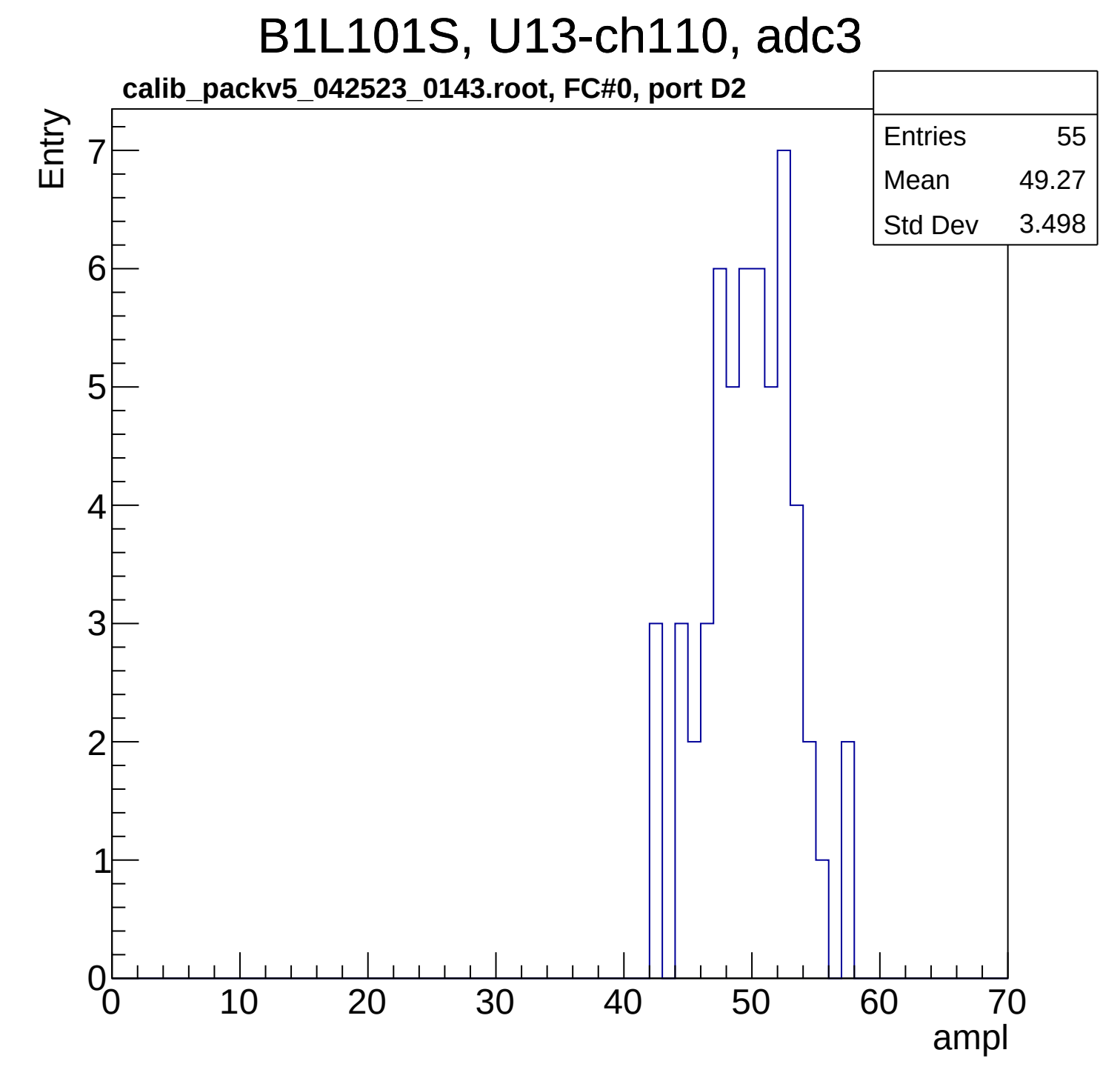
B1L101S, U13-ch110, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 49.27
Std Dev 3.498
Entry
ampl
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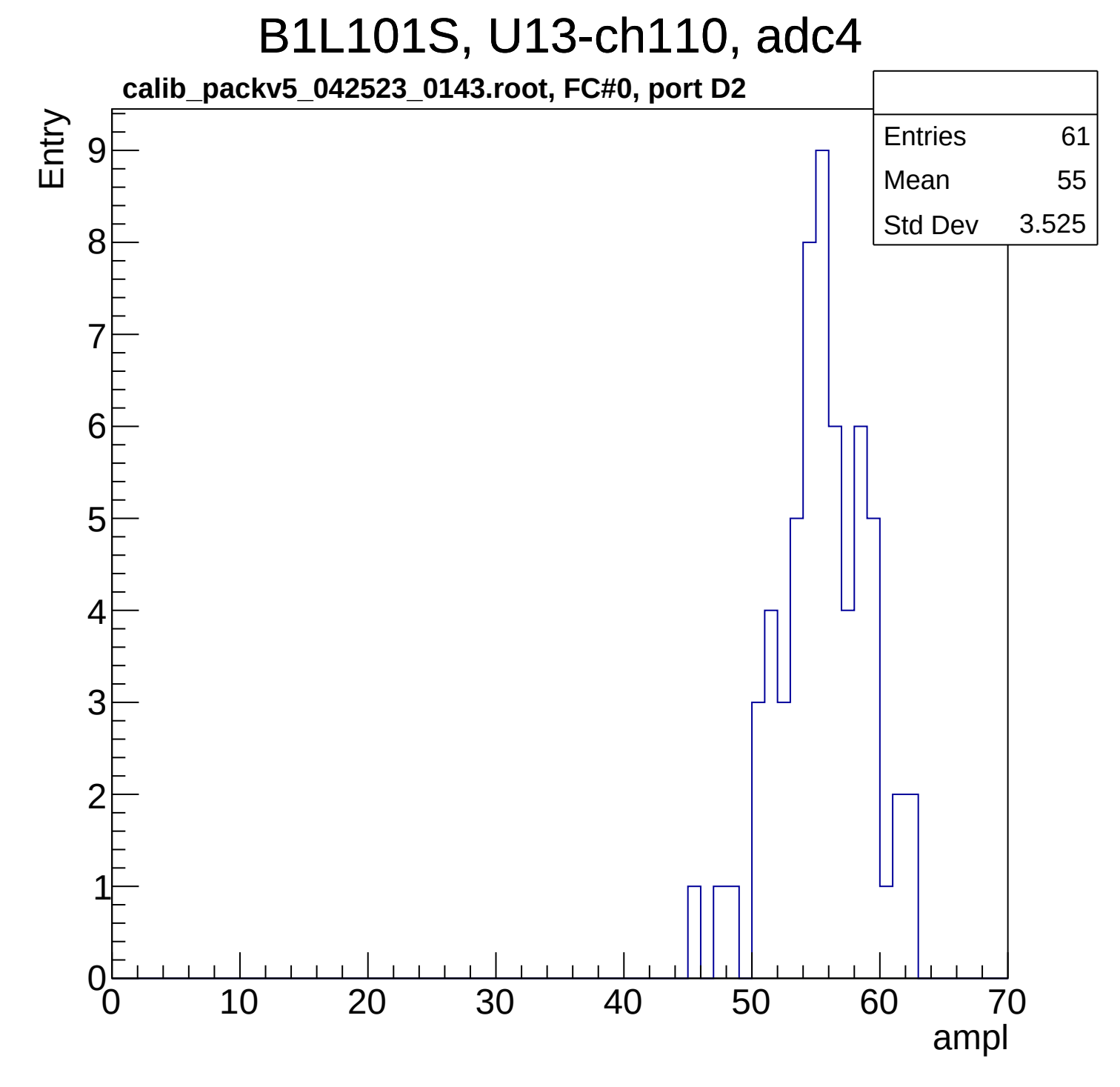
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch110, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 55
Std Dev 3.525
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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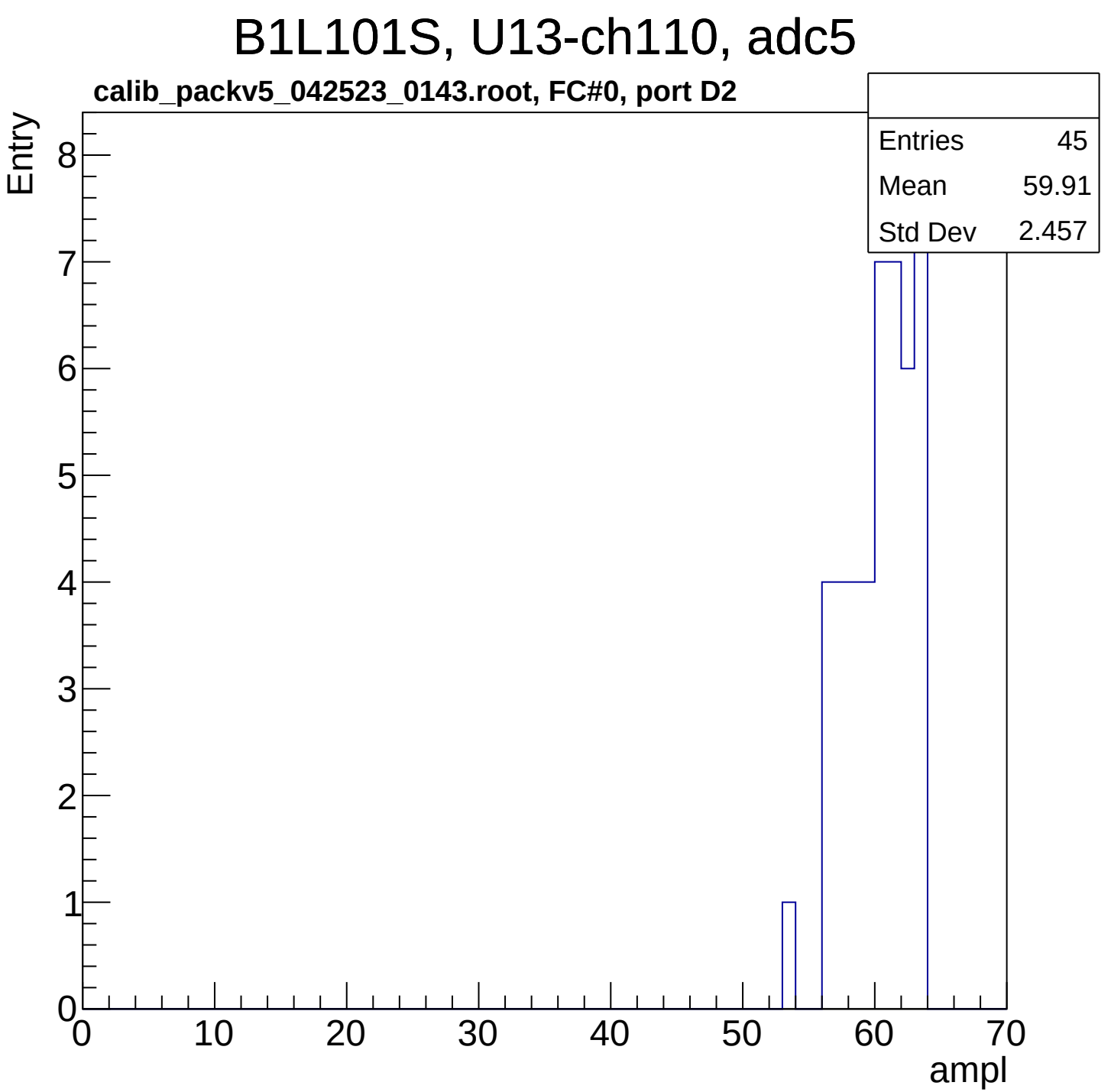
B1L101S, U13-ch110, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 45
Mean 59.91
Std Dev 2.457
Entry
ampl
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1
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3
4
5
6
7
8
0
10
20
30
40
50
60
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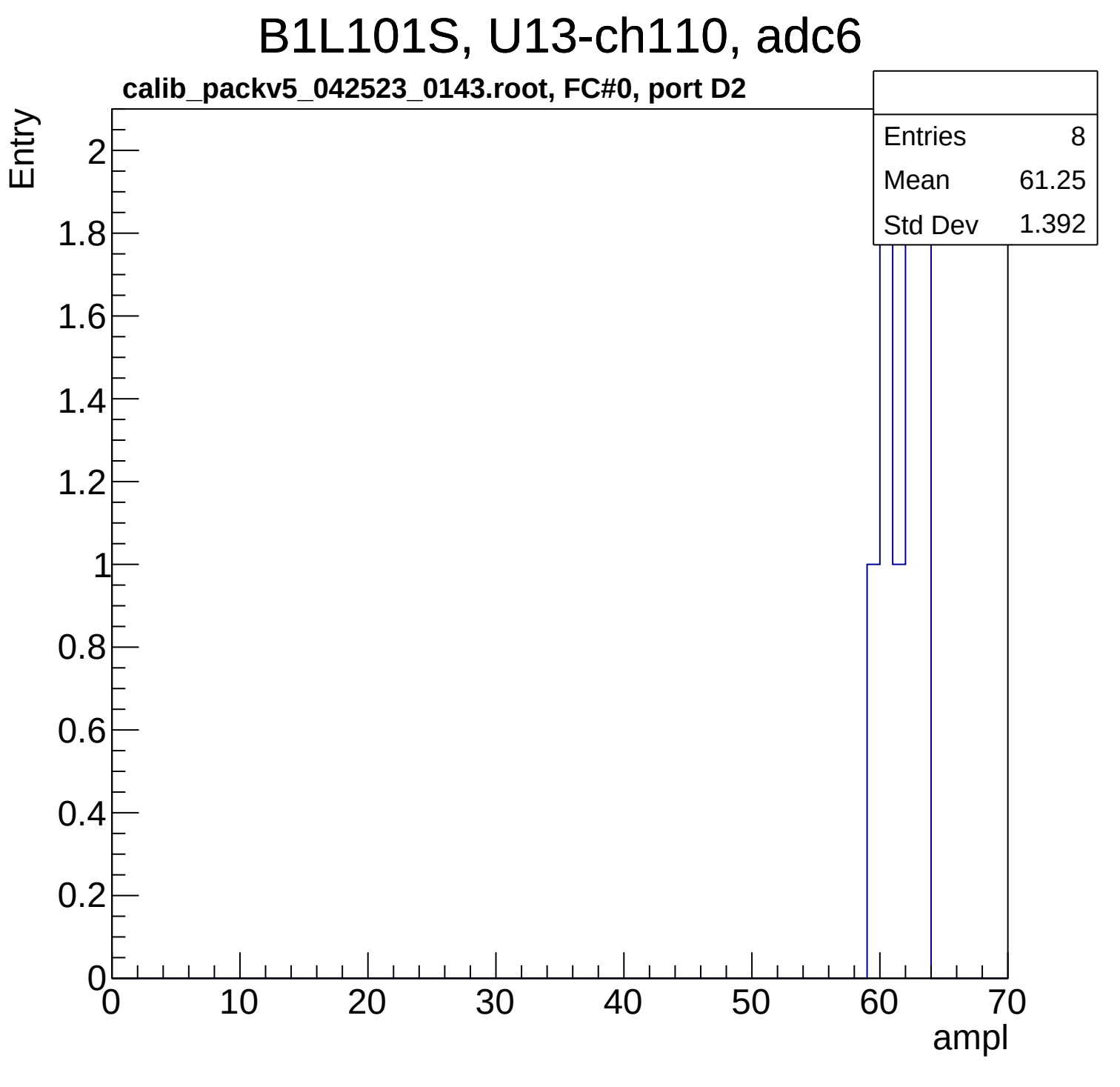
B1L101S, U13-ch110, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 8
Mean 61.25
Std Dev 1.392
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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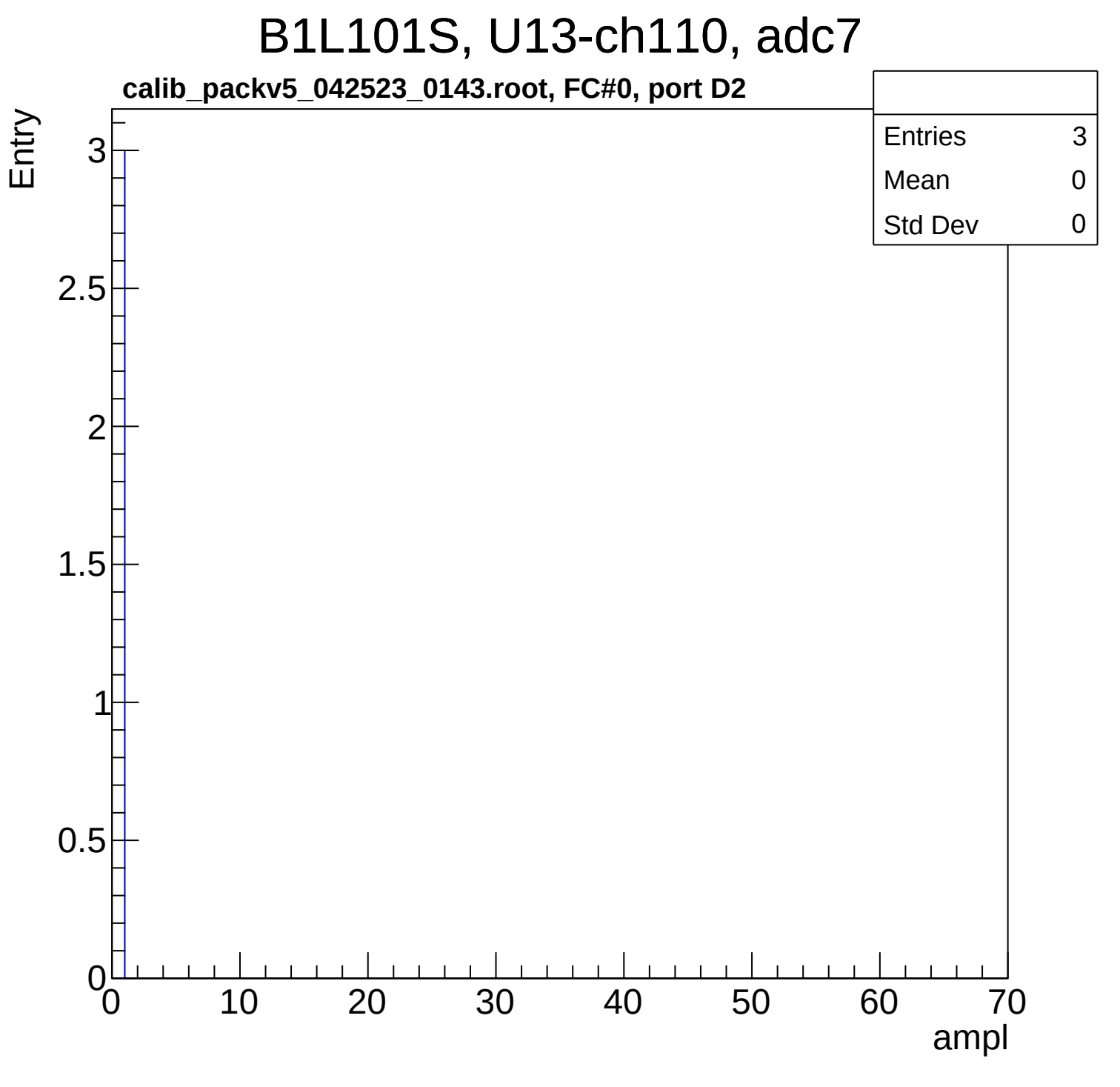
60
70


B1L101S, U13-ch110, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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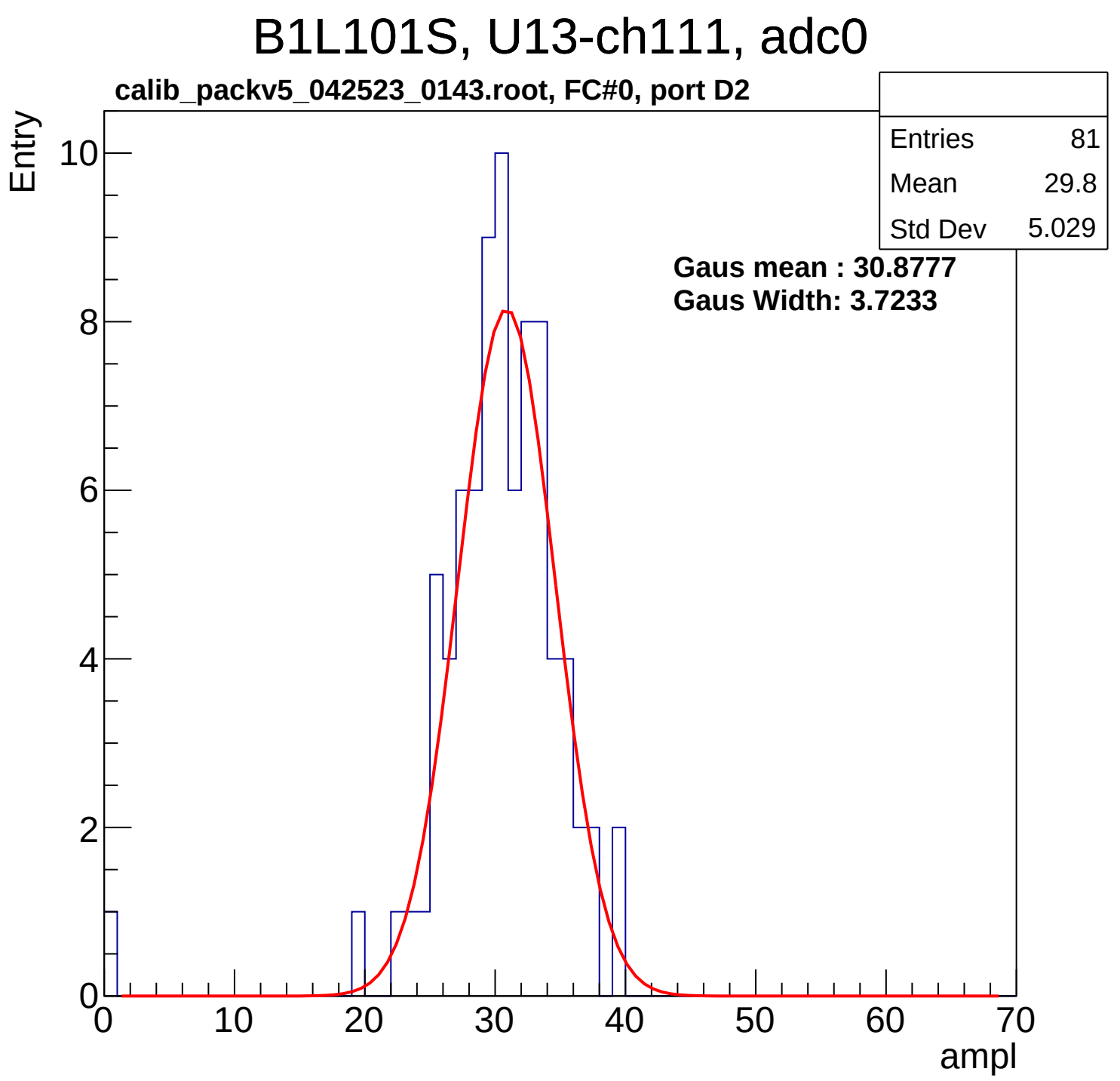
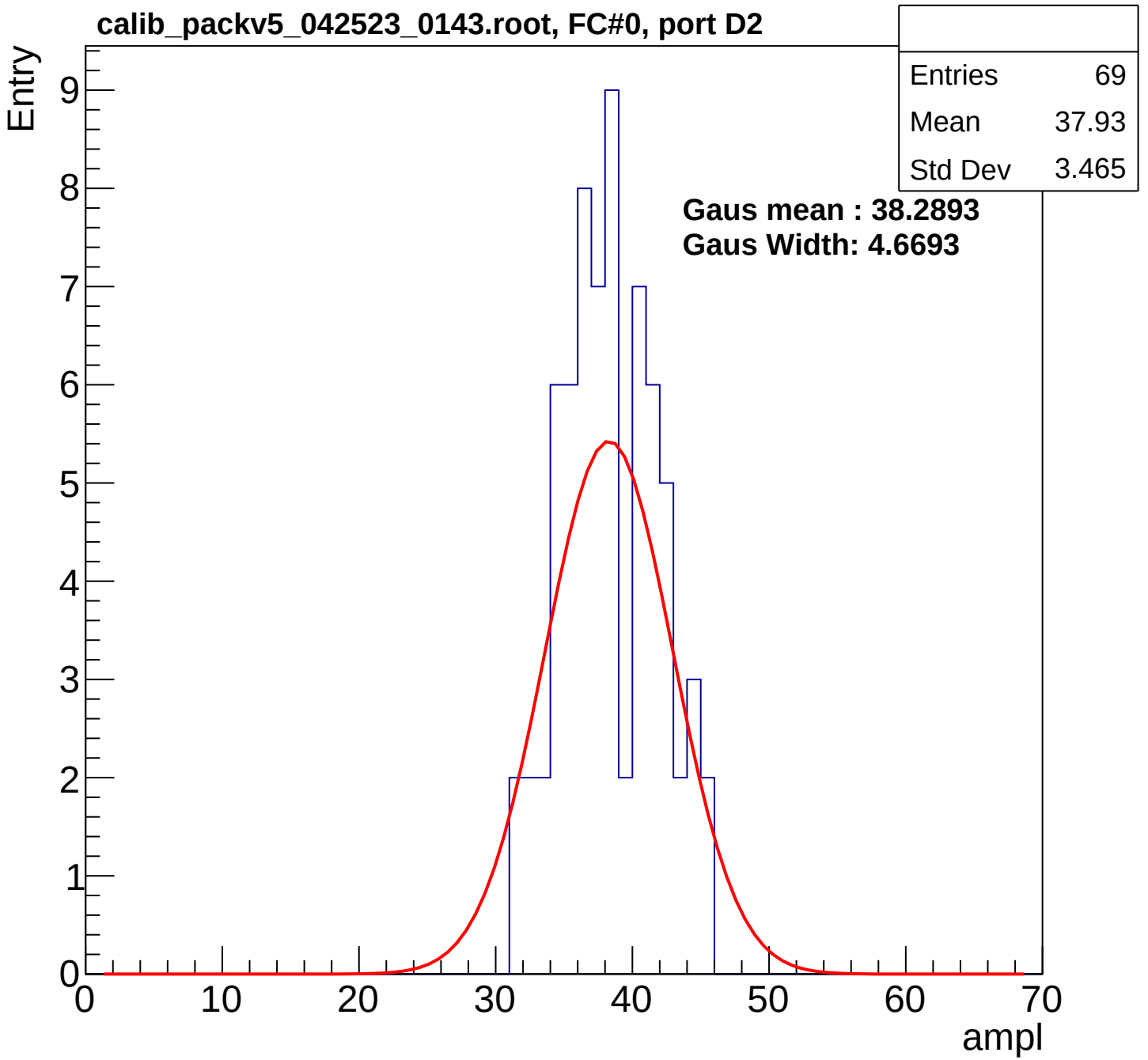
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch111, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 81
Mean 29.8
Std Dev 5.029
Gaus mean : 30.8777
Gaus Width: 3.7233
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch111, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 37.93
Std Dev 3.465
Gaus mean : 38.2893
Gaus Width: 4.6693
Entry
ampl
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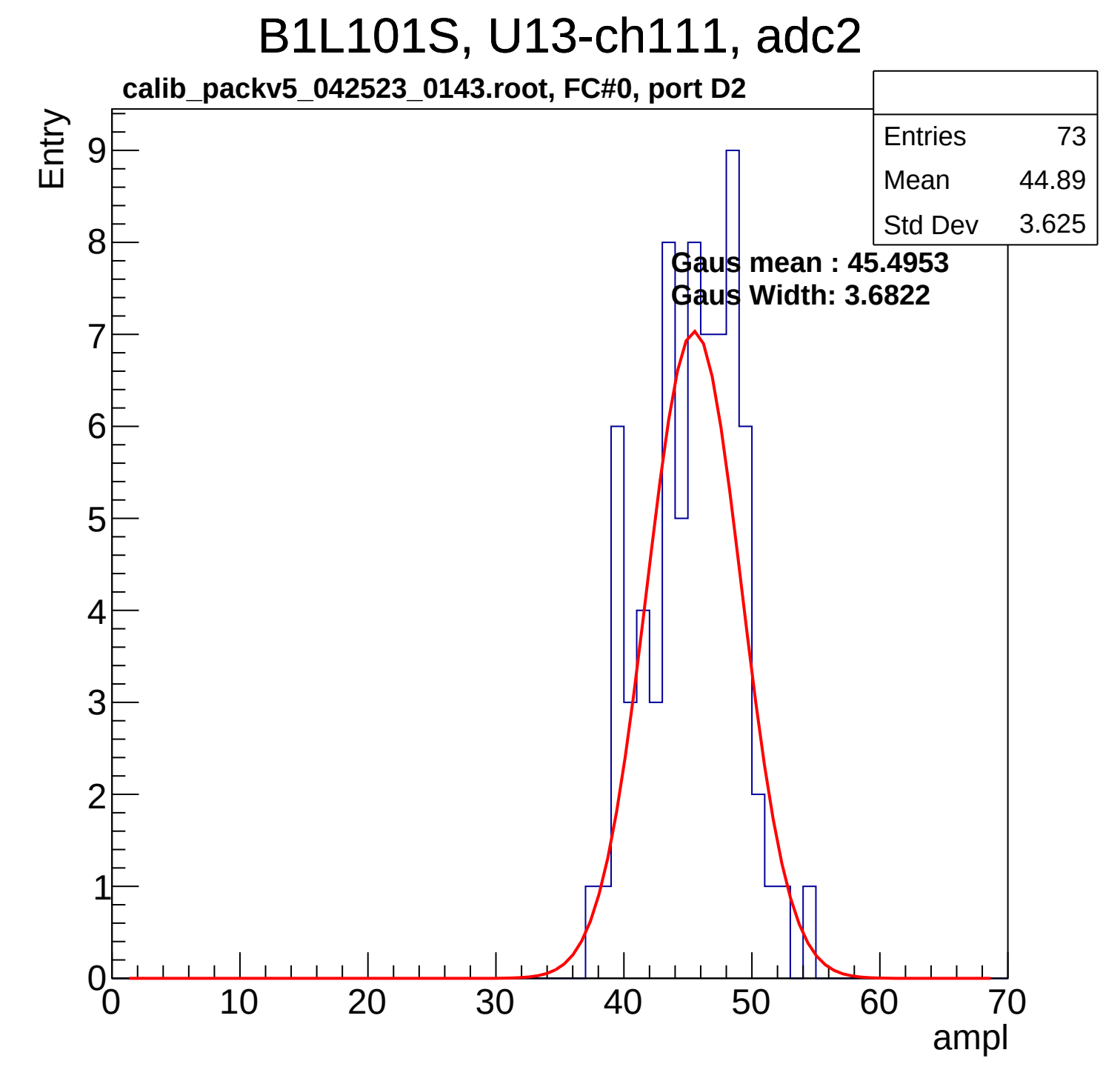
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch111, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 44.89
Std Dev 3.625
Gaus mean : 45.4953
Gaus Width: 3.6822
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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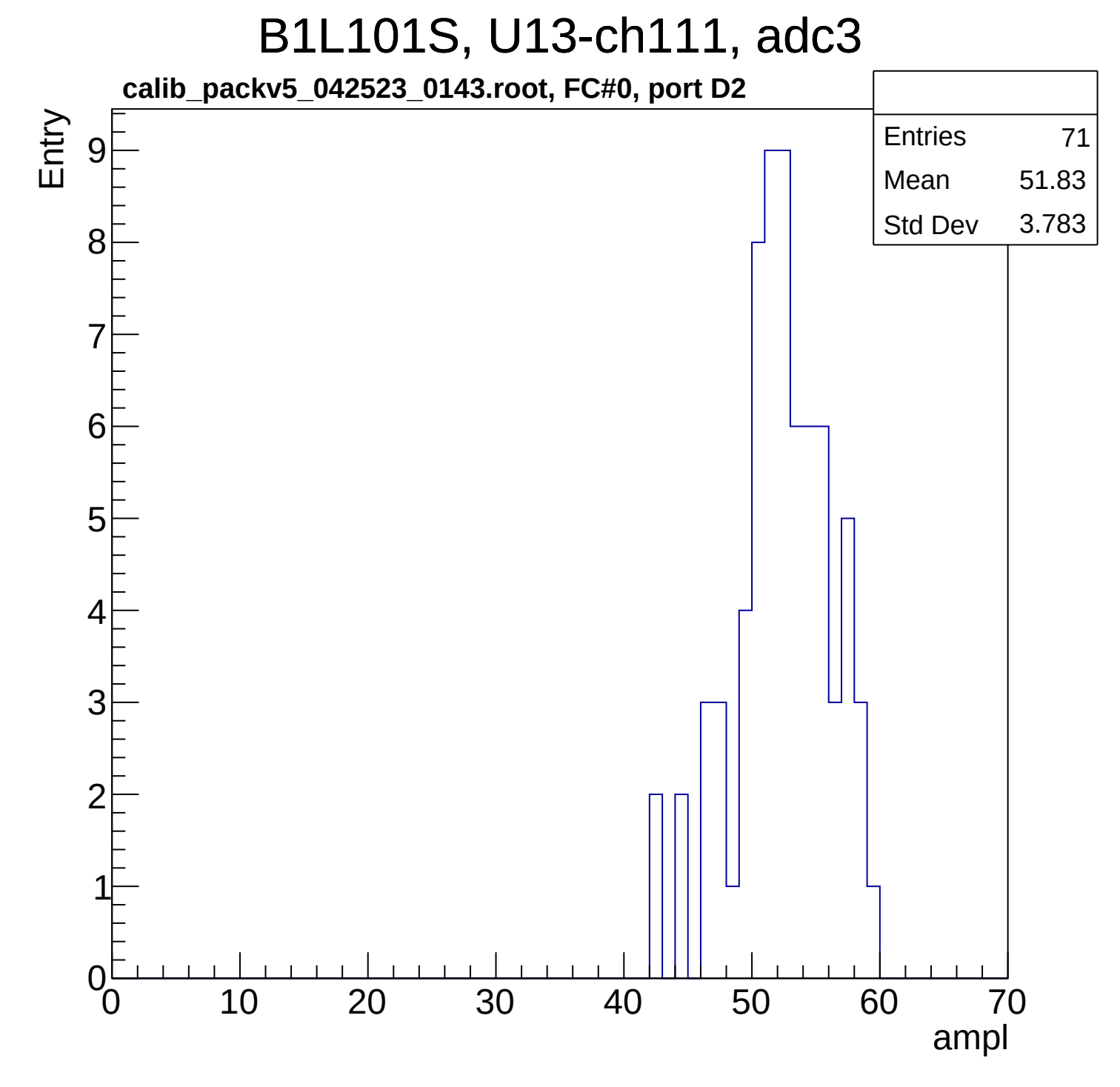
B1L101S, U13-ch111, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 51.83
Std Dev 3.783
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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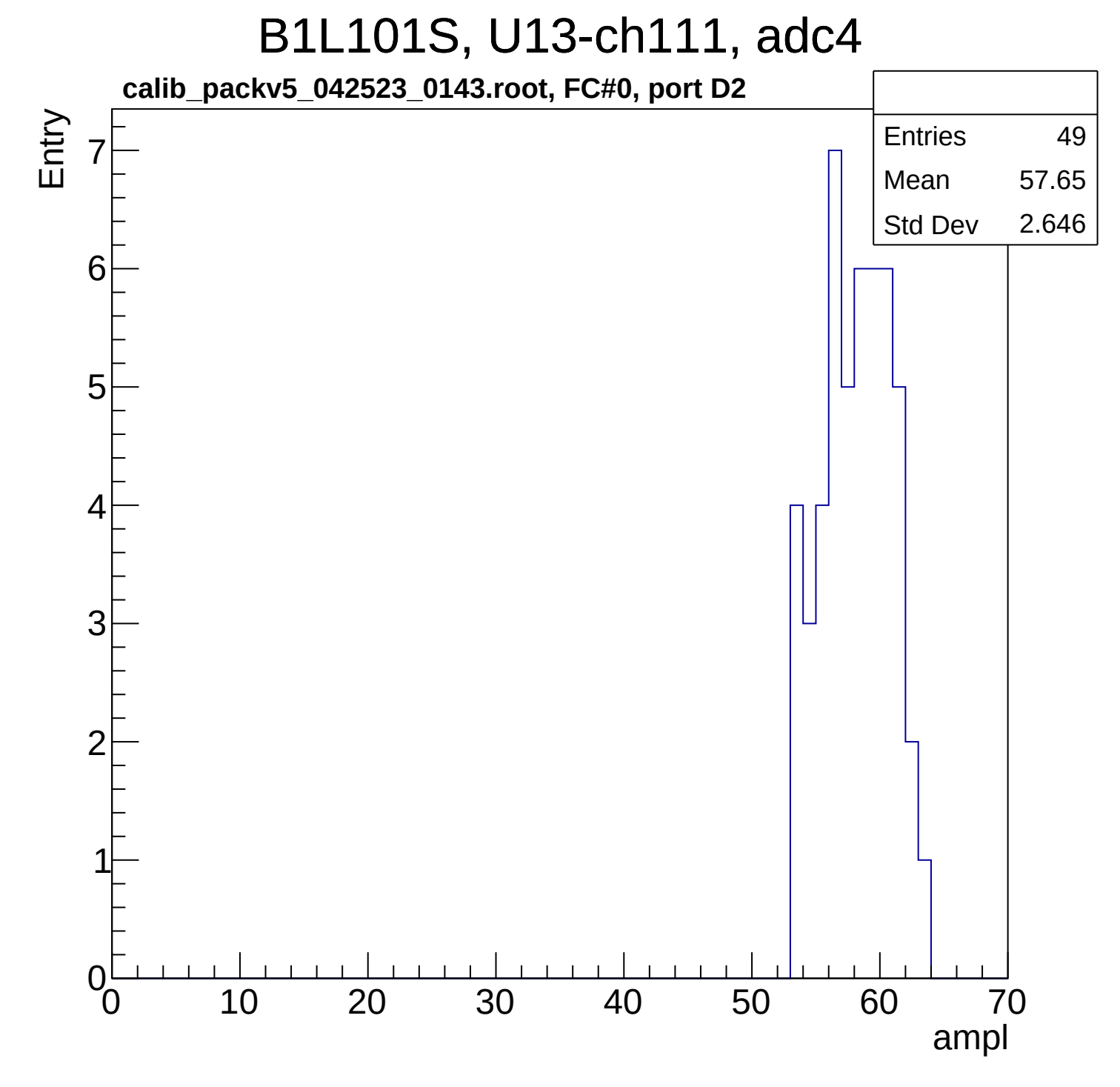
B1L101S, U13-ch111, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 49
Mean 57.65
Std Dev 2.646
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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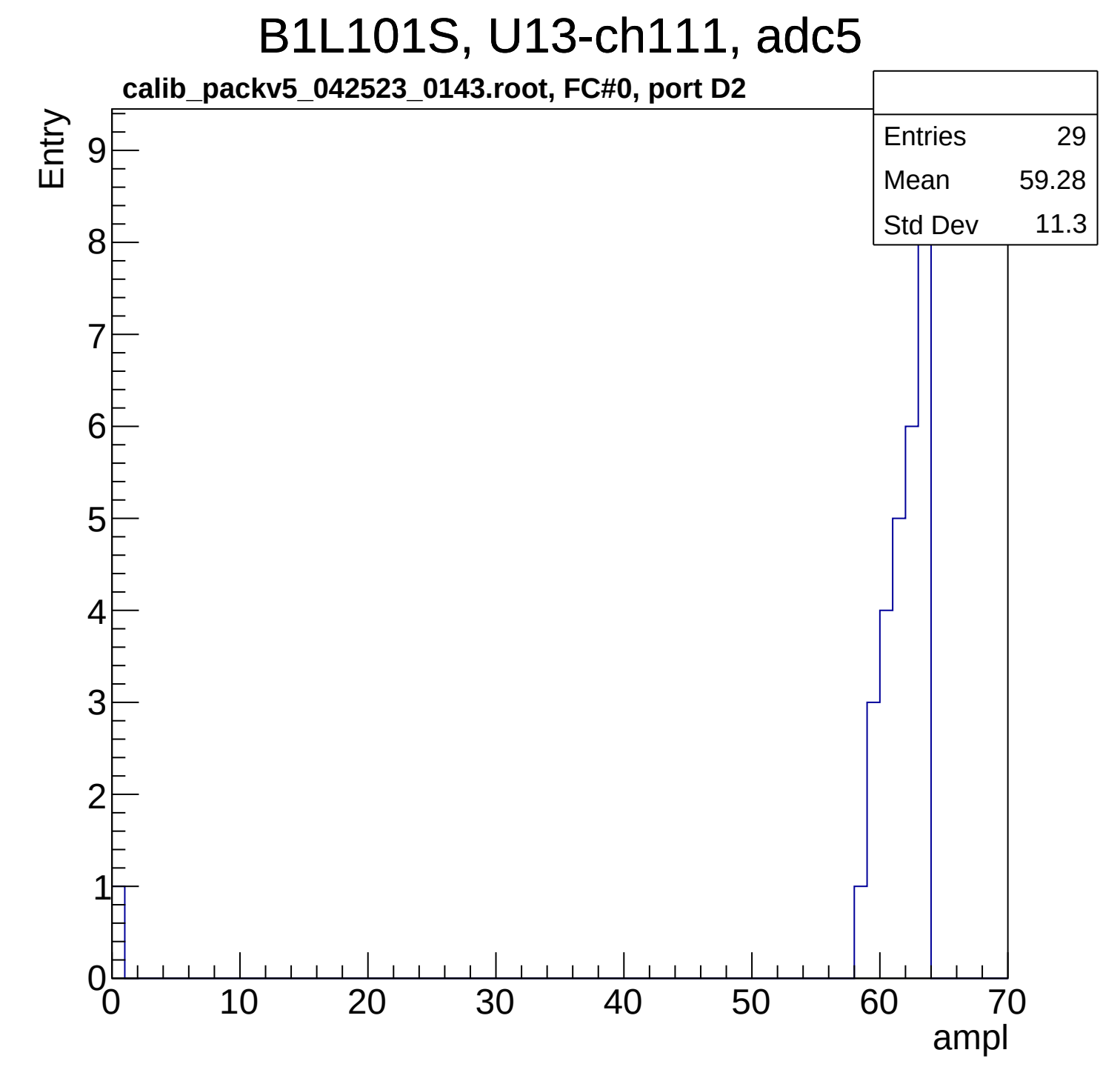
B1L101S, U13-ch111, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 29
Mean 59.28
Std Dev 11.3
Entry
ampl
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2
3
4
5
6
7
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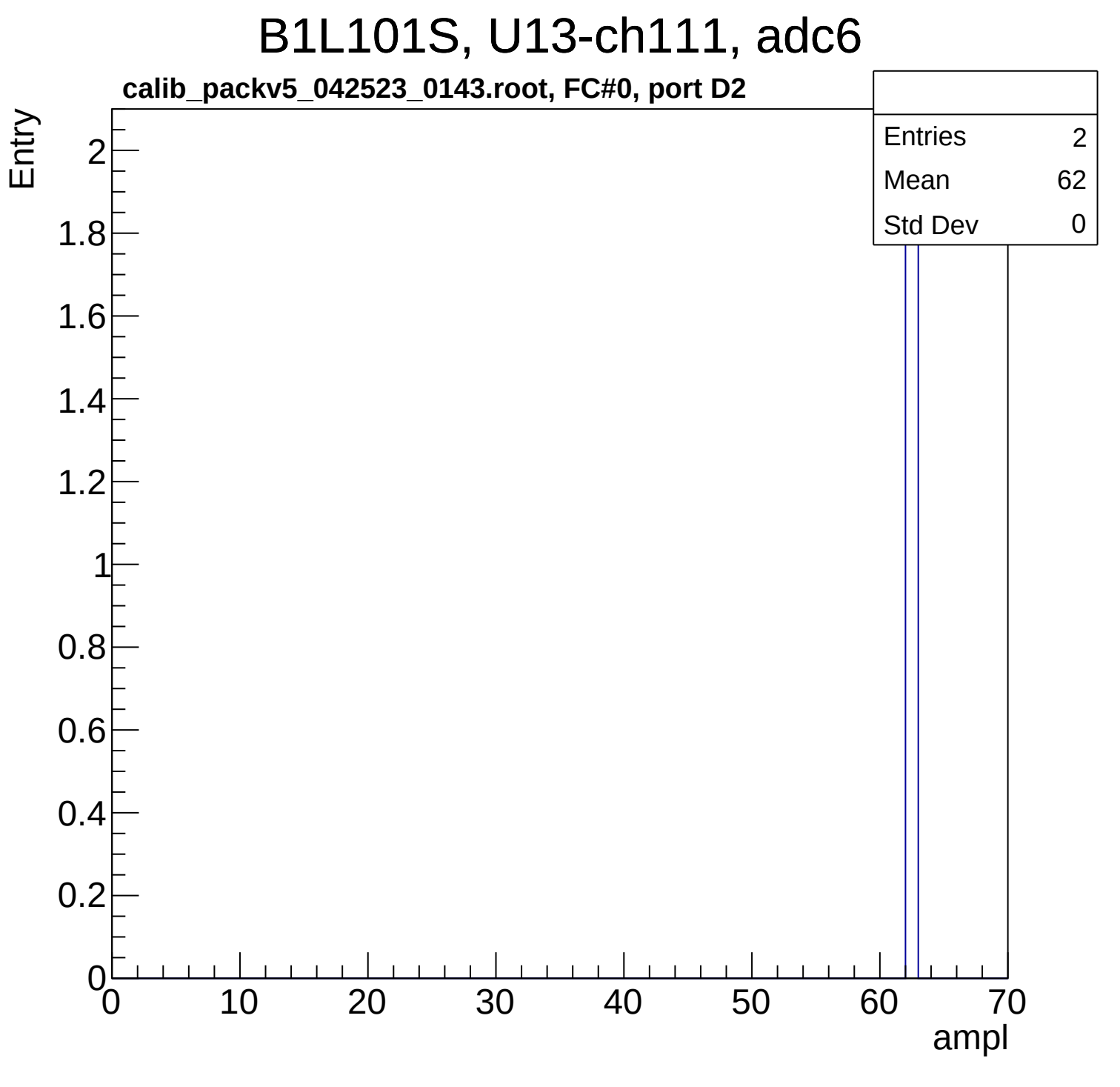
B1L101S, U13-ch111, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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40
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60
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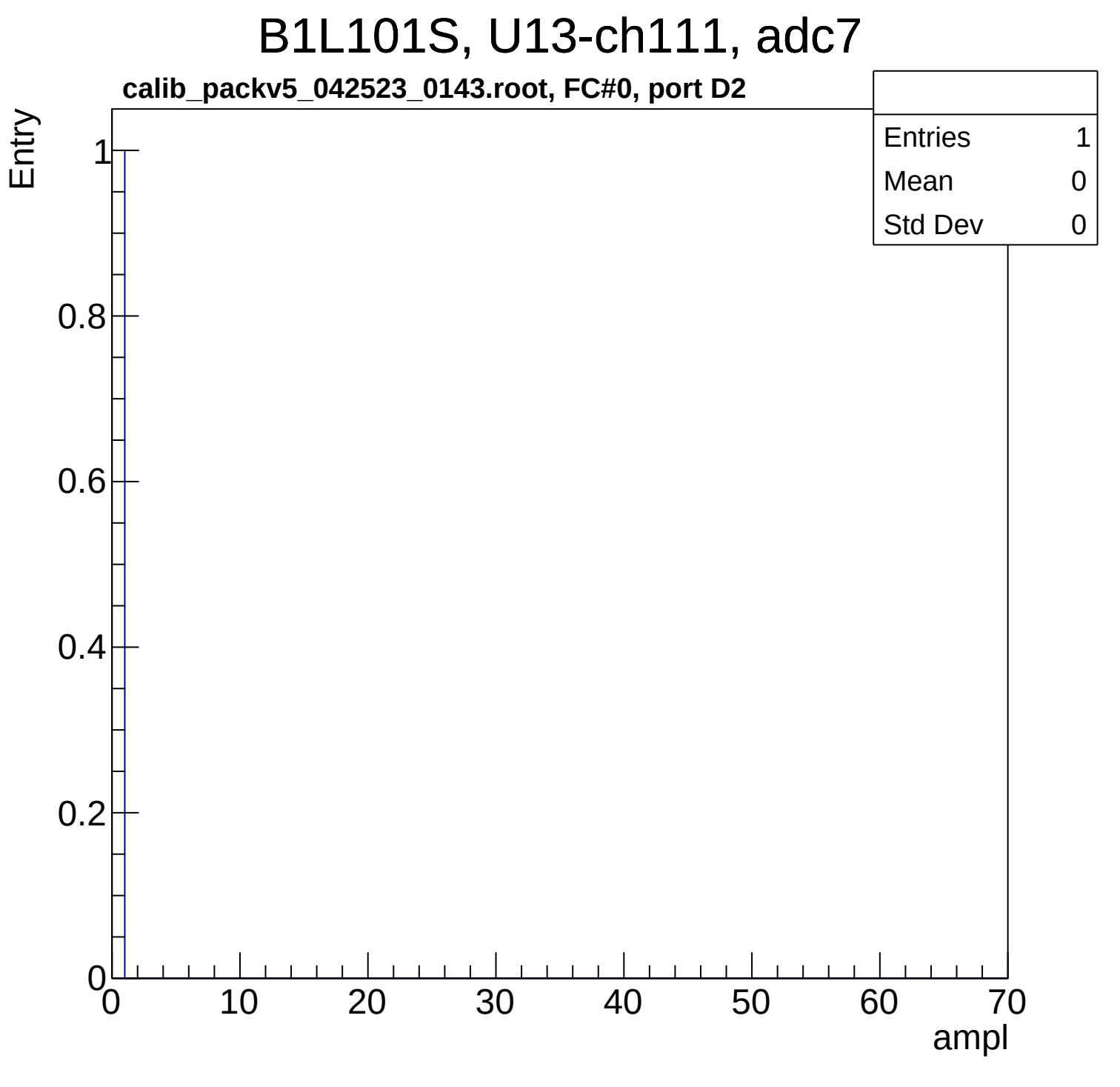
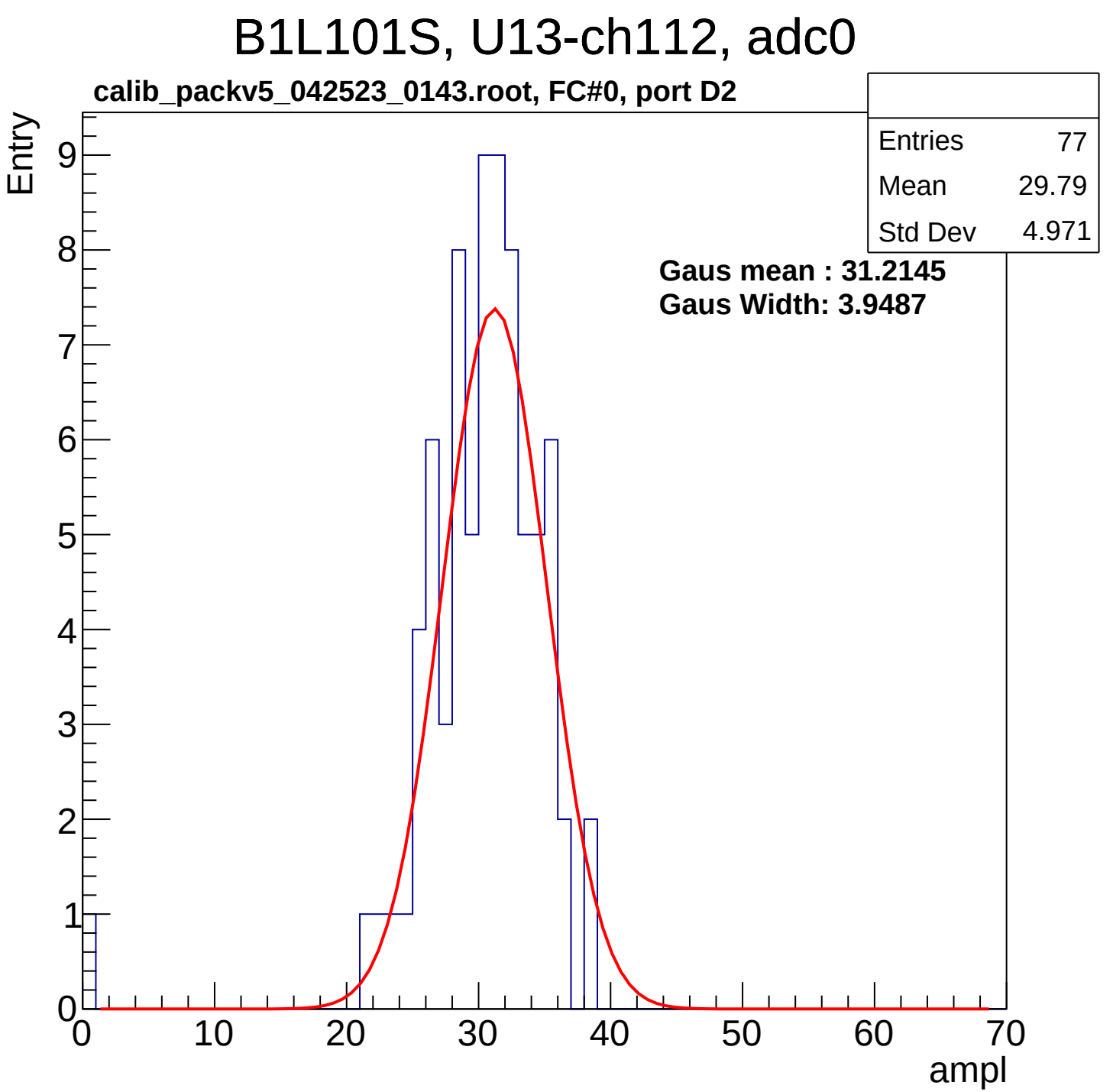

B1L101S, U13-ch111, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch112, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 29.79
Std Dev 4.971
Gaus mean : 31.2145
Gaus Width: 3.9487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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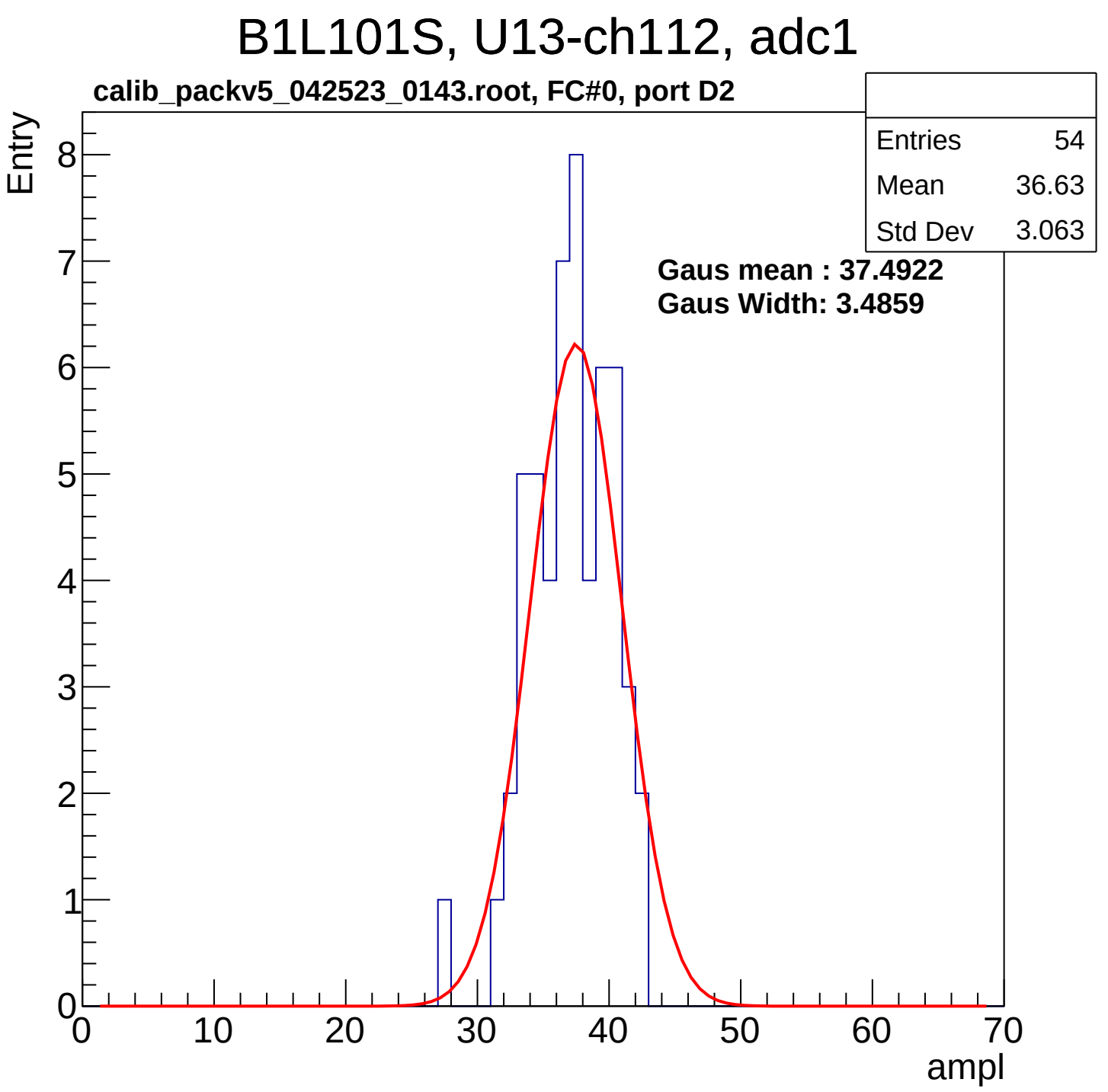
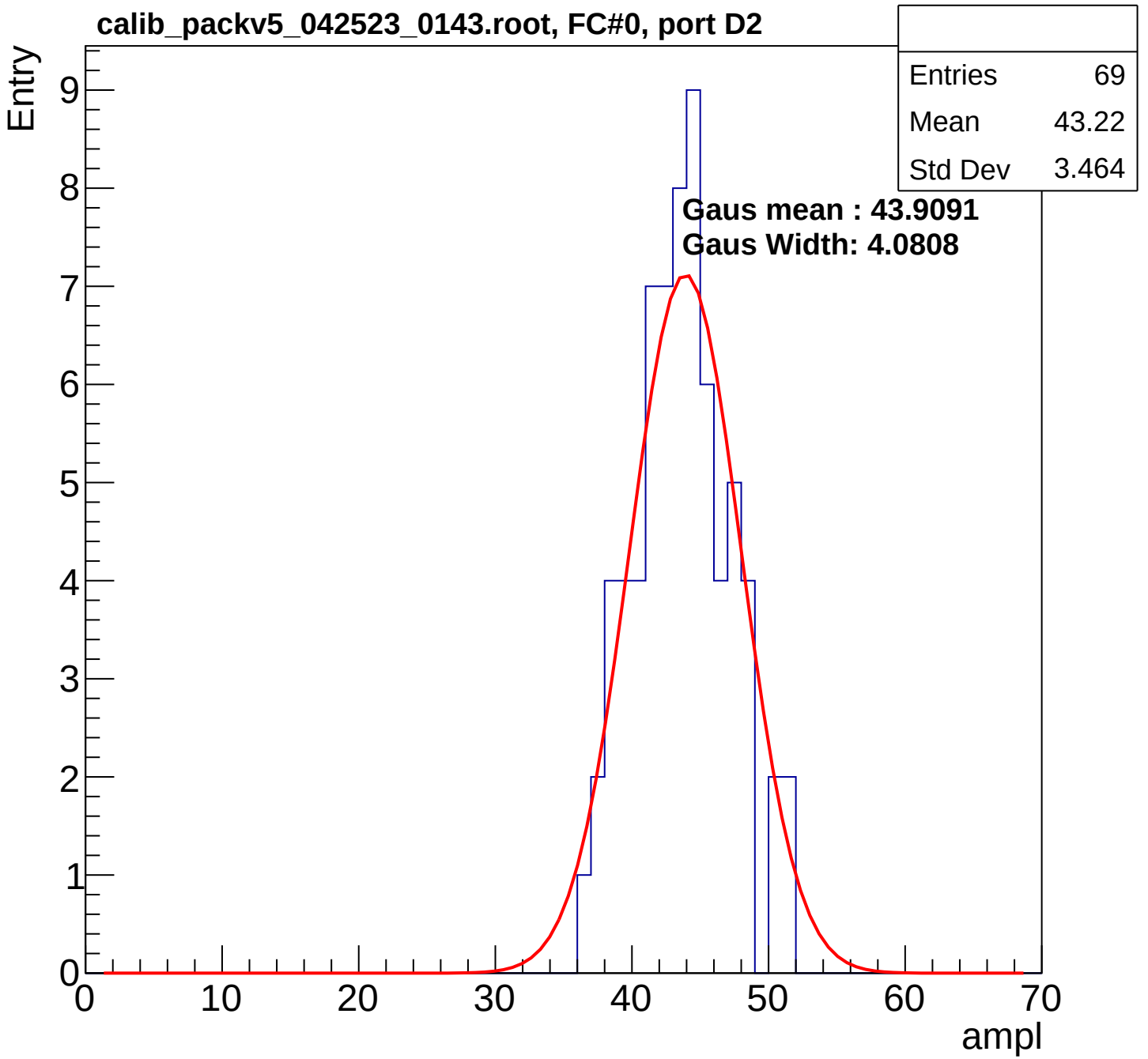
60
70


B1L101S, U13-ch112, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 36.63
Std Dev 3.063
Gaus mean : 37.4922
Gaus Width: 3.4859
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch112, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 43.22
Std Dev 3.464
Gaus mean : 43.9091
Gaus Width: 4.0808
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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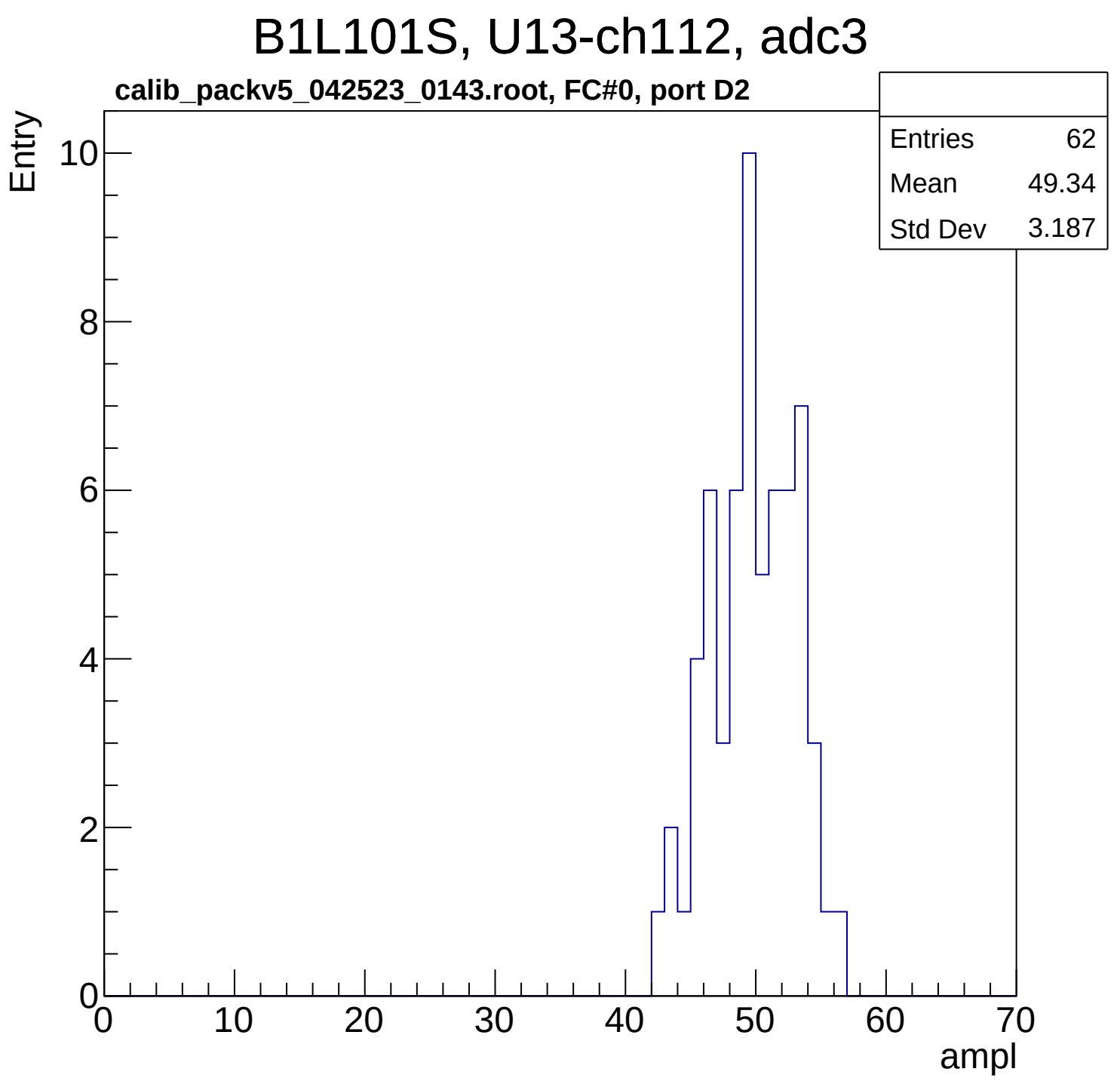
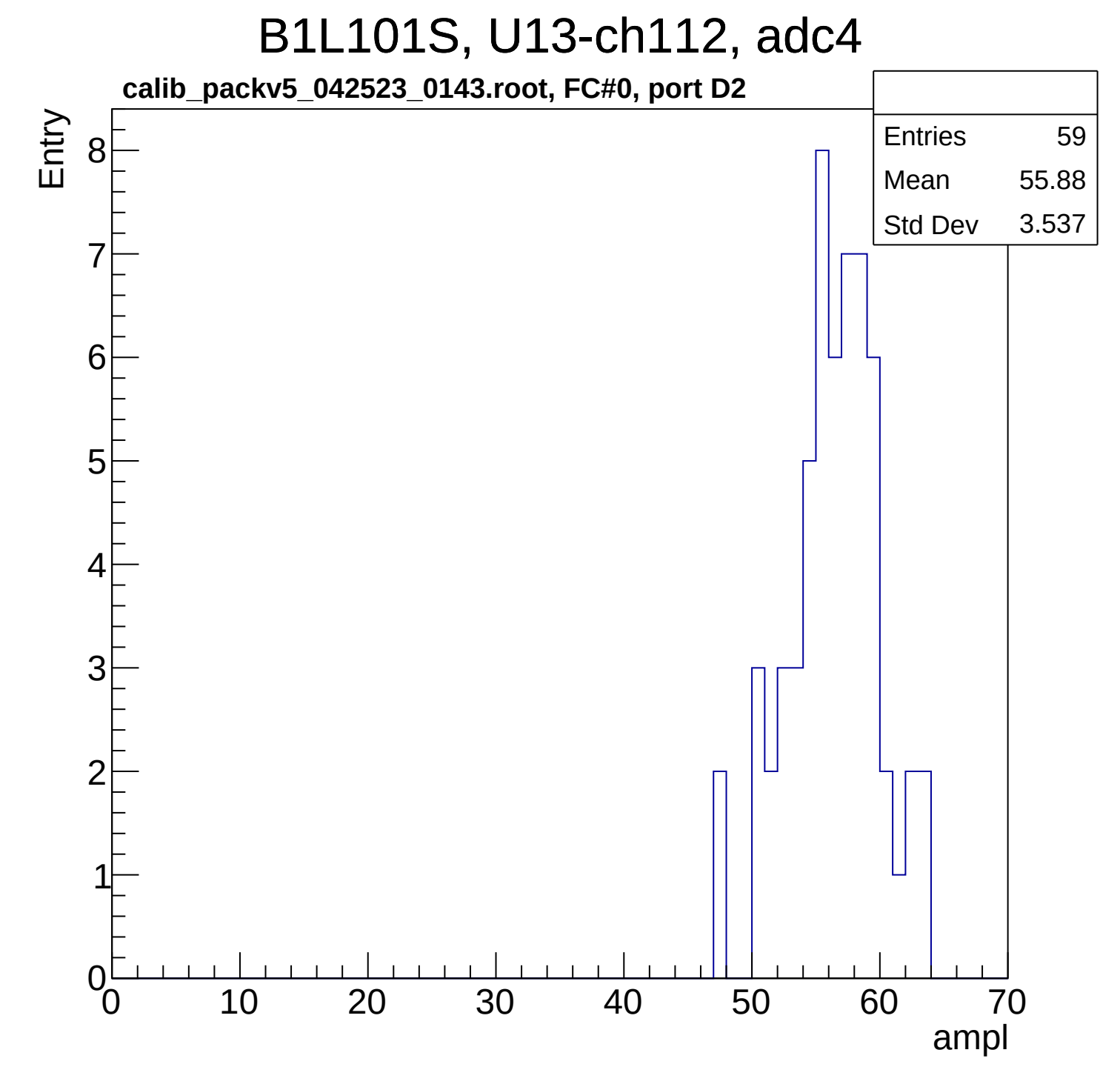
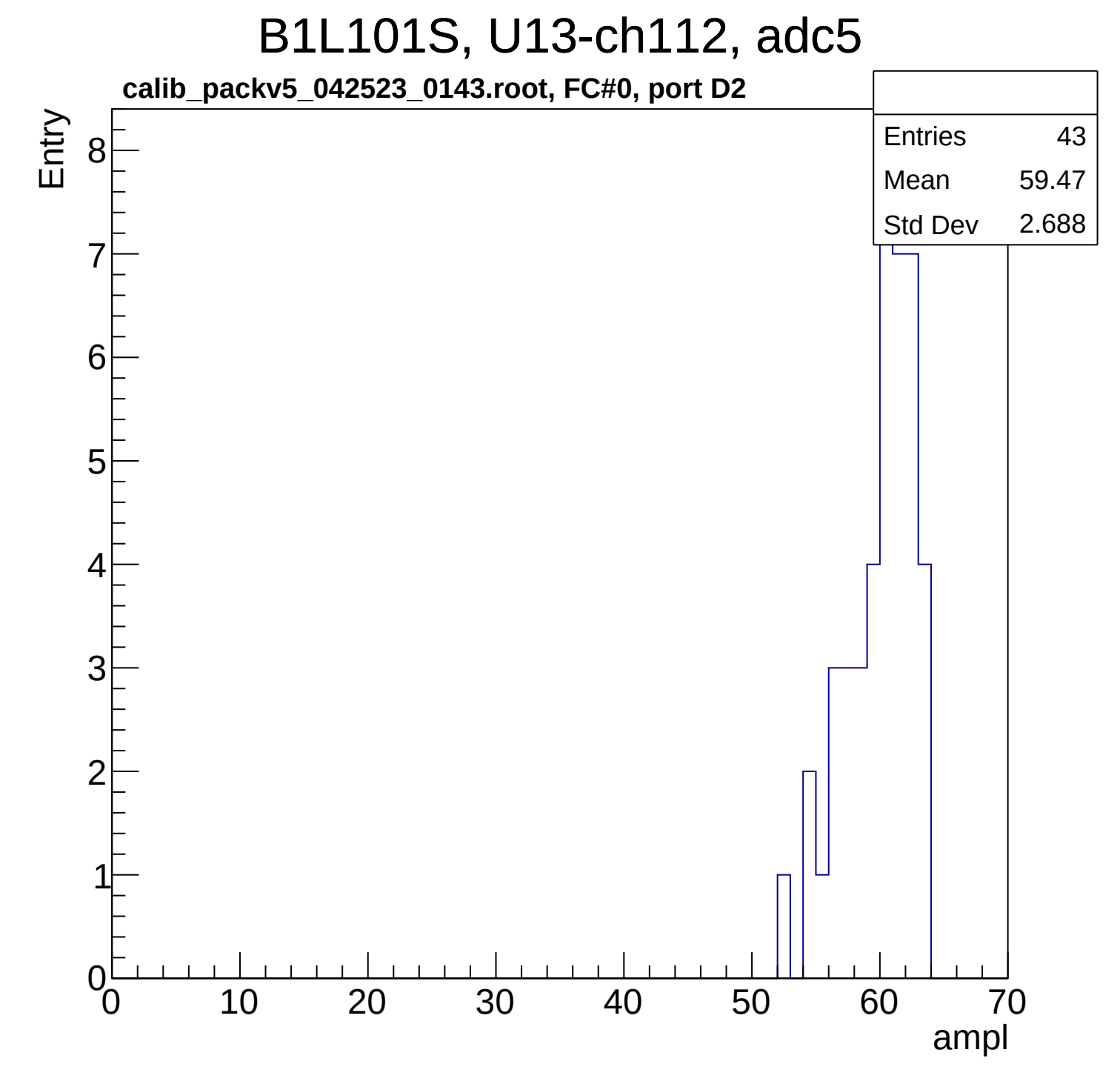

B1L101S, U13-ch112, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 49.34
Std Dev 3.187
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch112, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 55.88
Std Dev 3.537
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch112, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 43
Mean 59.47
Std Dev 2.688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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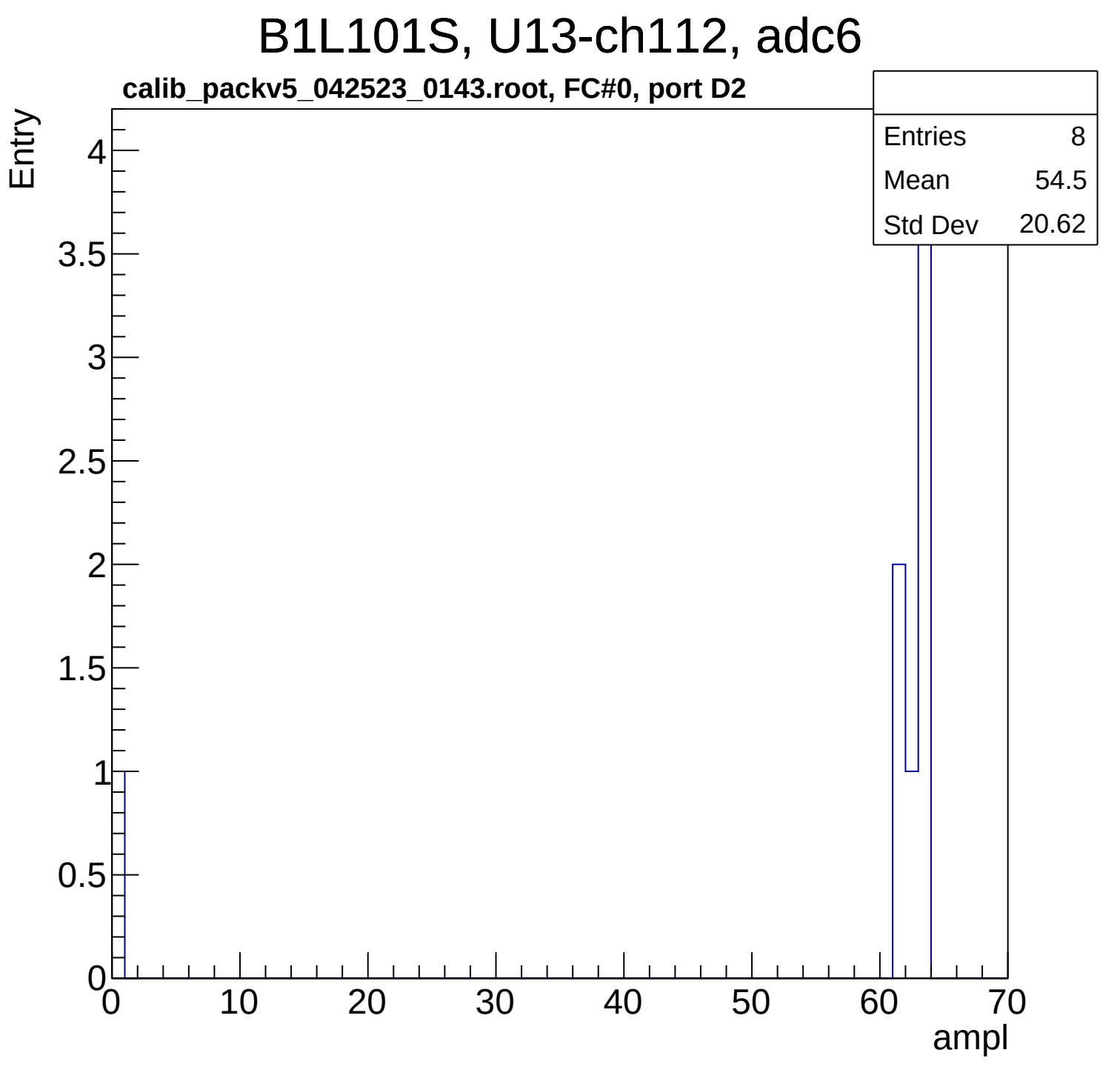
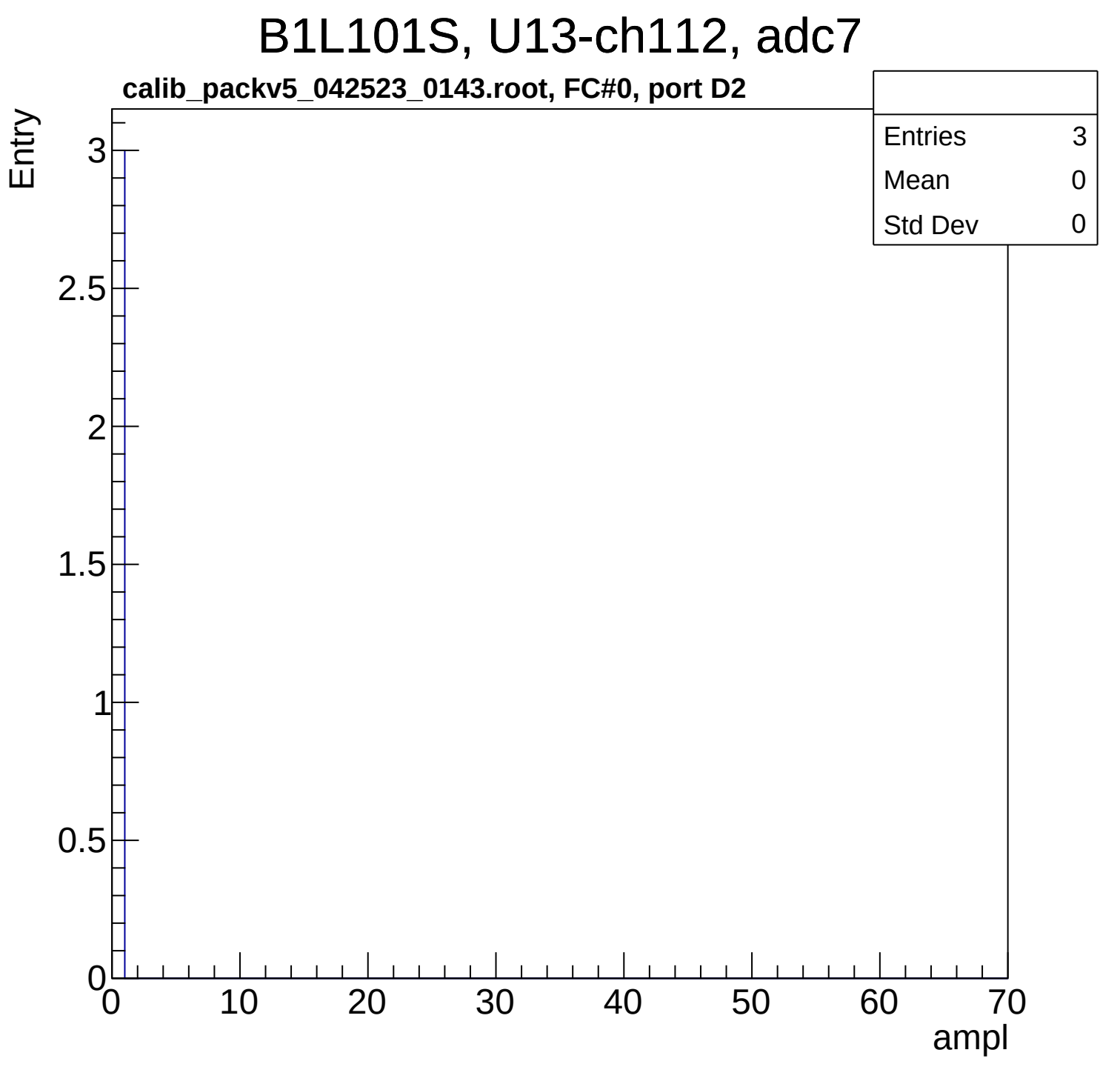
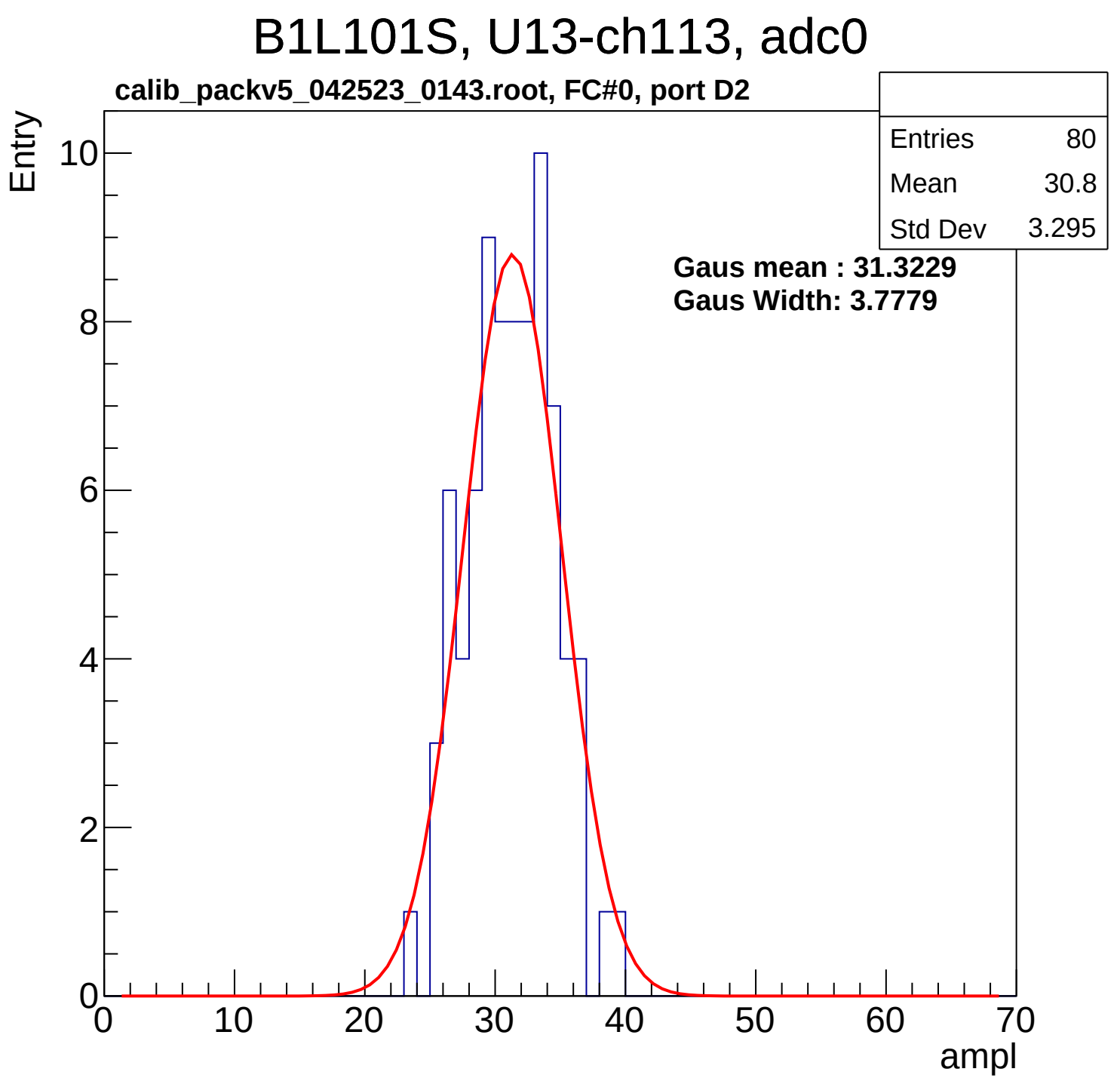
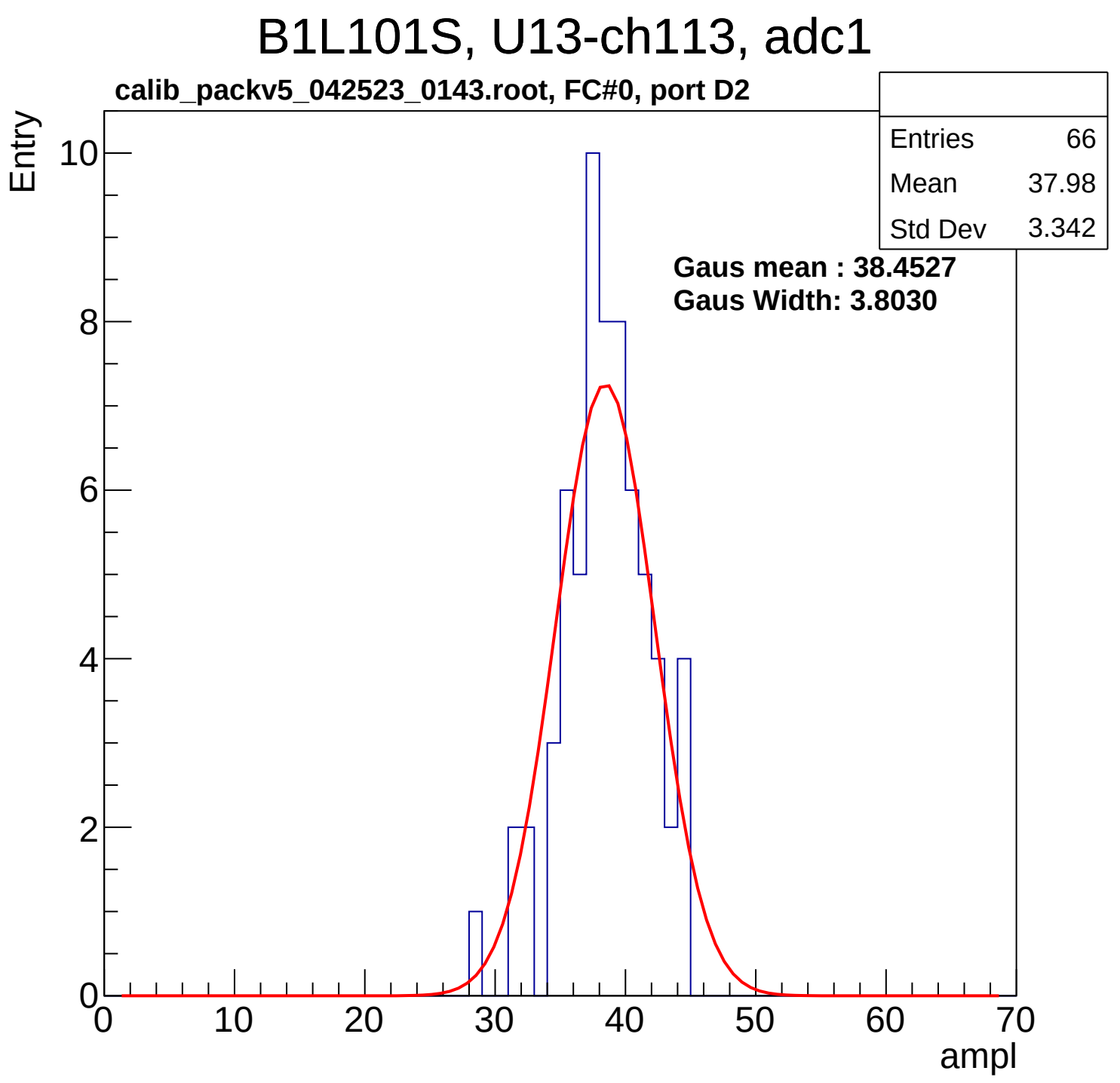
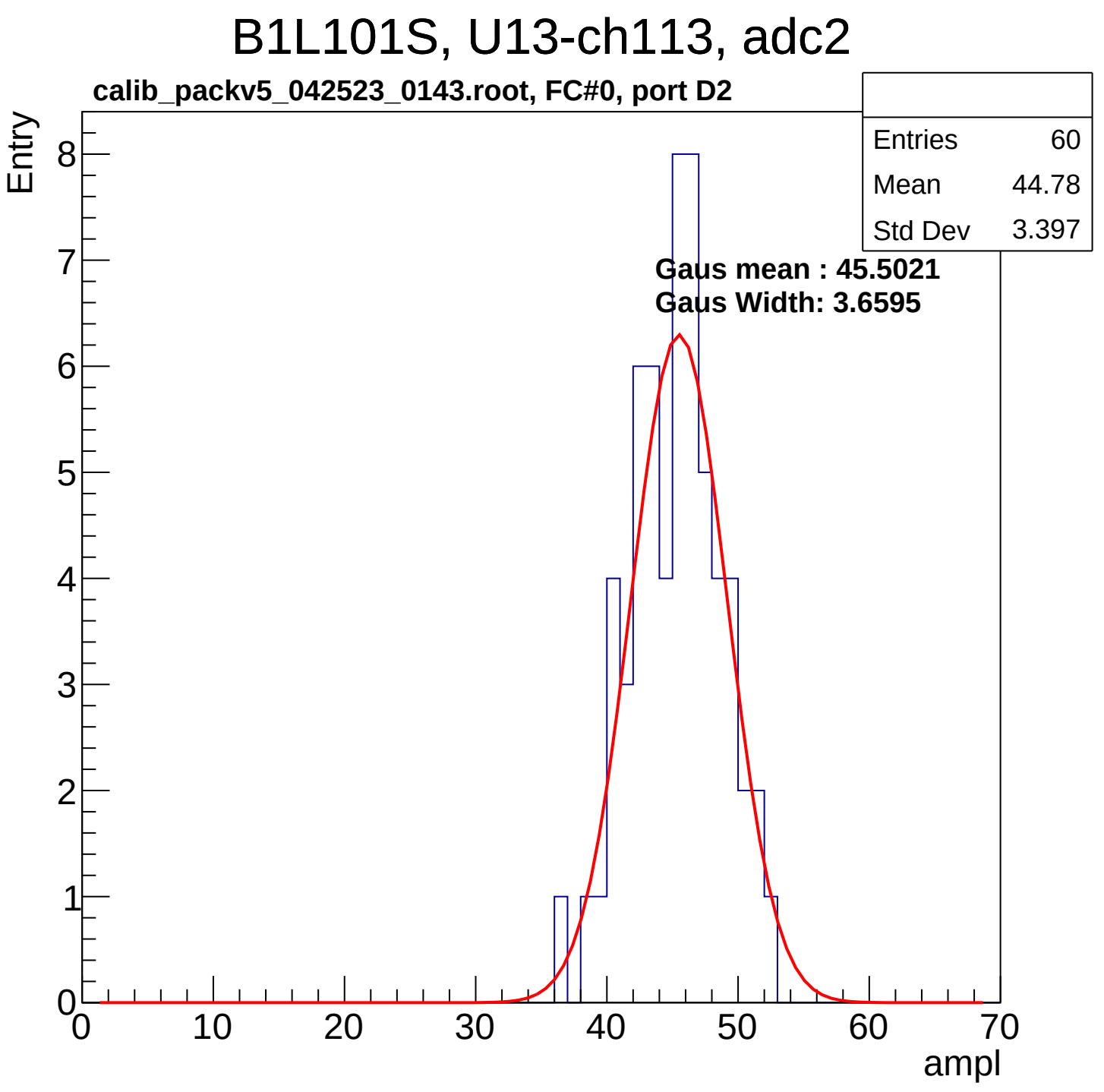
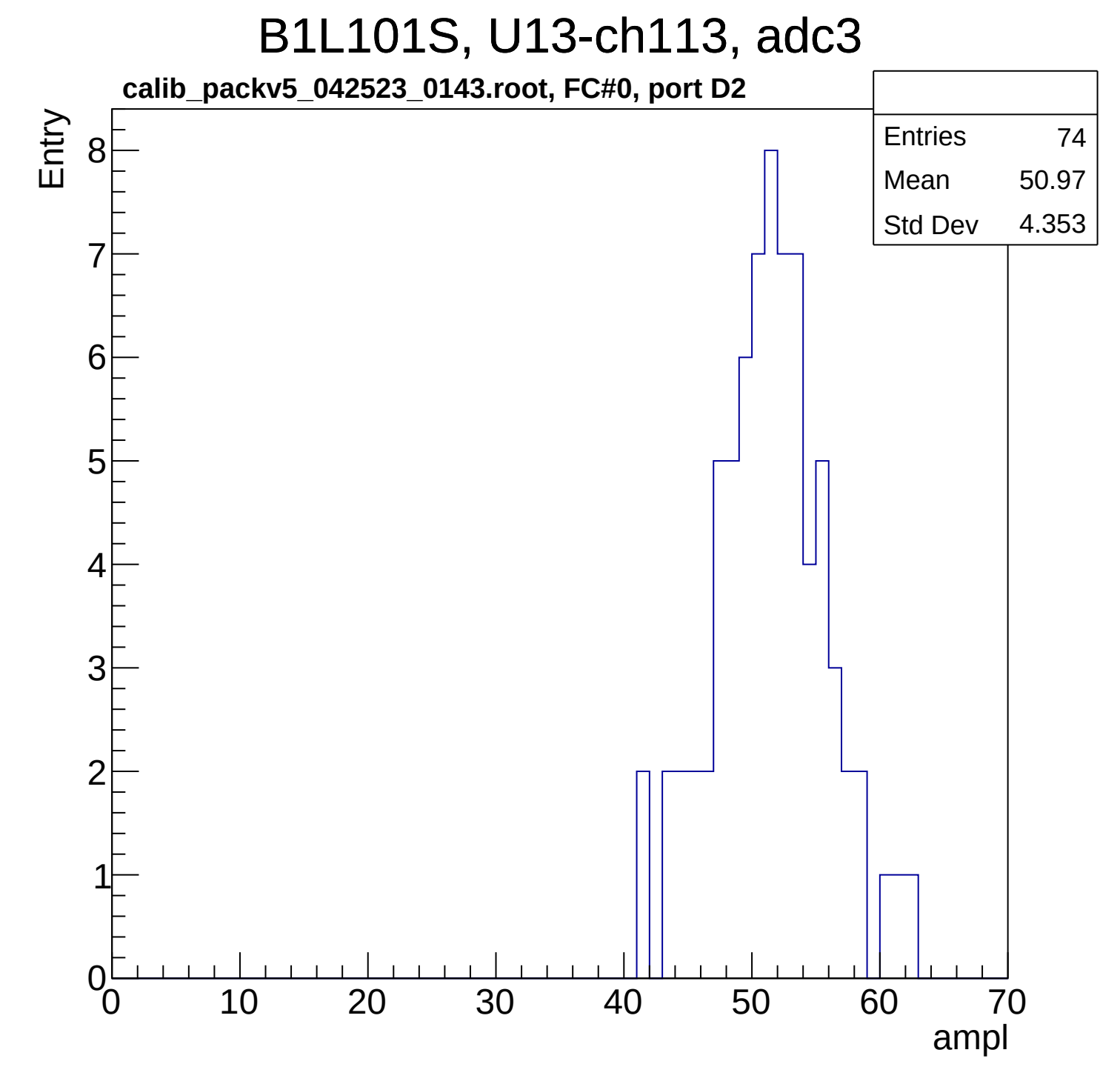

B1L101S, U13-ch112, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 8
Mean 54.5
Std Dev 20.62
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

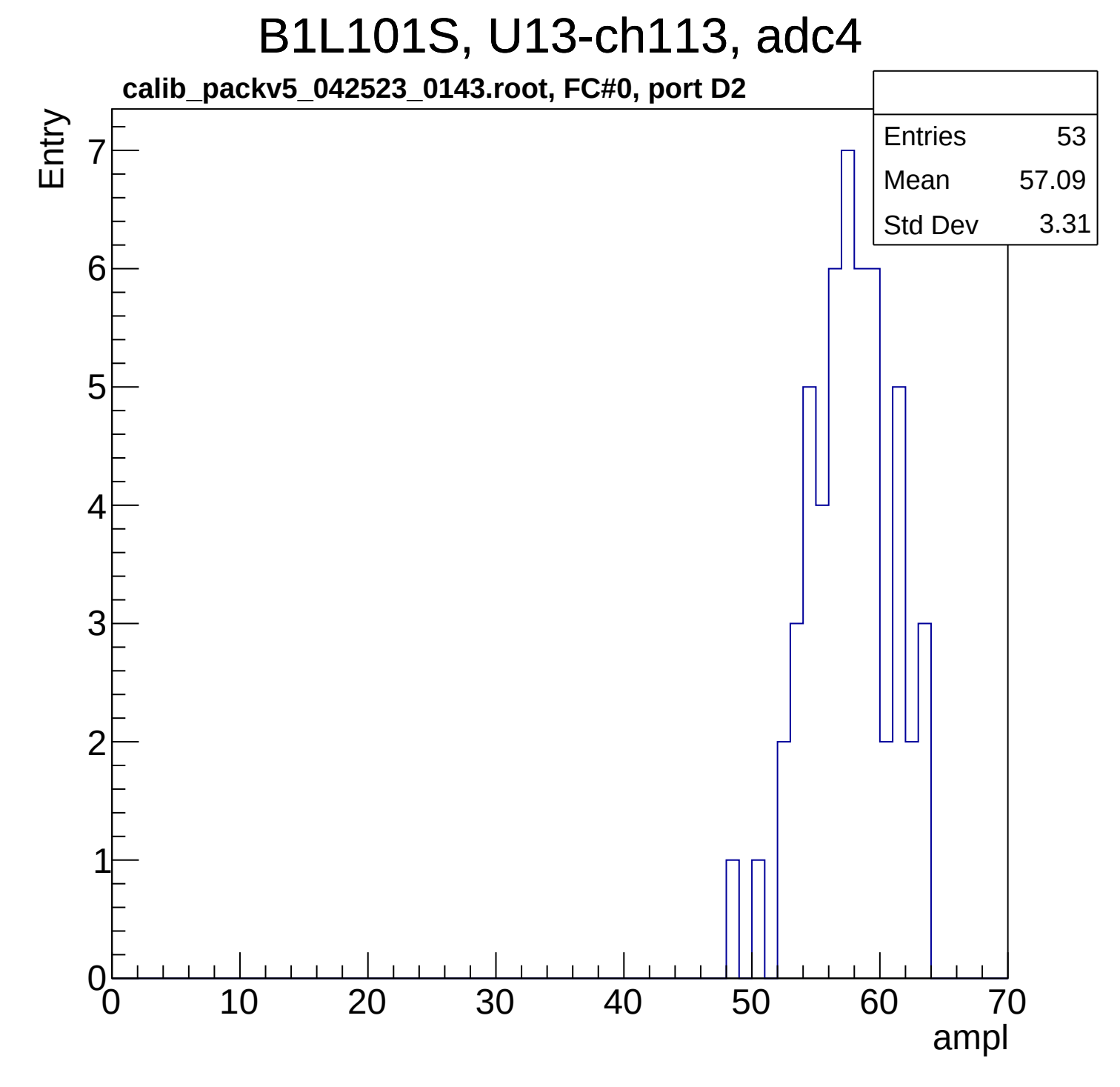

B1L101S, U13-ch112, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

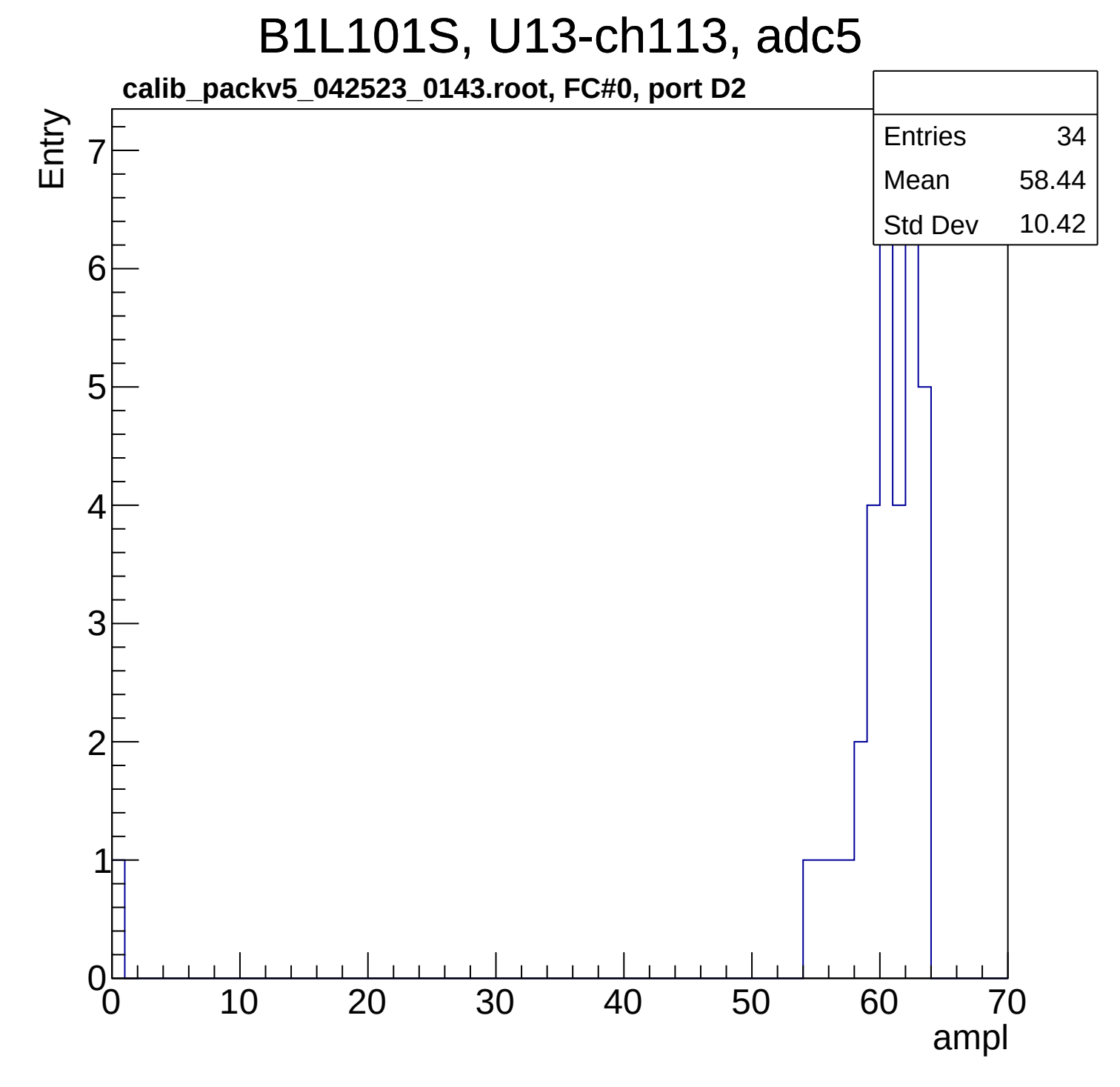

B1L101S, U13-ch113, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 80
Mean 30.8
Std Dev 3.295
Gaus mean : 31.3229
Gaus Width: 3.7779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

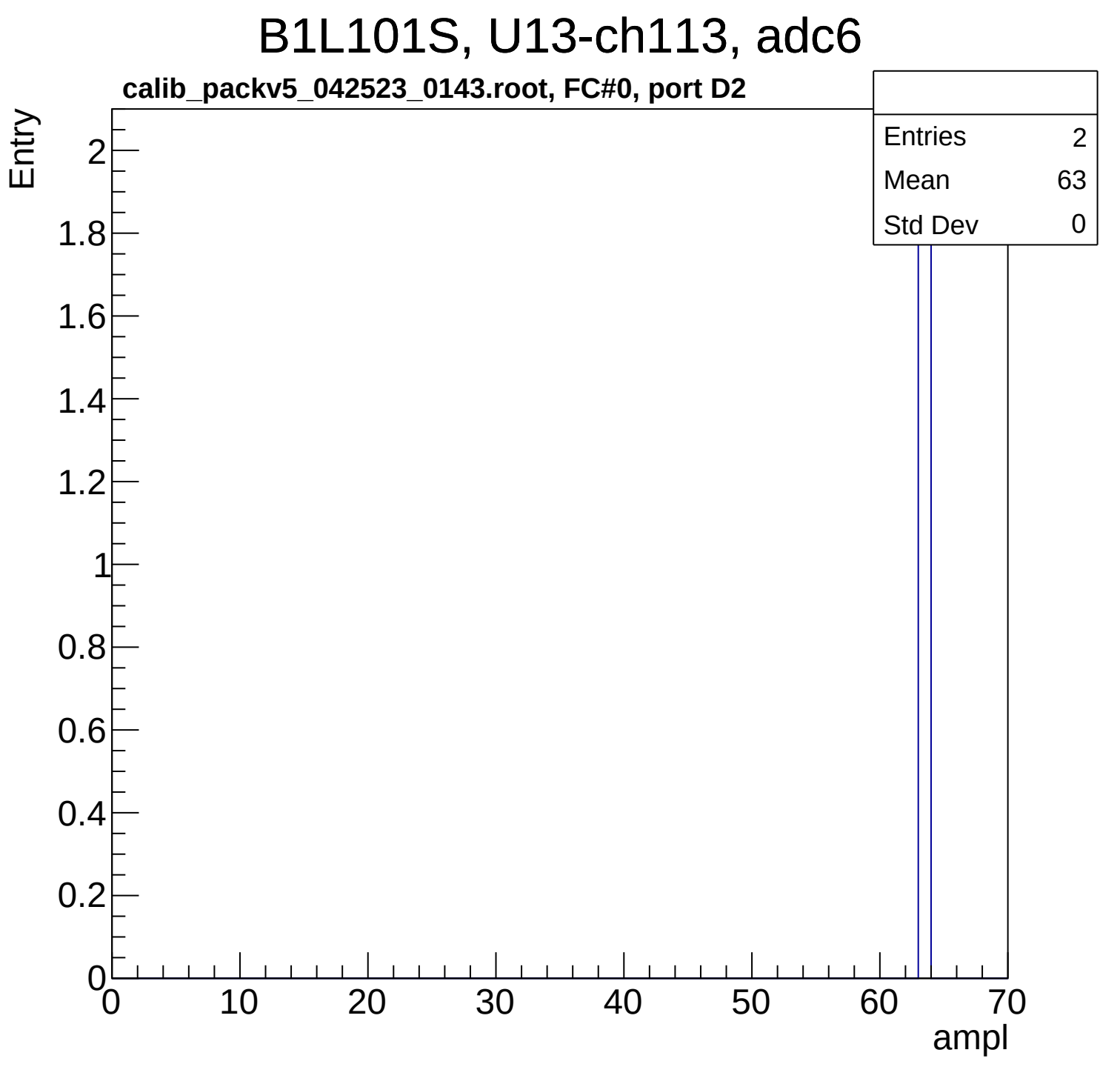

B1L101S, U13-ch113, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 66
Mean 37.98
Std Dev 3.342
Gaus mean : 38.4527
Gaus Width: 3.8030
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

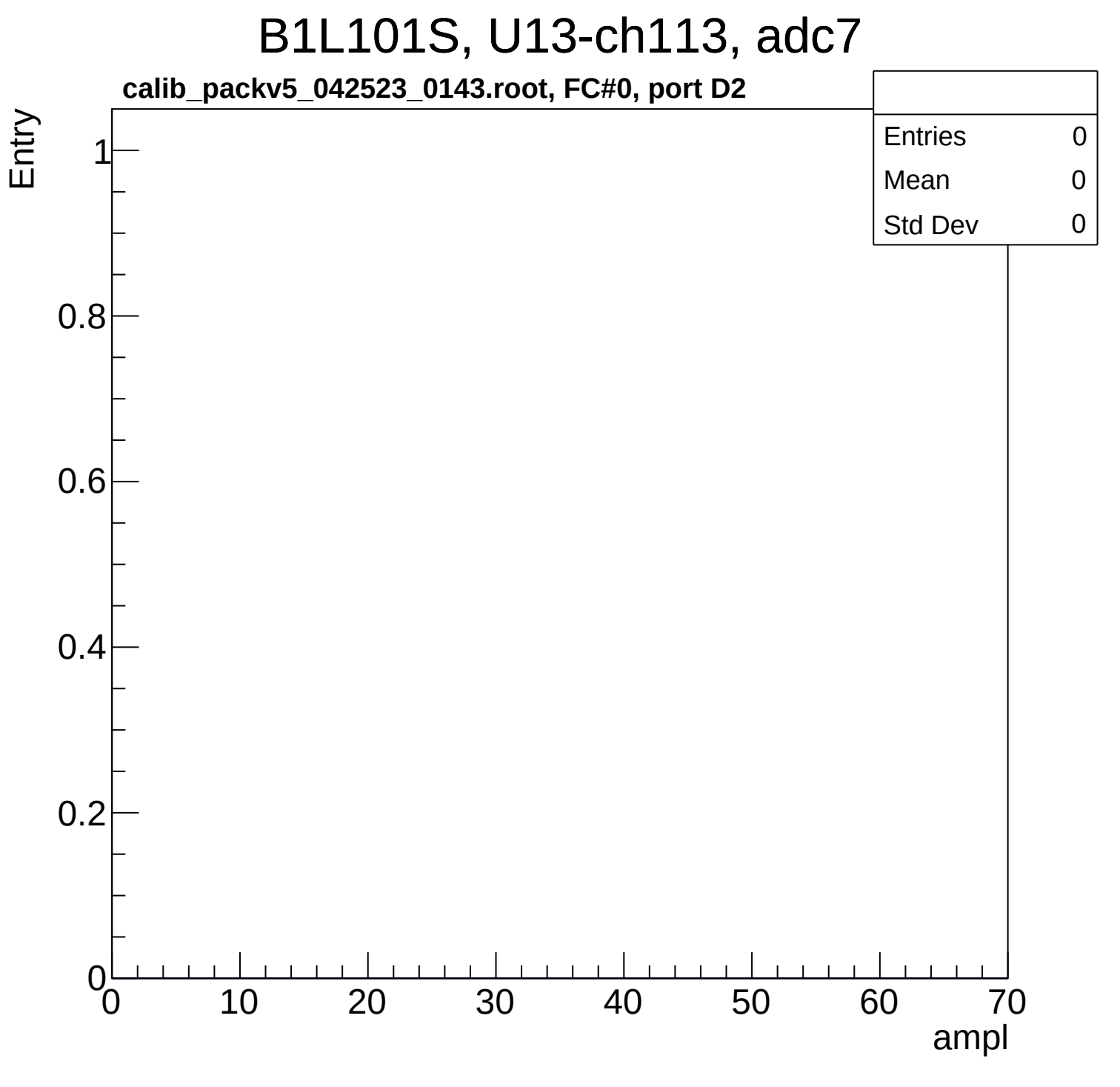

B1L101S, U13-ch113, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 44.78
Std Dev 3.397
Gaus mean : 45.5021
Gaus Width: 3.6595
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch113, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 50.97
Std Dev 4.353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch113, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 57.09
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch113, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 34
Mean 58.44
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch113, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch113, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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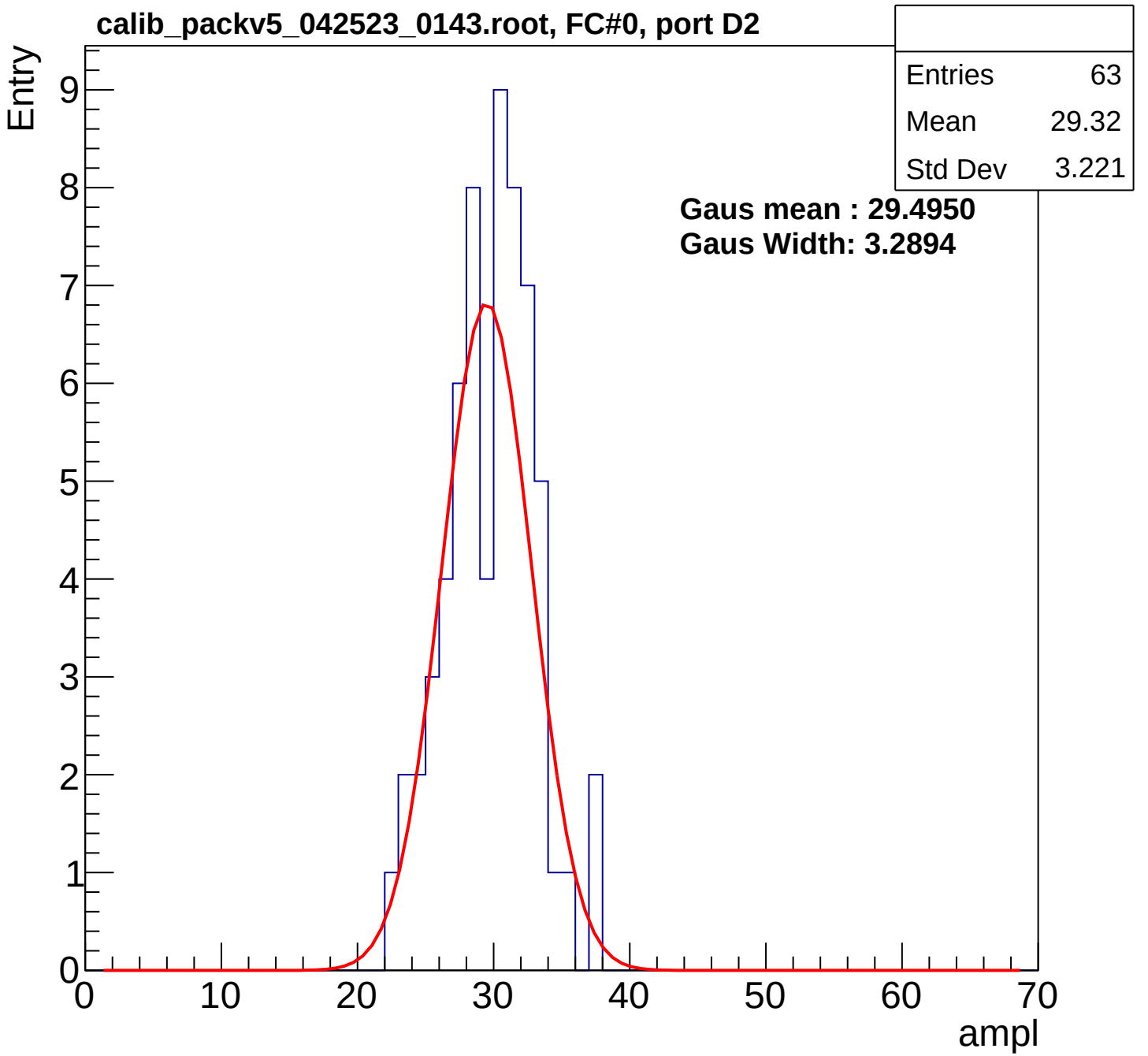
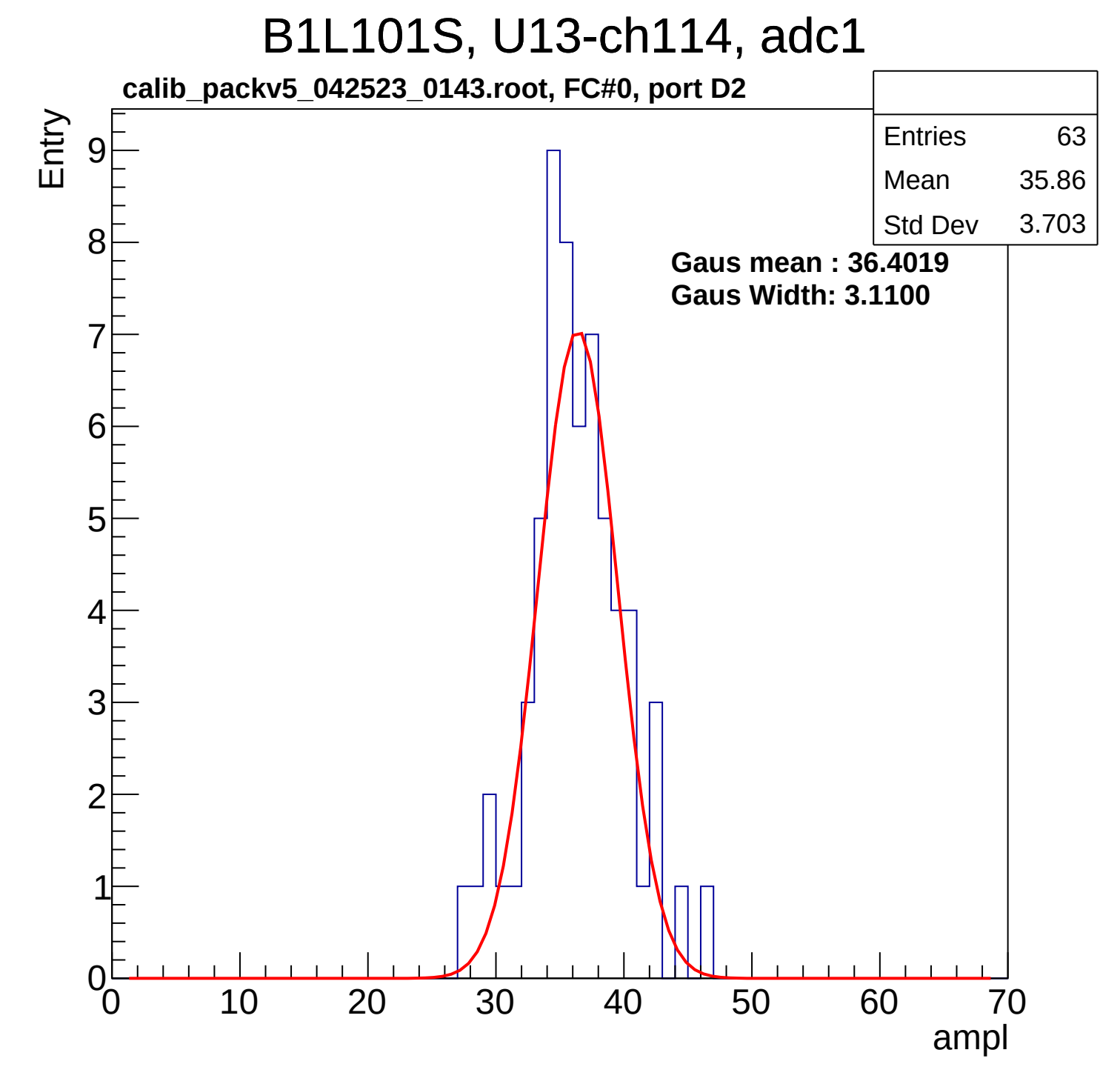
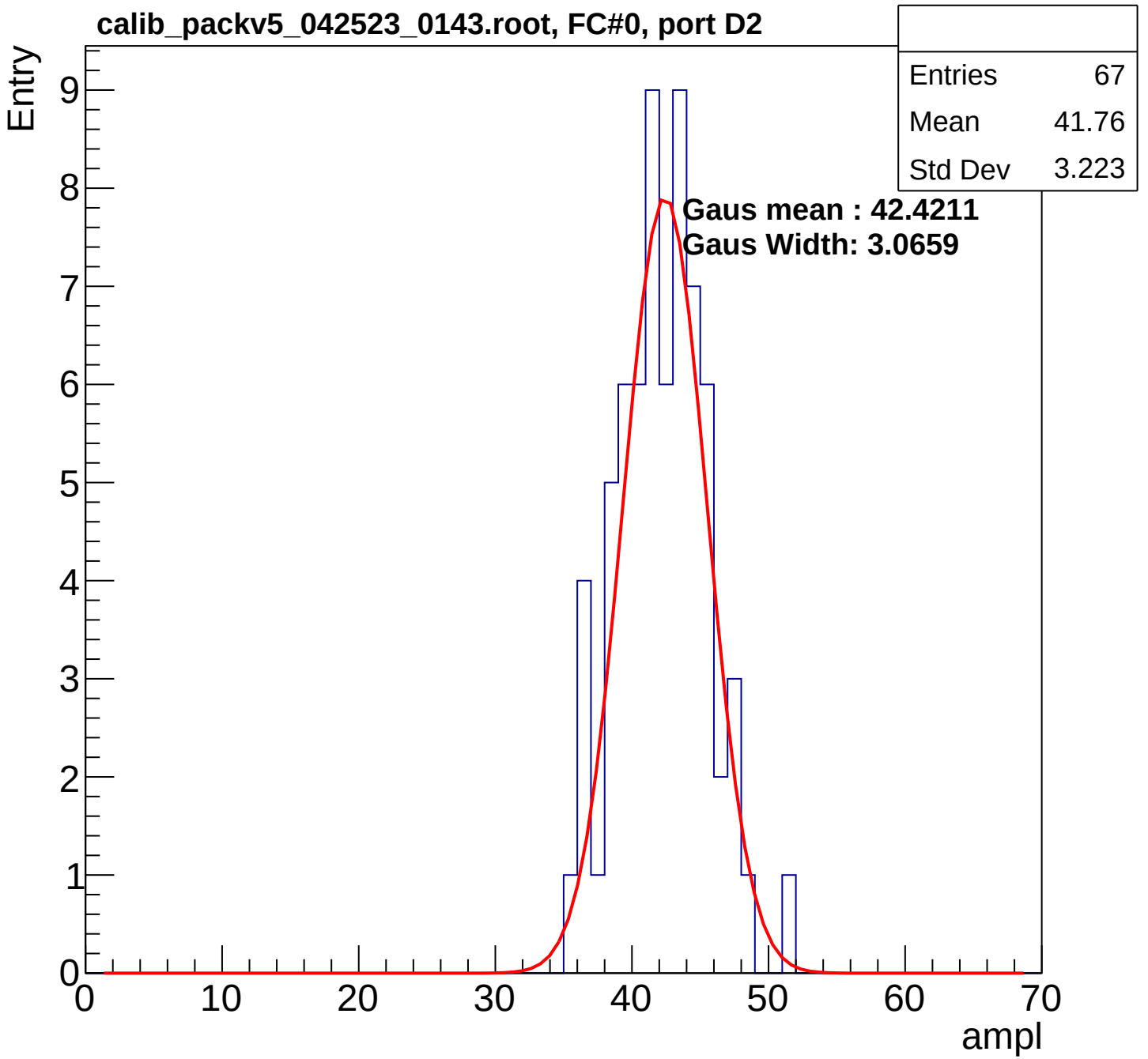
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch114, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 29.32
Std Dev 3.221
Gaus mean : 29.4950
Gaus Width: 3.2894
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L101S, U13-ch114, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 35.86
Std Dev 3.703
Gaus mean : 36.4019
Gaus Width: 3.1100
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch114, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 41.76
Std Dev 3.223
Gaus mean : 42.4211
Gaus Width: 3.0659
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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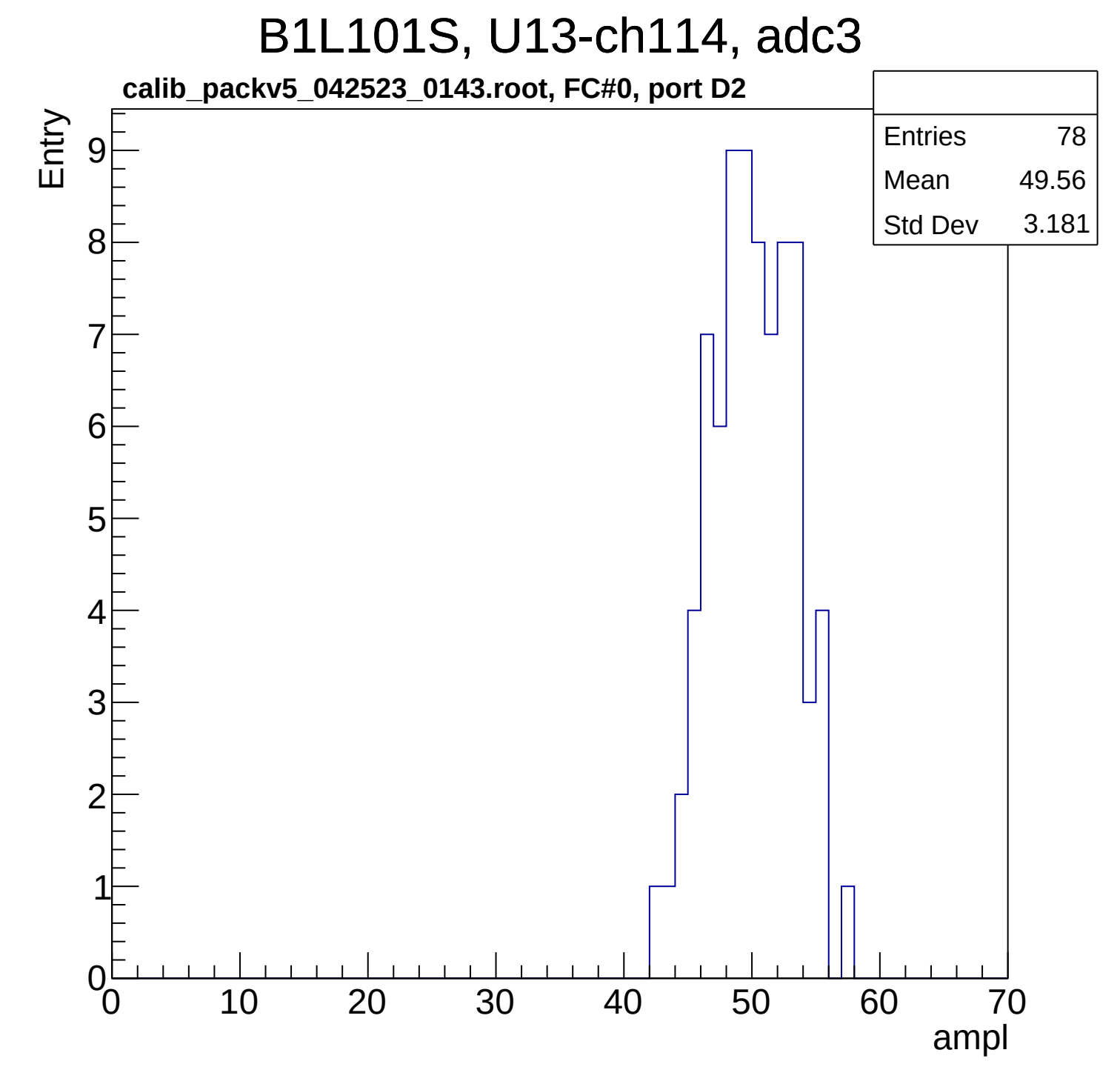
B1L101S, U13-ch114, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 49.56
Std Dev 3.181
Entry
ampl
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6
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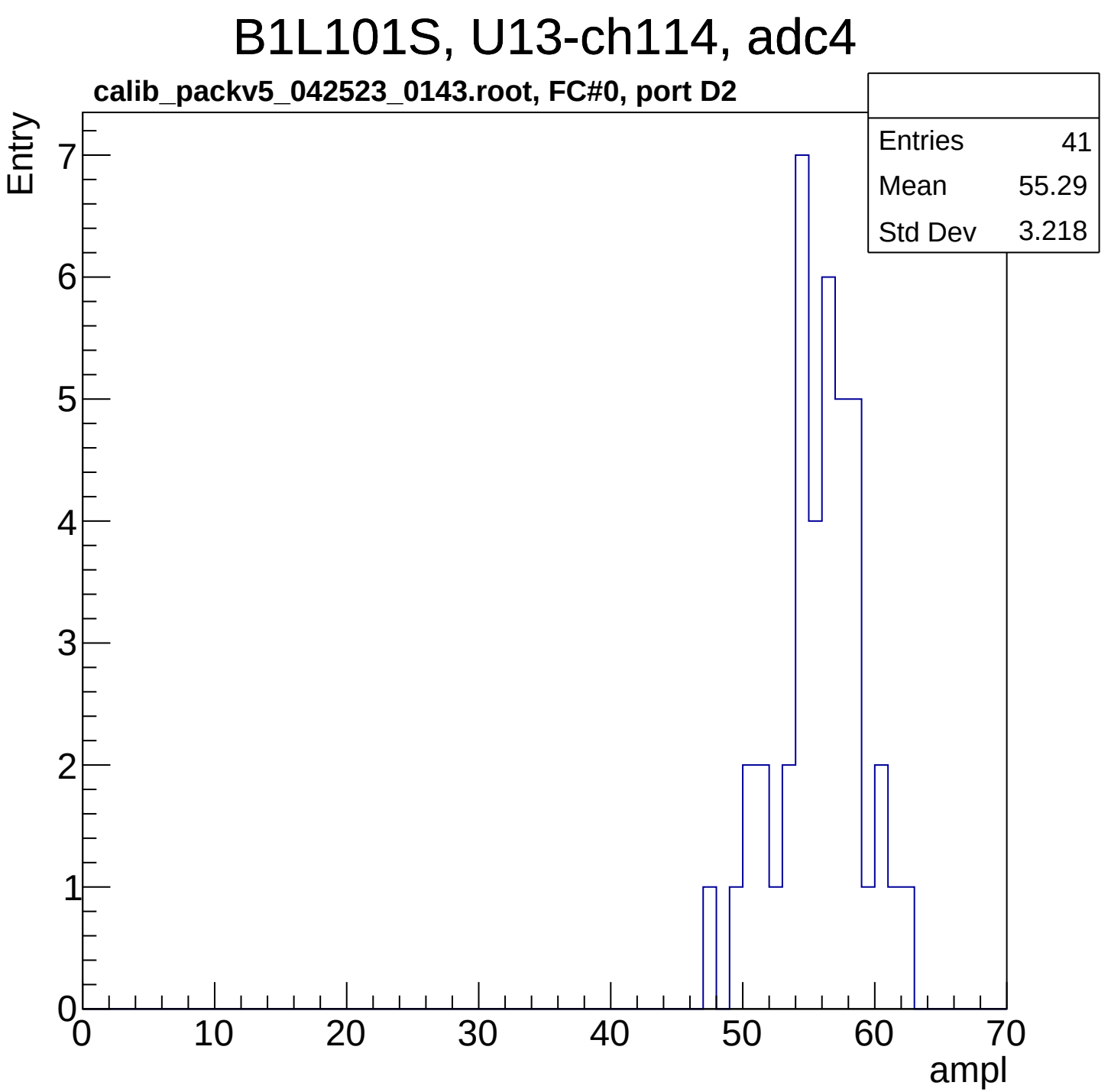
B1L101S, U13-ch114, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 41
Mean 55.29
Std Dev 3.218
Entry
ampl
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1
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3
4
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6
7
0
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60
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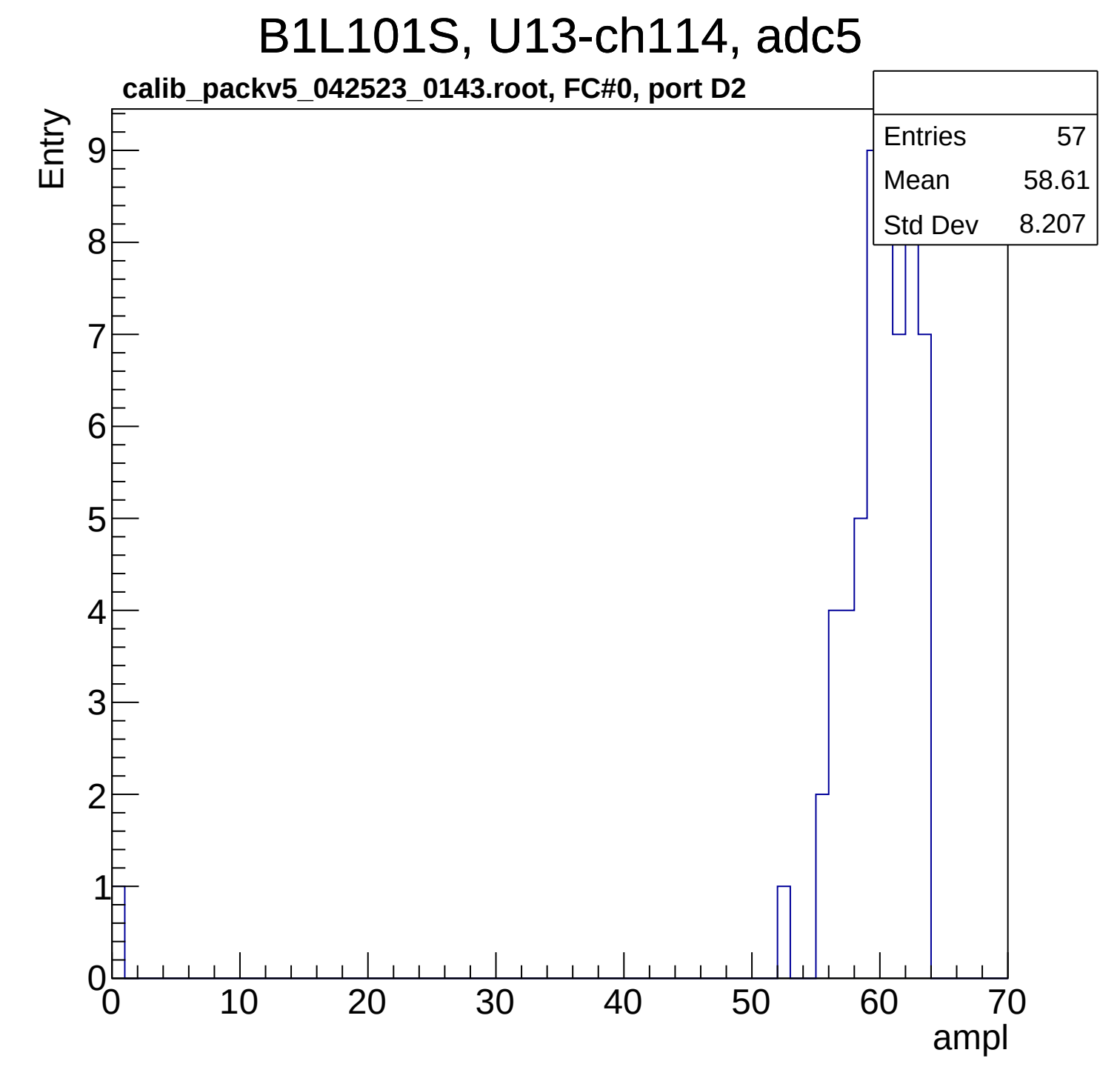
B1L101S, U13-ch114, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 58.61
Std Dev 8.207
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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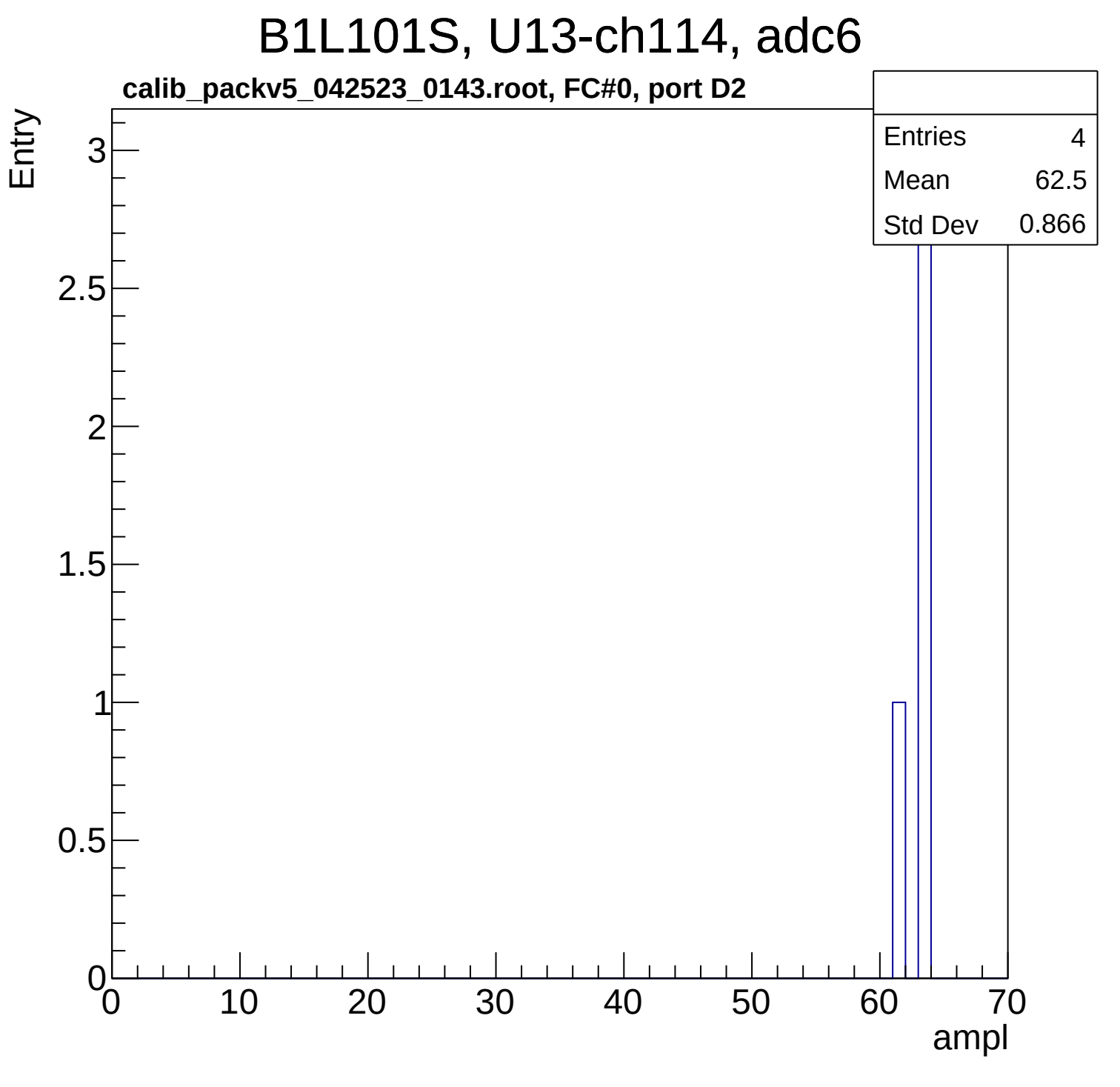
B1L101S, U13-ch114, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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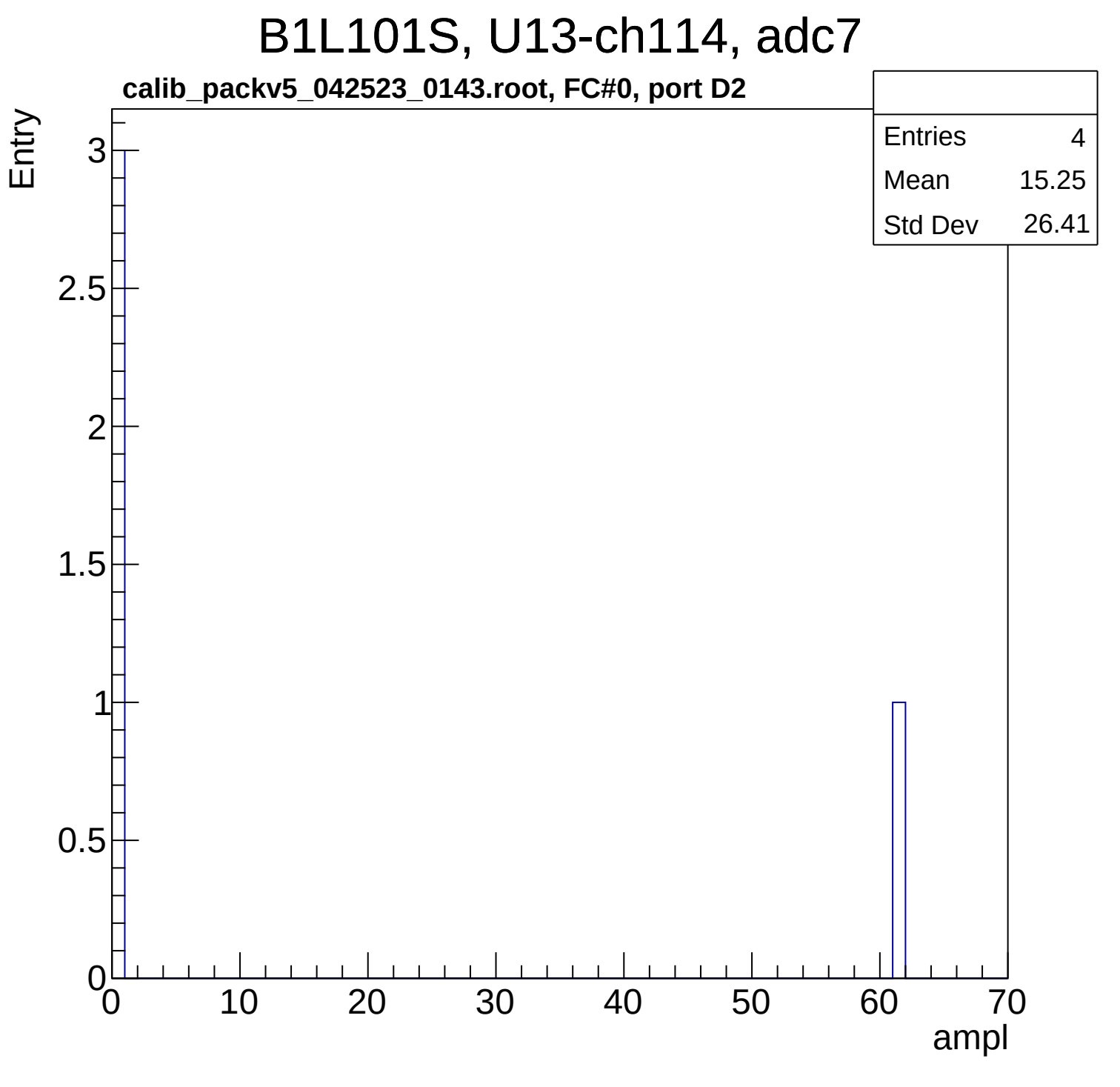
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch114, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 4
Mean 15.25
Std Dev 26.41
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
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60
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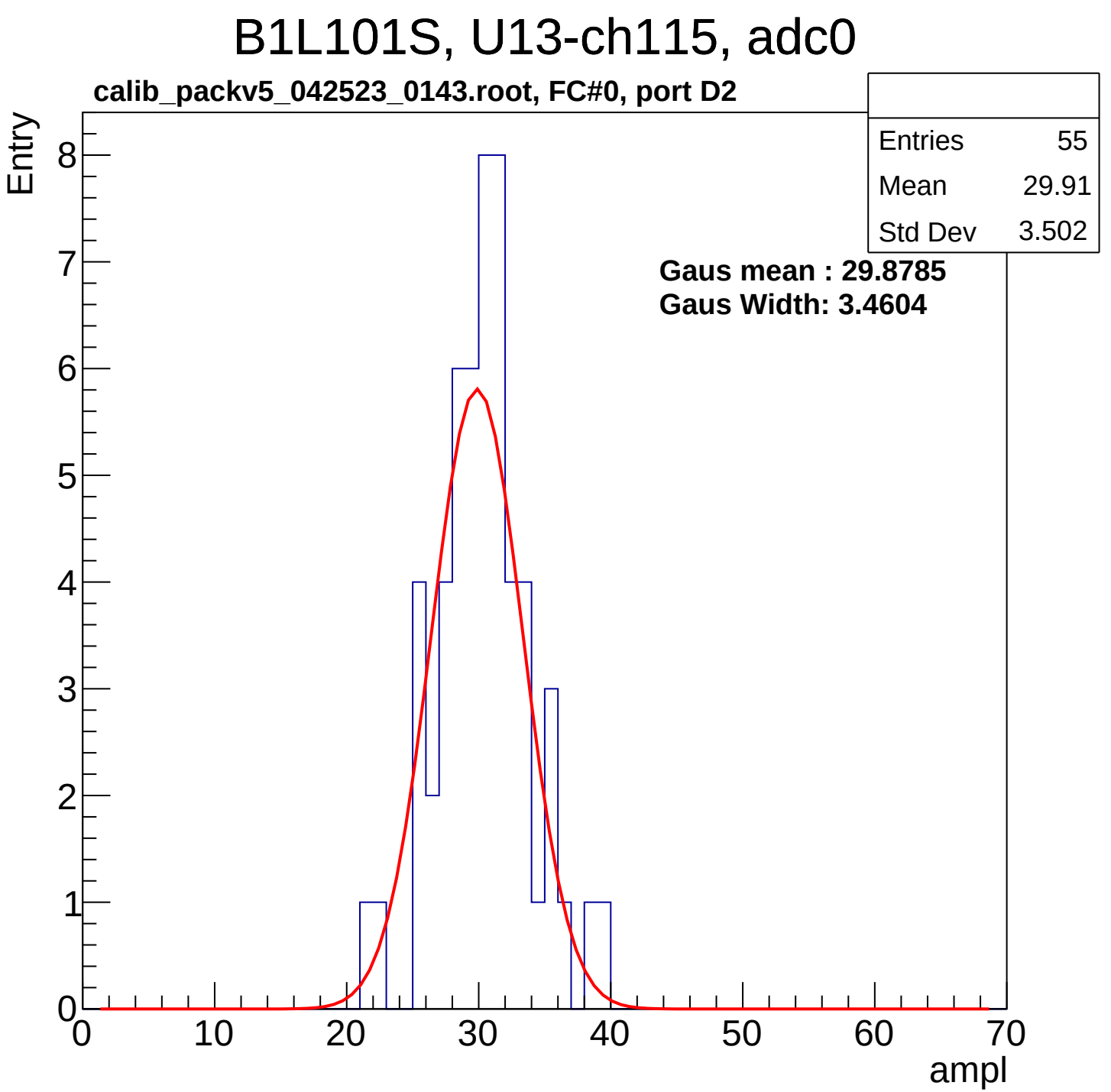
B1L101S, U13-ch115, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 29.91
Std Dev 3.502
Gaus mean : 29.8785
Gaus Width: 3.4604
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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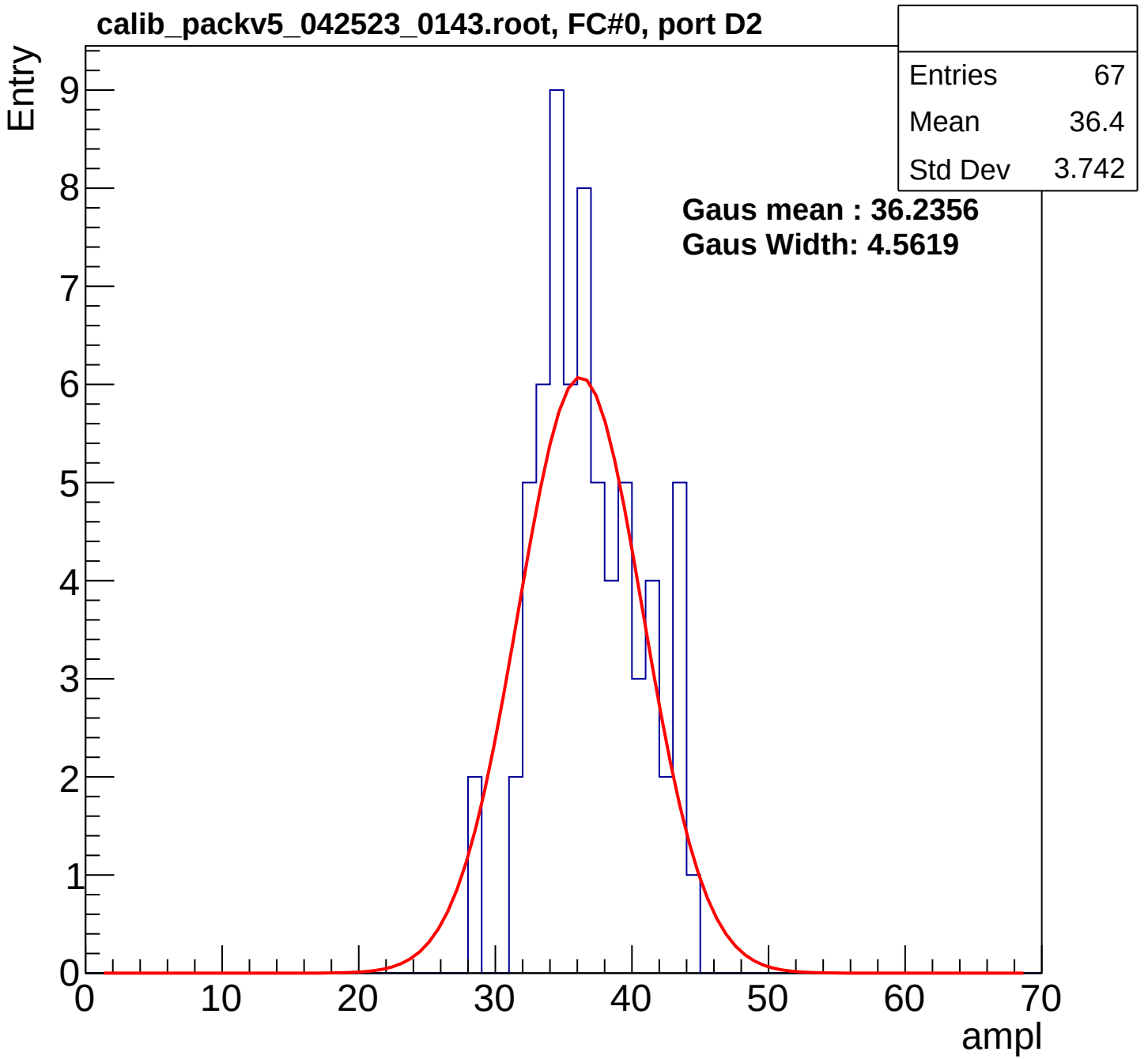
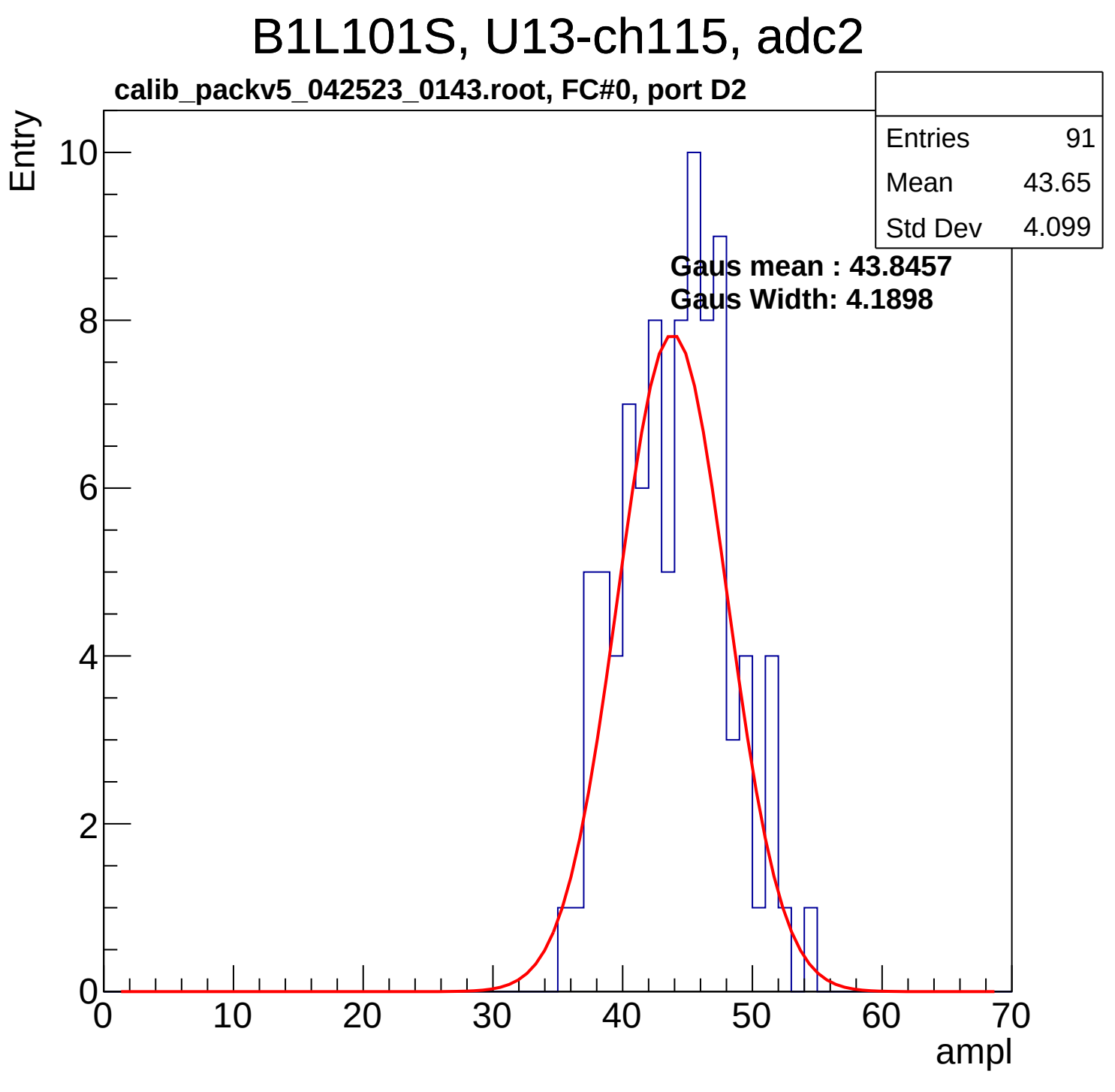
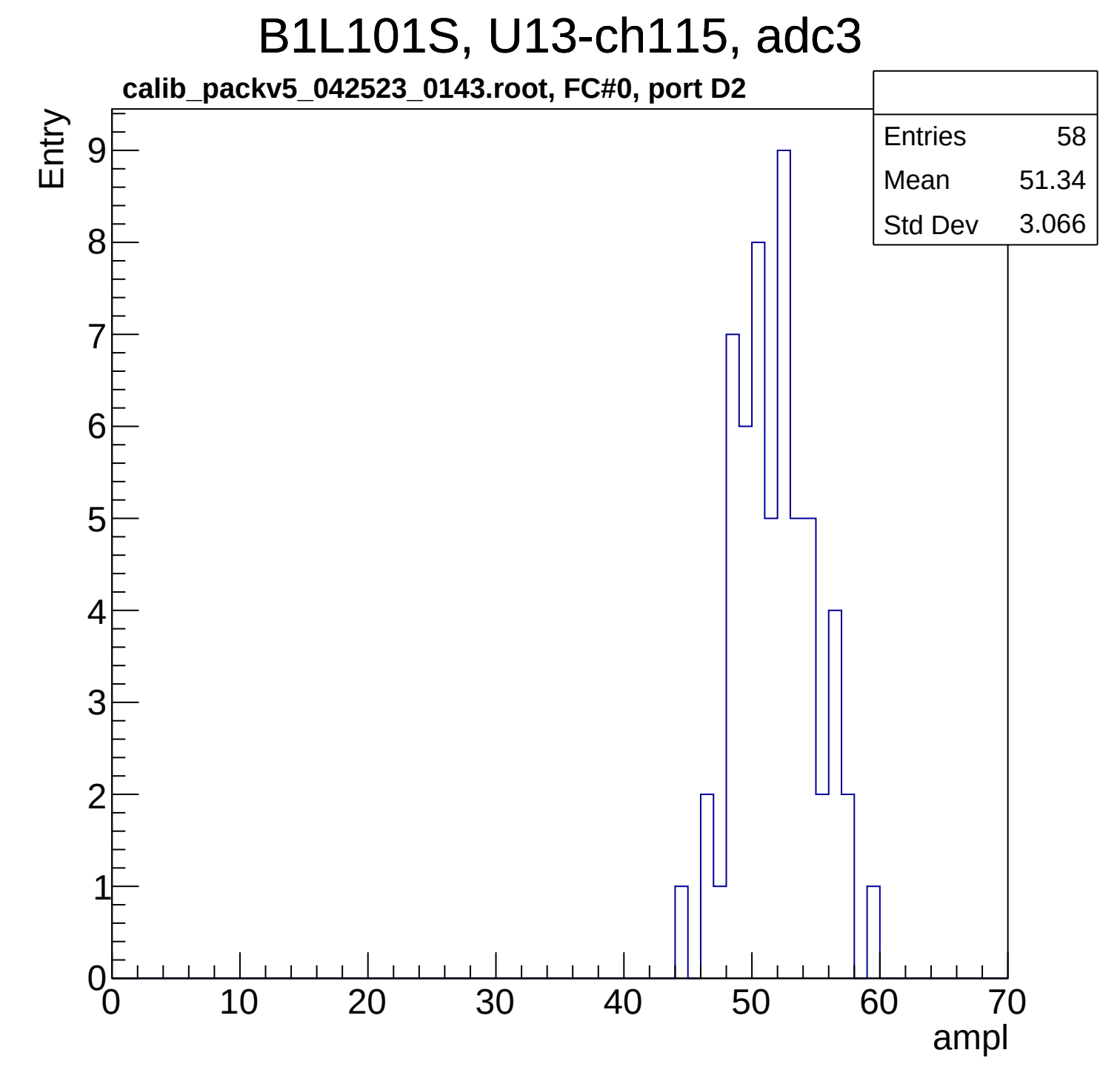

B1L101S, U13-ch115, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 36.4
Std Dev 3.742
Gaus mean : 36.2356
Gaus Width: 4.5619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch115, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 91
Mean 43.65
Std Dev 4.099
Gaus mean : 43.8457
Gaus Width: 4.1898
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch115, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 58
Mean 51.34
Std Dev 3.066
Entry
ampl
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3
4
5
6
7
8
9
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60
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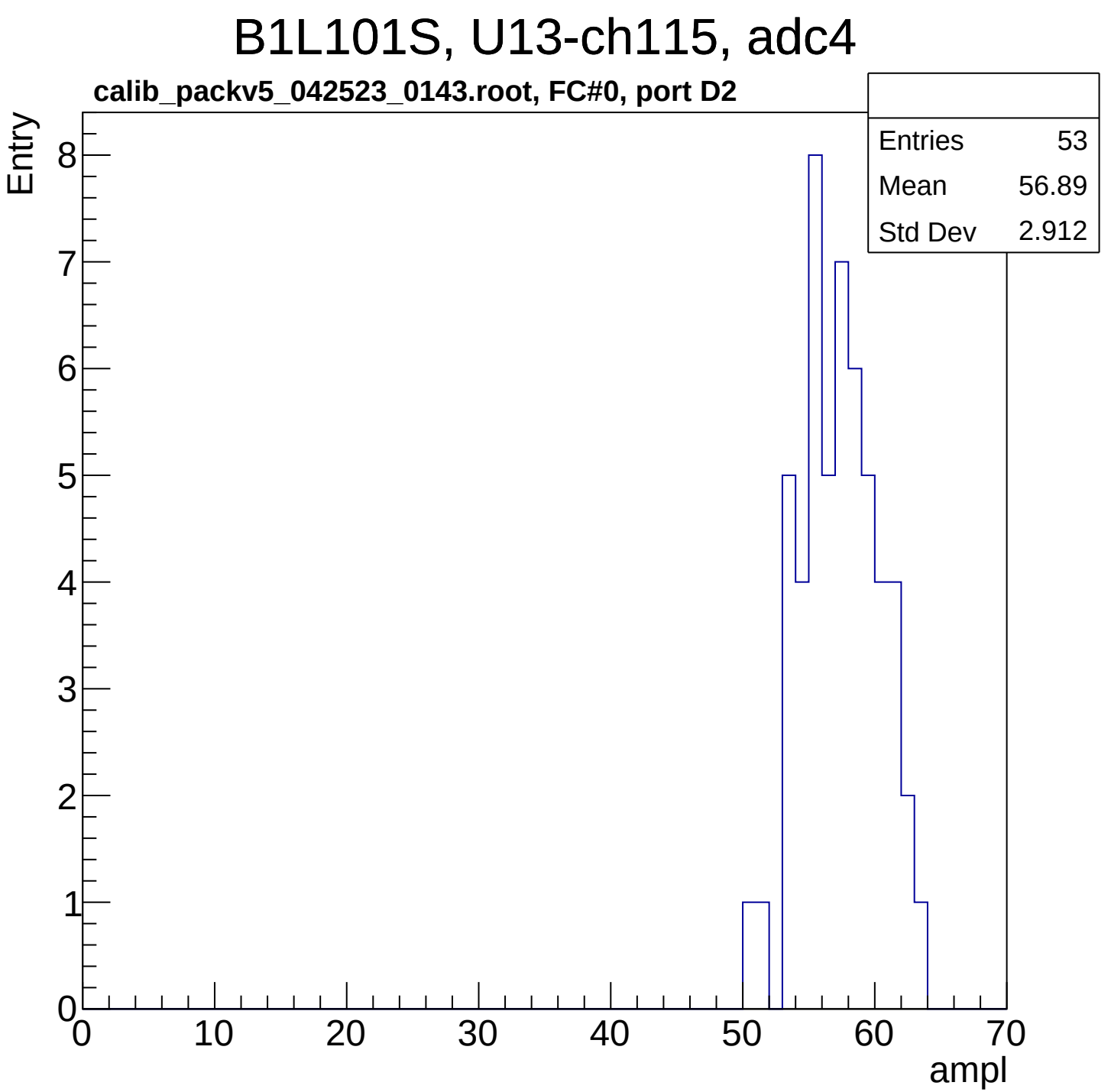
B1L101S, U13-ch115, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 53
Mean 56.89
Std Dev 2.912
Entry
ampl
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1
2
3
4
5
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7
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0
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40
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60
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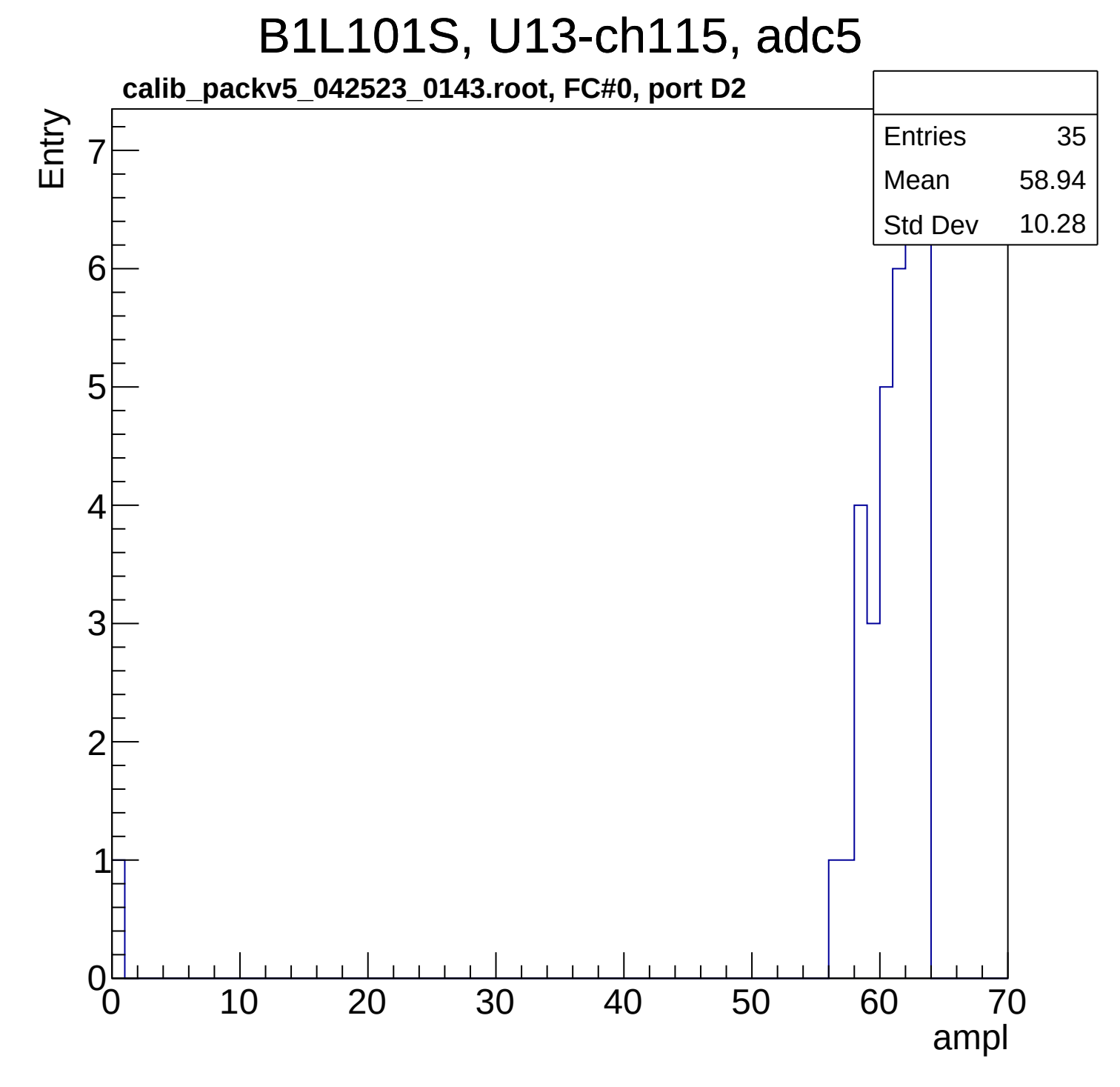
B1L101S, U13-ch115, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 35
Mean 58.94
Std Dev 10.28
Entry
ampl
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1
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4
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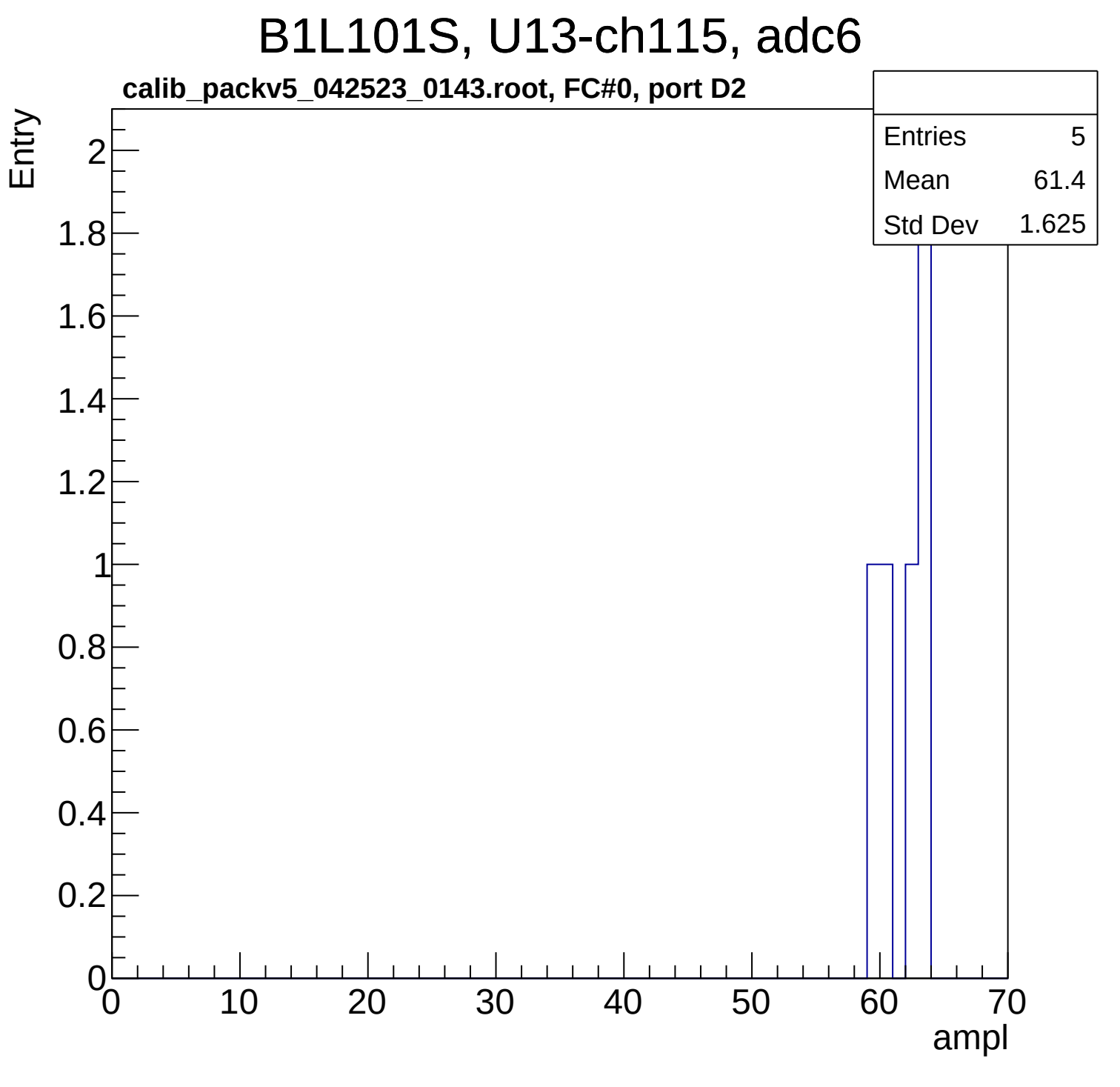
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch115, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 5
Mean 61.4
Std Dev 1.625
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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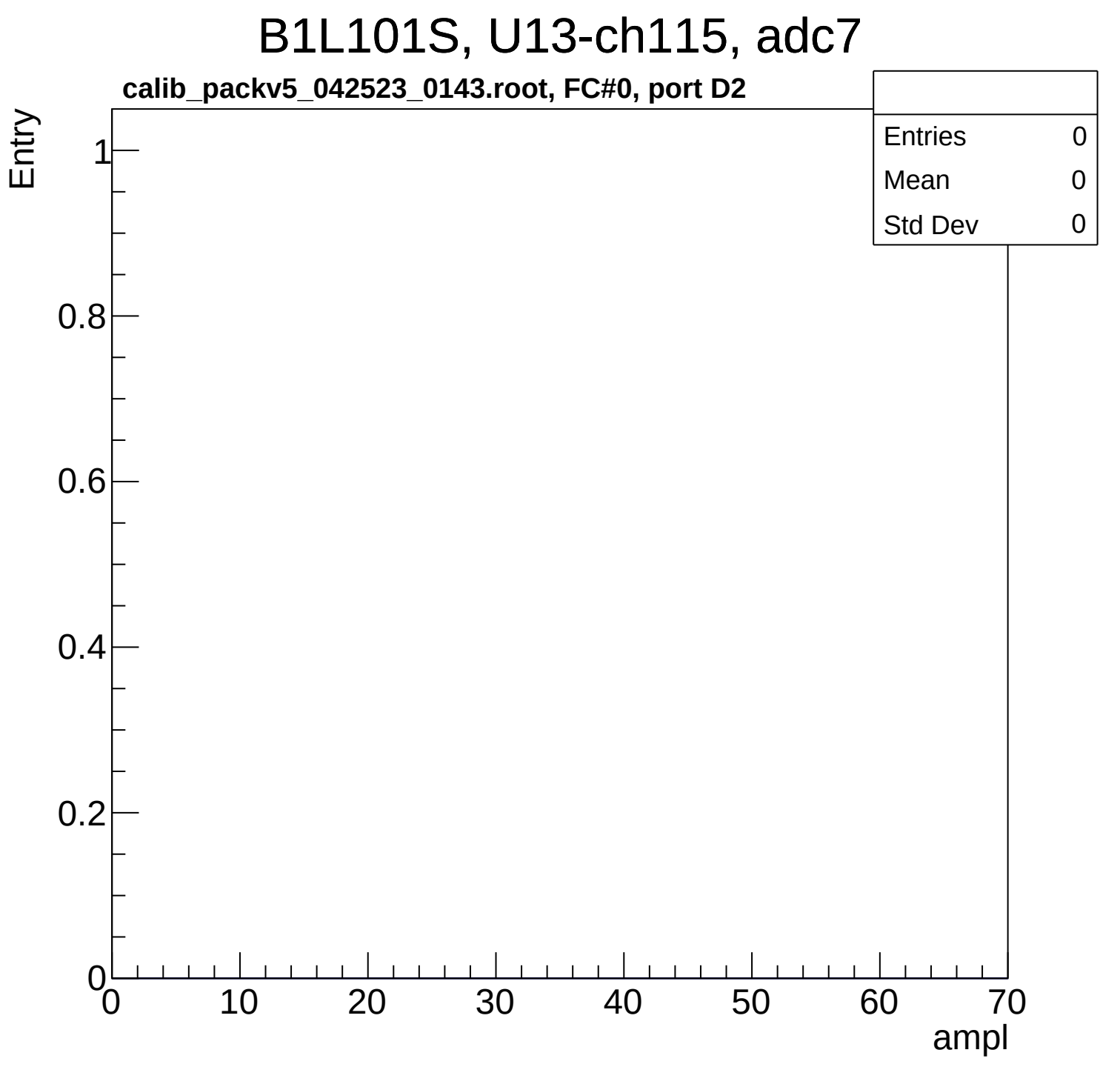
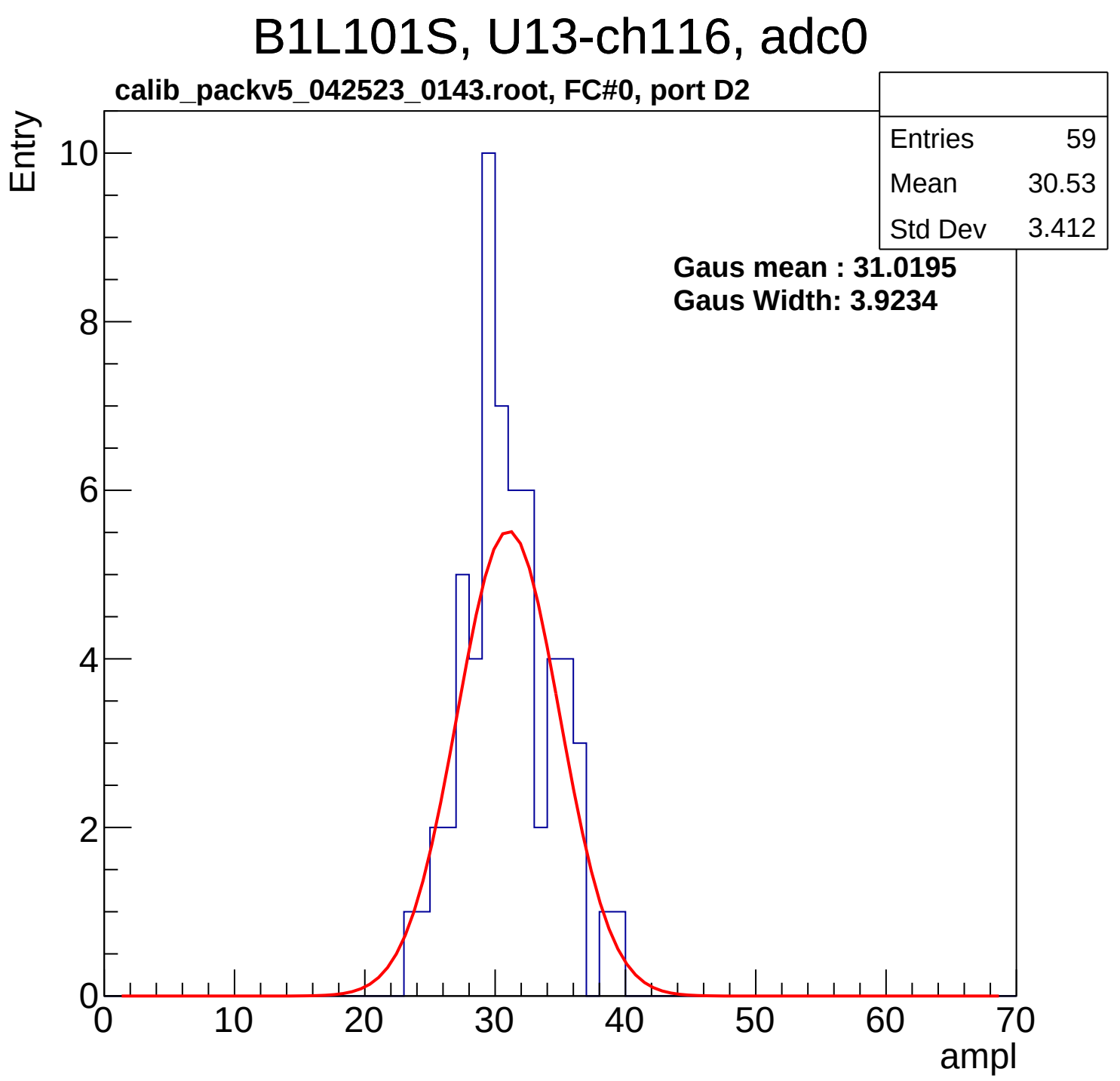

B1L101S, U13-ch115, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch116, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 30.53
Std Dev 3.412
Gaus mean : 31.0195
Gaus Width: 3.9234
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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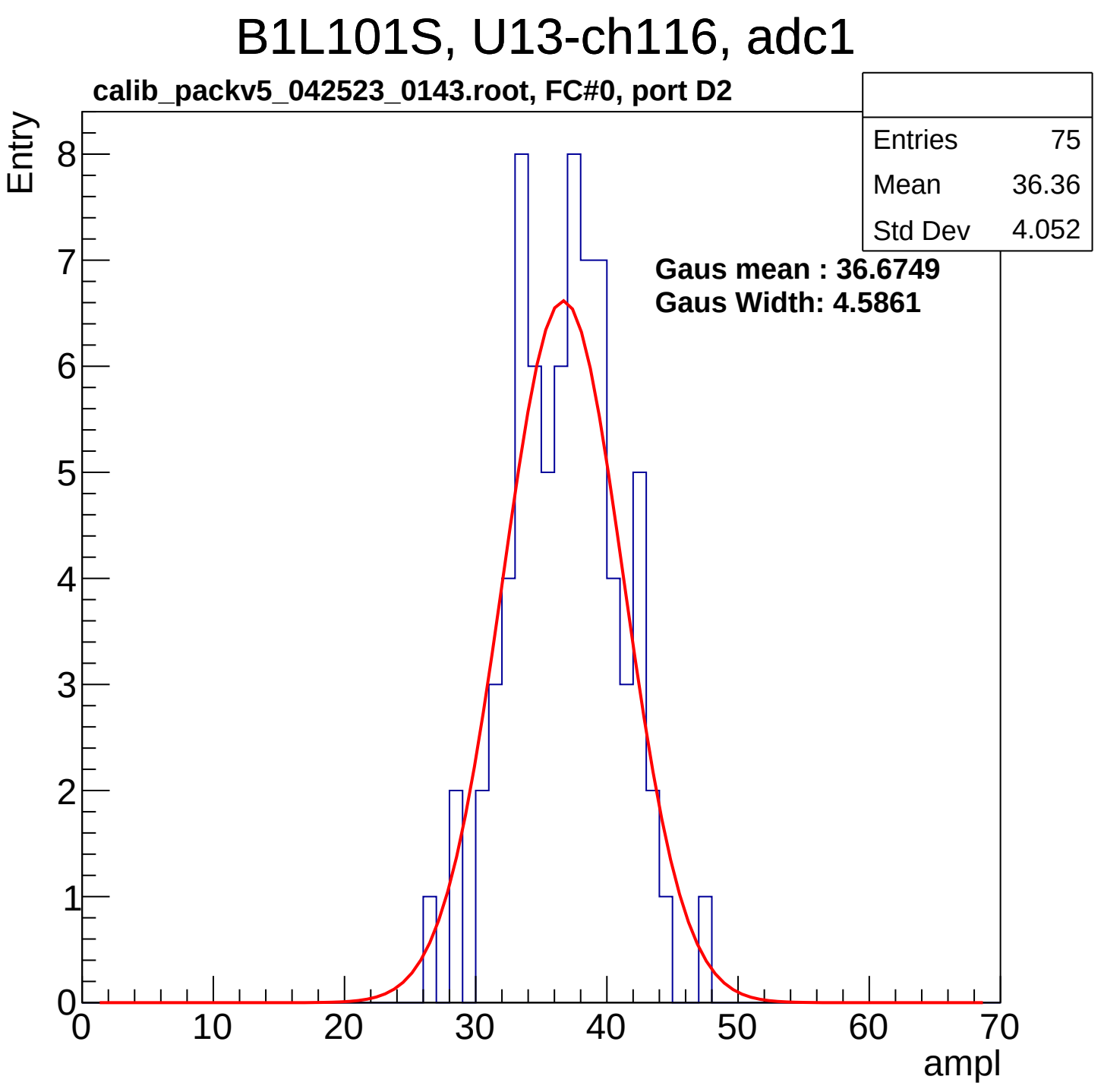
B1L101S, U13-ch116, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 75
Mean 36.36
Std Dev 4.052
Gaus mean : 36.6749
Gaus Width: 4.5861
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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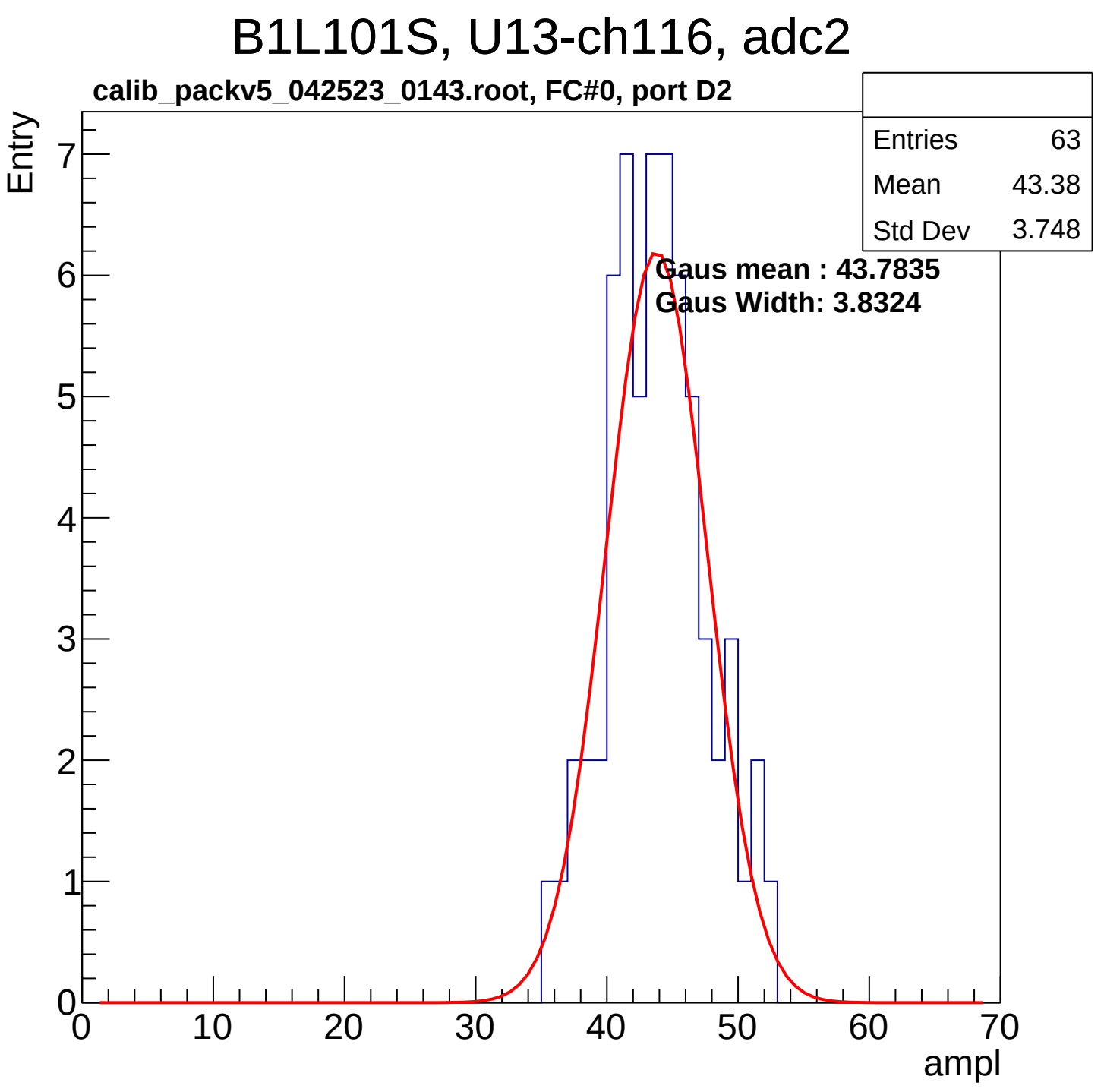
B1L101S, U13-ch116, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 63
Mean 43.38
Std Dev 3.748
Gaus mean : 43.7835
Gaus Width: 3.8324
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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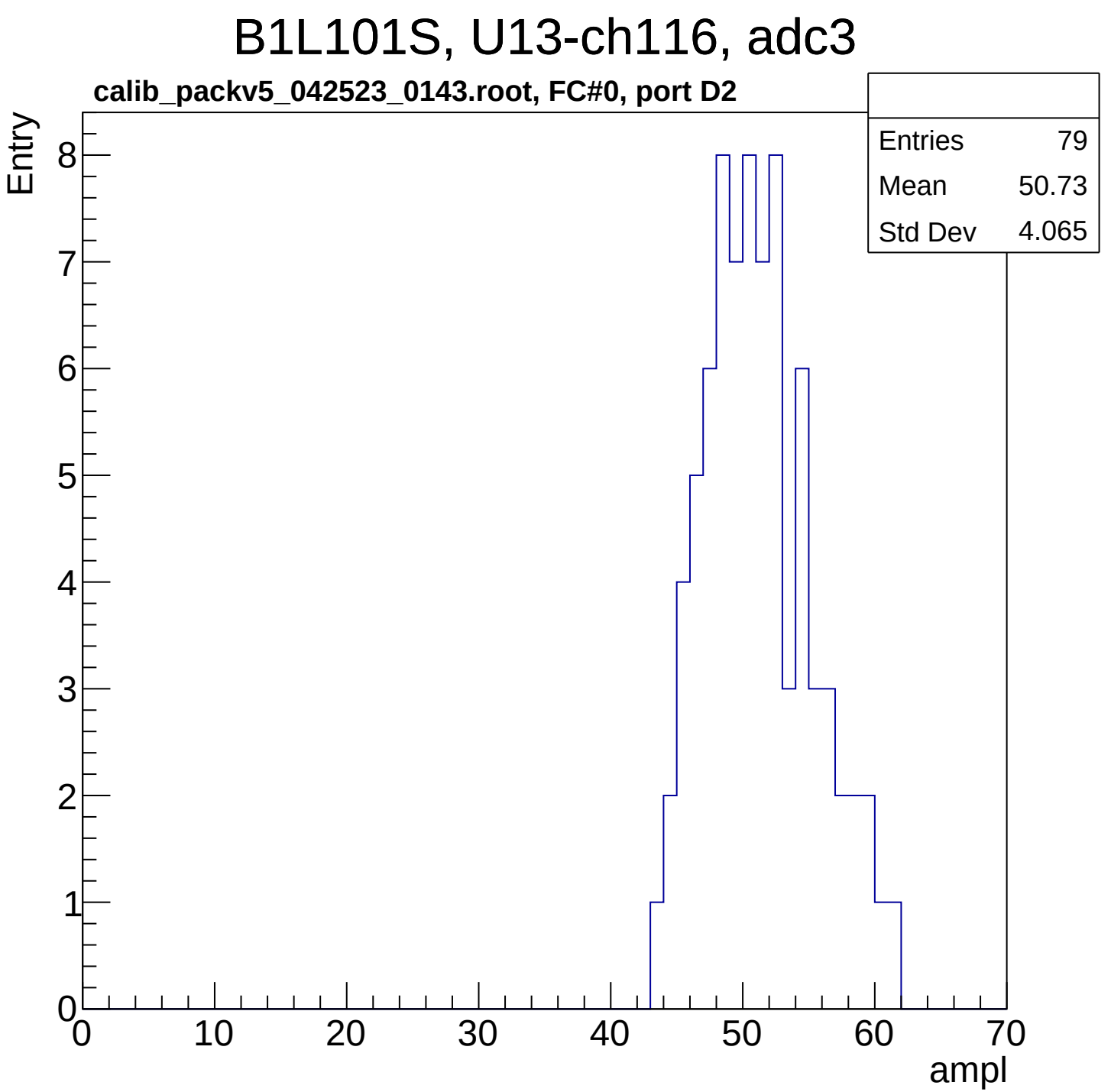
B1L101S, U13-ch116, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 79
Mean 50.73
Std Dev 4.065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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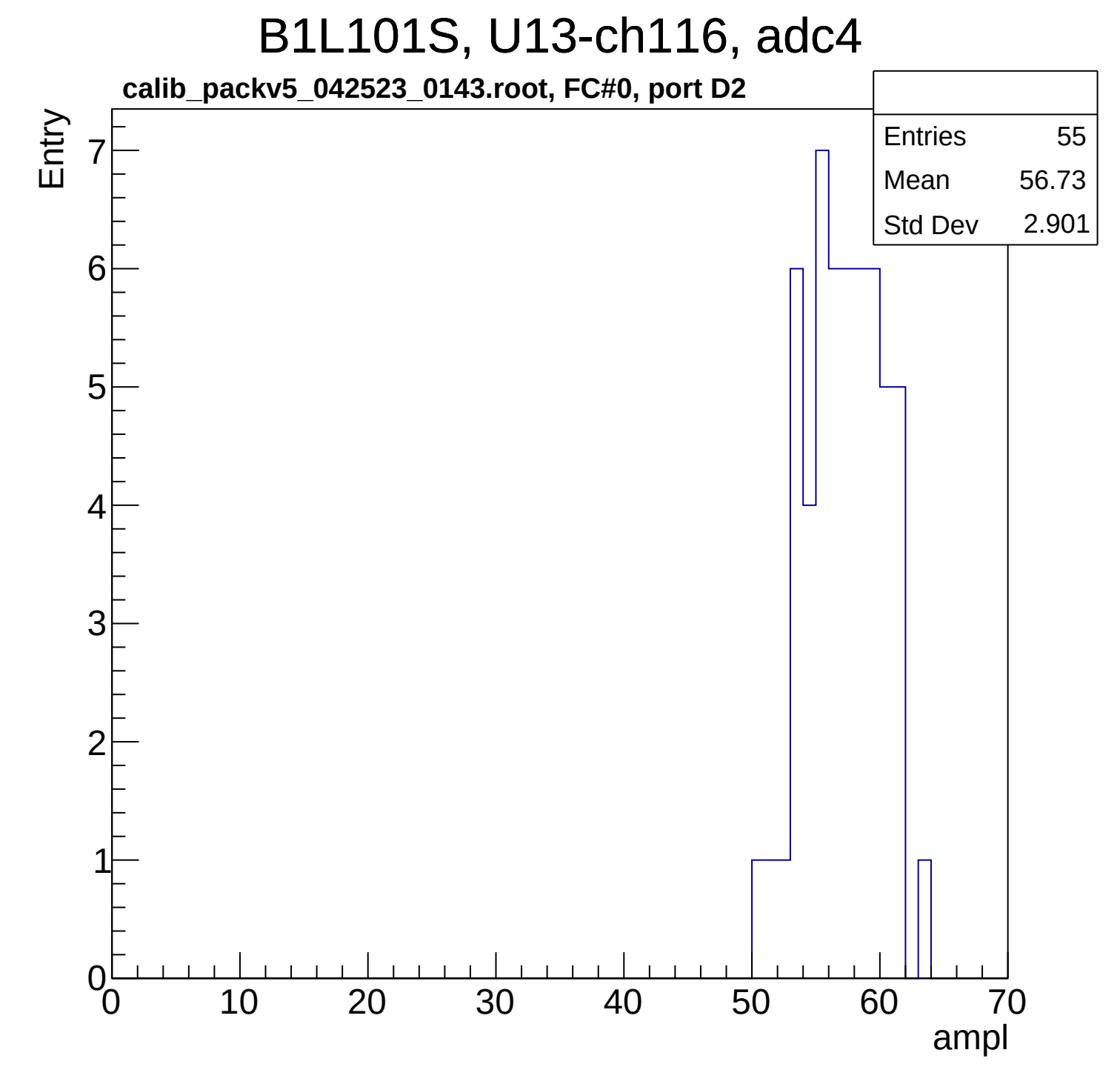
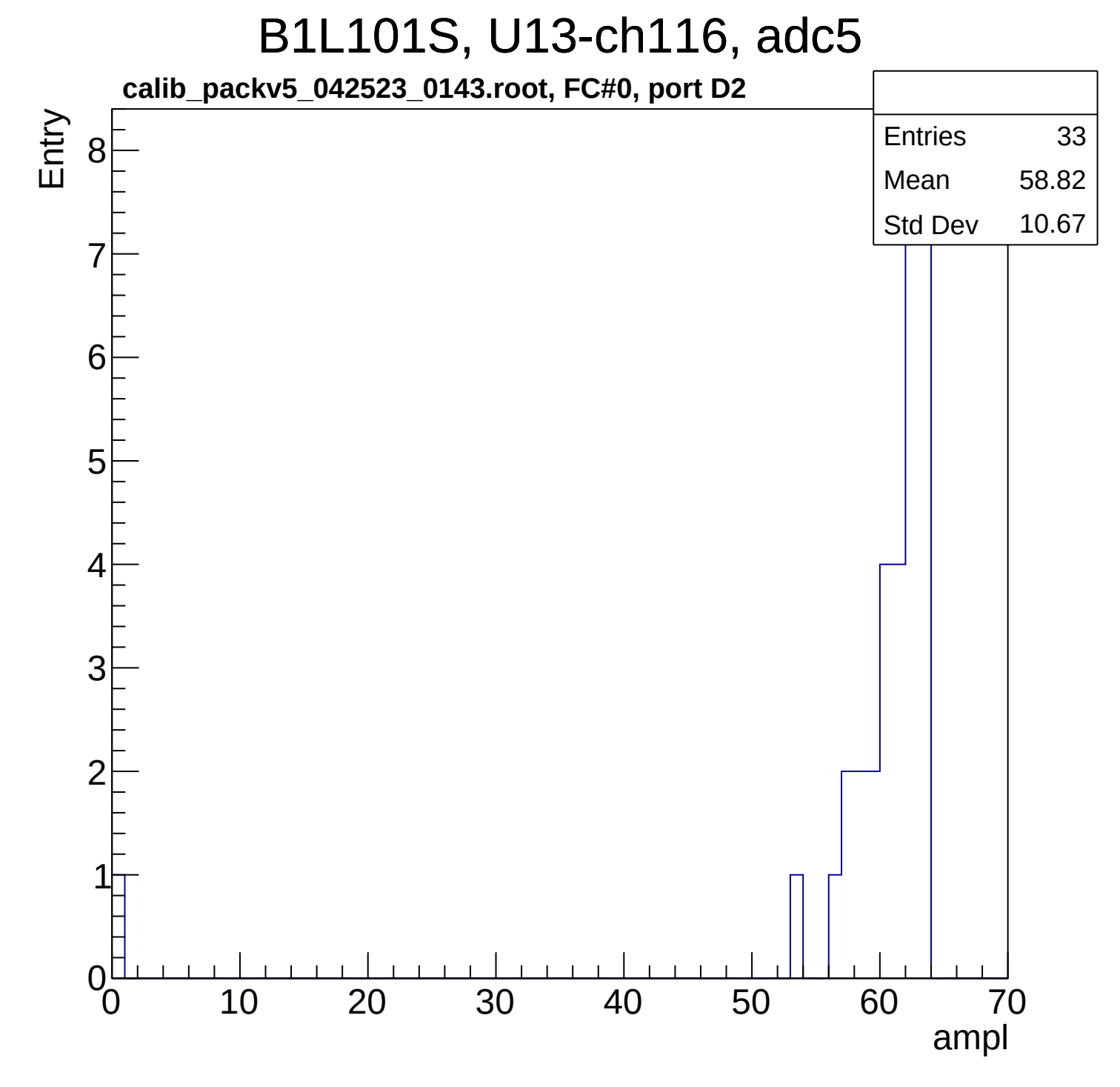
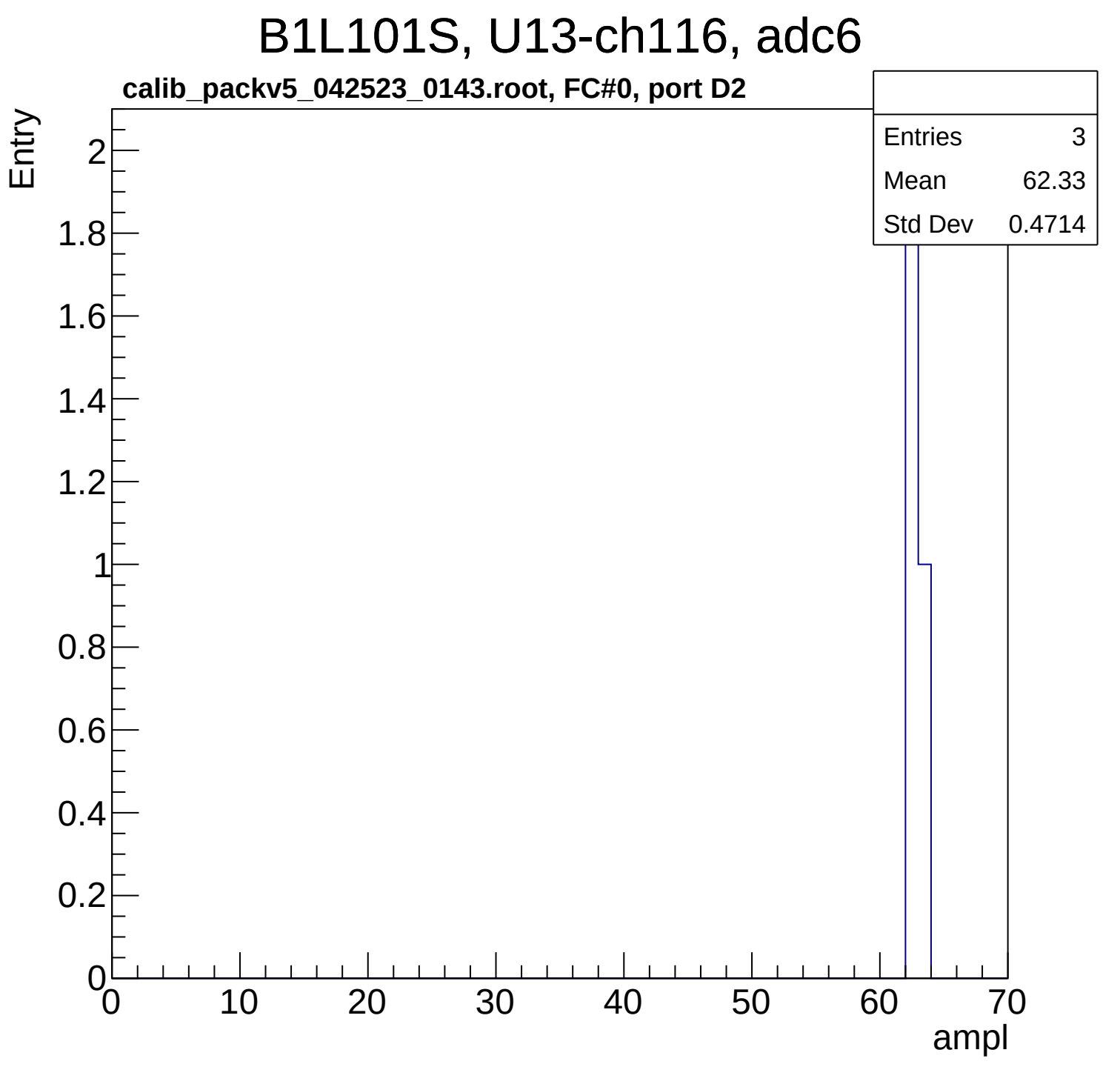
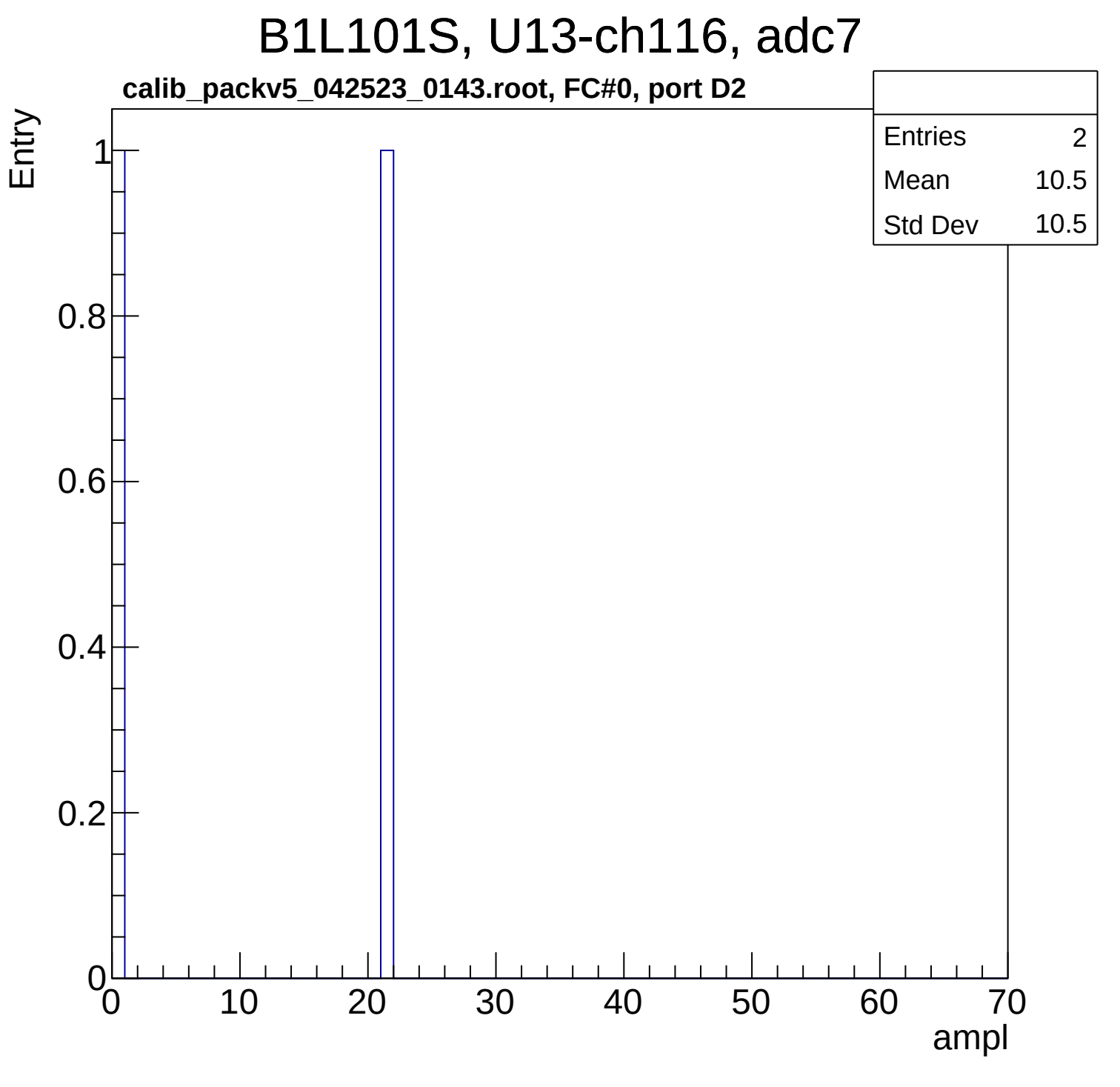
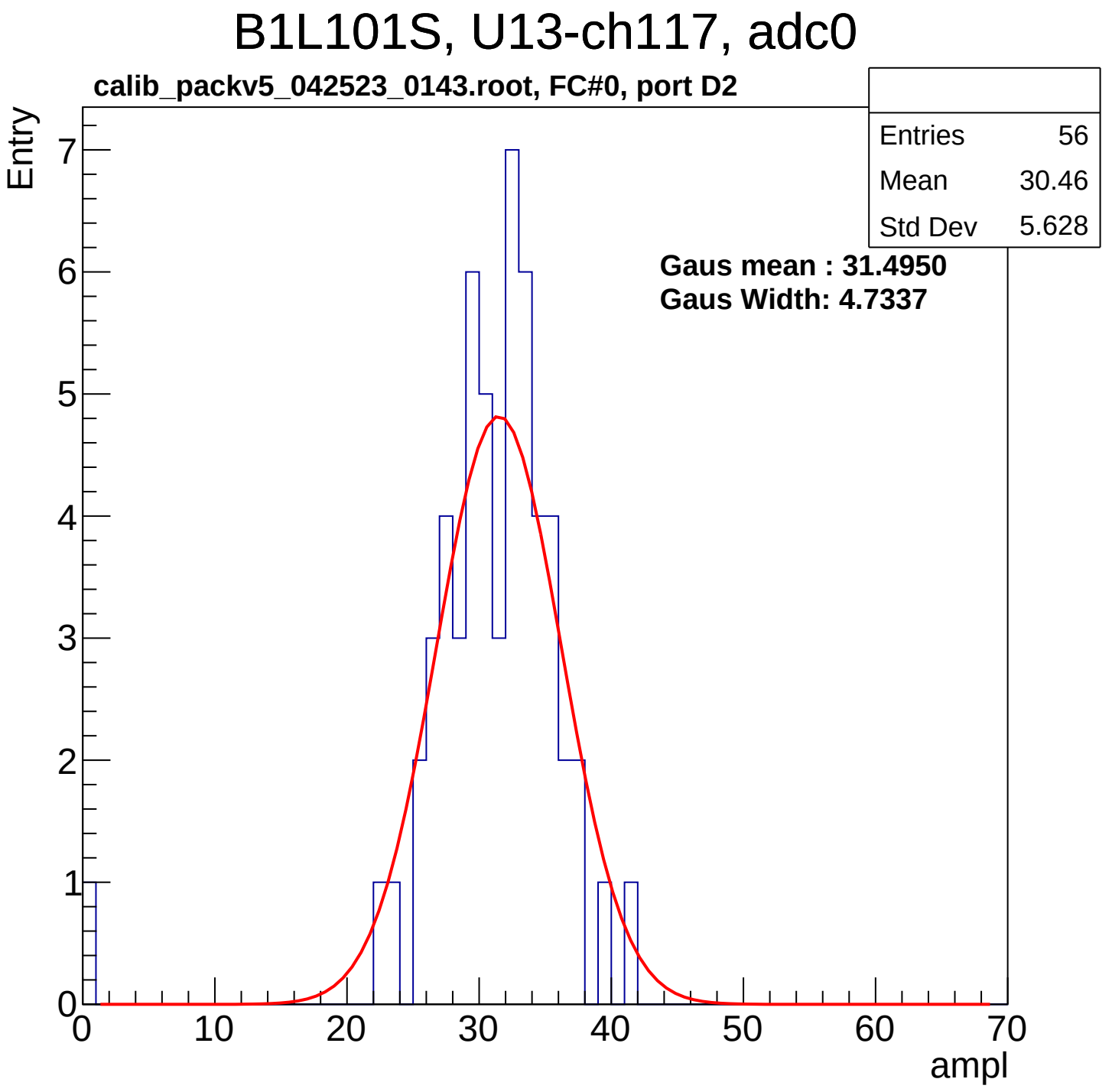

B1L101S, U13-ch116, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 56.73
Std Dev 2.901
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch116, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 33
Mean 58.82
Std Dev 10.67
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch116, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch116, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch117, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 30.46
Std Dev 5.628
Gaus mean : 31.4950
Gaus Width: 4.7337
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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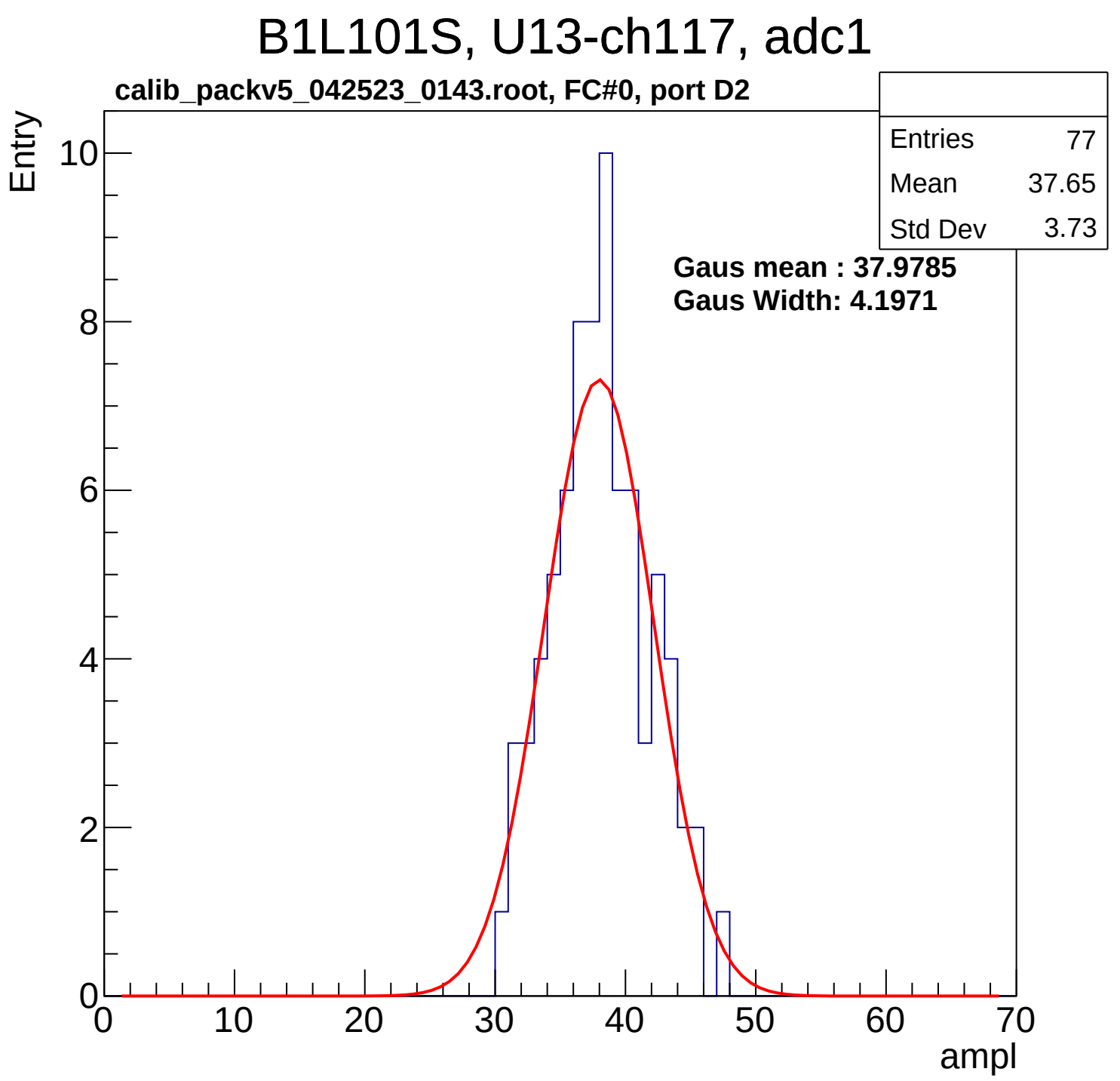
B1L101S, U13-ch117, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 77
Mean 37.65
Std Dev 3.73
Gaus mean : 37.9785
Gaus Width: 4.1971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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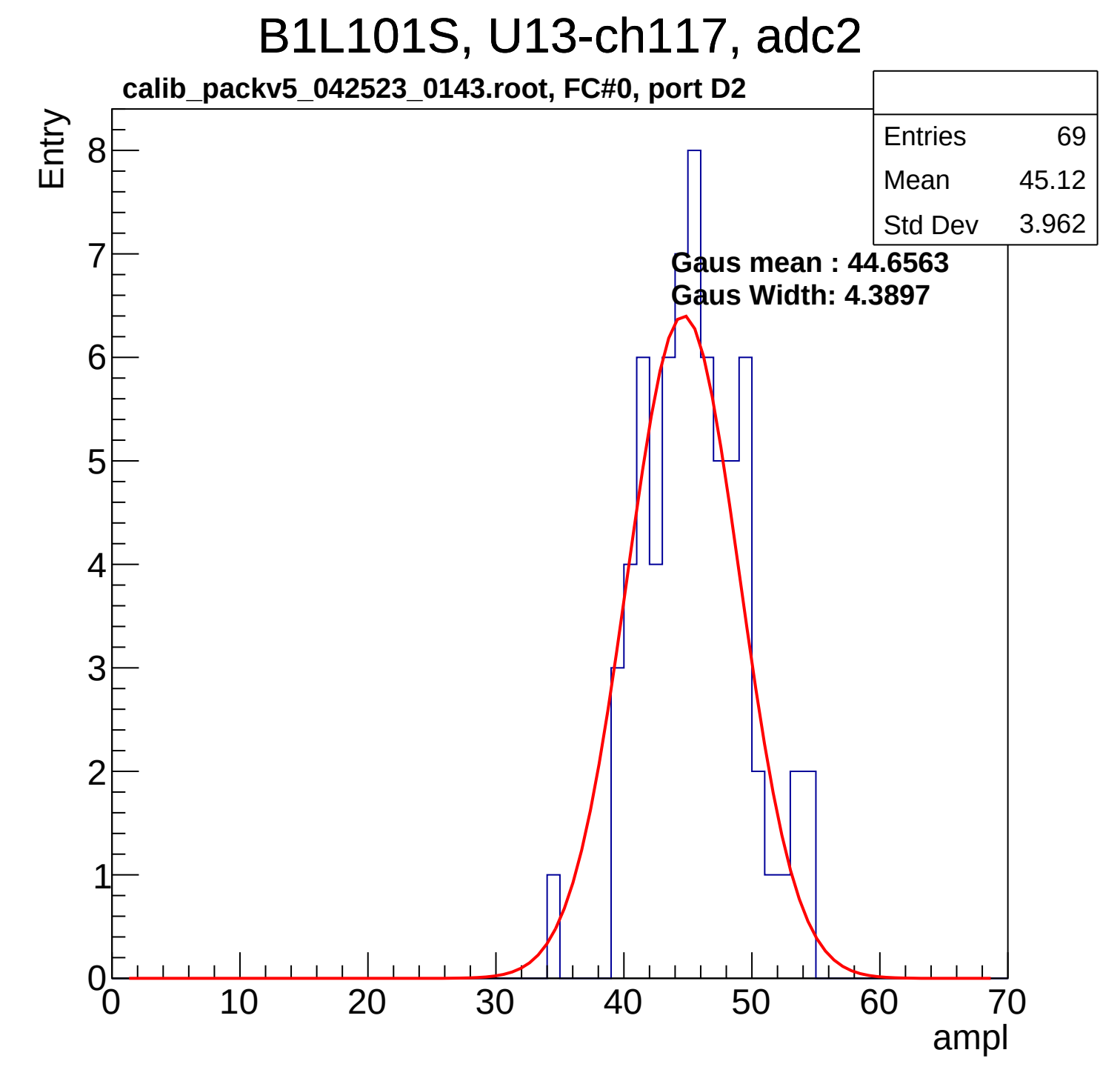
B1L101S, U13-ch117, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 45.12
Std Dev 3.962
Gaus mean : 44.6563
Gaus Width: 4.3897
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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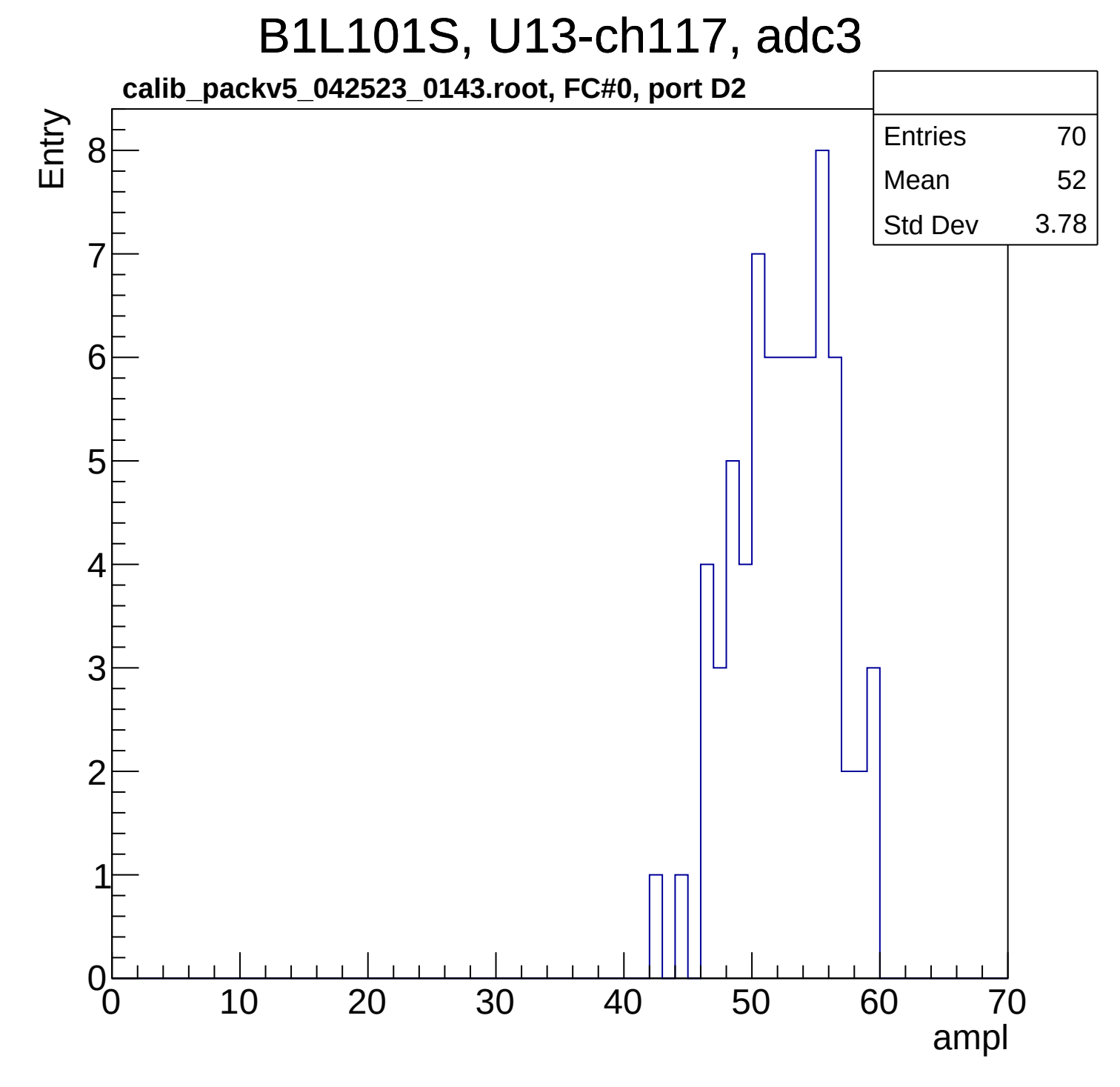
B1L101S, U13-ch117, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 52
Std Dev 3.78
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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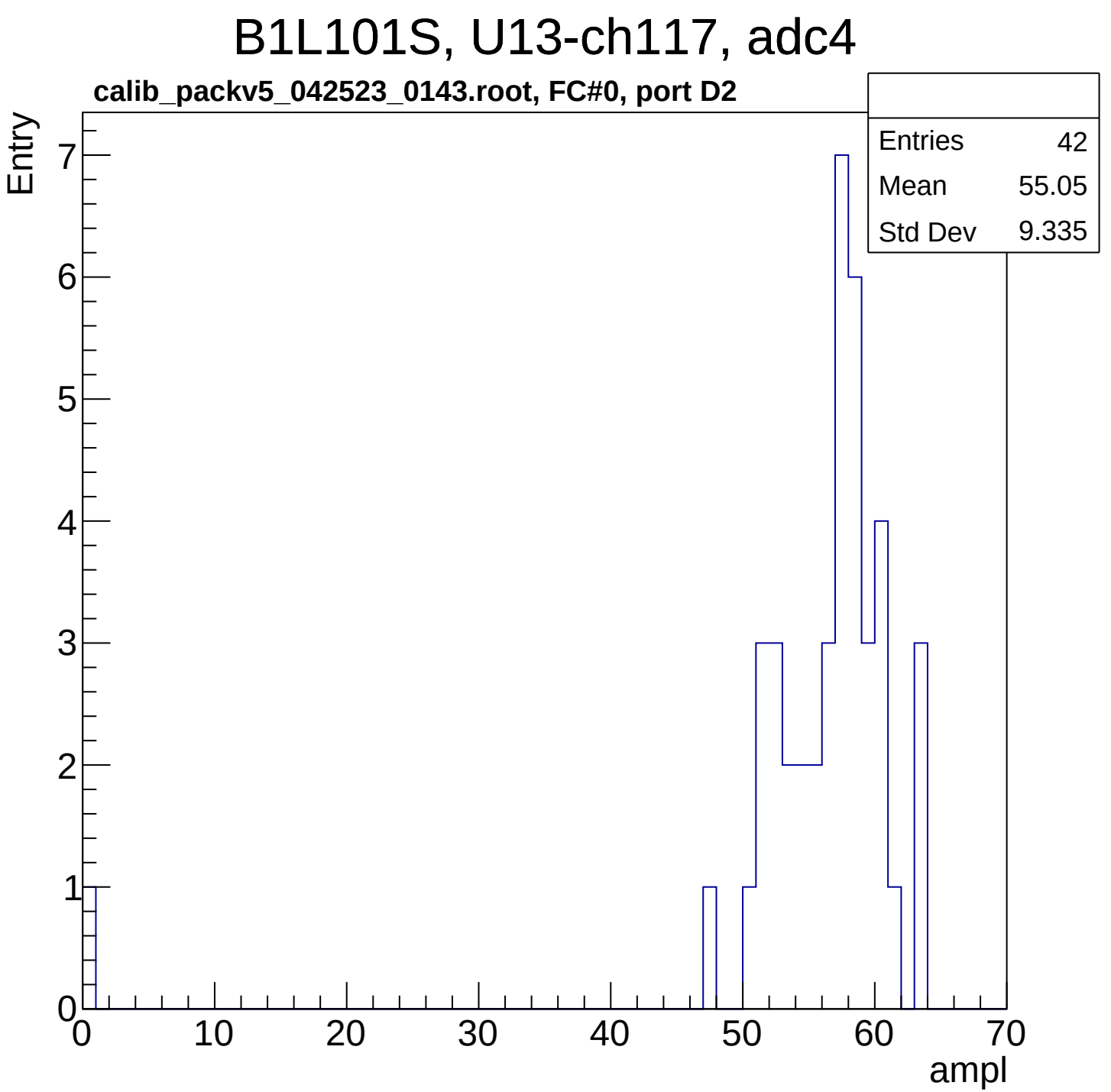
B1L101S, U13-ch117, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 42
Mean 55.05
Std Dev 9.335
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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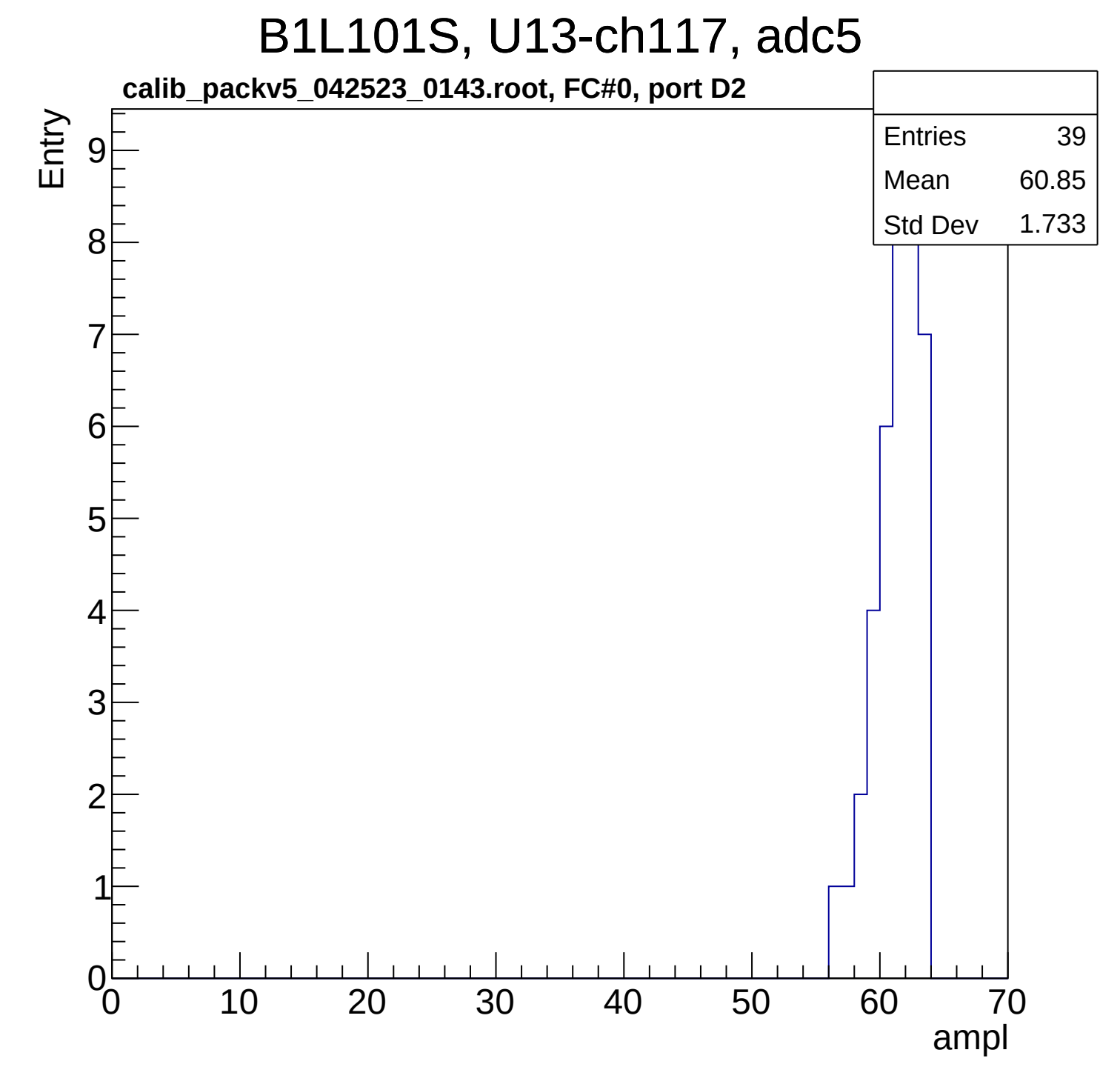
B1L101S, U13-ch117, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 39
Mean 60.85
Std Dev 1.733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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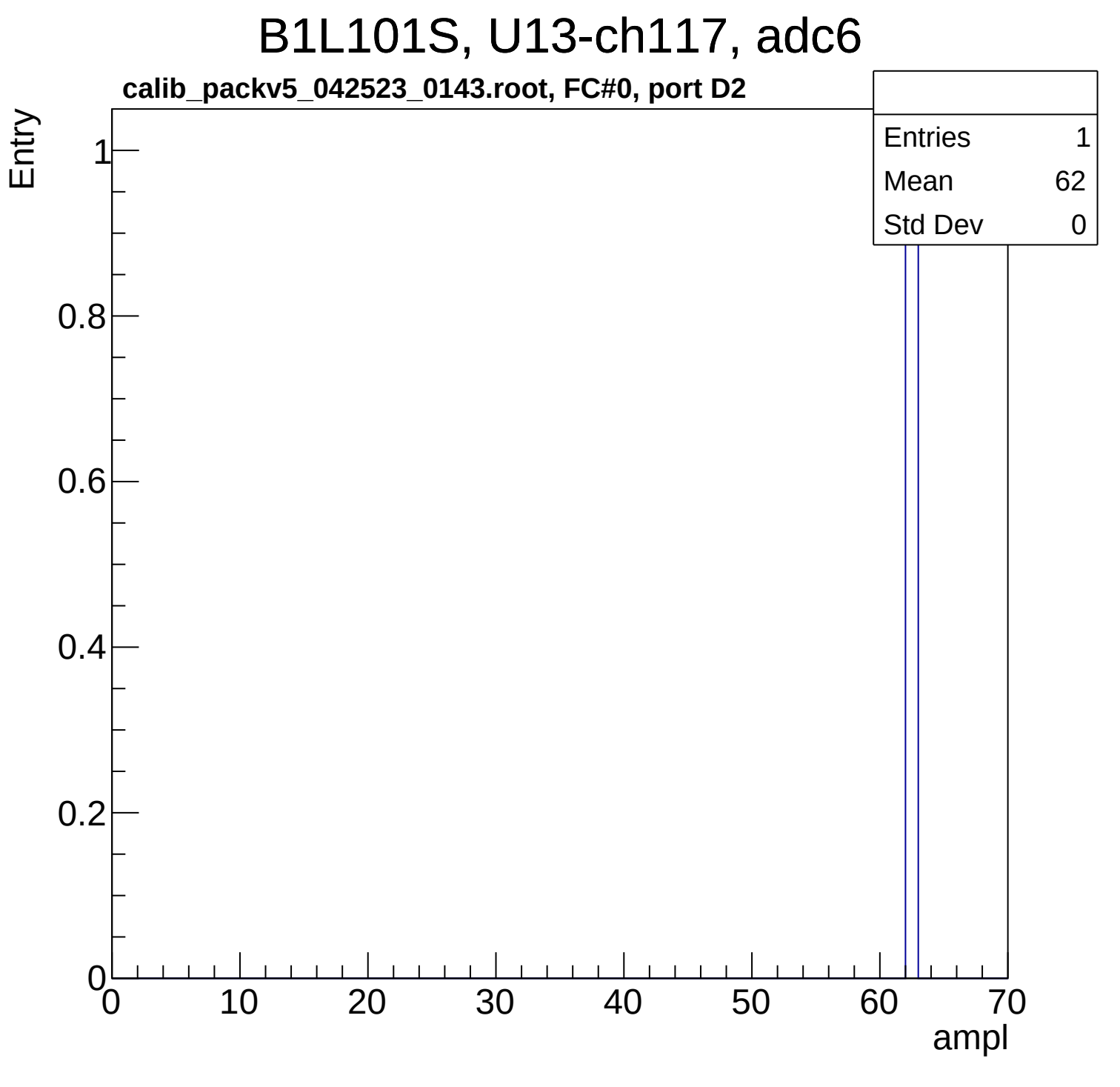
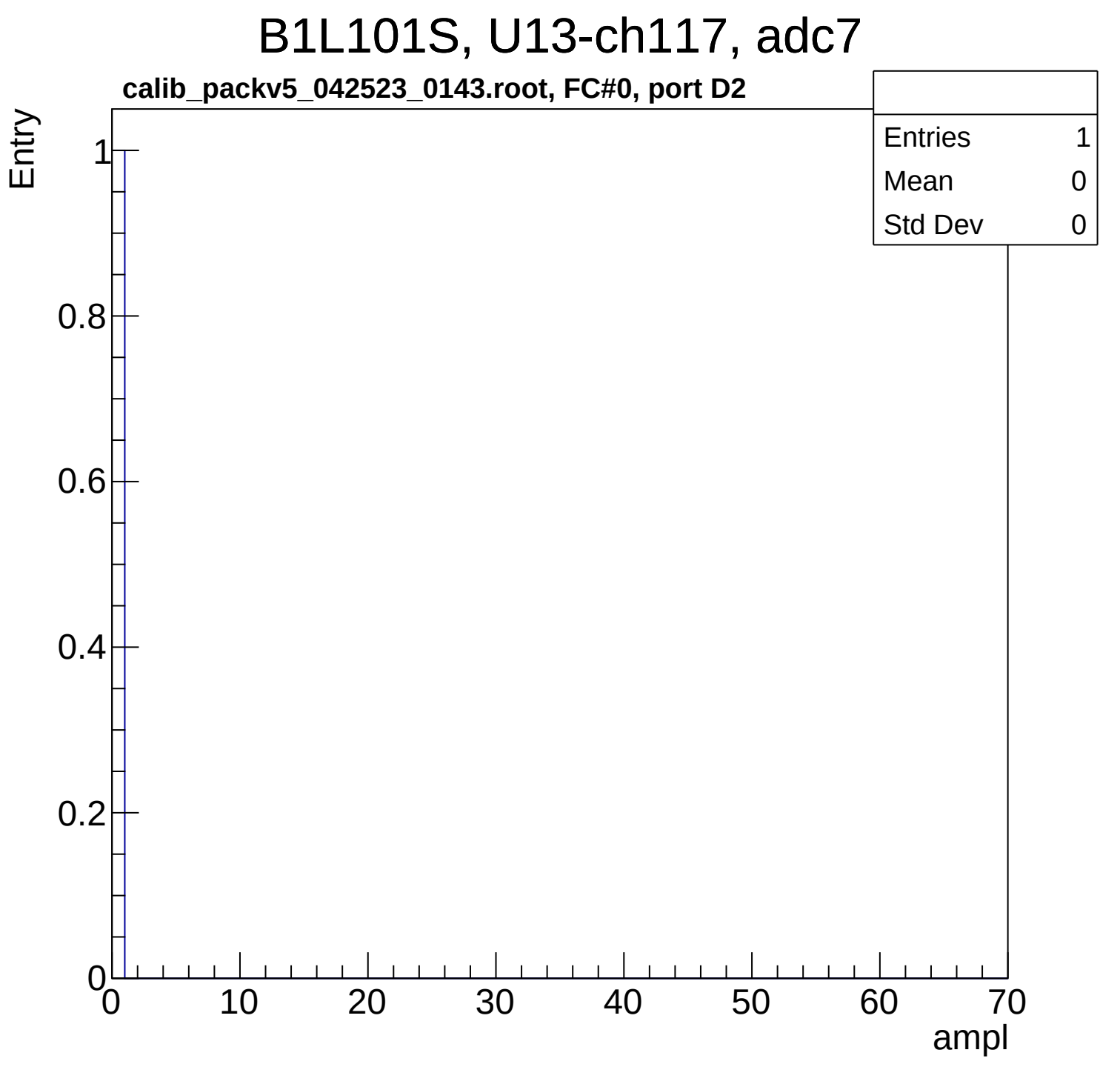
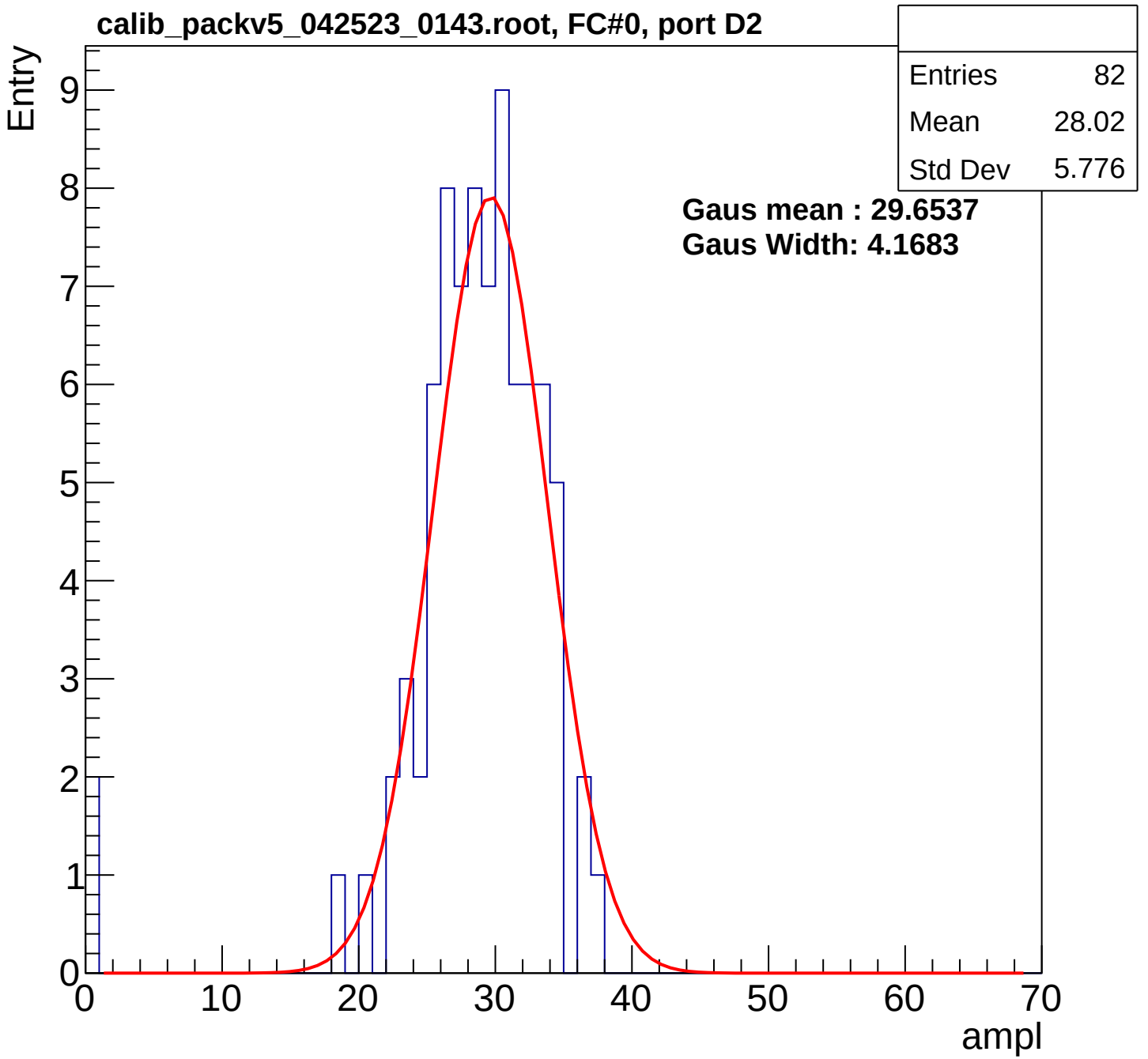

B1L101S, U13-ch117, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch117, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch118, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 82
Mean 28.02
Std Dev 5.776
Gaus mean : 29.6537
Gaus Width: 4.1683
Entry
ampl
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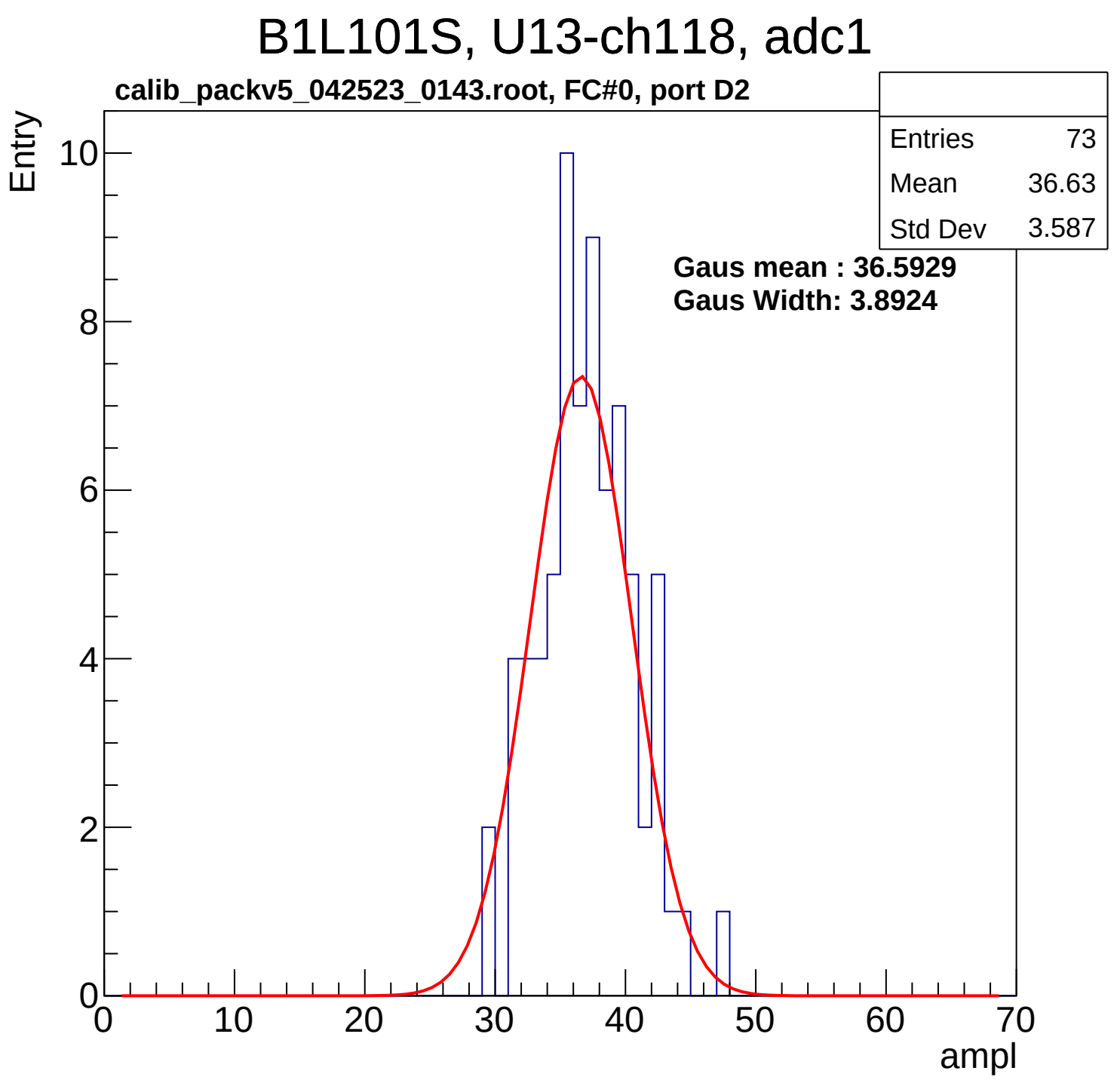
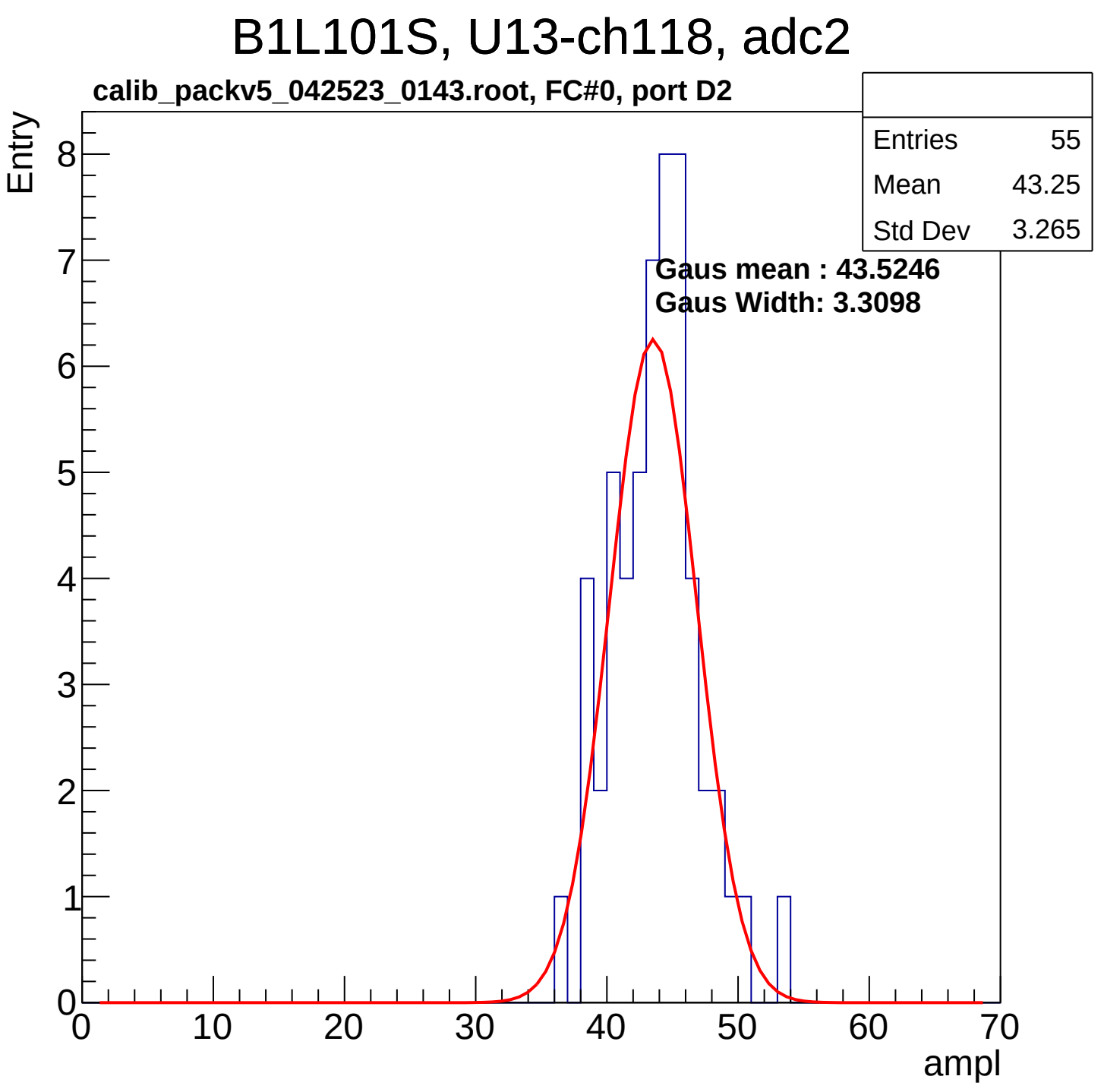
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch118, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 73
Mean 36.63
Std Dev 3.587
Gaus mean : 36.5929
Gaus Width: 3.8924
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch118, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 43.25
Std Dev 3.265
Gaus mean : 43.5246
Gaus Width: 3.3098
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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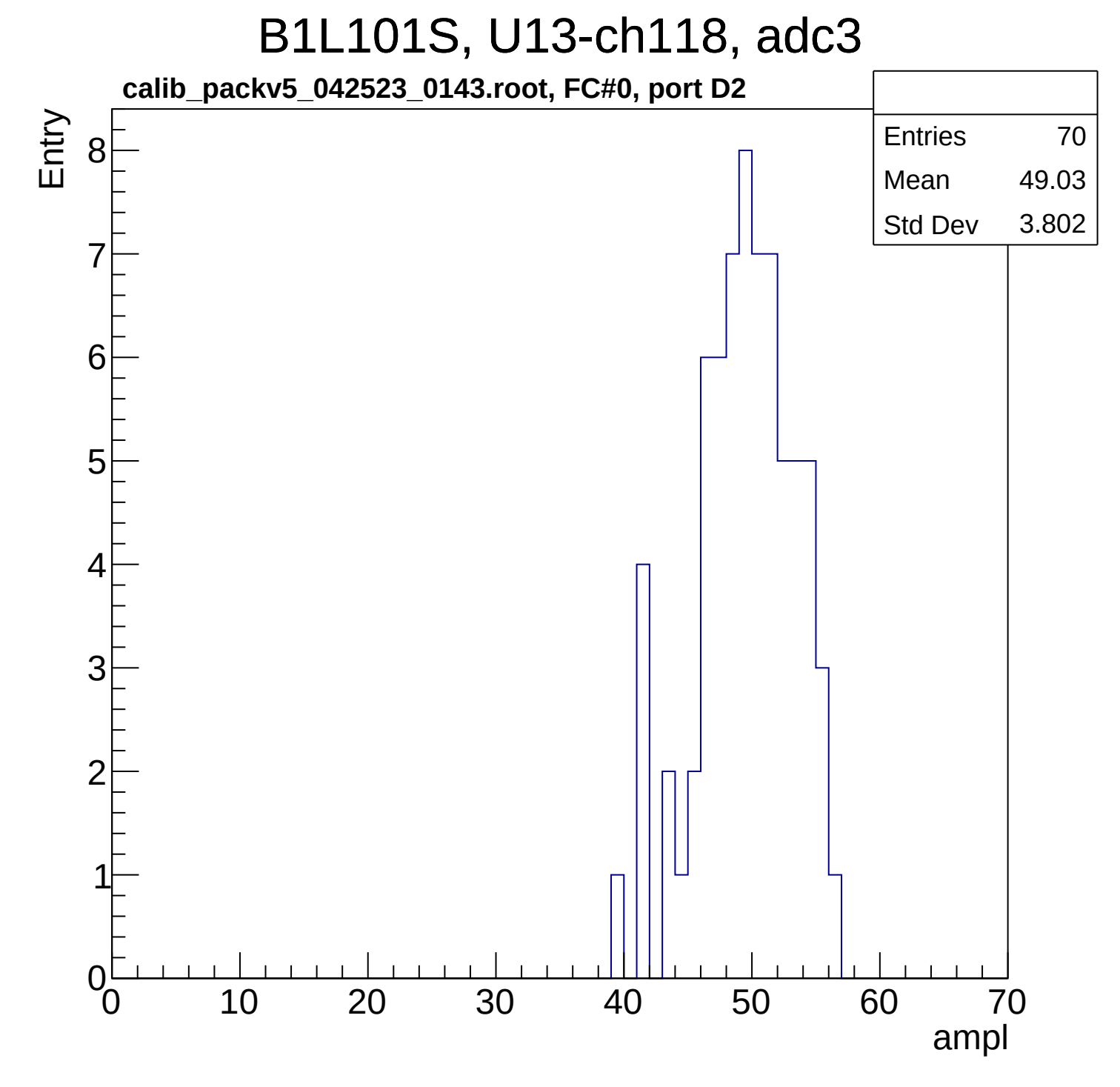
B1L101S, U13-ch118, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 49.03
Std Dev 3.802
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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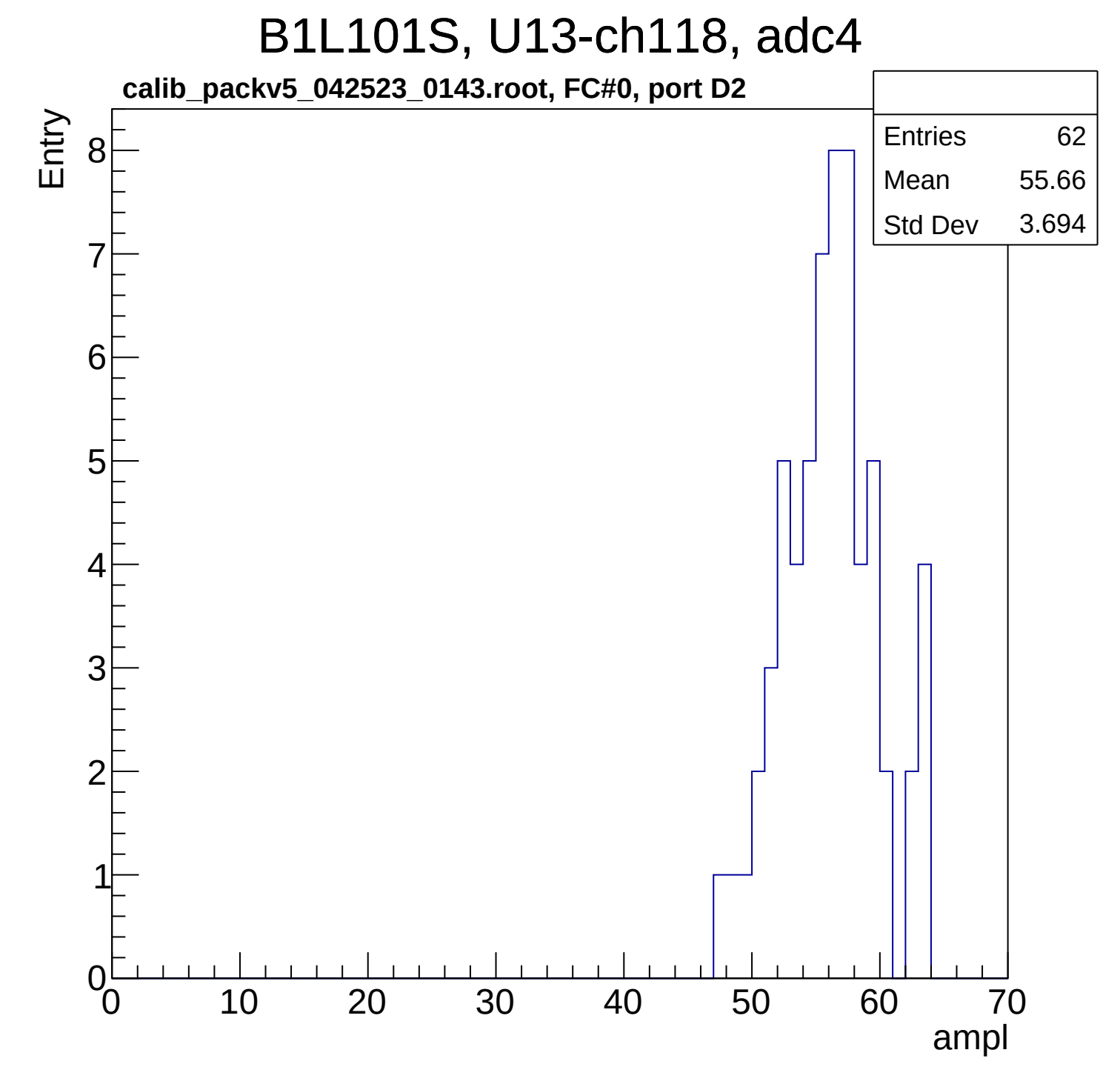
40
50
60
70


B1L101S, U13-ch118, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 55.66
Std Dev 3.694
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
50
60
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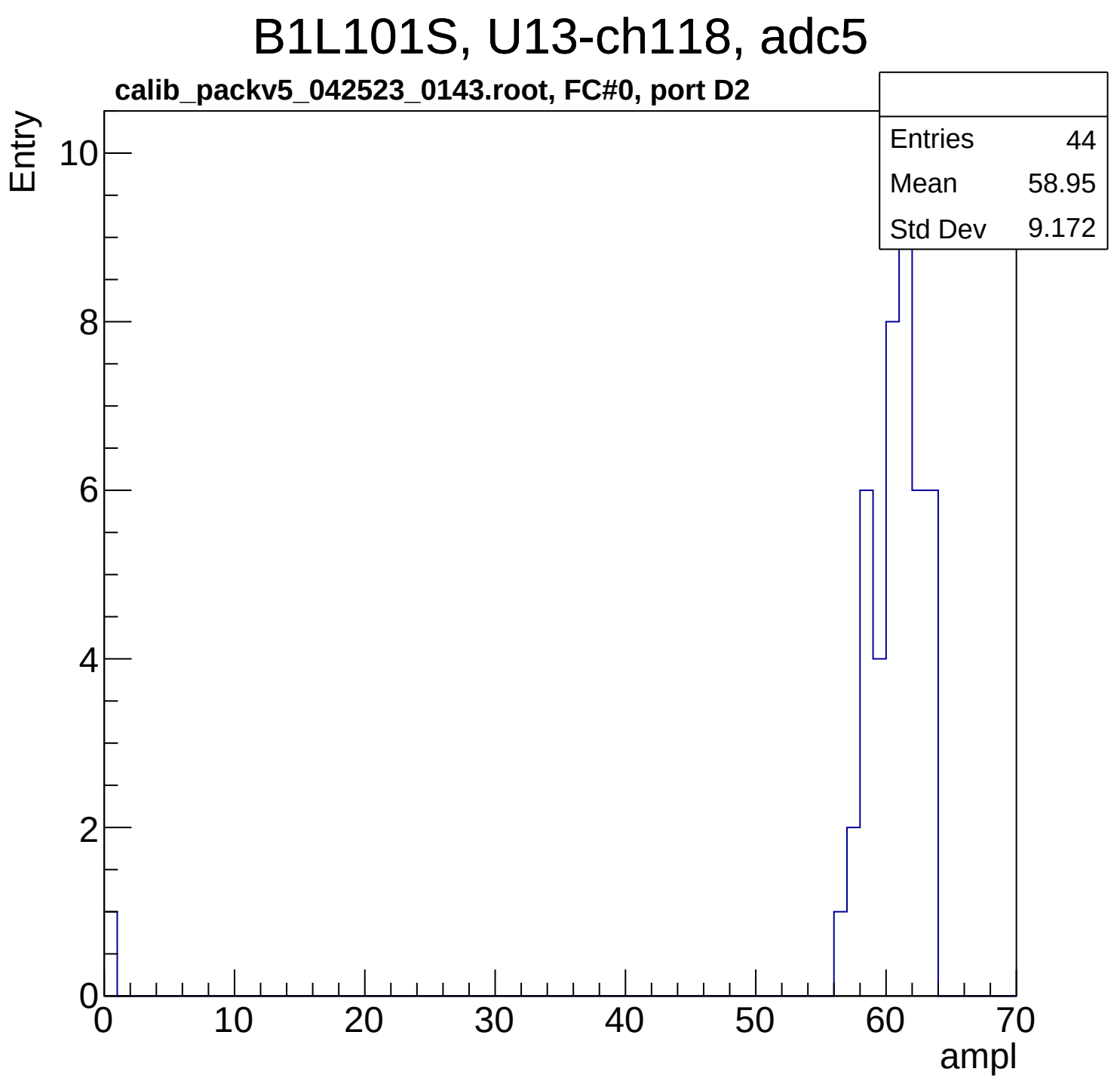
B1L101S, U13-ch118, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 44
Mean 58.95
Std Dev 9.172
Entry
0
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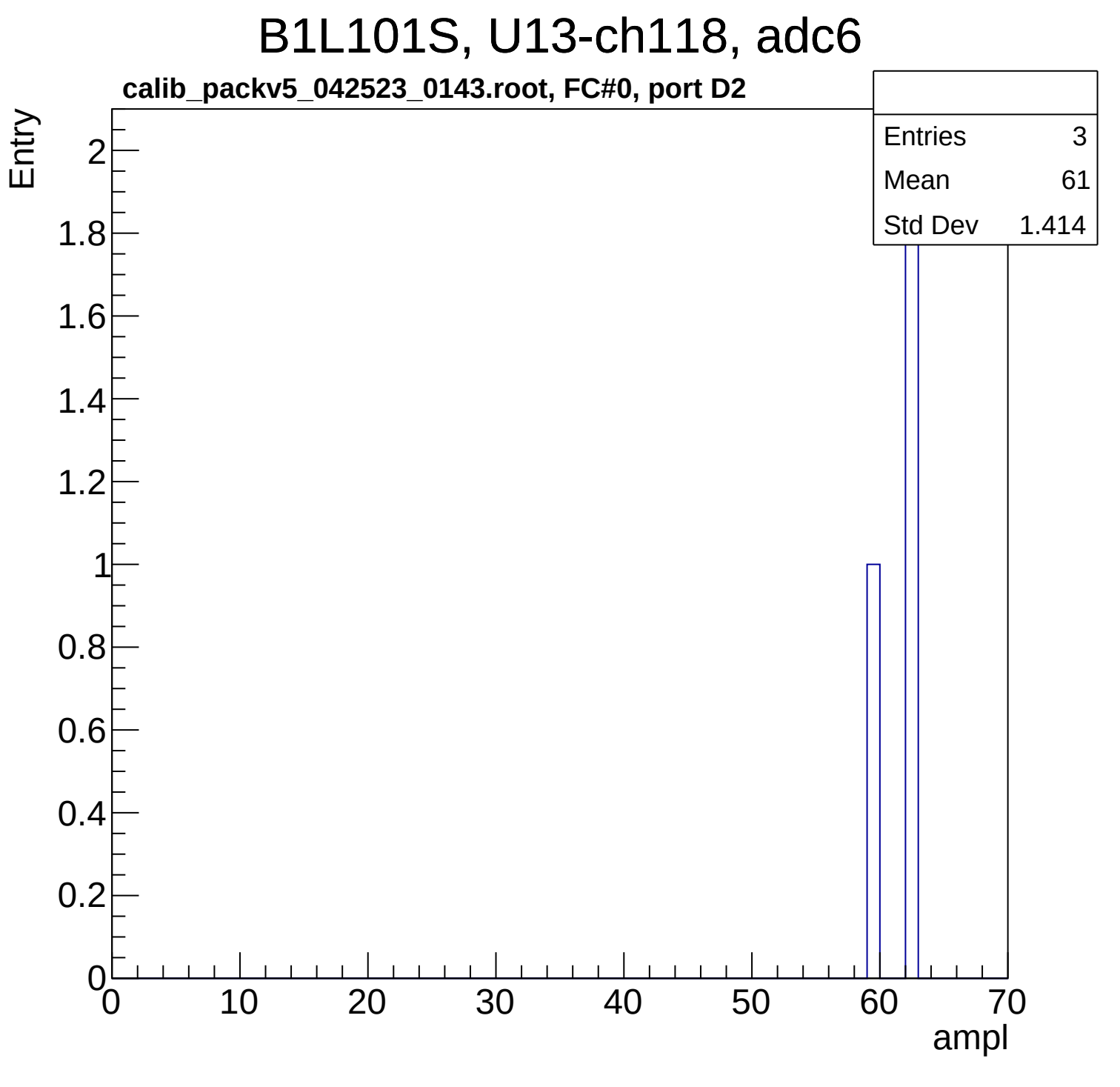
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch118, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 61
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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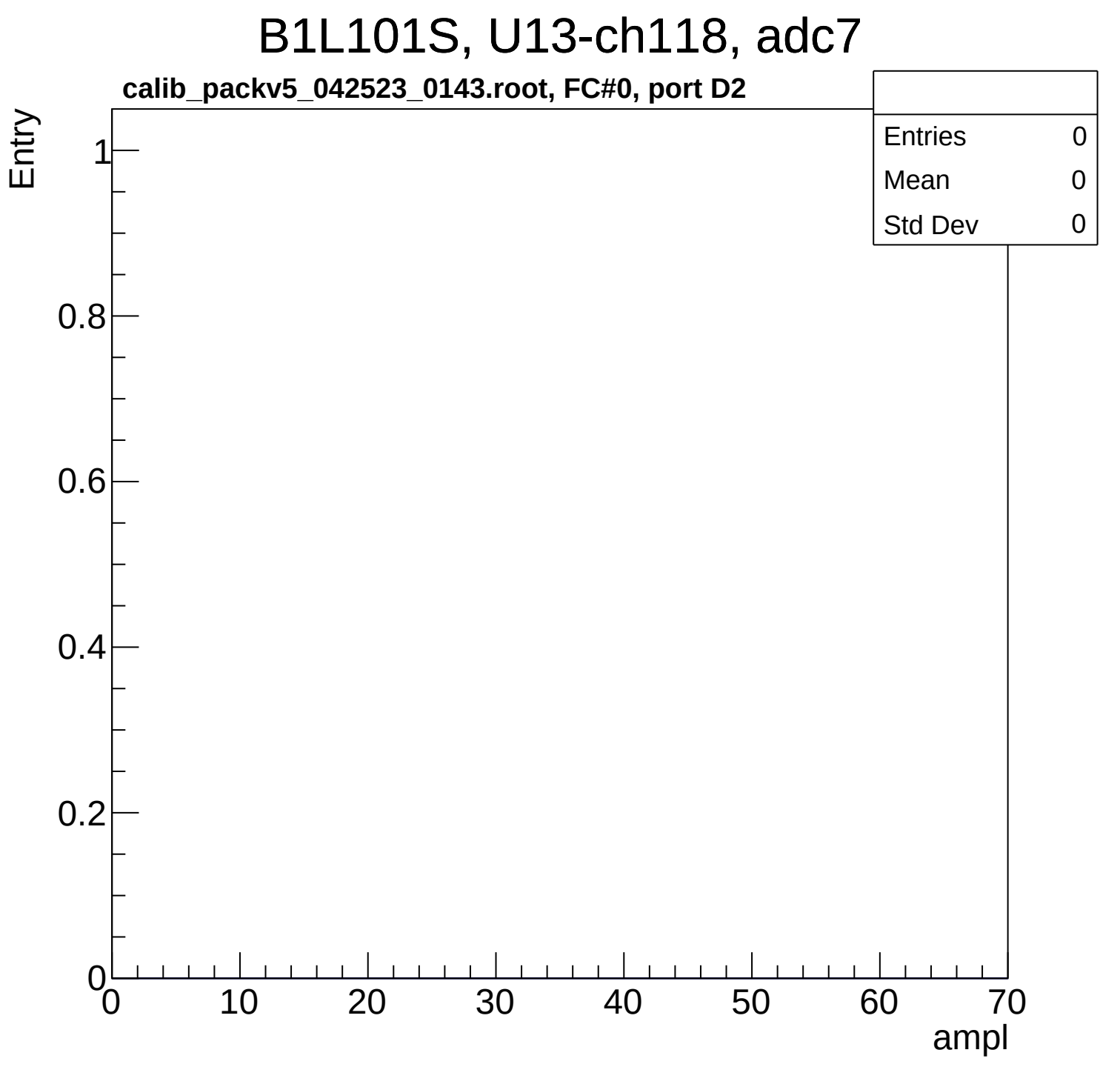
B1L101S, U13-ch118, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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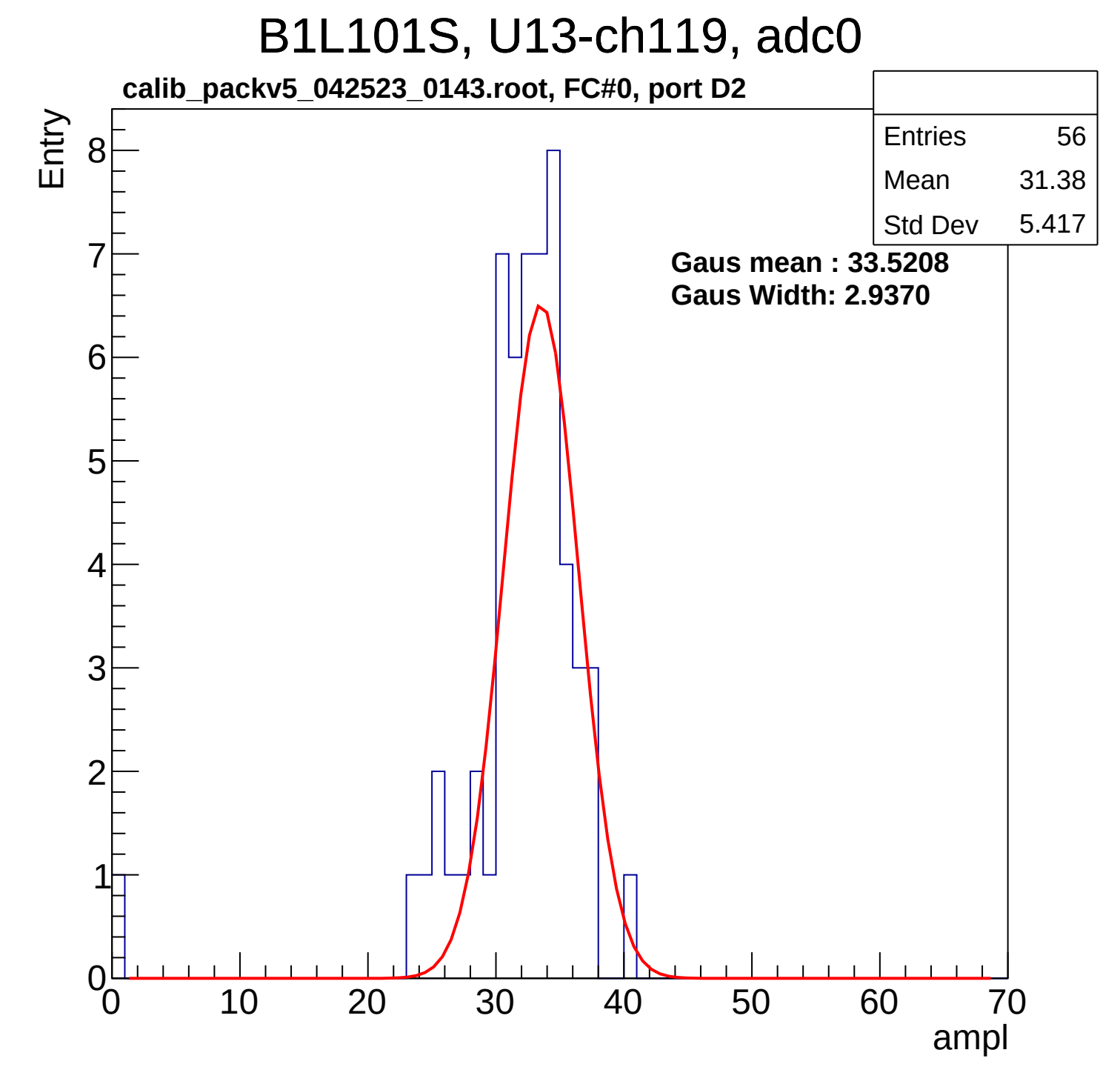
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch119, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 56
Mean 31.38
Std Dev 5.417
Gaus mean : 33.5208
Gaus Width: 2.9370
Entry
ampl
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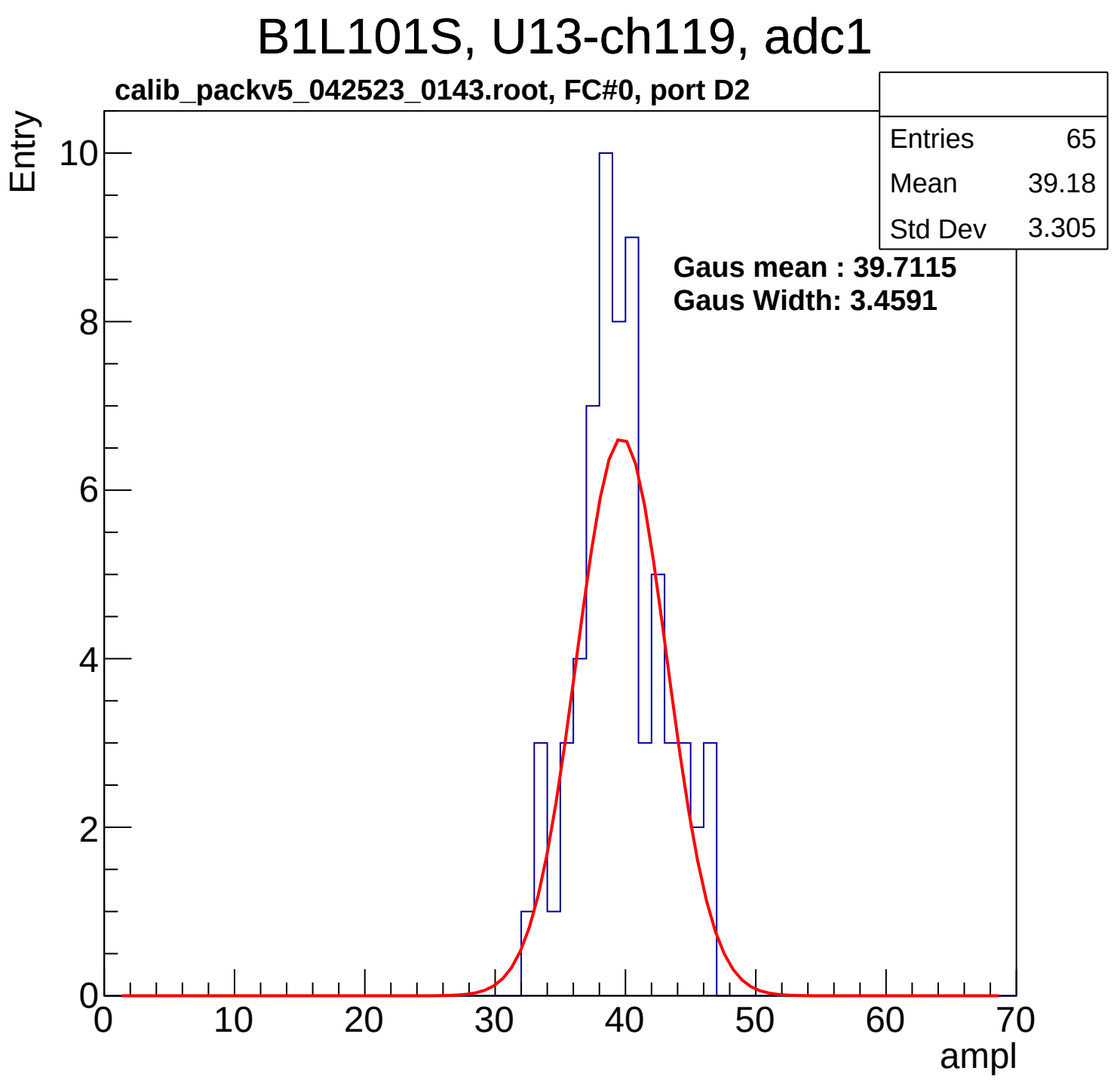
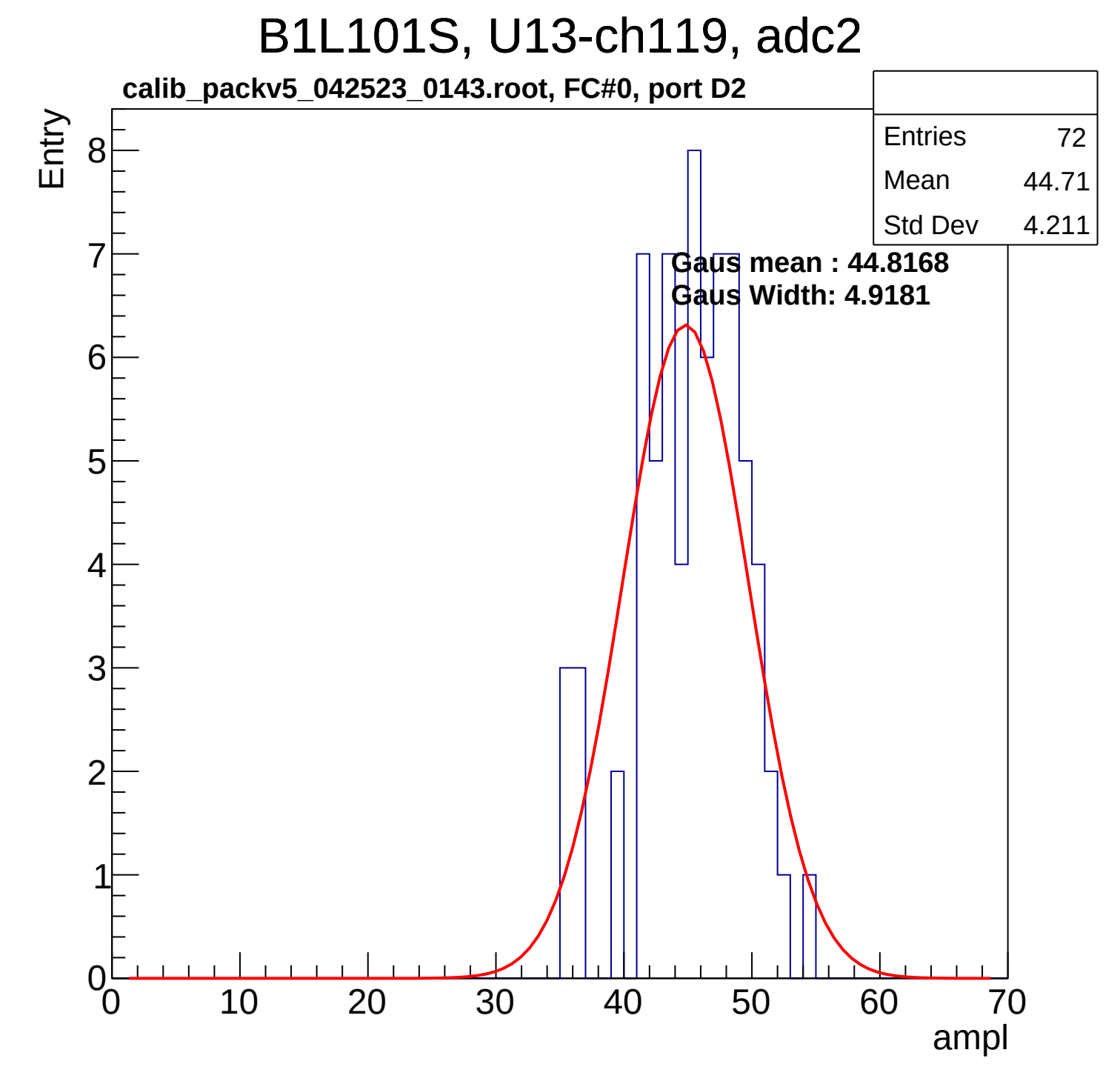
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch119, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 39.18
Std Dev 3.305
Gaus mean : 39.7115
Gaus Width: 3.4591
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch119, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 44.71
Std Dev 4.211
Gaus mean : 44.8168
Gaus Width: 4.9181
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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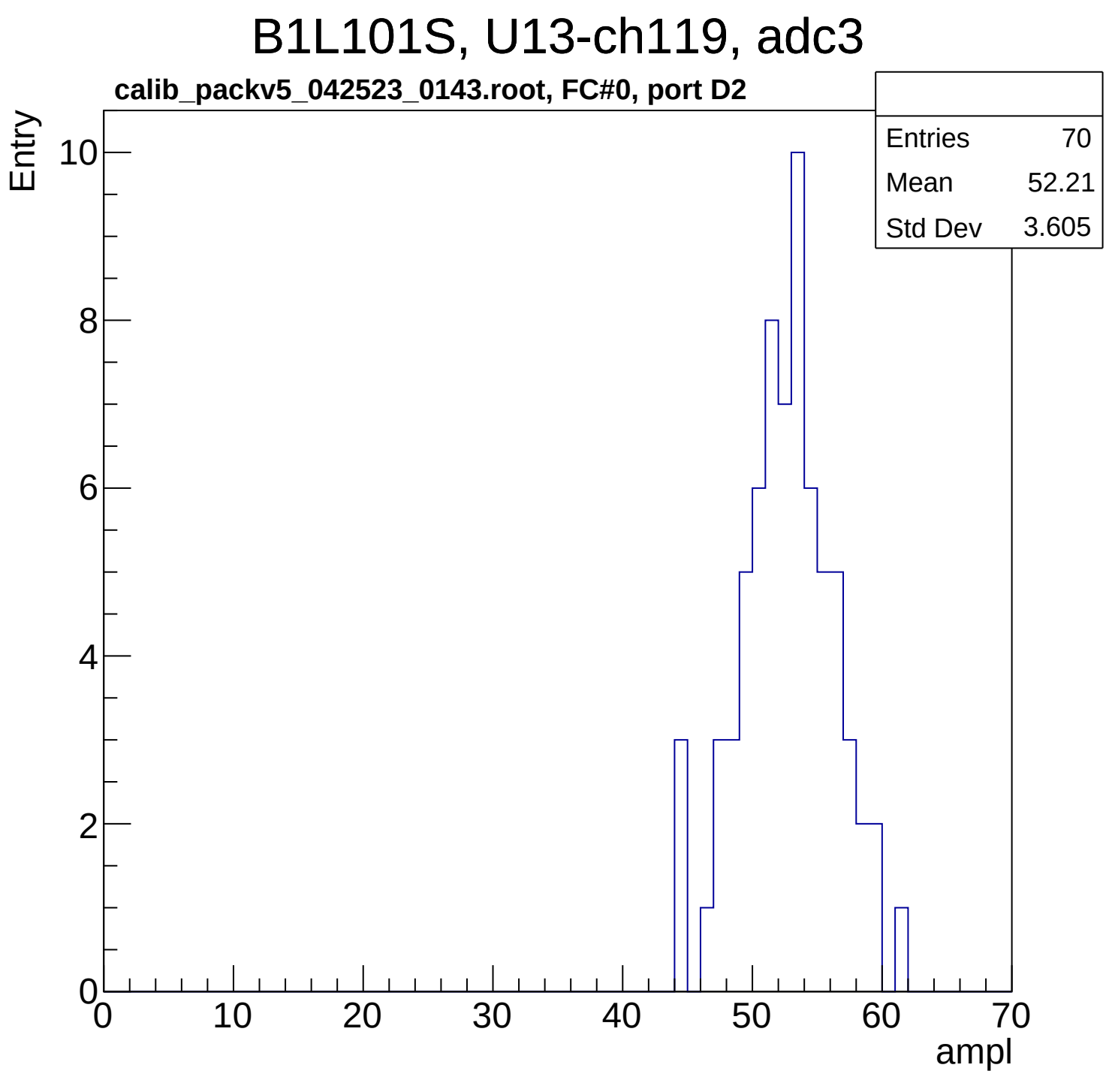
B1L101S, U13-ch119, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 52.21
Std Dev 3.605
Entry
ampl
0
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4
6
8
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0
10
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40
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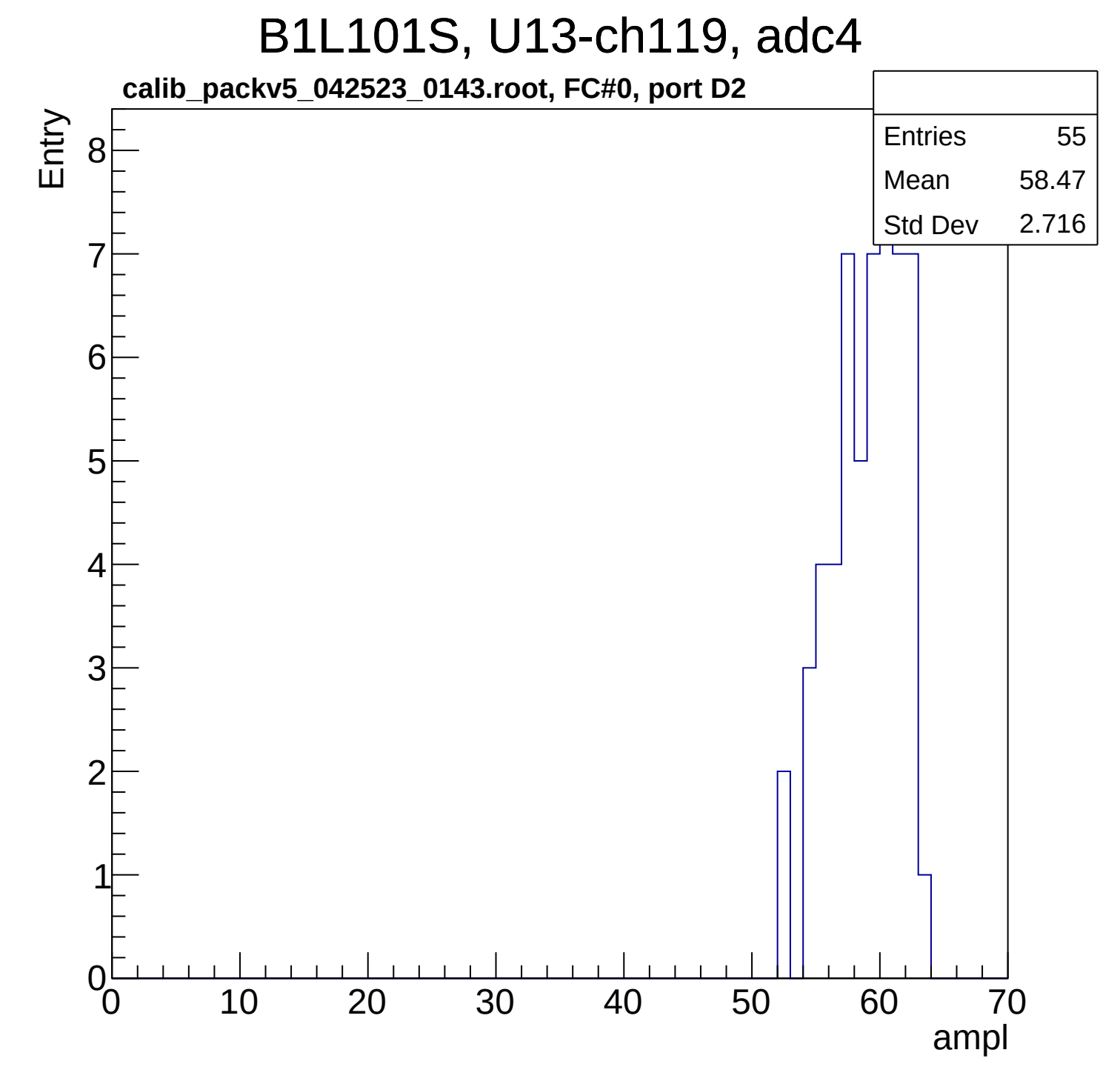
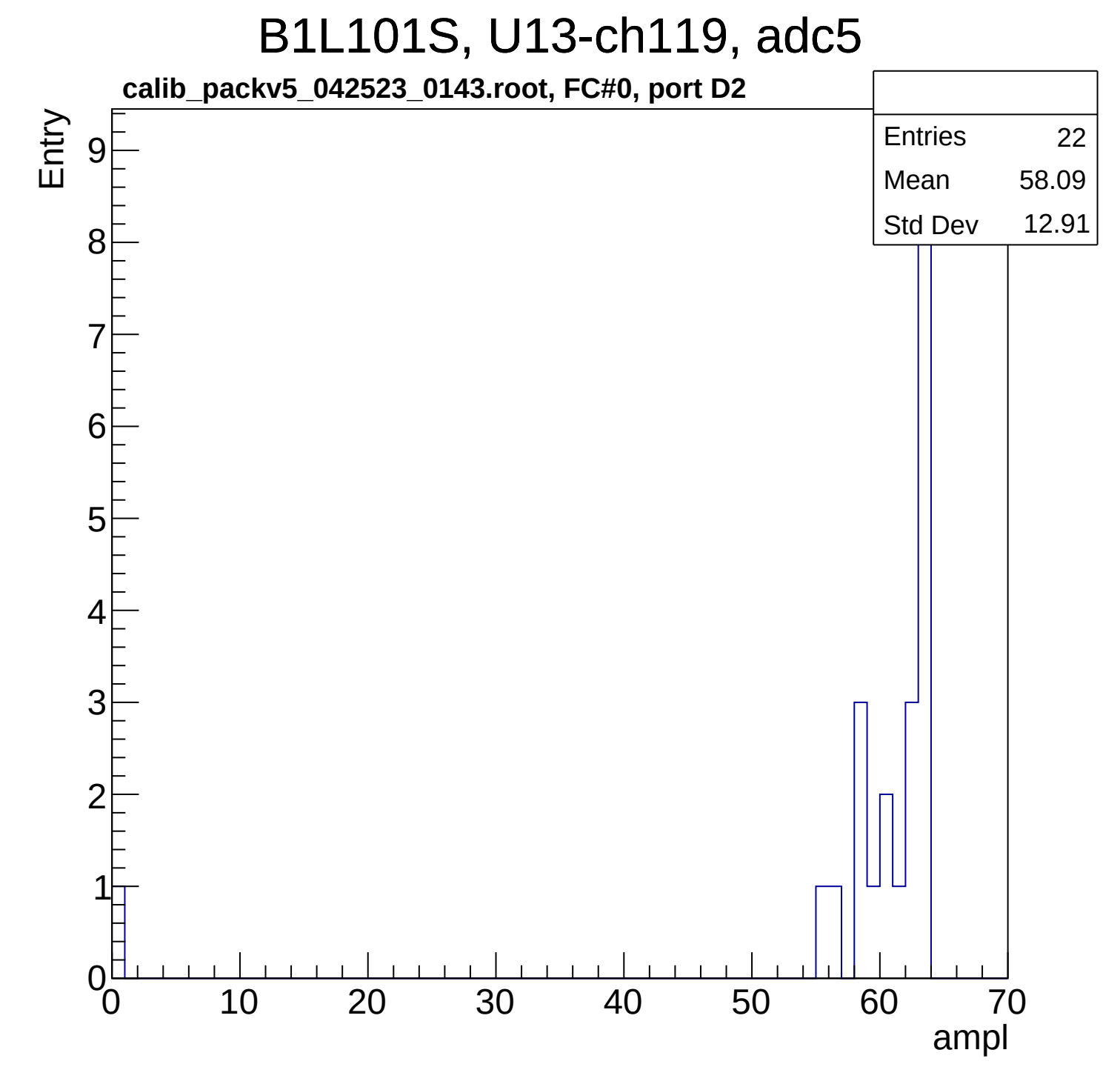
60
70


B1L101S, U13-ch119, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 55
Mean 58.47
Std Dev 2.716
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch119, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 22
Mean 58.09
Std Dev 12.91
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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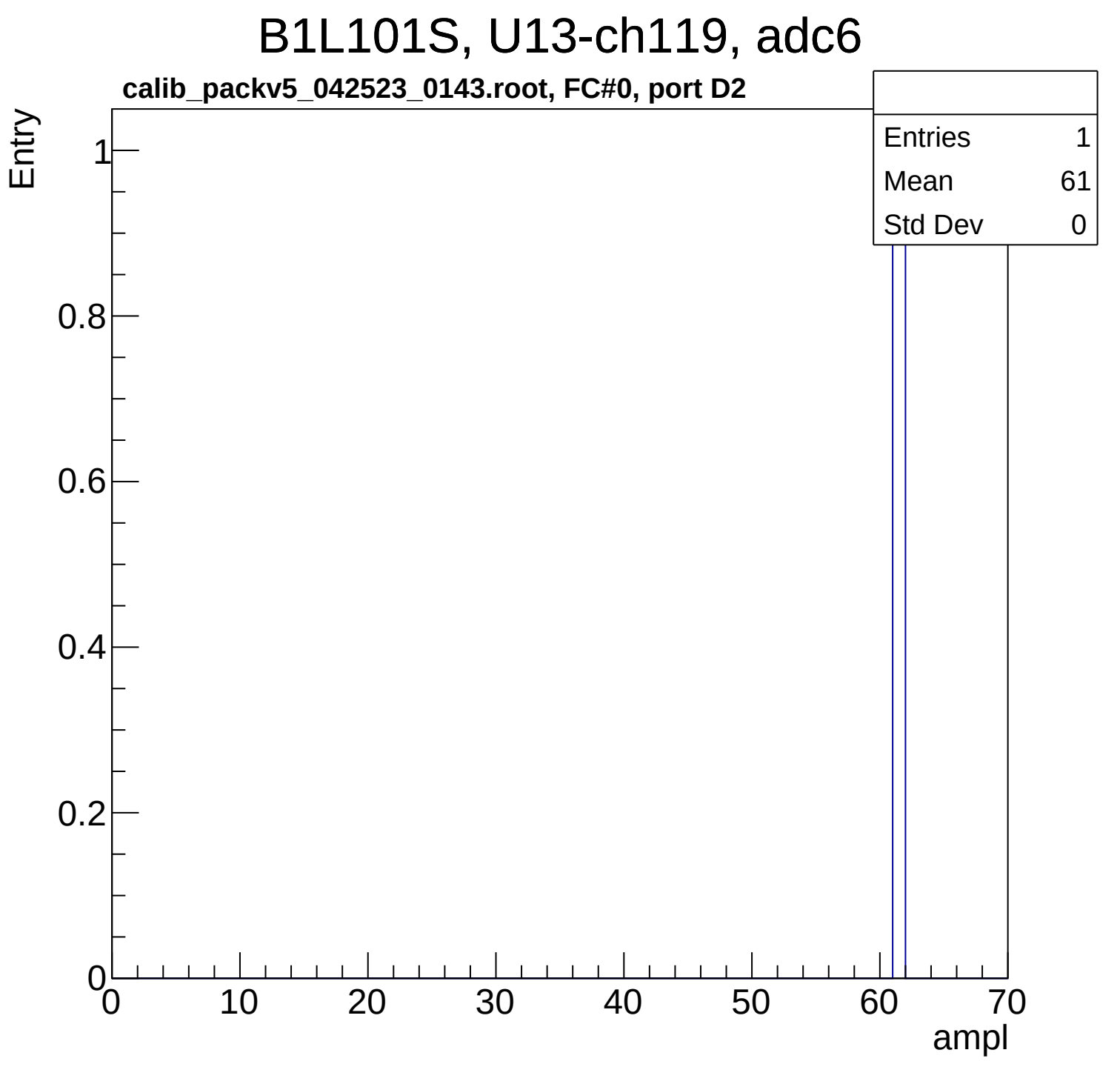
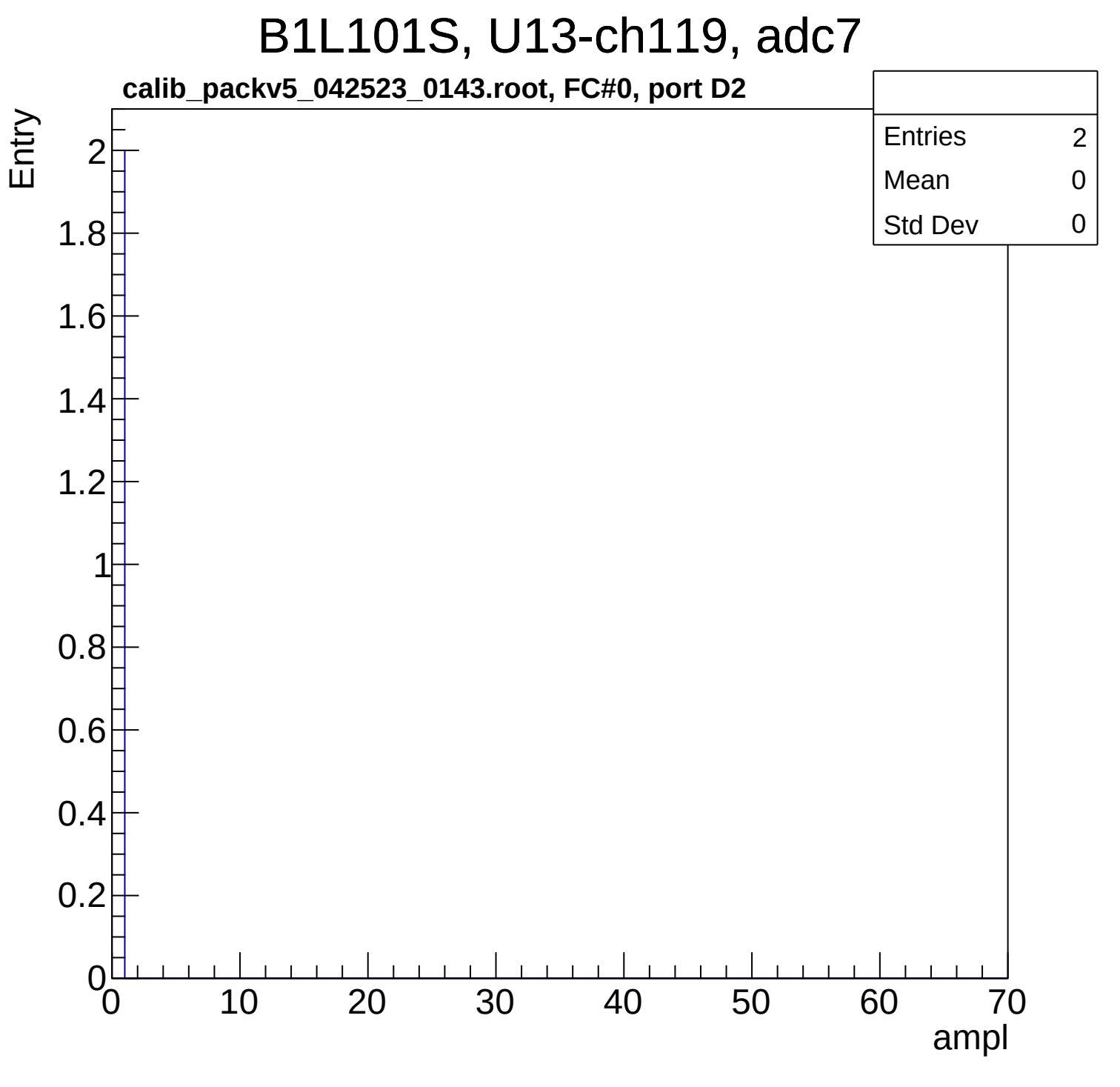

B1L101S, U13-ch119, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch119, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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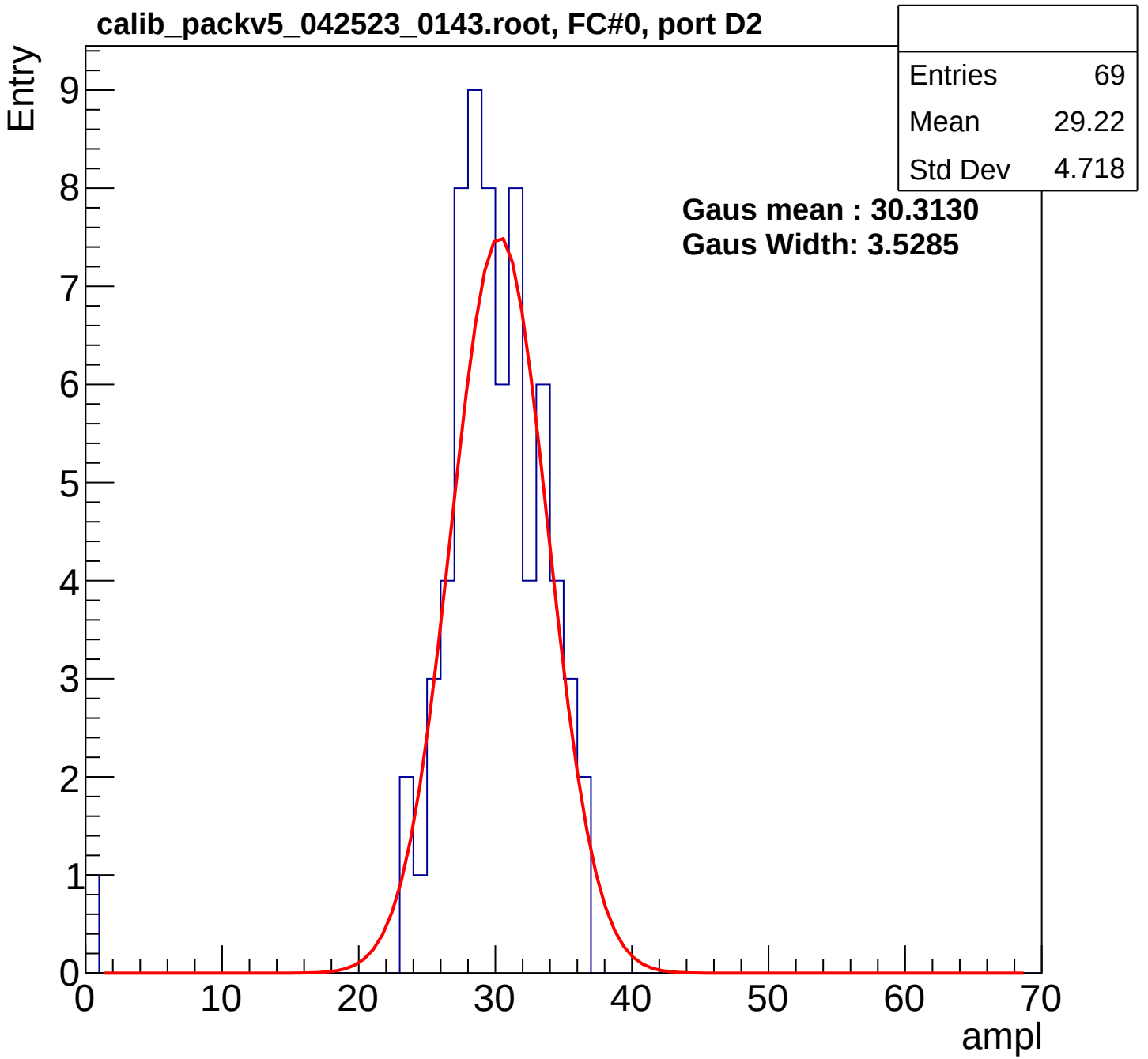
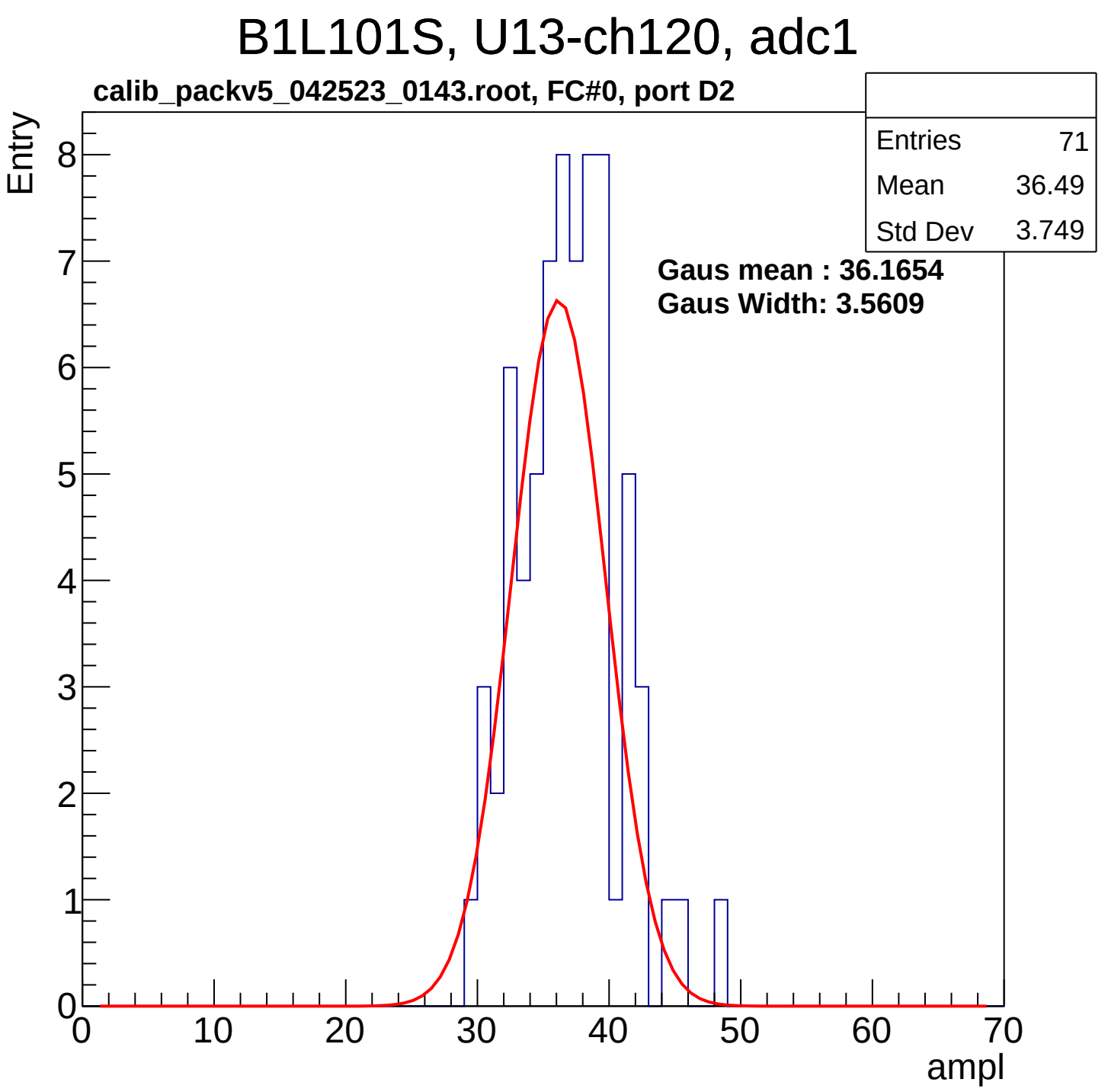
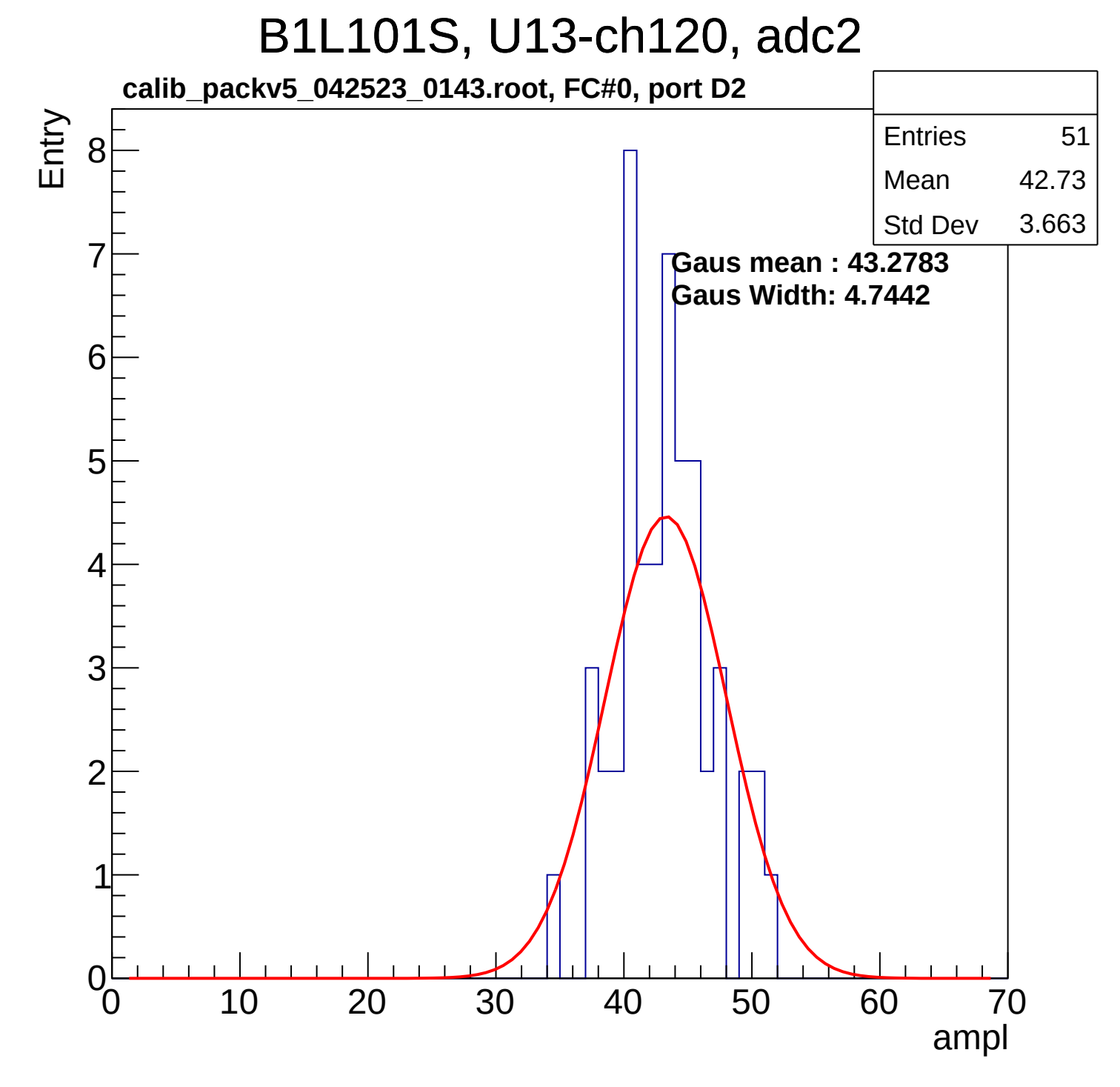
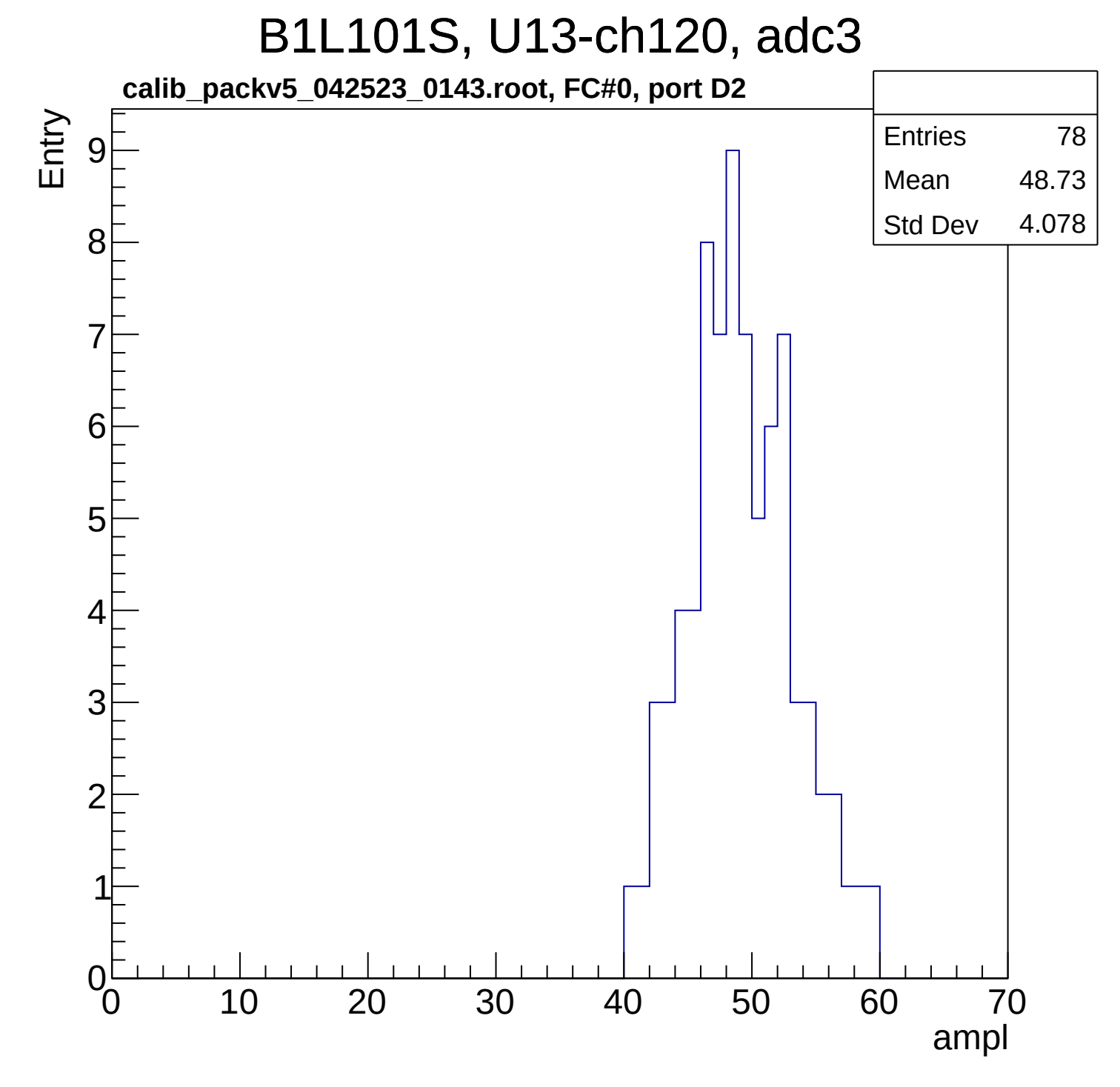
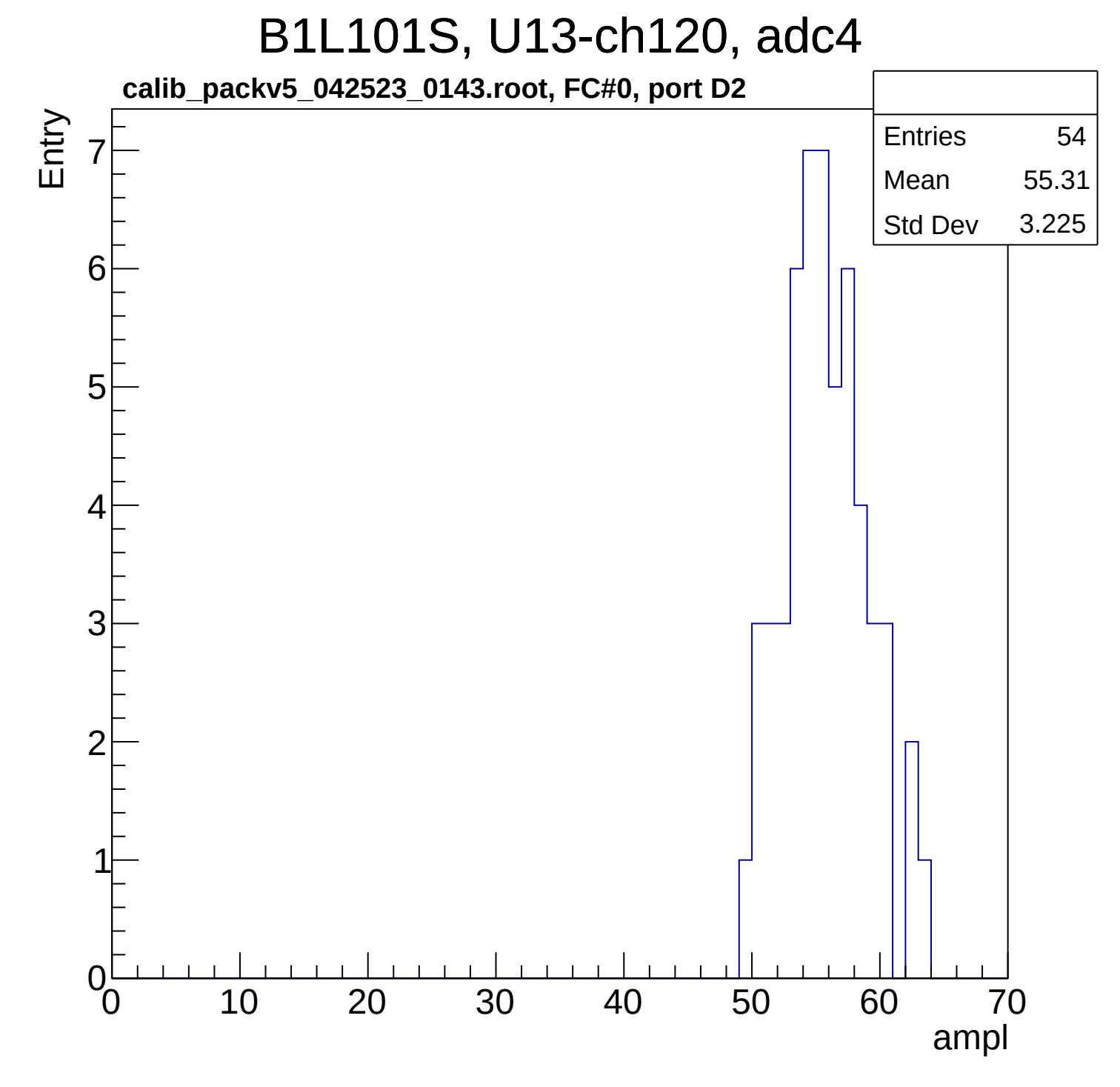
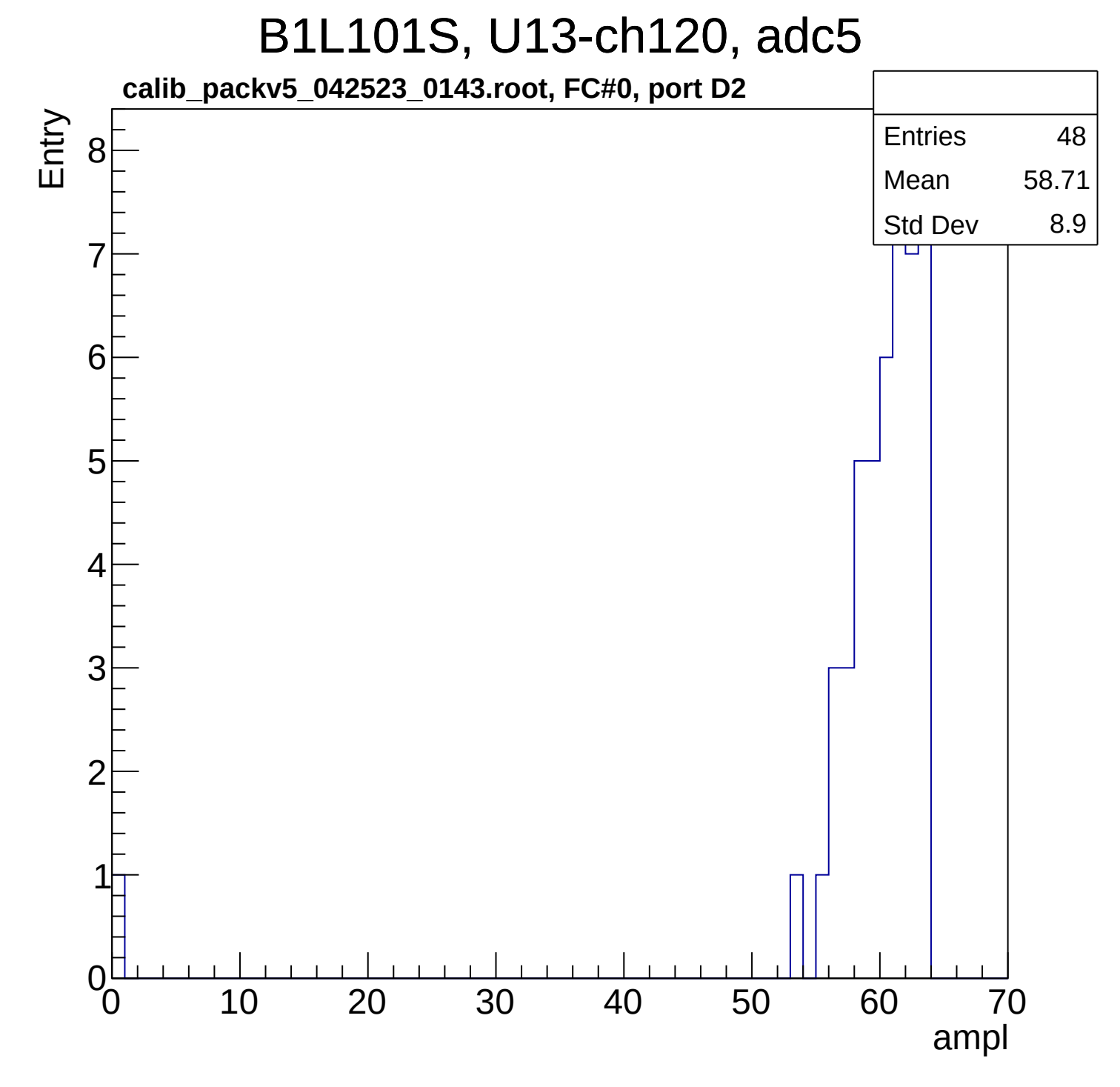

B1L101S, U13-ch120, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 29.22
Std Dev 4.718
Gaus mean : 30.3130
Gaus Width: 3.5285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

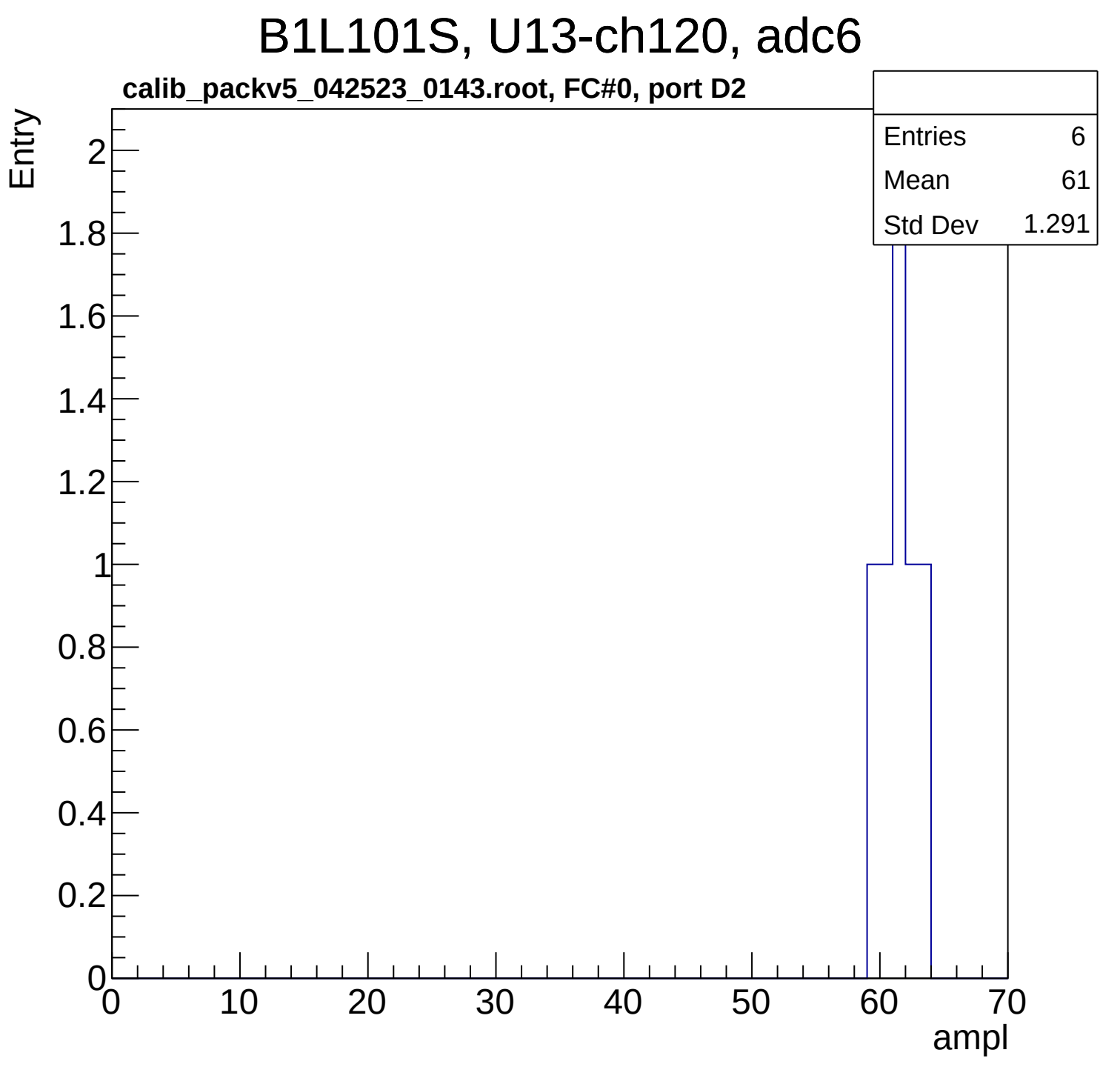

B1L101S, U13-ch120, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 71
Mean 36.49
Std Dev 3.749
Gaus mean : 36.1654
Gaus Width: 3.5609
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

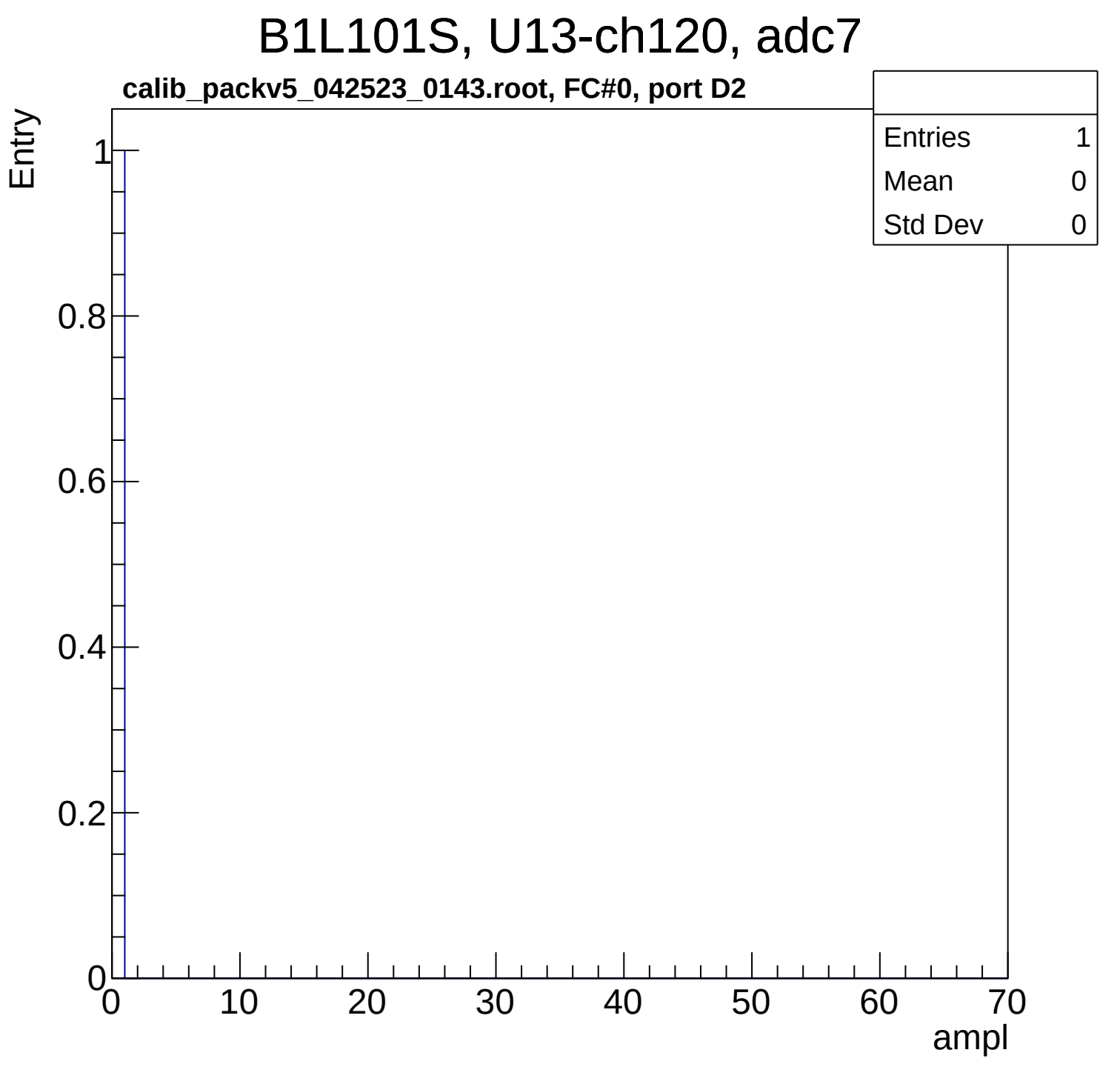

B1L101S, U13-ch120, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 42.73
Std Dev 3.663
Gaus mean : 43.2783
Gaus Width: 4.7442
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

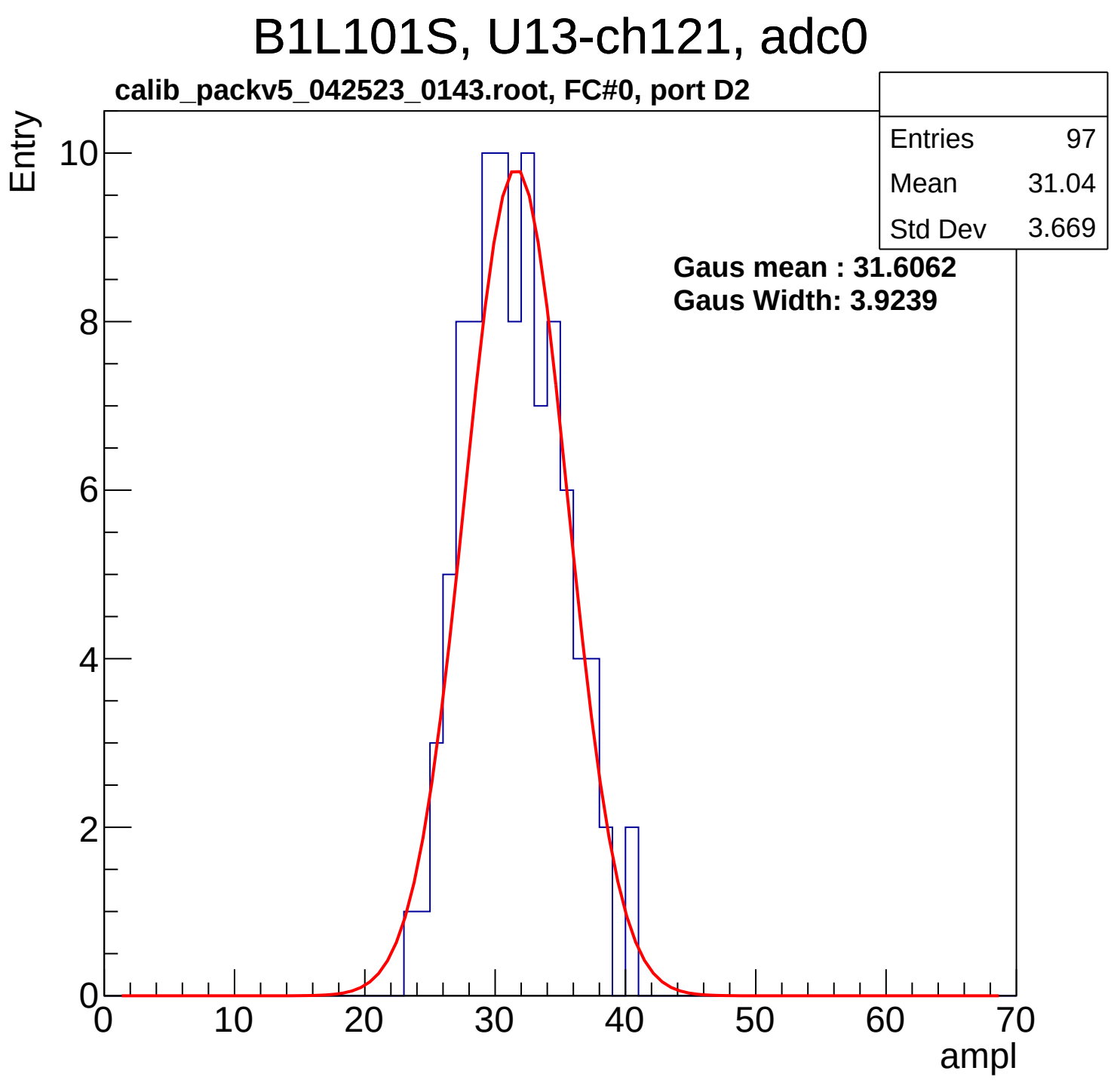

B1L101S, U13-ch120, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 48.73
Std Dev 4.078
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

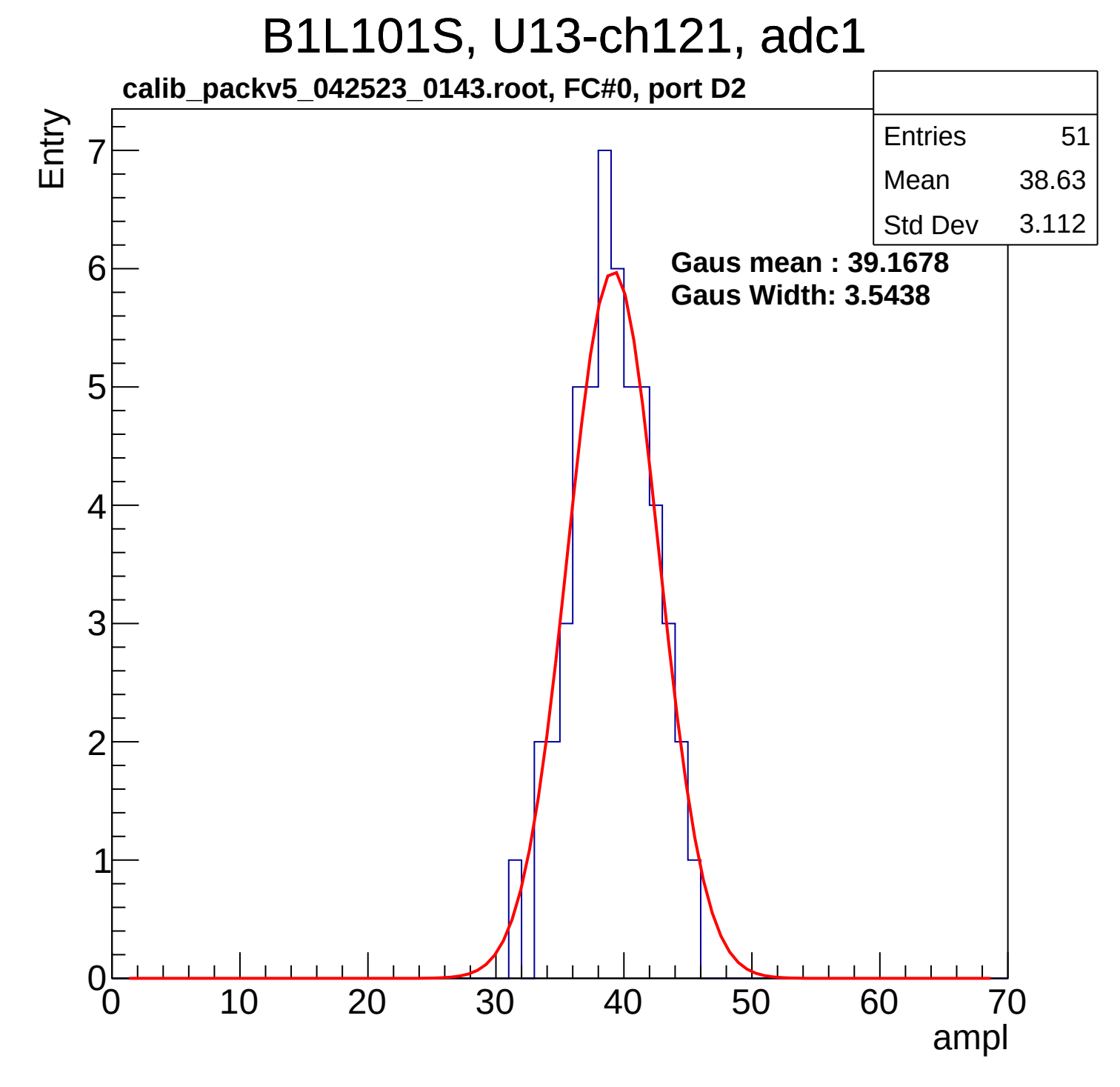

B1L101S, U13-ch120, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 55.31
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

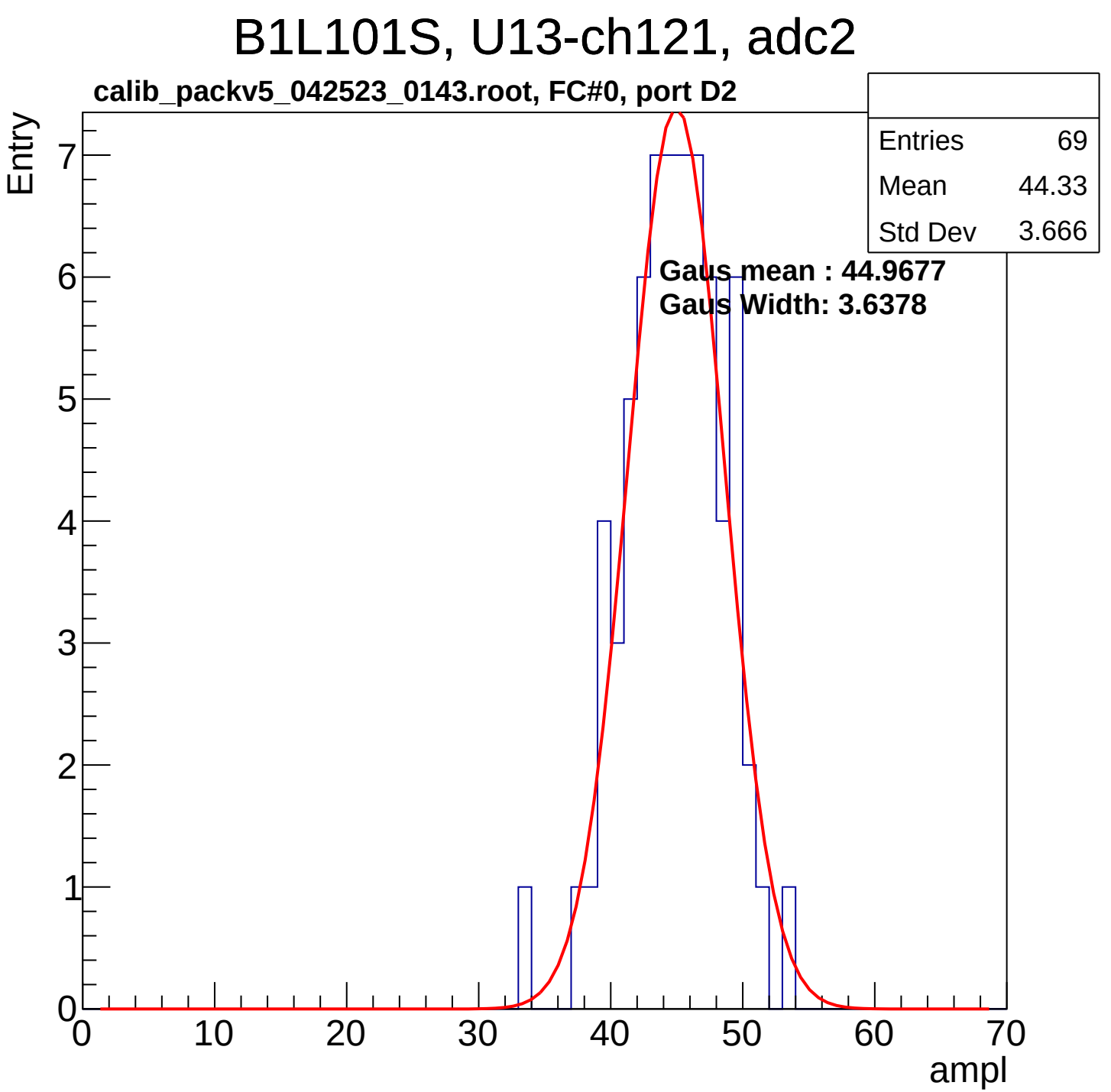

B1L101S, U13-ch120, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 48
Mean 58.71
Std Dev 8.9
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch120, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 6
Mean 61
Std Dev 1.291
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch120, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch121, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 97
Mean 31.04
Std Dev 3.669
Gaus mean : 31.6062
Gaus Width: 3.9239
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch121, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 51
Mean 38.63
Std Dev 3.112
Gaus mean : 39.1678
Gaus Width: 3.5438
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch121, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 44.33
Std Dev 3.666
Gaus mean : 44.9677
Gaus Width: 3.6378
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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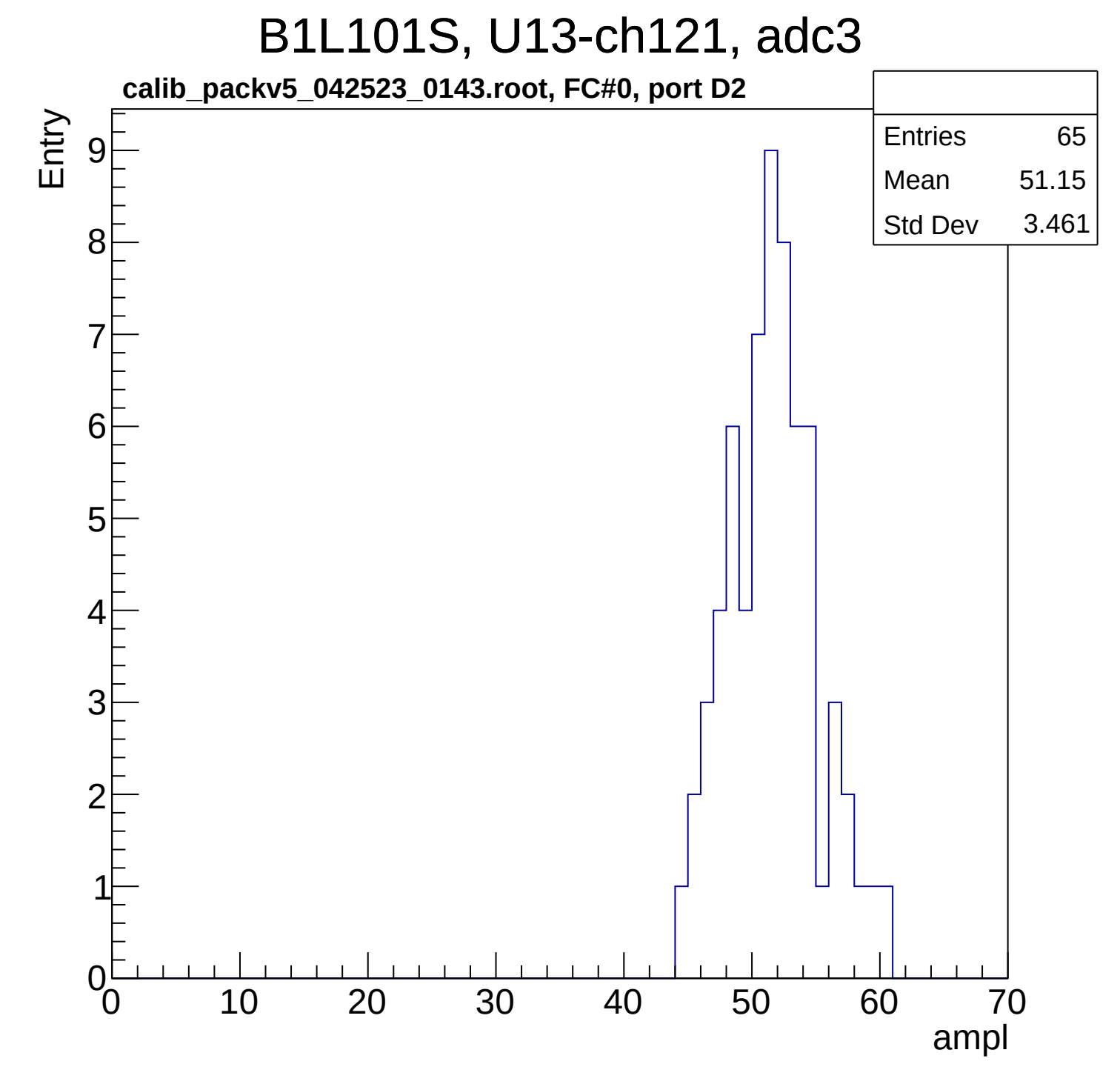
B1L101S, U13-ch121, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 51.15
Std Dev 3.461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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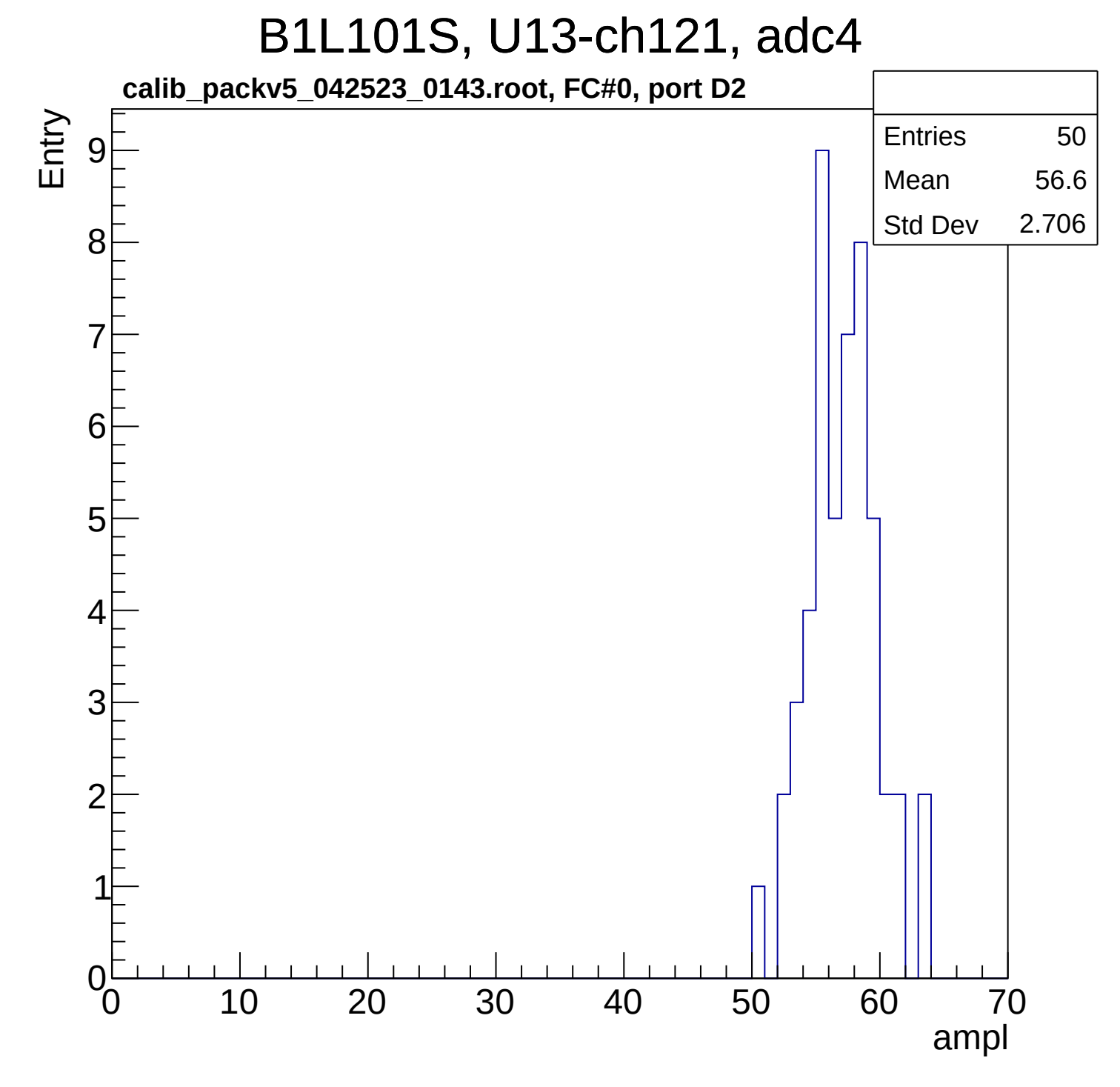
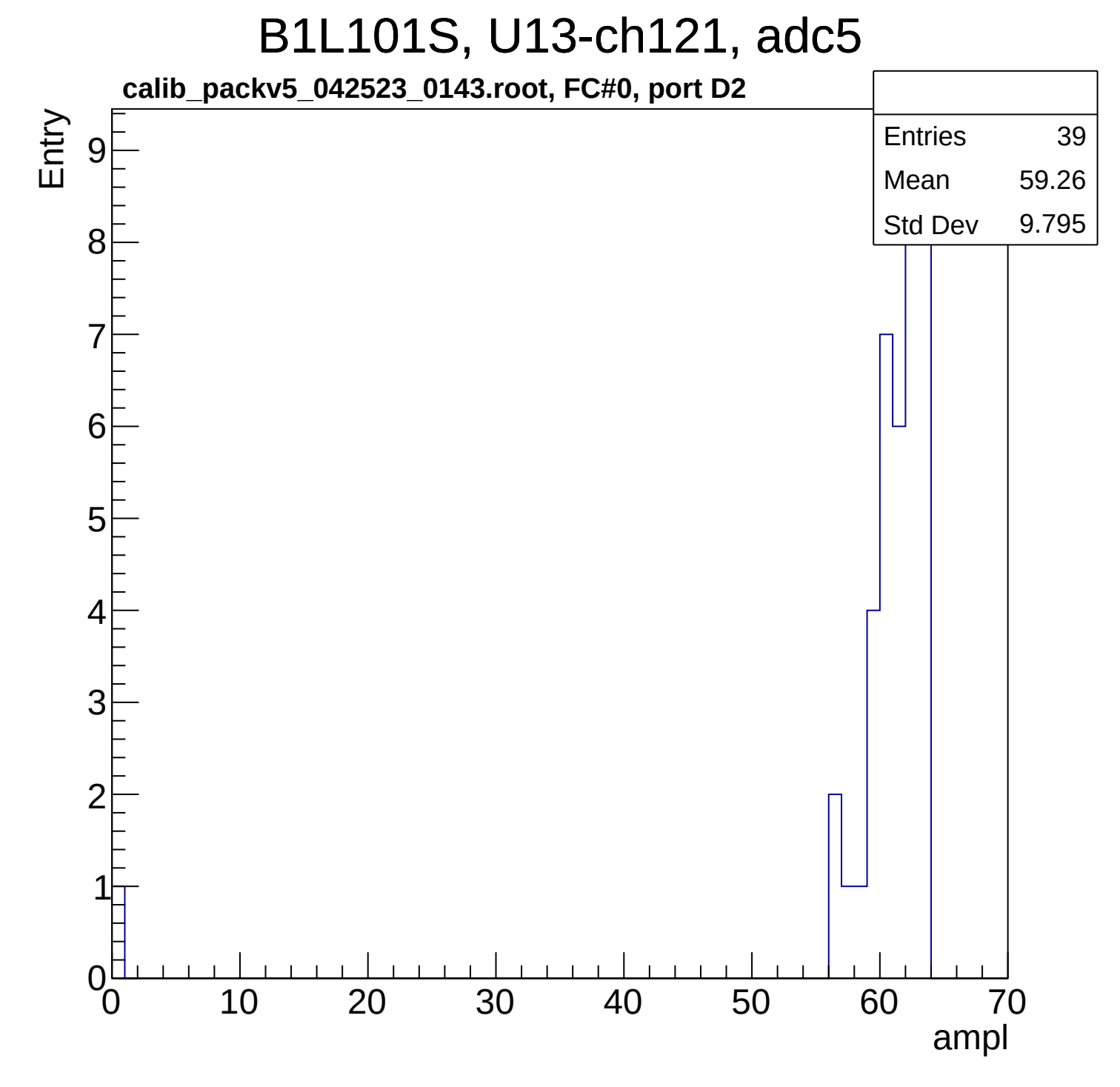
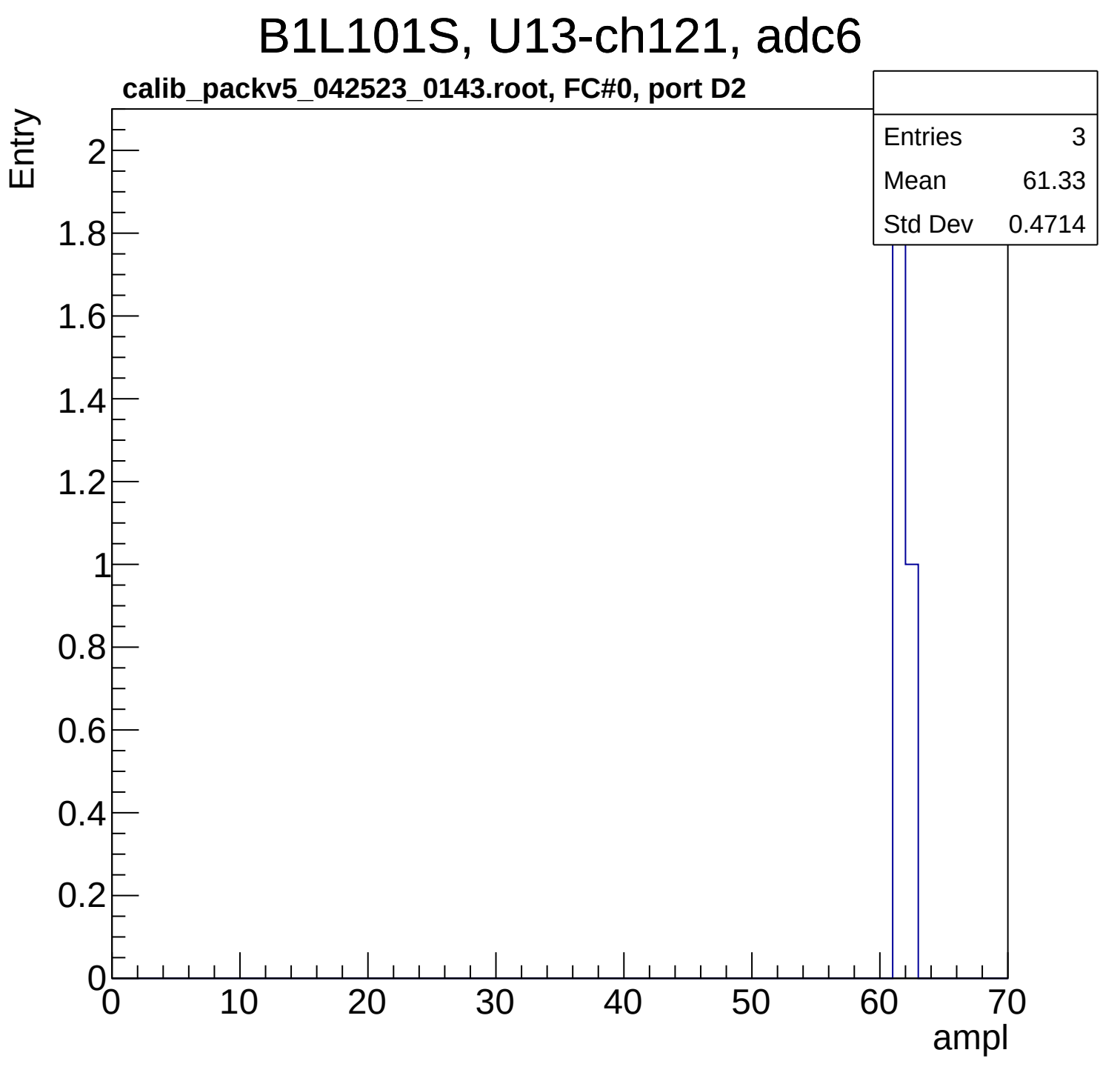
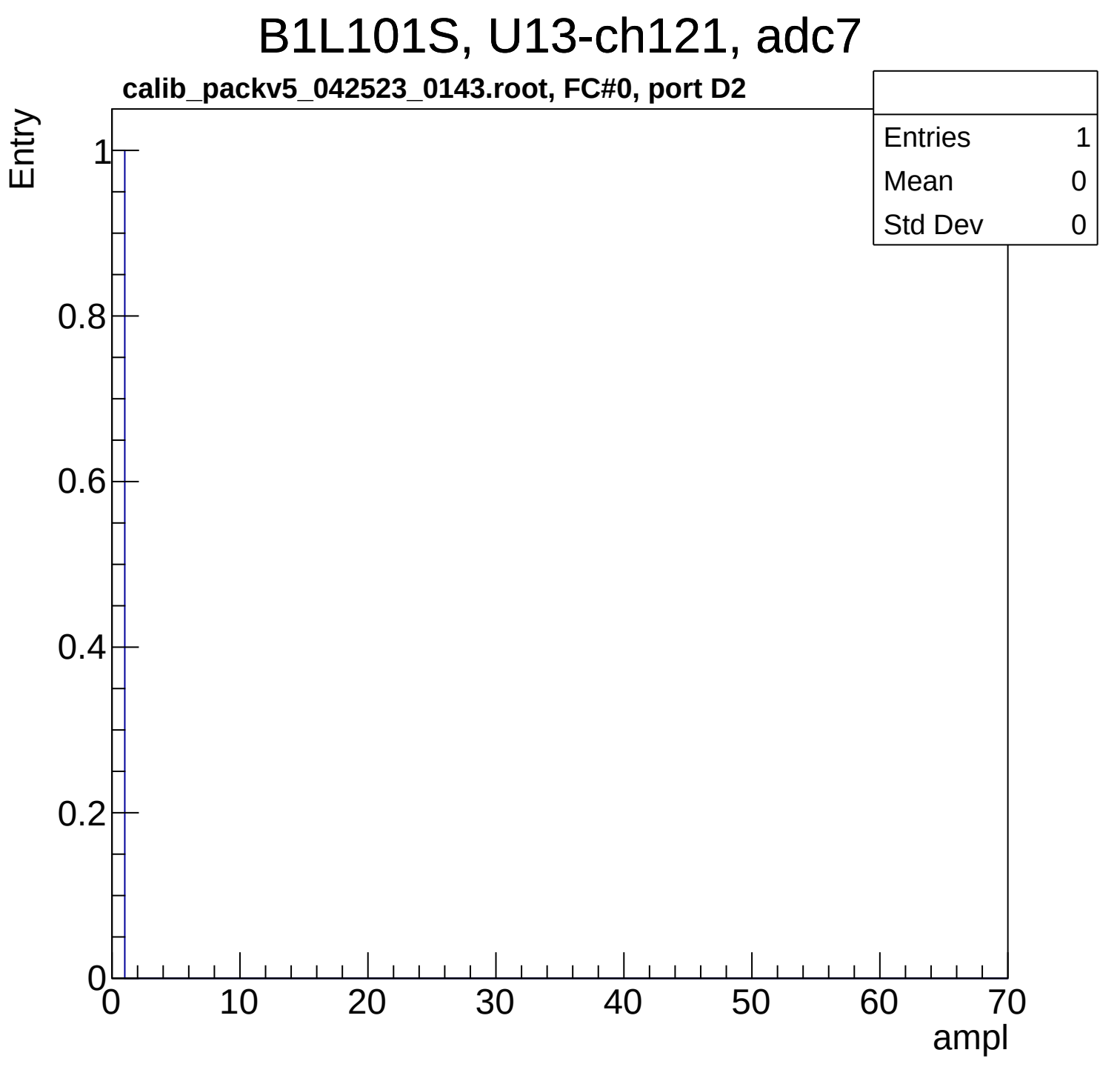
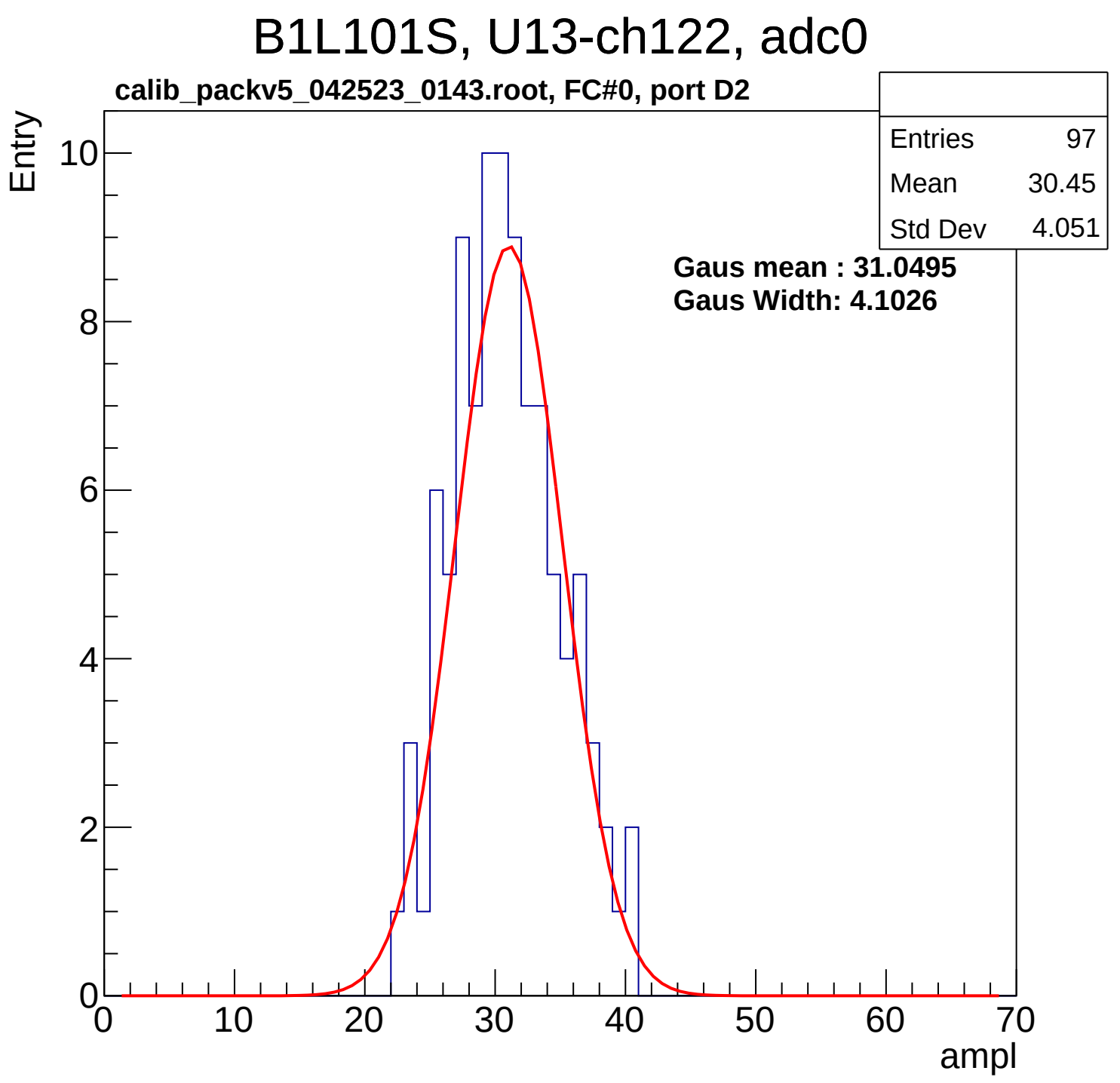
40
50
60
70

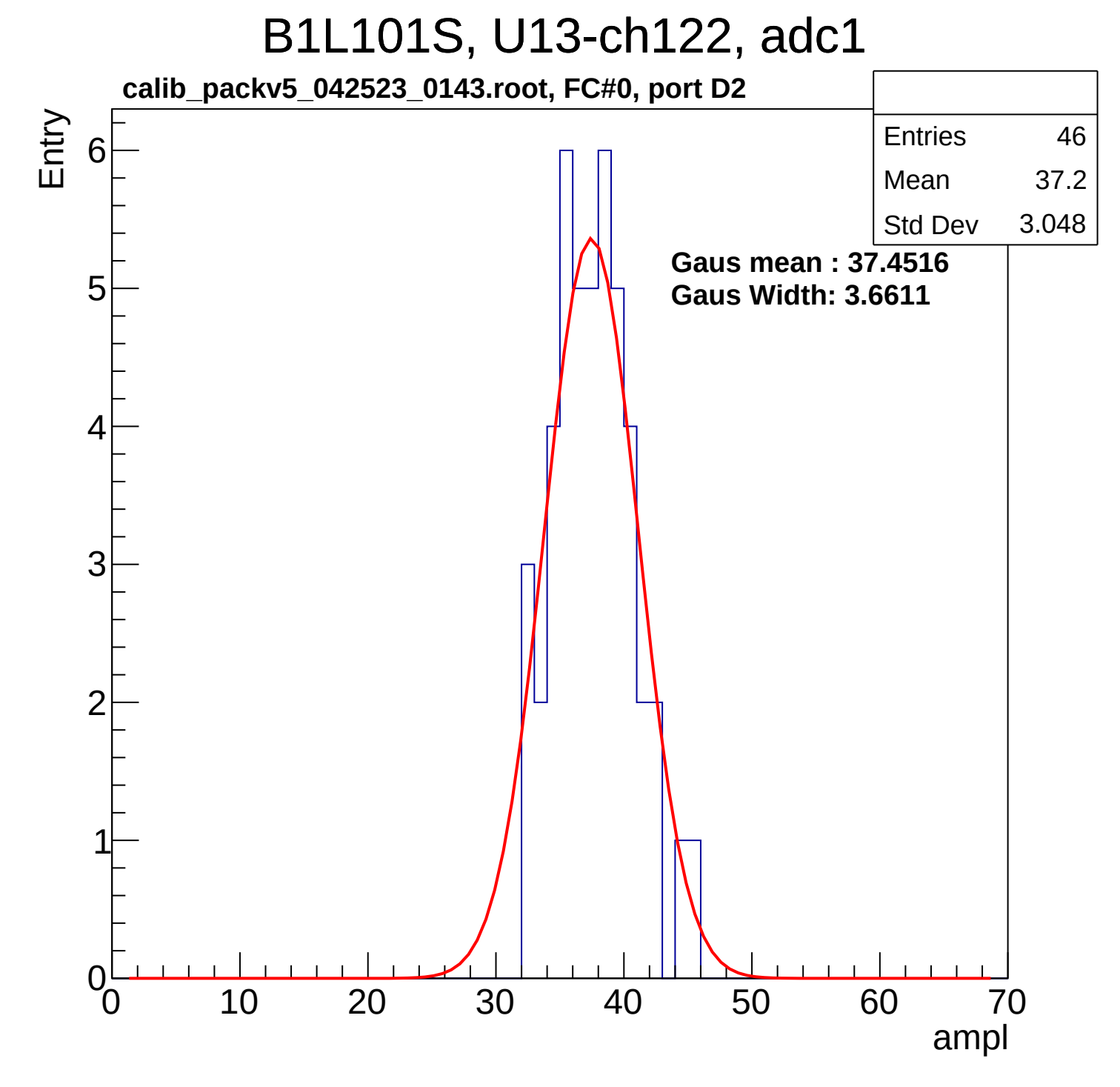

B1L101S, U13-ch121, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 50
Mean 56.6
Std Dev 2.706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

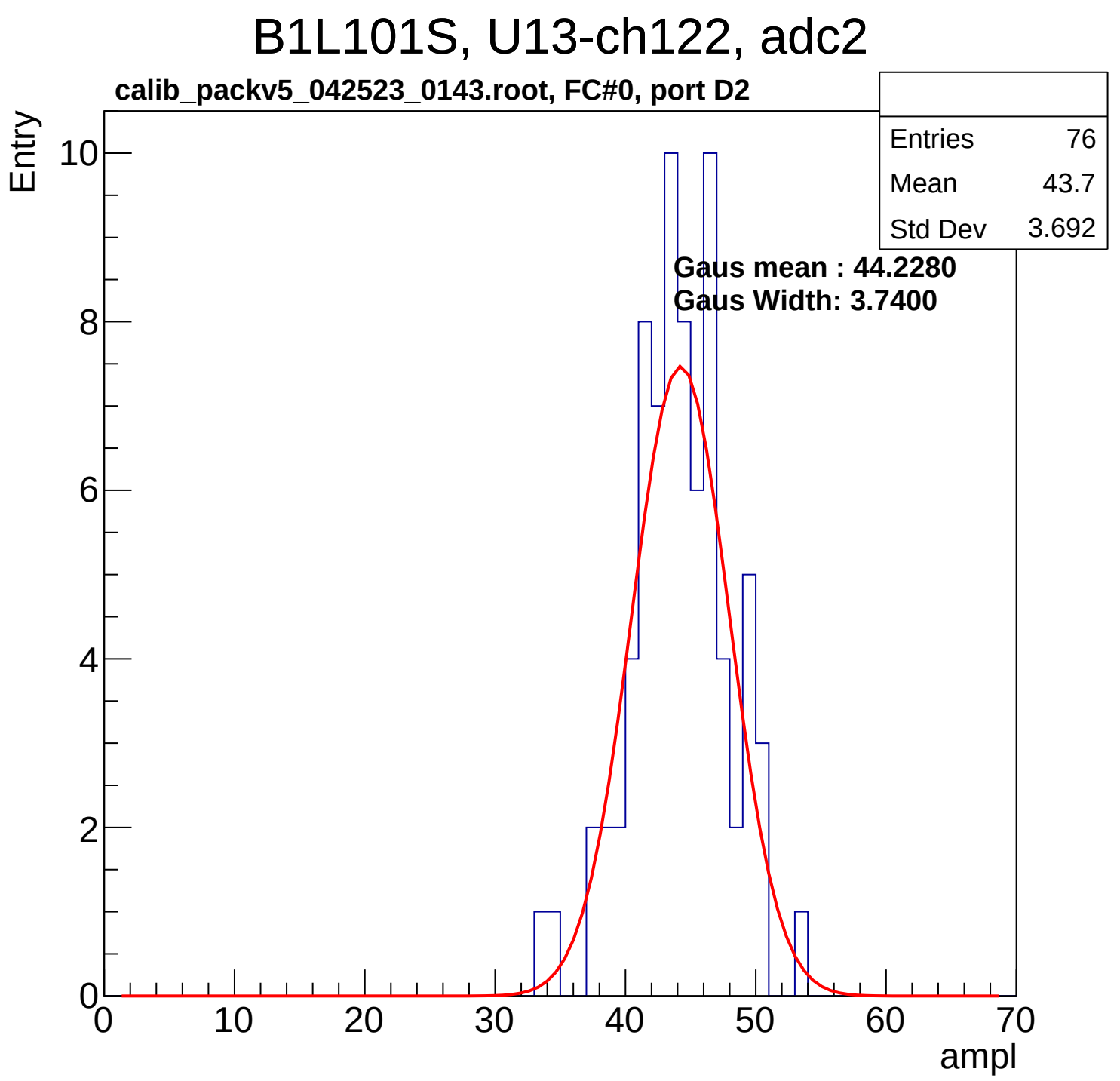

B1L101S, U13-ch121, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 39
Mean 59.26
Std Dev 9.795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

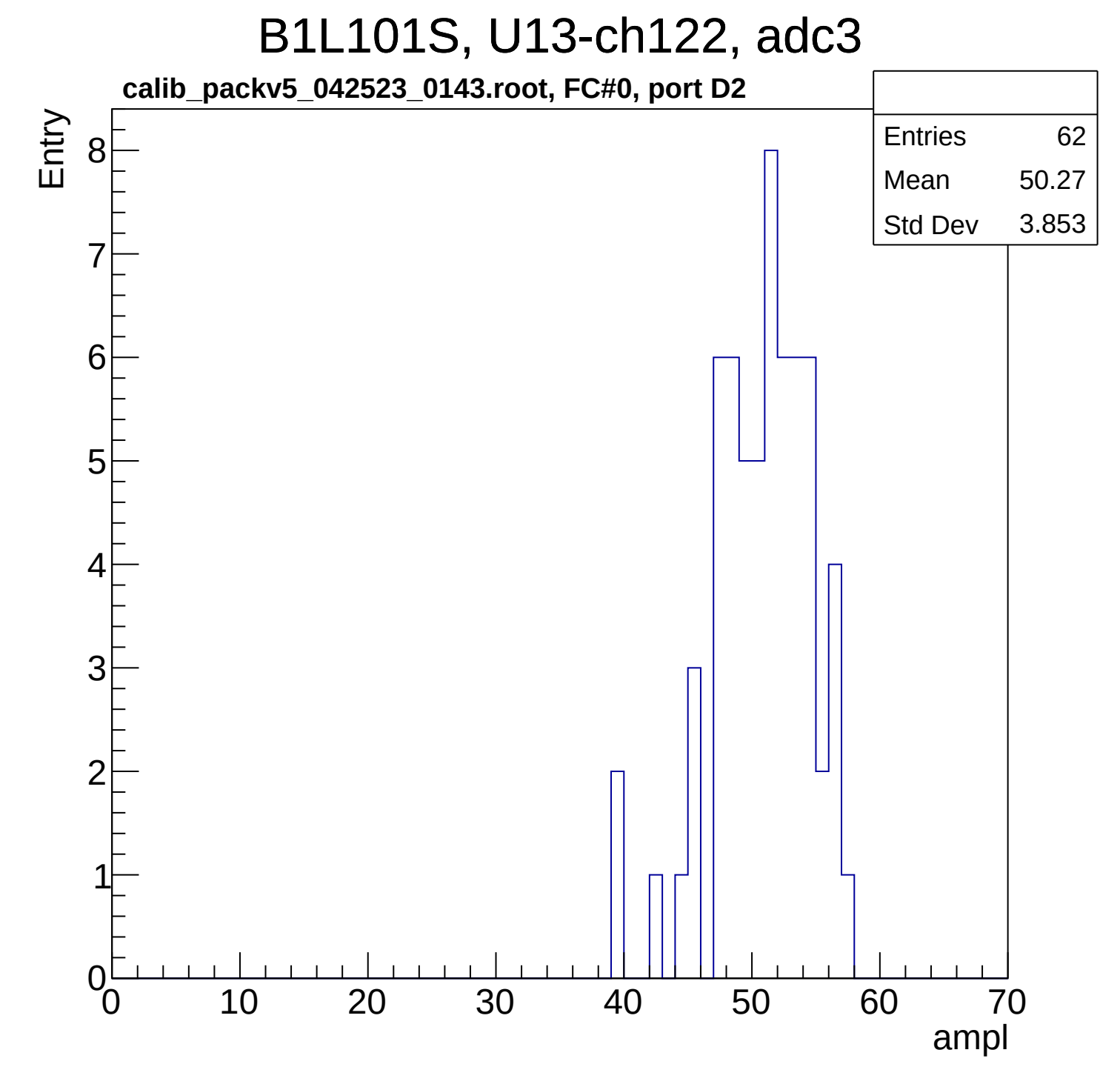

B1L101S, U13-ch121, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L101S, U13-ch121, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch122, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 97
Mean 30.45
Std Dev 4.051
Gaus mean : 31.0495
Gaus Width: 4.1026
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch122, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 37.2
Std Dev 3.048
Gaus mean : 37.4516
Gaus Width: 3.6611
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch122, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 43.7
Std Dev 3.692
Gaus mean : 44.2280
Gaus Width: 3.7400
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch122, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 50.27
Std Dev 3.853
Entry
ampl
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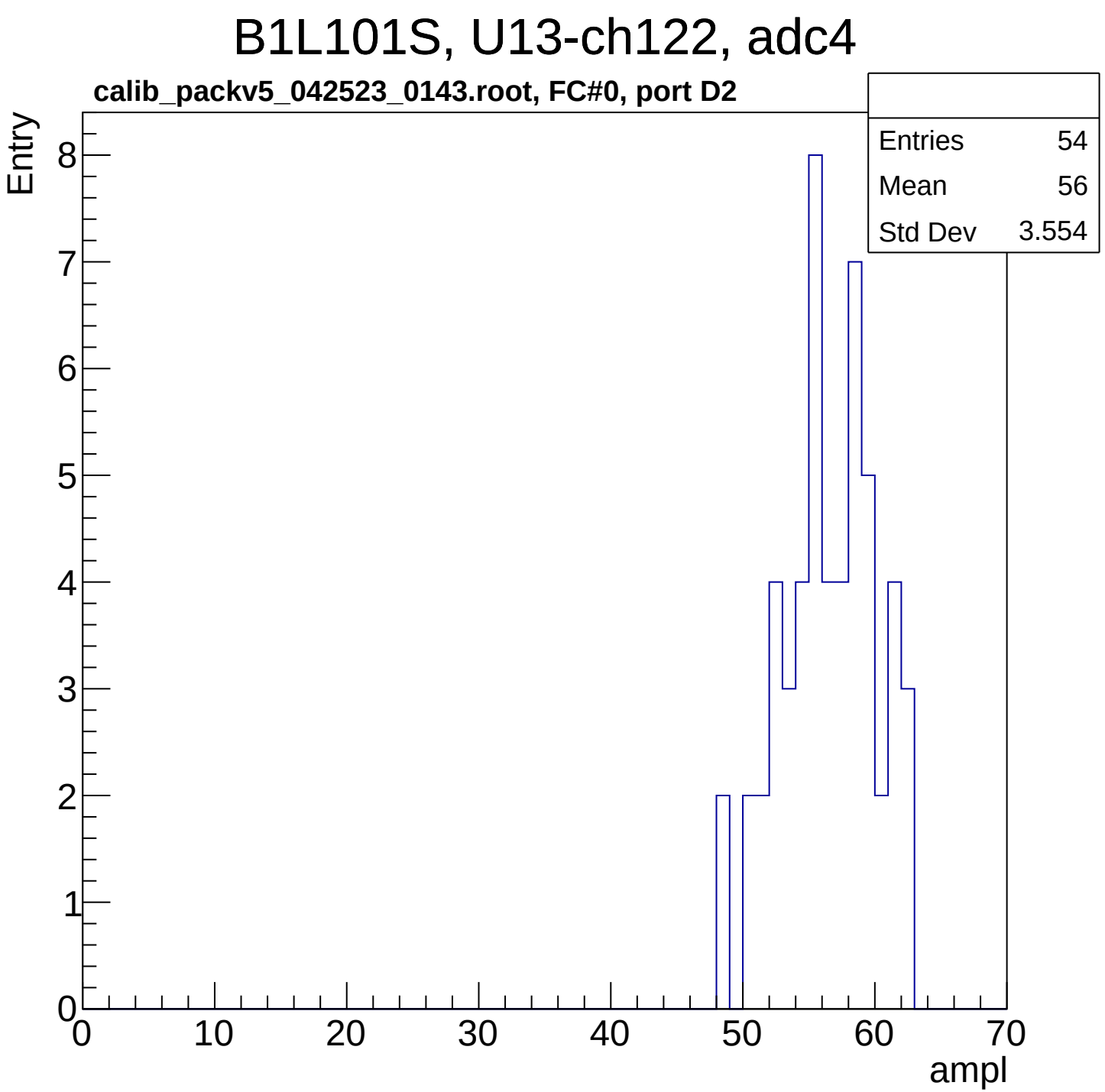
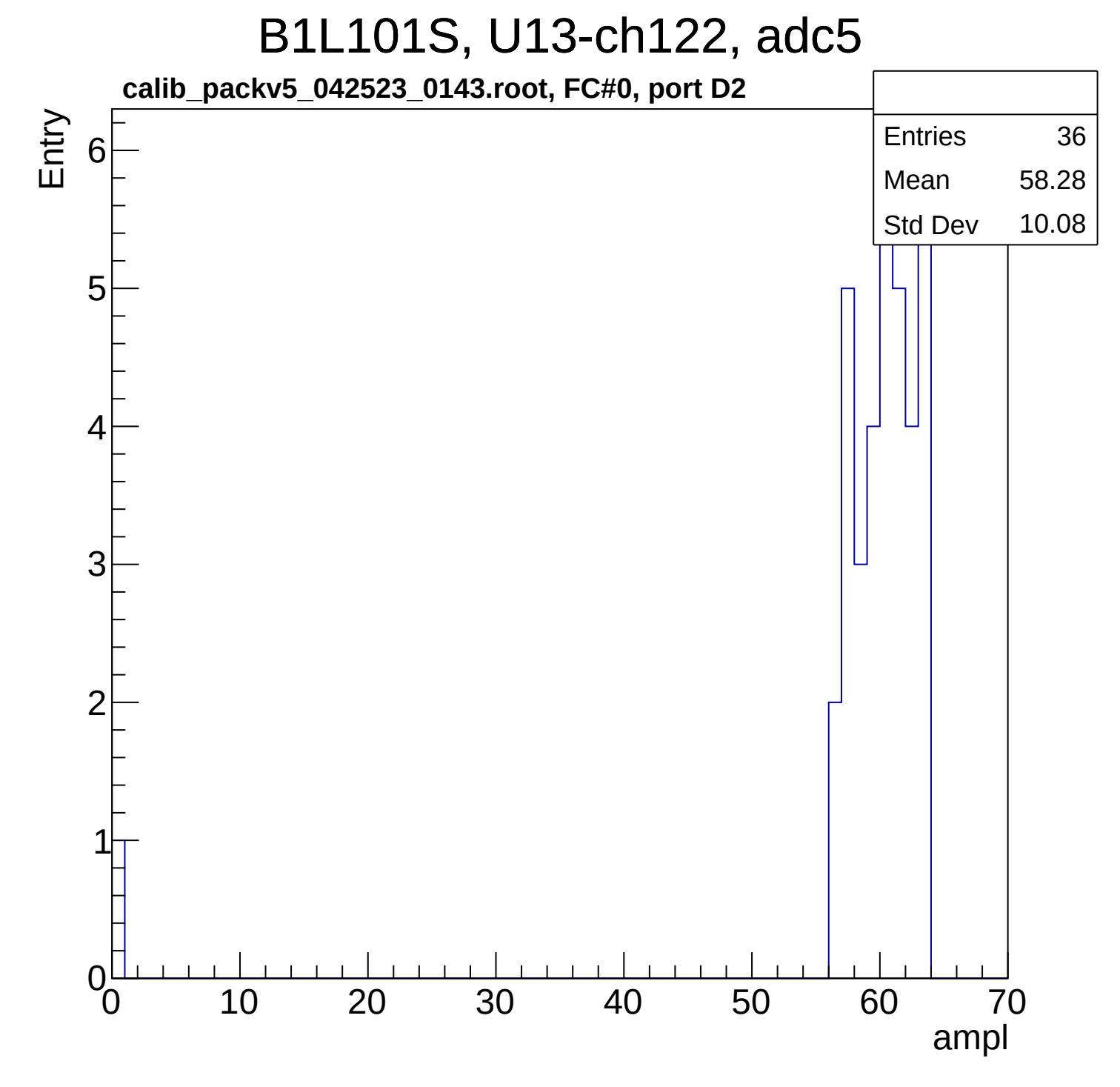
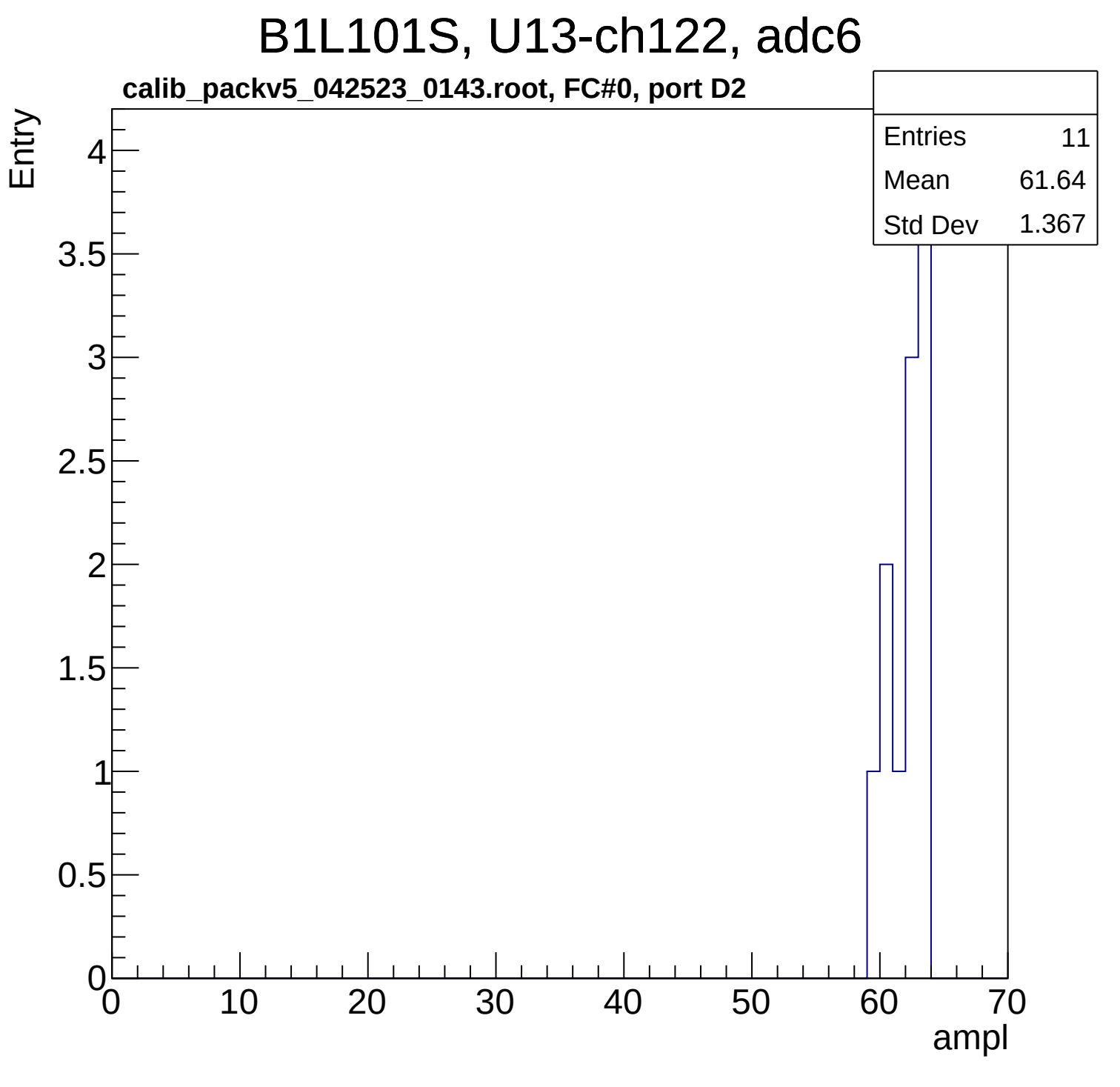
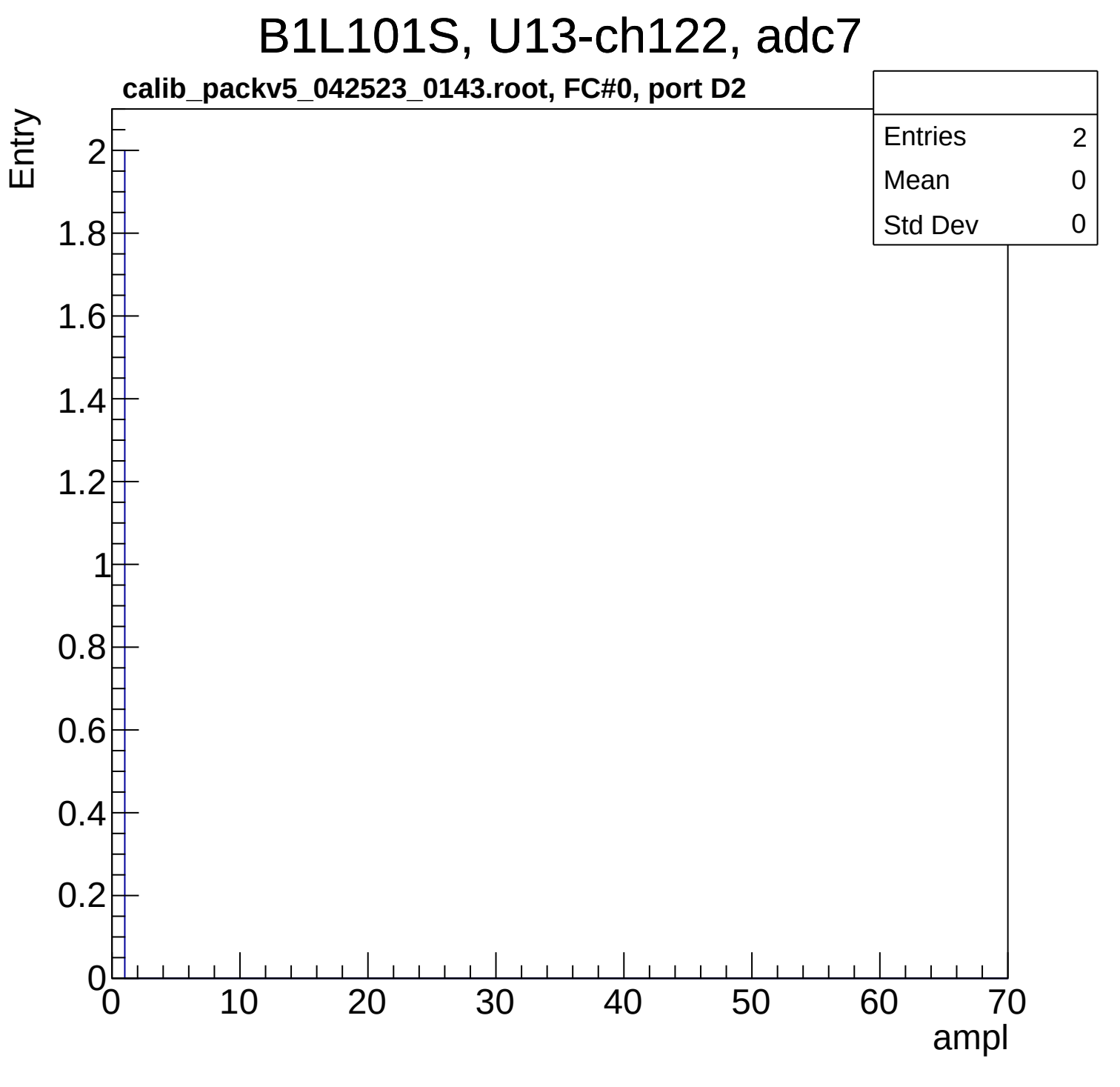
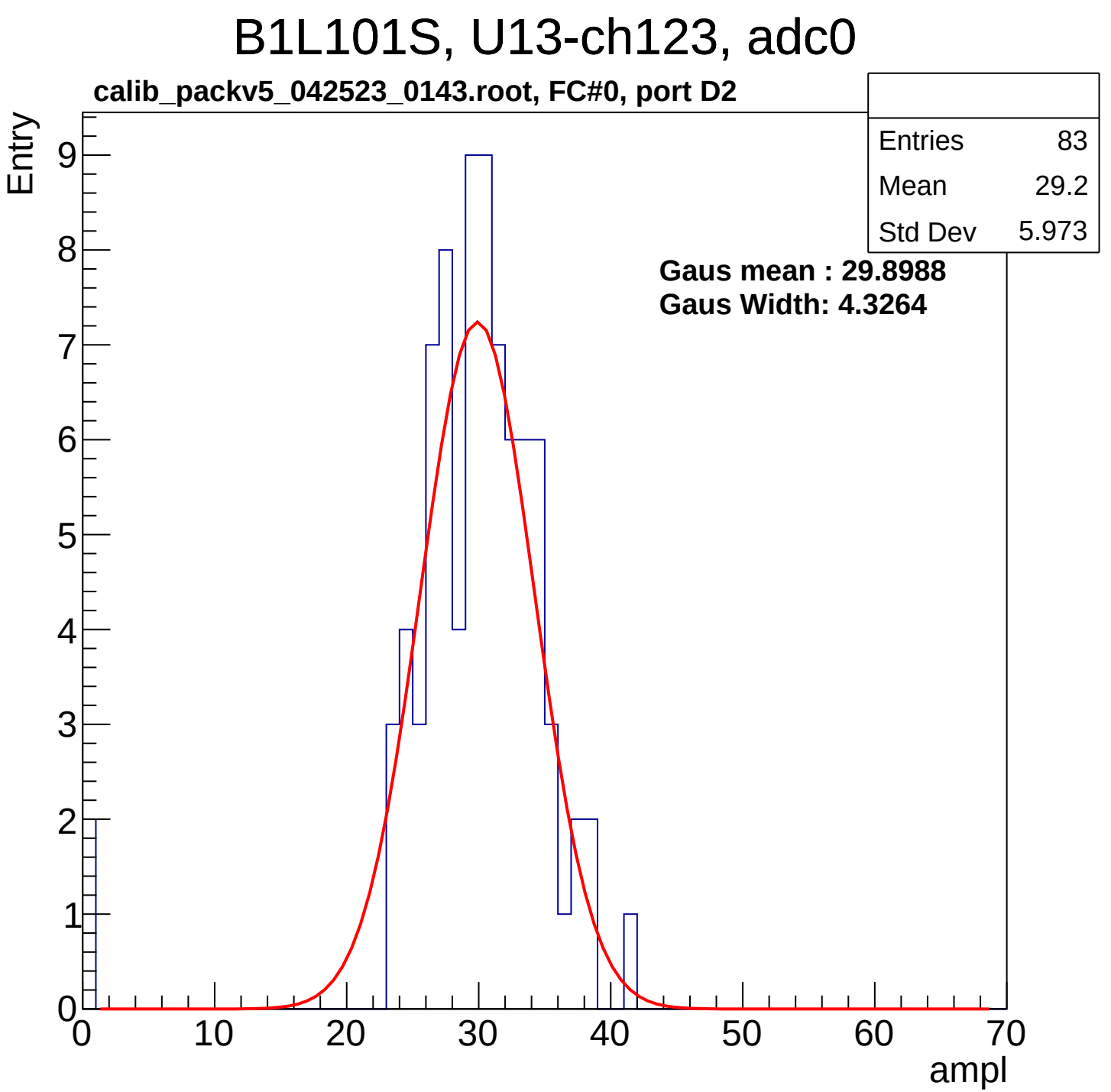
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch122, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 56
Std Dev 3.554
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch122, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 36
Mean 58.28
Std Dev 10.08
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L101S, U13-ch122, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 11
Mean 61.64
Std Dev 1.367
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L101S, U13-ch122, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch123, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 83
Mean 29.2
Std Dev 5.973
Gaus mean : 29.8988
Gaus Width: 4.3264
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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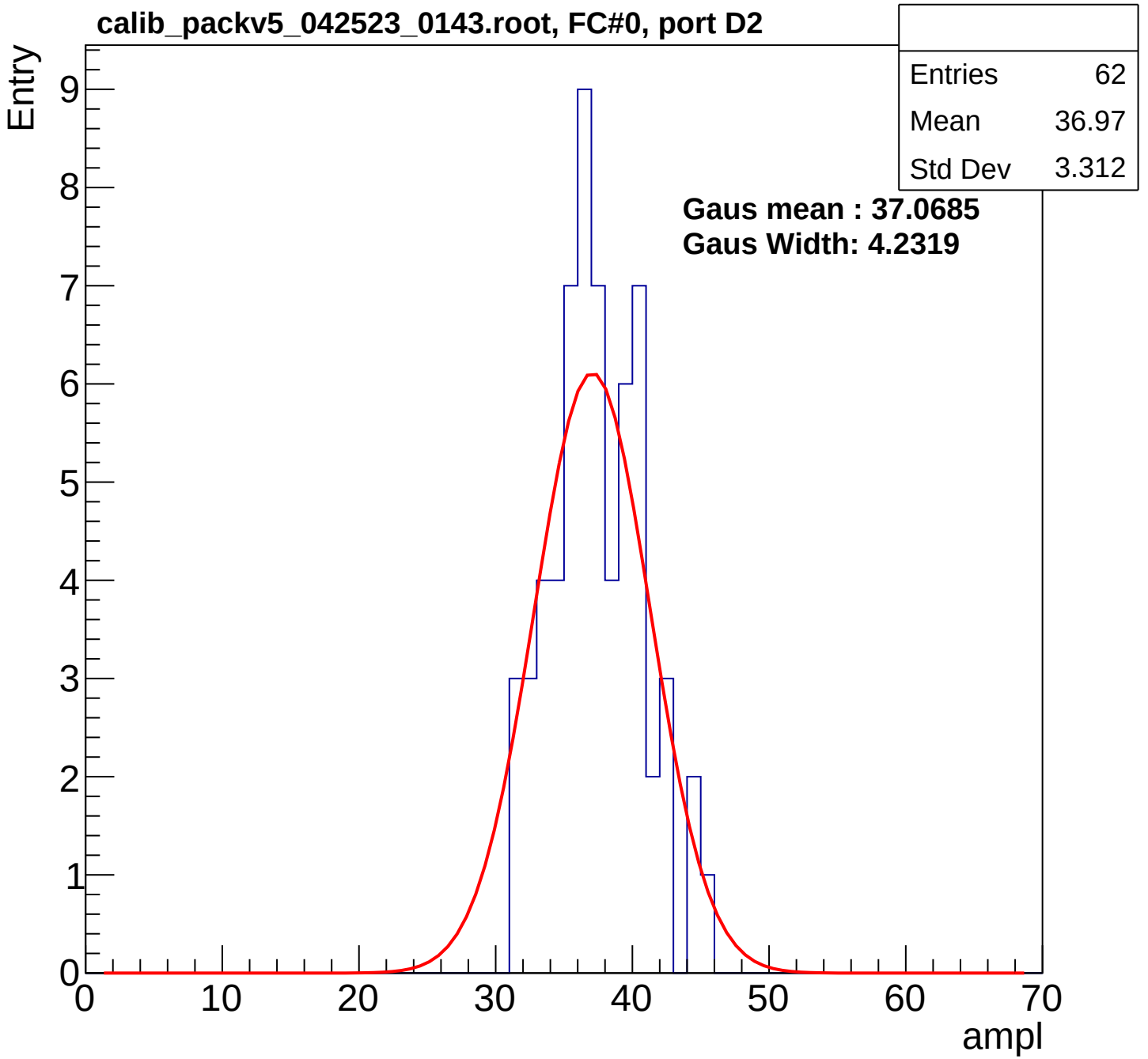
B1L101S, U13-ch123, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 36.97
Std Dev 3.312
Gaus mean : 37.0685
Gaus Width: 4.2319
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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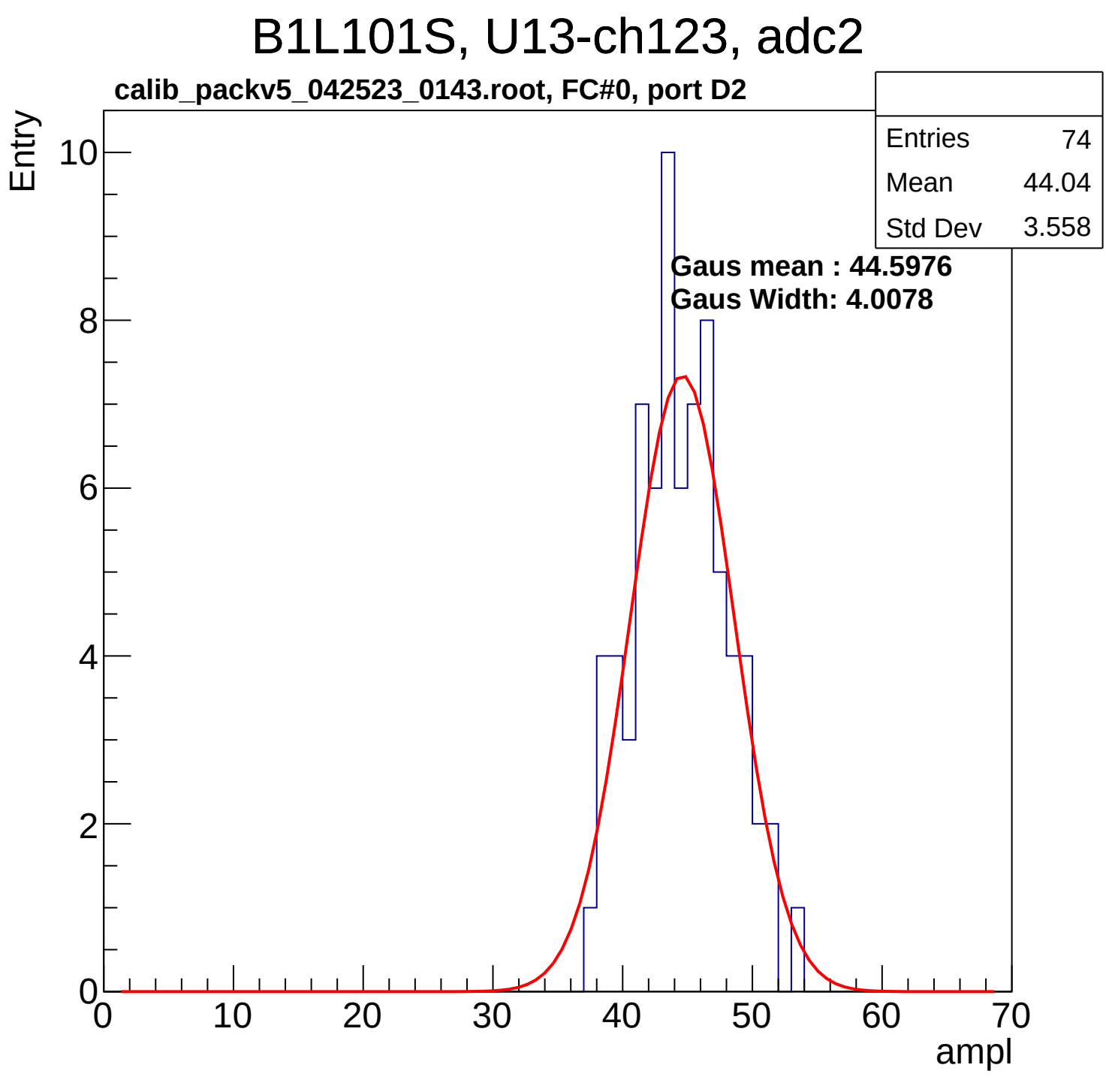
B1L101S, U13-ch123, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 74
Mean 44.04
Std Dev 3.558
Gaus mean : 44.5976
Gaus Width: 4.0078
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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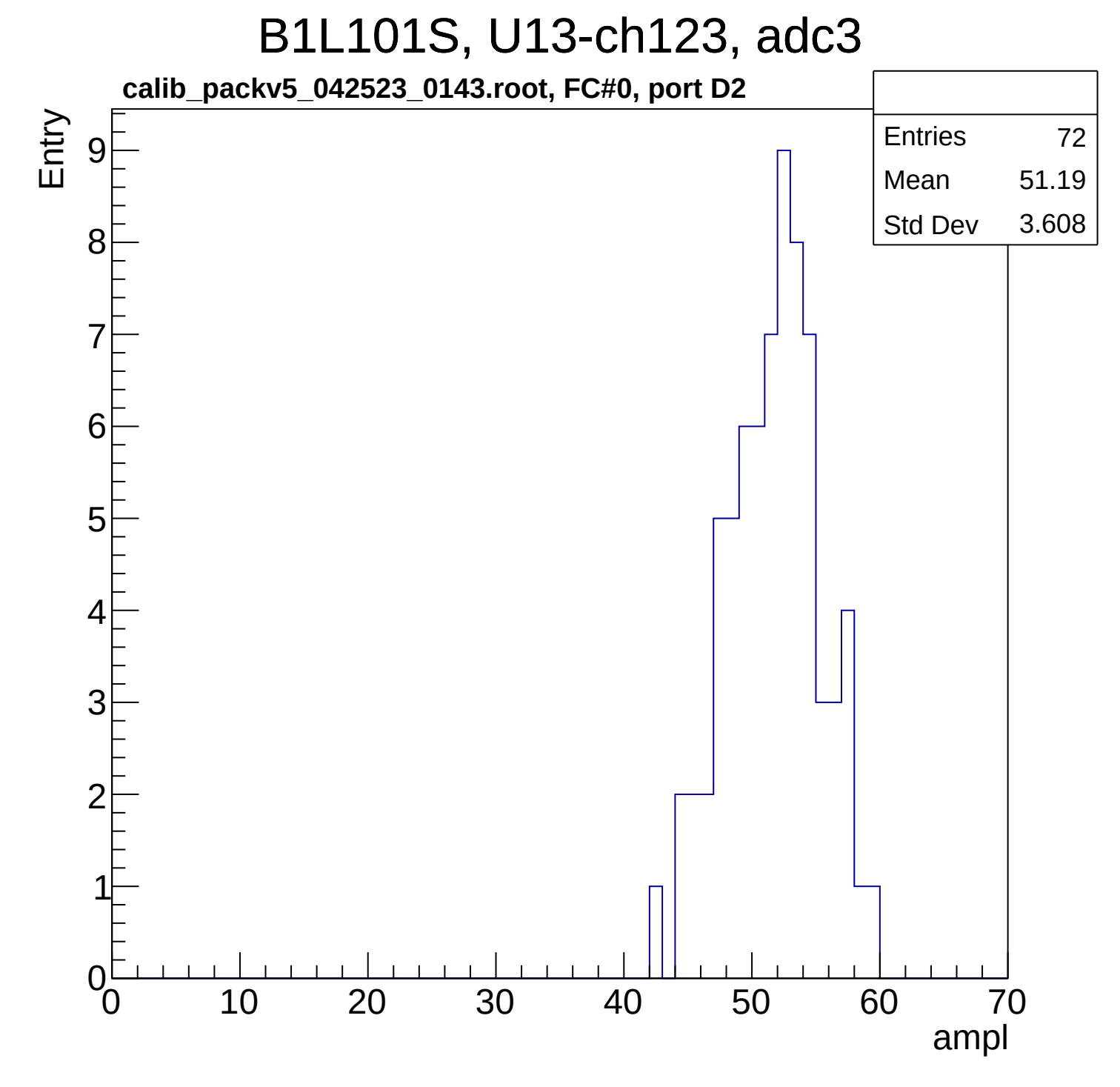
B1L101S, U13-ch123, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 72
Mean 51.19
Std Dev 3.608
Entry
ampl
0
1
2
3
4
5
6
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8
9
0
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40
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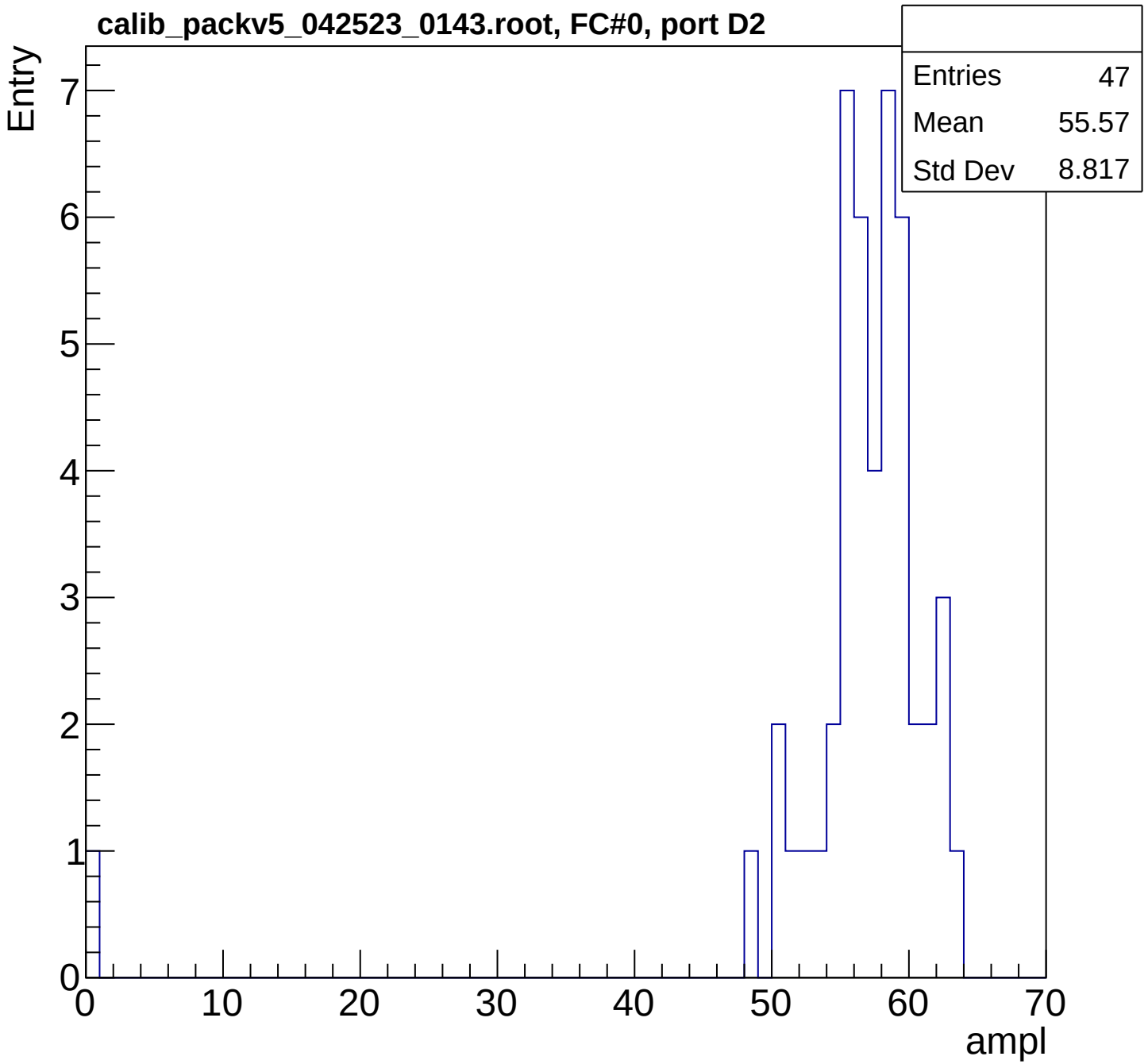
60
70


B1L101S, U13-ch123, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 47
Mean 55.57
Std Dev 8.817
Entry
ampl
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1
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3
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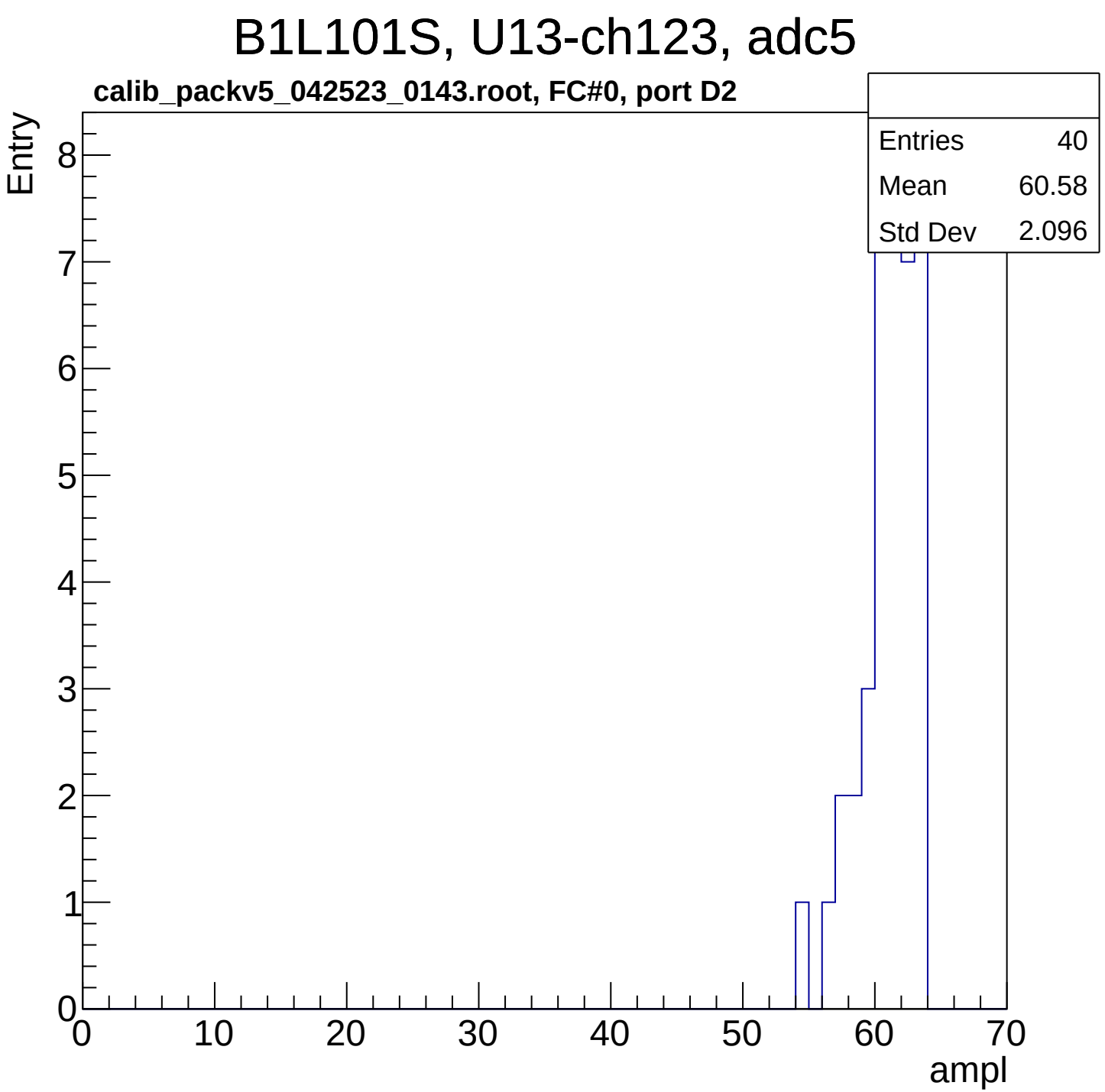
B1L101S, U13-ch123, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 40
Mean 60.58
Std Dev 2.096
Entry
ampl
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4
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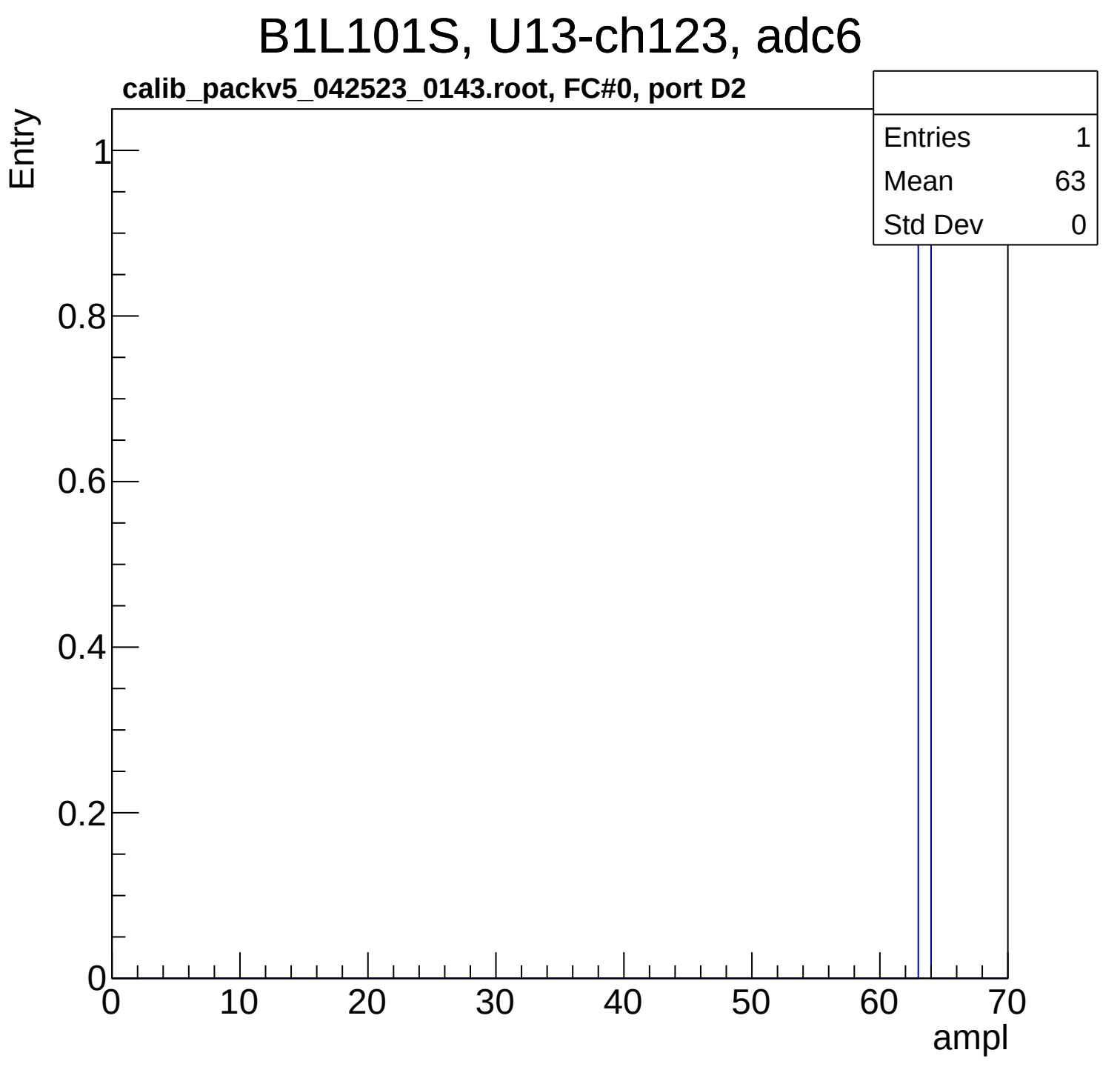
B1L101S, U13-ch123, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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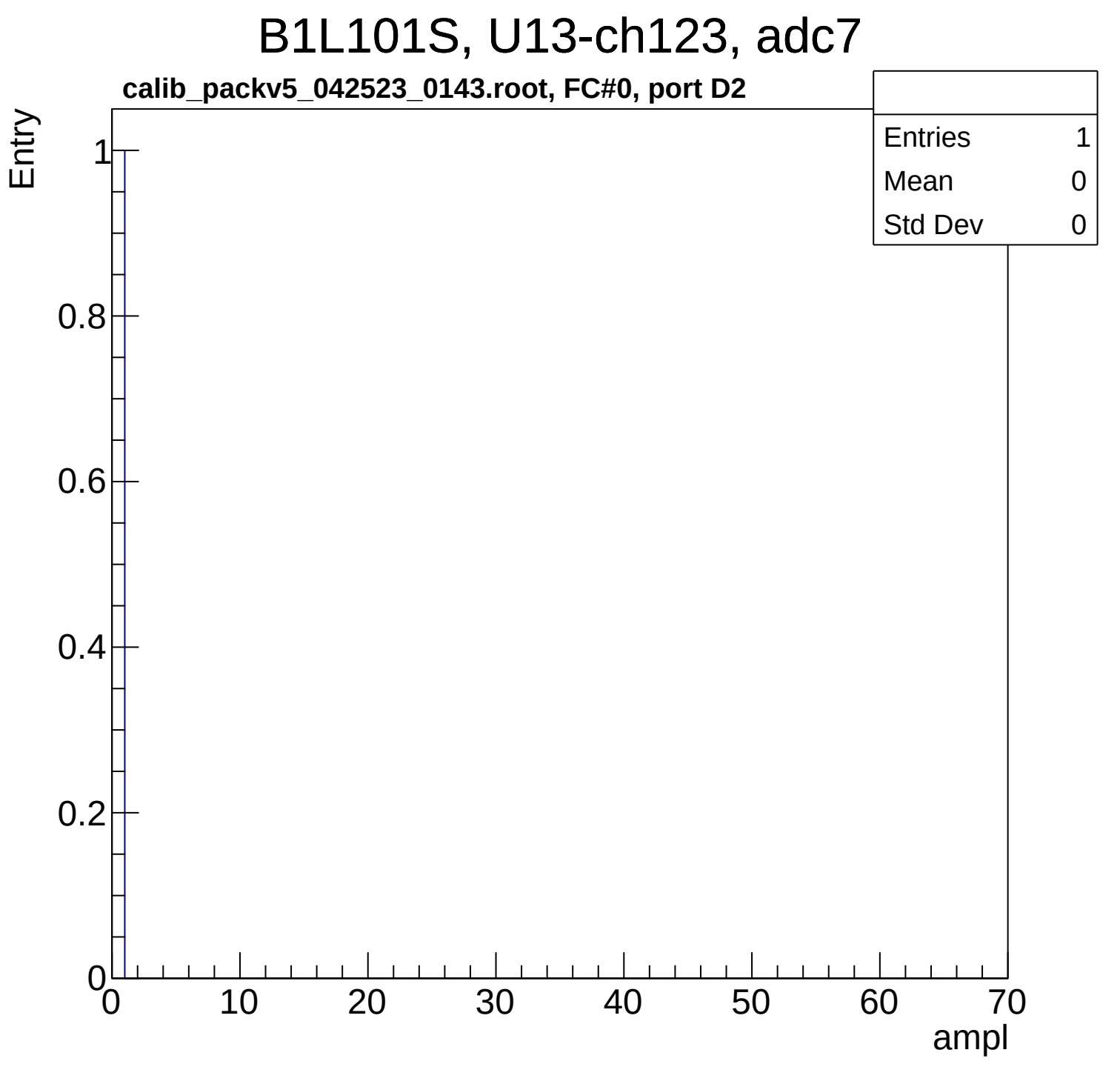
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch123, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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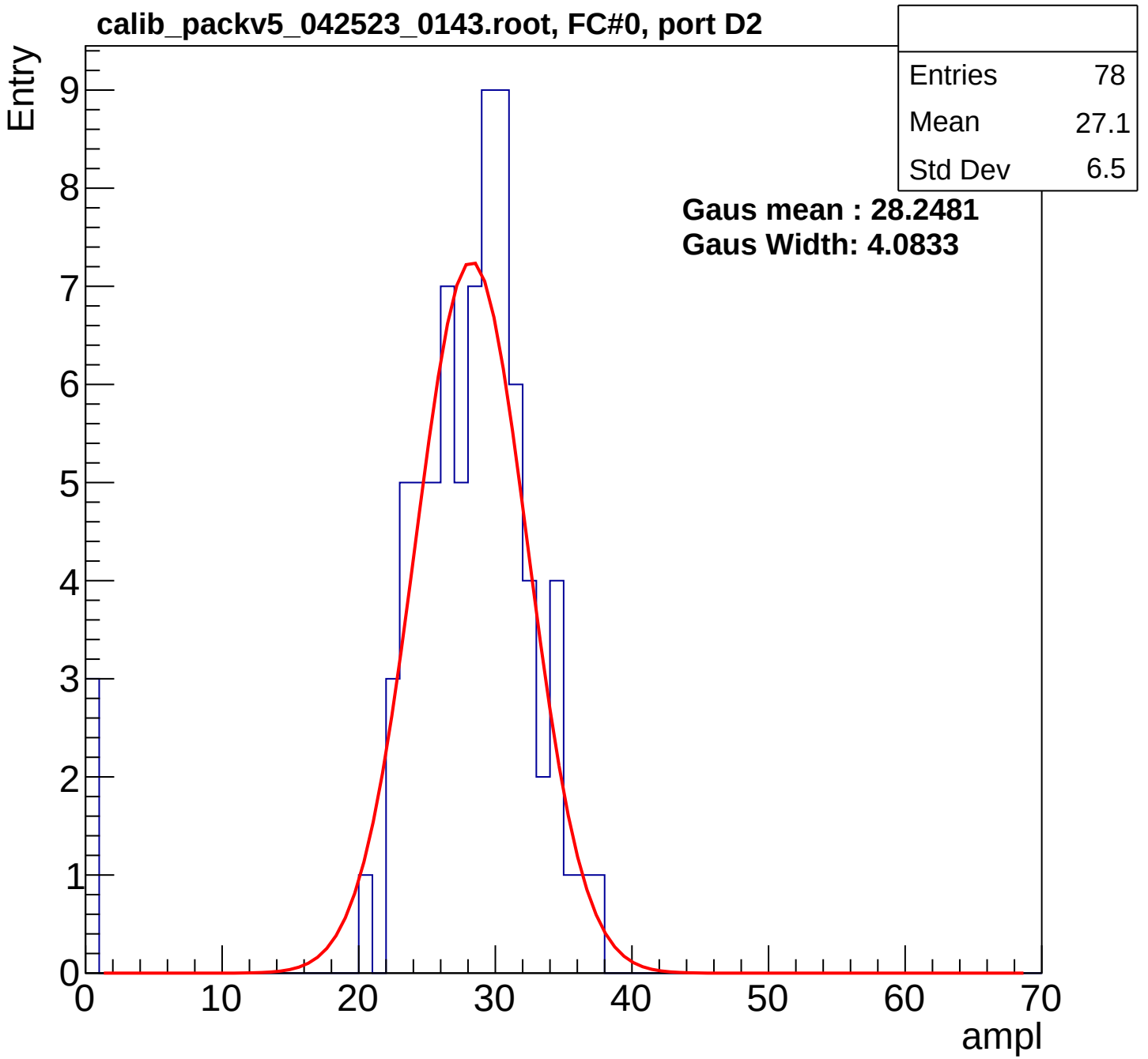
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch124, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 78
Mean 27.1
Std Dev 6.5
Gaus mean : 28.2481
Gaus Width: 4.0833
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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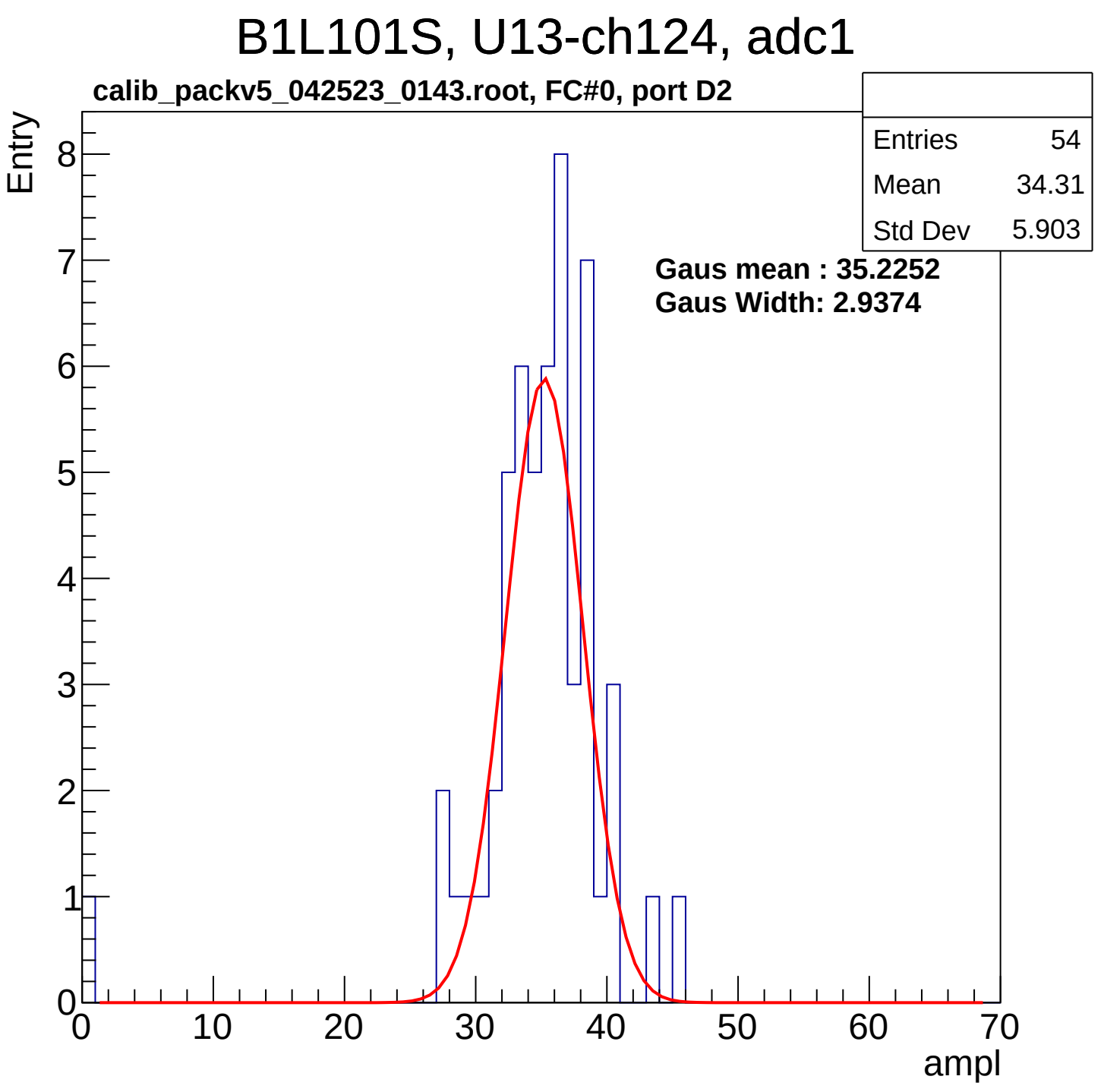
B1L101S, U13-ch124, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 54
Mean 34.31
Std Dev 5.903
Gaus mean : 35.2252
Gaus Width: 2.9374
Entry
ampl
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1
2
3
4
5
6
7
8
0
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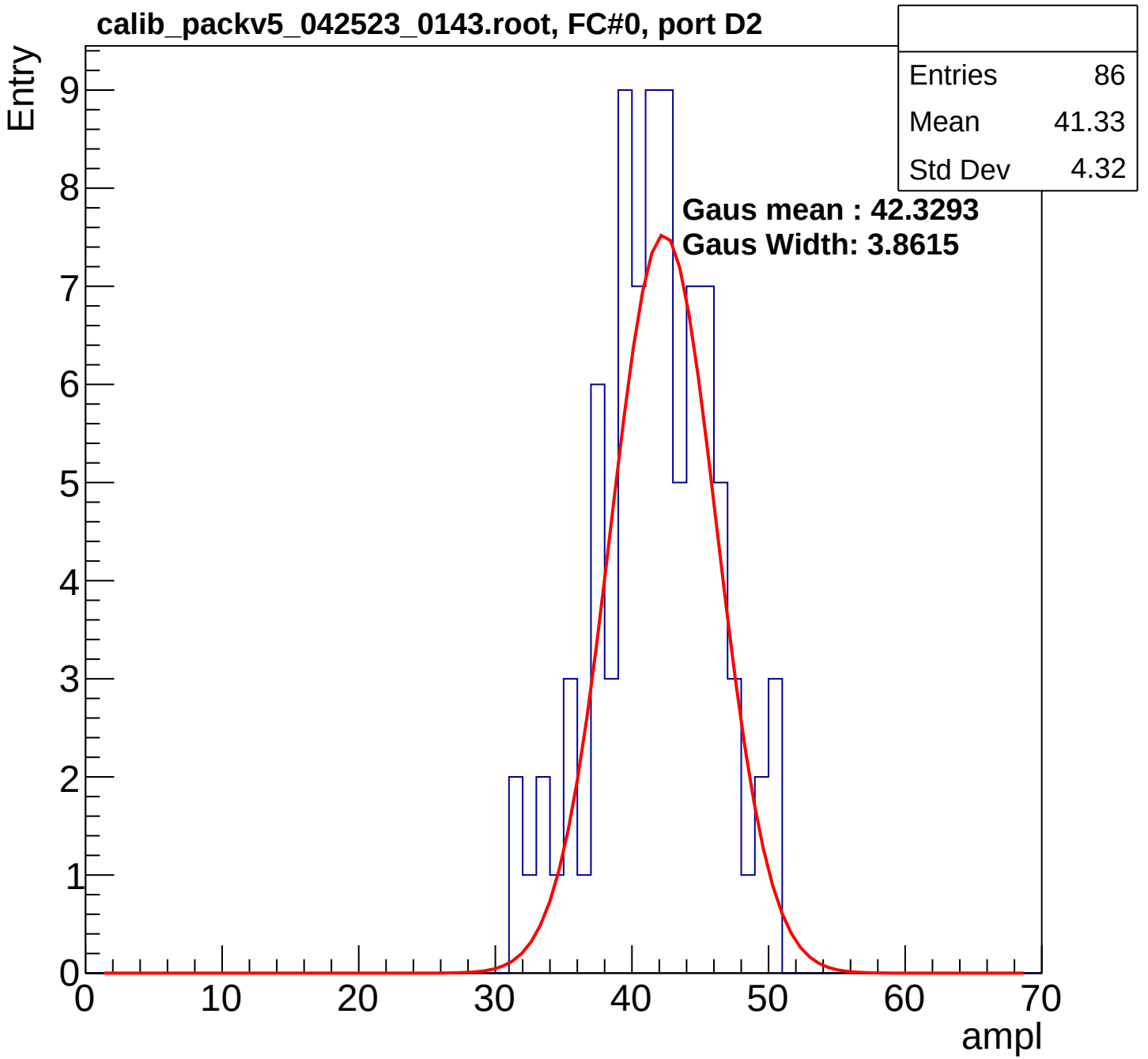
B1L101S, U13-ch124, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 86
Mean 41.33
Std Dev 4.32
Gaus mean : 42.3293
Gaus Width: 3.8615
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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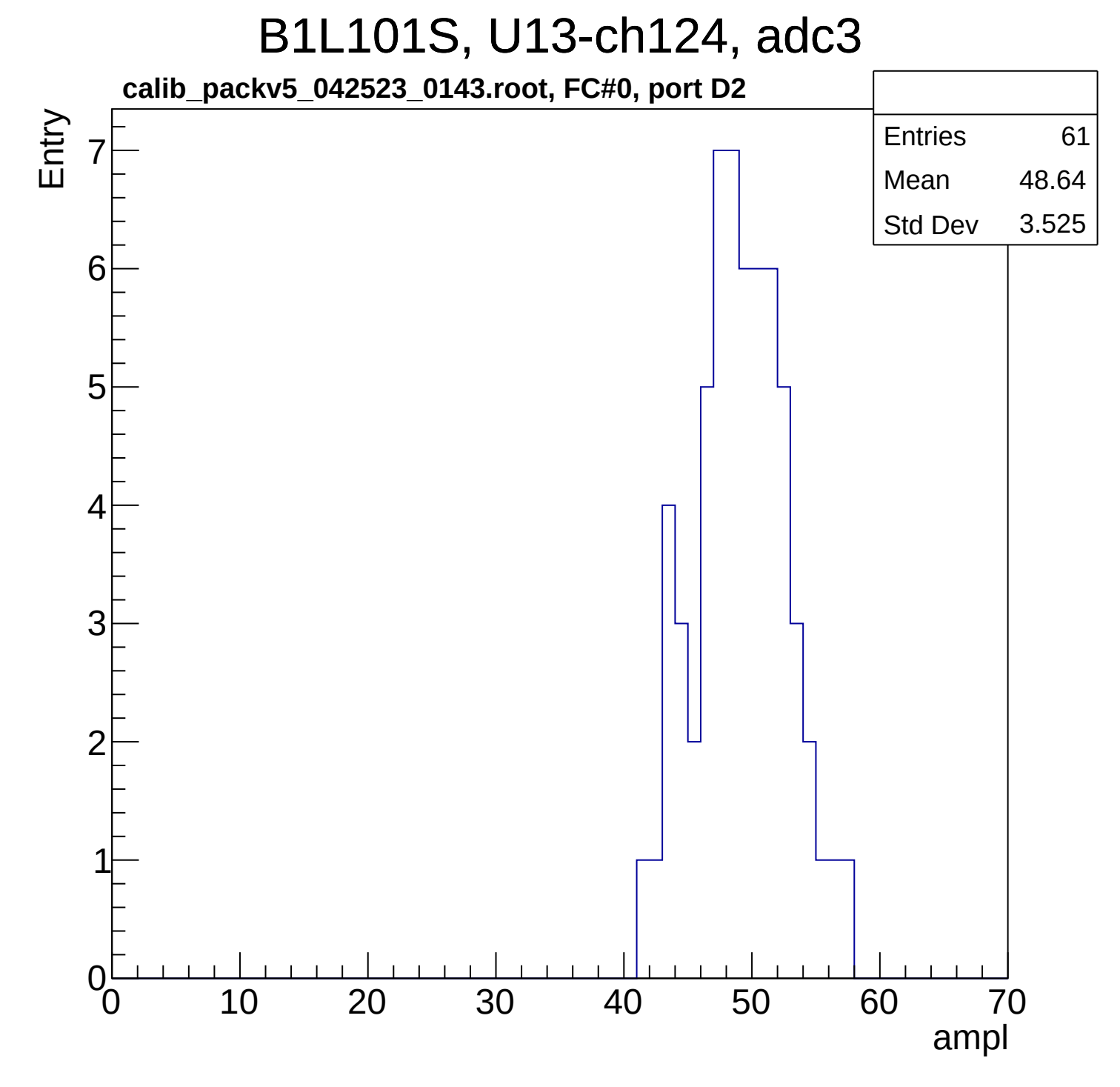
B1L101S, U13-ch124, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 61
Mean 48.64
Std Dev 3.525
Entry
ampl
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1
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3
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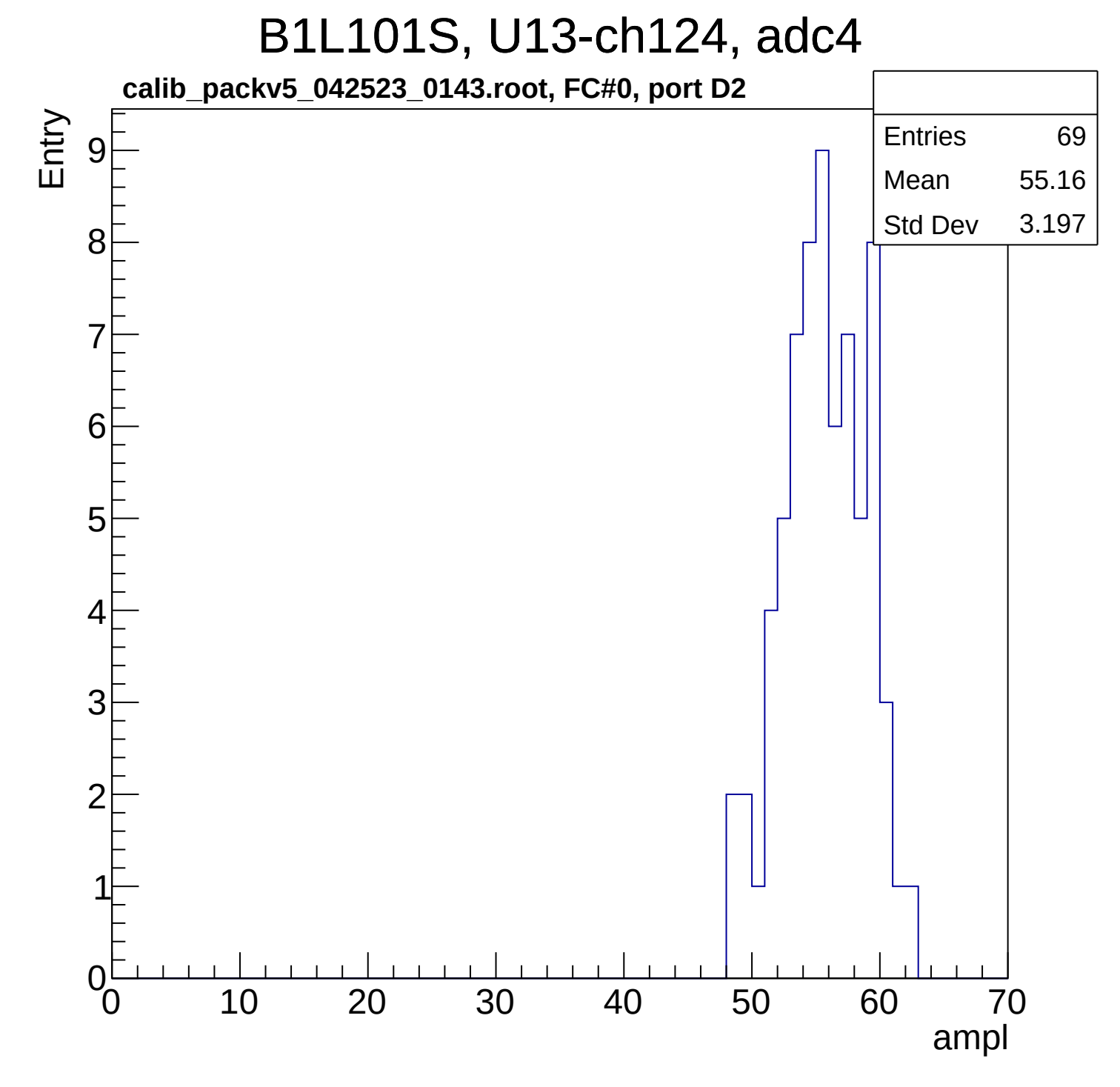
B1L101S, U13-ch124, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 69
Mean 55.16
Std Dev 3.197
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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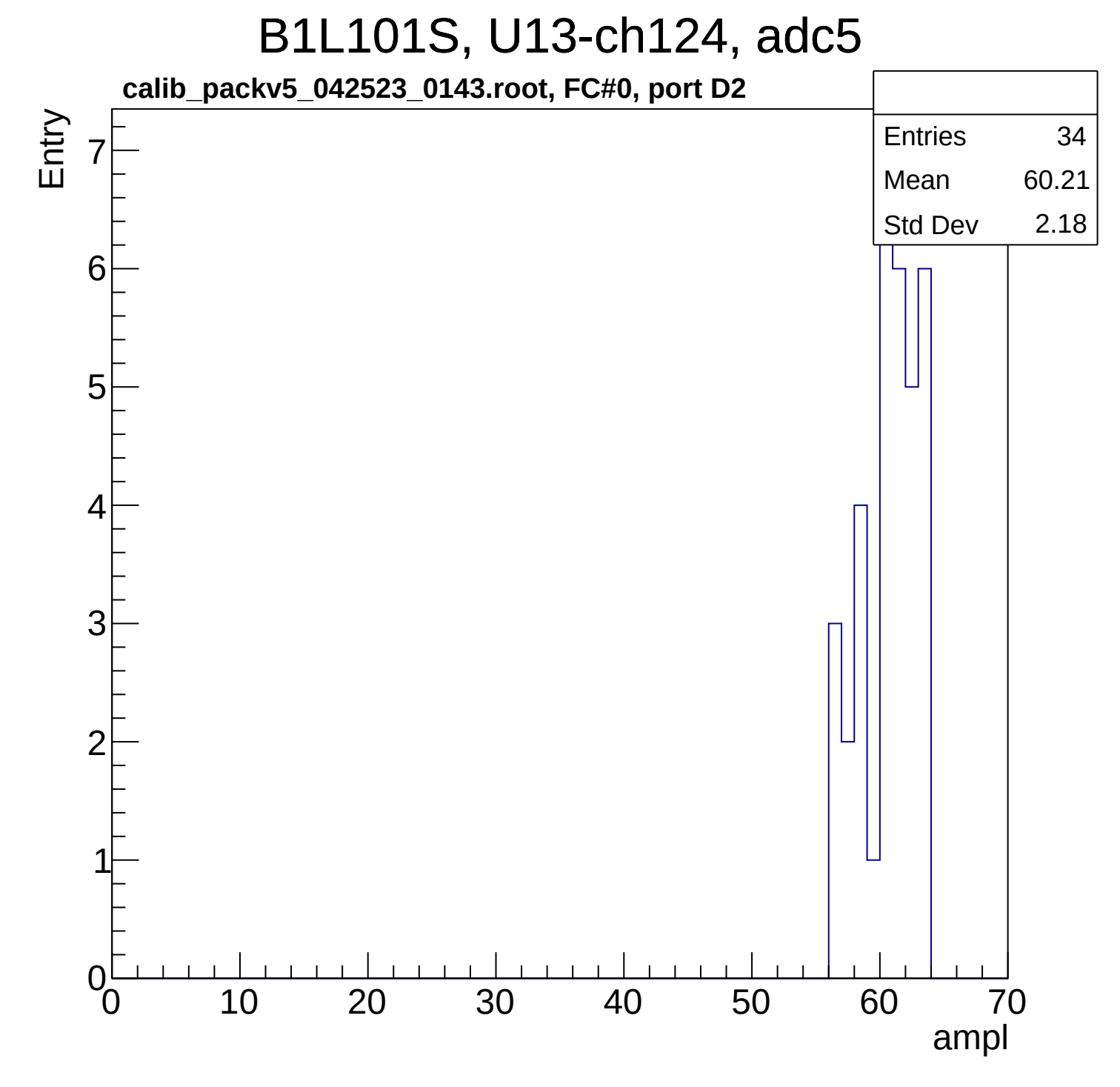
B1L101S, U13-ch124, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 34
Mean 60.21
Std Dev 2.18
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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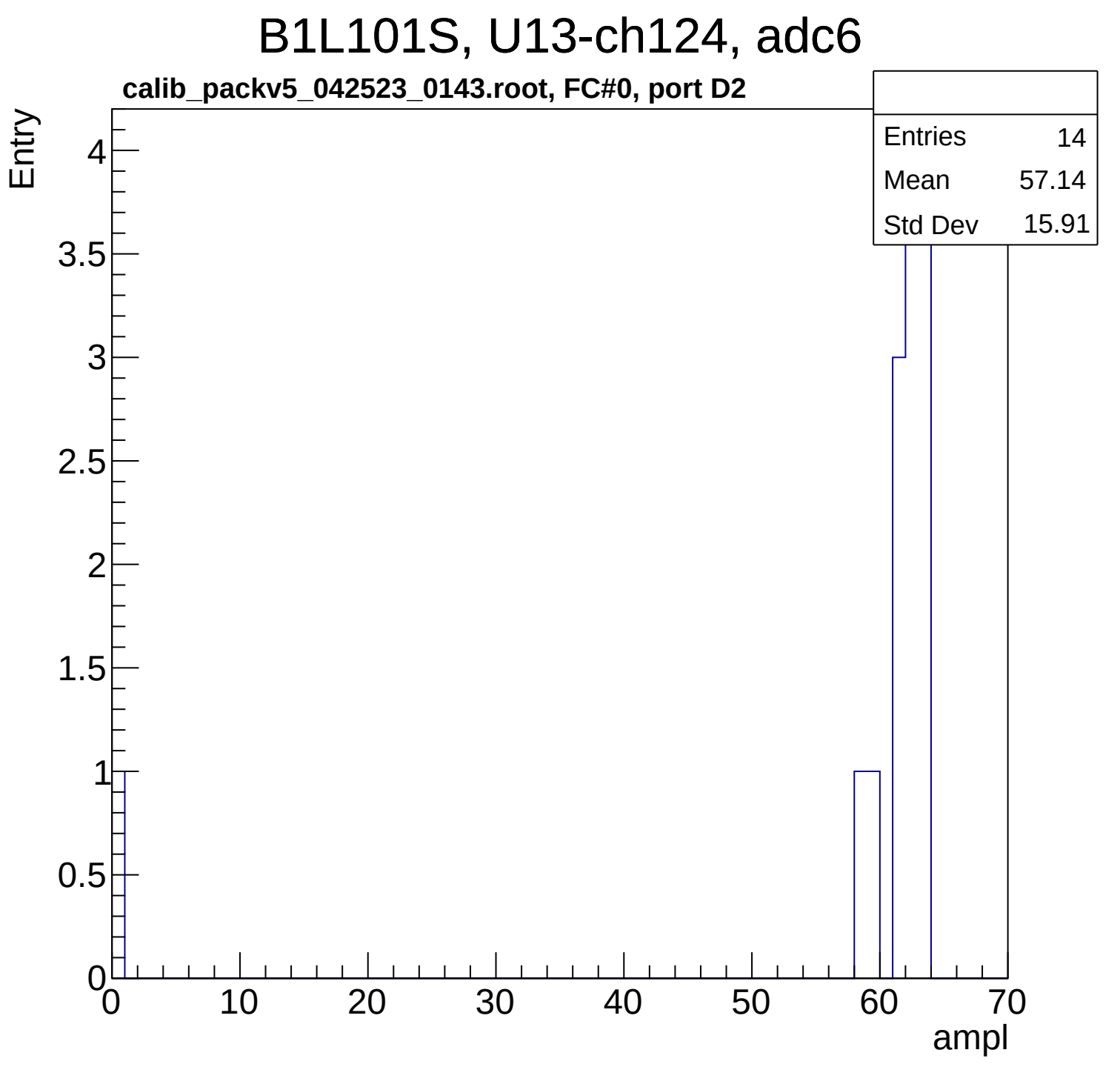
B1L101S, U13-ch124, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 14
Mean 57.14
Std Dev 15.91
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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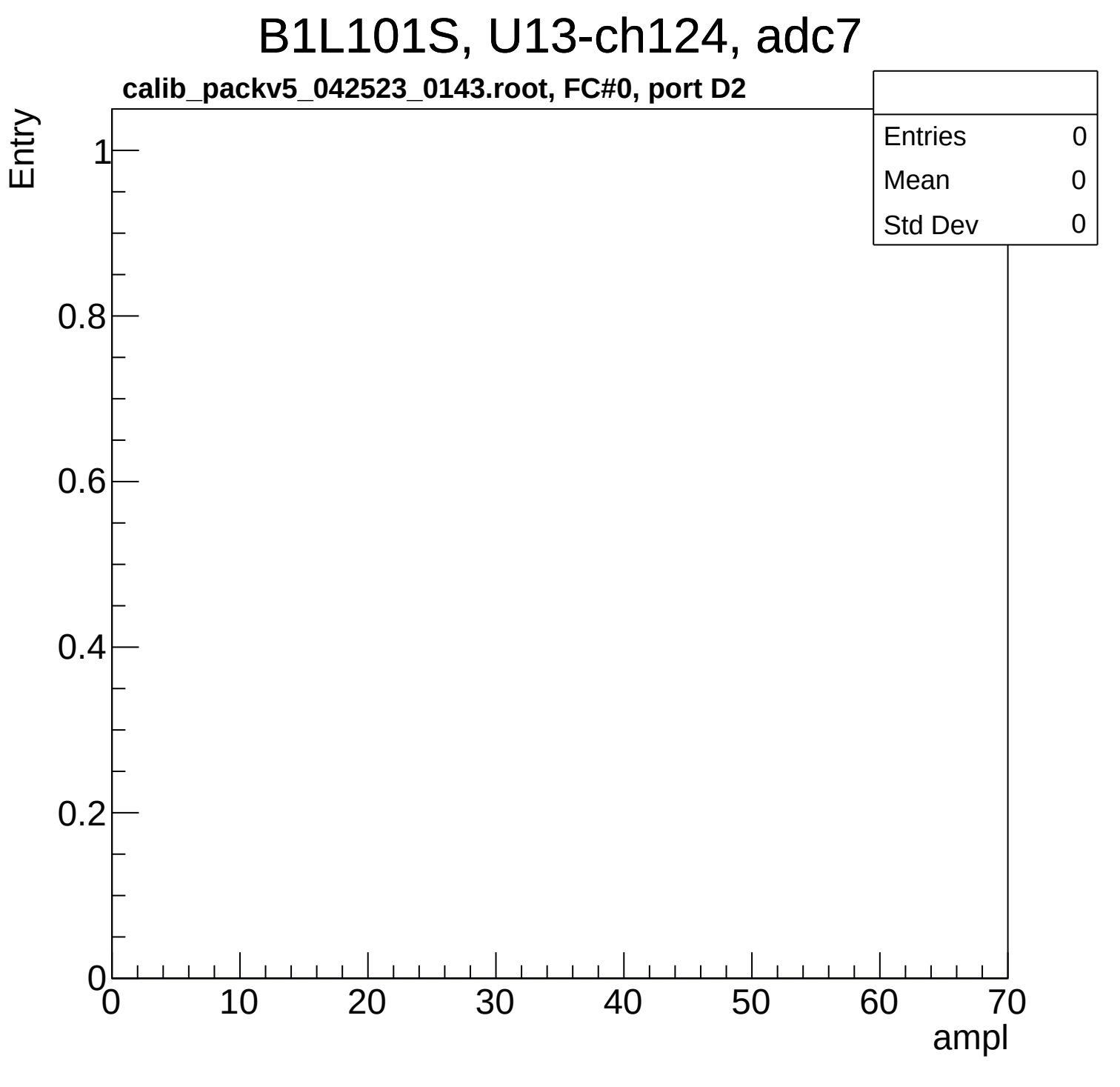
B1L101S, U13-ch124, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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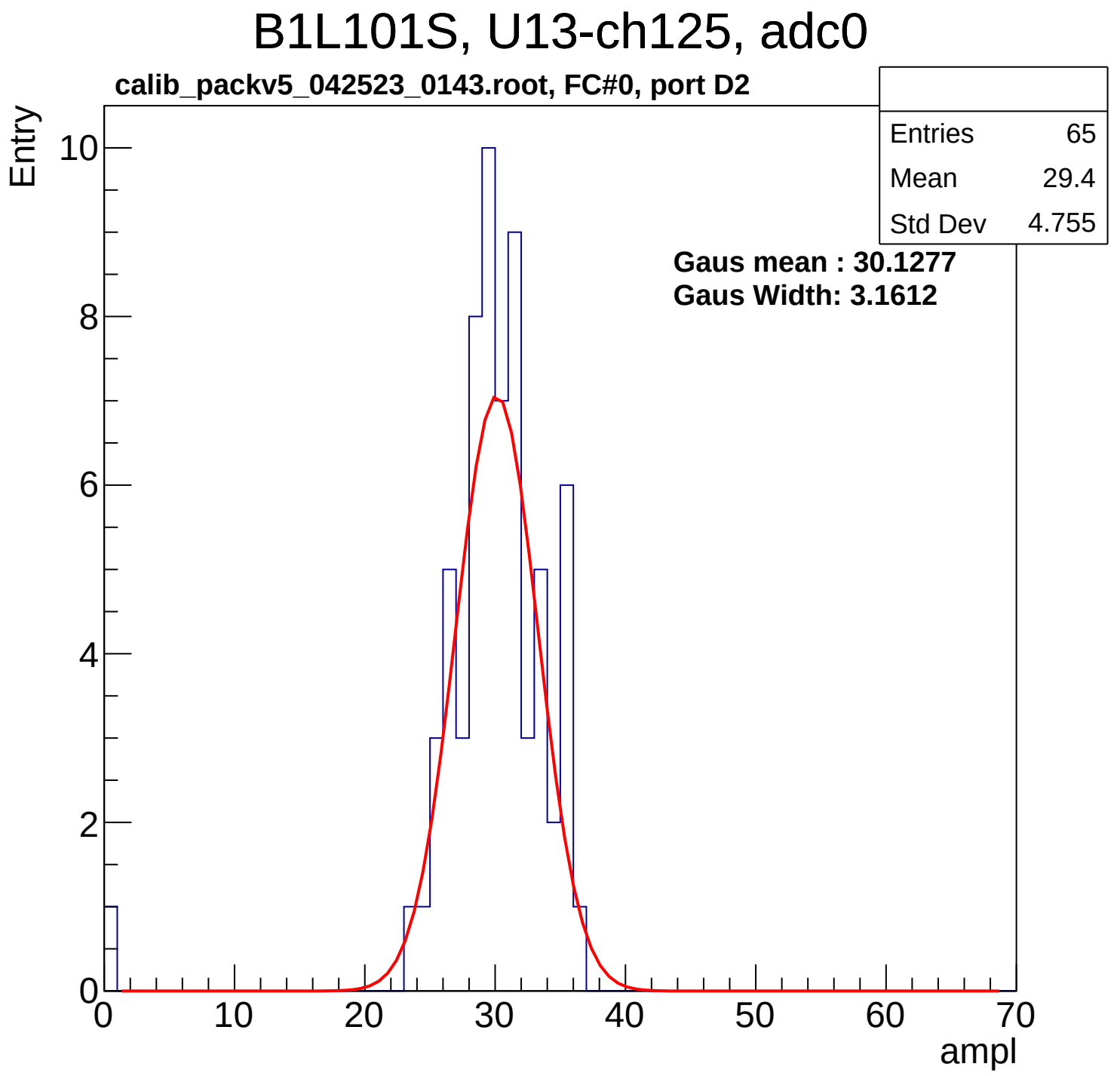
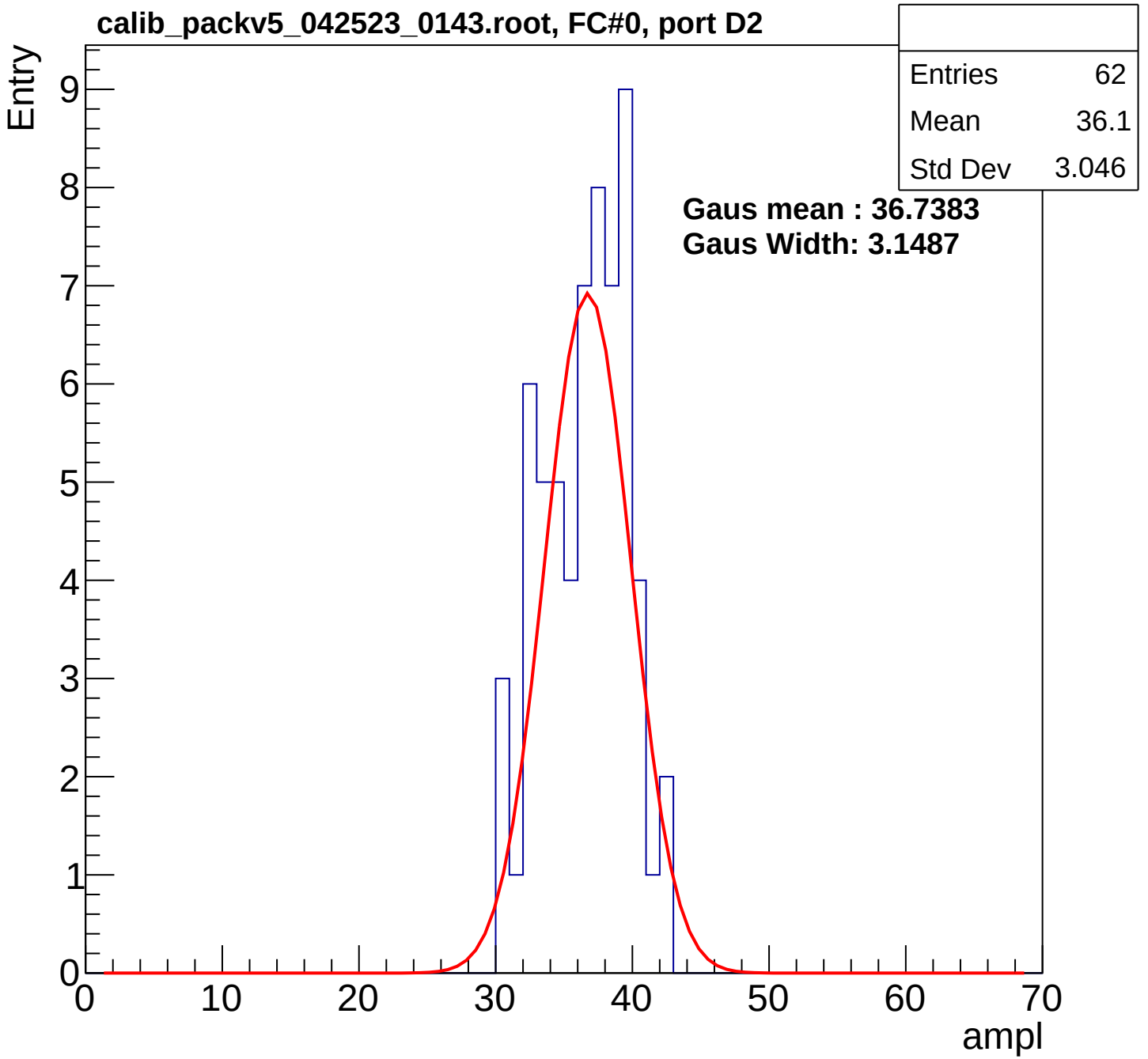
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch125, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 29.4
Std Dev 4.755
Gaus mean : 30.1277
Gaus Width: 3.1612
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L101S, U13-ch125, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 36.1
Std Dev 3.046
Gaus mean : 36.7383
Gaus Width: 3.1487
Entry
ampl
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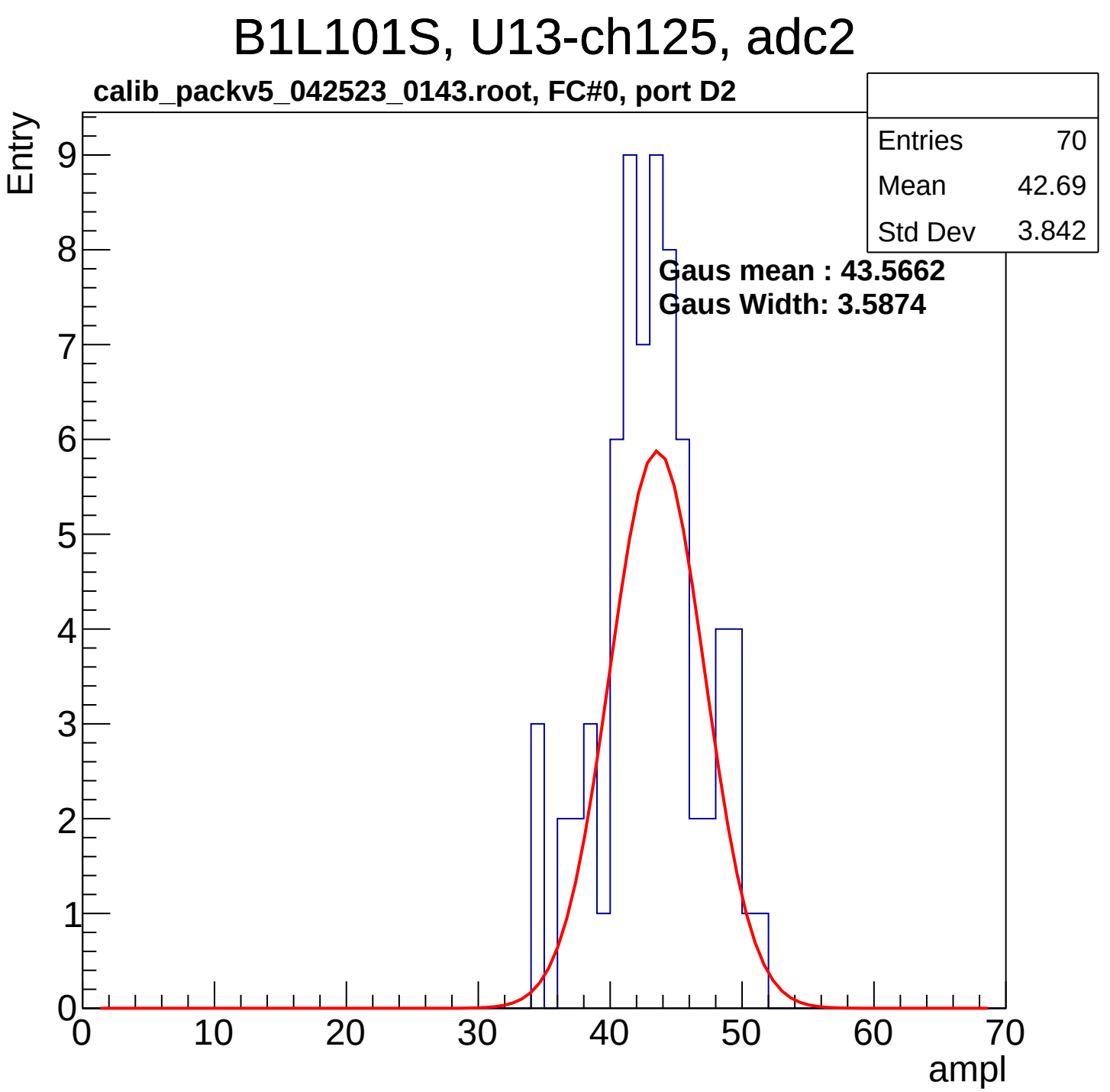
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L101S, U13-ch125, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 70
Mean 42.69
Std Dev 3.842
Gaus mean : 43.5662
Gaus Width: 3.5874
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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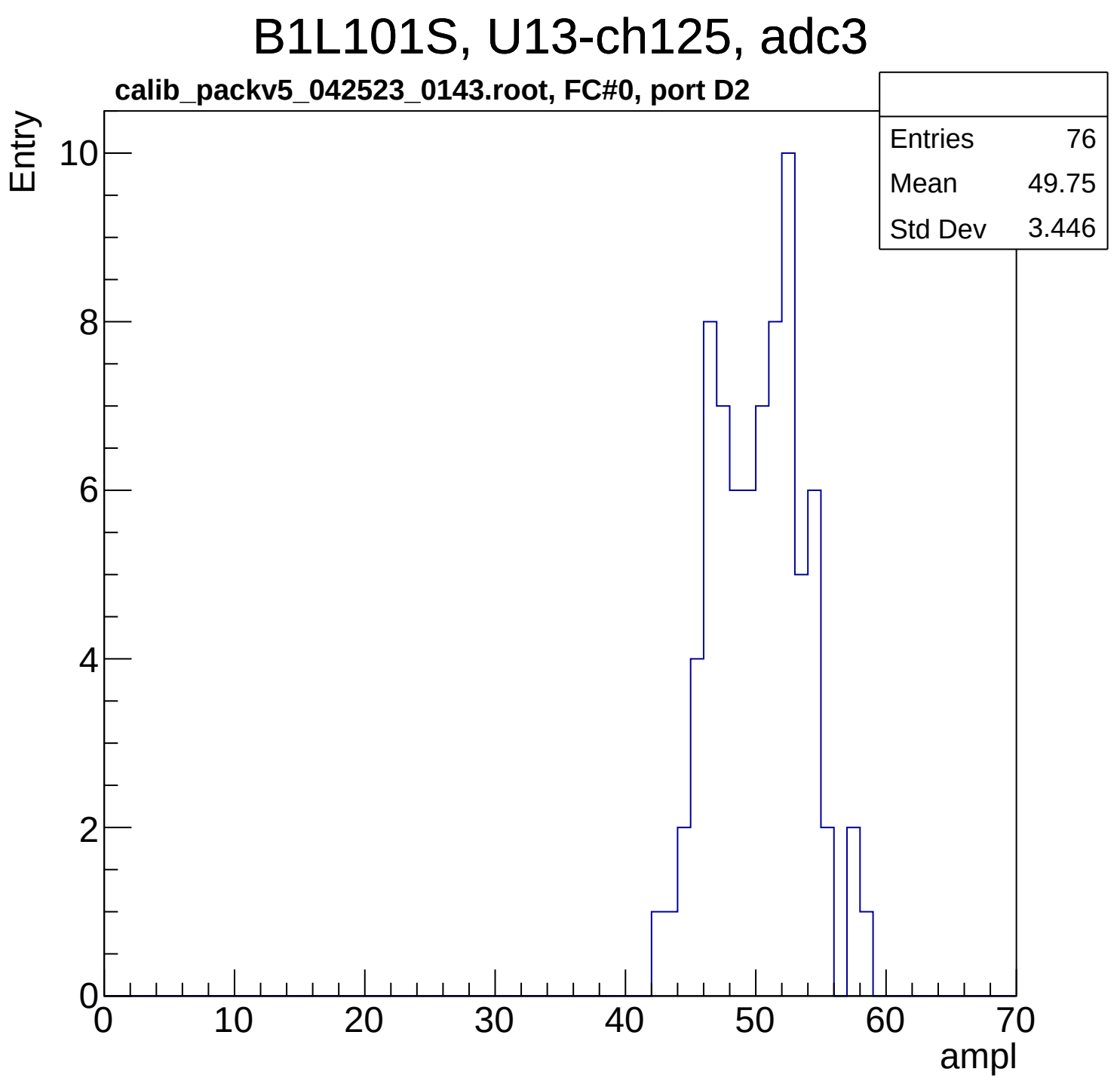
B1L101S, U13-ch125, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 76
Mean 49.75
Std Dev 3.446
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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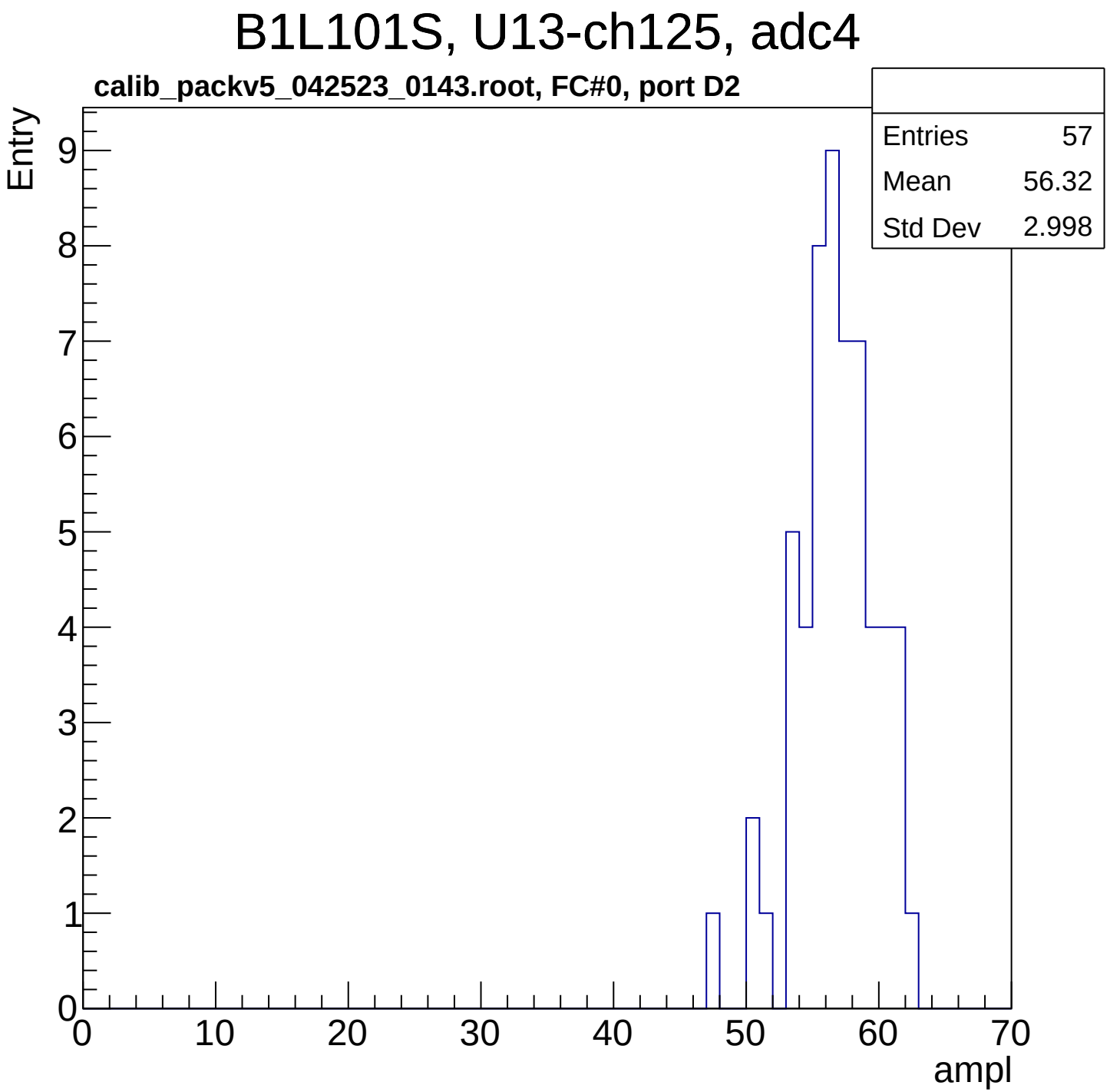
B1L101S, U13-ch125, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 57
Mean 56.32
Std Dev 2.998
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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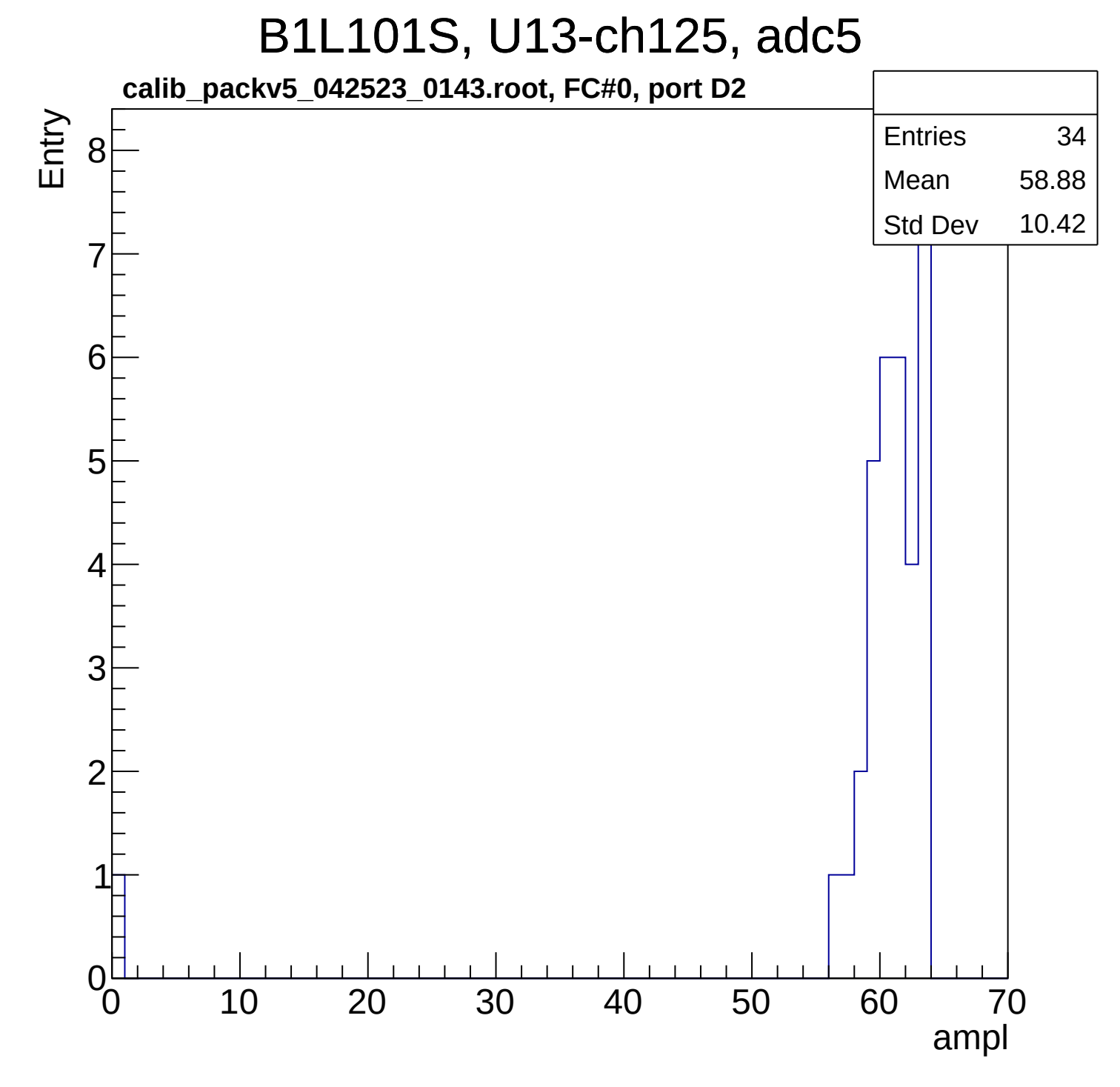
B1L101S, U13-ch125, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 34
Mean 58.88
Std Dev 10.42
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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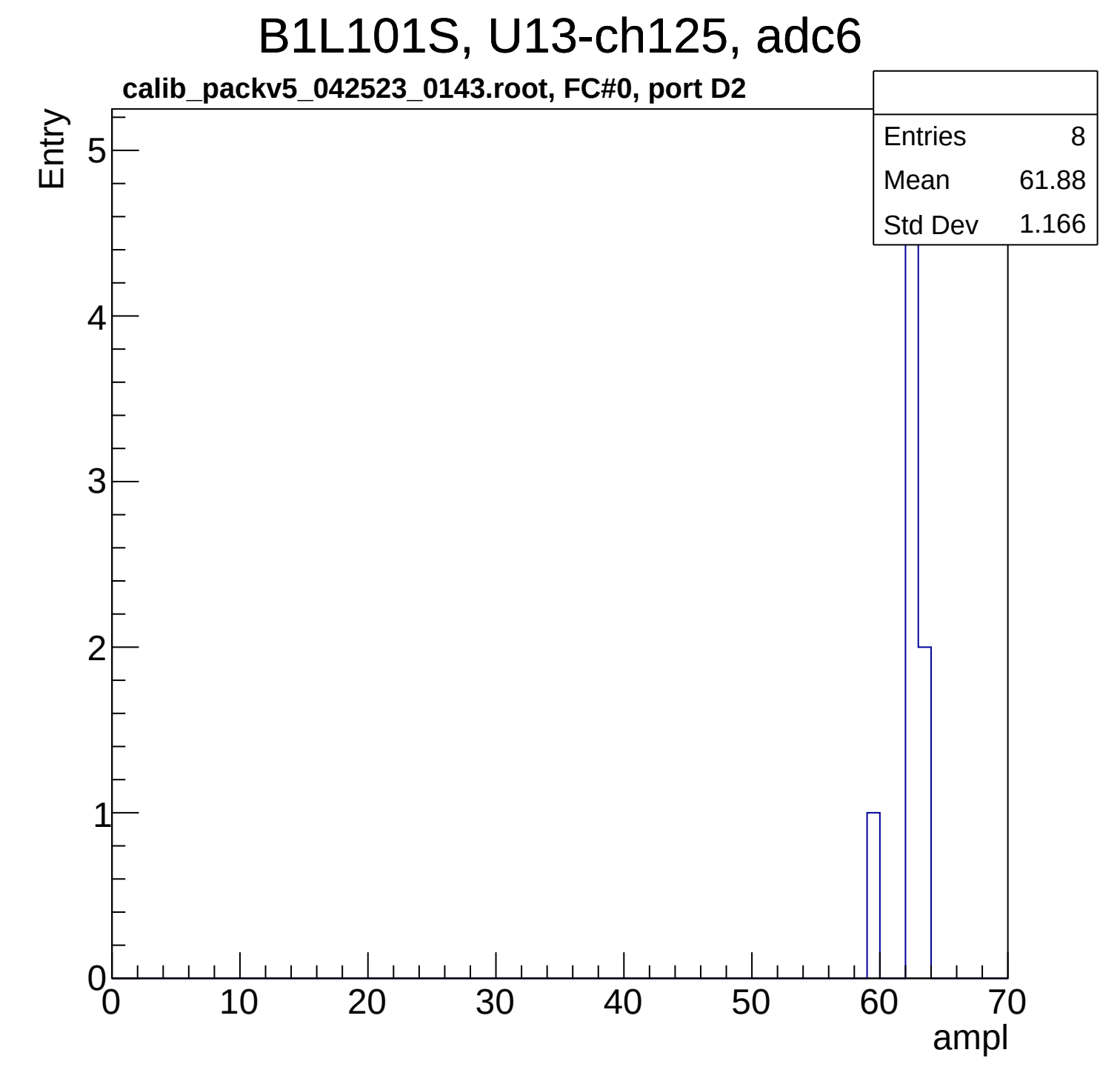
B1L101S, U13-ch125, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 8
Mean 61.88
Std Dev 1.166
Entry
ampl
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2
3
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0
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20
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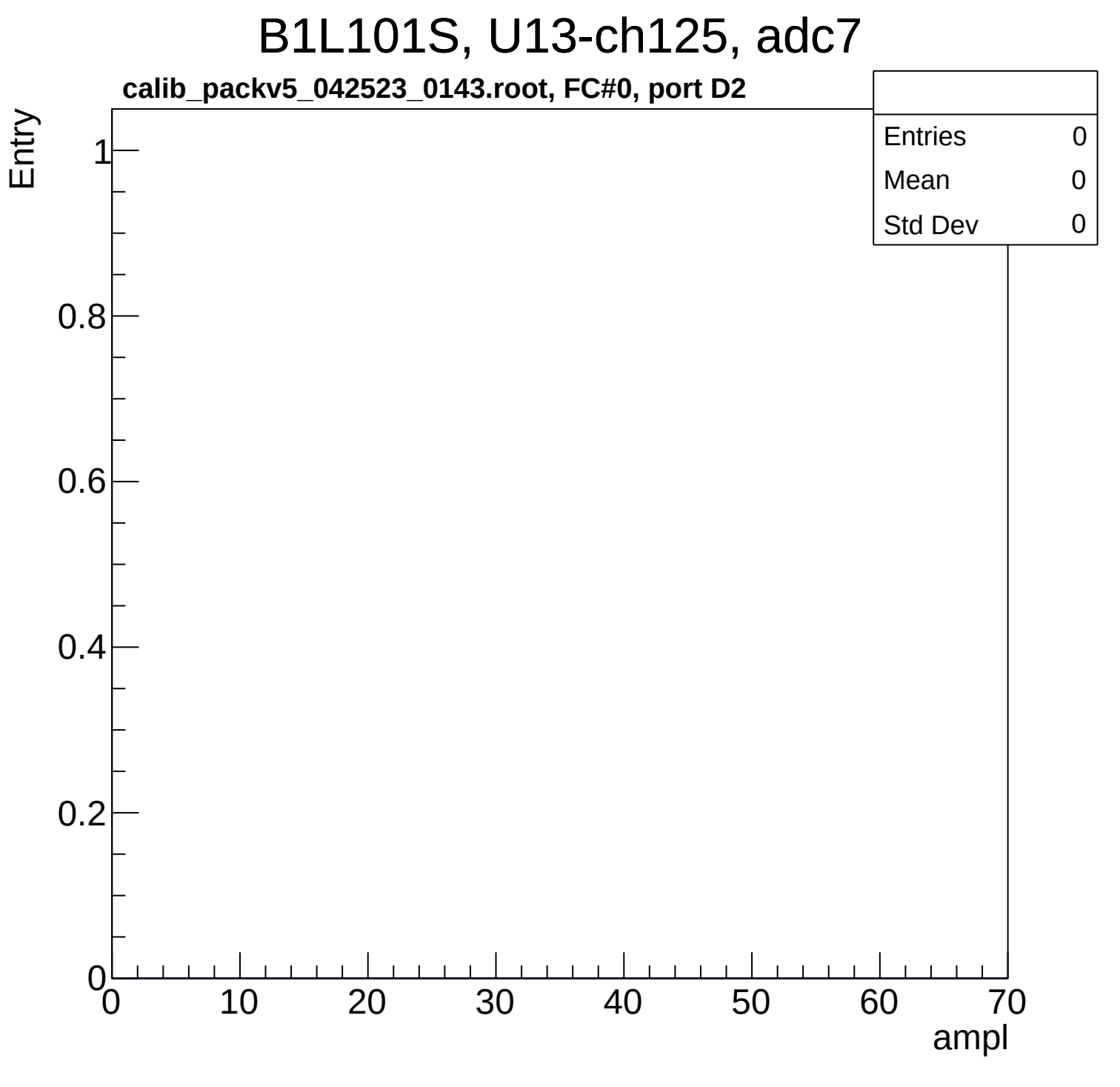
40
50
60
70


B1L101S, U13-ch125, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
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0
10
20
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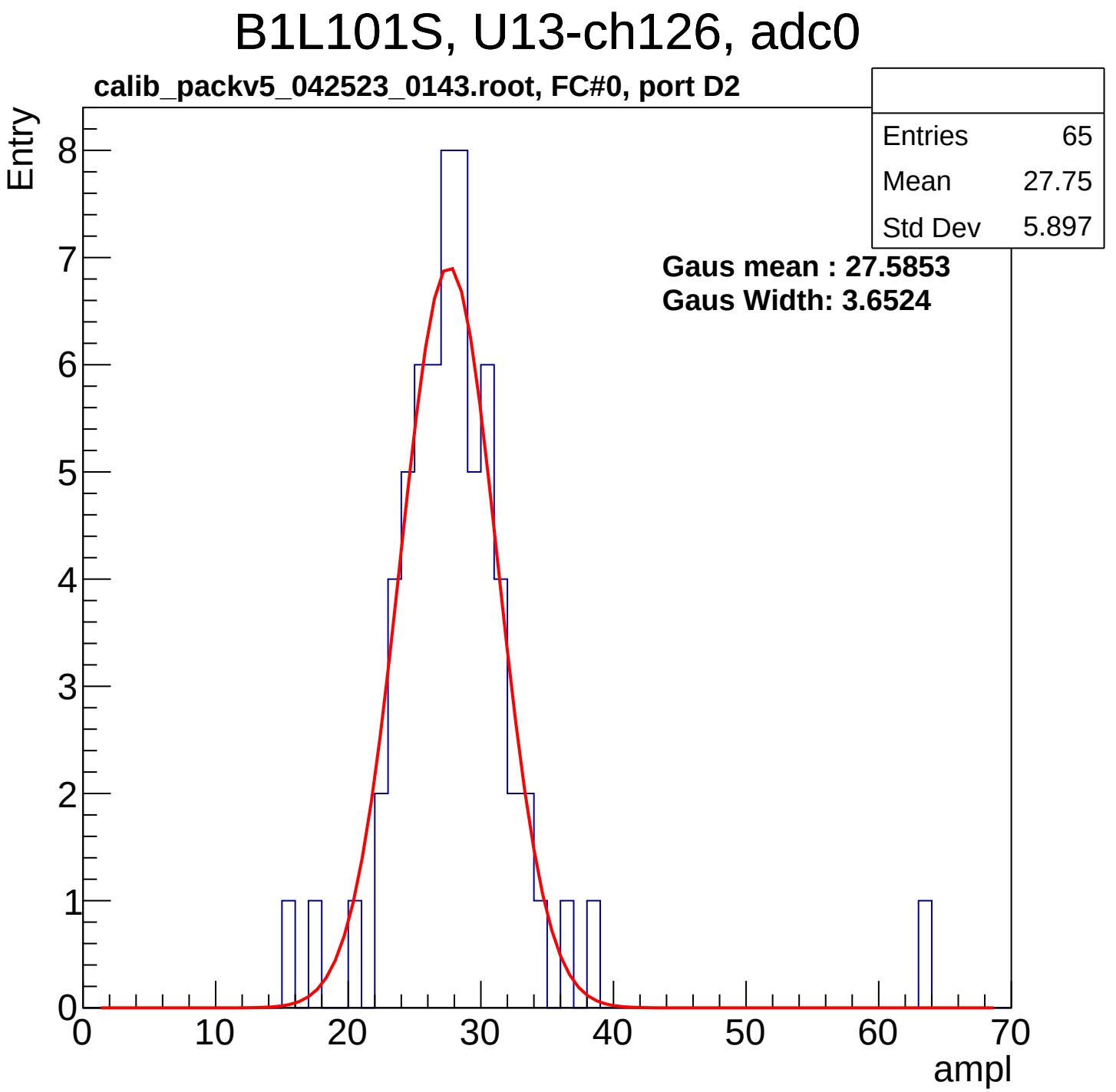
50
60
70
ampl


B1L101S, U13-ch126, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 65
Mean 27.75
Std Dev 5.897
Gaus mean : 27.5853
Gaus Width: 3.6524
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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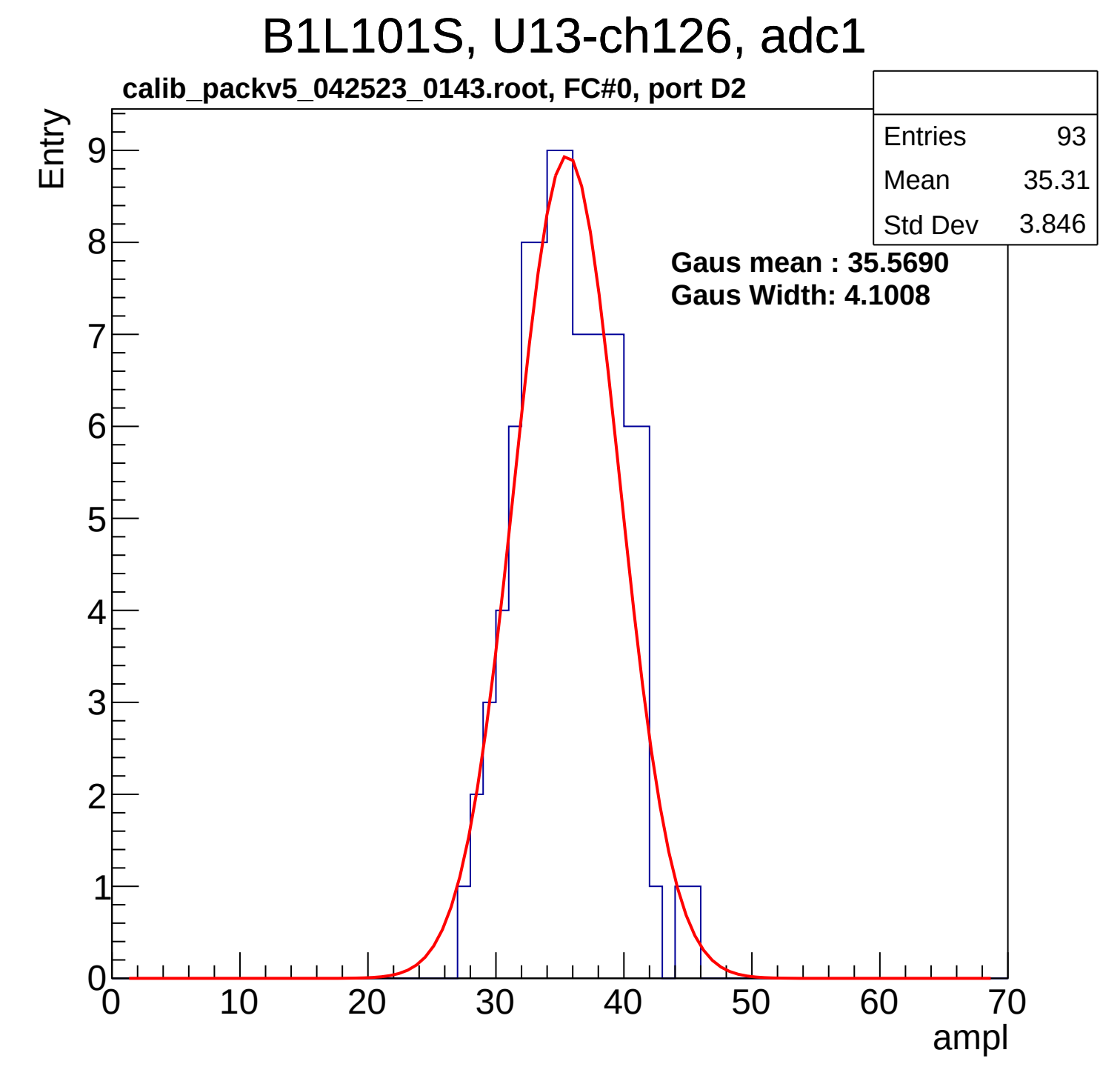
B1L101S, U13-ch126, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 93
Mean 35.31
Std Dev 3.846
Gaus mean : 35.5690
Gaus Width: 4.1008
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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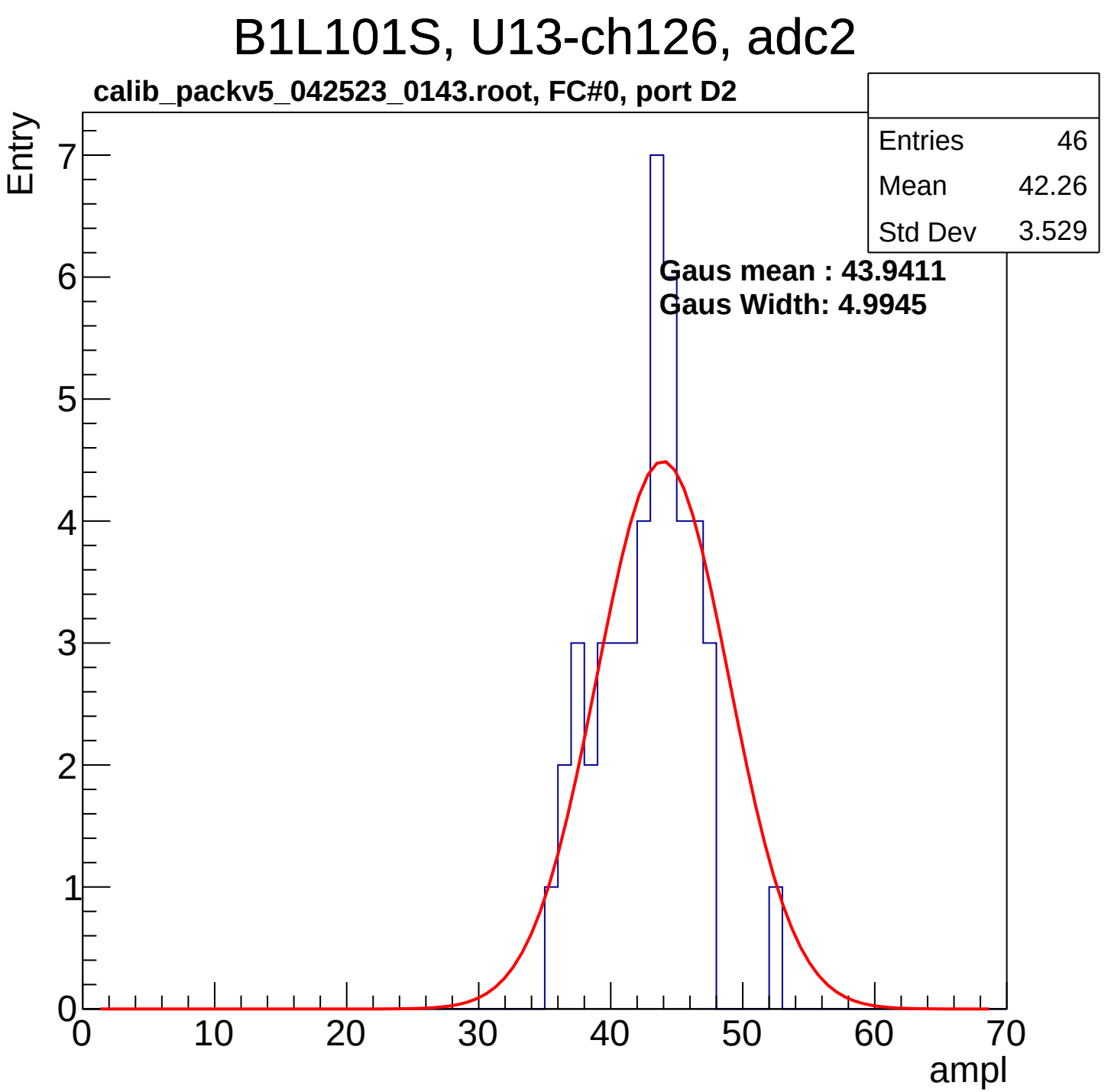
60
70


B1L101S, U13-ch126, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 46
Mean 42.26
Std Dev 3.529
Gaus mean : 43.9411
Gaus Width: 4.9945
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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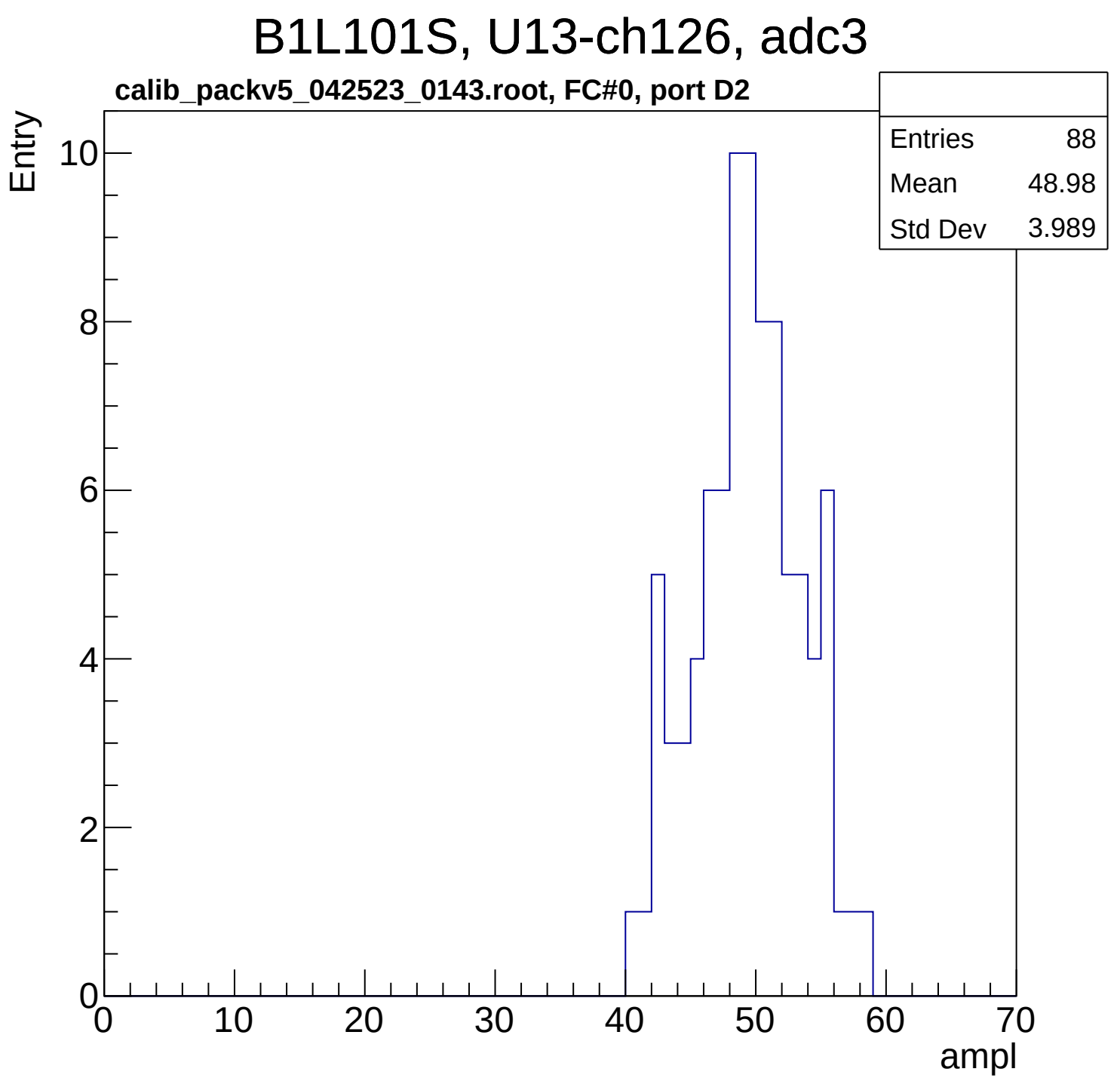
B1L101S, U13-ch126, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 88
Mean 48.98
Std Dev 3.989
Entry
ampl
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4
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8
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0
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40
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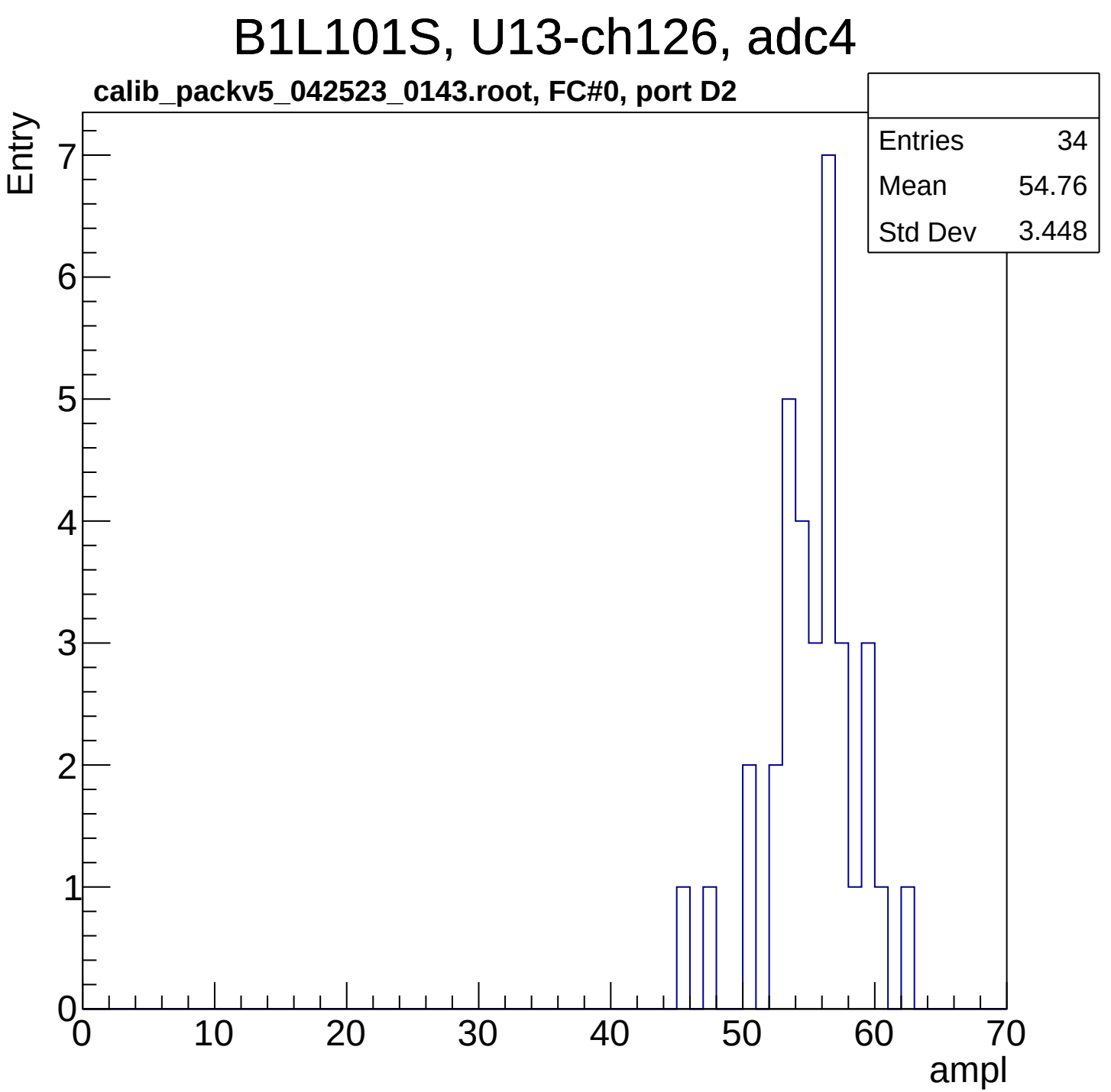
60
70


B1L101S, U13-ch126, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 34
Mean 54.76
Std Dev 3.448
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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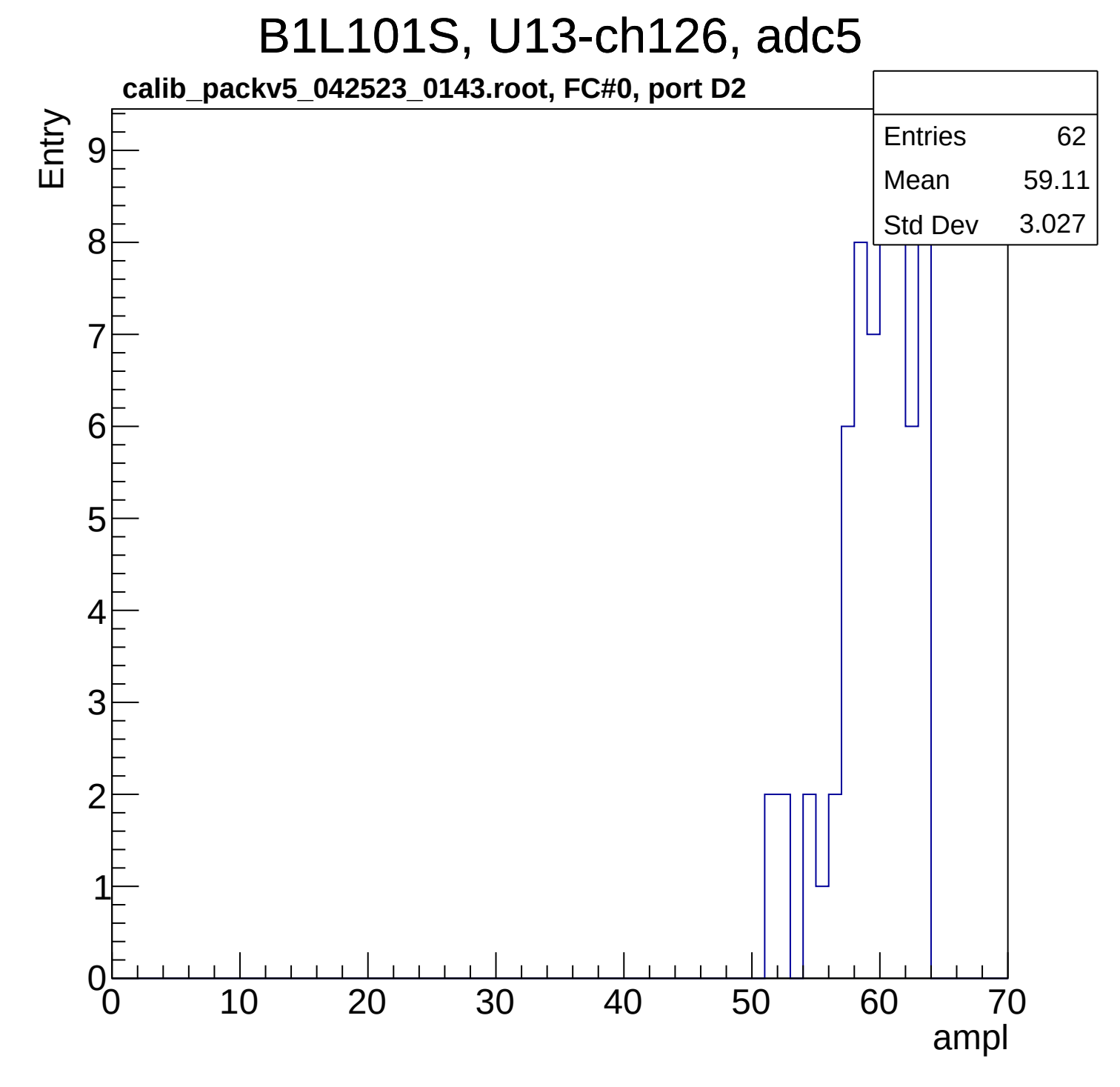
B1L101S, U13-ch126, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 59.11
Std Dev 3.027
Entry
ampl
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3
4
5
6
7
8
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0
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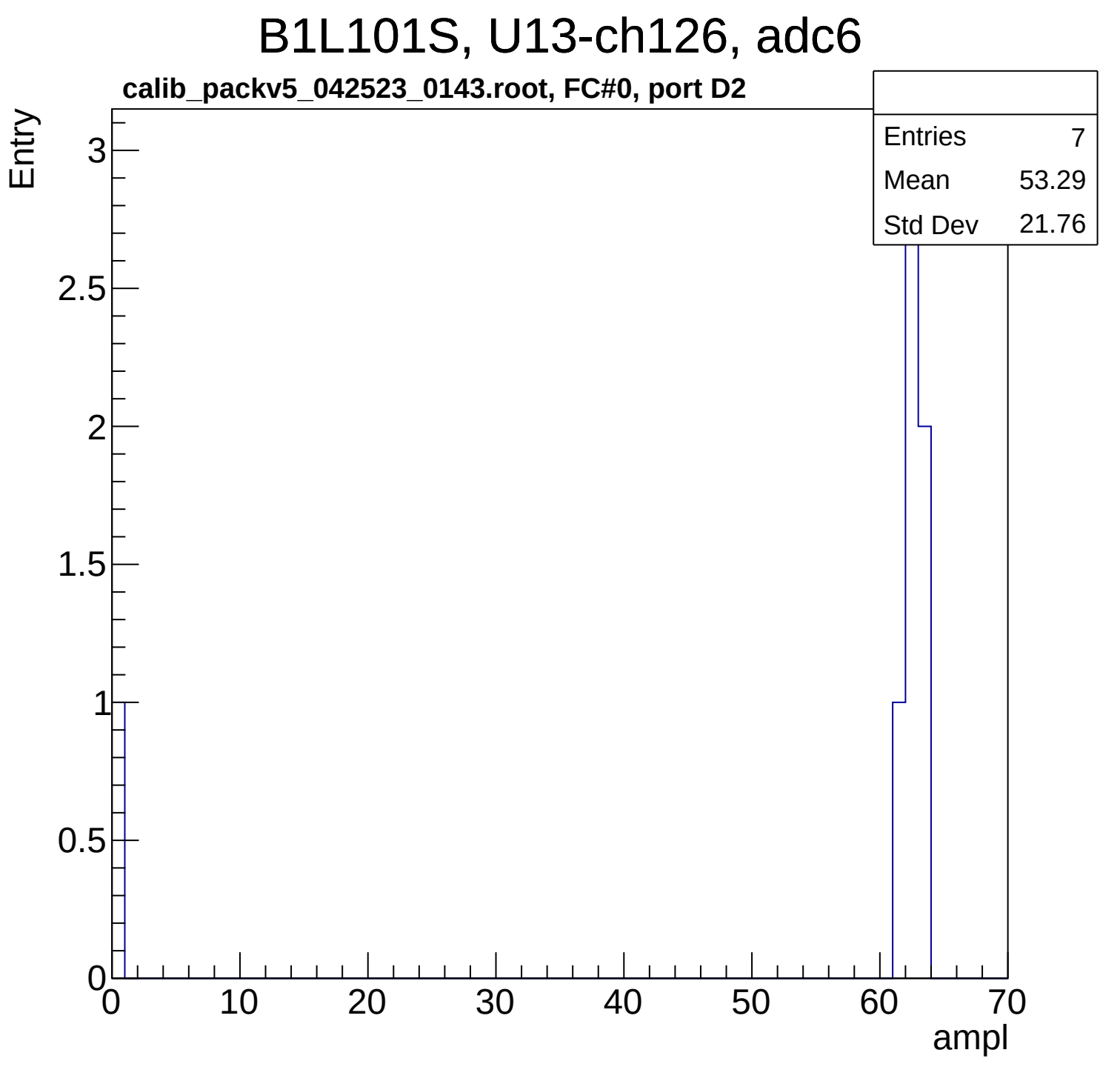
B1L101S, U13-ch126, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 7
Mean 53.29
Std Dev 21.76
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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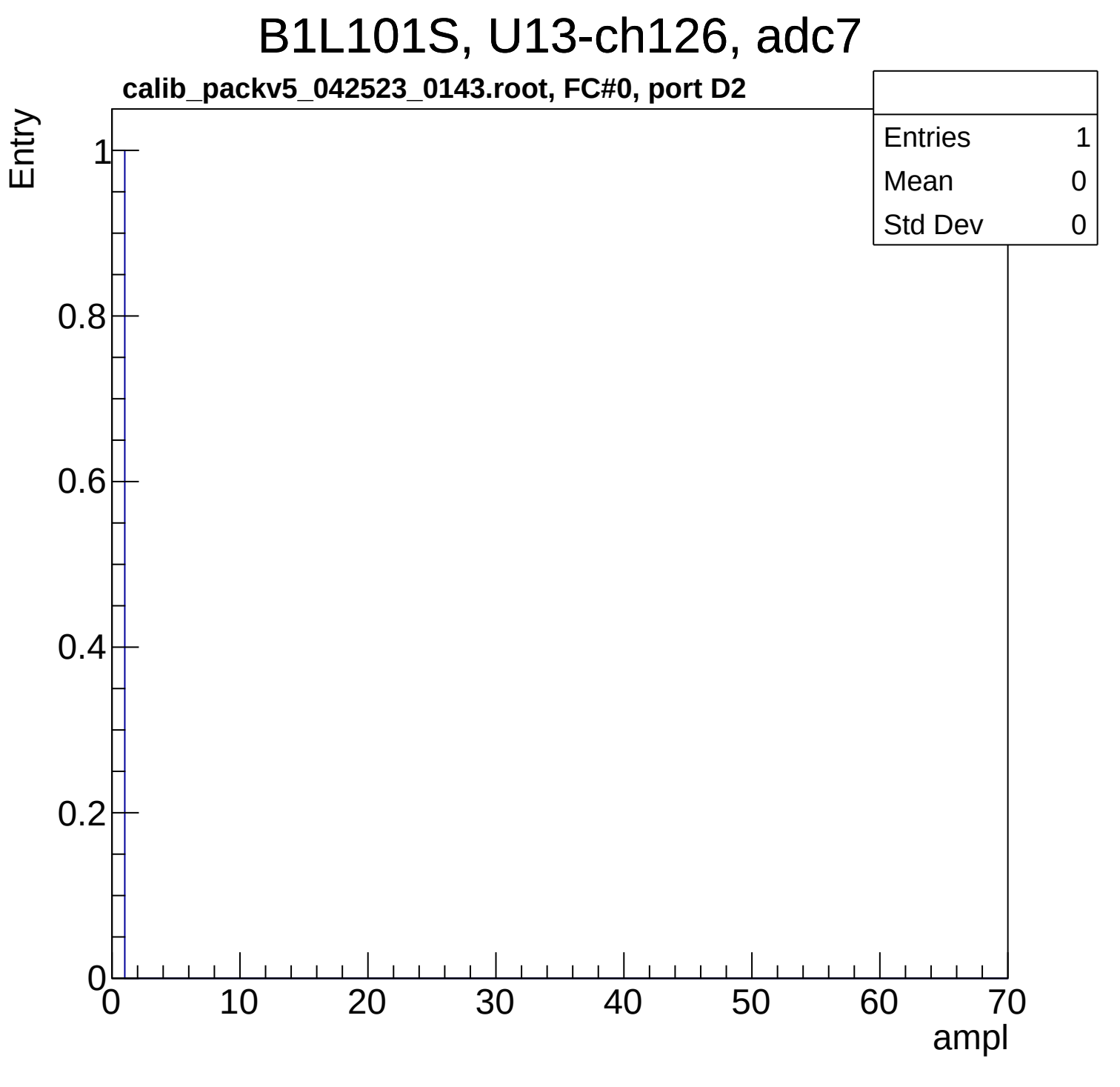
B1L101S, U13-ch126, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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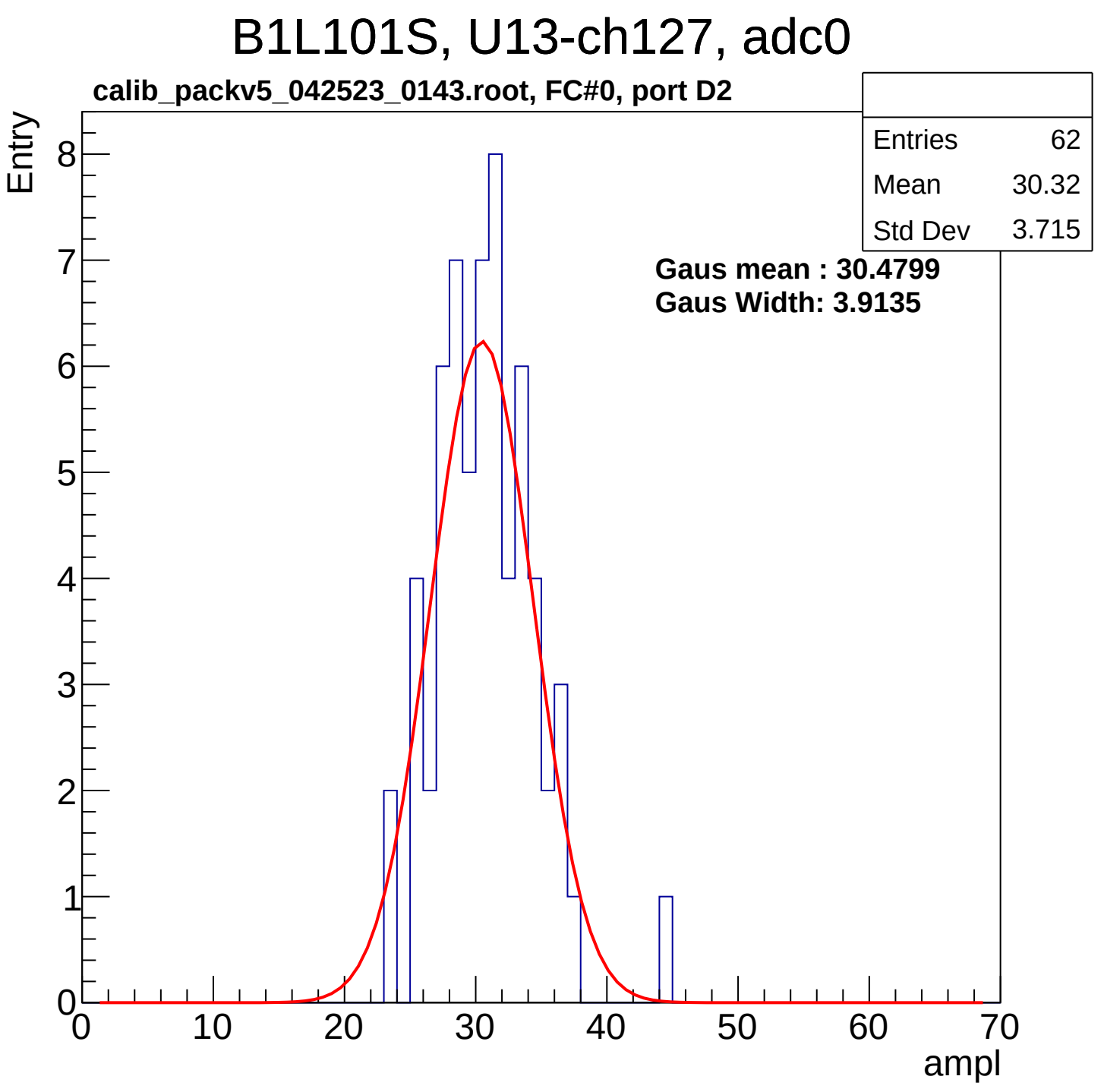
0
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch127, adc0
calib_packv5_042523_0143.root, FC#0, port D2
Entries 62
Mean 30.32
Std Dev 3.715
Gaus mean : 30.4799
Gaus Width: 3.9135
Entry
ampl
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1
2
3
4
5
6
7
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0
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50
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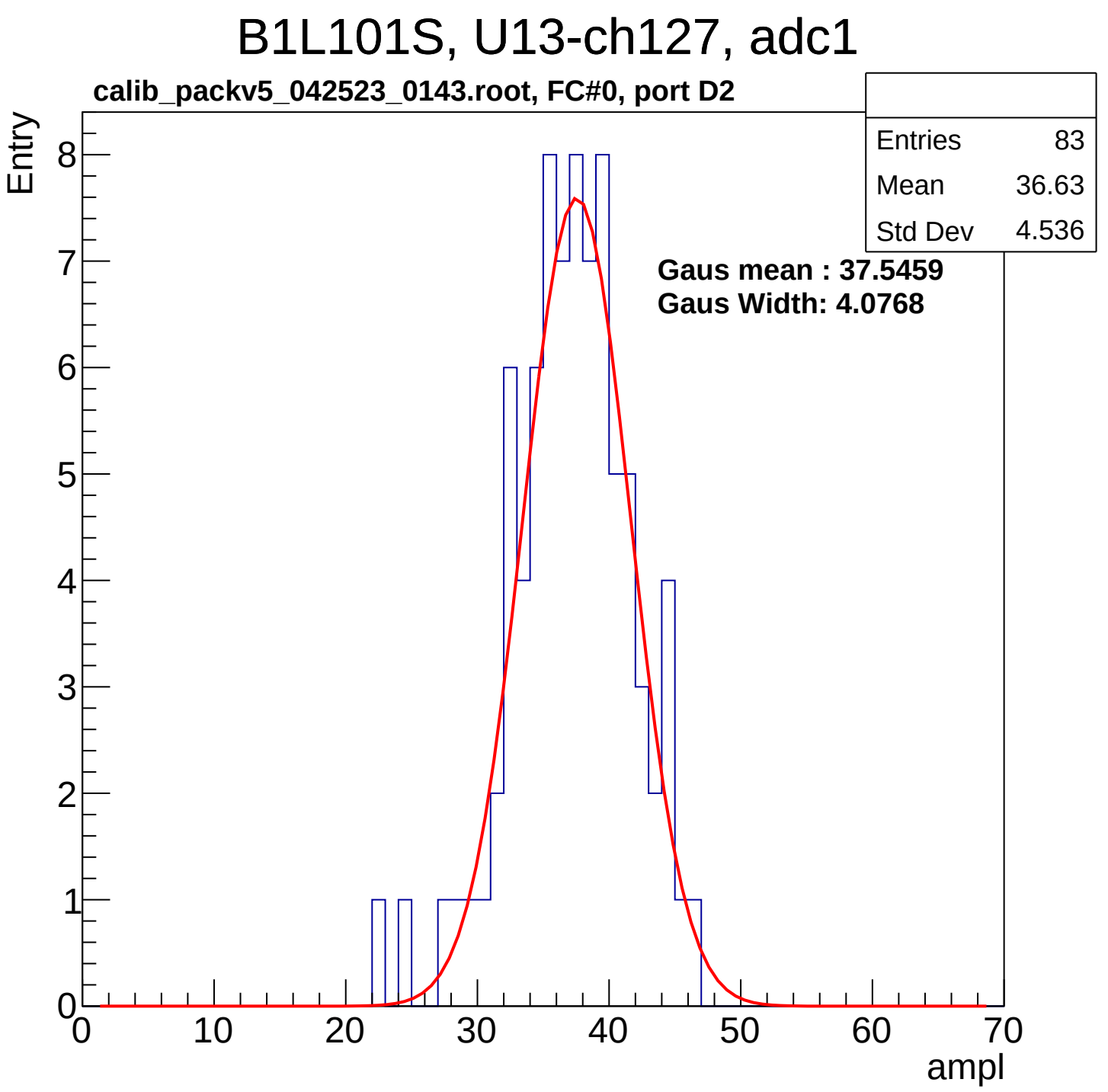
B1L101S, U13-ch127, adc1
calib_packv5_042523_0143.root, FC#0, port D2
Entries 83
Mean 36.63
Std Dev 4.536
Gaus mean : 37.5459
Gaus Width: 4.0768
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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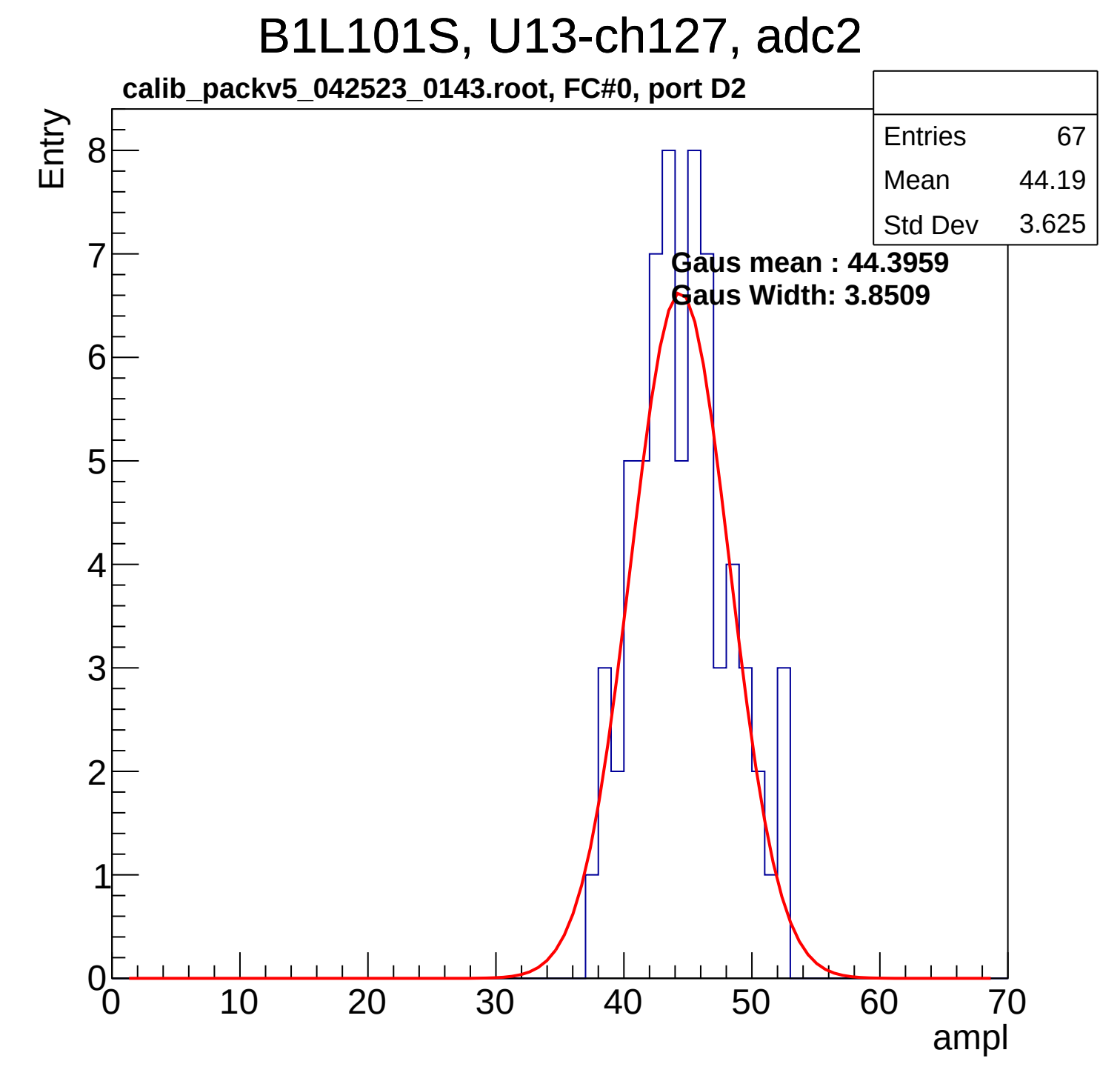
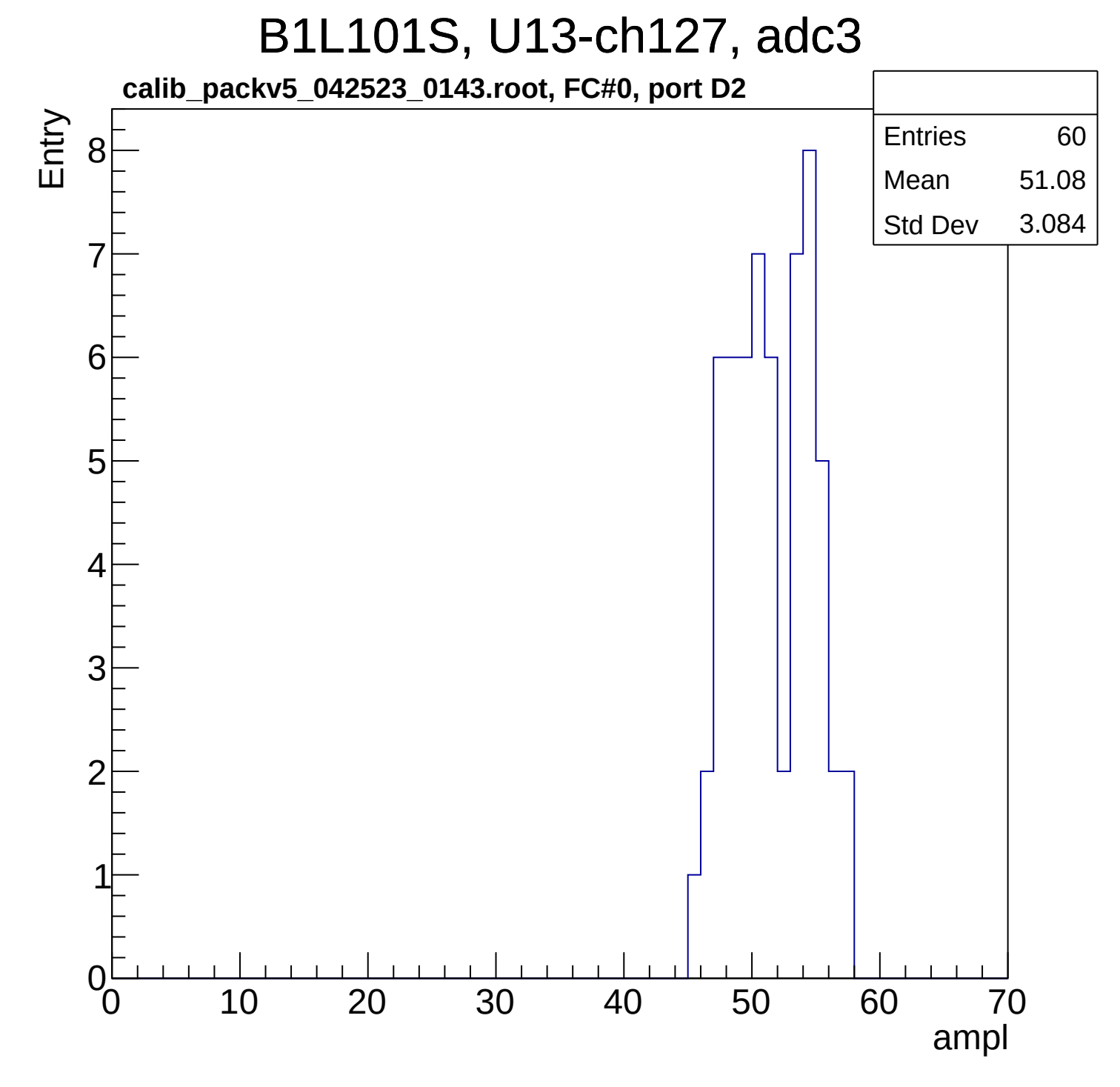
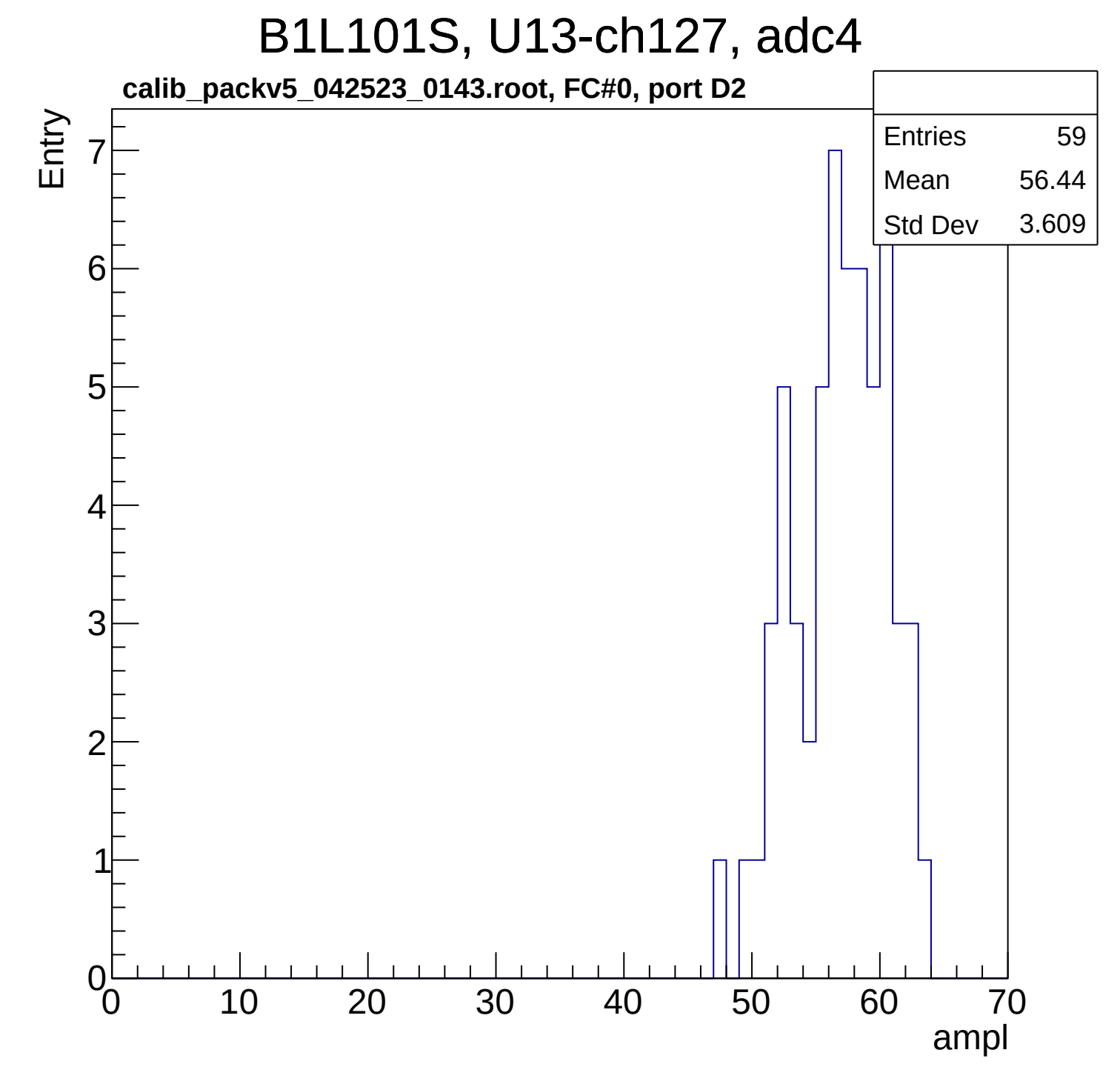
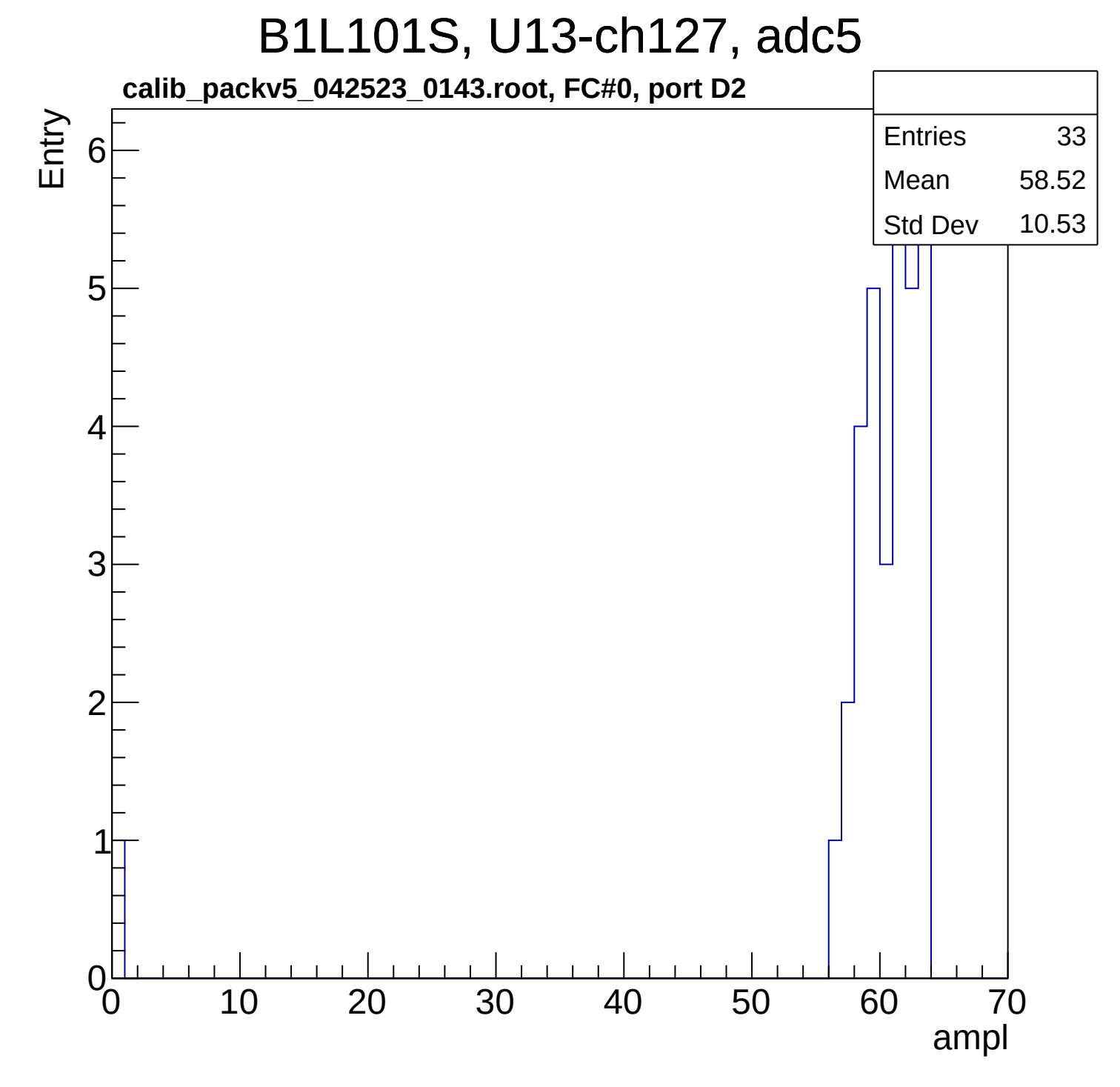
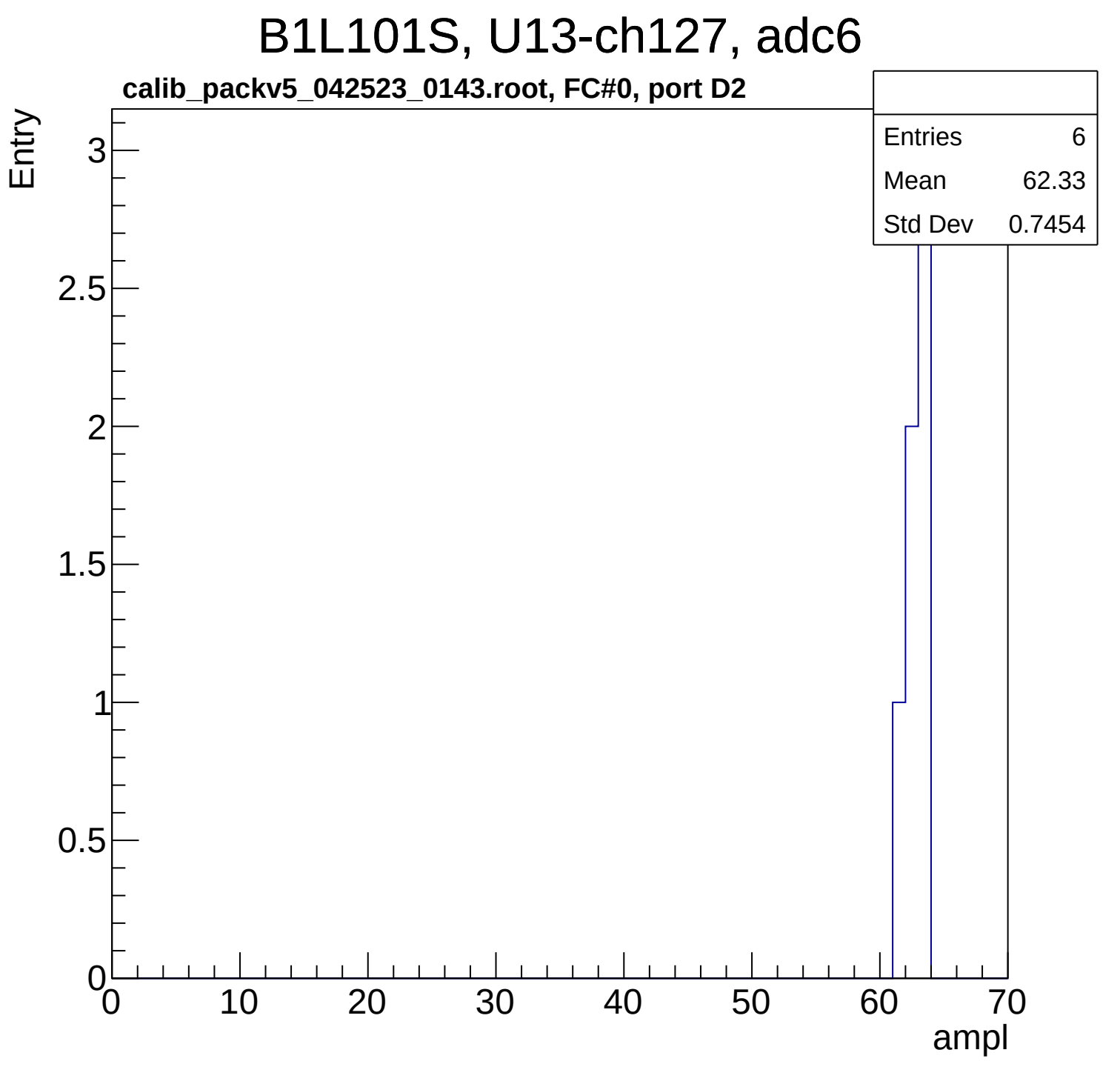
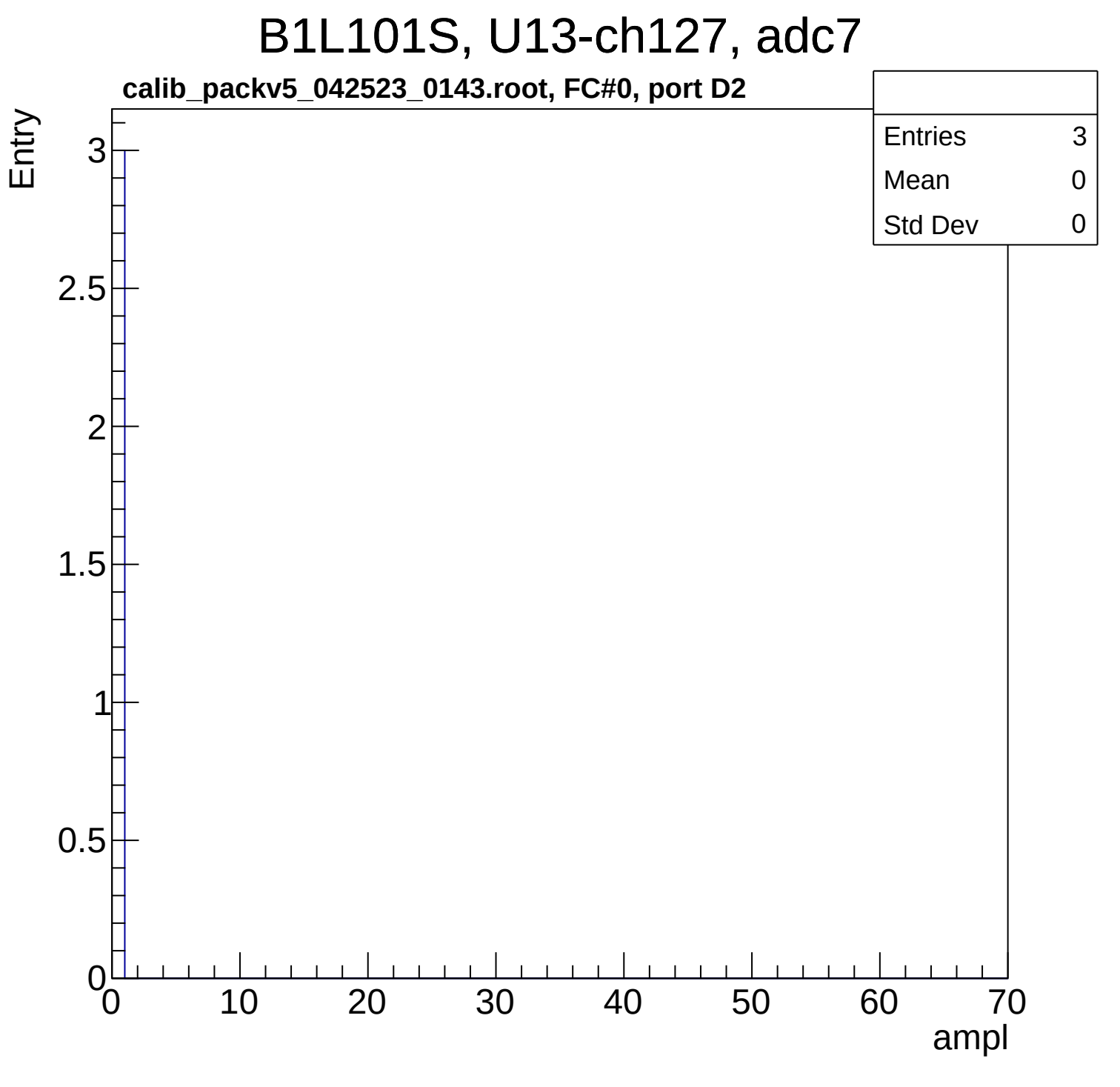

B1L101S, U13-ch127, adc2
calib_packv5_042523_0143.root, FC#0, port D2
Entries 67
Mean 44.19
Std Dev 3.625
Gaus mean : 44.3959
Gaus Width: 3.8509
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

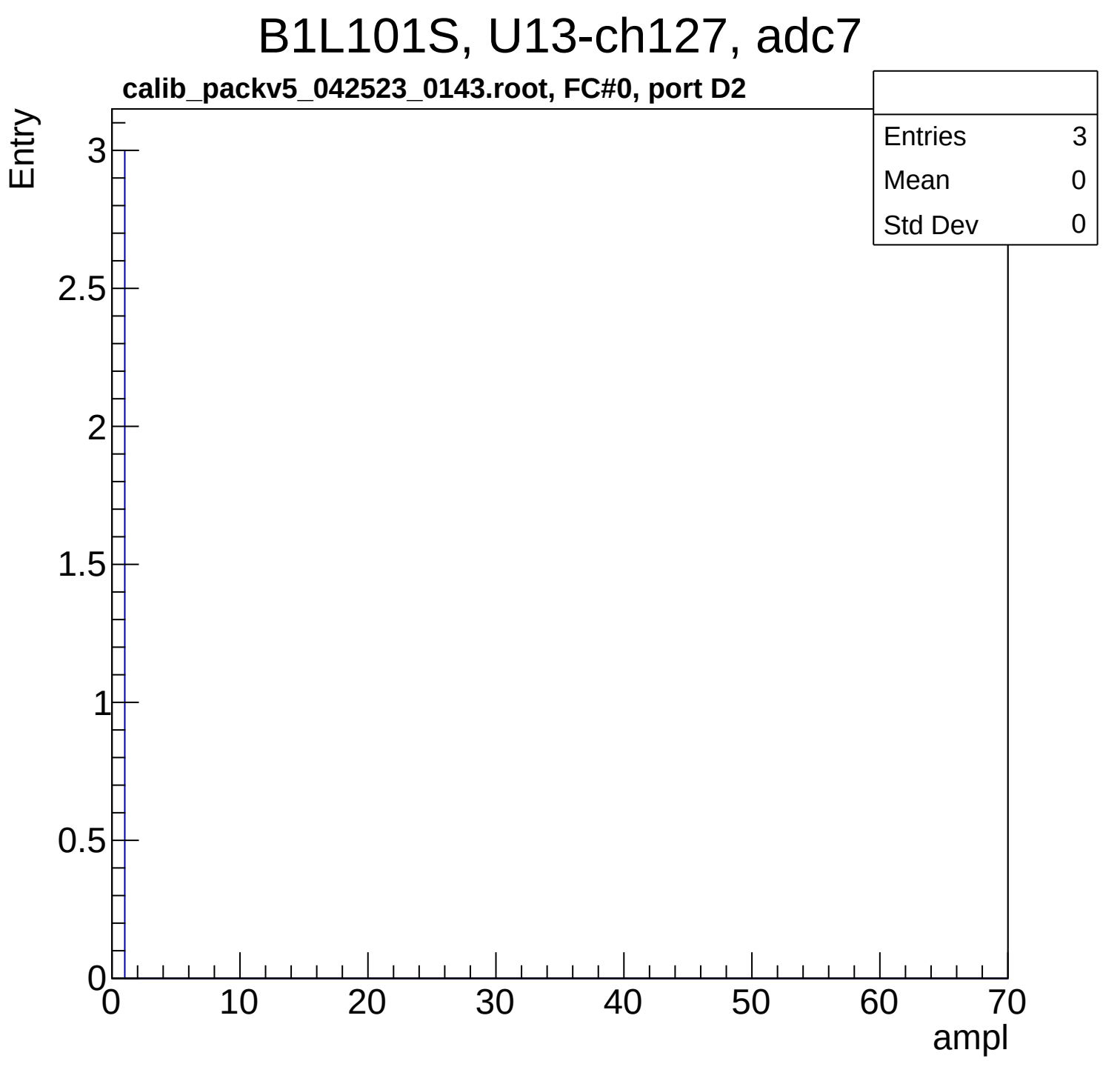

B1L101S, U13-ch127, adc3
calib_packv5_042523_0143.root, FC#0, port D2
Entries 60
Mean 51.08
Std Dev 3.084
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L101S, U13-ch127, adc4
calib_packv5_042523_0143.root, FC#0, port D2
Entries 59
Mean 56.44
Std Dev 3.609
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L101S, U13-ch127, adc5
calib_packv5_042523_0143.root, FC#0, port D2
Entries 33
Mean 58.52
Std Dev 10.53
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch127, adc6
calib_packv5_042523_0143.root, FC#0, port D2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L101S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L101S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#0, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl