

B1L104S, U14-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	31.7
Std Dev	28.43

Turn on : 52.8712

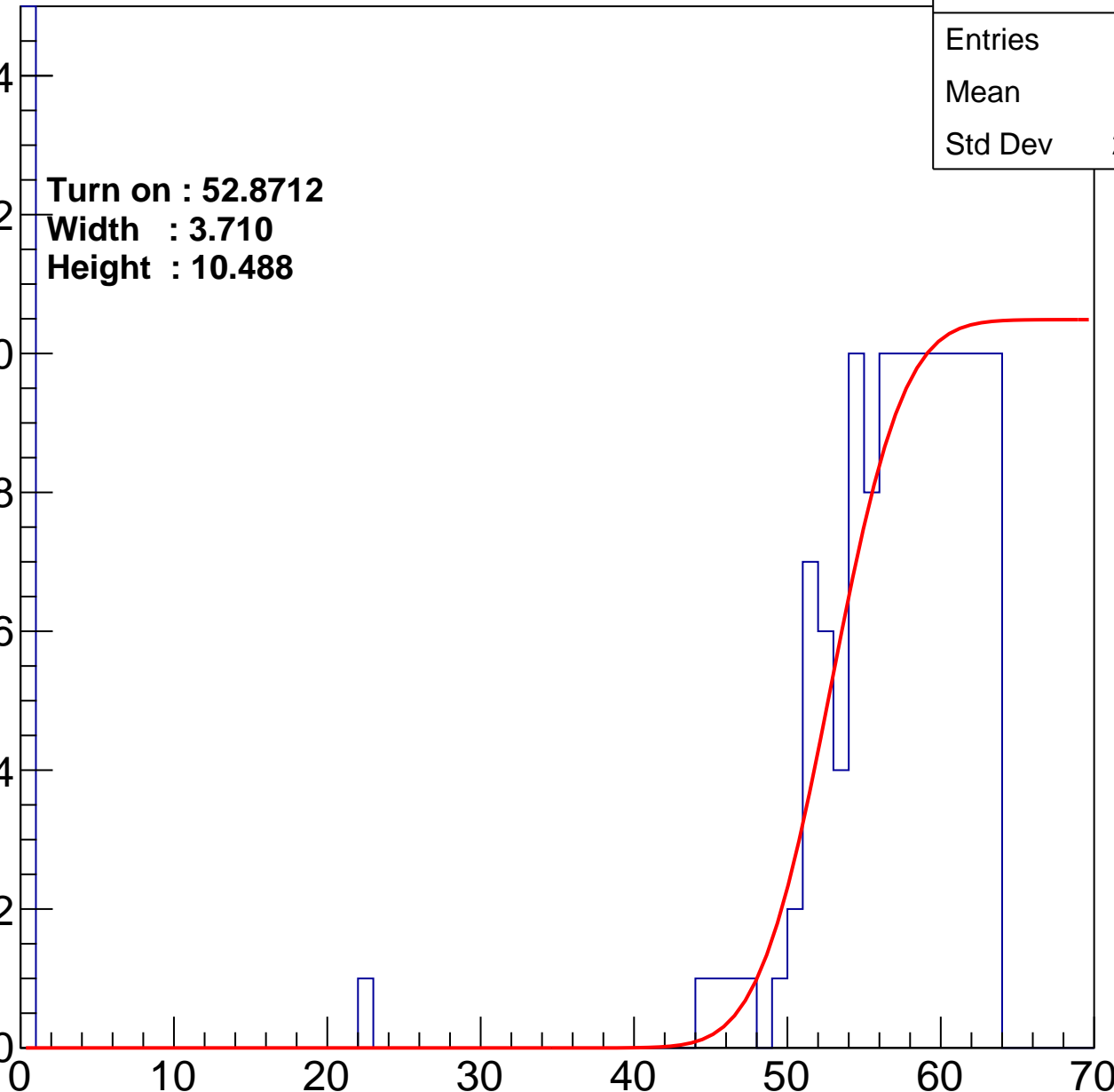
Width : 3.710

Height : 10.488

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	32.75
Std Dev	28.68

Turn on : 53.5078

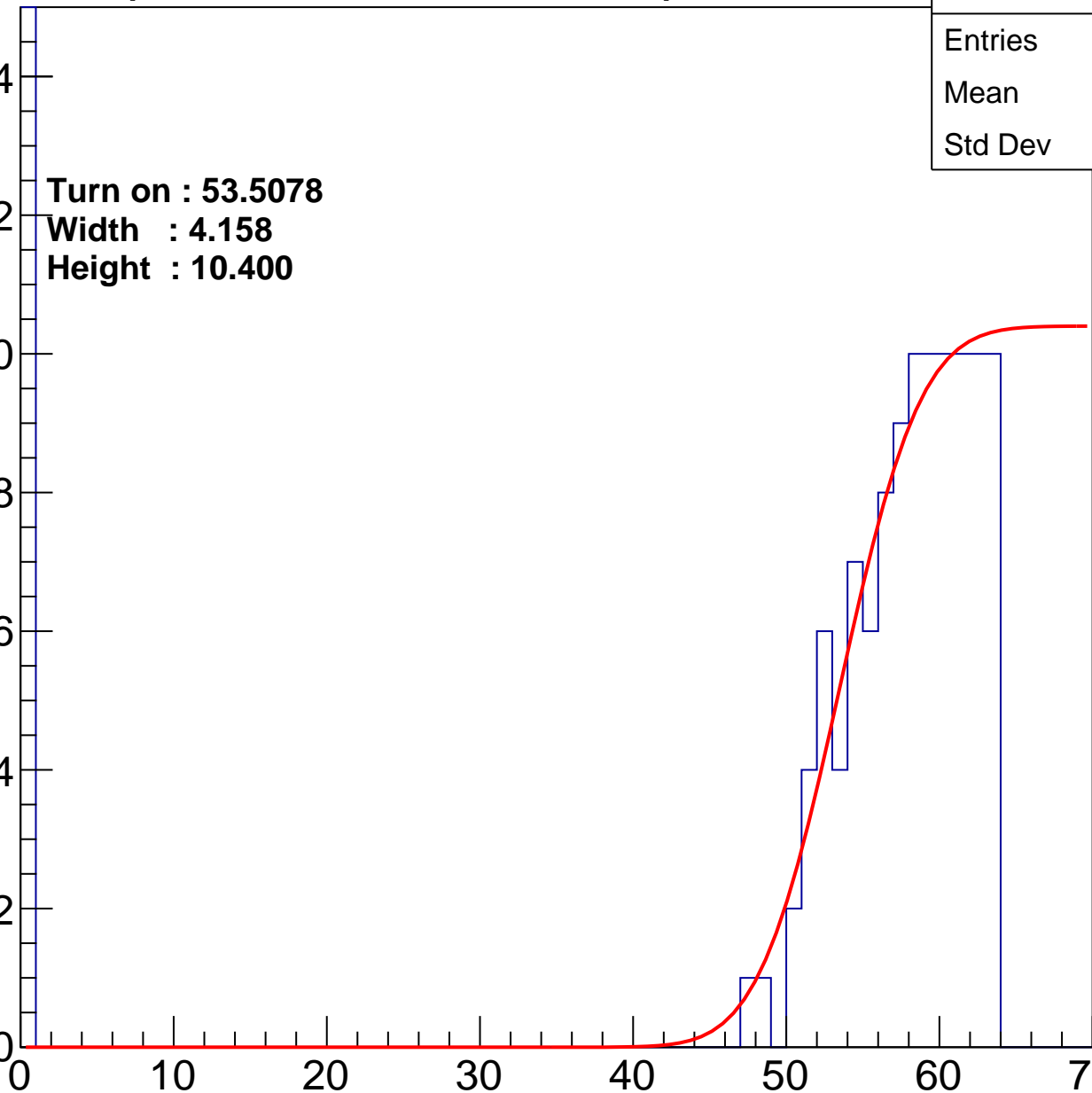
Width : 4.158

Height : 10.400

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	32.22
Std Dev	28.7

Turn on : 53.3485

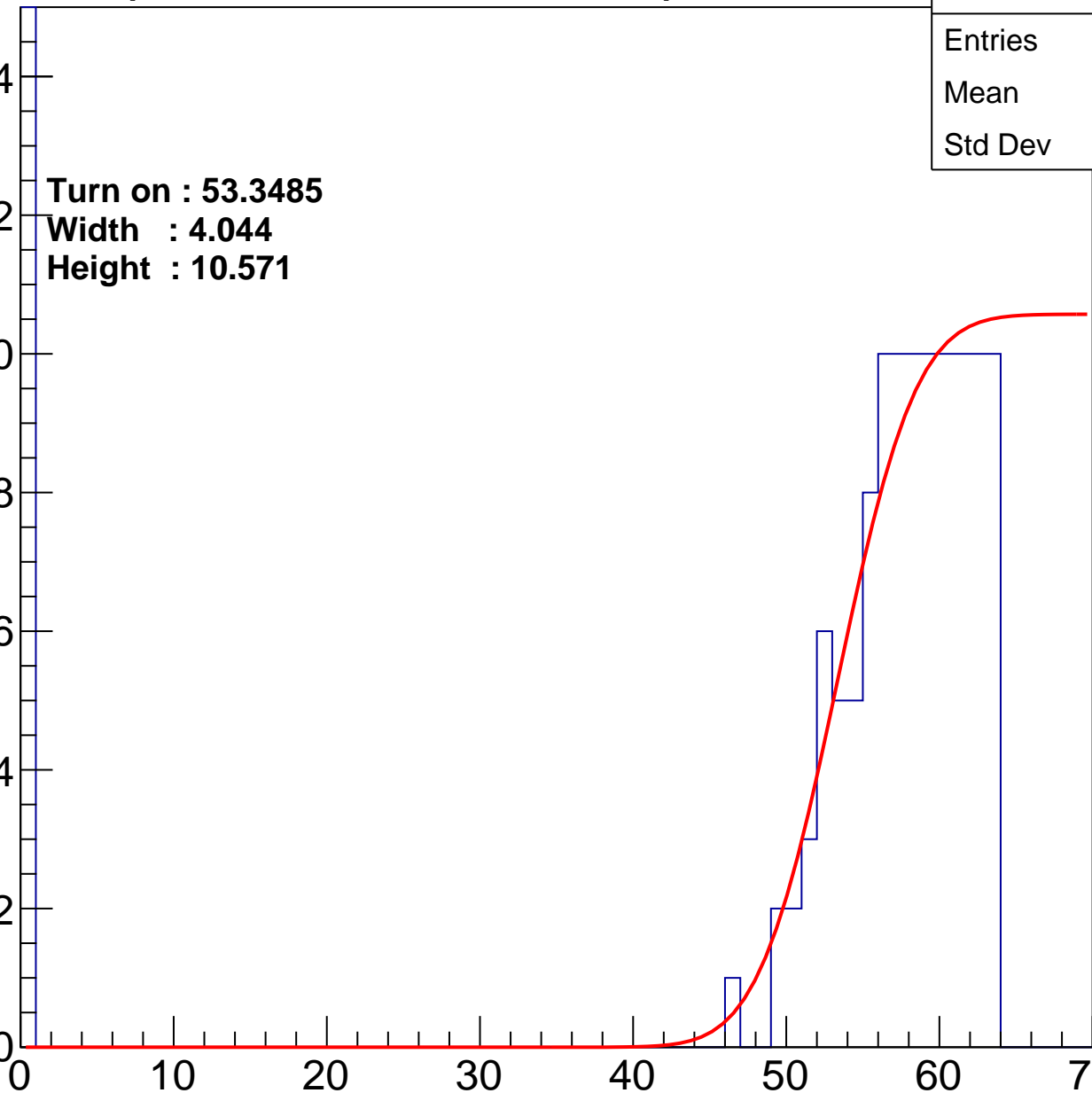
Width : 4.044

Height : 10.571

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	30.8
Std Dev	29.13

Turn on : 54.3664

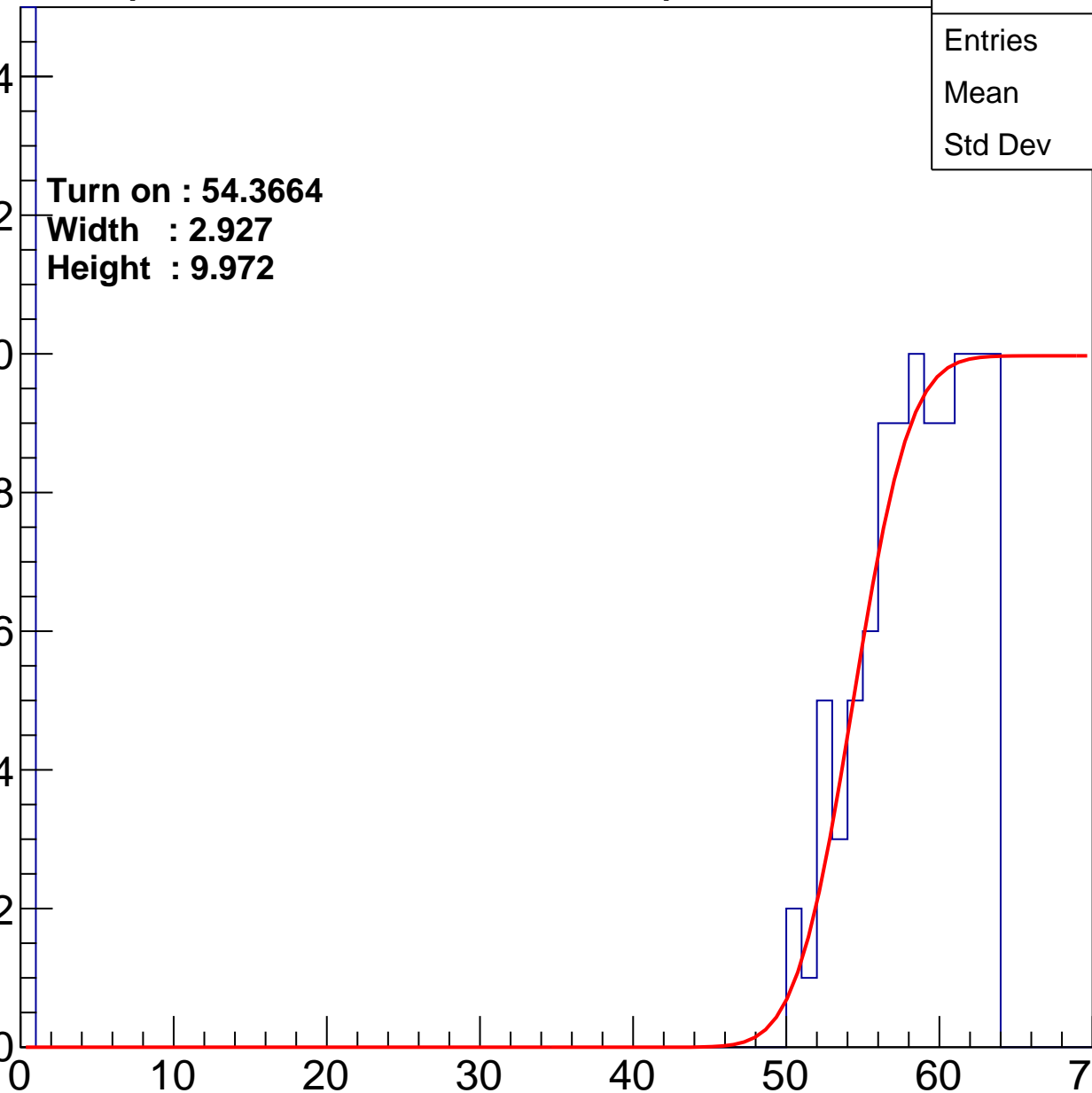
Width : 2.927

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	34.31
Std Dev	28.48

Turn on : 52.9211

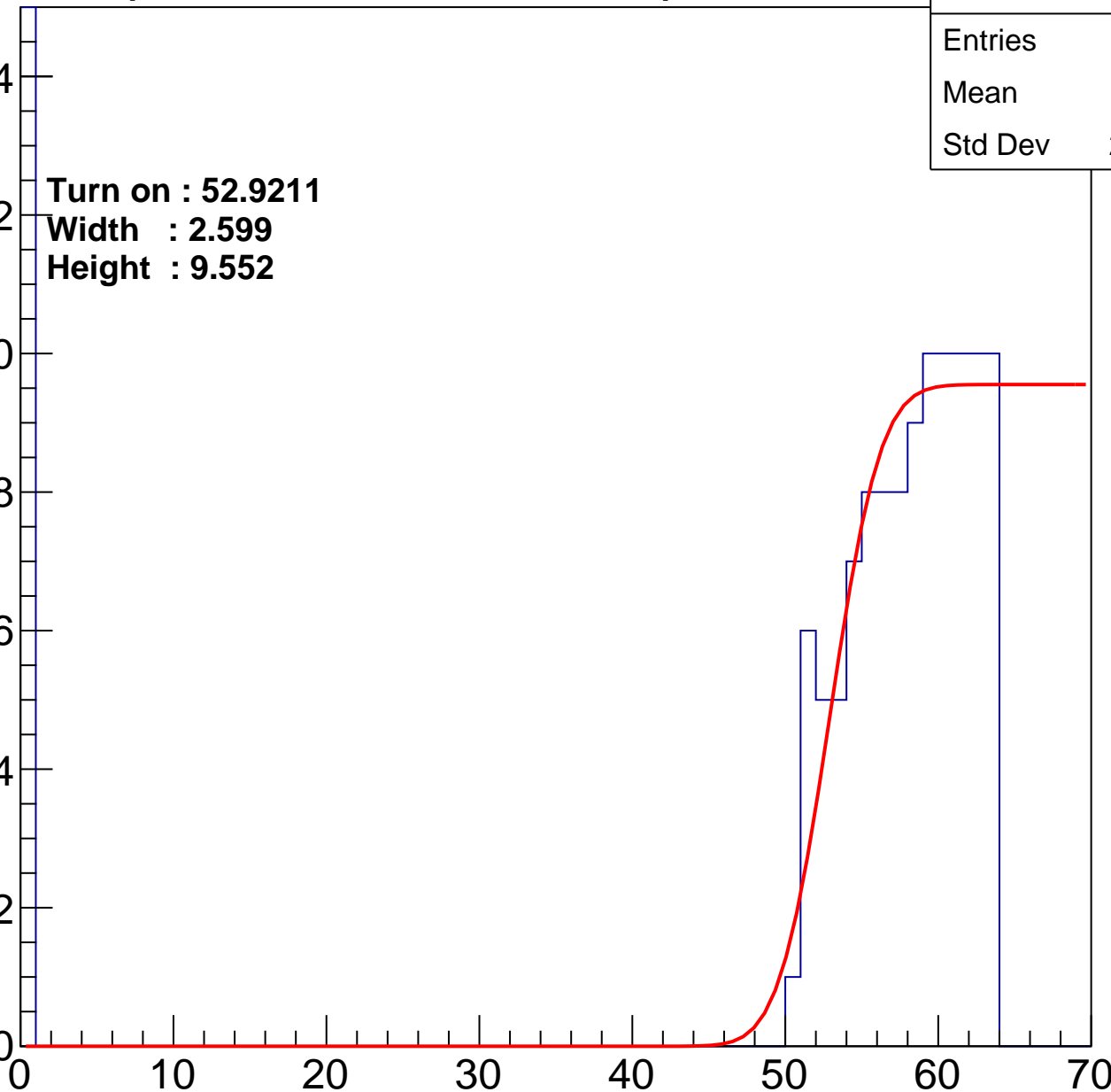
Width : 2.599

Height : 9.552

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	32.07
Std Dev	29.08

Turn on : 54.5587

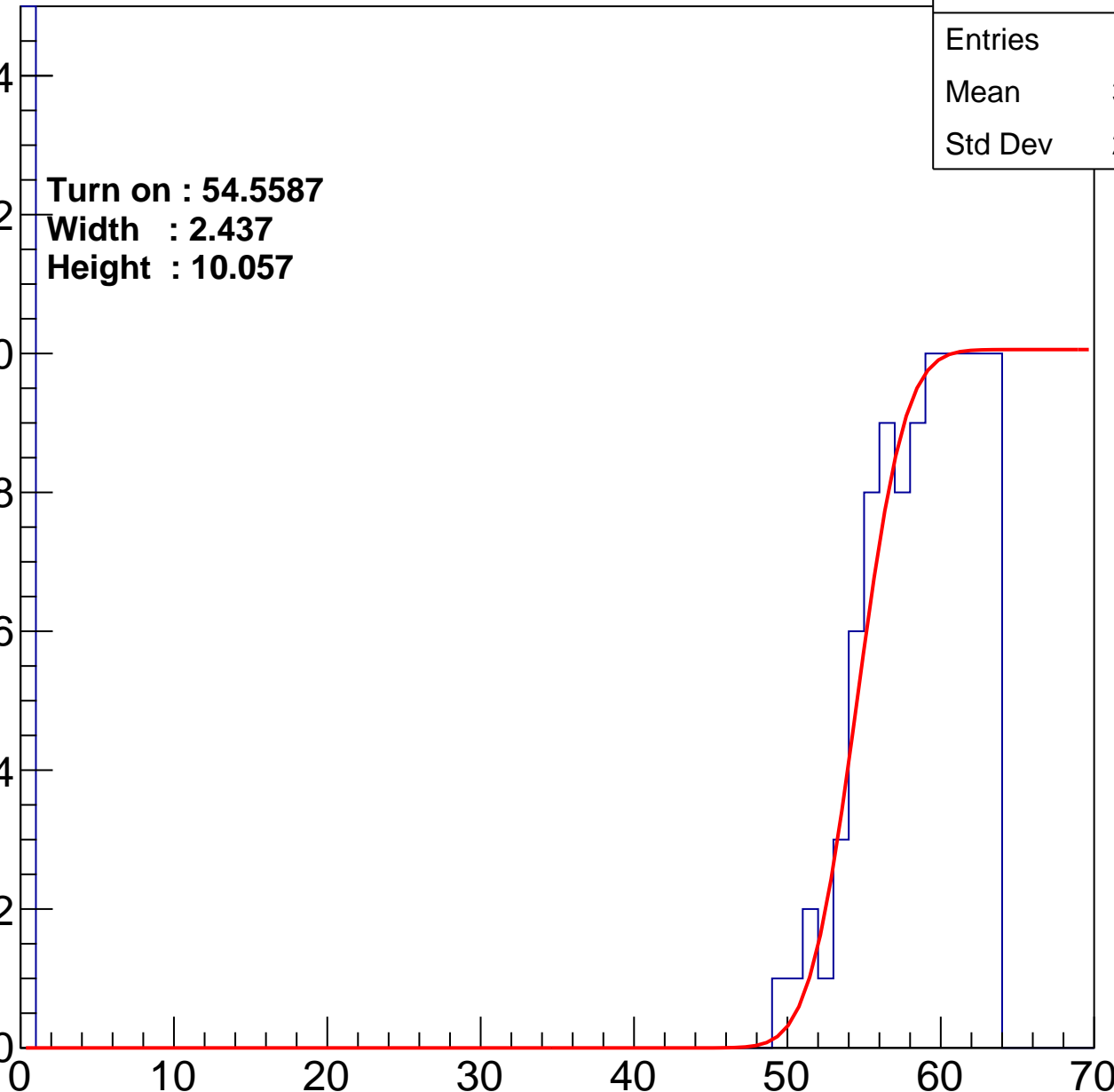
Width : 2.437

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	39.15
Std Dev	26.72

Turn on : 52.2098

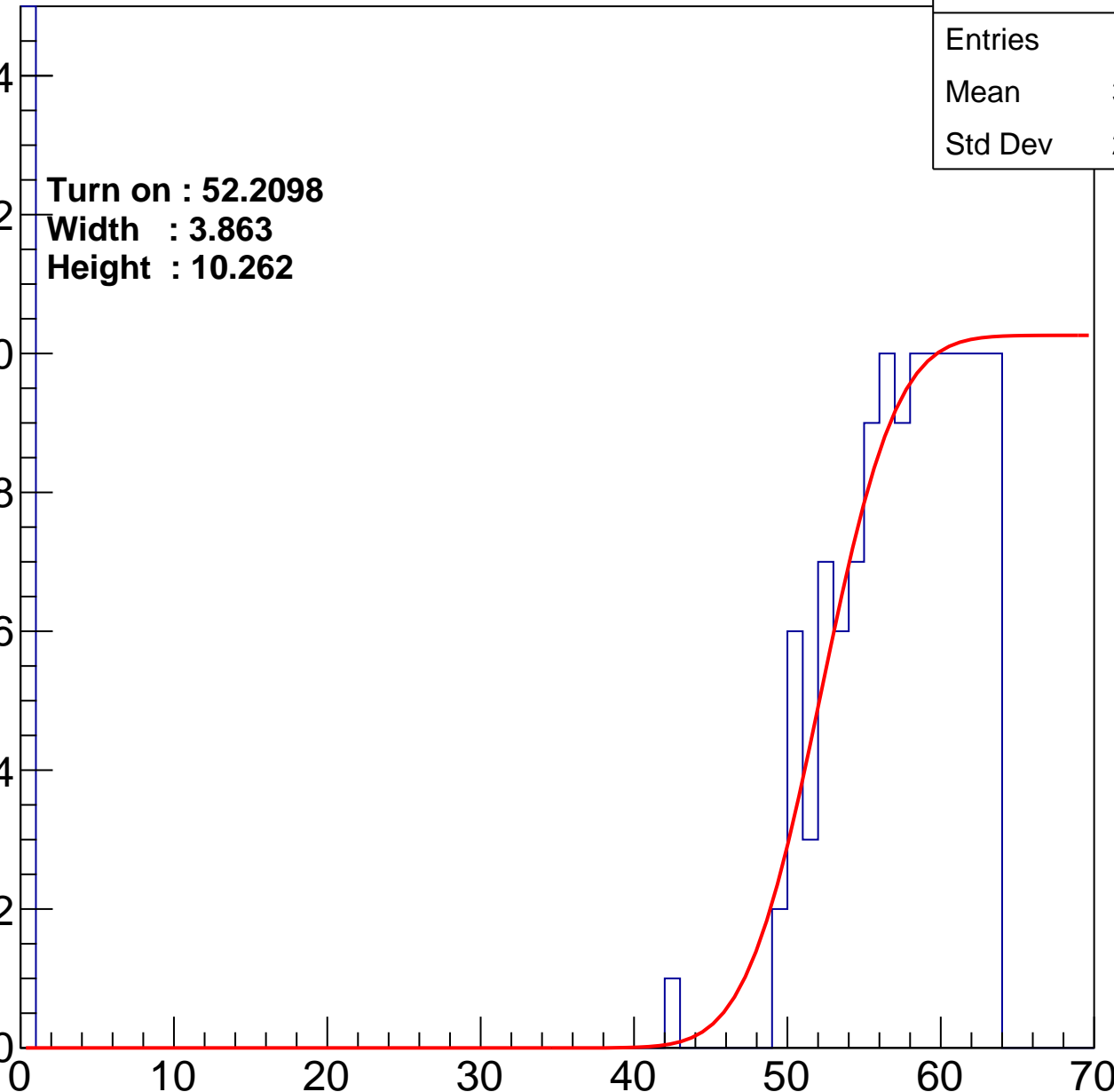
Width : 3.863

Height : 10.262

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	31.36
Std Dev	29.2

Turn on : 55.6046

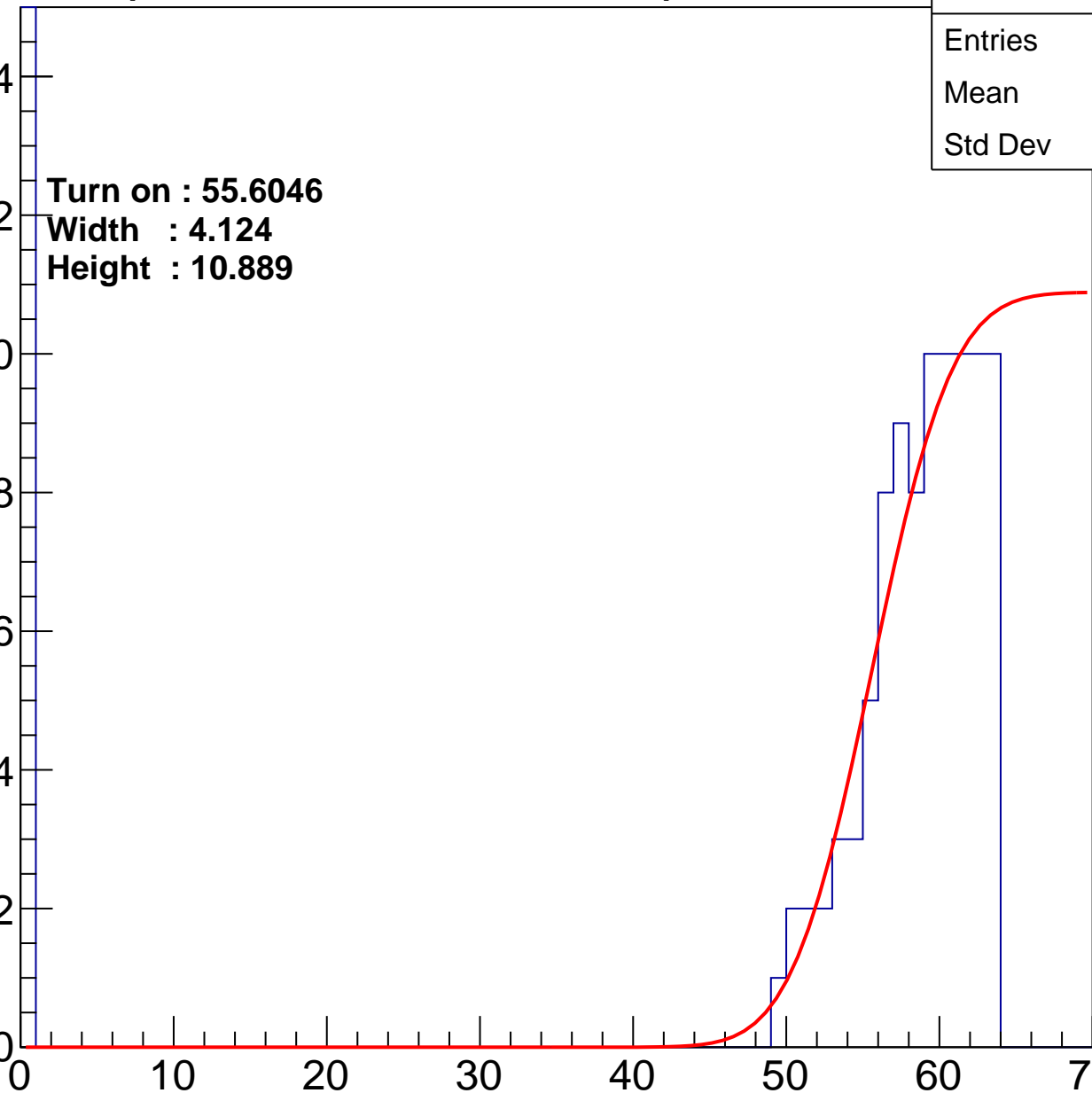
Width : 4.124

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	34.15
Std Dev	28.41

Turn on : 53.3257

Width : 3.602

Height : 10.278

Entry

14

12

10

8

6

4

2

0

0

10

20

30

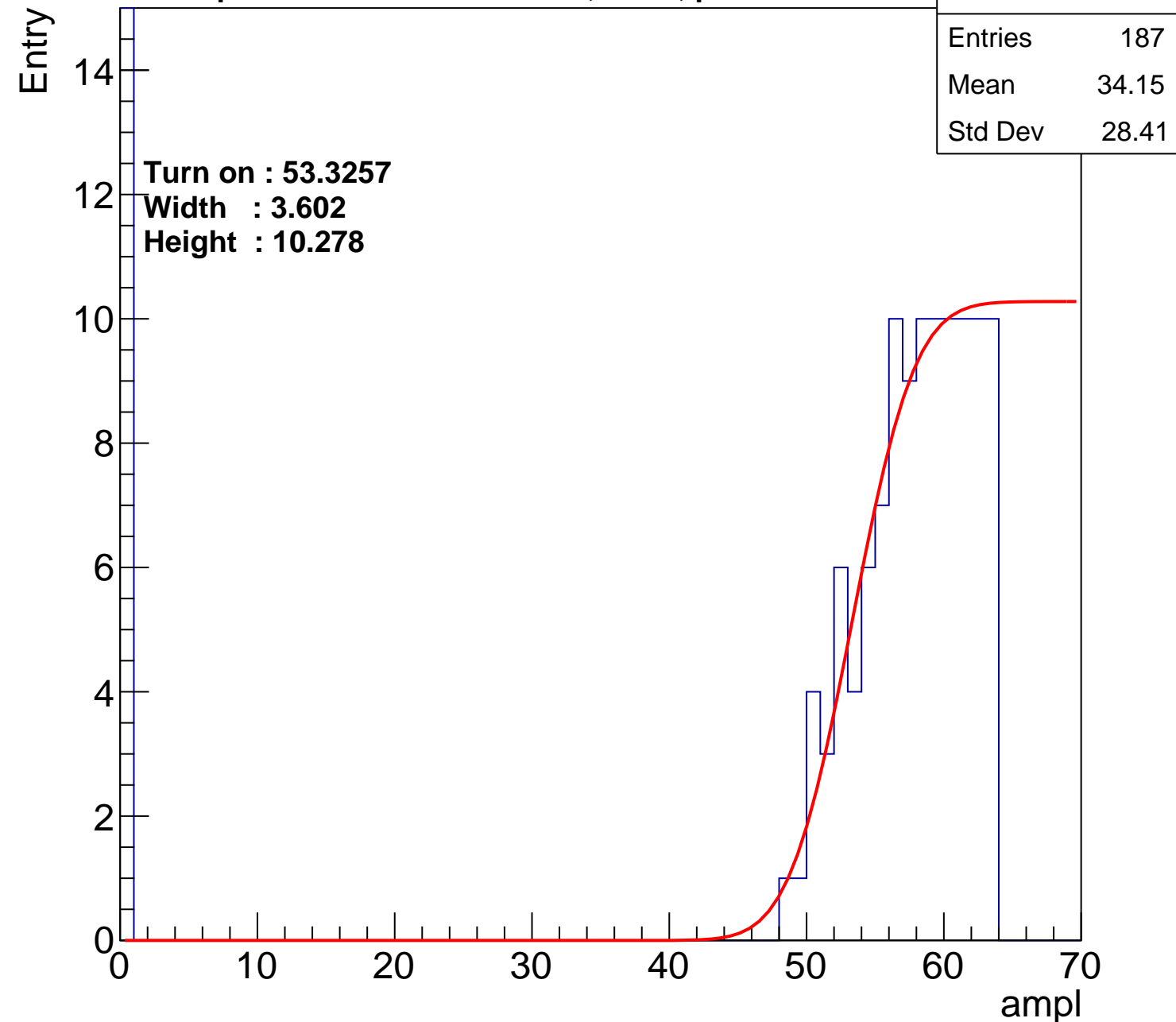
40

50

60

70

ampl



B1L104S, U14-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	35.06
Std Dev	28.48

Turn on : 54.1139

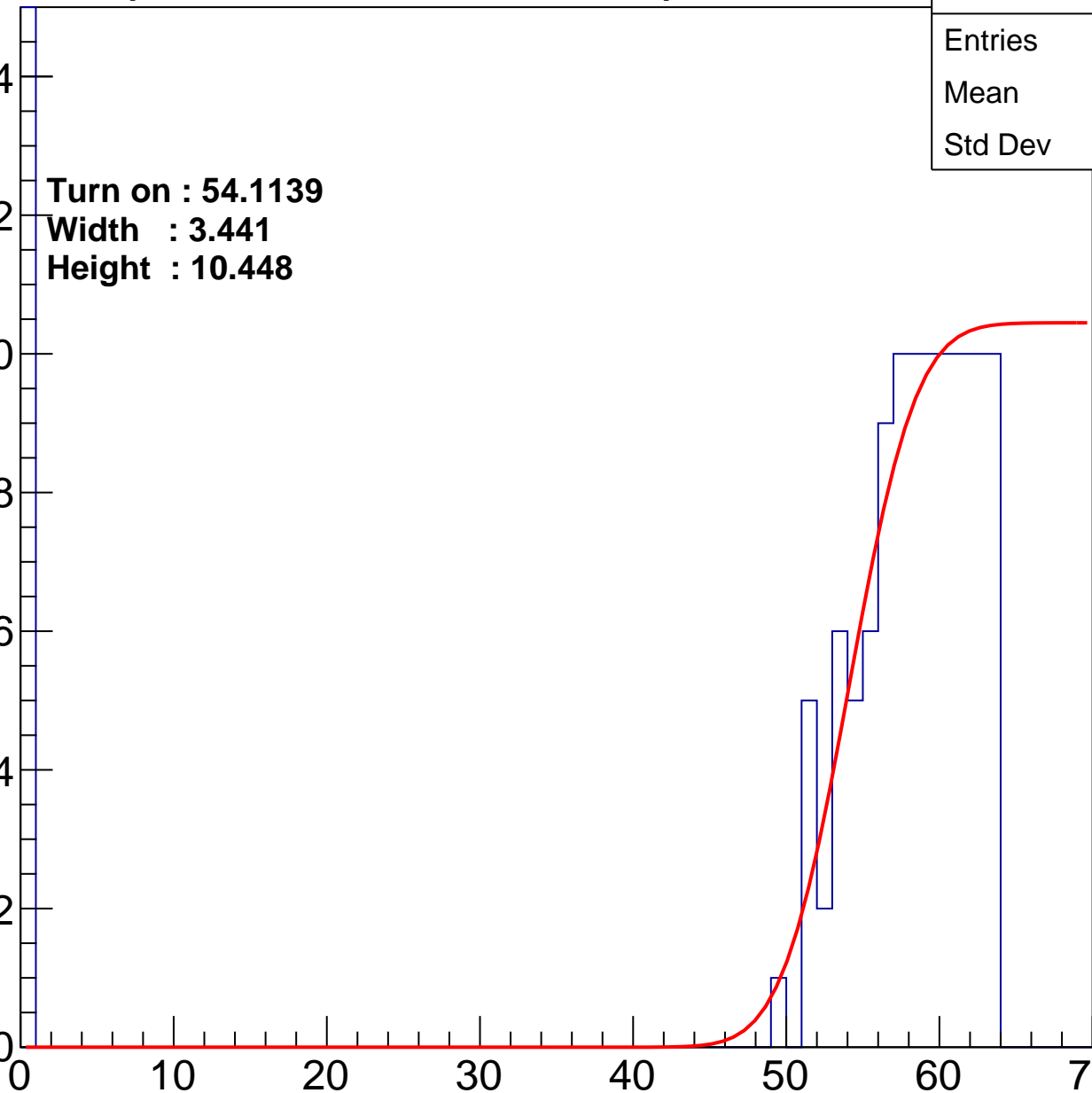
Width : 3.441

Height : 10.448

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	27.89
Std Dev	29.31

Turn on : 55.1265

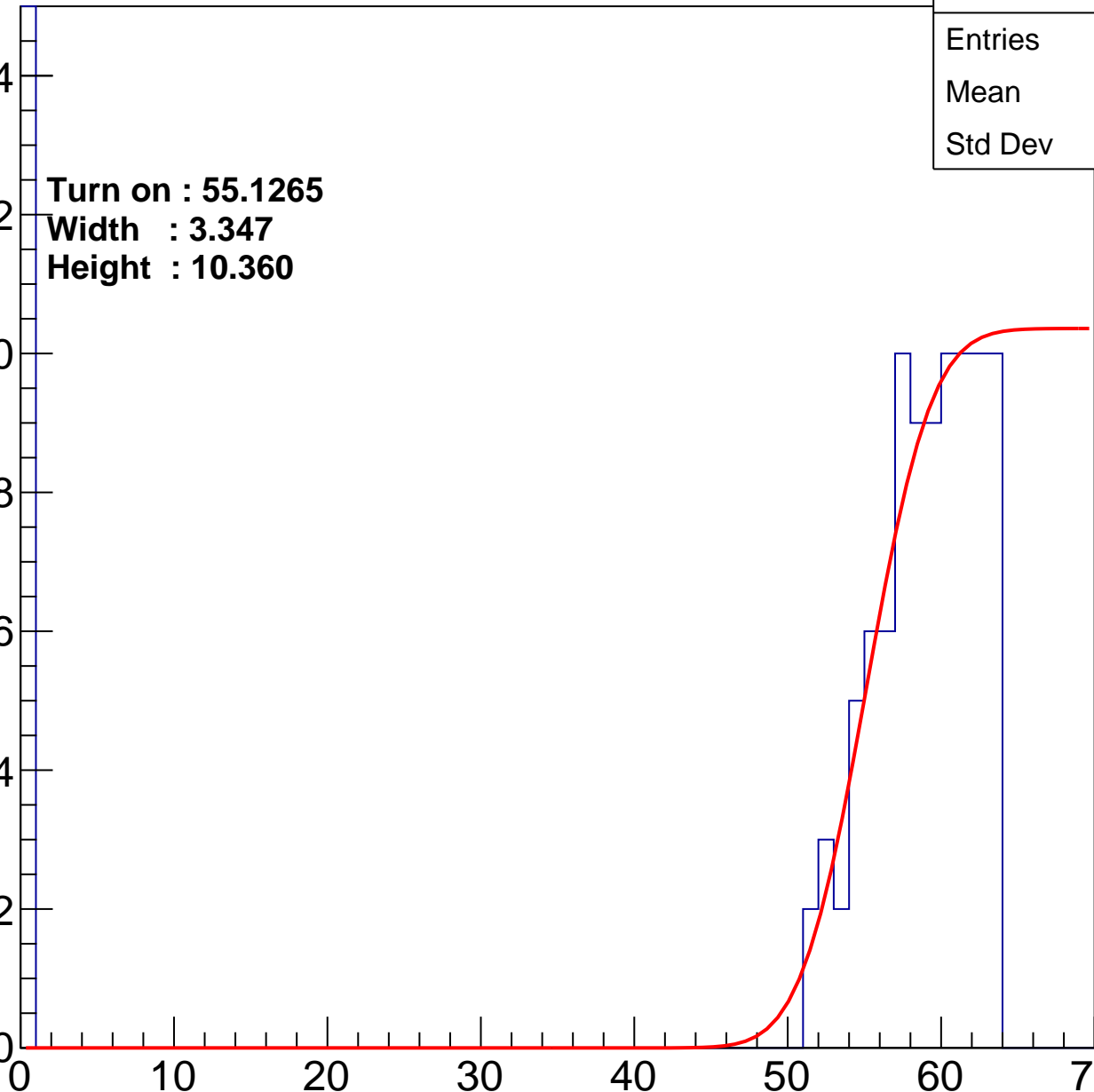
Width : 3.347

Height : 10.360

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	35.19
Std Dev	28.04

Turn on : 52.8222

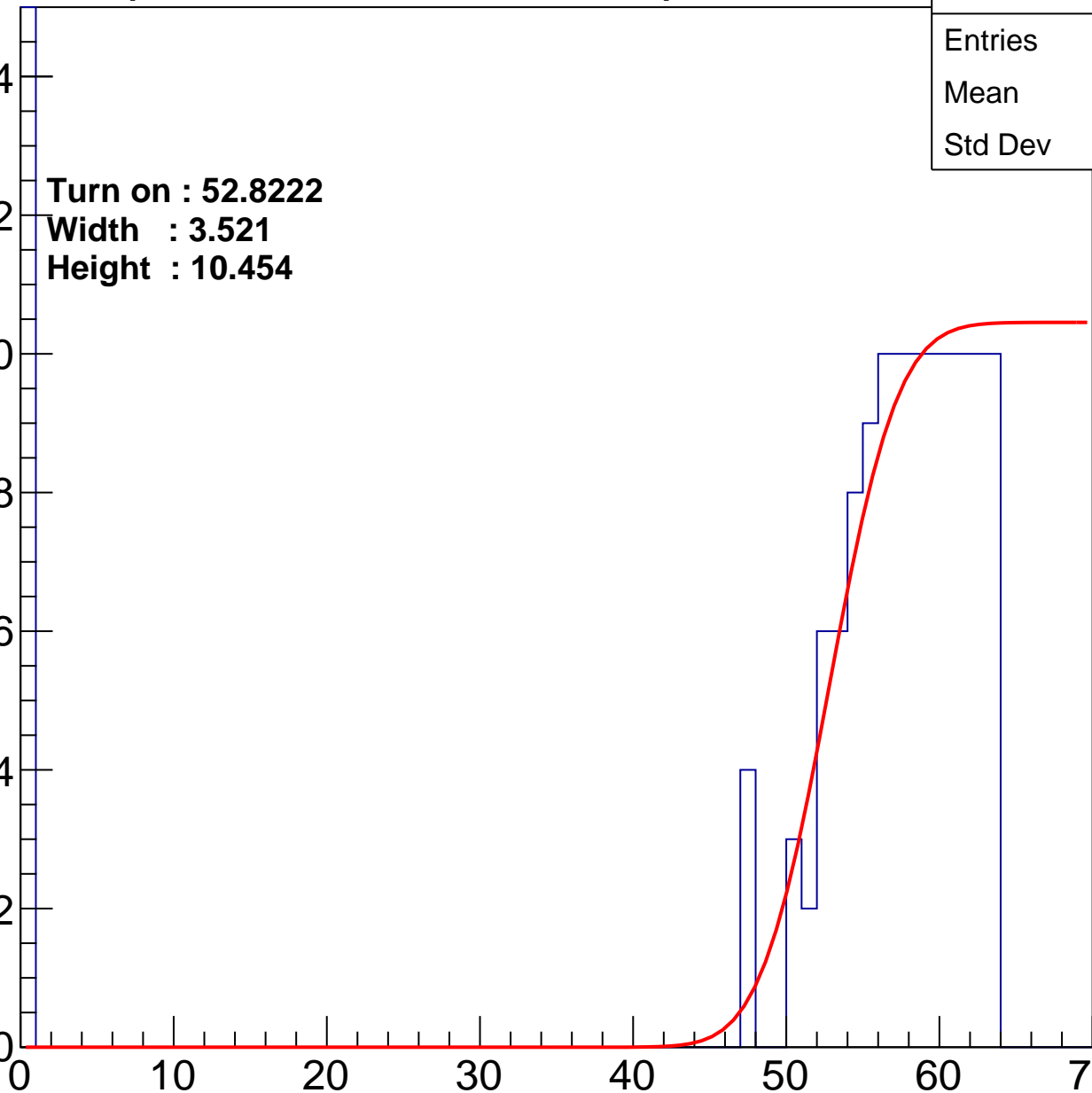
Width : 3.521

Height : 10.454

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch12

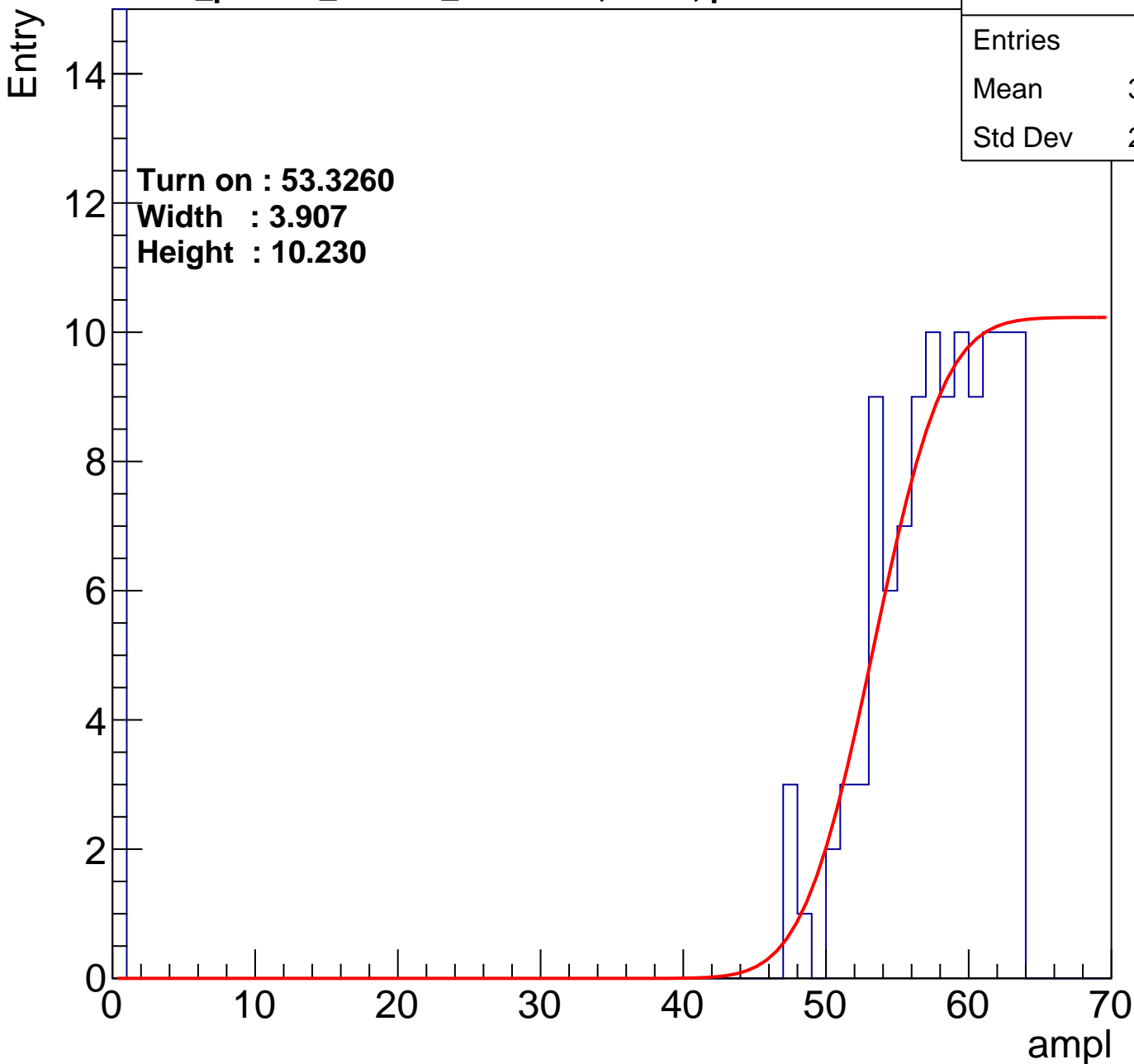
calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	34.43
Std Dev	28.28

Turn on : 53.3260

Width : 3.907

Height : 10.230



B1L104S, U14-ch13

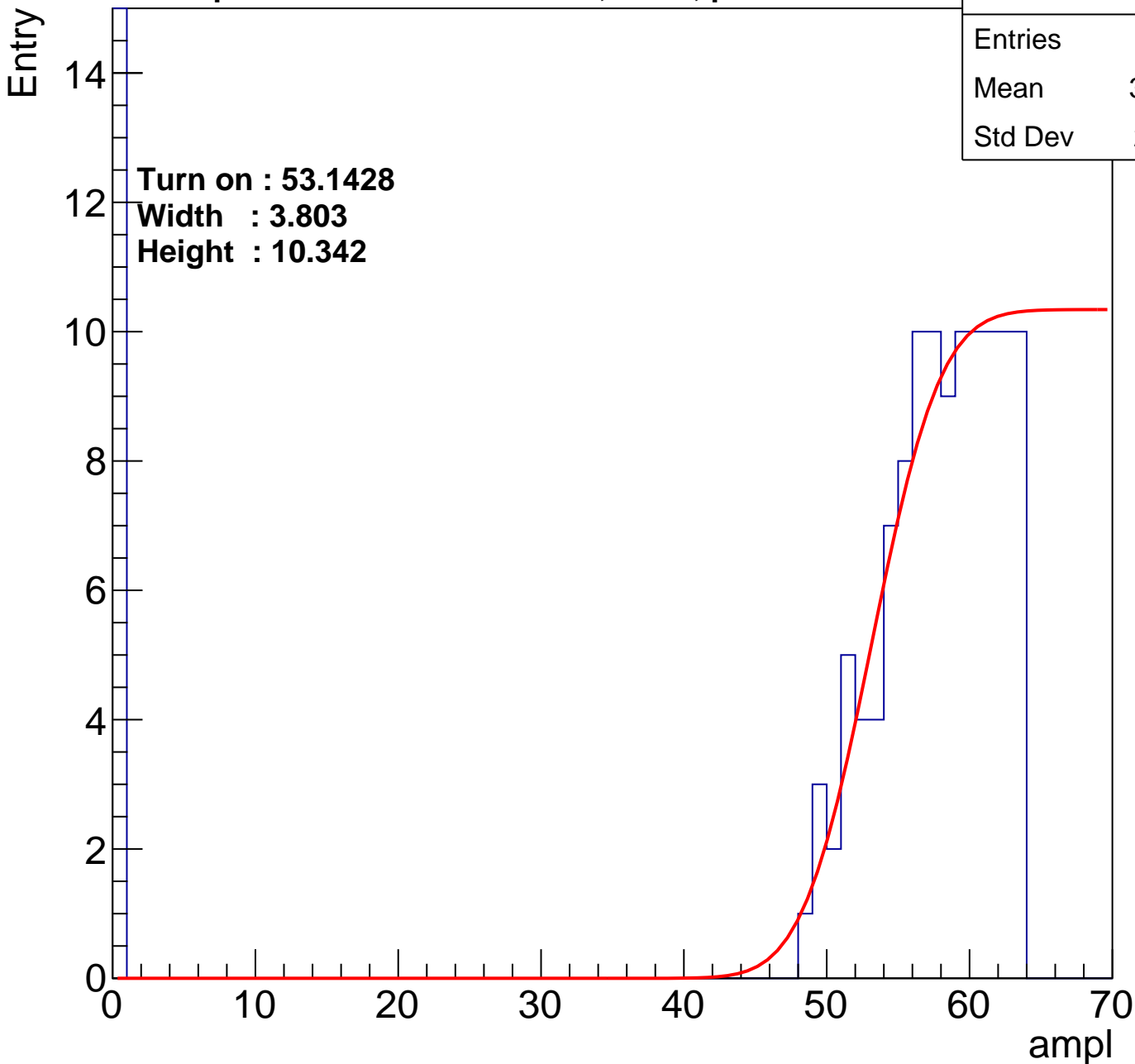
calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	34.89
Std Dev	28.21

Turn on : 53.1428

Width : 3.803

Height : 10.342



B1L104S, U14-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	37.31
Std Dev	27.52

Turn on : 53.3410

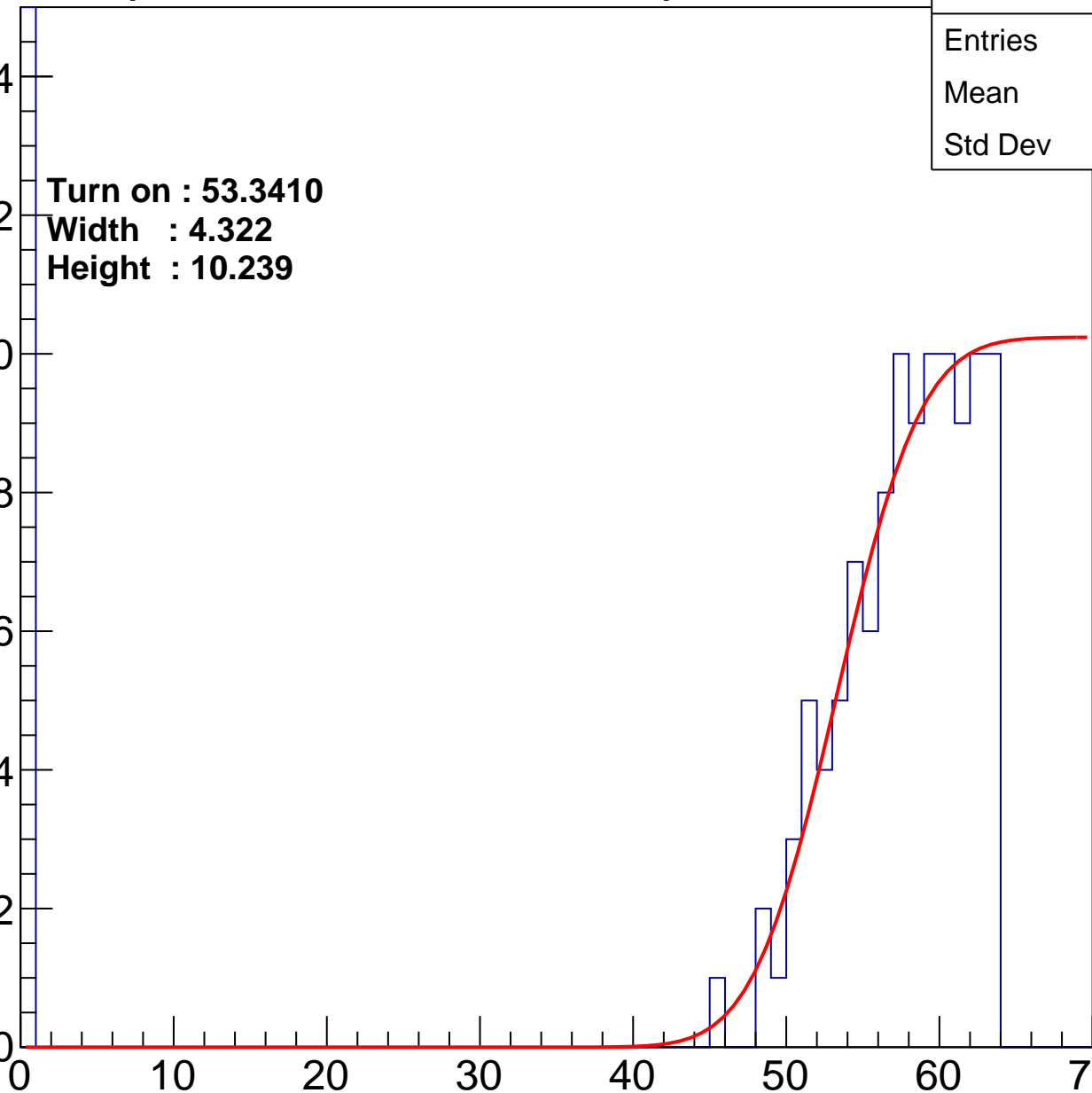
Width : 4.322

Height : 10.239

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	34.13
Std Dev	28.11

Turn on : 51.6209

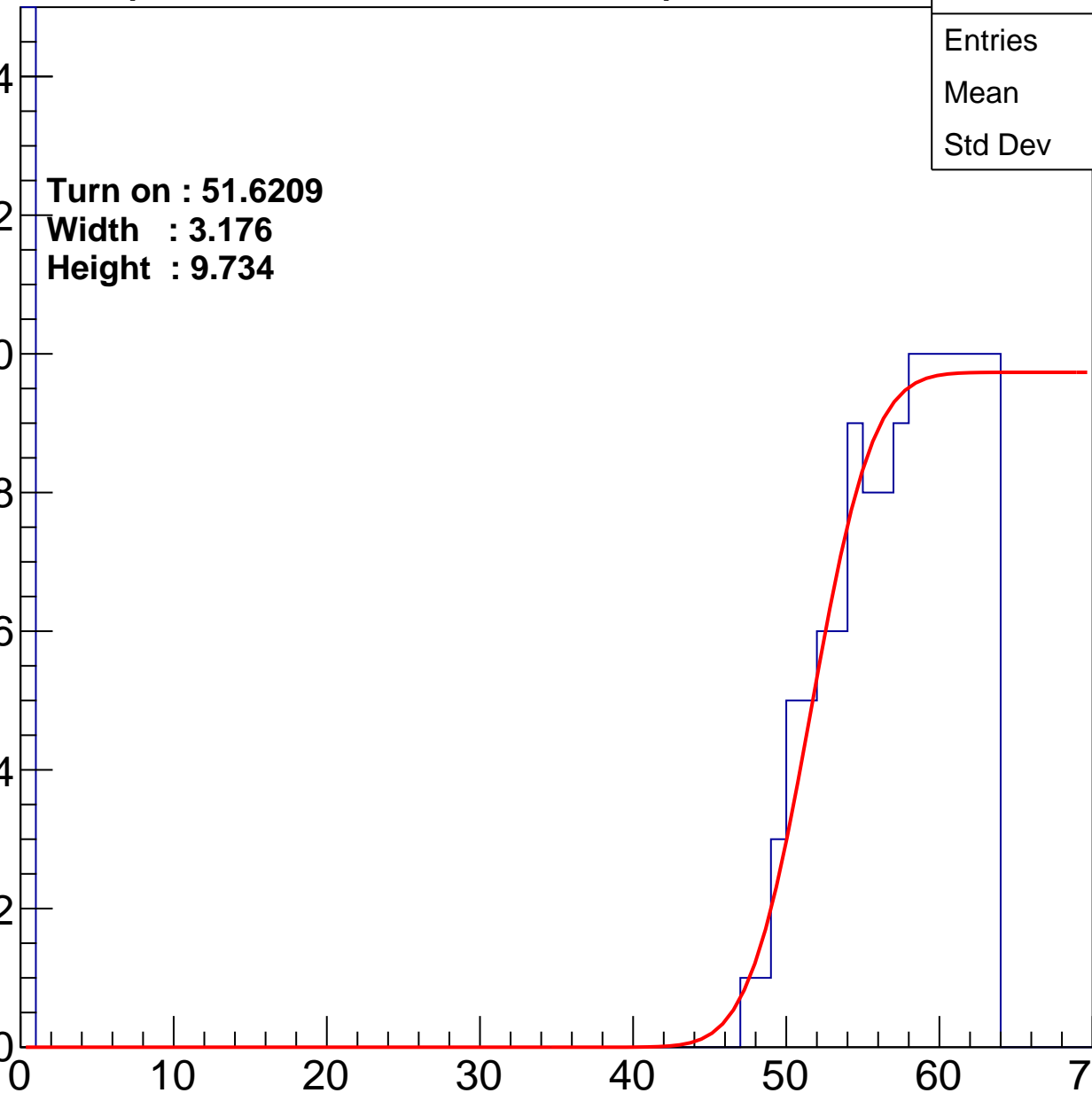
Width : 3.176

Height : 9.734

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	34.55
Std Dev	27.65

Turn on : 51.5358

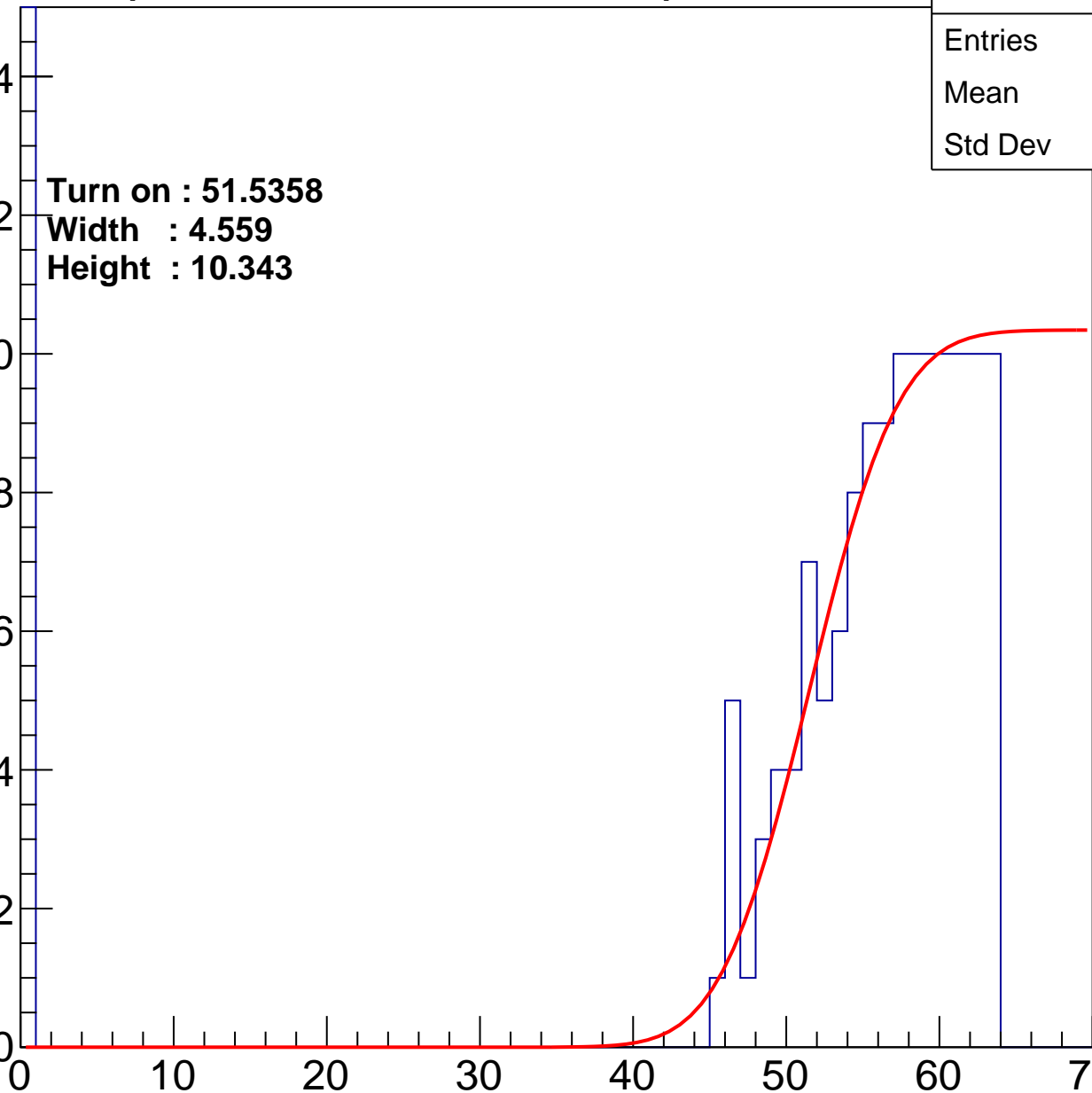
Width : 4.559

Height : 10.343

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	31.13
Std Dev	28.98

Turn on : 53.3129

Width : 4.295

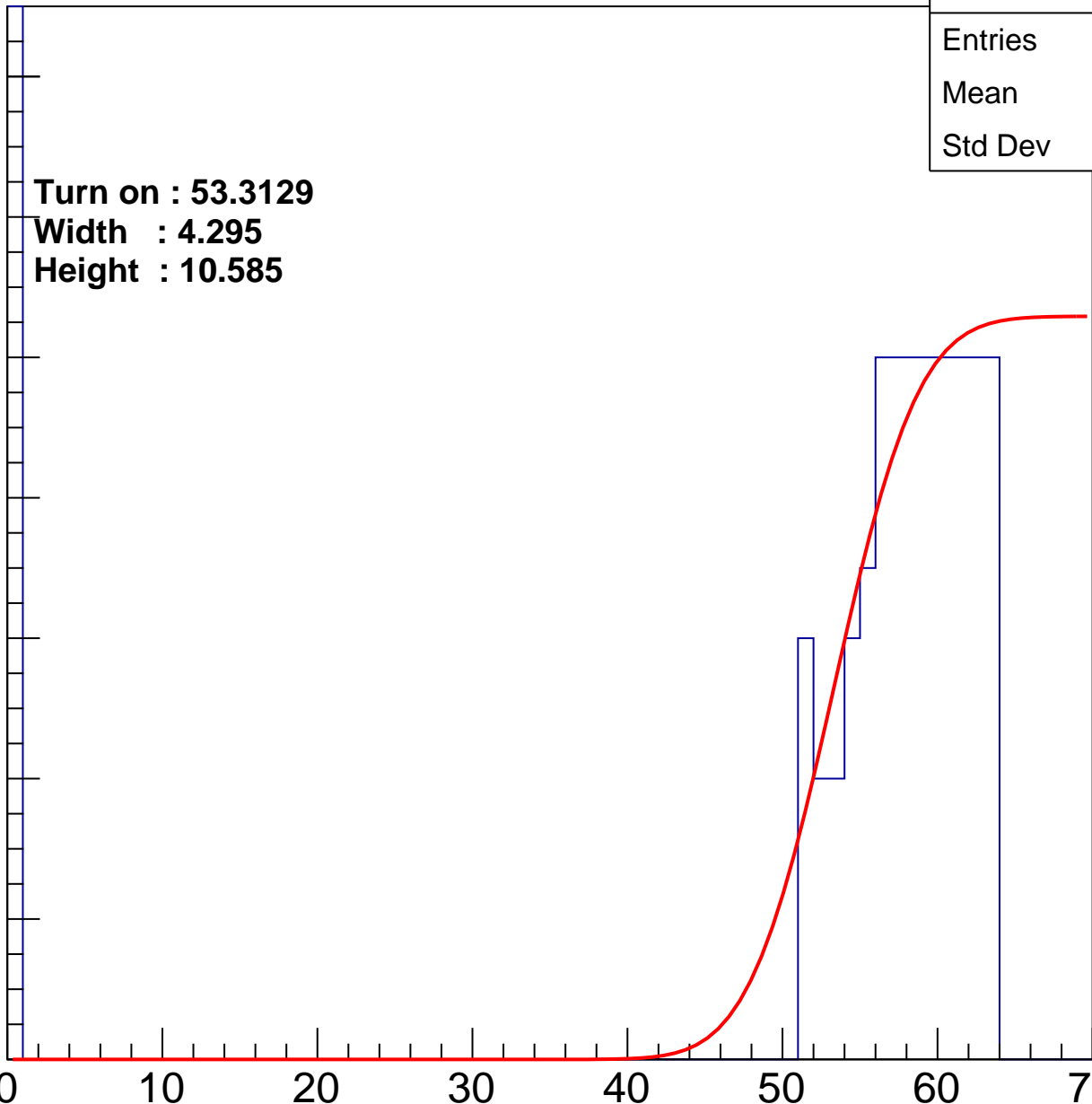
Height : 10.585

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U14-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	34.86
Std Dev	27.66

Turn on : 52.8788

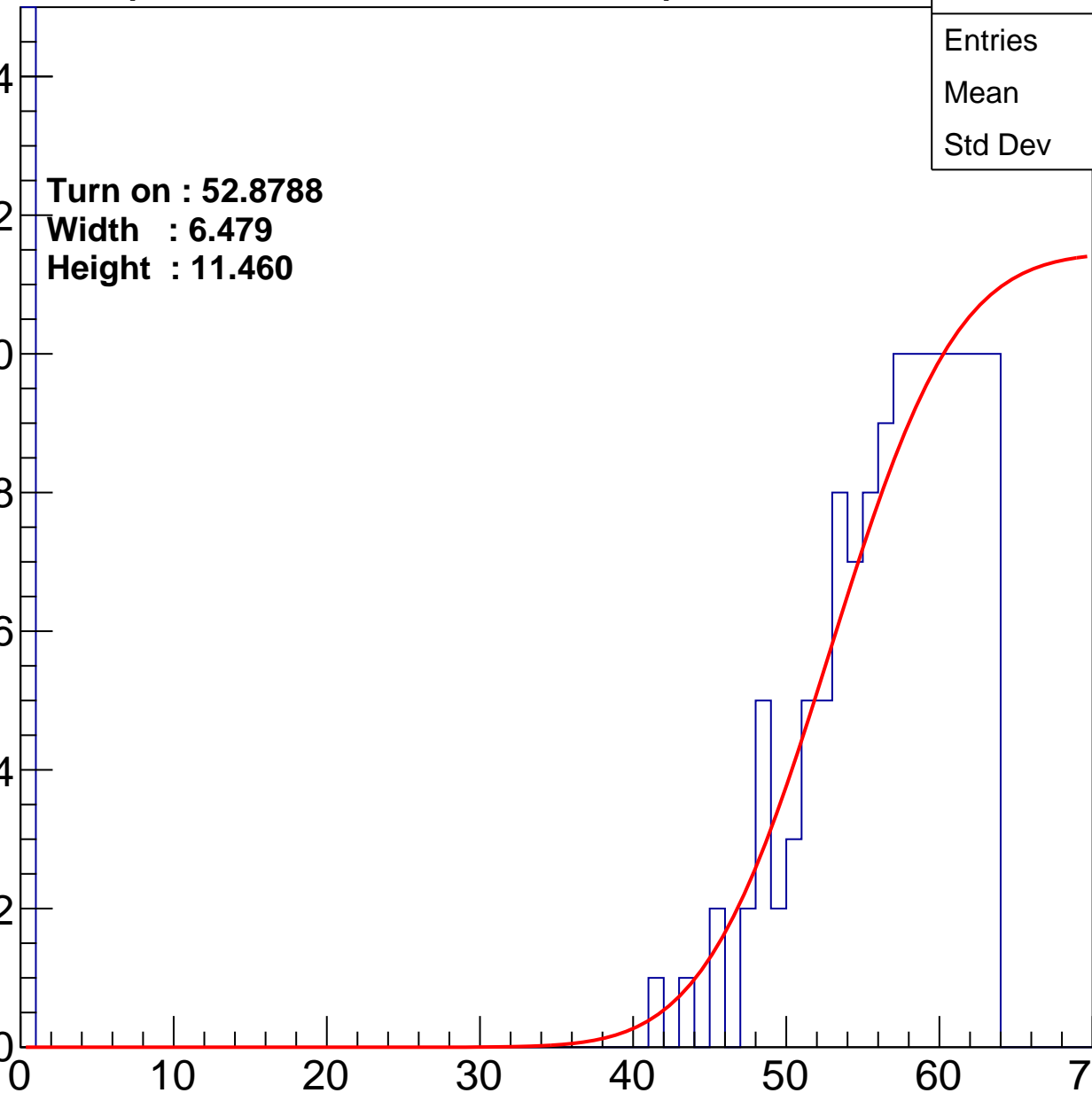
Width : 6.479

Height : 11.460

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch19

calib_packv5_033123_0516.root, FC#4, port A1

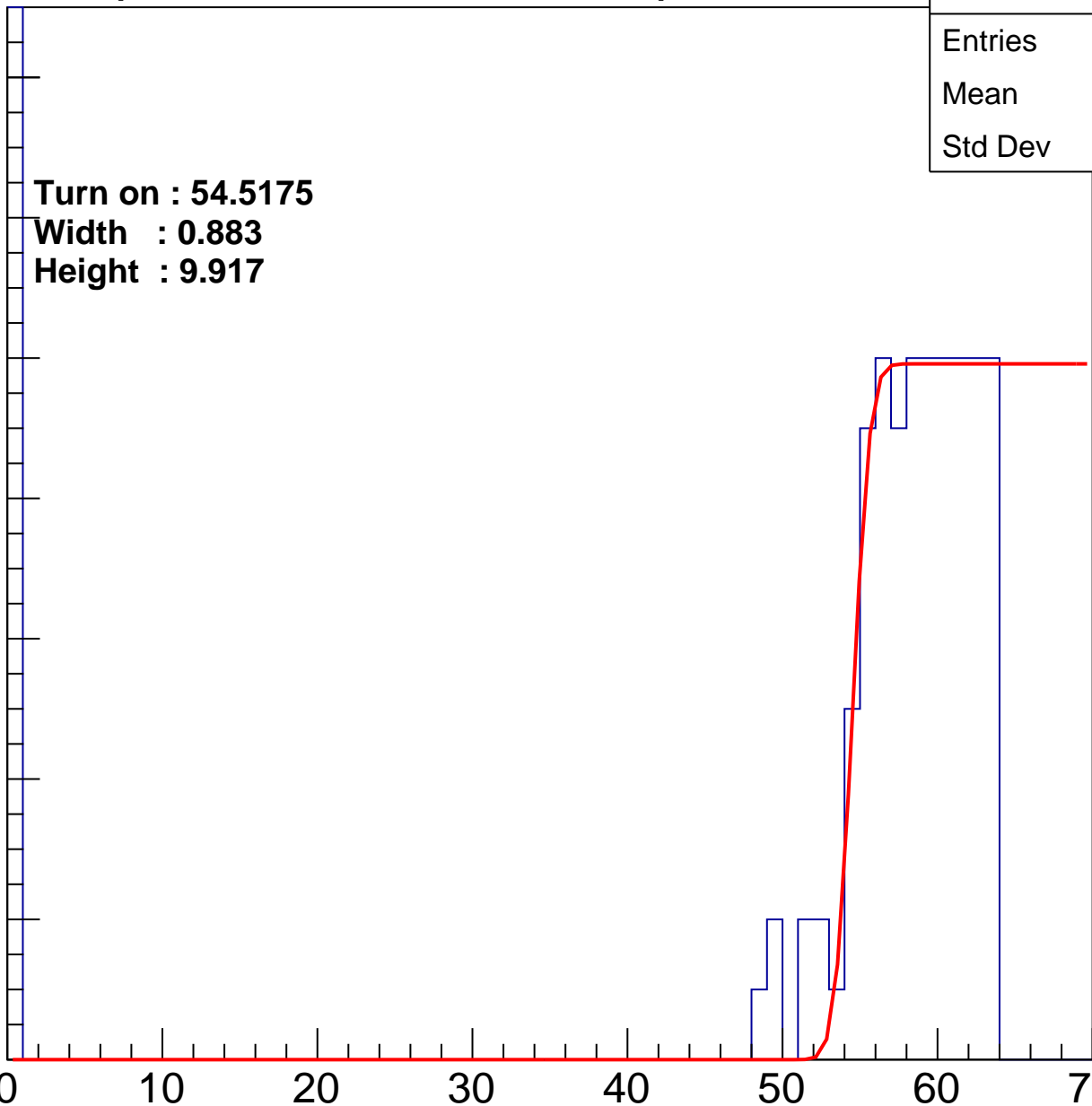
Entry

14
12
10
8
6
4
2
0

Turn on : 54.5175
Width : 0.883
Height : 9.917

Entries	184
Mean	31.92
Std Dev	29.05

ampl



B1L104S, U14-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	40.69
Std Dev	25.5

Turn on : 51.4200

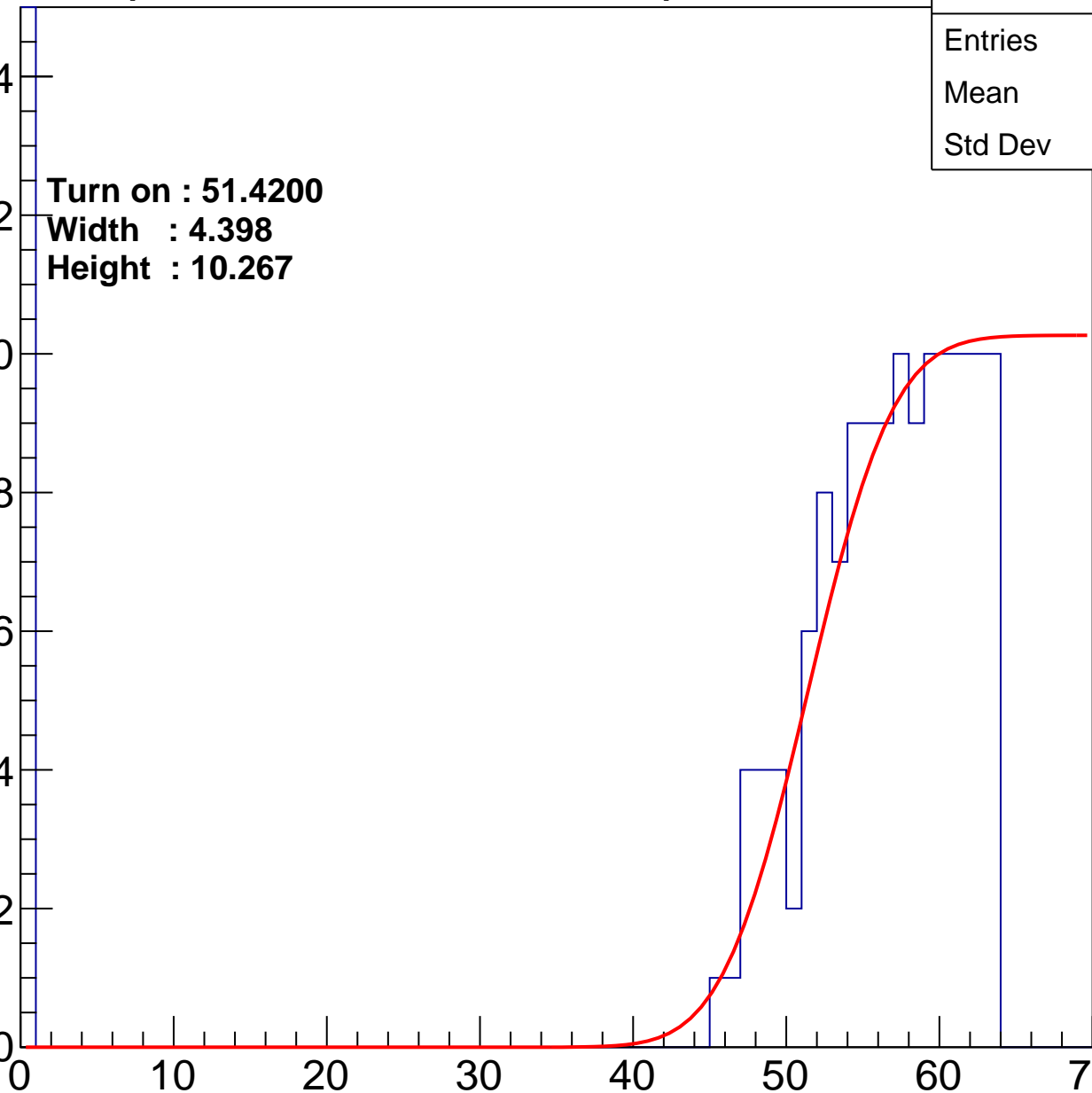
Width : 4.398

Height : 10.267

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	36.34
Std Dev	27.79

Turn on : 52.0334

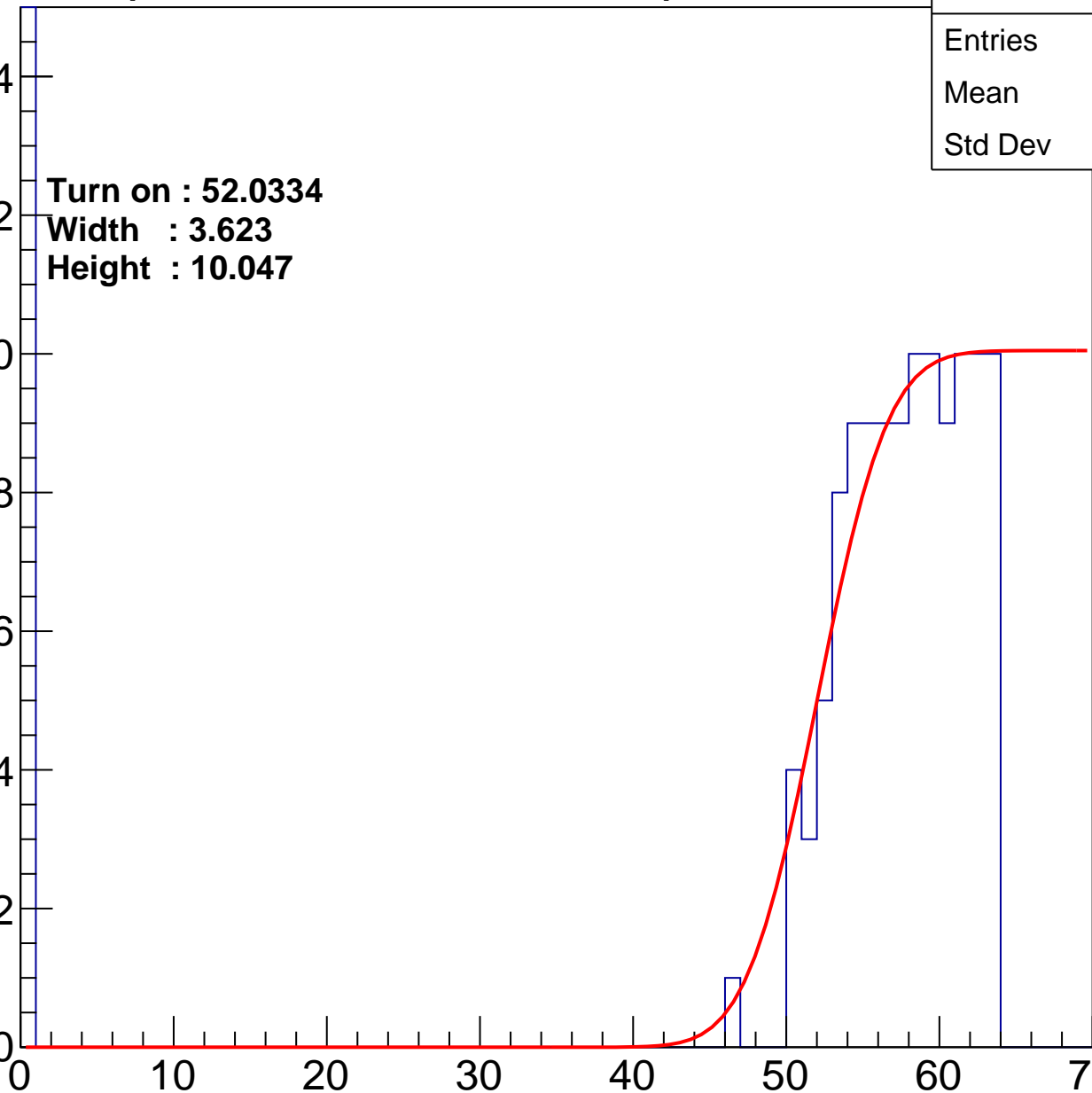
Width : 3.623

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	34.86
Std Dev	27.69

Turn on : 52.0825

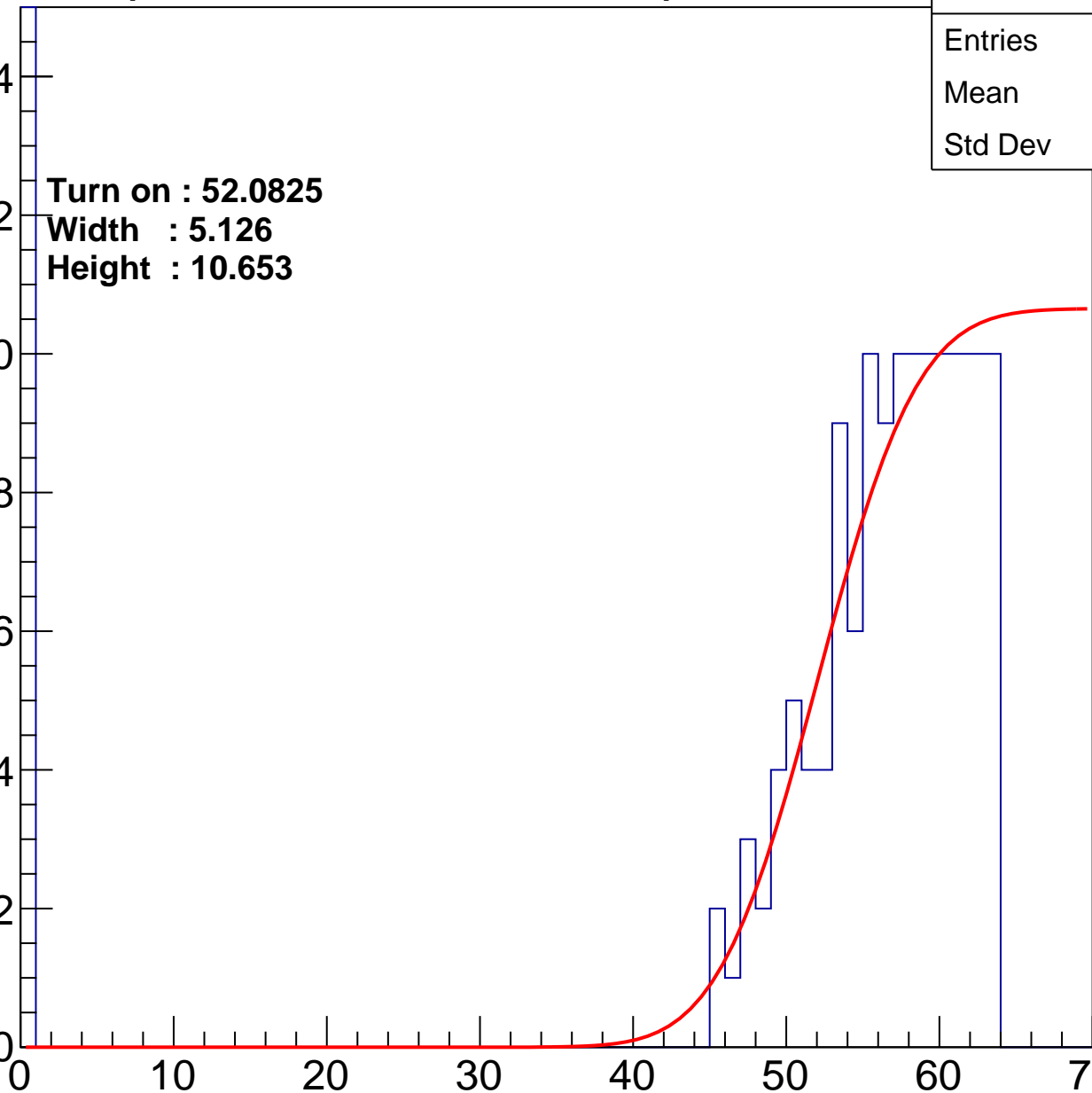
Width : 5.126

Height : 10.653

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	30.64
Std Dev	29.34

Turn on : 55.0403

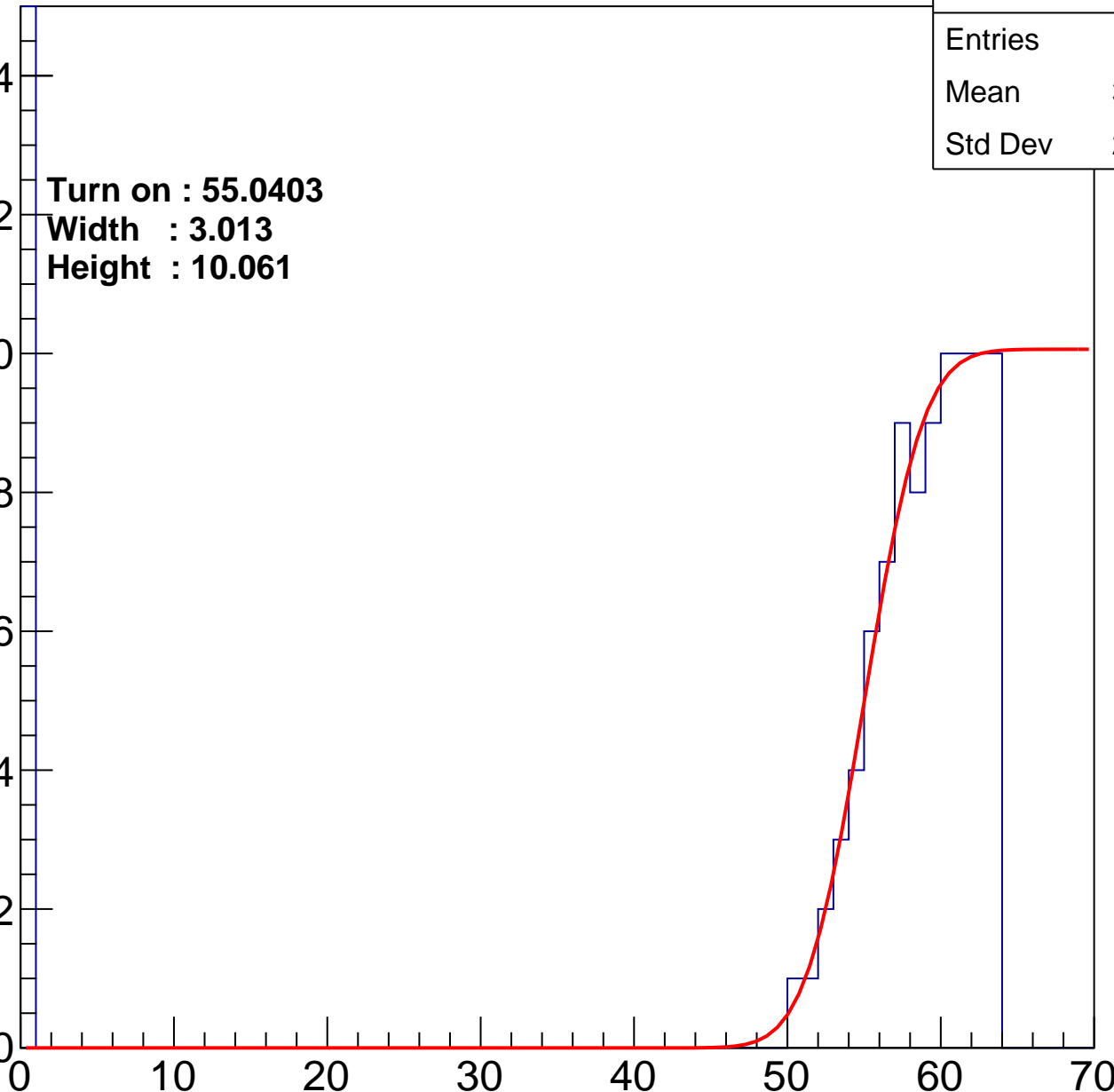
Width : 3.013

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	218
Mean	33.59
Std Dev	28.09

Turn on : 51.8167

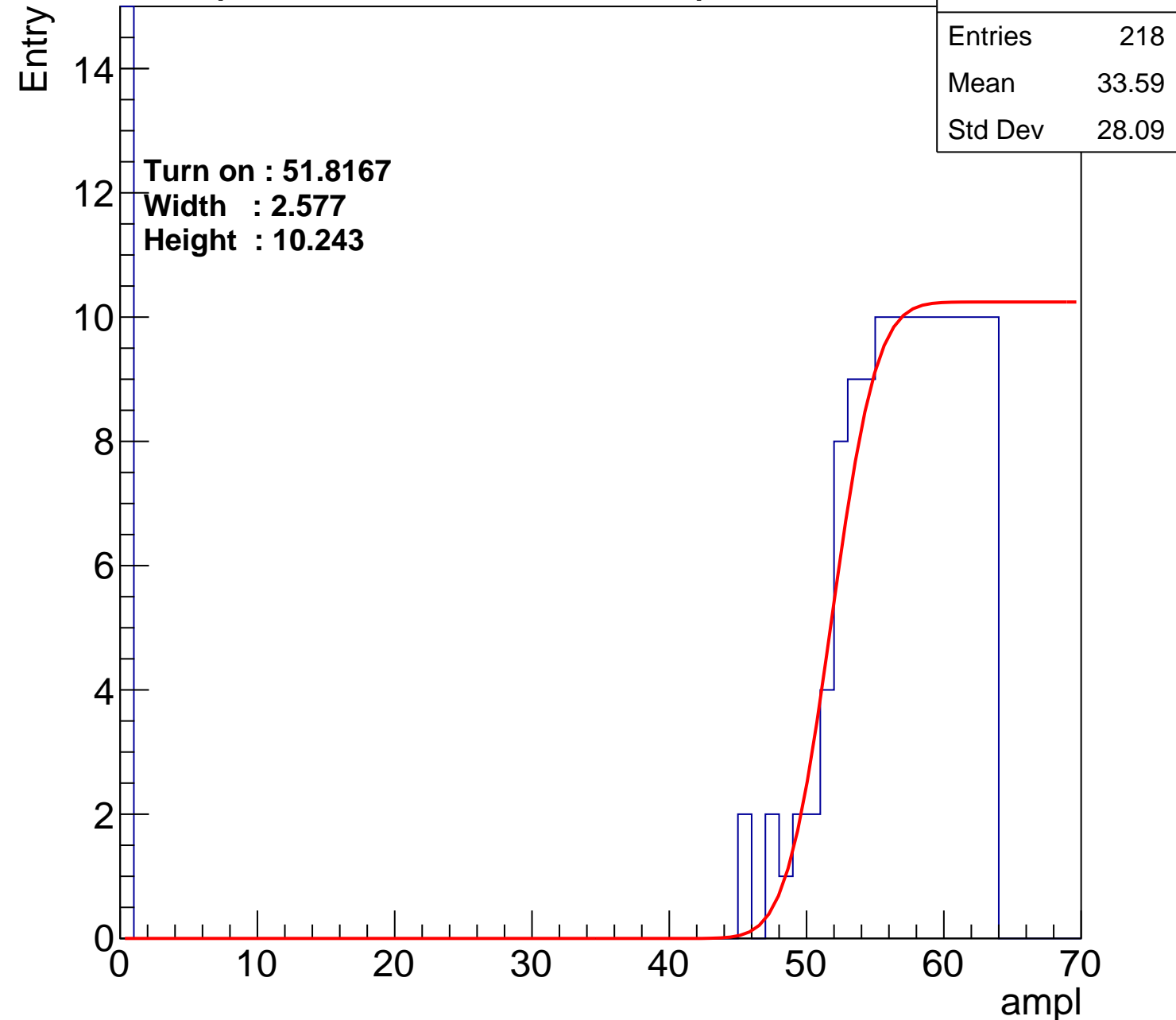
Width : 2.577

Height : 10.243

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch25

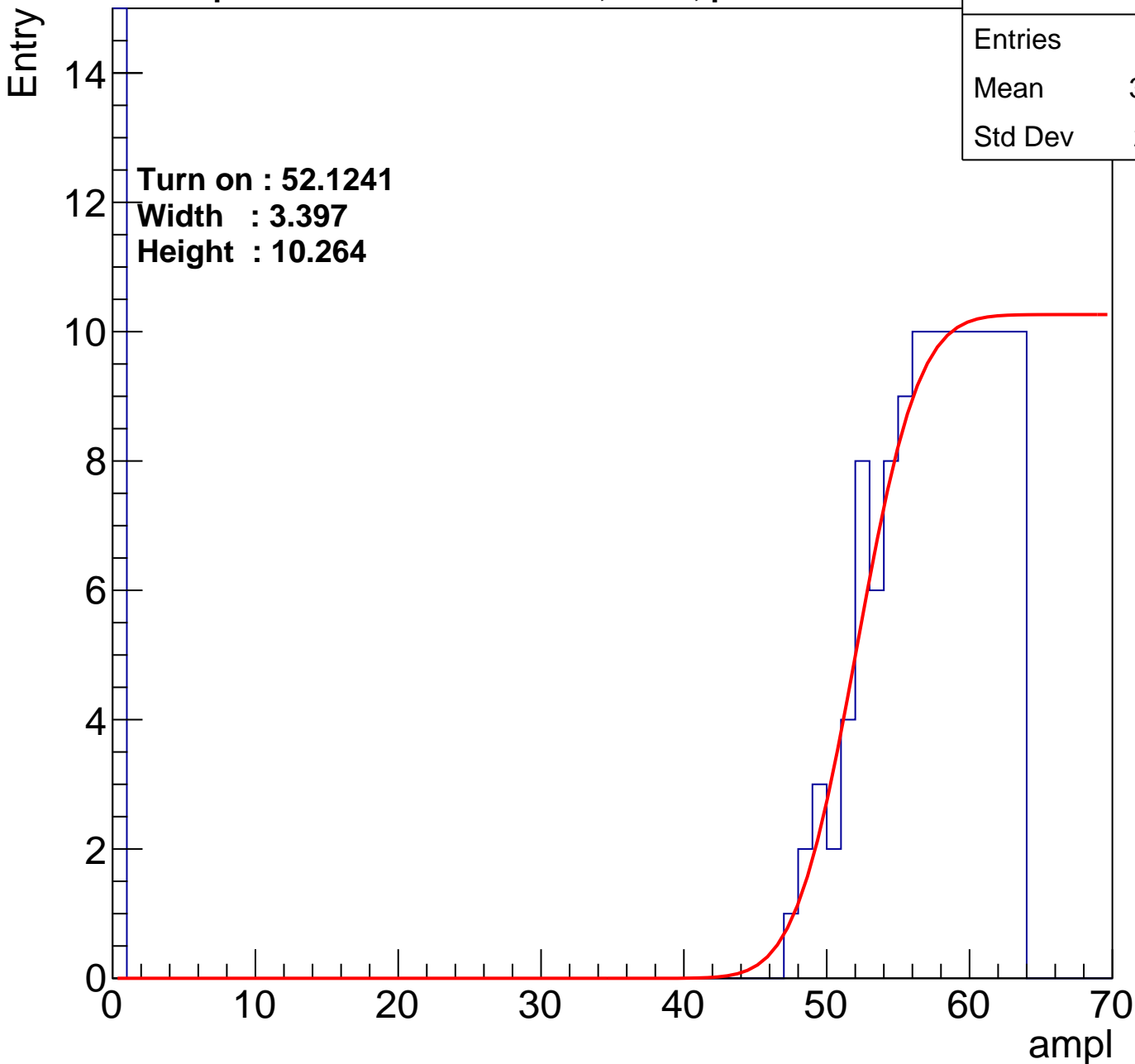
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	36.35
Std Dev	27.61

Turn on : 52.1241

Width : 3.397

Height : 10.264



B1L104S, U14-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	36.92
Std Dev	26.99

Turn on : 50.4693

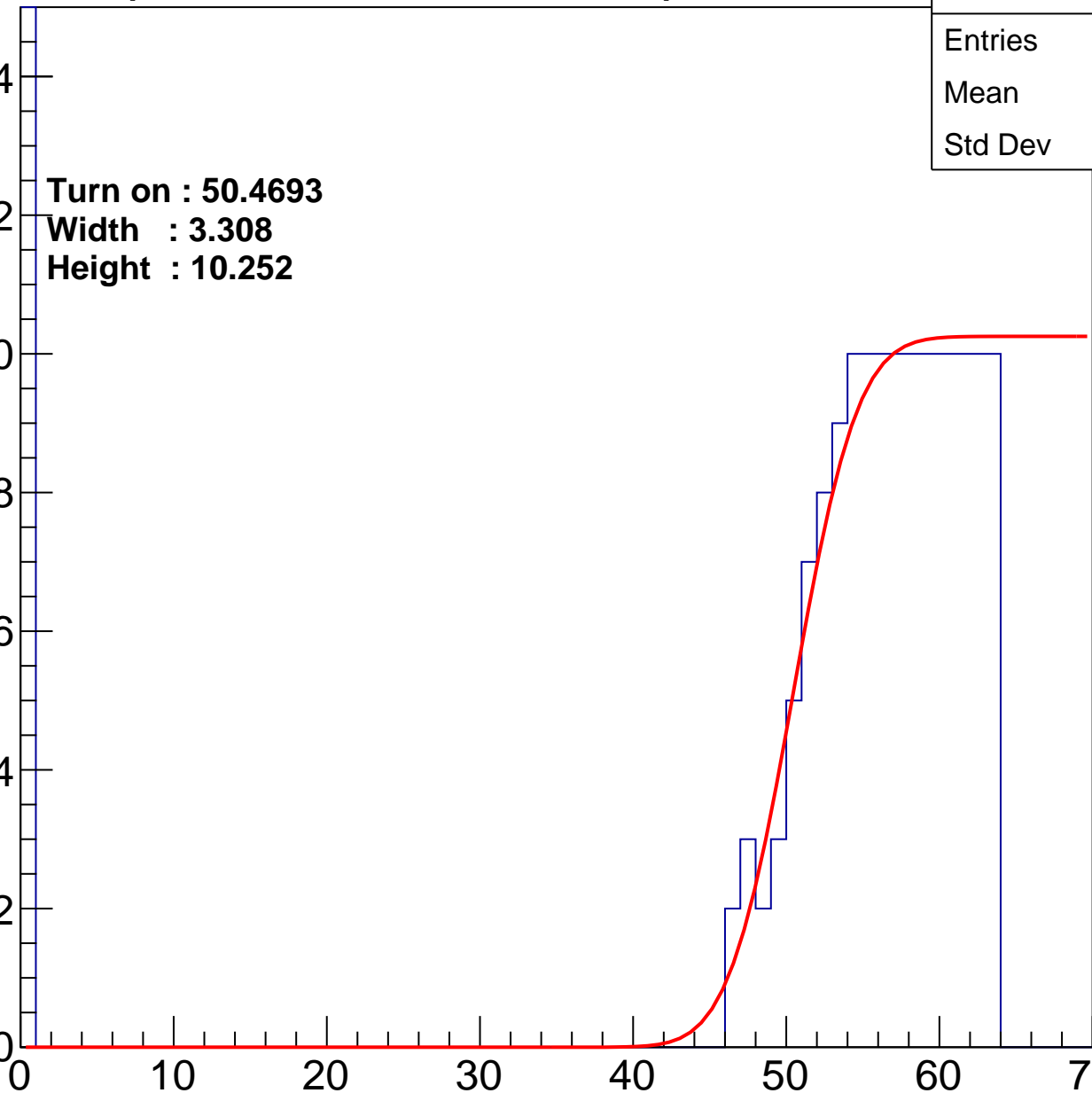
Width : 3.308

Height : 10.252

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch27

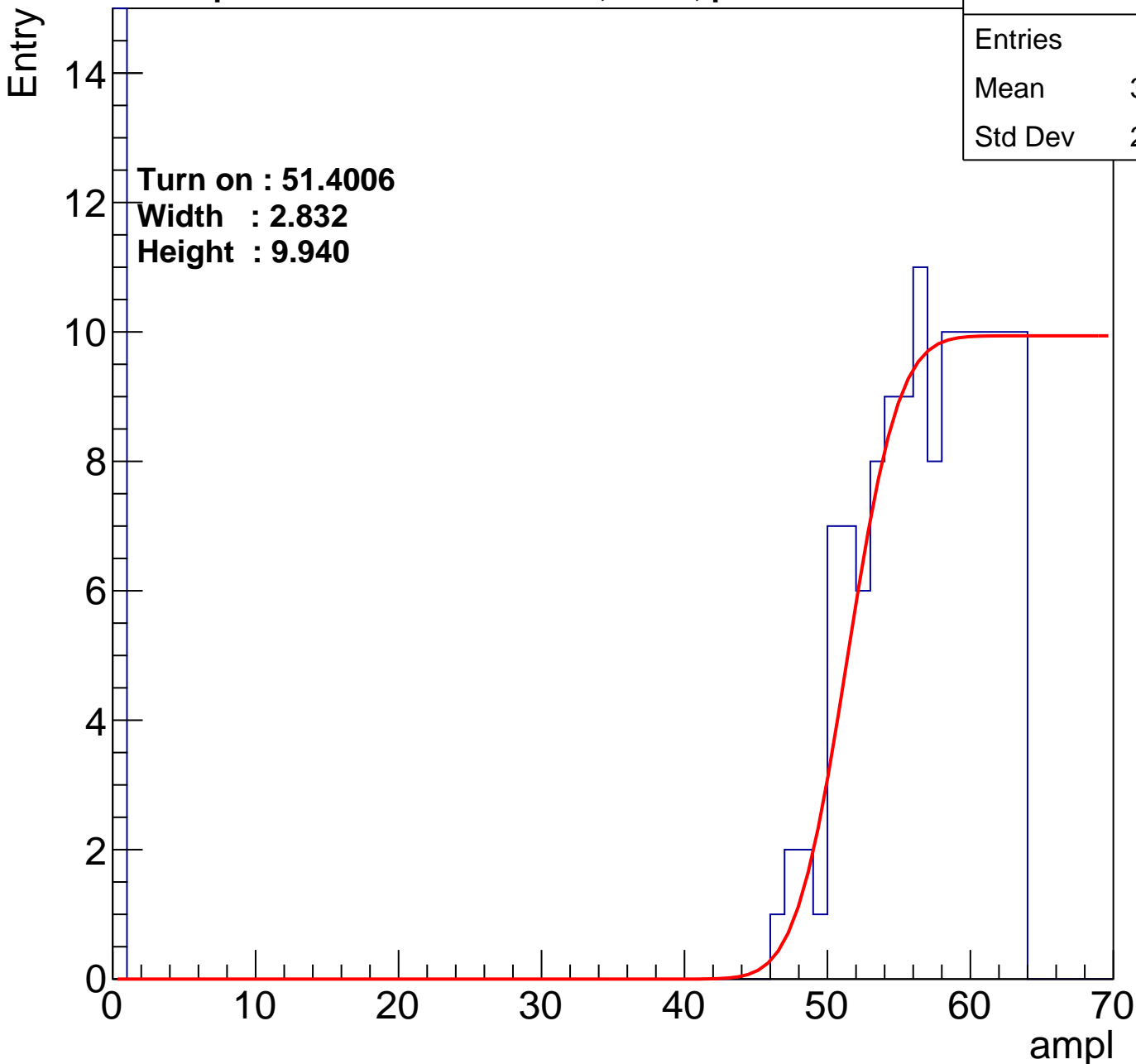
calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	37.24
Std Dev	27.06

Turn on : 51.4006

Width : 2.832

Height : 9.940



B1L104S, U14-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	34.11
Std Dev	28.21

Turn on : 52.8087

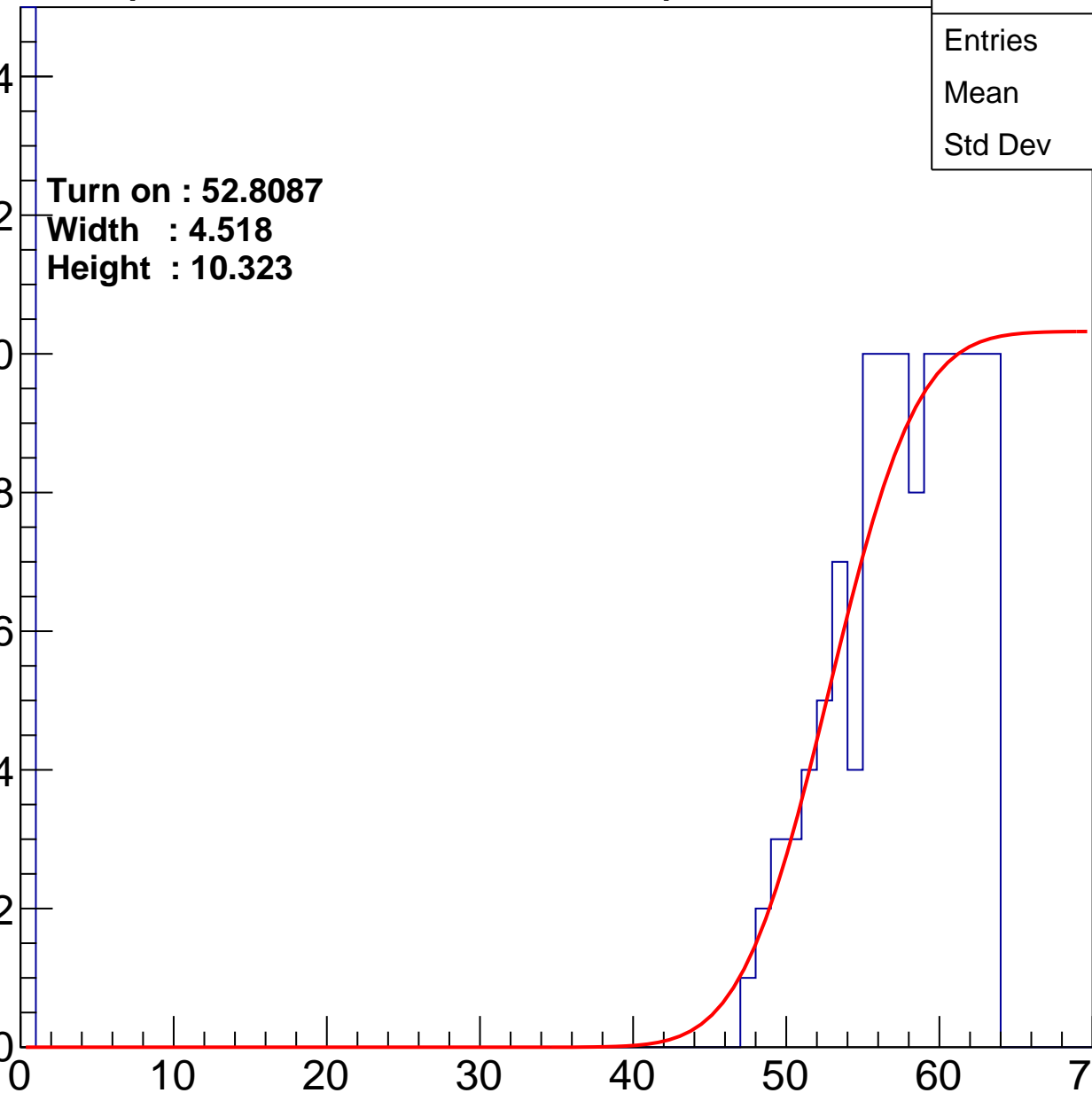
Width : 4.518

Height : 10.323

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	32.87
Std Dev	28.77

Turn on : 53.5403

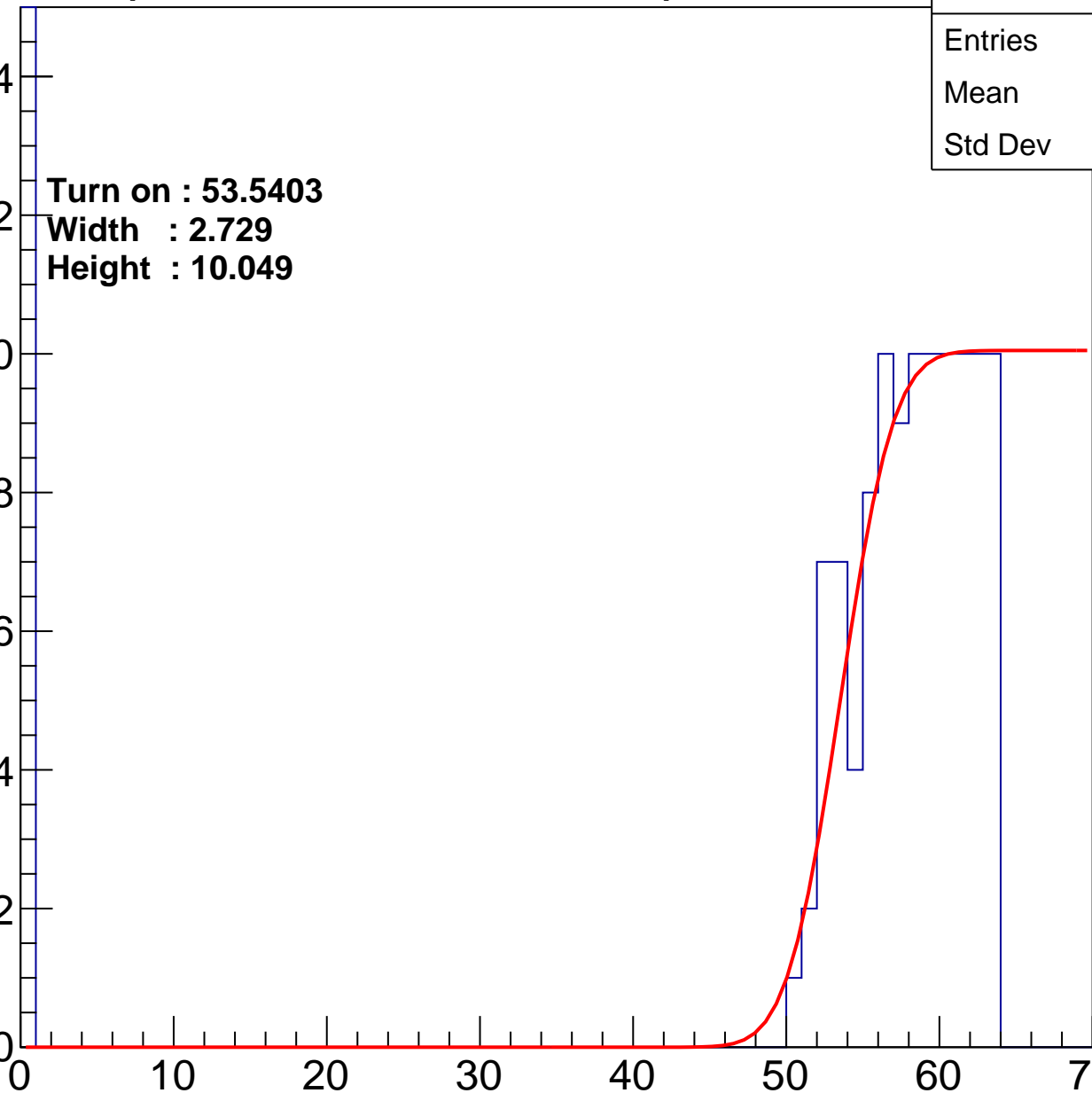
Width : 2.729

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	33.8
Std Dev	28.64

Turn on : 54.7541

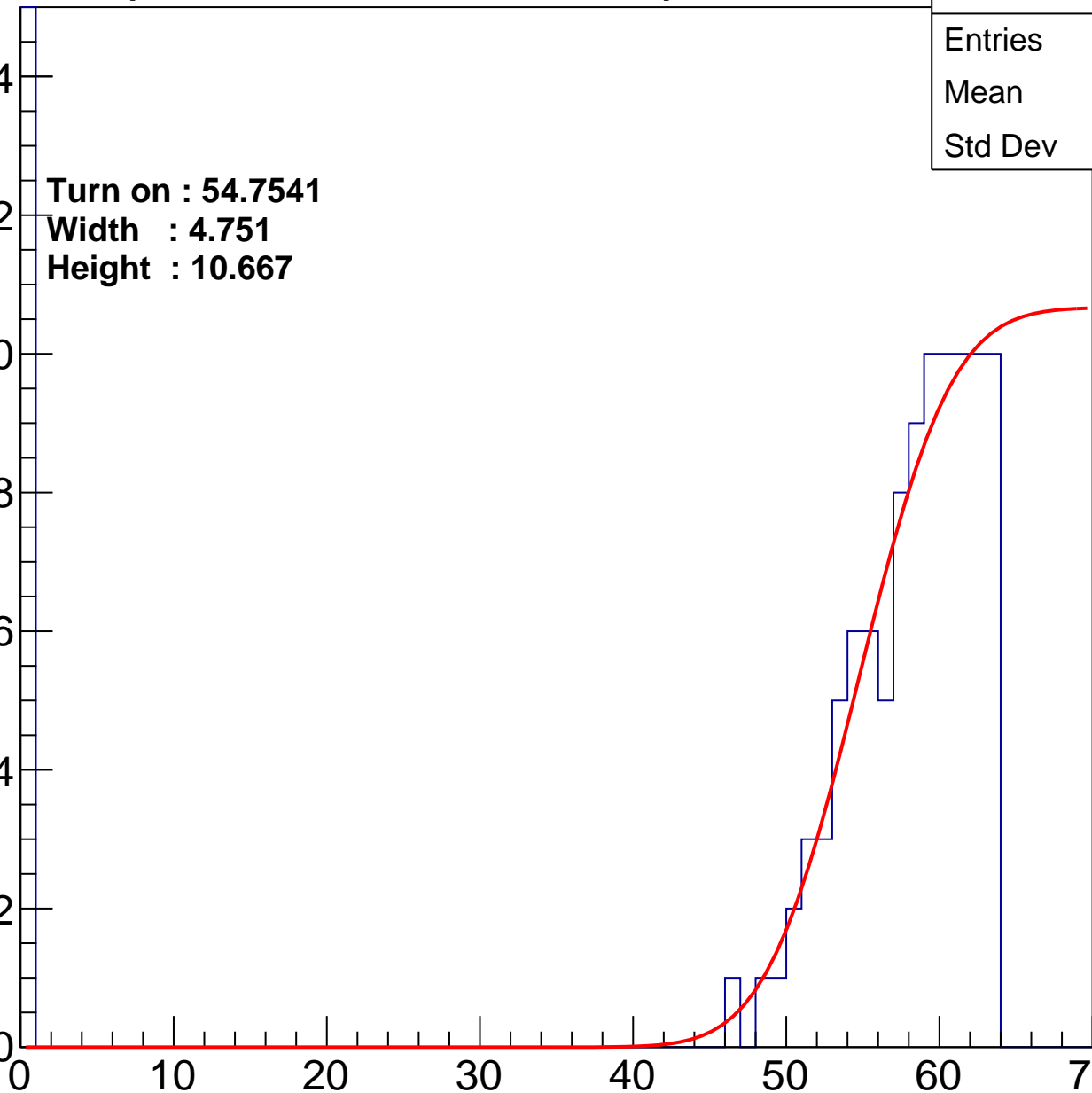
Width : 4.751

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	36.19
Std Dev	27.71

Turn on : 51.9187

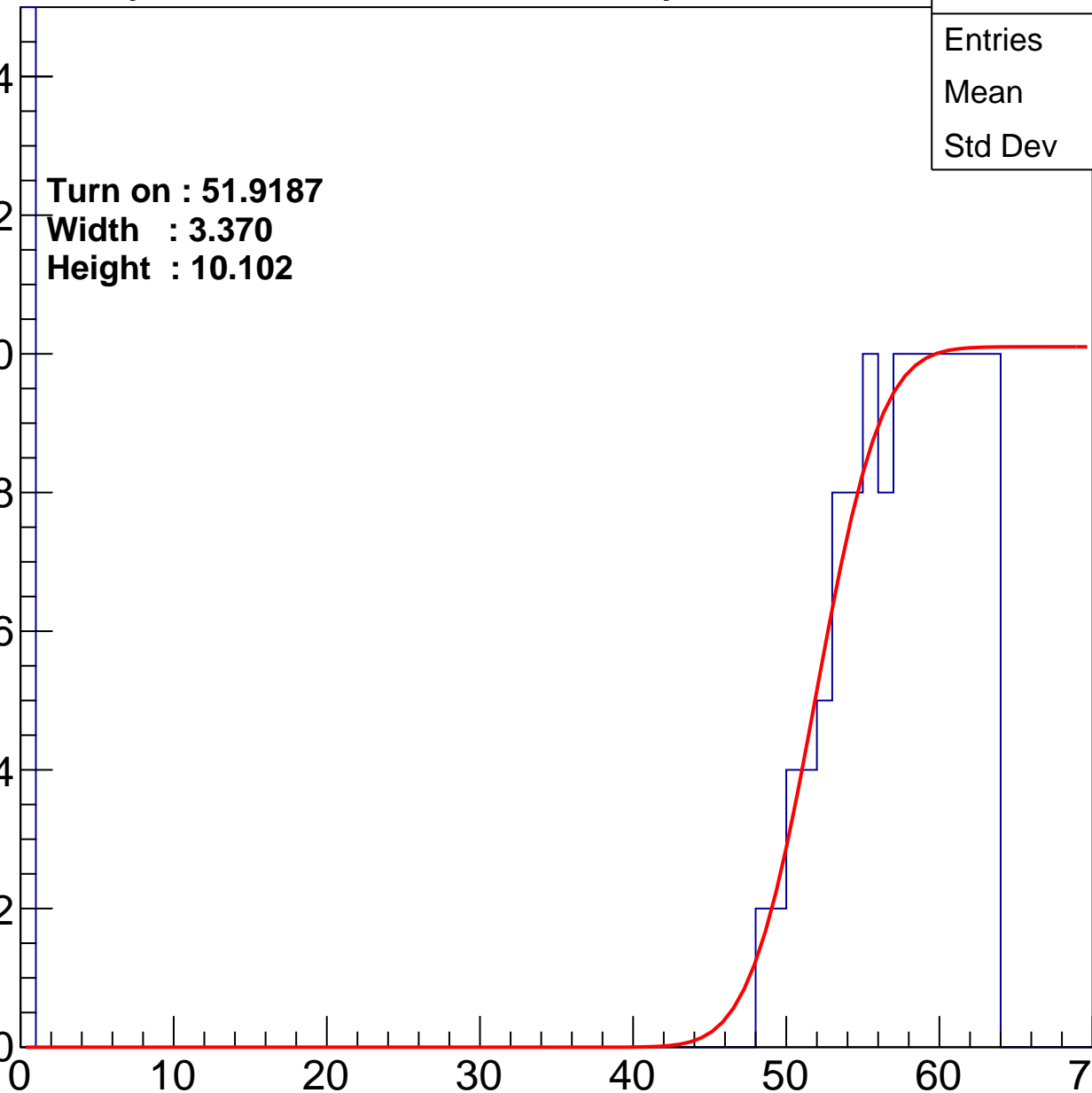
Width : 3.370

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	39.59
Std Dev	26.8

Turn on : 52.7574

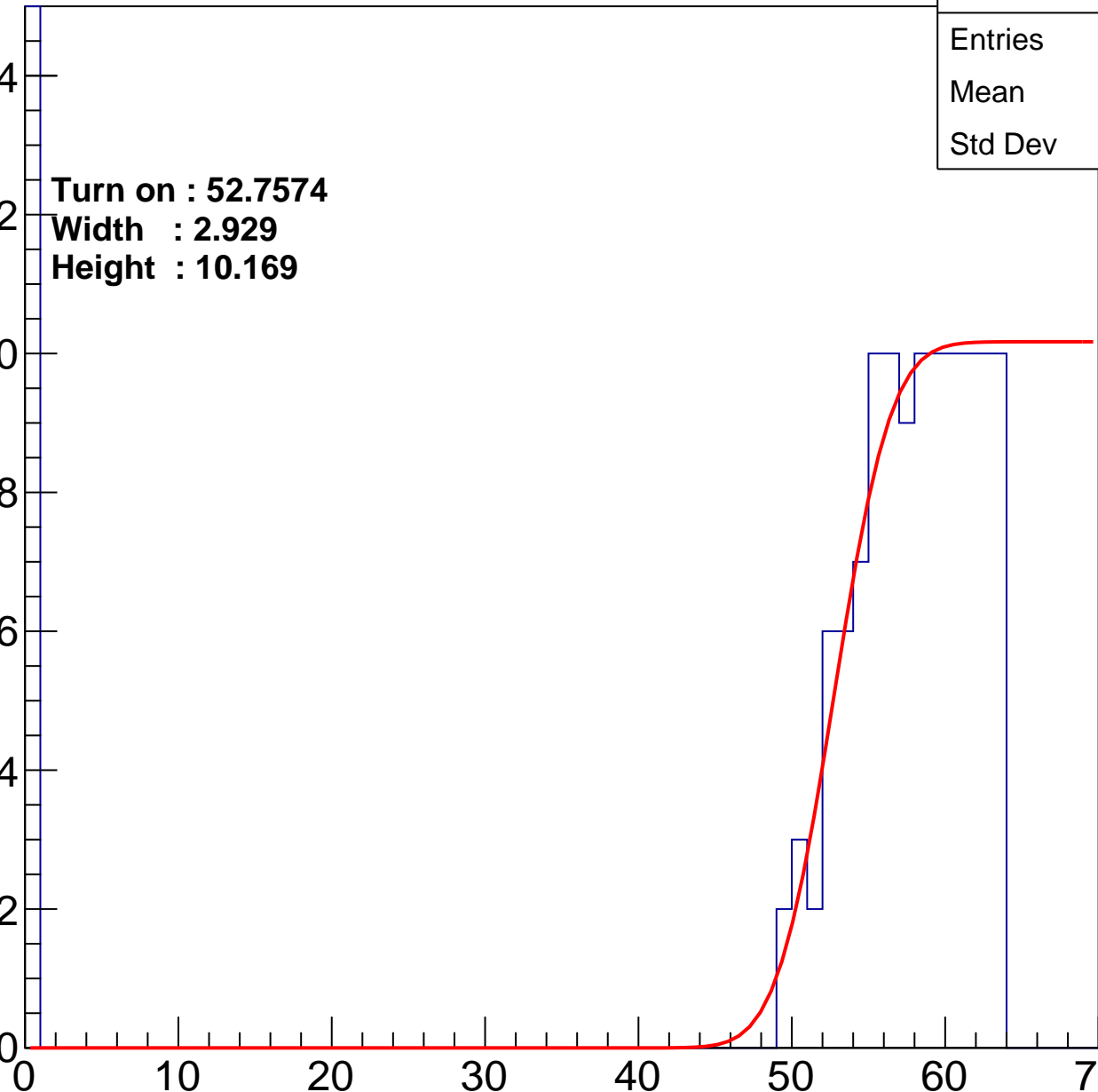
Width : 2.929

Height : 10.169

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	235
Mean	35.14
Std Dev	27.2

Turn on : 49.8303

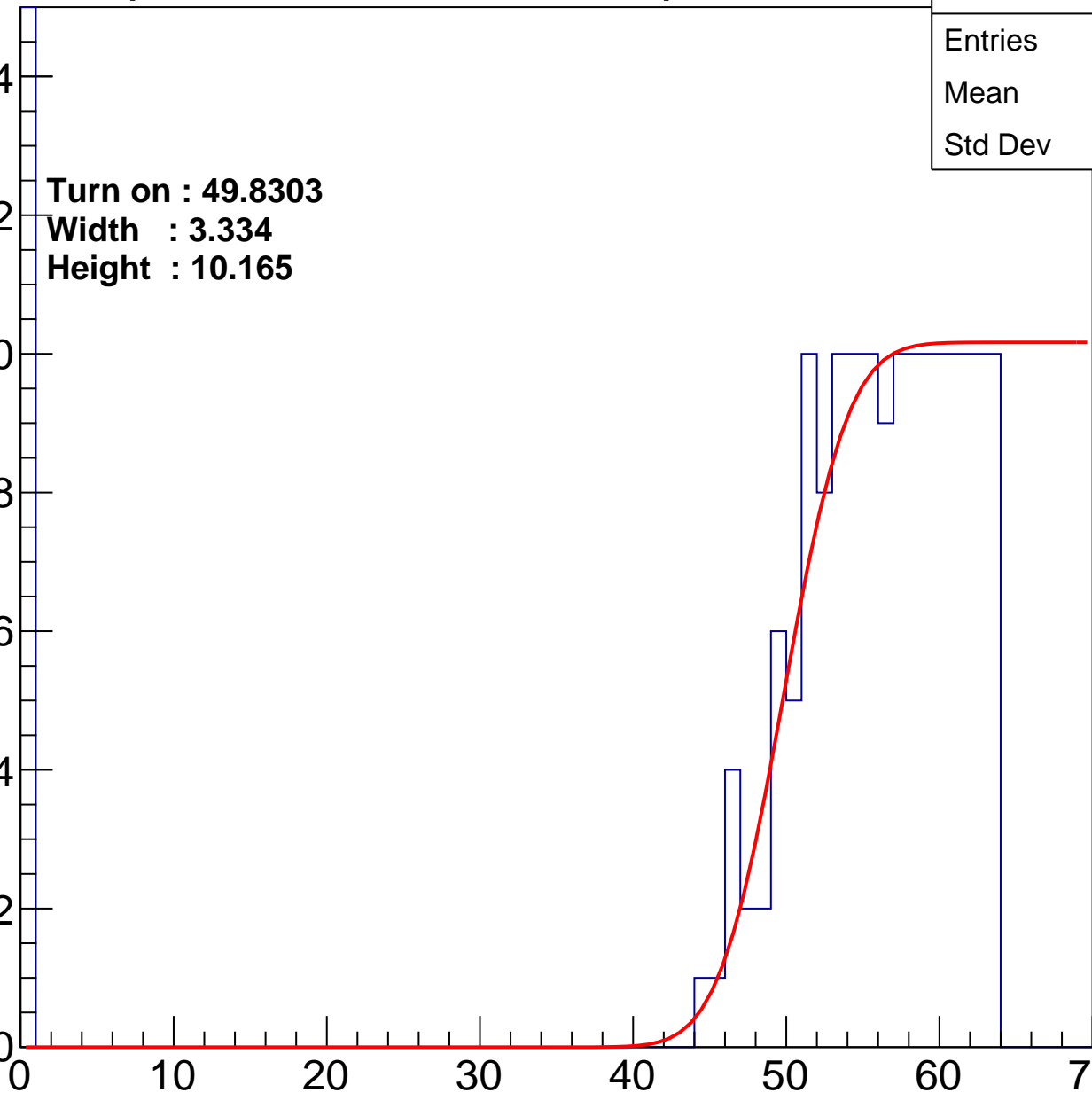
Width : 3.334

Height : 10.165

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	35.88
Std Dev	27.39

Turn on : 51.3428

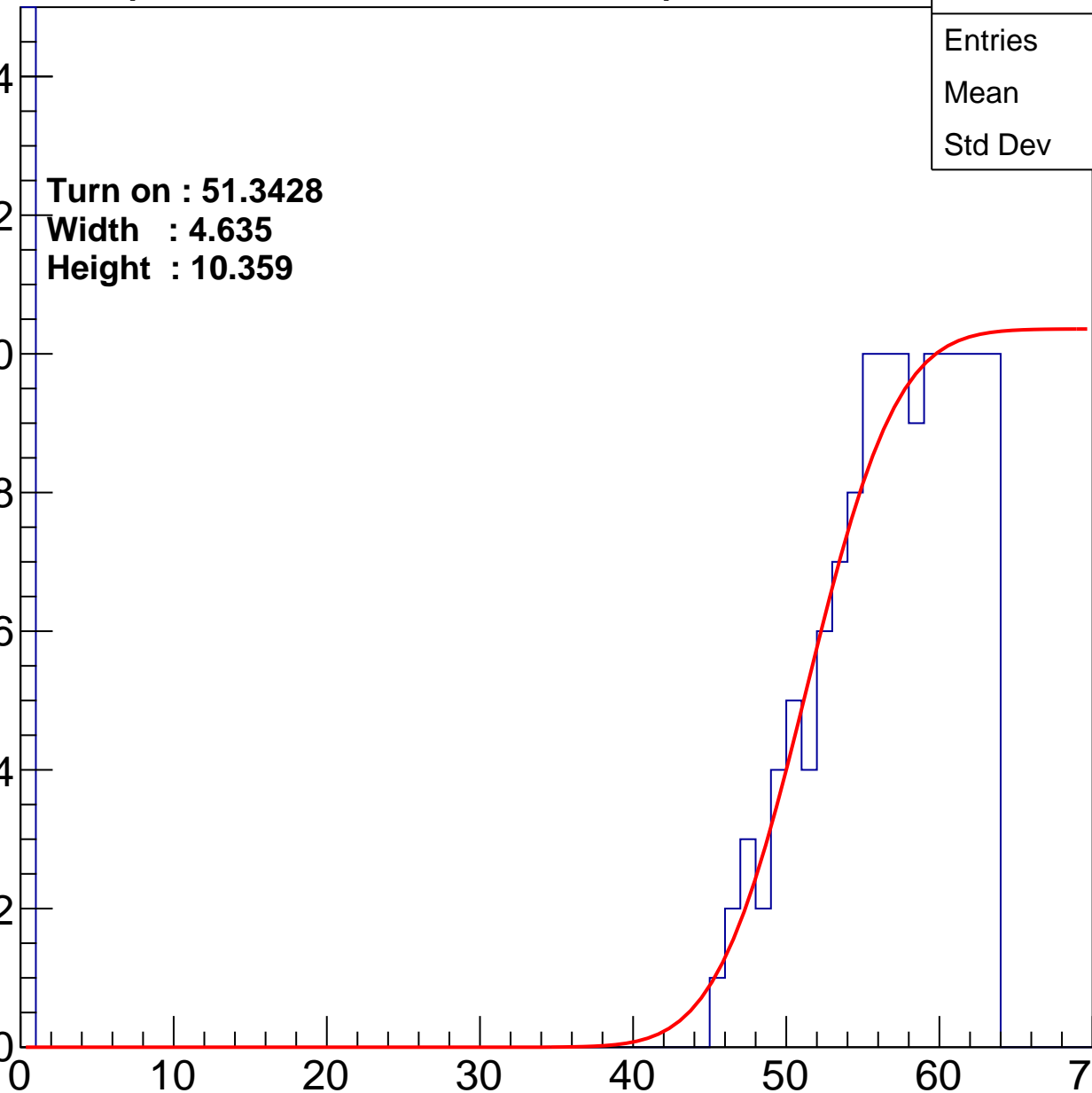
Width : 4.635

Height : 10.359

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	34.32
Std Dev	27.94

Turn on : 51.0658

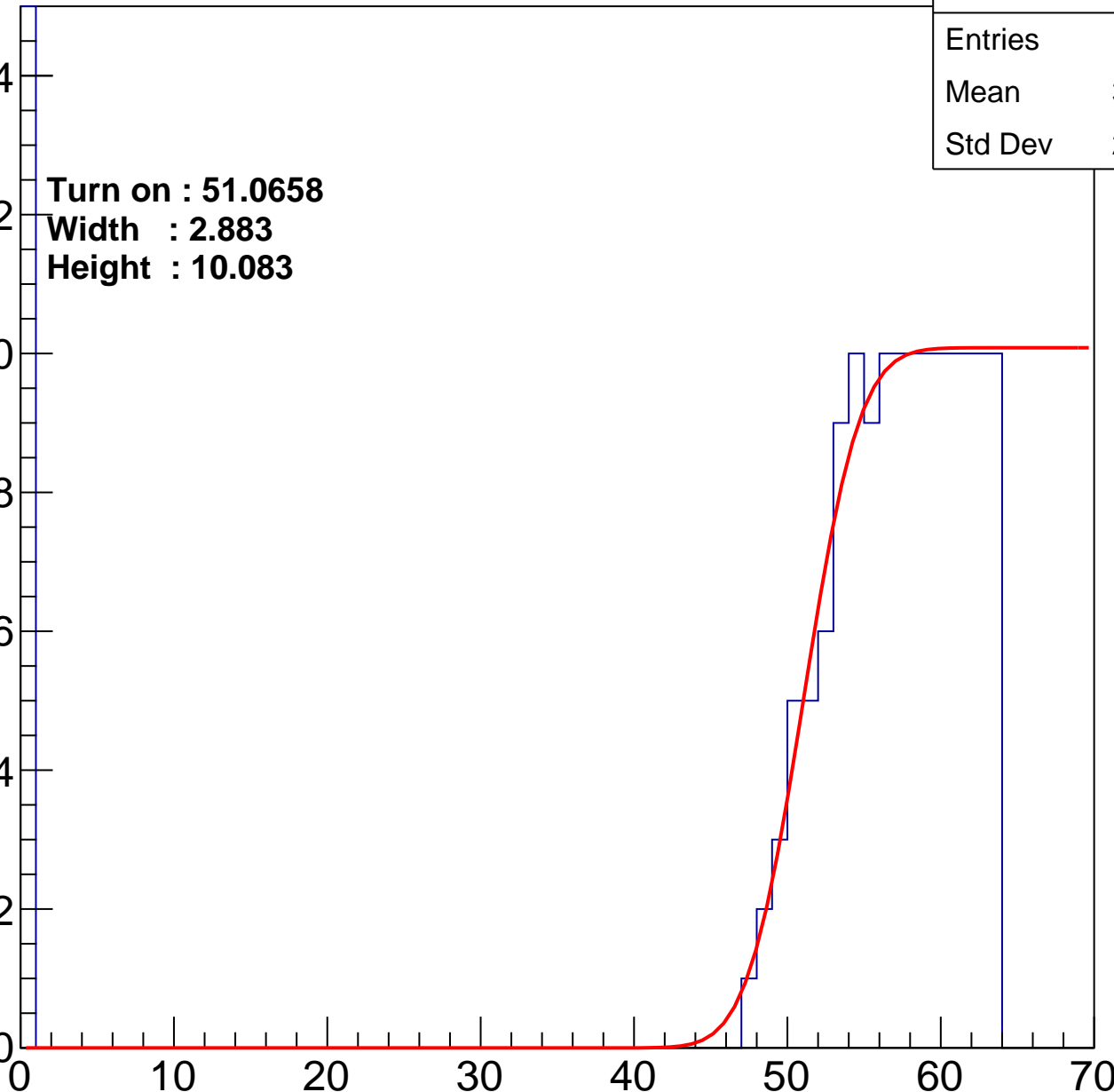
Width : 2.883

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	36.75
Std Dev	27.68

Turn on : 52.5542

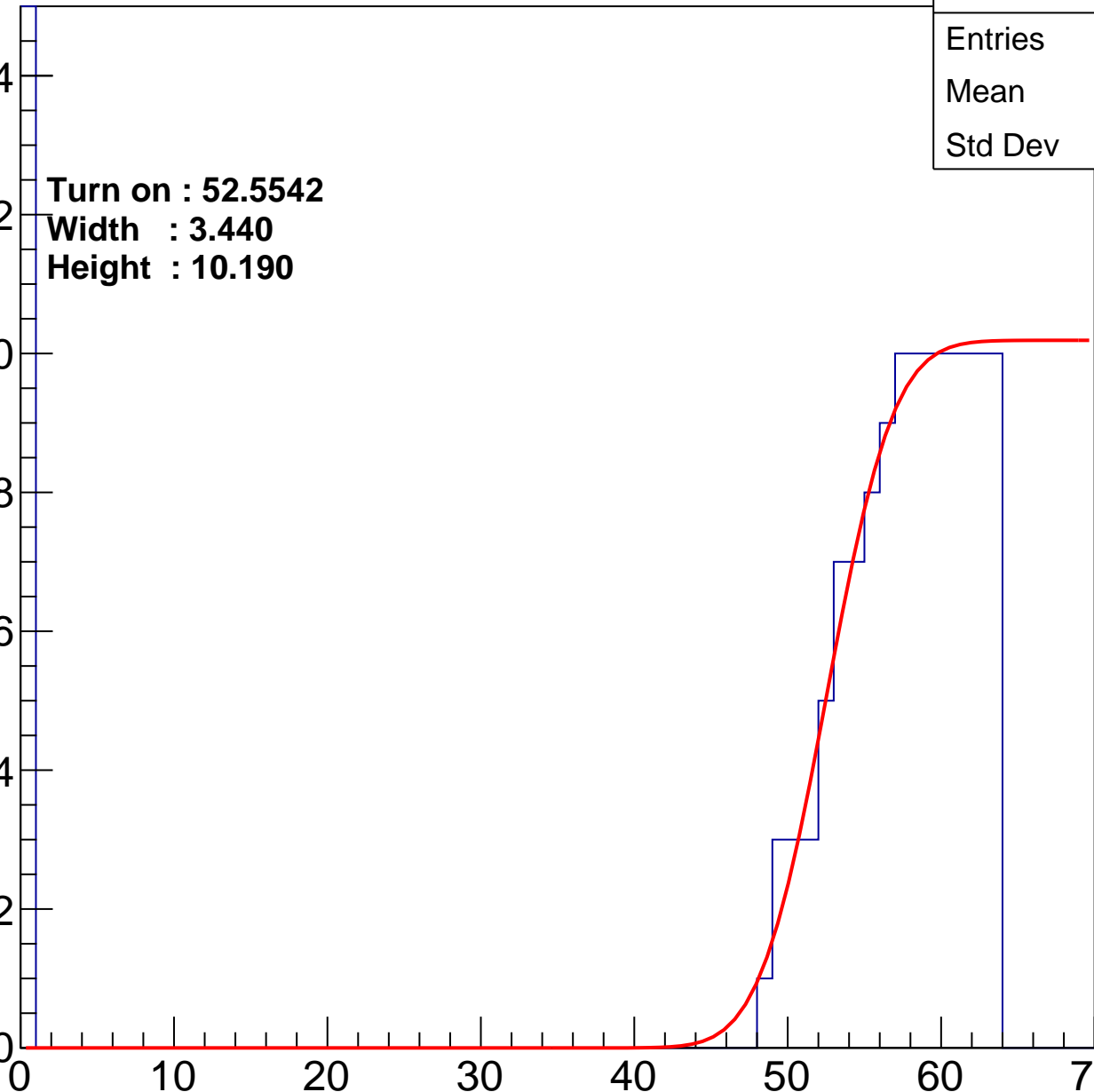
Width : 3.440

Height : 10.190

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	35.29
Std Dev	27.74

Turn on : 51.9813

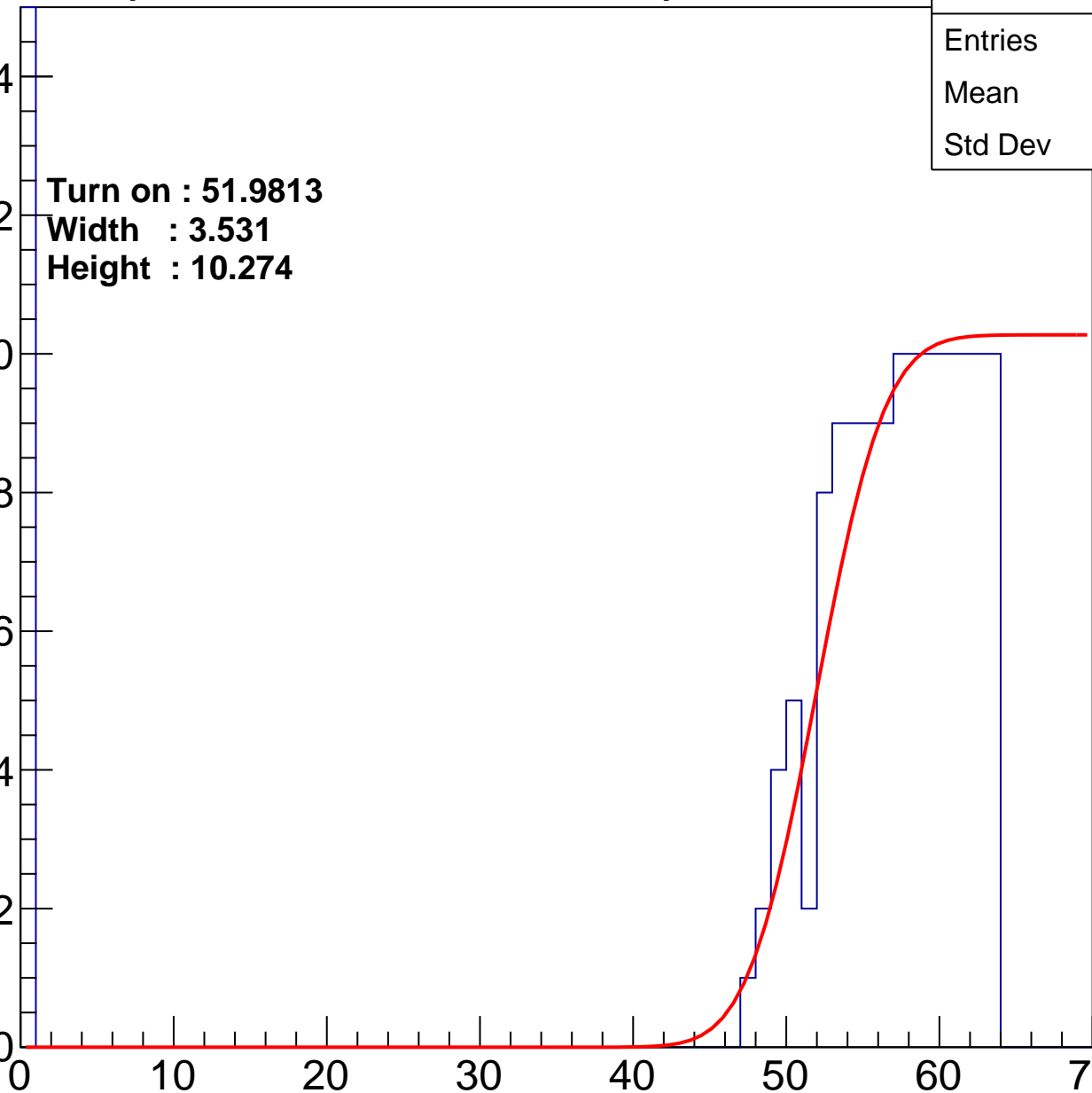
Width : 3.531

Height : 10.274

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	34.53
Std Dev	28.07

Turn on : 51.5550

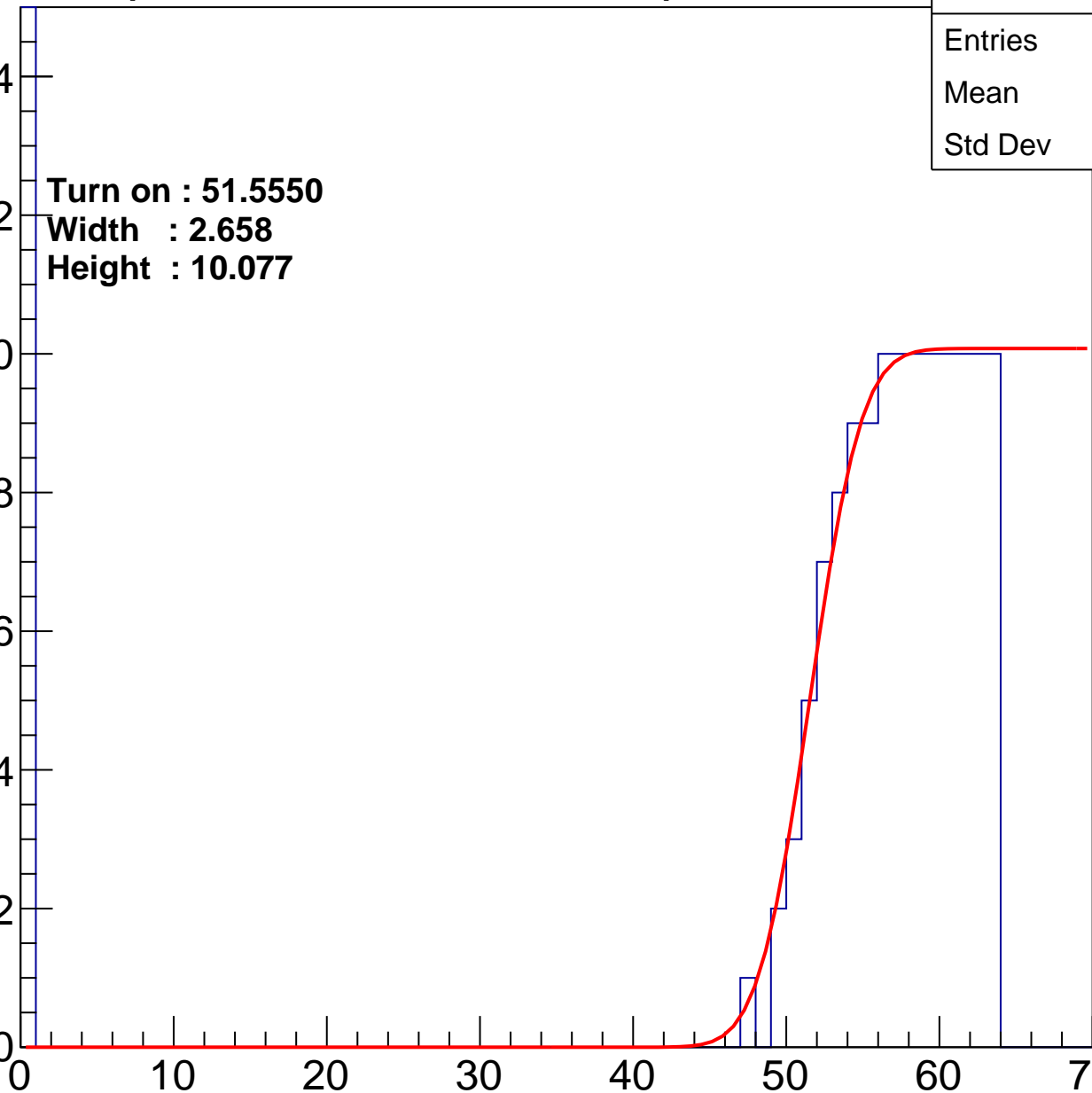
Width : 2.658

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	36.26
Std Dev	27.85

Turn on : 52.9909

Width : 3.321

Height : 10.302

Entry

14

12

10

8

6

4

2

0

0

10

20

30

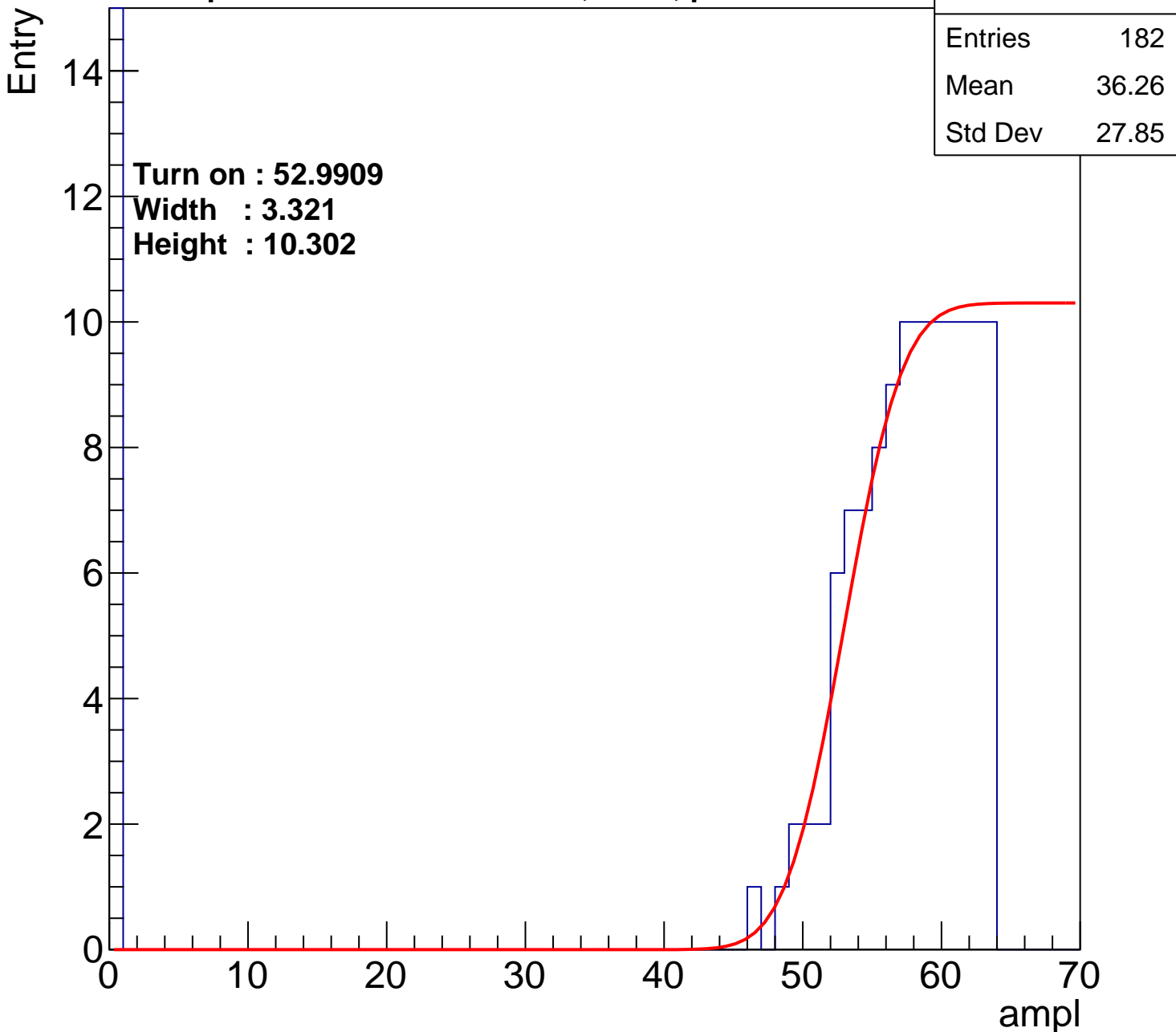
40

50

60

70

ampl



B1L104S, U14-ch40

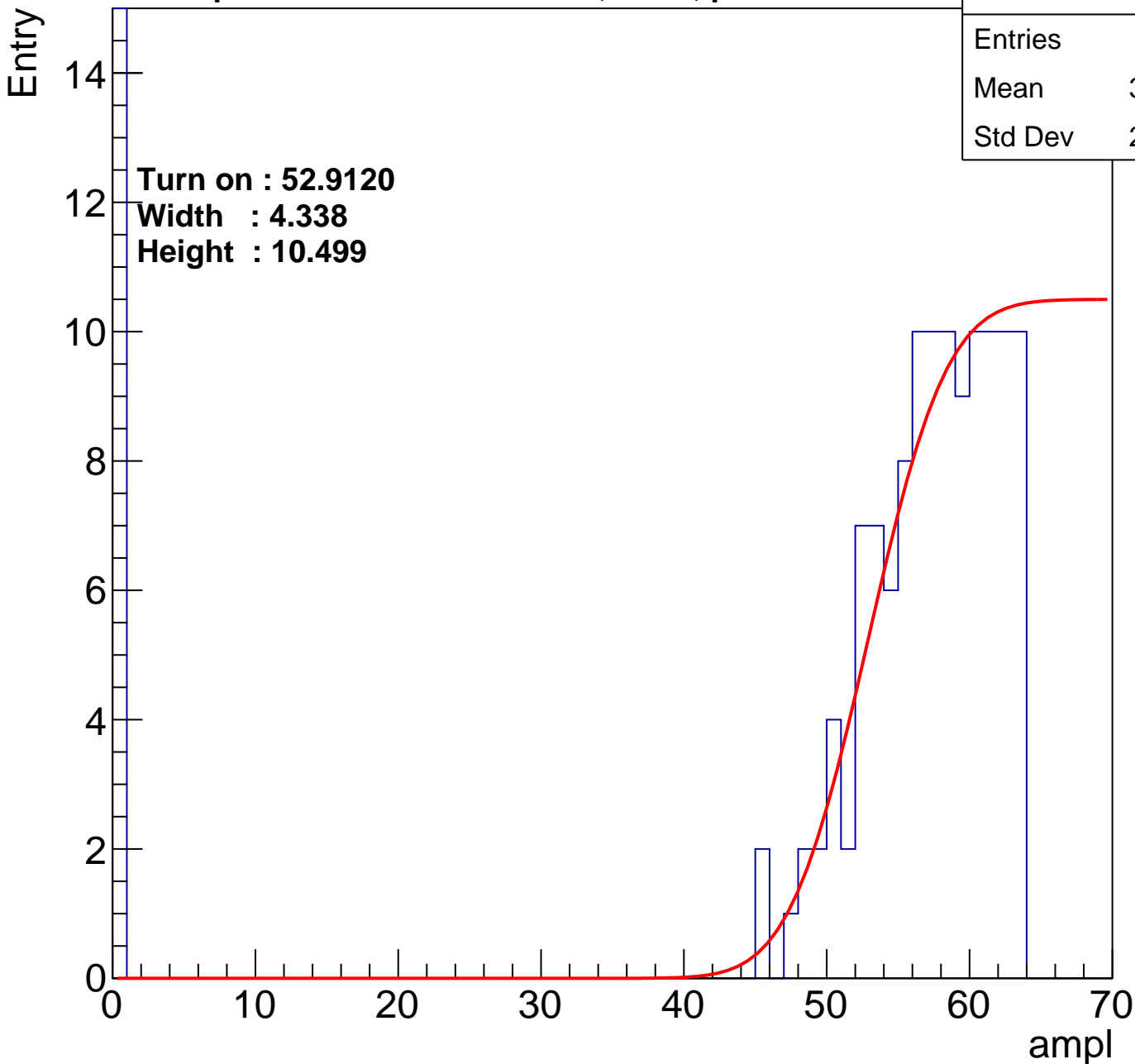
calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	39.05
Std Dev	26.67

Turn on : 52.9120

Width : 4.338

Height : 10.499



B1L104S, U14-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	37.75
Std Dev	27.74

Turn on : 54.1110

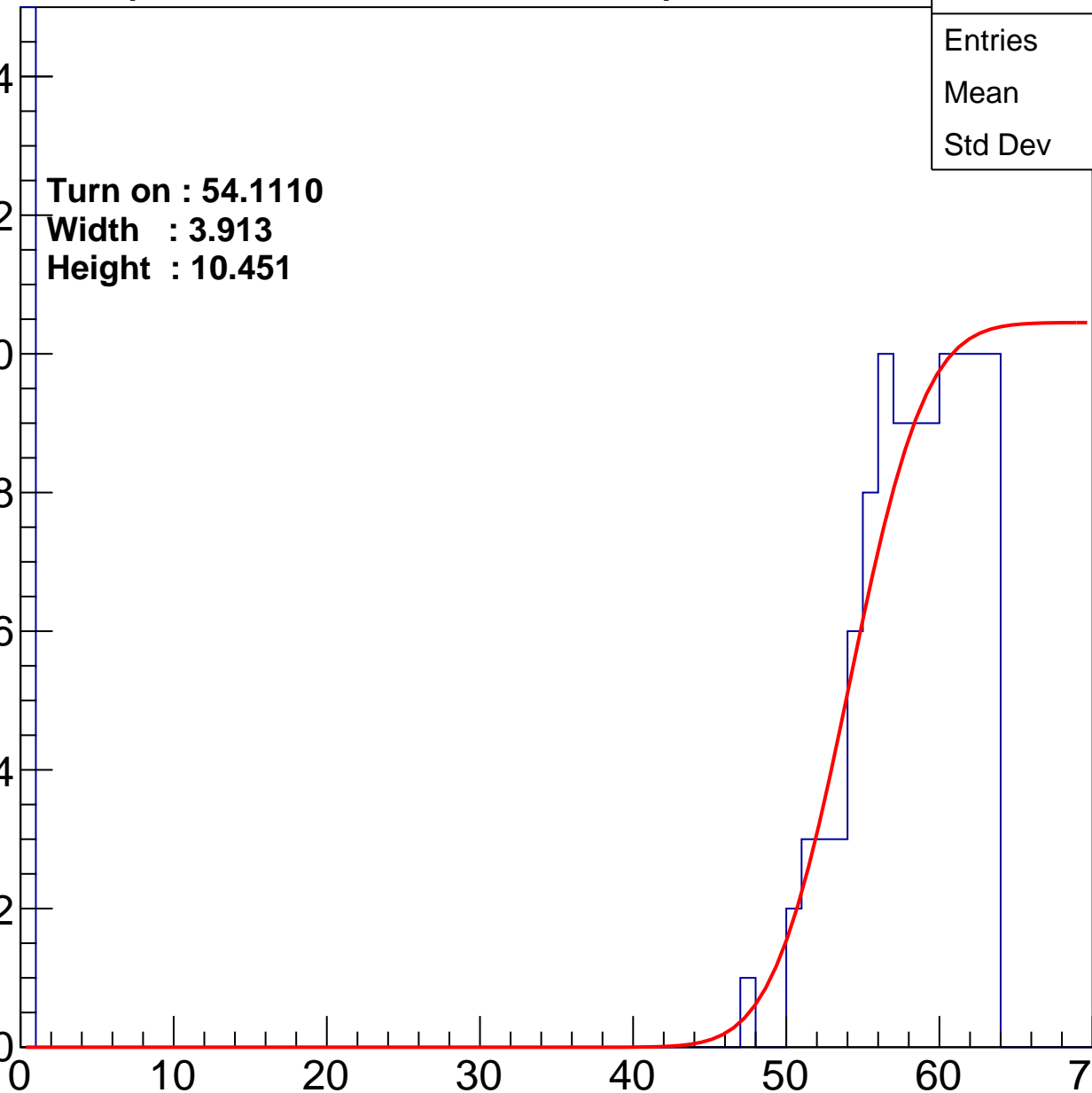
Width : 3.913

Height : 10.451

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch42

calib_packv5_033123_0516.root, FC#4, port A1

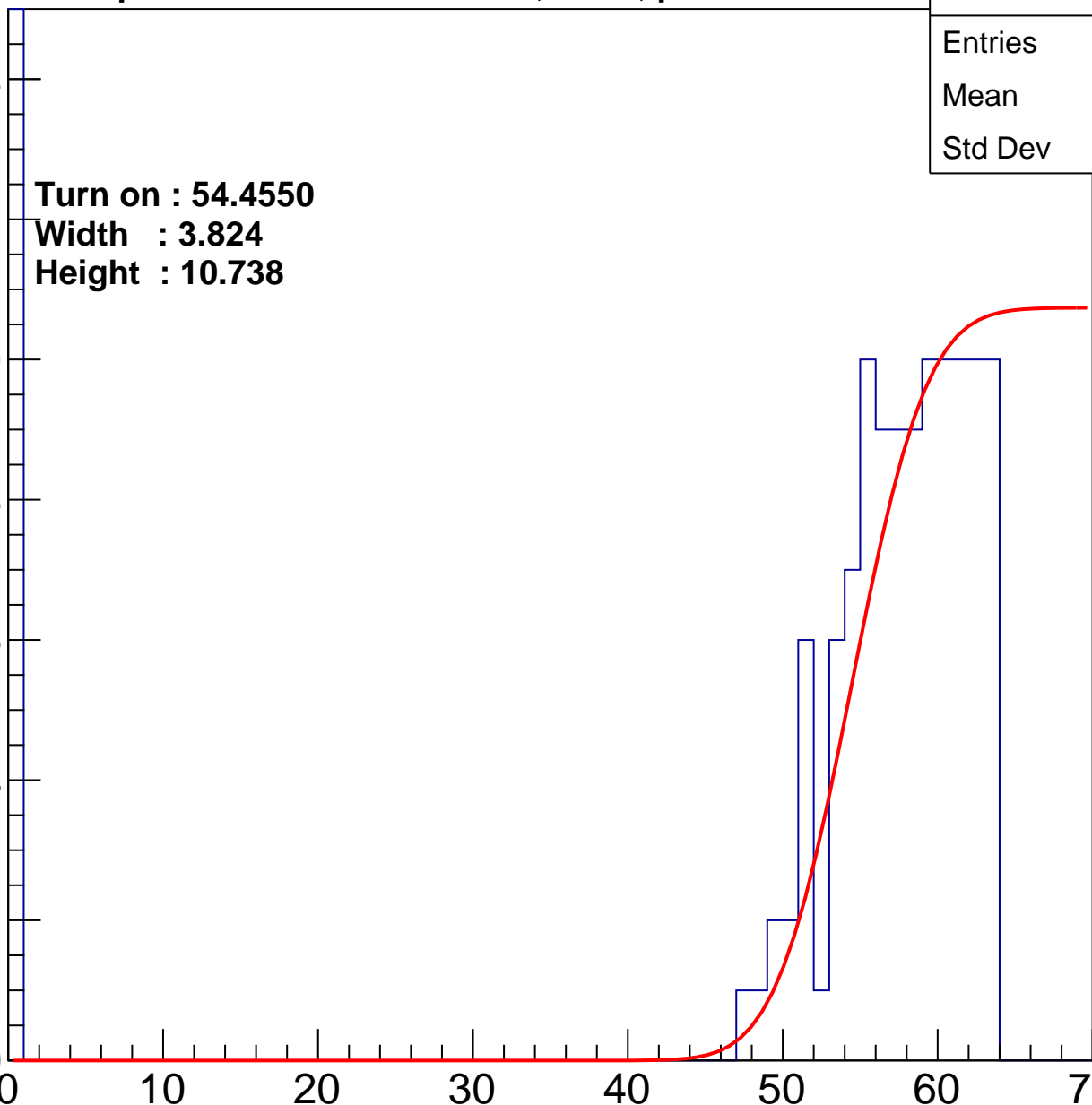
Entry

14
12
10
8
6
4
2
0

Turn on : 54.4550
Width : 3.824
Height : 10.738

Entries	213
Mean	30.45
Std Dev	28.79

ampl



B1L104S, U14-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	31.07
Std Dev	29.25

Turn on : 55.3653

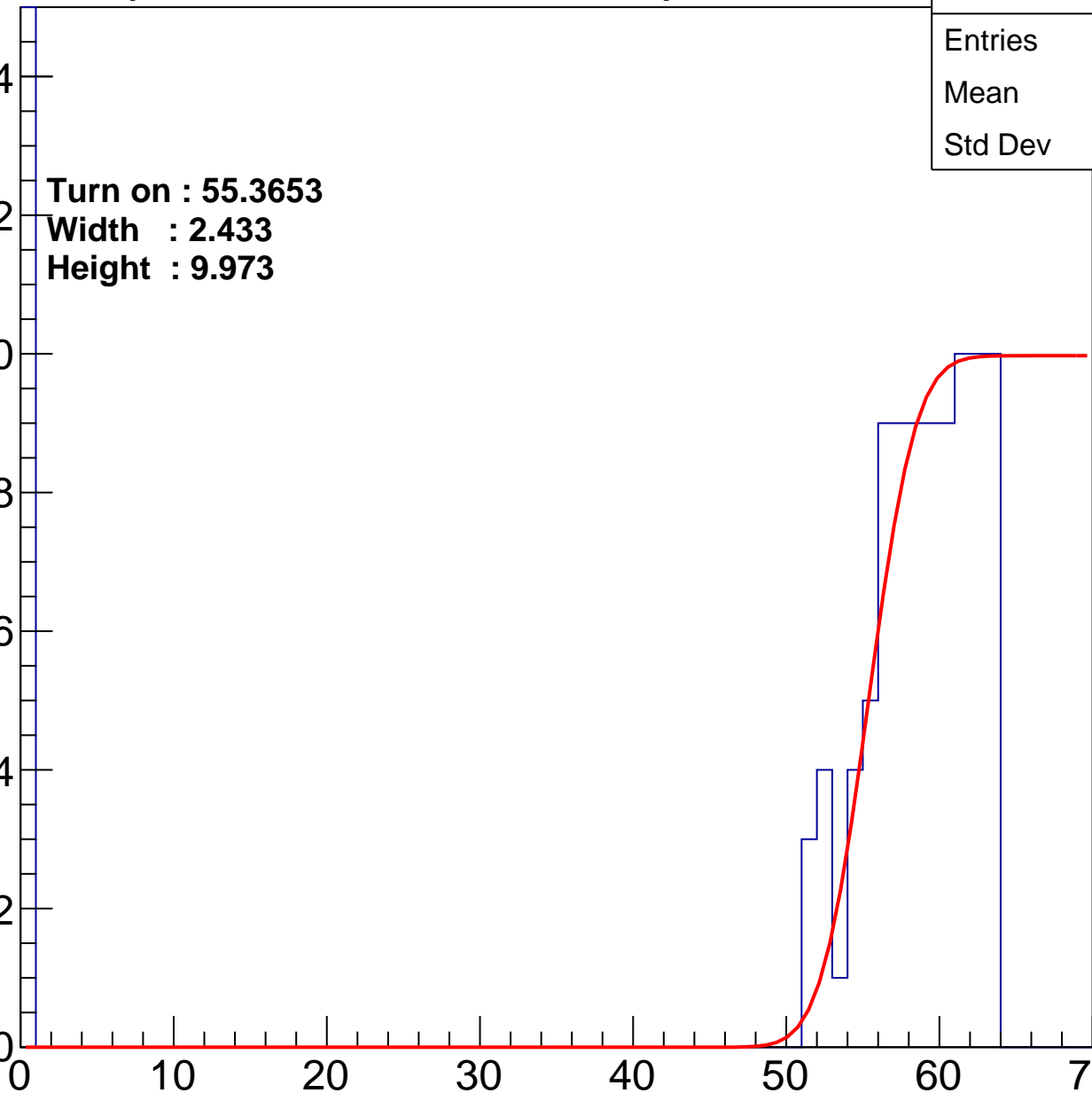
Width : 2.433

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	34.31
Std Dev	28.18

Turn on : 51.3602

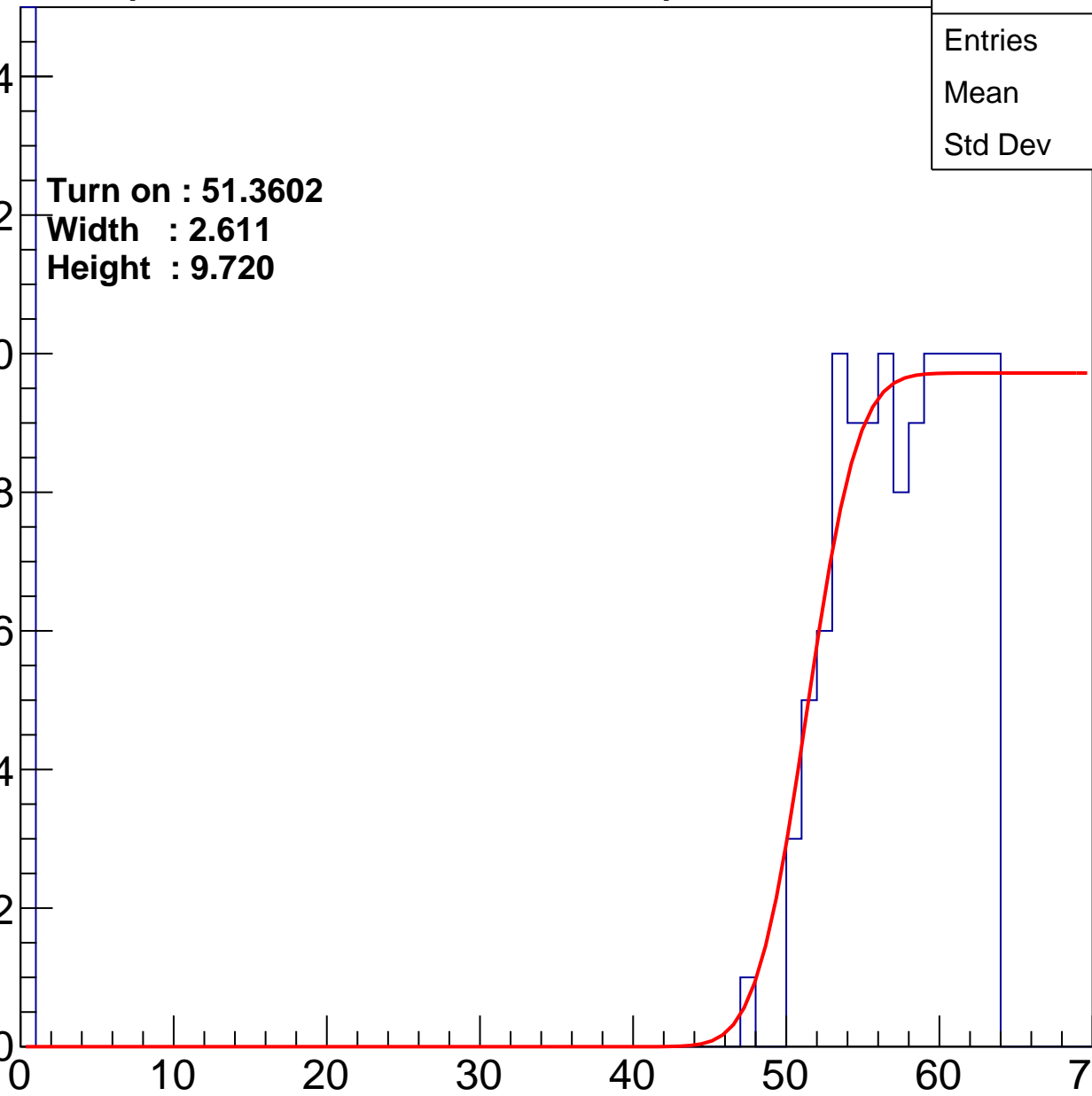
Width : 2.611

Height : 9.720

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	35.1
Std Dev	28.04

Turn on : 52.7885

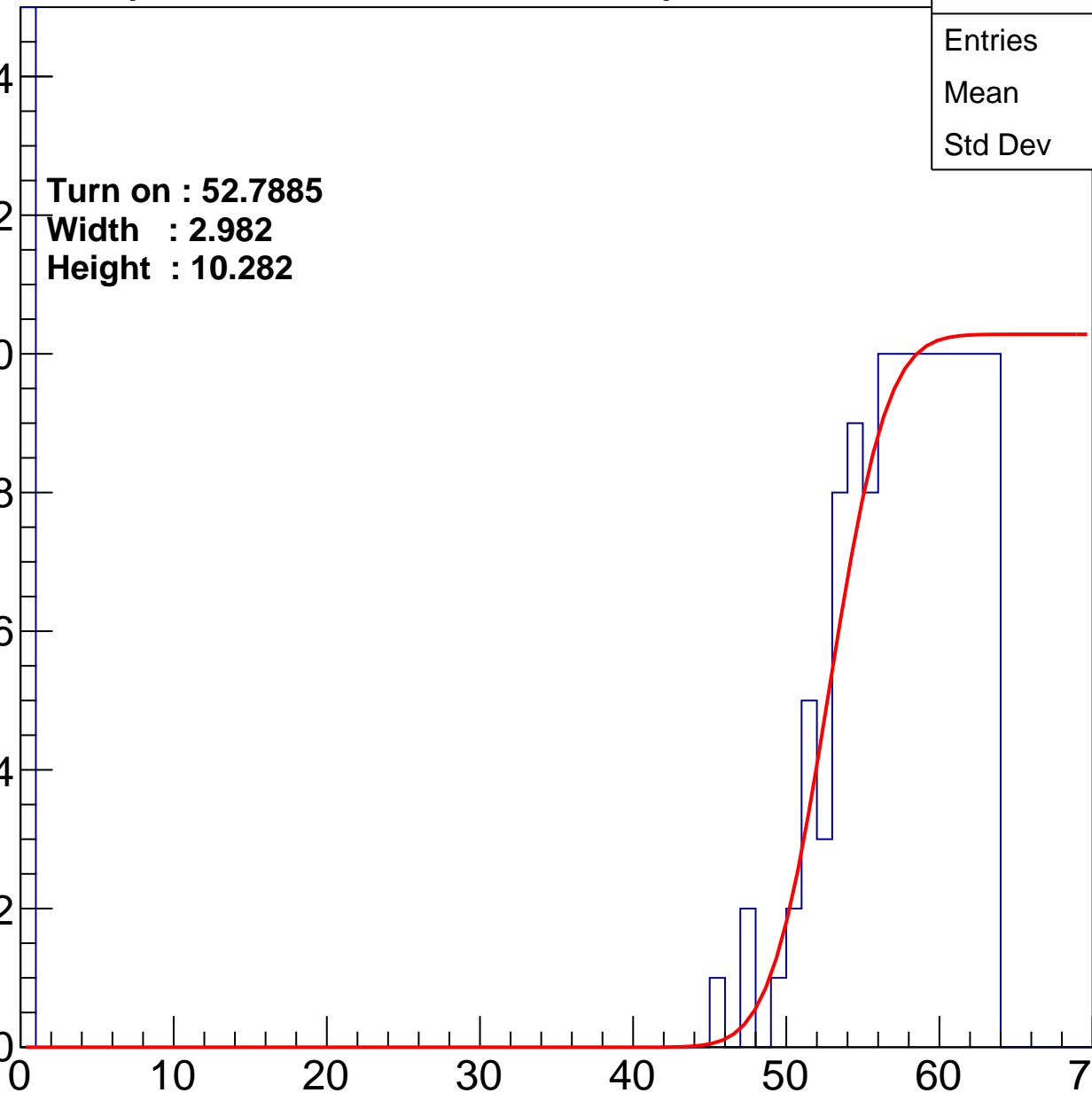
Width : 2.982

Height : 10.282

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	32.22
Std Dev	28.42

Turn on : 51.5493

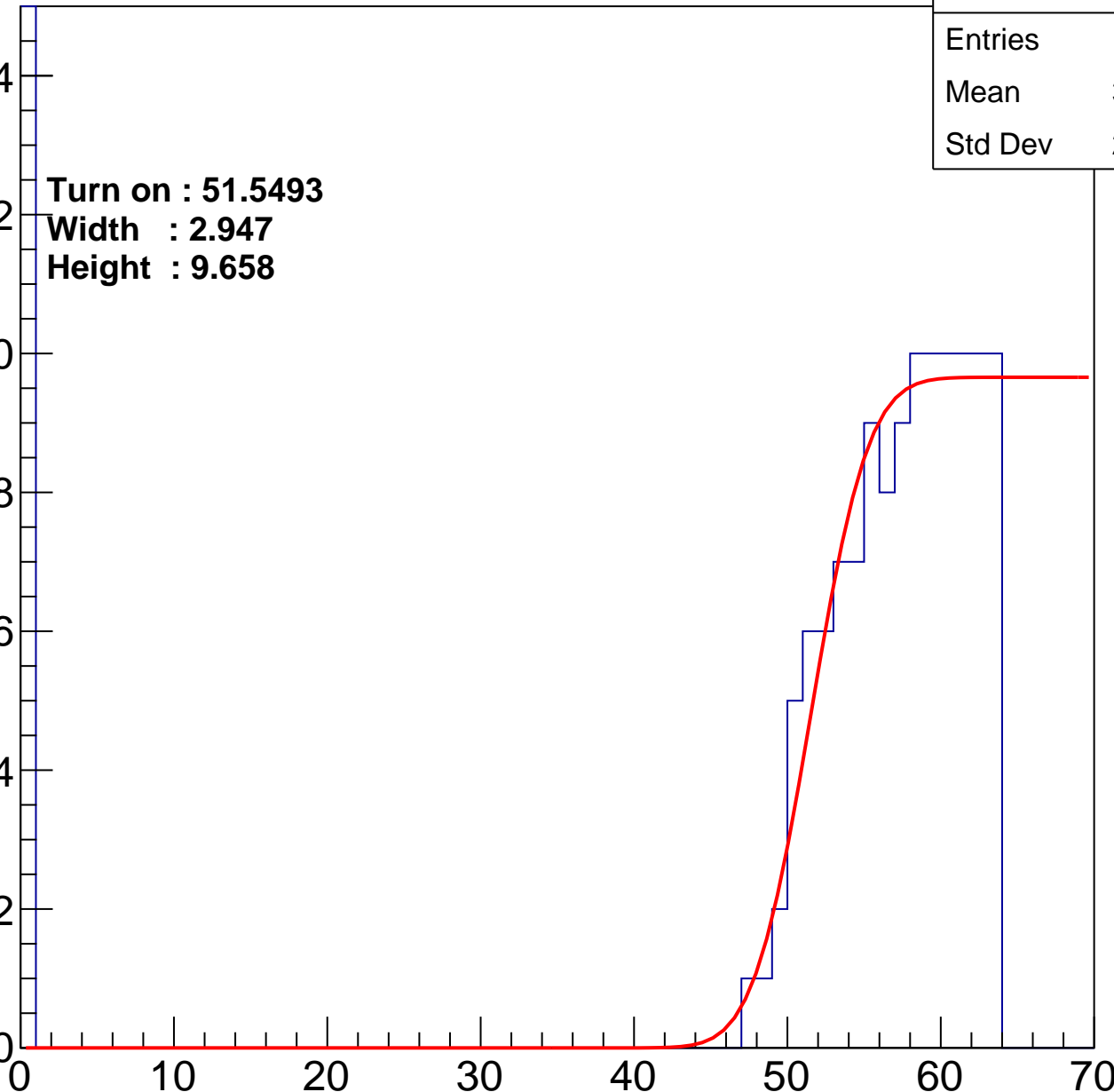
Width : 2.947

Height : 9.658

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	32.44
Std Dev	28.61

Turn on : 52.9354

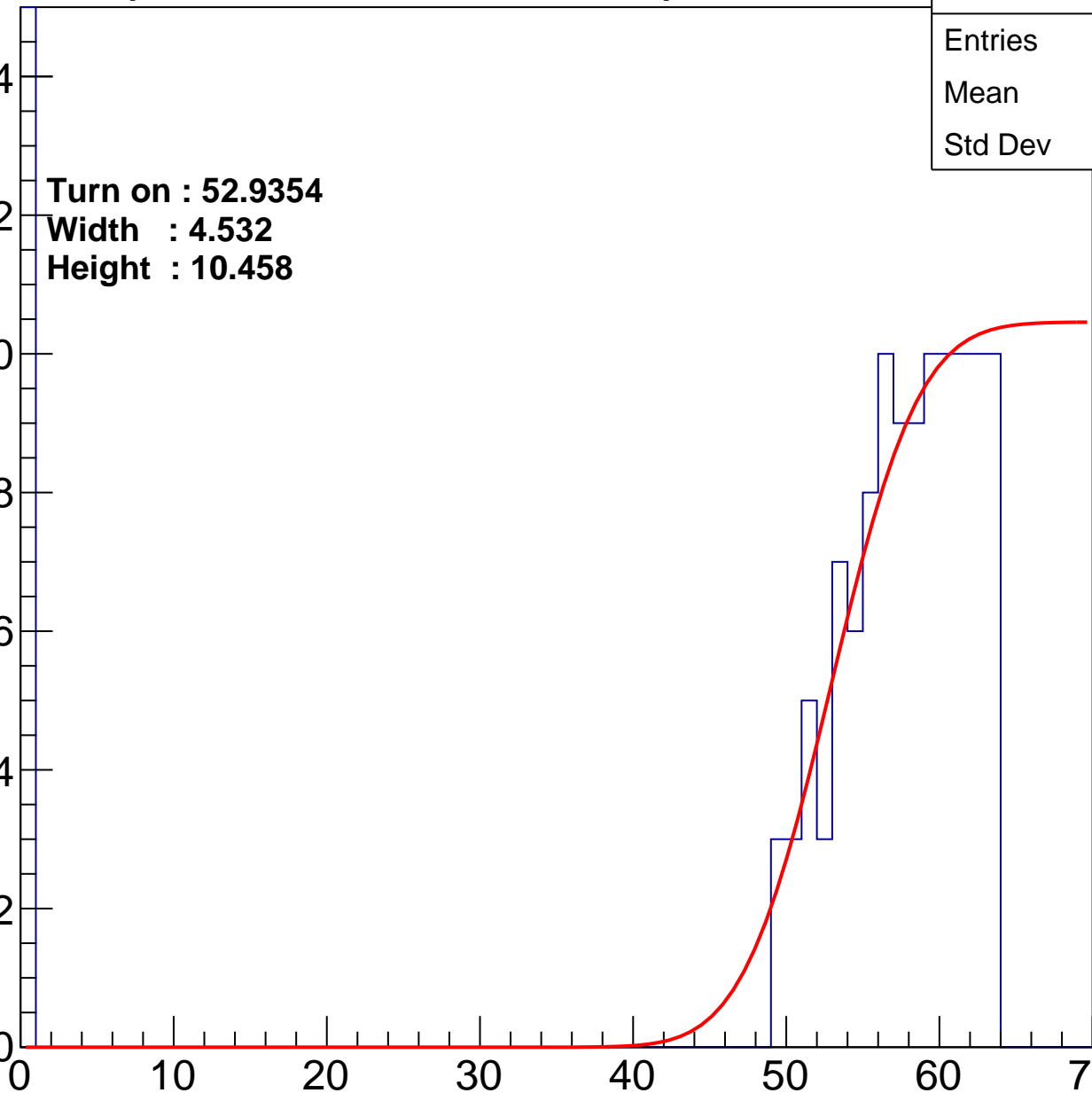
Width : 4.532

Height : 10.458

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch48

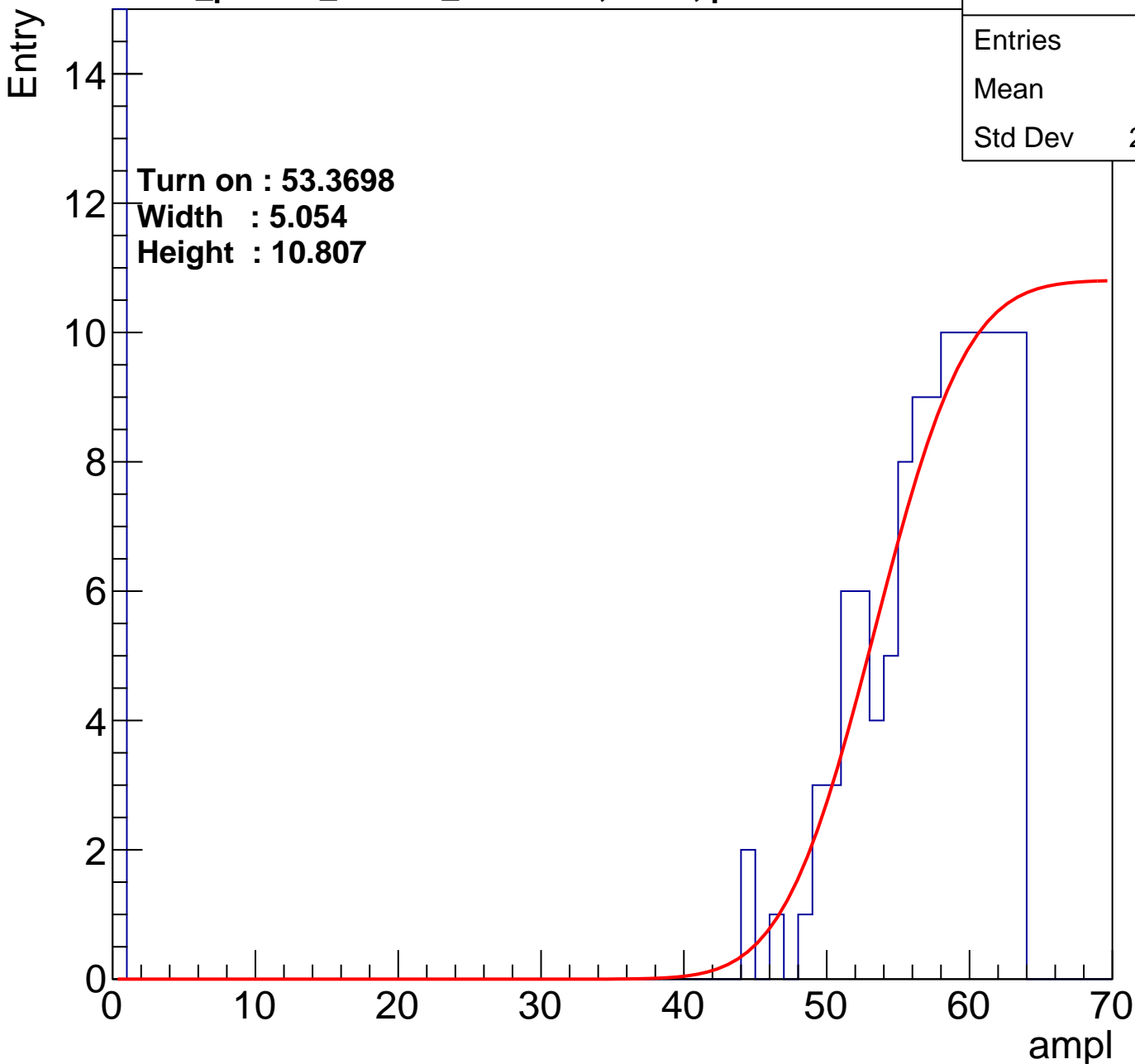
calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	32.2
Std Dev	28.44

Turn on : 53.3698

Width : 5.054

Height : 10.807



B1L104S, U14-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	33.06
Std Dev	28.33

Turn on : 51.6690

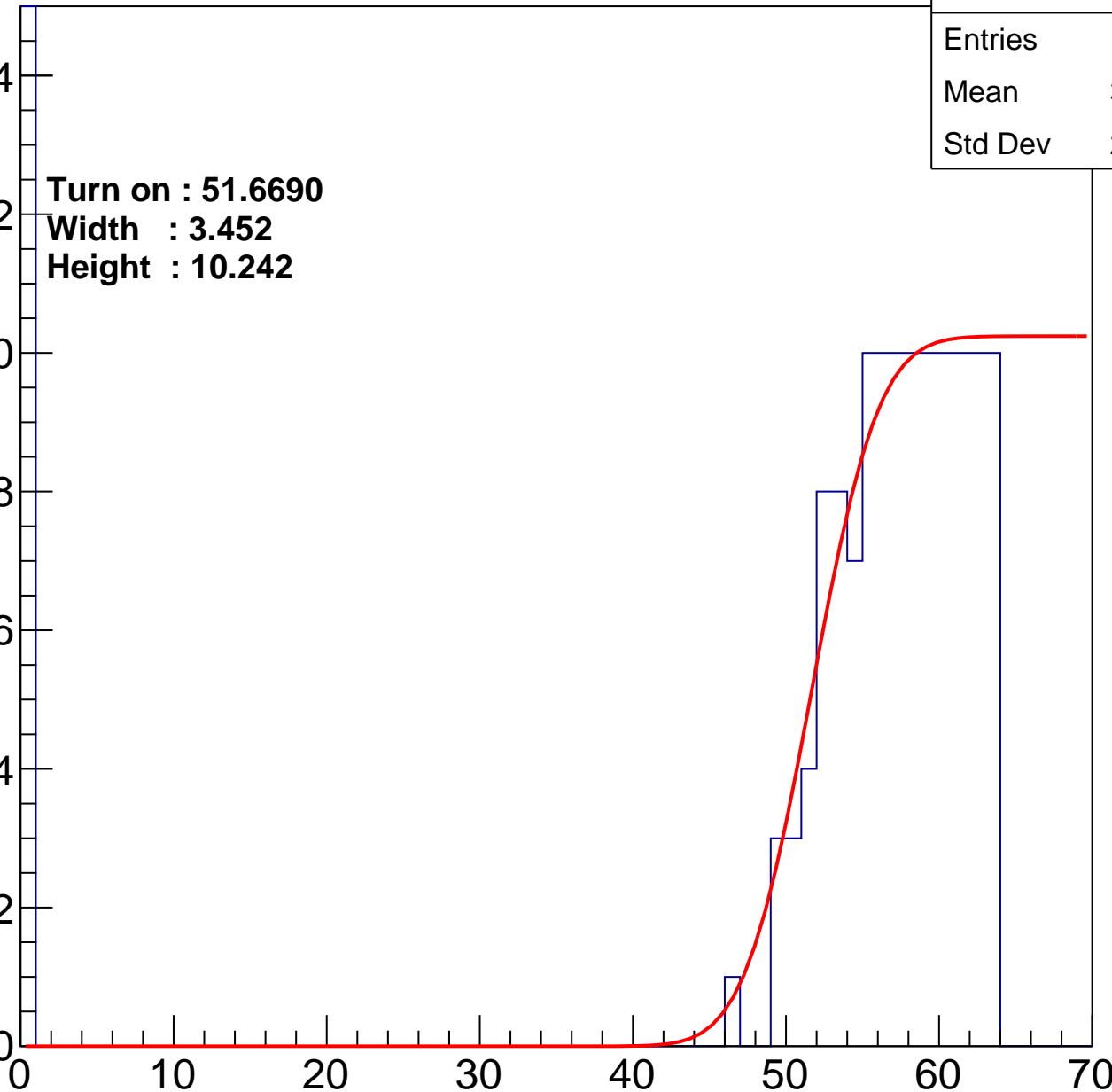
Width : 3.452

Height : 10.242

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	34.13
Std Dev	28.05

Turn on : 51.5139

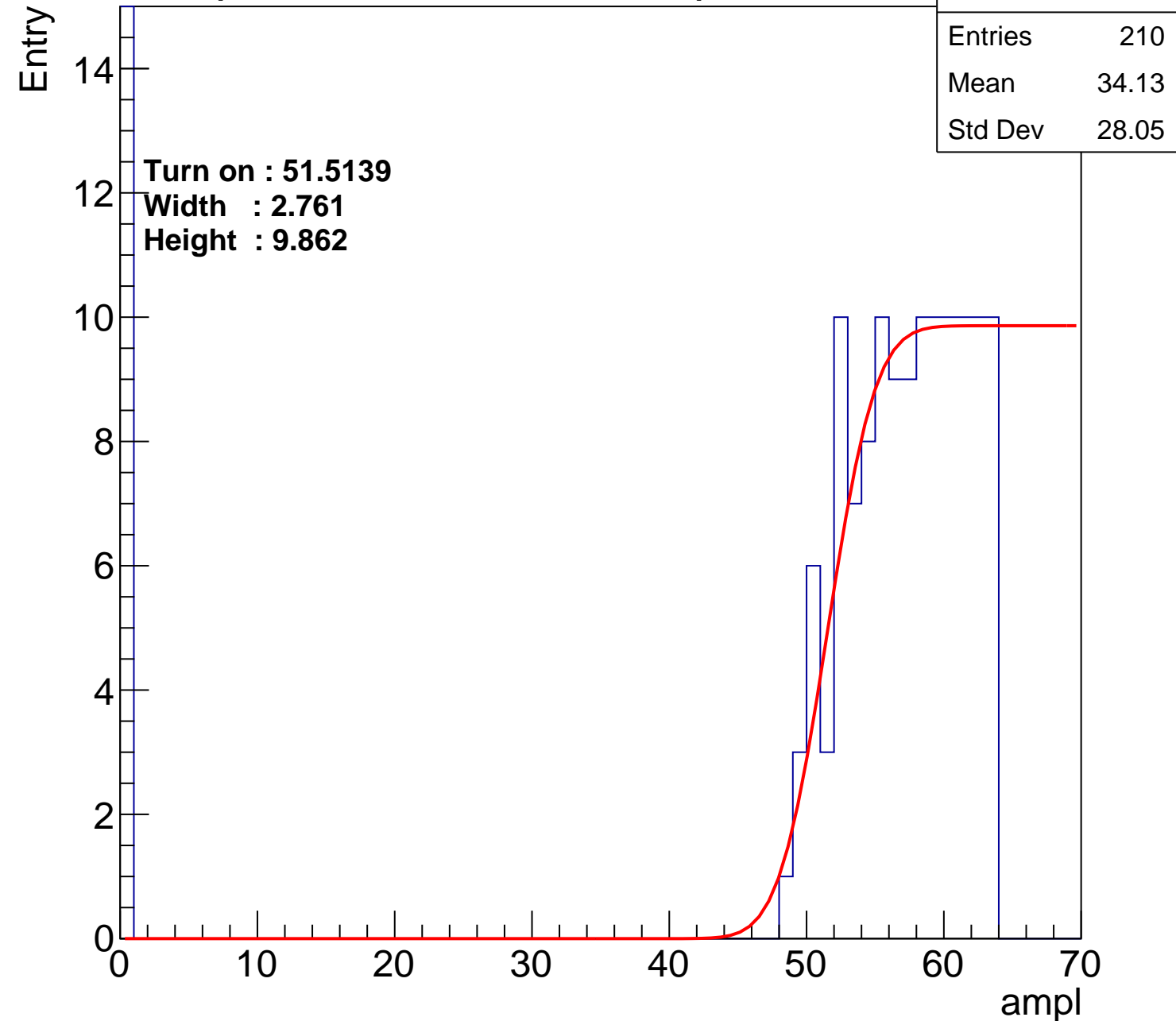
Width : 2.761

Height : 9.862

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch51

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	31.37
Std Dev	29.03

Turn on : 53.8652

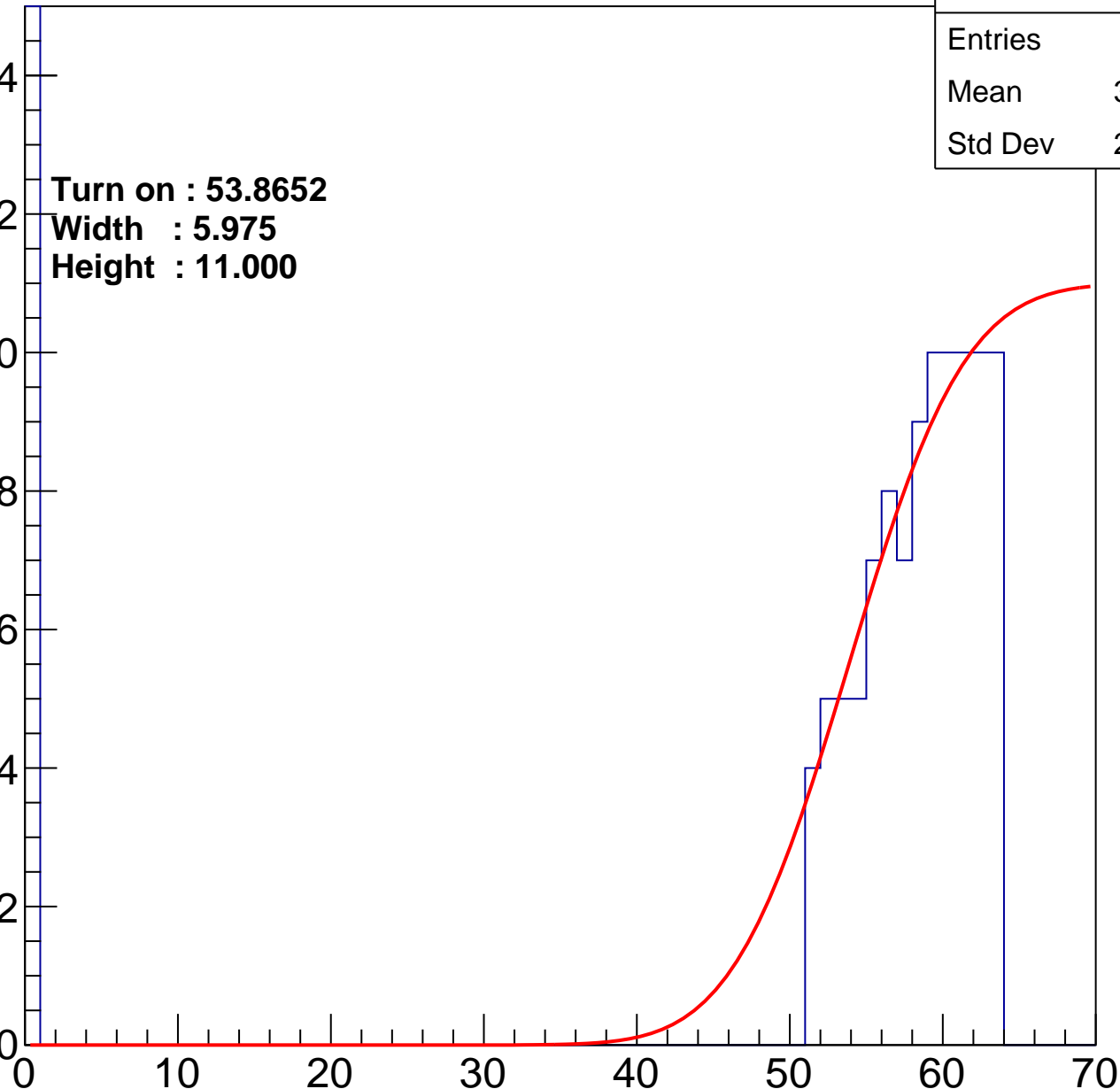
Width : 5.975

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch52

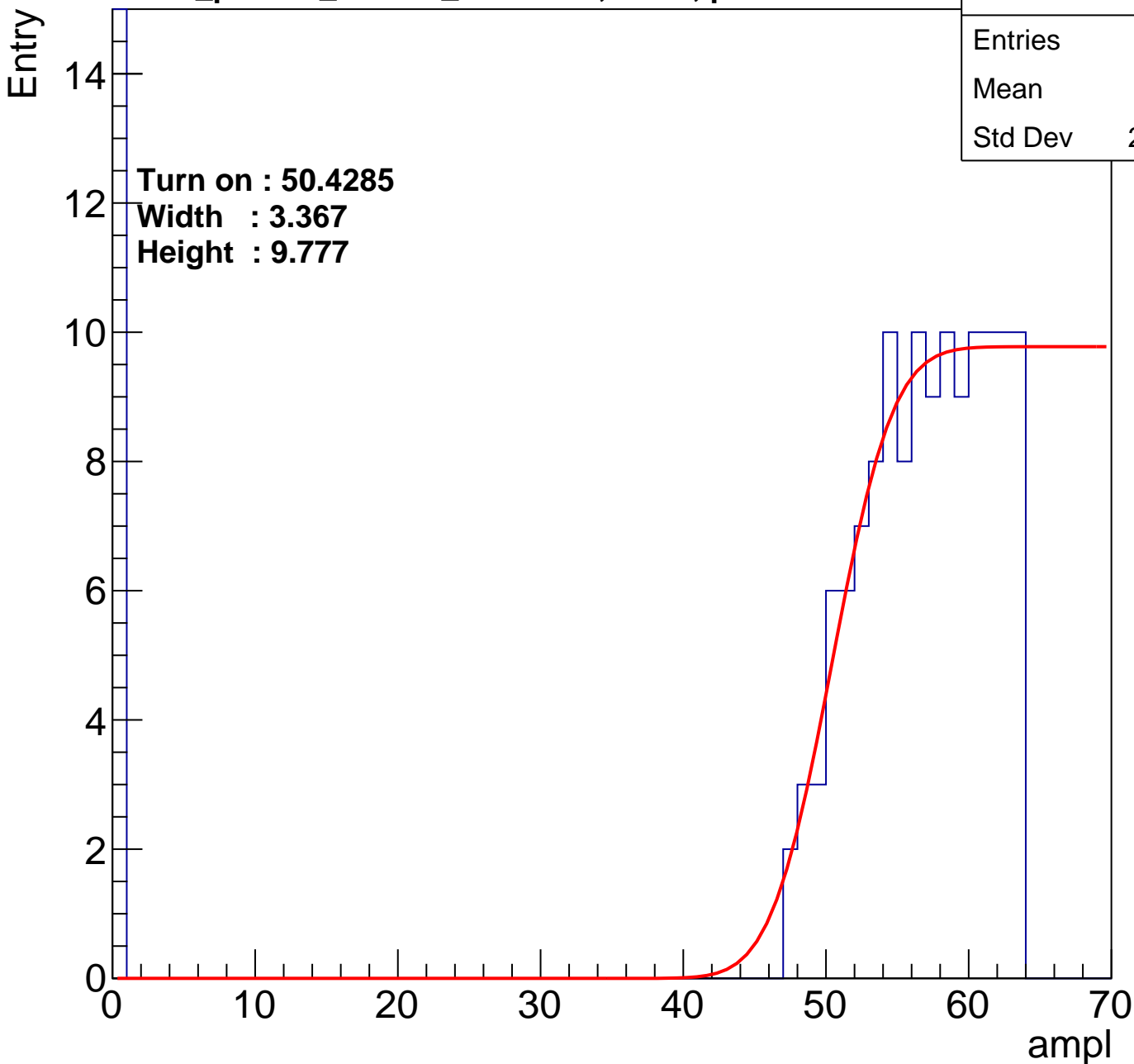
calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	37.2
Std Dev	27.03

Turn on : 50.4285

Width : 3.367

Height : 9.777



B1L104S, U14-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	221
Mean	31.01
Std Dev	28.61

Turn on : 52.2460

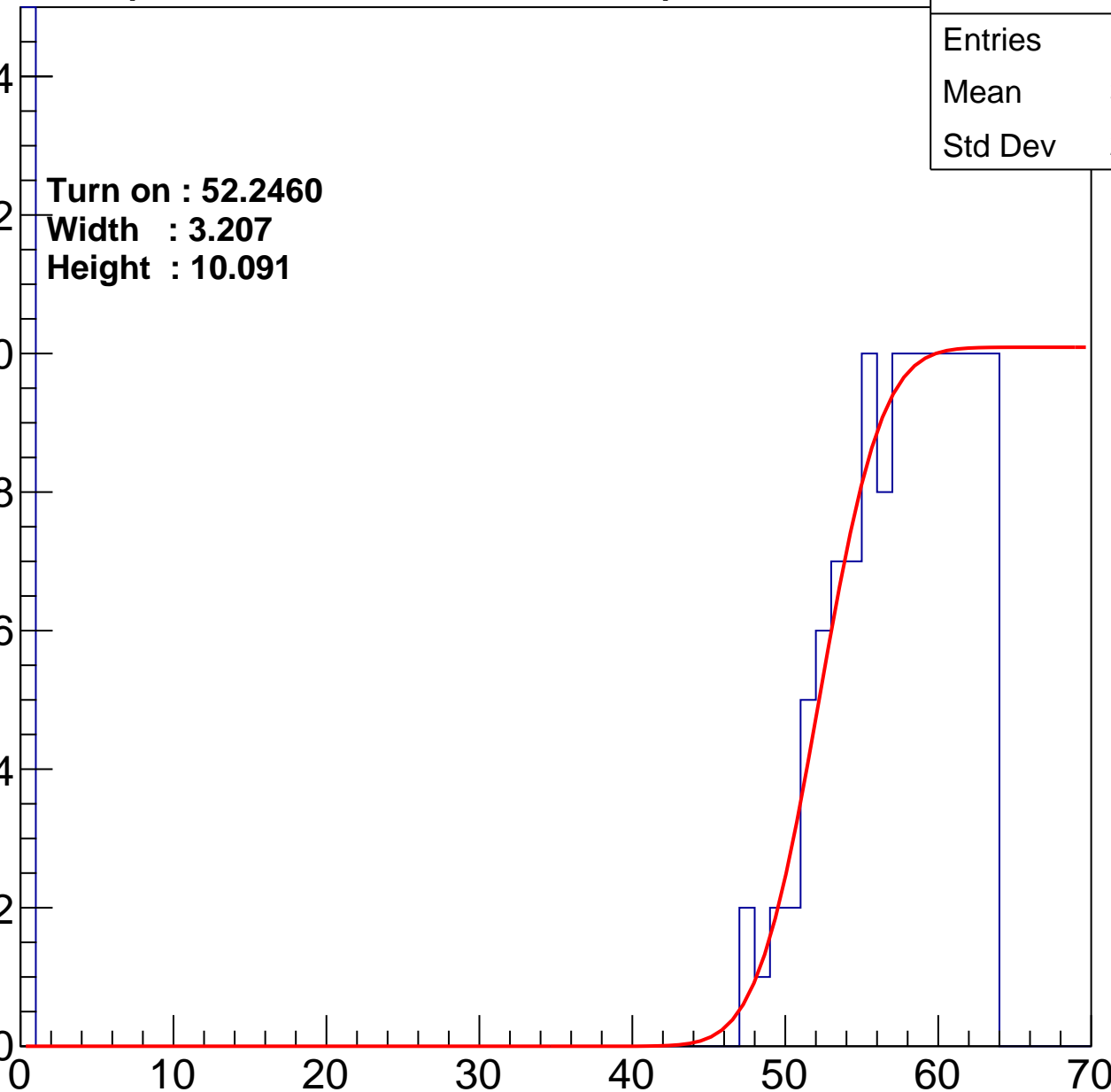
Width : 3.207

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	37.23
Std Dev	27.25

Turn on : 51.8644

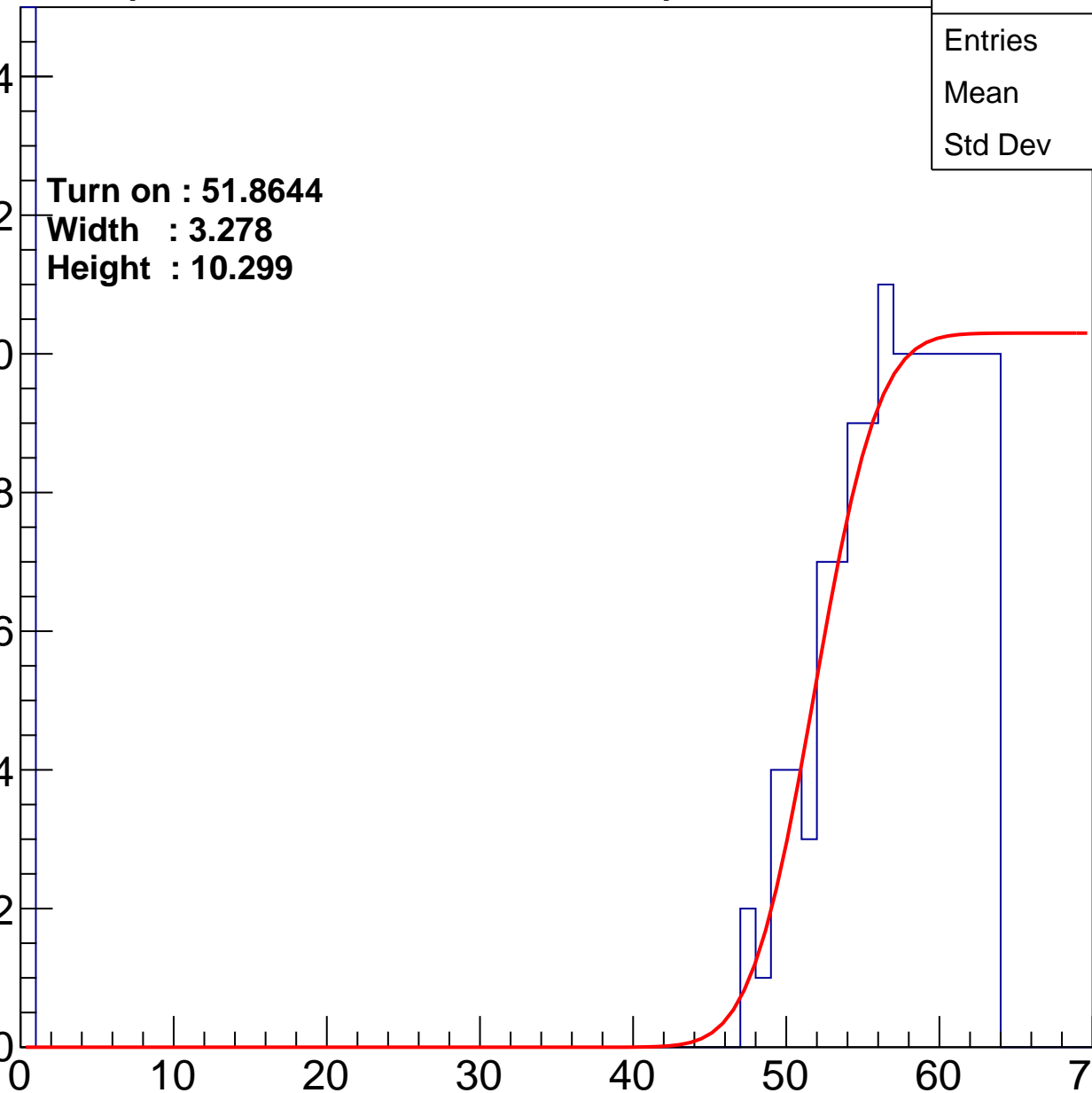
Width : 3.278

Height : 10.299

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	30.44
Std Dev	28.97

Turn on : 53.7685

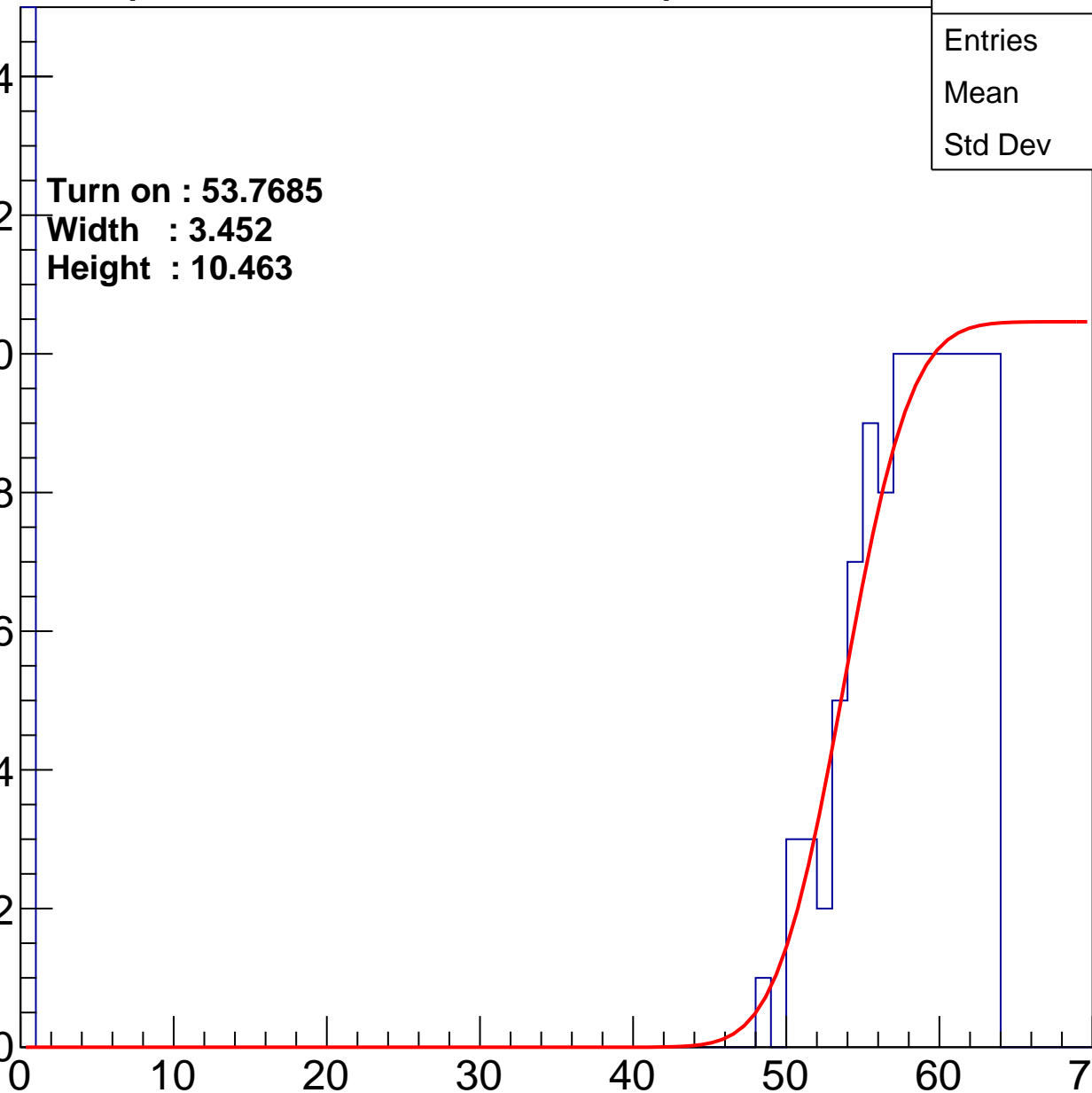
Width : 3.452

Height : 10.463

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	31.88
Std Dev	28.36

Turn on : 53.4074

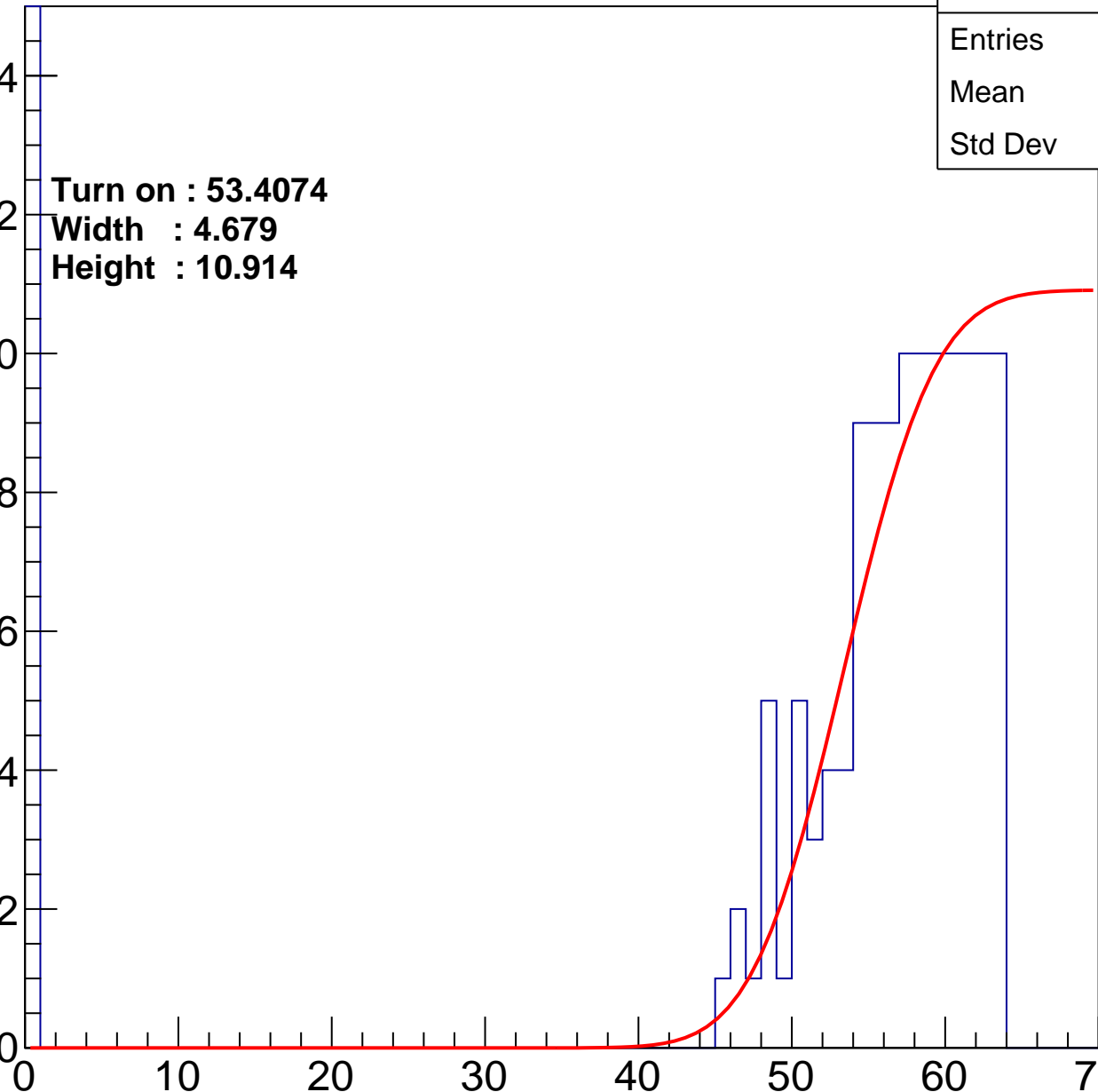
Width : 4.679

Height : 10.914

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	35.04
Std Dev	28.59

Turn on : 54.5211

Width : 3.378

Height : 10.511

Entry

14

12

10

8

6

4

2

0

0

10

20

30

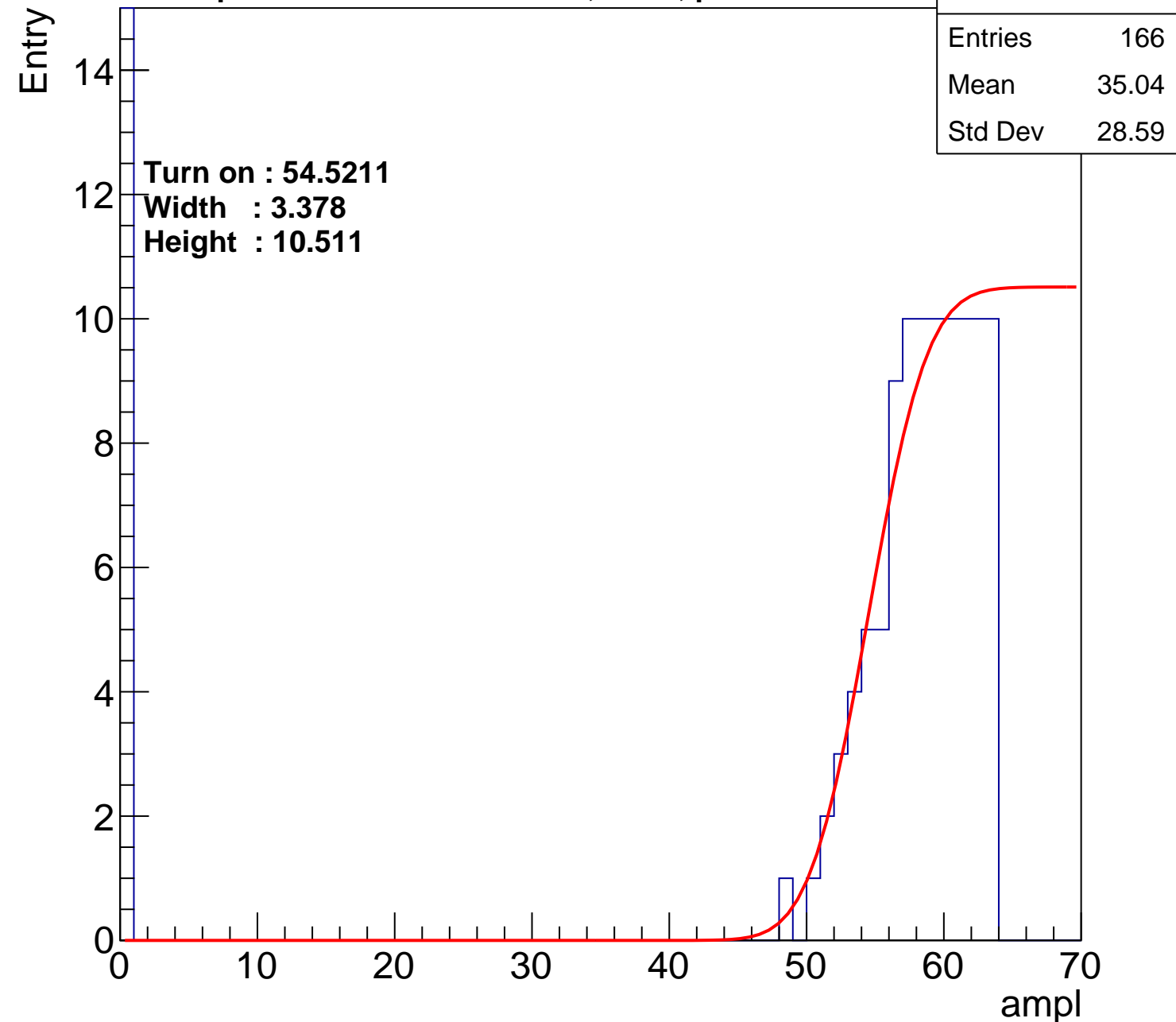
40

50

60

70

ampl



B1L104S, U14-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	32.54
Std Dev	28.37

Turn on : 53.0699

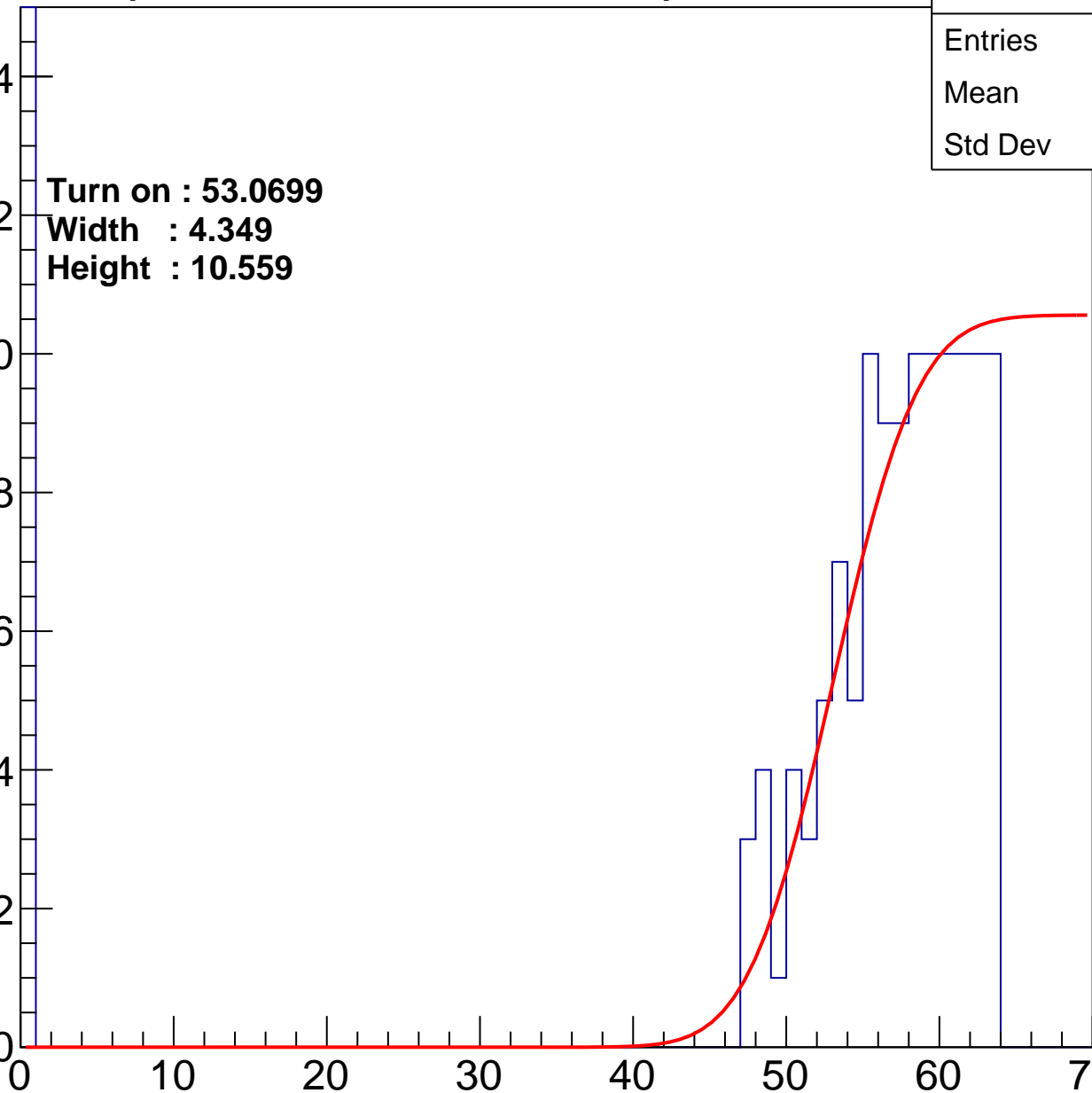
Width : 4.349

Height : 10.559

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	39.78
Std Dev	26.58

Turn on : 52.3998

Width : 5.122

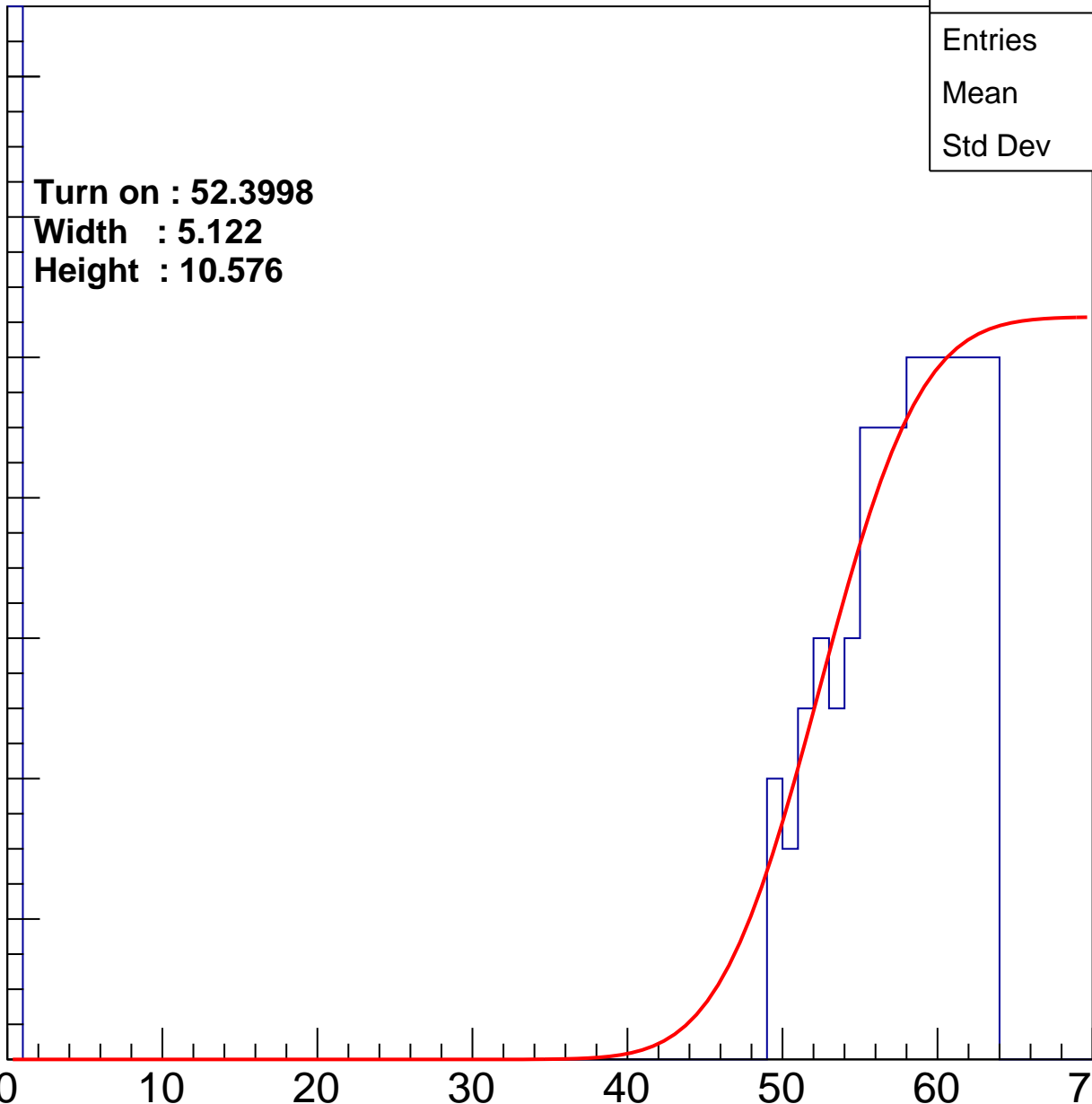
Height : 10.576

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U14-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	35.51
Std Dev	27.71

Turn on : 52.2451

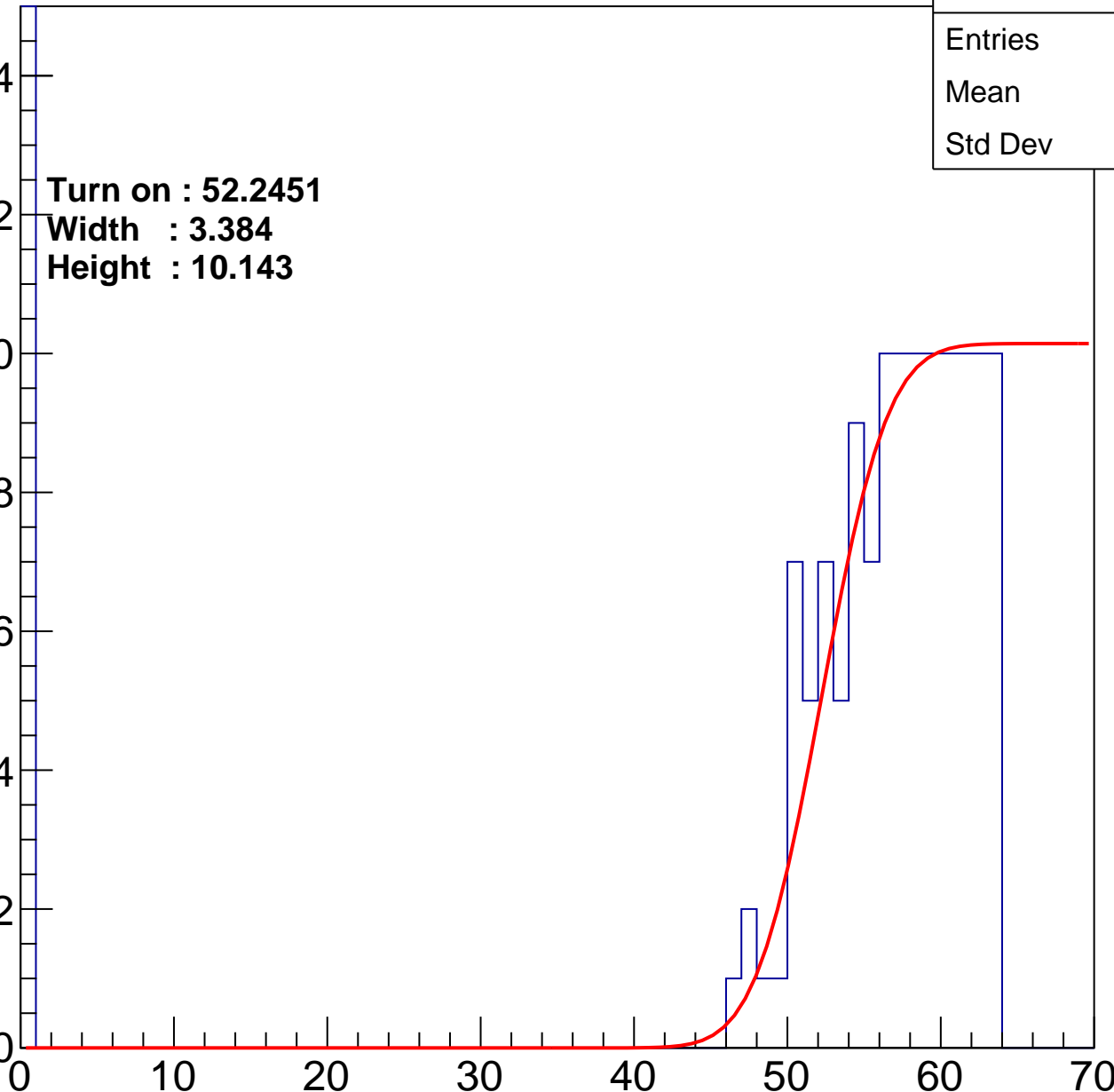
Width : 3.384

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	32.69
Std Dev	28.48

Turn on : 52.6462

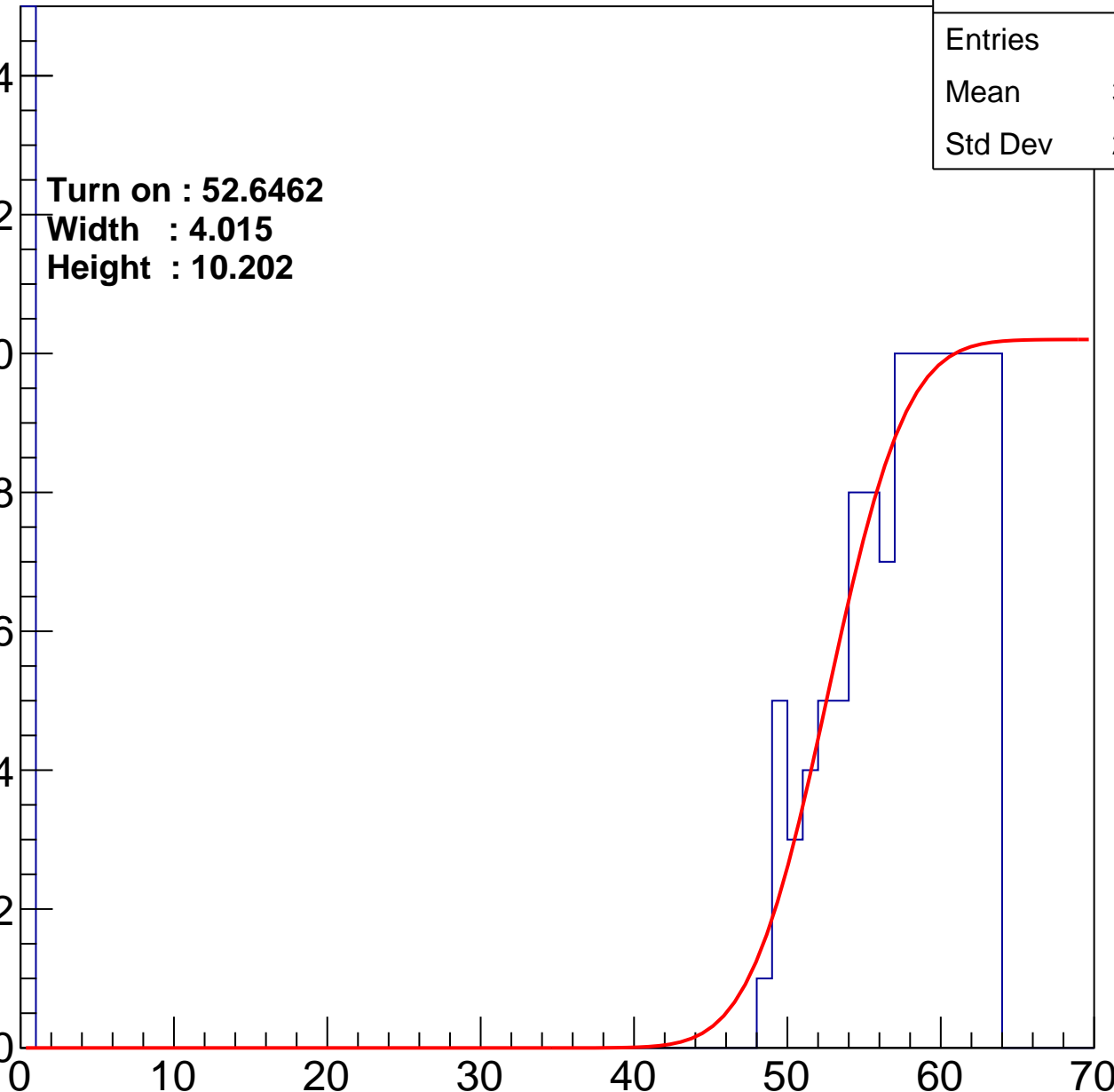
Width : 4.015

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	32.42
Std Dev	28.48

Turn on : 53.0020

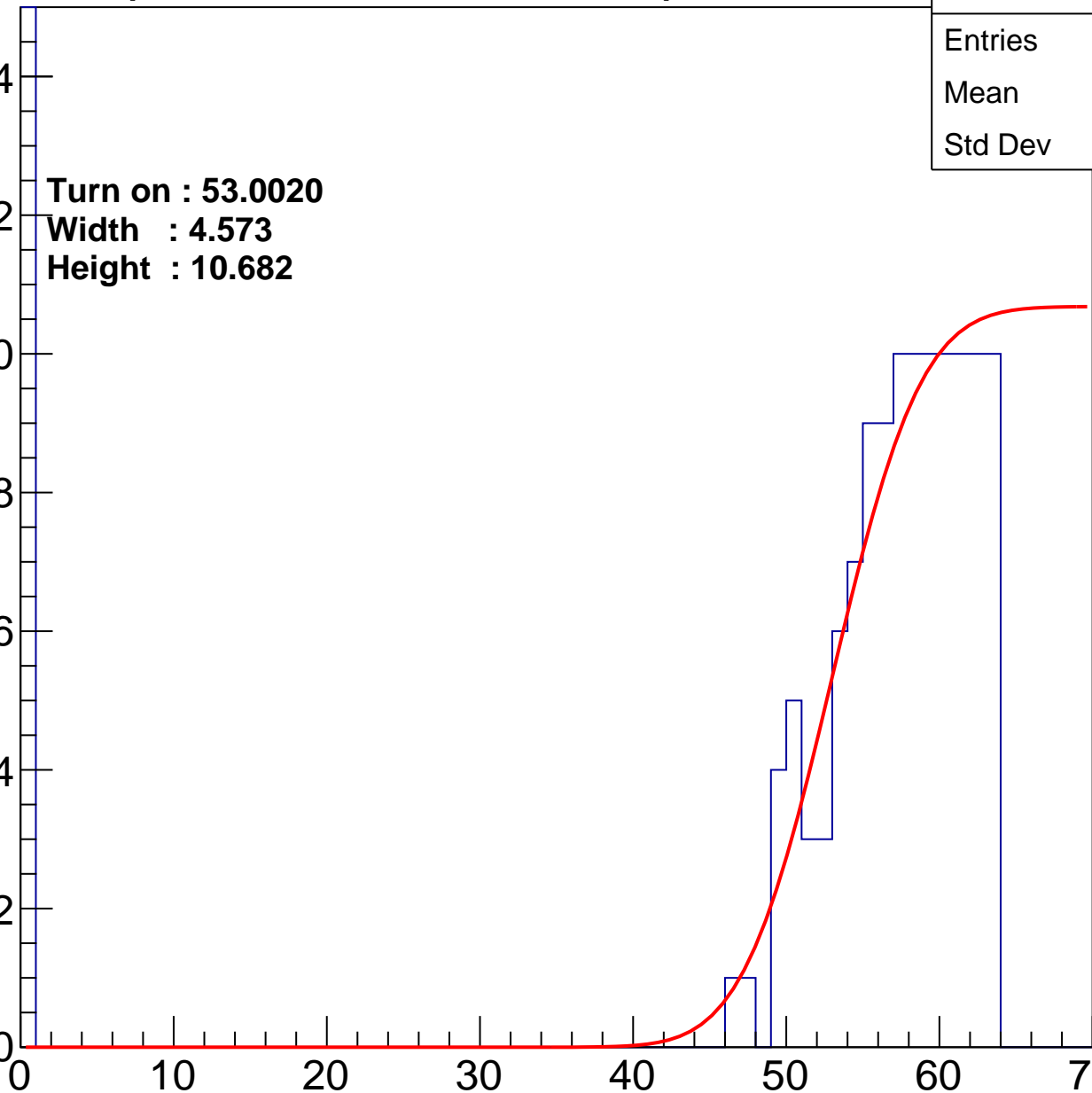
Width : 4.573

Height : 10.682

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	31.77
Std Dev	28.65

Turn on : 52.4992

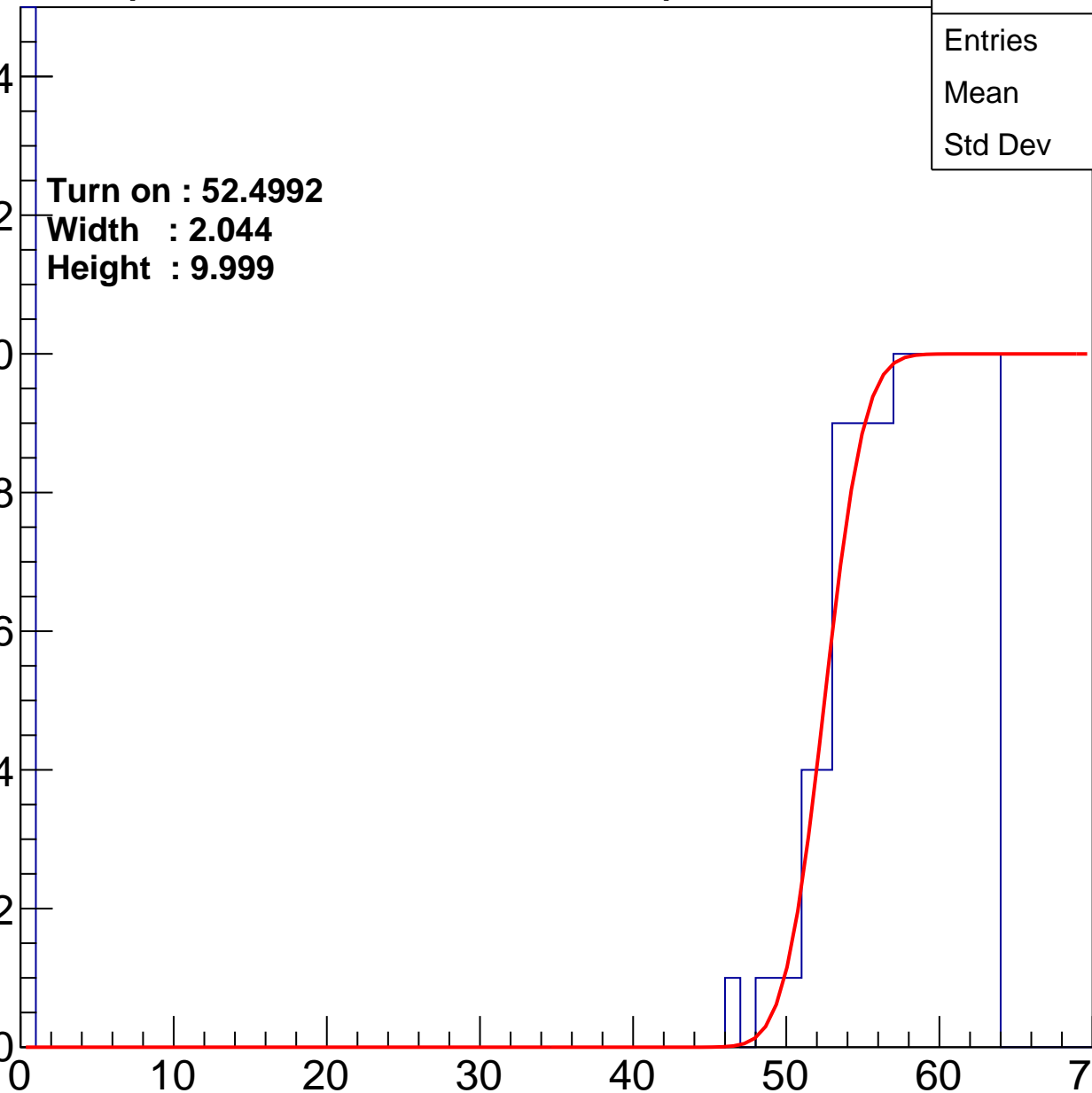
Width : 2.044

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	36.13
Std Dev	27.52

Turn on : 51.1707

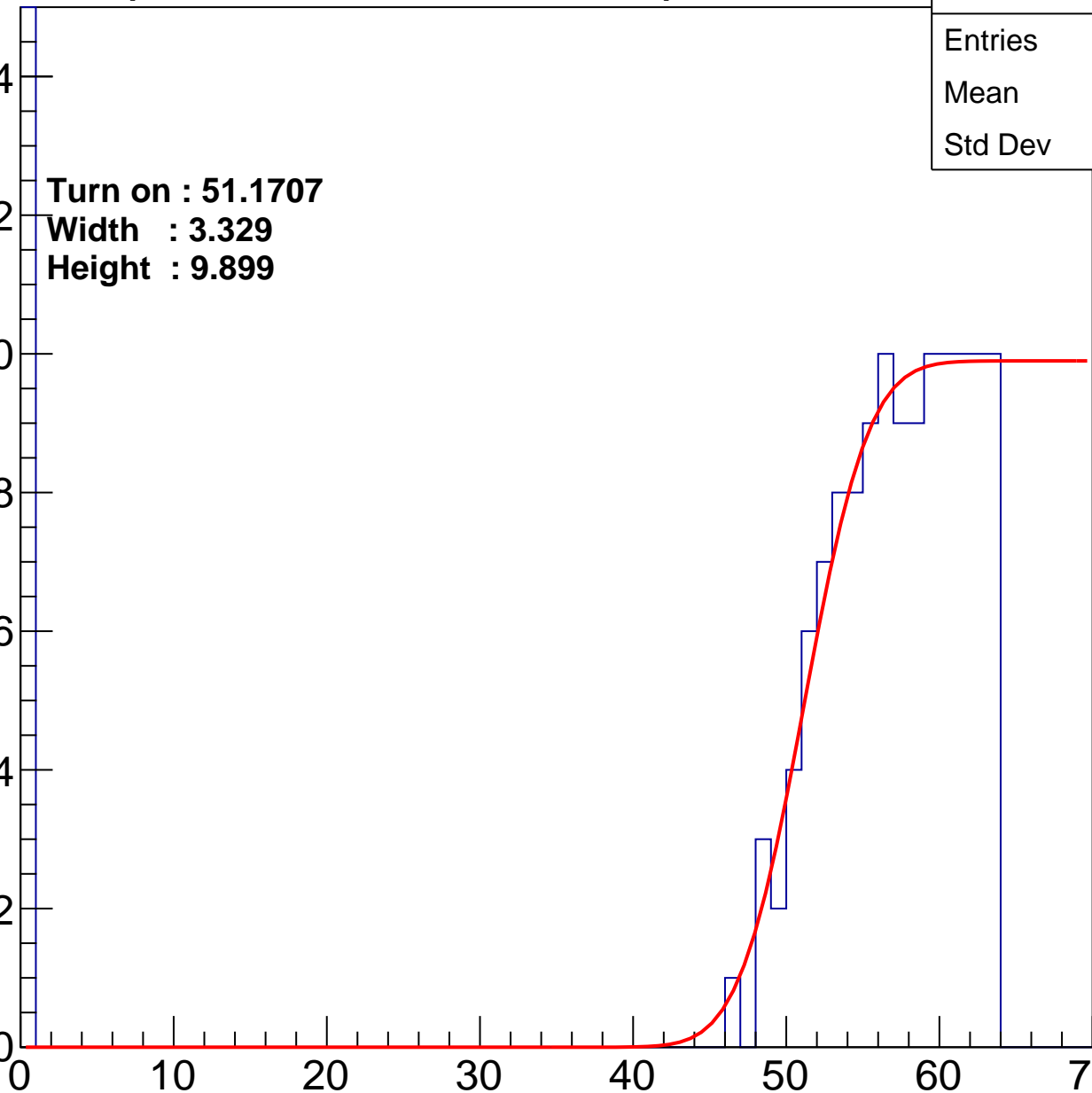
Width : 3.329

Height : 9.899

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch65

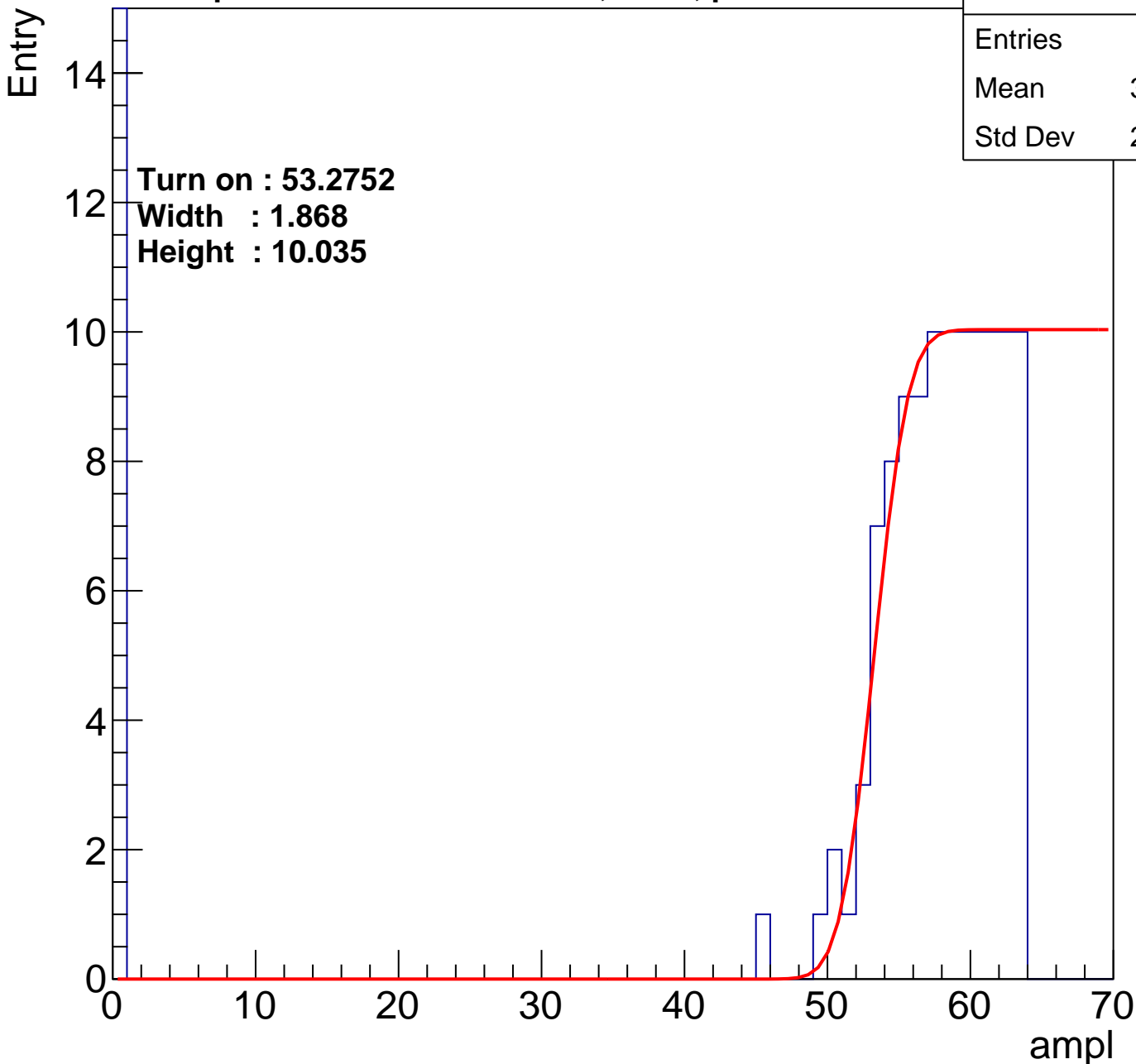
calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	31.54
Std Dev	28.84

Turn on : 53.2752

Width : 1.868

Height : 10.035



B1L104S, U14-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	37.31
Std Dev	26.93

Turn on : 51.1308

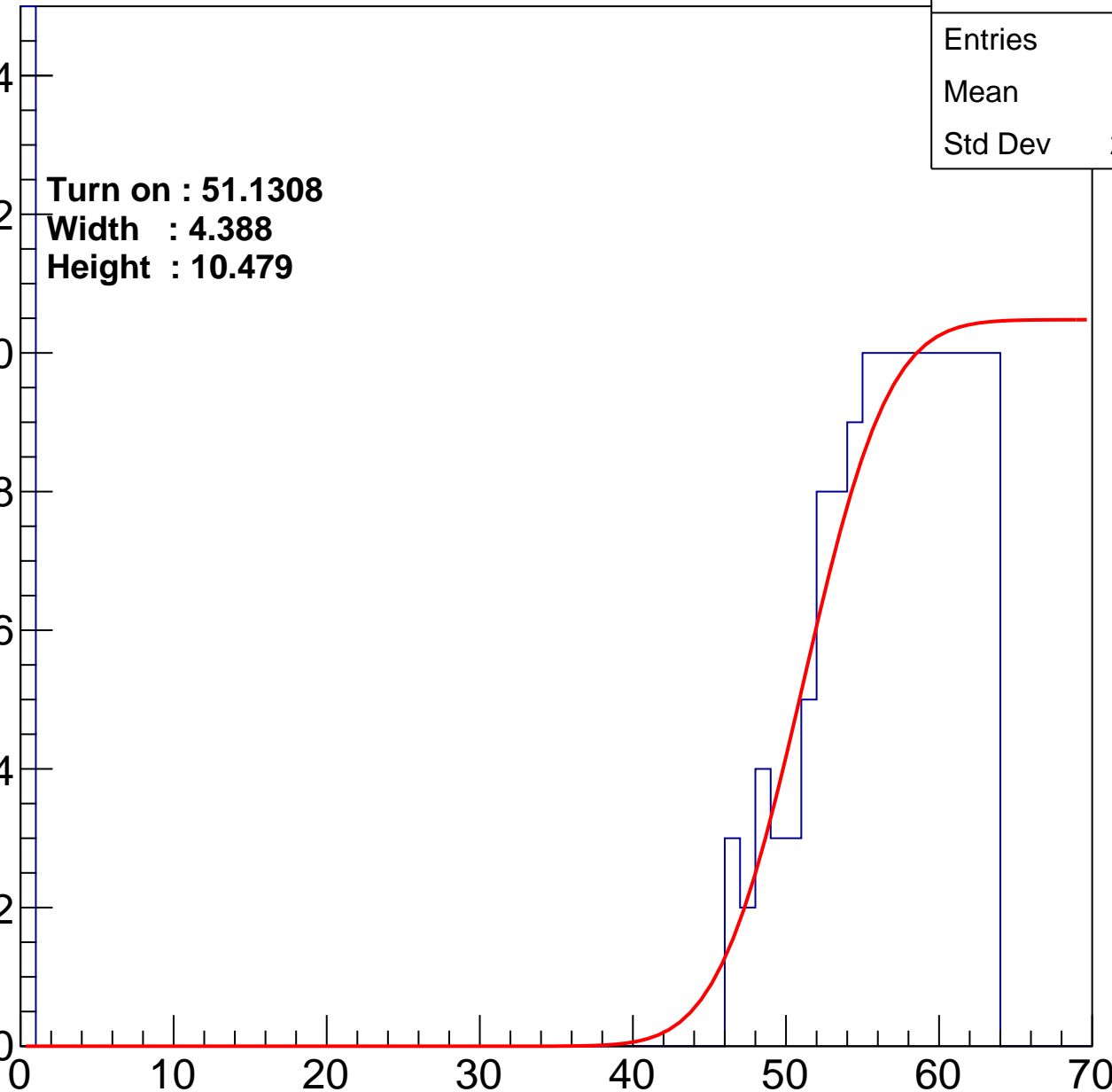
Width : 4.388

Height : 10.479

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	34.41
Std Dev	28.56

Turn on : 53.6743

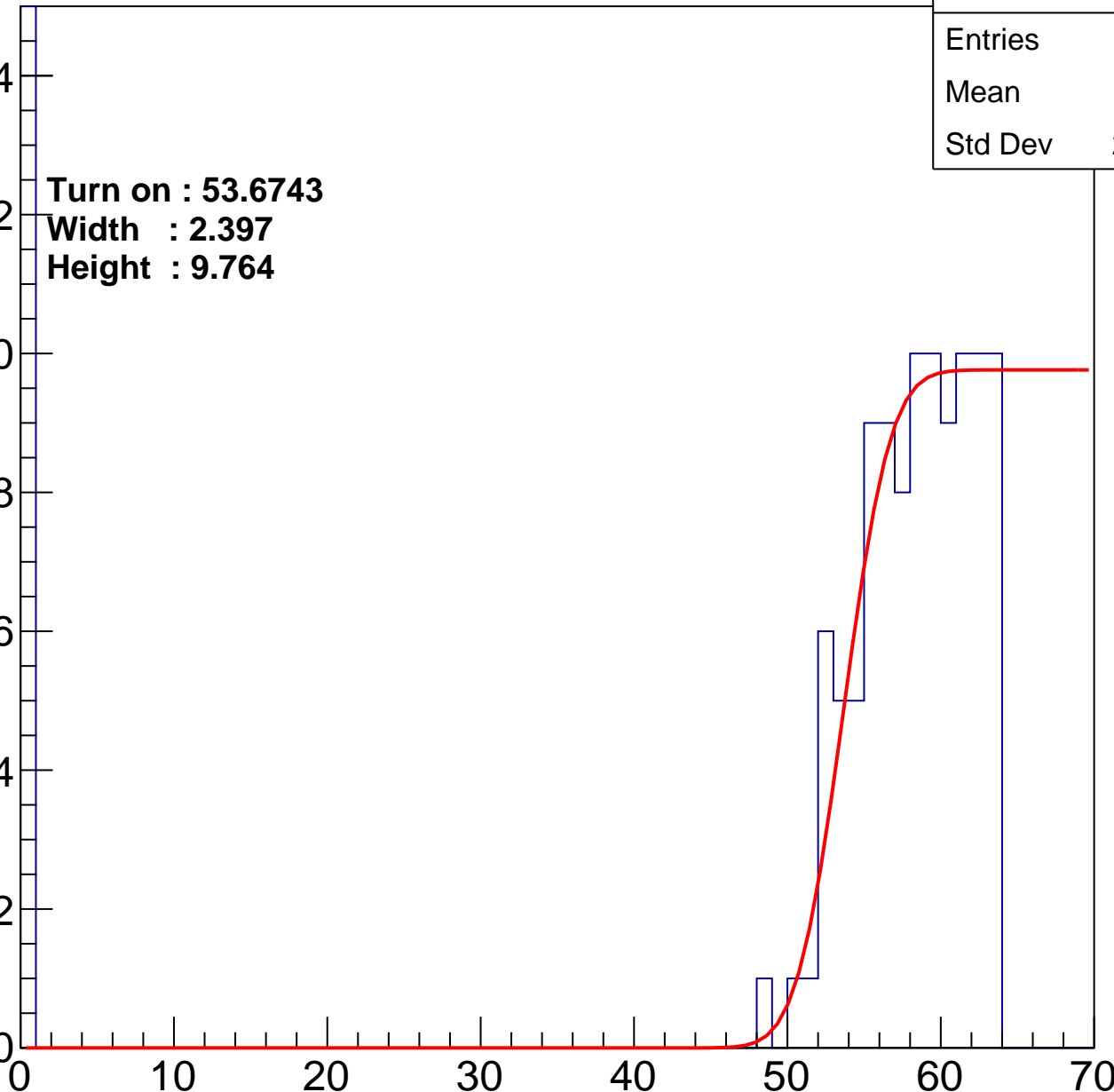
Width : 2.397

Height : 9.764

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	34.35
Std Dev	28.06

Turn on : 52.5772

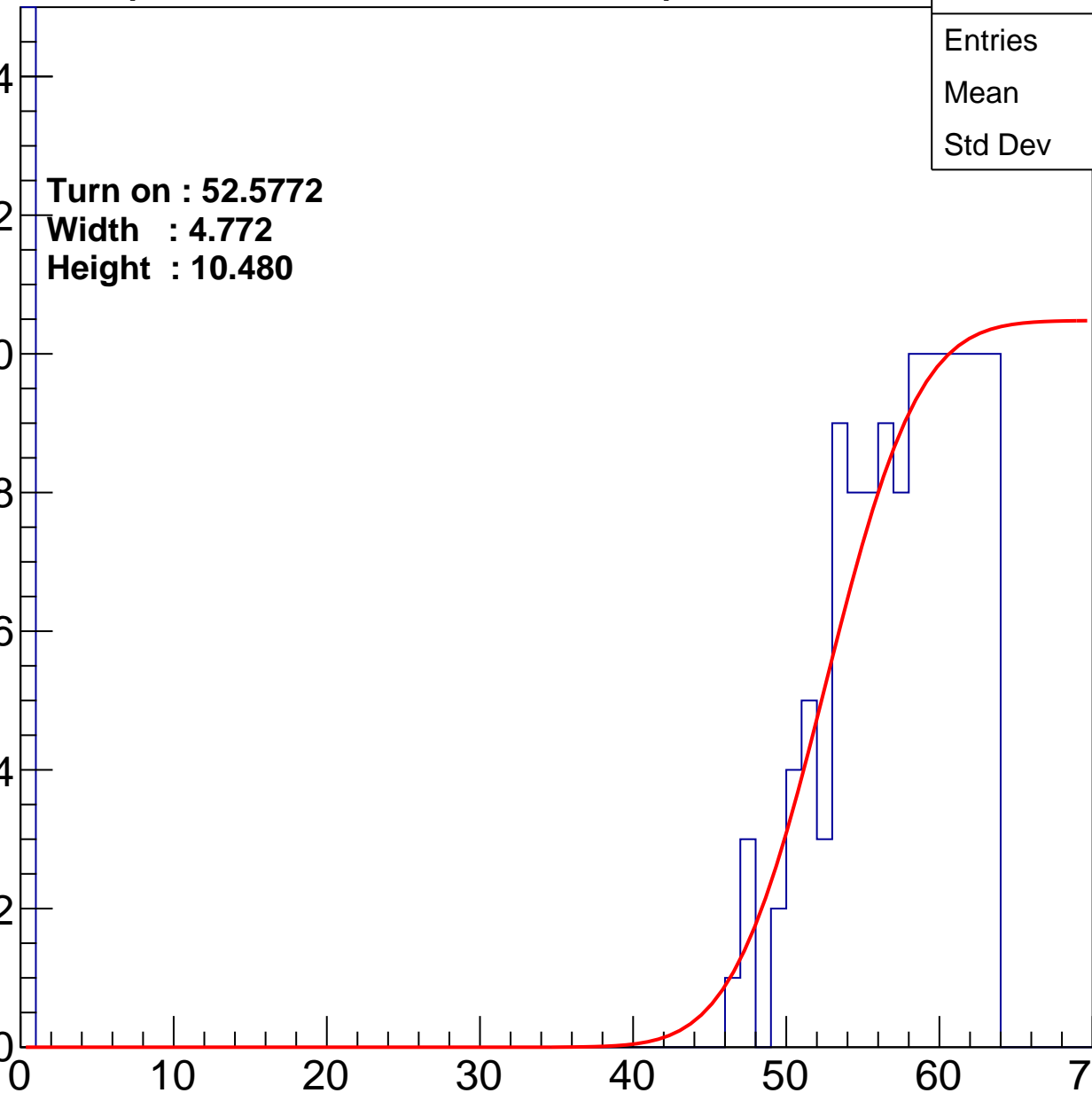
Width : 4.772

Height : 10.480

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.12
Std Dev	28.69

Turn on : 54.1182

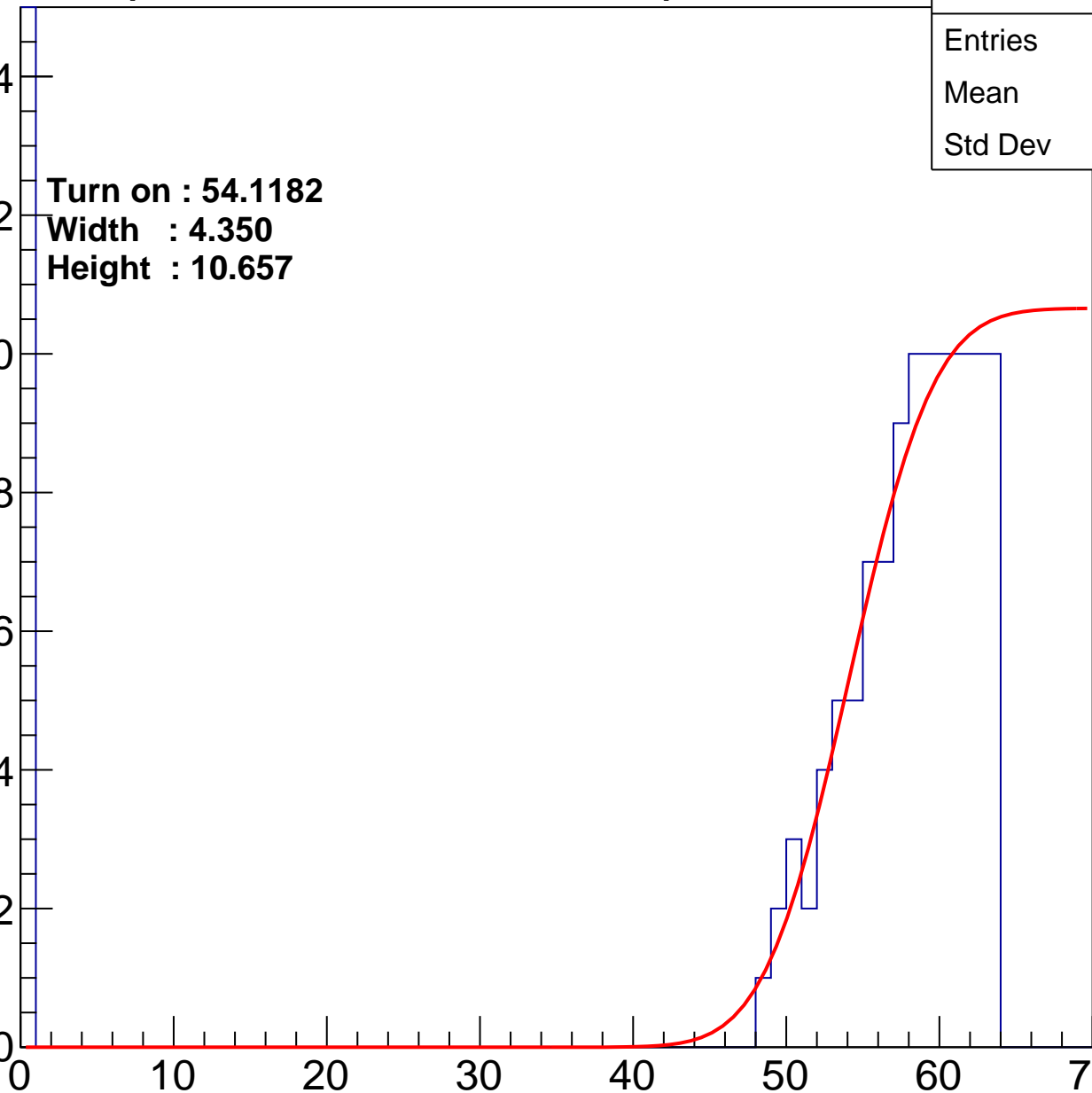
Width : 4.350

Height : 10.657

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	37.06
Std Dev	27.03

Turn on : 50.6637

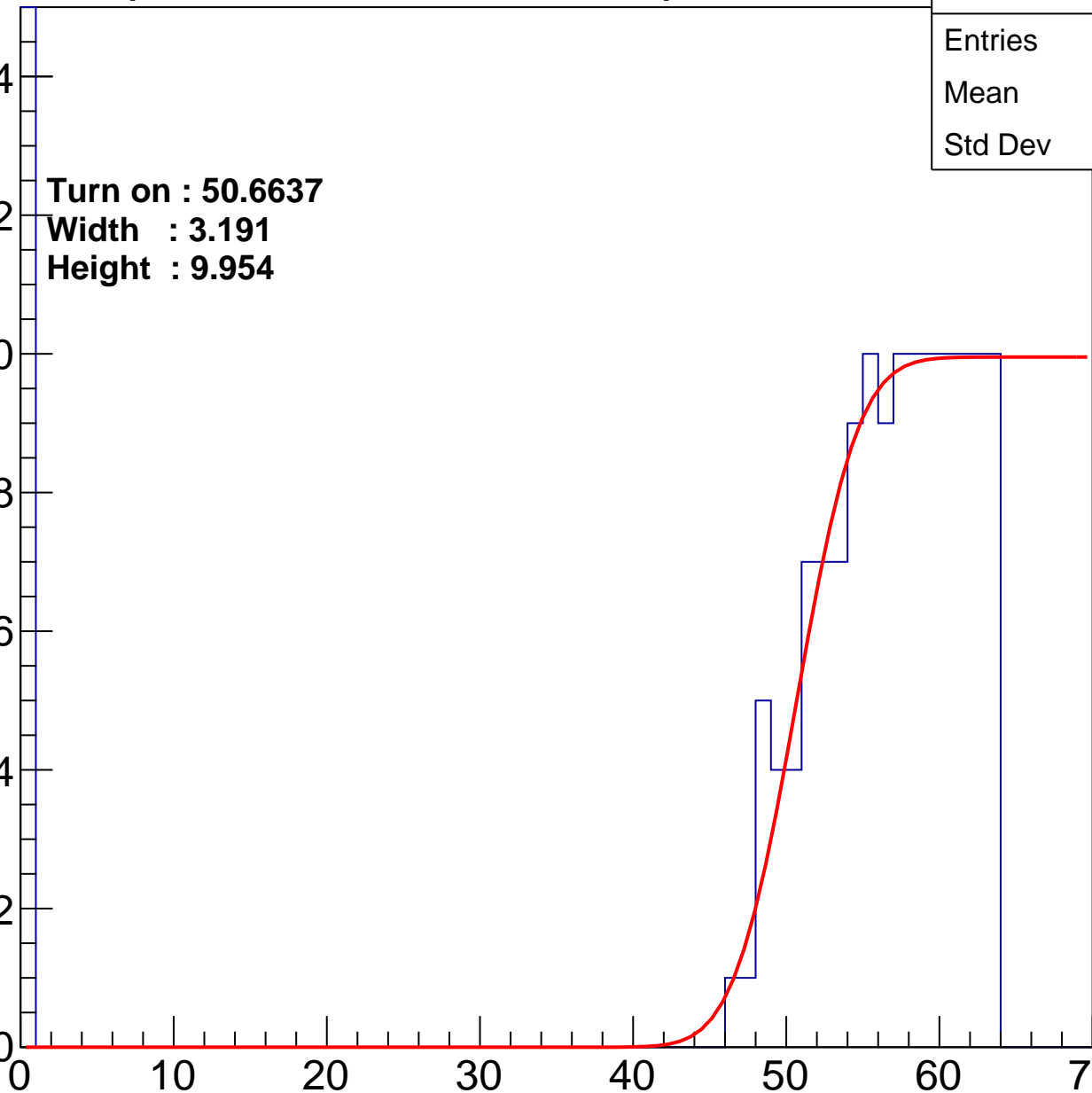
Width : 3.191

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch71

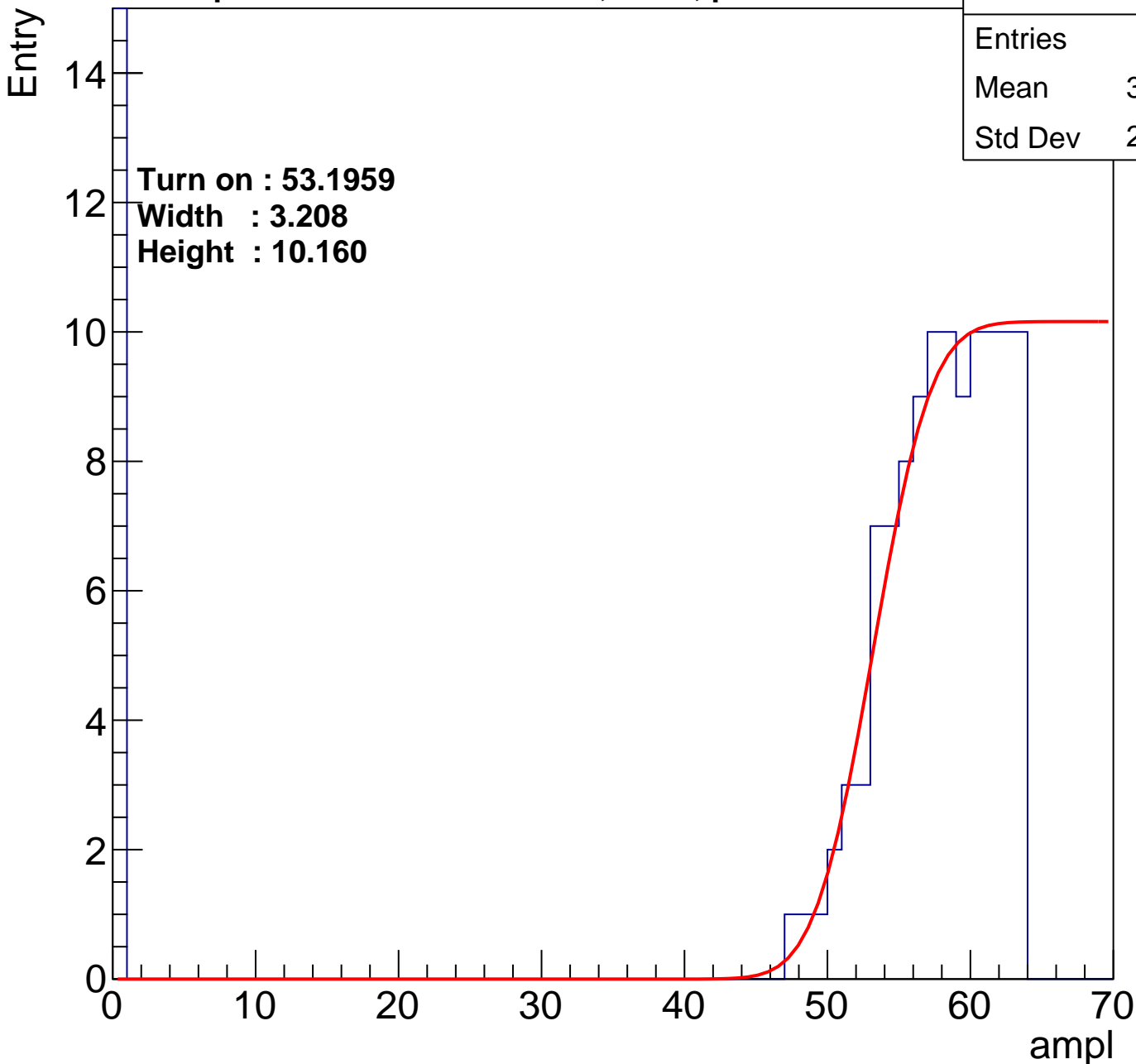
calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	37.35
Std Dev	27.63

Turn on : 53.1959

Width : 3.208

Height : 10.160



B1L104S, U14-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	35.02
Std Dev	28.09

Turn on : 53.2826

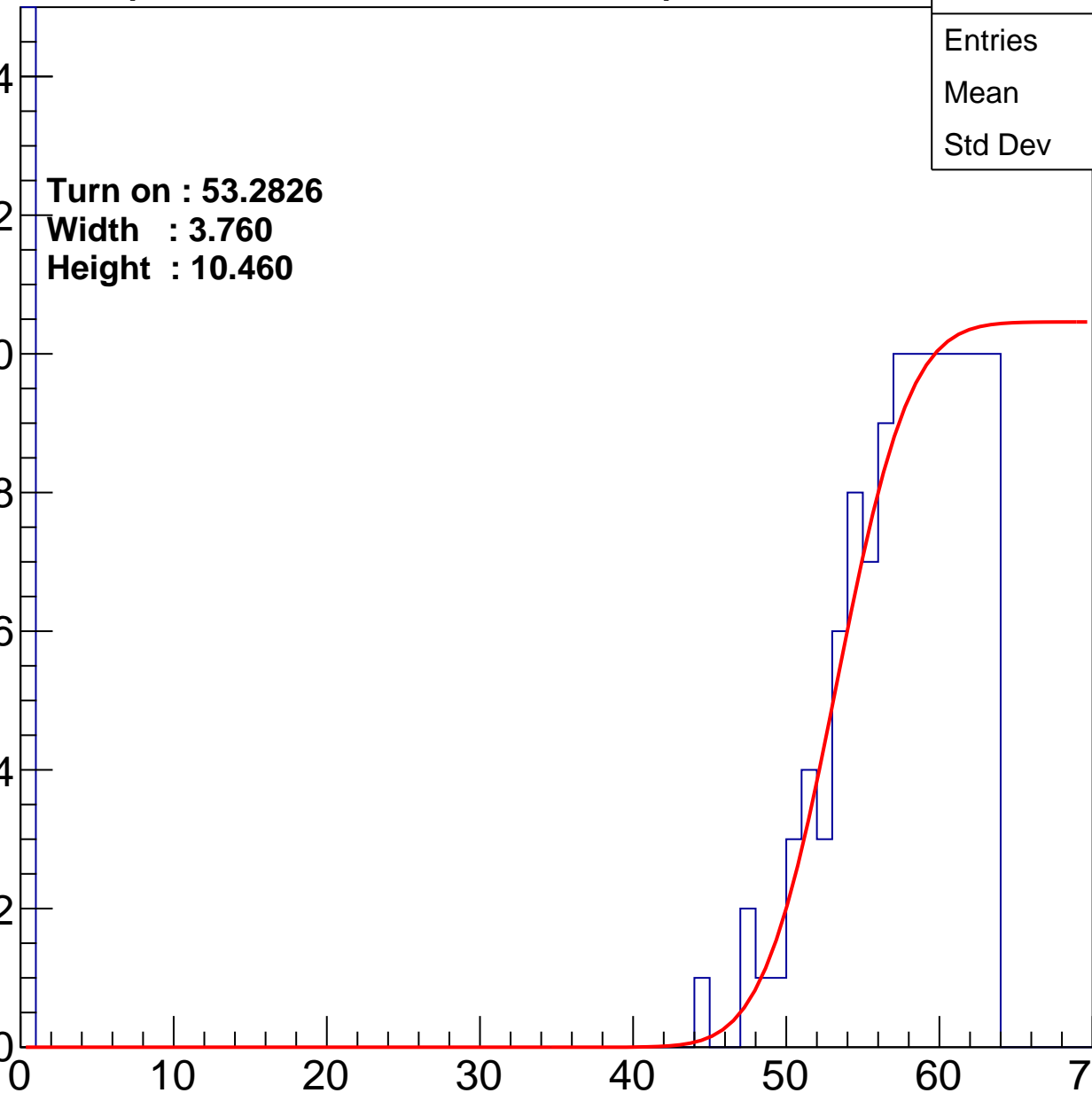
Width : 3.760

Height : 10.460

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	34.14
Std Dev	28.03

Turn on : 52.1042

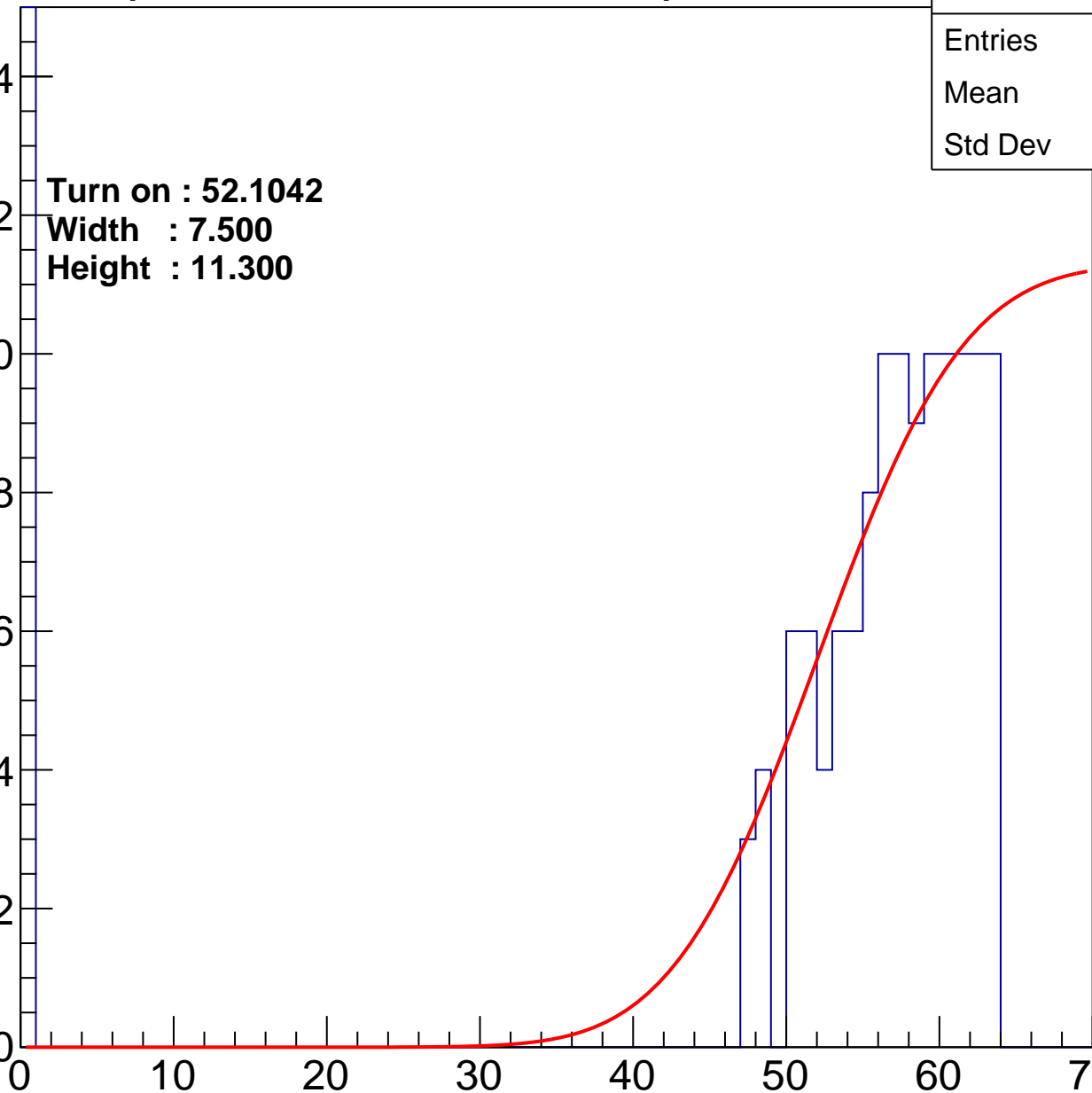
Width : 7.500

Height : 11.300

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	32.38
Std Dev	28.43

Turn on : 51.9828

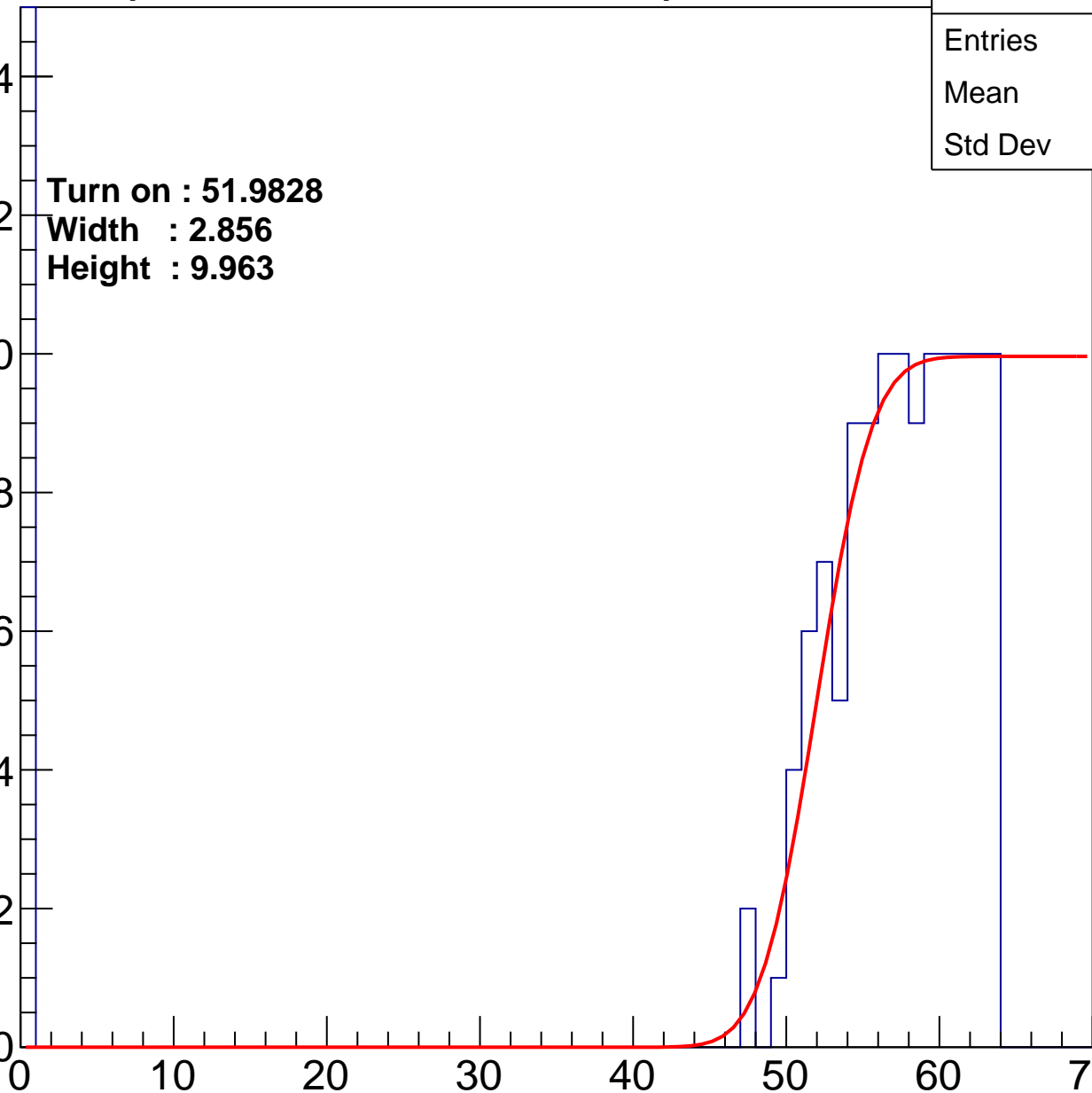
Width : 2.856

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	36.92
Std Dev	27.78

Turn on : 53.2496

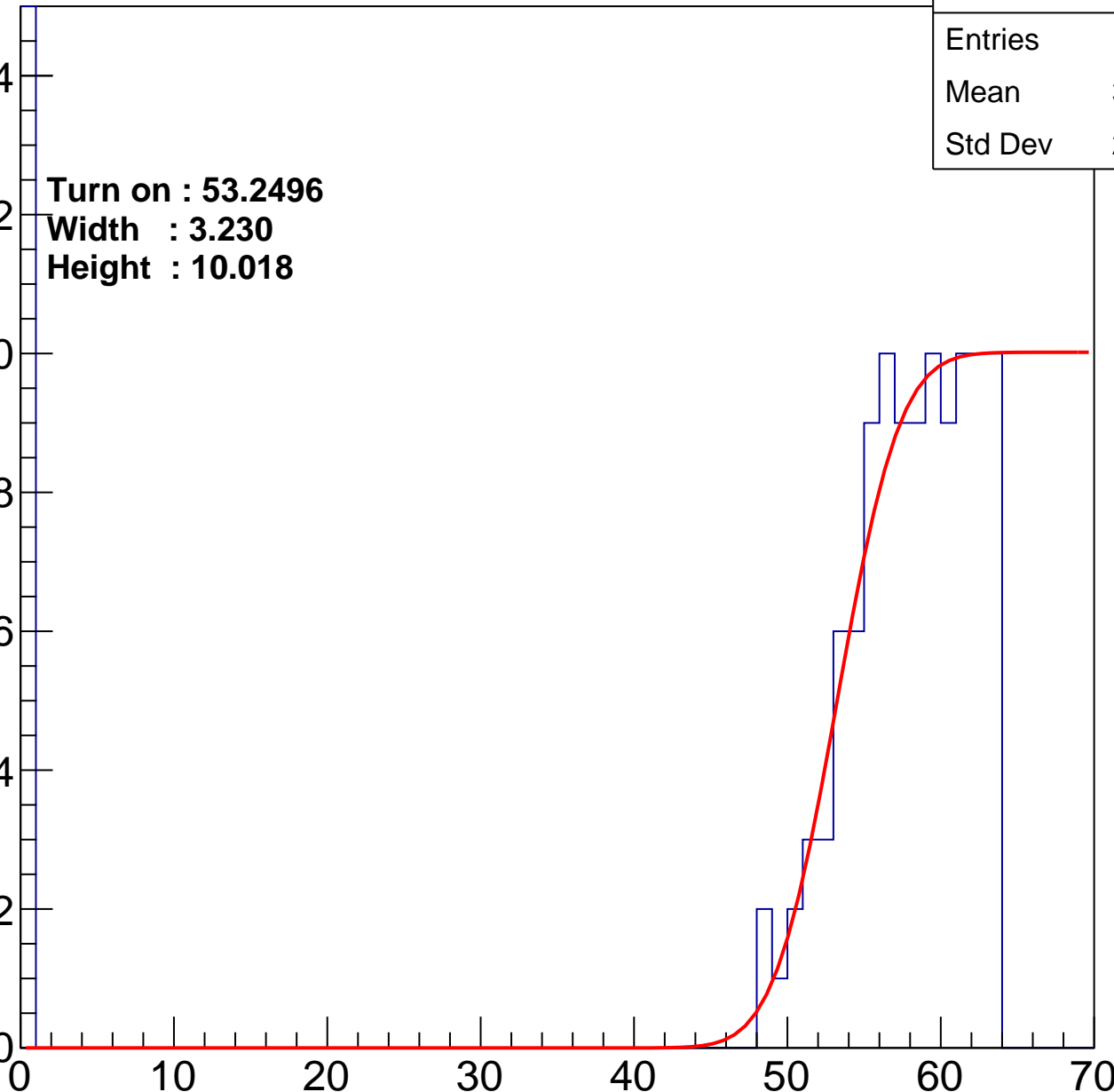
Width : 3.230

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	36.81
Std Dev	27.14

Turn on : 51.3350

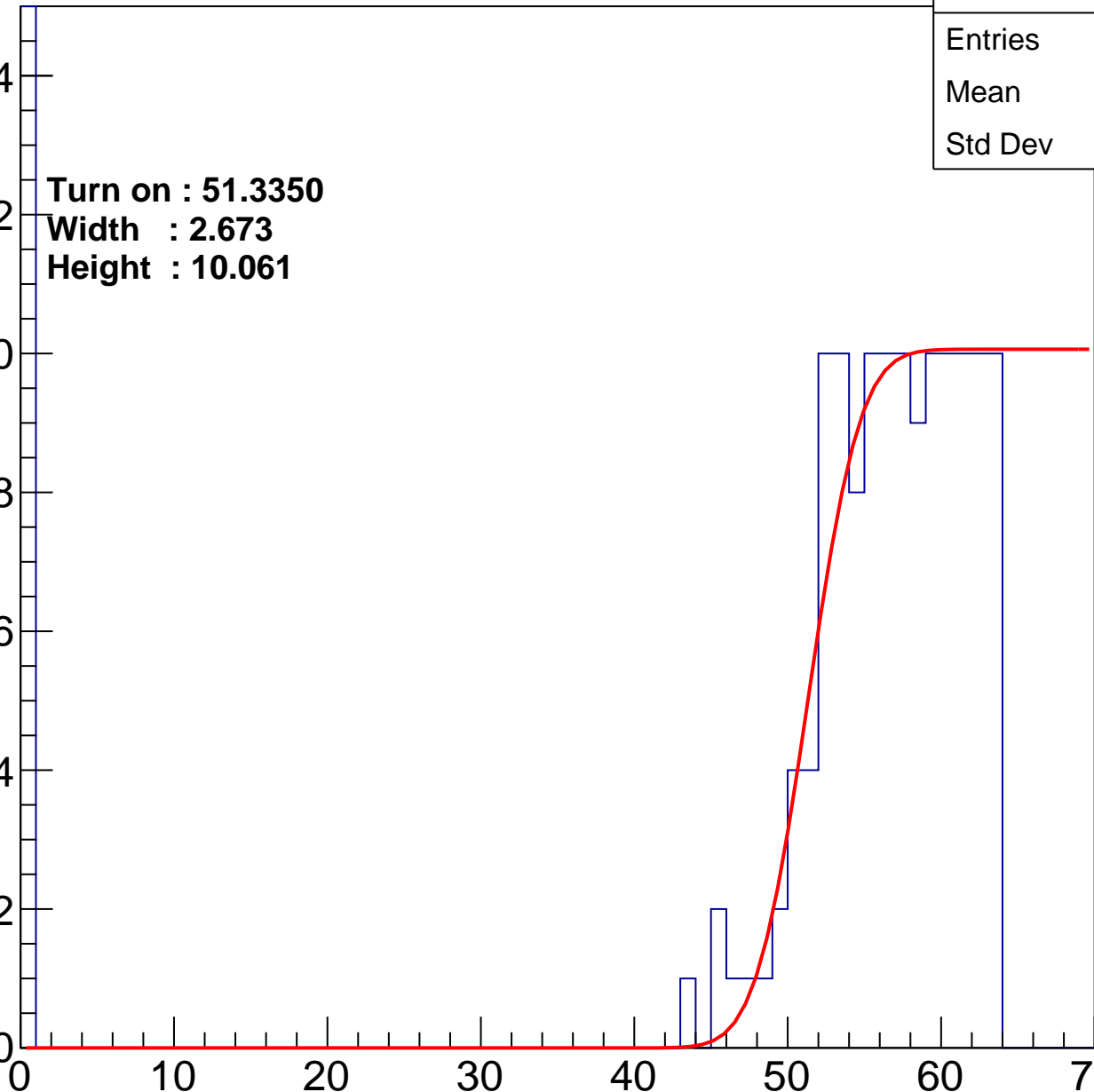
Width : 2.673

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	30.95
Std Dev	28.94

Turn on : 53.8531

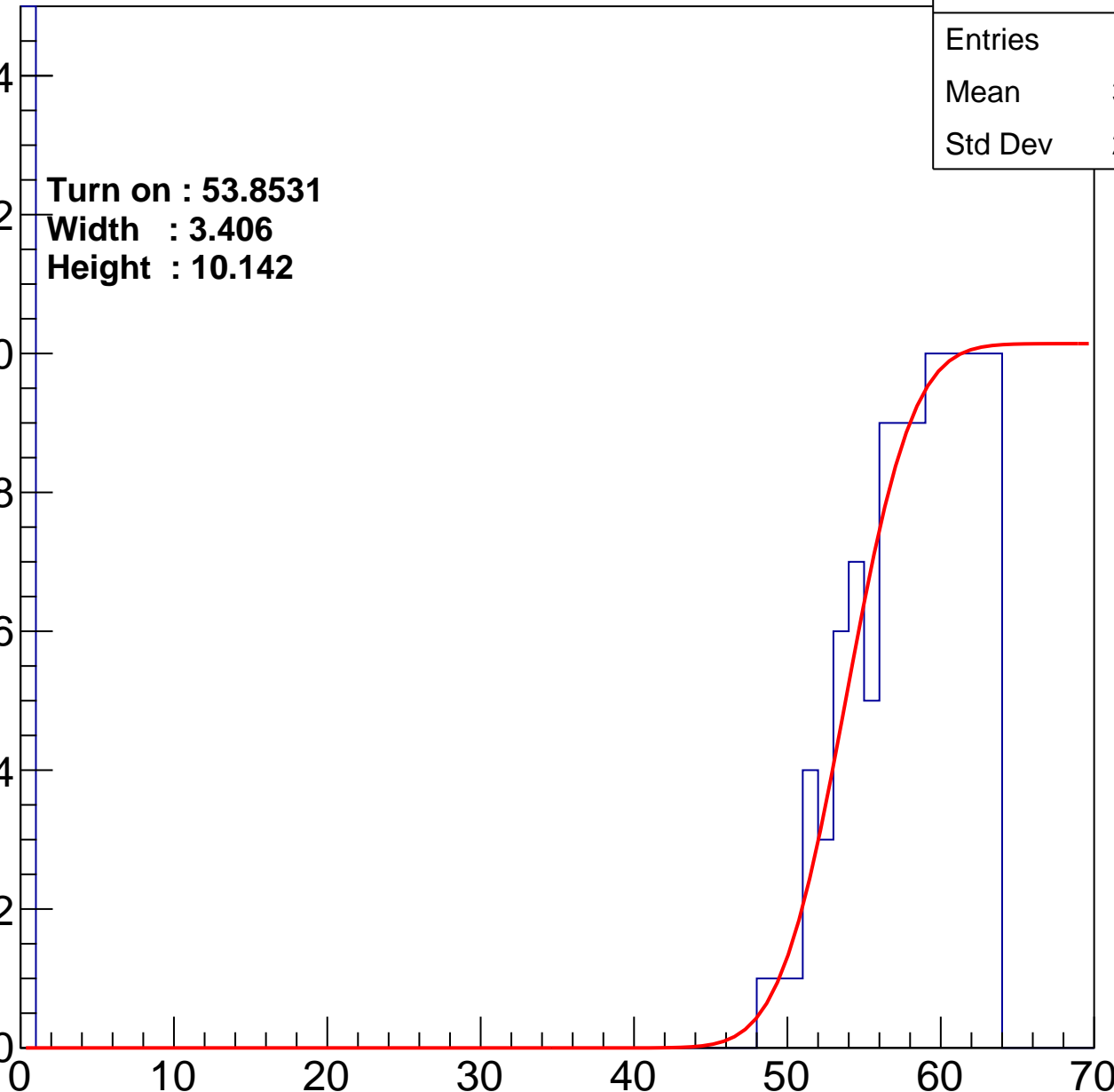
Width : 3.406

Height : 10.142

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	40.9
Std Dev	25.85

Turn on : 52.7688

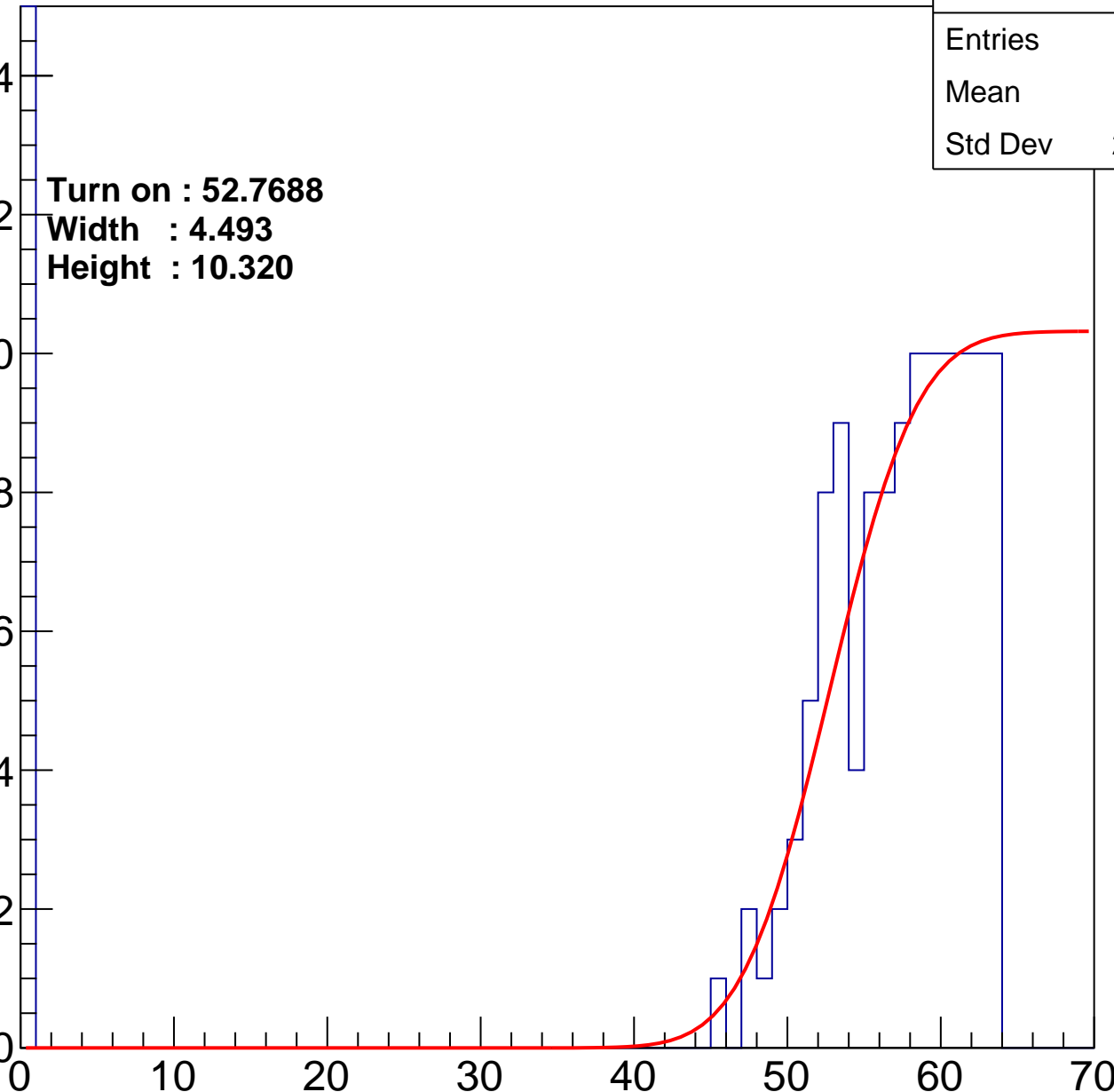
Width : 4.493

Height : 10.320

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch79

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	36.63
Std Dev	27.56

Turn on : 52.0684

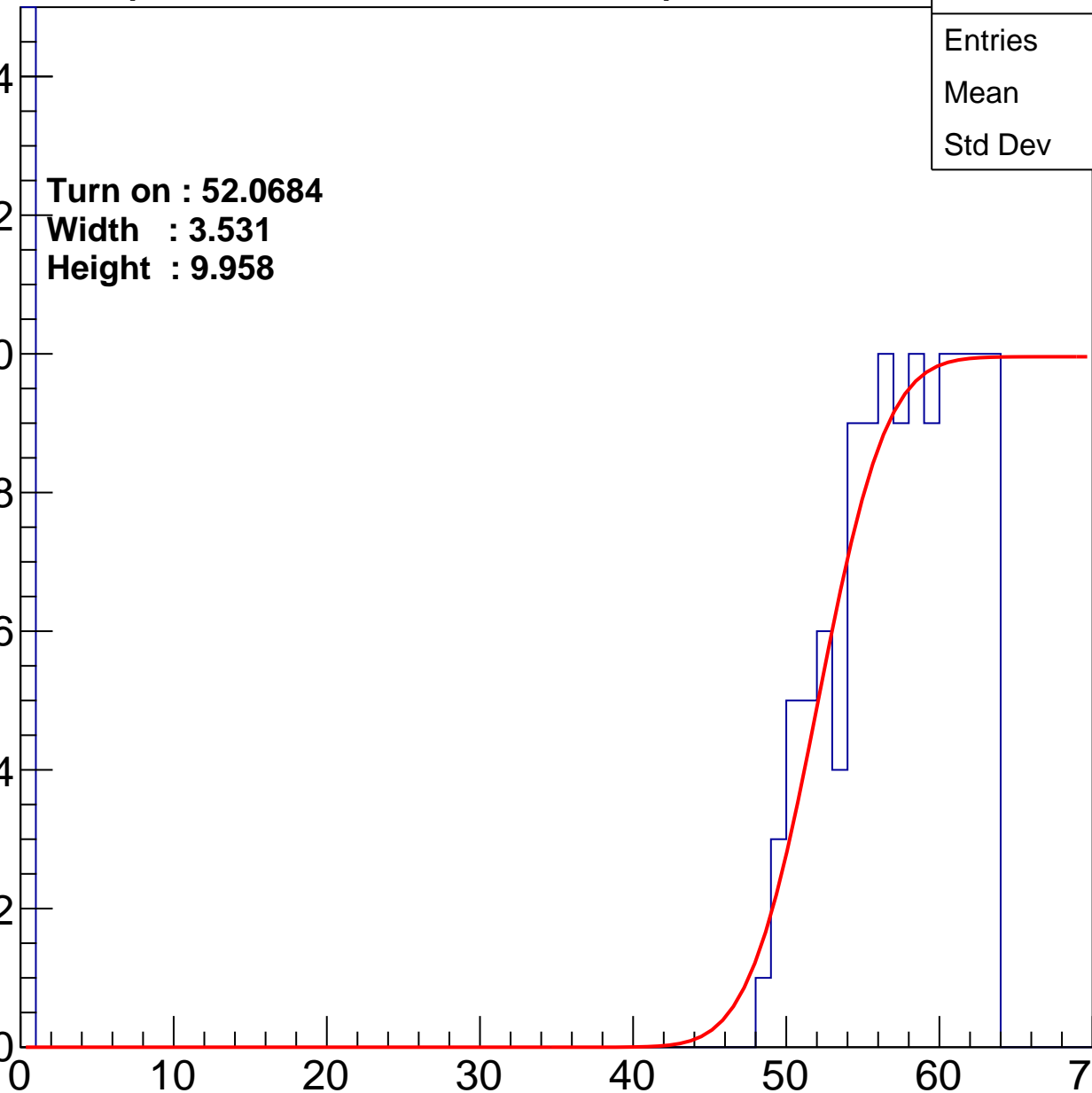
Width : 3.531

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	34.97
Std Dev	28.14

Turn on : 53.0674

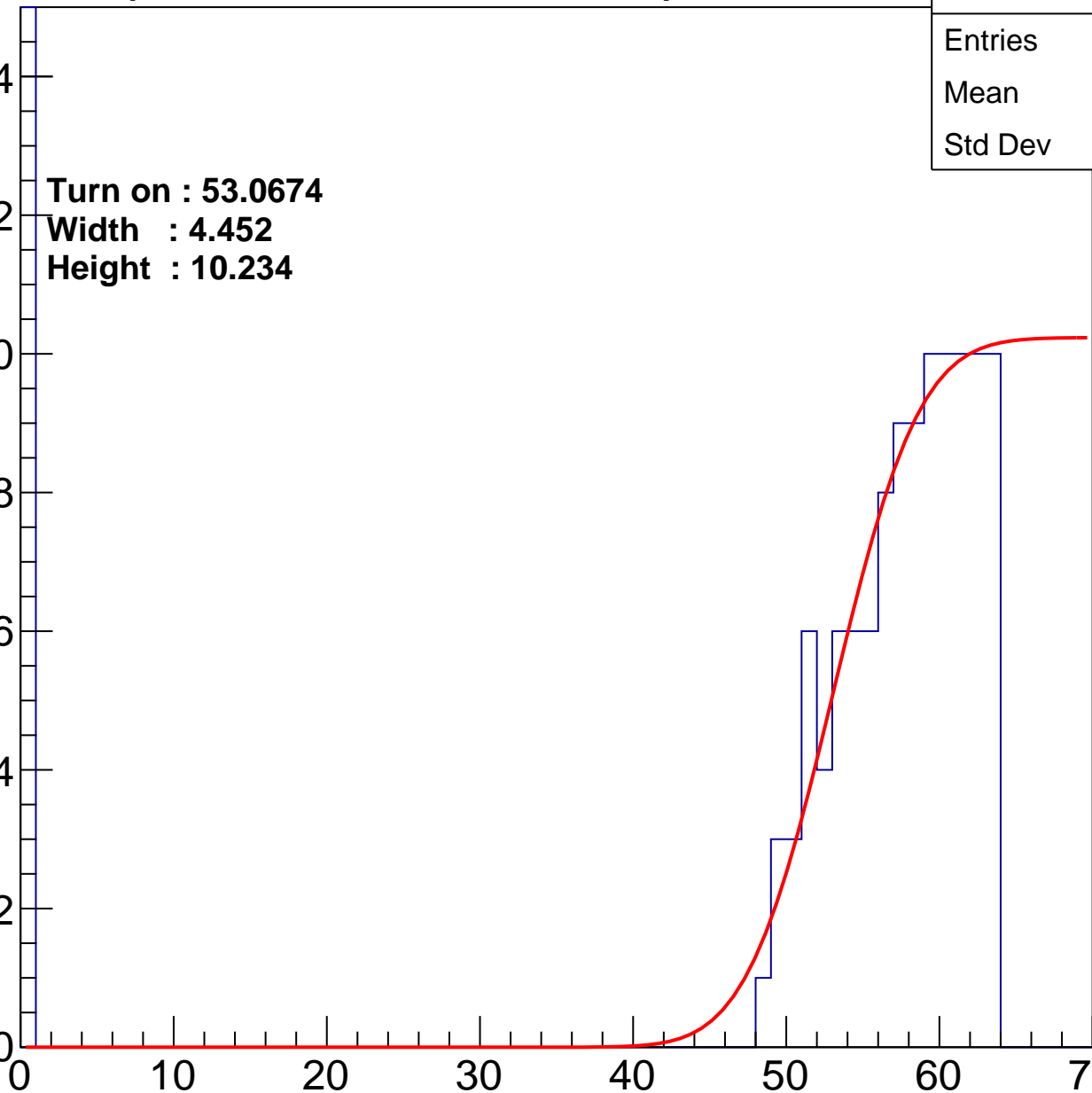
Width : 4.452

Height : 10.234

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch81

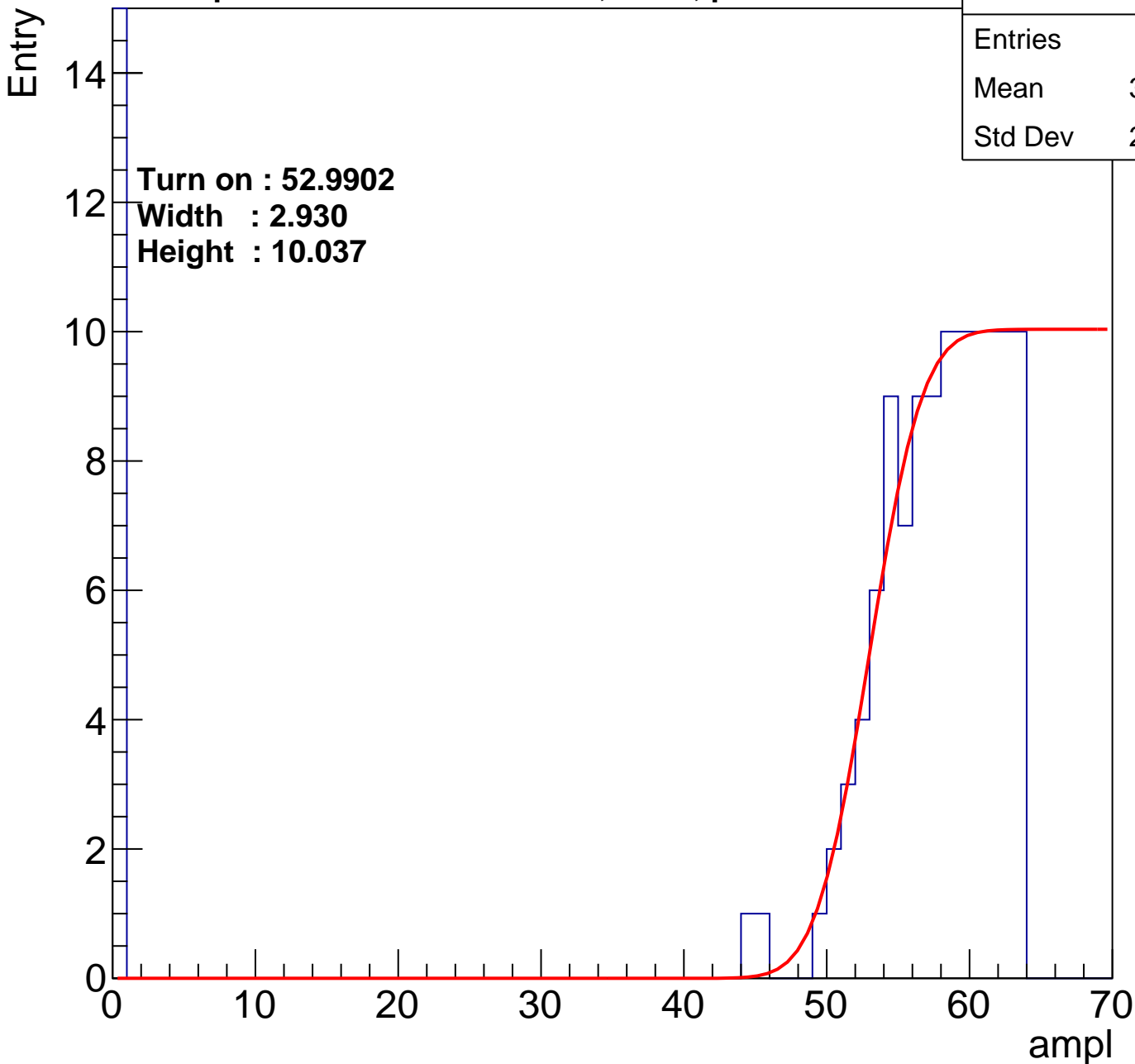
calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	36.77
Std Dev	27.76

Turn on : 52.9902

Width : 2.930

Height : 10.037



B1L104S, U14-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	218
Mean	35.58
Std Dev	27.33

Turn on : 50.3094

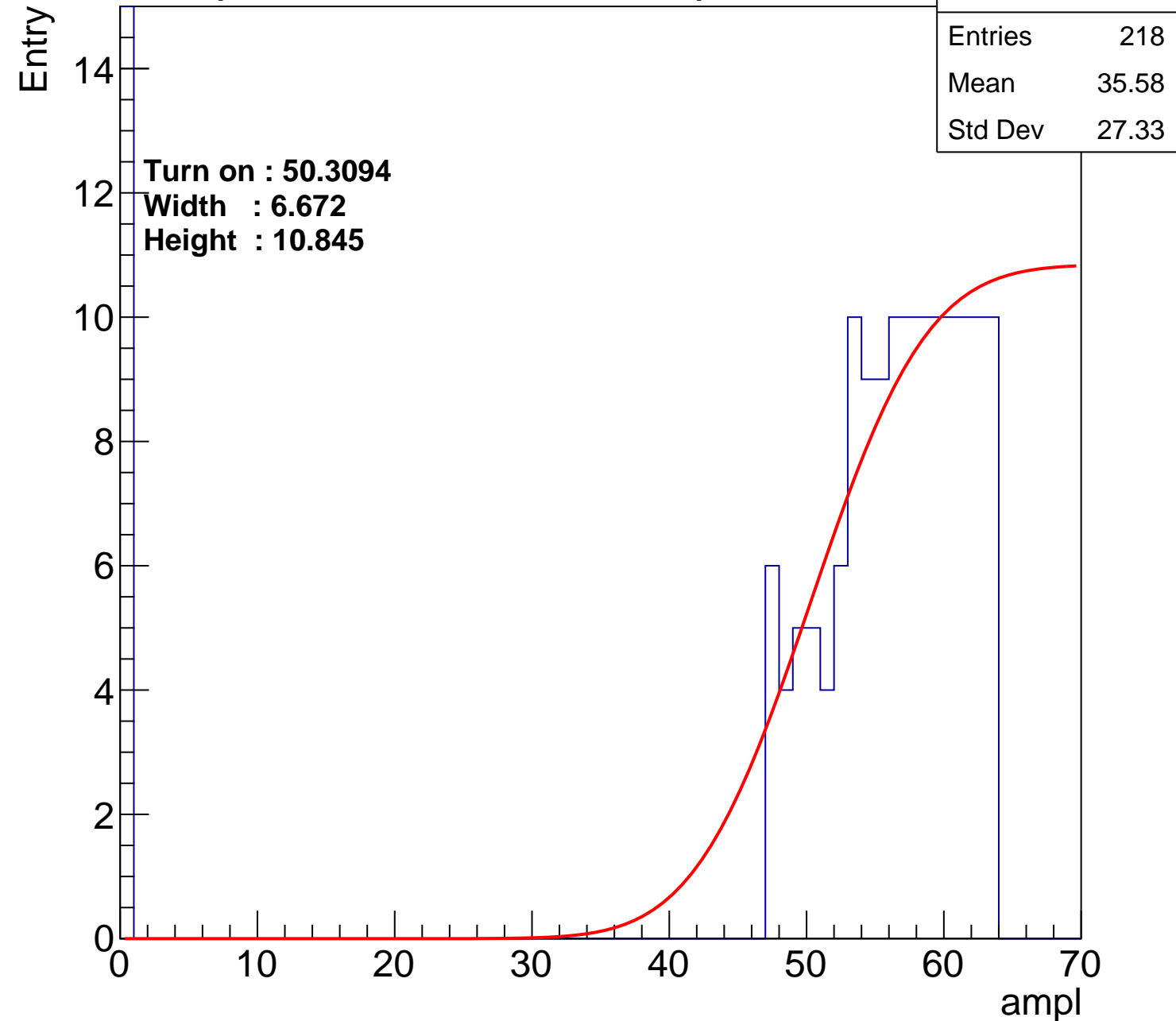
Width : 6.672

Height : 10.845

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	33.68
Std Dev	28.35

Turn on : 52.0378

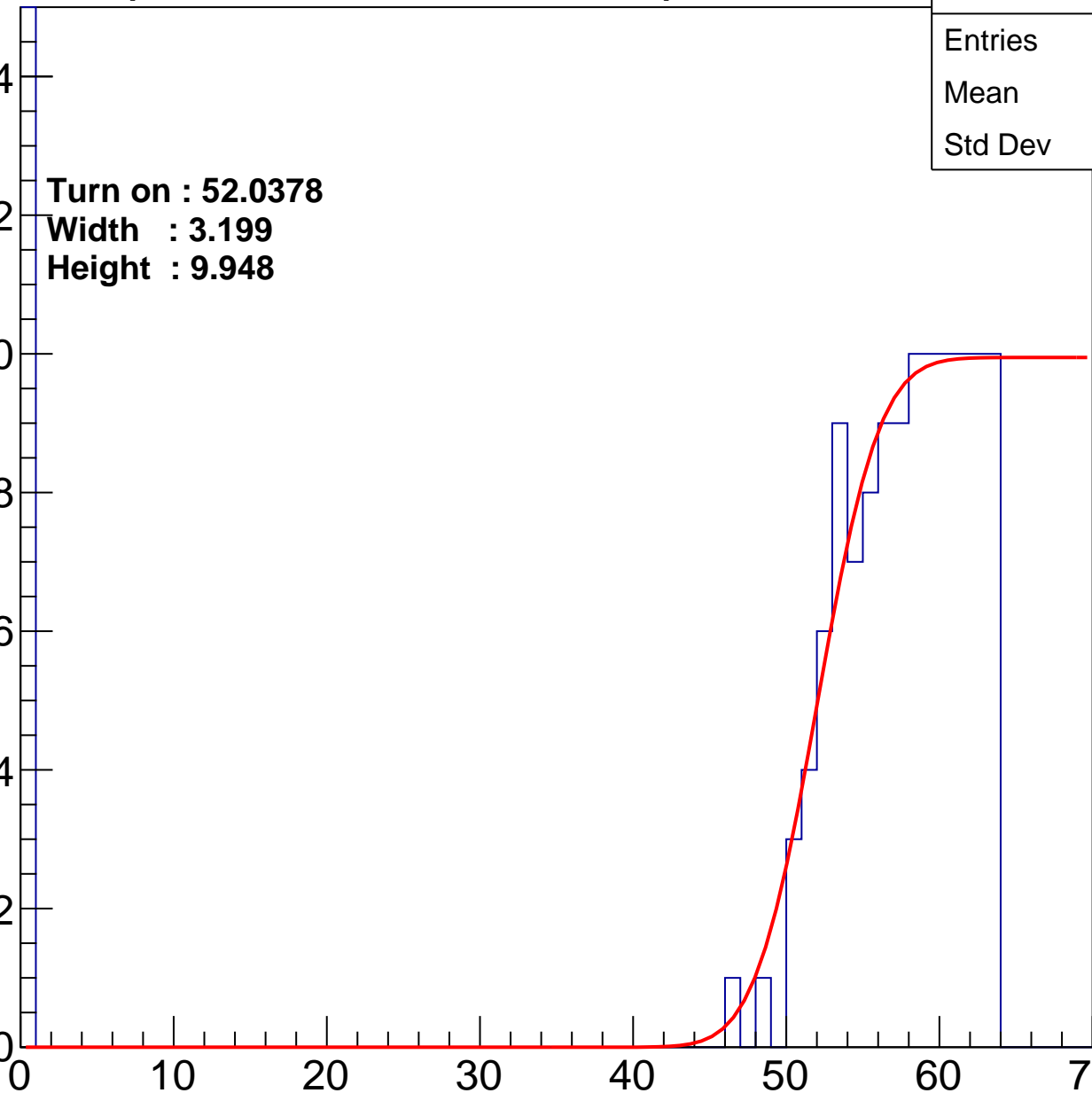
Width : 3.199

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	39.47
Std Dev	26.14

Turn on : 51.5428

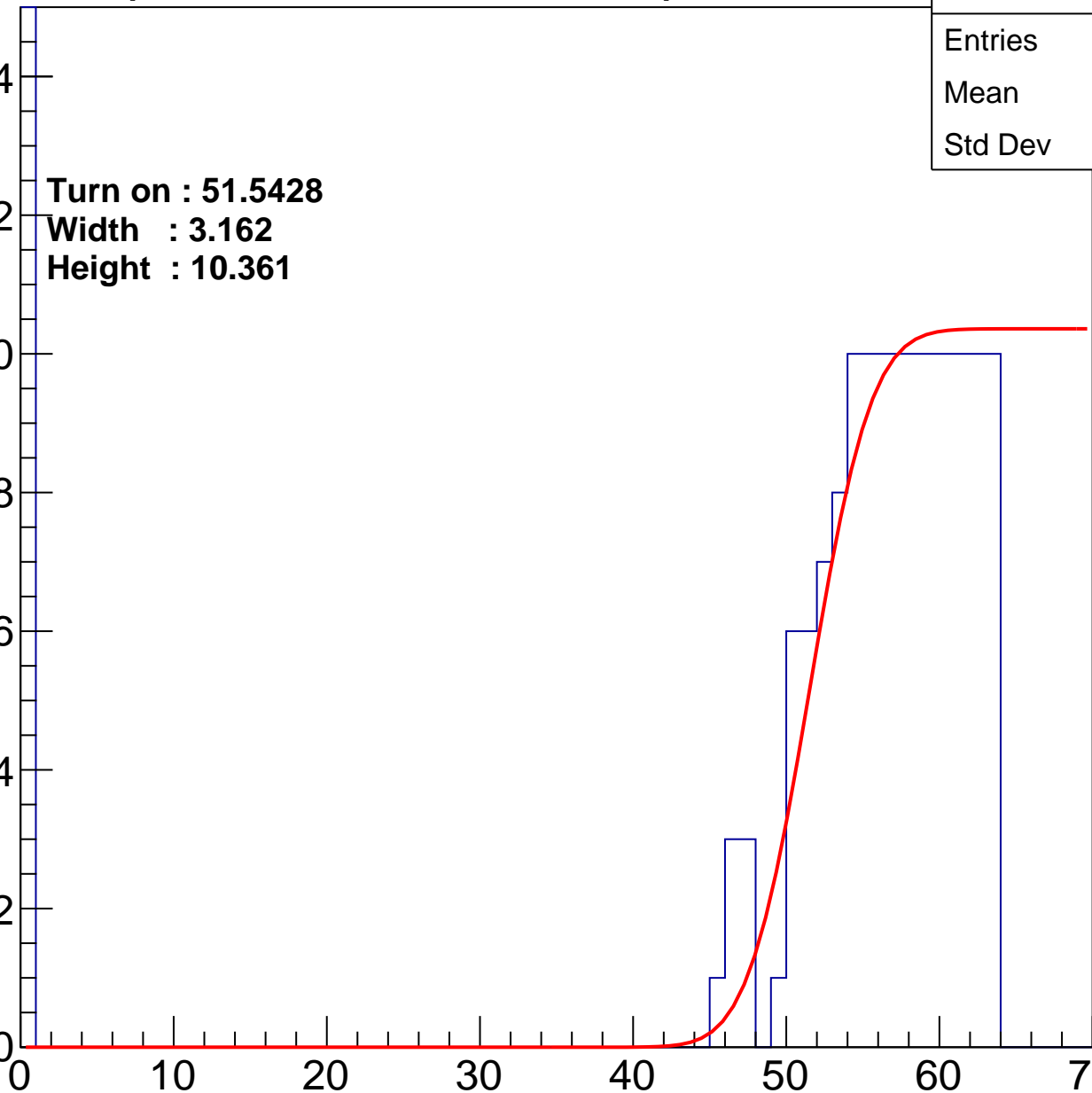
Width : 3.162

Height : 10.361

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	35.21
Std Dev	27.91

Turn on : 52.7452

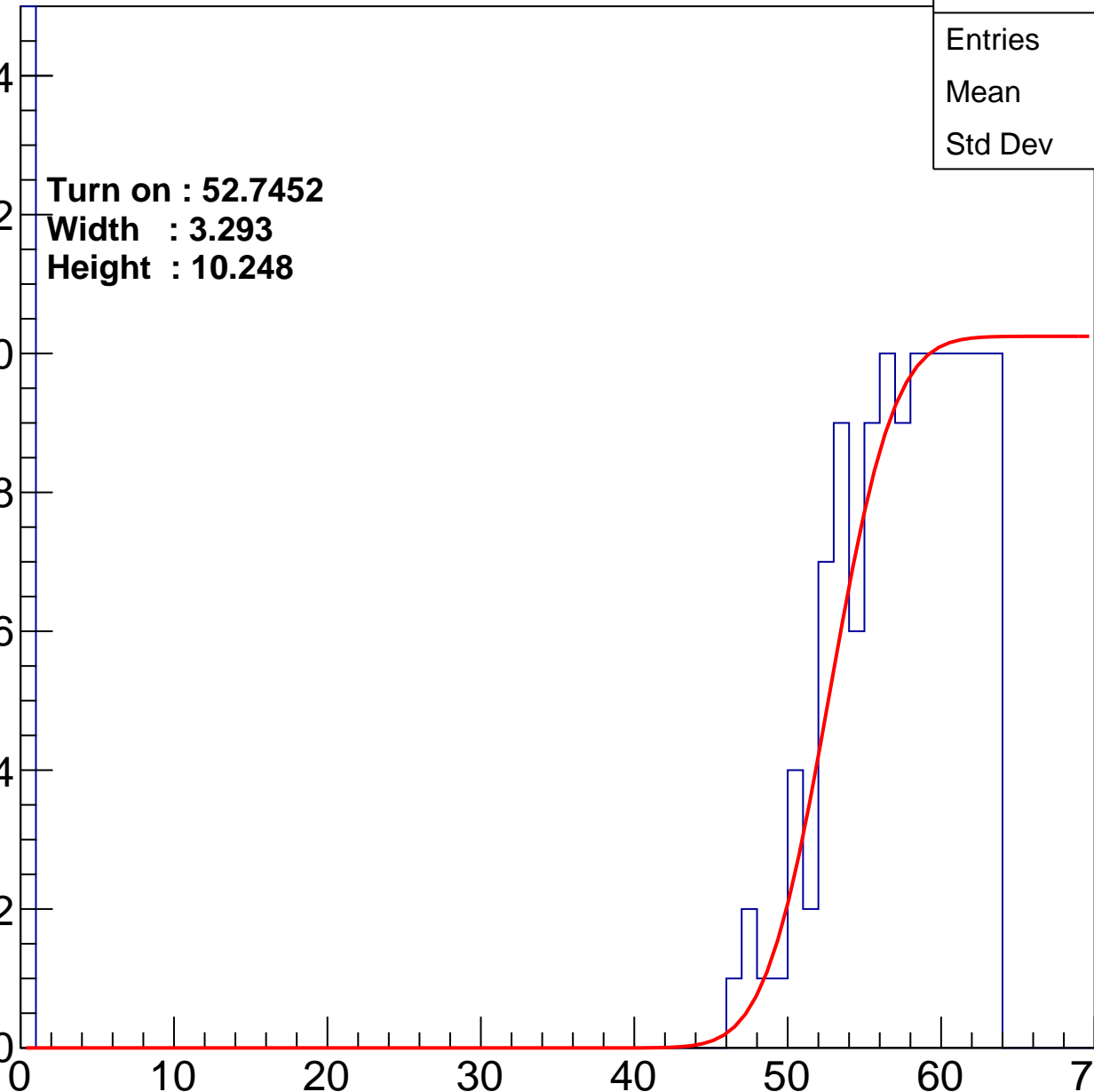
Width : 3.293

Height : 10.248

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch86

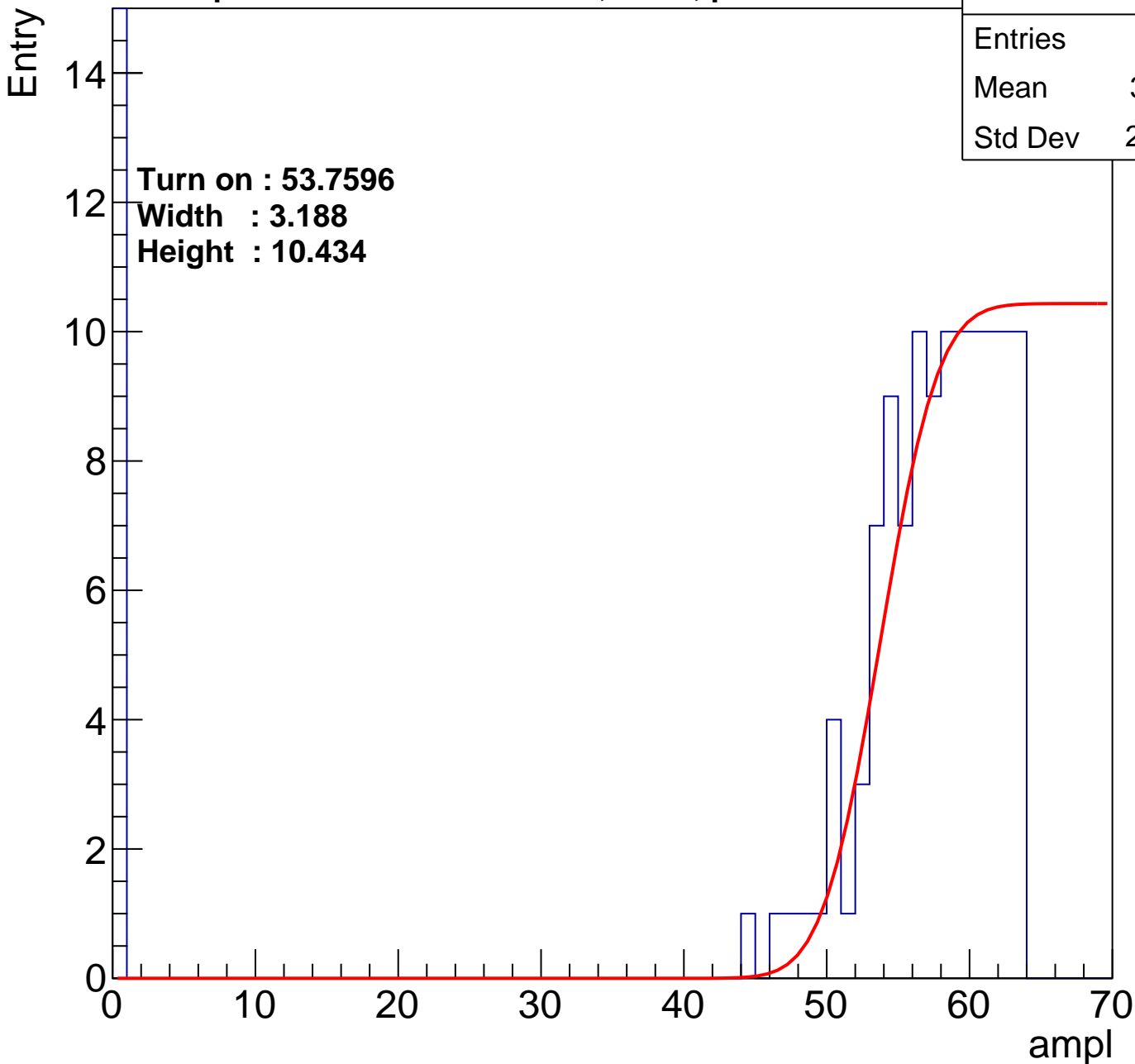
calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	38.51
Std Dev	27.09

Turn on : 53.7596

Width : 3.188

Height : 10.434



B1L104S, U14-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	34.72
Std Dev	28.43

Turn on : 53.8183

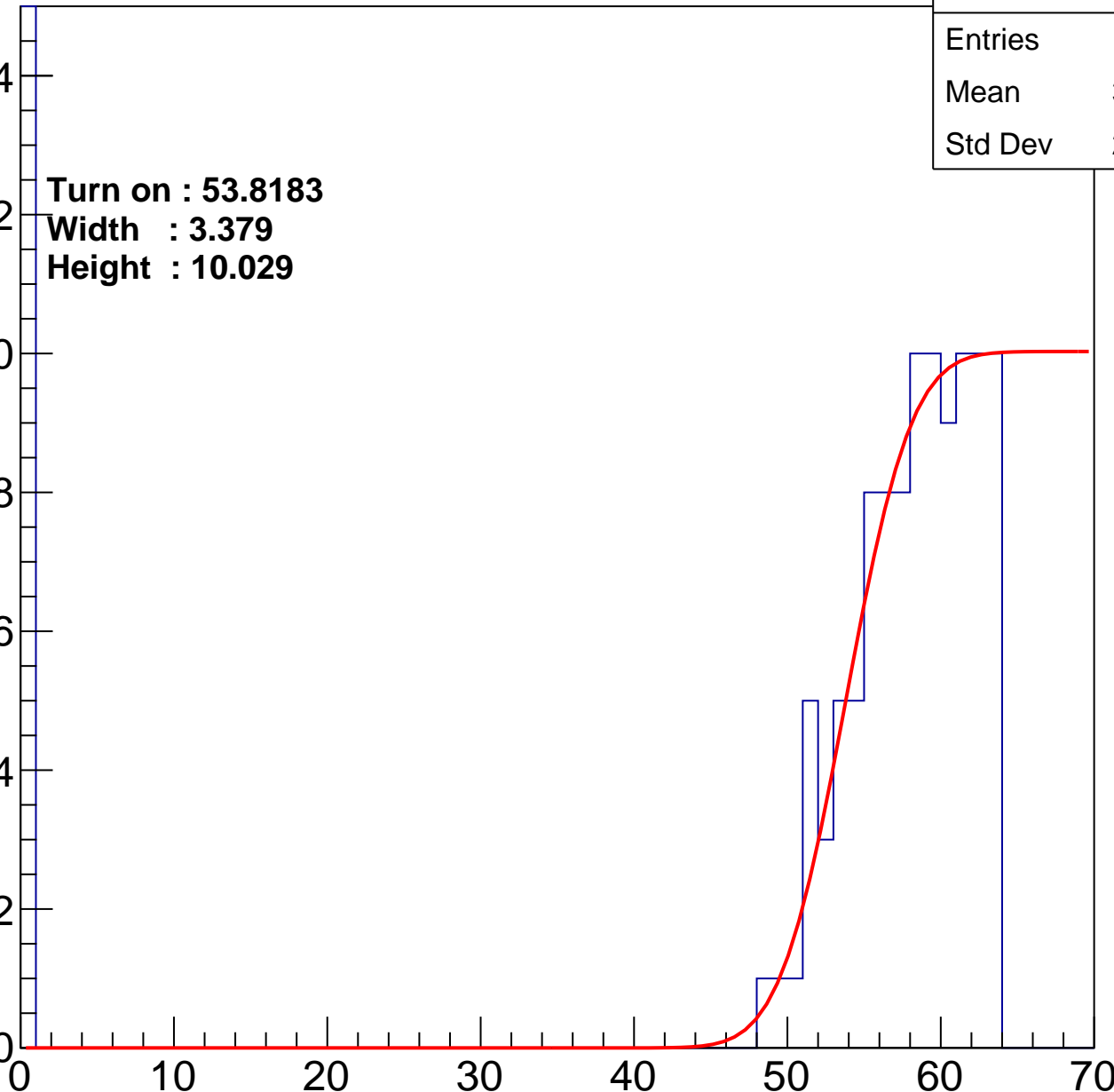
Width : 3.379

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	232
Mean	28.85
Std Dev	28.74

Turn on : 51.9542

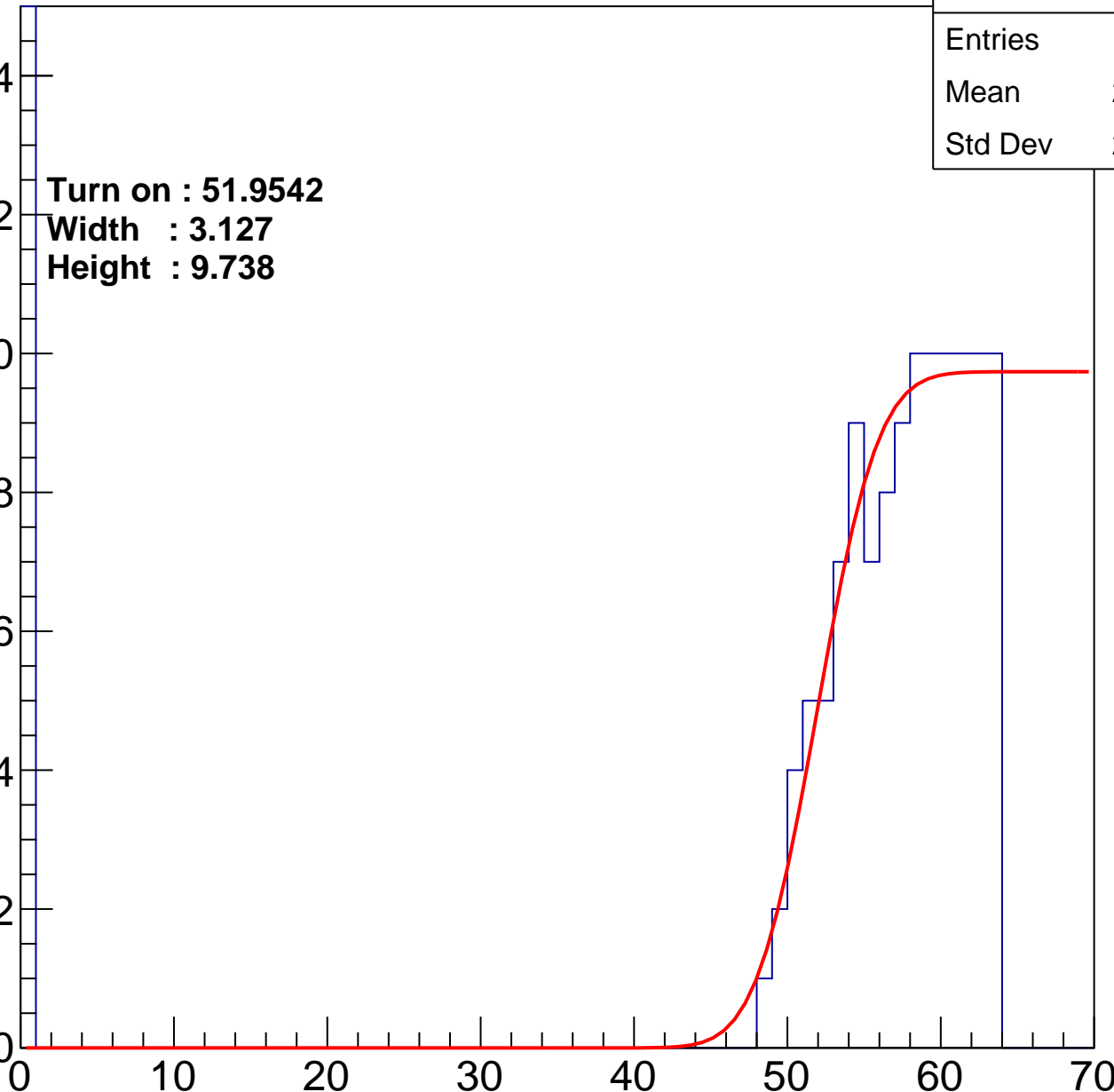
Width : 3.127

Height : 9.738

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	34.89
Std Dev	27.97

Turn on : 52.3425

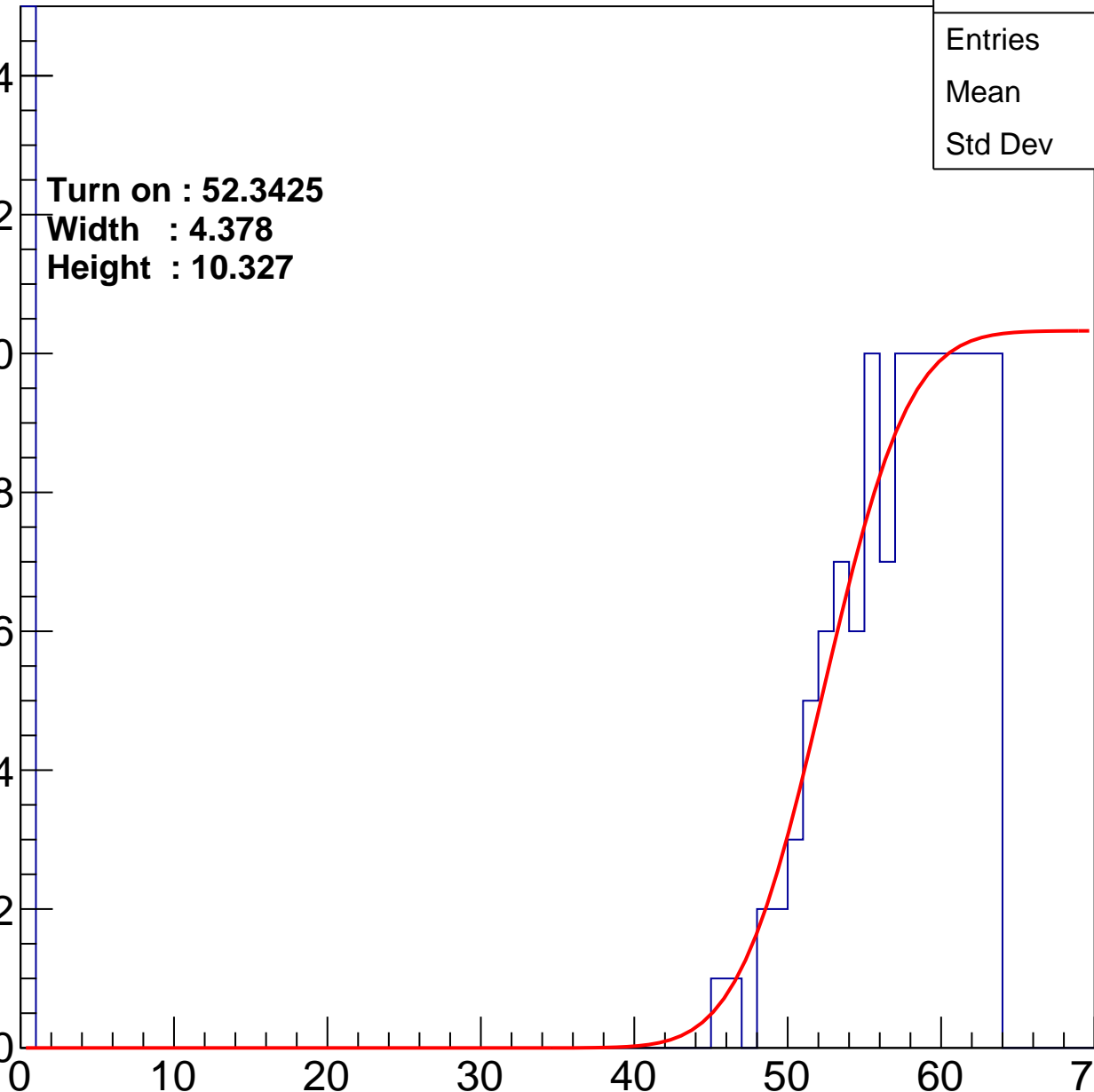
Width : 4.378

Height : 10.327

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	231
Mean	30.52
Std Dev	28.51

Turn on : 51.5807

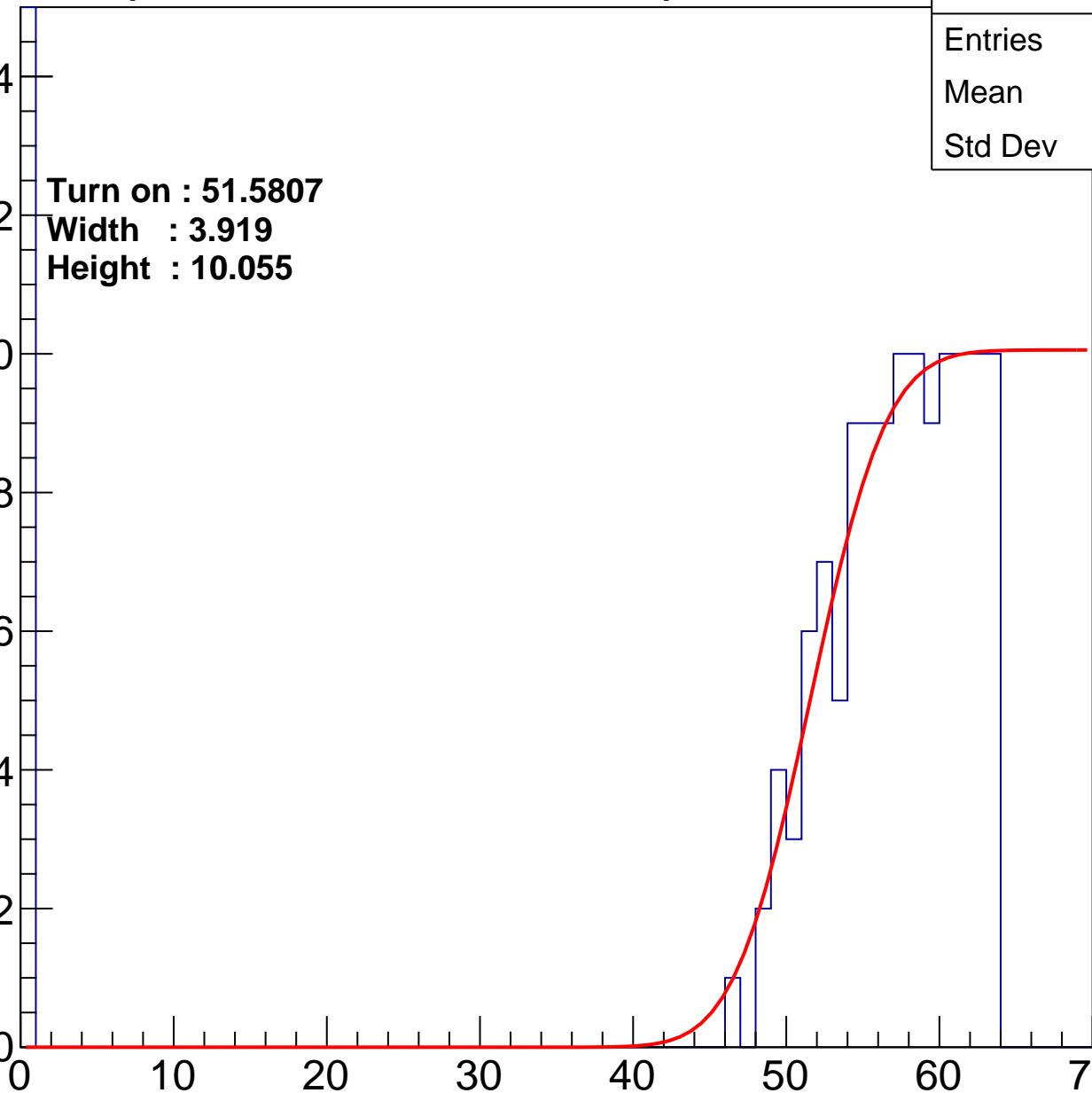
Width : 3.919

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch91

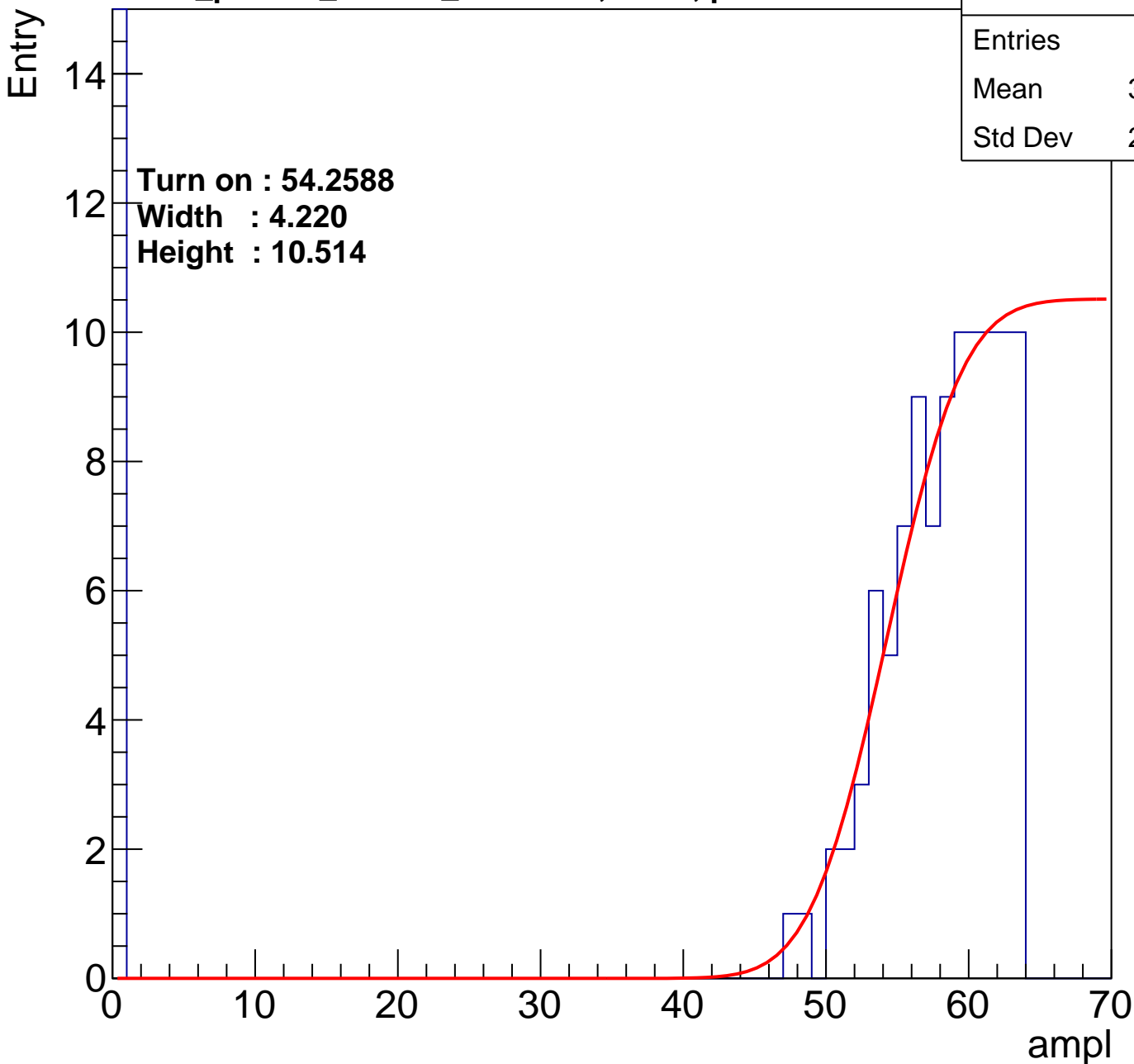
calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	31.06
Std Dev	28.98

Turn on : 54.2588

Width : 4.220

Height : 10.514



B1L104S, U14-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	33.38
Std Dev	28.3

Turn on : 52.3585

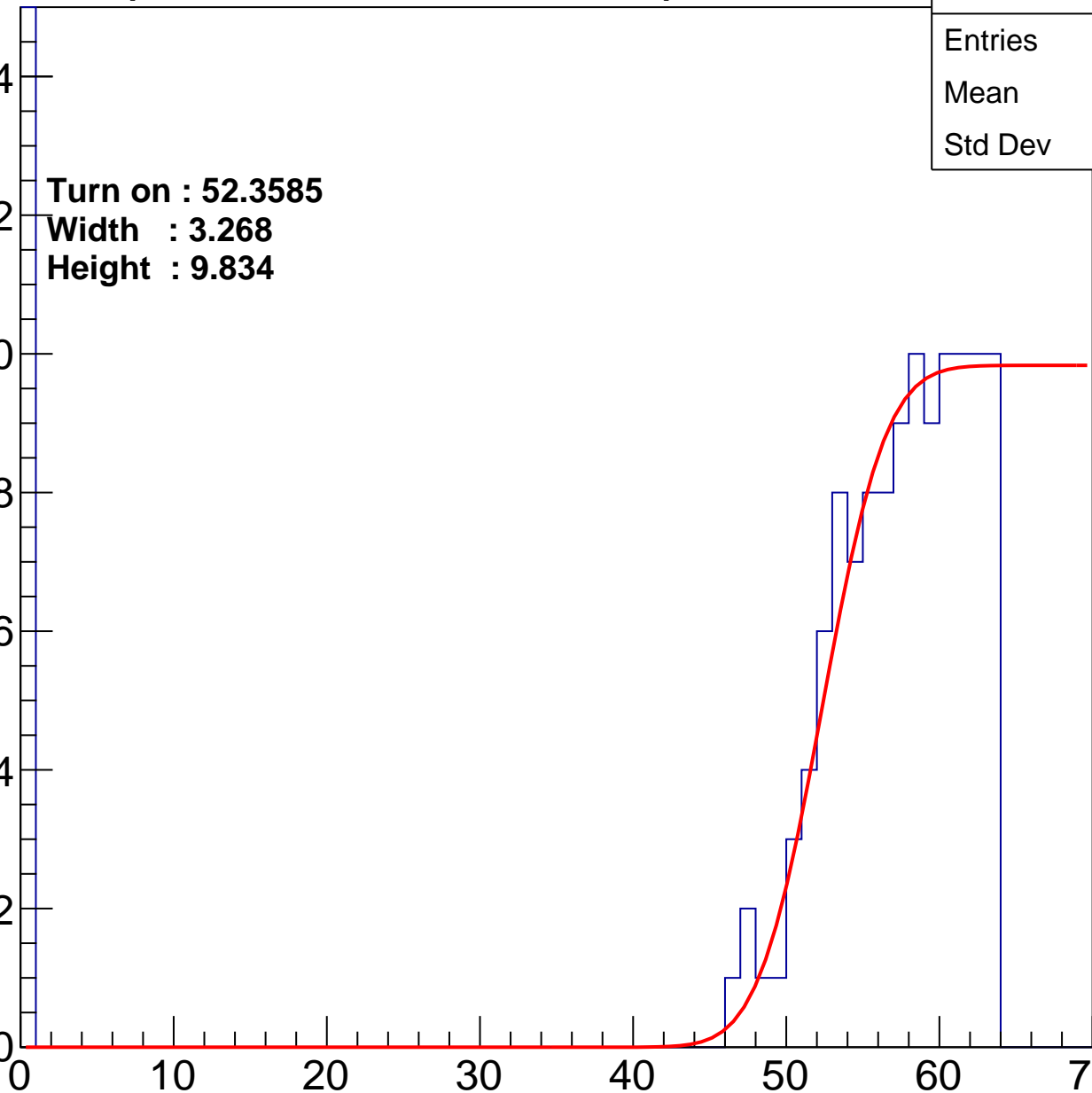
Width : 3.268

Height : 9.834

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	34.54
Std Dev	28.15

Turn on : 53.0522

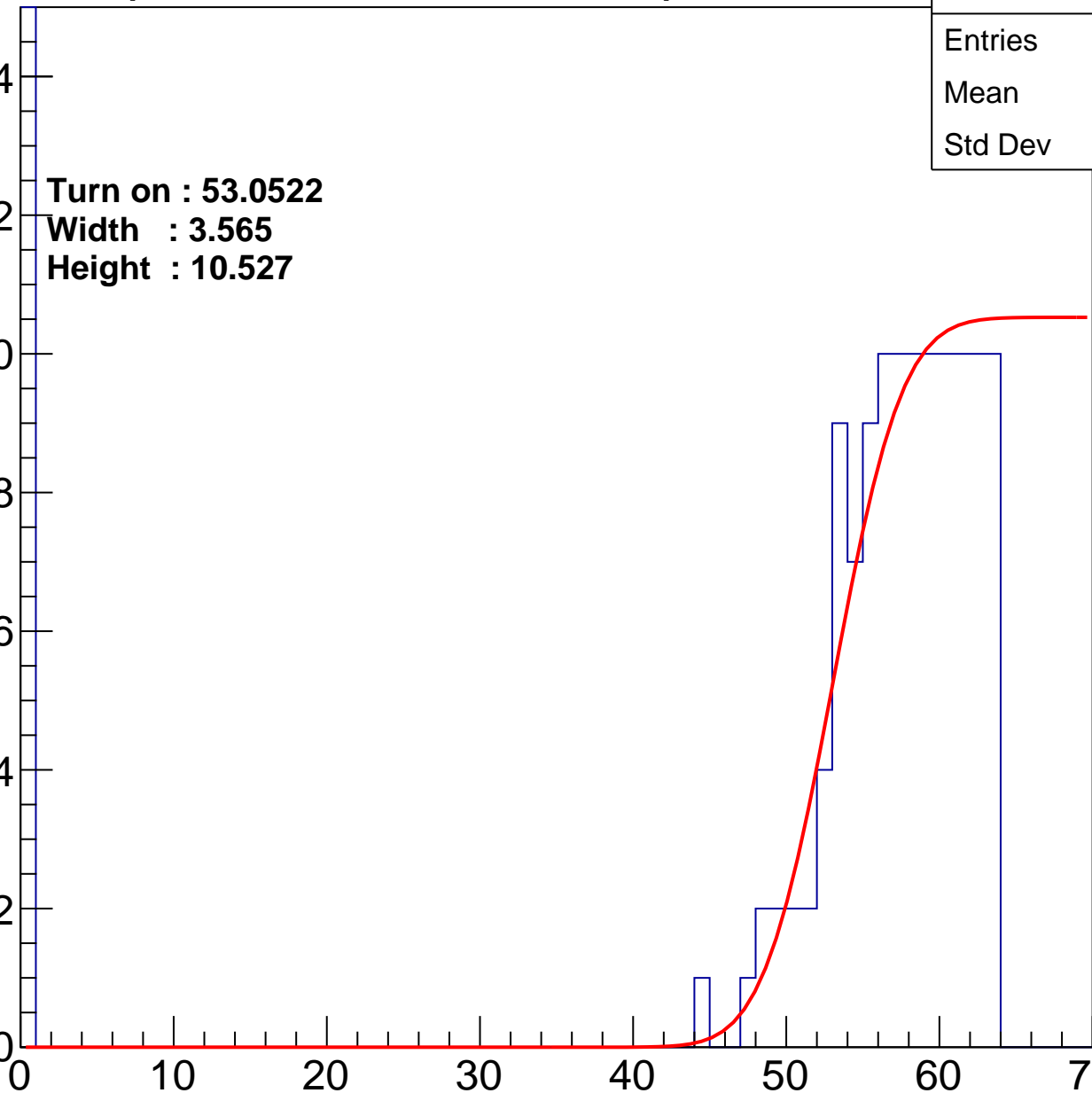
Width : 3.565

Height : 10.527

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch94

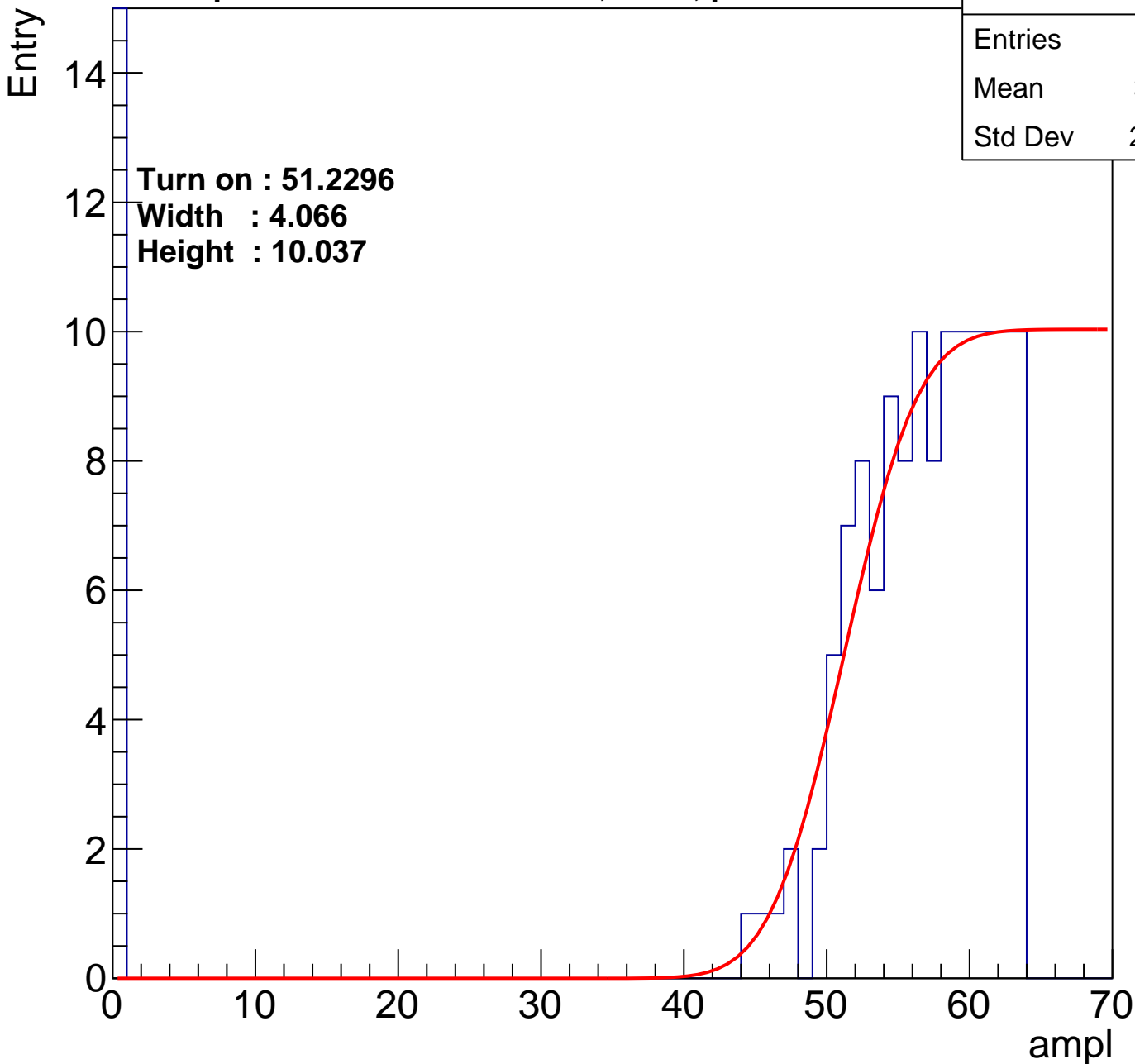
calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	32.61
Std Dev	28.15

Turn on : 51.2296

Width : 4.066

Height : 10.037



B1L104S, U14-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	32.72
Std Dev	28.77

Turn on : 53.6993

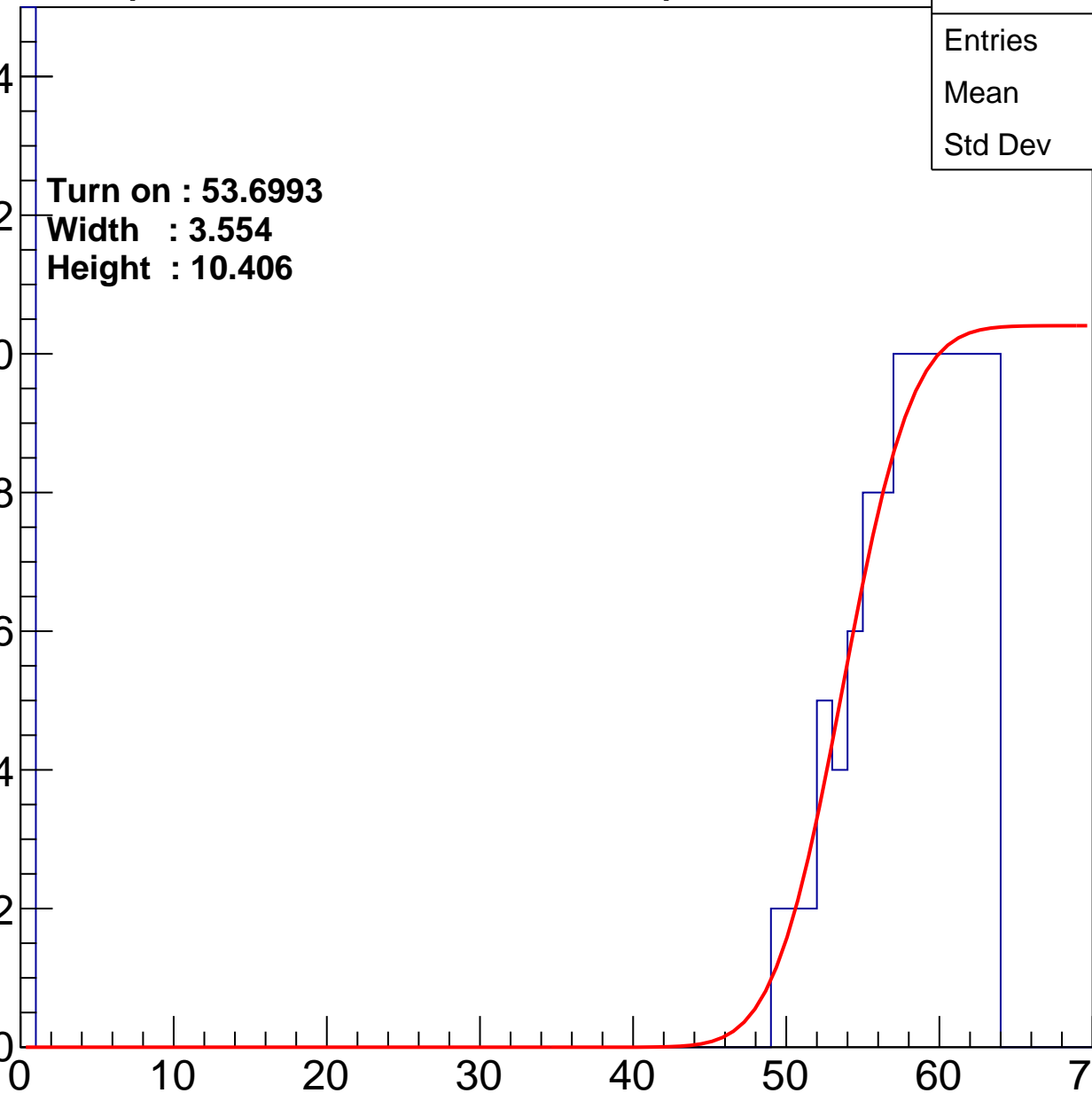
Width : 3.554

Height : 10.406

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	32.32
Std Dev	28.89

Turn on : 54.1632

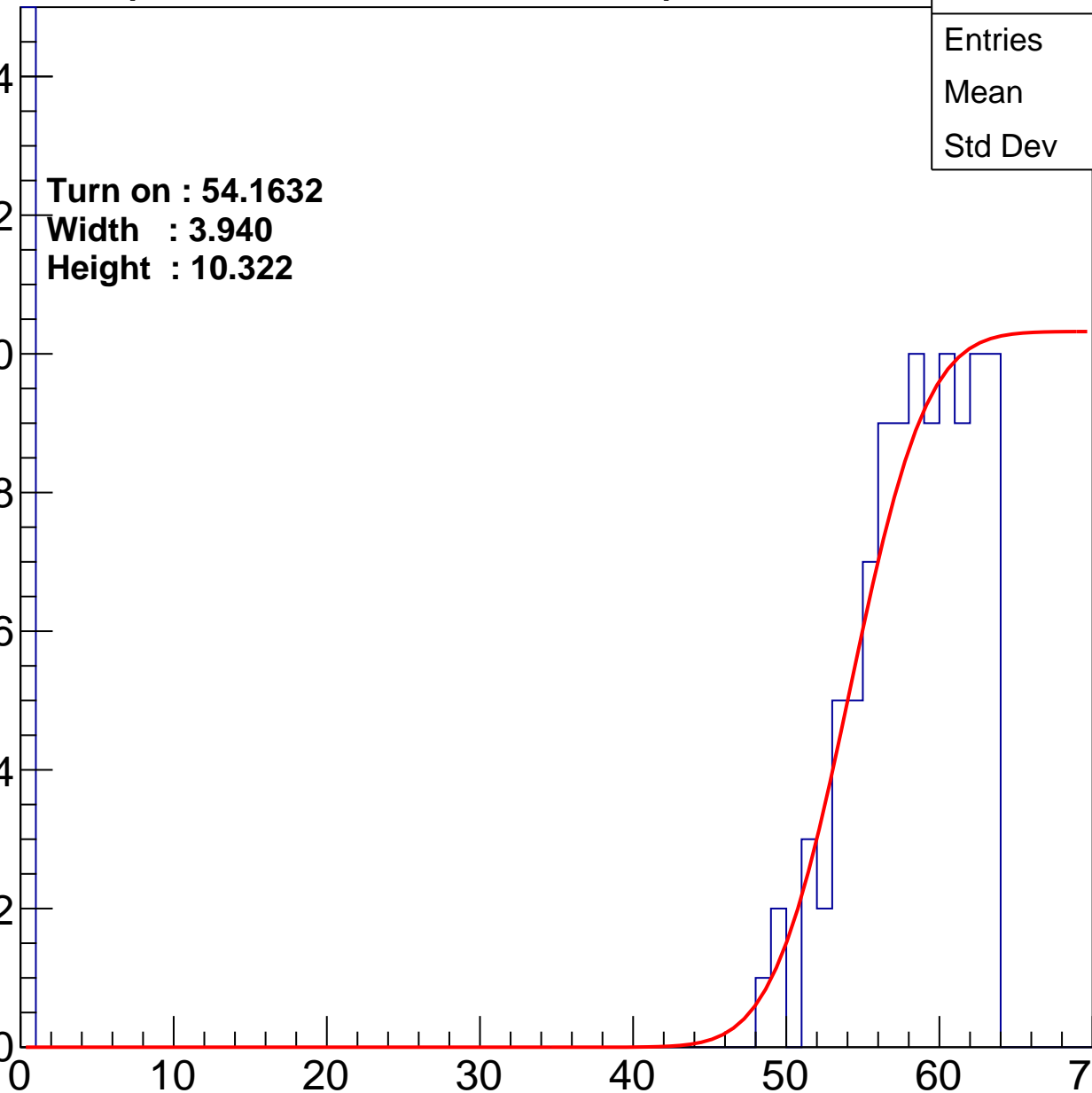
Width : 3.940

Height : 10.322

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch97

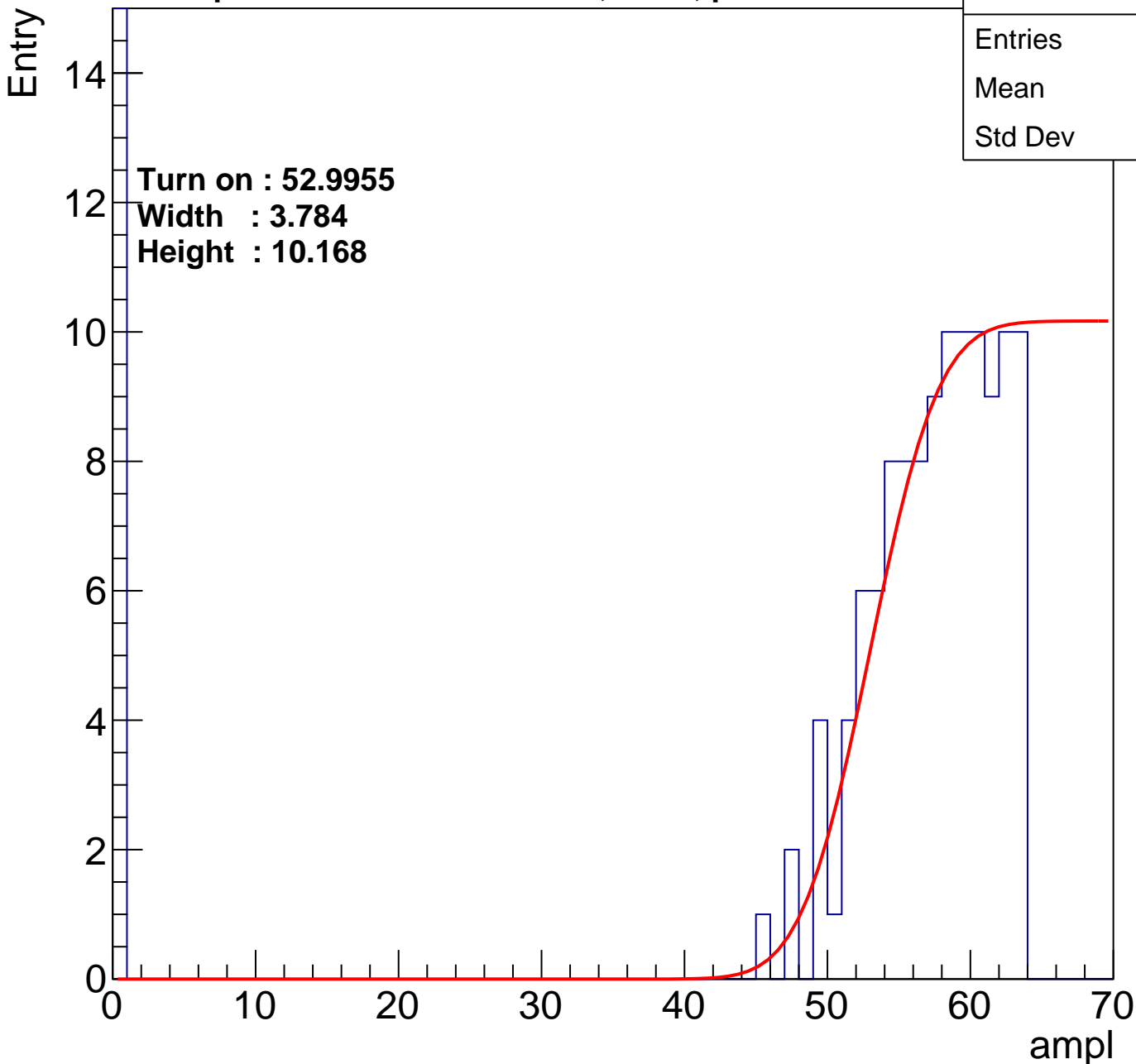
calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	37.2
Std Dev	27.4

Turn on : 52.9955

Width : 3.784

Height : 10.168



B1L104S, U14-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	35.18
Std Dev	27.98

Turn on : 52.8367

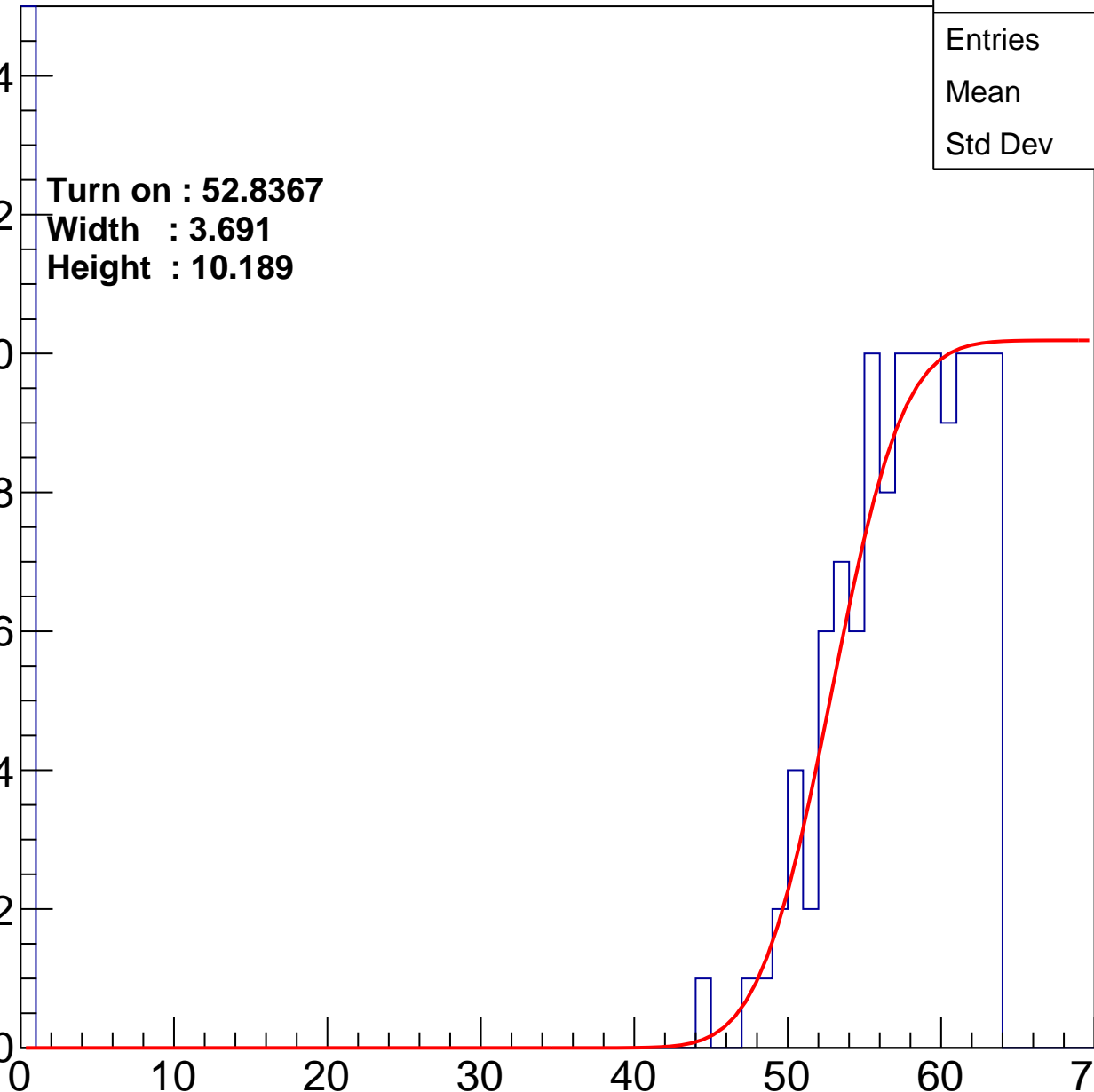
Width : 3.691

Height : 10.189

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	32.16
Std Dev	28.47

Turn on : 52.0735

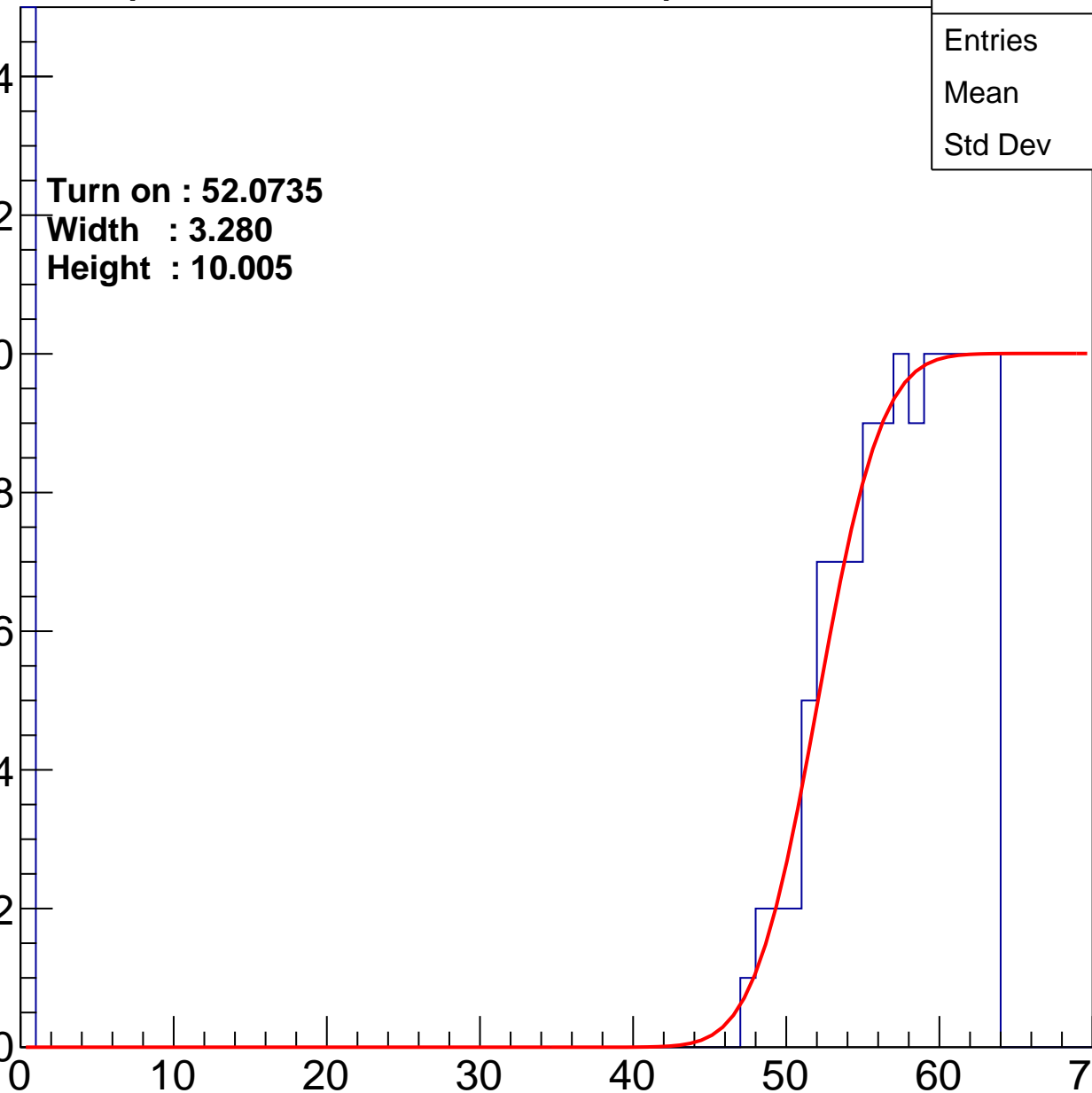
Width : 3.280

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	38.6
Std Dev	26.67

Turn on : 52.2037

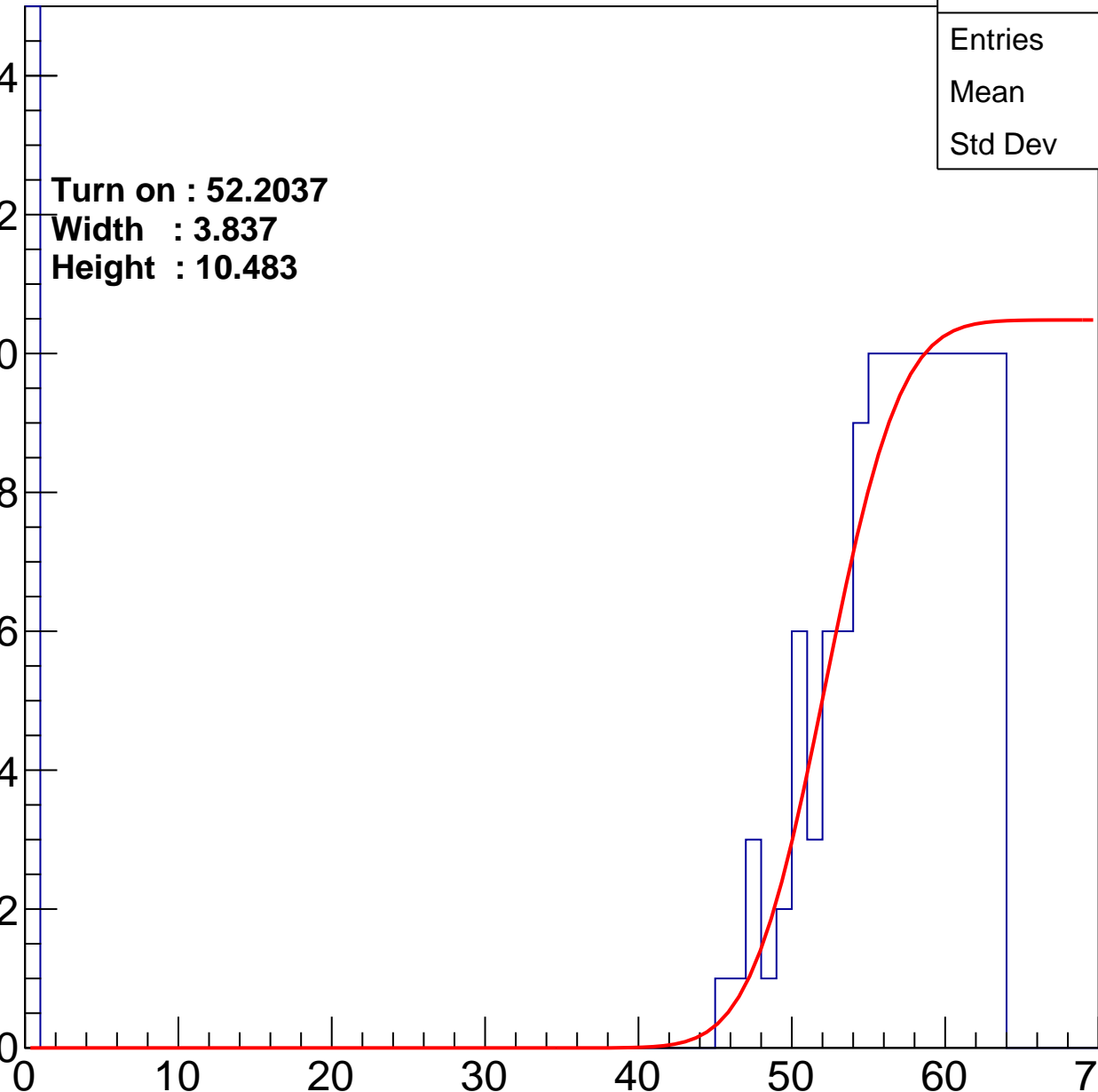
Width : 3.837

Height : 10.483

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	31.8
Std Dev	29.13

Turn on : 54.8465

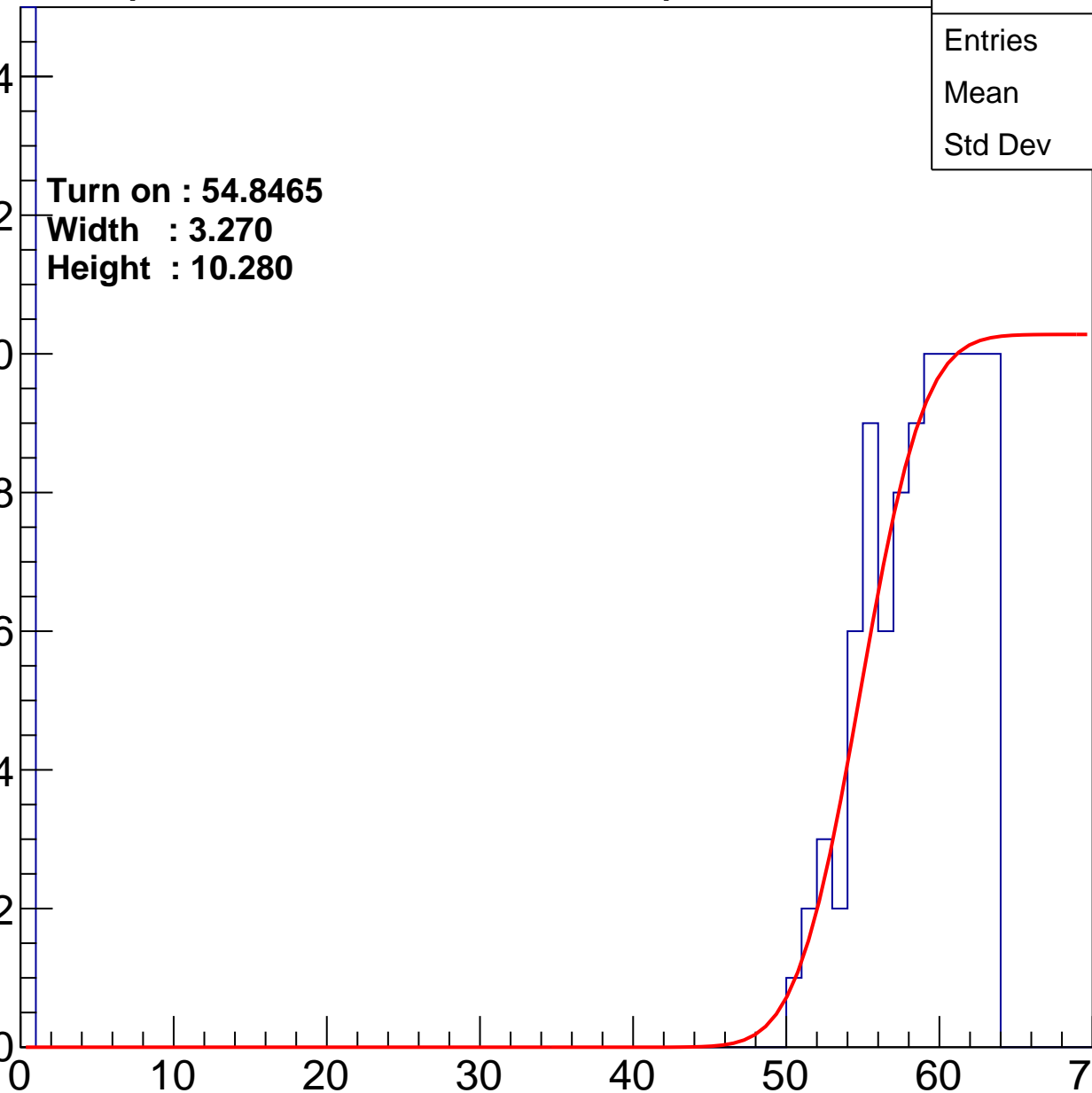
Width : 3.270

Height : 10.280

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	35.08
Std Dev	27.97

Turn on : 51.9135

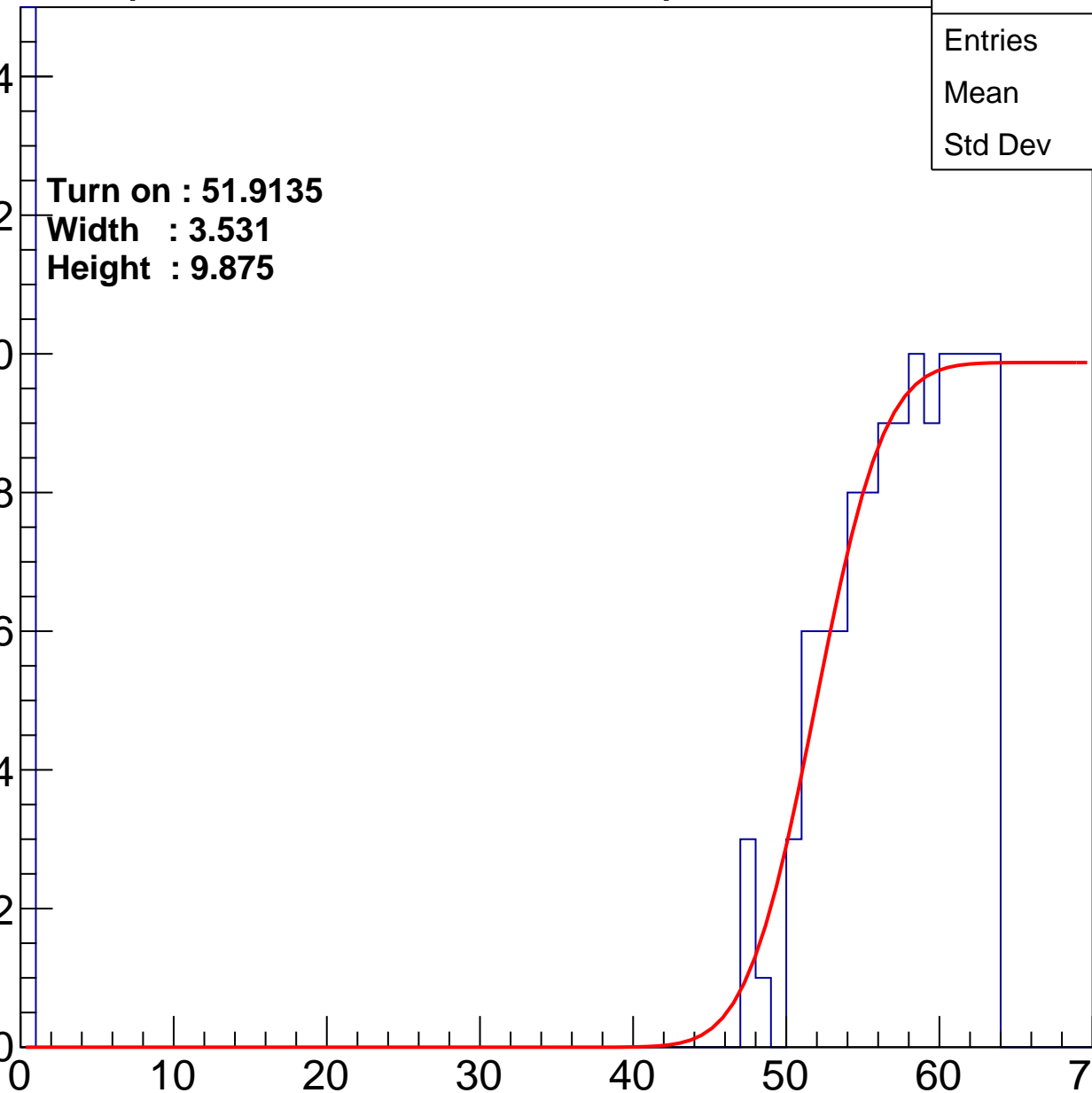
Width : 3.531

Height : 9.875

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	32
Std Dev	28.87

Turn on : 53.8467

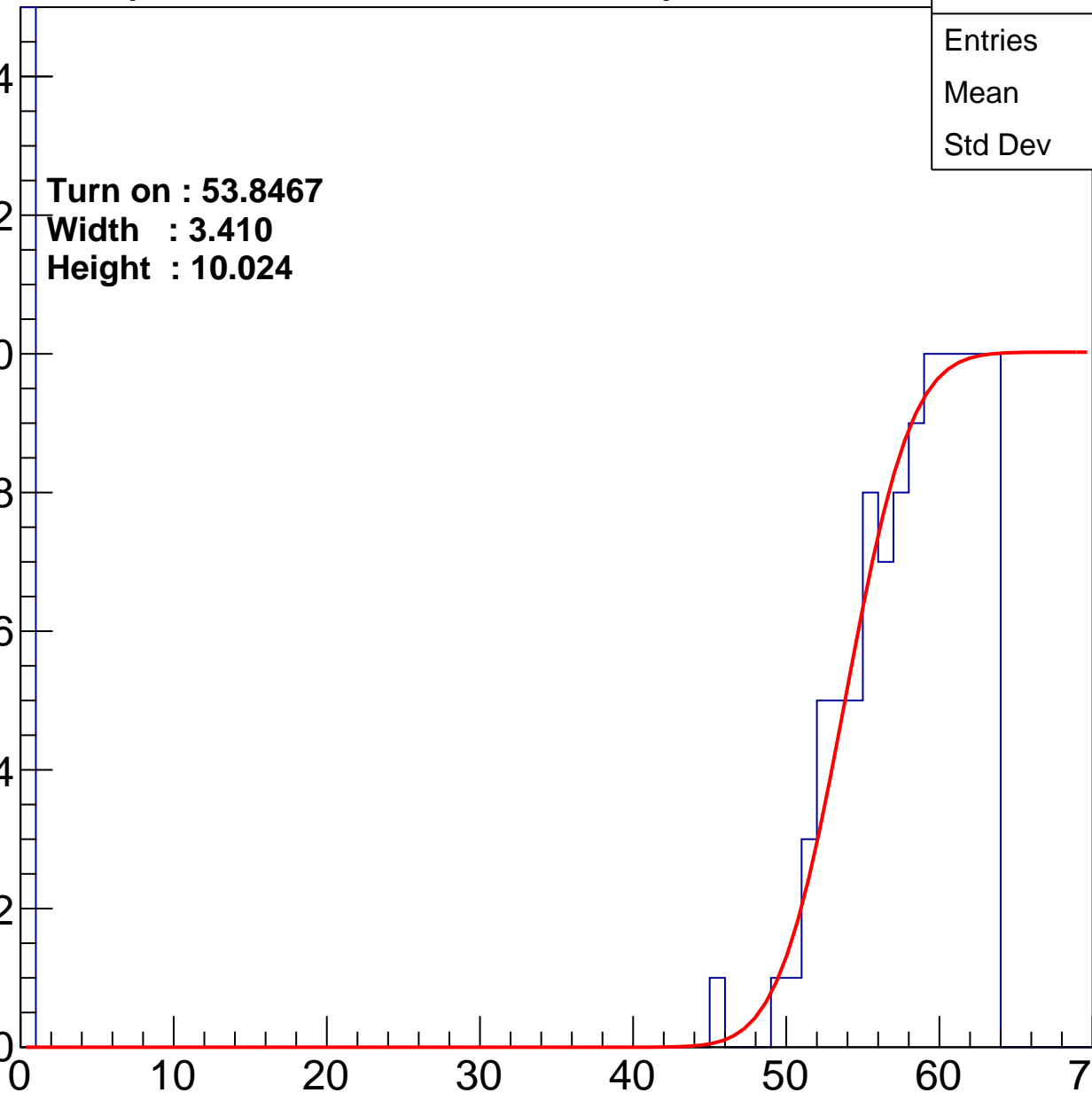
Width : 3.410

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch104

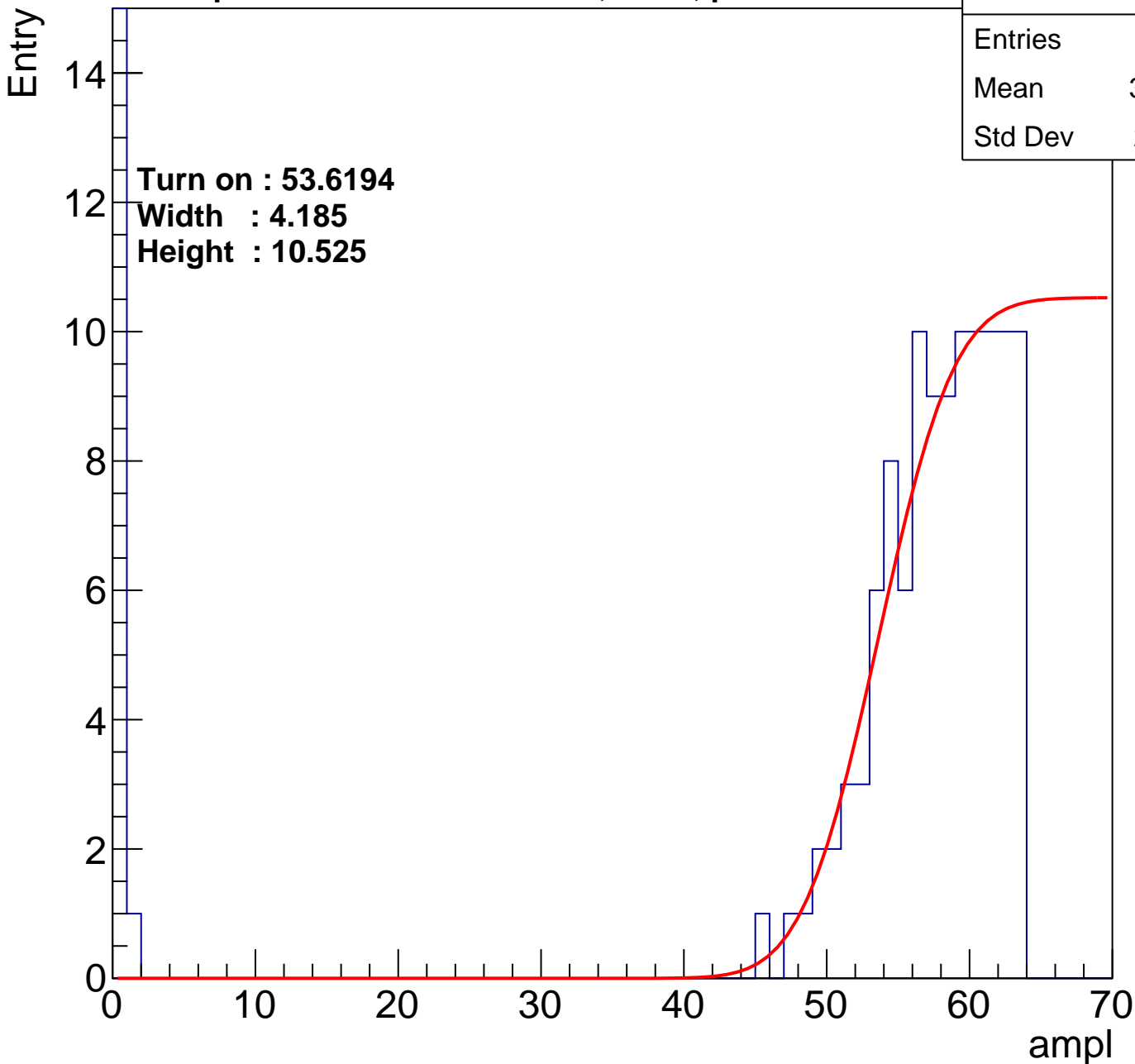
calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	32.52
Std Dev	28.61

Turn on : 53.6194

Width : 4.185

Height : 10.525



B1L104S, U14-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	36.26
Std Dev	27.82

Turn on : 53.1097

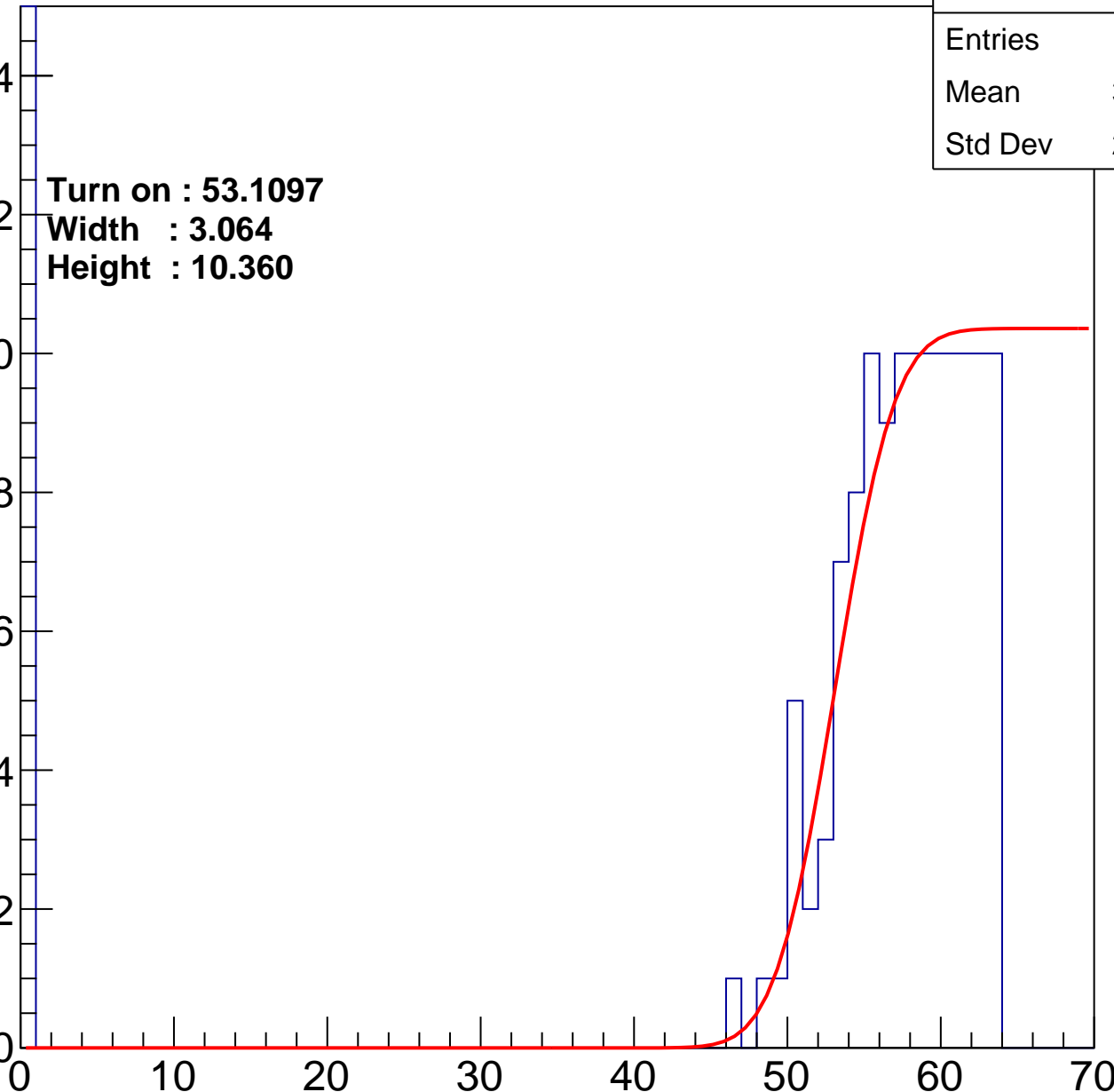
Width : 3.064

Height : 10.360

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	230
Mean	31.05
Std Dev	28.39

Turn on : 51.3725

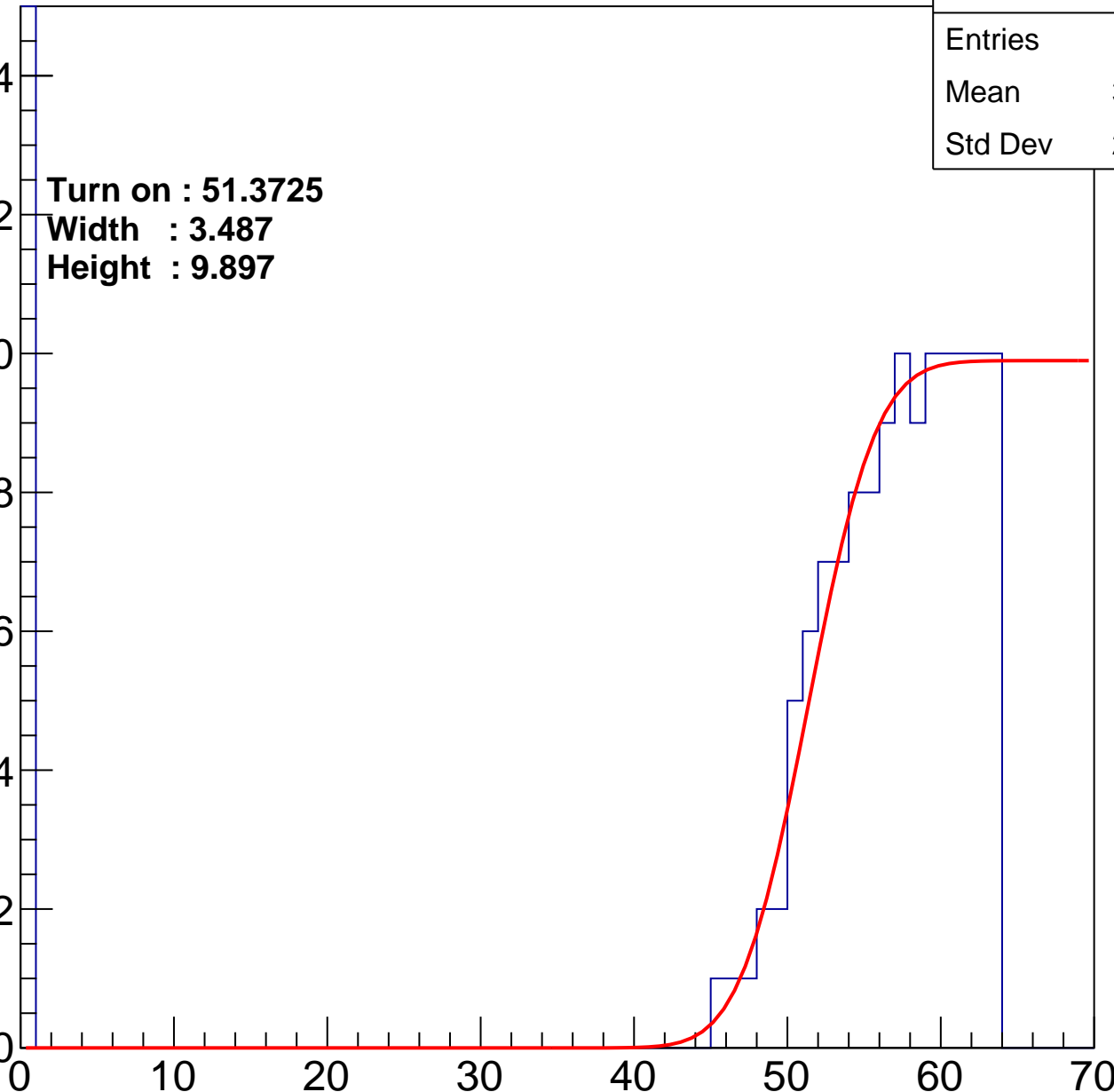
Width : 3.487

Height : 9.897

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch107

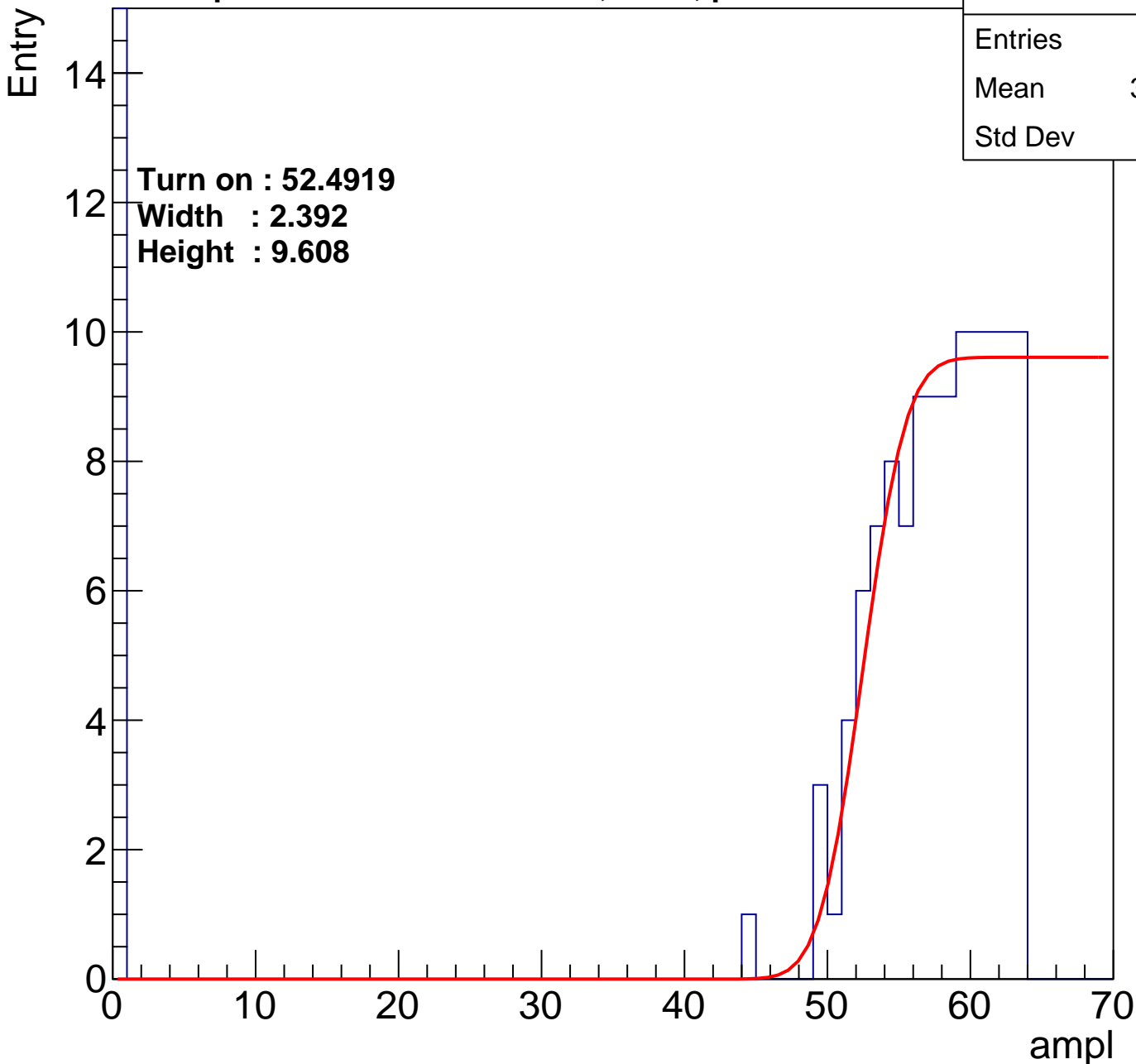
calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	39.84
Std Dev	26.6

Turn on : 52.4919

Width : 2.392

Height : 9.608



B1L104S, U14-ch108

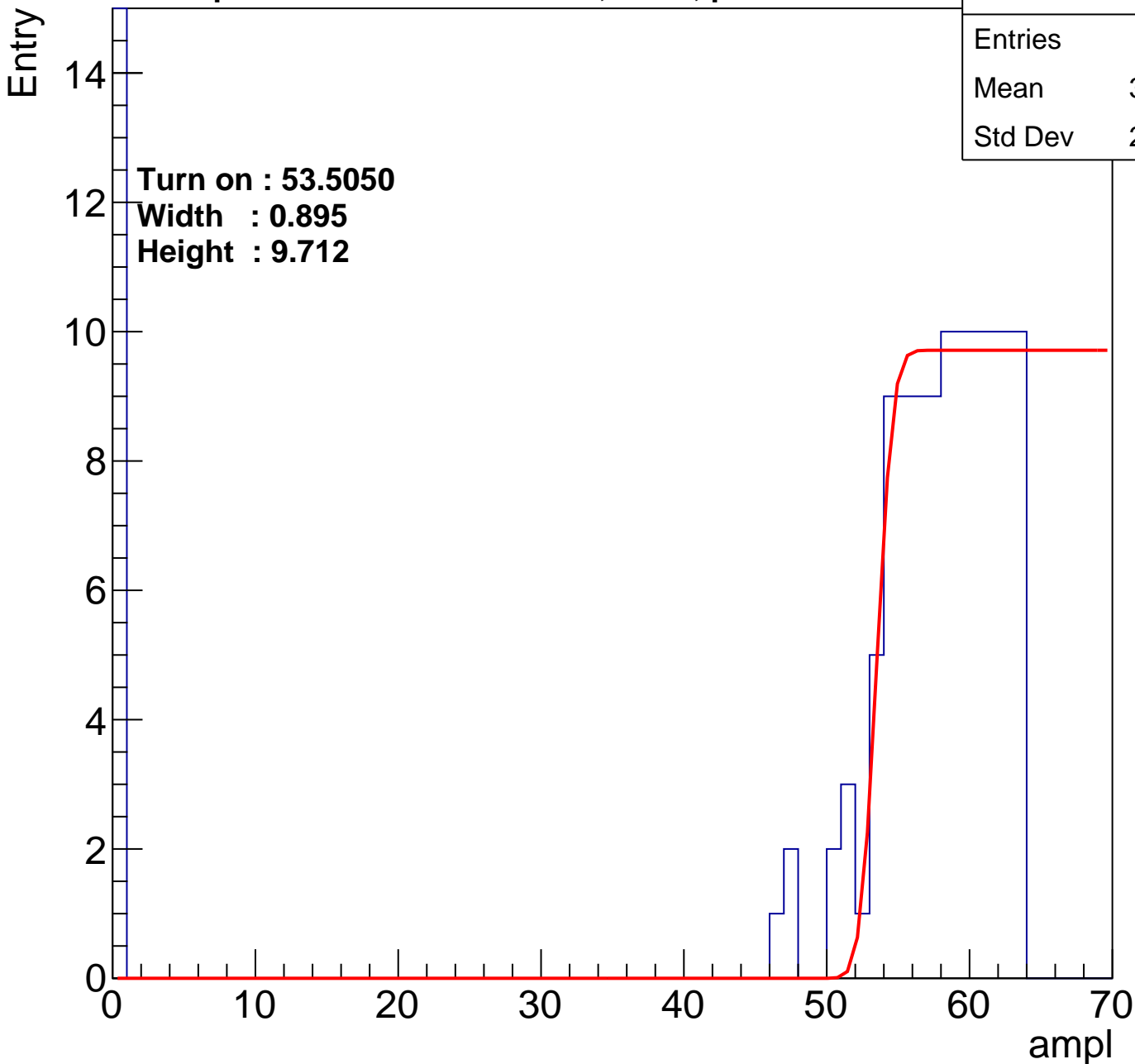
calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	31.38
Std Dev	28.83

Turn on : 53.5050

Width : 0.895

Height : 9.712



B1L104S, U14-ch109

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.15
Std Dev	28.7

Turn on : 54.3768

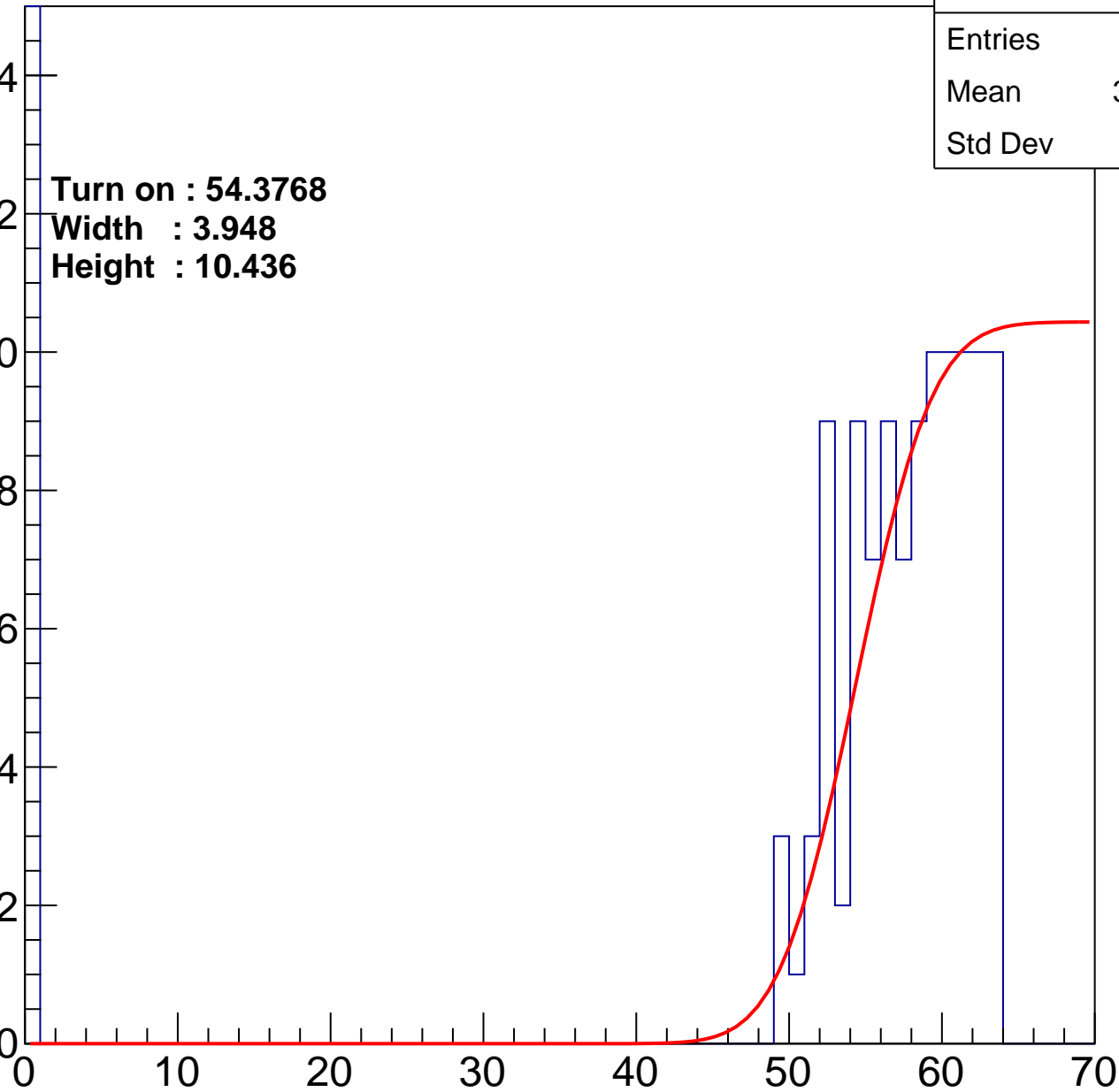
Width : 3.948

Height : 10.436

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	254
Mean	33.2
Std Dev	27.48

Turn on : 49.5017

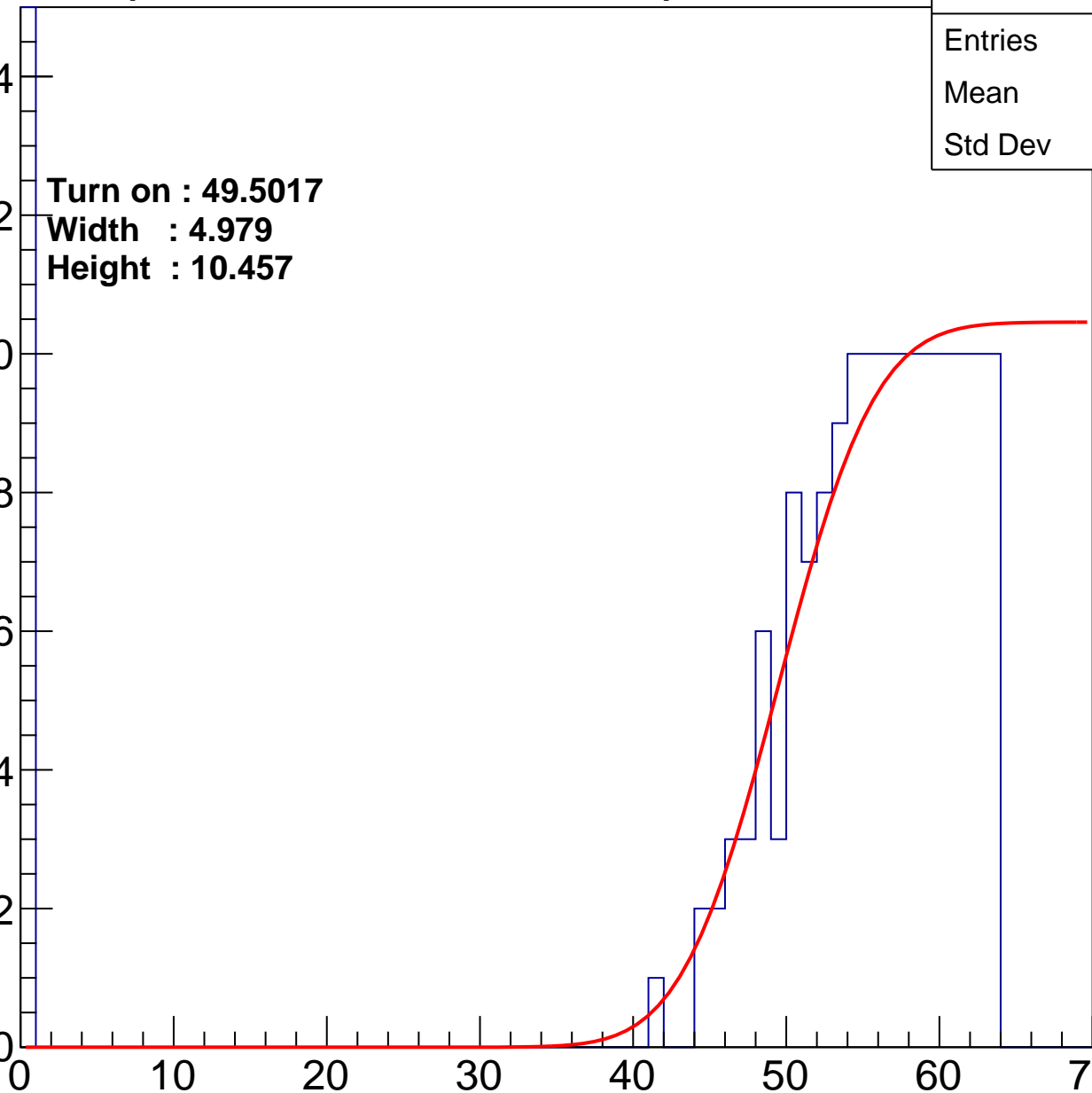
Width : 4.979

Height : 10.457

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	33.78
Std Dev	28.1

Turn on : 51.4402

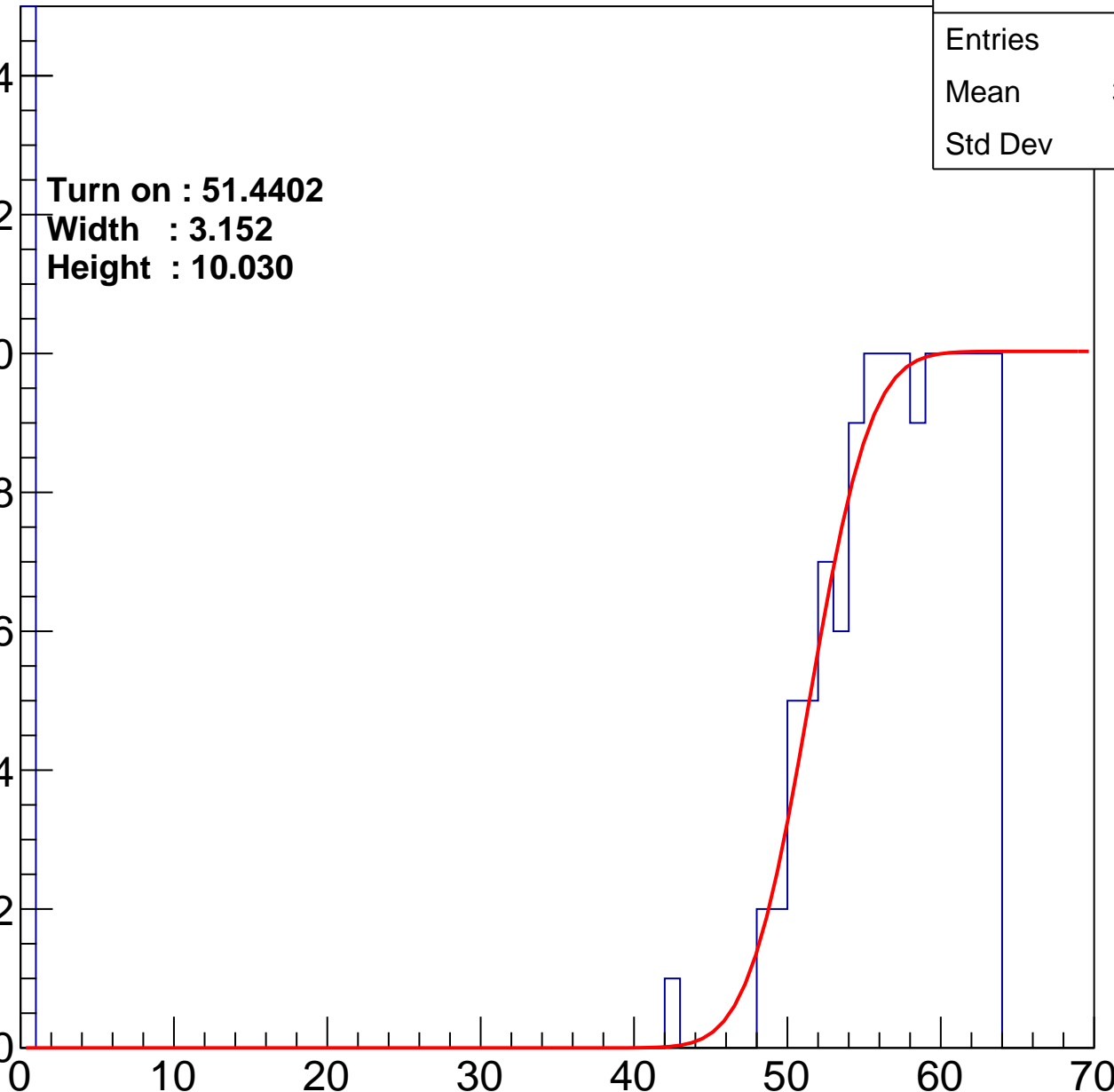
Width : 3.152

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	228
Mean	32.56
Std Dev	28

Turn on : 51.4096

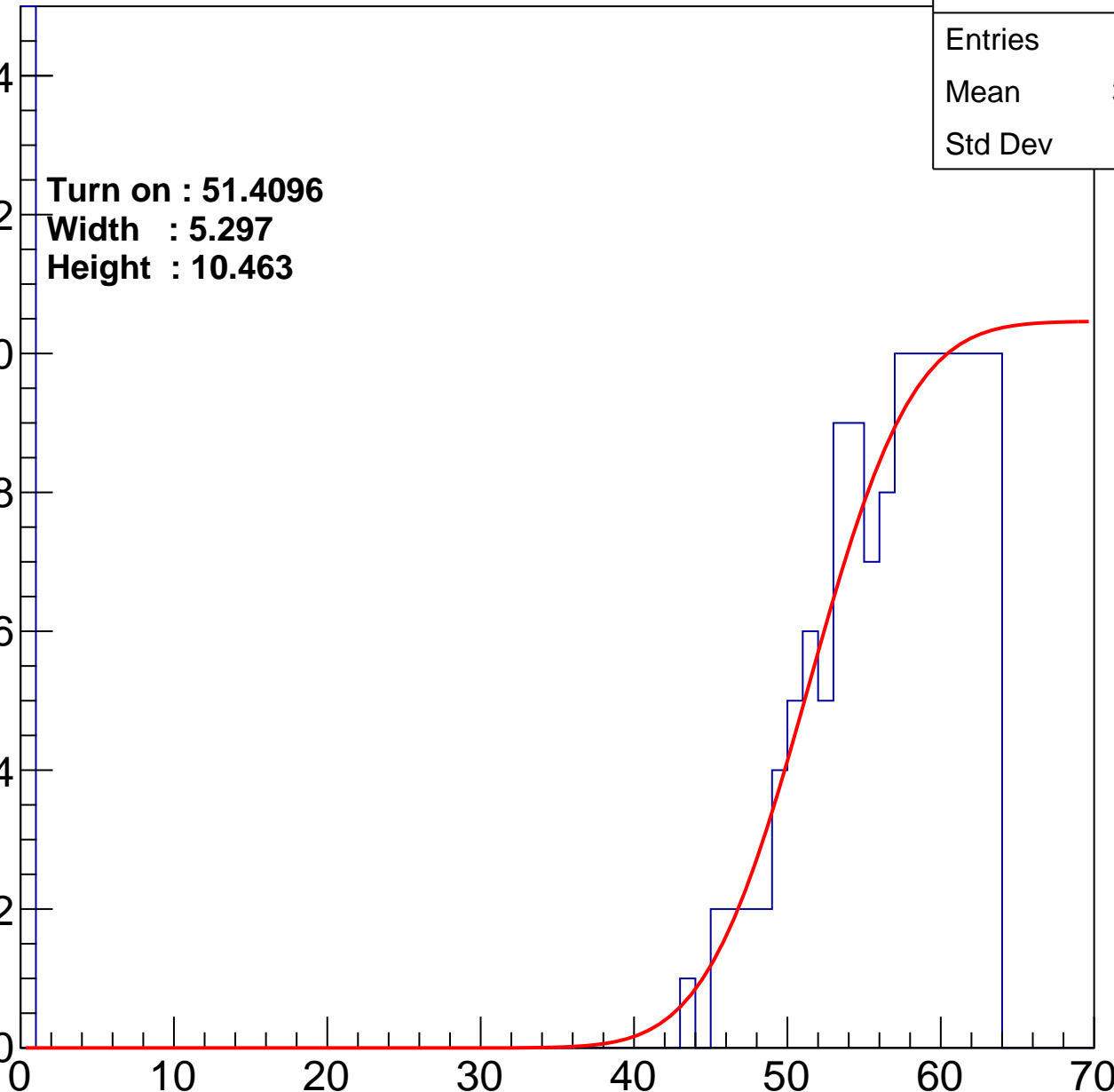
Width : 5.297

Height : 10.463

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	31.79
Std Dev	28.51

Turn on : 52.5424

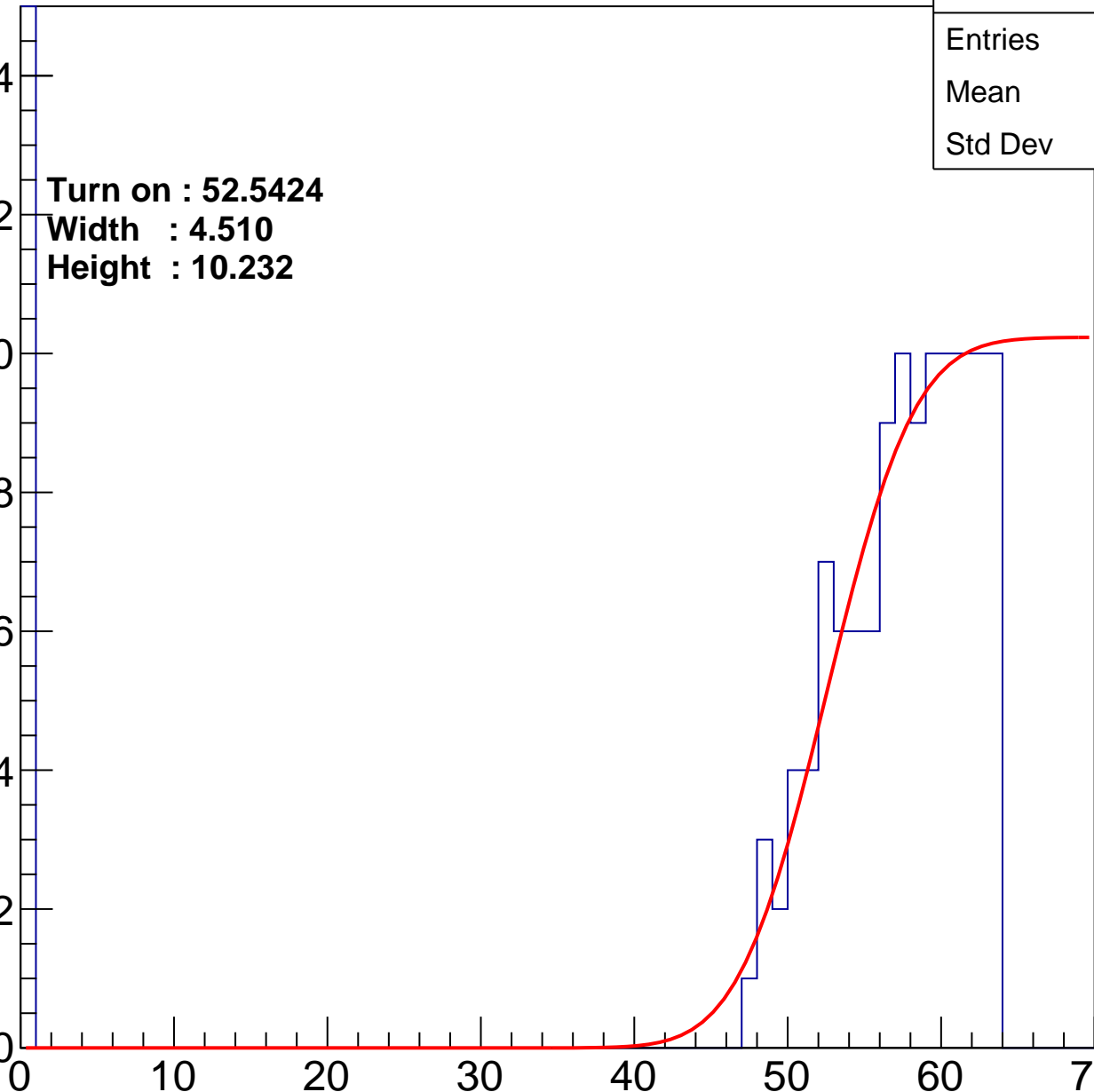
Width : 4.510

Height : 10.232

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch114

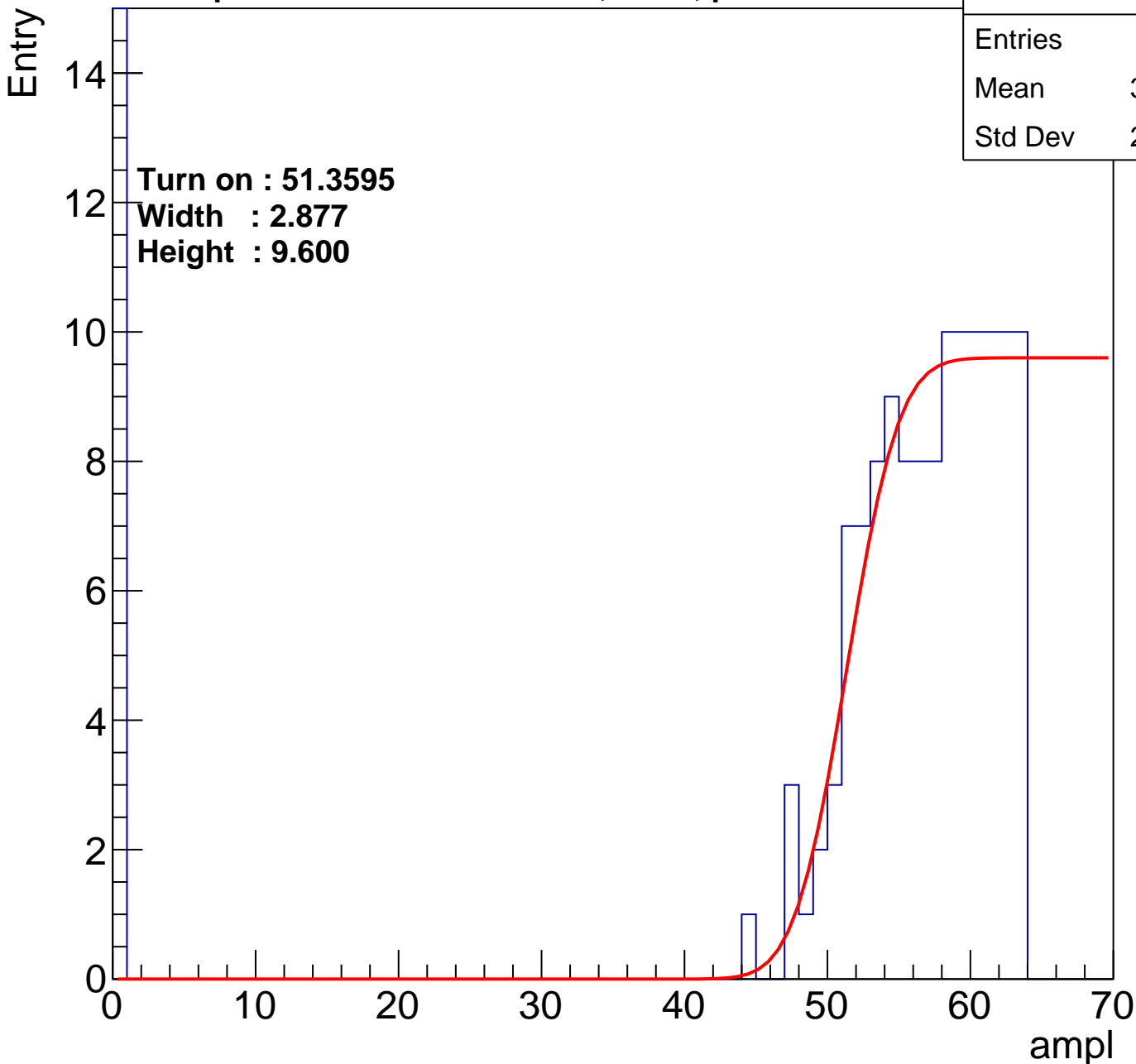
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	37.49
Std Dev	27.06

Turn on : 51.3595

Width : 2.877

Height : 9.600



B1L104S, U14-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	35.83
Std Dev	27.15

Turn on : 50.5580

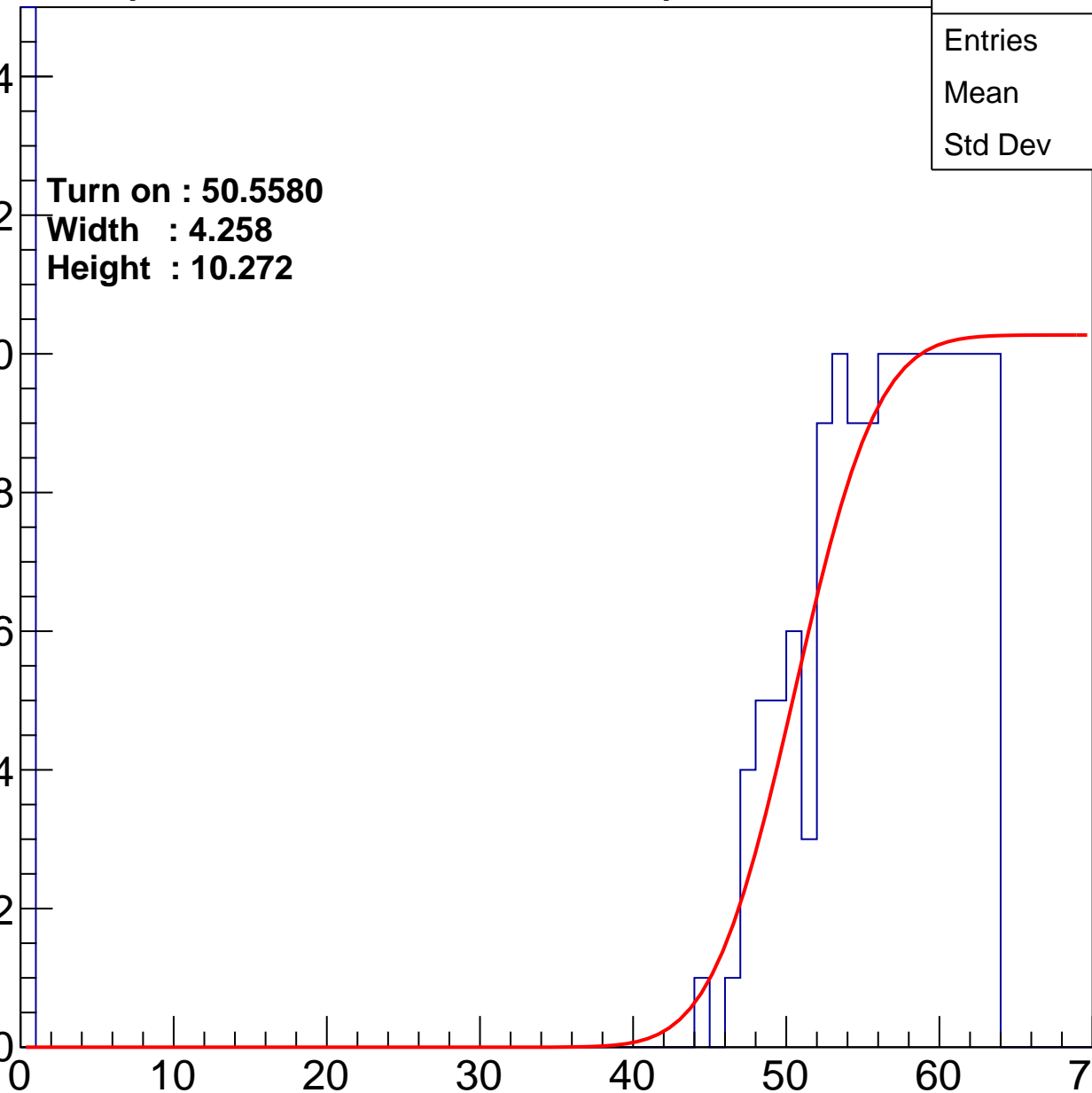
Width : 4.258

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch116

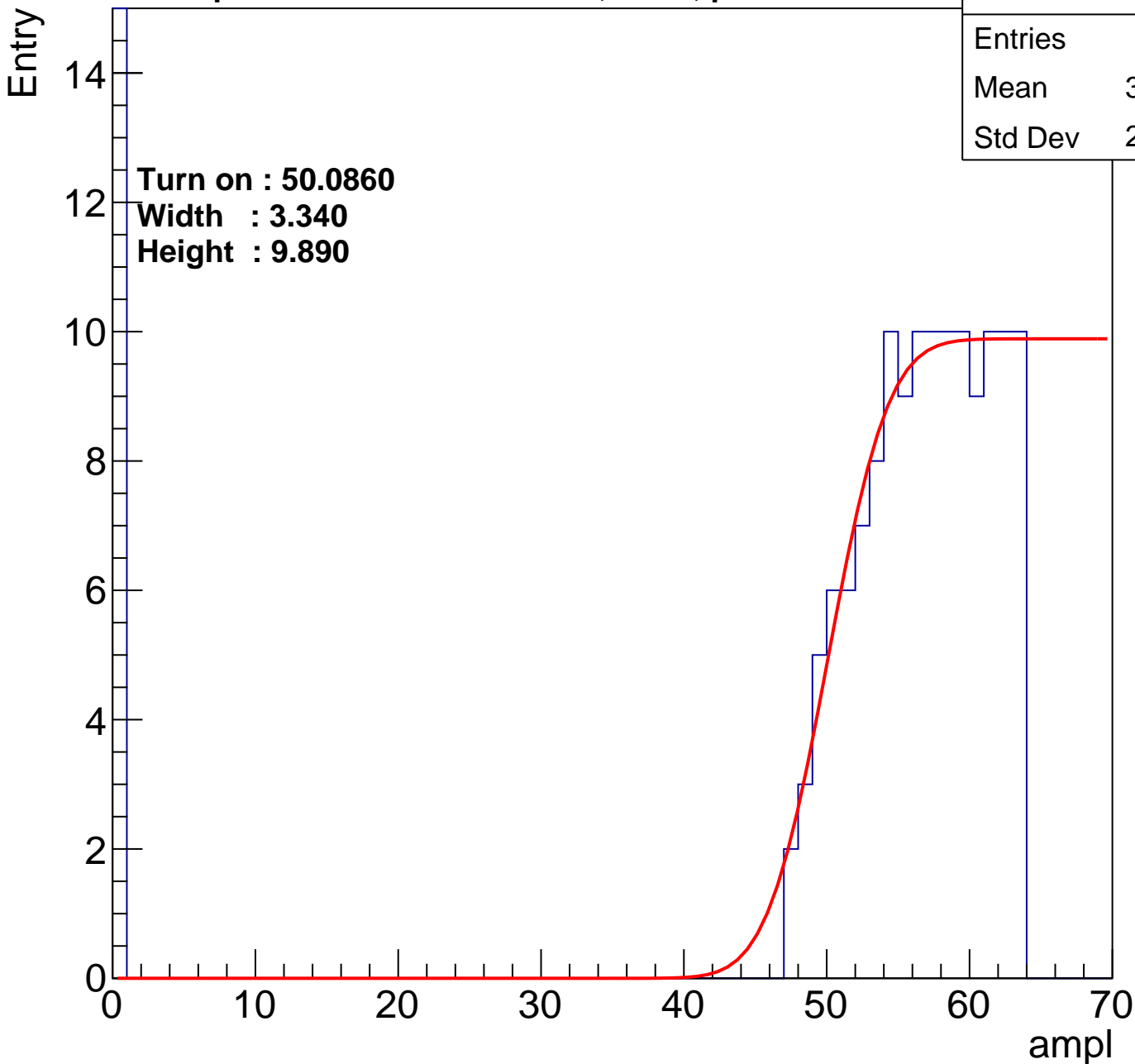
calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	39.85
Std Dev	25.93

Turn on : 50.0860

Width : 3.340

Height : 9.890



B1L104S, U14-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	221
Mean	24.58
Std Dev	28.92

Turn on : 55.0341

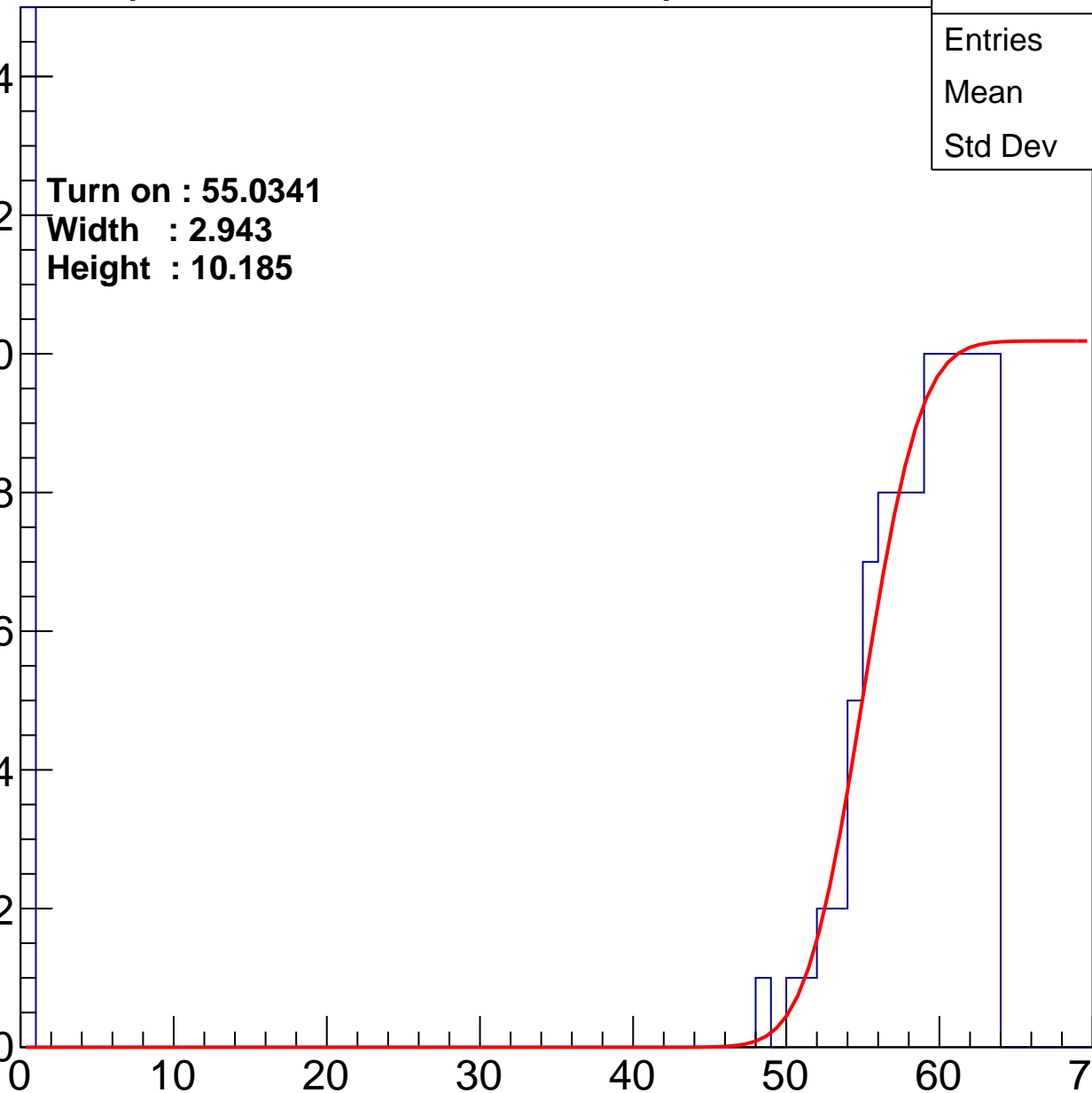
Width : 2.943

Height : 10.185

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	32.8
Std Dev	28.85

Turn on : 55.8320

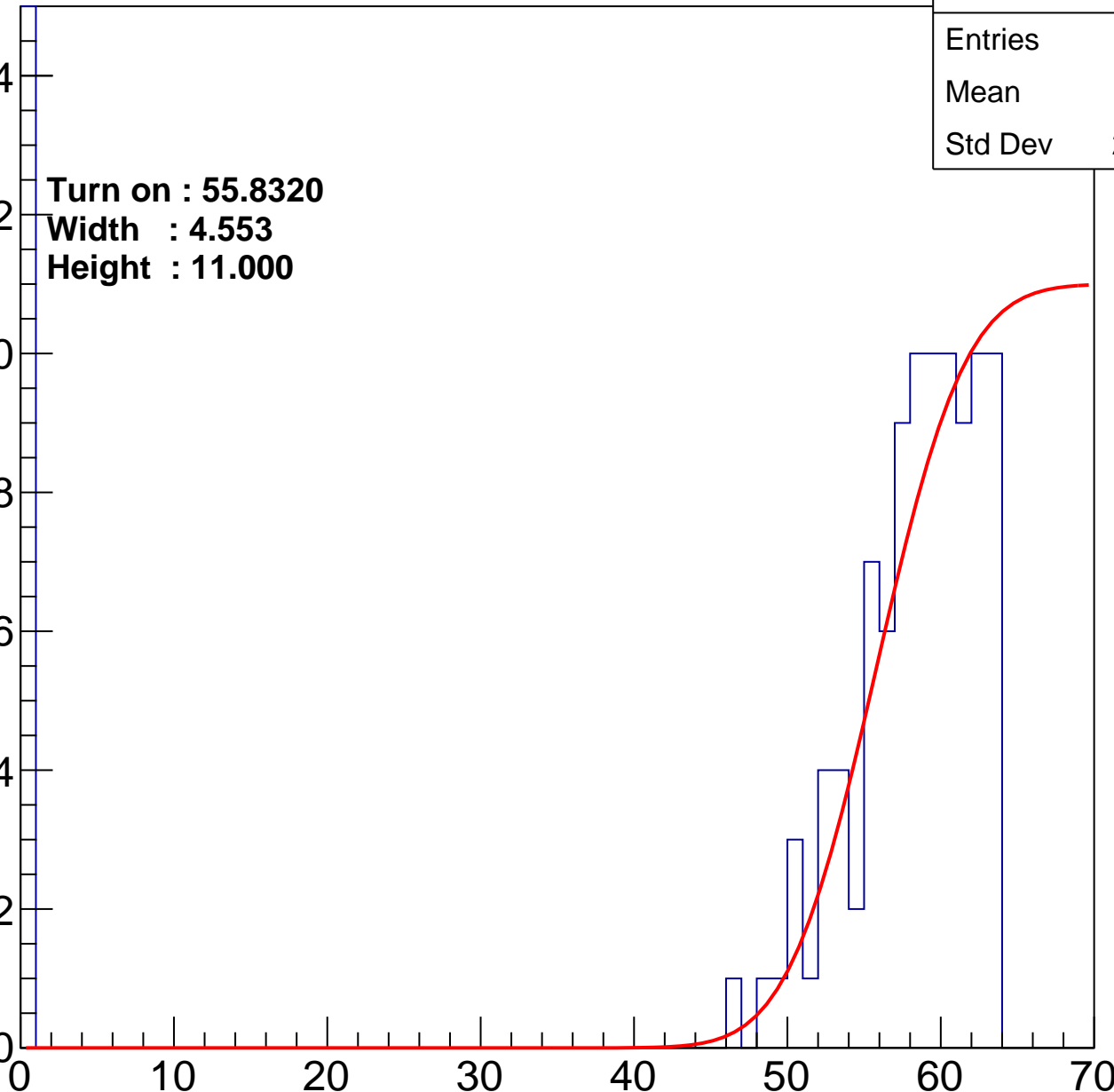
Width : 4.553

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	31.02
Std Dev	29.28

Turn on : 56.3823

Width : 4.196

Height : 10.111

Entry

70

50

40

30

20

10

0

0

10

20

30

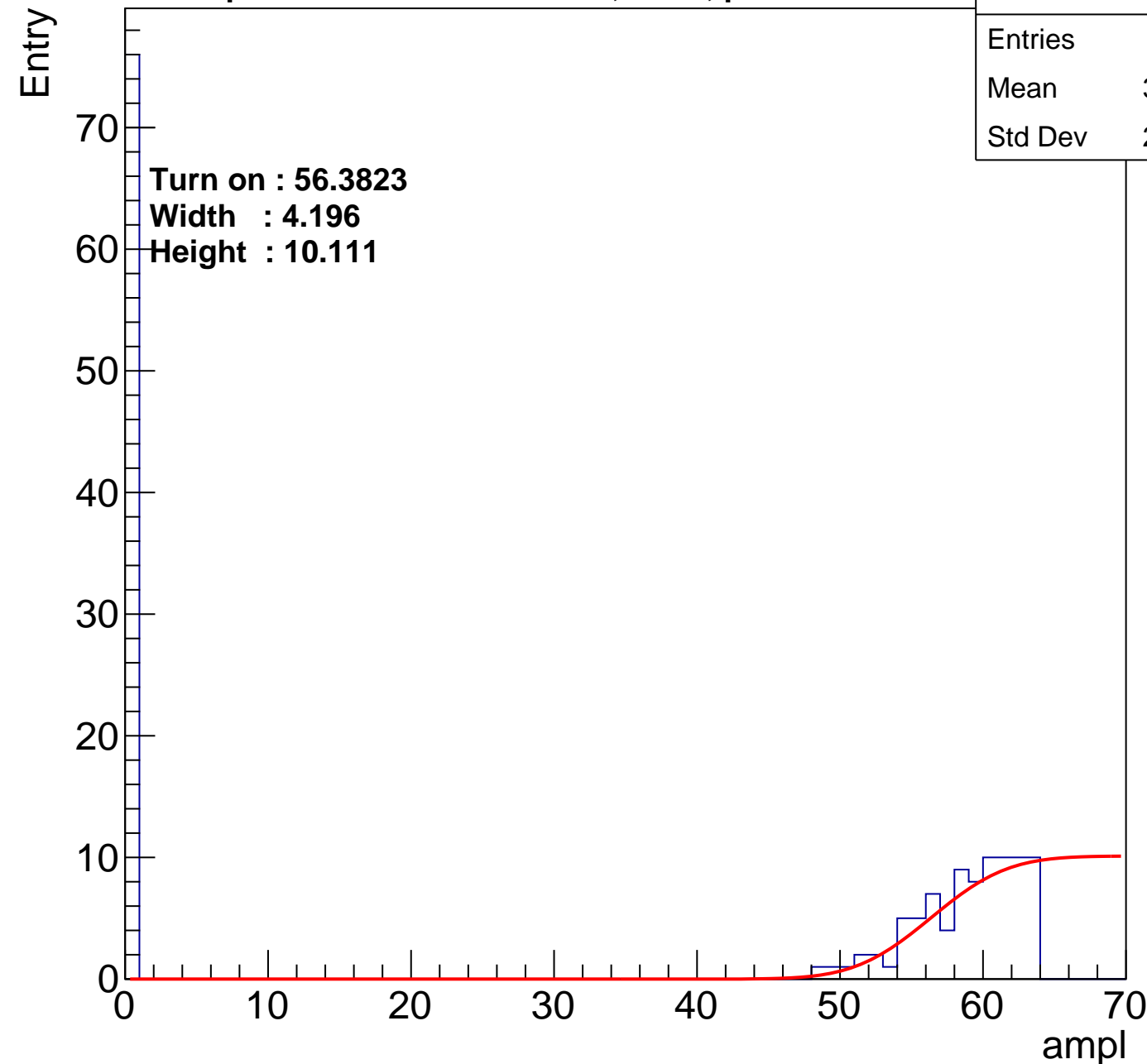
40

50

60

70

ampl



B1L104S, U14-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	34.96
Std Dev	27.56

Turn on : 51.5143

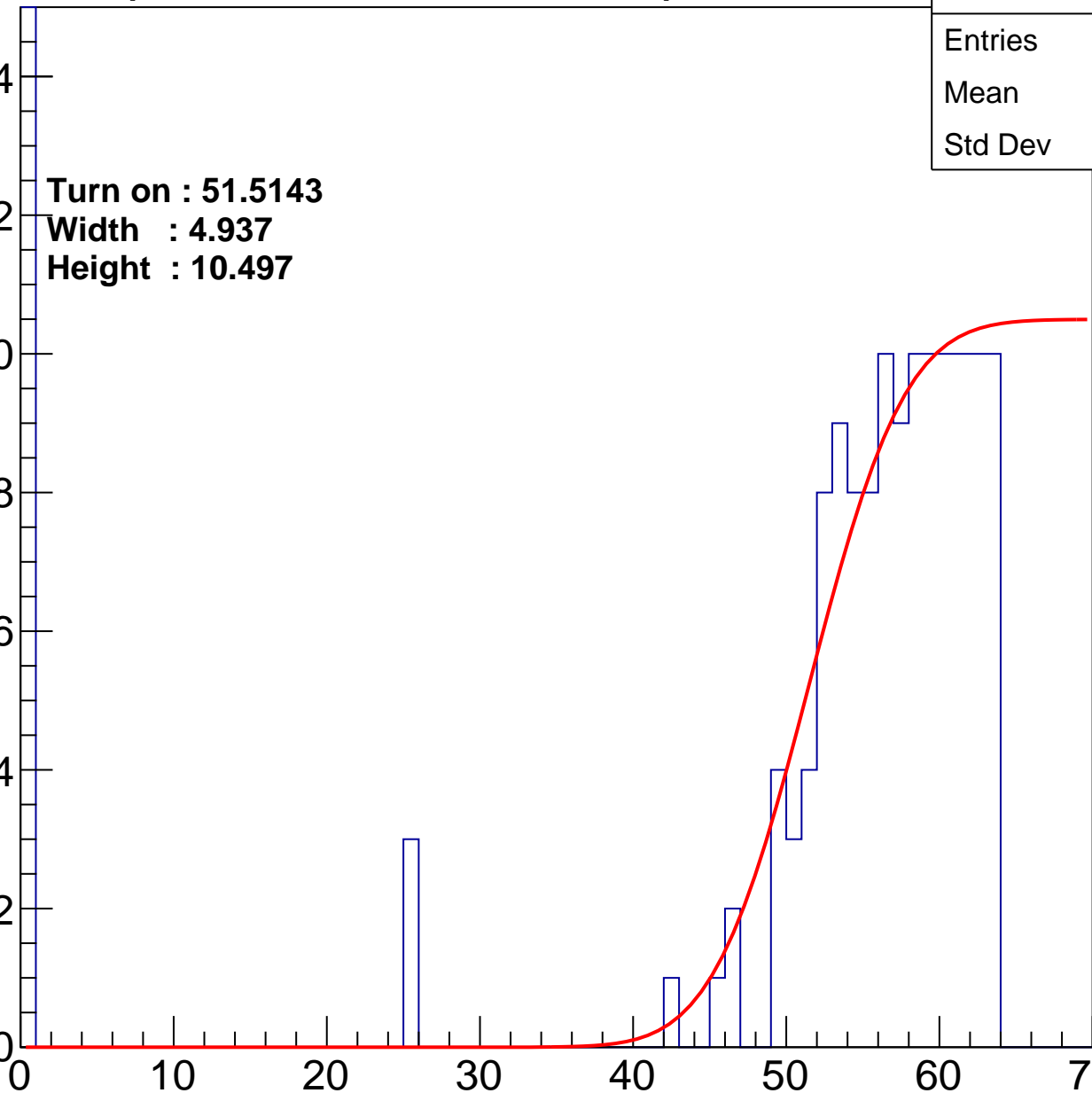
Width : 4.937

Height : 10.497

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	38.39
Std Dev	27.44

Turn on : 53.6141

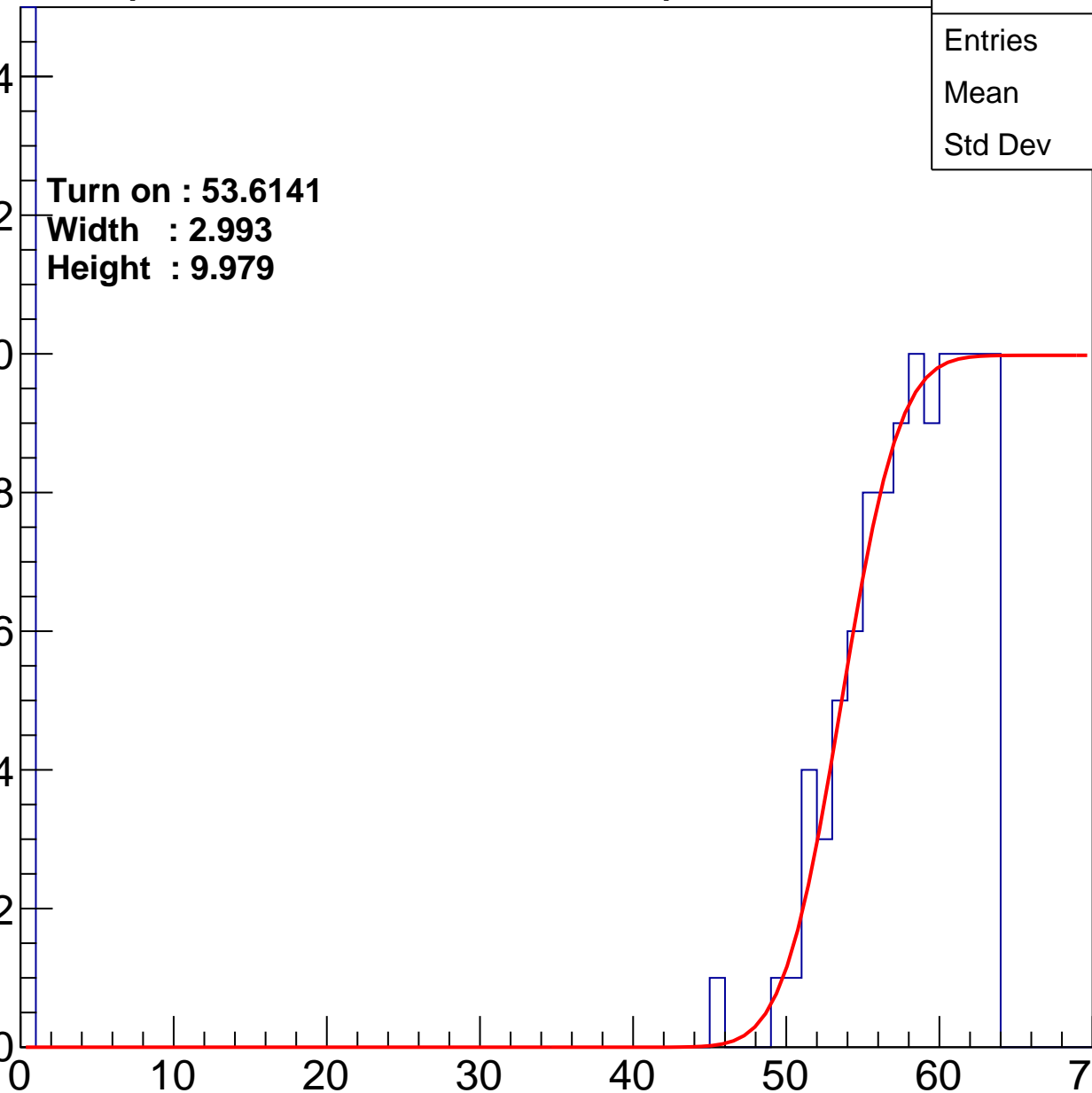
Width : 2.993

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	32.17
Std Dev	28.61

Turn on : 53.2365

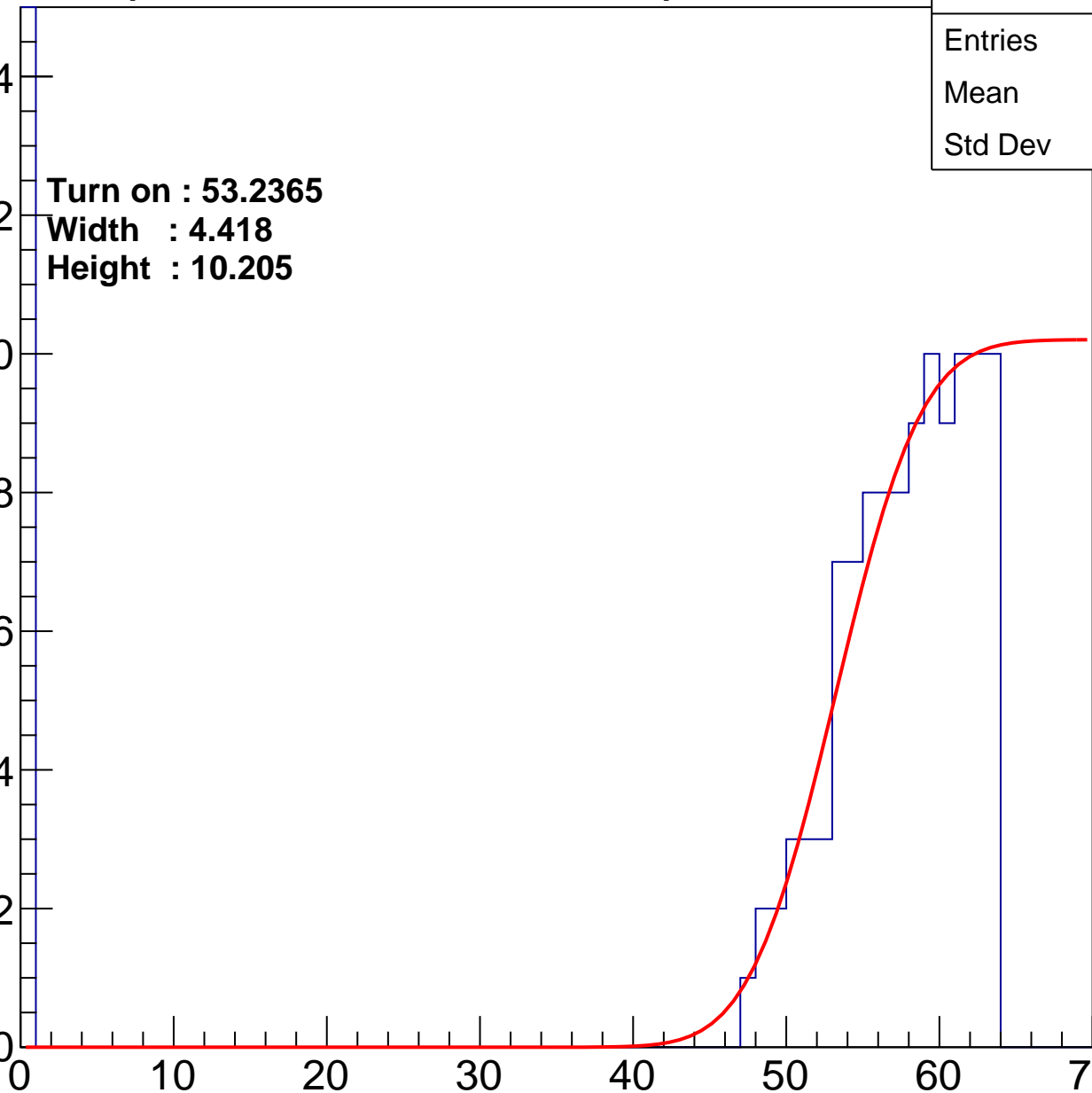
Width : 4.418

Height : 10.205

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	37.75
Std Dev	27.34

Turn on : 52.5242

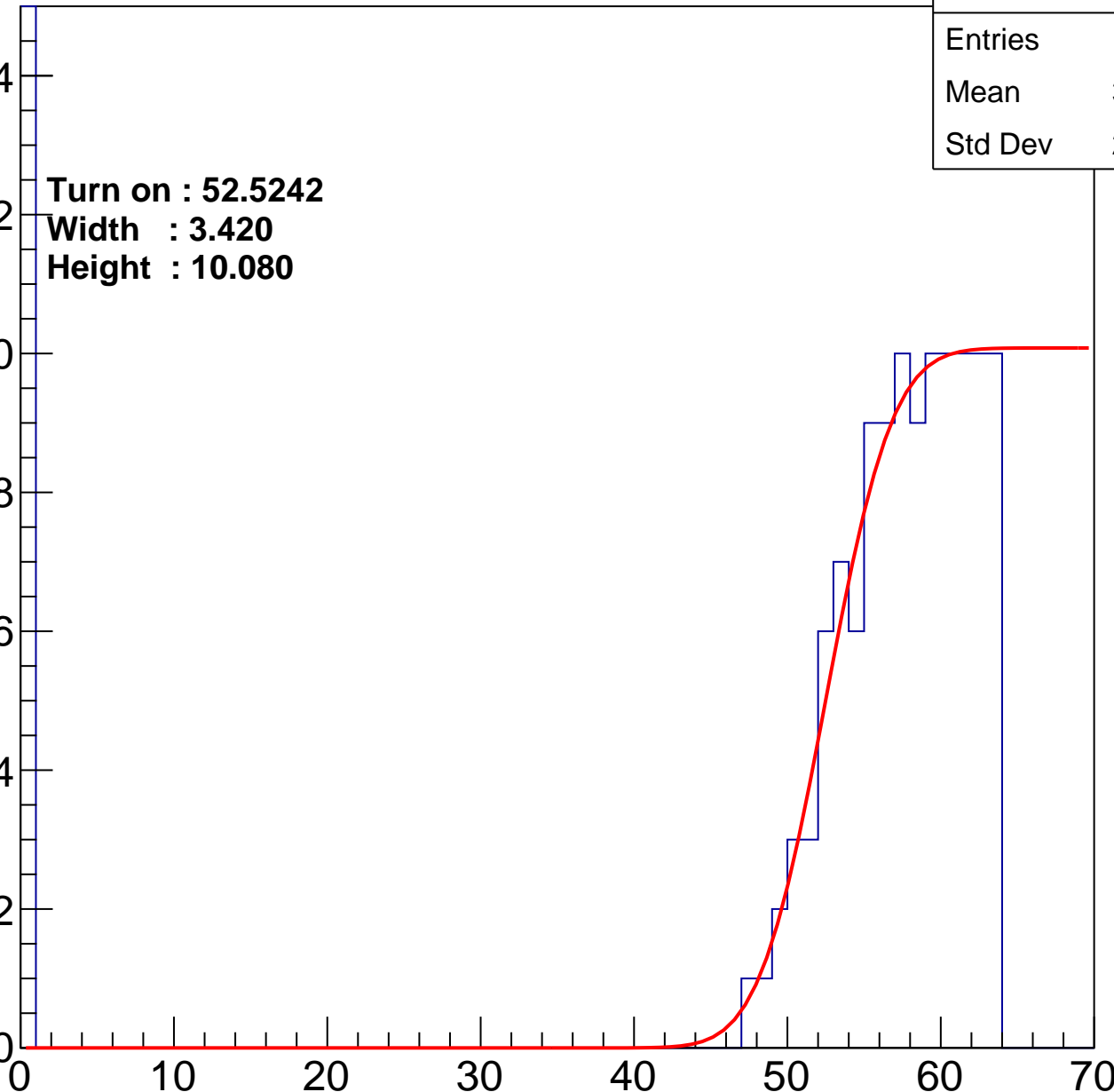
Width : 3.420

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	35.6
Std Dev	27.27

Turn on : 51.1574

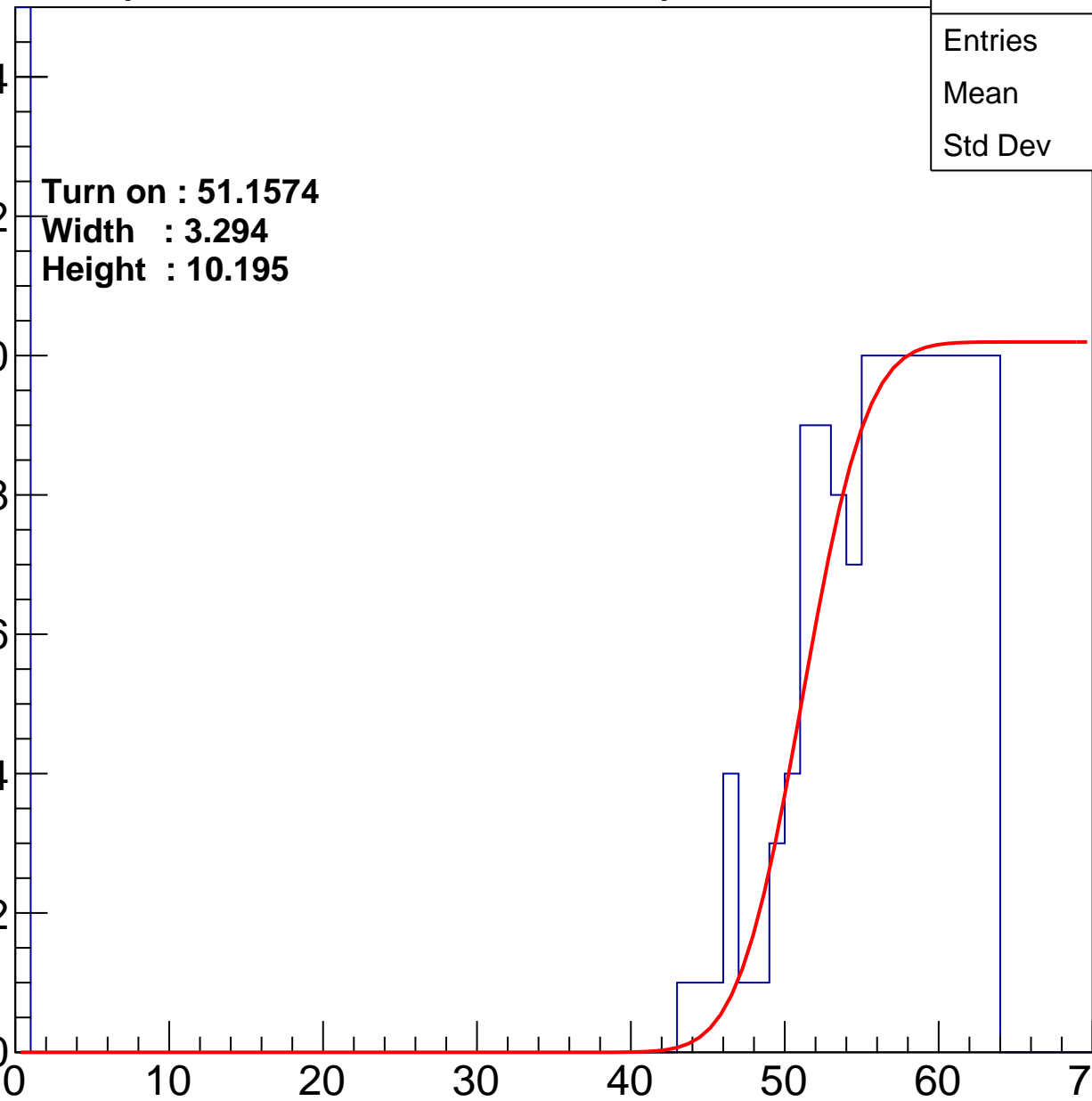
Width : 3.294

Height : 10.195

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.87
Std Dev	28.36

Turn on : 53.2242

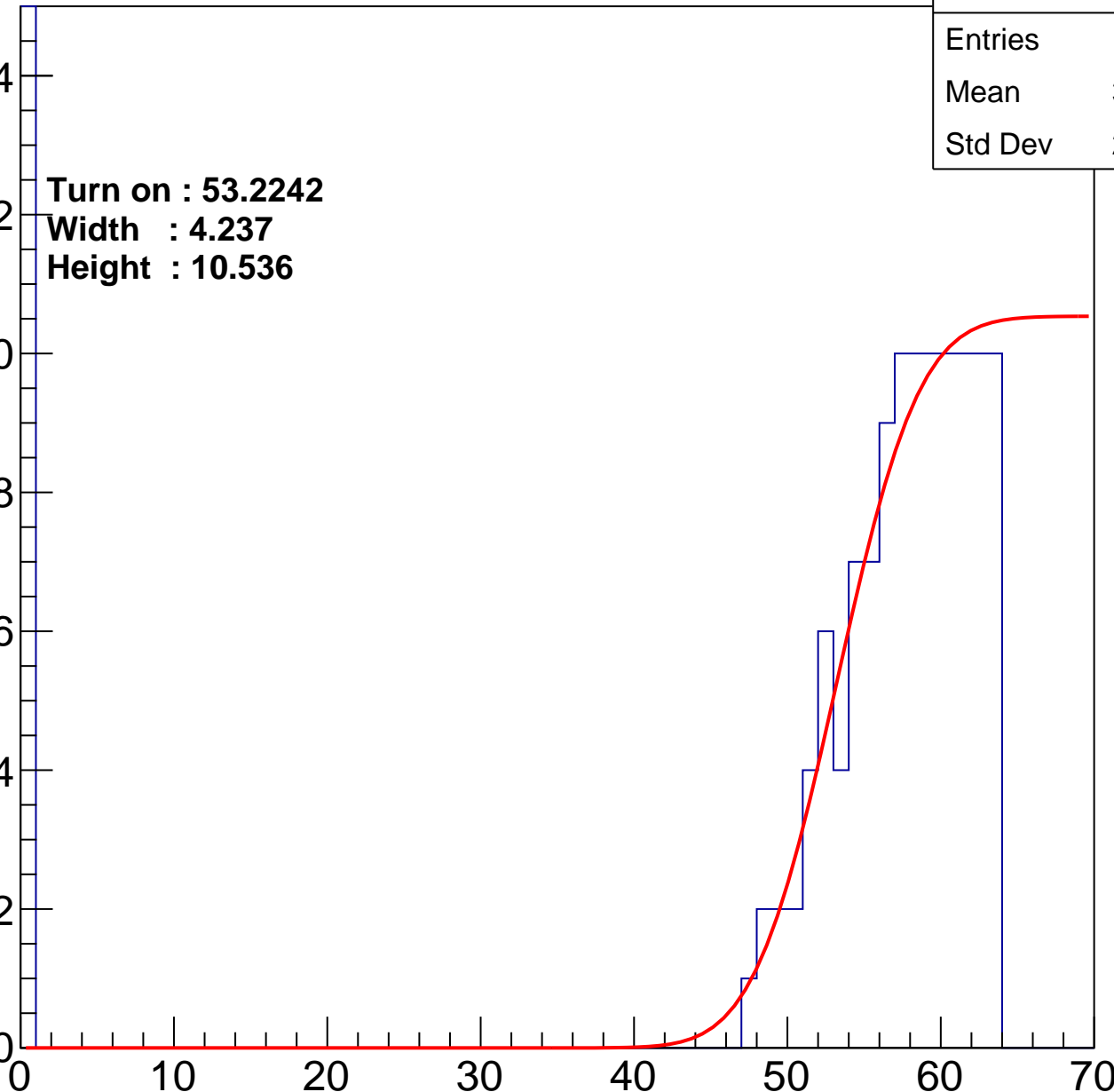
Width : 4.237

Height : 10.536

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	31.11
Std Dev	28.56

Turn on : 52.3856

Width : 3.206

Height : 9.991

Entry

14

12

10

8

6

4

2

0

0

10

20

30

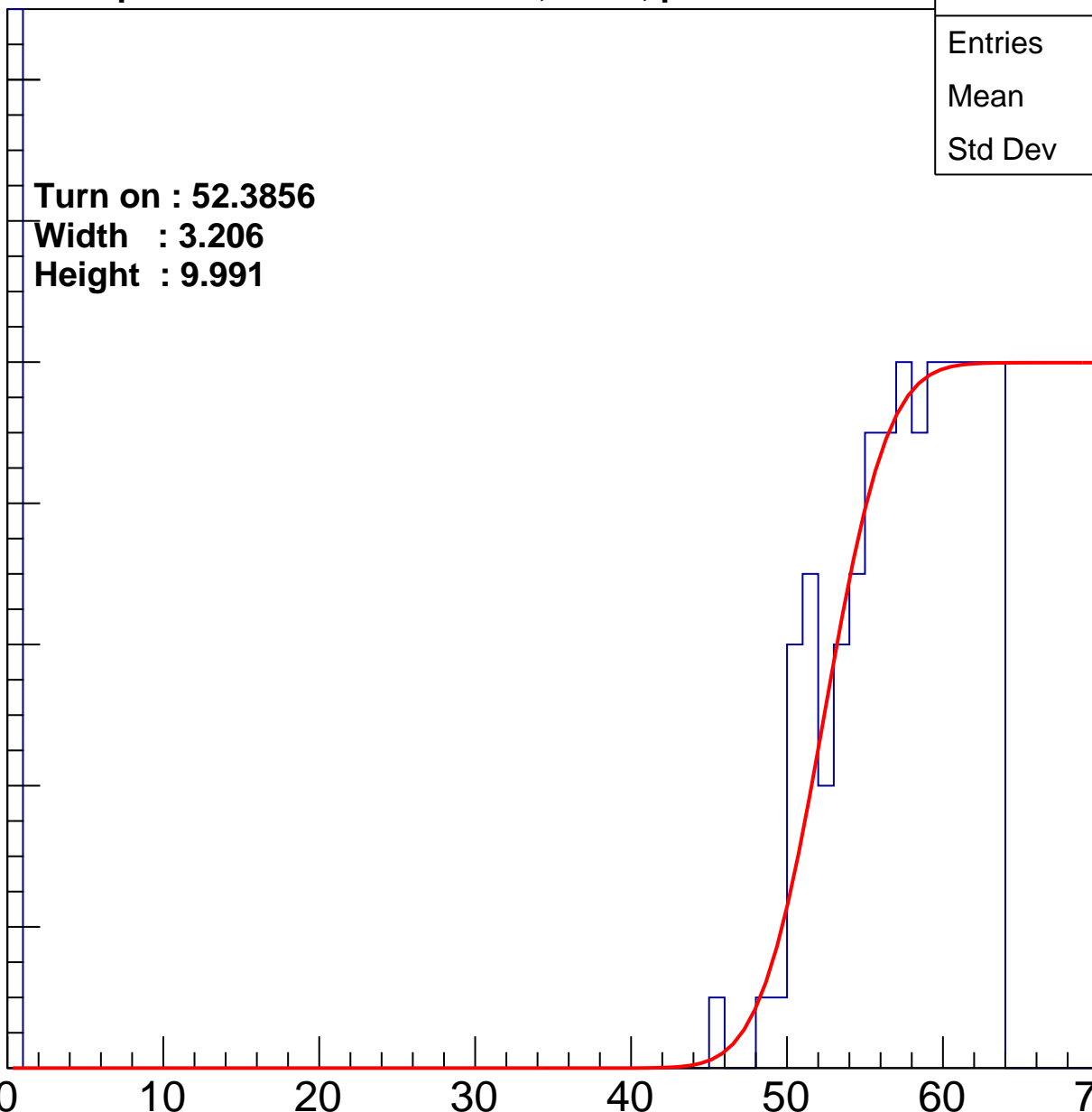
40

50

60

70

ampl



B1L104S, U14-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	30.59
Std Dev	28.69

Turn on : 52.8125

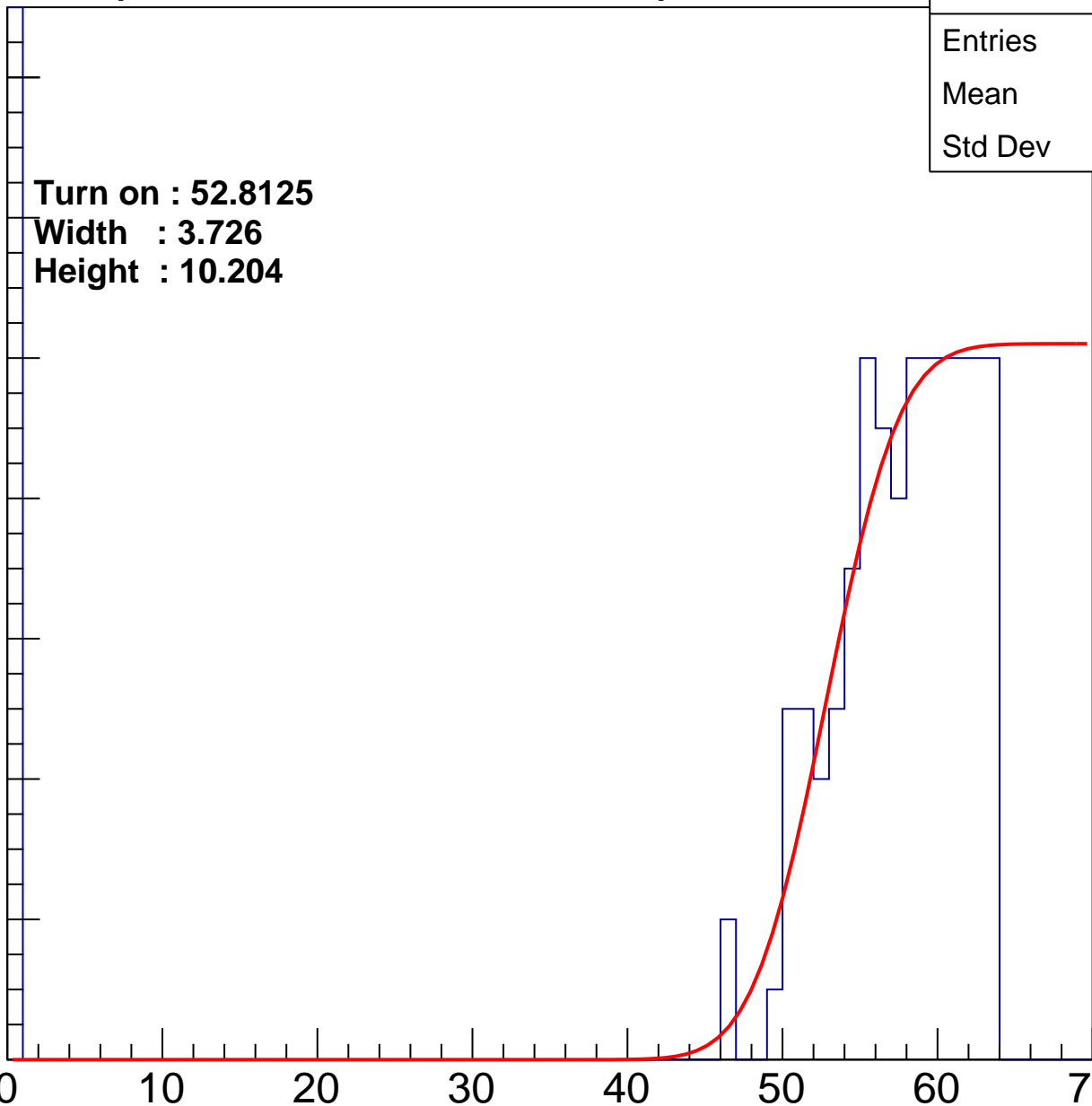
Width : 3.726

Height : 10.204

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U14-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	30.59
Std Dev	28.69

Turn on : 52.8125

Width : 3.726

Height : 10.204

Entry

14
12
10
8
6
4
2
0

ampl

