

B1L102S, U25-ch0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.26
Std Dev	11.78

Turn on : 27.0252

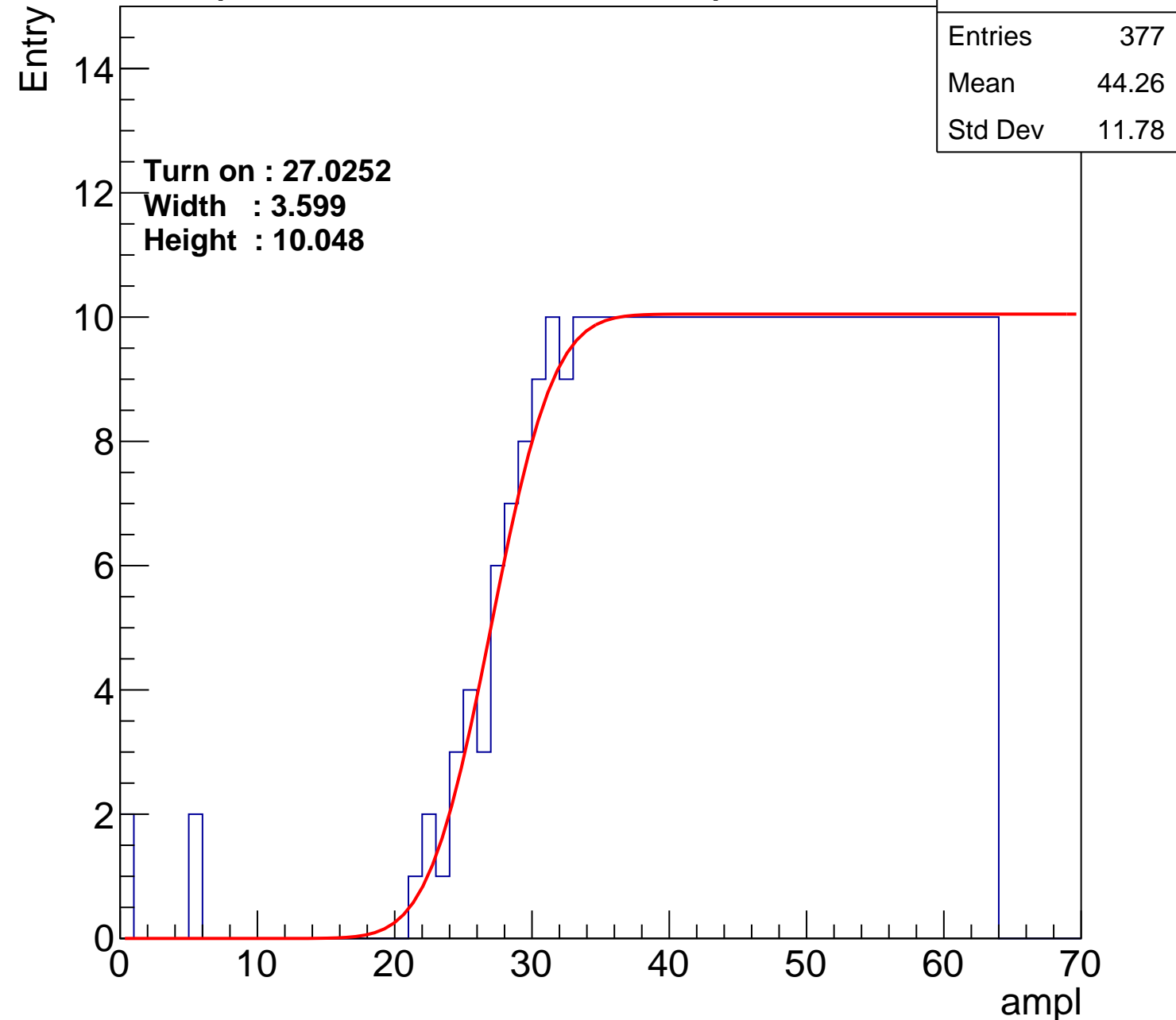
Width : 3.599

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 25.5987

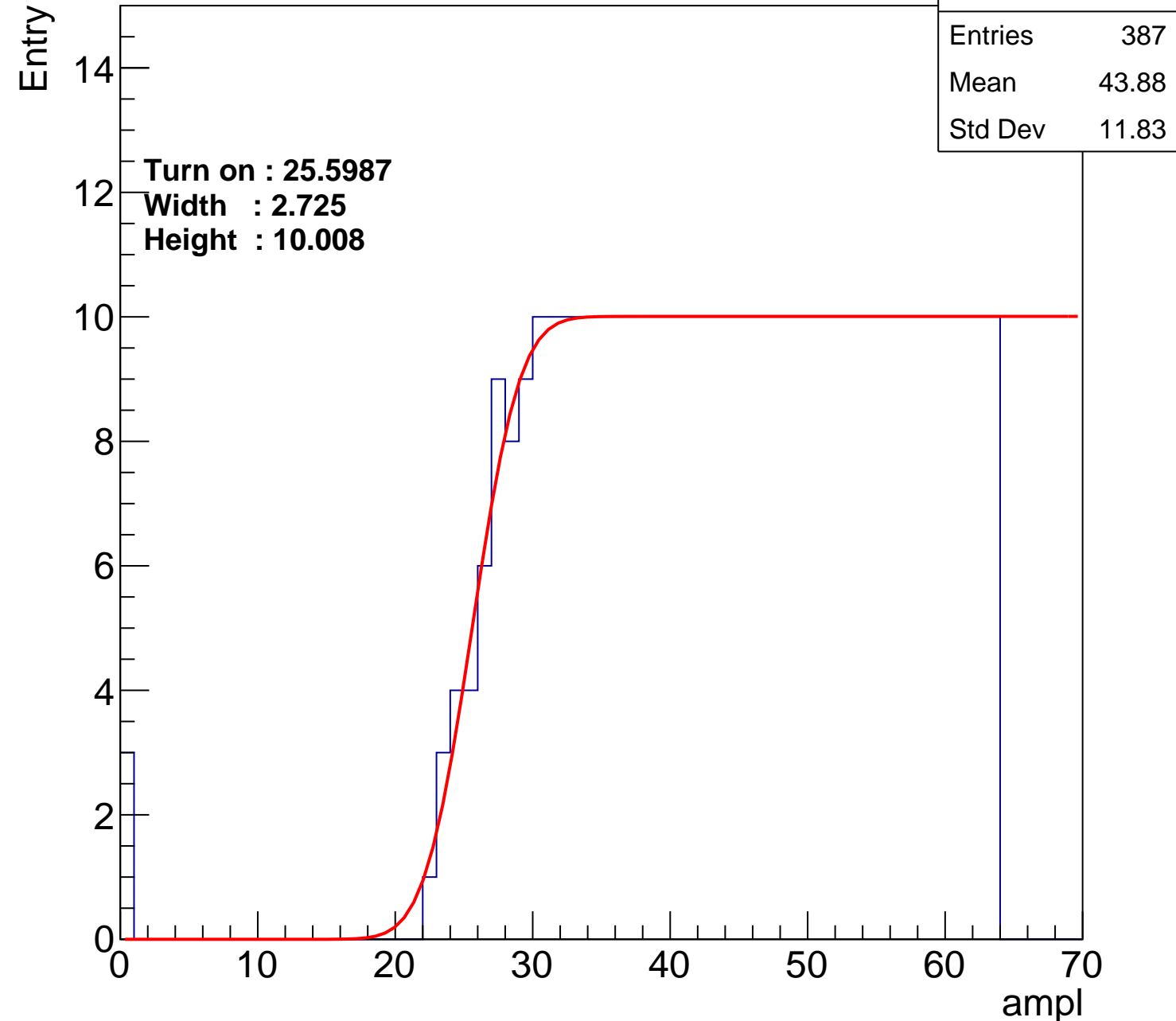
Width : 2.725

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.96
Std Dev	11.81

Turn on : 25.5043

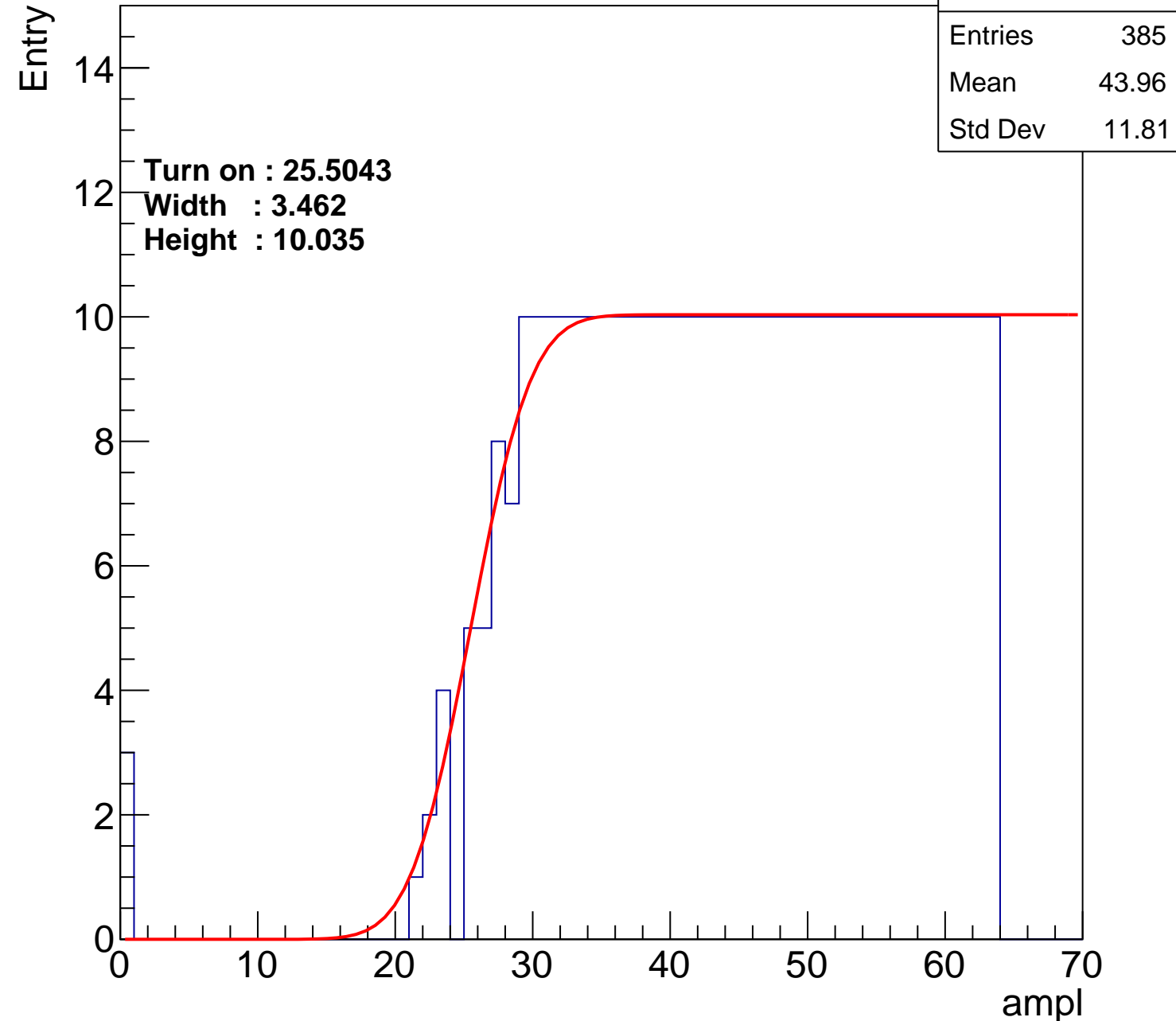
Width : 3.462

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.67
Std Dev	11.66

Turn on : 25.7494

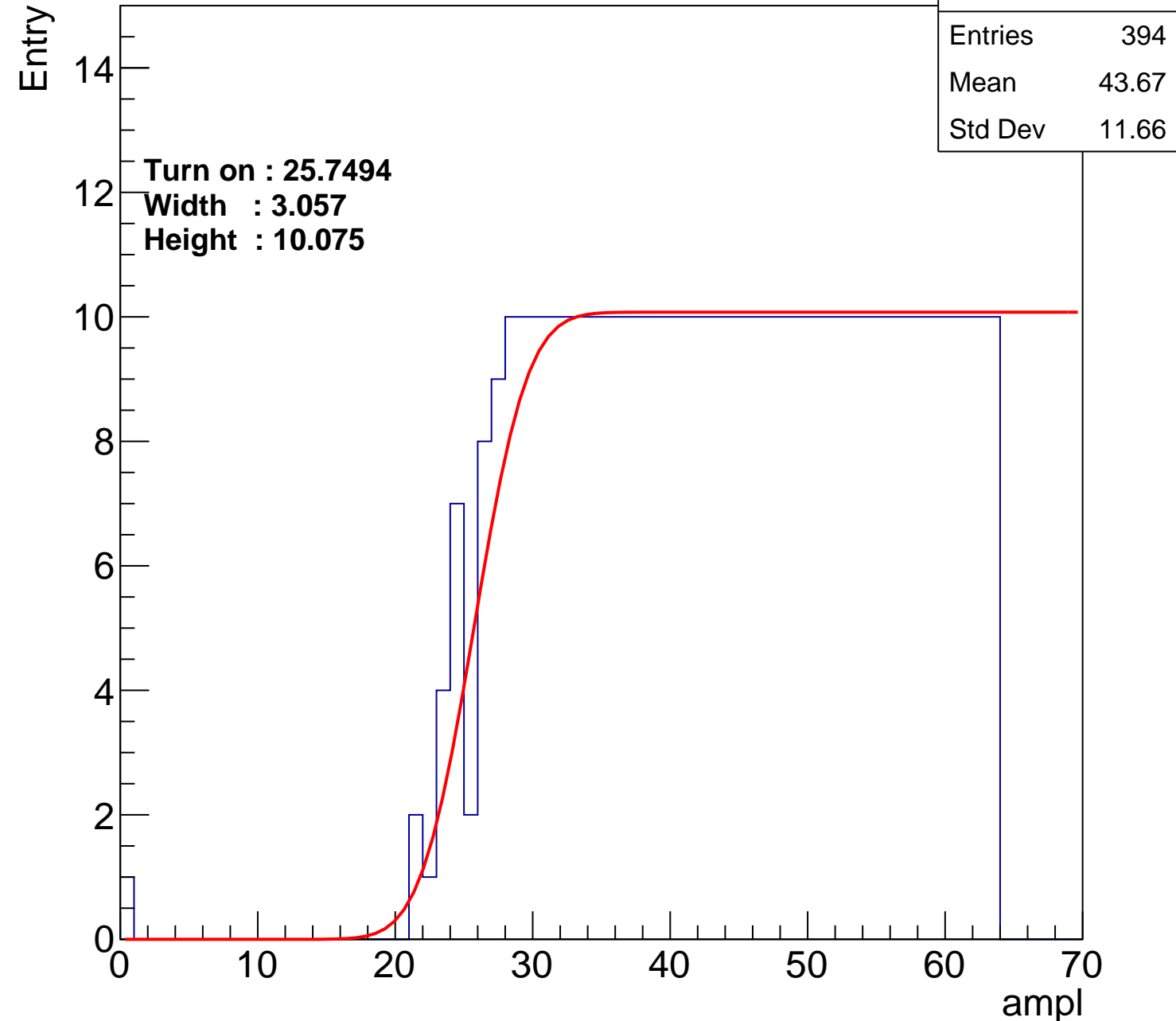
Width : 3.057

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.54
Std Dev	11.52

Turn on : 26.8588

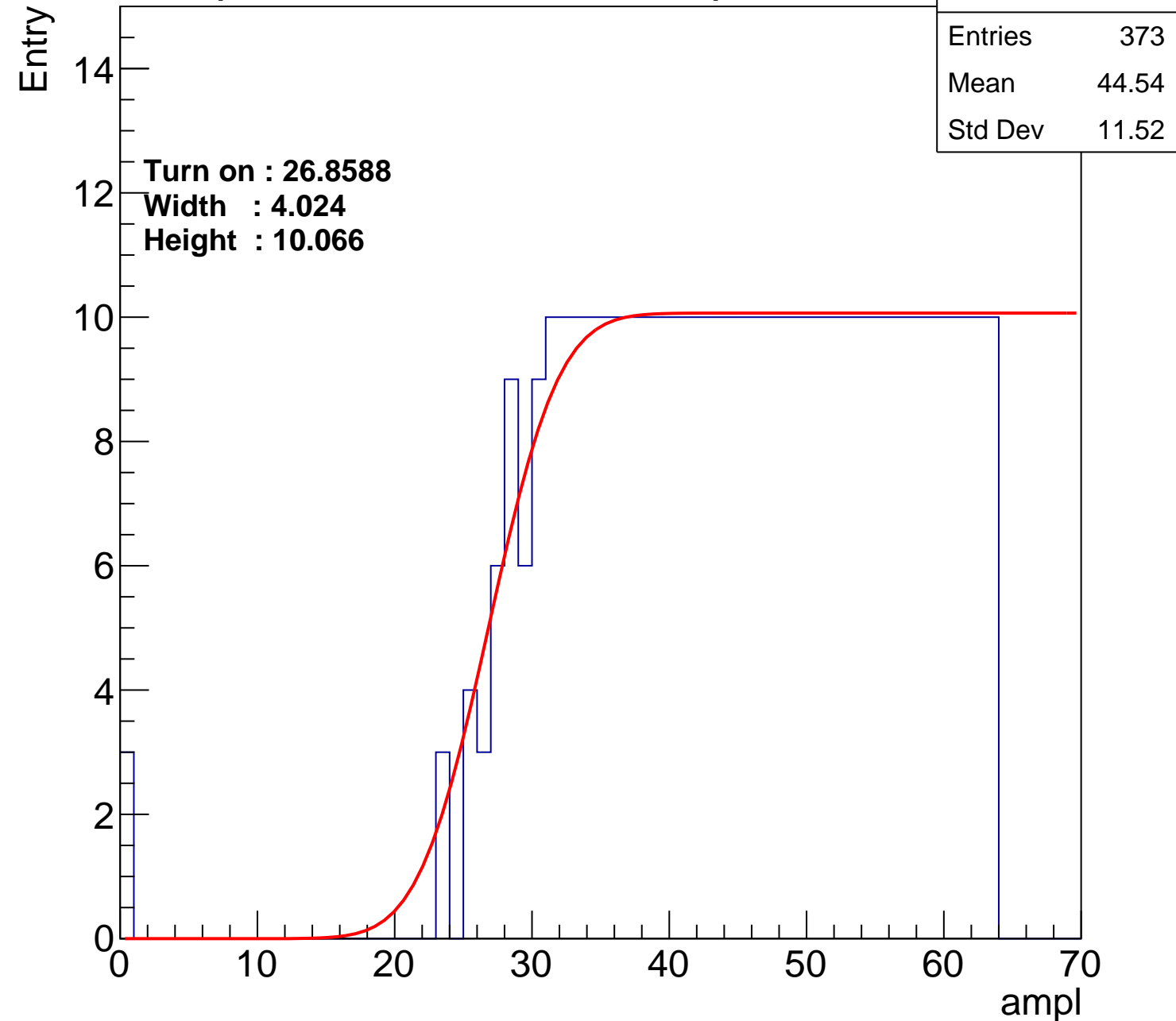
Width : 4.024

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	44.04
Std Dev	11.6

Turn on : 25.7114

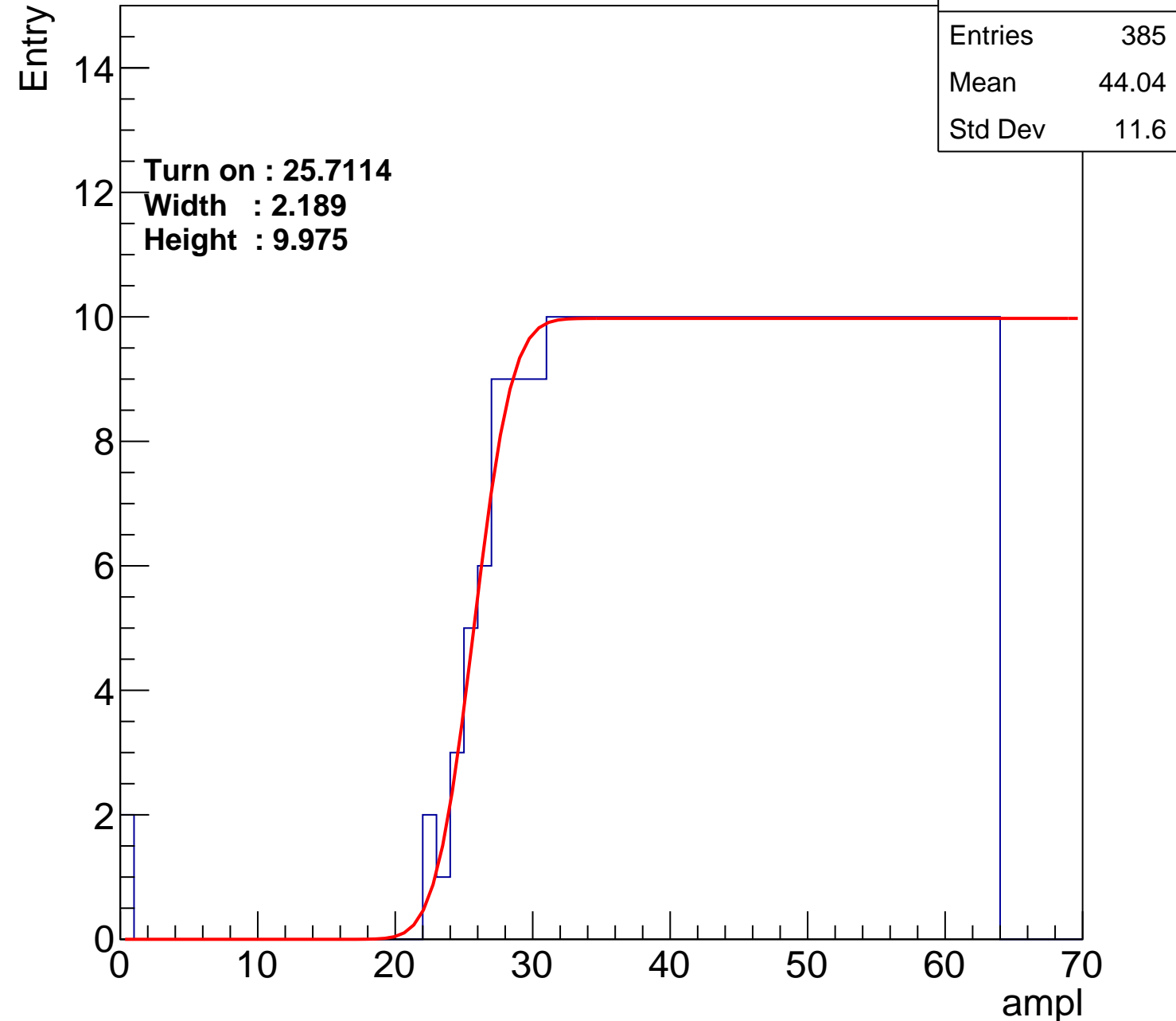
Width : 2.189

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.43
Std Dev	11.46

Turn on : 26.9763

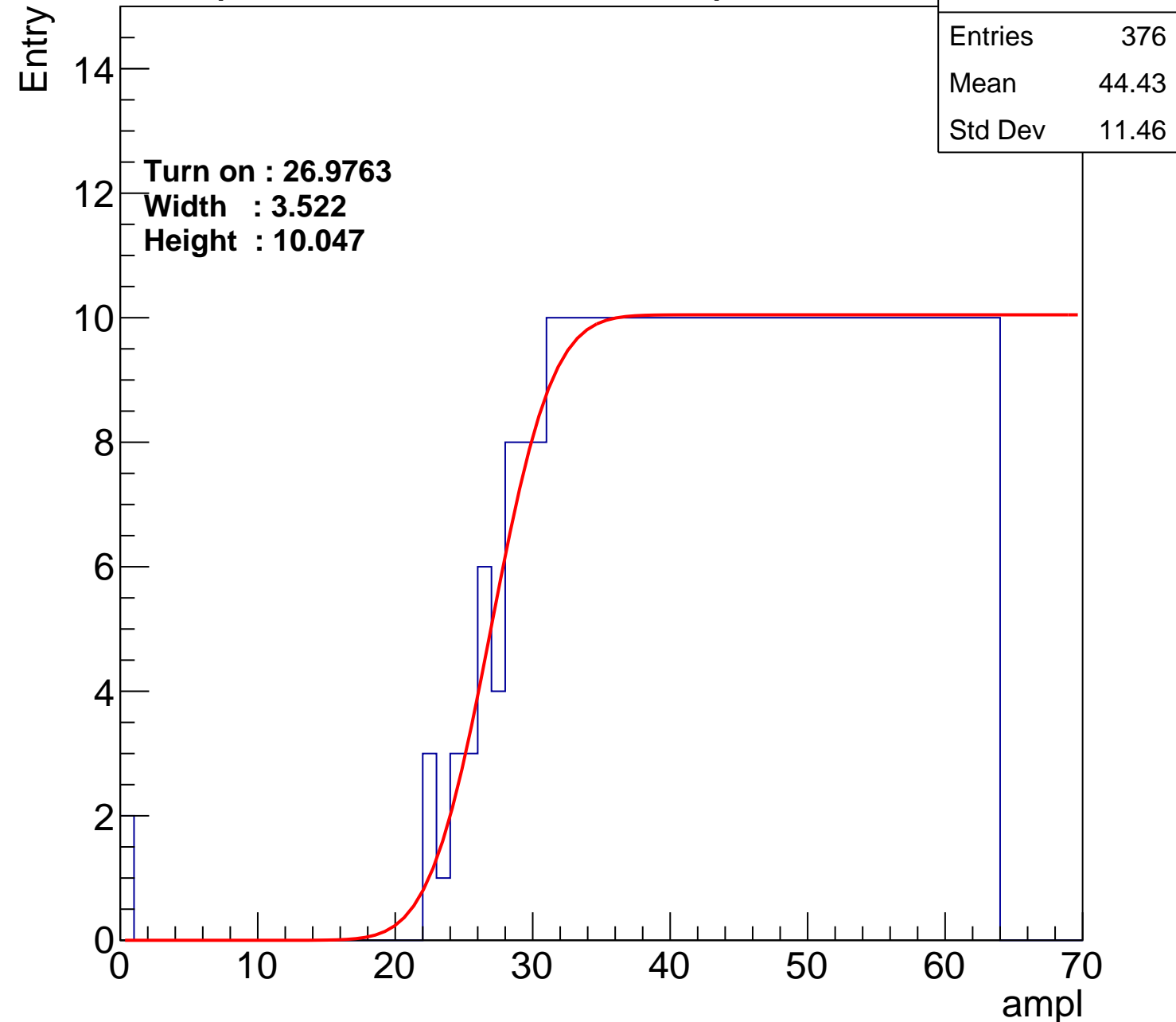
Width : 3.522

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch7

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.8689

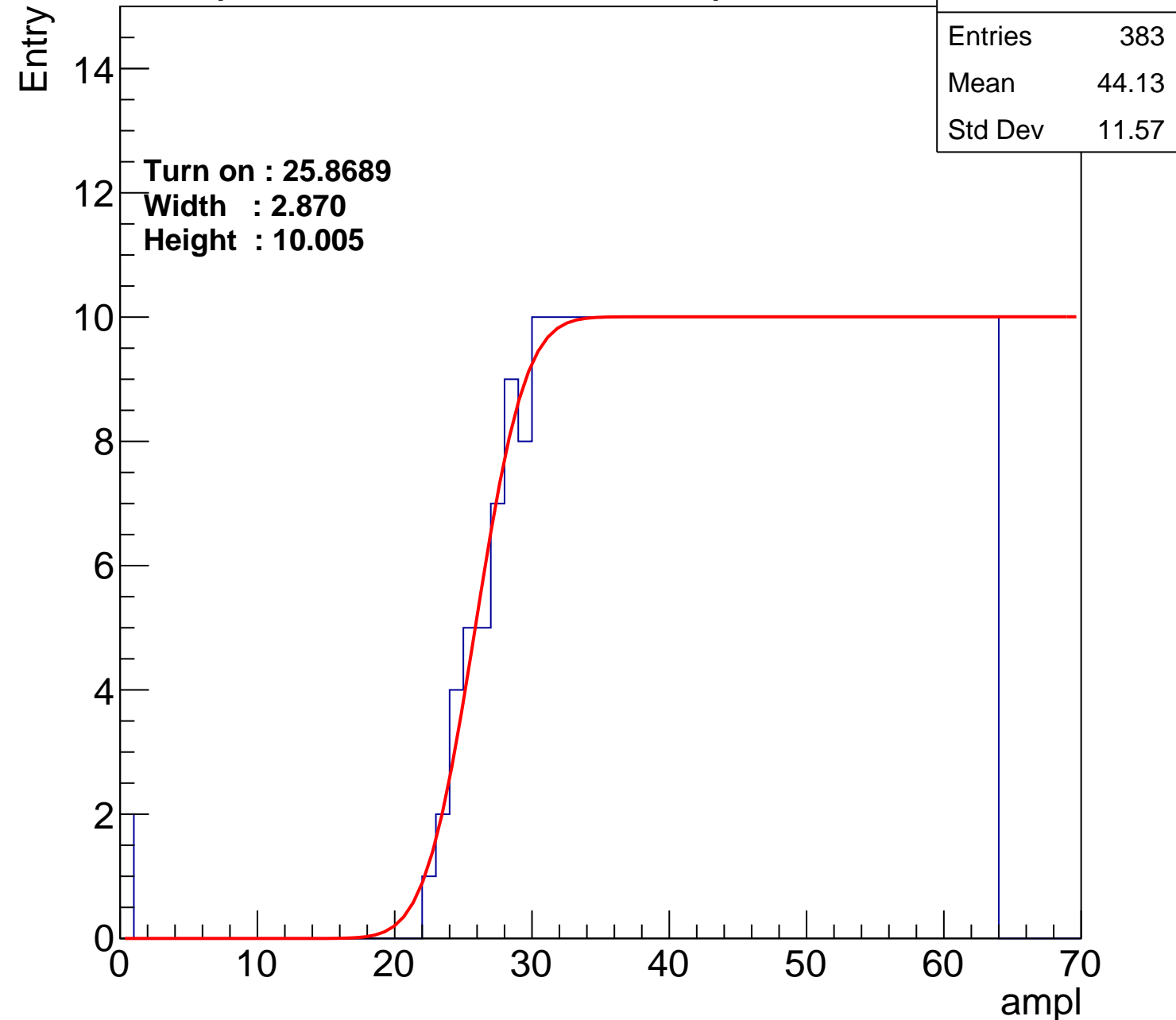
Width : 2.870

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 26.1096

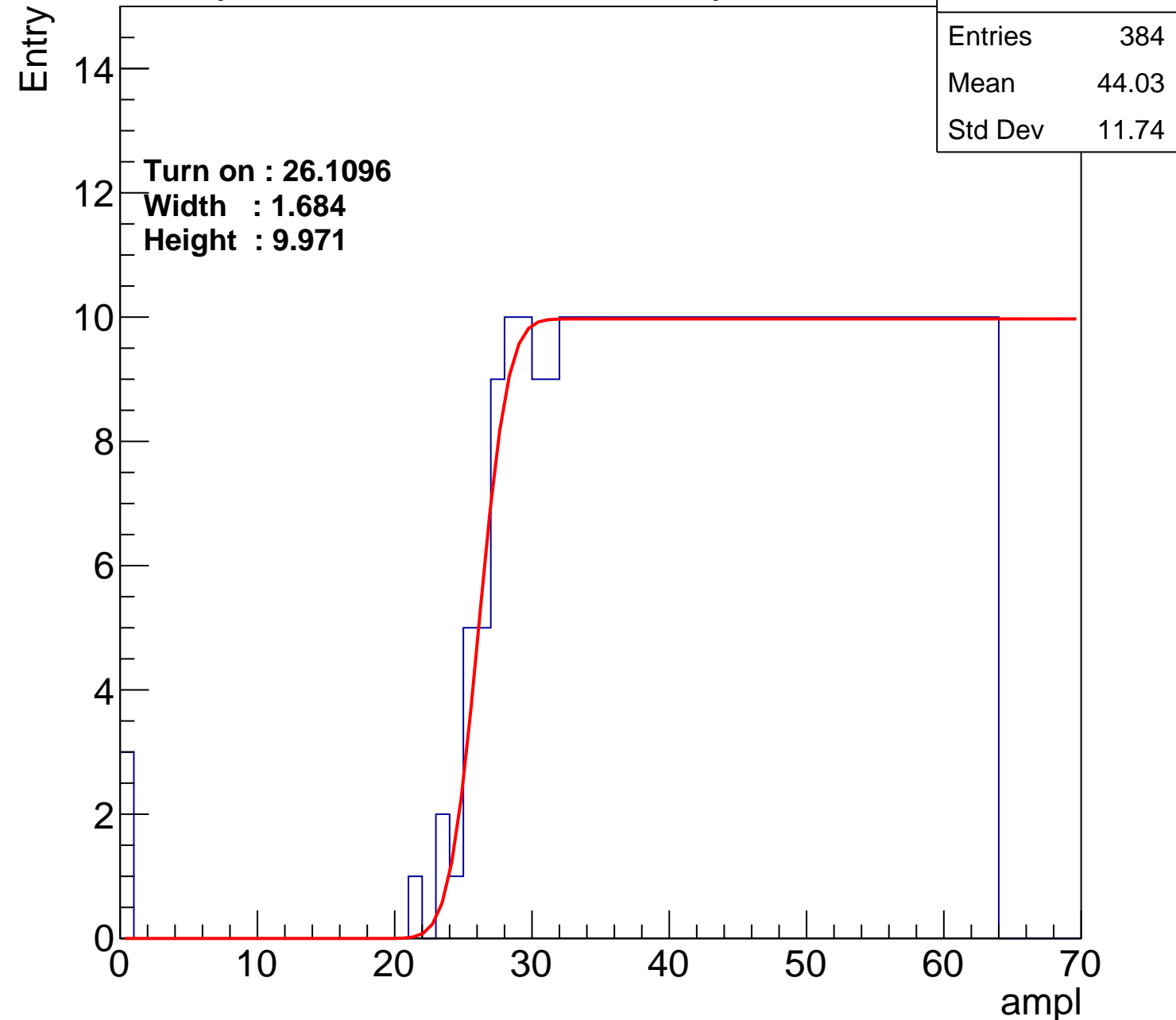
Width : 1.684

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch9

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.48
Std Dev	11.69

Turn on : 26.9944

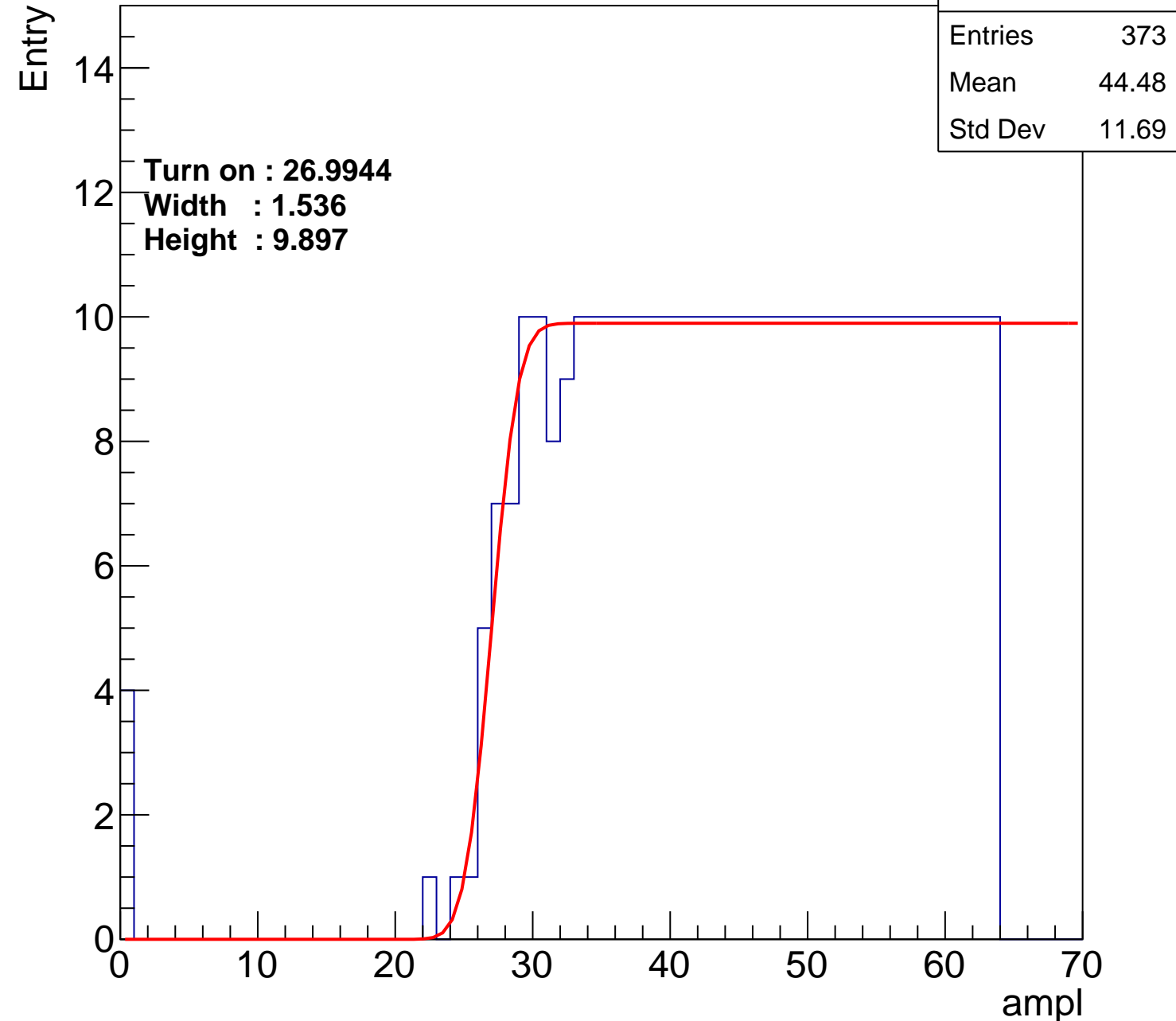
Width : 1.536

Height : 9.897

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch10

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 27.0760

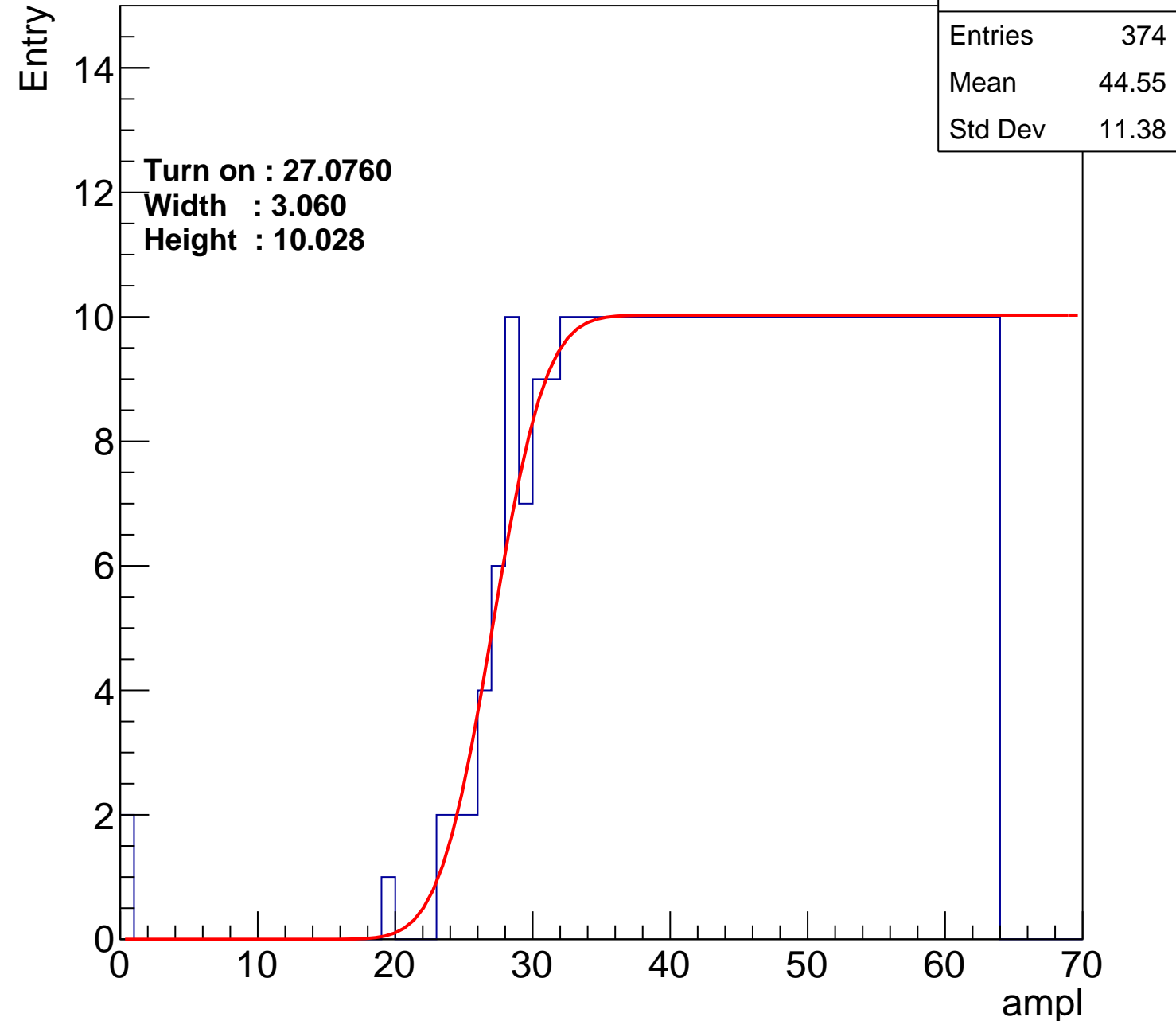
Width : 3.060

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch11

calib_packv5_042523_0143.root, FC#11, port A2

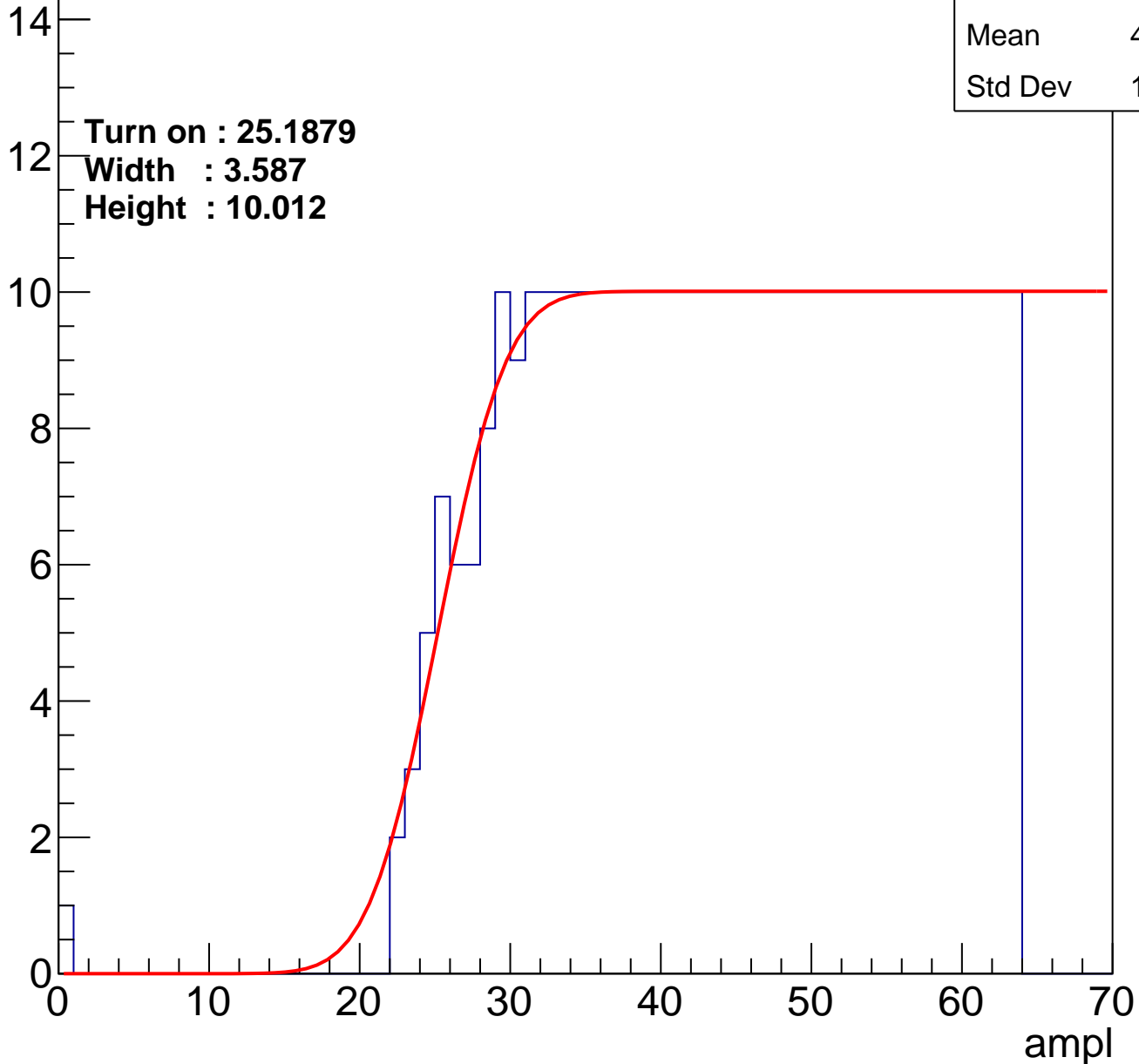
Entries	387
Mean	43.98
Std Dev	11.52

Turn on : 25.1879

Width : 3.587

Height : 10.012

Entry



B1L102S, U25-ch12

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 25.3816

Width : 3.064

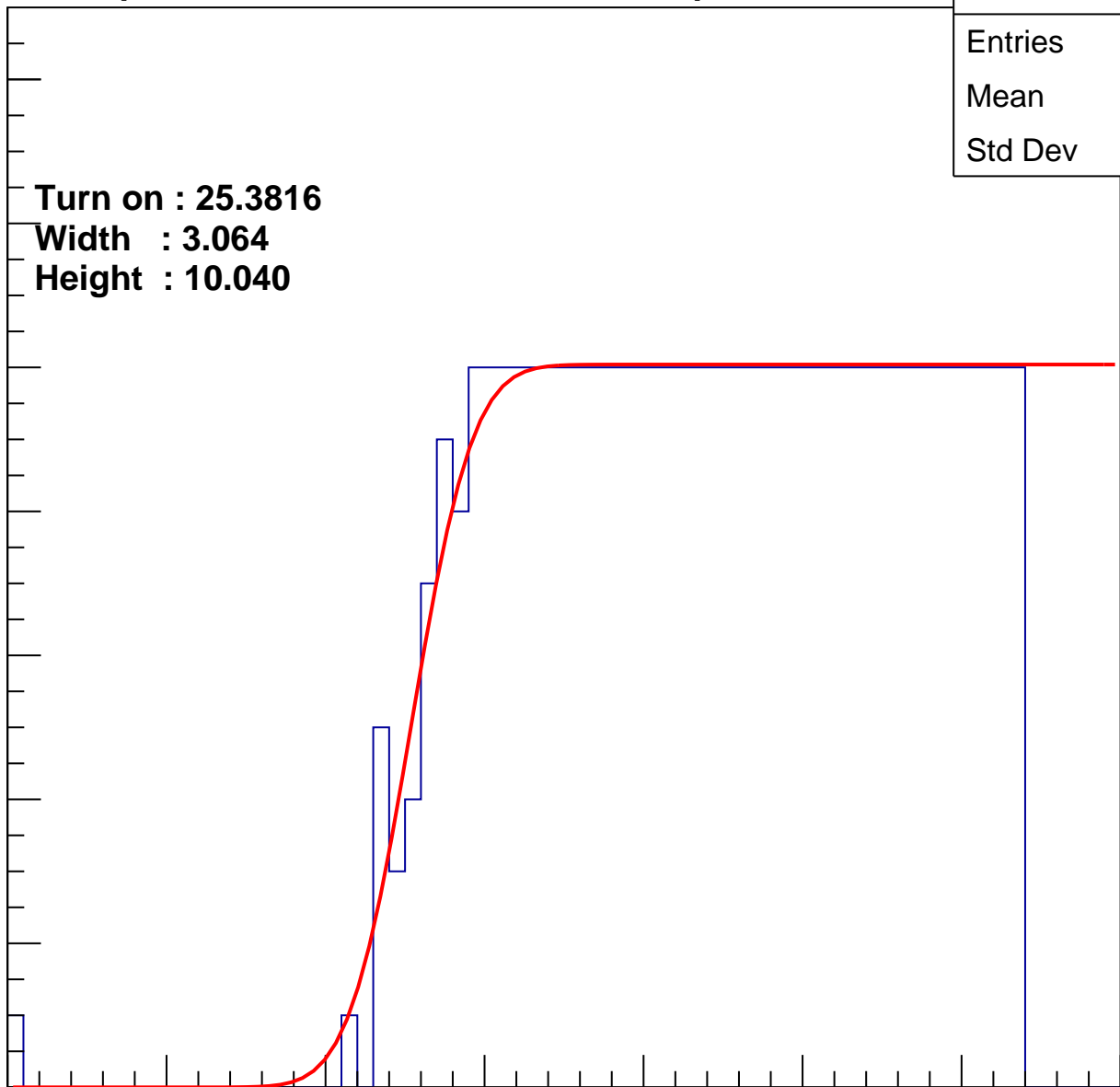
Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U25-ch13

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.8
Std Dev	11.25

Turn on : 27.8603

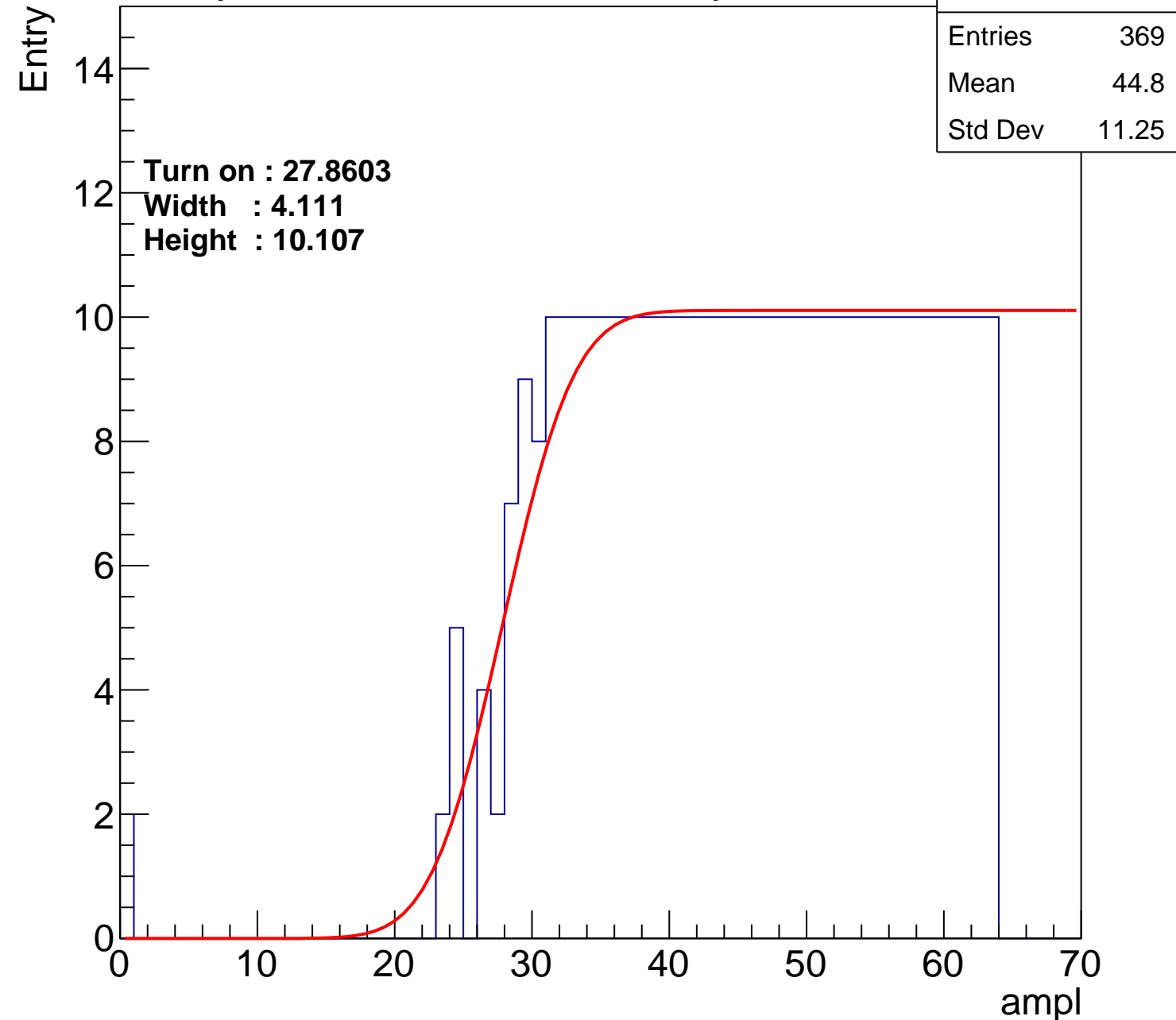
Width : 4.111

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch14

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.38
Std Dev	12.1

Turn on : 24.9093

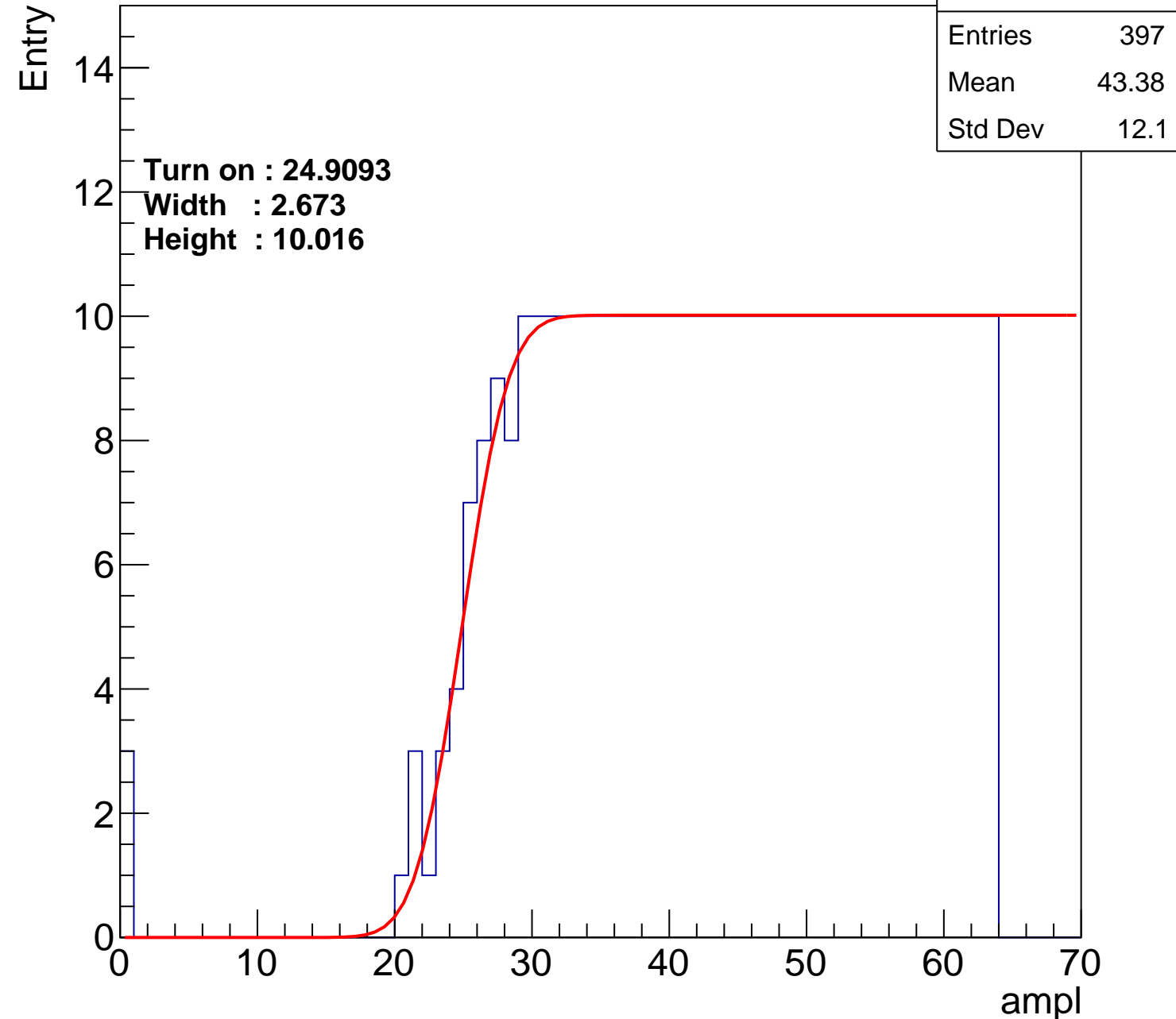
Width : 2.673

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch15

calib_packv5_042523_0143.root, FC#11, port A2

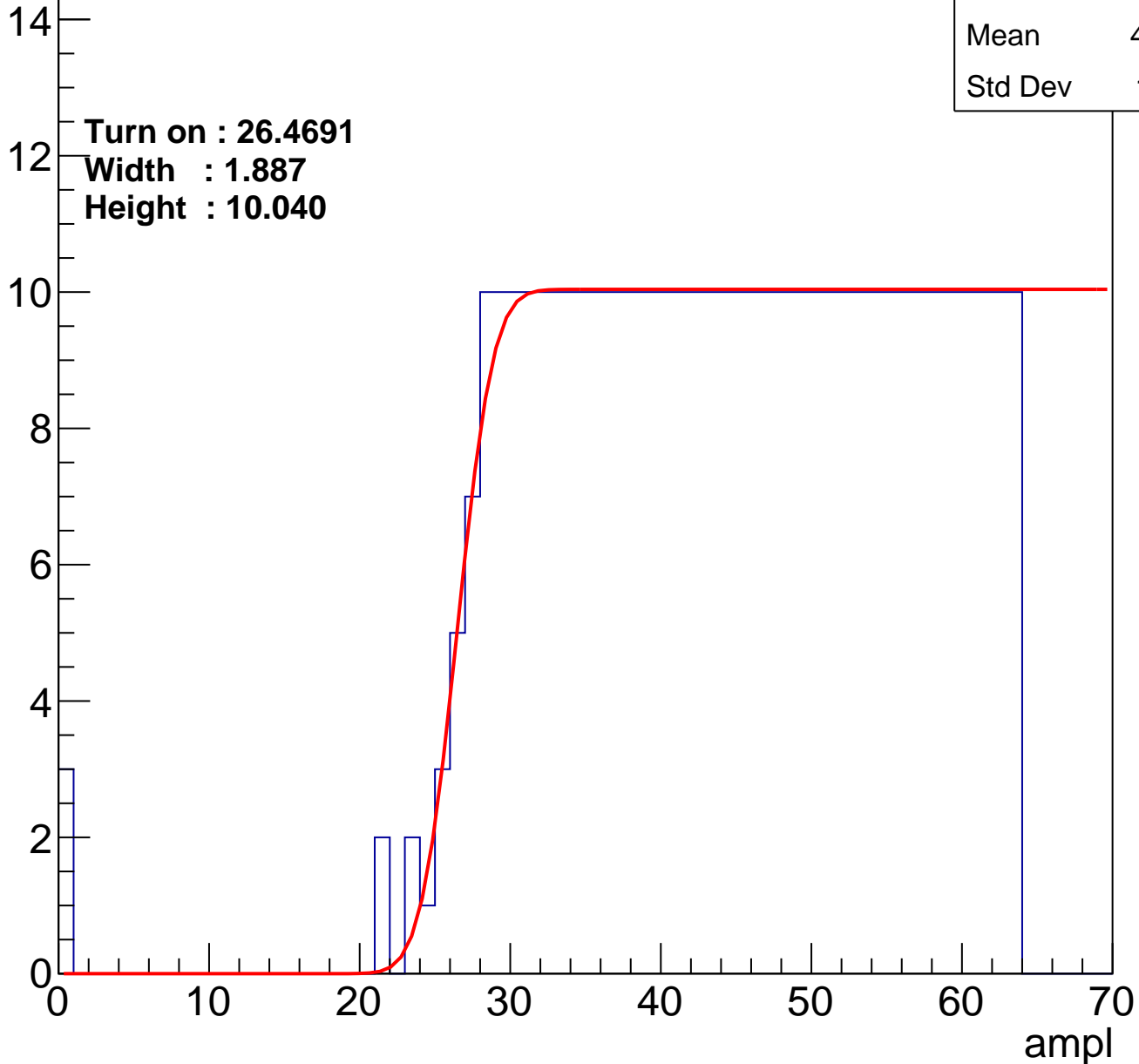
Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 26.4691

Width : 1.887

Height : 10.040

Entry



B1L102S, U25-ch16

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.21
Std Dev	11.72

Turn on : 26.5915

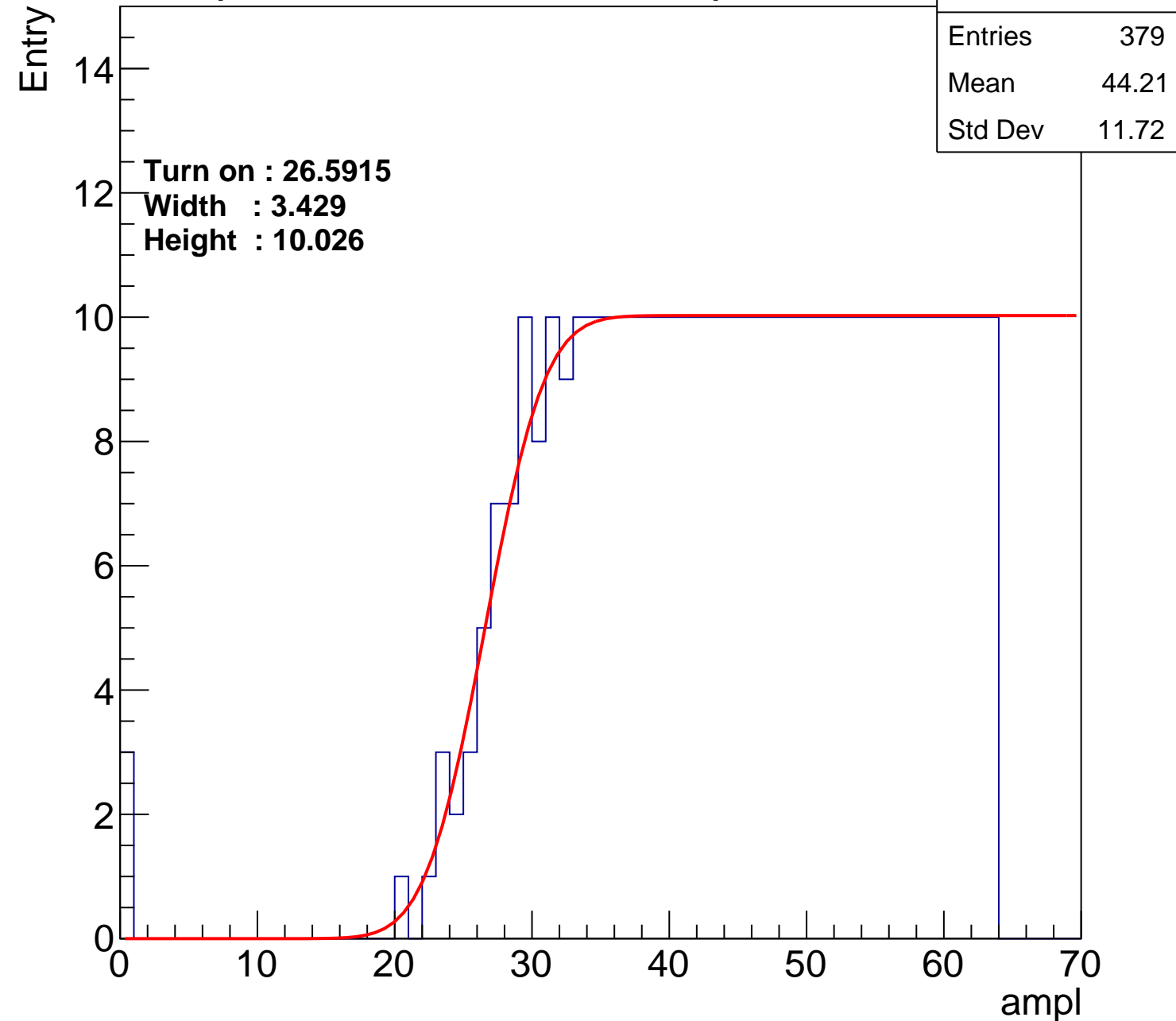
Width : 3.429

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch17

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	43.17
Std Dev	12.21

Turn on : 24.5595

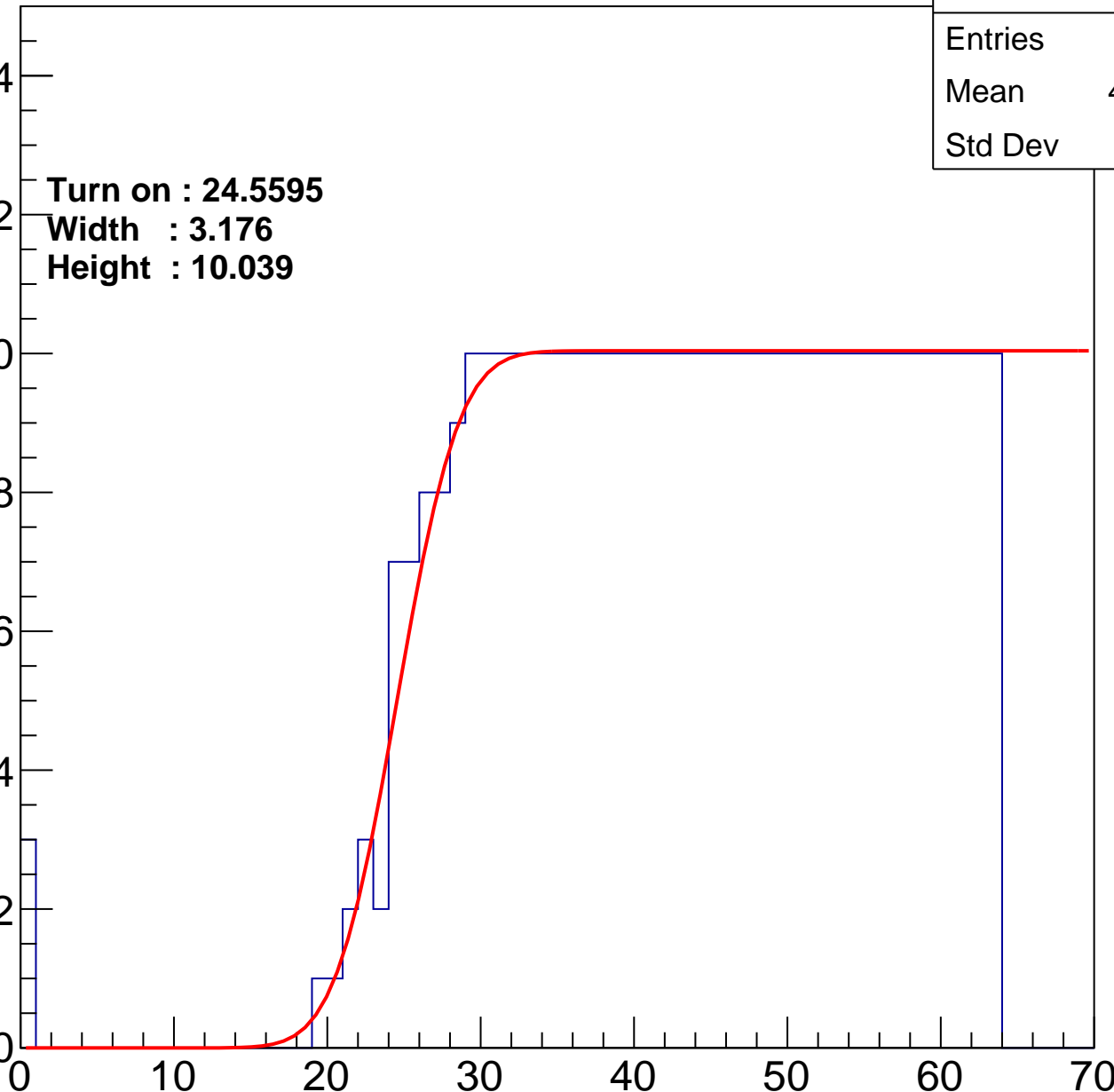
Width : 3.176

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch18

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	45
Std Dev	11.14

Turn on : 27.4751

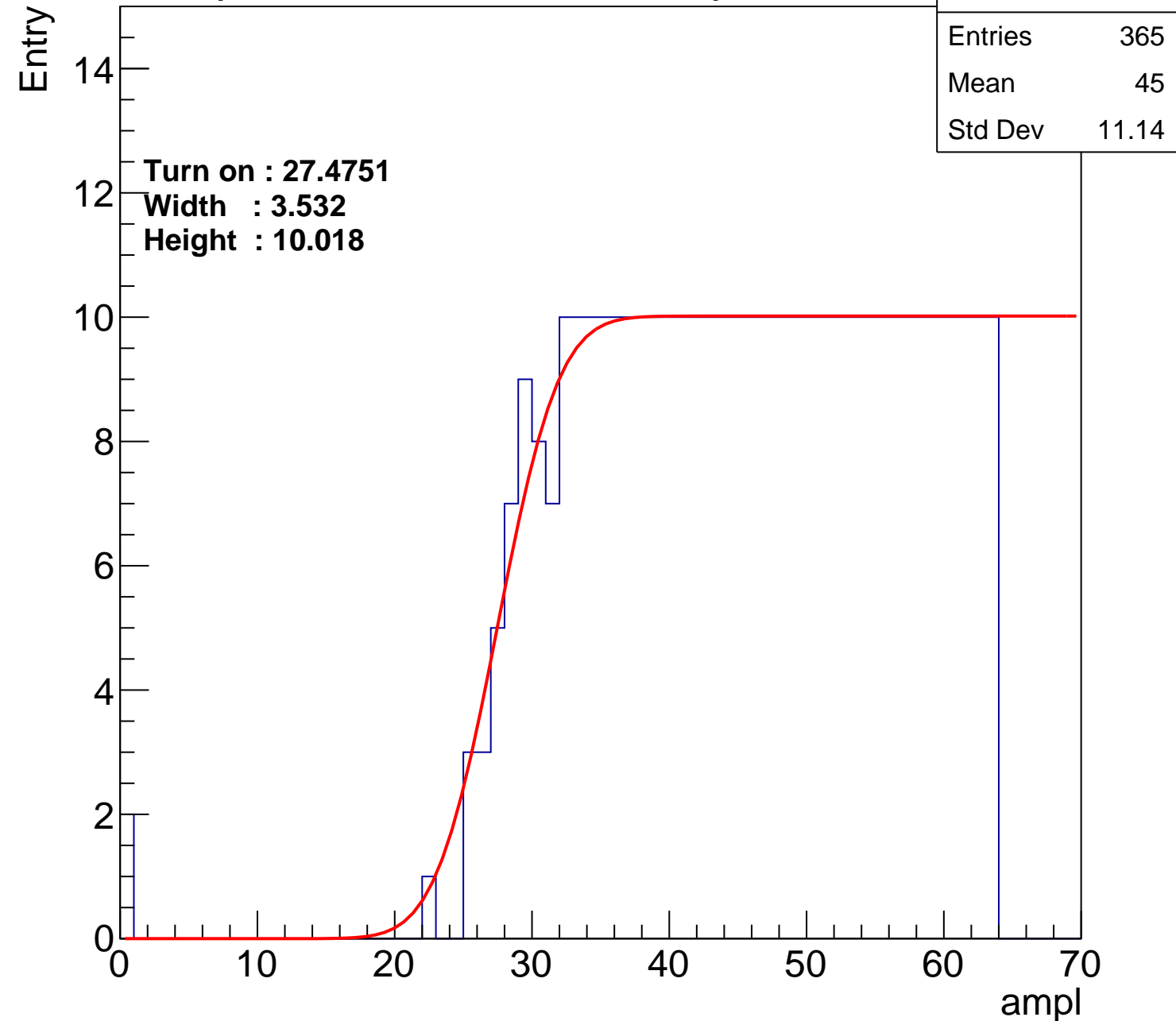
Width : 3.532

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch19

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.42
Std Dev	11.74

Turn on : 27.1250

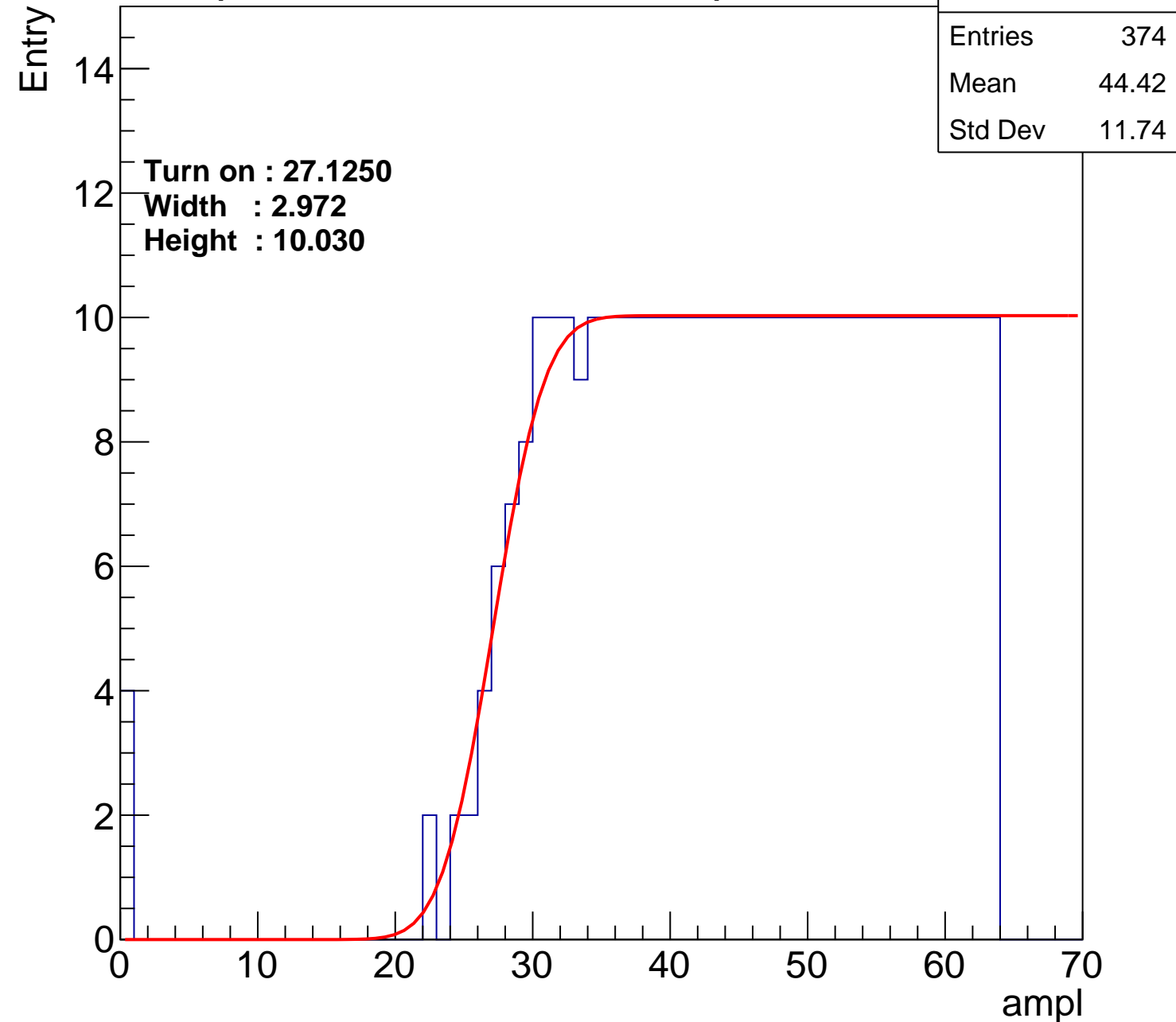
Width : 2.972

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch20

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.22
Std Dev	12.02

Turn on : 27.0036

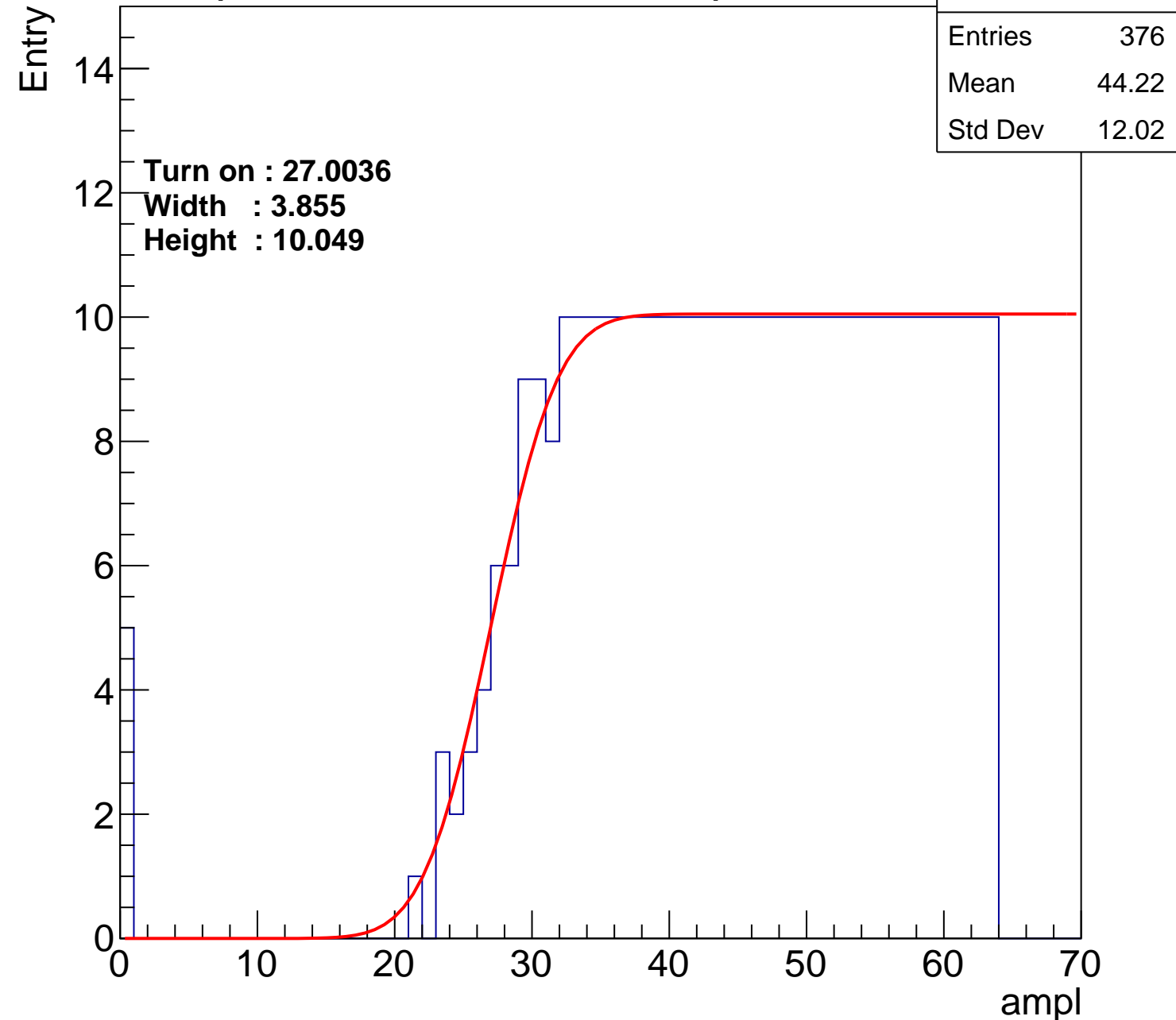
Width : 3.855

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch21

calib_packv5_042523_0143.root, FC#11, port A2

Entries	363
Mean	45.01
Std Dev	11.3

Turn on : 27.6013

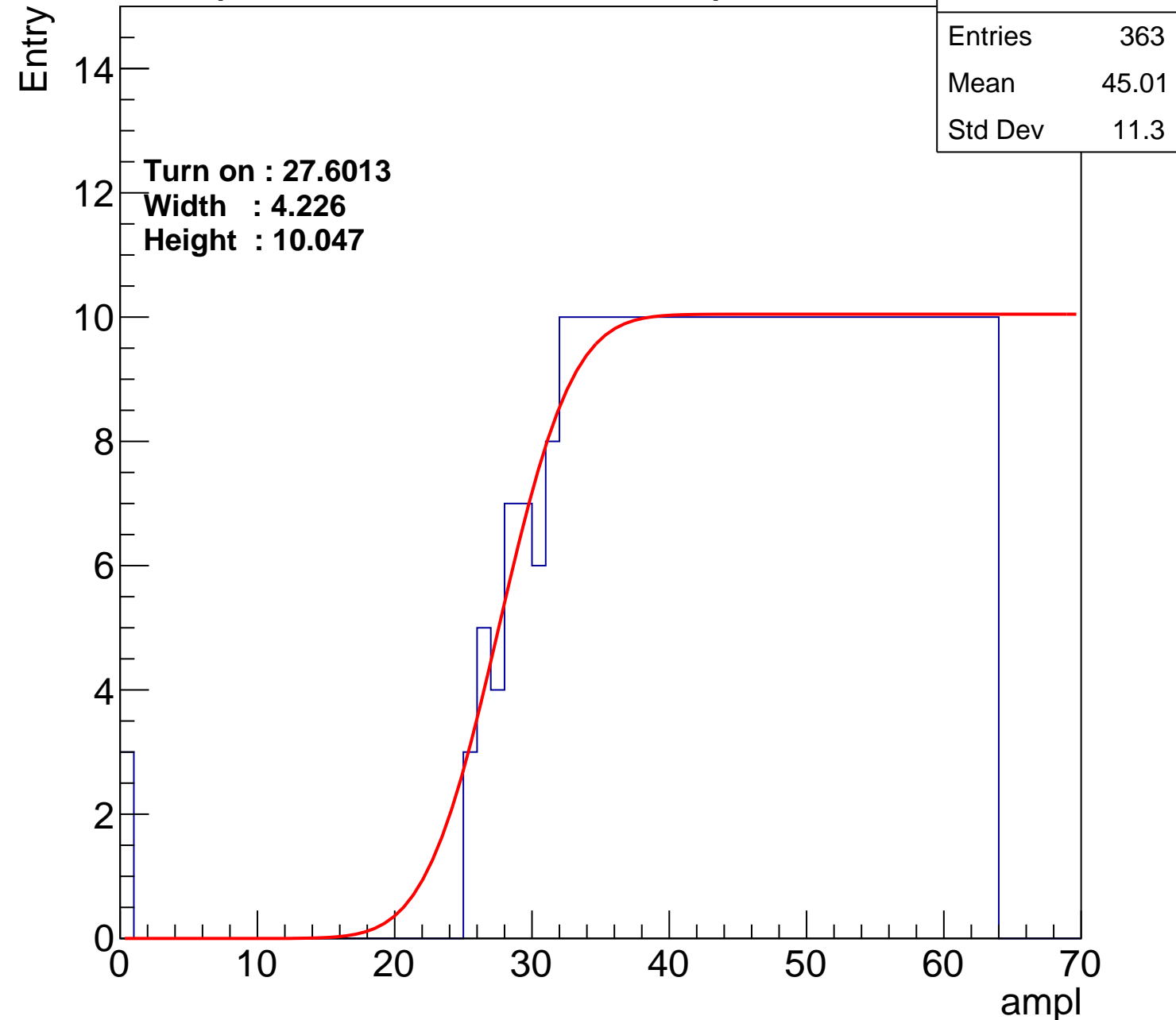
Width : 4.226

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch22

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.25
Std Dev	11.41

Turn on : 26.4131

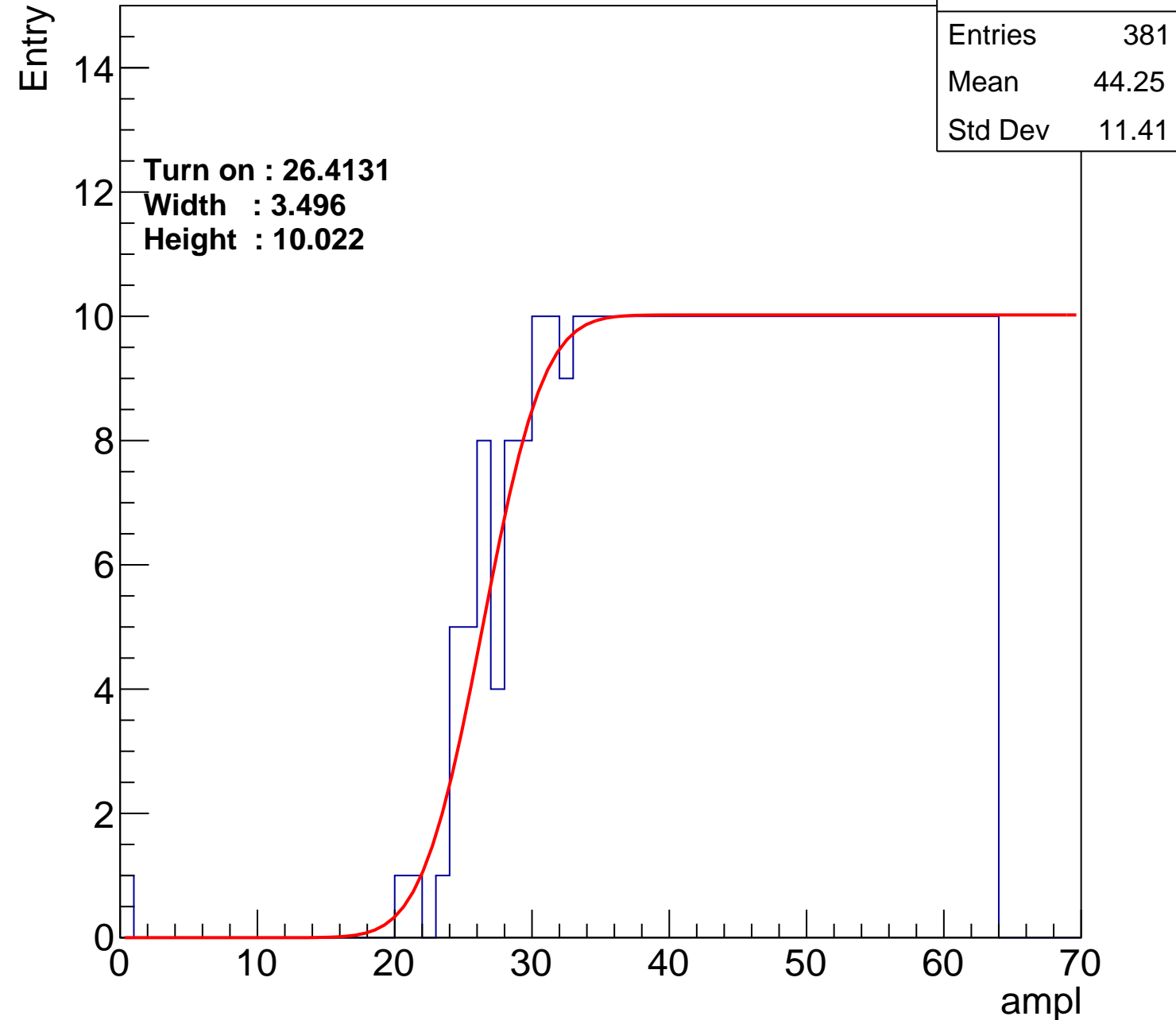
Width : 3.496

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch23

calib_packv5_042523_0143.root, FC#11, port A2

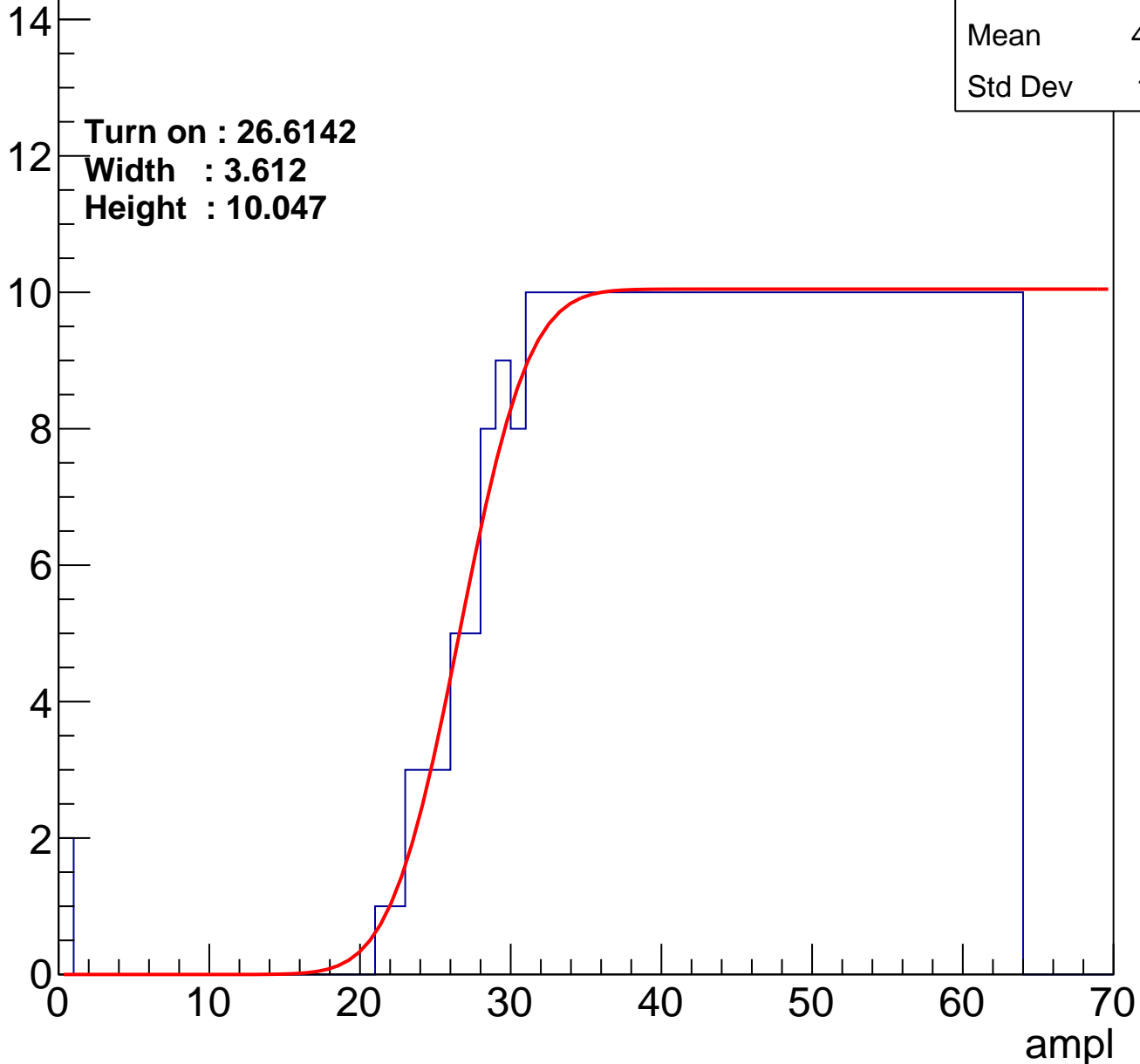
Entries	378
Mean	44.34
Std Dev	11.51

Turn on : 26.6142

Width : 3.612

Height : 10.047

Entry



B1L102S, U25-ch24

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	42.99
Std Dev	12.29

Turn on : 24.8783

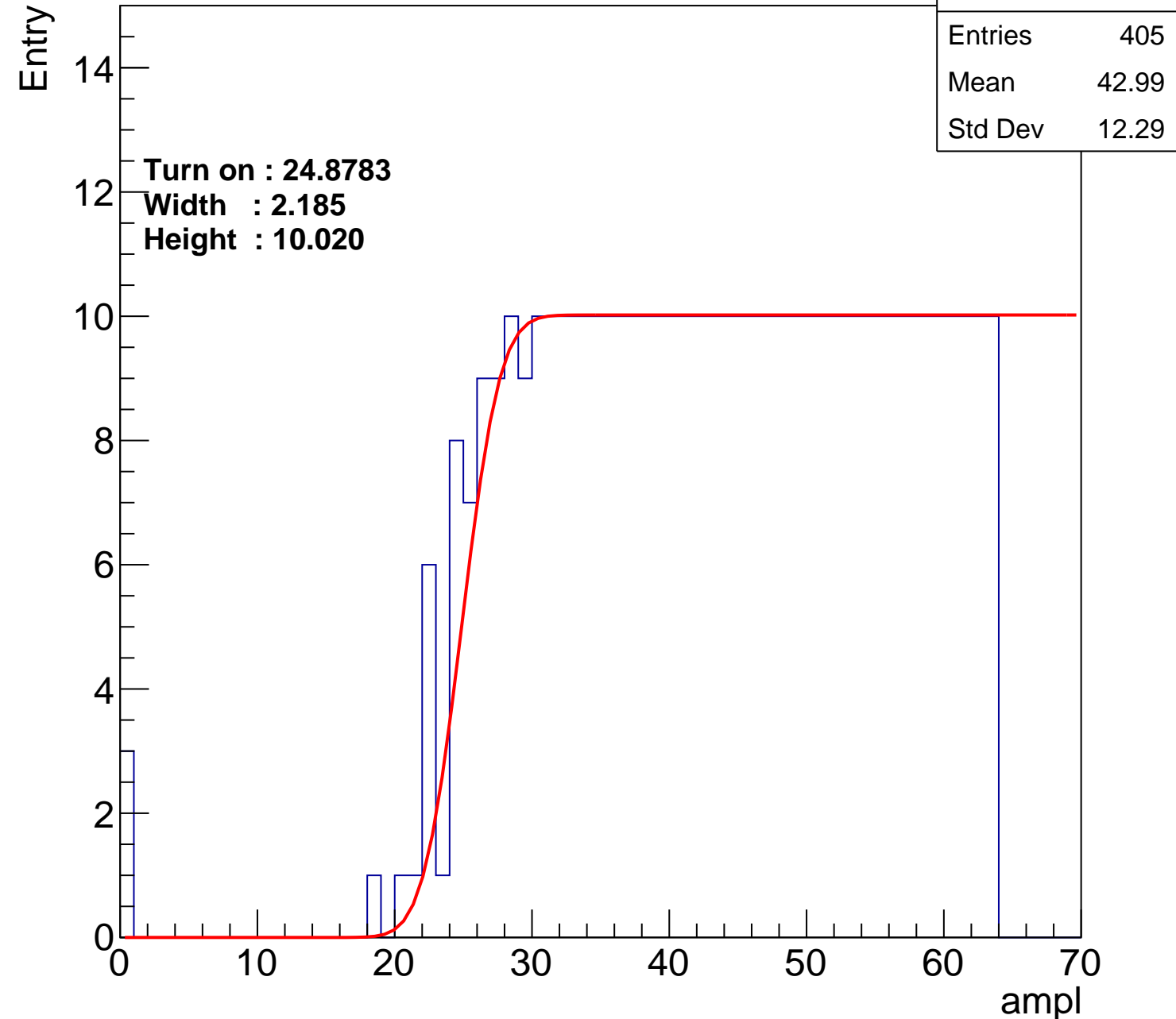
Width : 2.185

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch25

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.84
Std Dev	12

Turn on : 26.0867

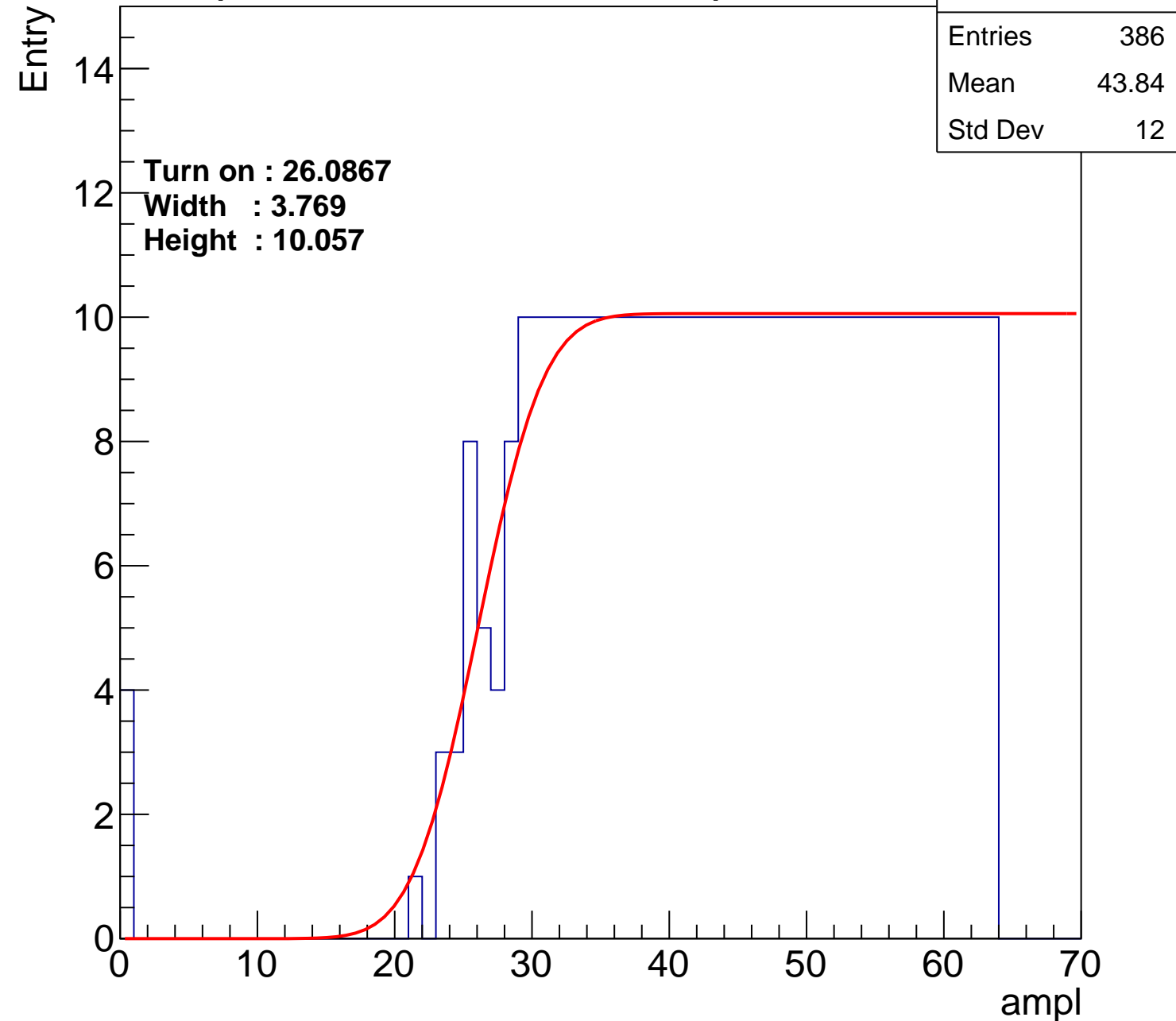
Width : 3.769

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch26

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.63
Std Dev	11.67

Turn on : 27.5946

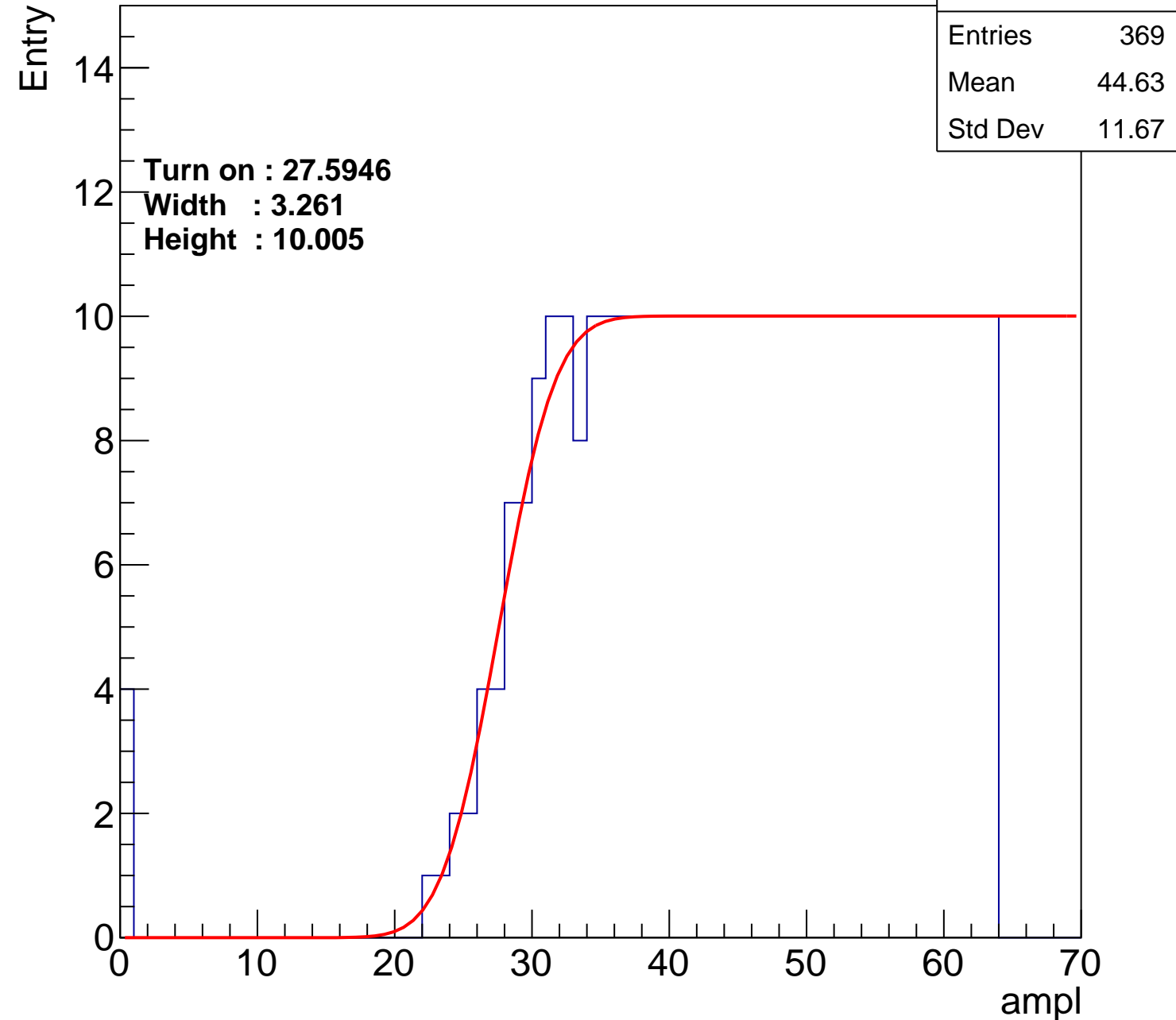
Width : 3.261

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch27

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.7898

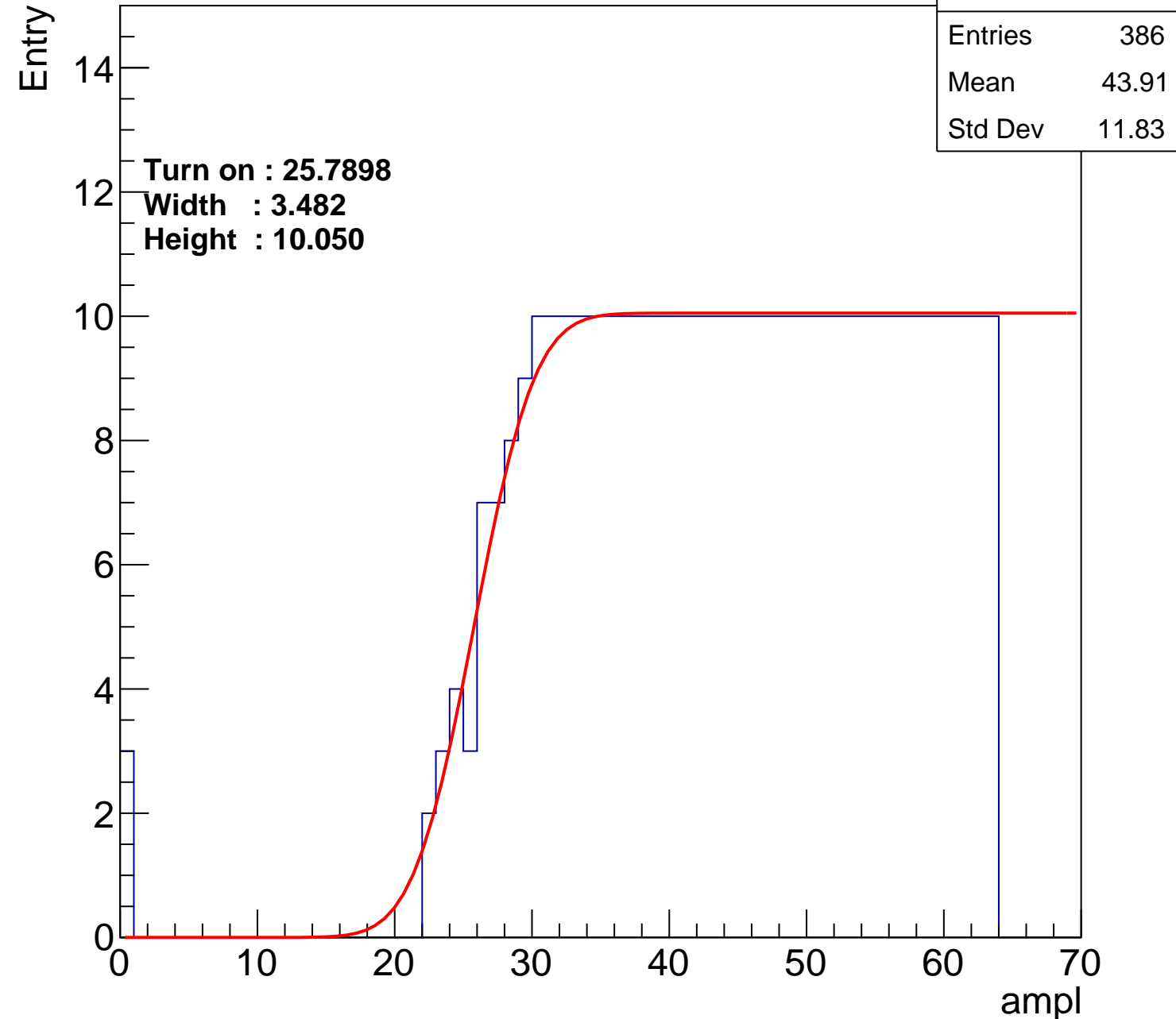
Width : 3.482

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 27.5013

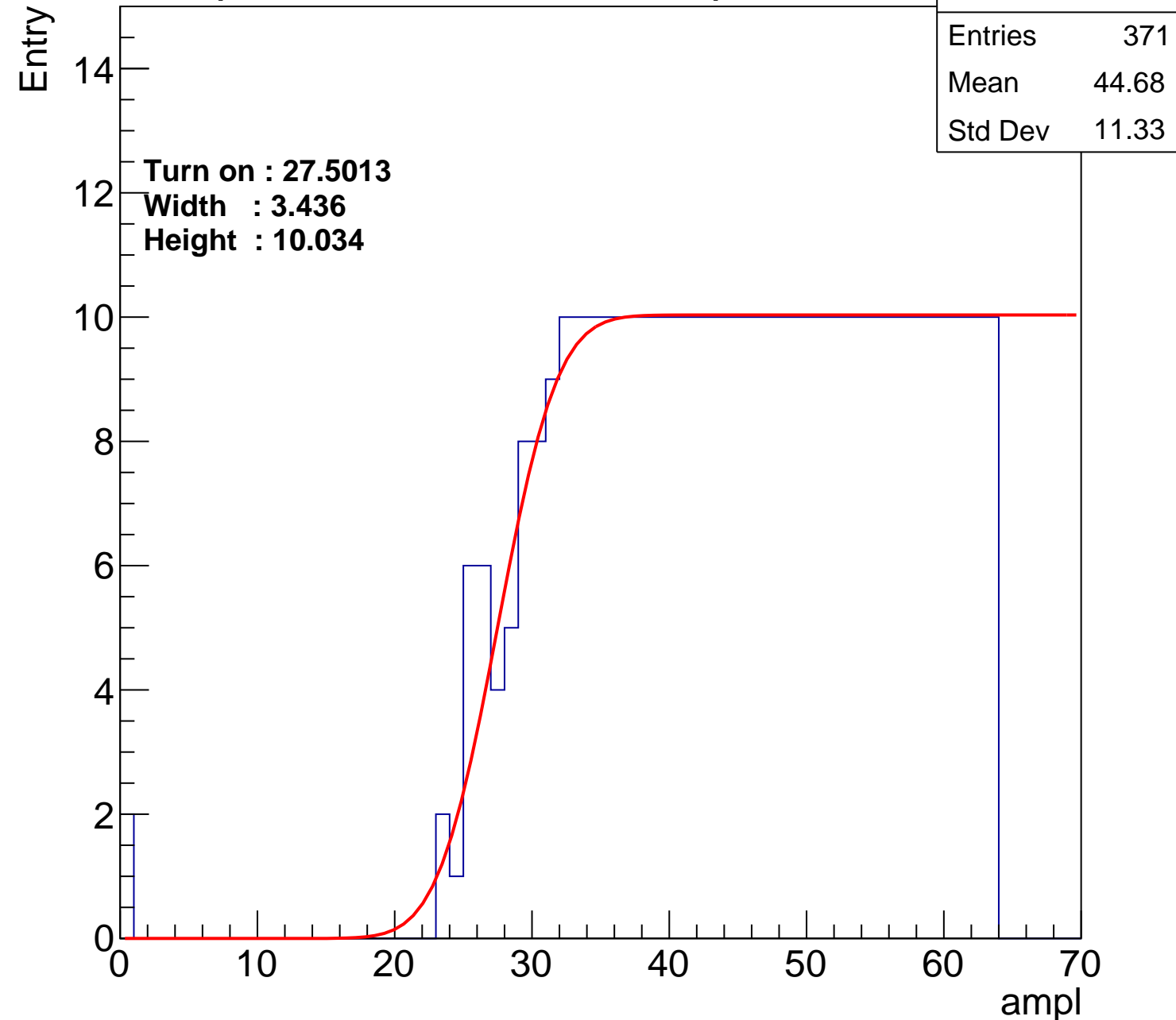
Width : 3.436

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch29

calib_packv5_042523_0143.root, FC#11, port A2

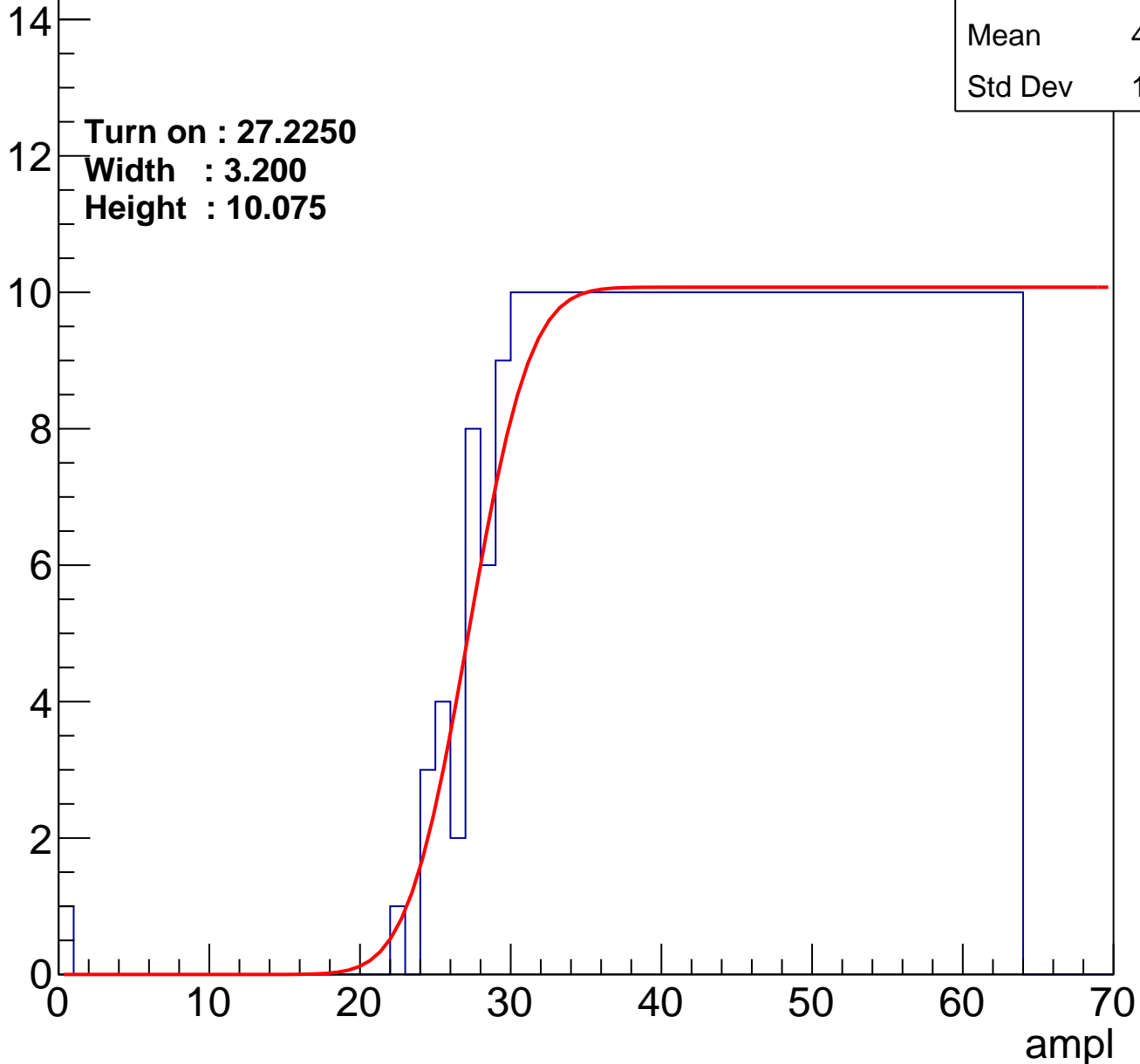
Entries	374
Mean	44.66
Std Dev	11.12

Turn on : 27.2250

Width : 3.200

Height : 10.075

Entry



B1L102S, U25-ch30

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.25
Std Dev	11.75

Turn on : 26.5265

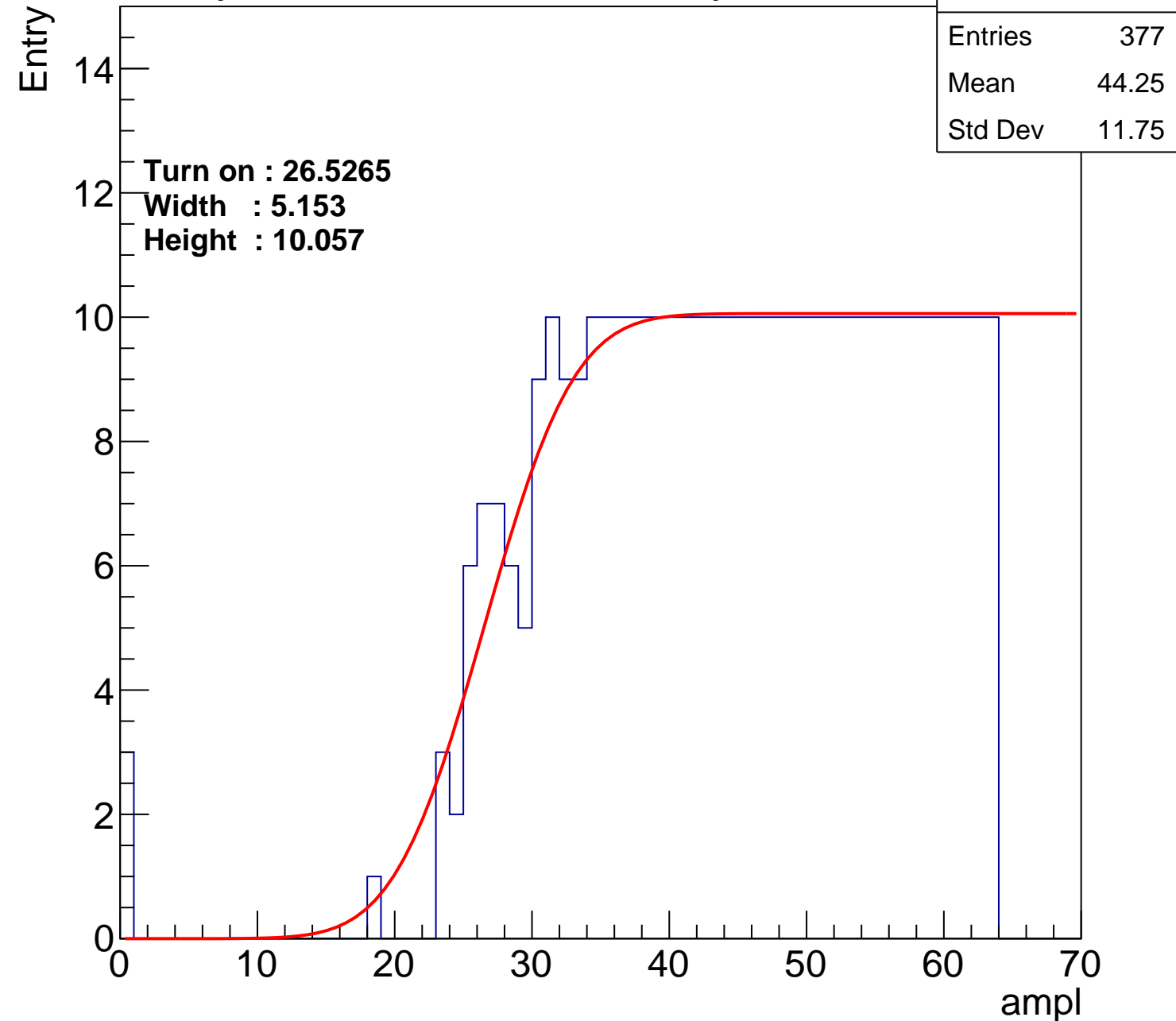
Width : 5.153

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch31

calib_packv5_042523_0143.root, FC#11, port A2

Entries	361
Mean	45.04
Std Dev	11.45

Turn on : 28.3552

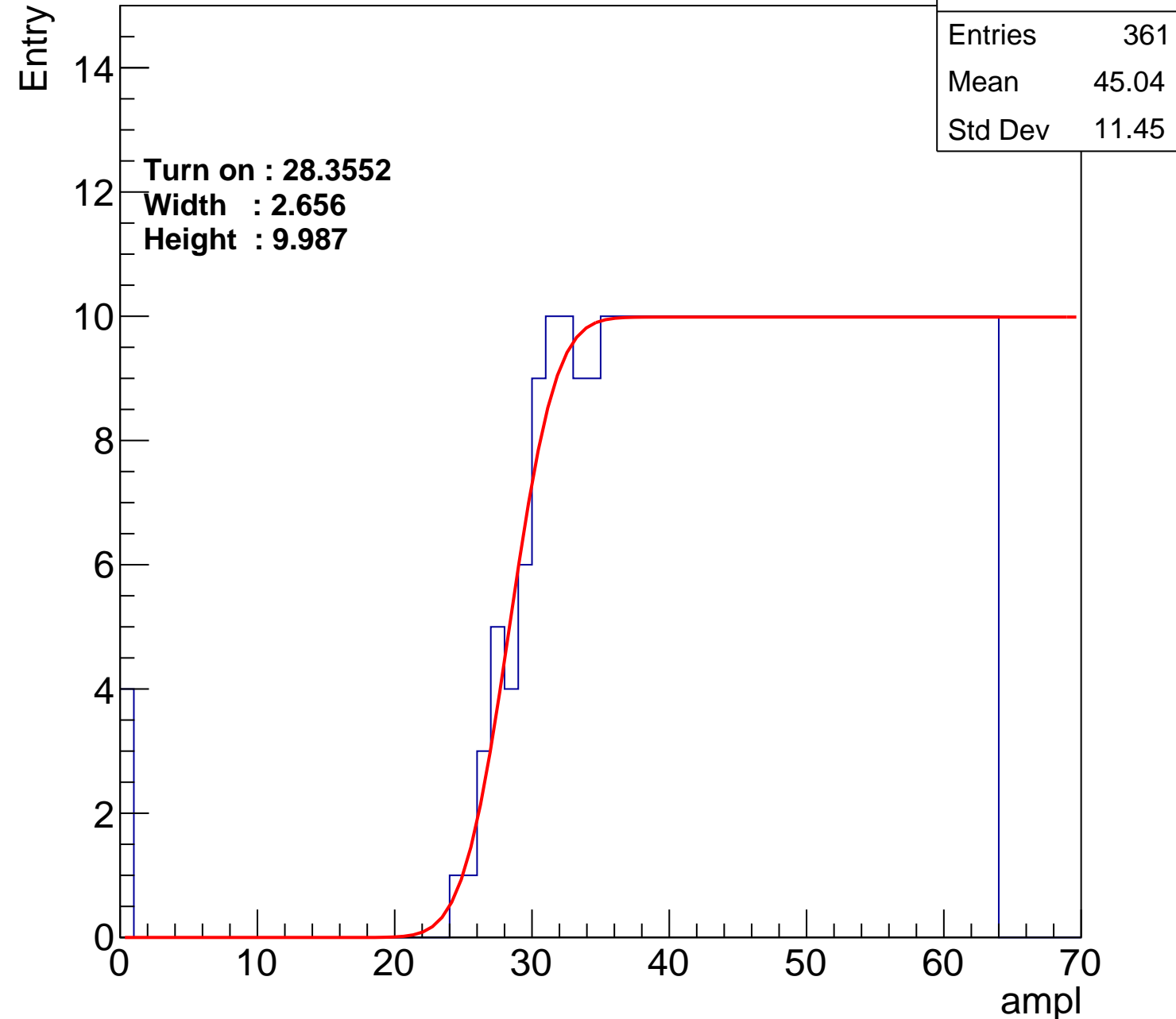
Width : 2.656

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	43.92
Std Dev	12.16

Turn on : 26.7211

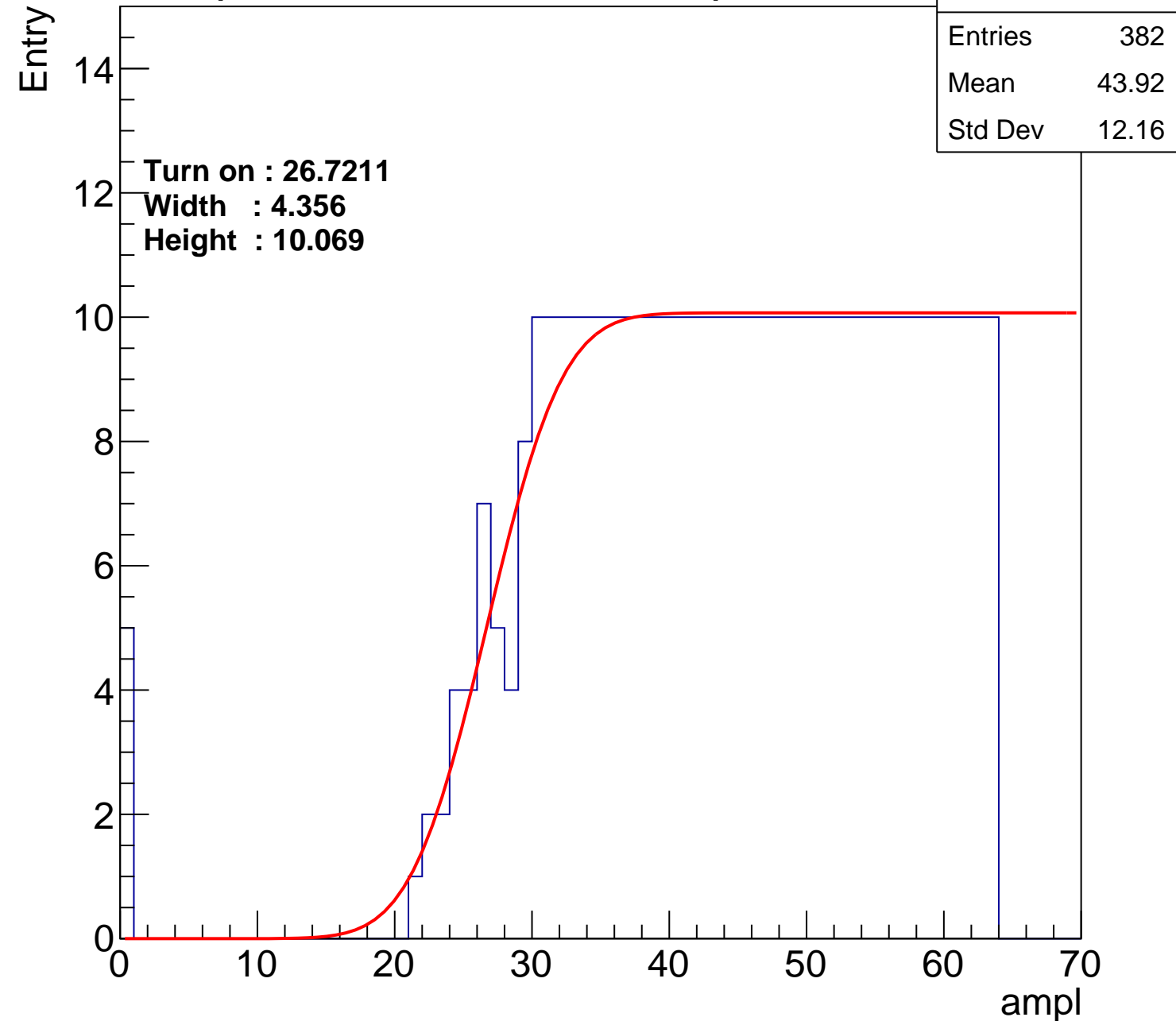
Width : 4.356

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch33

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.92
Std Dev	11.99

Turn on : 26.0765

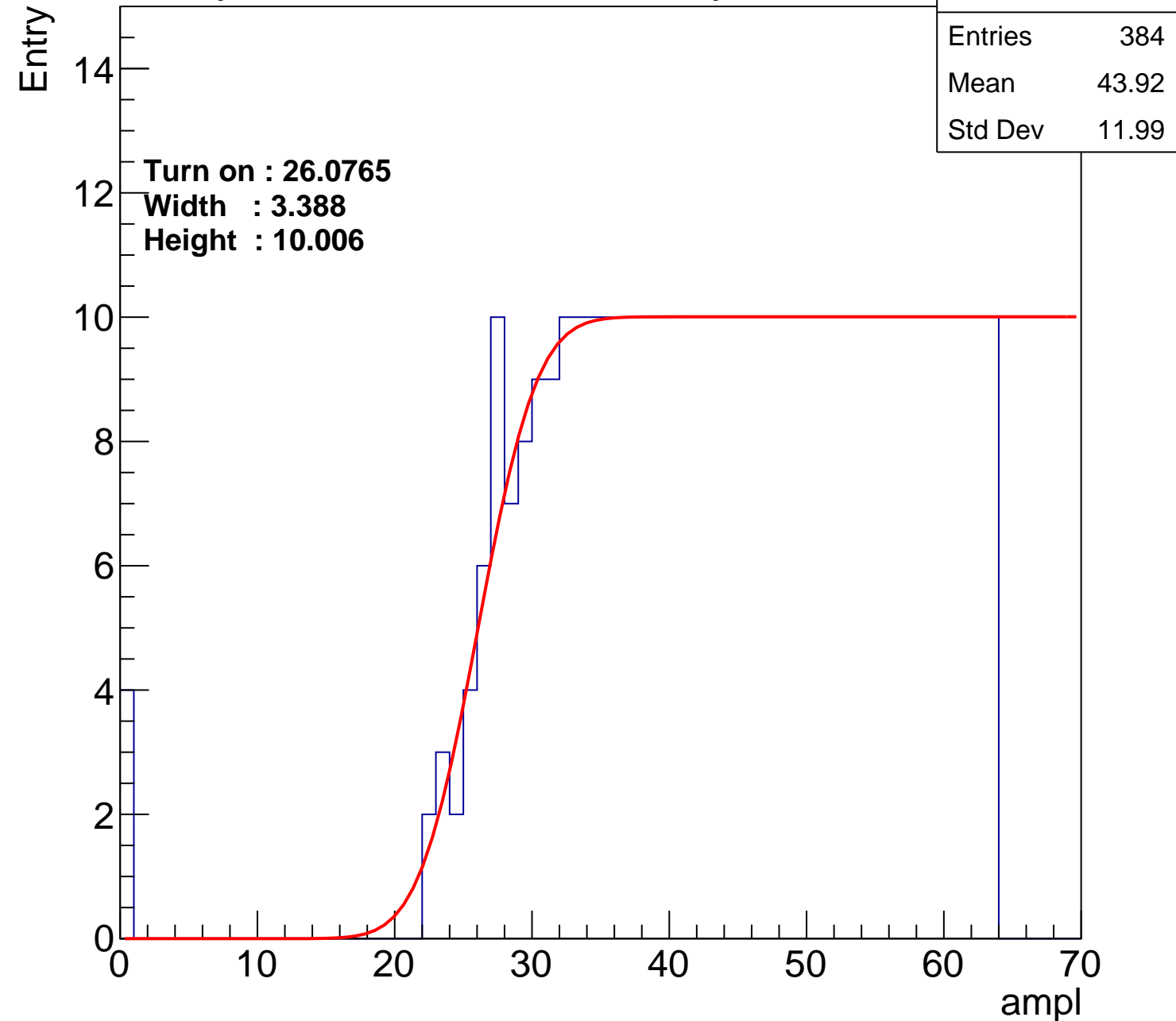
Width : 3.388

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch34

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.39
Std Dev	11.47

Turn on : 26.8159

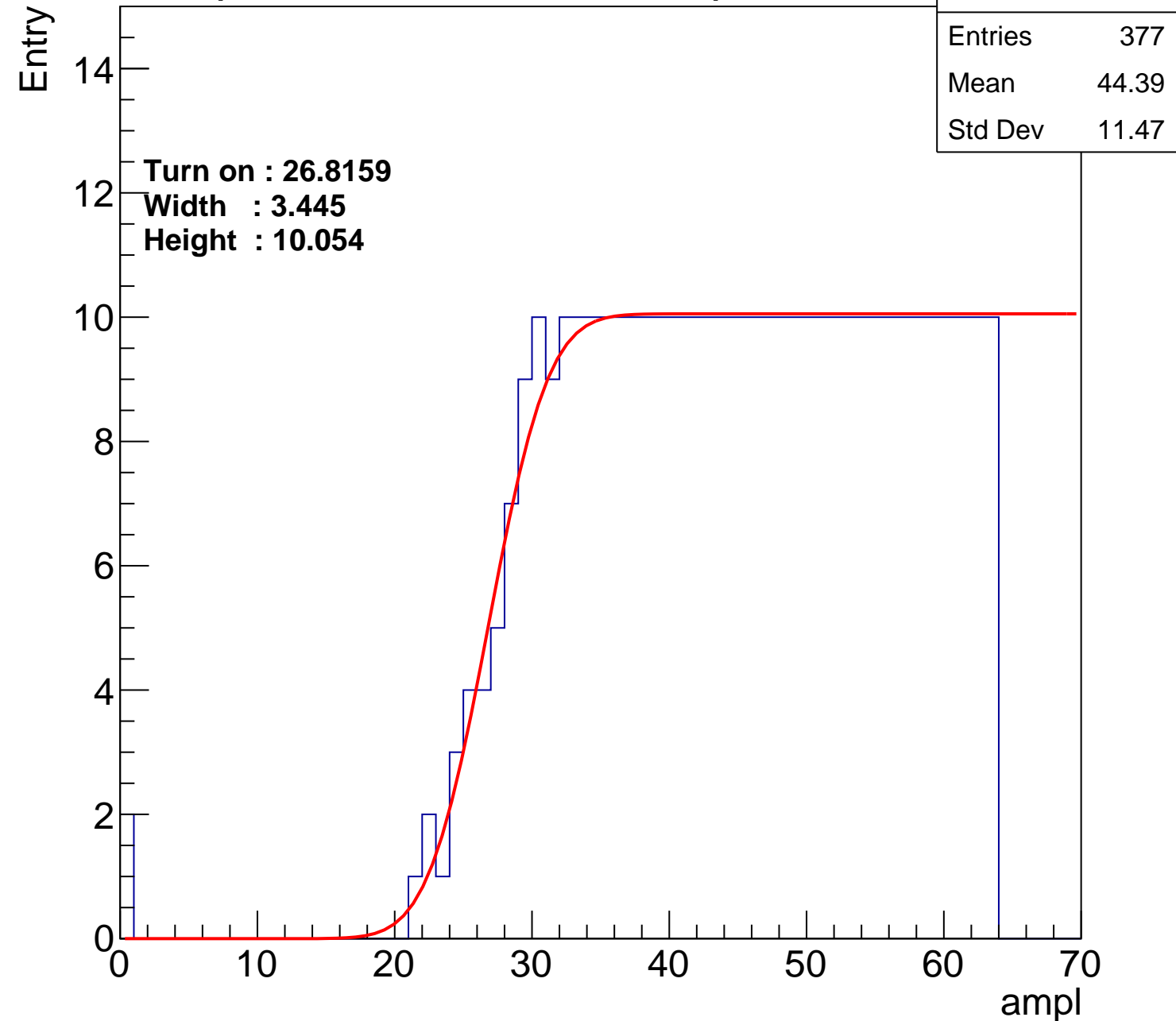
Width : 3.445

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch35

calib_packv5_042523_0143.root, FC#11, port A2

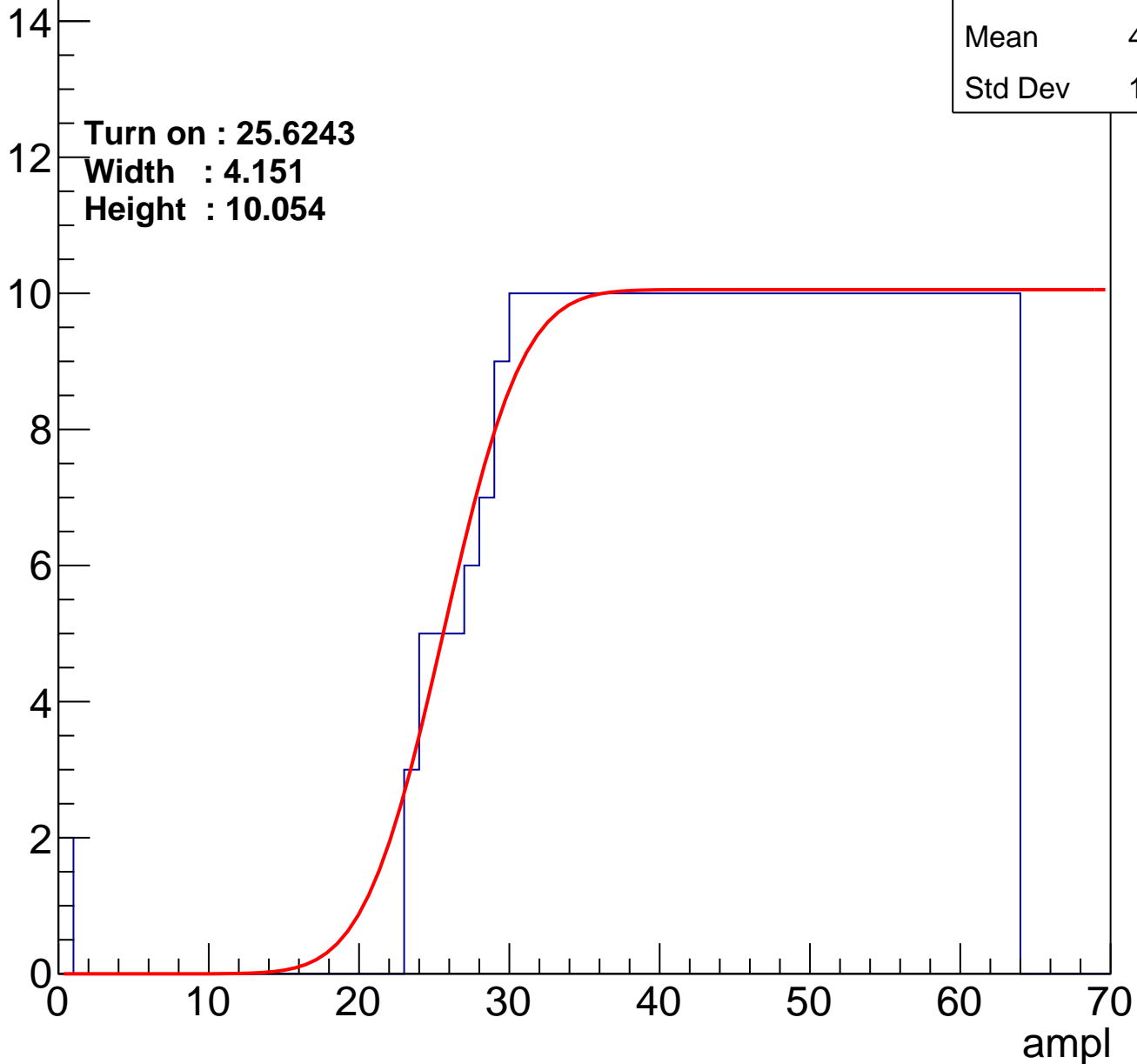
Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 25.6243

Width : 4.151

Height : 10.054

Entry



B1L102S, U25-ch36

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.84
Std Dev	11.71

Turn on : 25.5467

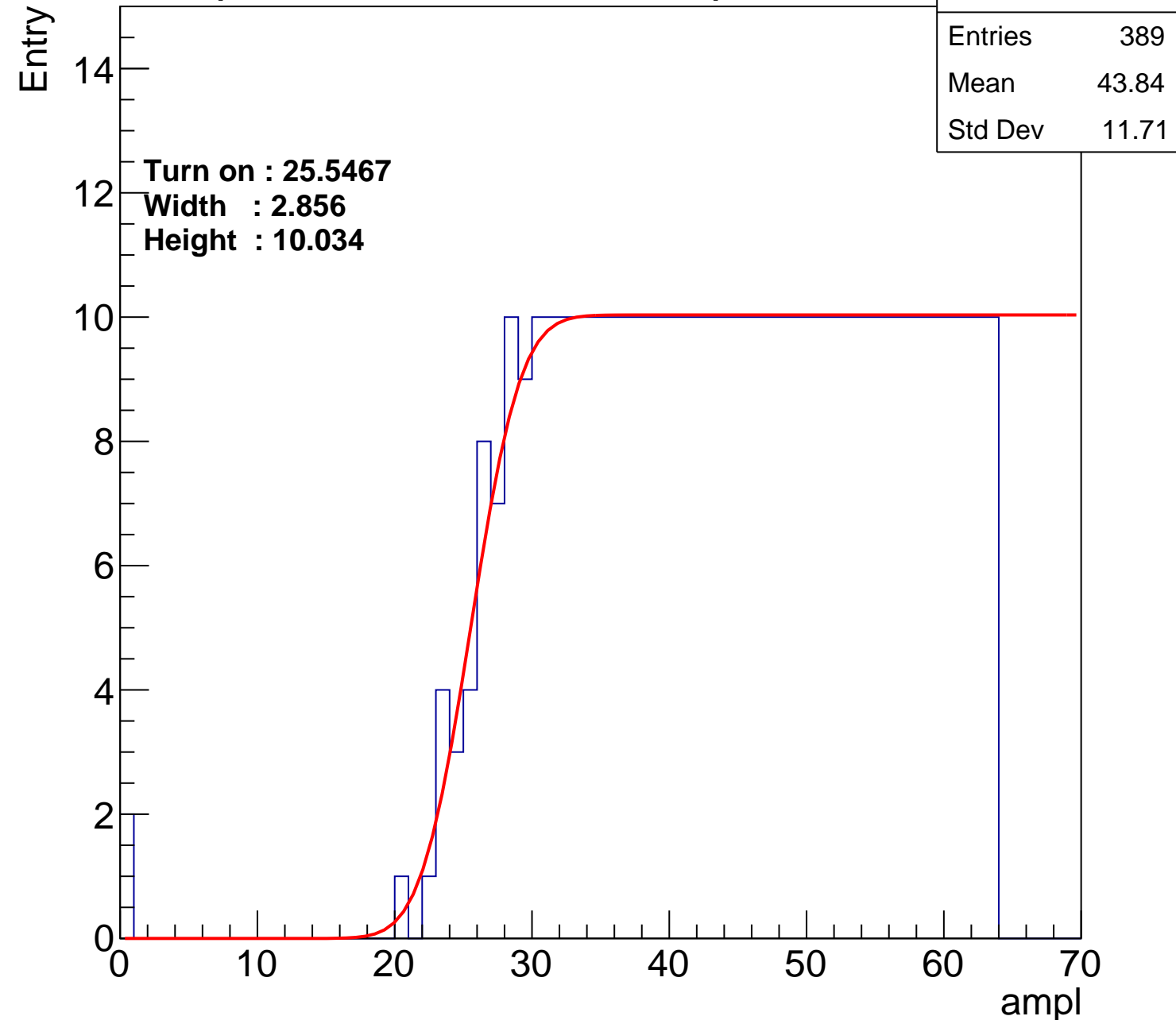
Width : 2.856

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.84
Std Dev	11.83

Turn on : 26.1044

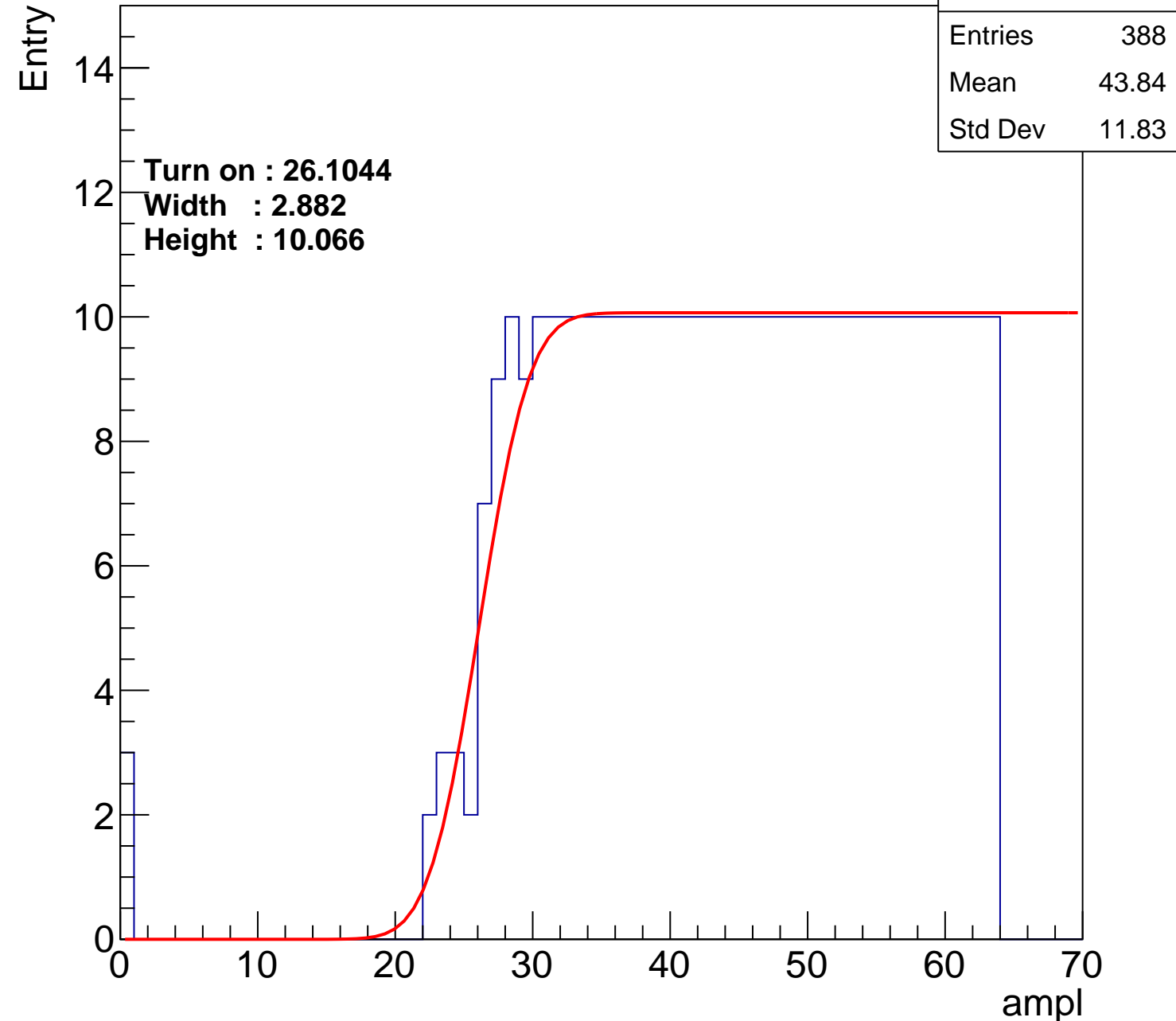
Width : 2.882

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch38

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 27.4976

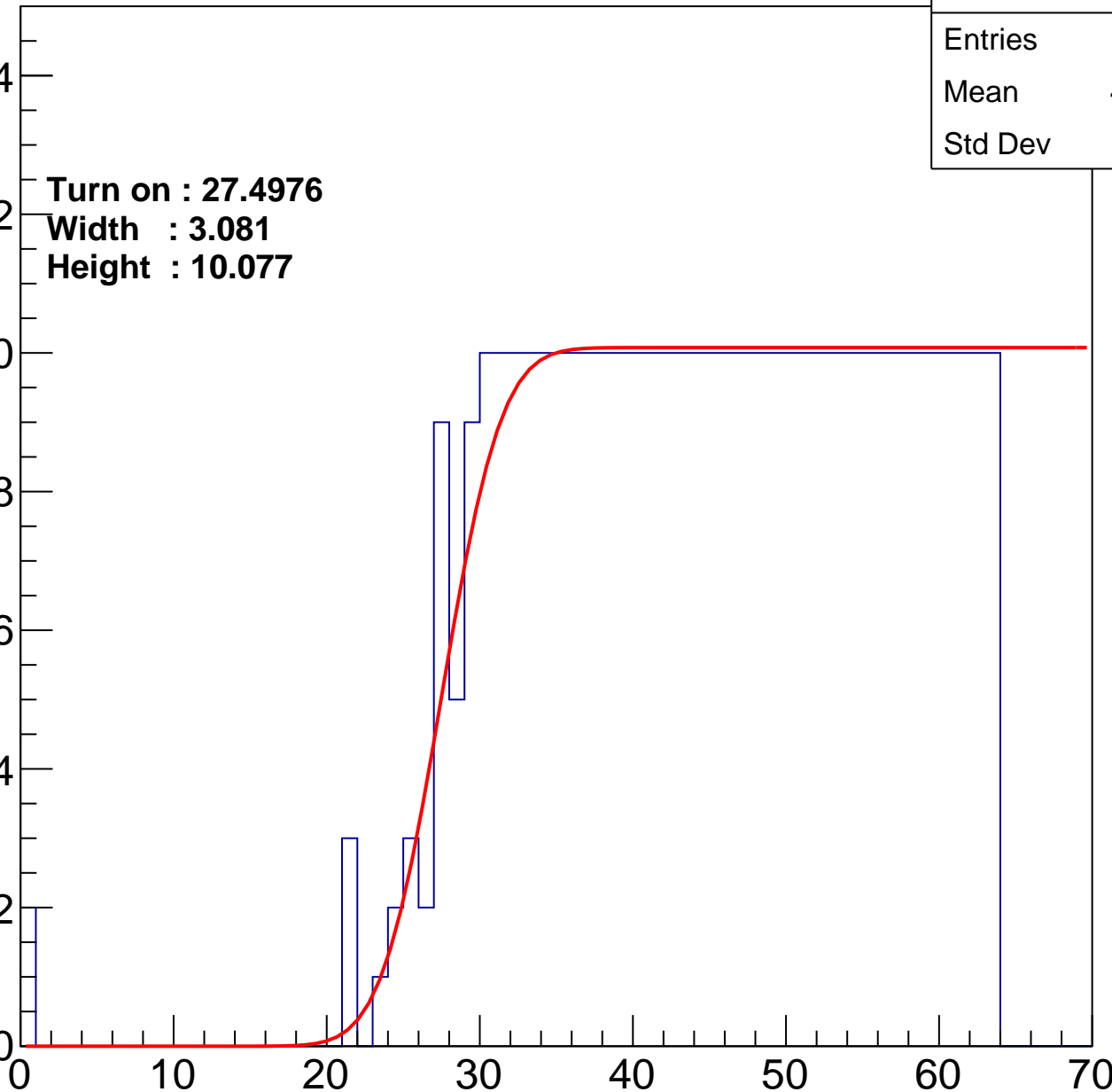
Width : 3.081

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch39

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.8670

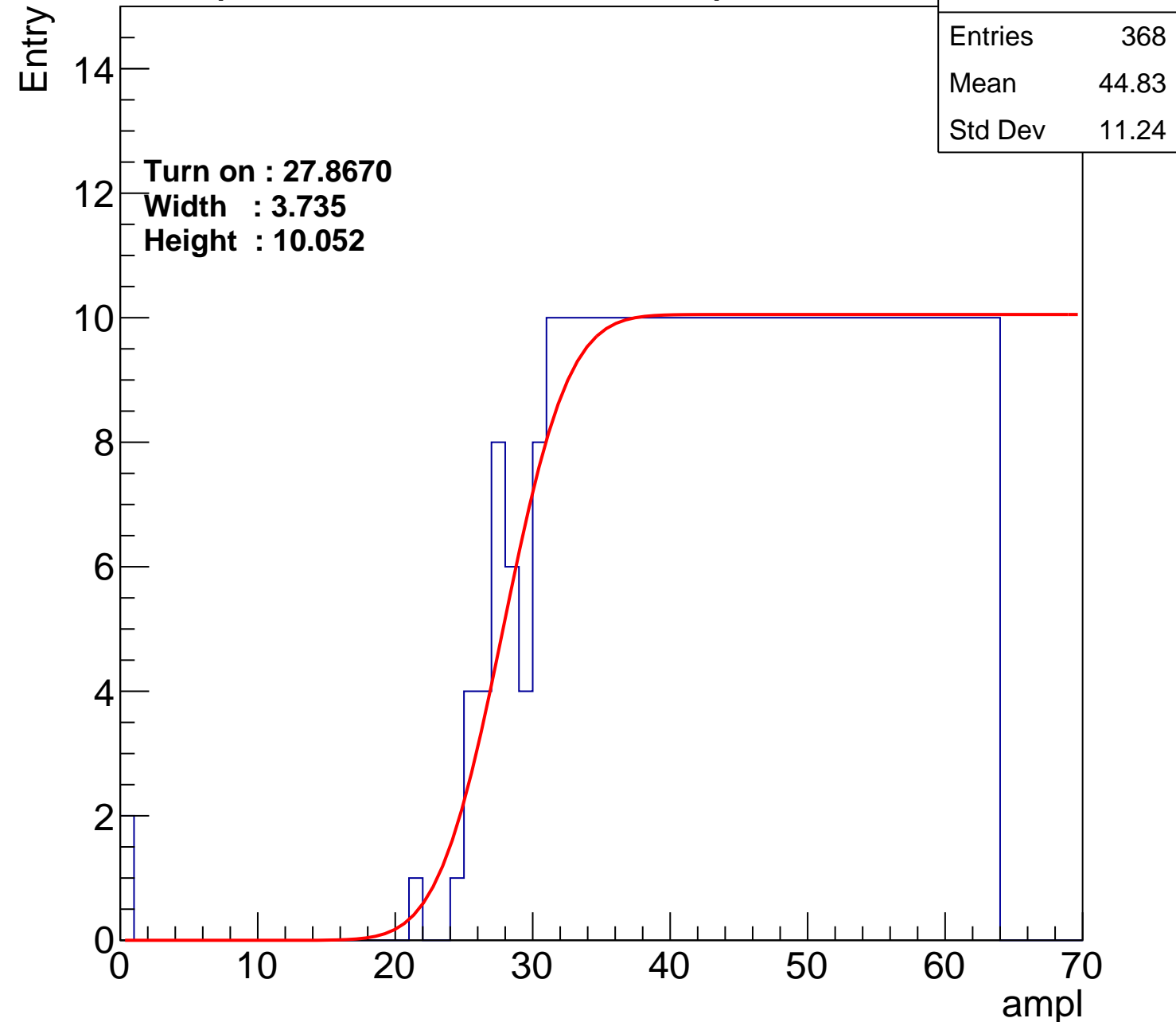
Width : 3.735

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch40

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 24.8893

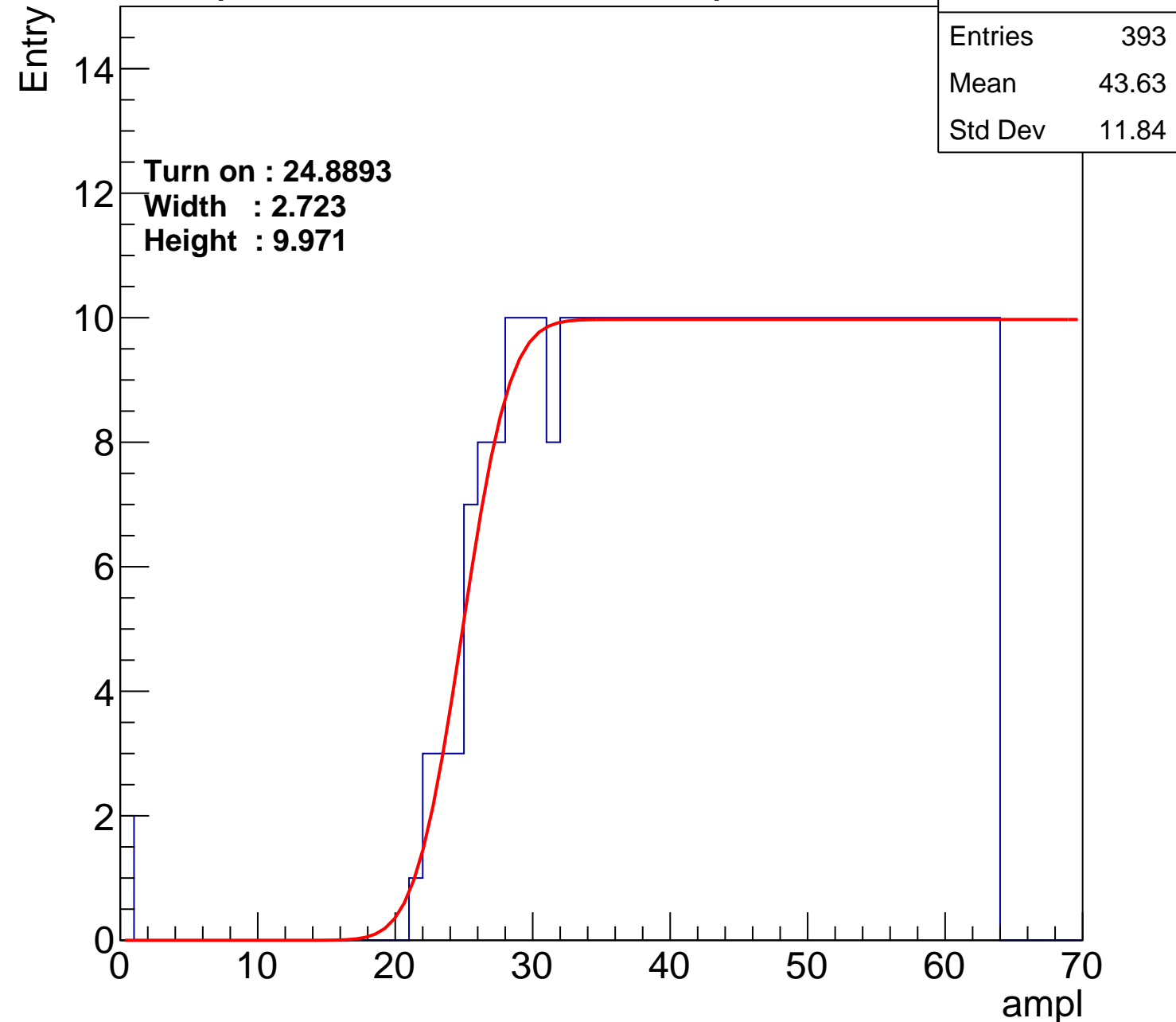
Width : 2.723

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch41

calib_packv5_042523_0143.root, FC#11, port A2

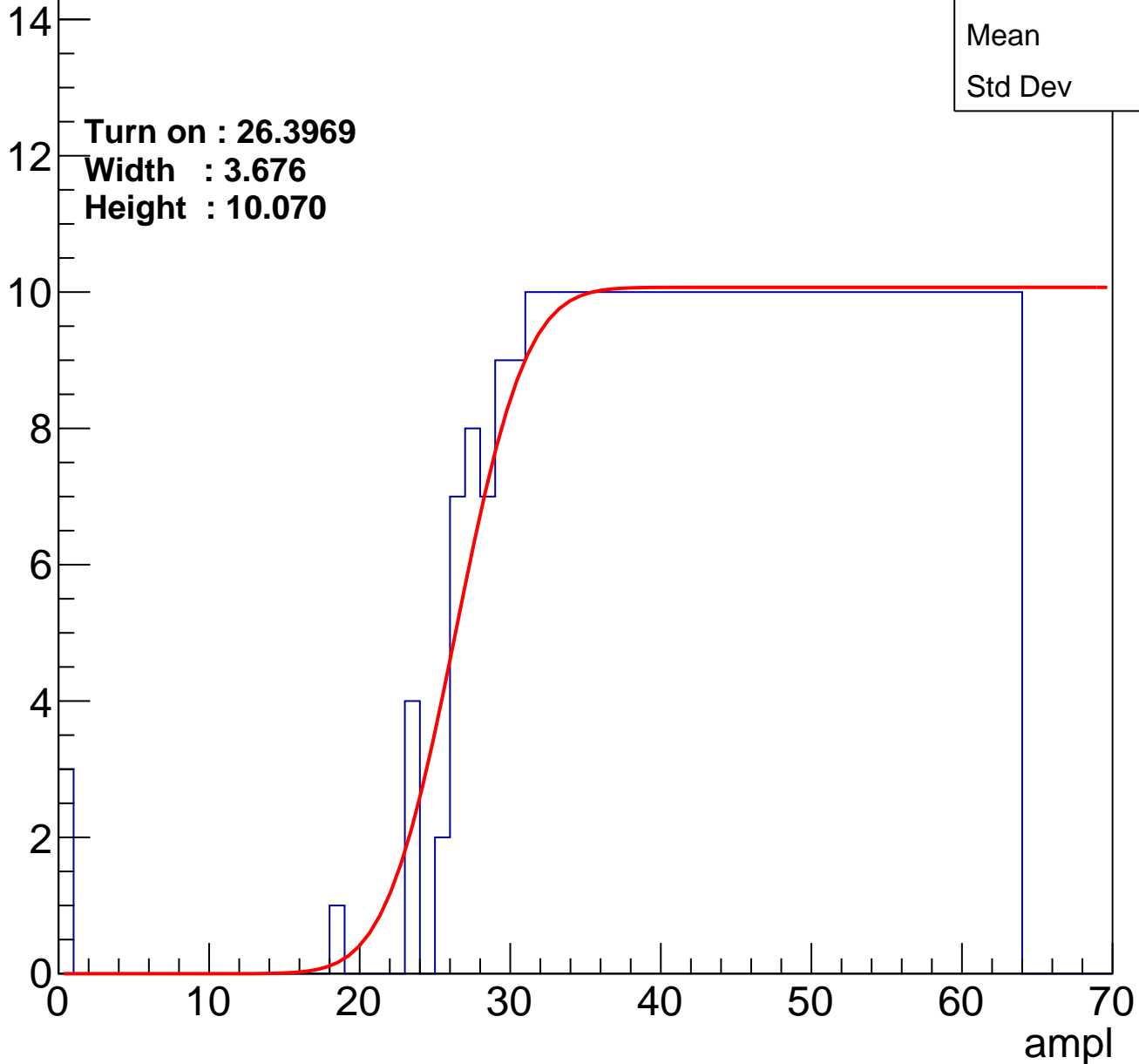
Entries	380
Mean	44.2
Std Dev	11.7

Turn on : 26.3969

Width : 3.676

Height : 10.070

Entry



B1L102S, U25-ch42

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	43.2
Std Dev	12.16

Turn on : 24.1149

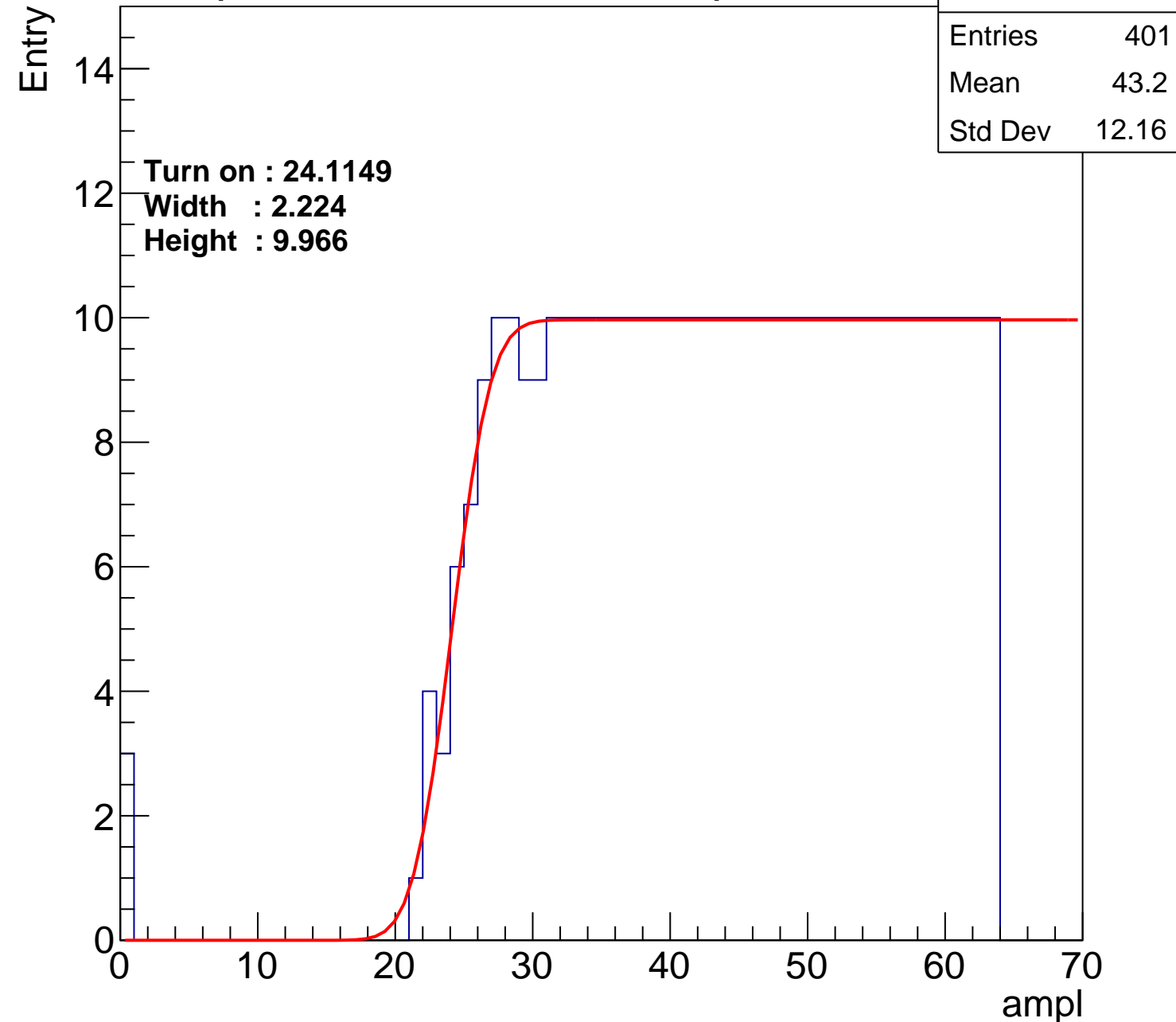
Width : 2.224

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch43

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.31
Std Dev	12.15

Turn on : 24.5620

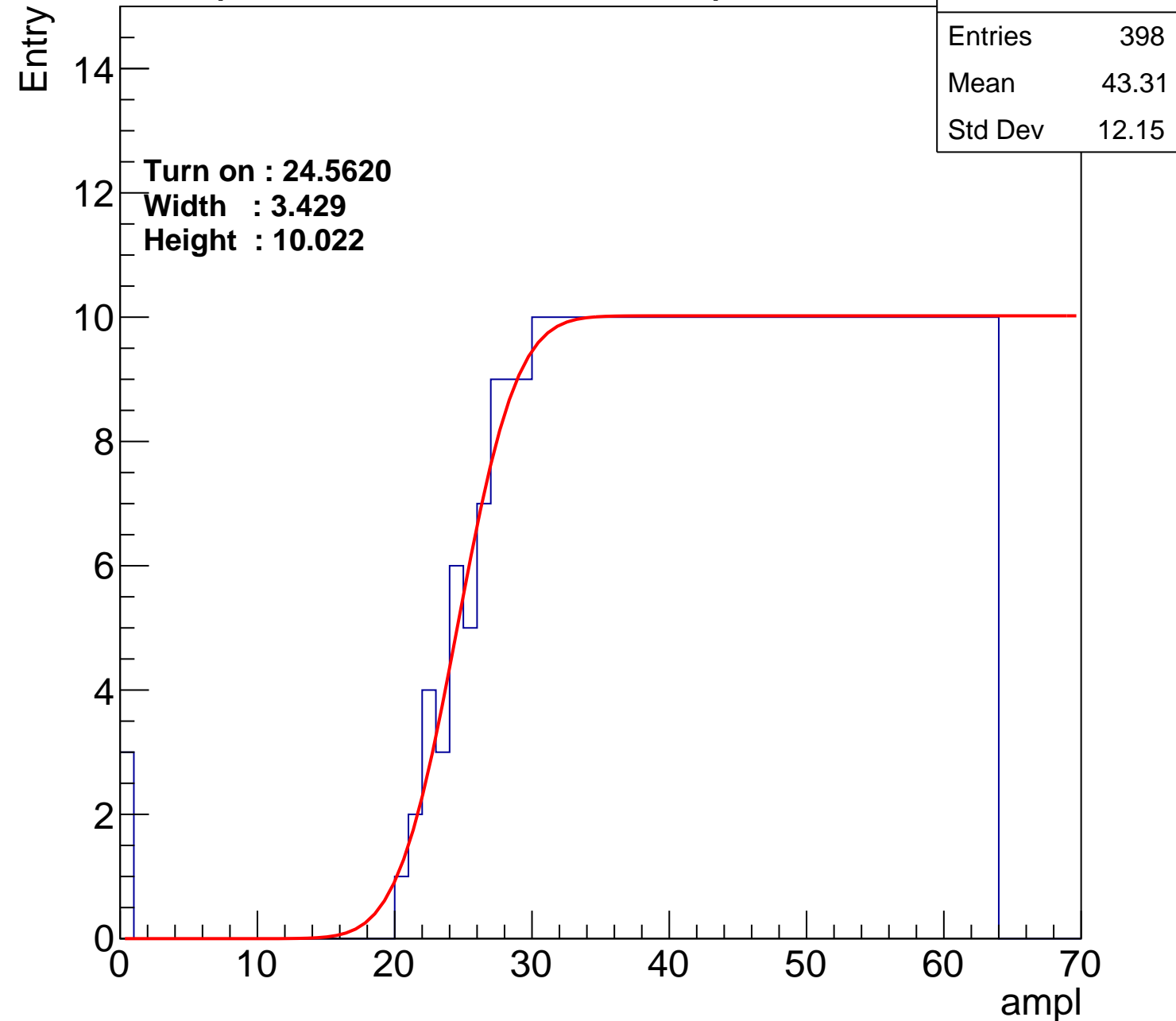
Width : 3.429

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch44

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.69
Std Dev	12.08

Turn on : 27.0251

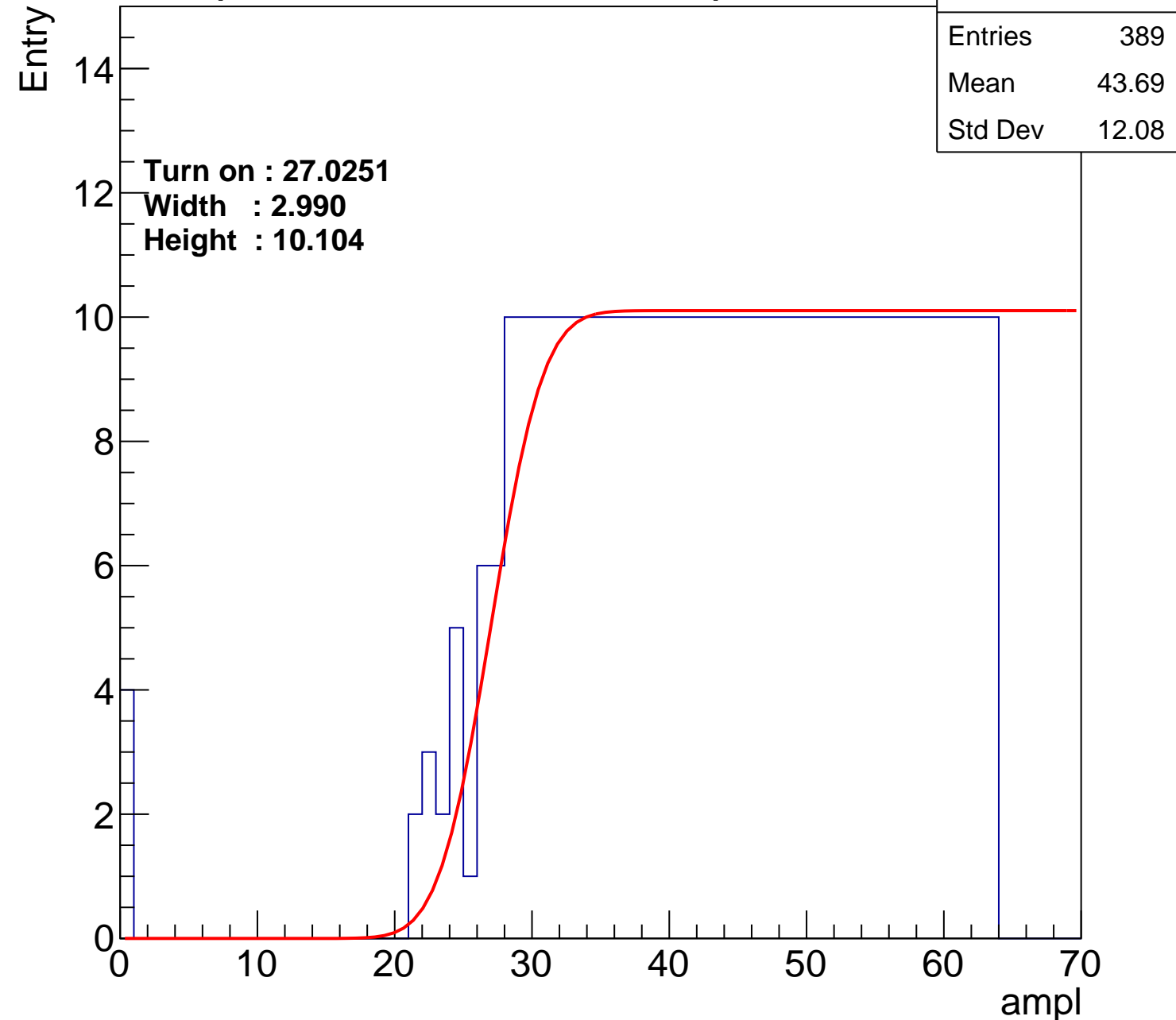
Width : 2.990

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch45

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.49
Std Dev	11.35

Turn on : 26.8197

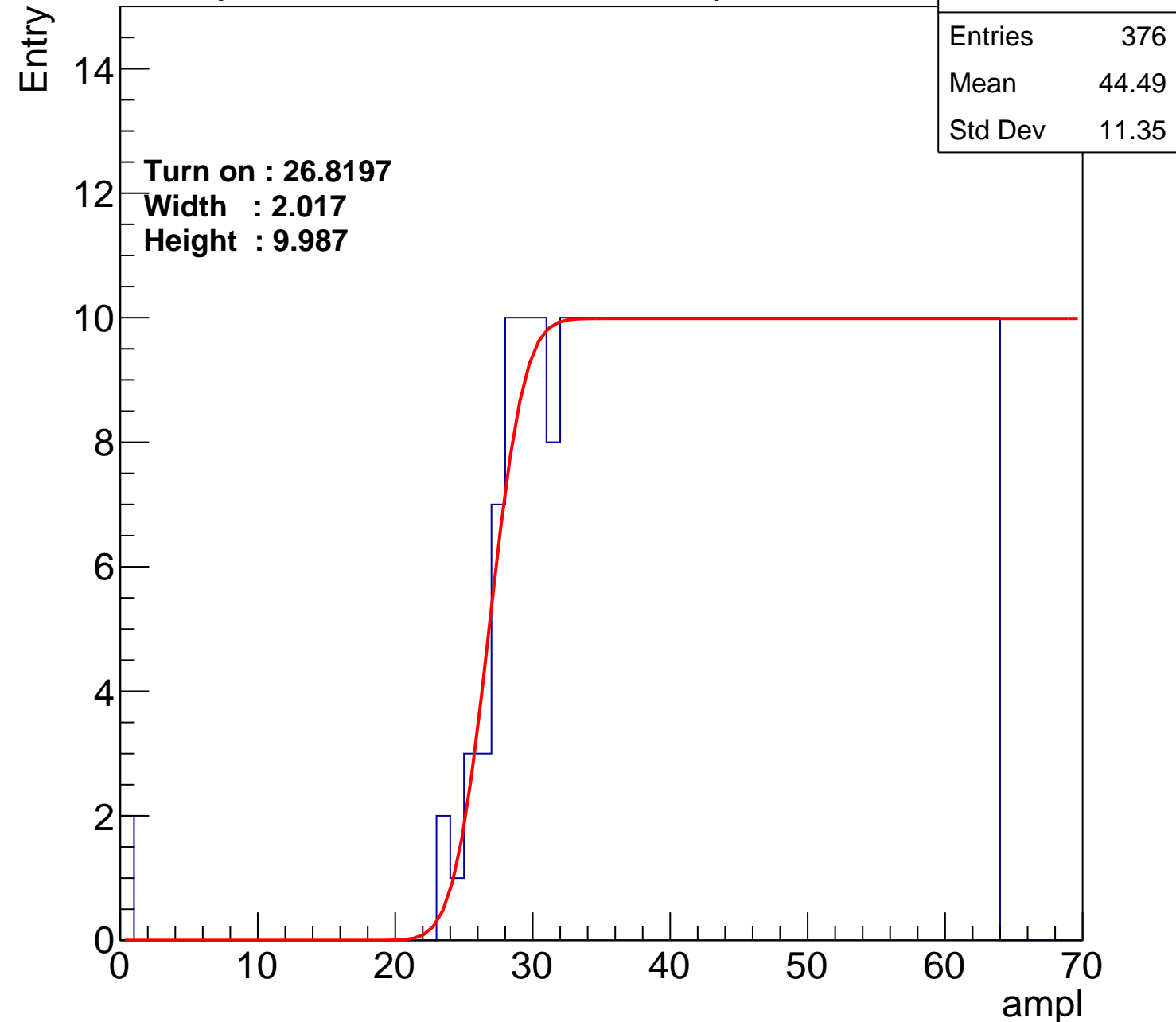
Width : 2.017

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch46

calib_packv5_042523_0143.root, FC#11, port A2

Entries	363
Mean	45.05
Std Dev	11.17

Turn on : 28.3290

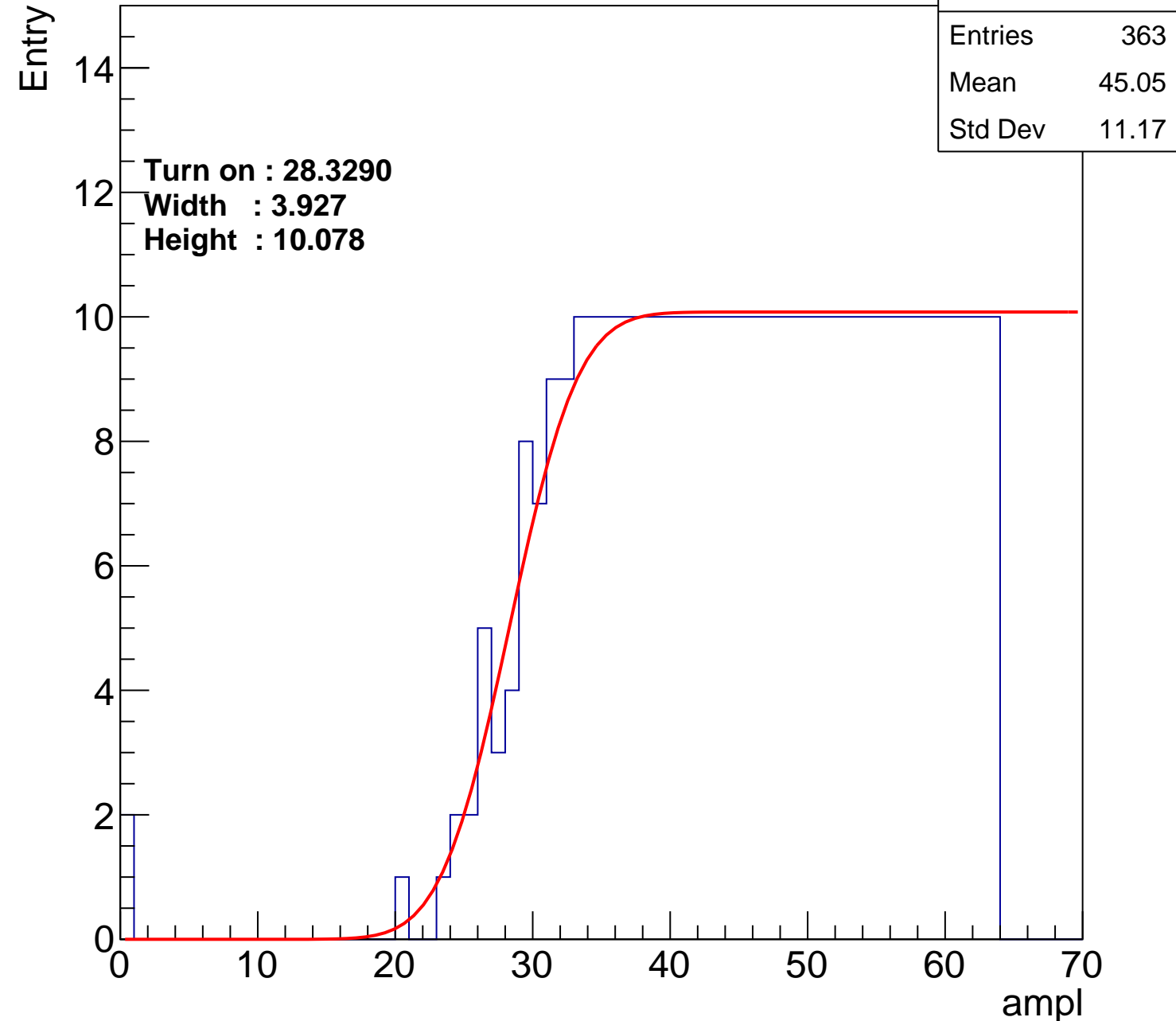
Width : 3.927

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch47

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.85
Std Dev	11.21

Turn on : 27.8174

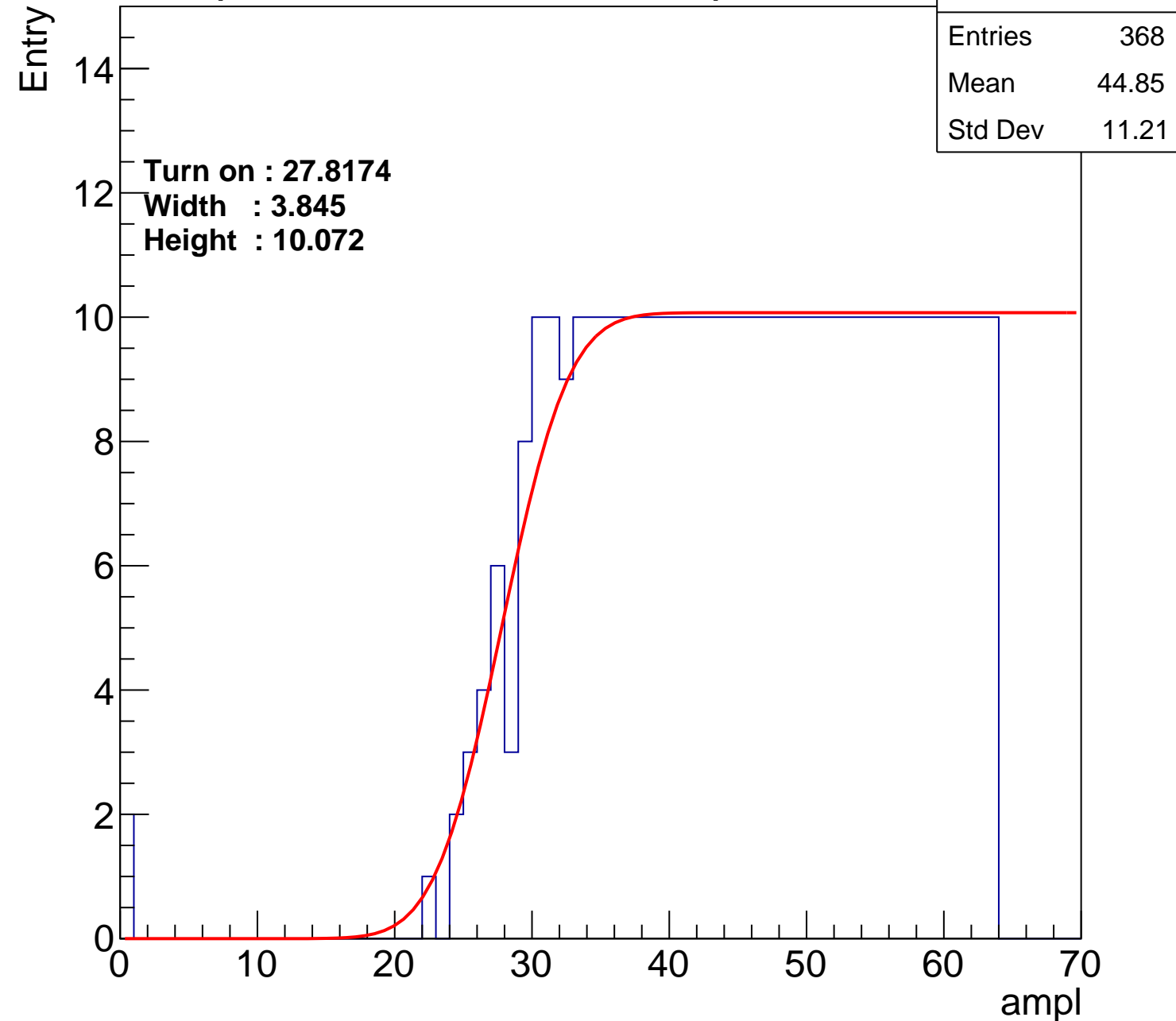
Width : 3.845

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch48

calib_packv5_042523_0143.root, FC#11, port A2

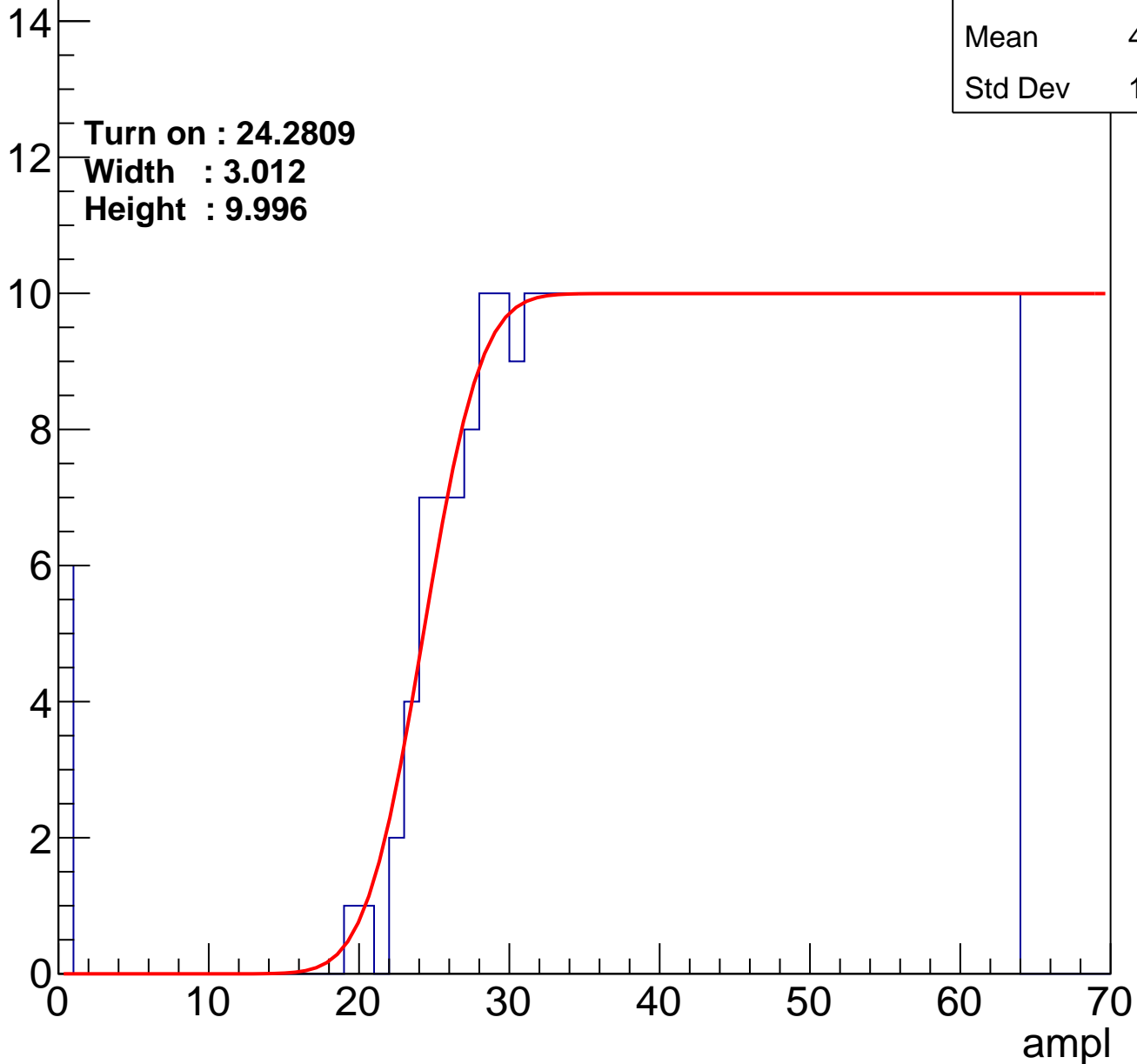
Entries	402
Mean	42.95
Std Dev	12.66

Turn on : 24.2809

Width : 3.012

Height : 9.996

Entry



B1L102S, U25-ch49

calib_packv5_042523_0143.root, FC#11, port A2

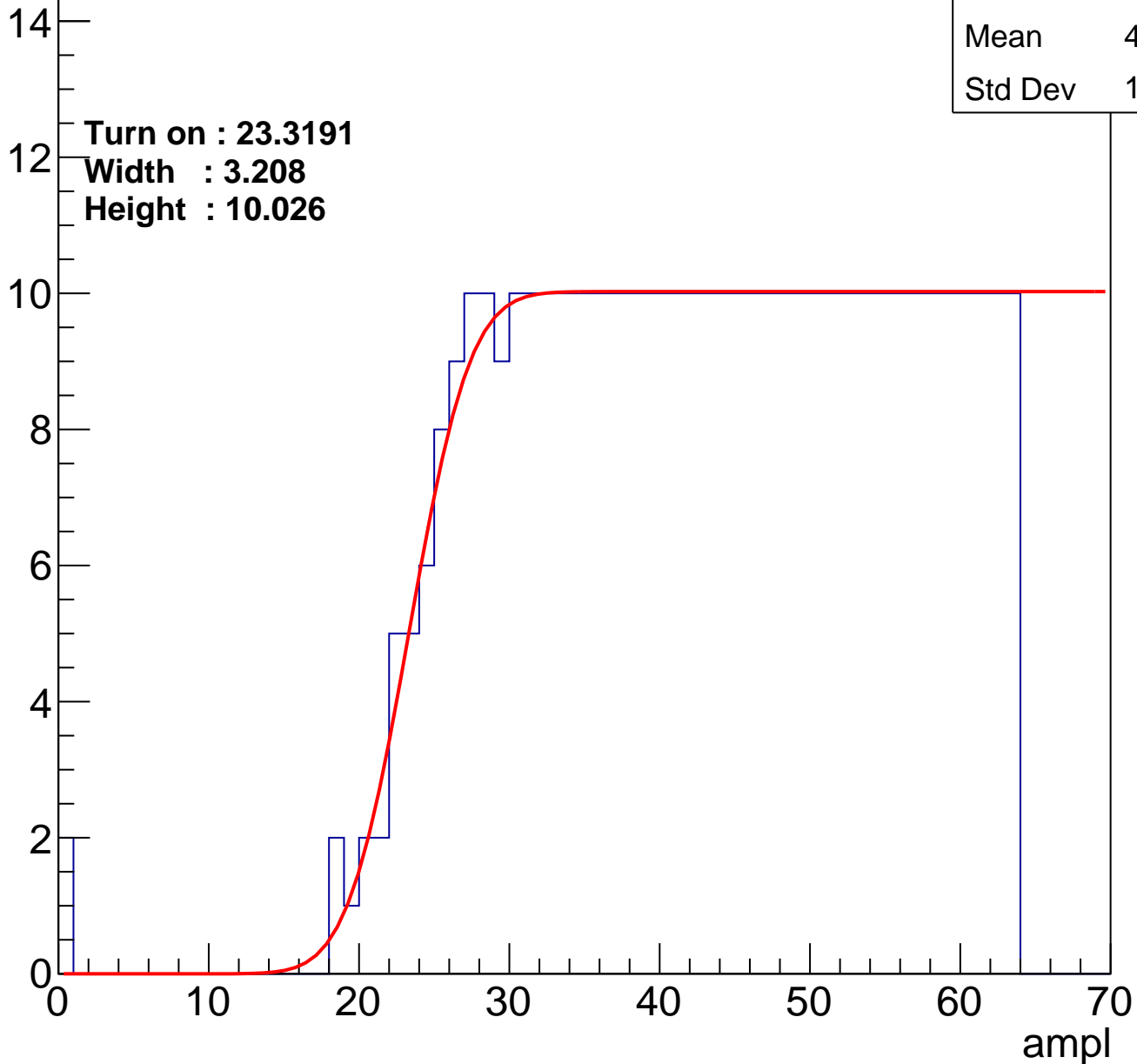
Entries	411
Mean	42.73
Std Dev	12.34

Turn on : 23.3191

Width : 3.208

Height : 10.026

Entry



B1L102S, U25-ch50

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.17
Std Dev	11.42

Turn on : 25.8706

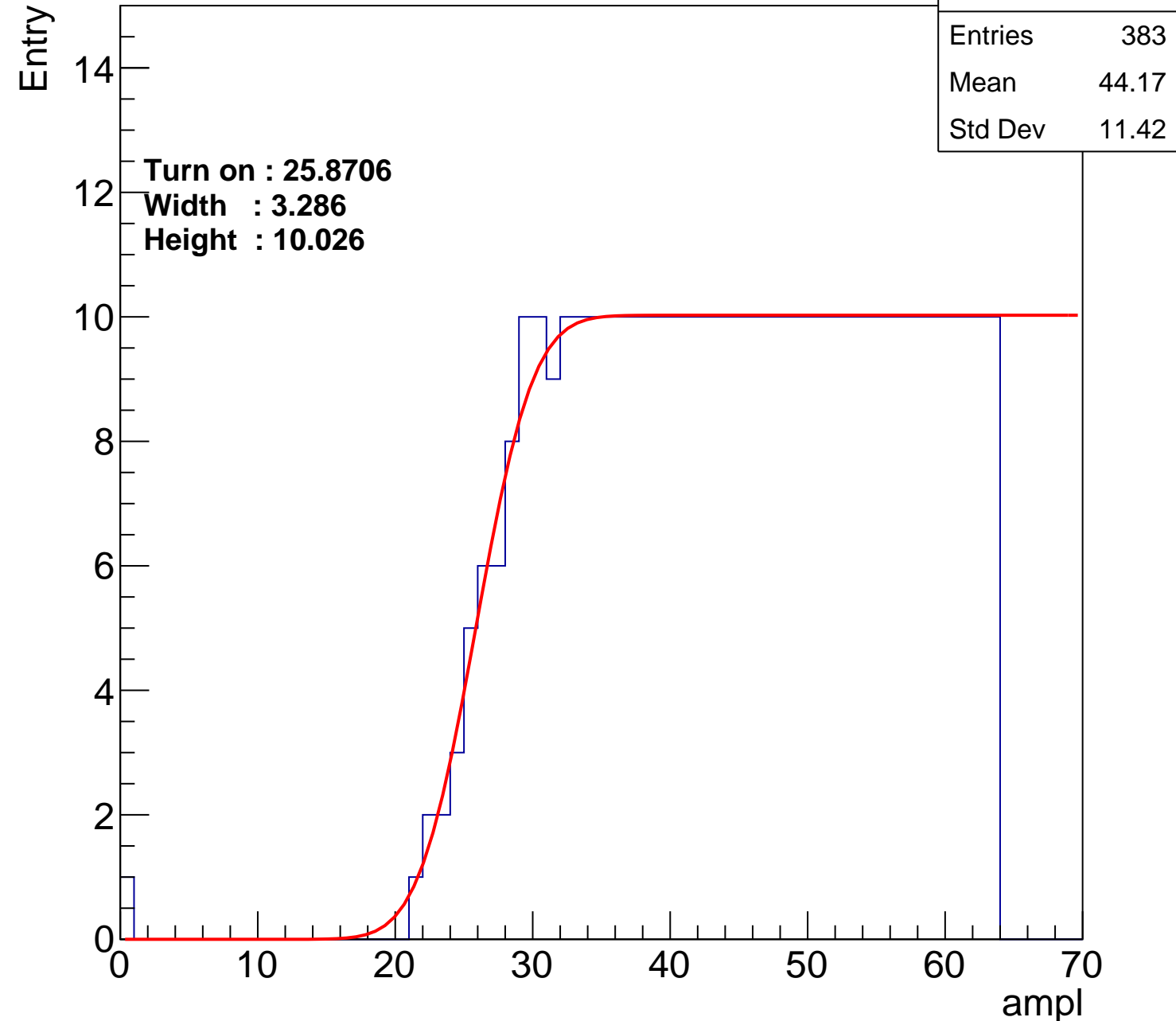
Width : 3.286

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch51

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.38
Std Dev	11.49

Turn on : 26.7821

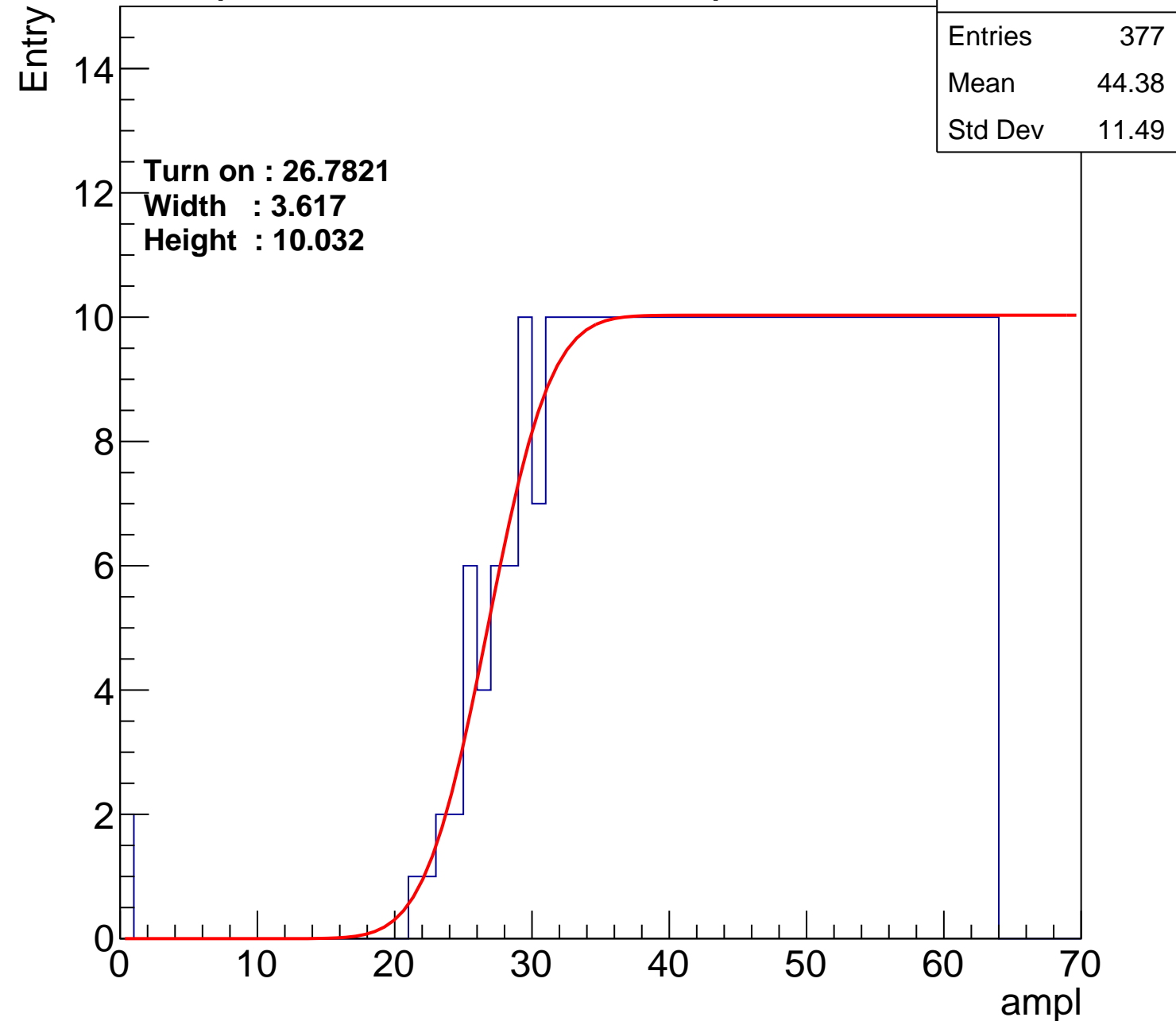
Width : 3.617

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch52

calib_packv5_042523_0143.root, FC#11, port A2

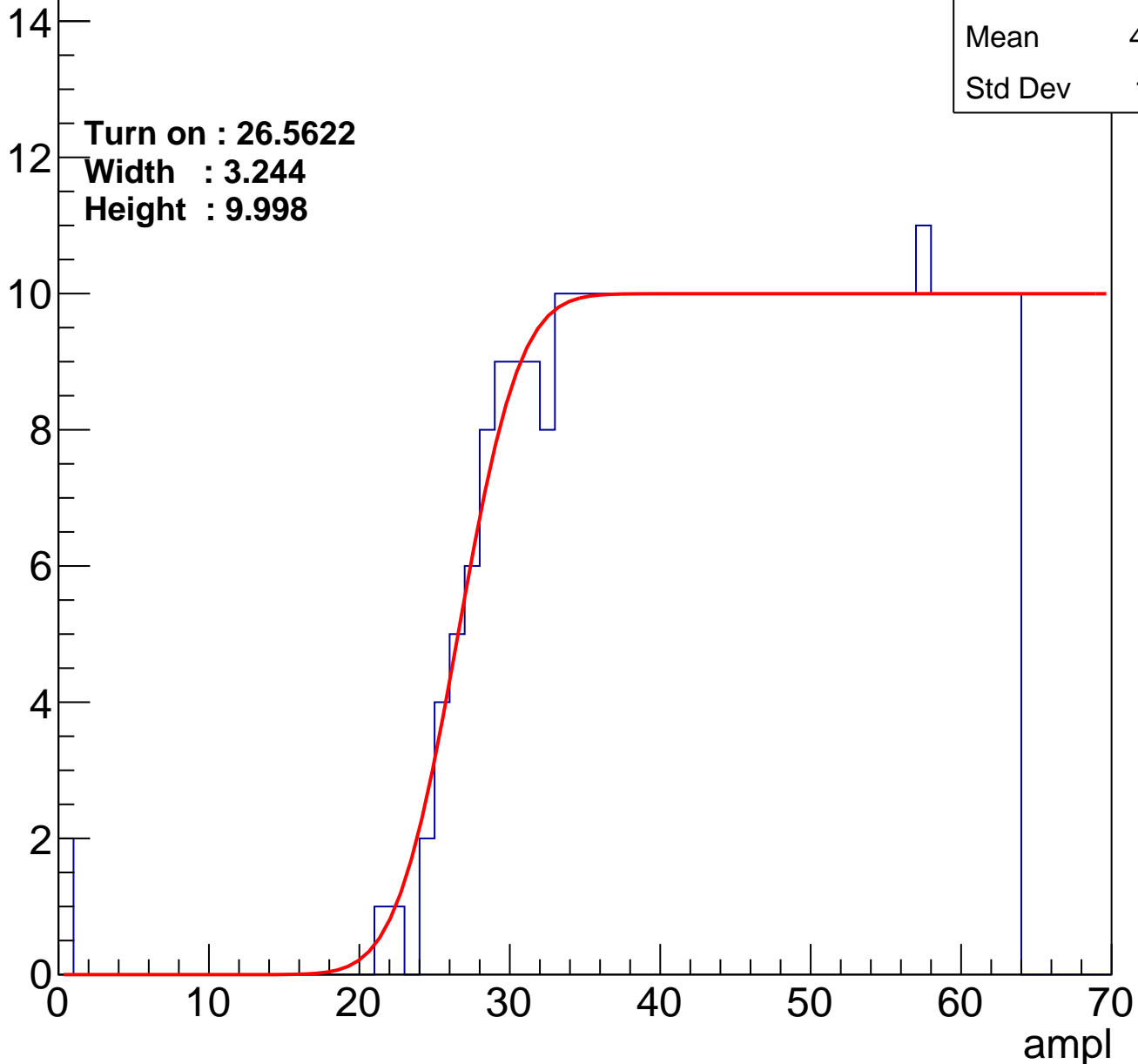
Entries	375
Mean	44.56
Std Dev	11.41

Turn on : 26.5622

Width : 3.244

Height : 9.998

Entry



B1L102S, U25-ch53

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.69
Std Dev	12.08

Turn on : 25.2024

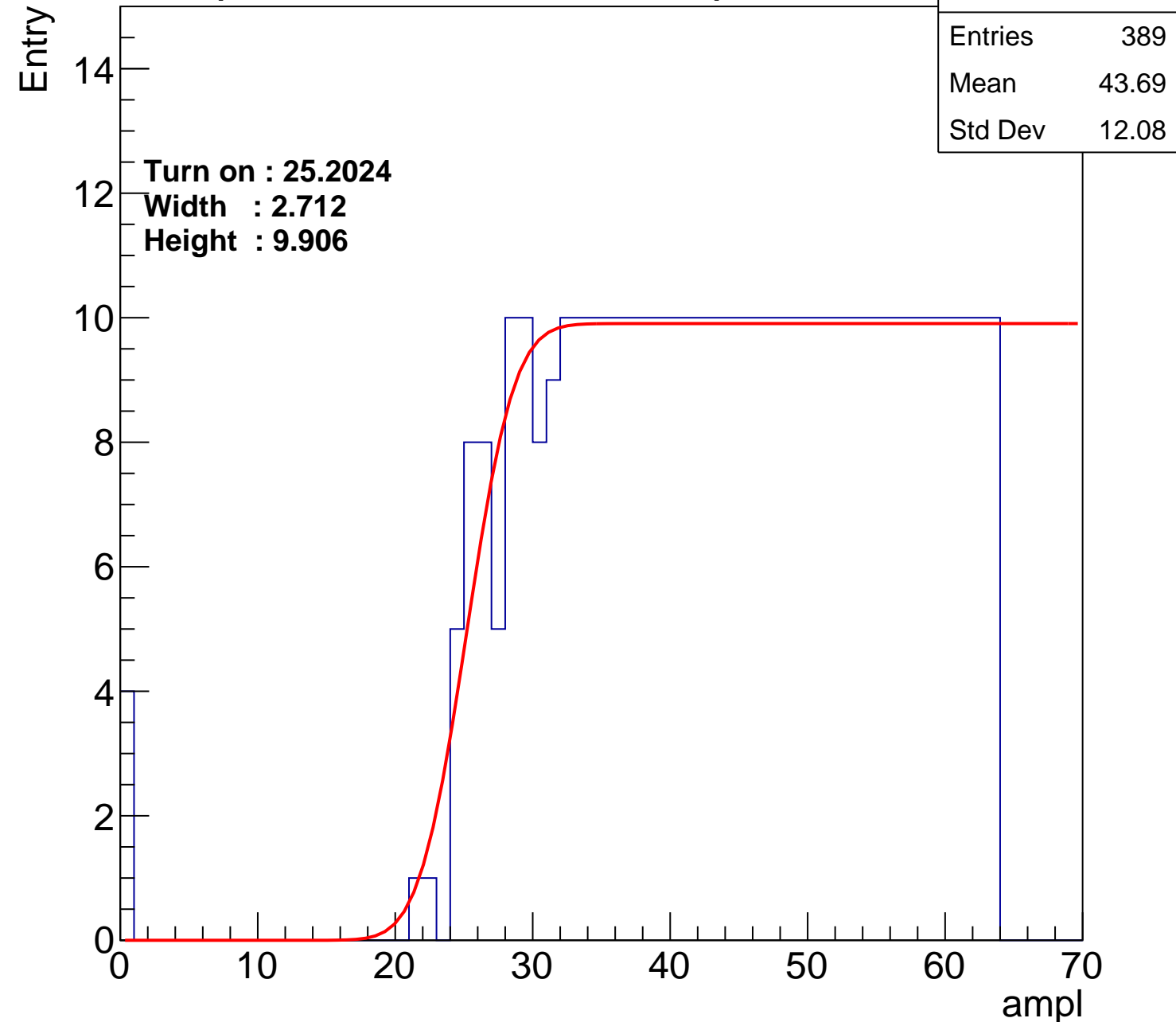
Width : 2.712

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch54

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.42
Std Dev	12.01

Turn on : 25.0121

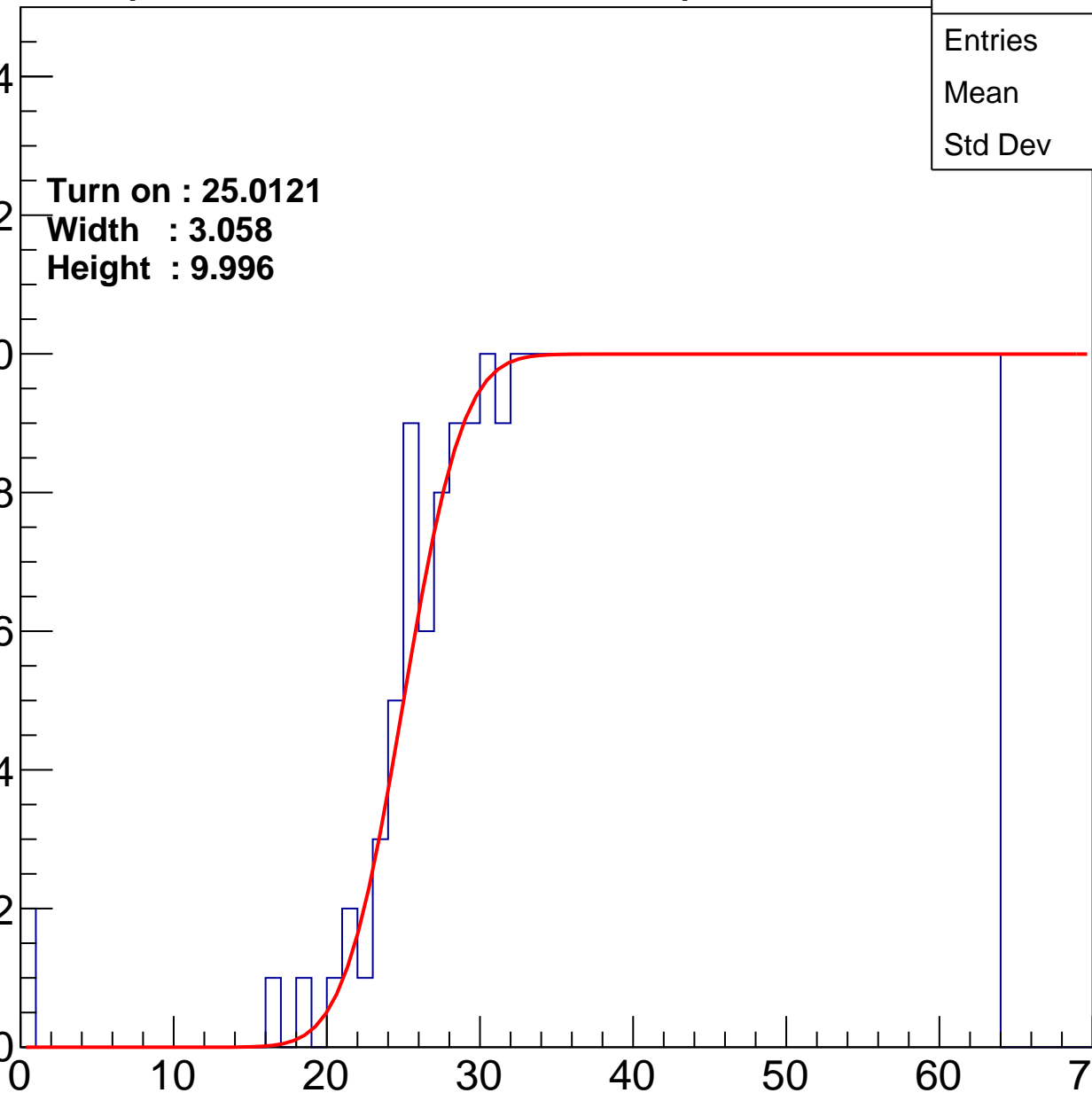
Width : 3.058

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch55

calib_packv5_042523_0143.root, FC#11, port A2

Entries	407
Mean	42.9
Std Dev	12.27

Turn on : 23.5844

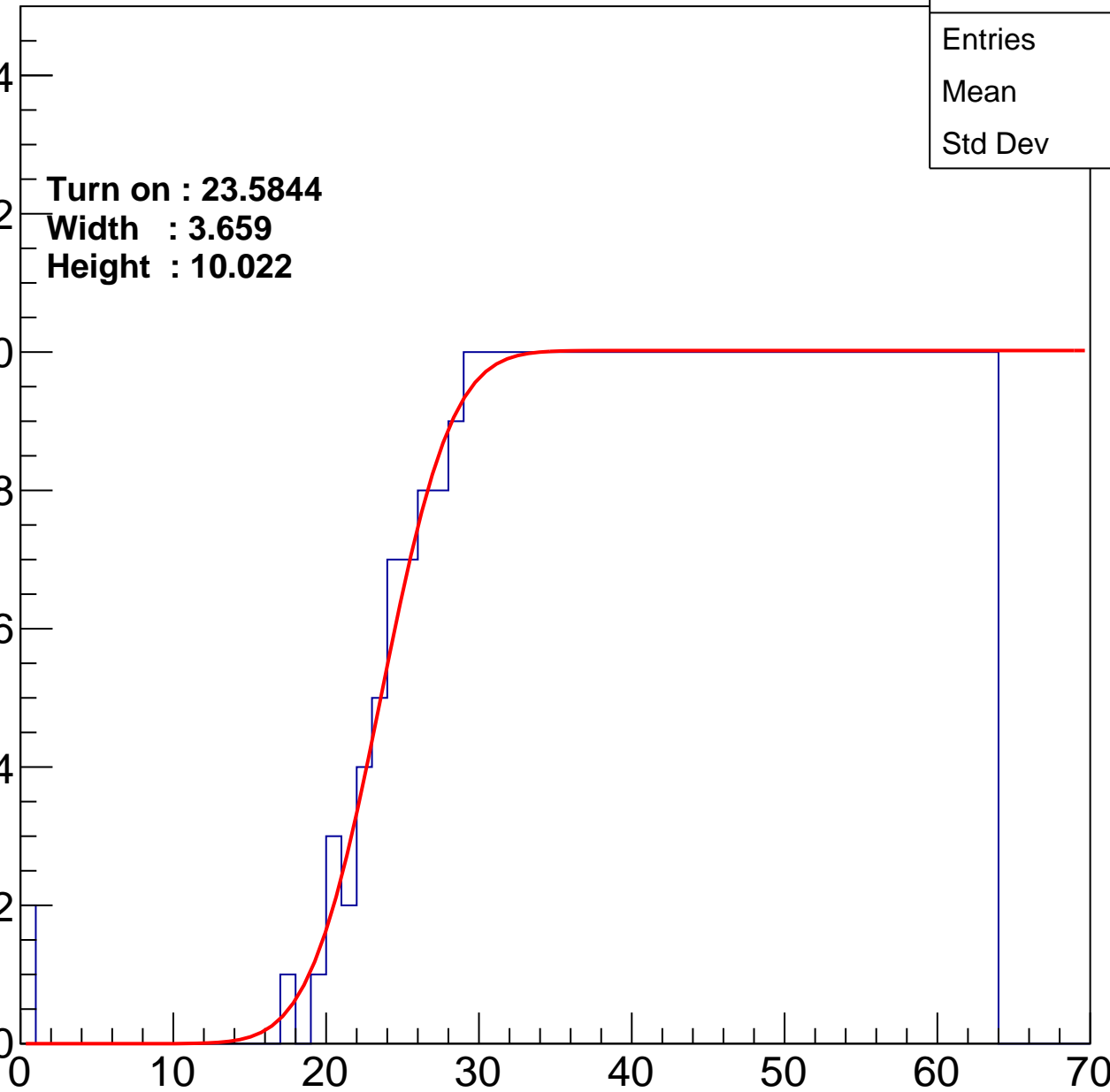
Width : 3.659

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch56

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.72
Std Dev	11.81

Turn on : 25.4907

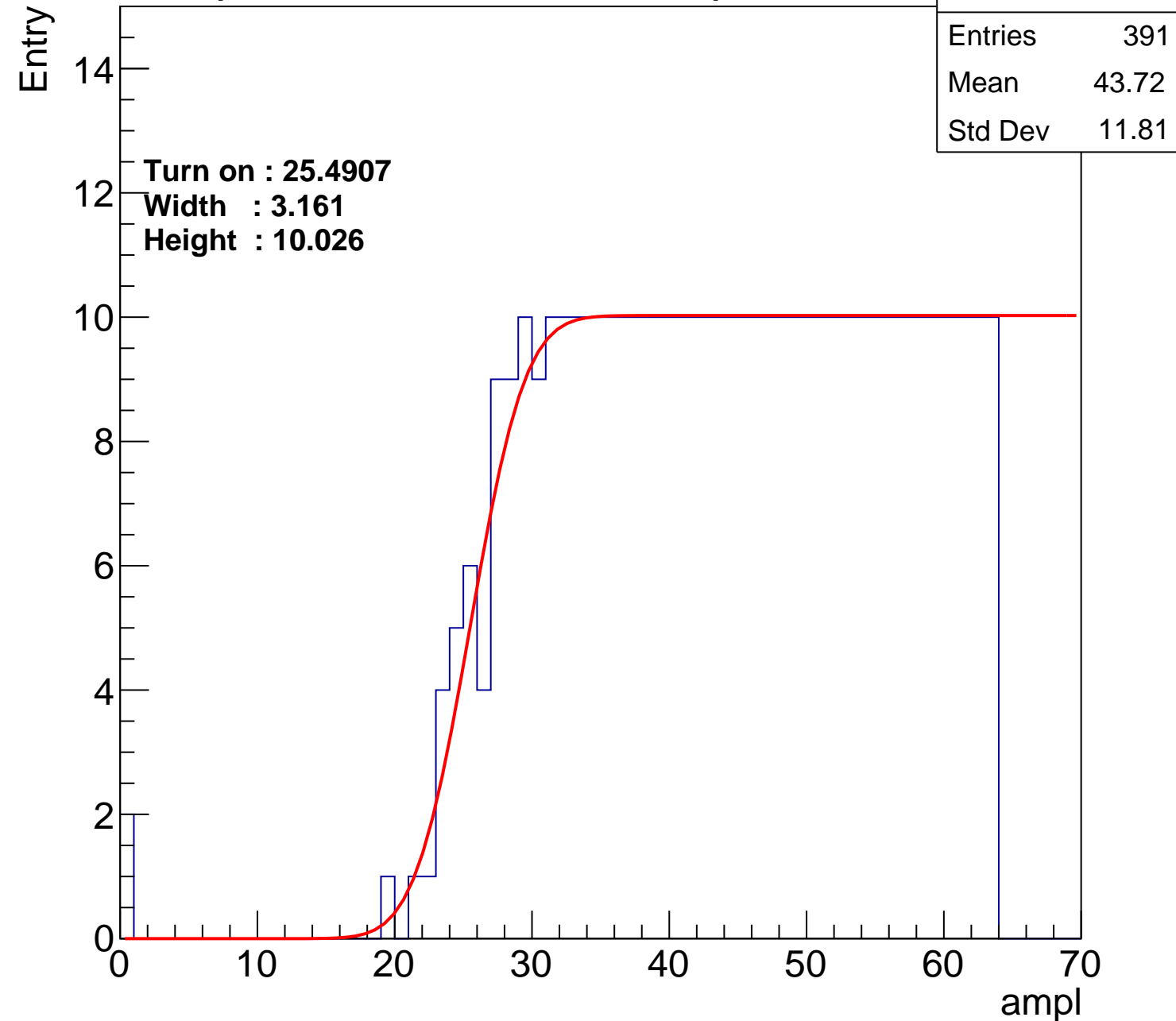
Width : 3.161

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch57

calib_packv5_042523_0143.root, FC#11, port A2

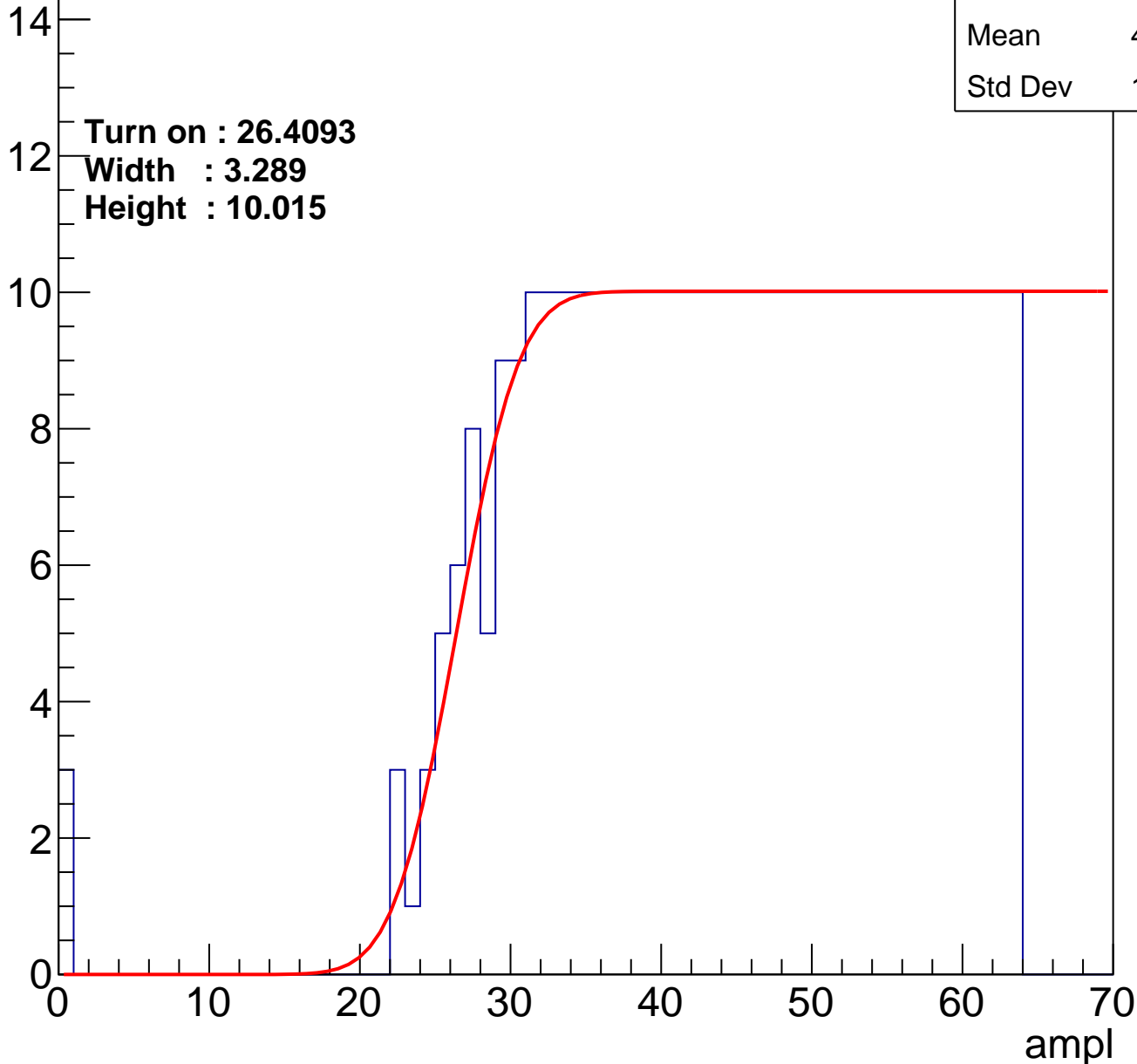
Entries	382
Mean	44.08
Std Dev	11.77

Turn on : 26.4093

Width : 3.289

Height : 10.015

Entry



B1L102S, U25-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.93
Std Dev	11.05

Turn on : 27.5500

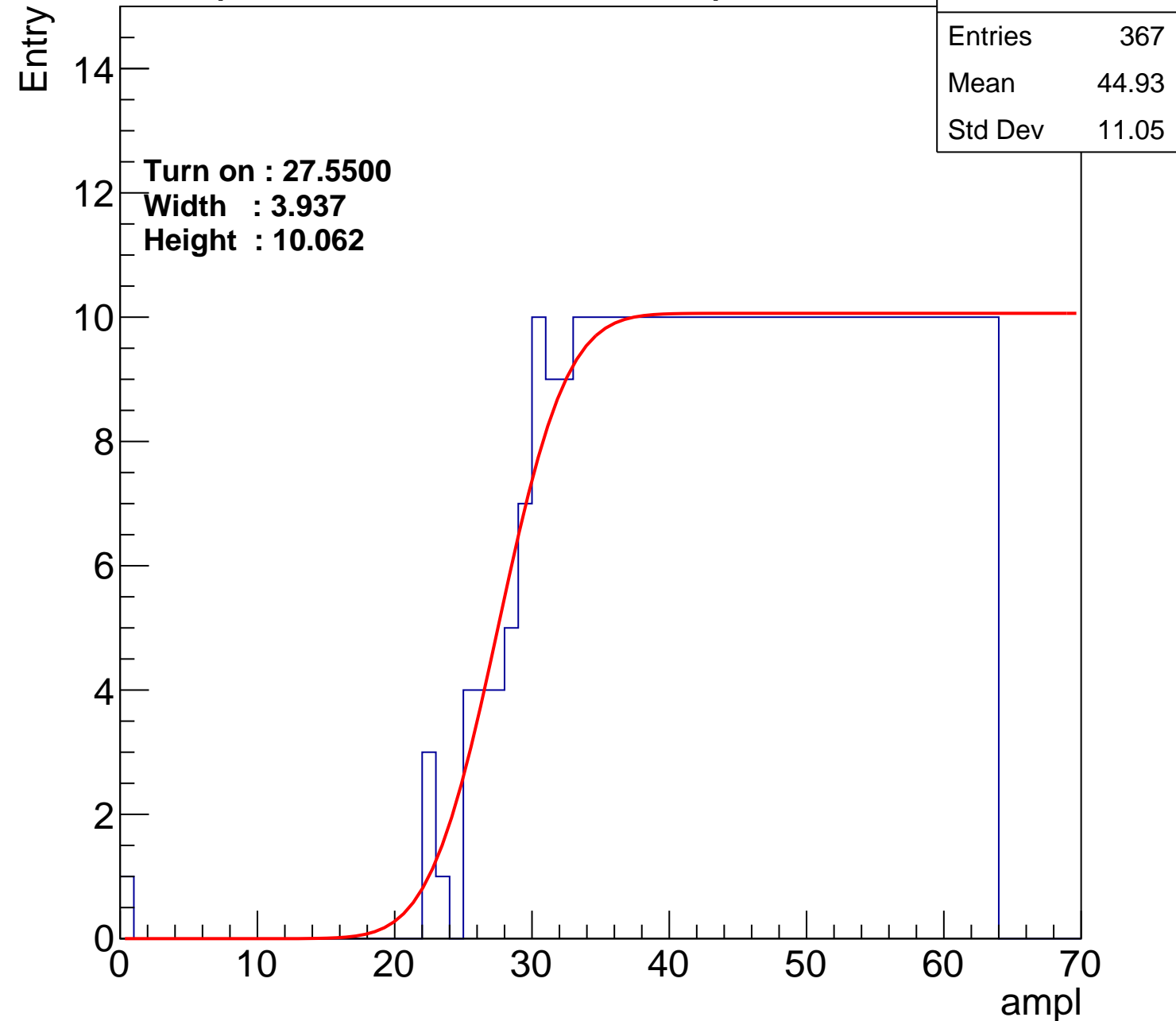
Width : 3.937

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.22
Std Dev	11.54

Turn on : 26.0730

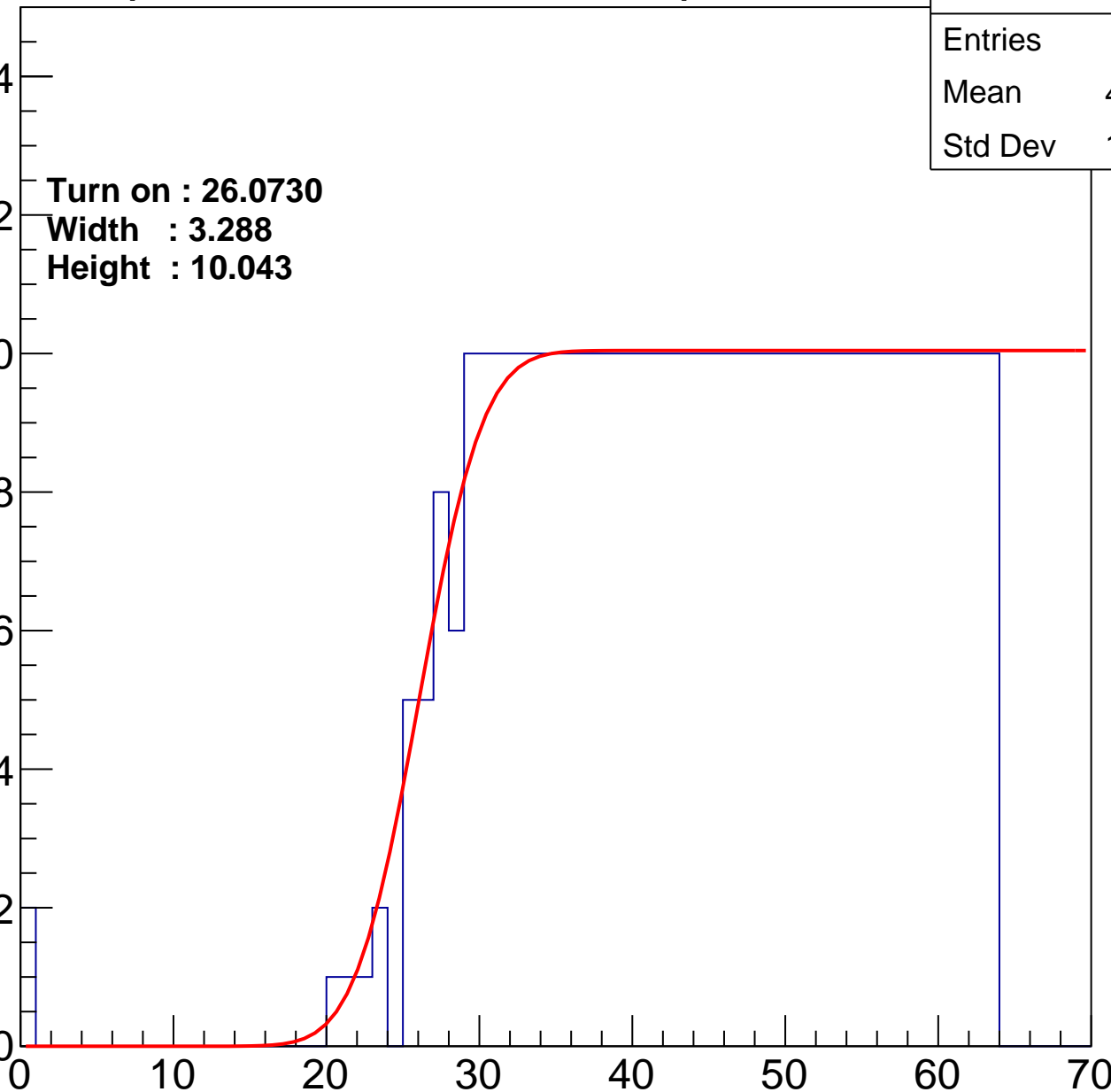
Width : 3.288

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch60

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.89
Std Dev	11.75

Turn on : 26.2128

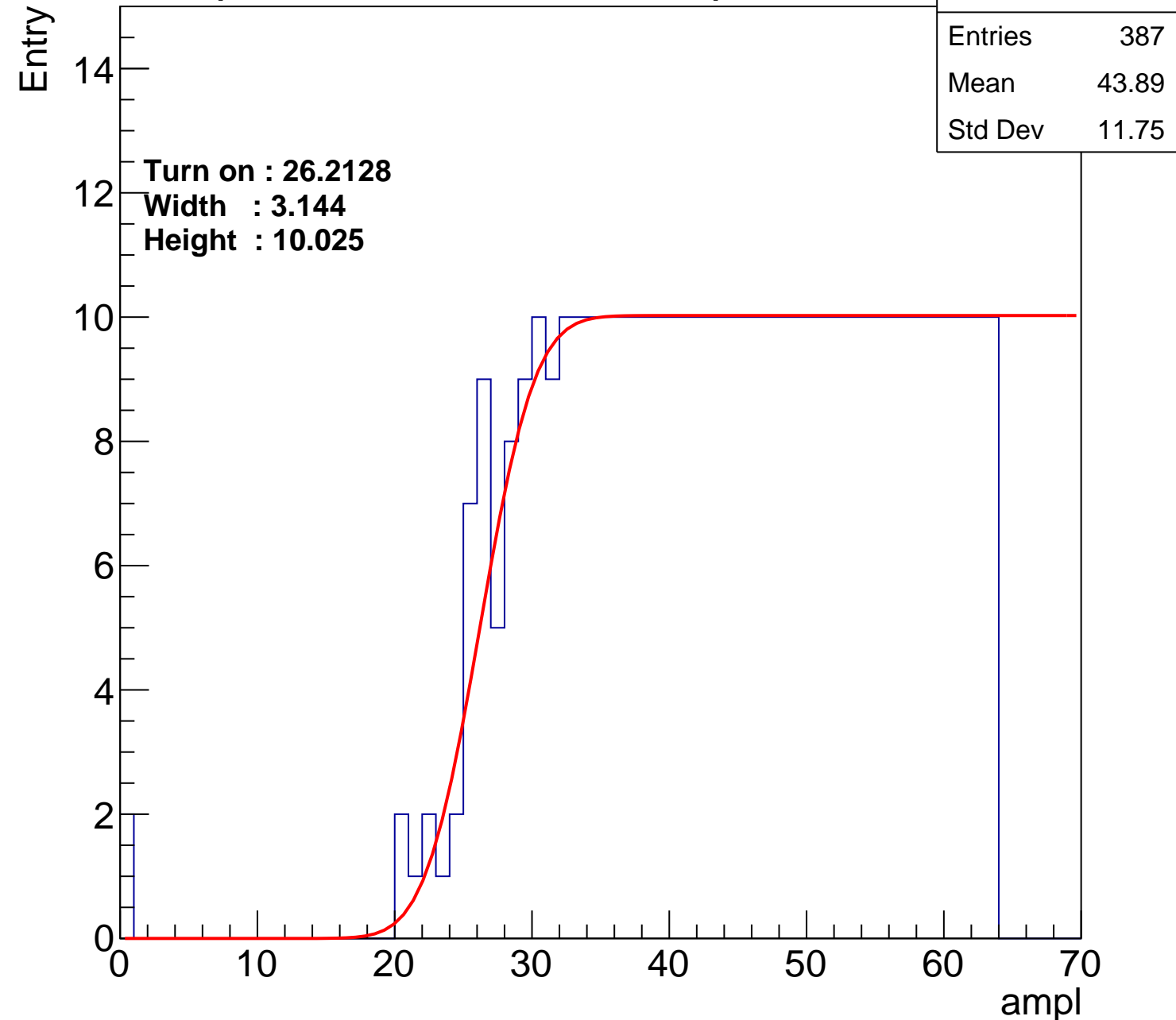
Width : 3.144

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch61

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.22
Std Dev	11.57

Turn on : 25.8923

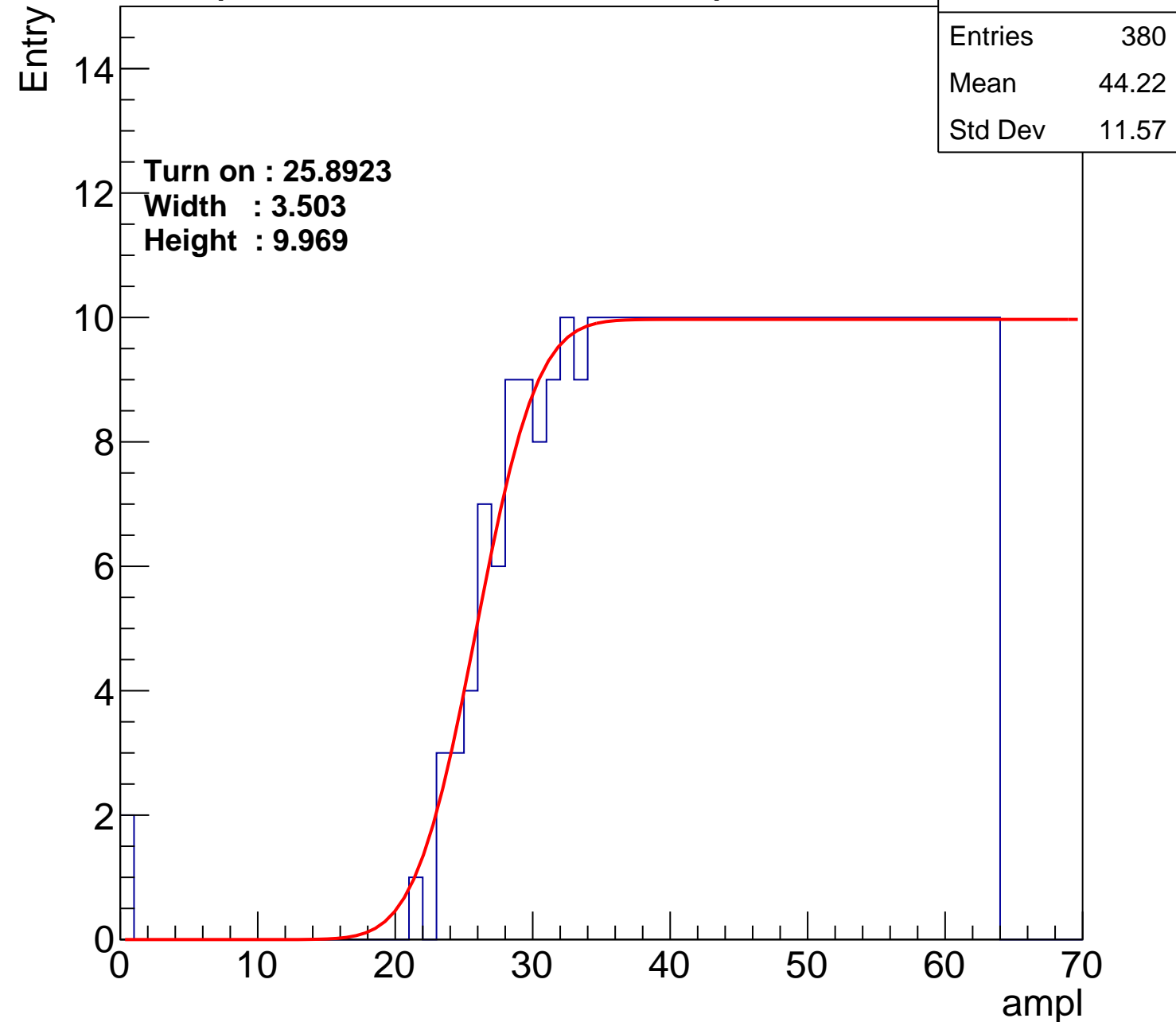
Width : 3.503

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch62

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.24
Std Dev	11.56

Turn on : 26.3790

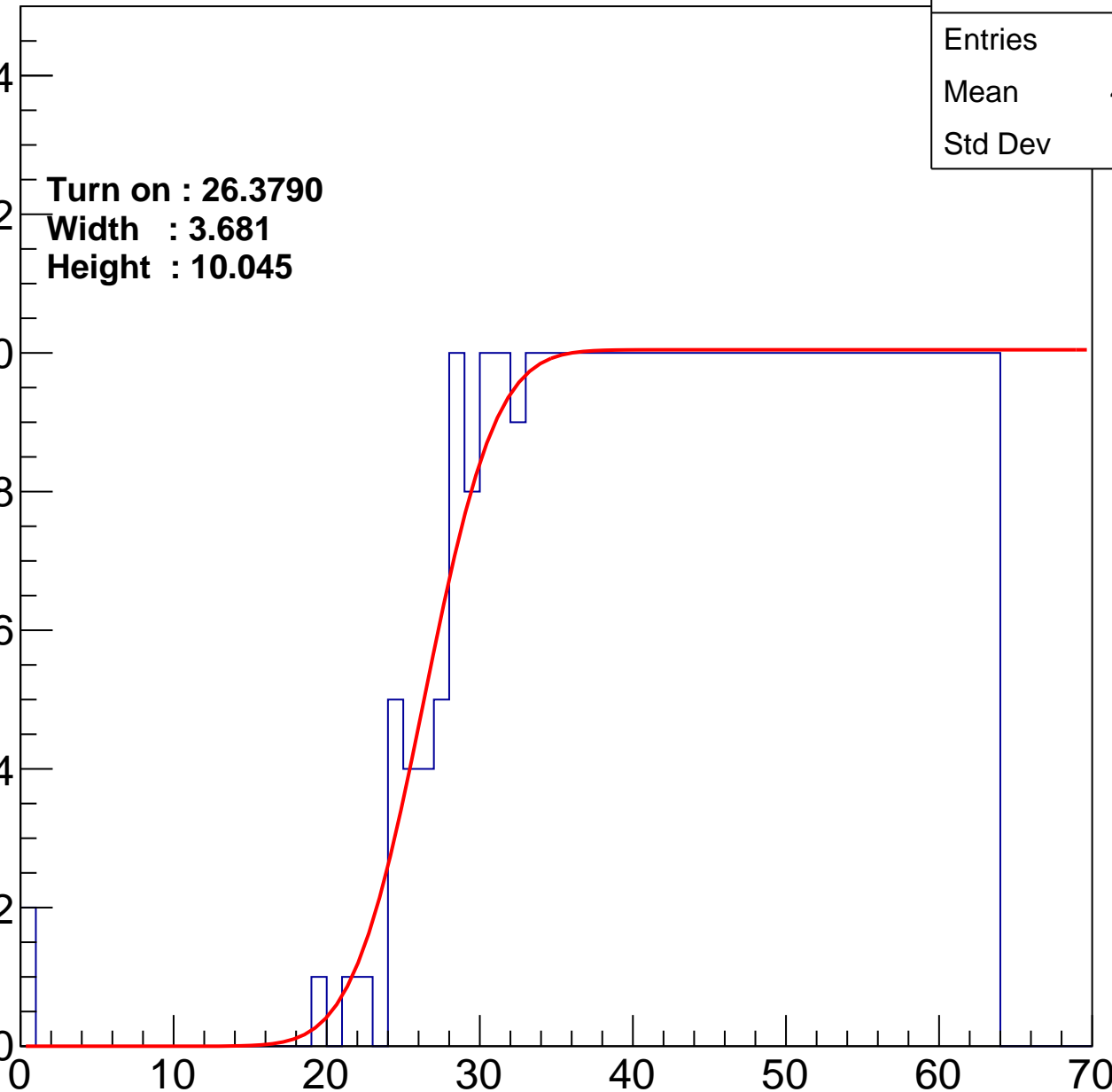
Width : 3.681

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch63

calib_packv5_042523_0143.root, FC#11, port A2

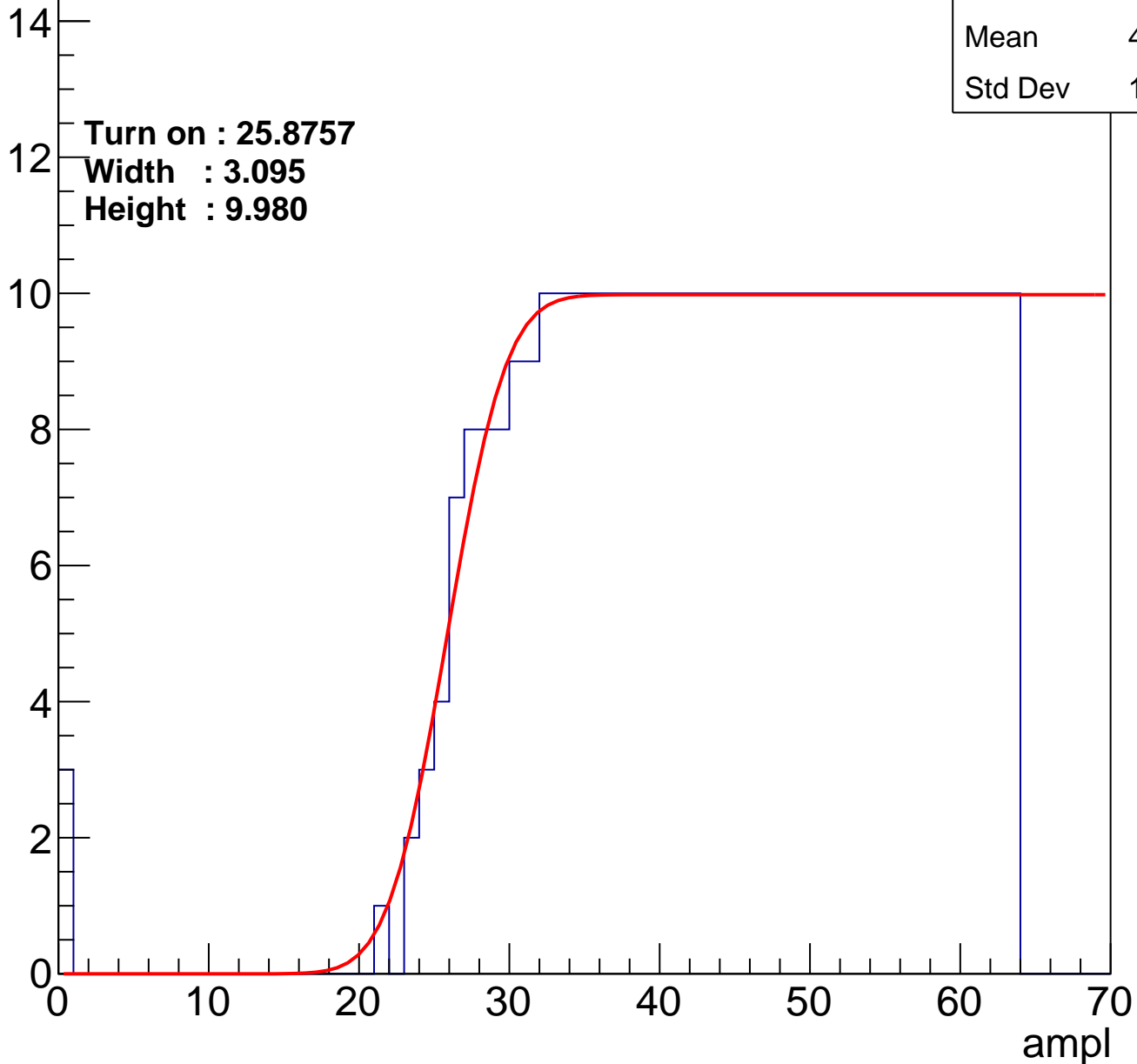
Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 25.8757

Width : 3.095

Height : 9.980

Entry



B1L102S, U25-ch64

calib_packv5_042523_0143.root, FC#11, port A2

Entries	359
Mean	45.28
Std Dev	11

Turn on : 28.3770

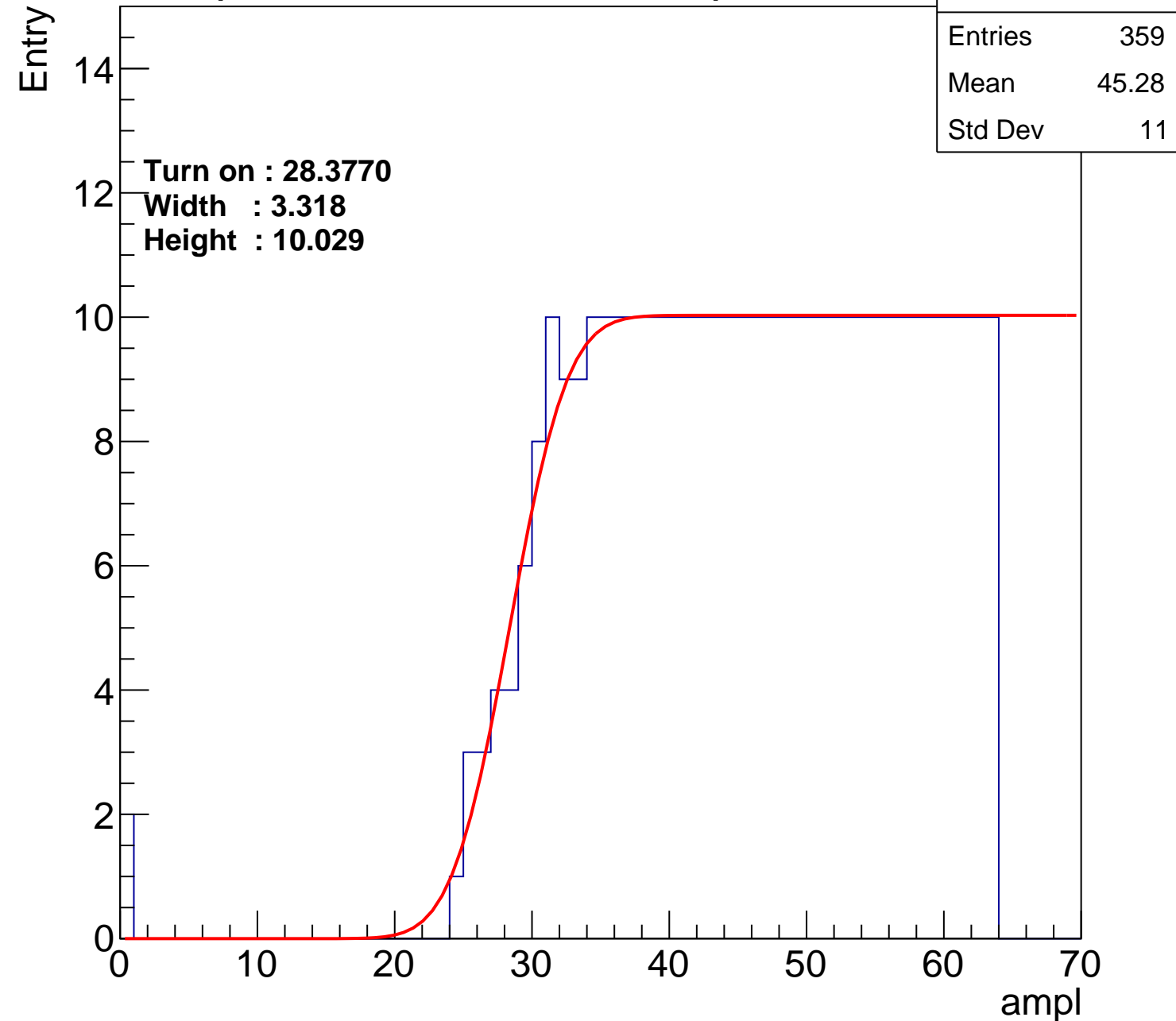
Width : 3.318

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch65

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.85
Std Dev	11.21

Turn on : 27.4711

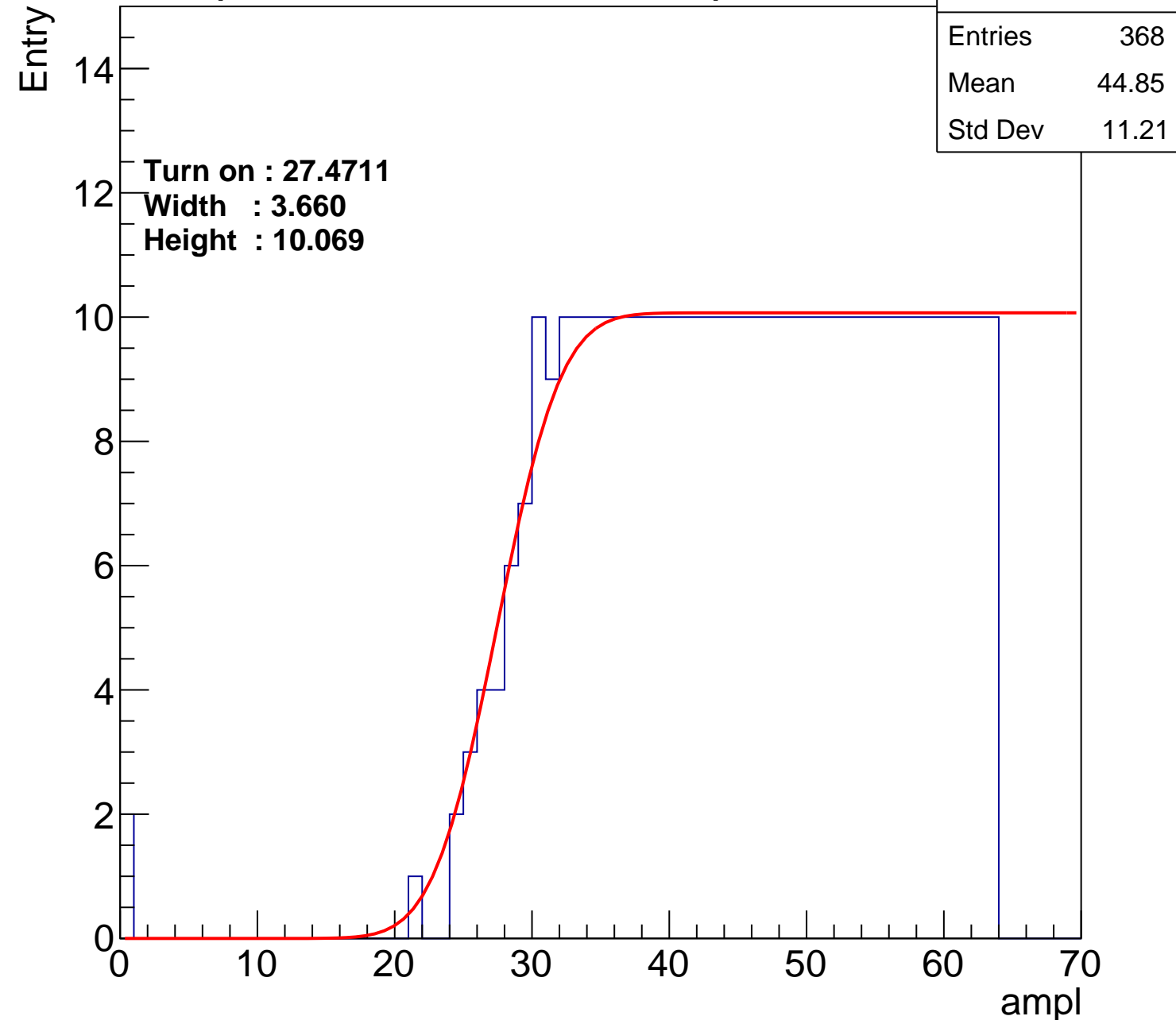
Width : 3.660

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch66

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.64
Std Dev	11.83

Turn on : 25.2717

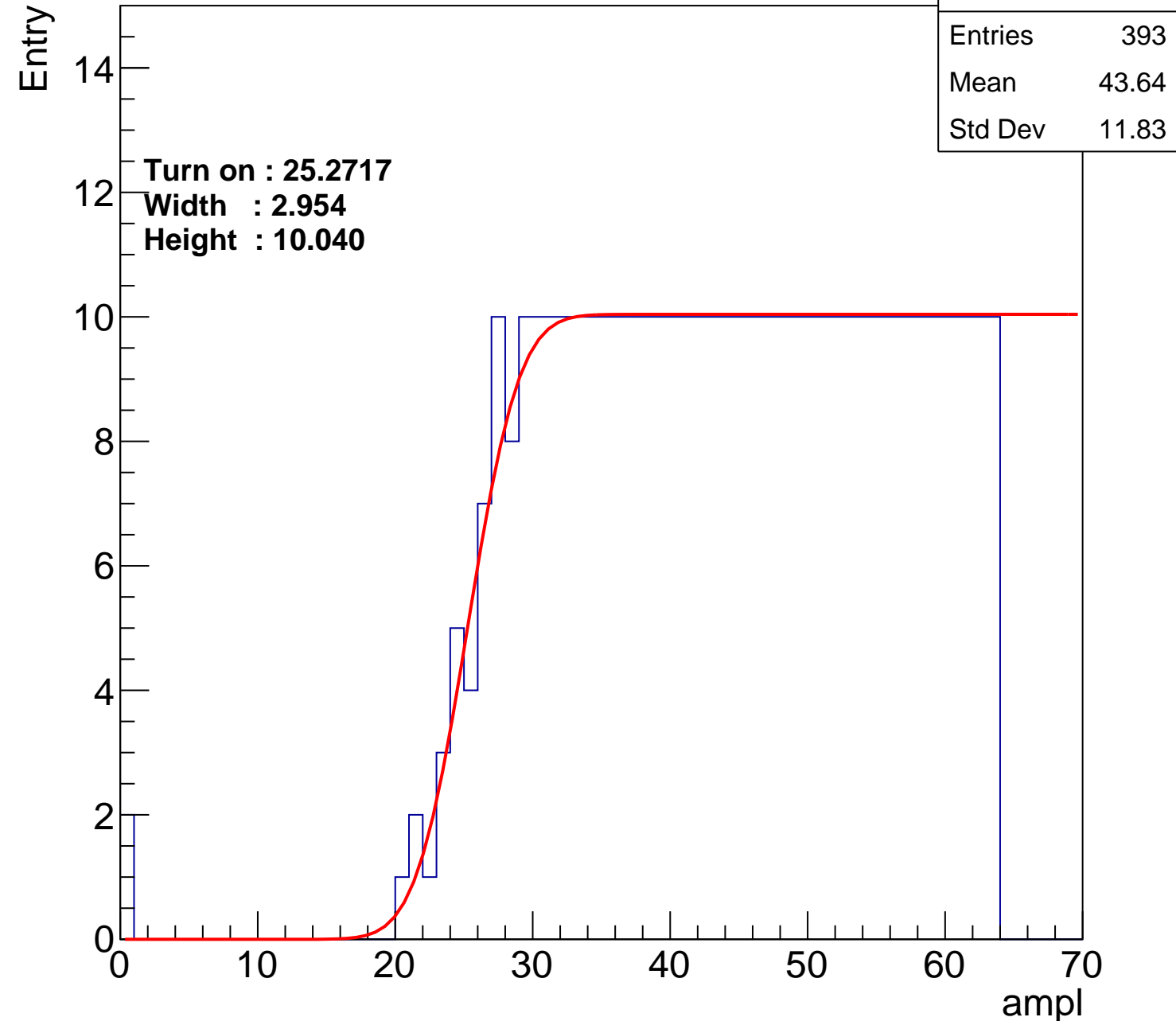
Width : 2.954

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch67

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.79
Std Dev	11.92

Turn on : 25.5373

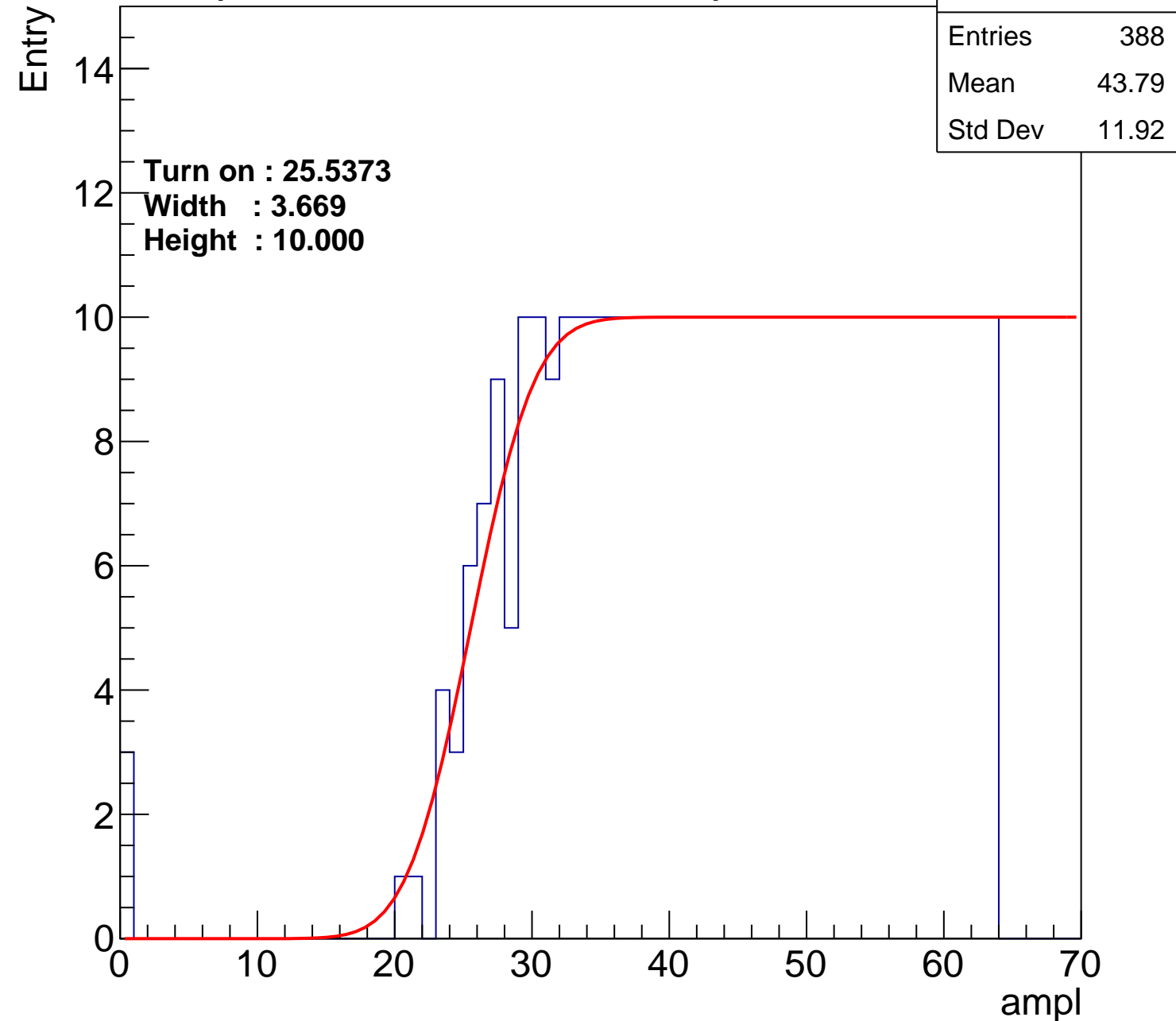
Width : 3.669

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch68

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.47
Std Dev	12.25

Turn on : 25.6863

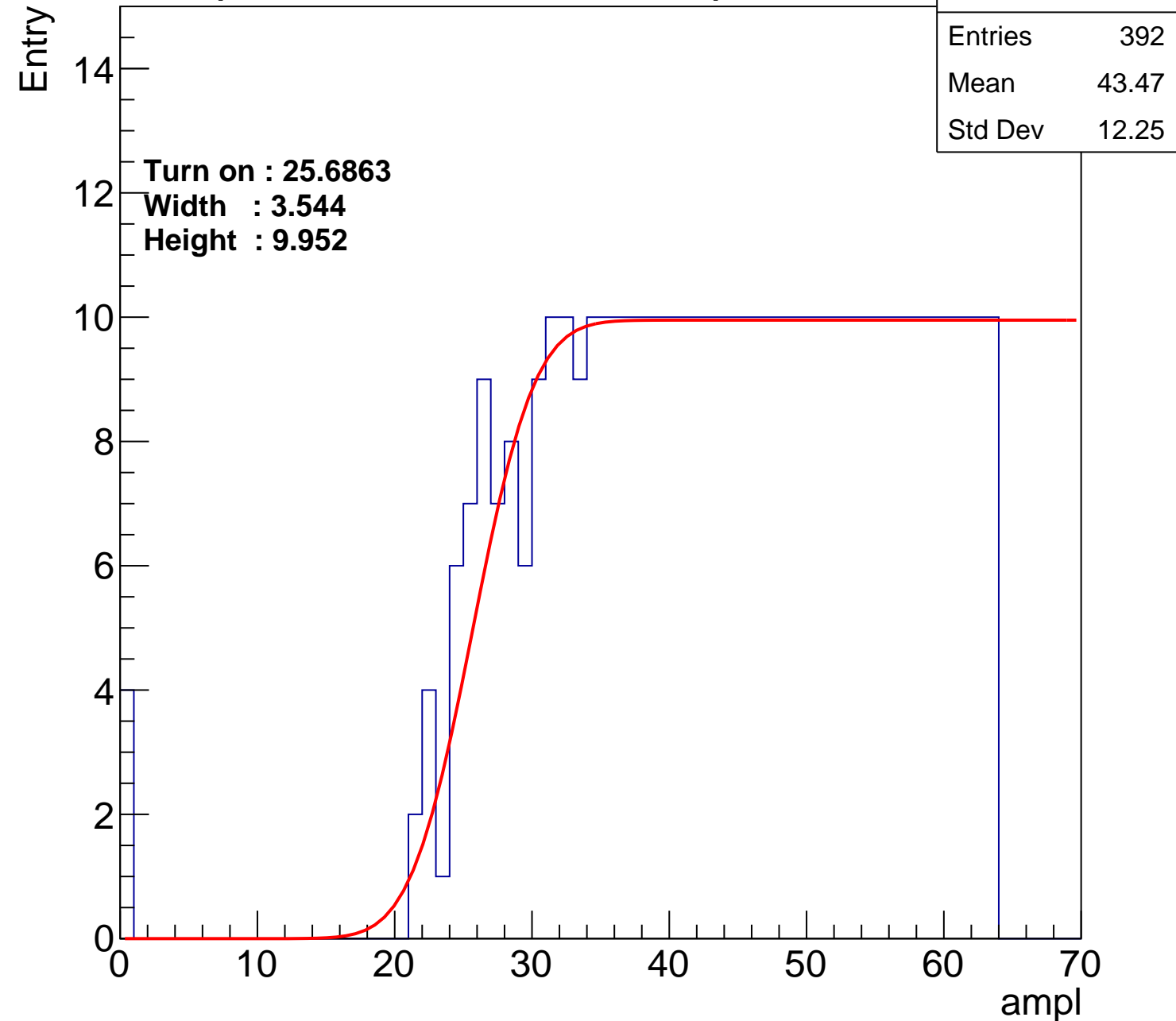
Width : 3.544

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch69

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.35
Std Dev	12.13

Turn on : 24.5851

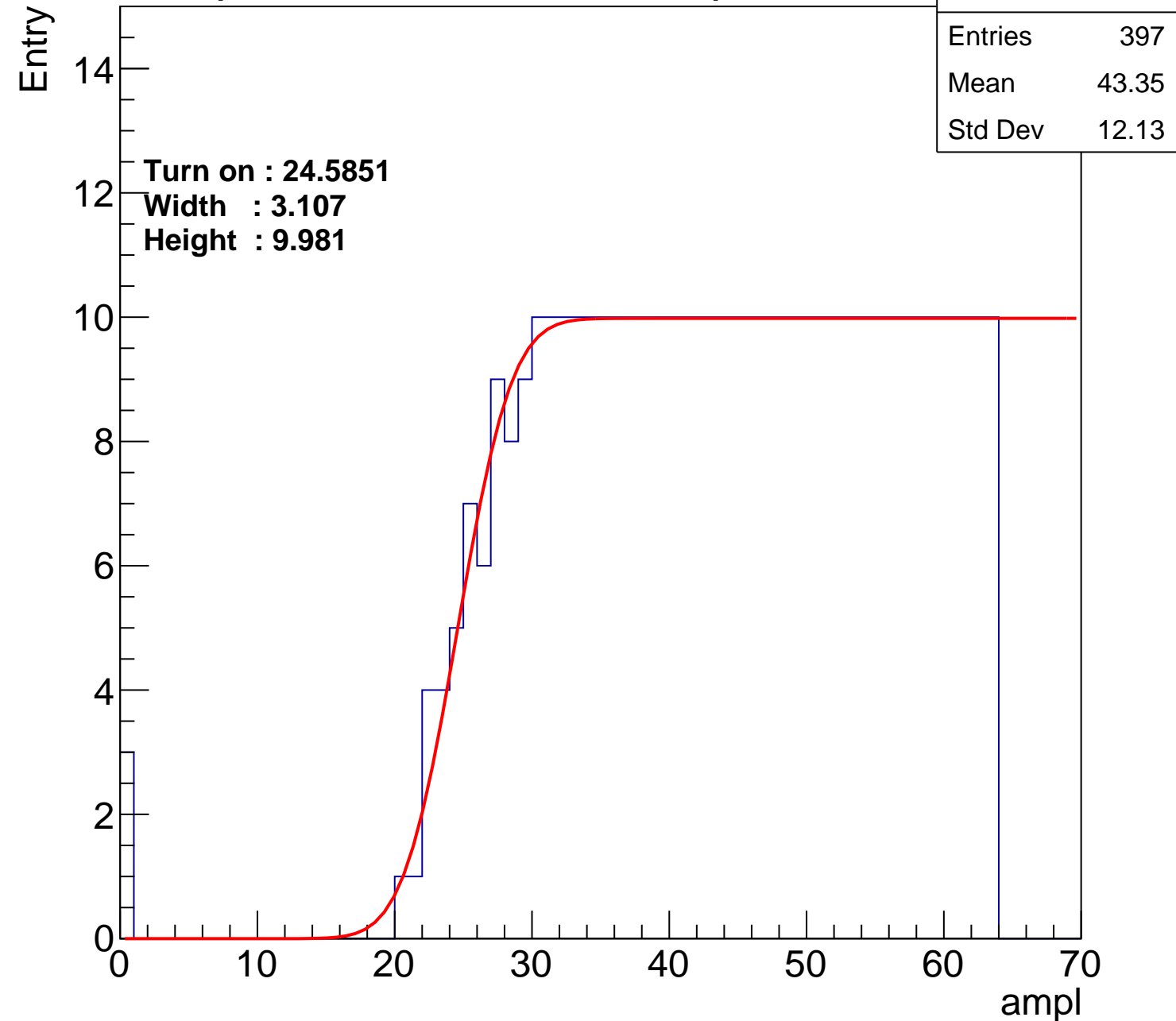
Width : 3.107

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch70

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.49
Std Dev	11.74

Turn on : 27.0988

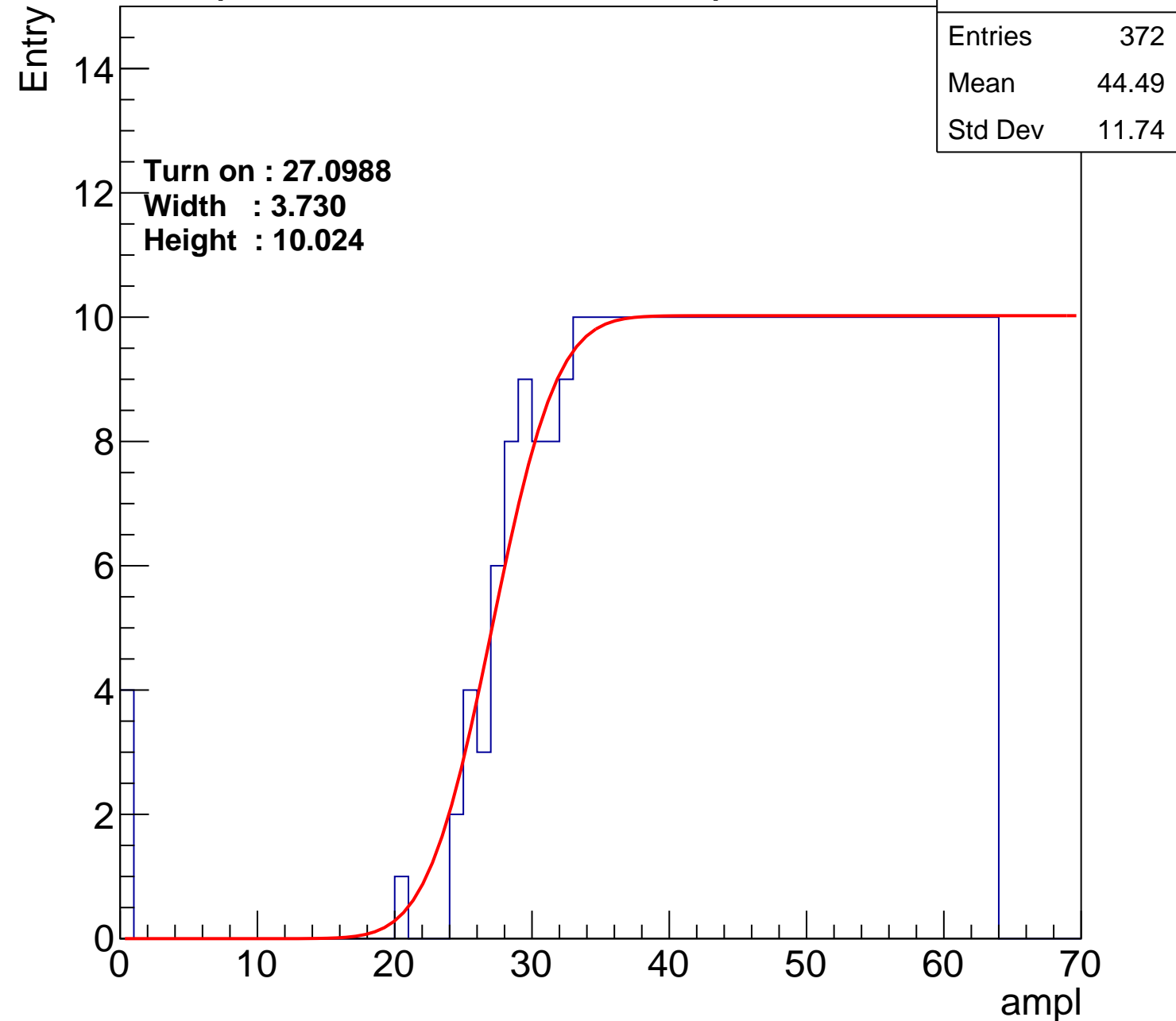
Width : 3.730

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch71

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.84
Std Dev	11.36

Turn on : 27.5601

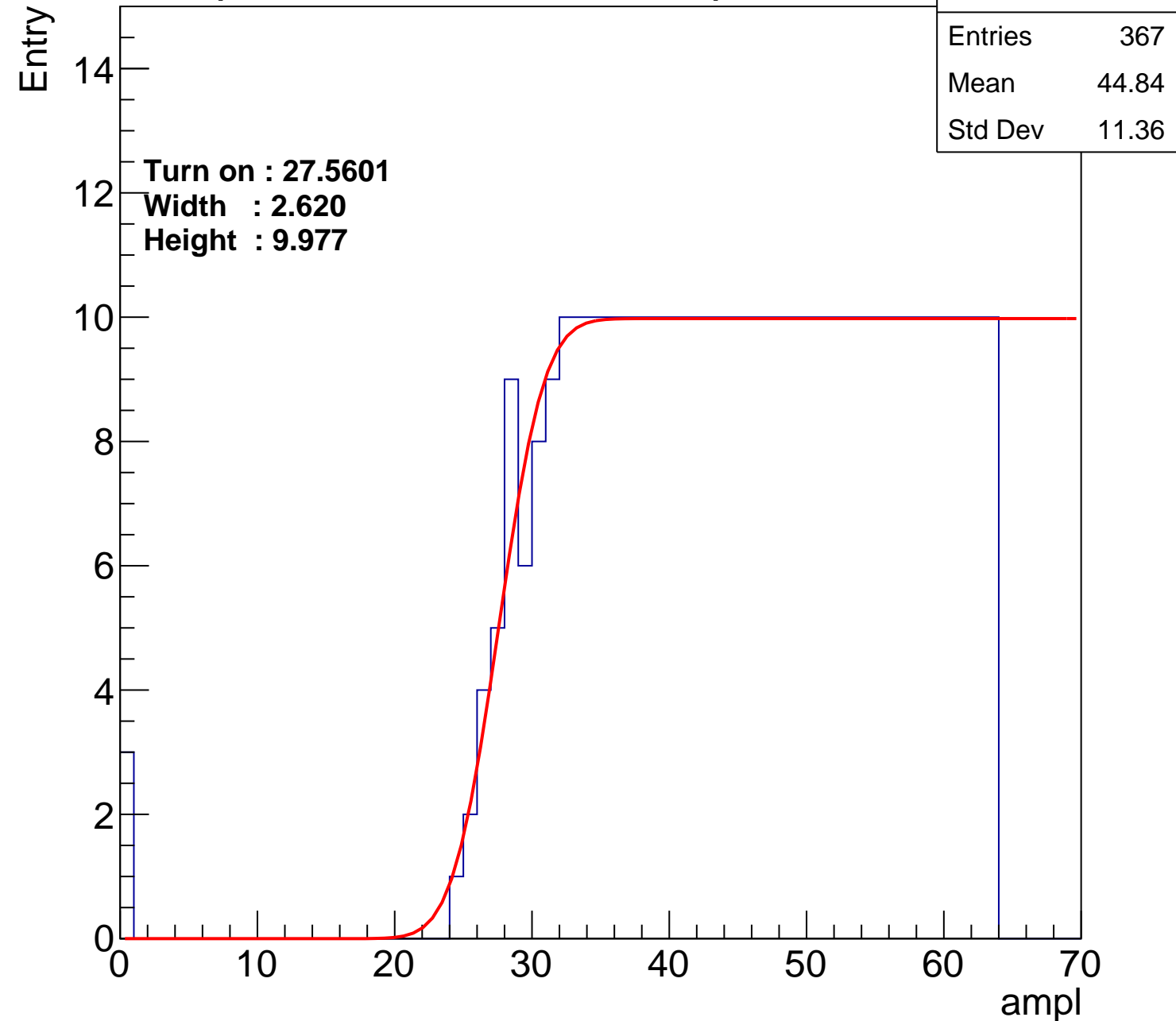
Width : 2.620

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch72

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.31
Std Dev	12.03

Turn on : 24.3038

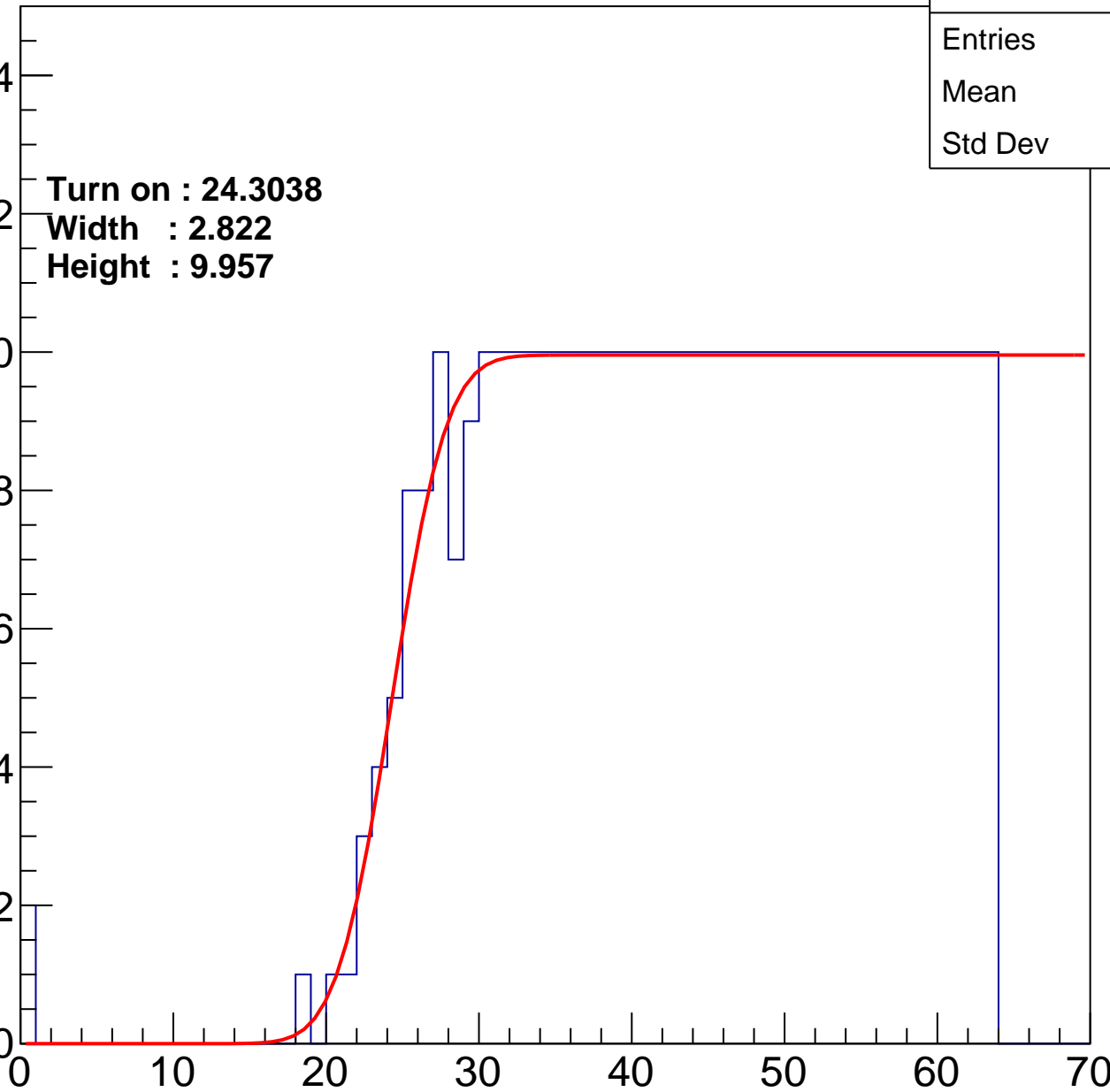
Width : 2.822

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch73

calib_packv5_042523_0143.root, FC#11, port A2

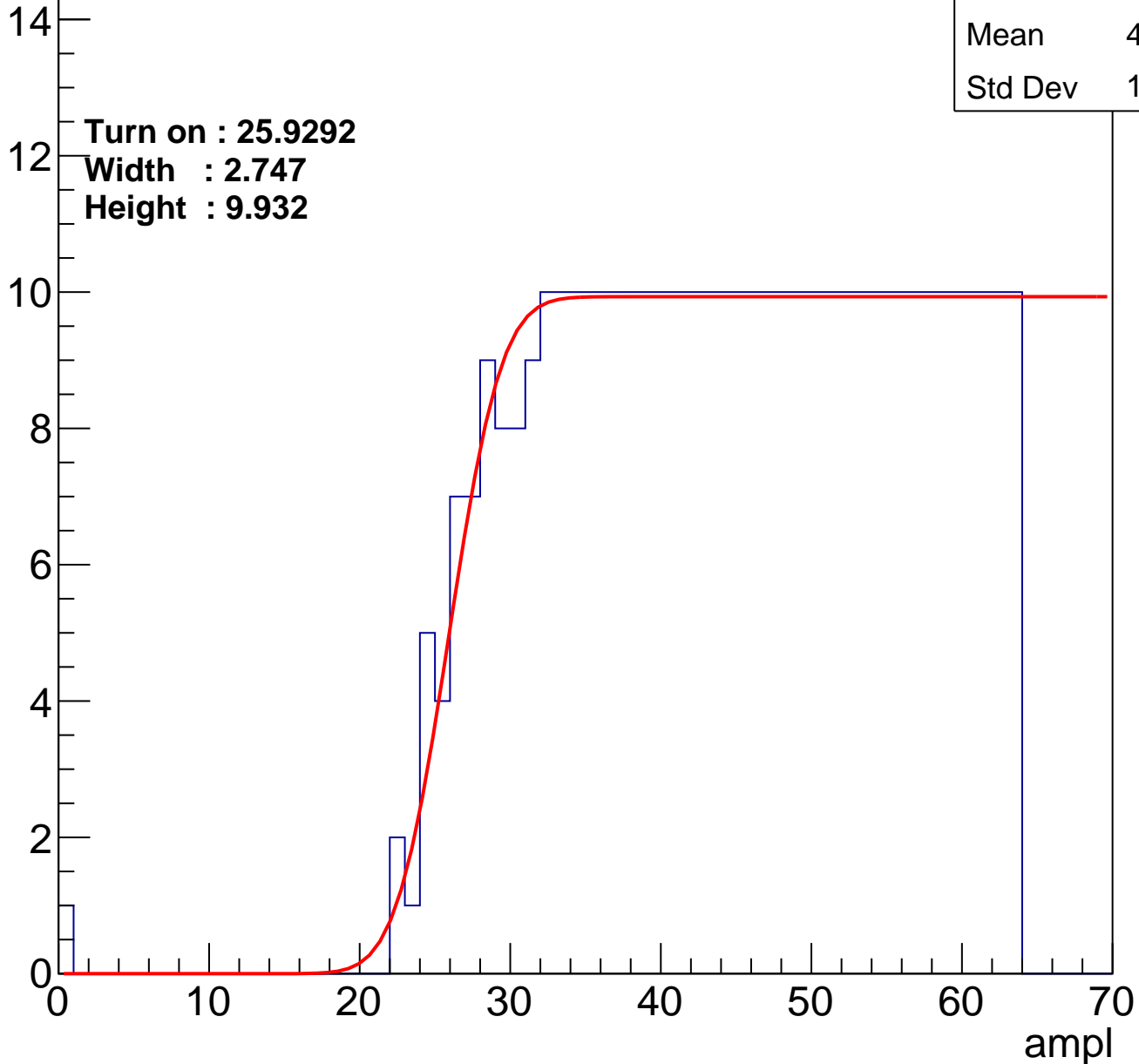
Entries	381
Mean	44.25
Std Dev	11.39

Turn on : 25.9292

Width : 2.747

Height : 9.932

Entry



B1L102S, U25-ch74

calib_packv5_042523_0143.root, FC#11, port A2

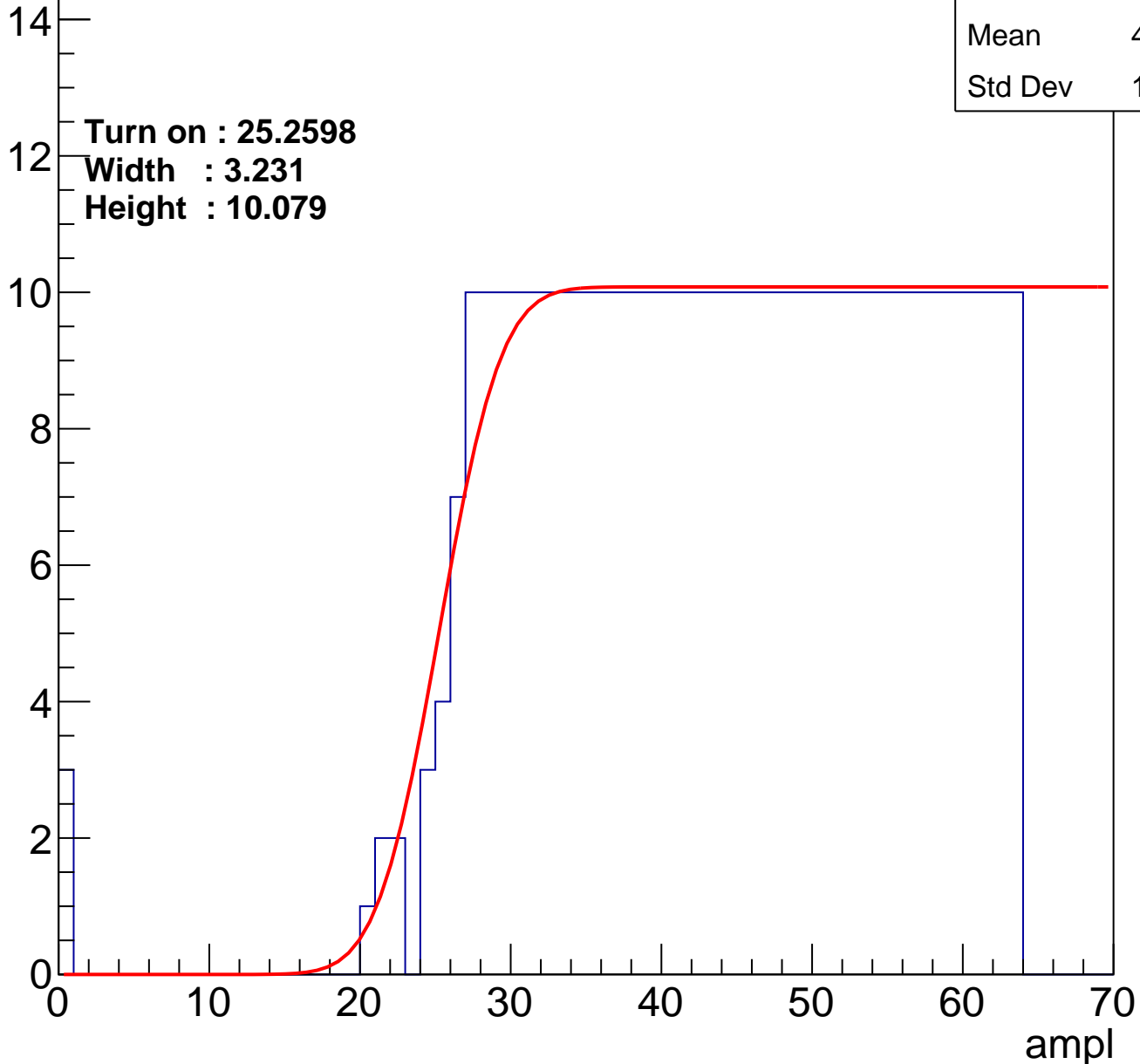
Entries	392
Mean	43.65
Std Dev	11.93

Turn on : 25.2598

Width : 3.231

Height : 10.079

Entry



B1L102S, U25-ch75

calib_packv5_042523_0143.root, FC#11, port A2

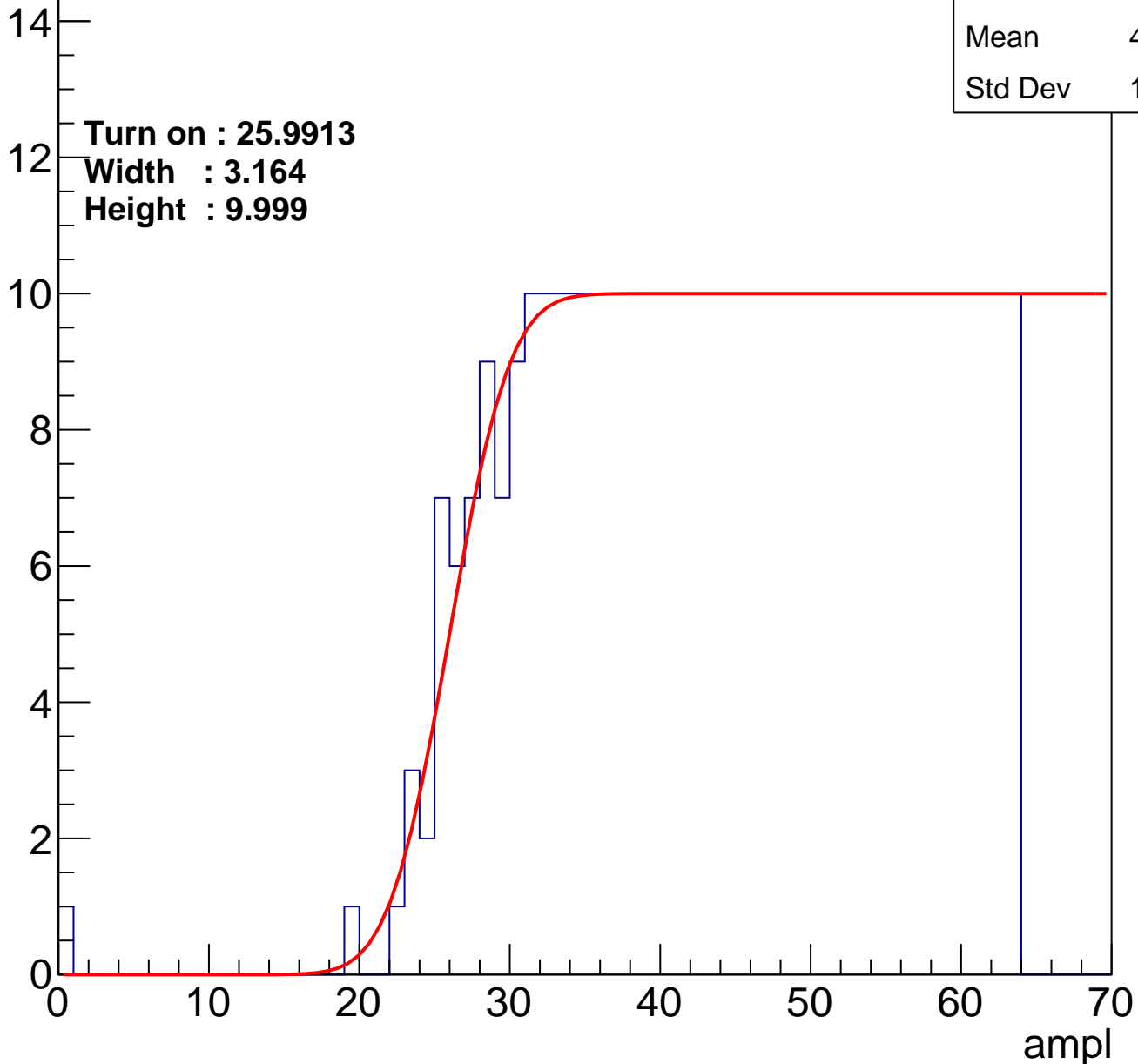
Entries	383
Mean	44.16
Std Dev	11.45

Turn on : 25.9913

Width : 3.164

Height : 9.999

Entry



B1L102S, U25-ch76

calib_packv5_042523_0143.root, FC#11, port A2

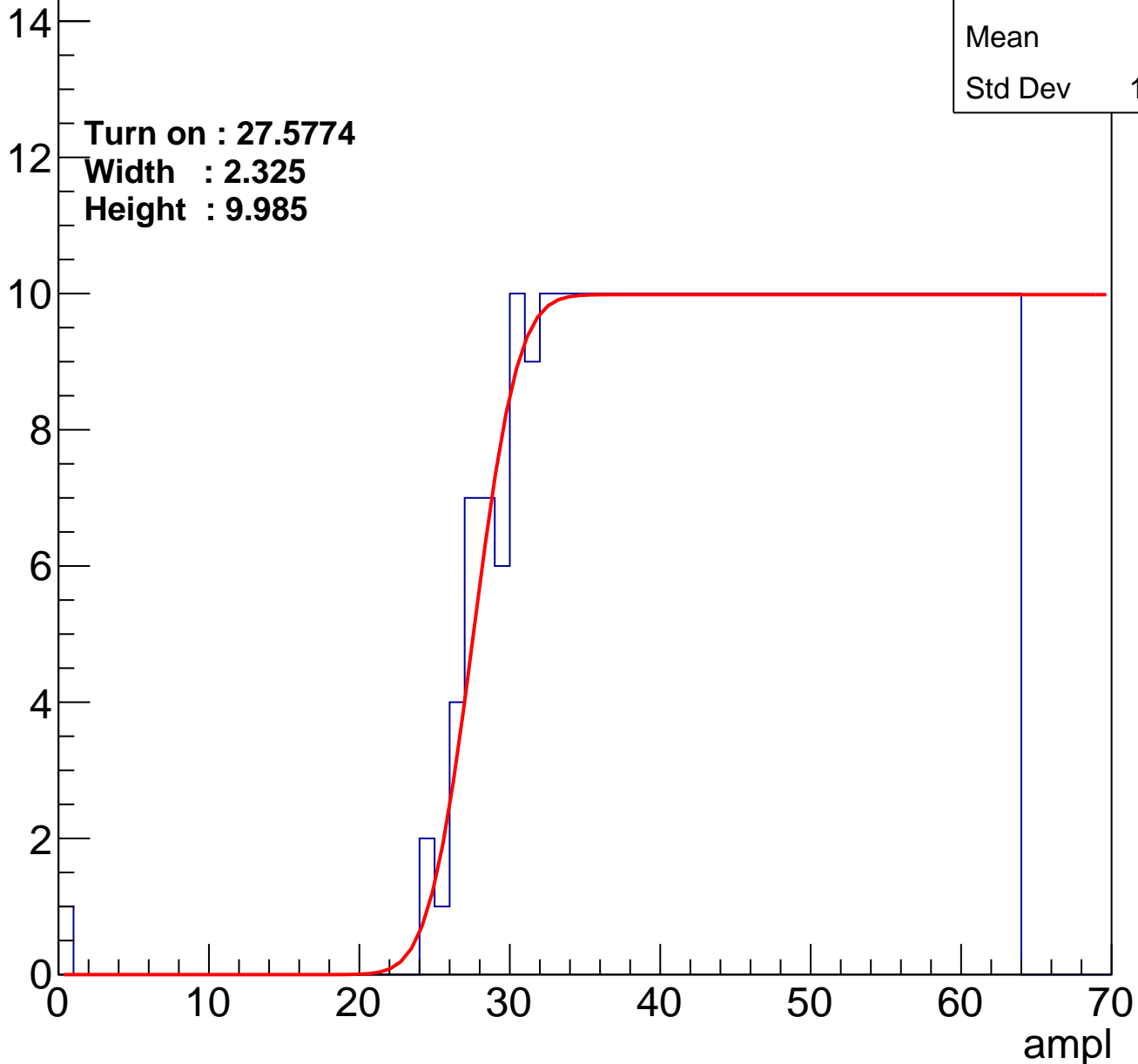
Entries	367
Mean	45
Std Dev	10.93

Turn on : 27.5774

Width : 2.325

Height : 9.985

Entry



B1L102S, U25-ch77

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 28.0726

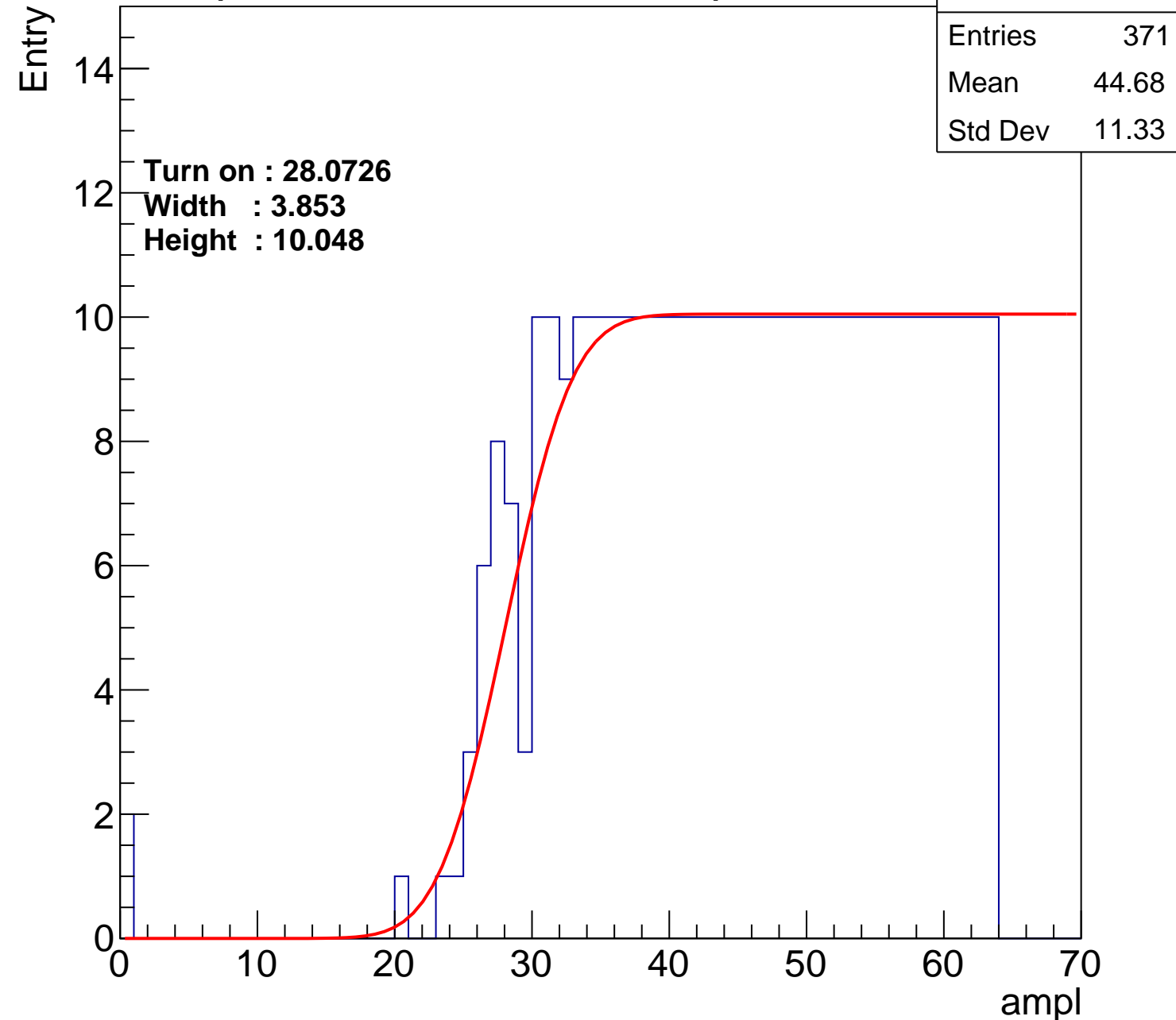
Width : 3.853

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch78

calib_packv5_042523_0143.root, FC#11, port A2

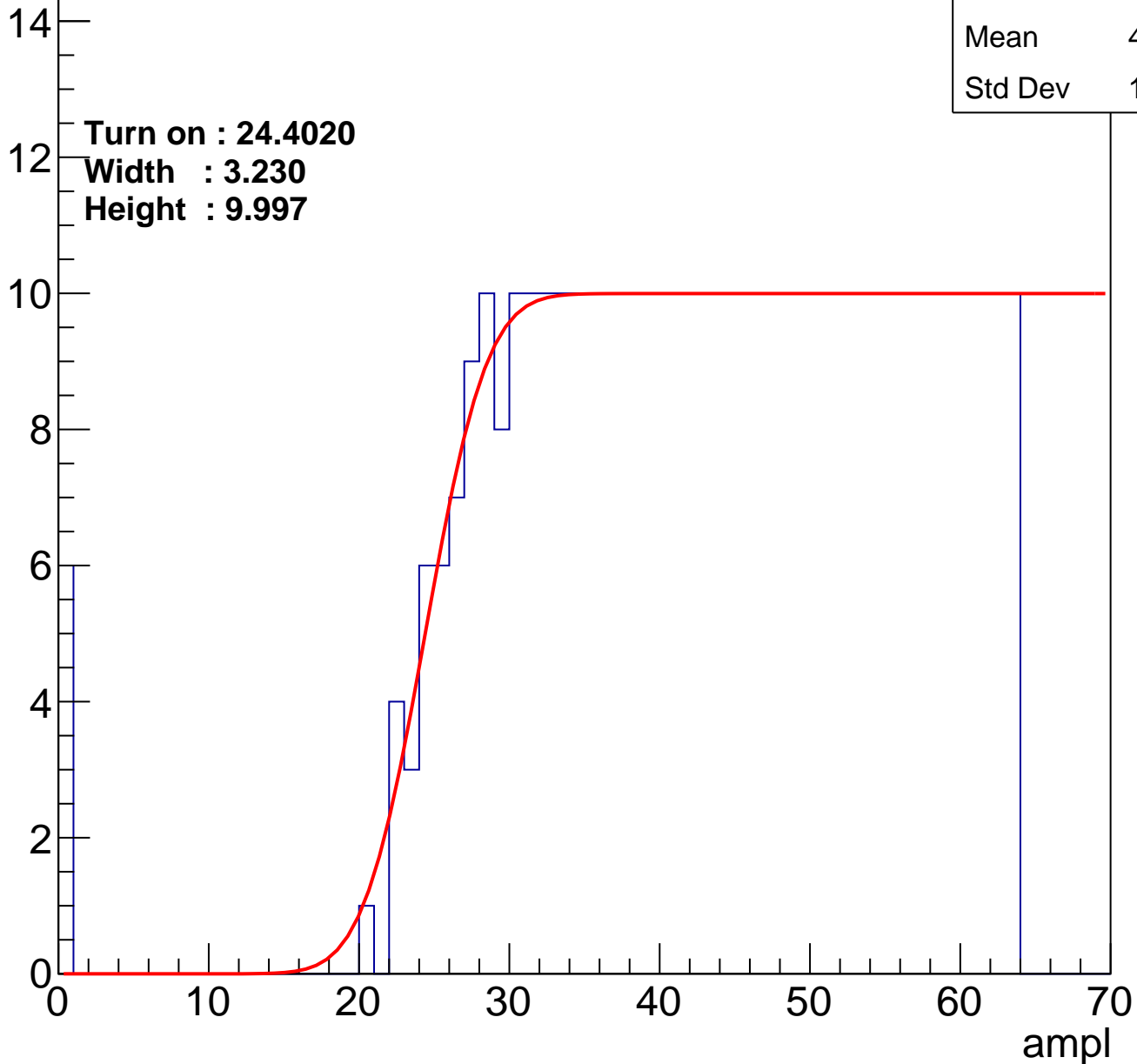
Entries	400
Mean	43.05
Std Dev	12.62

Turn on : 24.4020

Width : 3.230

Height : 9.997

Entry



calib_packv5_042523_0143.root, FC#11, port A2

calib_packv5_042523_0143.root, FC#11, port A2

Turn on : 27.4272
Width : 2.696
Height : 10.033



calib_packv5_042523_0143.root, FC#11, port A2

calib_packv5_042523_0143.root, FC#11, port A2

Turn on : 25.4412
Width : 3.170
Height : 9.996



B1L102S, U25-ch81

calib_packv5_042523_0143.root, FC#11, port A2

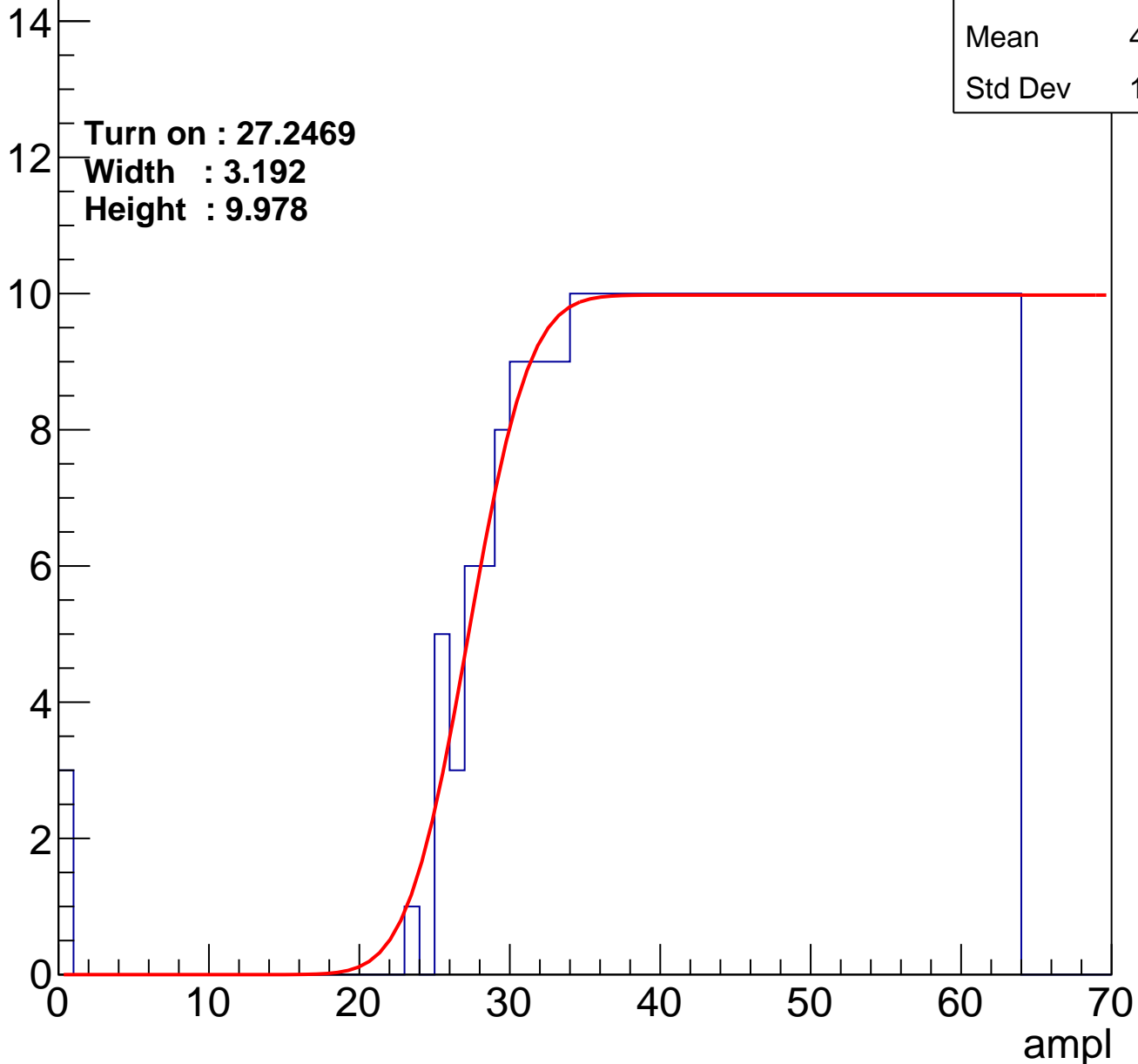
Entries	368
Mean	44.76
Std Dev	11.43

Turn on : 27.2469

Width : 3.192

Height : 9.978

Entry



B1L102S, U25-ch82

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.5076

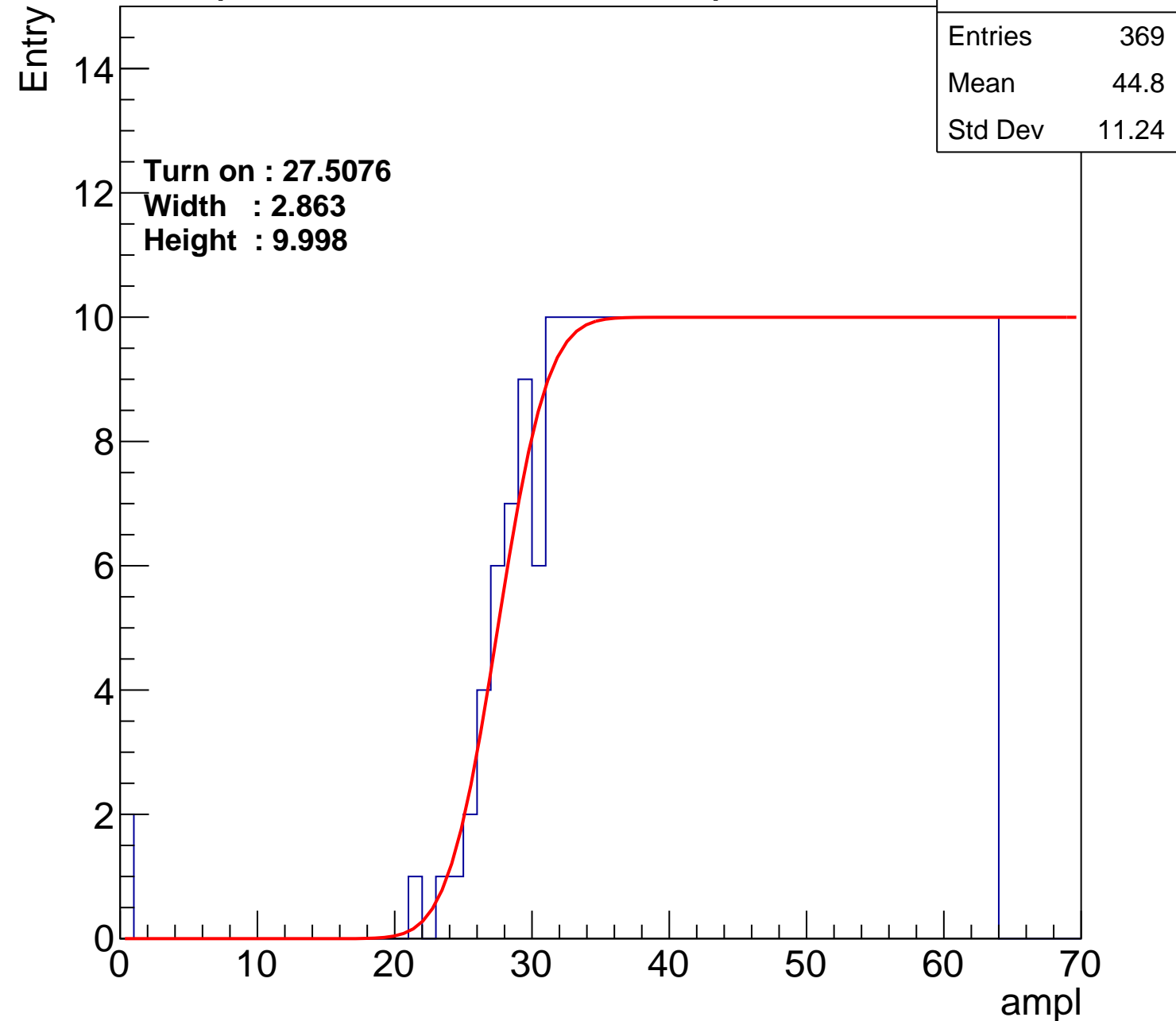
Width : 2.863

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch83

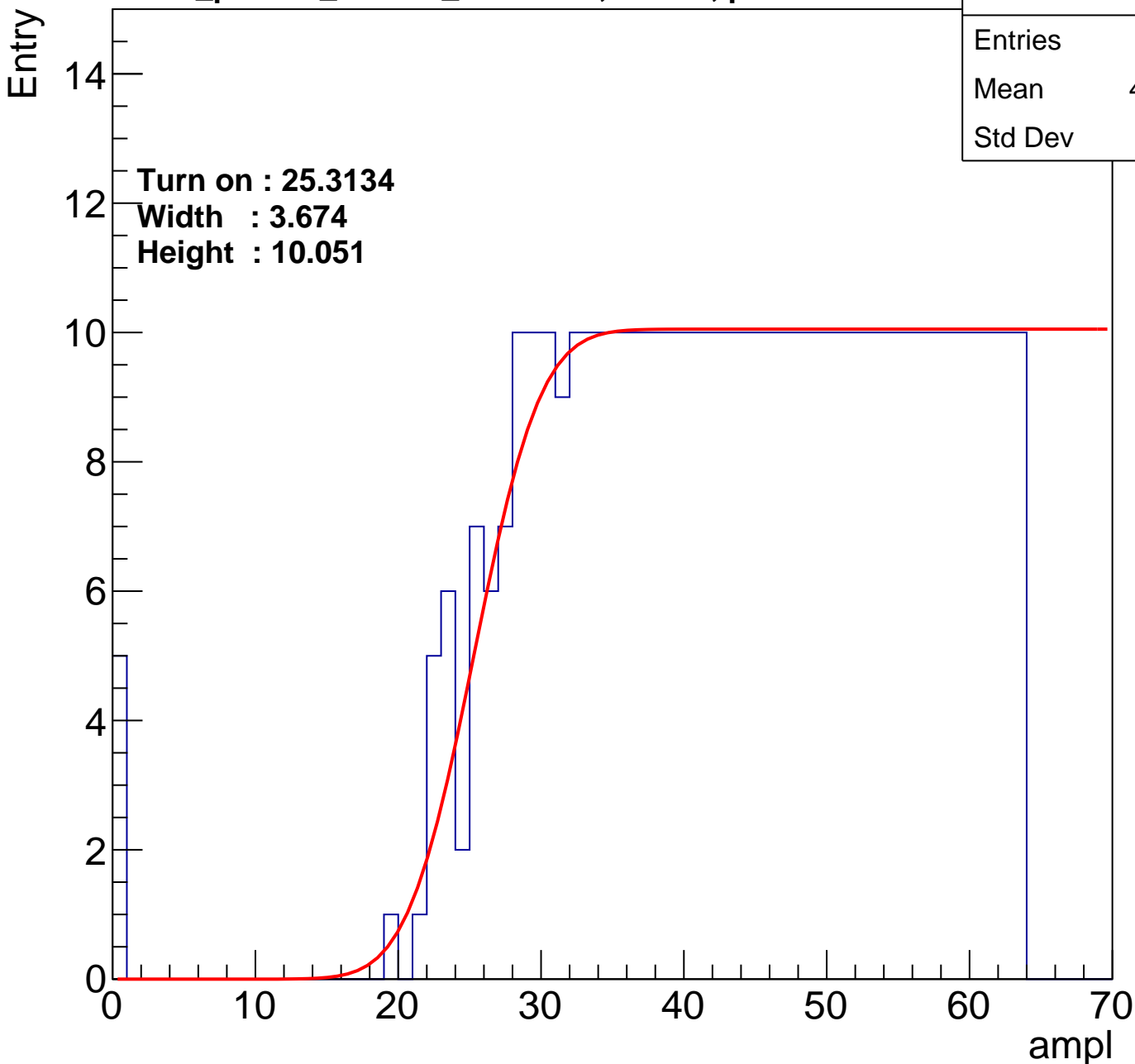
calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.12
Std Dev	12.5

Turn on : 25.3134

Width : 3.674

Height : 10.051



B1L102S, U25-ch84

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.8060

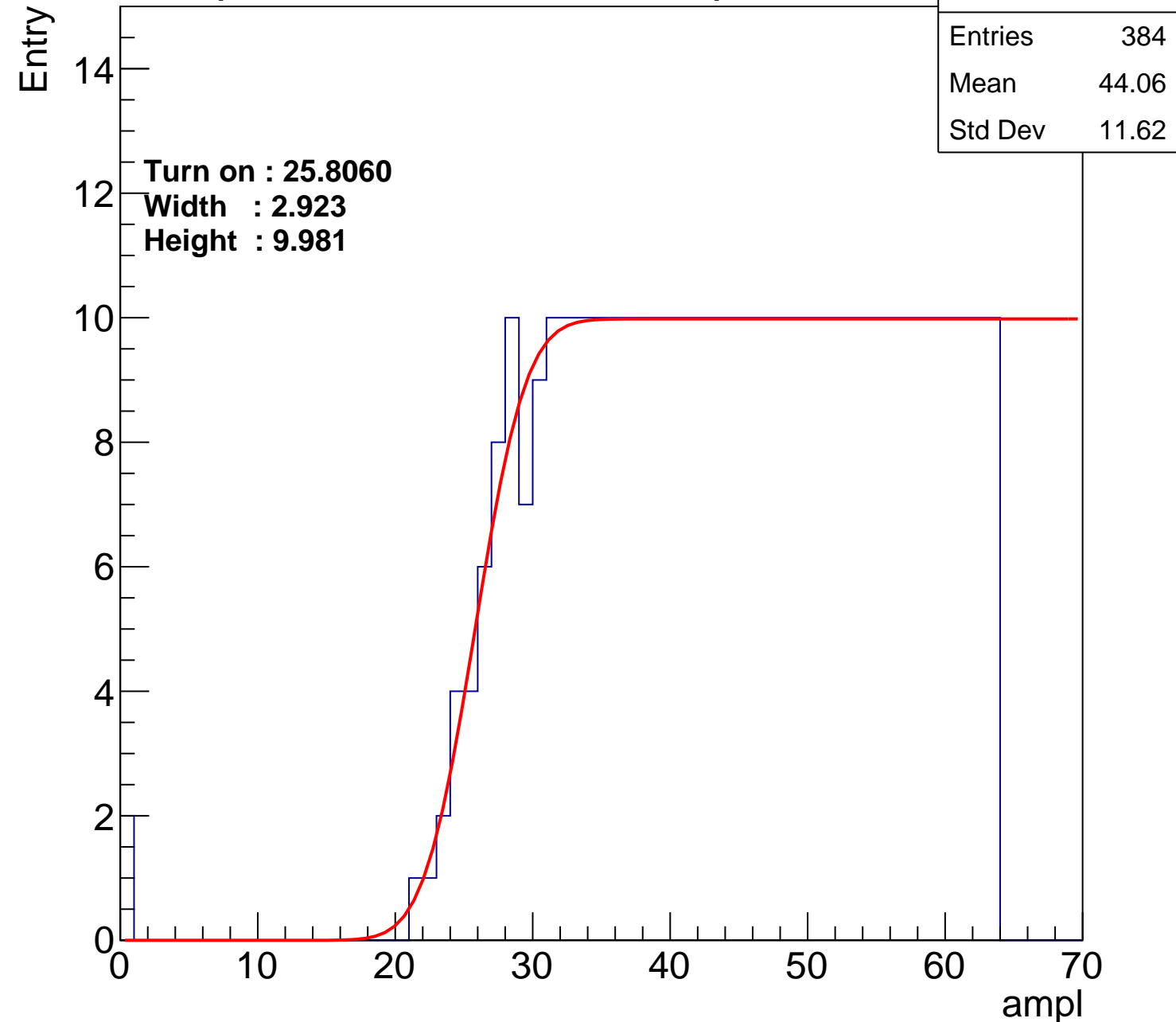
Width : 2.923

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch85

calib_packv5_042523_0143.root, FC#11, port A2

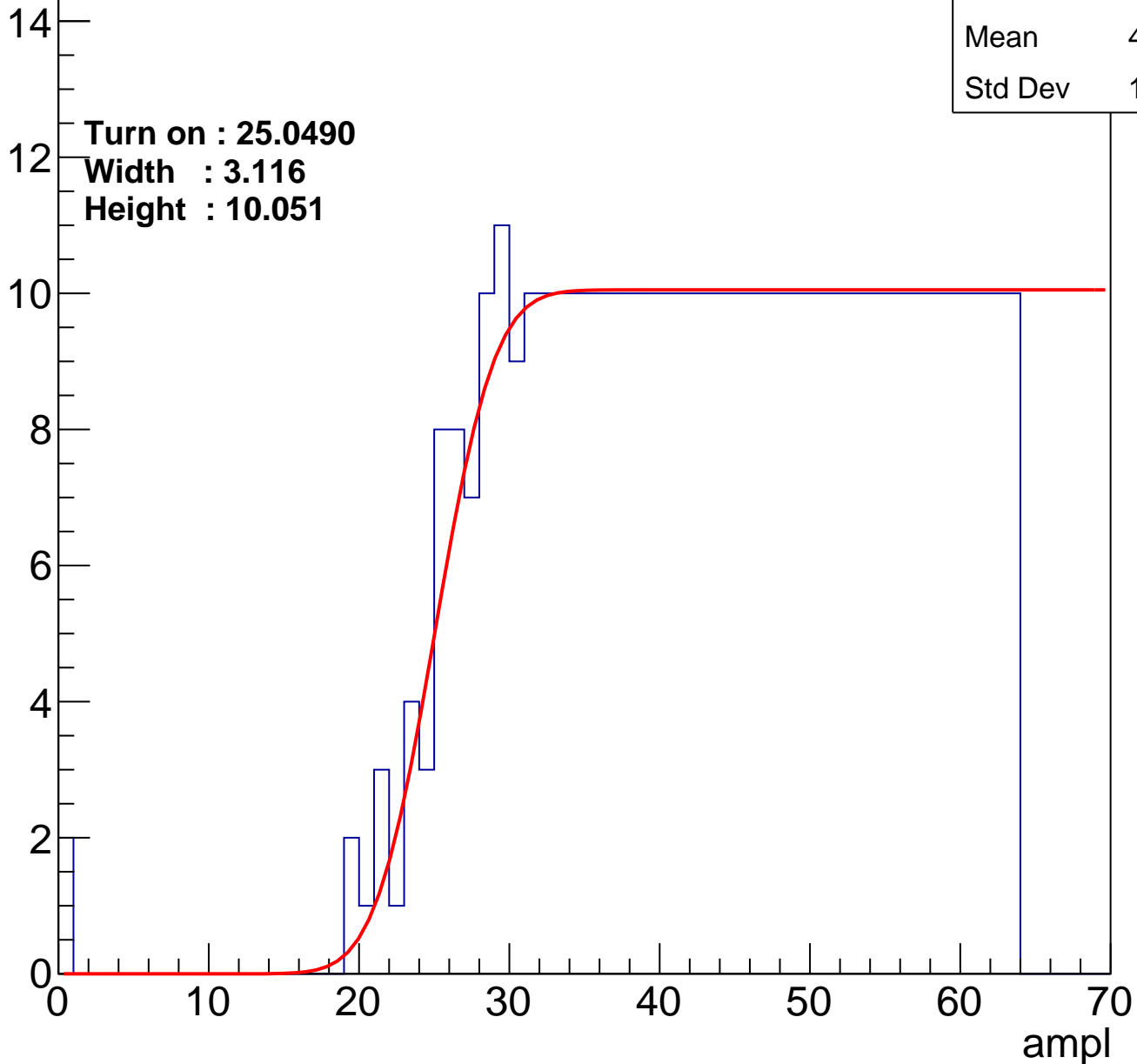
Entries	399
Mean	43.32
Std Dev	12.03

Turn on : 25.0490

Width : 3.116

Height : 10.051

Entry



B1L102S, U25-ch86

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.57
Std Dev	11.76

Turn on : 25.5772

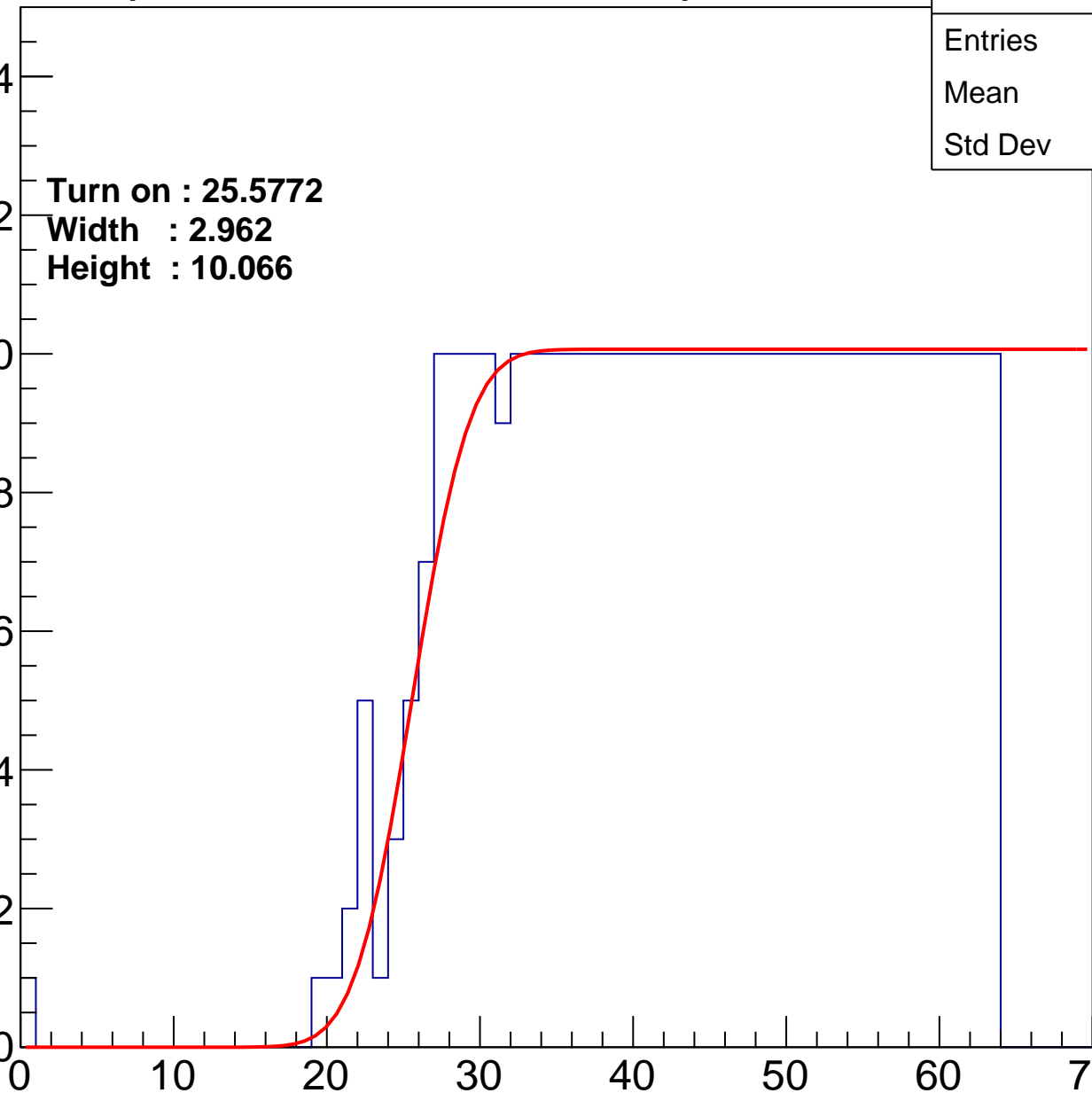
Width : 2.962

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch87

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.48
Std Dev	11.54

Turn on : 26.5934

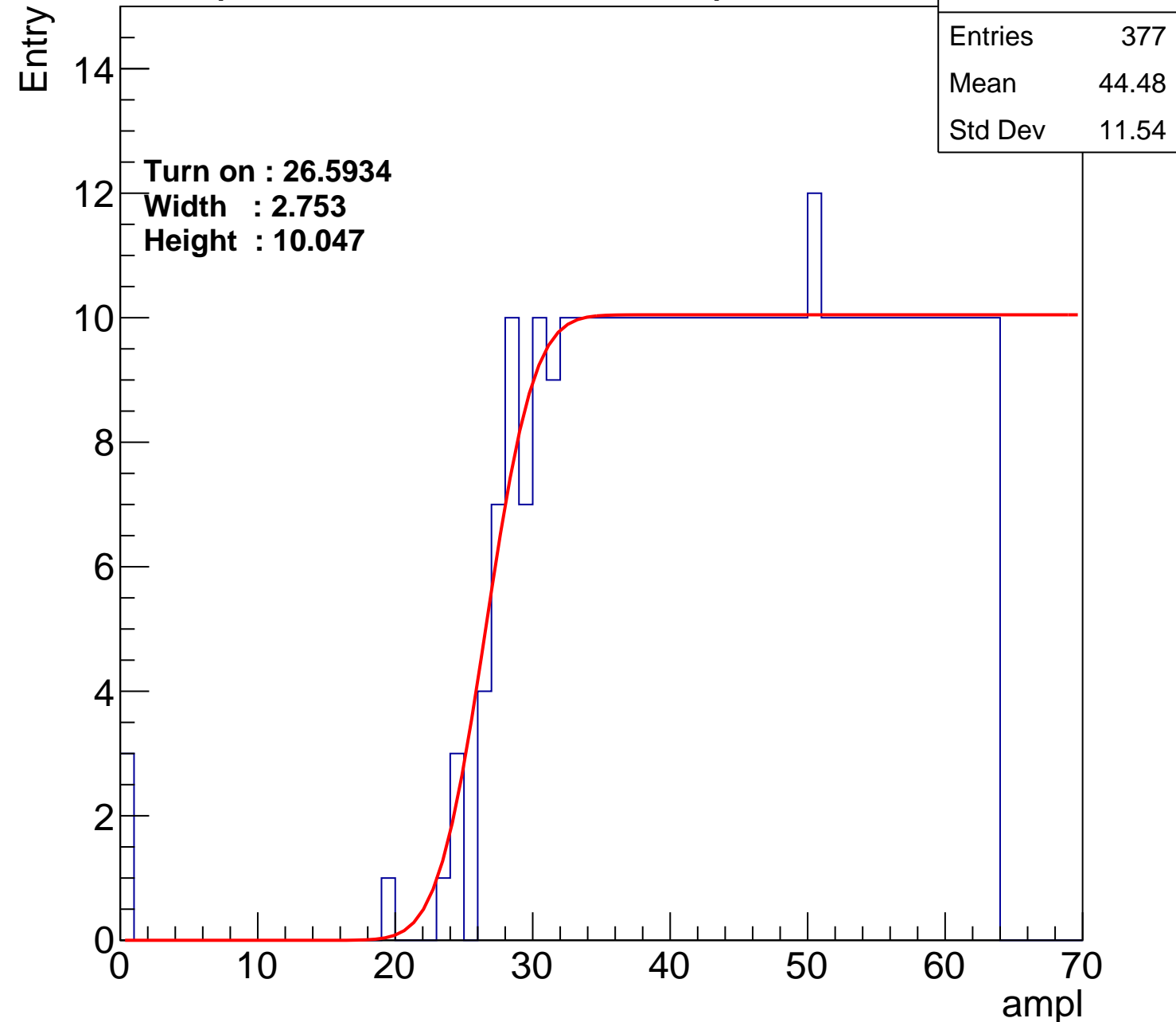
Width : 2.753

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch88

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.29
Std Dev	12.19

Turn on : 24.9629

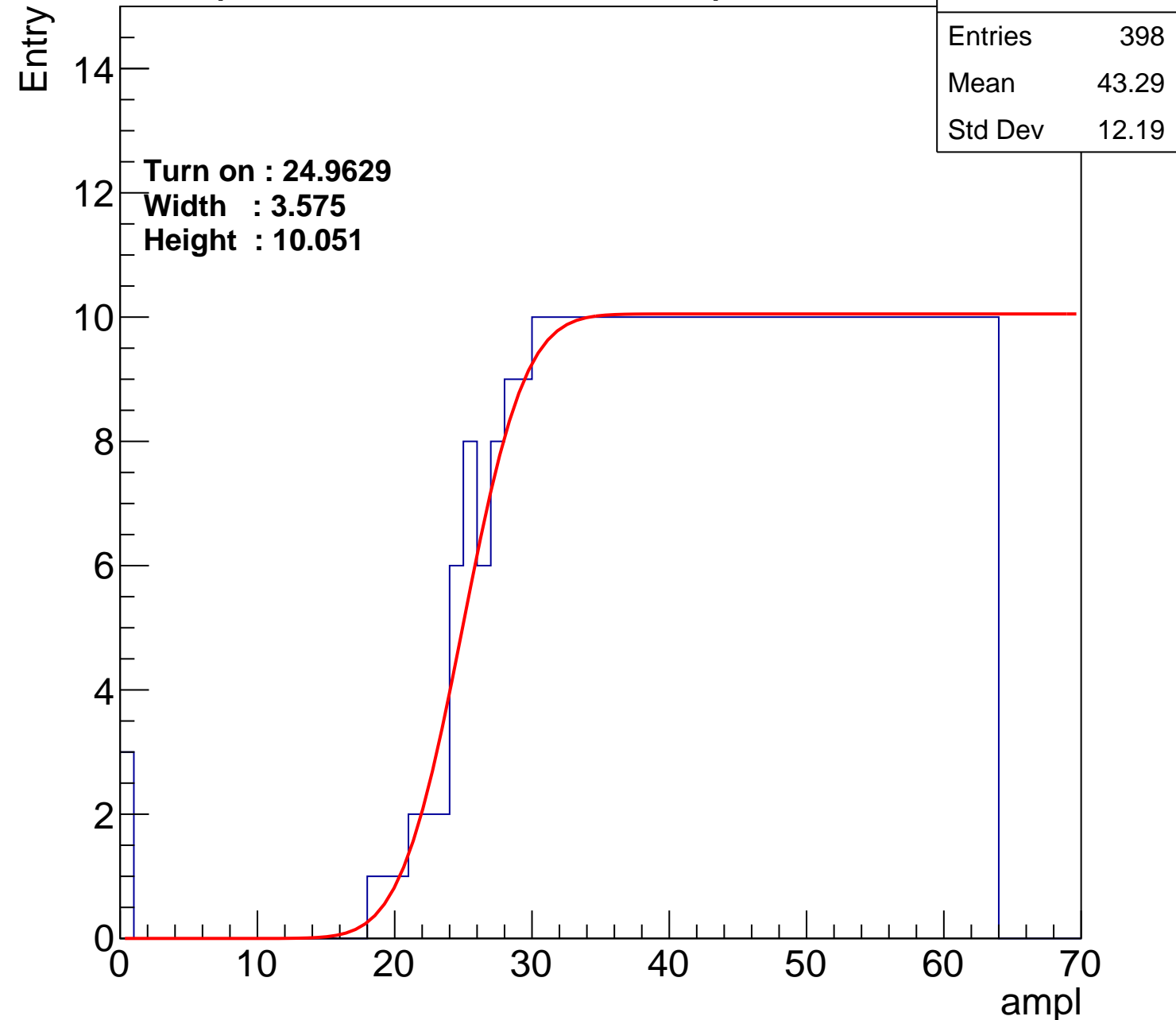
Width : 3.575

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch89

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 26.0037

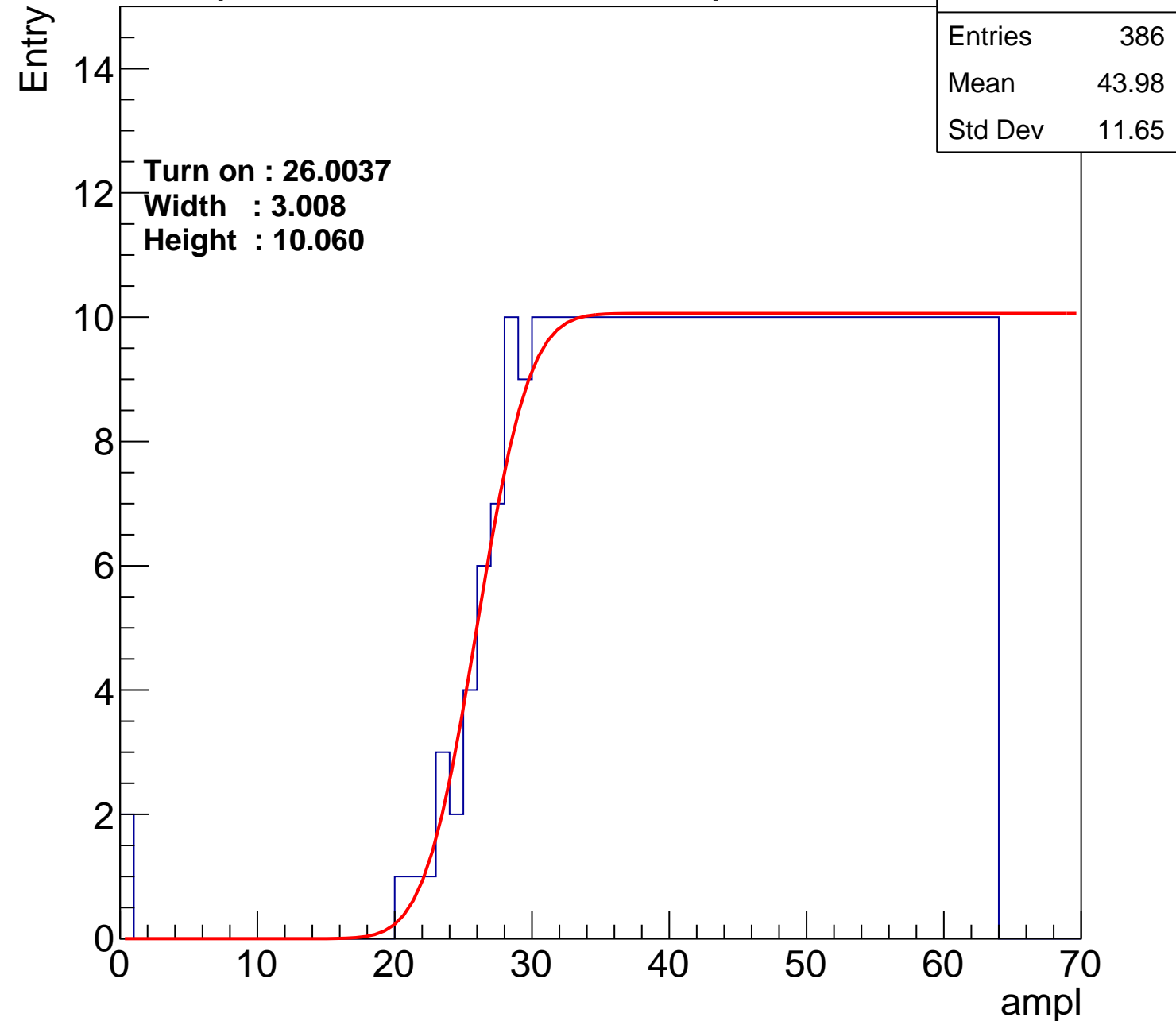
Width : 3.008

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch90

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.76
Std Dev	11.9

Turn on : 25.6174

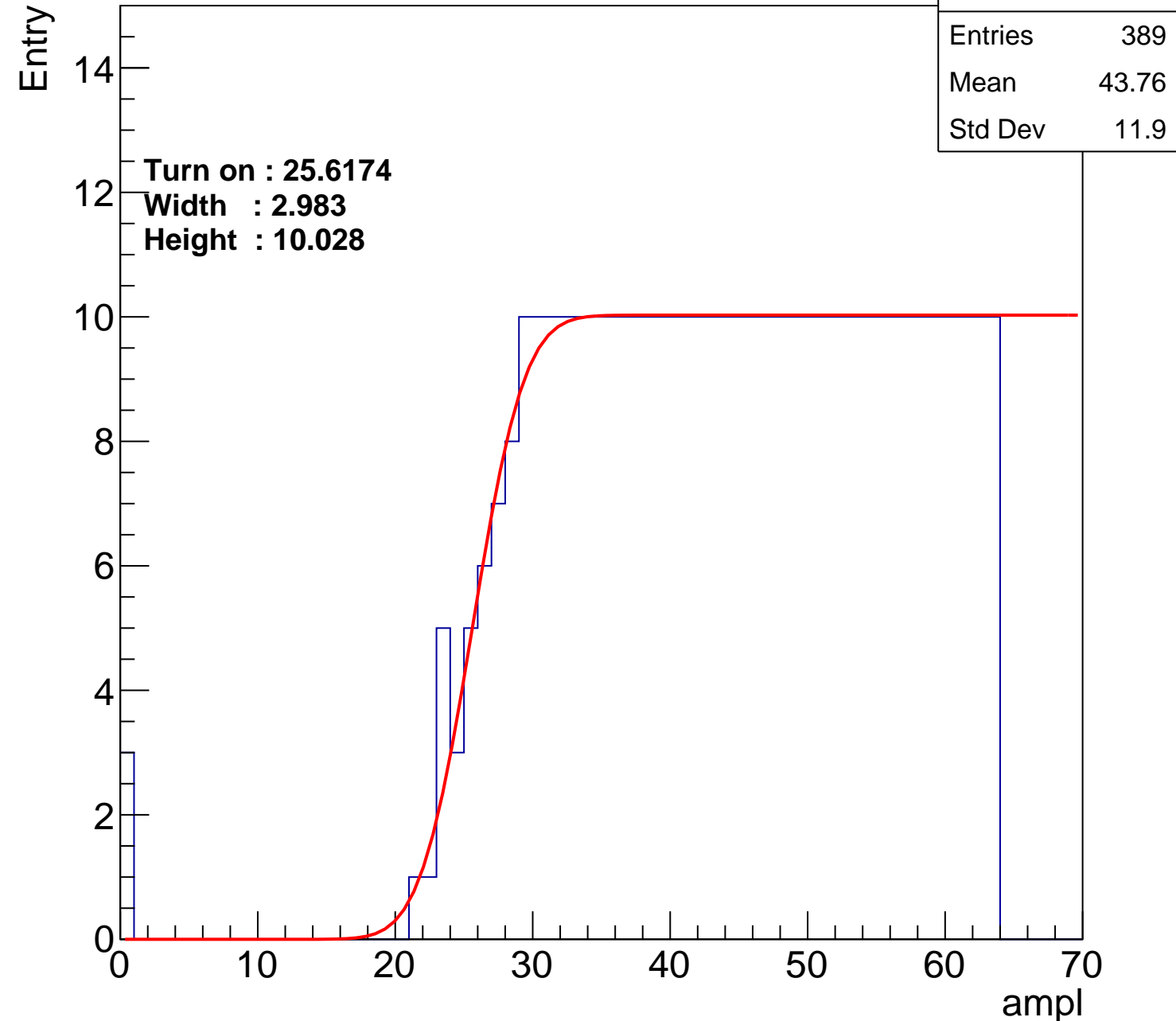
Width : 2.983

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch91

calib_packv5_042523_0143.root, FC#11, port A2

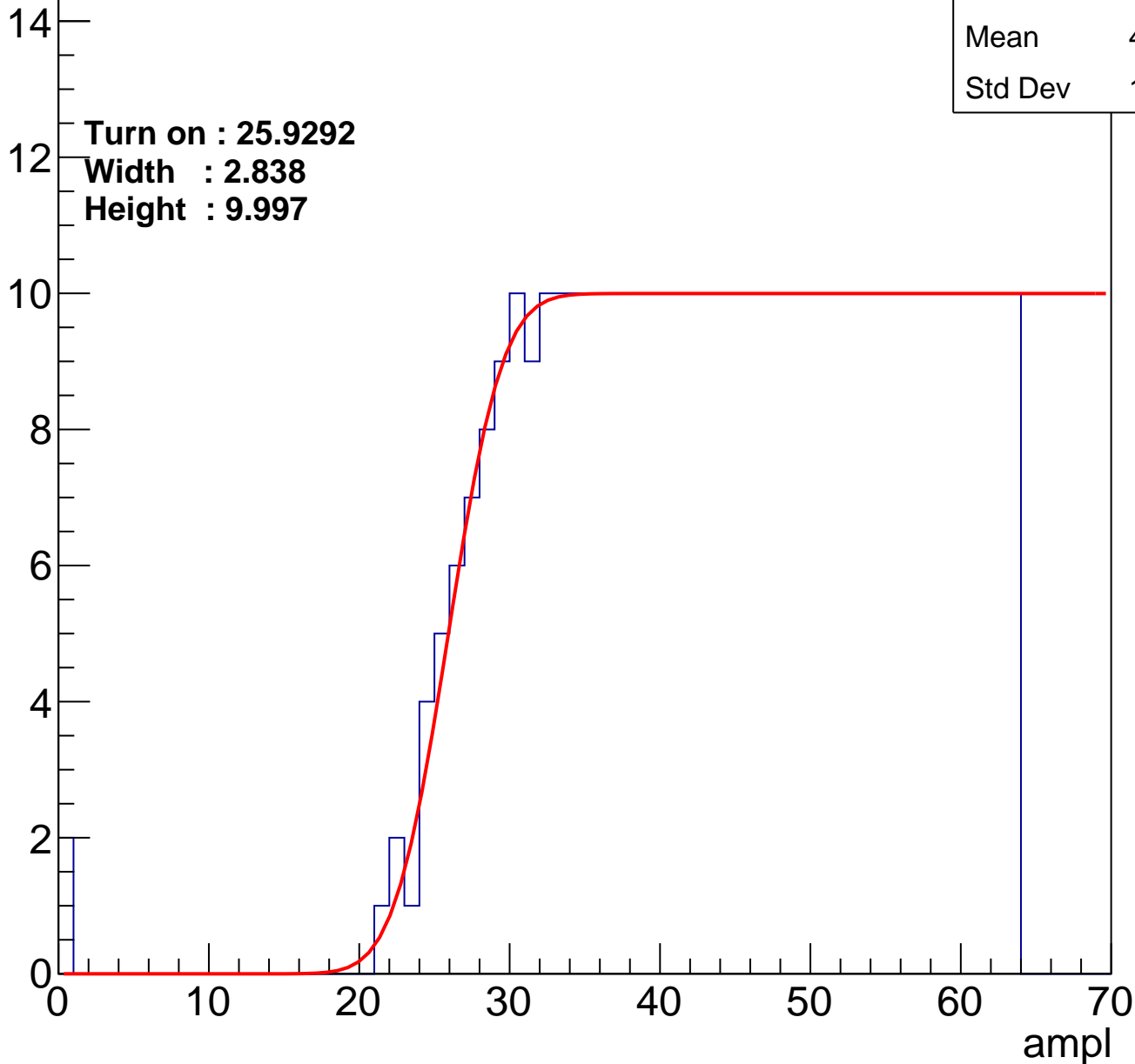
Entry

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 25.9292

Width : 2.838

Height : 9.997



B1L102S, U25-ch92

calib_packv5_042523_0143.root, FC#11, port A2

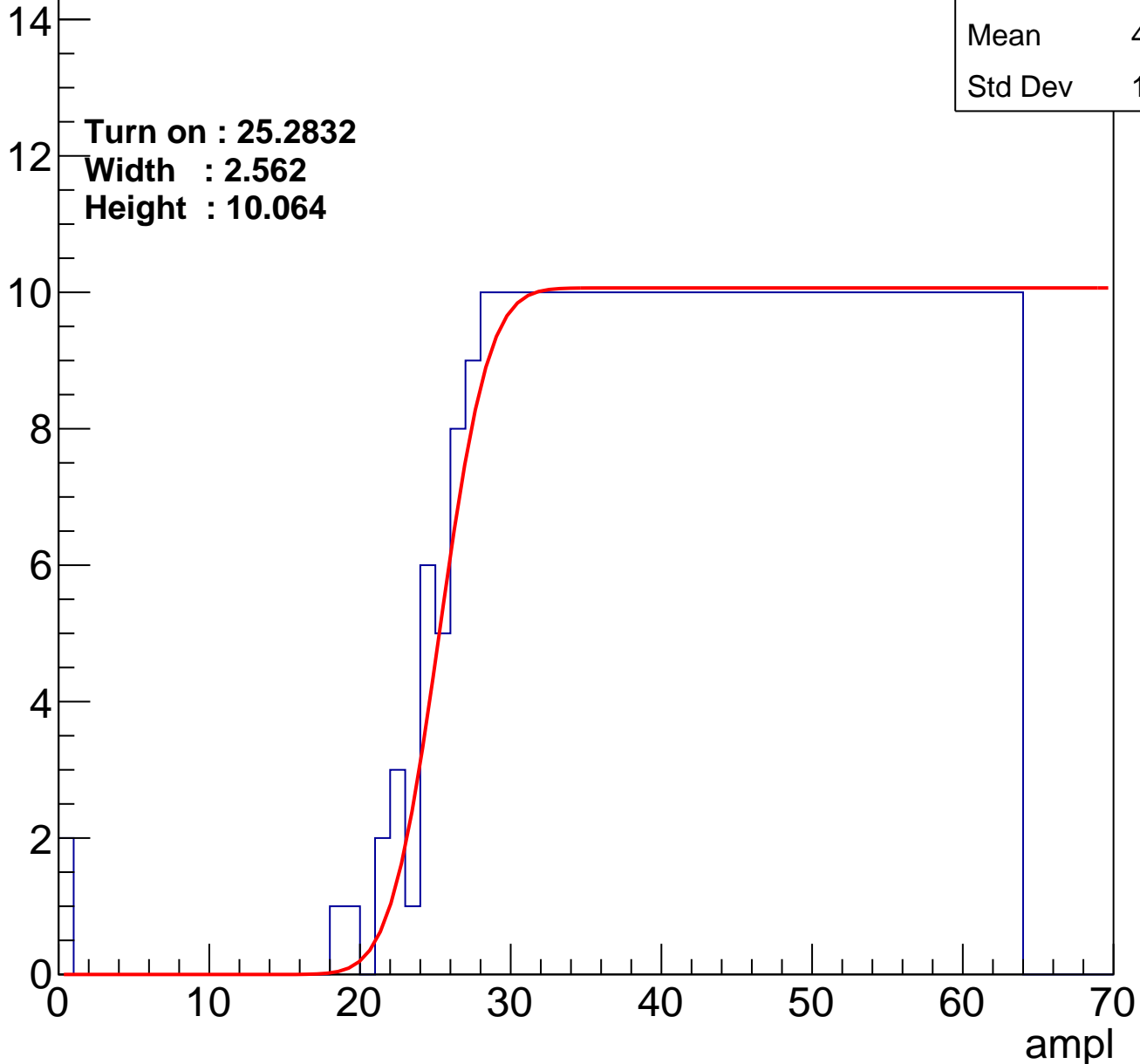
Entries	398
Mean	43.39
Std Dev	11.97

Turn on : 25.2832

Width : 2.562

Height : 10.064

Entry



B1L102S, U25-ch93

calib_packv5_042523_0143.root, FC#11, port A2

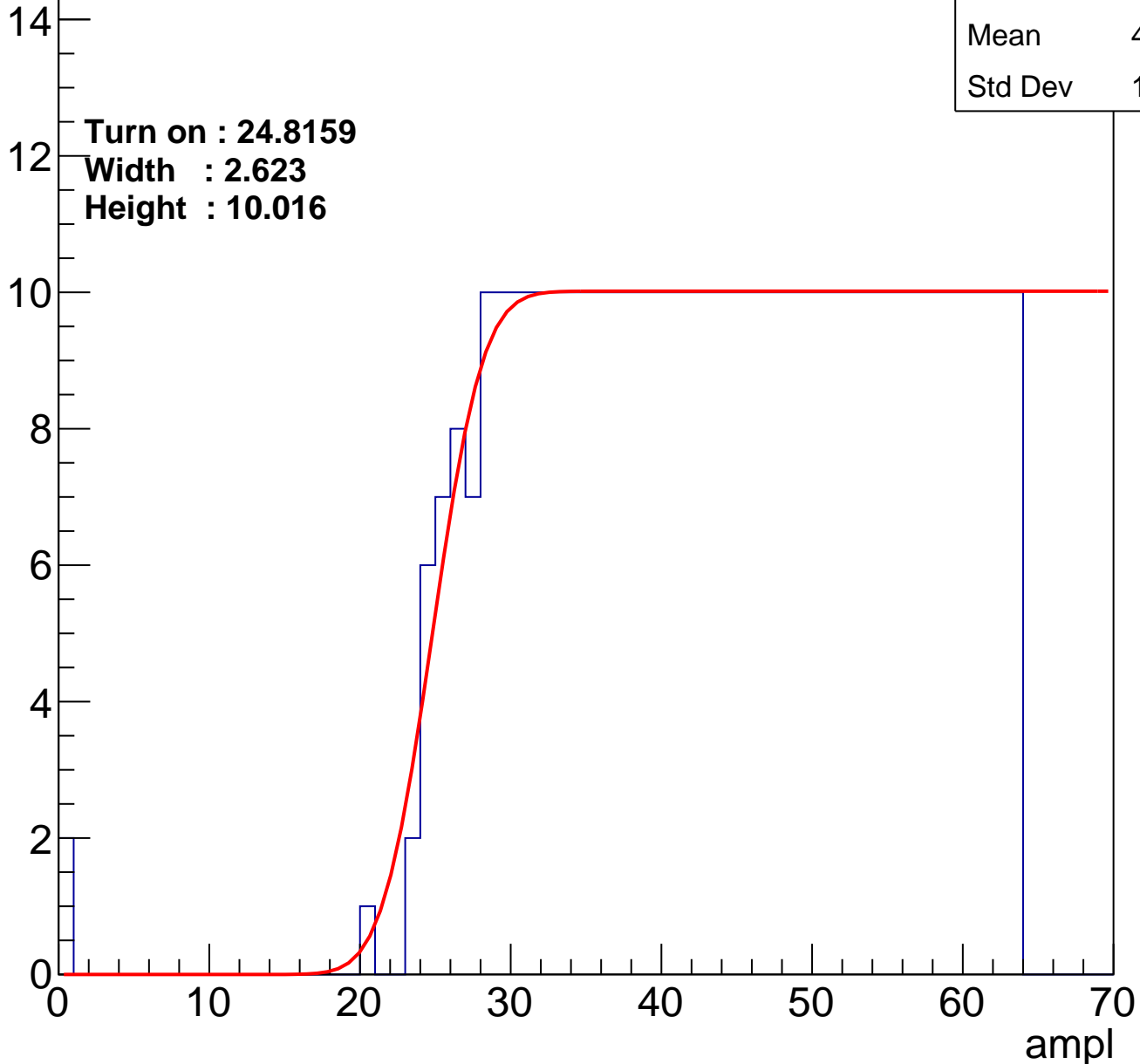
Entries	393
Mean	43.67
Std Dev	11.77

Turn on : 24.8159

Width : 2.623

Height : 10.016

Entry



B1L102S, U25-ch94

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.82
Std Dev	11.6

Turn on : 28.4326

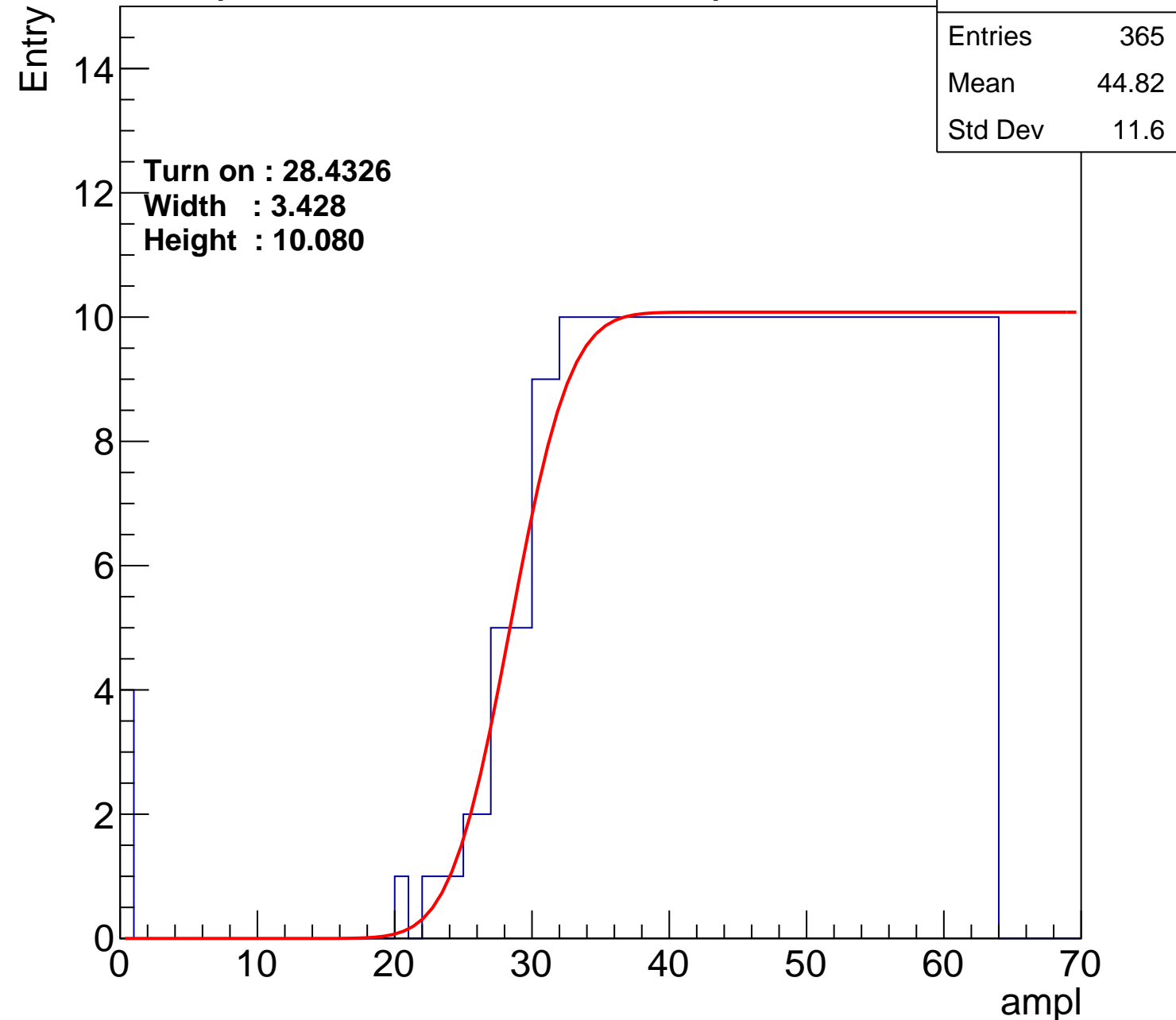
Width : 3.428

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch95

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.29
Std Dev	11.95

Turn on : 26.8528

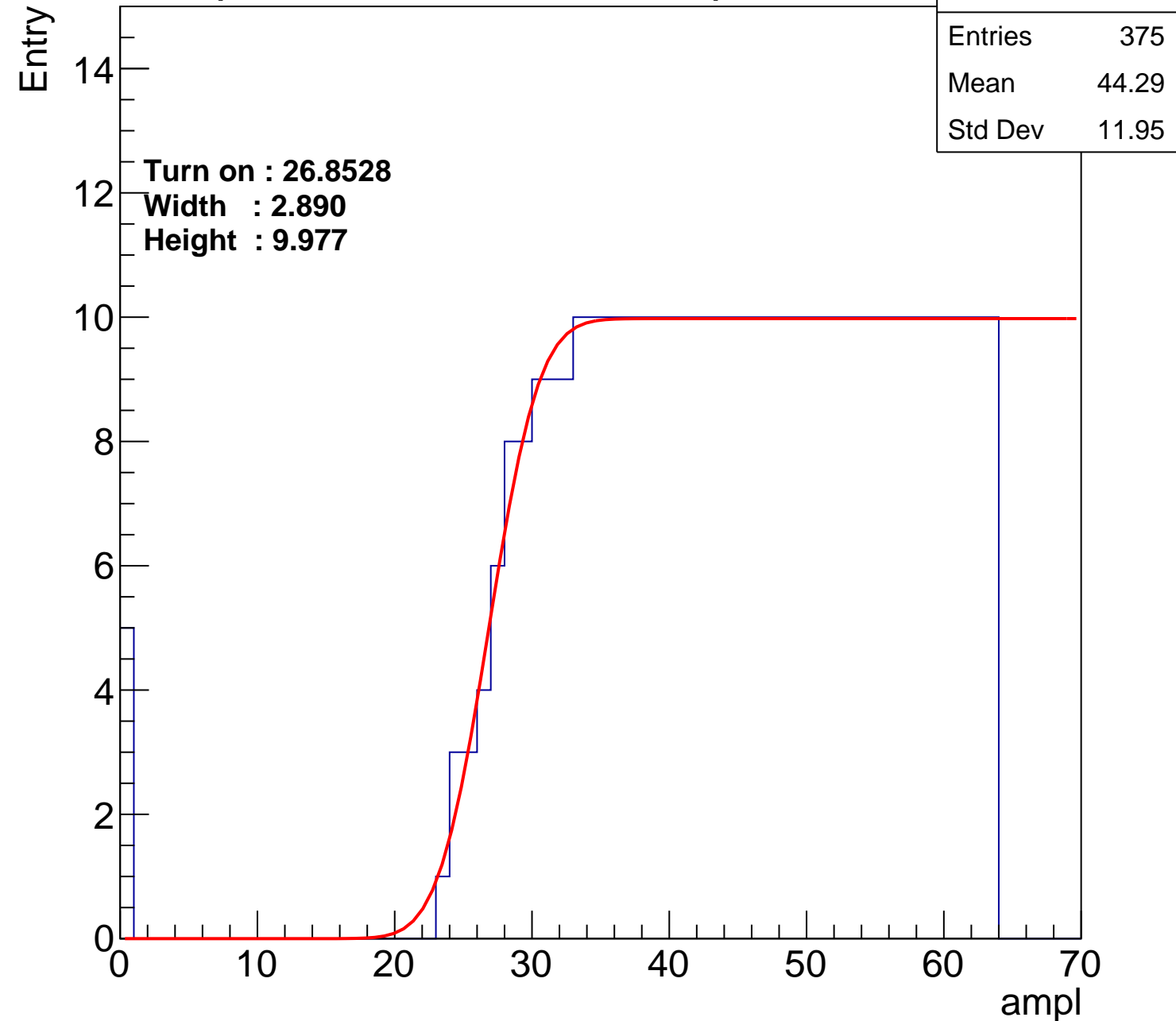
Width : 2.890

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch96

calib_packv5_042523_0143.root, FC#11, port A2

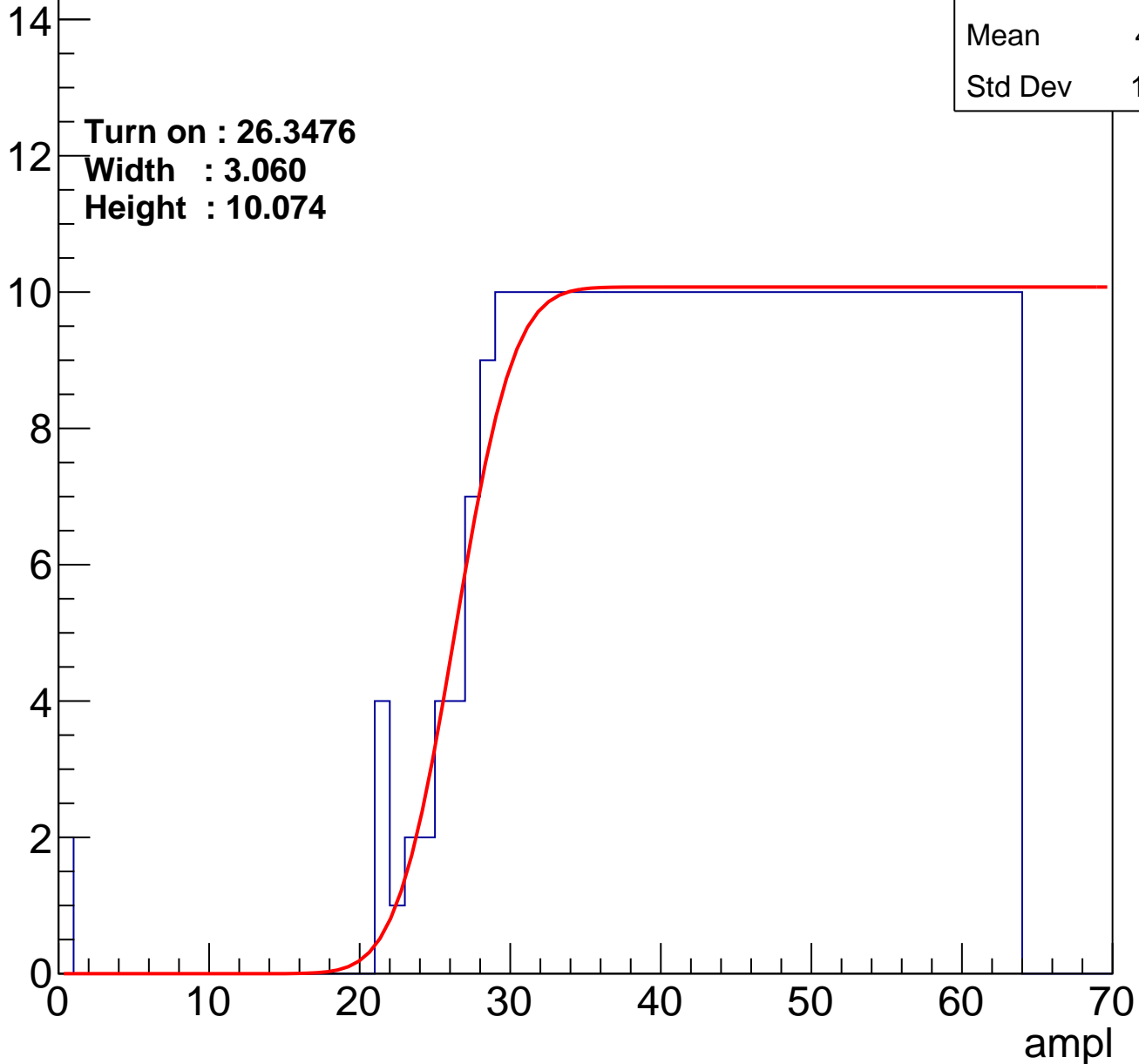
Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 26.3476

Width : 3.060

Height : 10.074

Entry



B1L102S, U25-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.1
Std Dev	11.63

Turn on : 26.8292

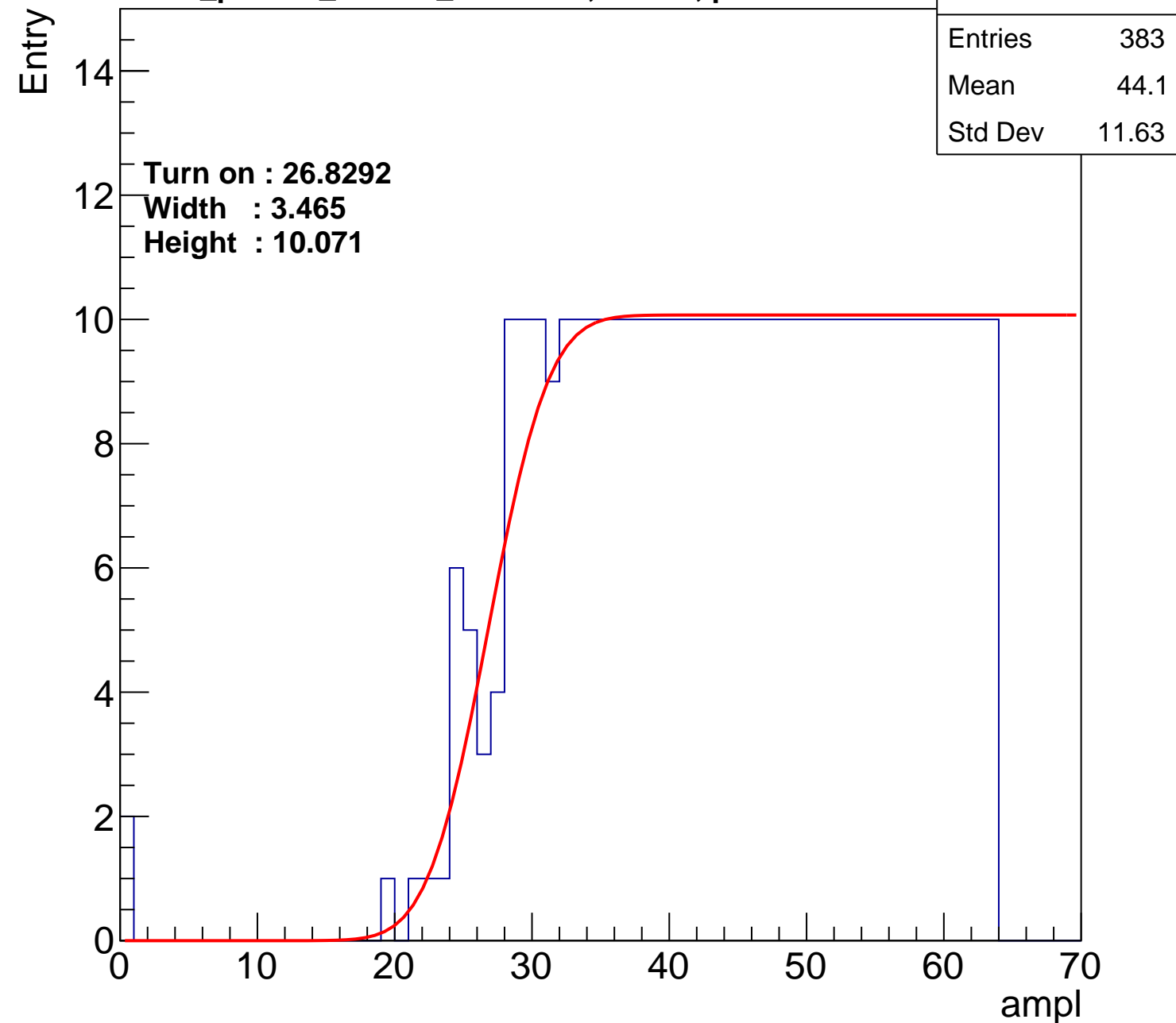
Width : 3.465

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch98

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.43
Std Dev	11.87

Turn on : 27.6628

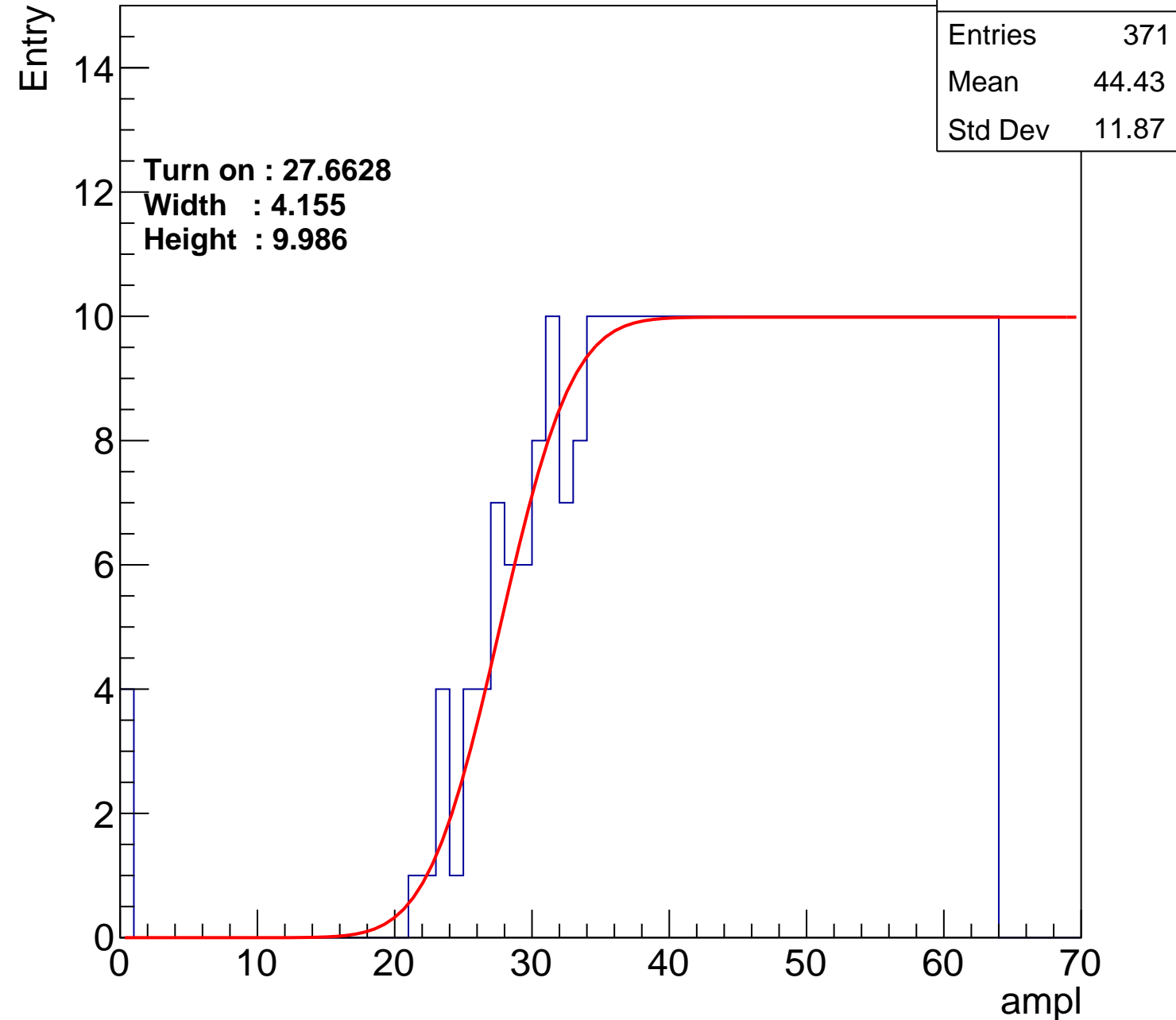
Width : 4.155

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch99

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44
Std Dev	11.91

Turn on : 26.2732

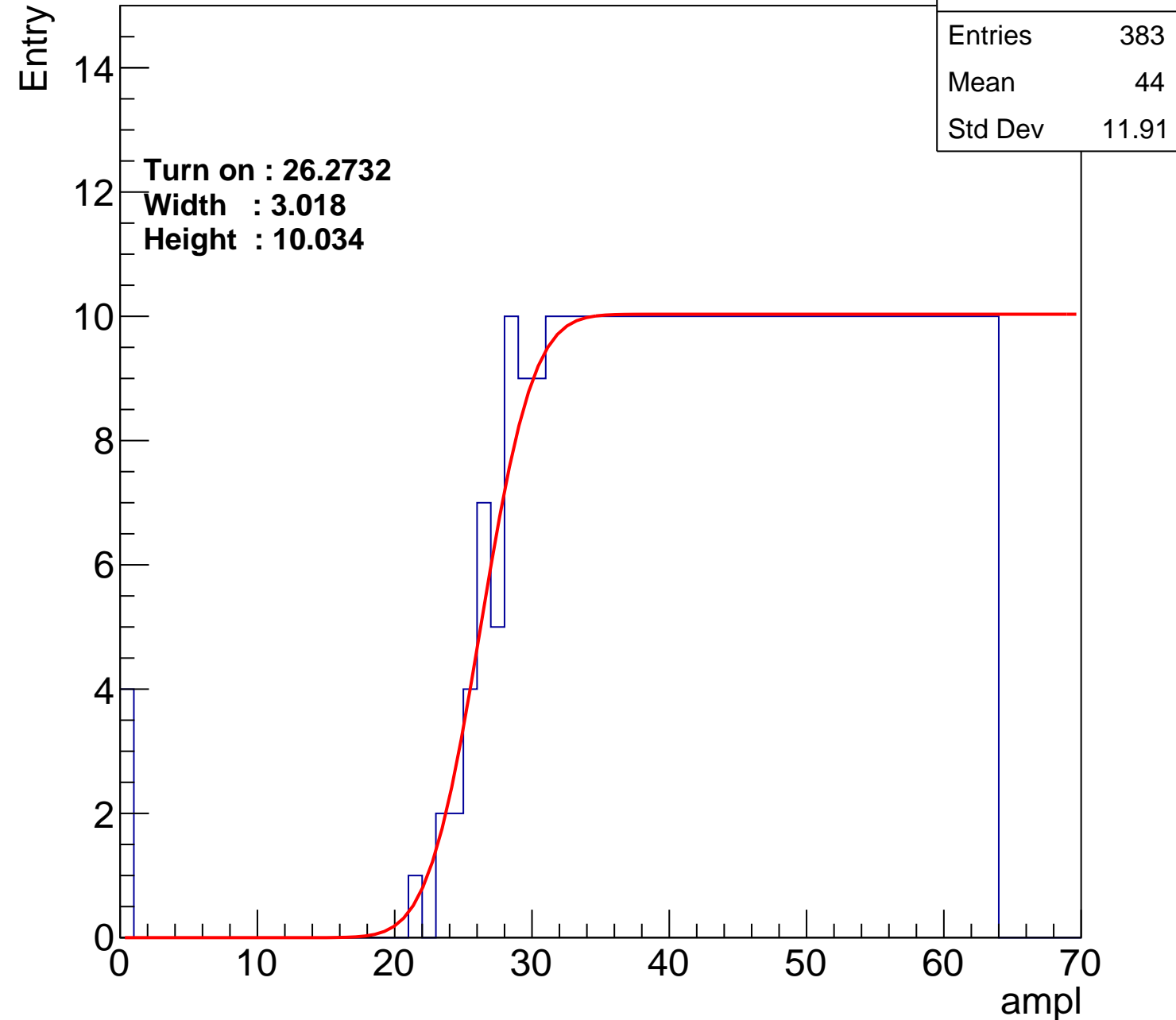
Width : 3.018

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch100

calib_packv5_042523_0143.root, FC#11, port A2

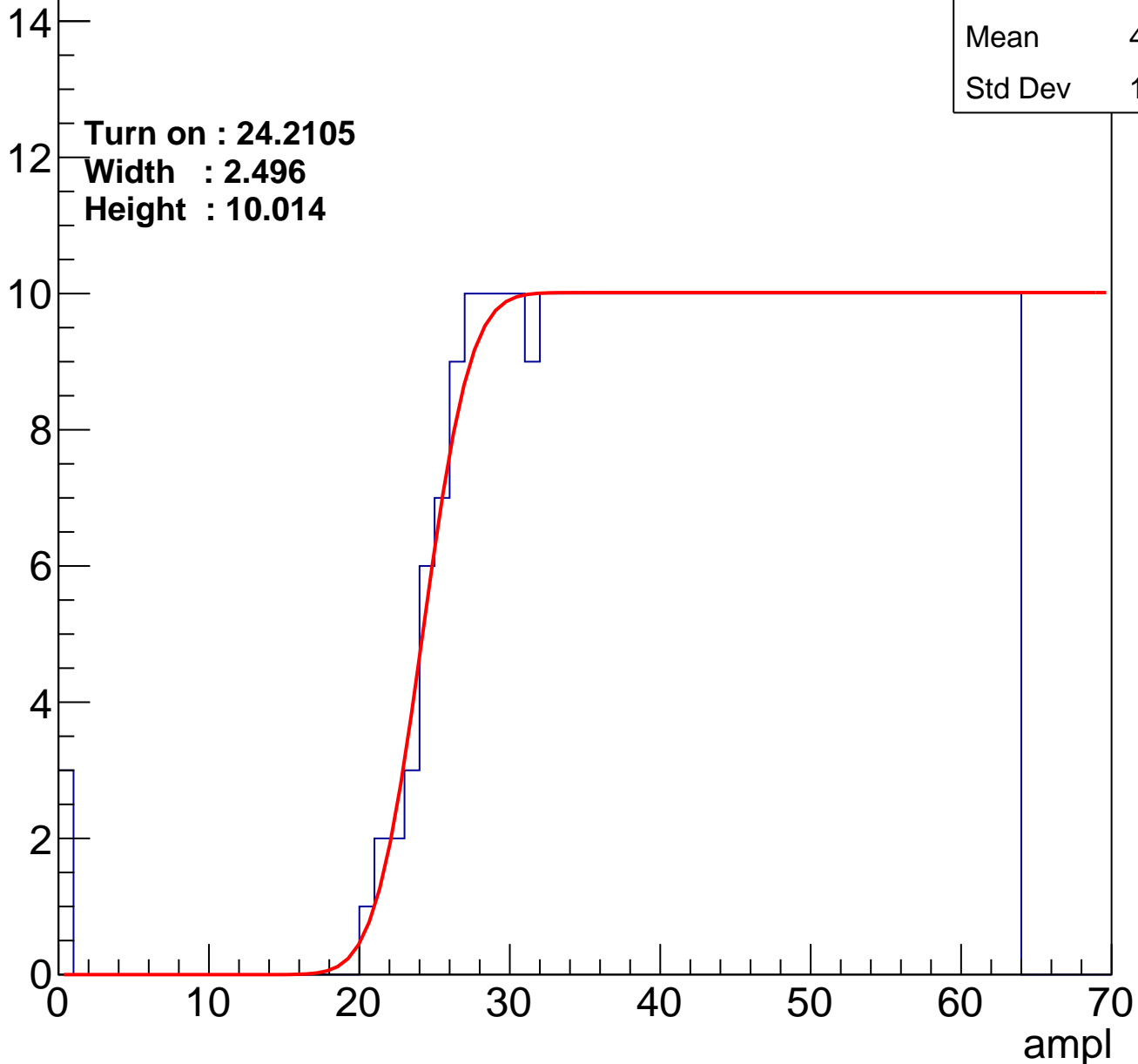
Entries	402
Mean	43.15
Std Dev	12.18

Turn on : 24.2105

Width : 2.496

Height : 10.014

Entry



B1L102S, U25-ch101

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	43.99
Std Dev	12.06

Turn on : 26.3003

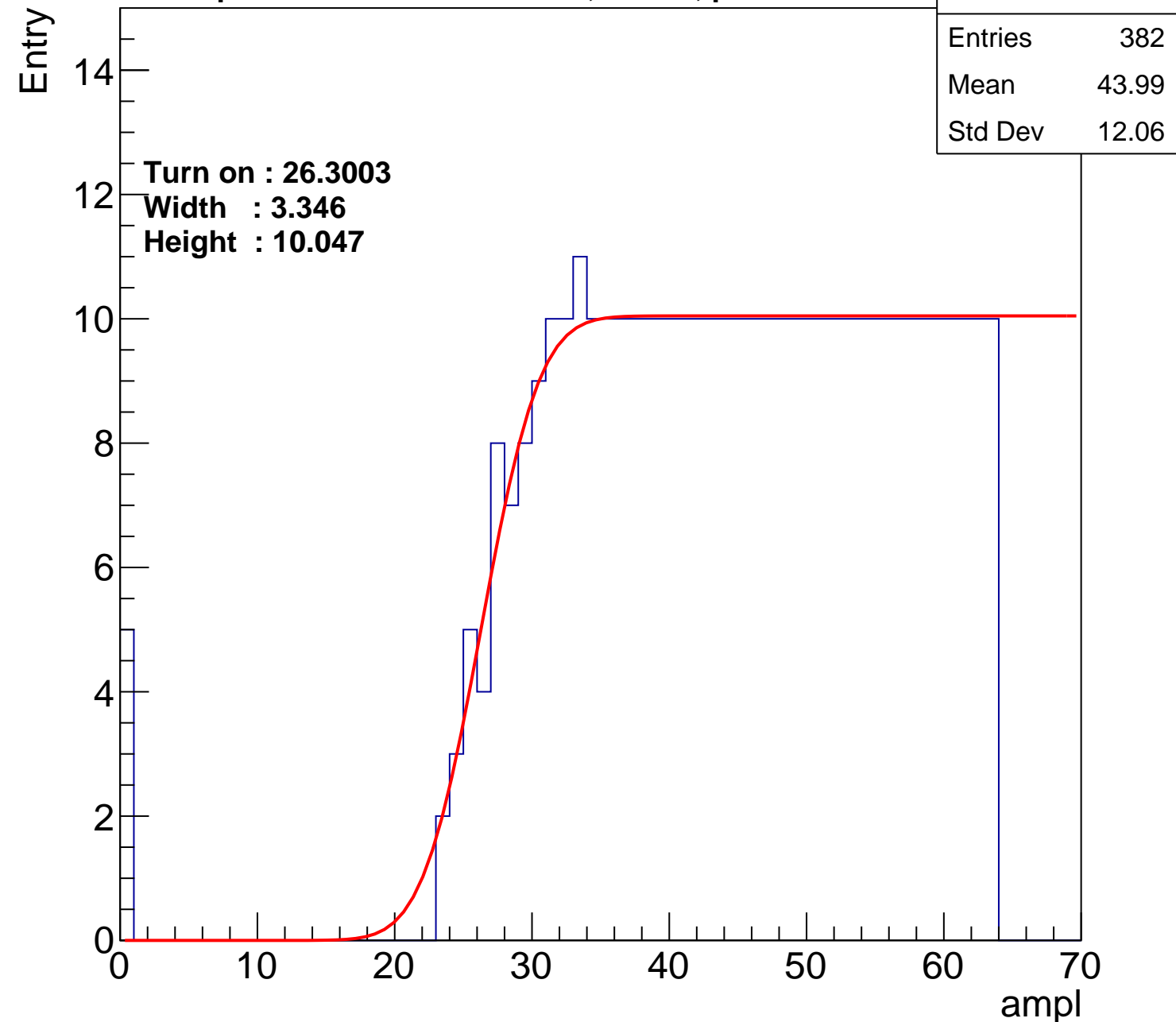
Width : 3.346

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch102

calib_packv5_042523_0143.root, FC#11, port A2

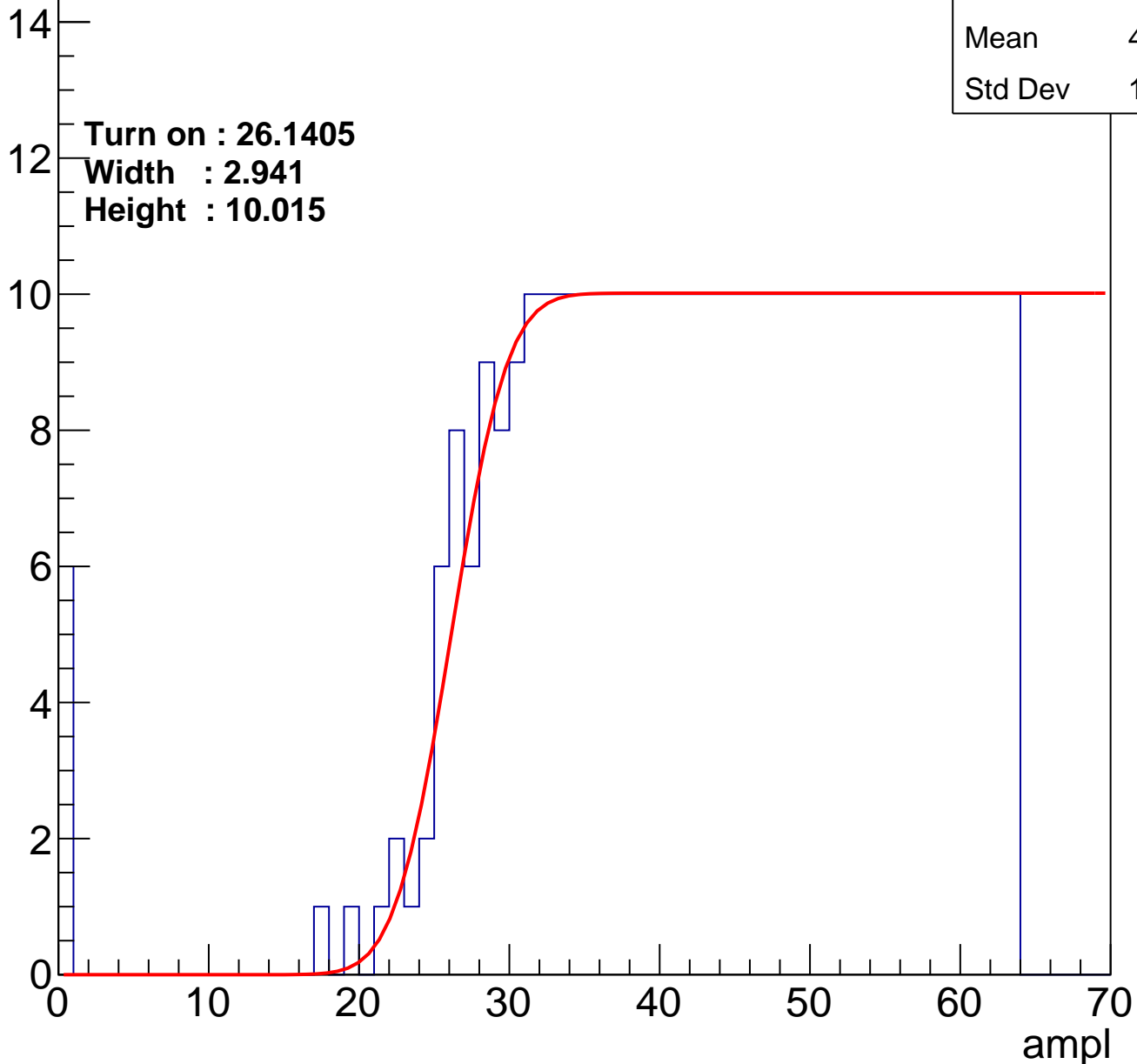
Entries	390
Mean	43.48
Std Dev	12.49

Turn on : 26.1405

Width : 2.941

Height : 10.015

Entry



B1L102S, U25-ch103

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.6
Std Dev	11.99

Turn on : 28.9305

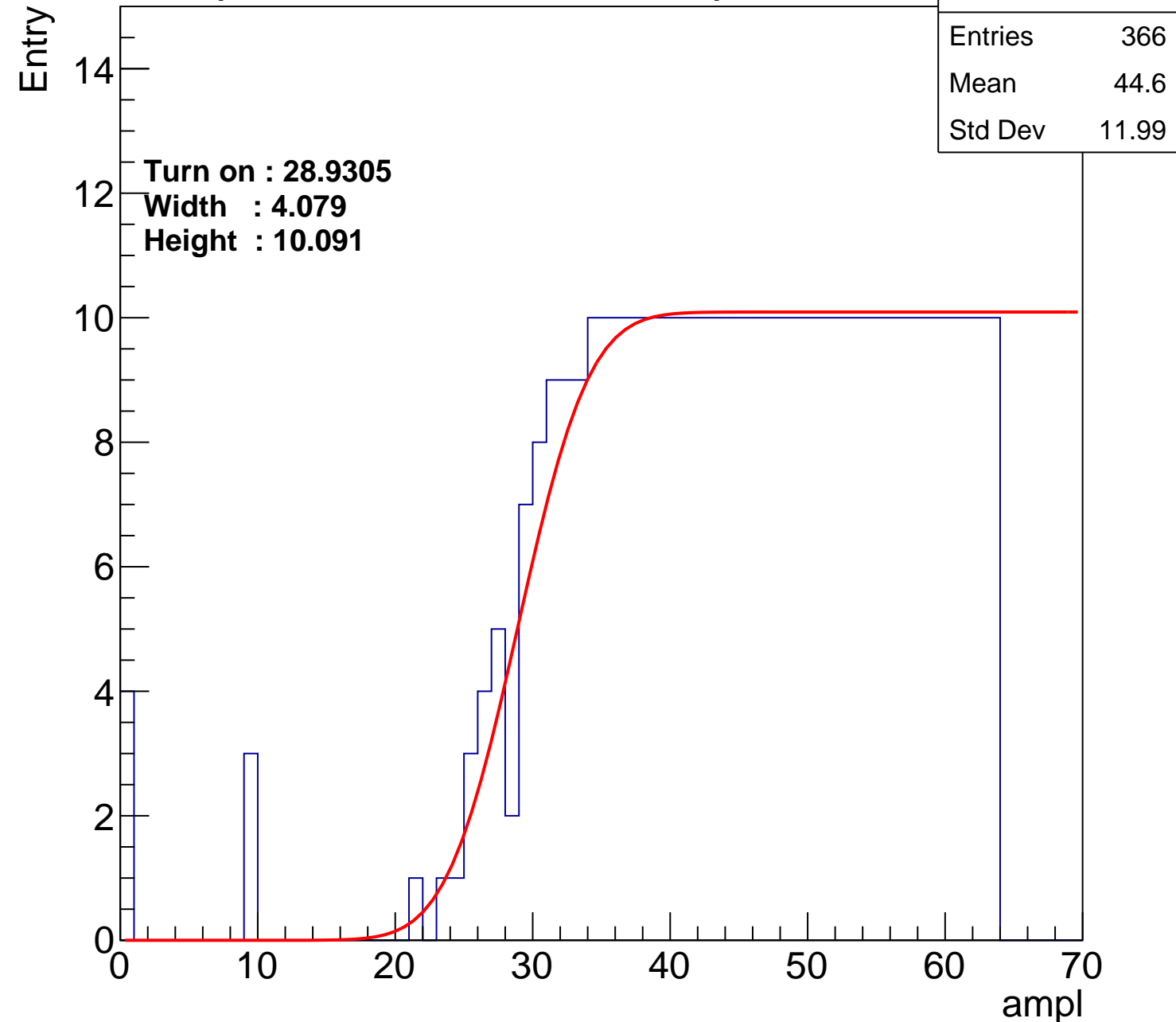
Width : 4.079

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch104

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.56
Std Dev	11.95

Turn on : 25.5745

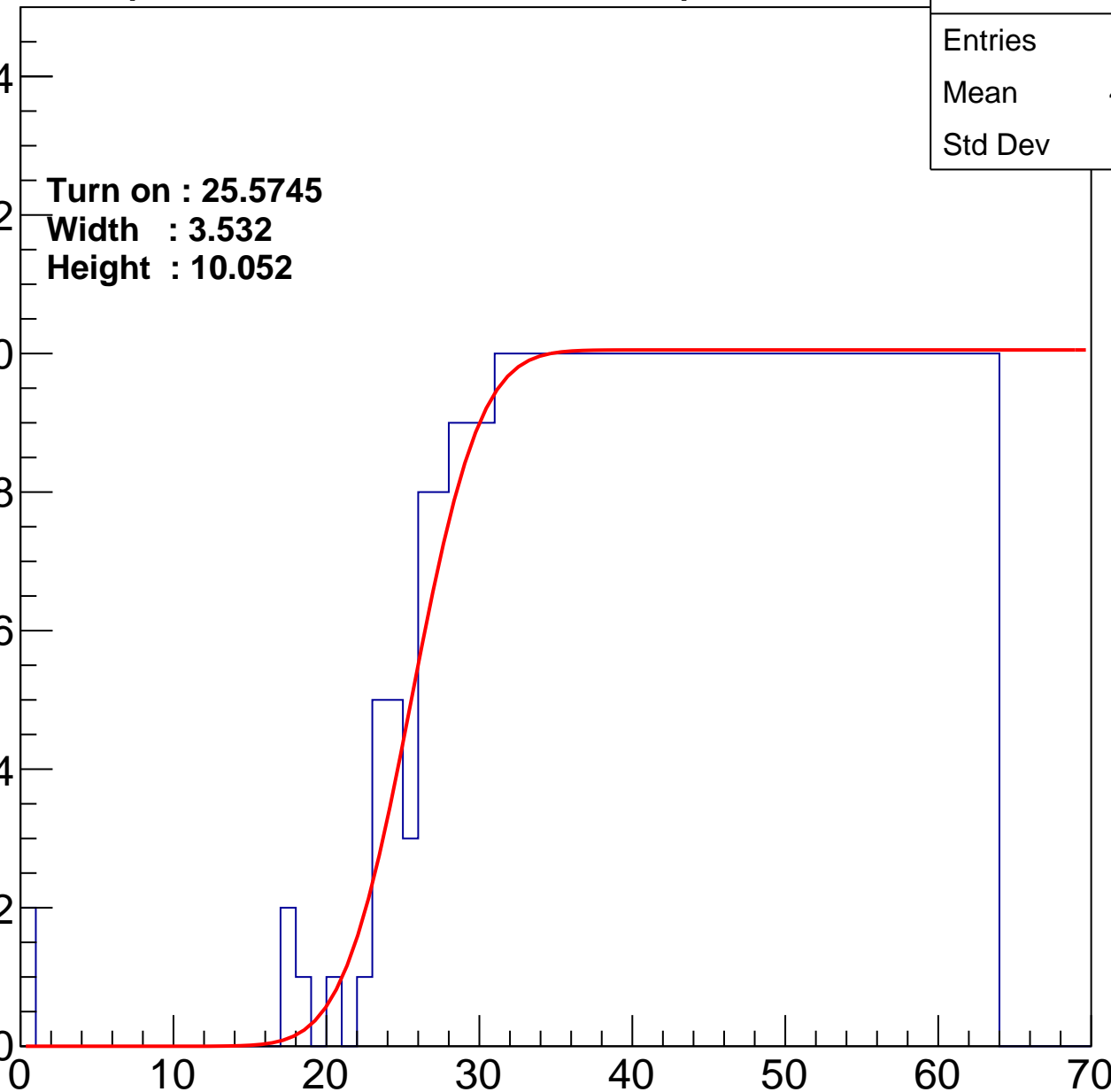
Width : 3.532

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch105

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	44.95
Std Dev	11.64

Turn on : 28.7435

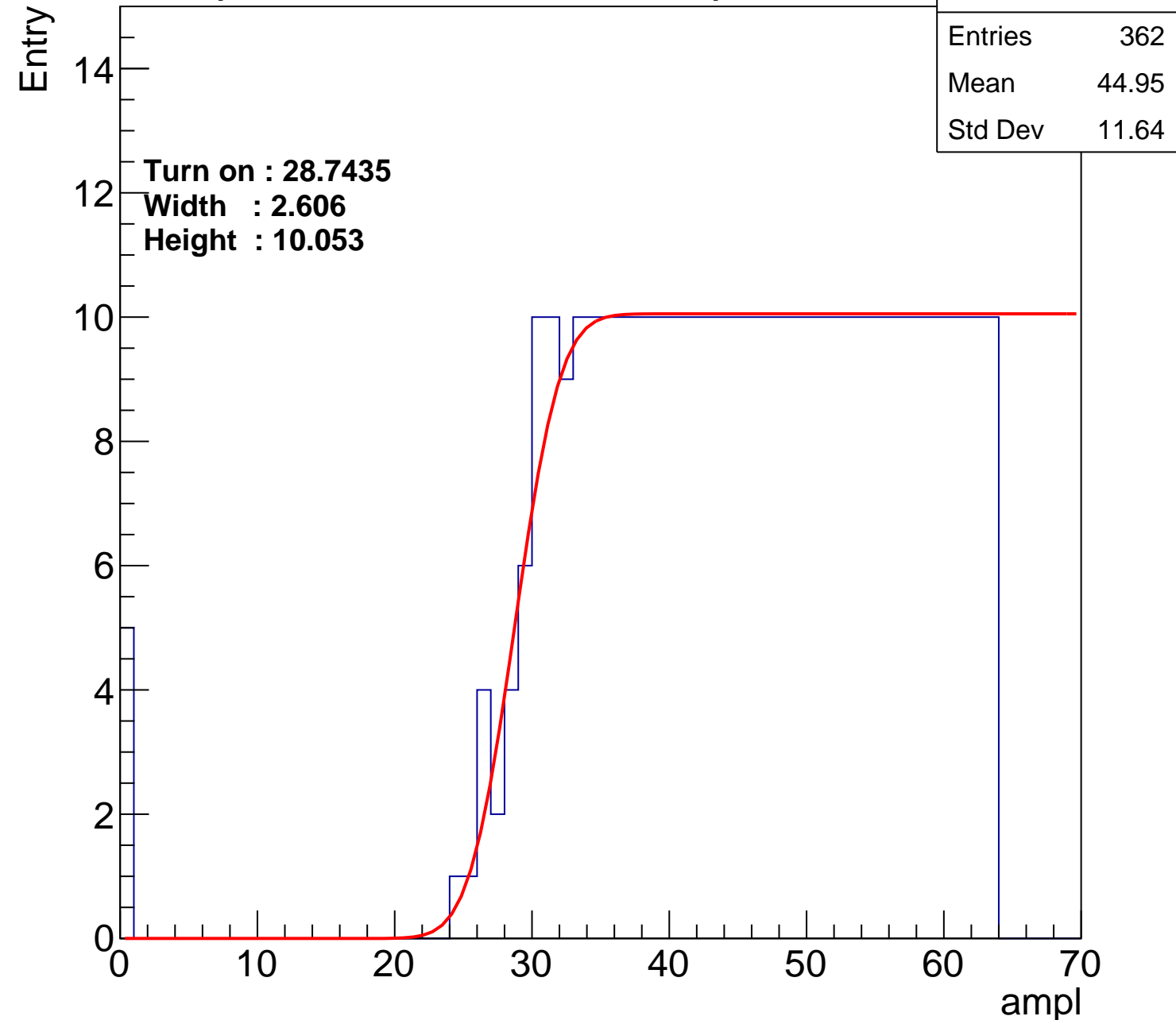
Width : 2.606

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch106

calib_packv5_042523_0143.root, FC#11, port A2

Entries	411
Mean	42.53
Std Dev	12.79

Turn on : 24.0519

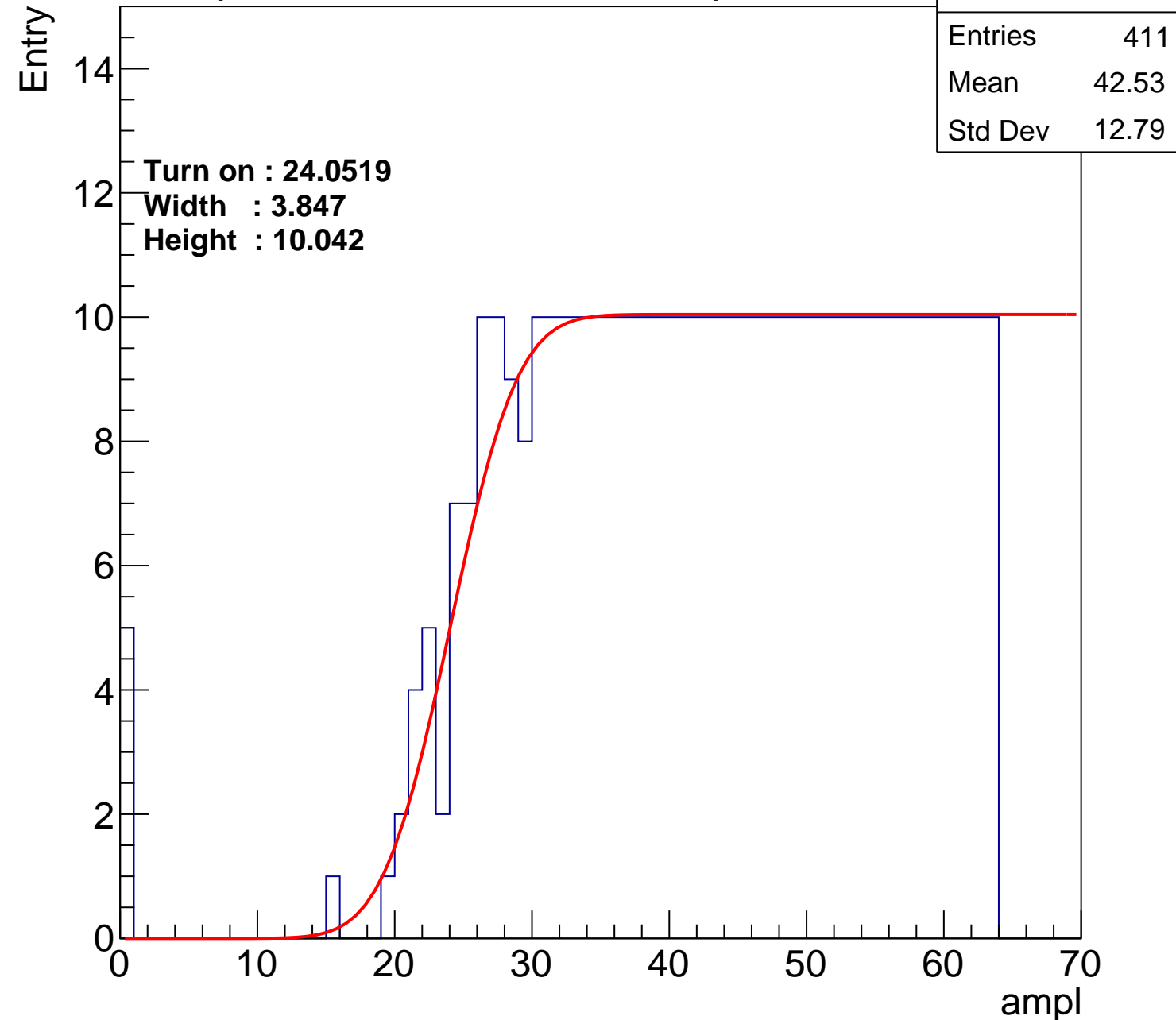
Width : 3.847

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch107

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.98
Std Dev	11.69

Turn on : 25.5280

Width : 3.198

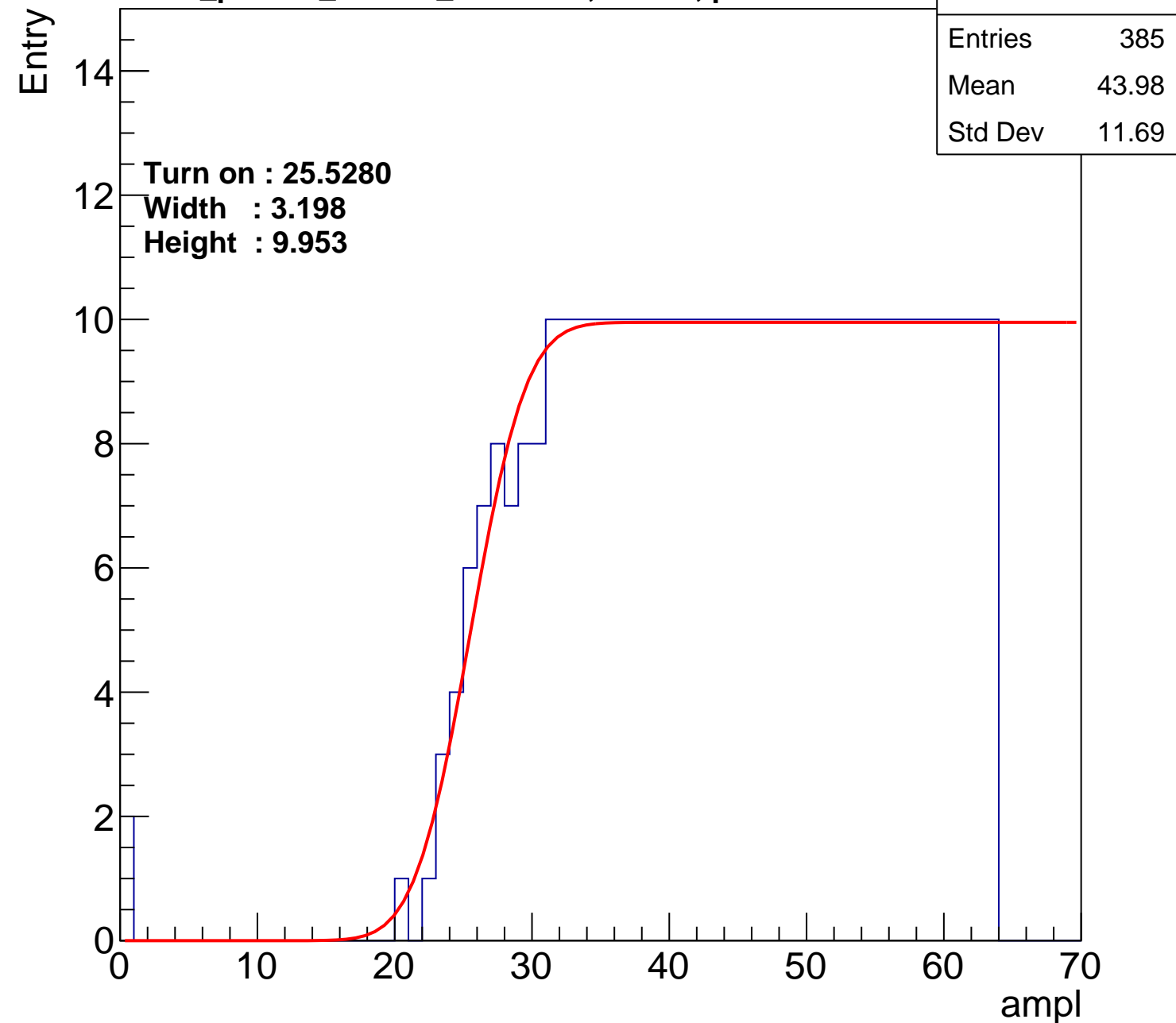
Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U25-ch108

calib_packv5_042523_0143.root, FC#11, port A2

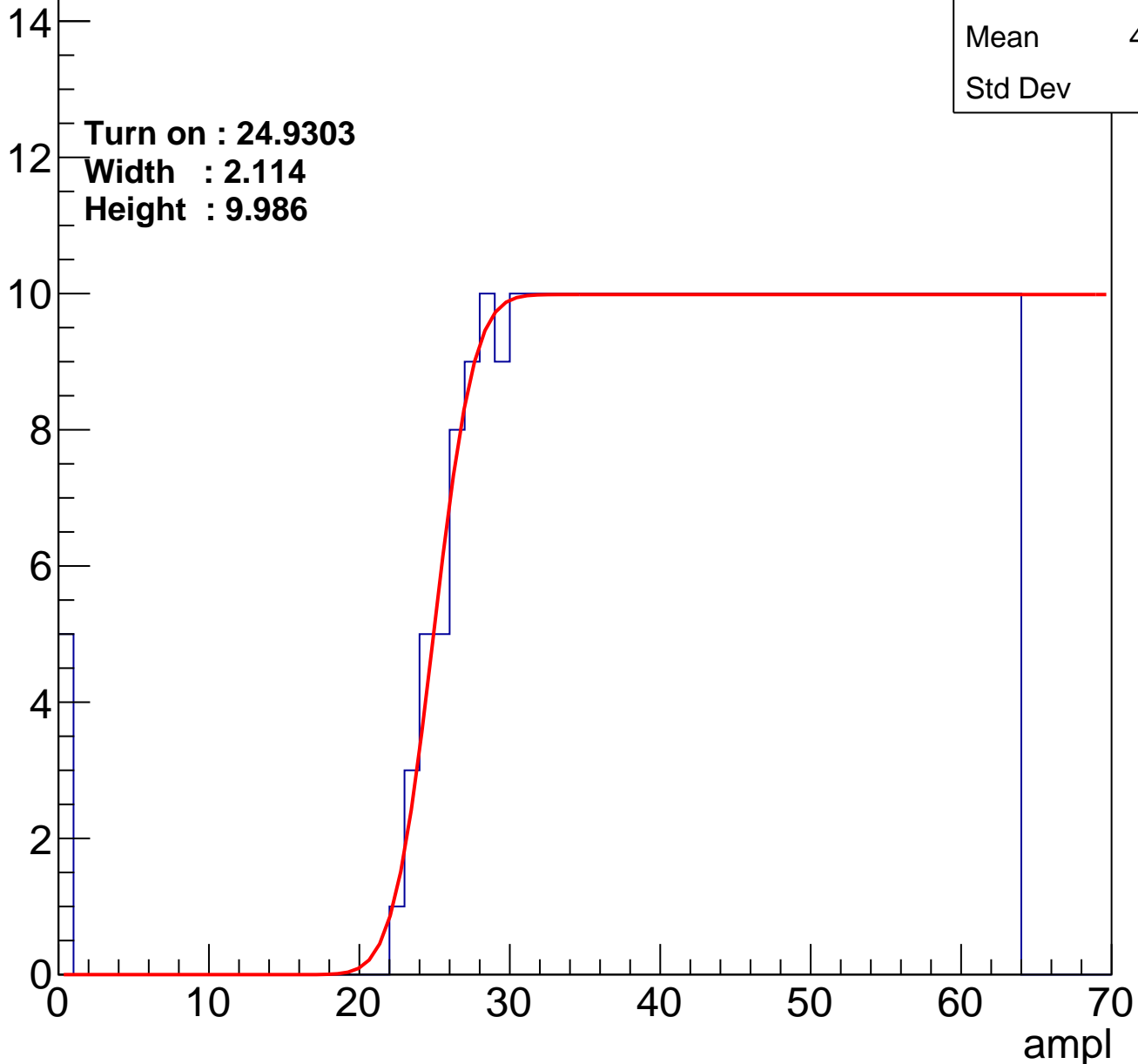
Entries	395
Mean	43.39
Std Dev	12.3

Turn on : 24.9303

Width : 2.114

Height : 9.986

Entry



B1L102S, U25-ch109

calib_packv5_042523_0143.root, FC#11, port A2

Entries	361
Mean	45.2
Std Dev	11.03

Turn on : 28.4218

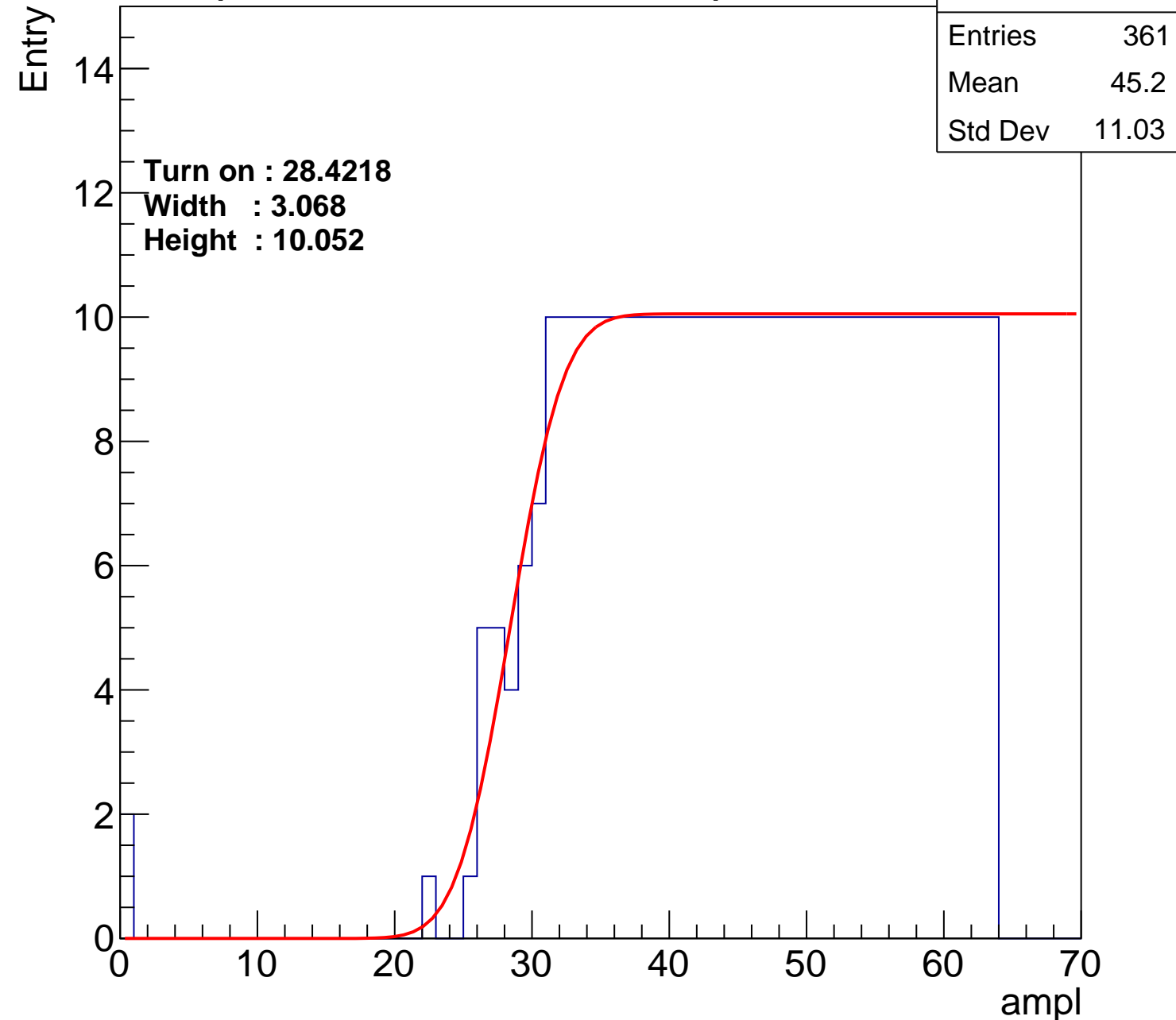
Width : 3.068

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch110

calib_packv5_042523_0143.root, FC#11, port A2

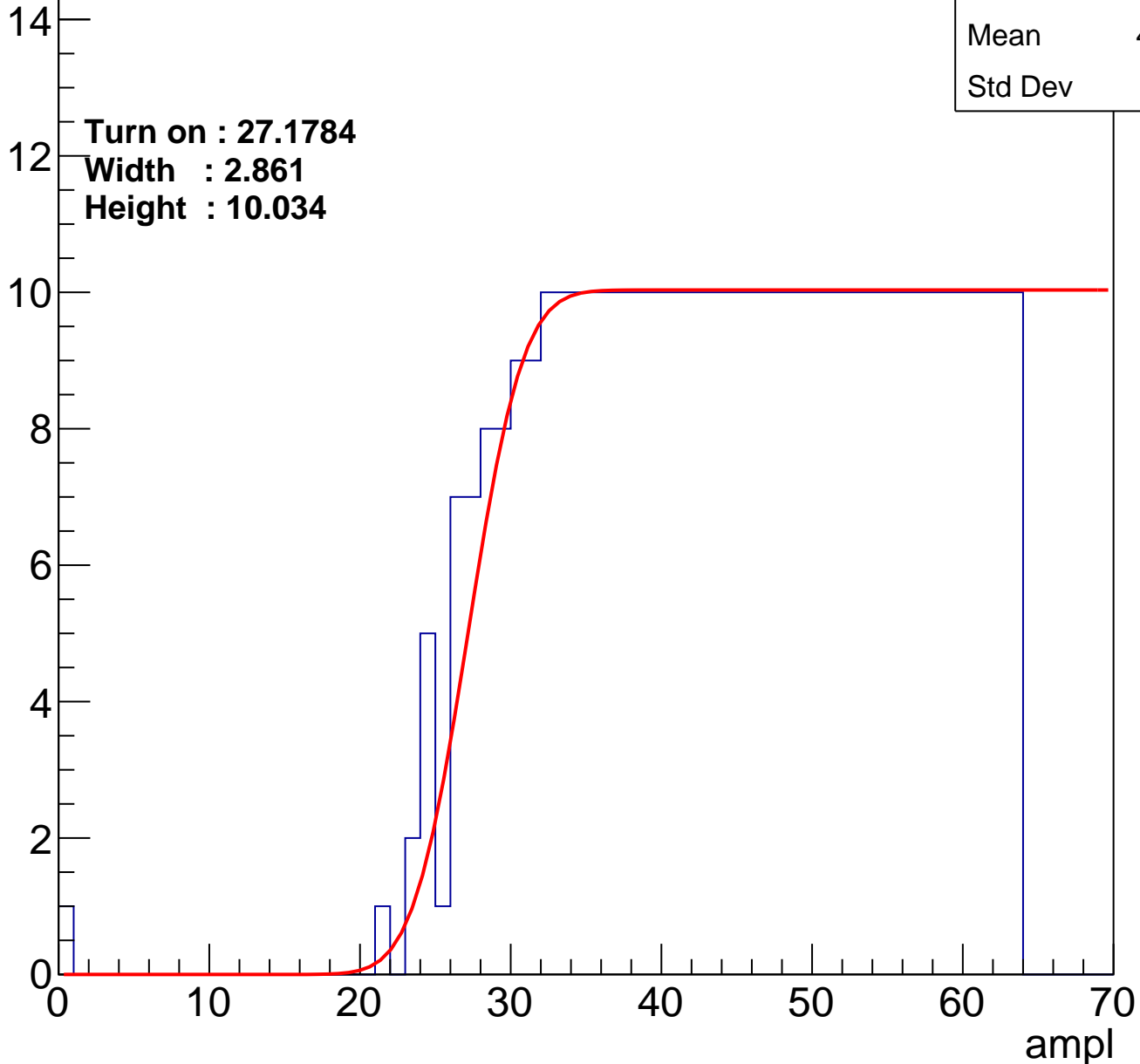
Entries	378
Mean	44.41
Std Dev	11.3

Turn on : 27.1784

Width : 2.861

Height : 10.034

Entry



B1L102S, U25-ch111

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.59
Std Dev	11.16

Turn on : 25.7892

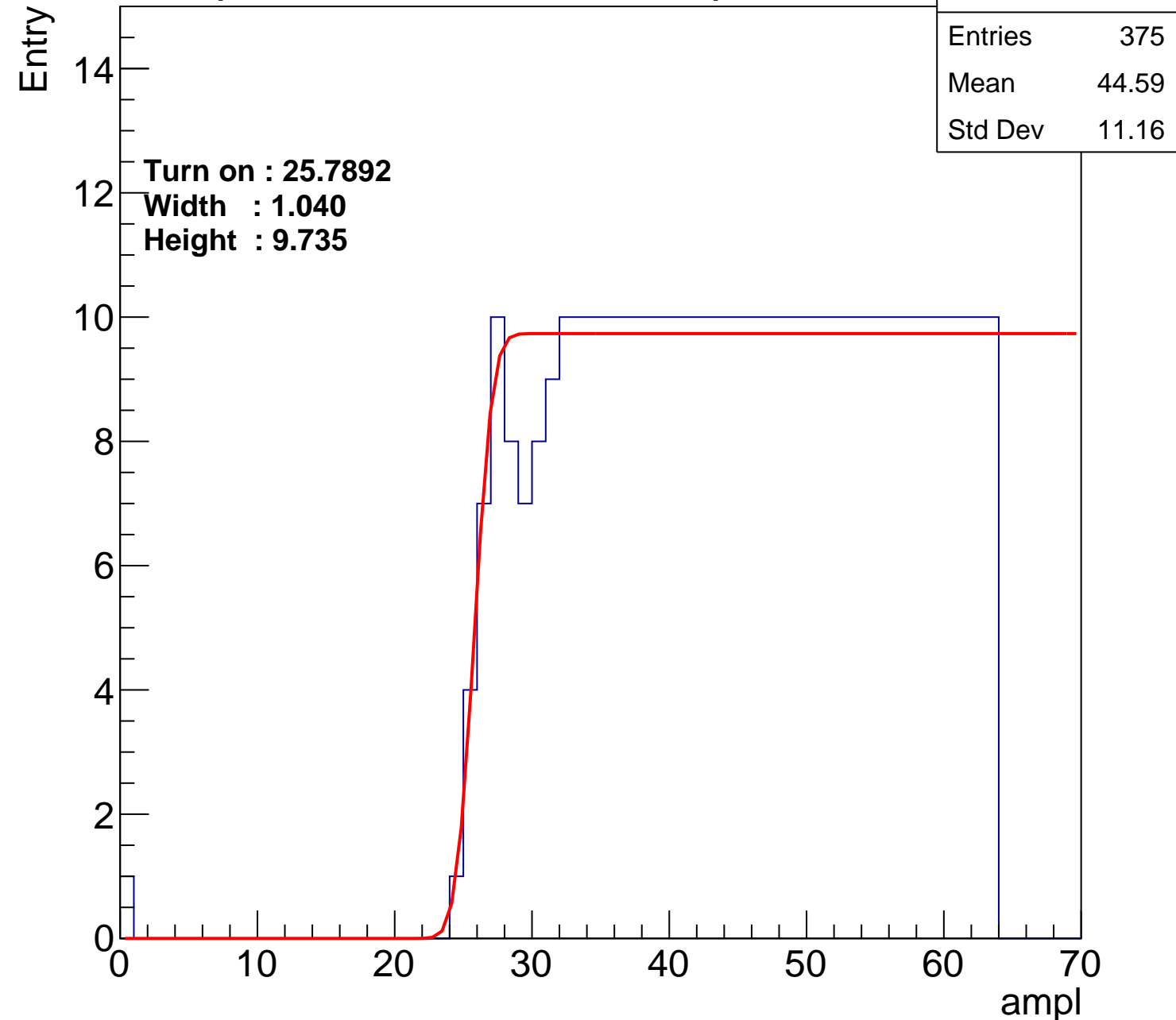
Width : 1.040

Height : 9.735

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch112

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.49
Std Dev	12.05

Turn on : 28.3991

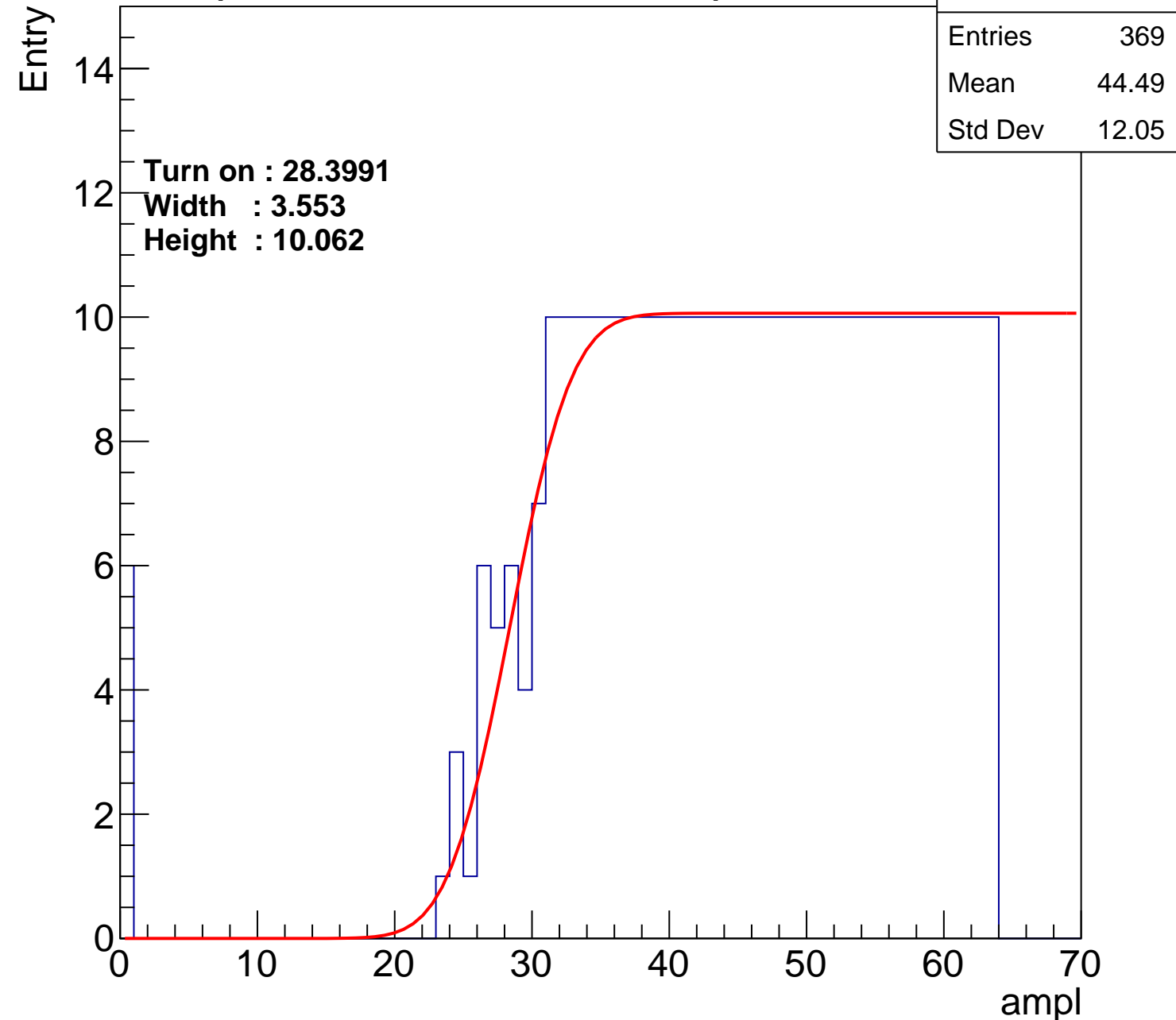
Width : 3.553

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch113

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.63
Std Dev	11.96

Turn on : 25.8689

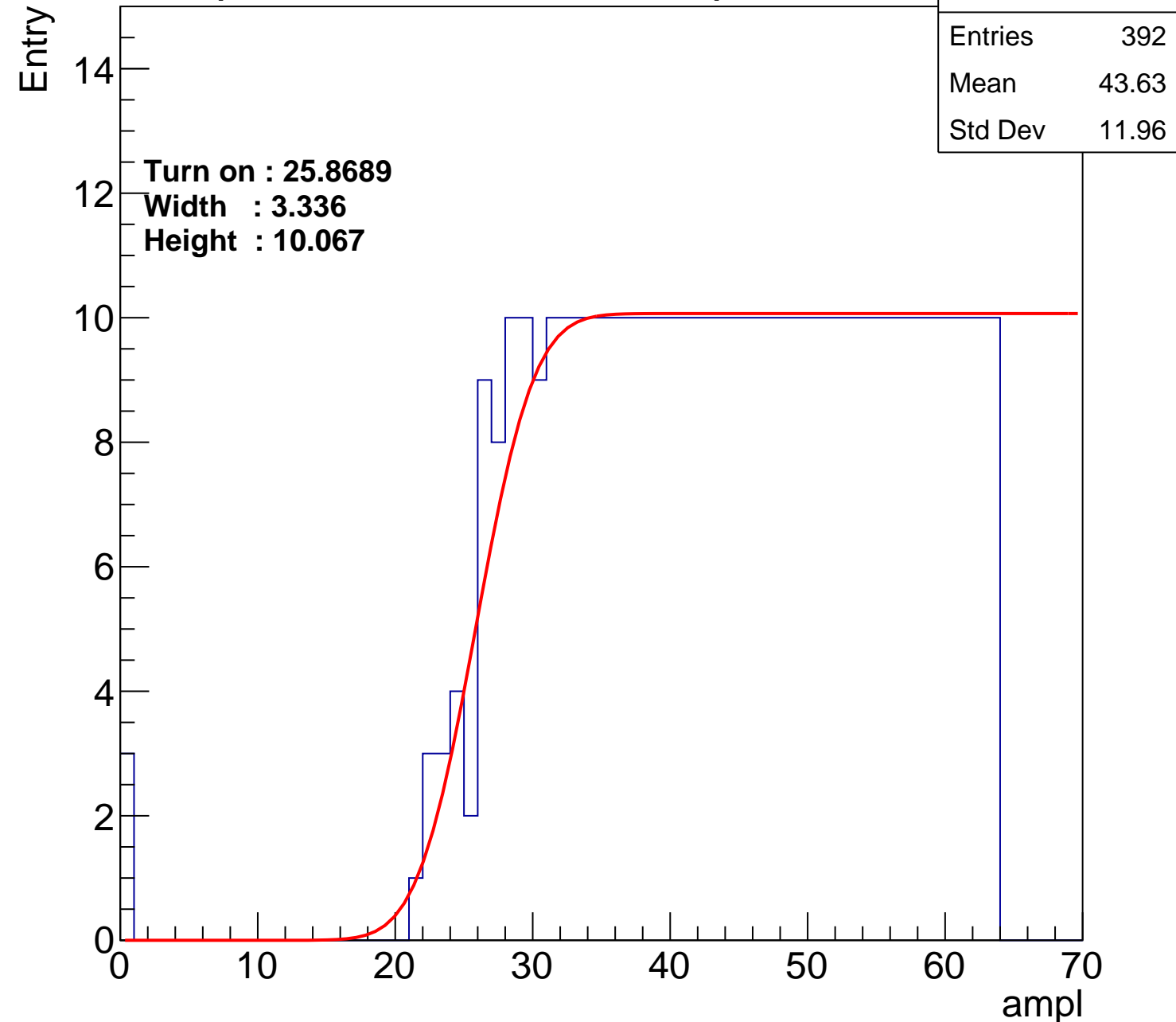
Width : 3.336

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch114

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.91
Std Dev	11.58

Turn on : 24.9834

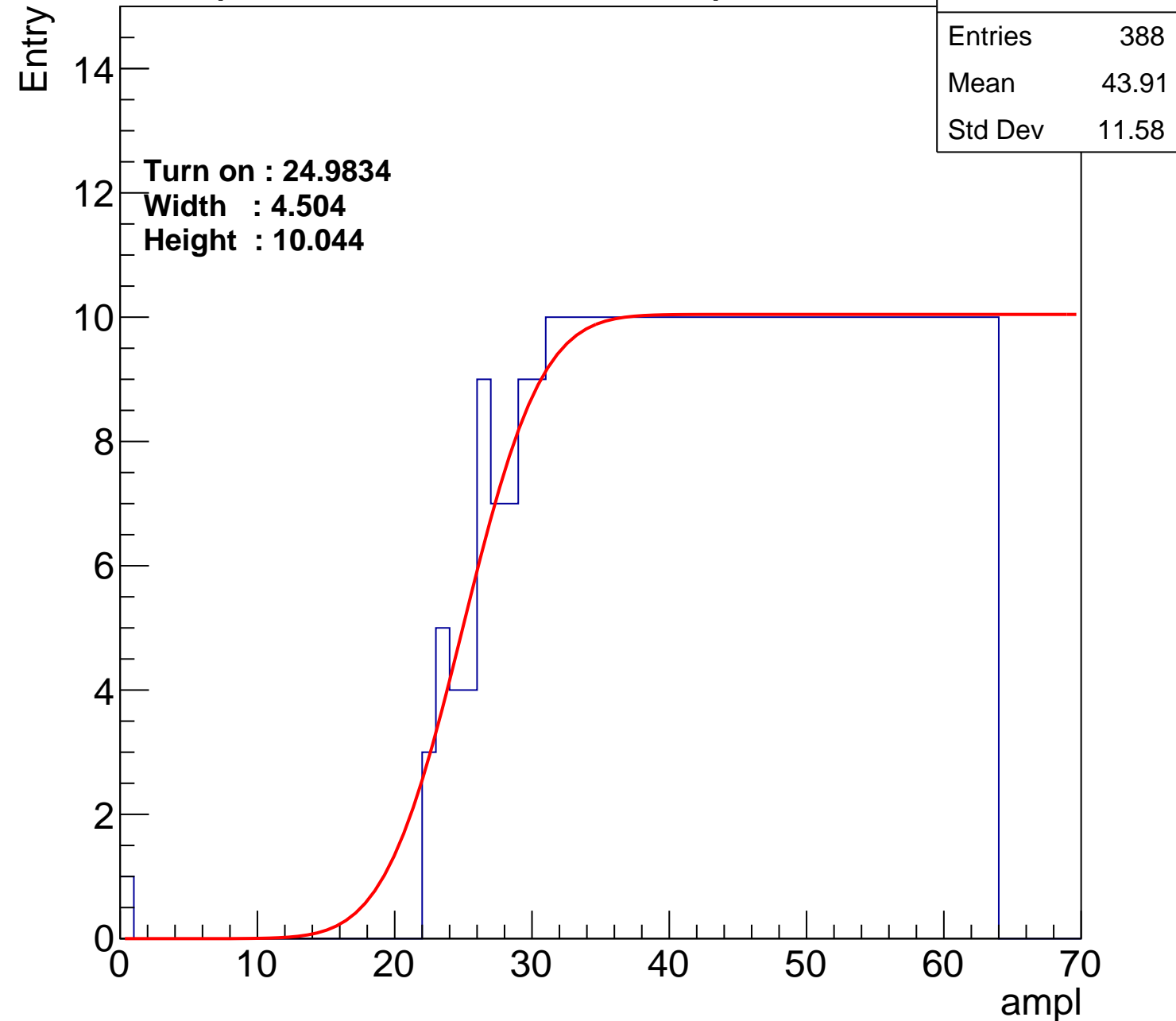
Width : 4.504

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch115

calib_packv5_042523_0143.root, FC#11, port A2

Entries	364
Mean	44.96
Std Dev	11.34

Turn on : 28.0742

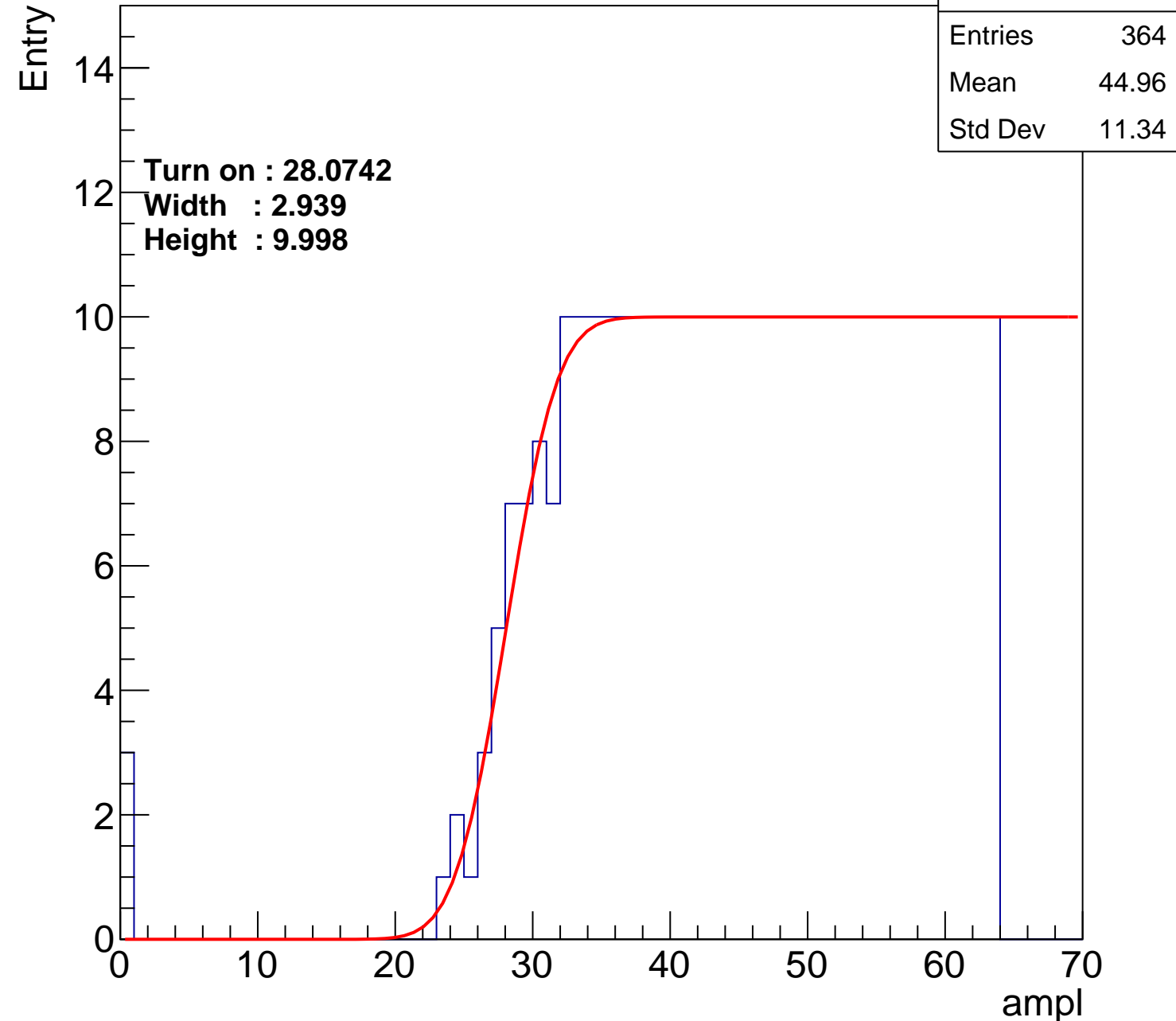
Width : 2.939

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch116

calib_packv5_042523_0143.root, FC#11, port A2

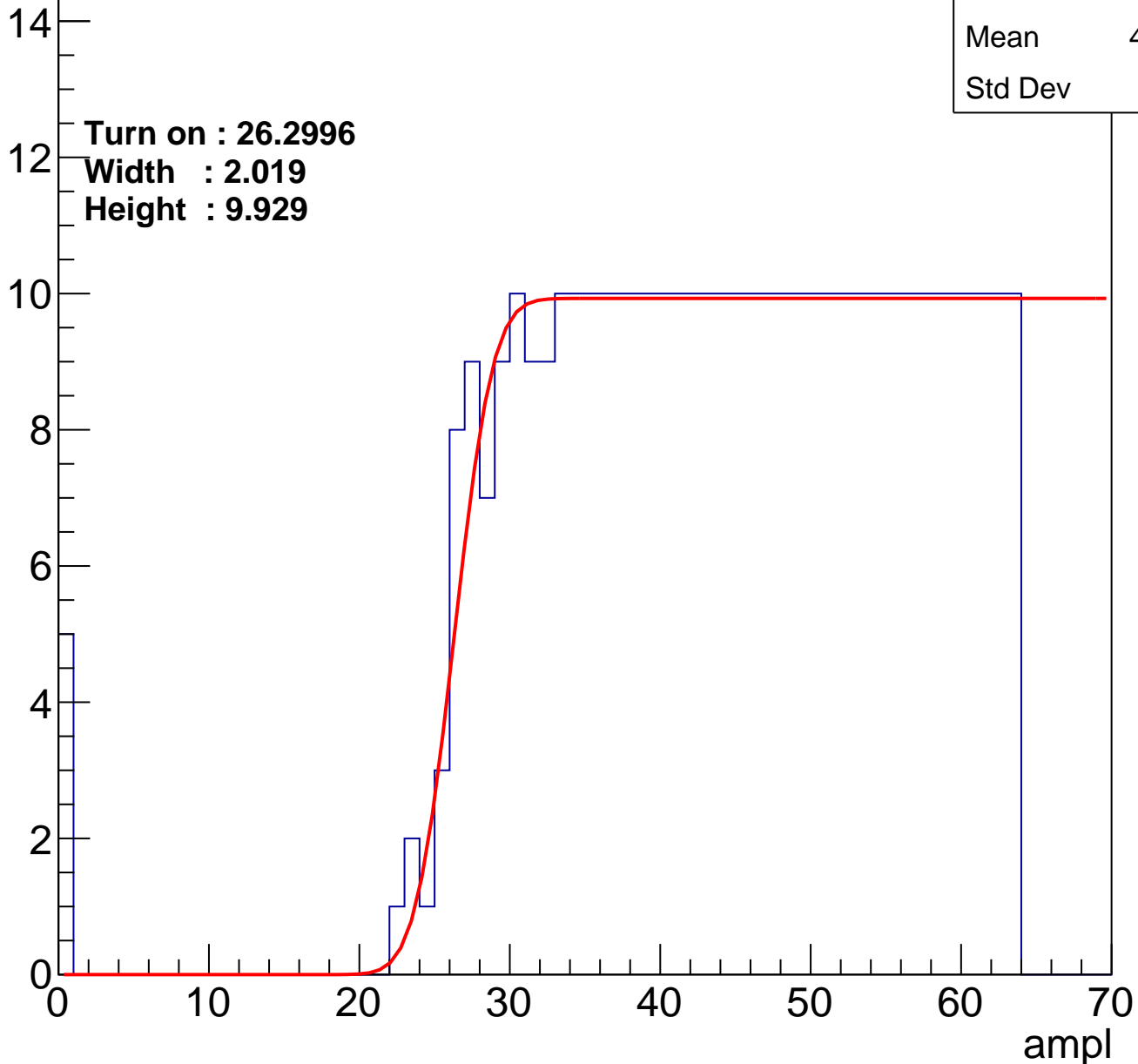
Entries	383
Mean	43.92
Std Dev	12.1

Turn on : 26.2996

Width : 2.019

Height : 9.929

Entry



B1L102S, U25-ch117

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.93
Std Dev	11.73

Turn on : 25.9747

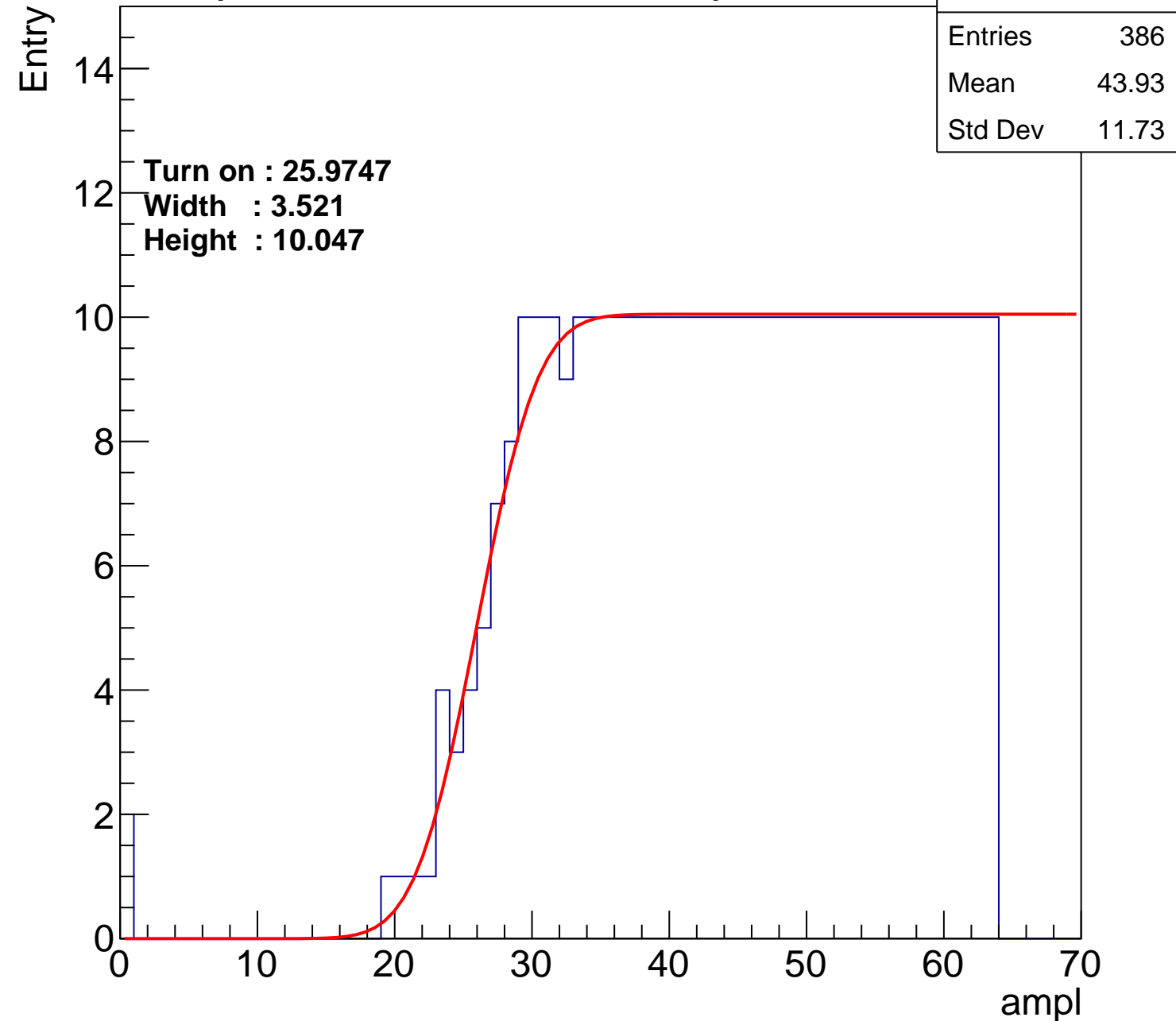
Width : 3.521

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch118

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.18
Std Dev	12.47

Turn on : 25.5171

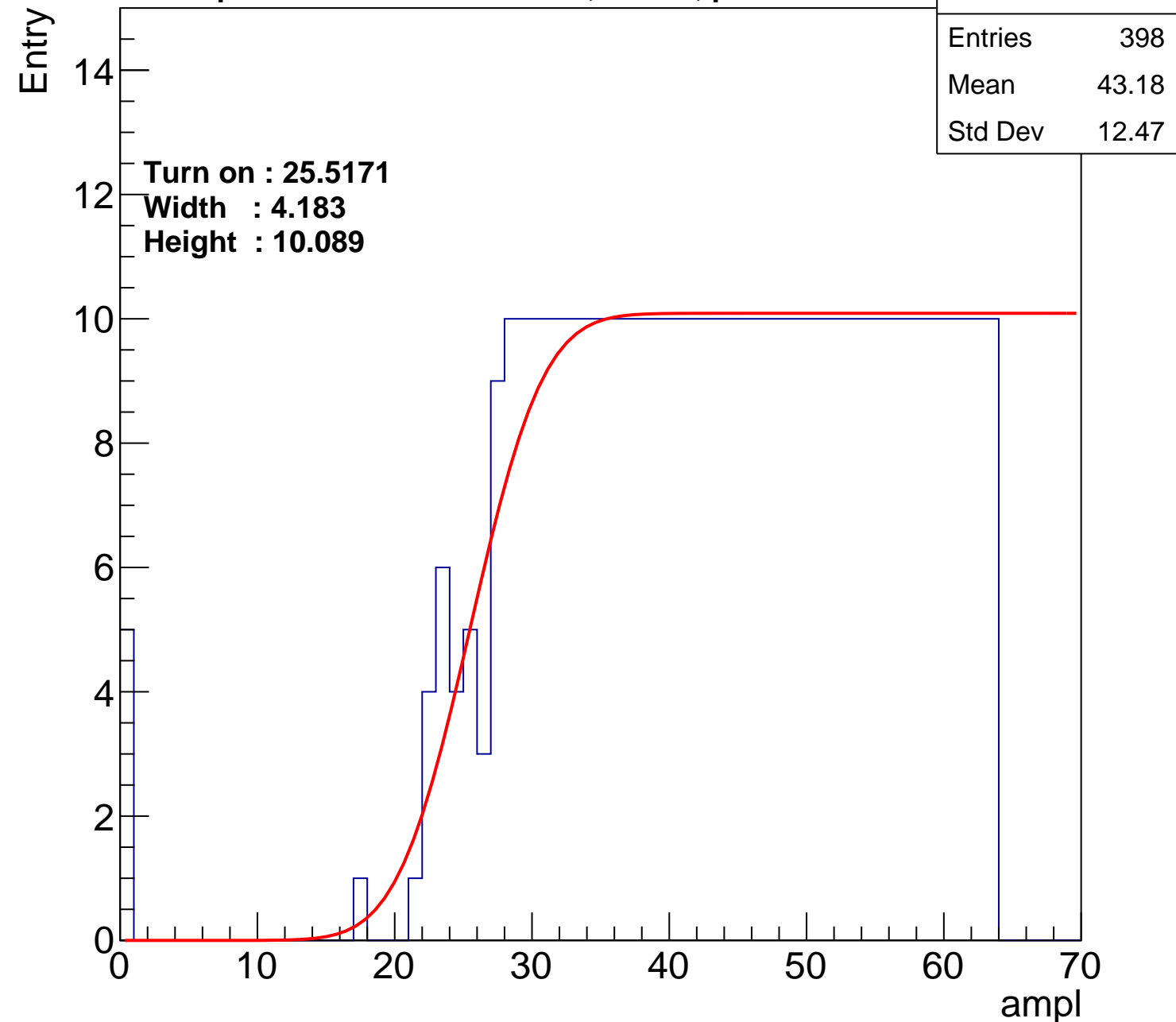
Width : 4.183

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch119

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.03
Std Dev	11.75

Turn on : 26.3290

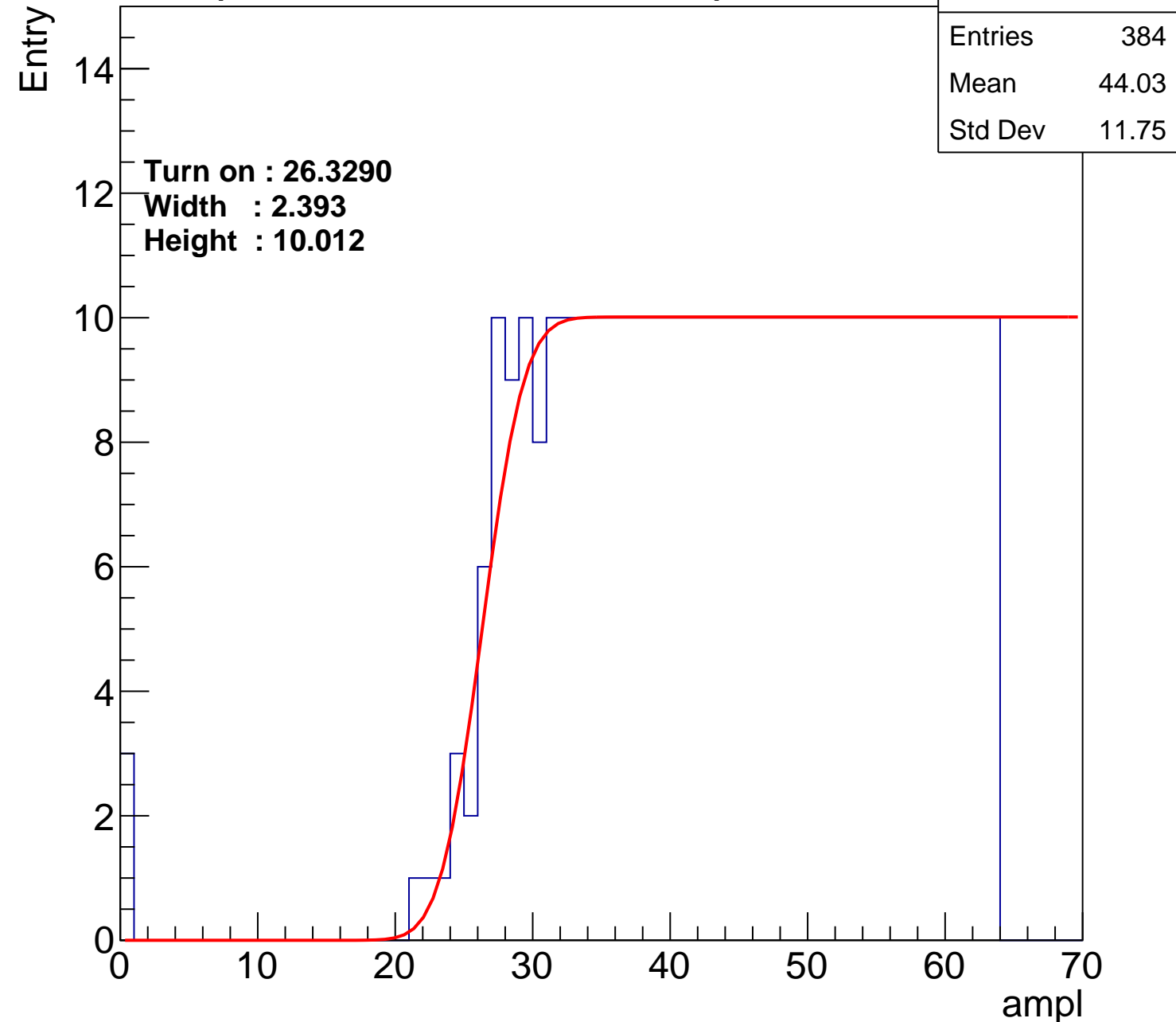
Width : 2.393

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch120

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	43.98
Std Dev	12.07

Turn on : 26.5883

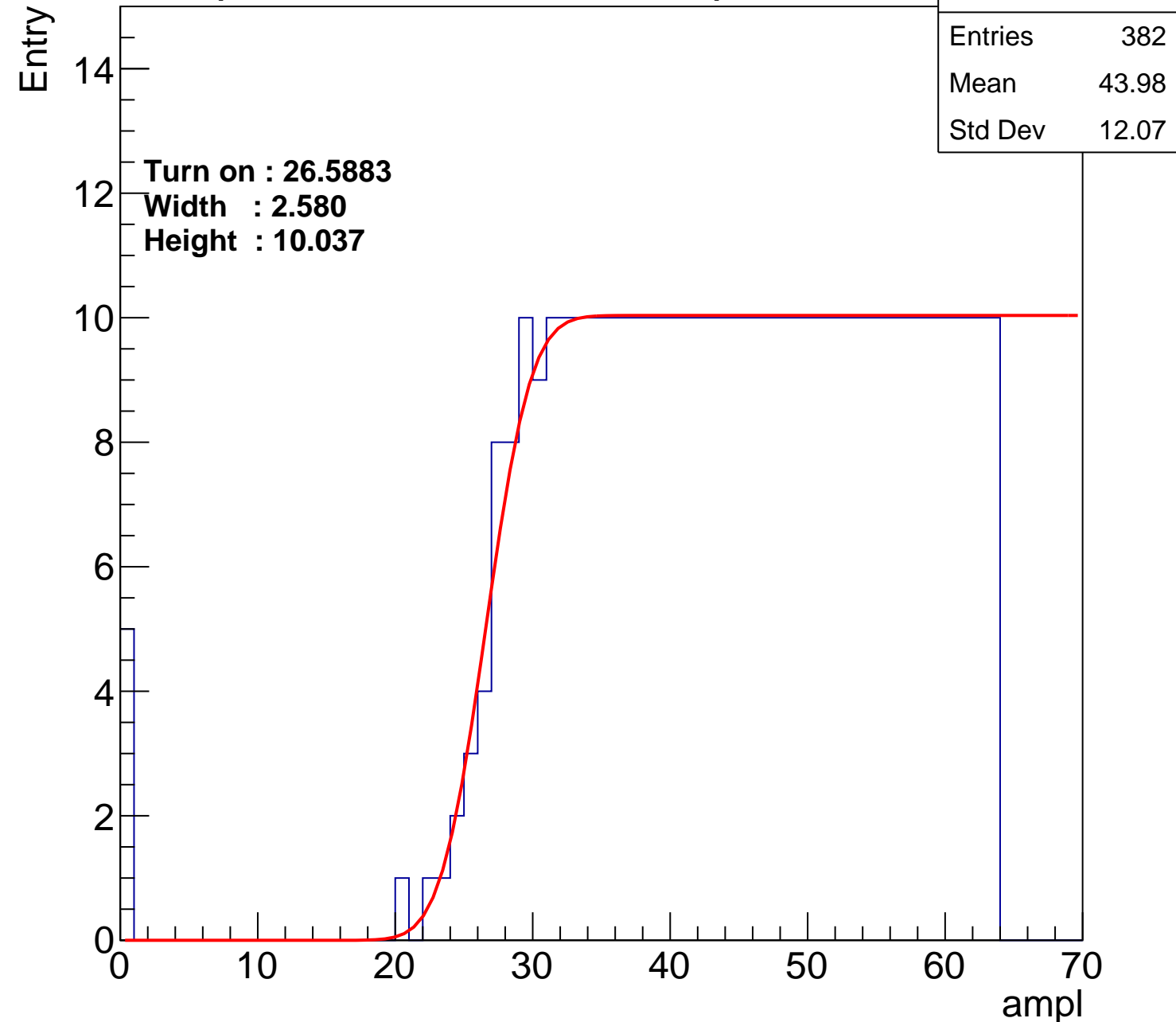
Width : 2.580

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch121

calib_packv5_042523_0143.root, FC#11, port A2

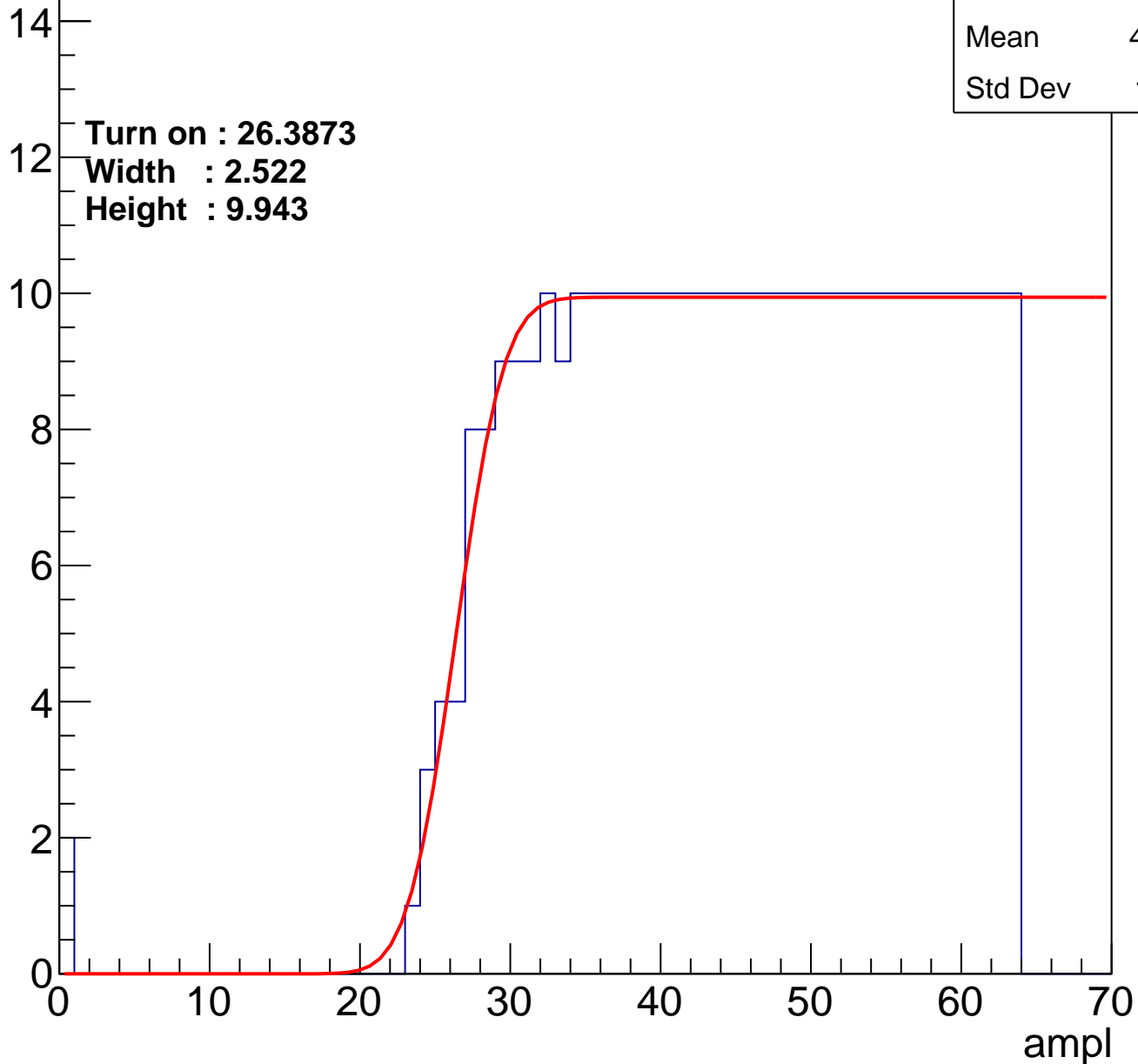
Entries	376
Mean	44.46
Std Dev	11.41

Turn on : 26.3873

Width : 2.522

Height : 9.943

Entry



B1L102S, U25-ch122

calib_packv5_042523_0143.root, FC#11, port A2

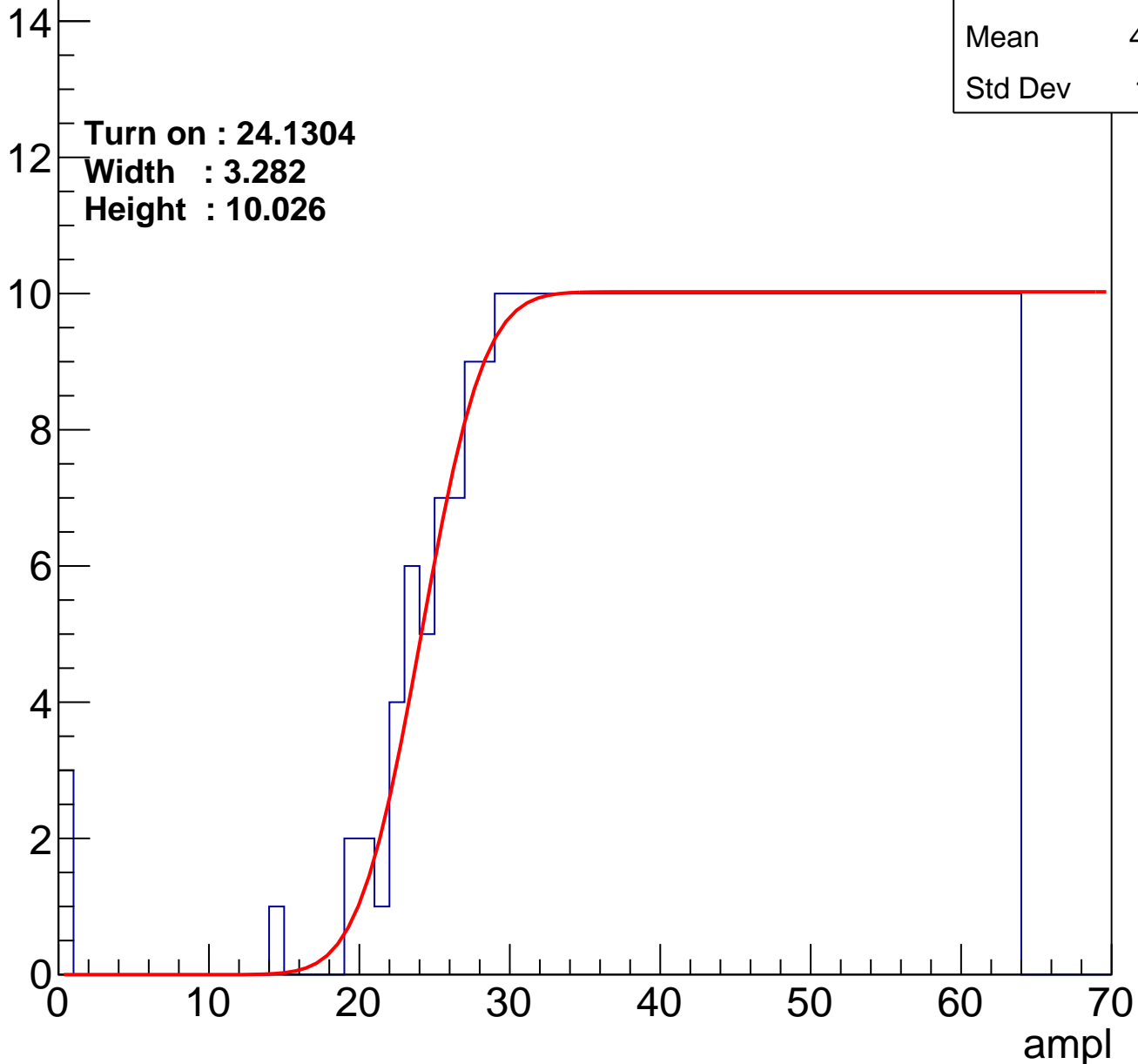
Entries	406
Mean	42.88
Std Dev	12.41

Turn on : 24.1304

Width : 3.282

Height : 10.026

Entry



B1L102S, U25-ch123

calib_packv5_042523_0143.root, FC#11, port A2

Entries	358
Mean	45.11
Std Dev	11.6

Turn on : 28.2681

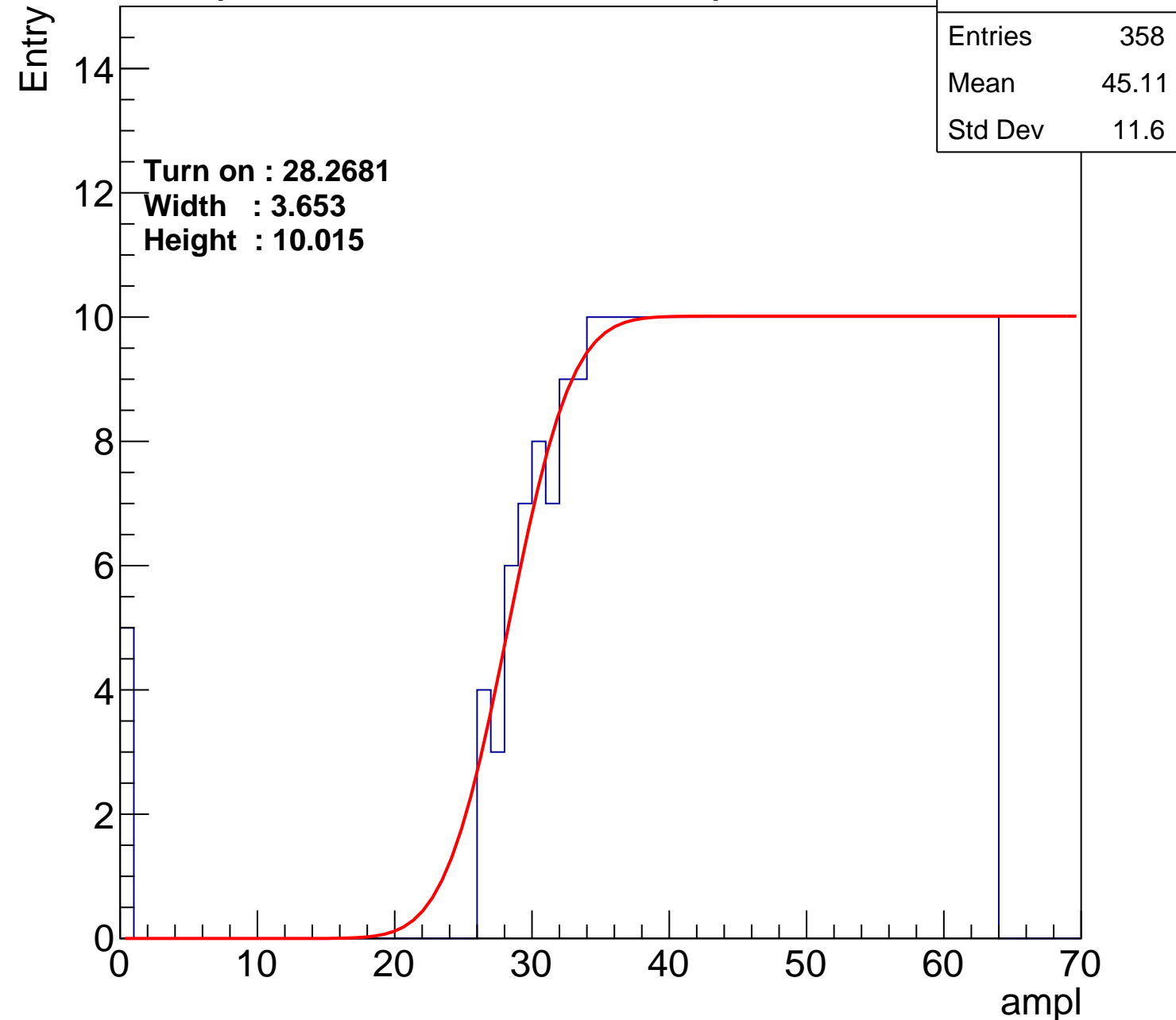
Width : 3.653

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch124

calib_packv5_042523_0143.root, FC#11, port A2

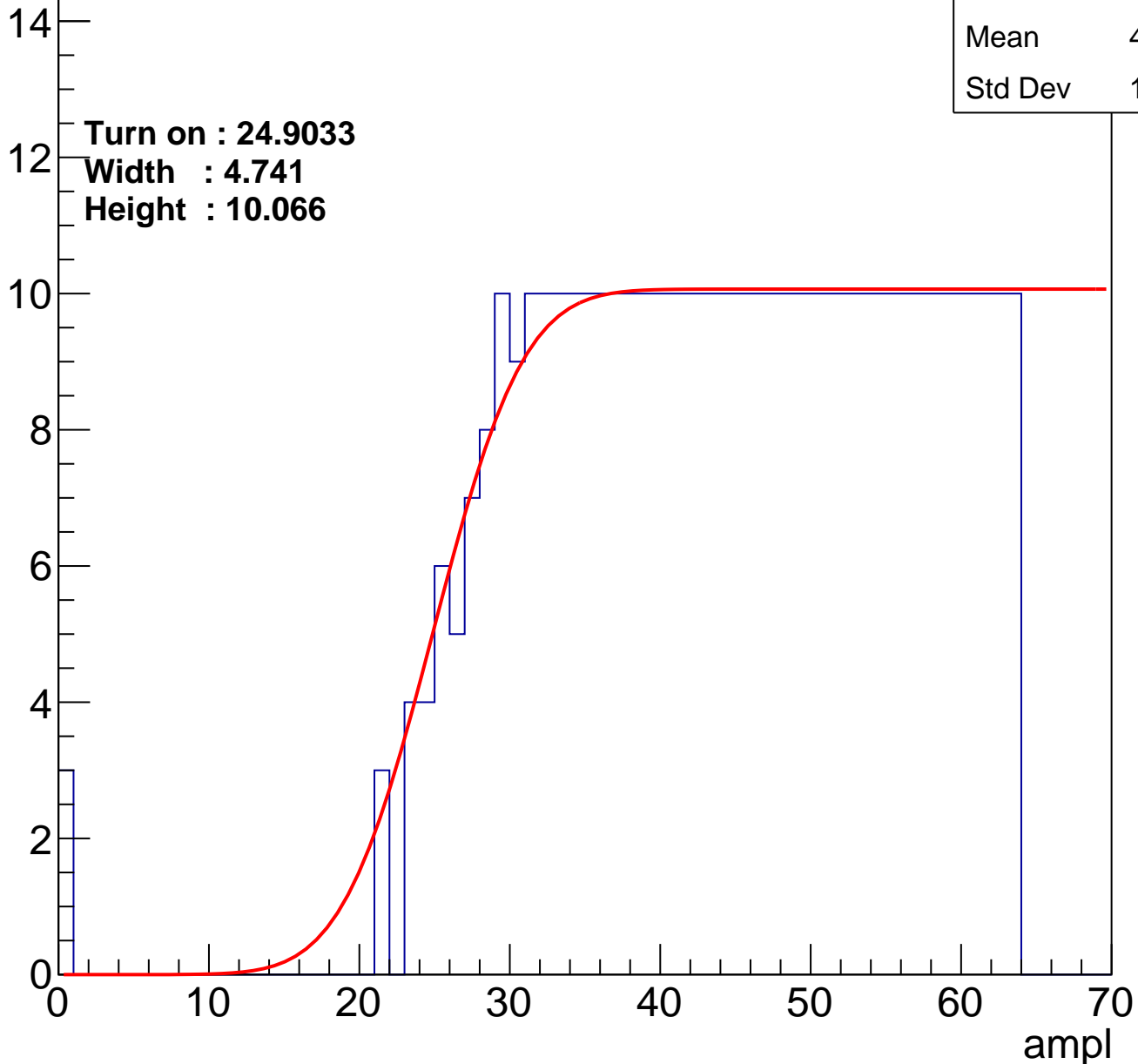
Entries	389
Mean	43.74
Std Dev	11.94

Turn on : 24.9033

Width : 4.741

Height : 10.066

Entry



B1L102S, U25-ch125

calib_packv5_042523_0143.root, FC#11, port A2

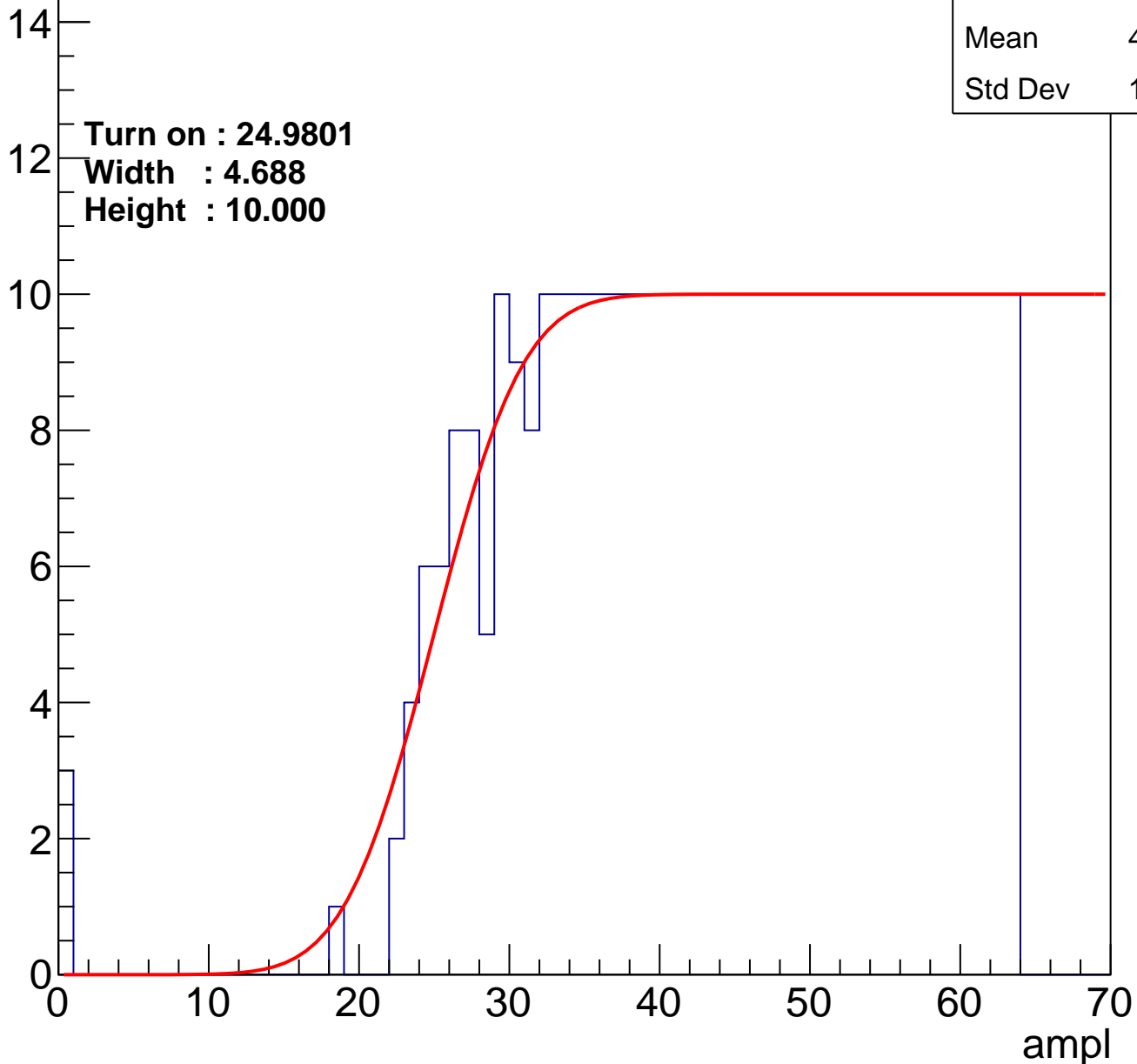
Entries	390
Mean	43.64
Std Dev	12.03

Turn on : 24.9801

Width : 4.688

Height : 10.000

Entry



B1L102S, U25-ch126

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.25
Std Dev	11.46

Turn on : 26.5197

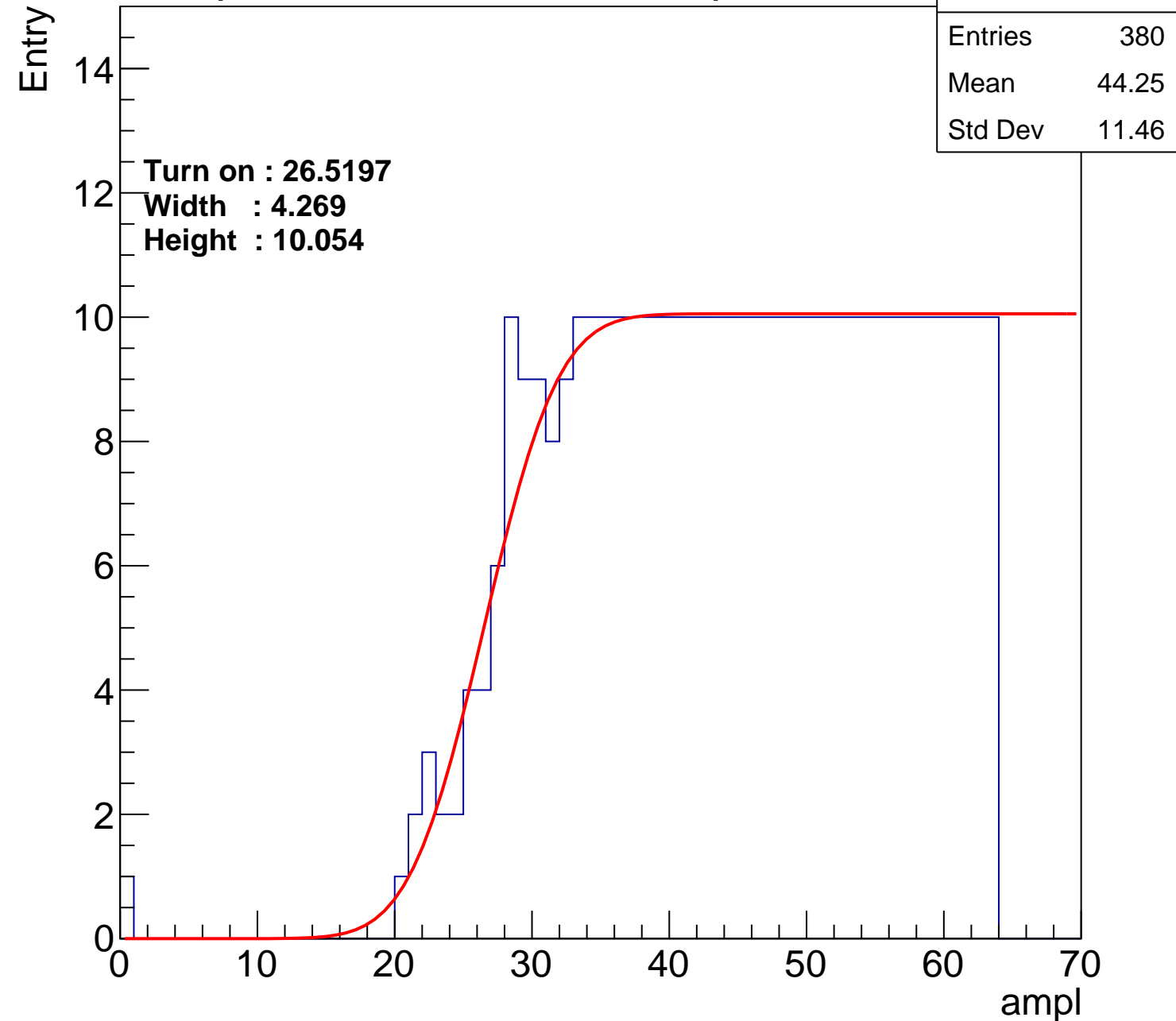
Width : 4.269

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.68
Std Dev	12.38

Turn on : 26.4459

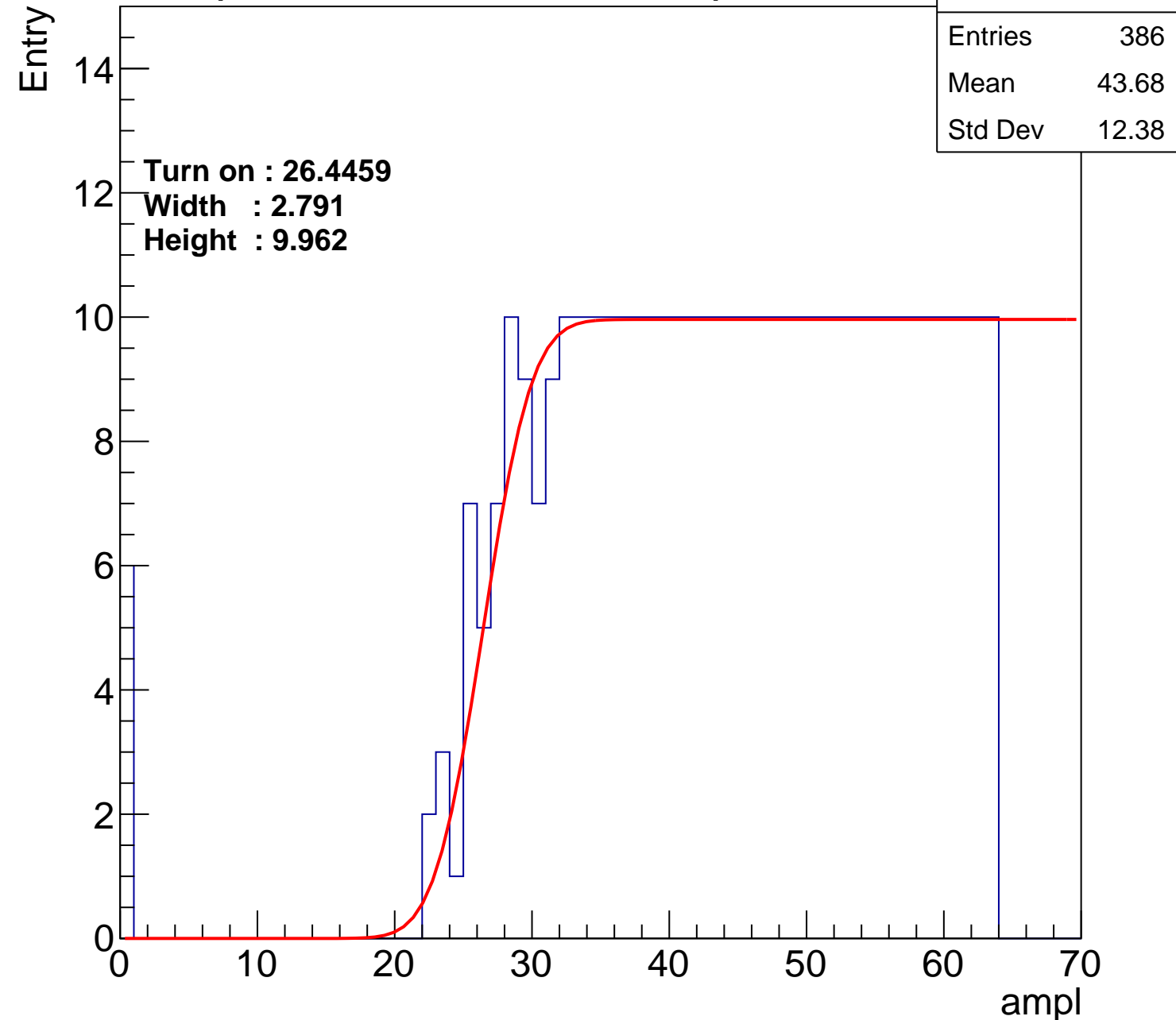
Width : 2.791

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U25-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.68
Std Dev	12.38

Turn on : 26.4459

Width : 2.791

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl

