

B0L002S, U14-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.28
Std Dev	11.01

Turn on : 28.0766

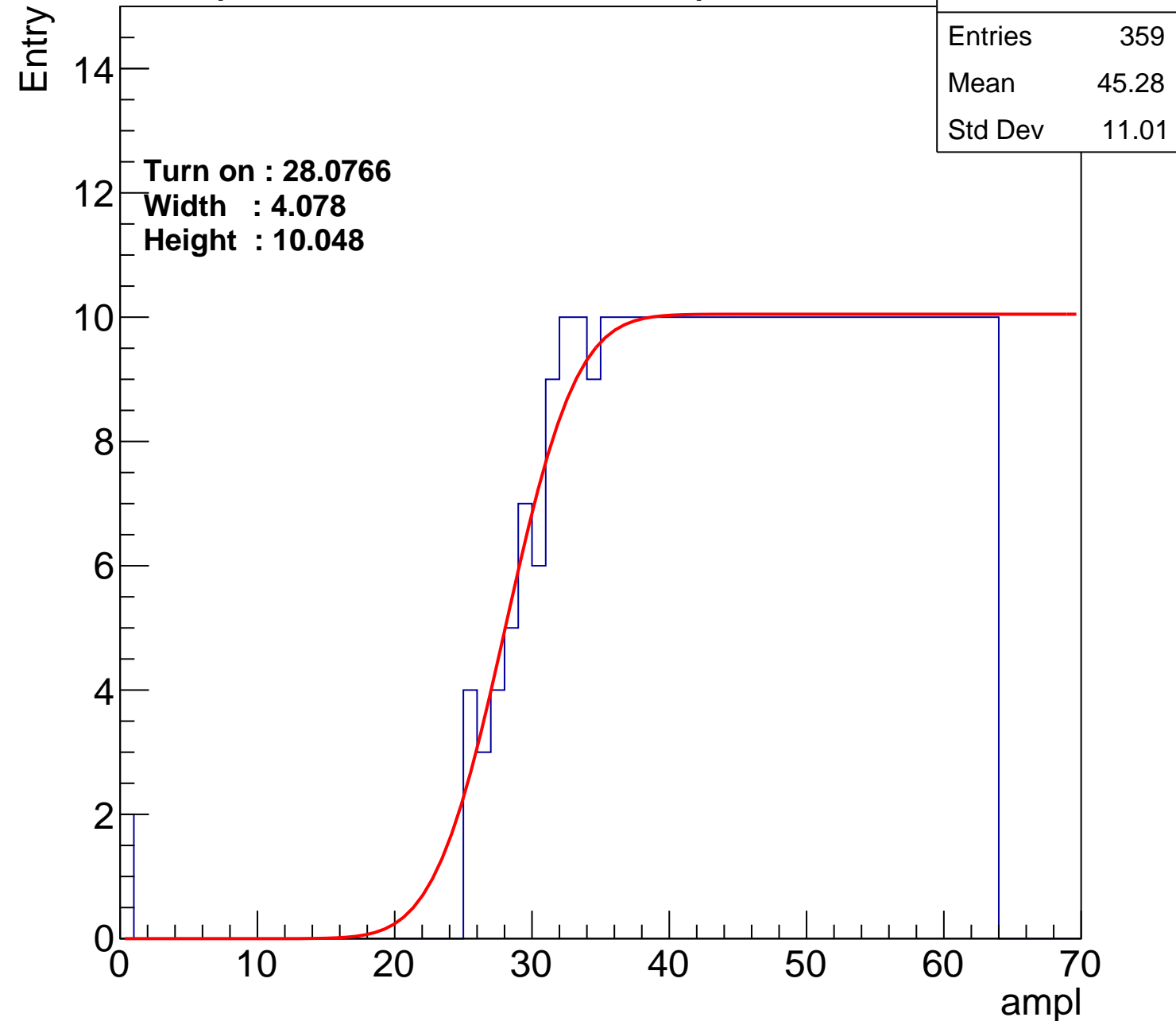
Width : 4.078

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.13
Std Dev	11.75

Turn on : 26.3262

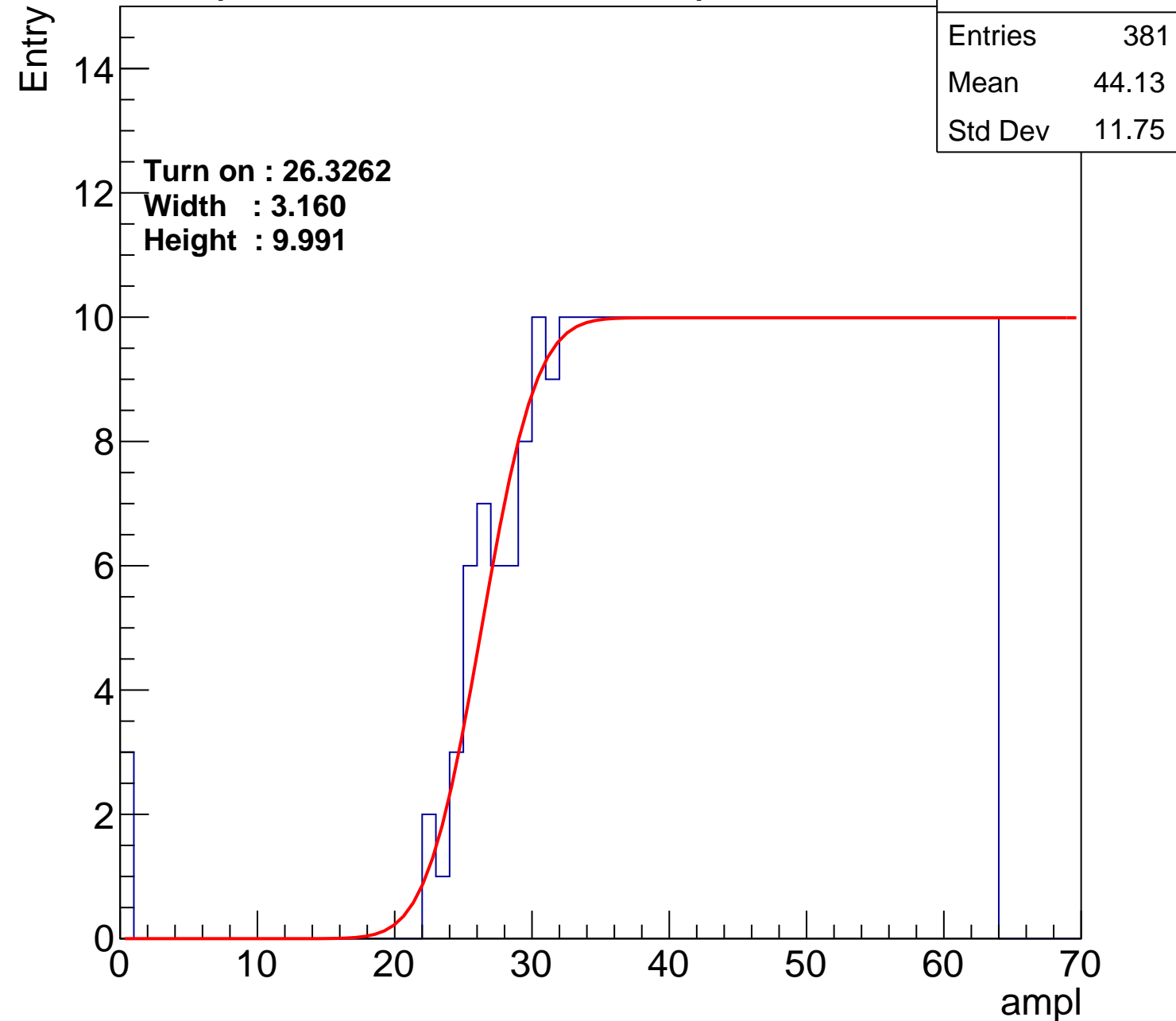
Width : 3.160

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch2

calib_packv5_042523_0143.root, FC#8, port C1

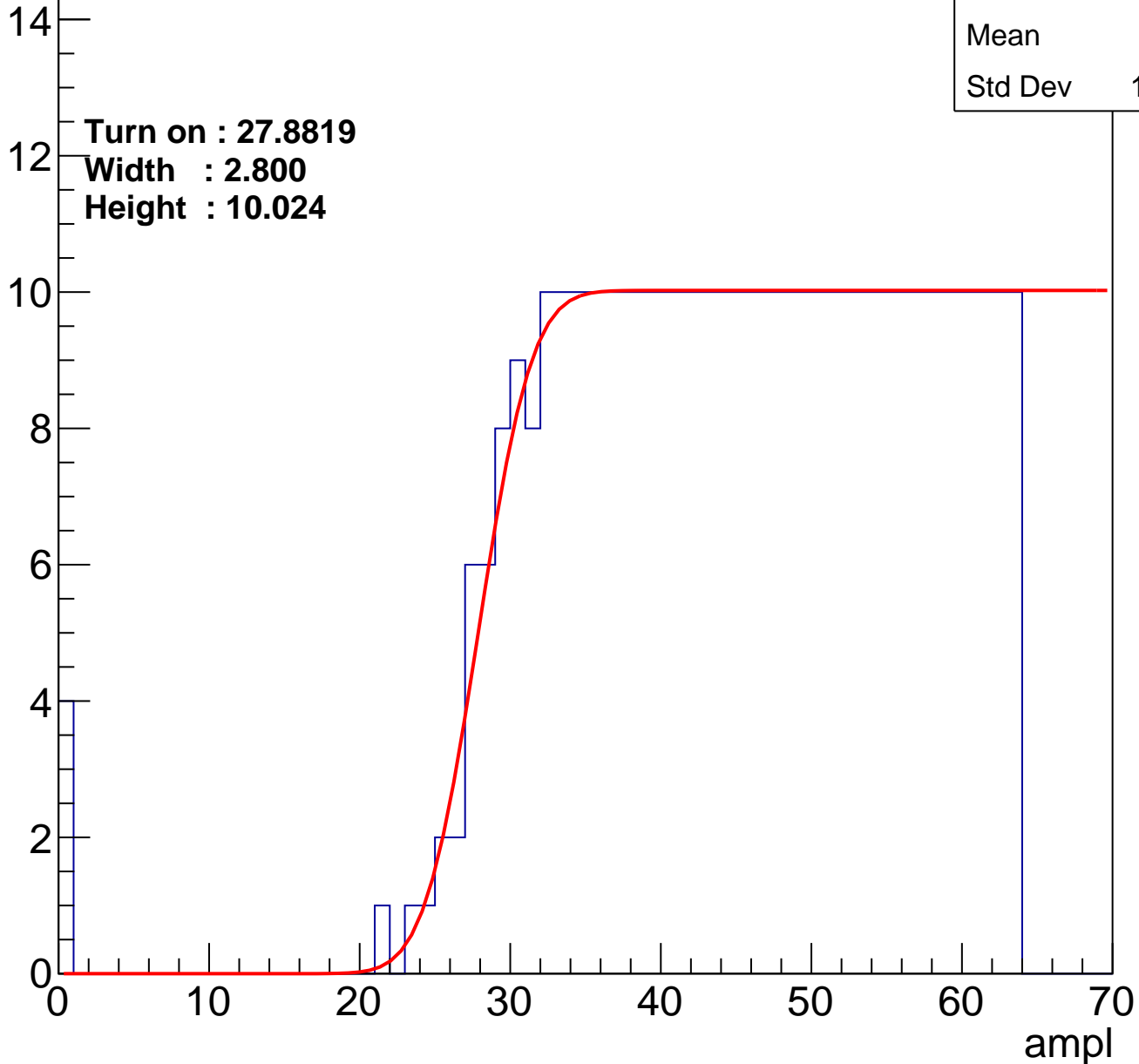
Entries	368
Mean	44.7
Std Dev	11.62

Turn on : 27.8819

Width : 2.800

Height : 10.024

Entry



B0L002S, U14-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.57
Std Dev	11.32

Turn on : 26.4253

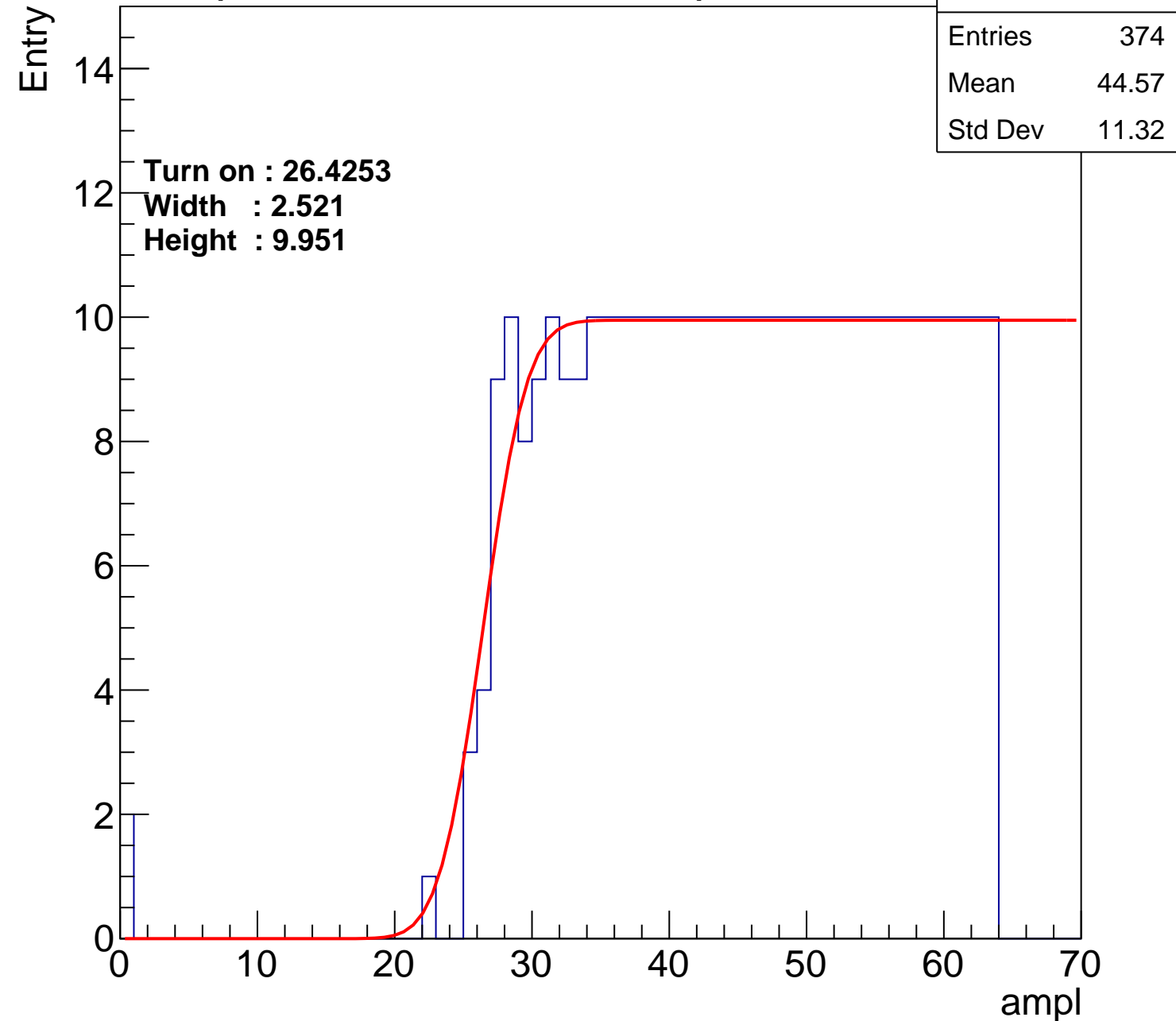
Width : 2.521

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.74
Std Dev	11.74

Turn on : 27.9655

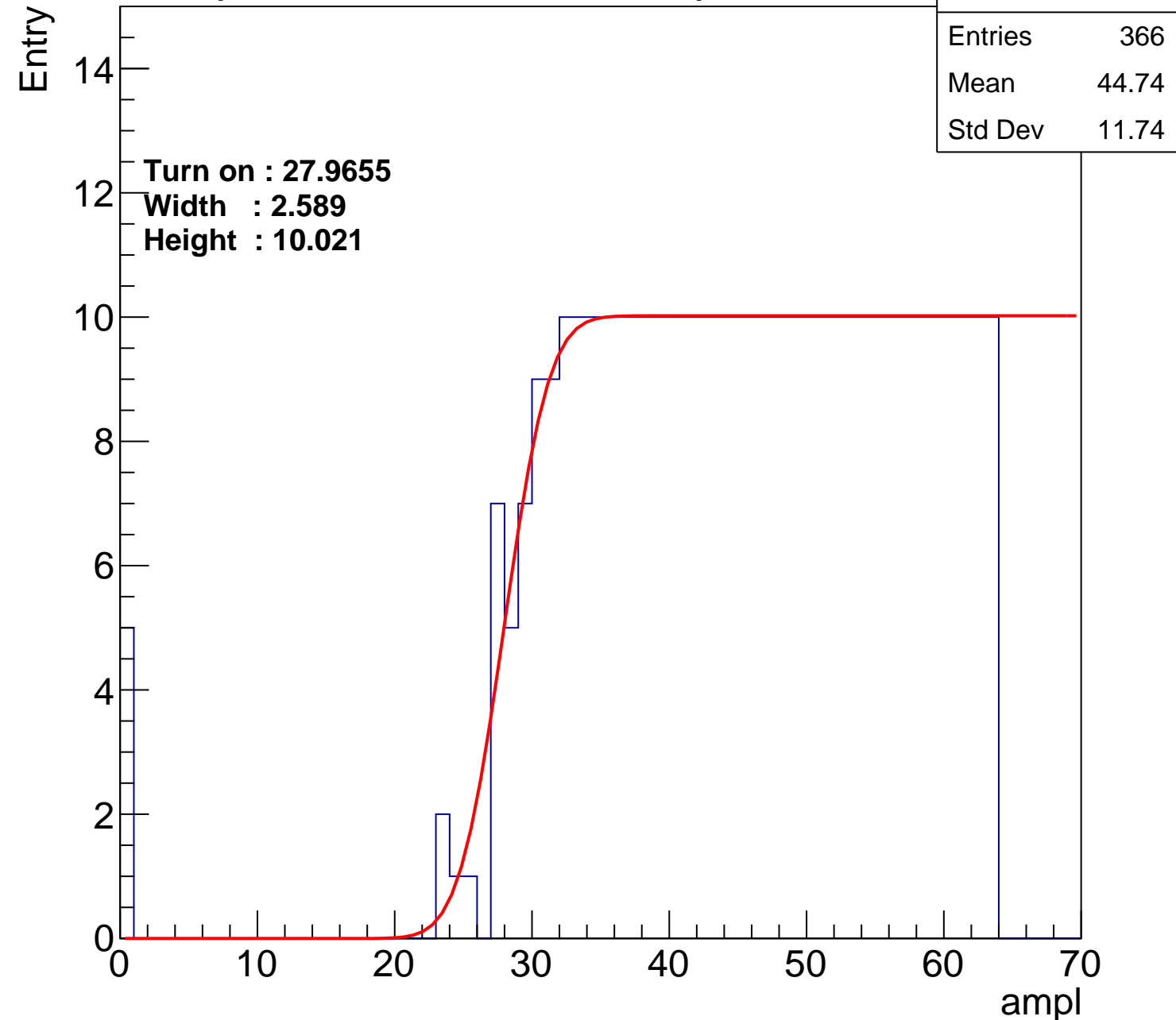
Width : 2.589

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch5

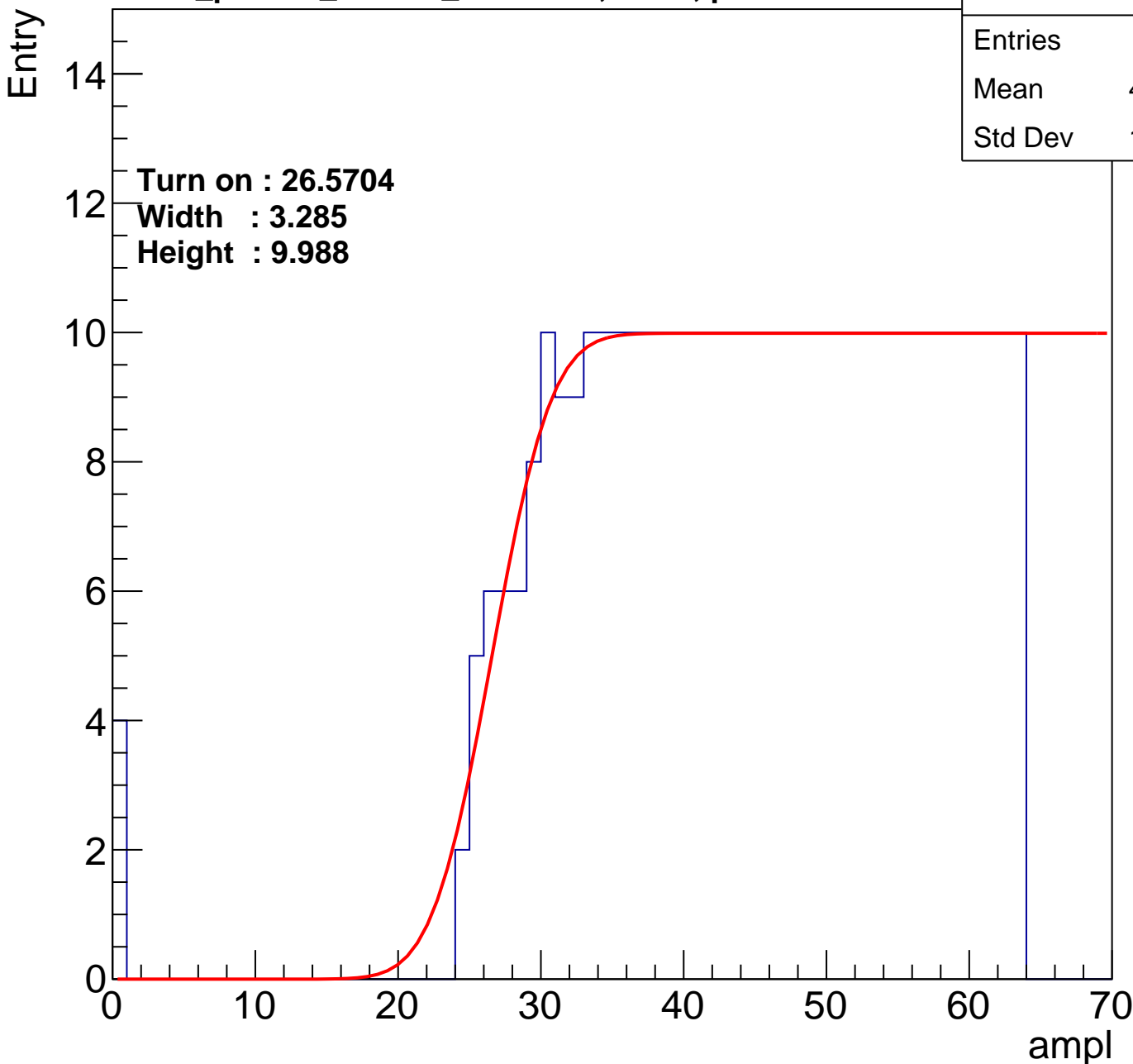
calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.37
Std Dev	11.76

Turn on : 26.5704

Width : 3.285

Height : 9.988



B0L002S, U14-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.16
Std Dev	11.08

Turn on : 27.8845

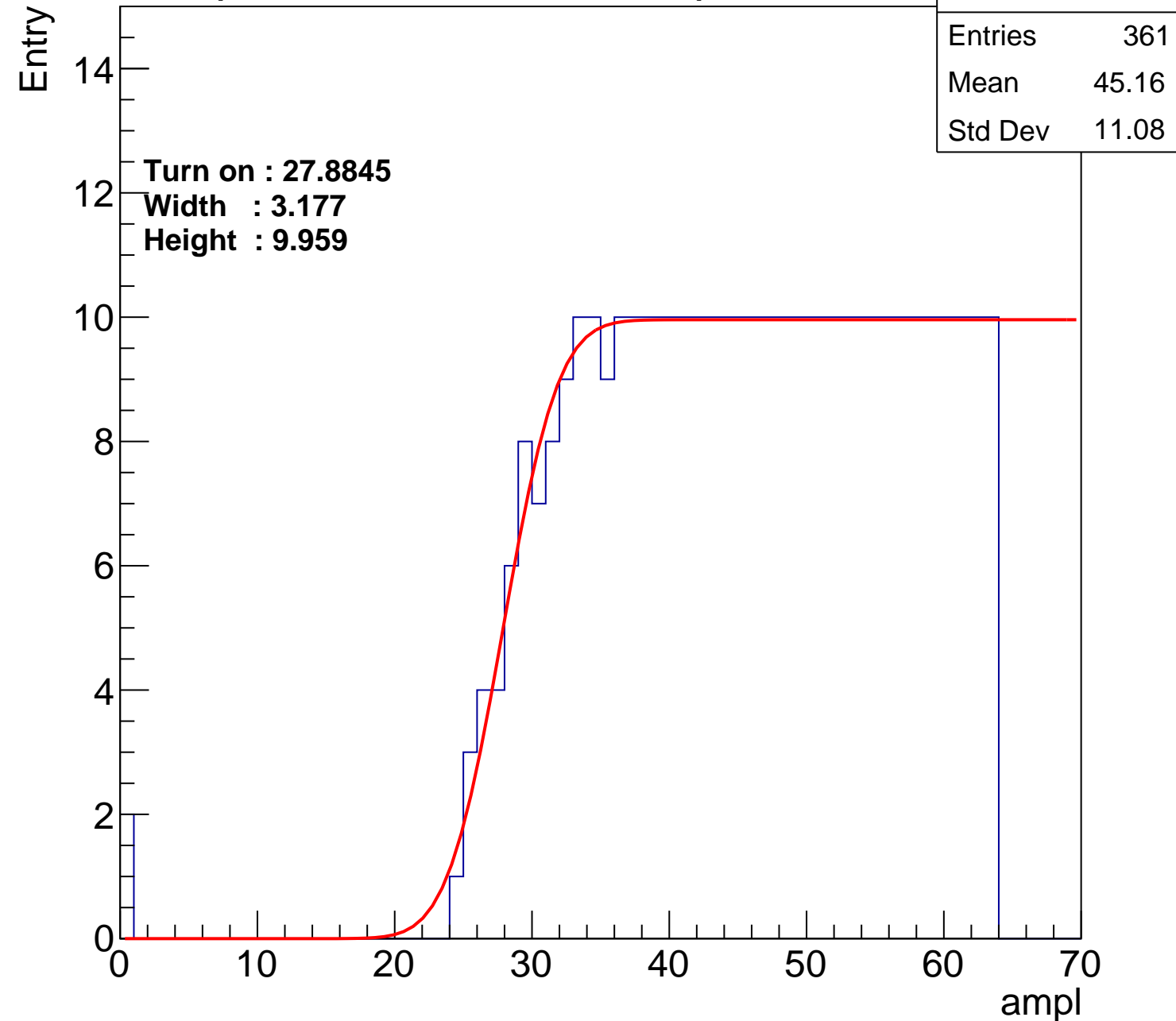
Width : 3.177

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch7

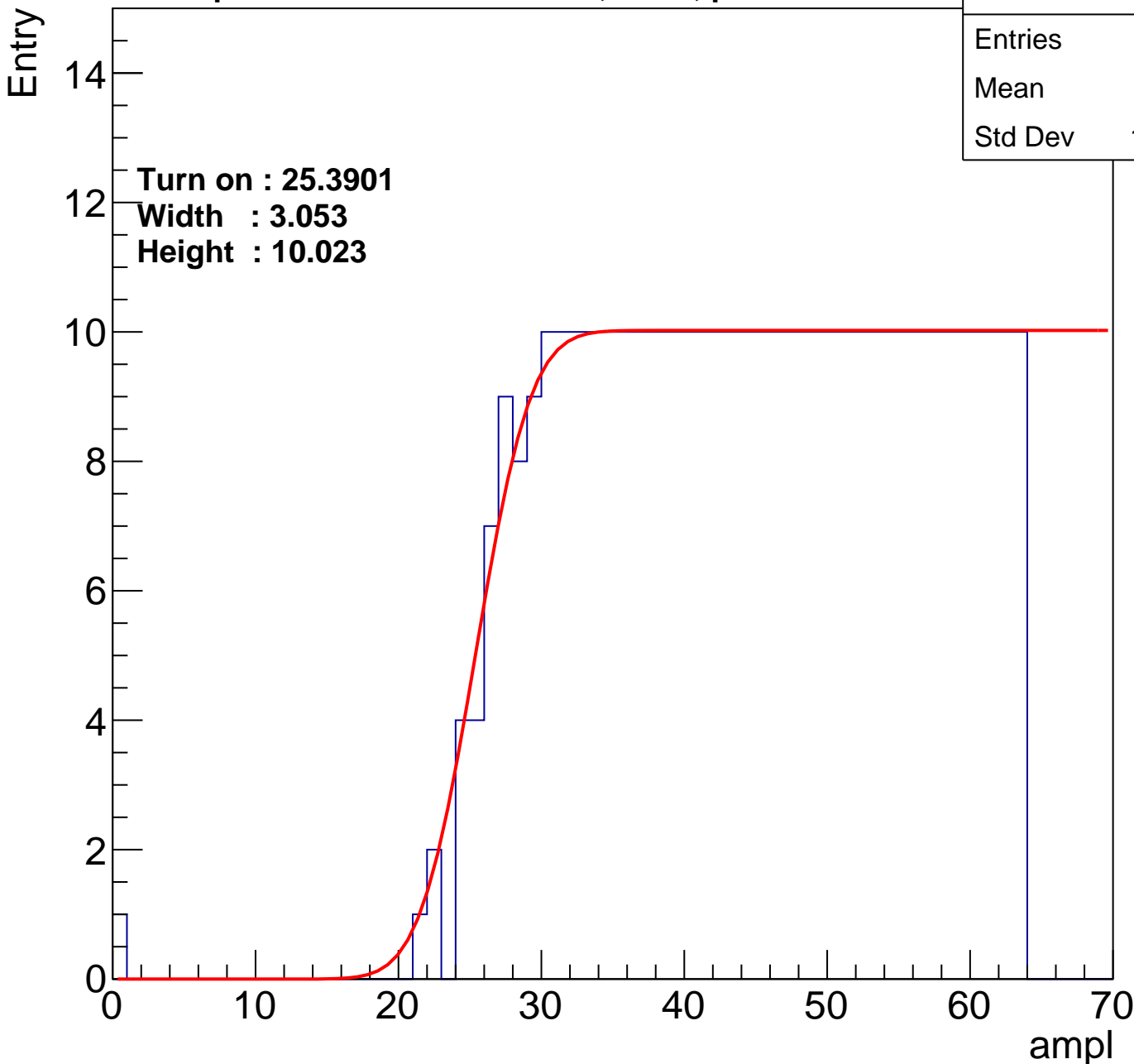
calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 25.3901

Width : 3.053

Height : 10.023



B0L002S, U14-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 26.3303

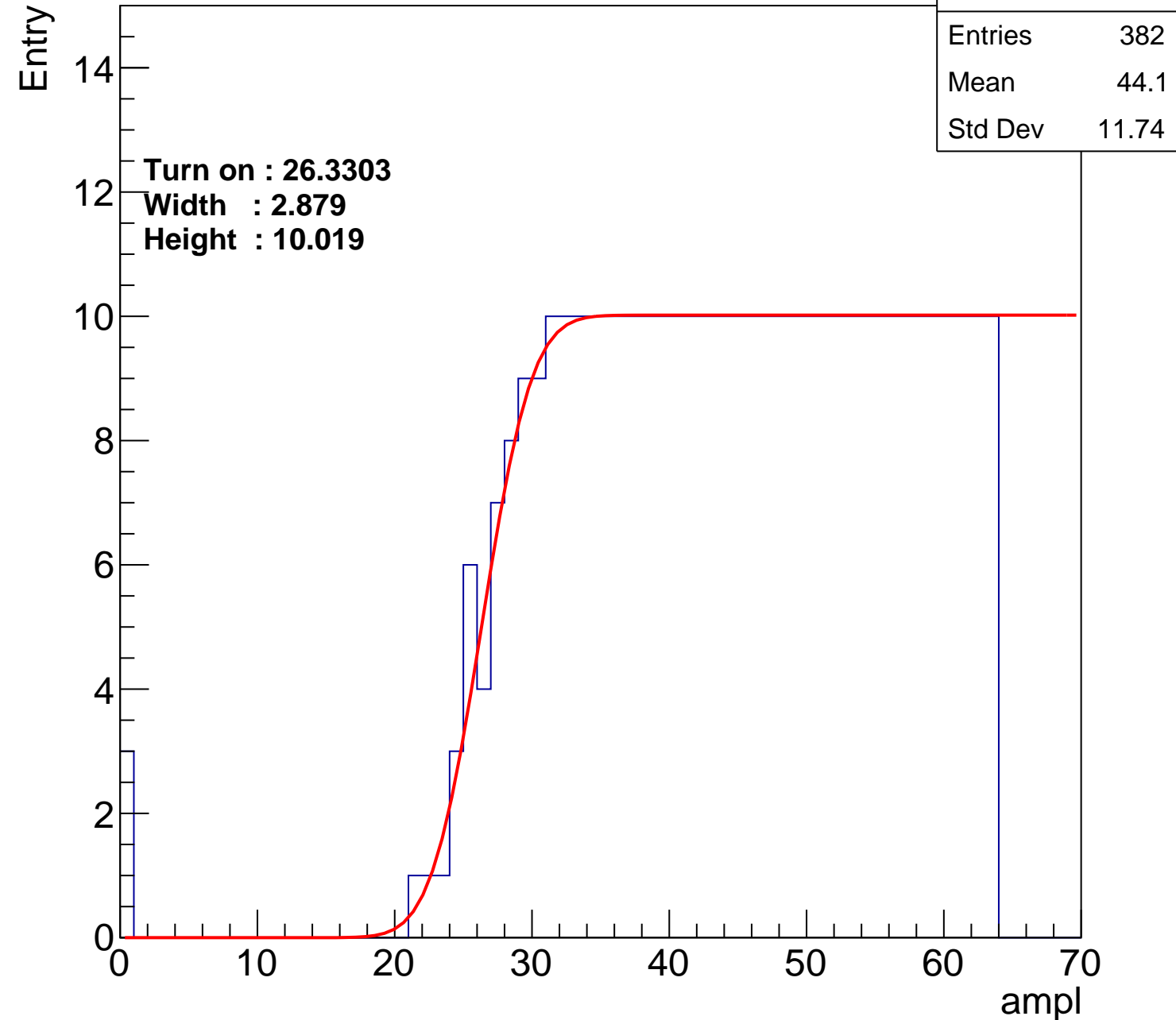
Width : 2.879

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.22
Std Dev	11.27

Turn on : 29.2068

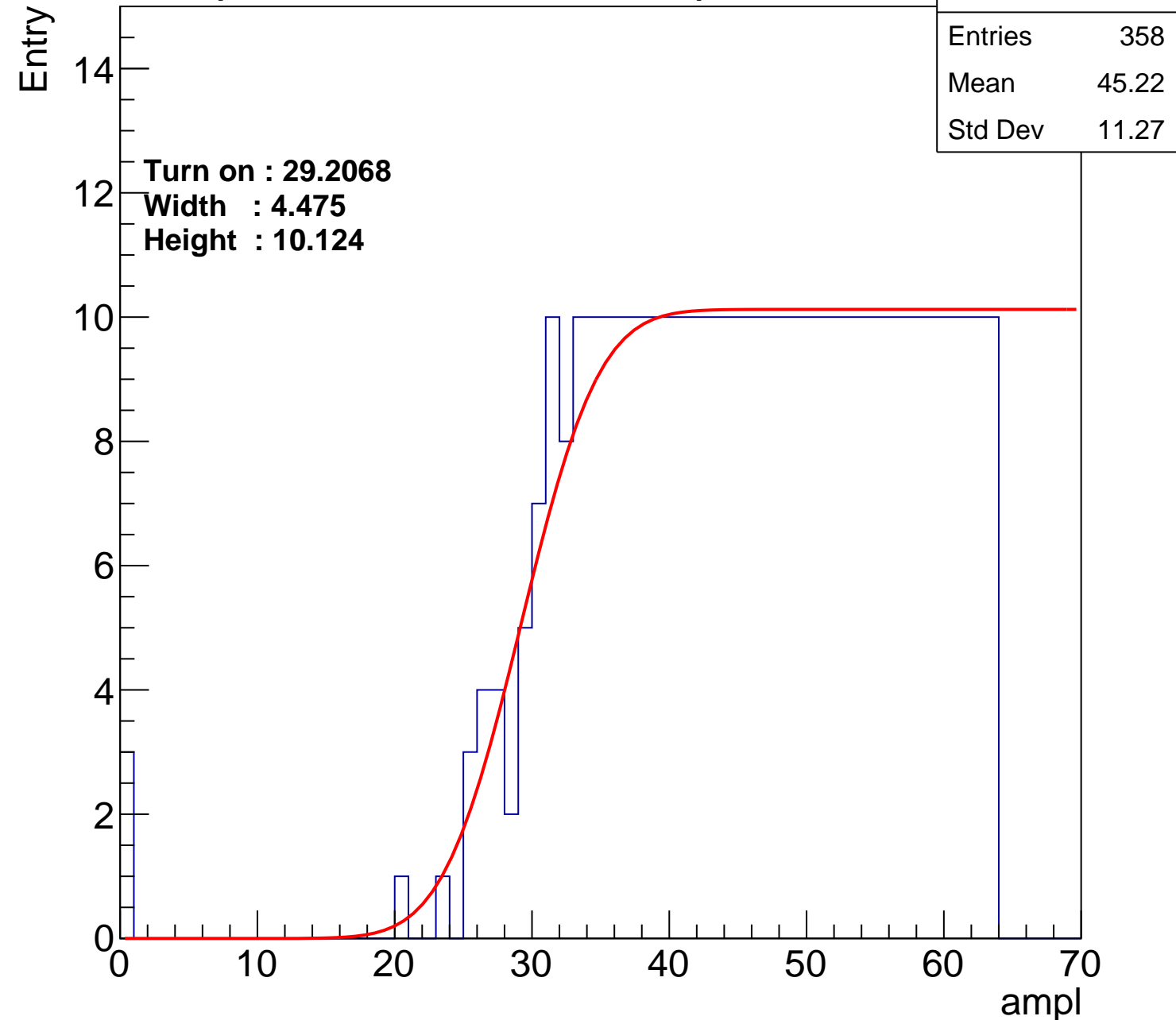
Width : 4.475

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	394
Mean	43.51
Std Dev	12.04

Turn on : 25.5864

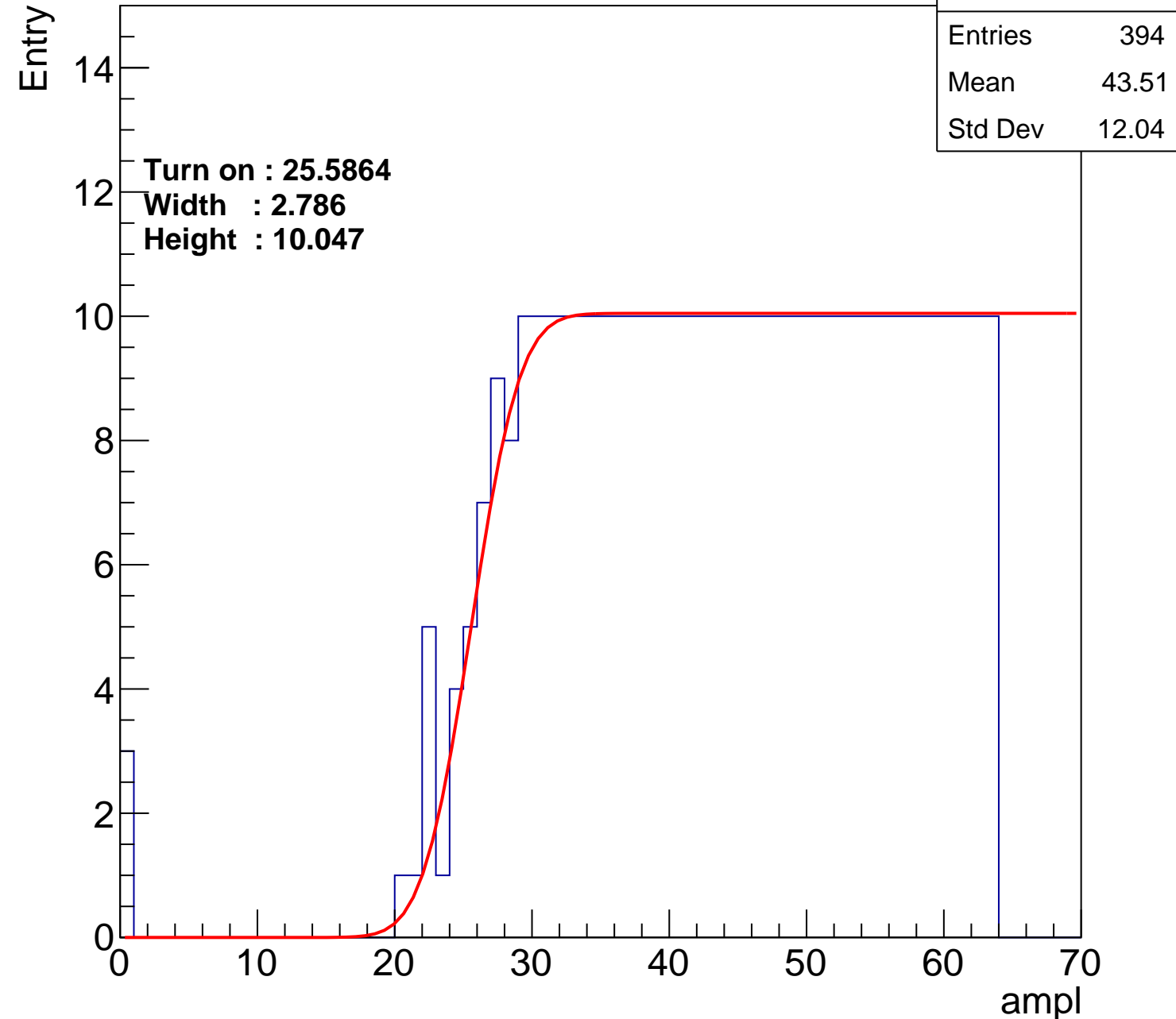
Width : 2.786

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch11

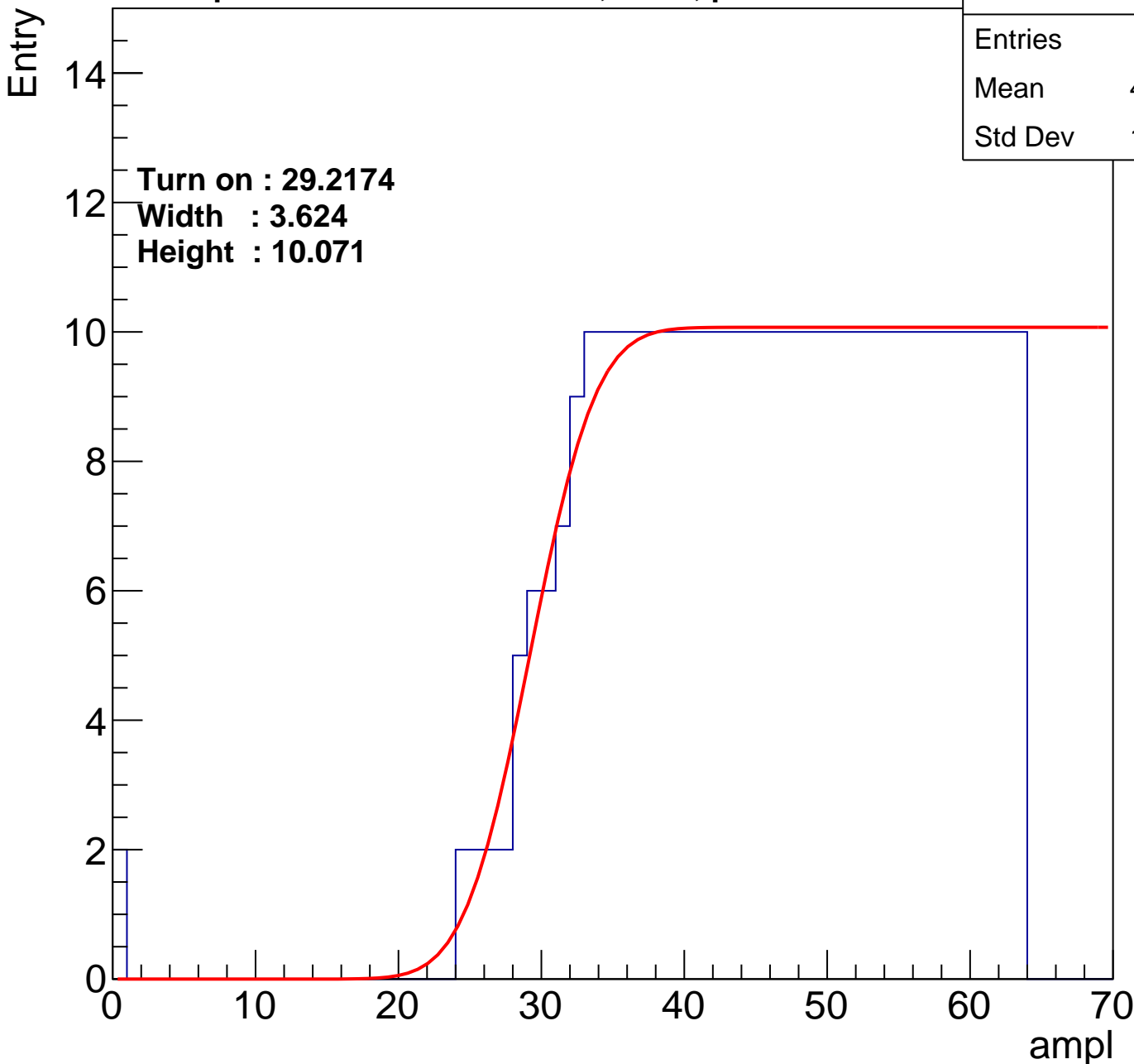
calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.56
Std Dev	10.88

Turn on : 29.2174

Width : 3.624

Height : 10.071



B0L002S, U14-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.8
Std Dev	11.38

Turn on : 27.6766

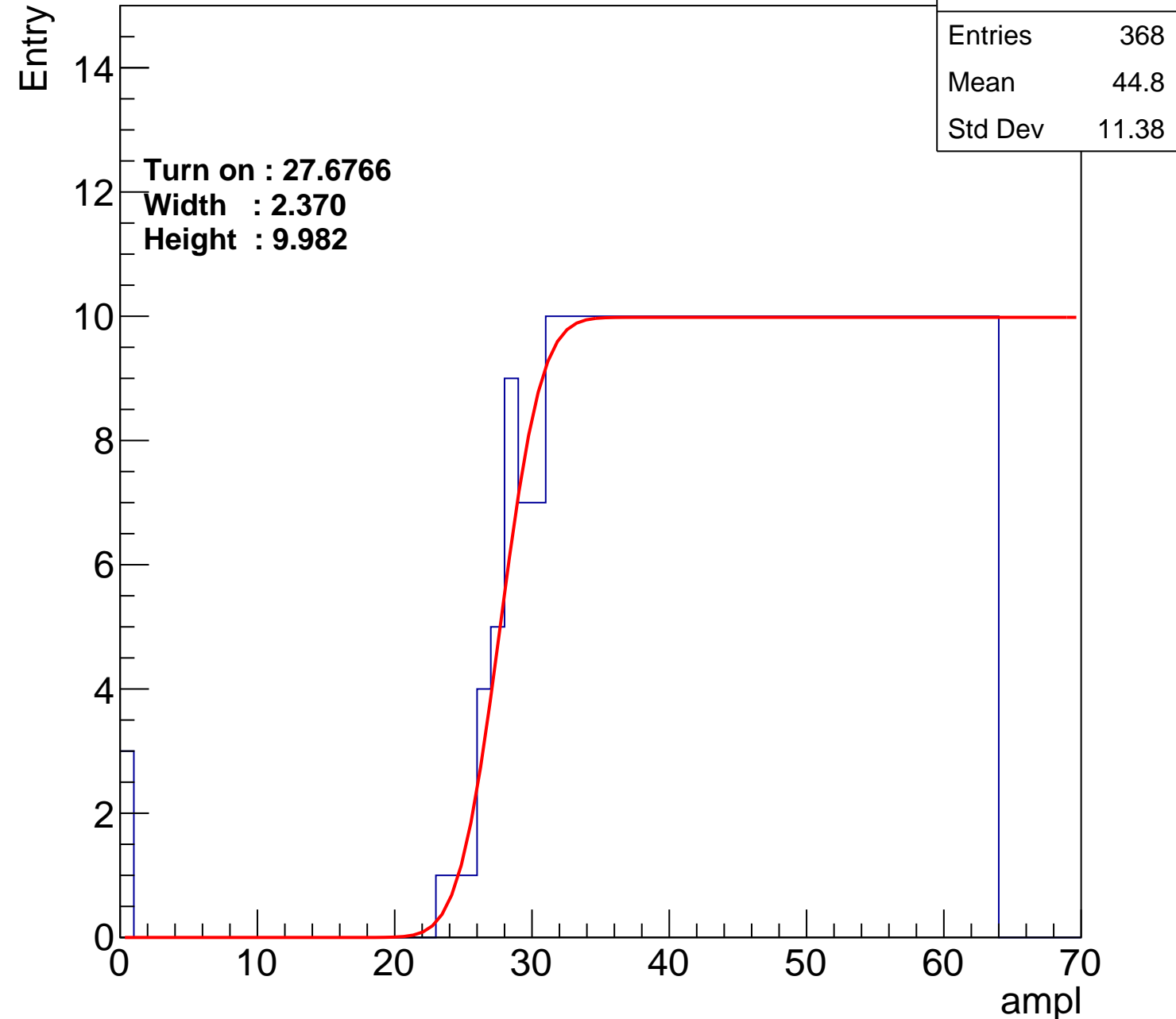
Width : 2.370

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.59
Std Dev	10.67

Turn on : 27.9664

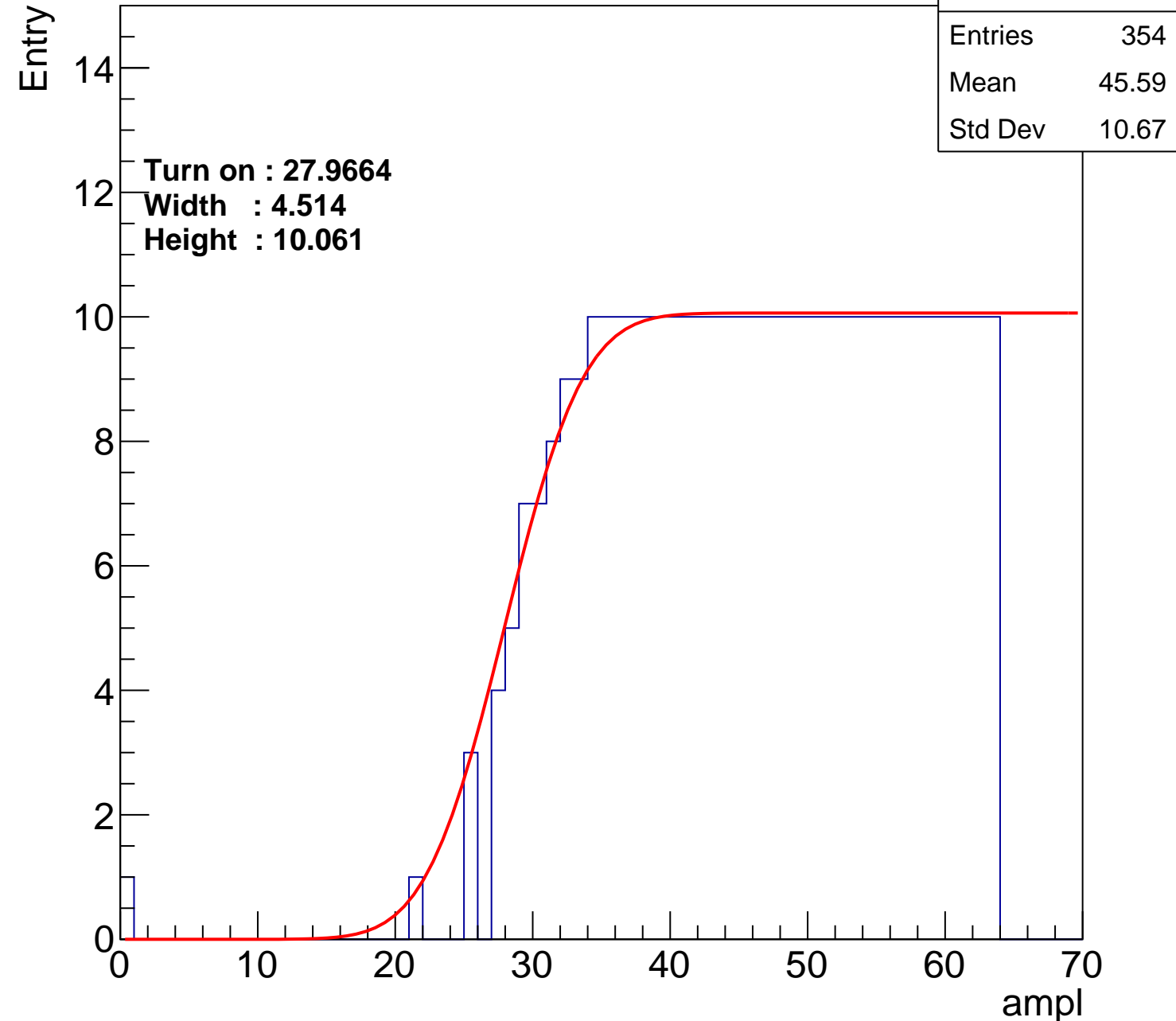
Width : 4.514

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch14

calib_packv5_042523_0143.root, FC#8, port C1

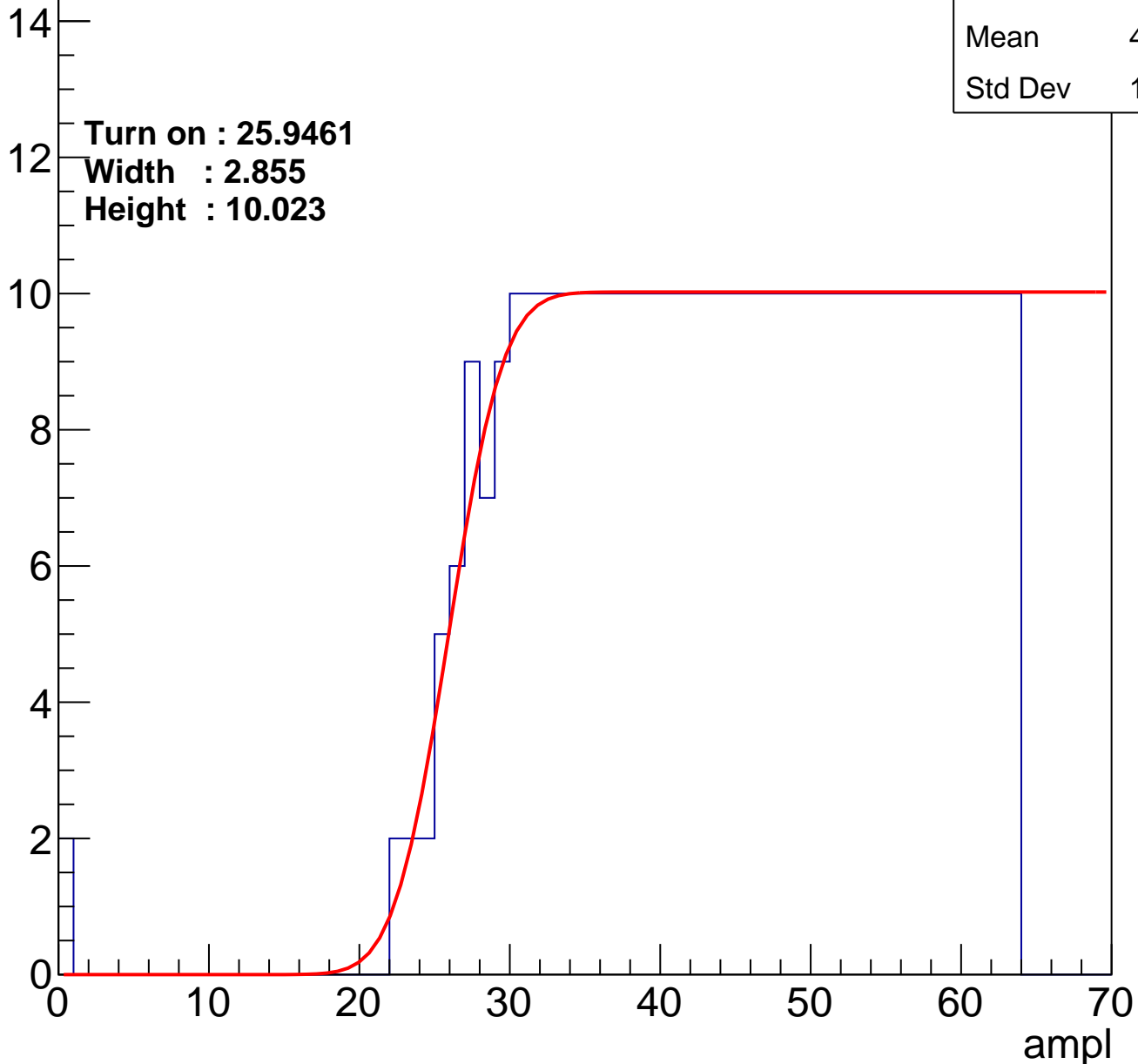
Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.9461

Width : 2.855

Height : 10.023

Entry



B0L002S, U14-ch15

calib_packv5_042523_0143.root, FC#8, port C1

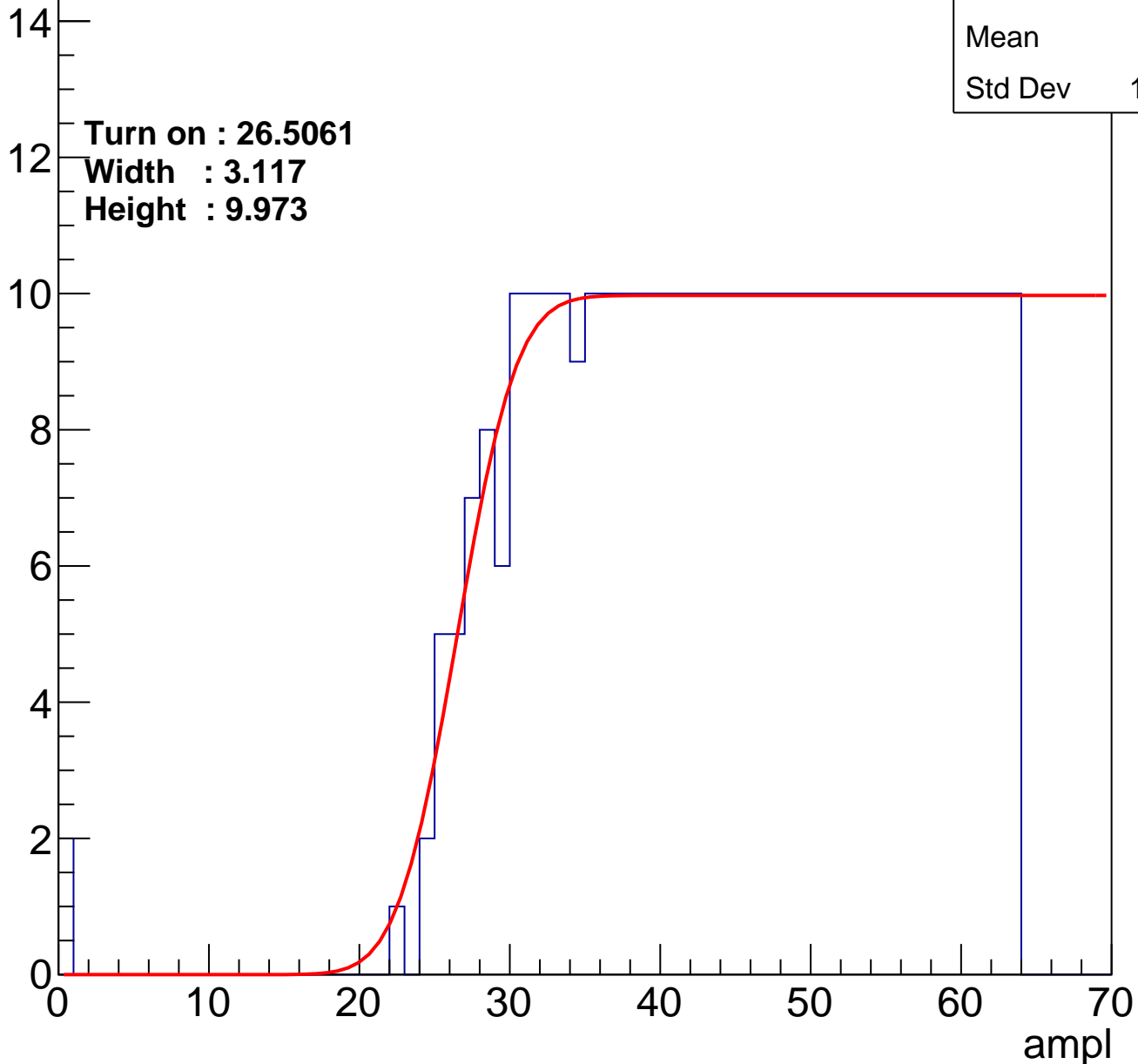
Entries	375
Mean	44.5
Std Dev	11.39

Turn on : 26.5061

Width : 3.117

Height : 9.973

Entry



B0L002S, U14-ch16

calib_packv5_042523_0143.root, FC#8, port C1

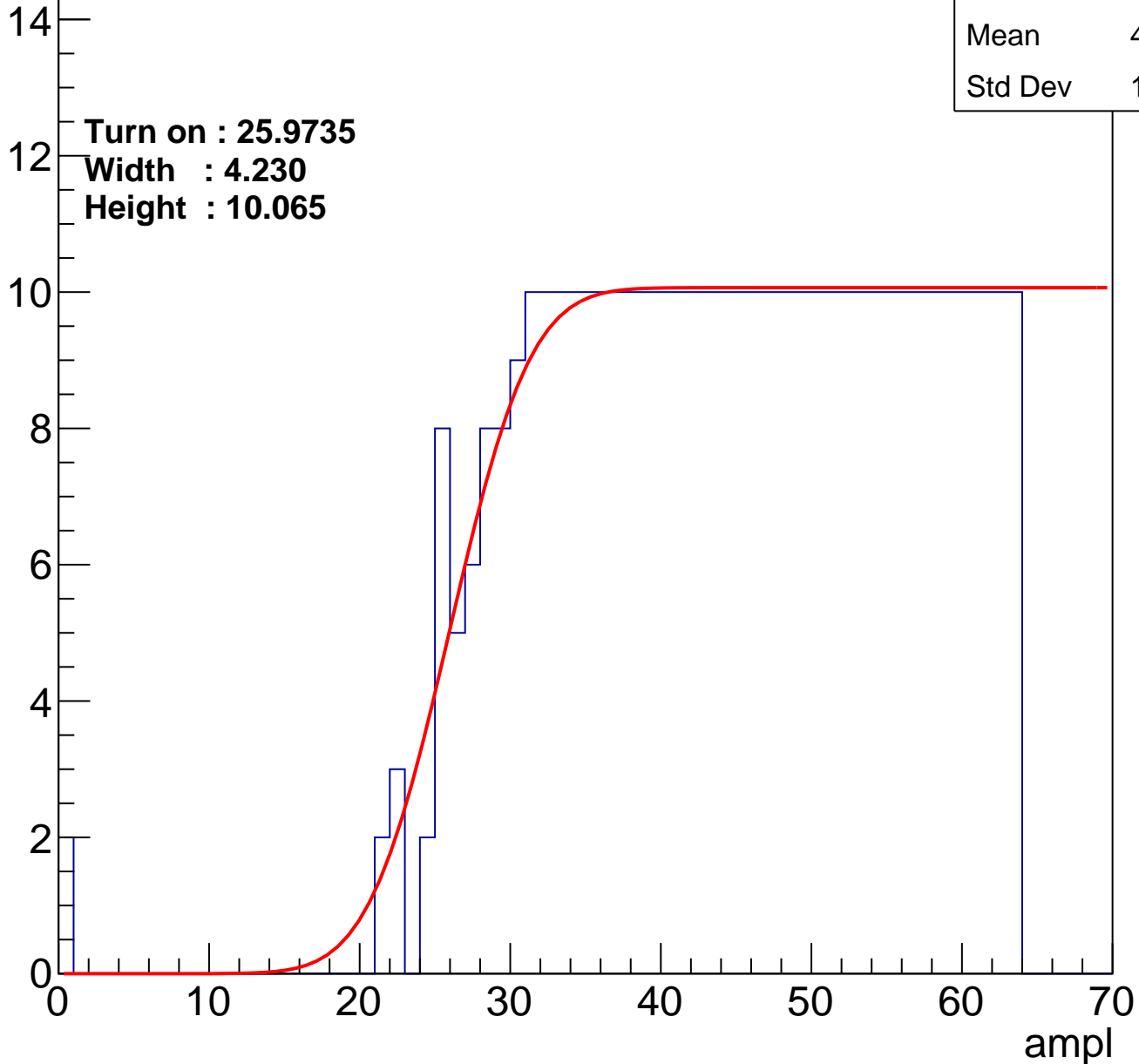
Entries	383
Mean	44.08
Std Dev	11.64

Turn on : 25.9735

Width : 4.230

Height : 10.065

Entry



B0L002S, U14-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.46
Std Dev	11.27

Turn on : 26.3333

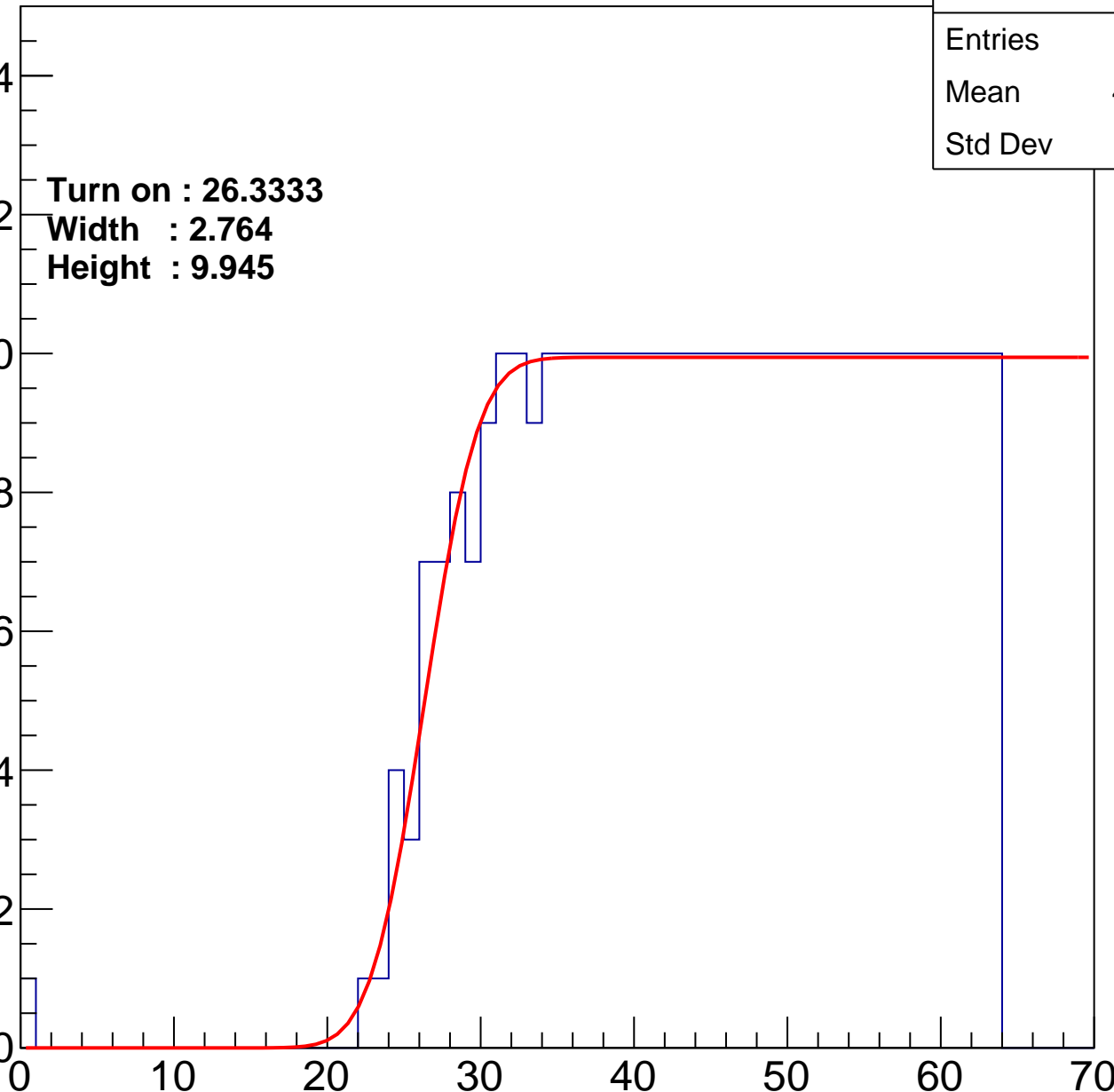
Width : 2.764

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch18

calib_packv5_042523_0143.root, FC#8, port C1

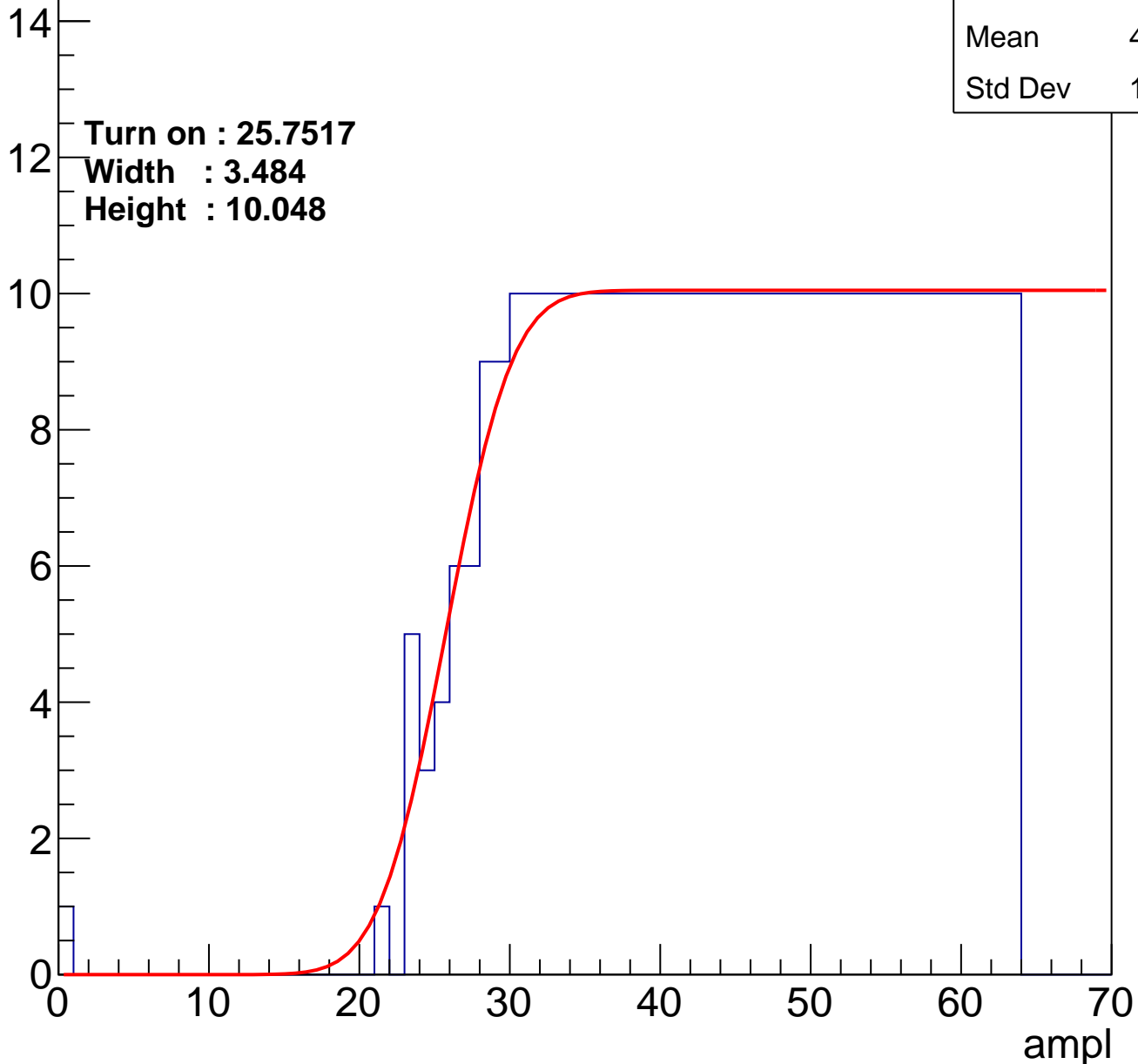
Entries	384
Mean	44.14
Std Dev	11.43

Turn on : 25.7517

Width : 3.484

Height : 10.048

Entry



B0L002S, U14-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.23
Std Dev	10.99

Turn on : 27.6127

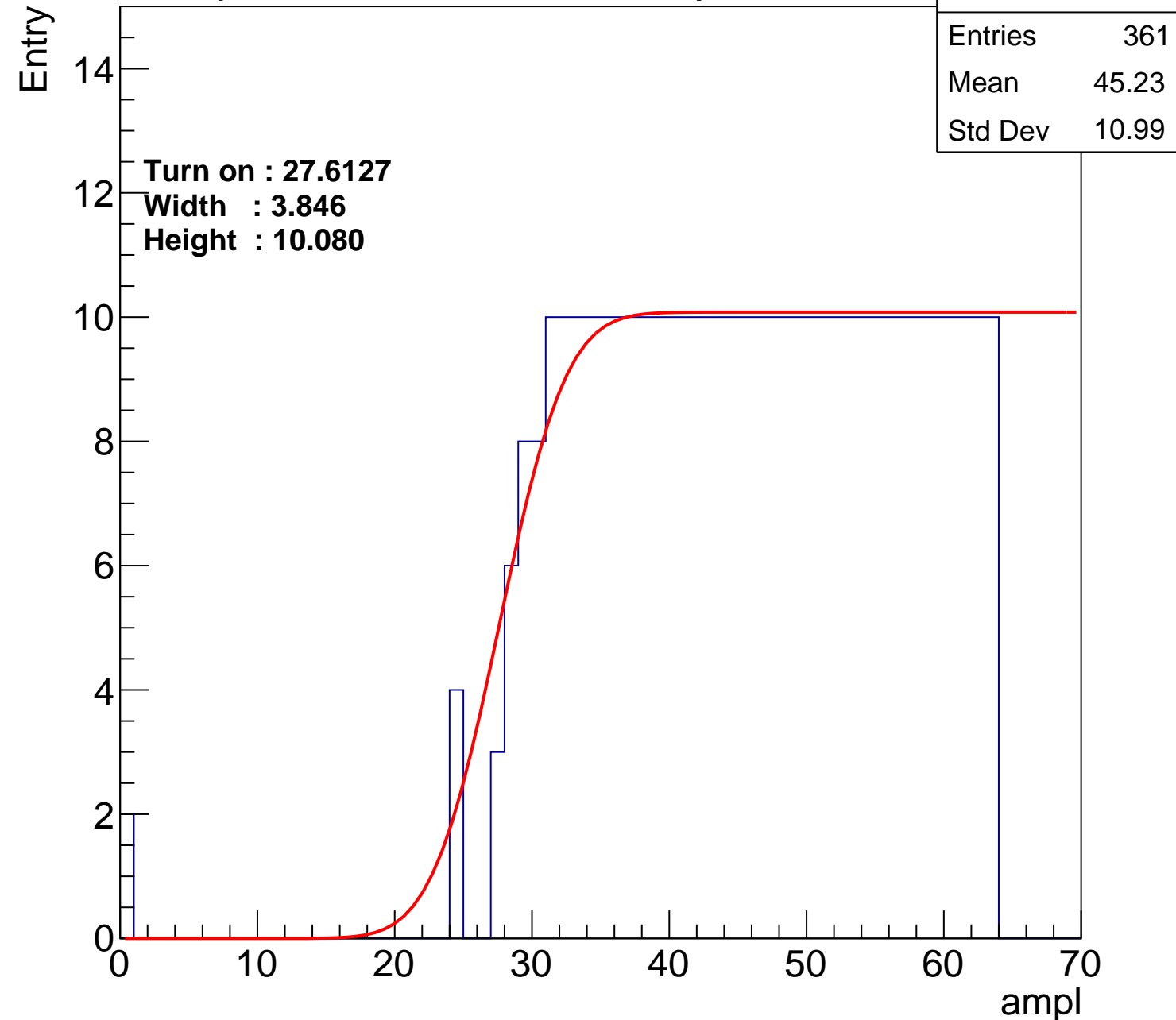
Width : 3.846

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.36
Std Dev	10.78

Turn on : 28.2558

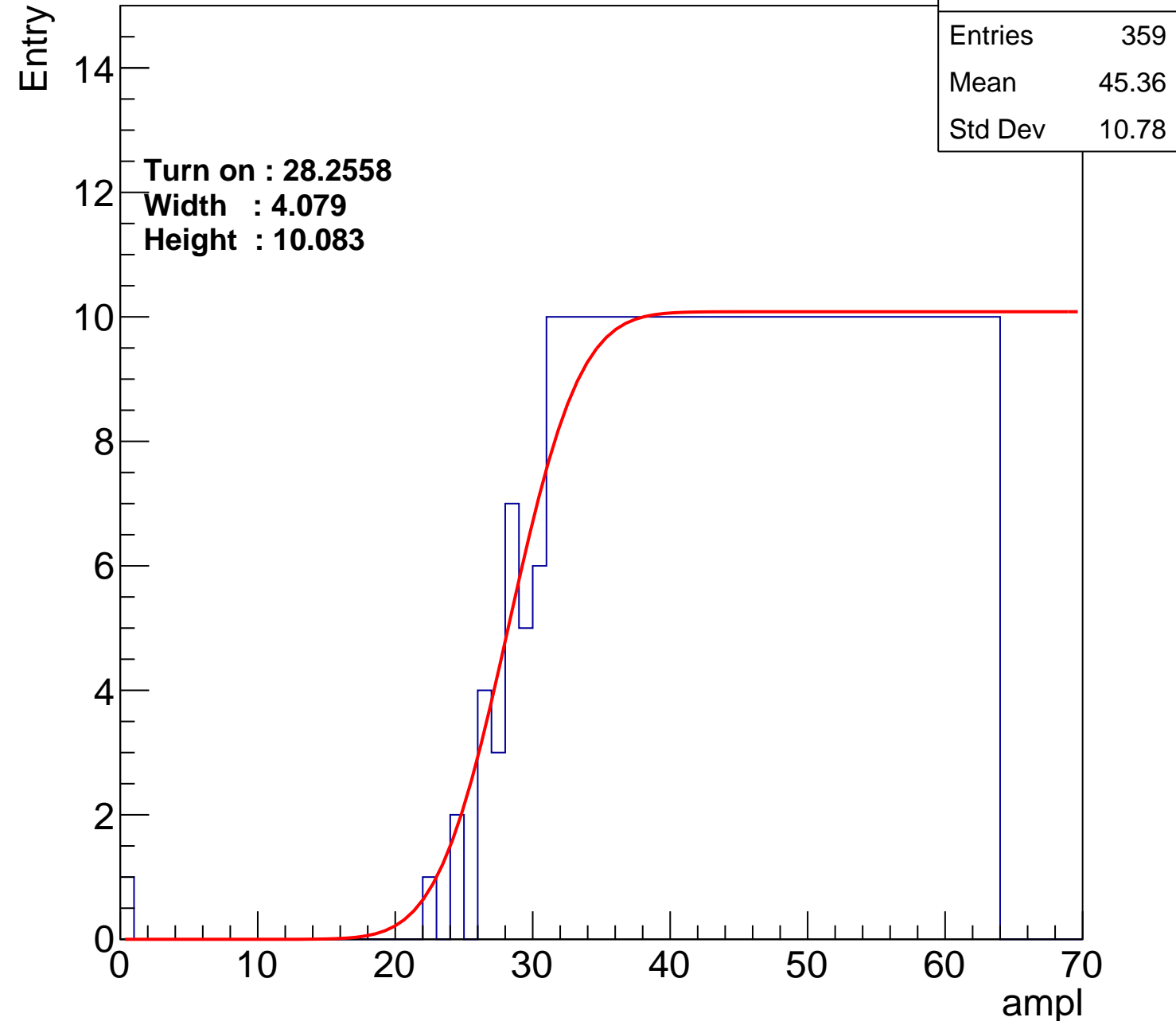
Width : 4.079

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch21

calib_packv5_042523_0143.root, FC#8, port C1

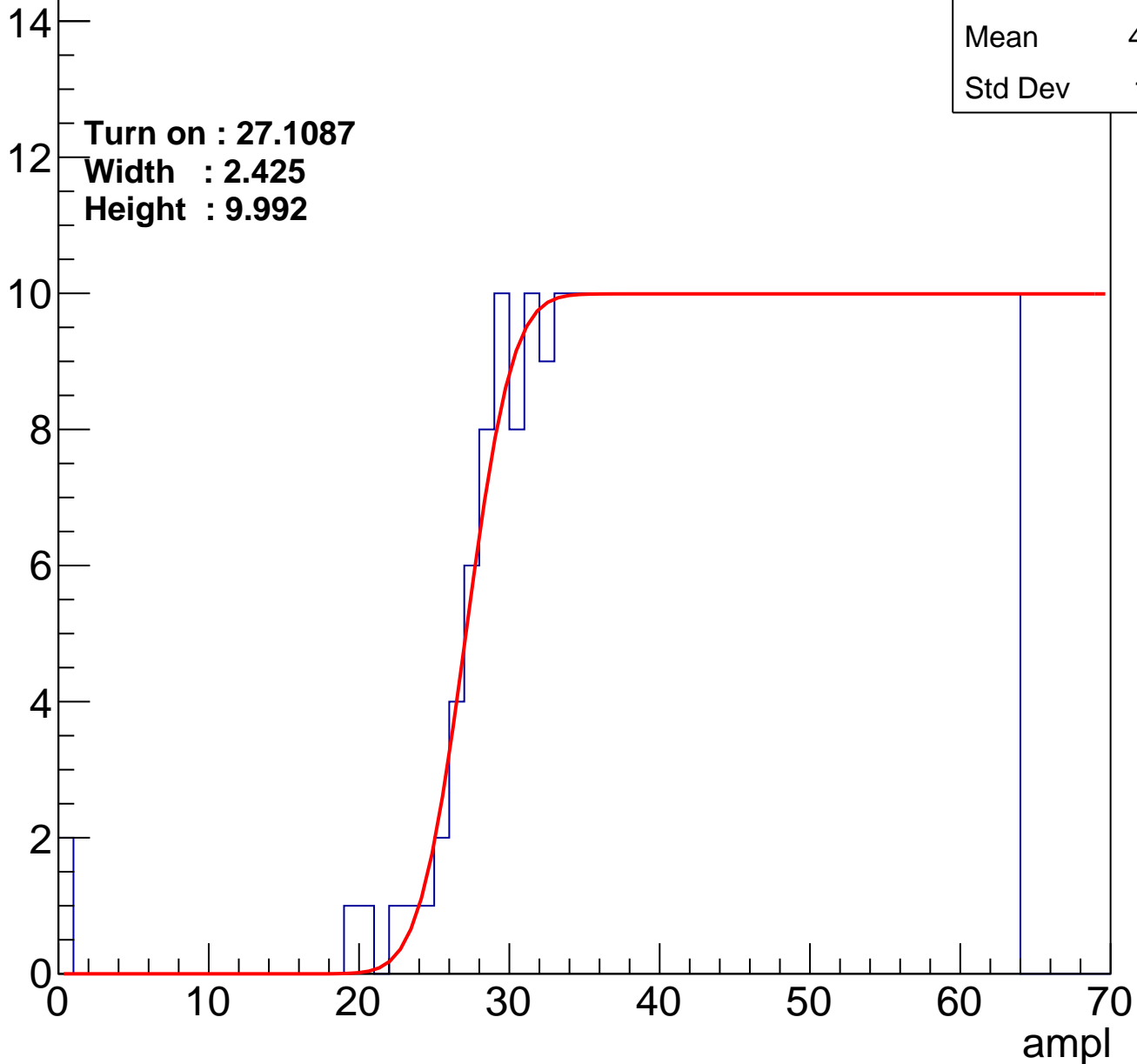
Entries	374
Mean	44.53
Std Dev	11.41

Turn on : 27.1087

Width : 2.425

Height : 9.992

Entry



B0L002S, U14-ch22

calib_packv5_042523_0143.root, FC#8, port C1

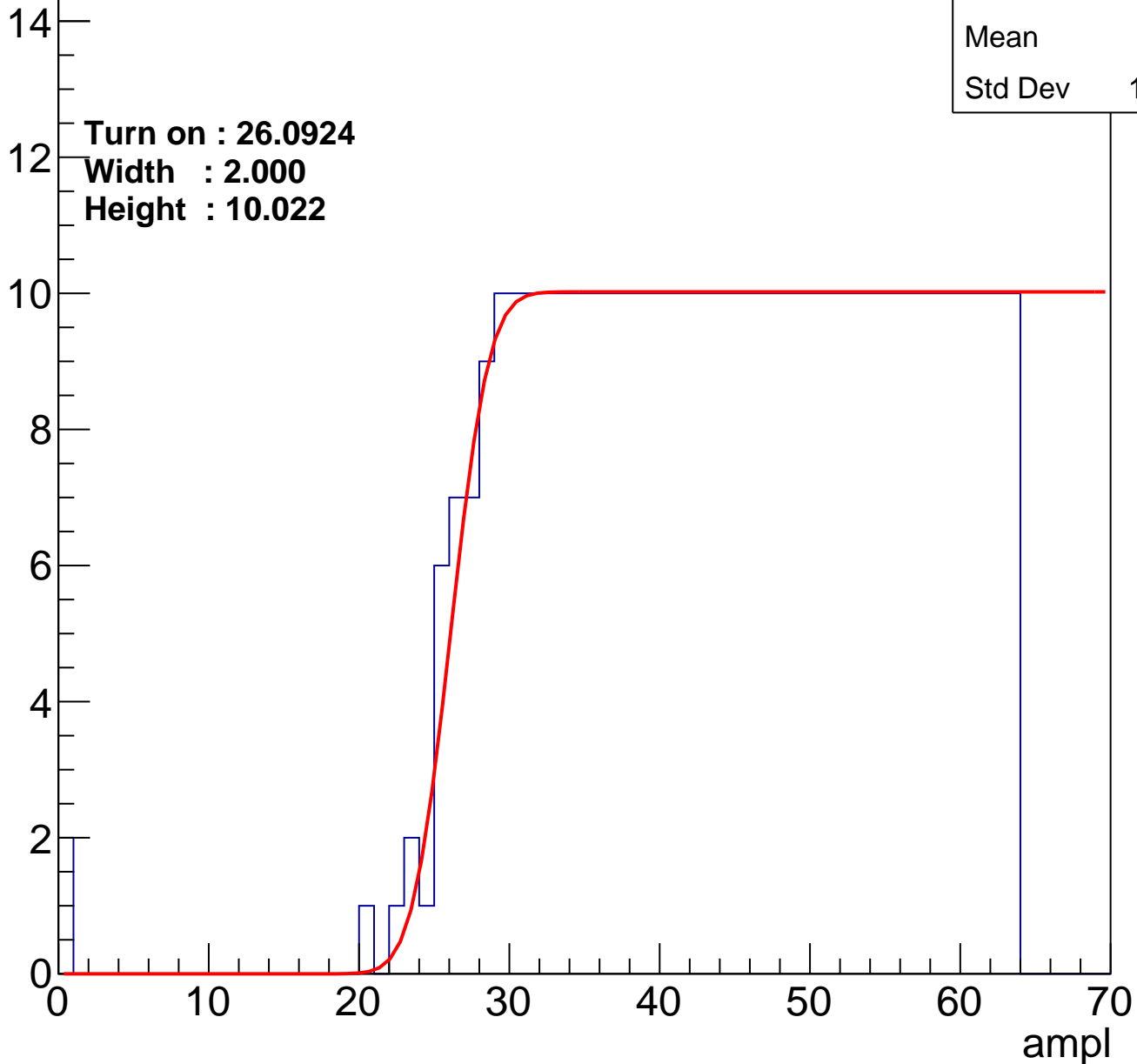
Entries	386
Mean	44
Std Dev	11.62

Turn on : 26.0924

Width : 2.000

Height : 10.022

Entry



B0L002S, U14-ch23

calib_packv5_042523_0143.root, FC#8, port C1

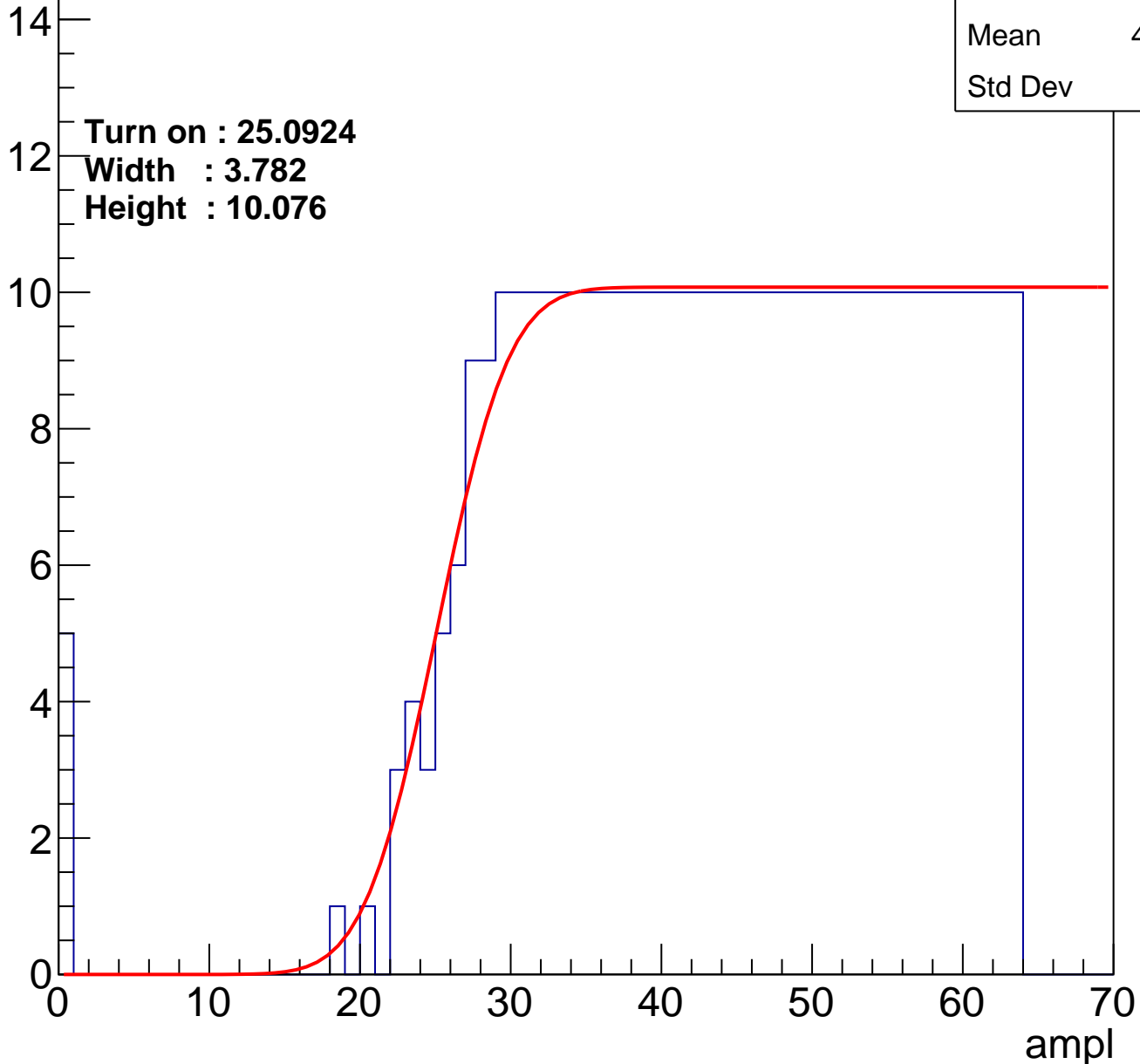
Entries	396
Mean	43.29
Std Dev	12.4

Turn on : 25.0924

Width : 3.782

Height : 10.076

Entry



B0L002S, U14-ch24

calib_packv5_042523_0143.root, FC#8, port C1

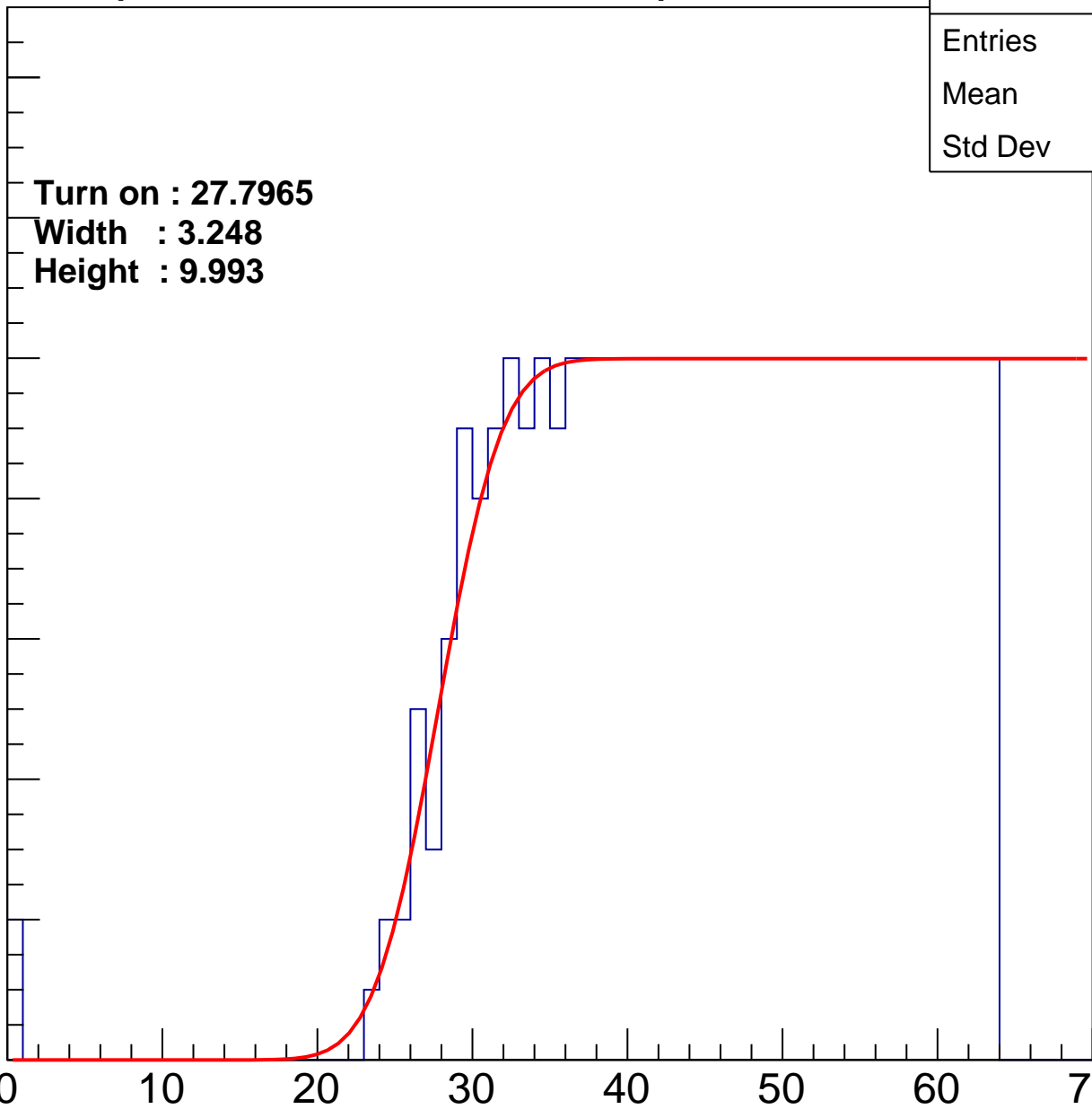
Entry

14
12
10
8
6
4
2
0

Turn on : 27.7965
Width : 3.248
Height : 9.993

Entries	365
Mean	44.96
Std Dev	11.18

ampl



B0L002S, U14-ch25

calib_packv5_042523_0143.root, FC#8, port C1

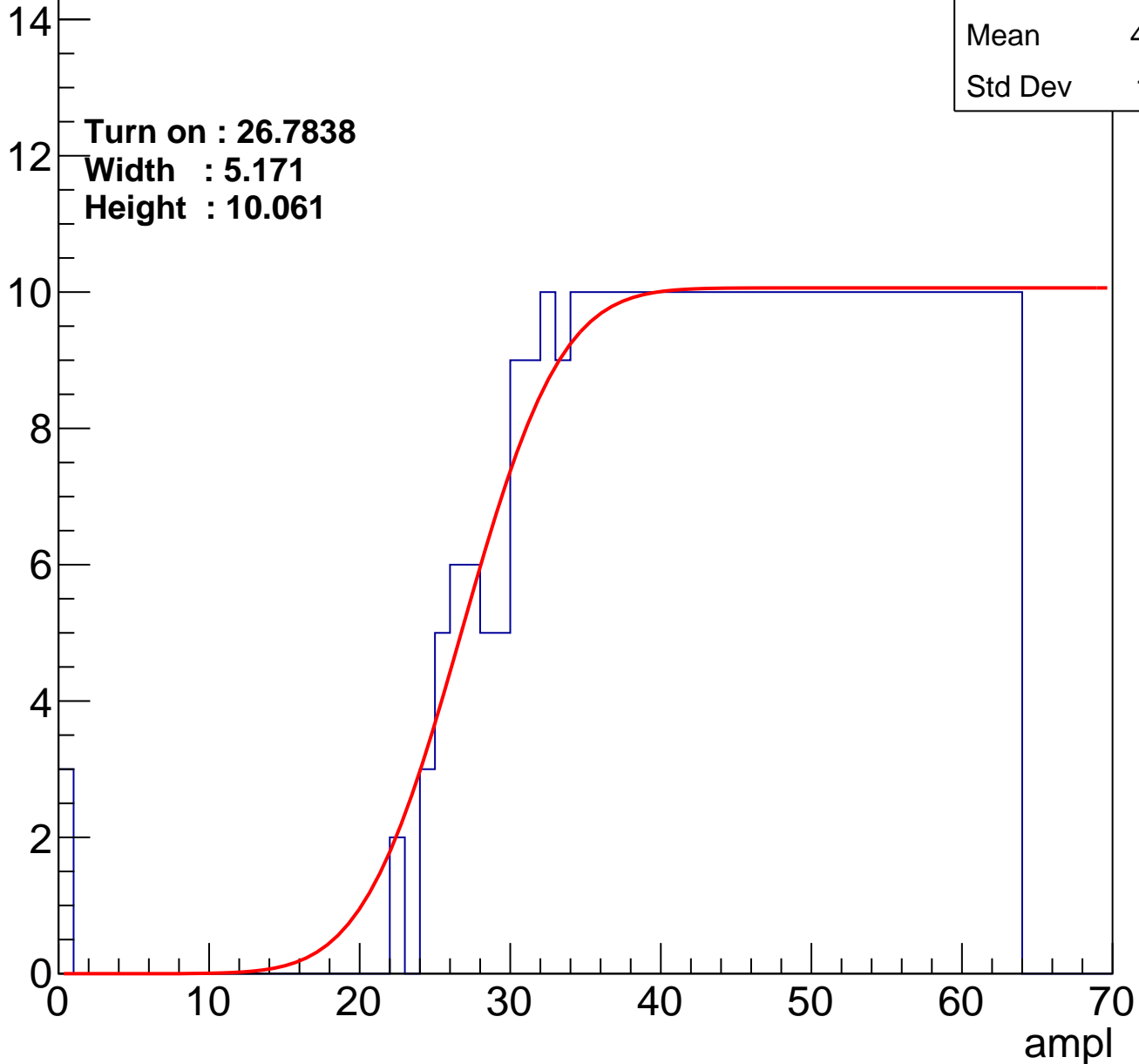
Entries	372
Mean	44.52
Std Dev	11.61

Turn on : 26.7838

Width : 5.171

Height : 10.061

Entry



B0L002S, U14-ch26

calib_packv5_042523_0143.root, FC#8, port C1

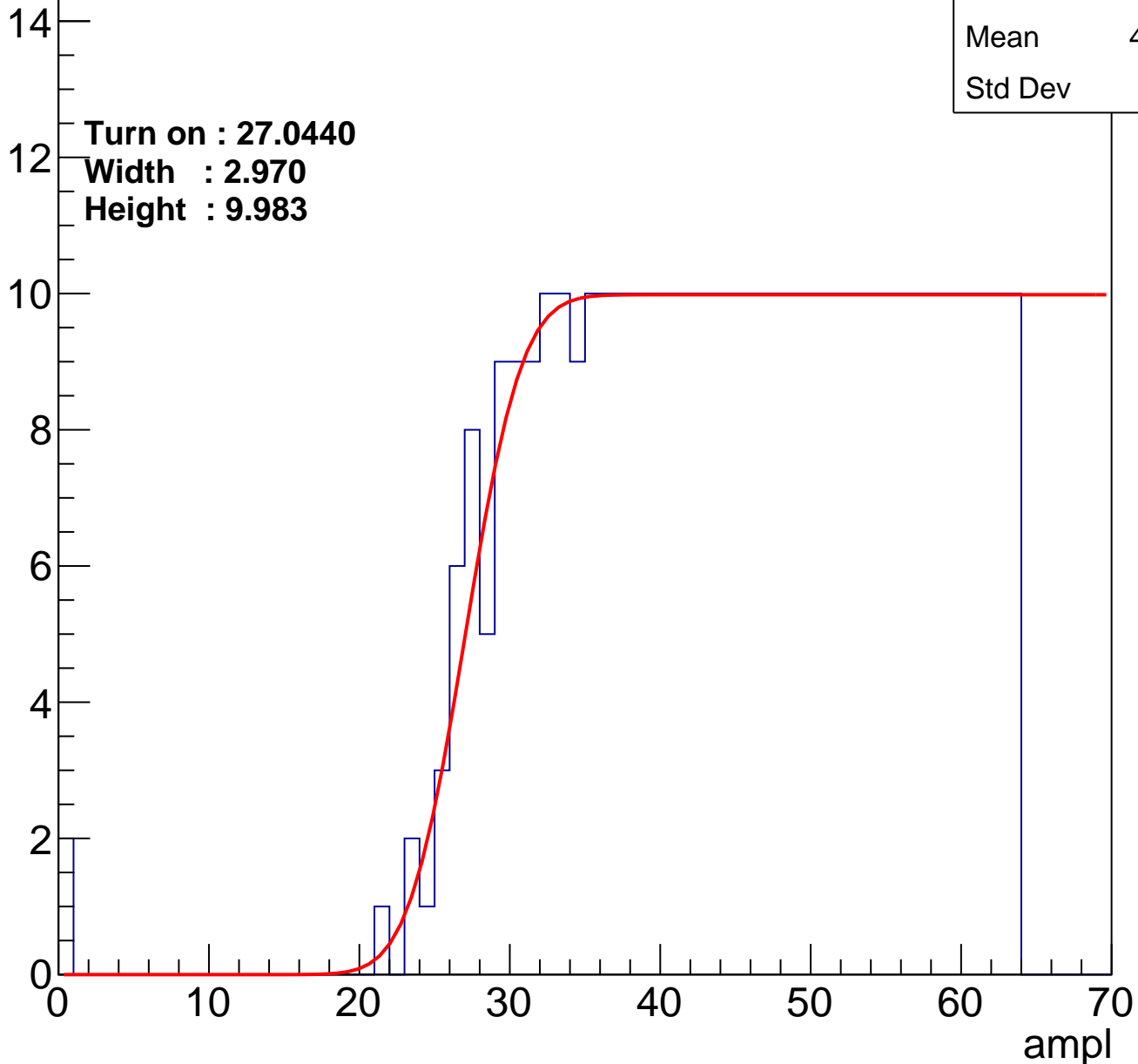
Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 27.0440

Width : 2.970

Height : 9.983

Entry



B0L002S, U14-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	397
Mean	43.32
Std Dev	12.25

Turn on : 25.1426

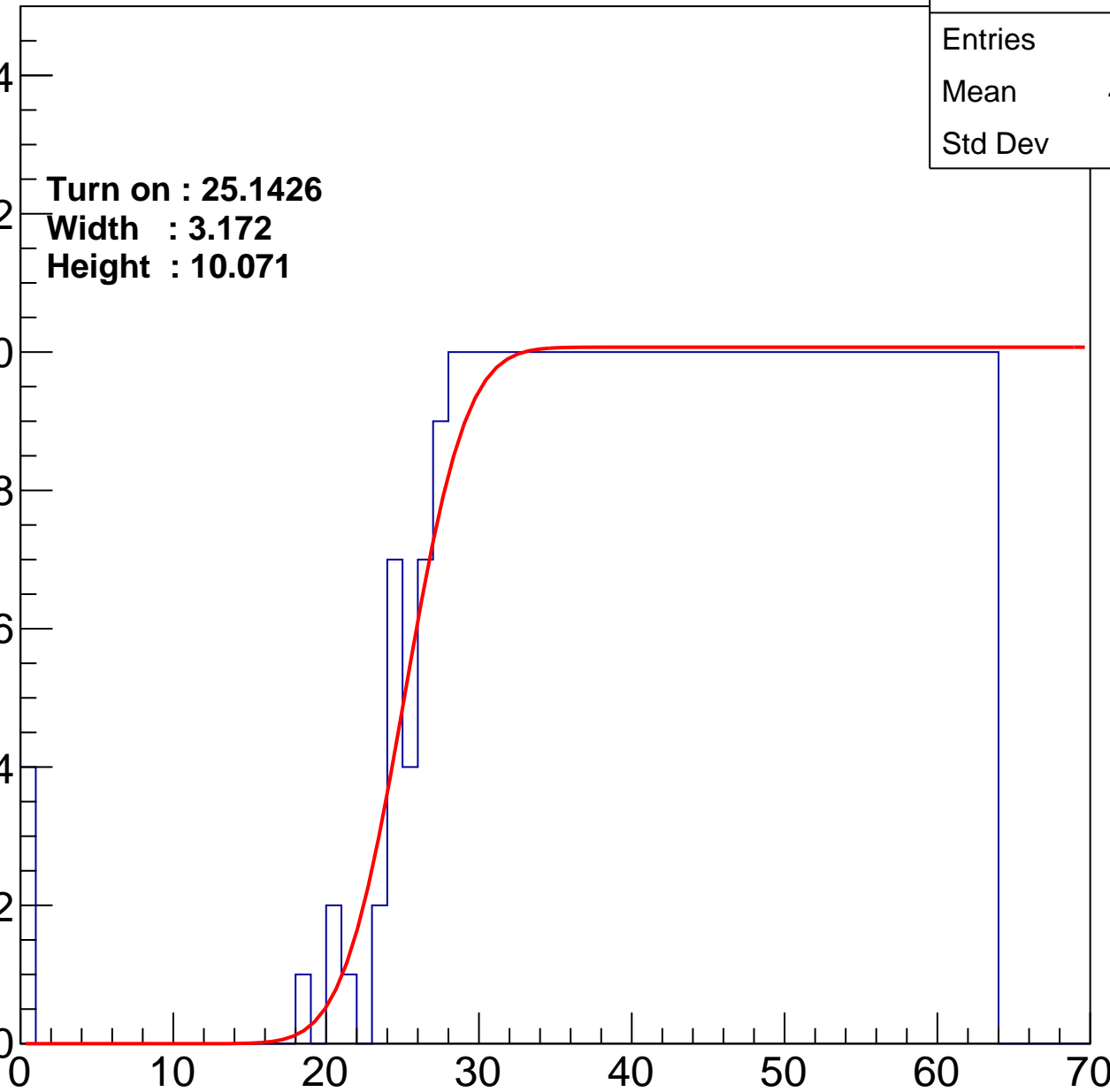
Width : 3.172

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.77
Std Dev	11.24

Turn on : 27.4931

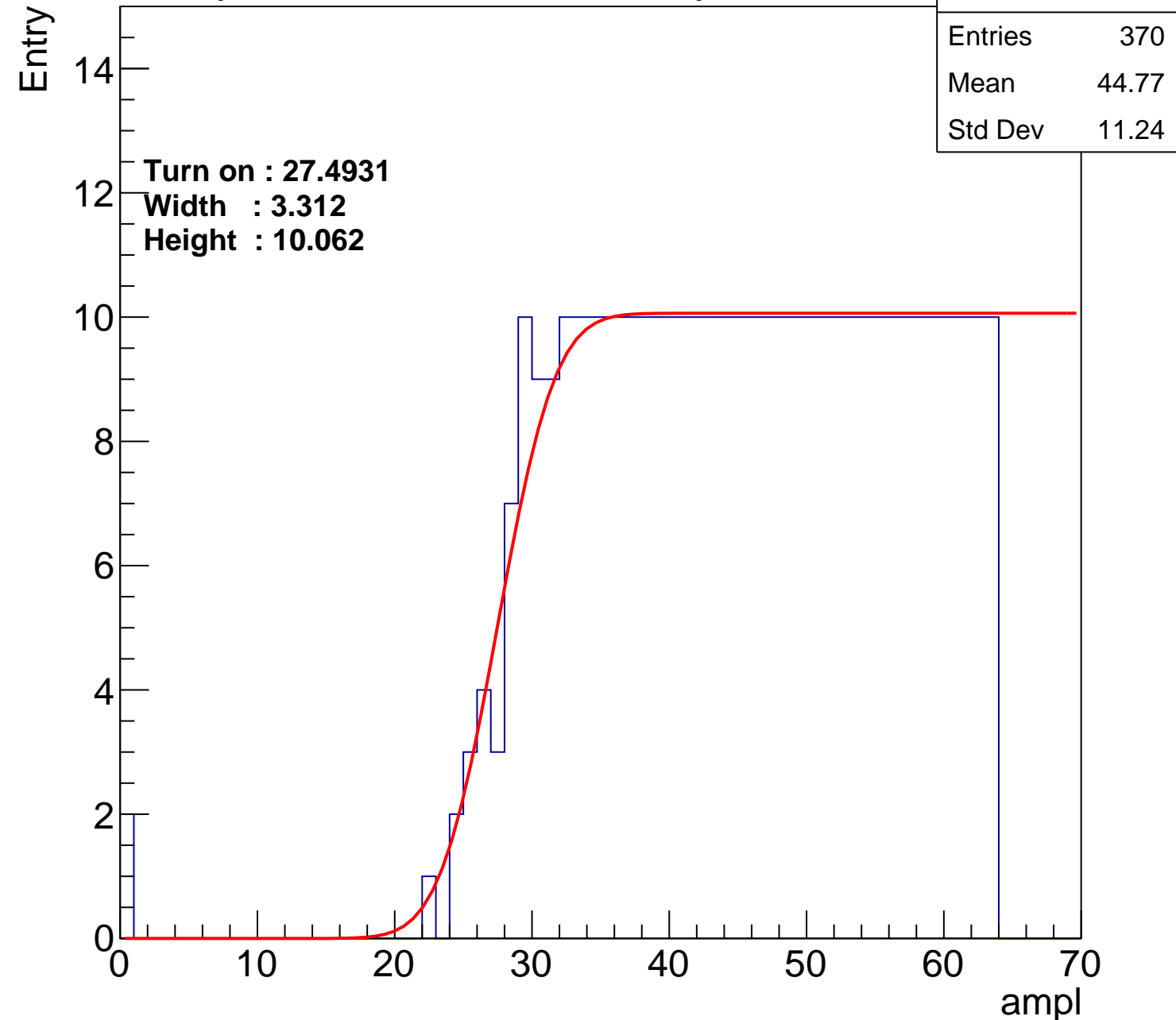
Width : 3.312

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch29

calib_packv5_042523_0143.root, FC#8, port C1

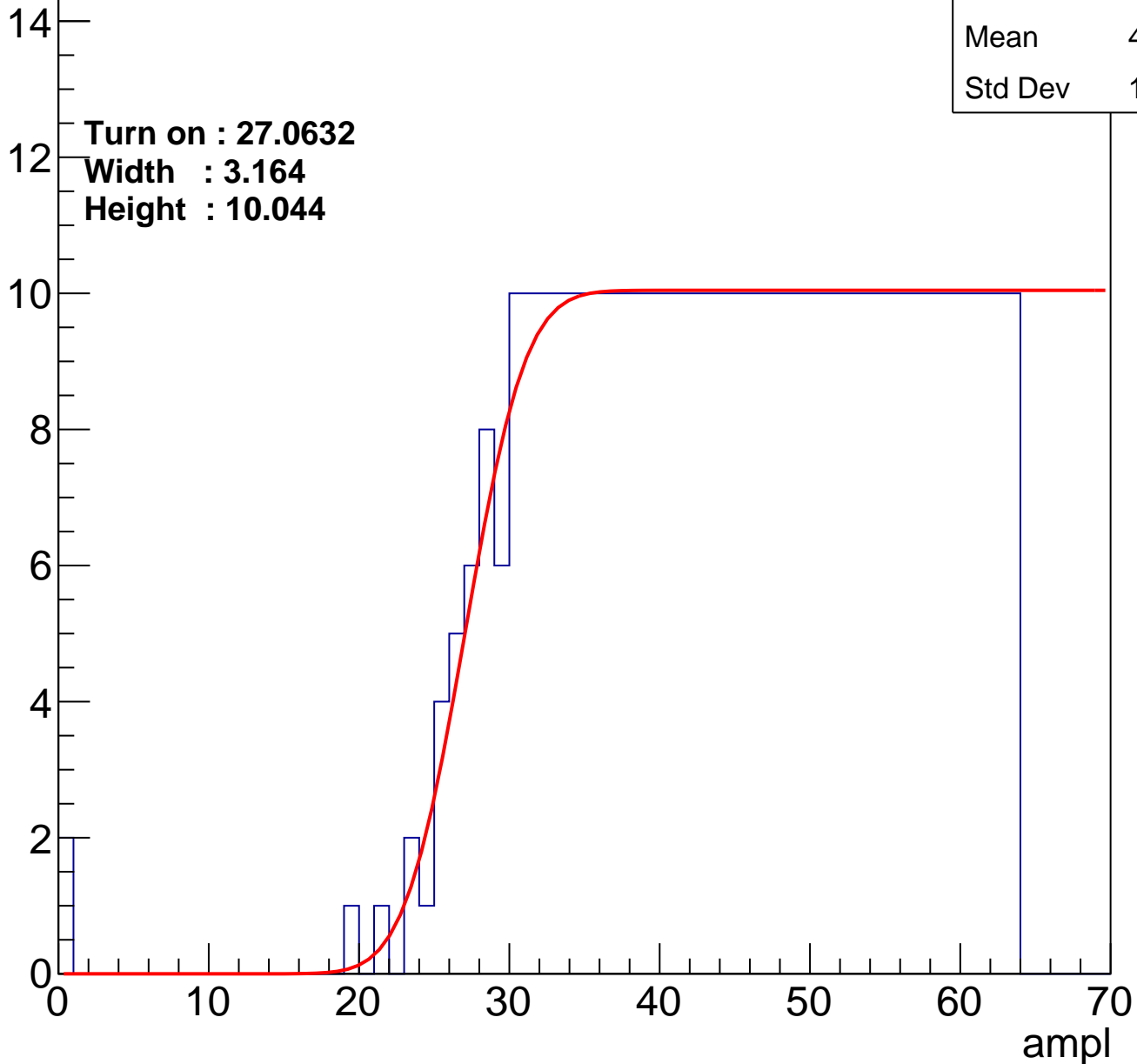
Entries	376
Mean	44.44
Std Dev	11.45

Turn on : 27.0632

Width : 3.164

Height : 10.044

Entry



B0L002S, U14-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.35
Std Dev	11.35

Turn on : 29.1731

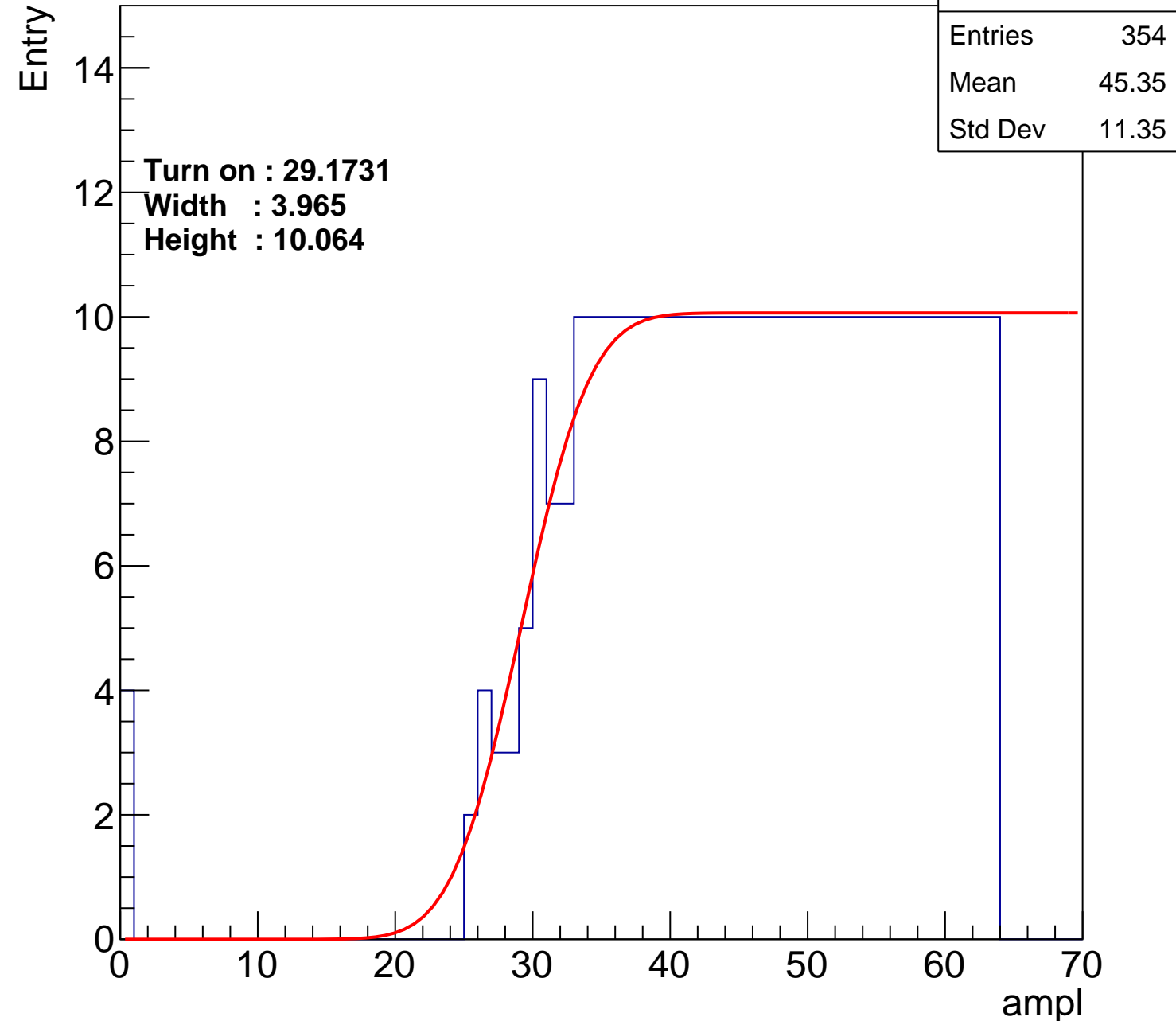
Width : 3.965

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.08
Std Dev	11.06

Turn on : 27.7350

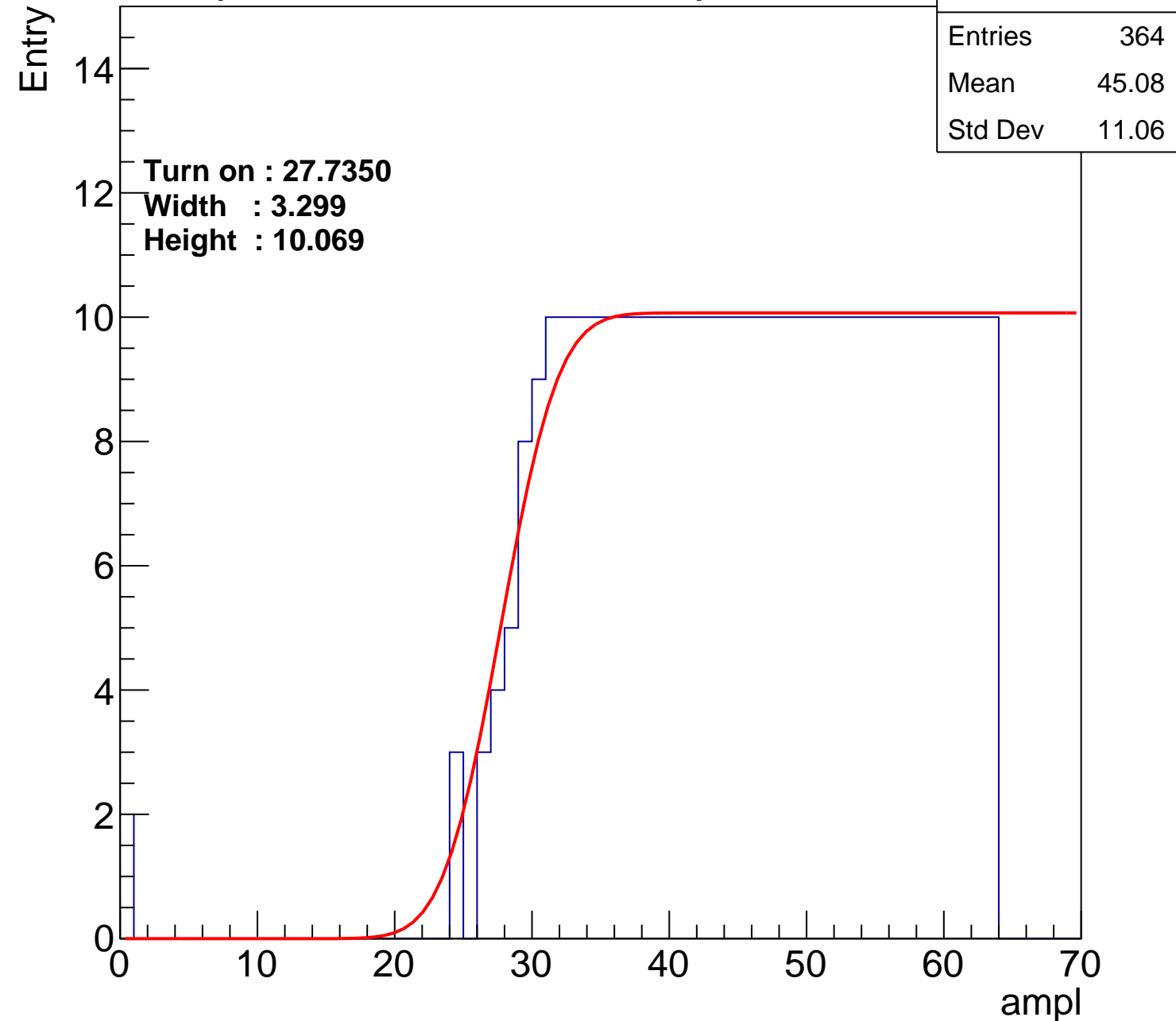
Width : 3.299

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	395
Mean	43.54
Std Dev	11.87

Turn on : 24.8318

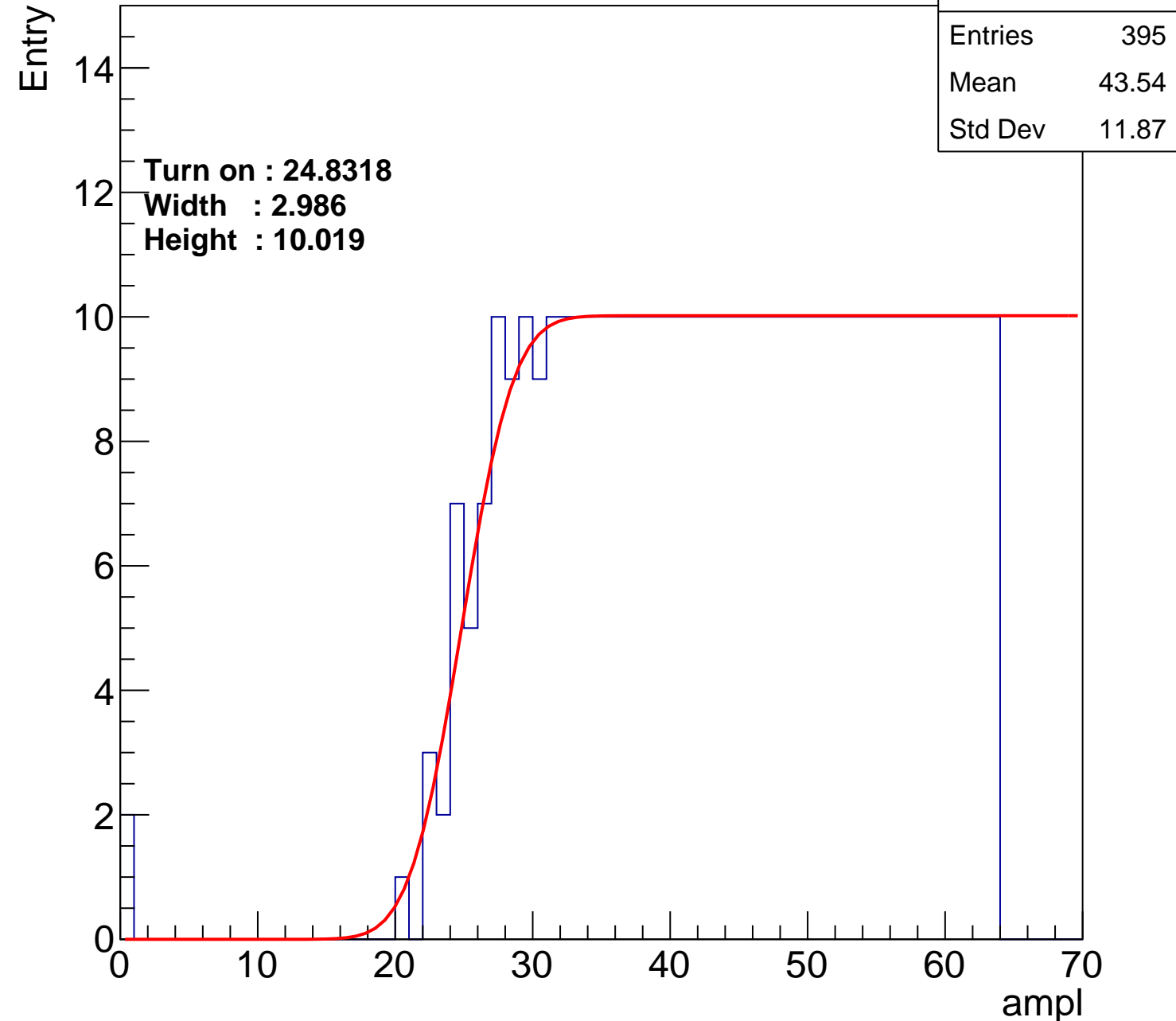
Width : 2.986

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch33

calib_packv5_042523_0143.root, FC#8, port C1

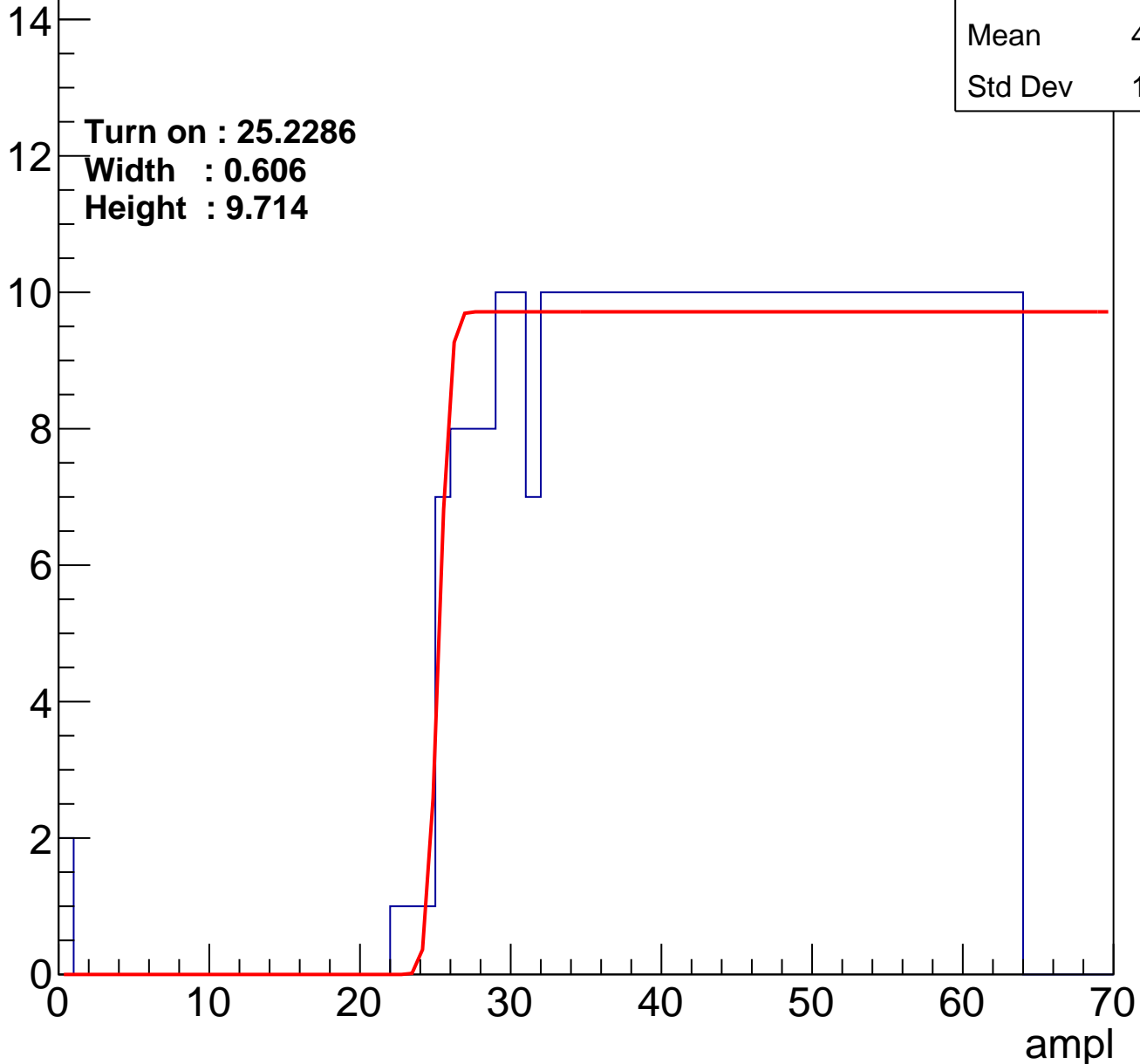
Entries	383
Mean	44.12
Std Dev	11.57

Turn on : 25.2286

Width : 0.606

Height : 9.714

Entry



B0L002S, U14-ch34

calib_packv5_042523_0143.root, FC#8, port C1

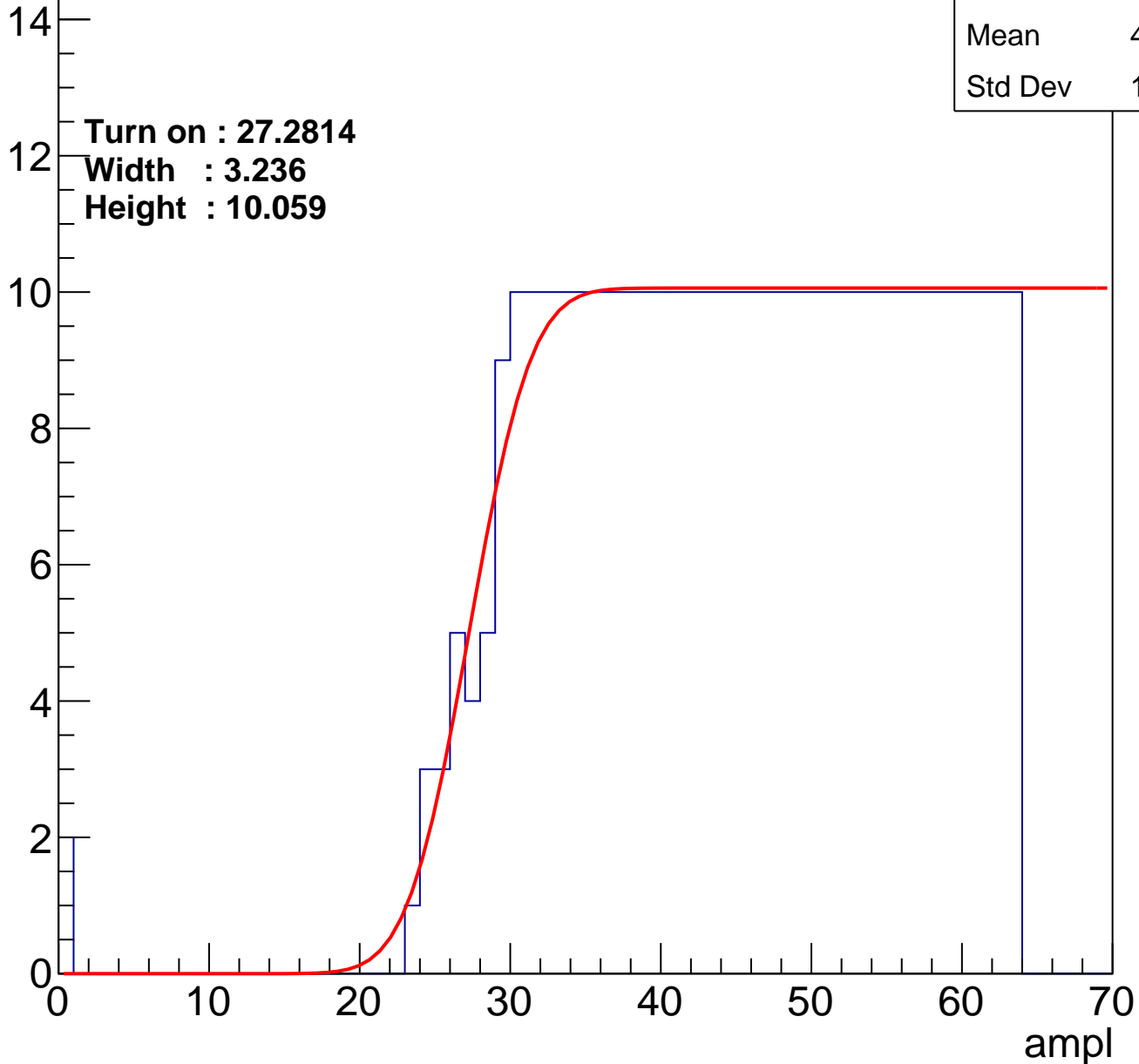
Entries	372
Mean	44.67
Std Dev	11.28

Turn on : 27.2814

Width : 3.236

Height : 10.059

Entry



B0L002S, U14-ch35

calib_packv5_042523_0143.root, FC#8, port C1

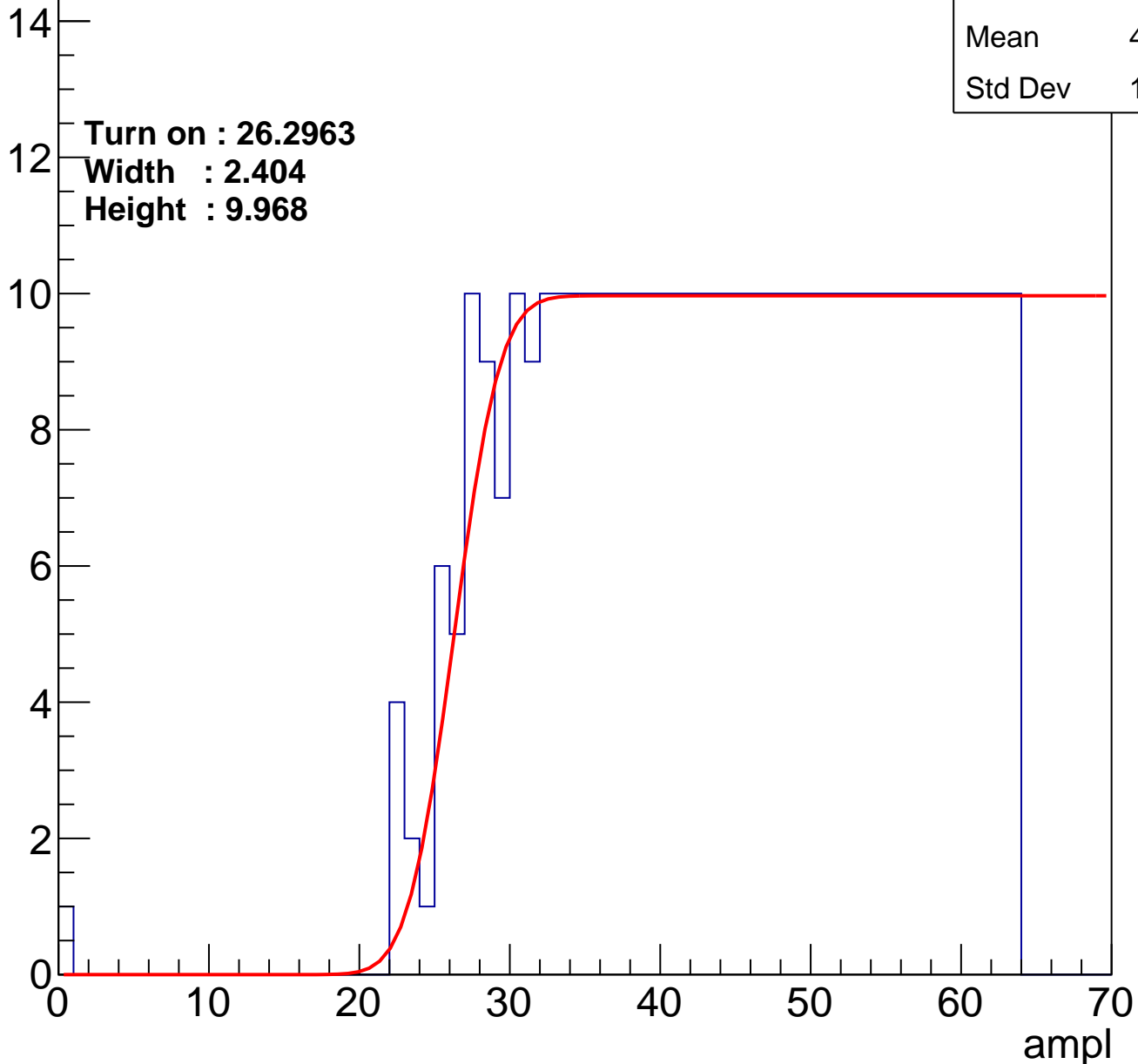
Entries	384
Mean	44.12
Std Dev	11.45

Turn on : 26.2963

Width : 2.404

Height : 9.968

Entry



B0L002S, U14-ch36

calib_packv5_042523_0143.root, FC#8, port C1

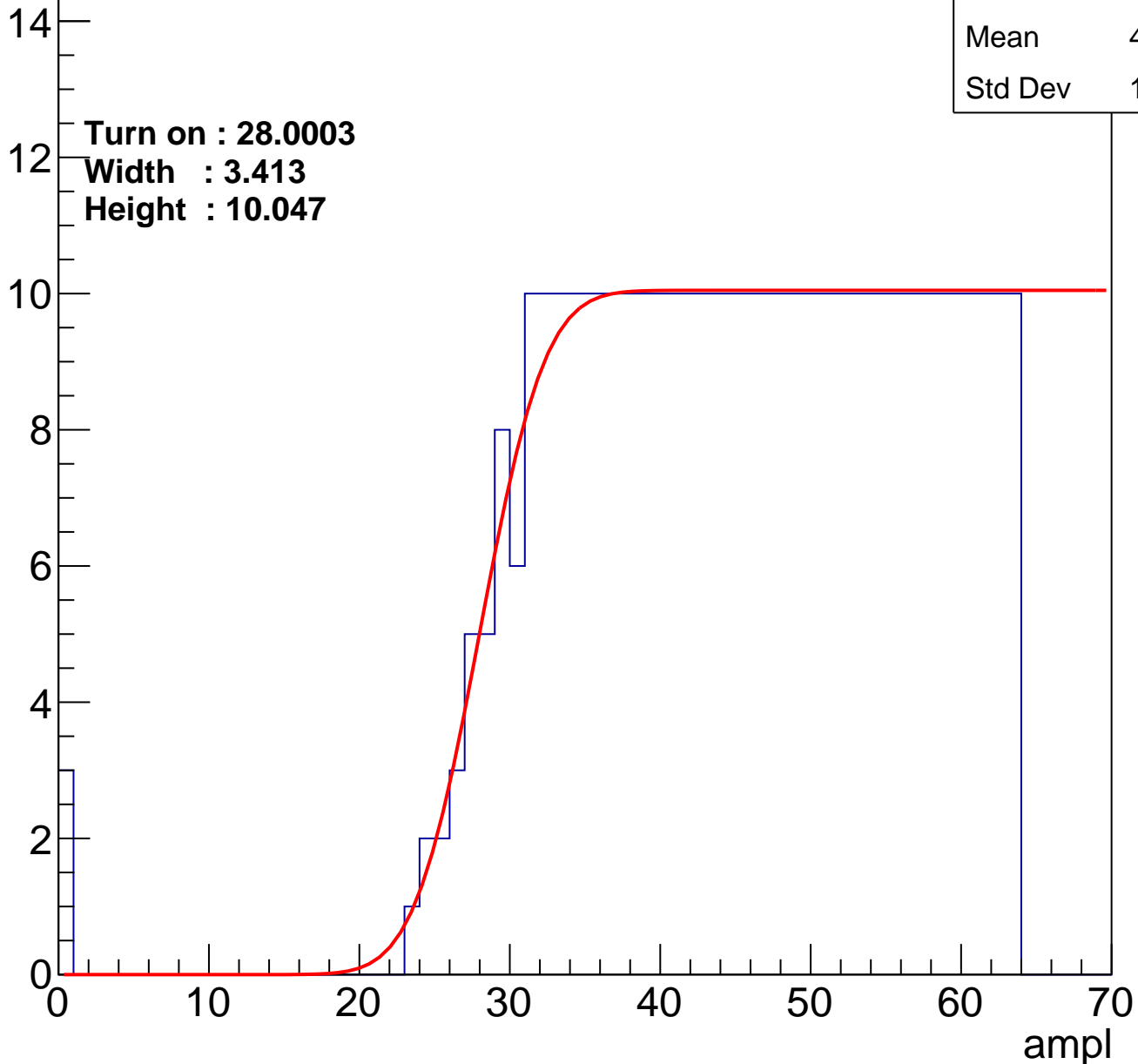
Entries	365
Mean	44.92
Std Dev	11.35

Turn on : 28.0003

Width : 3.413

Height : 10.047

Entry



B0L002S, U14-ch37

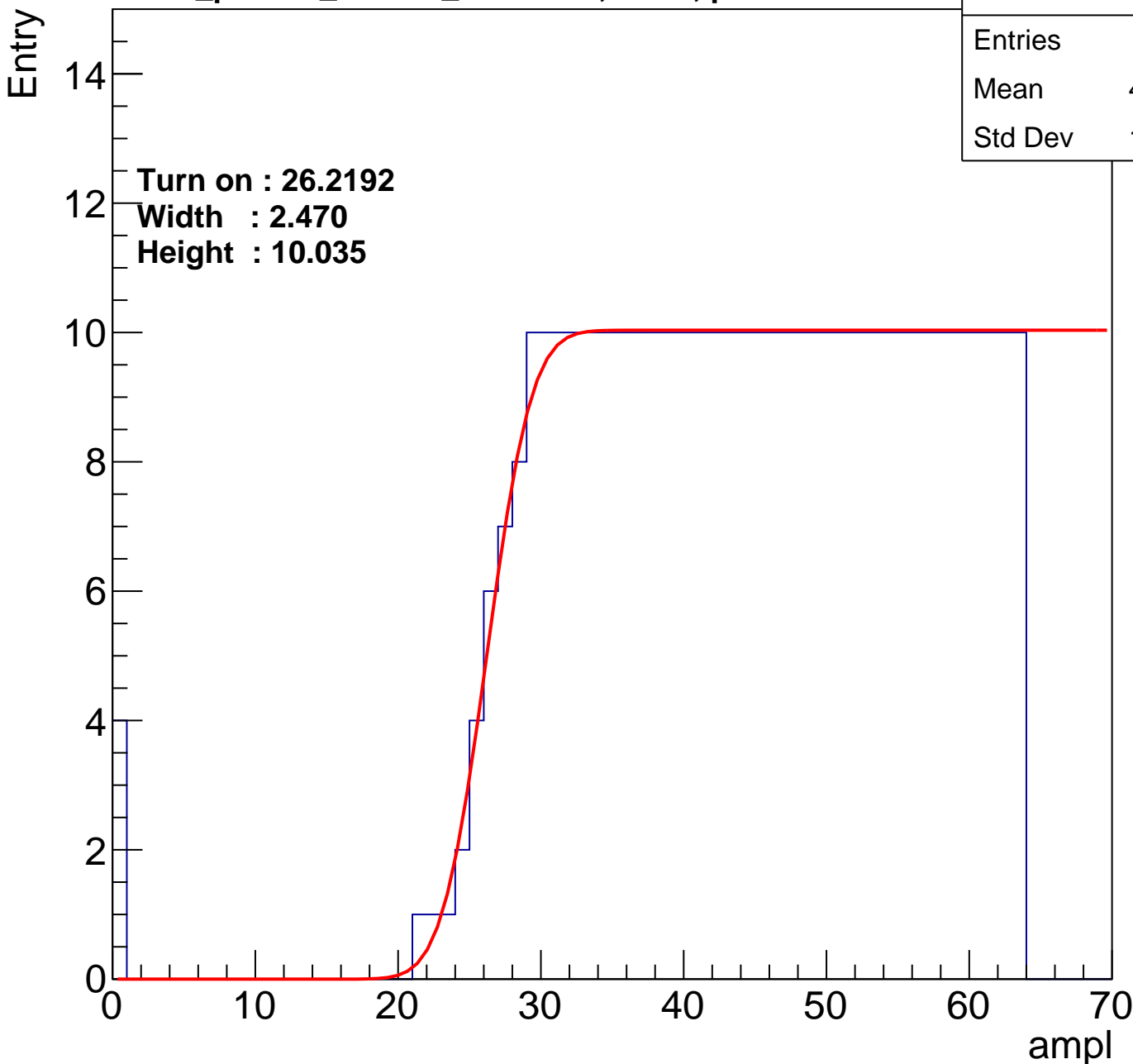
calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	43.97
Std Dev	11.92

Turn on : 26.2192

Width : 2.470

Height : 10.035



B0L002S, U14-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.41
Std Dev	10.77

Turn on : 28.4802

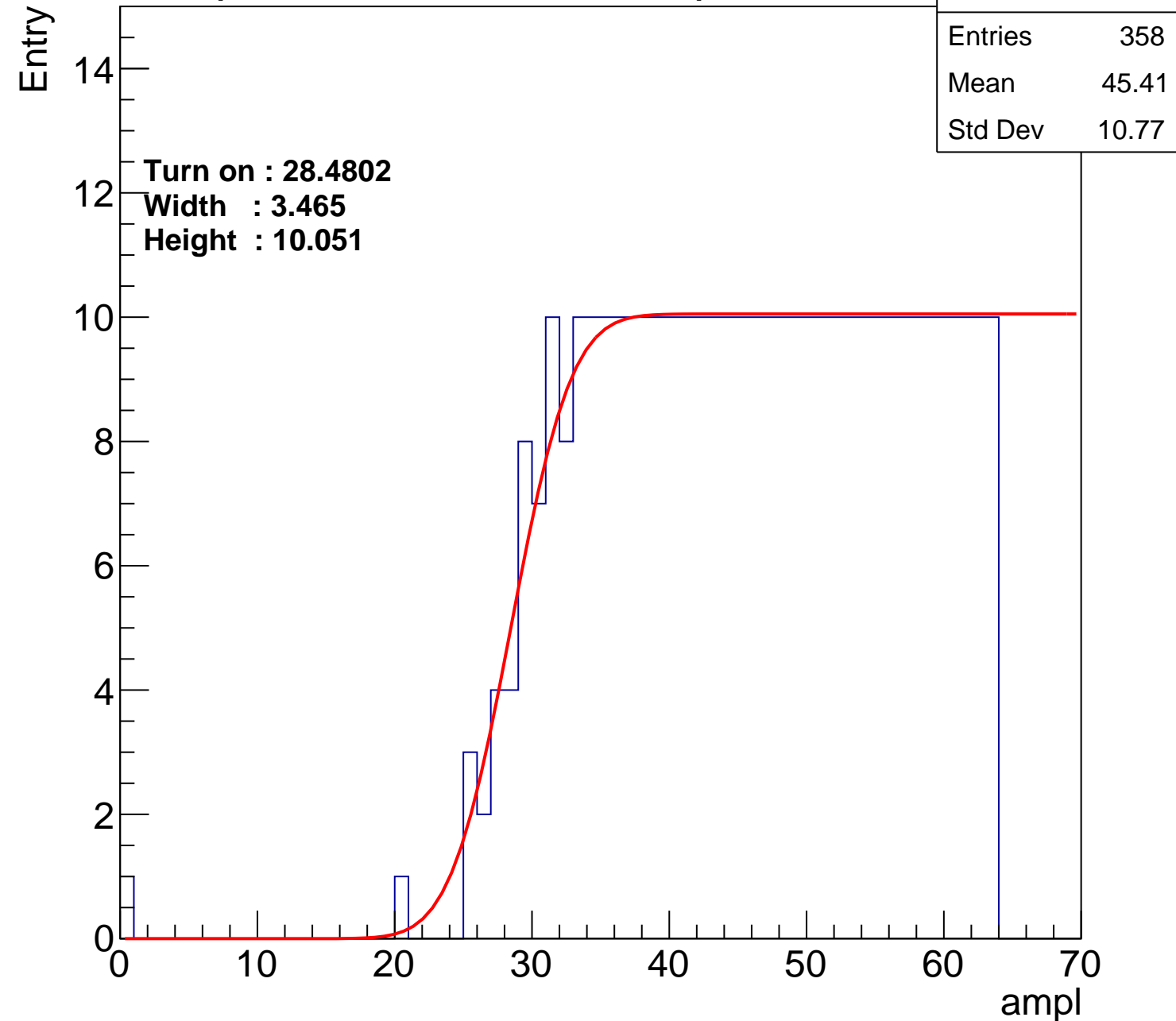
Width : 3.465

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.72
Std Dev	11.83

Turn on : 29.2228

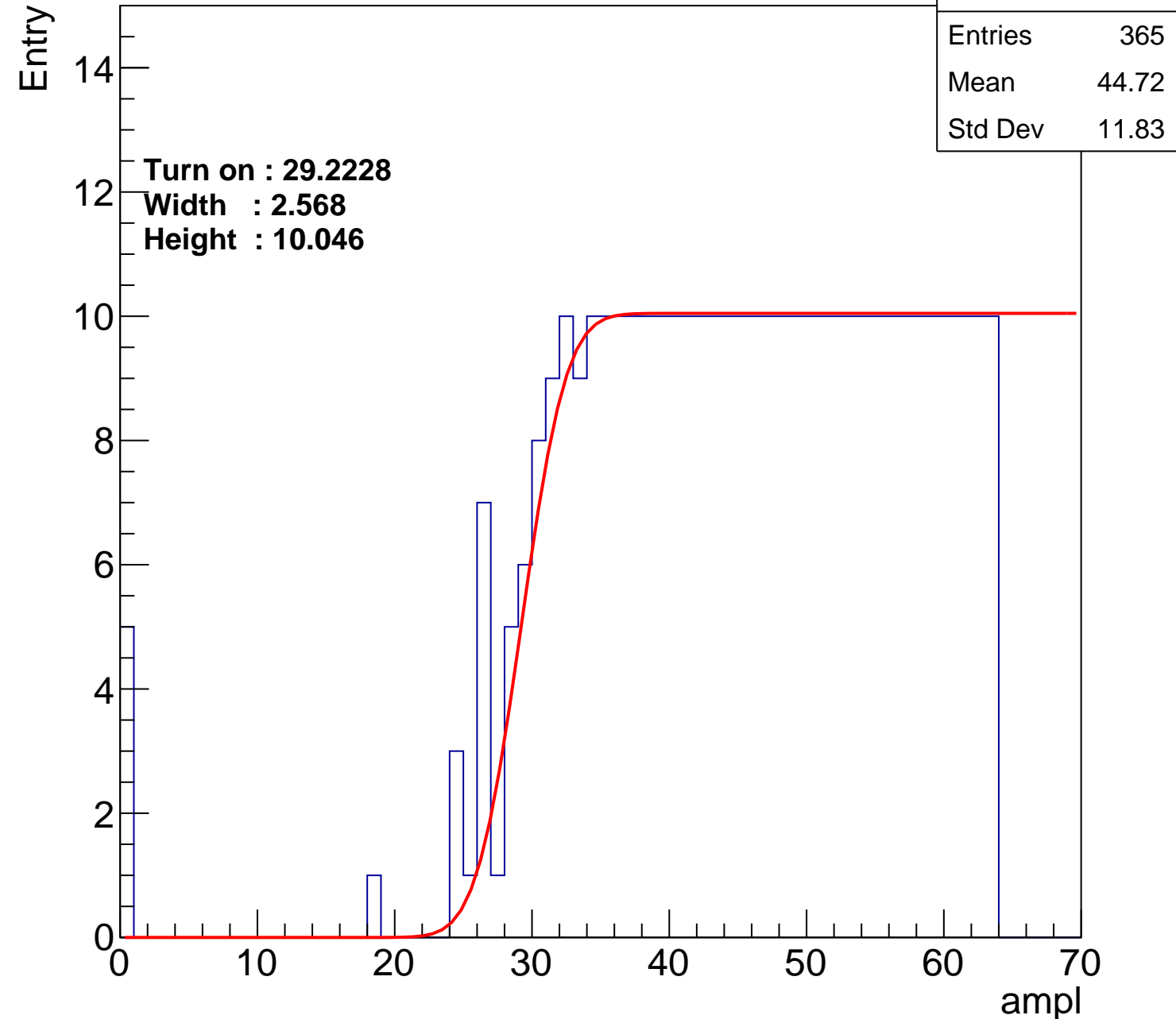
Width : 2.568

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 25.2960

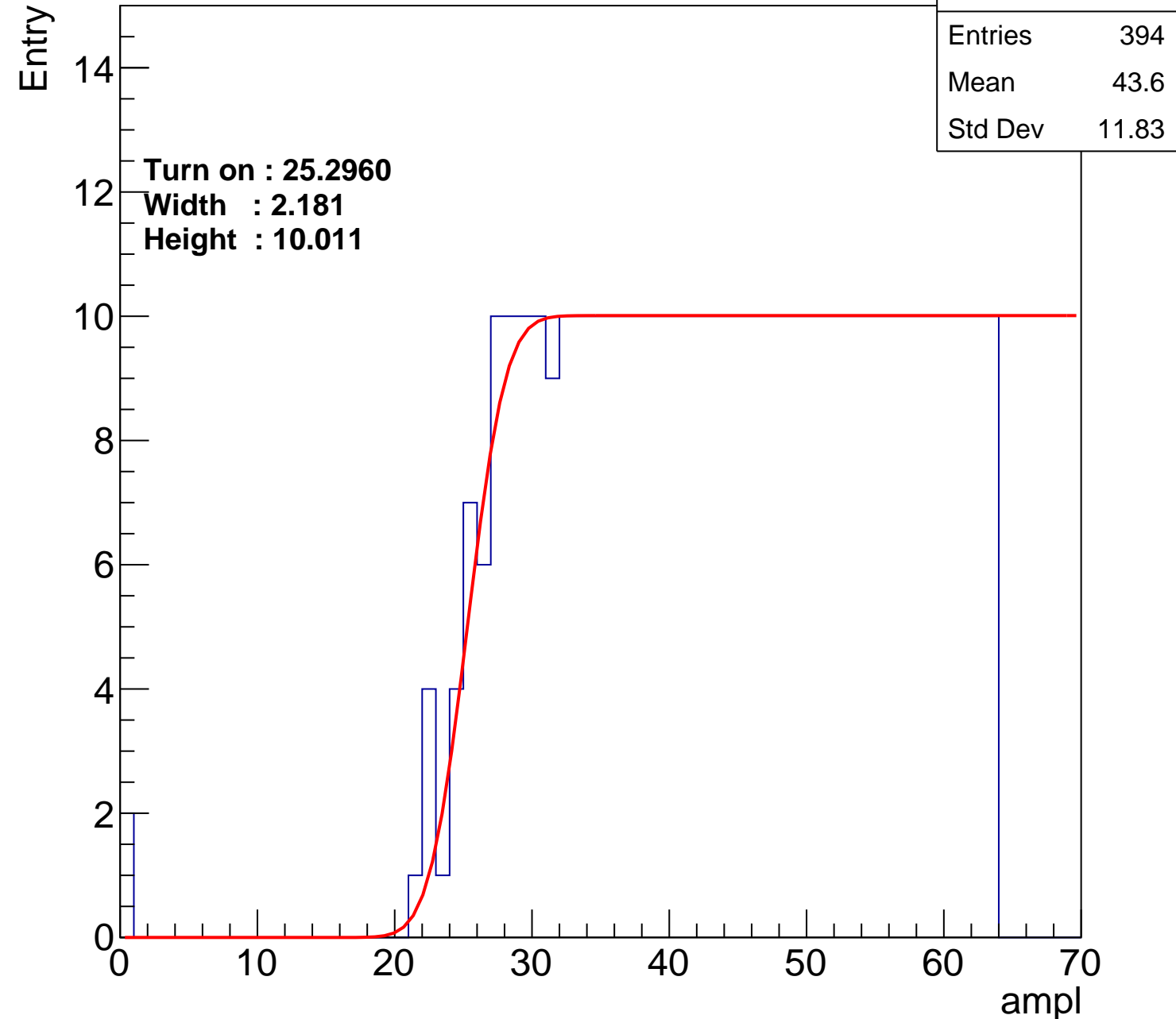
Width : 2.181

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	340
Mean	46.23
Std Dev	10.5

Turn on : 29.9435

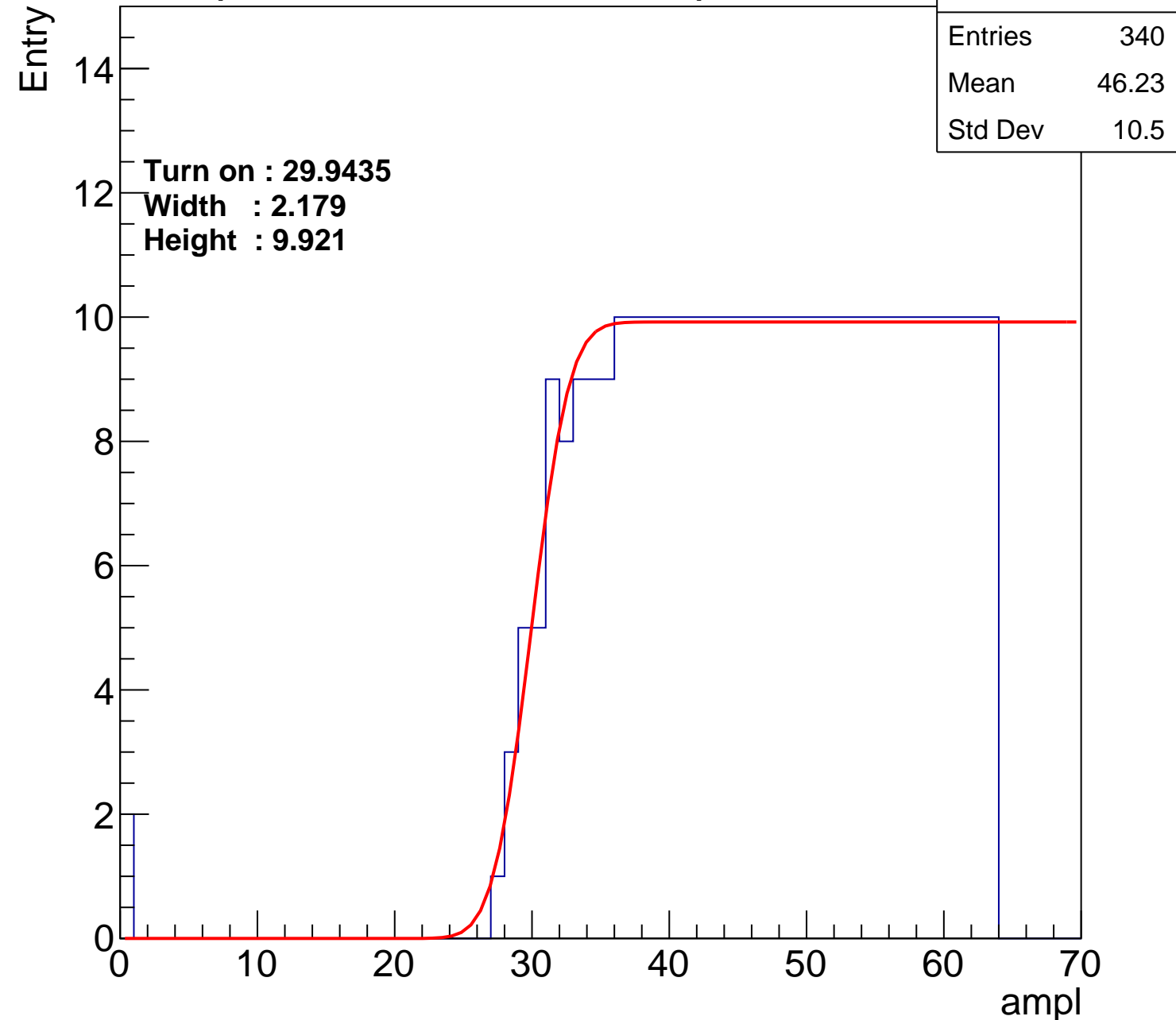
Width : 2.179

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch42

calib_packv5_042523_0143.root, FC#8, port C1

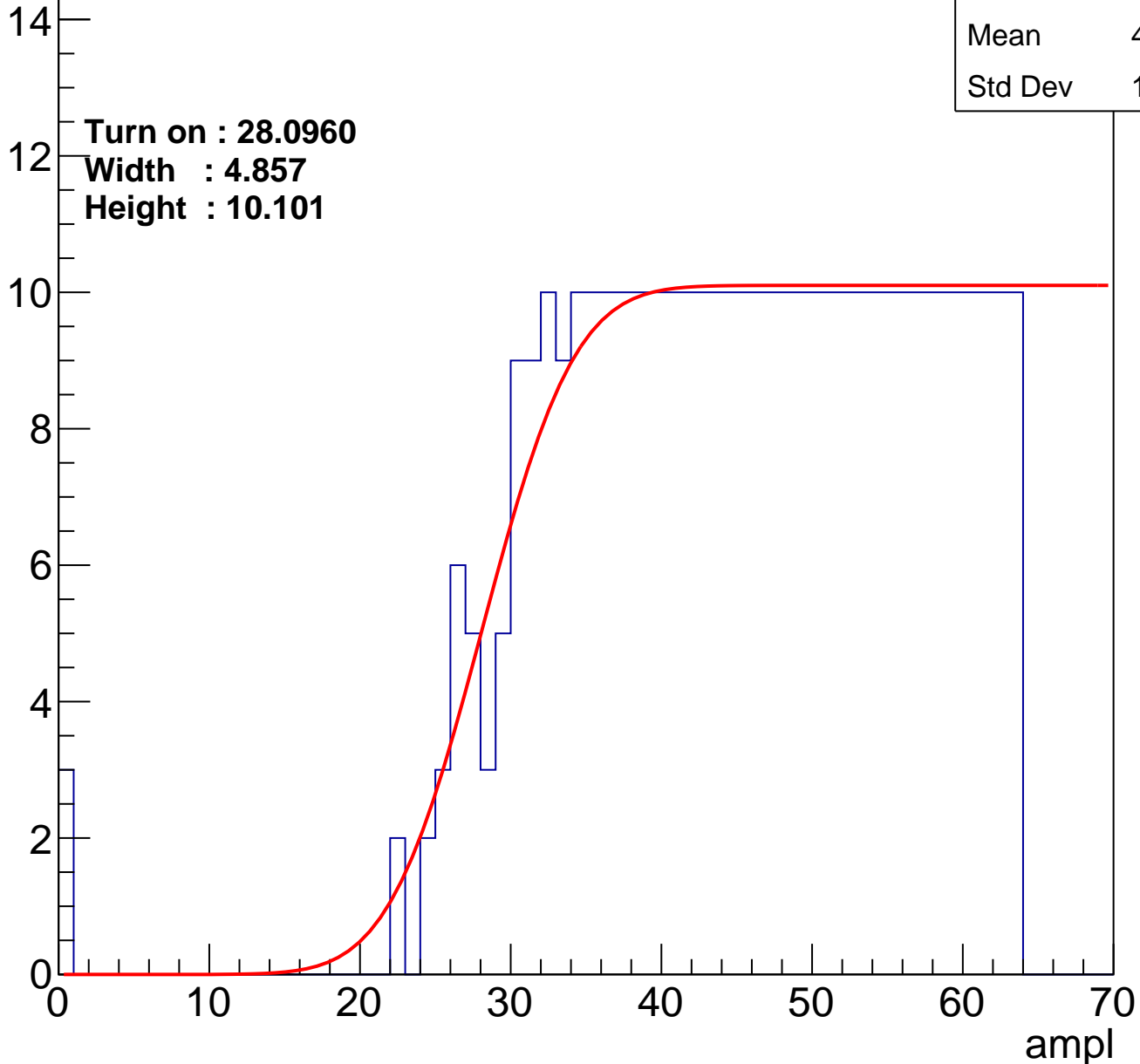
Entries	366
Mean	44.82
Std Dev	11.46

Turn on : 28.0960

Width : 4.857

Height : 10.101

Entry



B0L002S, U14-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.61
Std Dev	11.52

Turn on : 27.1493

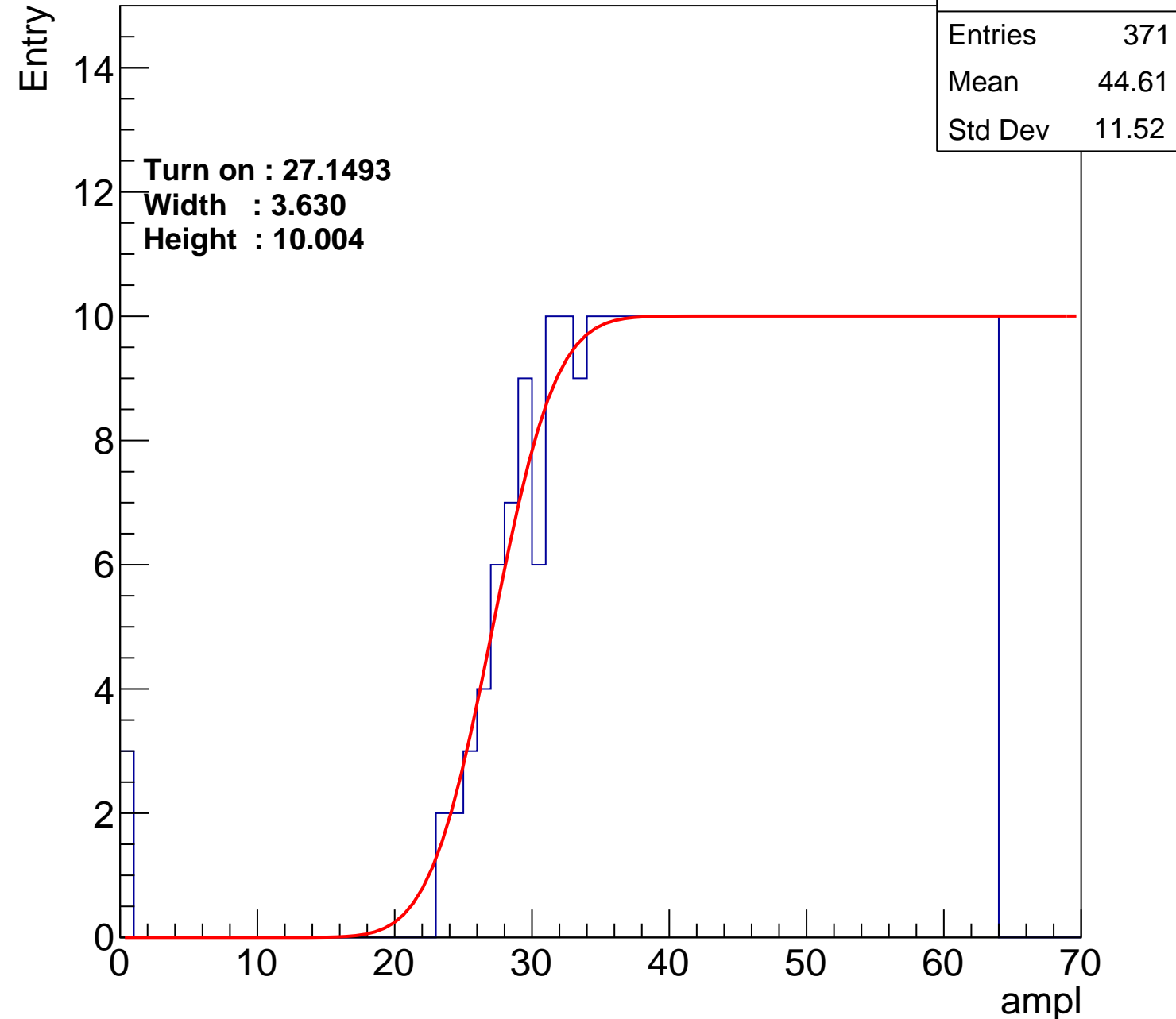
Width : 3.630

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.12
Std Dev	11.45

Turn on : 25.5338

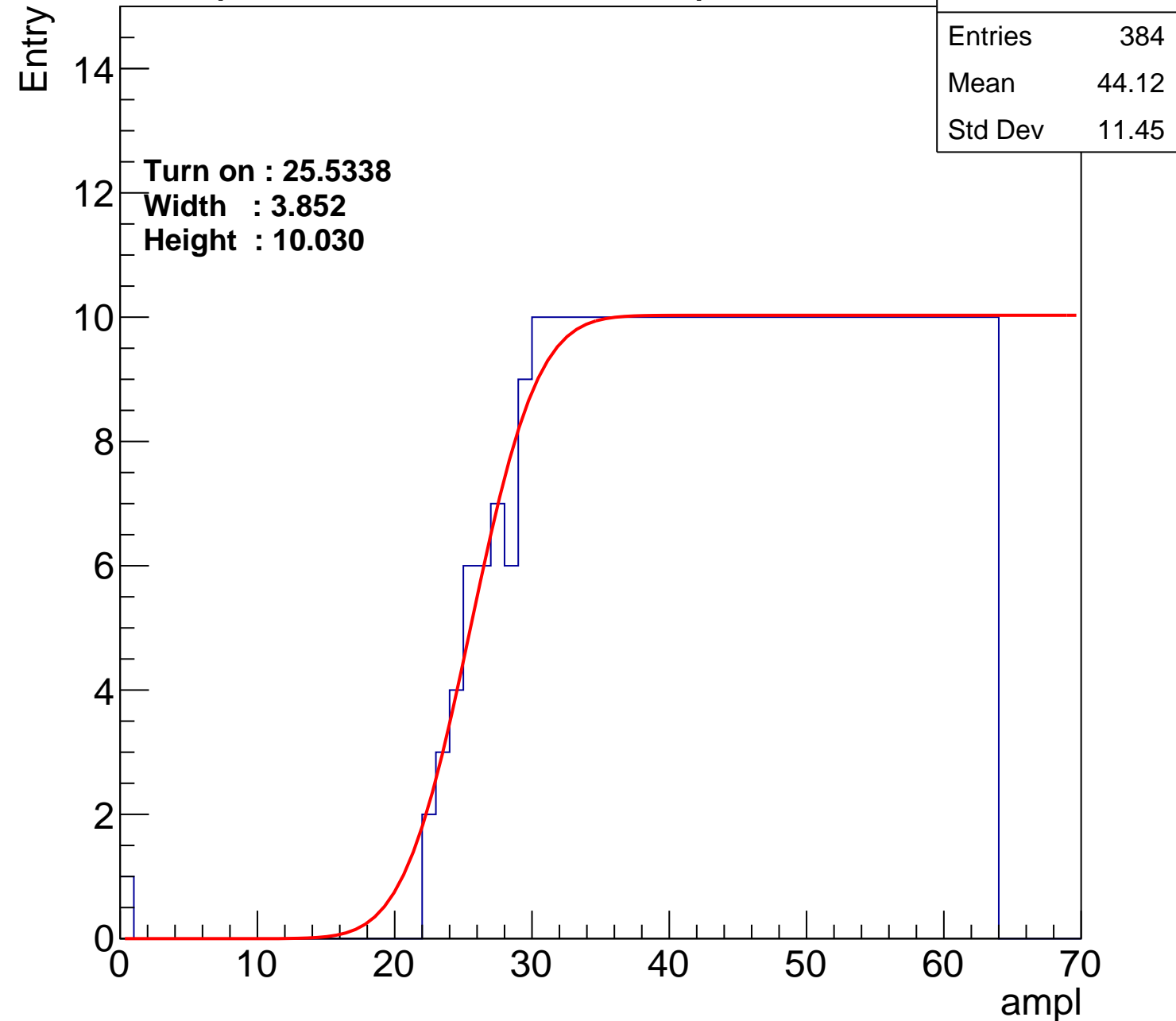
Width : 3.852

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.72
Std Dev	11.27

Turn on : 26.9915

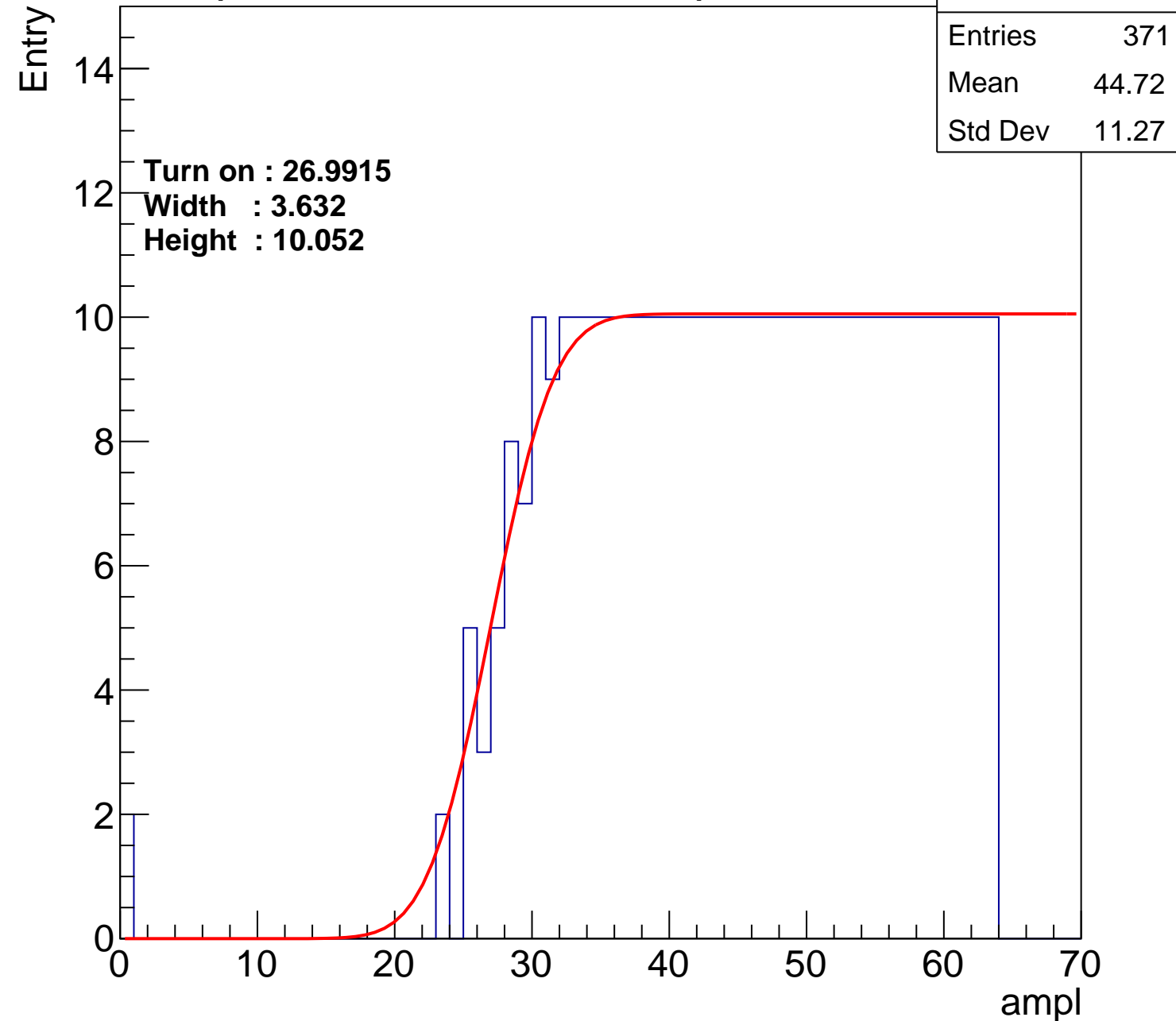
Width : 3.632

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.57
Std Dev	11.4

Turn on : 27.6300

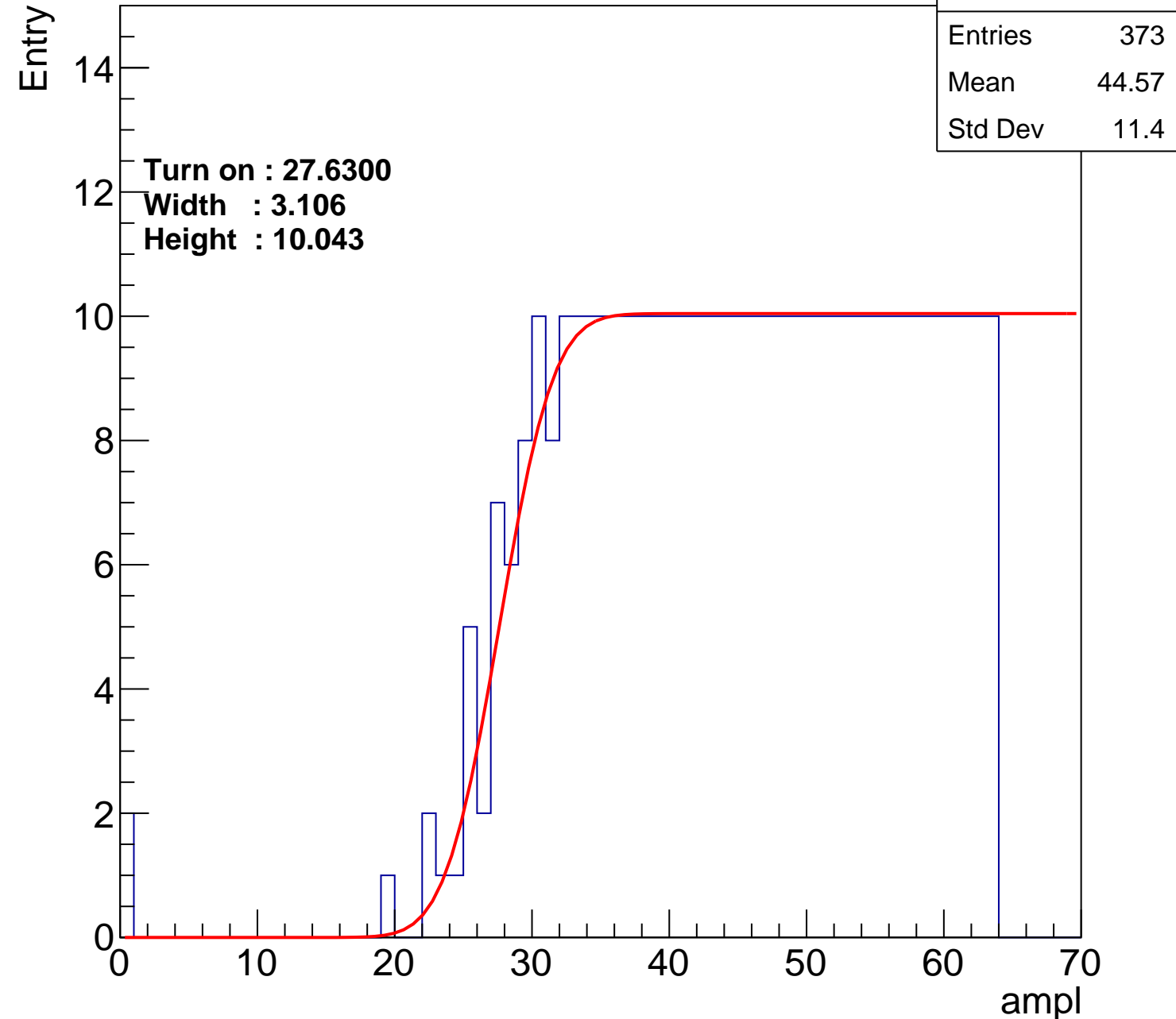
Width : 3.106

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch47

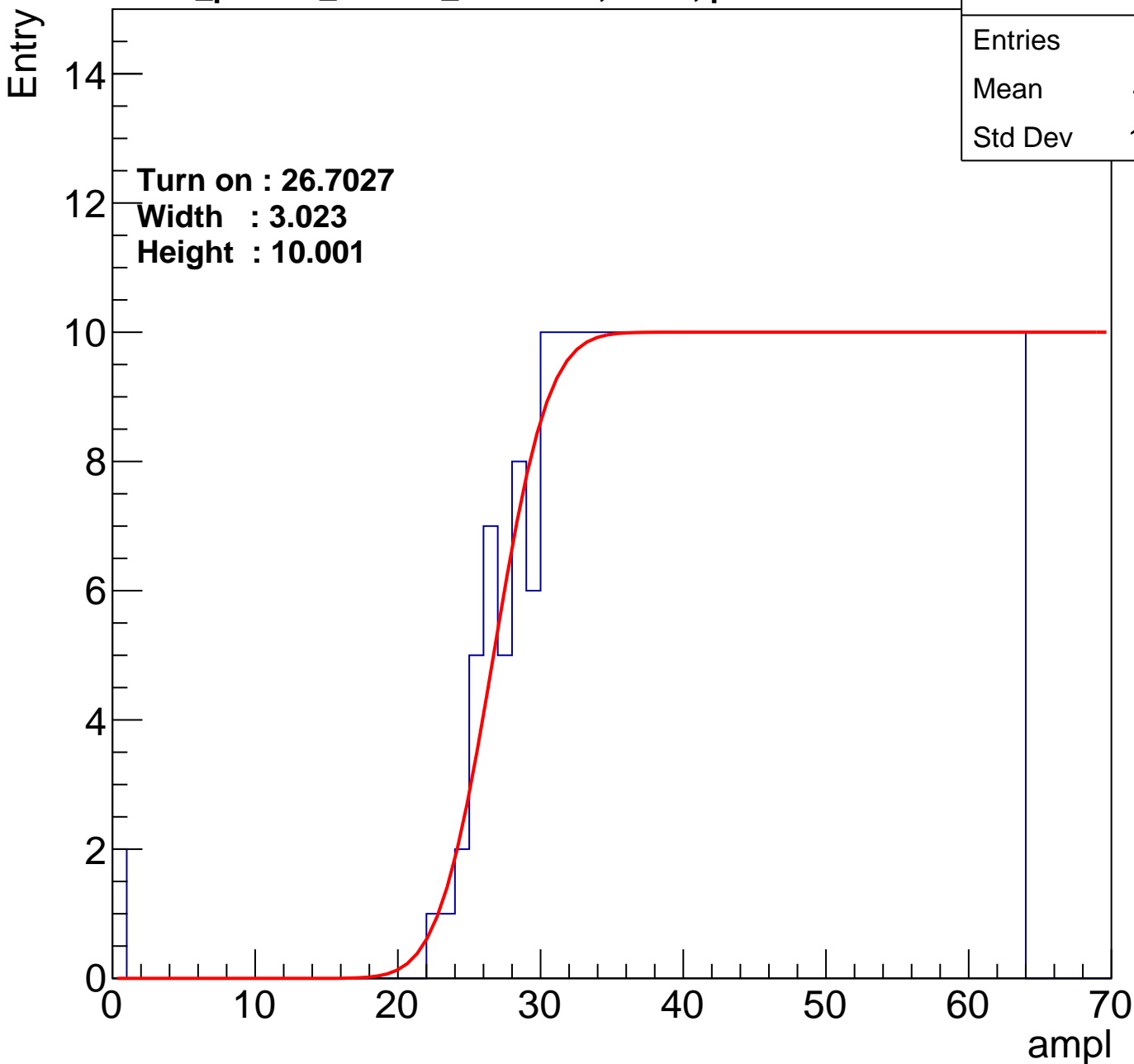
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 26.7027

Width : 3.023

Height : 10.001

Entries	377
Mean	44.41
Std Dev	11.44



B0L002S, U14-ch48

calib_packv5_042523_0143.root, FC#8, port C1

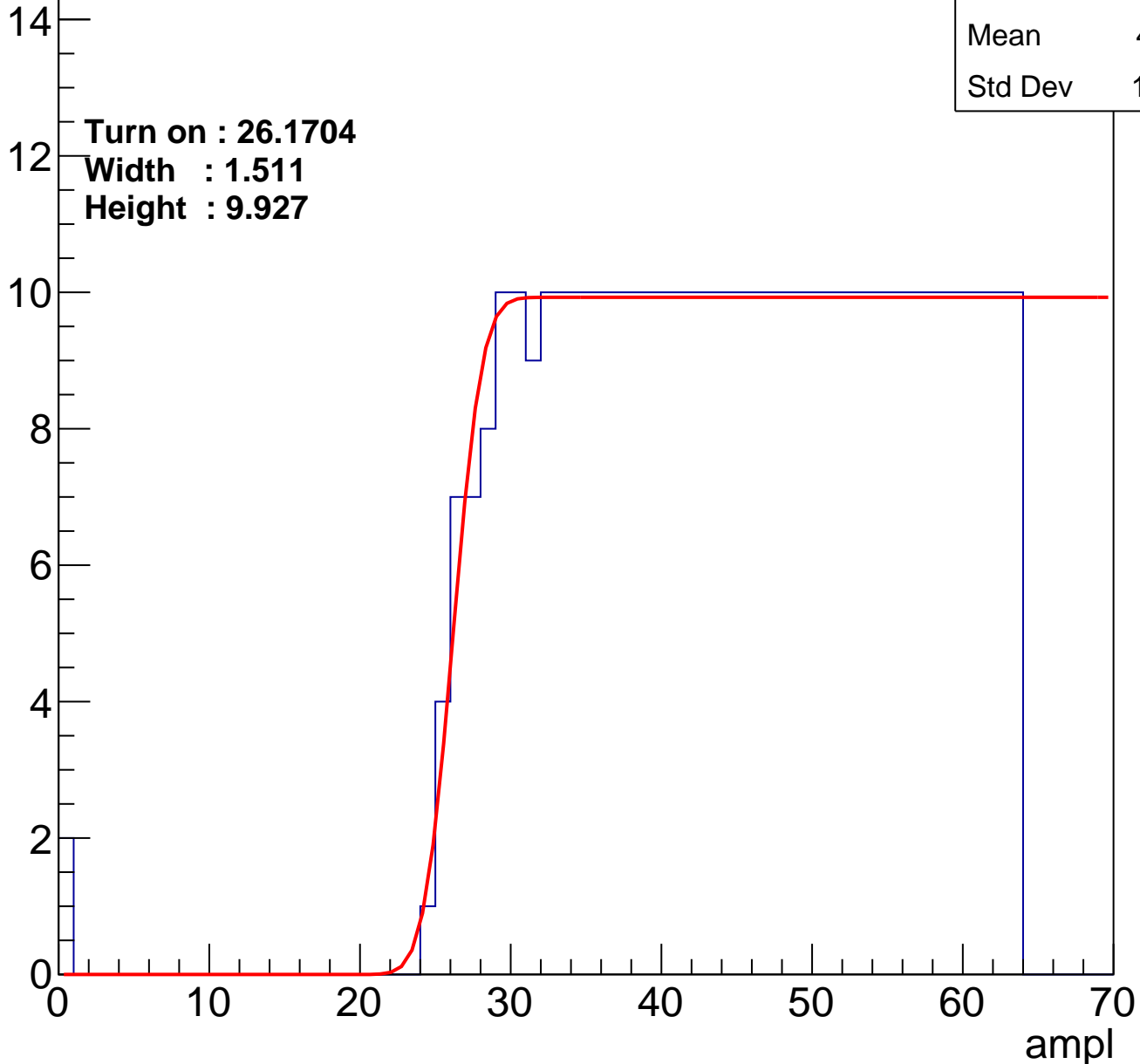
Entries	378
Mean	44.41
Std Dev	11.38

Turn on : 26.1704

Width : 1.511

Height : 9.927

Entry



B0L002S, U14-ch49

calib_packv5_042523_0143.root, FC#8, port C1

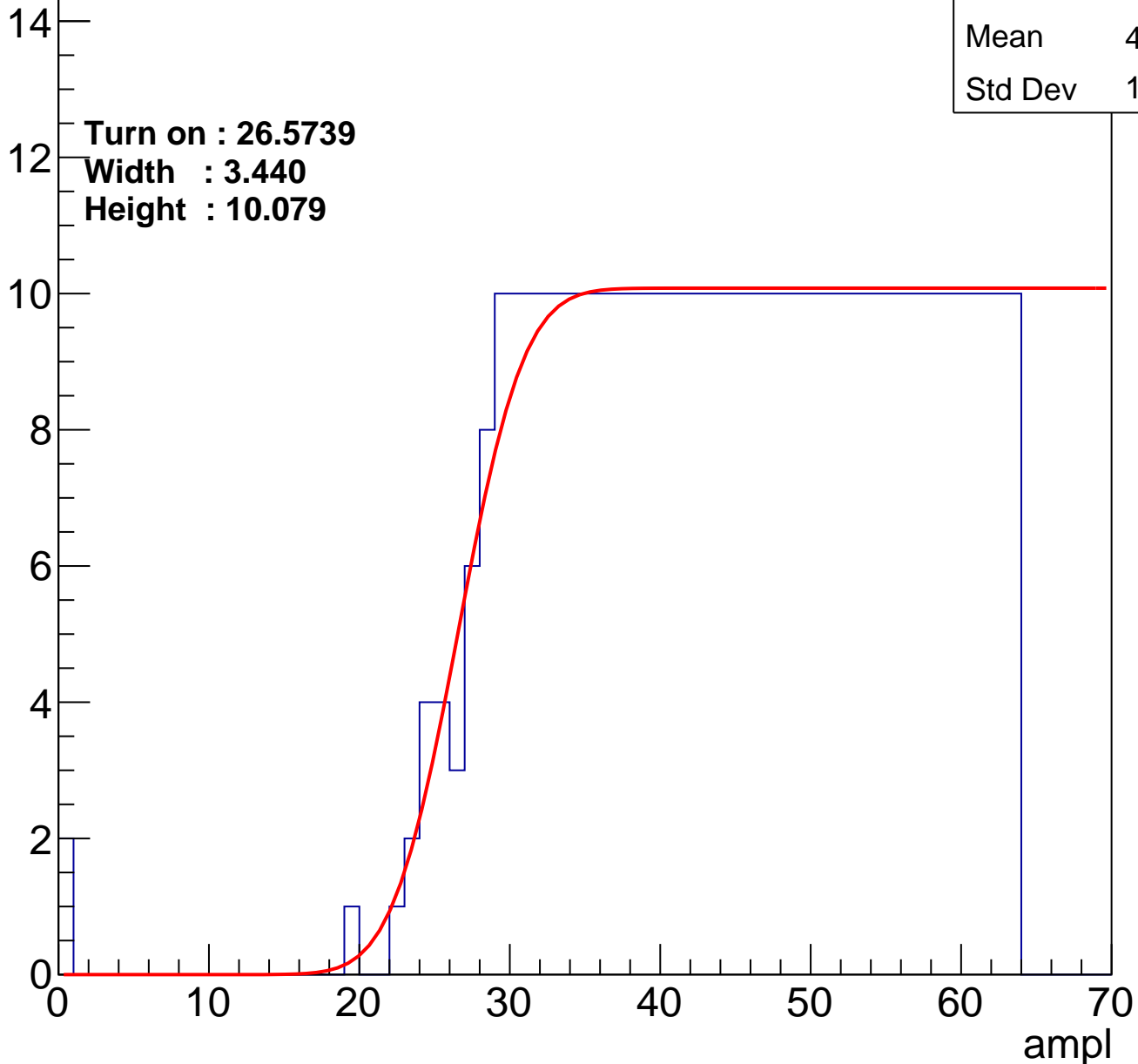
Entries	381
Mean	44.22
Std Dev	11.54

Turn on : 26.5739

Width : 3.440

Height : 10.079

Entry

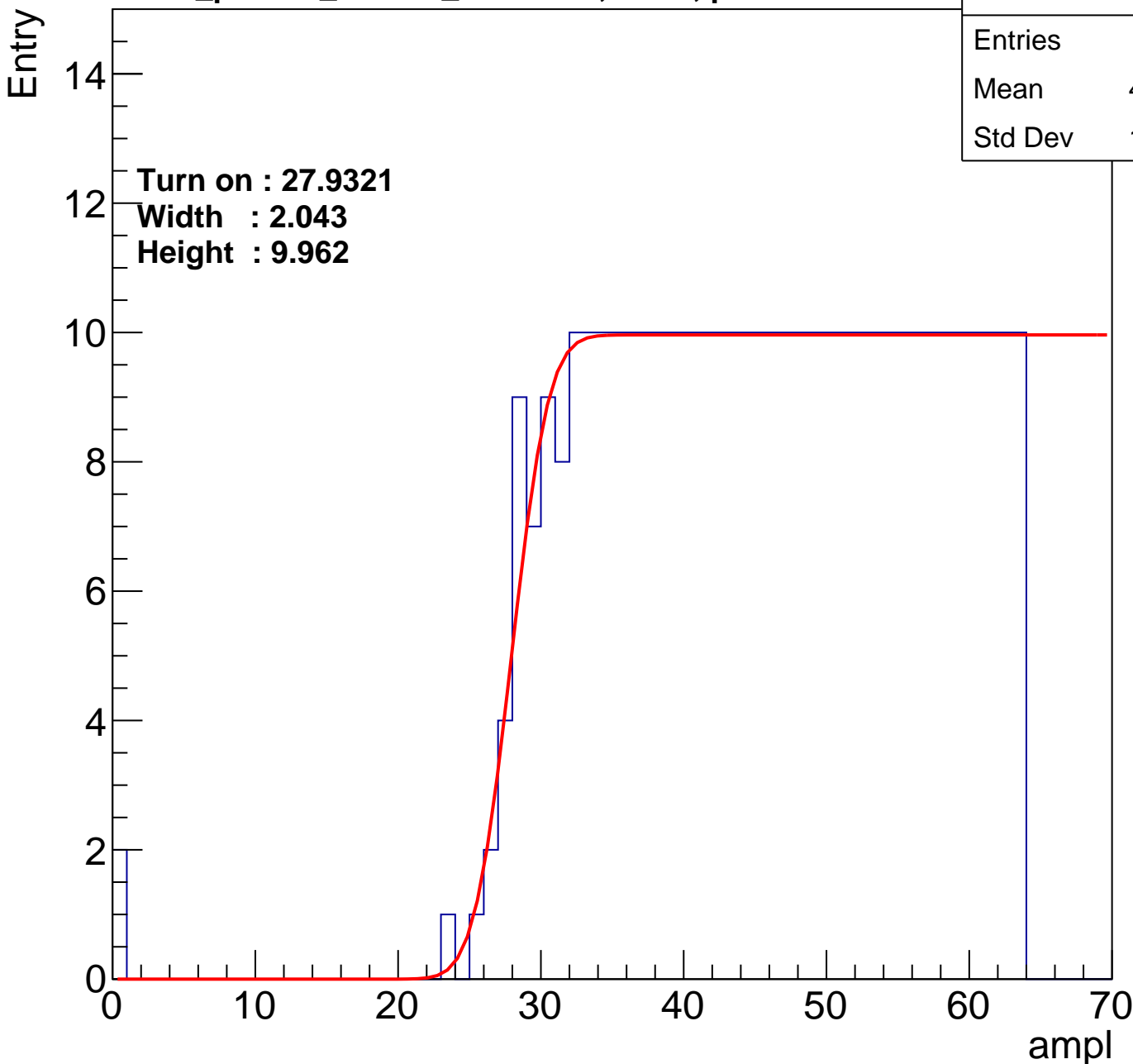


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.13
Std Dev	11.04

Height : 9.962



B0L002S, U14-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.34
Std Dev	11.63

Turn on : 27.0736

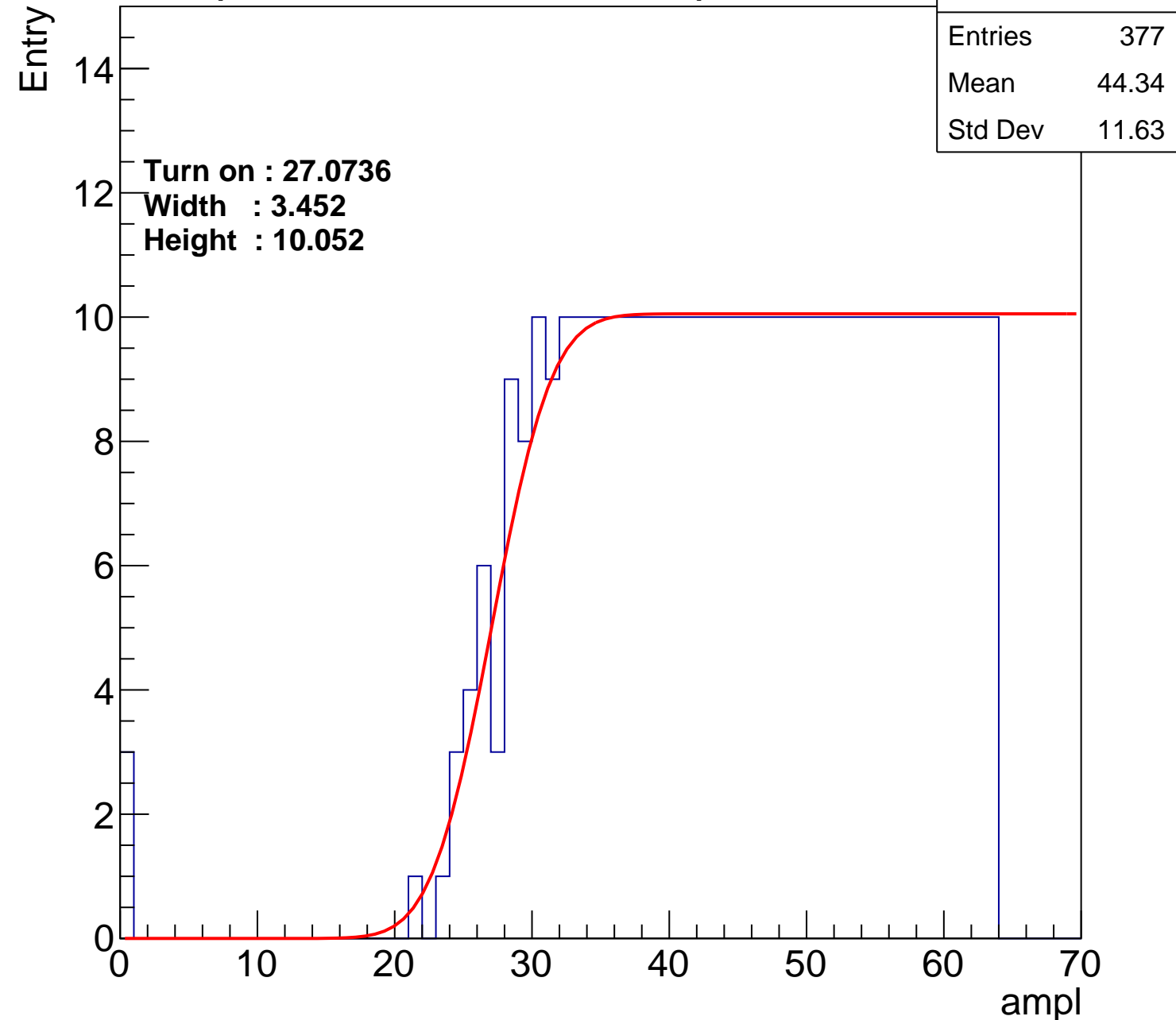
Width : 3.452

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch52

calib_packv5_042523_0143.root, FC#8, port C1

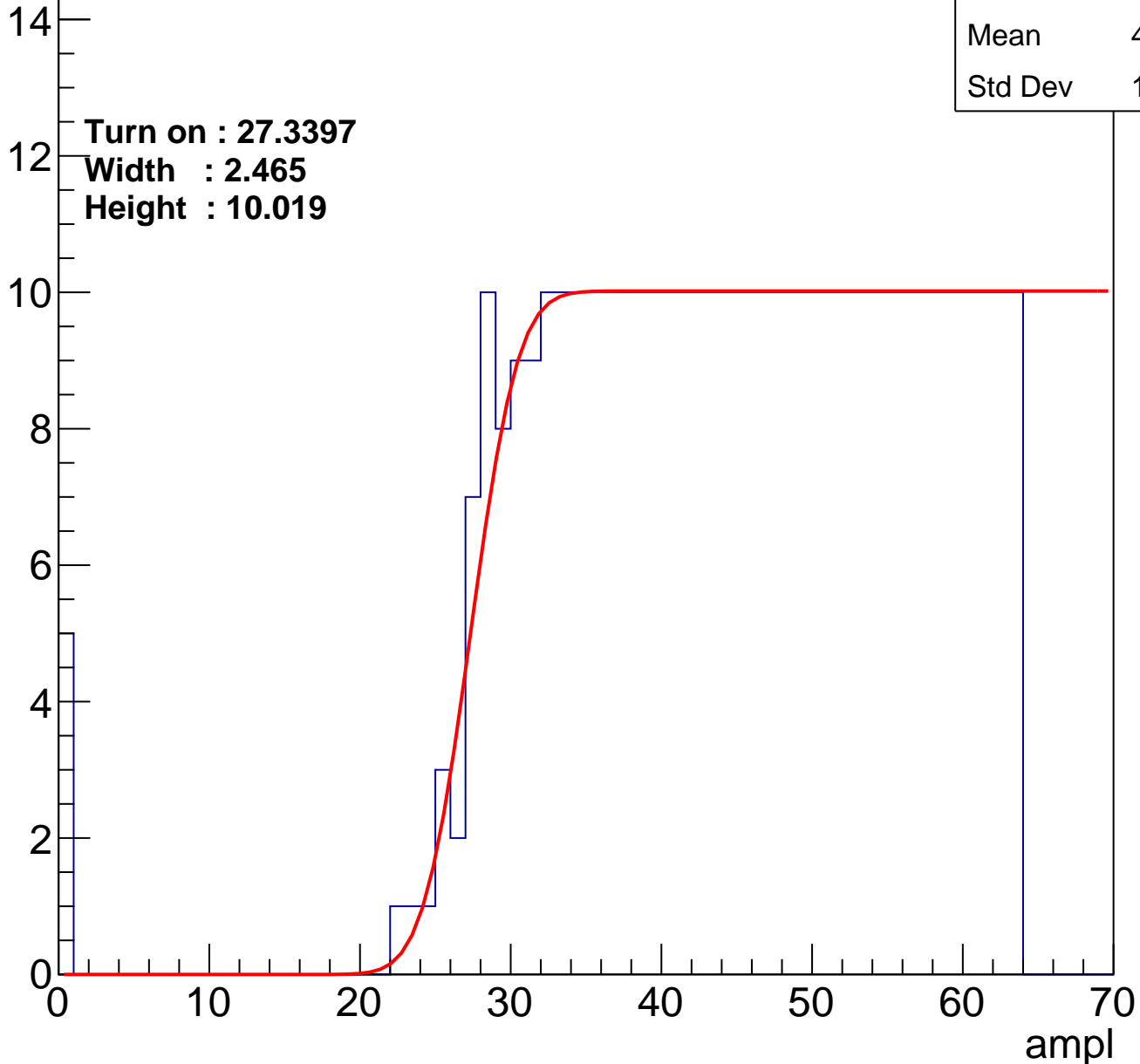
Entries	376
Mean	44.27
Std Dev	11.94

Turn on : 27.3397

Width : 2.465

Height : 10.019

Entry



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.8786
Width : 2.596
Height : 10.057



B0L002S, U14-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.05
Std Dev	10.9

Turn on : 27.7515

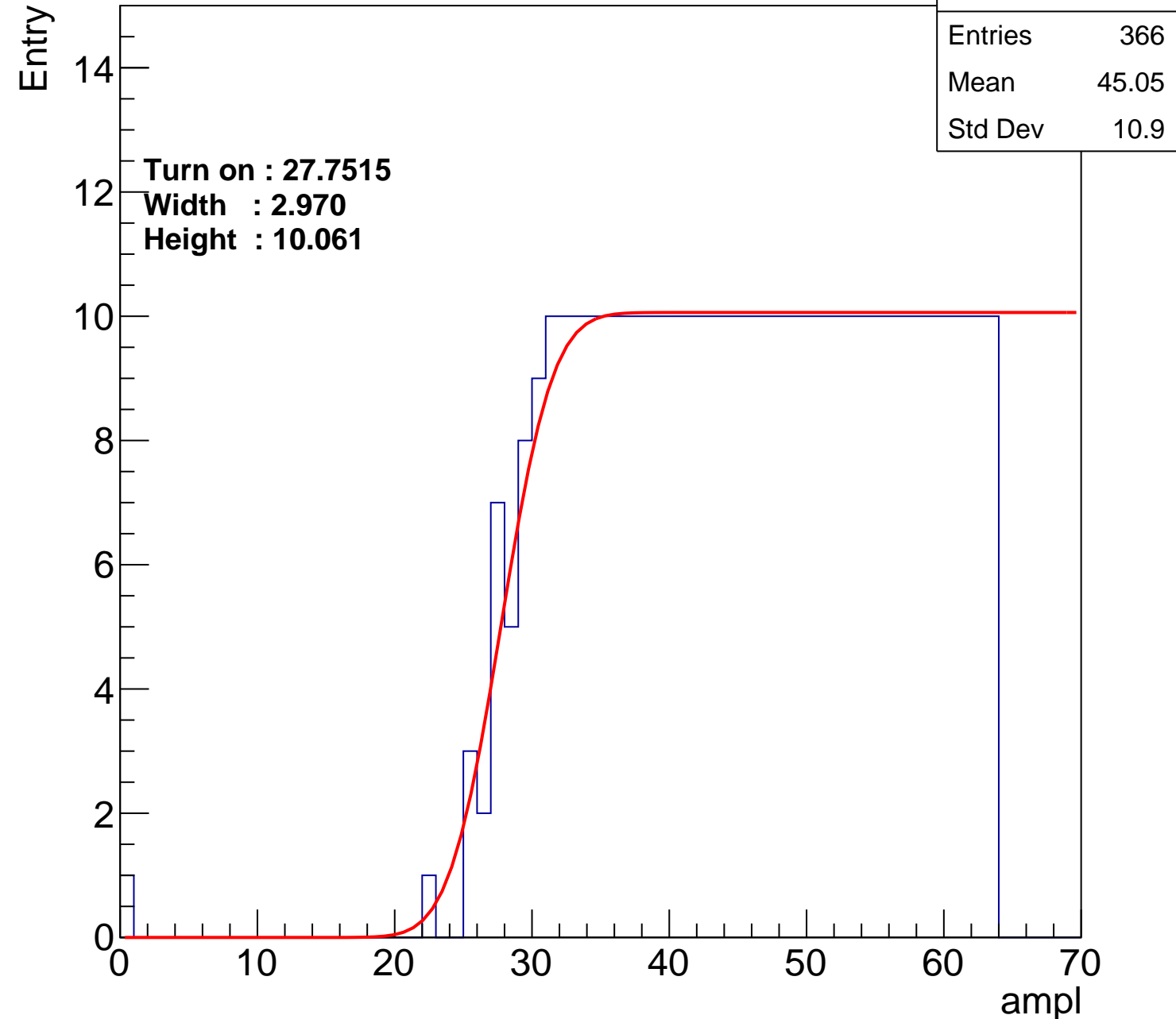
Width : 2.970

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

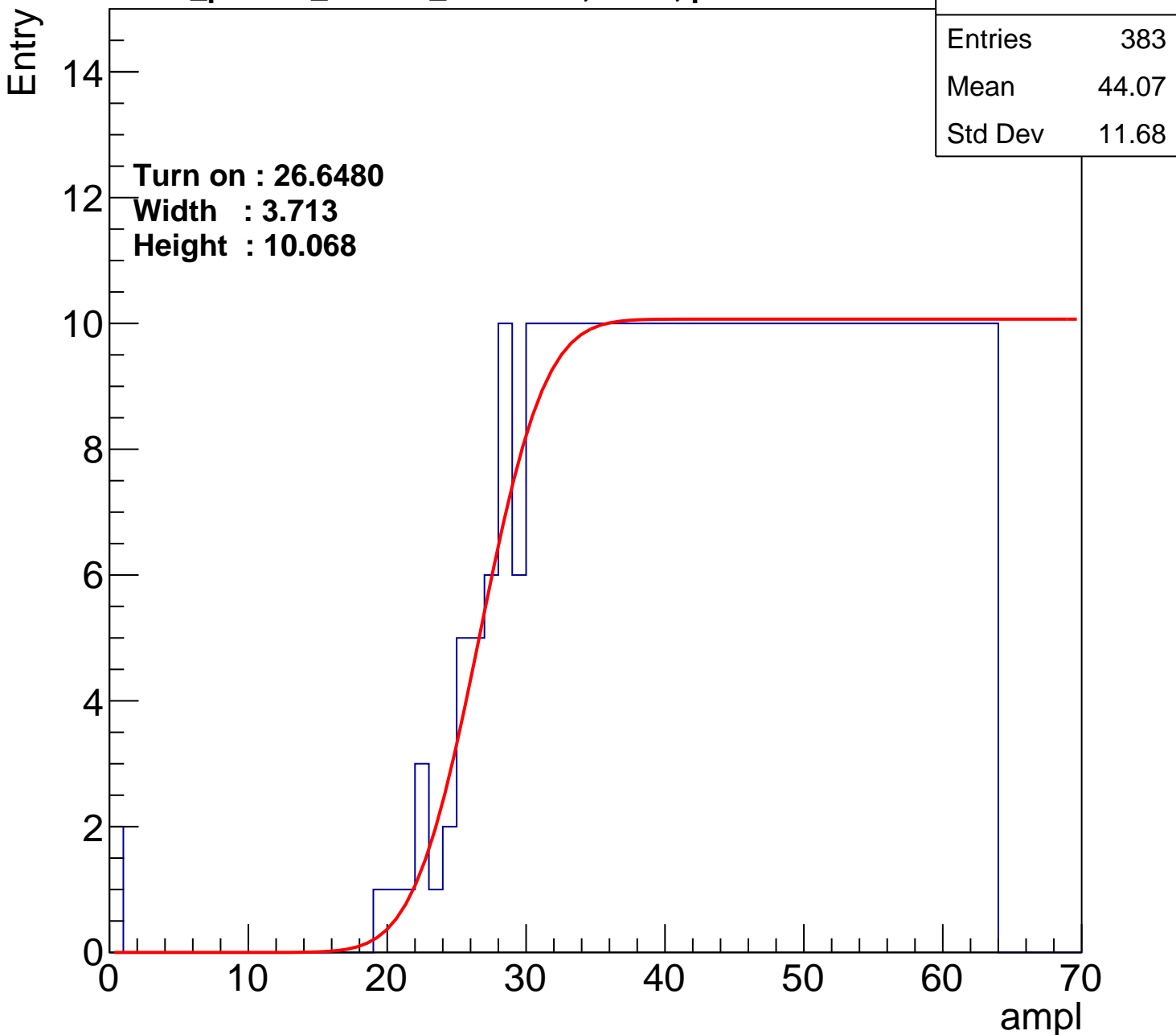
Entries	383
Mean	44.07
Std Dev	11.68

Mean	44.07
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Std Dev	11.68
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Width : 3.713

Height : 10.068



B0L002S, U14-ch56

calib_packv5_042523_0143.root, FC#8, port C1

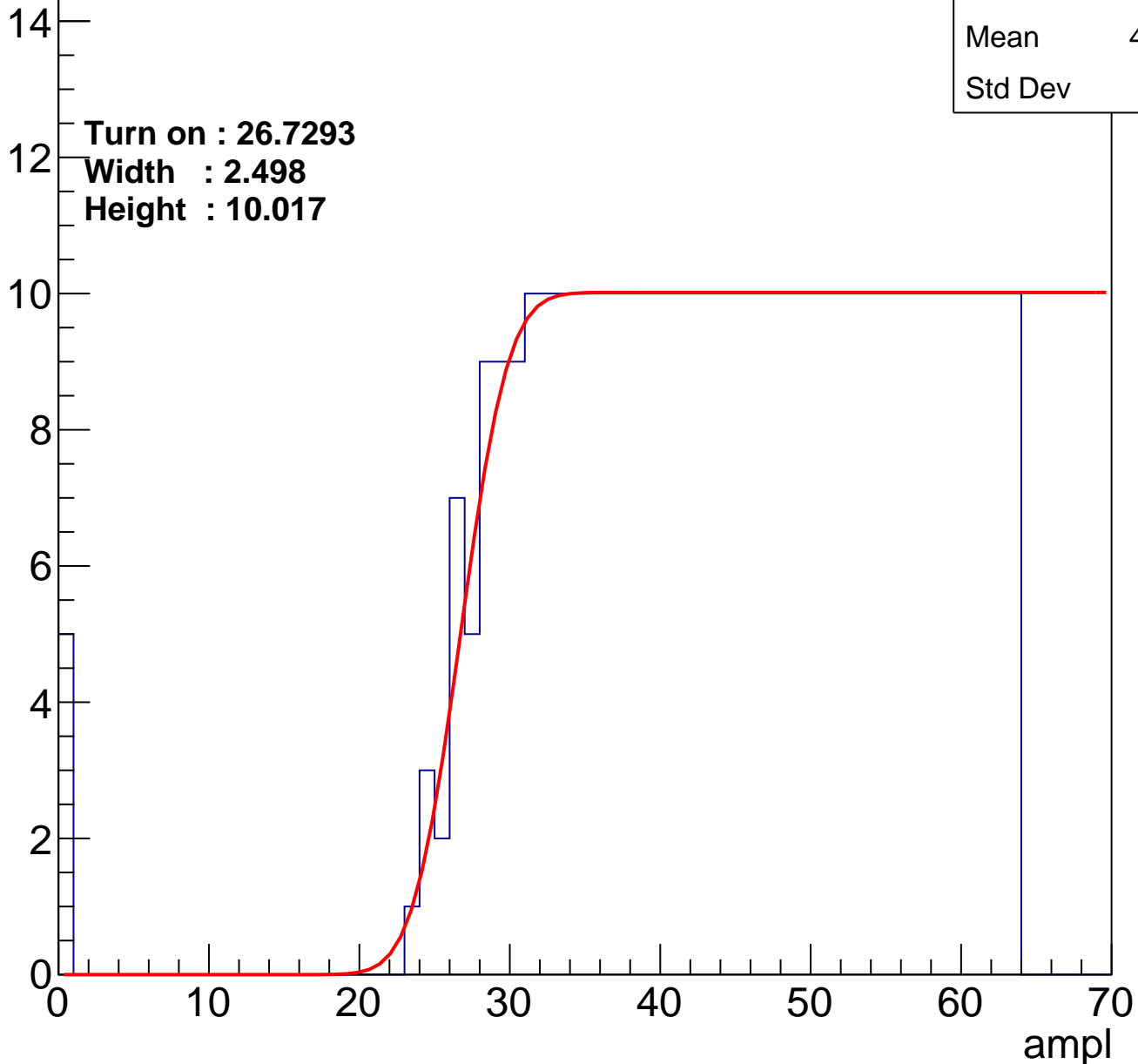
Entries	380
Mean	44.09
Std Dev	12

Turn on : 26.7293

Width : 2.498

Height : 10.017

Entry



B0L002S, U14-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.36
Std Dev	11.83

Turn on : 27.3497

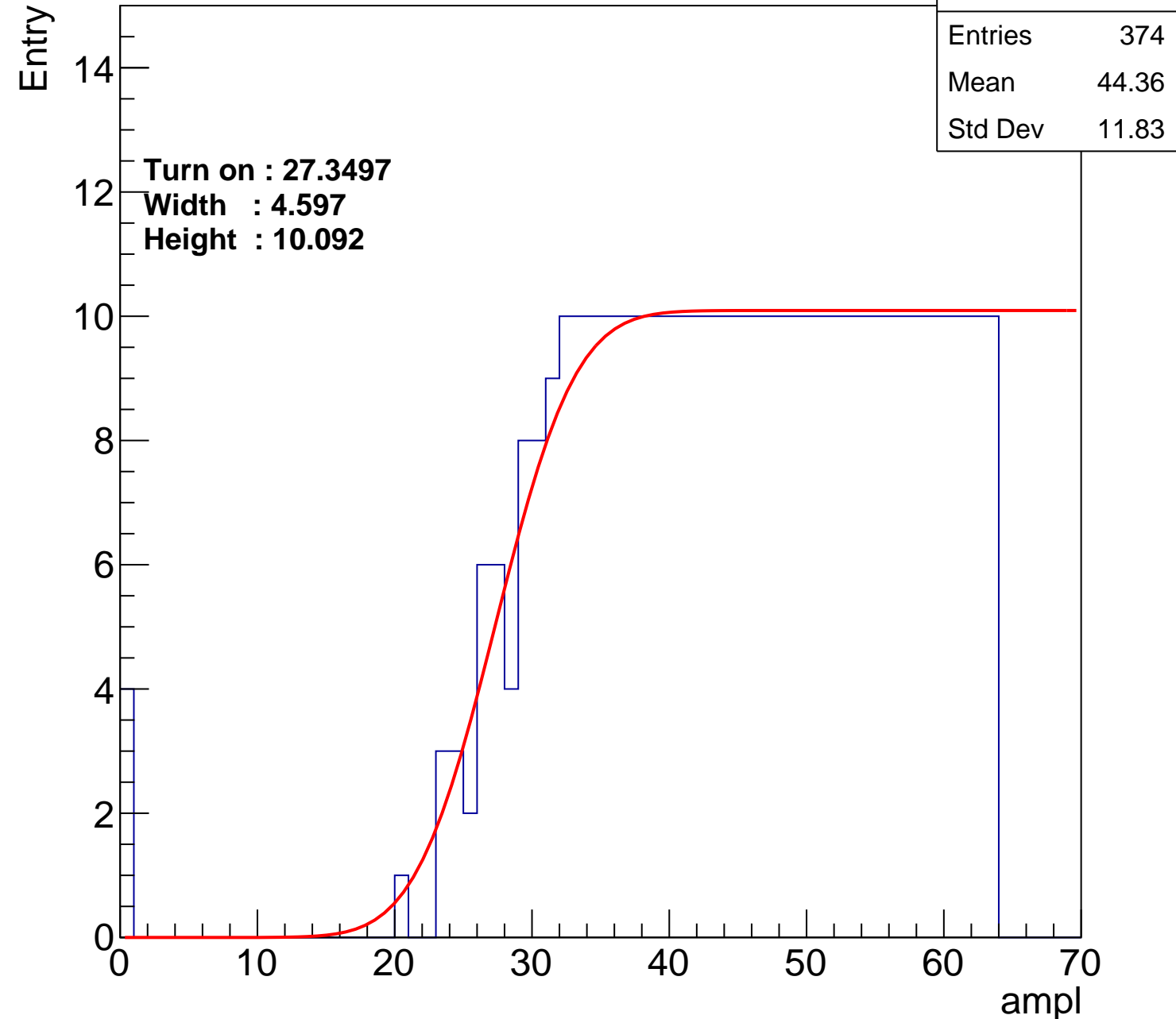
Width : 4.597

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.83
Std Dev	11.25

Turn on : 27.5040

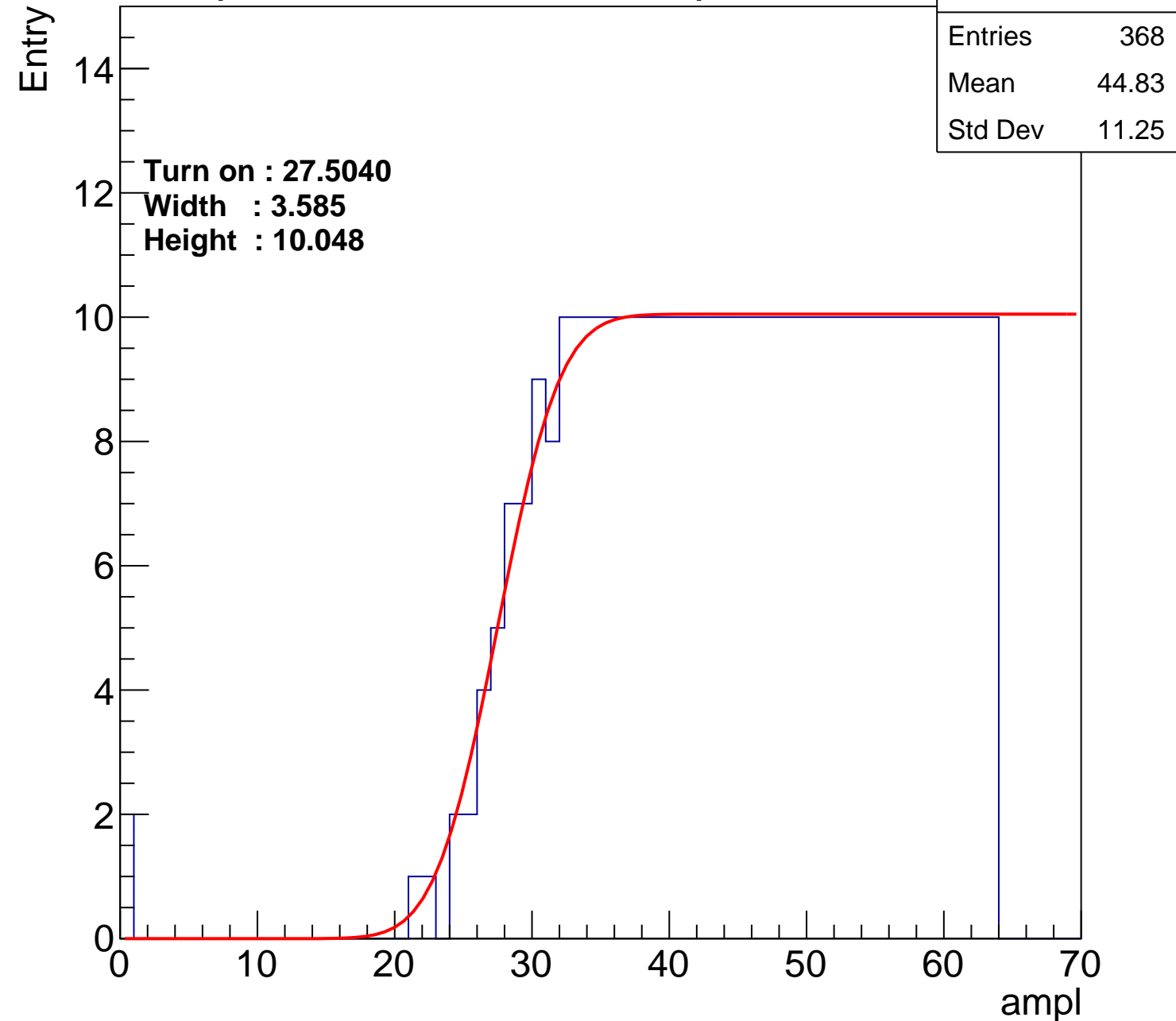
Width : 3.585

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch59

calib_packv5_042523_0143.root, FC#8, port C1

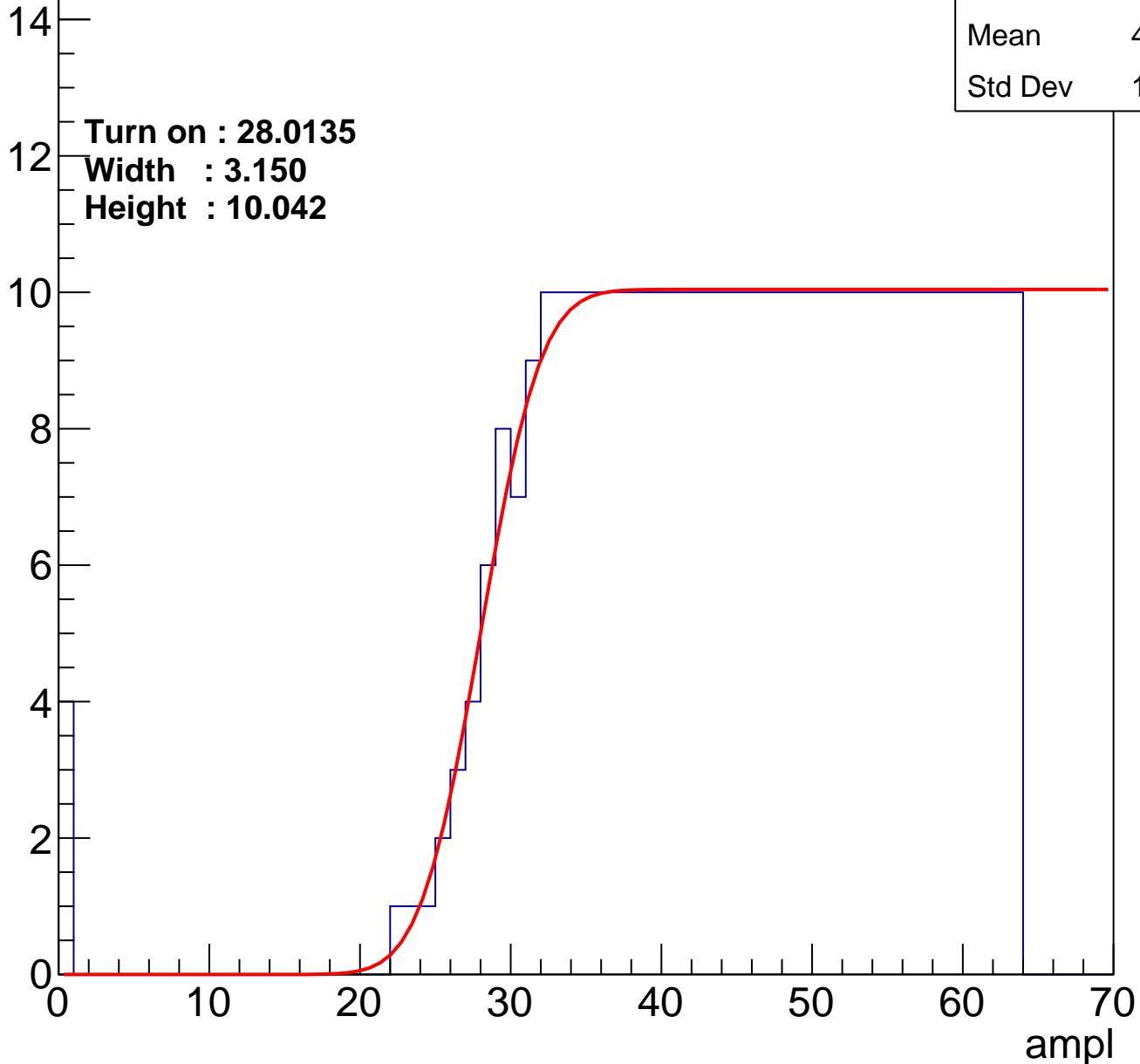
Entries	366
Mean	44.79
Std Dev	11.58

Turn on : 28.0135

Width : 3.150

Height : 10.042

Entry



B0L002S, U14-ch60

calib_packv5_042523_0143.root, FC#8, port C1

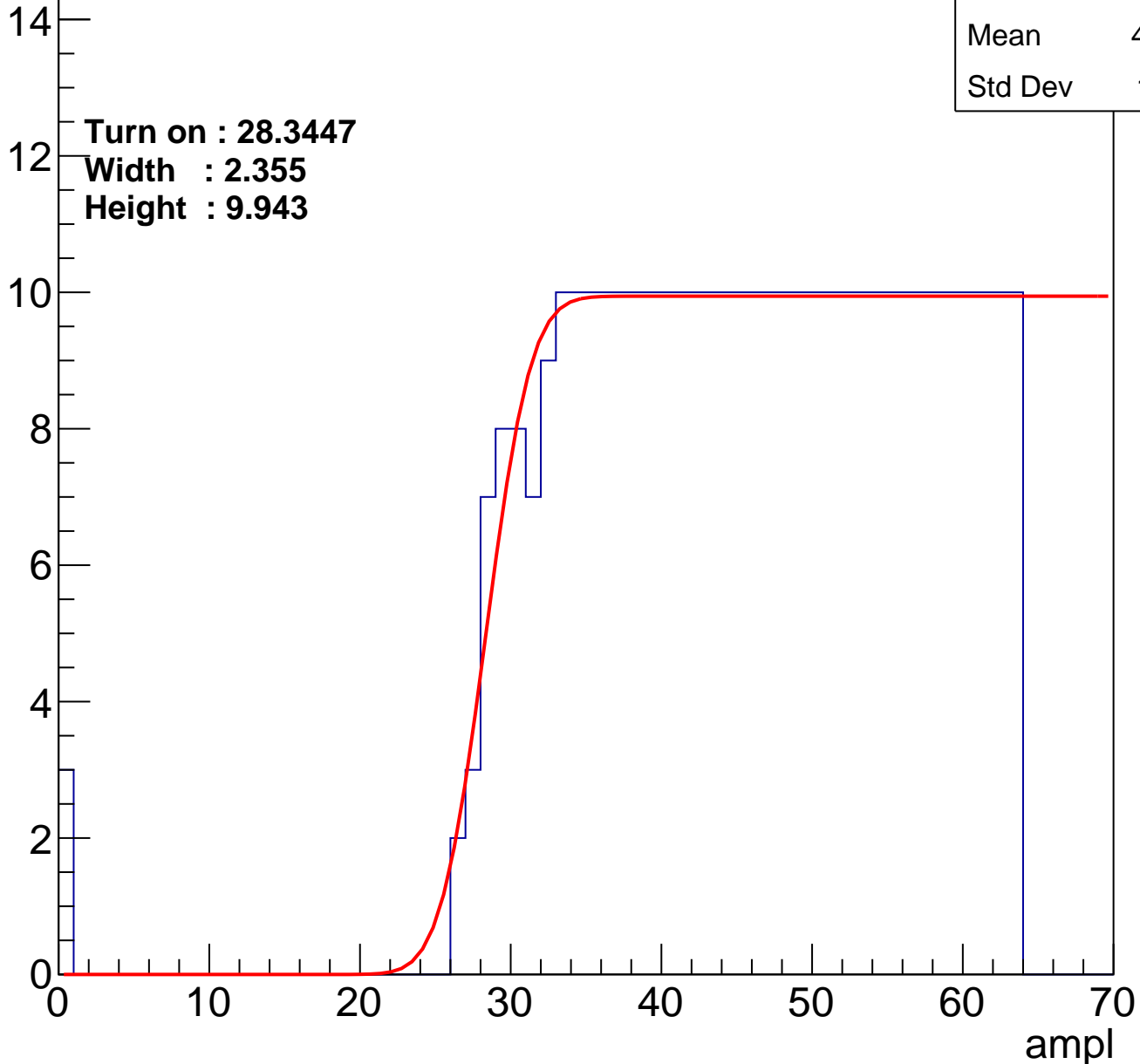
Entries	357
Mean	45.34
Std Dev	11.11

Turn on : 28.3447

Width : 2.355

Height : 9.943

Entry



B0L002S, U14-ch61

calib_packv5_042523_0143.root, FC#8, port C1

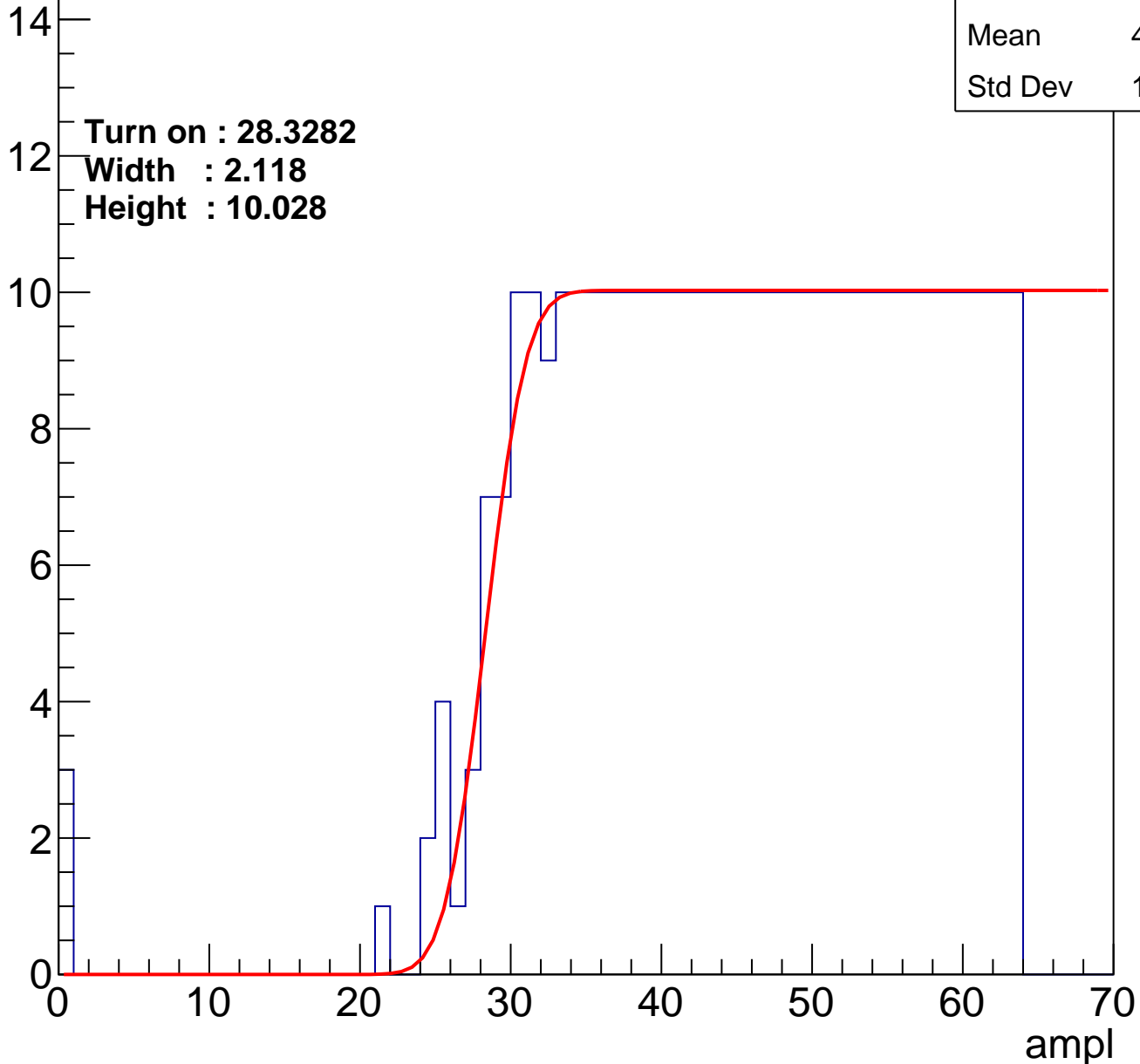
Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 28.3282

Width : 2.118

Height : 10.028

Entry



B0L002S, U14-ch62

calib_packv5_042523_0143.root, FC#8, port C1

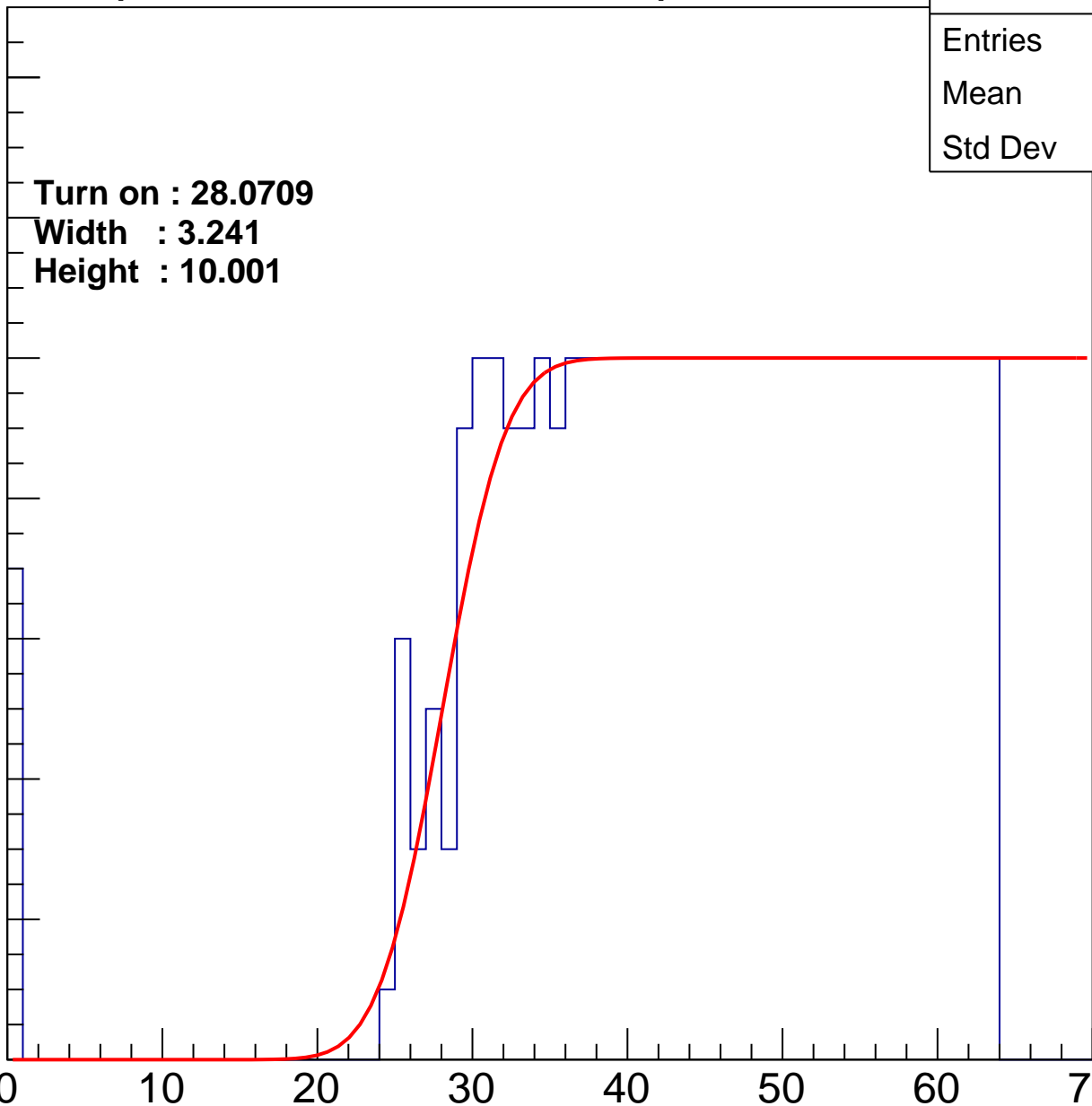
Entry

14
12
10
8
6
4
2
0

Turn on : 28.0709
Width : 3.241
Height : 10.001

Entries	371
Mean	44.32
Std Dev	12.26

ampl



B0L002S, U14-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.7
Std Dev	11.95

Turn on : 28.2341

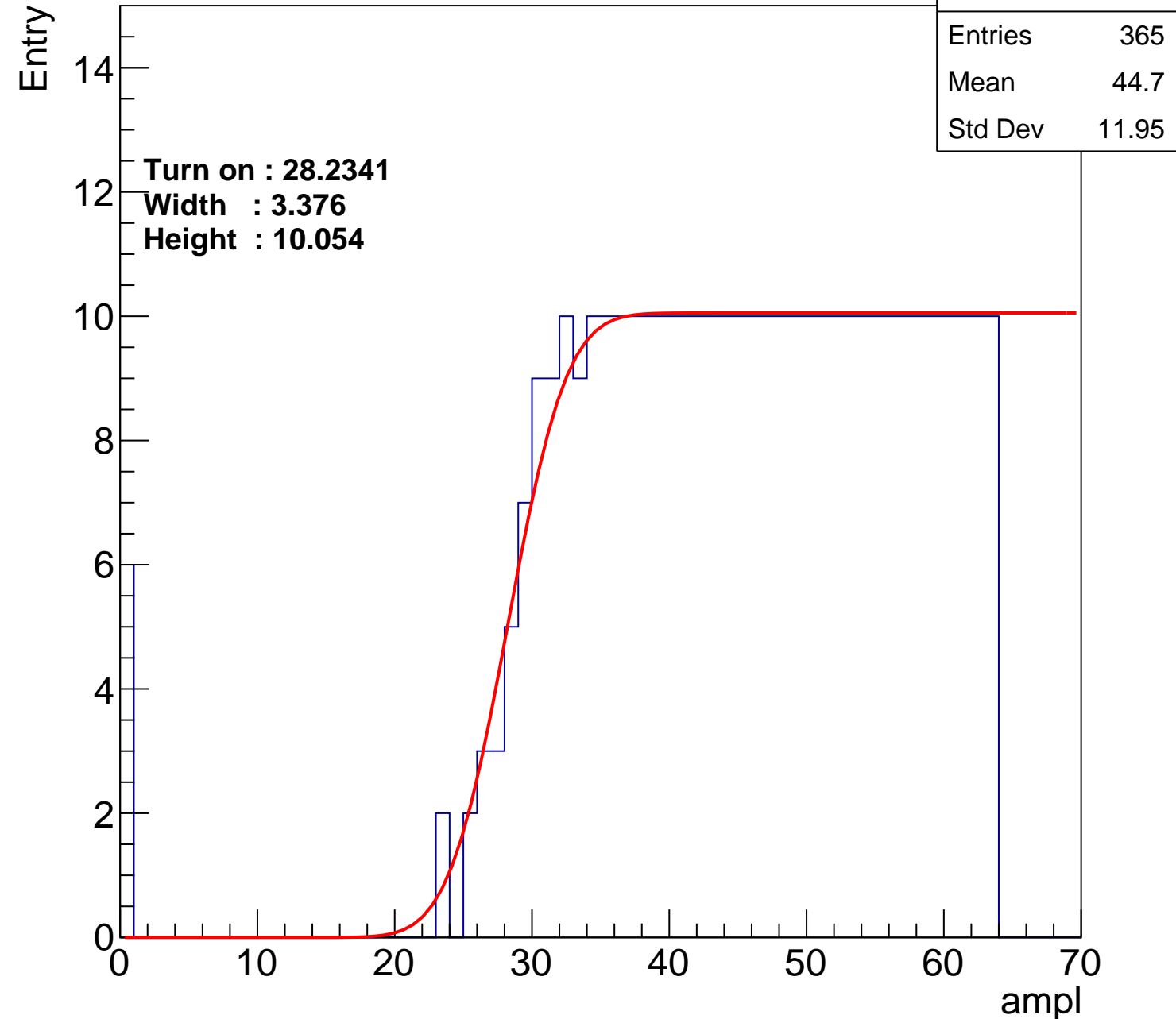
Width : 3.376

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.84
Std Dev	11.51

Turn on : 28.1121

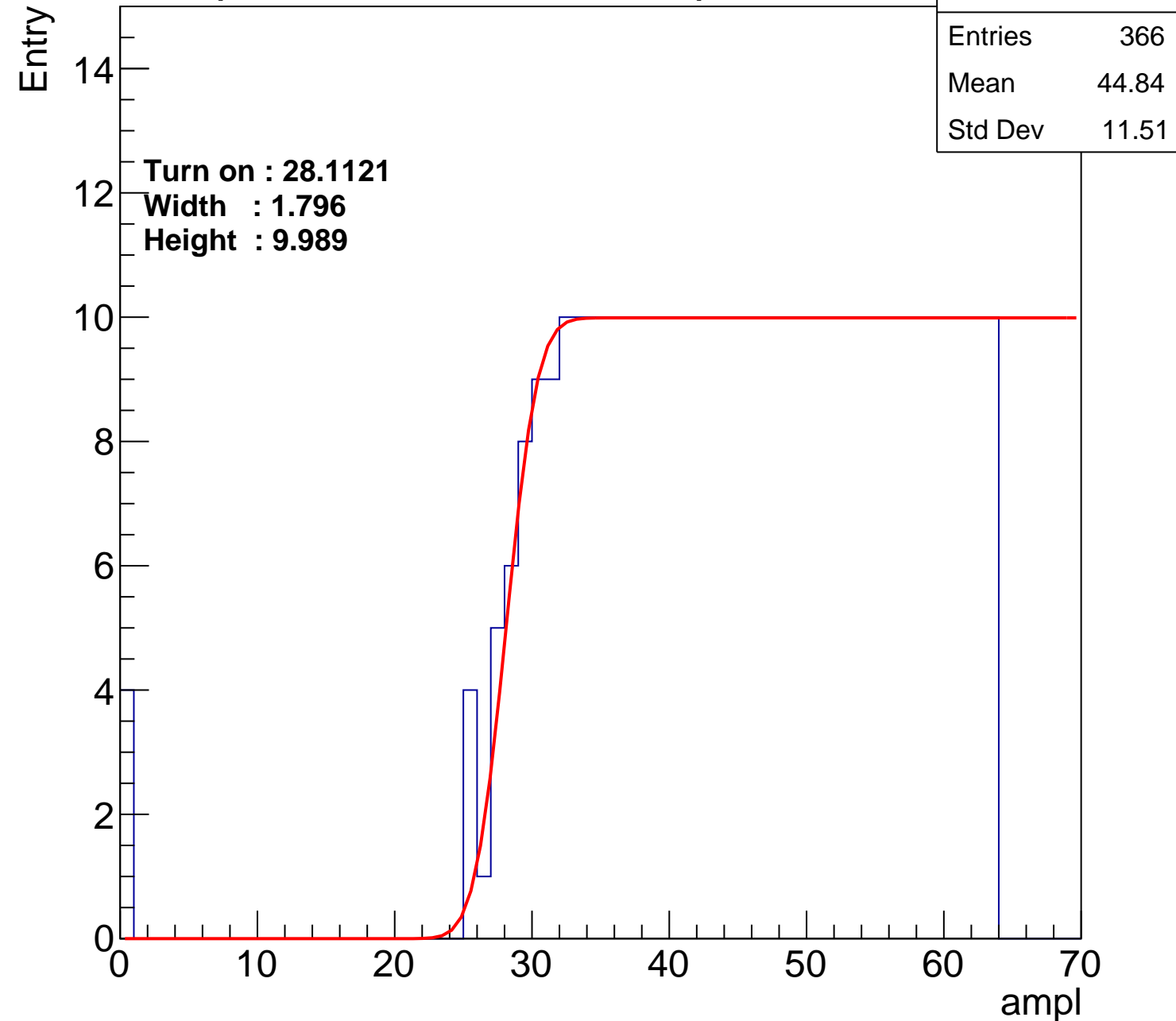
Width : 1.796

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch65

calib_packv5_042523_0143.root, FC#8, port C1

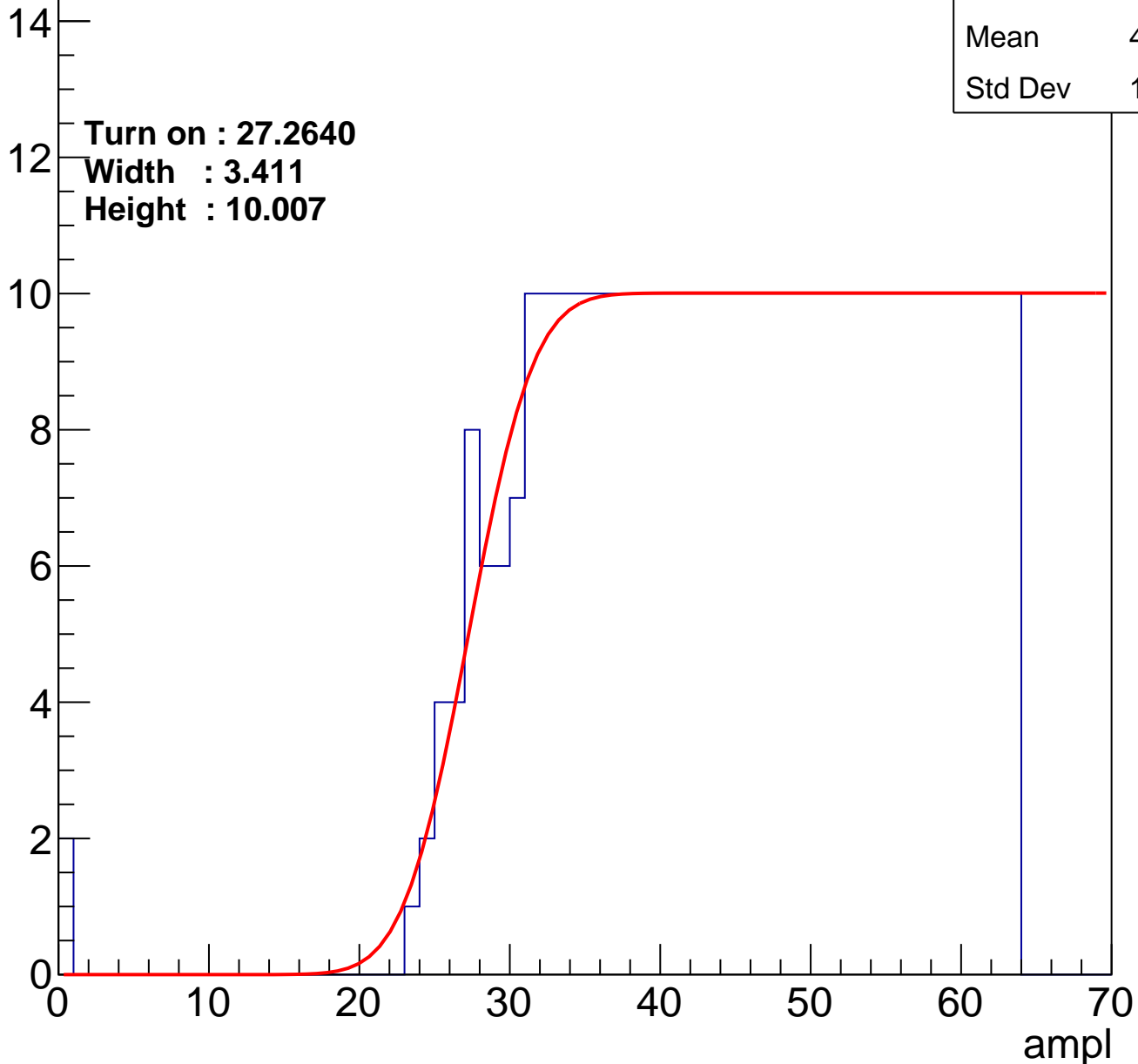
Entries	370
Mean	44.74
Std Dev	11.28

Turn on : 27.2640

Width : 3.411

Height : 10.007

Entry



B0L002S, U14-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 26.8502

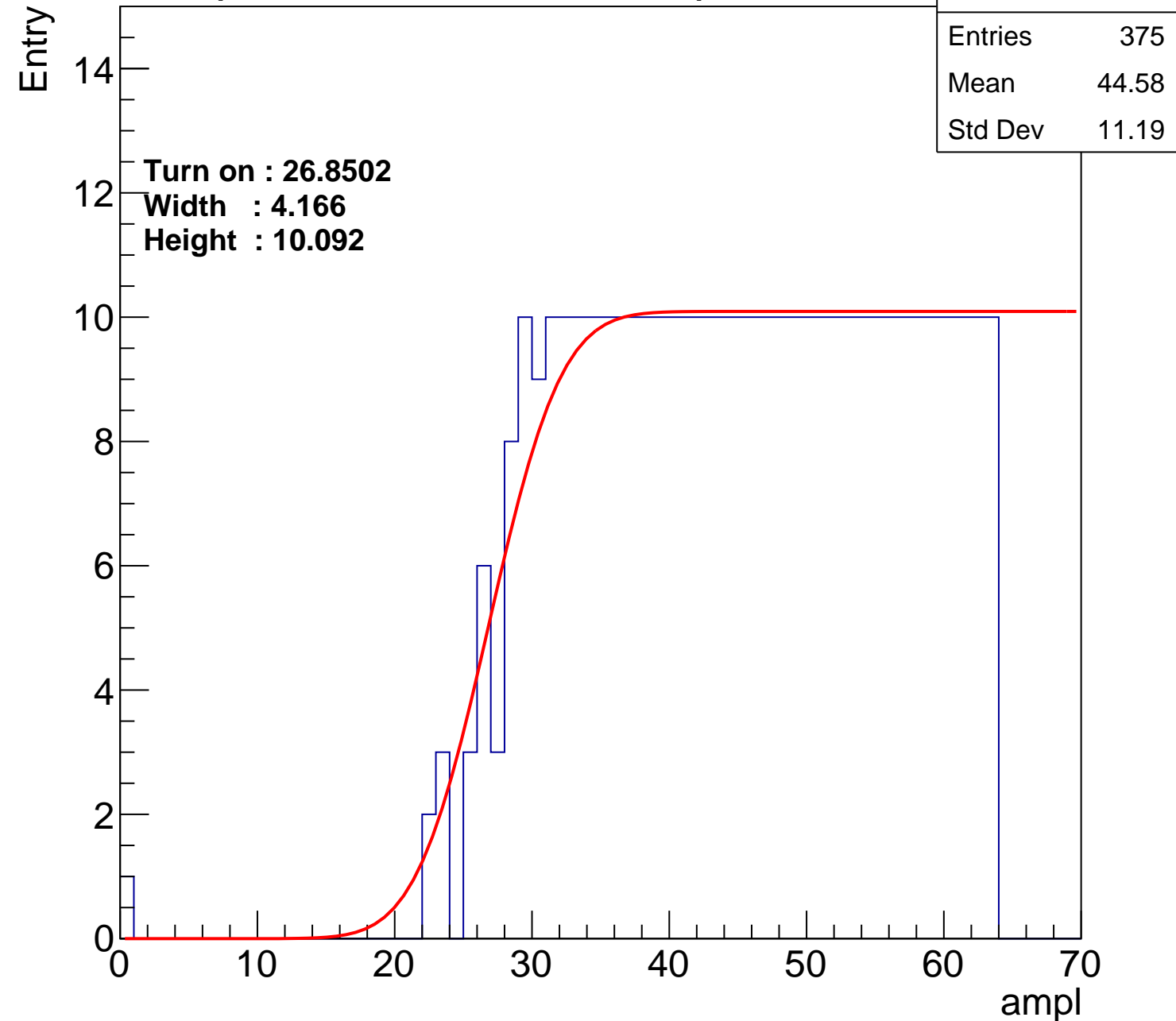
Width : 4.166

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch67

calib_packv5_042523_0143.root, FC#8, port C1

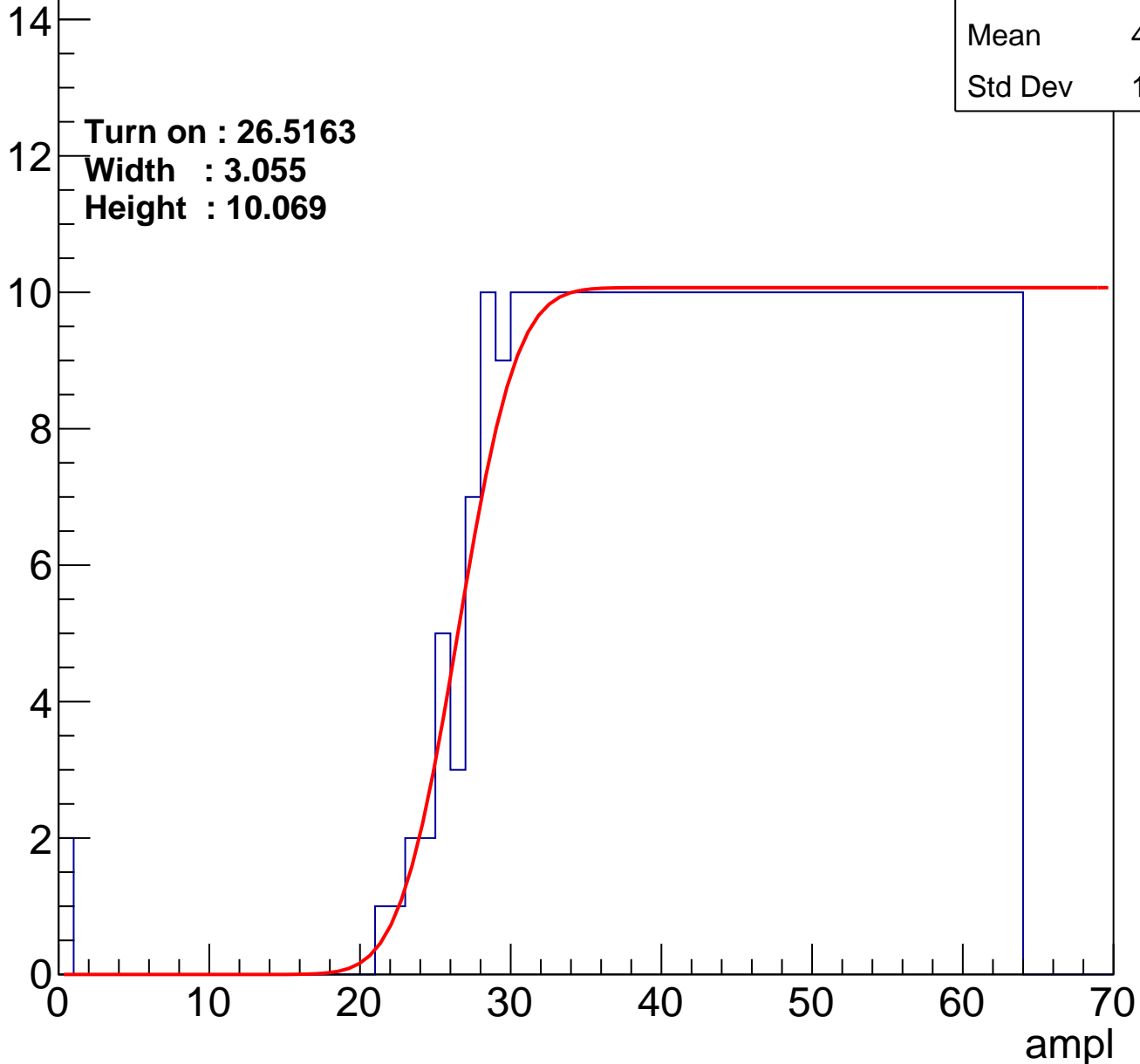
Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.5163

Width : 3.055

Height : 10.069

Entry



B0L002S, U14-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.28
Std Dev	11.37

Turn on : 26.5833

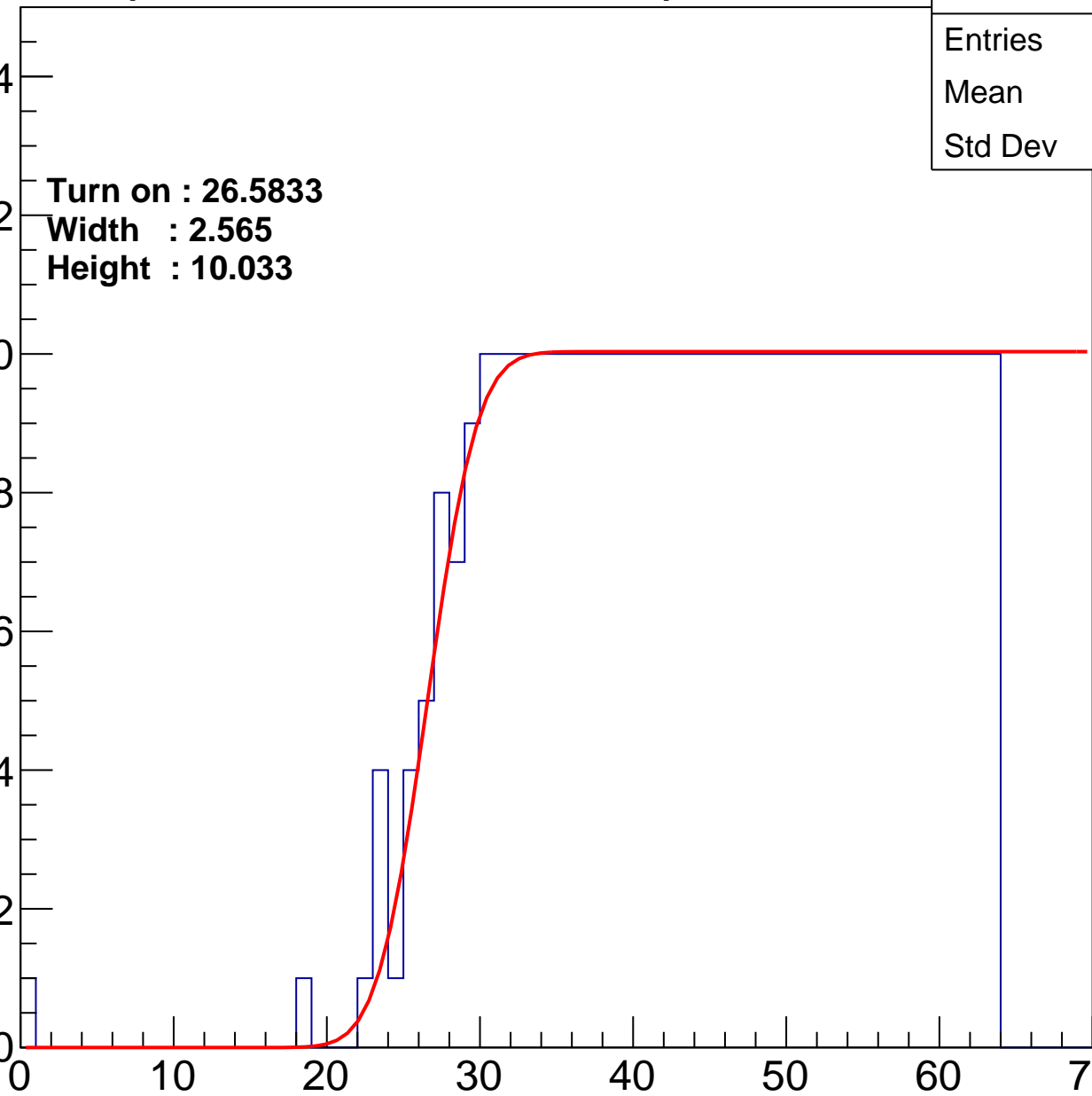
Width : 2.565

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.42
Std Dev	10.73

Turn on : 28.4310

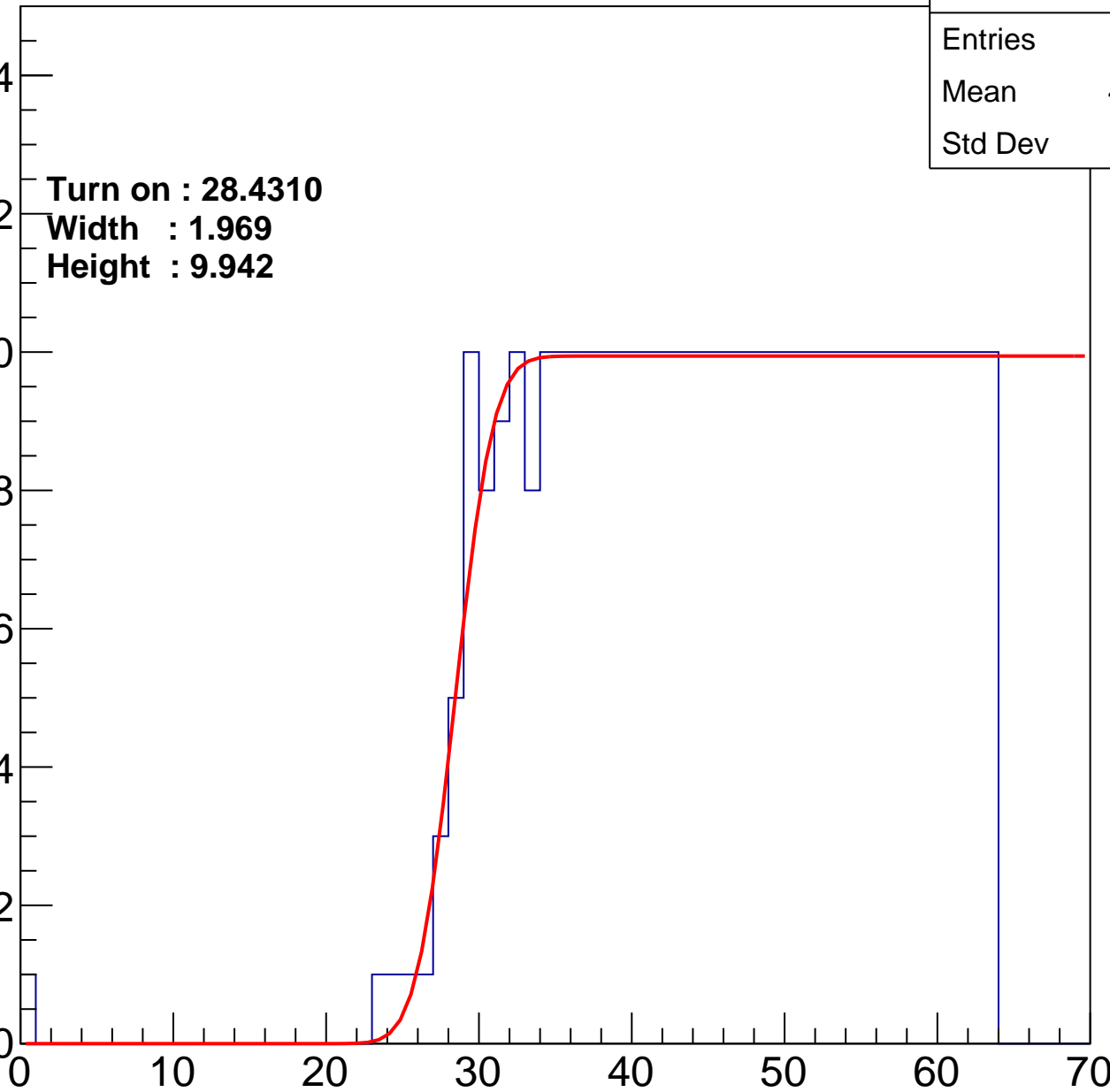
Width : 1.969

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	391
Mean	43.63
Std Dev	12.08

Turn on : 25.4197

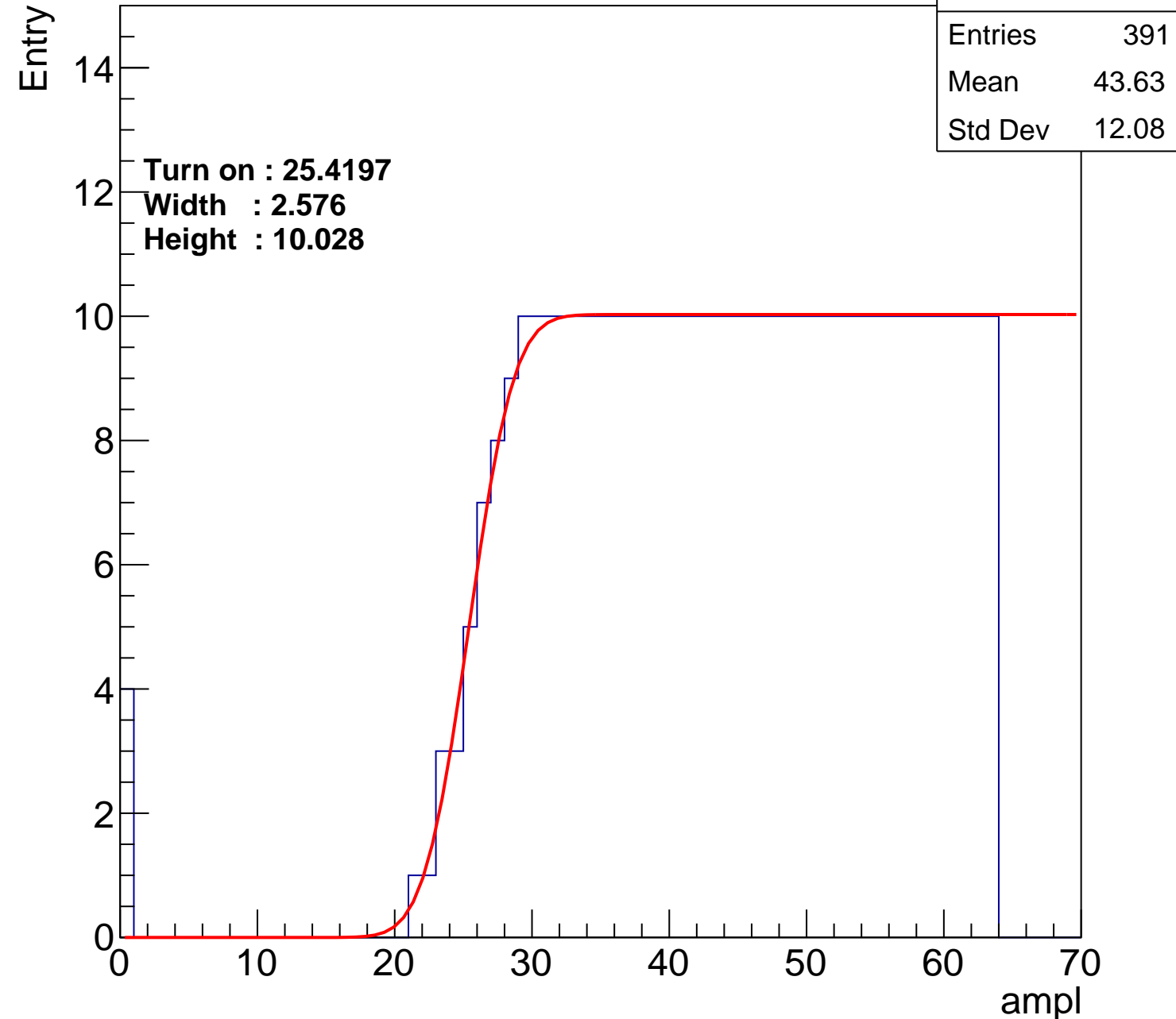
Width : 2.576

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.85
Std Dev	11.76

Turn on : 28.2065

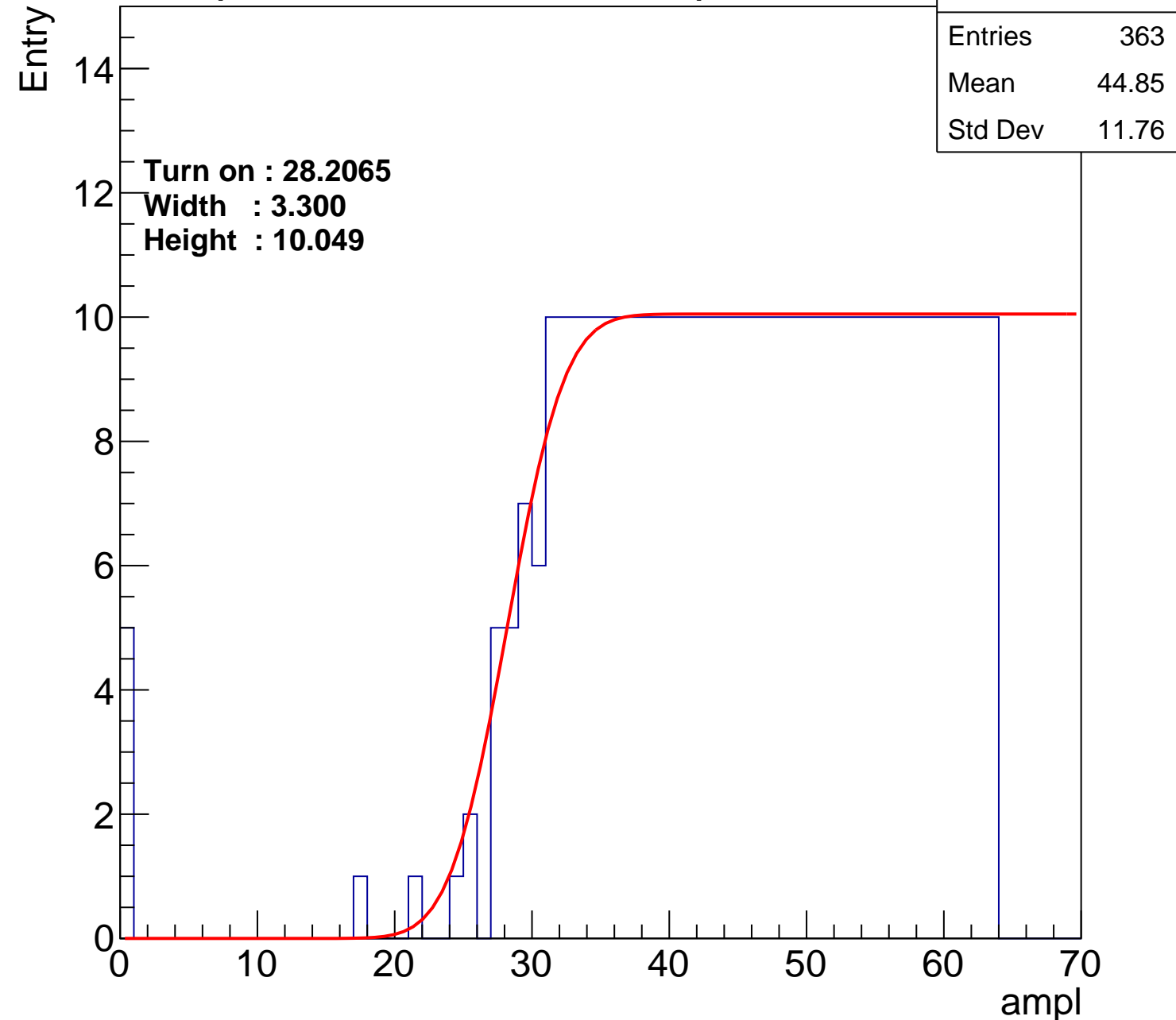
Width : 3.300

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.67
Std Dev	11.47

Mean	44.67
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Std Dev	11.47
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Width : 2.819

Height : 9.973

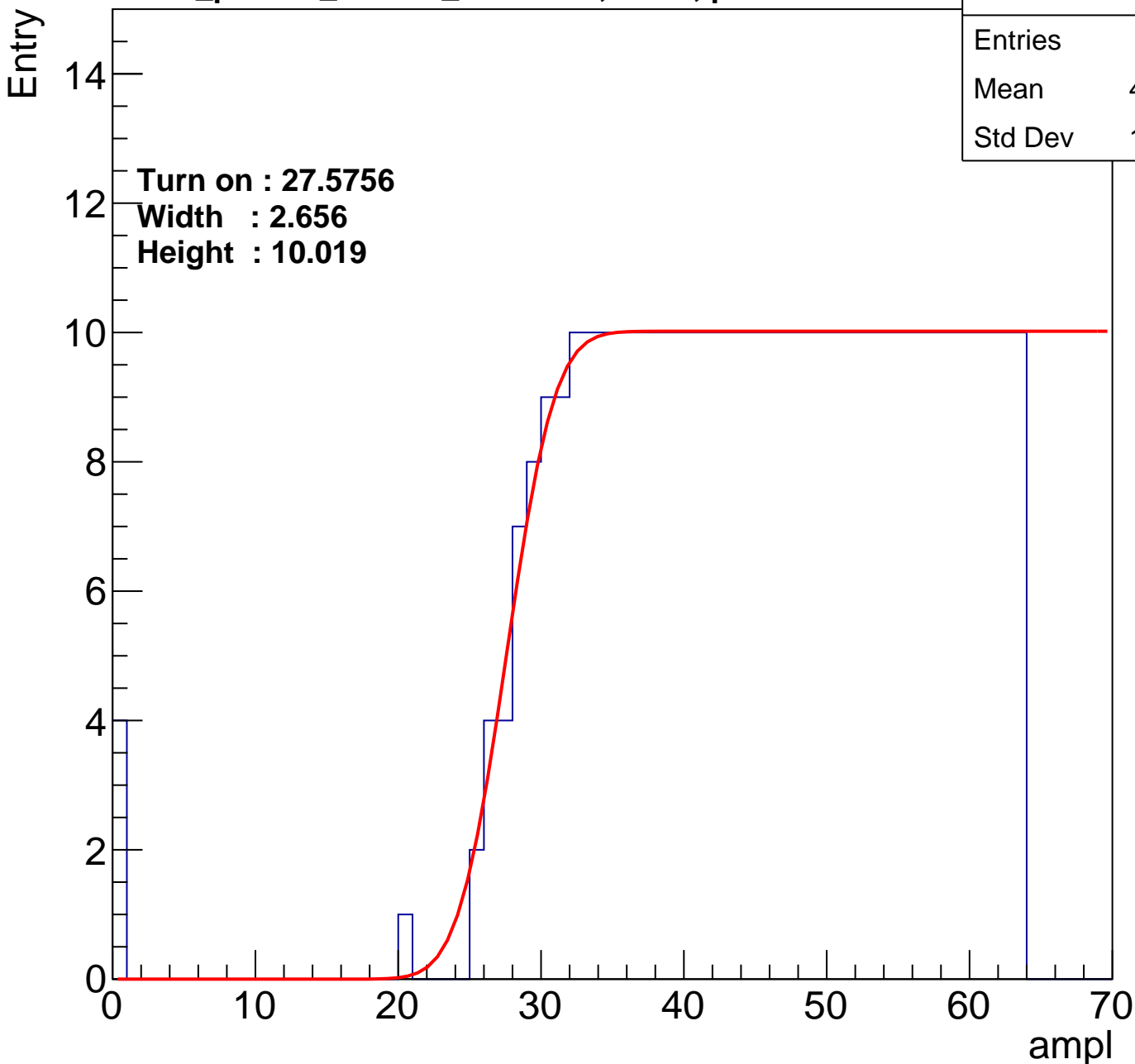


calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.73
Std Dev	11.58

Std Dev	11.58
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Height : 10.019



B0L002S, U14-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.61
Std Dev	11.67

Turn on : 27.8963

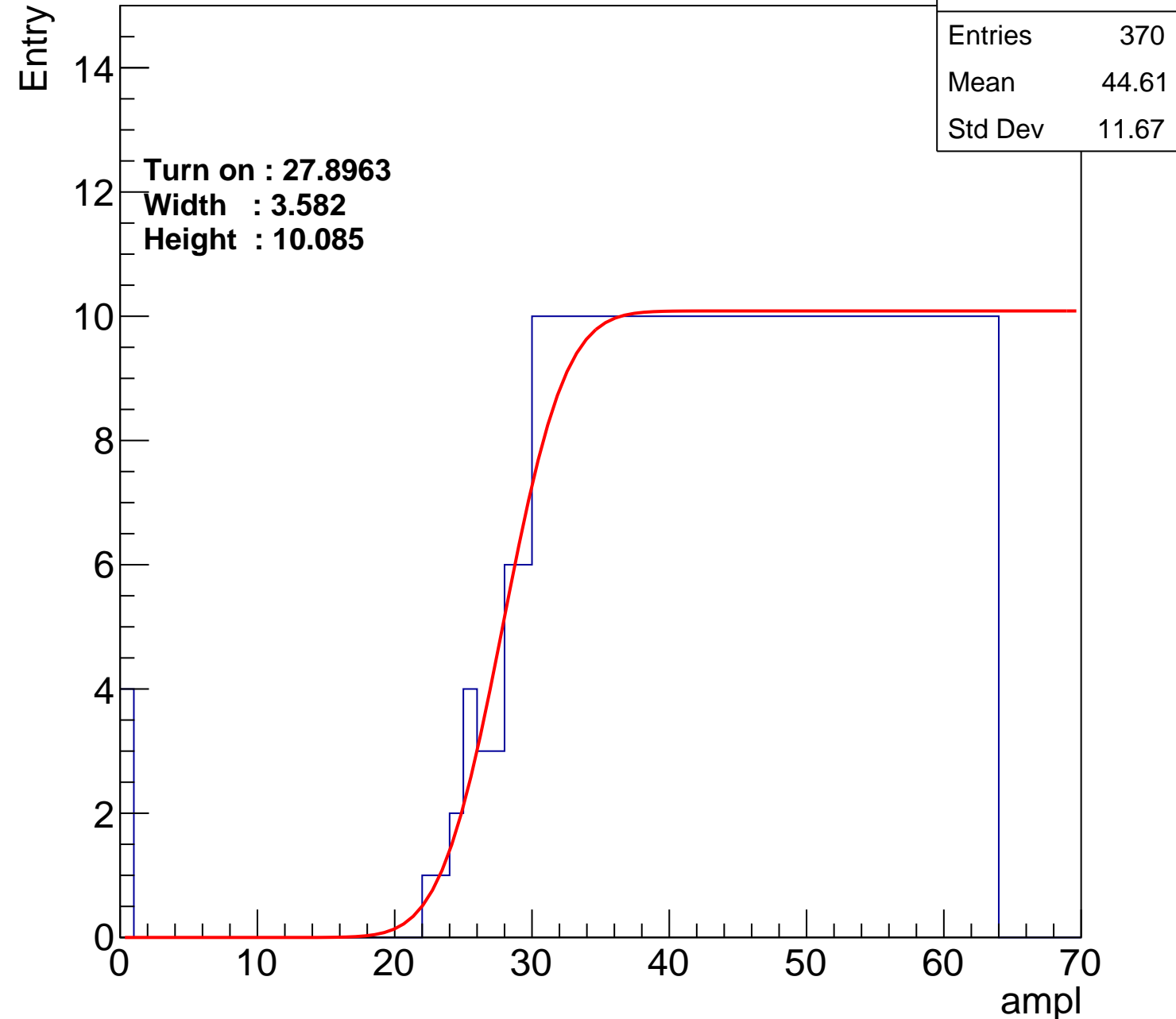
Width : 3.582

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch75

calib_packv5_042523_0143.root, FC#8, port C1

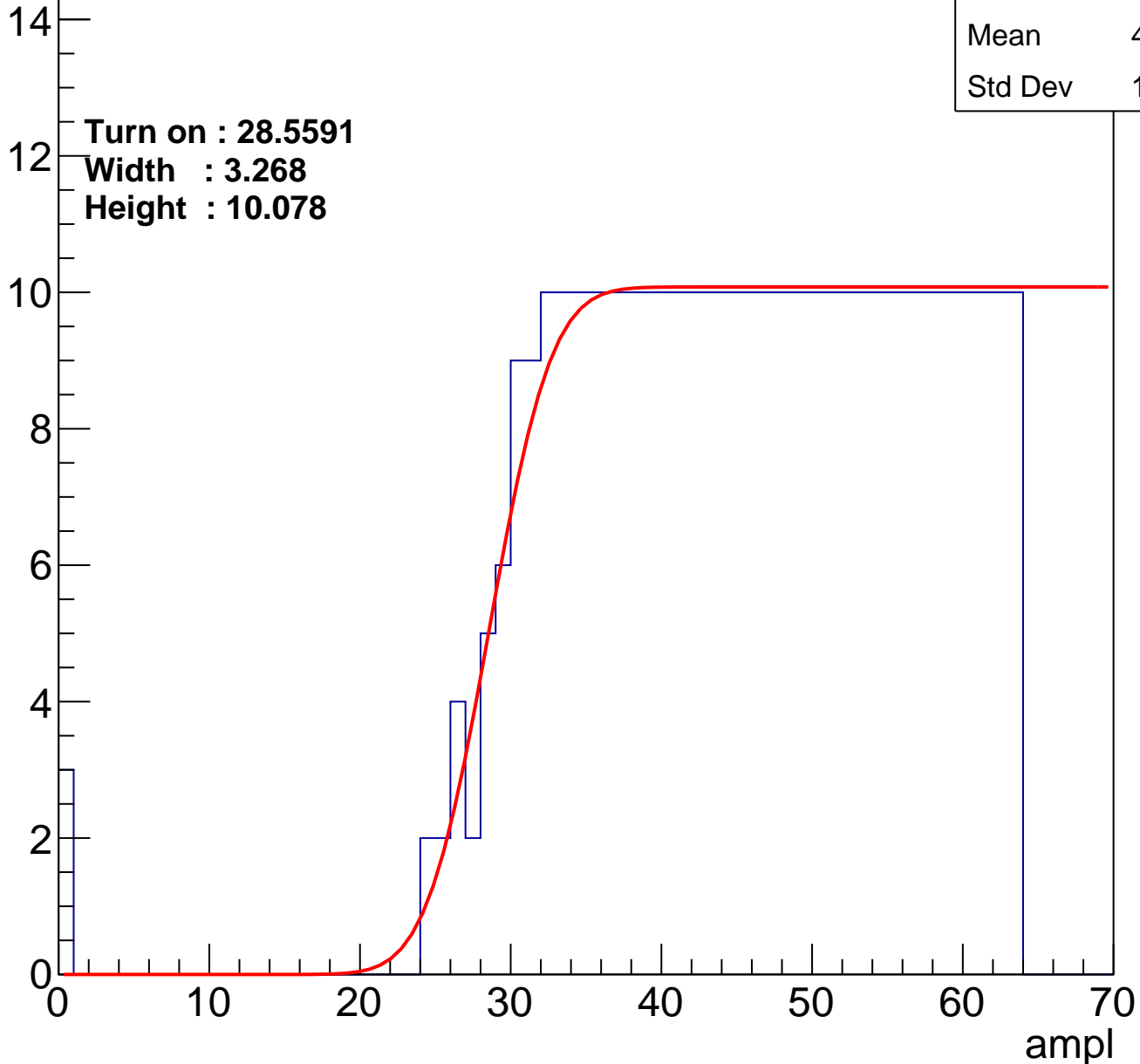
Entries	362
Mean	45.08
Std Dev	11.26

Turn on : 28.5591

Width : 3.268

Height : 10.078

Entry



B0L002S, U14-ch76

calib_packv5_042523_0143.root, FC#8, port C1

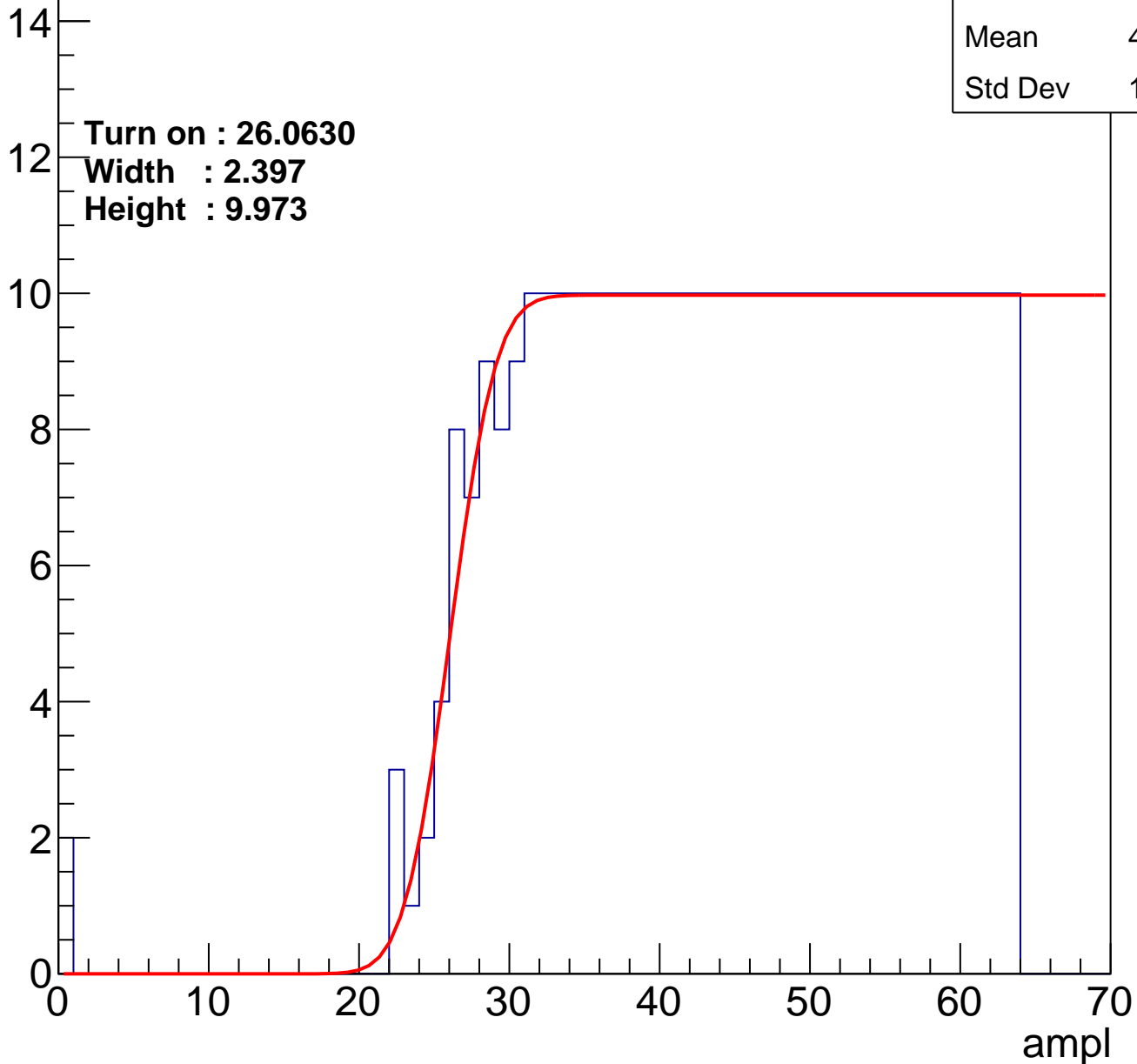
Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.0630

Width : 2.397

Height : 9.973

Entry



B0L002S, U14-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.13
Std Dev	10.88

Turn on : 28.2264

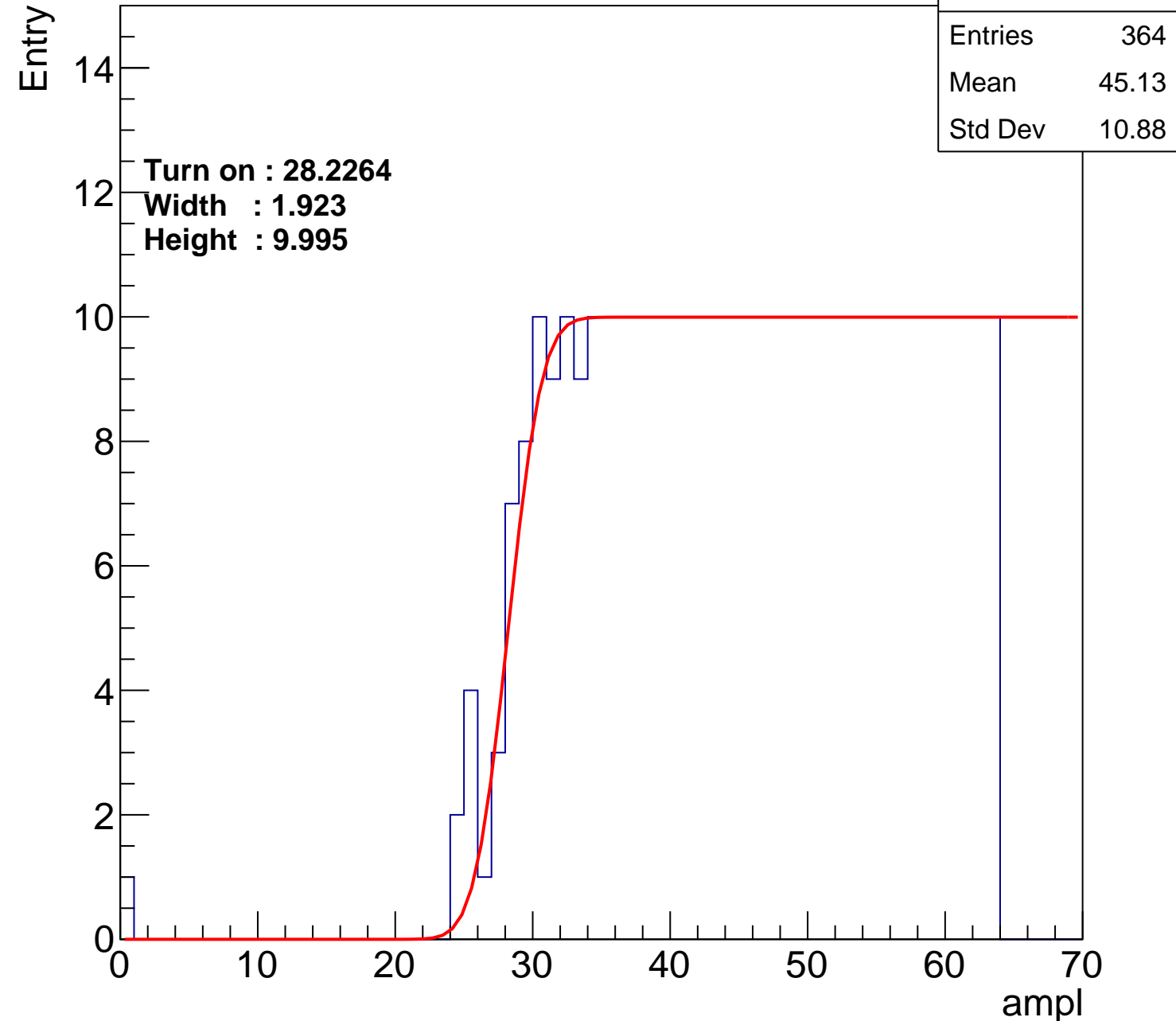
Width : 1.923

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.03
Std Dev	11.96

Turn on : 26.1776

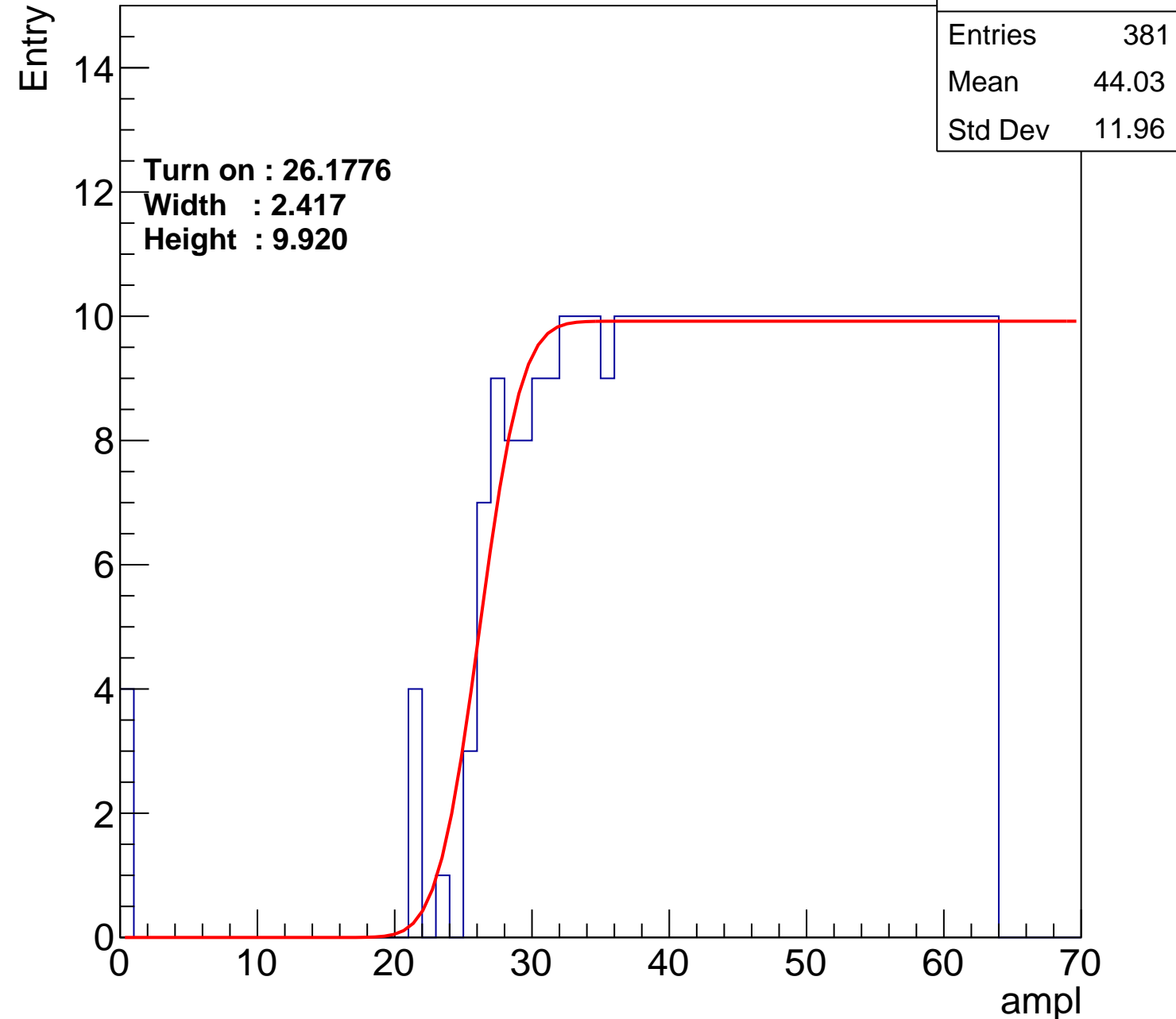
Width : 2.417

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch79

calib_packv5_042523_0143.root, FC#8, port C1

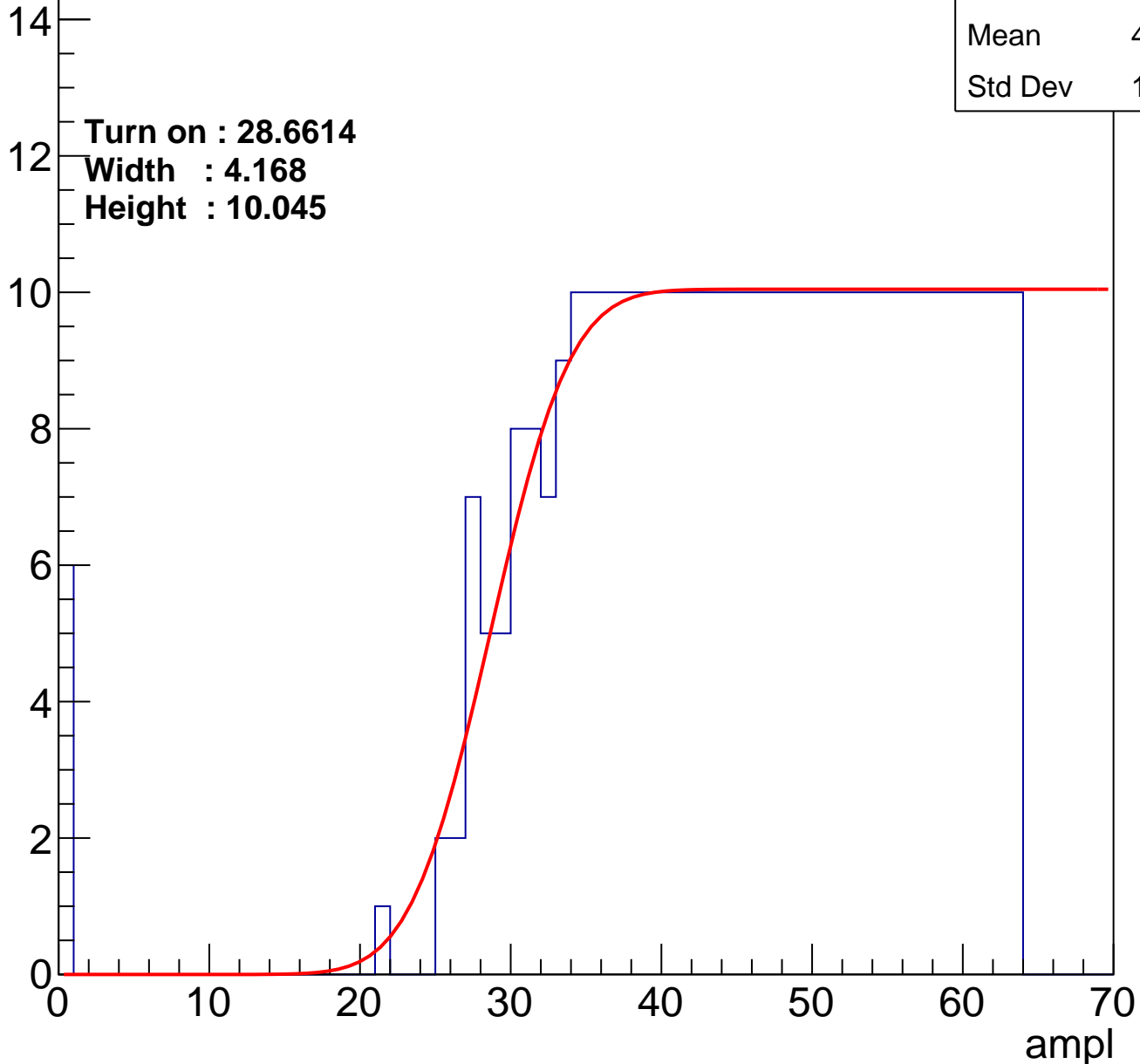
Entries	360
Mean	44.88
Std Dev	11.93

Turn on : 28.6614

Width : 4.168

Height : 10.045

Entry



B0L002S, U14-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.62
Std Dev	11.63

Turn on : 27.3877

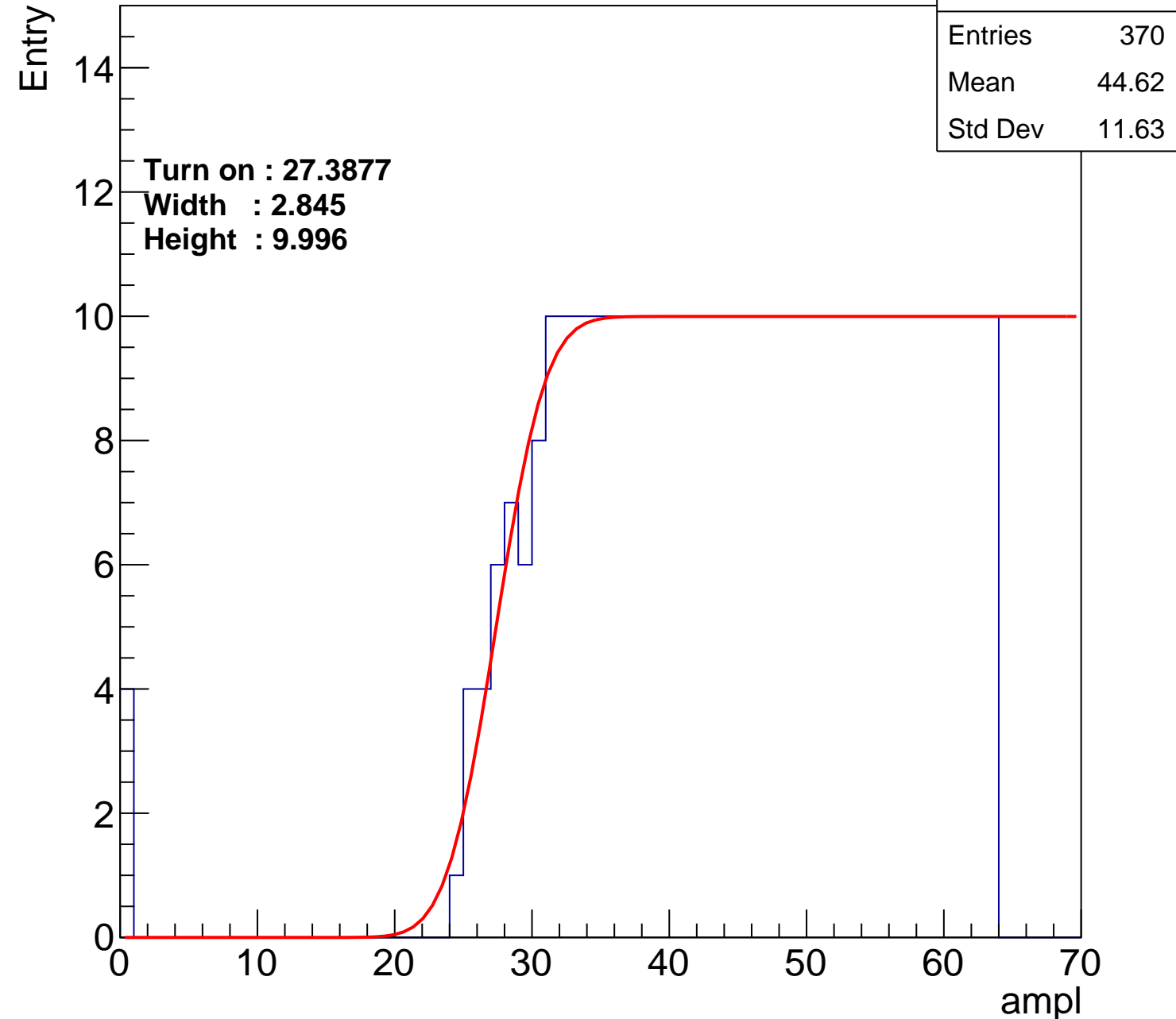
Width : 2.845

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.25
Std Dev	10.31

Turn on : 30.1634

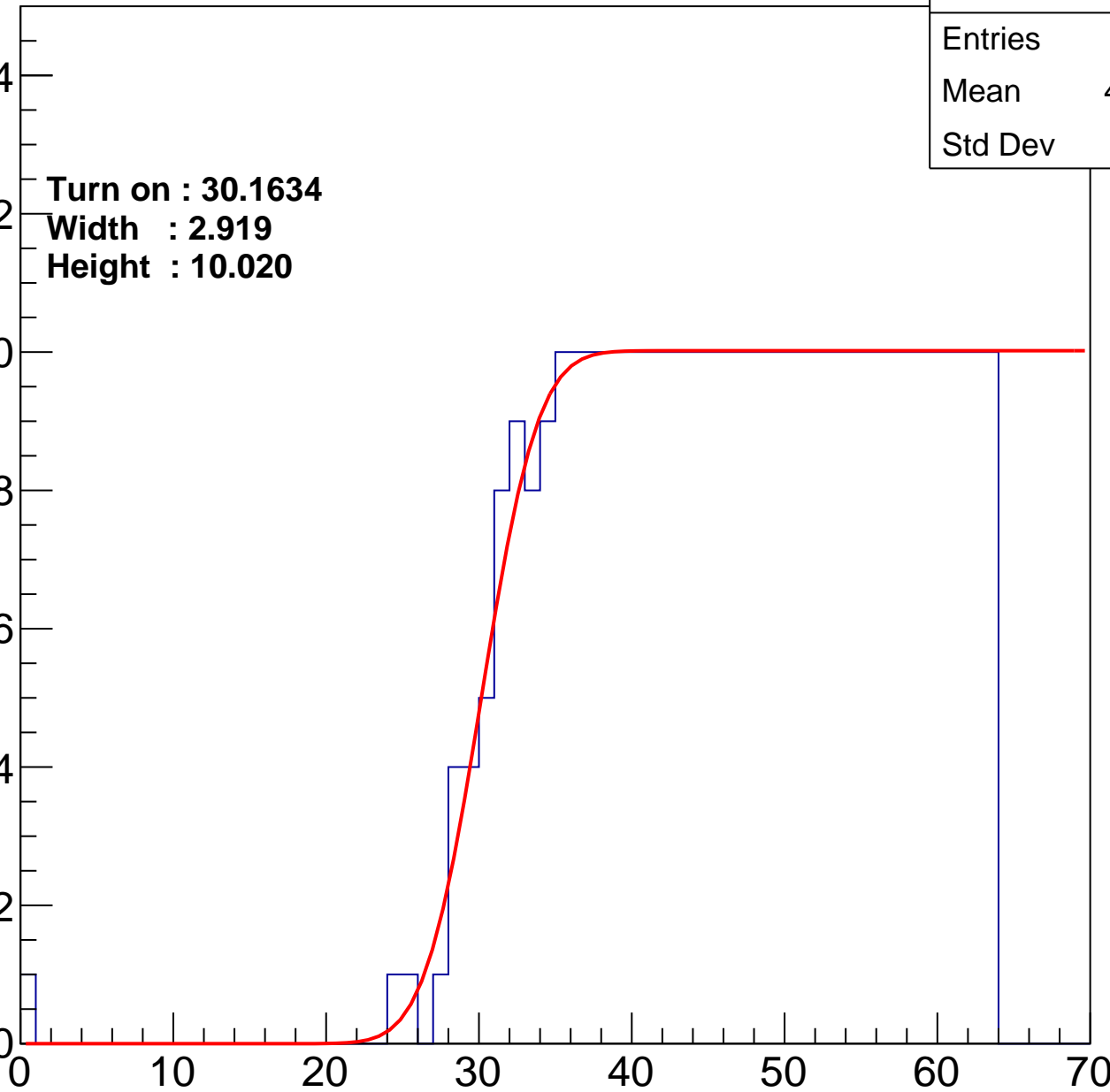
Width : 2.919

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.29
Std Dev	10.98

Turn on : 28.2827

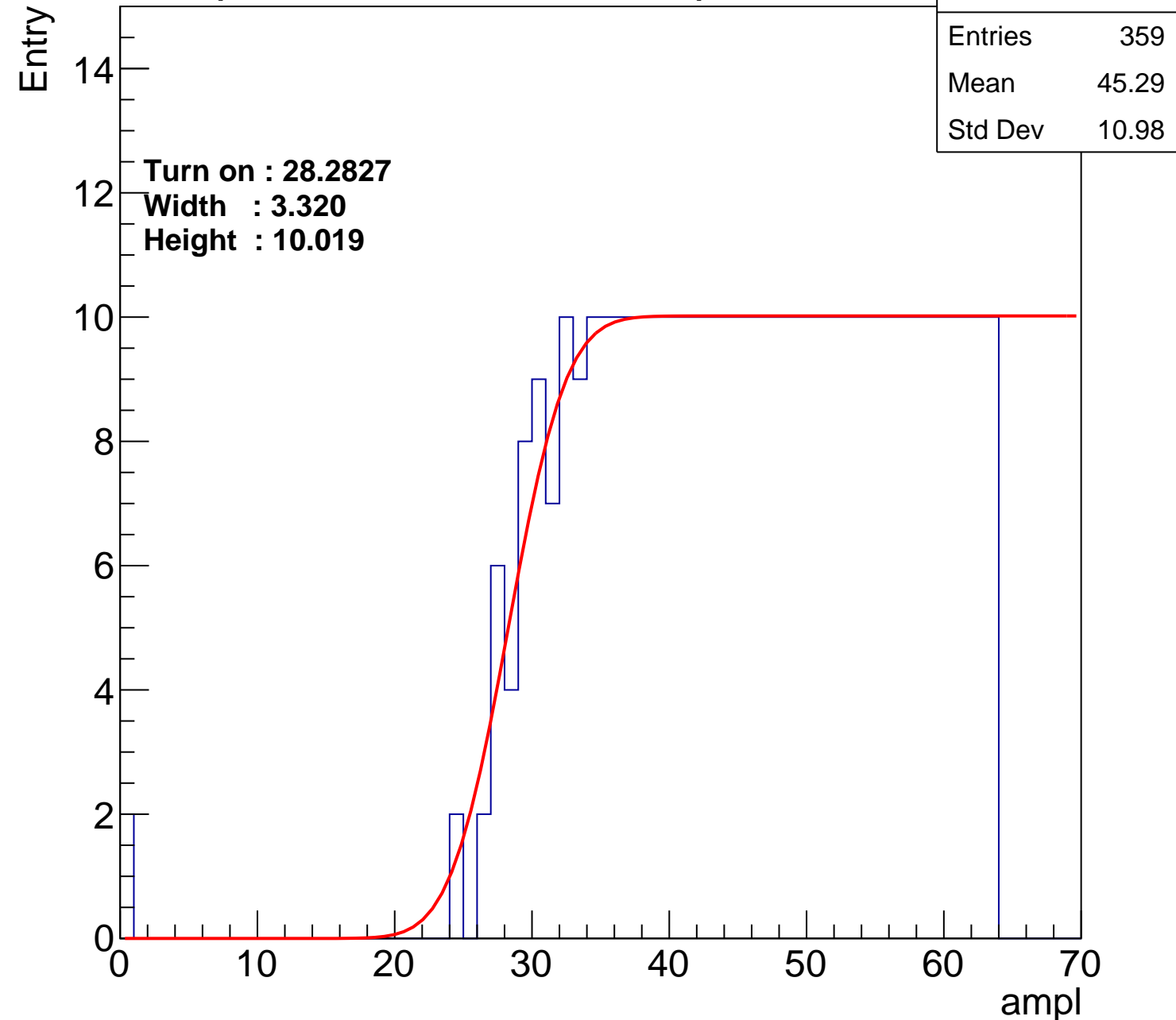
Width : 3.320

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.2
Std Dev	10.86

Turn on : 27.7776

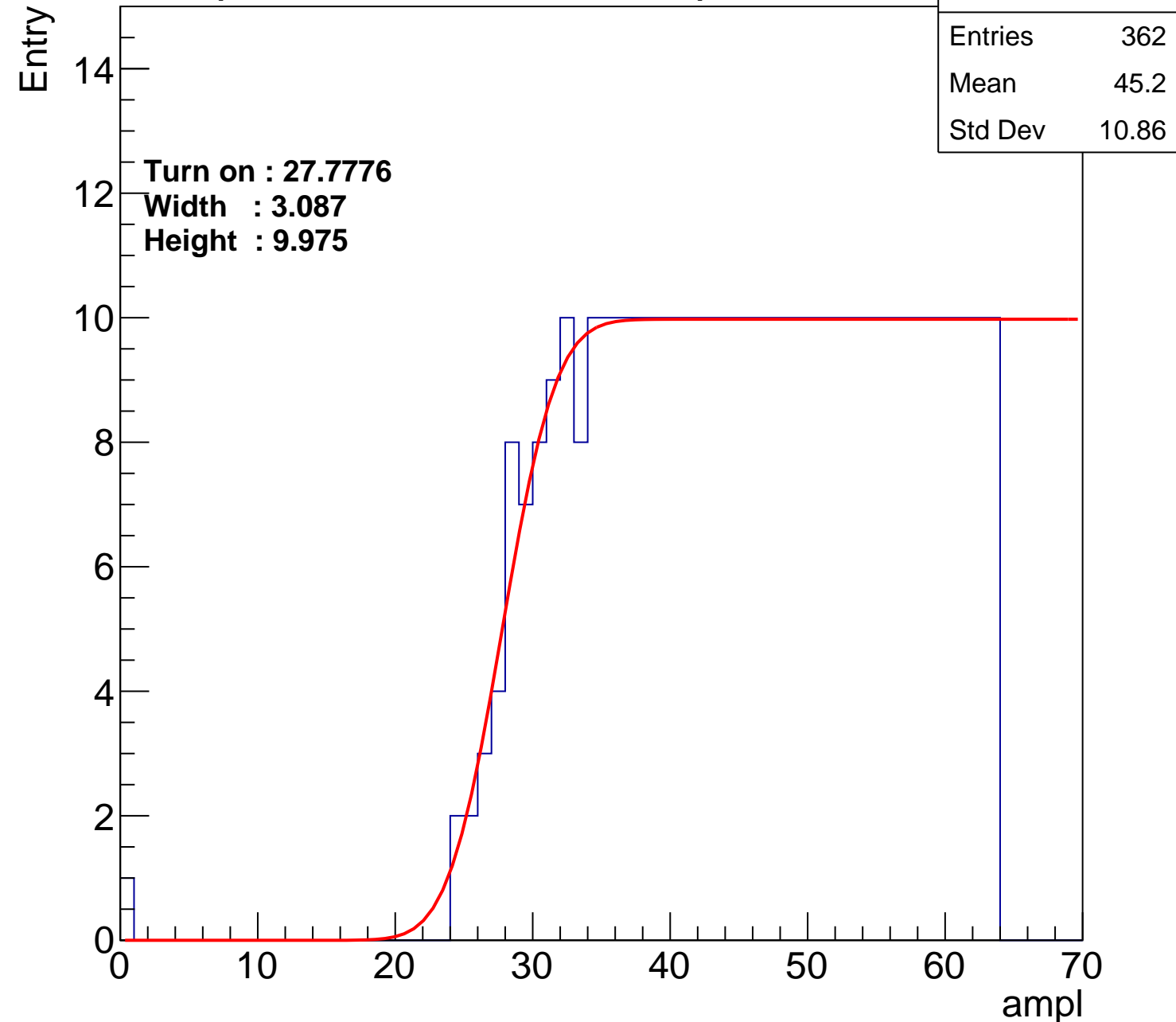
Width : 3.087

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.66
Std Dev	11.58

Turn on : 27.8202

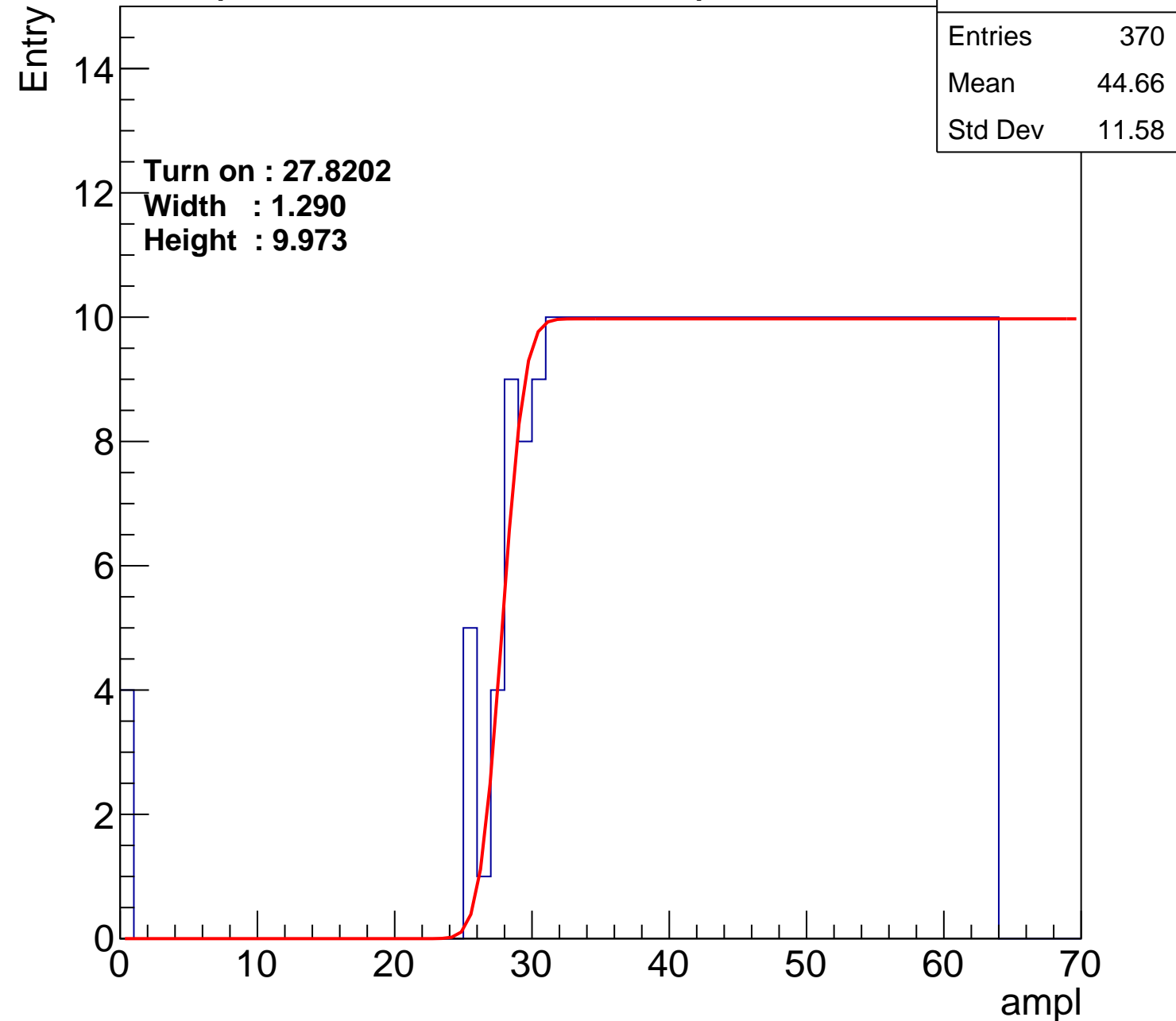
Width : 1.290

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.77
Std Dev	11.28

Turn on : 27.3389

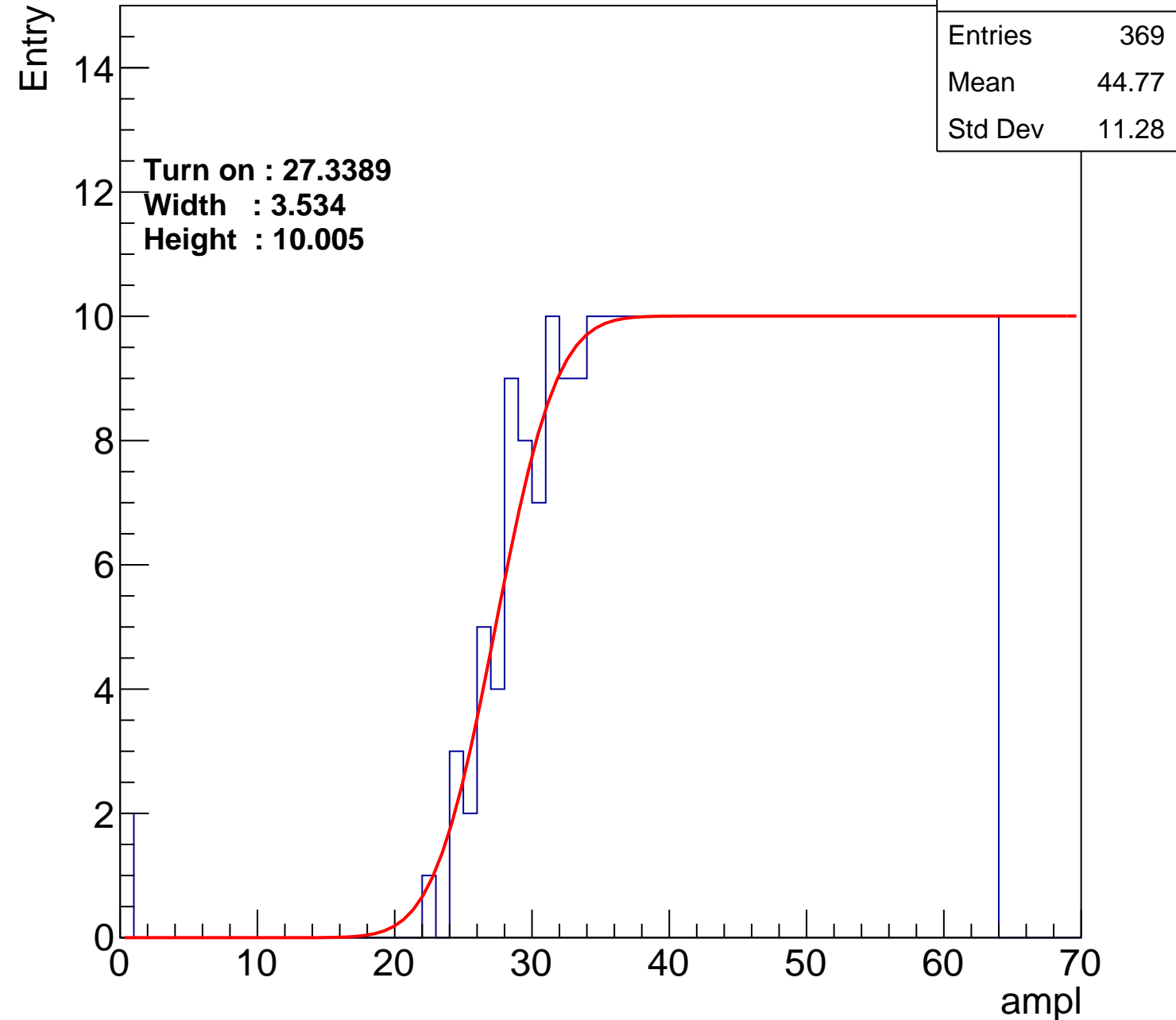
Width : 3.534

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.03
Std Dev	10.95

Turn on : 28.0138

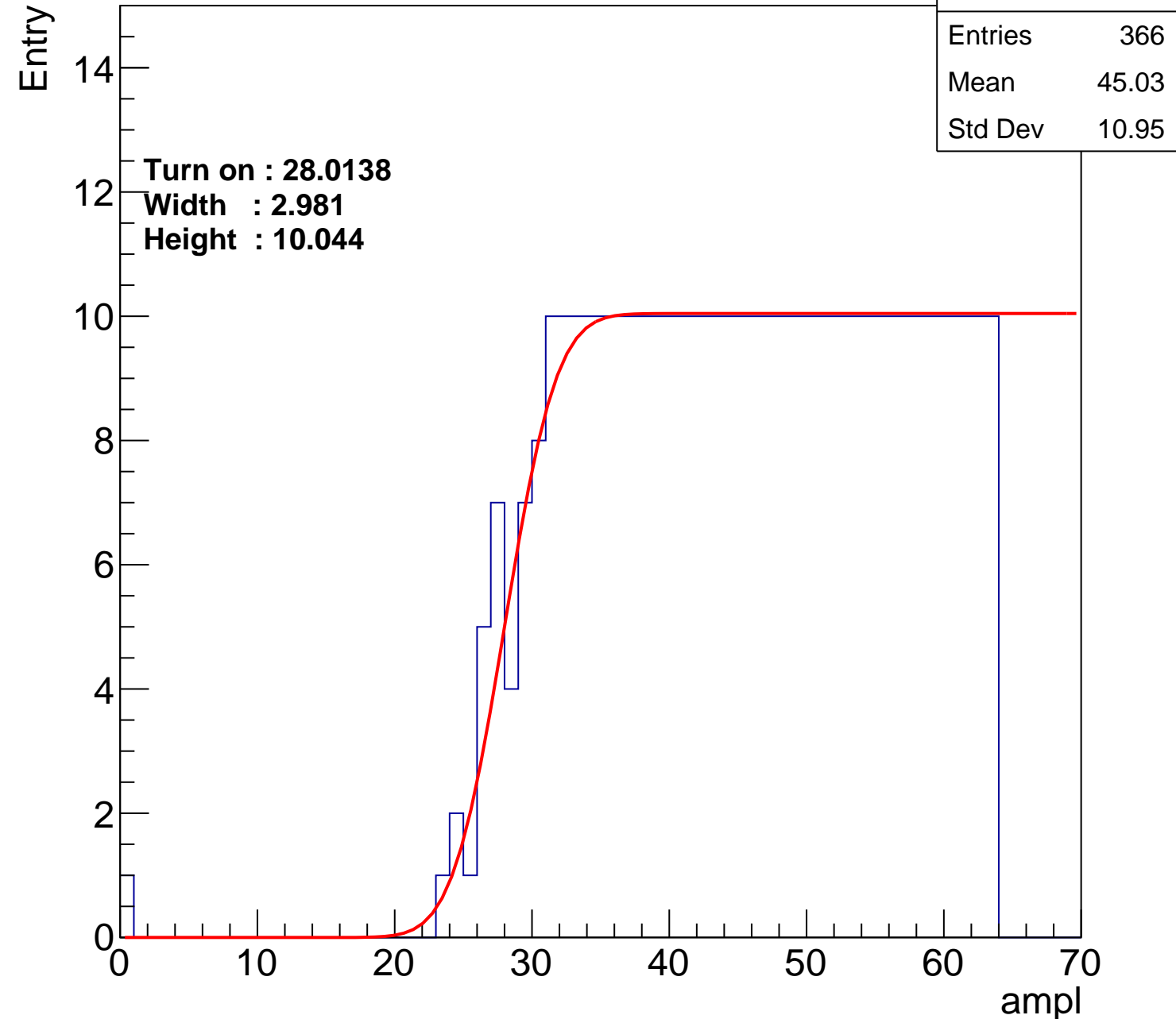
Width : 2.981

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.67
Std Dev	10.6

Turn on : 29.0066

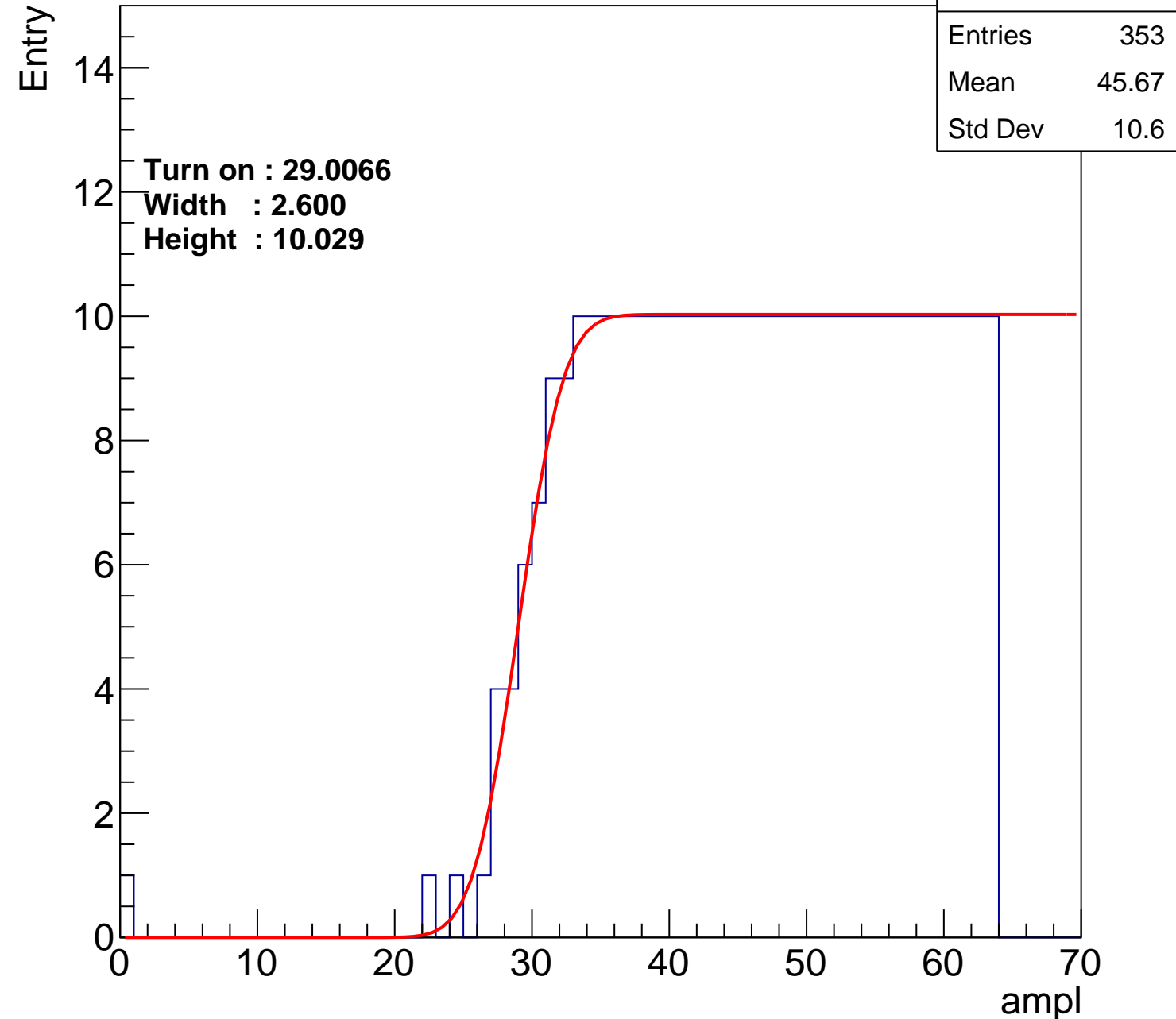
Width : 2.600

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch88

calib_packv5_042523_0143.root, FC#8, port C1

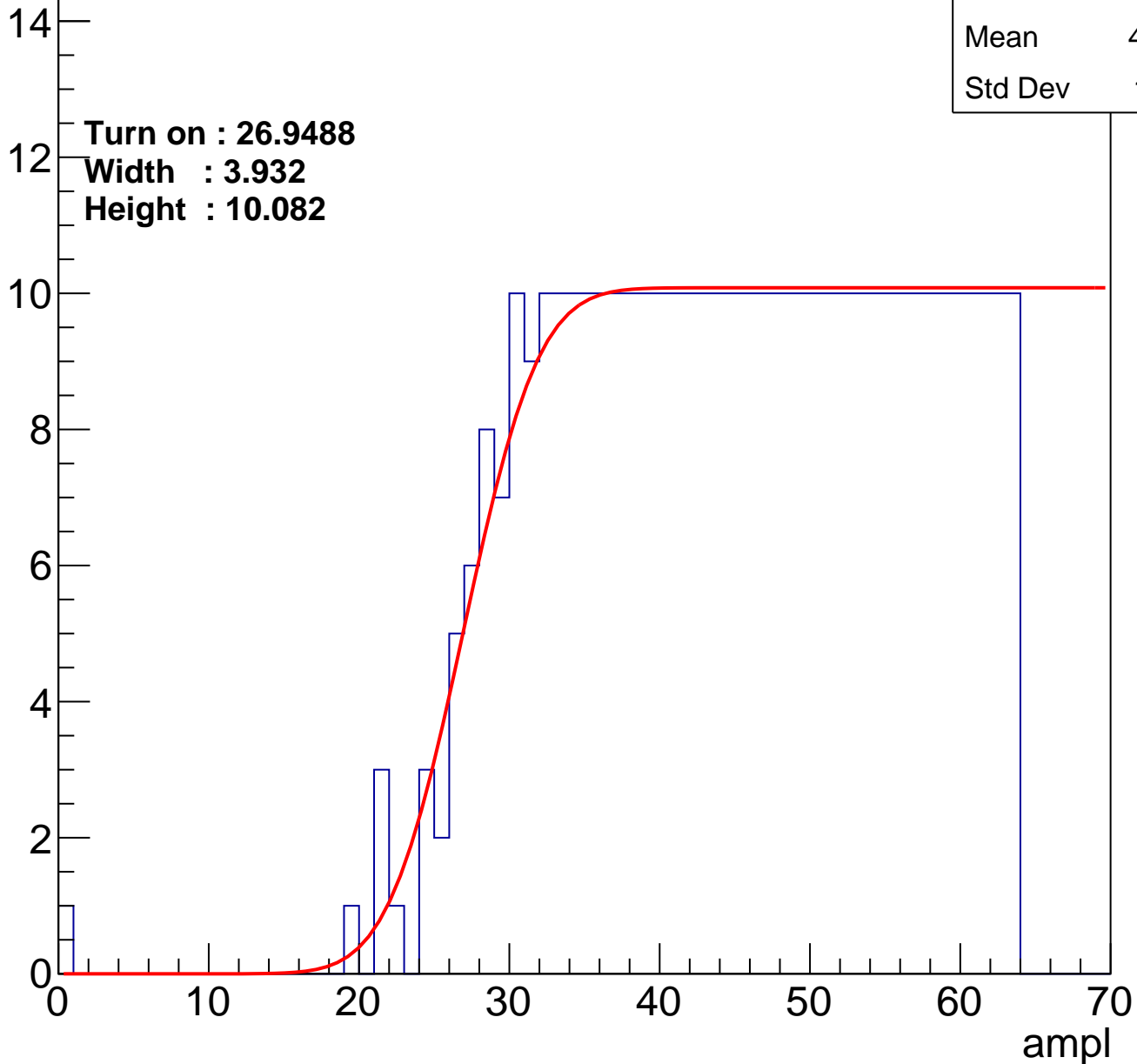
Entries	376
Mean	44.48
Std Dev	11.31

Turn on : 26.9488

Width : 3.932

Height : 10.082

Entry



B0L002S, U14-ch89

calib_packv5_042523_0143.root, FC#8, port C1

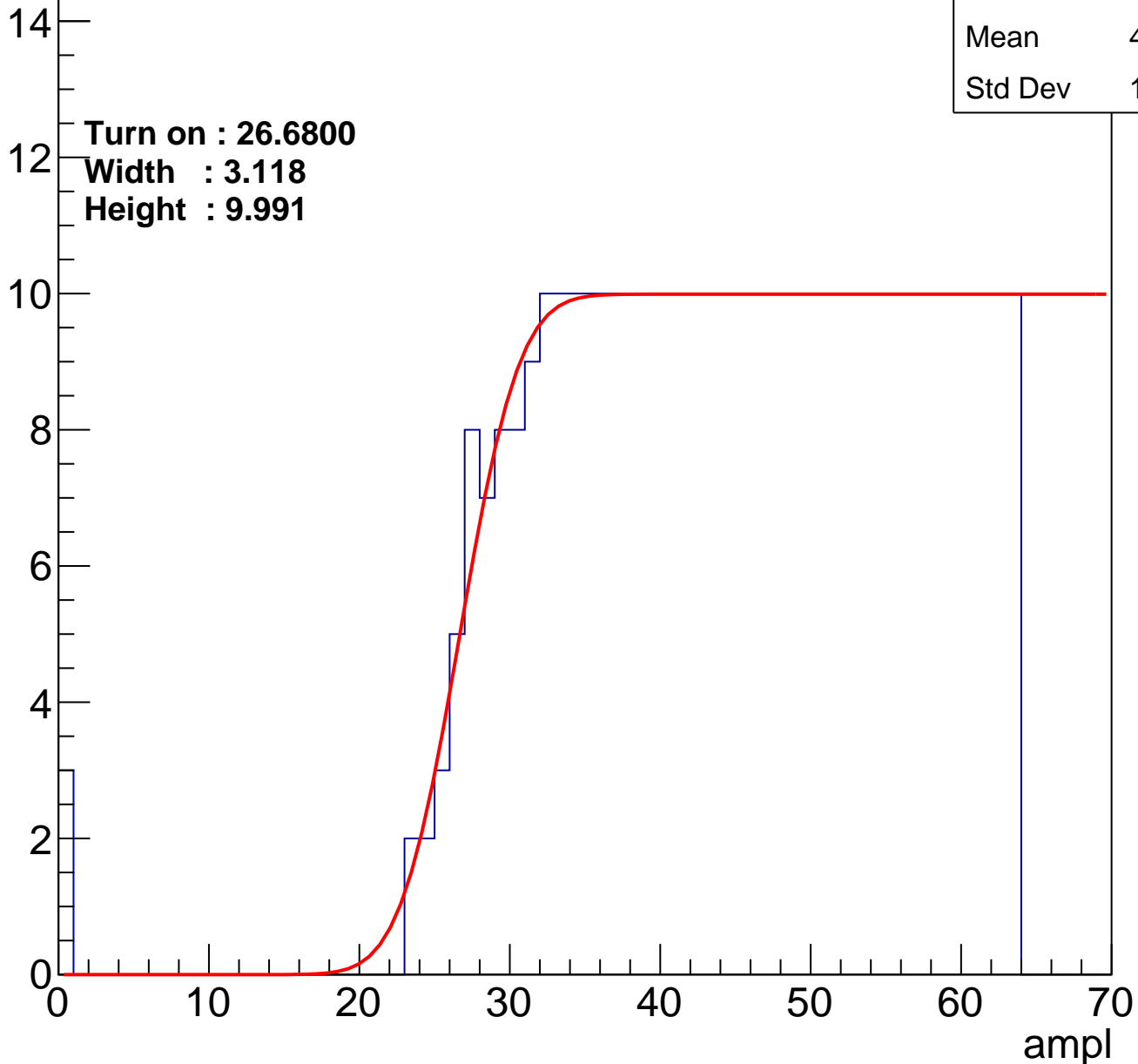
Entries	375
Mean	44.43
Std Dev	11.58

Turn on : 26.6800

Width : 3.118

Height : 9.991

Entry



B0L002S, U14-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 27.1134

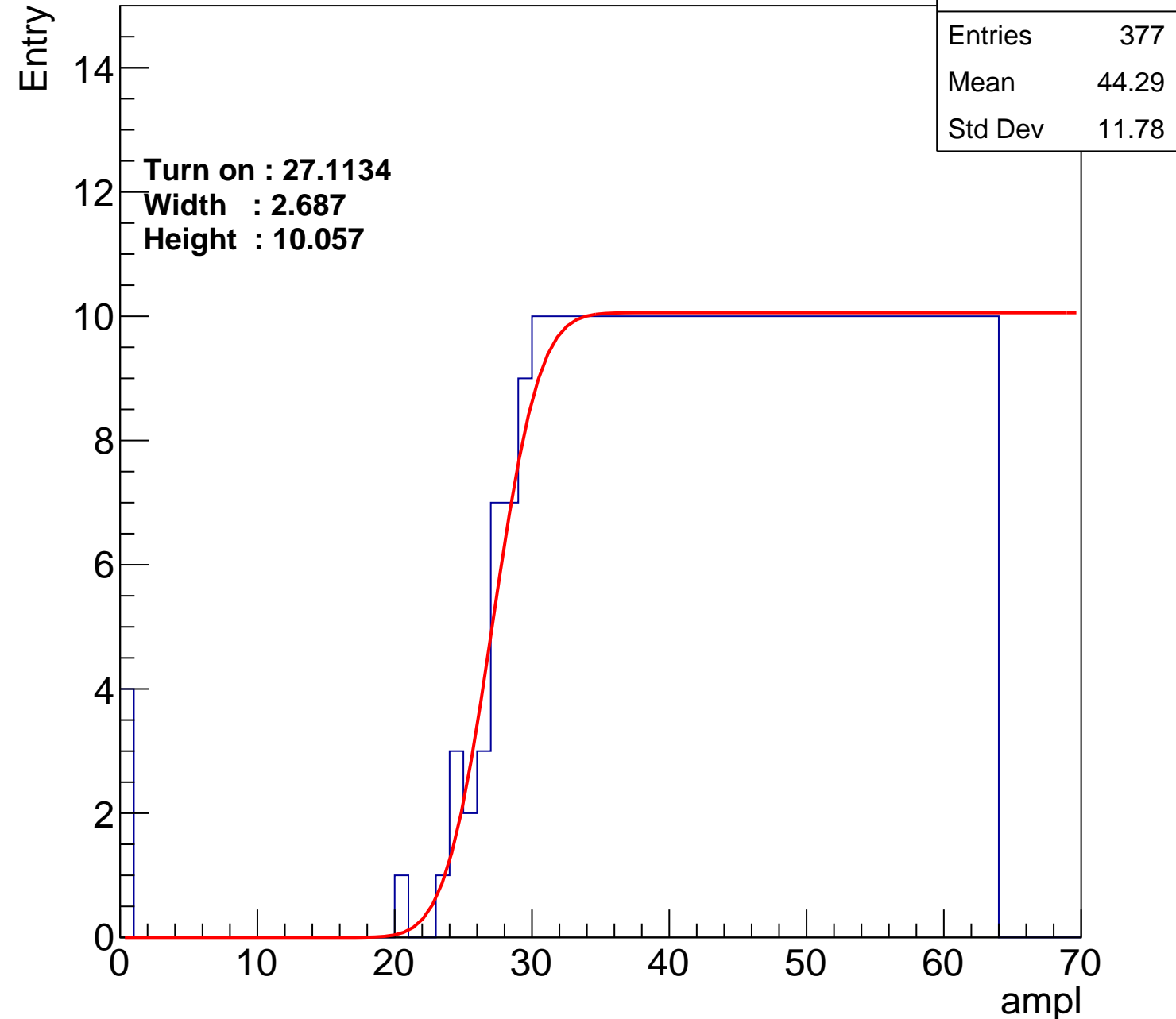
Width : 2.687

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.2
Std Dev	11.57

Turn on : 26.7993

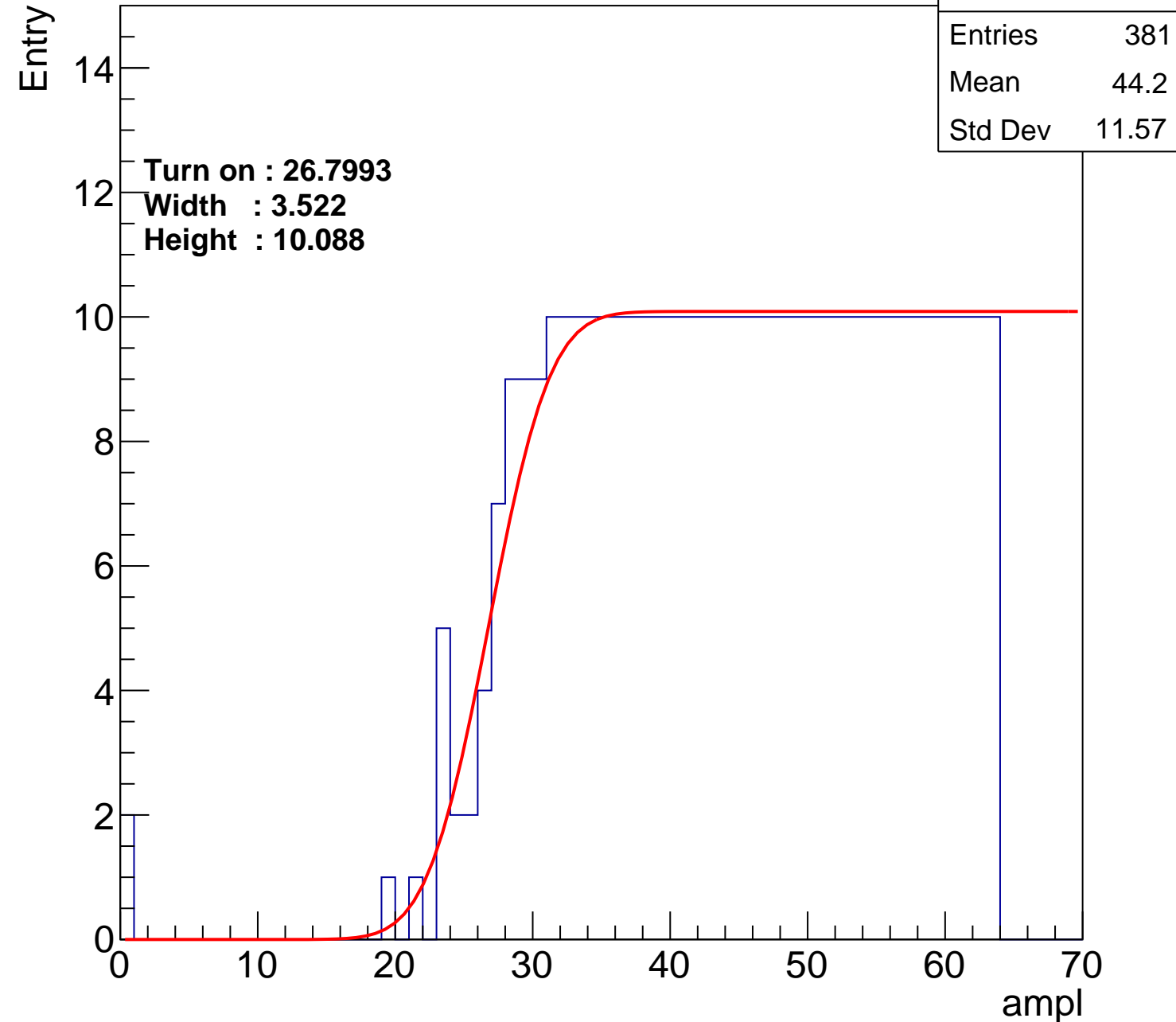
Width : 3.522

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.07
Std Dev	12.08

Turn on : 27.1762

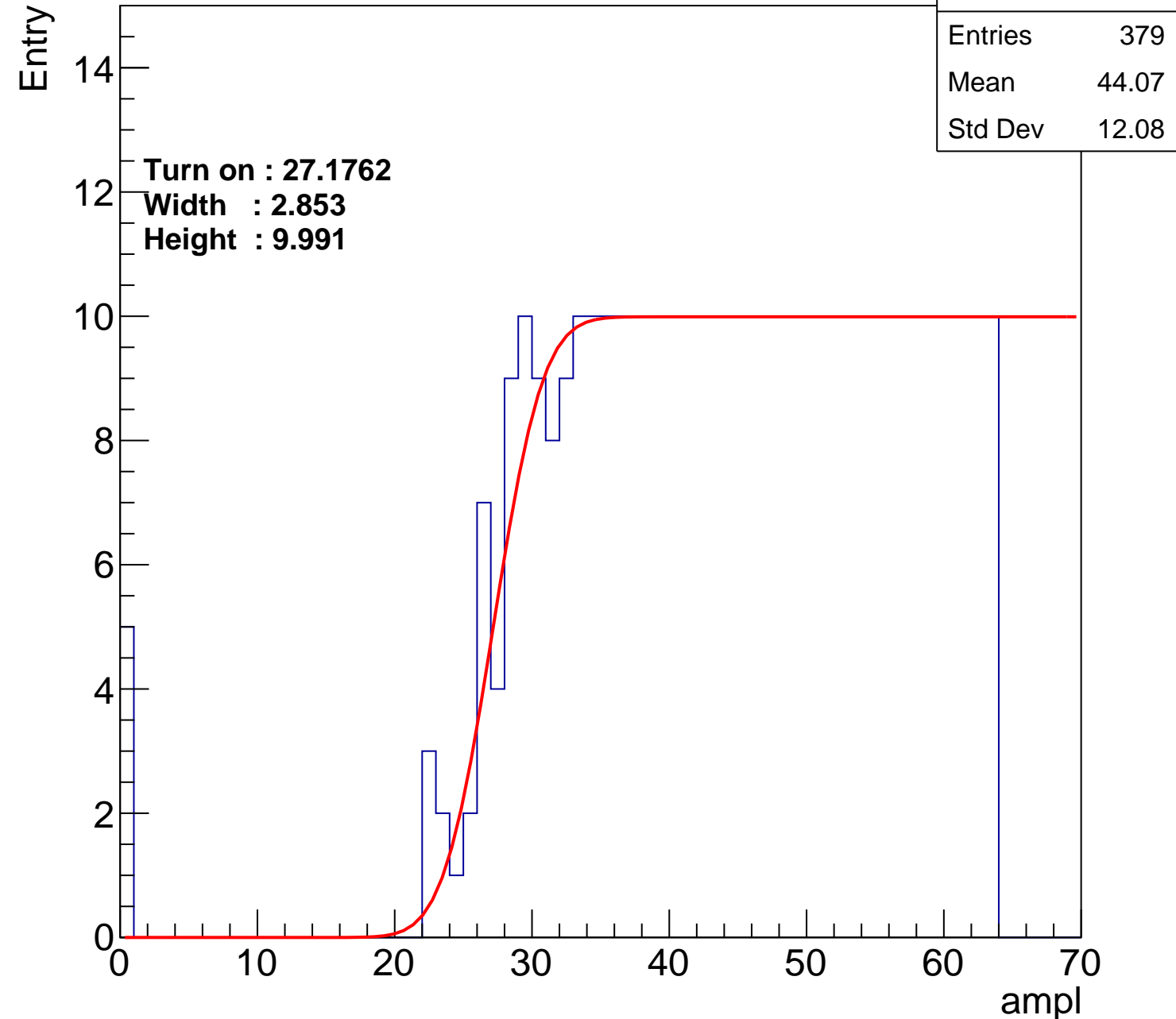
Width : 2.853

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch93

calib_packv5_042523_0143.root, FC#8, port C1

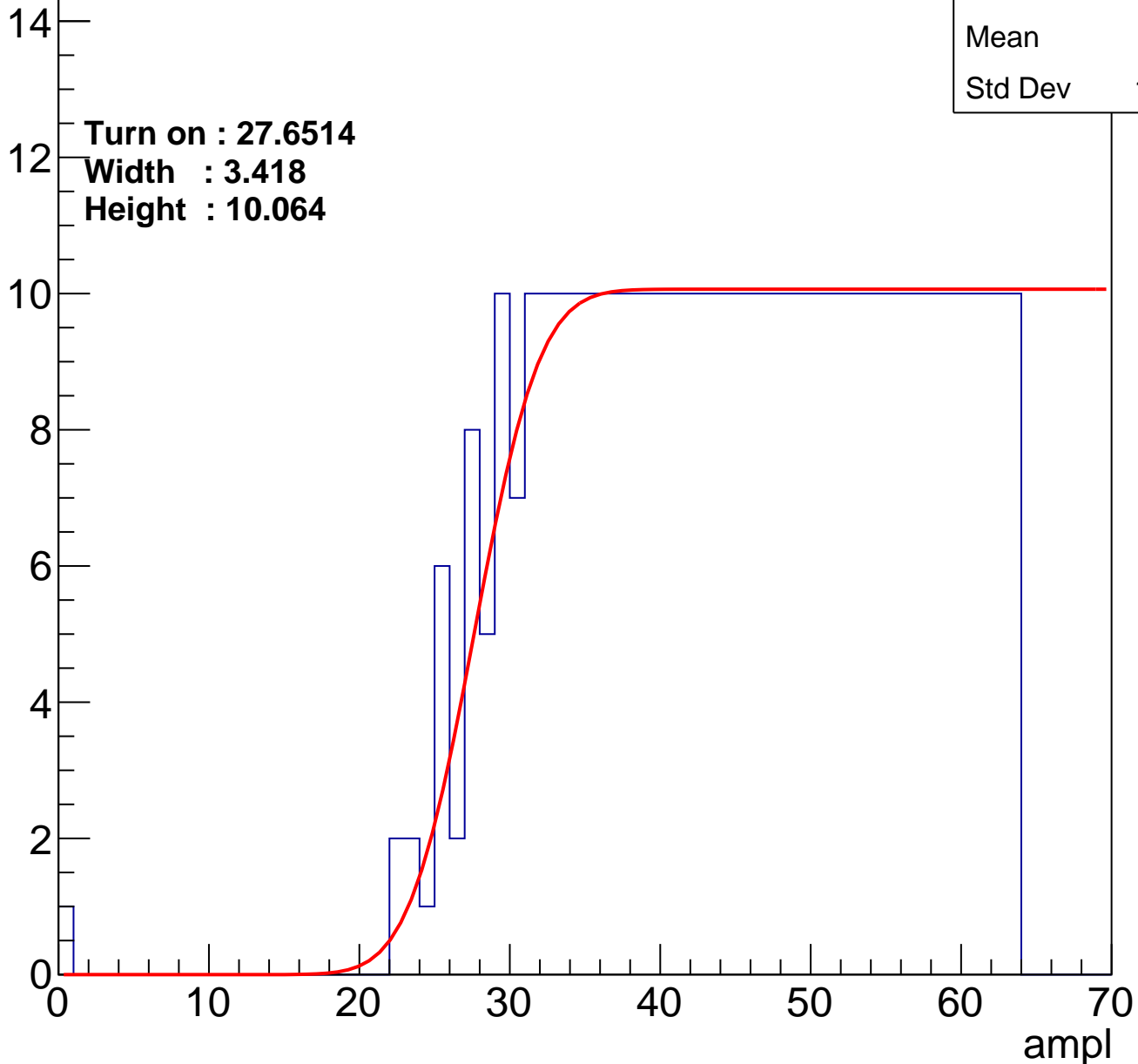
Entries	374
Mean	44.6
Std Dev	11.21

Turn on : 27.6514

Width : 3.418

Height : 10.064

Entry



B0L002S, U14-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.75
Std Dev	10.78

Turn on : 28.8954

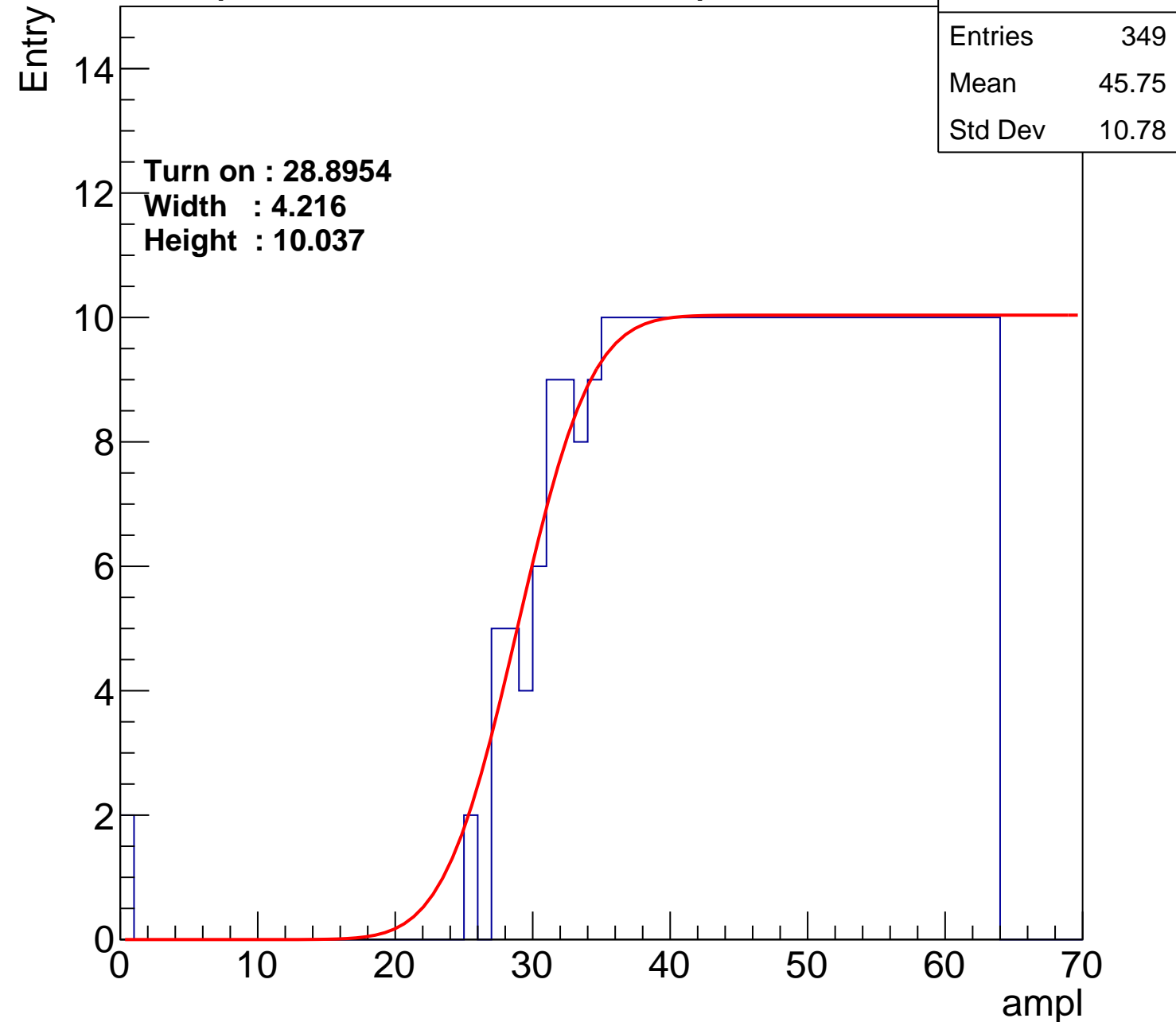
Width : 4.216

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.9302
Width : 3.738
Height : 10.076



B0L002S, U14-ch96

calib_packv5_042523_0143.root, FC#8, port C1

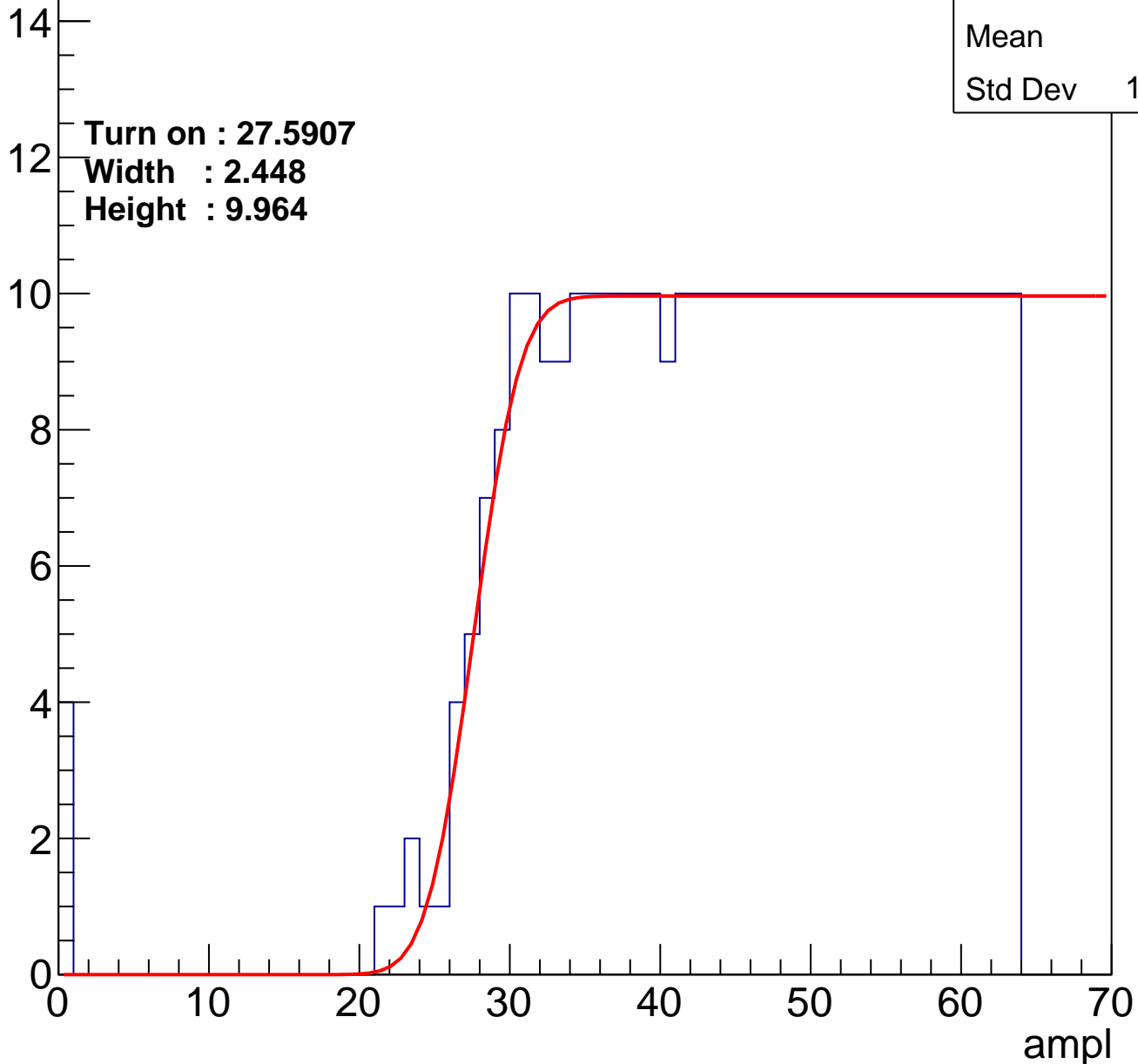
Entries	371
Mean	44.5
Std Dev	11.75

Turn on : 27.5907

Width : 2.448

Height : 9.964

Entry



B0L002S, U14-ch97

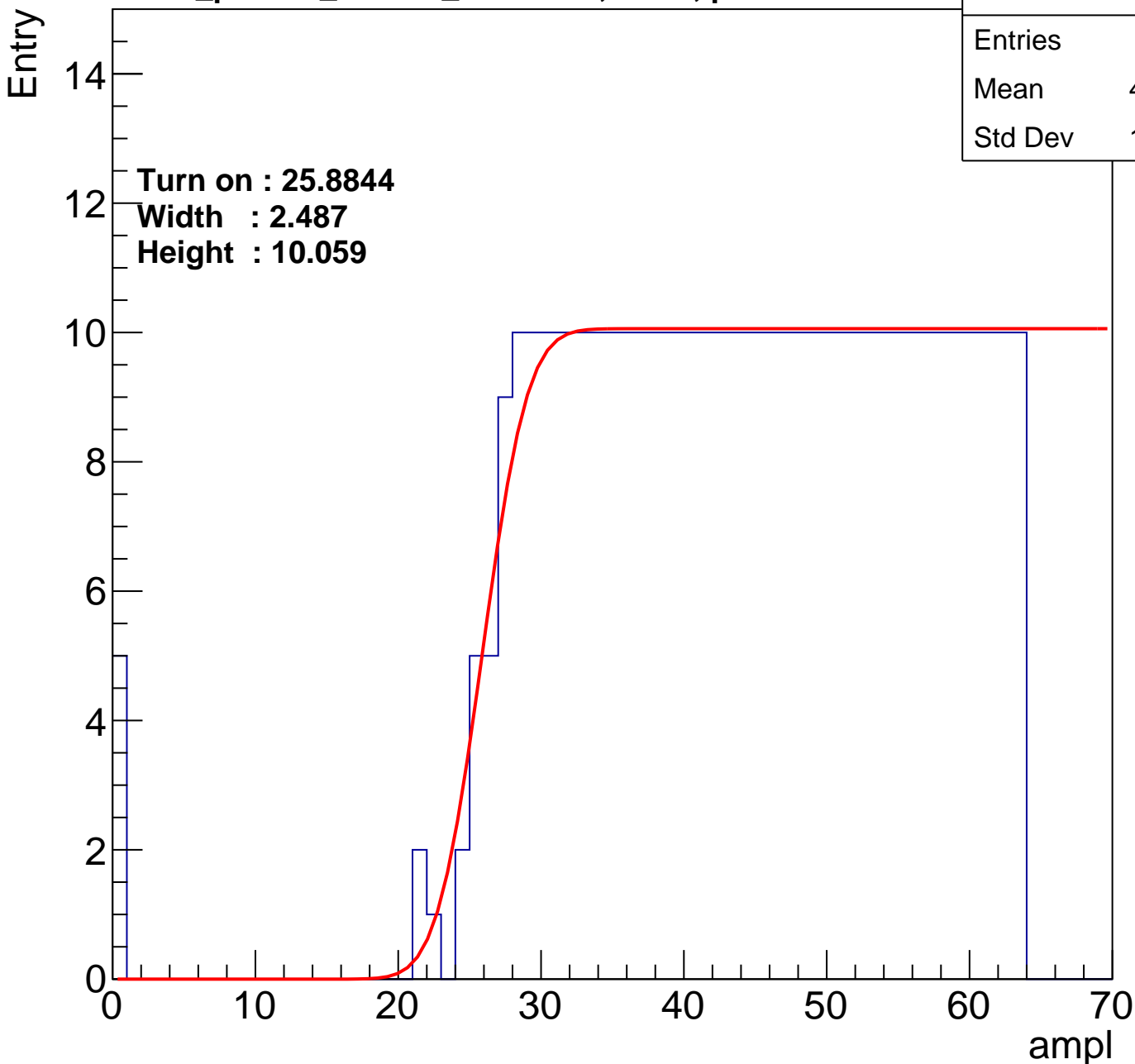
calib_packv5_042523_0143.root, FC#8, port C1

Entries	389
Mean	43.68
Std Dev	12.18

Turn on : 25.8844

Width : 2.487

Height : 10.059



B0L002S, U14-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.54
Std Dev	11.25

Turn on : 26.7808

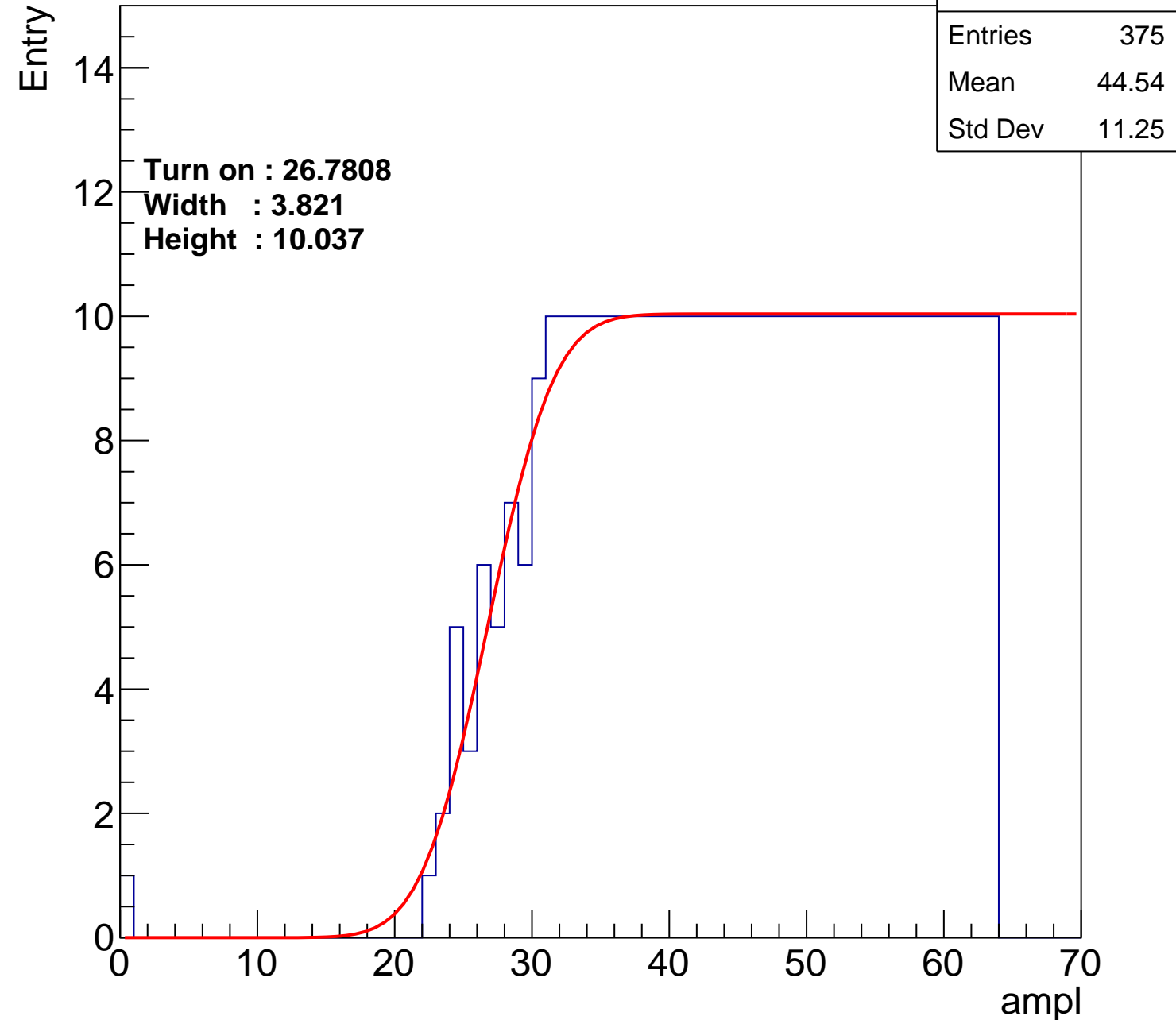
Width : 3.821

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.58
Std Dev	11.18

Turn on : 26.3492

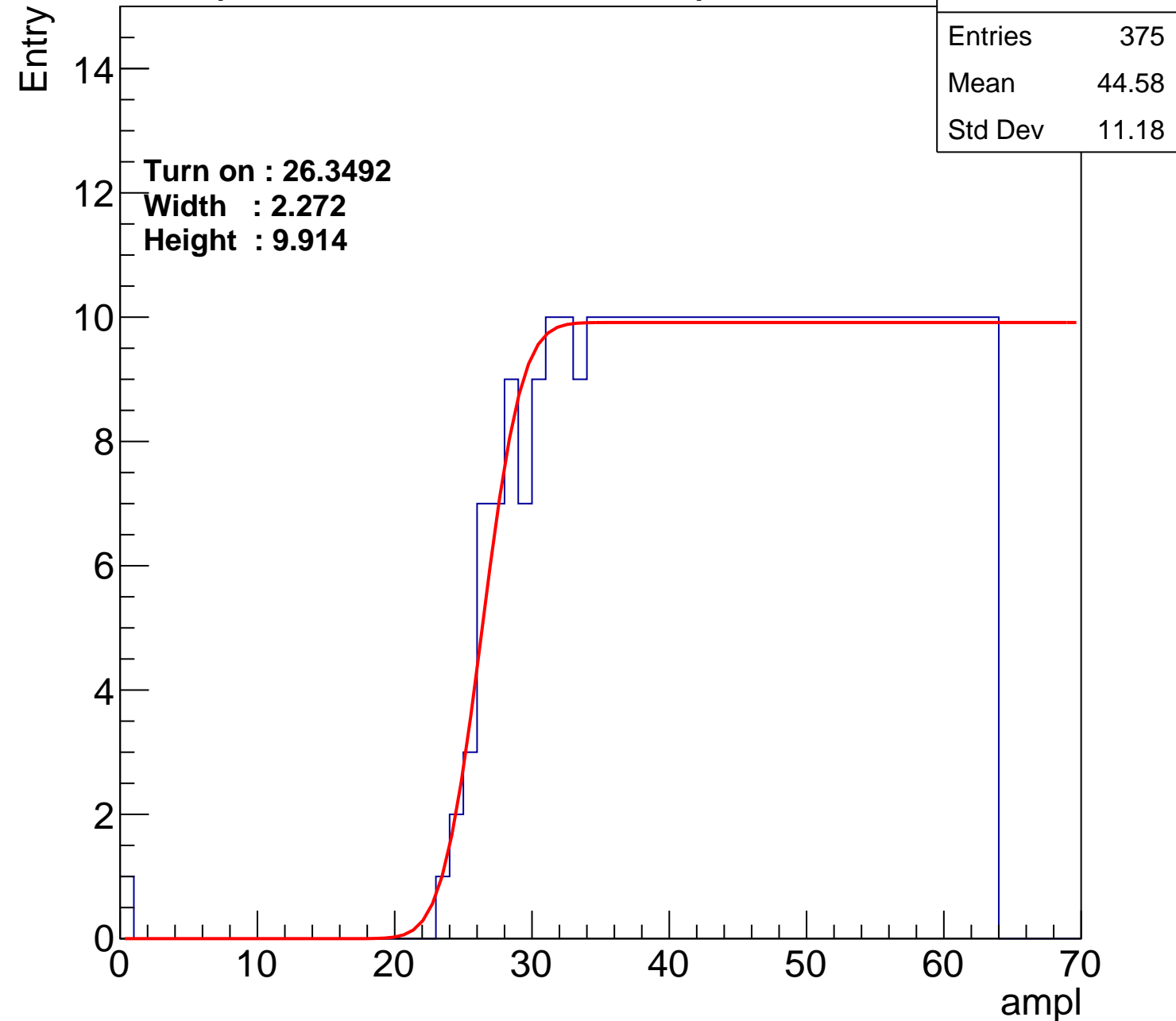
Width : 2.272

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.77
Std Dev	11.77

Turn on : 24.9006

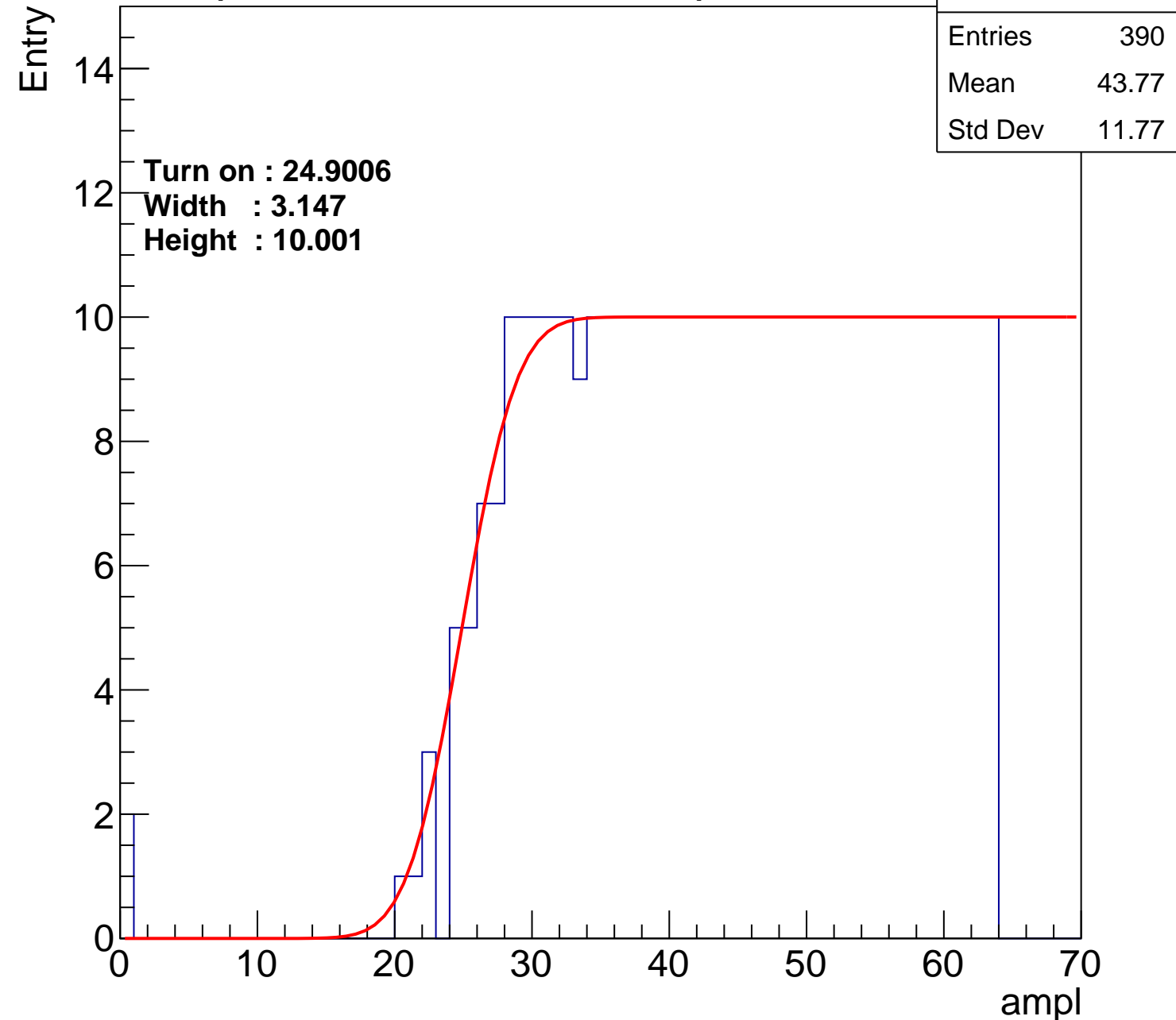
Width : 3.147

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.08
Std Dev	10.43

Turn on : 30.1038

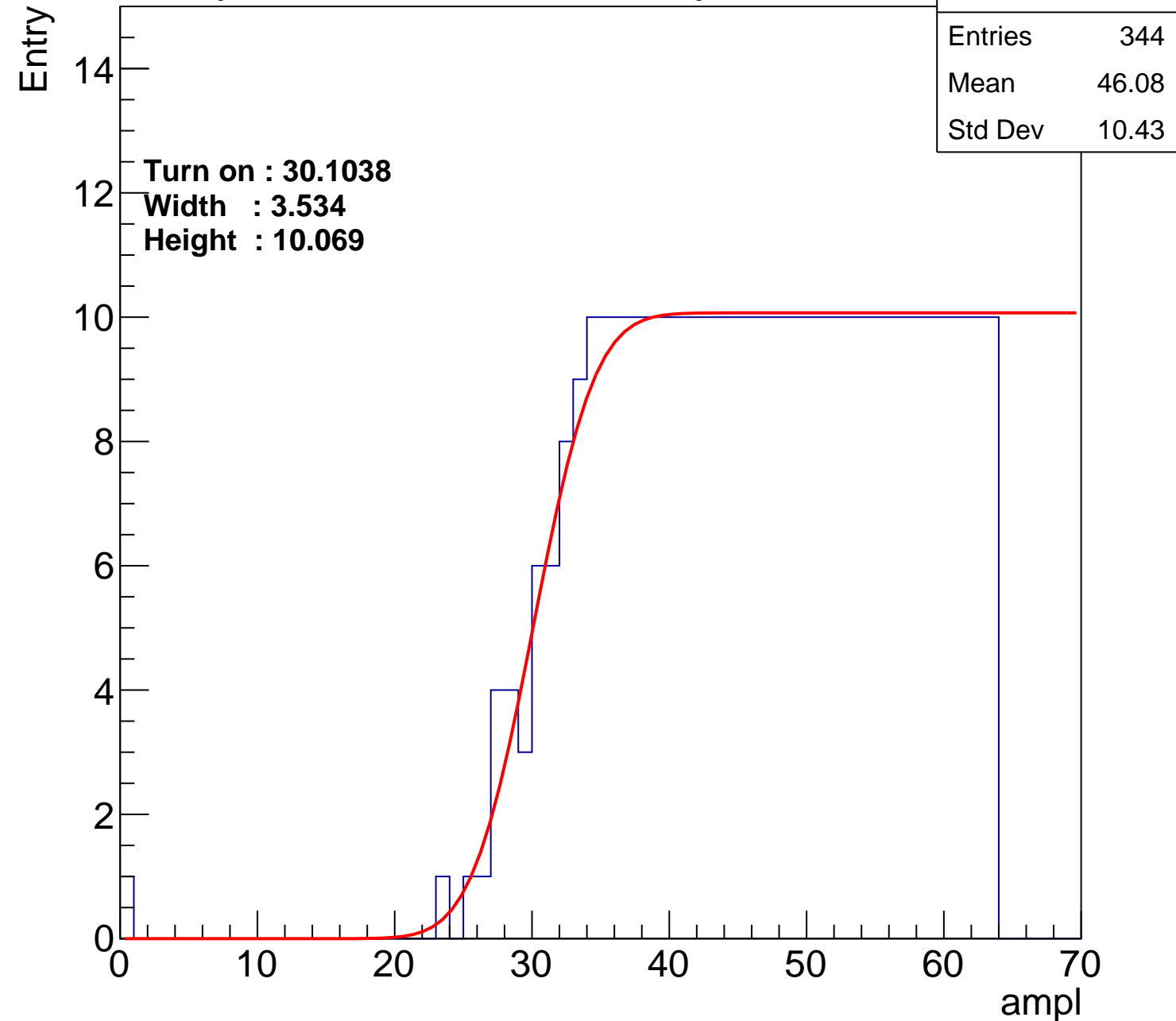
Width : 3.534

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.05
Std Dev	11.16

Turn on : 28.3760

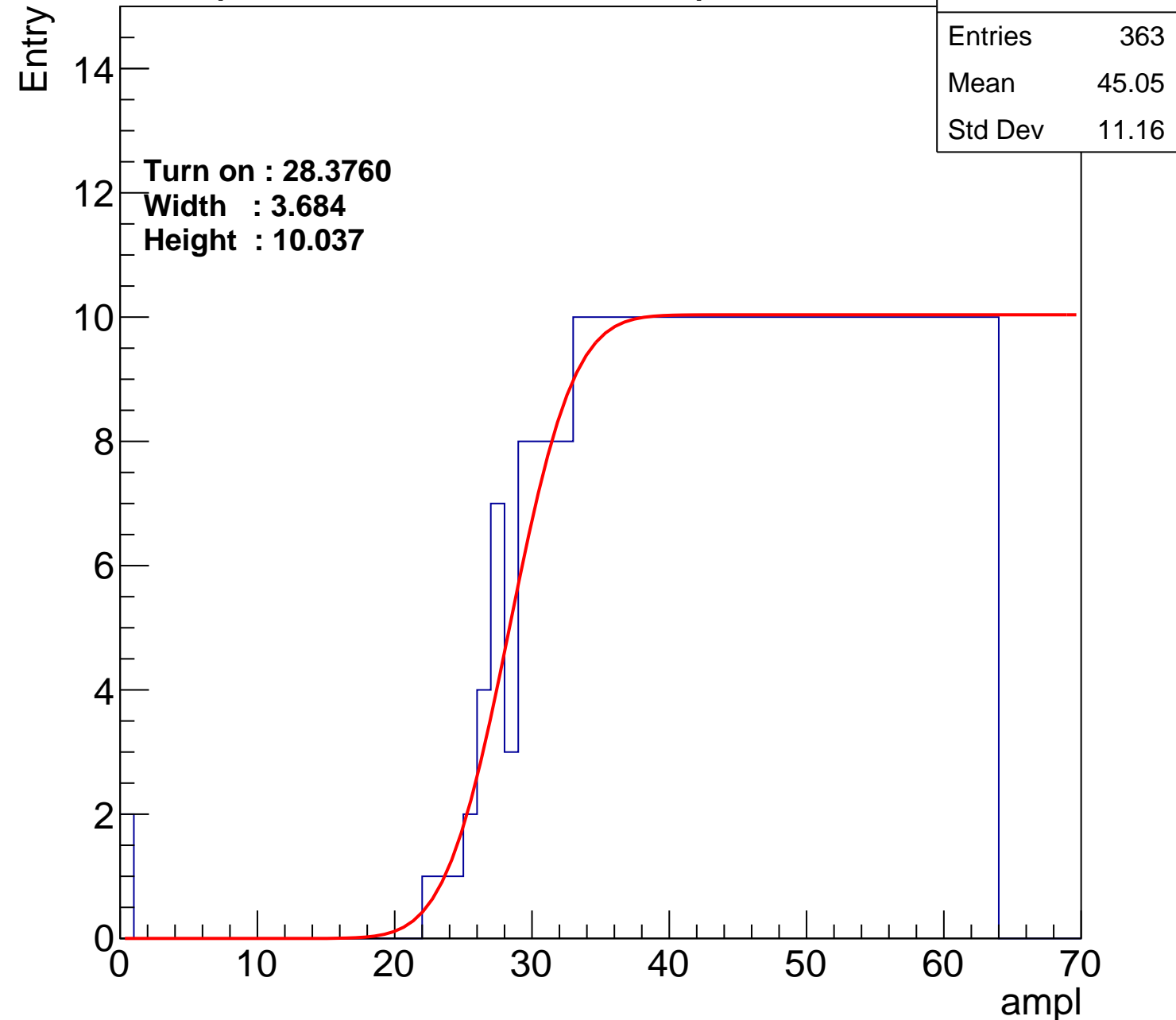
Width : 3.684

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch103

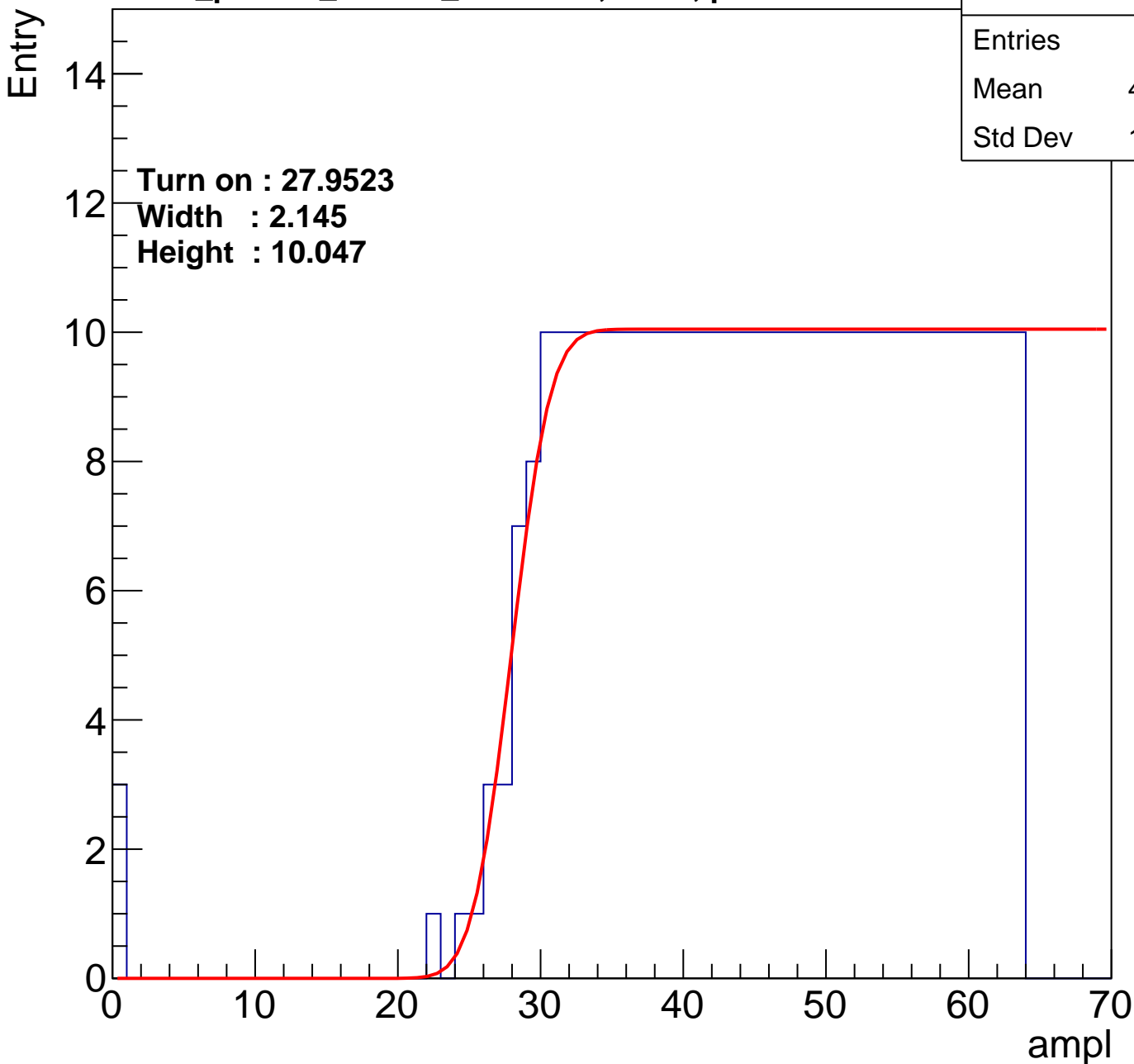
calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.87
Std Dev	11.32

Turn on : 27.9523

Width : 2.145

Height : 10.047



B0L002S, U14-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.11
Std Dev	11.48

Turn on : 26.2728

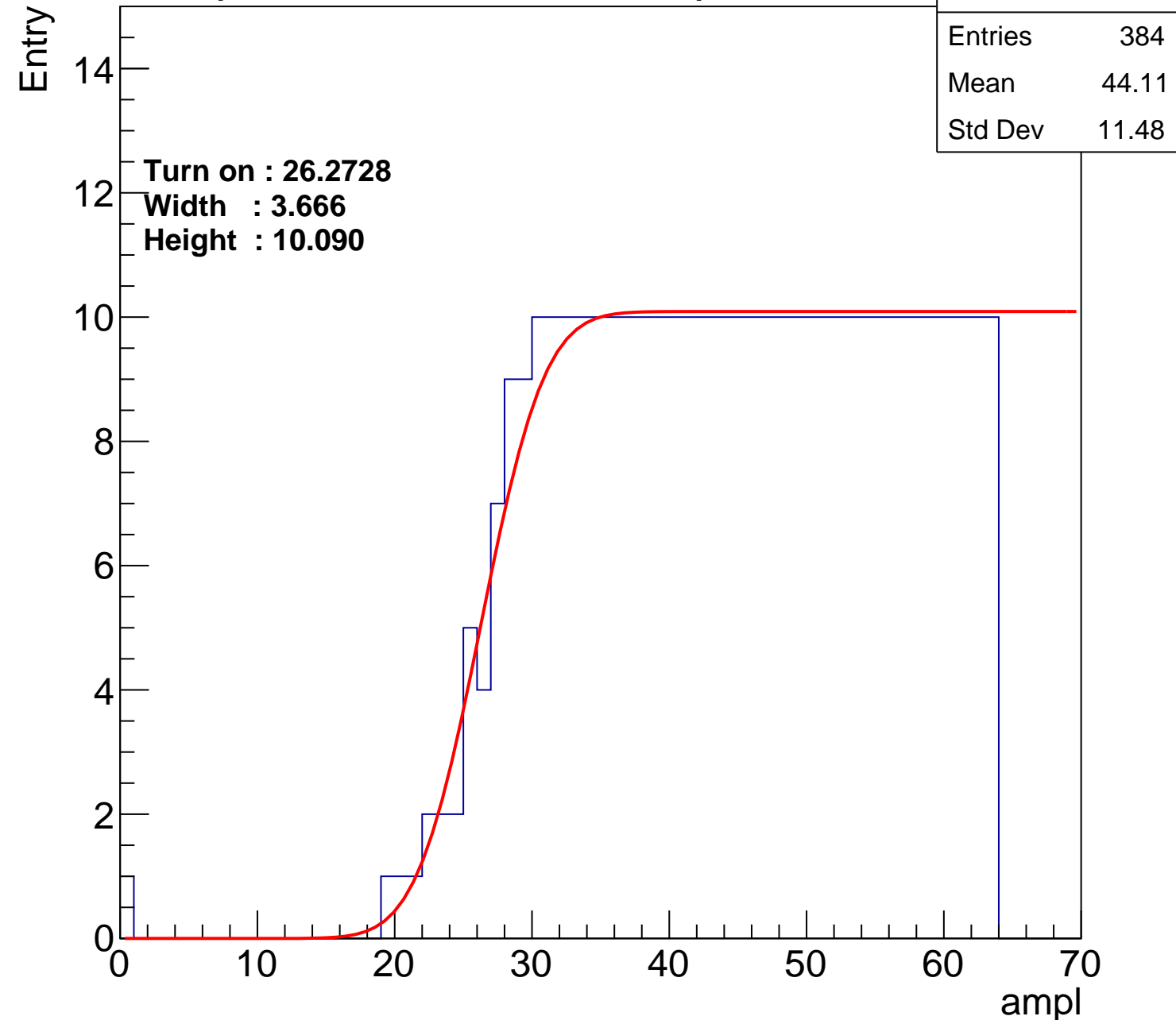
Width : 3.666

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	389
Mean	43.76
Std Dev	11.86

Turn on : 25.4558

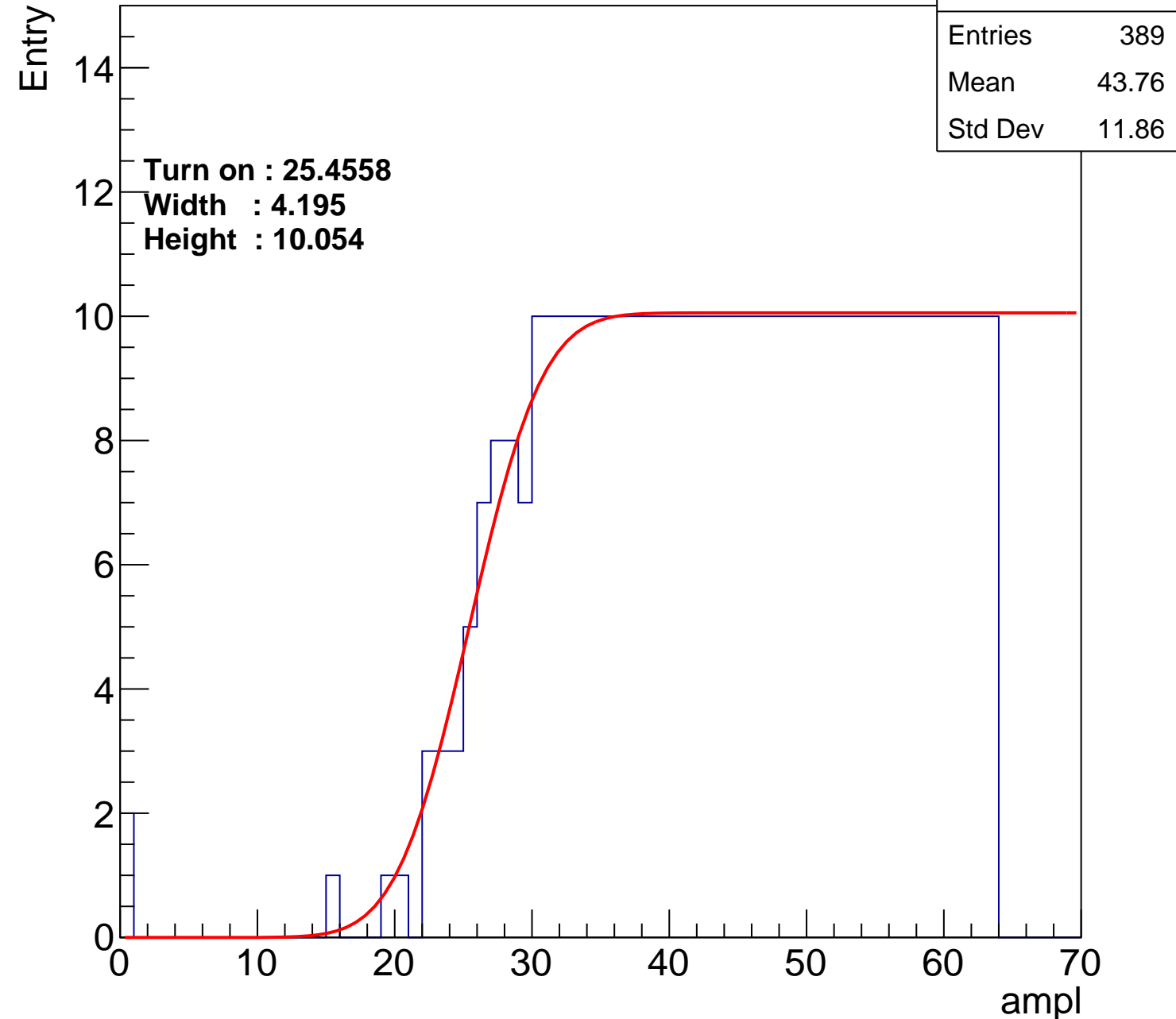
Width : 4.195

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.85
Std Dev	11.73

Turn on : 28.4588

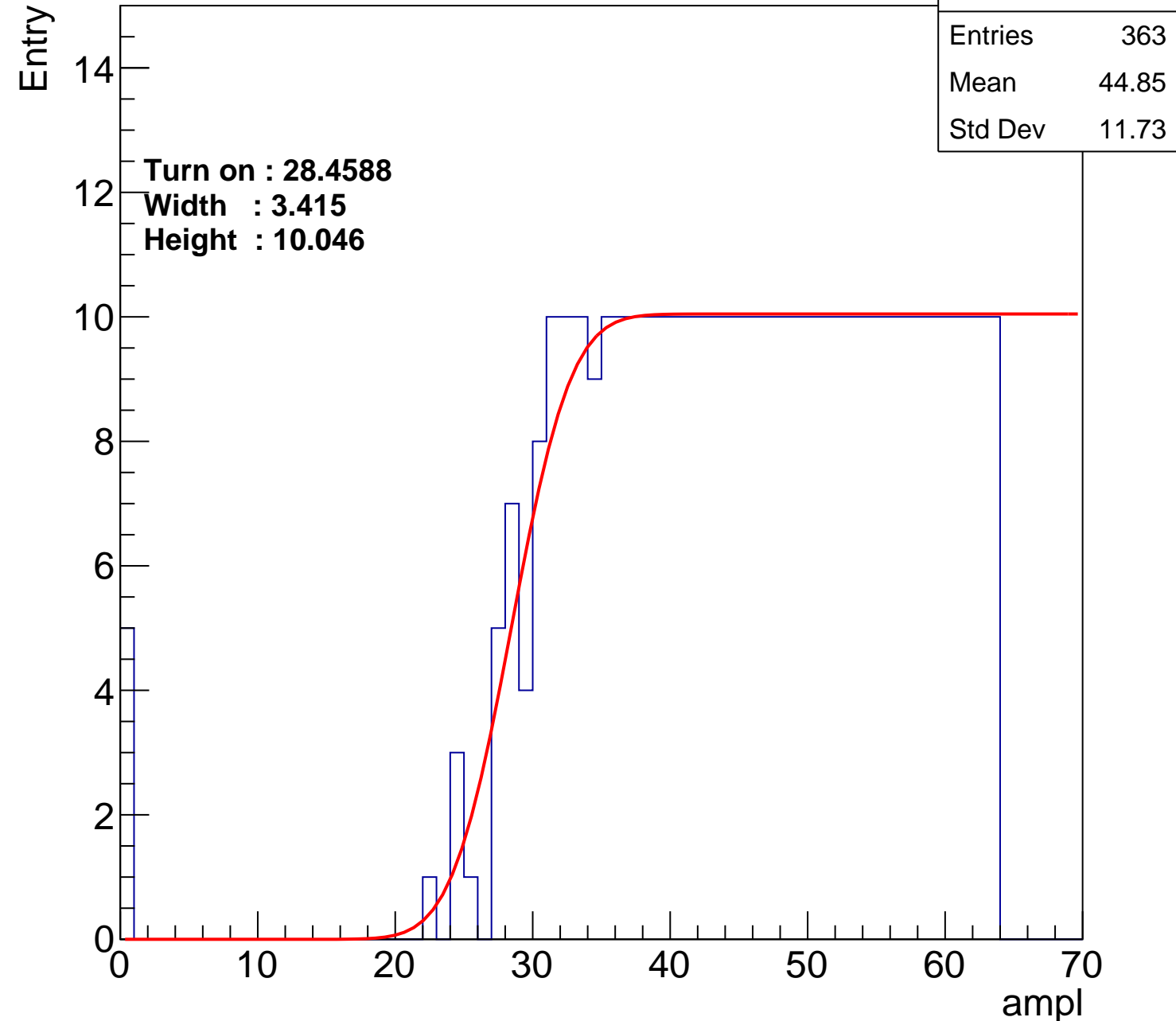
Width : 3.415

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch107

calib_packv5_042523_0143.root, FC#8, port C1

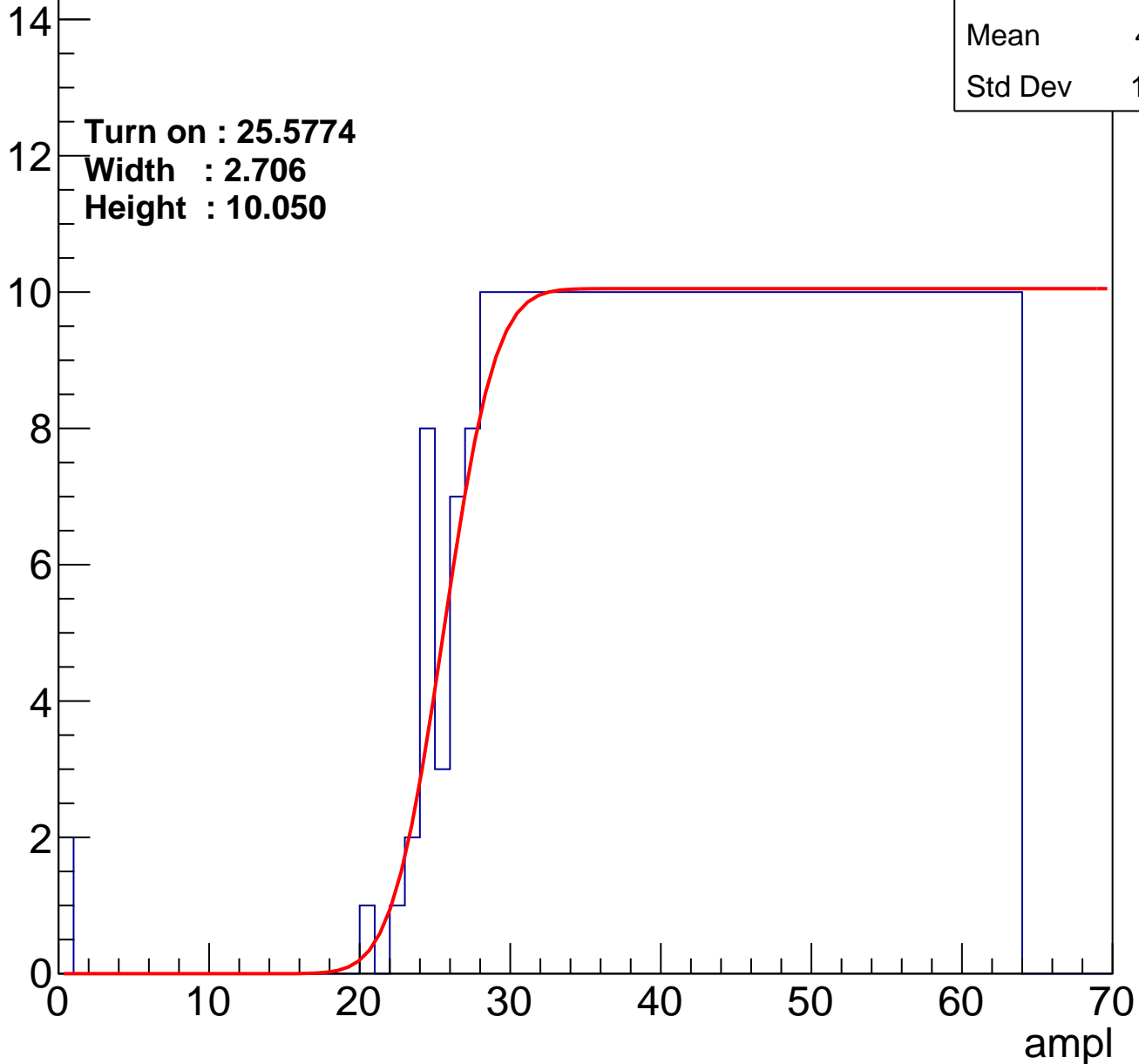
Entries	392
Mean	43.71
Std Dev	11.77

Turn on : 25.5774

Width : 2.706

Height : 10.050

Entry



B0L002S, U14-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.02
Std Dev	11.29

Turn on : 28.2213

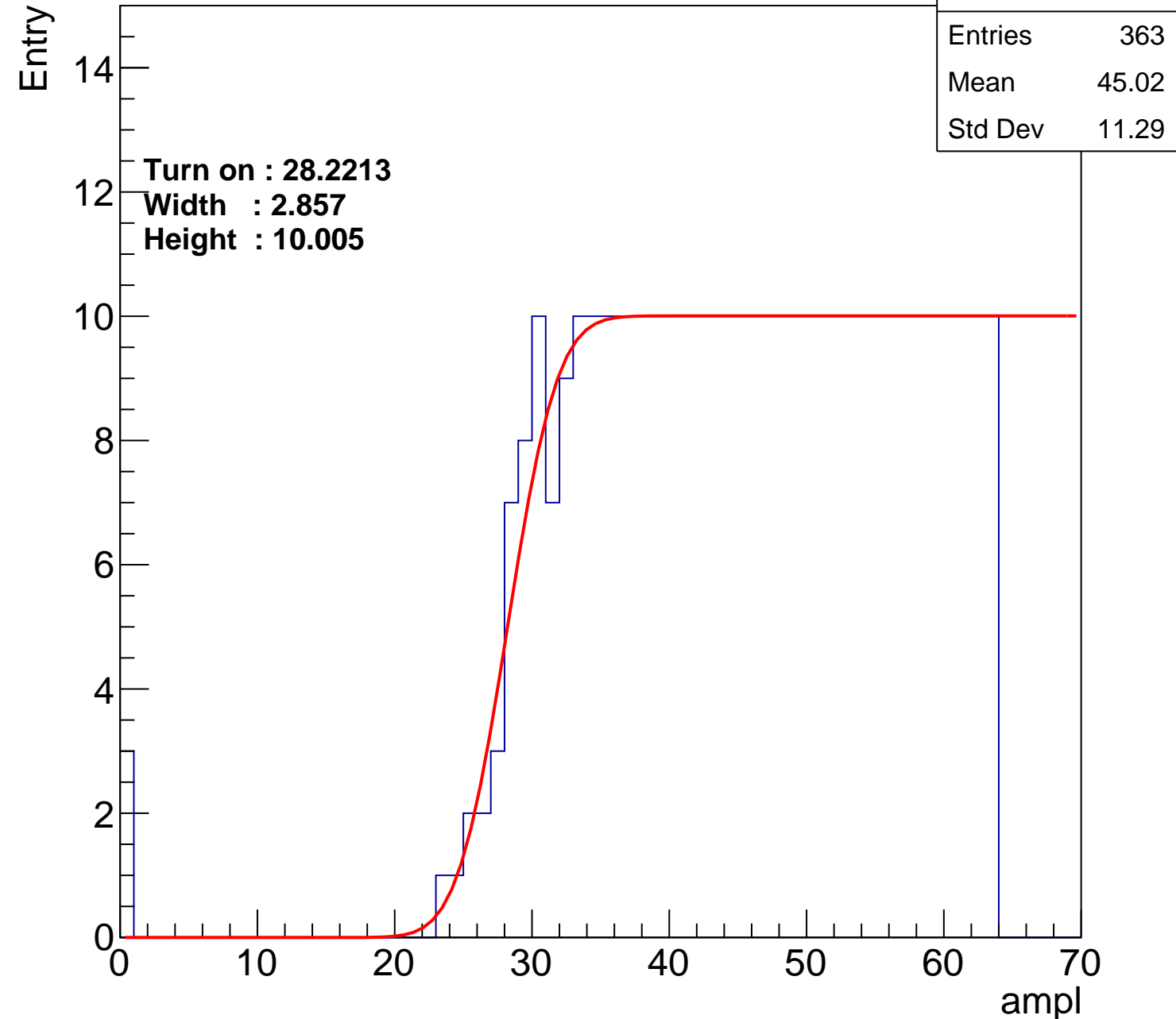
Width : 2.857

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.67
Std Dev	11.28

Turn on : 27.3372

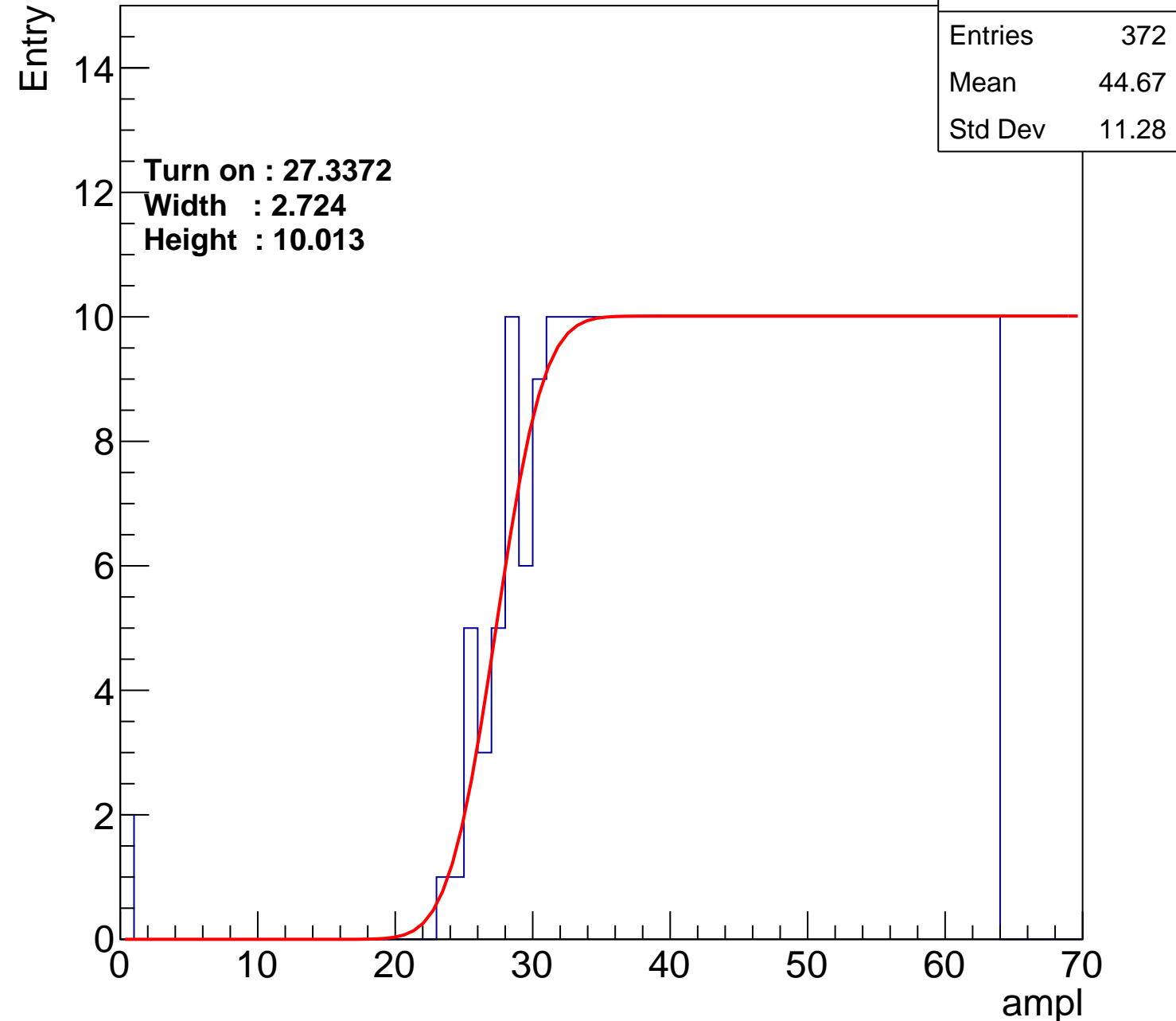
Width : 2.724

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.74
Std Dev	10.81

Turn on : 29.9190

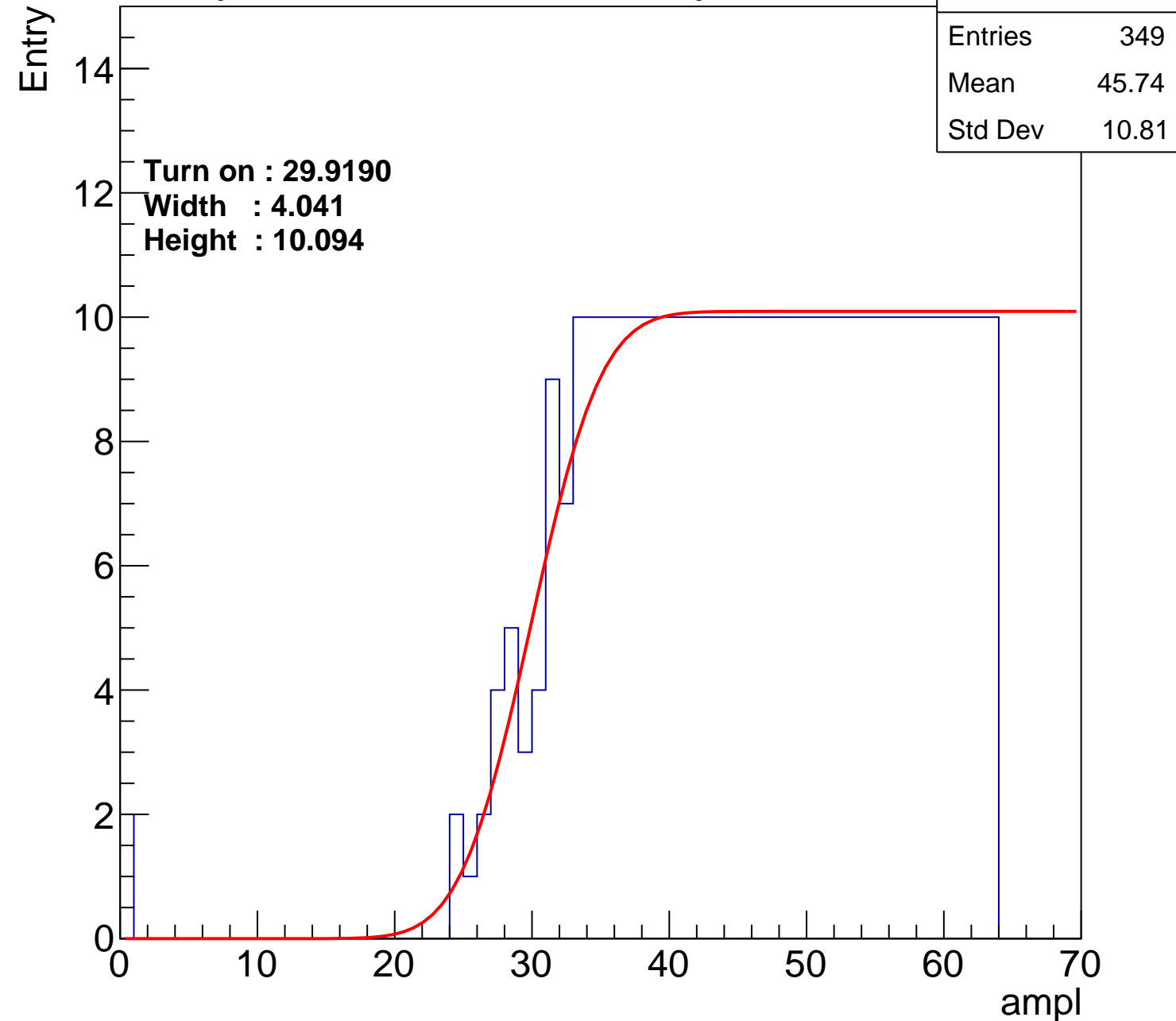
Width : 4.041

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.13
Std Dev	10.95

Turn on : 28.4657

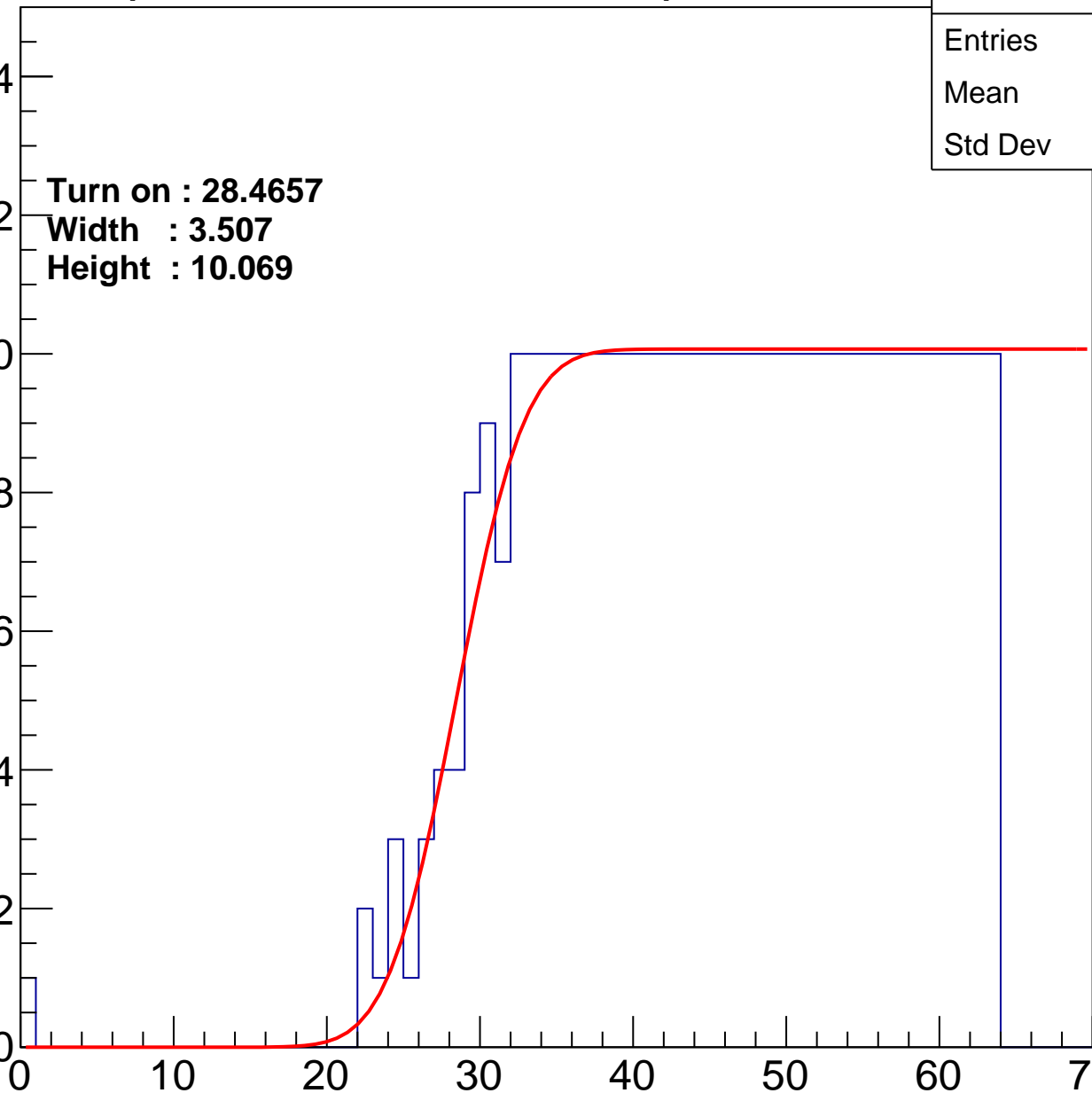
Width : 3.507

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch112

calib_packv5_042523_0143.root, FC#8, port C1

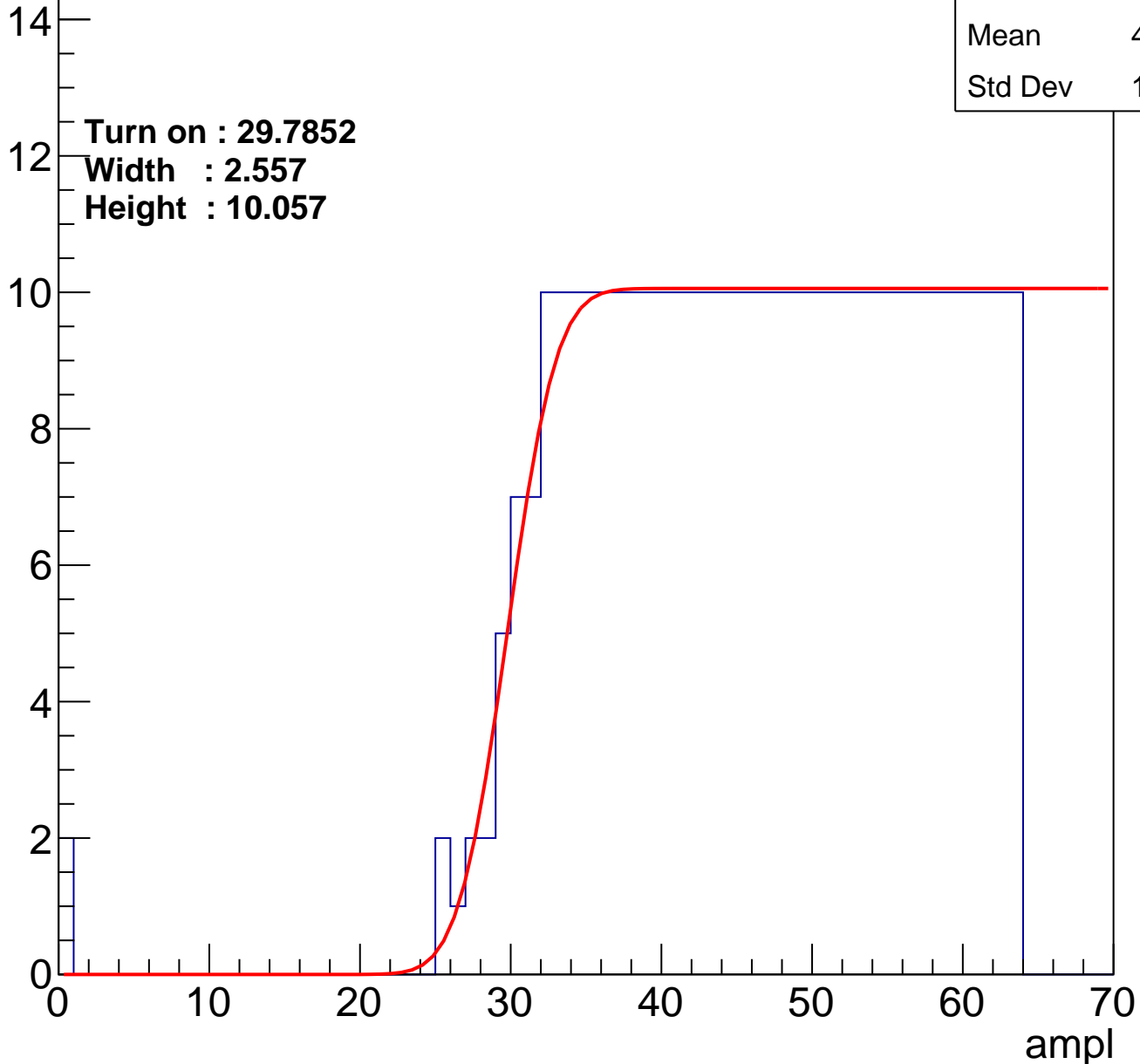
Entries	348
Mean	45.86
Std Dev	10.68

Turn on : 29.7852

Width : 2.557

Height : 10.057

Entry



B0L002S, U14-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.6262

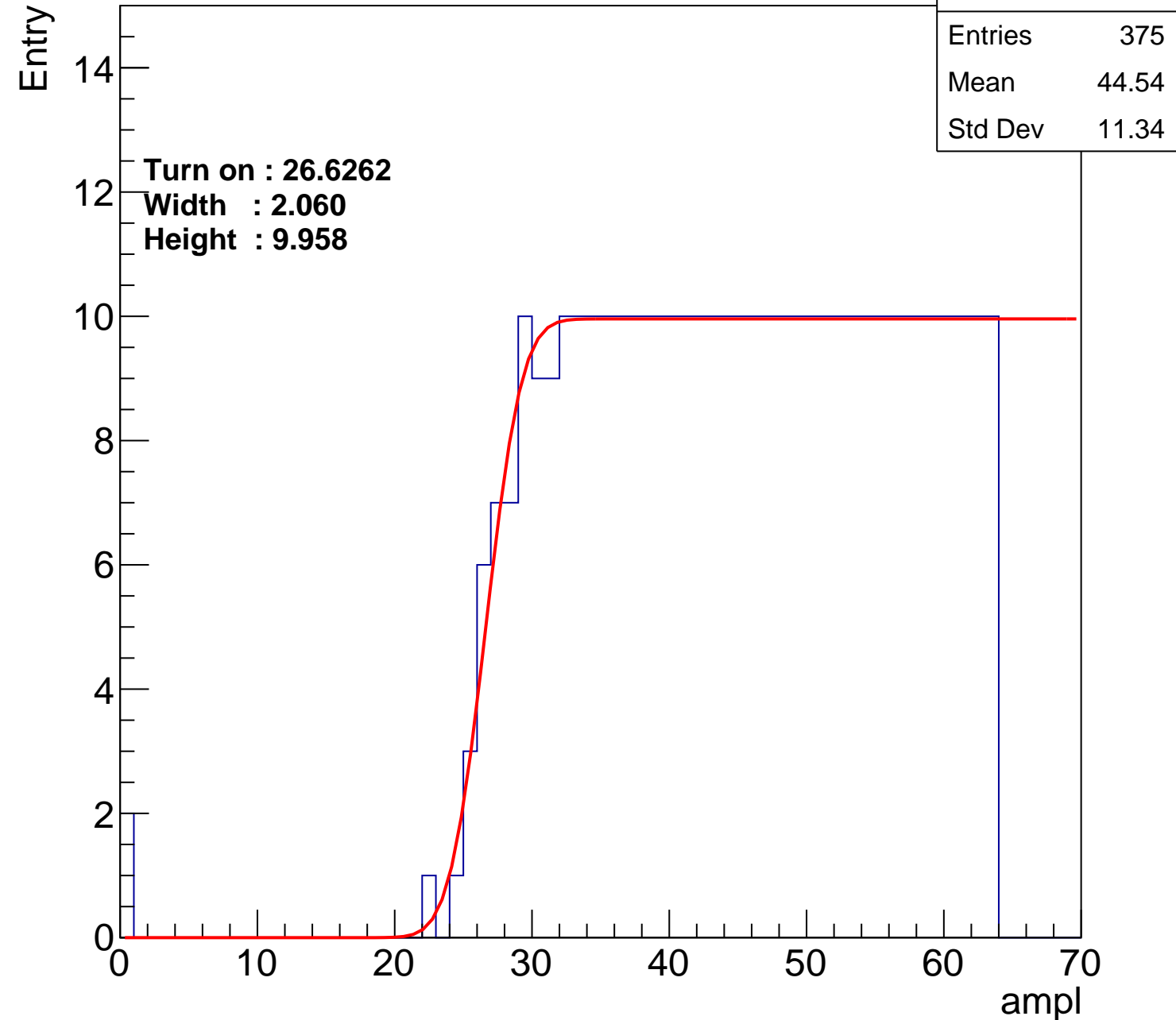
Width : 2.060

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	400
Mean	43.21
Std Dev	12.25

Turn on : 24.3319

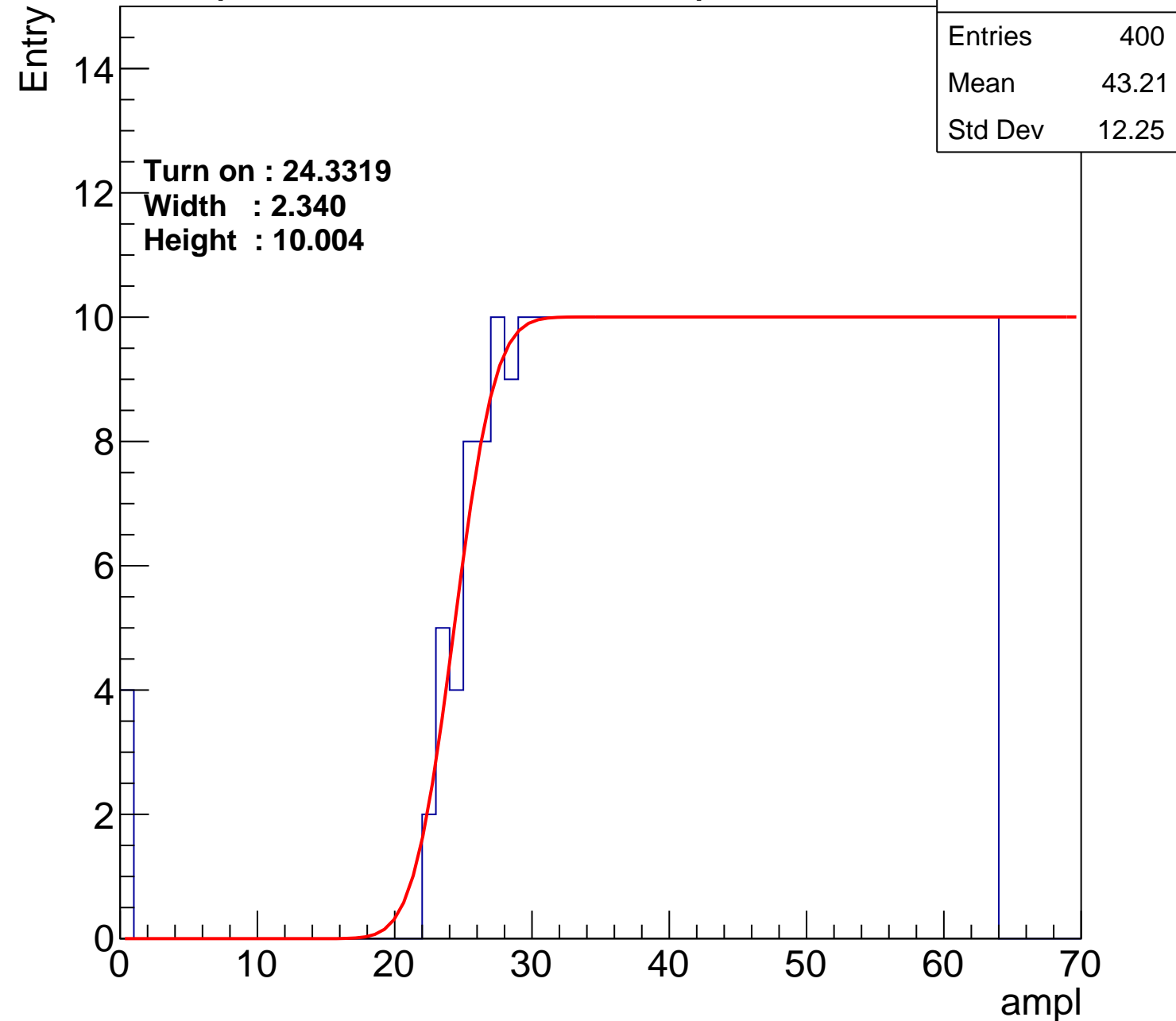
Width : 2.340

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.28
Std Dev	11.8

Turn on : 27.3151

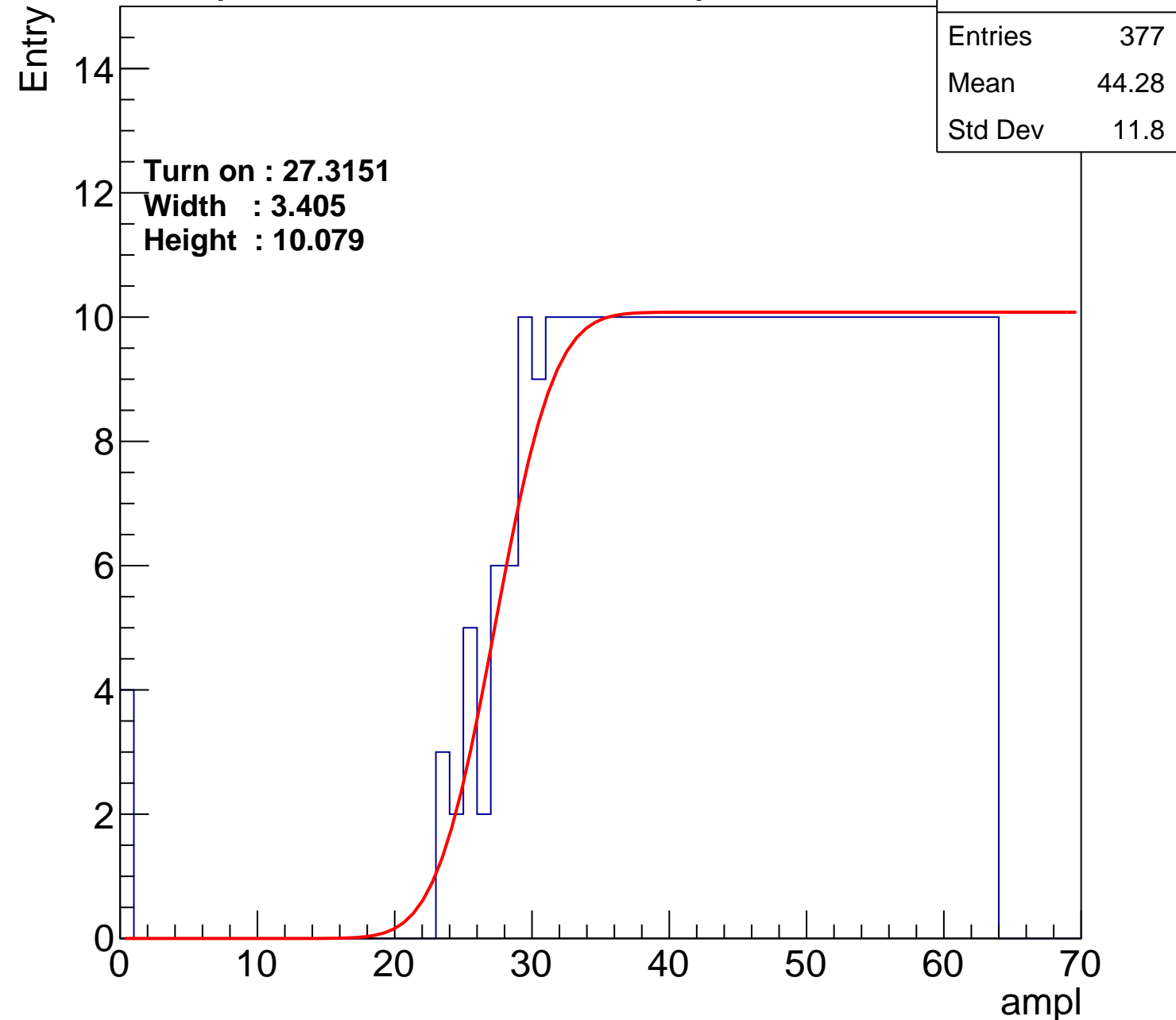
Width : 3.405

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.0809

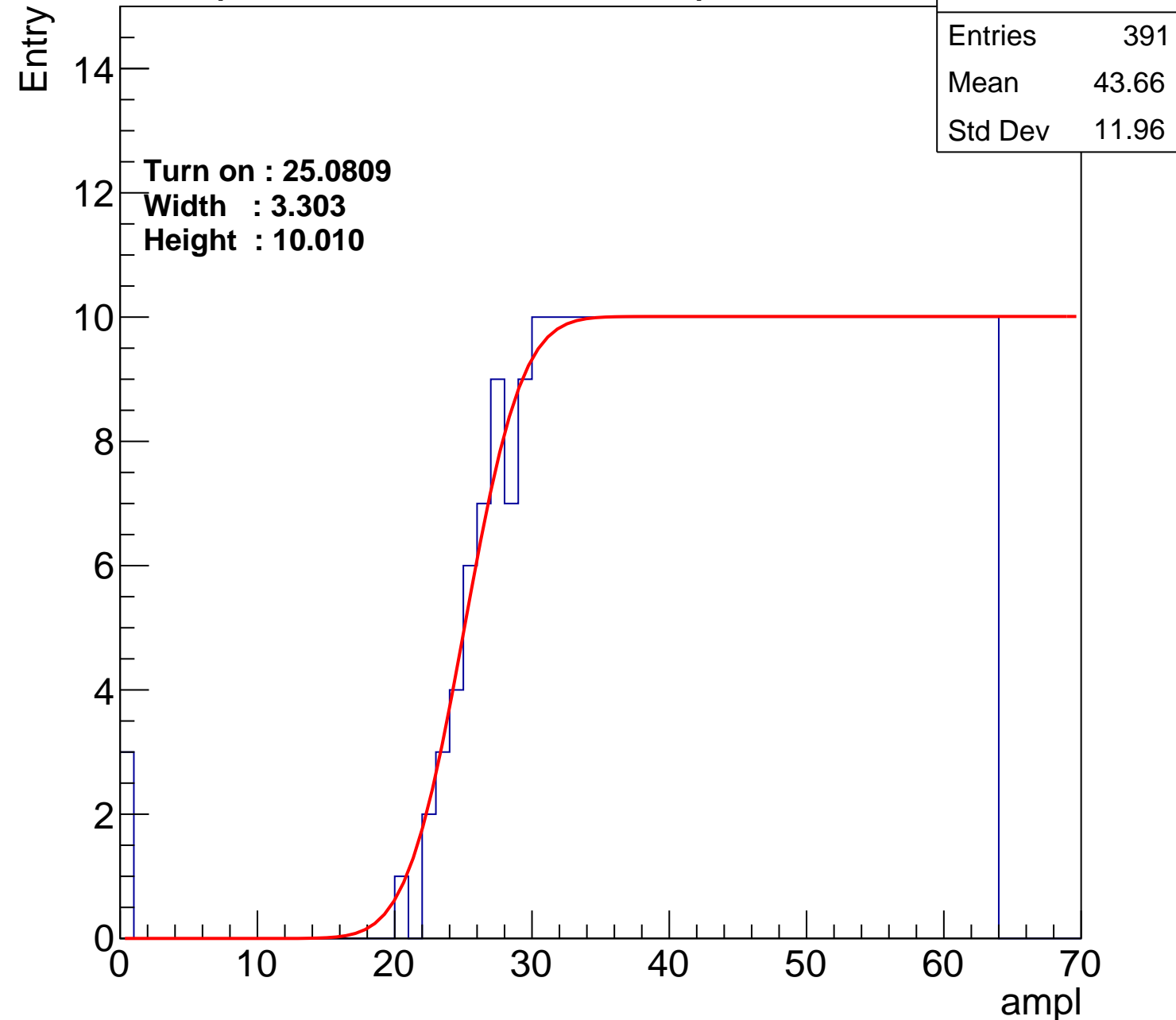
Width : 3.303

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	388
Mean	43.79
Std Dev	11.91

Turn on : 26.3457

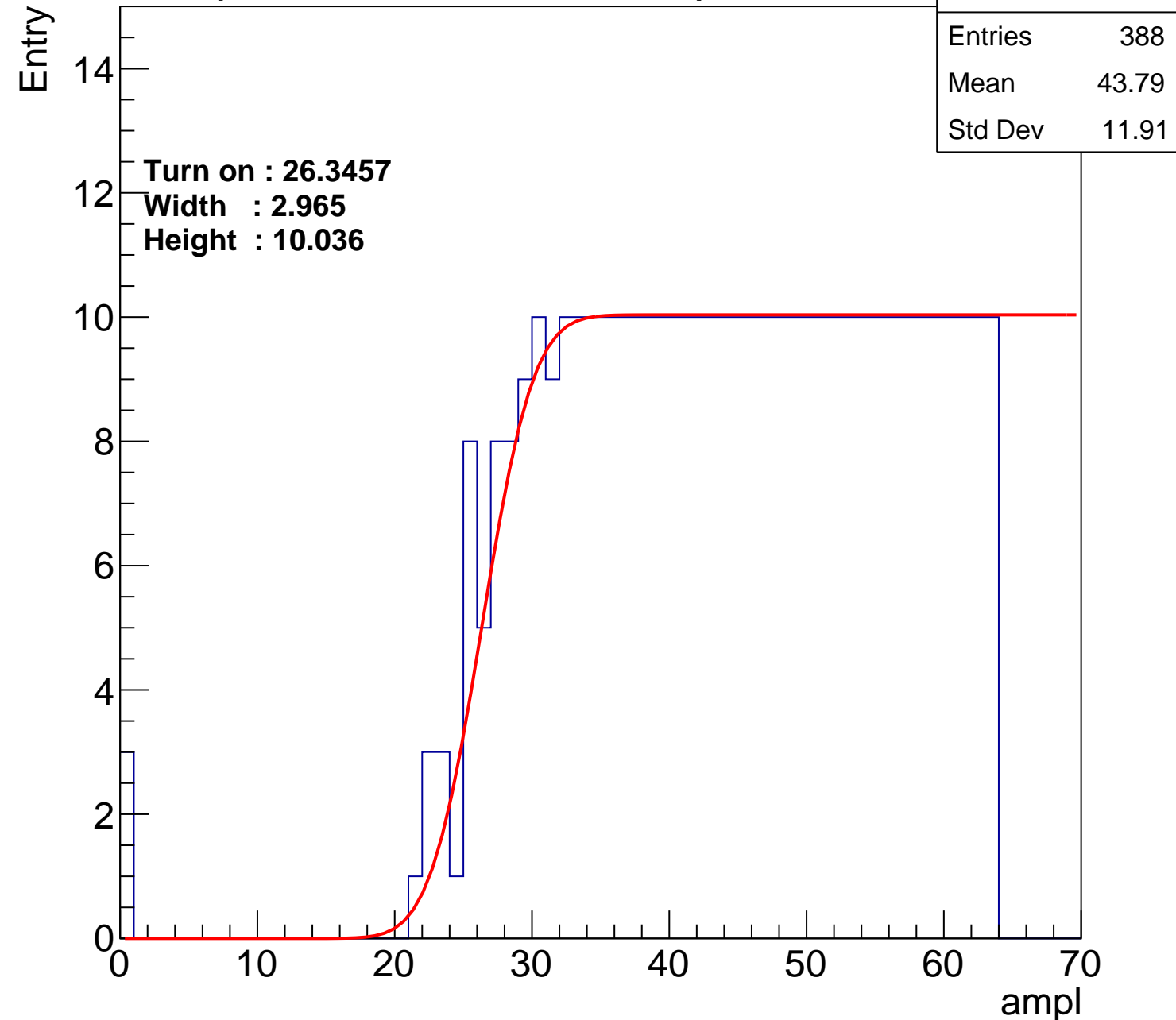
Width : 2.965

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 25.6045

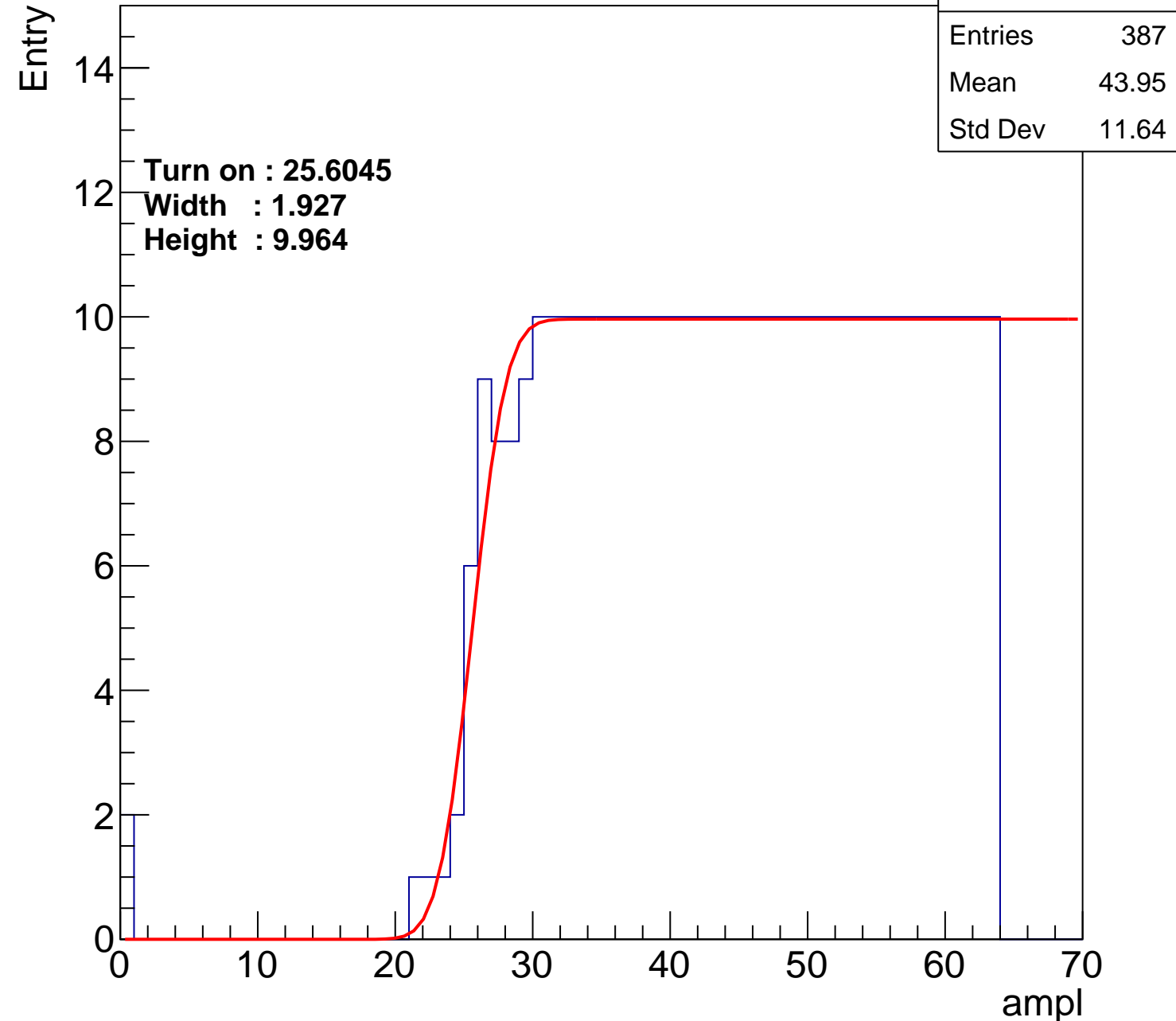
Width : 1.927

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.72
Std Dev	10.77

Turn on : 29.0820

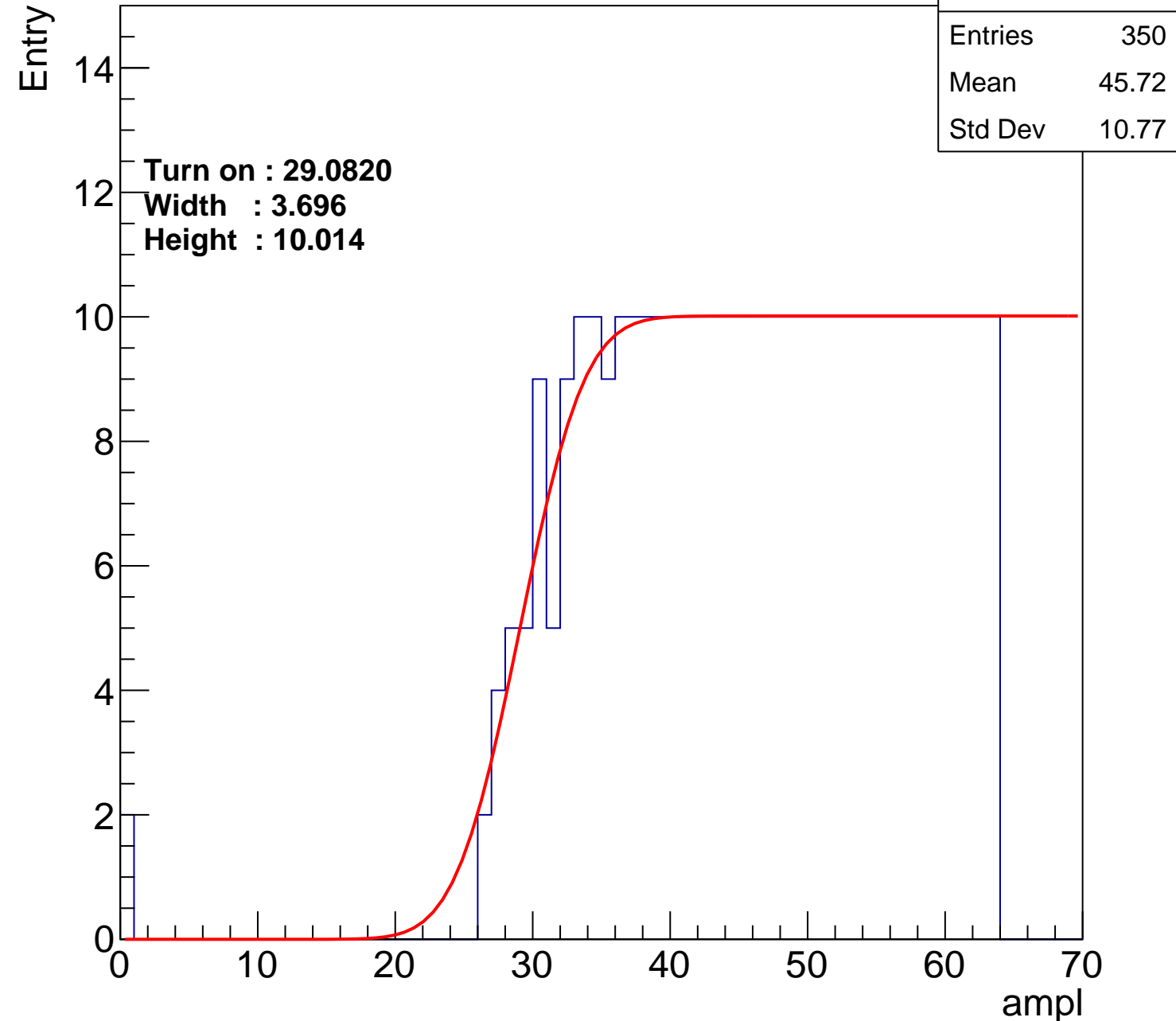
Width : 3.696

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.05
Std Dev	11.5

Turn on : 28.9506

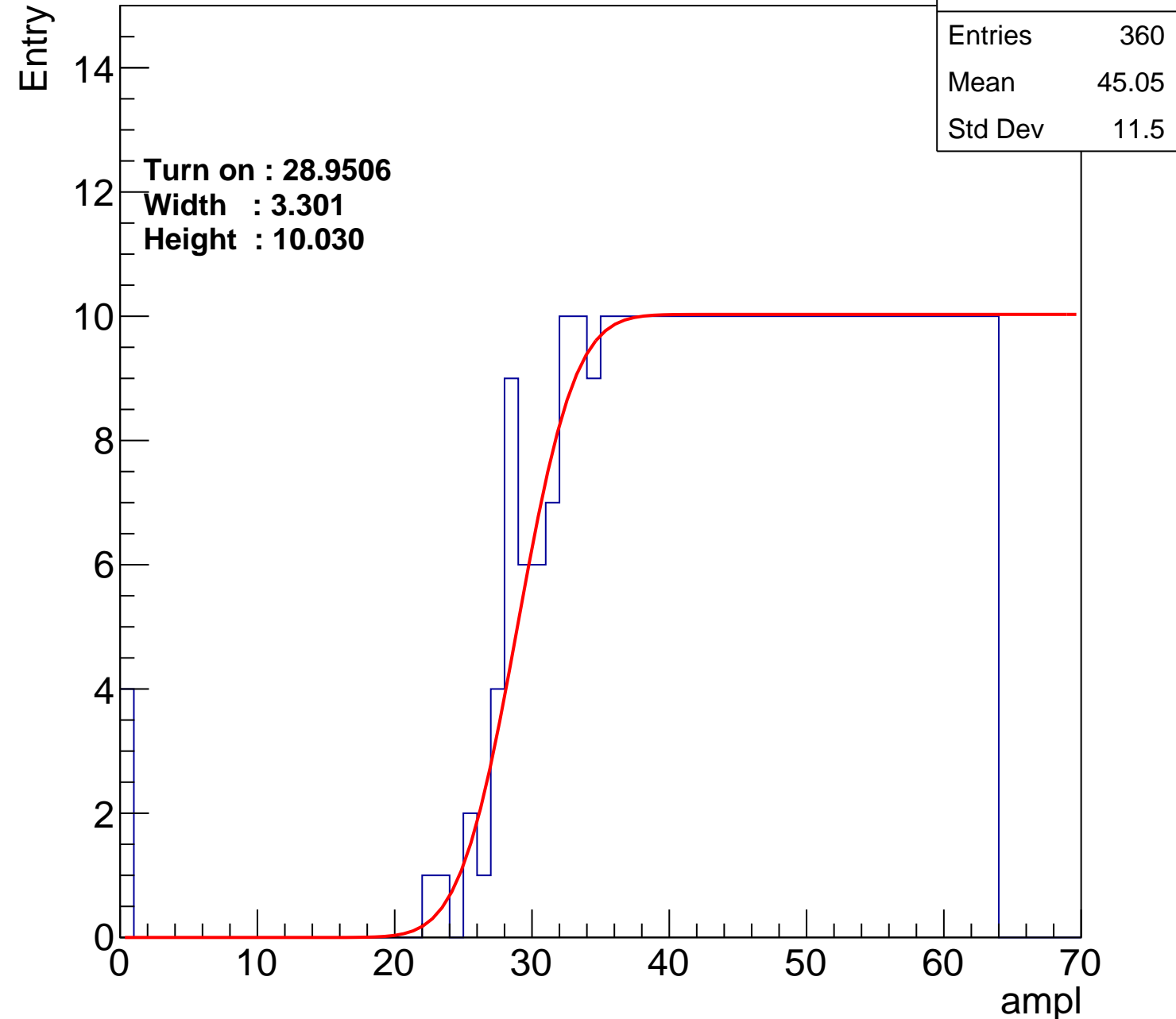
Width : 3.301

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.51
Std Dev	11.38

Turn on : 26.8340

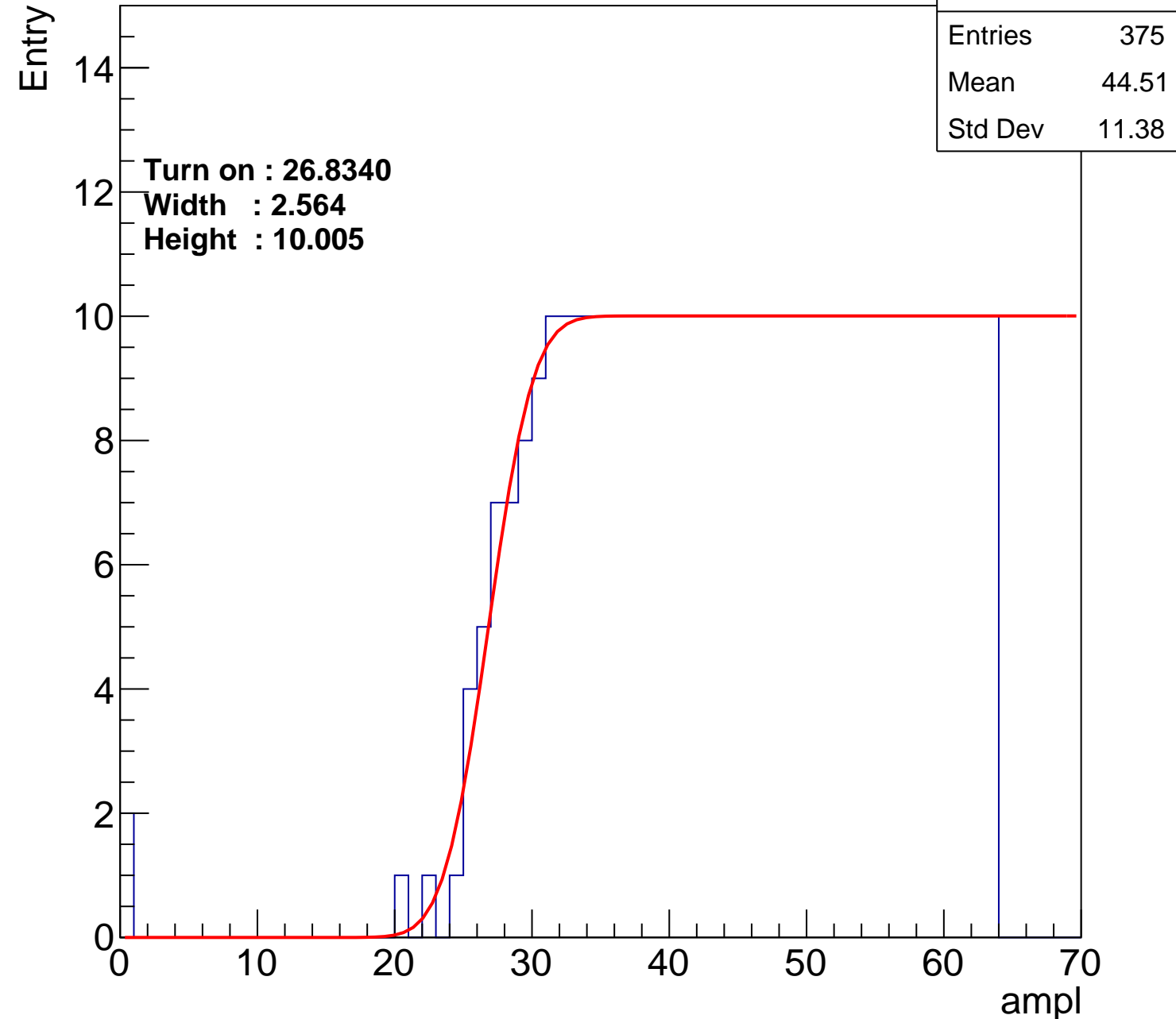
Width : 2.564

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.78
Std Dev	11.2

Turn on : 28.1651

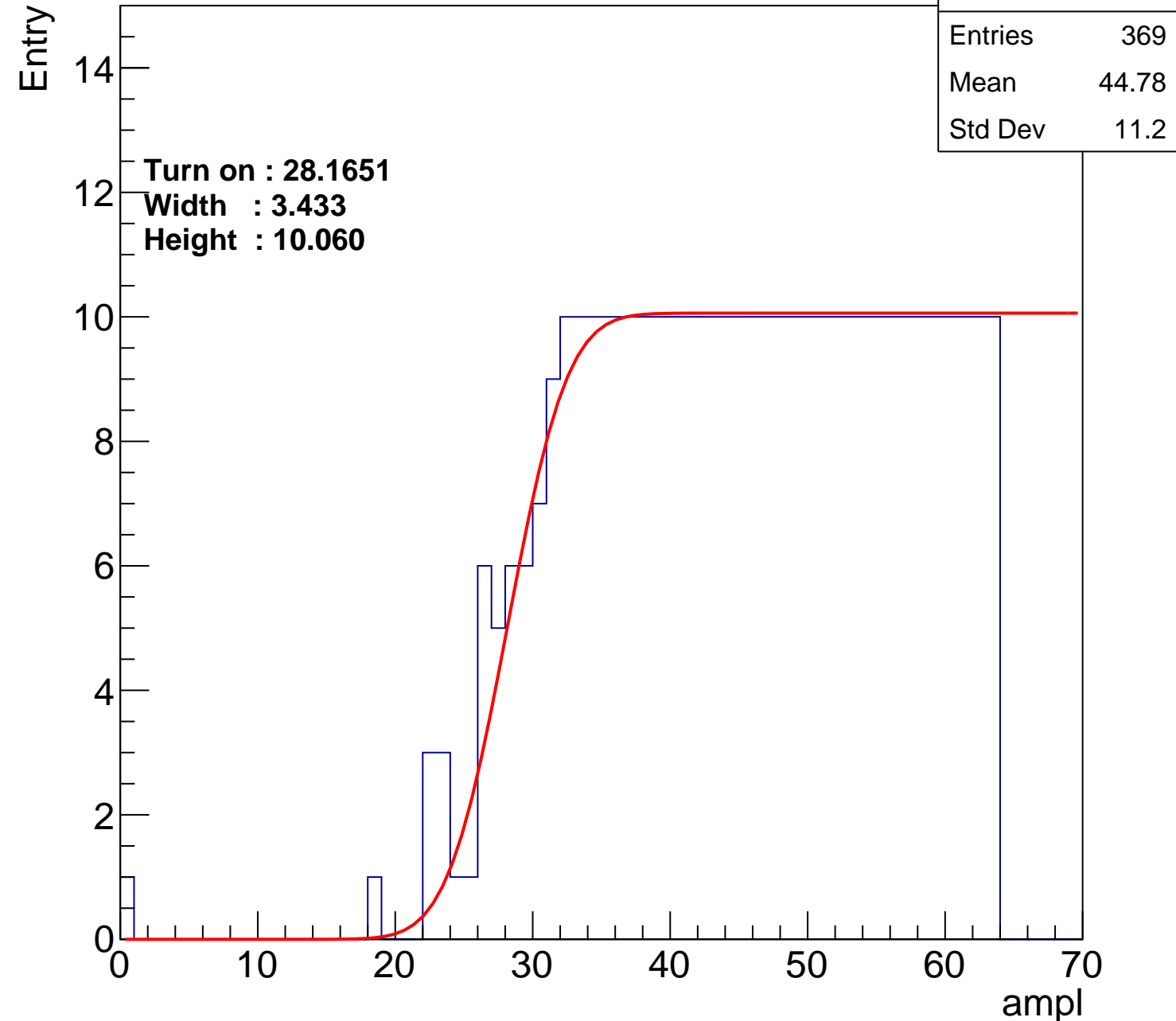
Width : 3.433

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U14-ch123

calib_packv5_042523_0143.root, FC#8, port C1

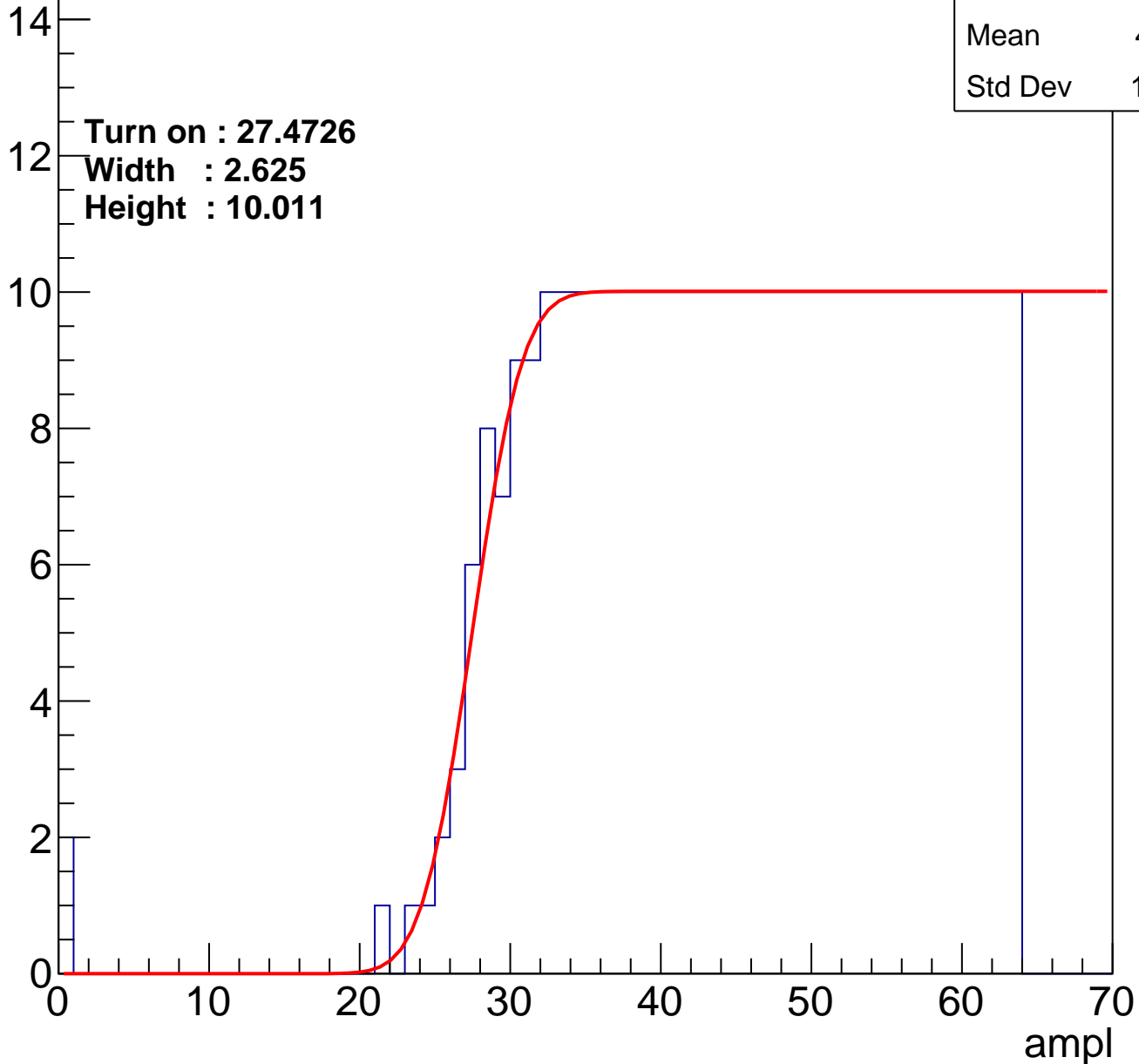
Entries	369
Mean	44.81
Std Dev	11.23

Turn on : 27.4726

Width : 2.625

Height : 10.011

Entry



B0L002S, U14-ch124

calib_packv5_042523_0143.root, FC#8, port C1

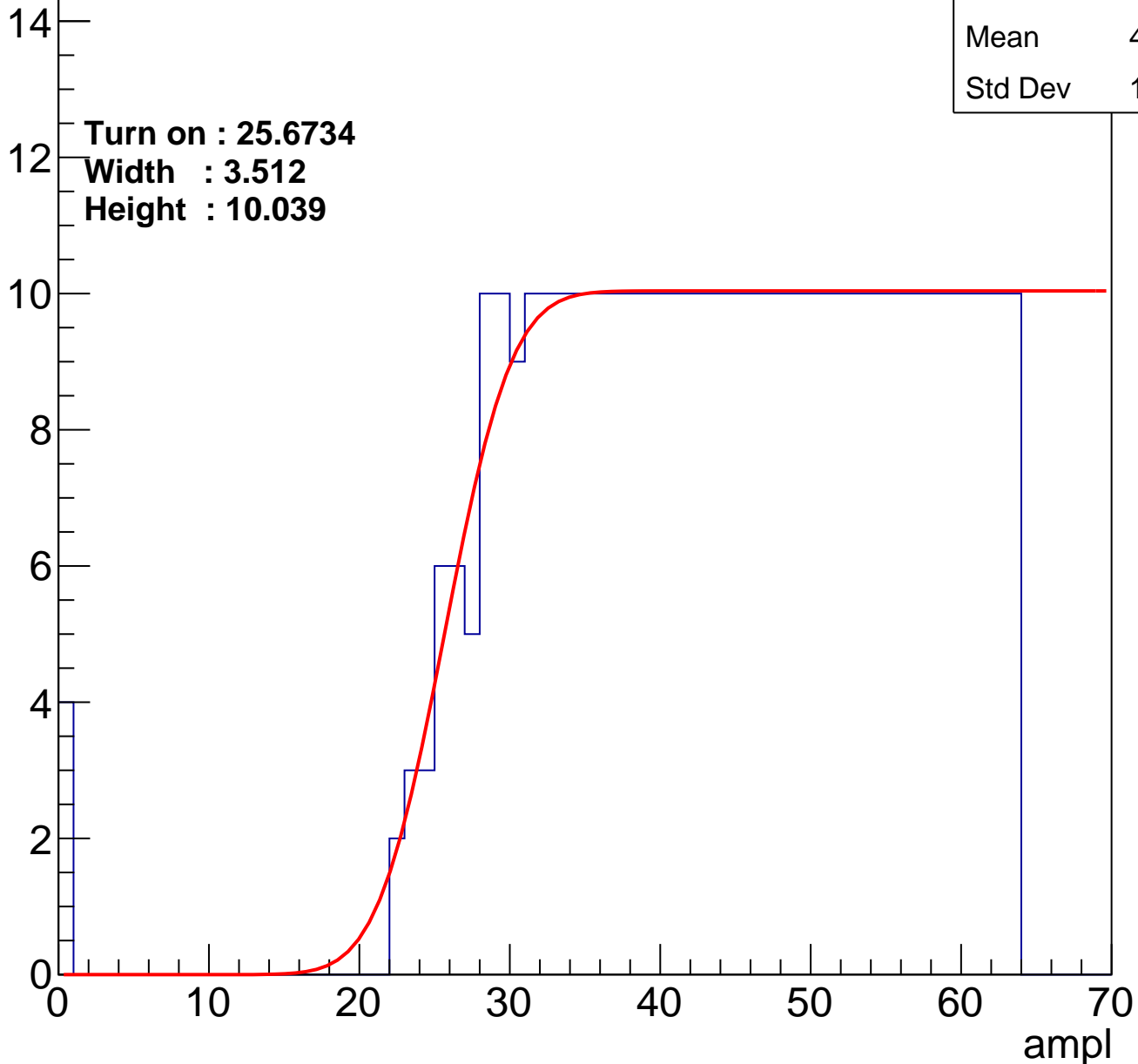
Entries	388
Mean	43.75
Std Dev	12.04

Turn on : 25.6734

Width : 3.512

Height : 10.039

Entry



B0L002S, U14-ch125

calib_packv5_042523_0143.root, FC#8, port C1

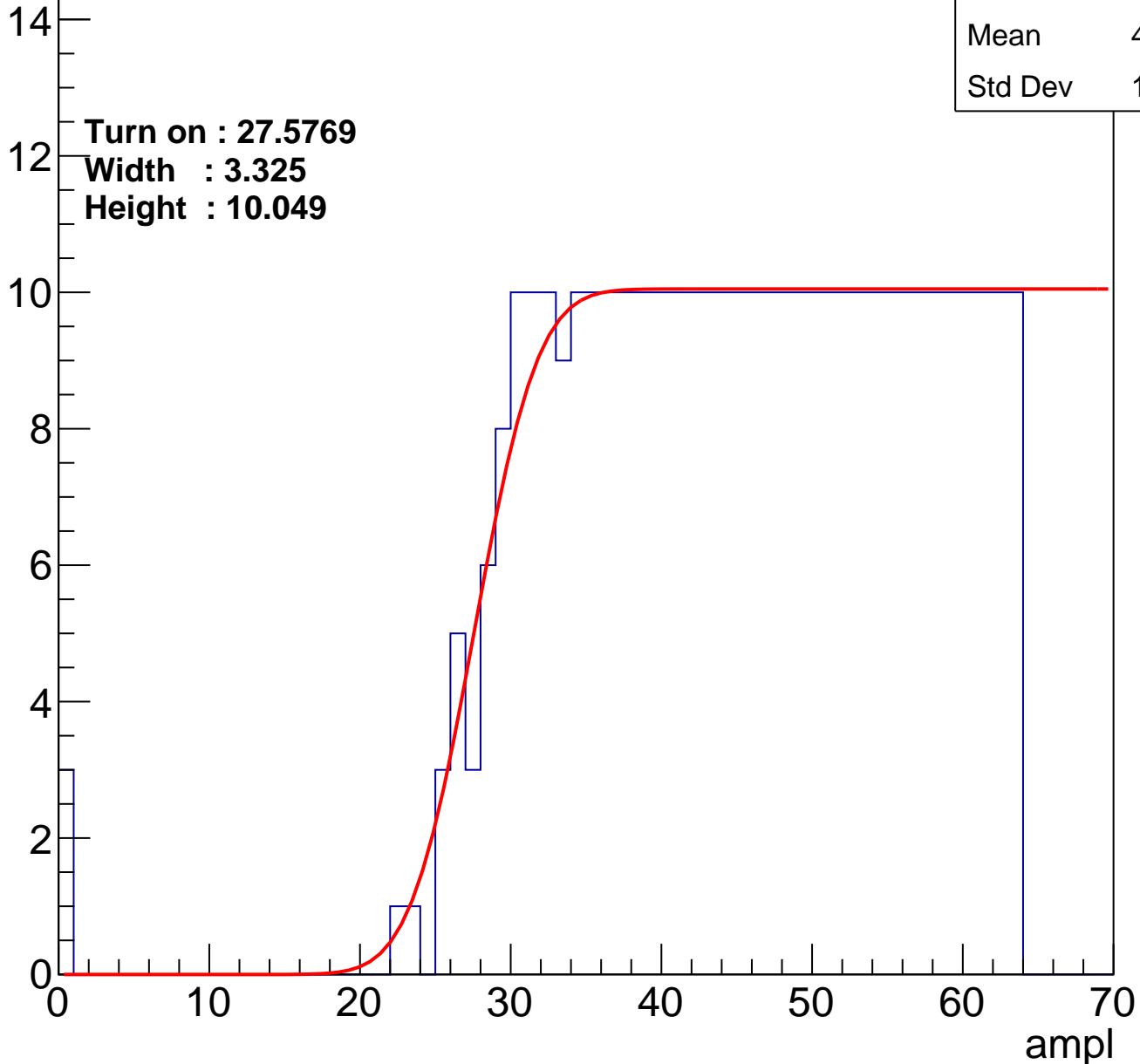
Entries	369
Mean	44.74
Std Dev	11.43

Turn on : 27.5769

Width : 3.325

Height : 10.049

Entry



B0L002S, U14-ch126

calib_packv5_042523_0143.root, FC#8, port C1

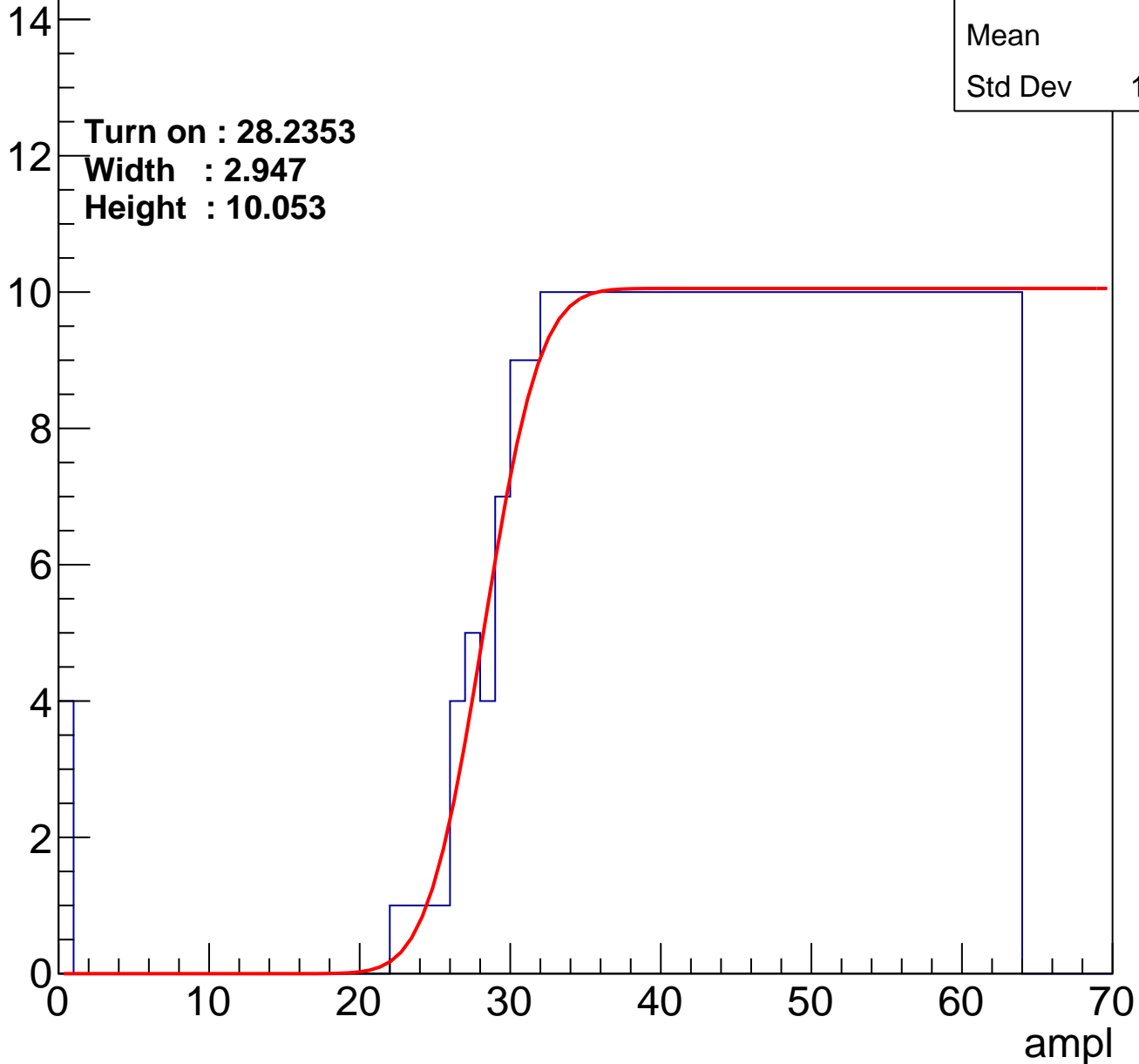
Entries	366
Mean	44.8
Std Dev	11.57

Turn on : 28.2353

Width : 2.947

Height : 10.053

Entry



B0L002S, U14-ch127

calib_packv5_042523_0143.root, FC#8, port C1

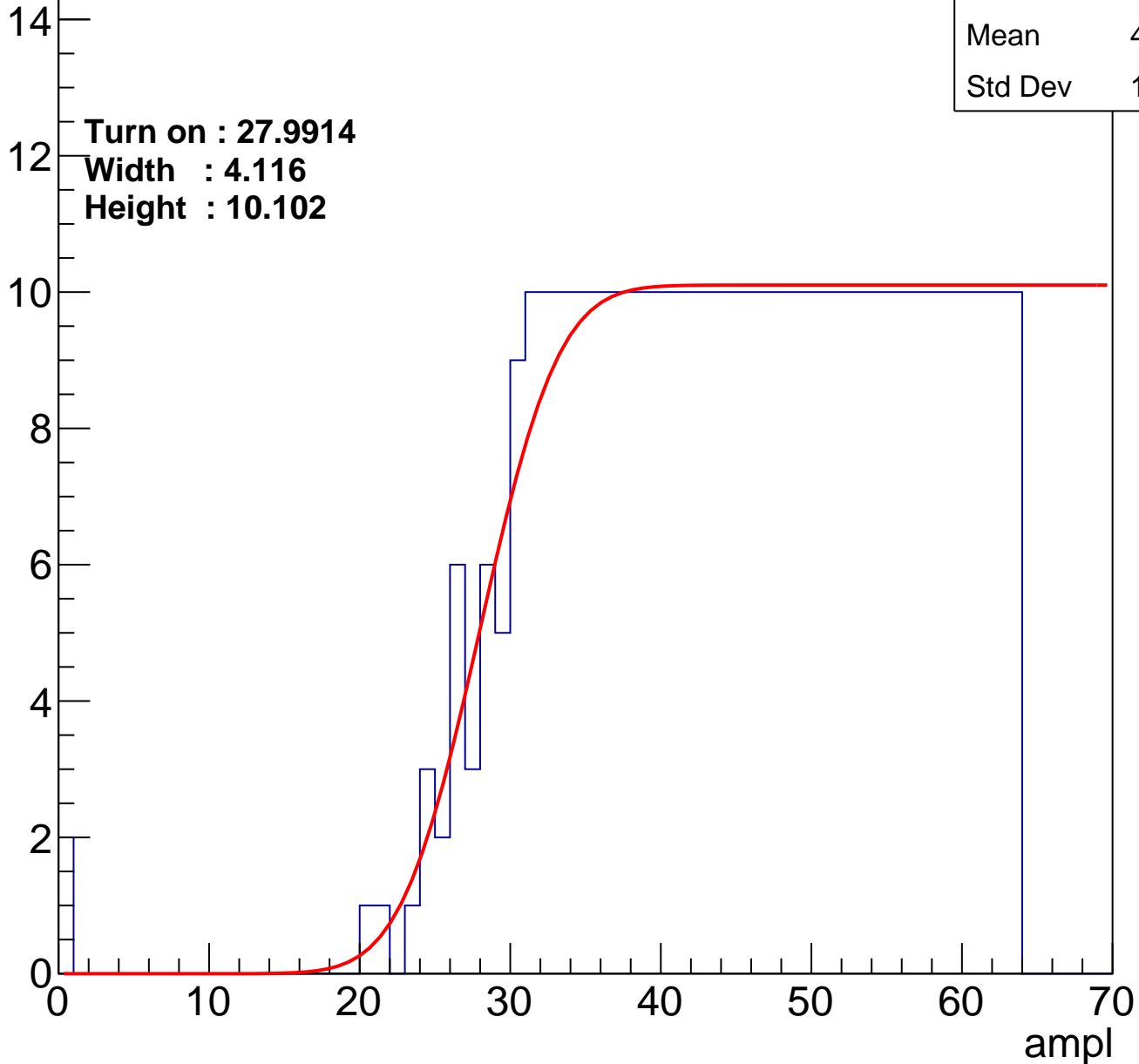
Entries	369
Mean	44.76
Std Dev	11.32

Turn on : 27.9914

Width : 4.116

Height : 10.102

Entry



B0L002S, U14-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.76
Std Dev	11.32

Turn on : 27.9914

Width : 4.116

Height : 10.102

Entry

