

B0L102S, U1-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.16
Std Dev	11.43

Turn on : 25.8240

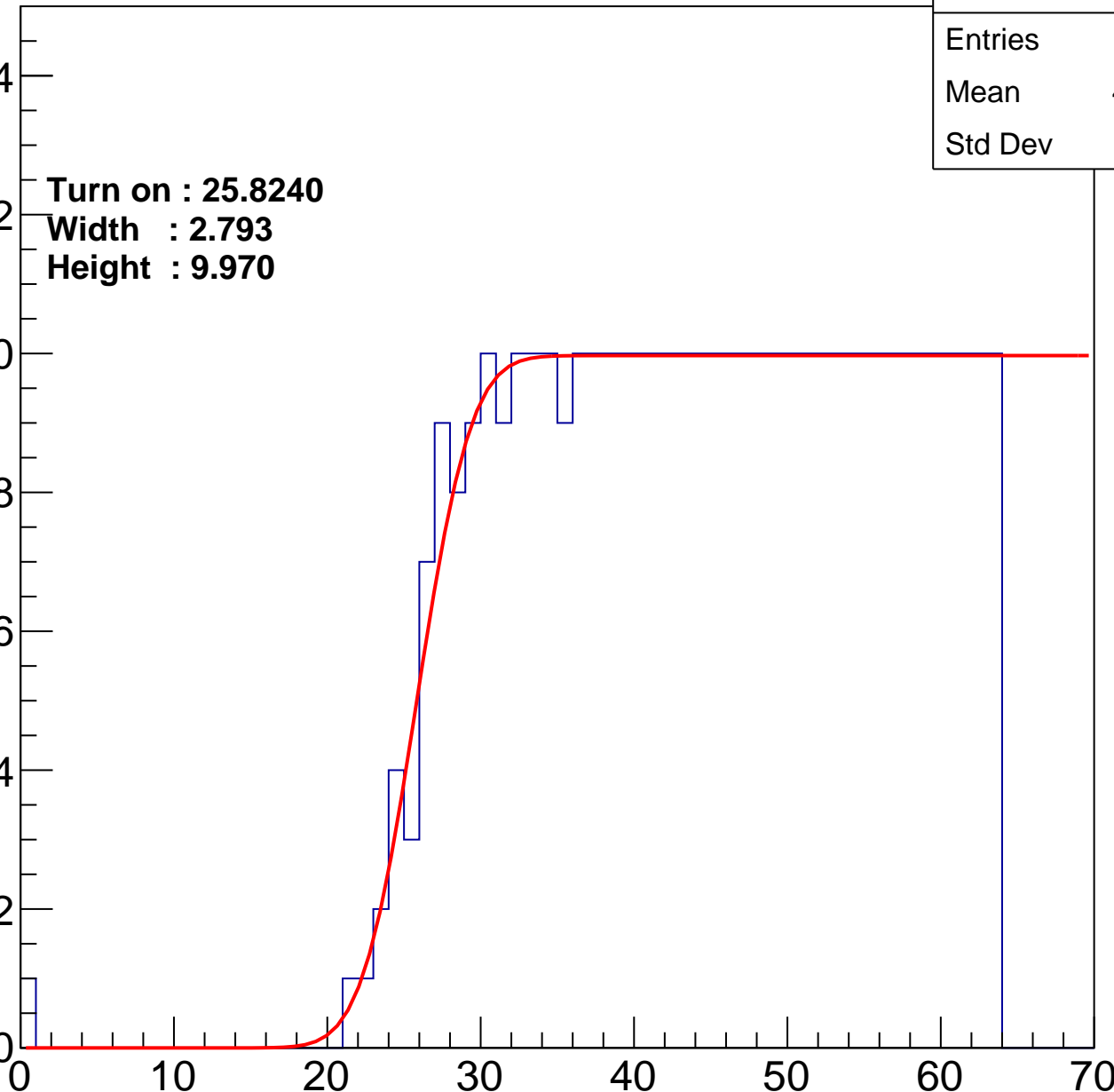
Width : 2.793

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.42
Std Dev	11.62

Turn on : 28.2587

Width : 3.225

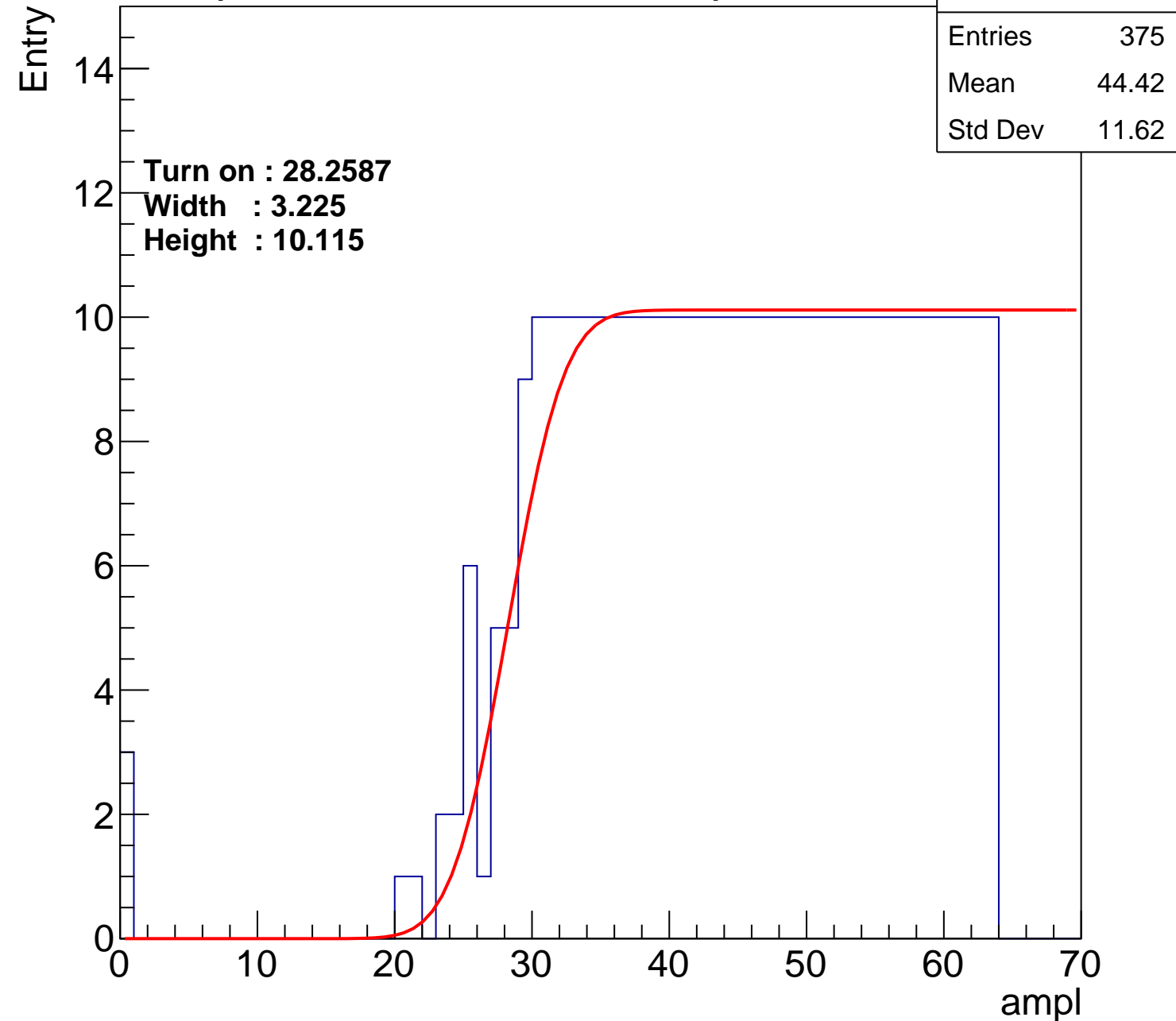
Height : 10.115

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U1-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 25.6393

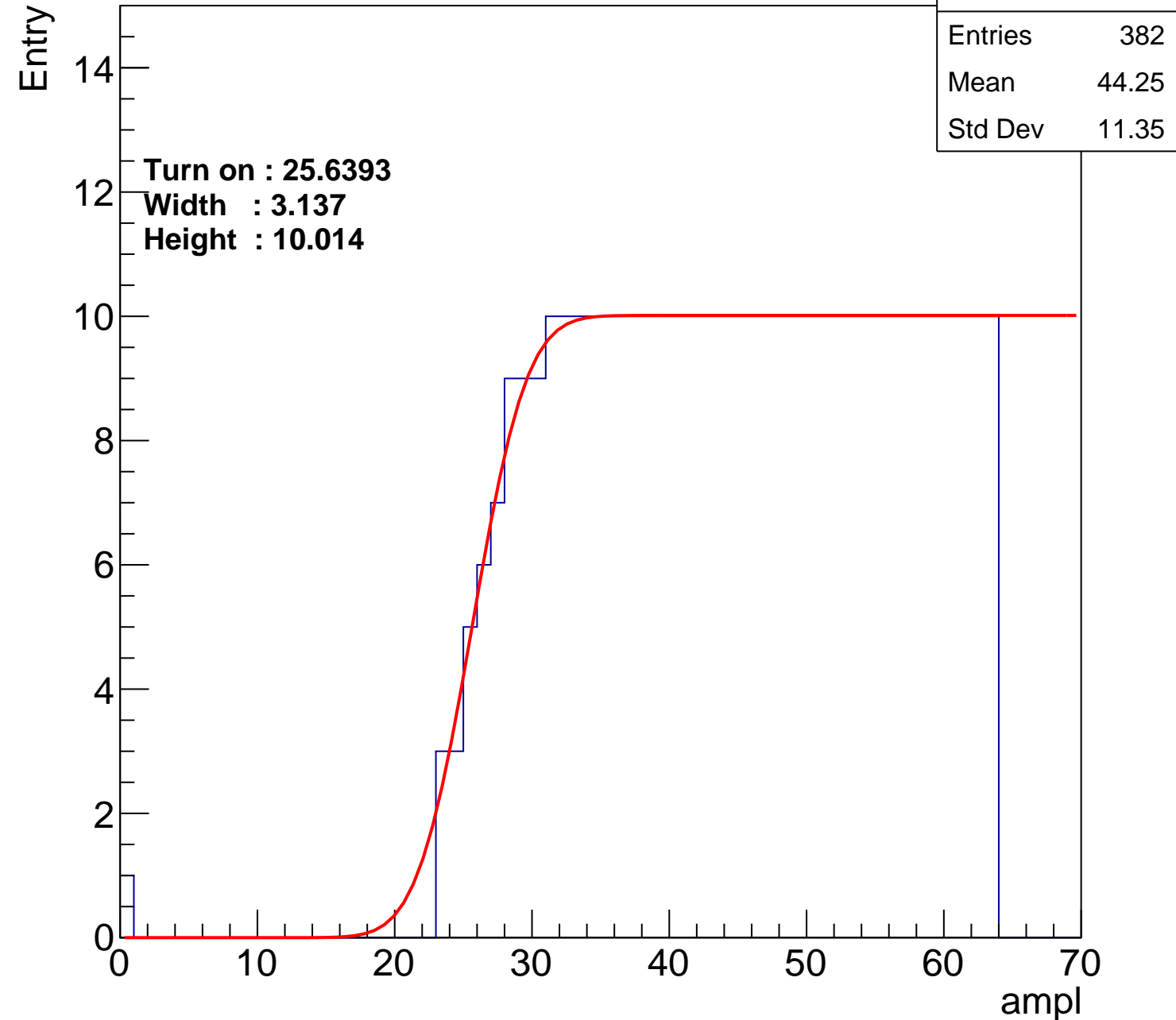
Width : 3.137

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch3

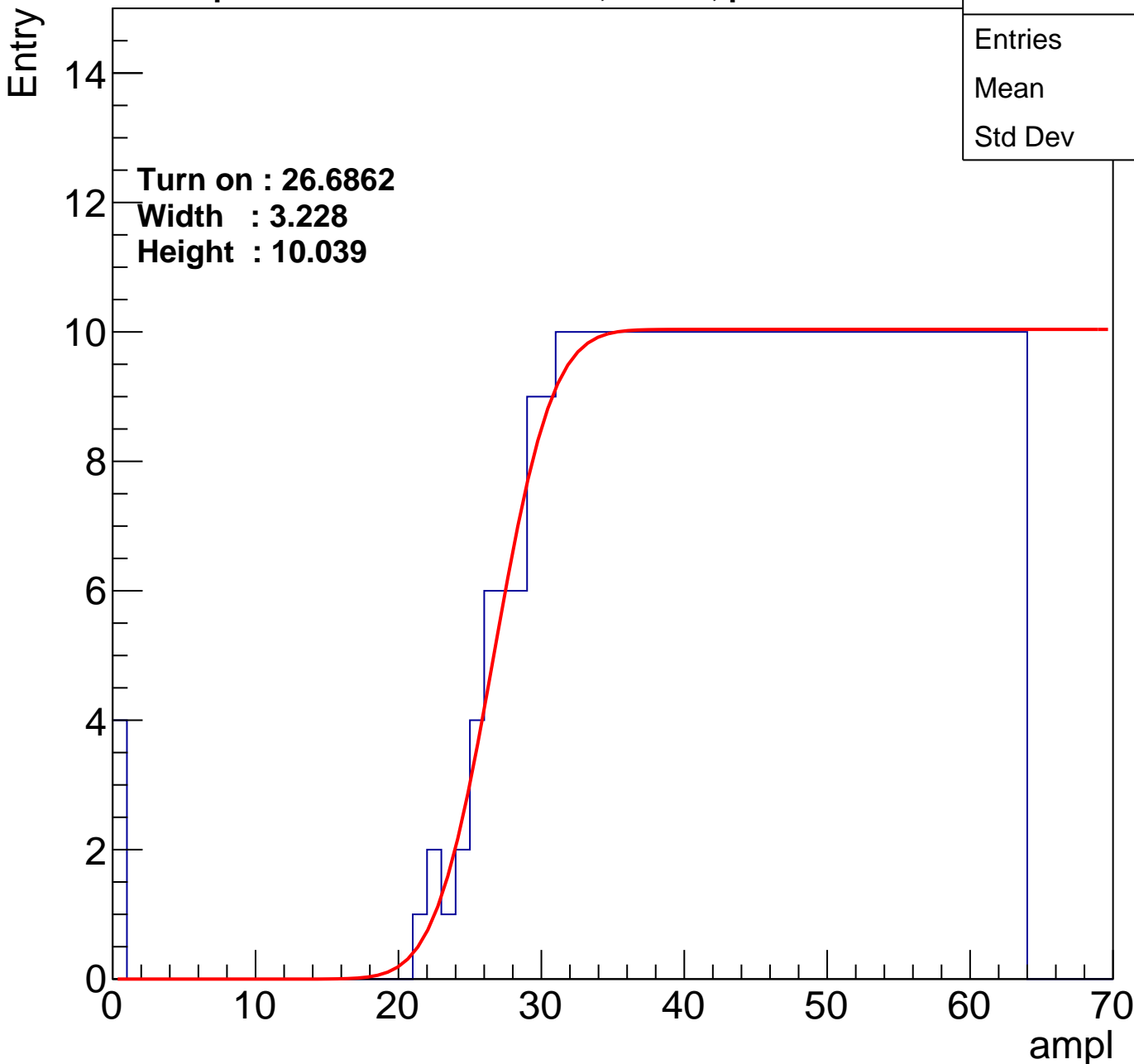
calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.11
Std Dev	11.9

Turn on : 26.6862

Width : 3.228

Height : 10.039



B0L102S, U1-ch4

calib_packv5_042523_0143.root, FC#12, port B1

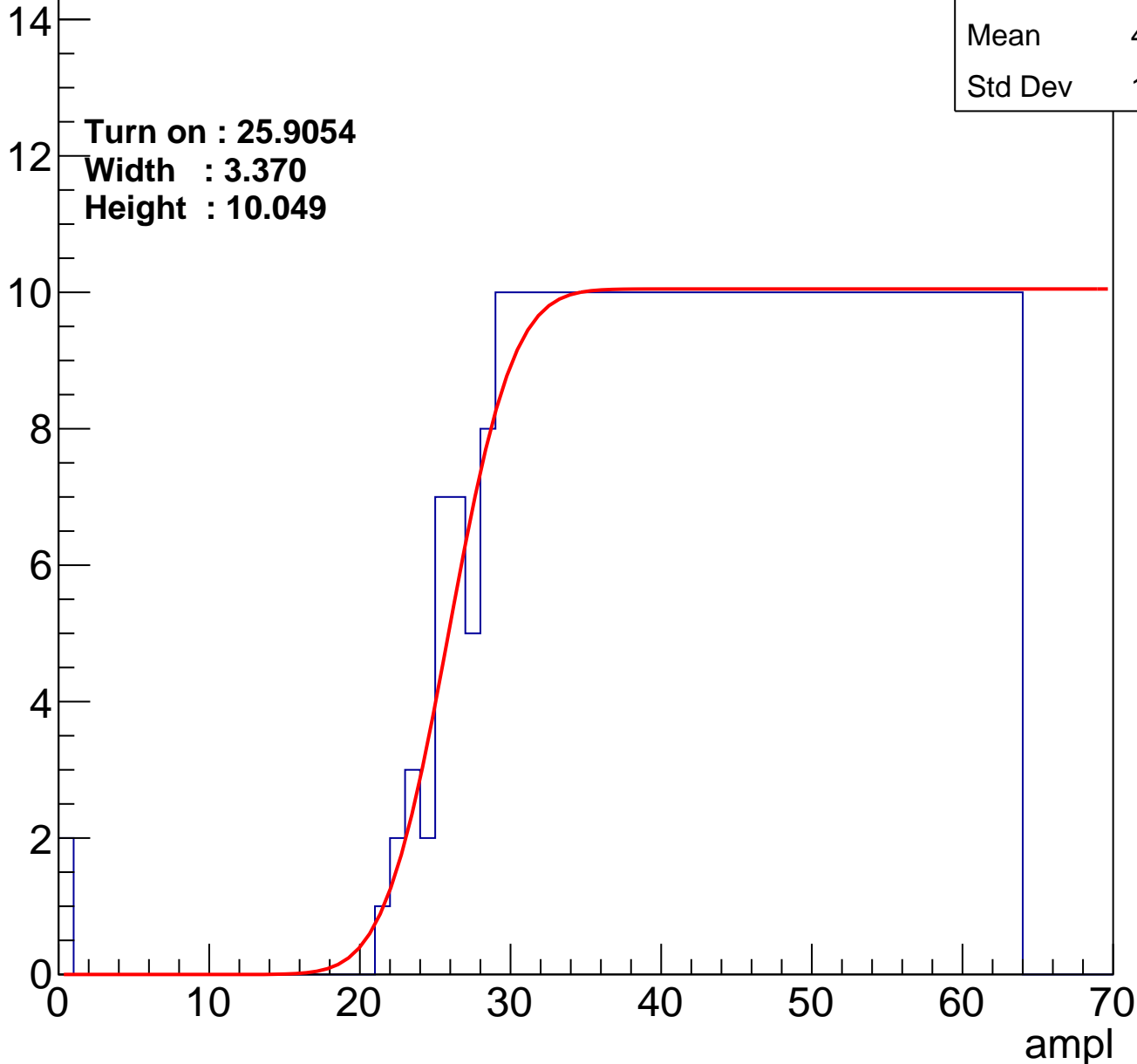
Entries	387
Mean	43.92
Std Dev	11.69

Turn on : 25.9054

Width : 3.370

Height : 10.049

Entry



B0L102S, U1-ch5

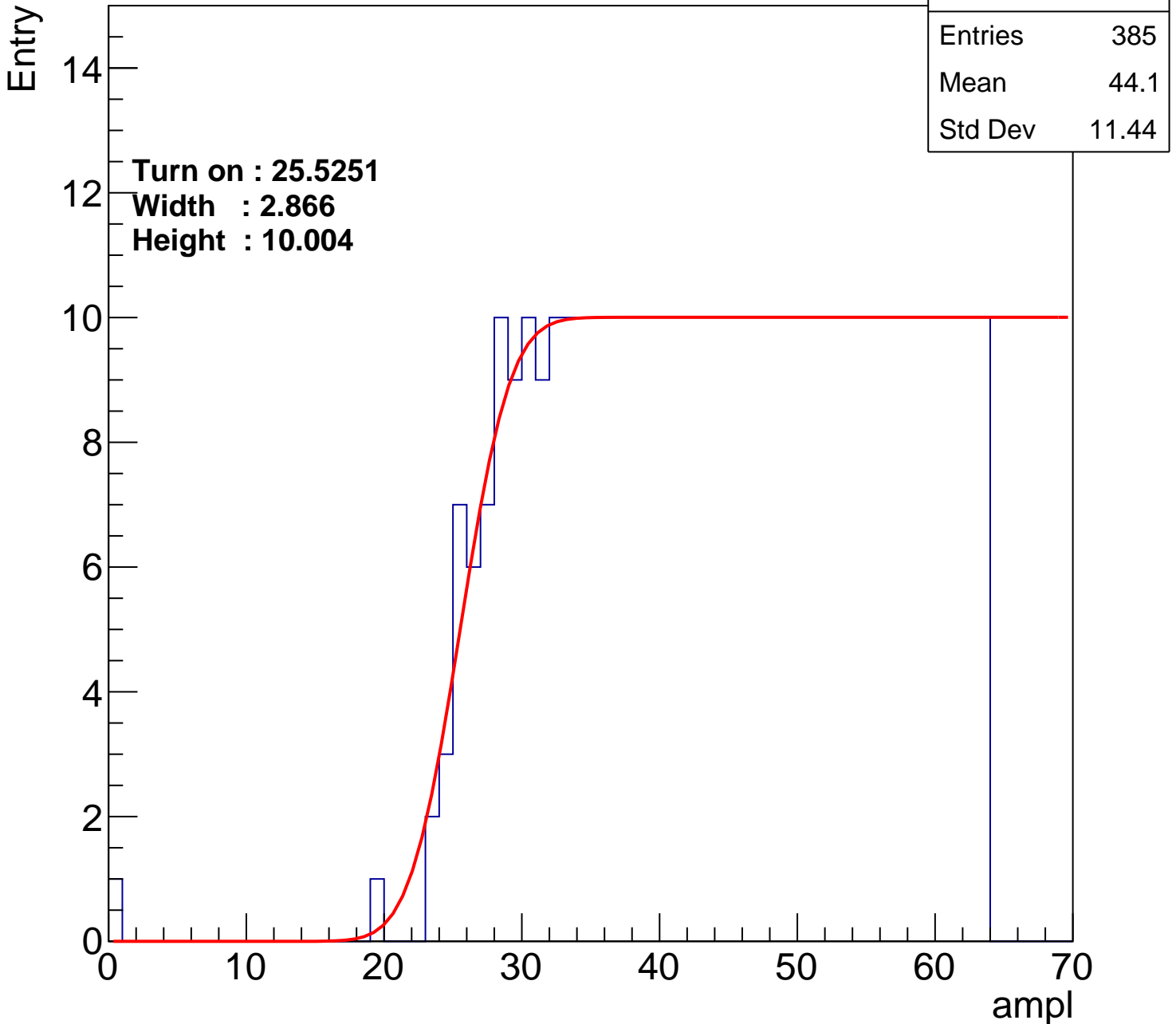
calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.5251

Width : 2.866

Height : 10.004



B0L102S, U1-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.93
Std Dev	11.91

Turn on : 26.2443

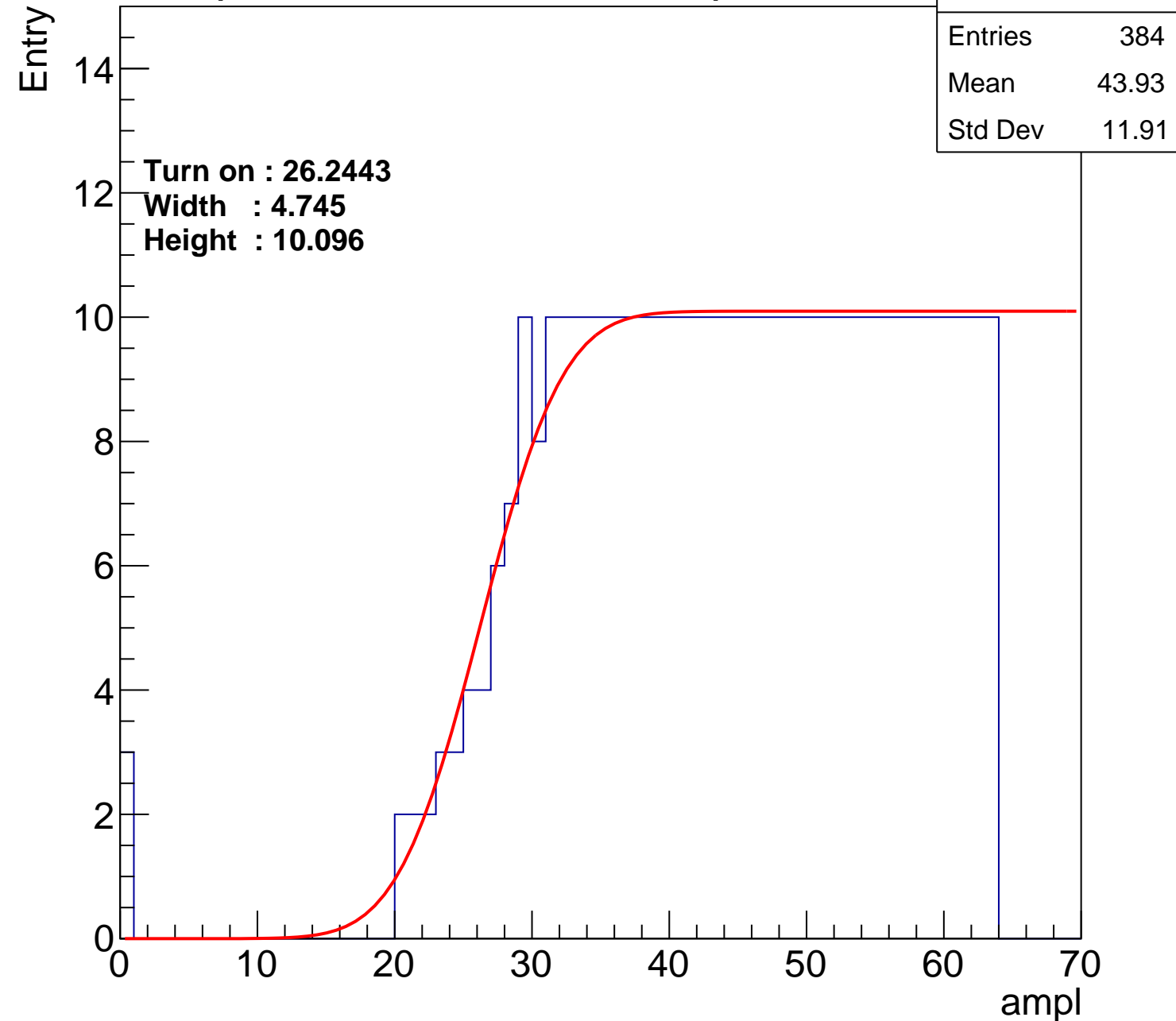
Width : 4.745

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.66
Std Dev	12.25

Turn on : 26.0745

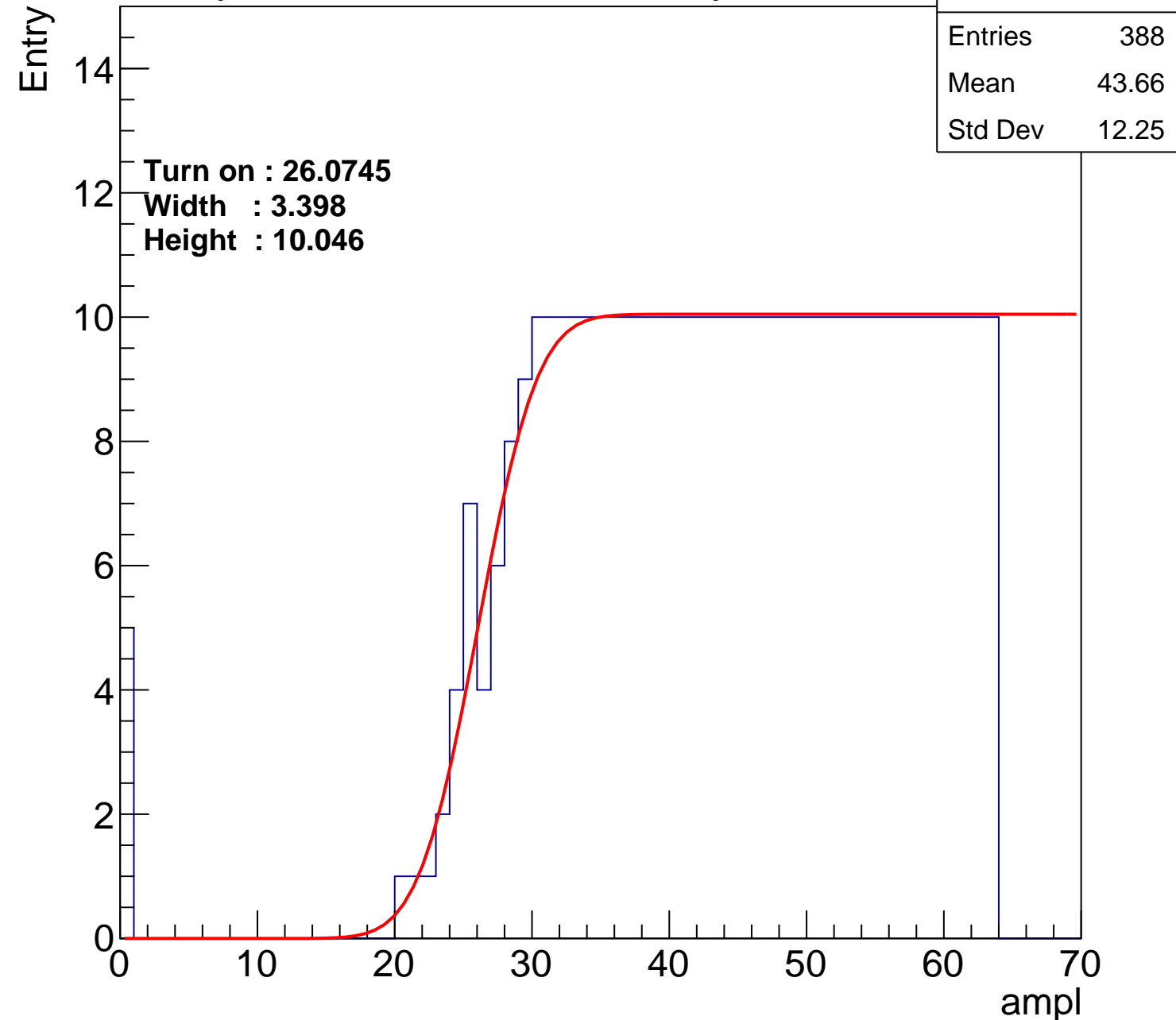
Width : 3.398

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch8

calib_packv5_042523_0143.root, FC#12, port B1

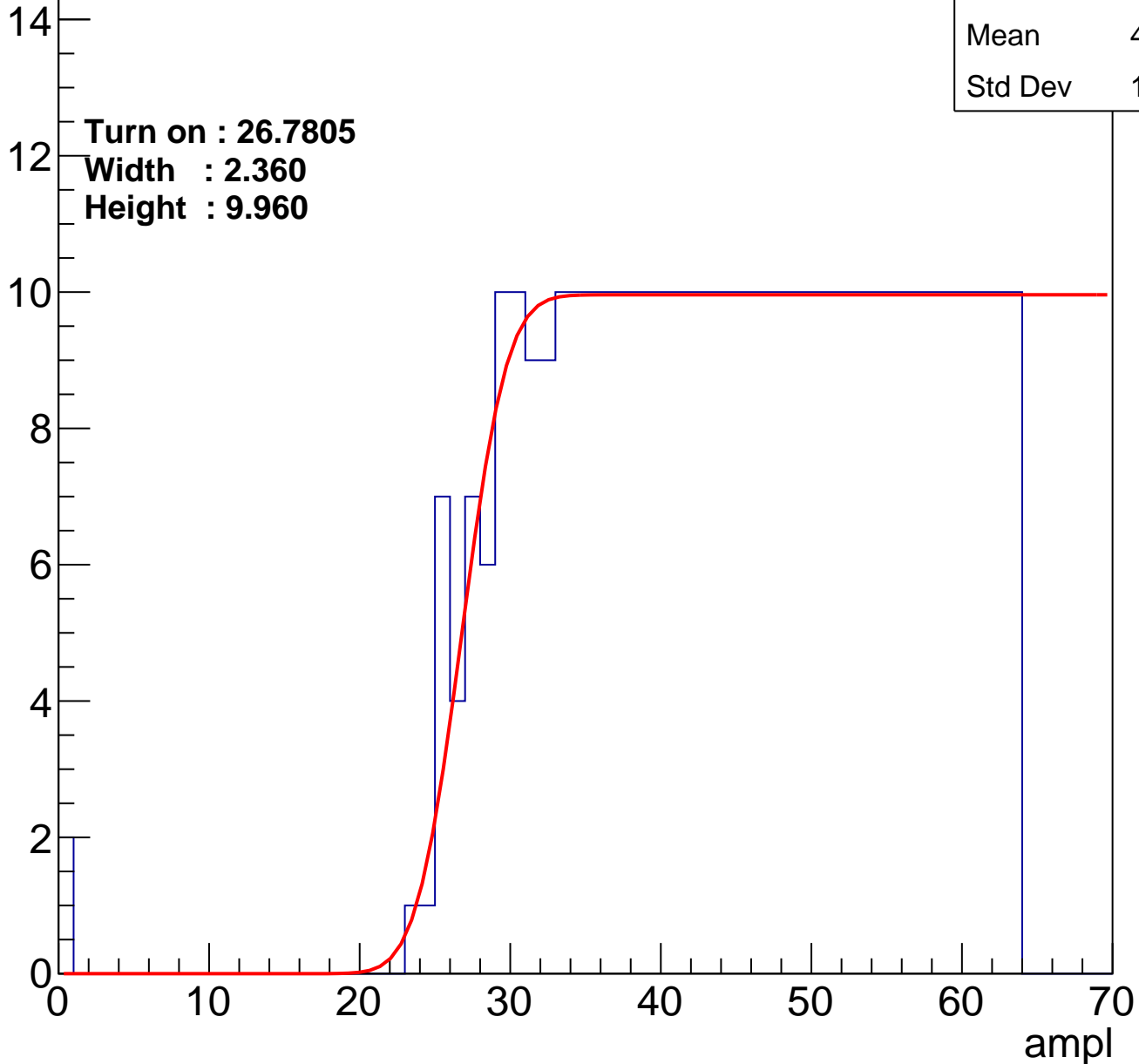
Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.7805

Width : 2.360

Height : 9.960

Entry



B0L102S, U1-ch9

calib_packv5_042523_0143.root, FC#12, port B1

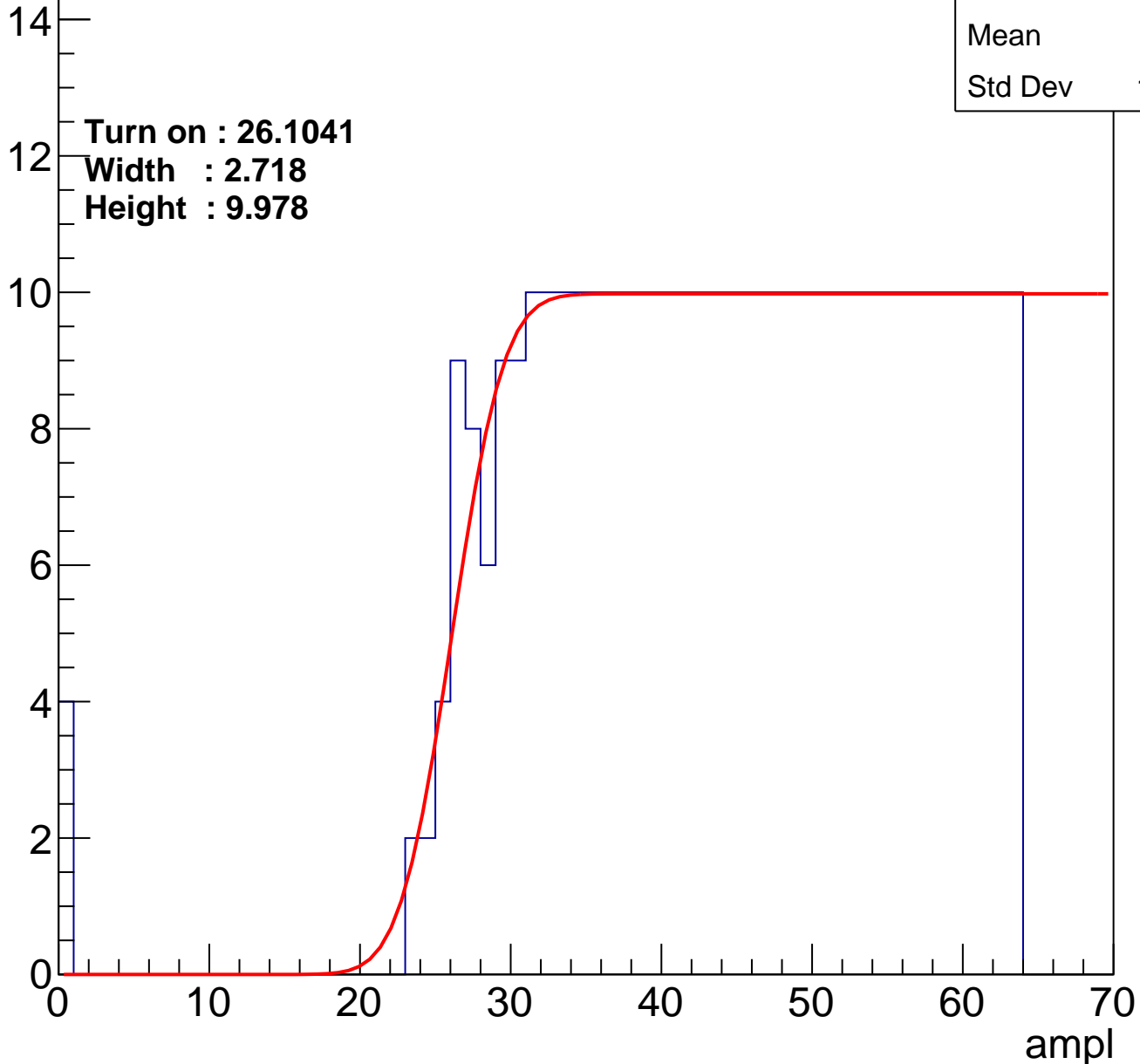
Entries	383
Mean	44
Std Dev	11.91

Turn on : 26.1041

Width : 2.718

Height : 9.978

Entry



B0L102S, U1-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.4
Std Dev	11.52

Turn on : 26.9782

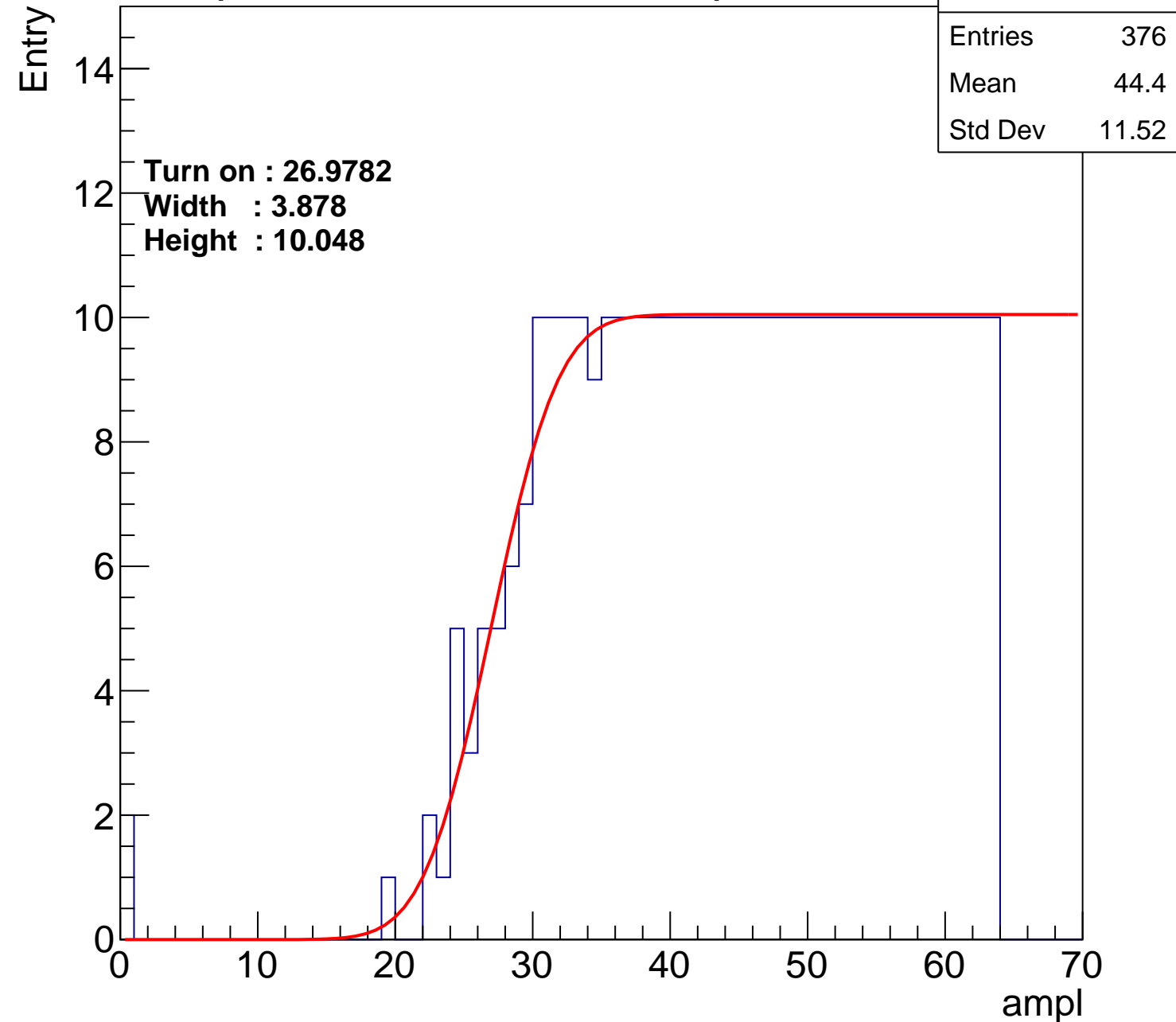
Width : 3.878

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.95
Std Dev	11.81

Turn on : 25.7381

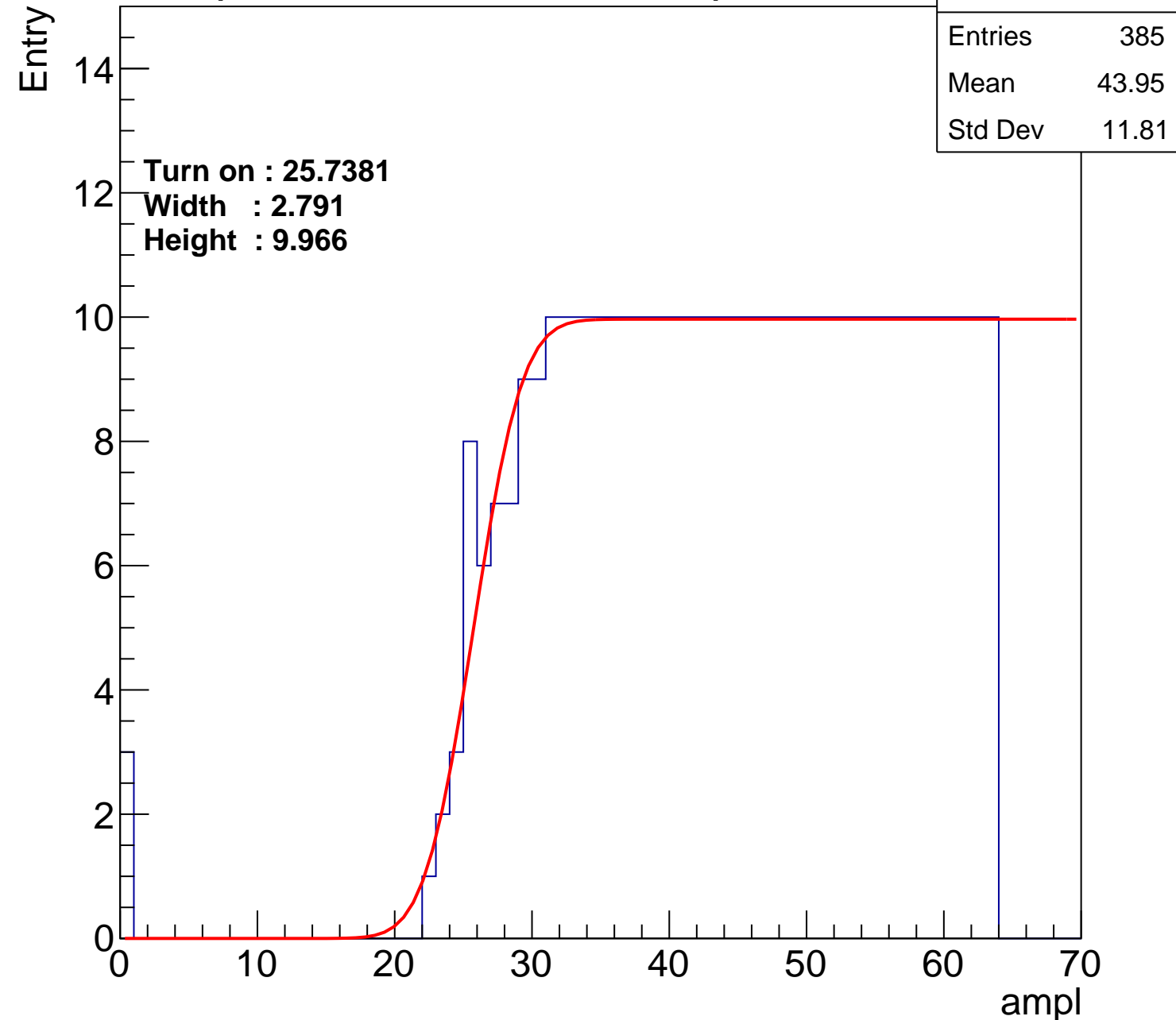
Width : 2.791

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.67
Std Dev	11.71

Turn on : 25.2072

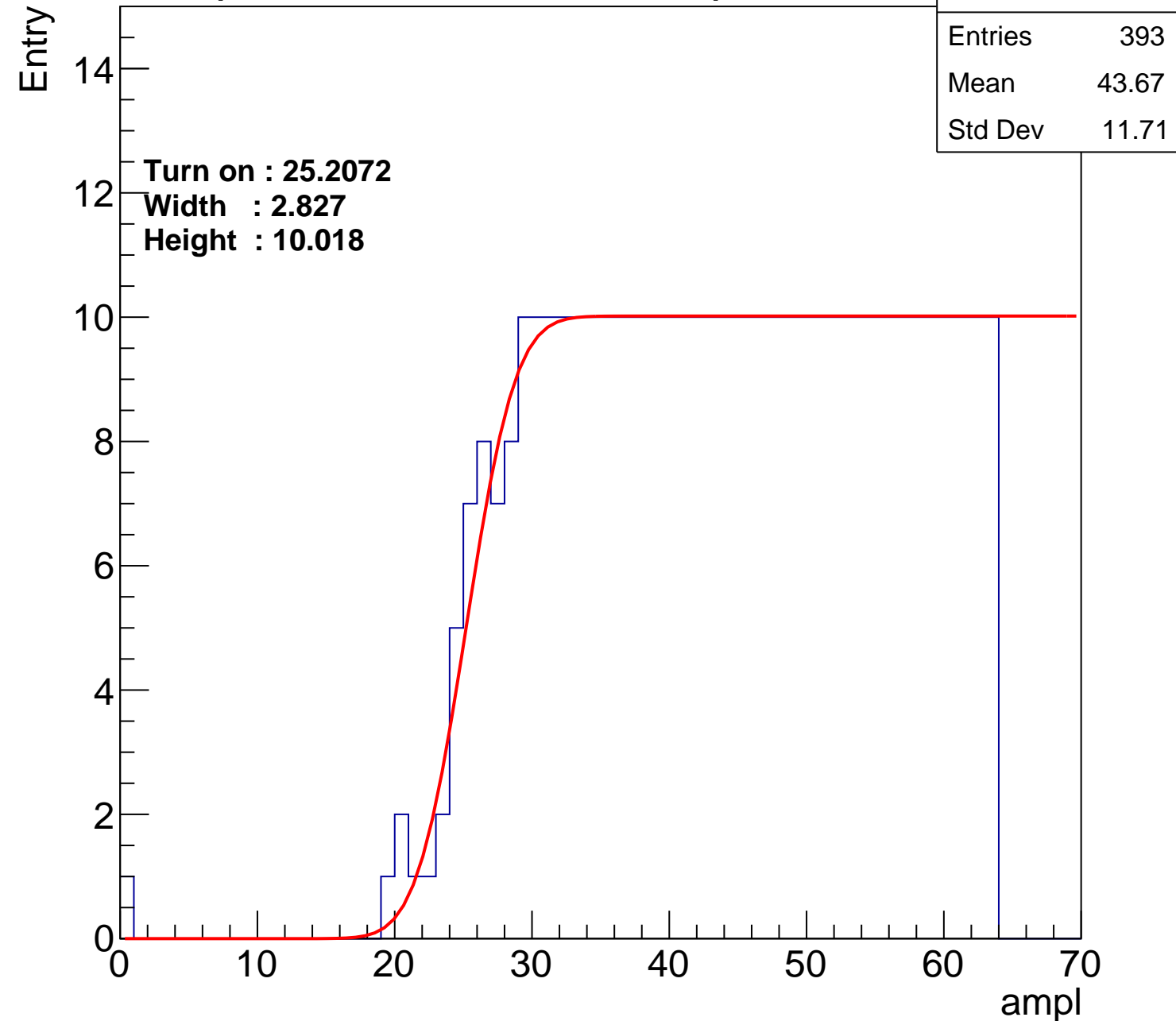
Width : 2.827

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 24.6267

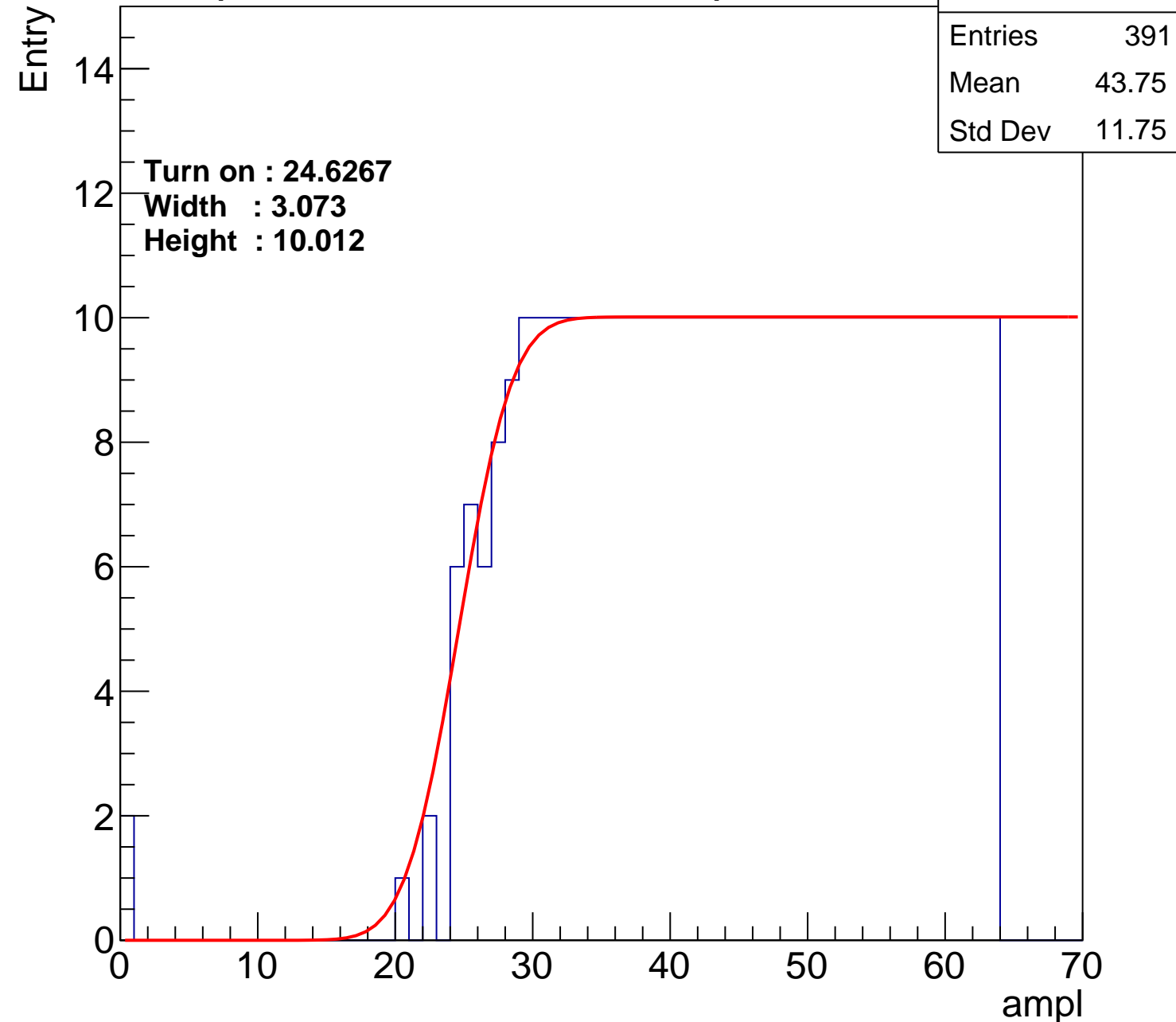
Width : 3.073

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.33
Std Dev	11.6

Turn on : 26.4801

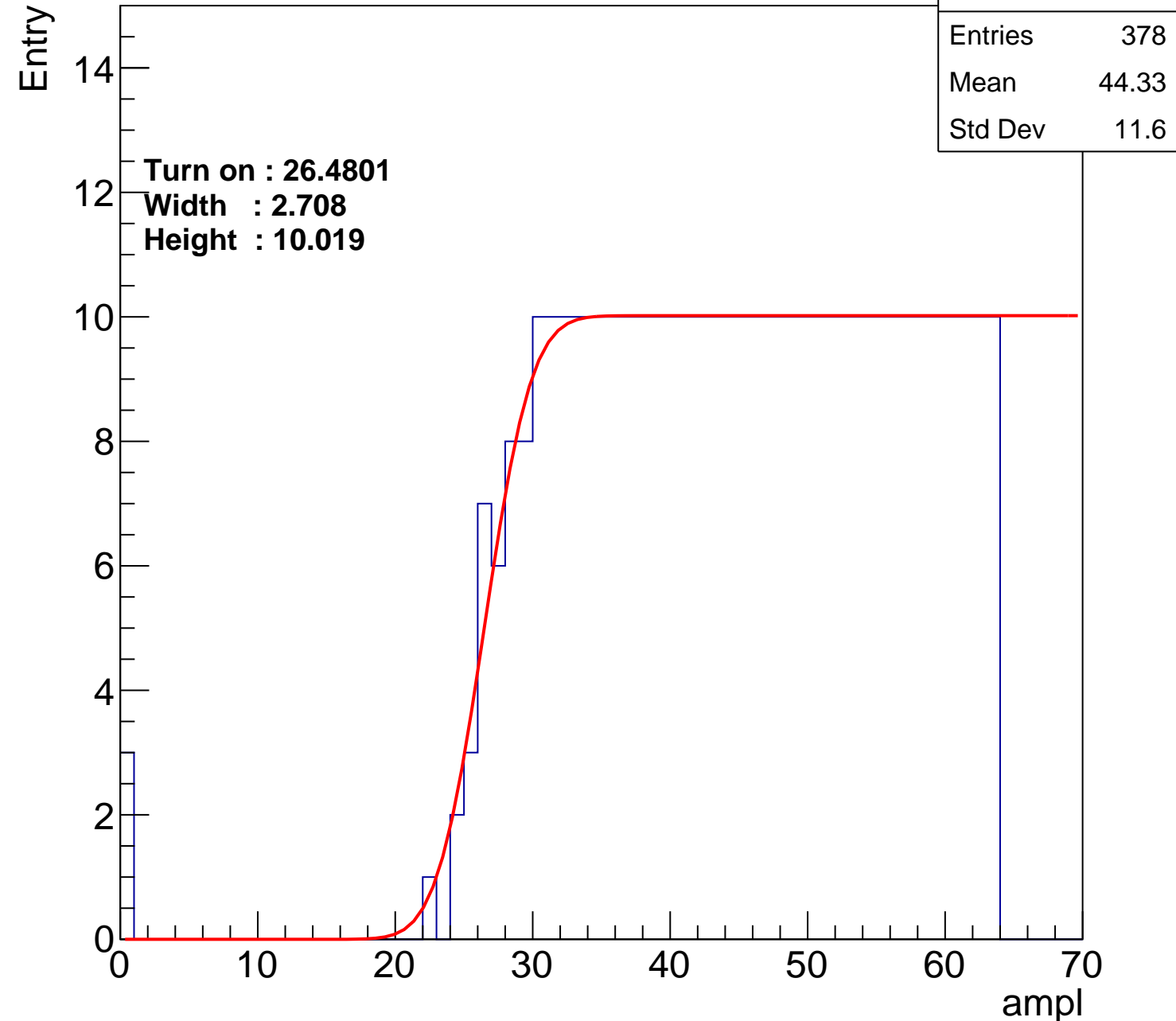
Width : 2.708

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.93
Std Dev	10.99

Turn on : 27.8161

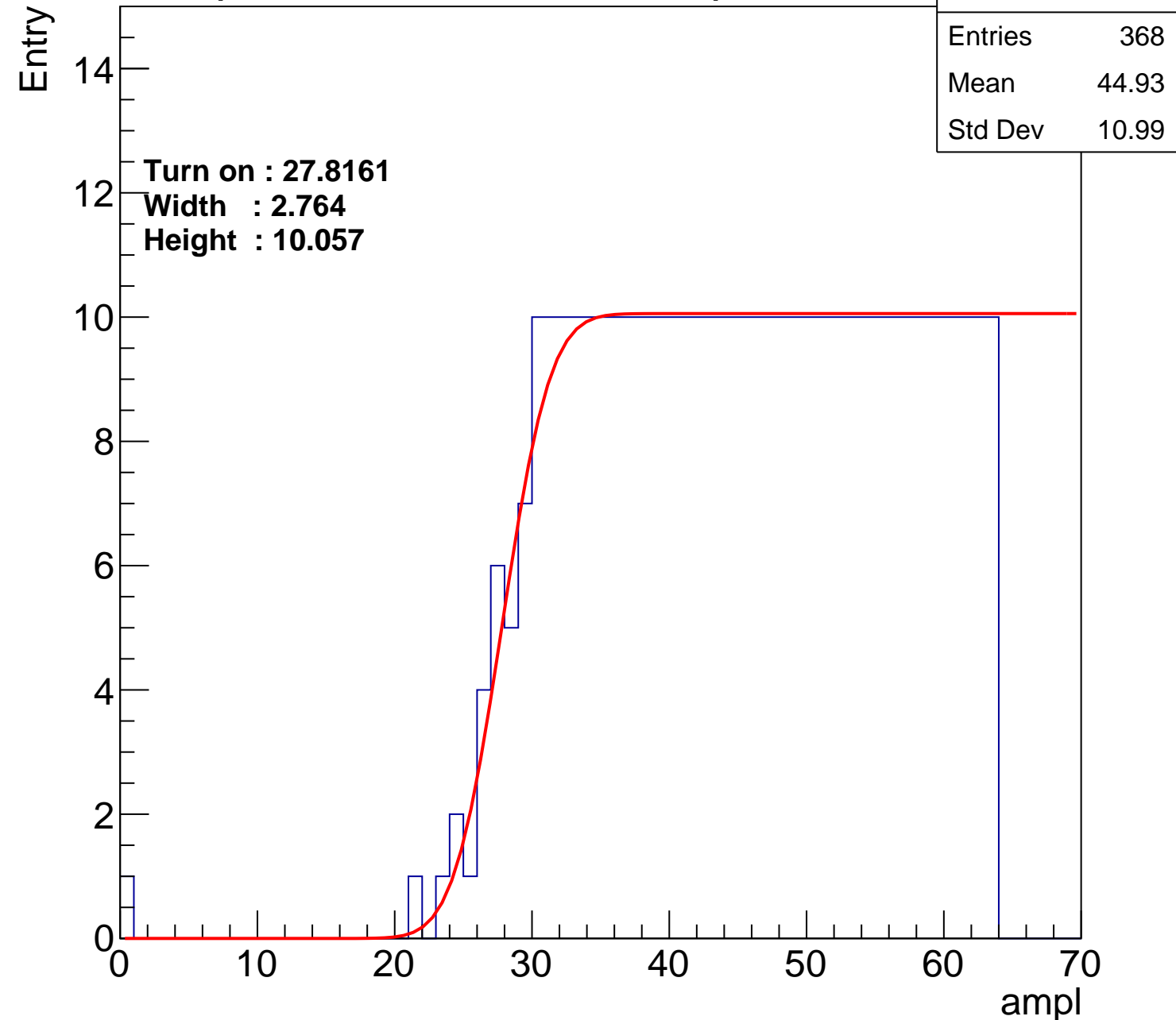
Width : 2.764

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.63
Std Dev	12.01

Turn on : 25.3125

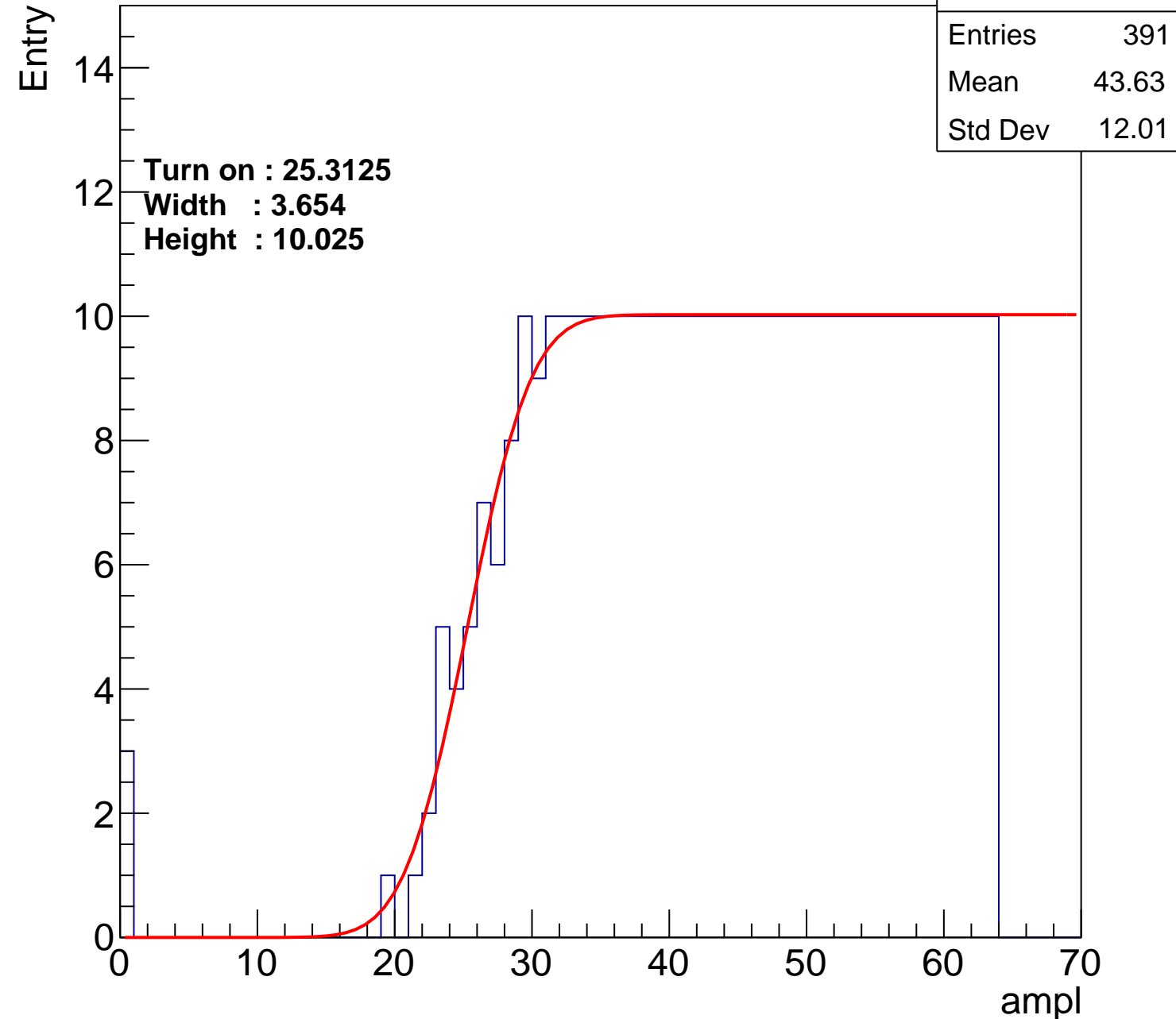
Width : 3.654

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.52
Std Dev	11.91

Turn on : 24.8577

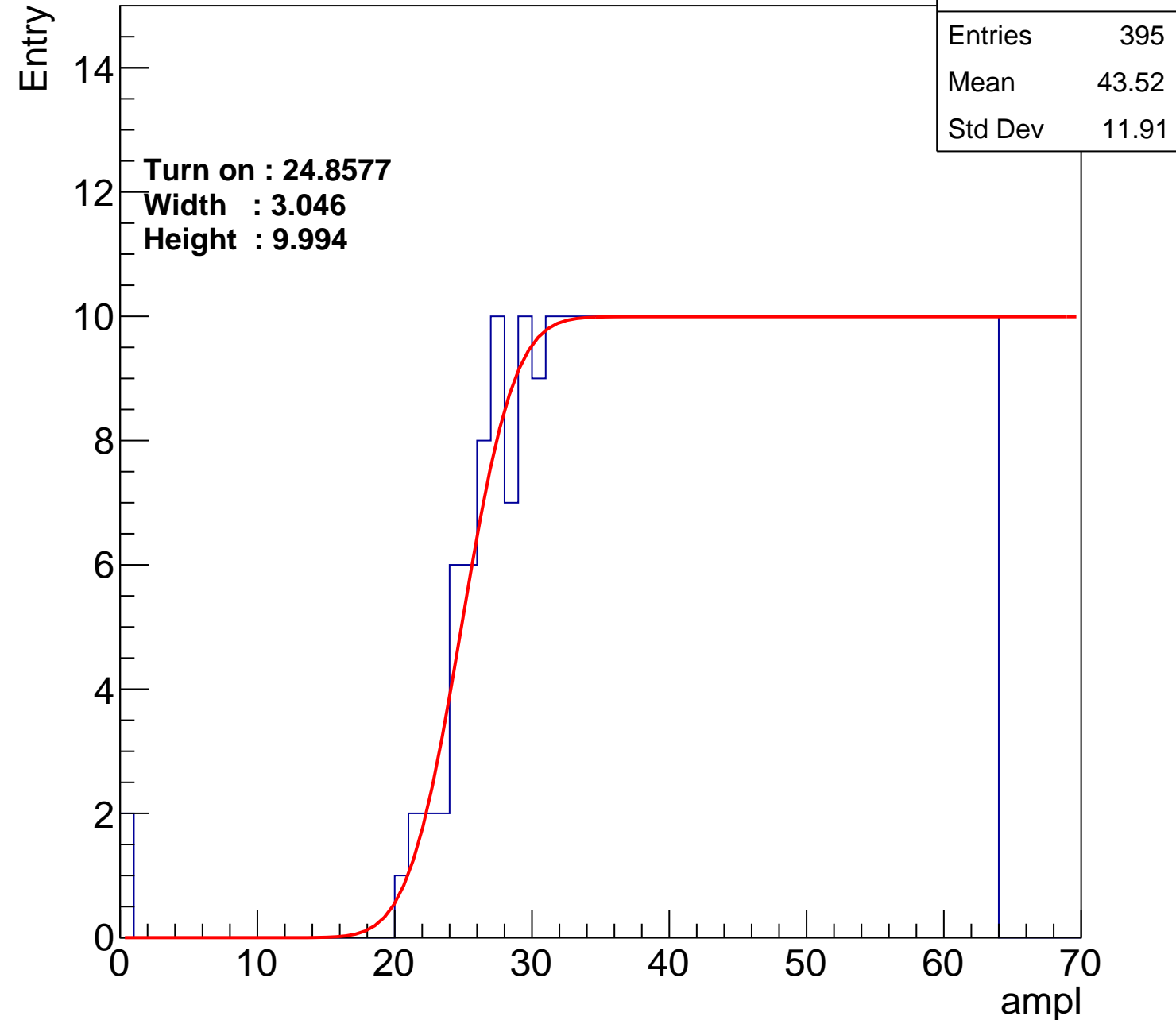
Width : 3.046

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.51
Std Dev	11.81

Turn on : 24.7930

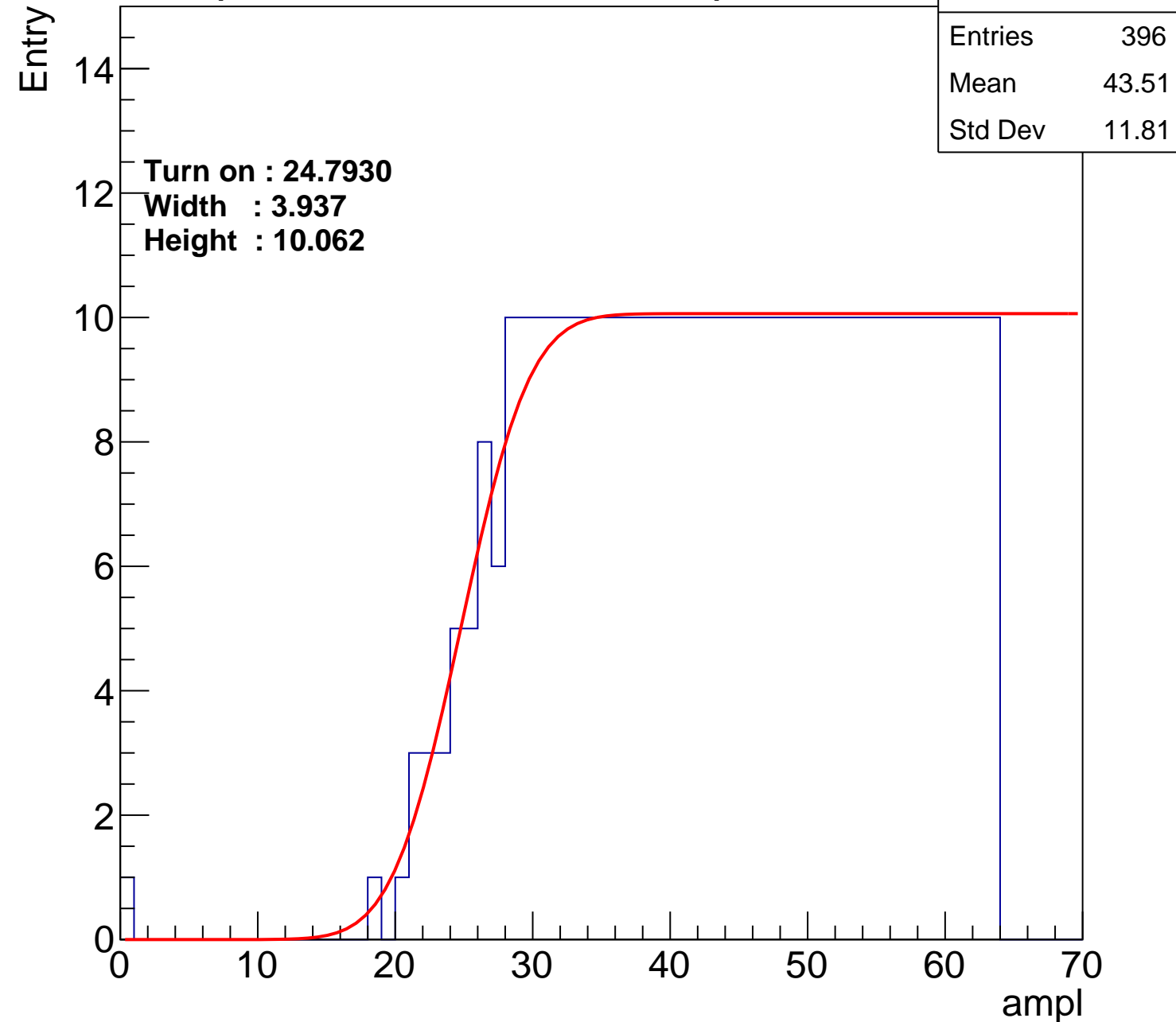
Width : 3.937

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.76
Std Dev	11.44

Turn on : 27.9333

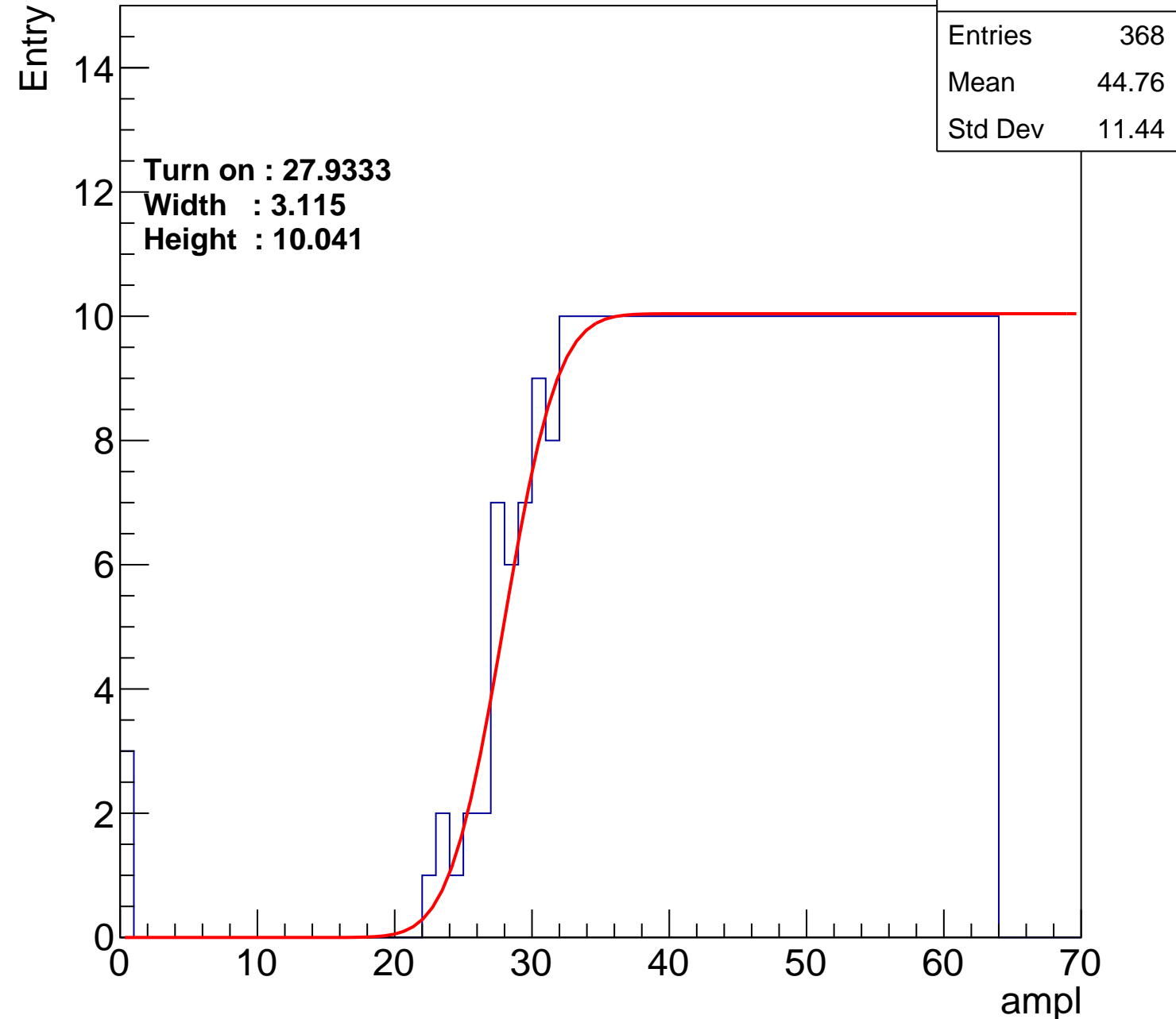
Width : 3.115

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.8031

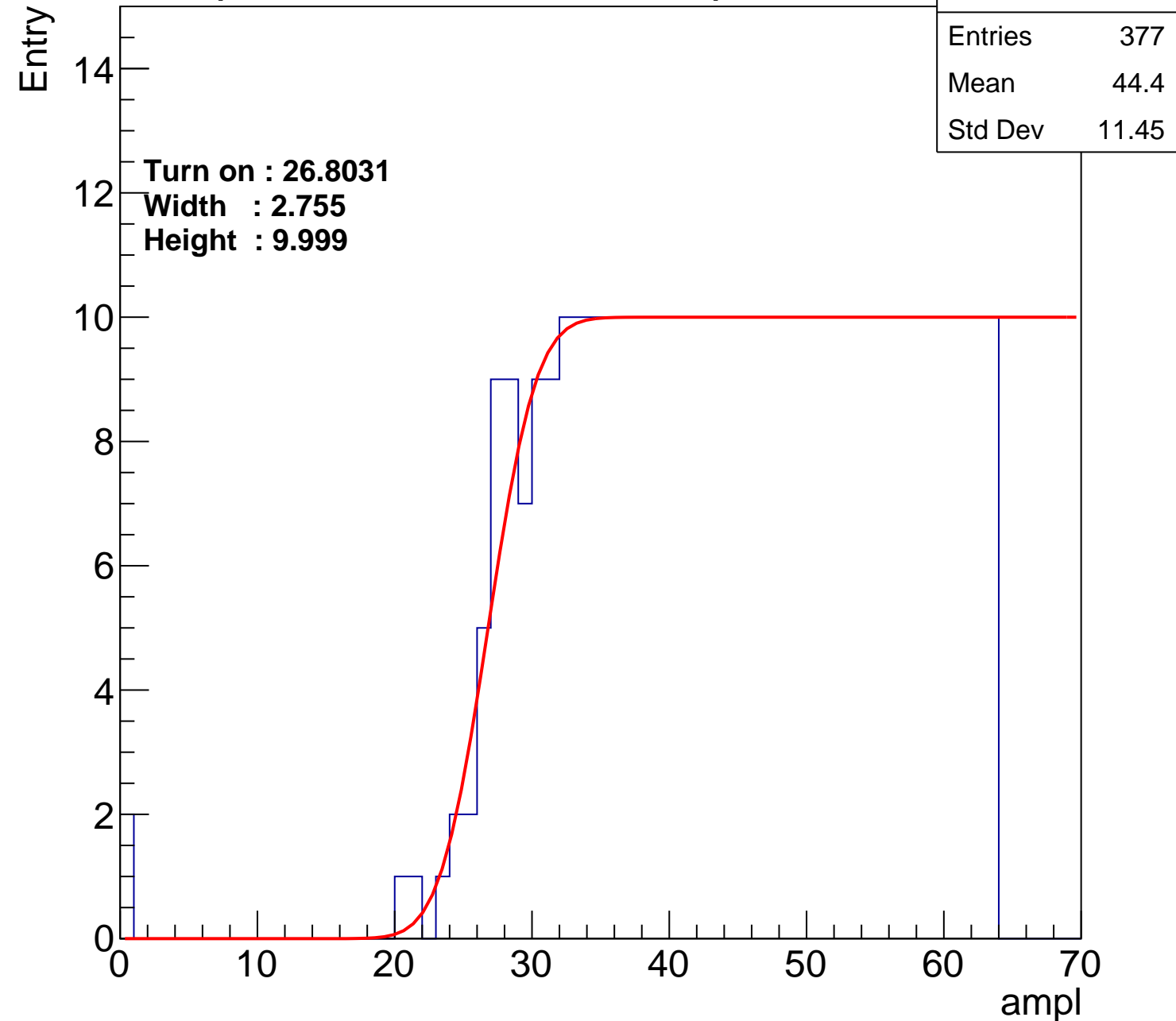
Width : 2.755

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.82
Std Dev	11.21

Turn on : 27.7623

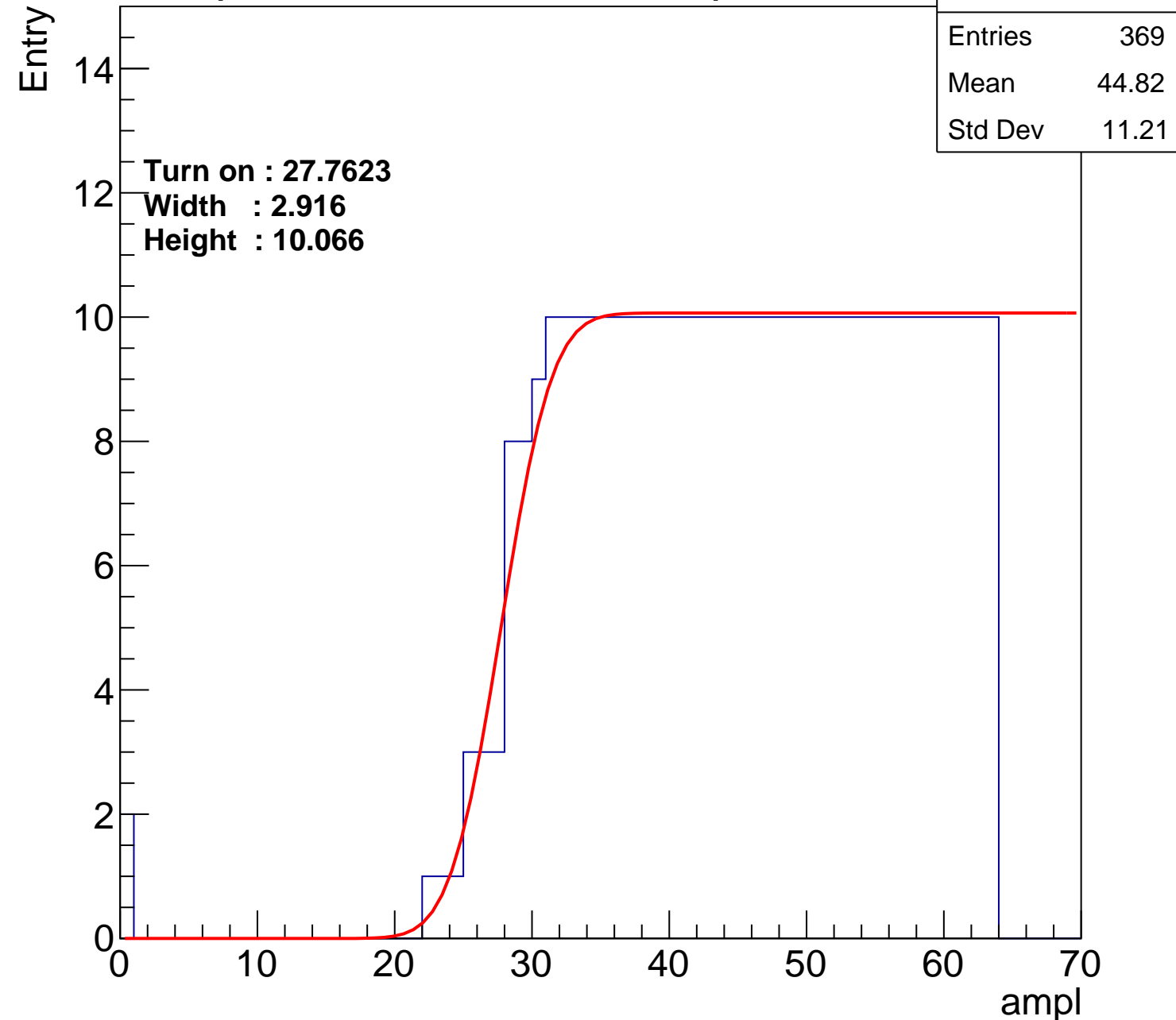
Width : 2.916

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.33
Std Dev	11.76

Turn on : 26.8343

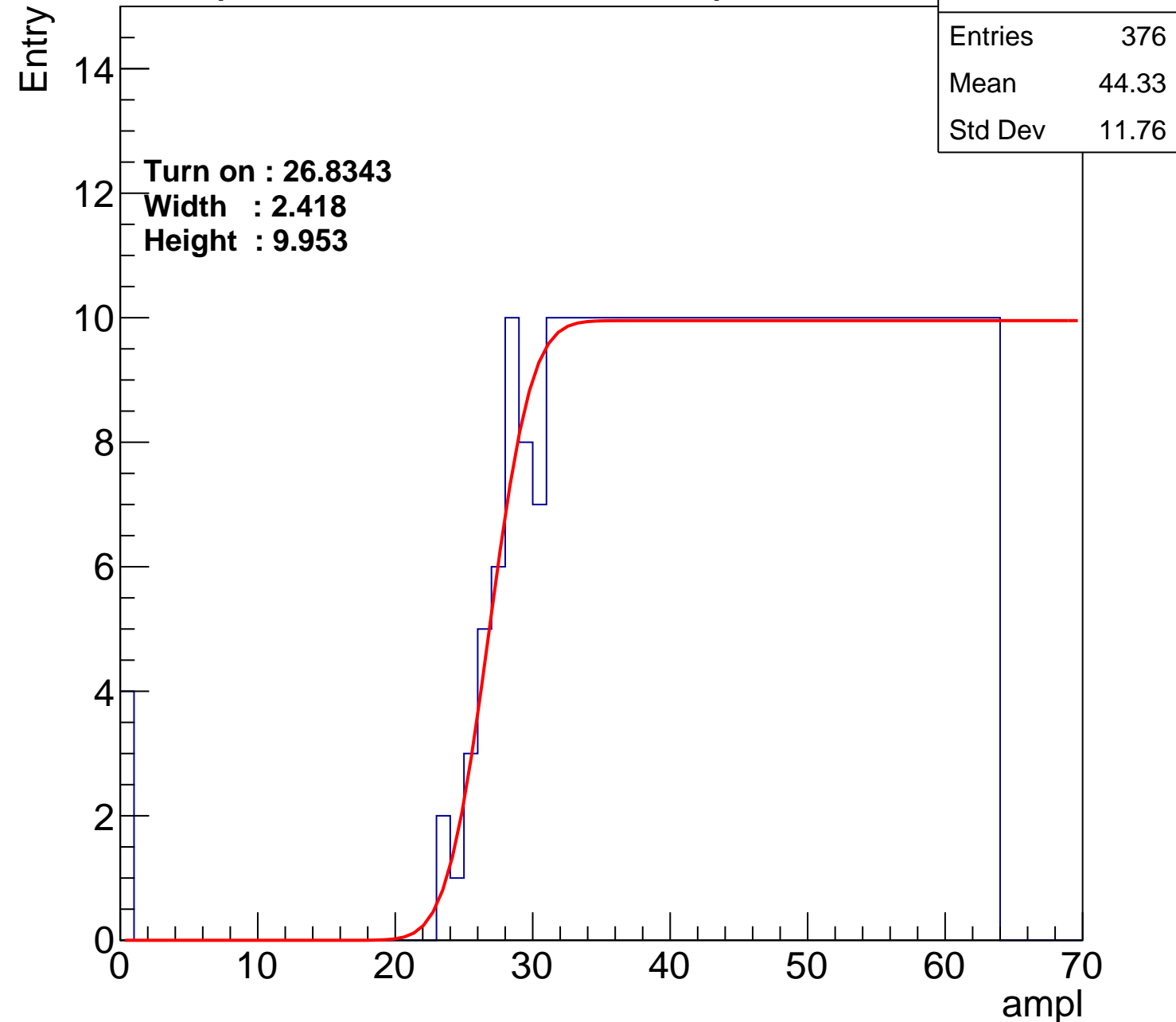
Width : 2.418

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	44.97
Std Dev	11.37

Turn on : 28.1143

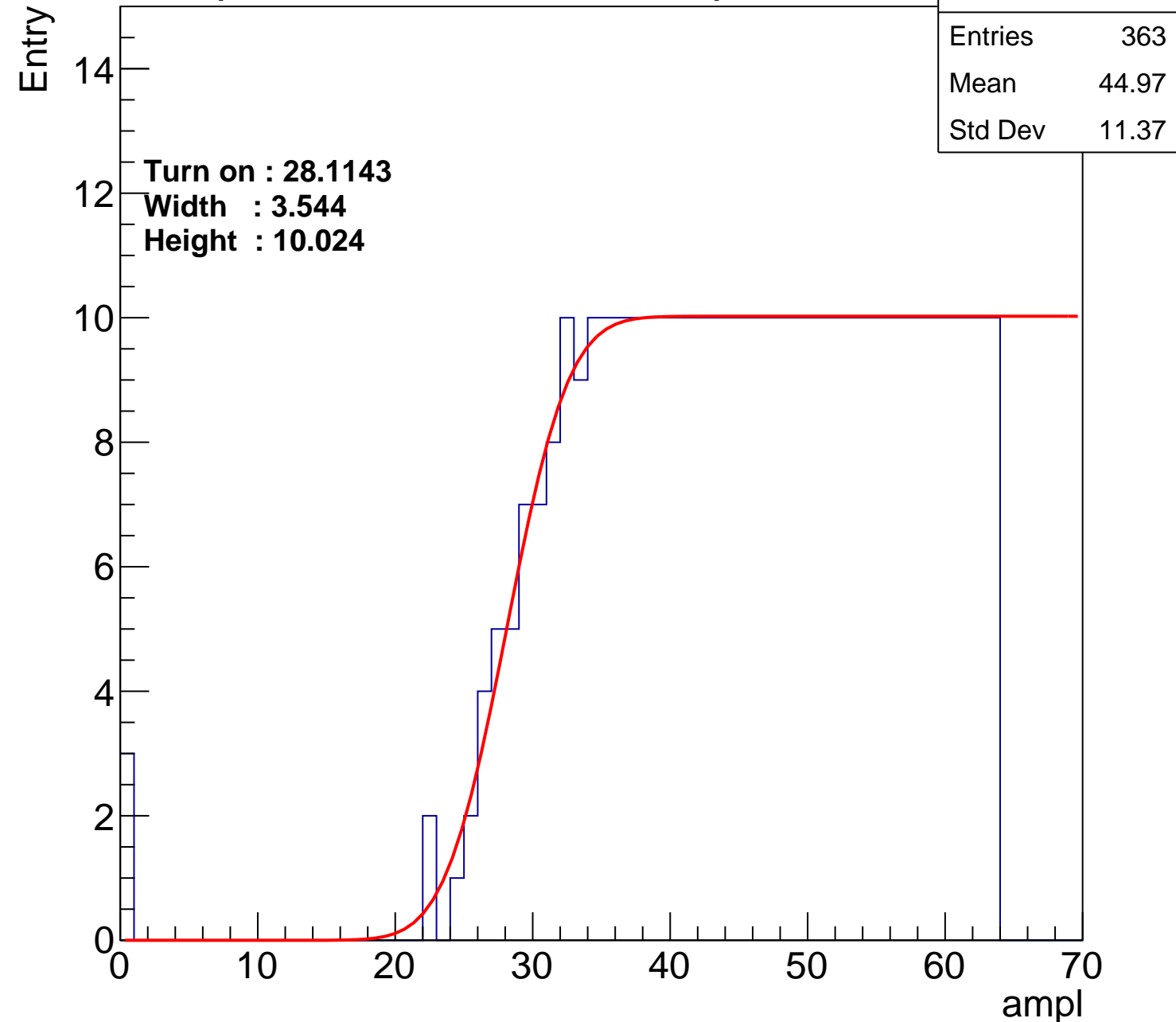
Width : 3.544

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.84
Std Dev	11.78

Turn on : 25.9883

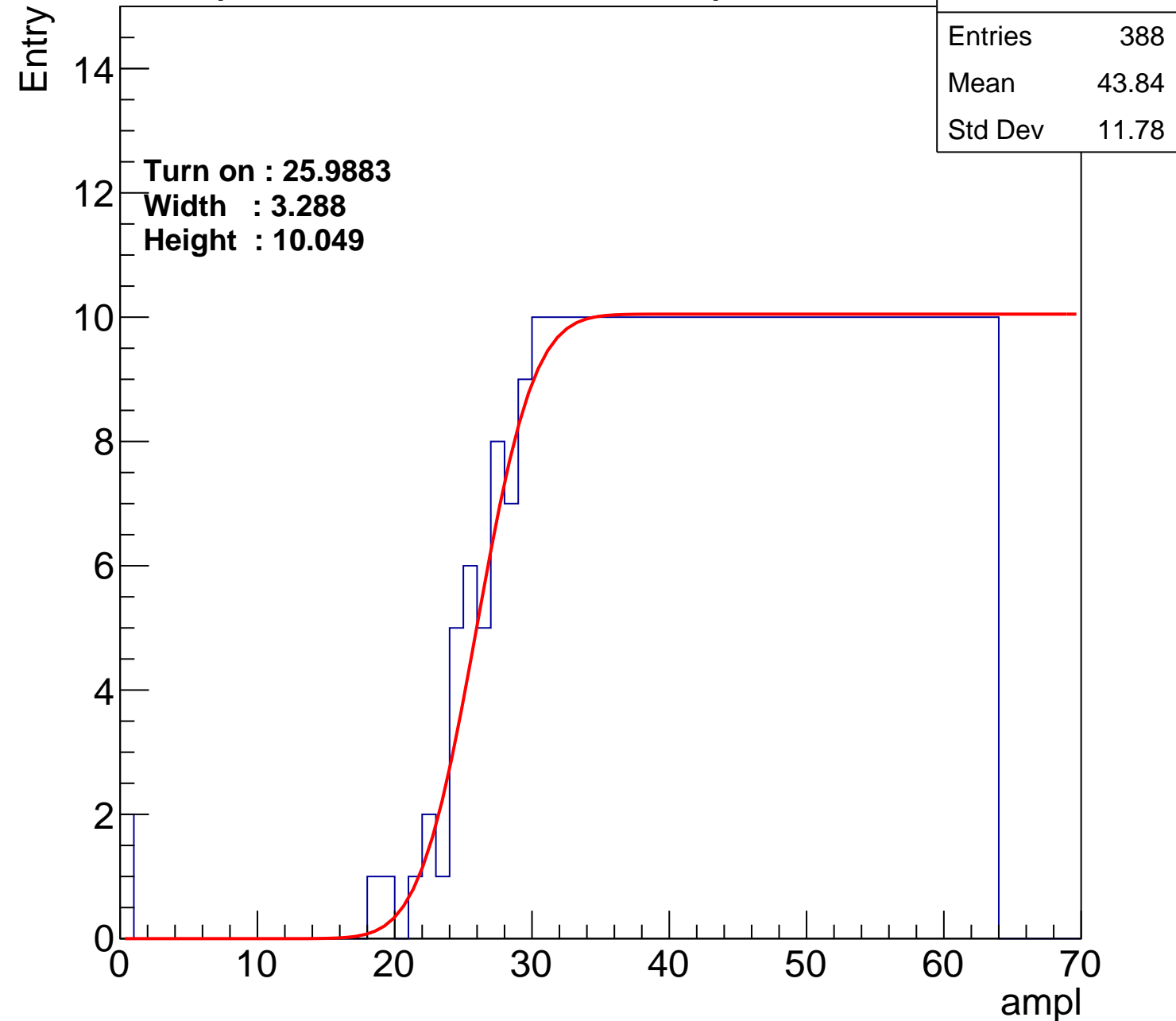
Width : 3.288

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.76
Std Dev	12.7

Turn on : 24.0427

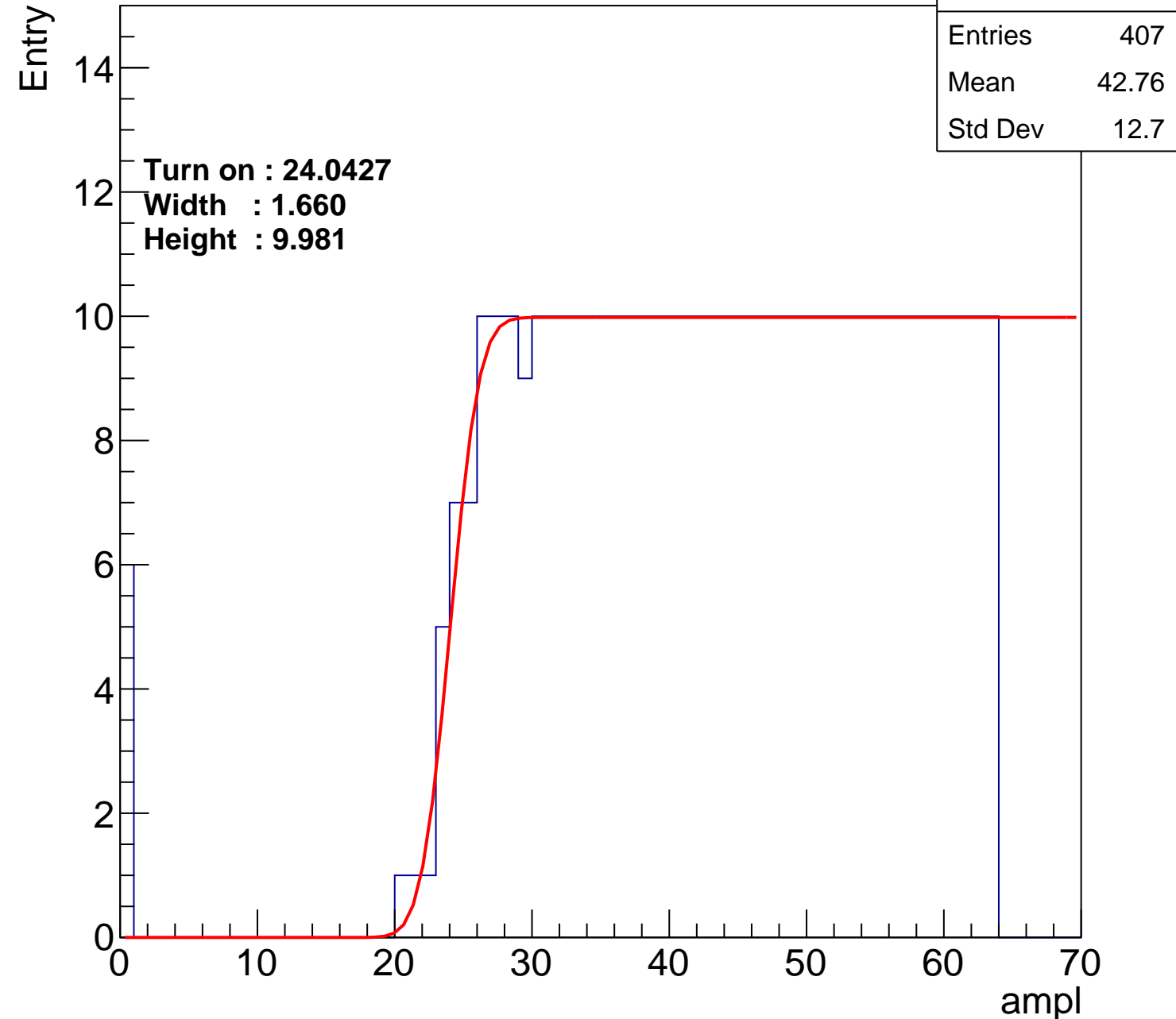
Width : 1.660

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch26

calib_packv5_042523_0143.root, FC#12, port B1

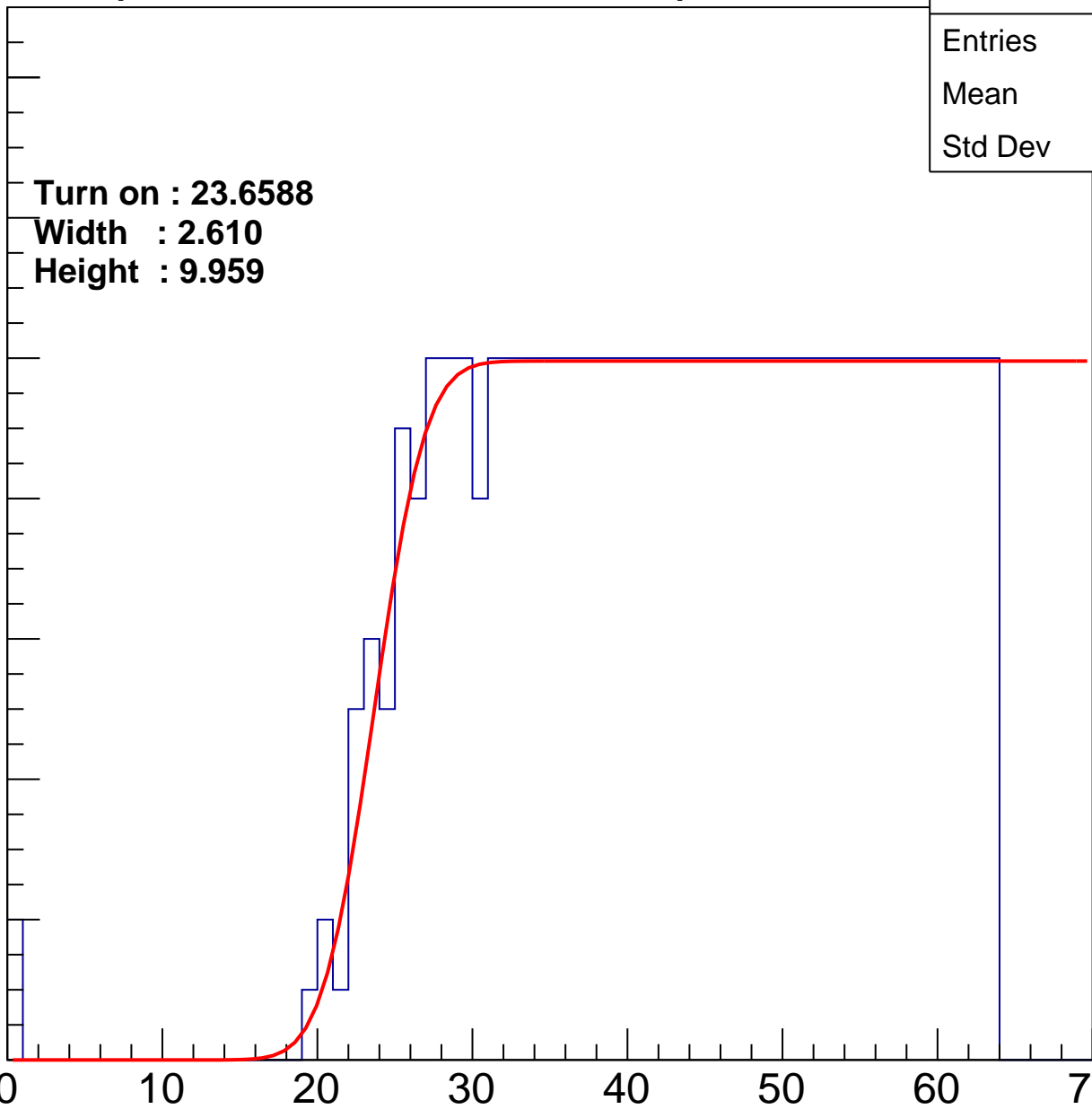
Entry

14
12
10
8
6
4
2
0

Turn on : 23.6588
Width : 2.610
Height : 9.959

Entries	407
Mean	42.93
Std Dev	12.22

ampl



B0L102S, U1-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.47
Std Dev	12.56

Turn on : 25.8823

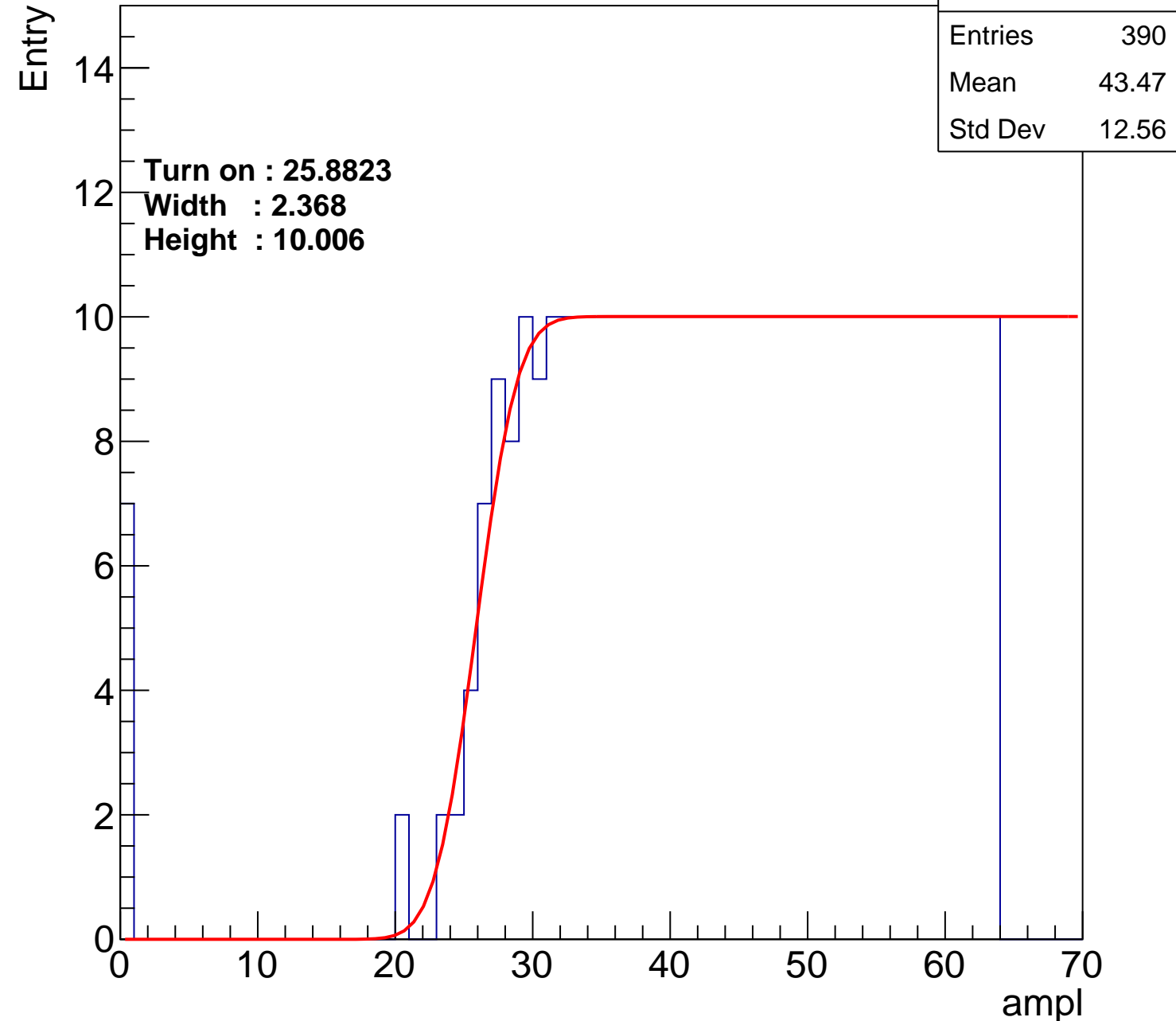
Width : 2.368

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch28

calib_packv5_042523_0143.root, FC#12, port B1

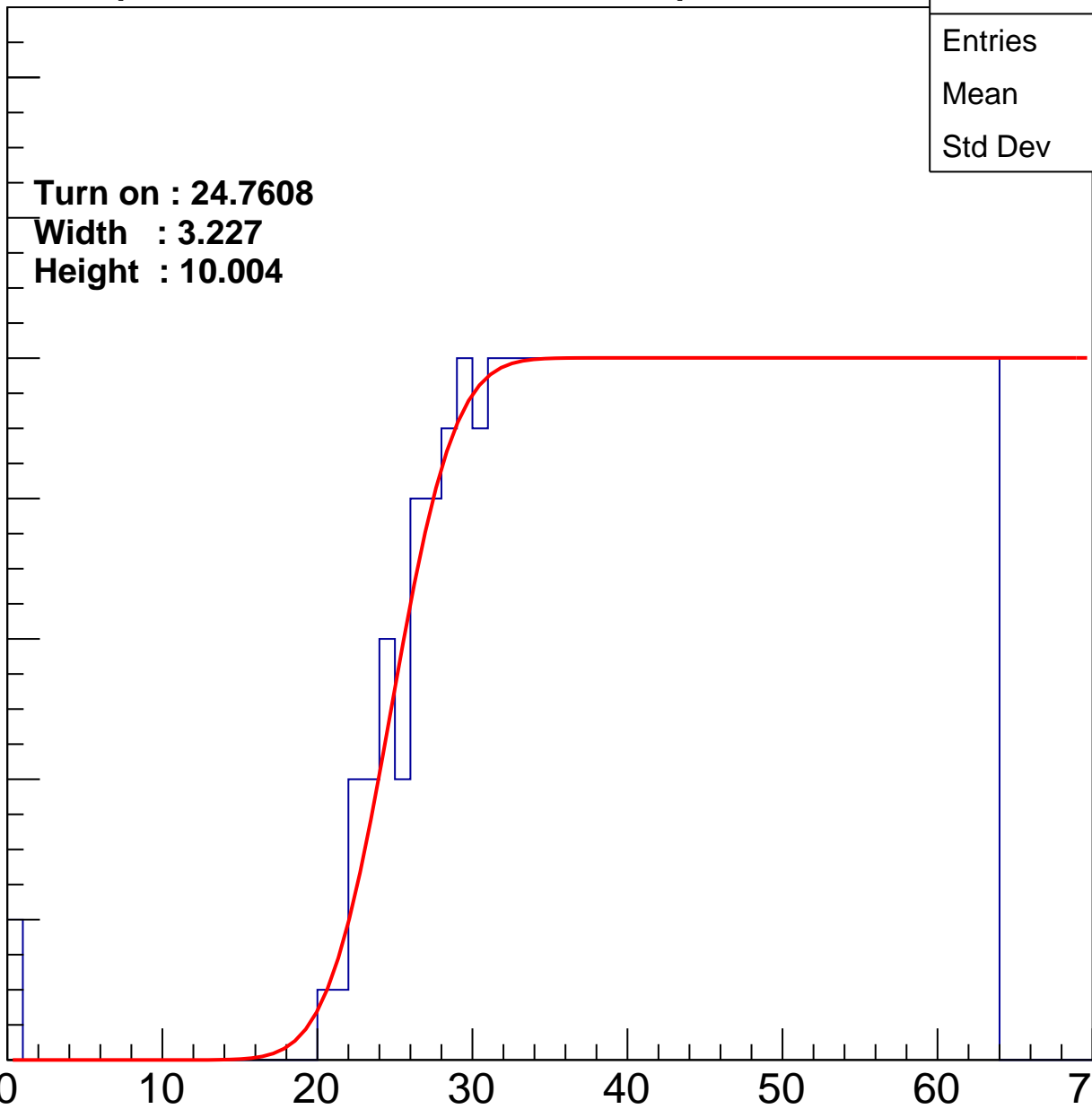
Entry

14
12
10
8
6
4
2
0

Turn on : 24.7608
Width : 3.227
Height : 10.004

Entries	396
Mean	43.46
Std Dev	11.95

ampl



B0L102S, U1-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.96
Std Dev	11.19

Turn on : 27.6960

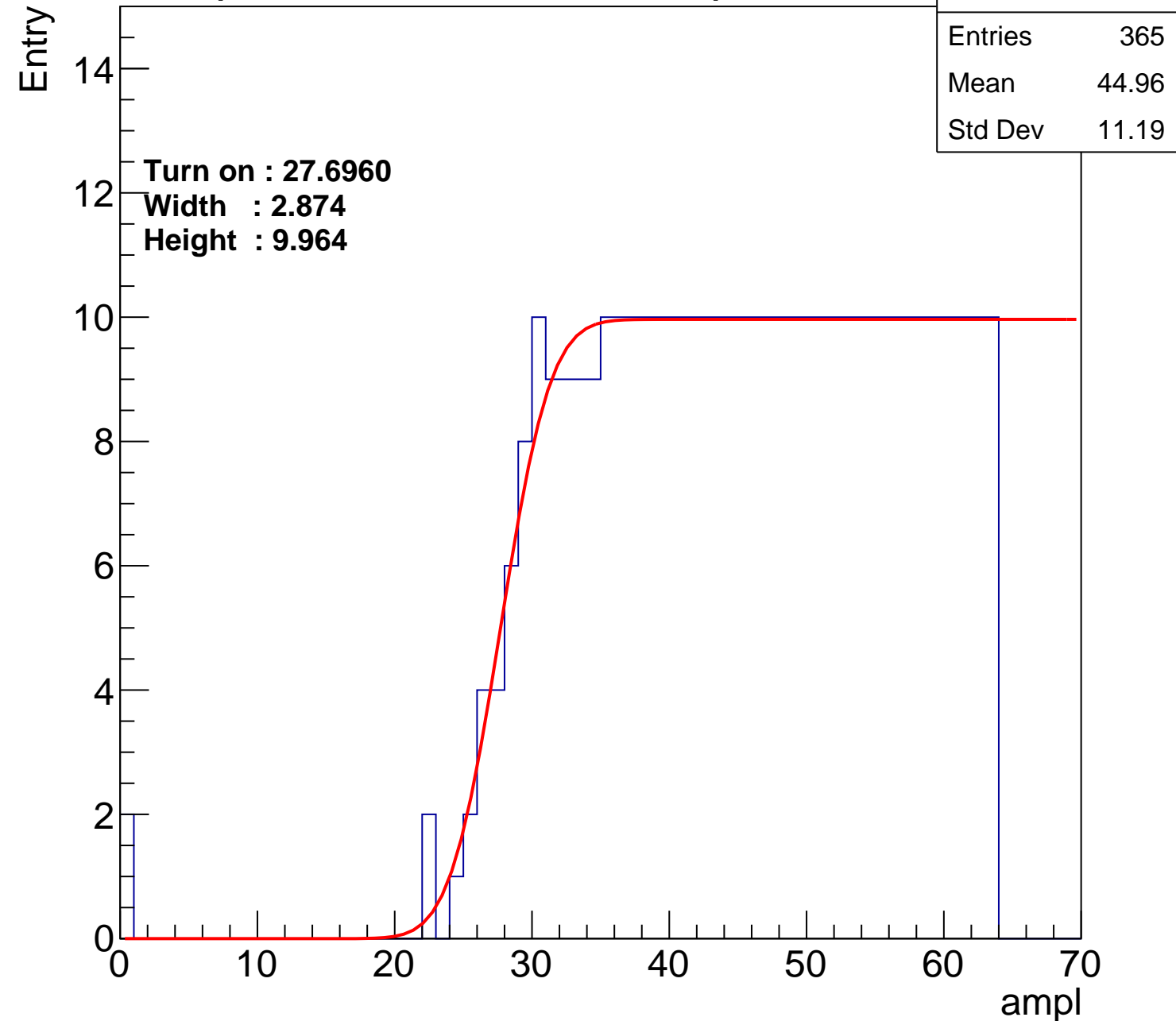
Width : 2.874

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.7
Std Dev	11.29

Turn on : 27.5980

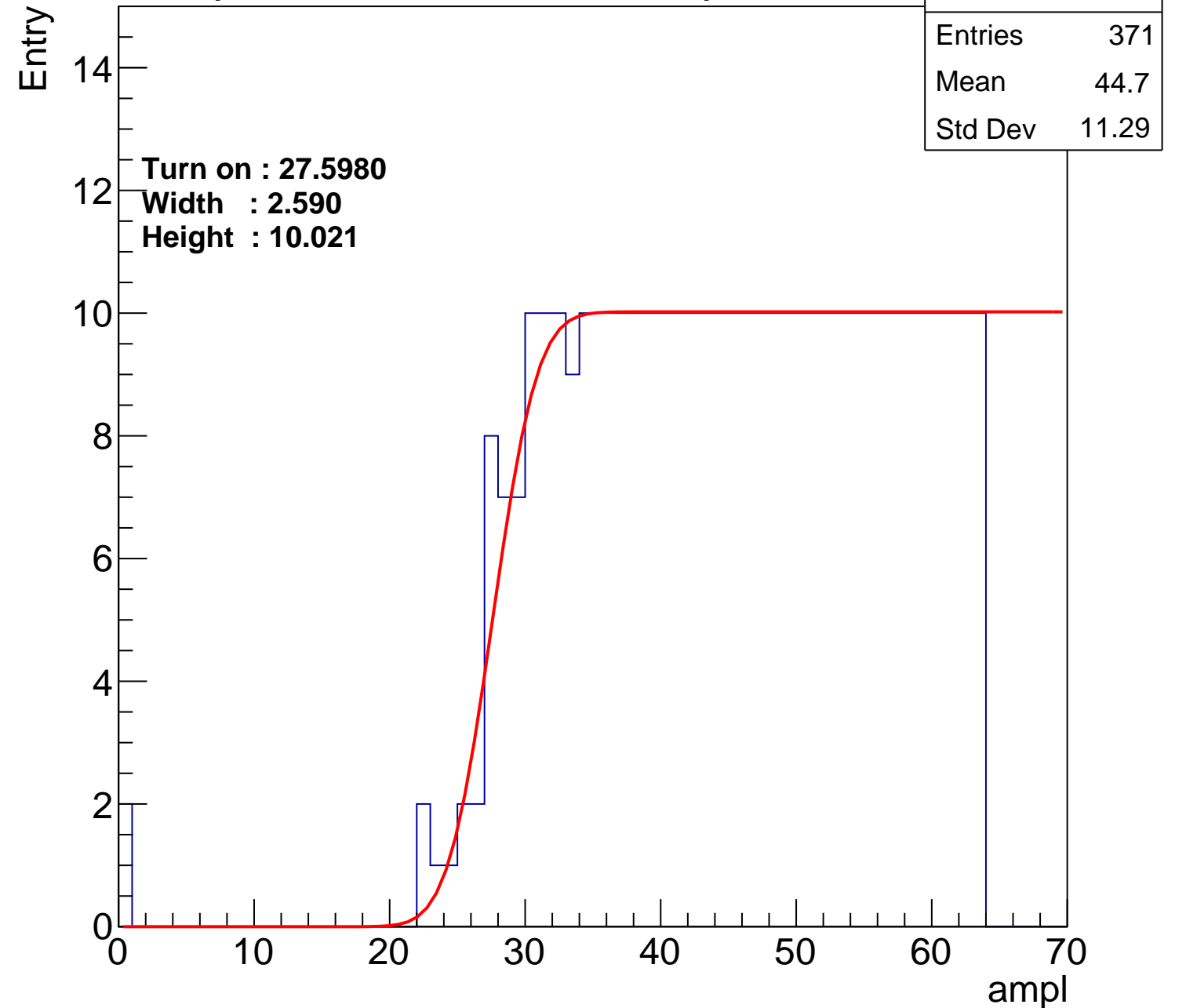
Width : 2.590

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.33
Std Dev	11.65

Turn on : 26.7166

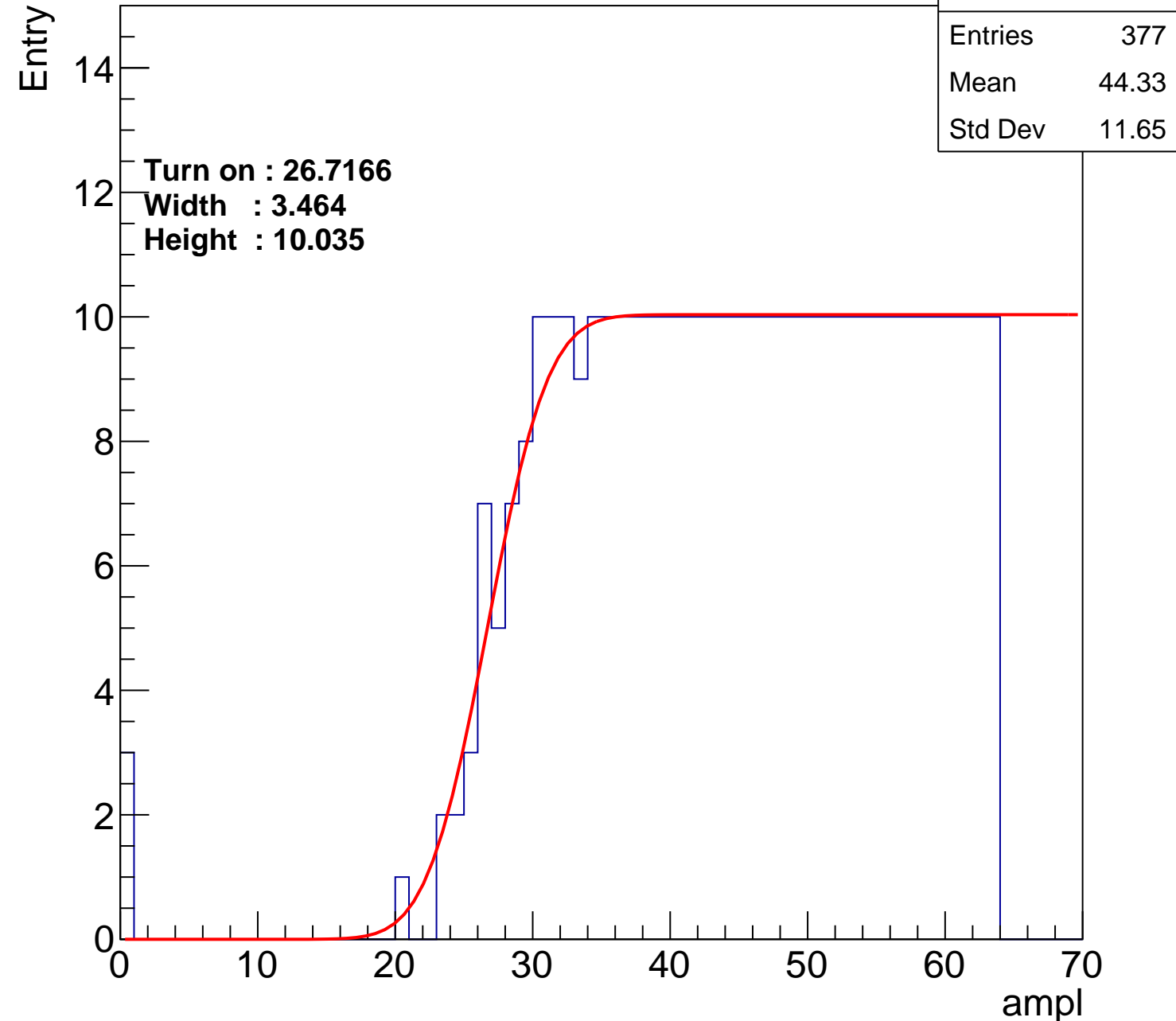
Width : 3.464

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.22
Std Dev	12.08

Turn on : 24.6123

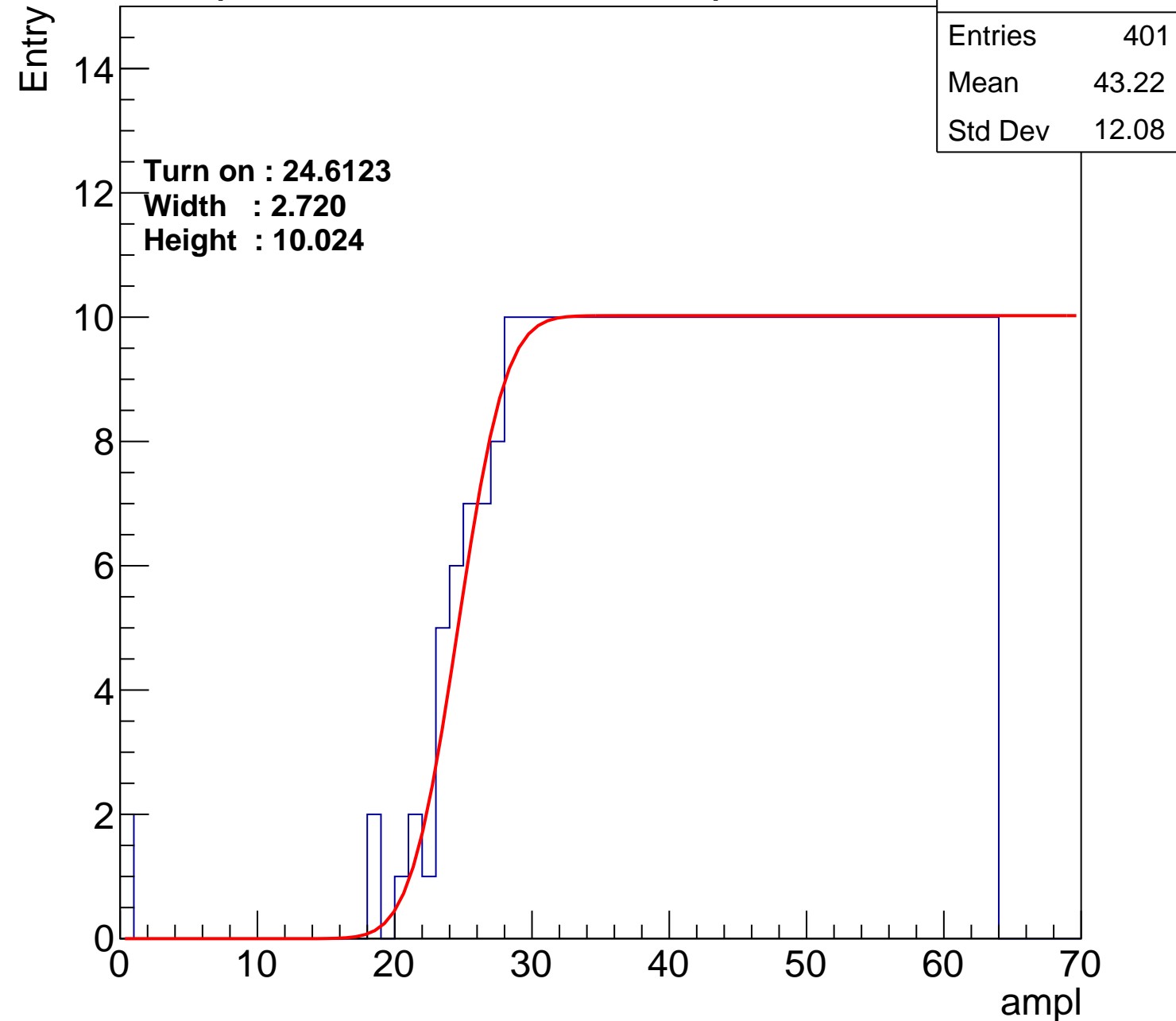
Width : 2.720

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	362
Mean	44.89
Std Dev	11.73

Turn on : 28.2434

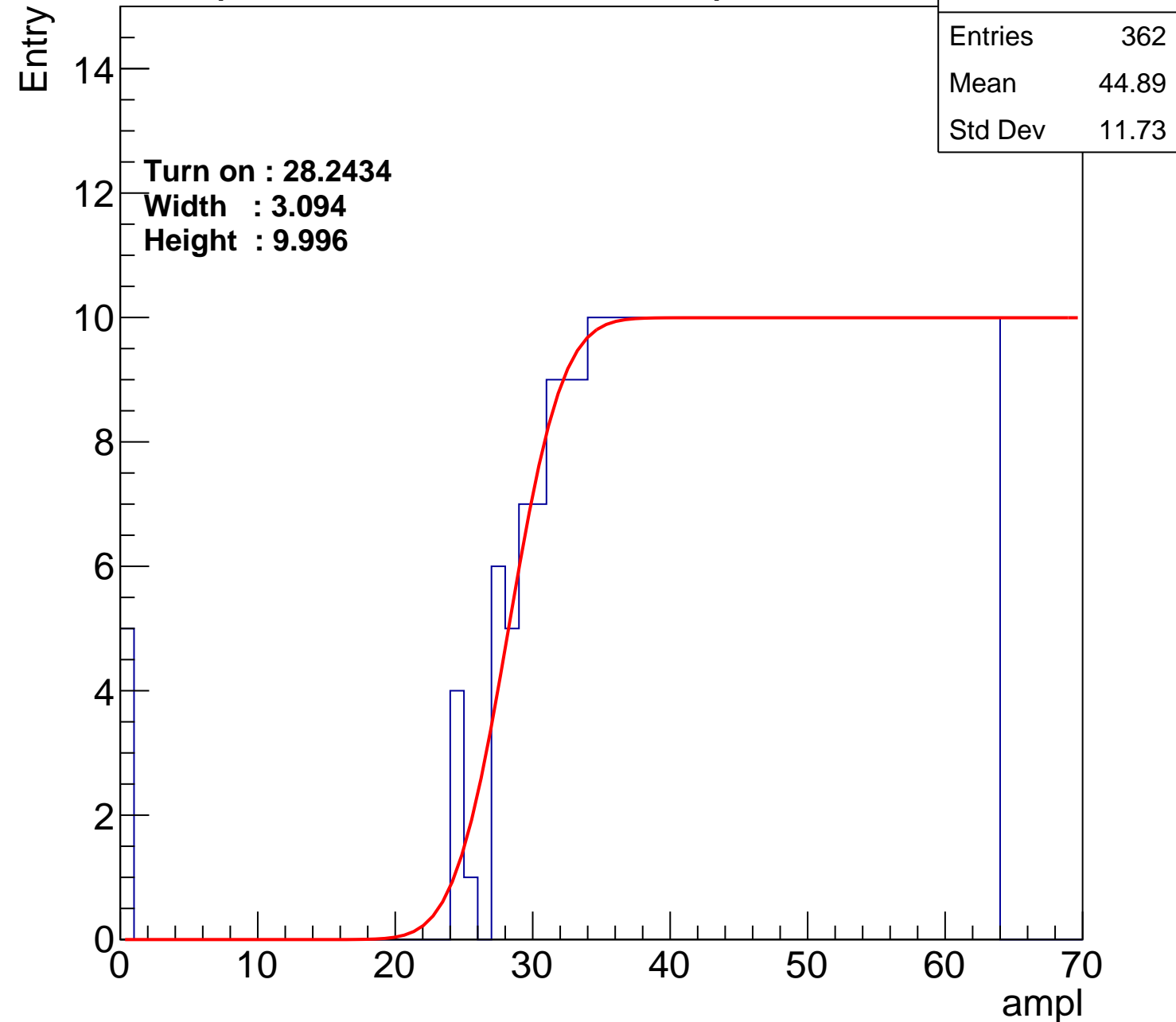
Width : 3.094

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.82
Std Dev	11.62

Turn on : 25.2622

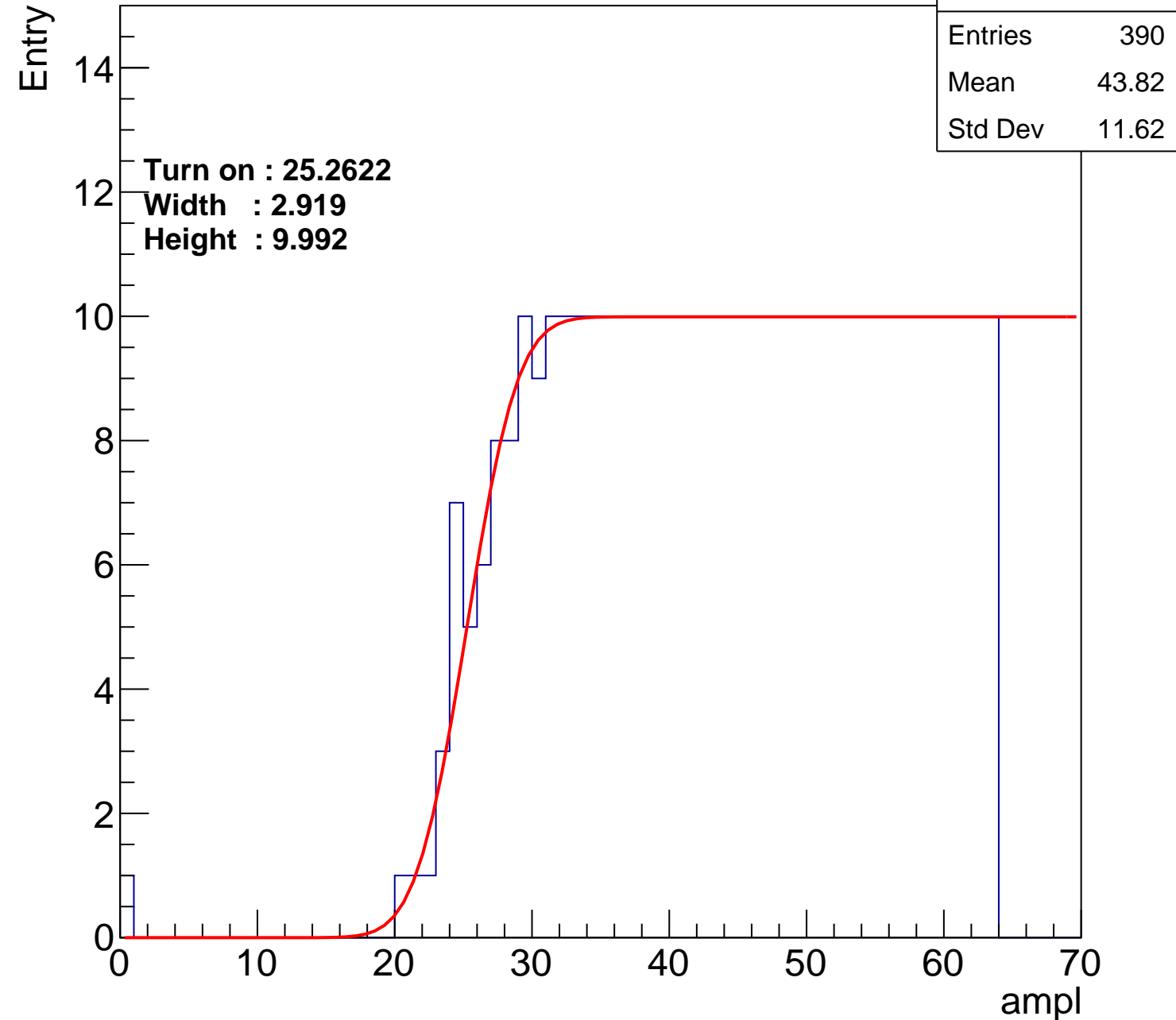
Width : 2.919

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.15
Std Dev	11.84

Turn on : 26.3927

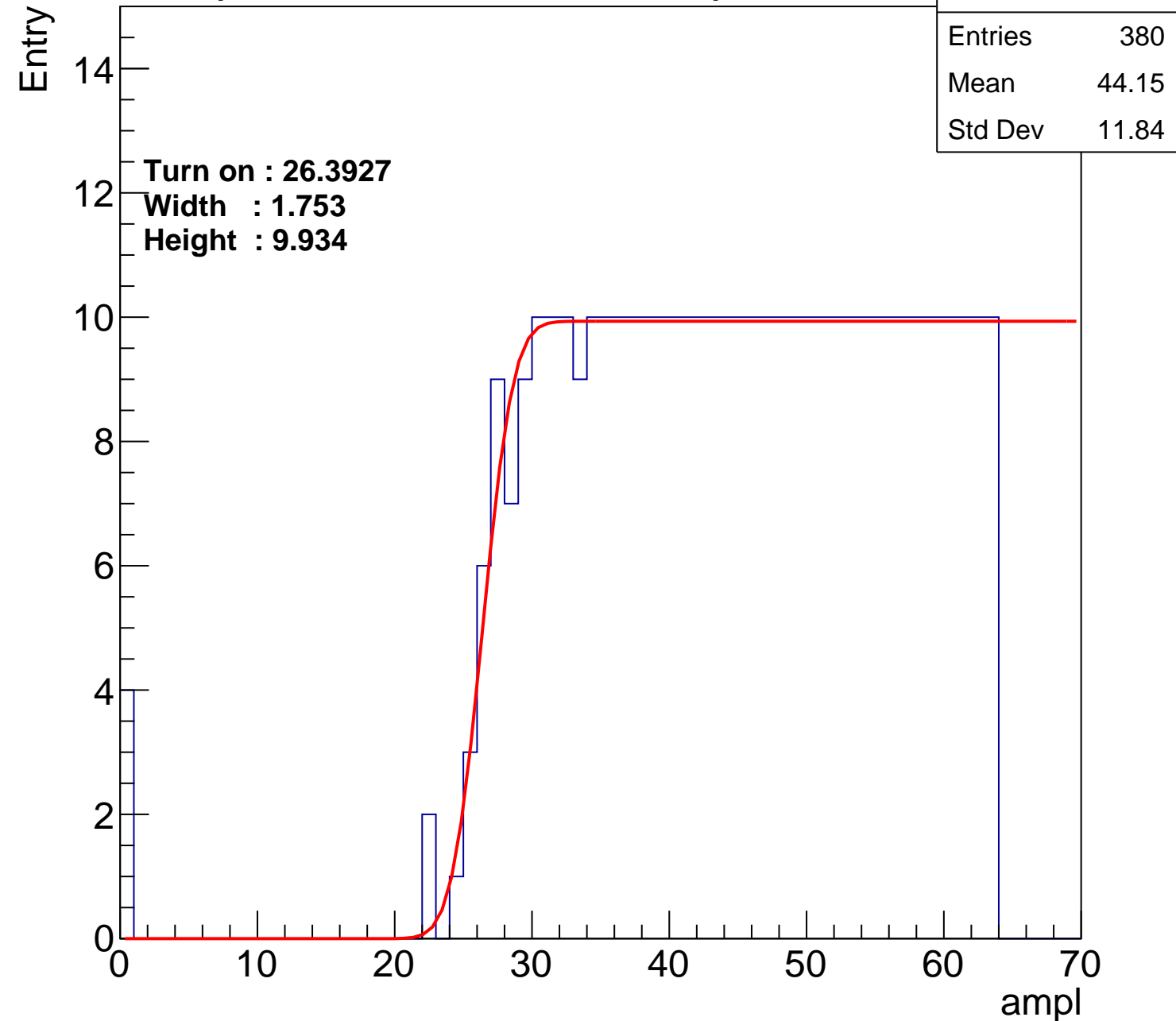
Width : 1.753

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch36

calib_packv5_042523_0143.root, FC#12, port B1

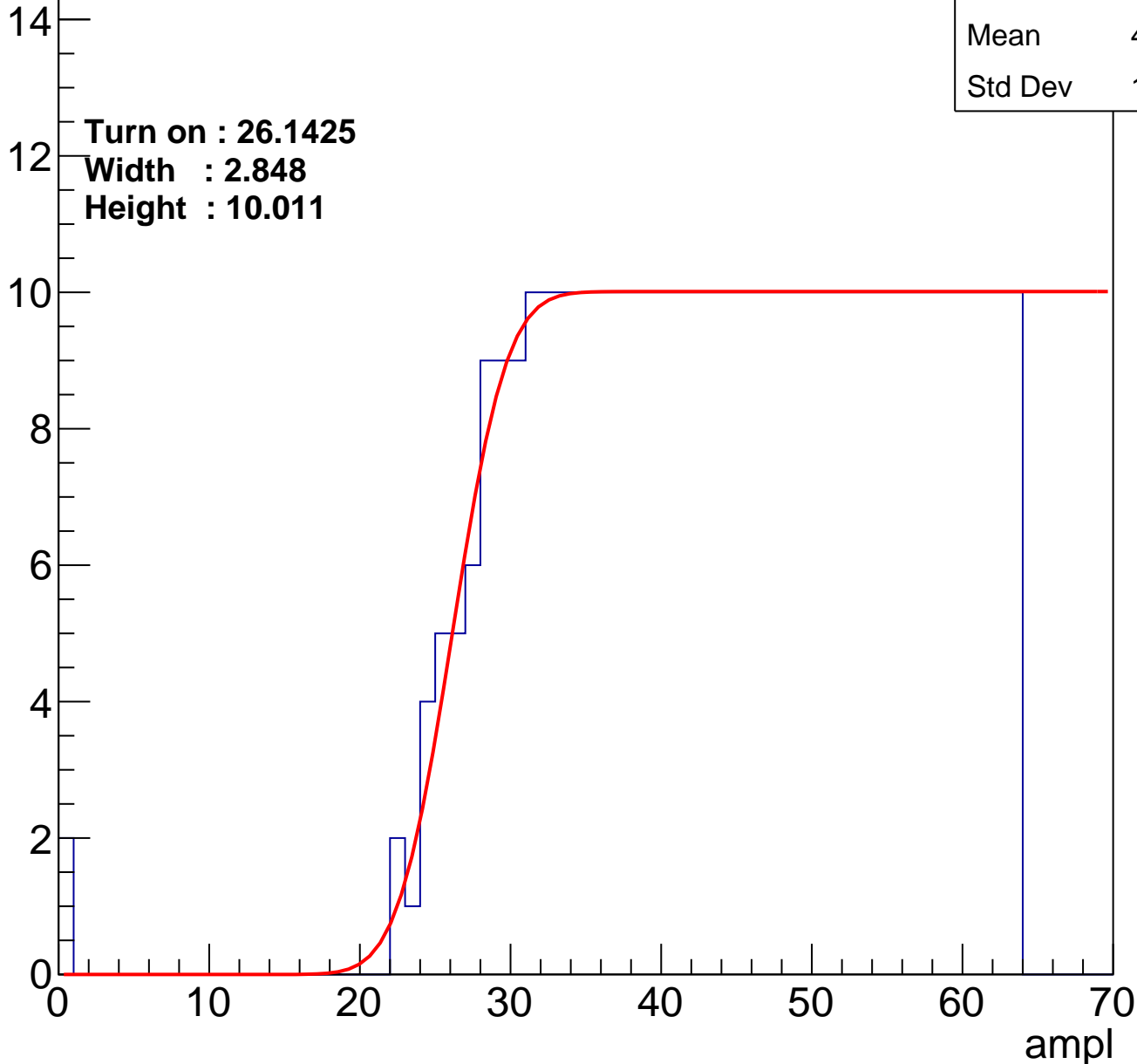
Entry

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.1425

Width : 2.848

Height : 10.011



B0L102S, U1-ch37

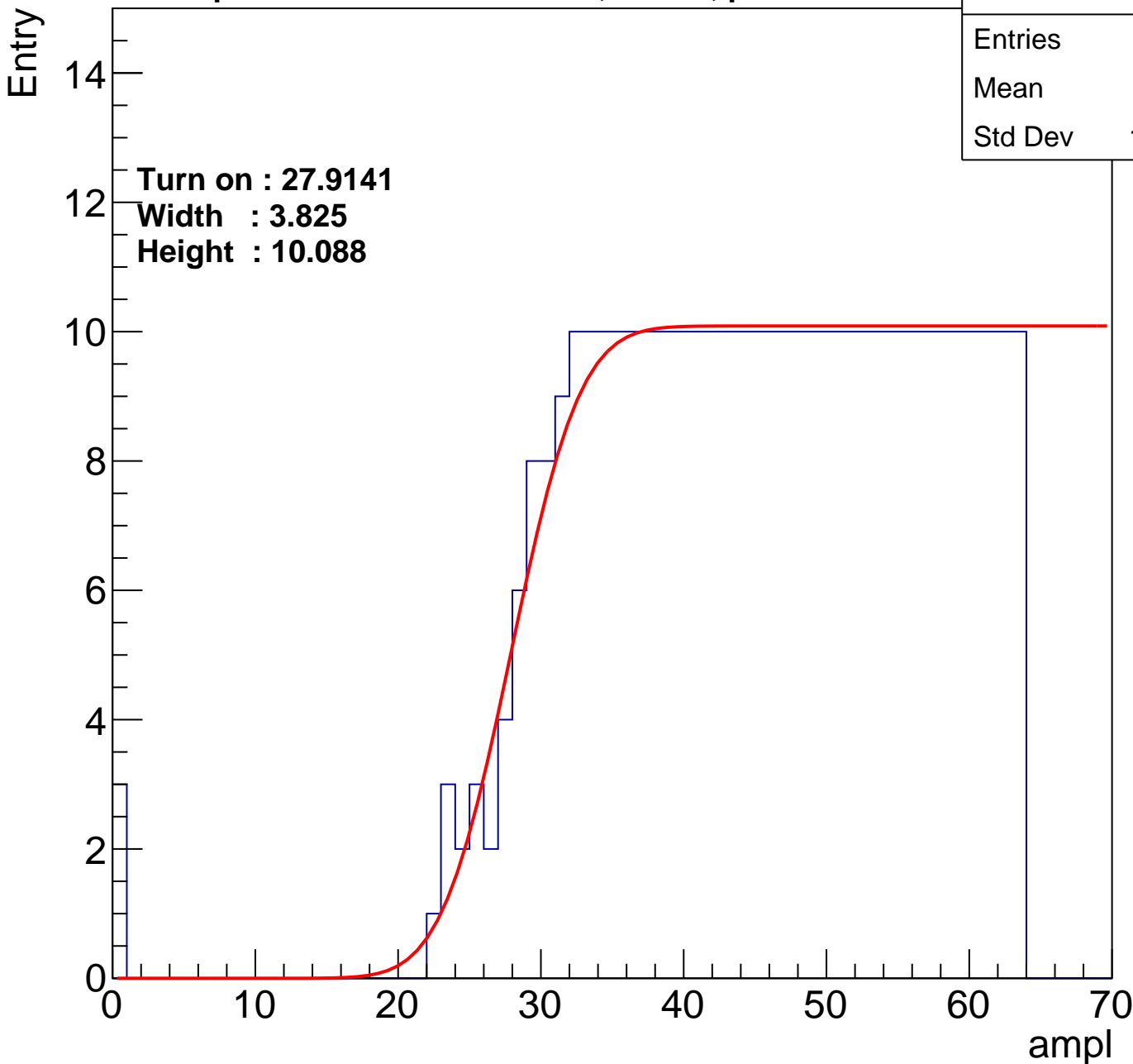
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 27.9141

Width : 3.825

Height : 10.088

Entries	369
Mean	44.7
Std Dev	11.49



B0L102S, U1-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.34
Std Dev	12.38

Turn on : 24.4545

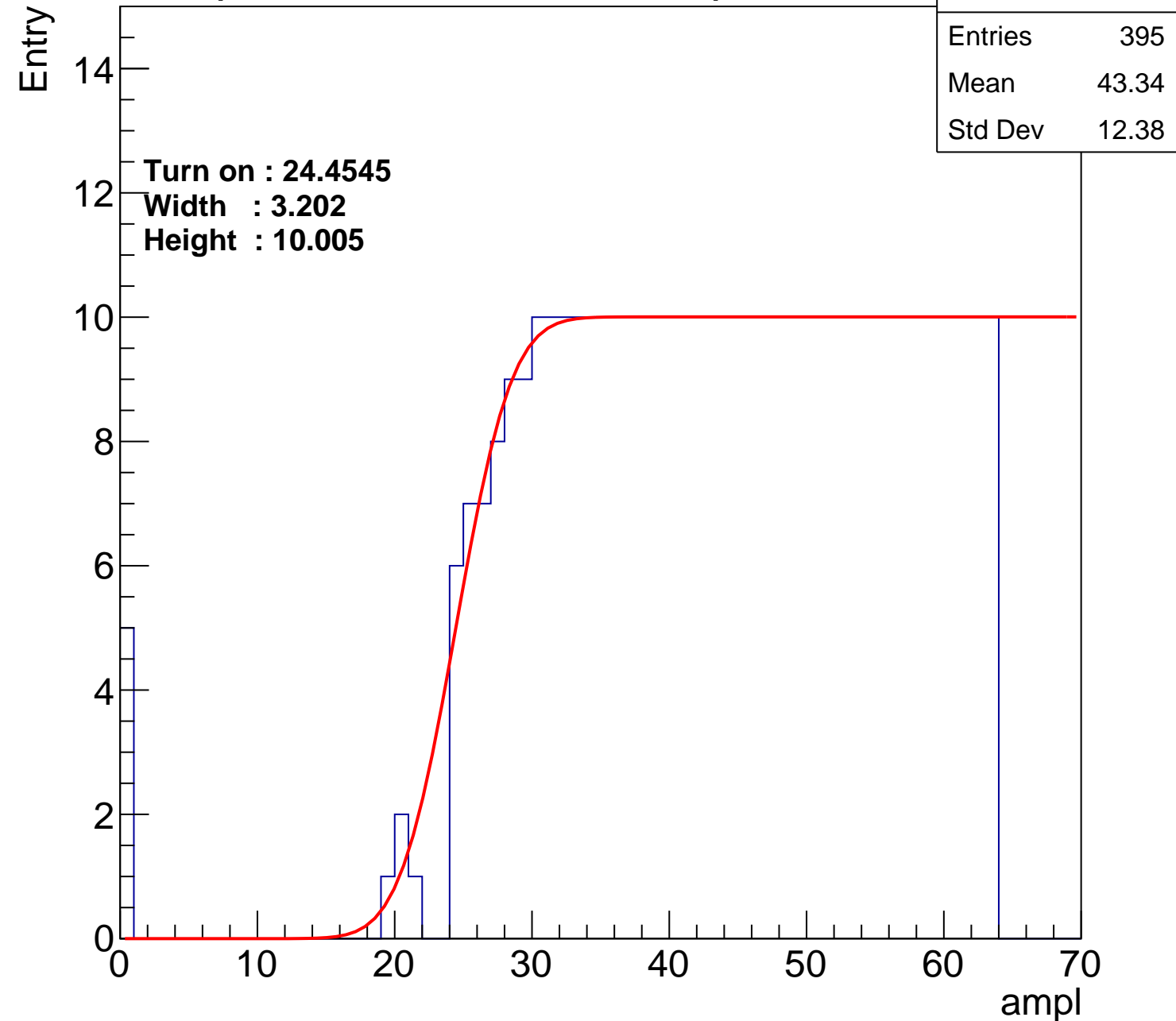
Width : 3.202

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.4033

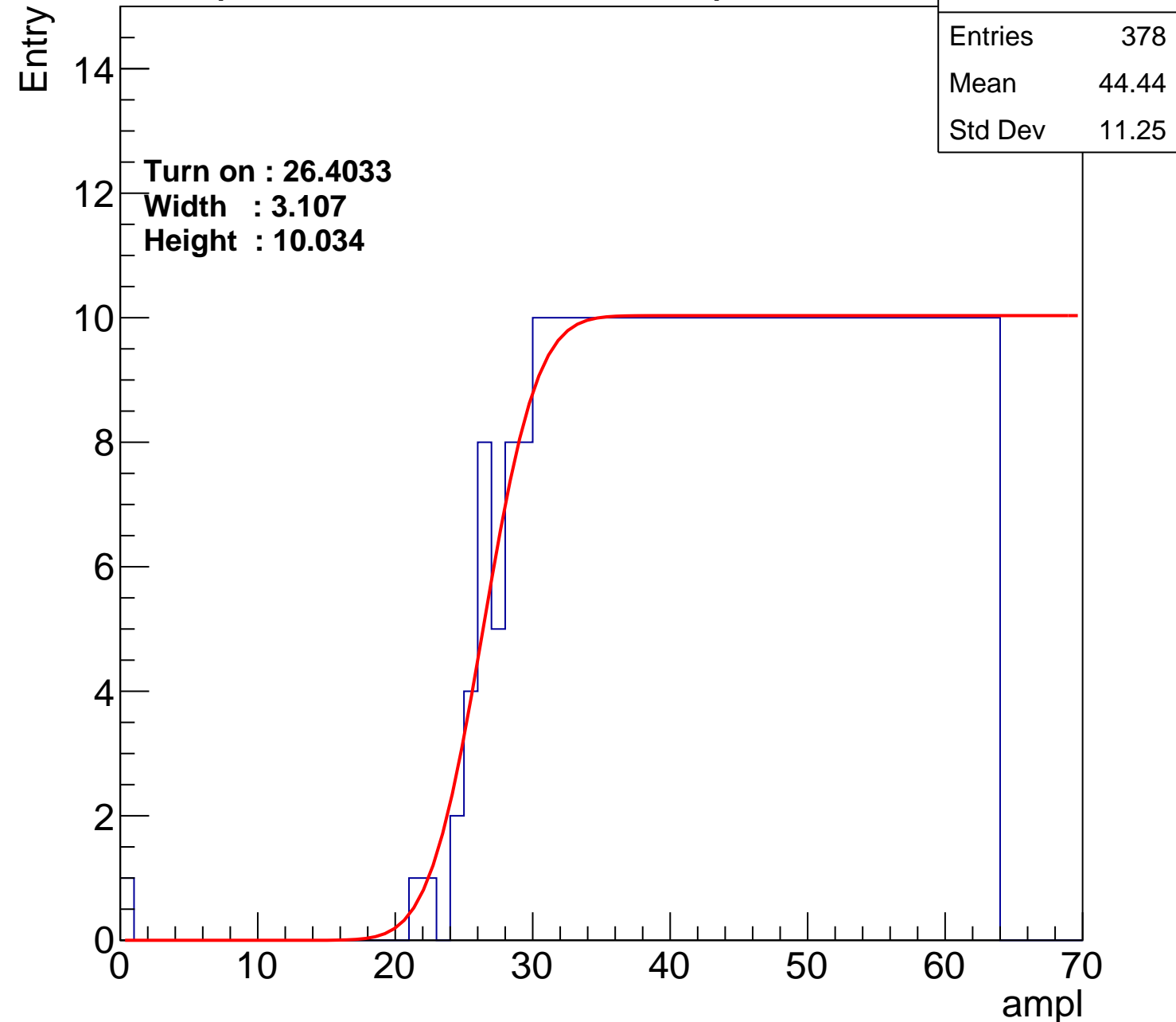
Width : 3.107

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.32
Std Dev	12.4

Turn on : 25.4424

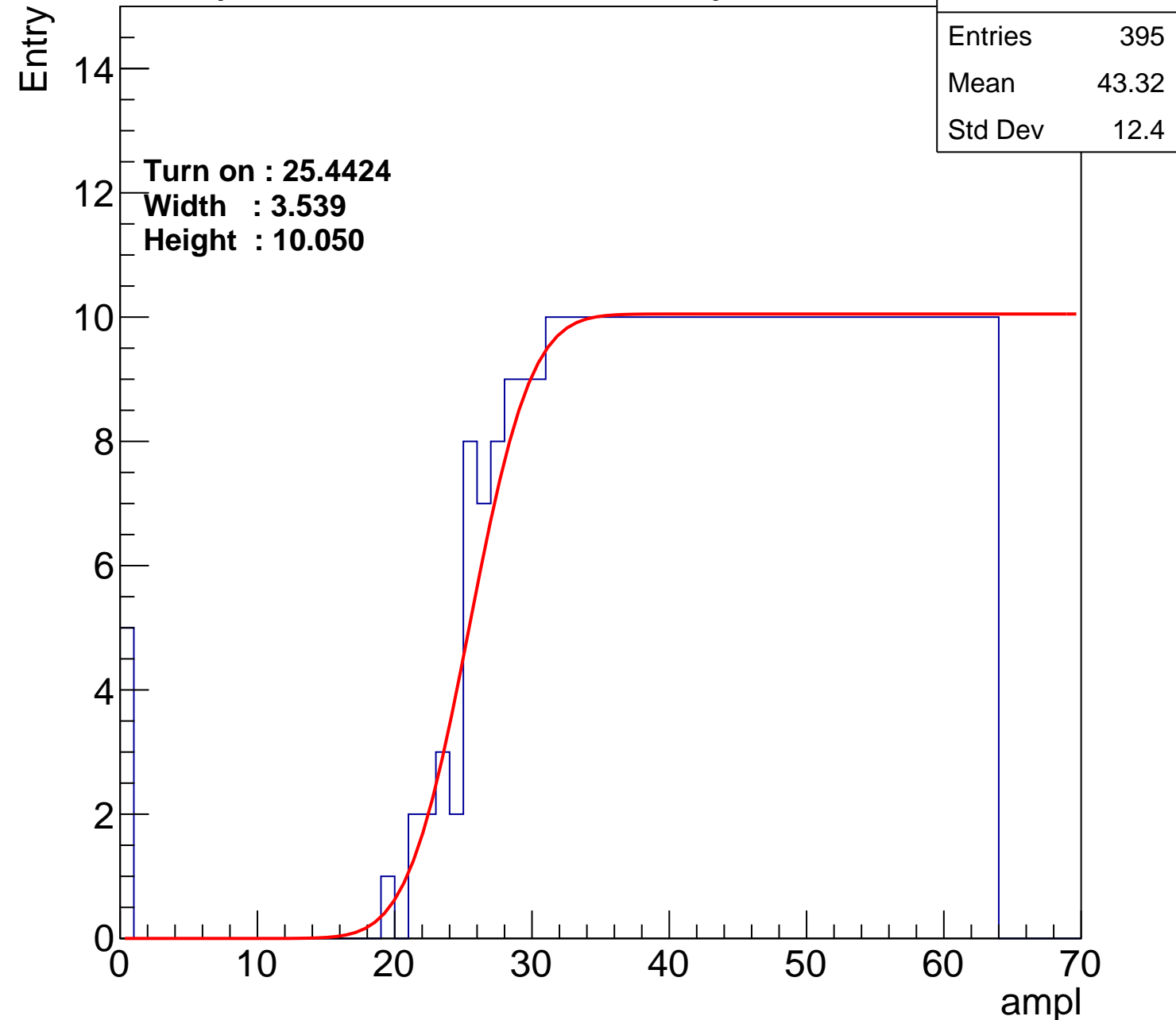
Width : 3.539

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch41

calib_packv5_042523_0143.root, FC#12, port B1

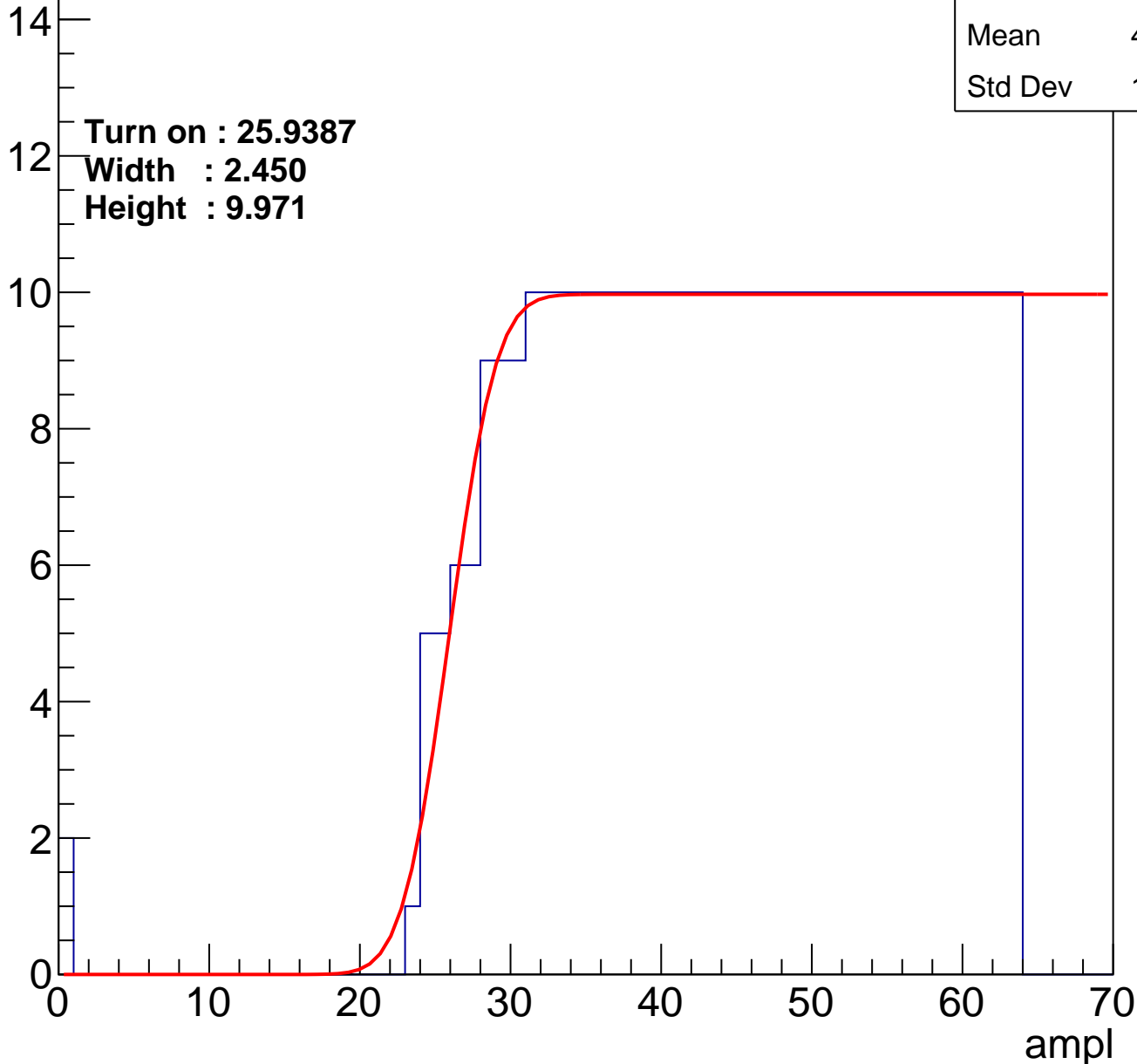
Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 25.9387

Width : 2.450

Height : 9.971

Entry



B0L102S, U1-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.38
Std Dev	11.67

Turn on : 27.0233

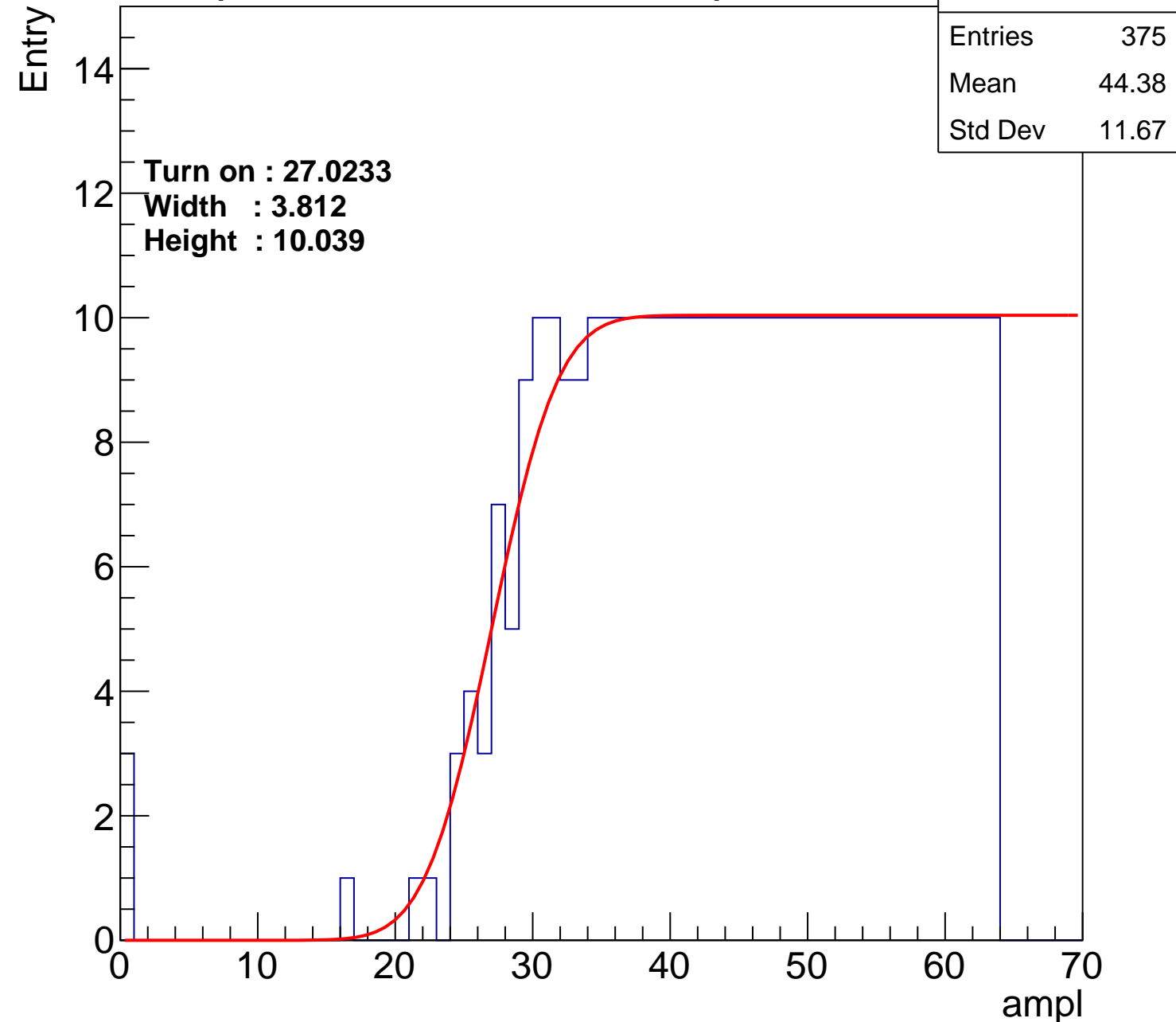
Width : 3.812

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.59
Std Dev	11.31

Turn on : 26.9451

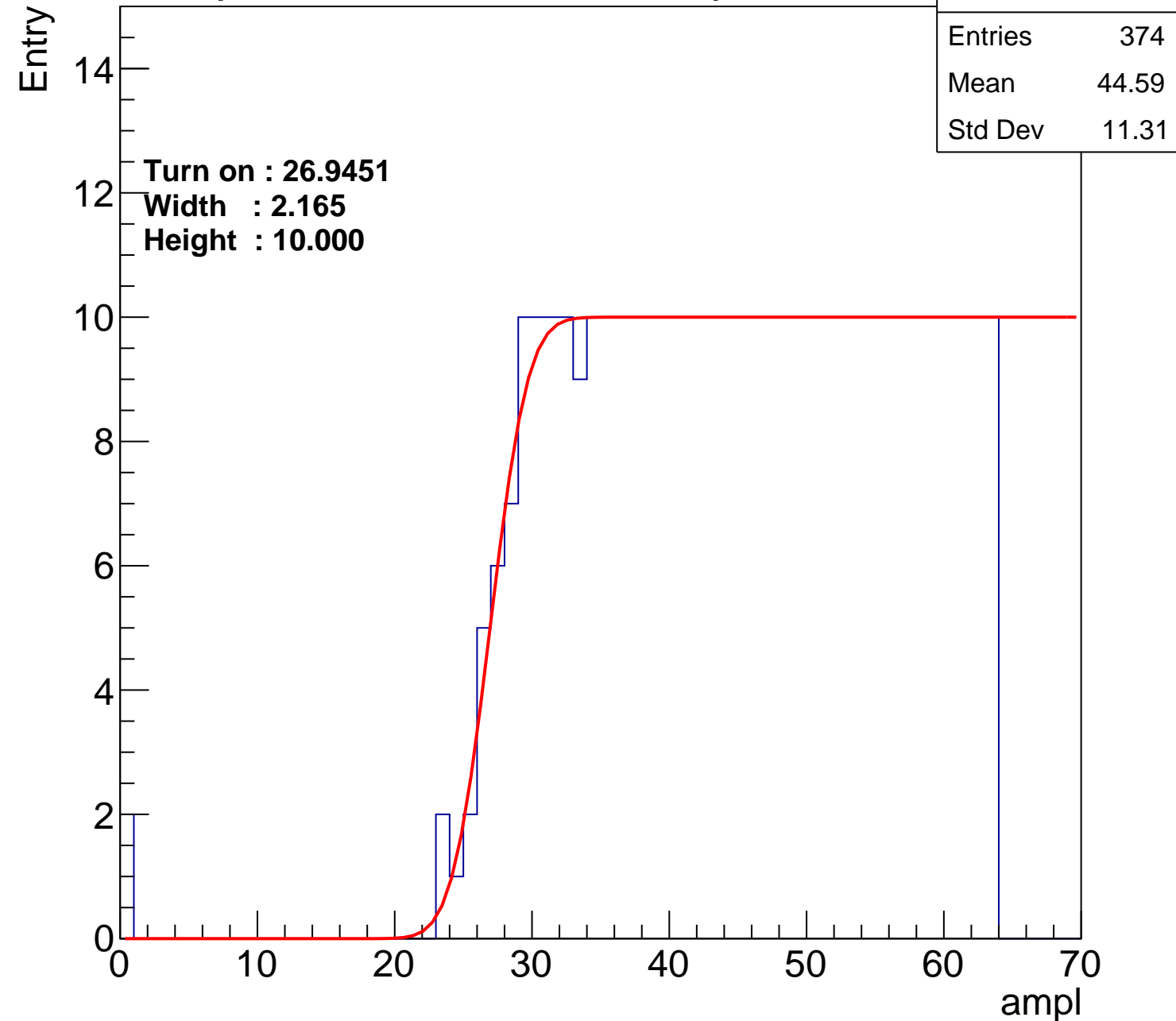
Width : 2.165

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	416
Mean	42.51
Std Dev	12.38

Turn on : 22.9653

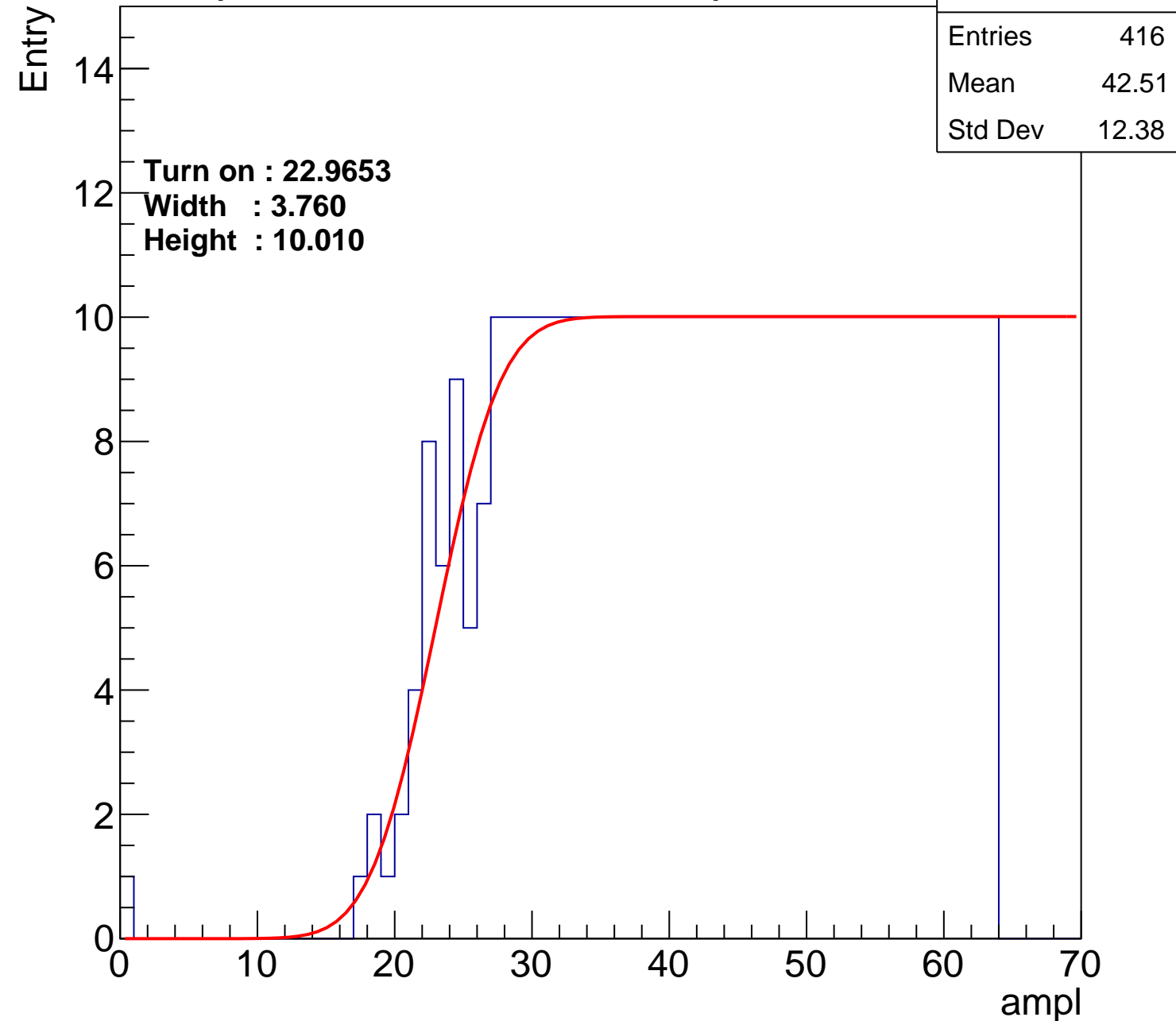
Width : 3.760

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 25.7726

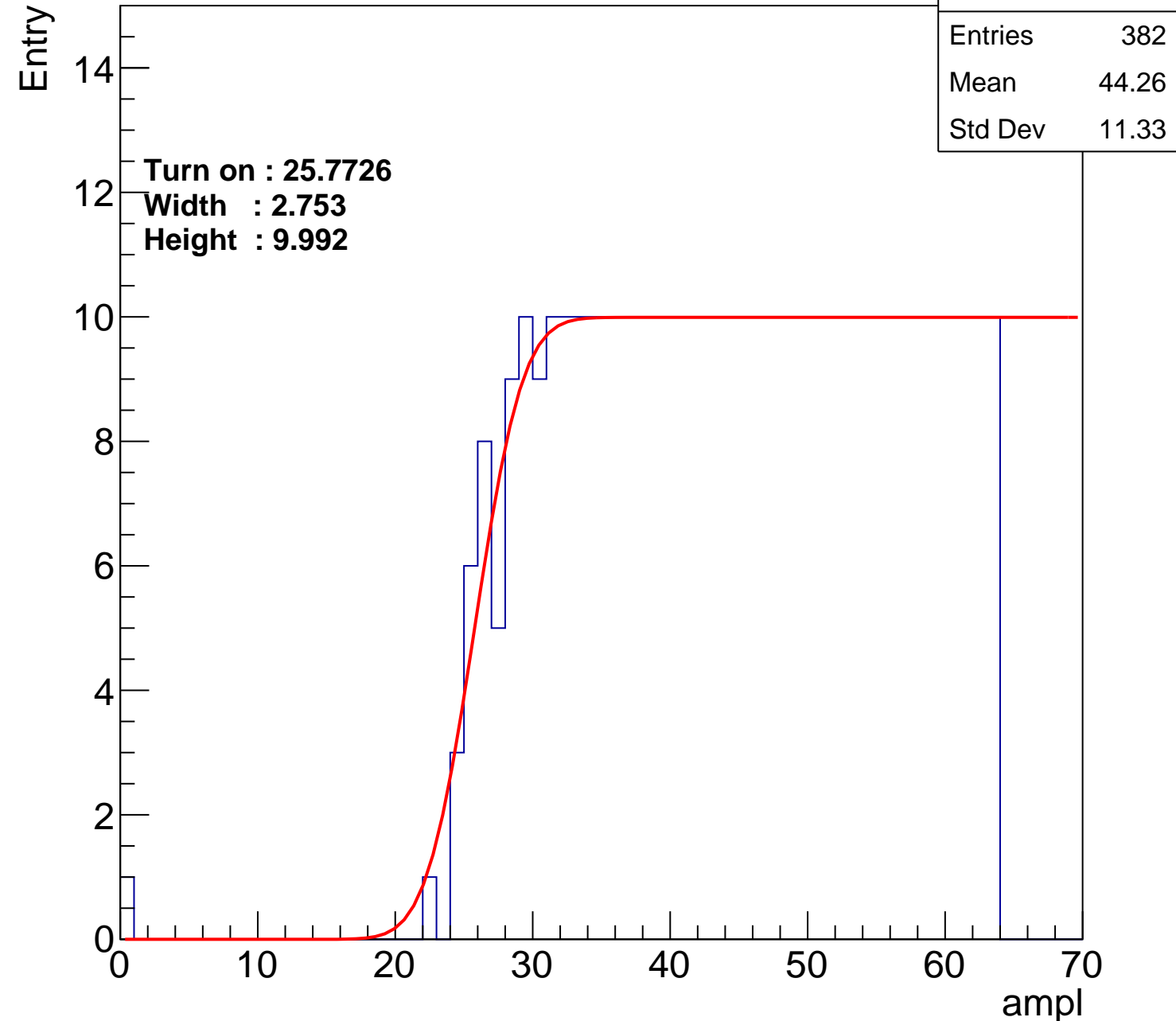
Width : 2.753

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.25
Std Dev	12.46

Turn on : 24.8988

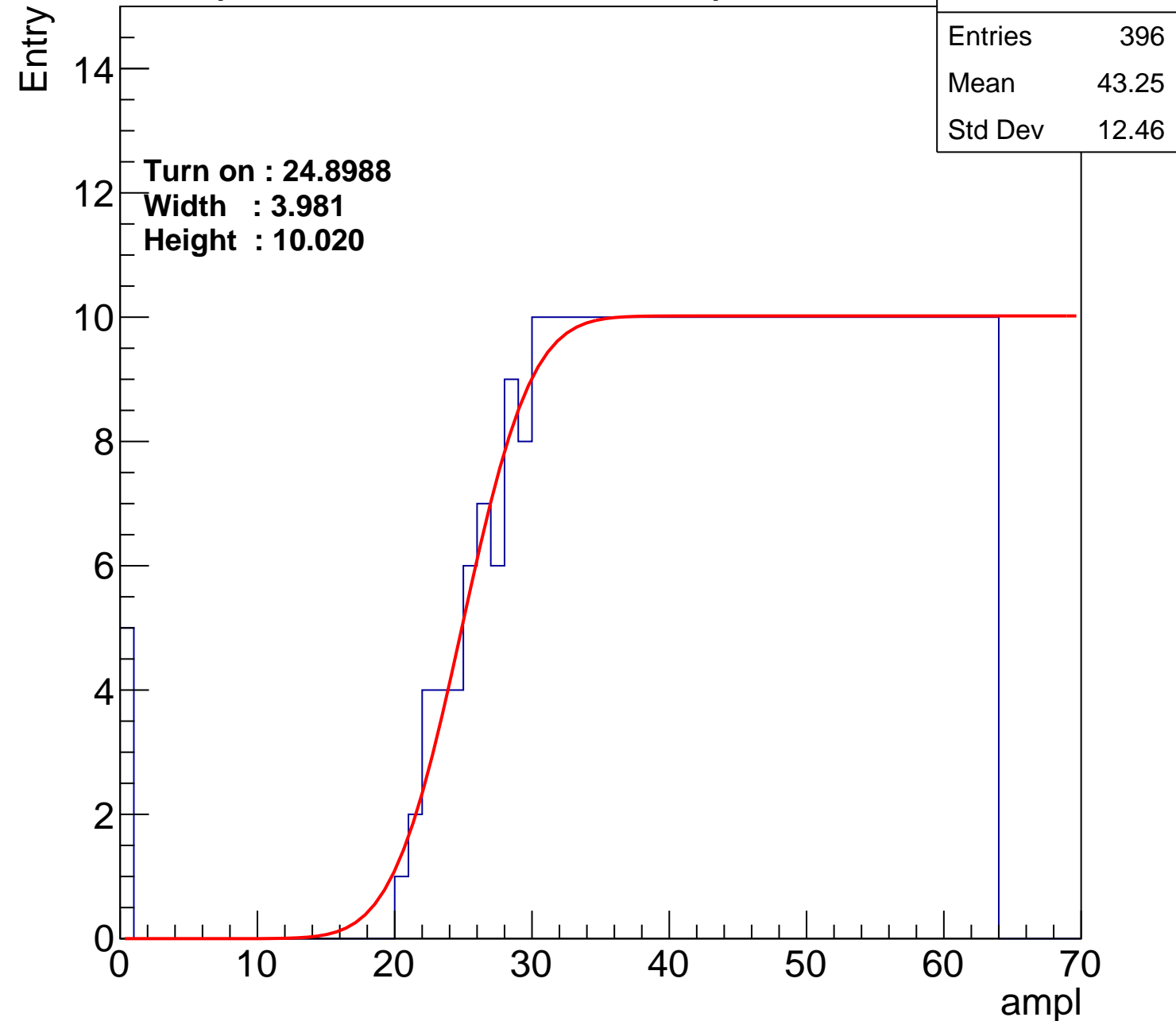
Width : 3.981

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch47

calib_packv5_042523_0143.root, FC#12, port B1

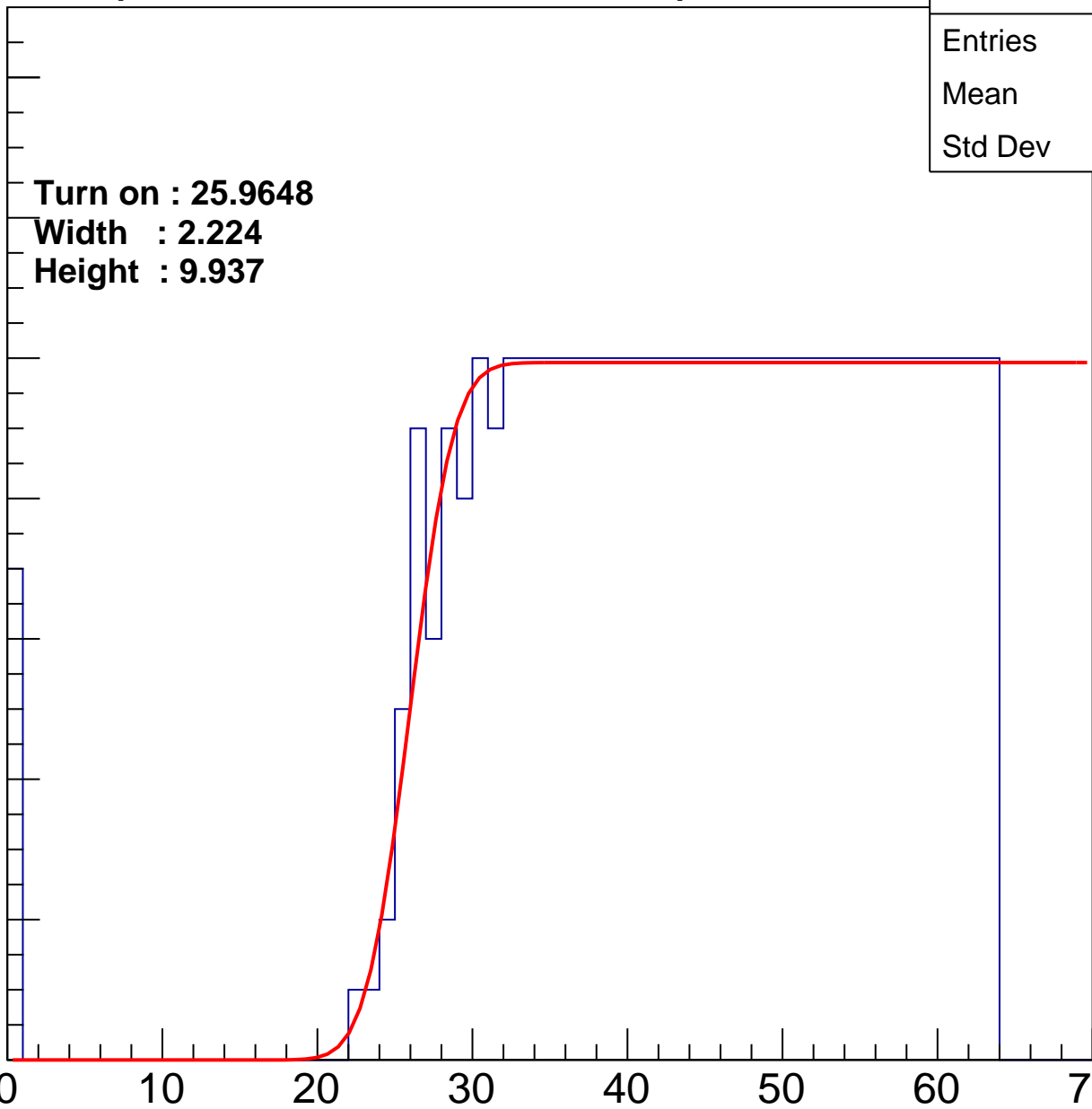
Entry

14
12
10
8
6
4
2
0

Turn on : 25.9648
Width : 2.224
Height : 9.937

Entries	387
Mean	43.61
Std Dev	12.5

ampl



B0L102S, U1-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.38
Std Dev	12.14

Turn on : 25.0086

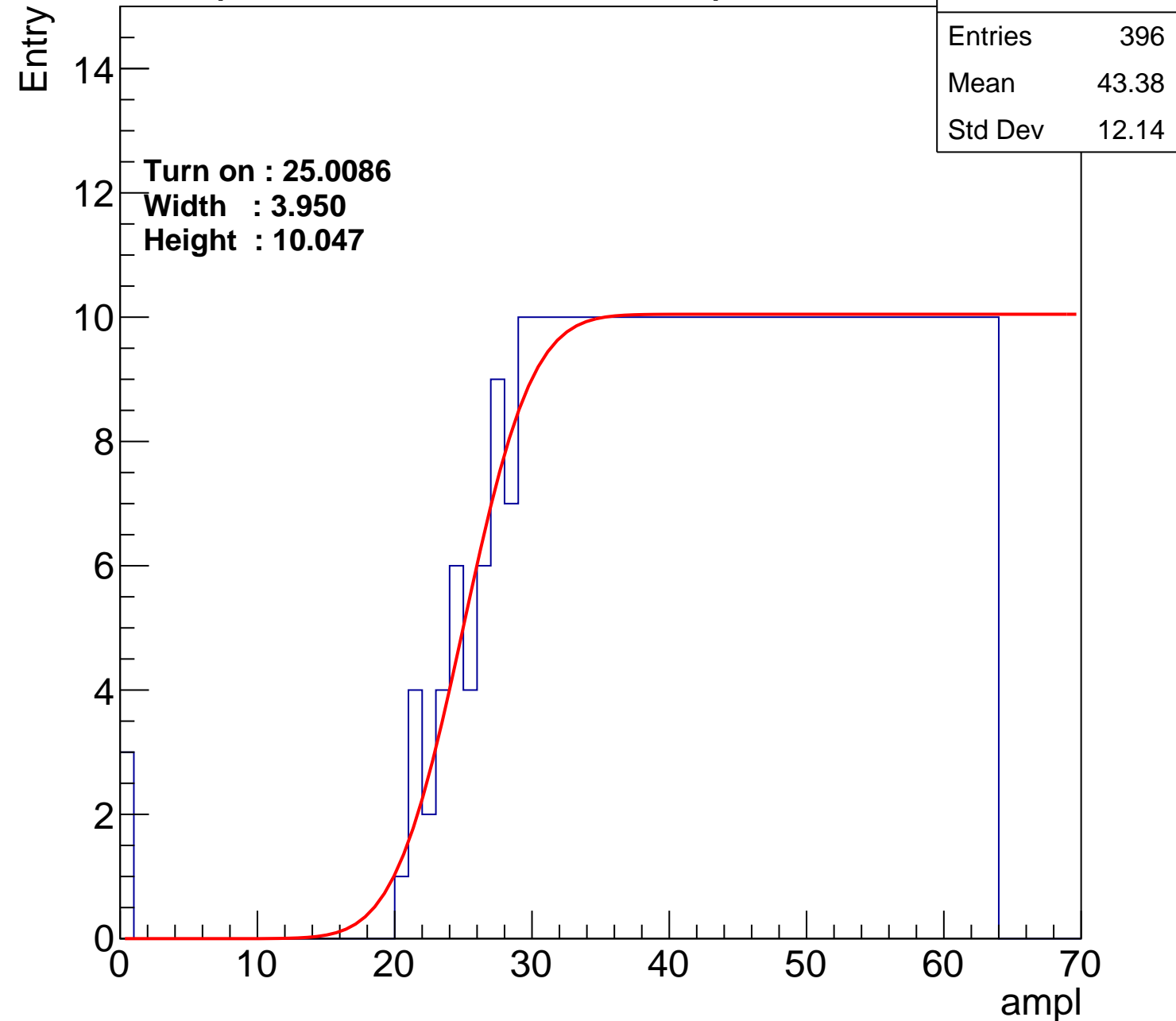
Width : 3.950

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch49

calib_packv5_042523_0143.root, FC#12, port B1

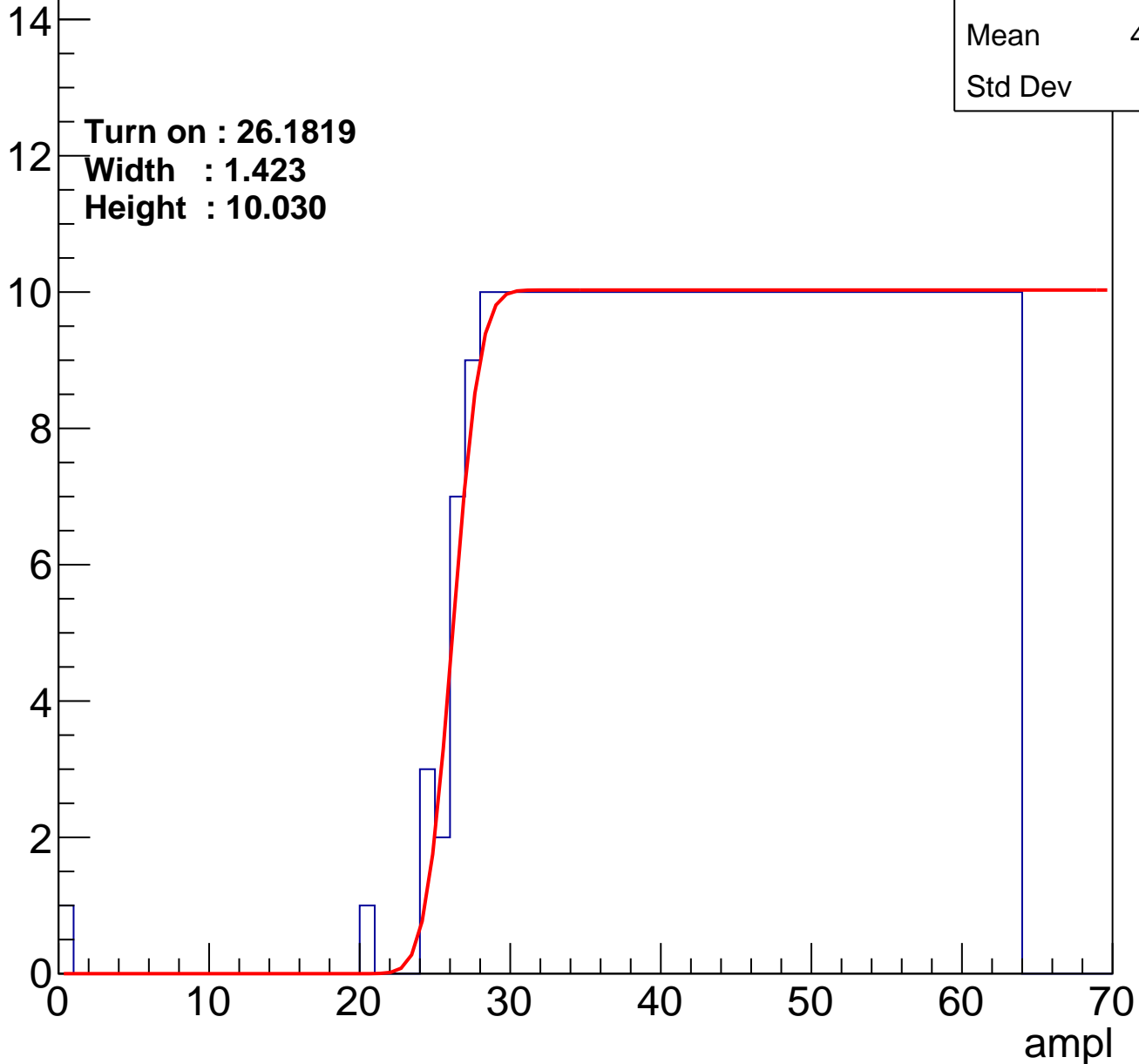
Entries	383
Mean	44.25
Std Dev	11.3

Turn on : 26.1819

Width : 1.423

Height : 10.030

Entry



B0L102S, U1-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	419
Mean	42.1
Std Dev	13.14

Turn on : 23.0813

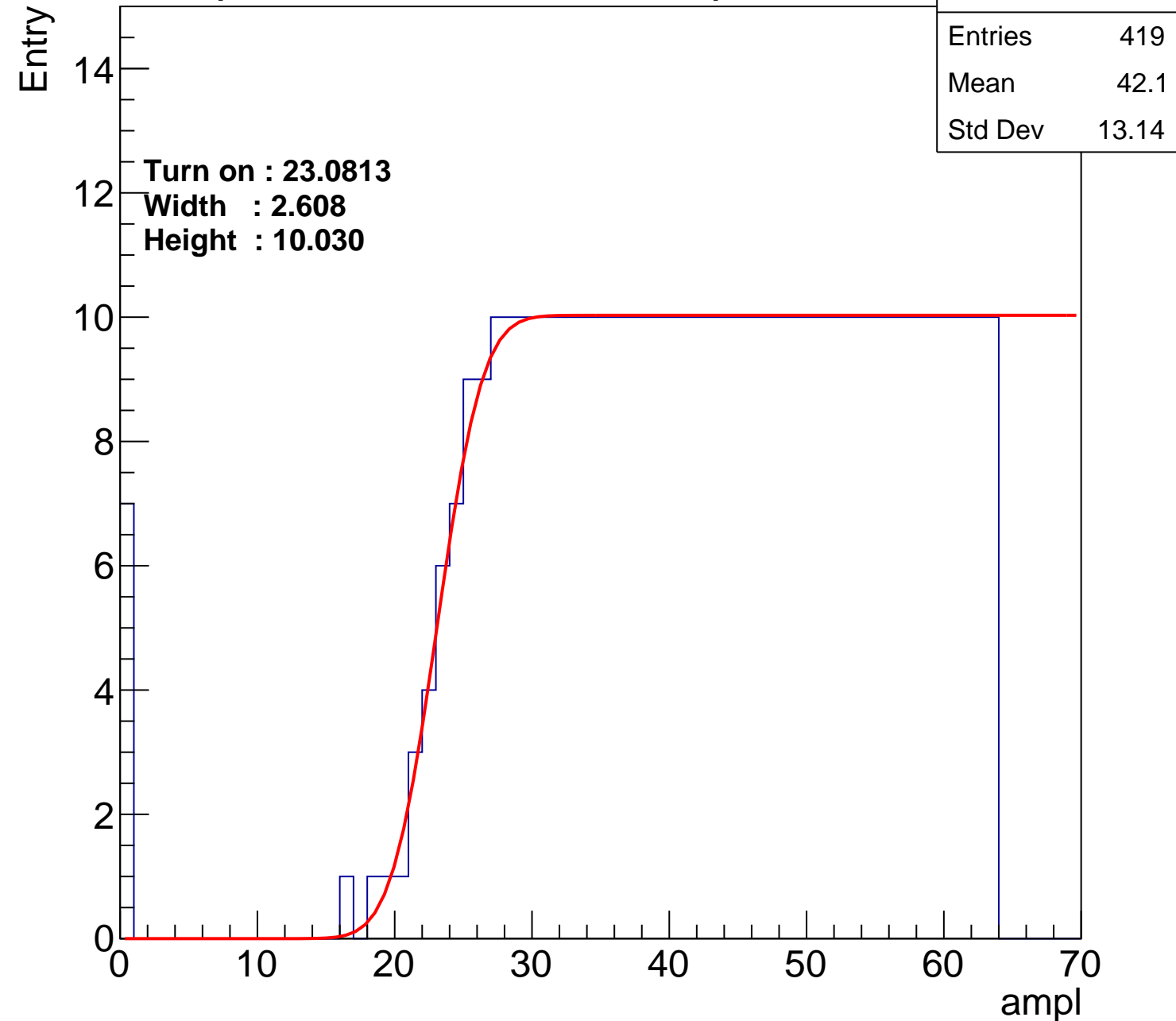
Width : 2.608

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.14
Std Dev	12.21

Turn on : 24.4128

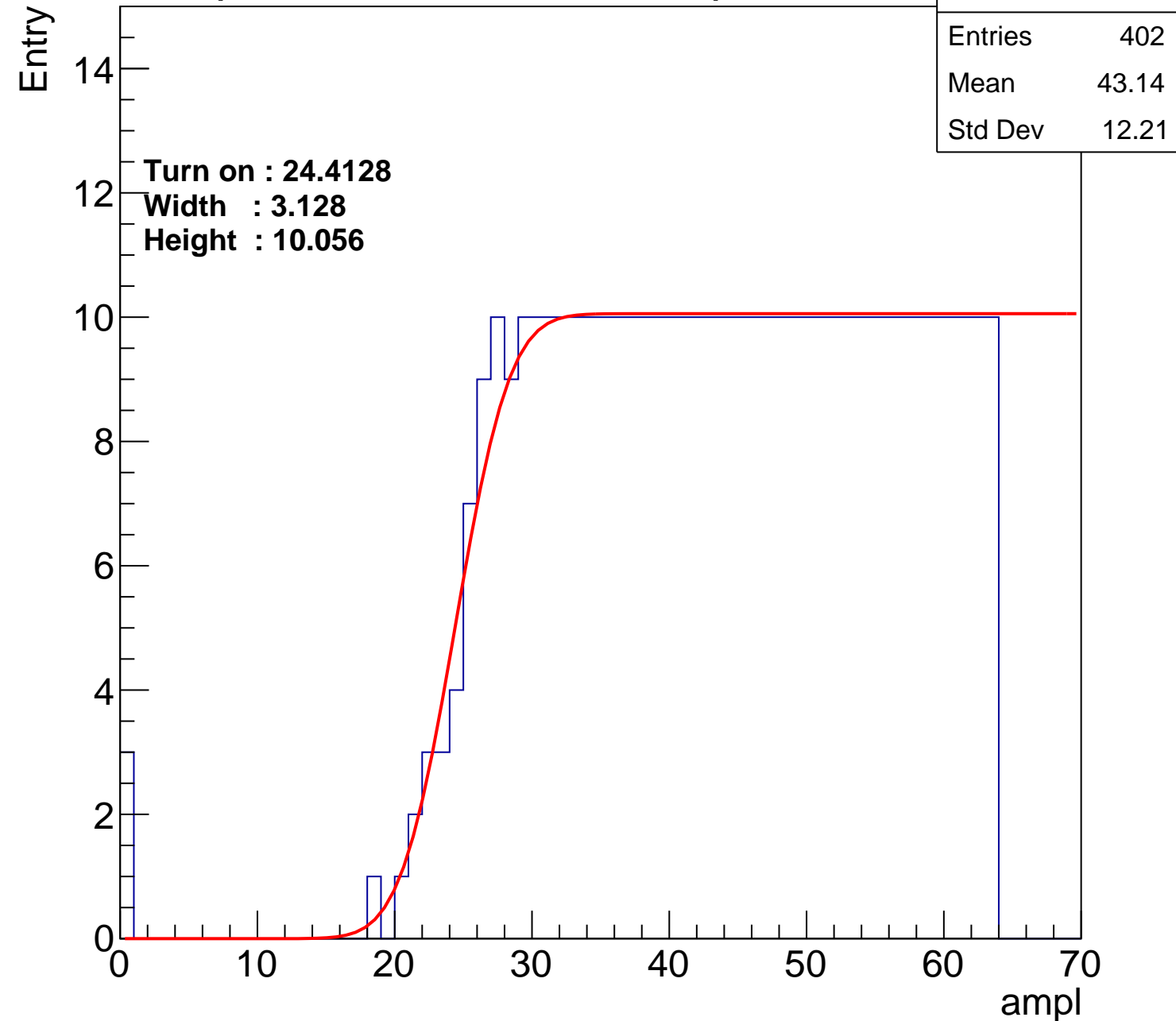
Width : 3.128

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.07
Std Dev	11.8

Turn on : 27.2932

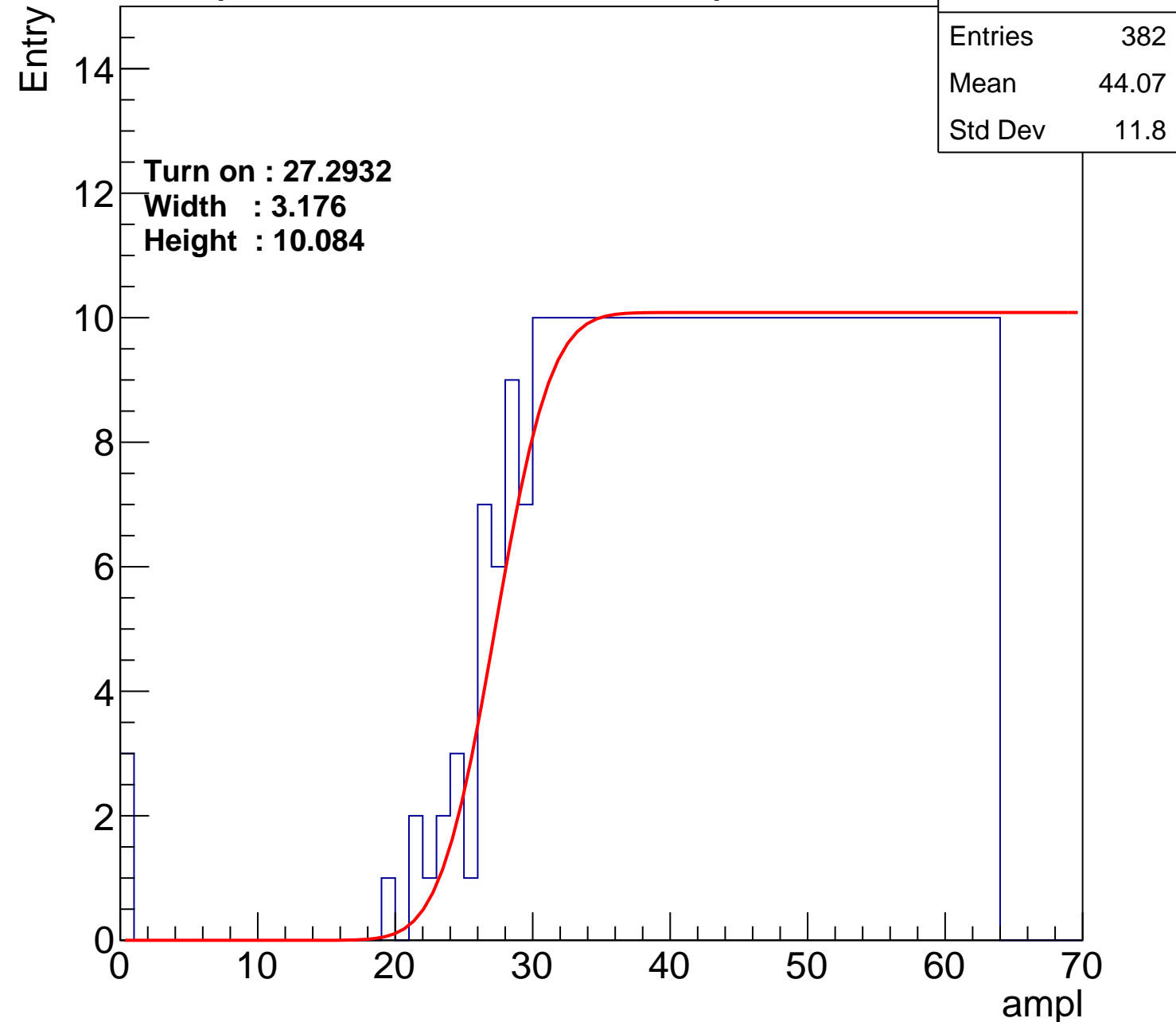
Width : 3.176

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch53

calib_packv5_042523_0143.root, FC#12, port B1

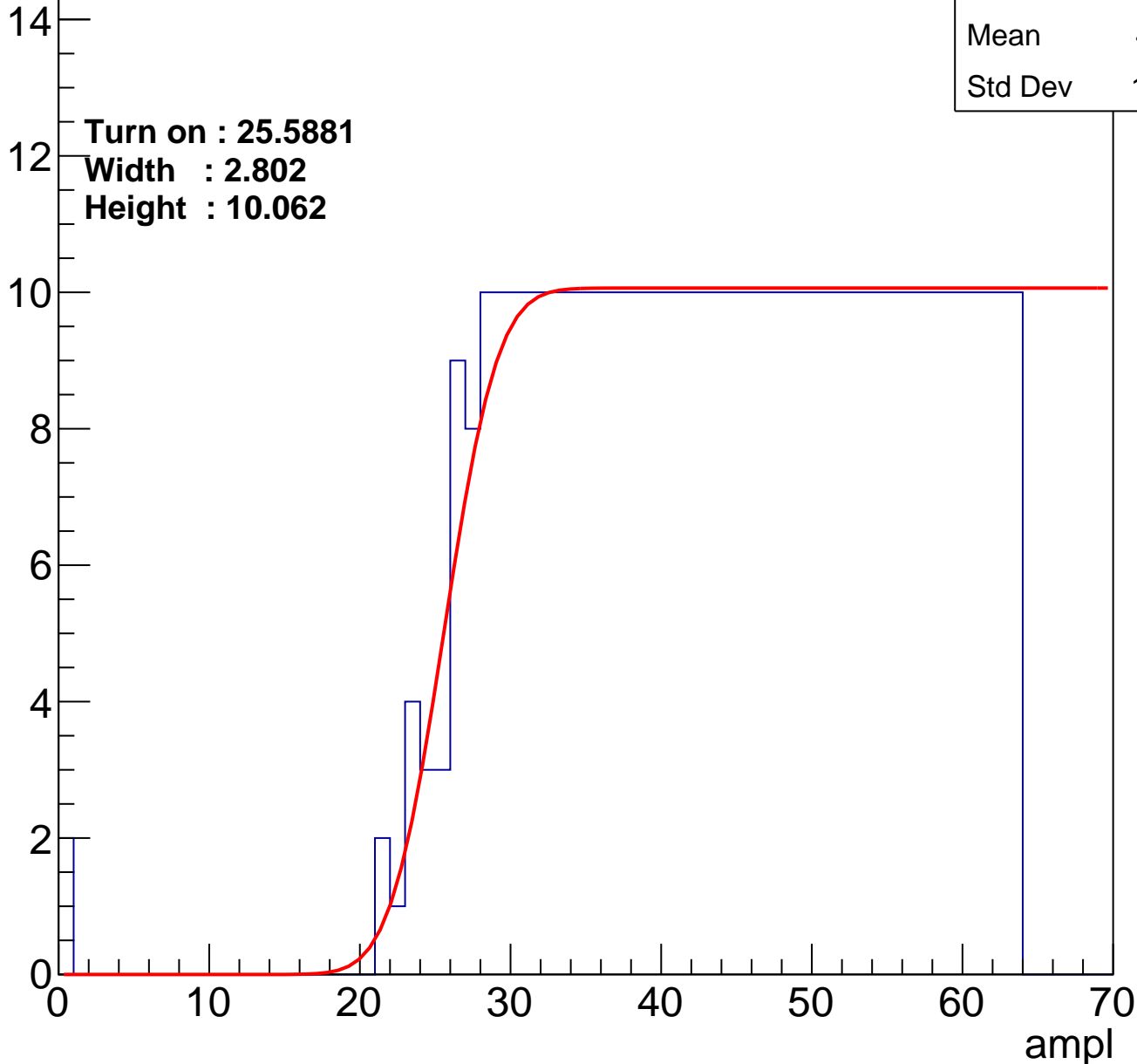
Entries	392
Mean	43.71
Std Dev	11.77

Turn on : 25.5881

Width : 2.802

Height : 10.062

Entry



B0L102S, U1-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	418
Mean	42.35
Std Dev	12.61

Turn on : 22.0578

Width : 2.730

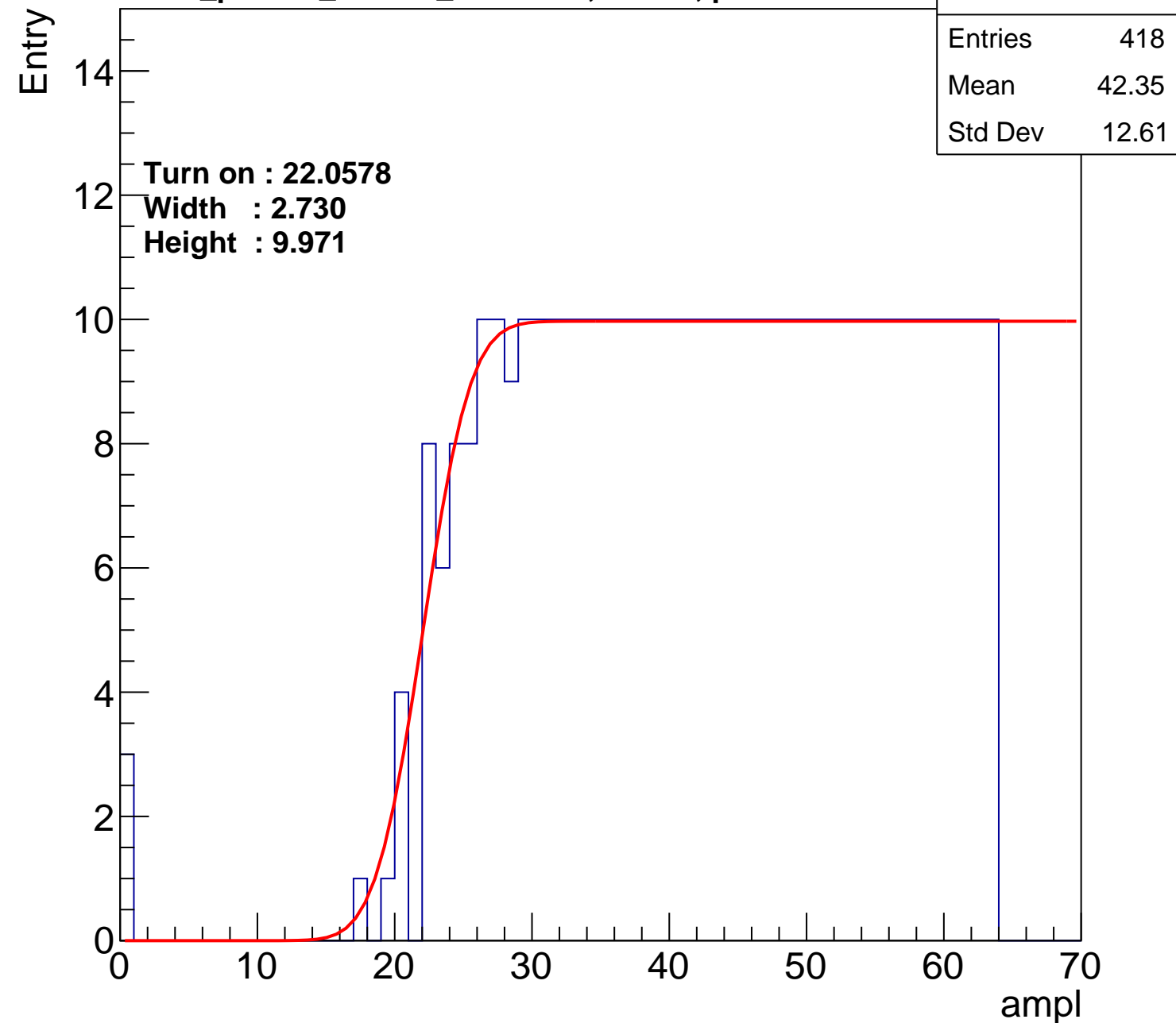
Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U1-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.8929

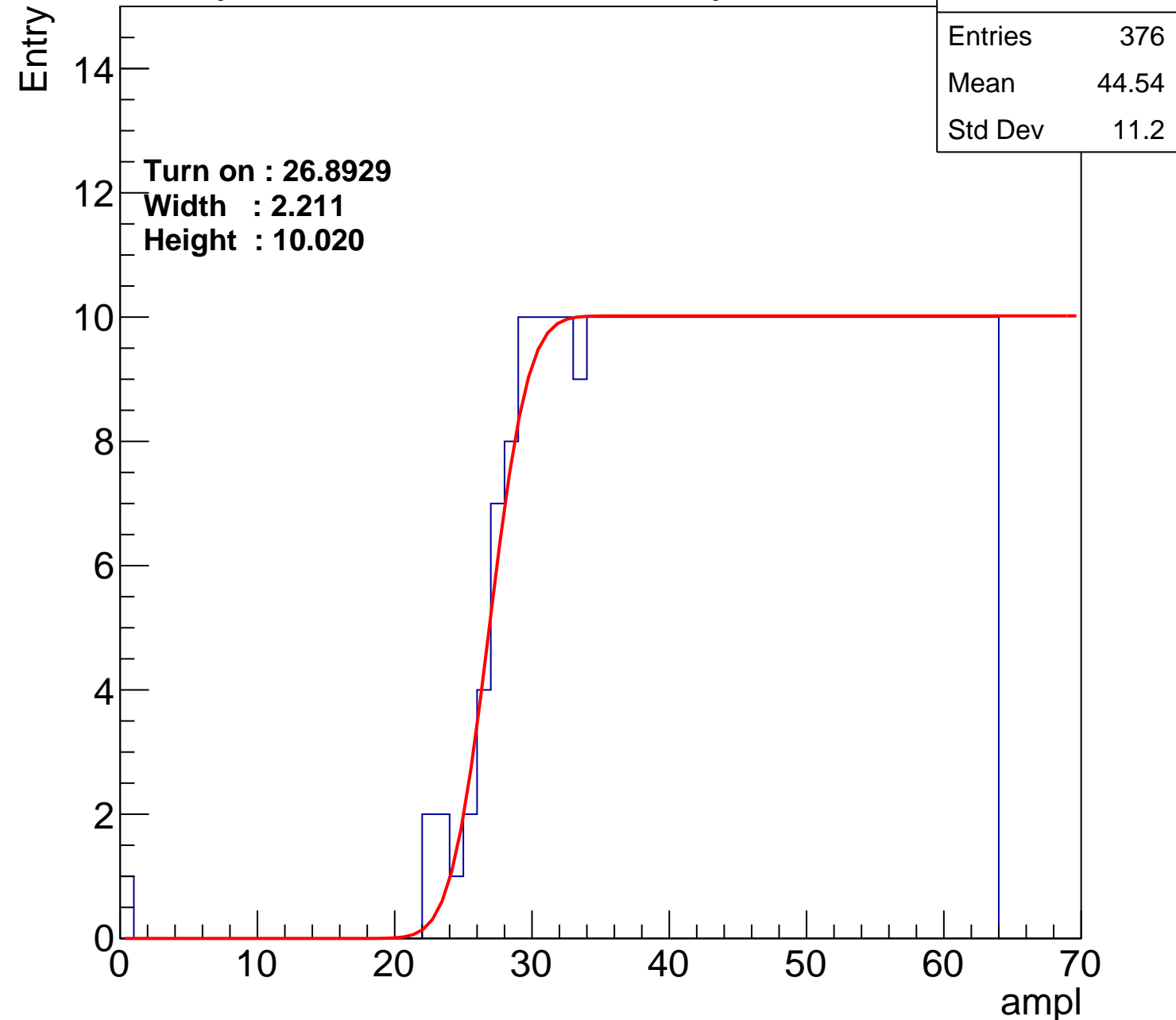
Width : 2.211

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.66
Std Dev	12.59

Turn on : 23.4612

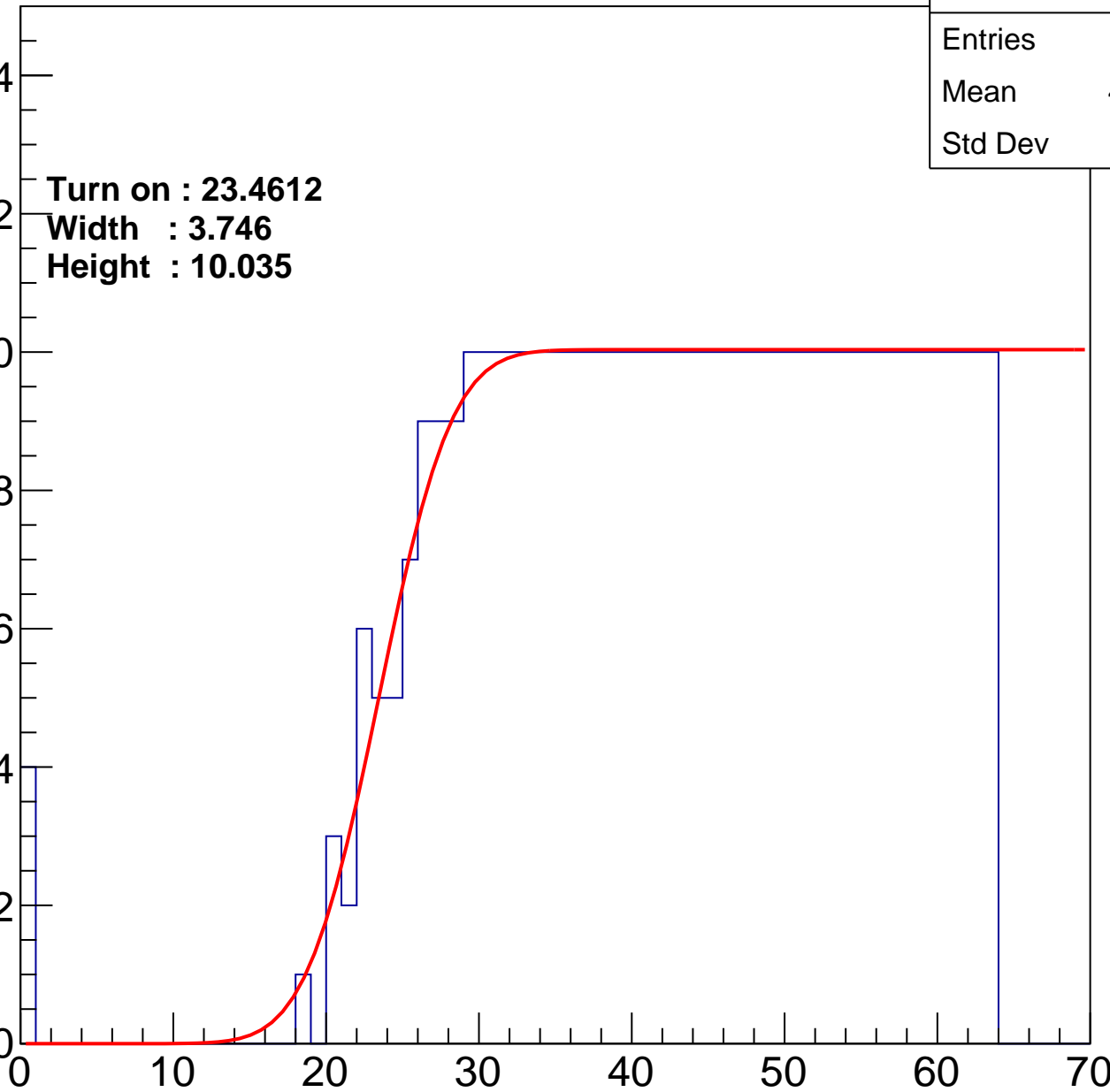
Width : 3.746

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.56
Std Dev	11.23

Turn on : 27.4688

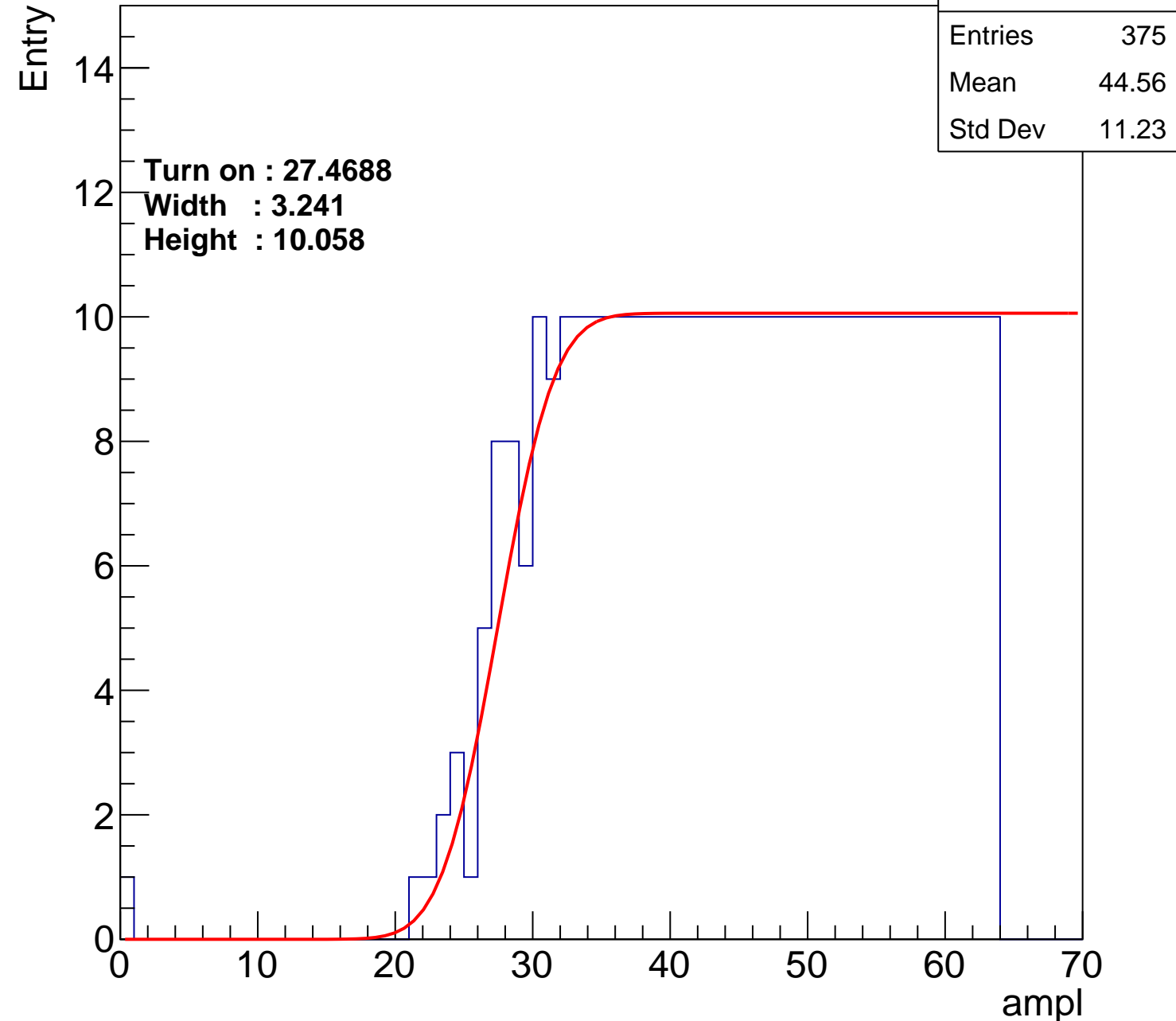
Width : 3.241

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.91
Std Dev	11.88

Turn on : 25.8634

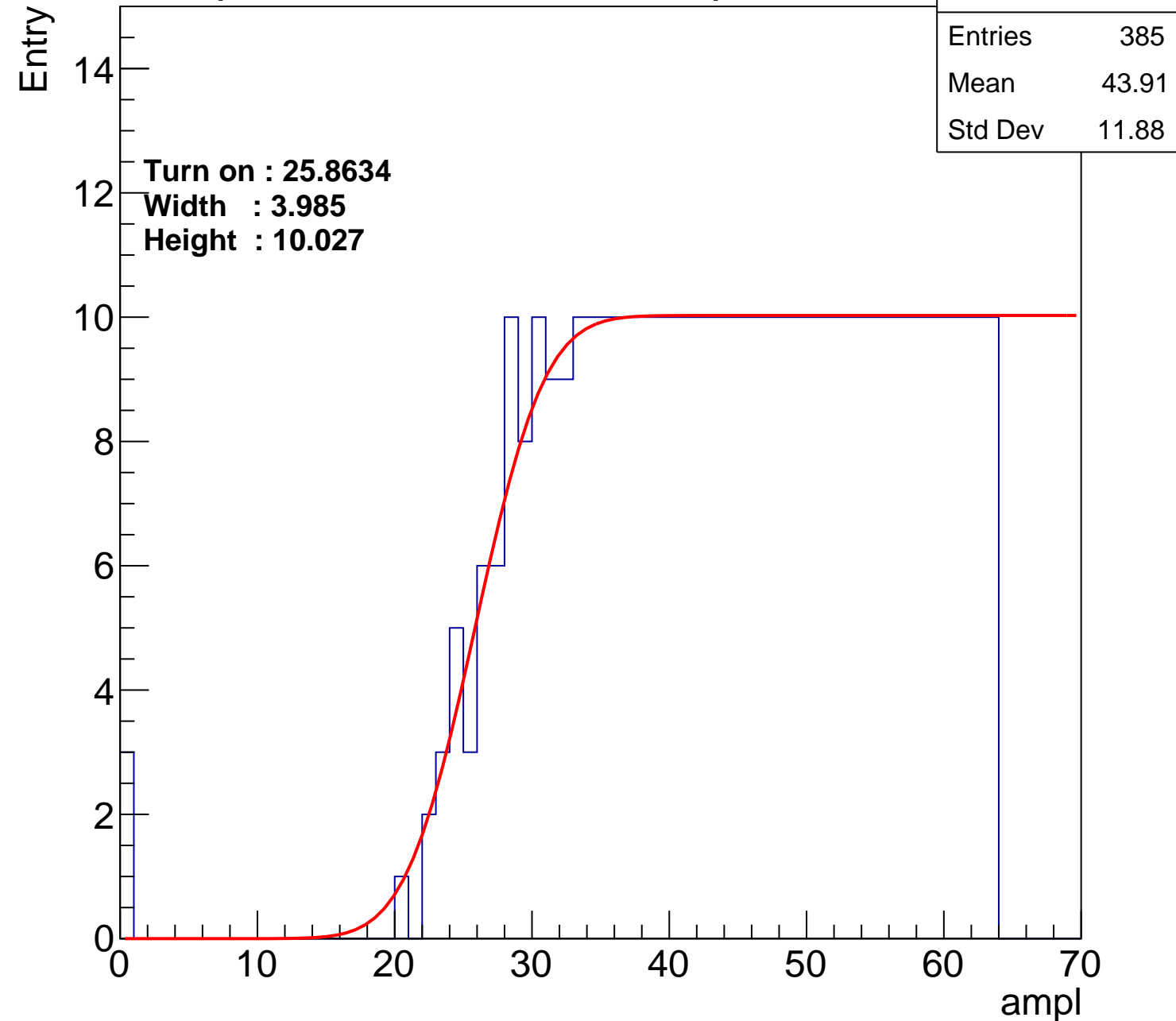
Width : 3.985

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.79
Std Dev	11.92

Turn on : 25.7009

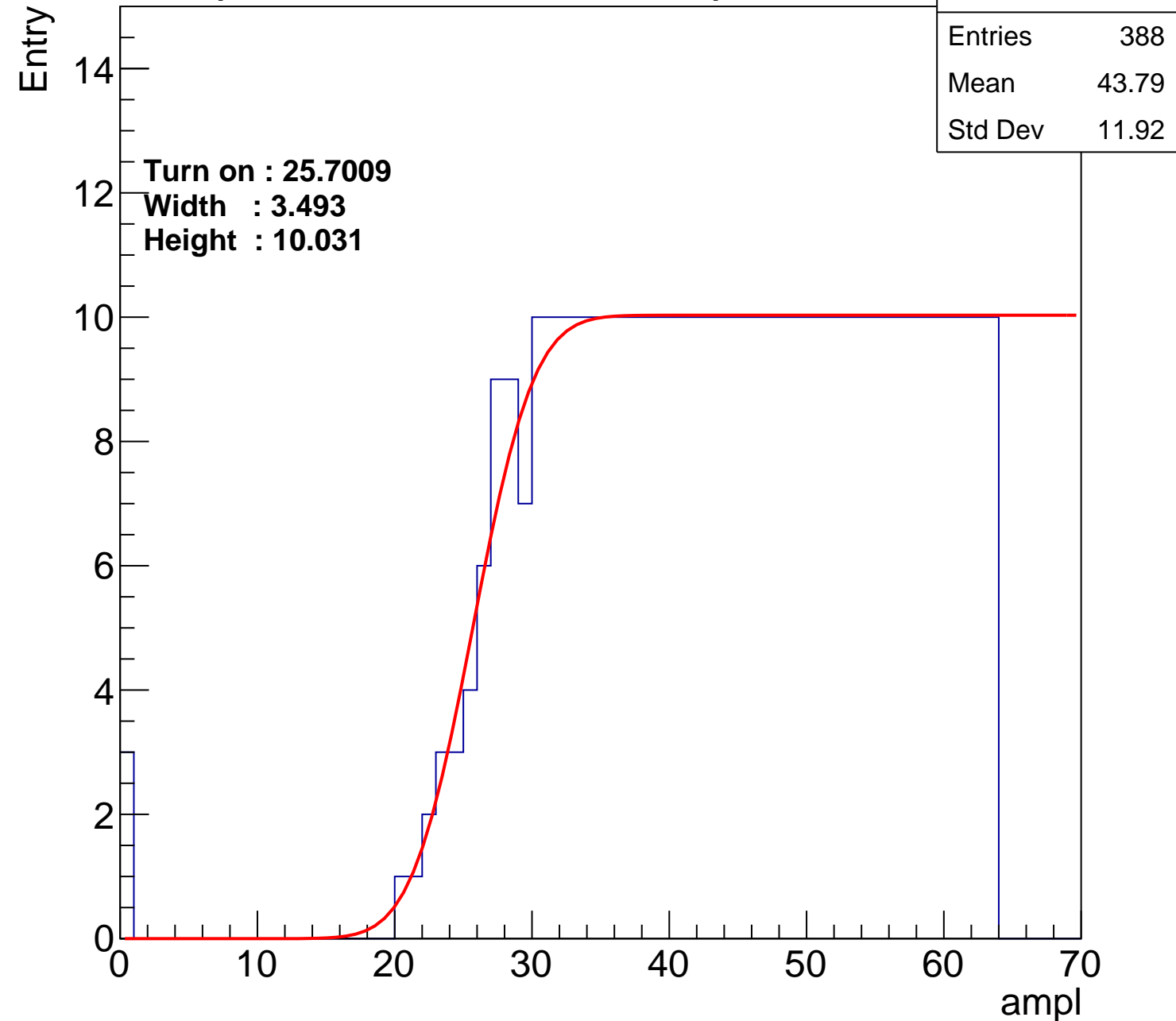
Width : 3.493

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.37
Std Dev	12.22

Turn on : 25.8325

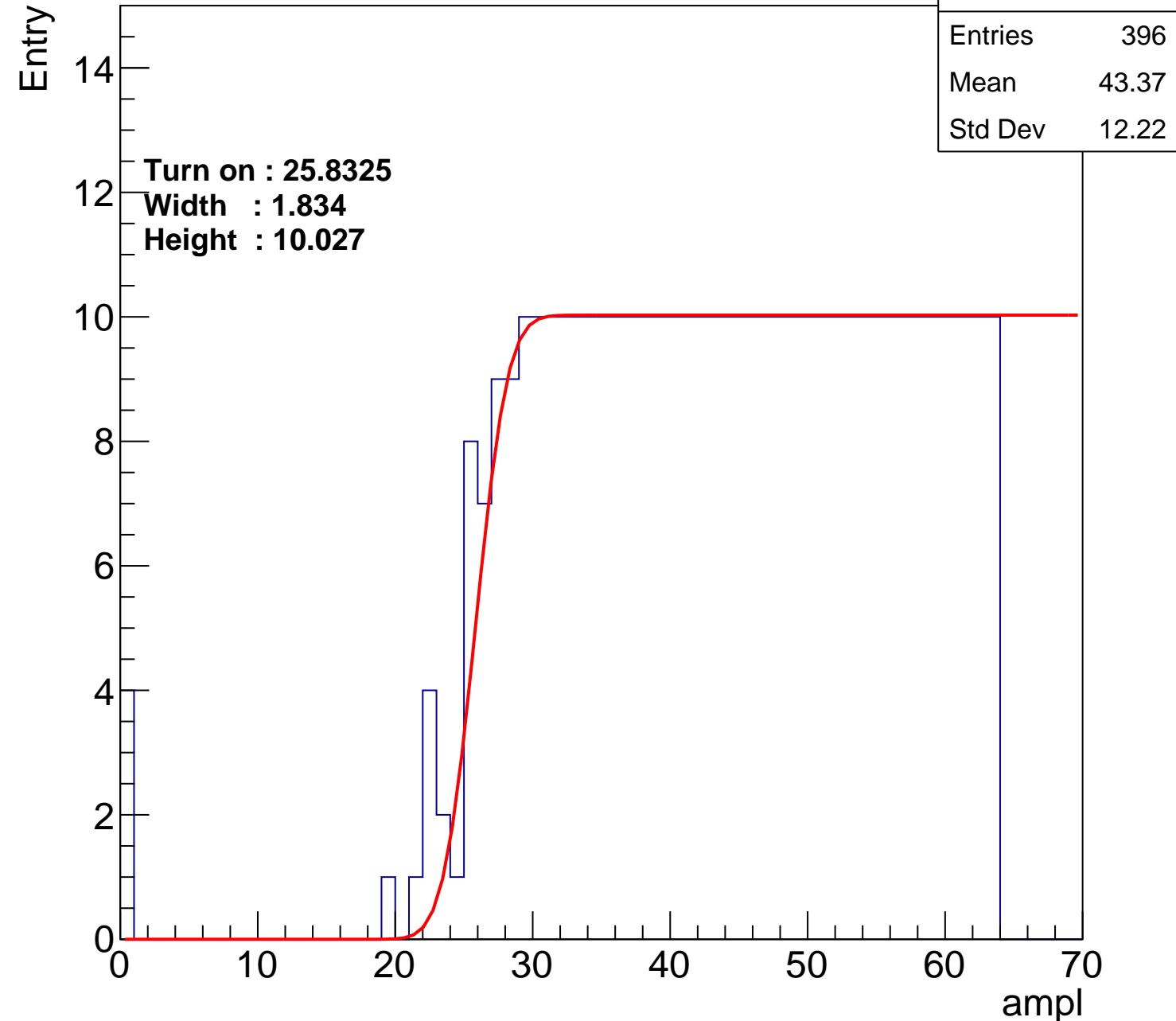
Width : 1.834

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.42
Std Dev	11.48

Turn on : 27.0203

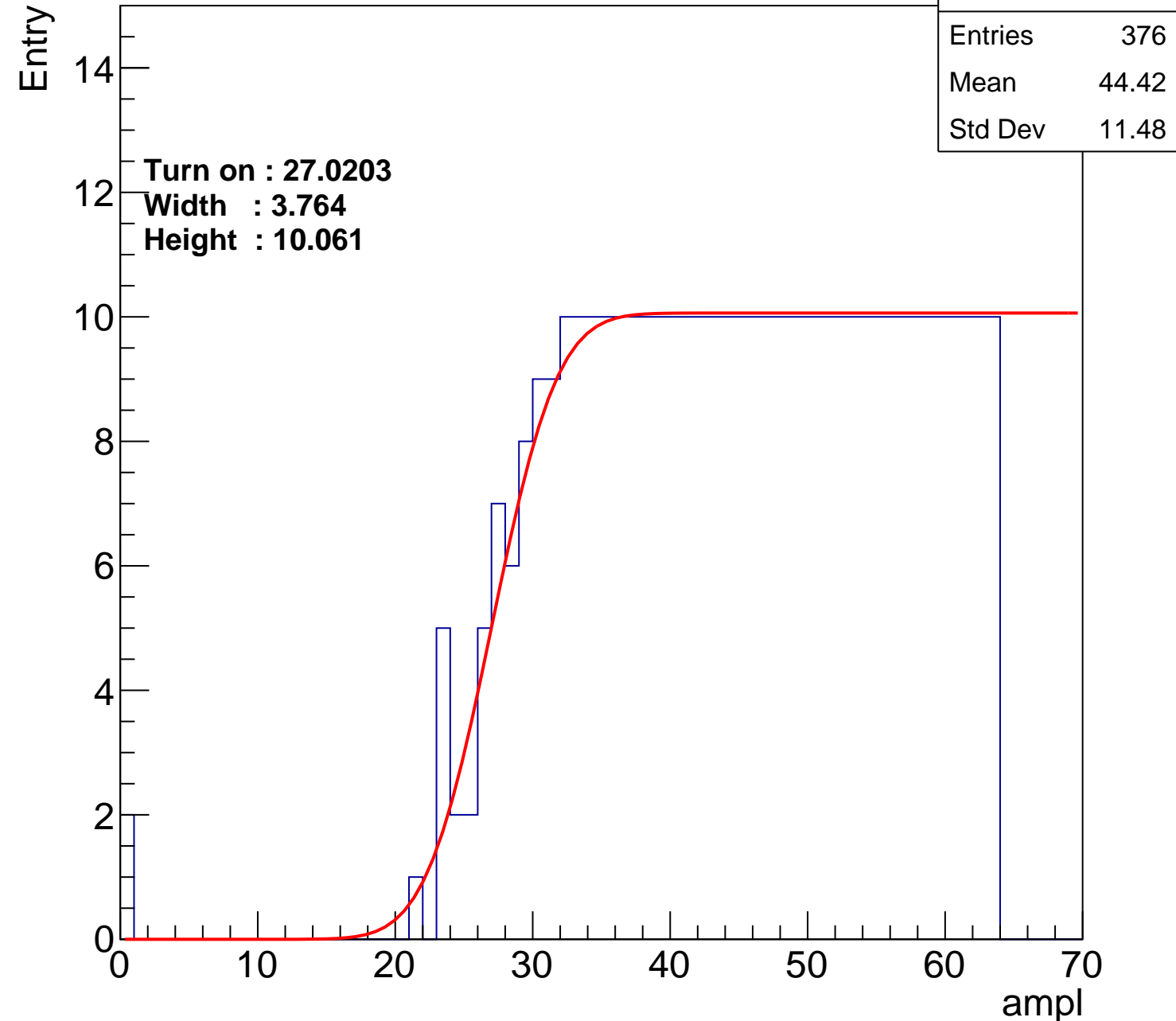
Width : 3.764

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.55
Std Dev	12.2

Turn on : 25.7596

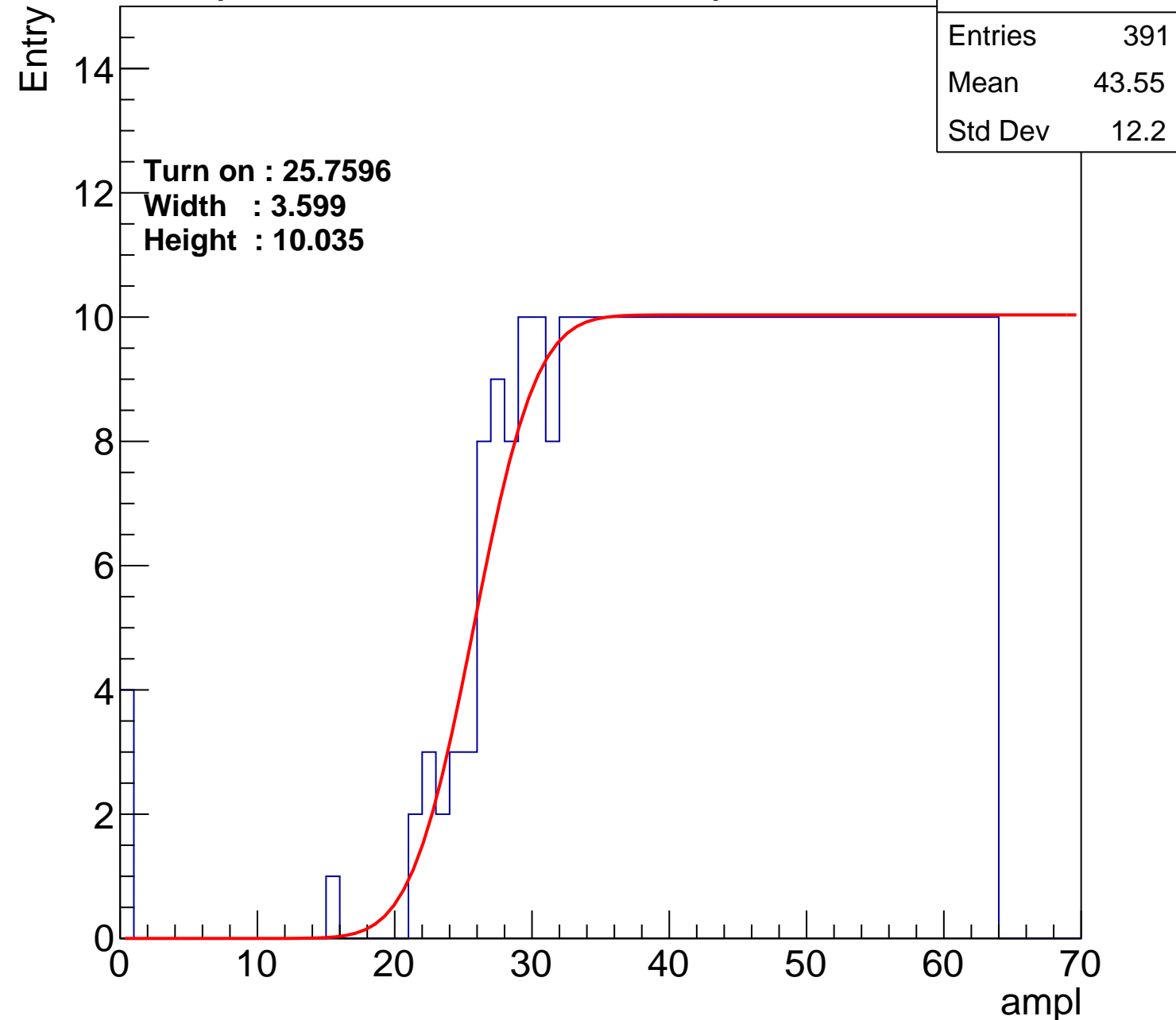
Width : 3.599

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch63

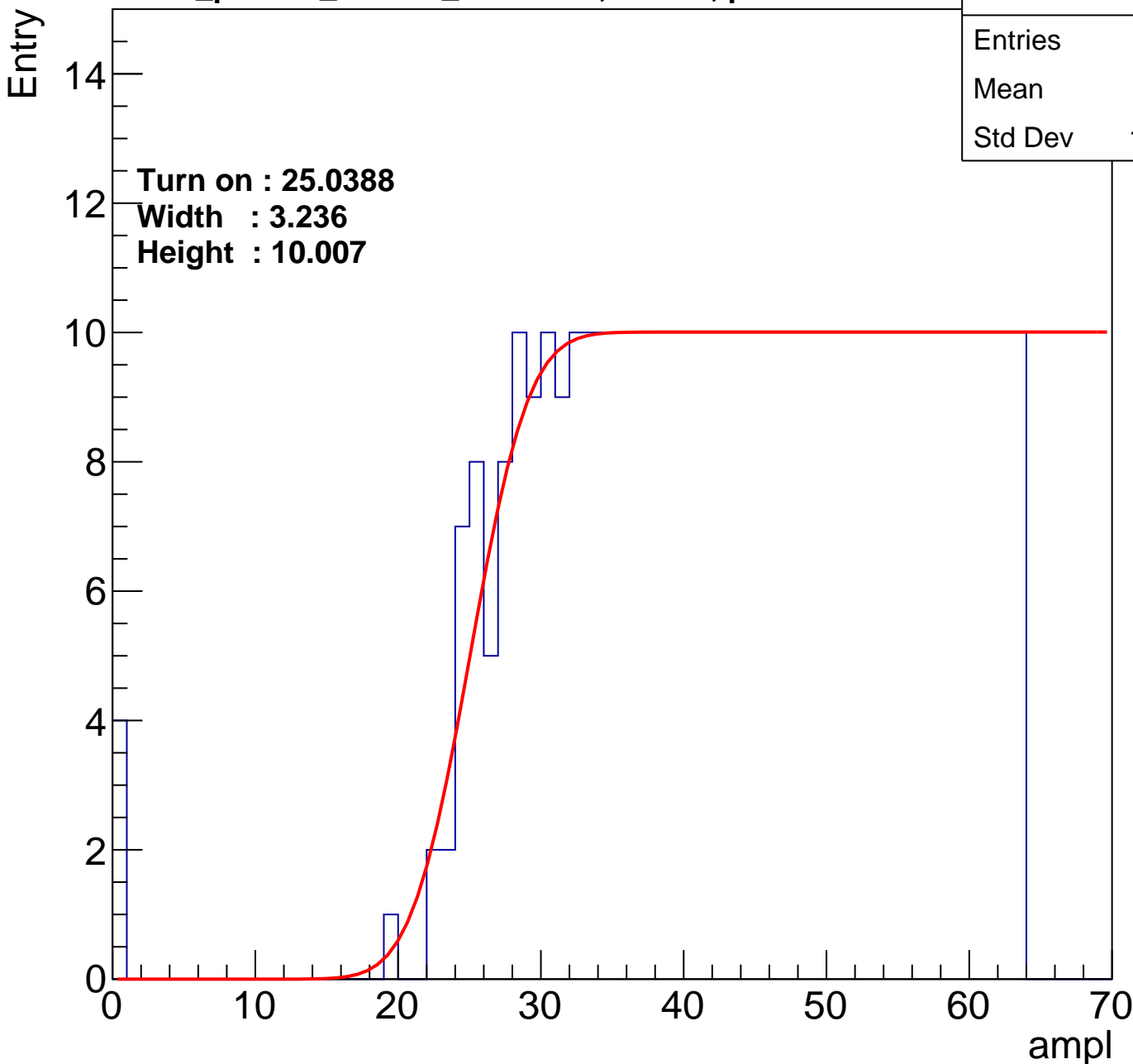
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.0388

Width : 3.236

Height : 10.007

Entries	395
Mean	43.4
Std Dev	12.22



B0L102S, U1-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 25.2406

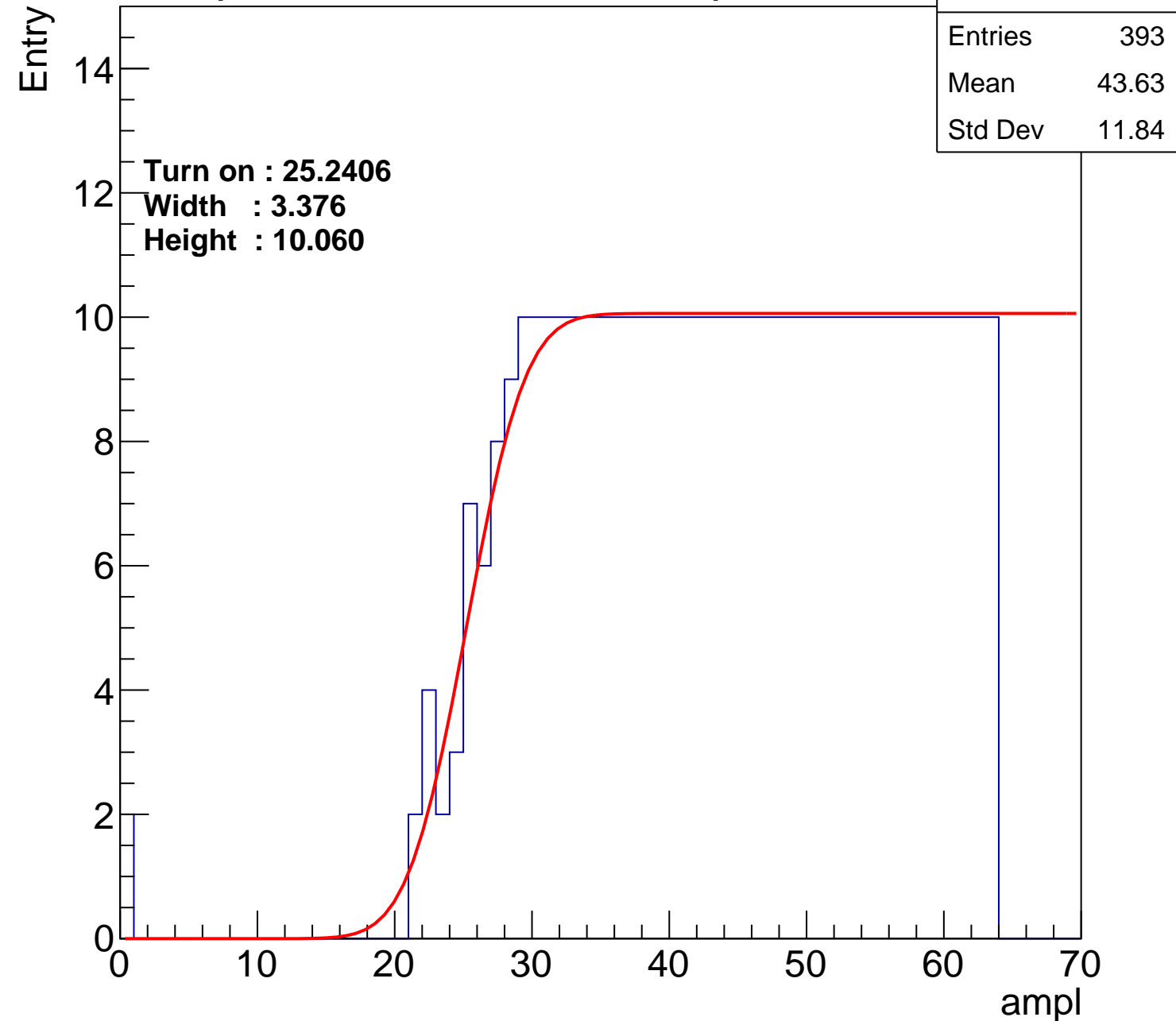
Width : 3.376

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.23
Std Dev	12.17

Turn on : 27.5109

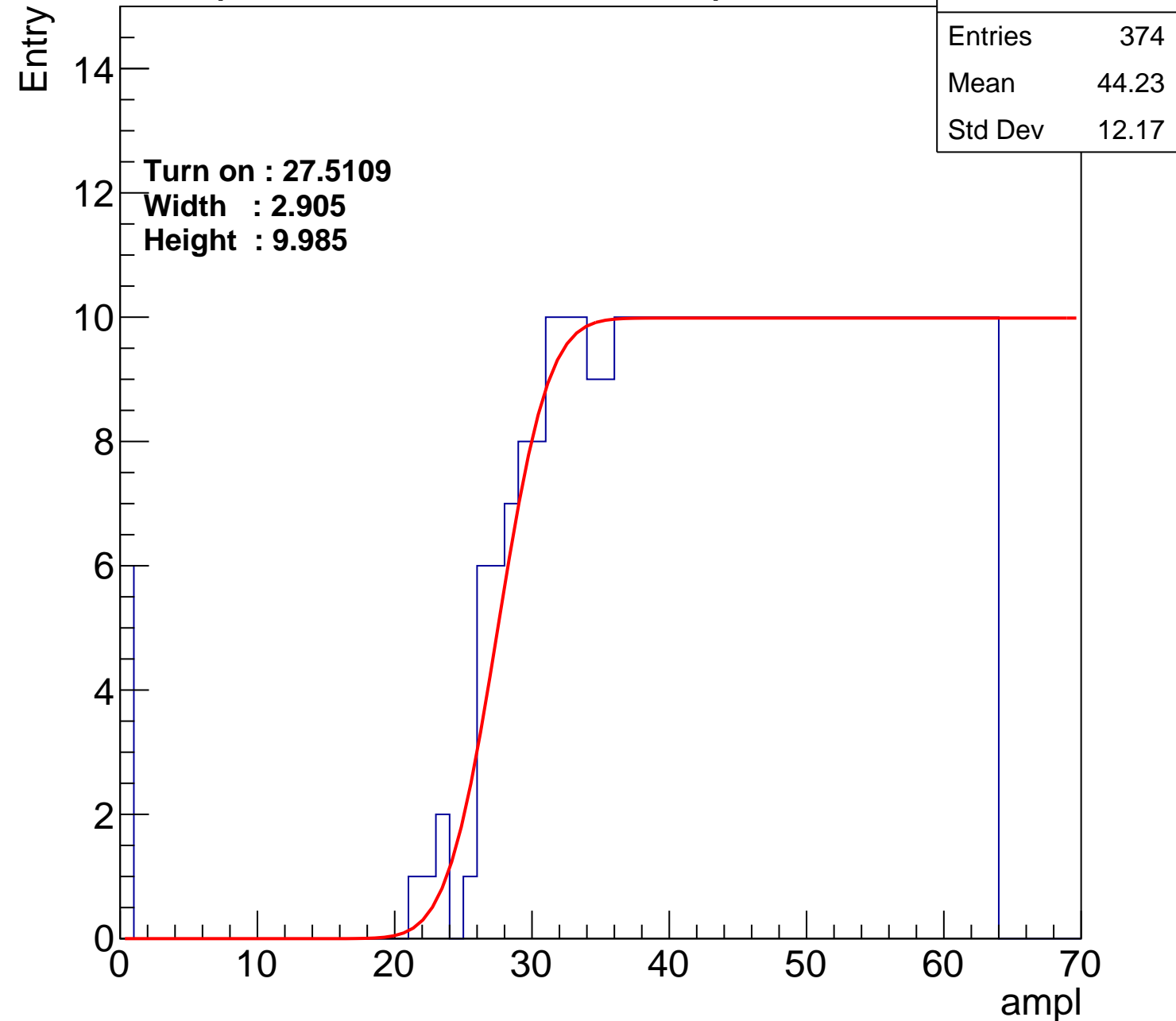
Width : 2.905

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.51
Std Dev	12.78

Turn on : 23.5852

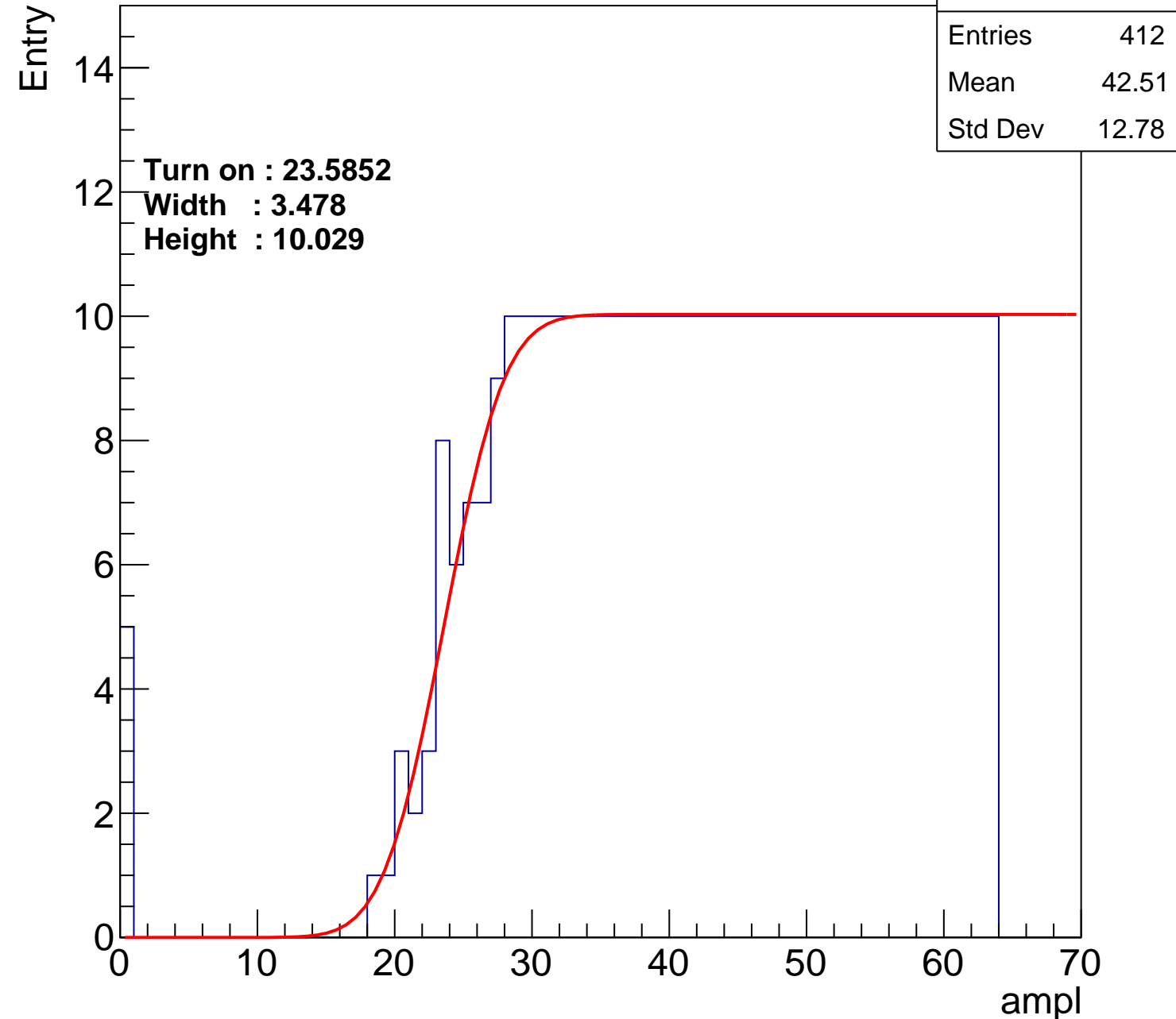
Width : 3.478

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.48
Std Dev	11.81

Turn on : 27.5209

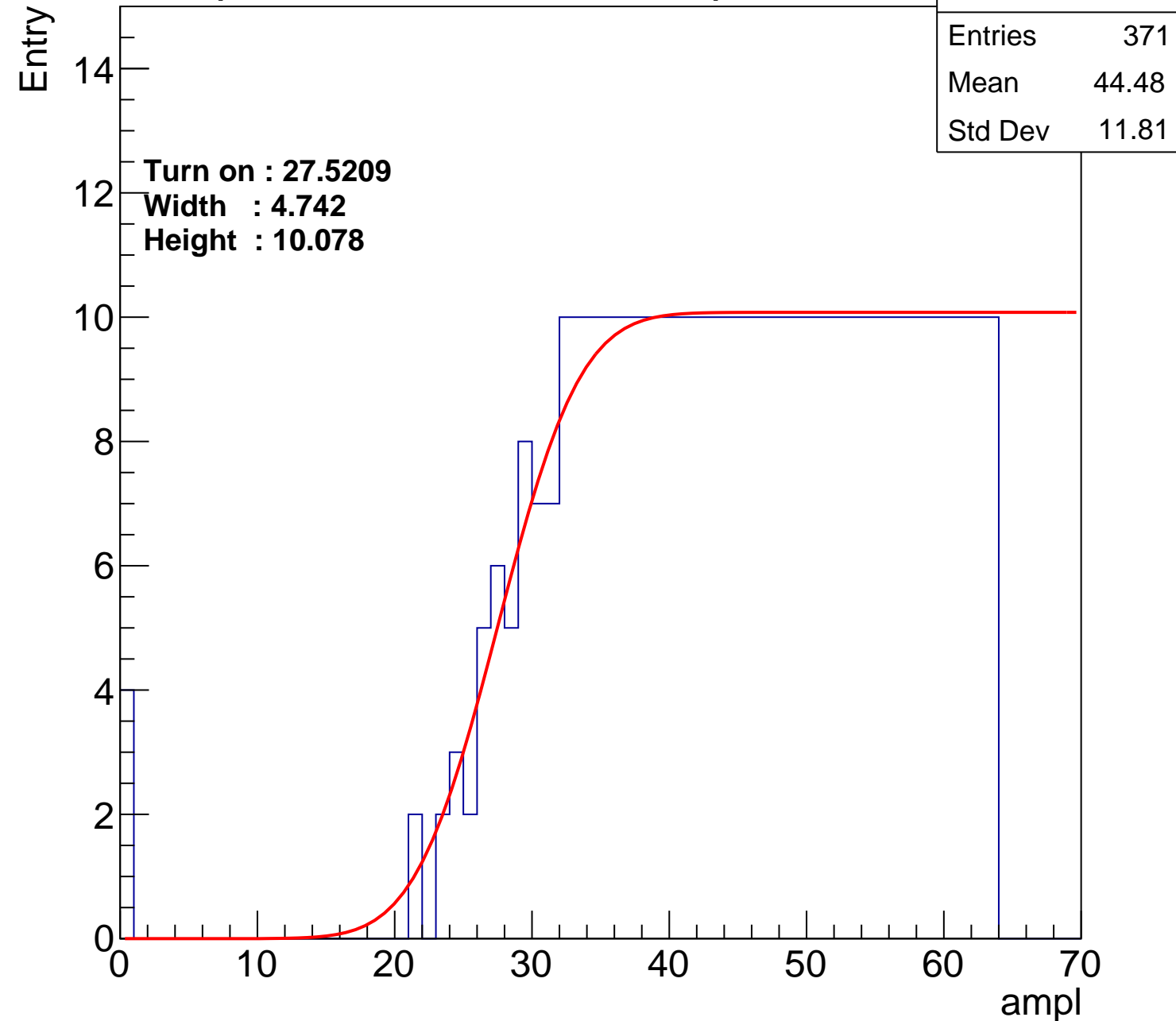
Width : 4.742

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch68

calib_packv5_042523_0143.root, FC#12, port B1

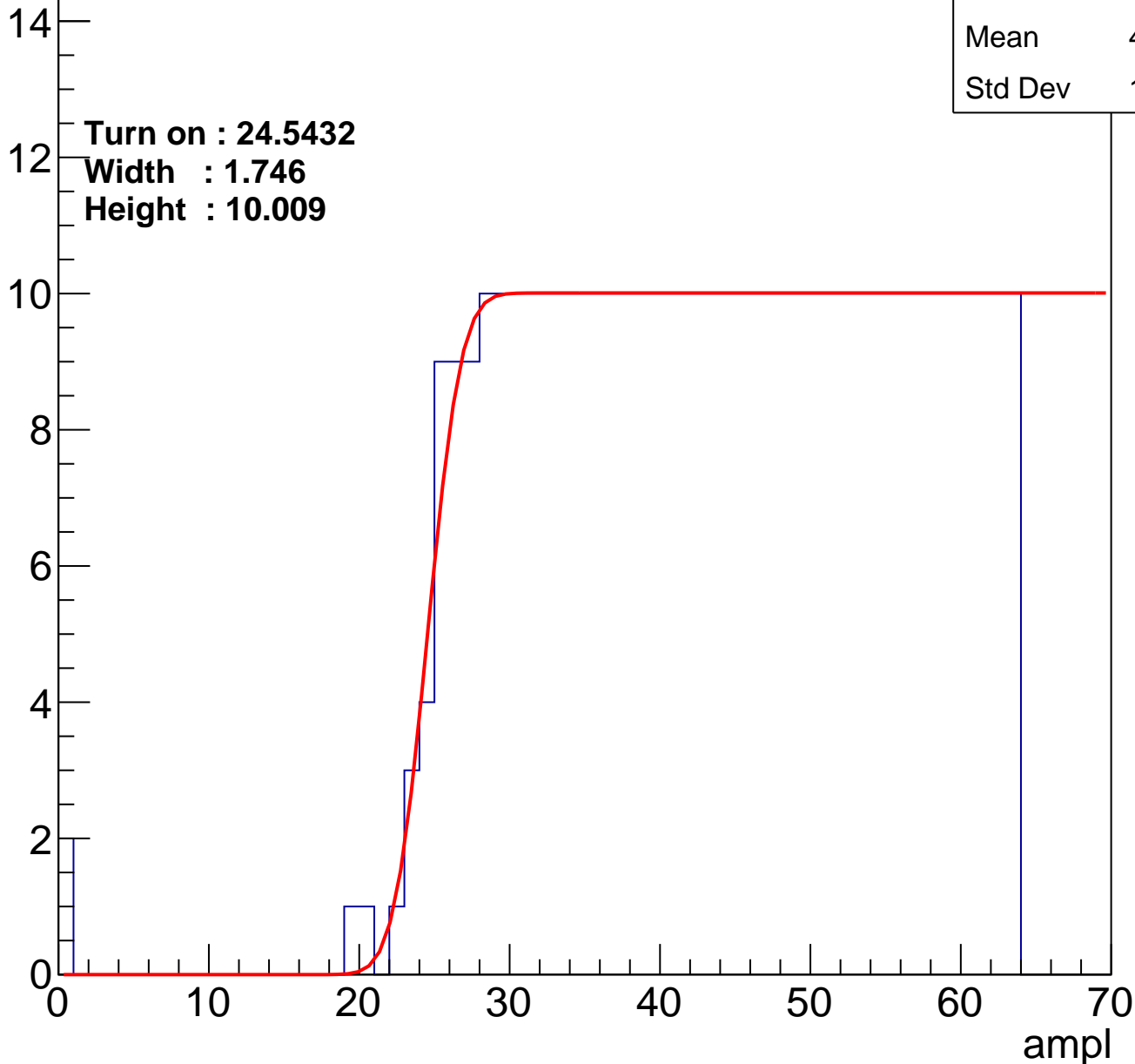
Entry

Entries	399
Mean	43.38
Std Dev	11.93

Turn on : 24.5432

Width : 1.746

Height : 10.009



B0L102S, U1-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.36
Std Dev	11.46

Turn on : 26.4111

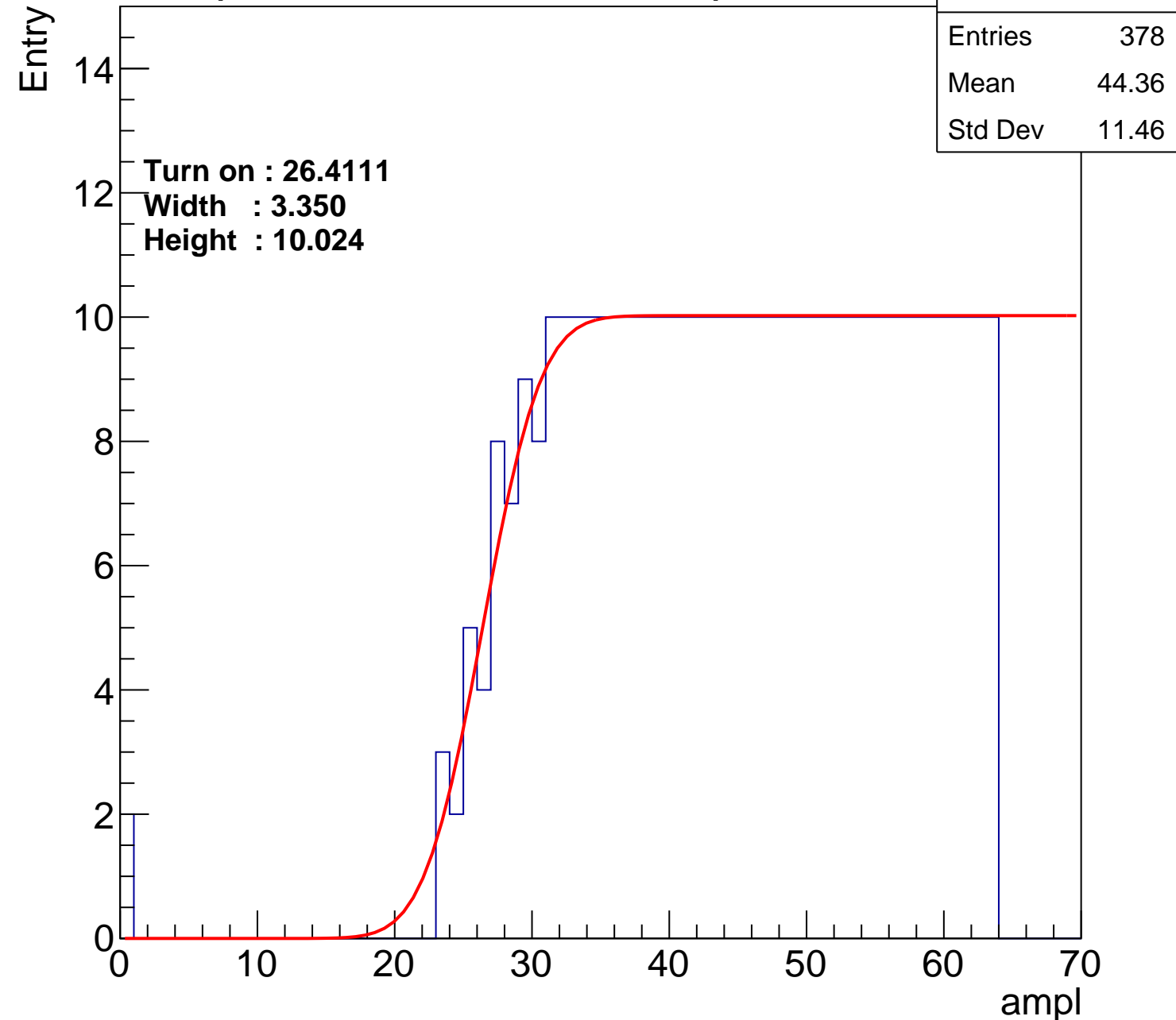
Width : 3.350

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch70

calib_packv5_042523_0143.root, FC#12, port B1

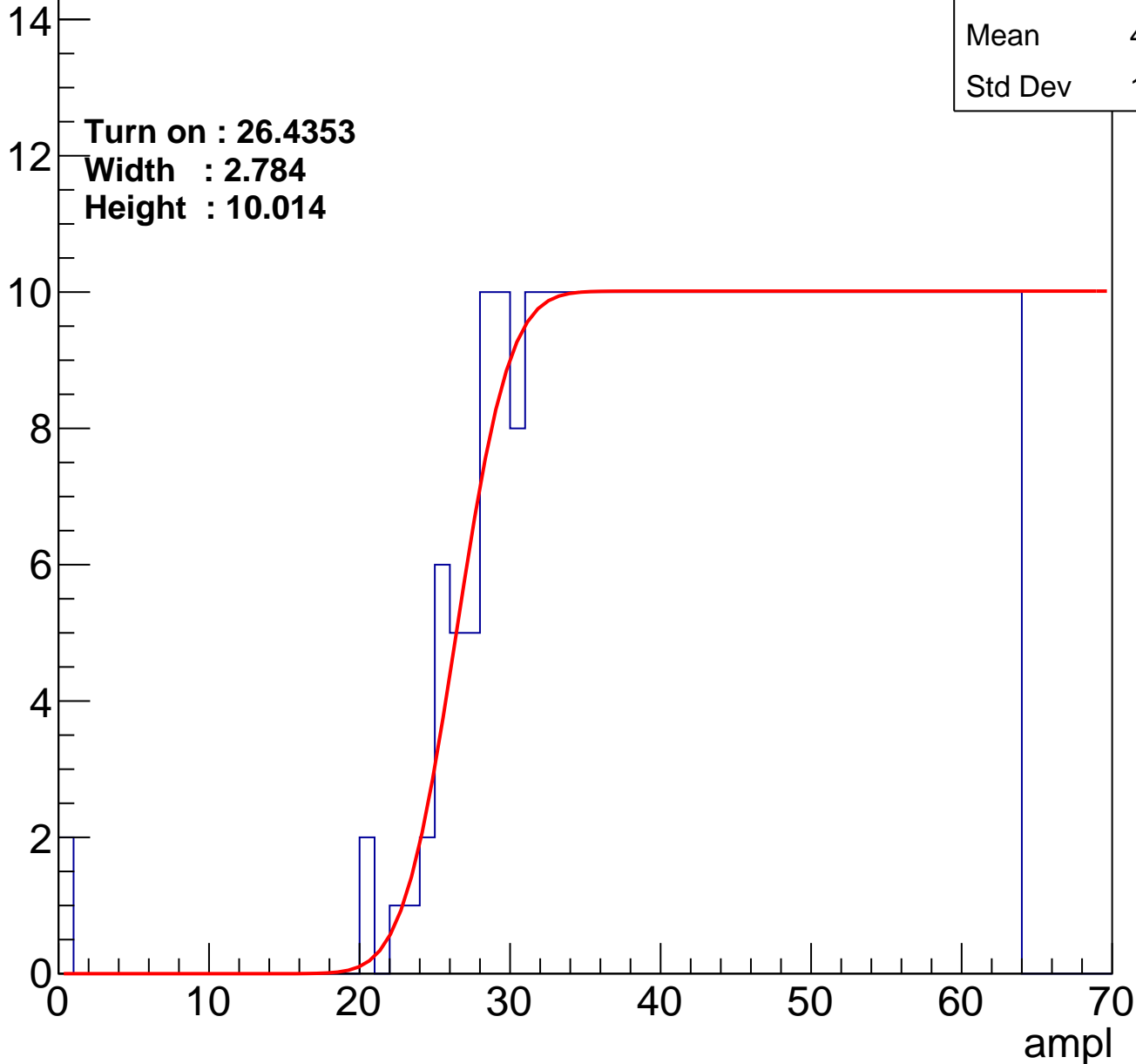
Entry

Entries	382
Mean	44.16
Std Dev	11.58

Turn on : 26.4353

Width : 2.784

Height : 10.014



B0L102S, U1-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.35
Std Dev	12.02

Turn on : 24.5404

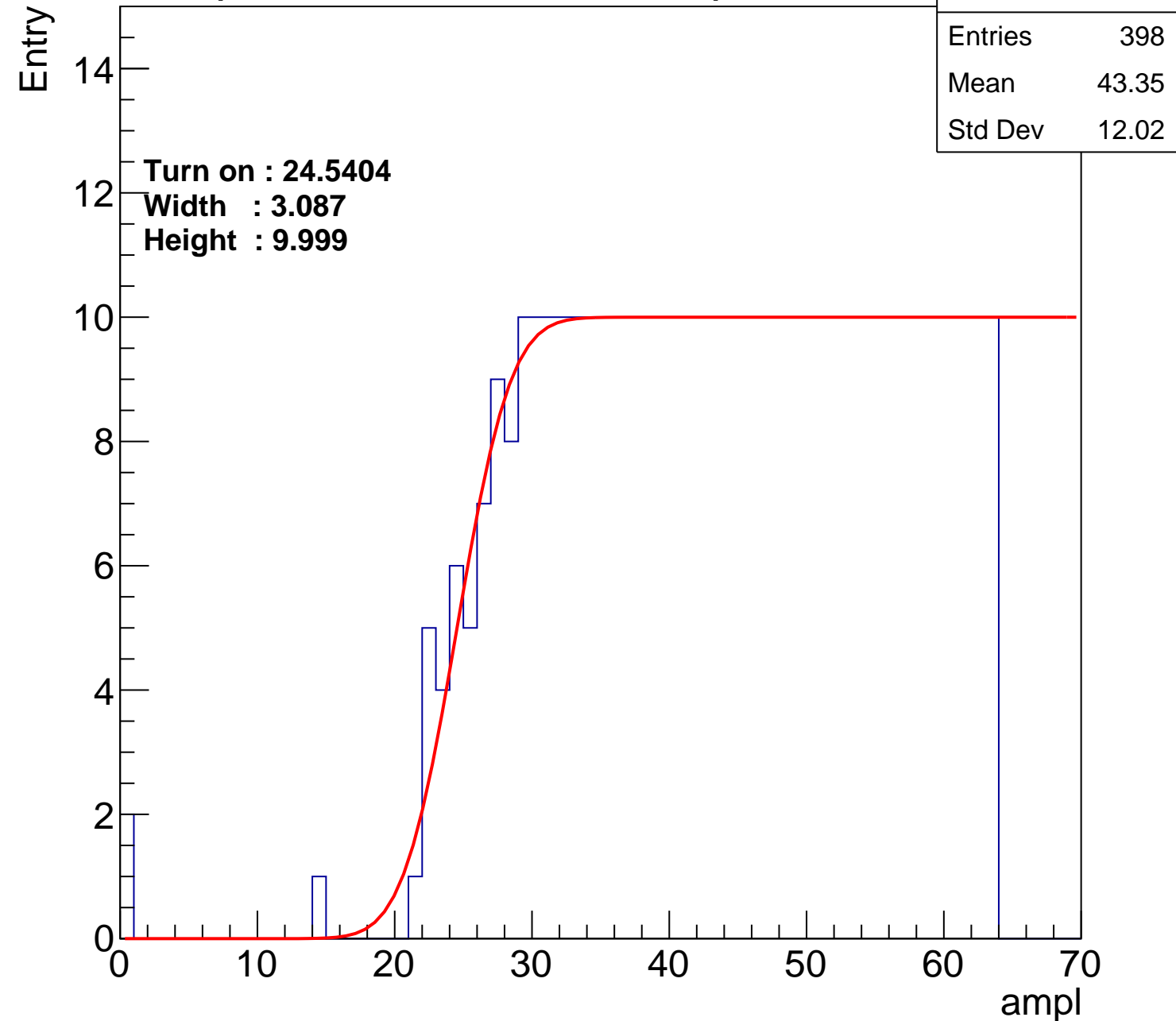
Width : 3.087

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.94
Std Dev	12.1

Turn on : 22.9903

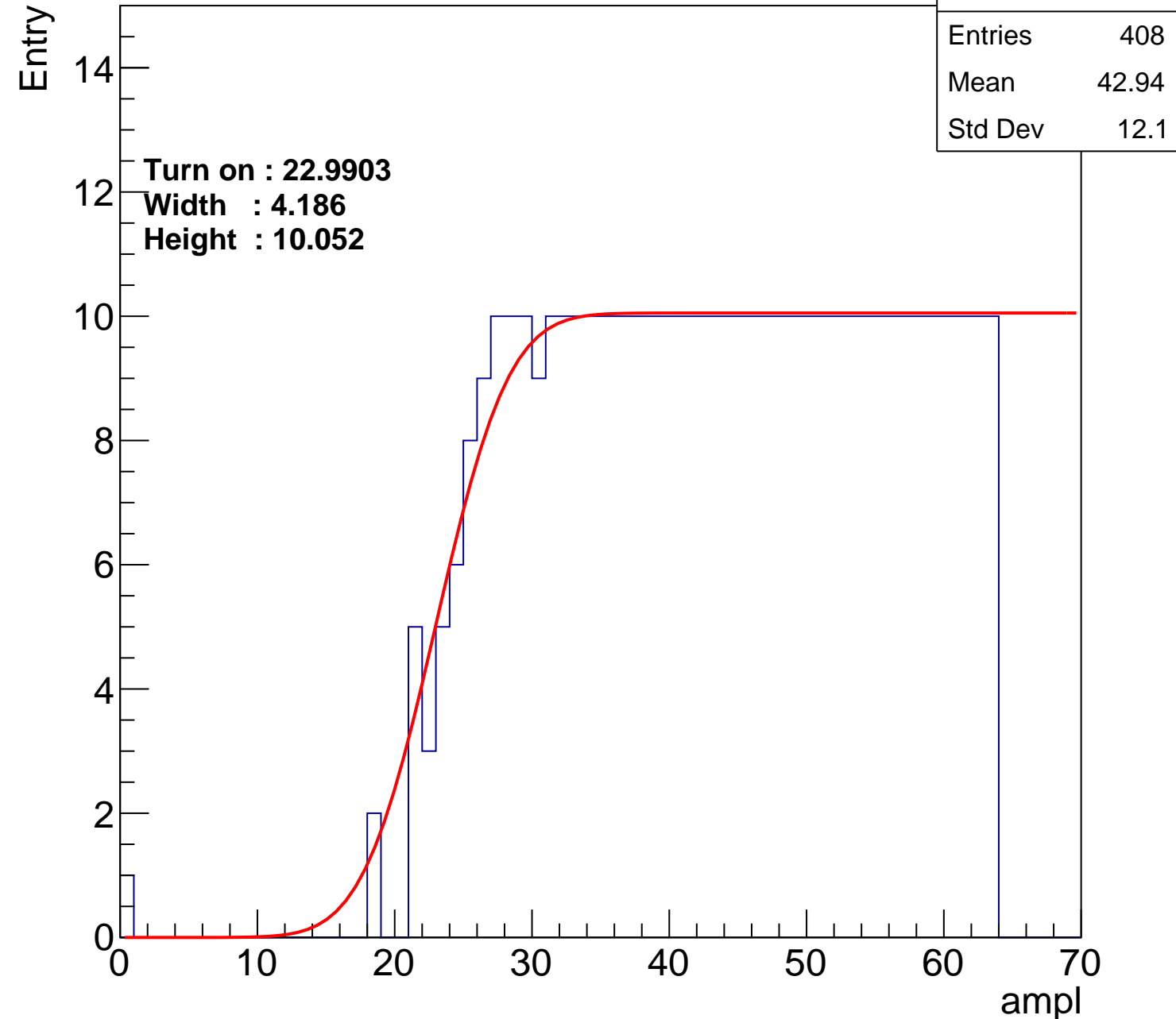
Width : 4.186

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.62
Std Dev	11.37

Turn on : 26.4525

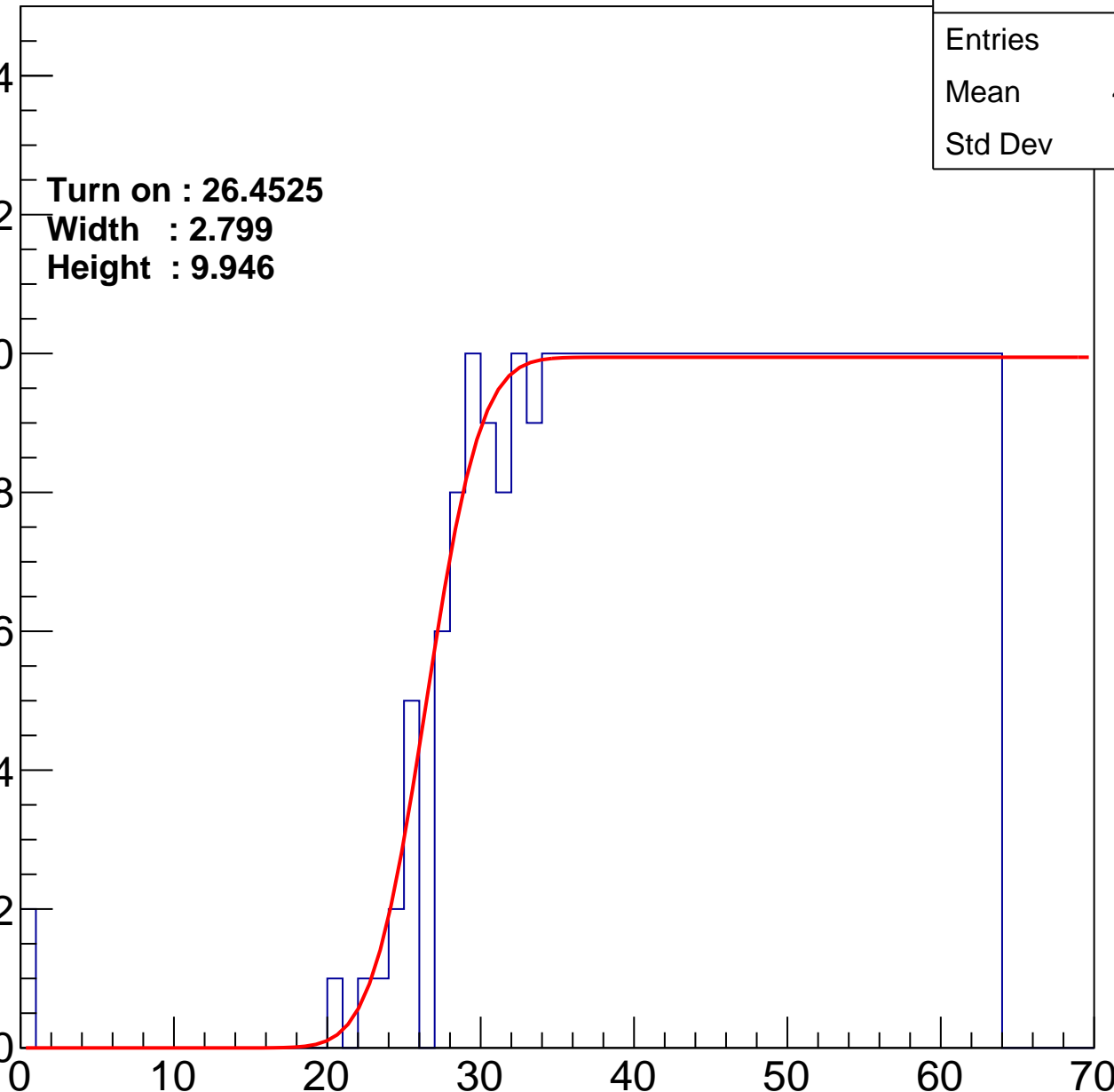
Width : 2.799

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 25.6959

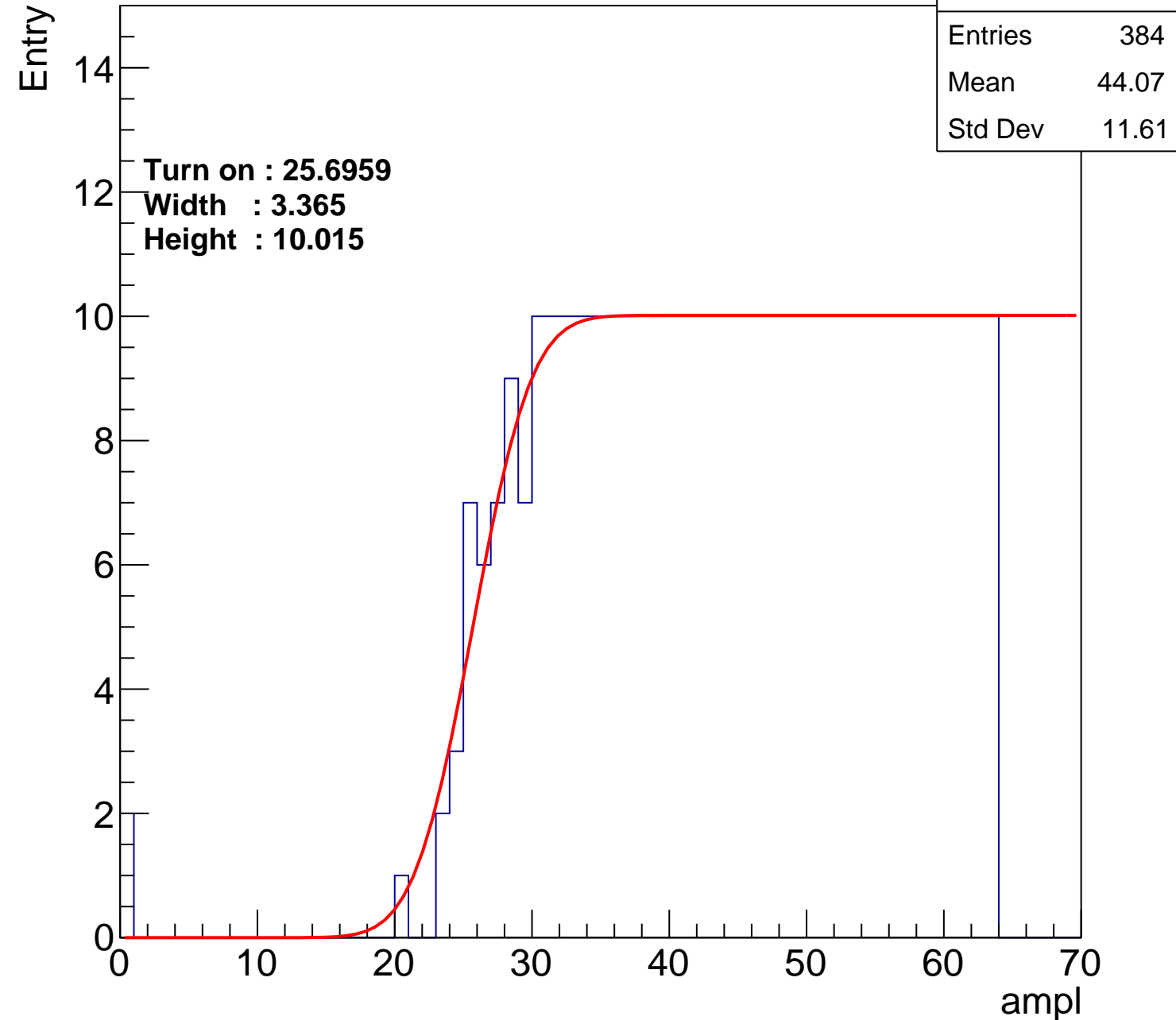
Width : 3.365

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.06
Std Dev	12.76

Turn on : 24.9175

Width : 4.138

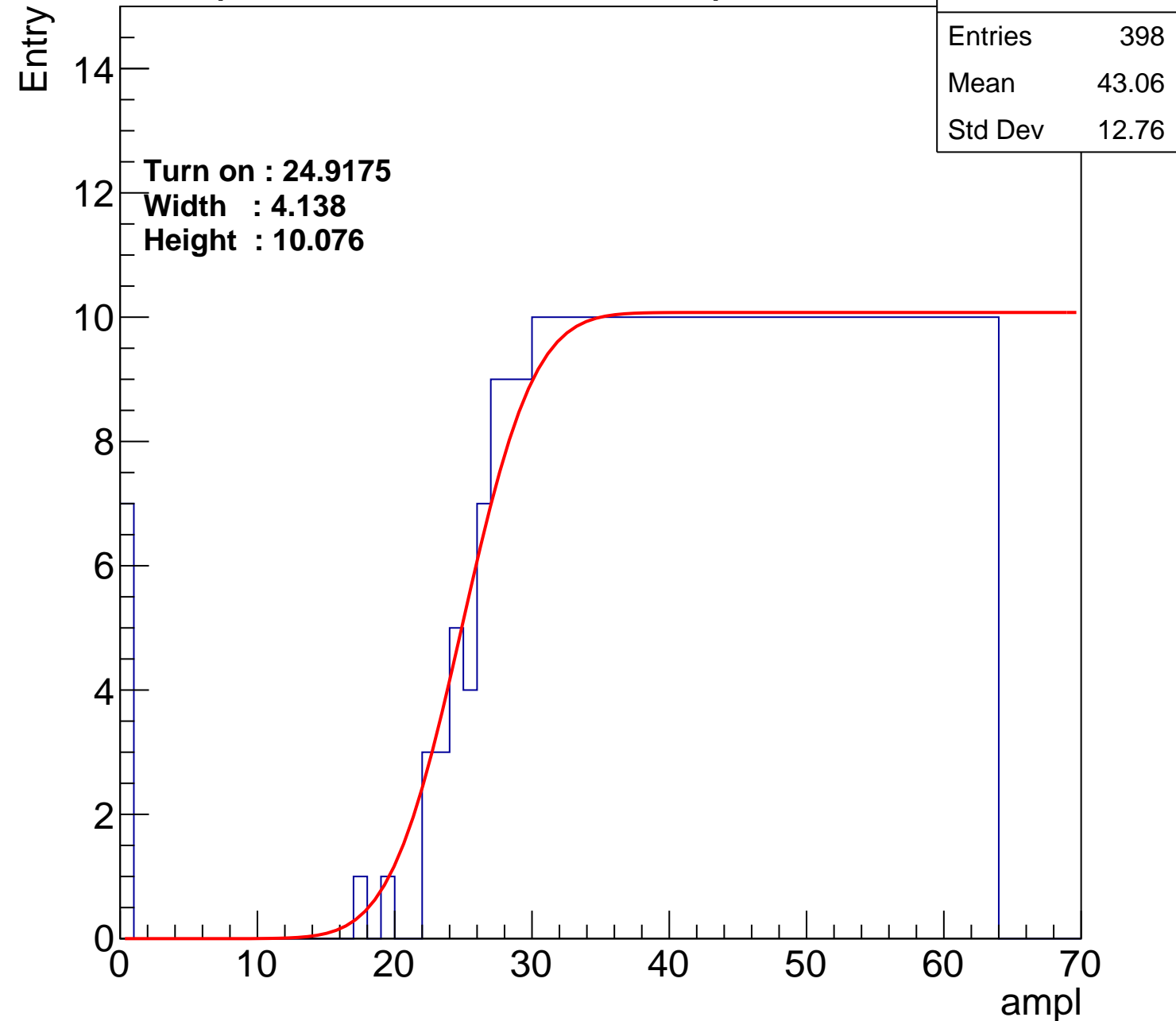
Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U1-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.3
Std Dev	12

Turn on : 23.9344

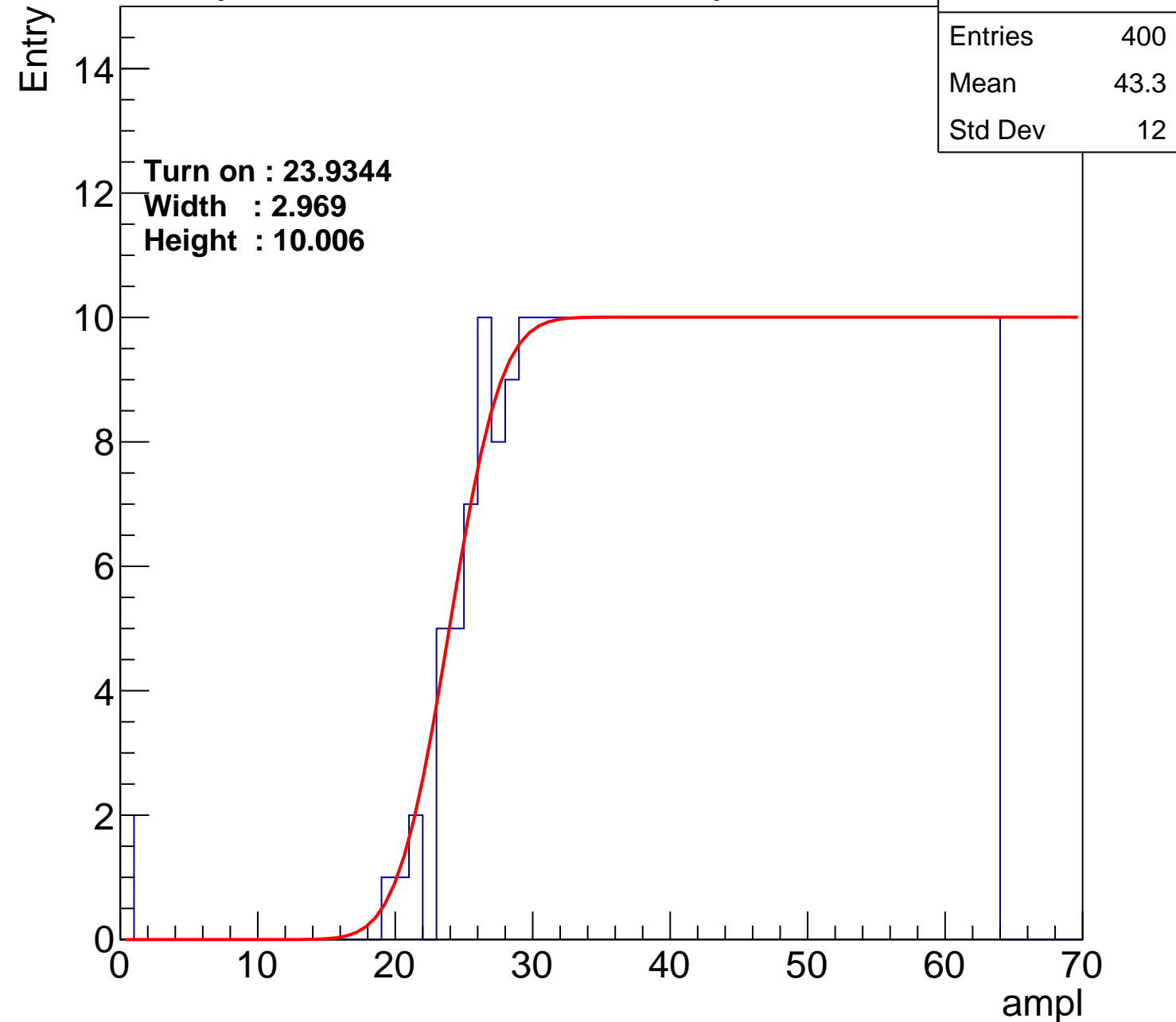
Width : 2.969

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch77

calib_packv5_042523_0143.root, FC#12, port B1

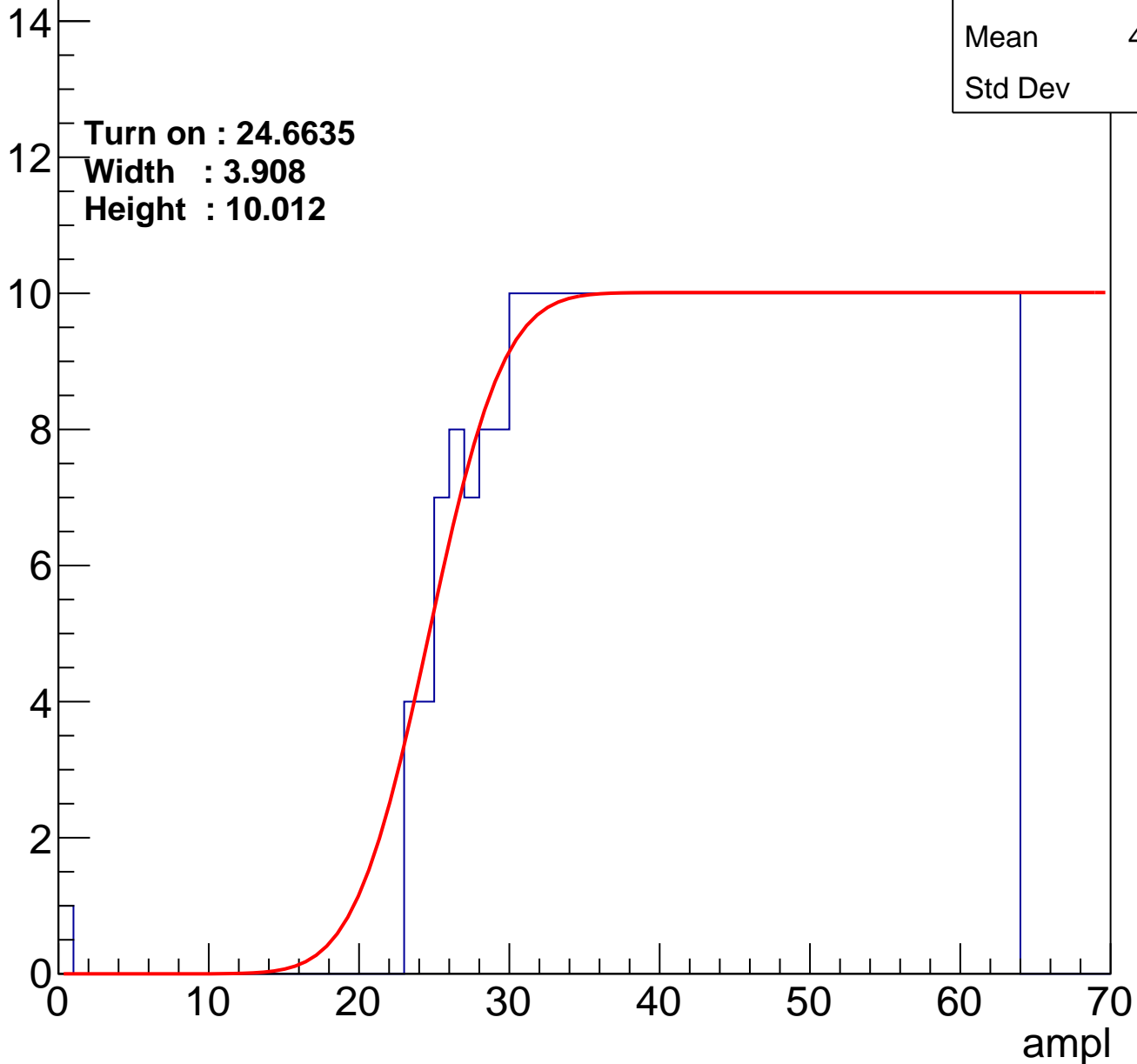
Entry

Entries	387
Mean	43.99
Std Dev	11.5

Turn on : 24.6635

Width : 3.908

Height : 10.012



B0L102S, U1-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.69
Std Dev	12.55

Turn on : 23.6714

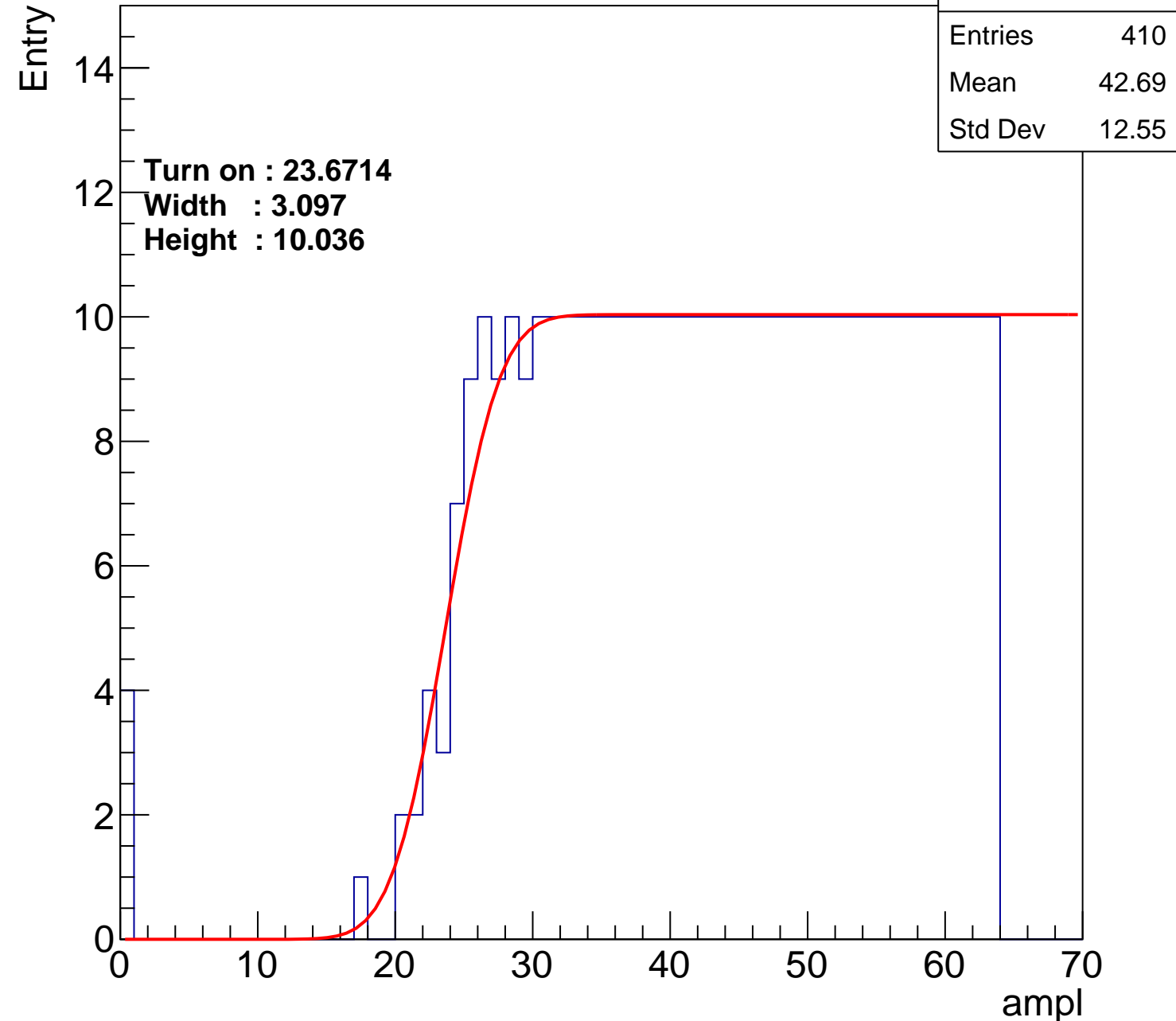
Width : 3.097

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.23
Std Dev	12.16

Turn on : 24.3929

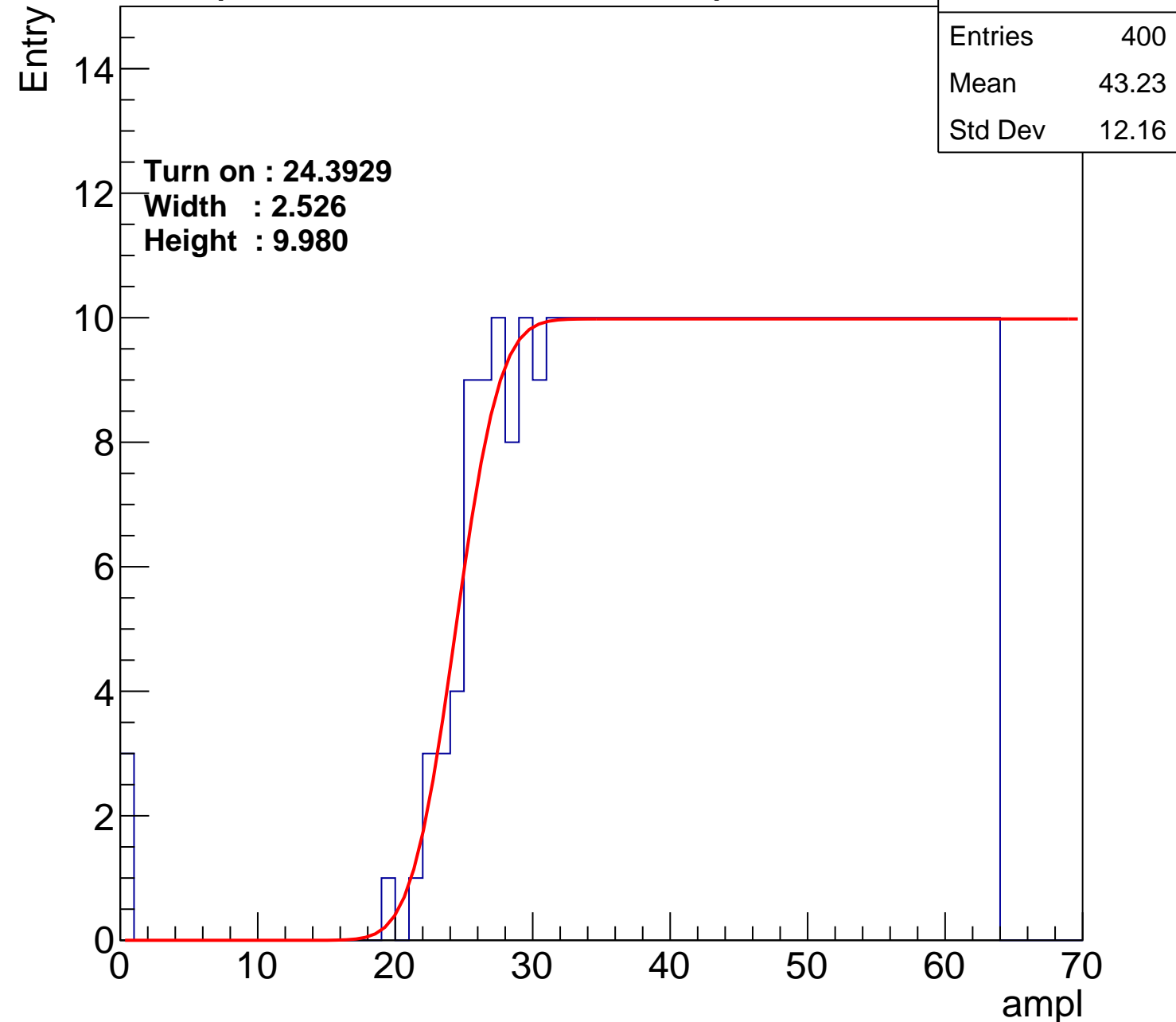
Width : 2.526

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch80

calib_packv5_042523_0143.root, FC#12, port B1

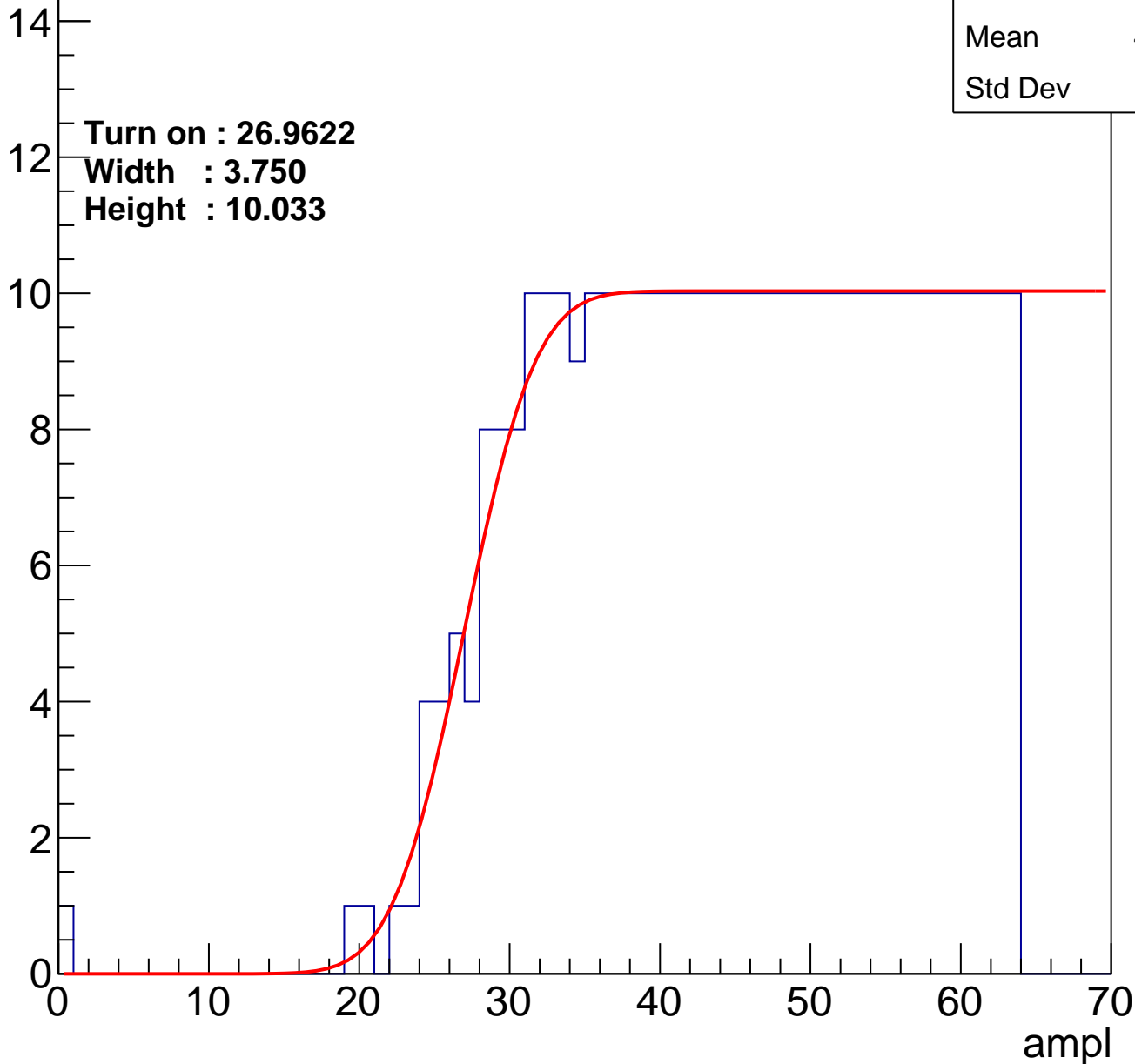
Entry

Entries	375
Mean	44.51
Std Dev	11.31

Turn on : 26.9622

Width : 3.750

Height : 10.033



B0L102S, U1-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.44
Std Dev	11.54

Turn on : 27.7236

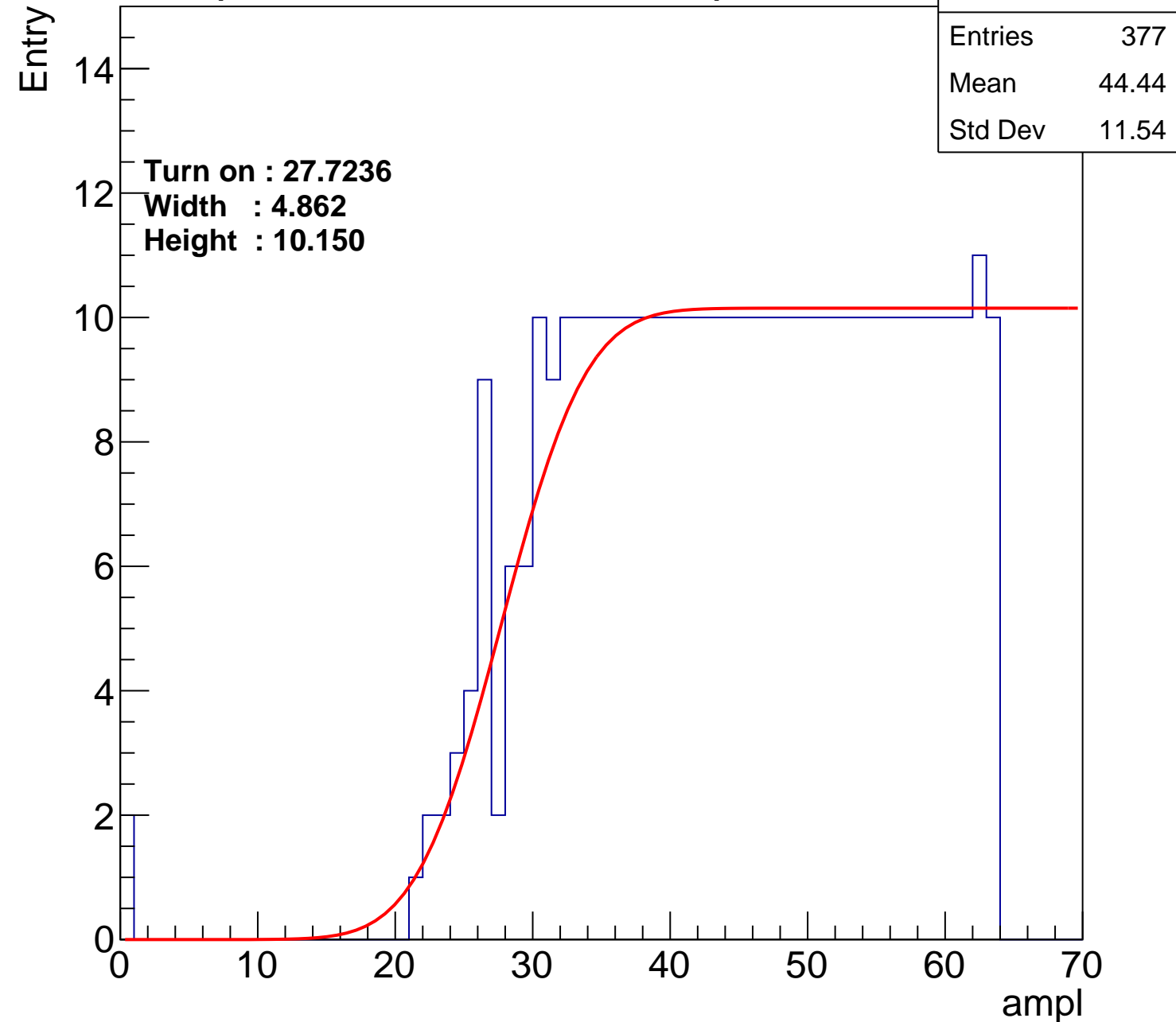
Width : 4.862

Height : 10.150

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch82

calib_packv5_042523_0143.root, FC#12, port B1

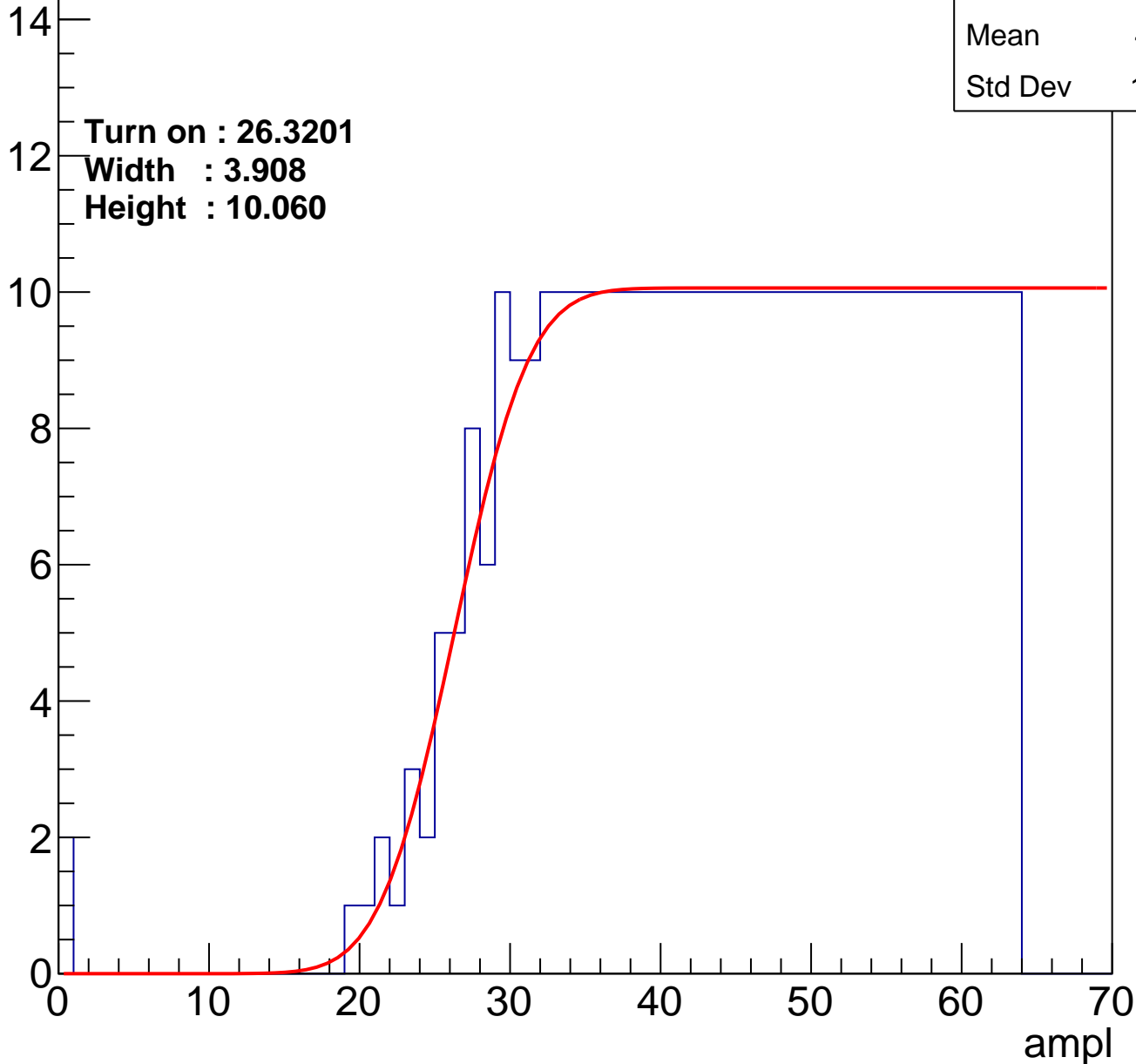
Entry

Entries	384
Mean	44.01
Std Dev	11.72

Turn on : 26.3201

Width : 3.908

Height : 10.060



B0L102S, U1-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 26.4472

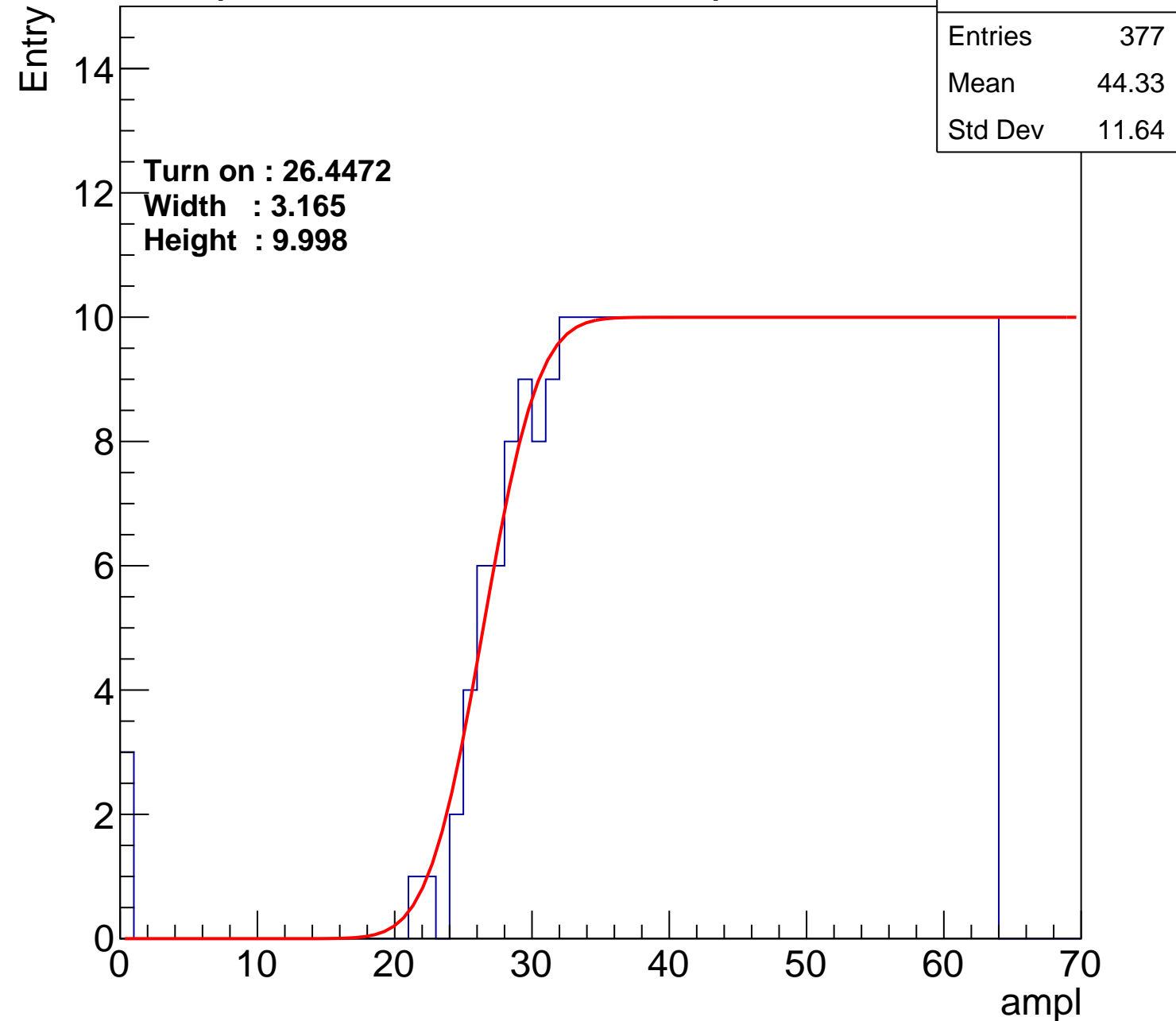
Width : 3.165

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.88
Std Dev	11.58

Turn on : 24.7837

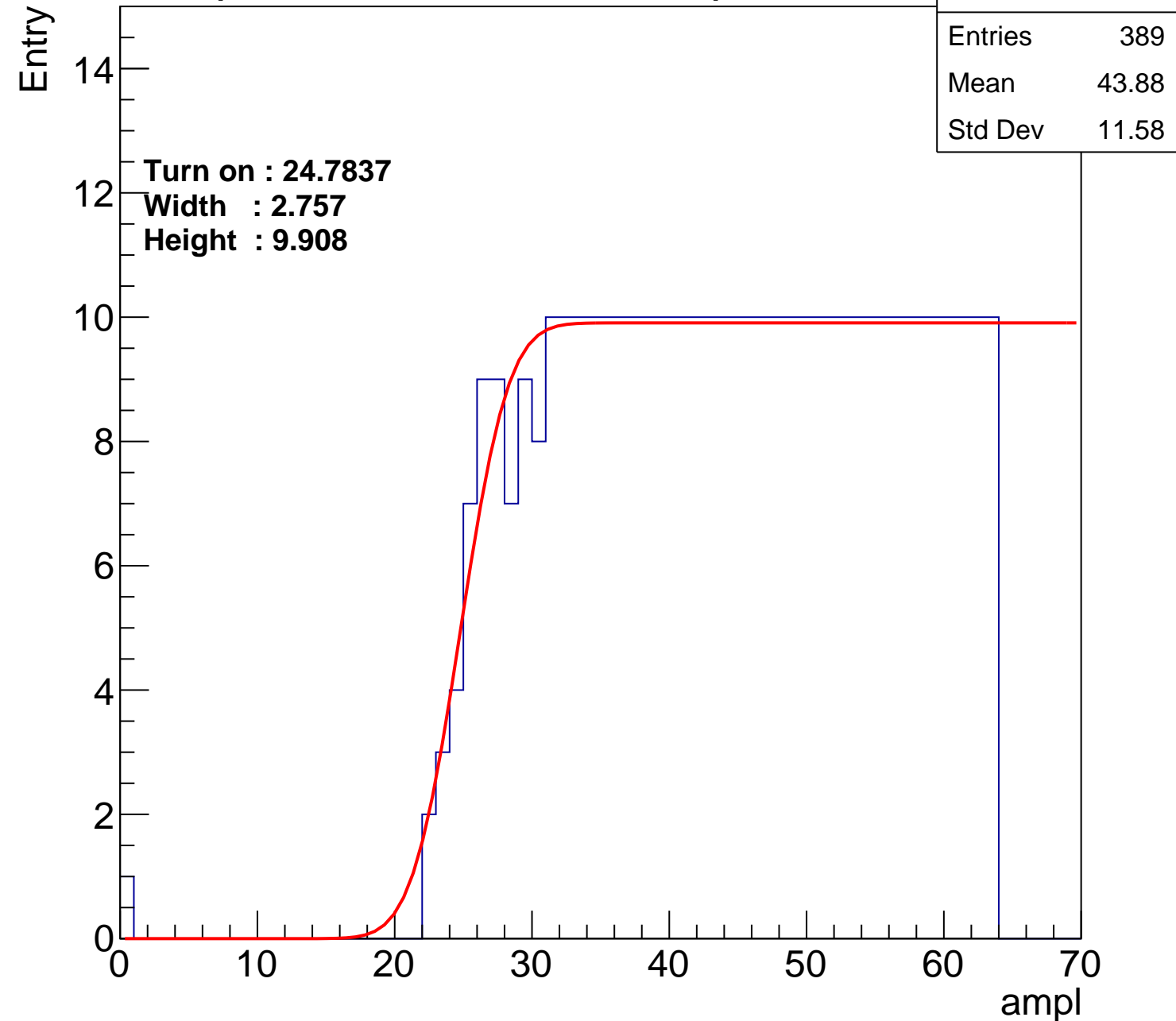
Width : 2.757

Height : 9.908

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.91
Std Dev	12.31

Turn on : 24.0853

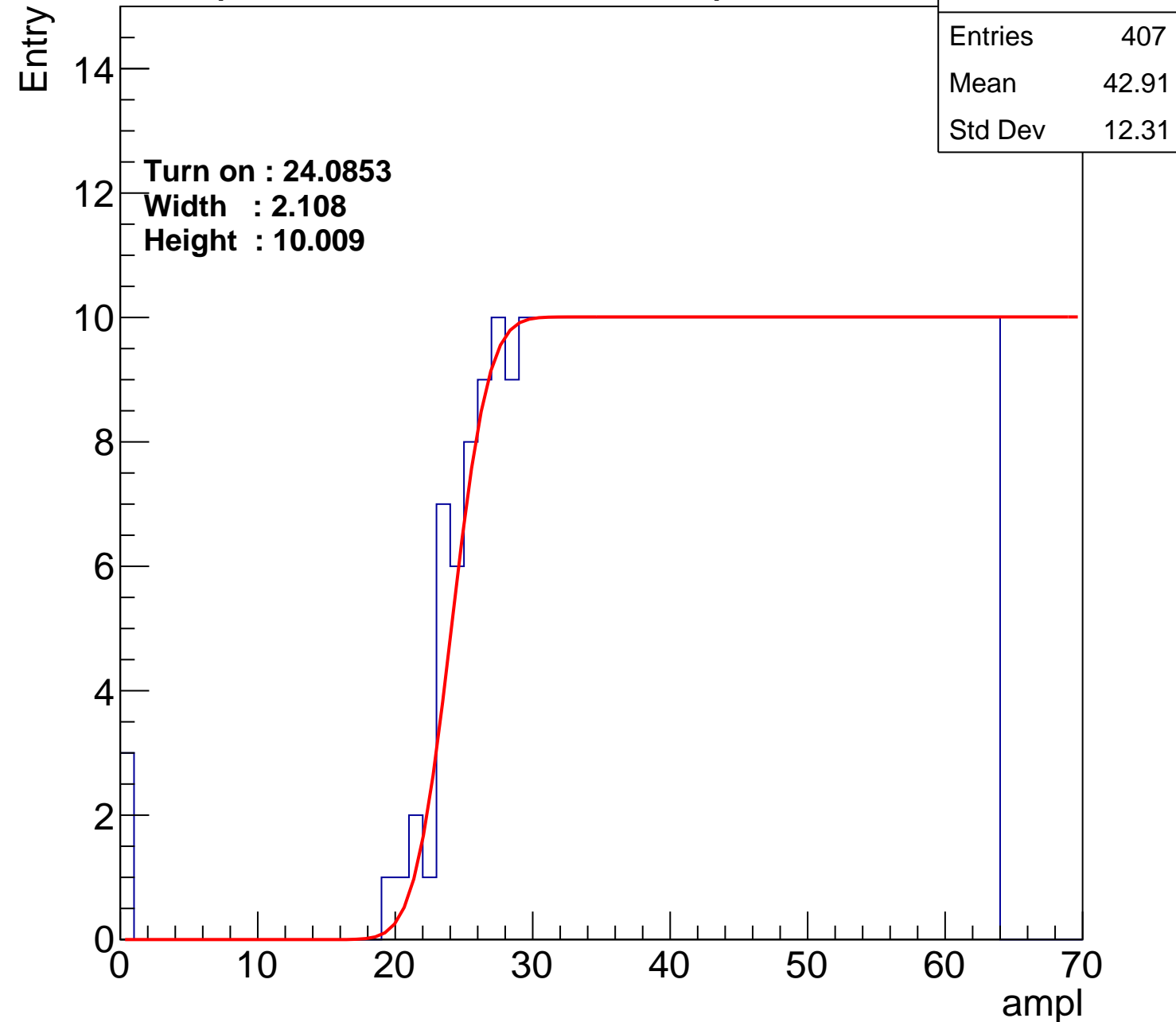
Width : 2.108

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch86

calib_packv5_042523_0143.root, FC#12, port B1

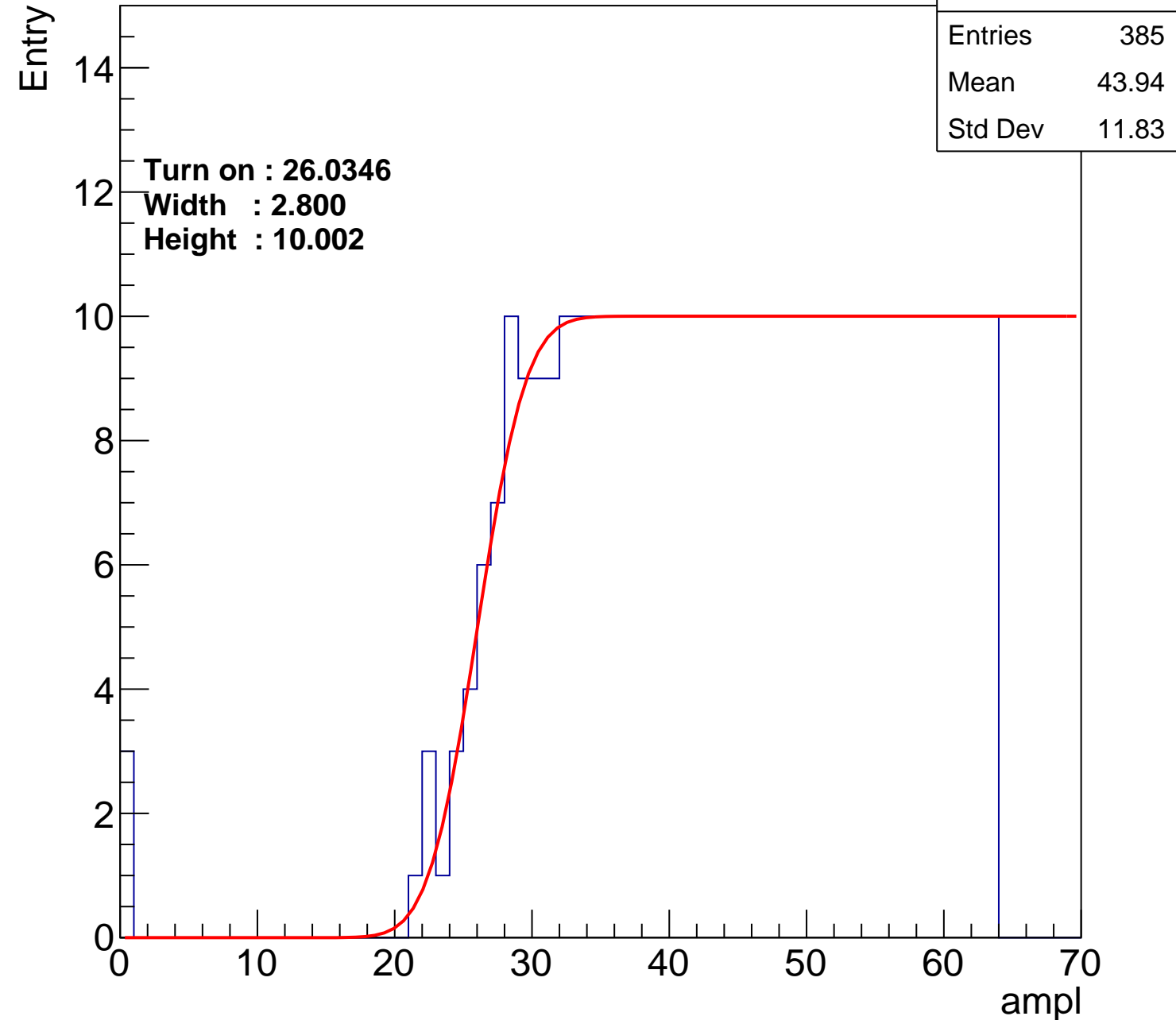
Entry

14
12
10
8
6
4
2
0

Turn on : 26.0346
Width : 2.800
Height : 10.002

Entries	385
Mean	43.94
Std Dev	11.83

ampl



B0L102S, U1-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.79
Std Dev	11.91

Turn on : 25.3731

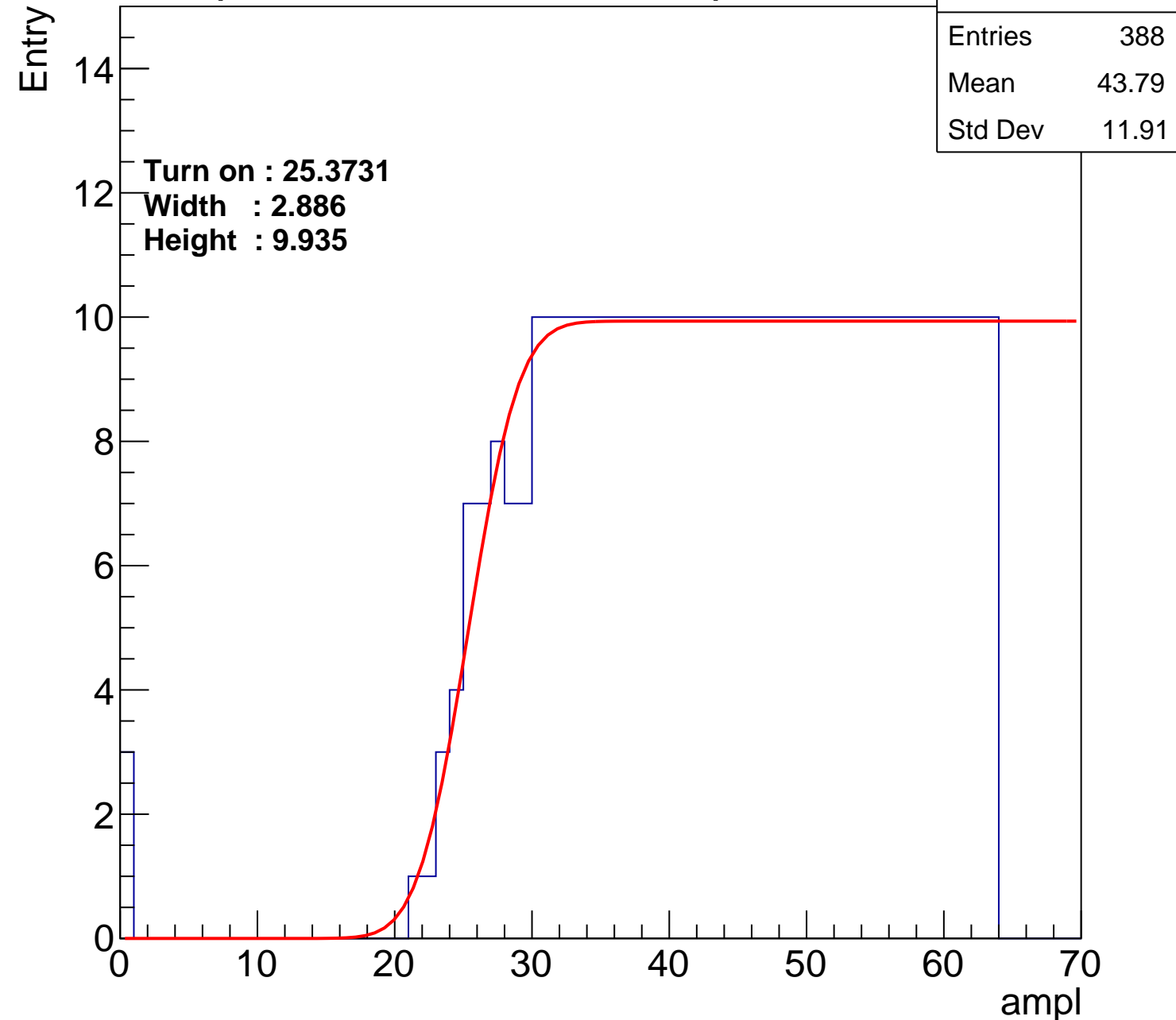
Width : 2.886

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.84
Std Dev	11.77

Turn on : 25.4447

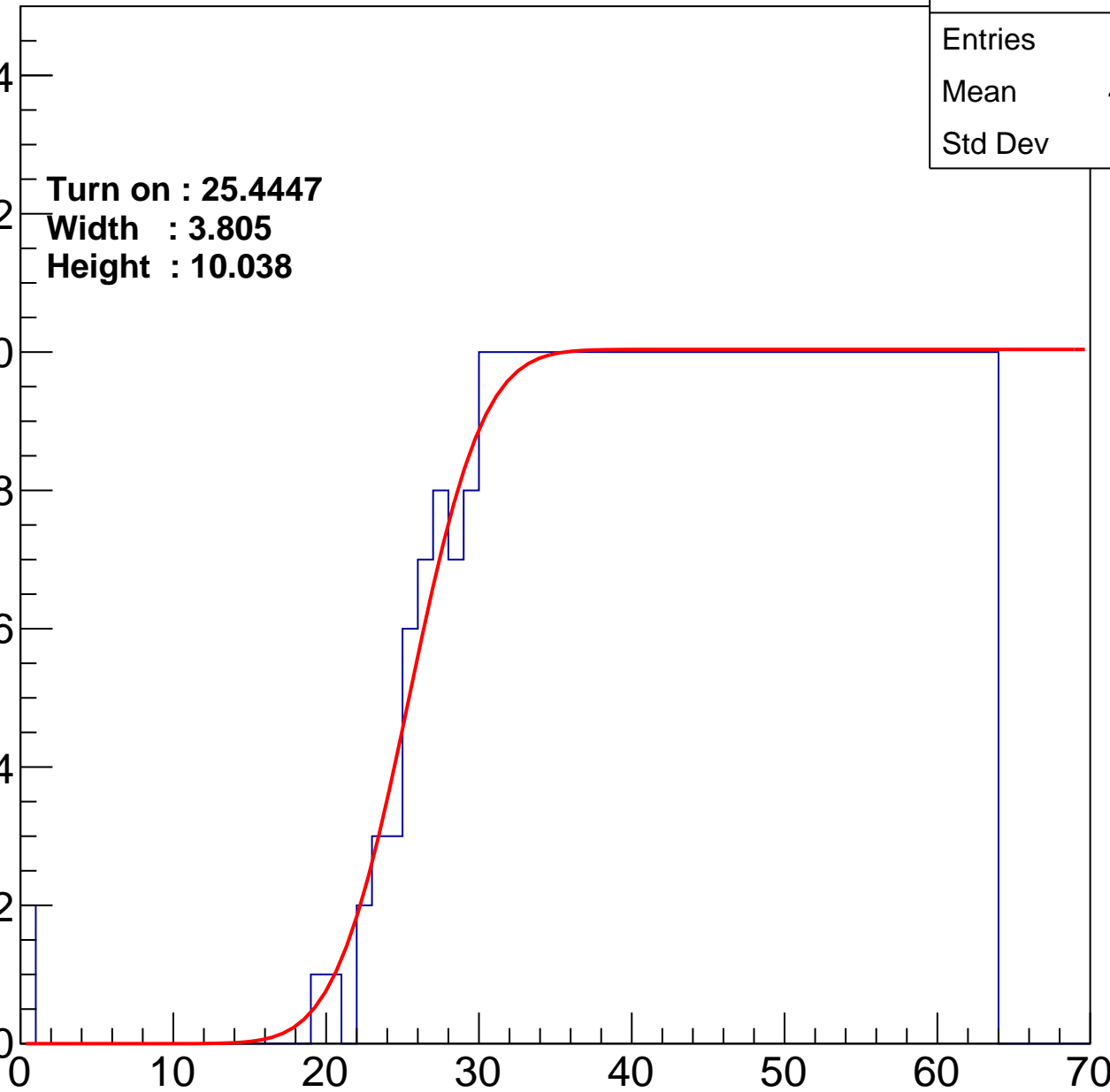
Width : 3.805

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.65
Std Dev	12.54

Turn on : 27.1311

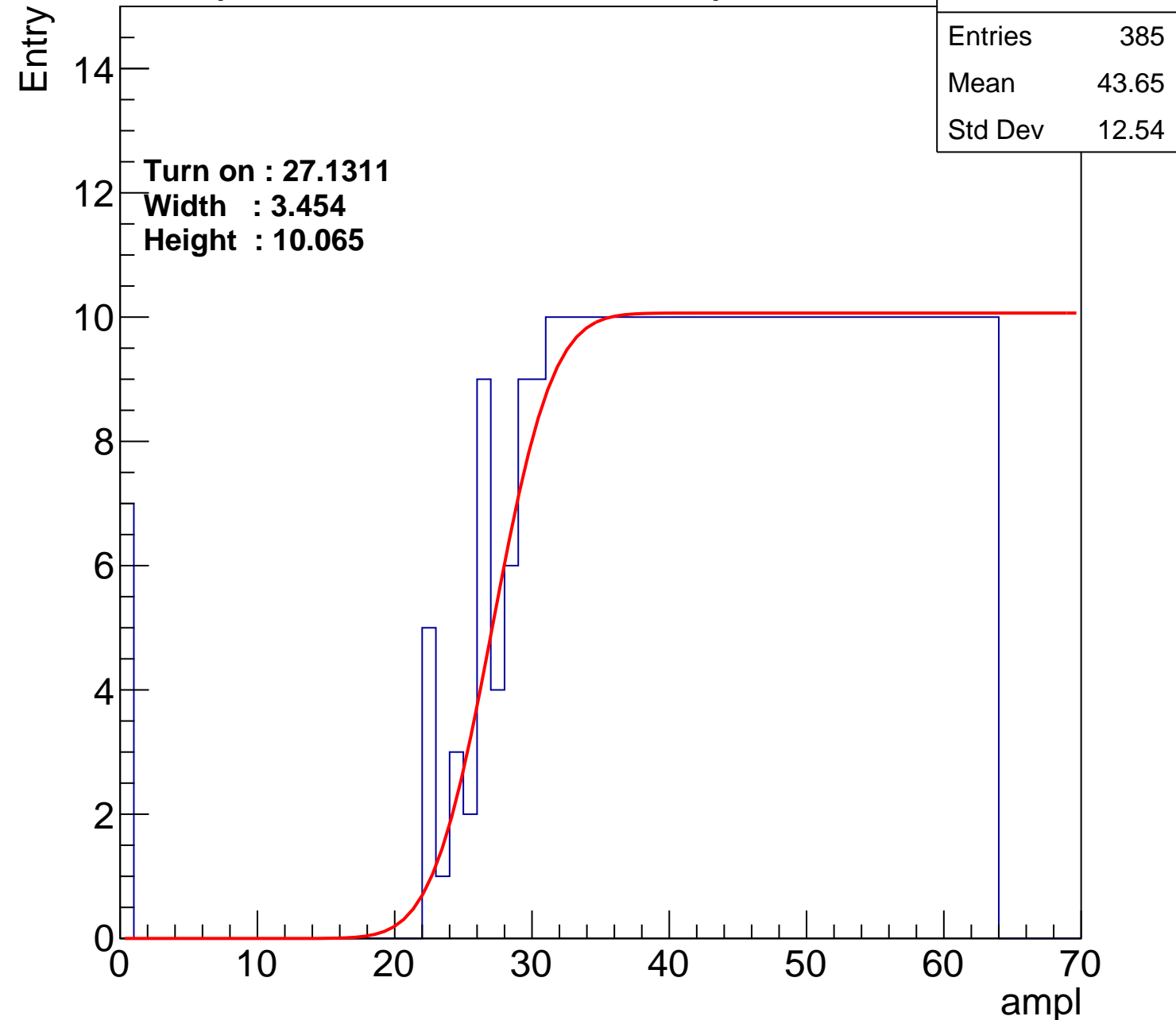
Width : 3.454

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.07
Std Dev	11.84

Turn on : 27.2047

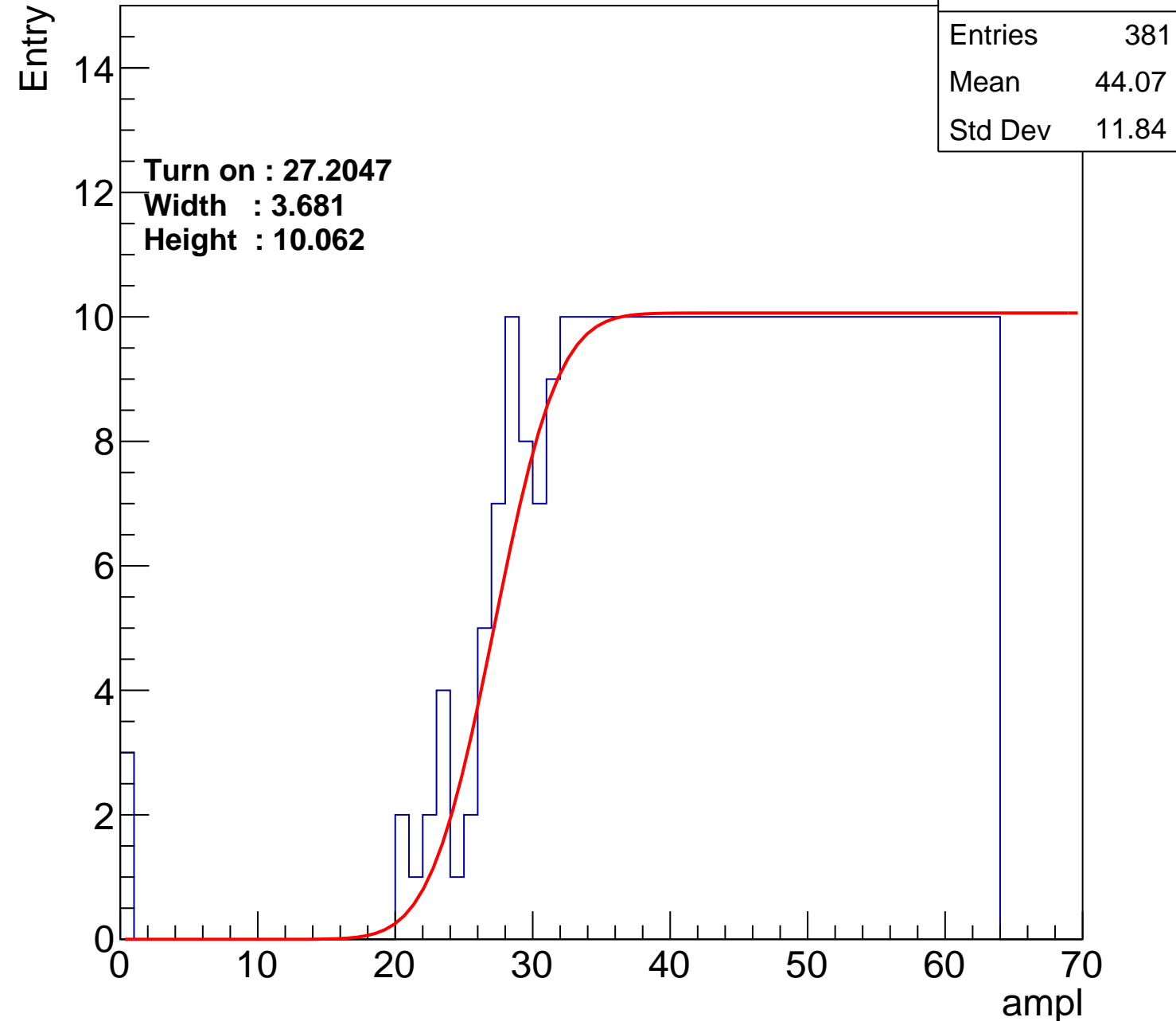
Width : 3.681

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	416
Mean	42.42
Std Dev	12.61

Turn on : 22.4657

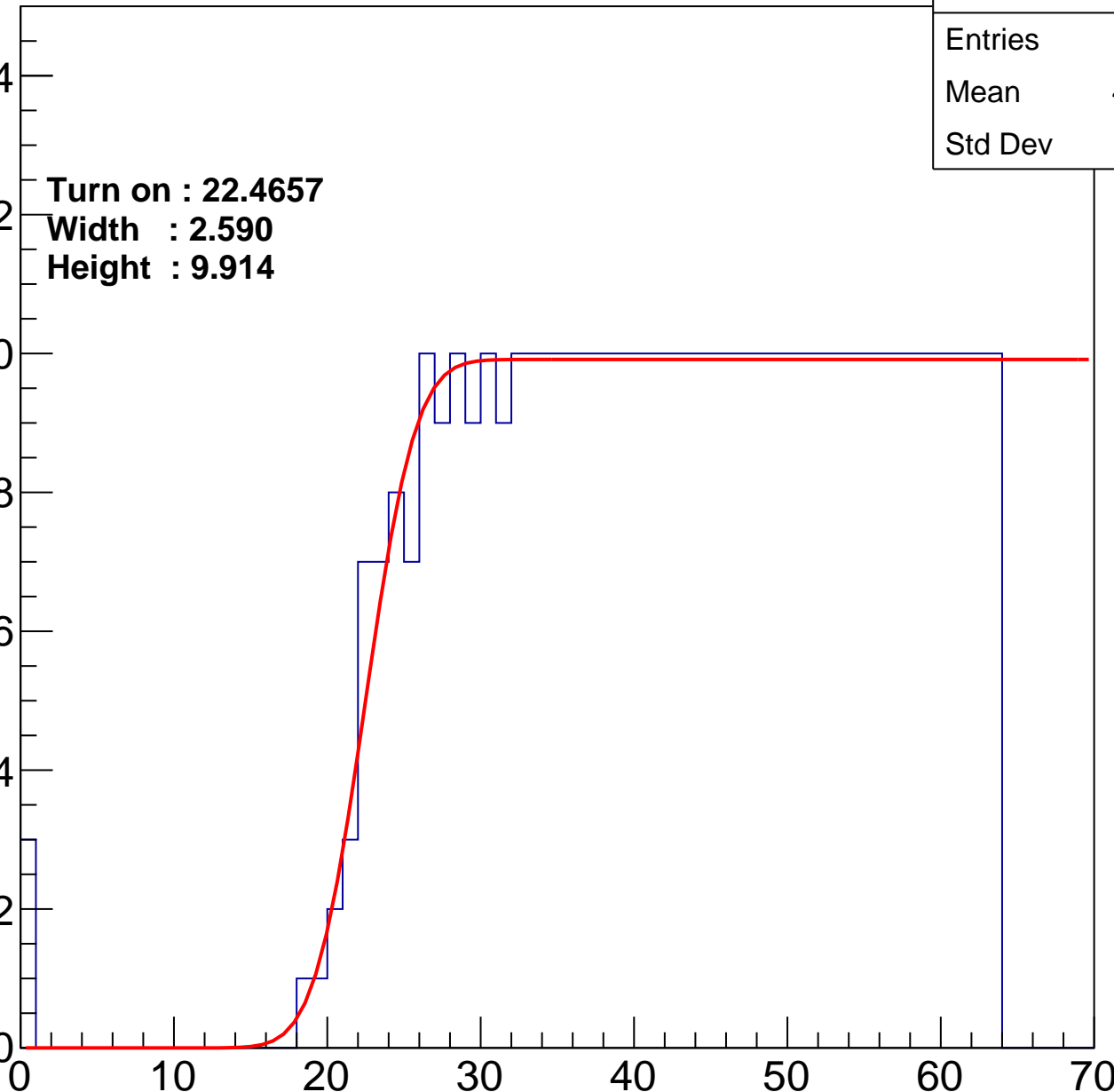
Width : 2.590

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.63
Std Dev	12.12

Turn on : 25.7215

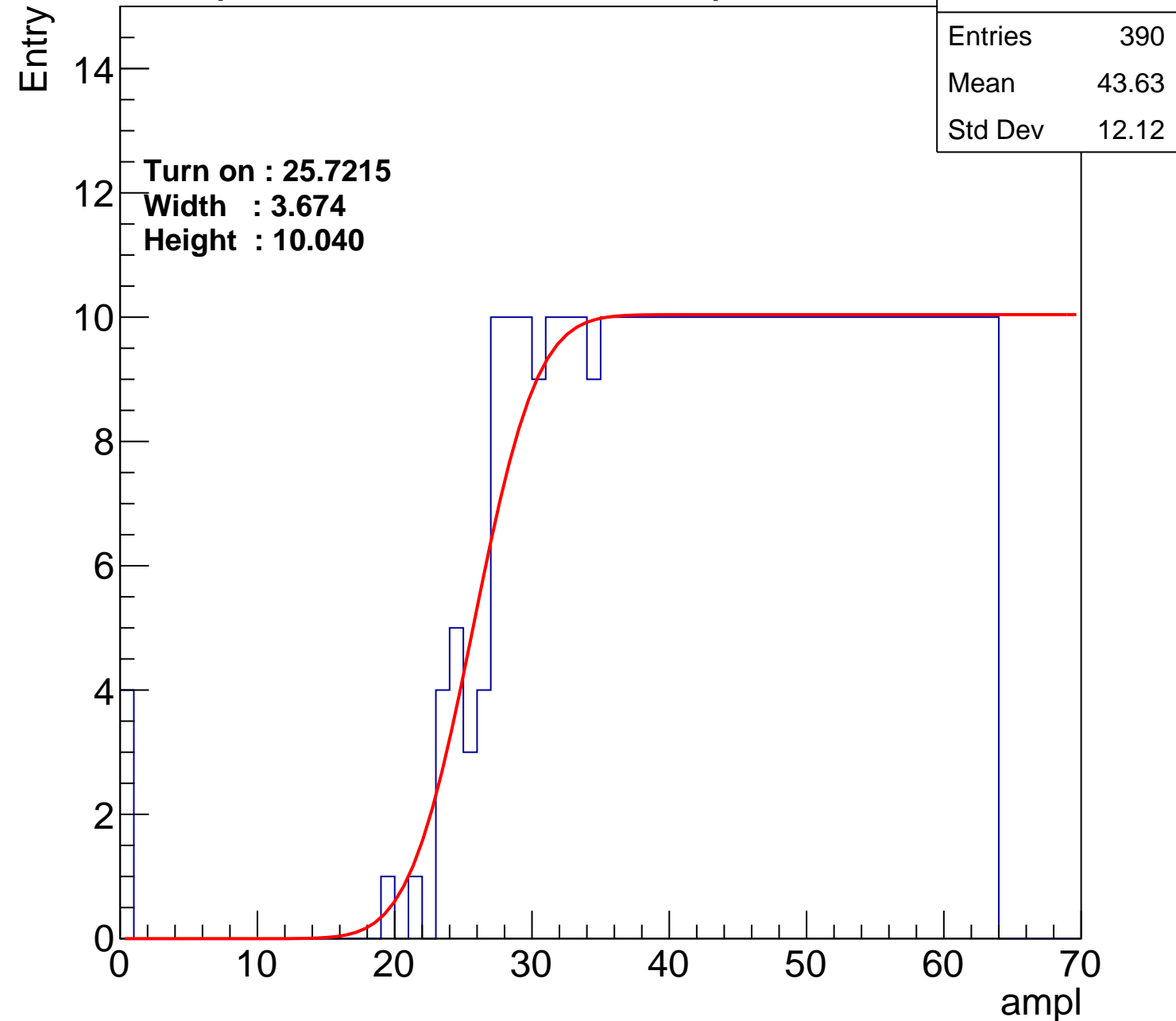
Width : 3.674

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.02
Std Dev	11.94

Turn on : 26.5506

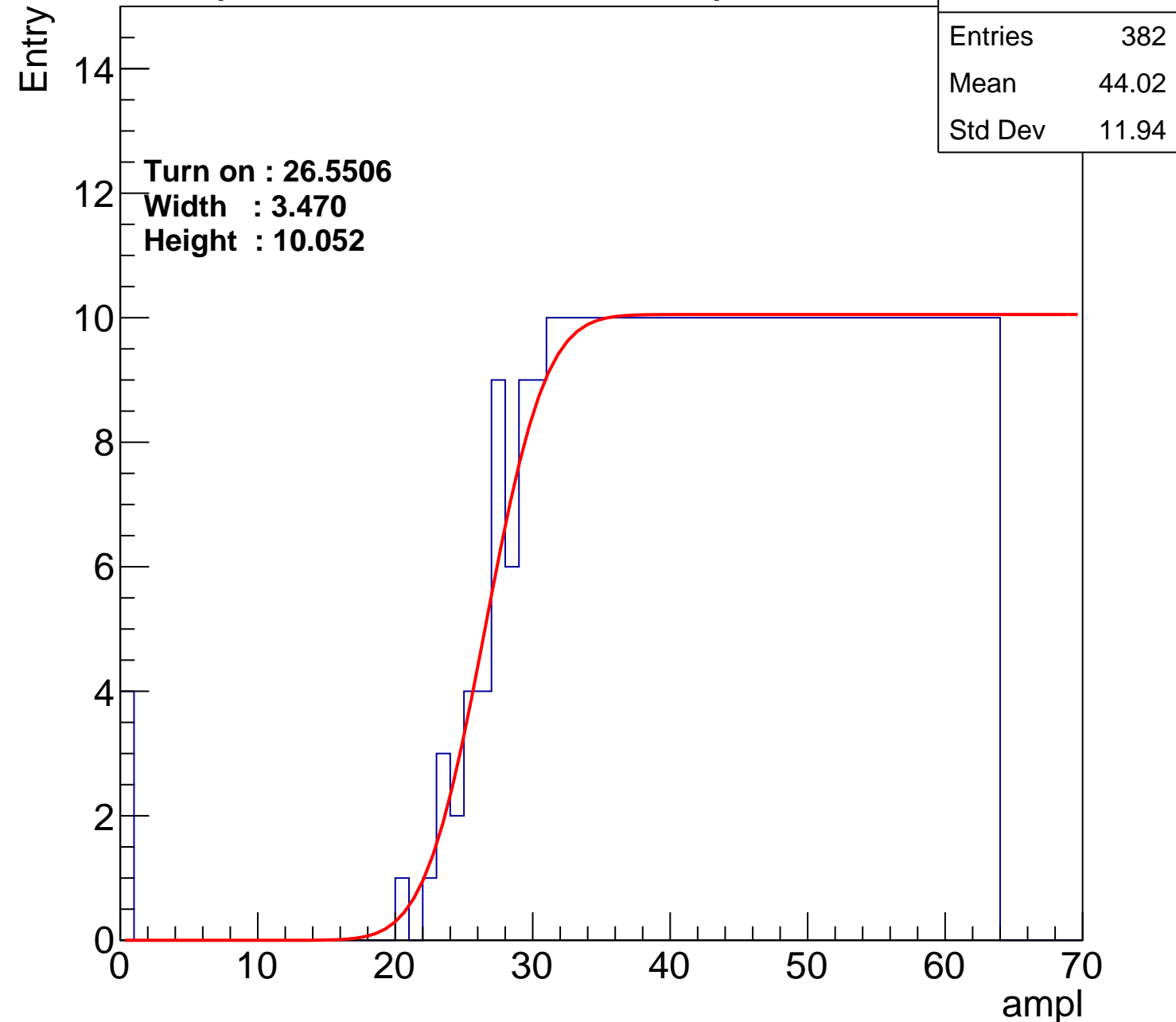
Width : 3.470

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	422
Mean	42.12
Std Dev	12.79

Turn on : 22.7376

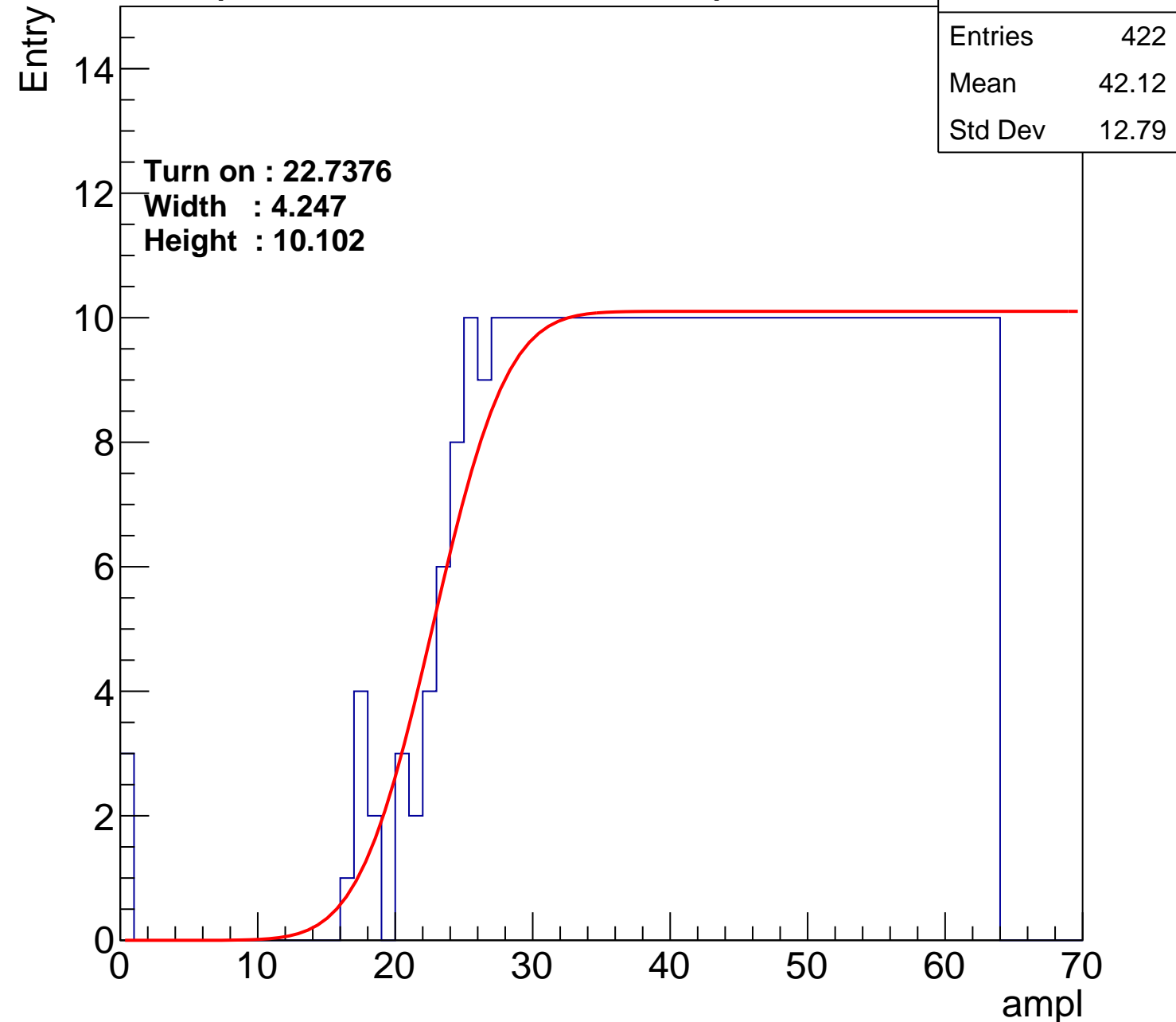
Width : 4.247

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.49
Std Dev	11.22

Turn on : 26.6700

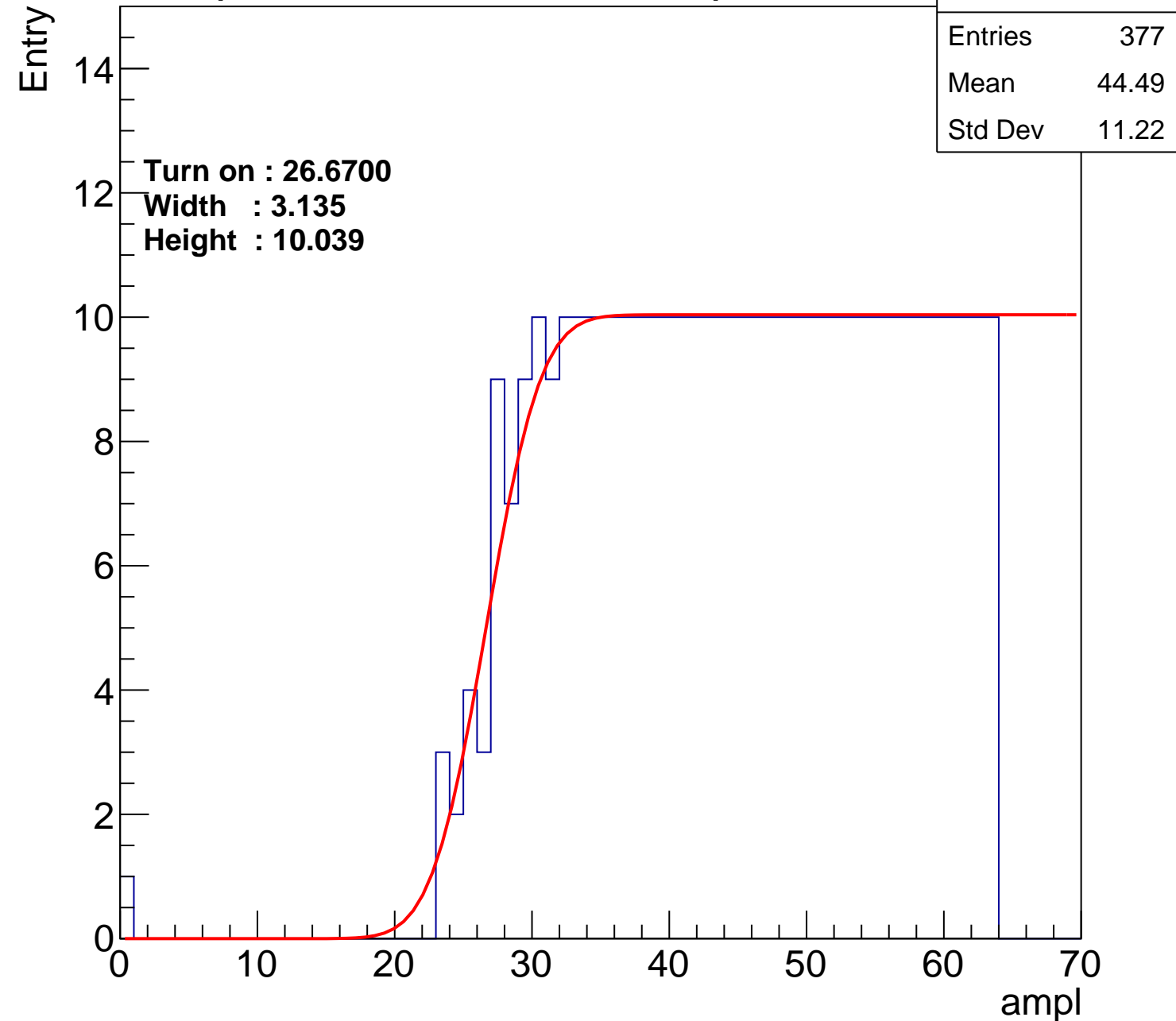
Width : 3.135

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.5
Std Dev	12

Turn on : 24.6807

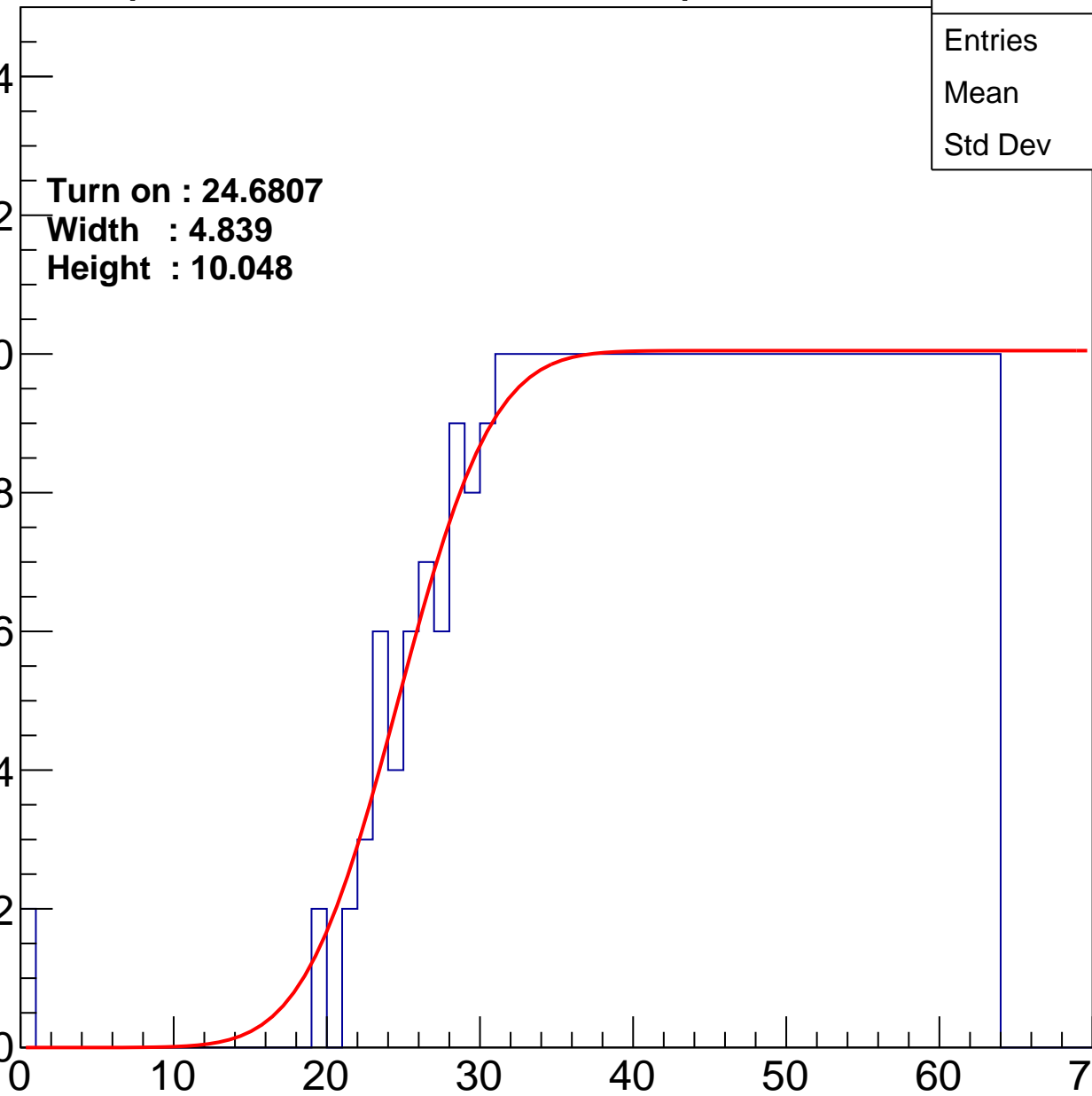
Width : 4.839

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.71
Std Dev	11.34

Turn on : 27.6822

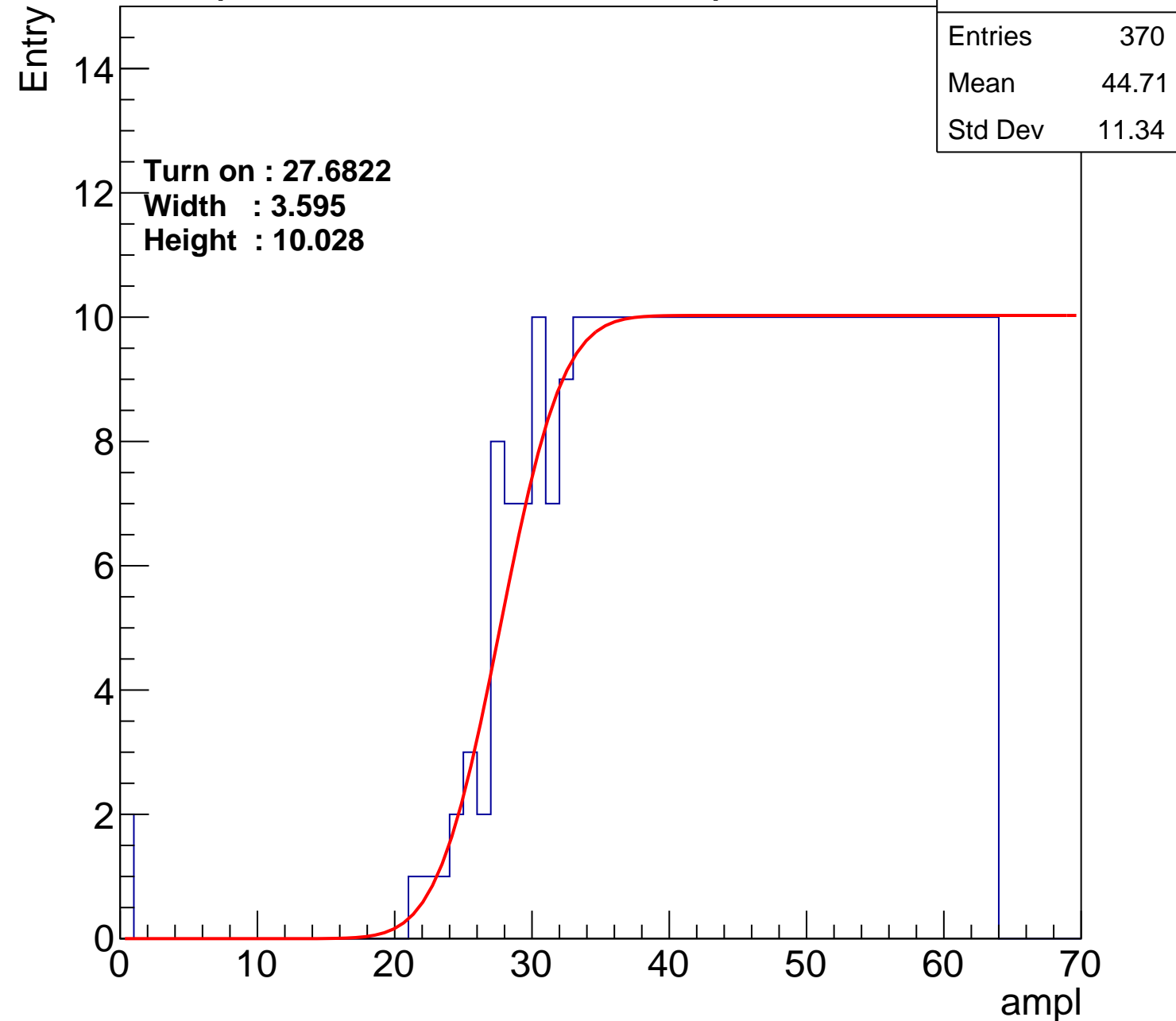
Width : 3.595

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	418
Mean	42.31
Std Dev	12.69

Turn on : 23.0427

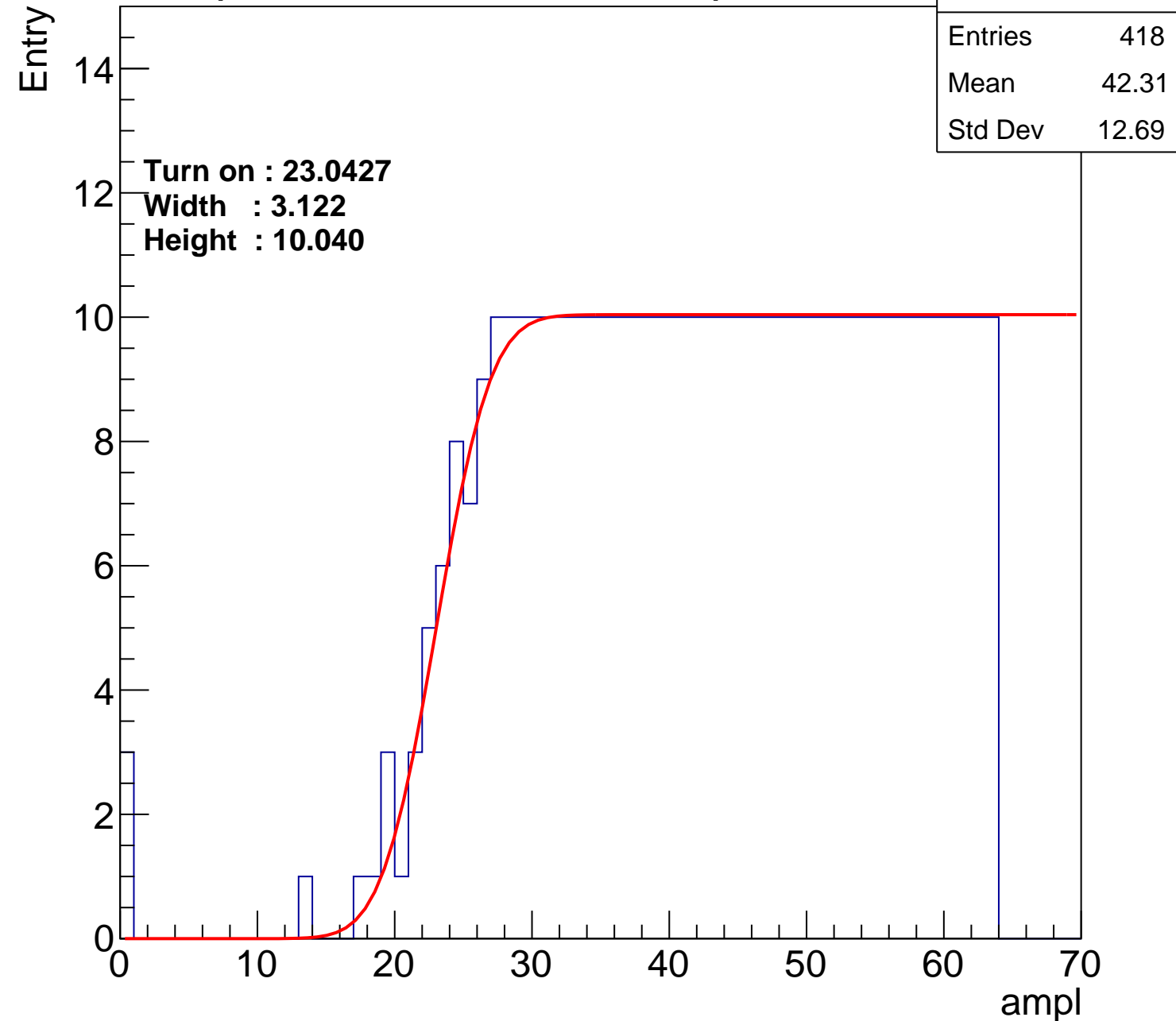
Width : 3.122

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch99

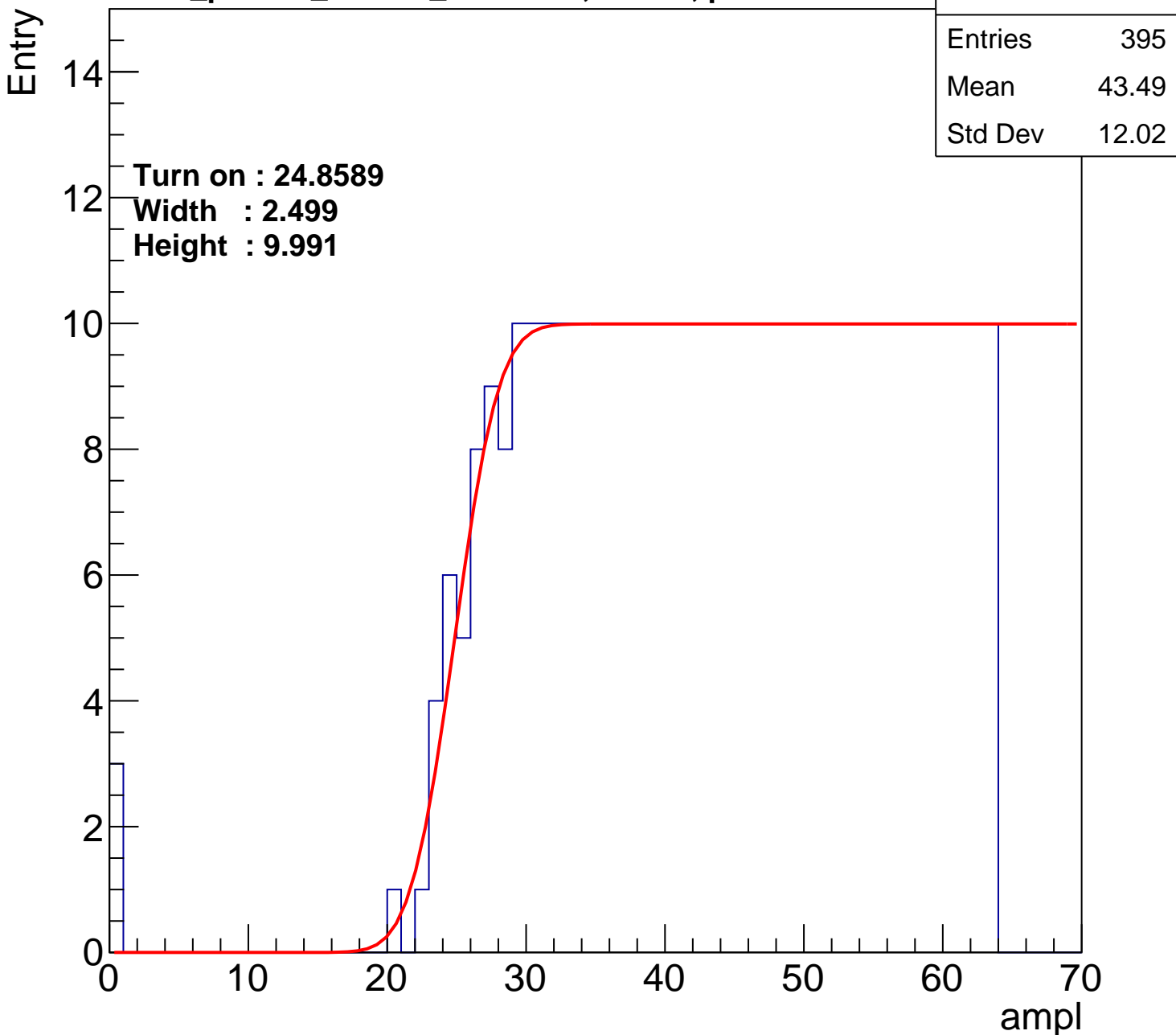
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 24.8589

Width : 2.499

Height : 9.991

Entries	395
Mean	43.49
Std Dev	12.02



B0L102S, U1-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.9
Std Dev	11.72

Turn on : 26.1589

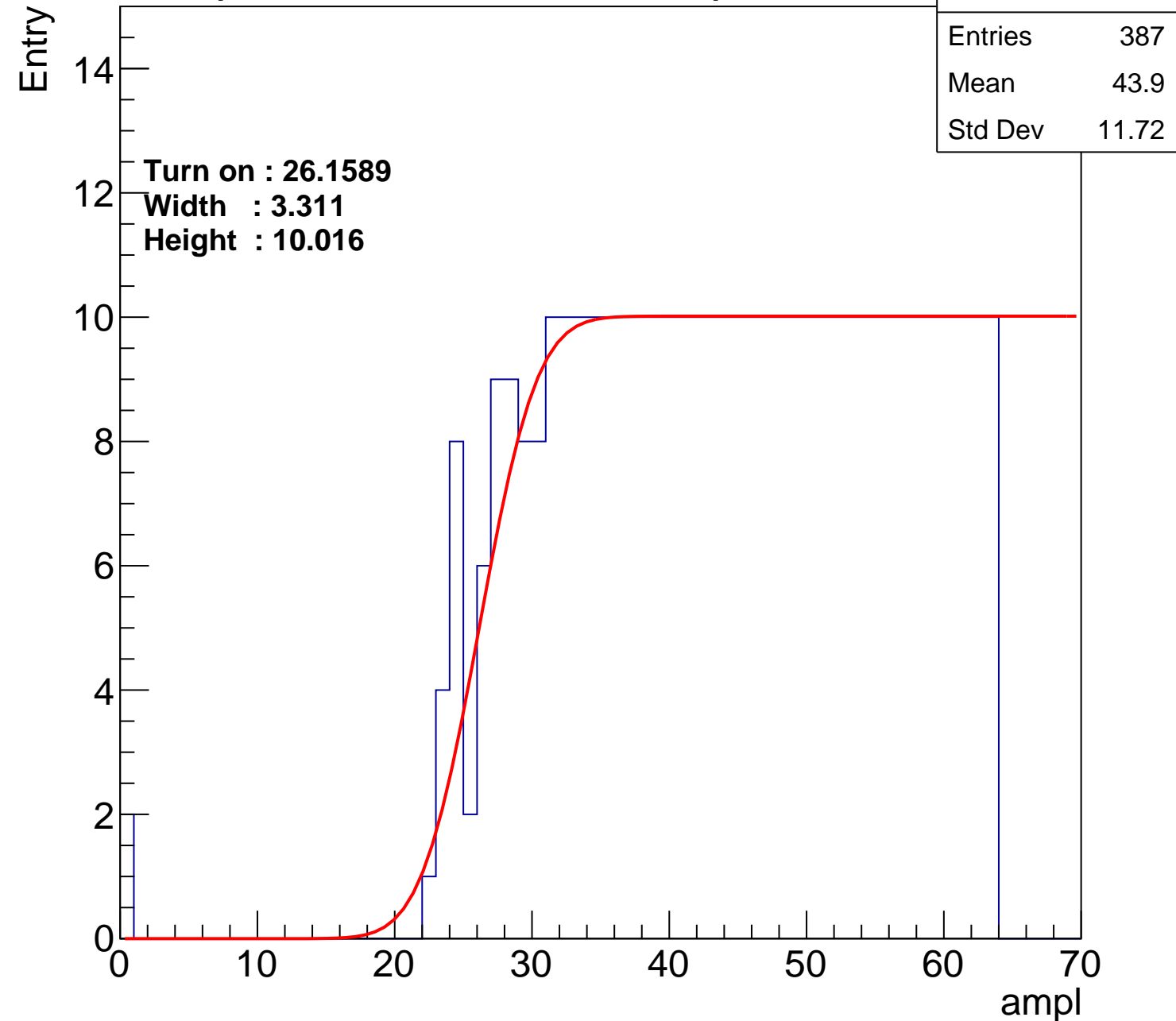
Width : 3.311

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch101

calib_packv5_042523_0143.root, FC#12, port B1

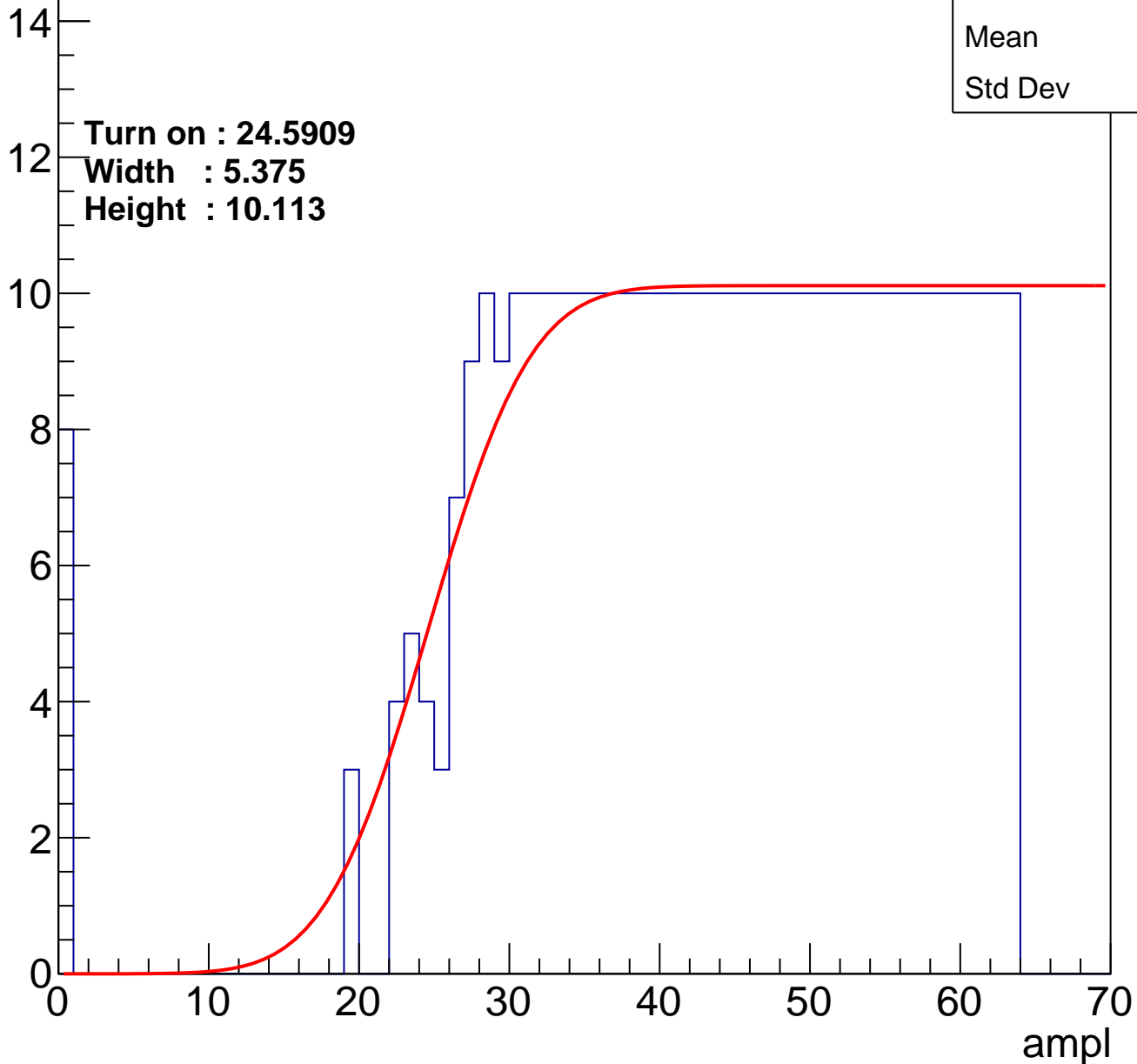
Entries	402
Mean	42.8
Std Dev	13

Turn on : 24.5909

Width : 5.375

Height : 10.113

Entry



B0L102S, U1-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.03
Std Dev	12.58

Turn on : 24.5632

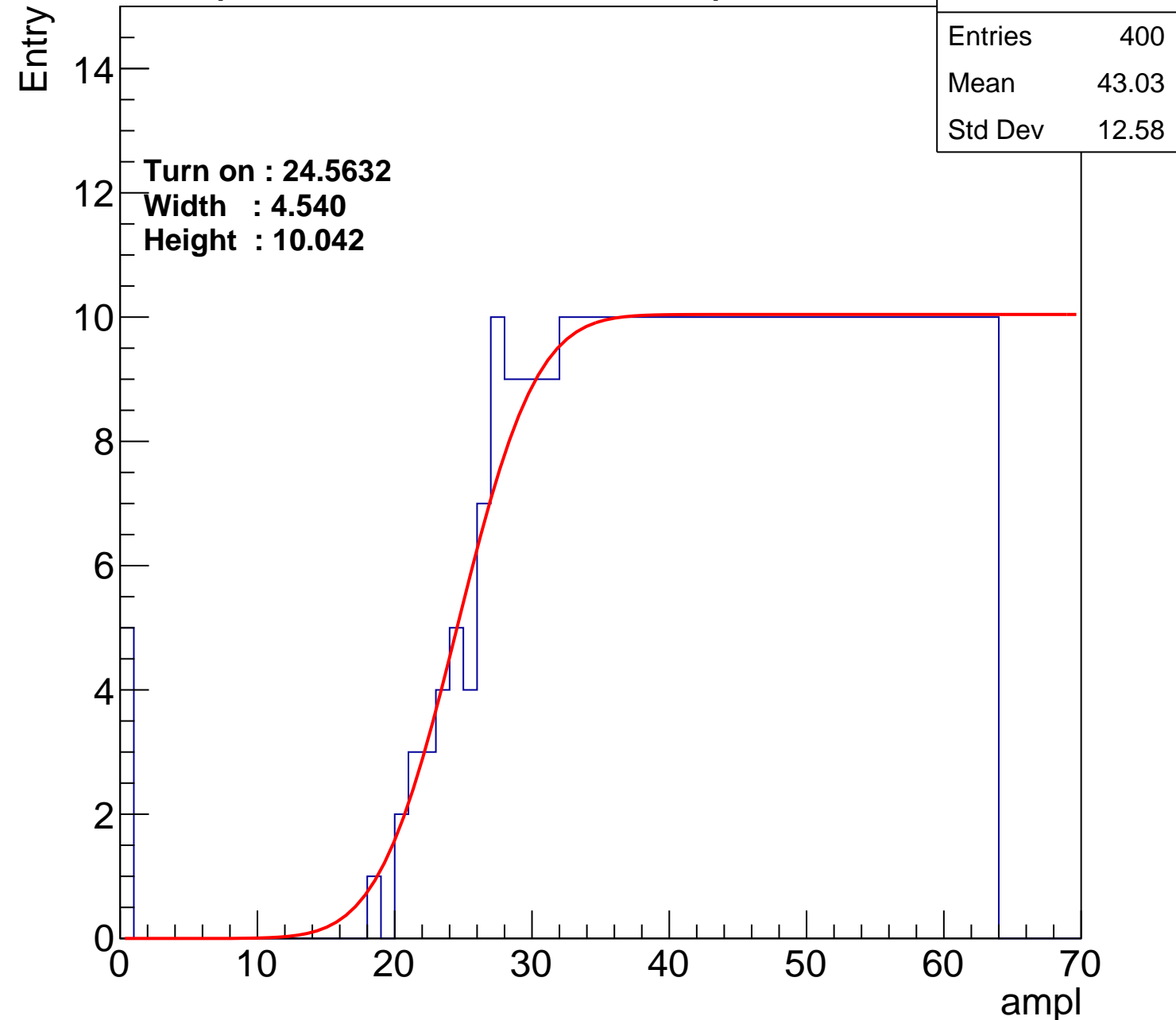
Width : 4.540

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.49
Std Dev	11.87

Turn on : 28.3122

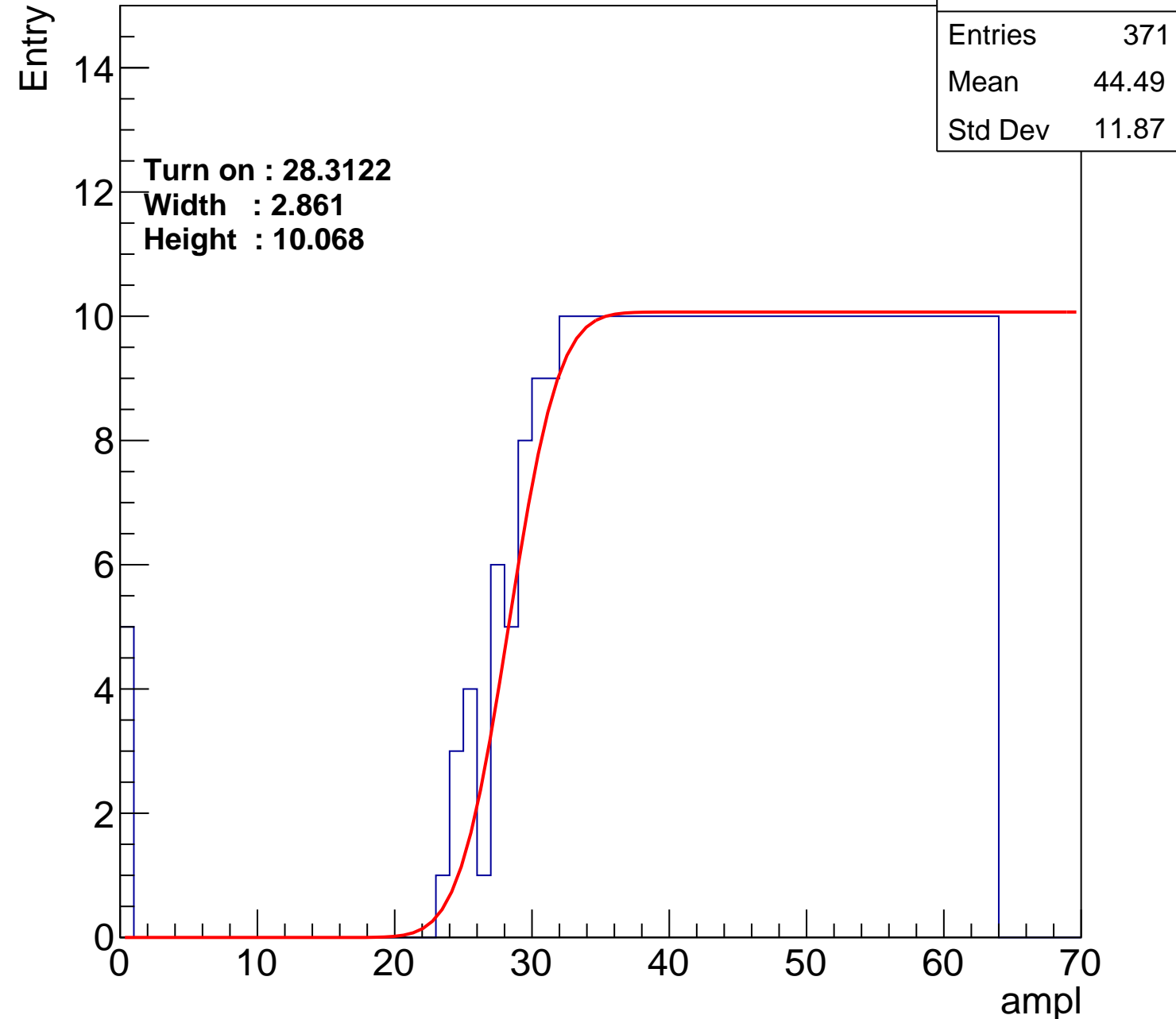
Width : 2.861

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.21
Std Dev	12.2

Turn on : 25.6526

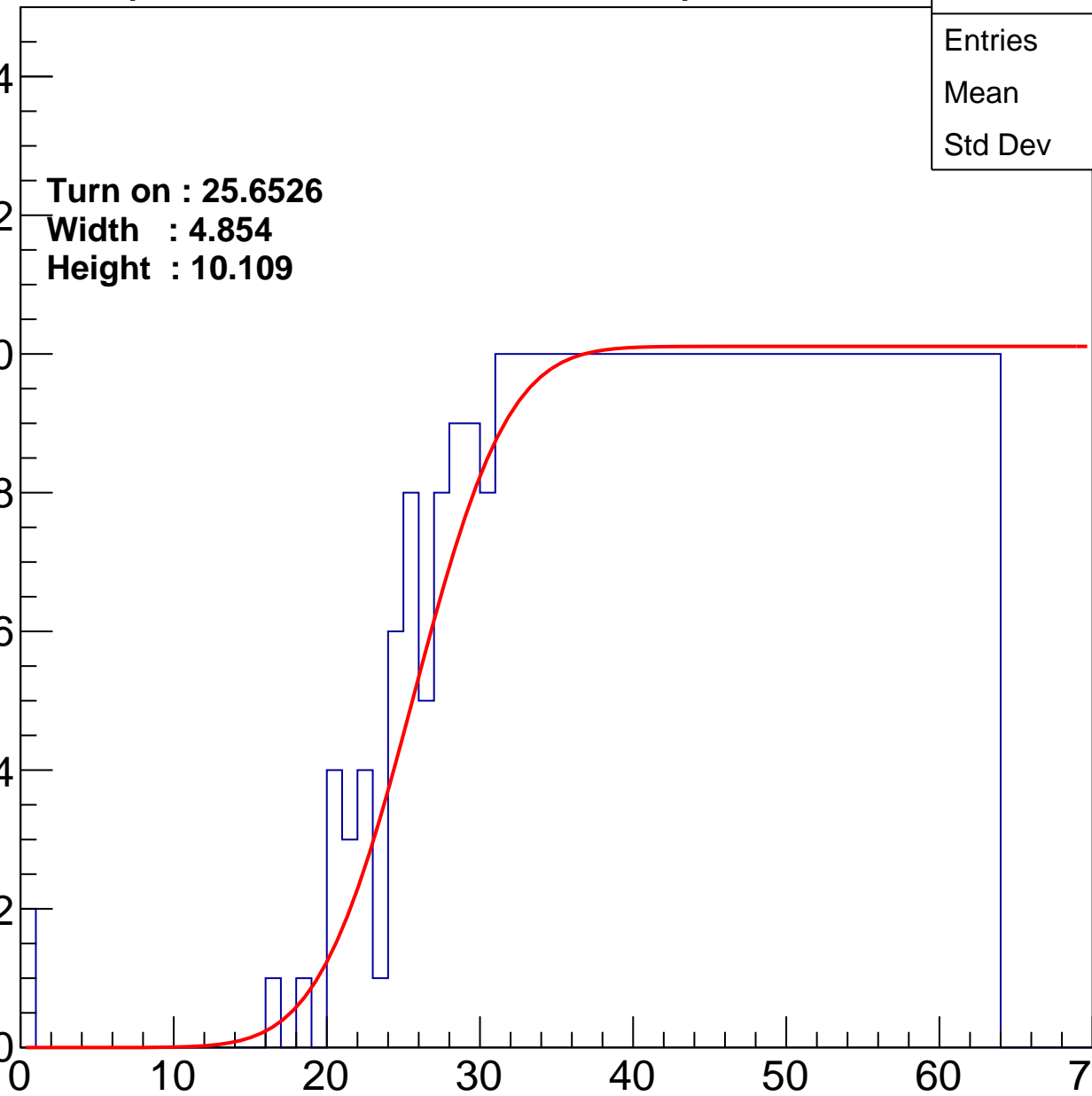
Width : 4.854

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.02
Std Dev	12.11

Turn on : 26.6573

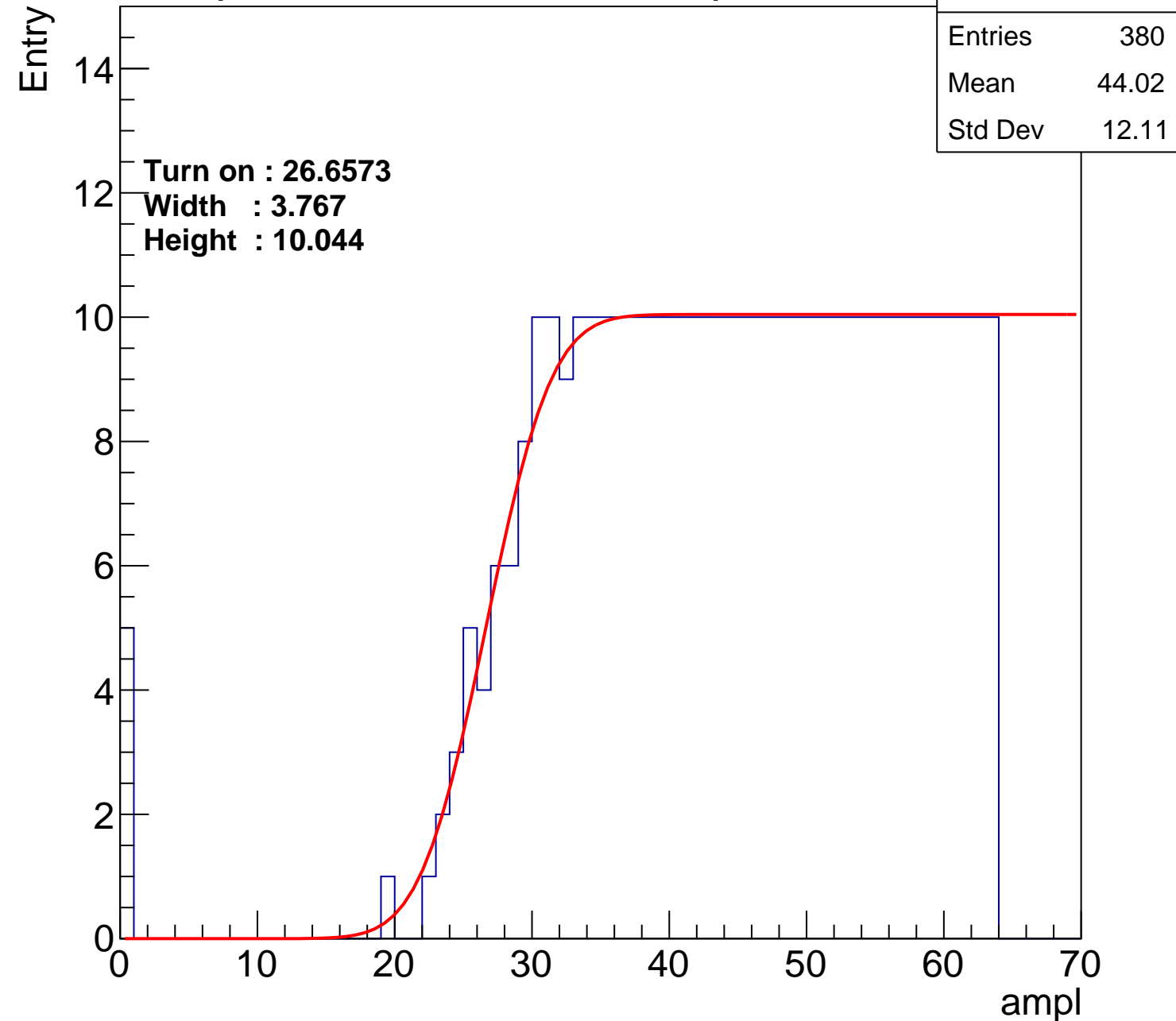
Width : 3.767

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch106

calib_packv5_042523_0143.root, FC#12, port B1

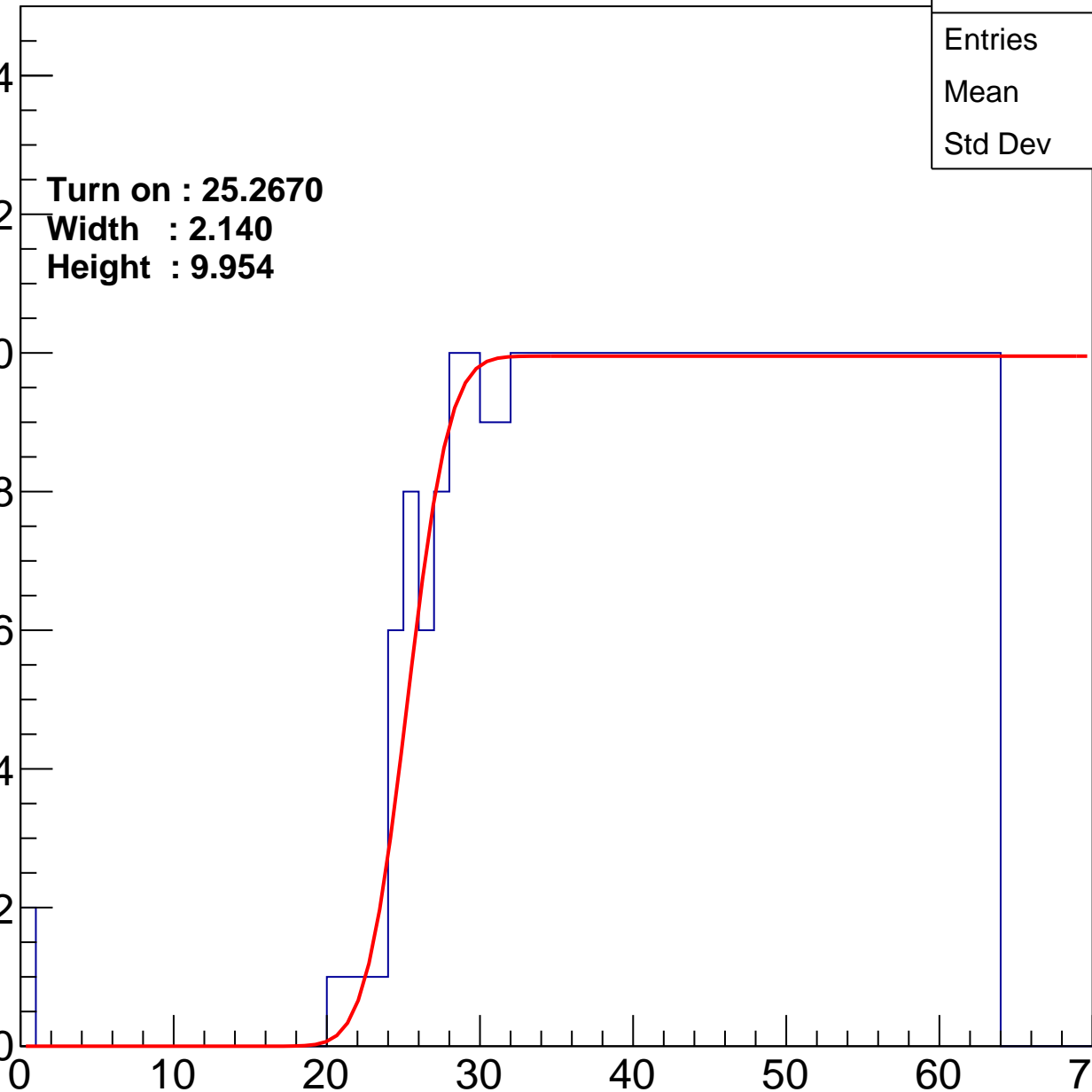
Entry

14
12
10
8
6
4
2
0

Turn on : 25.2670
Width : 2.140
Height : 9.954

Entries	392
Mean	43.68
Std Dev	11.81

ampl



B0L102S, U1-ch107

calib_packv5_042523_0143.root, FC#12, port B1

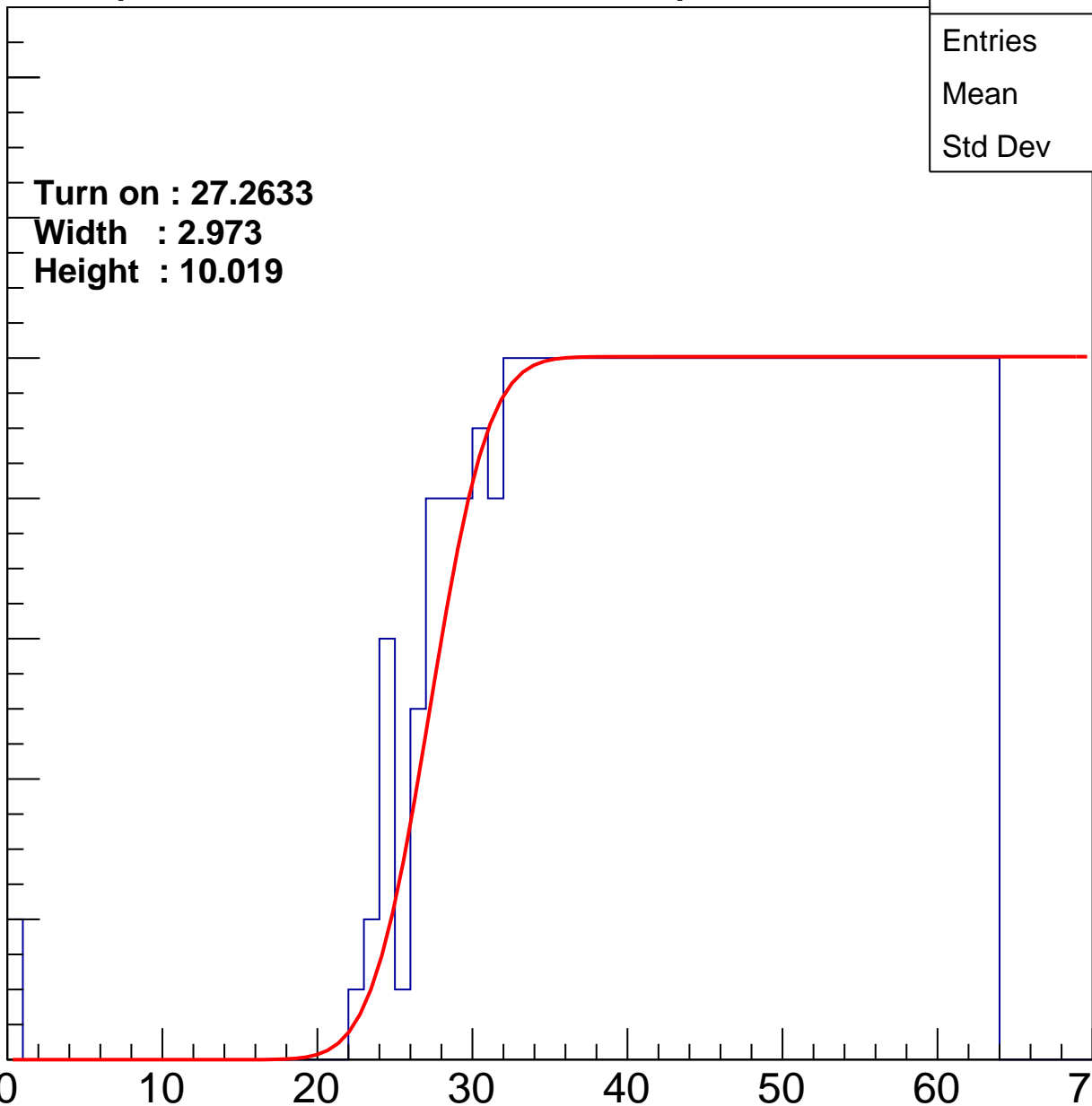
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2633
Width : 2.973
Height : 10.019

Entries	378
Mean	44.33
Std Dev	11.51

ampl



B0L102S, U1-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.48
Std Dev	11.56

Turn on : 26.7989

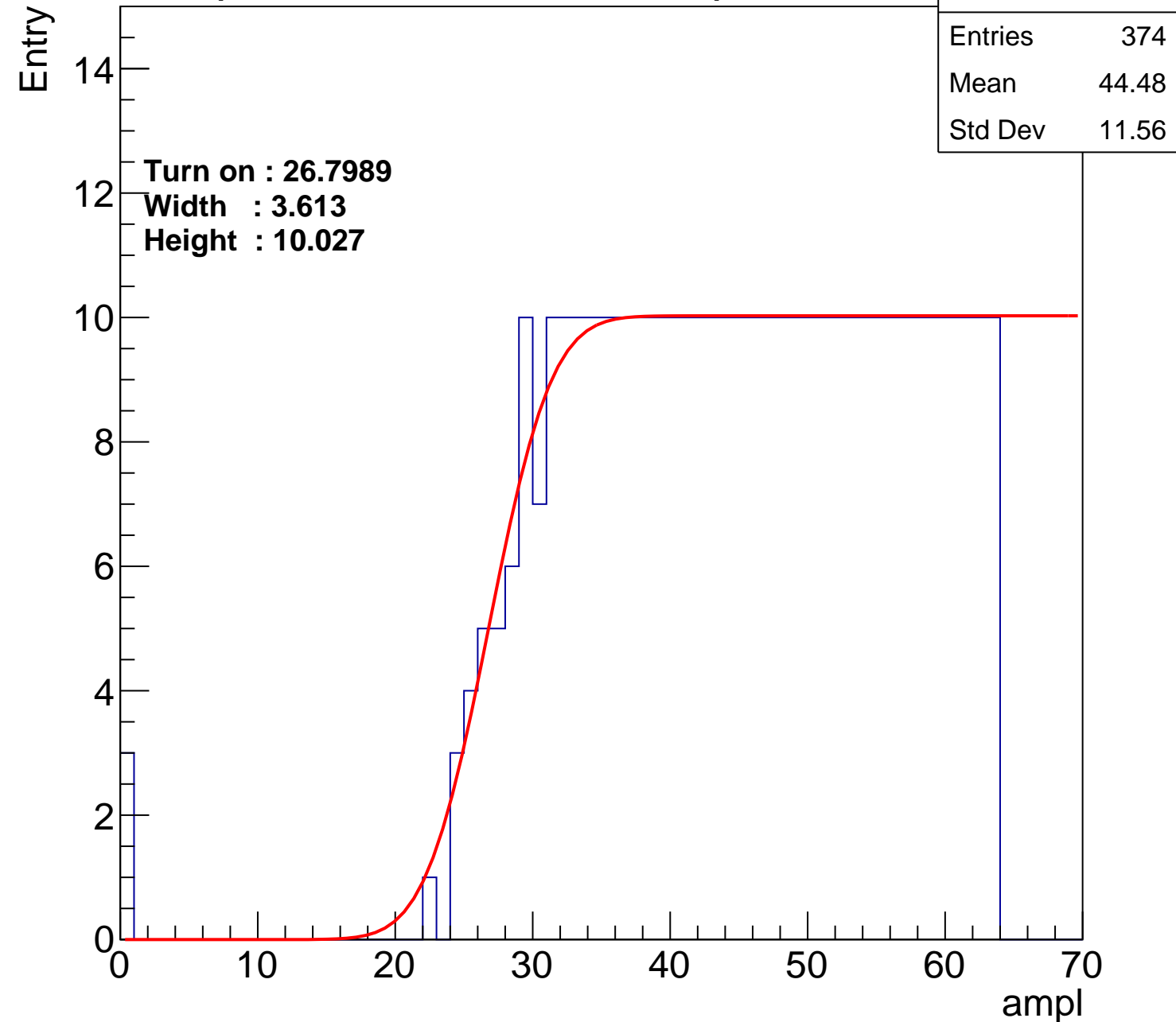
Width : 3.613

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.76
Std Dev	11.9

Turn on : 25.5427

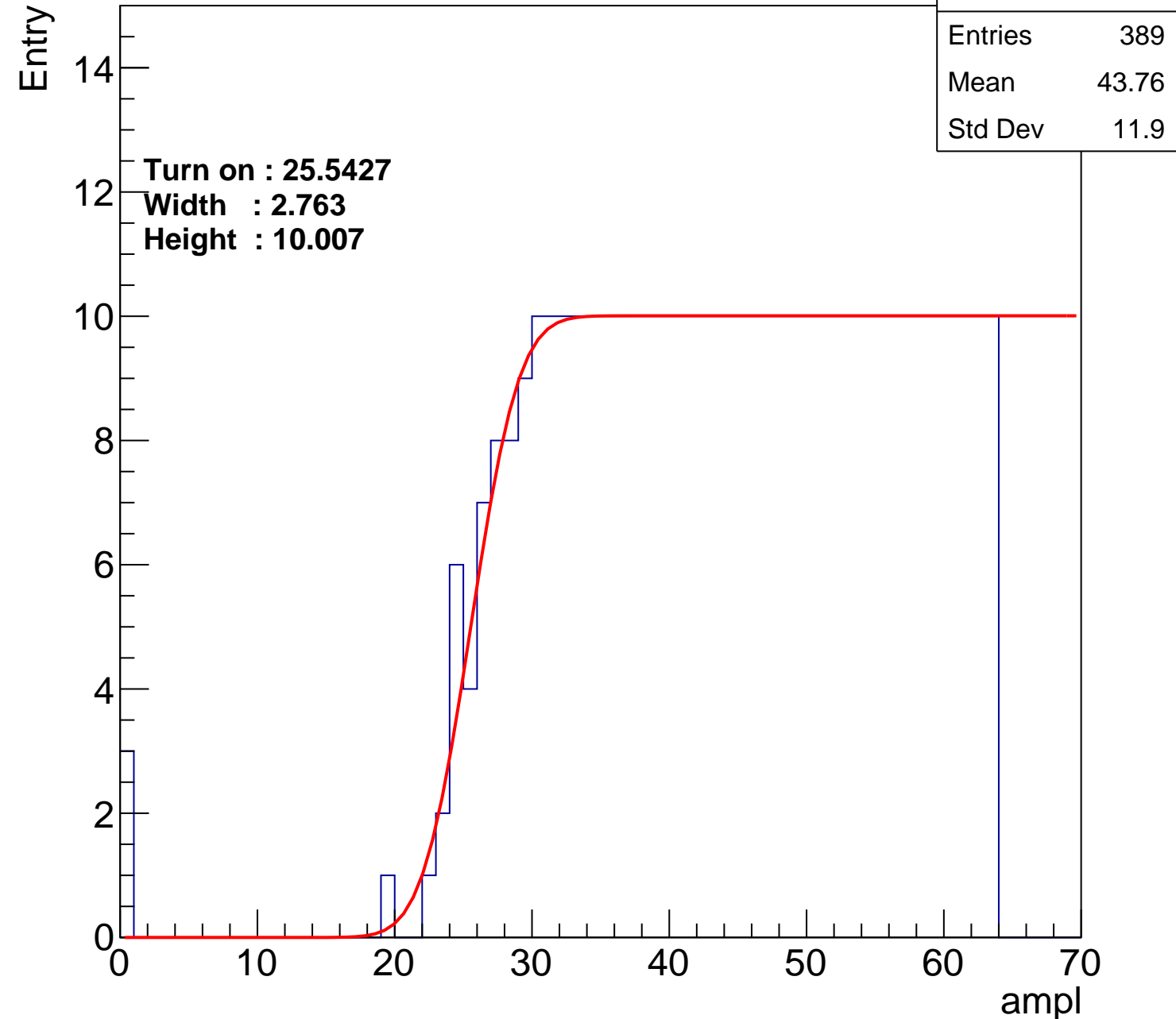
Width : 2.763

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.32
Std Dev	12.18

Turn on : 24.7527

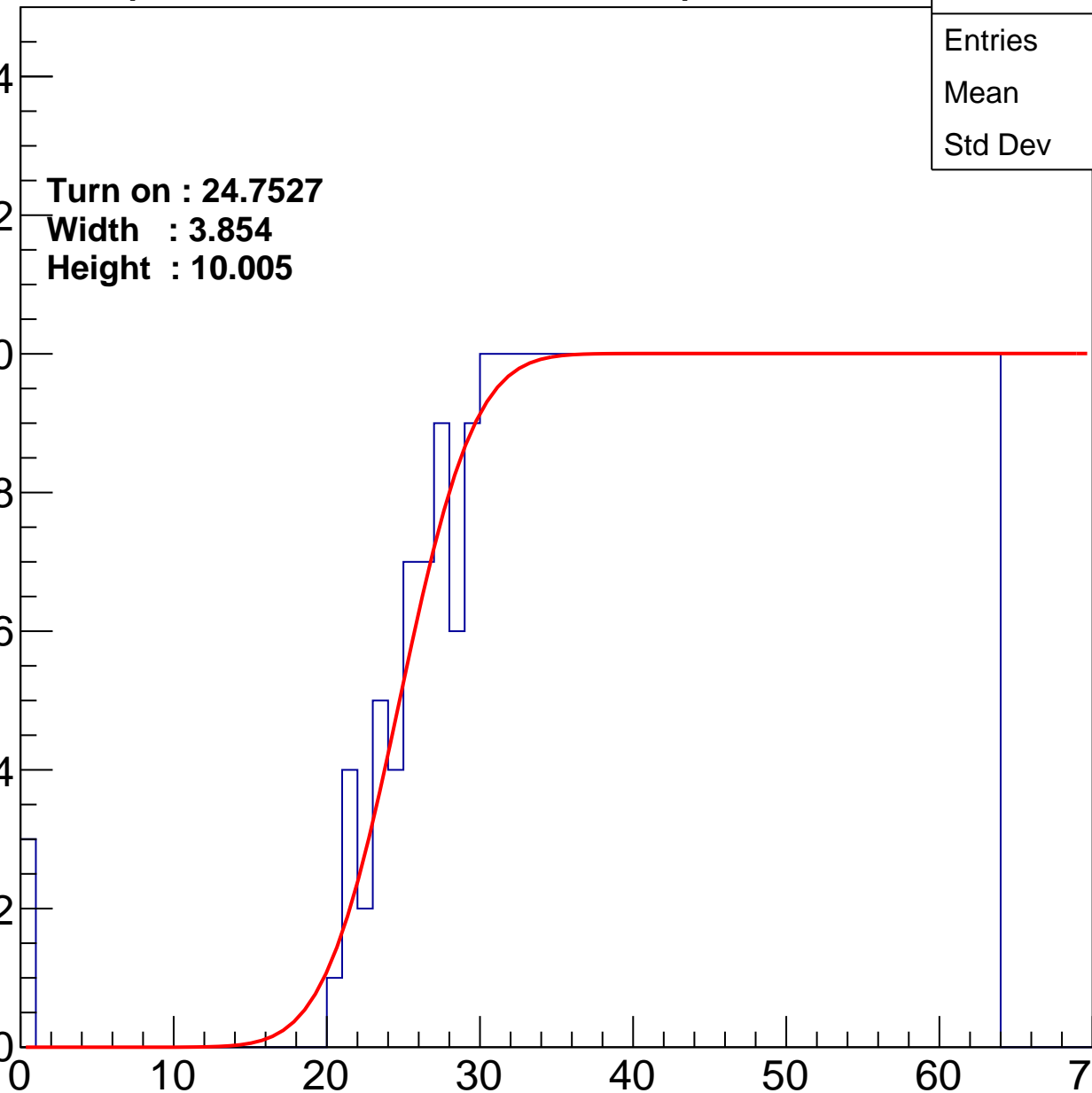
Width : 3.854

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.72
Std Dev	11.96

Turn on : 25.5952

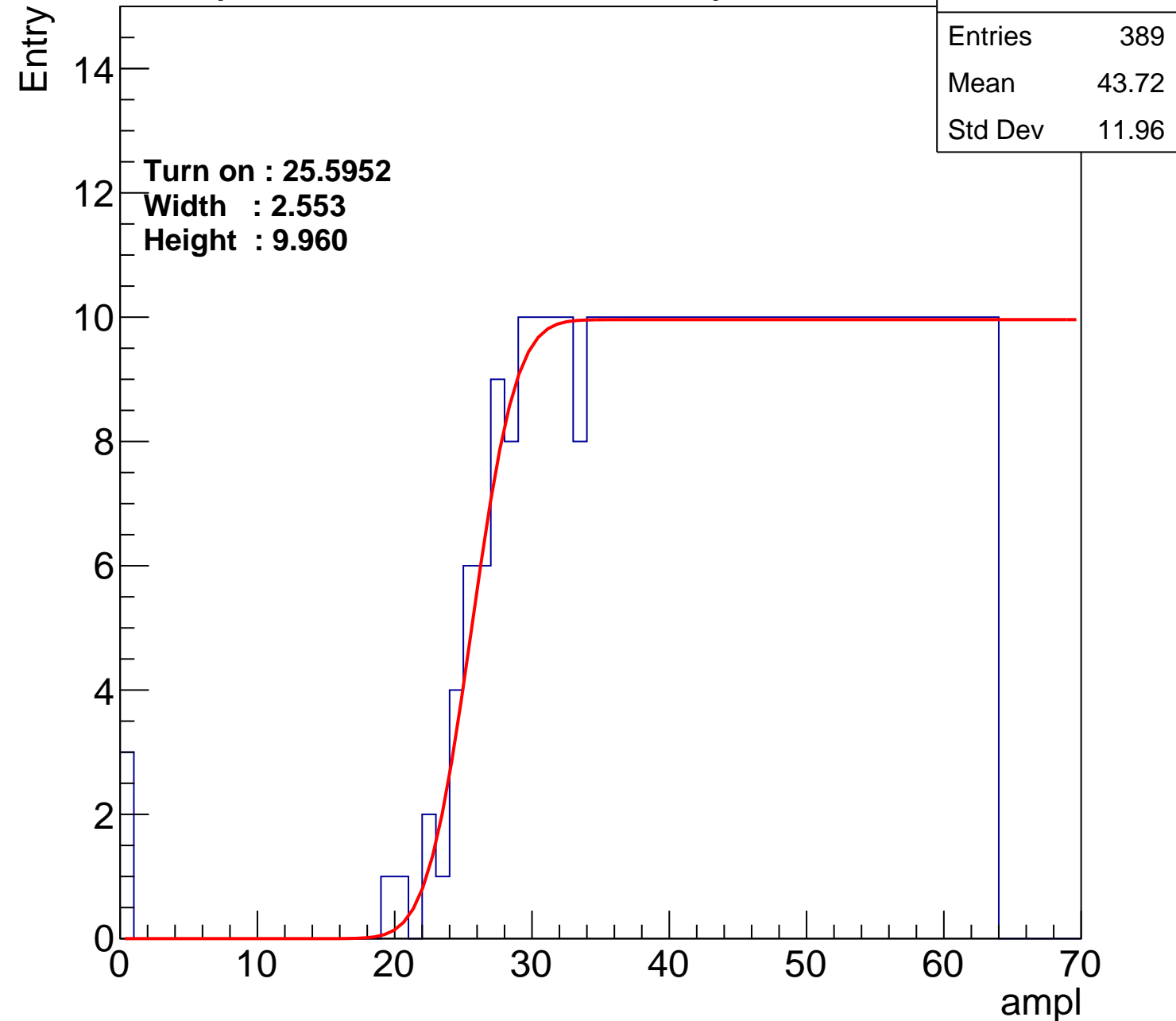
Width : 2.553

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch112

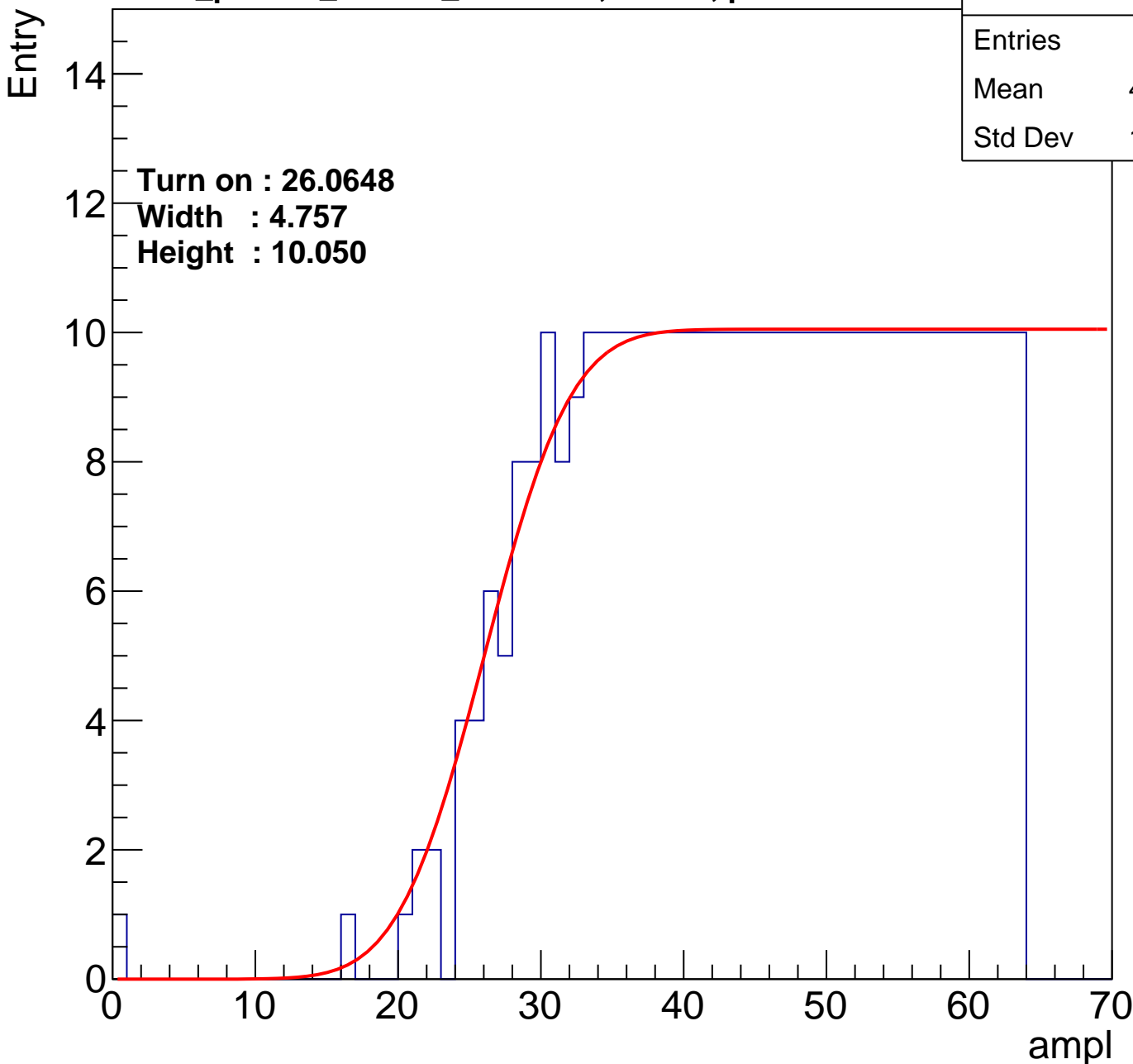
calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.28
Std Dev	11.48

Turn on : 26.0648

Width : 4.757

Height : 10.050



B0L102S, U1-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.47
Std Dev	11.94

Turn on : 25.3820

Width : 3.028

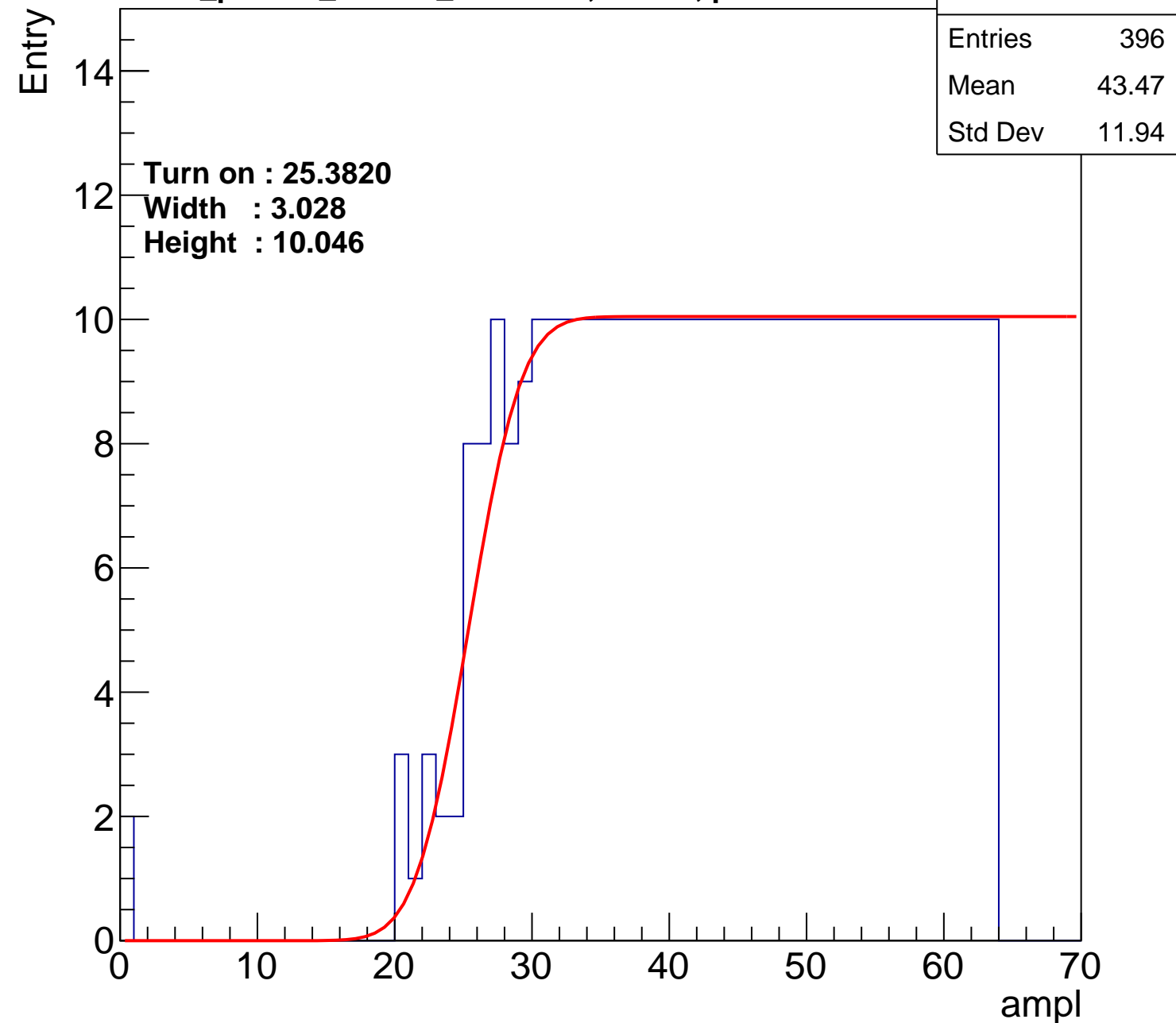
Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U1-ch114

calib_packv5_042523_0143.root, FC#12, port B1

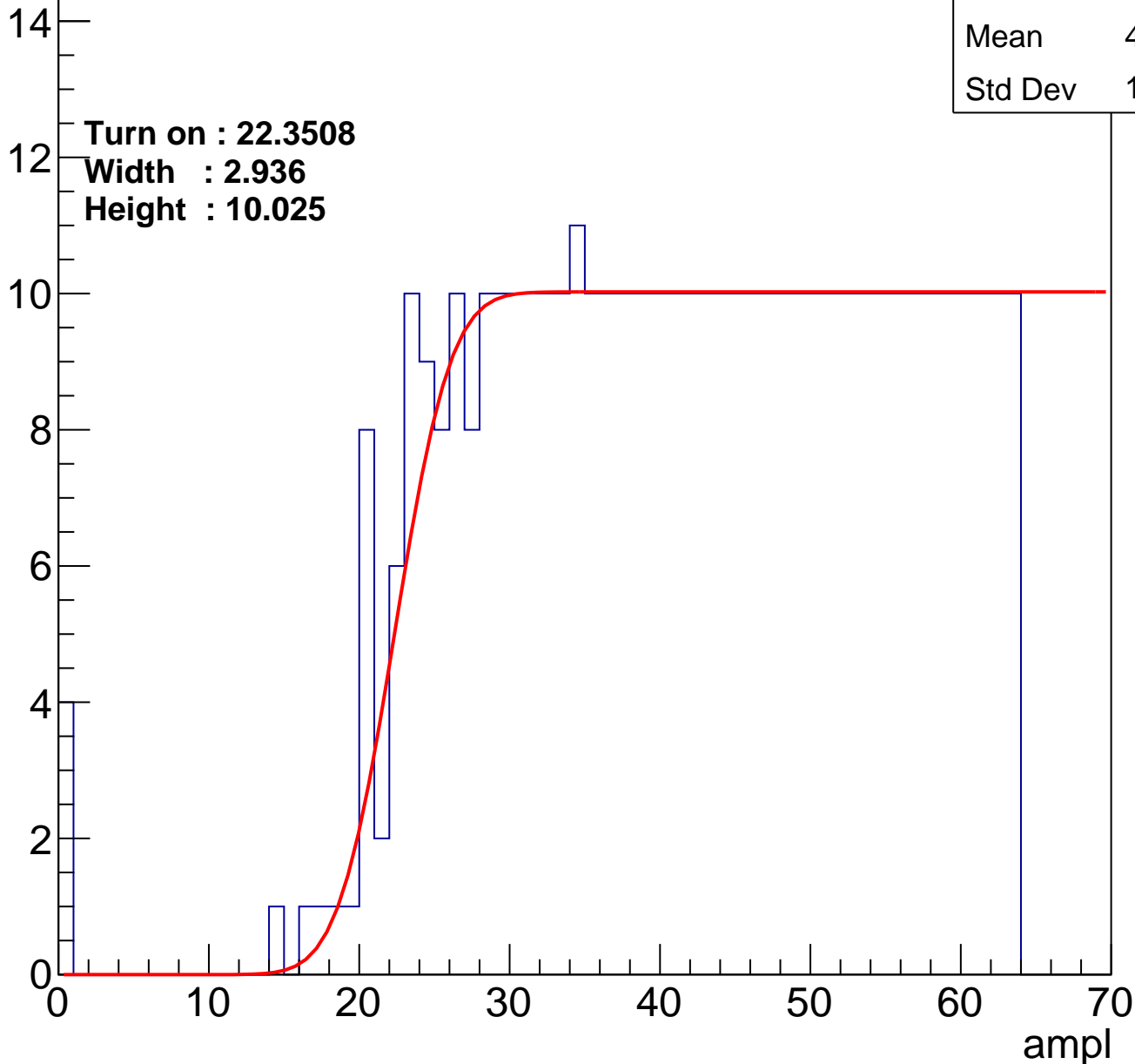
Entries	431
Mean	41.66
Std Dev	13.09

Turn on : 22.3508

Width : 2.936

Height : 10.025

Entry



B0L102S, U1-ch115

calib_packv5_042523_0143.root, FC#12, port B1

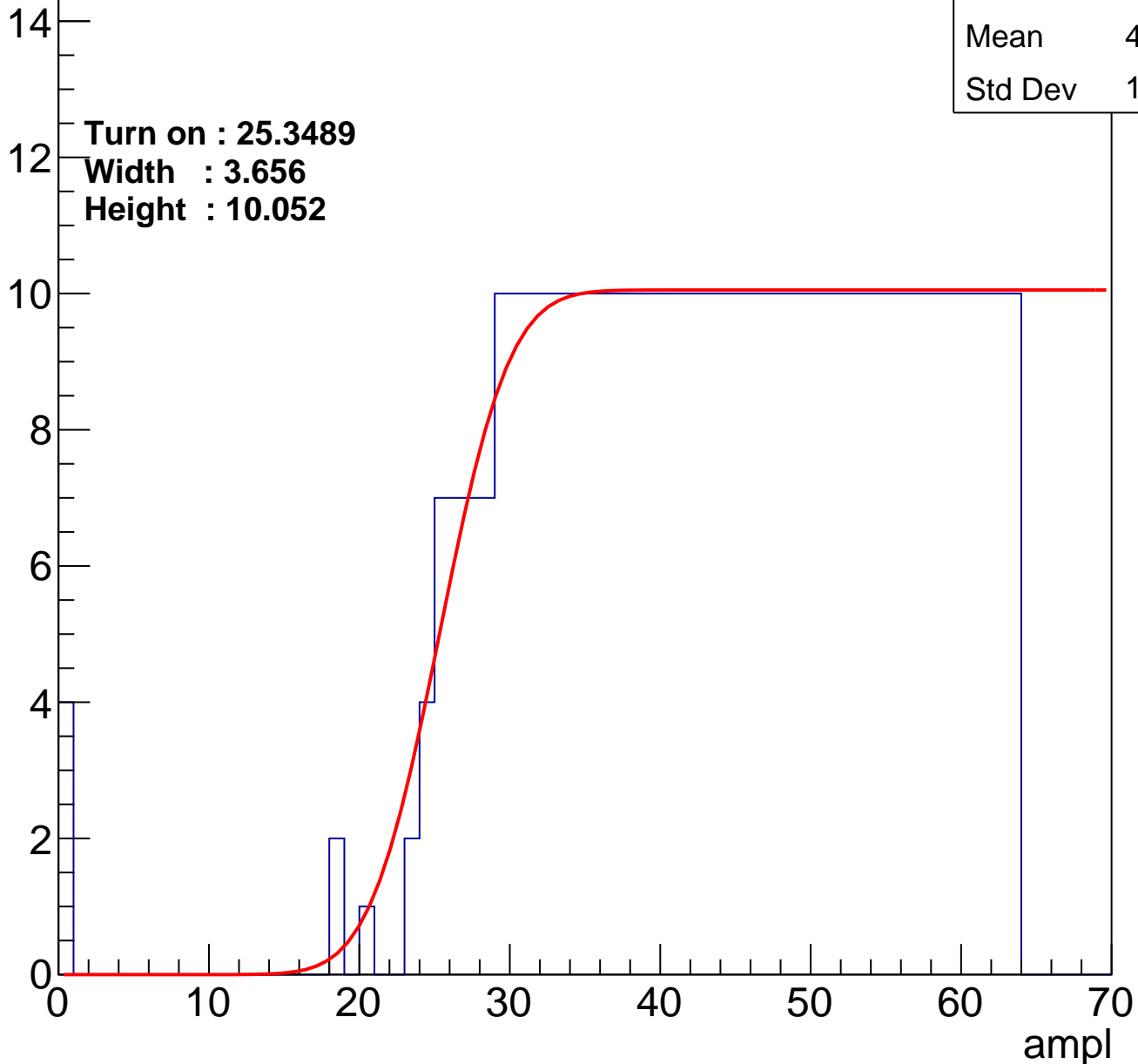
Entries	391
Mean	43.58
Std Dev	12.16

Turn on : 25.3489

Width : 3.656

Height : 10.052

Entry



B0L102S, U1-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.93
Std Dev	12.22

Turn on : 24.3759

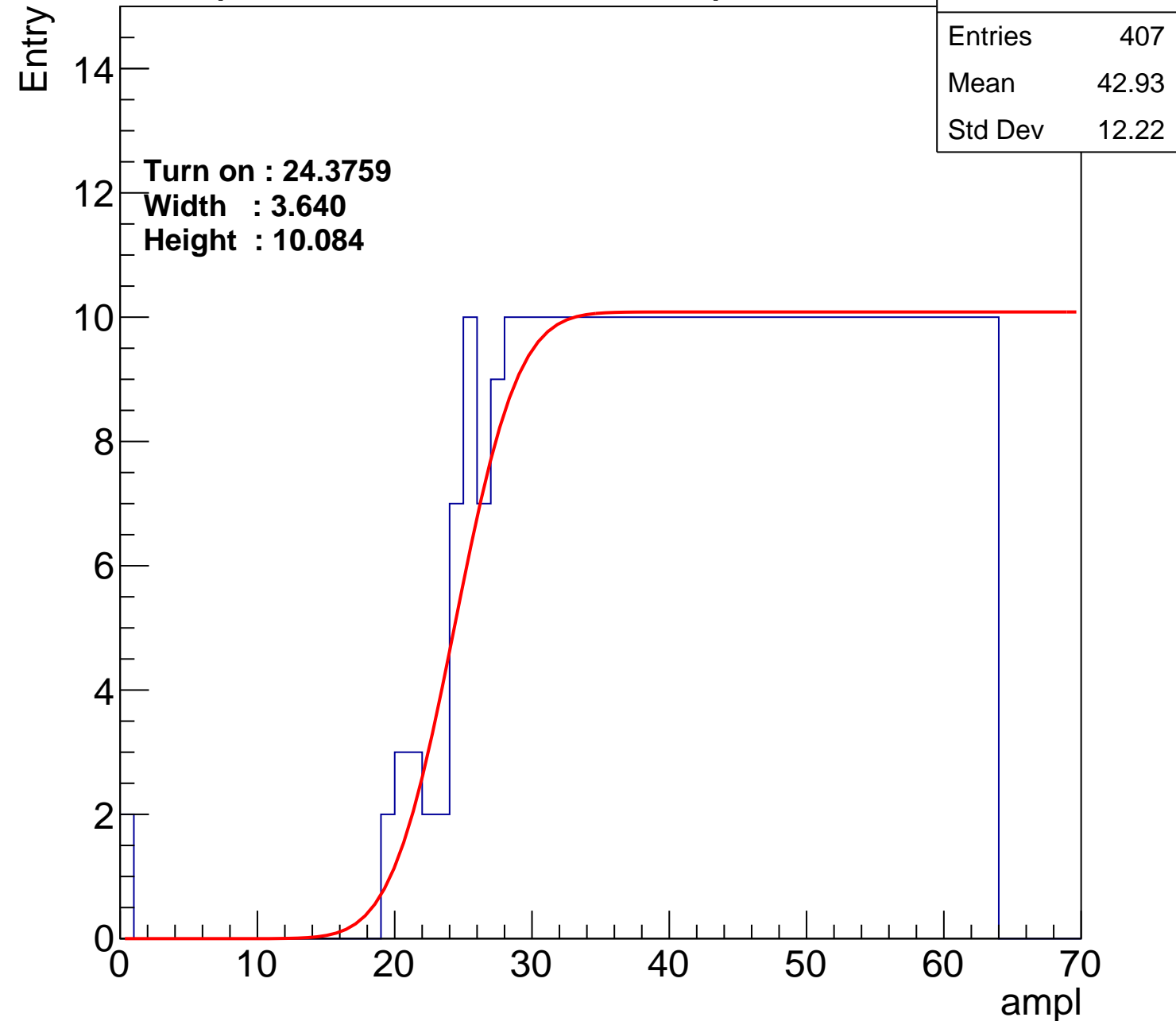
Width : 3.640

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.5
Std Dev	11.94

Turn on : 24.8207

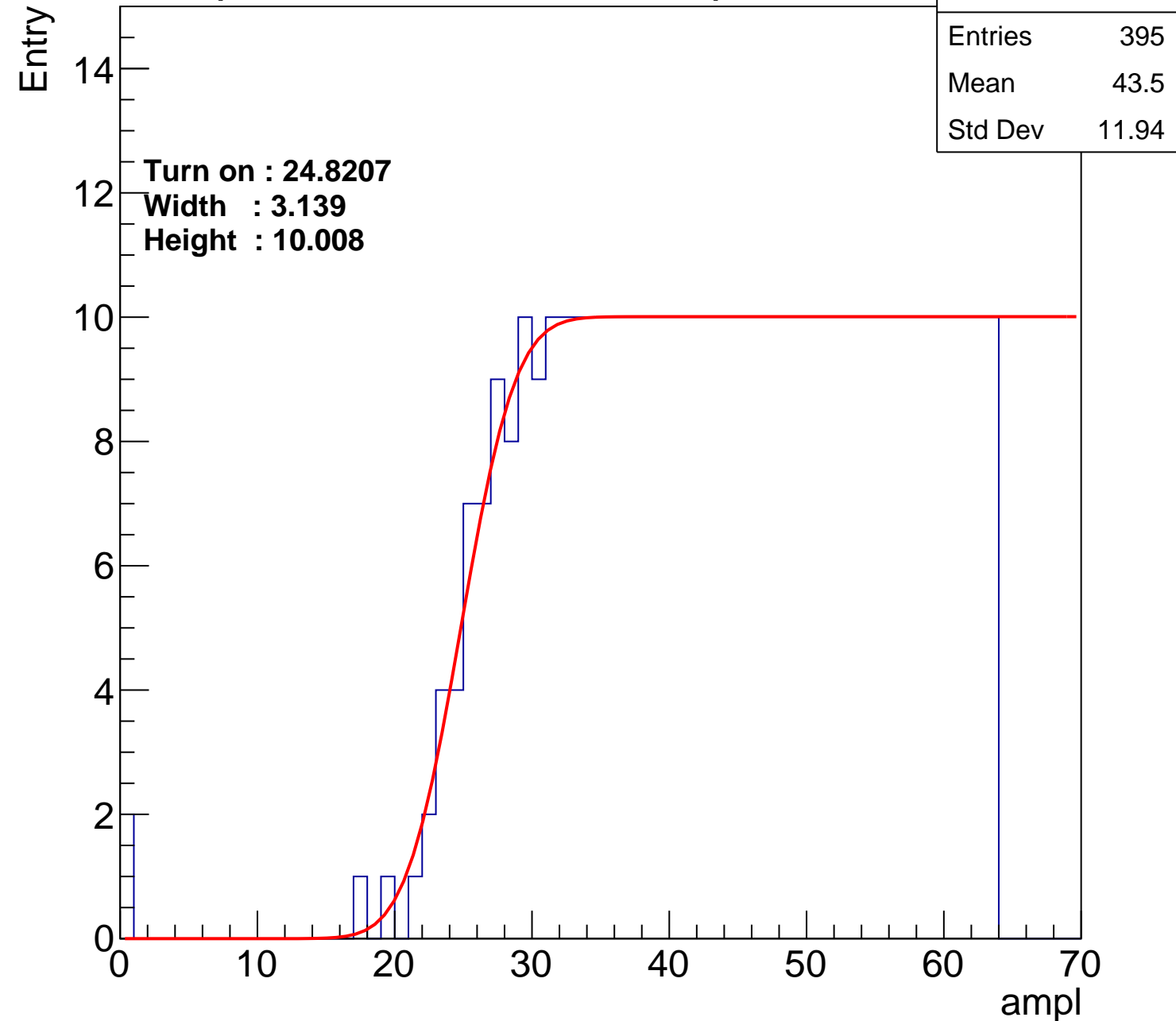
Width : 3.139

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.4901

Width : 3.328

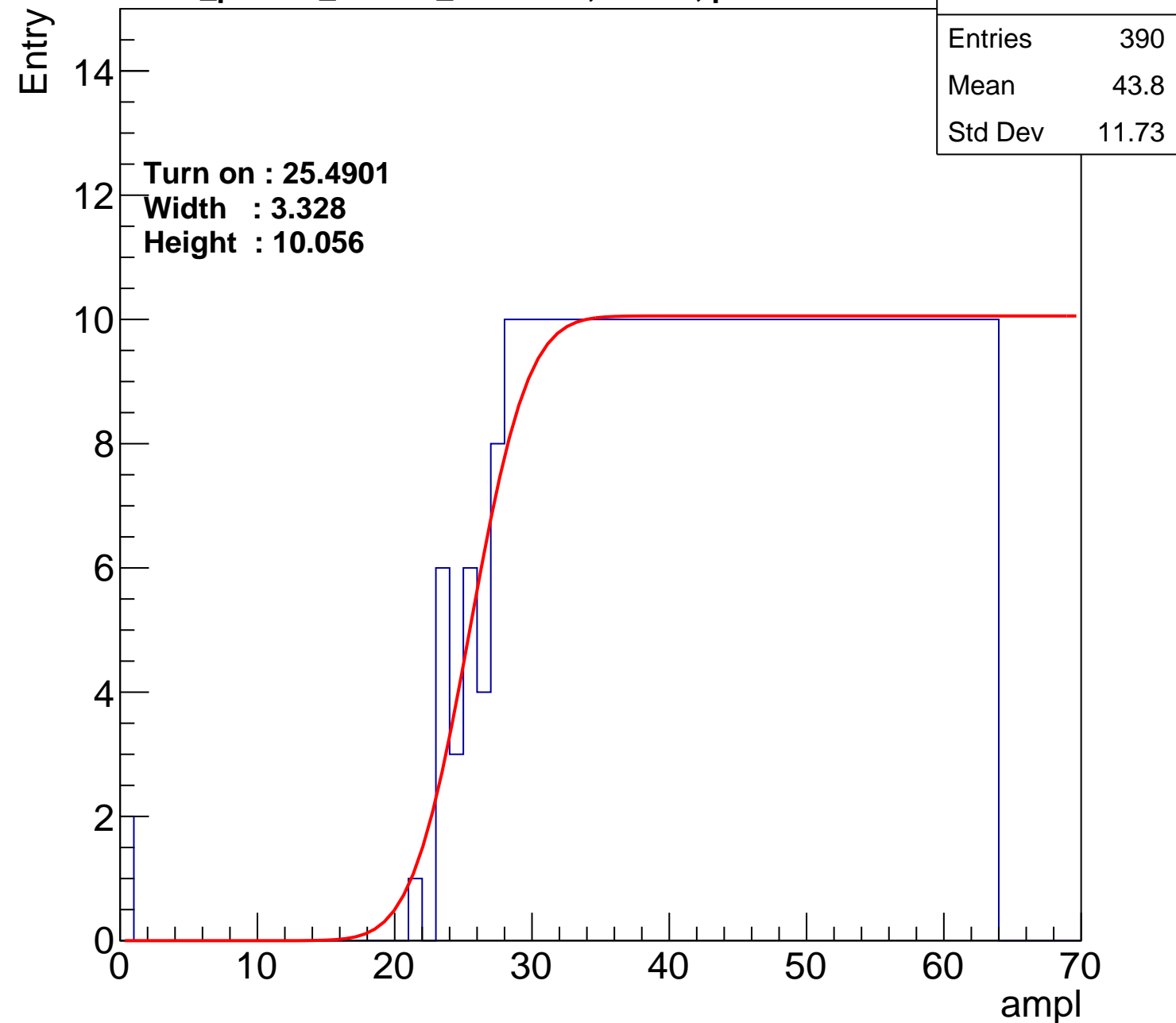
Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U1-ch119

calib_packv5_042523_0143.root, FC#12, port B1

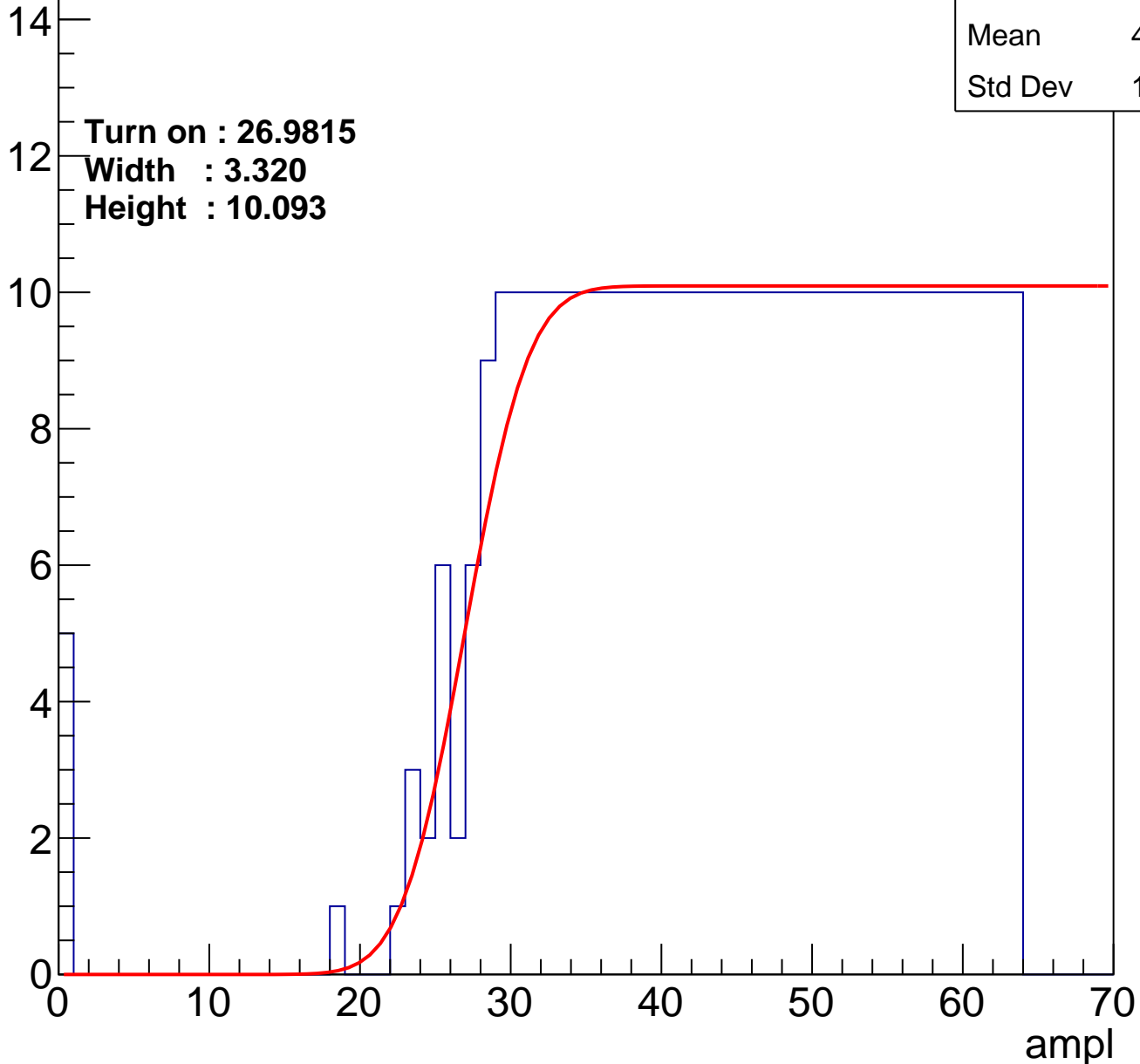
Entries	385
Mean	43.83
Std Dev	12.16

Turn on : 26.9815

Width : 3.320

Height : 10.093

Entry



B0L102S, U1-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.76
Std Dev	11.35

Turn on : 27.7077

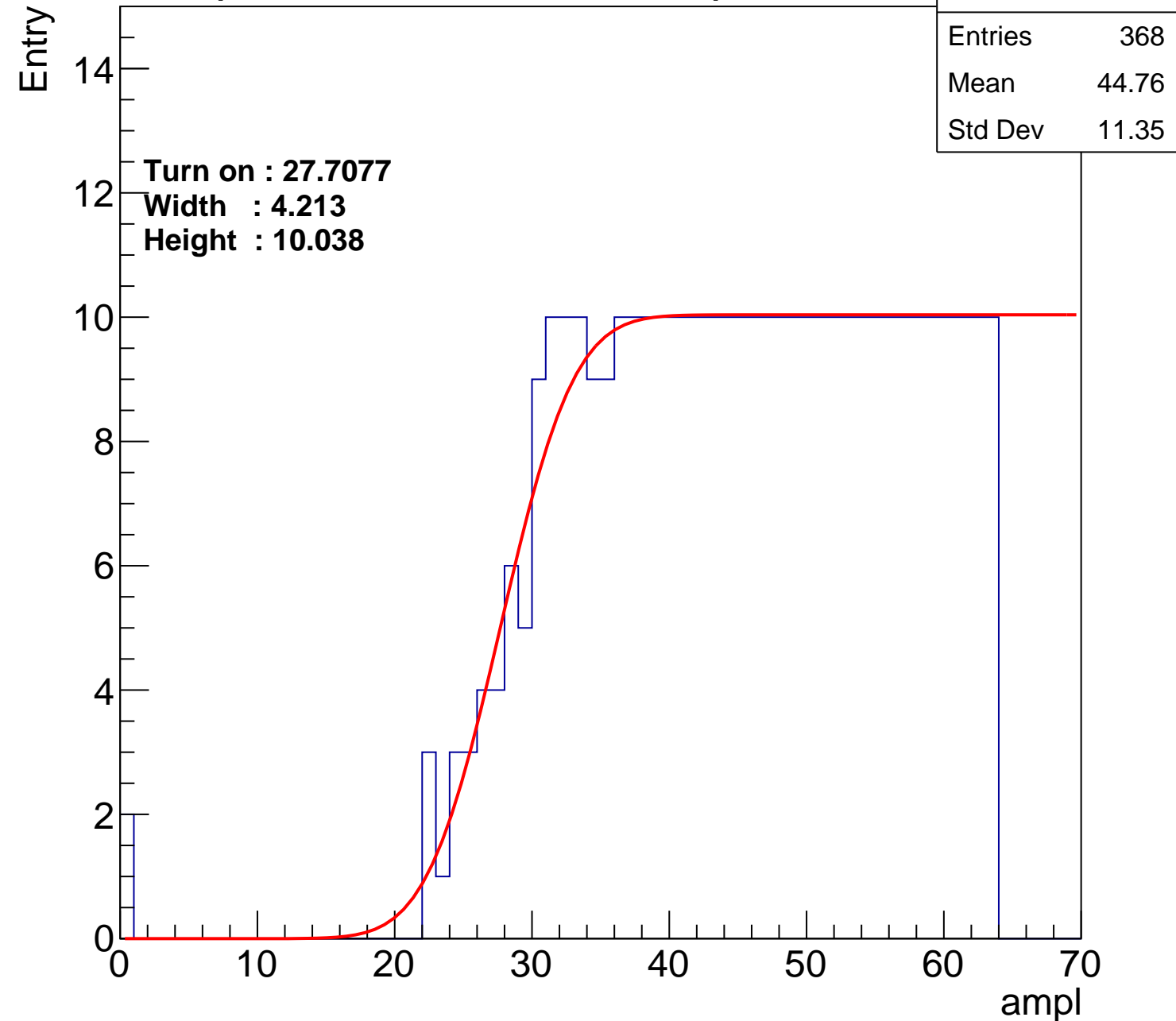
Width : 4.213

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.86
Std Dev	11.91

Turn on : 25.8155

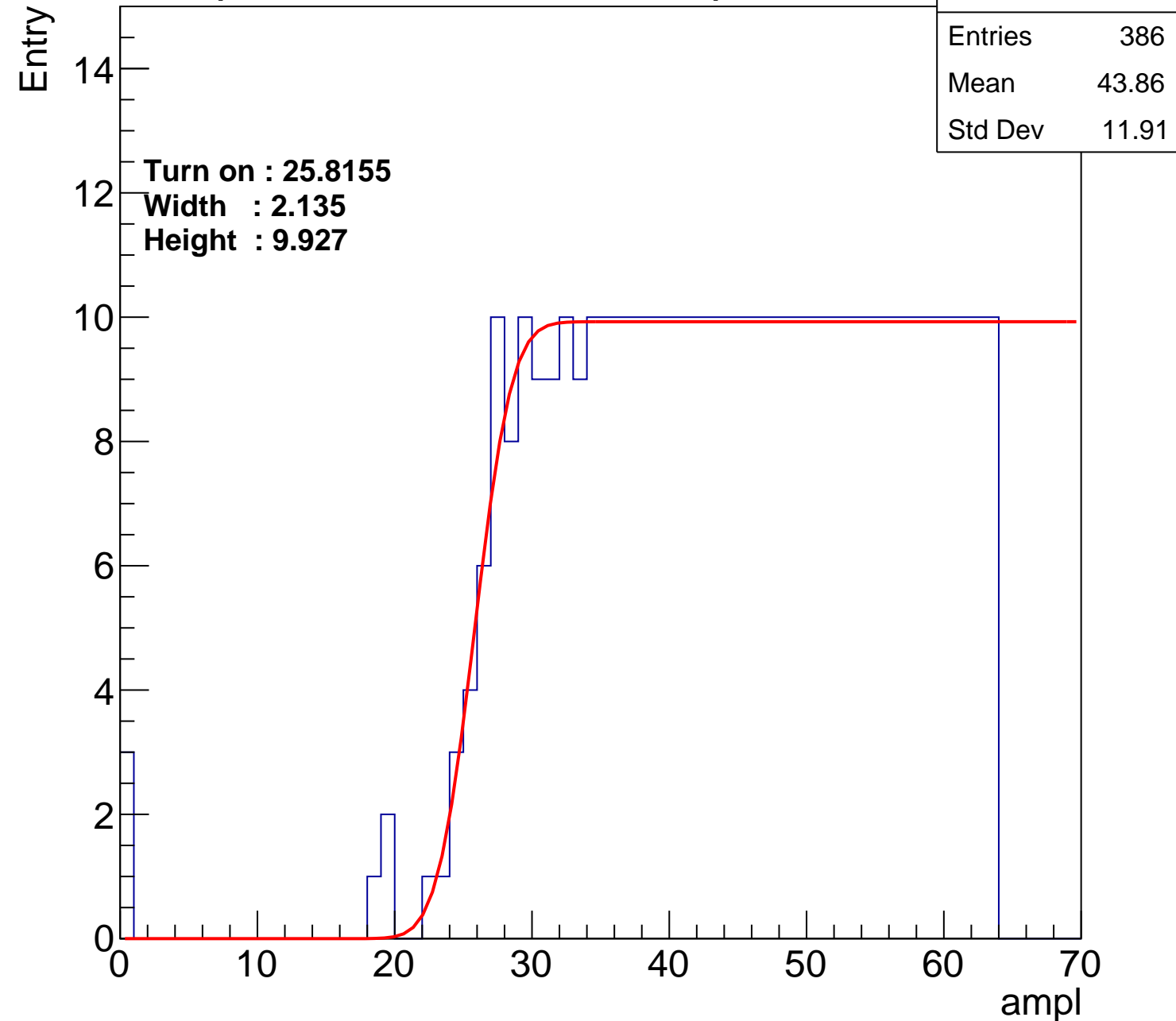
Width : 2.135

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.99
Std Dev	11.73

Turn on : 25.9638

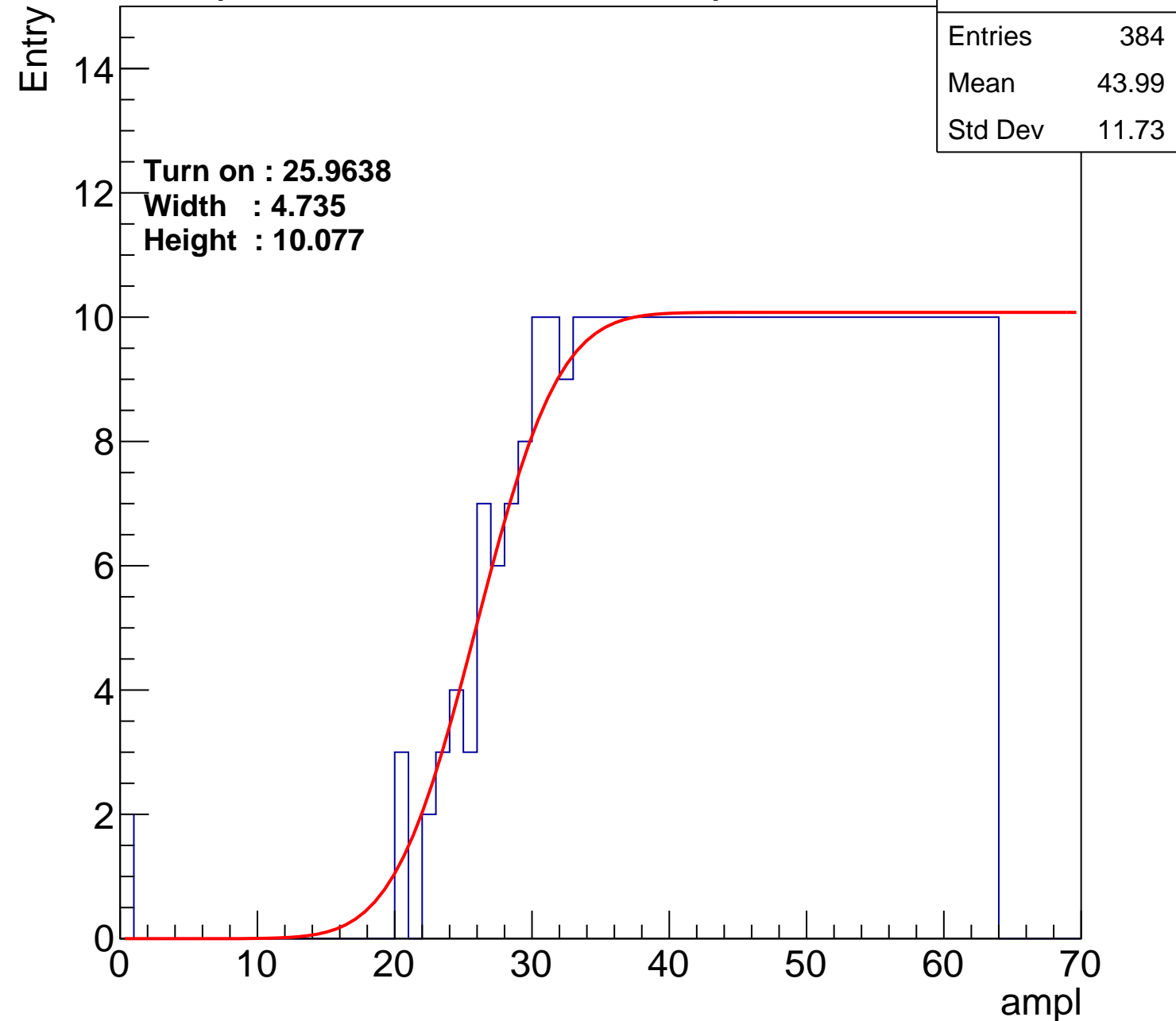
Width : 4.735

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.41
Std Dev	11.63

Turn on : 27.1475

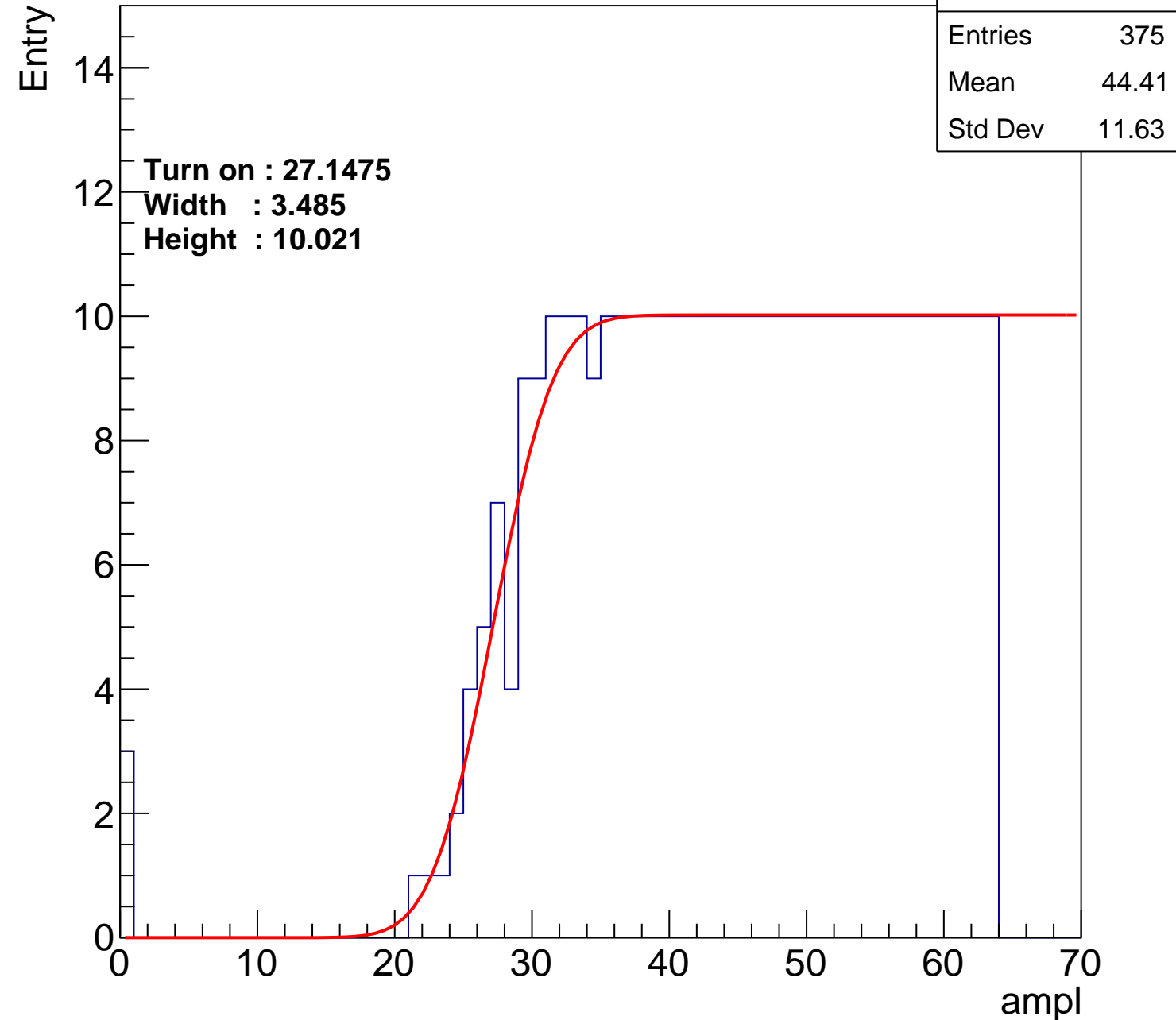
Width : 3.485

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.77
Std Dev	11.65

Turn on : 24.8541

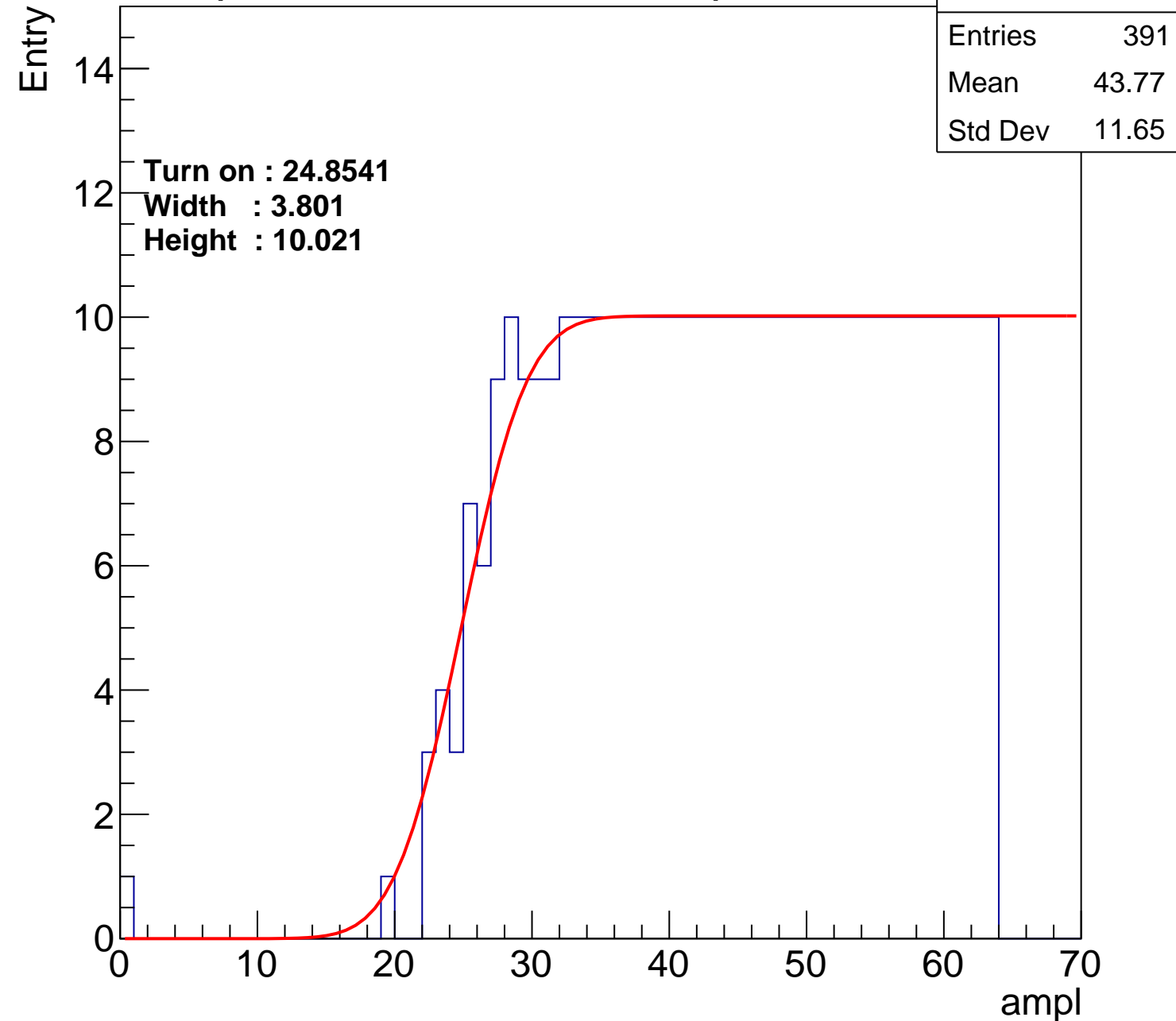
Width : 3.801

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.9
Std Dev	11.71

Turn on : 25.2062

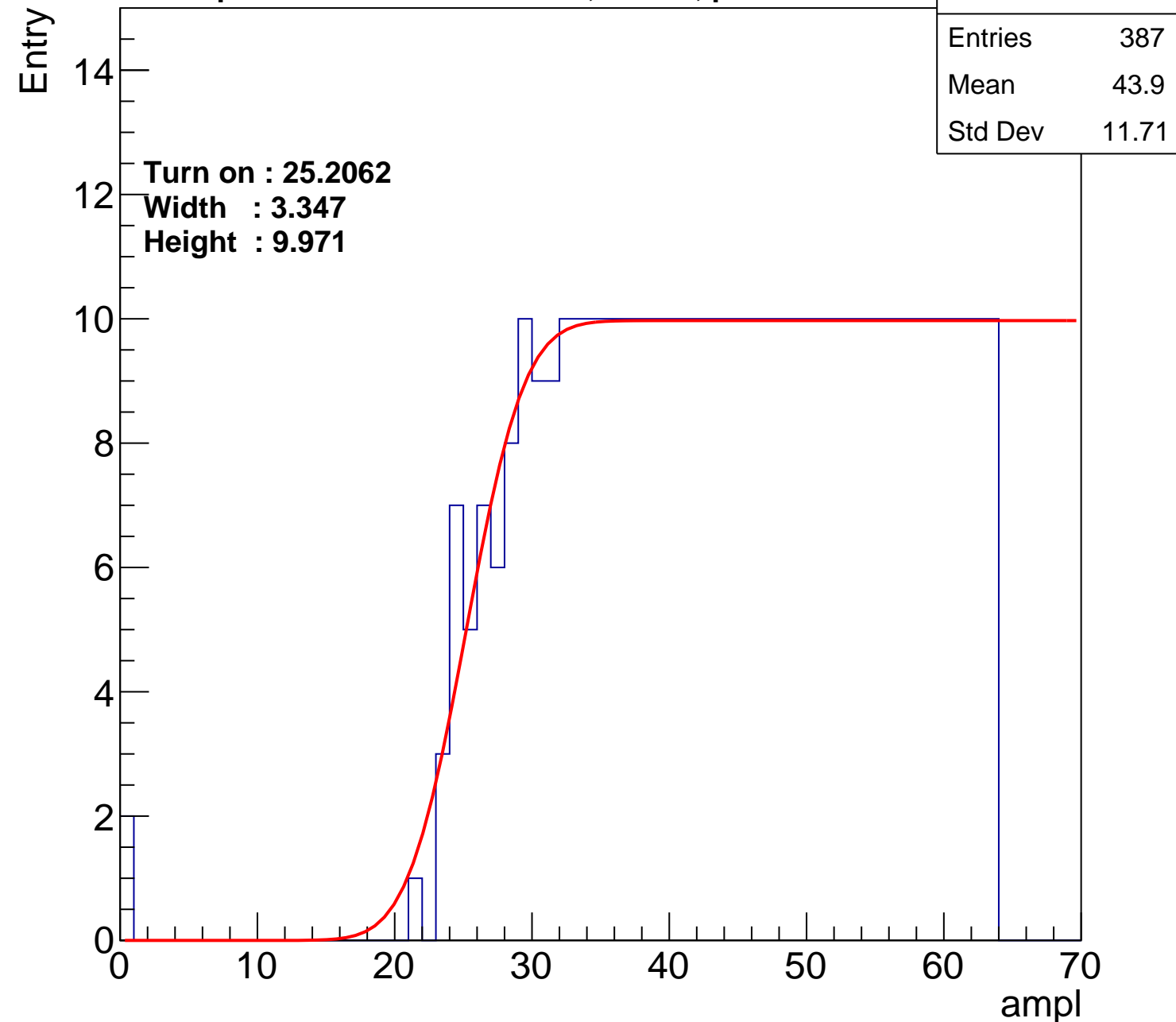
Width : 3.347

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch126

calib_packv5_042523_0143.root, FC#12, port B1

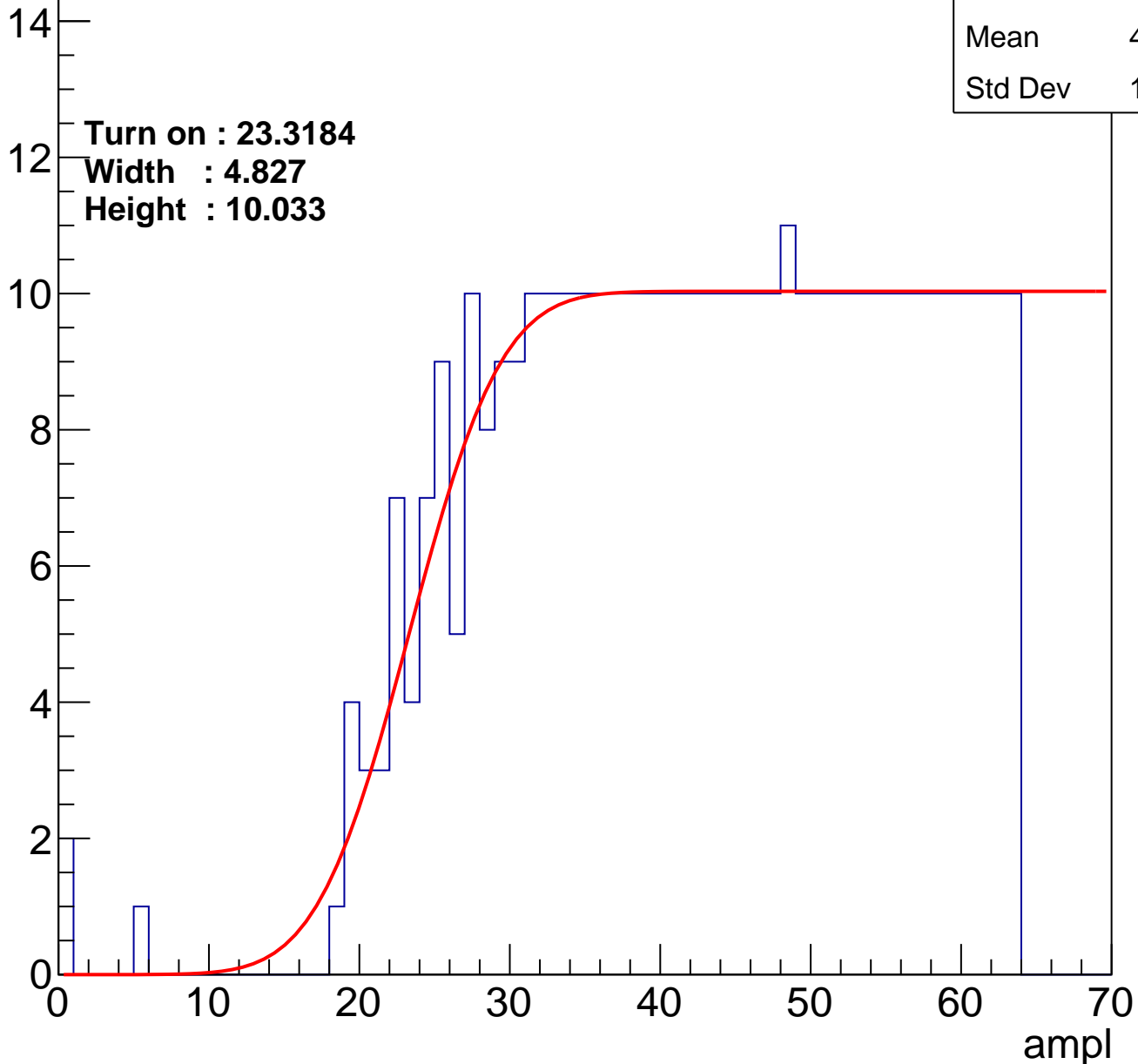
Entries	413
Mean	42.55
Std Dev	12.59

Turn on : 23.3184

Width : 4.827

Height : 10.033

Entry



B0L102S, U1-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.81
Std Dev	11.77

Turn on : 25.3162

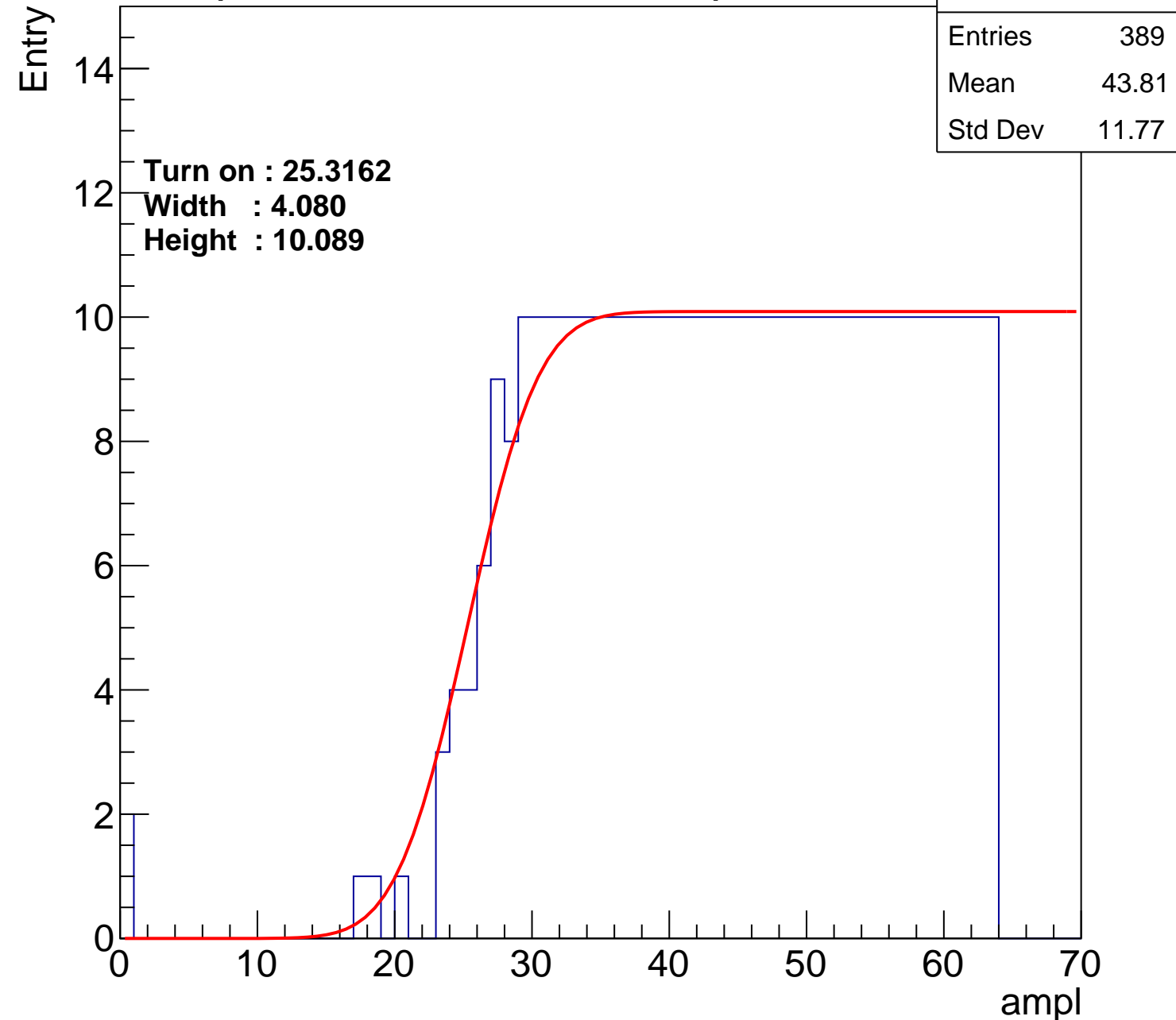
Width : 4.080

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U1-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.81
Std Dev	11.77

Turn on : 25.3162

Width : 4.080

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl

