

B0L101S, U24-ch0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

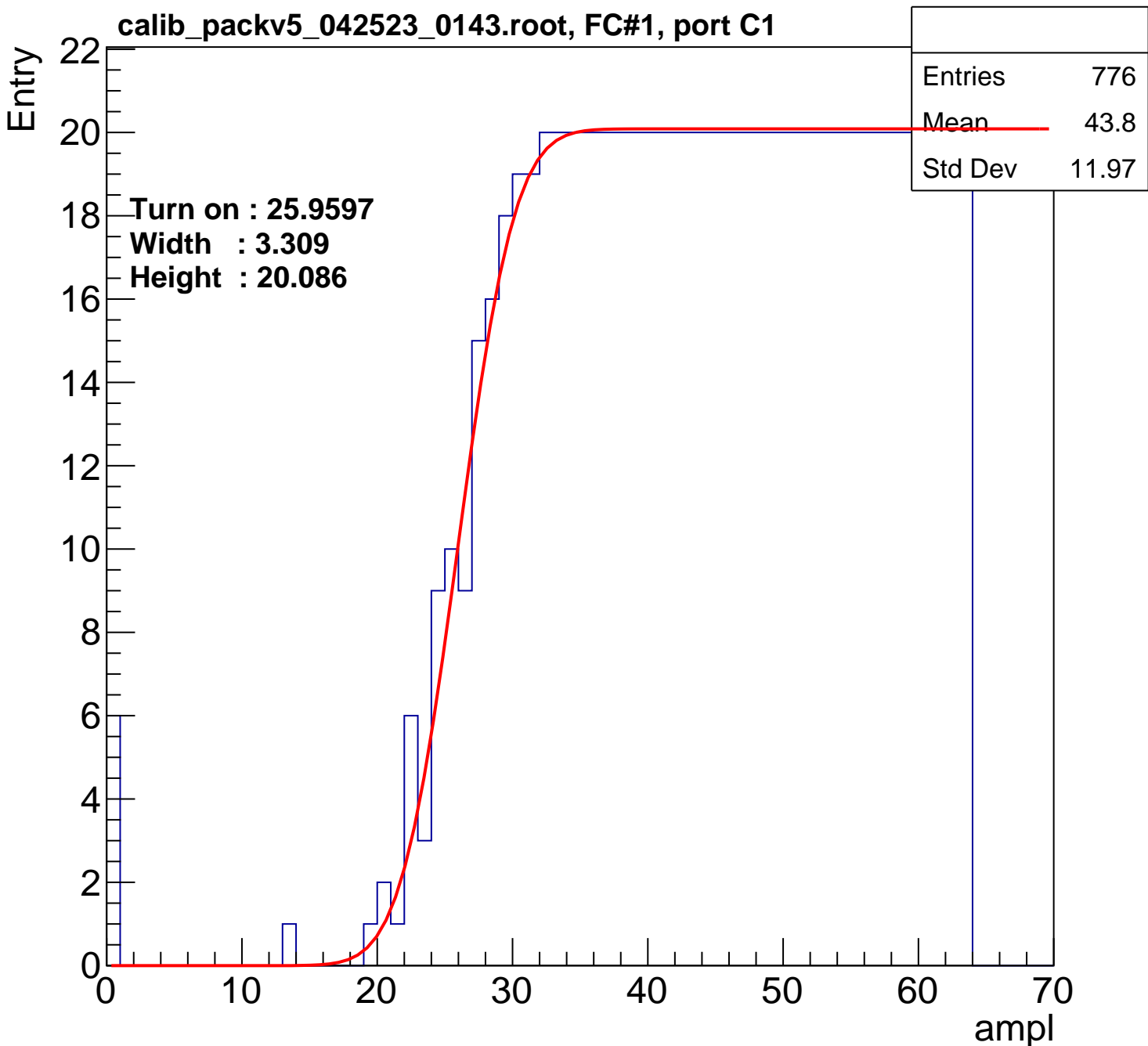
22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9597
Width : 3.309
Height : 20.086

Entries	776
Mean	43.8
Std Dev	11.97

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch1

calib_packv5_042523_0143.root, FC#1, port C1

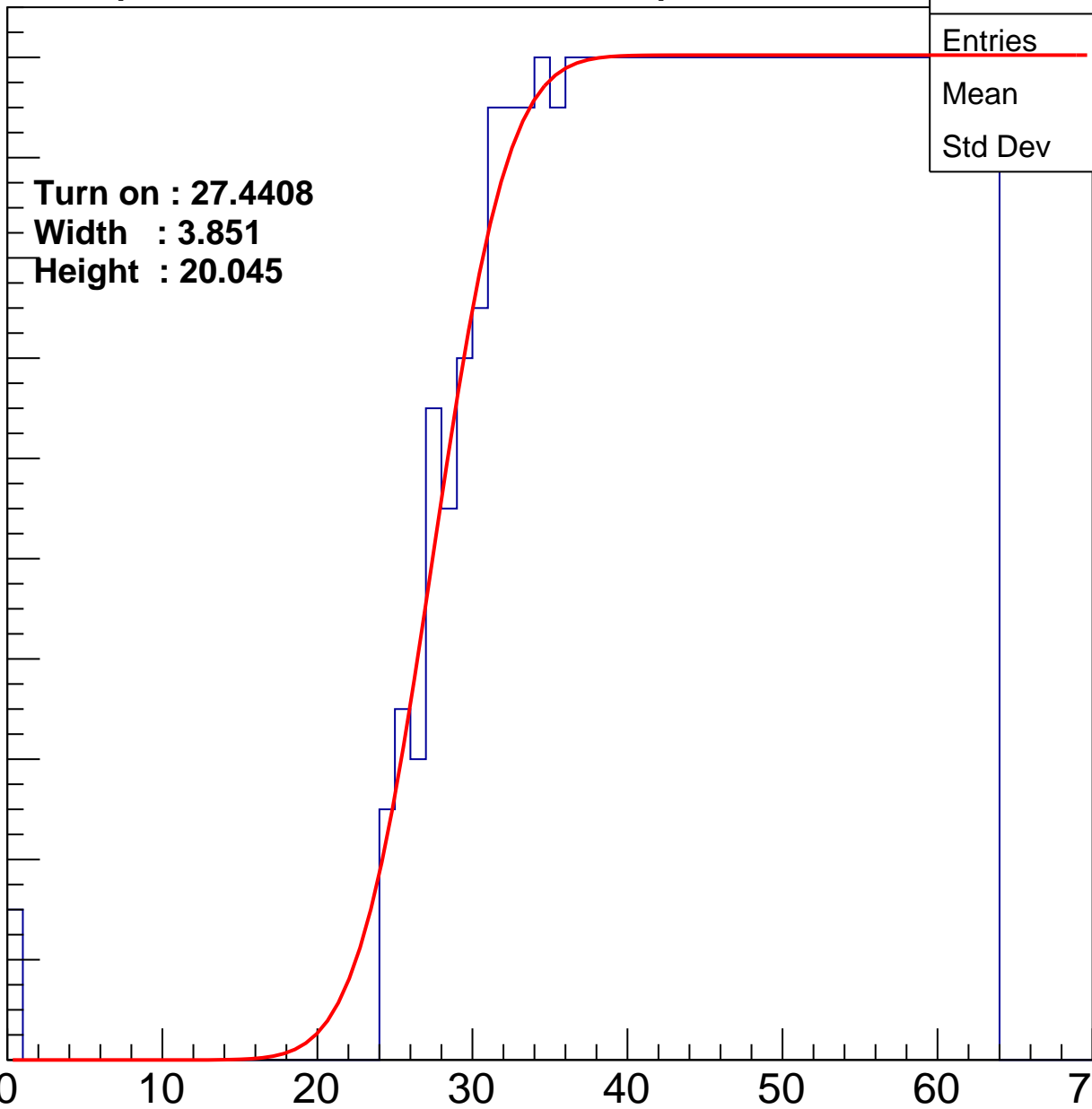
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4408
Width : 3.851
Height : 20.045

Entries	730
Mean	45.01
Std Dev	11.07

ampl



B0L101S, U24-ch2

calib_packv5_042523_0143.root, FC#1, port C1

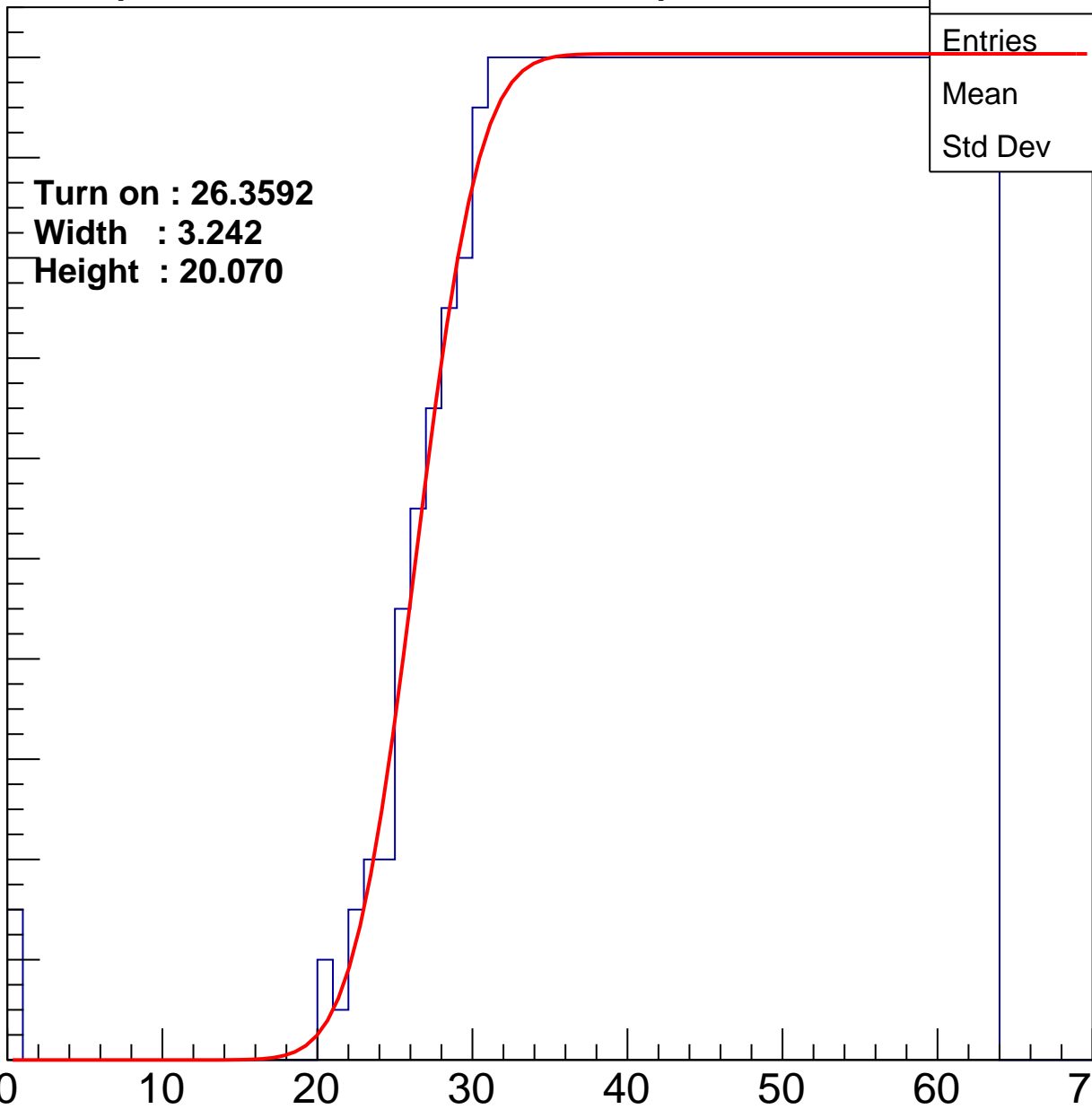
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3592
Width : 3.242
Height : 20.070

Entries	760
Mean	44.28
Std Dev	11.46

ampl



B0L101S, U24-ch3

calib_packv5_042523_0143.root, FC#1, port C1

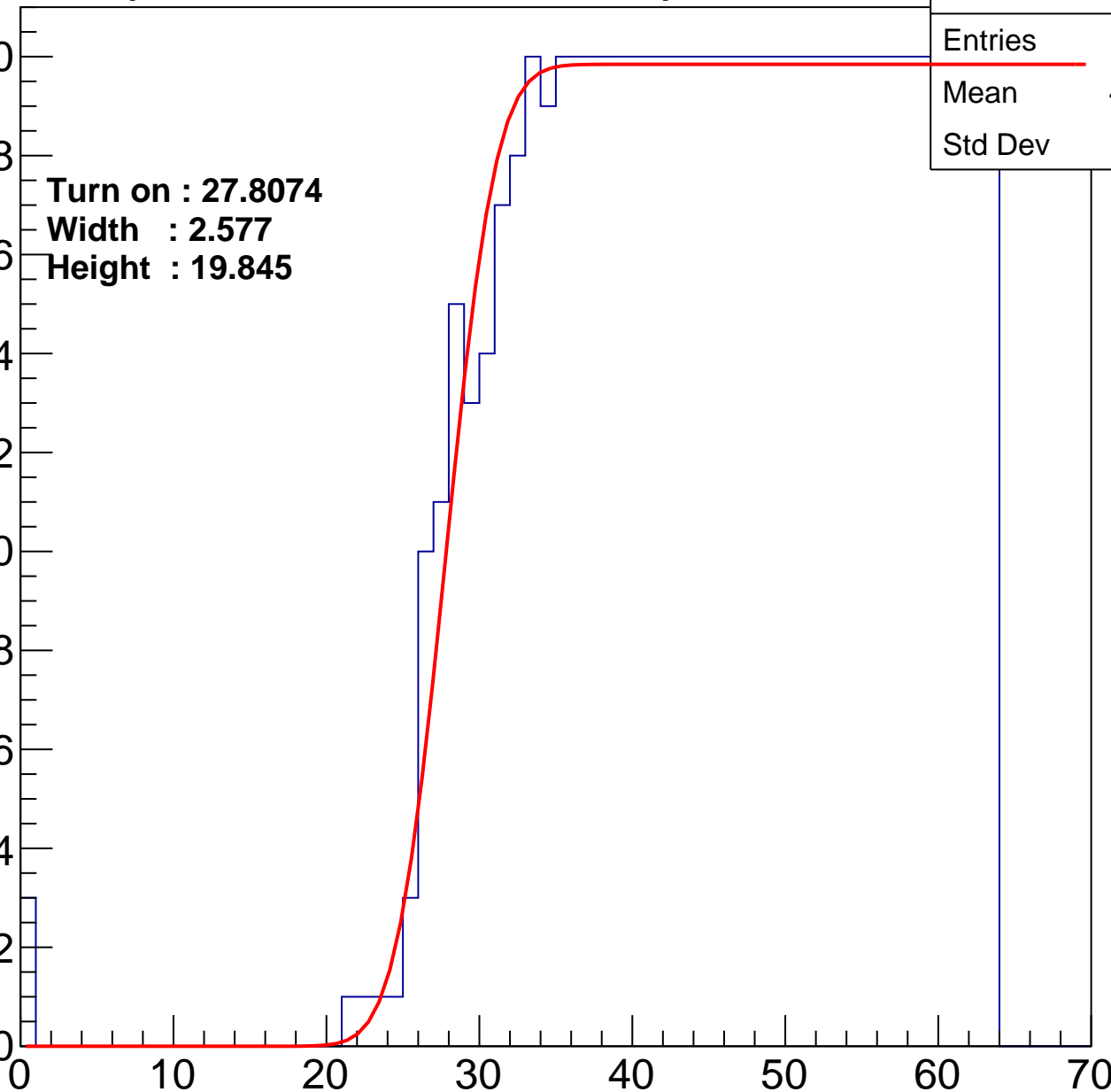
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8074
Width : 2.577
Height : 19.845

Entries	727
Mean	45.07
Std Dev	11.05

ampl



B0L101S, U24-ch4

calib_packv5_042523_0143.root, FC#1, port C1

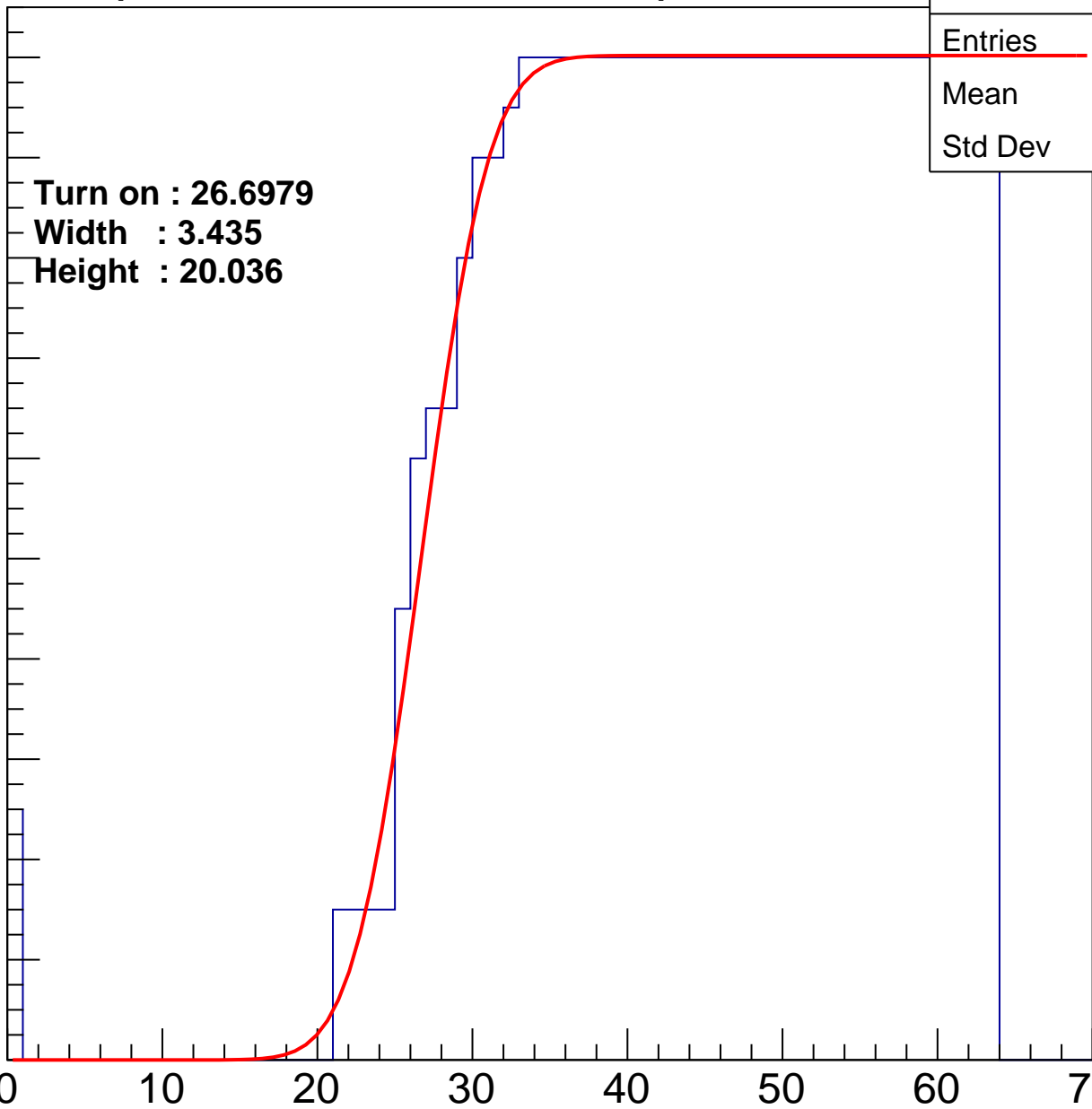
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6979
Width : 3.435
Height : 20.036

Entries	755
Mean	44.31
Std Dev	11.61

ampl



B0L101S, U24-ch5

calib_packv5_042523_0143.root, FC#1, port C1

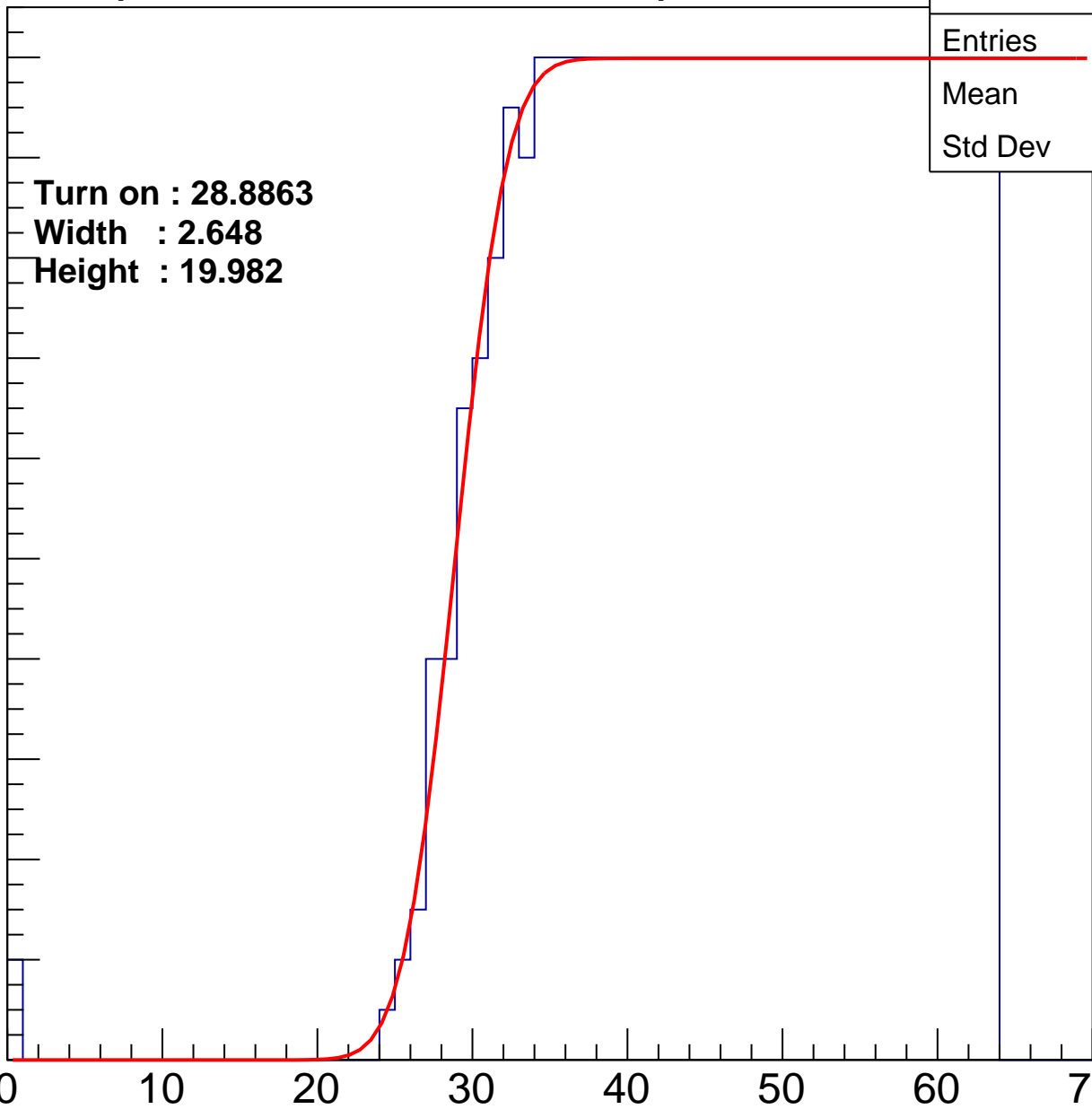
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.8863
Width : 2.648
Height : 19.982

Entries	704
Mean	45.72
Std Dev	10.57

ampl



B0L101S, U24-ch6

calib_packv5_042523_0143.root, FC#1, port C1

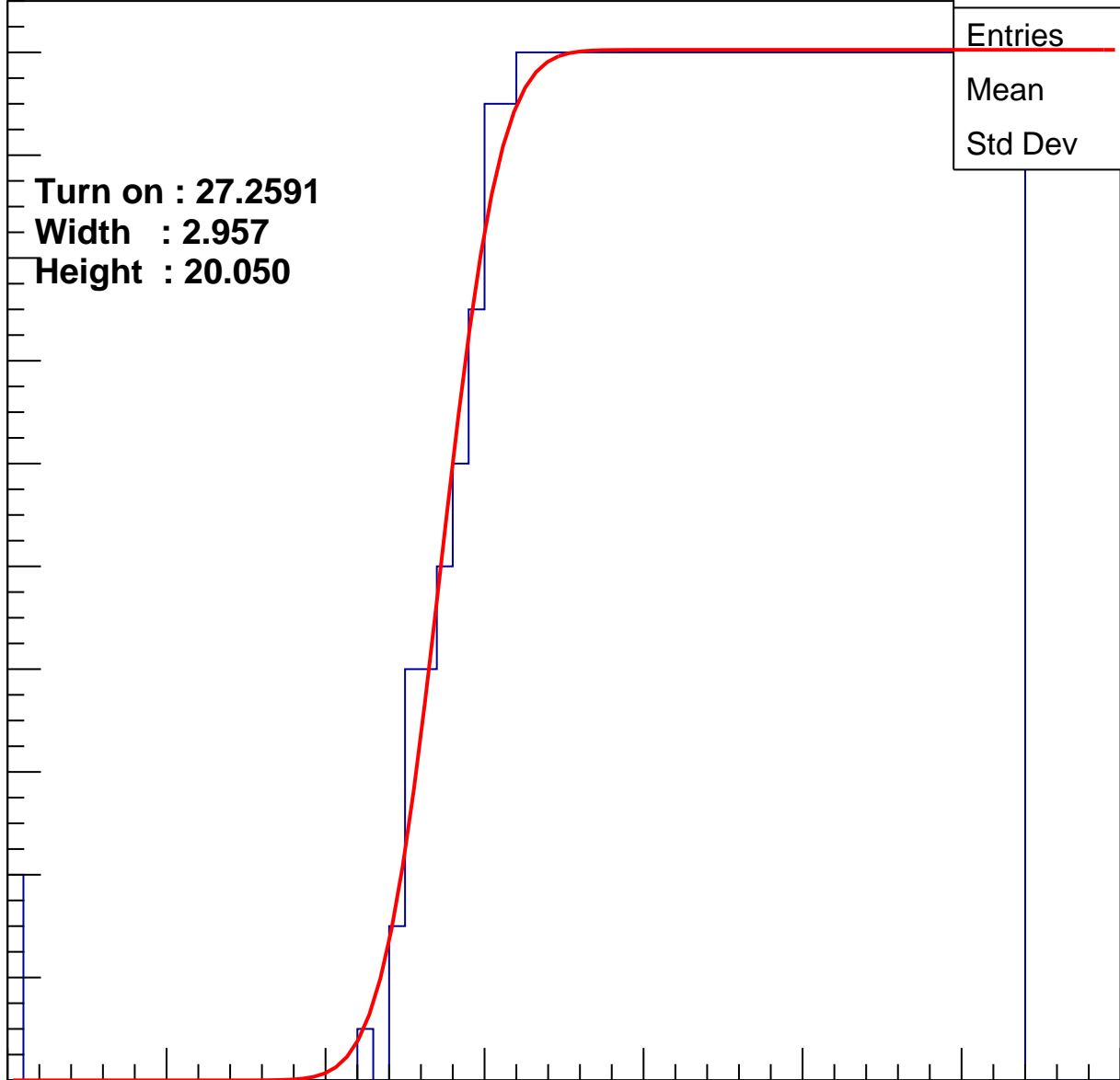
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2591
Width : 2.957
Height : 20.050

Entries	739
Mean	44.79
Std Dev	11.23

ampl



B0L101S, U24-ch7

calib_packv5_042523_0143.root, FC#1, port C1

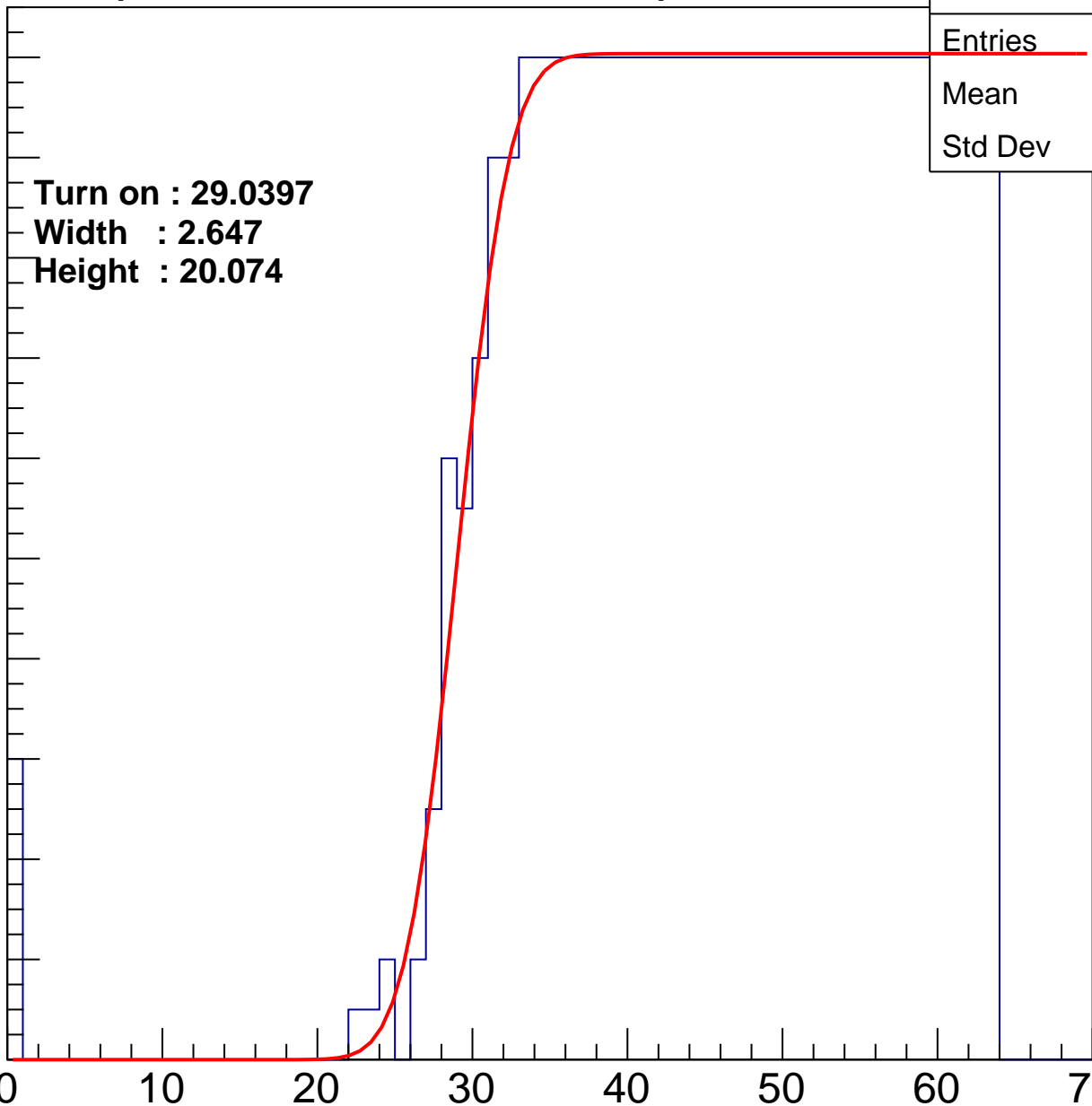
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.0397
Width : 2.647
Height : 20.074

Entries	710
Mean	45.42
Std Dev	11.1

ampl



B0L101S, U24-ch8

calib_packv5_042523_0143.root, FC#1, port C1

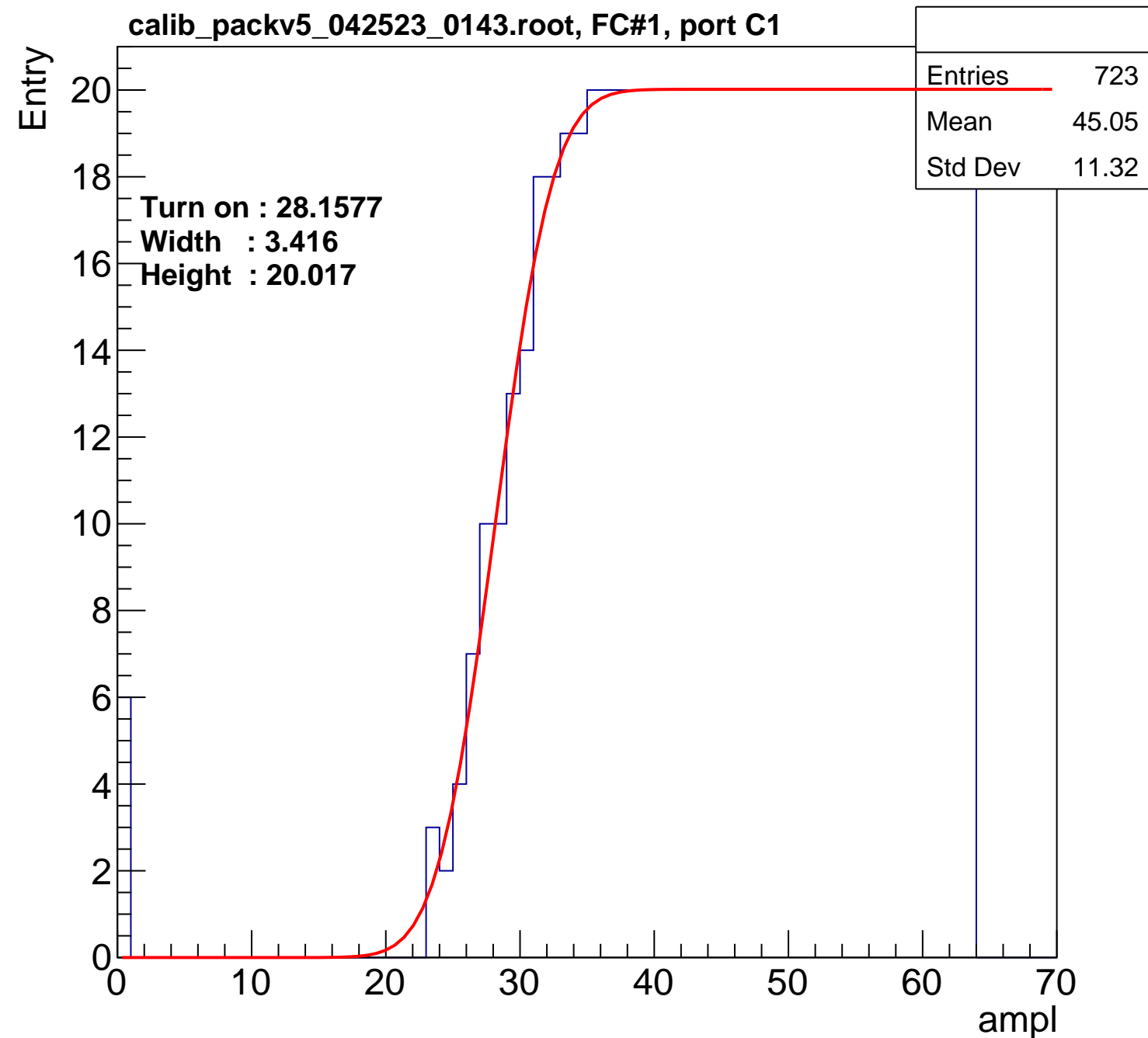
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1577
Width : 3.416
Height : 20.017

Entries	723
Mean	45.05
Std Dev	11.32

ampl



B0L101S, U24-ch9

calib_packv5_042523_0143.root, FC#1, port C1

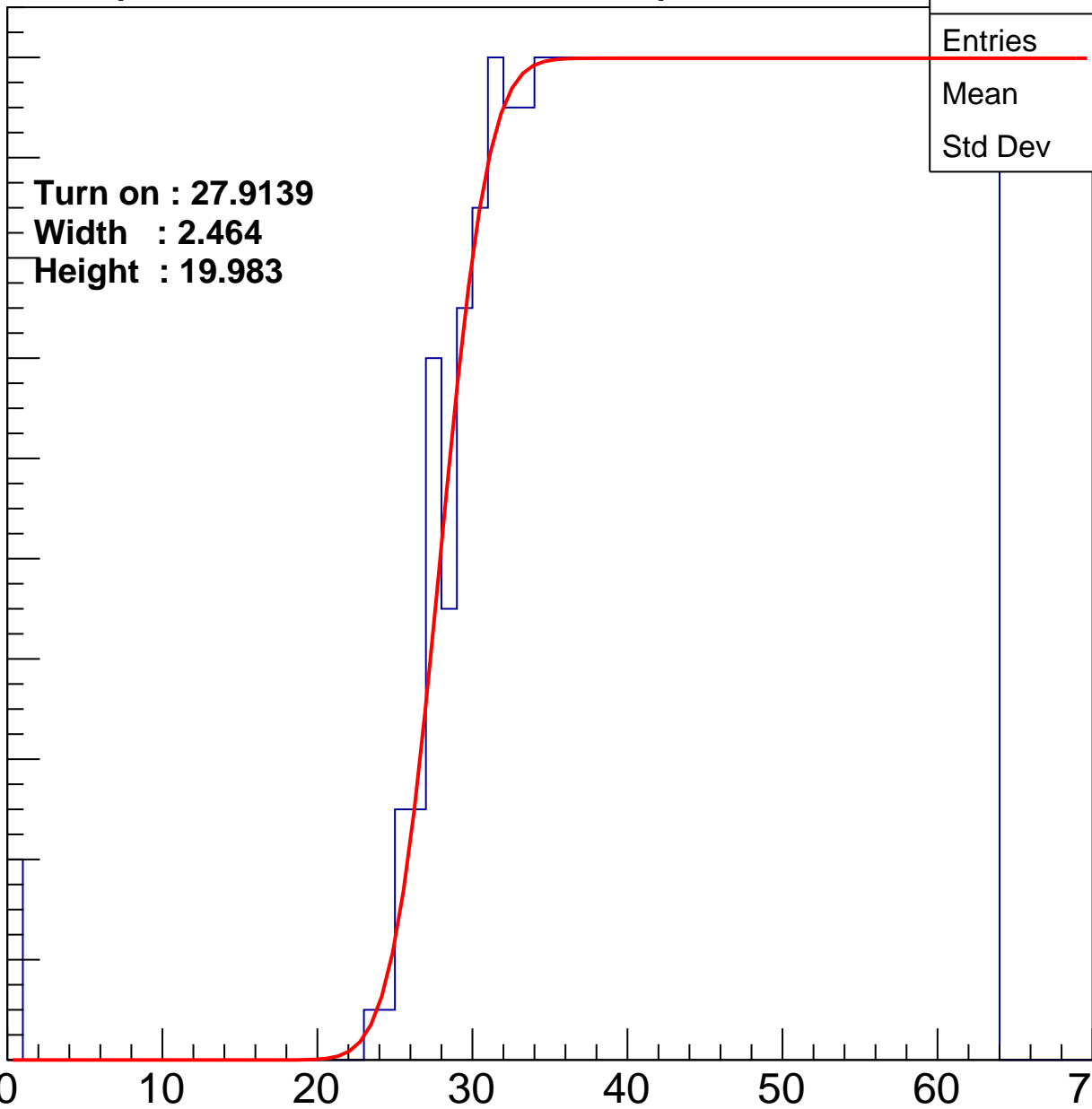
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9139
Width : 2.464
Height : 19.983

Entries	729
Mean	45.04
Std Dev	11.1

ampl



B0L101S, U24-ch10

calib_packv5_042523_0143.root, FC#1, port C1

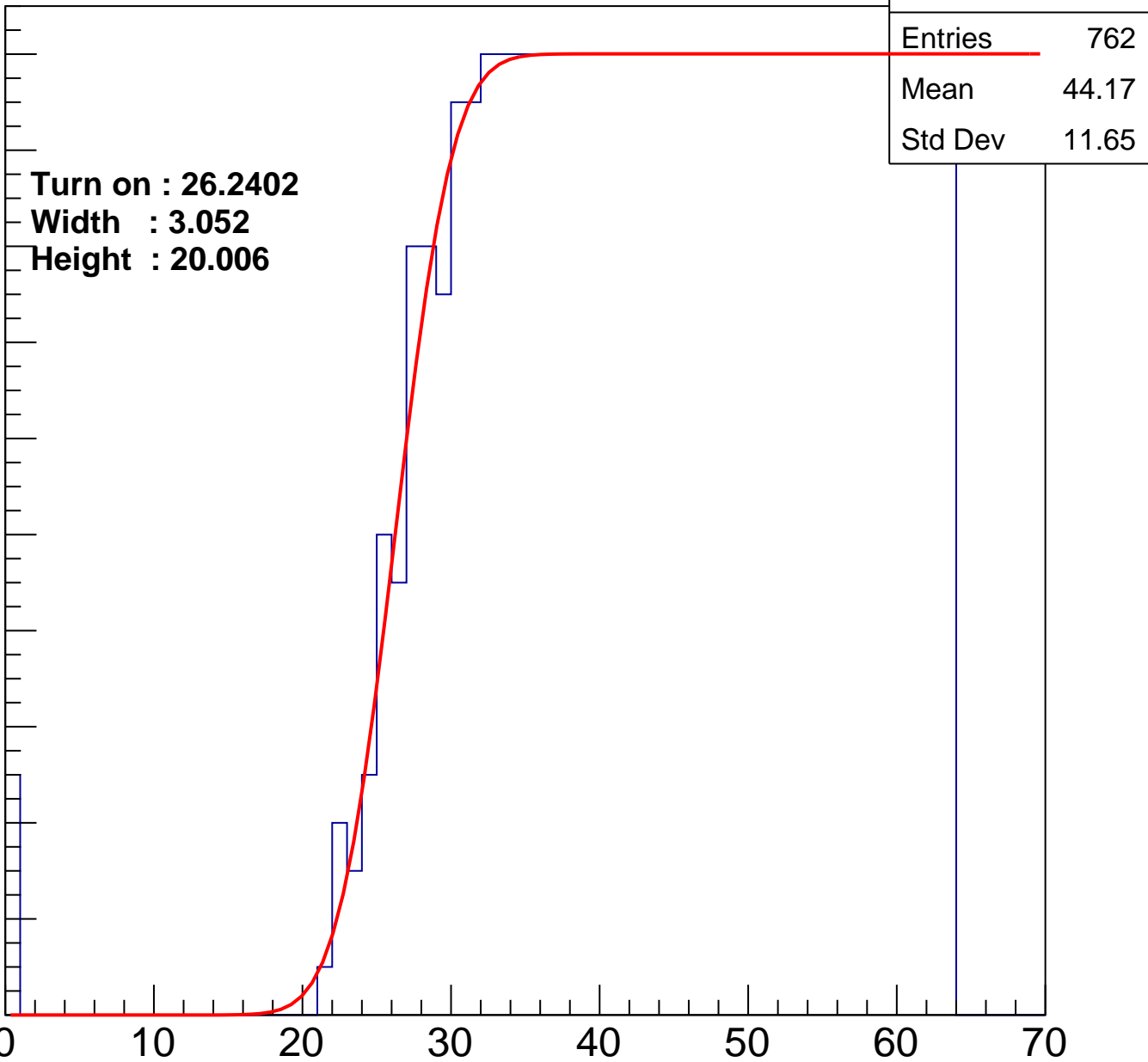
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2402
Width : 3.052
Height : 20.006

Entries	762
Mean	44.17
Std Dev	11.65

ampl



B0L101S, U24-ch11

calib_packv5_042523_0143.root, FC#1, port C1

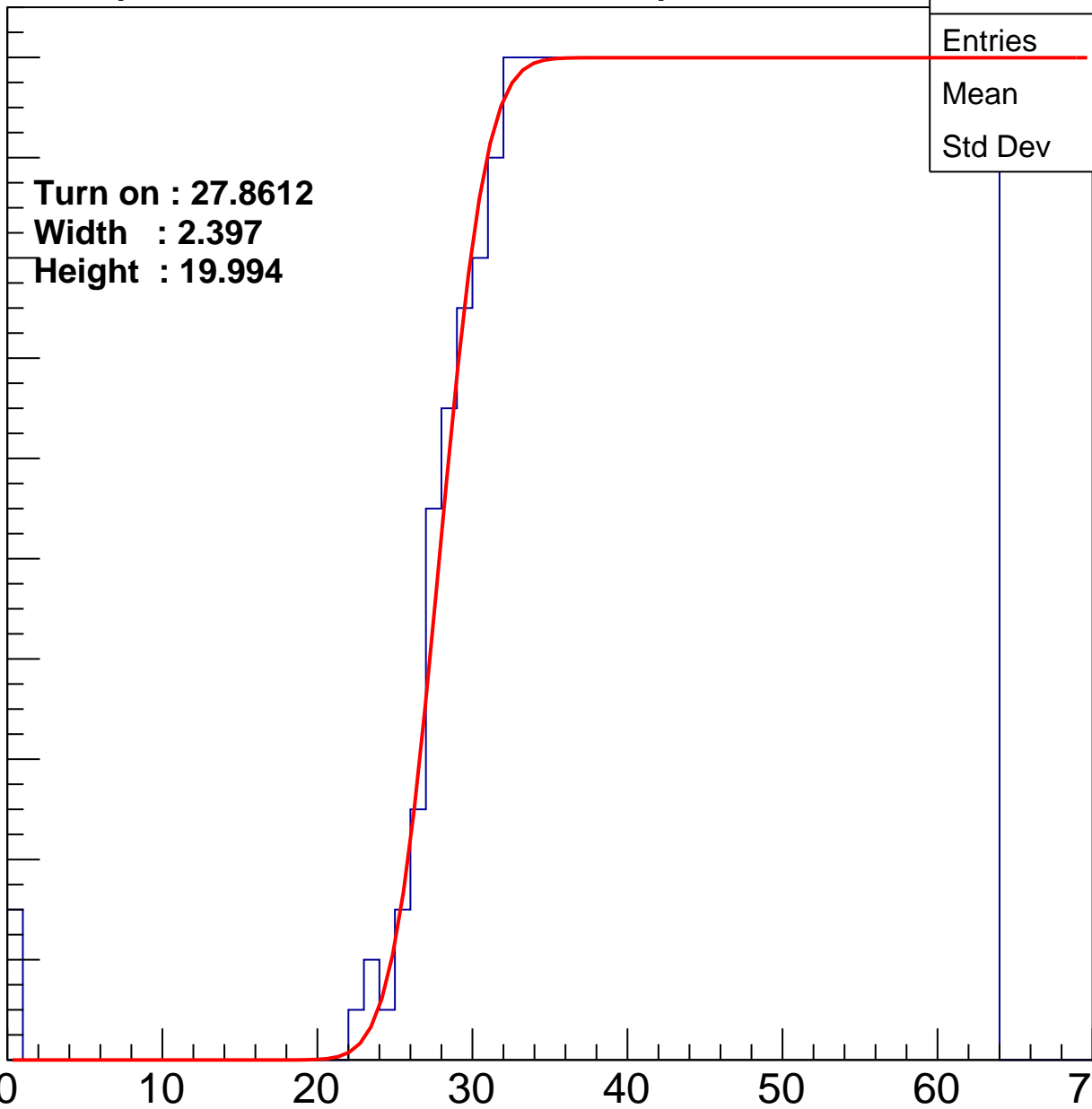
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8612
Width : 2.397
Height : 19.994

Entries	728
Mean	45.1
Std Dev	10.99

ampl



B0L101S, U24-ch12

calib_packv5_042523_0143.root, FC#1, port C1

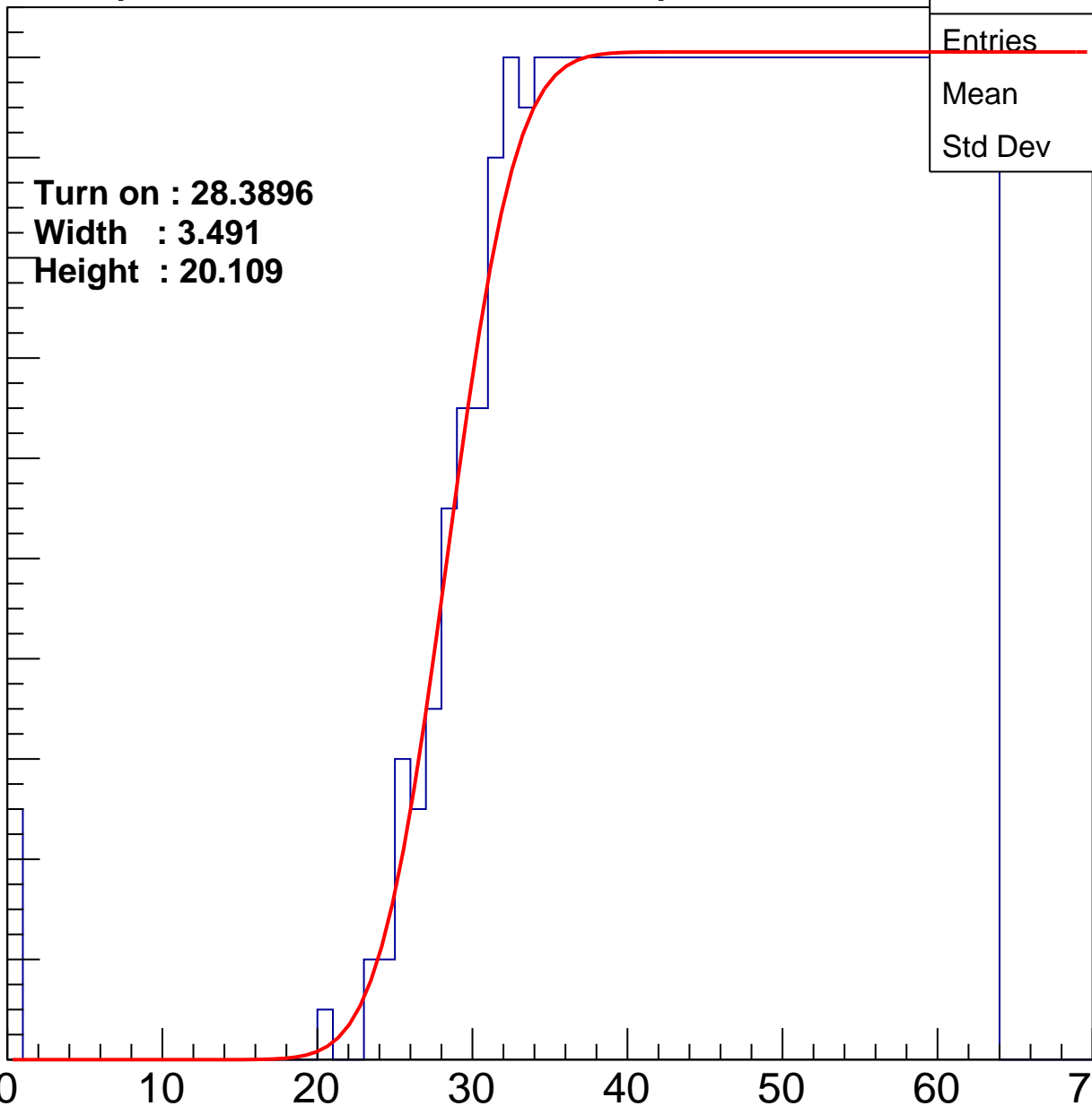
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3896
Width : 3.491
Height : 20.109

Entries	722
Mean	45.13
Std Dev	11.19

ampl



B0L101S, U24-ch13

calib_packv5_042523_0143.root, FC#1, port C1

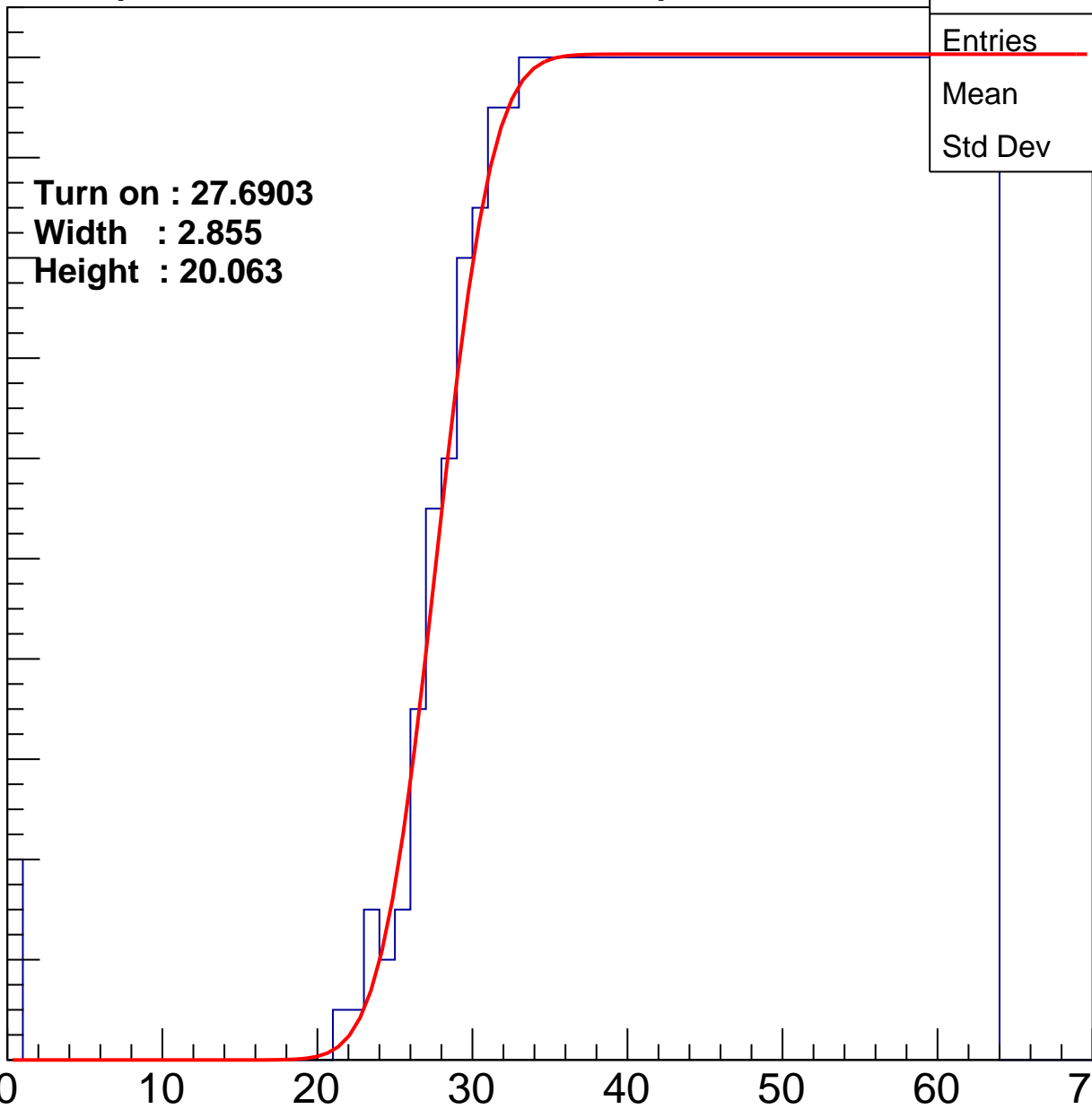
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6903
Width : 2.855
Height : 20.063

Entries	735
Mean	44.87
Std Dev	11.21

ampl



B0L101S, U24-ch14

calib_packv5_042523_0143.root, FC#1, port C1

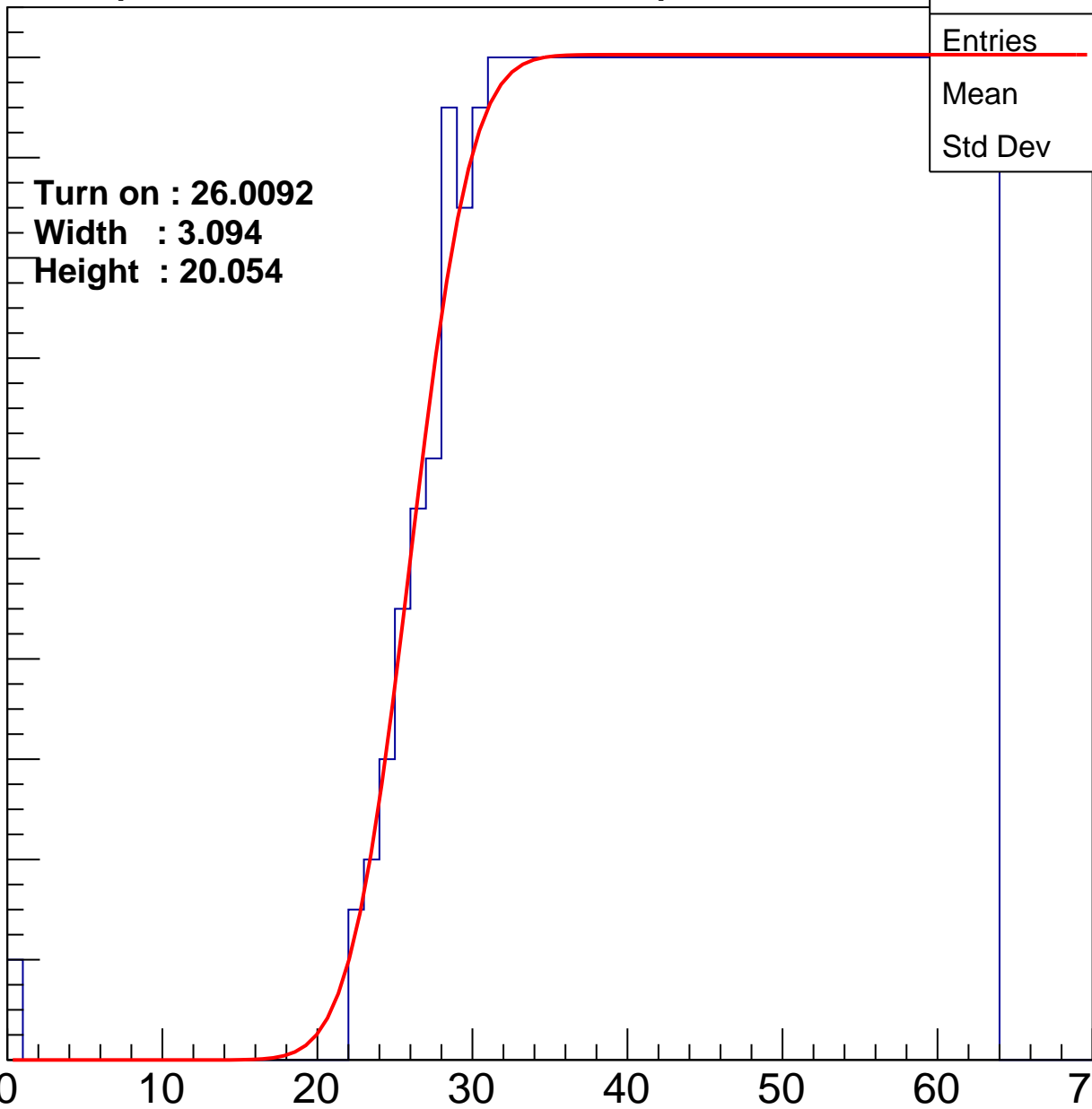
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0092
Width : 3.094
Height : 20.054

Entries	762
Mean	44.29
Std Dev	11.33

ampl



B0L101S, U24-ch15

calib_packv5_042523_0143.root, FC#1, port C1

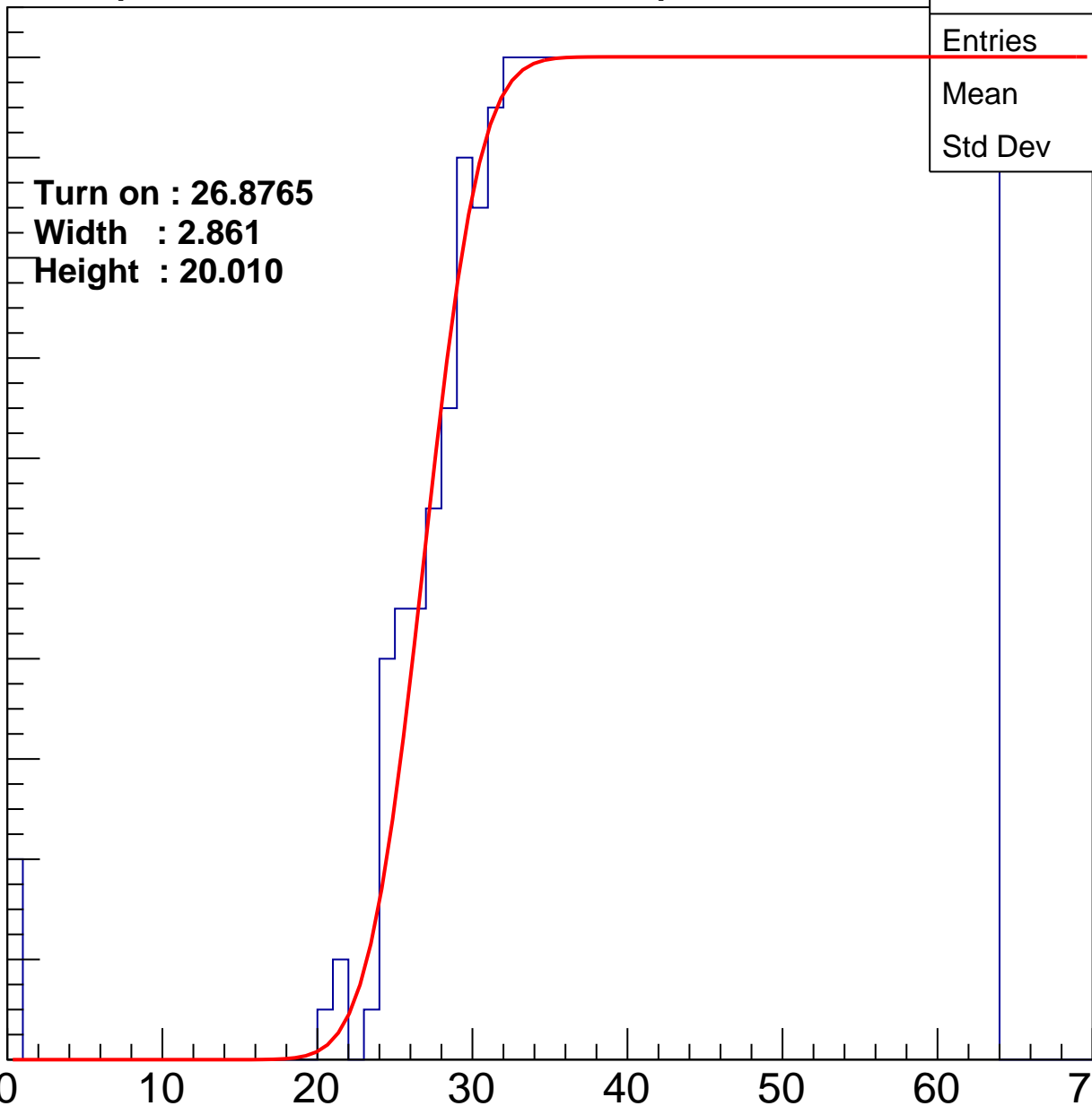
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8765
Width : 2.861
Height : 20.010

Entries	752
Mean	44.44
Std Dev	11.45

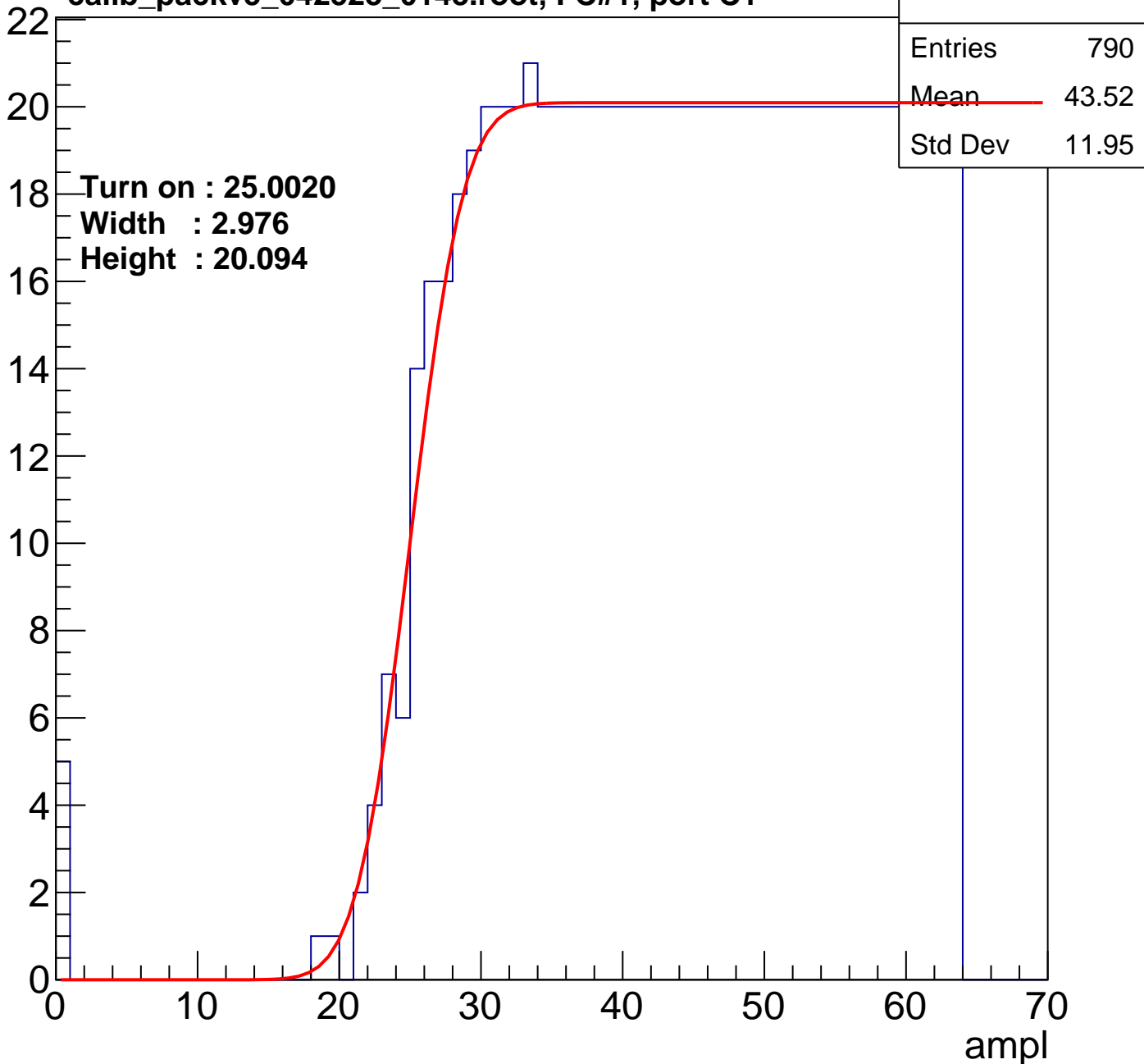
ampl



B0L101S, U24-ch16

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch17

calib_packv5_042523_0143.root, FC#1, port C1

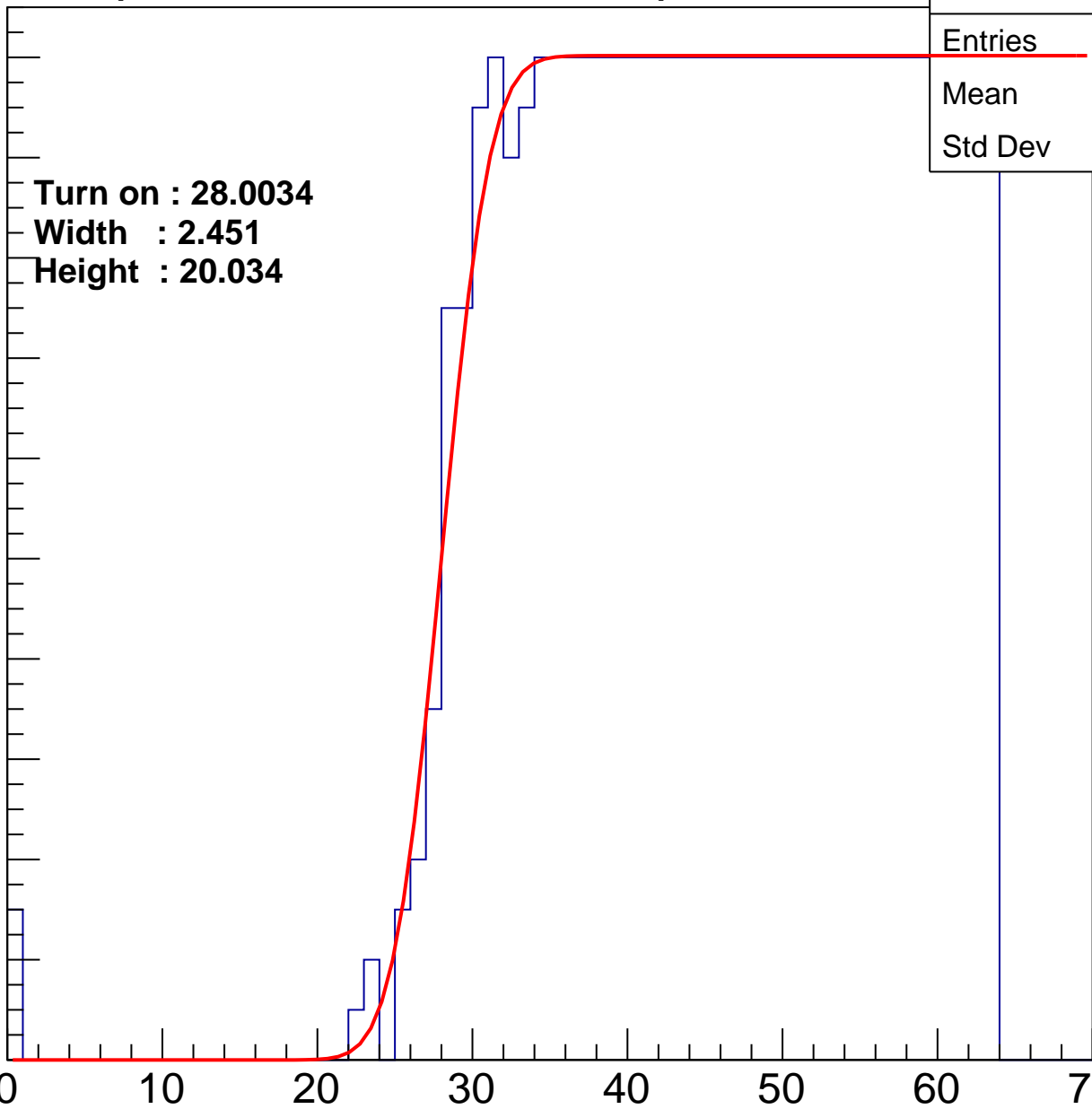
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0034
Width : 2.451
Height : 20.034

Entries	726
Mean	45.16
Std Dev	10.94

ampl



B0L101S, U24-ch18

calib_packv5_042523_0143.root, FC#1, port C1

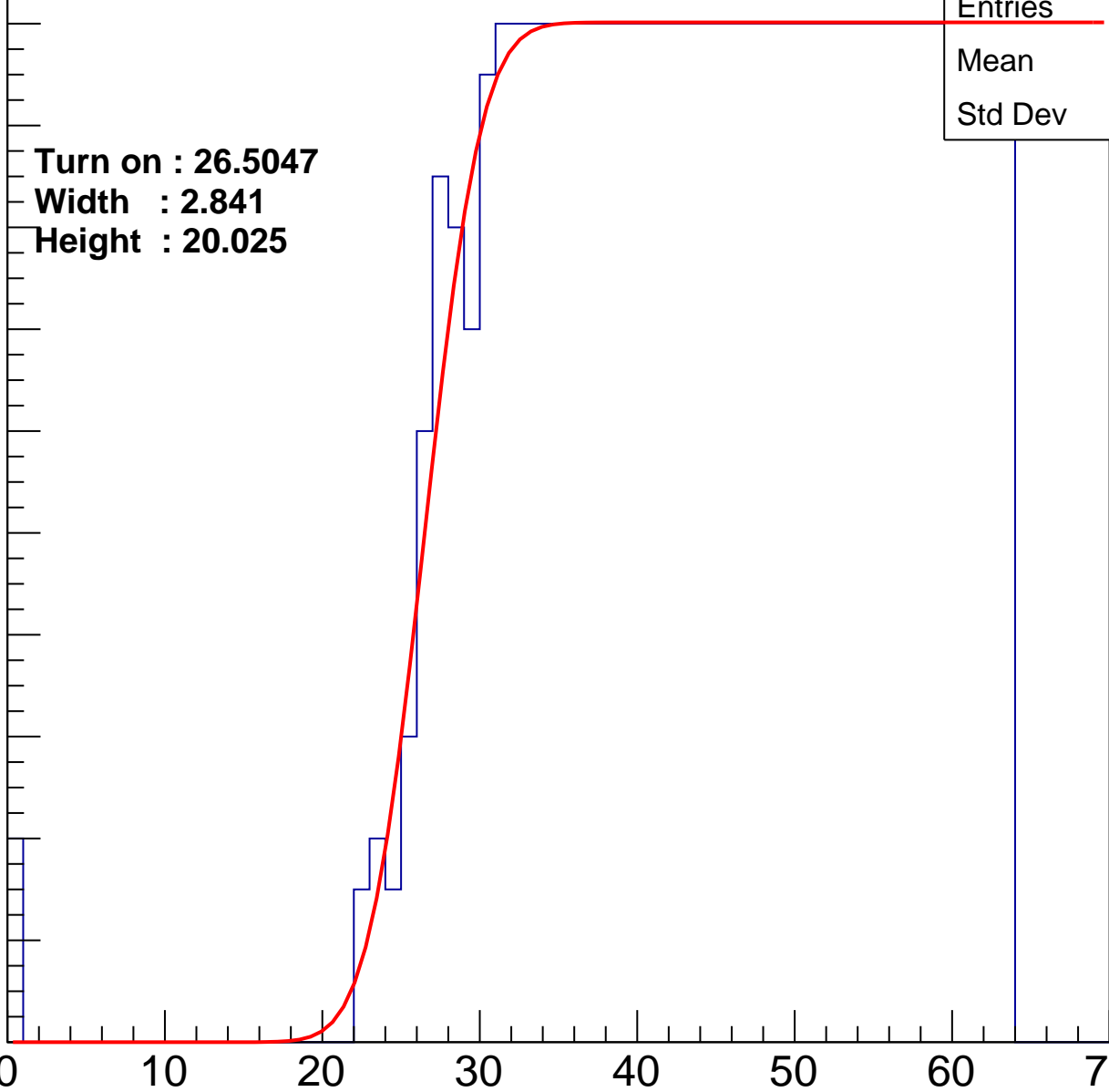
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5047
Width : 2.841
Height : 20.025

Entries	758
Mean	44.32
Std Dev	11.47

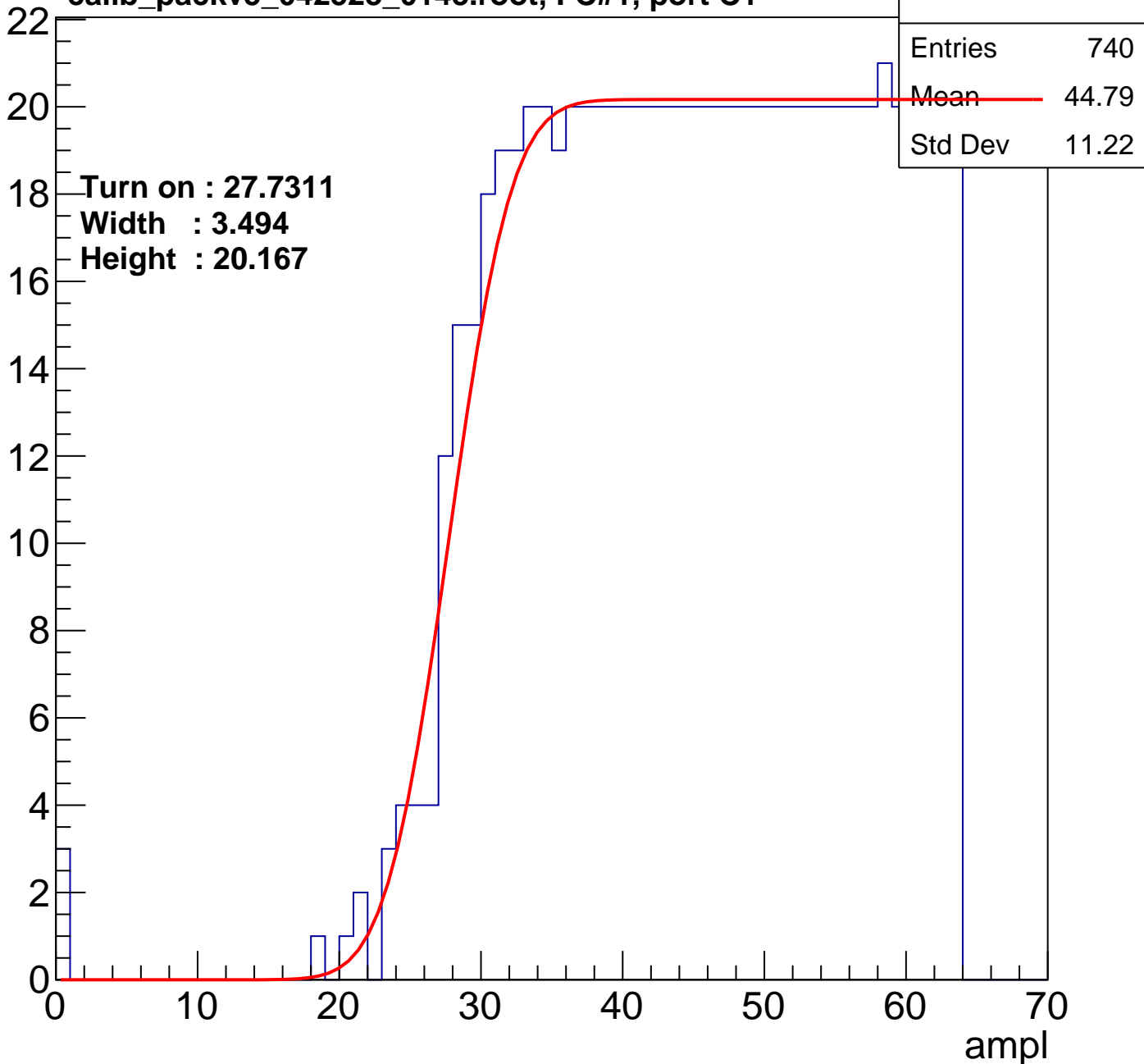
ampl



B0L101S, U24-ch19

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch20

calib_packv5_042523_0143.root, FC#1, port C1

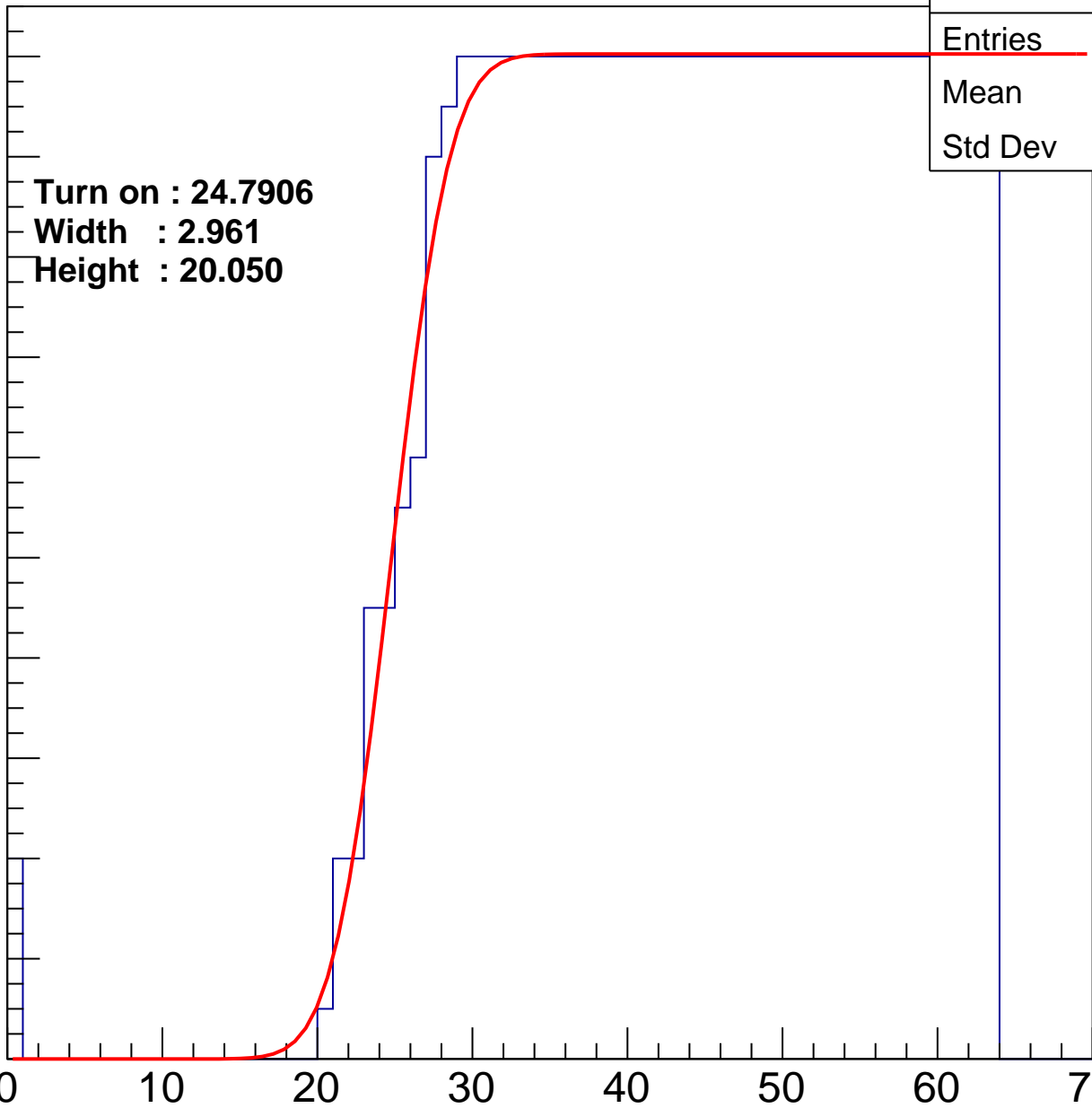
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7906
Width : 2.961
Height : 20.050

Entries	791
Mean	43.51
Std Dev	11.89

ampl



B0L101S, U24-ch21

calib_packv5_042523_0143.root, FC#1, port C1

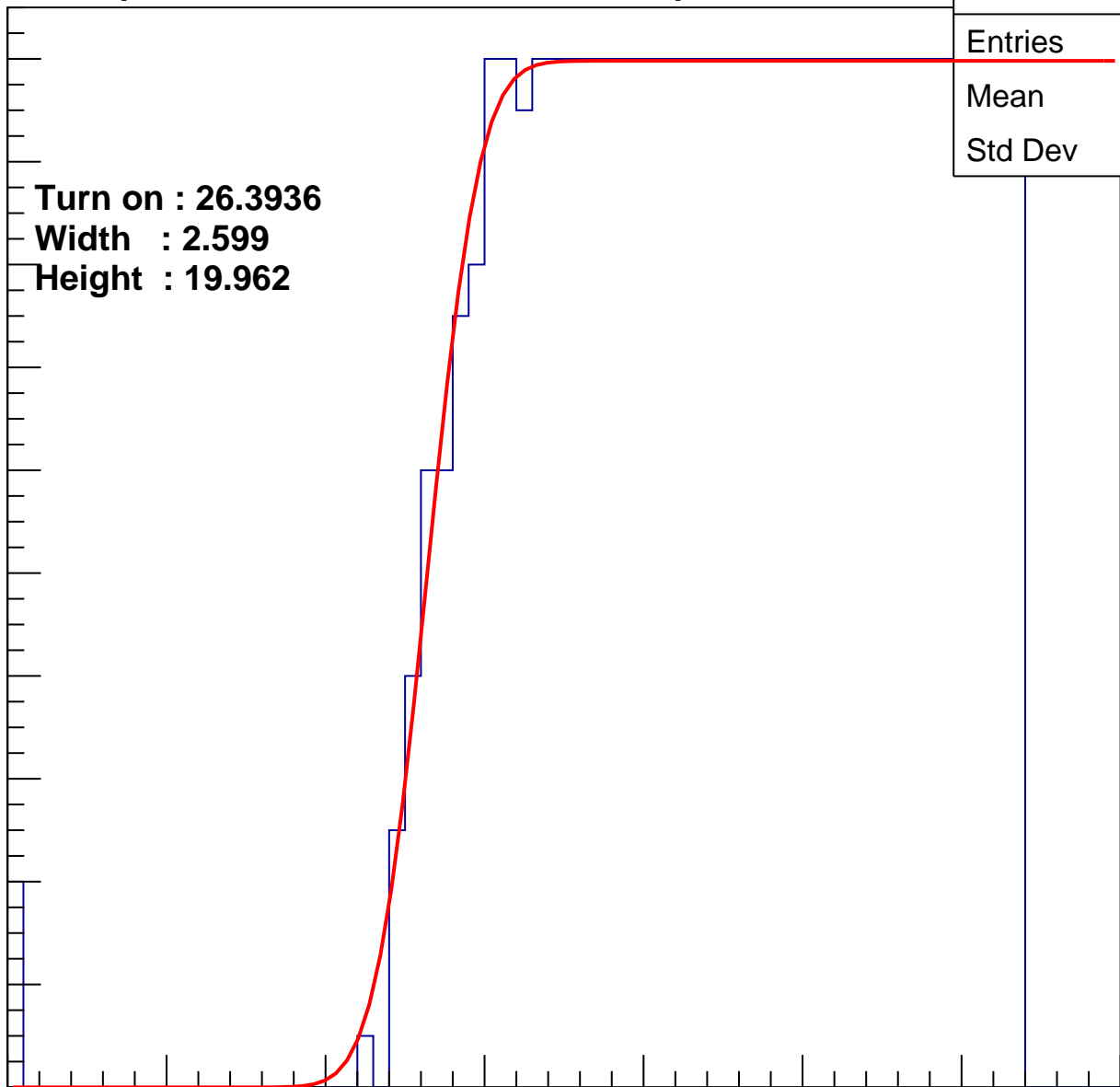
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3936
Width : 2.599
Height : 19.962

Entries	752
Mean	44.48
Std Dev	11.37

ampl



B0L101S, U24-ch22

calib_packv5_042523_0143.root, FC#1, port C1

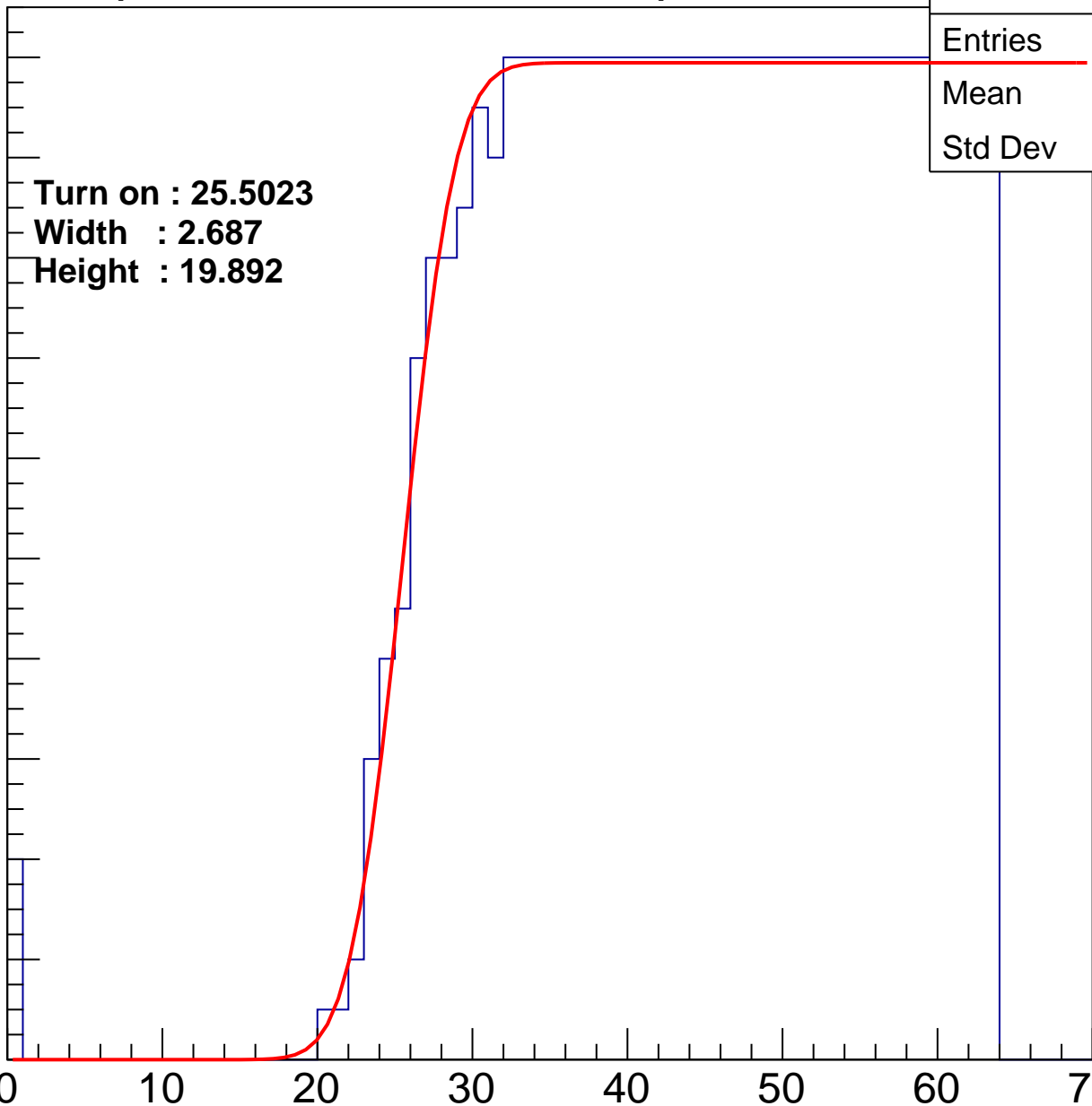
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5023
Width : 2.687
Height : 19.892

Entries	771
Mean	43.98
Std Dev	11.68

ampl



B0L101S, U24-ch23

calib_packv5_042523_0143.root, FC#1, port C1

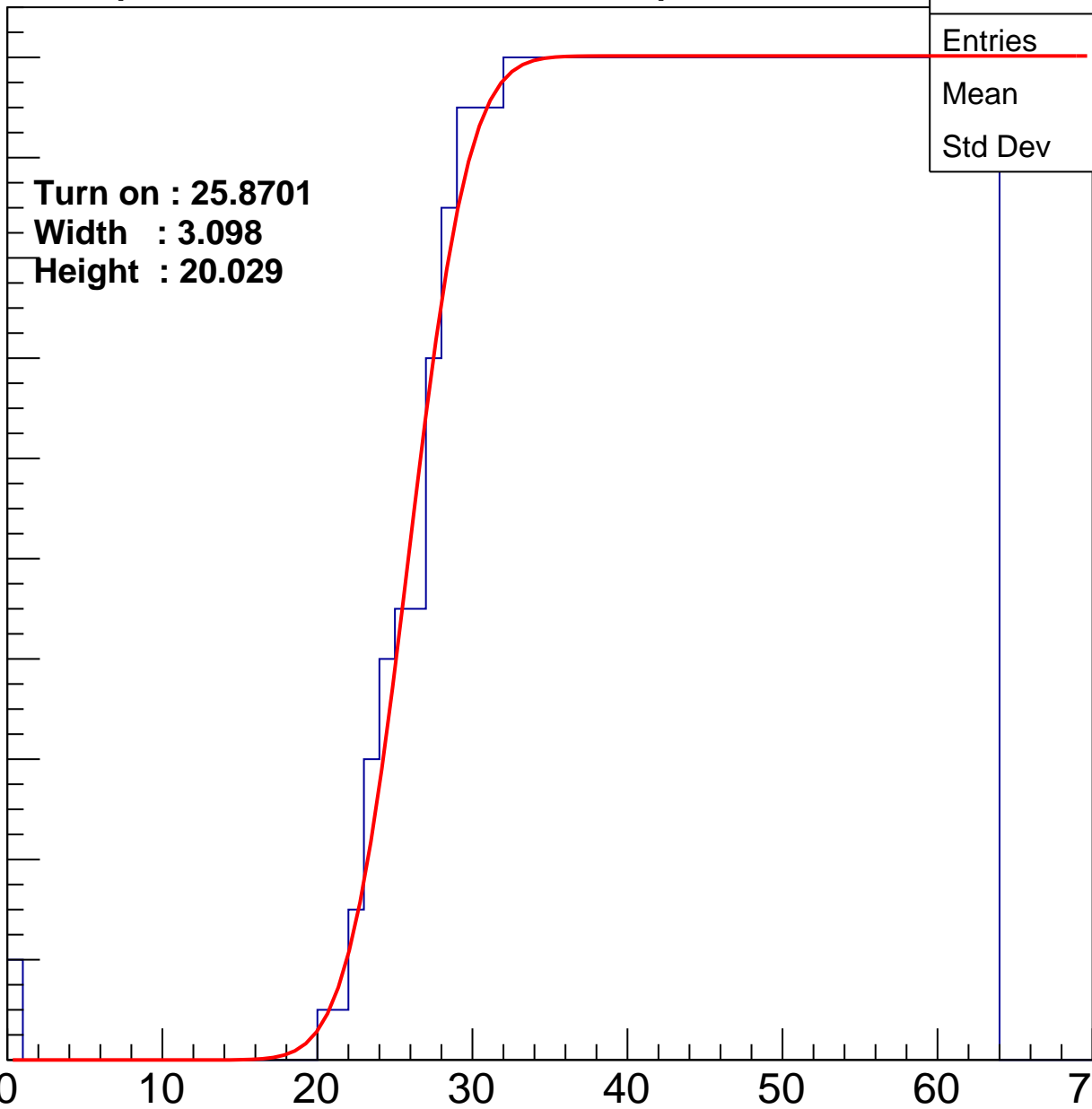
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8701
Width : 3.098
Height : 20.029

Entries	767
Mean	44.15
Std Dev	11.44

ampl



B0L101S, U24-ch24

calib_packv5_042523_0143.root, FC#1, port C1

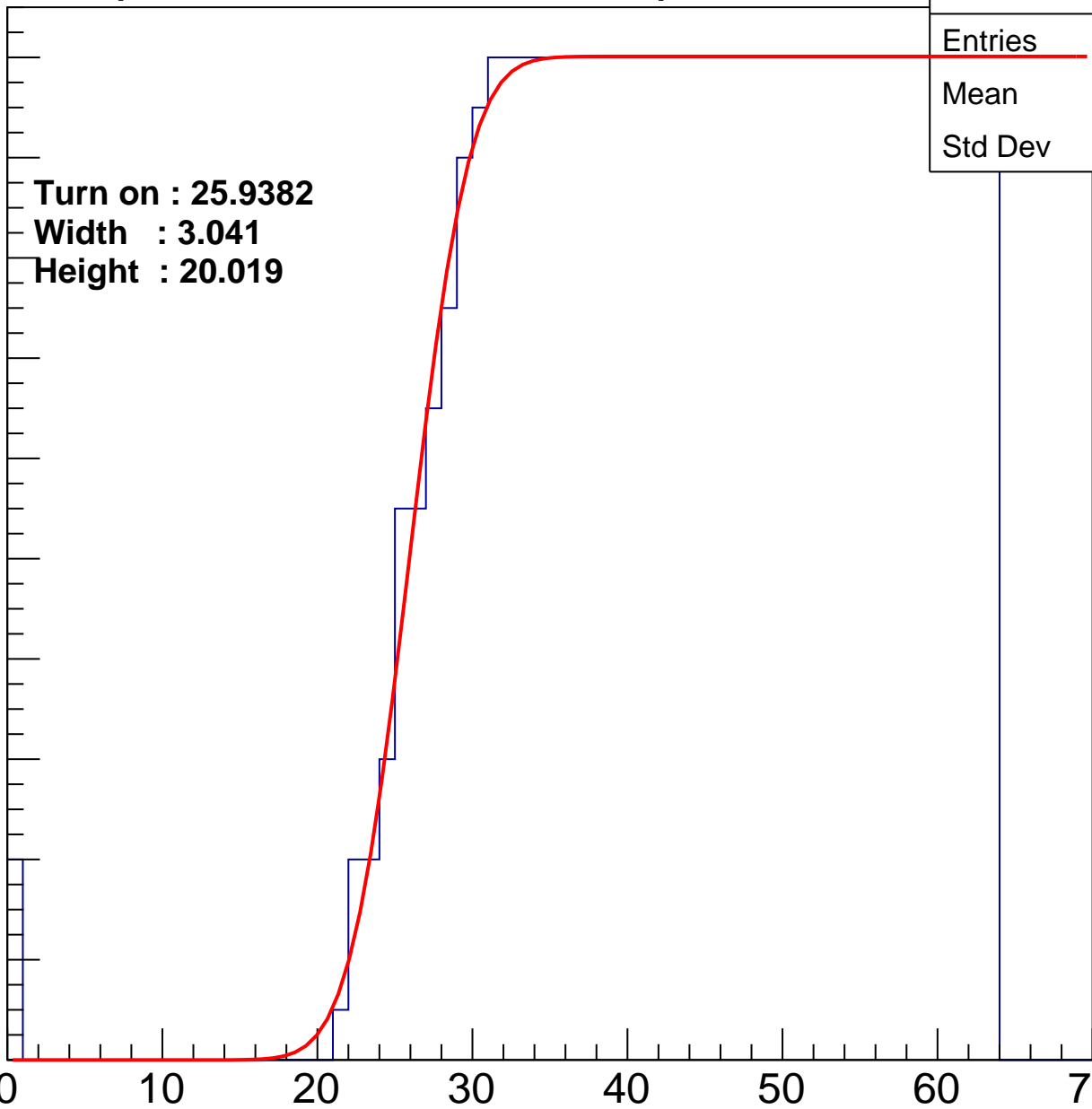
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9382
Width : 3.041
Height : 20.019

Entries	766
Mean	44.11
Std Dev	11.6

ampl



B0L101S, U24-ch25

calib_packv5_042523_0143.root, FC#1, port C1

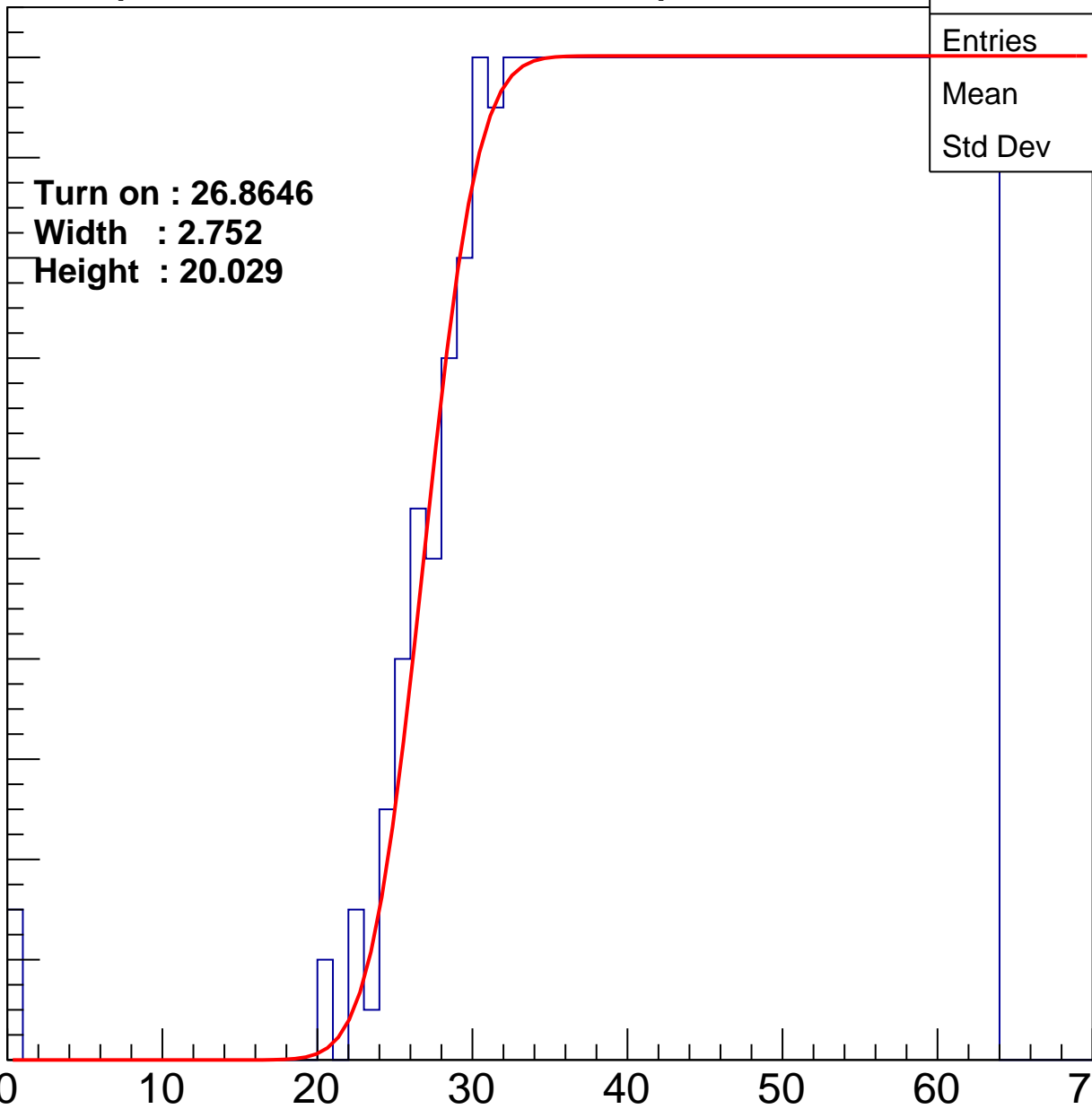
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8646
Width : 2.752
Height : 20.029

Entries	752
Mean	44.48
Std Dev	11.34

ampl



B0L101S, U24-ch26

calib_packv5_042523_0143.root, FC#1, port C1

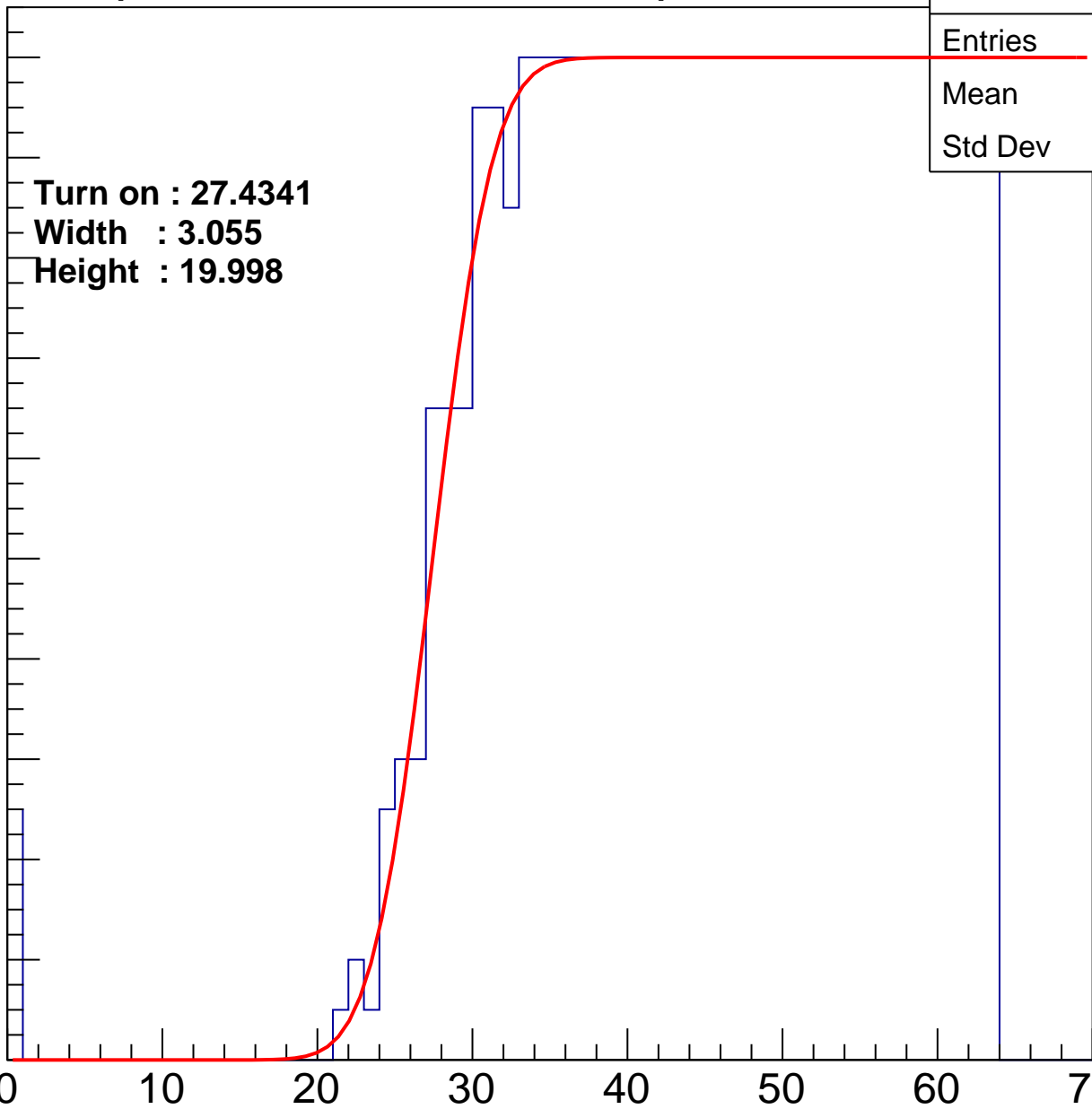
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4341
Width : 3.055
Height : 19.998

Entries	740
Mean	44.69
Std Dev	11.41

ampl



B0L101S, U24-ch27

calib_packv5_042523_0143.root, FC#1, port C1

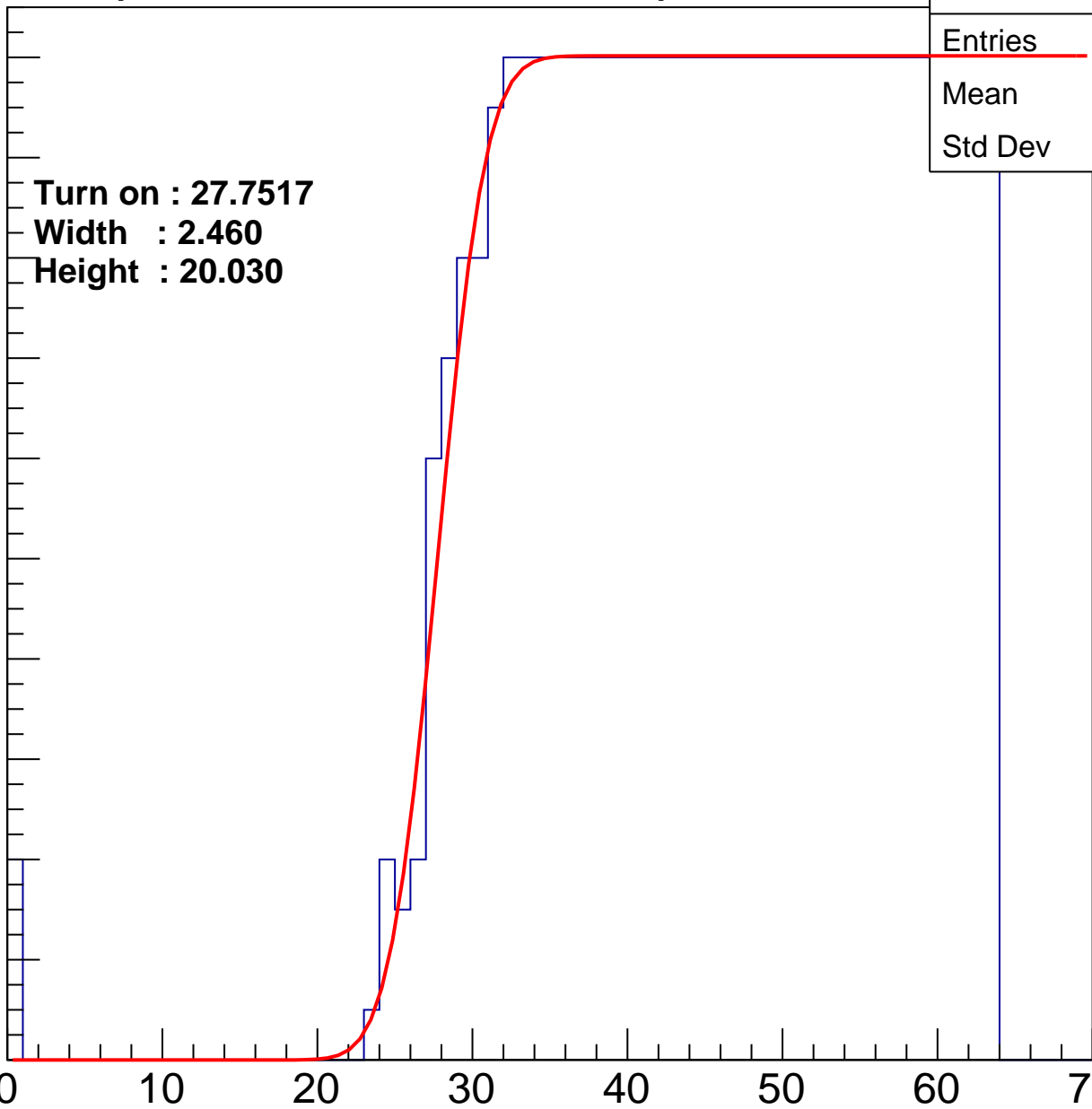
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7517
Width : 2.460
Height : 20.030

Entries	733
Mean	44.95
Std Dev	11.14

ampl



B0L101S, U24-ch28

calib_packv5_042523_0143.root, FC#1, port C1

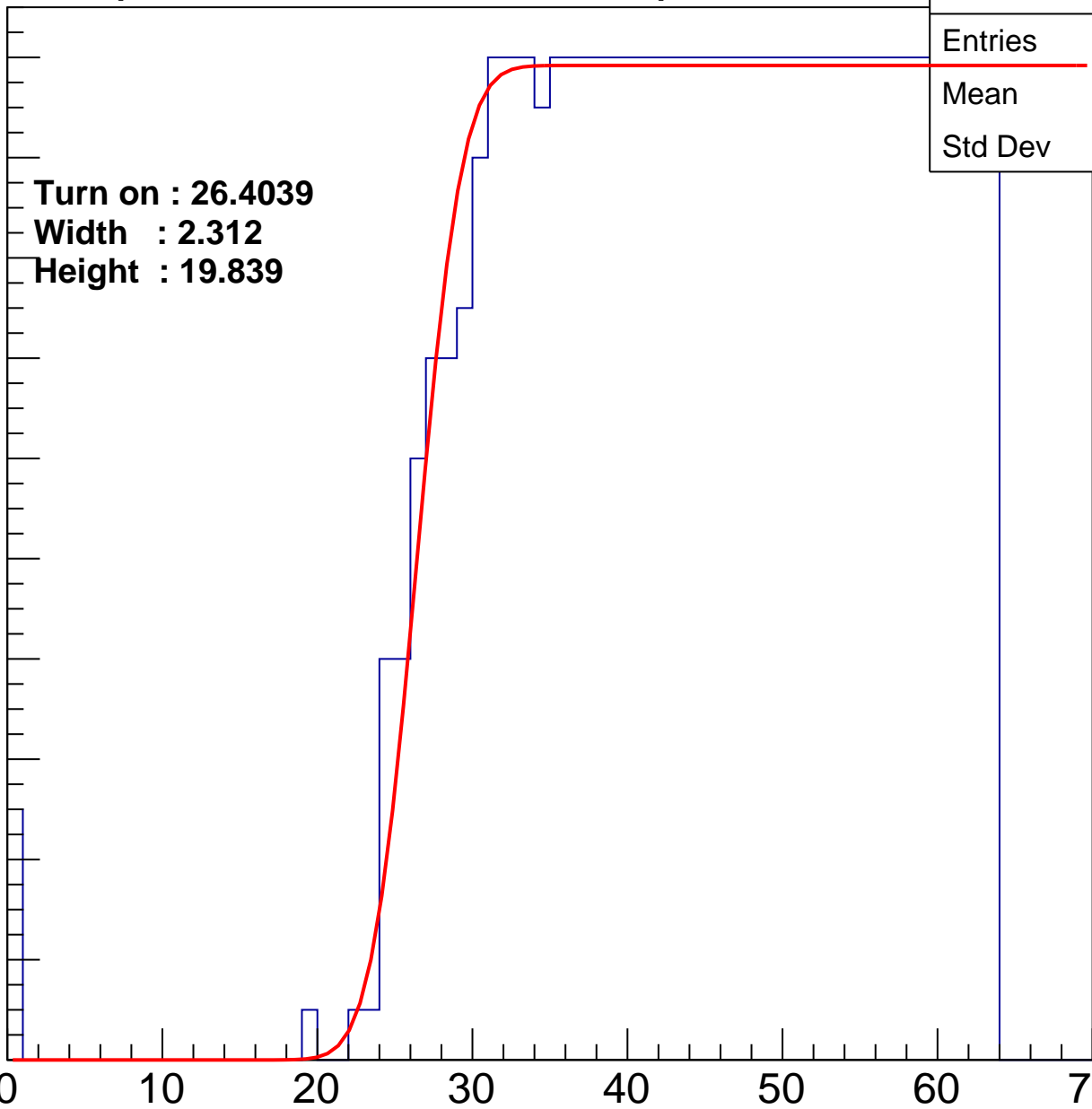
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4039
Width : 2.312
Height : 19.839

Entries	756
Mean	44.31
Std Dev	11.58

ampl



B0L101S, U24-ch29

calib_packv5_042523_0143.root, FC#1, port C1

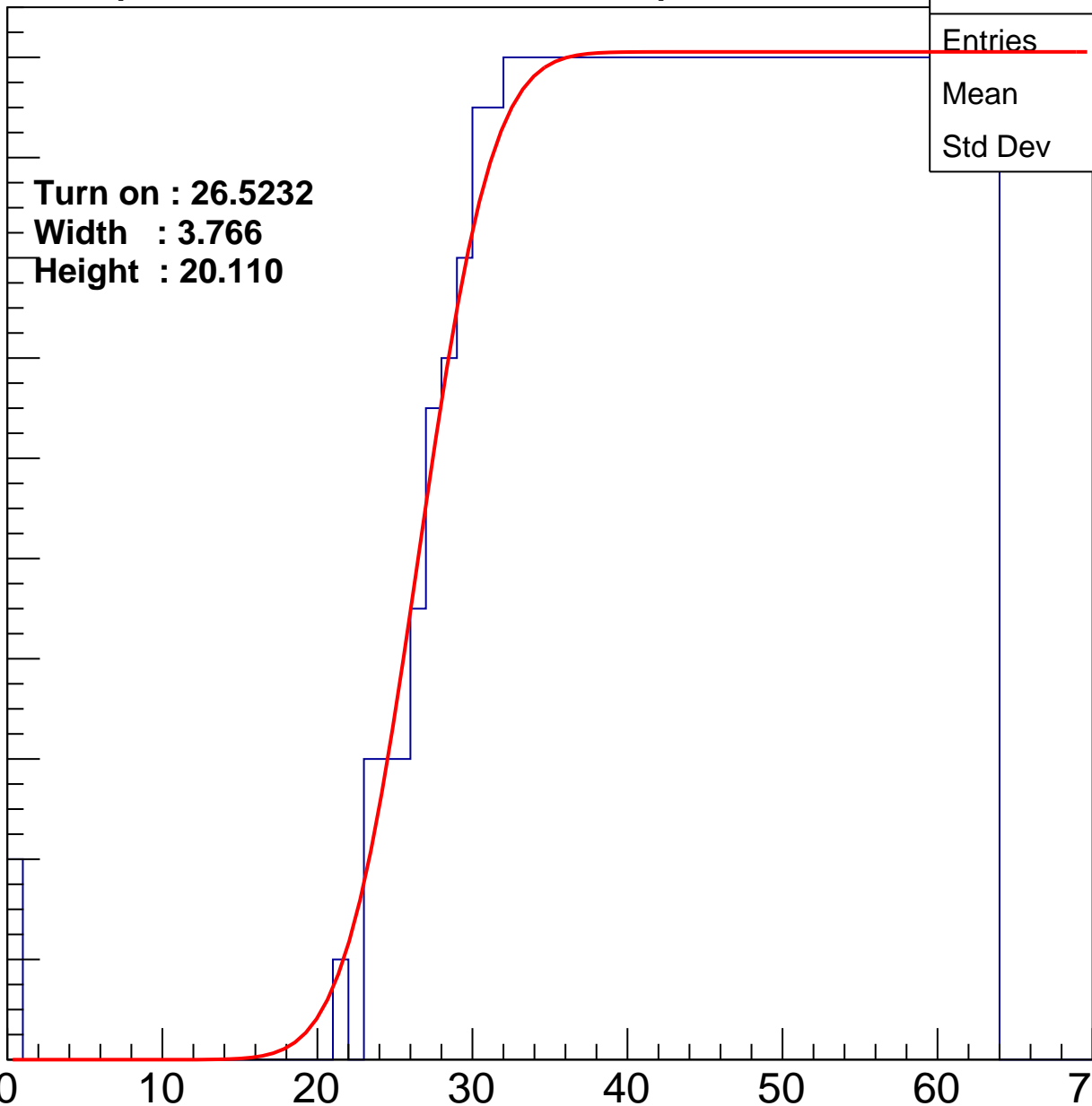
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5232
Width : 3.766
Height : 20.110

Entries	754
Mean	44.4
Std Dev	11.46

ampl



B0L101S, U24-ch30

calib_packv5_042523_0143.root, FC#1, port C1

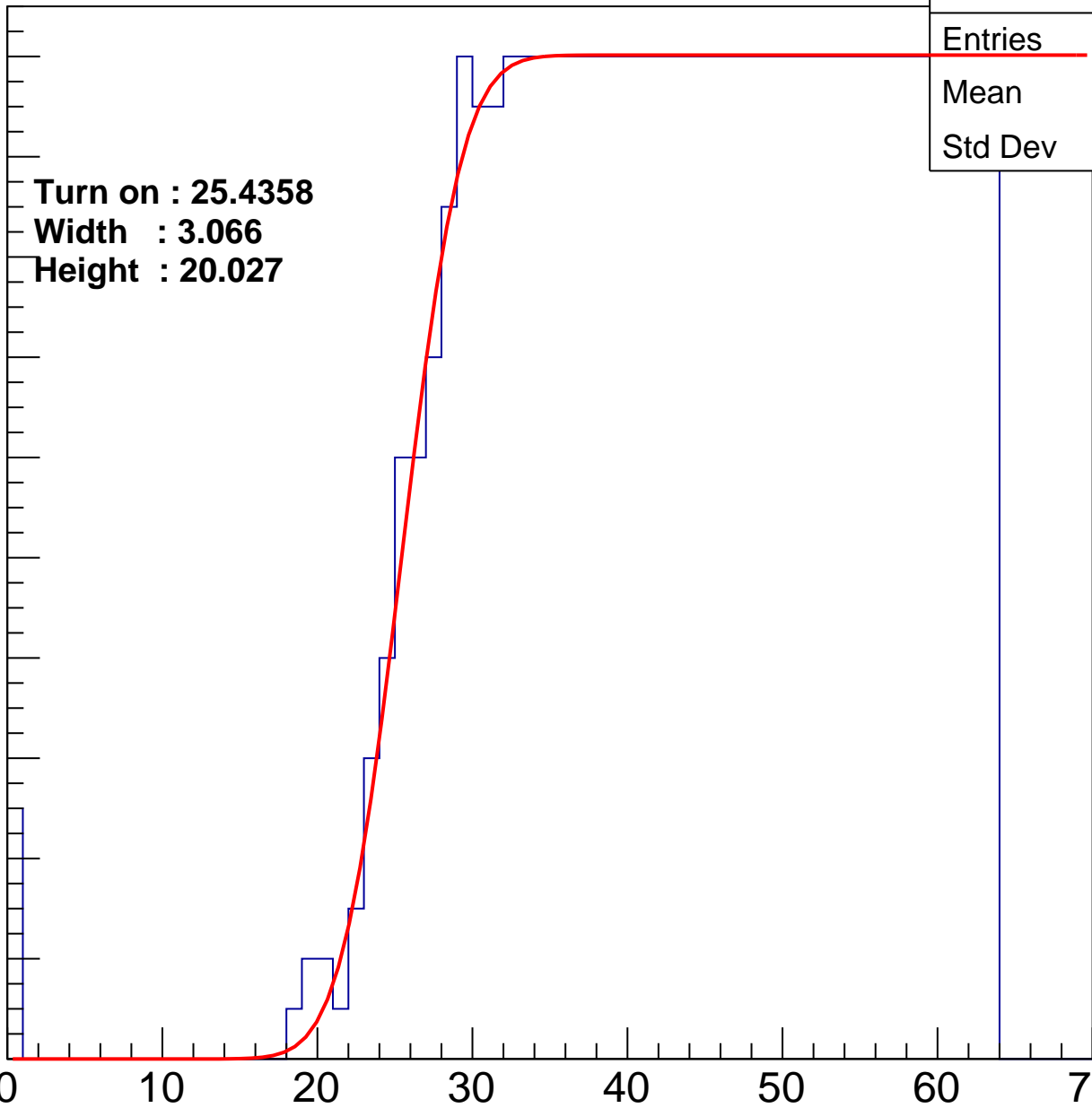
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4358
Width : 3.066
Height : 20.027

Entries	781
Mean	43.69
Std Dev	11.92

ampl



B0L101S, U24-ch31

calib_packv5_042523_0143.root, FC#1, port C1

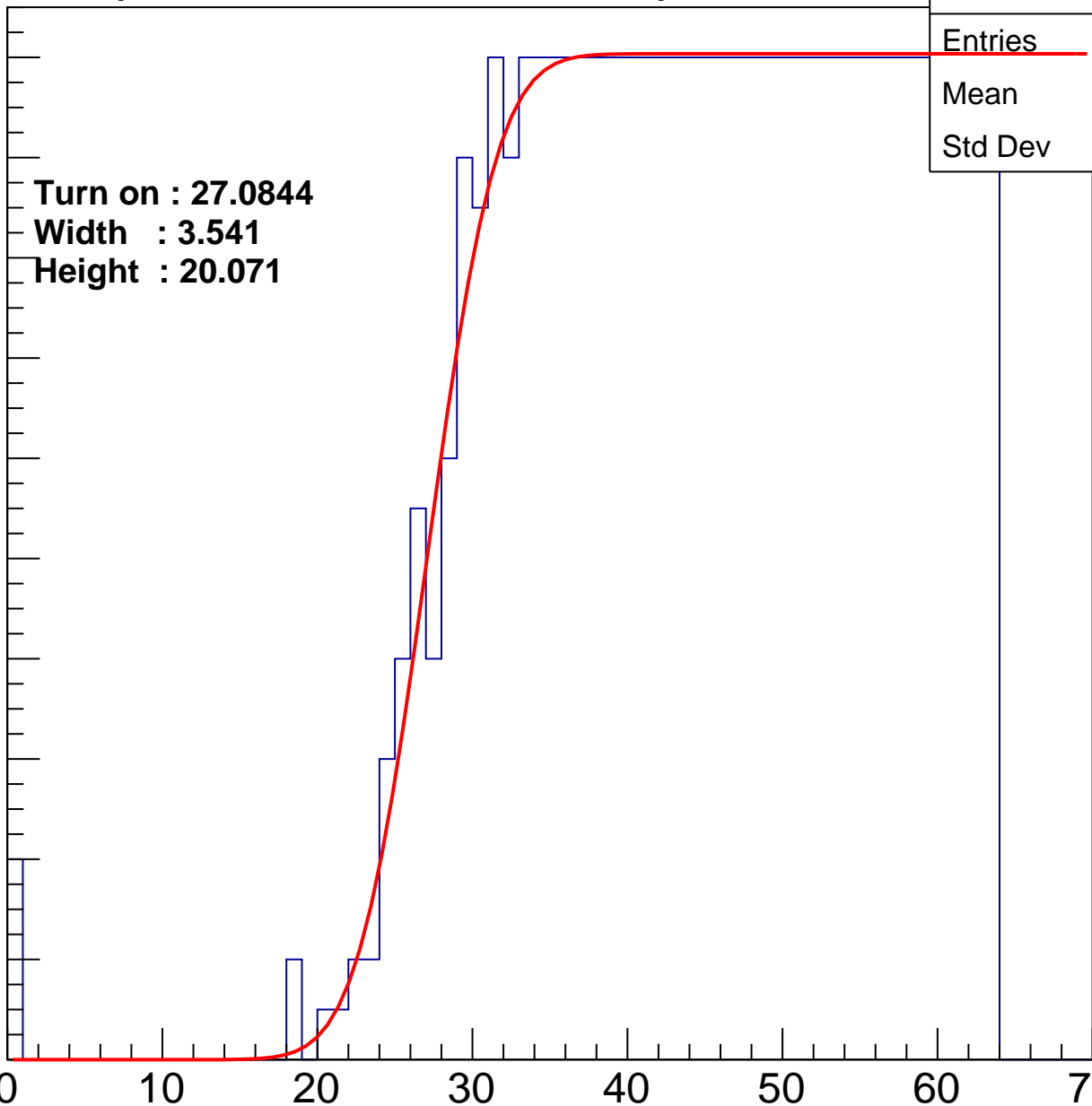
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0844
Width : 3.541
Height : 20.071

Entries	750
Mean	44.45
Std Dev	11.49

ampl



B0L101S, U24-ch32

calib_packv5_042523_0143.root, FC#1, port C1

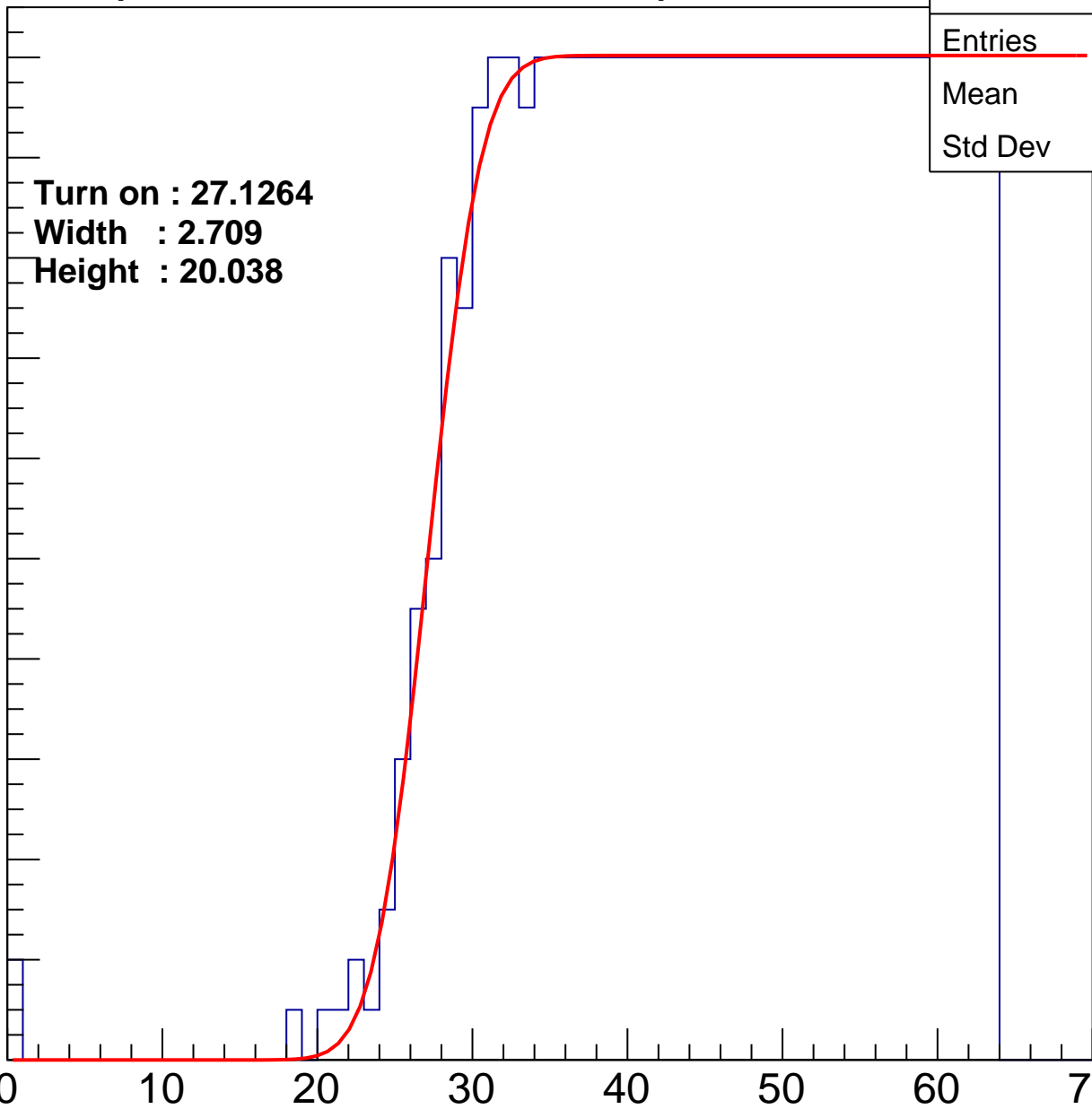
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1264
Width : 2.709
Height : 20.038

Entries	745
Mean	44.69
Std Dev	11.16

ampl



B0L101S, U24-ch33

calib_packv5_042523_0143.root, FC#1, port C1

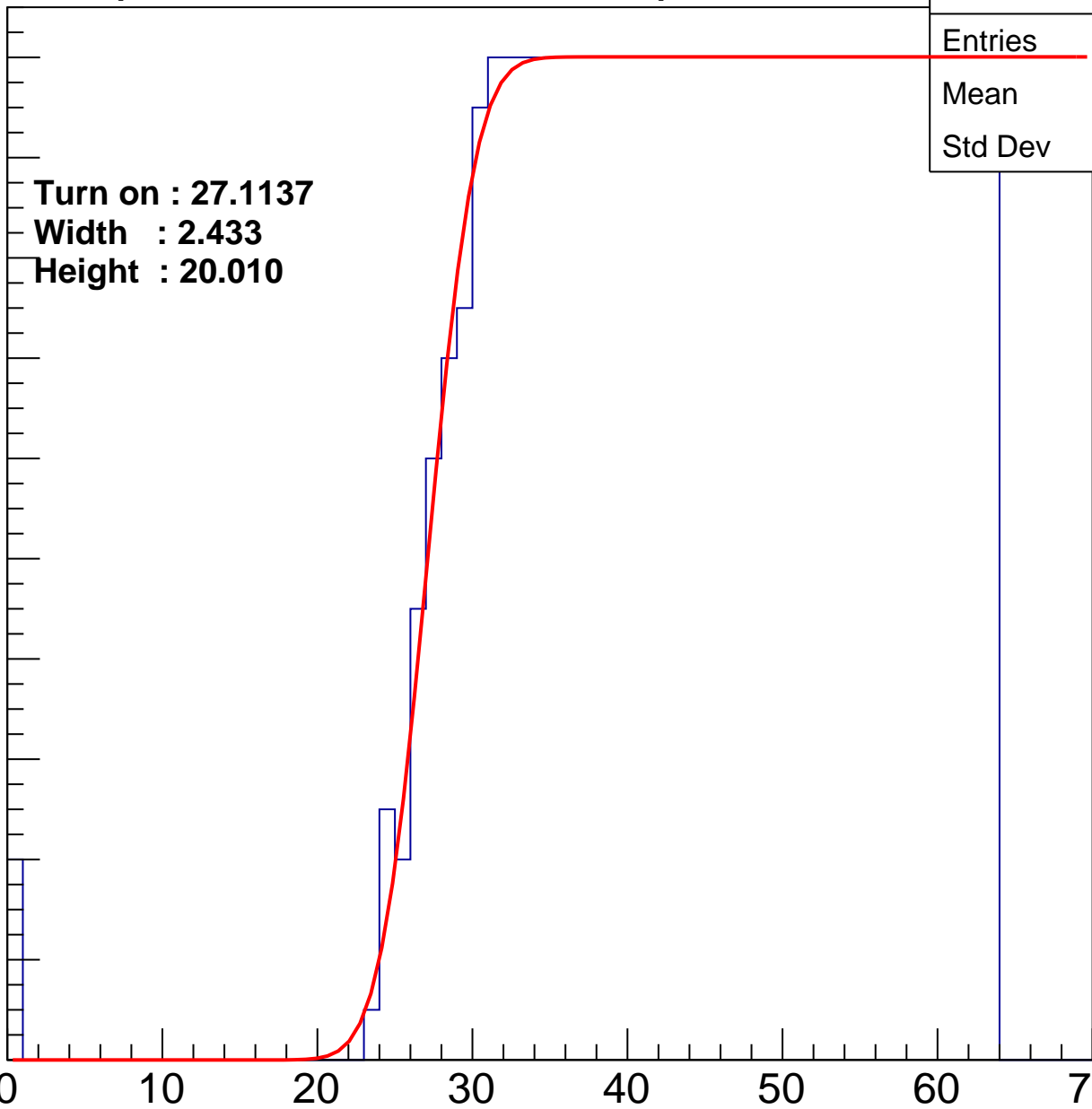
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1137
Width : 2.433
Height : 20.010

Entries	743
Mean	44.71
Std Dev	11.25

ampl



B0L101S, U24-ch34

calib_packv5_042523_0143.root, FC#1, port C1

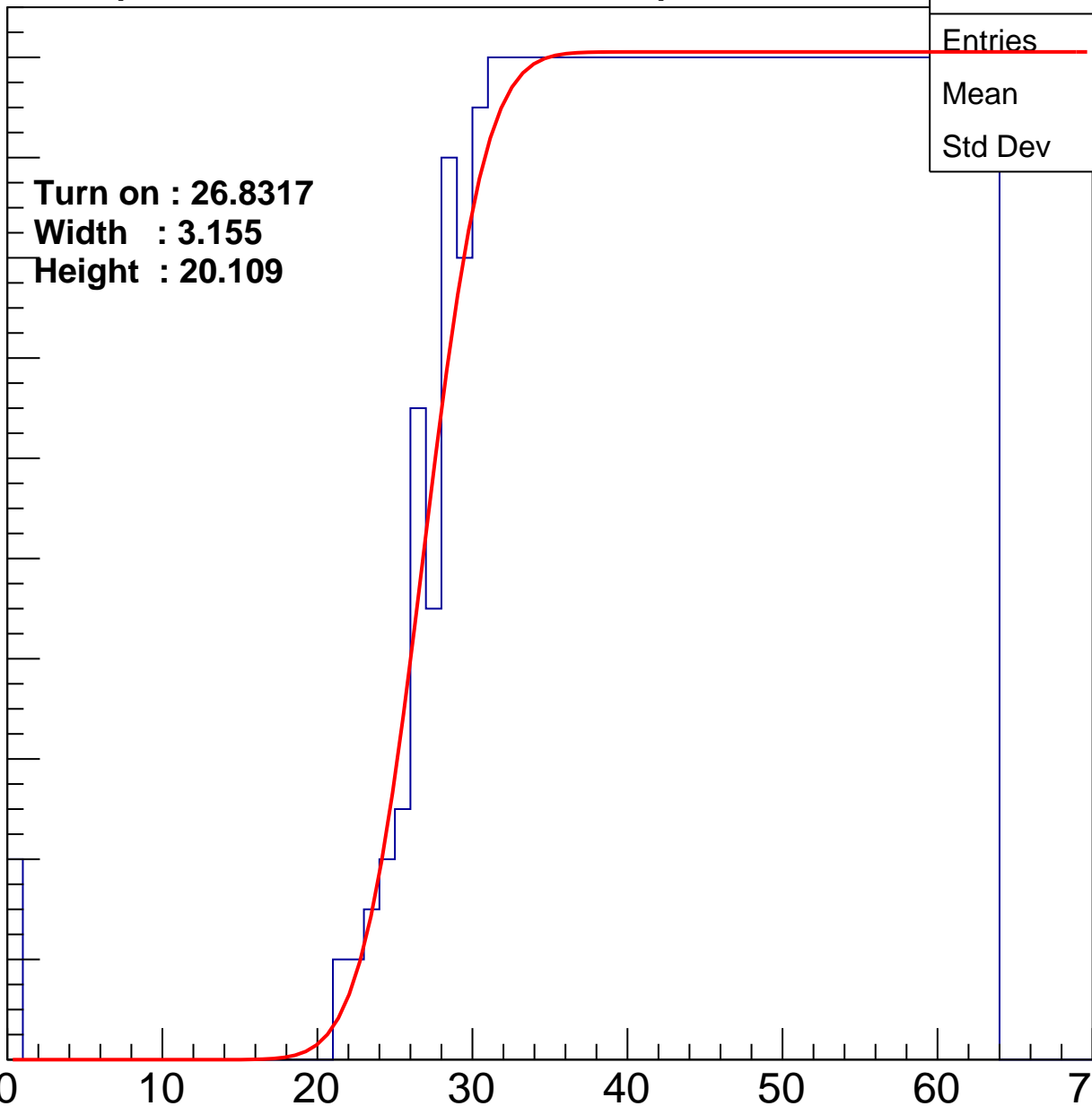
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8317
Width : 3.155
Height : 20.109

Entries	755
Mean	44.39
Std Dev	11.45

ampl



B0L101S, U24-ch35

calib_packv5_042523_0143.root, FC#1, port C1

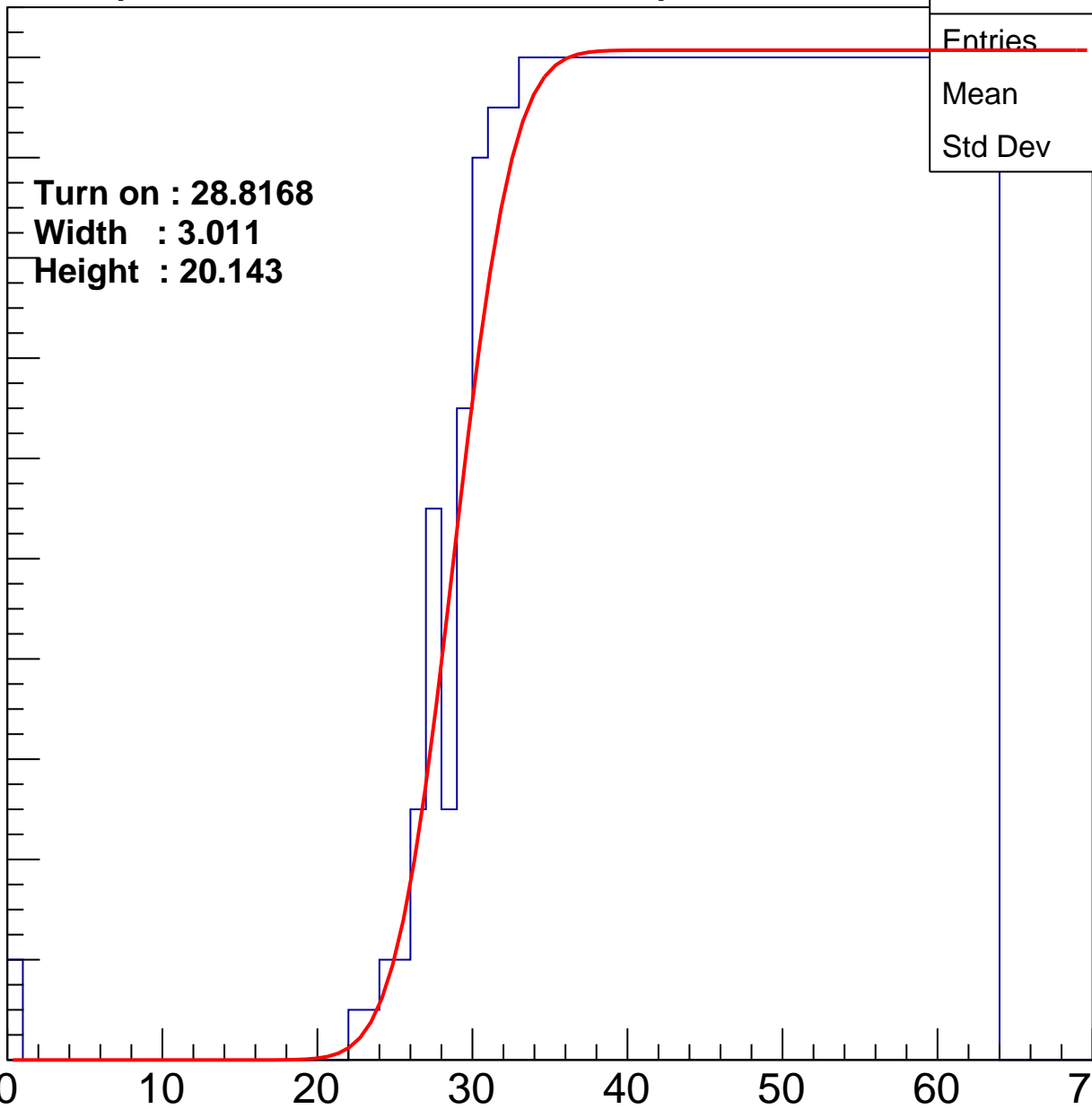
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.8168
Width : 3.011
Height : 20.143

Entries	718
Mean	45.38
Std Dev	10.75

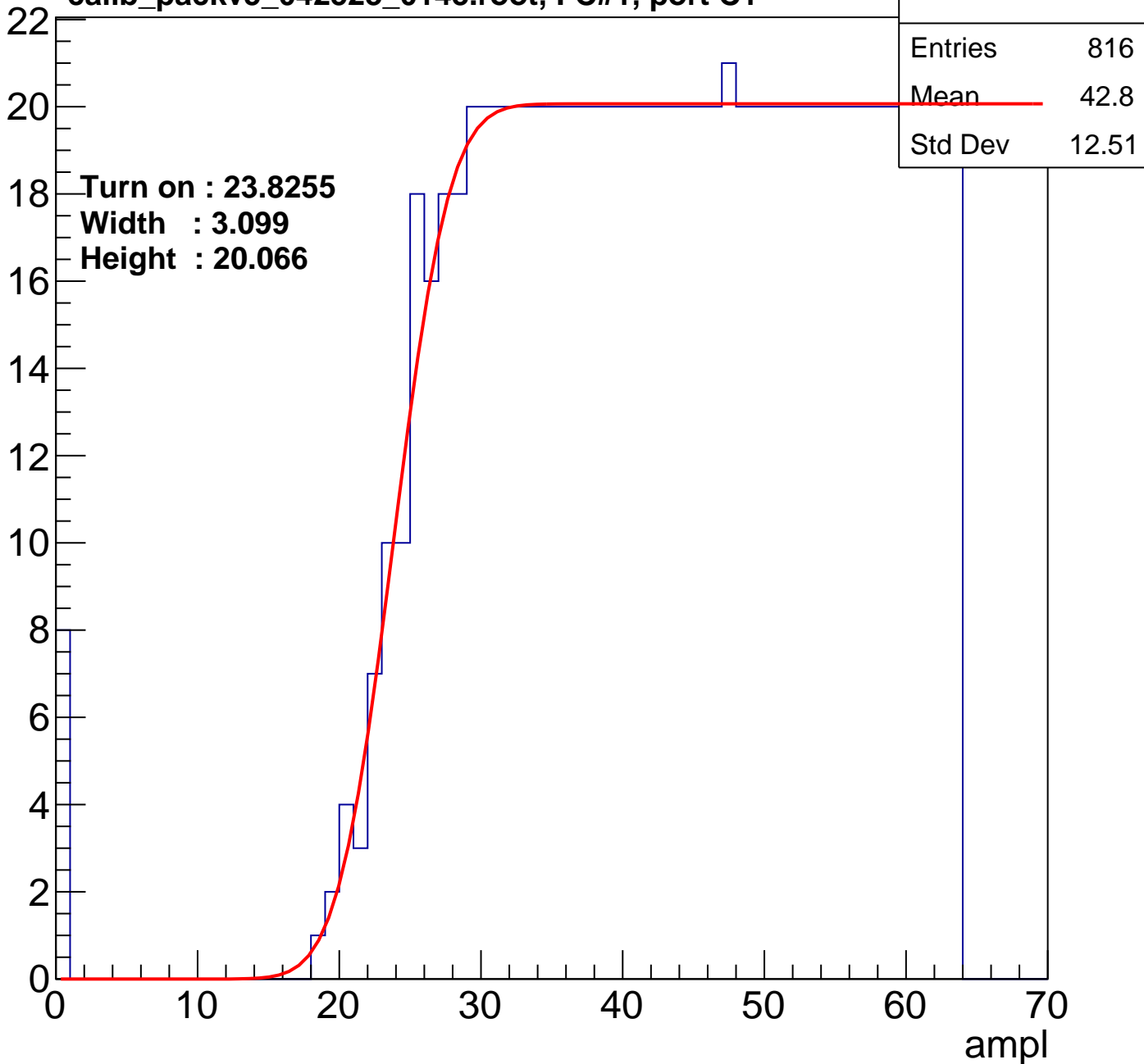
ampl



B0L101S, U24-ch36

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch37

calib_packv5_042523_0143.root, FC#1, port C1

Entry

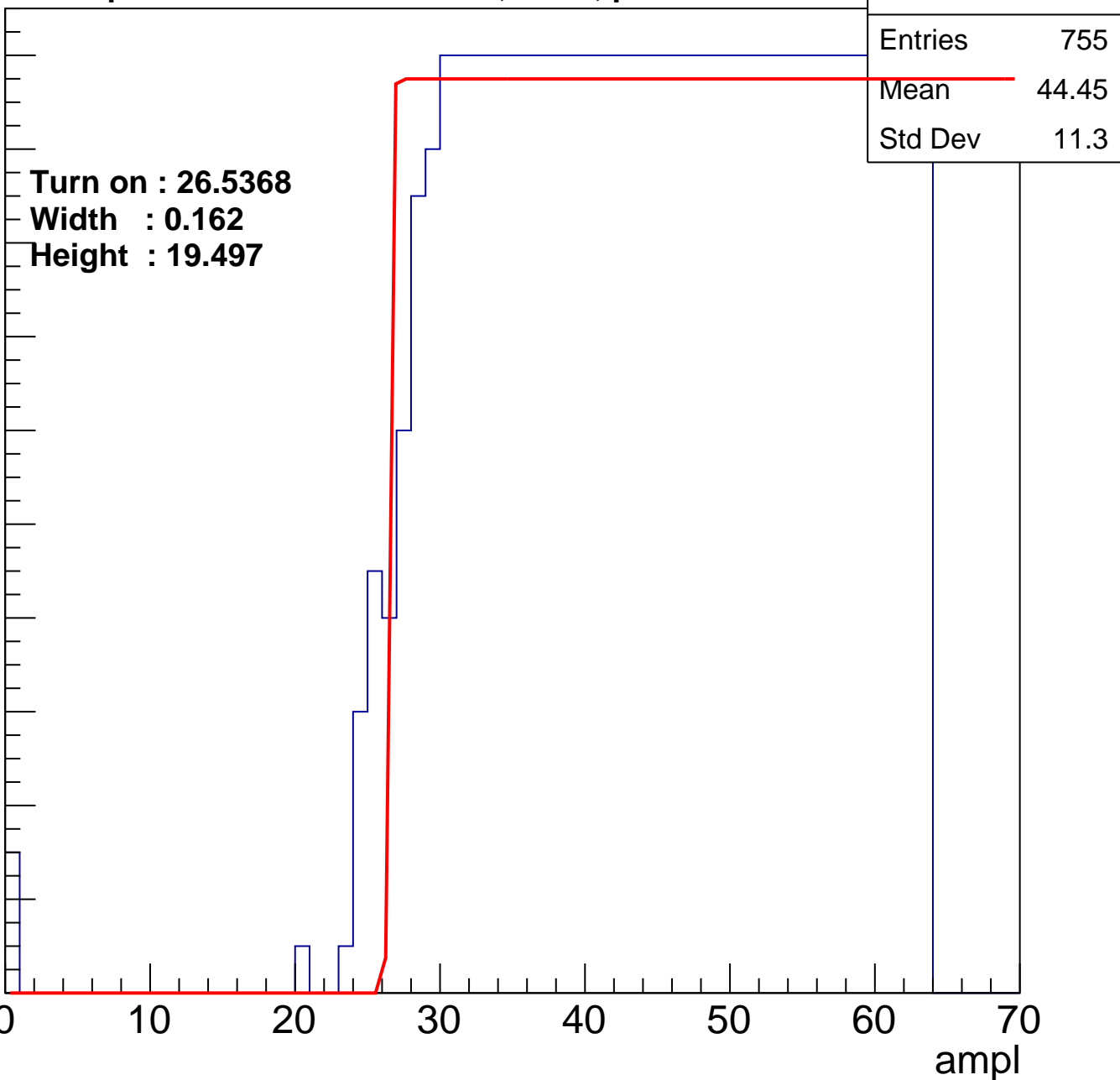
20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5368
Width : 0.162
Height : 19.497

Entries	755
Mean	44.45
Std Dev	11.3

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch38

calib_packv5_042523_0143.root, FC#1, port C1

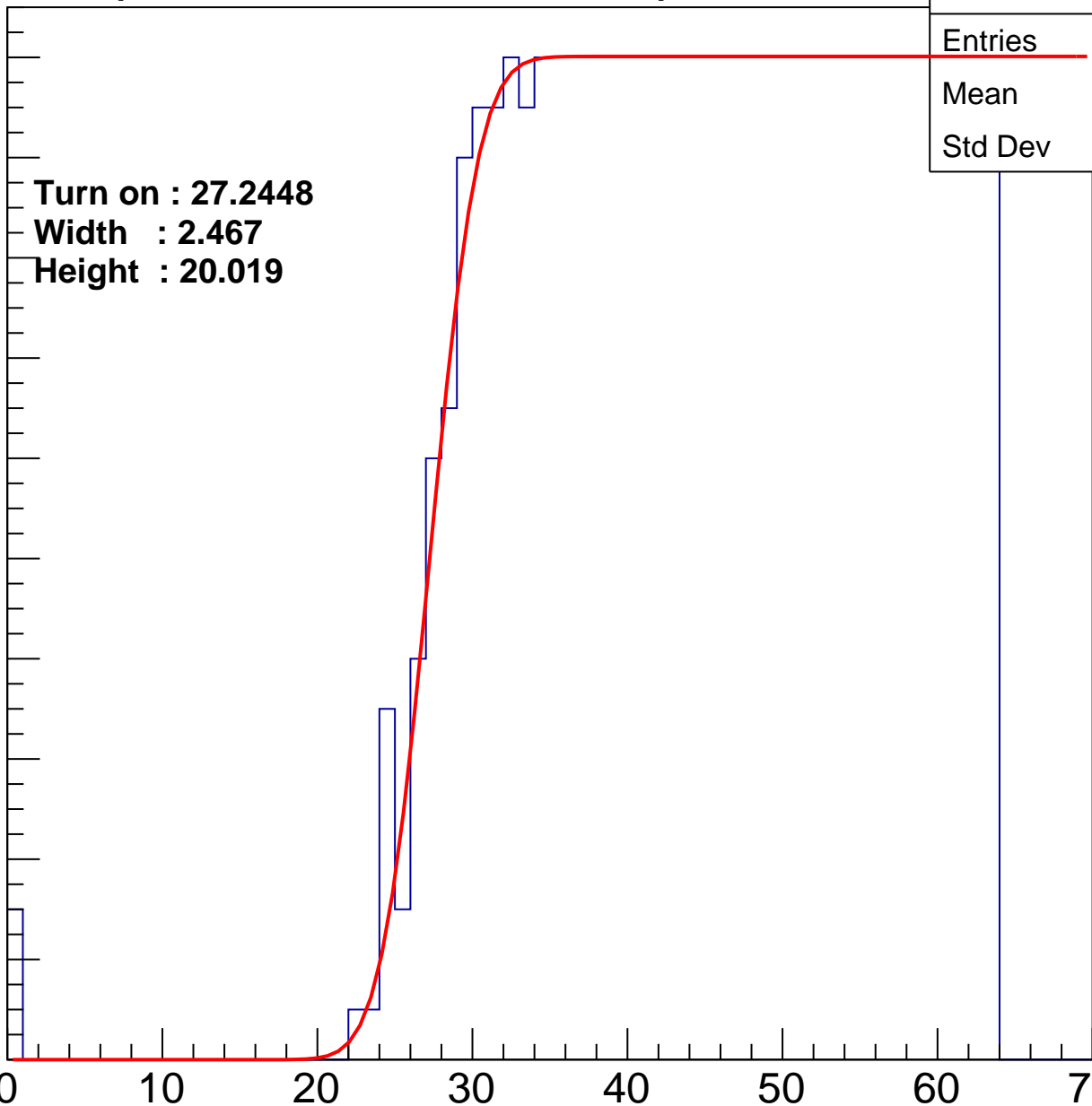
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2448
Width : 2.467
Height : 20.019

Entries	743
Mean	44.73
Std Dev	11.18

ampl



B0L101S, U24-ch39

calib_packv5_042523_0143.root, FC#1, port C1

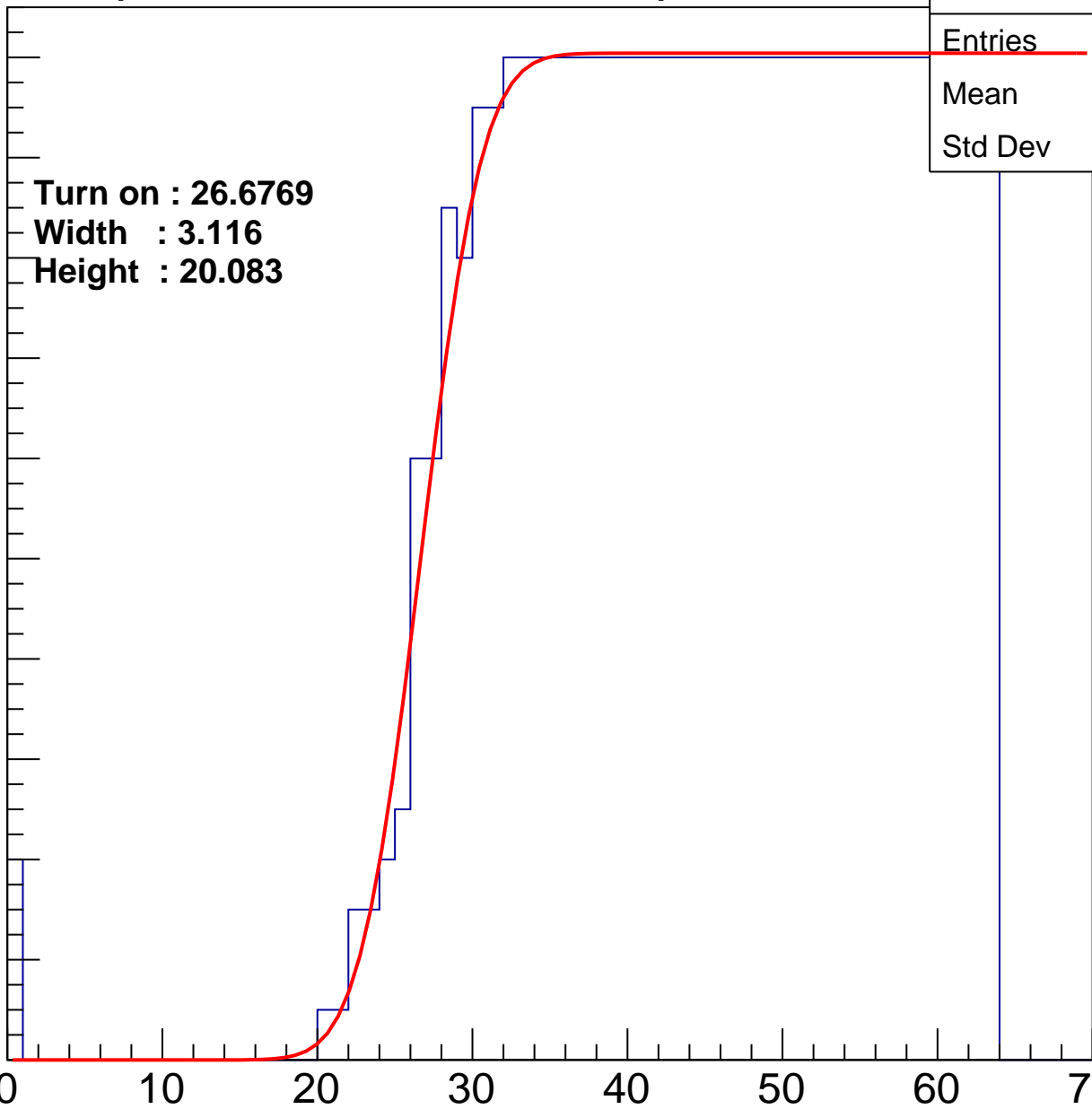
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6769
Width : 3.116
Height : 20.083

Entries	756
Mean	44.35
Std Dev	11.48

ampl



B0L101S, U24-ch40

calib_packv5_042523_0143.root, FC#1, port C1

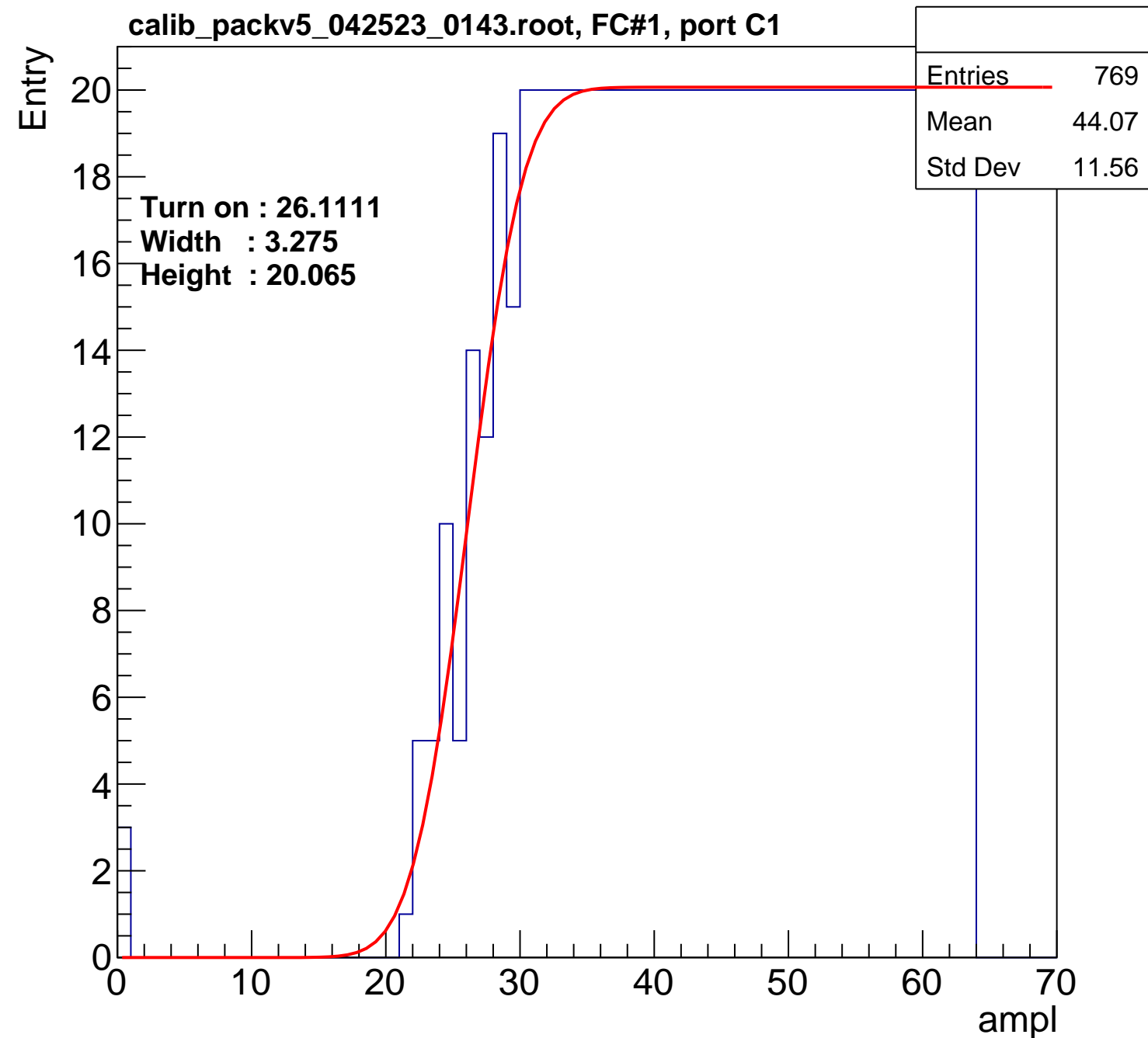
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1111
Width : 3.275
Height : 20.065

Entries	769
Mean	44.07
Std Dev	11.56

ampl



B0L101S, U24-ch41

calib_packv5_042523_0143.root, FC#1, port C1

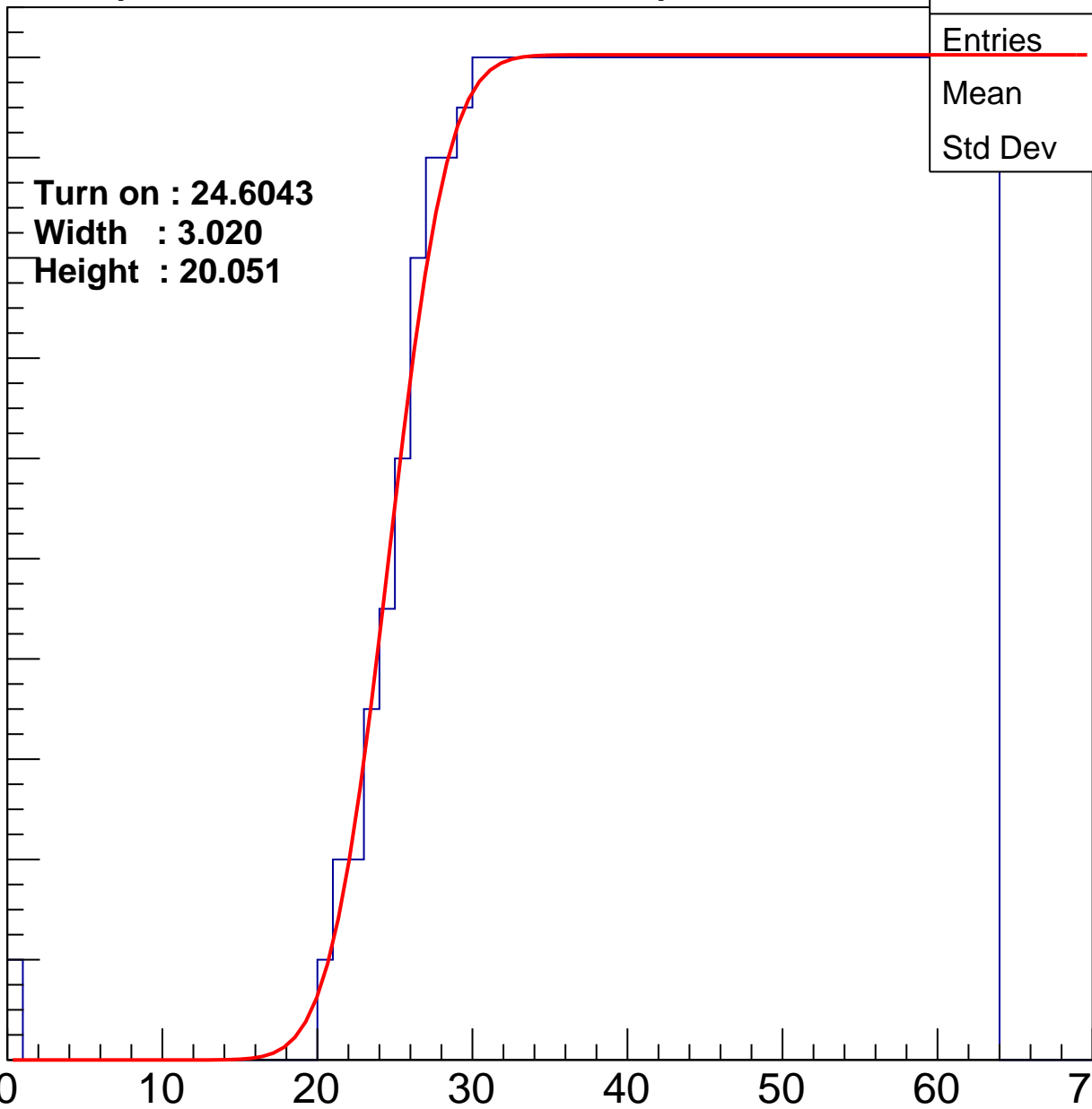
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6043
Width : 3.020
Height : 20.051

Entries	791
Mean	43.57
Std Dev	11.73

ampl



B0L101S, U24-ch42

calib_packv5_042523_0143.root, FC#1, port C1

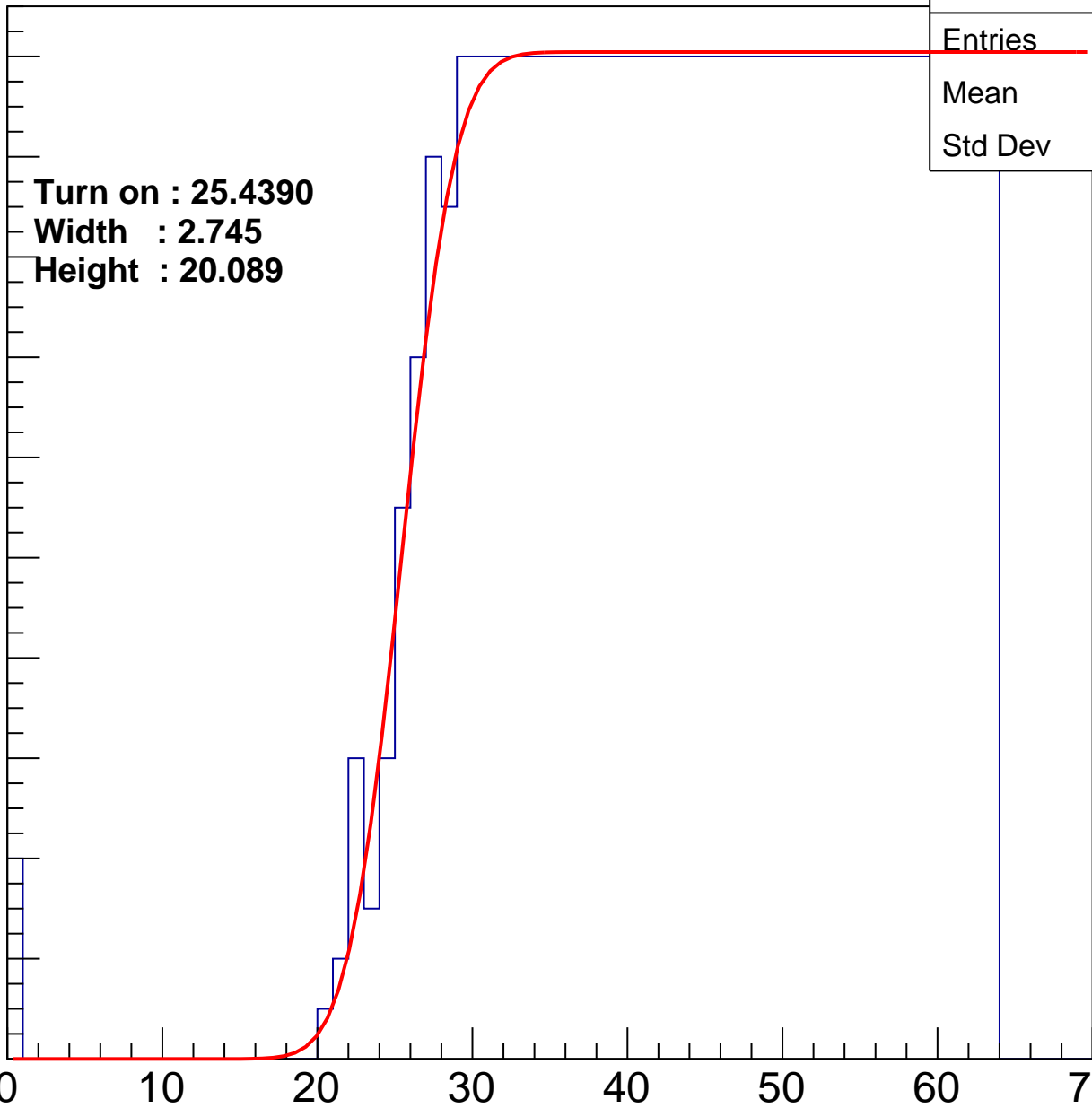
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4390
Width : 2.745
Height : 20.089

Entries	782
Mean	43.74
Std Dev	11.76

ampl



B0L101S, U24-ch43

calib_packv5_042523_0143.root, FC#1, port C1

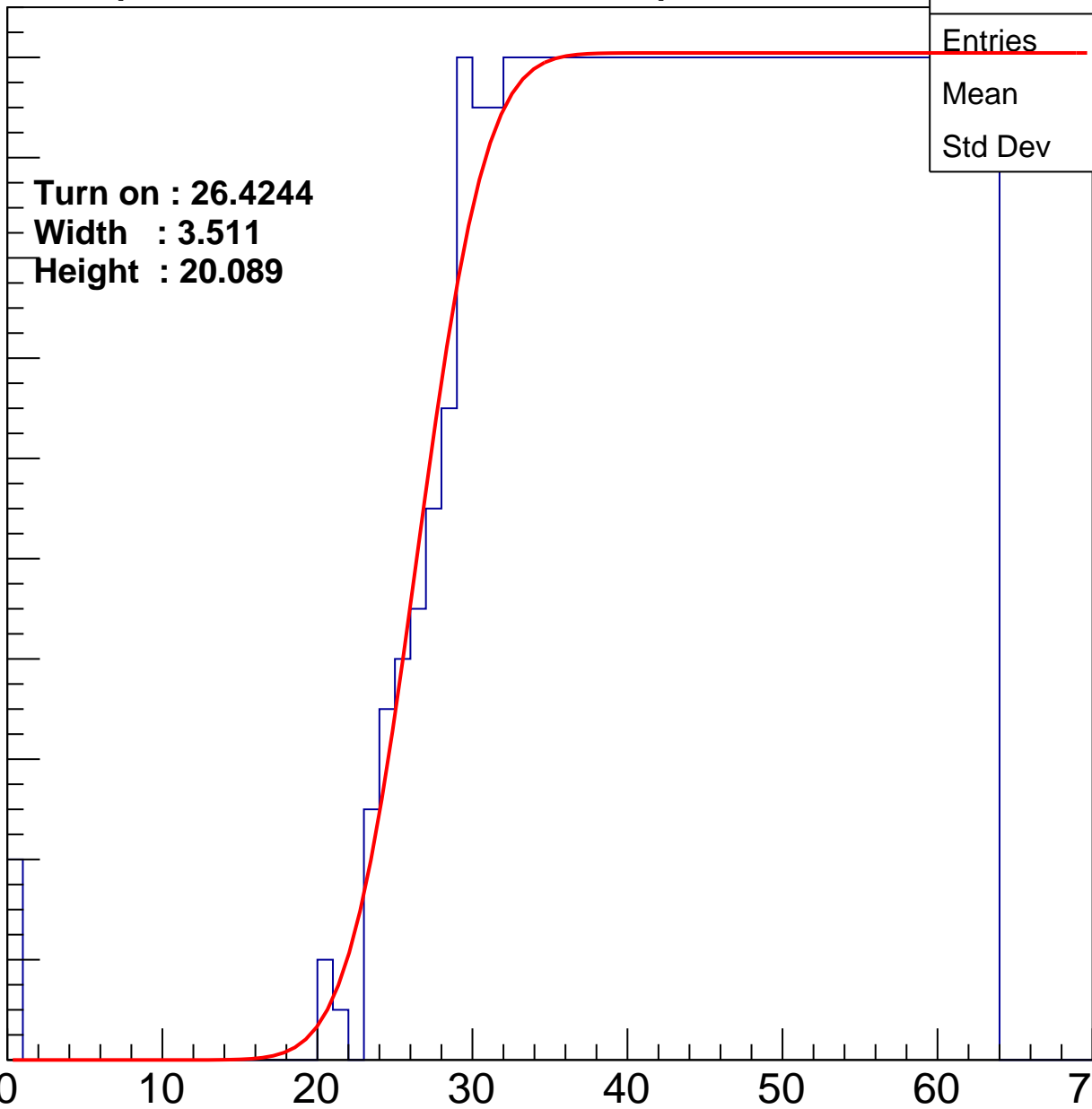
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4244
Width : 3.511
Height : 20.089

Entries	758
Mean	44.3
Std Dev	11.52

ampl



B0L101S, U24-ch44

calib_packv5_042523_0143.root, FC#1, port C1

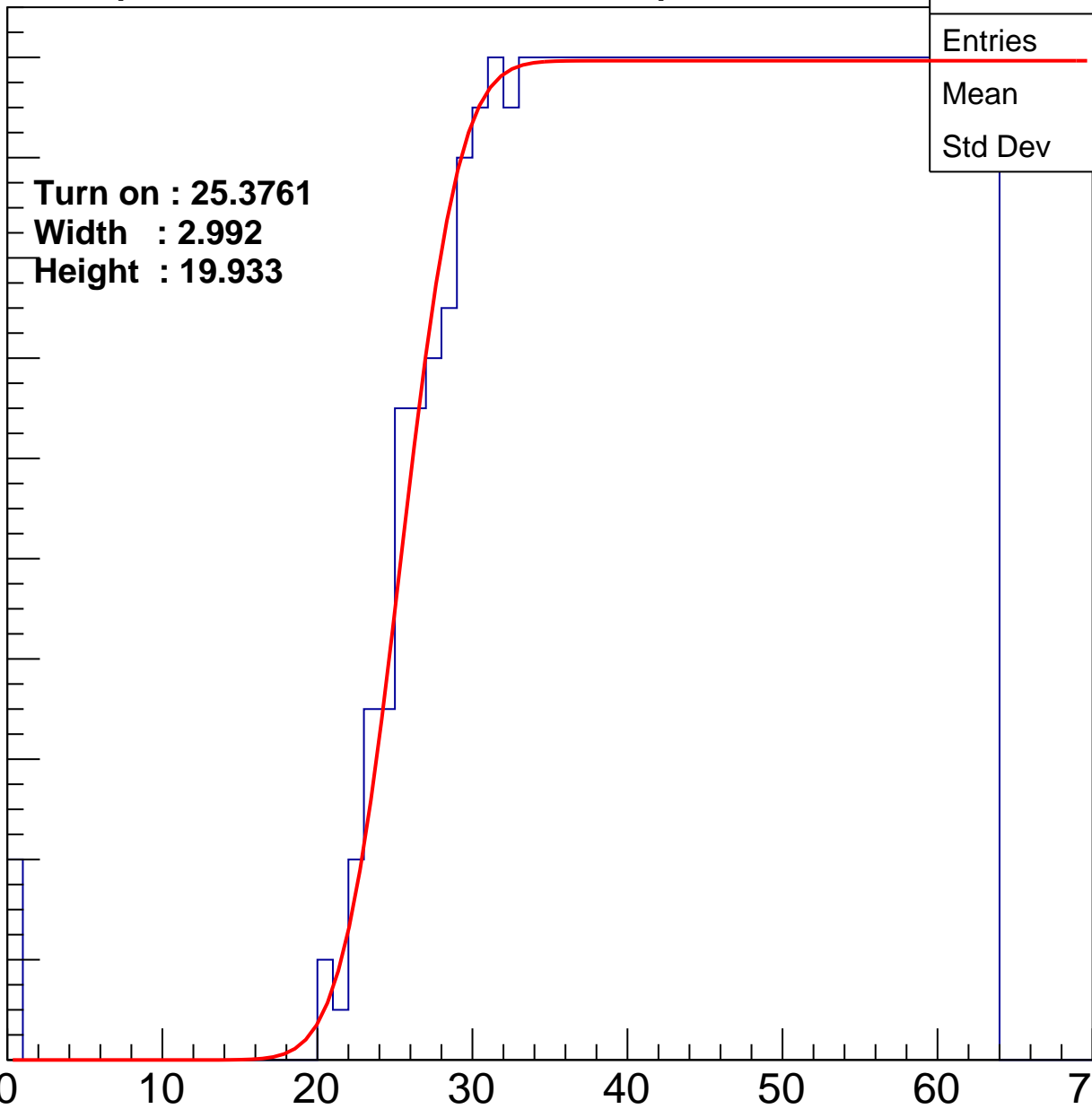
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3761
Width : 2.992
Height : 19.933

Entries	776
Mean	43.84
Std Dev	11.77

ampl



B0L101S, U24-ch45

calib_packv5_042523_0143.root, FC#1, port C1

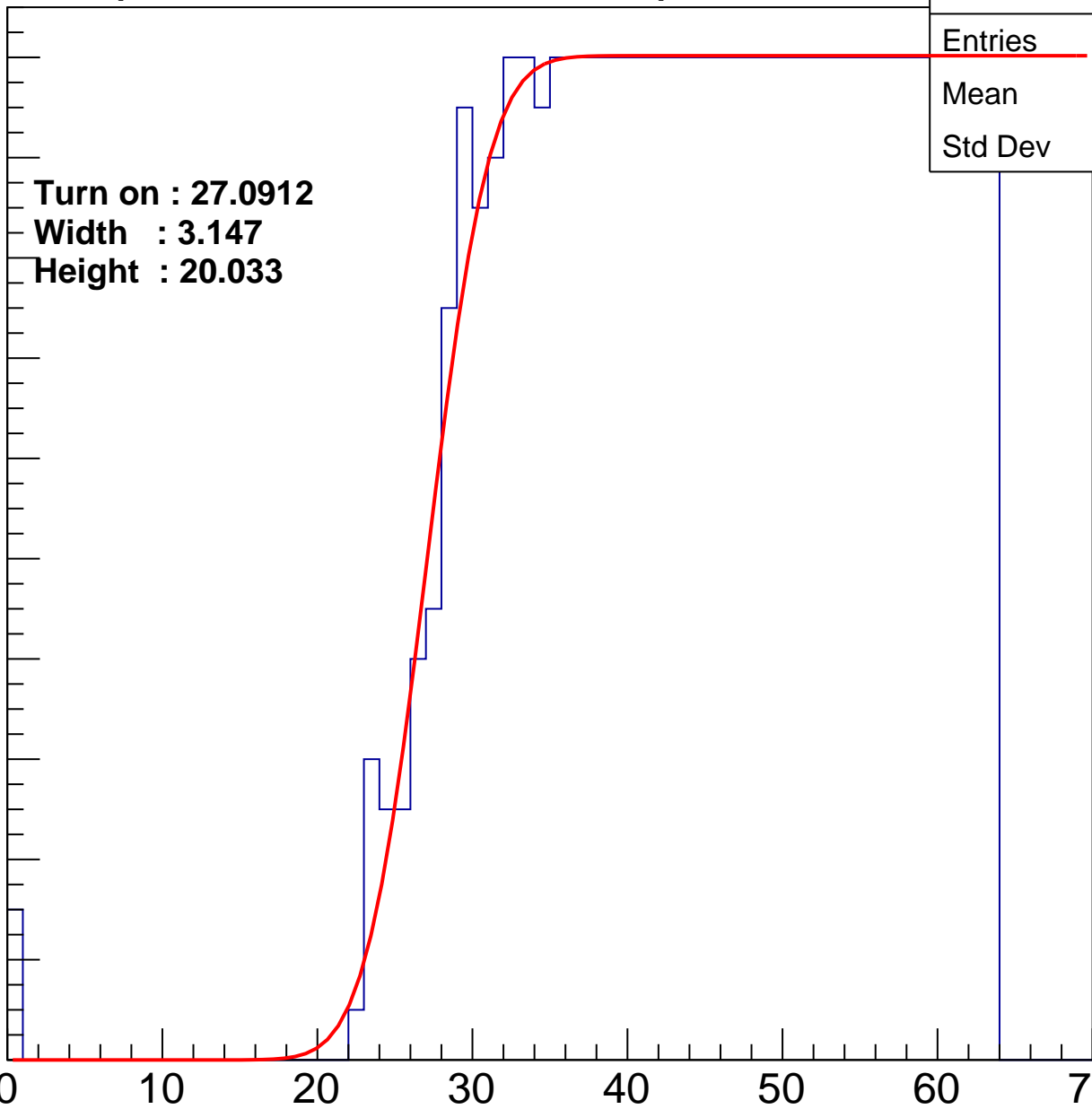
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0912
Width : 3.147
Height : 20.033

Entries	745
Mean	44.65
Std Dev	11.26

ampl



B0L101S, U24-ch46

calib_packv5_042523_0143.root, FC#1, port C1

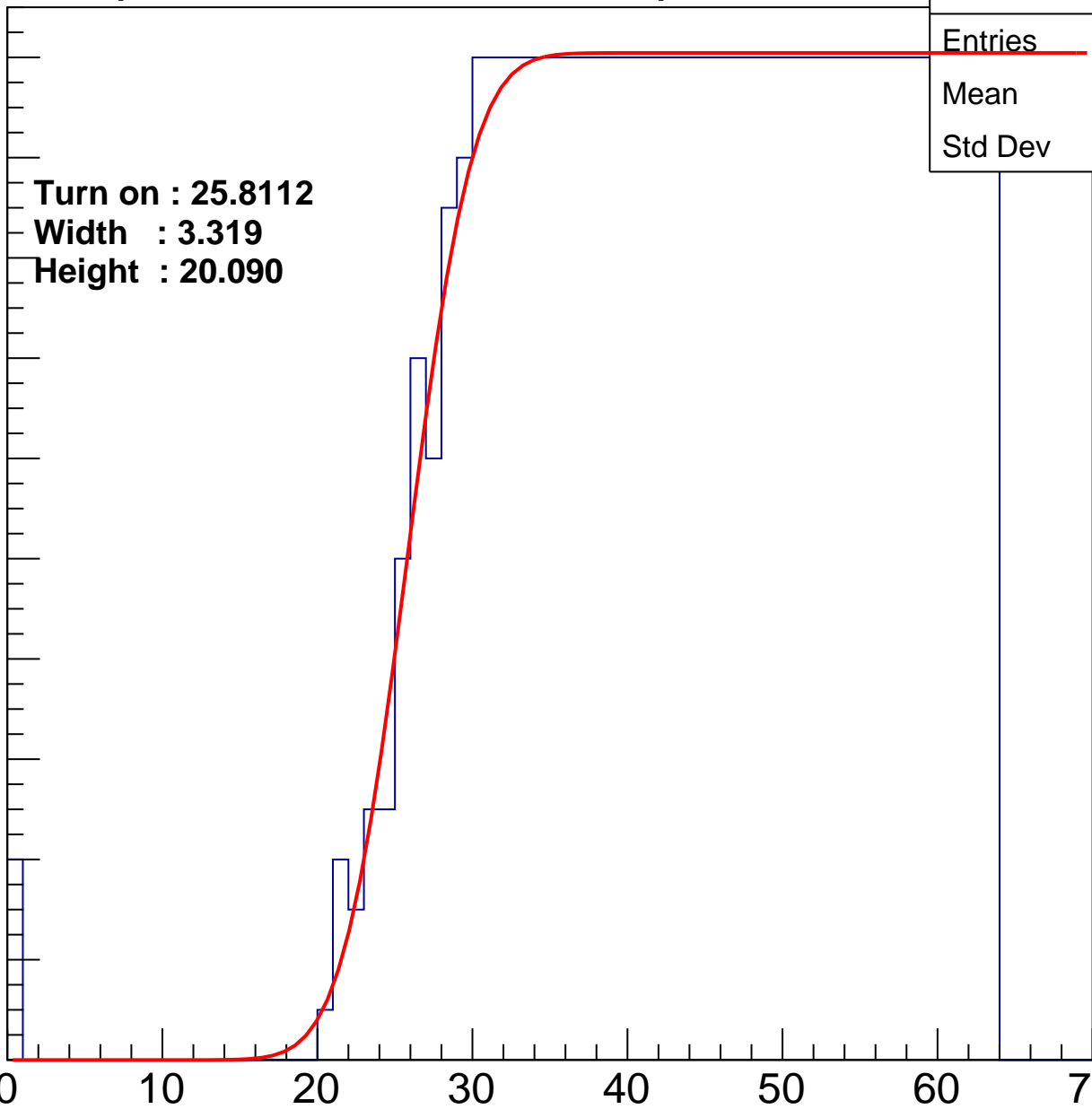
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8112
Width : 3.319
Height : 20.090

Entries	773
Mean	43.93
Std Dev	11.7

ampl



B0L101S, U24-ch47

calib_packv5_042523_0143.root, FC#1, port C1

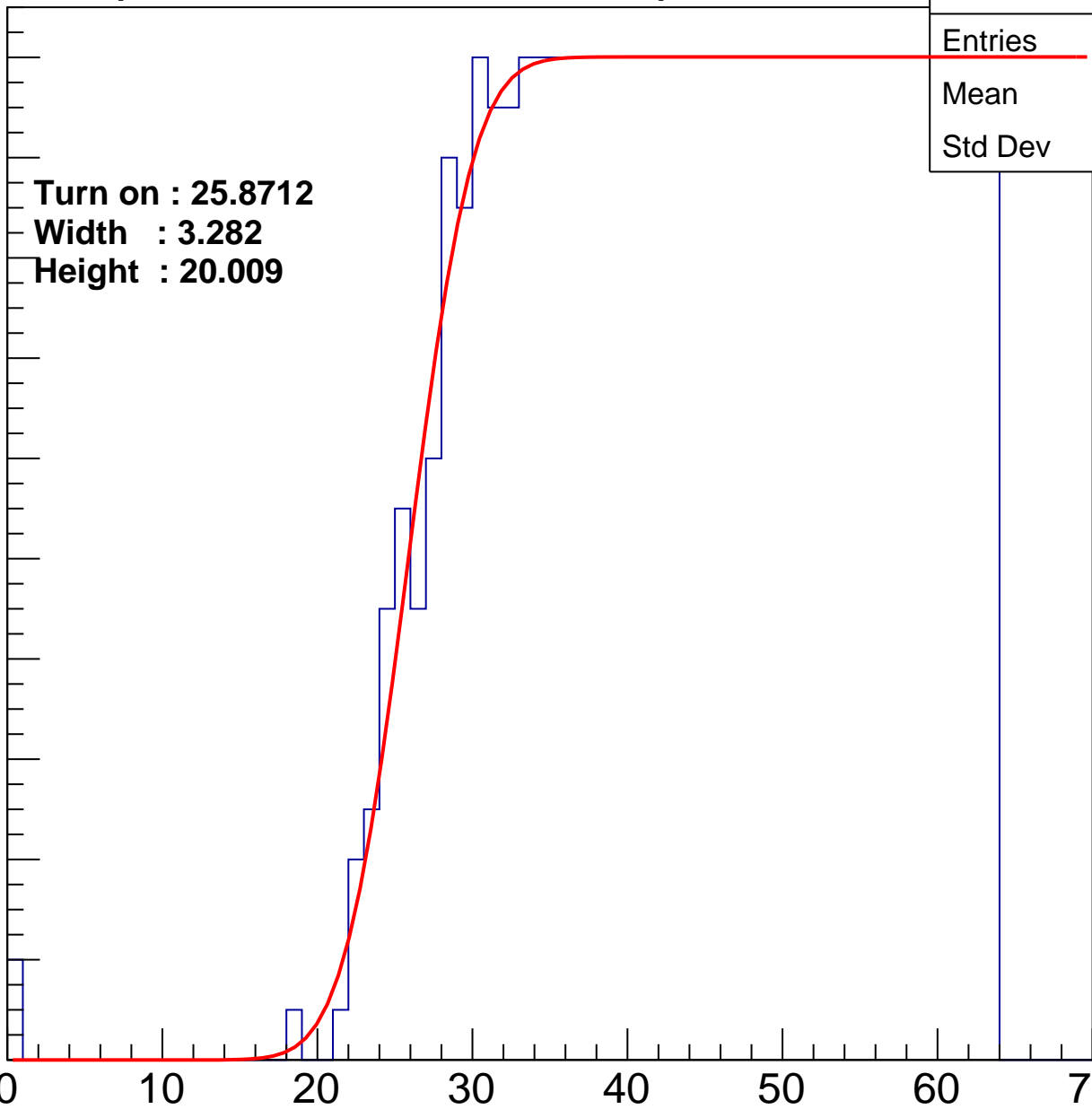
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8712
Width : 3.282
Height : 20.009

Entries	767
Mean	44.13
Std Dev	11.47

ampl



B0L101S, U24-ch48

calib_packv5_042523_0143.root, FC#1, port C1

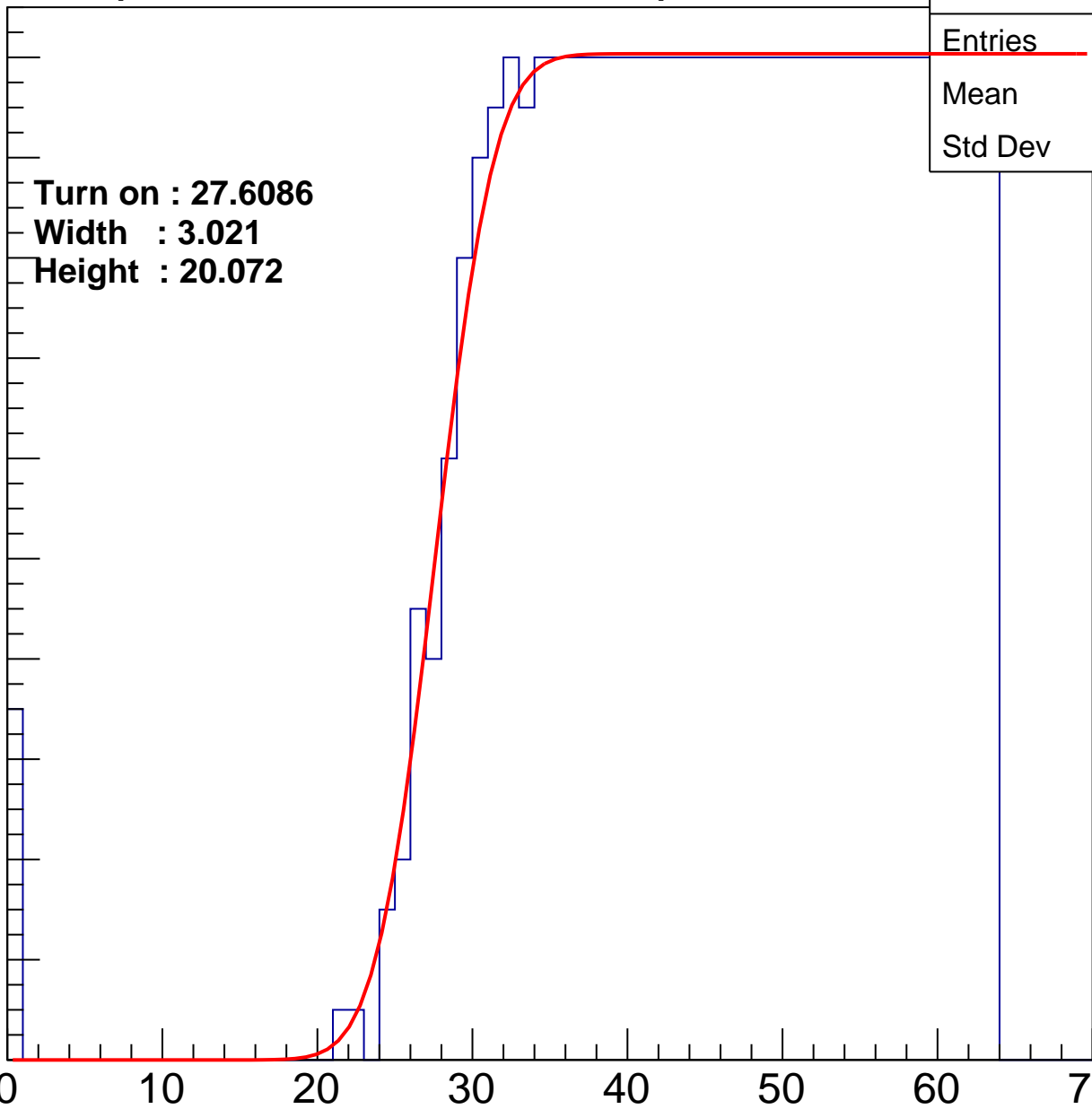
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6086
Width : 3.021
Height : 20.072

Entries	737
Mean	44.72
Std Dev	11.52

ampl



B0L101S, U24-ch49

calib_packv5_042523_0143.root, FC#1, port C1

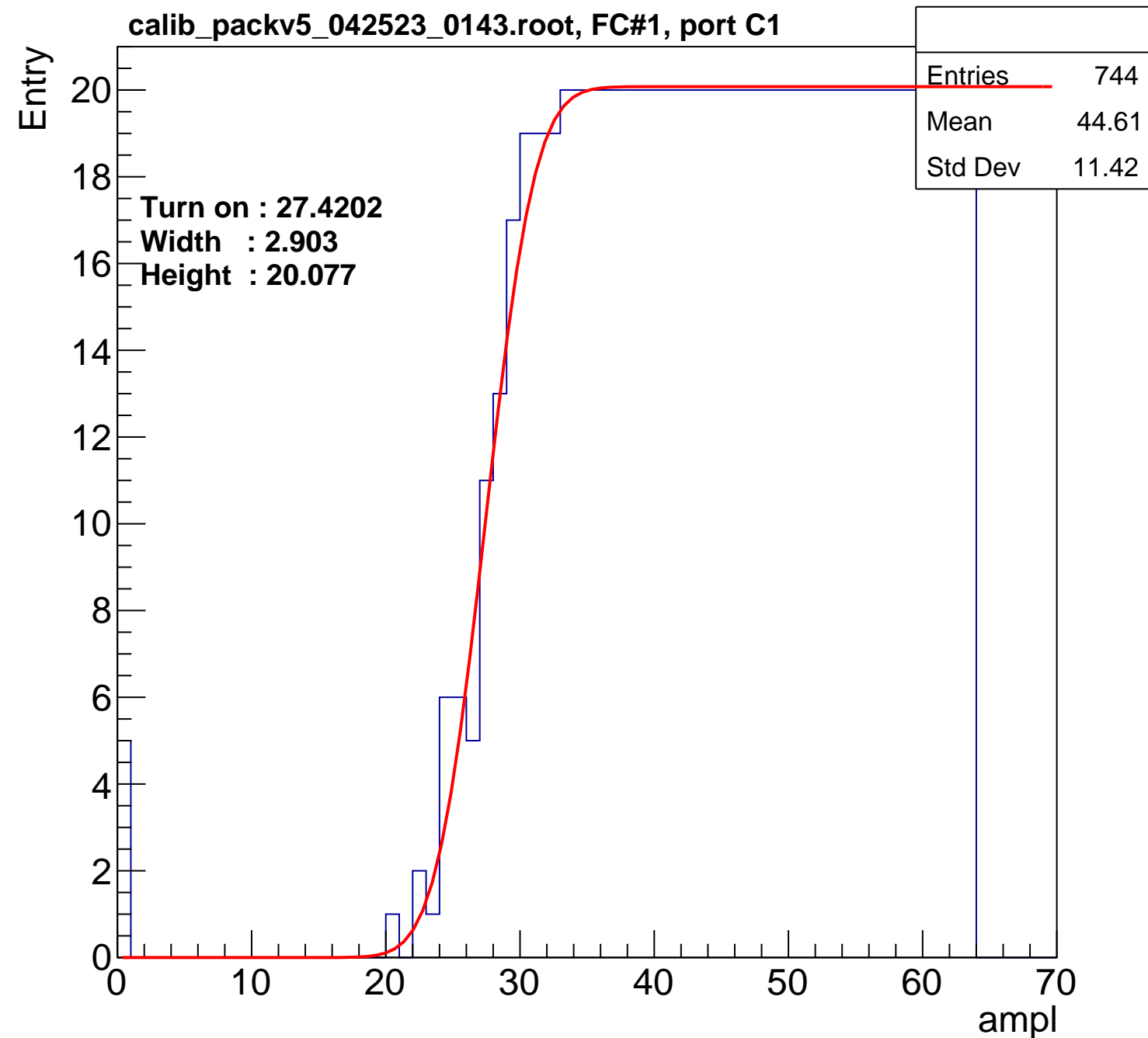
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4202
Width : 2.903
Height : 20.077

Entries	744
Mean	44.61
Std Dev	11.42

ampl



B0L101S, U24-ch50

calib_packv5_042523_0143.root, FC#1, port C1

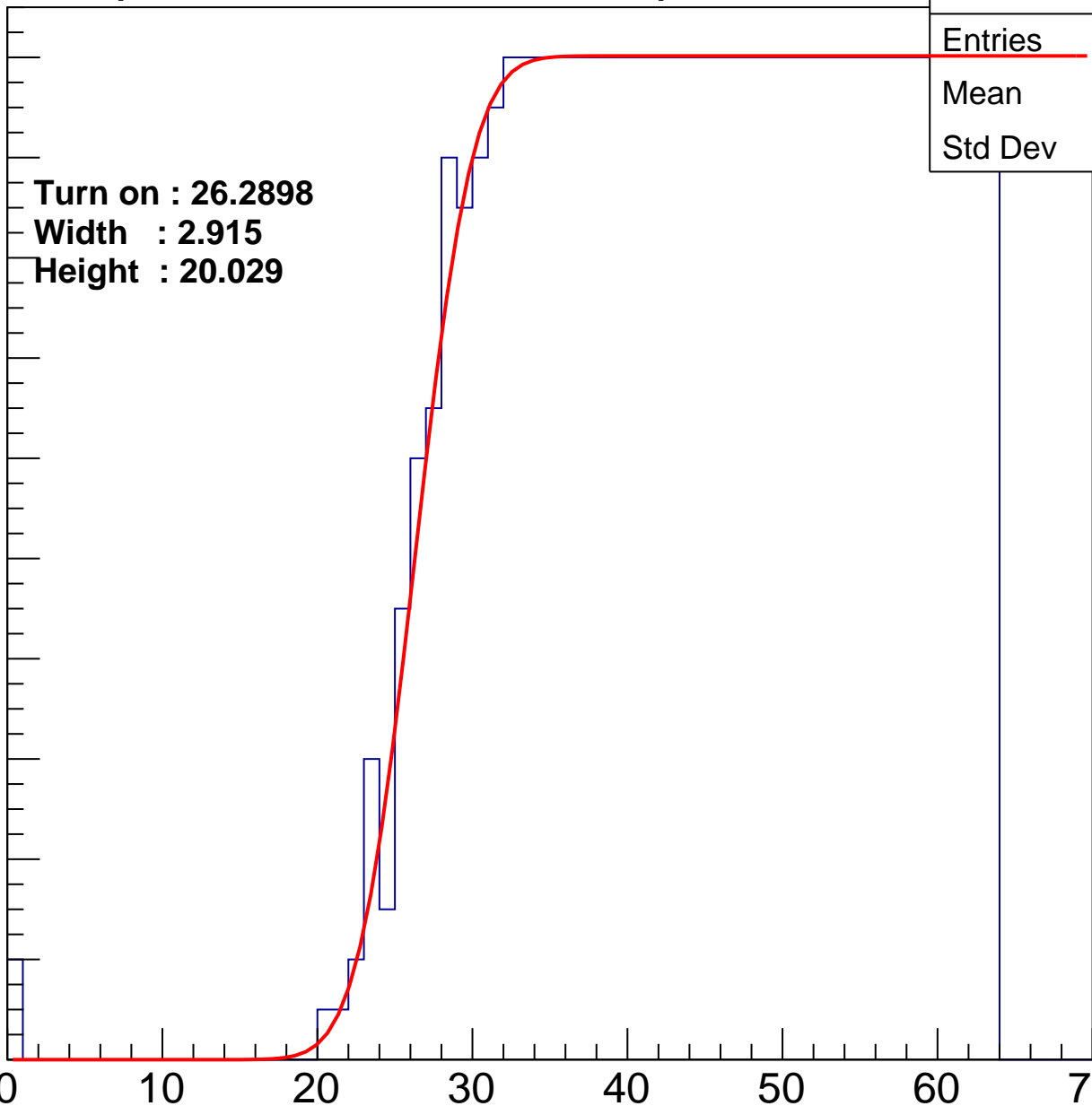
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2898
Width : 2.915
Height : 20.029

Entries	761
Mean	44.3
Std Dev	11.36

ampl



B0L101S, U24-ch51

calib_packv5_042523_0143.root, FC#1, port C1

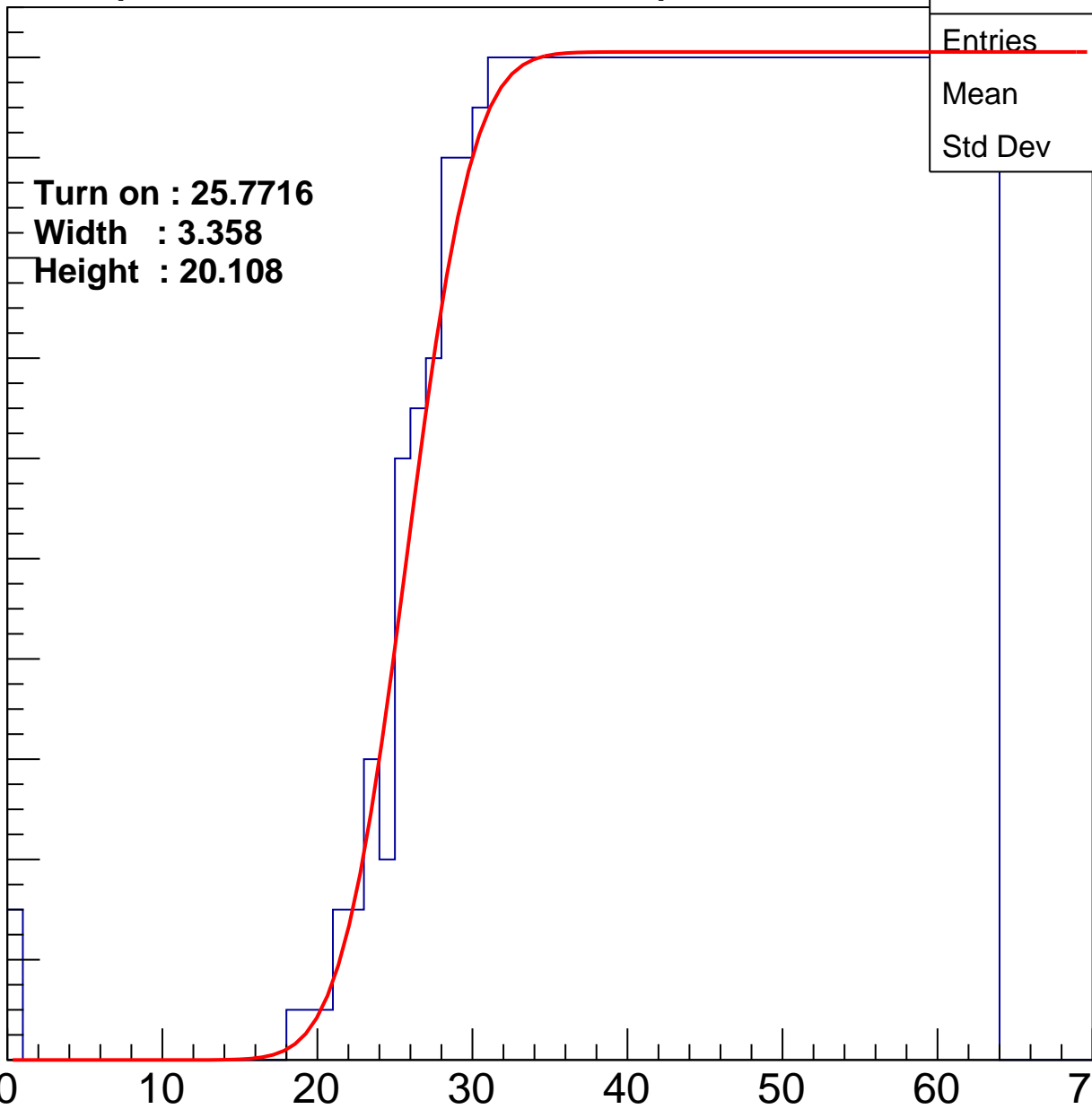
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7716
Width : 3.358
Height : 20.108

Entries	776
Mean	43.88
Std Dev	11.67

ampl



B0L101S, U24-ch52

calib_packv5_042523_0143.root, FC#1, port C1

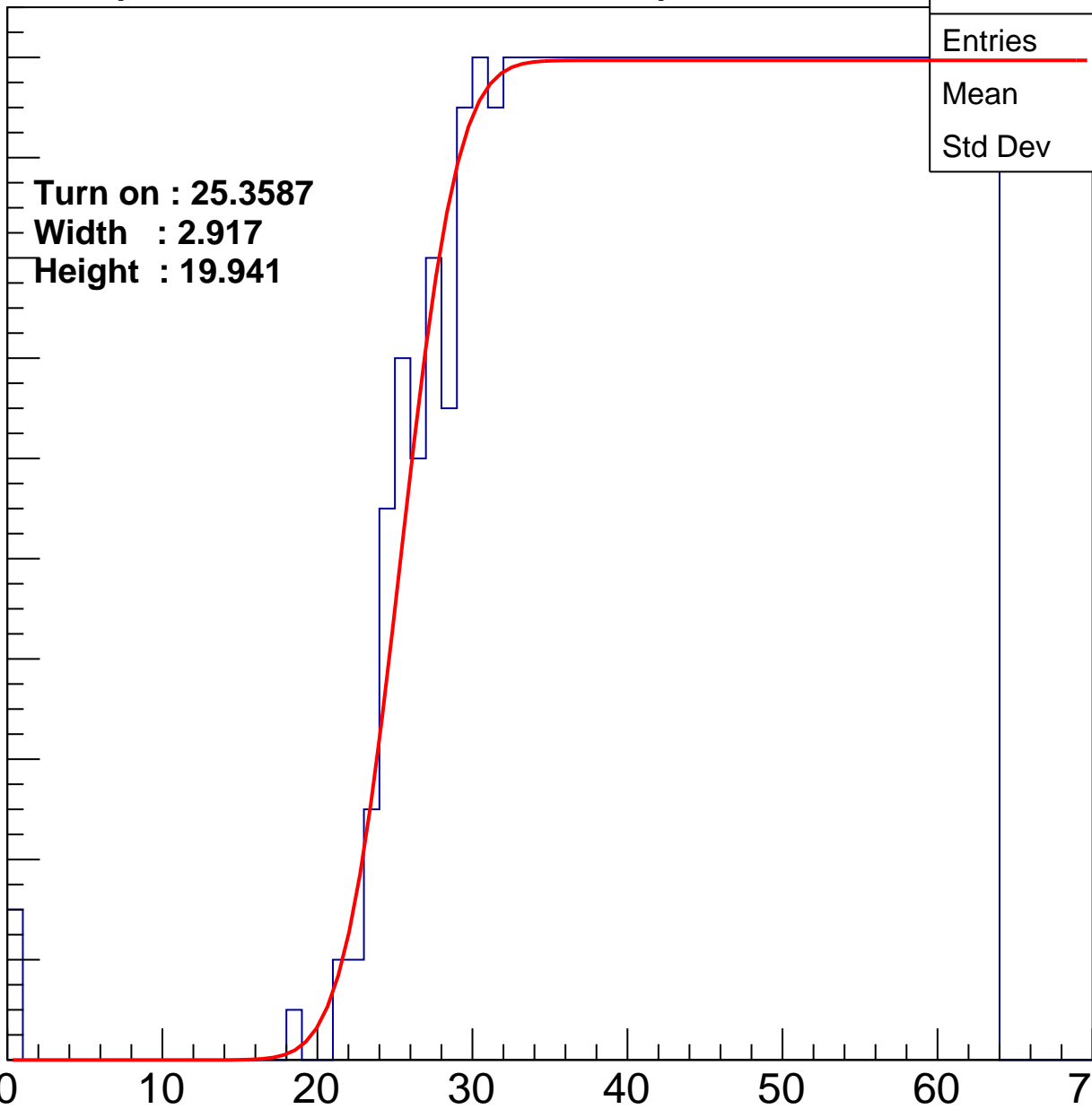
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3587
Width : 2.917
Height : 19.941

Entries	777
Mean	43.86
Std Dev	11.67

ampl



B0L101S, U24-ch53

calib_packv5_042523_0143.root, FC#1, port C1

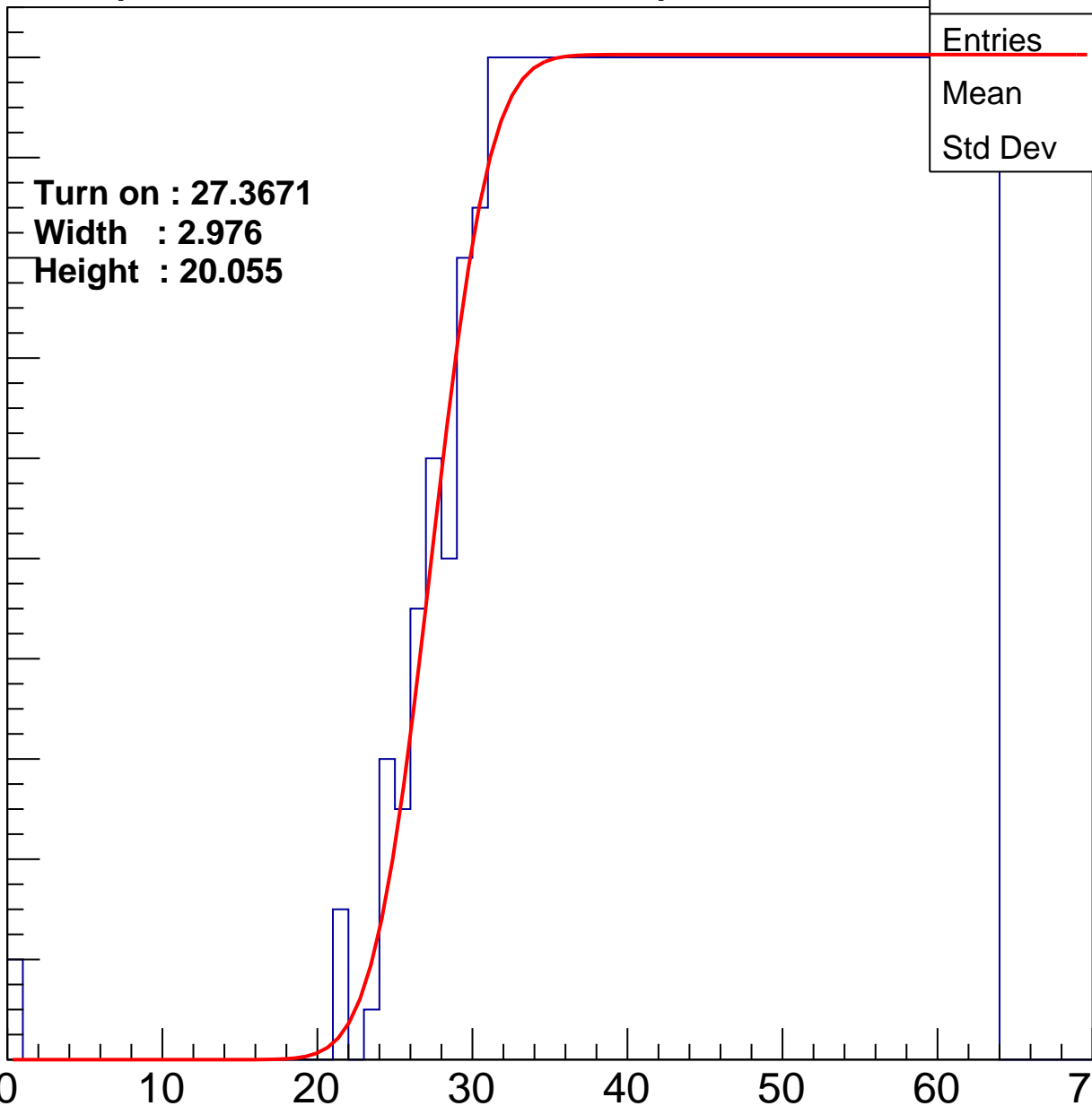
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3671
Width : 2.976
Height : 20.055

Entries	741
Mean	44.79
Std Dev	11.1

ampl



B0L101S, U24-ch54

calib_packv5_042523_0143.root, FC#1, port C1

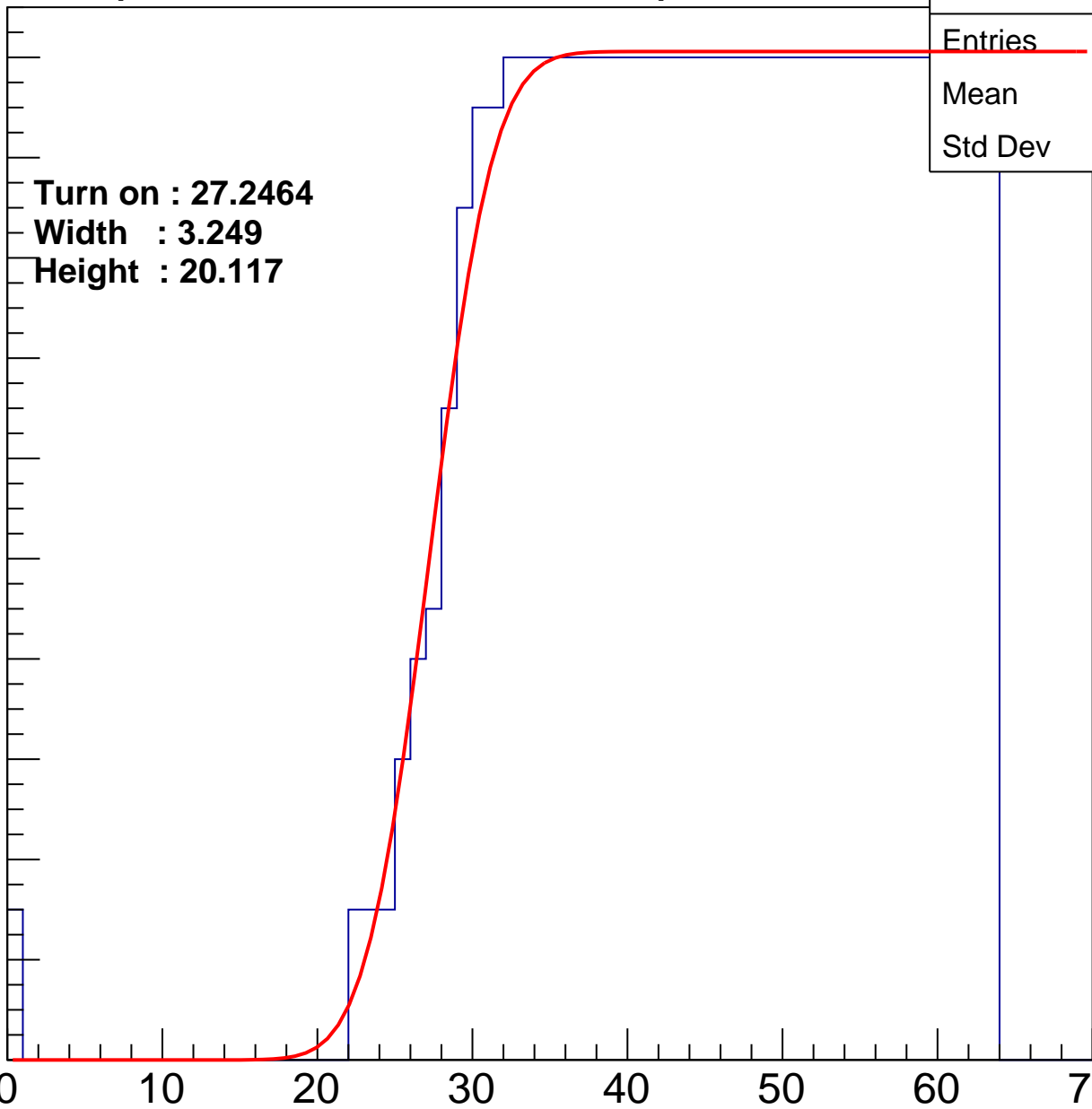
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2464
Width : 3.249
Height : 20.117

Entries	743
Mean	44.72
Std Dev	11.2

ampl



B0L101S, U24-ch55

calib_packv5_042523_0143.root, FC#1, port C1

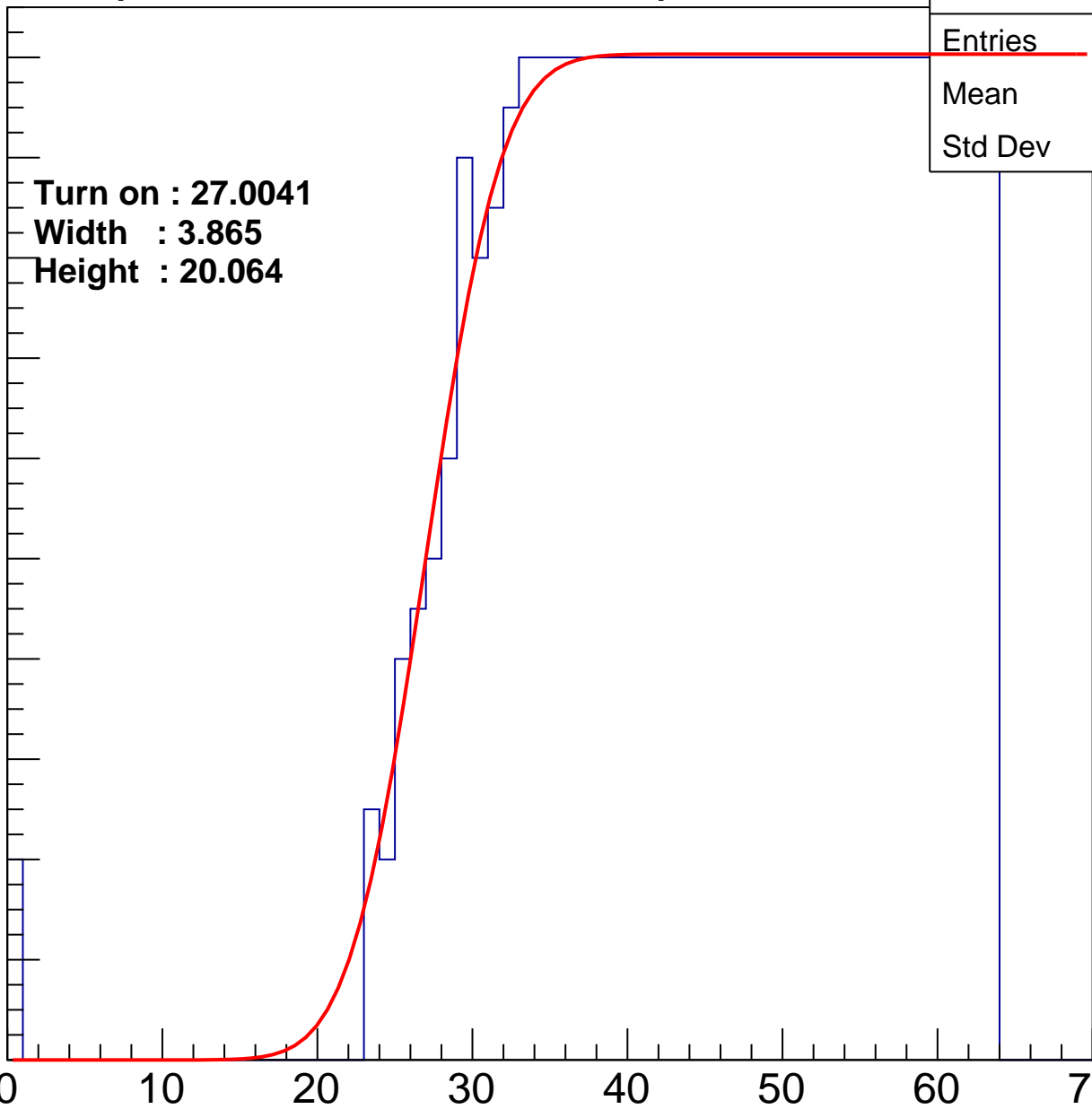
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0041
Width : 3.865
Height : 20.064

Entries	742
Mean	44.67
Std Dev	11.33

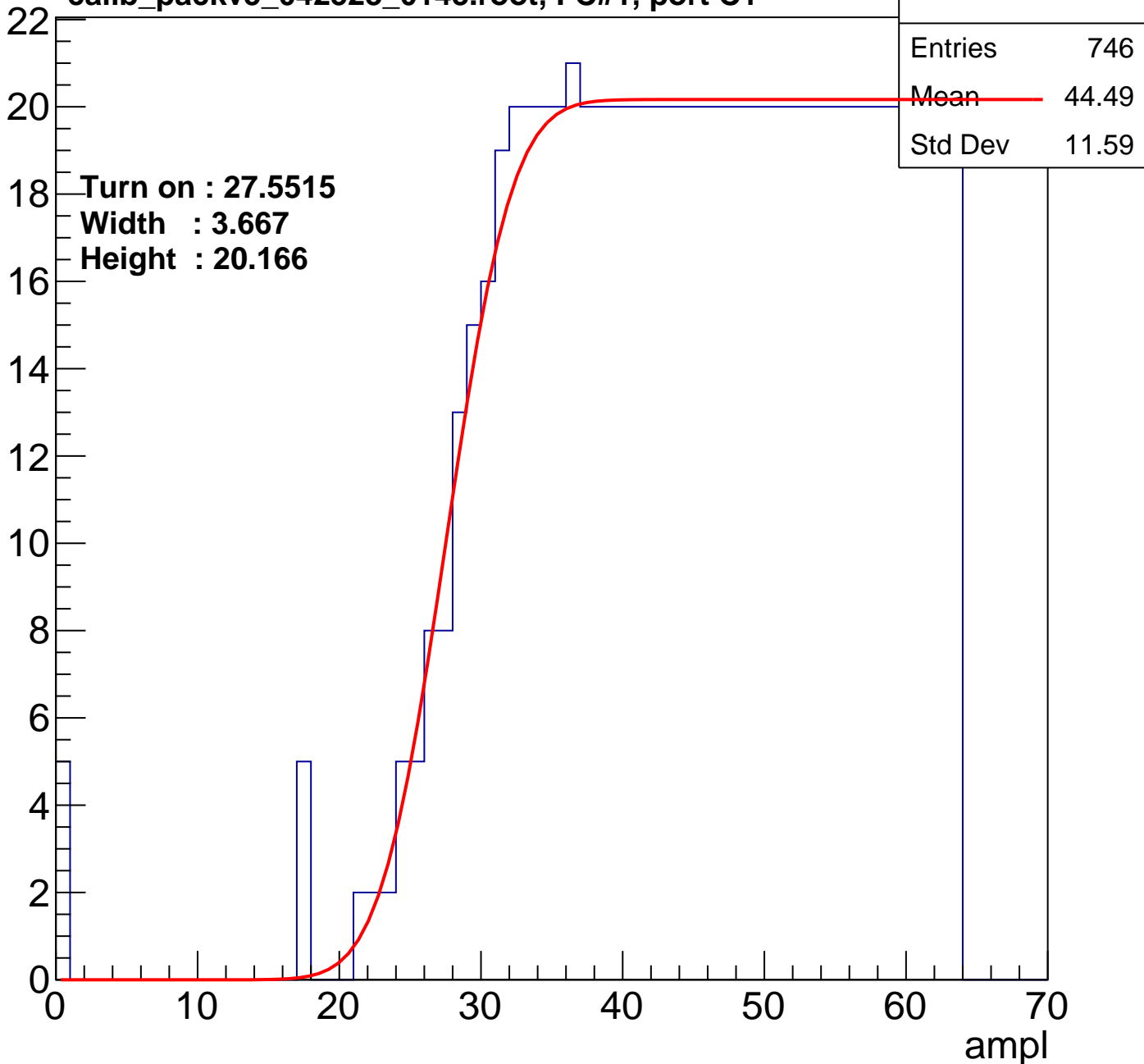
ampl



B0L101S, U24-ch56

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch57

calib_packv5_042523_0143.root, FC#1, port C1

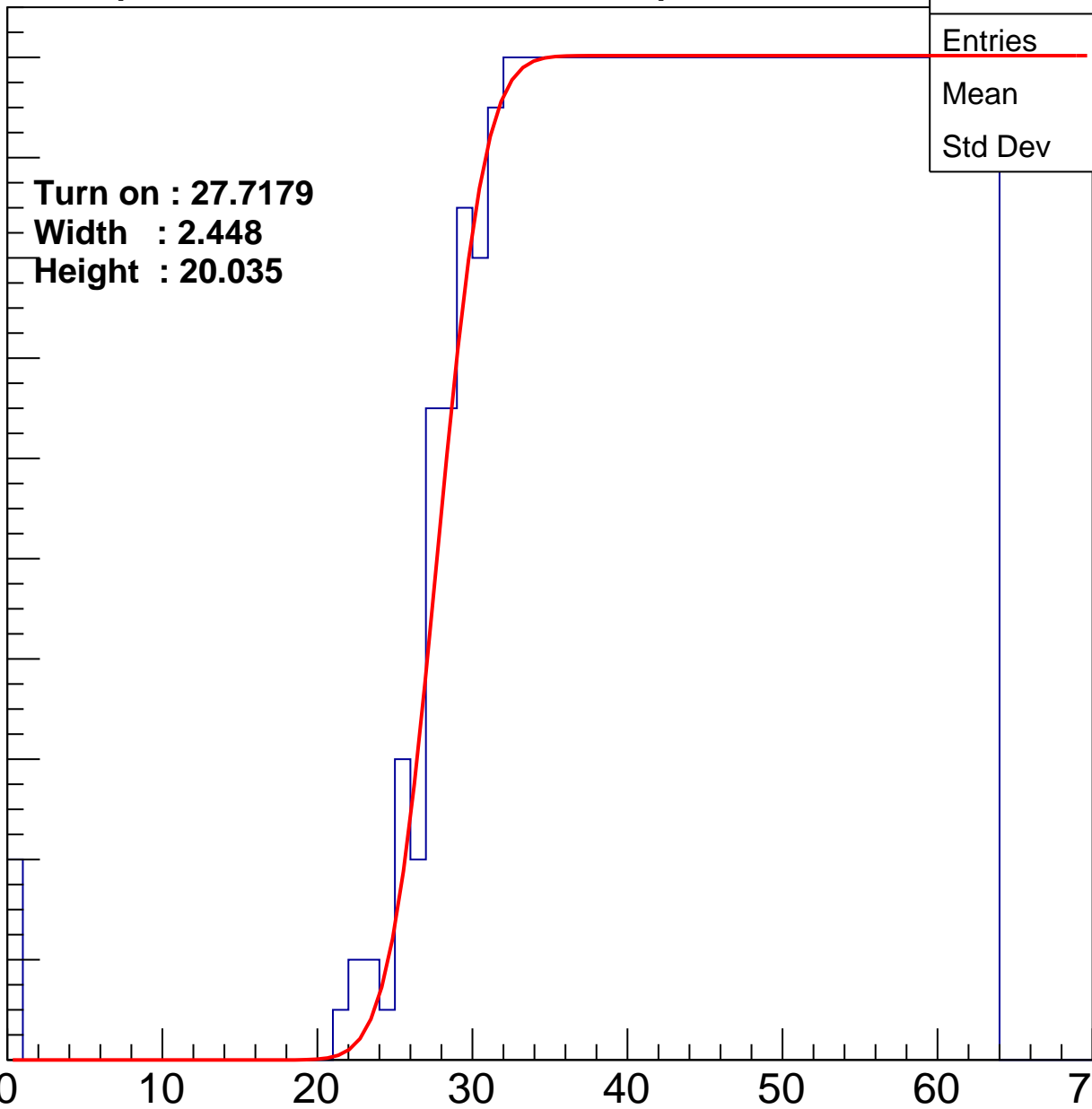
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7179
Width : 2.448
Height : 20.035

Entries	738
Mean	44.8
Std Dev	11.24

ampl



B0L101S, U24-ch58

calib_packv5_042523_0143.root, FC#1, port C1

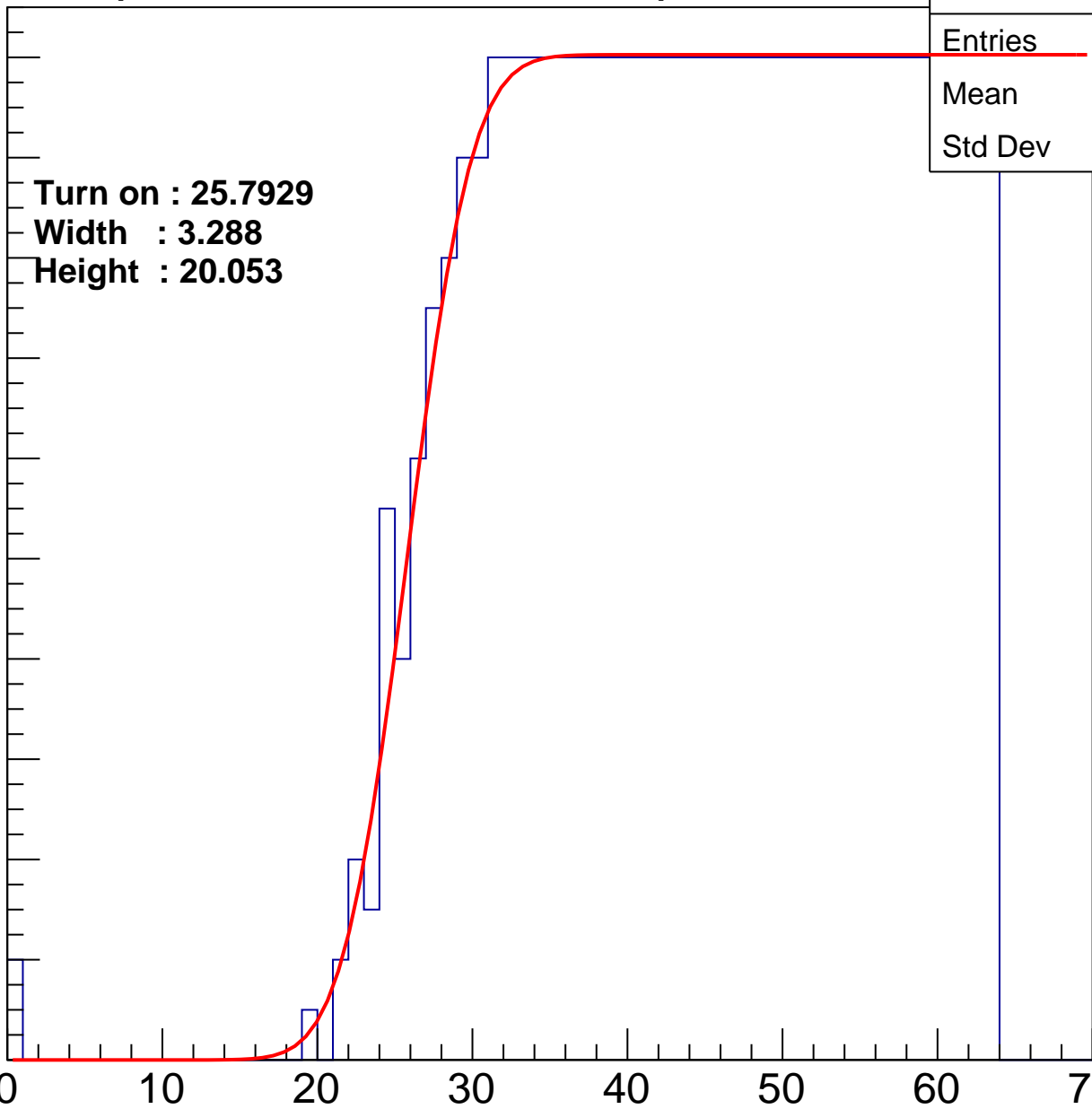
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7929
Width : 3.288
Height : 20.053

Entries	770
Mean	44.06
Std Dev	11.5

ampl



B0L101S, U24-ch59

calib_packv5_042523_0143.root, FC#1, port C1

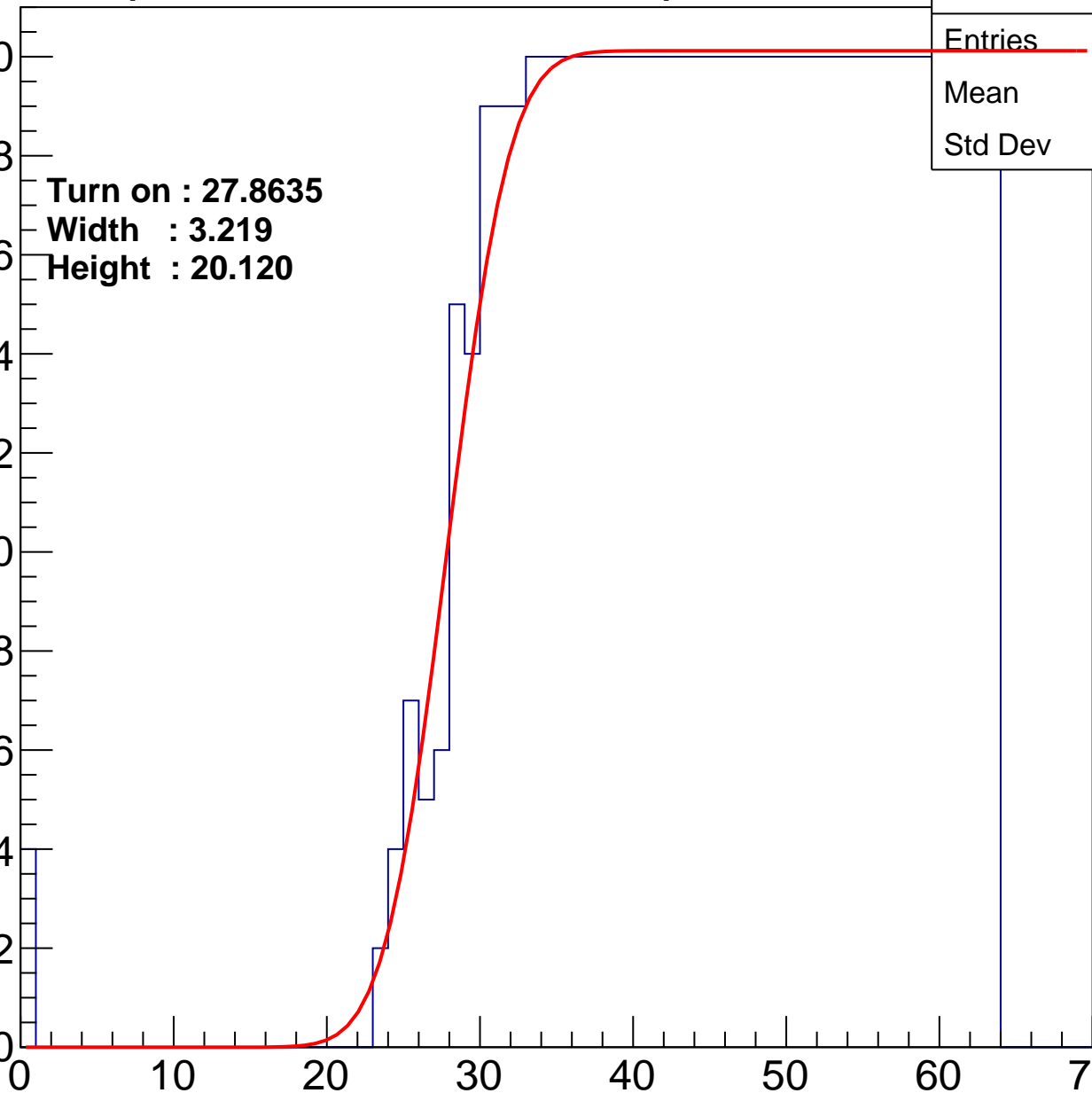
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8635
Width : 3.219
Height : 20.120

Entries	734
Mean	44.91
Std Dev	11.18

ampl



B0L101S, U24-ch60

calib_packv5_042523_0143.root, FC#1, port C1

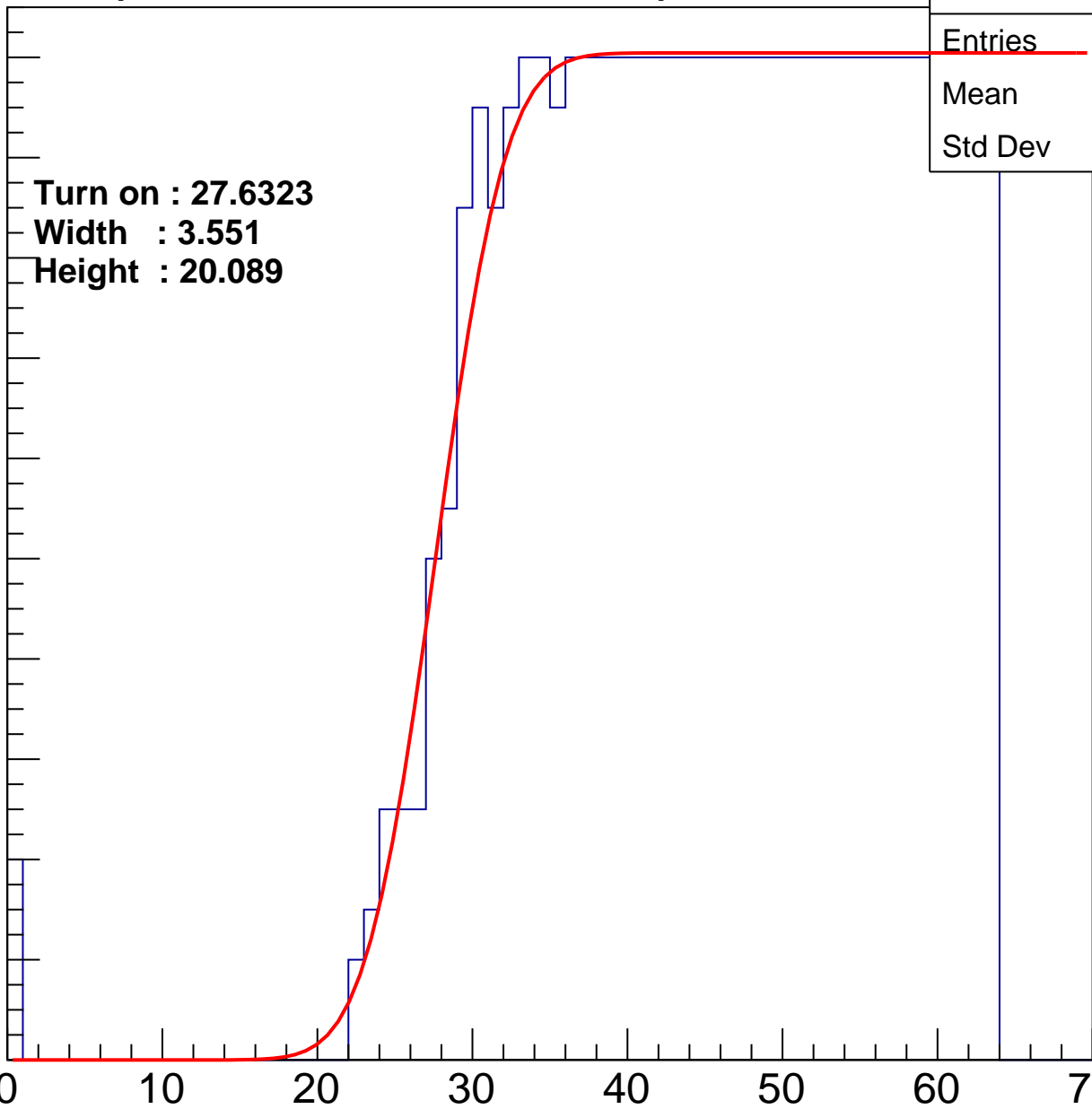
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6323
Width : 3.551
Height : 20.089

Entries	736
Mean	44.82
Std Dev	11.26

ampl



B0L101S, U24-ch61

calib_packv5_042523_0143.root, FC#1, port C1

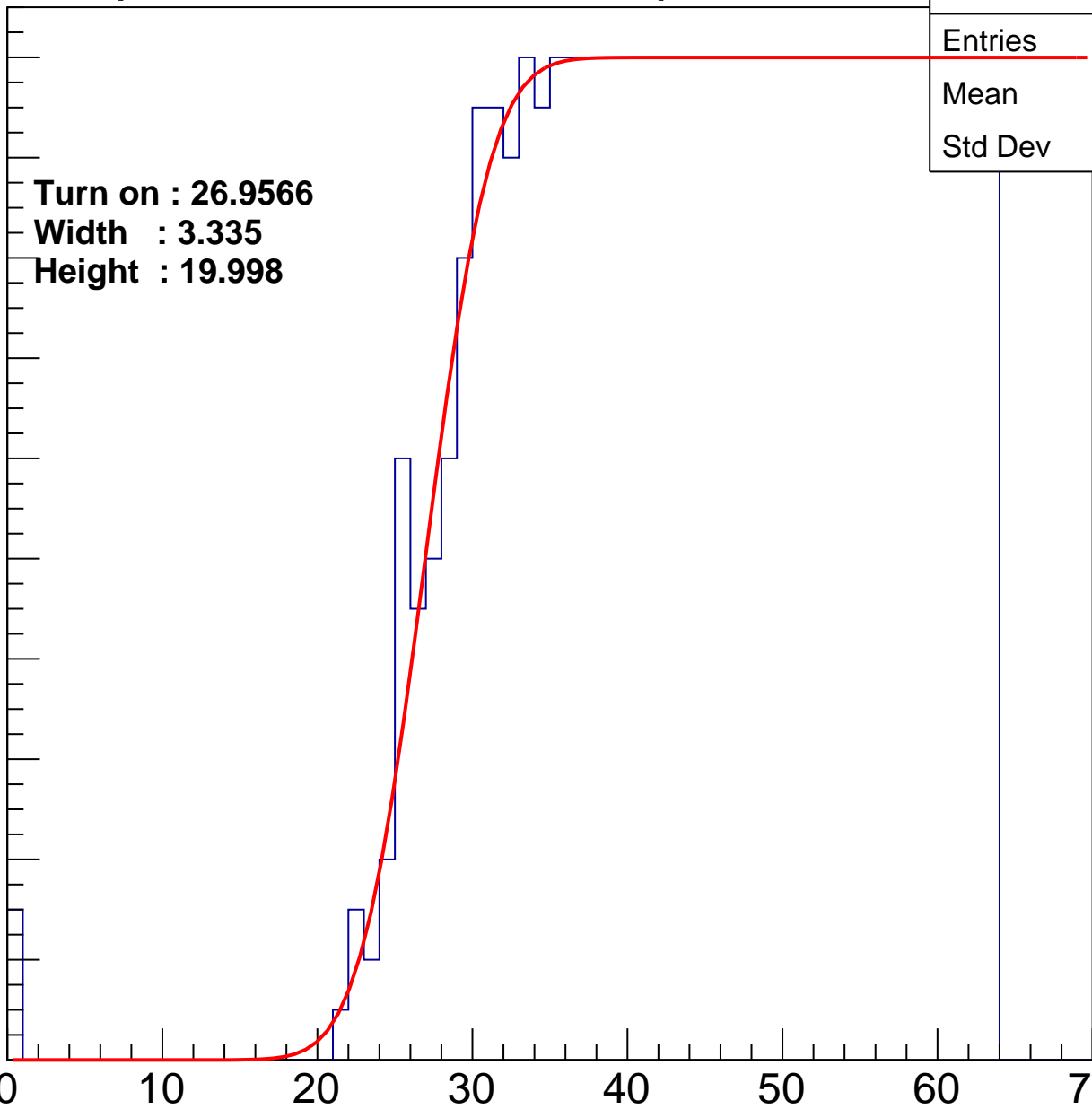
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9566
Width : 3.335
Height : 19.998

Entries	747
Mean	44.57
Std Dev	11.32

ampl



B0L101S, U24-ch62

calib_packv5_042523_0143.root, FC#1, port C1

Entry

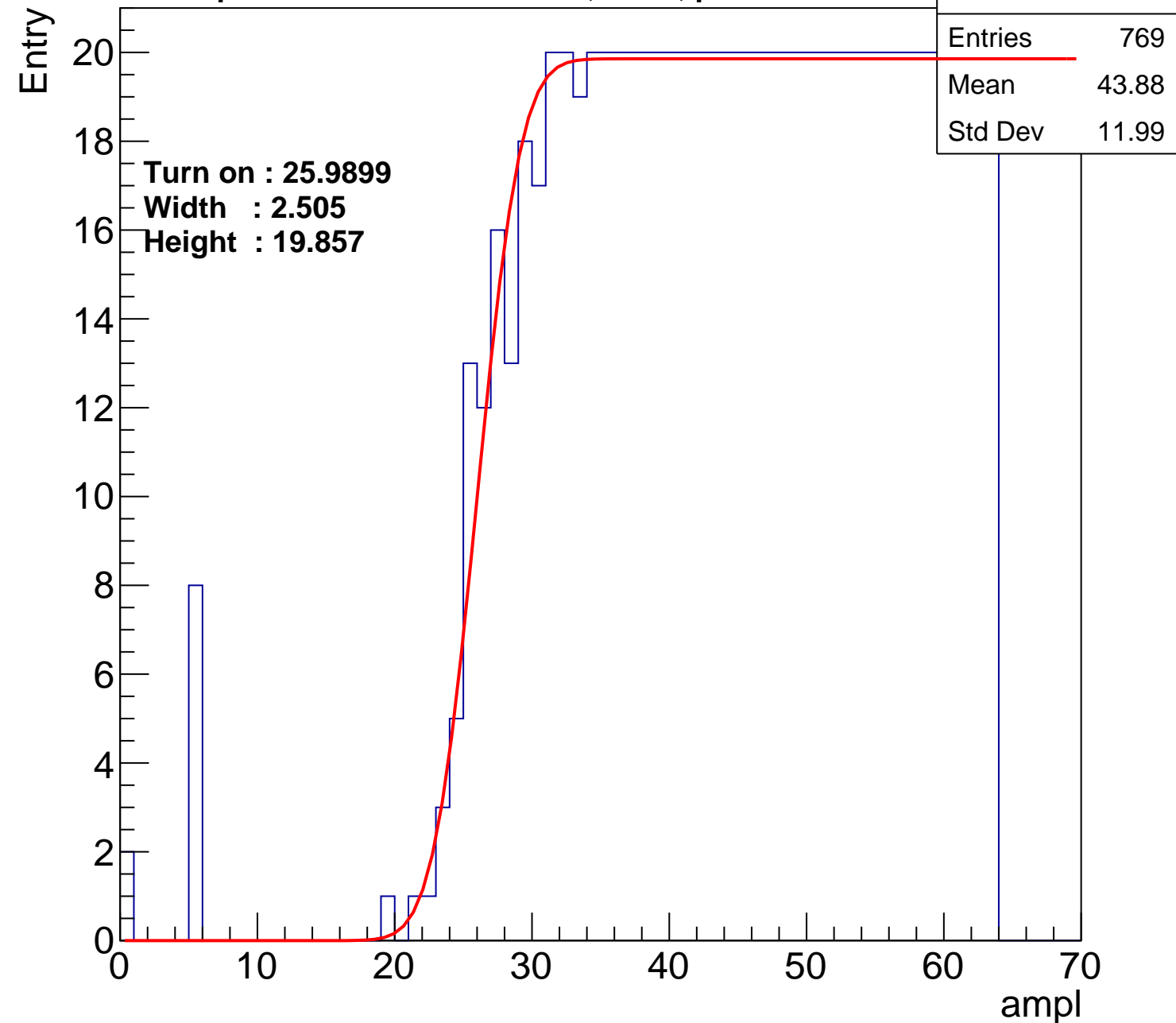
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9899
Width : 2.505
Height : 19.857

Entries	769
Mean	43.88
Std Dev	11.99

ampl

0 10 20 30 40 50 60 70



B0L101S, U24-ch63

calib_packv5_042523_0143.root, FC#1, port C1

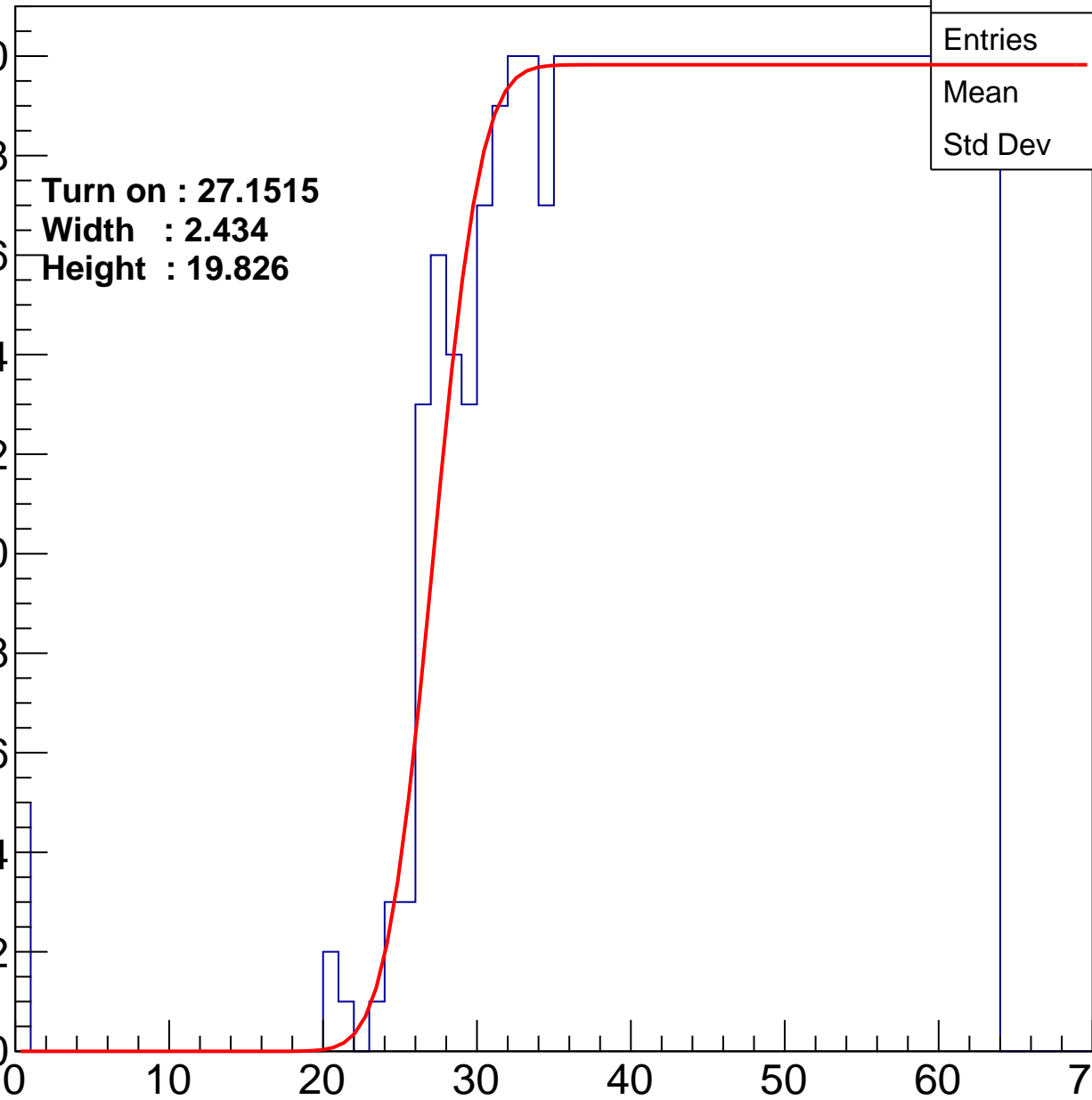
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1515
Width : 2.434
Height : 19.826

Entries	744
Mean	44.58
Std Dev	11.47

ampl



B0L101S, U24-ch64

calib_packv5_042523_0143.root, FC#1, port C1

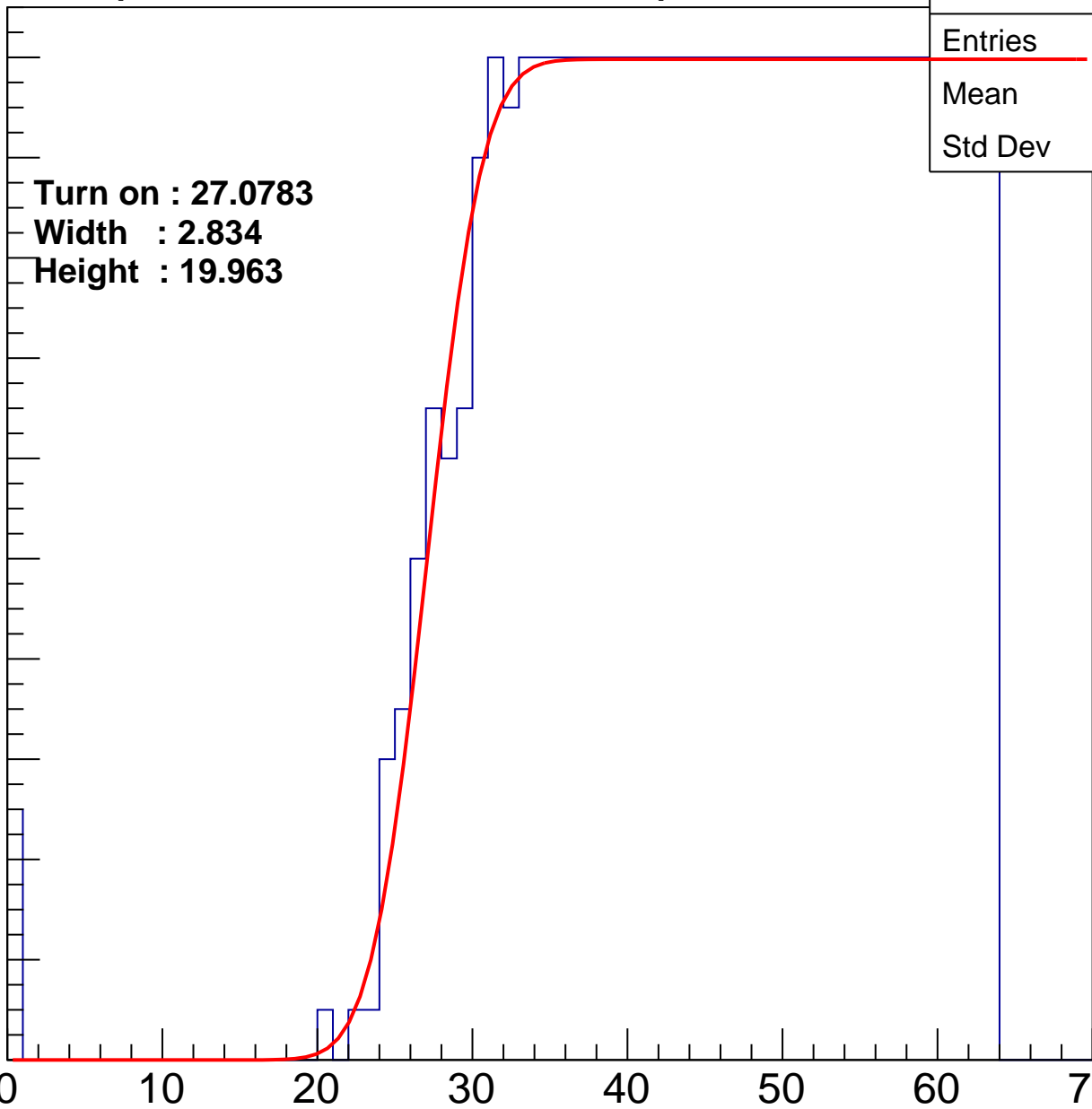
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0783
Width : 2.834
Height : 19.963

Entries	746
Mean	44.55
Std Dev	11.46

ampl



B0L101S, U24-ch65

calib_packv5_042523_0143.root, FC#1, port C1

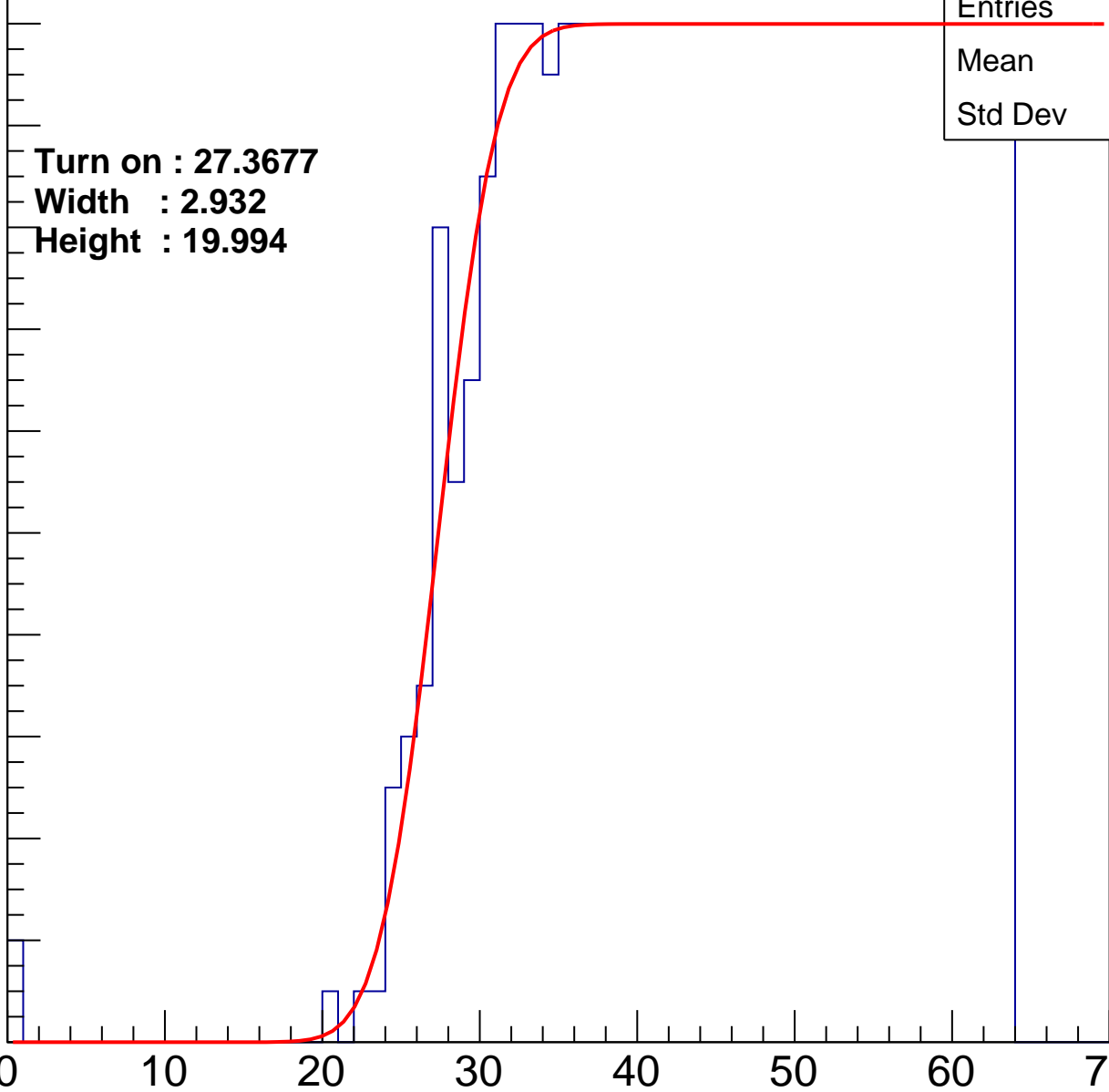
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3677
Width : 2.932
Height : 19.994

Entries	739
Mean	44.83
Std Dev	11.08

ampl



B0L101S, U24-ch66

calib_packv5_042523_0143.root, FC#1, port C1

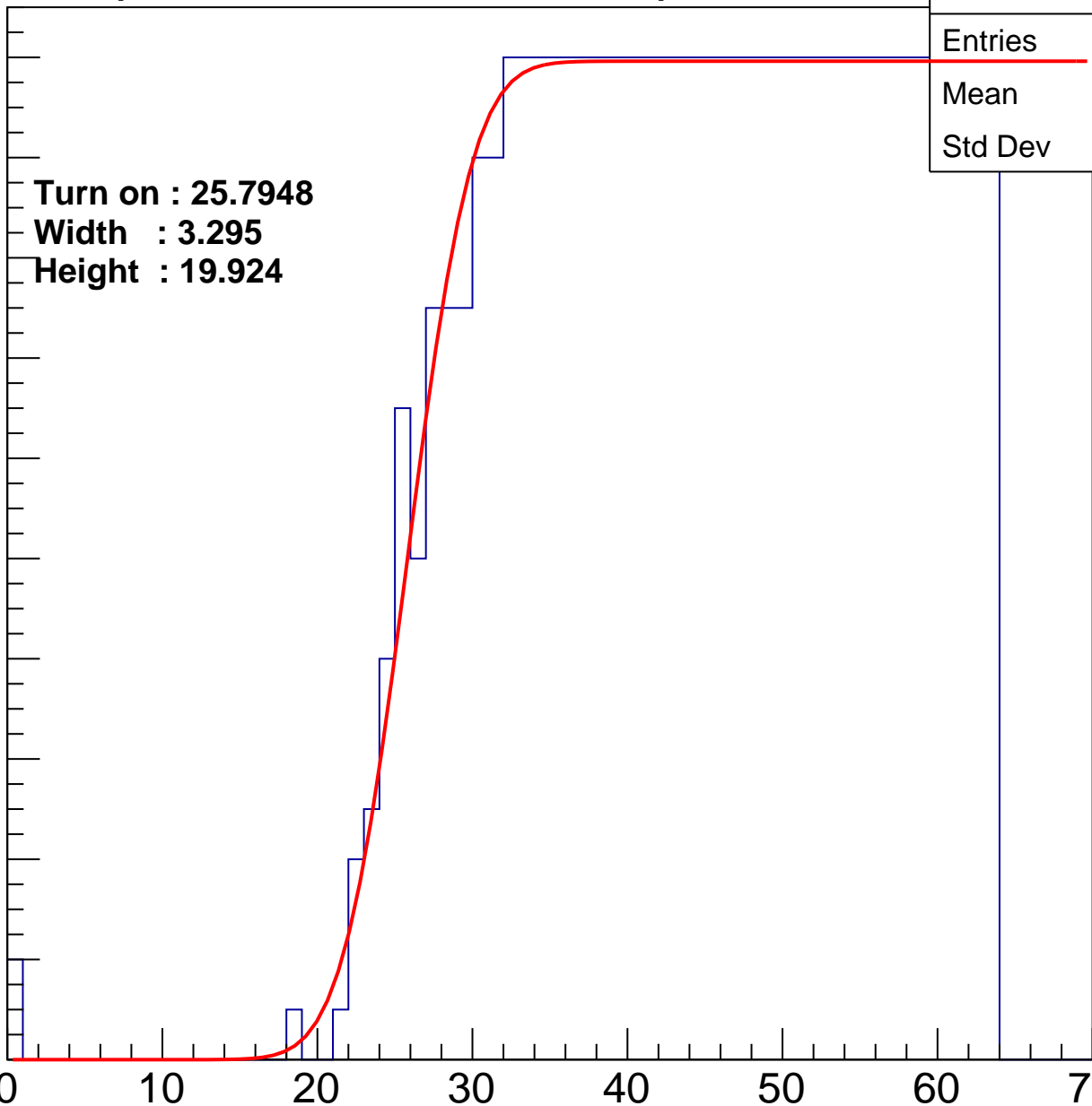
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7948
Width : 3.295
Height : 19.924

Entries	765
Mean	44.15
Std Dev	11.48

ampl



B0L101S, U24-ch67

calib_packv5_042523_0143.root, FC#1, port C1

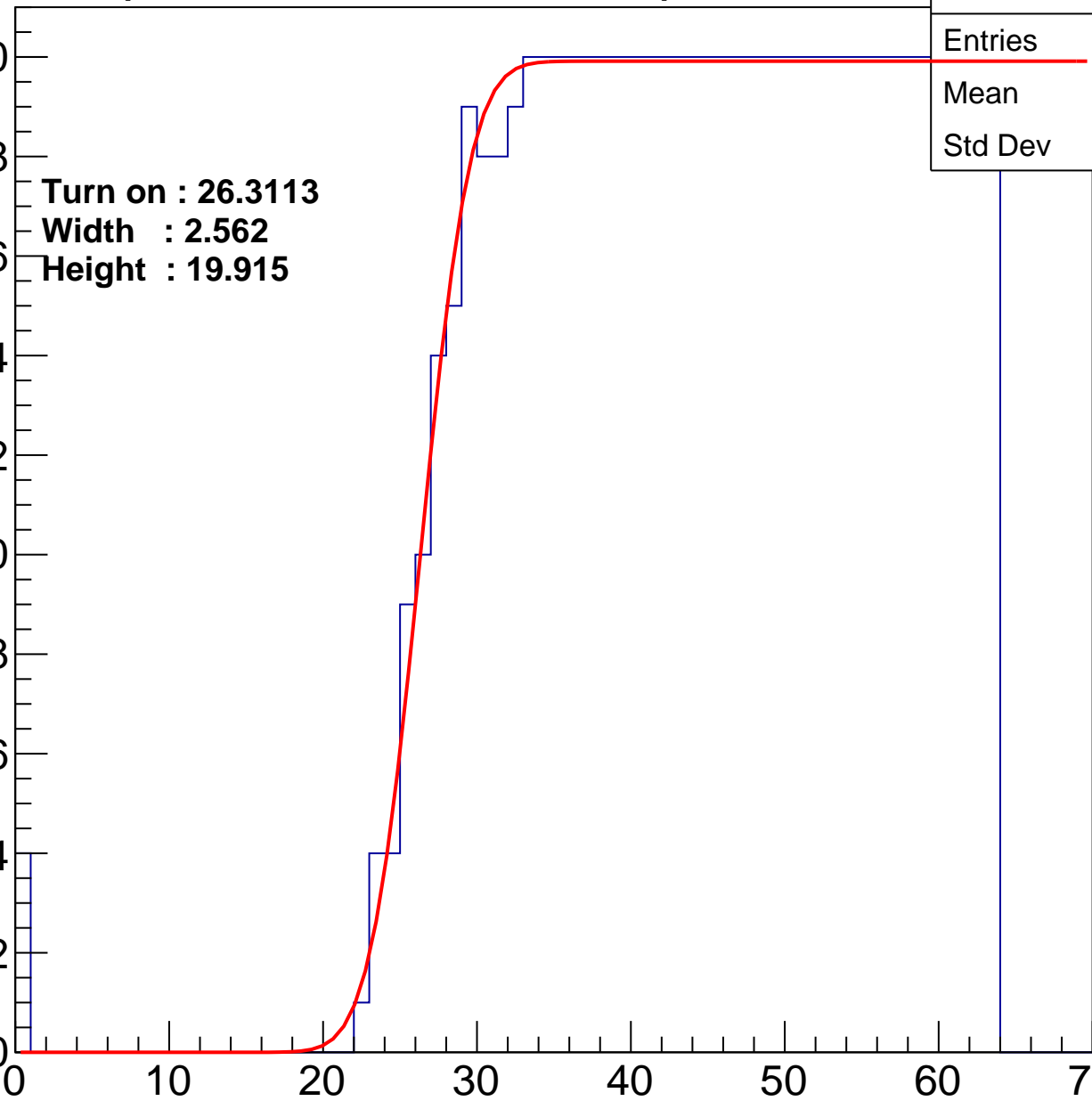
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3113
Width : 2.562
Height : 19.915

Entries	755
Mean	44.38
Std Dev	11.45

ampl



B0L101S, U24-ch68

calib_packv5_042523_0143.root, FC#1, port C1

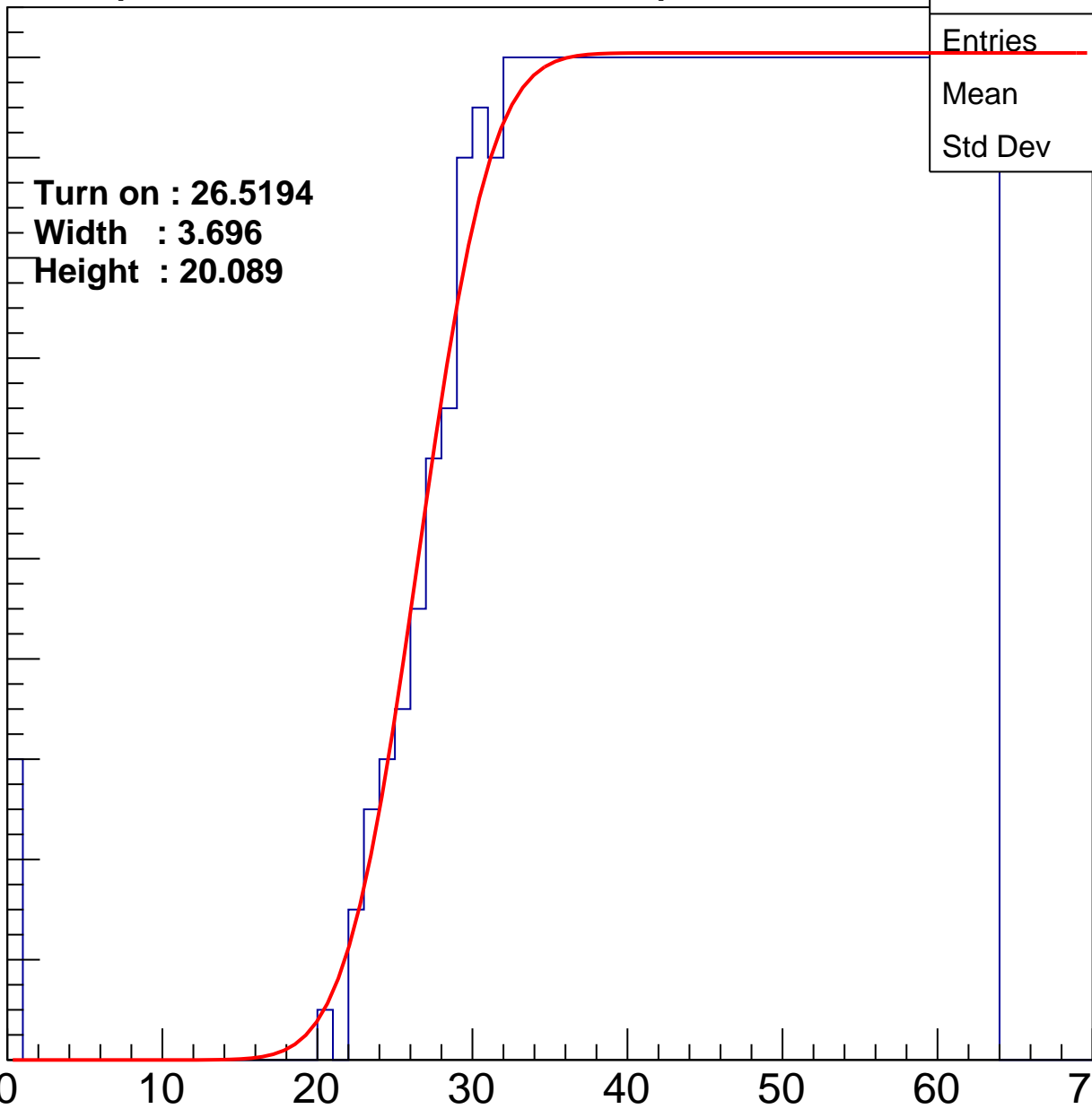
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5194
Width : 3.696
Height : 20.089

Entries	757
Mean	44.24
Std Dev	11.7

ampl



B0L101S, U24-ch69

calib_packv5_042523_0143.root, FC#1, port C1

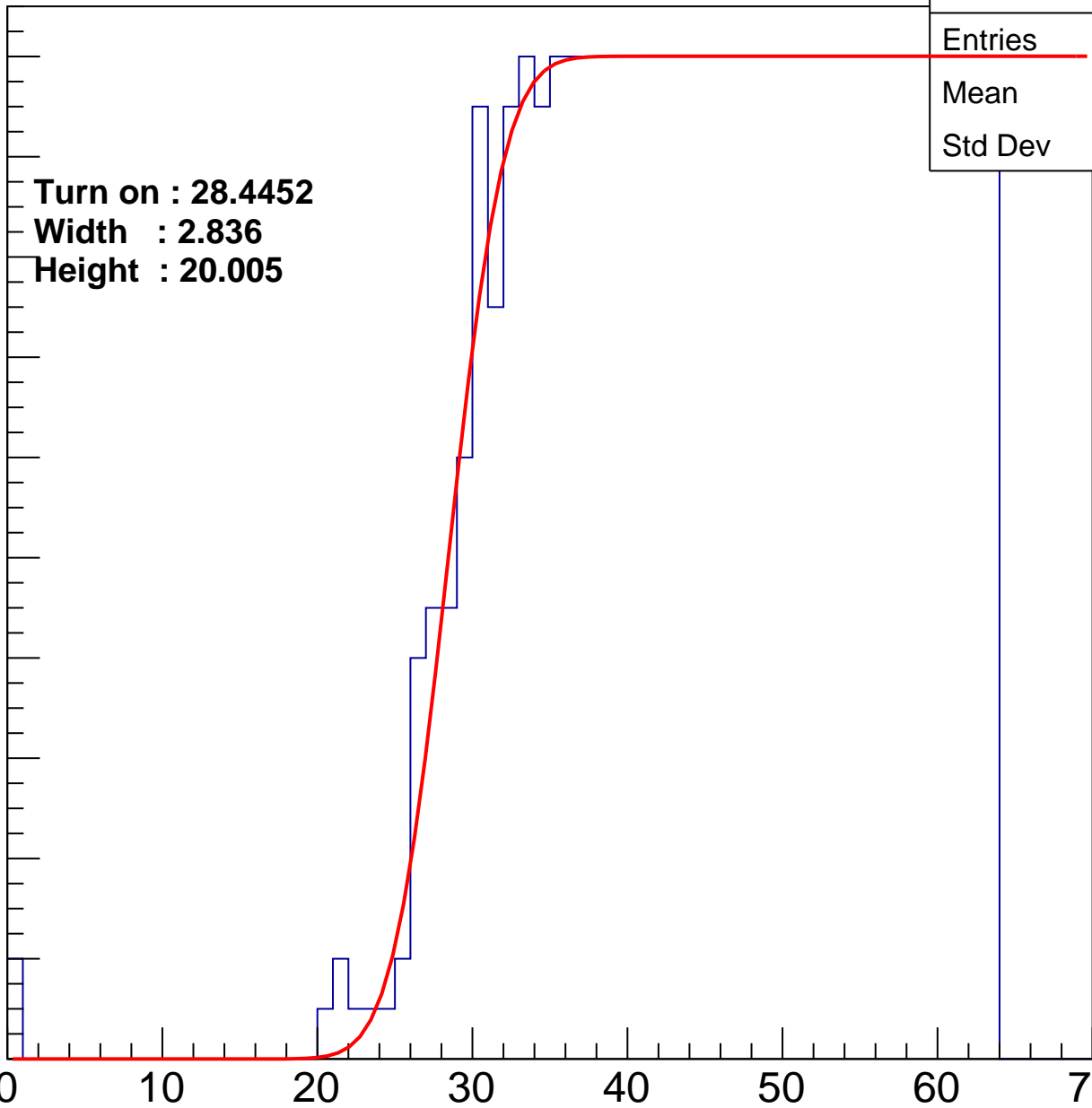
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4452
Width : 2.836
Height : 20.005

Entries	720
Mean	45.28
Std Dev	10.86

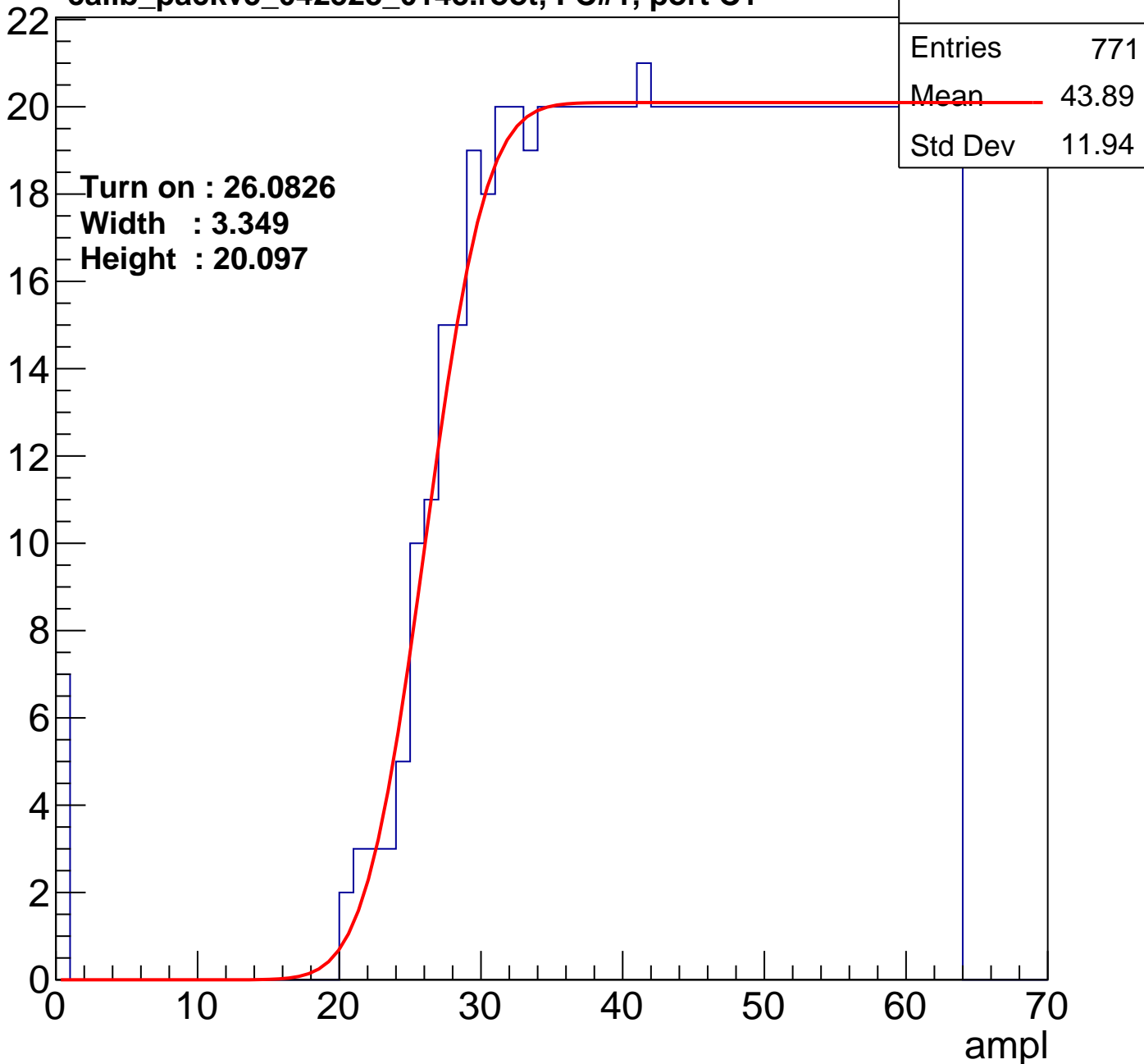
ampl



B0L101S, U24-ch70

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch71

calib_packv5_042523_0143.root, FC#1, port C1

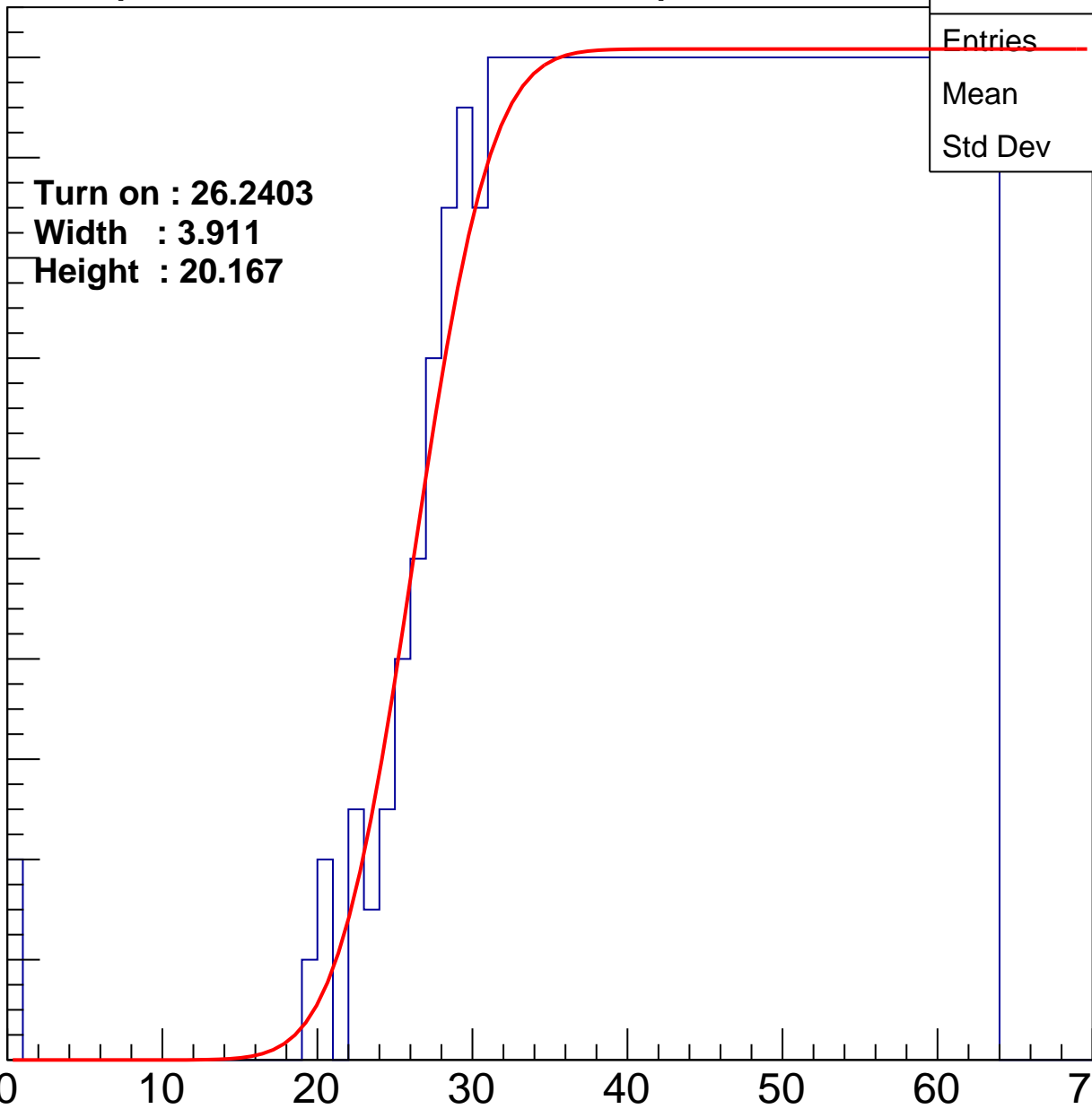
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2403
Width : 3.911
Height : 20.167

Entries	768
Mean	44.03
Std Dev	11.69

ampl



B0L101S, U24-ch72

calib_packv5_042523_0143.root, FC#1, port C1

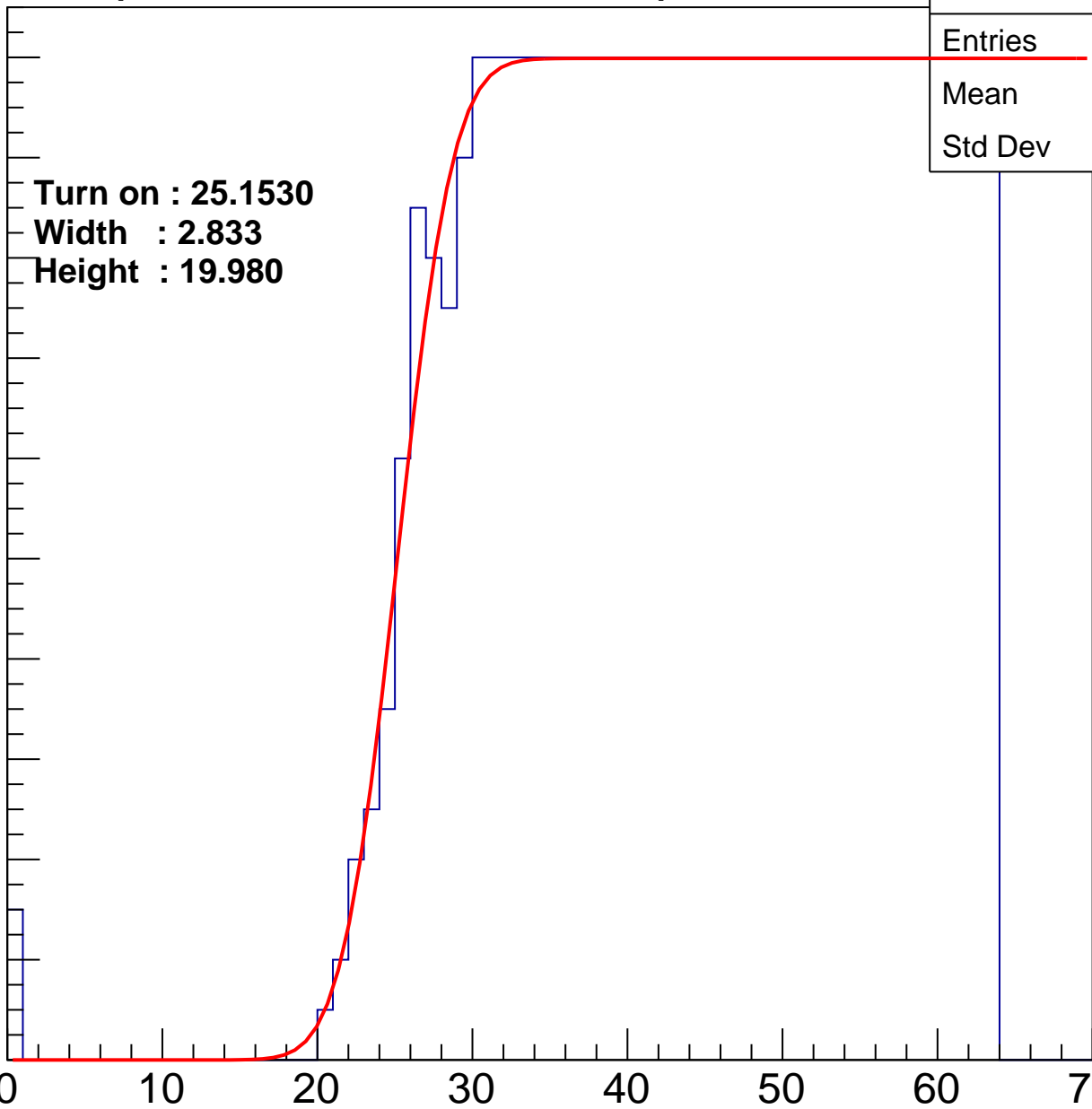
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1530
Width : 2.833
Height : 19.980

Entries	780
Mean	43.81
Std Dev	11.68

ampl



B0L101S, U24-ch73

calib_packv5_042523_0143.root, FC#1, port C1

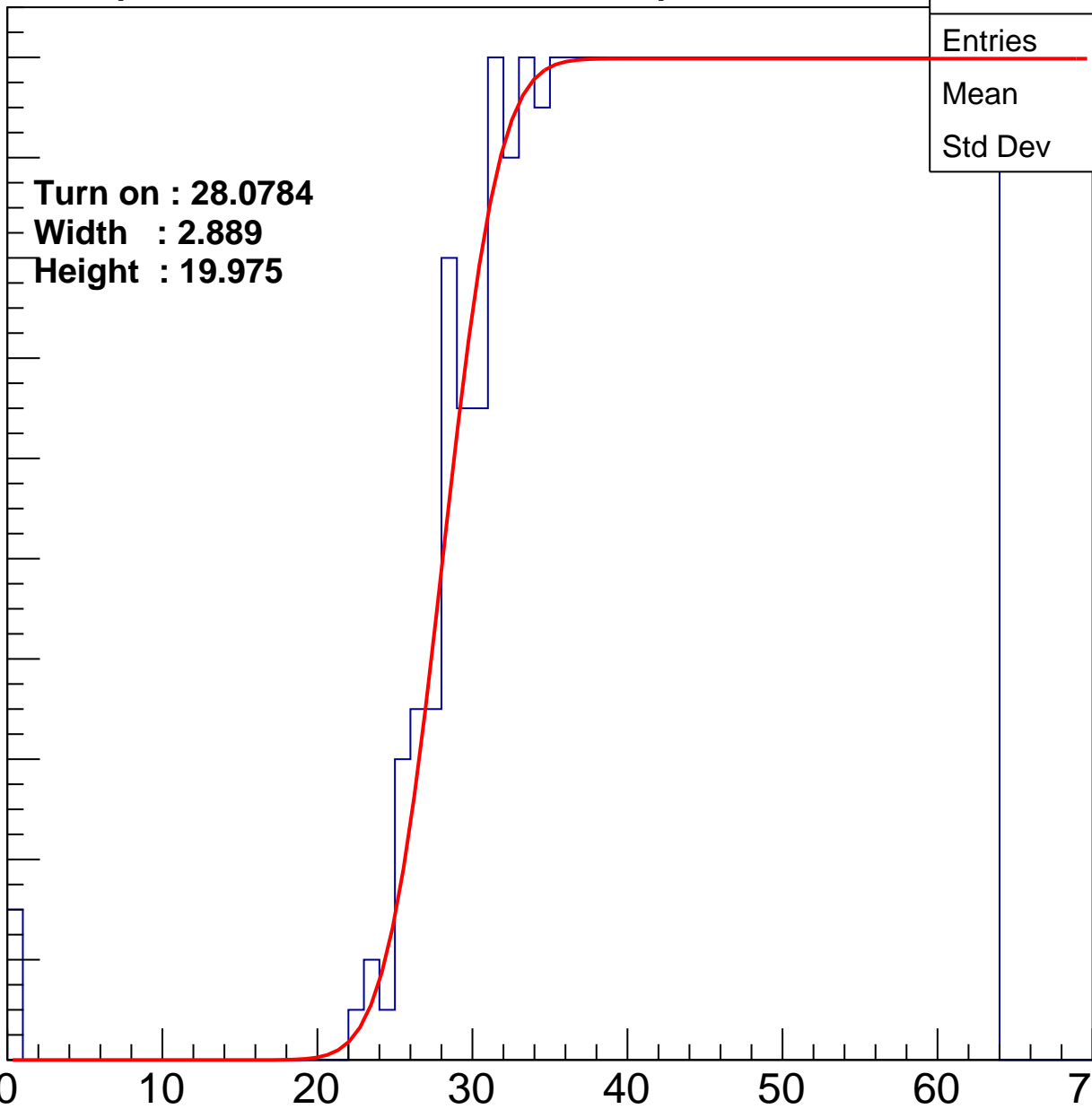
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0784
Width : 2.889
Height : 19.975

Entries	726
Mean	45.11
Std Dev	11.02

ampl



B0L101S, U24-ch74

calib_packv5_042523_0143.root, FC#1, port C1

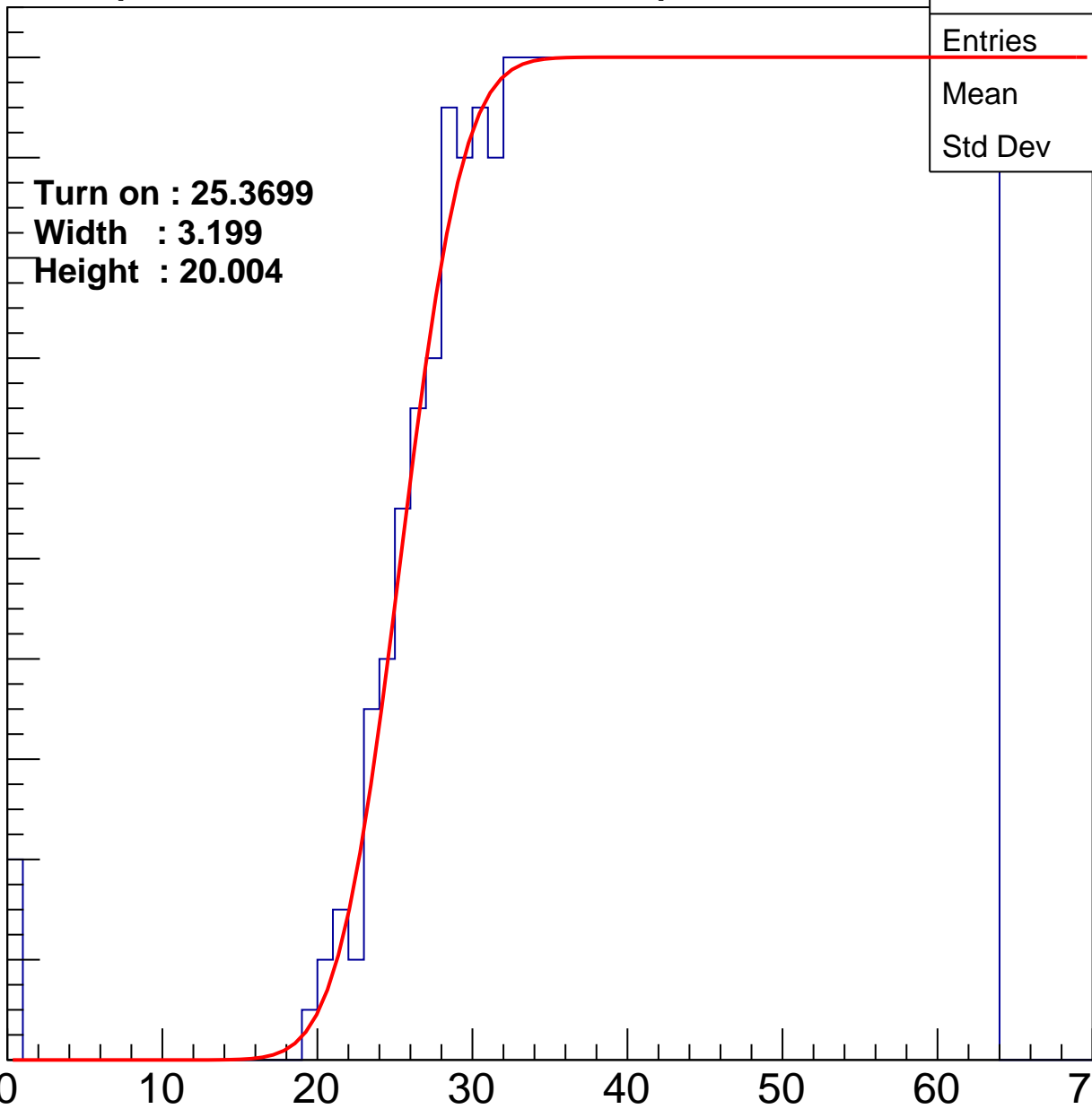
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3699
Width : 3.199
Height : 20.004

Entries	779
Mean	43.76
Std Dev	11.81

ampl



B0L101S, U24-ch75

calib_packv5_042523_0143.root, FC#1, port C1

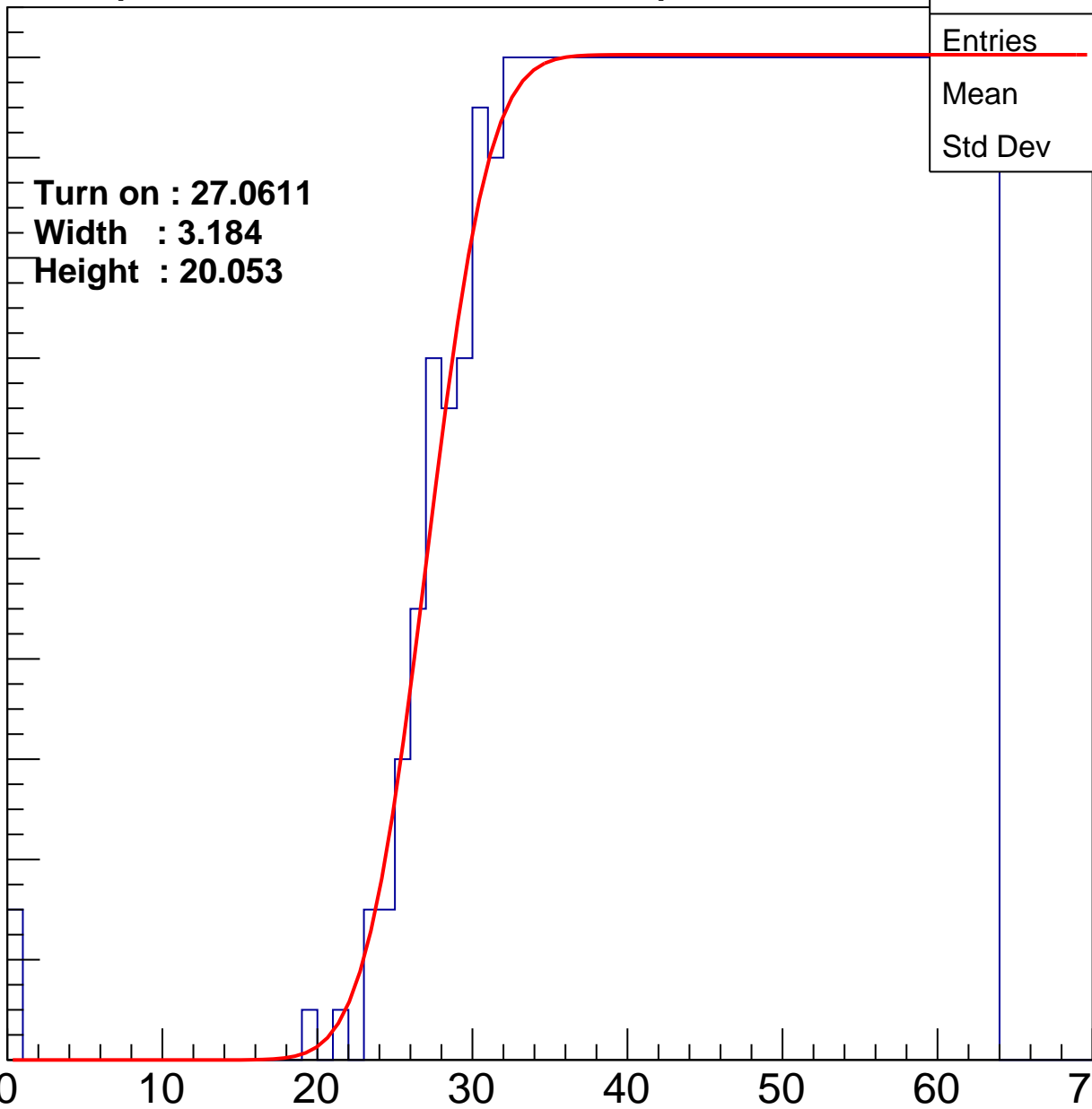
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0611
Width : 3.184
Height : 20.053

Entries	744
Mean	44.68
Std Dev	11.24

ampl



B0L101S, U24-ch76

calib_packv5_042523_0143.root, FC#1, port C1

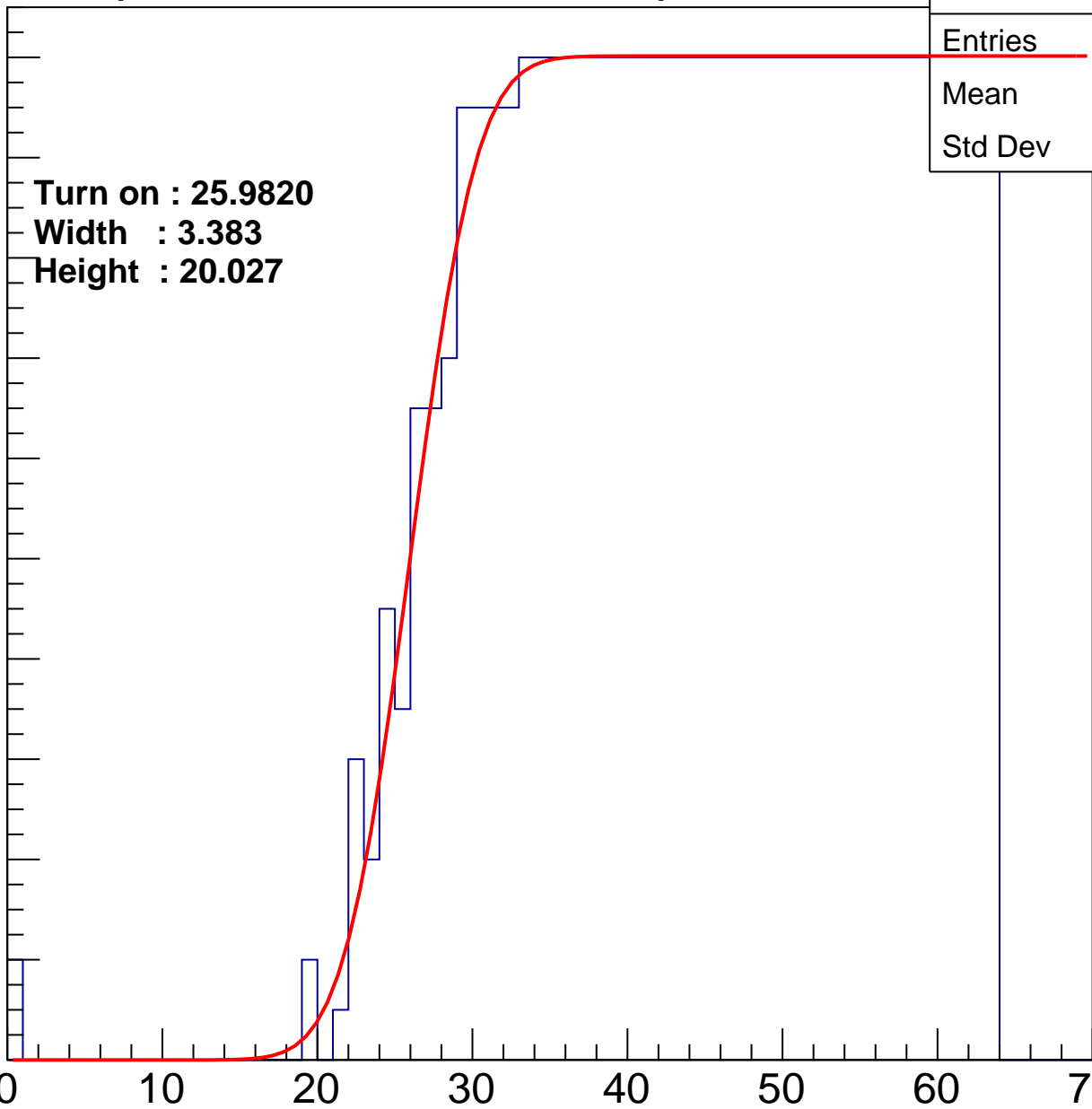
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9820
Width : 3.383
Height : 20.027

Entries	767
Mean	44.11
Std Dev	11.5

ampl



B0L101S, U24-ch77

calib_packv5_042523_0143.root, FC#1, port C1

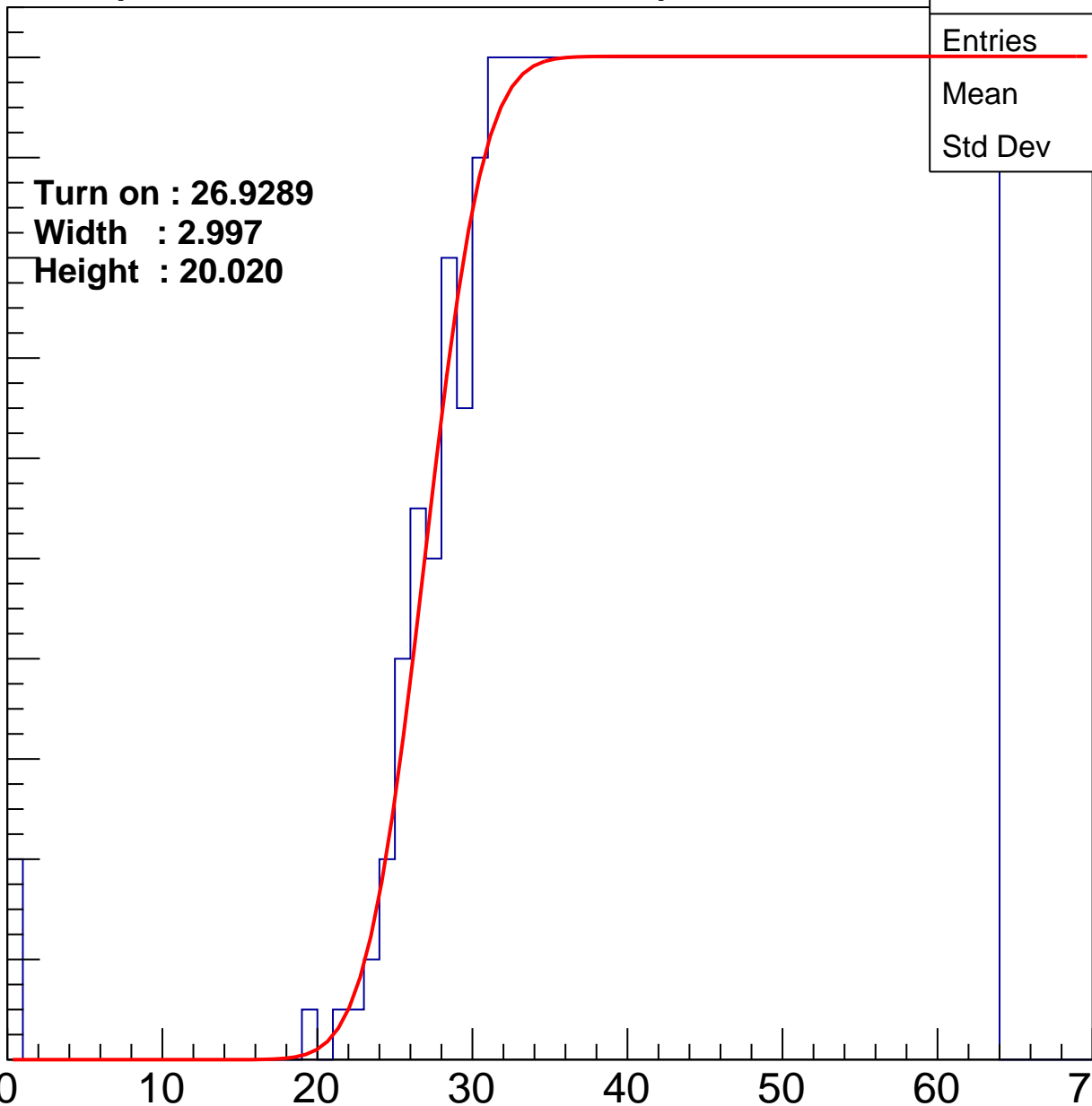
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9289
Width : 2.997
Height : 20.020

Entries	749
Mean	44.52
Std Dev	11.4

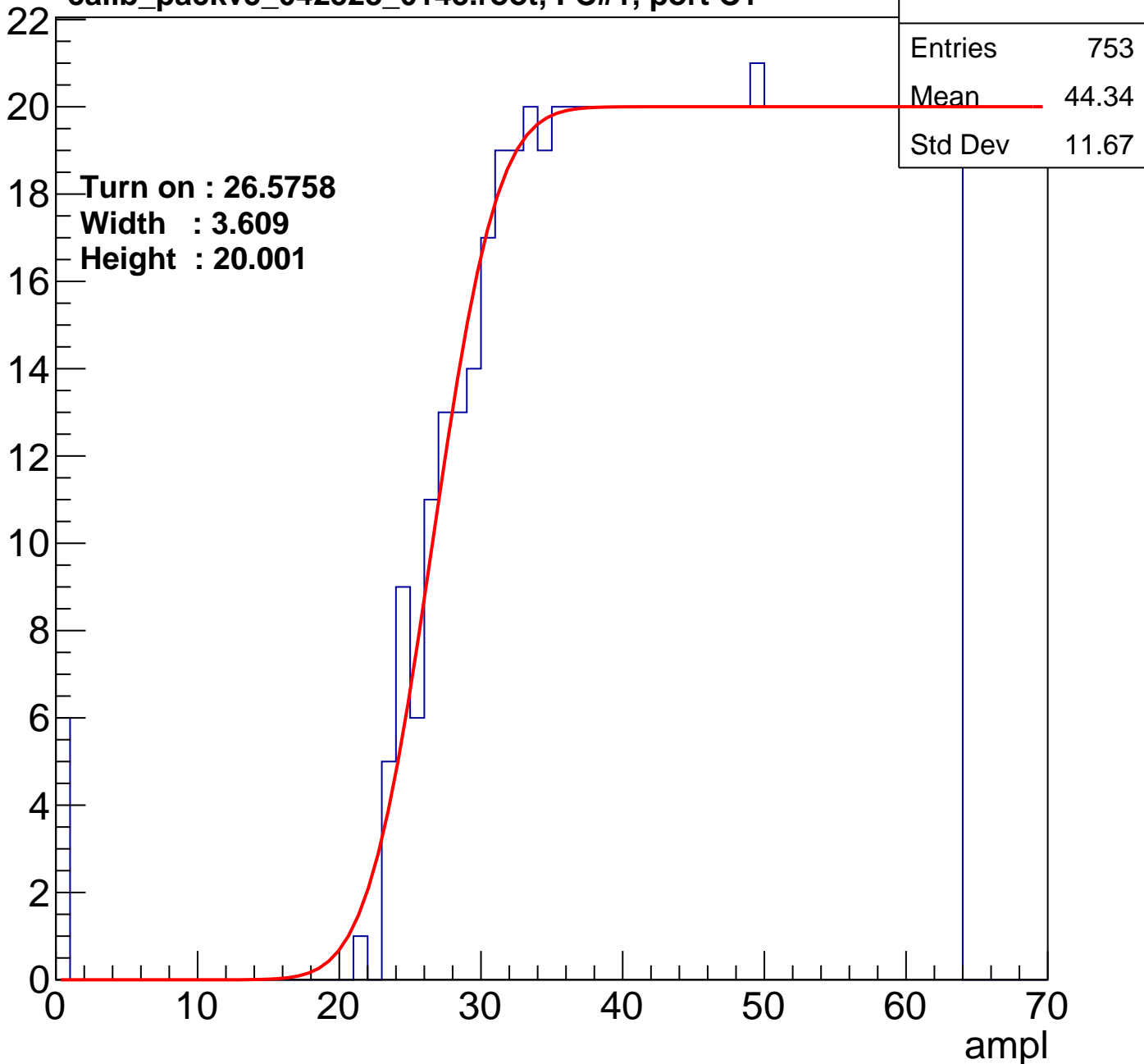
ampl



B0L101S, U24-ch78

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch79

calib_packv5_042523_0143.root, FC#1, port C1

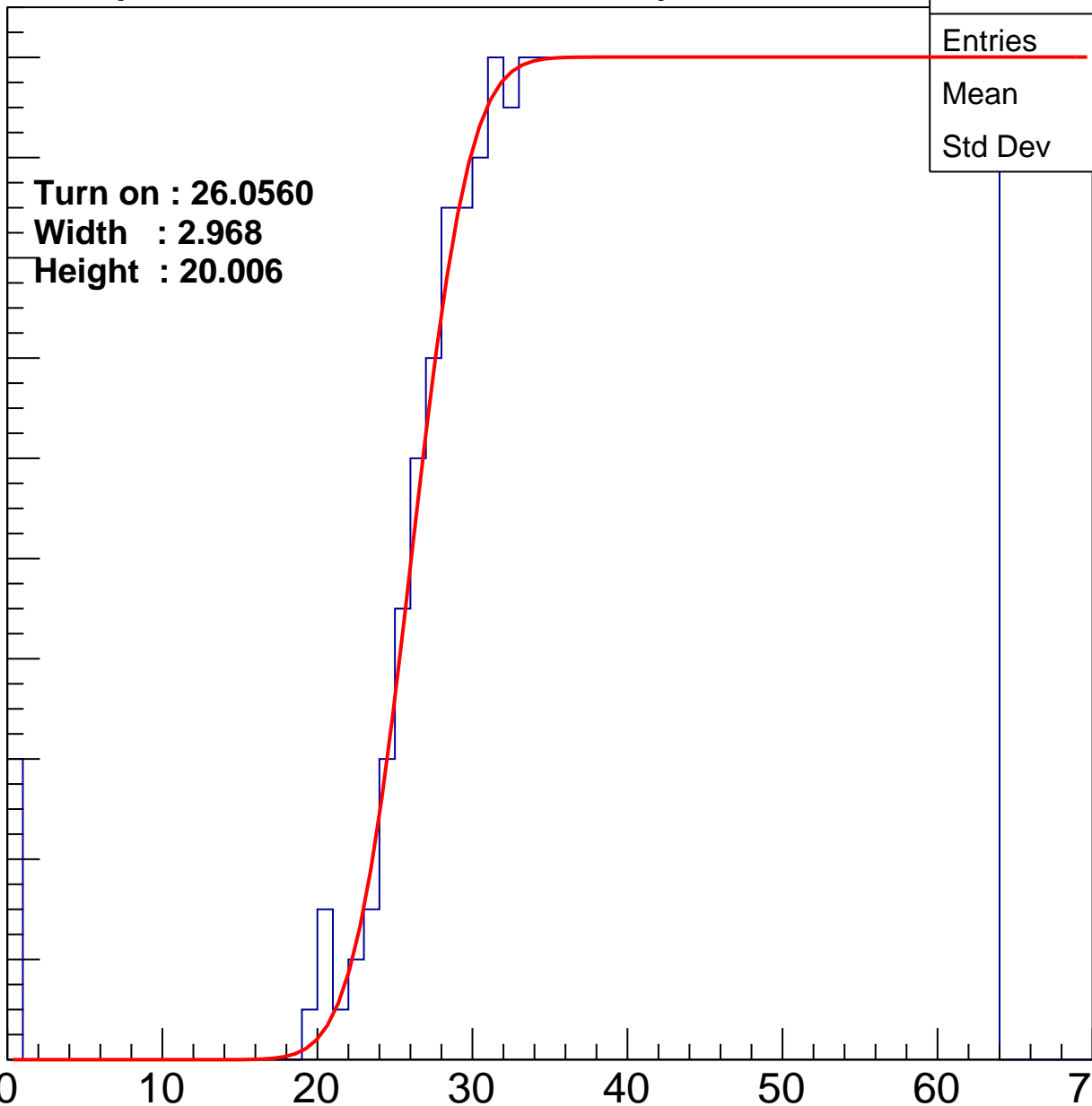
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0560
Width : 2.968
Height : 20.006

Entries	768
Mean	43.97
Std Dev	11.84

ampl



B0L101S, U24-ch80

calib_packv5_042523_0143.root, FC#1, port C1

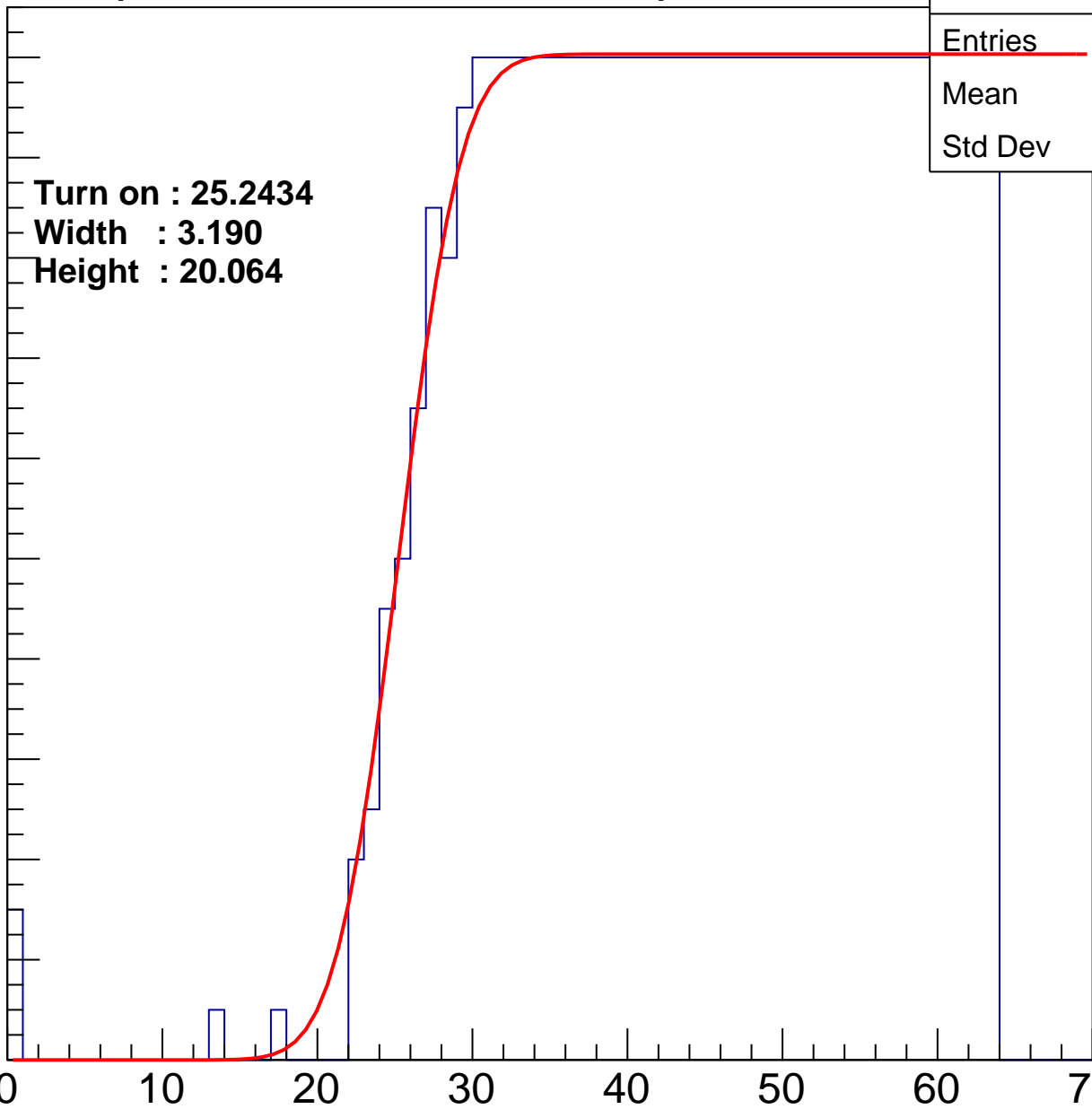
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2434
Width : 3.190
Height : 20.064

Entries	778
Mean	43.85
Std Dev	11.67

ampl



B0L101S, U24-ch81

calib_packv5_042523_0143.root, FC#1, port C1

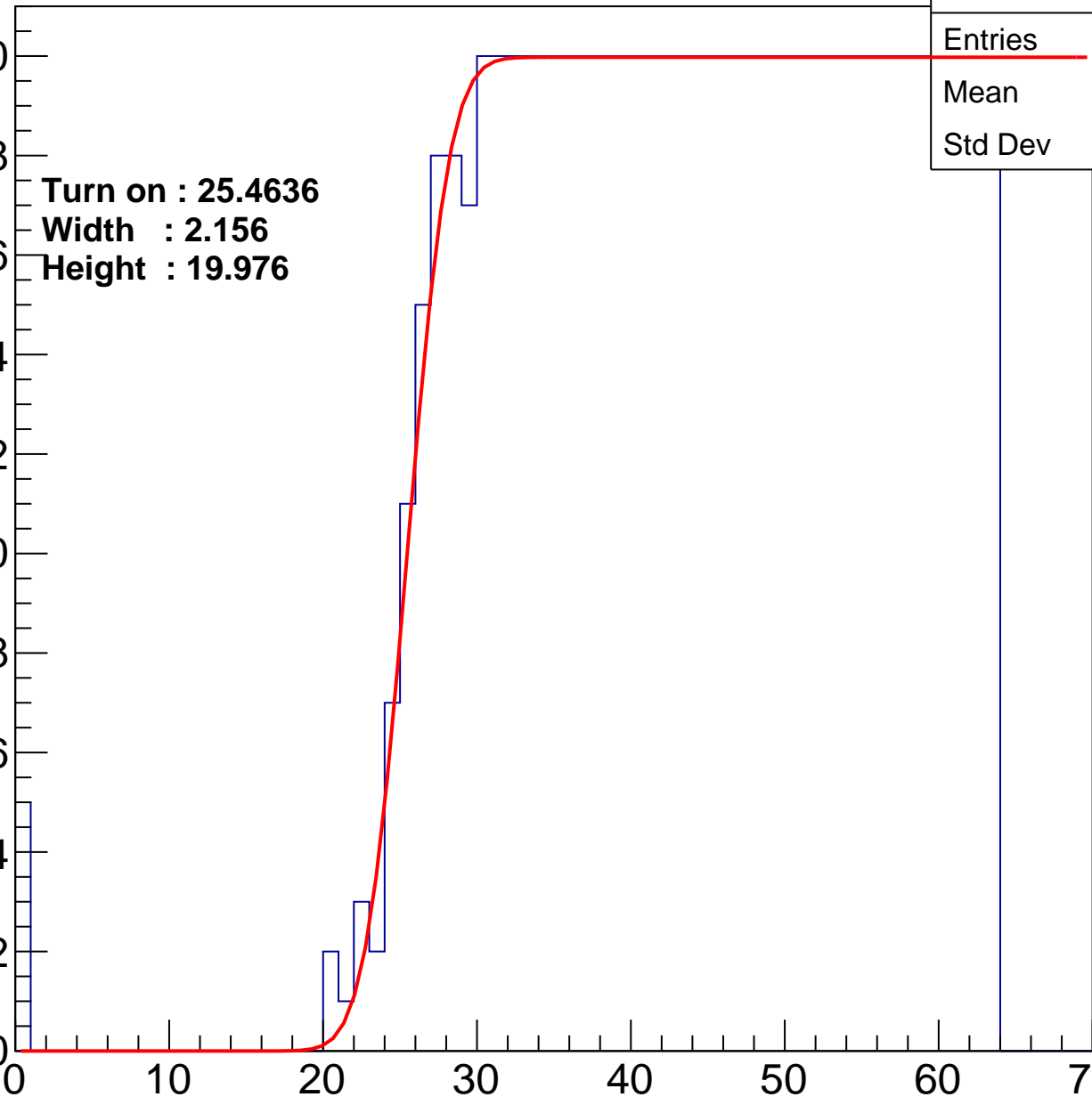
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4636
Width : 2.156
Height : 19.976

Entries	779
Mean	43.79
Std Dev	11.81

ampl



B0L101S, U24-ch82

calib_packv5_042523_0143.root, FC#1, port C1

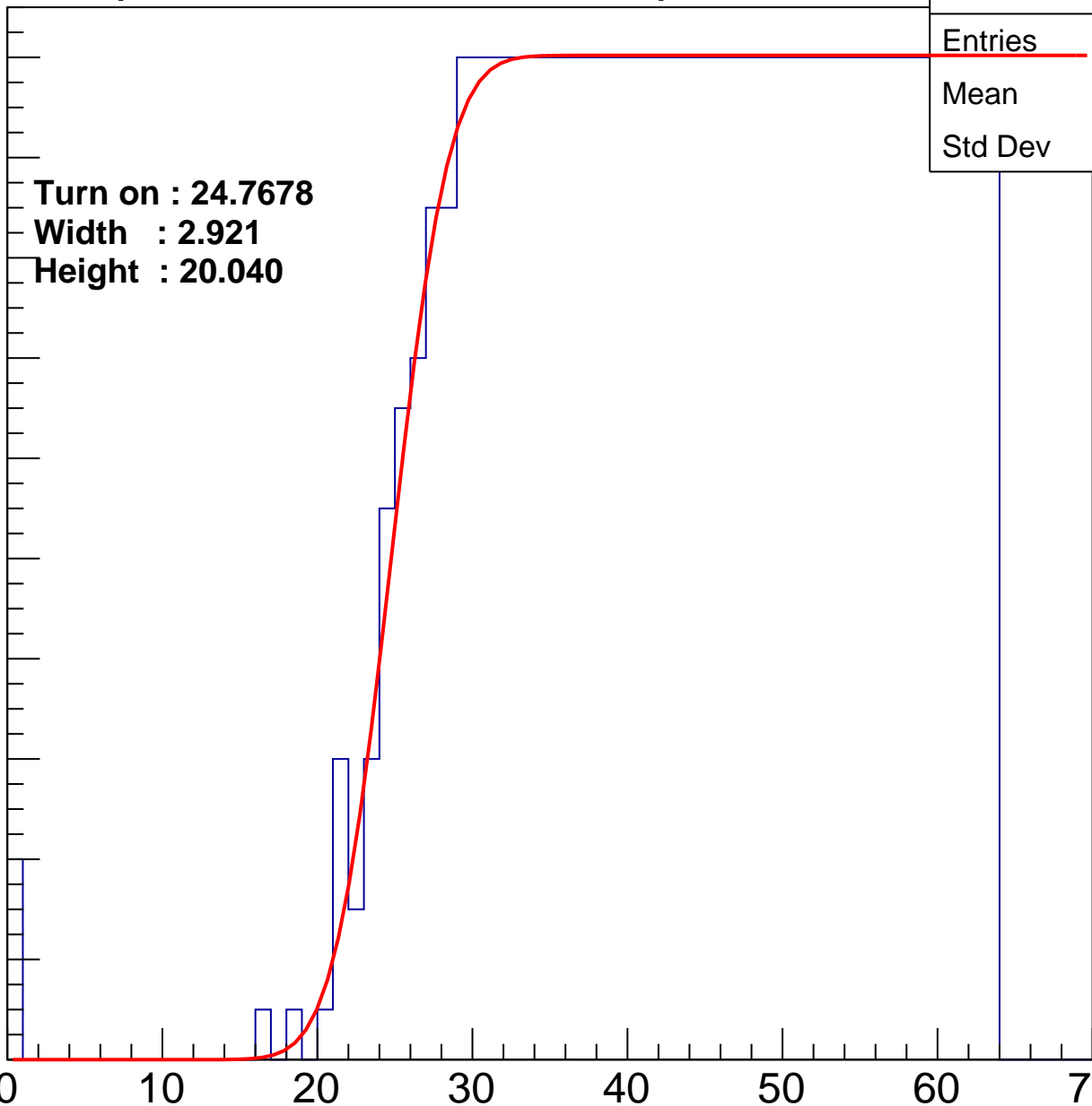
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7678
Width : 2.921
Height : 20.040

Entries	794
Mean	43.42
Std Dev	11.98

ampl



B0L101S, U24-ch83

calib_packv5_042523_0143.root, FC#1, port C1

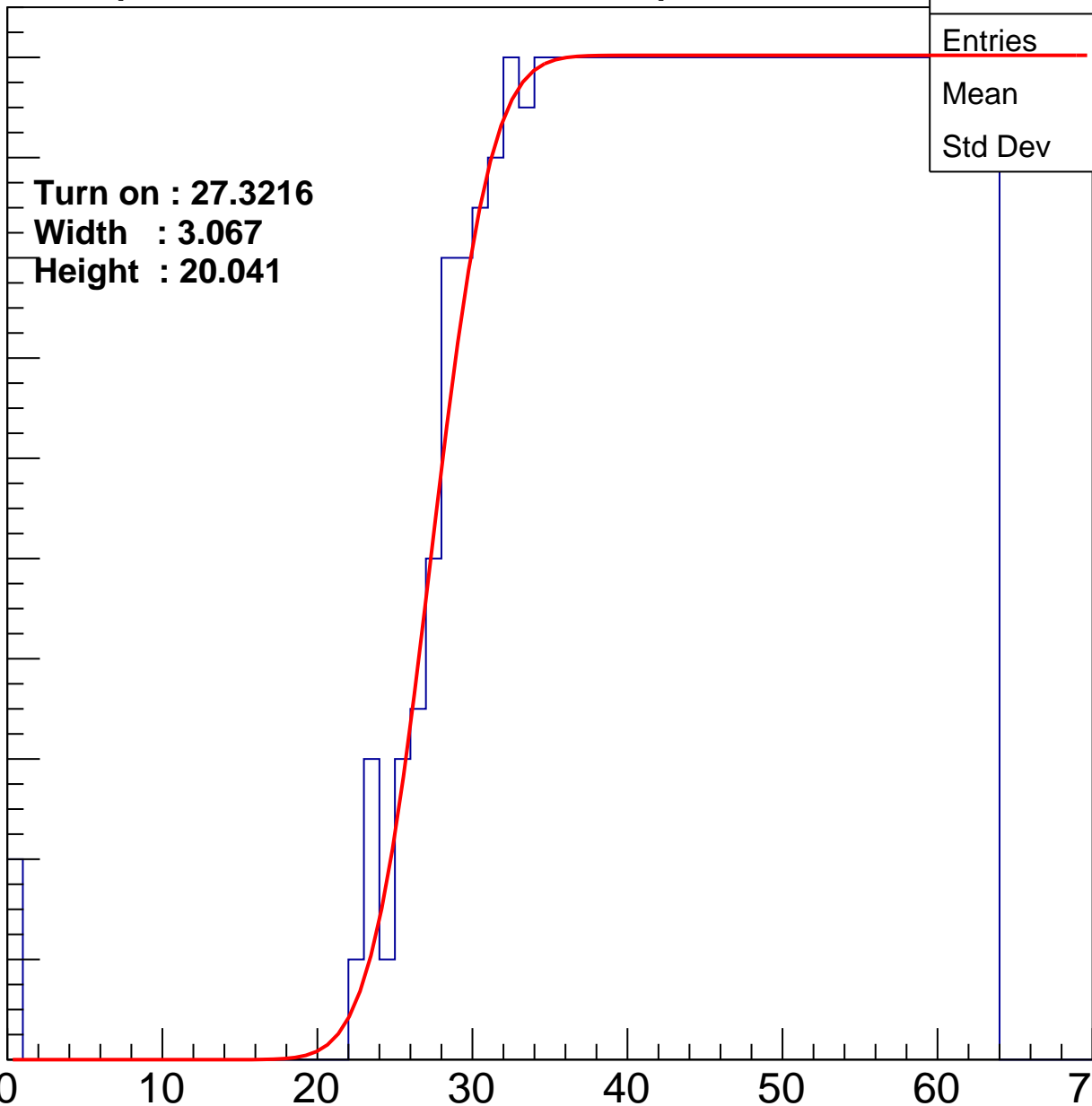
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3216
Width : 3.067
Height : 20.041

Entries	743
Mean	44.66
Std Dev	11.34

ampl



B0L101S, U24-ch84

calib_packv5_042523_0143.root, FC#1, port C1

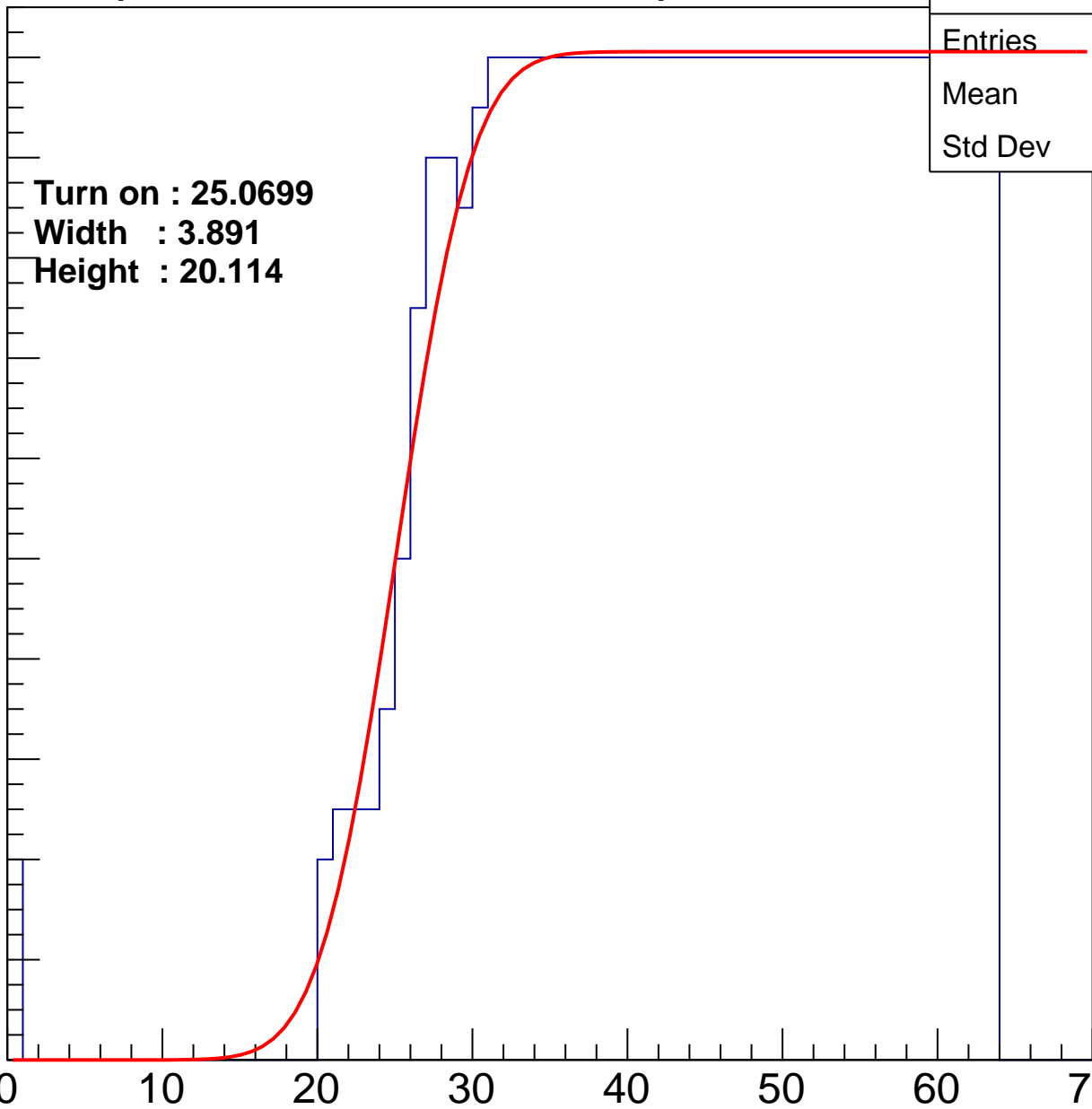
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0699
Width : 3.891
Height : 20.114

Entries	787
Mean	43.57
Std Dev	11.91

ampl



B0L101S, U24-ch85

calib_packv5_042523_0143.root, FC#1, port C1

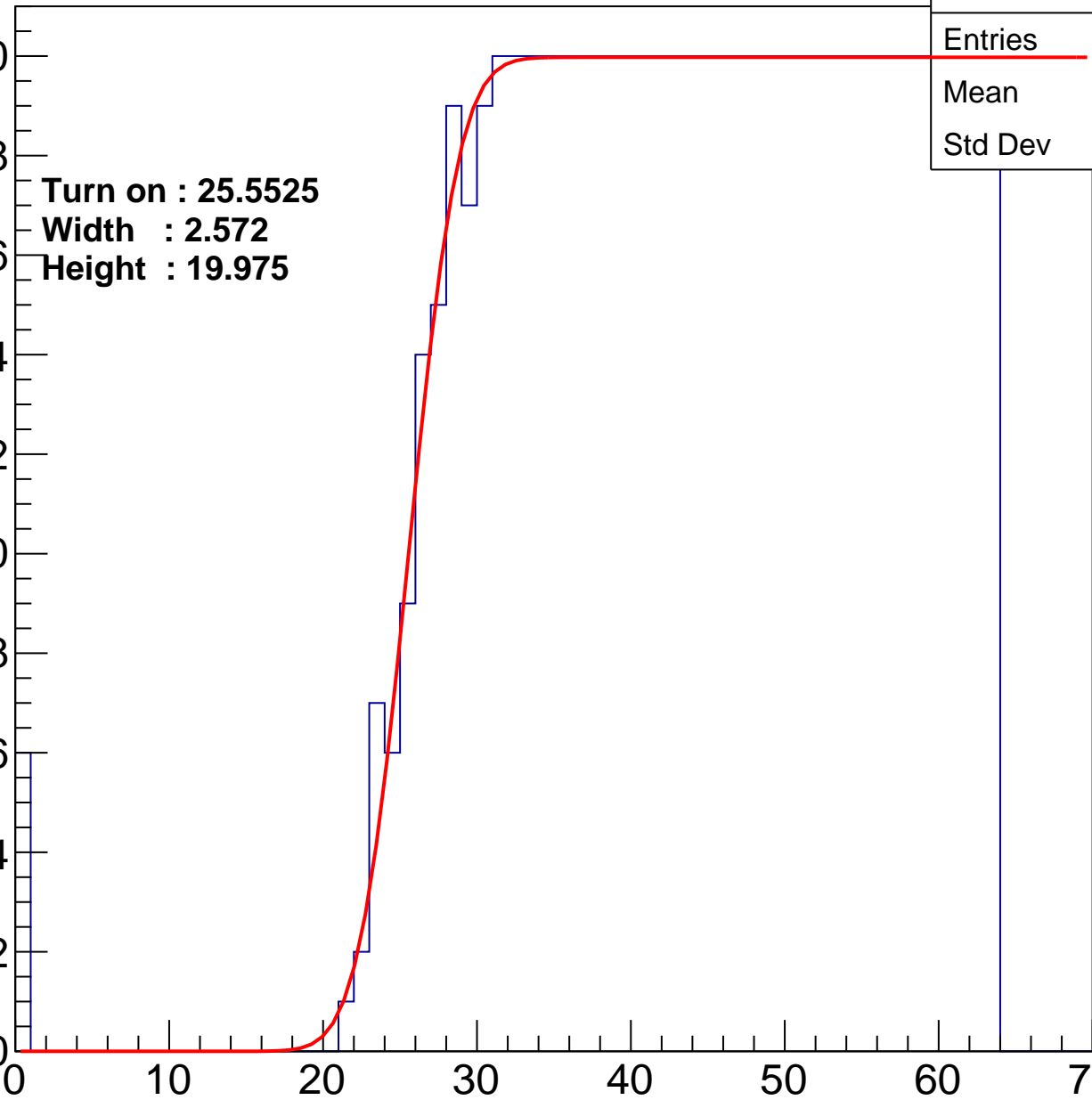
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5525
Width : 2.572
Height : 19.975

Entries	775
Mean	43.84
Std Dev	11.85

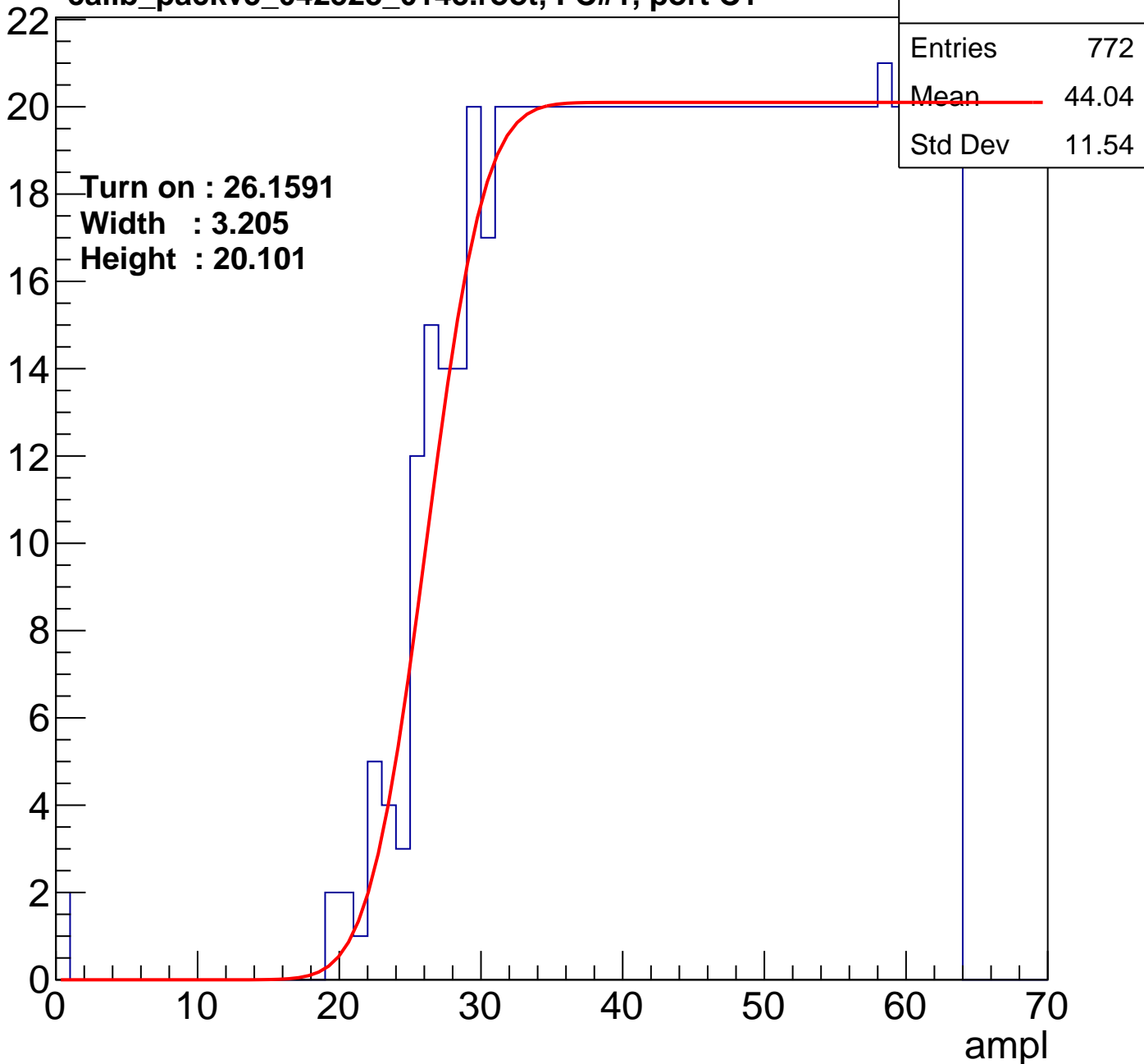
ampl



B0L101S, U24-ch86

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch87

calib_packv5_042523_0143.root, FC#1, port C1

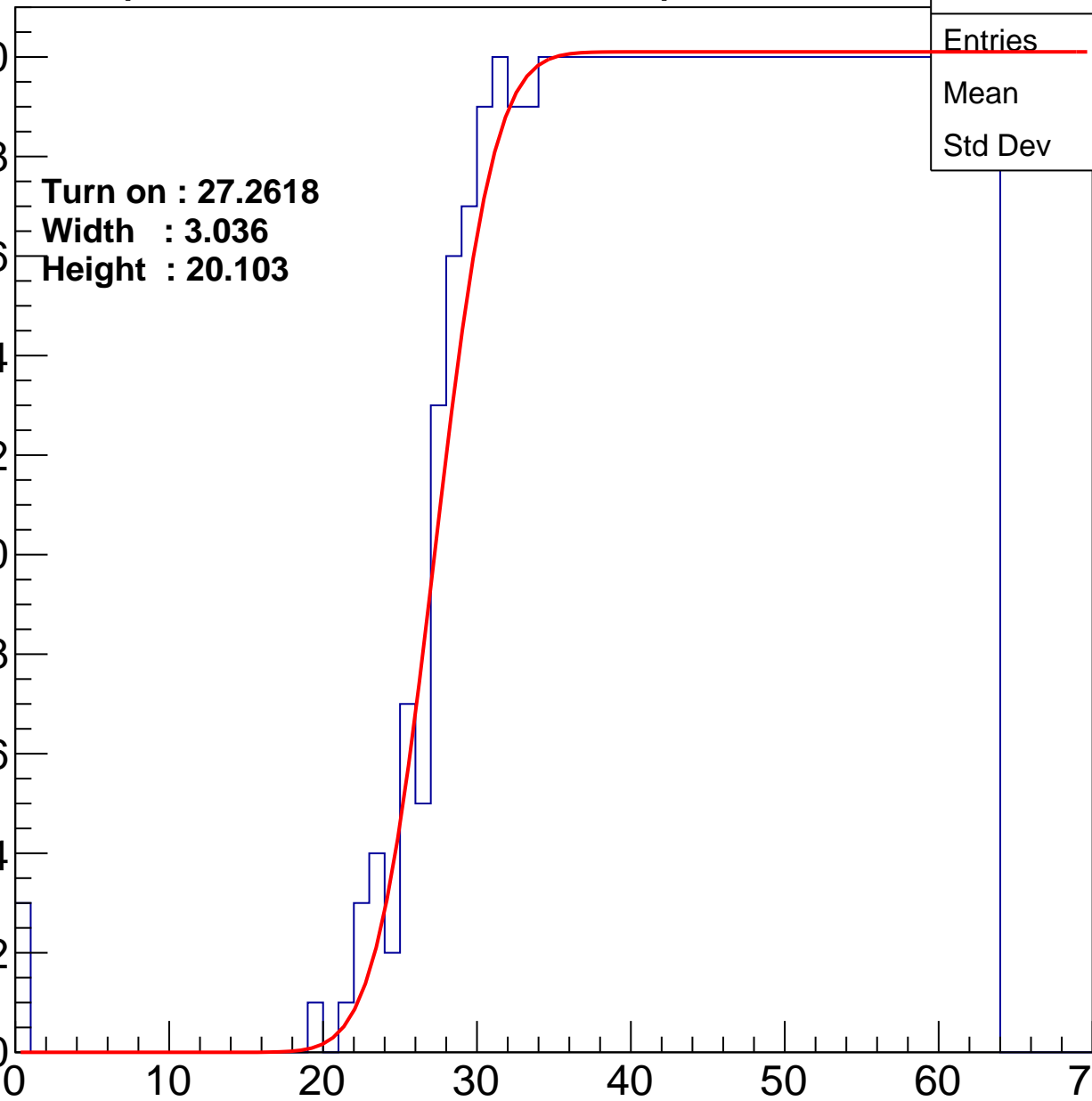
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2618
Width : 3.036
Height : 20.103

Entries	749
Mean	44.55
Std Dev	11.31

ampl



B0L101S, U24-ch88

calib_packv5_042523_0143.root, FC#1, port C1

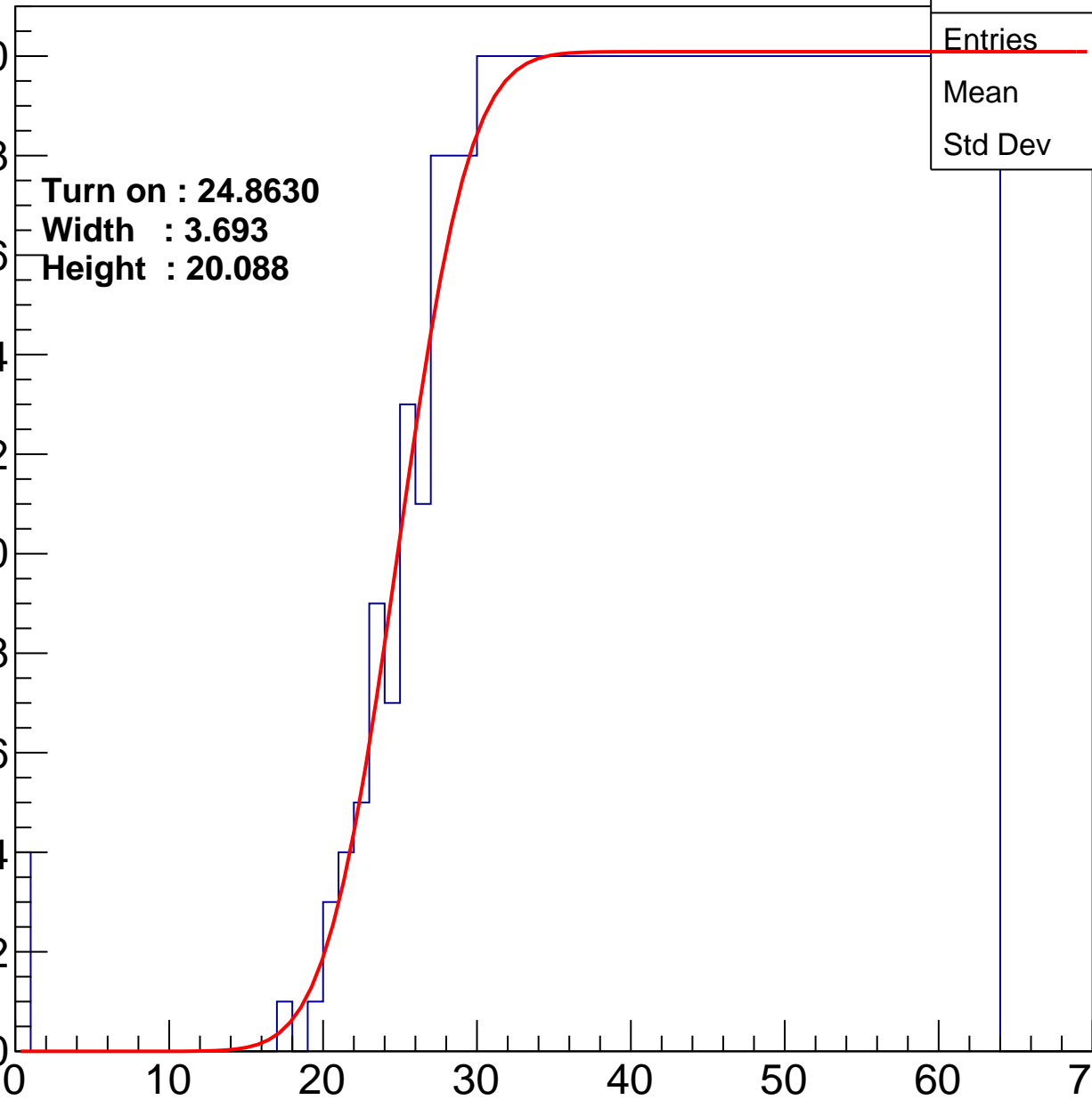
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8630
Width : 3.693
Height : 20.088

Entries	792
Mean	43.44
Std Dev	11.98

ampl



B0L101S, U24-ch89

calib_packv5_042523_0143.root, FC#1, port C1

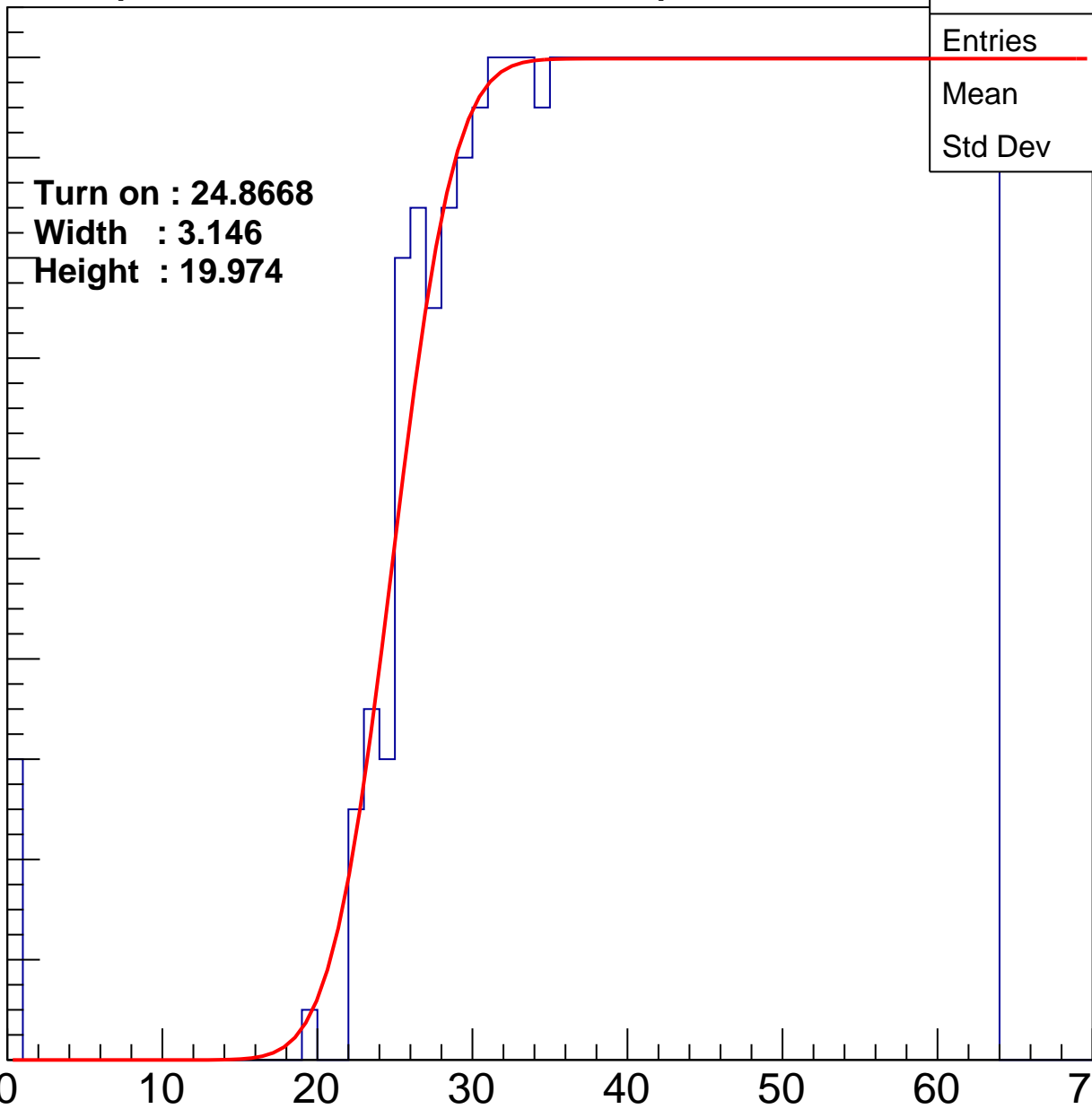
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8668
Width : 3.146
Height : 19.974

Entries	786
Mean	43.56
Std Dev	12.01

ampl



B0L101S, U24-ch90

calib_packv5_042523_0143.root, FC#1, port C1

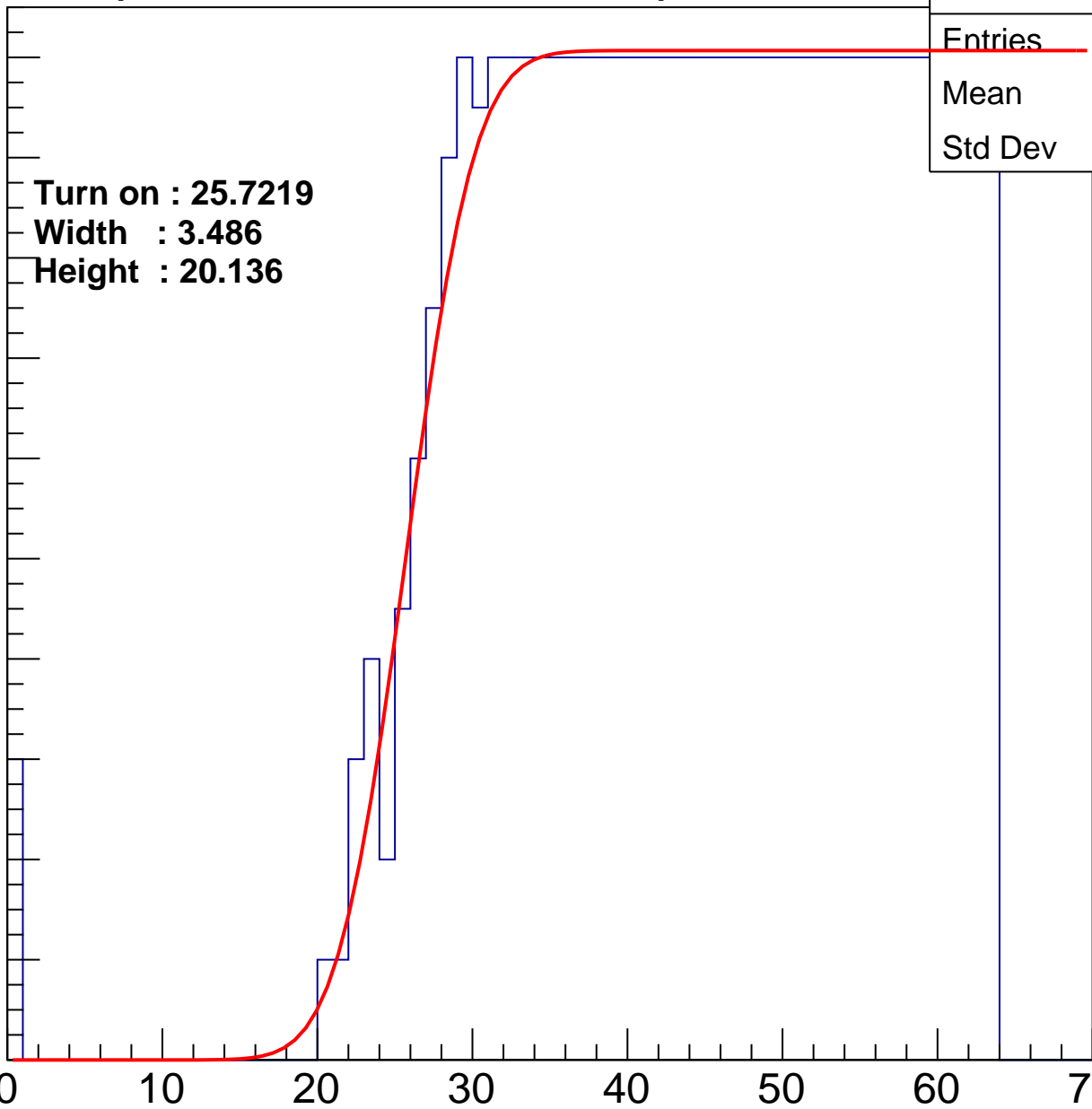
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7219
Width : 3.486
Height : 20.136

Entries	781
Mean	43.67
Std Dev	11.97

ampl



B0L101S, U24-ch91

calib_packv5_042523_0143.root, FC#1, port C1

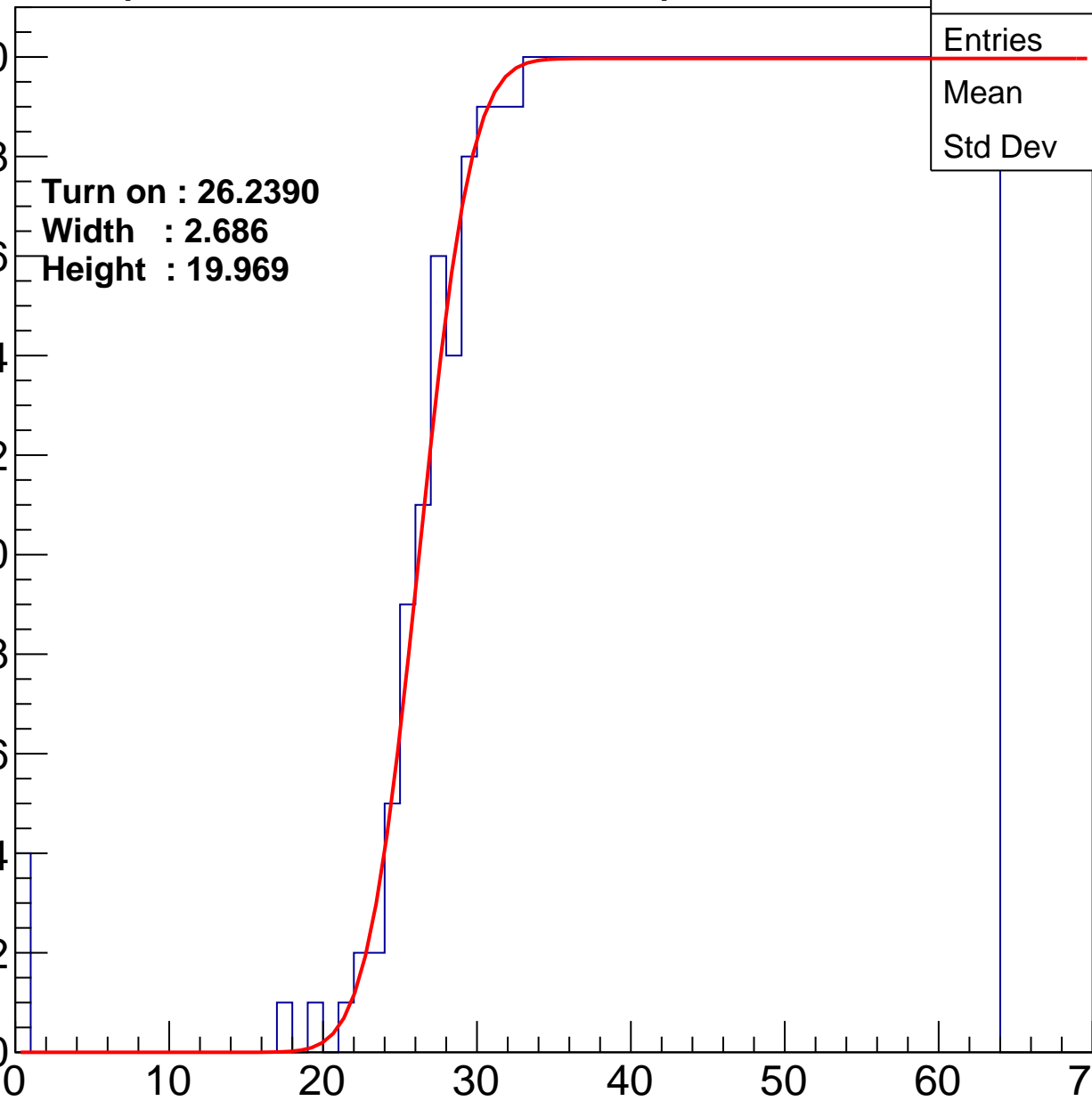
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2390
Width : 2.686
Height : 19.969

Entries	761
Mean	44.22
Std Dev	11.56

ampl



B0L101S, U24-ch92

calib_packv5_042523_0143.root, FC#1, port C1

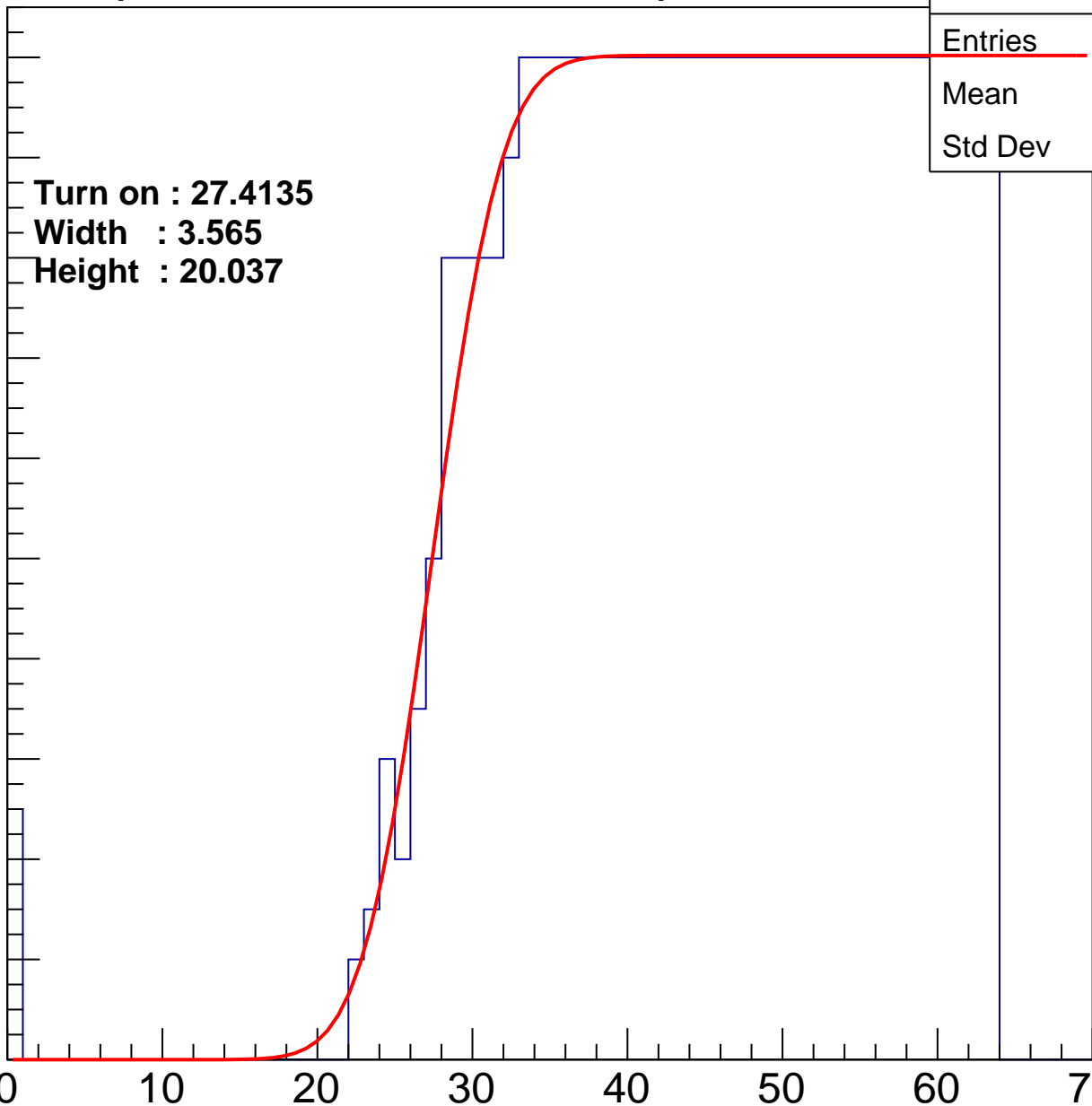
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4135
Width : 3.565
Height : 20.037

Entries	739
Mean	44.7
Std Dev	11.41

ampl



B0L101S, U24-ch93

calib_packv5_042523_0143.root, FC#1, port C1

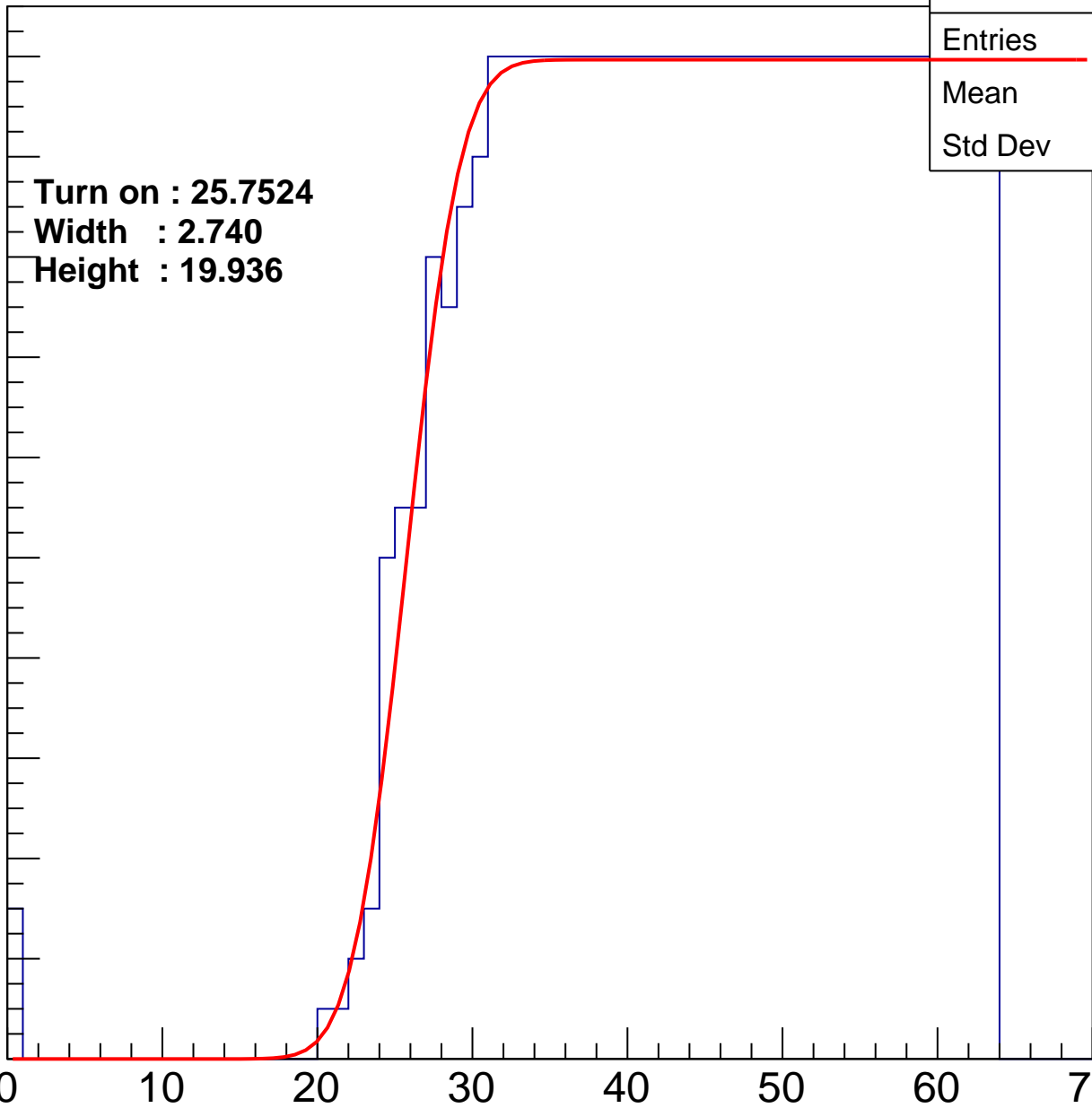
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7524
Width : 2.740
Height : 19.936

Entries	768
Mean	44.09
Std Dev	11.54

ampl



B0L101S, U24-ch94

calib_packv5_042523_0143.root, FC#1, port C1

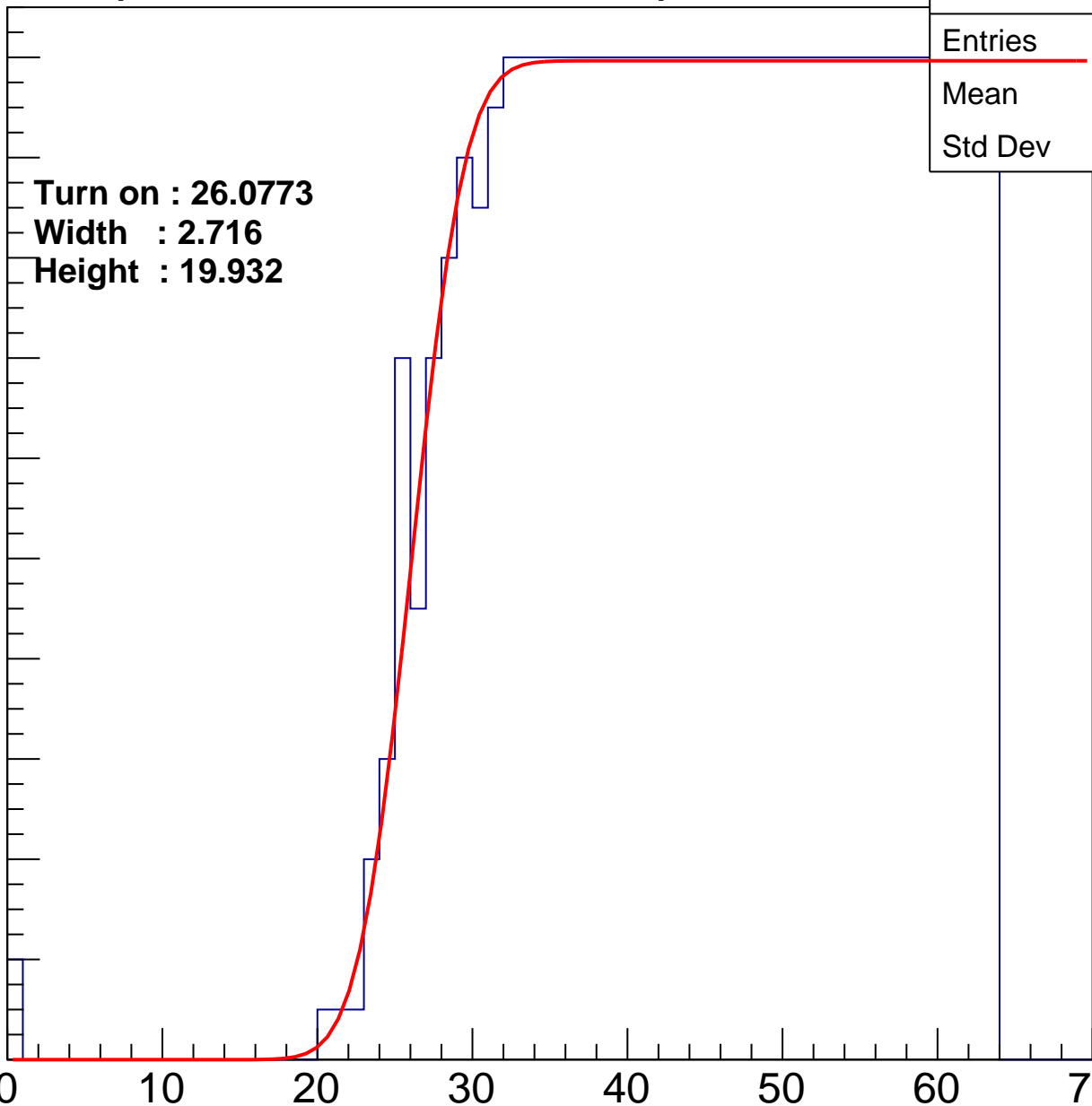
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0773
Width : 2.716
Height : 19.932

Entries	762
Mean	44.27
Std Dev	11.38

ampl



B0L101S, U24-ch95

calib_packv5_042523_0143.root, FC#1, port C1

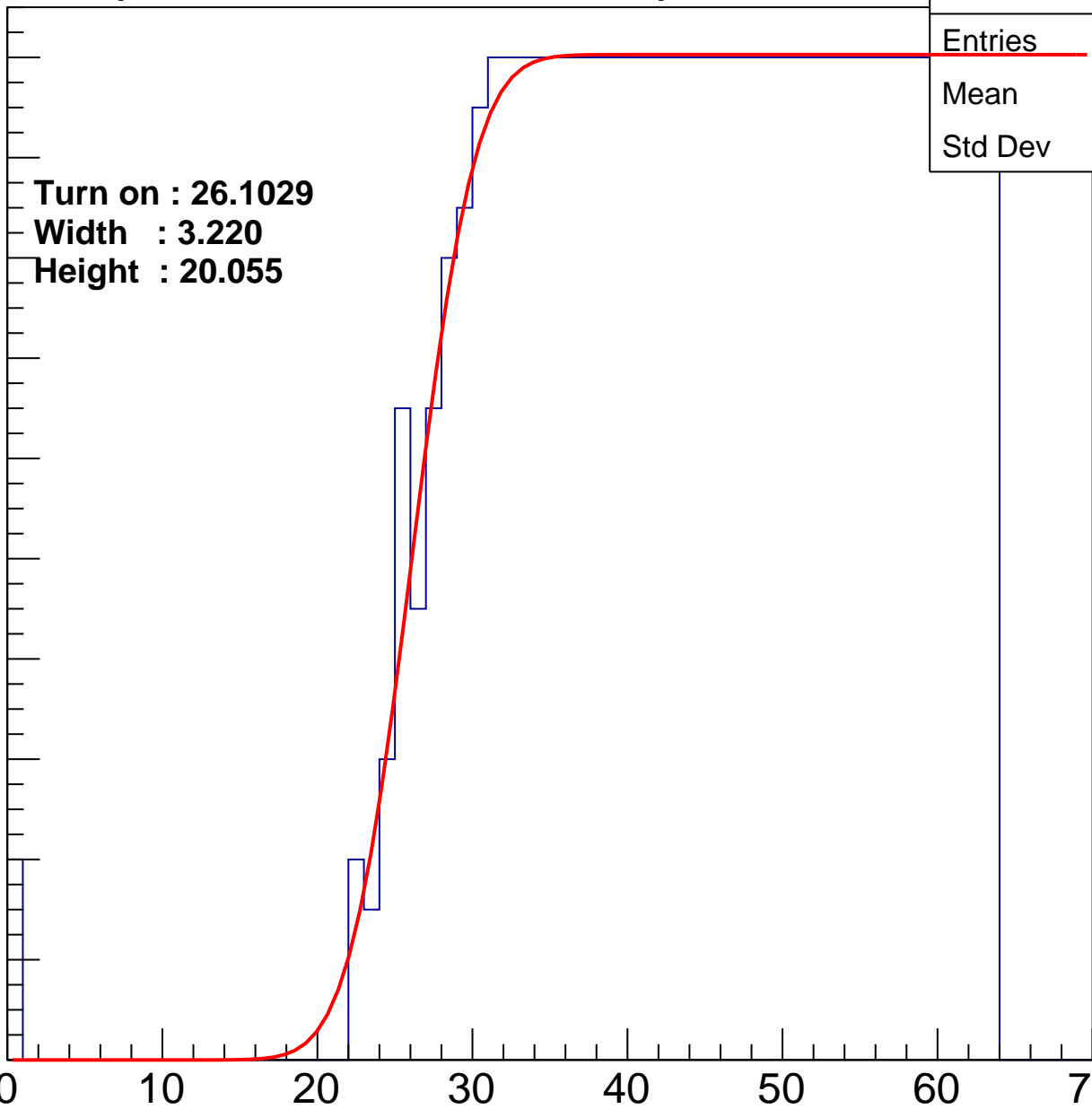
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1029
Width : 3.220
Height : 20.055

Entries	764
Mean	44.16
Std Dev	11.56

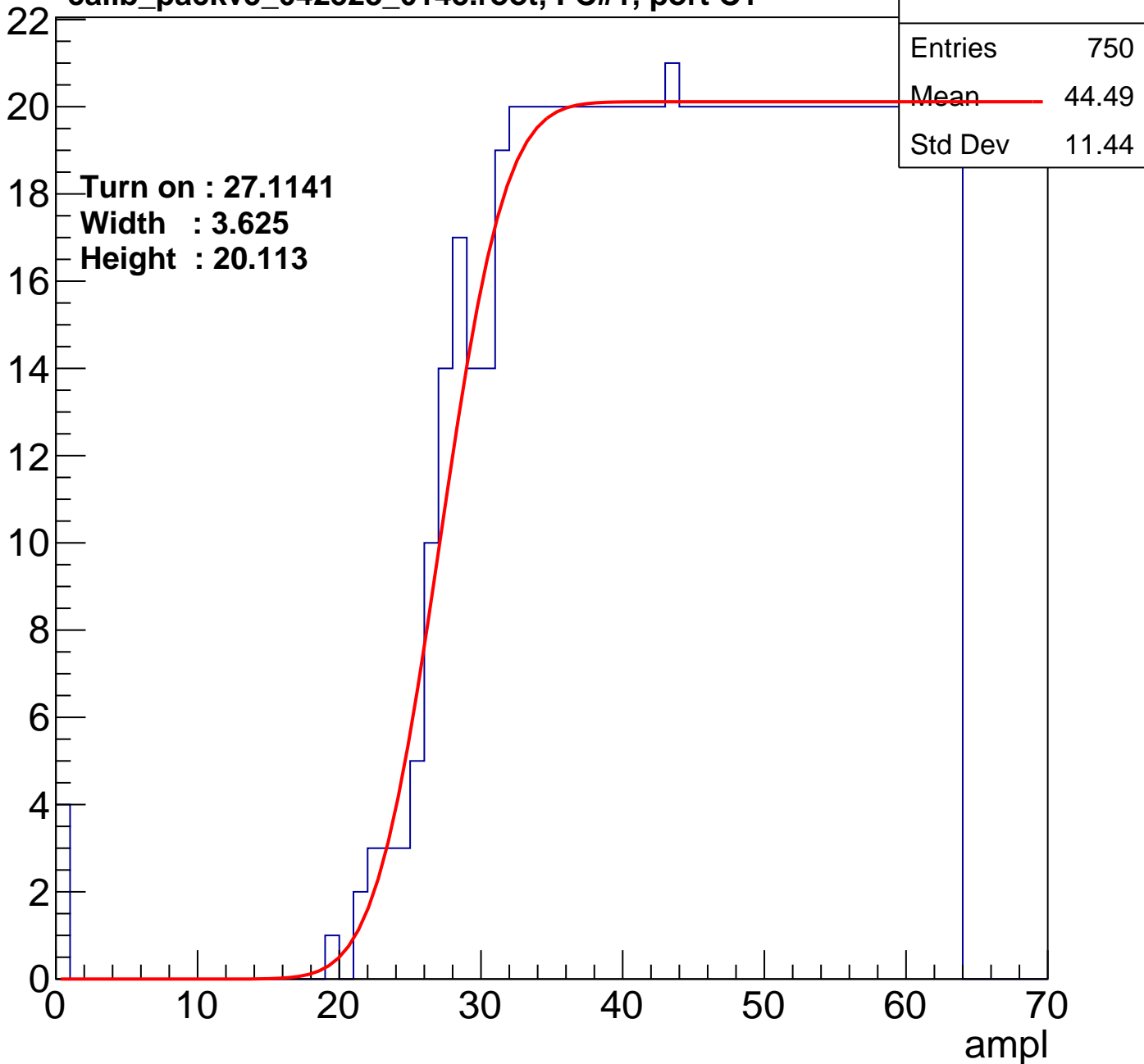
ampl



B0L101S, U24-ch96

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch97

calib_packv5_042523_0143.root, FC#1, port C1

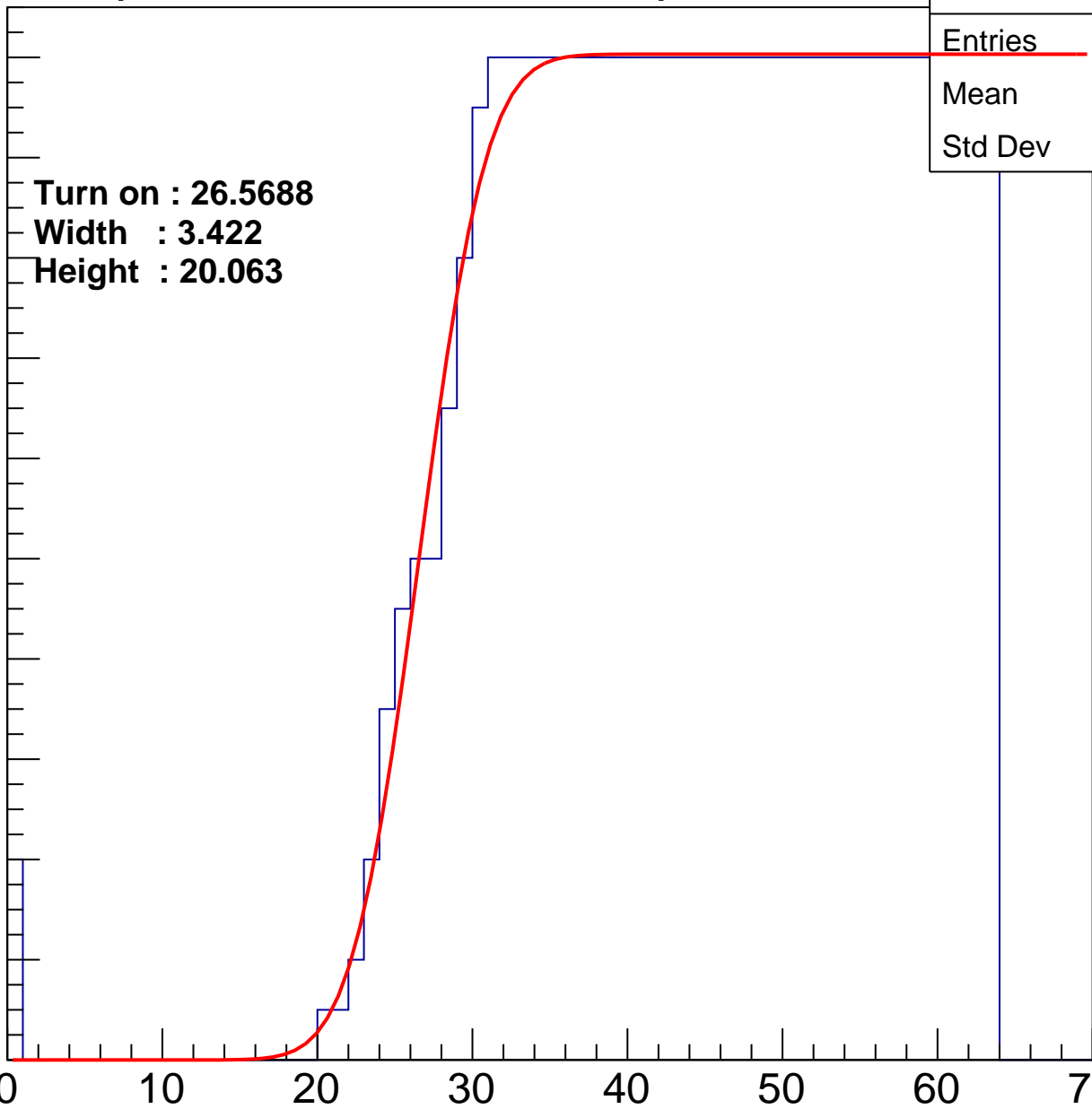
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5688
Width : 3.422
Height : 20.063

Entries	756
Mean	44.34
Std Dev	11.51

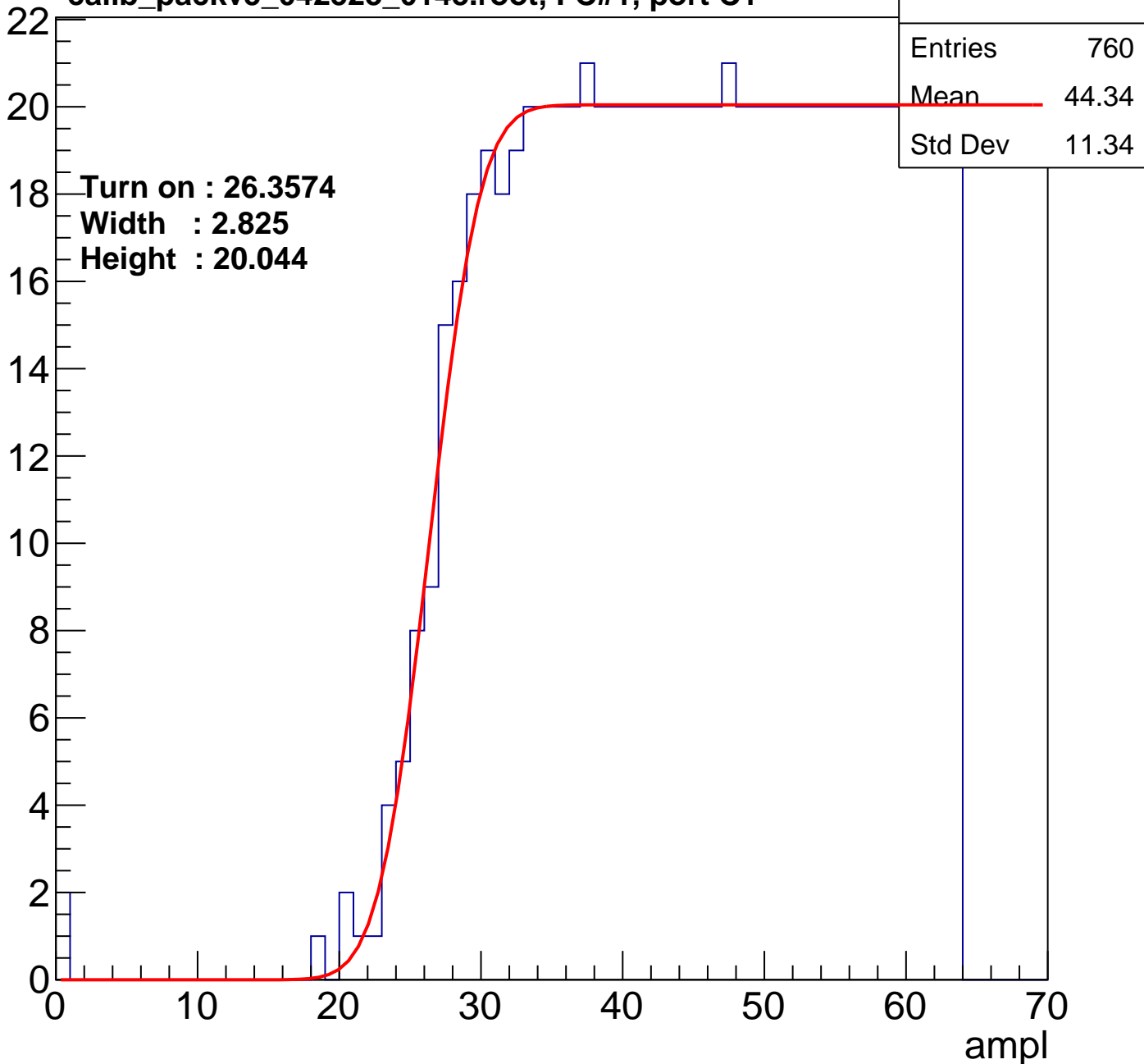
ampl



B0L101S, U24-ch98

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch99

calib_packv5_042523_0143.root, FC#1, port C1

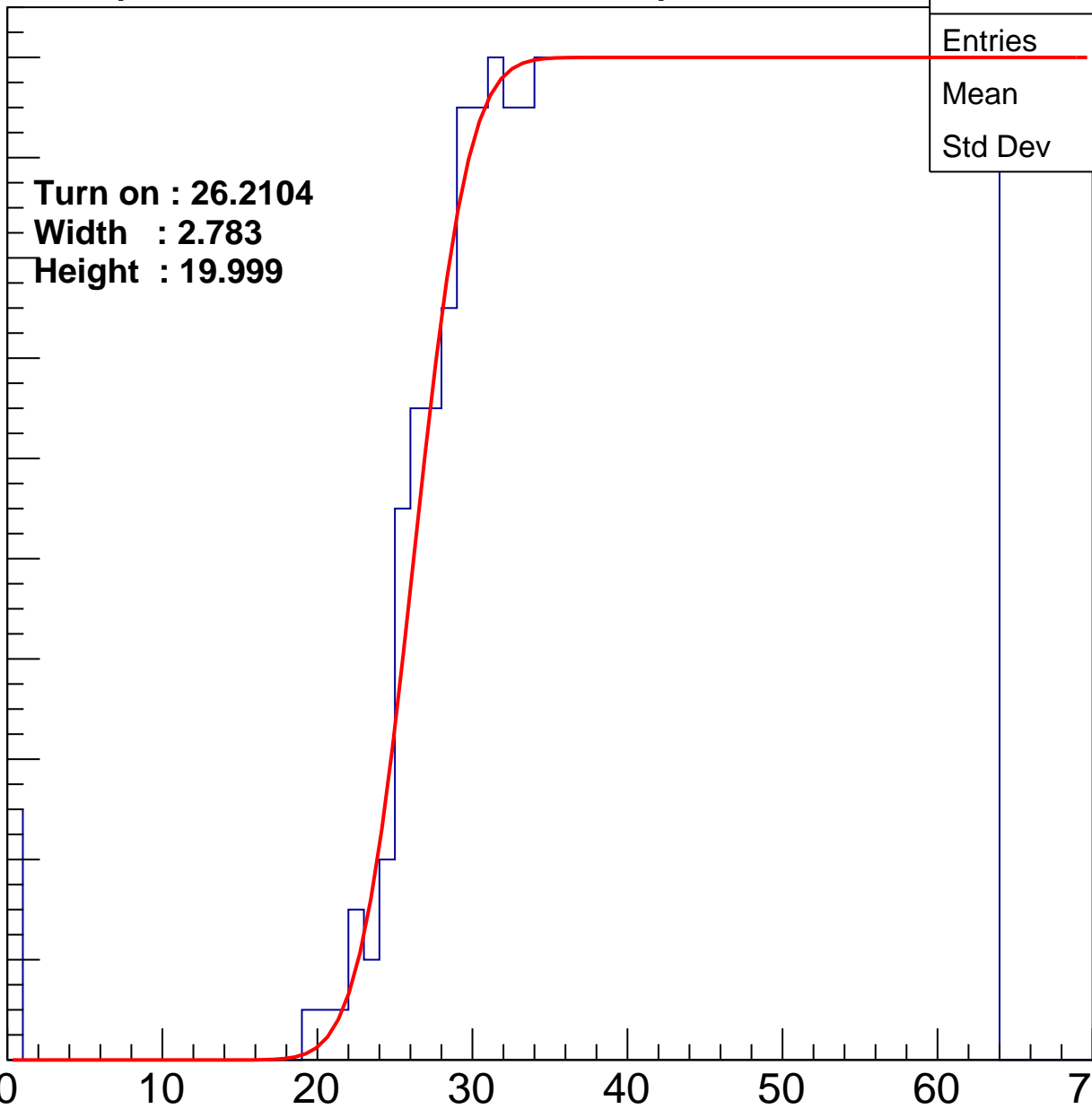
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2104
Width : 2.783
Height : 19.999

Entries	765
Mean	44.09
Std Dev	11.7

ampl



B0L101S, U24-ch100

calib_packv5_042523_0143.root, FC#1, port C1

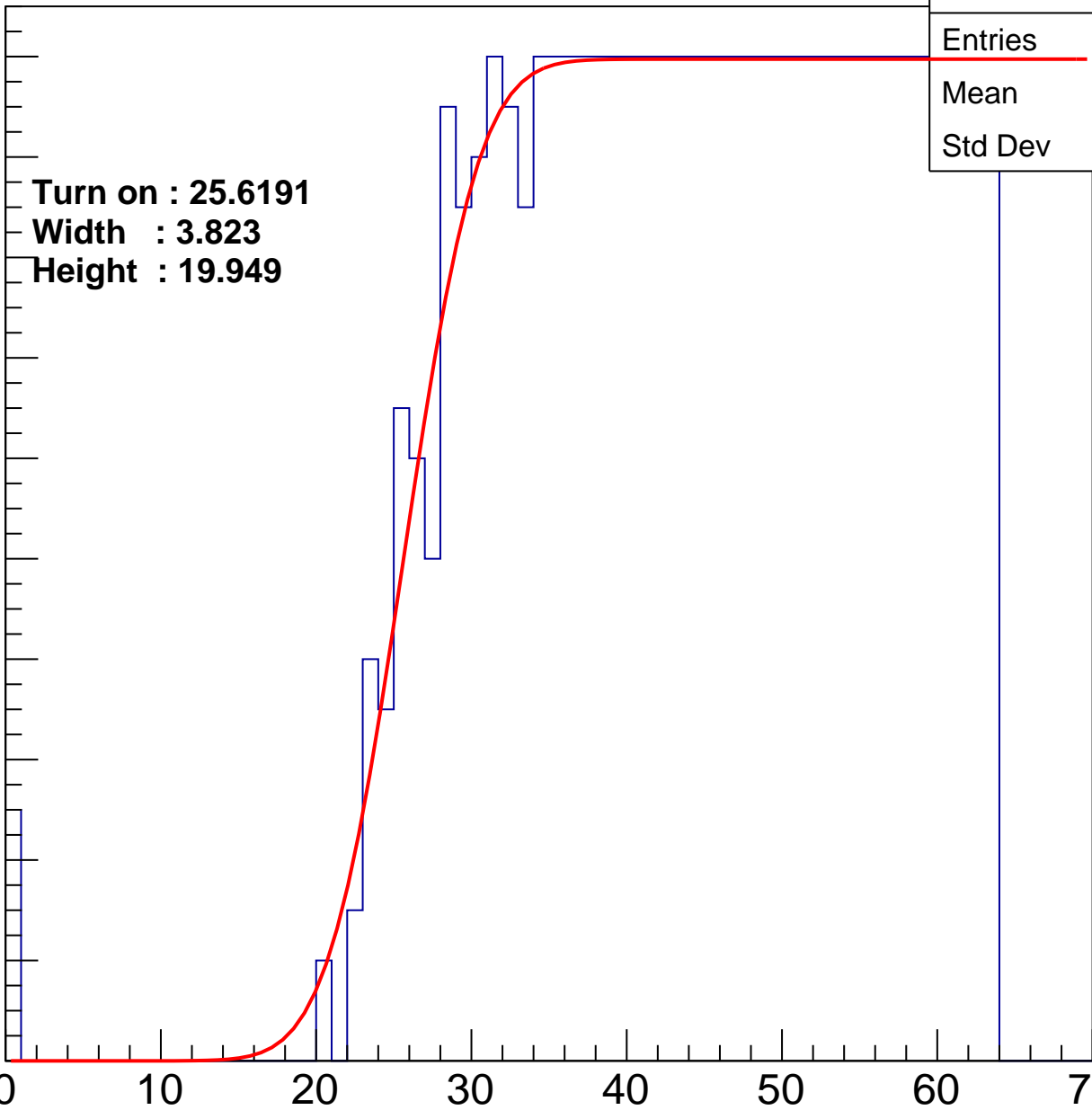
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6191
Width : 3.823
Height : 19.949

Entries	770
Mean	43.92
Std Dev	11.82

ampl



B0L101S, U24-ch101

calib_packv5_042523_0143.root, FC#1, port C1

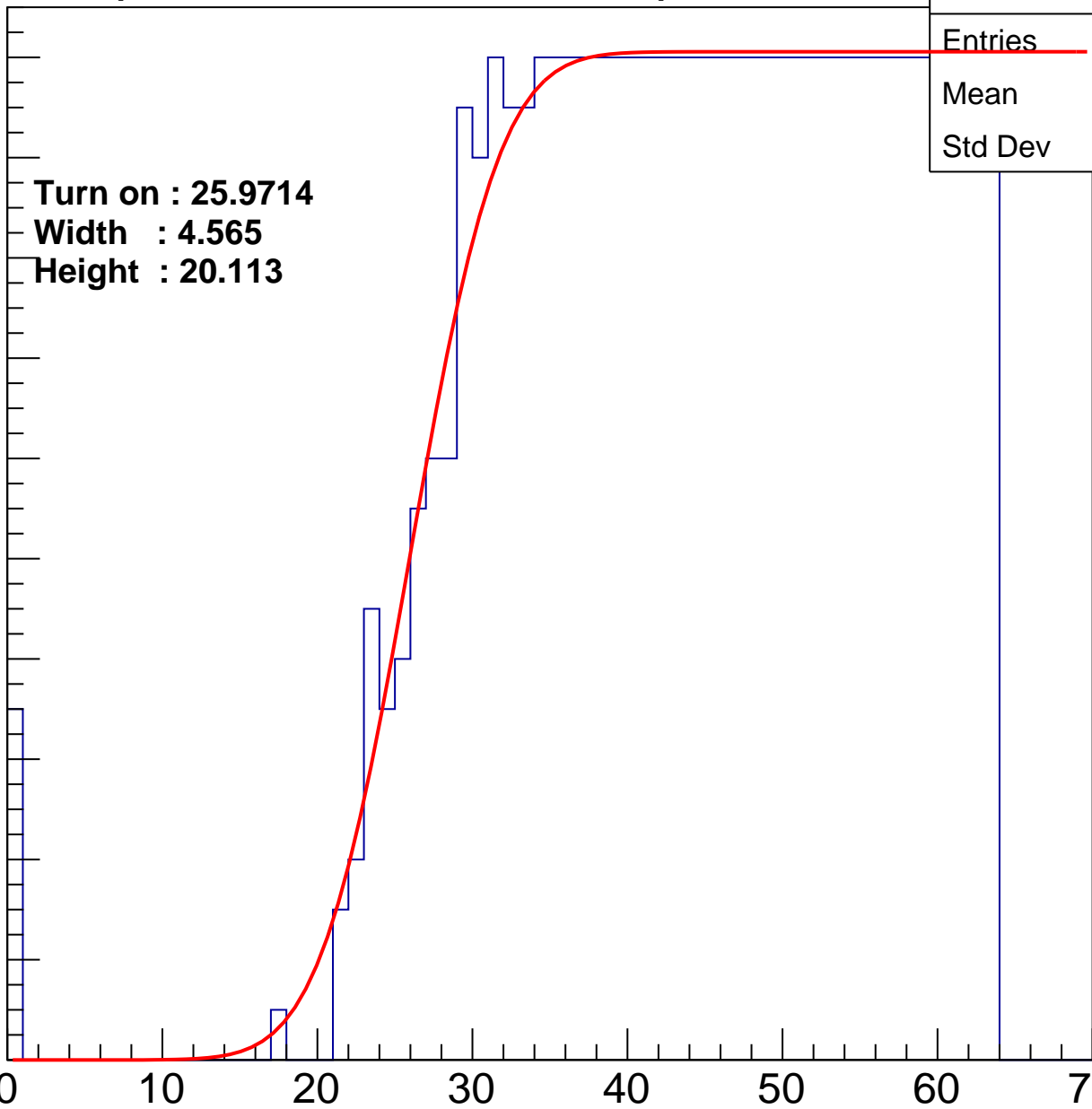
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9714
Width : 4.565
Height : 20.113

Entries	769
Mean	43.87
Std Dev	12.01

ampl



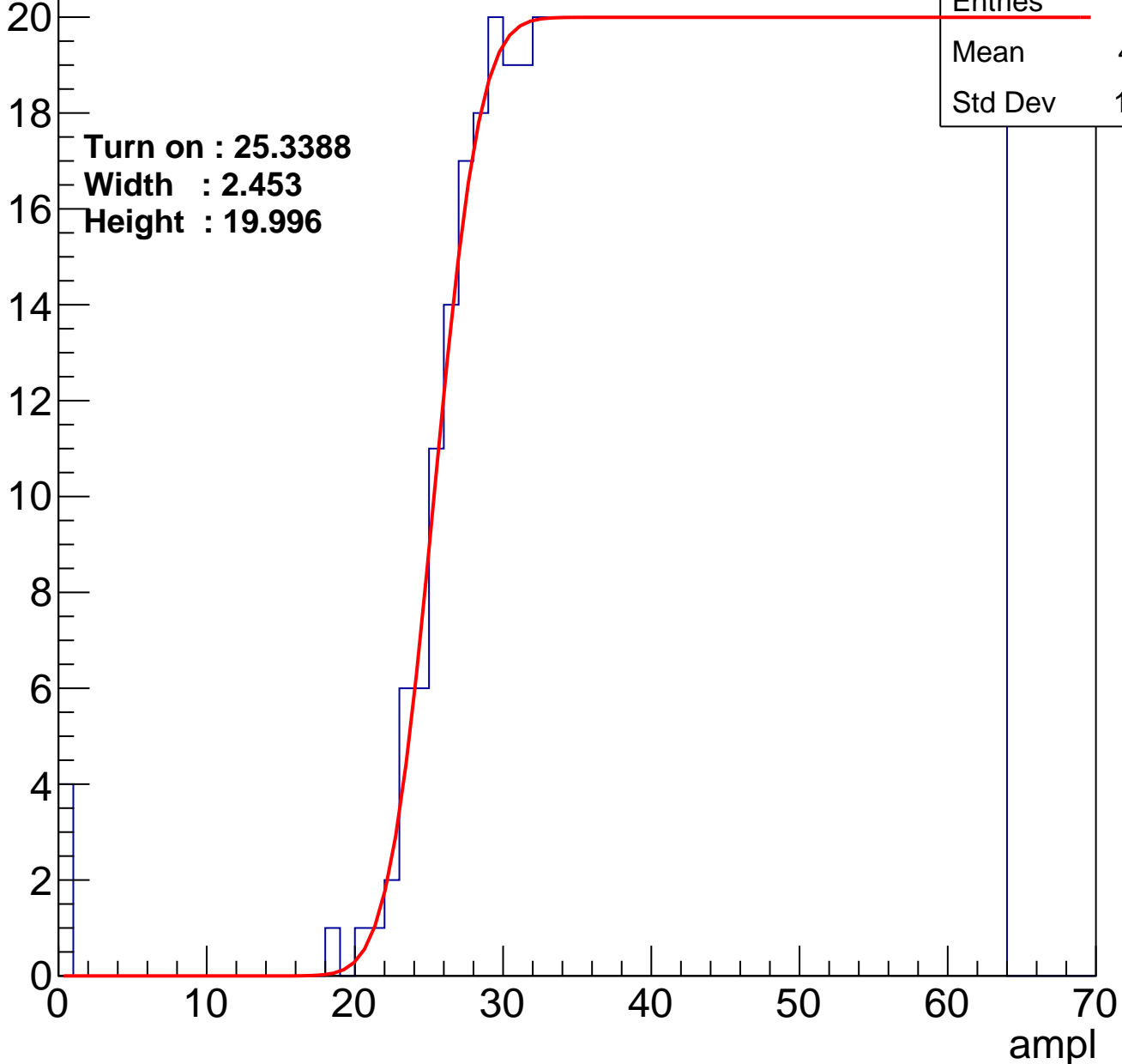
B0L101S, U24-ch102

calib_packv5_042523_0143.root, FC#1, port C1

Entries	779
Mean	43.81
Std Dev	11.74

Turn on : 25.3388
Width : 2.453
Height : 19.996

Entry



B0L101S, U24-ch103

calib_packv5_042523_0143.root, FC#1, port C1

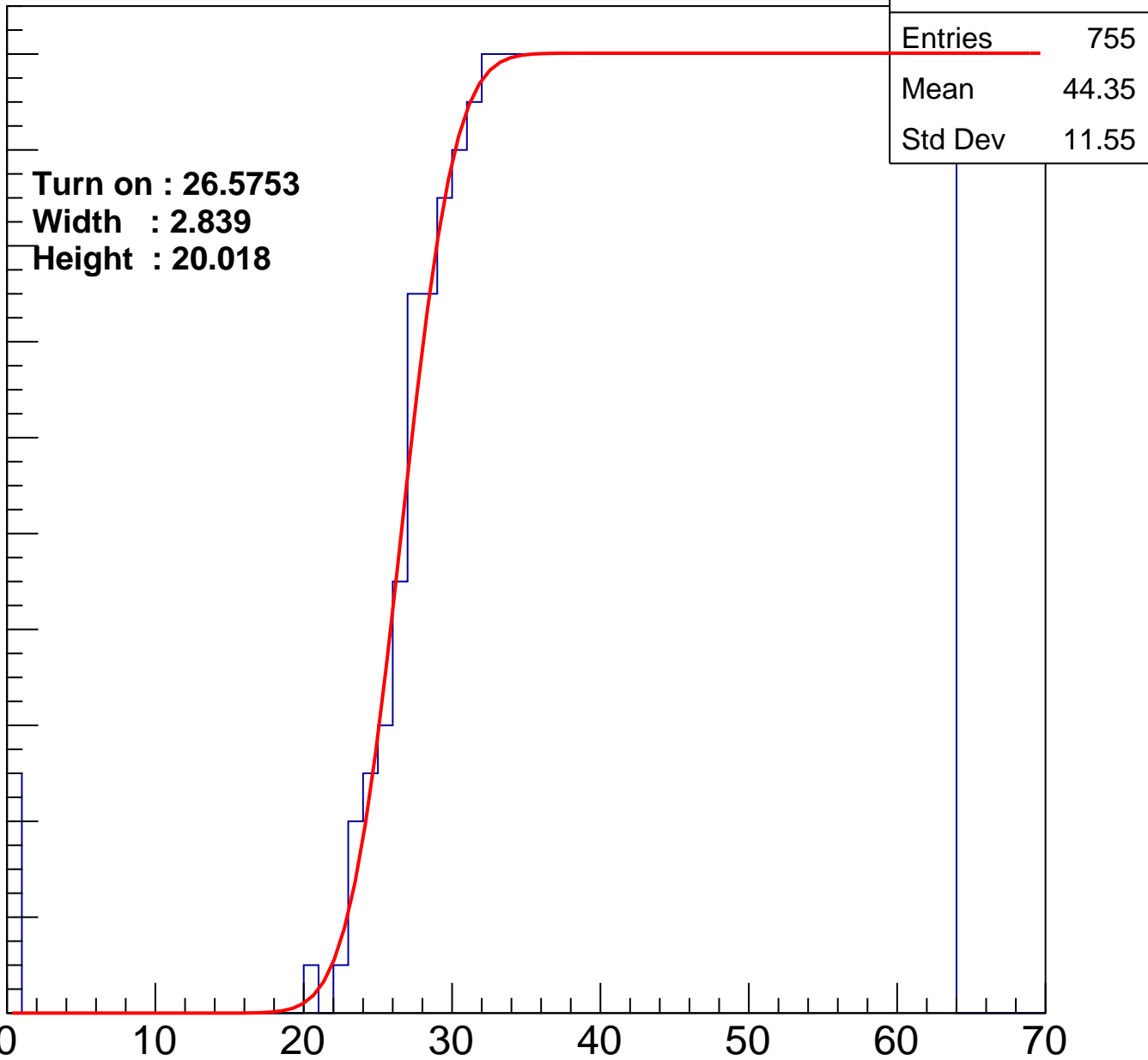
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5753
Width : 2.839
Height : 20.018

Entries	755
Mean	44.35
Std Dev	11.55

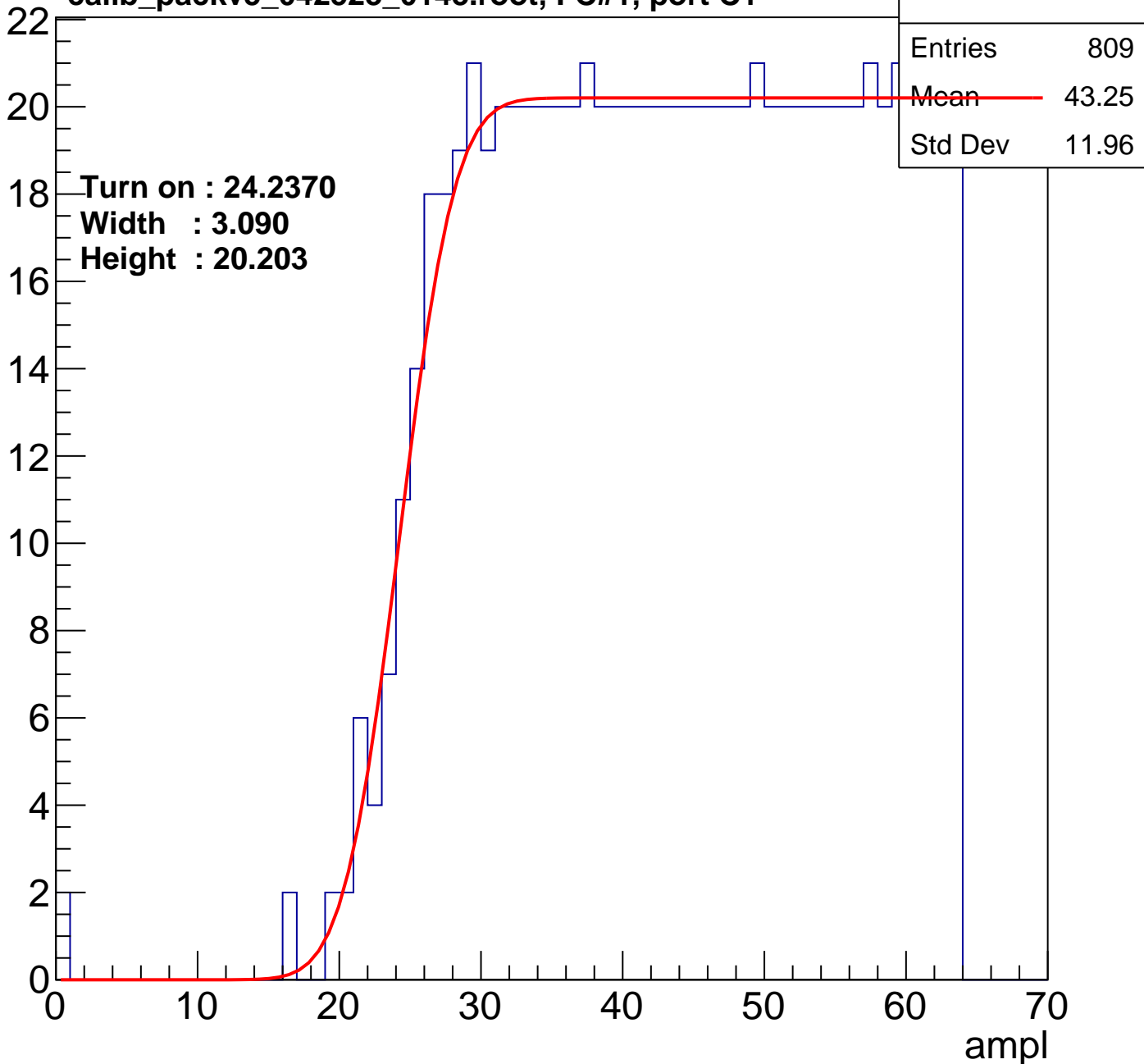
ampl



B0L101S, U24-ch104

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch105

calib_packv5_042523_0143.root, FC#1, port C1

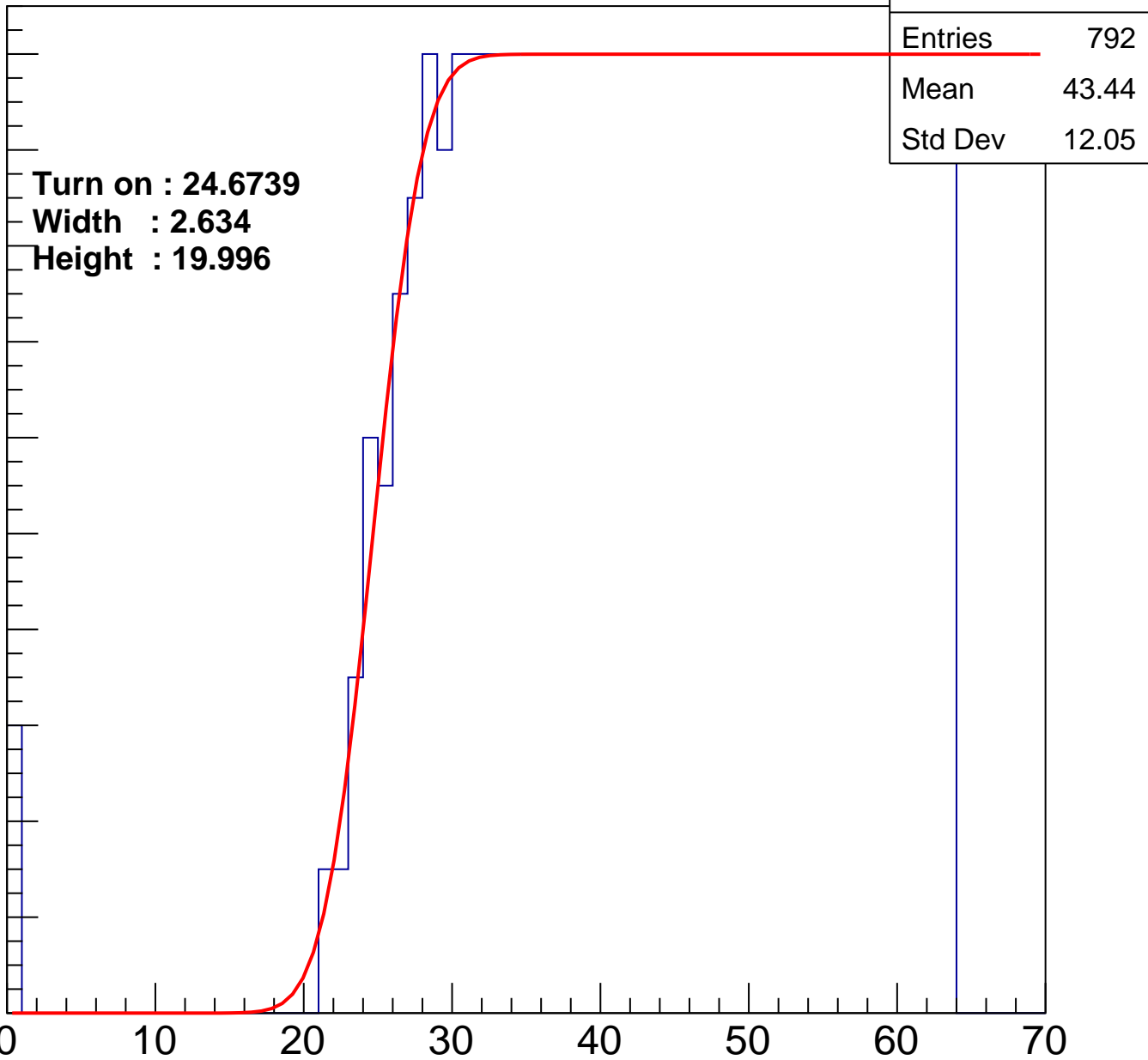
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6739
Width : 2.634
Height : 19.996

Entries	792
Mean	43.44
Std Dev	12.05

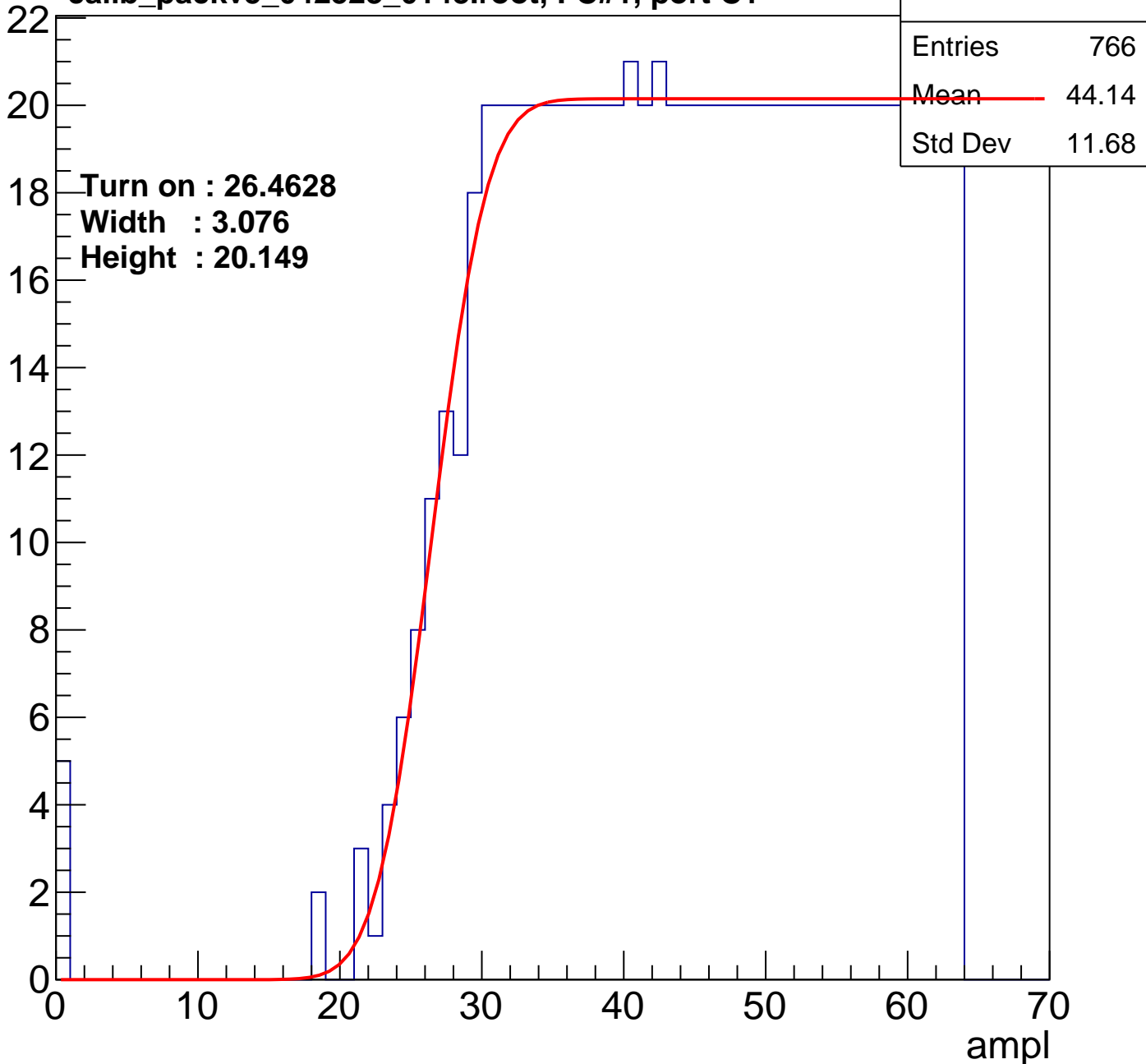
ampl



B0L101S, U24-ch106

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch107

calib_packv5_042523_0143.root, FC#1, port C1

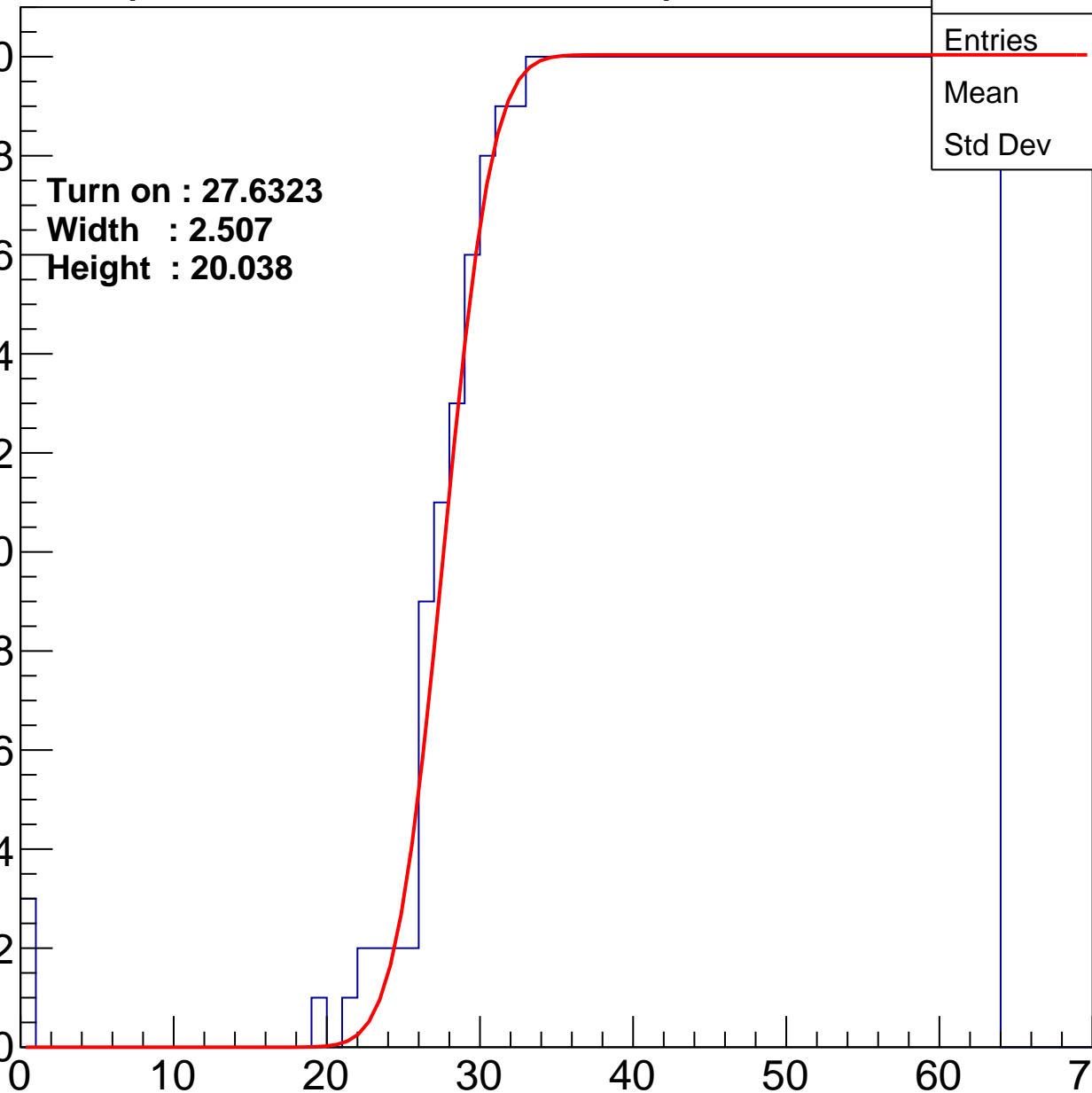
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6323
Width : 2.507
Height : 20.038

Entries	738
Mean	44.83
Std Dev	11.16

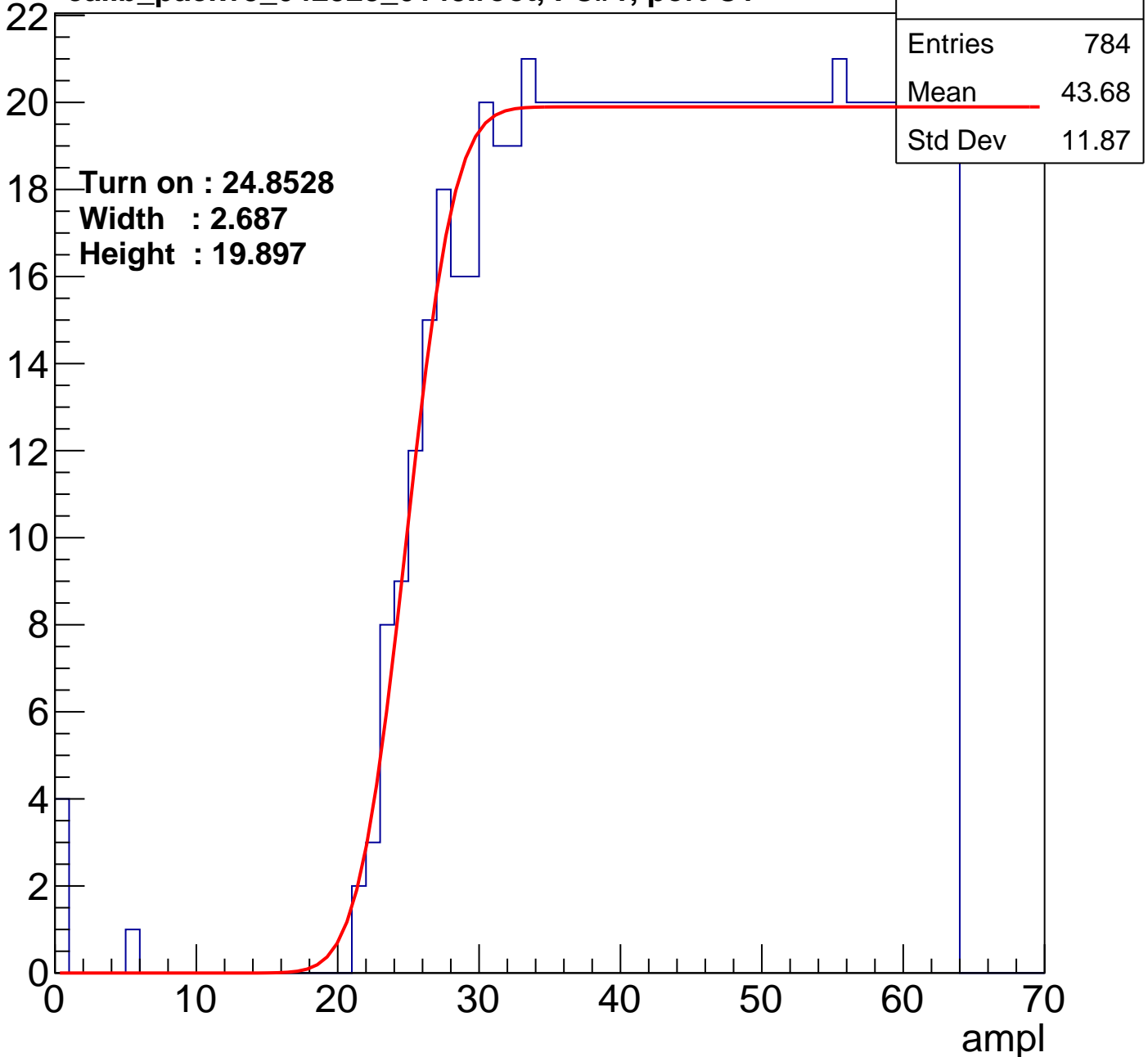
ampl



B0L101S, U24-ch108

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch109

calib_packv5_042523_0143.root, FC#1, port C1

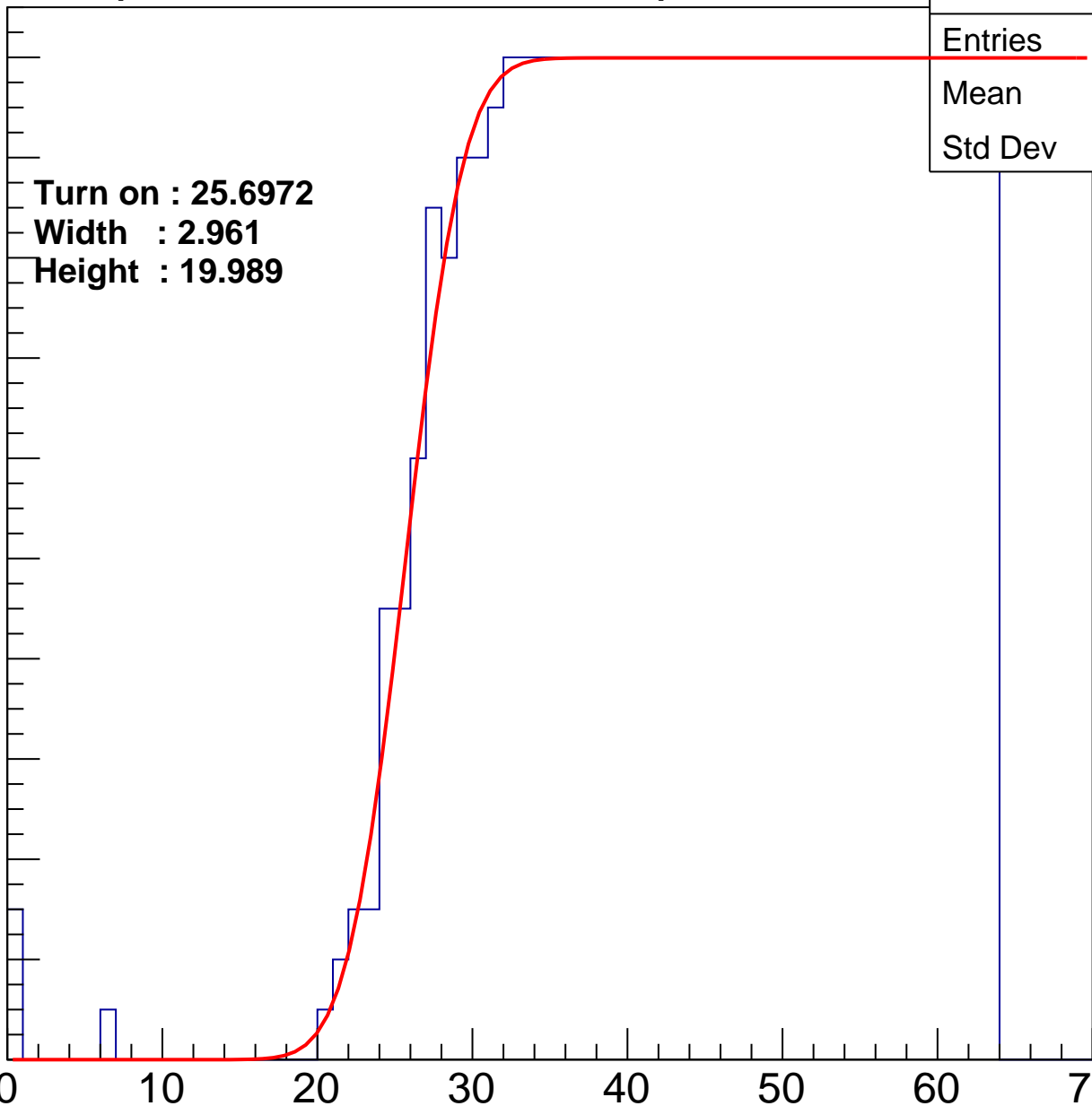
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6972
Width : 2.961
Height : 19.989

Entries	771
Mean	43.99
Std Dev	11.65

ampl



B0L101S, U24-ch110

calib_packv5_042523_0143.root, FC#1, port C1

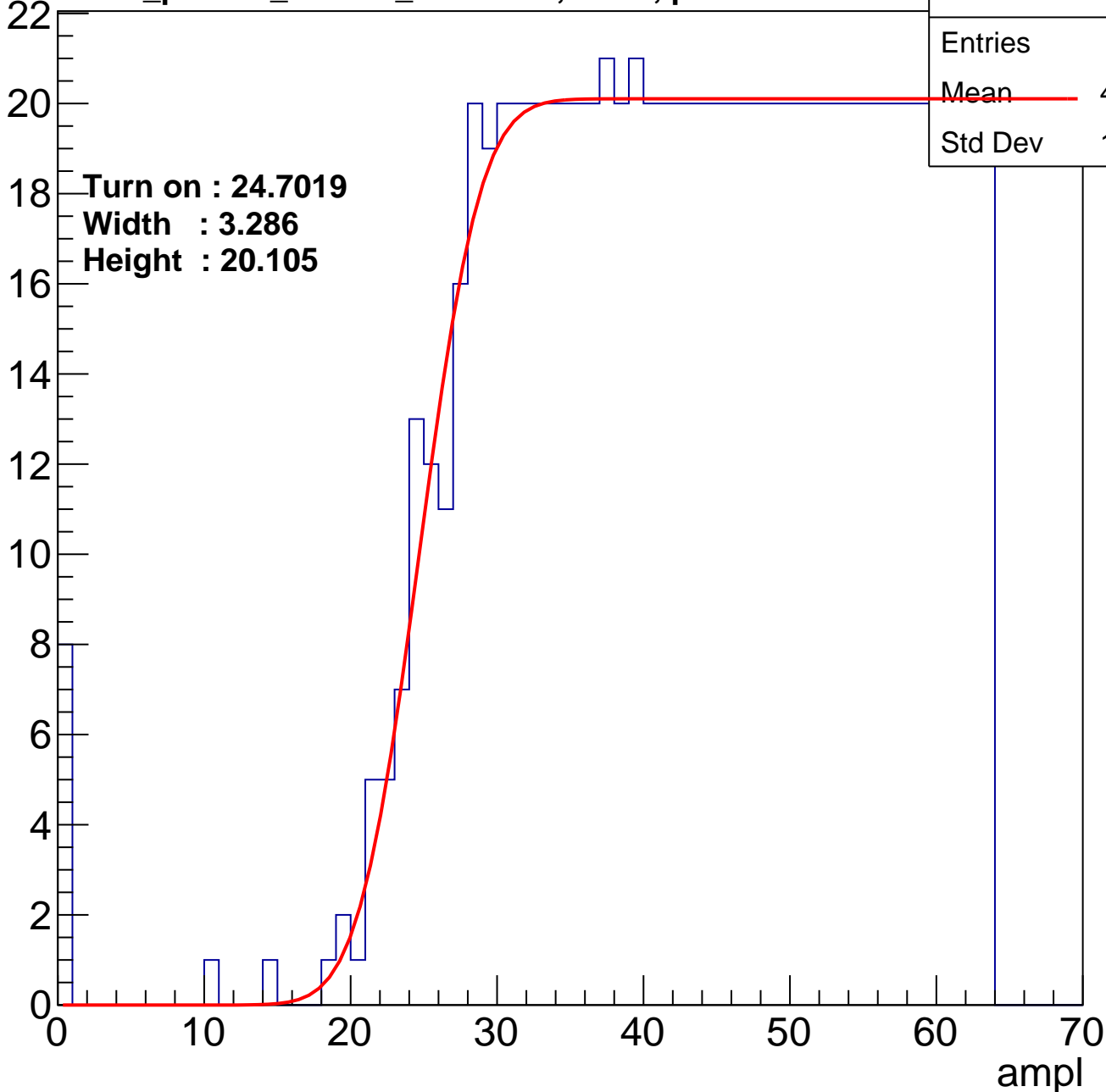
Entries	804
Mean	43.05
Std Dev	12.44

Turn on : 24.7019

Width : 3.286

Height : 20.105

Entry



B0L101S, U24-ch111

calib_packv5_042523_0143.root, FC#1, port C1

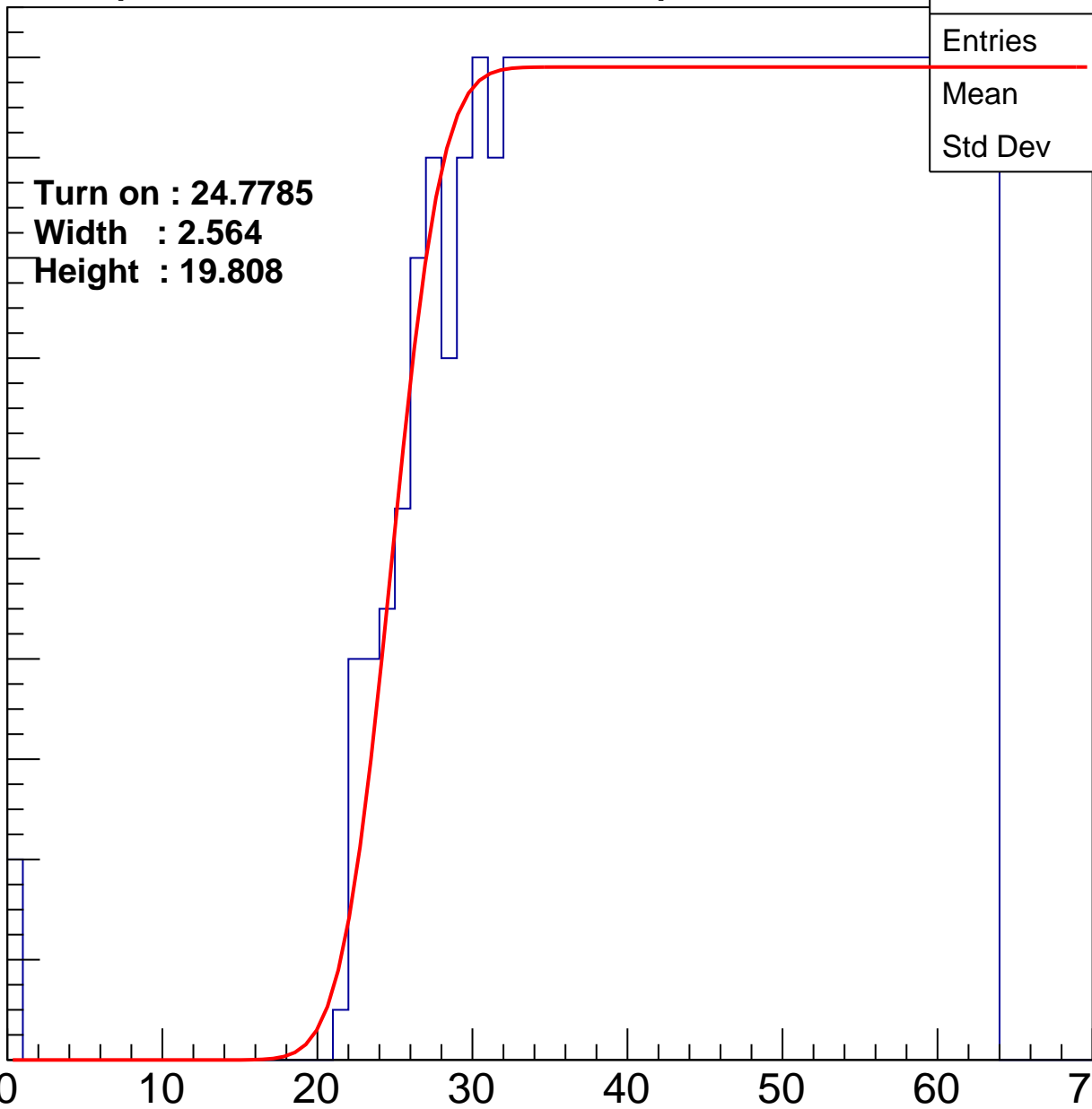
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7785
Width : 2.564
Height : 19.808

Entries	785
Mean	43.63
Std Dev	11.87

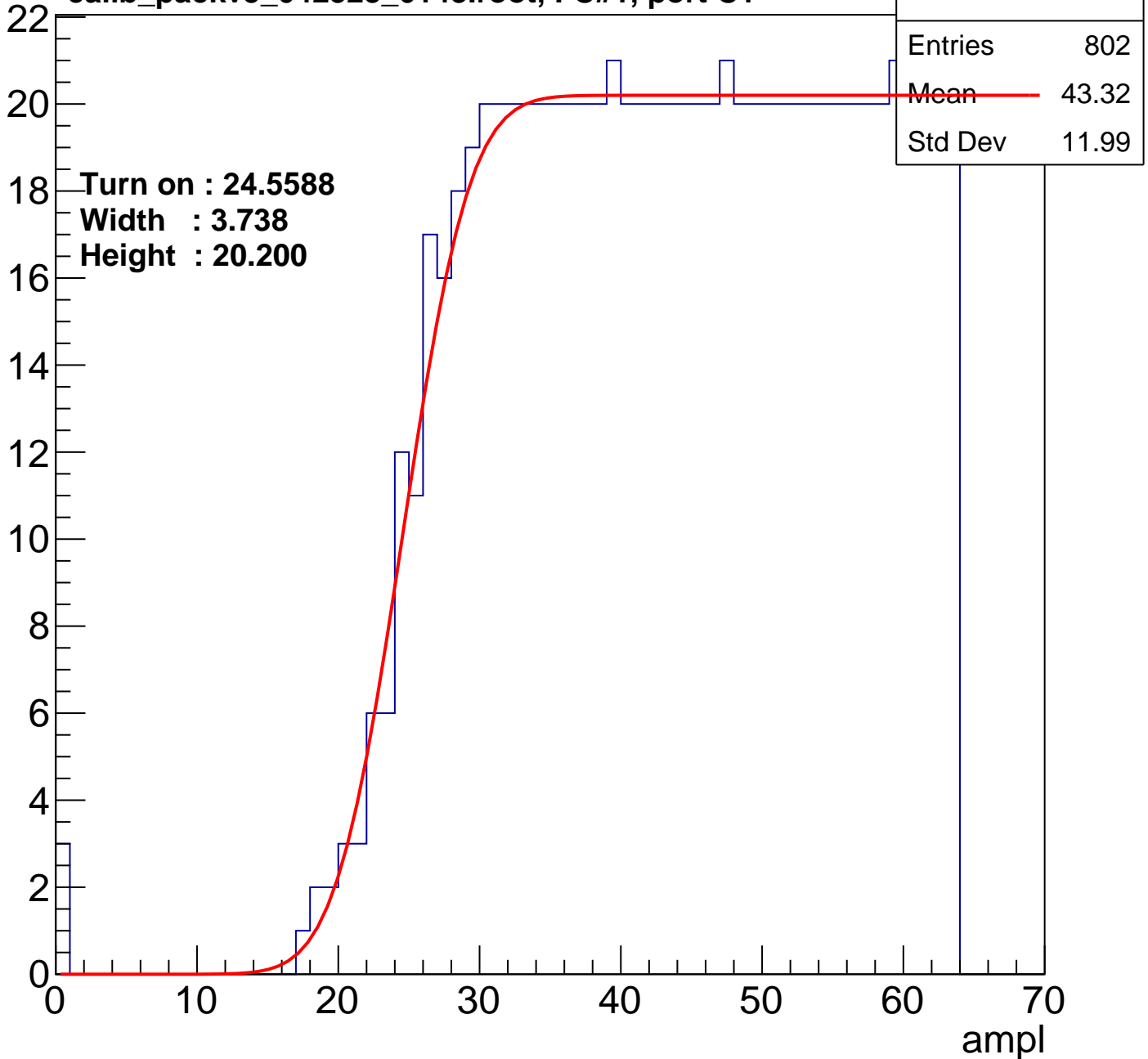
ampl



B0L101S, U24-ch112

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch113

calib_packv5_042523_0143.root, FC#1, port C1

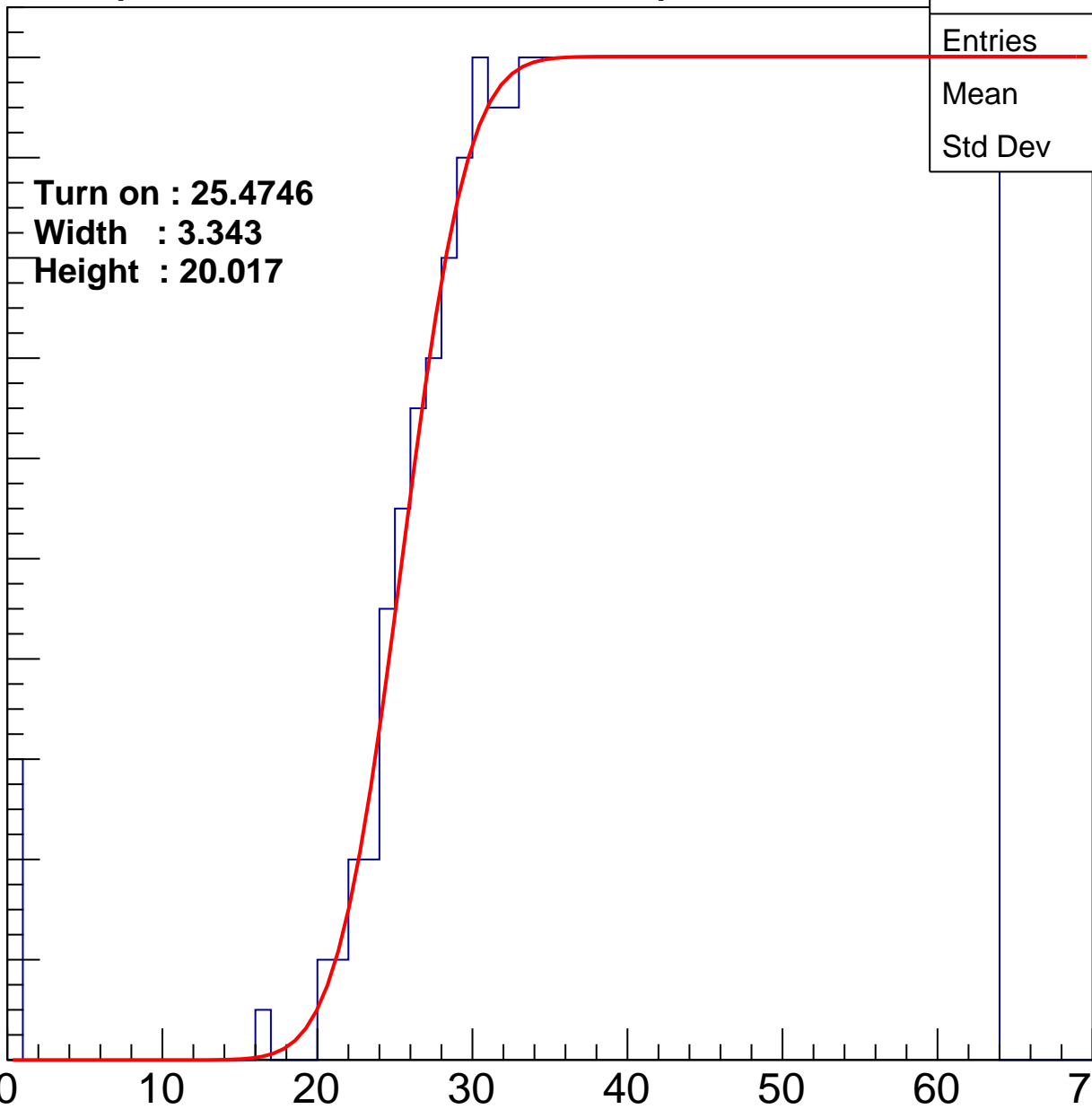
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4746
Width : 3.343
Height : 20.017

Entries	778
Mean	43.72
Std Dev	11.98

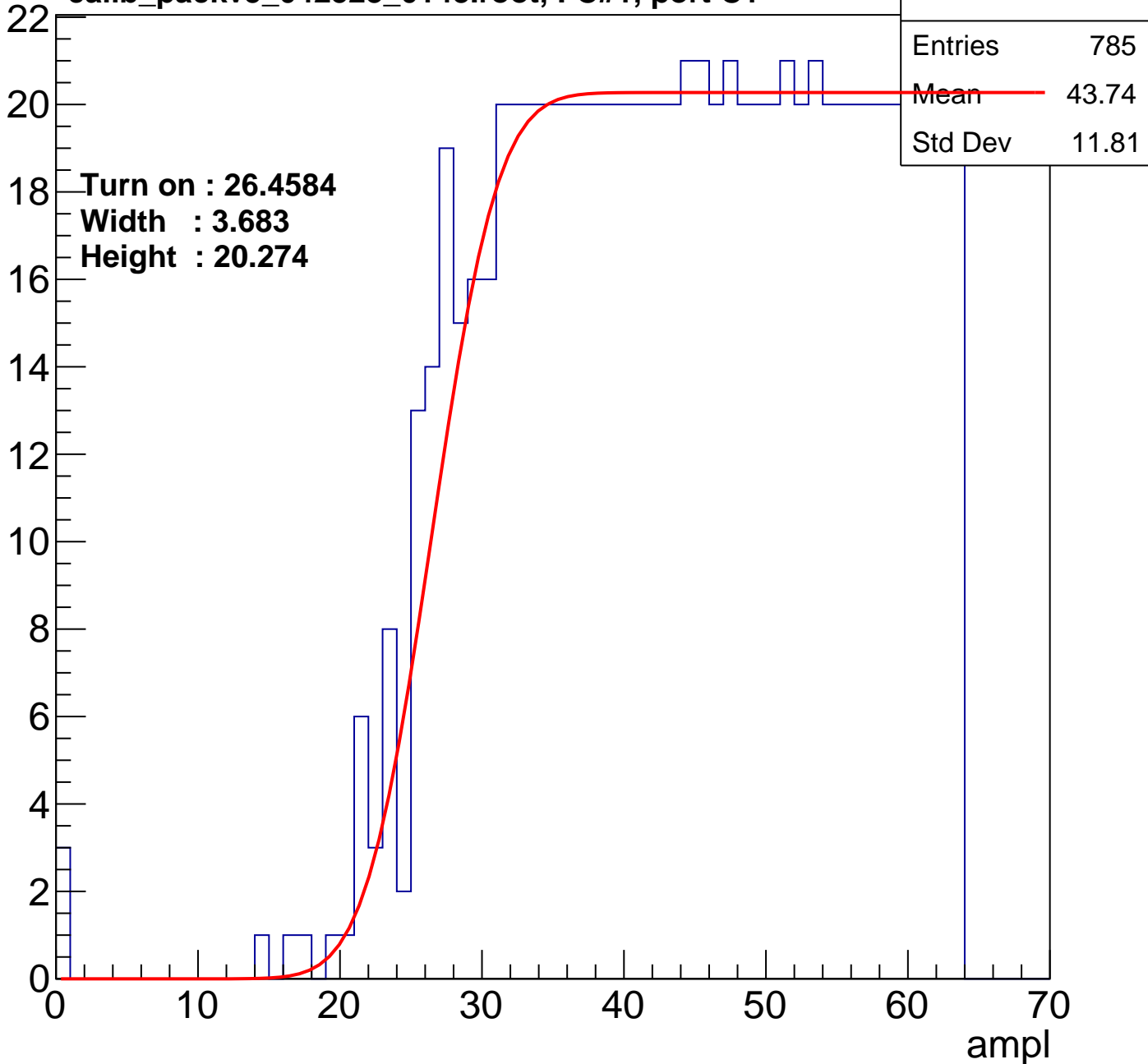
ampl



B0L101S, U24-ch114

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch115

calib_packv5_042523_0143.root, FC#1, port C1

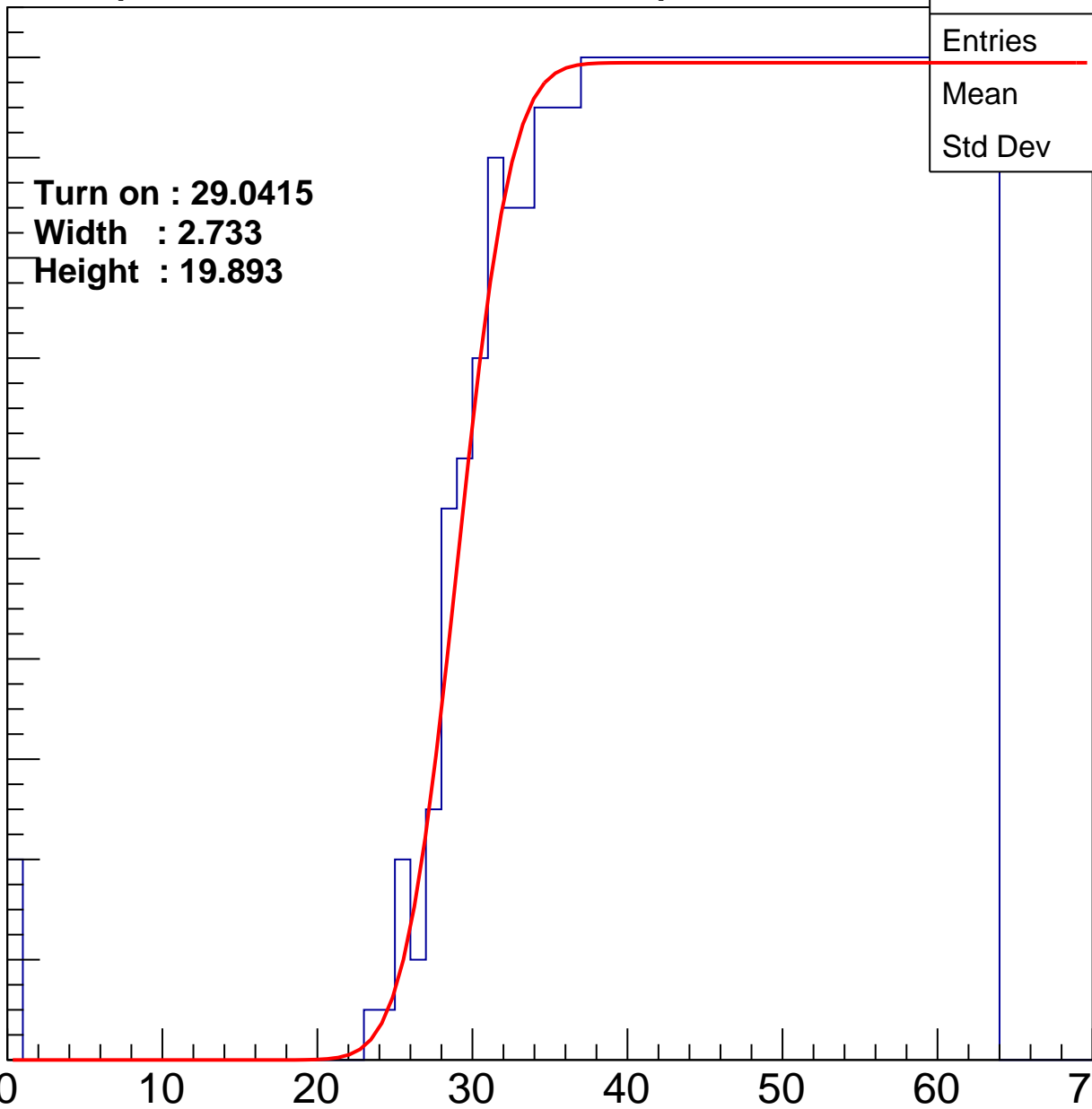
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.0415
Width : 2.733
Height : 19.893

Entries	703
Mean	45.62
Std Dev	10.86

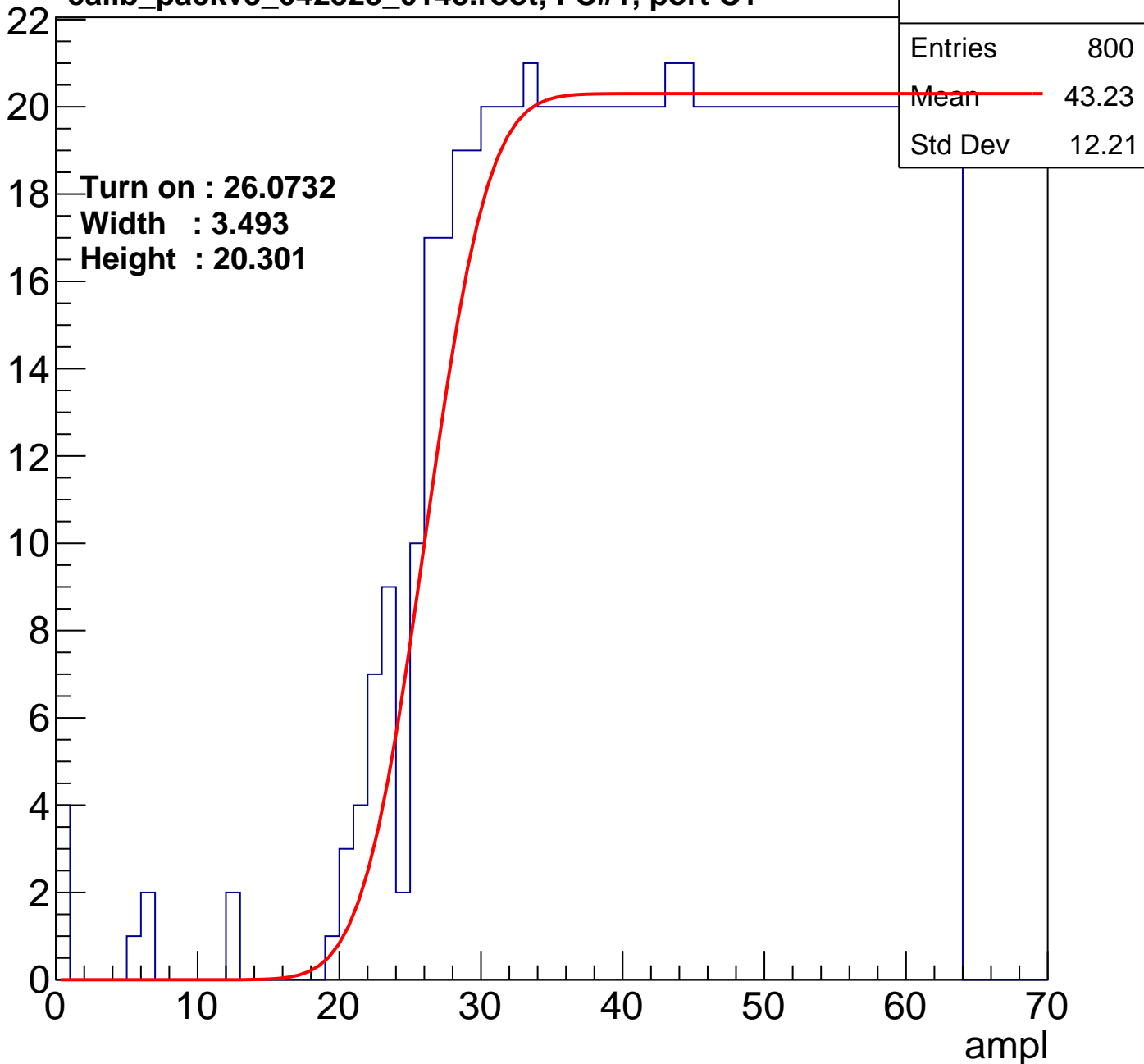
ampl



B0L101S, U24-ch116

calib_packv5_042523_0143.root, FC#1, port C1

Entry

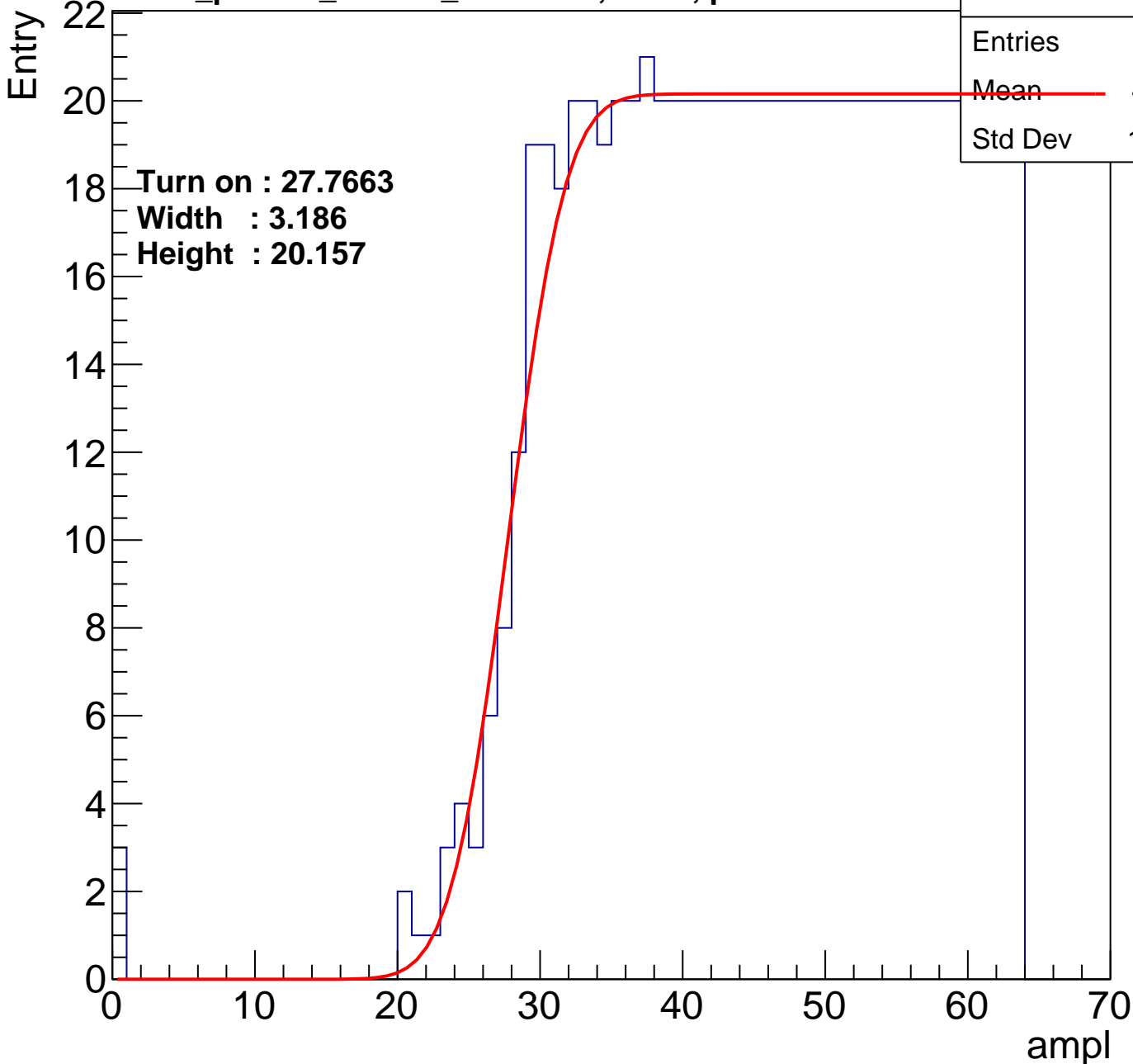


B0L101S, U24-ch117

calib_packv5_042523_0143.root, FC#1, port C1

Entries	739
Mean	44.81
Std Dev	11.17

Turn on : 27.7663
Width : 3.186
Height : 20.157



B0L101S, U24-ch118

calib_packv5_042523_0143.root, FC#1, port C1

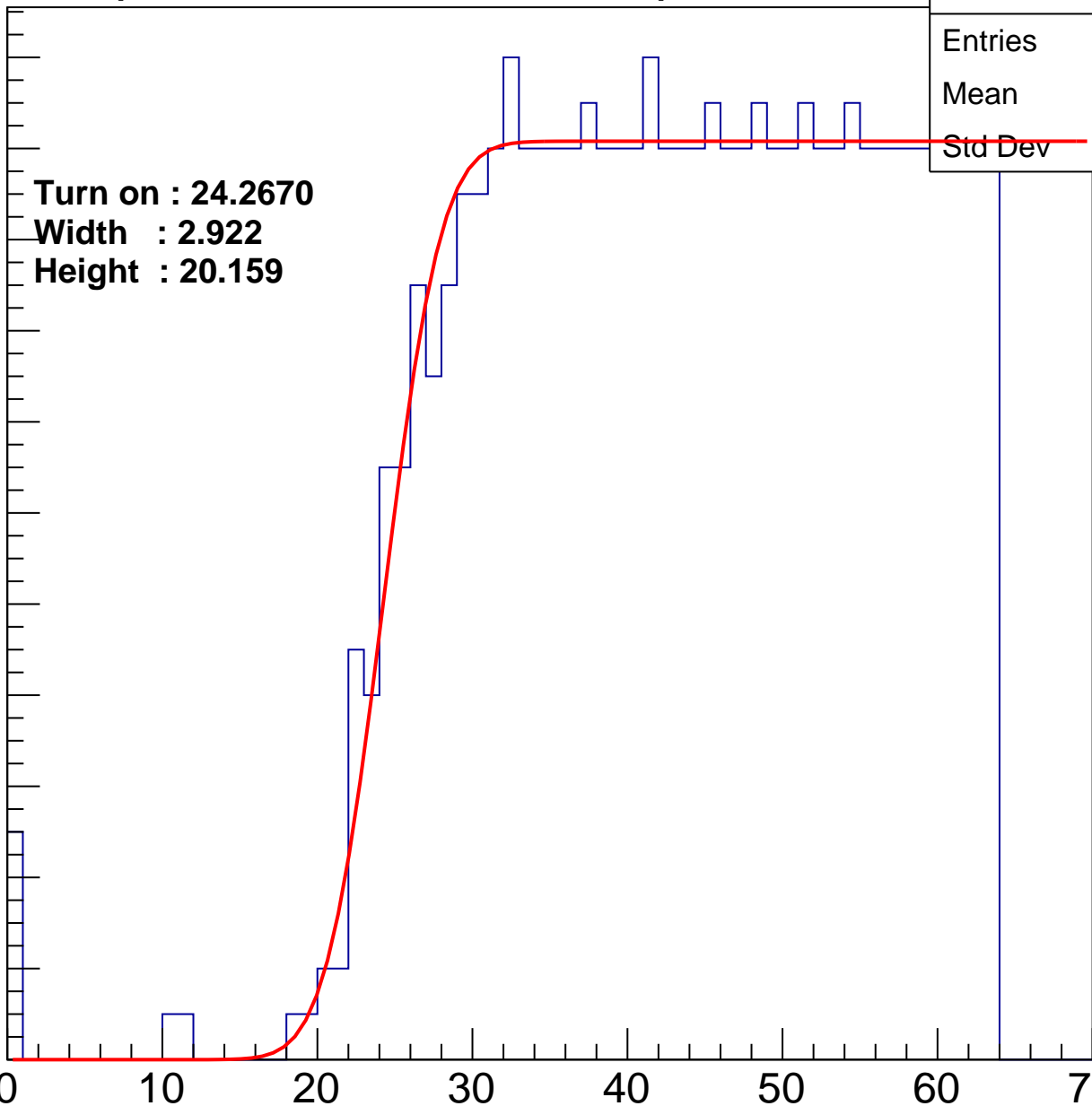
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.2670
Width : 2.922
Height : 20.159

Entries	812
Mean	43.11
Std Dev	12.21

ampl



B0L101S, U24-ch119

calib_packv5_042523_0143.root, FC#1, port C1

Entry

20

18

16

14

12

10

8

6

4

2

0

Turn on : 25.1042

Width : 3.413

Height : 20.035

Entries	789
Mean	43.51
Std Dev	11.95

ampl

0 10 20 30 40 50 60 70

B0L101S, U24-ch120

calib_packv5_042523_0143.root, FC#1, port C1

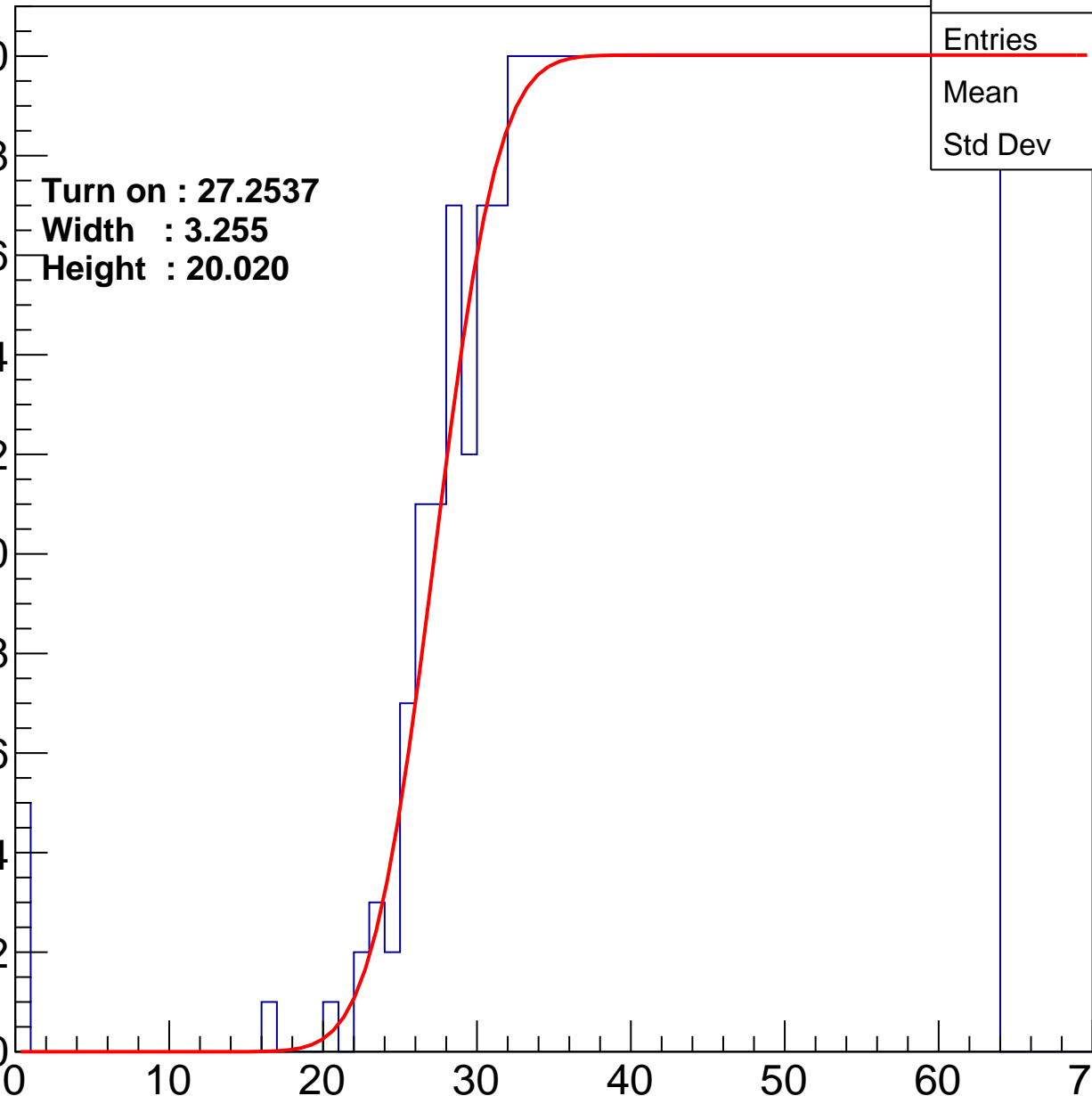
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2537
Width : 3.255
Height : 20.020

Entries	746
Mean	44.53
Std Dev	11.51

ampl



B0L101S, U24-ch121

calib_packv5_042523_0143.root, FC#1, port C1

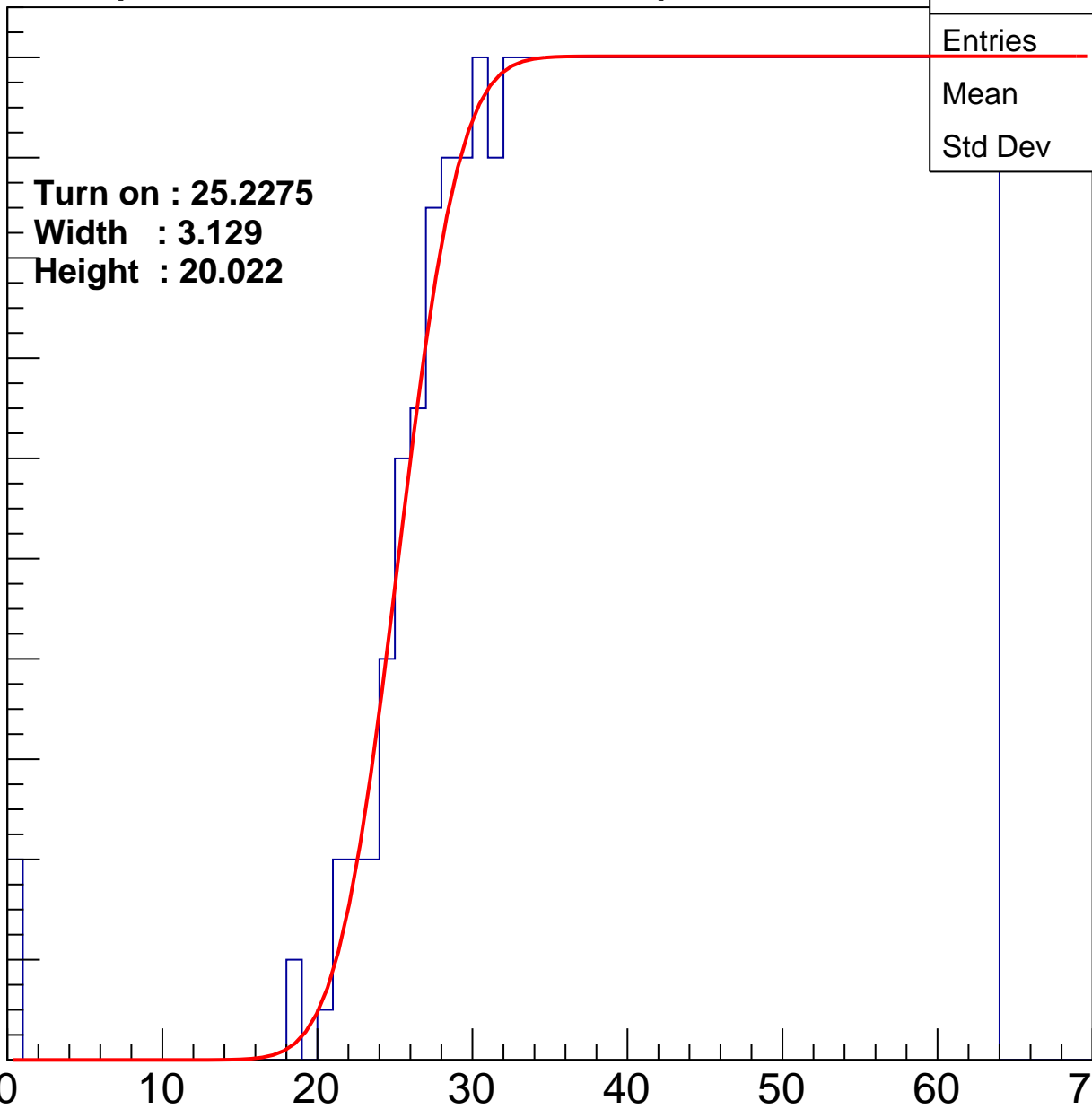
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2275
Width : 3.129
Height : 20.022

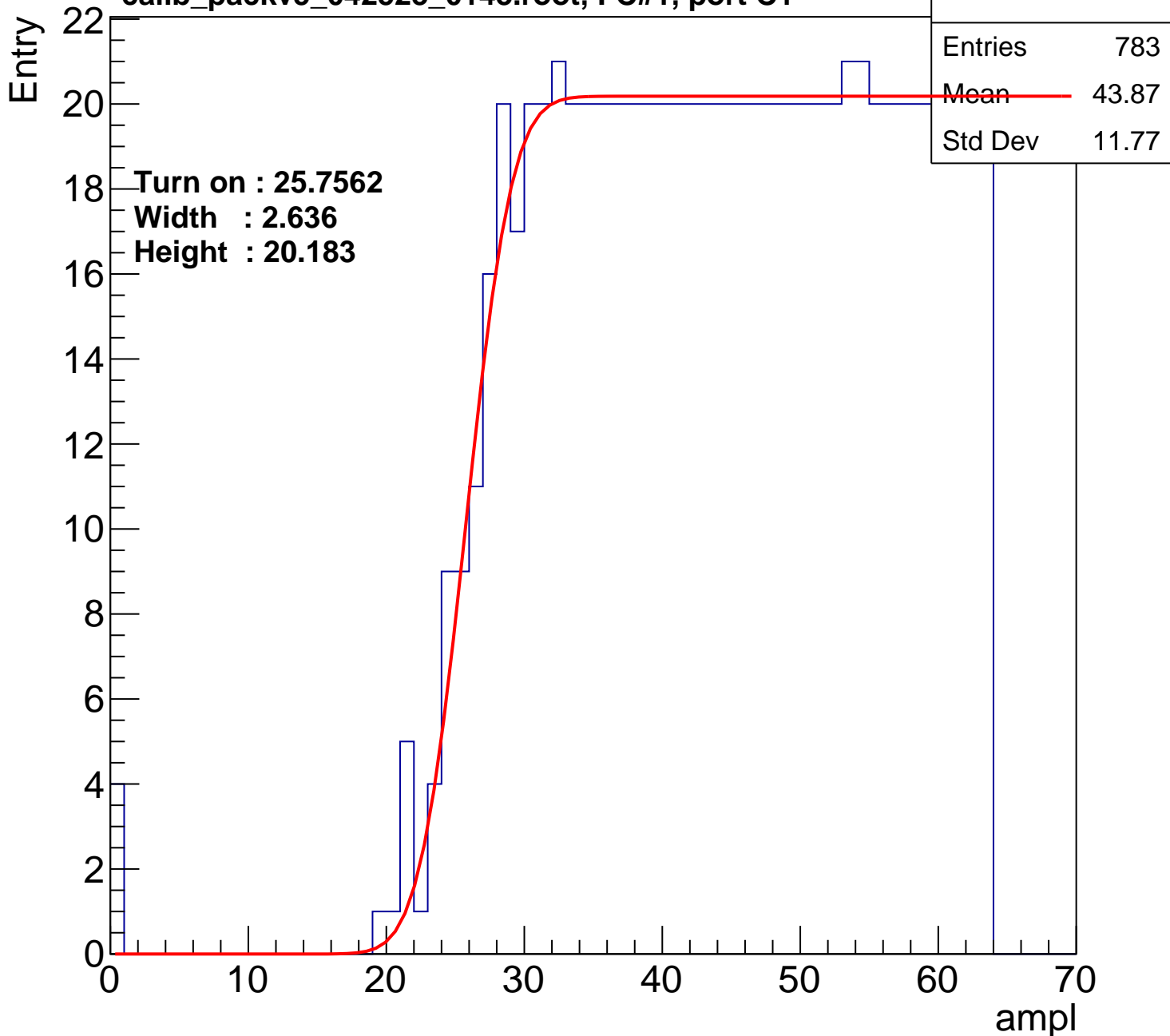
Entries	783
Mean	43.67
Std Dev	11.86

ampl



B0L101S, U24-ch122

calib_packv5_042523_0143.root, FC#1, port C1



B0L101S, U24-ch123

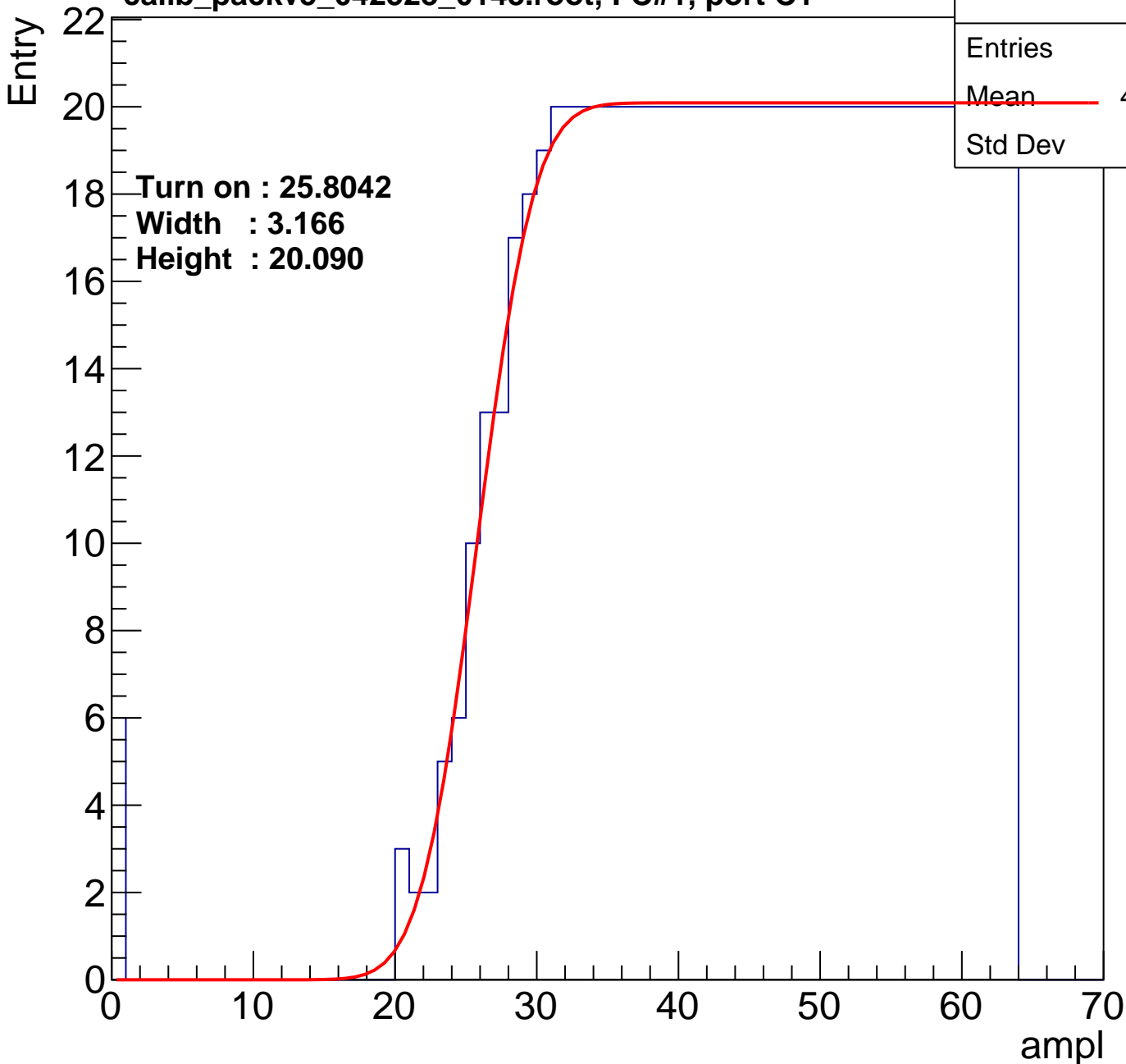
calib_packv5_042523_0143.root, FC#1, port C1

Entries	775
Mean	43.86
Std Dev	11.9

Turn on : 25.8042

Width : 3.166

Height : 20.090

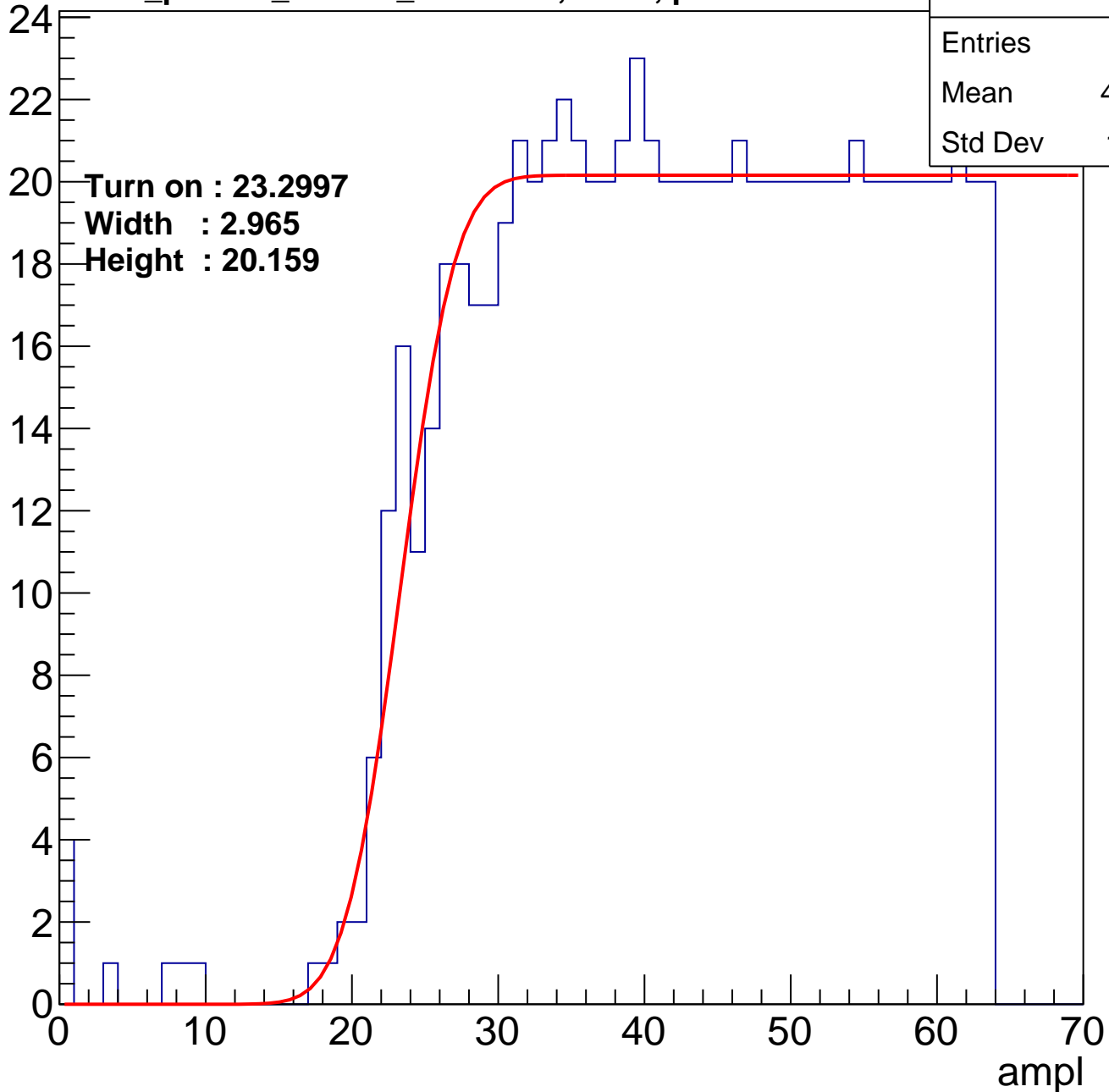


B0L101S, U24-ch124

calib_packv5_042523_0143.root, FC#1, port C1

Entries	835
Mean	42.57
Std Dev	12.51

Entry



B0L101S, U24-ch125

calib_packv5_042523_0143.root, FC#1, port C1

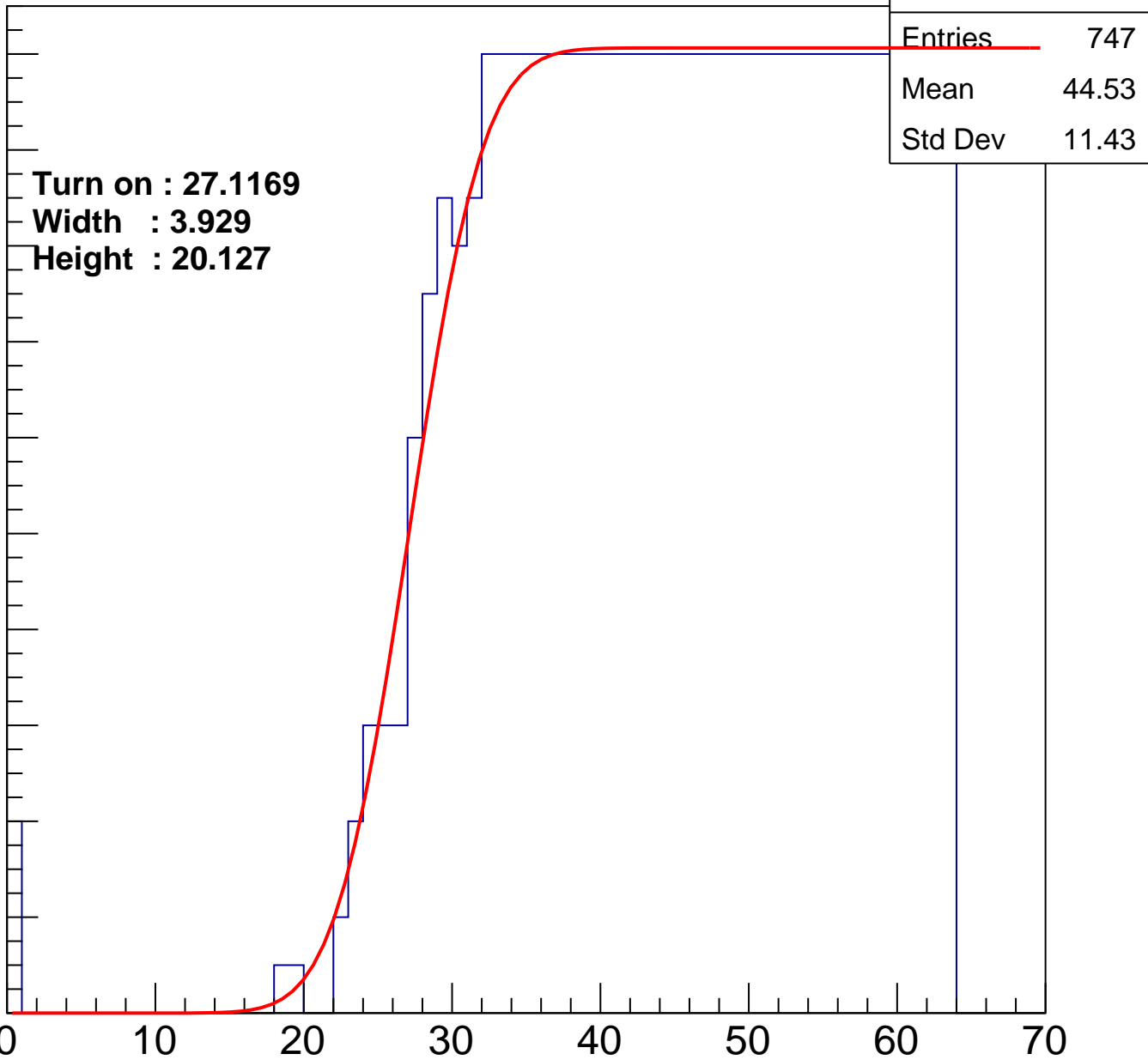
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1169
Width : 3.929
Height : 20.127

Entries	747
Mean	44.53
Std Dev	11.43

ampl



B0L101S, U24-ch126

calib_packv5_042523_0143.root, FC#1, port C1

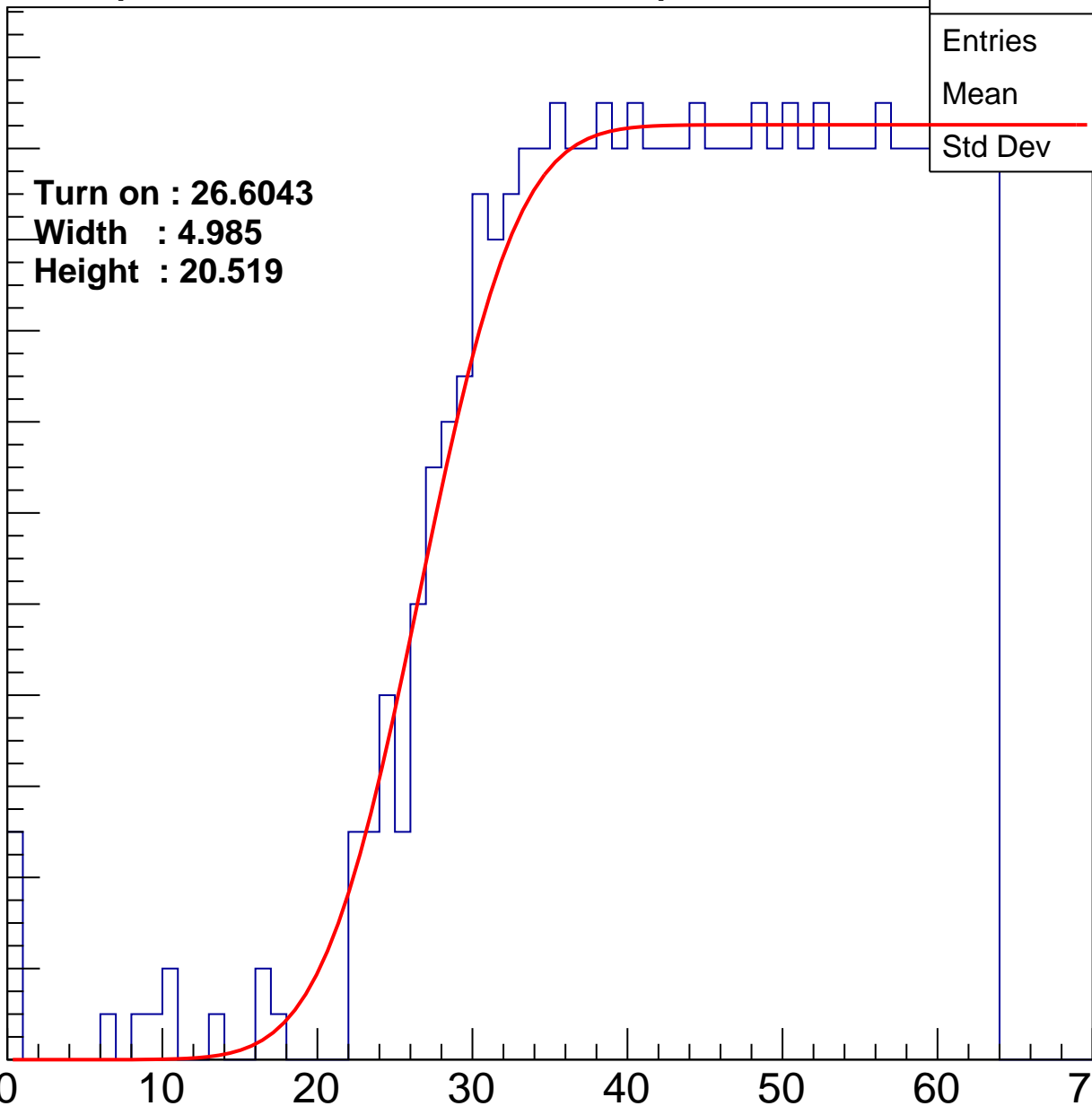
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6043
Width : 4.985
Height : 20.519

Entries	775
Mean	43.96
Std Dev	12.07

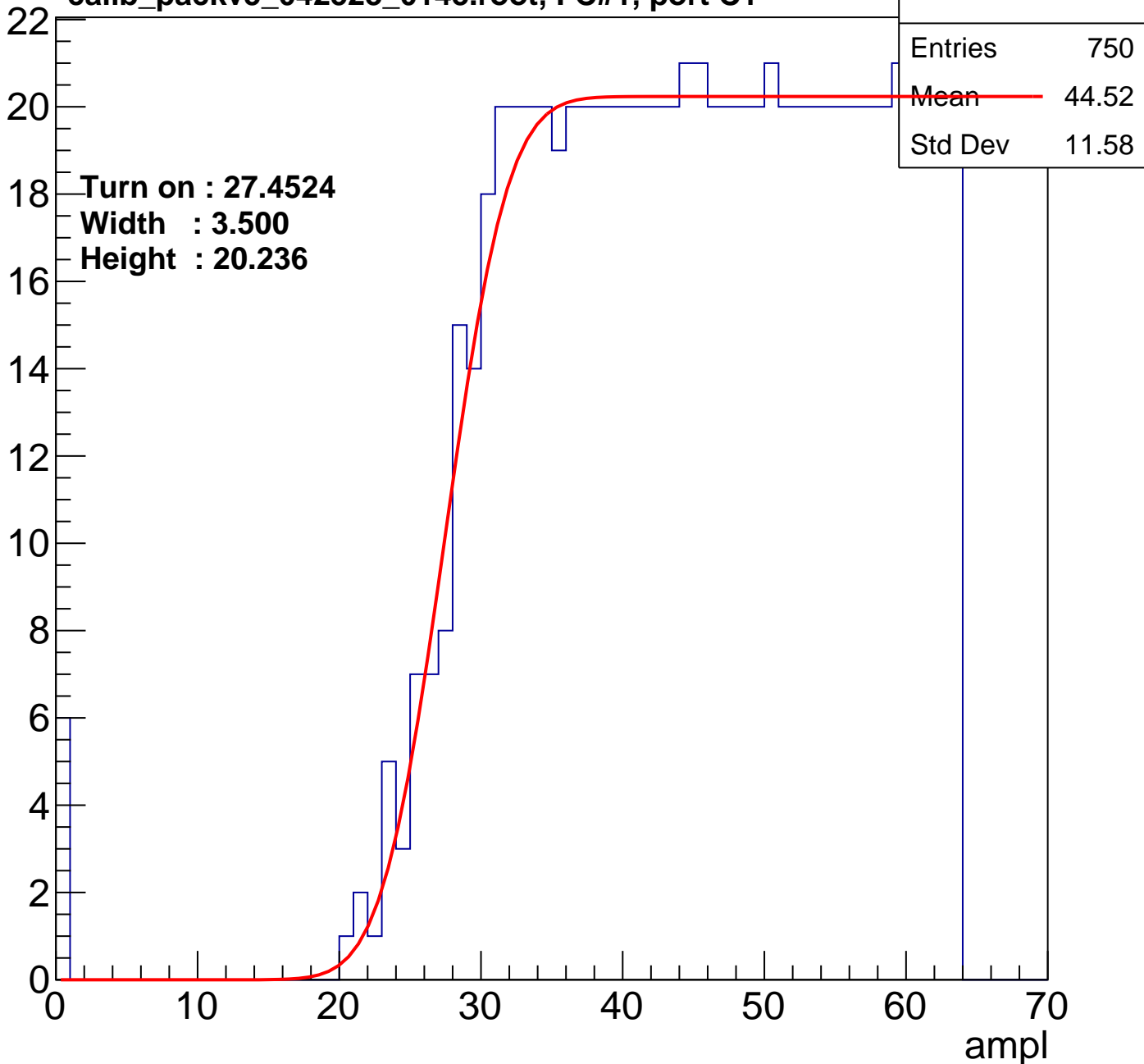
ampl



B0L101S, U24-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U24-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry

