

B1L102S, U15-ch0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	413
Mean	42.4
Std Dev	12.95

Turn on : 23.9728

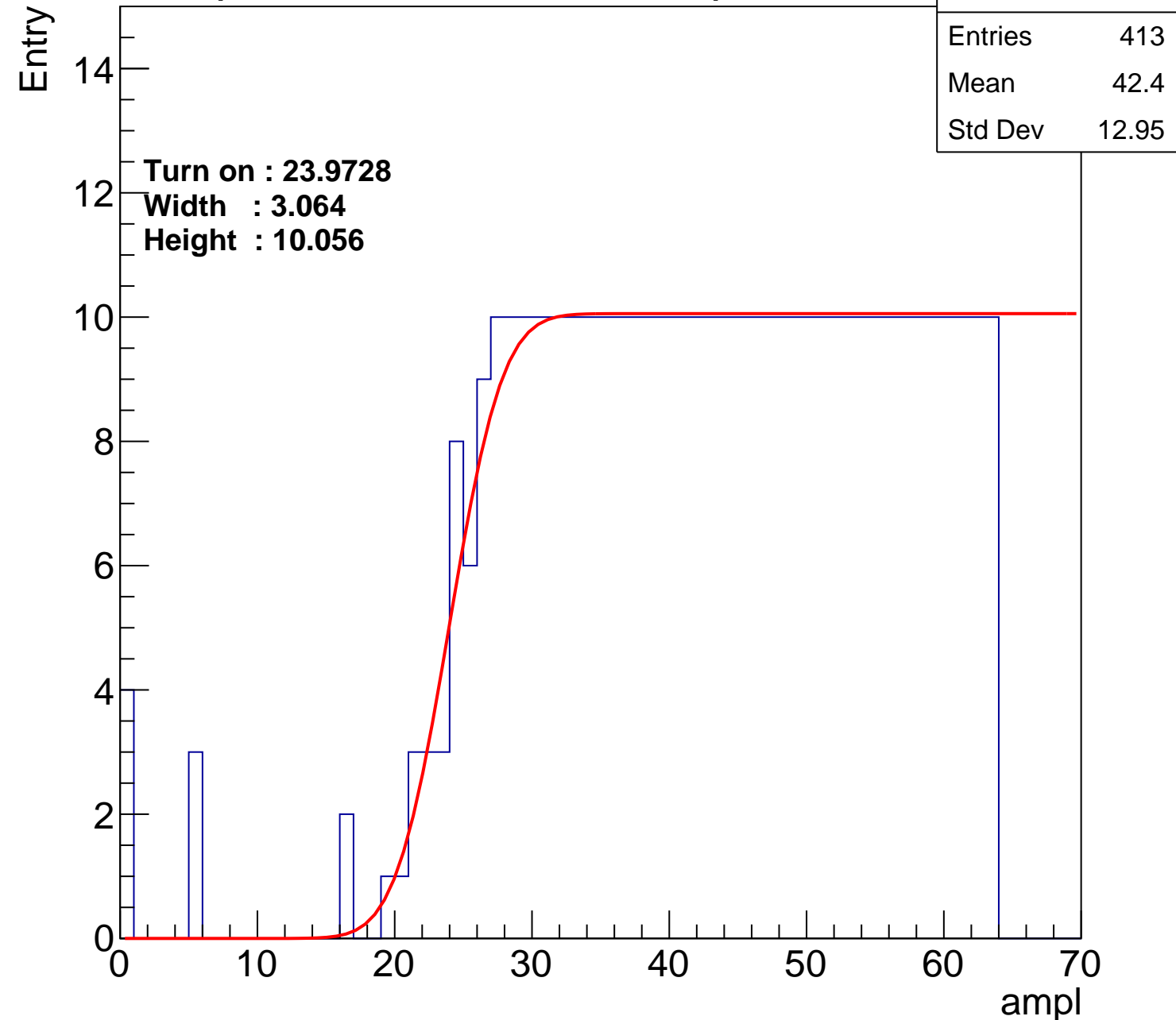
Width : 3.064

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.16
Std Dev	11.63

Turn on : 25.5158

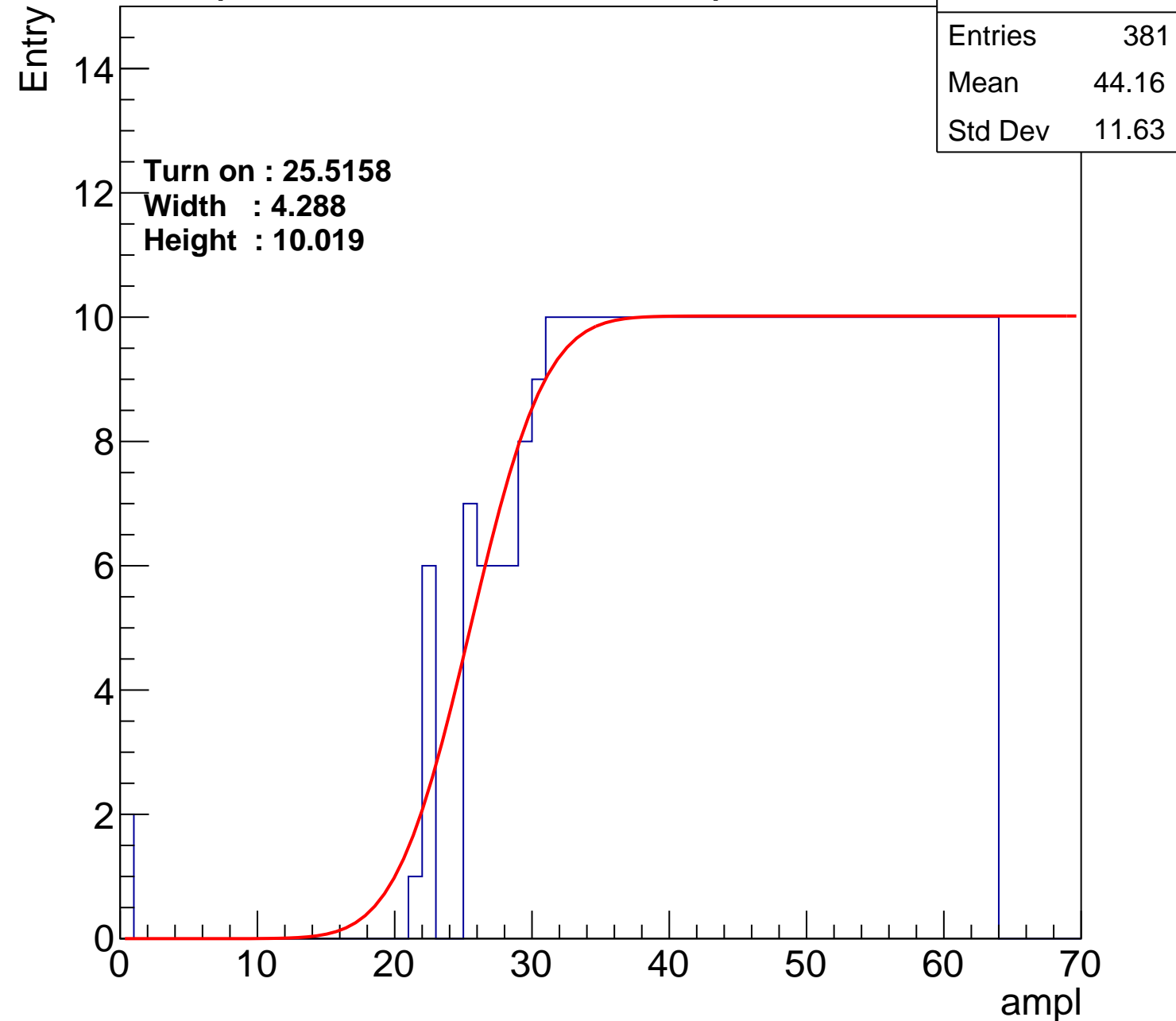
Width : 4.288

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.26
Std Dev	11.64

Turn on : 26.7466

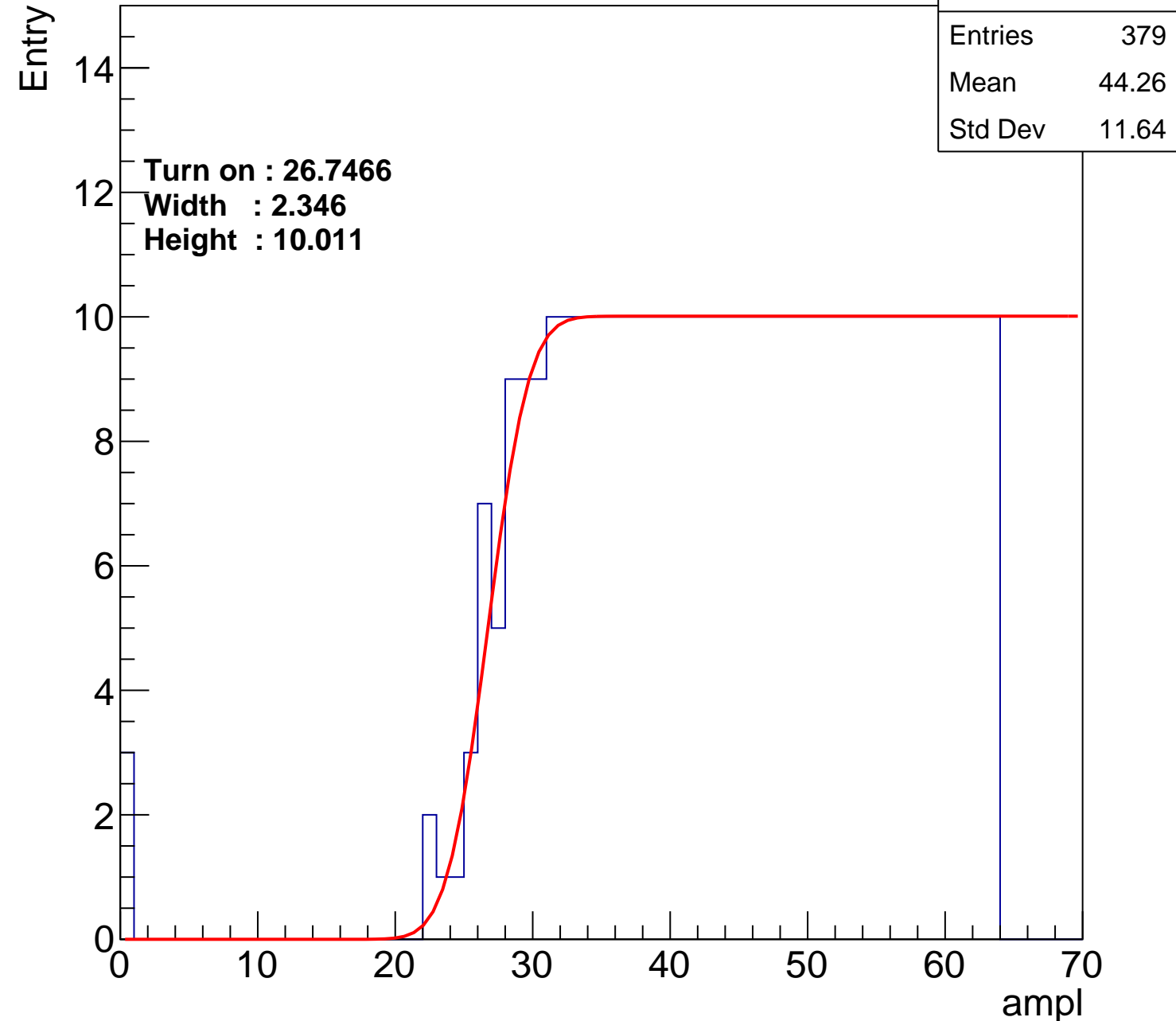
Width : 2.346

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	43.99
Std Dev	12.05

Turn on : 25.9846

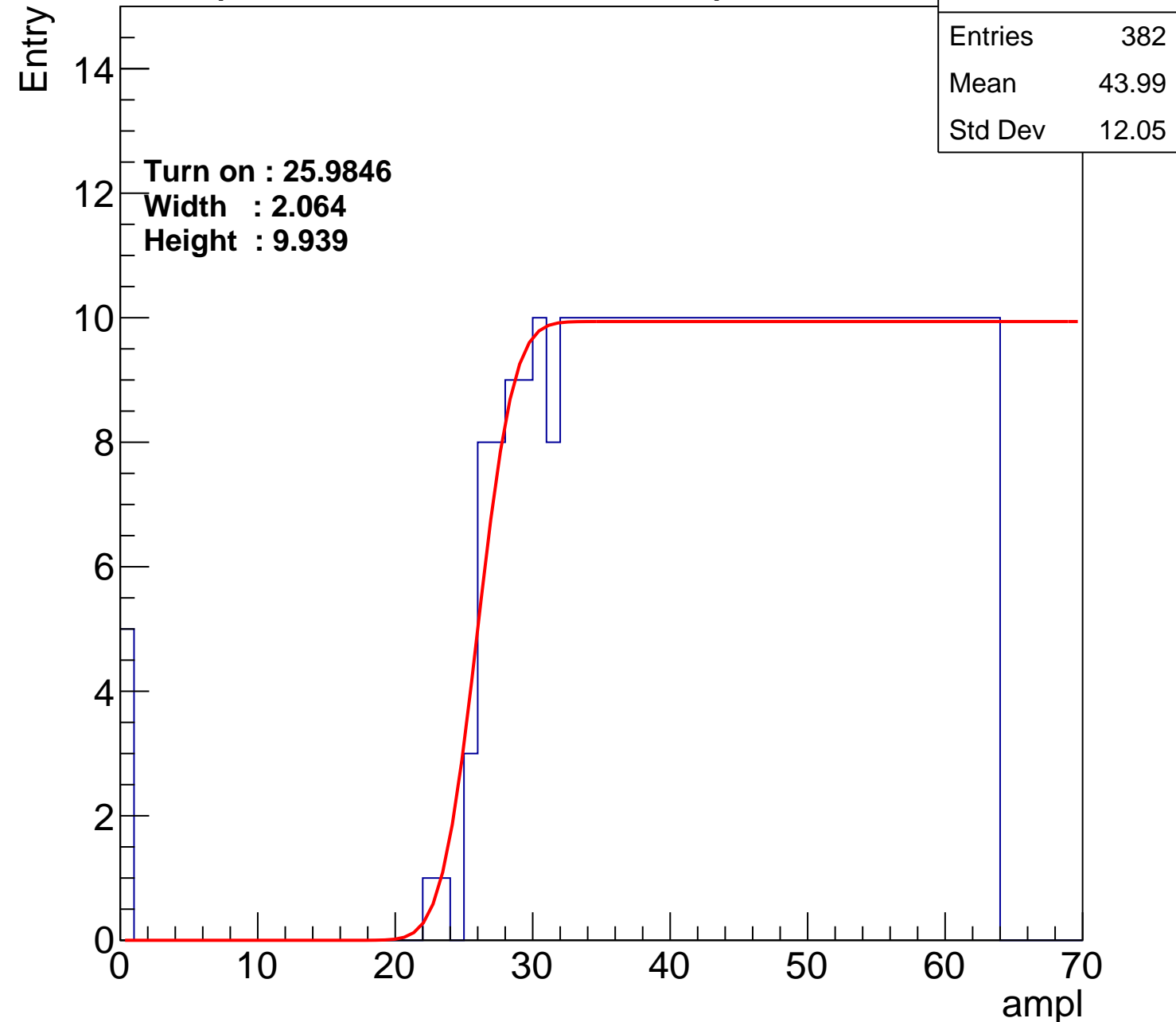
Width : 2.064

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.11
Std Dev	11.85

Turn on : 26.3398

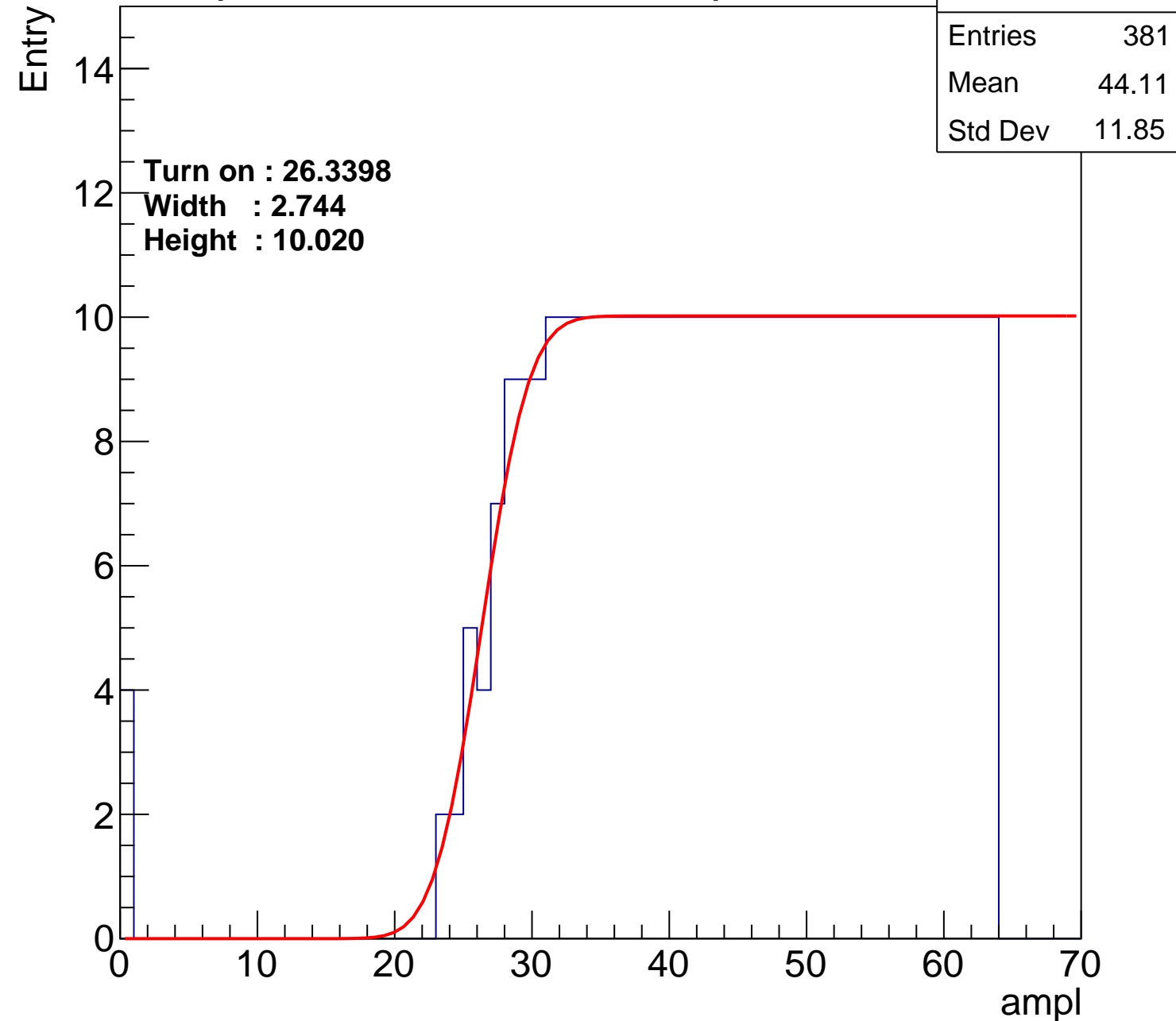
Width : 2.744

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.62
Std Dev	12.03

Turn on : 25.3349

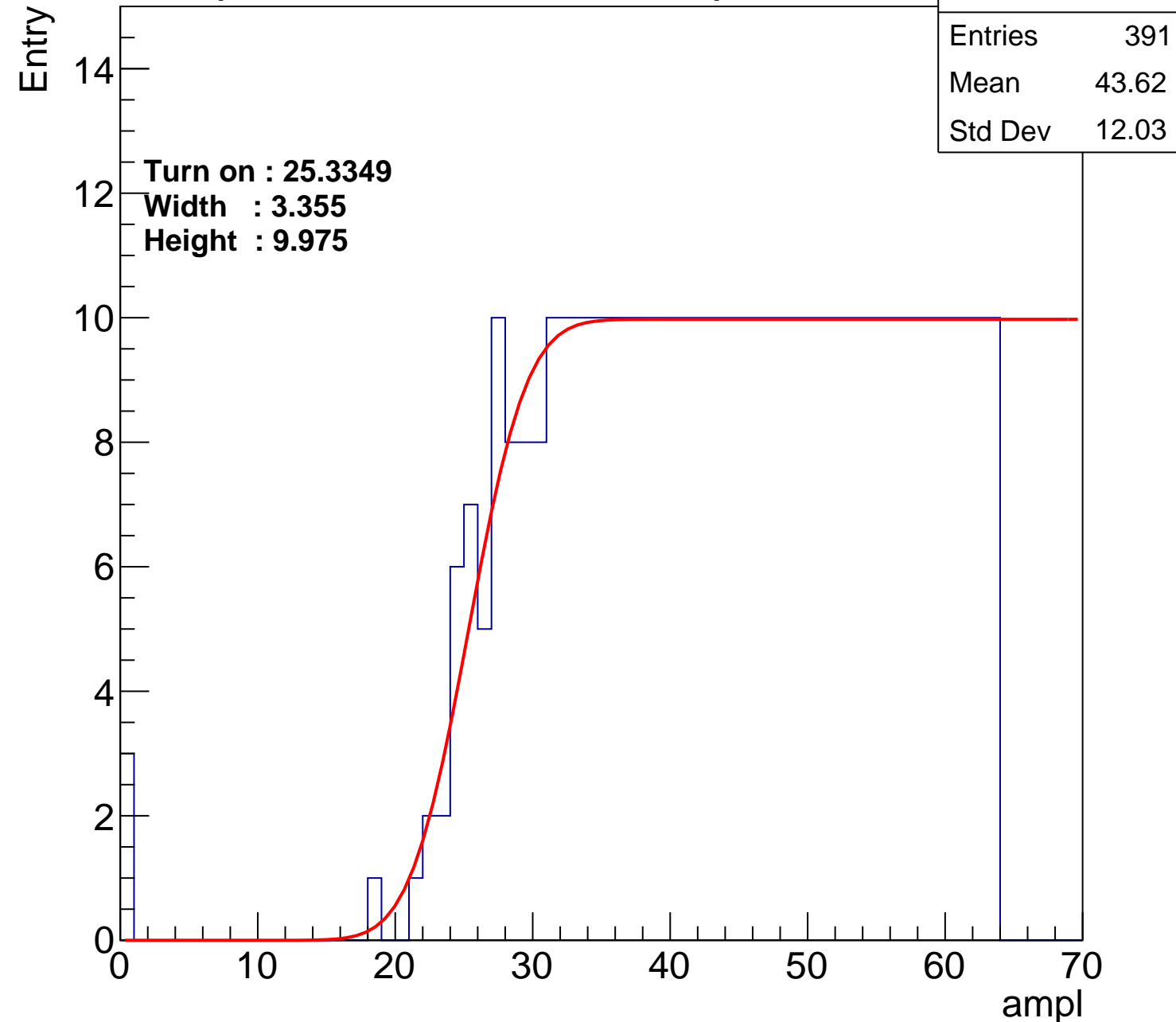
Width : 3.355

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.33
Std Dev	12.39

Turn on : 25.4086

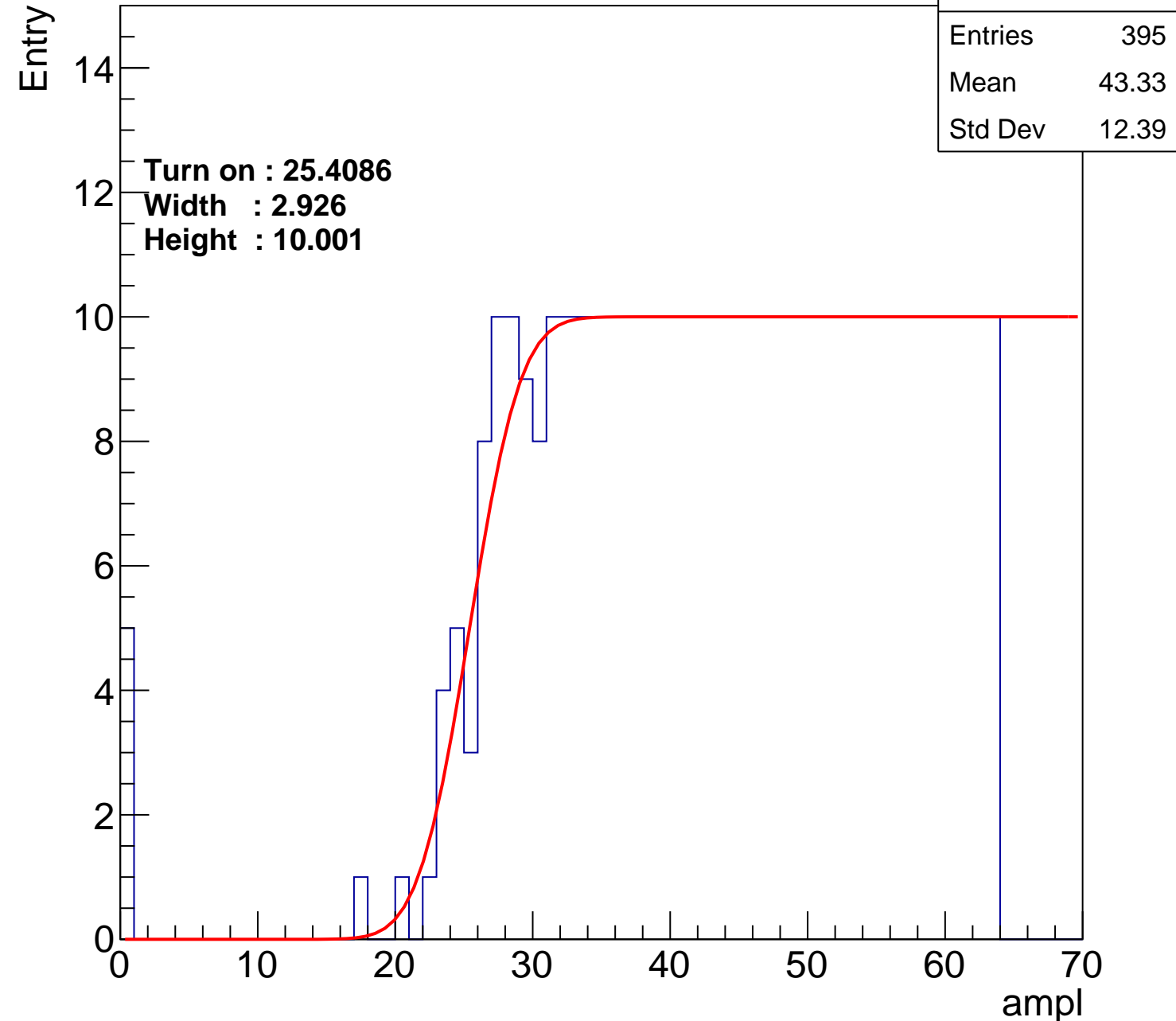
Width : 2.926

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch7

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.35
Std Dev	11.79

Turn on : 27.3398

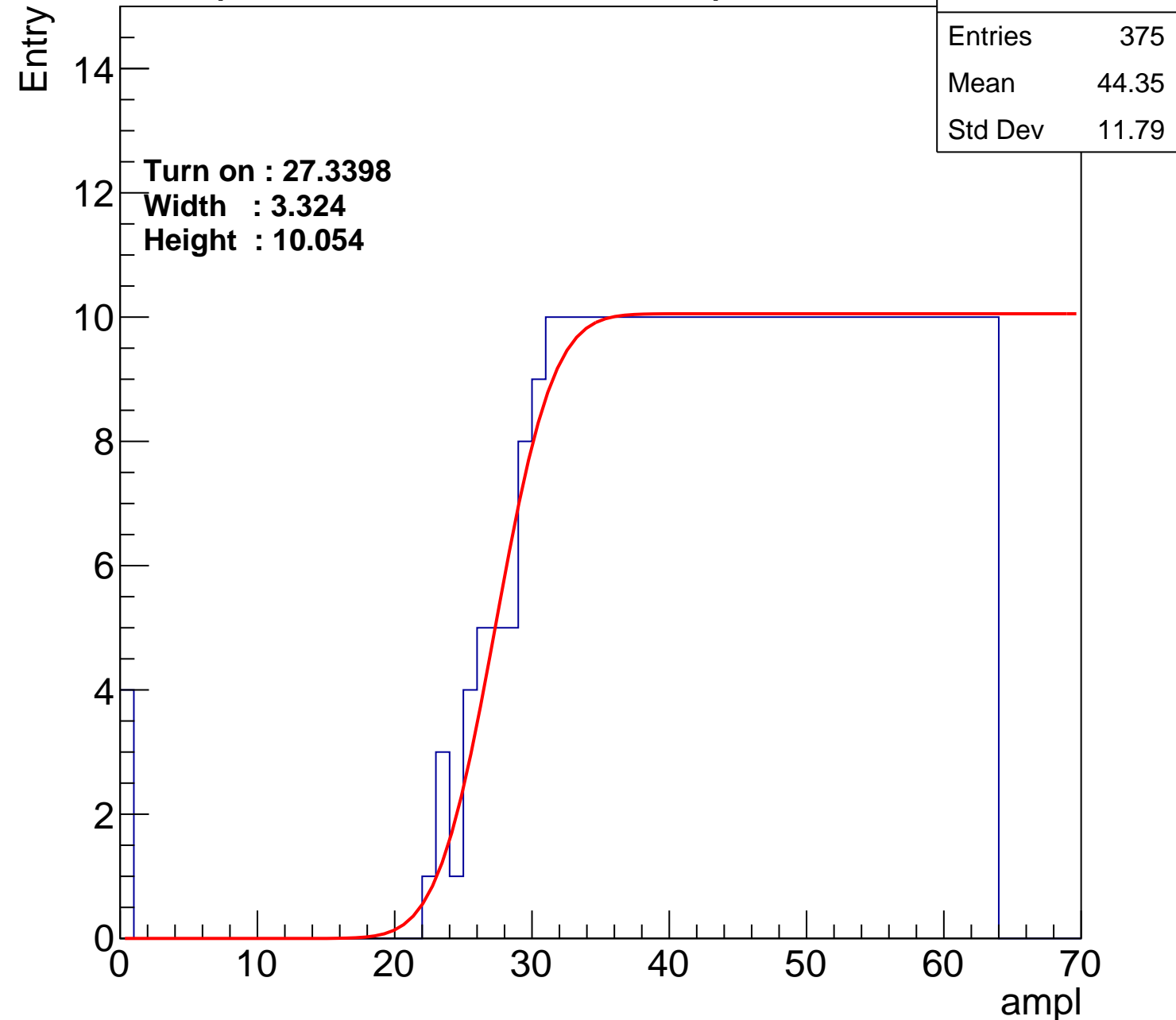
Width : 3.324

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.99
Std Dev	11.69

Turn on : 25.7458

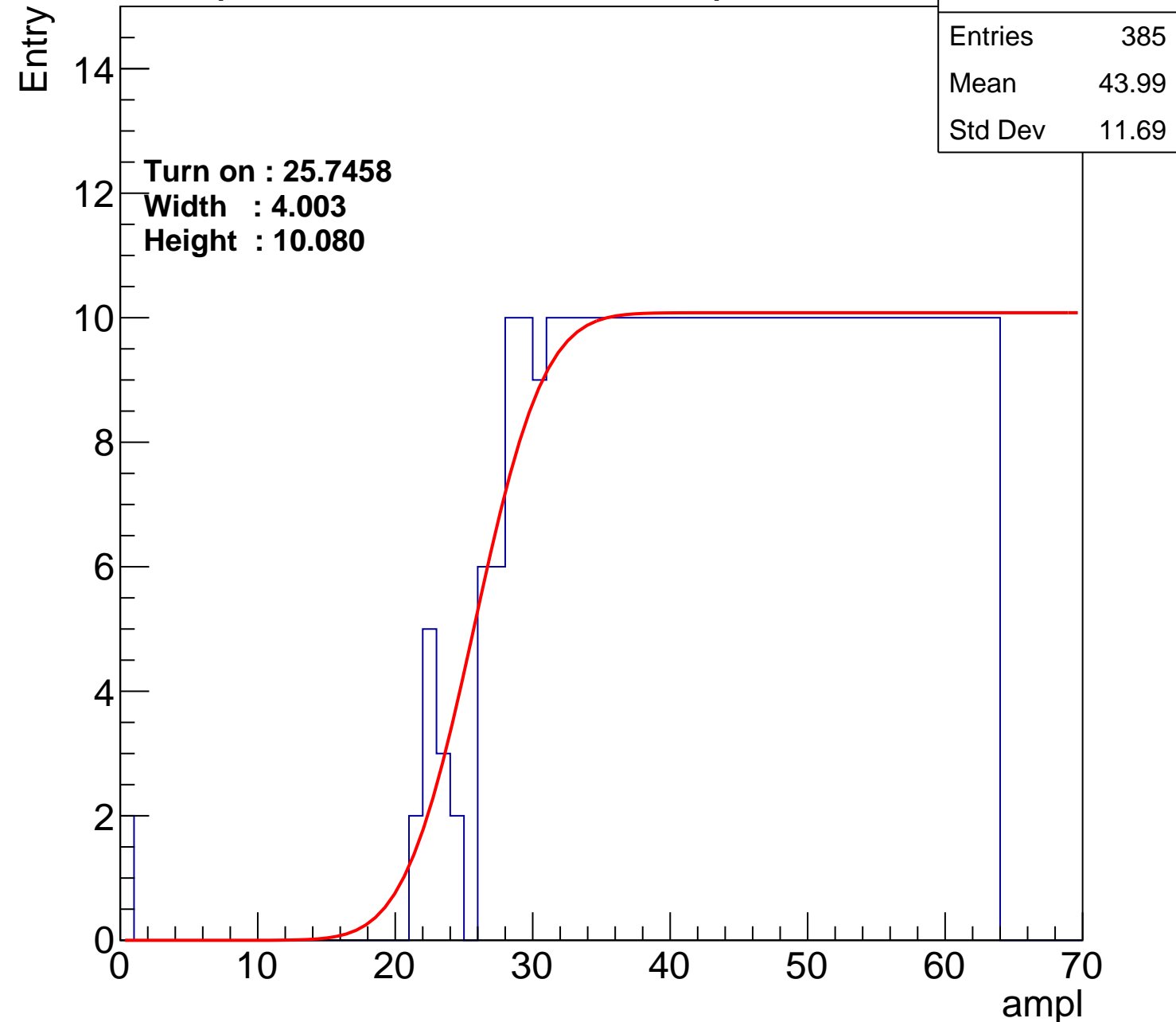
Width : 4.003

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch9

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 25.5631

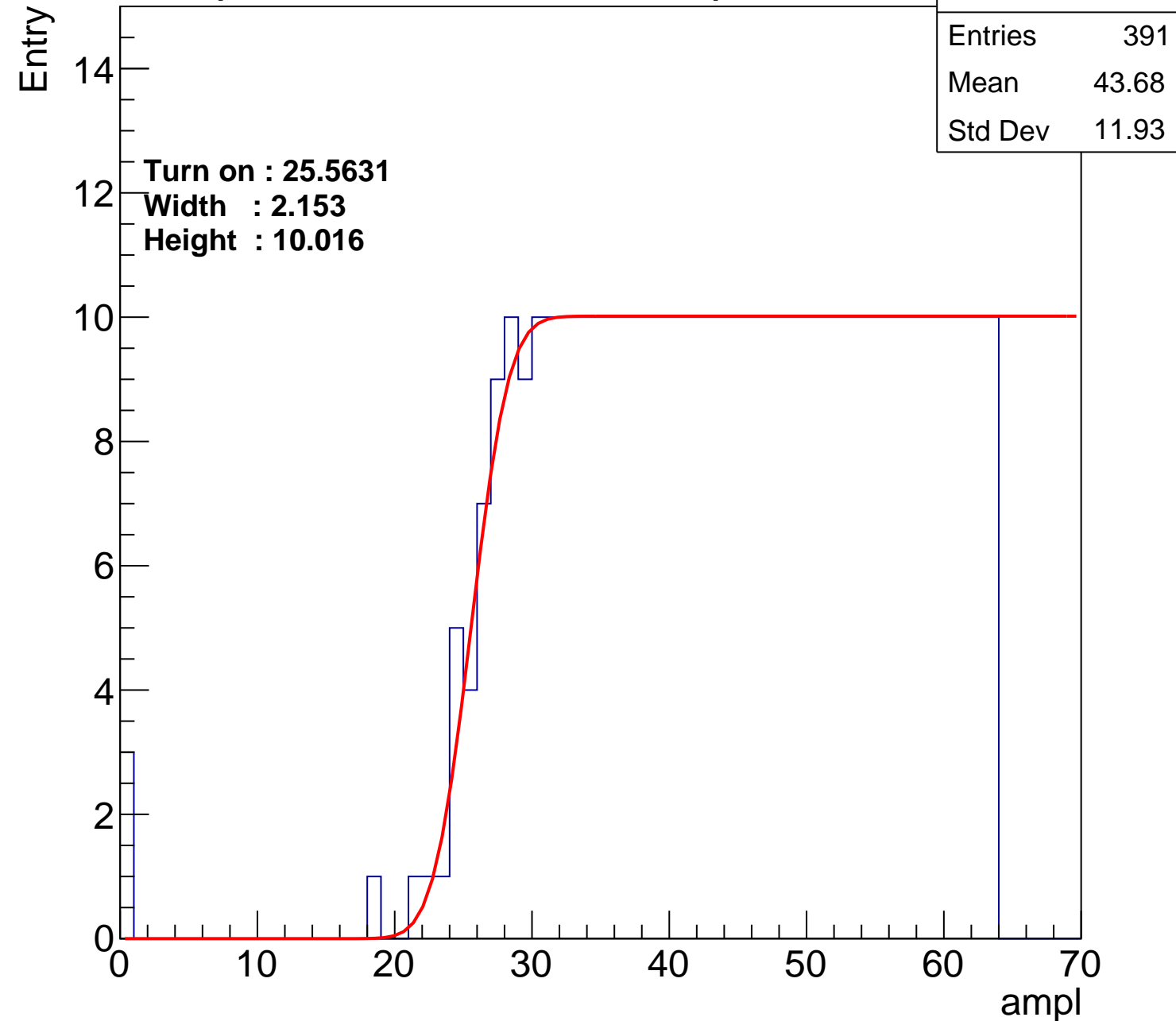
Width : 2.153

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch10

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.67
Std Dev	11.14

Turn on : 26.8965

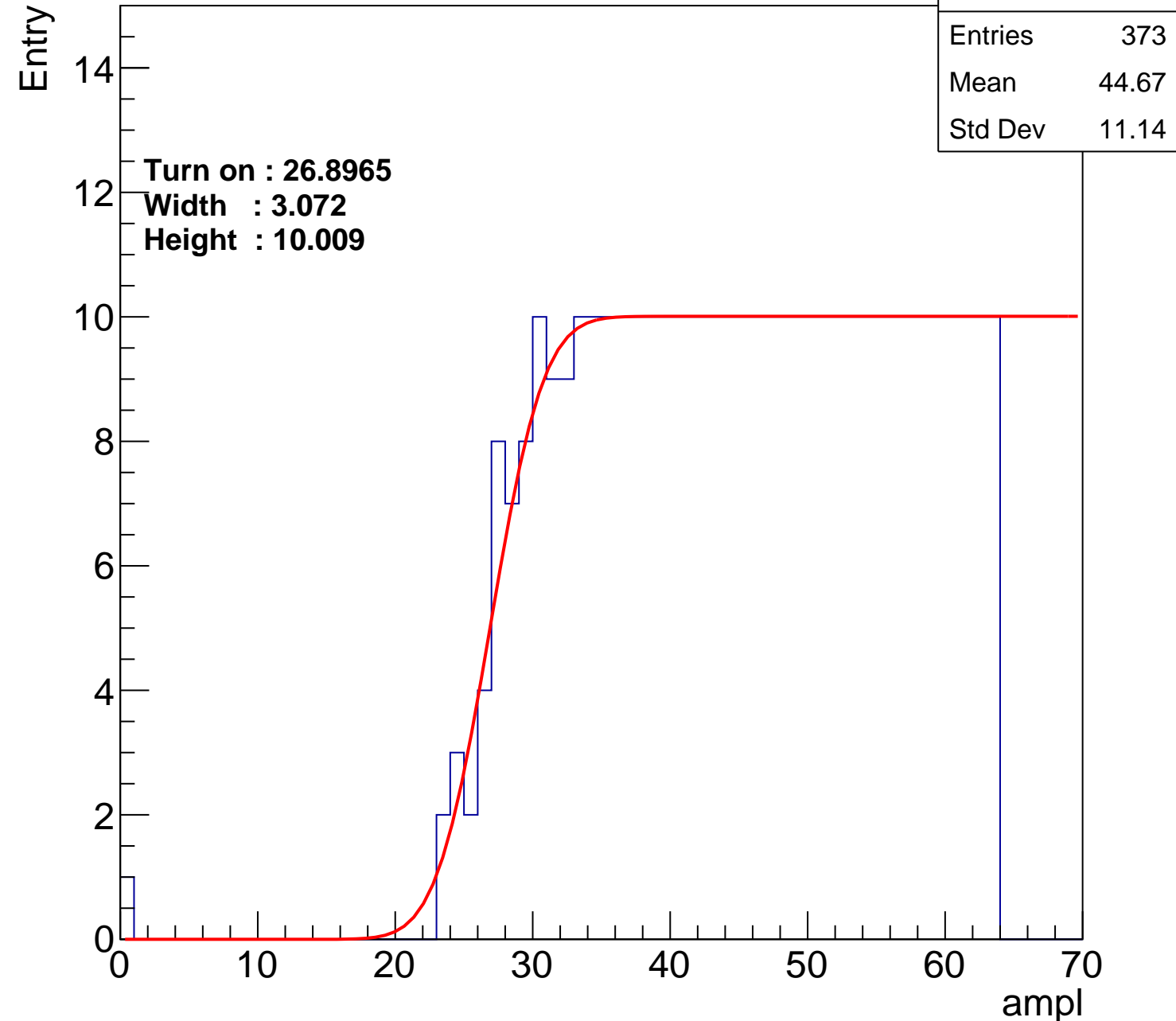
Width : 3.072

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch11

calib_packv5_042523_0143.root, FC#11, port A2

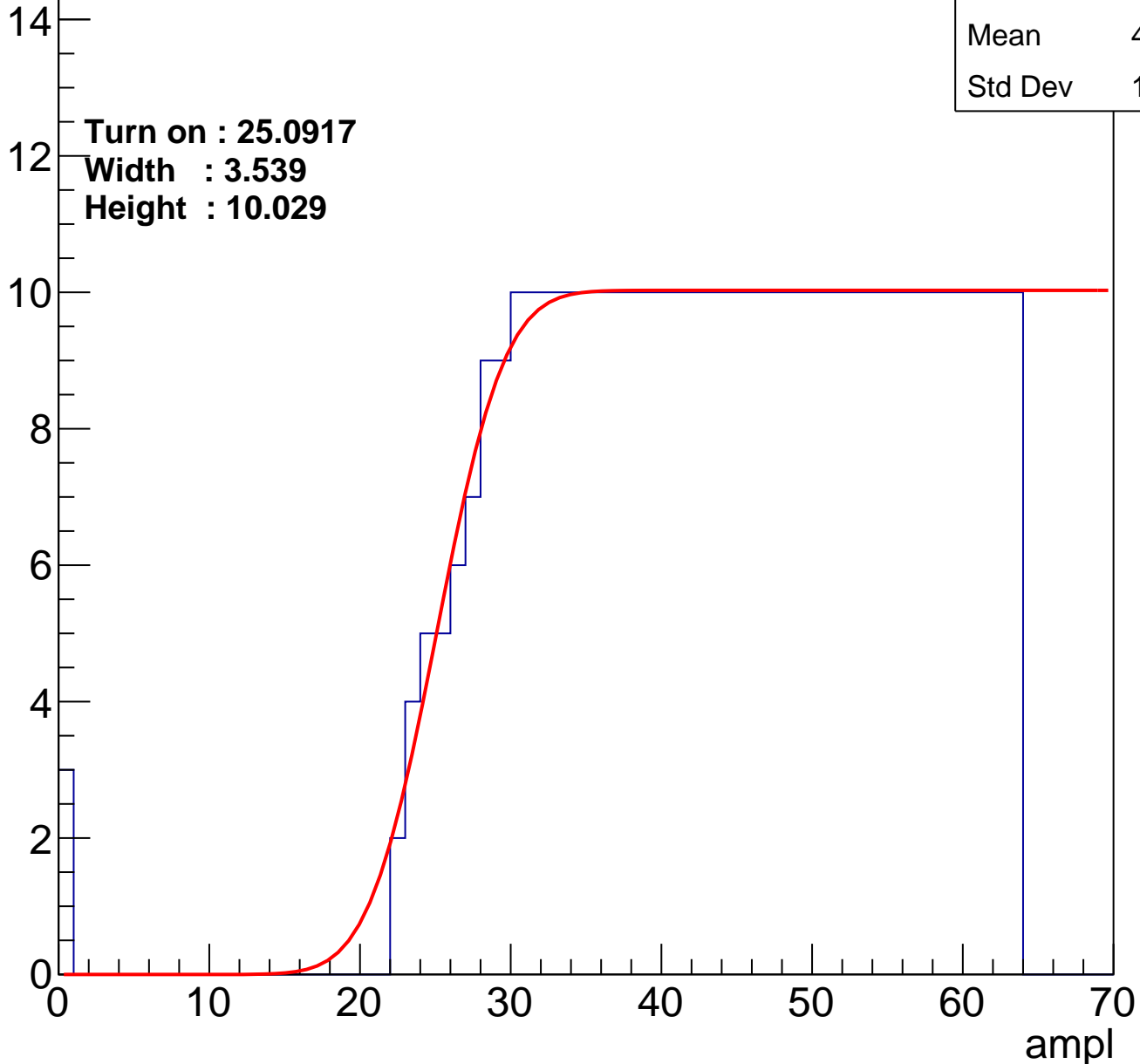
Entries	390
Mean	43.72
Std Dev	11.92

Turn on : 25.0917

Width : 3.539

Height : 10.029

Entry



B1L102S, U15-ch12

calib_packv5_042523_0143.root, FC#11, port A2

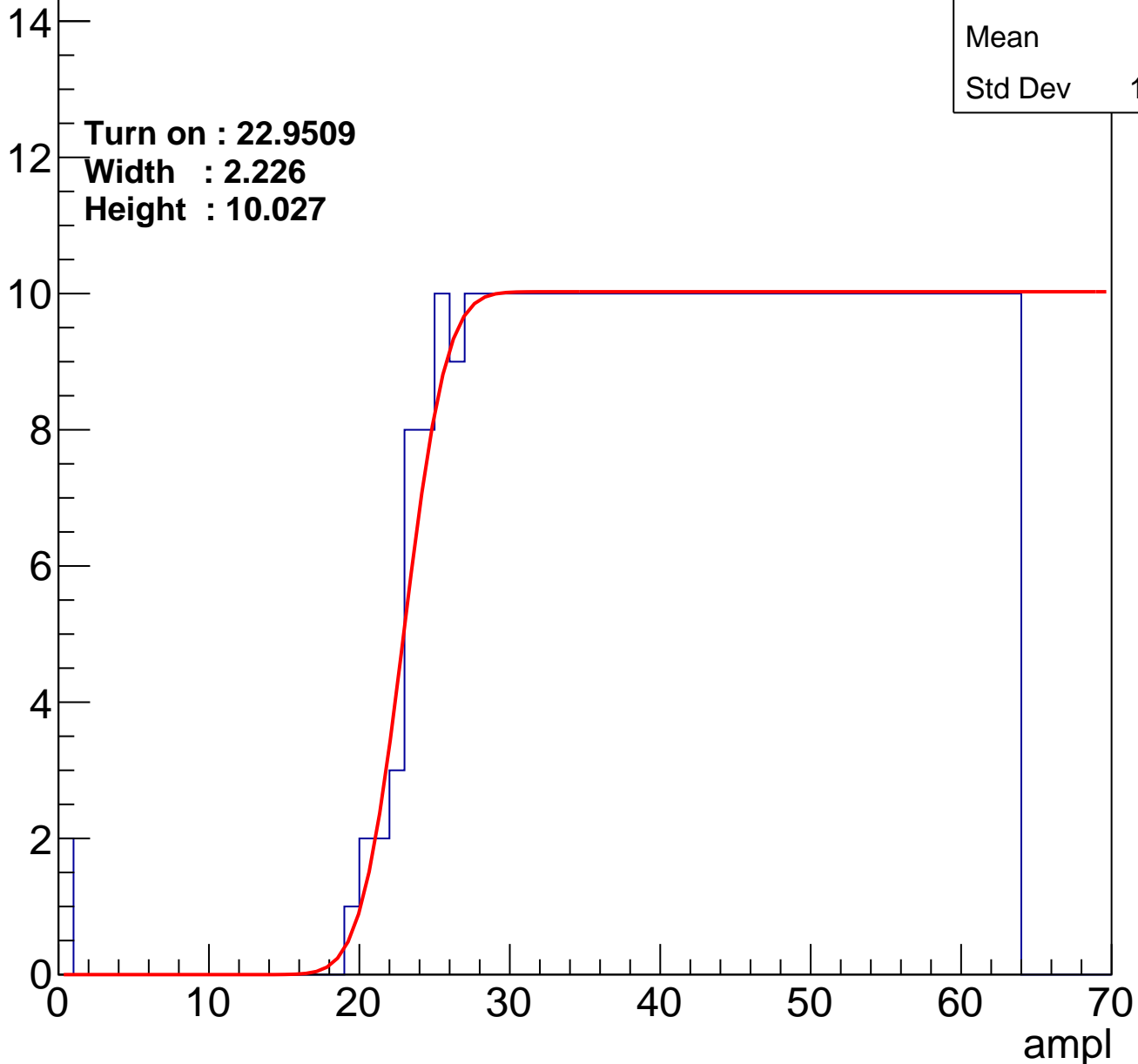
Entries	415
Mean	42.6
Std Dev	12.34

Turn on : 22.9509

Width : 2.226

Height : 10.027

Entry



B1L102S, U15-ch13

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	45.01
Std Dev	11.04

Turn on : 28.0247

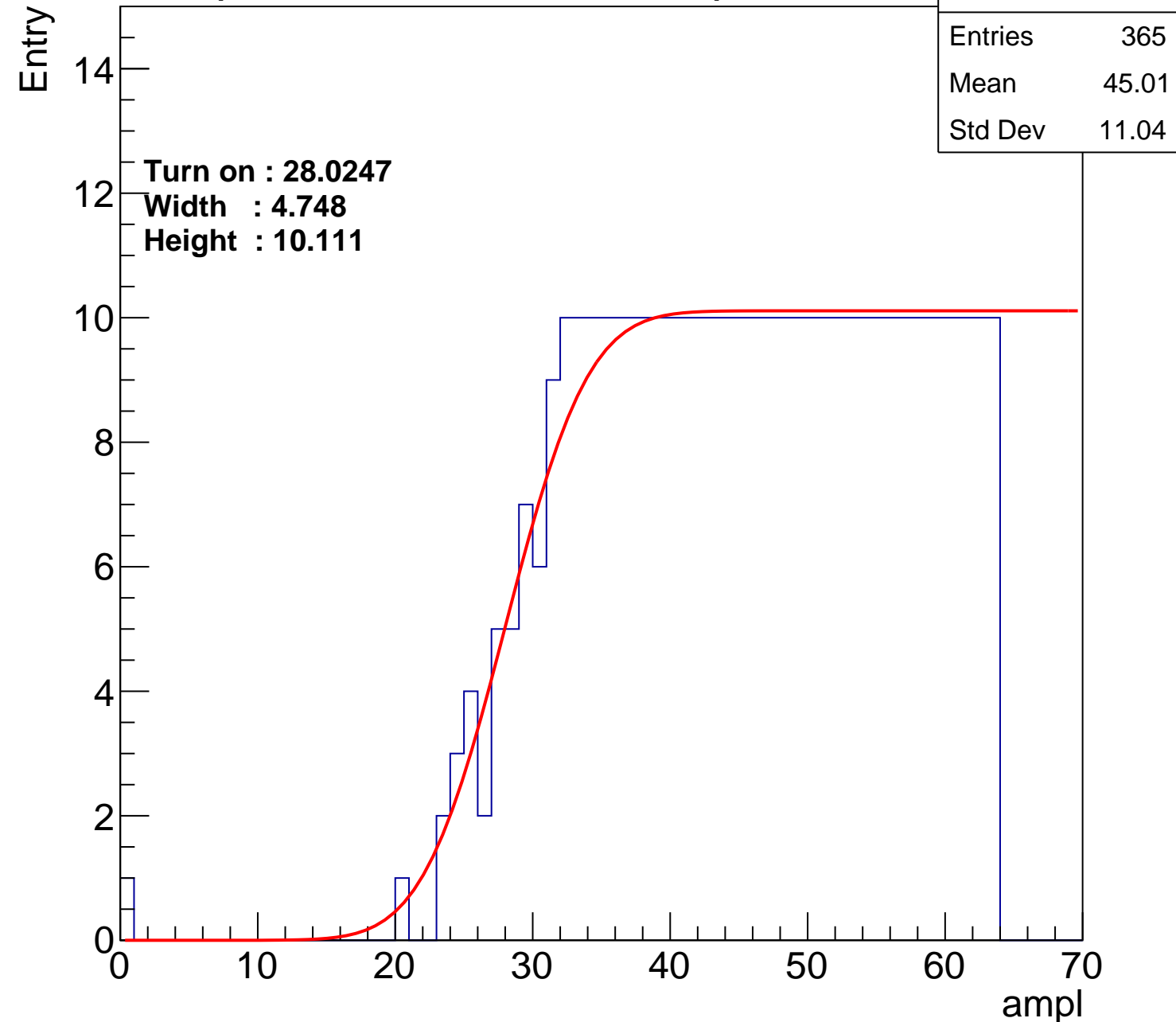
Width : 4.748

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch14

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.07
Std Dev	11.75

Turn on : 26.2366

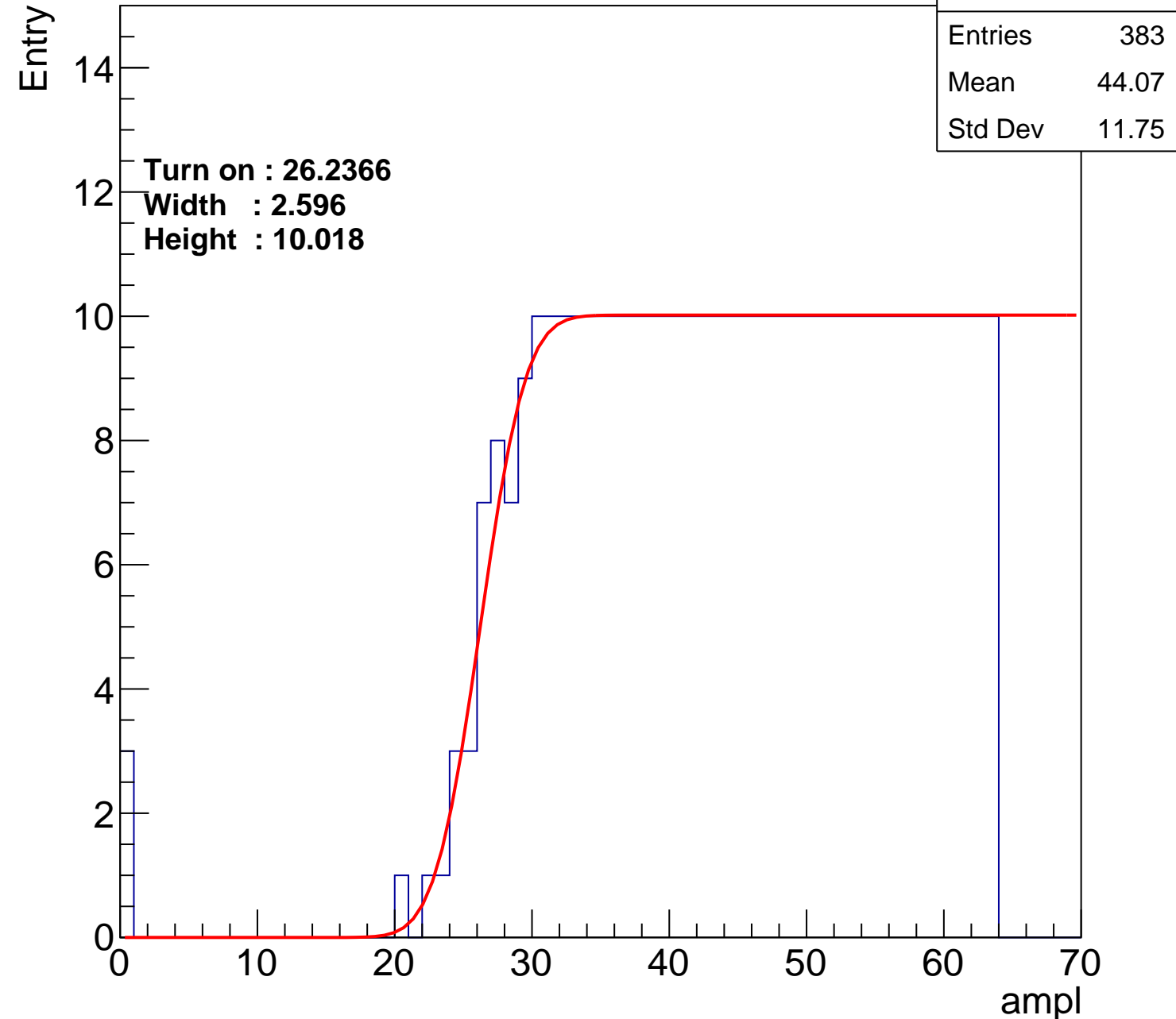
Width : 2.596

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch15

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.81
Std Dev	12.05

Turn on : 26.2538

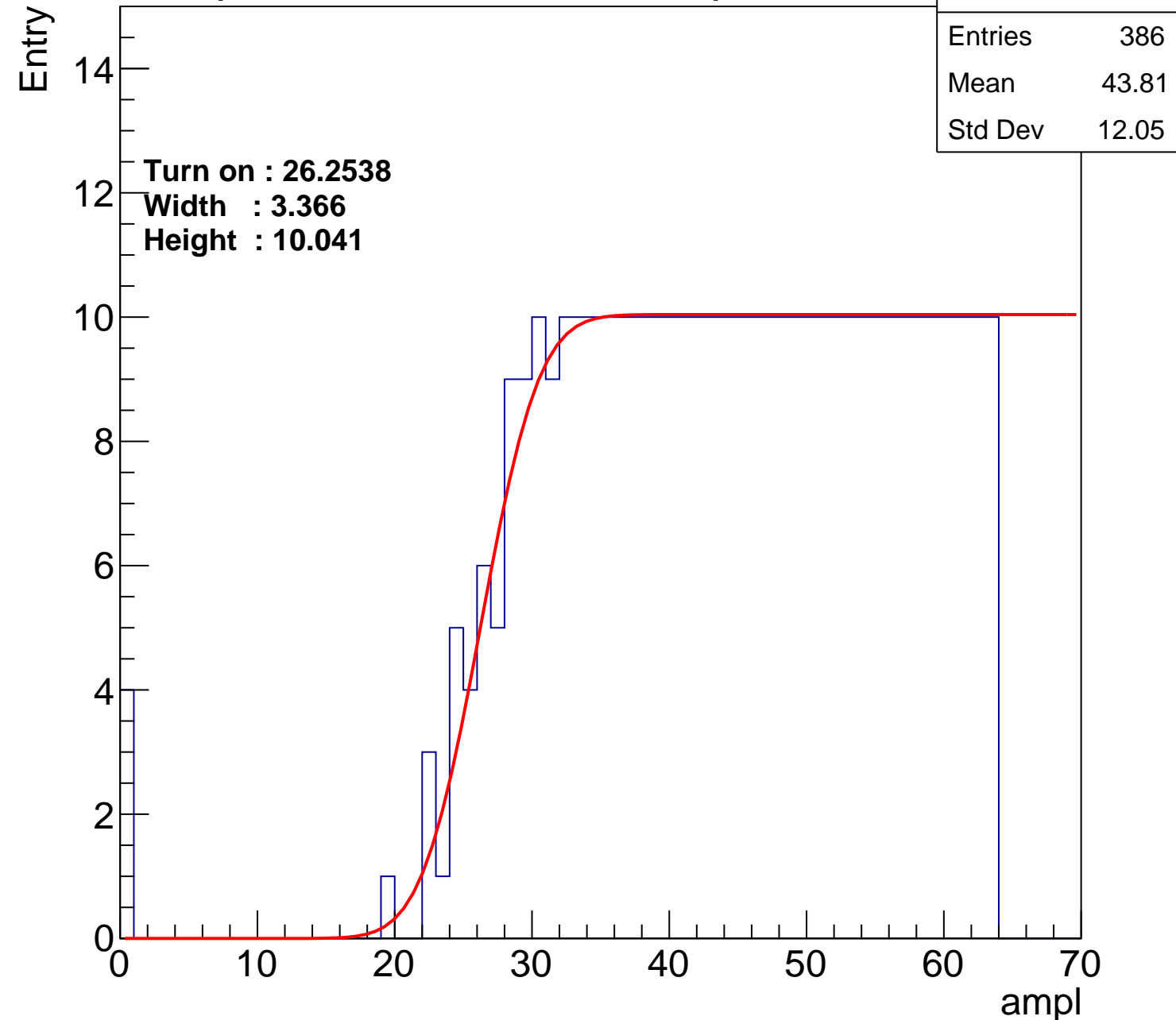
Width : 3.366

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch16

calib_packv5_042523_0143.root, FC#11, port A2

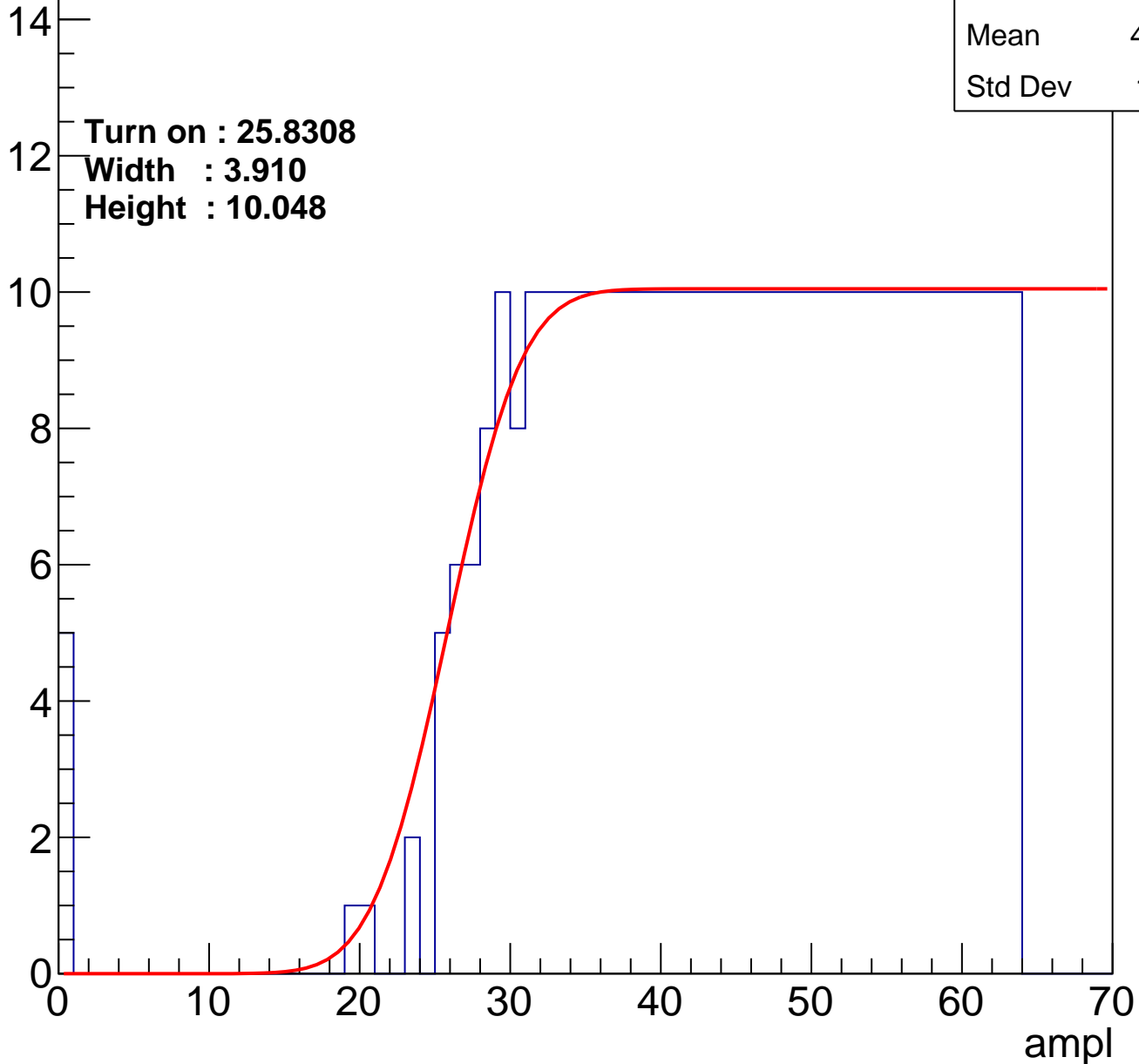
Entries	382
Mean	43.96
Std Dev	12.11

Turn on : 25.8308

Width : 3.910

Height : 10.048

Entry



B1L102S, U15-ch17

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.59
Std Dev	11.18

Turn on : 26.9890

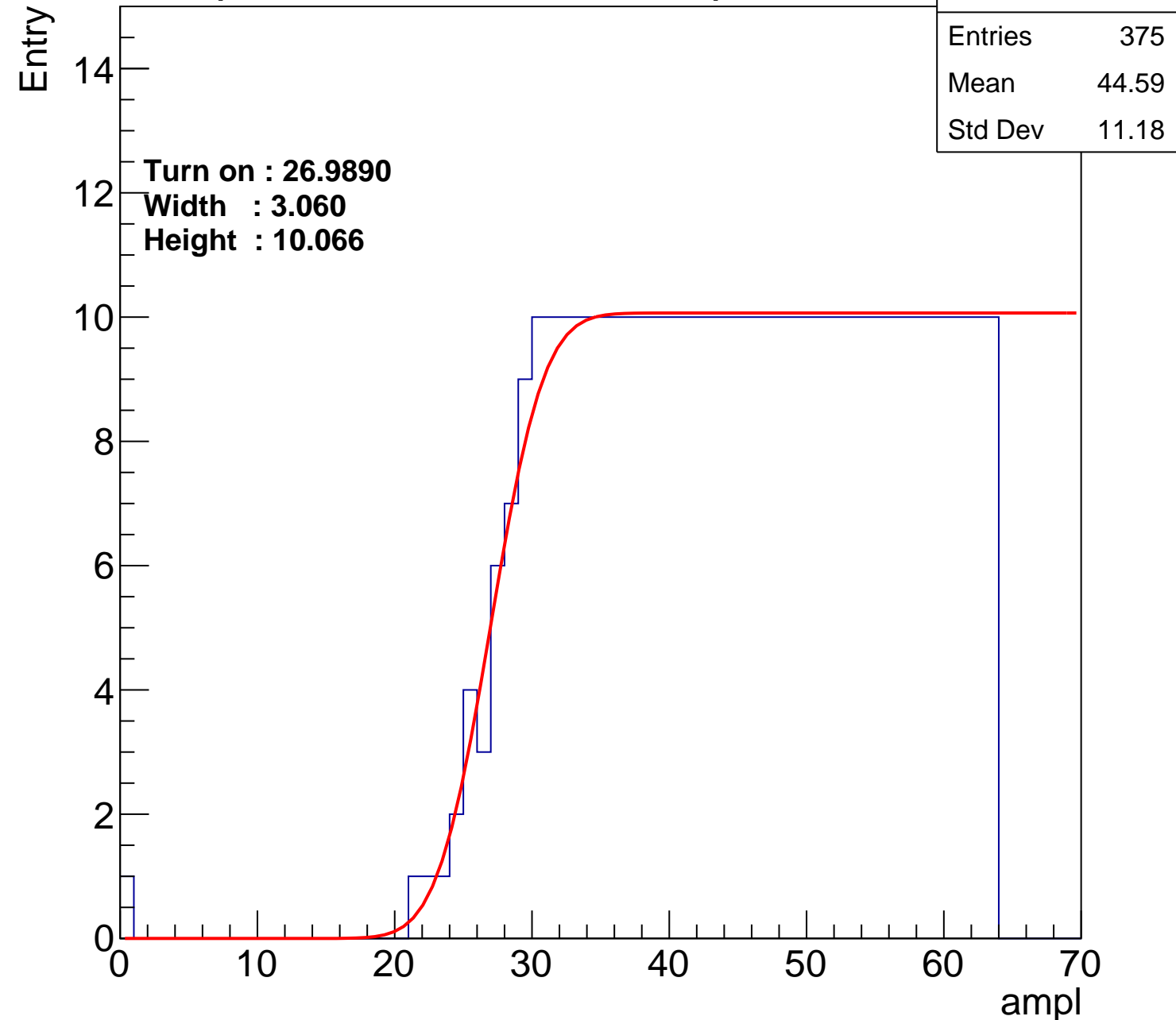
Width : 3.060

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch18

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.14
Std Dev	11.6

Turn on : 26.5037

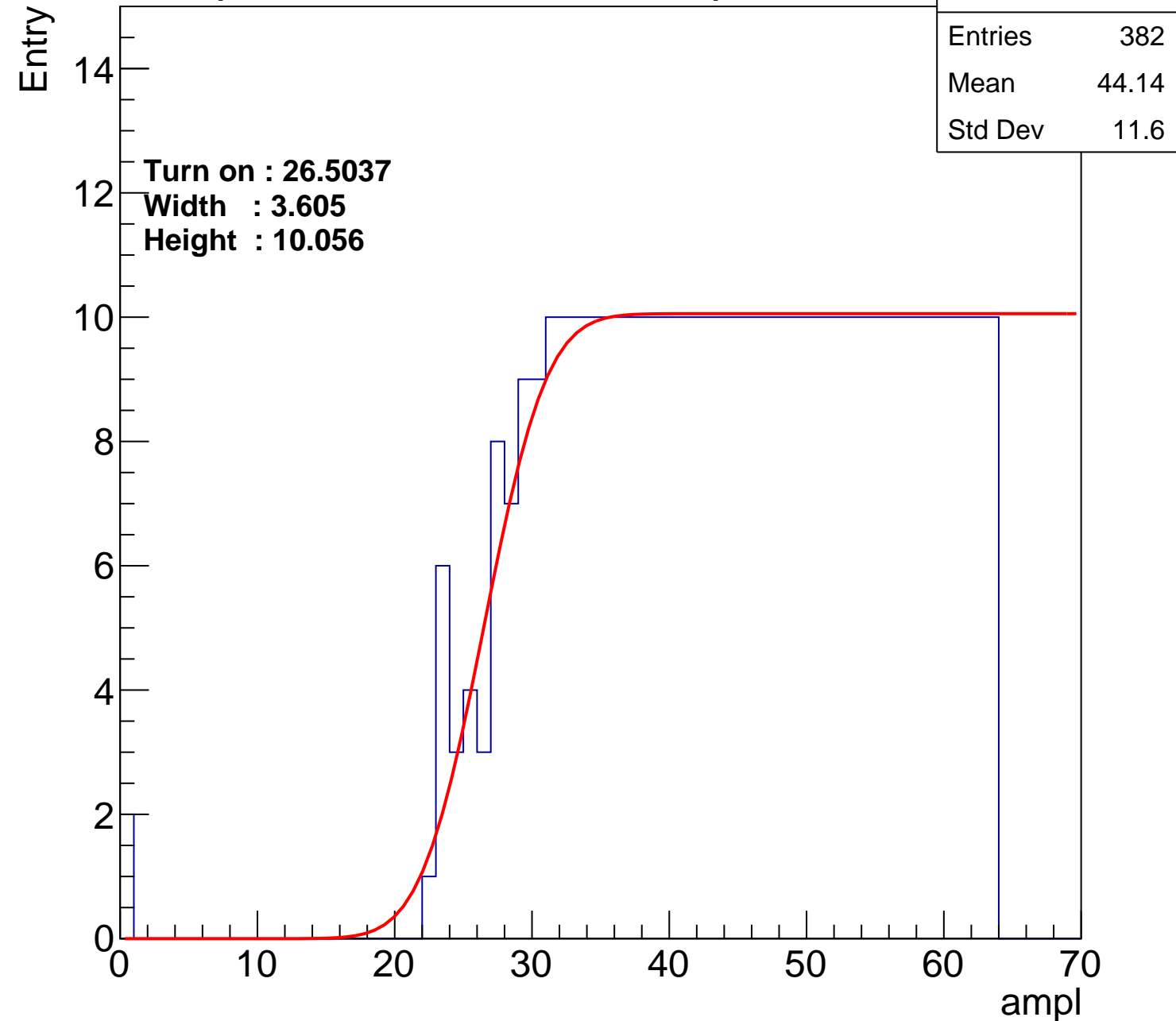
Width : 3.605

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch19

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 25.1301

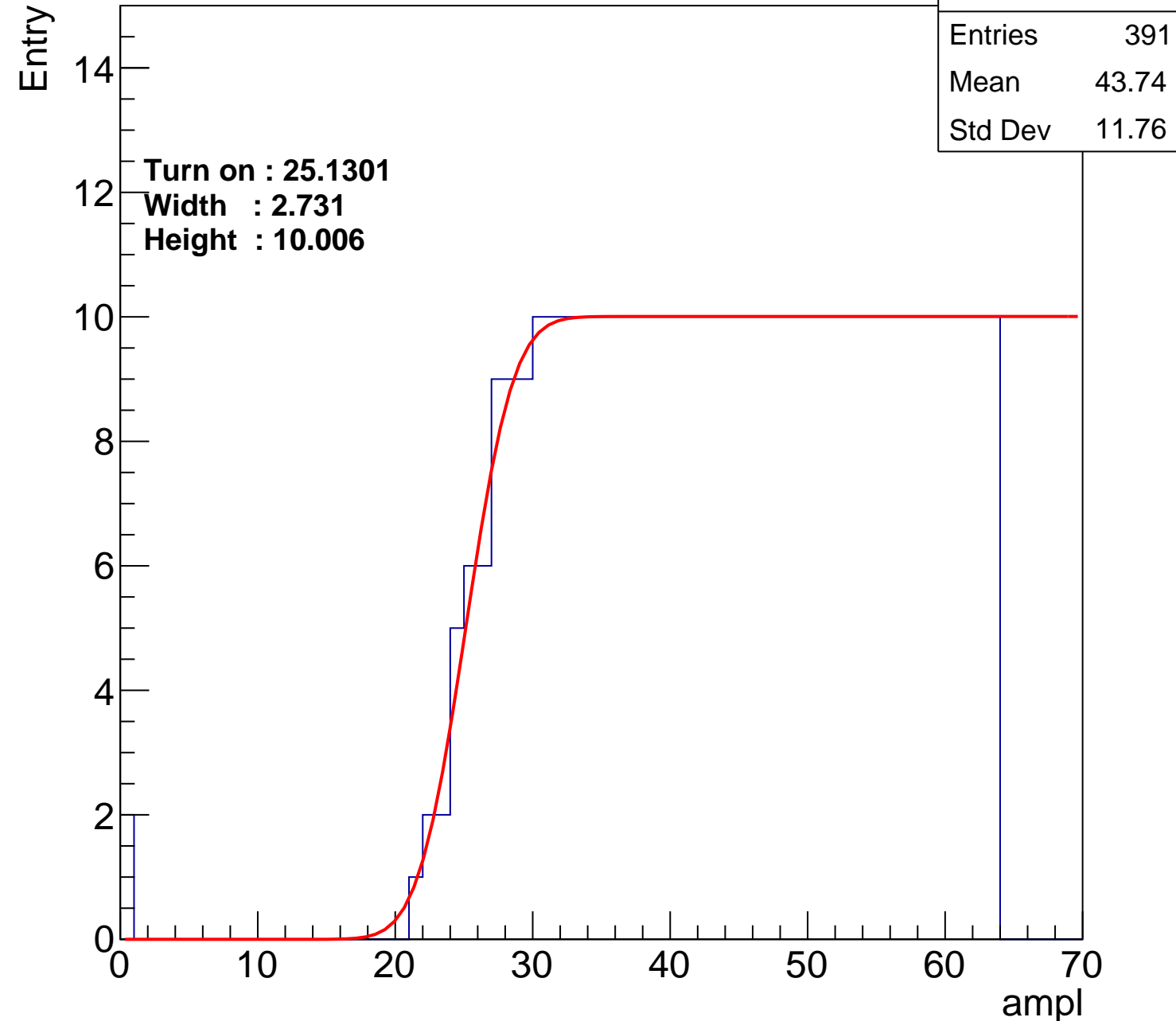
Width : 2.731

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch20

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.5
Std Dev	12.11

Turn on : 24.9274

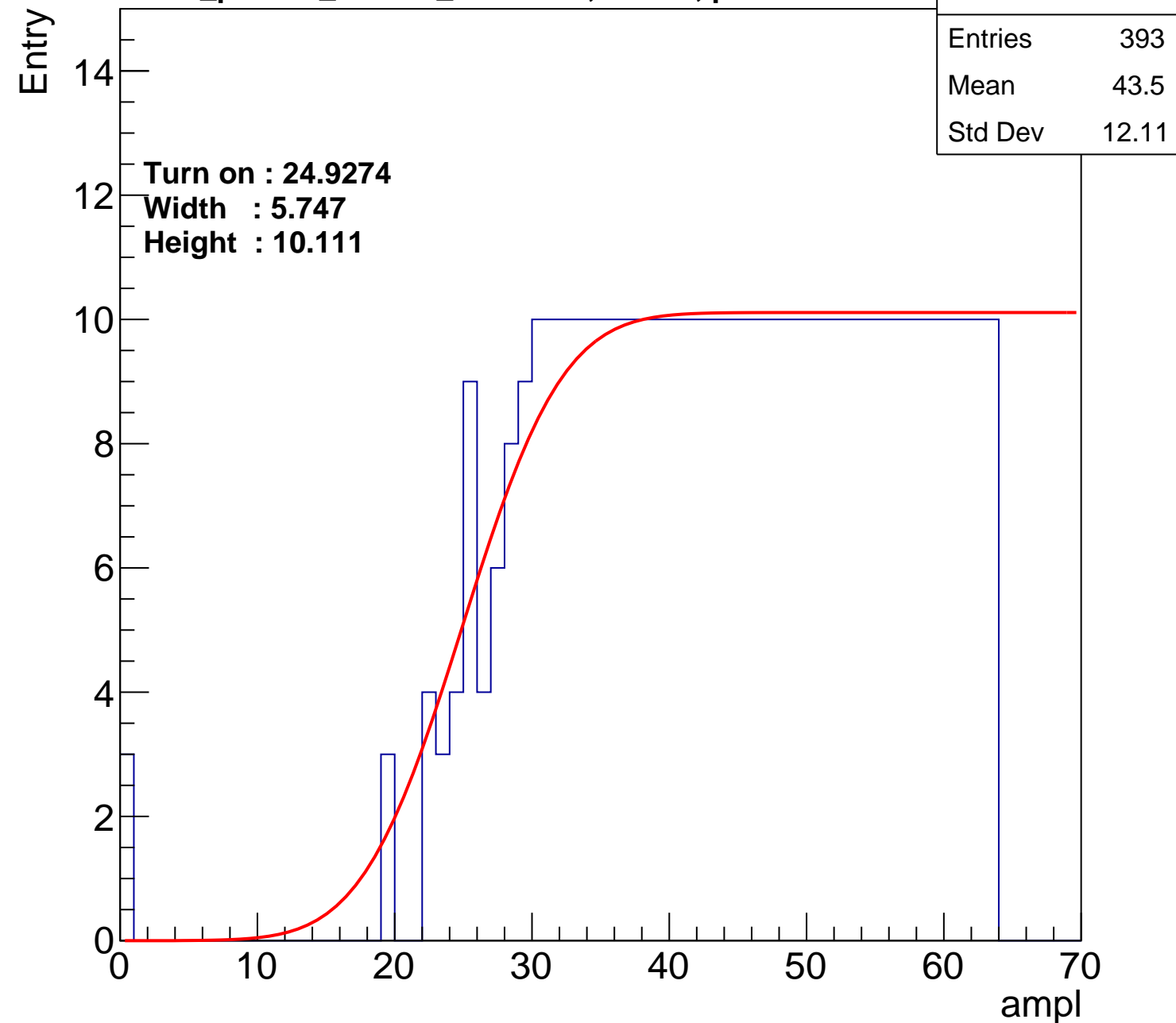
Width : 5.747

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch21

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	43.08
Std Dev	12.09

Turn on : 23.8078

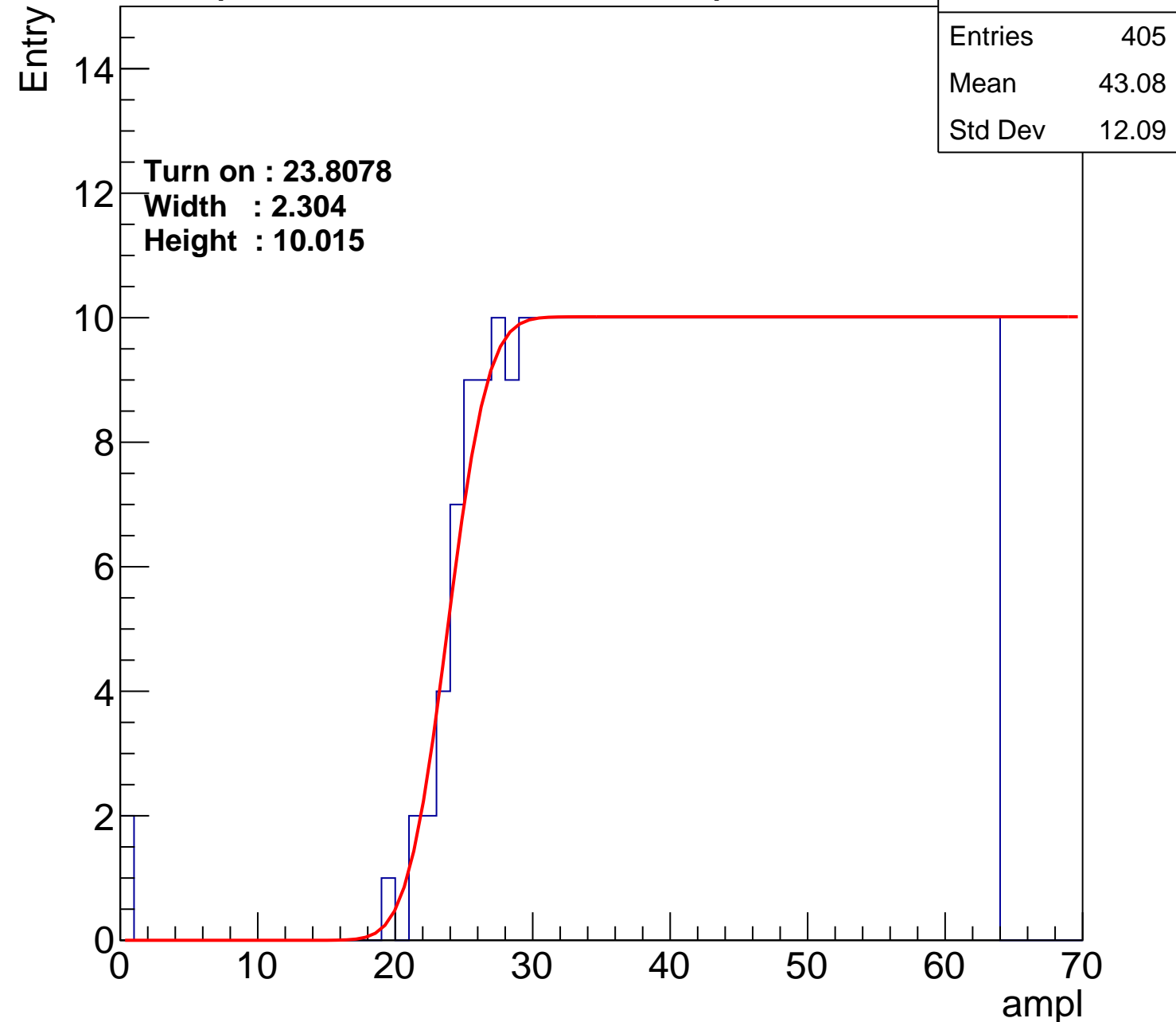
Width : 2.304

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch22

calib_packv5_042523_0143.root, FC#11, port A2

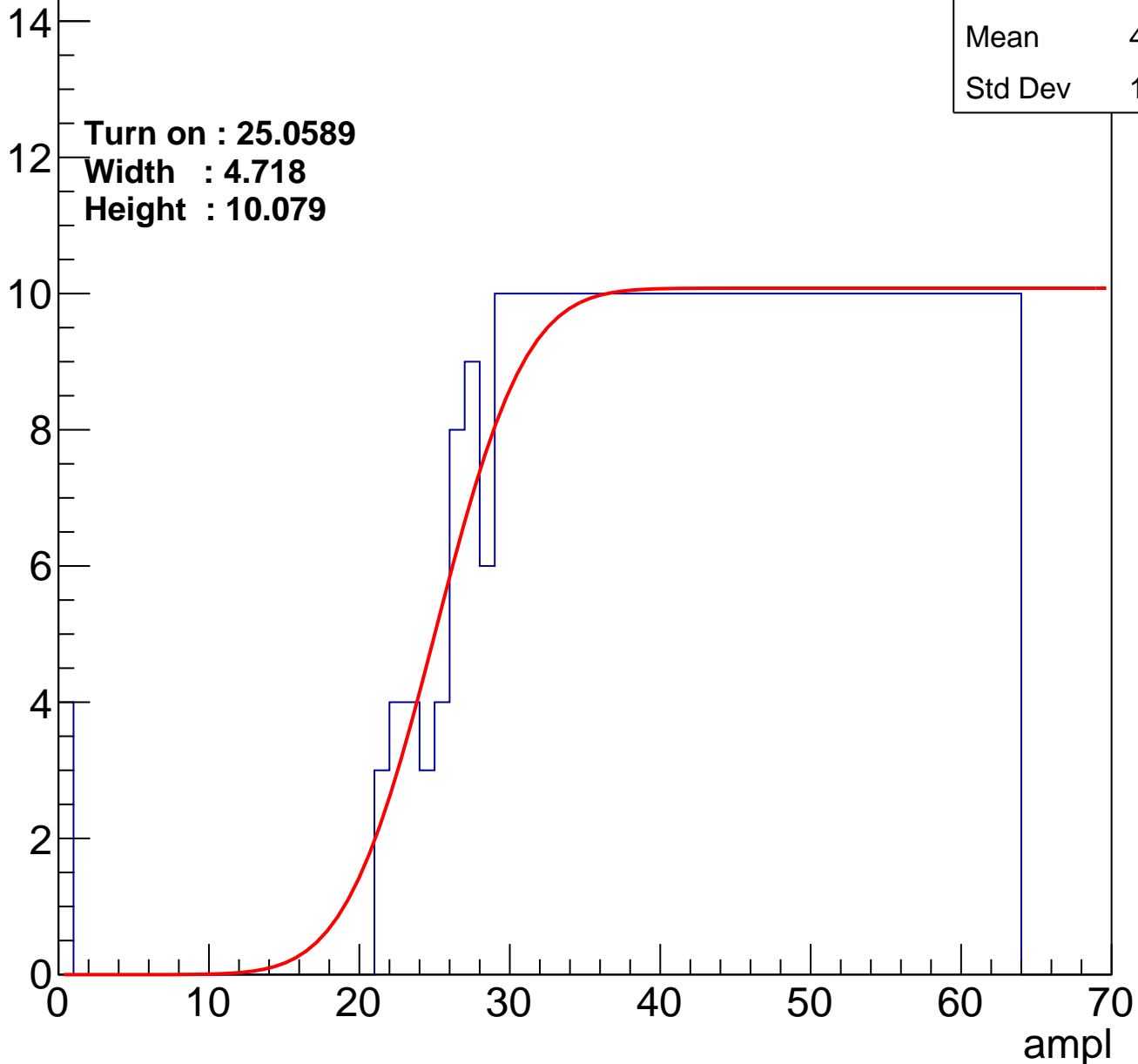
Entries	395
Mean	43.38
Std Dev	12.26

Turn on : 25.0589

Width : 4.718

Height : 10.079

Entry



B1L102S, U15-ch23

calib_packv5_042523_0143.root, FC#11, port A2

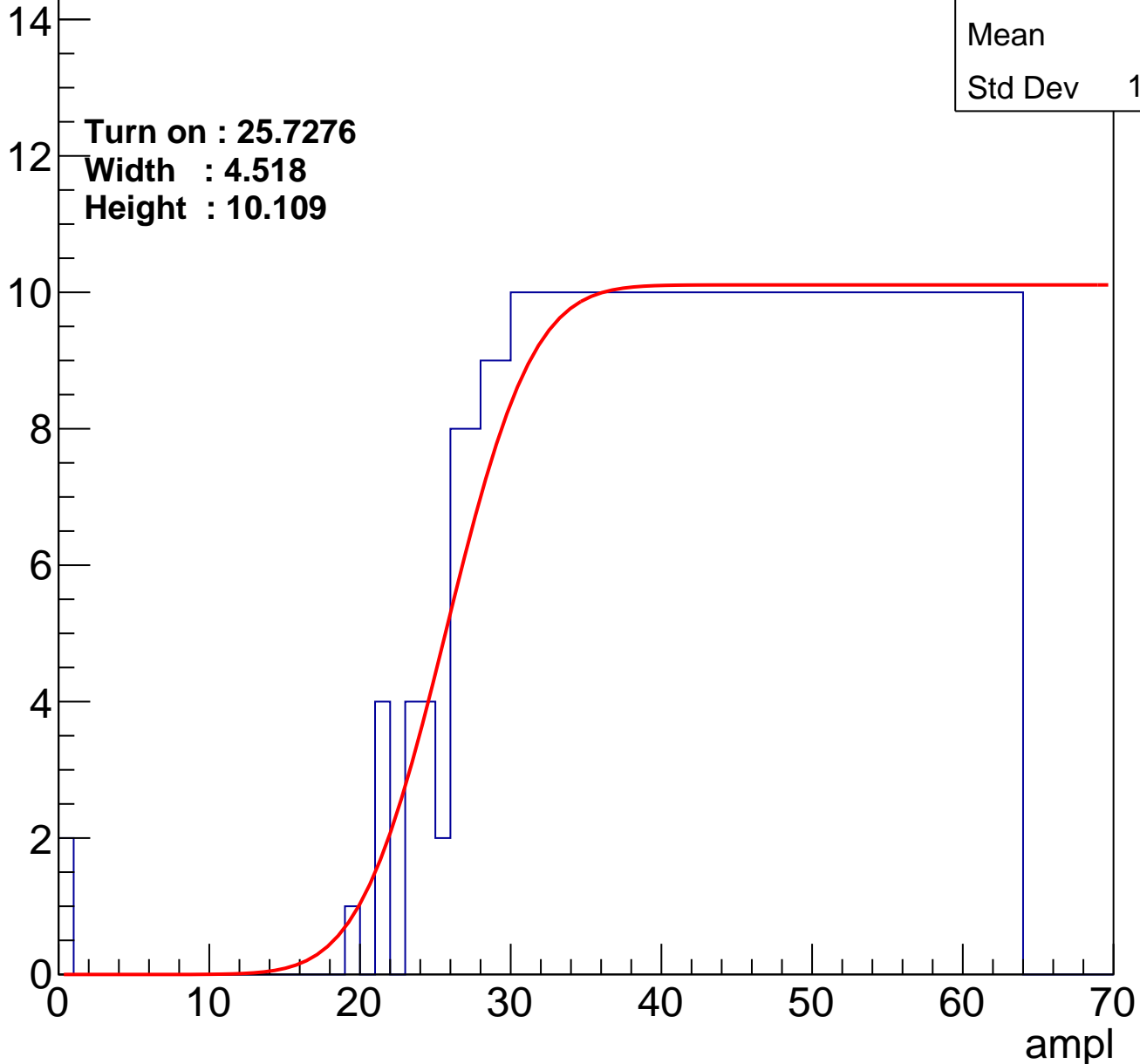
Entries	391
Mean	43.7
Std Dev	11.83

Turn on : 25.7276

Width : 4.518

Height : 10.109

Entry



B1L102S, U15-ch24

calib_packv5_042523_0143.root, FC#11, port A2

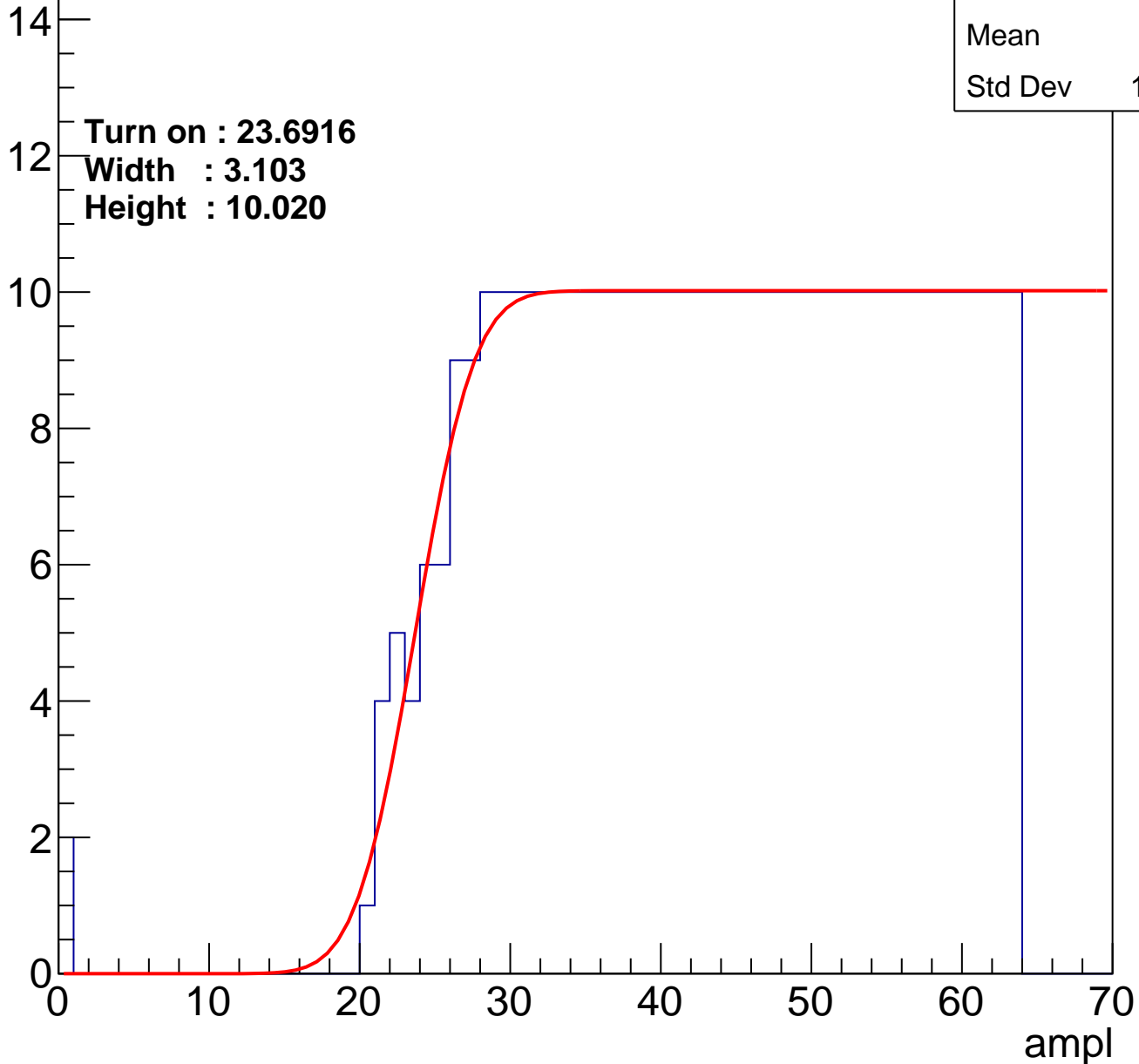
Entries	406
Mean	43
Std Dev	12.17

Turn on : 23.6916

Width : 3.103

Height : 10.020

Entry



B1L102S, U15-ch25

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.62
Std Dev	11.97

Turn on : 25.2622

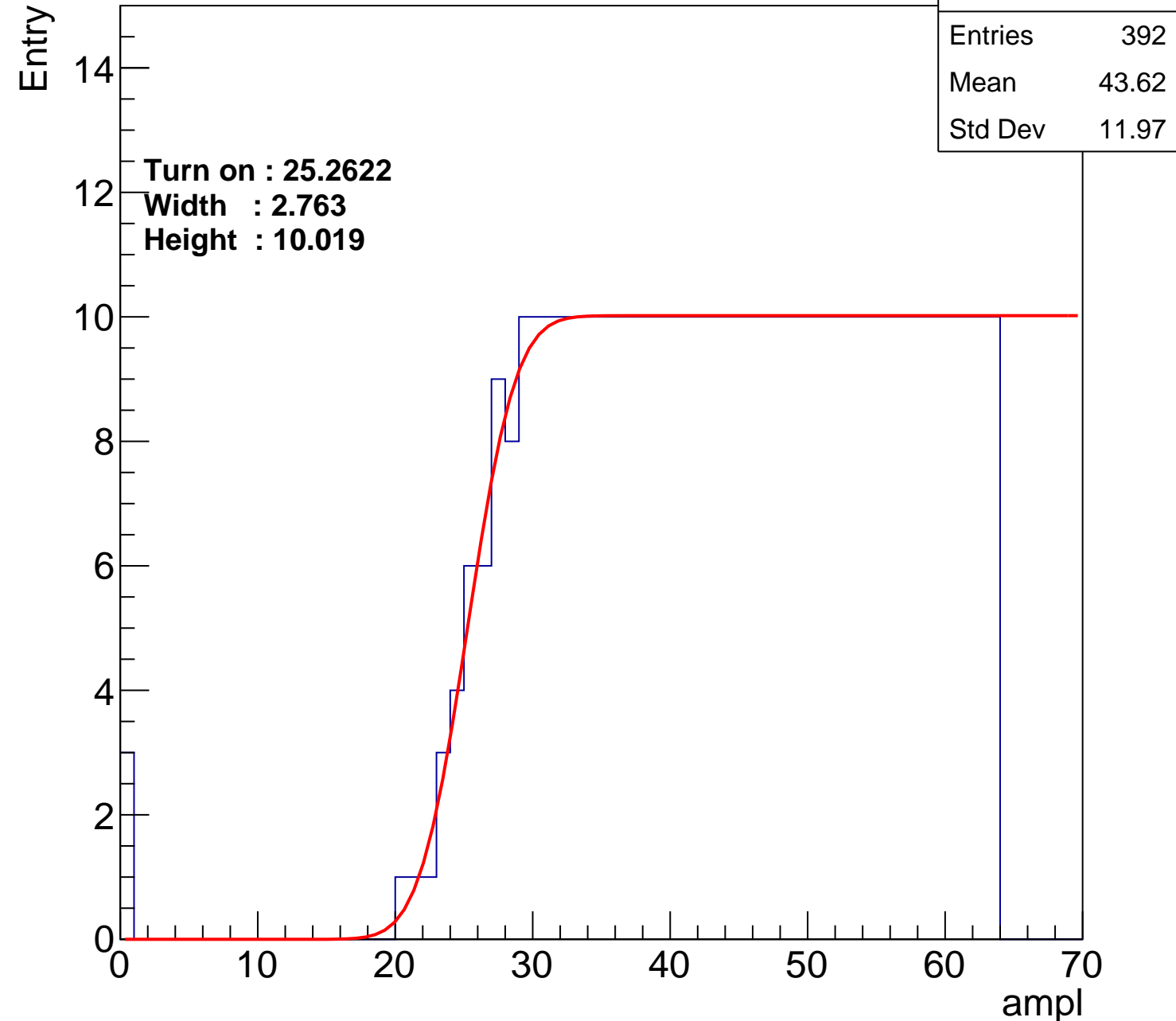
Width : 2.763

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch26

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.5983

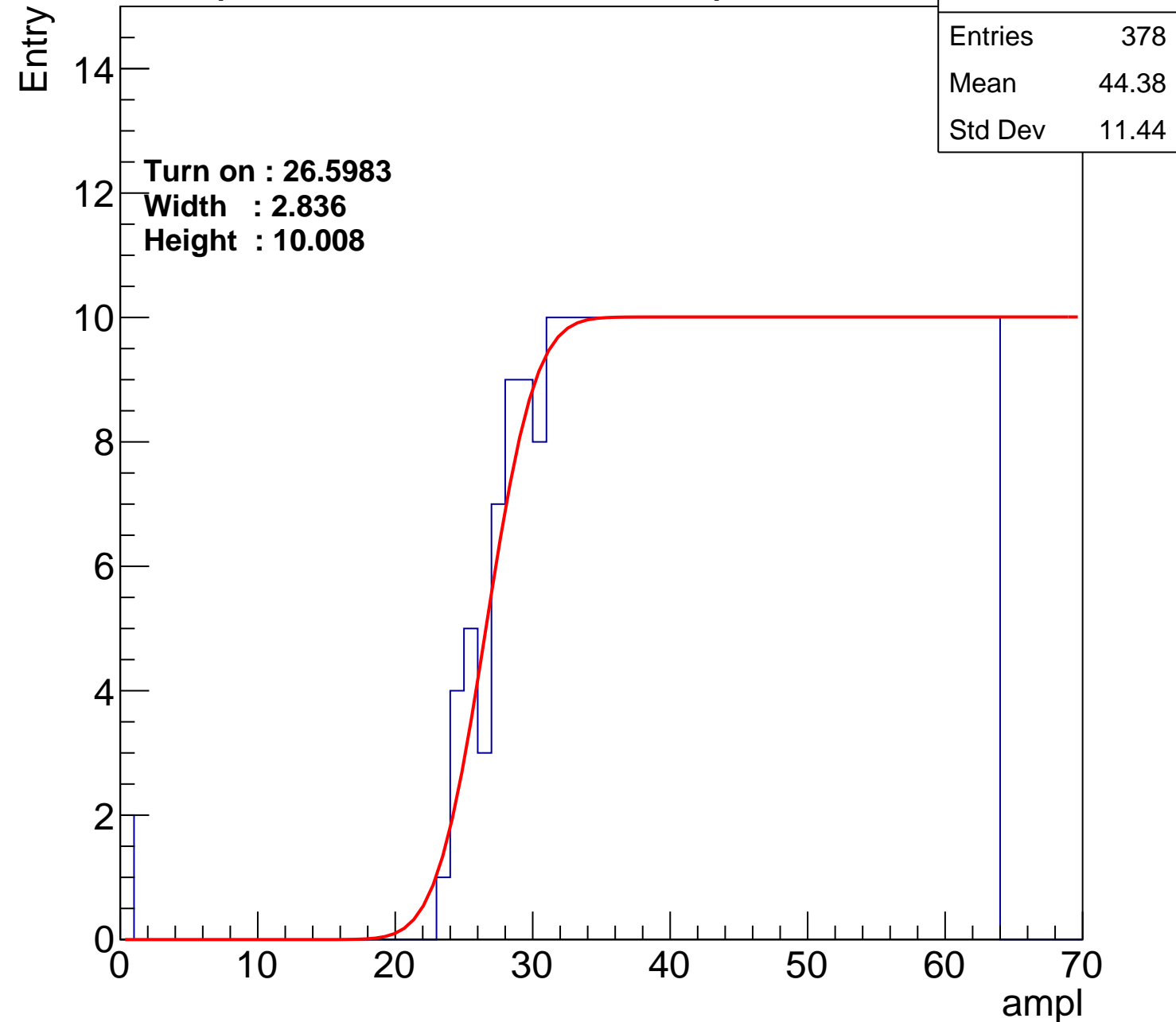
Width : 2.836

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch27

calib_packv5_042523_0143.root, FC#11, port A2

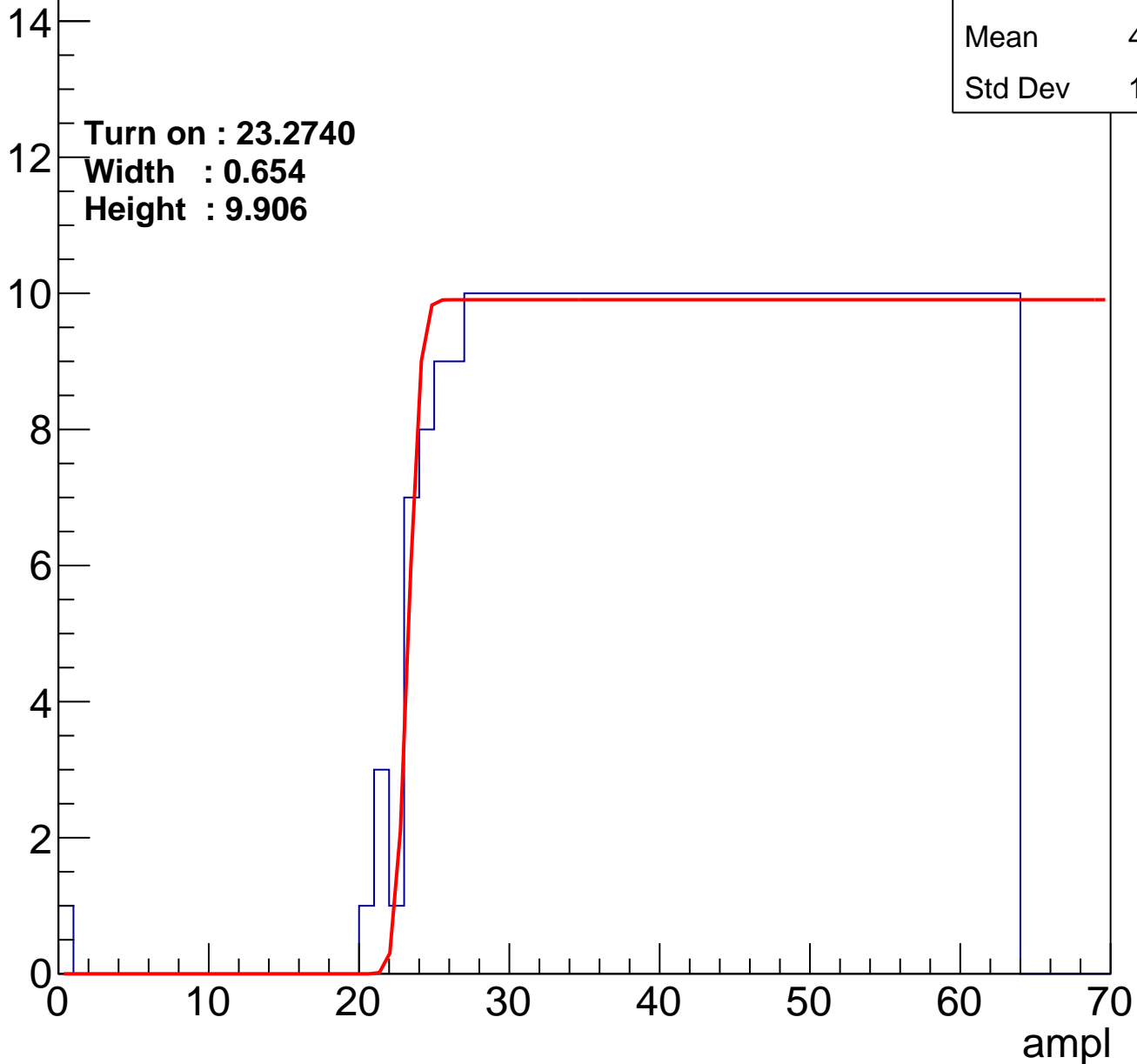
Entries	409
Mean	42.95
Std Dev	12.02

Turn on : 23.2740

Width : 0.654

Height : 9.906

Entry



B1L102S, U15-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	42.8
Std Dev	12.75

Turn on : 24.7960

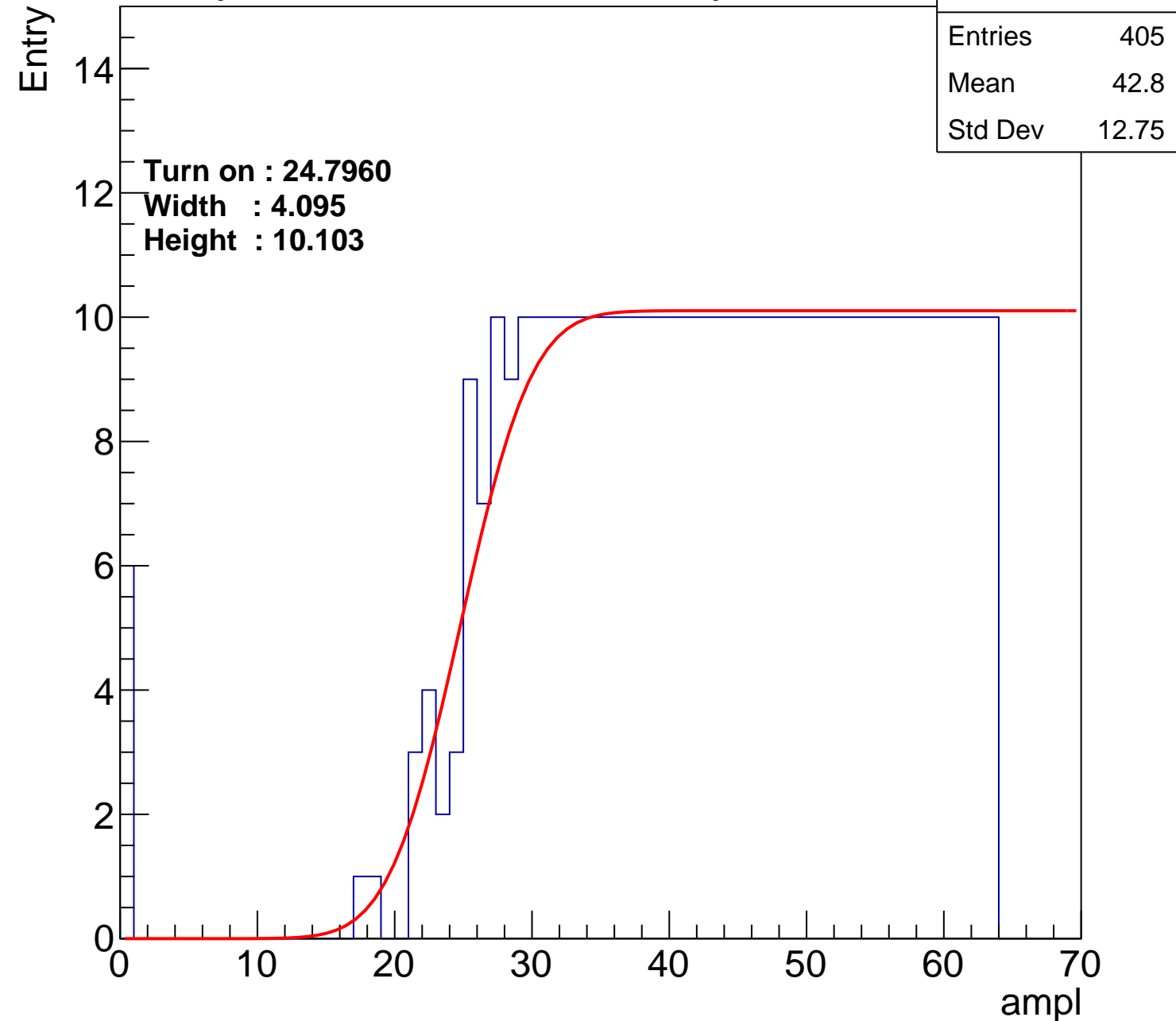
Width : 4.095

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch29

calib_packv5_042523_0143.root, FC#11, port A2

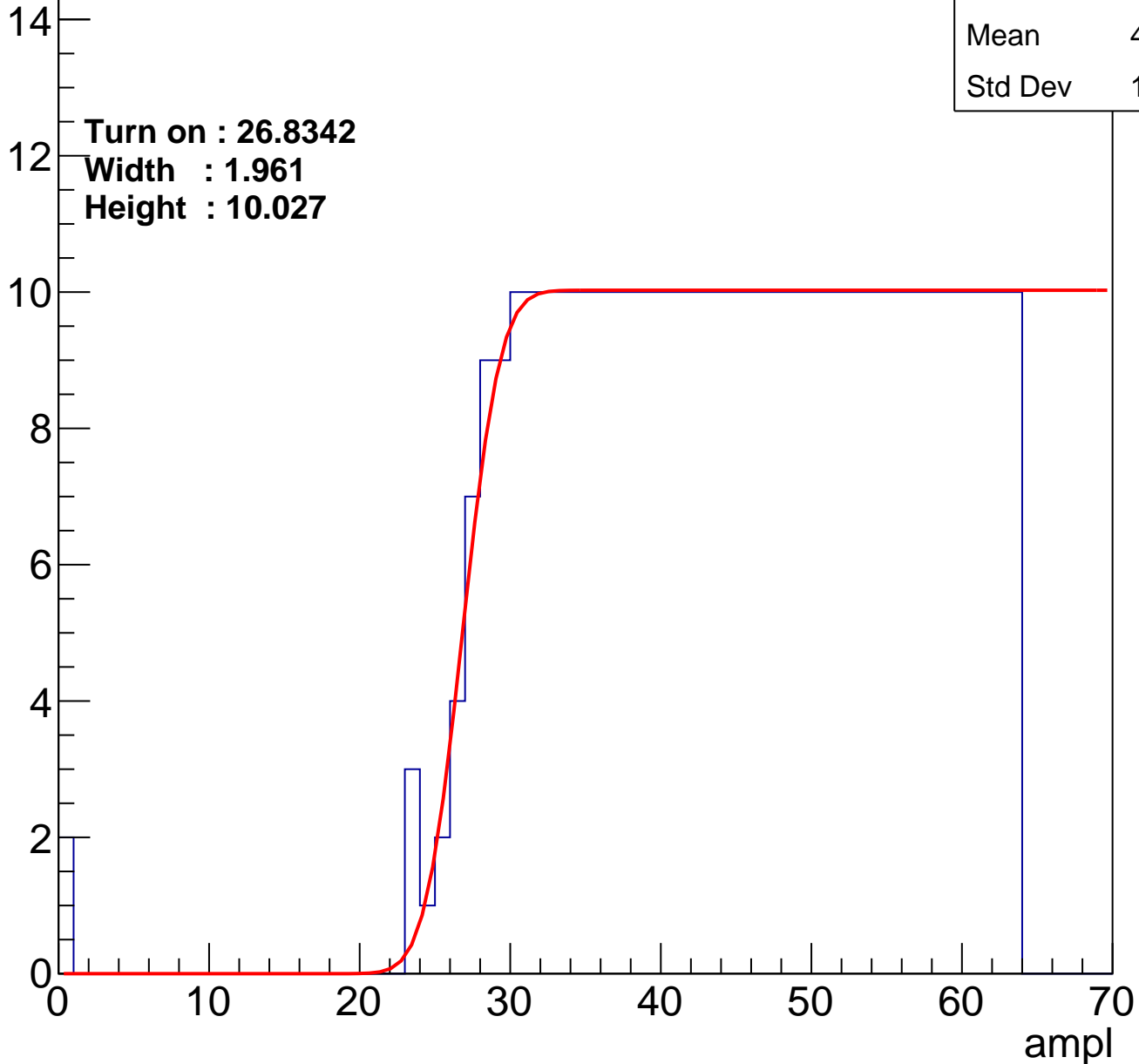
Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 26.8342

Width : 1.961

Height : 10.027

Entry



B1L102S, U15-ch30

calib_packv5_042523_0143.root, FC#11, port A2

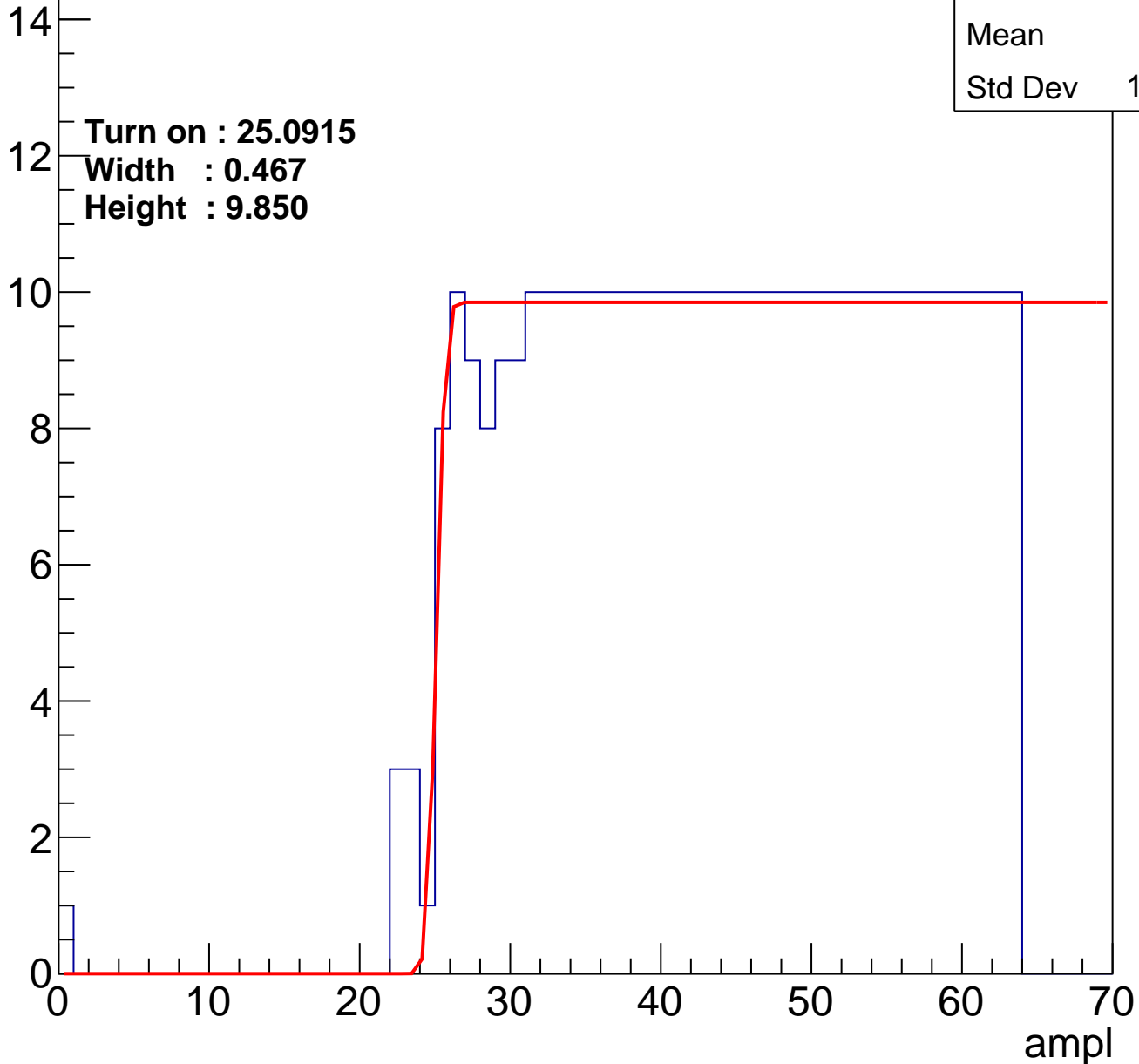
Entries	391
Mean	43.8
Std Dev	11.59

Turn on : 25.0915

Width : 0.467

Height : 9.850

Entry



B1L102S, U15-ch31

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.8
Std Dev	11.77

Turn on : 25.5277

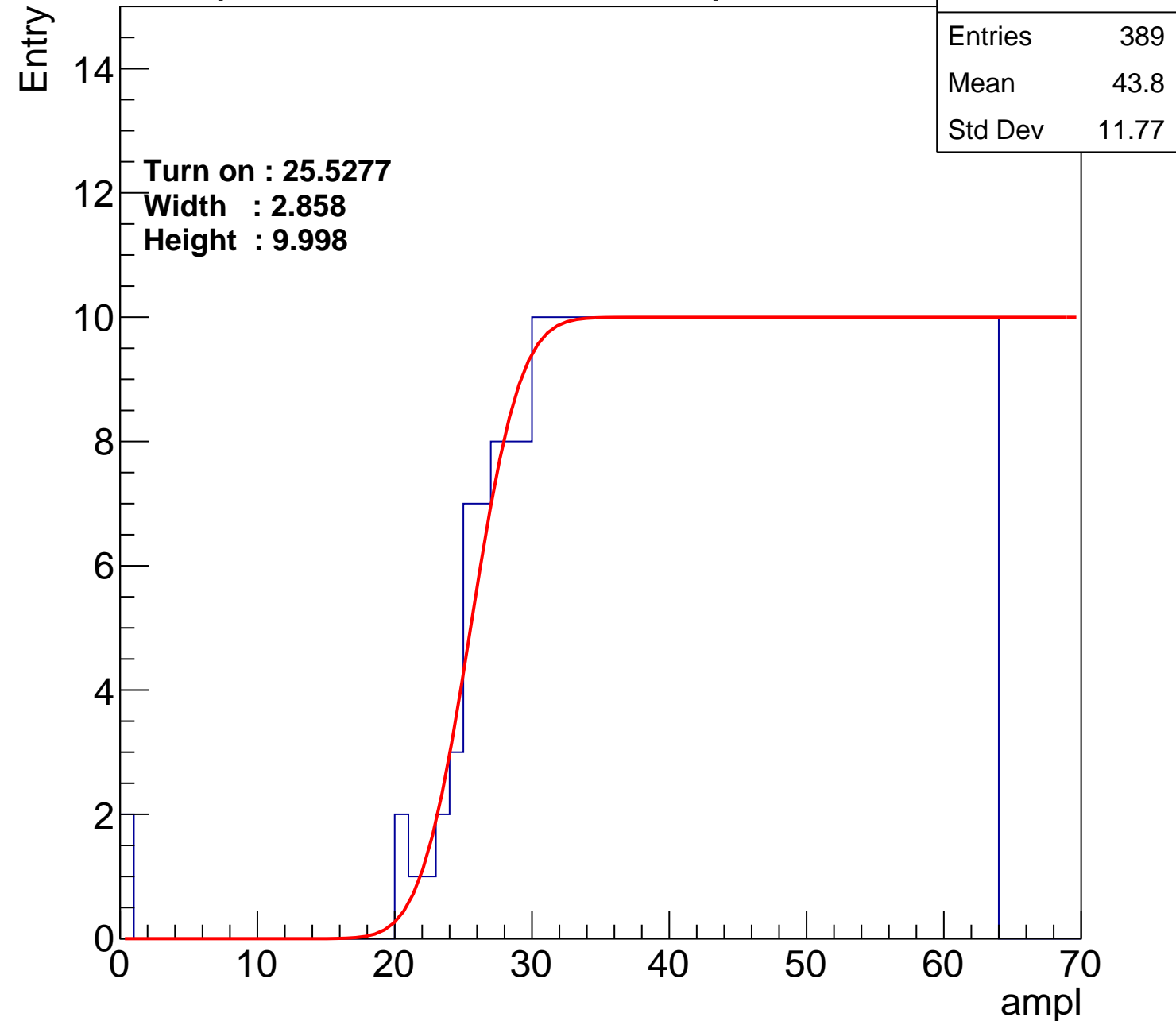
Width : 2.858

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.89
Std Dev	11.86

Turn on : 26.1976

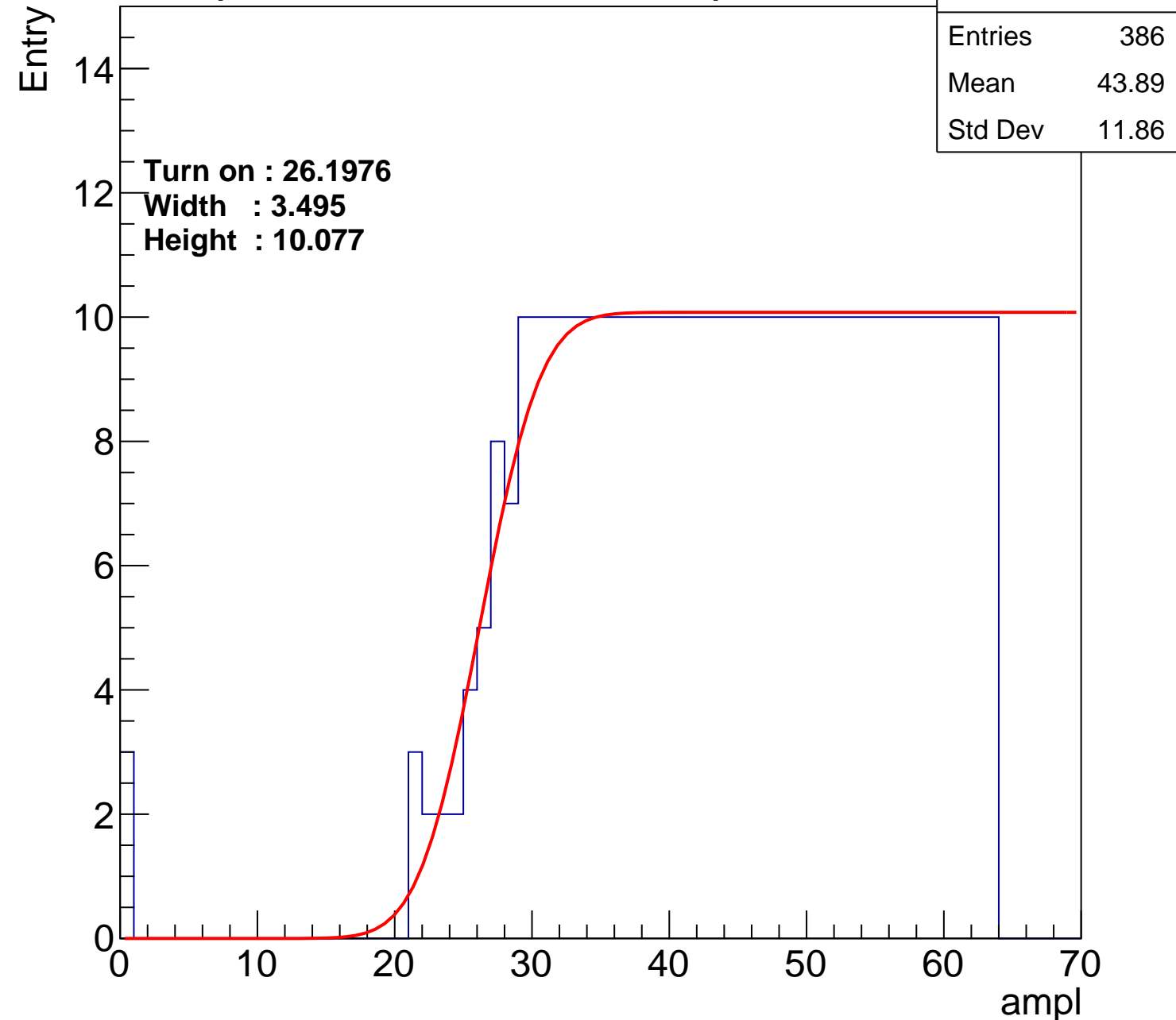
Width : 3.495

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch33

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	43.11
Std Dev	12.37

Turn on : 24.8750

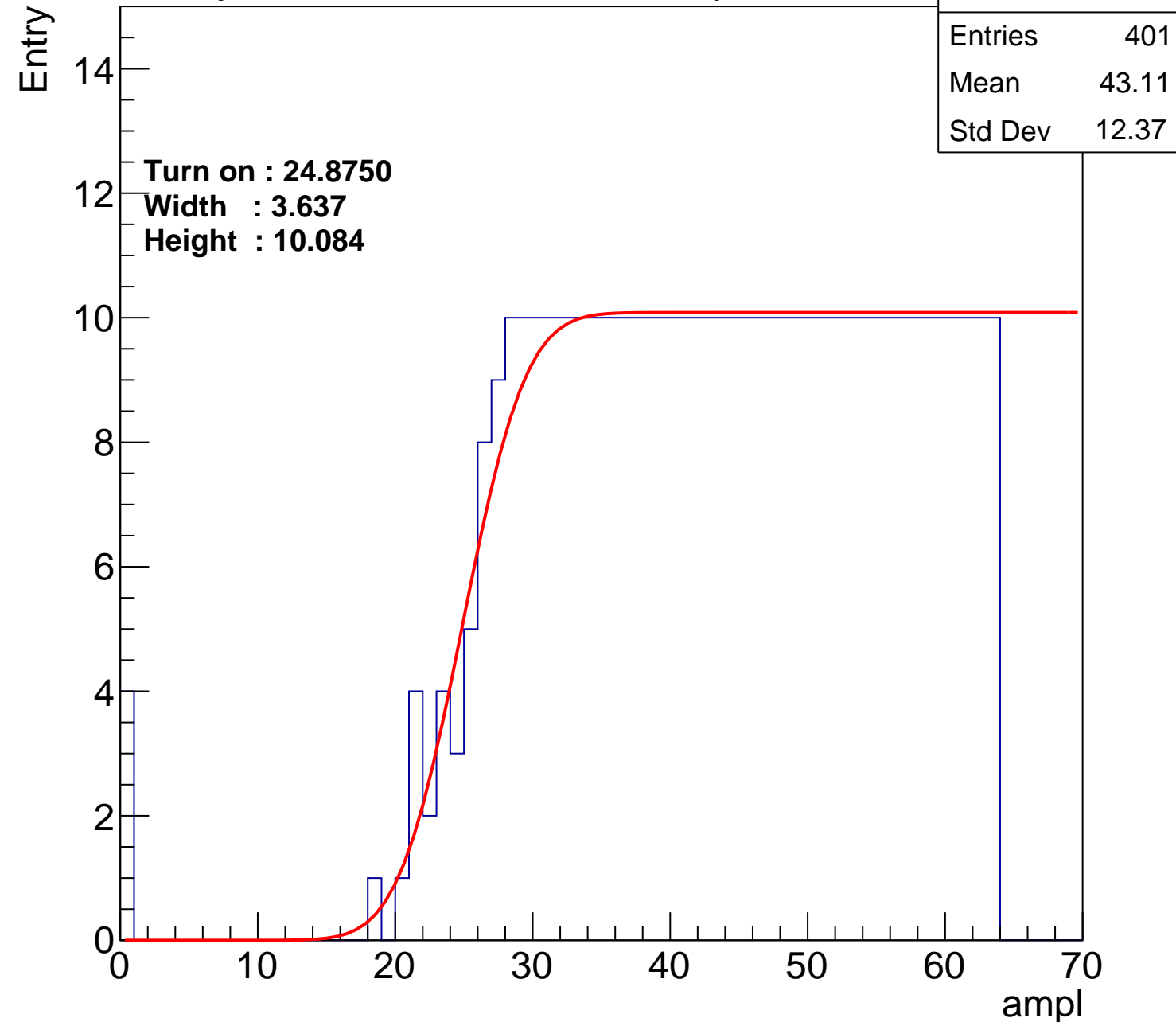
Width : 3.637

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch34

calib_packv5_042523_0143.root, FC#11, port A2

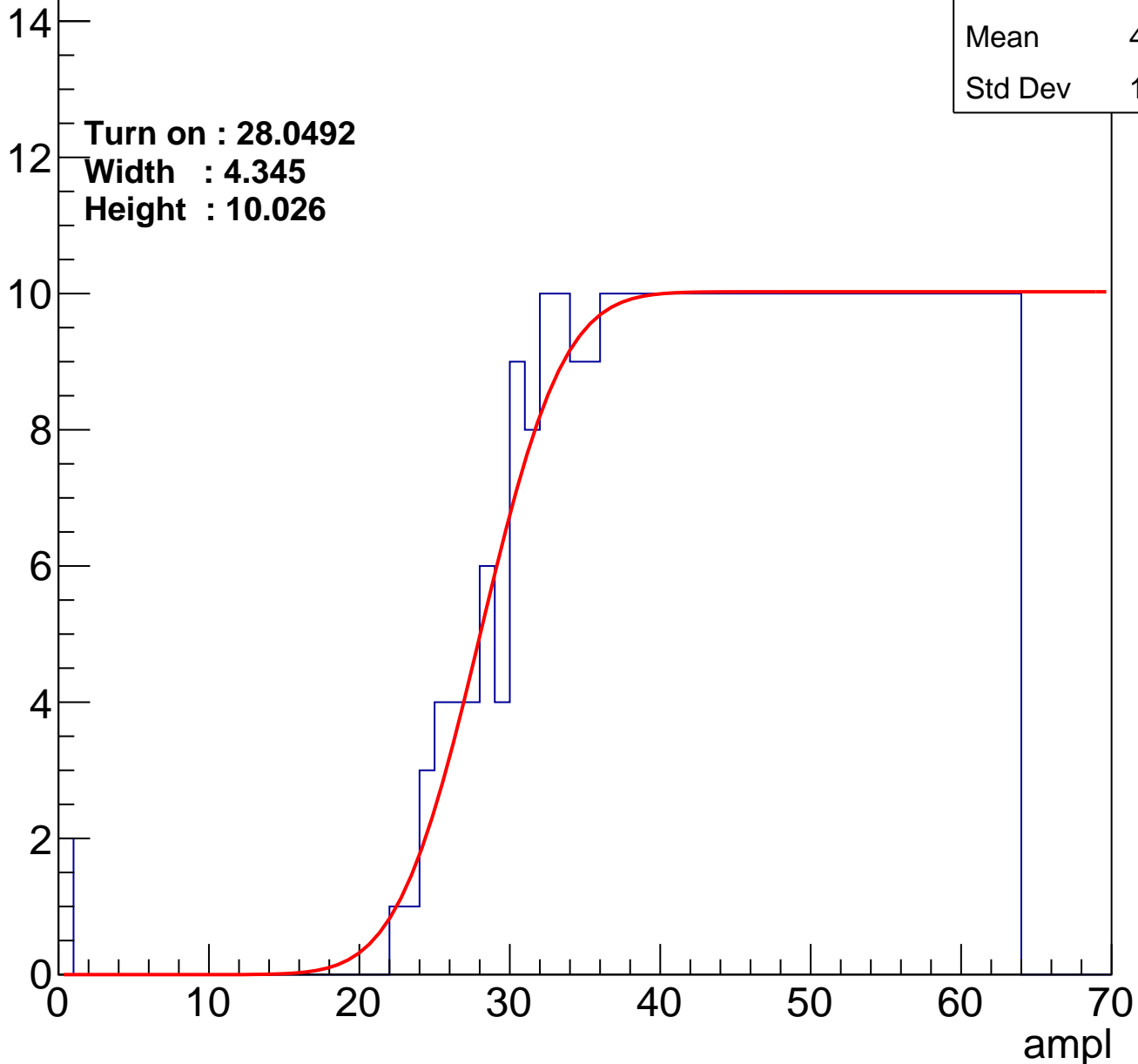
Entries	364
Mean	44.95
Std Dev	11.26

Turn on : 28.0492

Width : 4.345

Height : 10.026

Entry



B1L102S, U15-ch35

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.3
Std Dev	12.16

Turn on : 24.8615

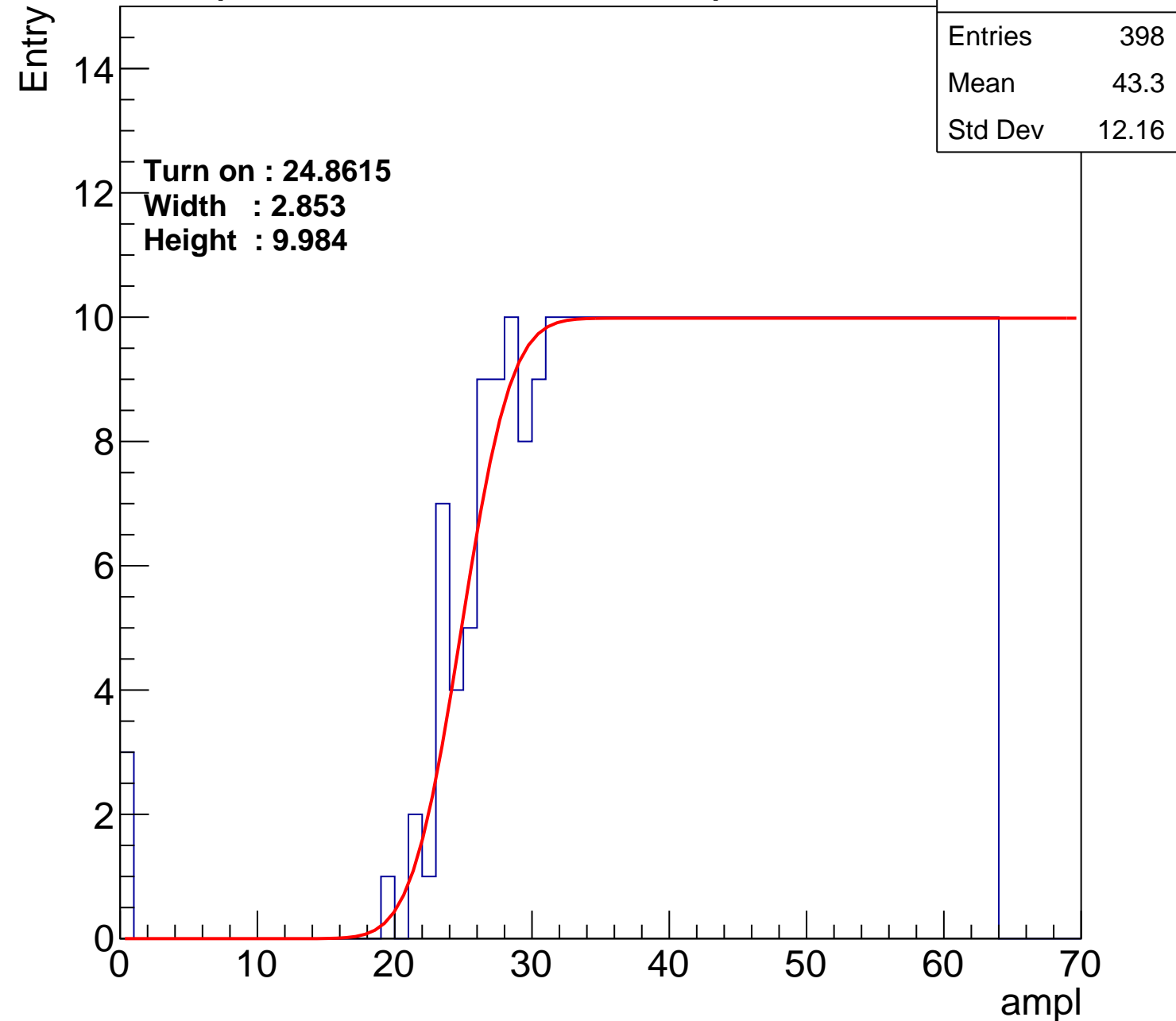
Width : 2.853

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch36

calib_packv5_042523_0143.root, FC#11, port A2

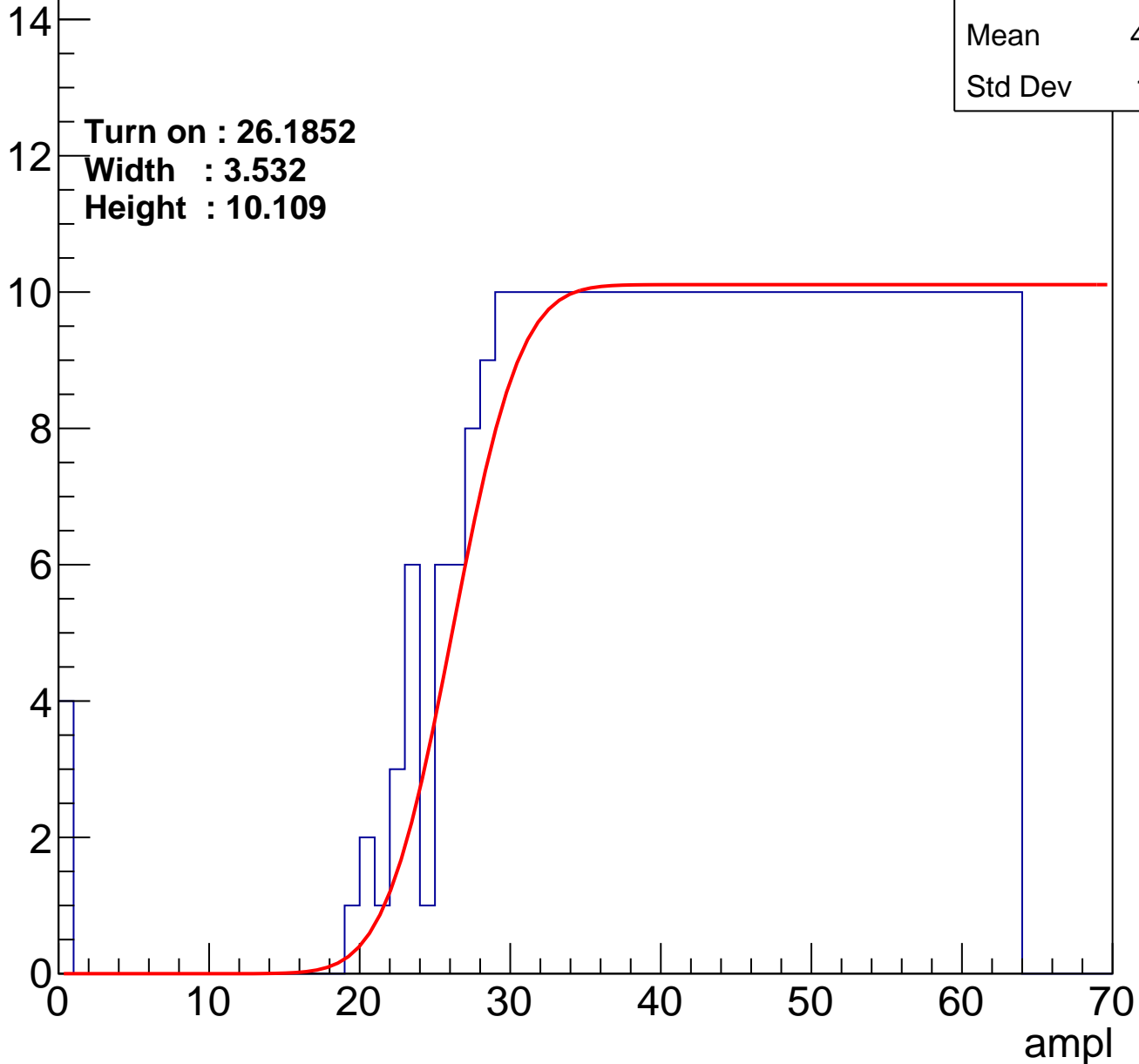
Entries	397
Mean	43.28
Std Dev	12.31

Turn on : 26.1852

Width : 3.532

Height : 10.109

Entry



B1L102S, U15-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.11
Std Dev	11.59

Turn on : 26.3778

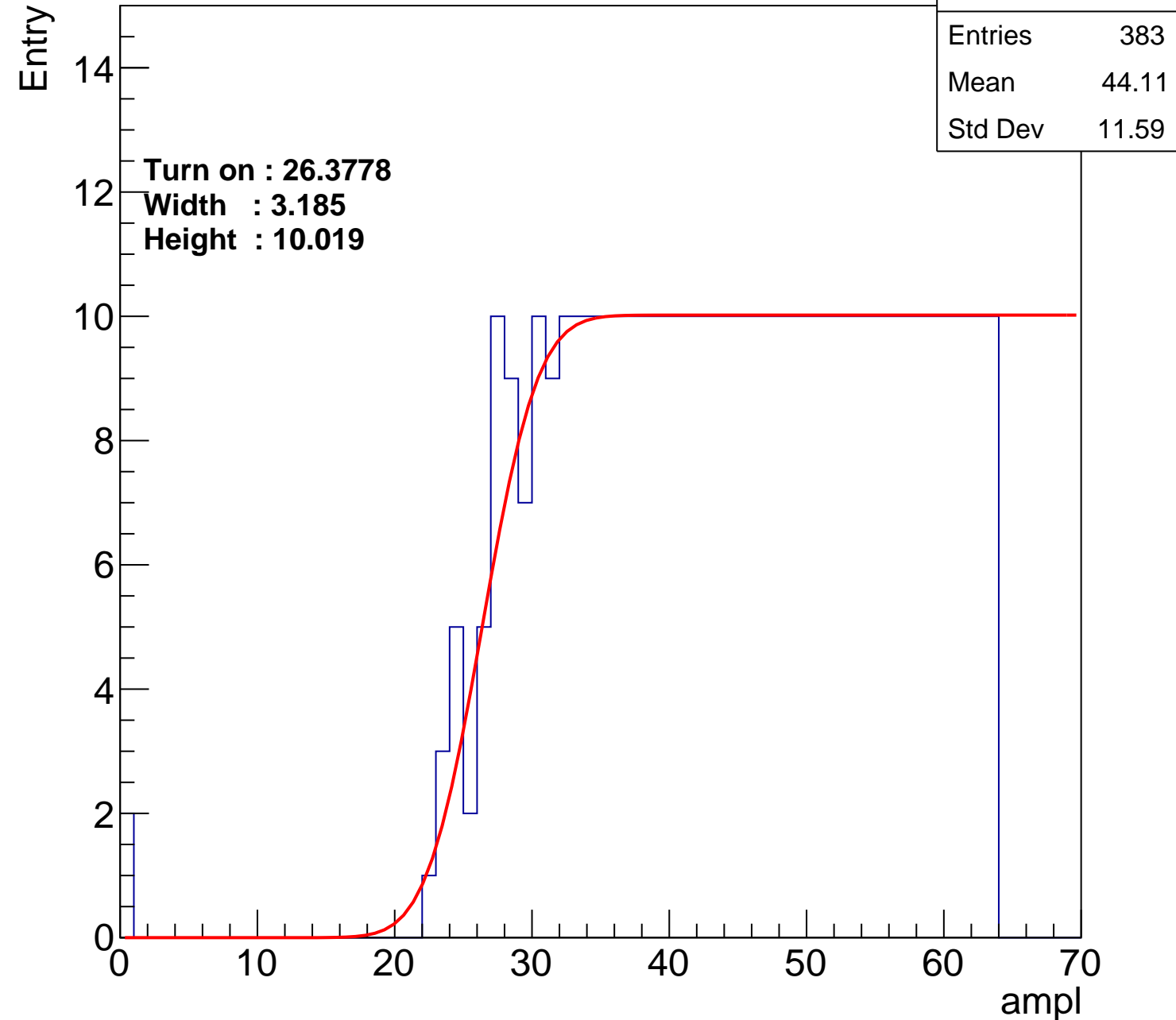
Width : 3.185

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch38

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.38
Std Dev	11.49

Turn on : 26.4387

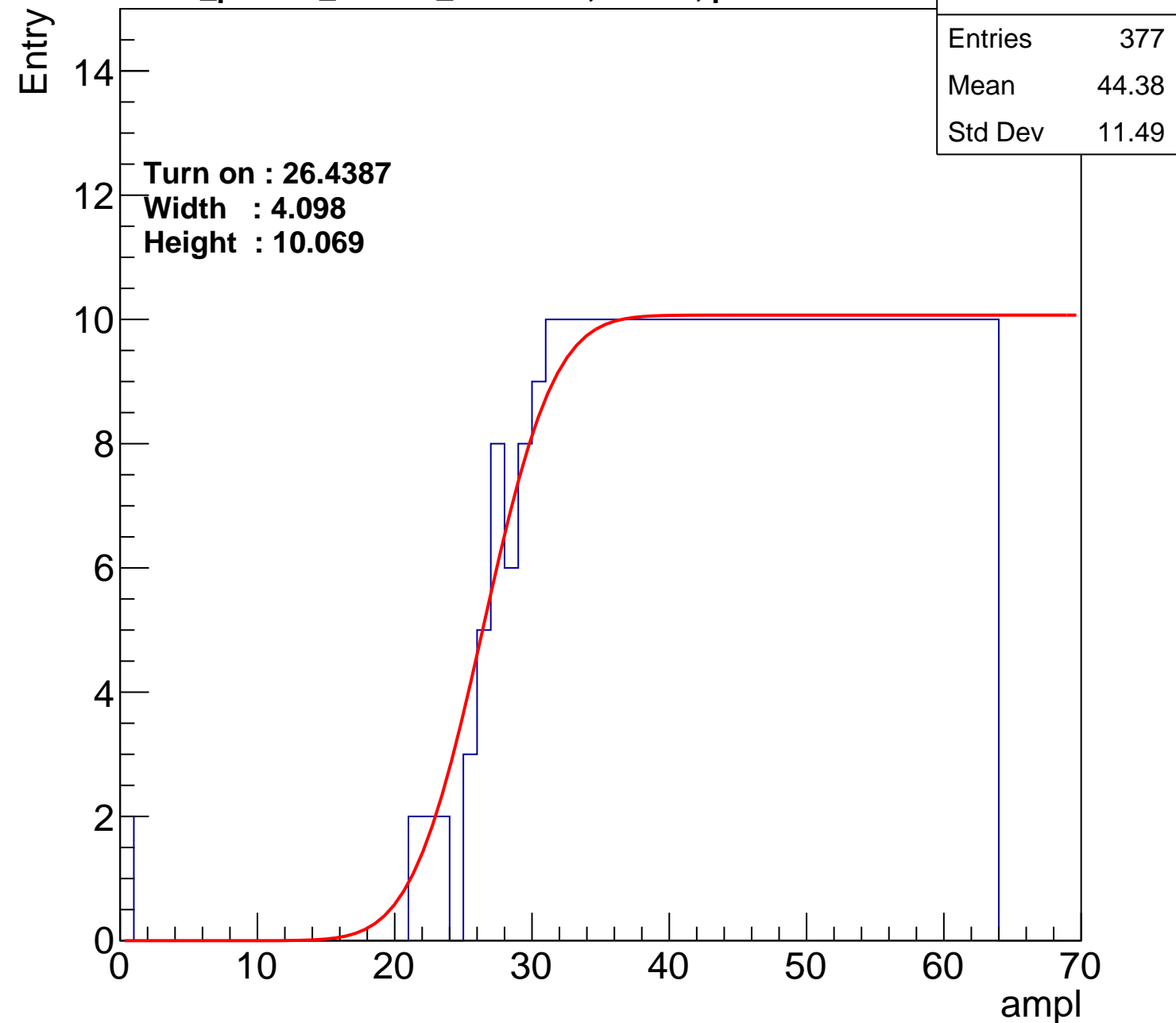
Width : 4.098

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch39

calib_packv5_042523_0143.root, FC#11, port A2

Entries	370
Mean	44.59
Std Dev	11.68

Turn on : 27.1697

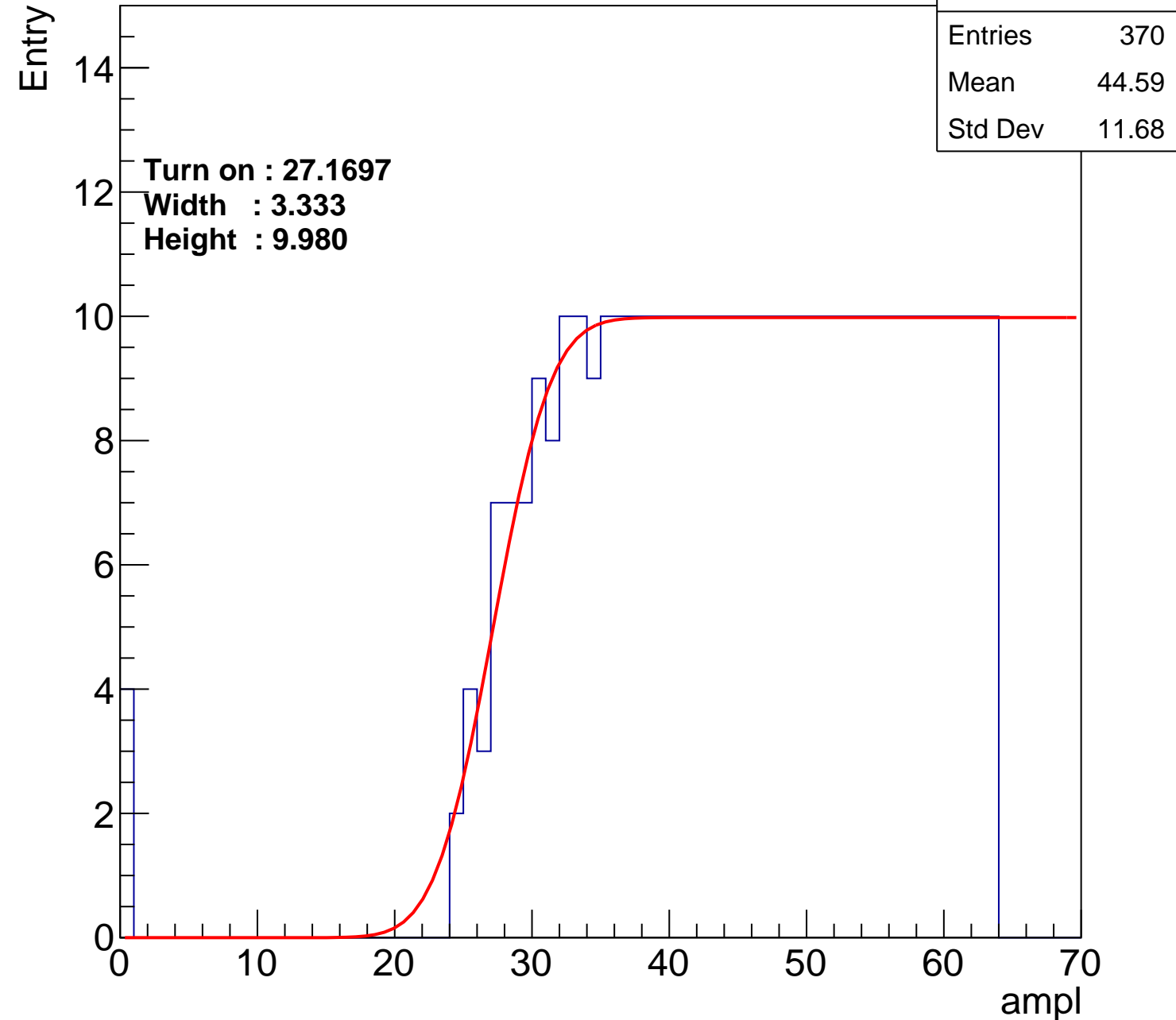
Width : 3.333

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch40

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	43.11
Std Dev	12.09

Turn on : 23.8674

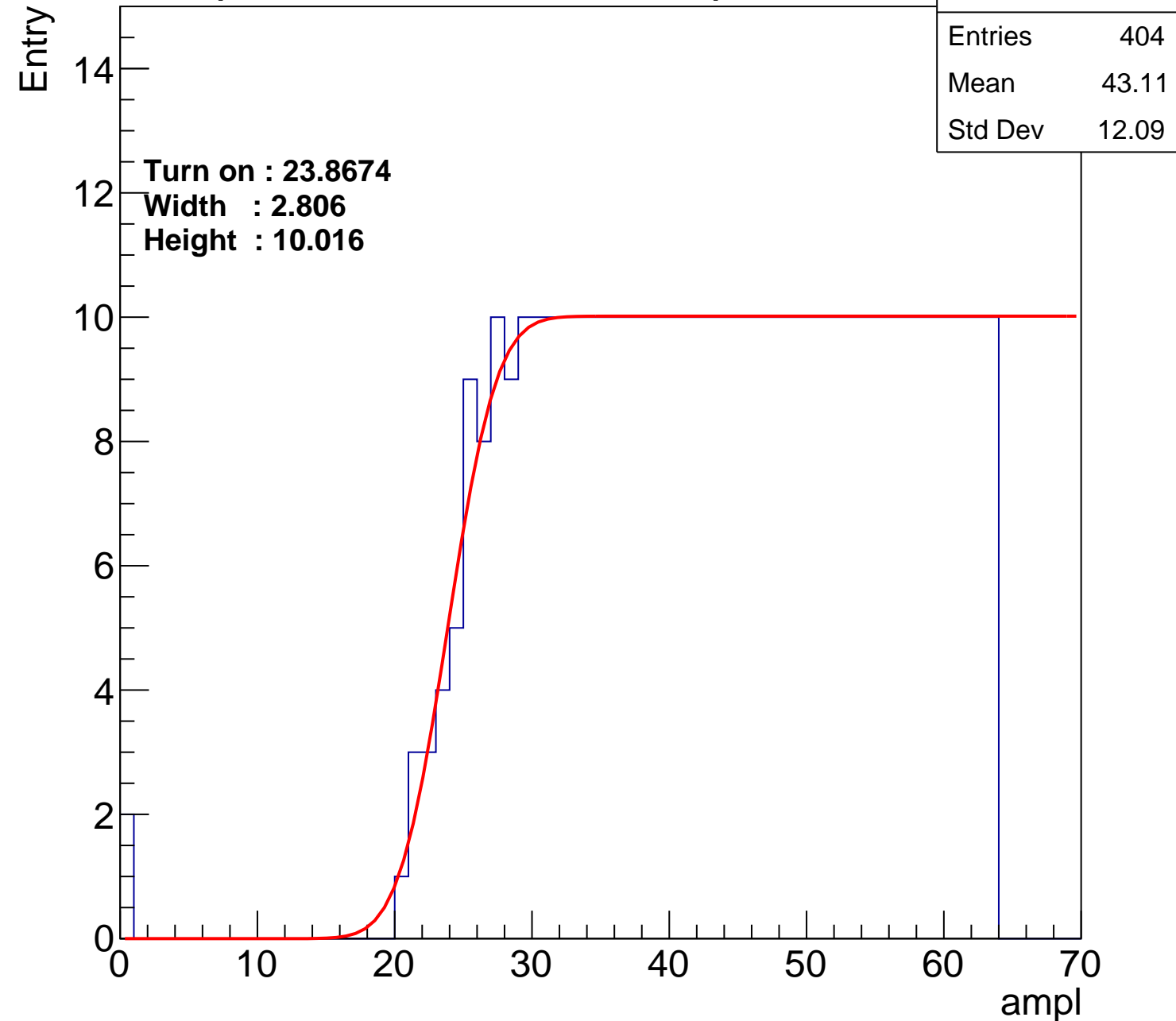
Width : 2.806

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch41

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	43.06
Std Dev	12.23

Turn on : 24.3766

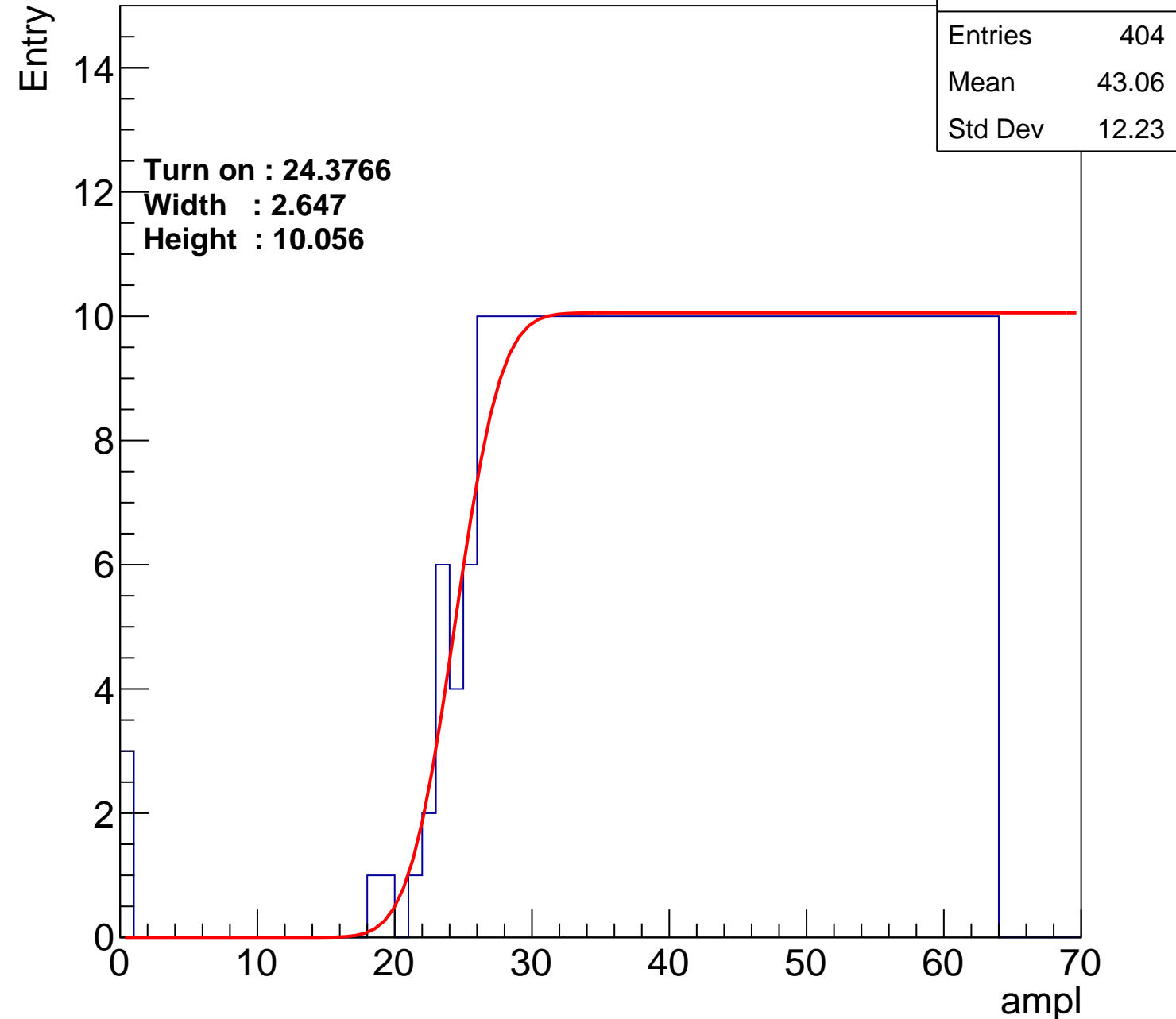
Width : 2.647

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch42

calib_packv5_042523_0143.root, FC#11, port A2

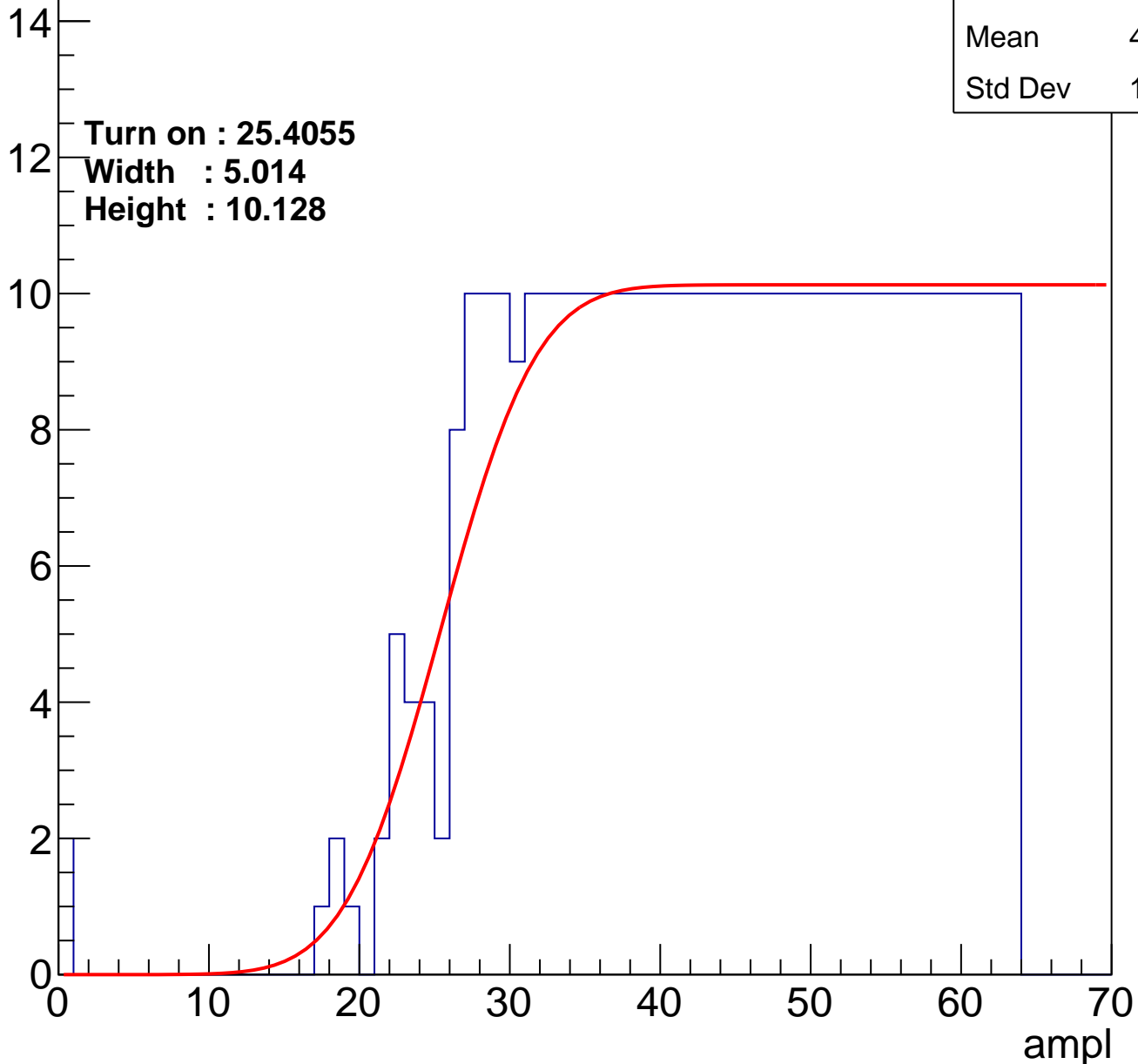
Entries	400
Mean	43.23
Std Dev	12.13

Turn on : 25.4055

Width : 5.014

Height : 10.128

Entry



B1L102S, U15-ch43

calib_packv5_042523_0143.root, FC#11, port A2

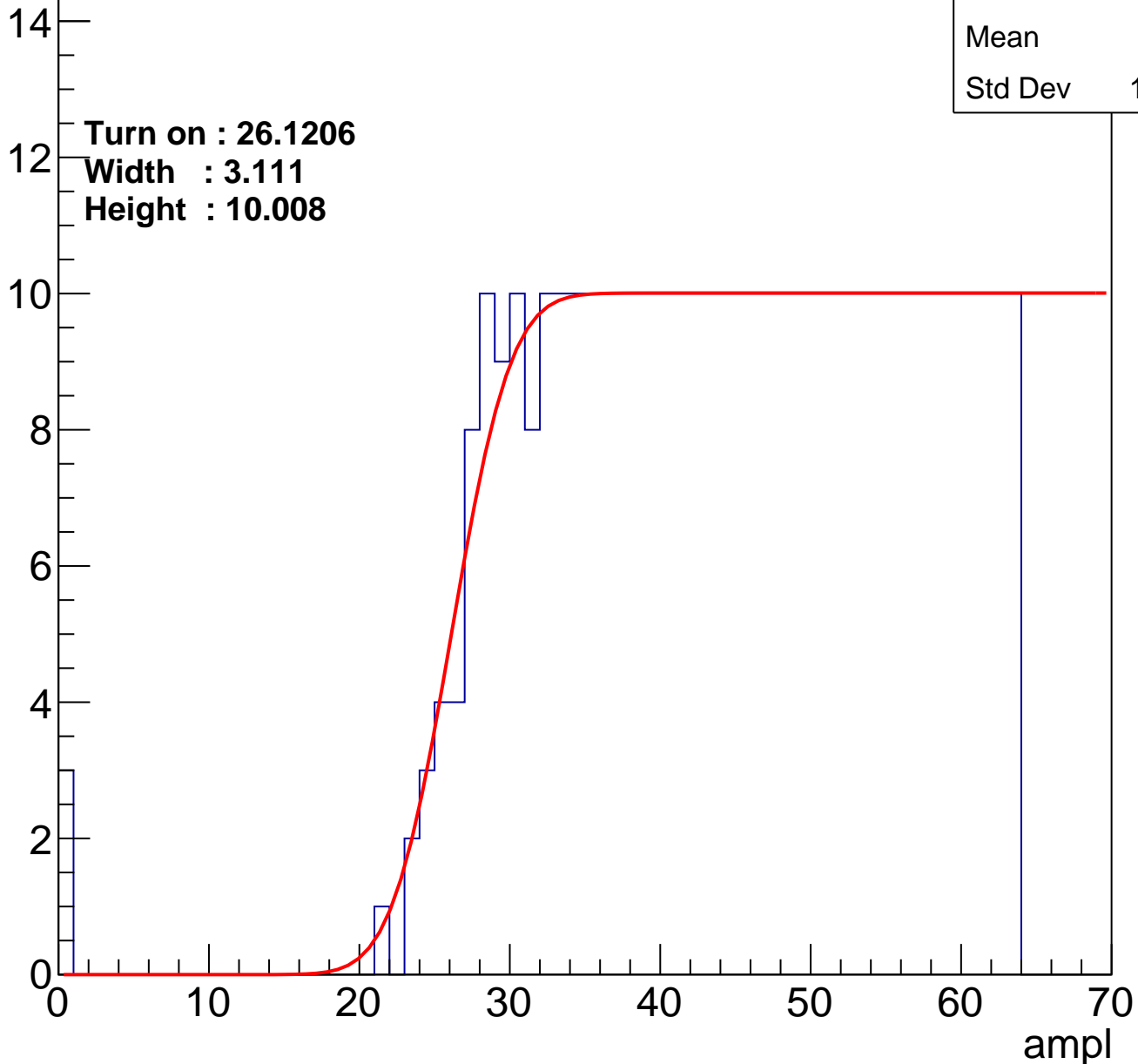
Entry

Entries	382
Mean	44.1
Std Dev	11.73

Turn on : 26.1206

Width : 3.111

Height : 10.008



B1L102S, U15-ch44

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.45
Std Dev	12.08

Turn on : 25.1737

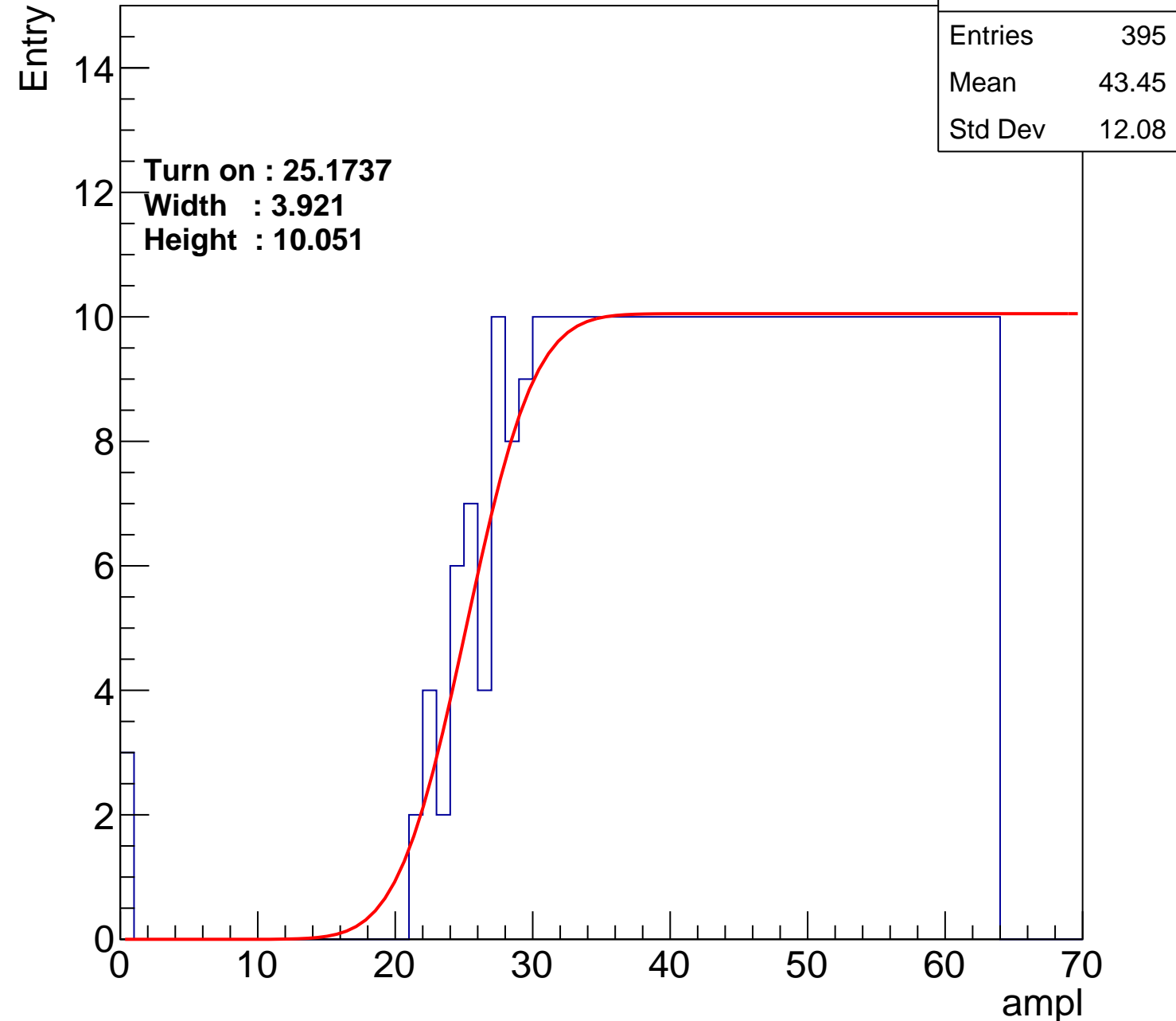
Width : 3.921

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch45

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.77
Std Dev	11.82

Turn on : 25.3871

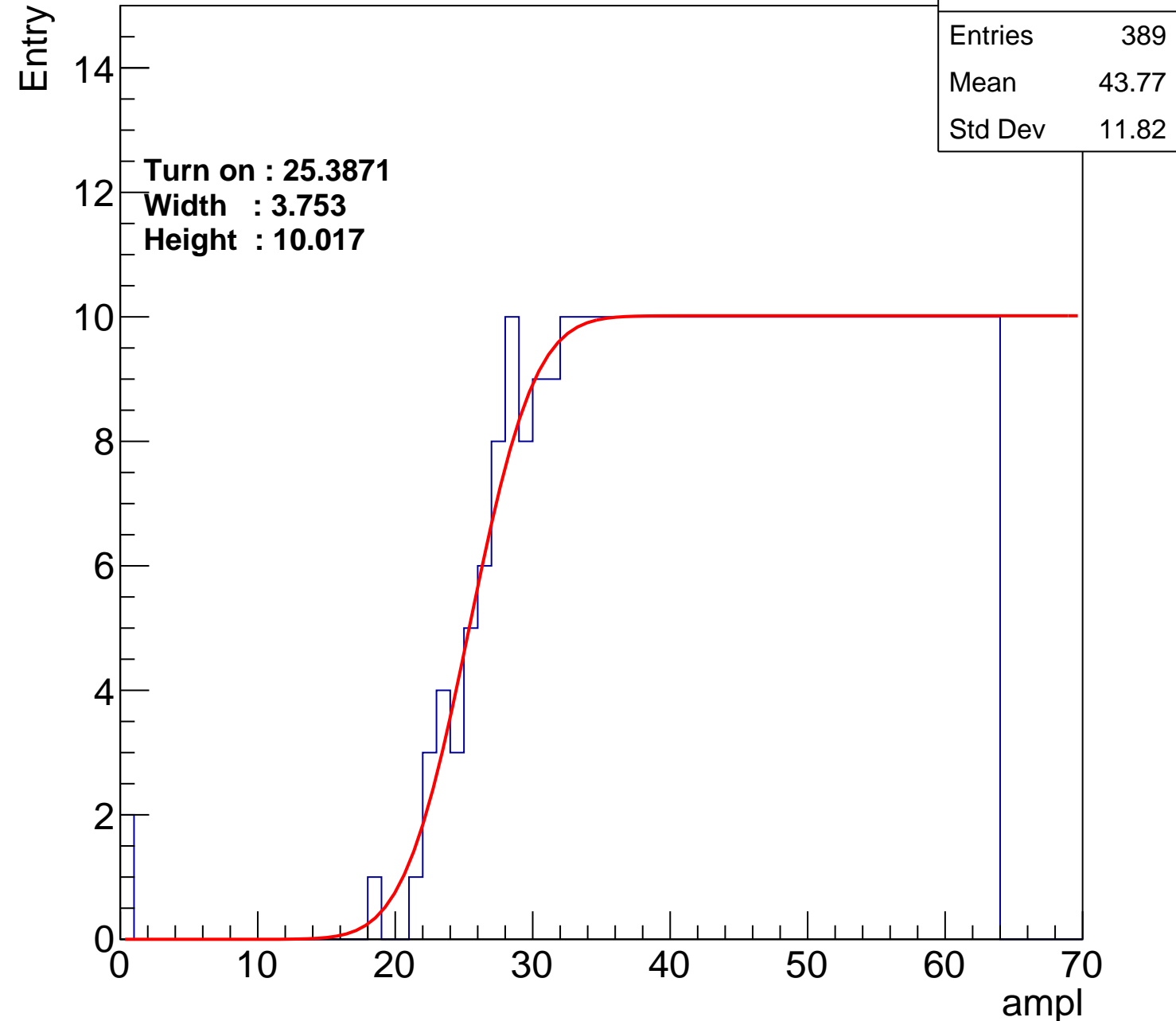
Width : 3.753

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch46

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.25
Std Dev	11.97

Turn on : 27.3848

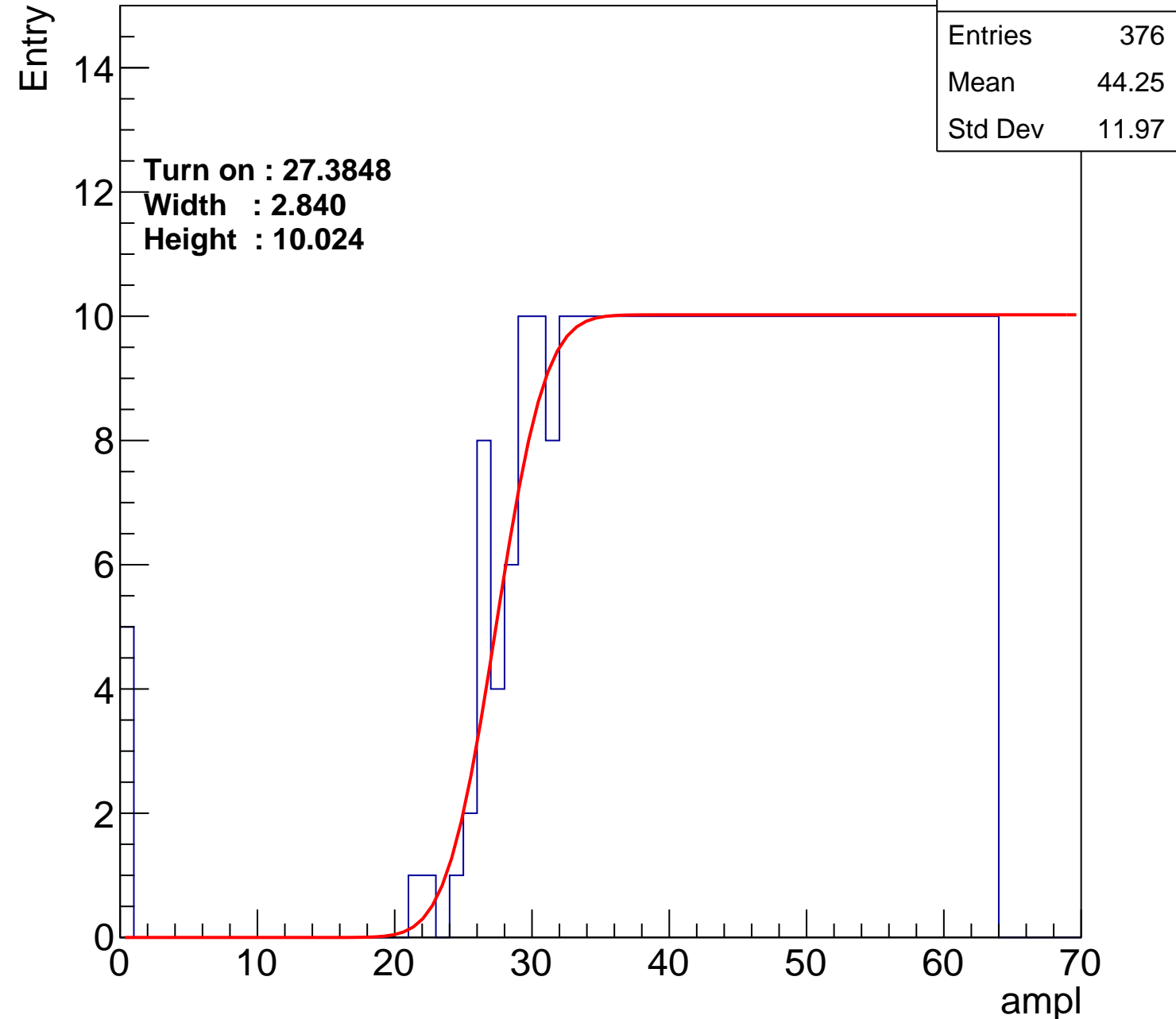
Width : 2.840

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch47

calib_packv5_042523_0143.root, FC#11, port A2

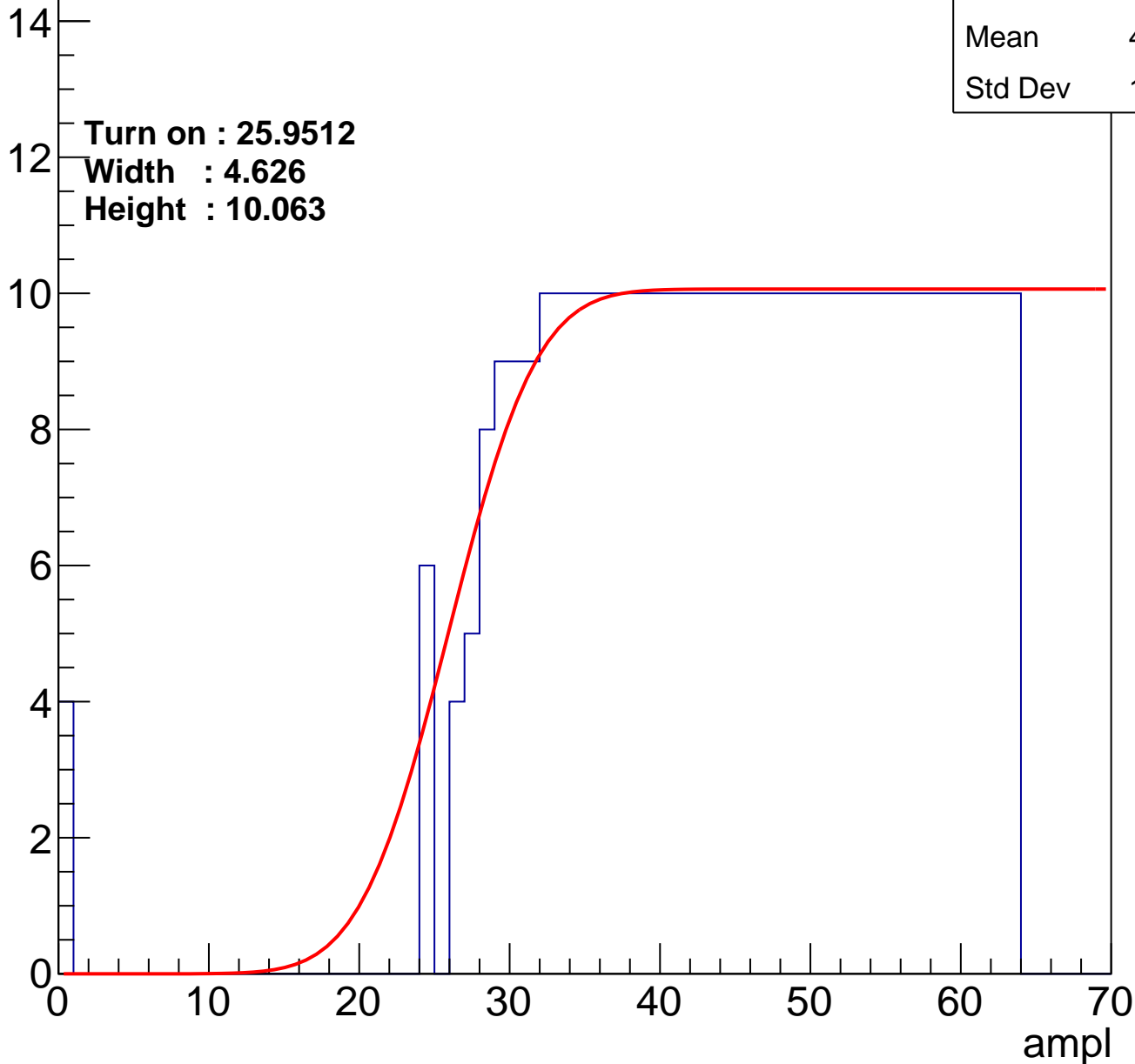
Entry

Entries	374
Mean	44.43
Std Dev	11.72

Turn on : 25.9512

Width : 4.626

Height : 10.063



B1L102S, U15-ch48

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.55
Std Dev	12.14

Turn on : 25.2961

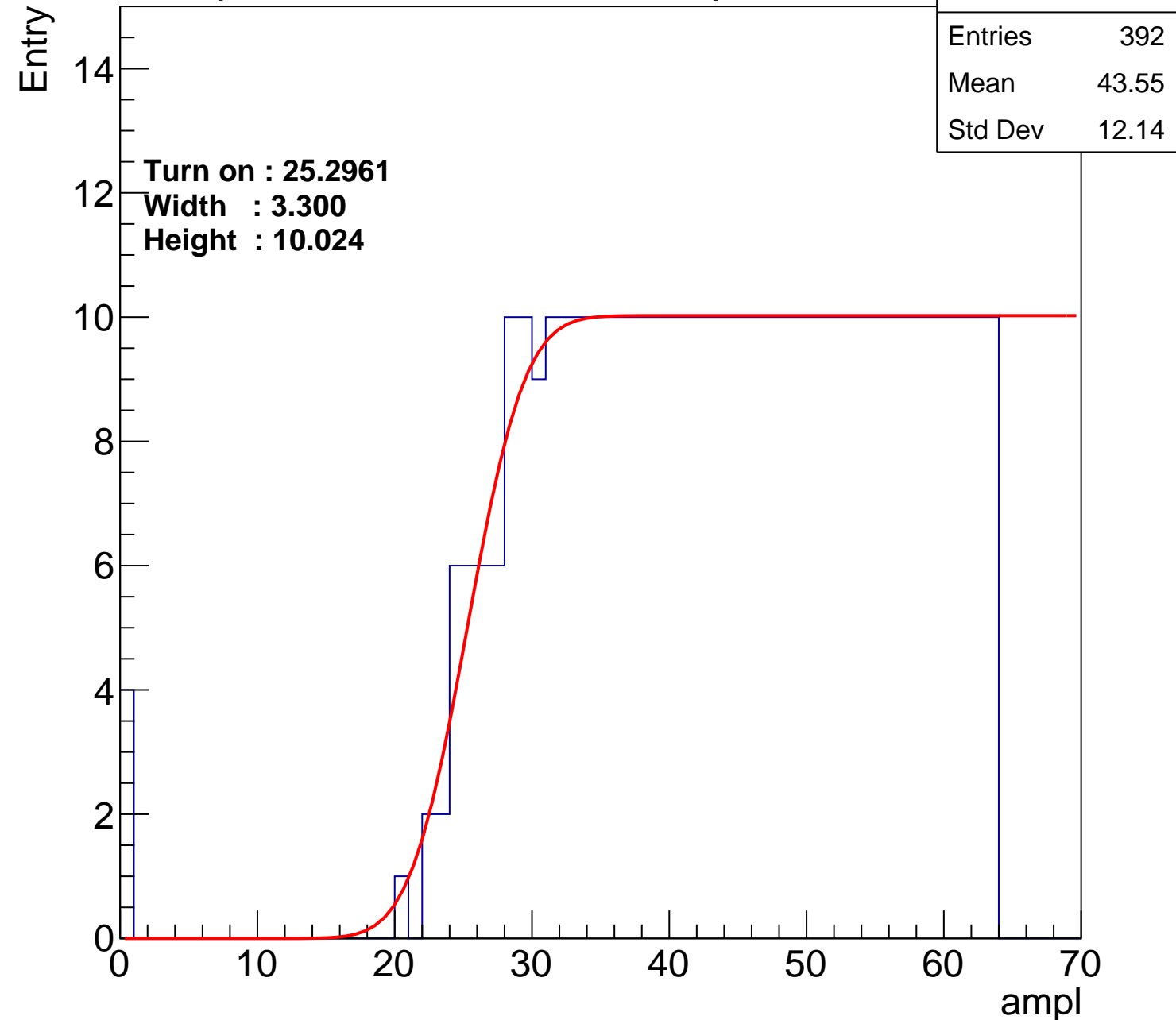
Width : 3.300

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch49

calib_packv5_042523_0143.root, FC#11, port A2

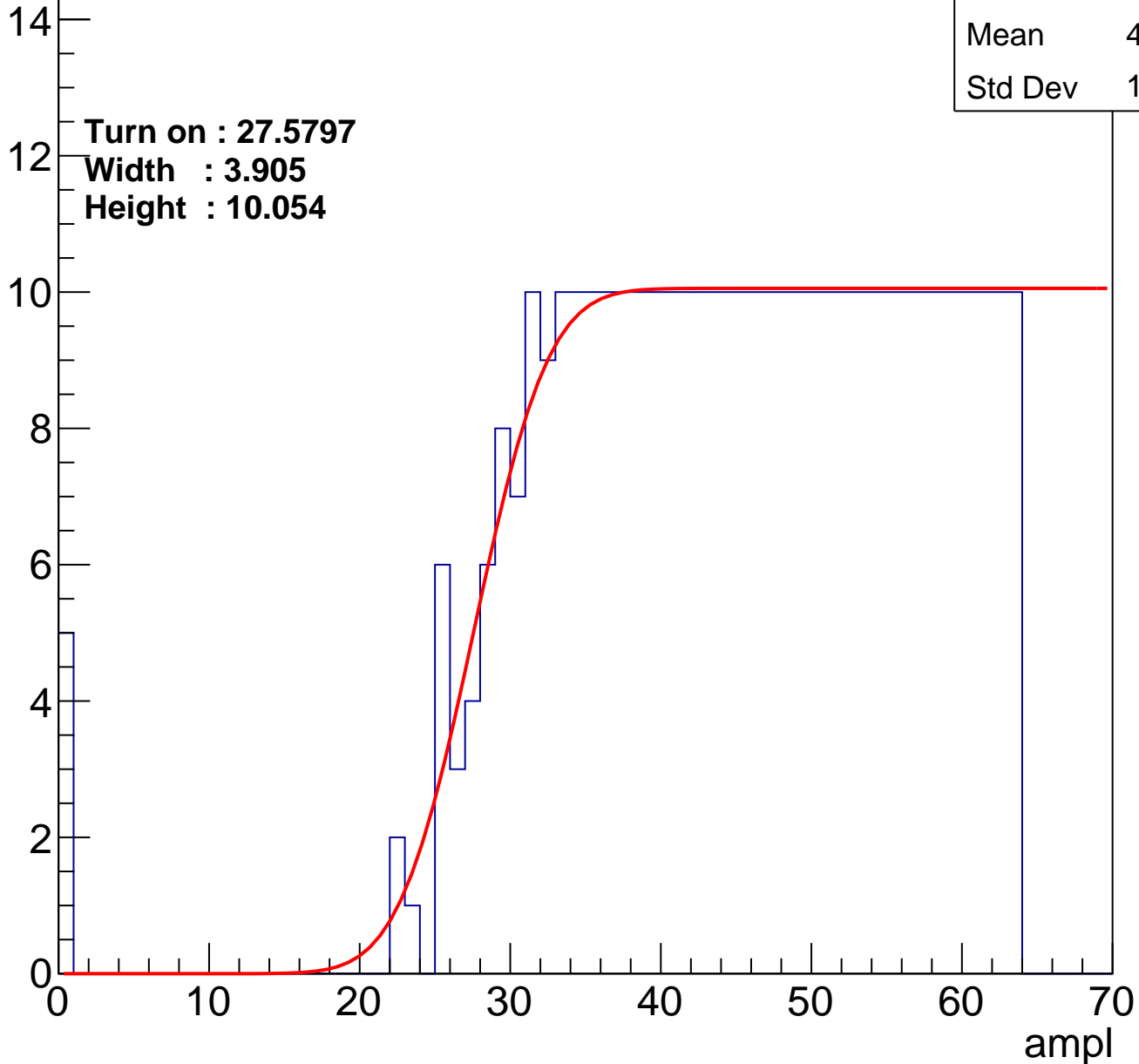
Entries	371
Mean	44.45
Std Dev	11.93

Turn on : 27.5797

Width : 3.905

Height : 10.054

Entry



B1L102S, U15-ch50

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.8
Std Dev	11.72

Turn on : 25.7443

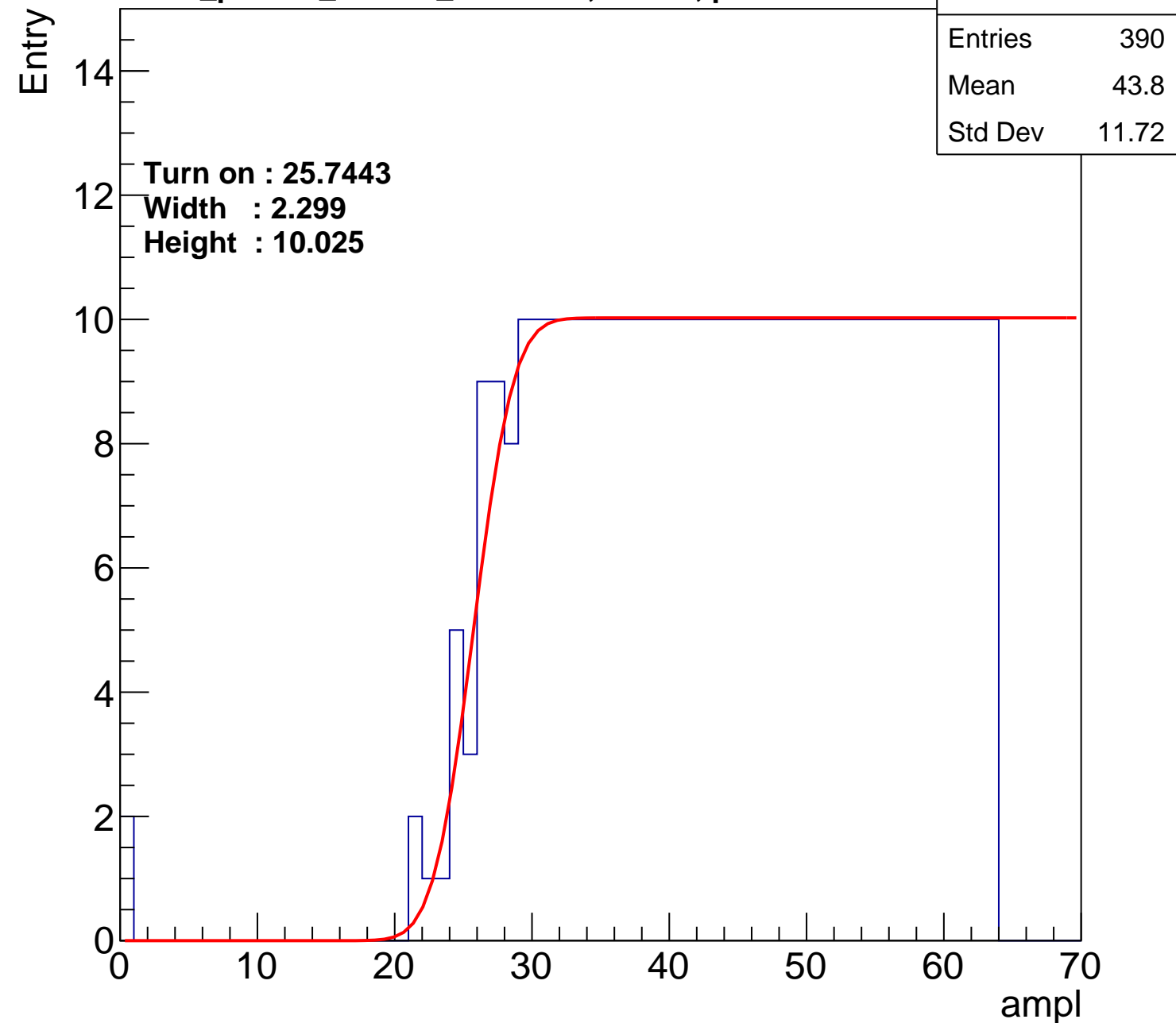
Width : 2.299

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch51

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.1677

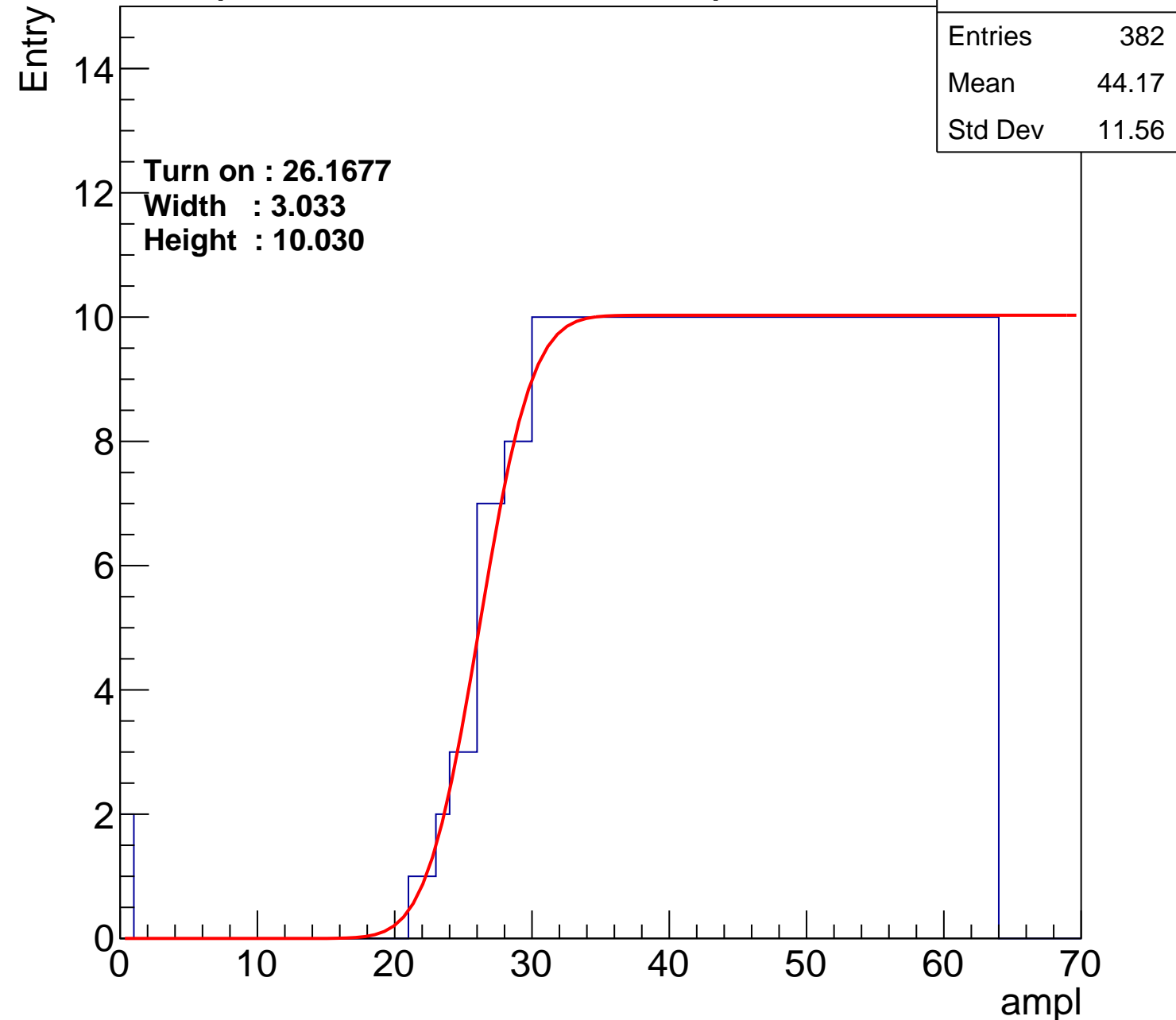
Width : 3.033

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch52

calib_packv5_042523_0143.root, FC#11, port A2

Entries	406
Mean	42.97
Std Dev	12.26

Turn on : 22.4278

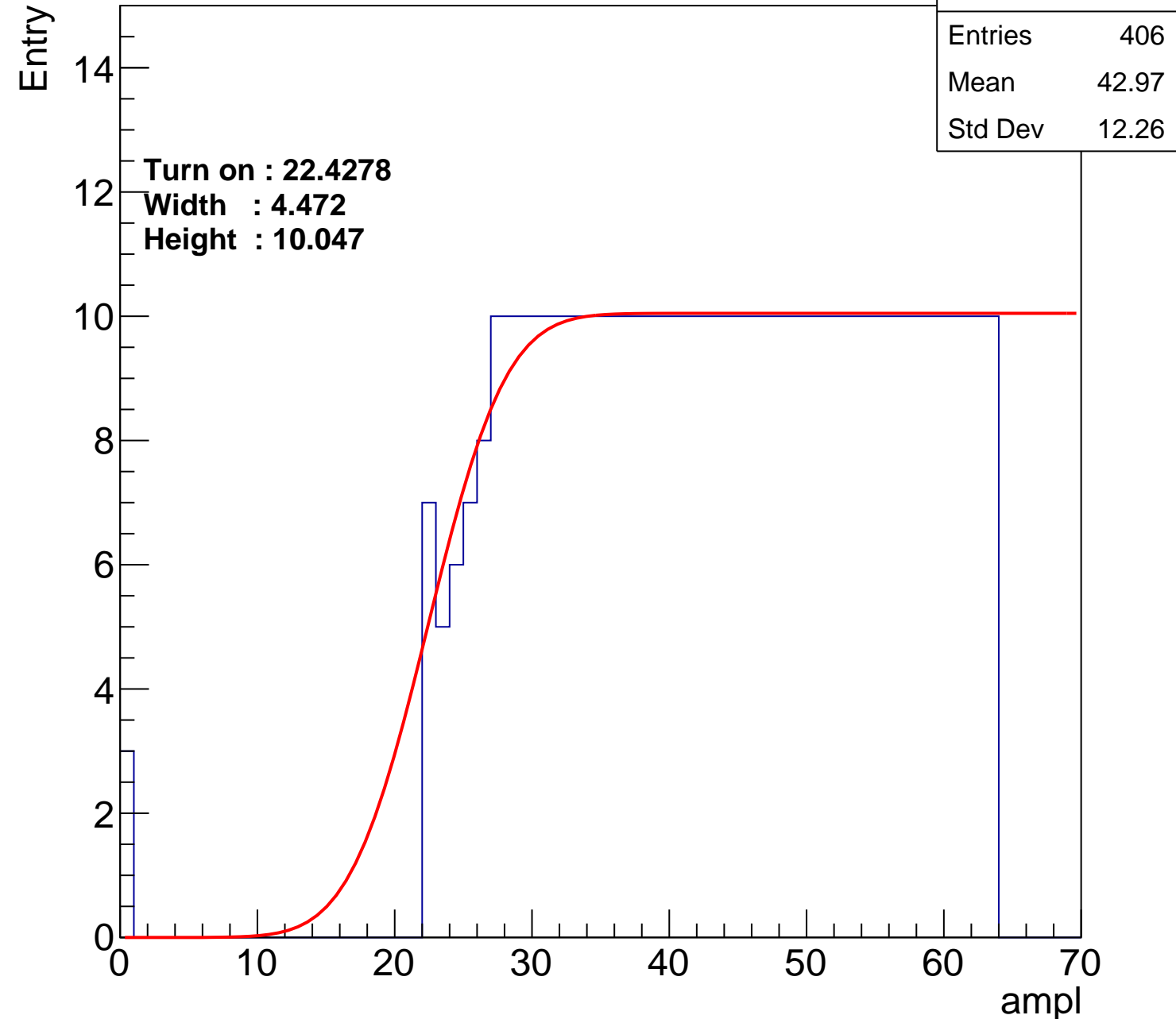
Width : 4.472

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch53

calib_packv5_042523_0143.root, FC#11, port A2

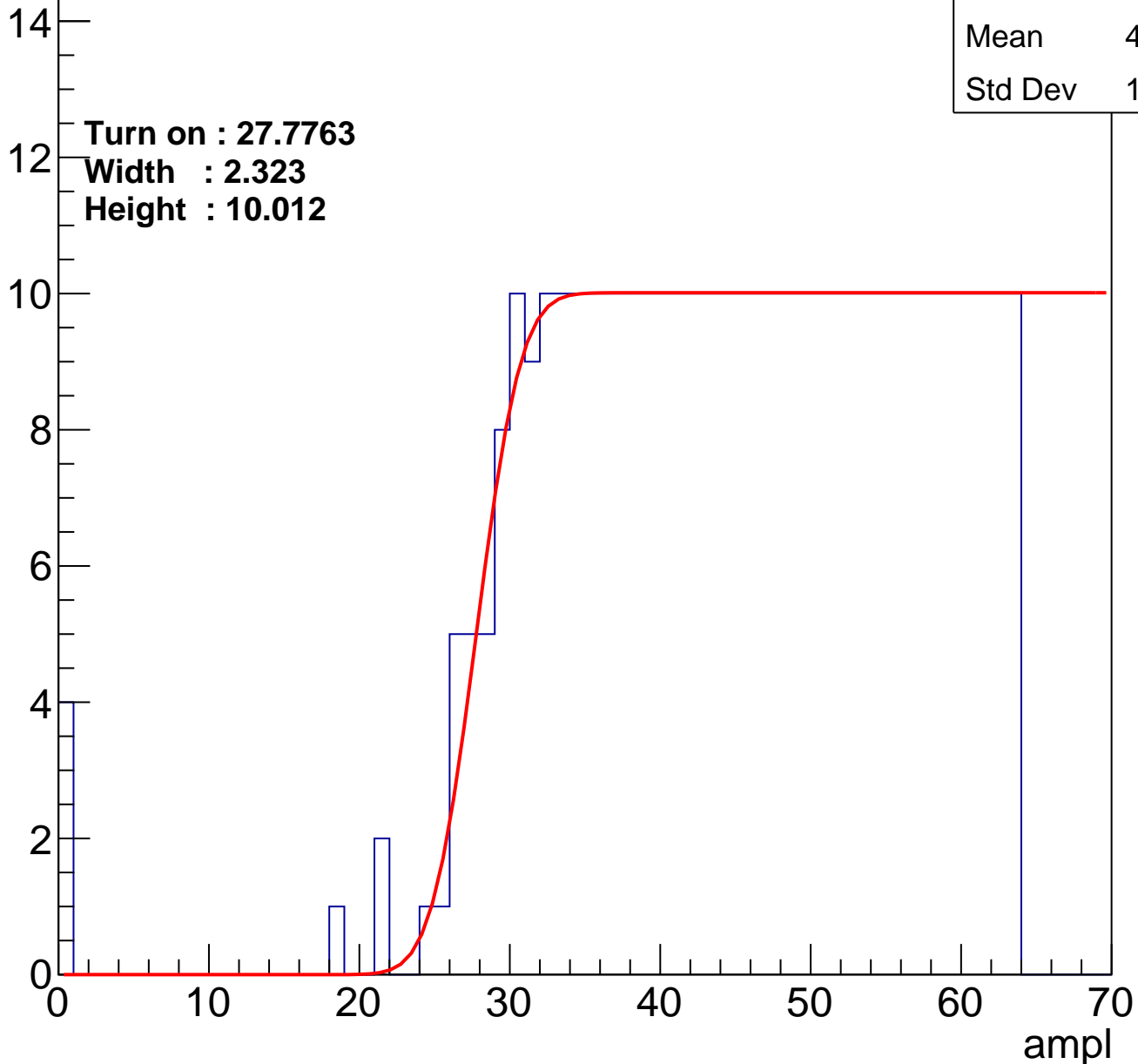
Entries	371
Mean	44.54
Std Dev	11.72

Turn on : 27.7763

Width : 2.323

Height : 10.012

Entry



B1L102S, U15-ch54

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.94
Std Dev	11.9

Turn on : 26.0917

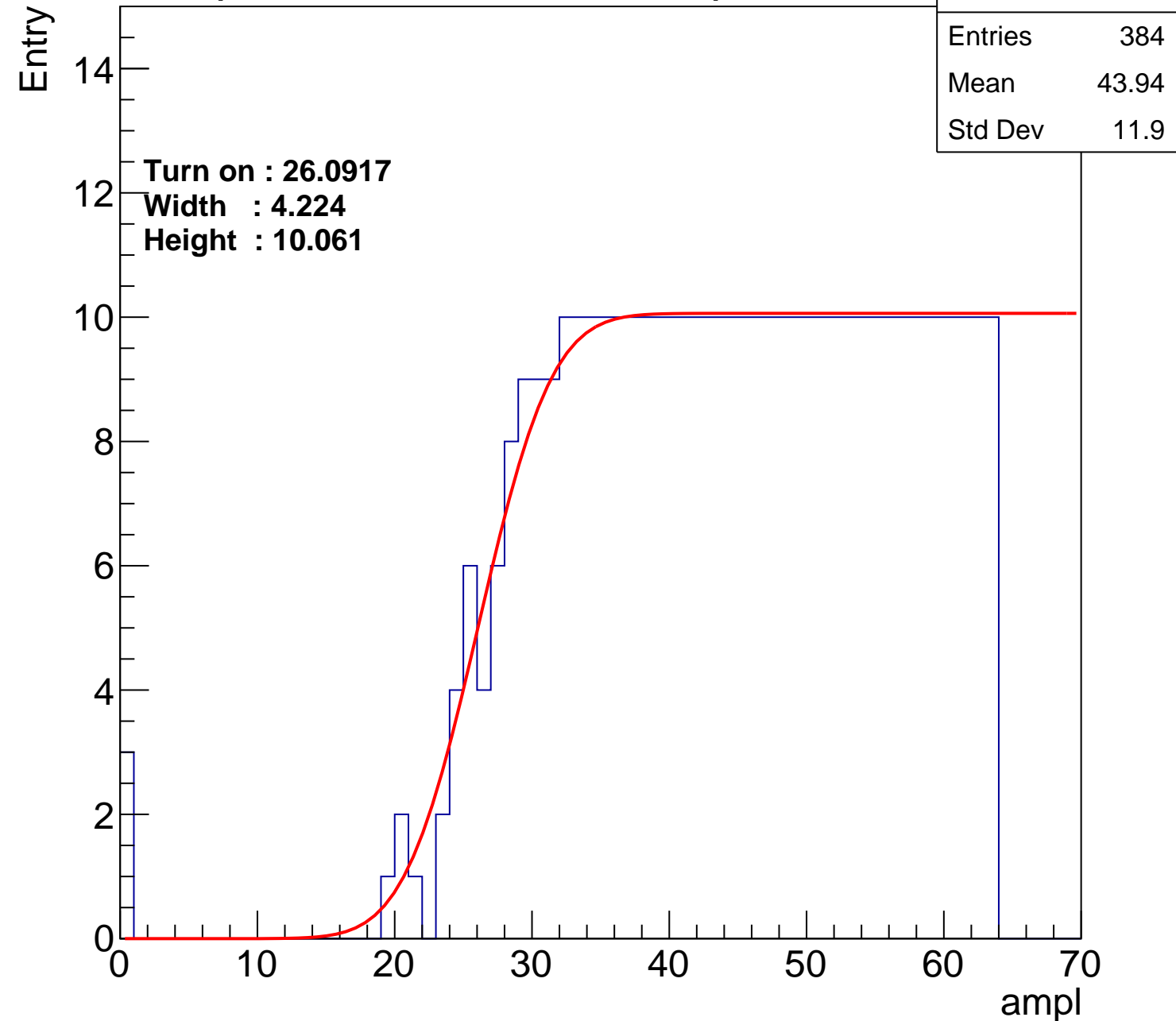
Width : 4.224

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch55

calib_packv5_042523_0143.root, FC#11, port A2

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.6085

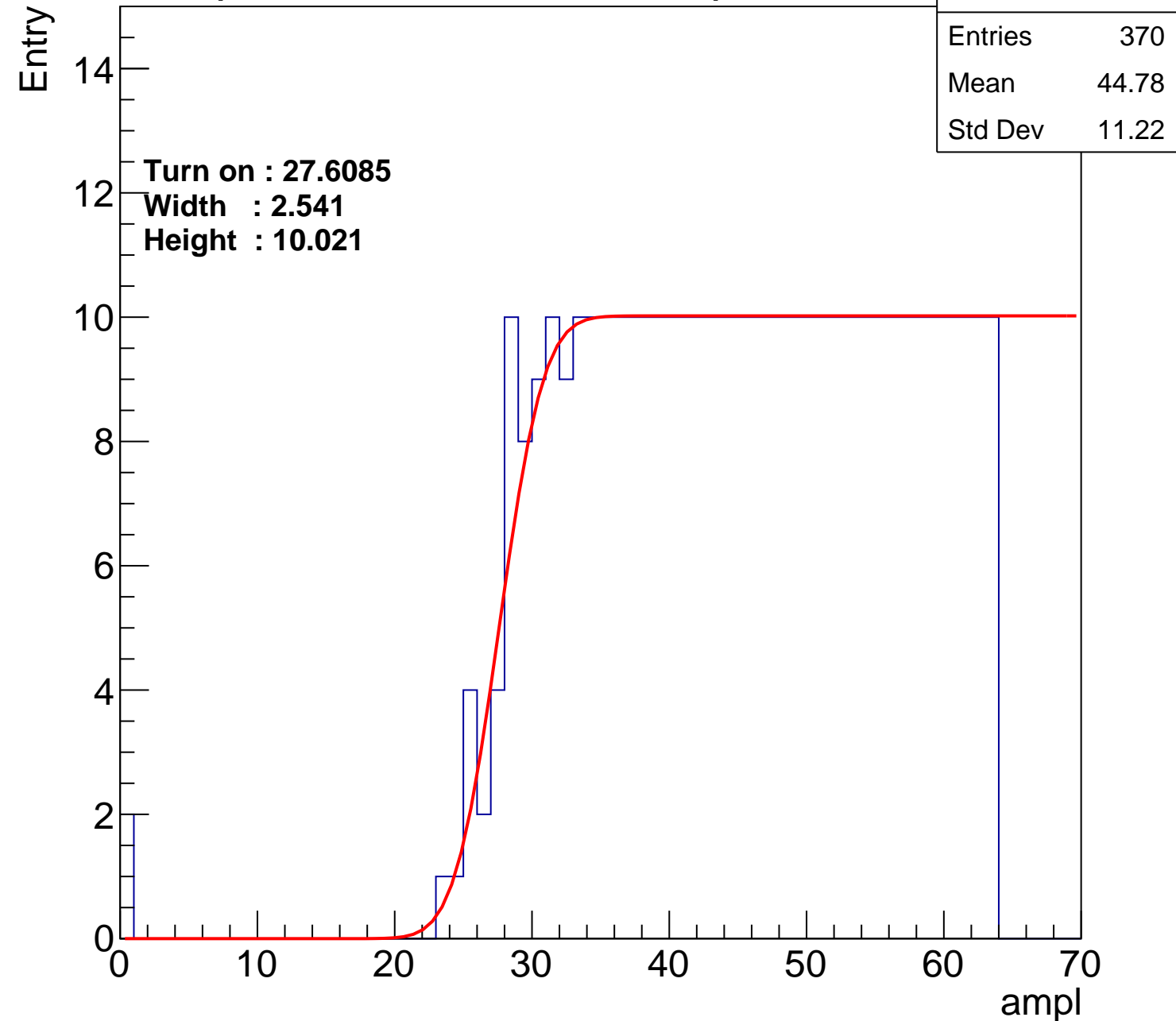
Width : 2.541

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch56

calib_packv5_042523_0143.root, FC#11, port A2

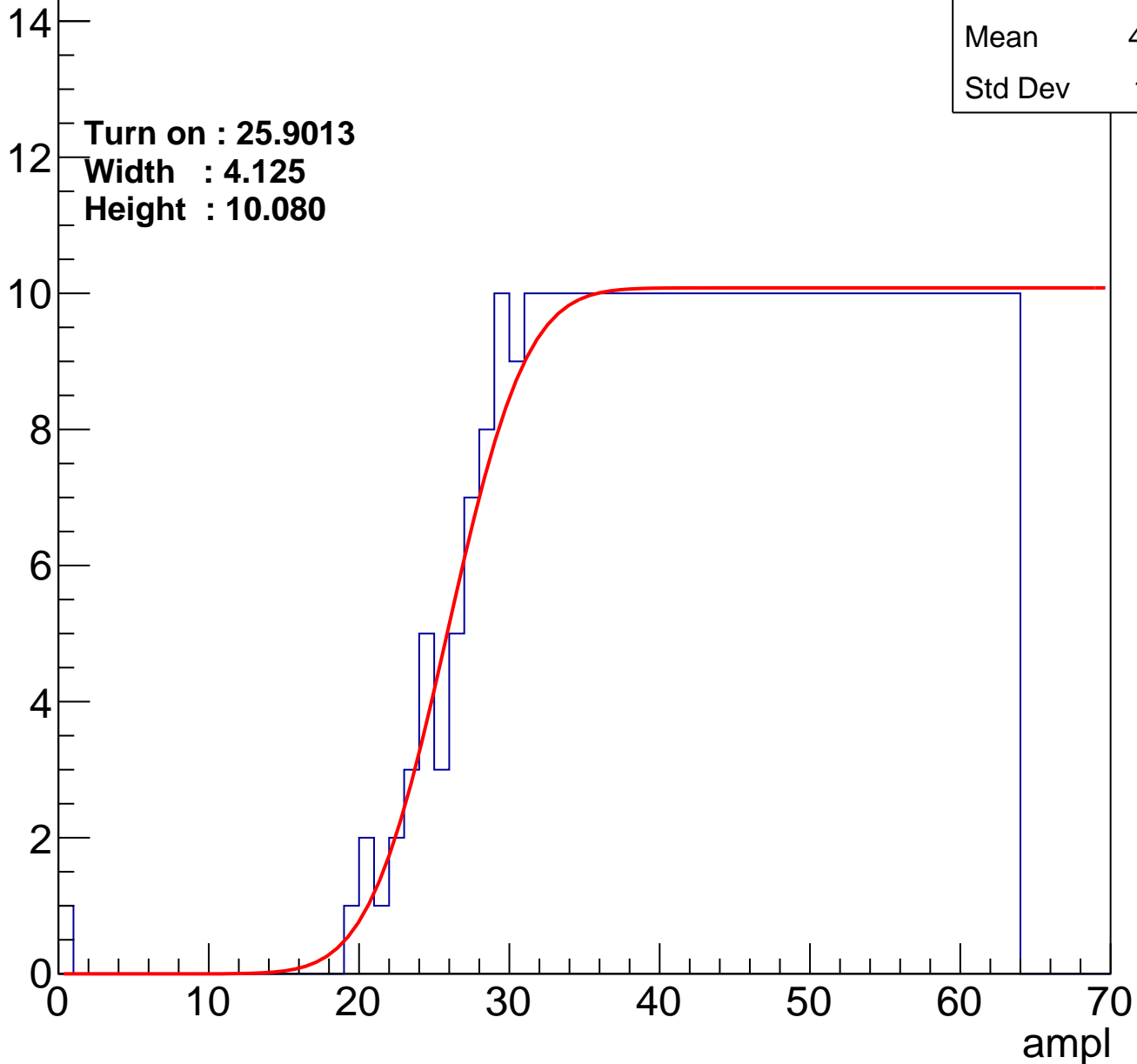
Entry

Entries	387
Mean	43.93
Std Dev	11.61

Turn on : 25.9013

Width : 4.125

Height : 10.080



B1L102S, U15-ch57

calib_packv5_042523_0143.root, FC#11, port A2

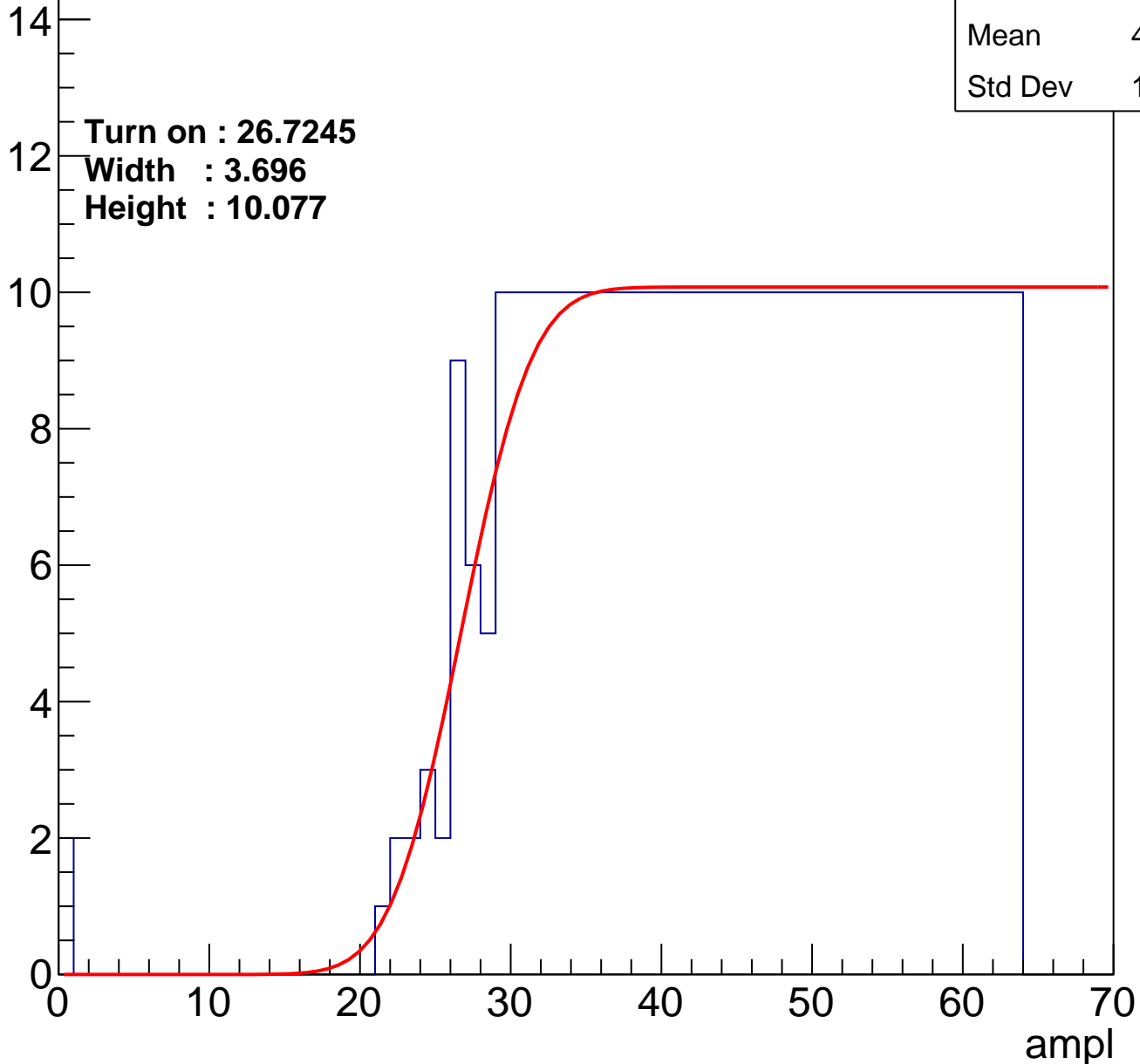
Entries	382
Mean	44.16
Std Dev	11.58

Turn on : 26.7245

Width : 3.696

Height : 10.077

Entry



B1L102S, U15-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	42.76
Std Dev	12.86

Turn on : 24.9381

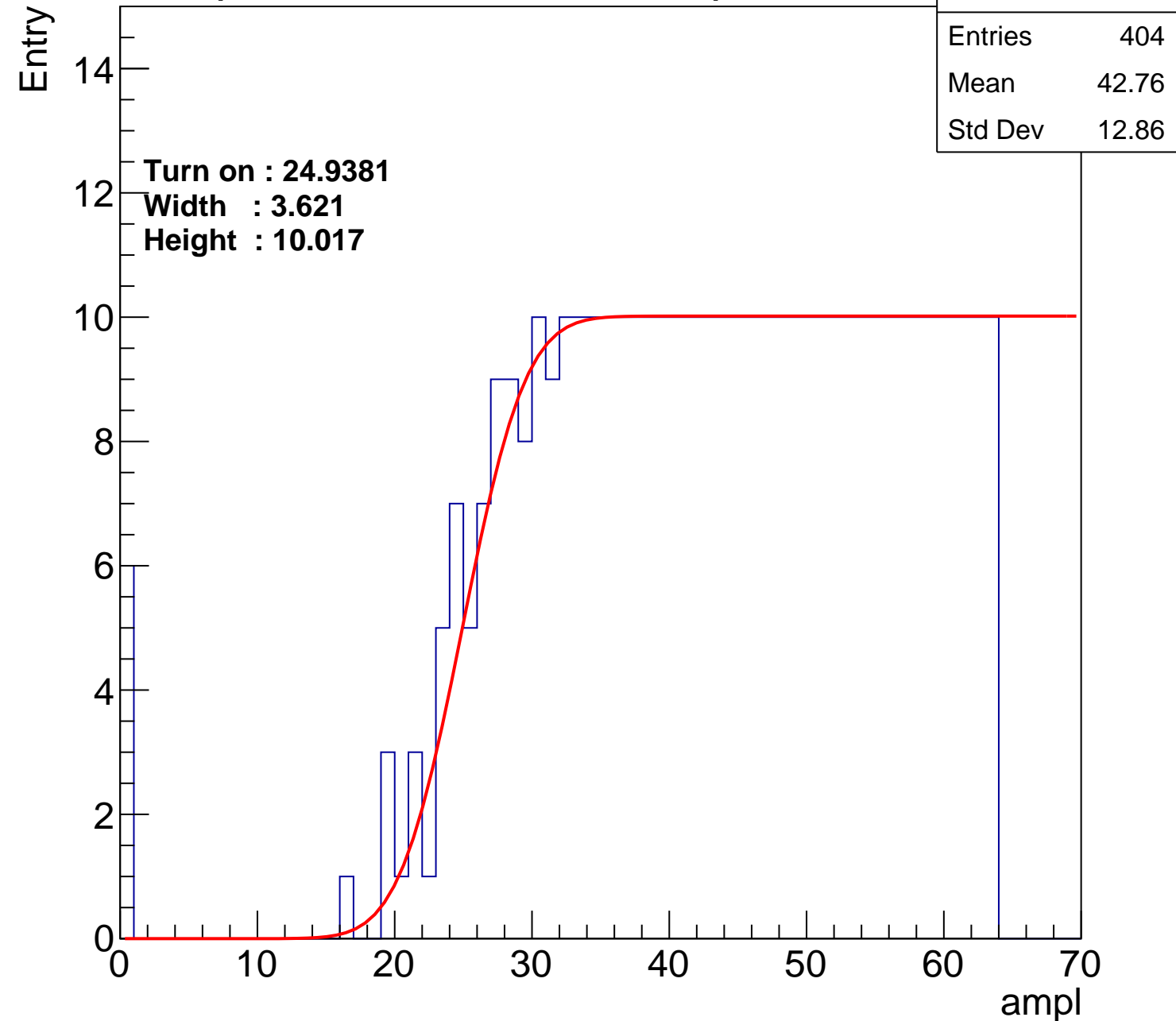
Width : 3.621

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.26
Std Dev	12.3

Turn on : 25.0723

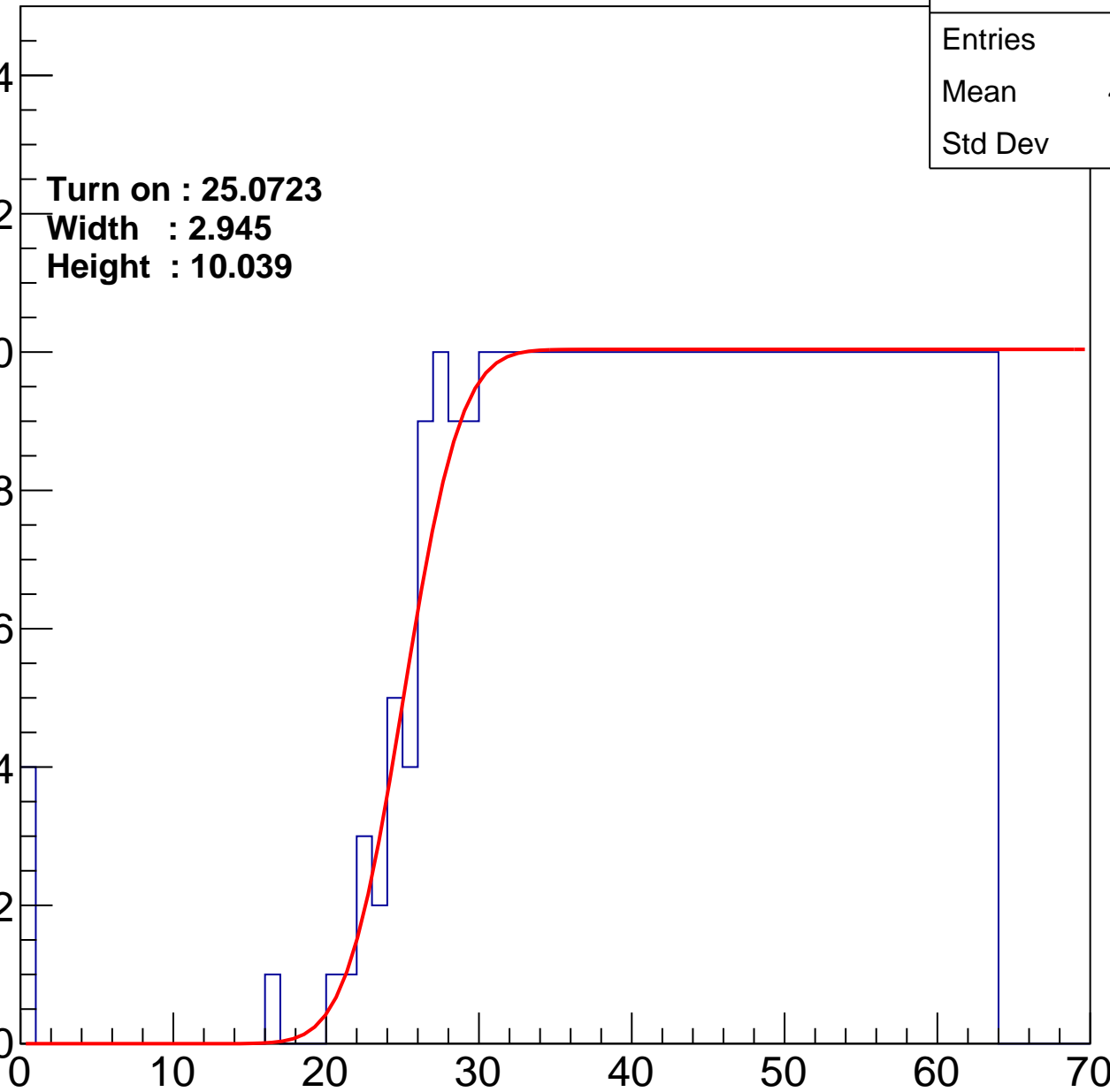
Width : 2.945

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch60

calib_packv5_042523_0143.root, FC#11, port A2

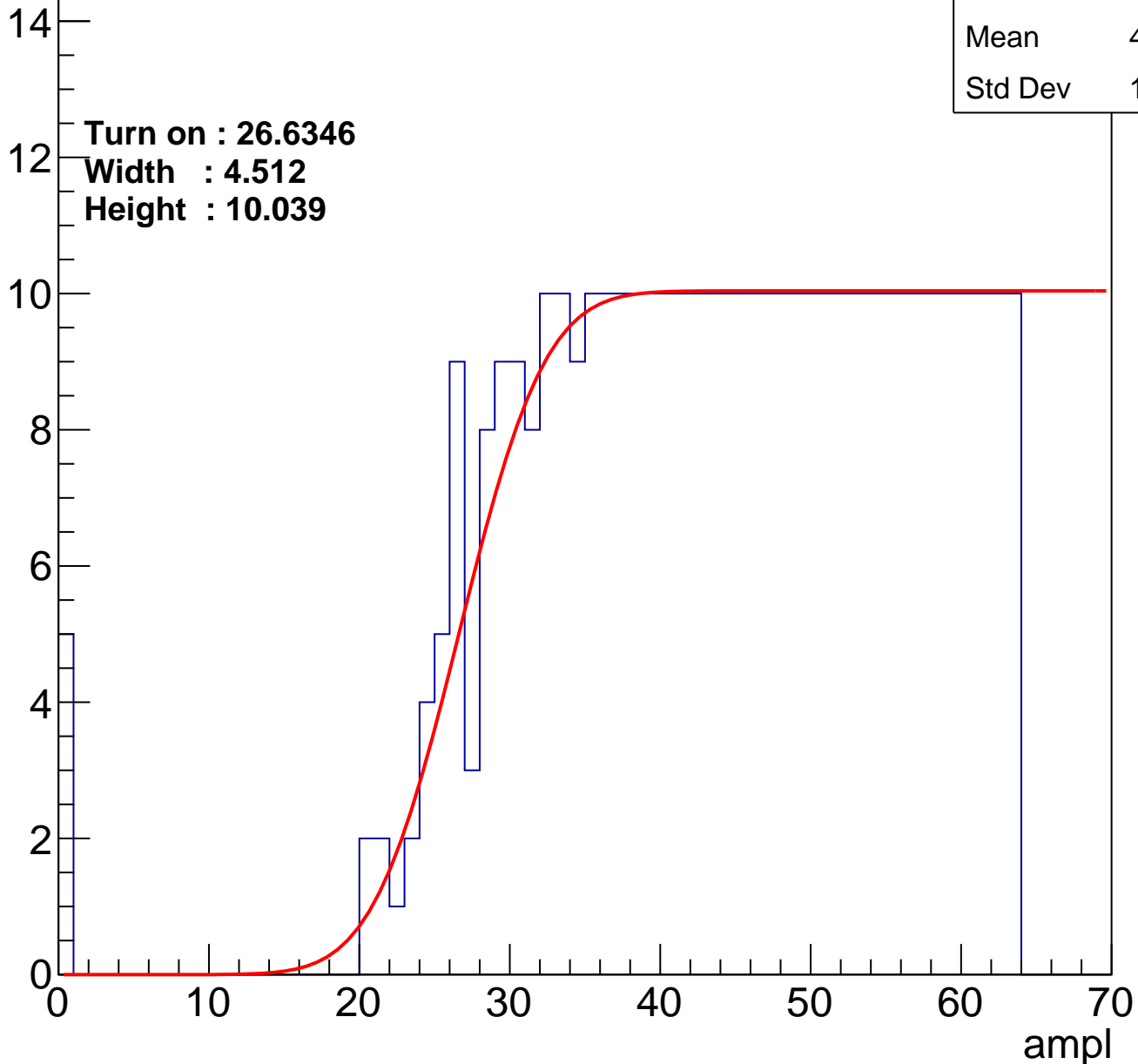
Entries	386
Mean	43.67
Std Dev	12.33

Turn on : 26.6346

Width : 4.512

Height : 10.039

Entry



B1L102S, U15-ch61

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.8
Std Dev	12.02

Turn on : 26.3191

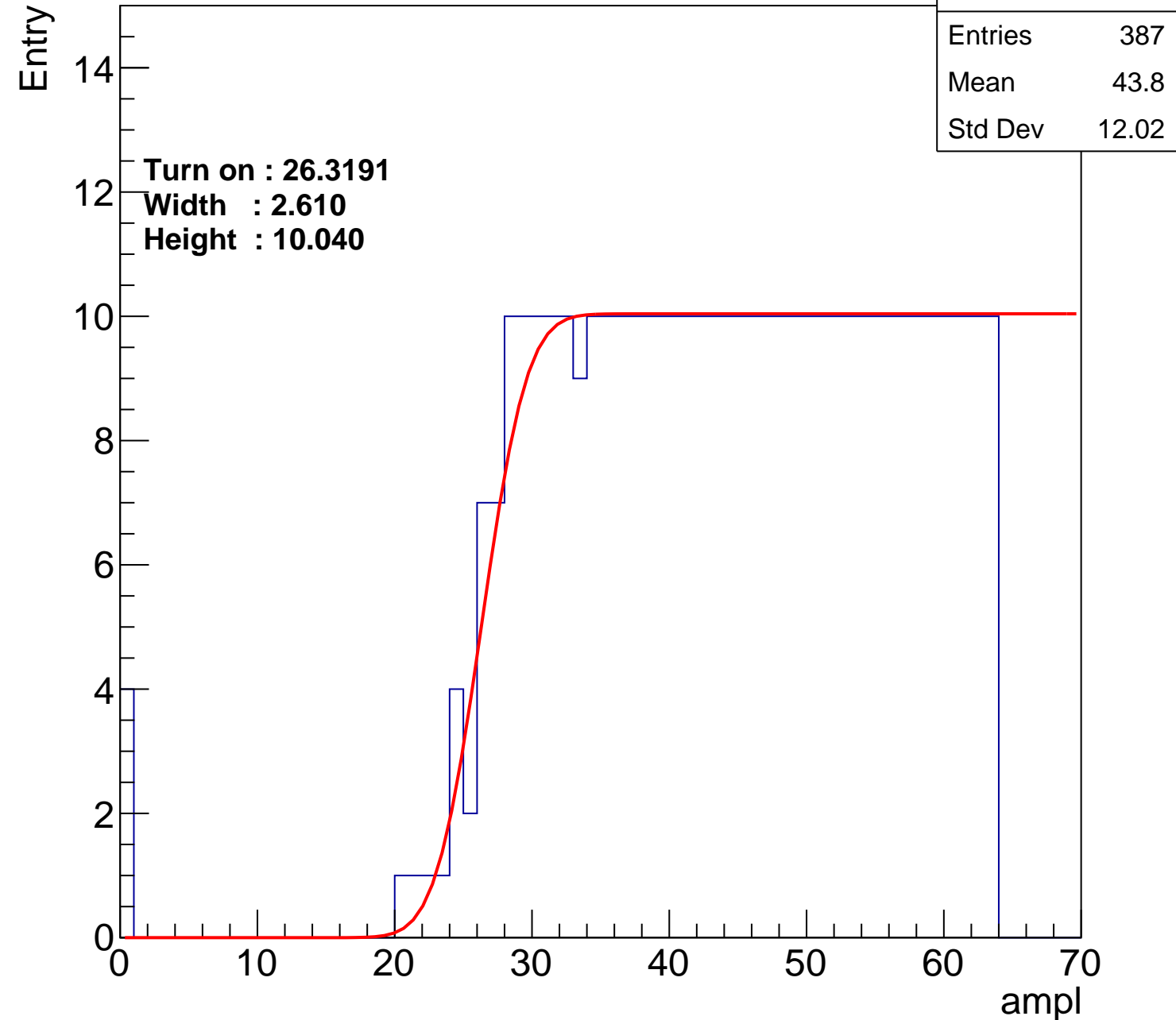
Width : 2.610

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch62

calib_packv5_042523_0143.root, FC#11, port A2

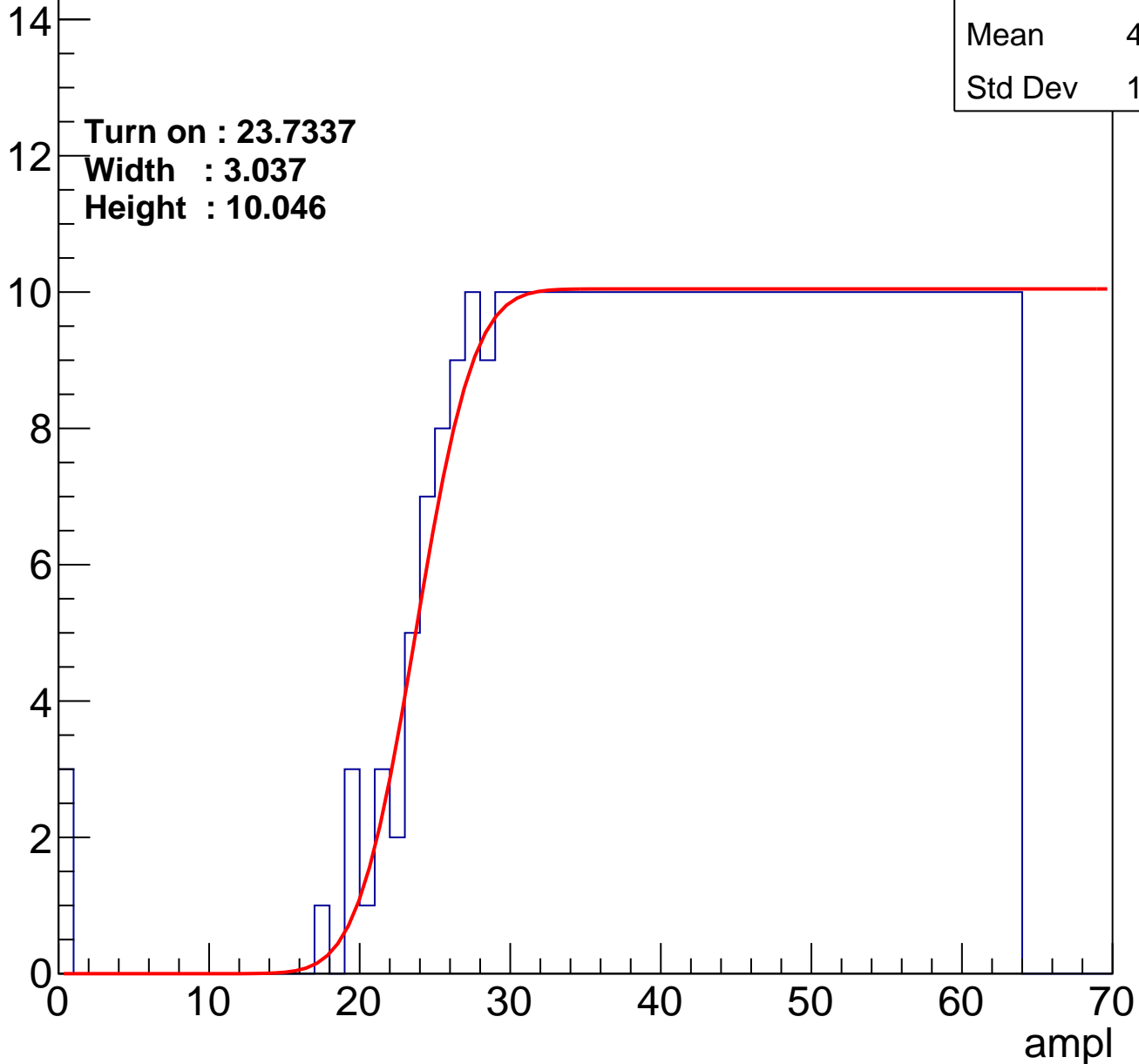
Entries	411
Mean	42.68
Std Dev	12.47

Turn on : 23.7337

Width : 3.037

Height : 10.046

Entry



B1L102S, U15-ch63

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.81
Std Dev	11.75

Turn on : 25.3249

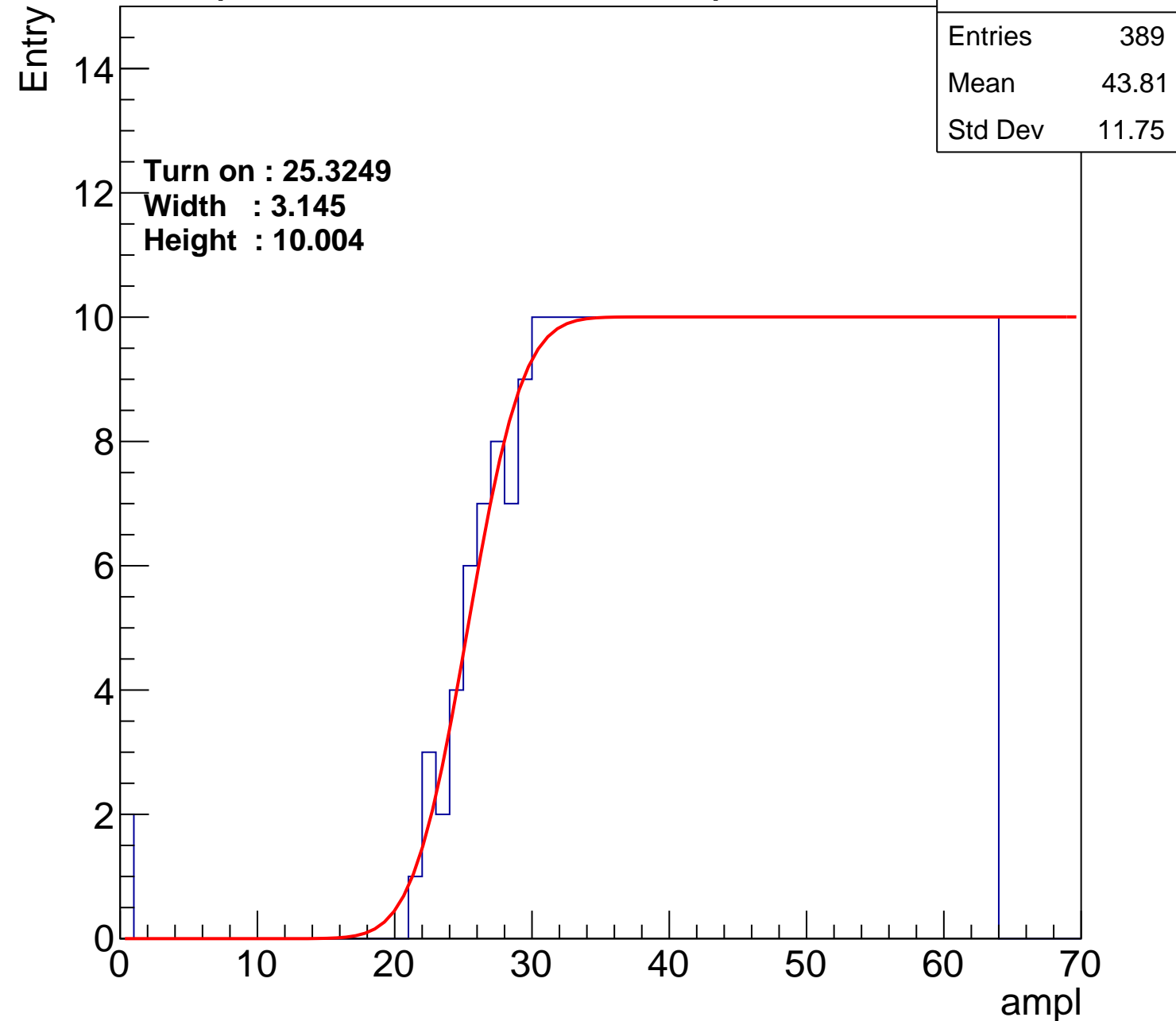
Width : 3.145

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch64

calib_packv5_042523_0143.root, FC#11, port A2

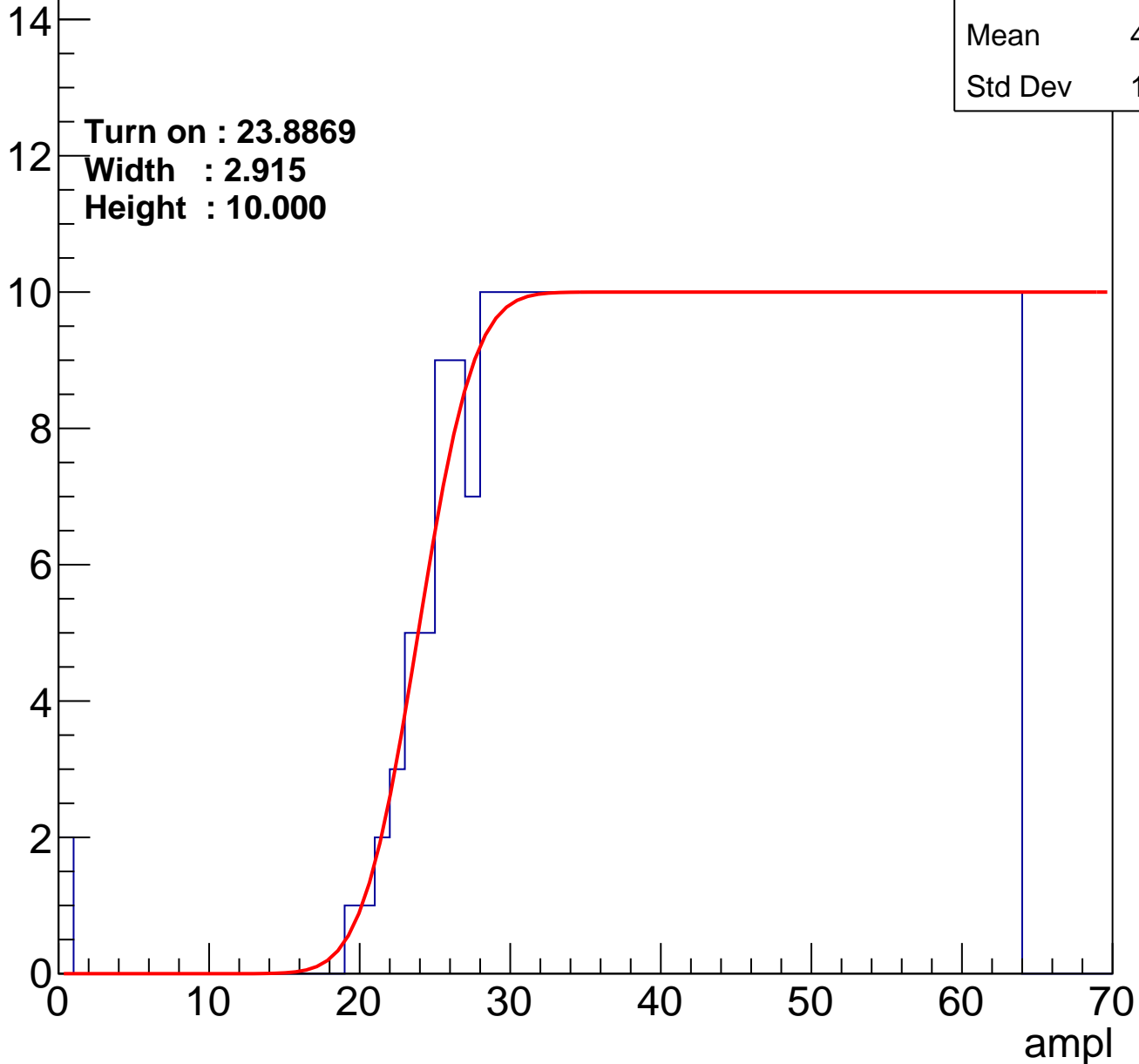
Entries	404
Mean	43.09
Std Dev	12.12

Turn on : 23.8869

Width : 2.915

Height : 10.000

Entry



B1L102S, U15-ch65

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 27.0365

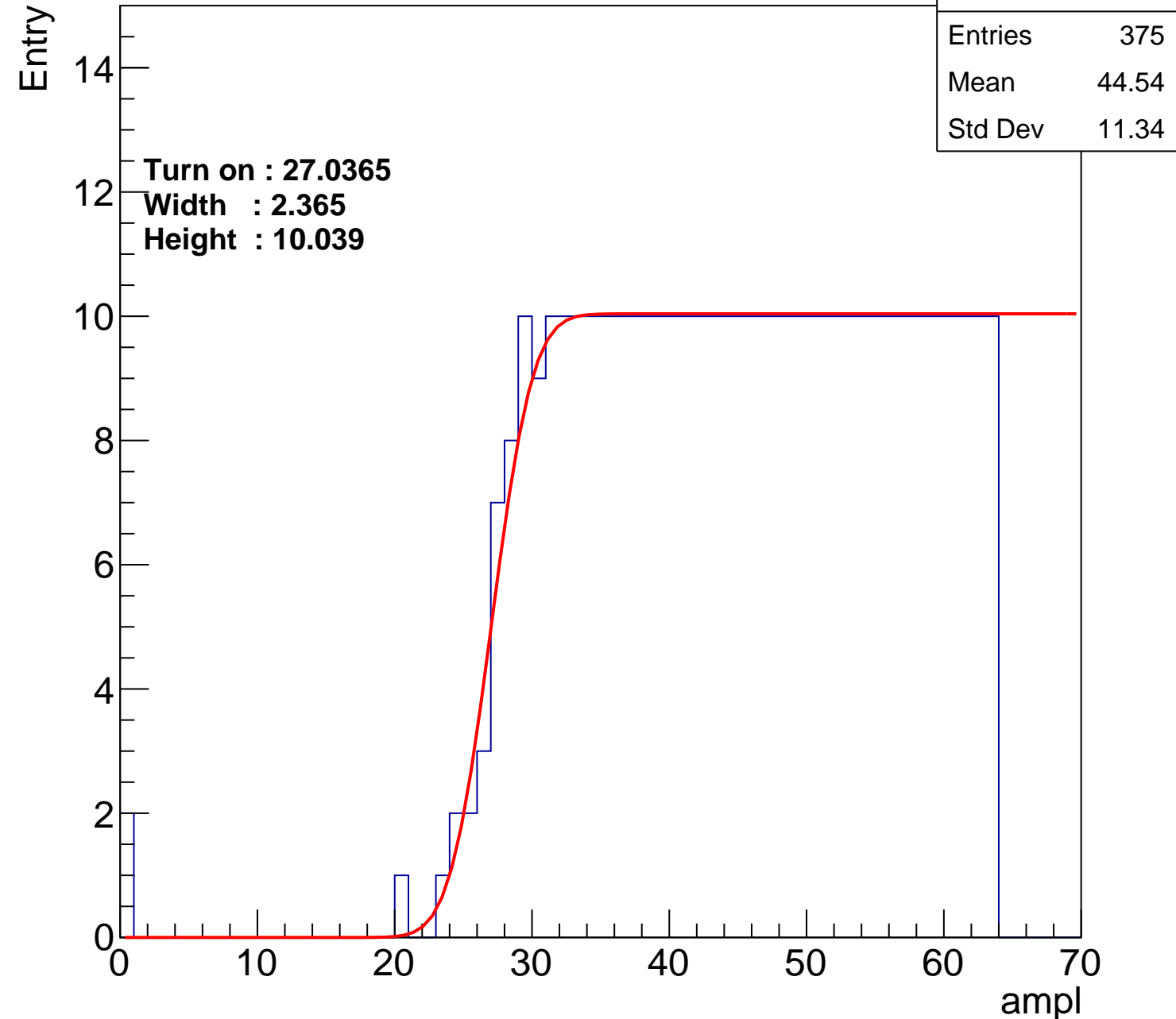
Width : 2.365

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch66

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	42.91
Std Dev	12.52

Turn on : 23.9103

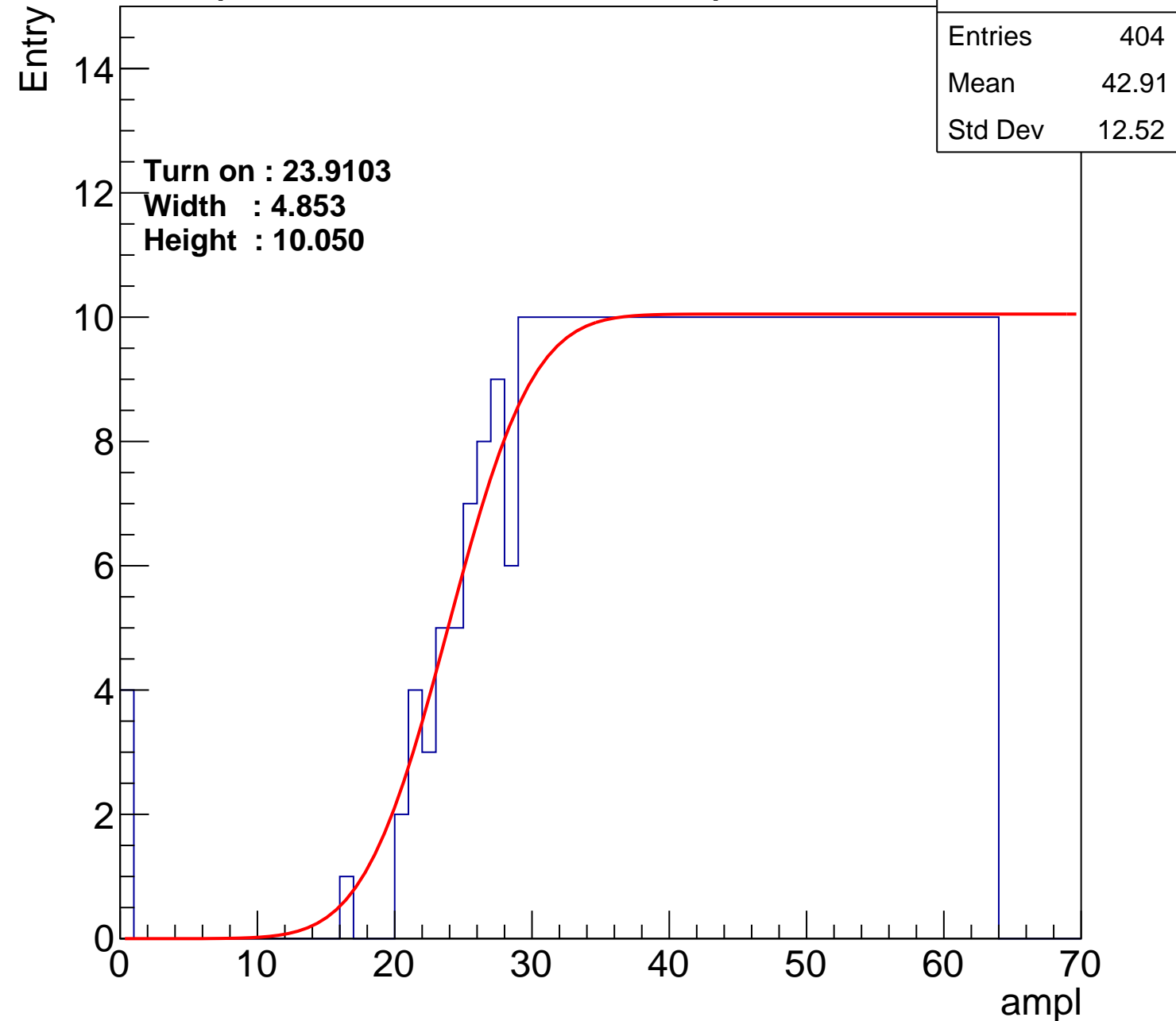
Width : 4.853

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch67

calib_packv5_042523_0143.root, FC#11, port A2

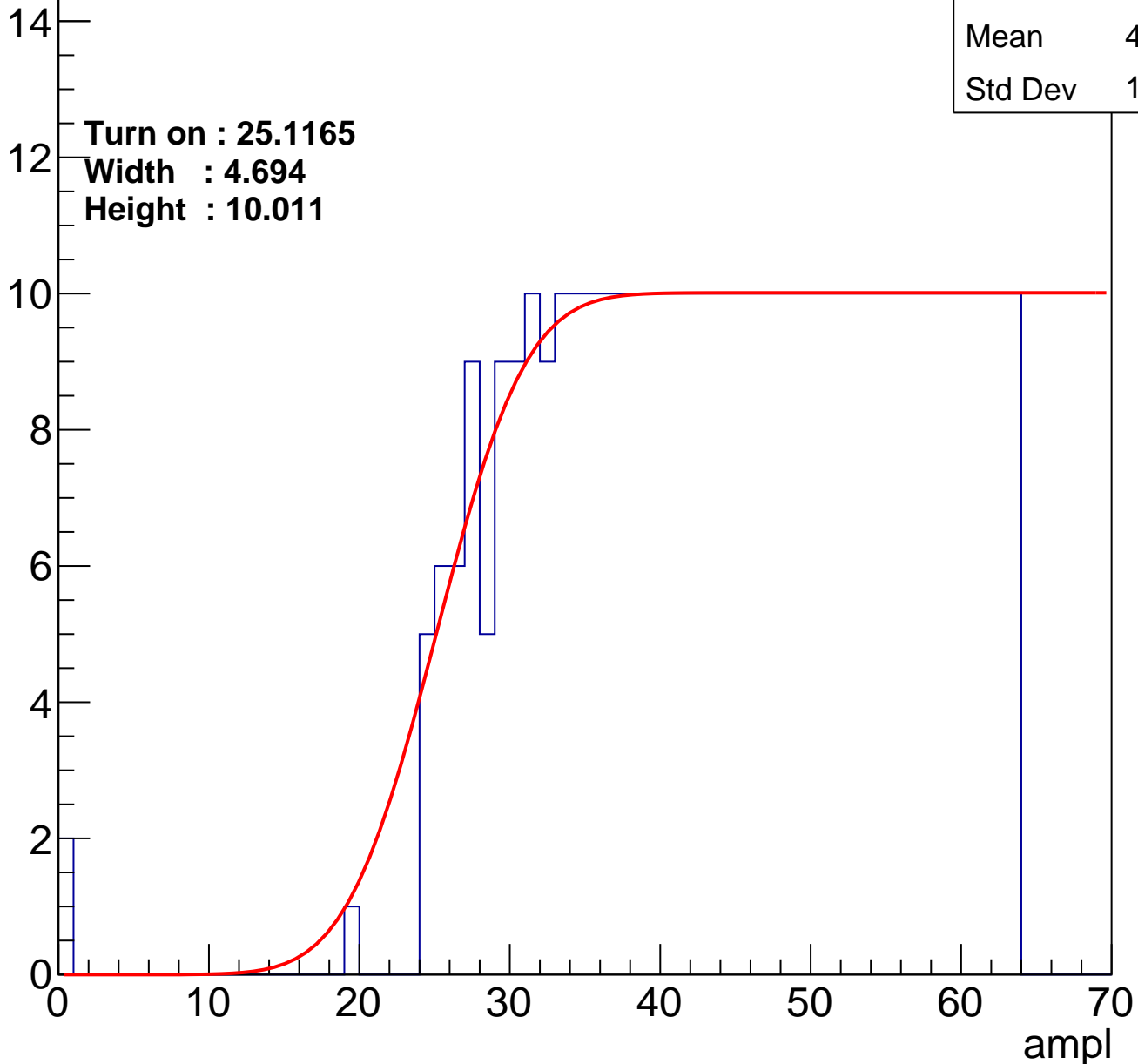
Entries	381
Mean	44.19
Std Dev	11.57

Turn on : 25.1165

Width : 4.694

Height : 10.011

Entry



B1L102S, U15-ch68

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	42.89
Std Dev	12.5

Turn on : 25.1977

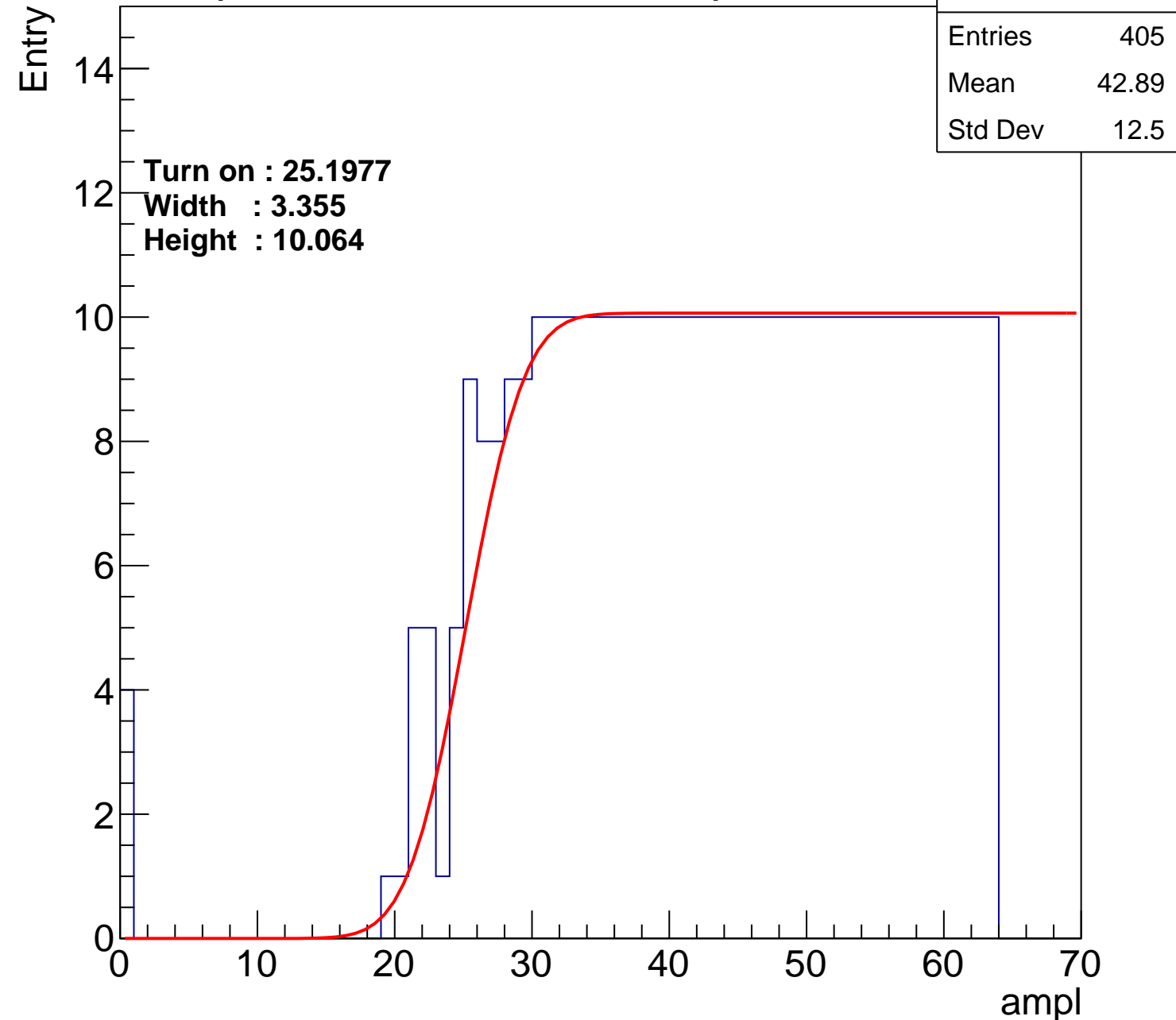
Width : 3.355

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch69

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 26.1167

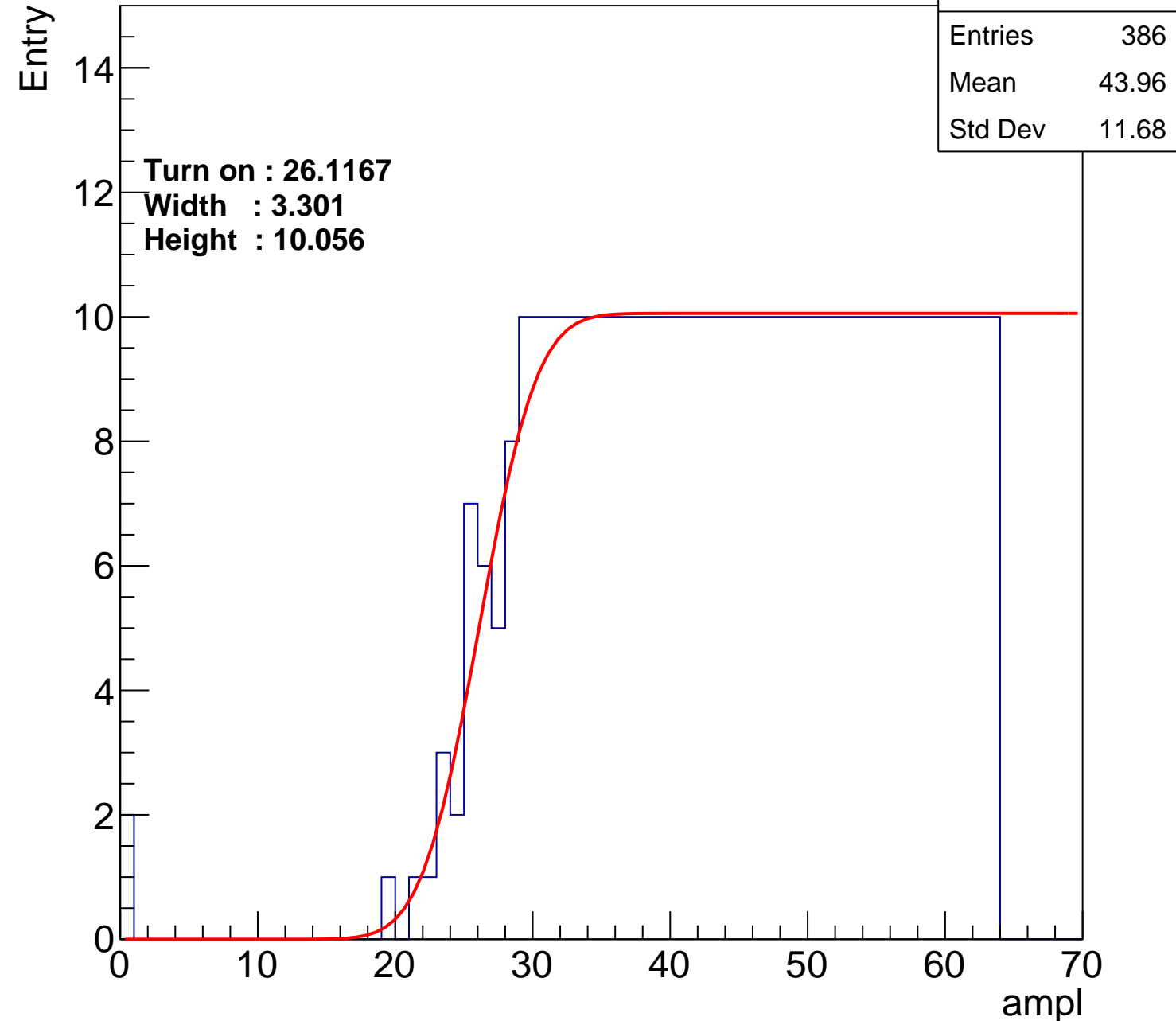
Width : 3.301

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch70

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.91
Std Dev	11.87

Turn on : 25.8972

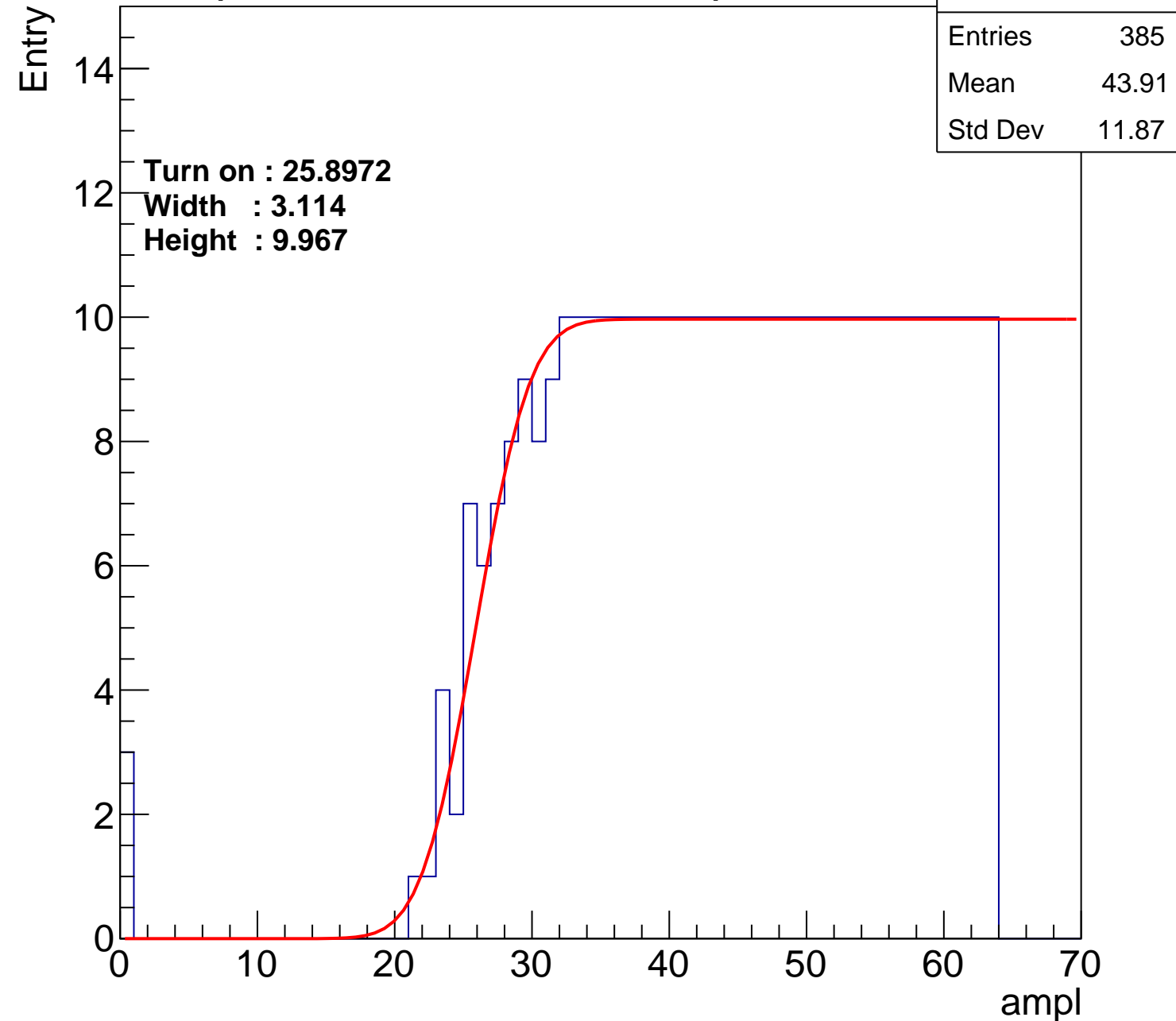
Width : 3.114

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch71

calib_packv5_042523_0143.root, FC#11, port A2

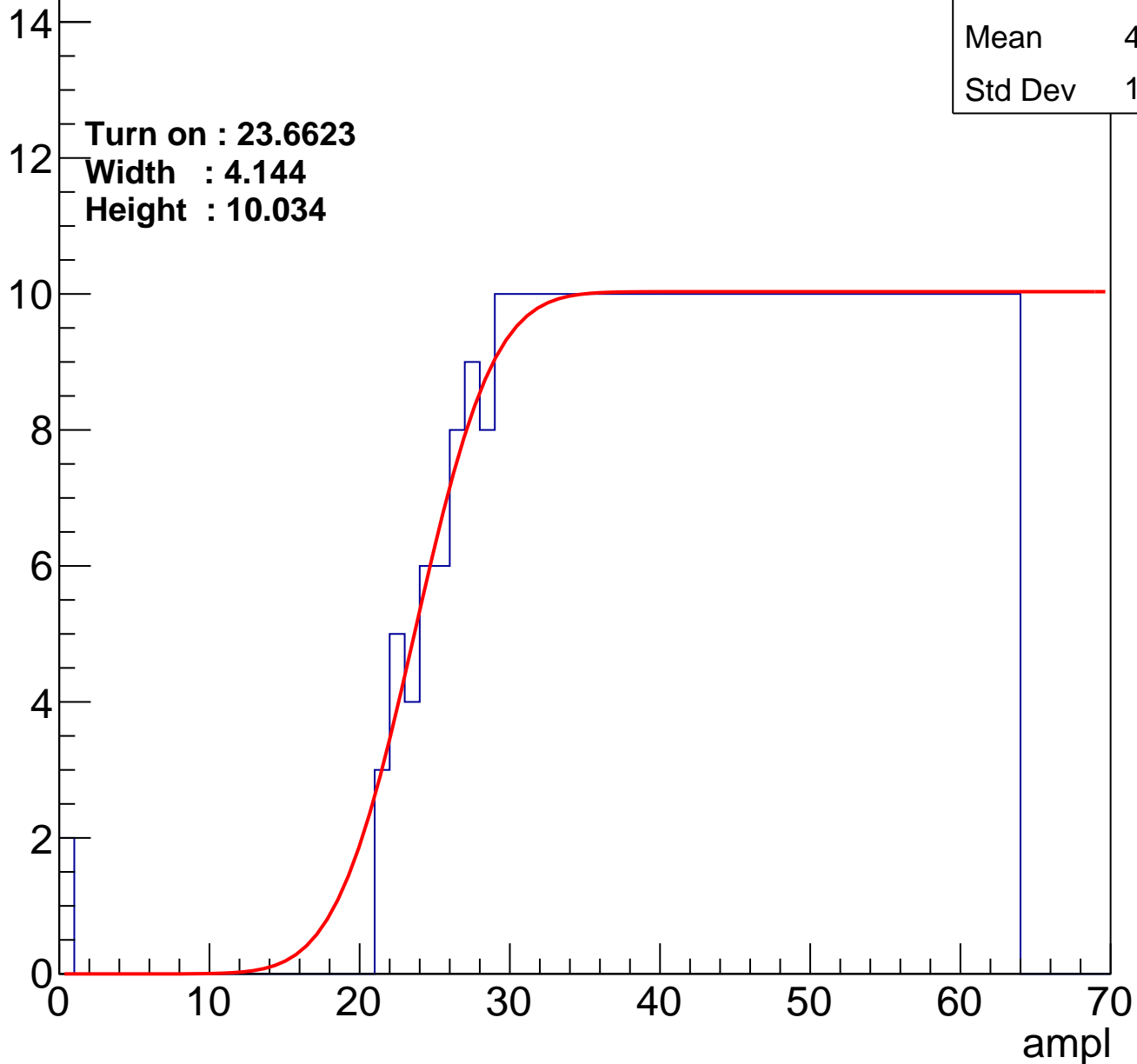
Entries	401
Mean	43.23
Std Dev	12.06

Turn on : 23.6623

Width : 4.144

Height : 10.034

Entry



B1L102S, U15-ch72

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.82
Std Dev	11.98

Turn on : 26.0048

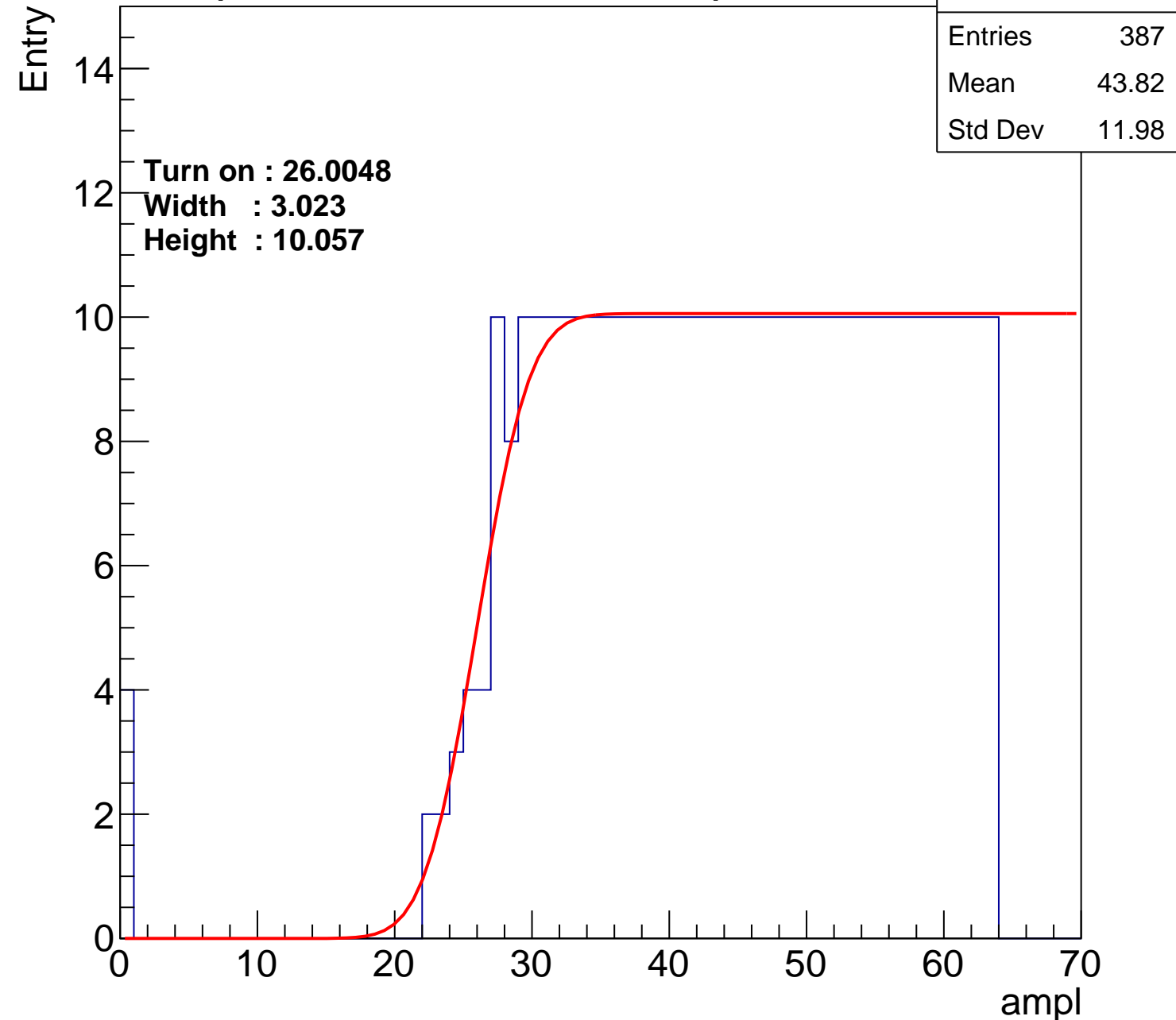
Width : 3.023

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch73

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.45
Std Dev	11.61

Turn on : 27.1641

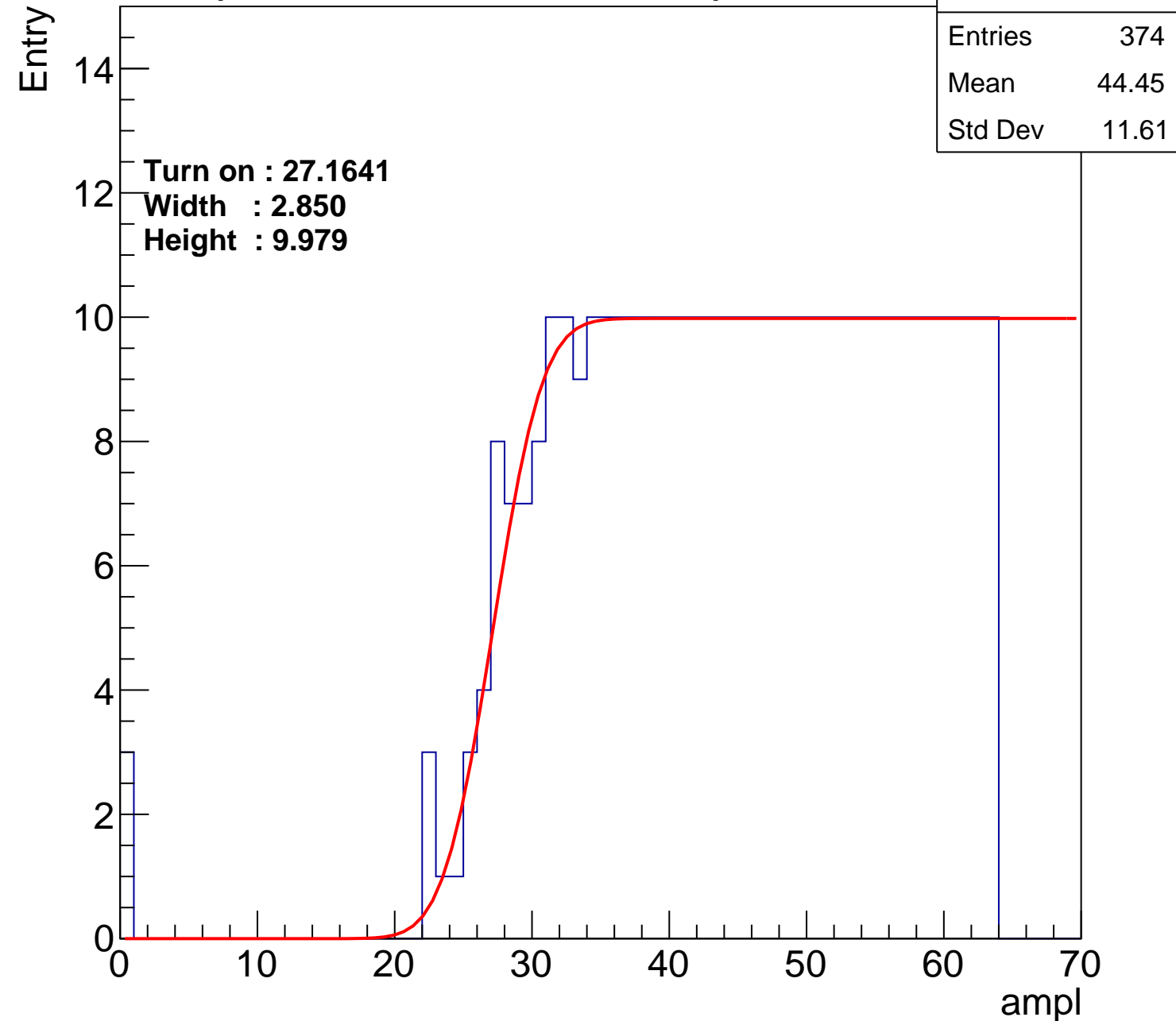
Width : 2.850

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch74

calib_packv5_042523_0143.root, FC#11, port A2

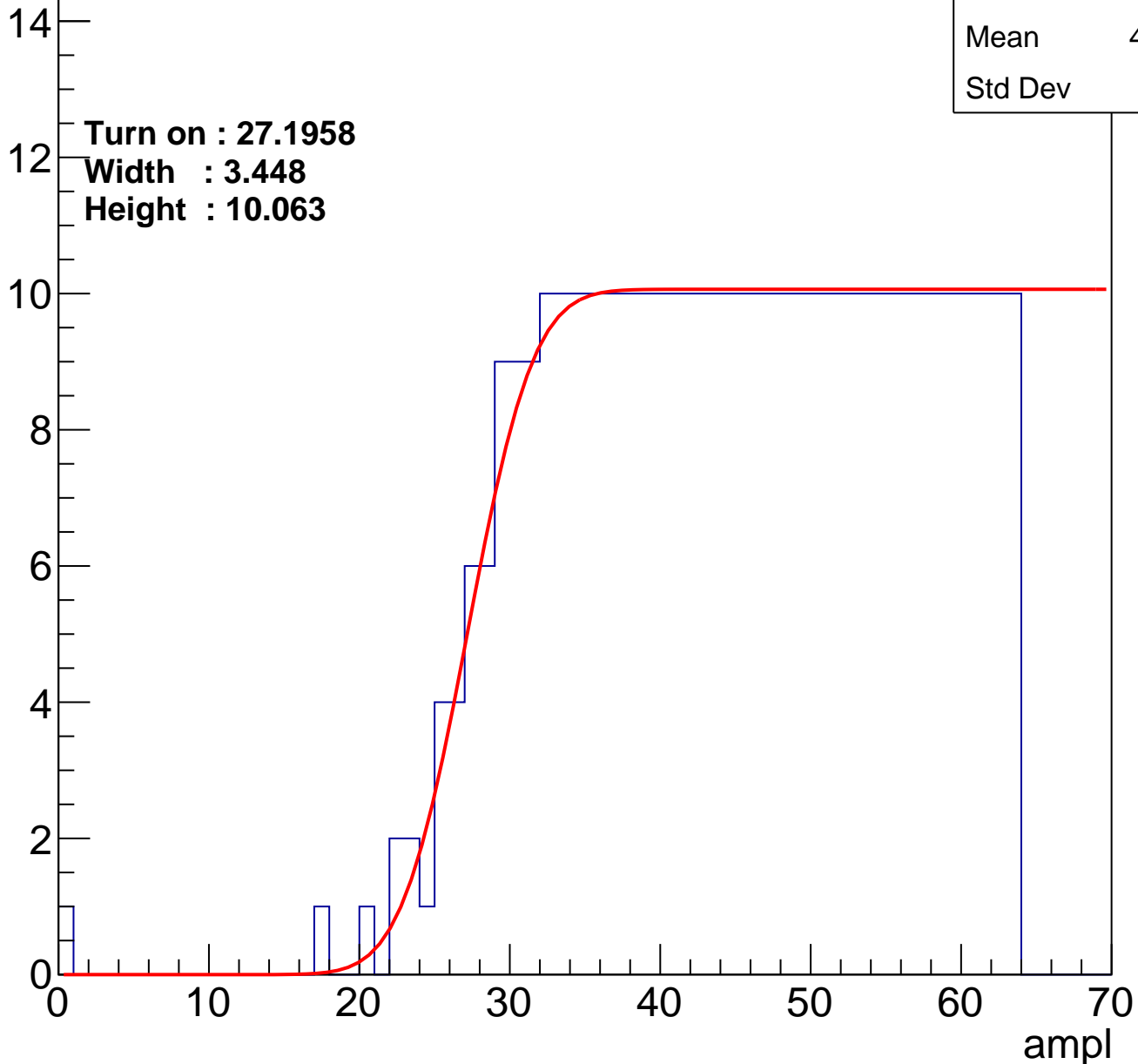
Entries	375
Mean	44.52
Std Dev	11.3

Turn on : 27.1958

Width : 3.448

Height : 10.063

Entry



B1L102S, U15-ch75

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.46
Std Dev	12.46

Turn on : 25.1451

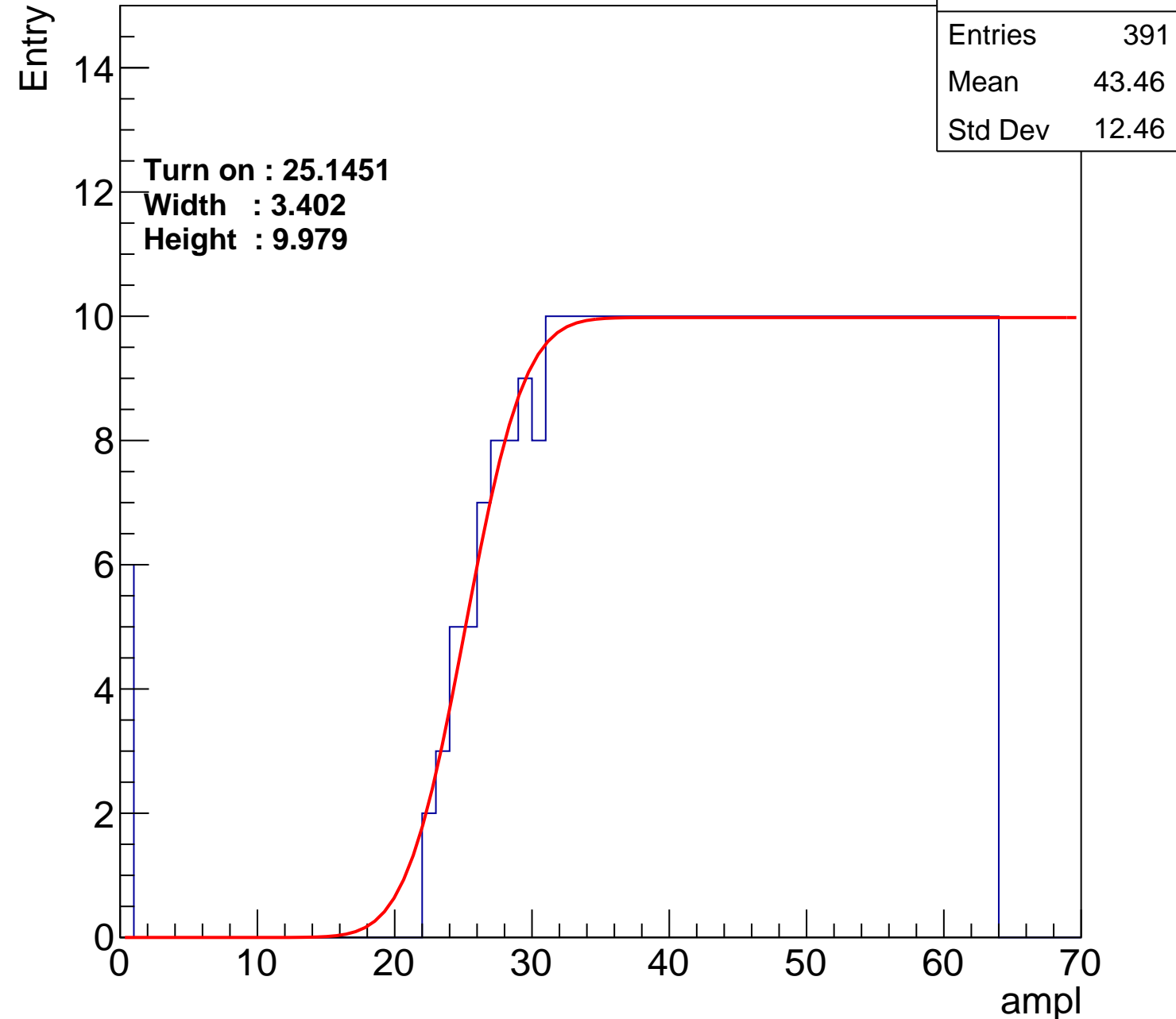
Width : 3.402

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch76

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.74
Std Dev	11.83

Turn on : 25.1399

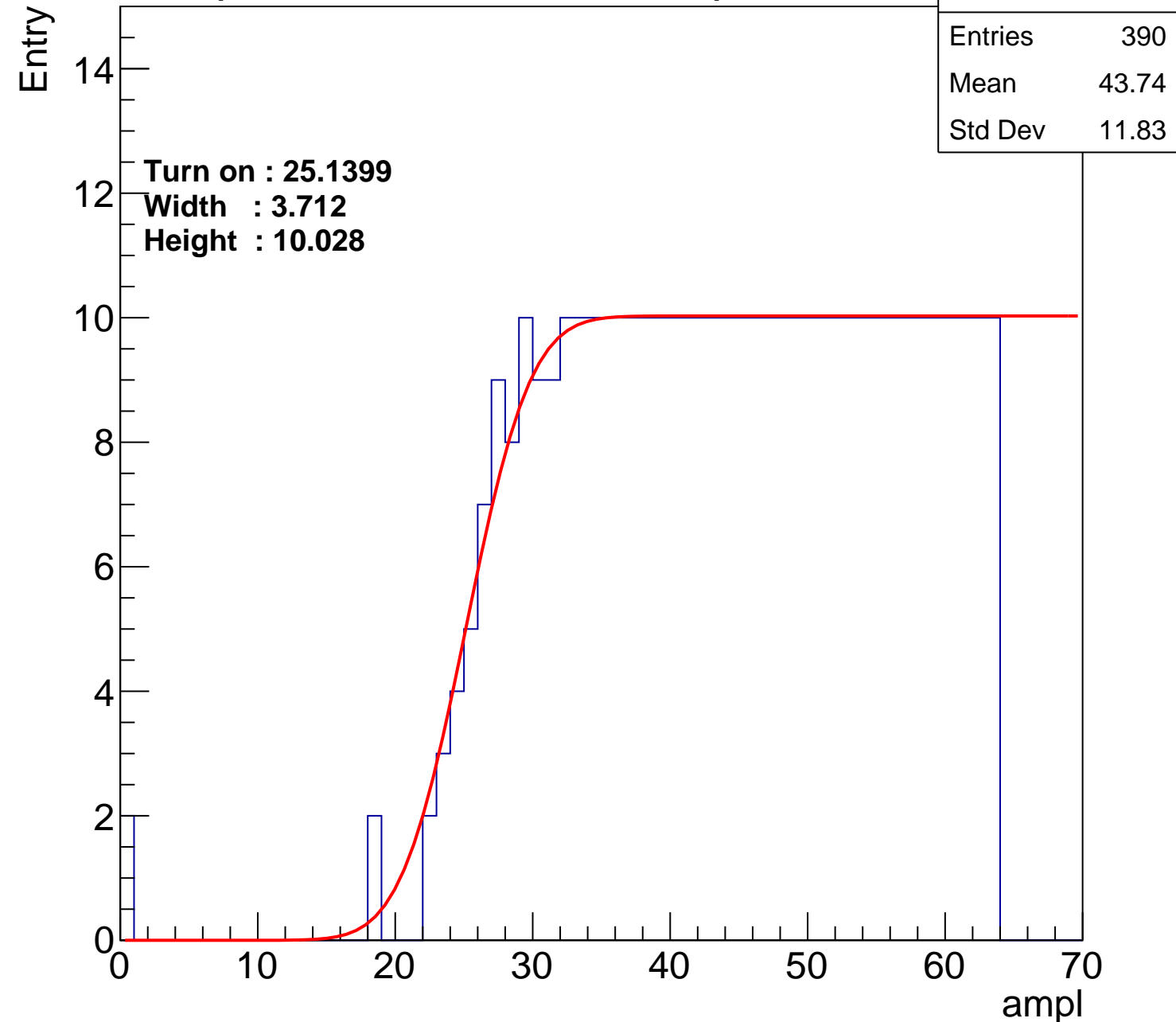
Width : 3.712

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch77

calib_packv5_042523_0143.root, FC#11, port A2

Entries	407
Mean	42.72
Std Dev	12.75

Turn on : 23.9462

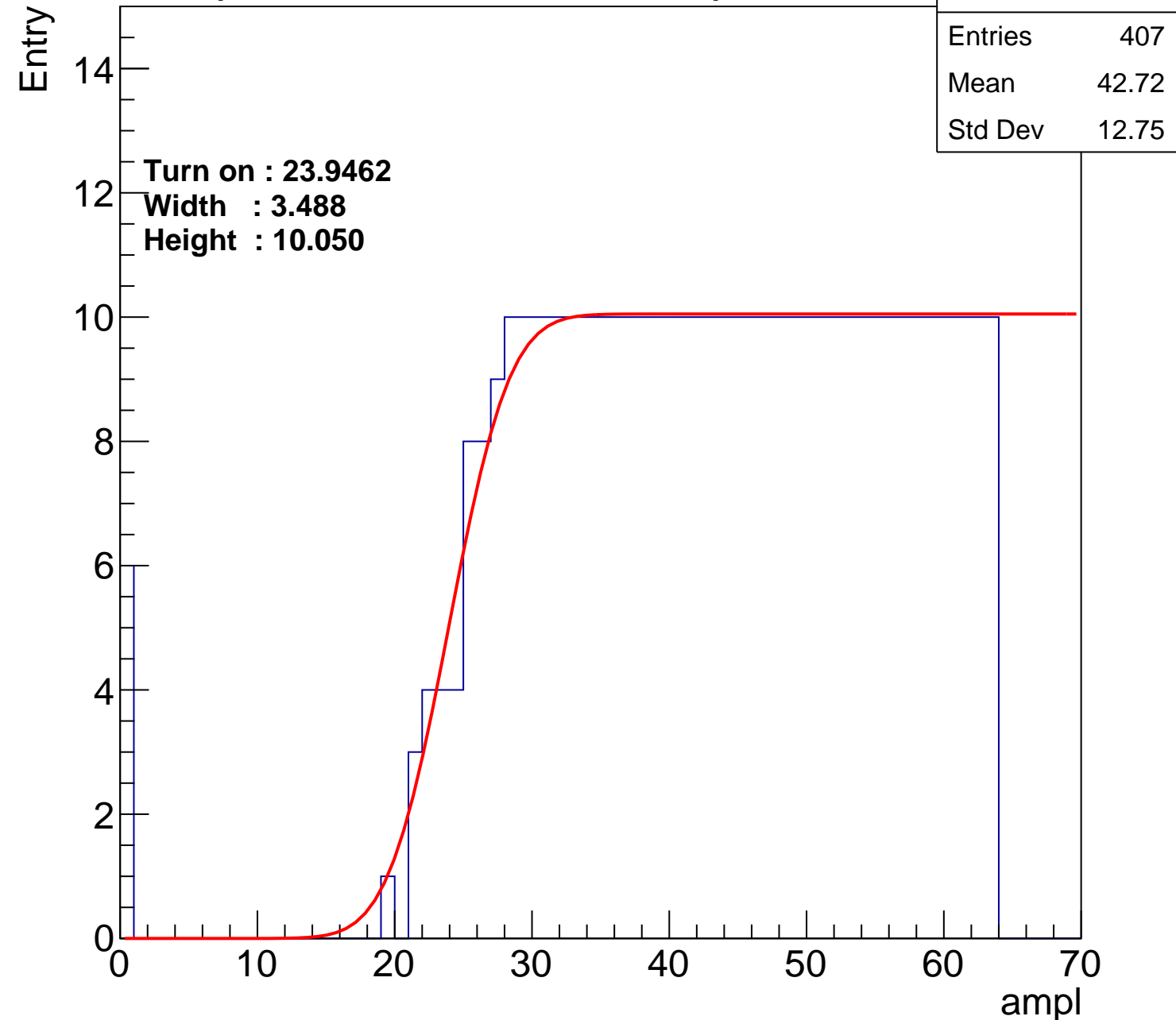
Width : 3.488

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch78

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.6
Std Dev	12.17

Turn on : 25.6007

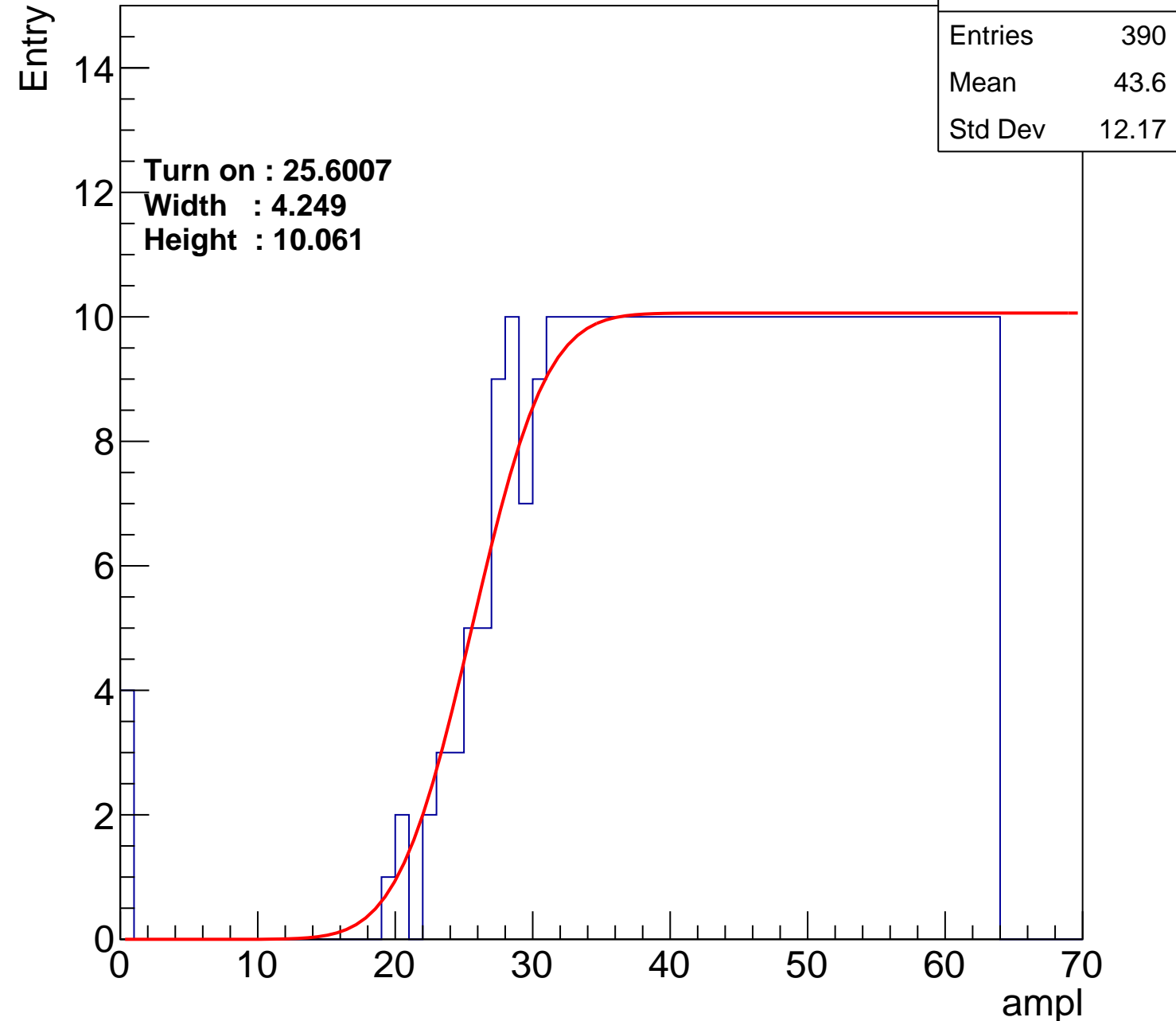
Width : 4.249

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch79

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.98
Std Dev	11.7

Turn on : 25.9255

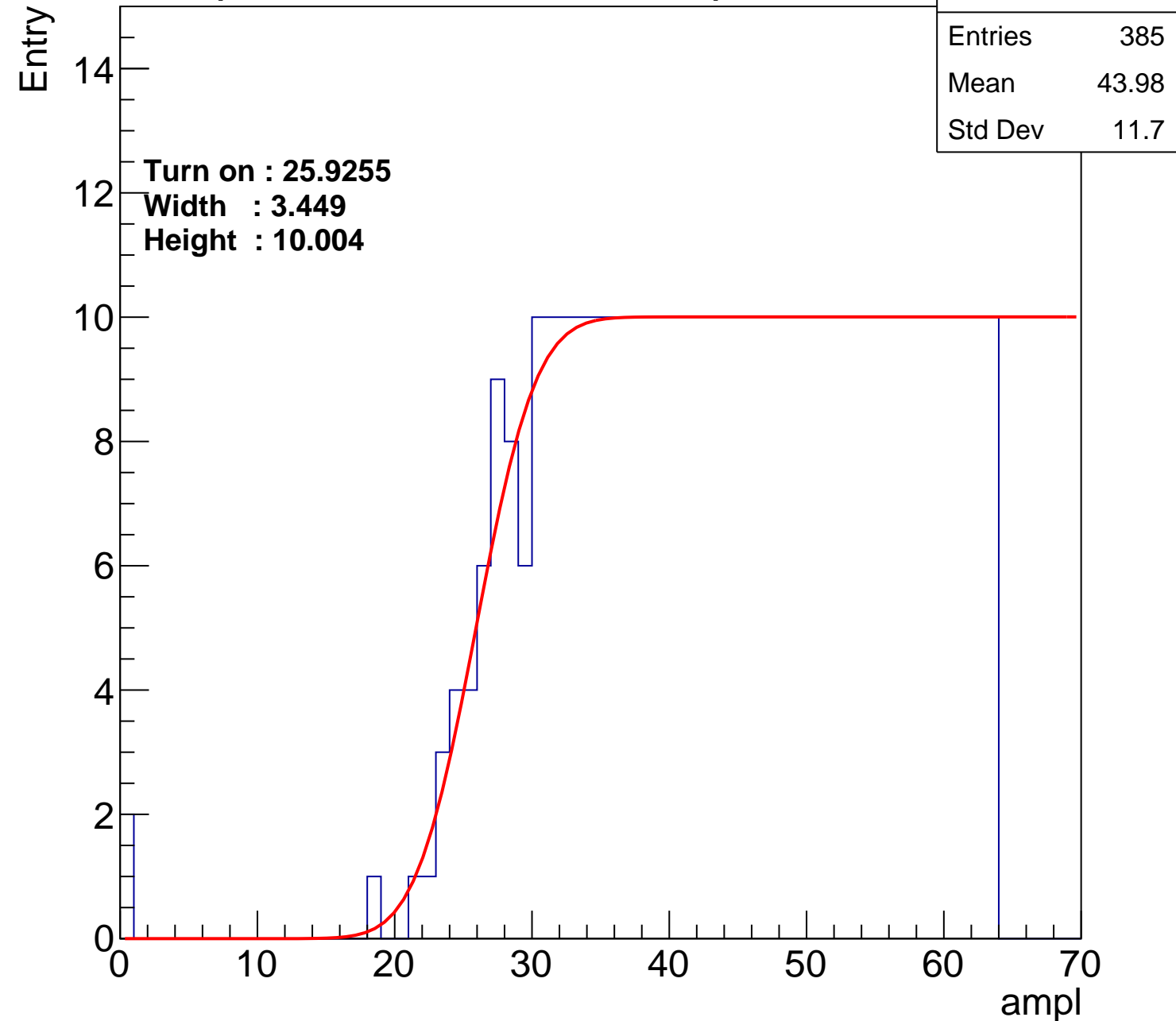
Width : 3.449

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch80

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.48
Std Dev	12.09

Turn on : 25.2768

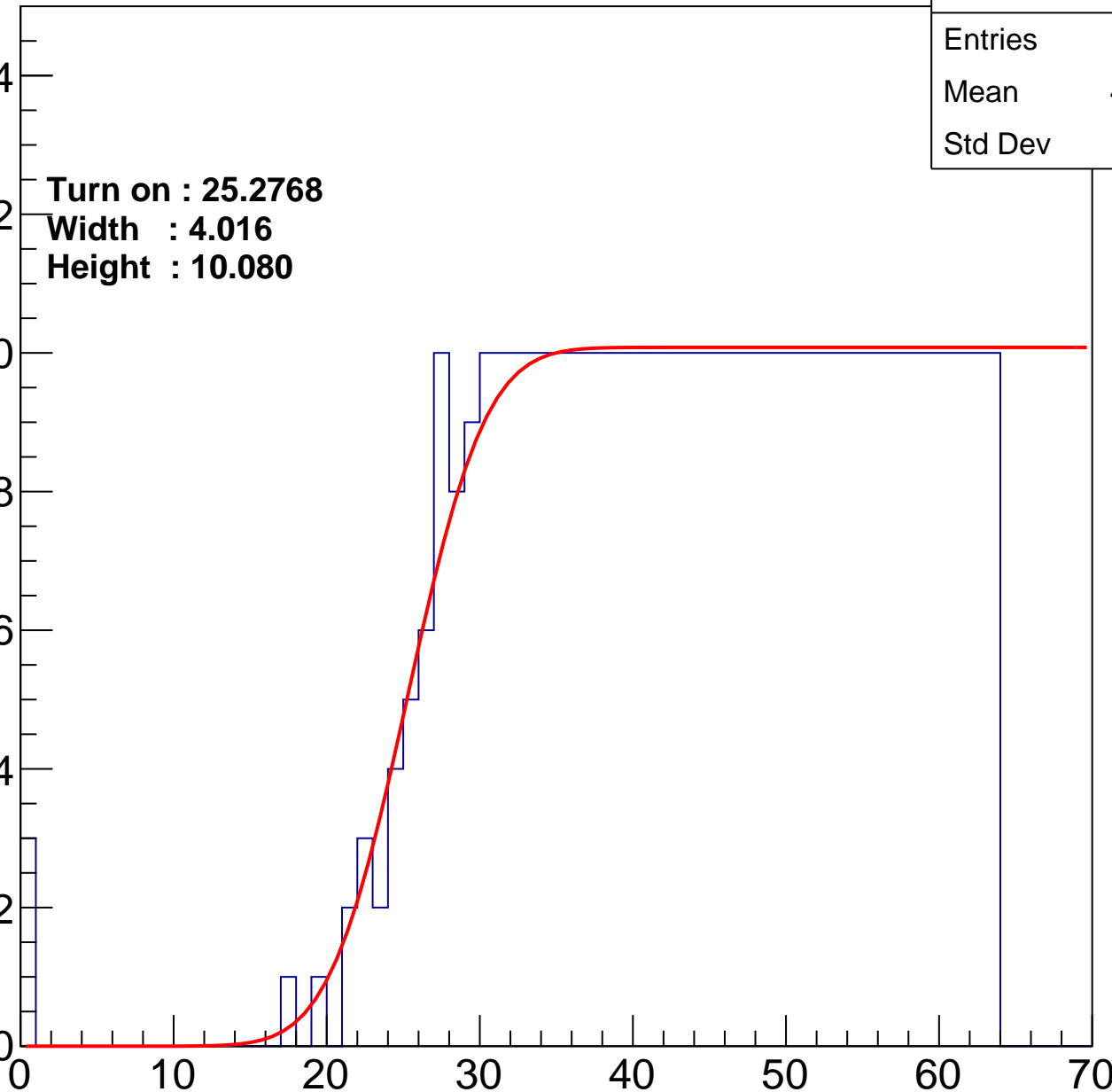
Width : 4.016

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch81

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.81
Std Dev	12.01

Turn on : 25.7420

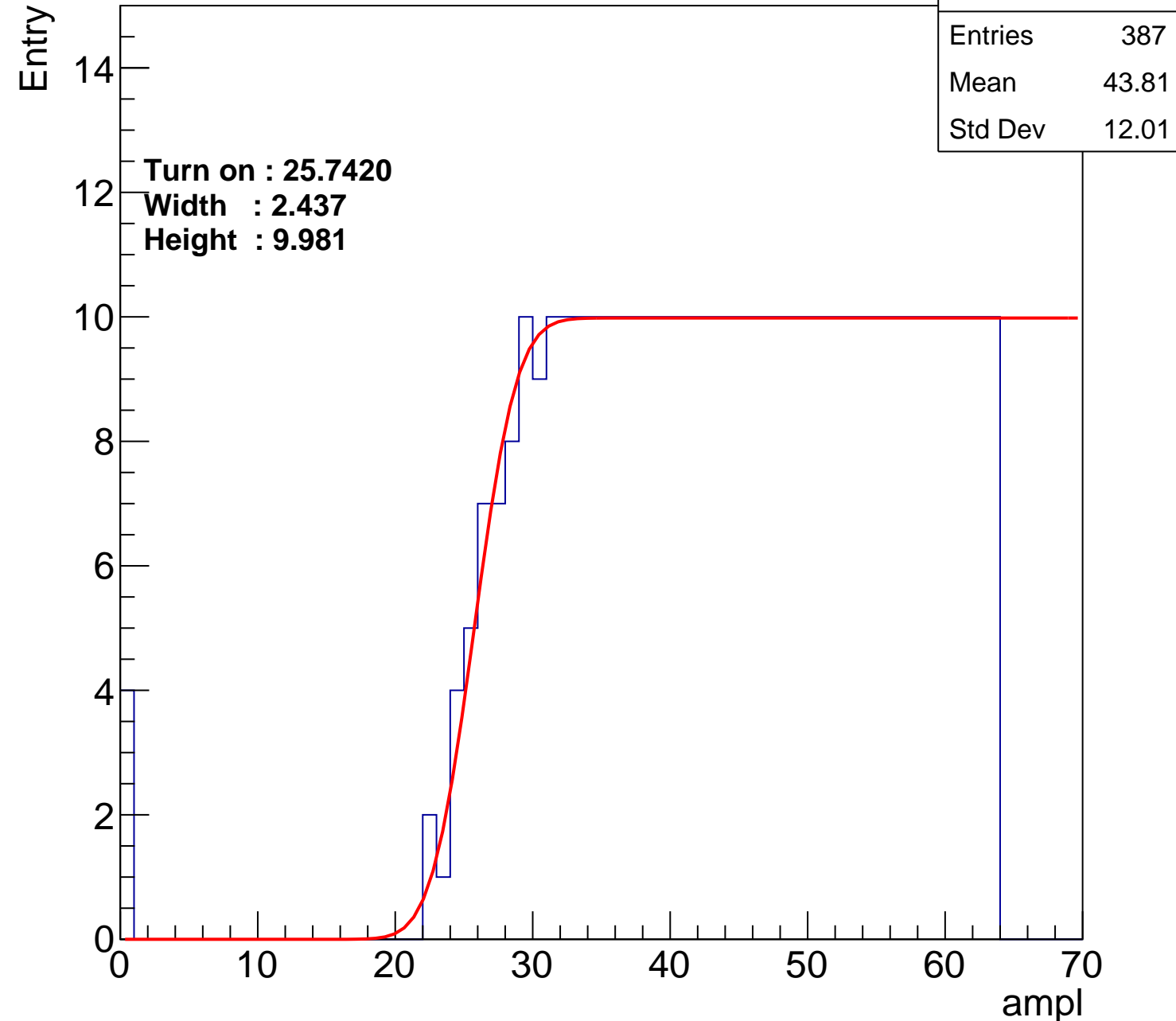
Width : 2.437

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch82

calib_packv5_042523_0143.root, FC#11, port A2

Entries	407
Mean	42.92
Std Dev	12.23

Turn on : 23.0328

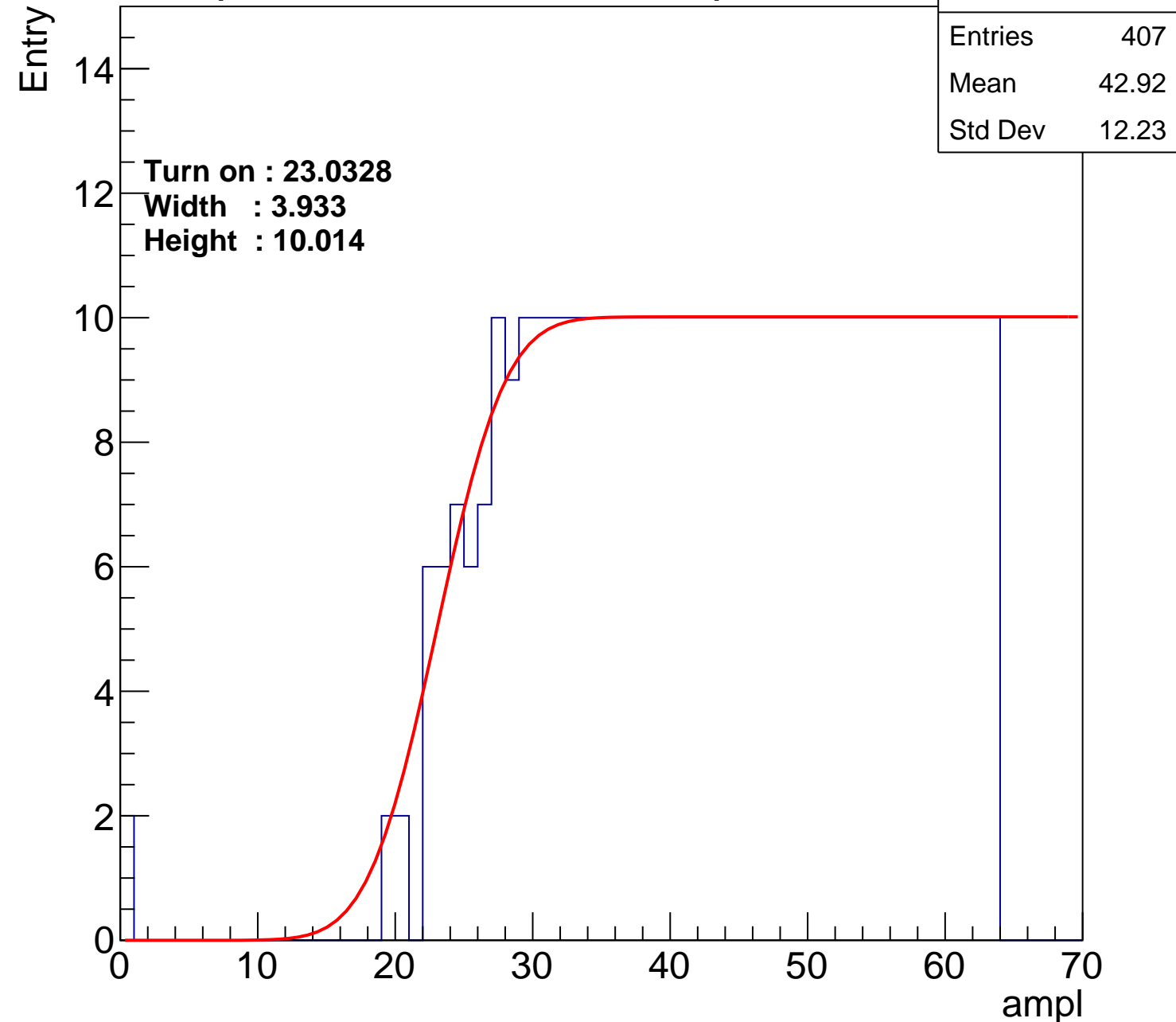
Width : 3.933

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch83

calib_packv5_042523_0143.root, FC#11, port A2

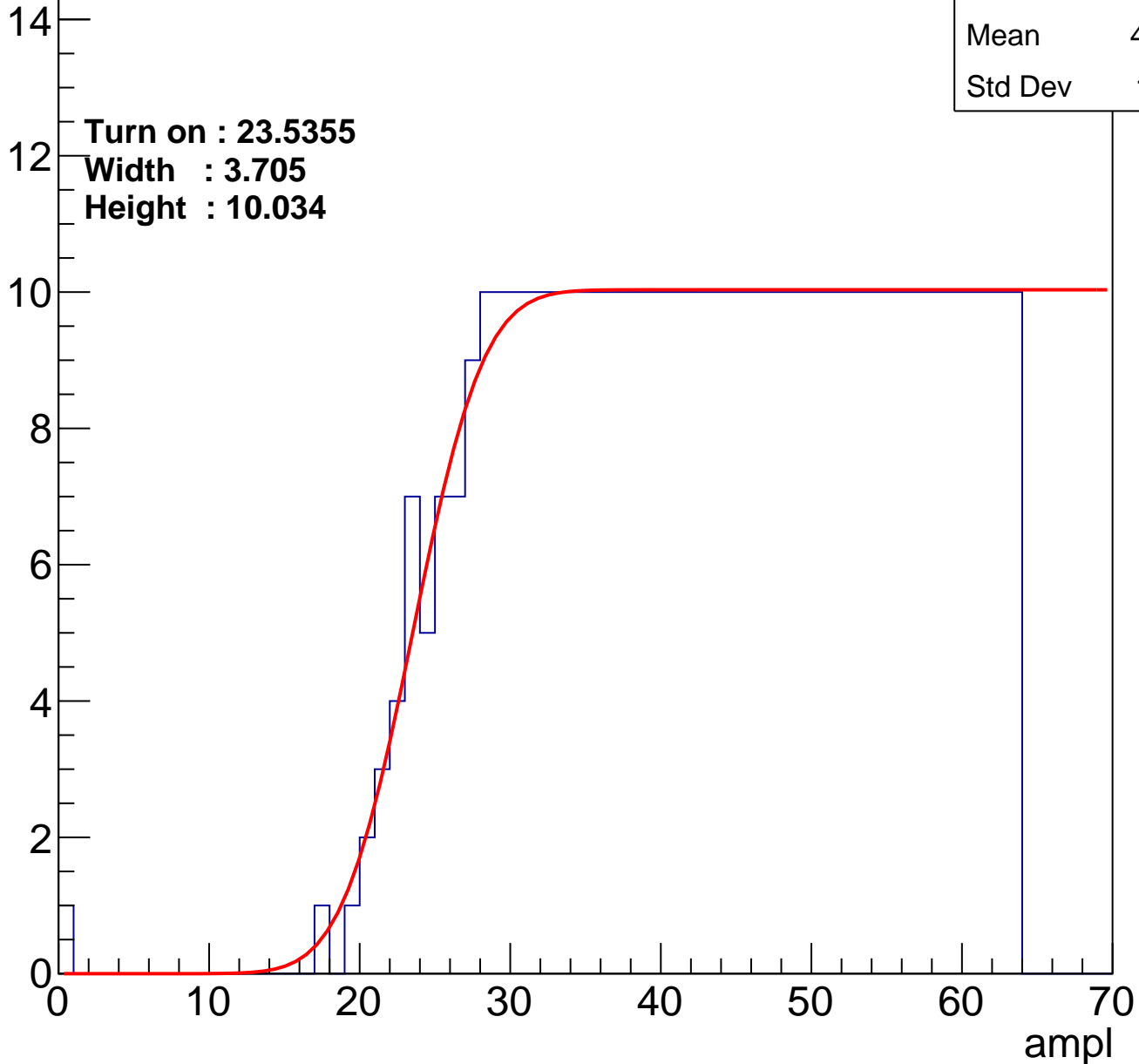
Entries	407
Mean	42.97
Std Dev	12.11

Turn on : 23.5355

Width : 3.705

Height : 10.034

Entry



B1L102S, U15-ch84

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.41
Std Dev	12.48

Turn on : 25.4191

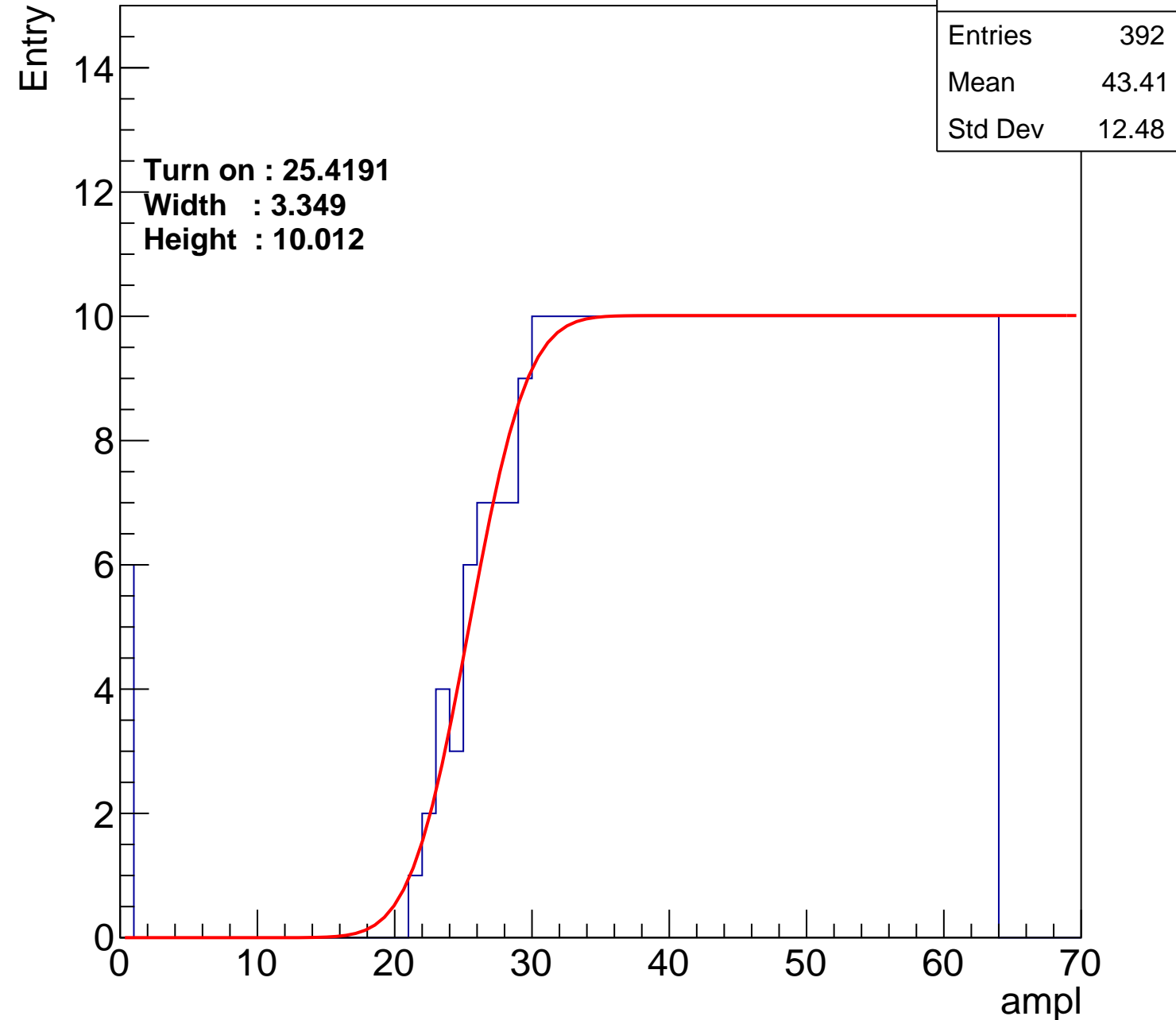
Width : 3.349

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch85

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.77
Std Dev	11.91

Turn on : 26.0978

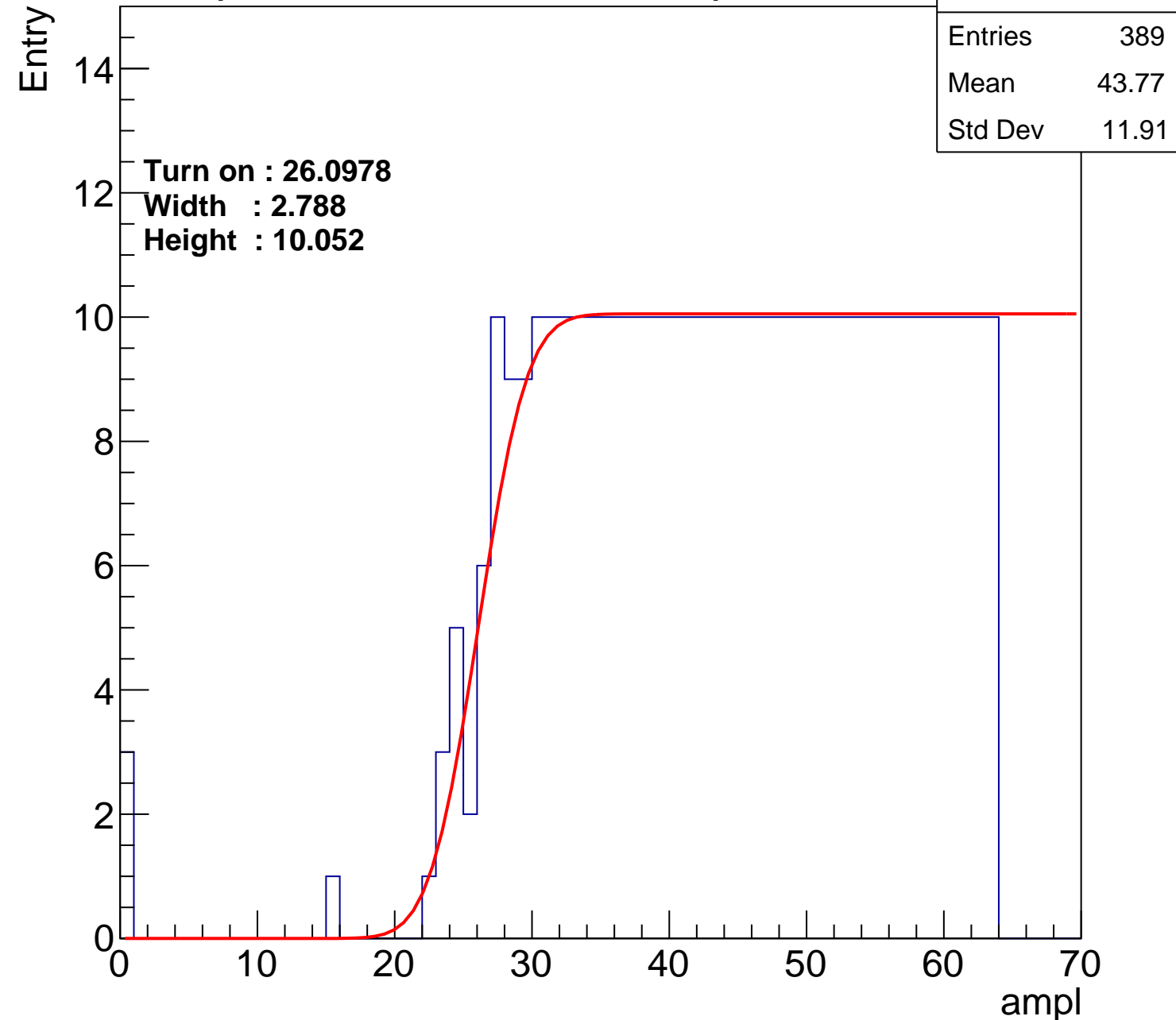
Width : 2.788

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch86

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.59
Std Dev	11.24

Turn on : 27.4327

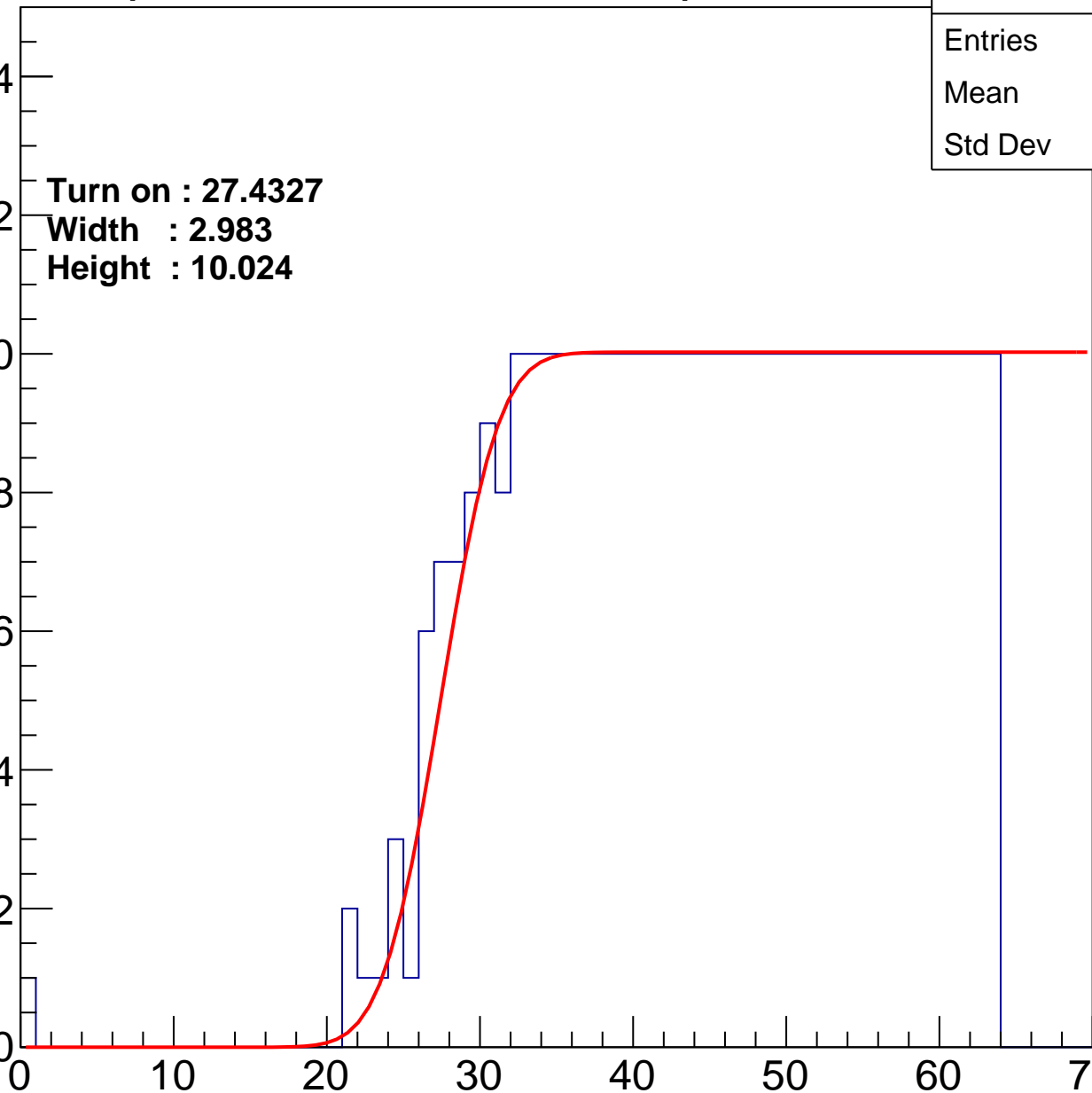
Width : 2.983

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch87

calib_packv5_042523_0143.root, FC#11, port A2

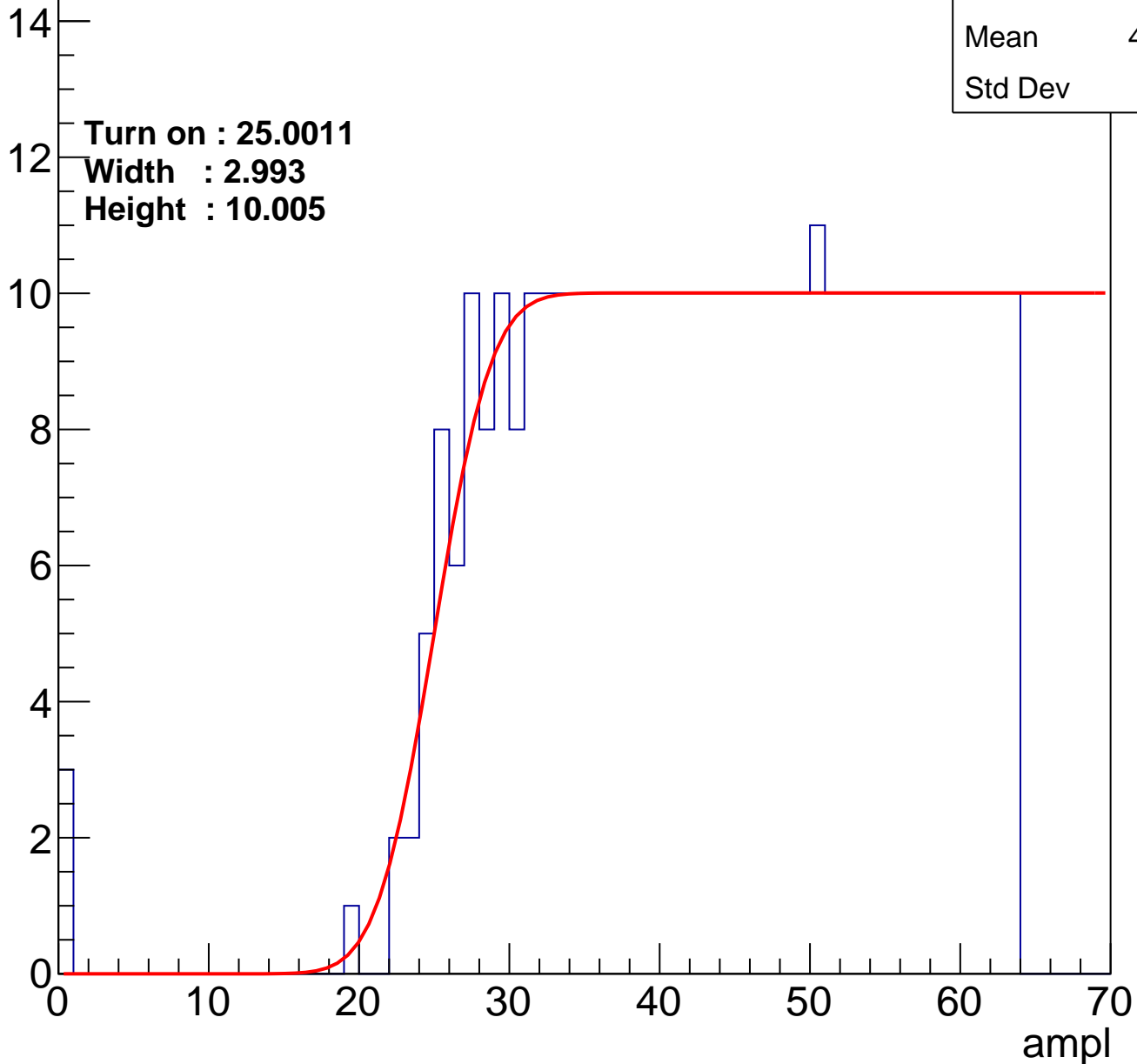
Entries	394
Mean	43.58
Std Dev	12

Turn on : 25.0011

Width : 2.993

Height : 10.005

Entry



B1L102S, U15-ch88

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.74
Std Dev	11.7

Turn on : 25.2661

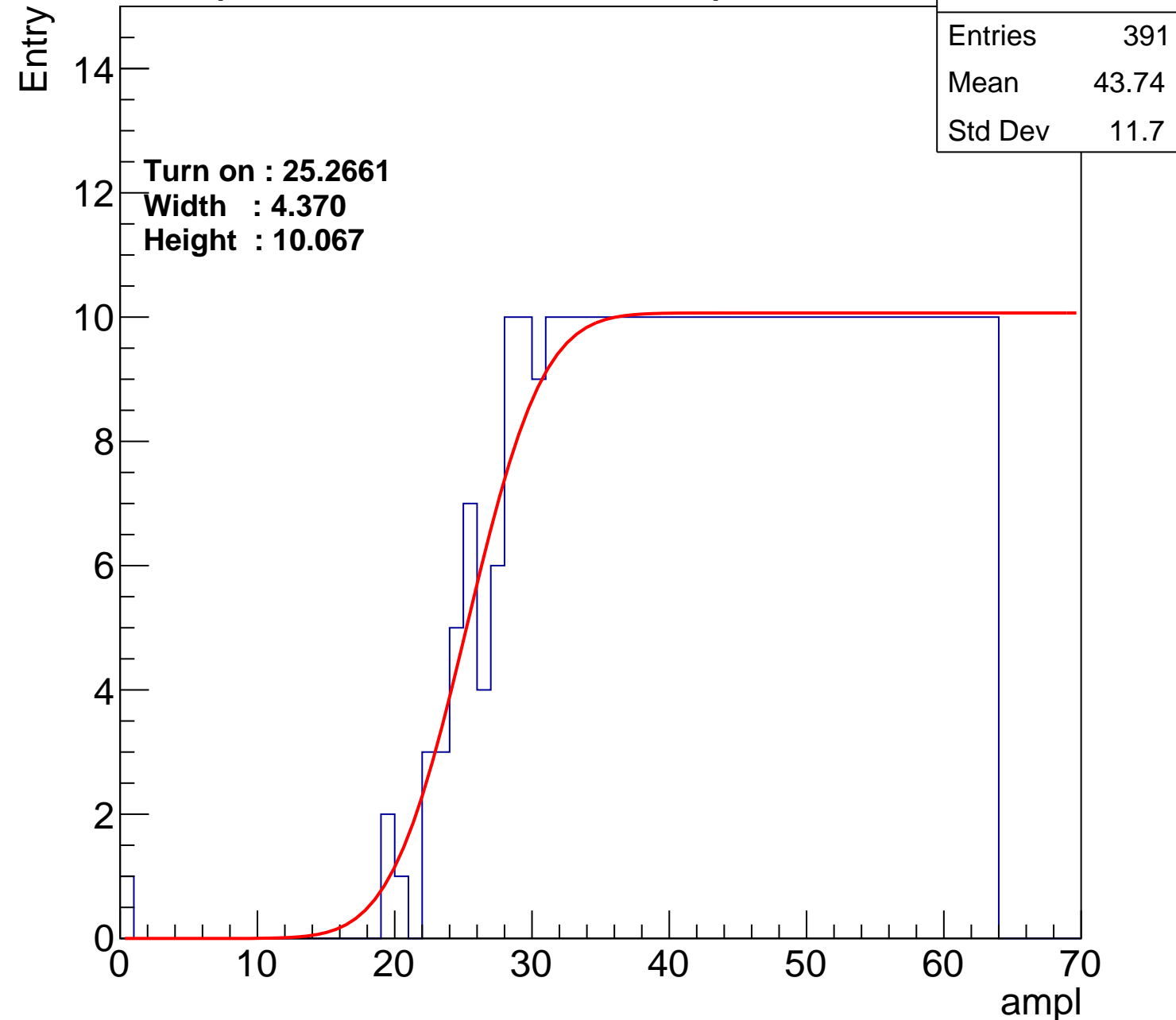
Width : 4.370

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch89

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.81
Std Dev	12.17

Turn on : 26.3592

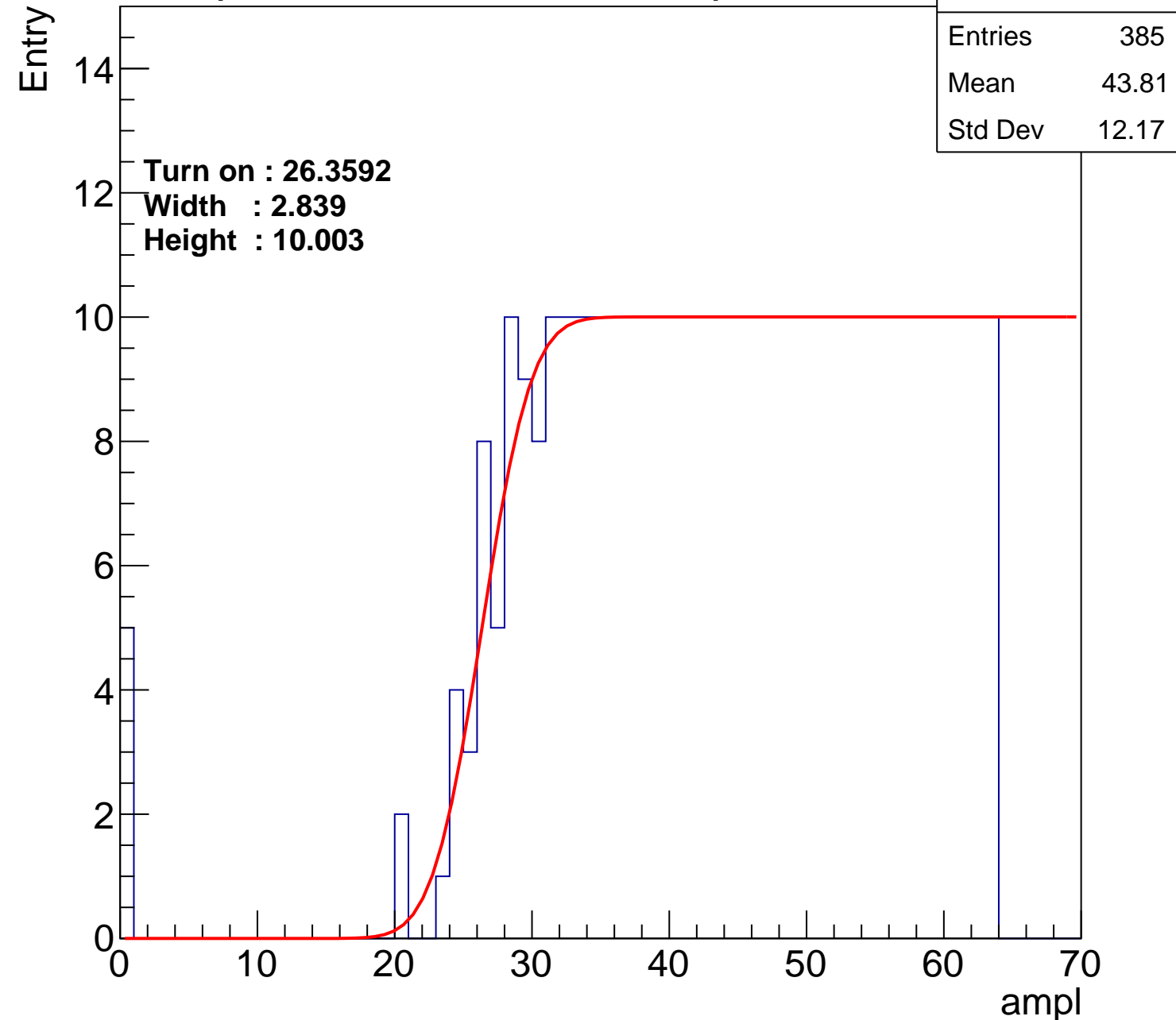
Width : 2.839

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch90

calib_packv5_042523_0143.root, FC#11, port A2

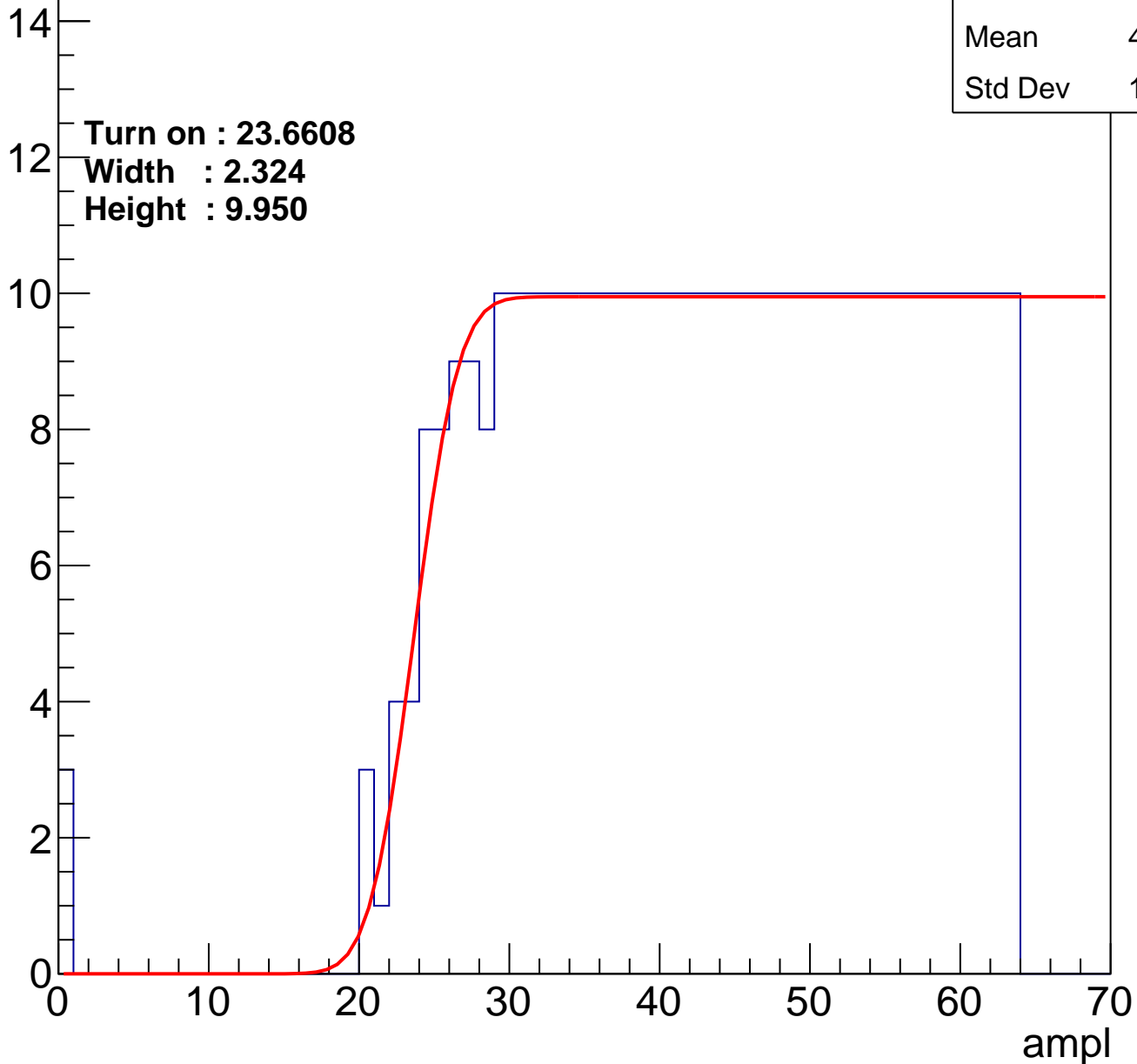
Entries	407
Mean	42.88
Std Dev	12.34

Turn on : 23.6608

Width : 2.324

Height : 9.950

Entry



B1L102S, U15-ch91

calib_packv5_042523_0143.root, FC#11, port A2

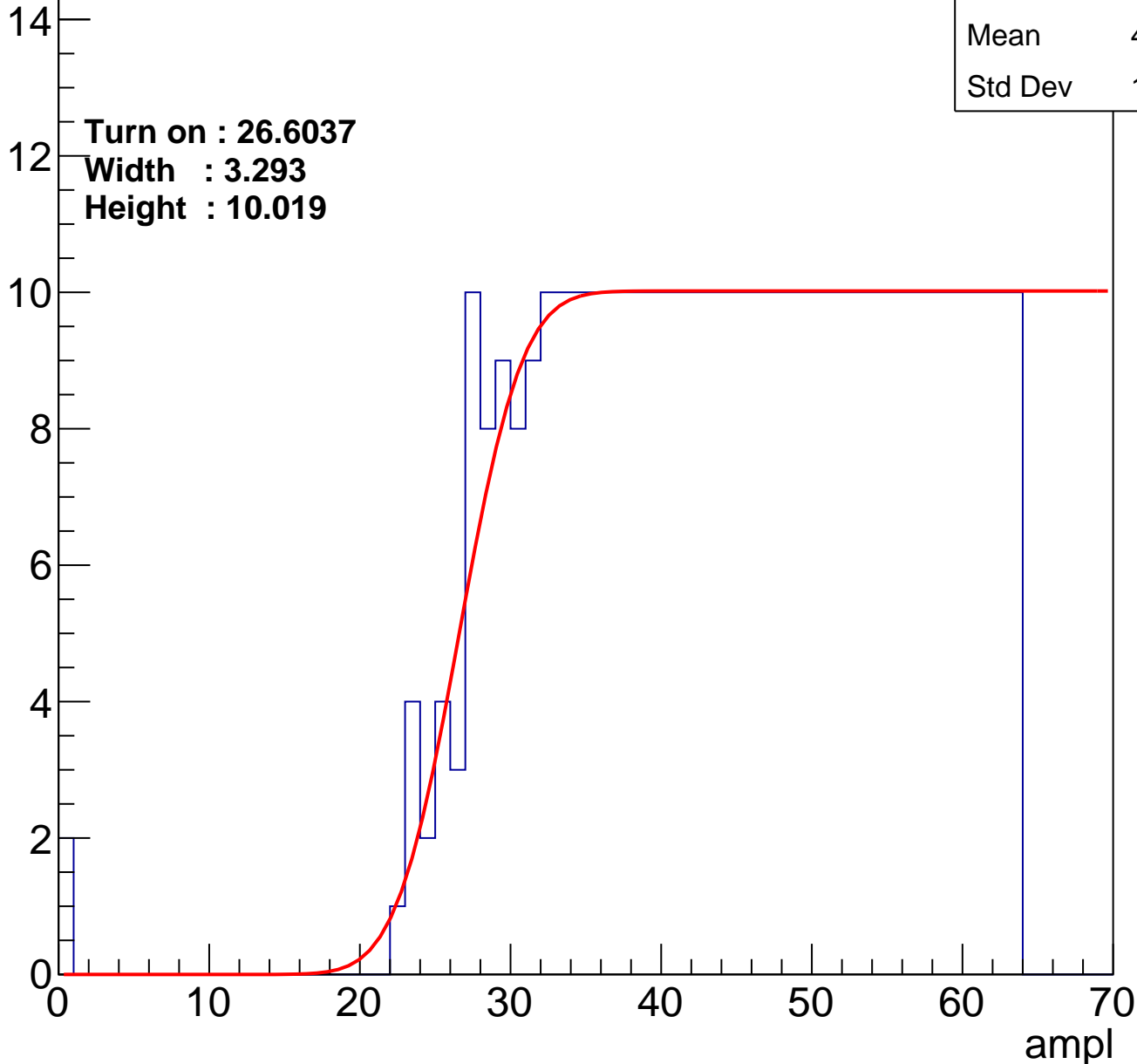
Entries	380
Mean	44.25
Std Dev	11.54

Turn on : 26.6037

Width : 3.293

Height : 10.019

Entry



B1L102S, U15-ch92

calib_packv5_042523_0143.root, FC#11, port A2

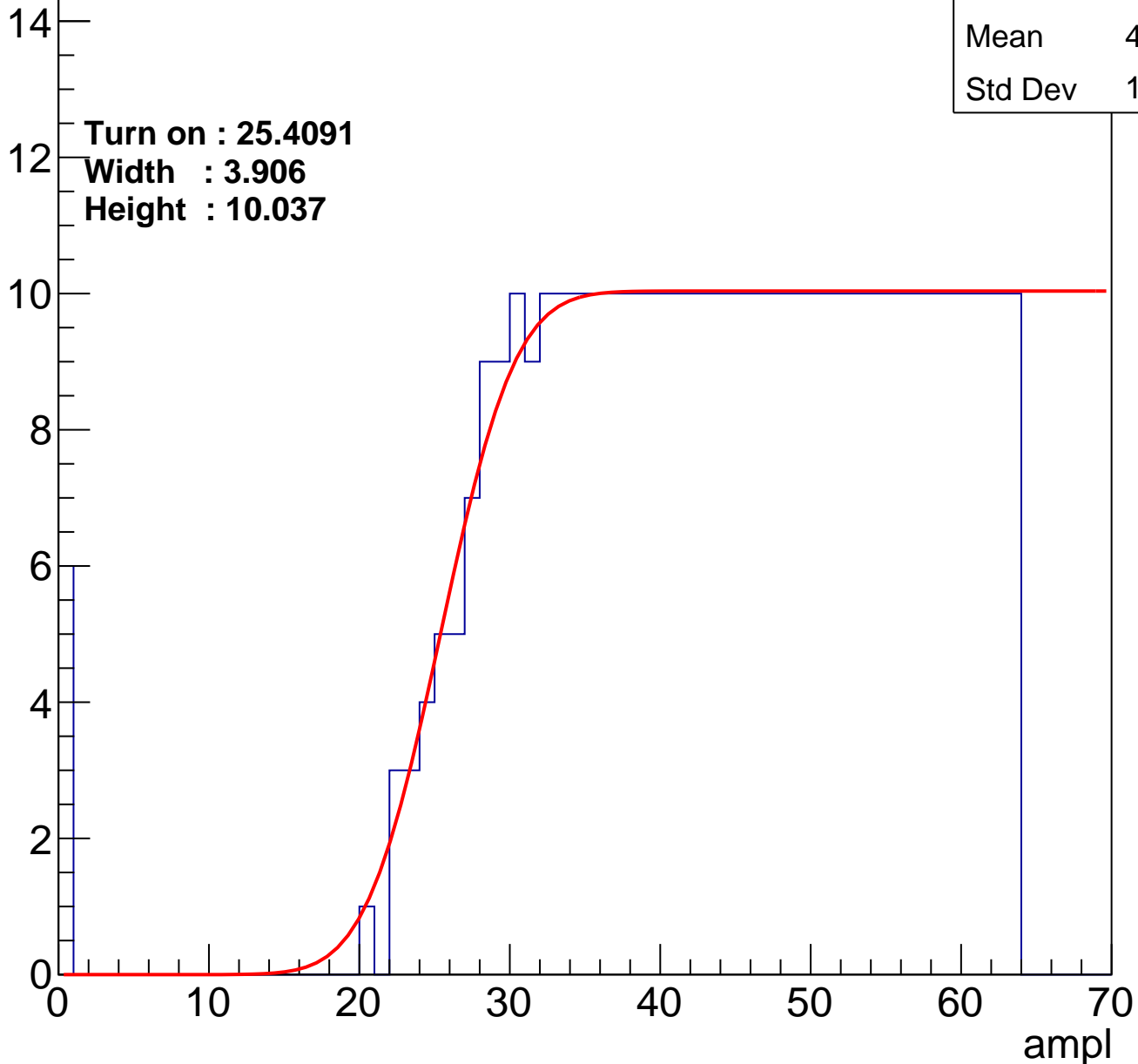
Entries	391
Mean	43.45
Std Dev	12.48

Turn on : 25.4091

Width : 3.906

Height : 10.037

Entry



B1L102S, U15-ch93

calib_packv5_042523_0143.root, FC#11, port A2

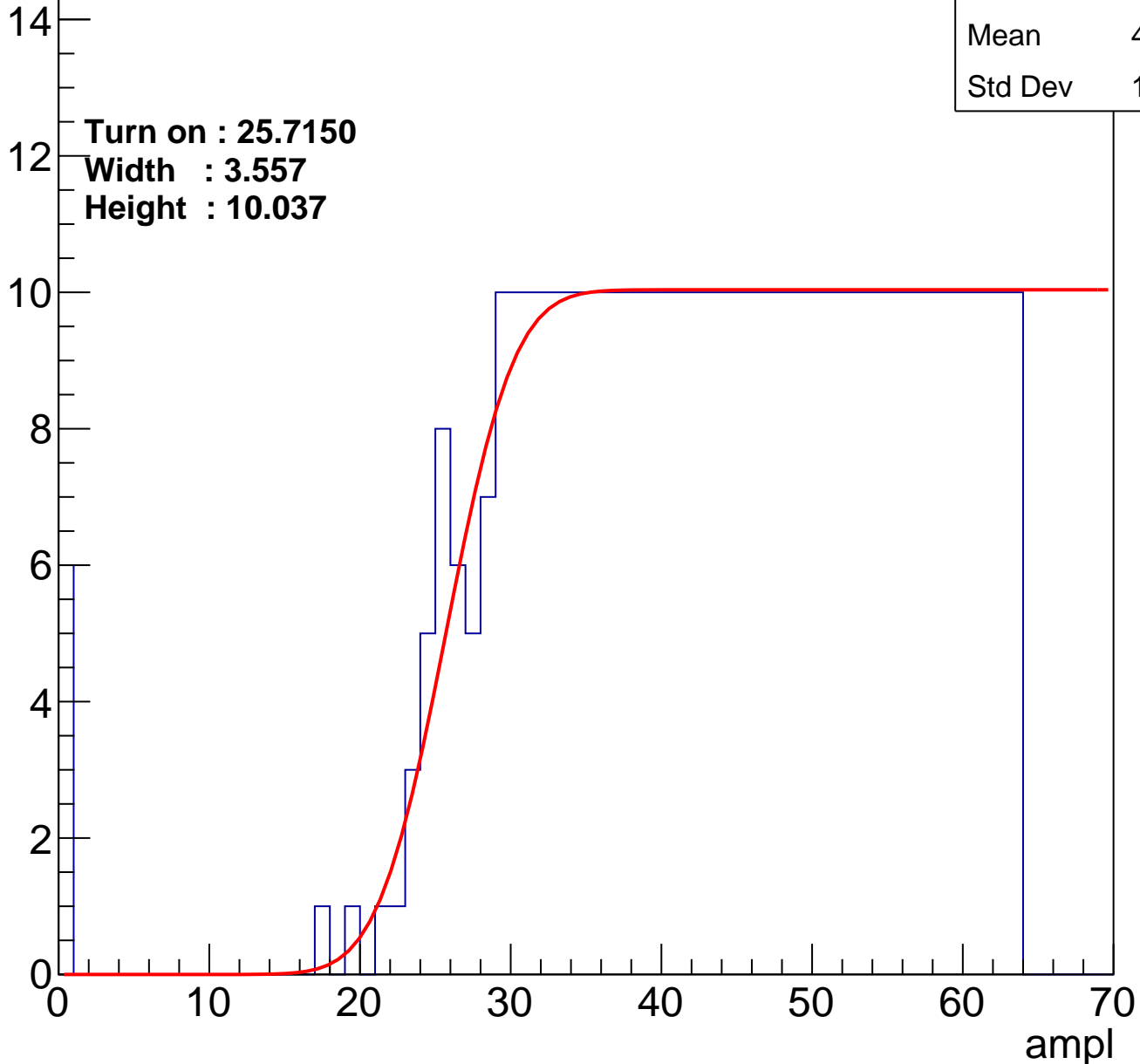
Entries	394
Mean	43.29
Std Dev	12.57

Turn on : 25.7150

Width : 3.557

Height : 10.037

Entry



B1L102S, U15-ch94

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.0557

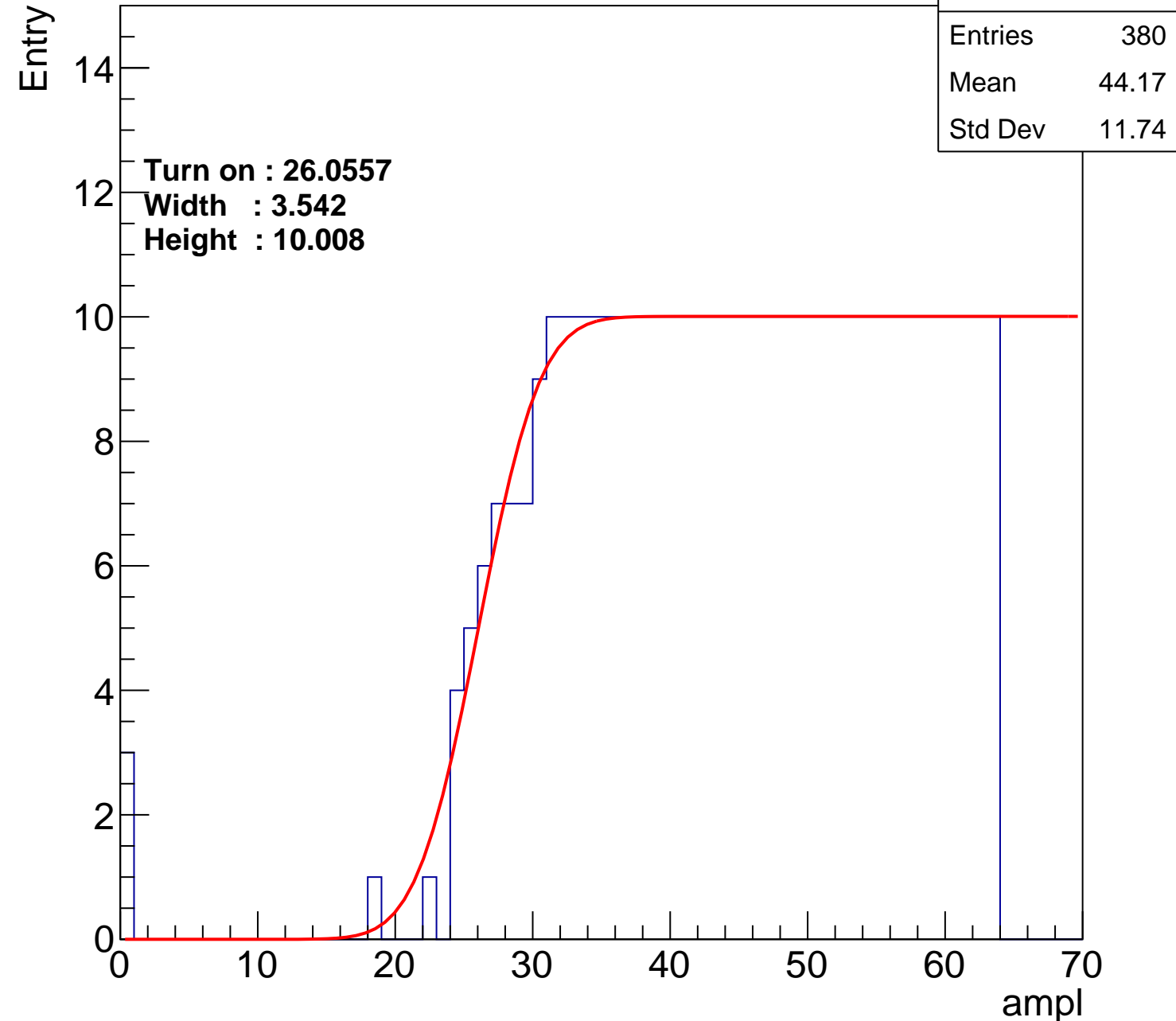
Width : 3.542

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch95

calib_packv5_042523_0143.root, FC#11, port A2

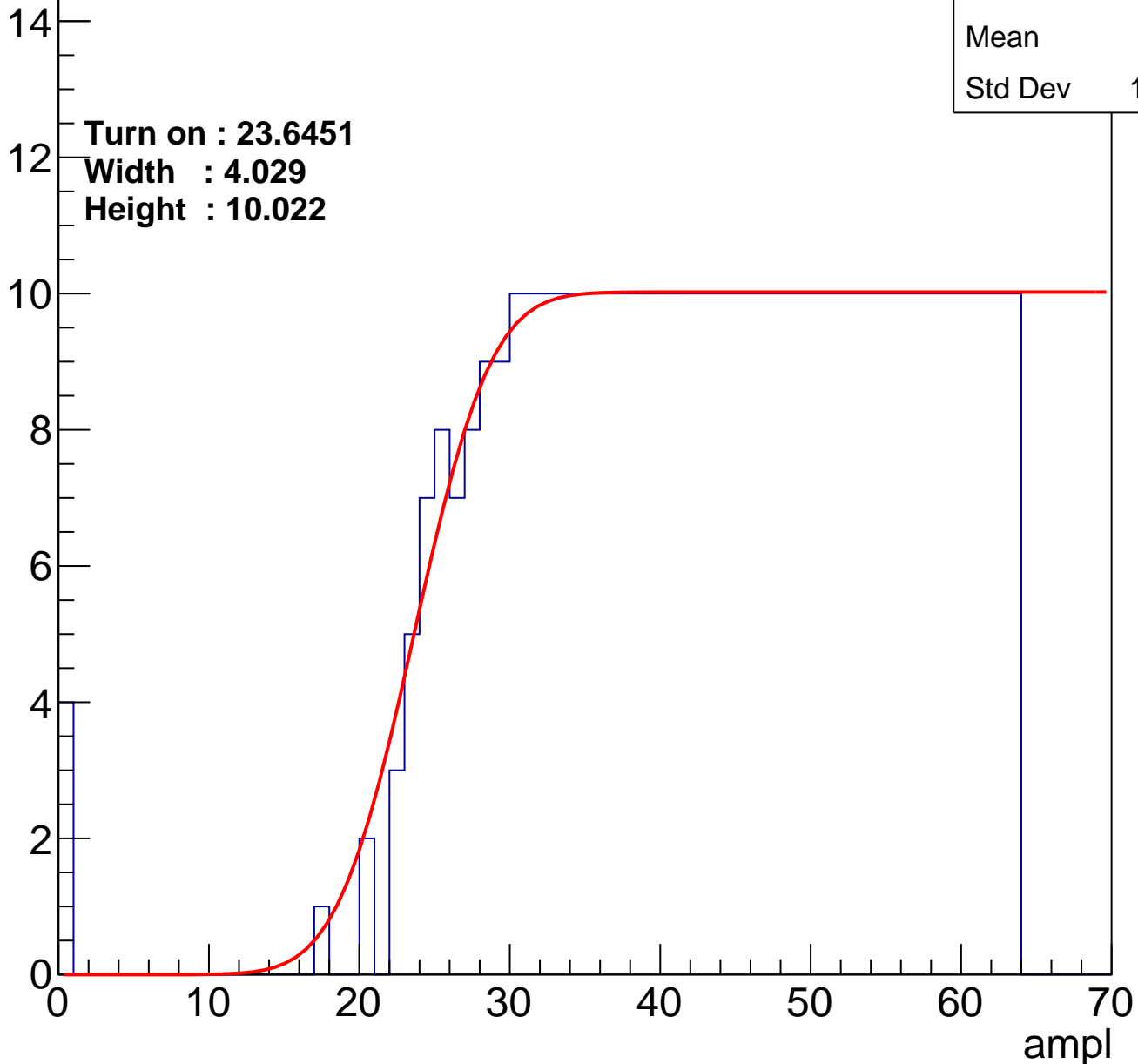
Entries	403
Mean	43
Std Dev	12.44

Turn on : 23.6451

Width : 4.029

Height : 10.022

Entry



B1L102S, U15-ch96

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.72
Std Dev	12.01

Turn on : 25.6182

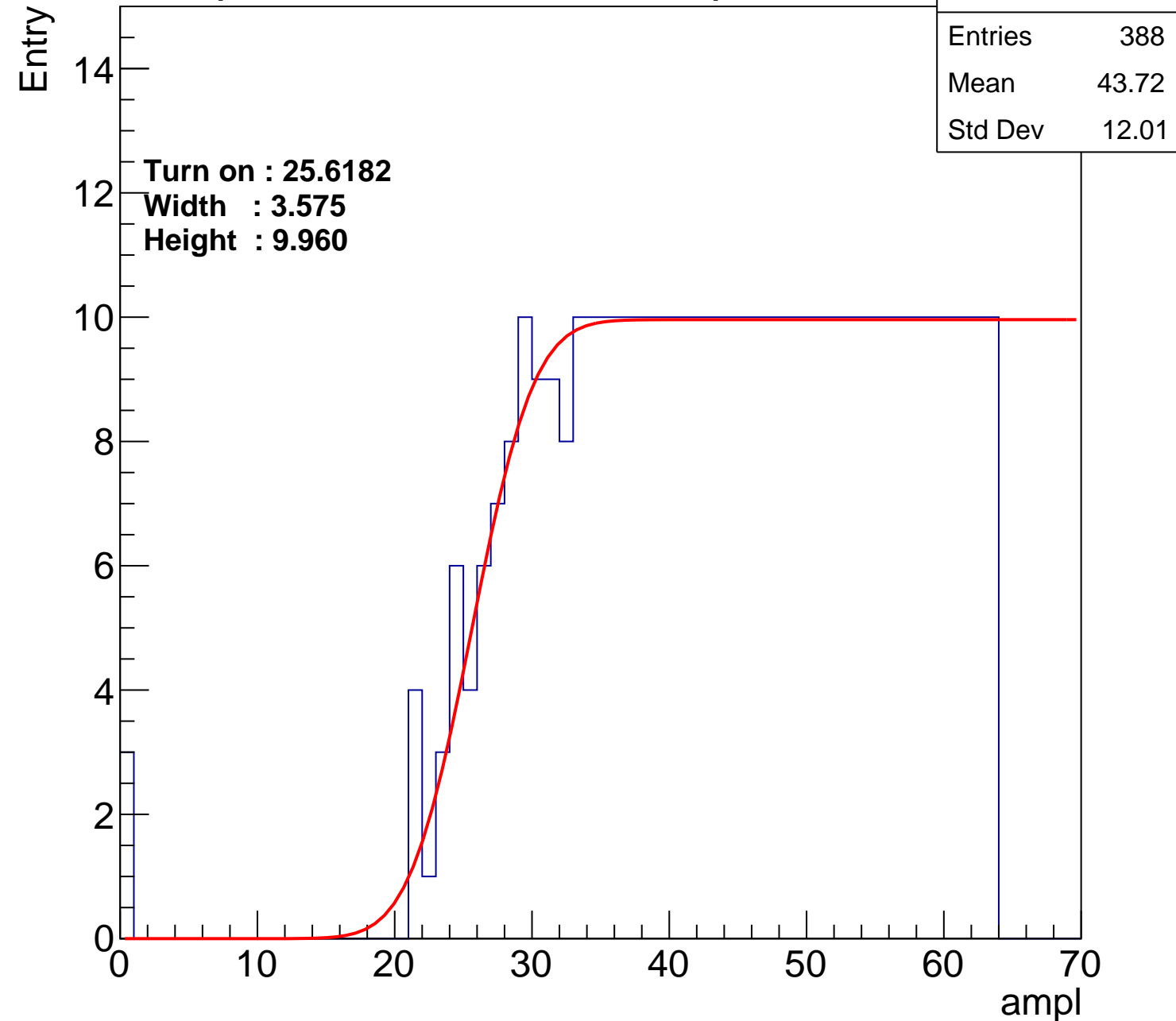
Width : 3.575

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.32
Std Dev	12.64

Turn on : 25.5063

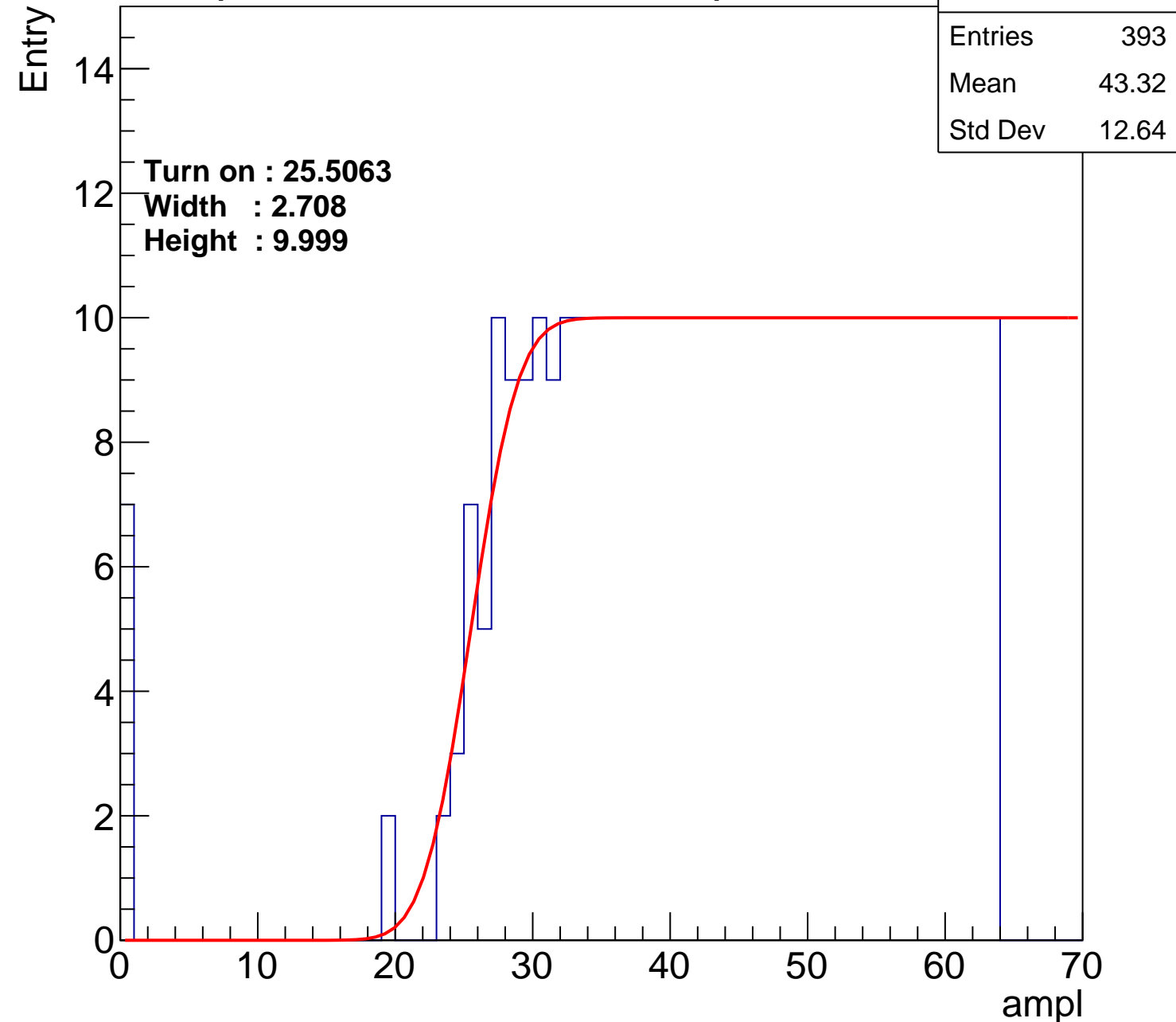
Width : 2.708

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch98

calib_packv5_042523_0143.root, FC#11, port A2

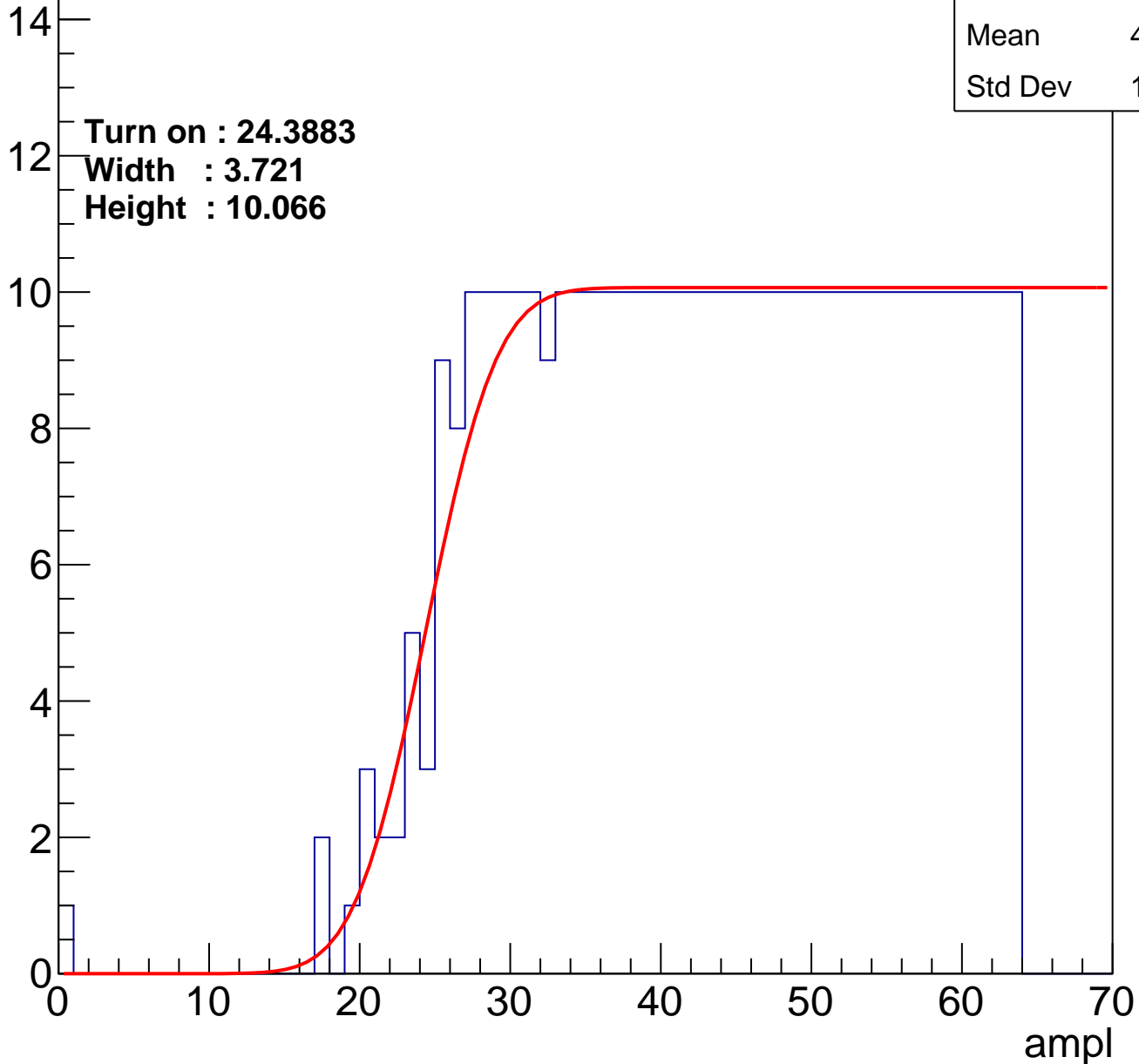
Entries	405
Mean	43.05
Std Dev	12.08

Turn on : 24.3883

Width : 3.721

Height : 10.066

Entry



B1L102S, U15-ch99

calib_packv5_042523_0143.root, FC#11, port A2

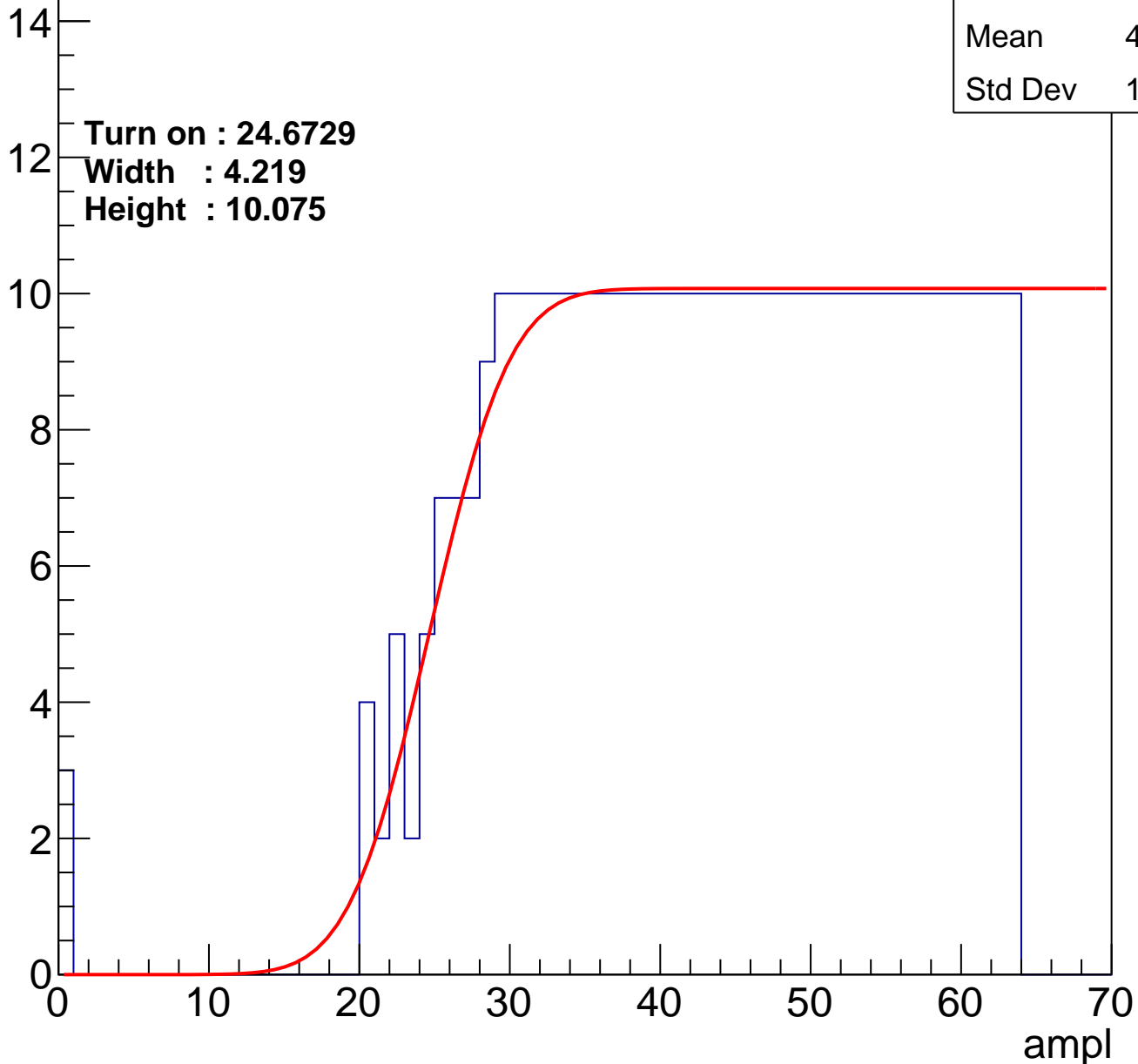
Entries	401
Mean	43.13
Std Dev	12.27

Turn on : 24.6729

Width : 4.219

Height : 10.075

Entry



B1L102S, U15-ch100

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.56
Std Dev	11.54

Turn on : 26.9390

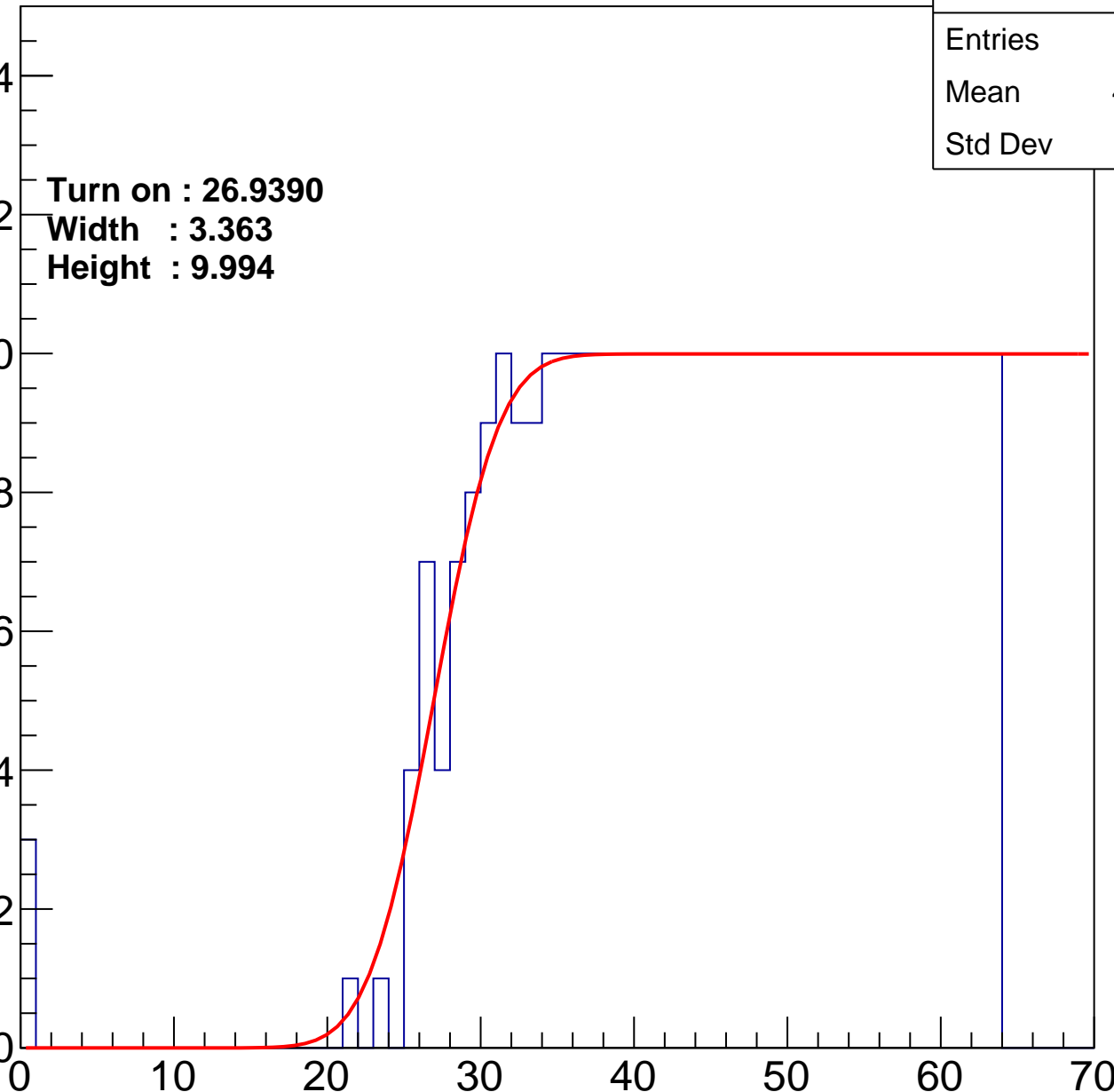
Width : 3.363

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch101

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.47
Std Dev	12.17

Turn on : 25.9914

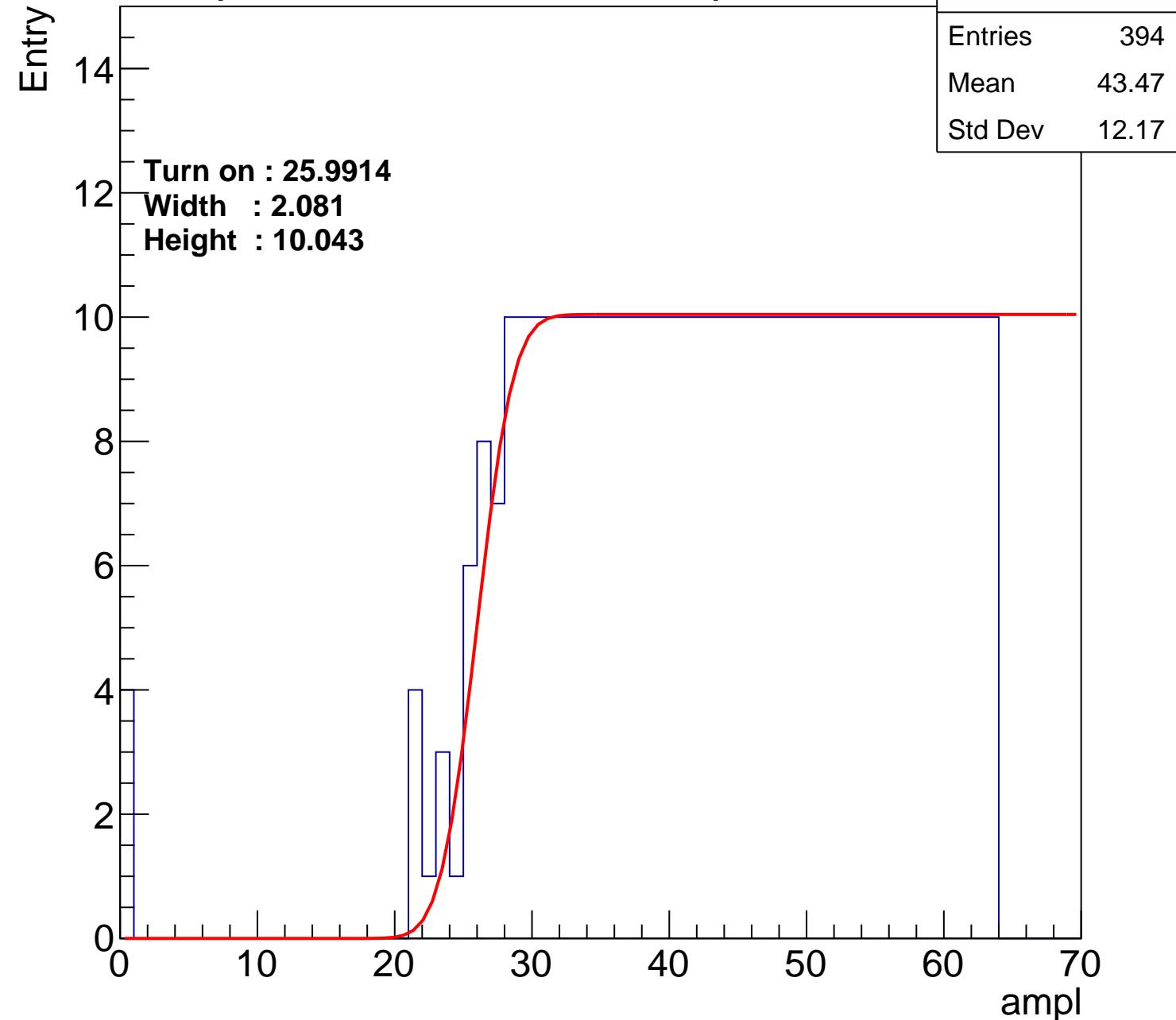
Width : 2.081

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch102

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.97
Std Dev	11.61

Turn on : 26.3648

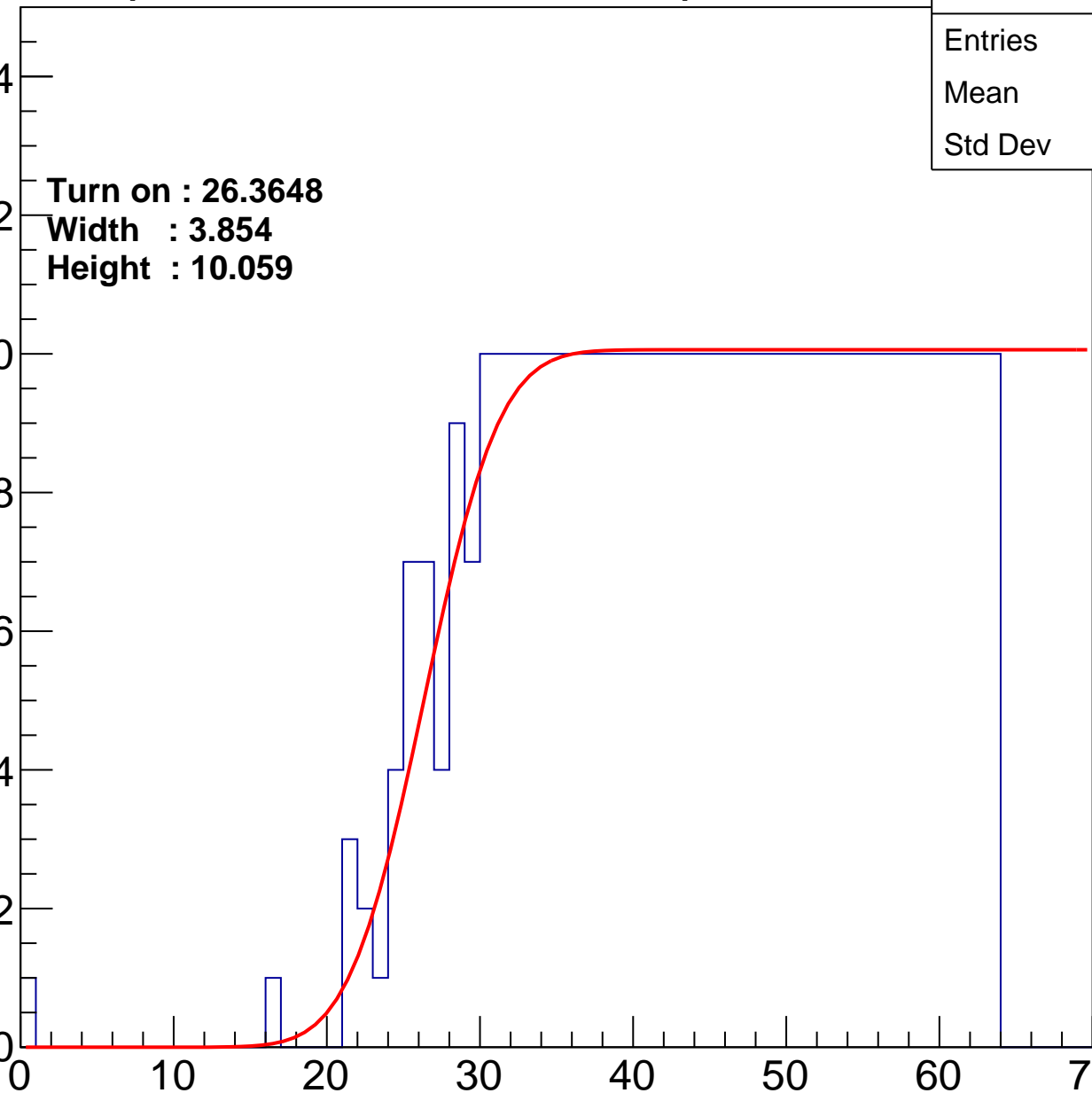
Width : 3.854

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch103

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.4
Std Dev	12.5

Turn on : 25.6697

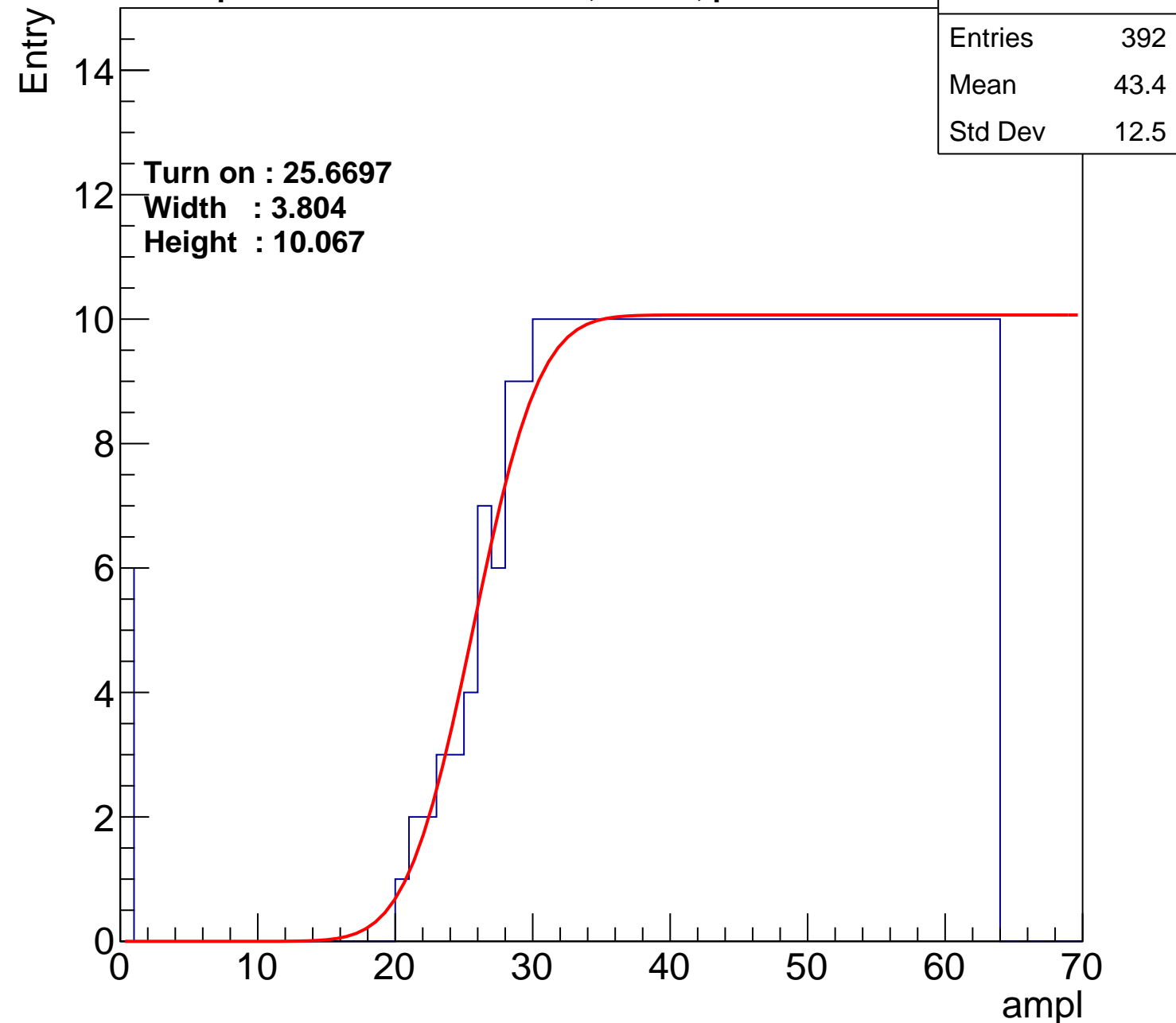
Width : 3.804

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch104

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.36
Std Dev	12.27

Turn on : 25.3822

Width : 2.926

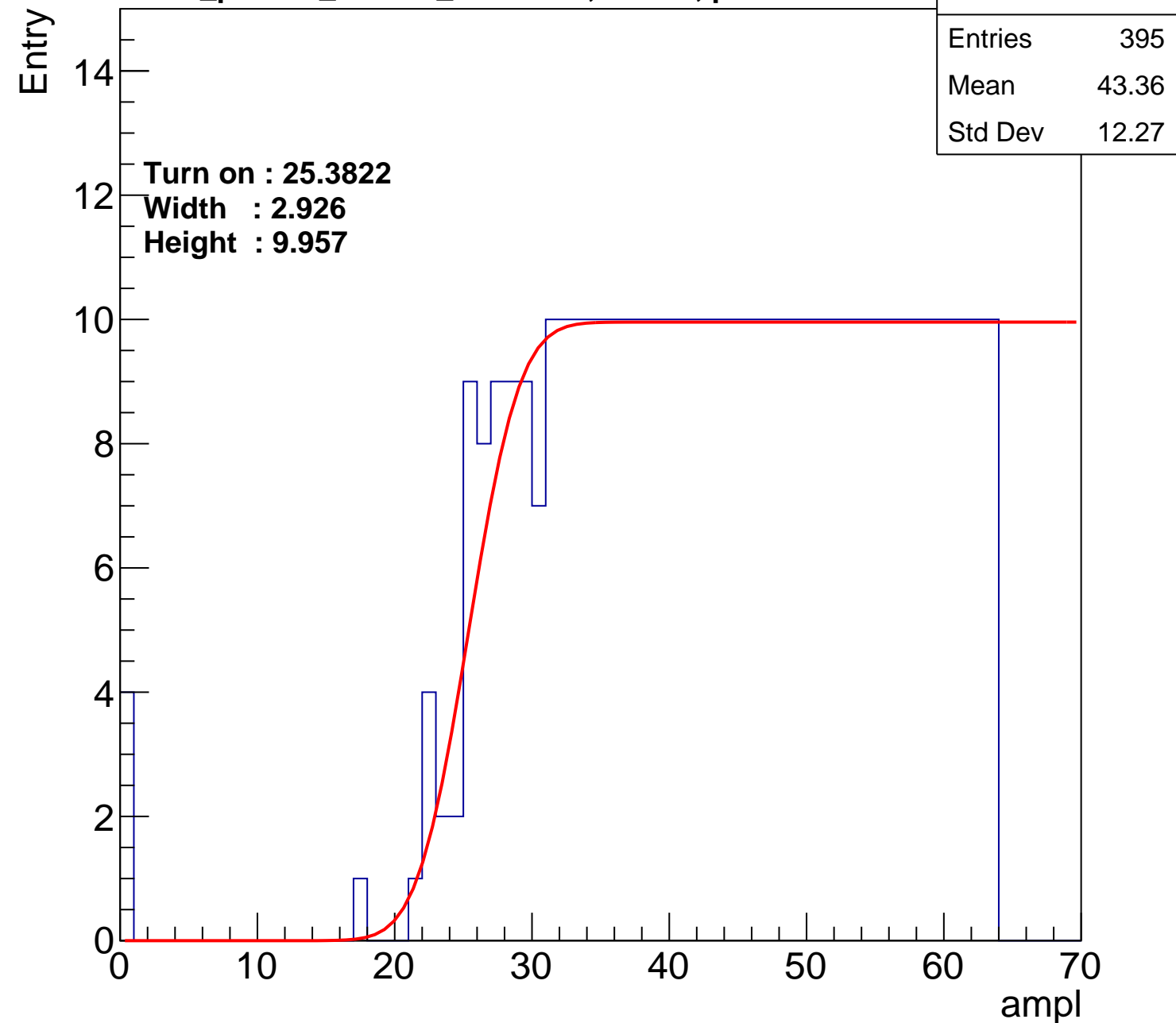
Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch105

calib_packv5_042523_0143.root, FC#11, port A2

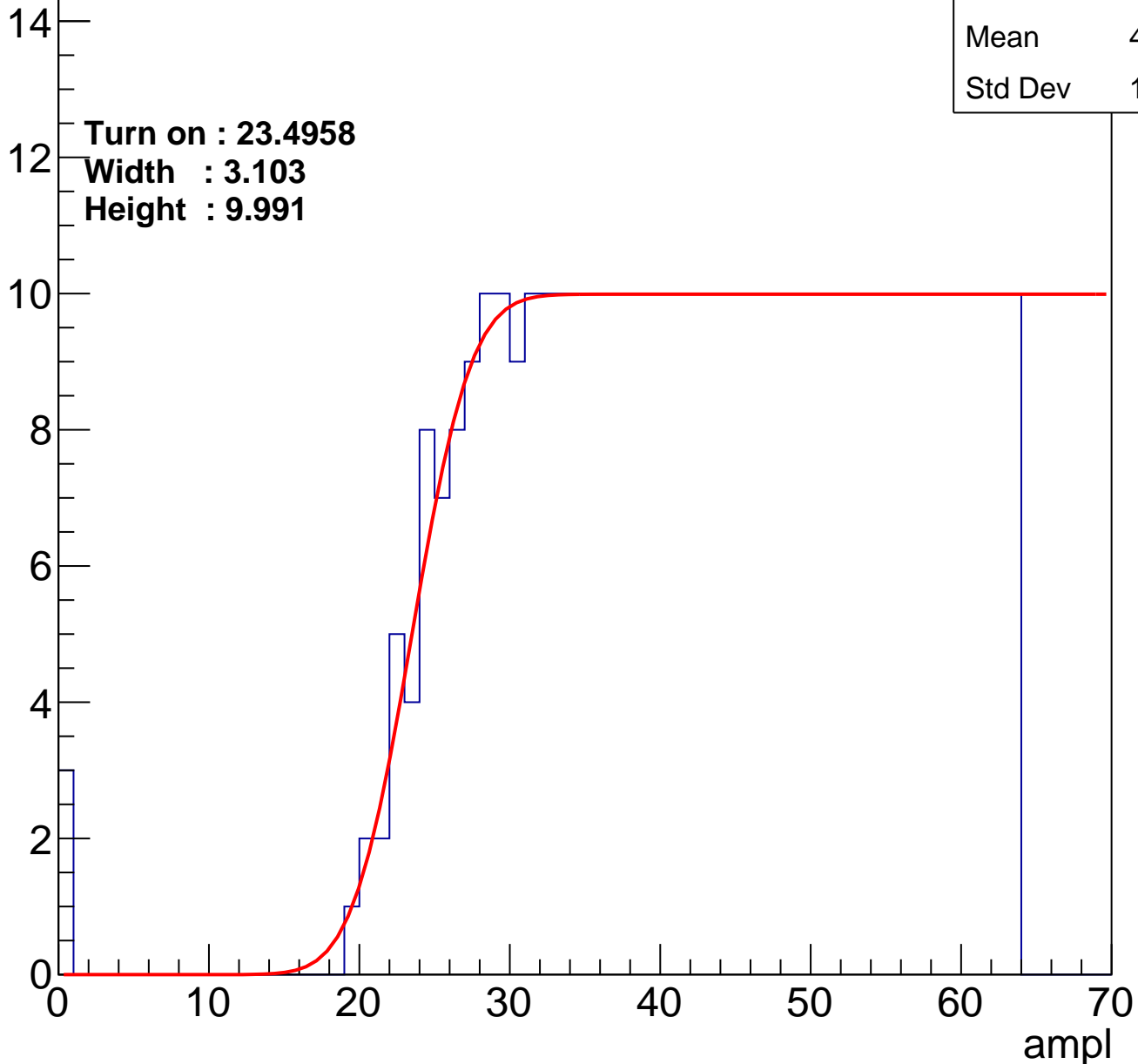
Entries	408
Mean	42.82
Std Dev	12.39

Turn on : 23.4958

Width : 3.103

Height : 9.991

Entry



B1L102S, U15-ch106

calib_packv5_042523_0143.root, FC#11, port A2

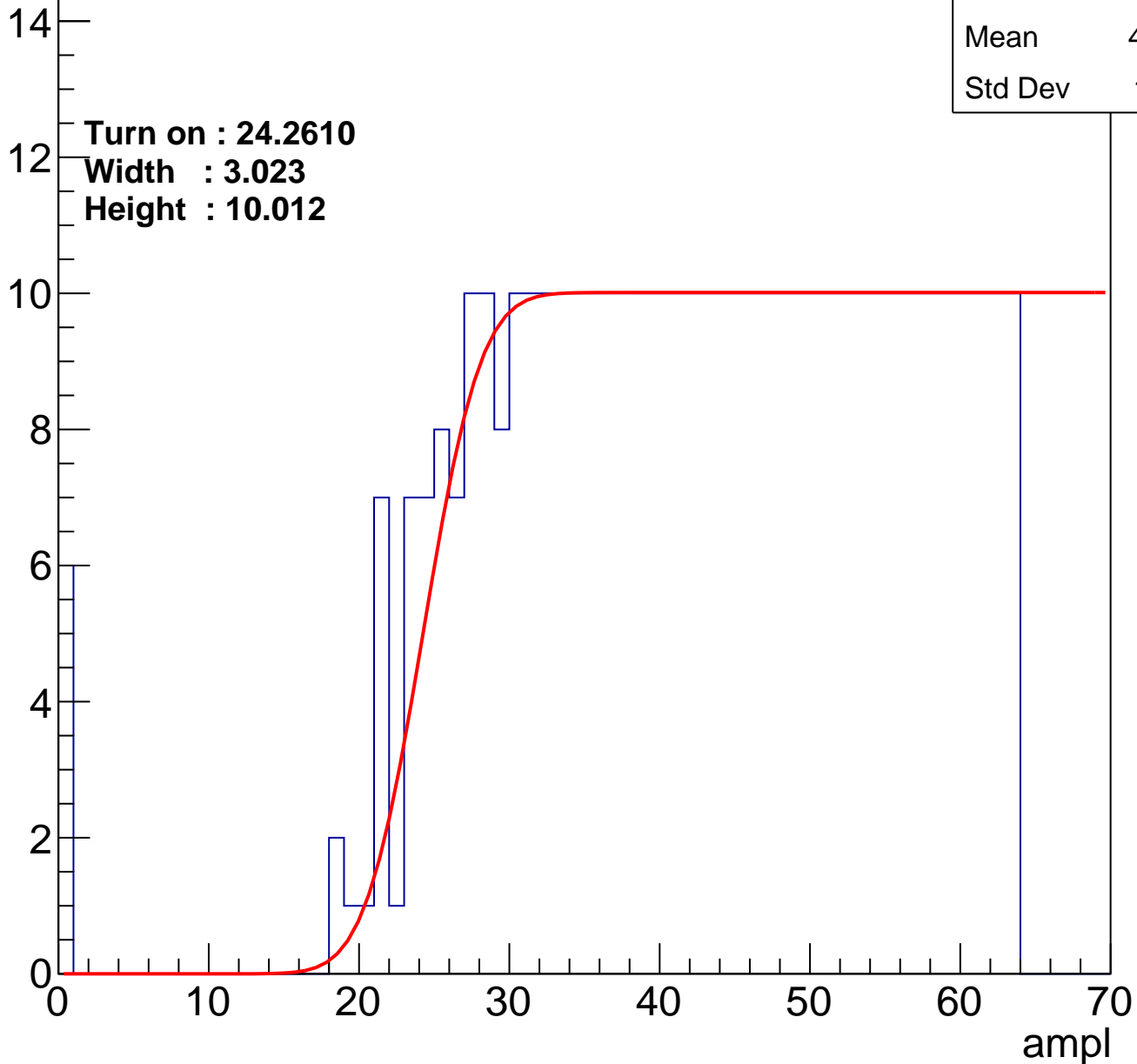
Entries	415
Mean	42.28
Std Dev	13.01

Turn on : 24.2610

Width : 3.023

Height : 10.012

Entry



B1L102S, U15-ch107

calib_packv5_042523_0143.root, FC#11, port A2

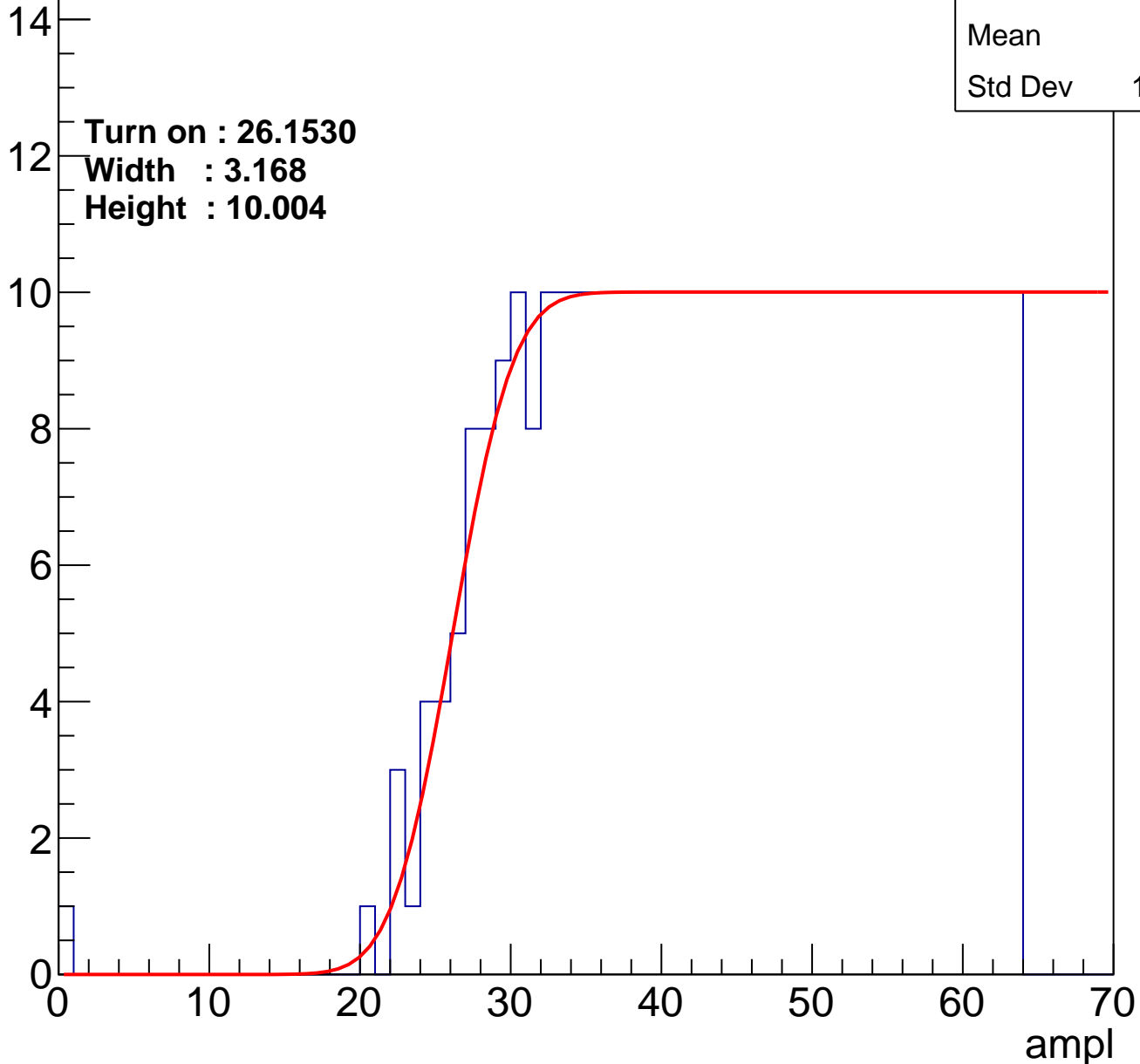
Entries	382
Mean	44.2
Std Dev	11.44

Turn on : 26.1530

Width : 3.168

Height : 10.004

Entry



B1L102S, U15-ch108

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.31
Std Dev	11.92

Turn on : 24.5885

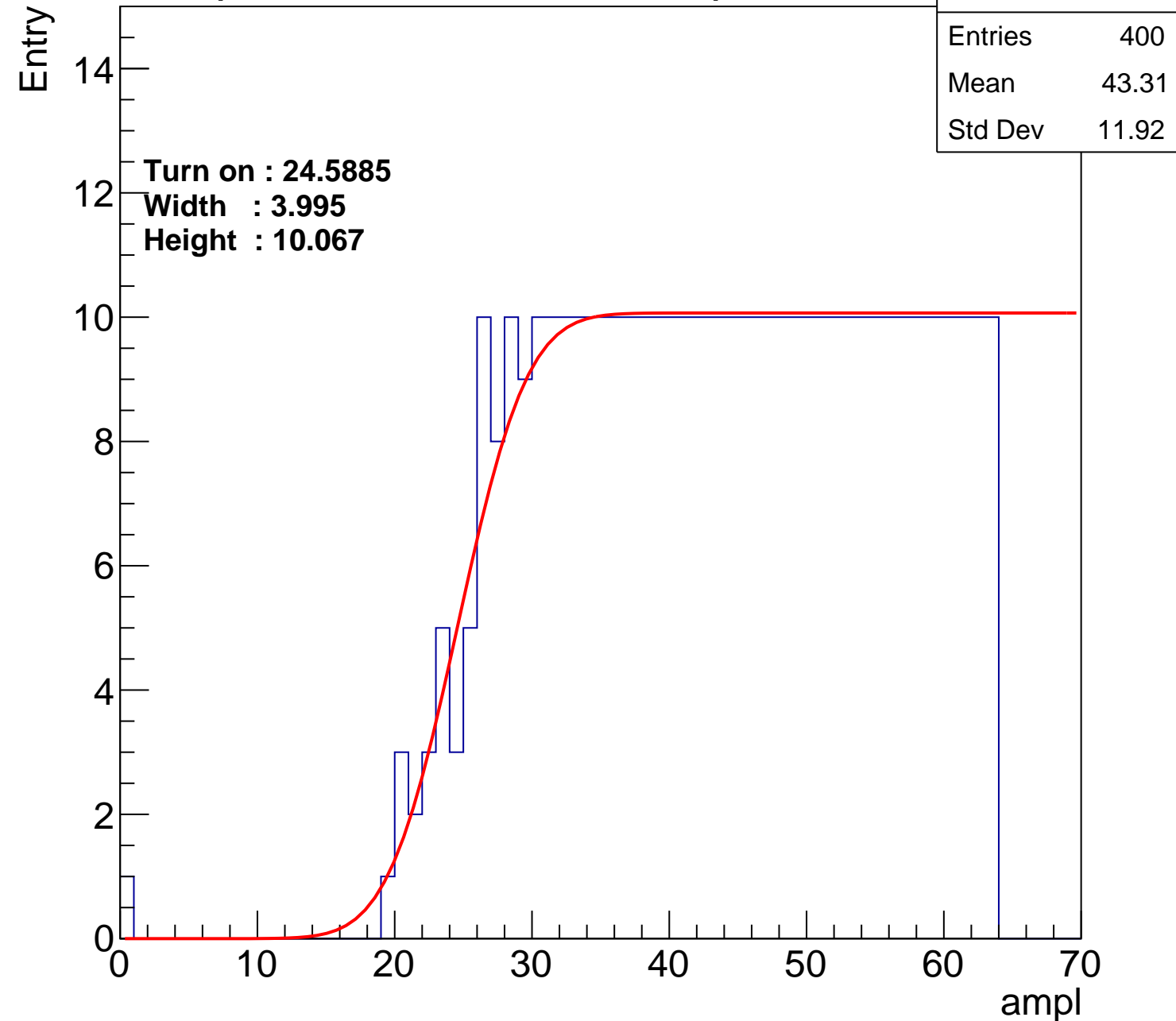
Width : 3.995

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch109

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.78
Std Dev	11.77

Turn on : 25.5939

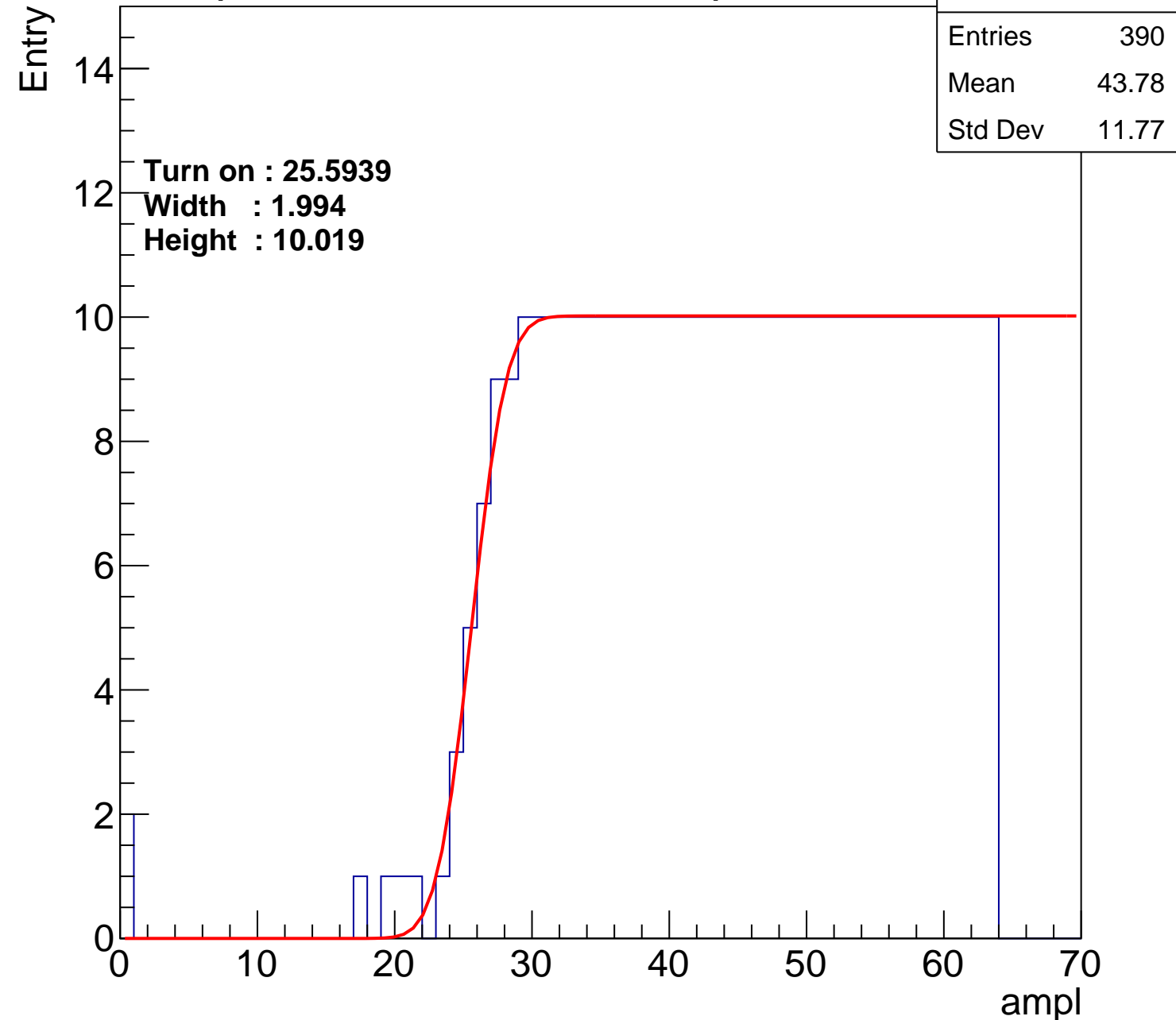
Width : 1.994

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch110

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.14
Std Dev	12.48

Turn on : 24.5318

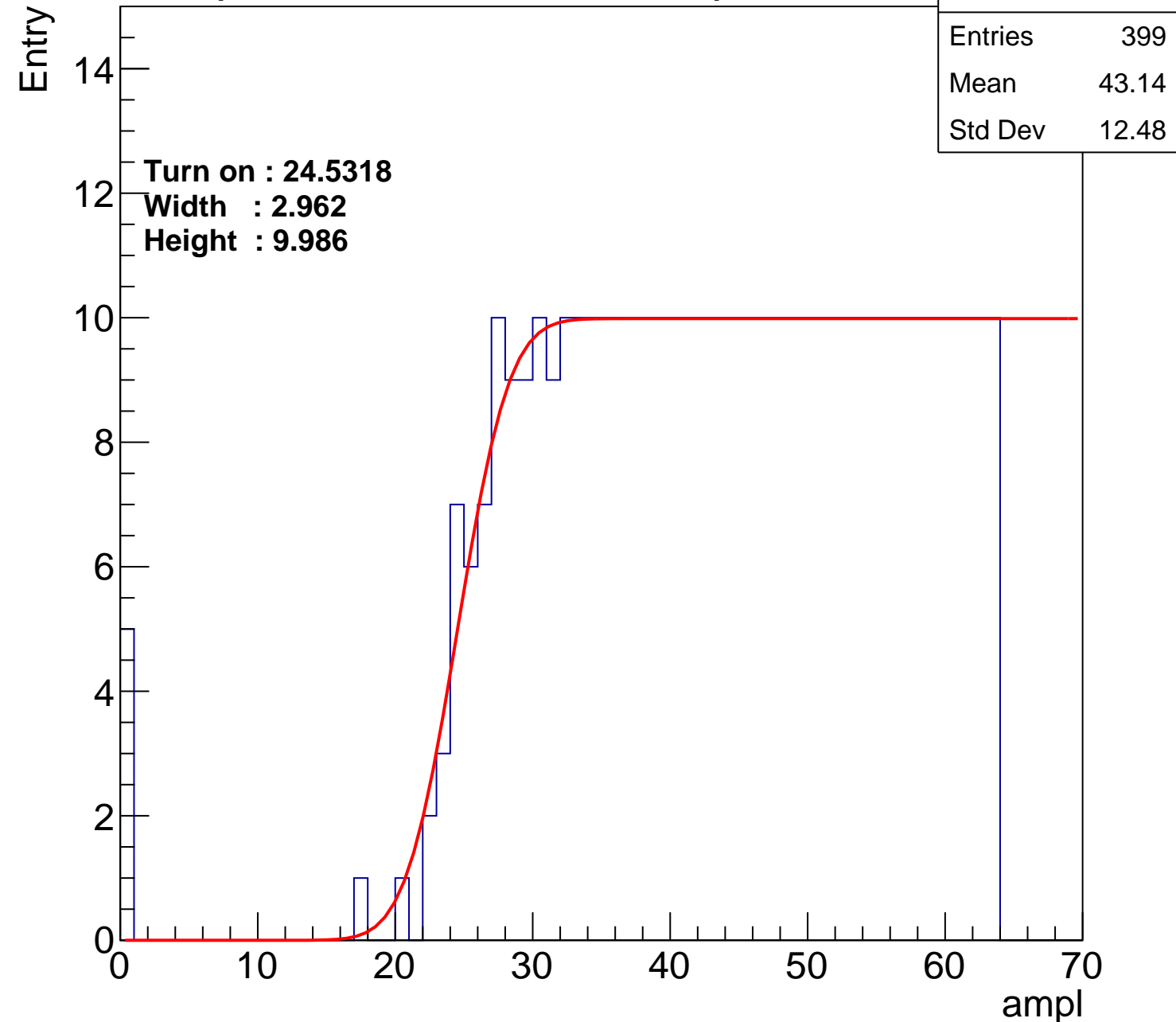
Width : 2.962

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch111

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.67
Std Dev	12.28

Turn on : 26.0530

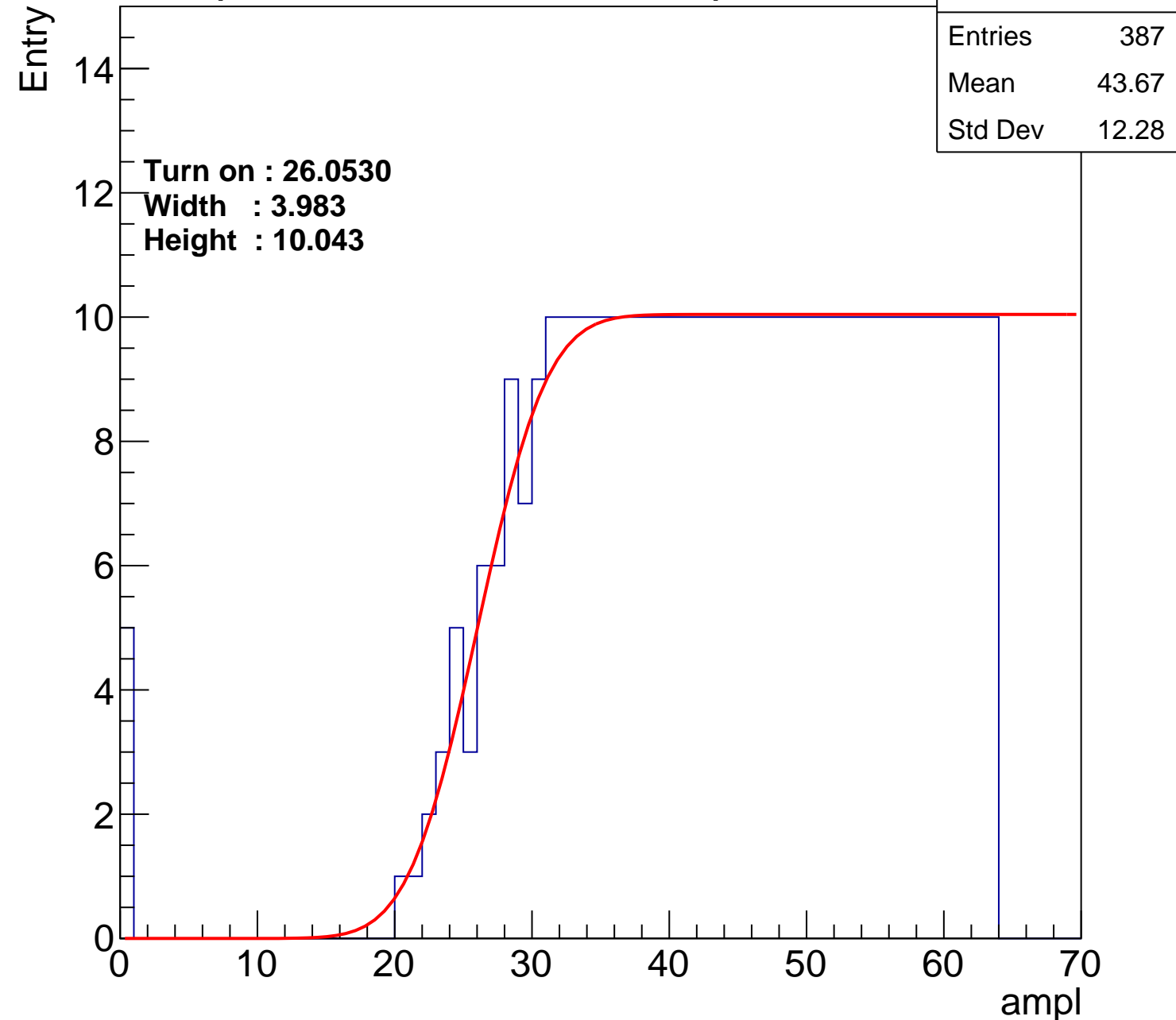
Width : 3.983

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch112

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	43.09
Std Dev	12.07

Turn on : 24.1321

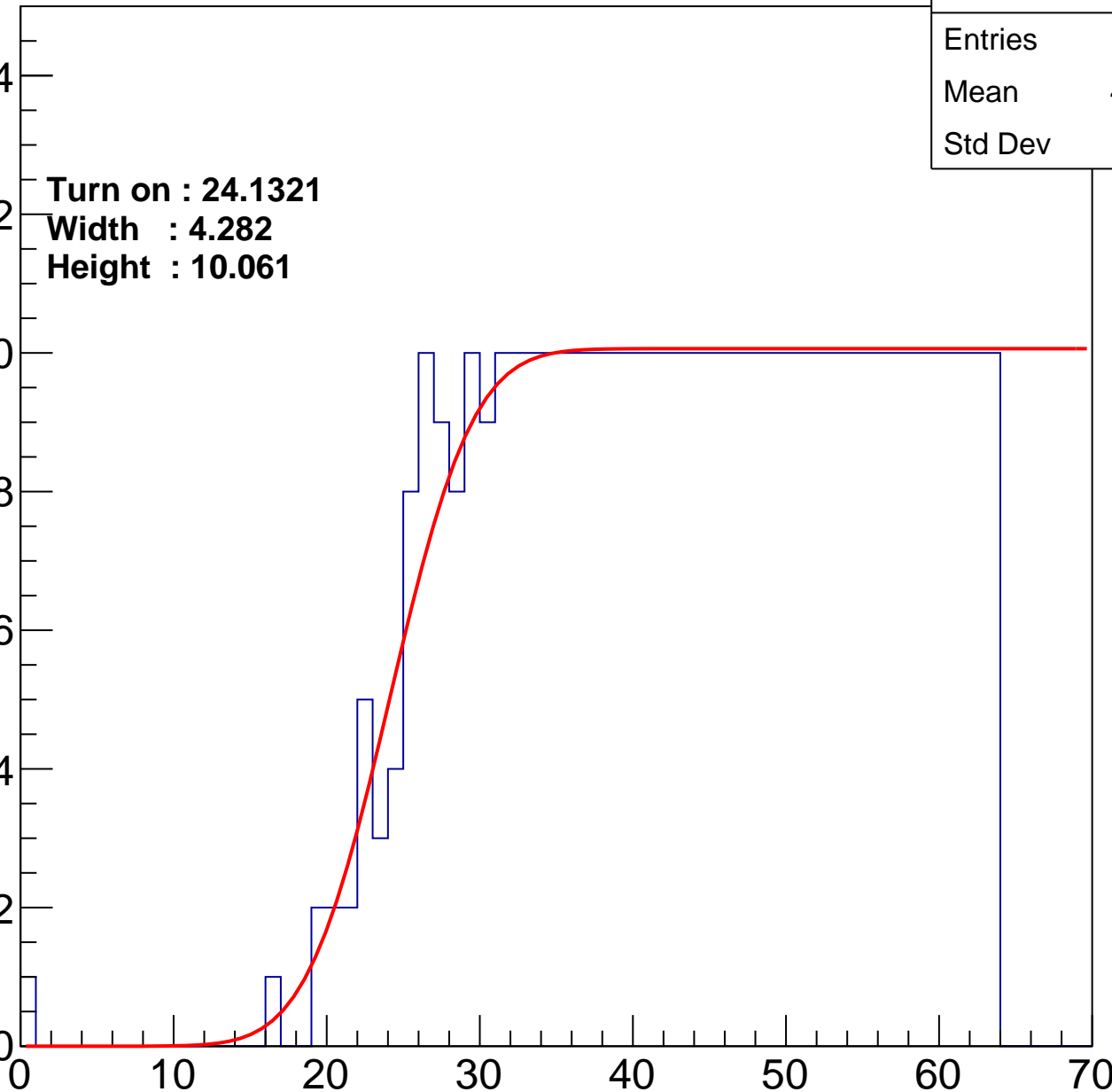
Width : 4.282

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch113

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.34
Std Dev	11.8

Turn on : 26.7757

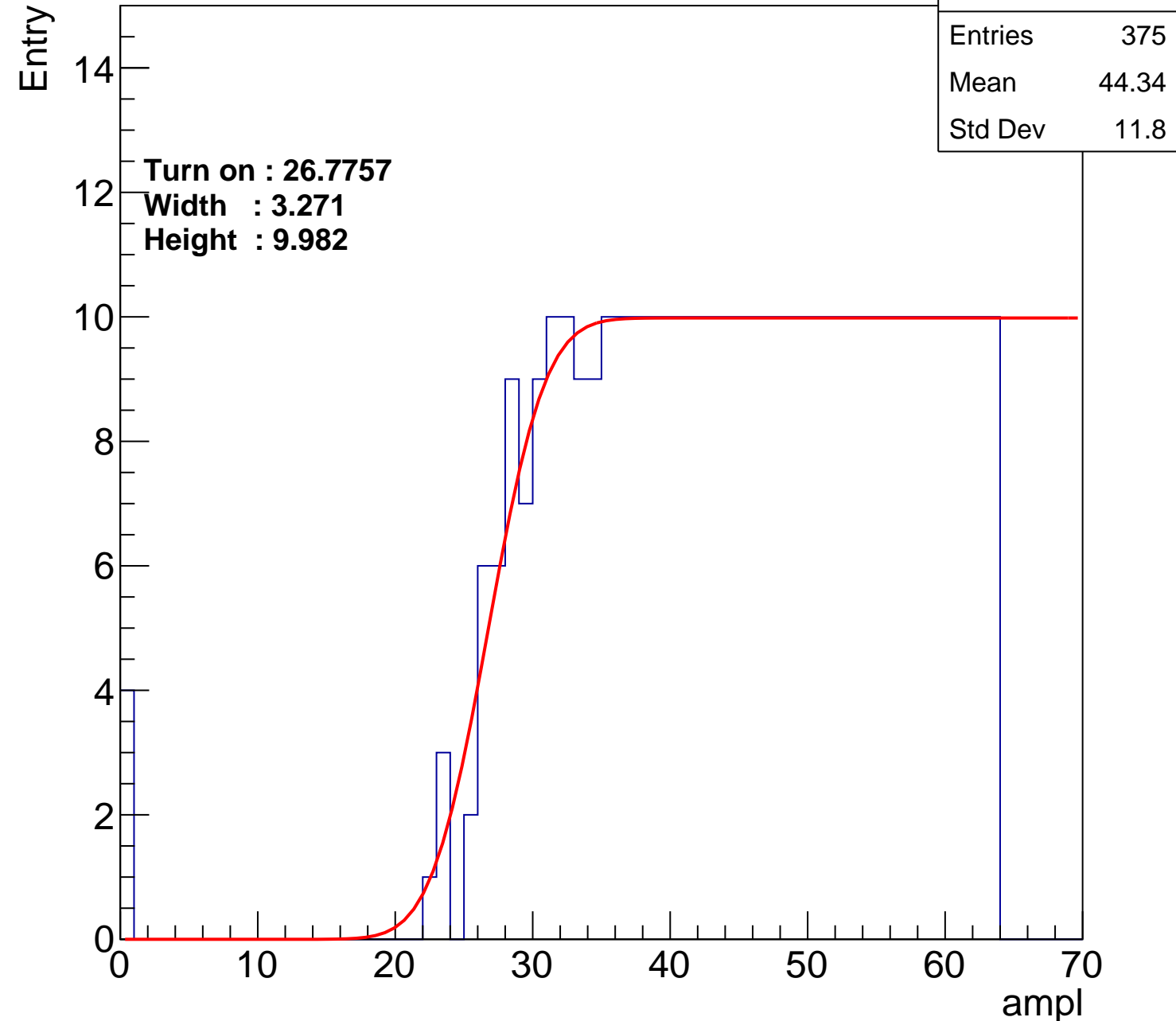
Width : 3.271

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch114

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.68
Std Dev	11.74

Turn on : 25.5790

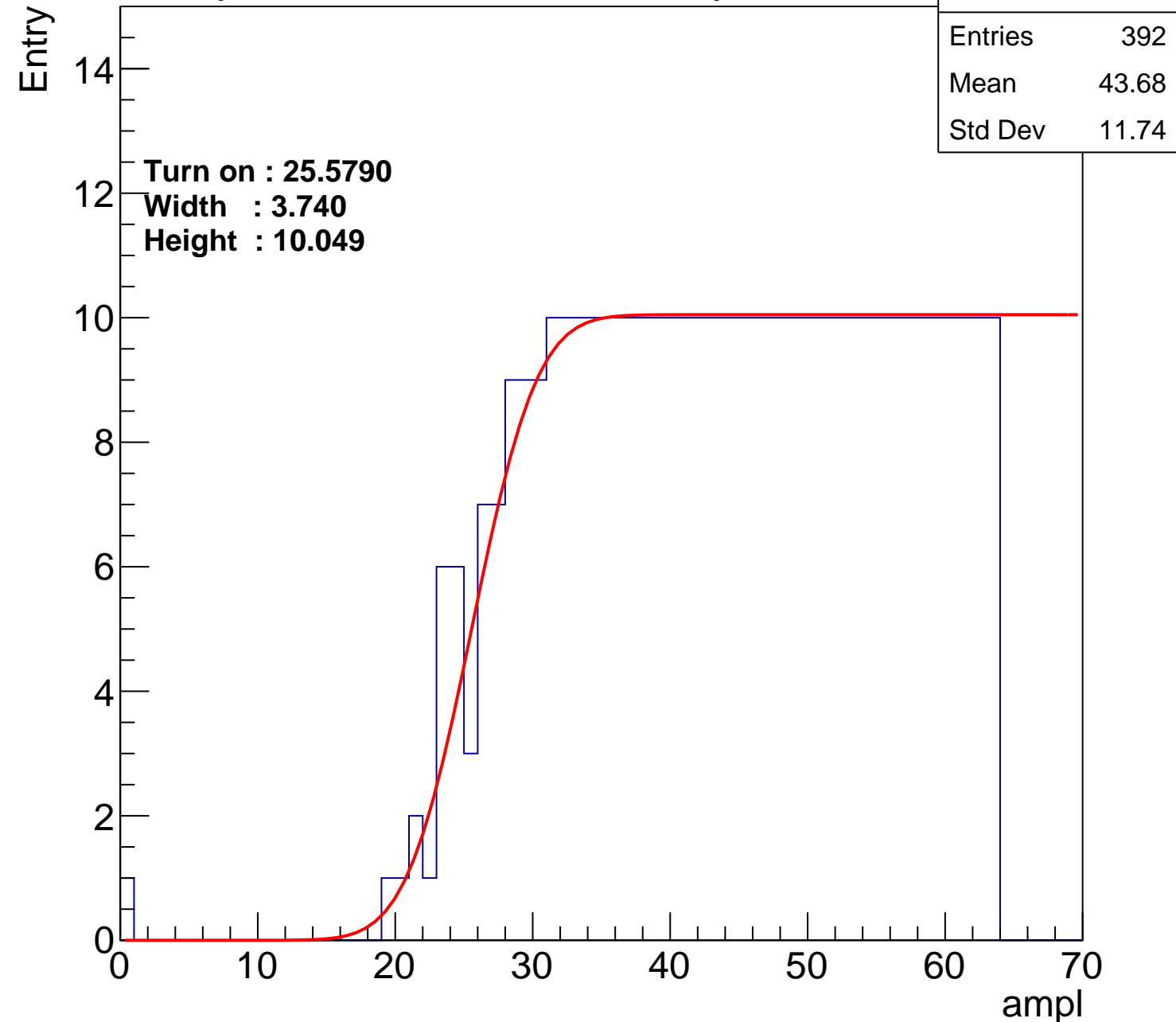
Width : 3.740

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch115

calib_packv5_042523_0143.root, FC#11, port A2

Entries	363
Mean	44.96
Std Dev	11.37

Turn on : 27.6260

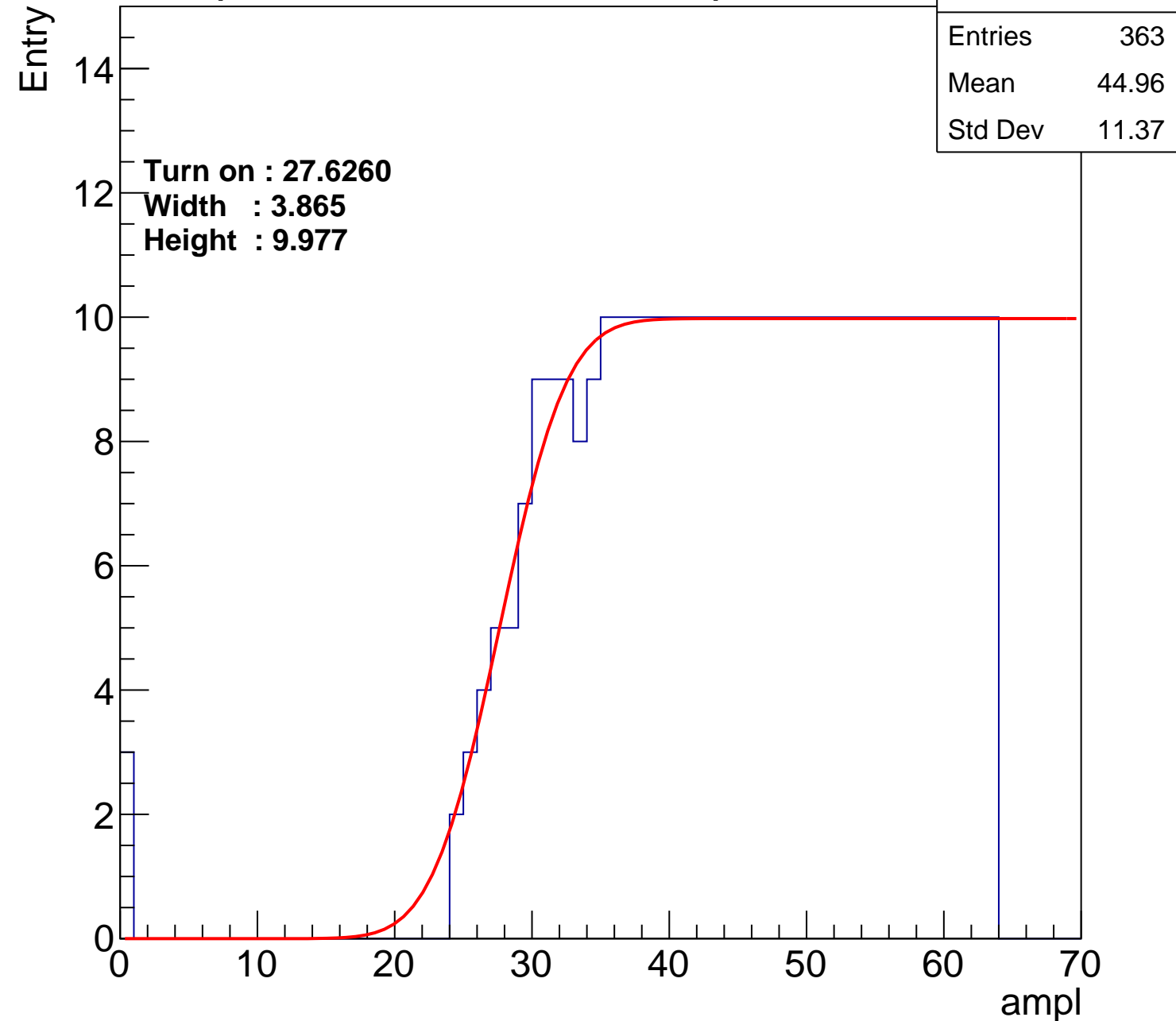
Width : 3.865

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch116

calib_packv5_042523_0143.root, FC#11, port A2

Entries	409
Mean	42.87
Std Dev	12.16

Turn on : 23.5344

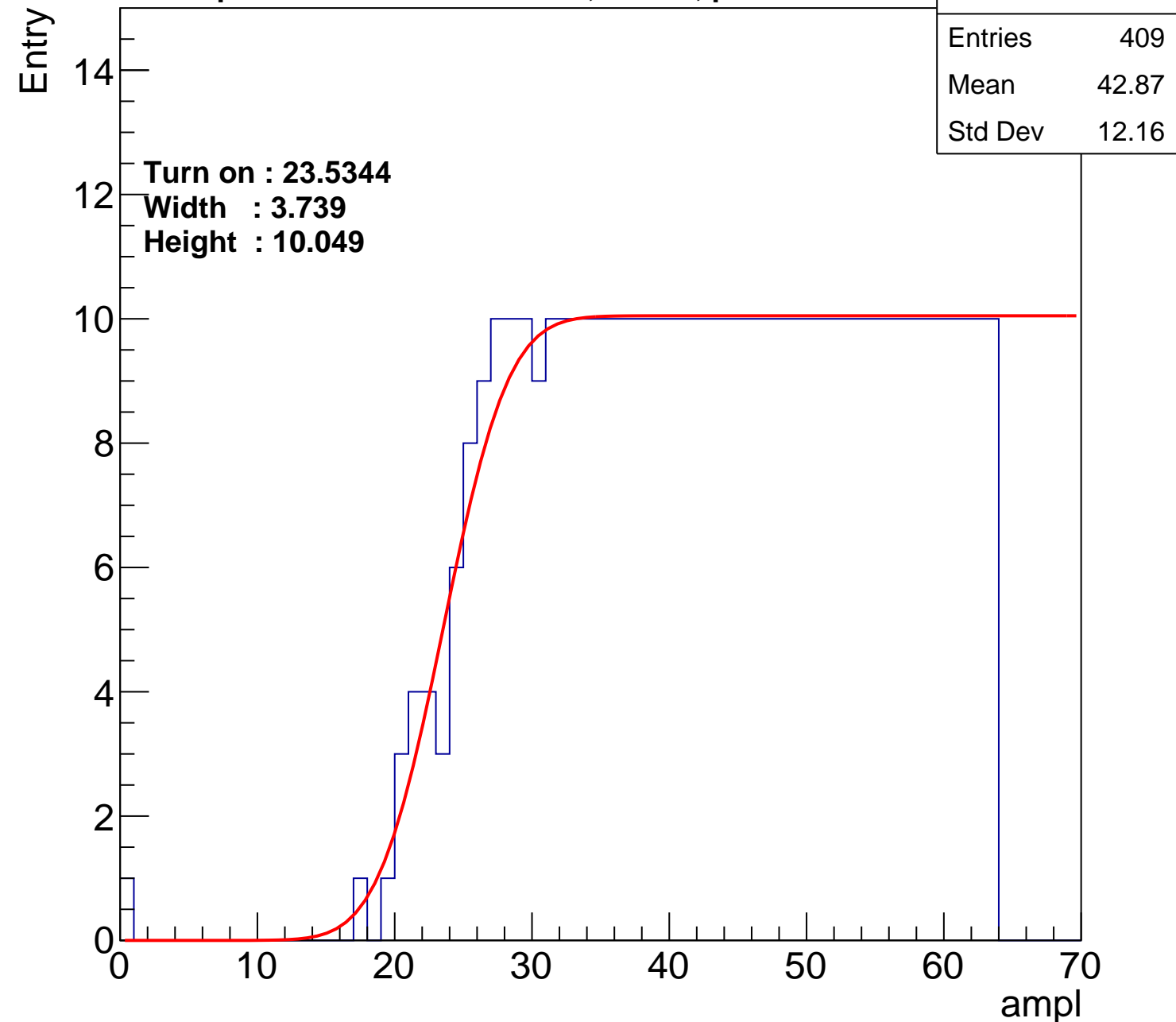
Width : 3.739

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch117

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	43
Std Dev	12.22

Turn on : 24.0324

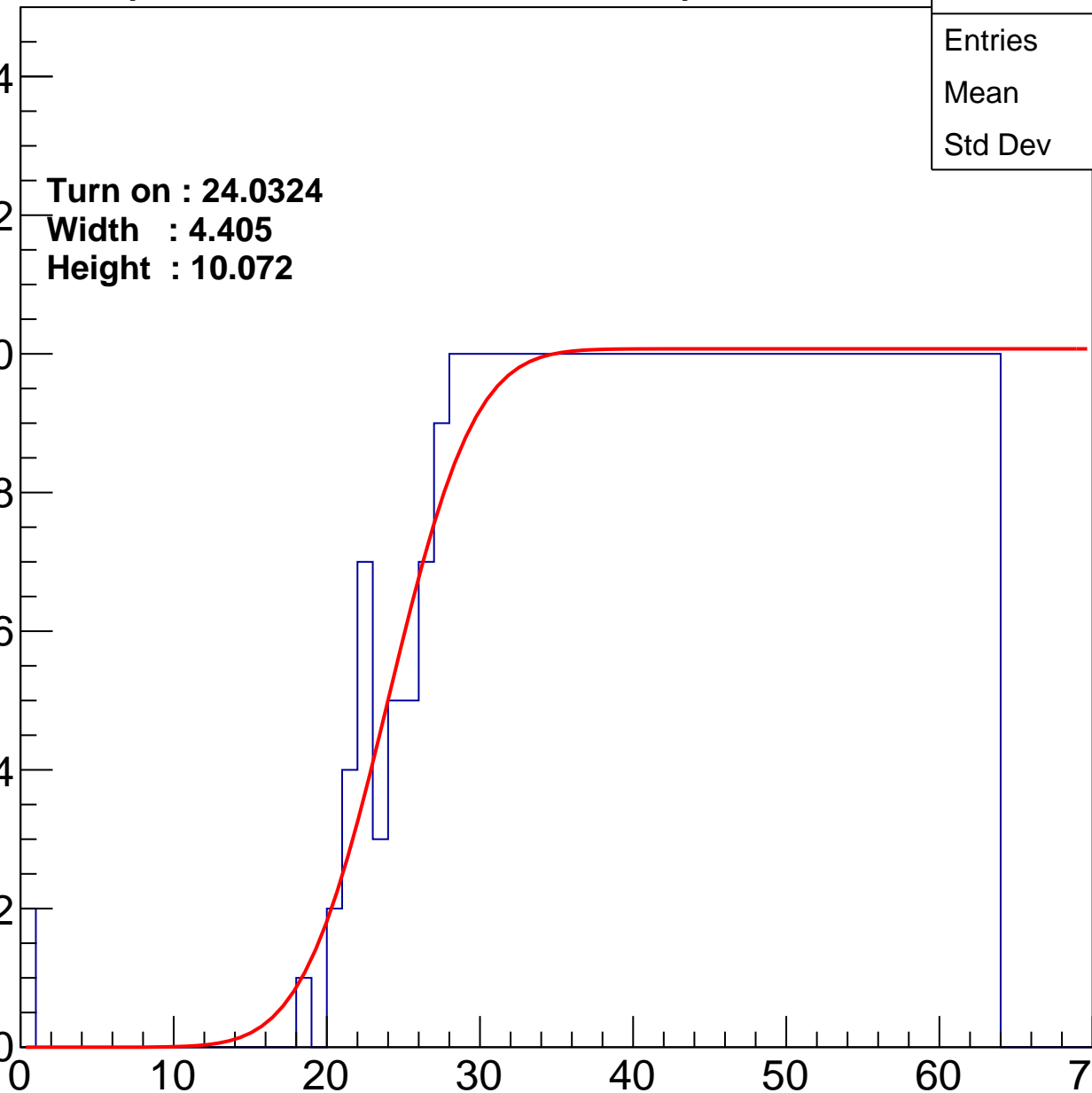
Width : 4.405

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch118

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.44
Std Dev	12.1

Turn on : 24.6408

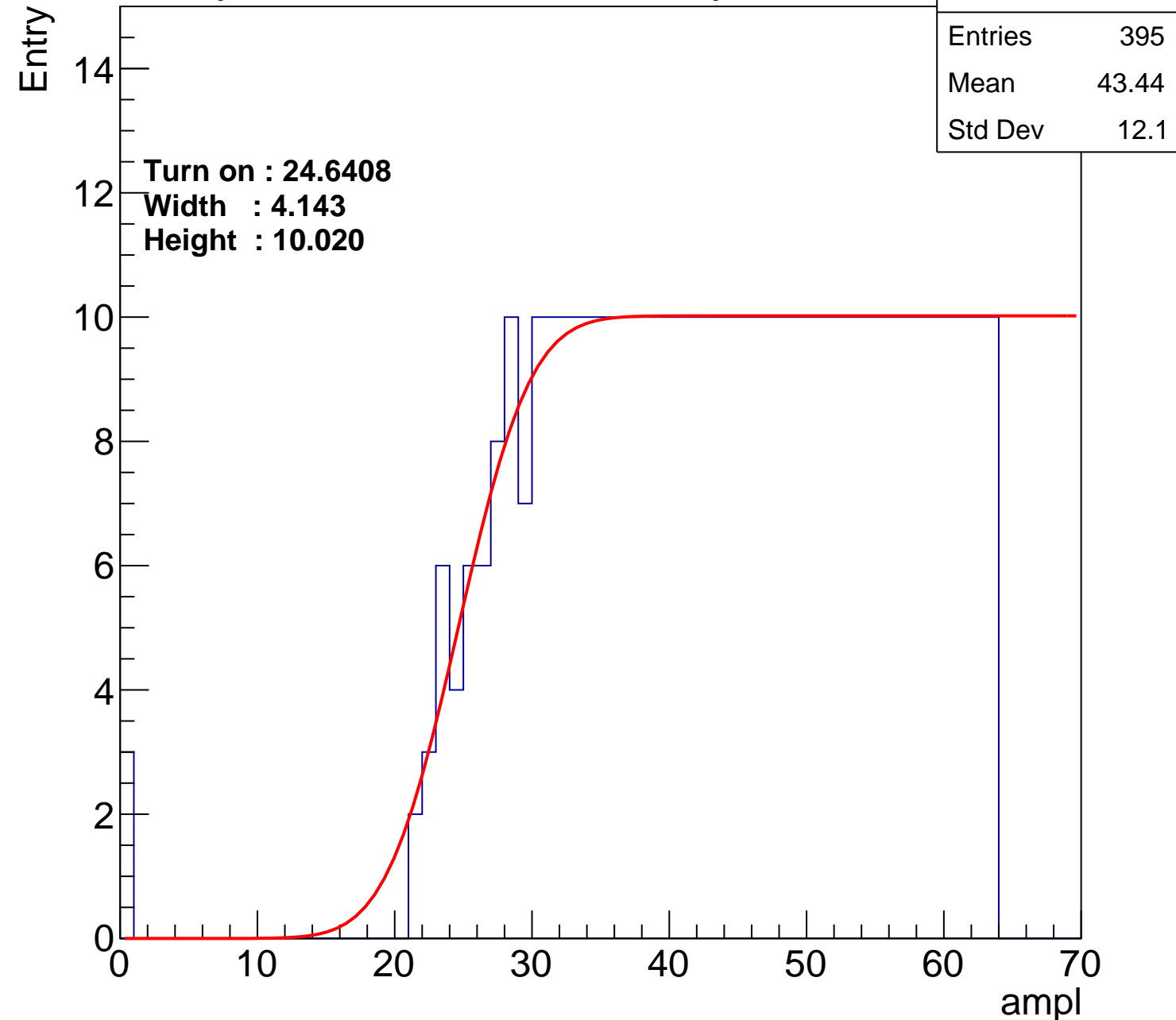
Width : 4.143

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch119

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.53
Std Dev	11.64

Turn on : 27.6230

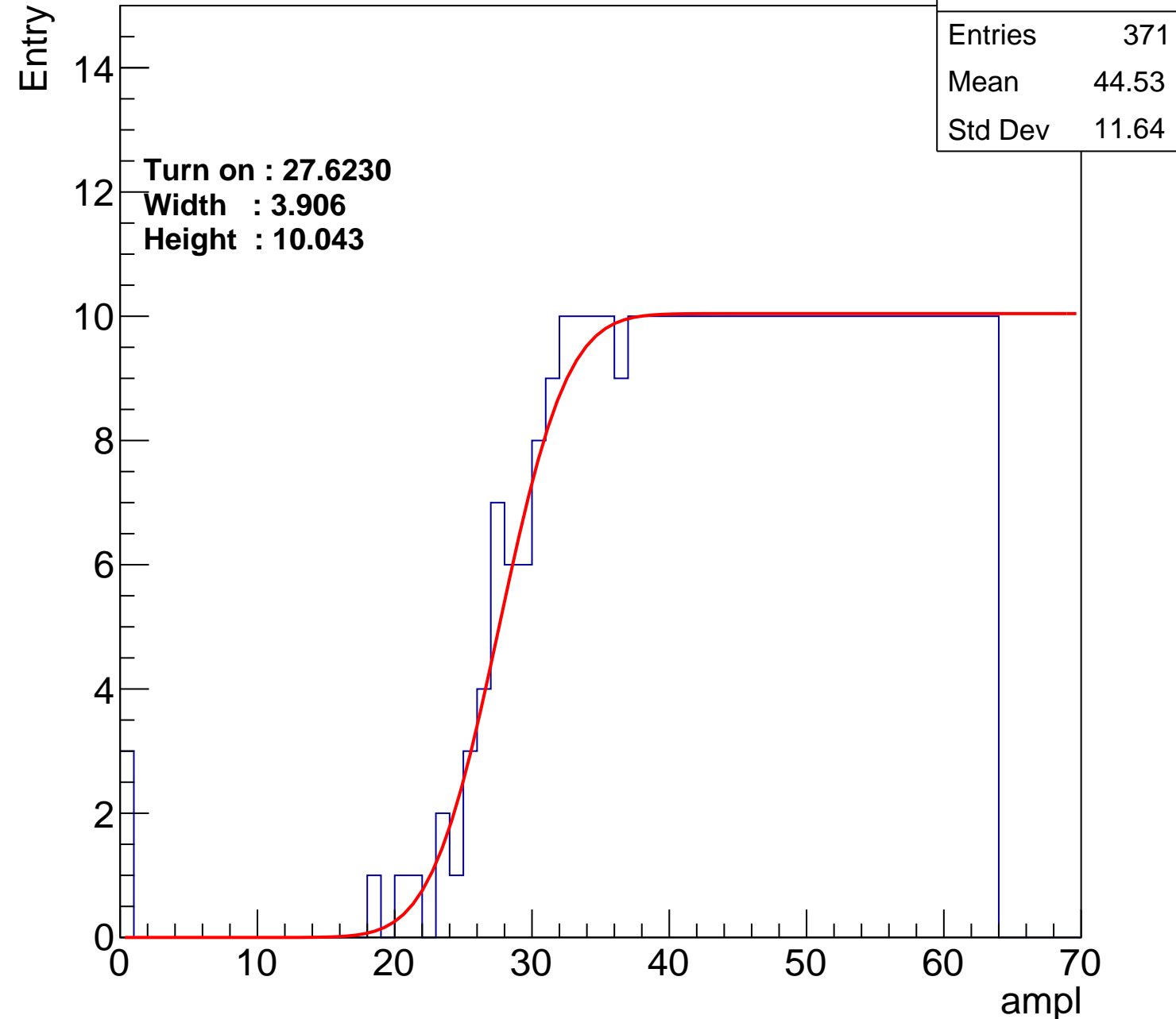
Width : 3.906

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch120

calib_packv5_042523_0143.root, FC#11, port A2

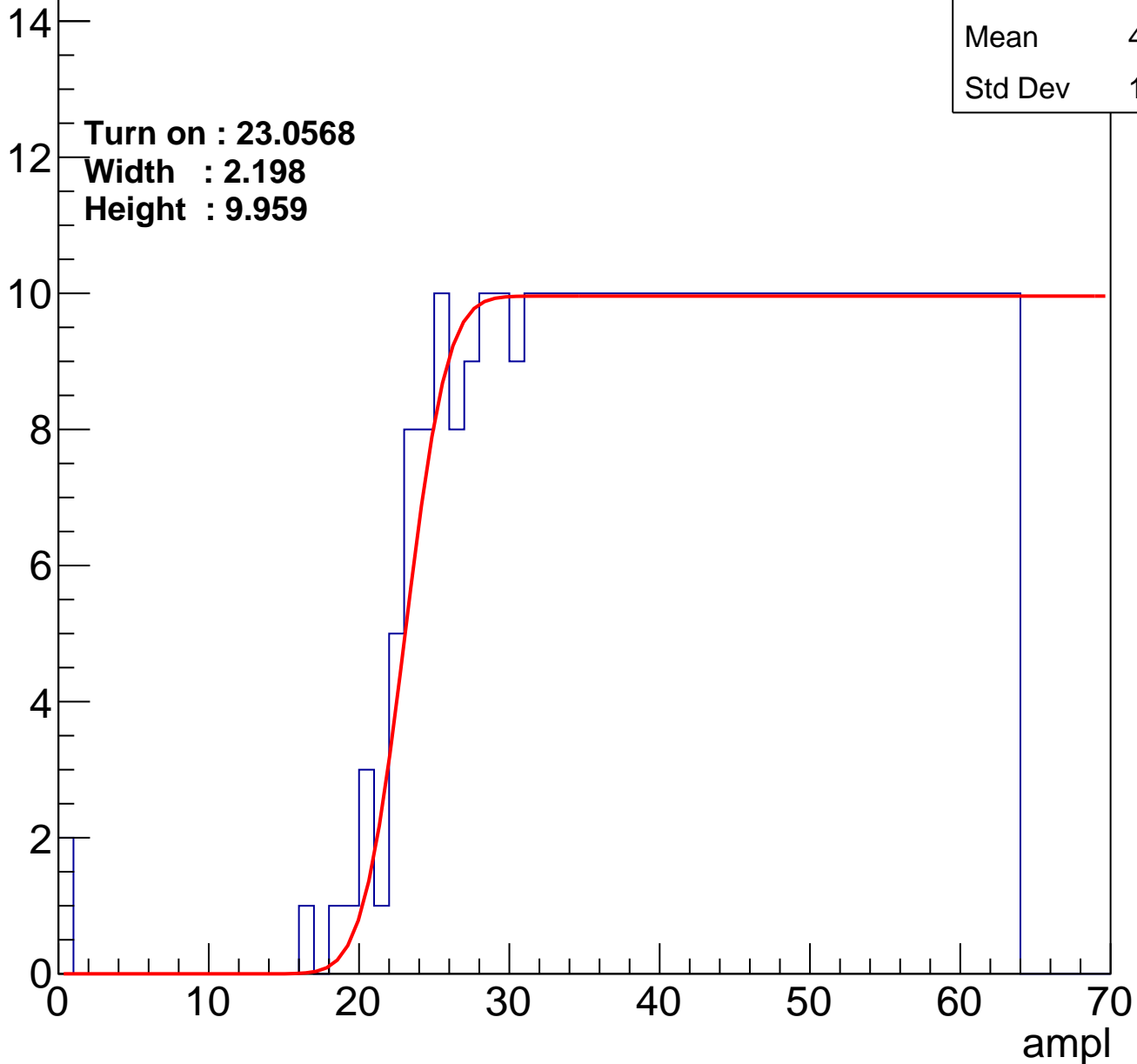
Entries	416
Mean	42.48
Std Dev	12.47

Turn on : 23.0568

Width : 2.198

Height : 9.959

Entry



B1L102S, U15-ch121

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.67
Std Dev	12.16

Turn on : 25.7595

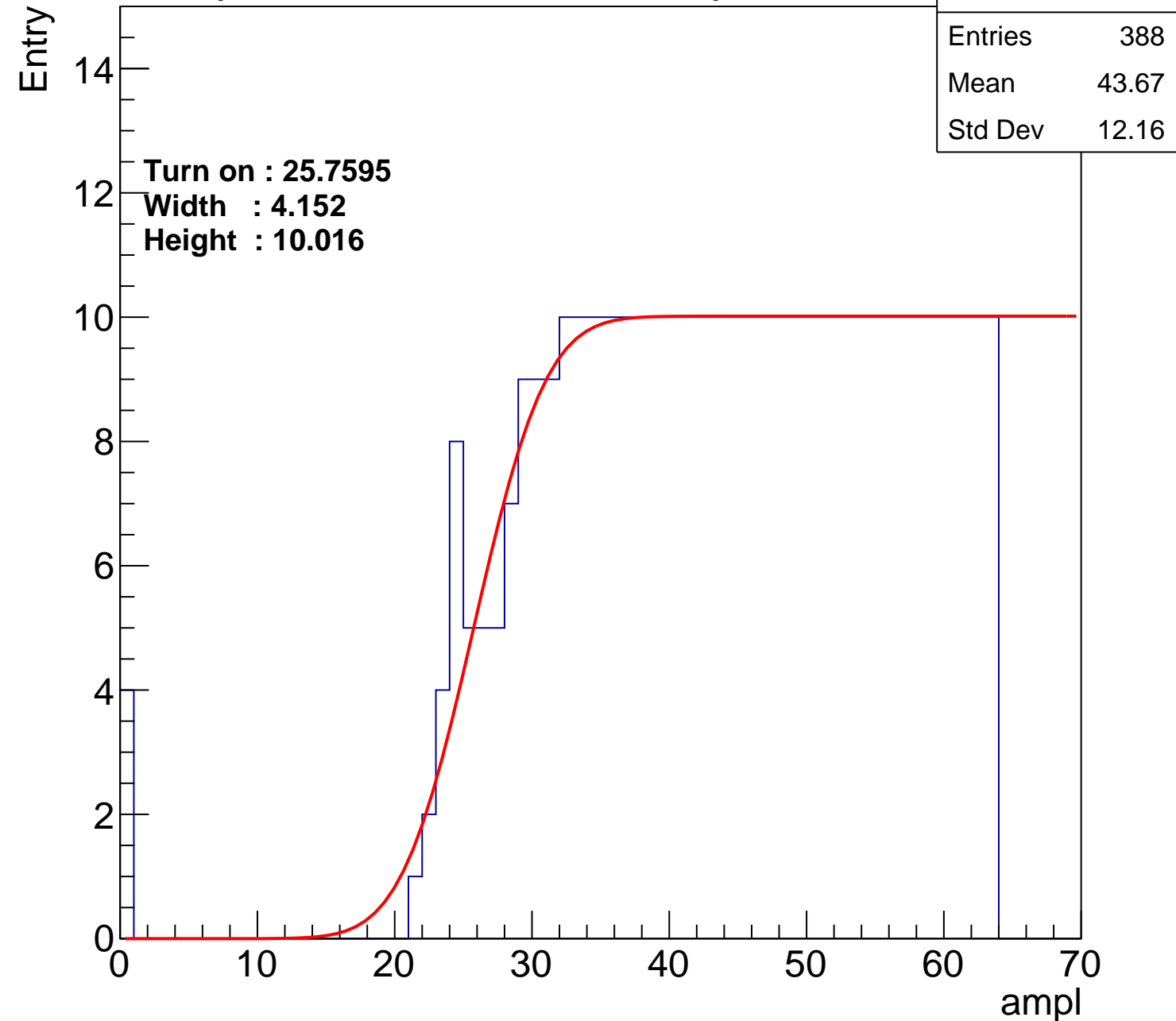
Width : 4.152

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch122

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.03
Std Dev	11.97

Turn on : 26.4987

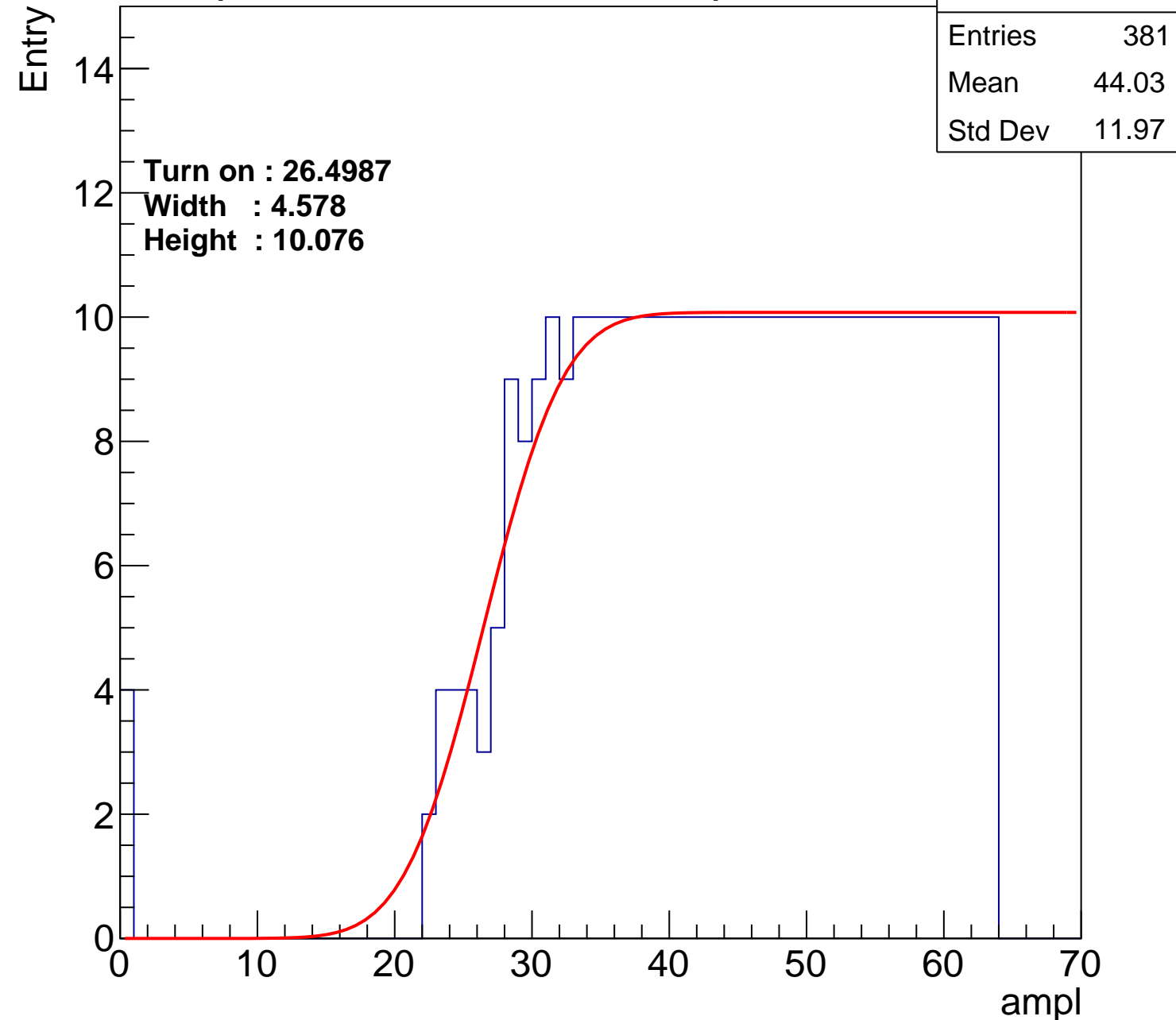
Width : 4.578

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch123

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.94
Std Dev	11.72

Turn on : 25.8367

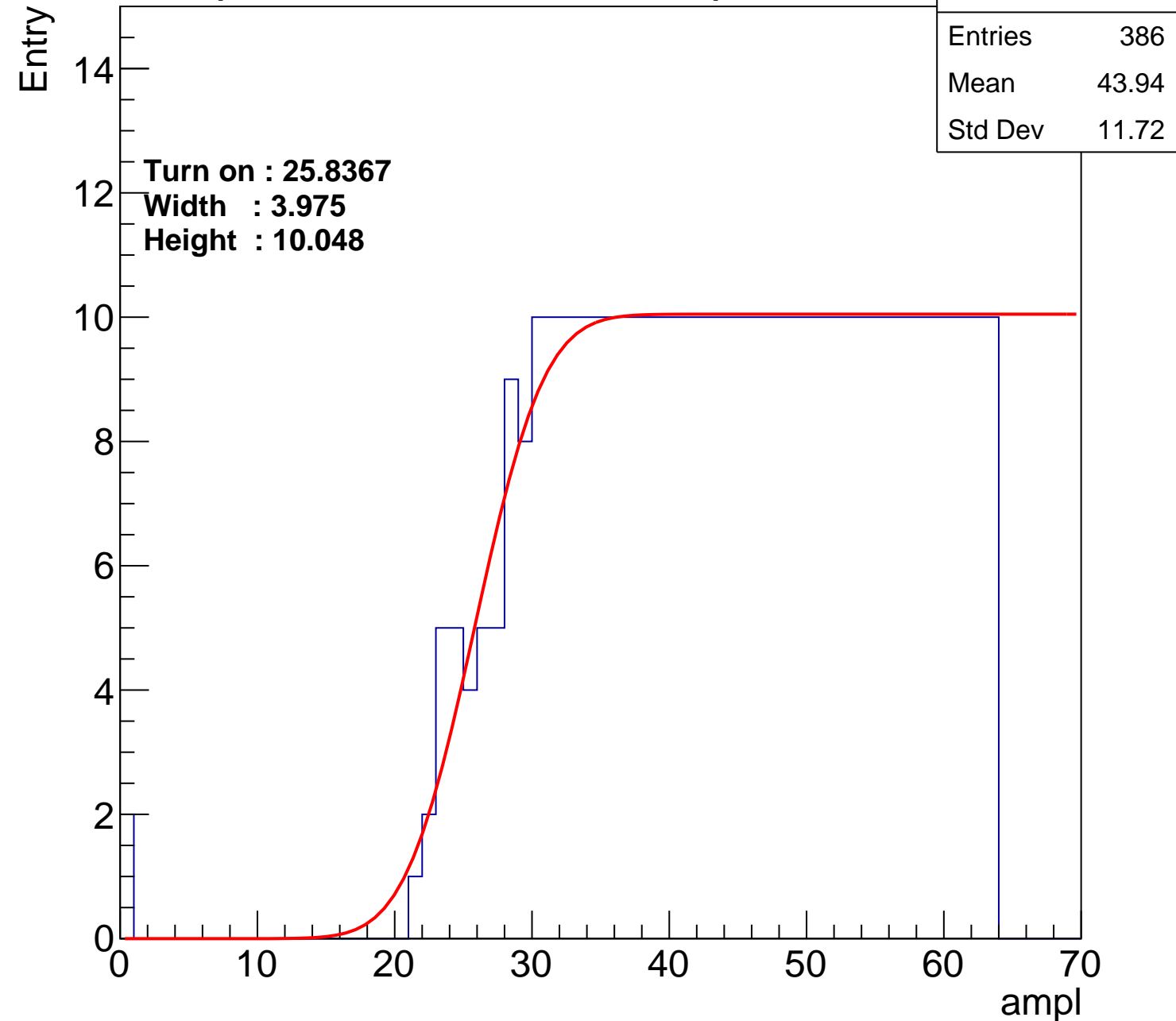
Width : 3.975

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch124

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	43.09
Std Dev	12.27

Turn on : 23.6596

Width : 2.728

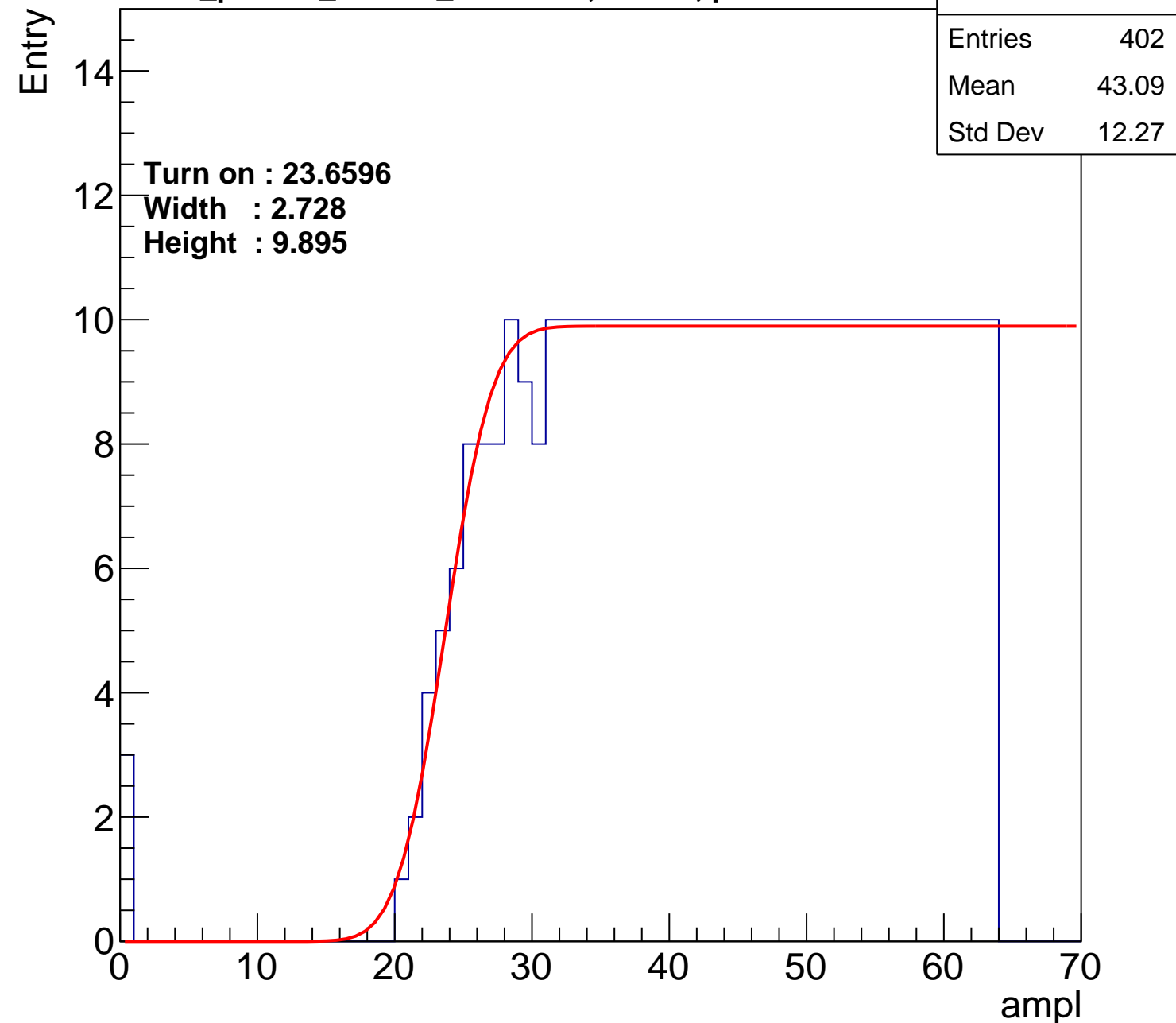
Height : 9.895

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch125

calib_packv5_042523_0143.root, FC#11, port A2

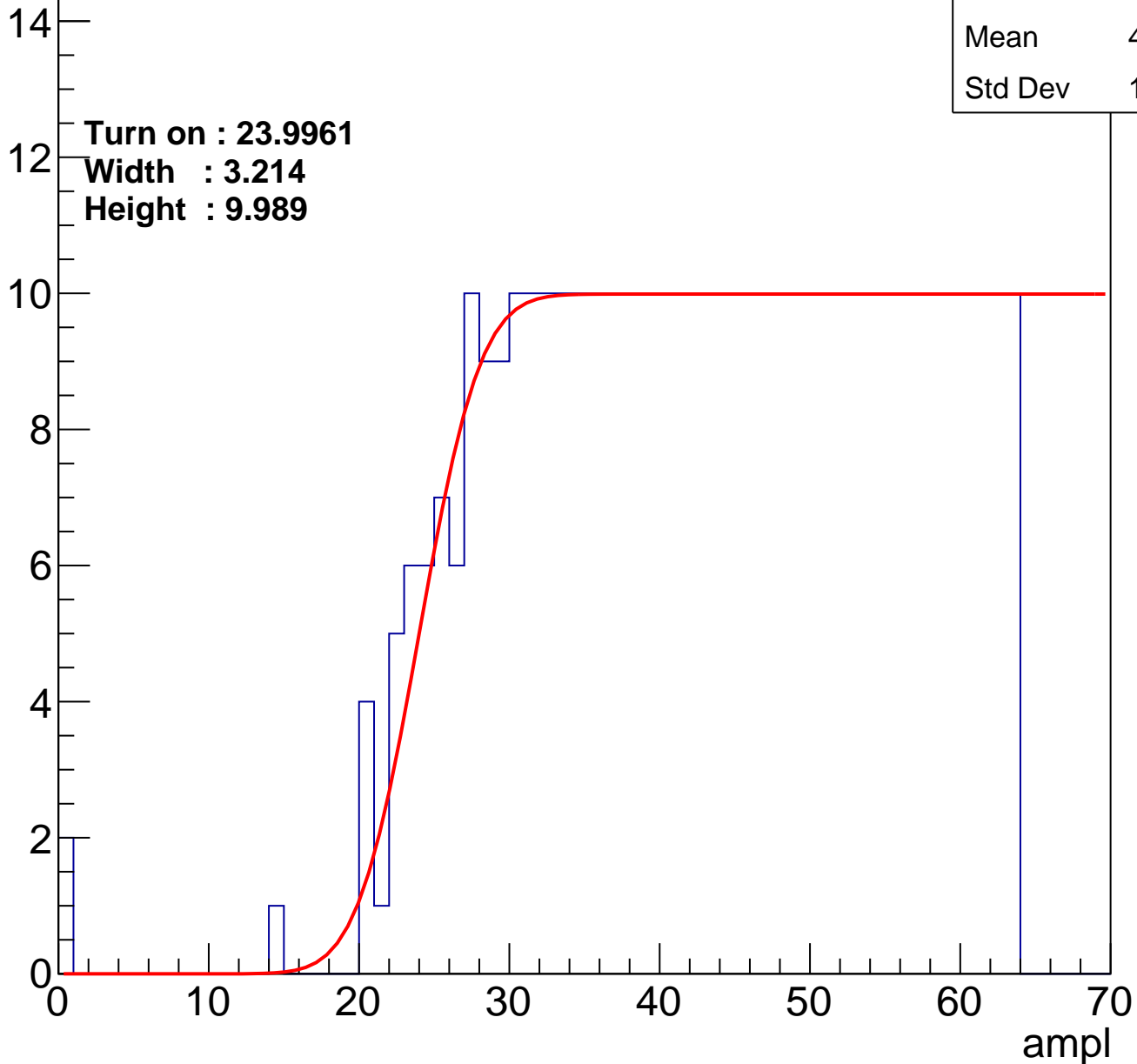
Entries	406
Mean	42.93
Std Dev	12.27

Turn on : 23.9961

Width : 3.214

Height : 9.989

Entry



B1L102S, U15-ch126

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.45
Std Dev	12.43

Turn on : 25.8677

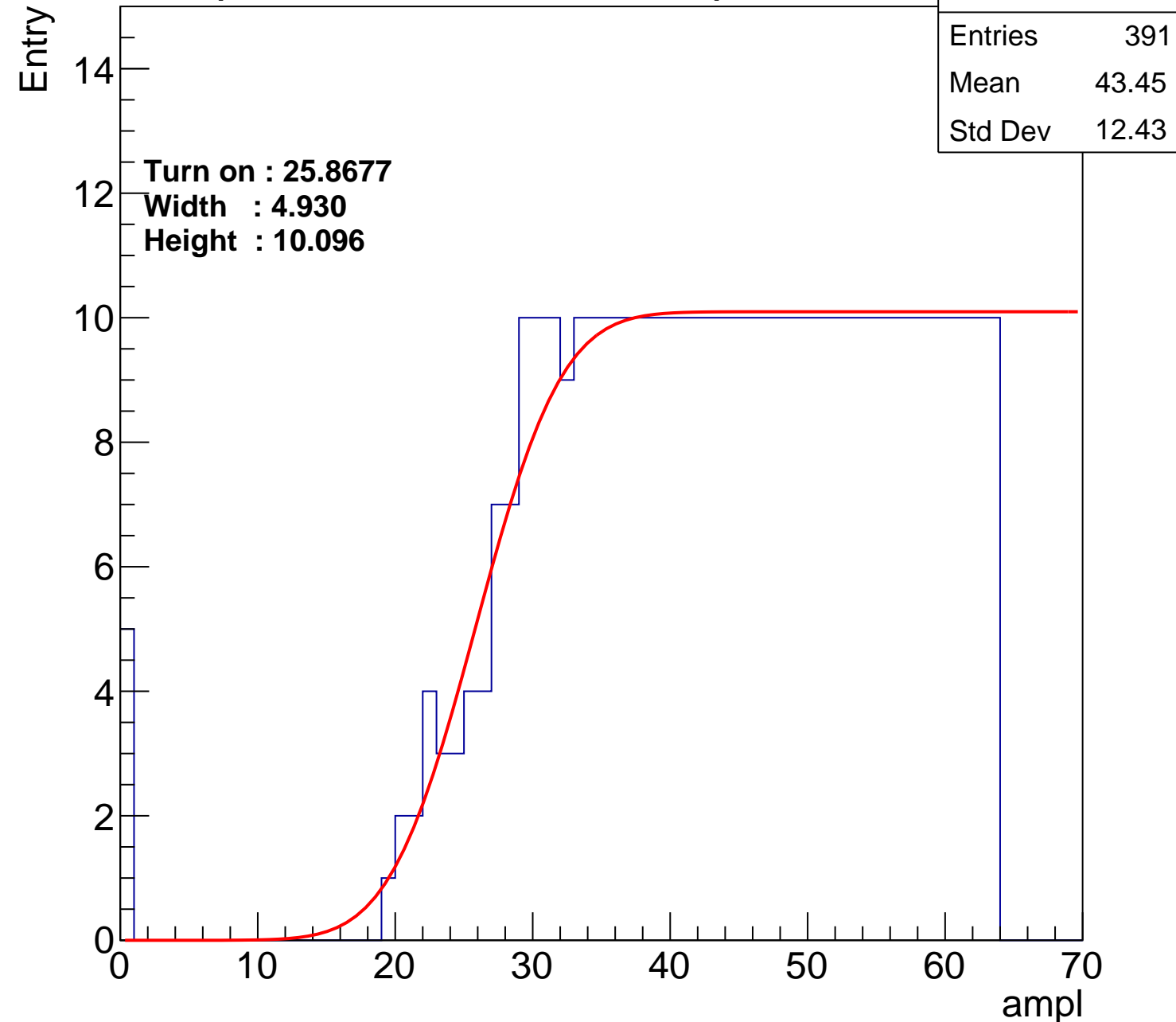
Width : 4.930

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	42.89
Std Dev	12.6

Turn on : 24.2337

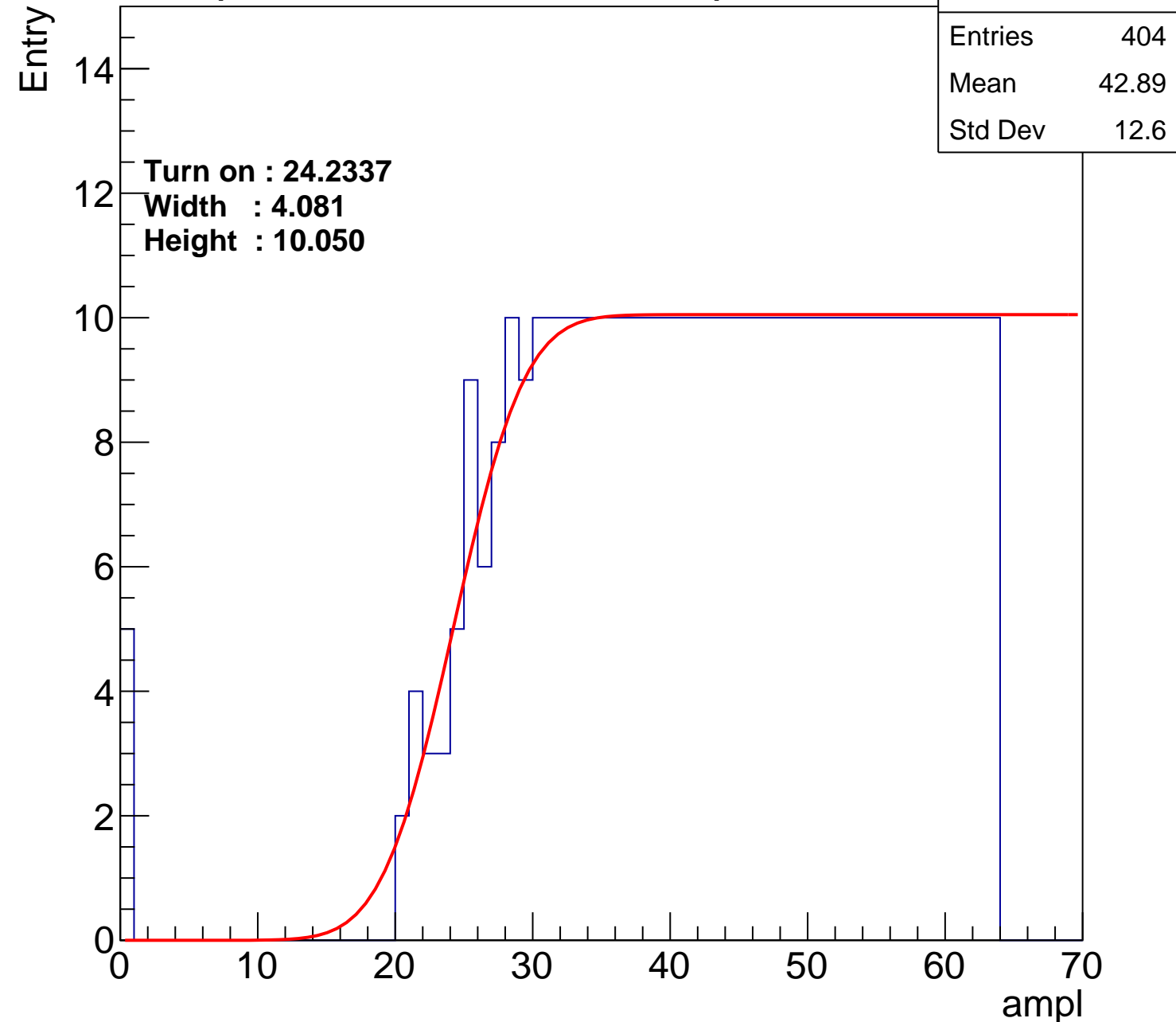
Width : 4.081

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U15-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	42.89
Std Dev	12.6

Turn on : 24.2337

Width : 4.081

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl

