

B0L102S, U19-ch0

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.38 |
| Std Dev | 11.3 |

Turn on : 26.3879

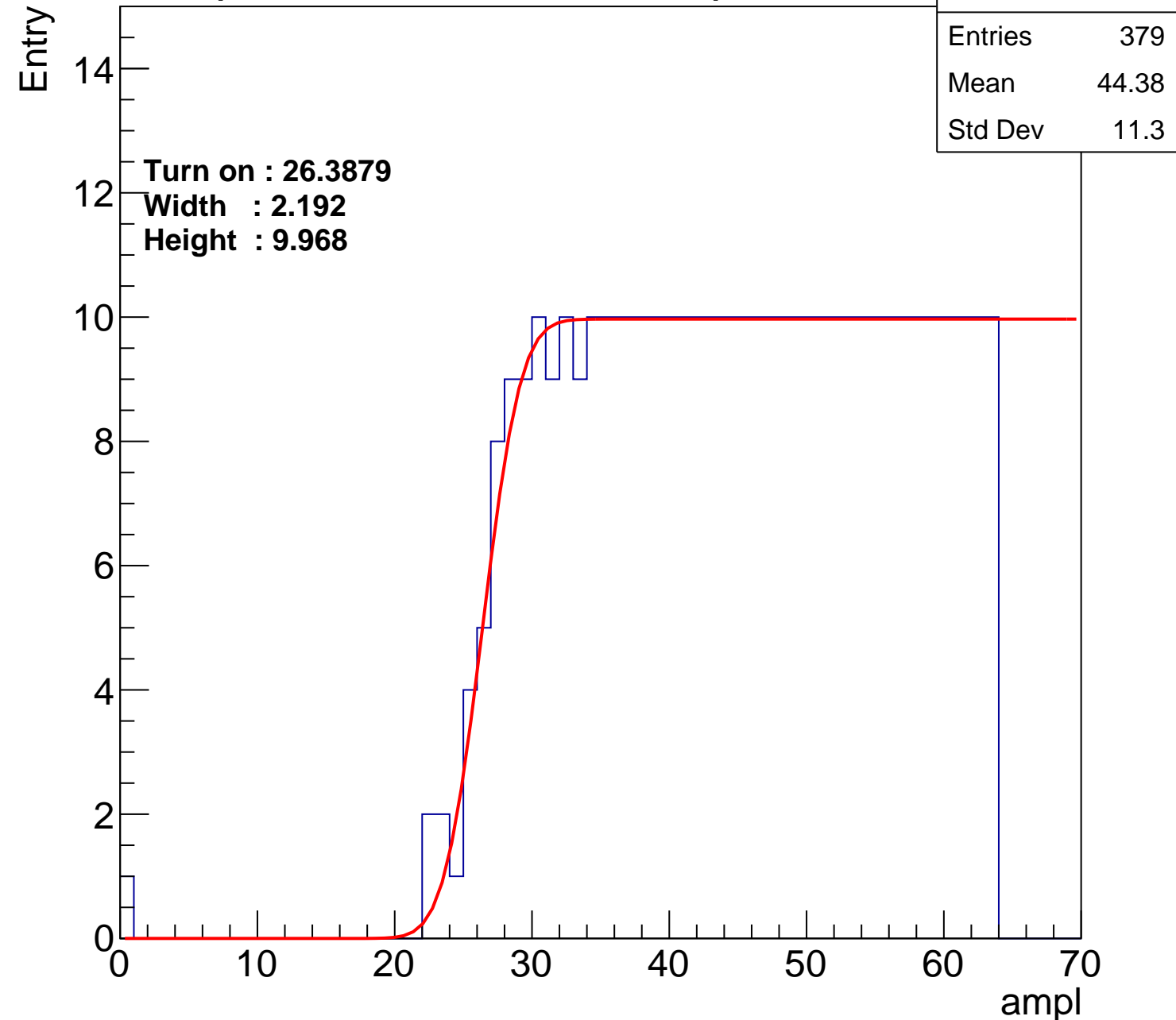
Width : 2.192

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch1

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.11 |
| Std Dev | 11.21 |

Turn on : 28.3149

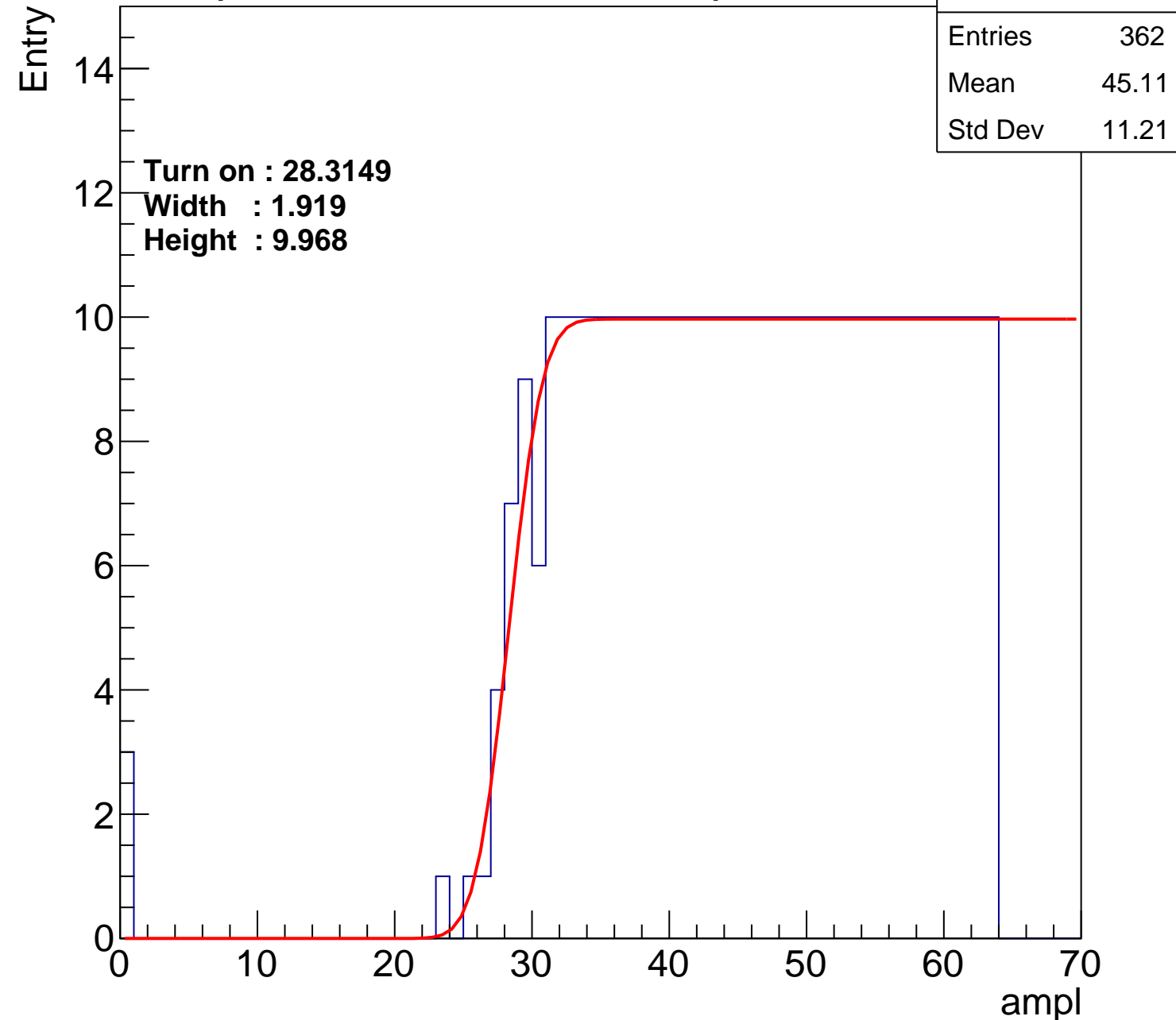
Width : 1.919

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch2

calib_packv5_042523_0143.root, FC#12, port B1

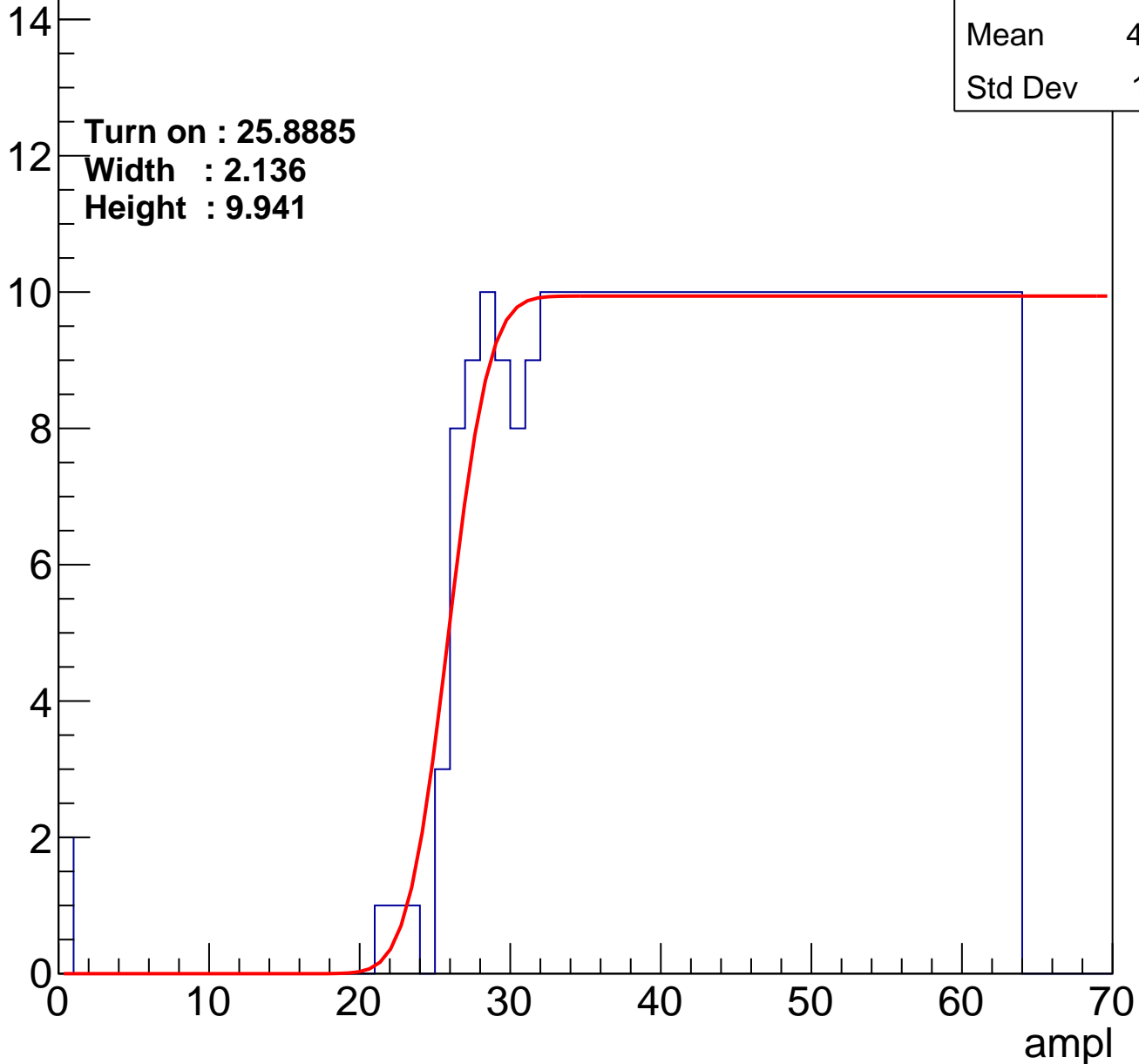
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.23 |
| Std Dev | 11.51 |

Turn on : 25.8885

Width : 2.136

Height : 9.941

Entry



B0L102S, U19-ch3

calib_packv5_042523_0143.root, FC#12, port B1

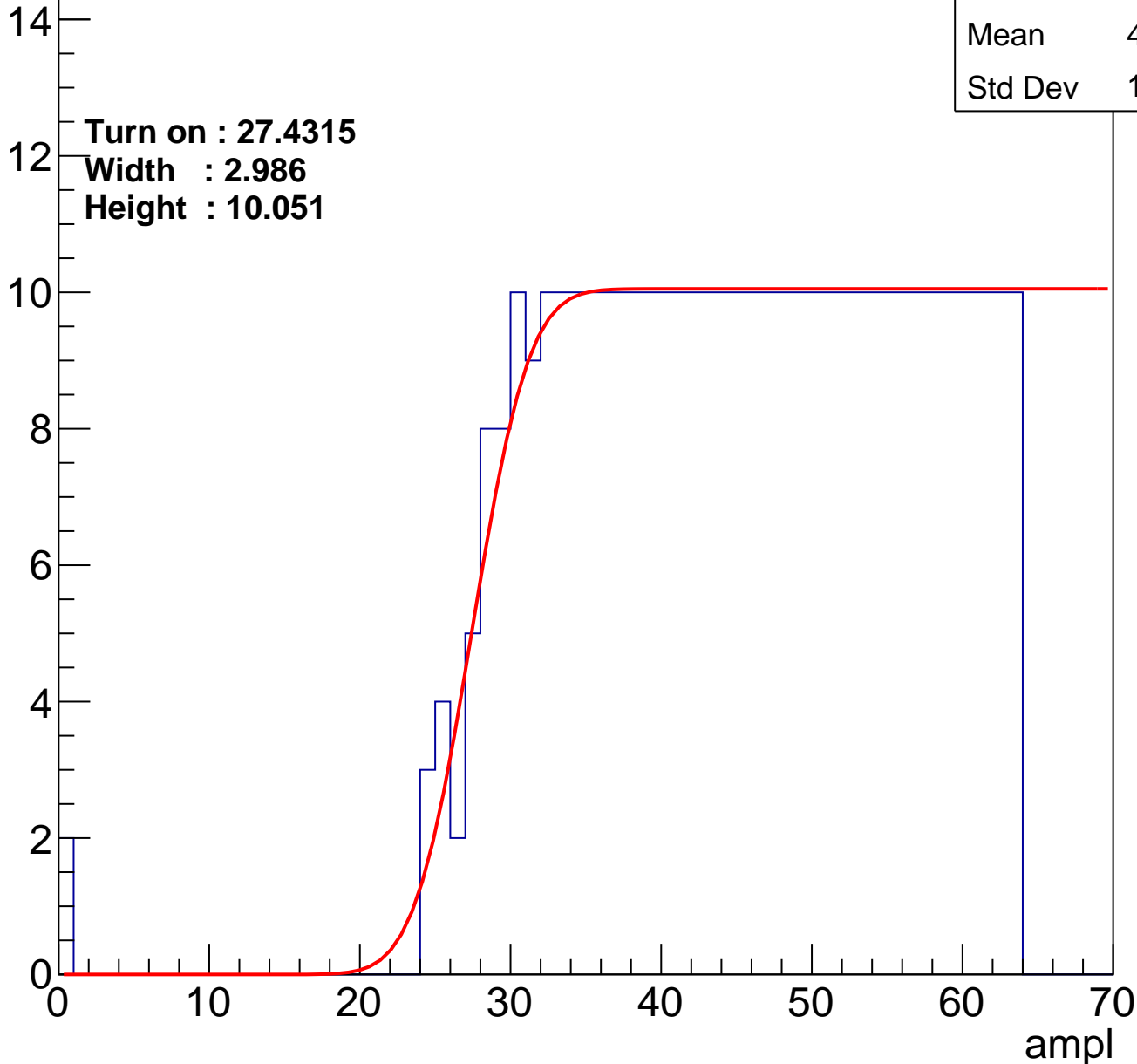
Entry

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.73 |
| Std Dev | 11.25 |

Turn on : 27.4315

Width : 2.986

Height : 10.051



B0L102S, U19-ch4

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.19 |
| Std Dev | 10.9 |

Turn on : 28.3835

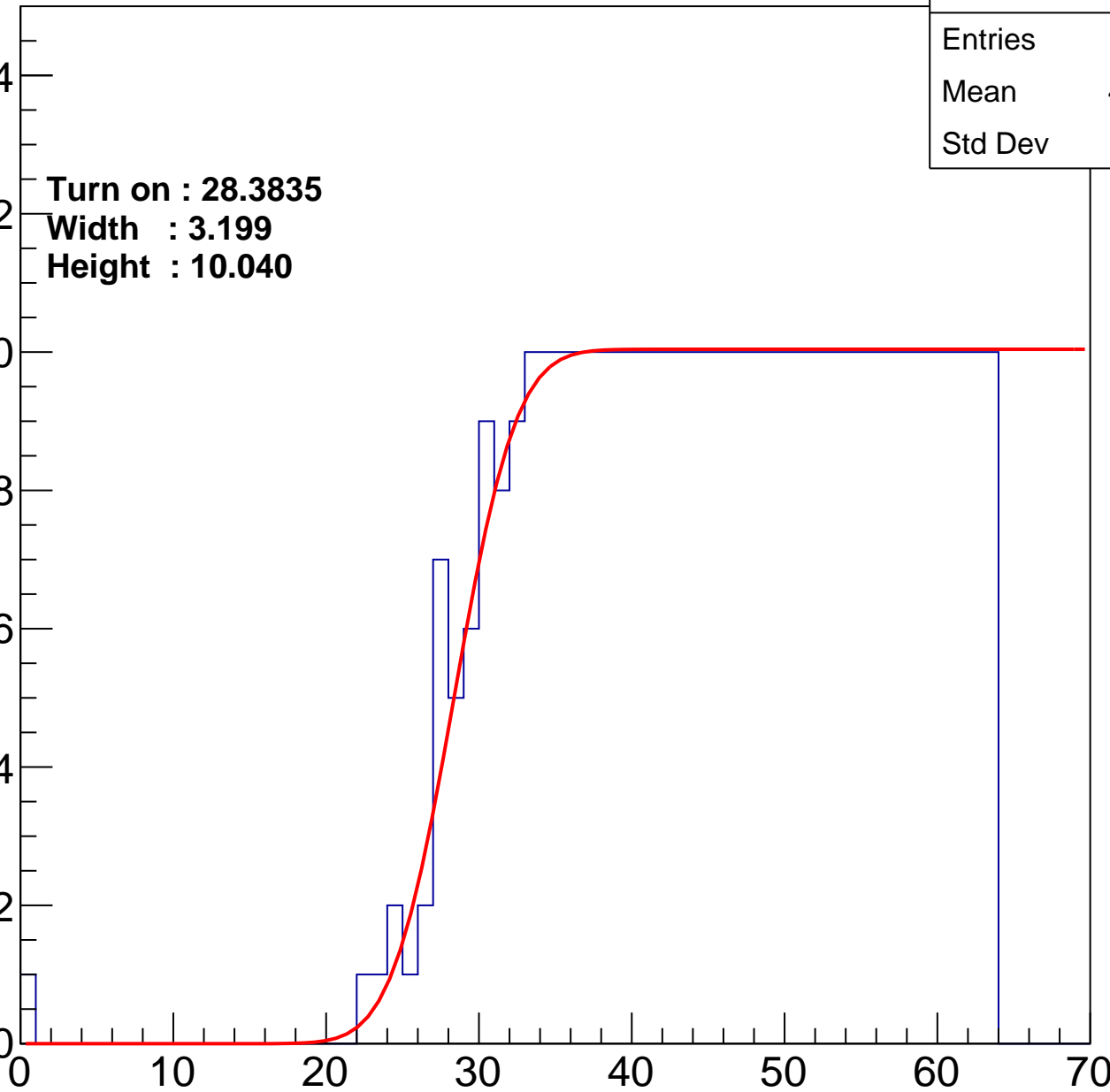
Width : 3.199

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch5

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.31 |
| Std Dev | 10.96 |

Turn on : 28.5899

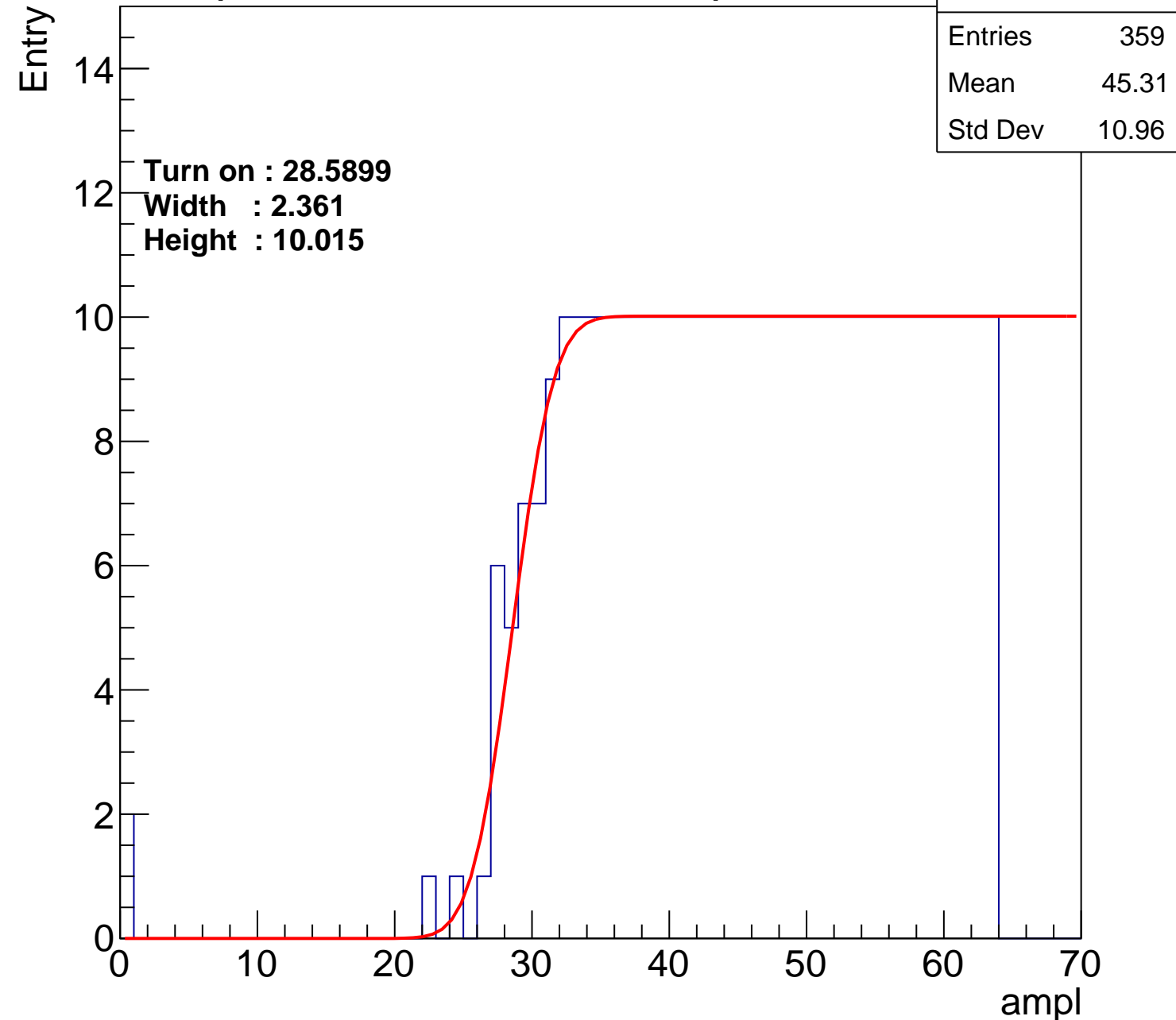
Width : 2.361

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch6

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.33 |
| Std Dev | 11.46 |

Turn on : 26.6452

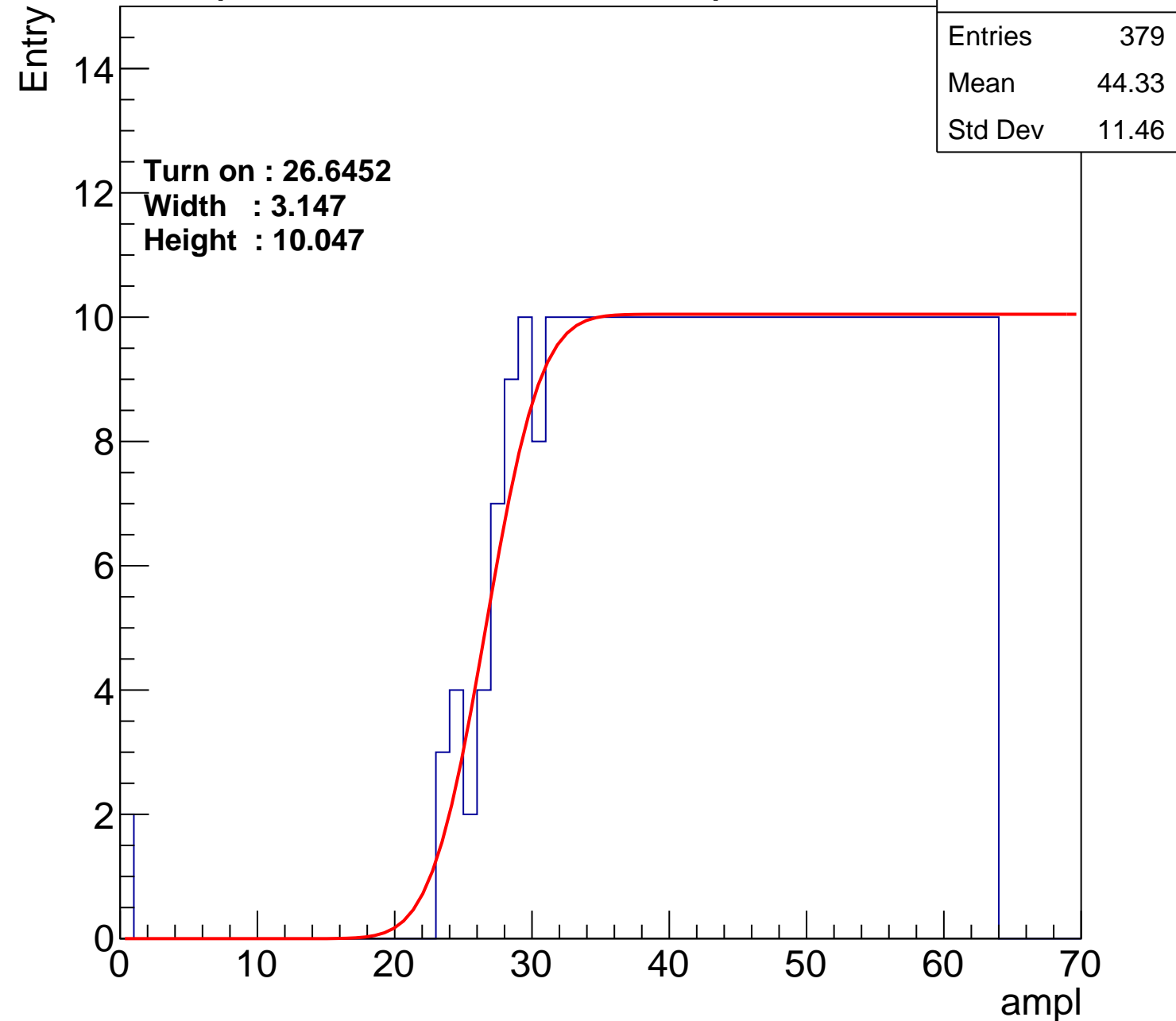
Width : 3.147

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch7

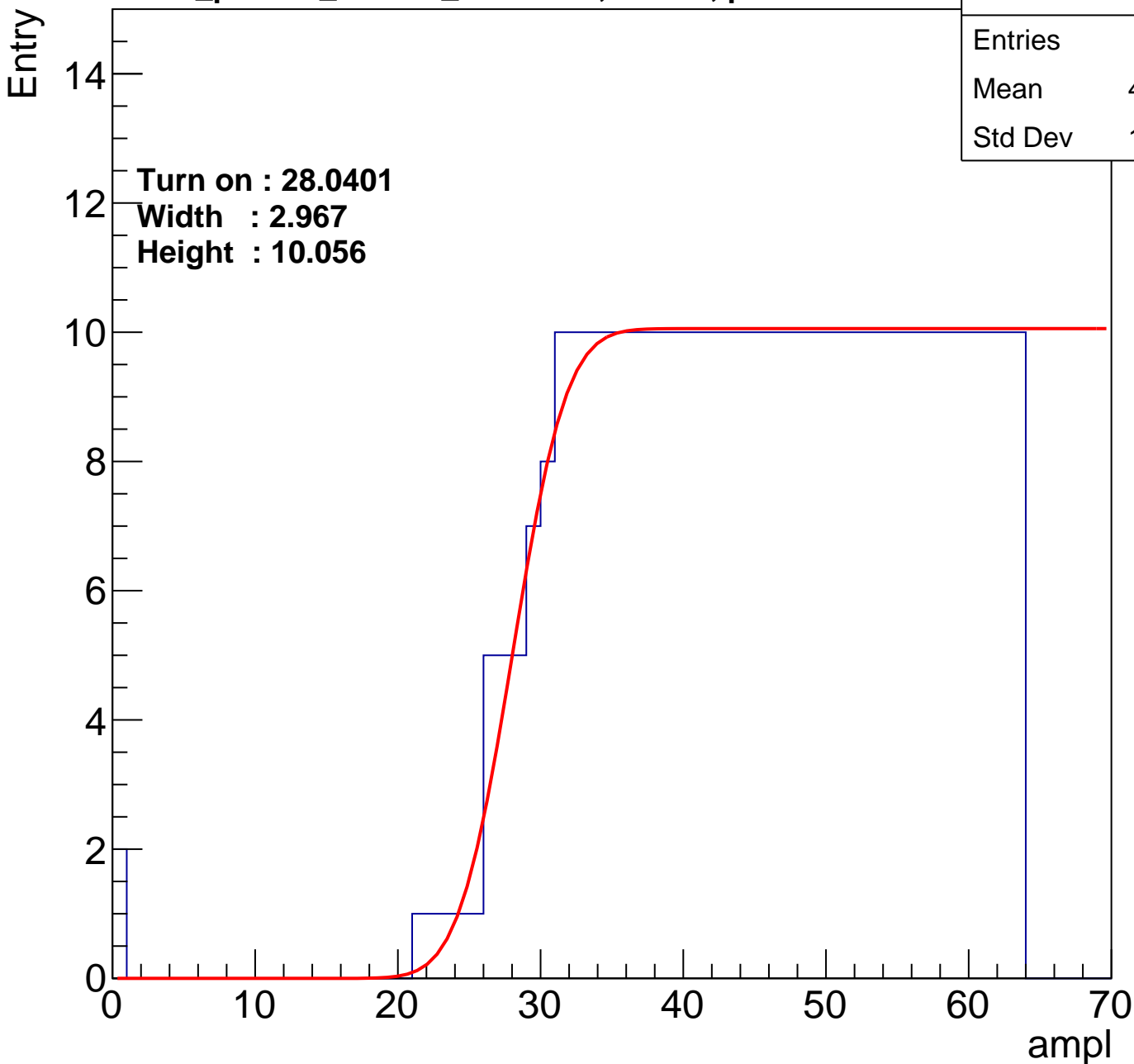
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 28.0401

Width : 2.967

Height : 10.056

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.89 |
| Std Dev | 11.22 |



B0L102S, U19-ch8

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.73 |
| Std Dev | 11.25 |

Turn on : 27.0974

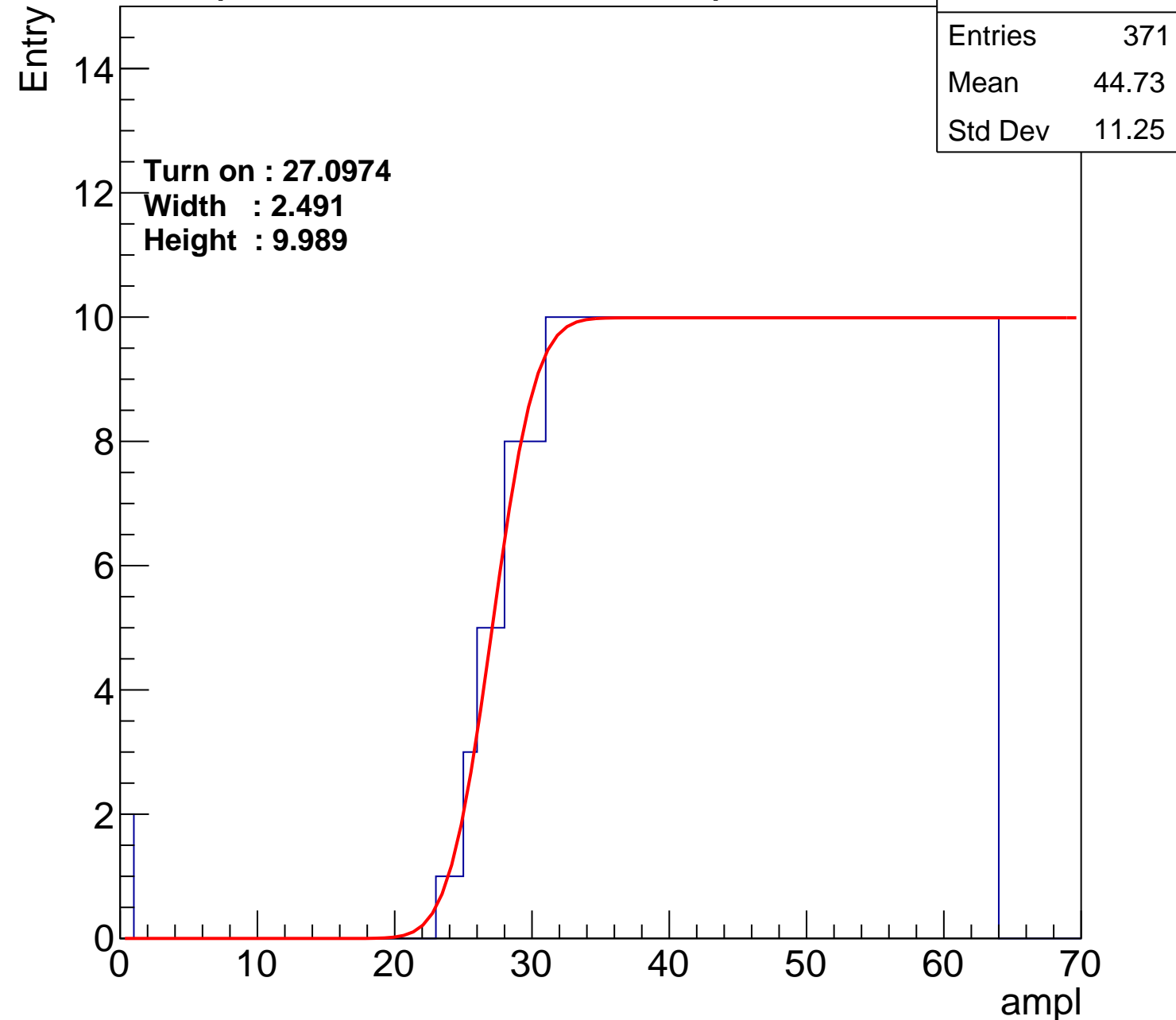
Width : 2.491

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch9

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.13 |
| Std Dev | 12.01 |

Turn on : 27.0279

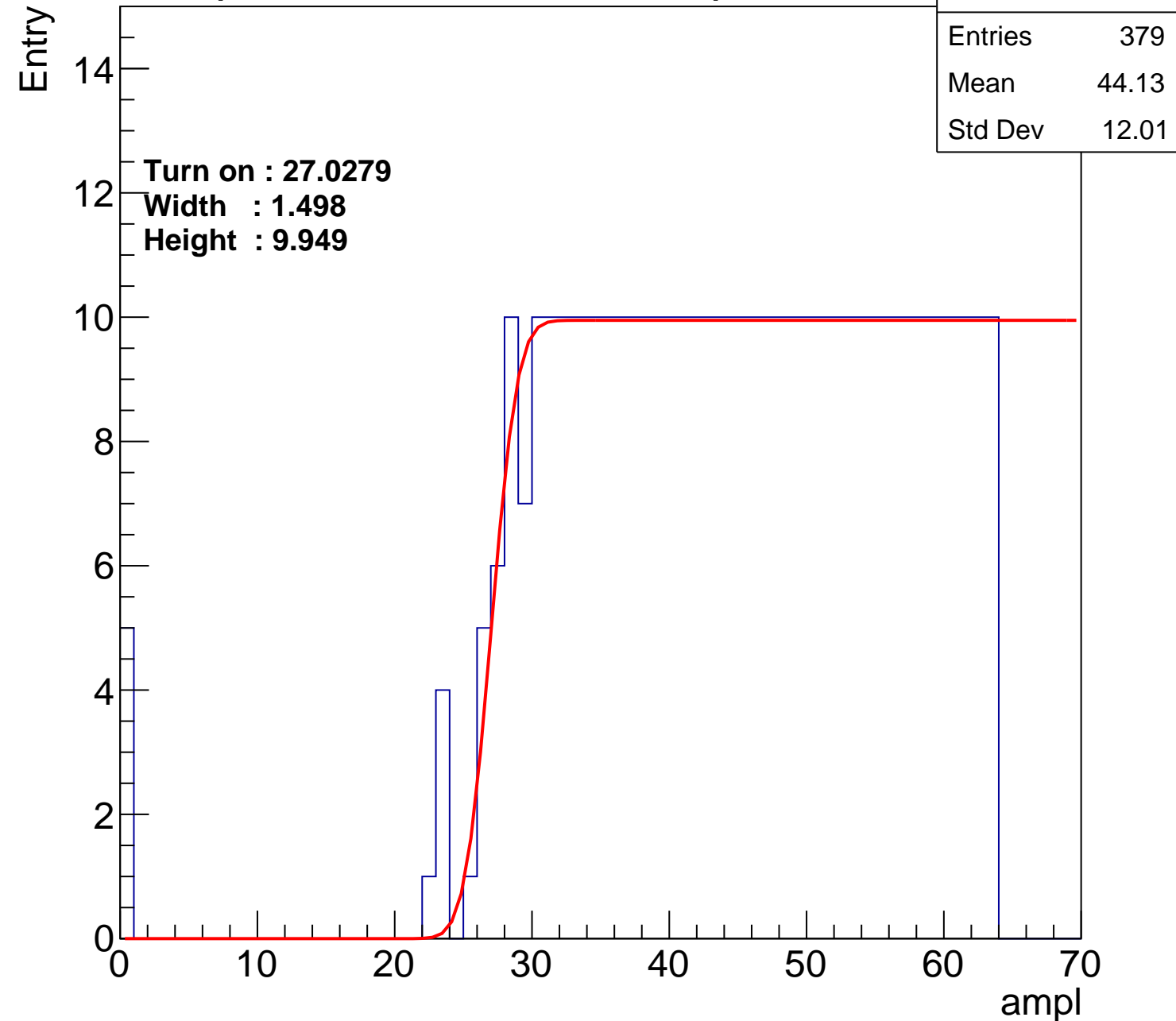
Width : 1.498

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch10

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.18 |
| Std Dev | 11.55 |

Turn on : 26.6118

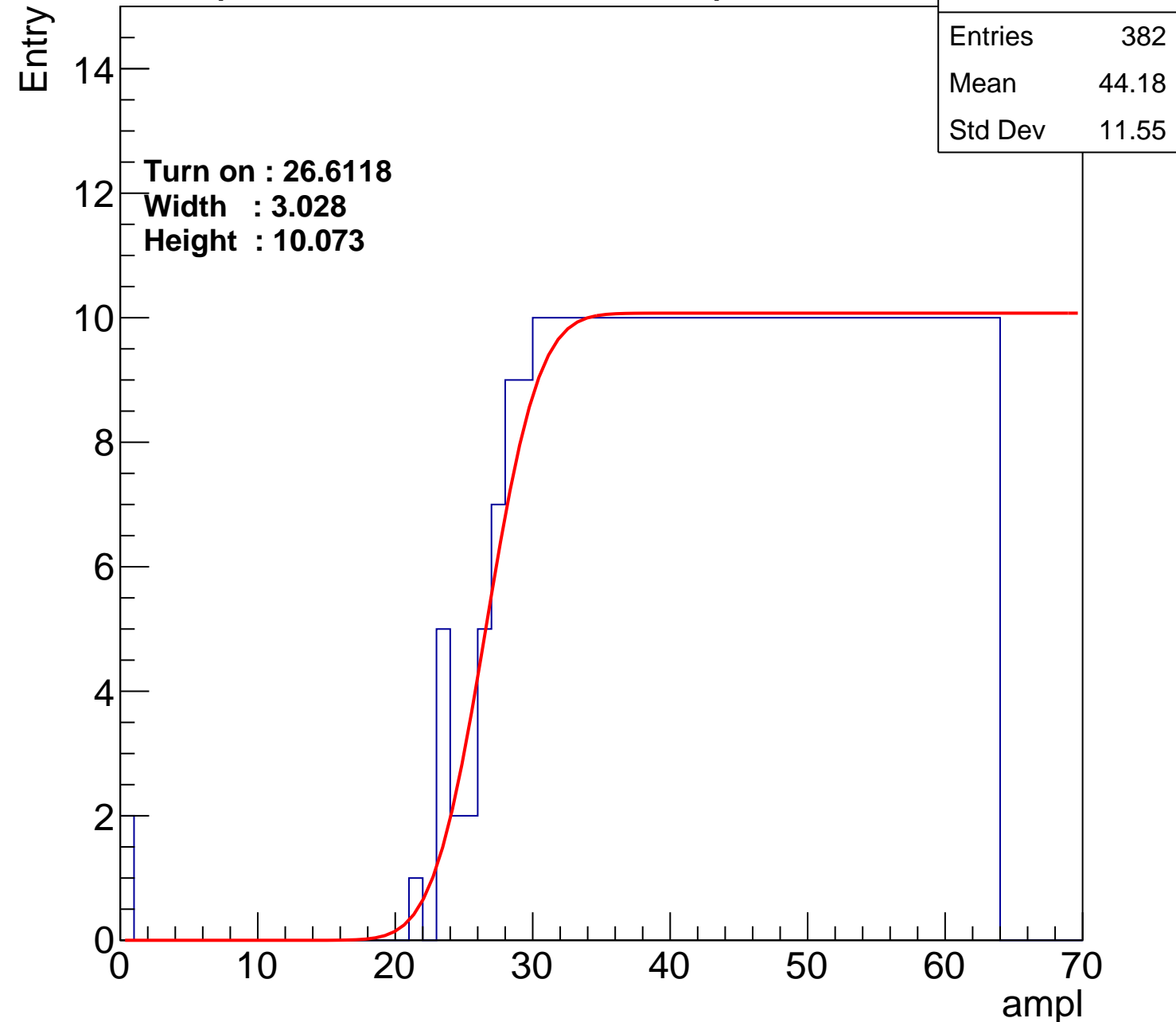
Width : 3.028

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch11

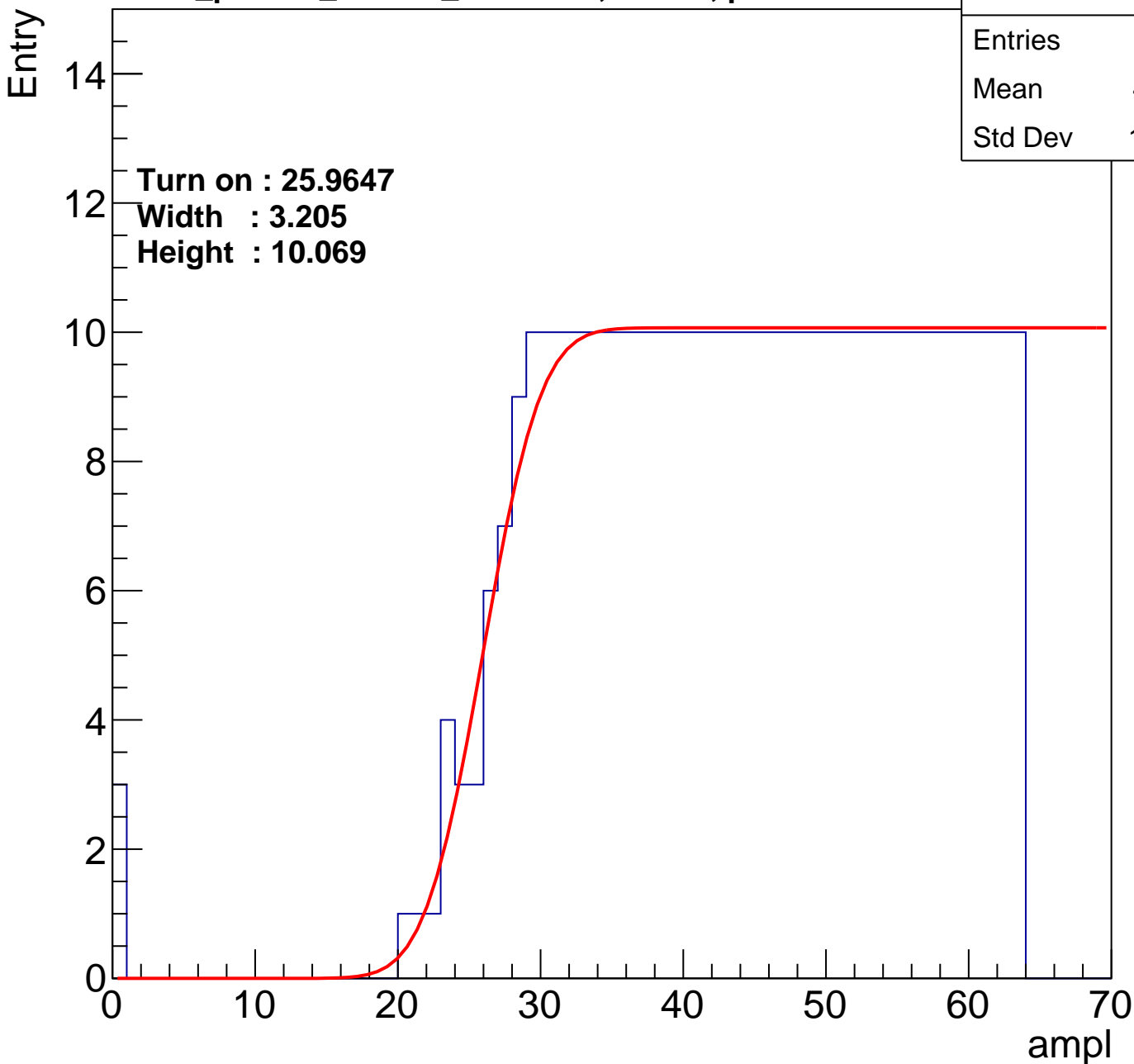
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.9647

Width : 3.205

Height : 10.069

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.81 |
| Std Dev | 11.88 |



B0L102S, U19-ch12

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.89 |
| Std Dev | 11.14 |

Turn on : 27.2932

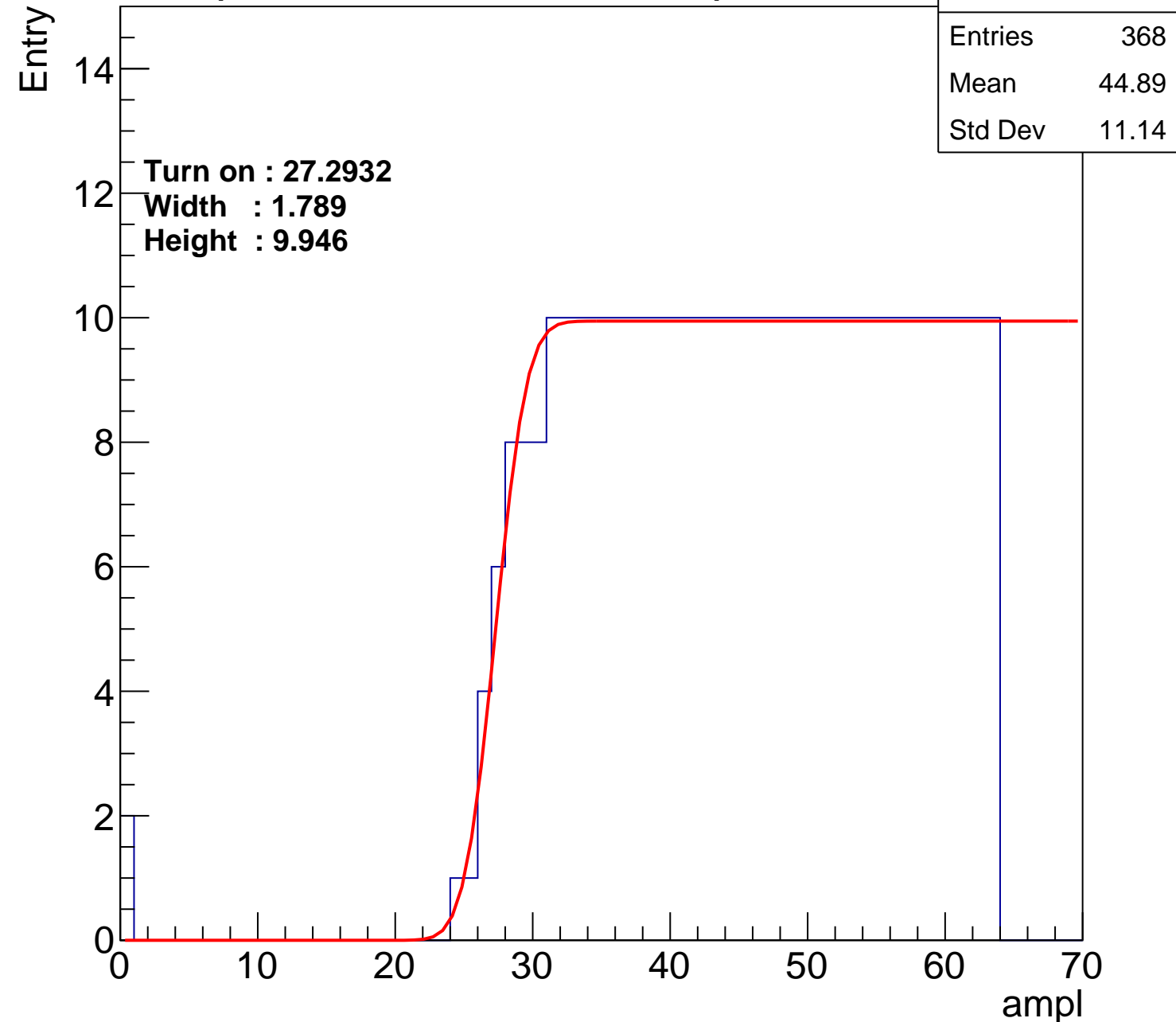
Width : 1.789

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch13

calib_packv5_042523_0143.root, FC#12, port B1

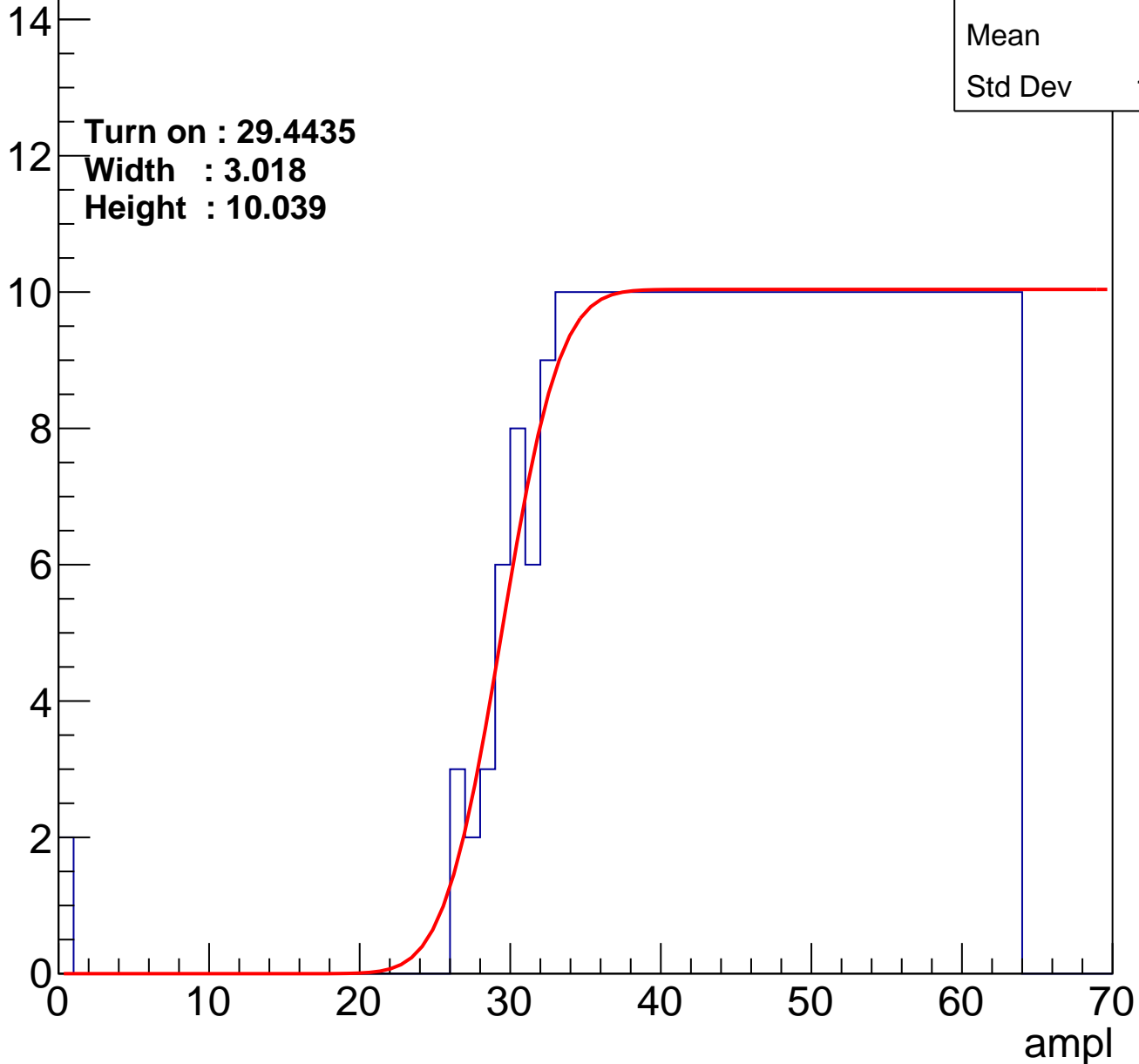
| | |
|---------|-------|
| Entries | 349 |
| Mean | 45.8 |
| Std Dev | 10.71 |

Turn on : 29.4435

Width : 3.018

Height : 10.039

Entry



B0L102S, U19-ch14

calib_packv5_042523_0143.root, FC#12, port B1

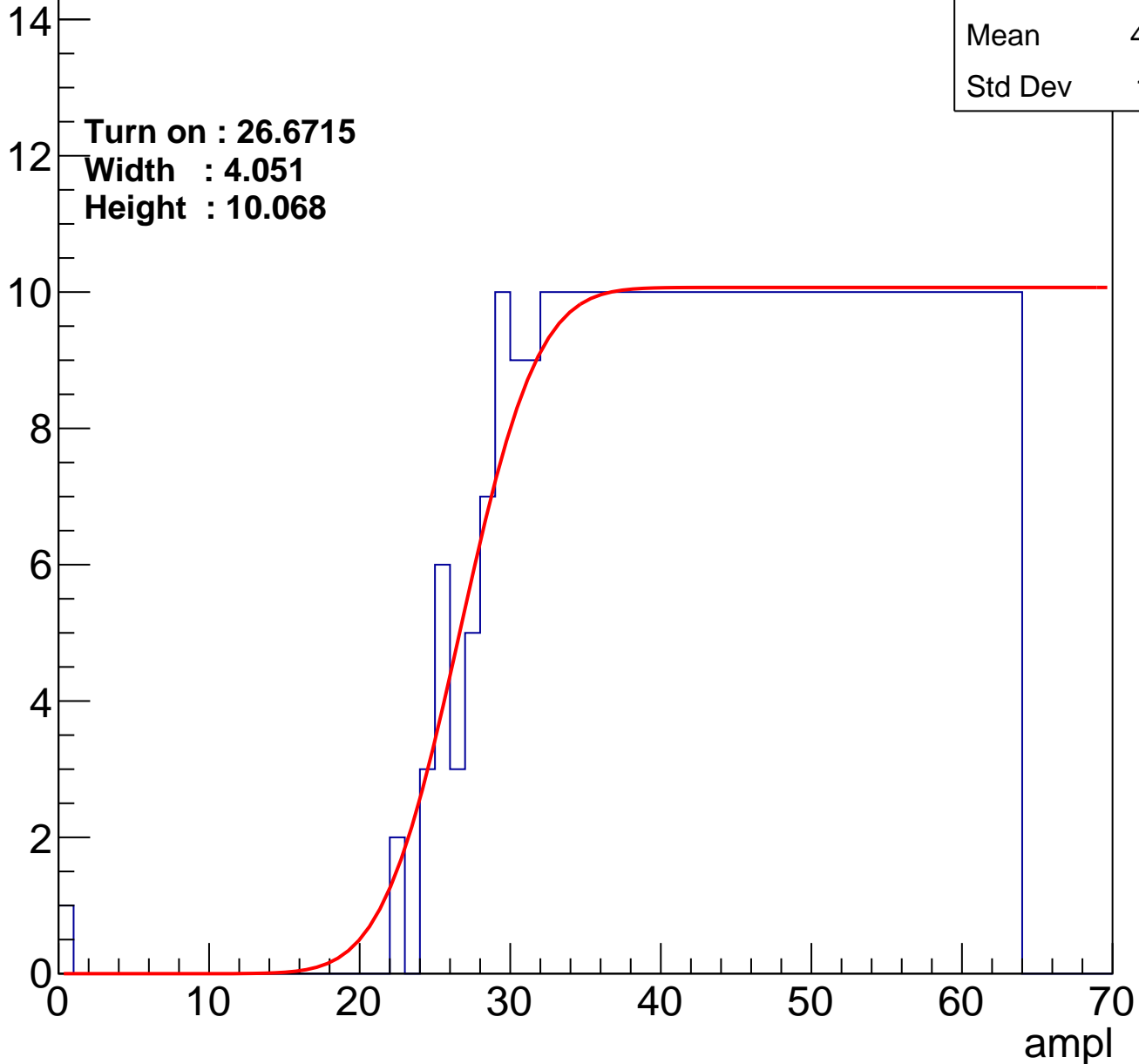
| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.57 |
| Std Dev | 11.21 |

Turn on : 26.6715

Width : 4.051

Height : 10.068

Entry



B0L102S, U19-ch15

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.67 |
| Std Dev | 11.1 |

Turn on : 26.6224

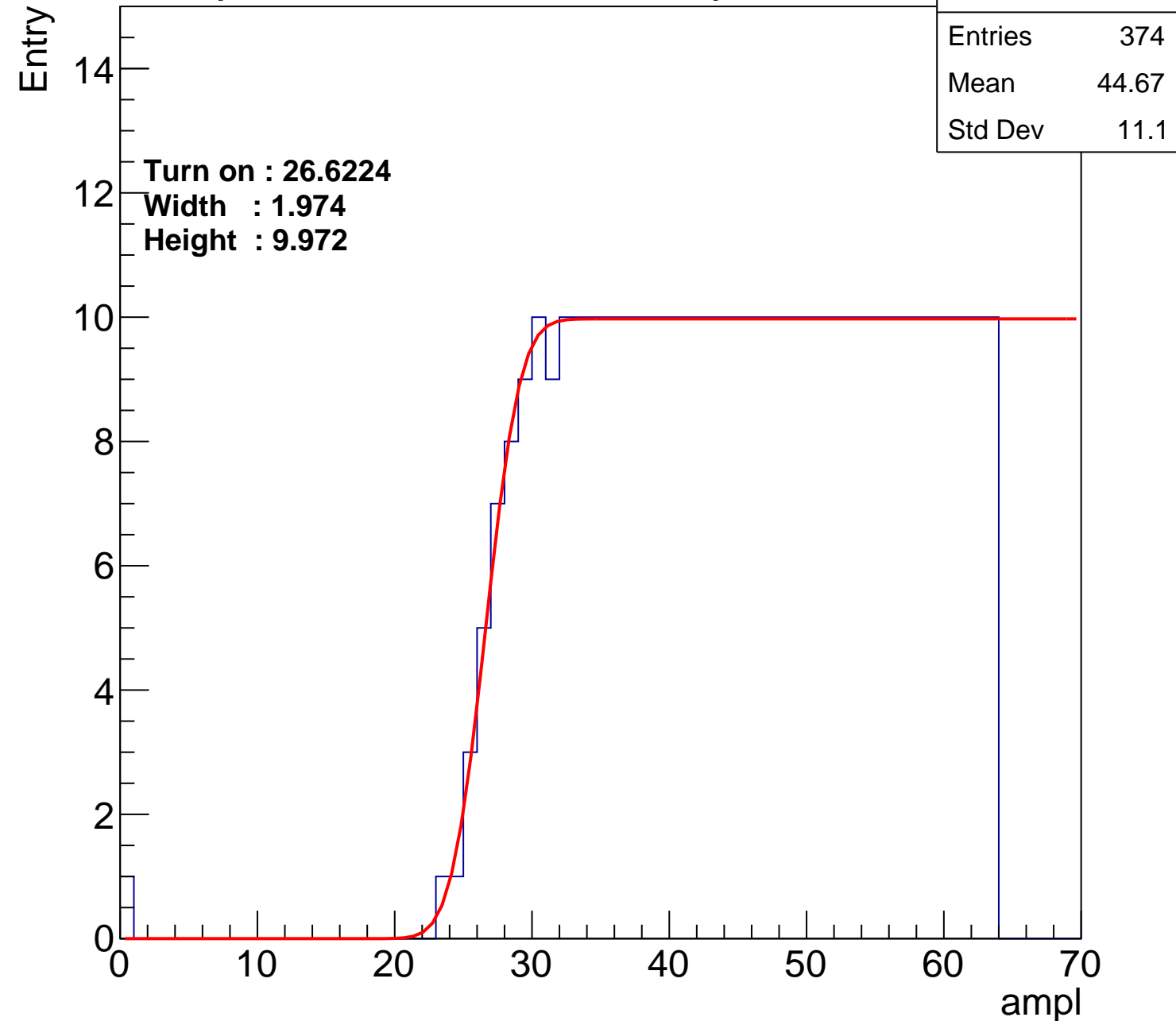
Width : 1.974

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch16

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.05 |
| Std Dev | 11.64 |

Turn on : 26.1897

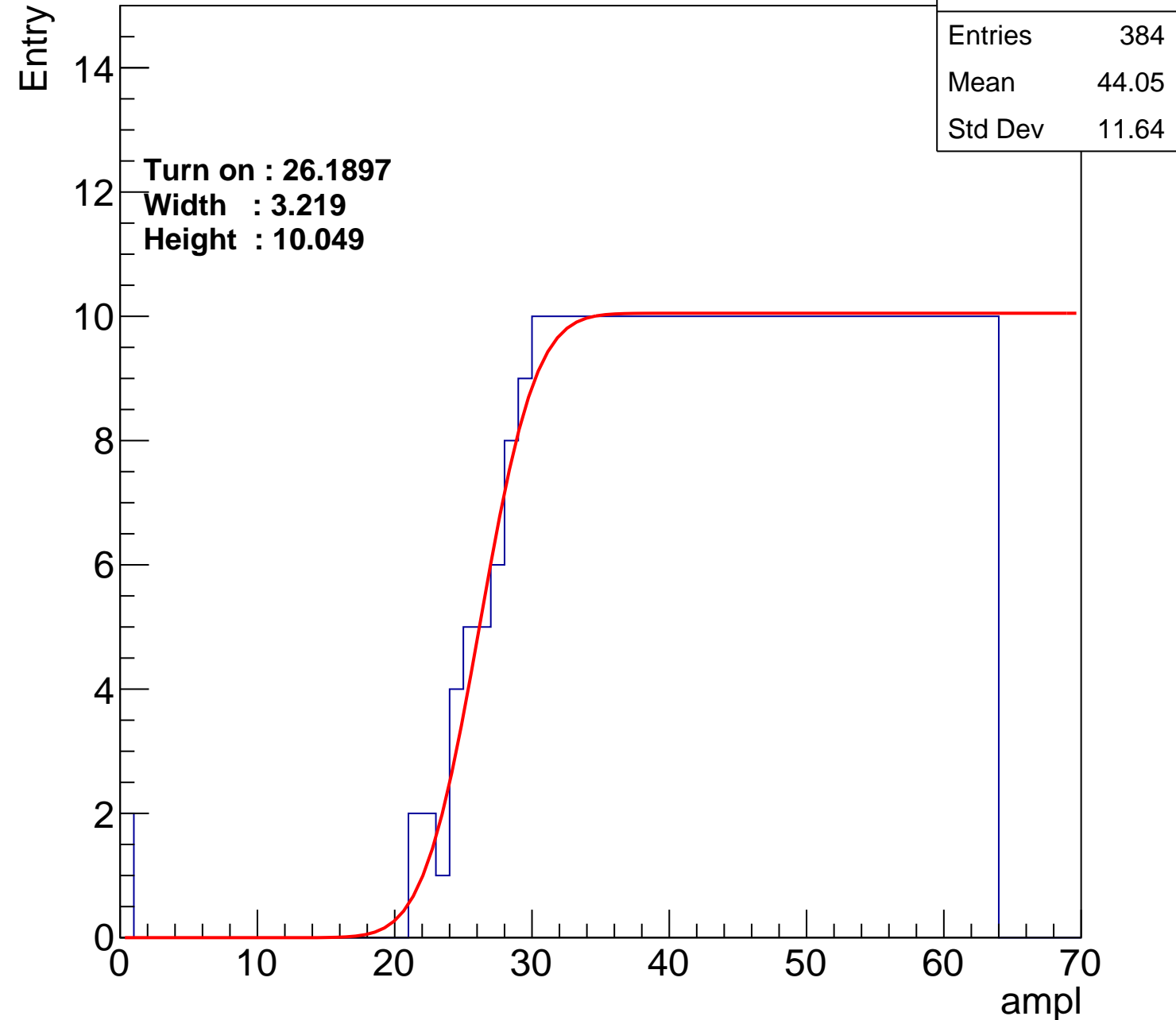
Width : 3.219

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch17

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 354 |
| Mean | 45.65 |
| Std Dev | 10.58 |

Turn on : 28.9658

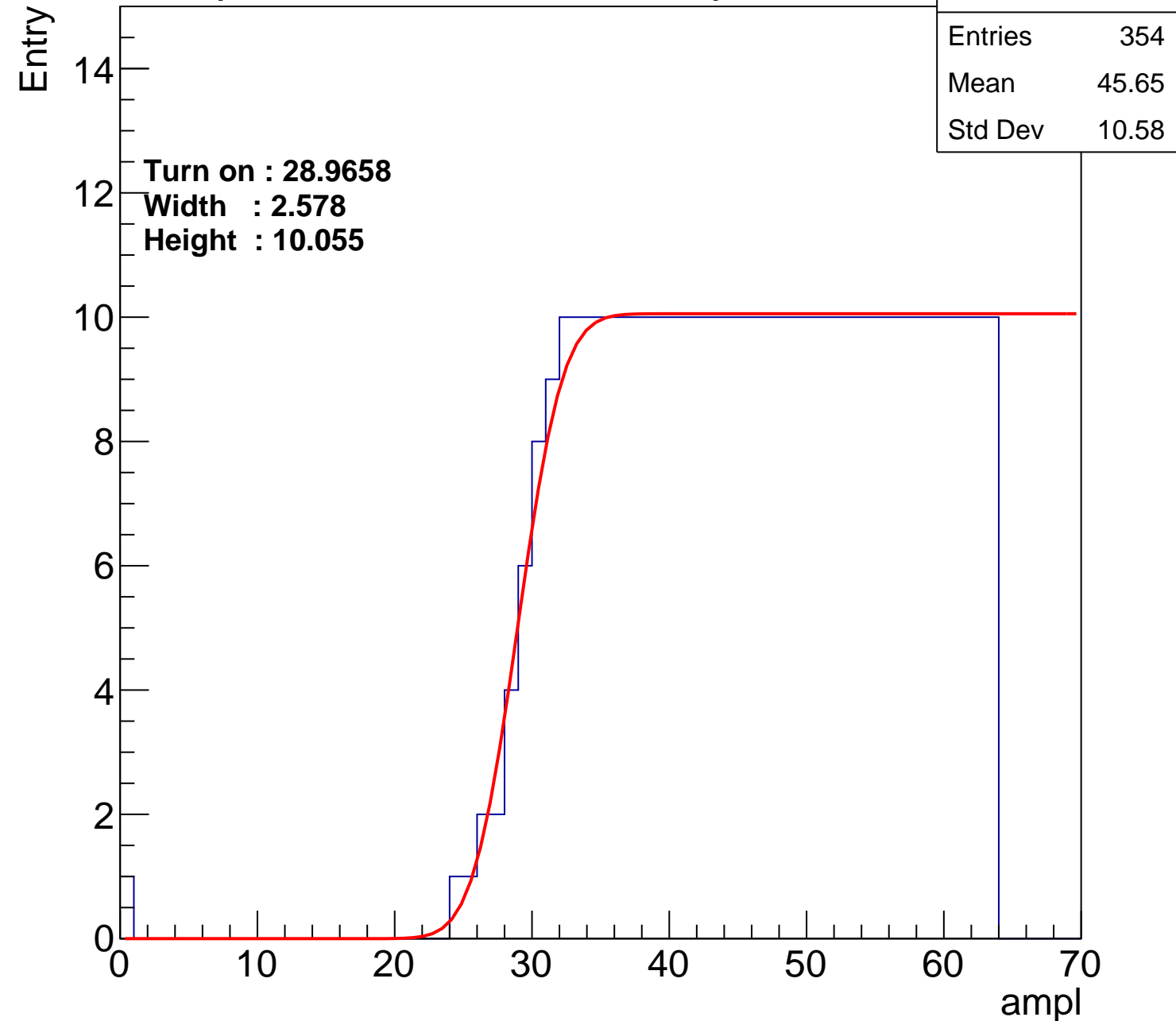
Width : 2.578

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch18

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.53 |
| Std Dev | 11.54 |

Turn on : 27.3010

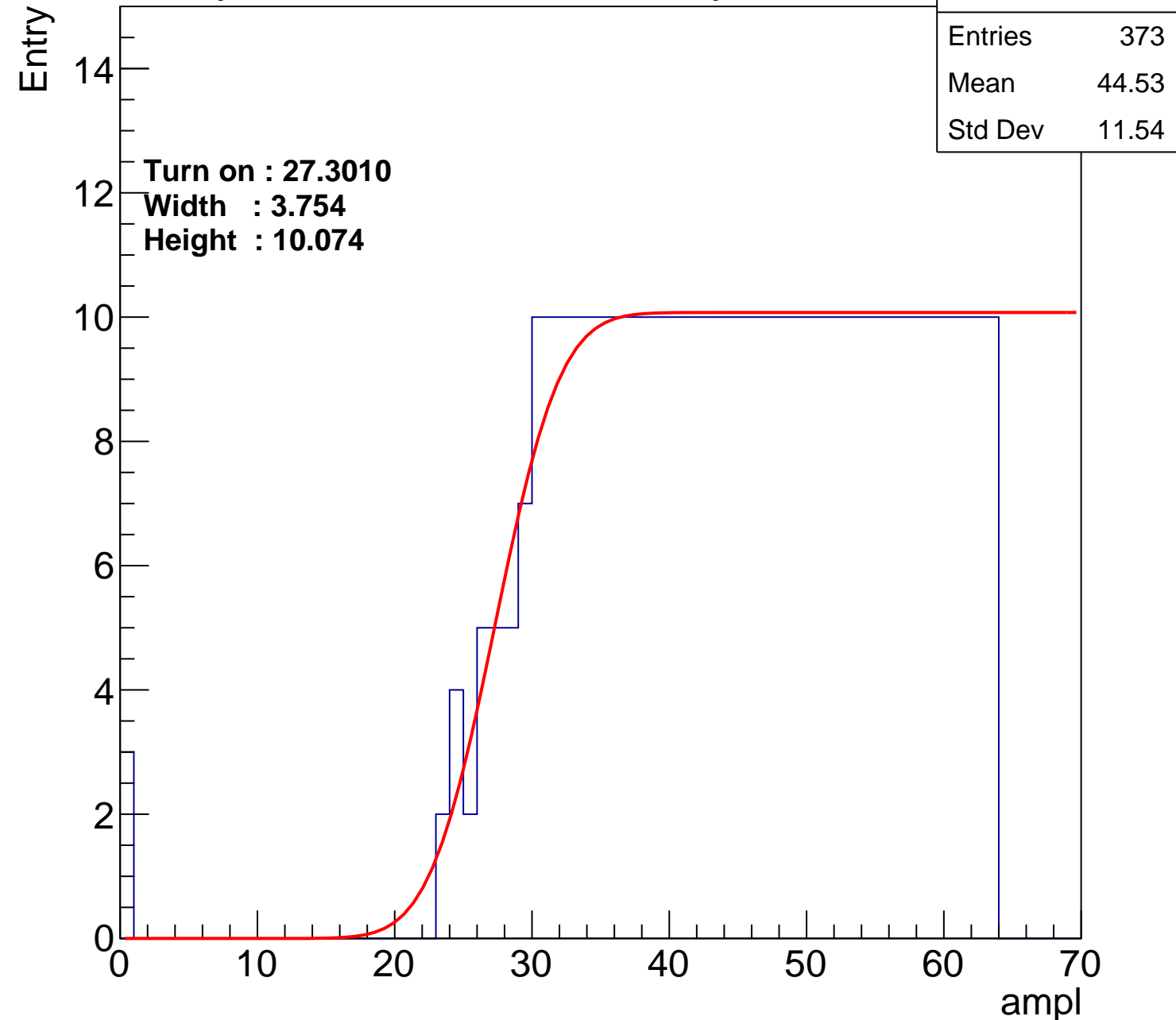
Width : 3.754

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch19

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.83 |
| Std Dev | 11.33 |

Turn on : 27.5657

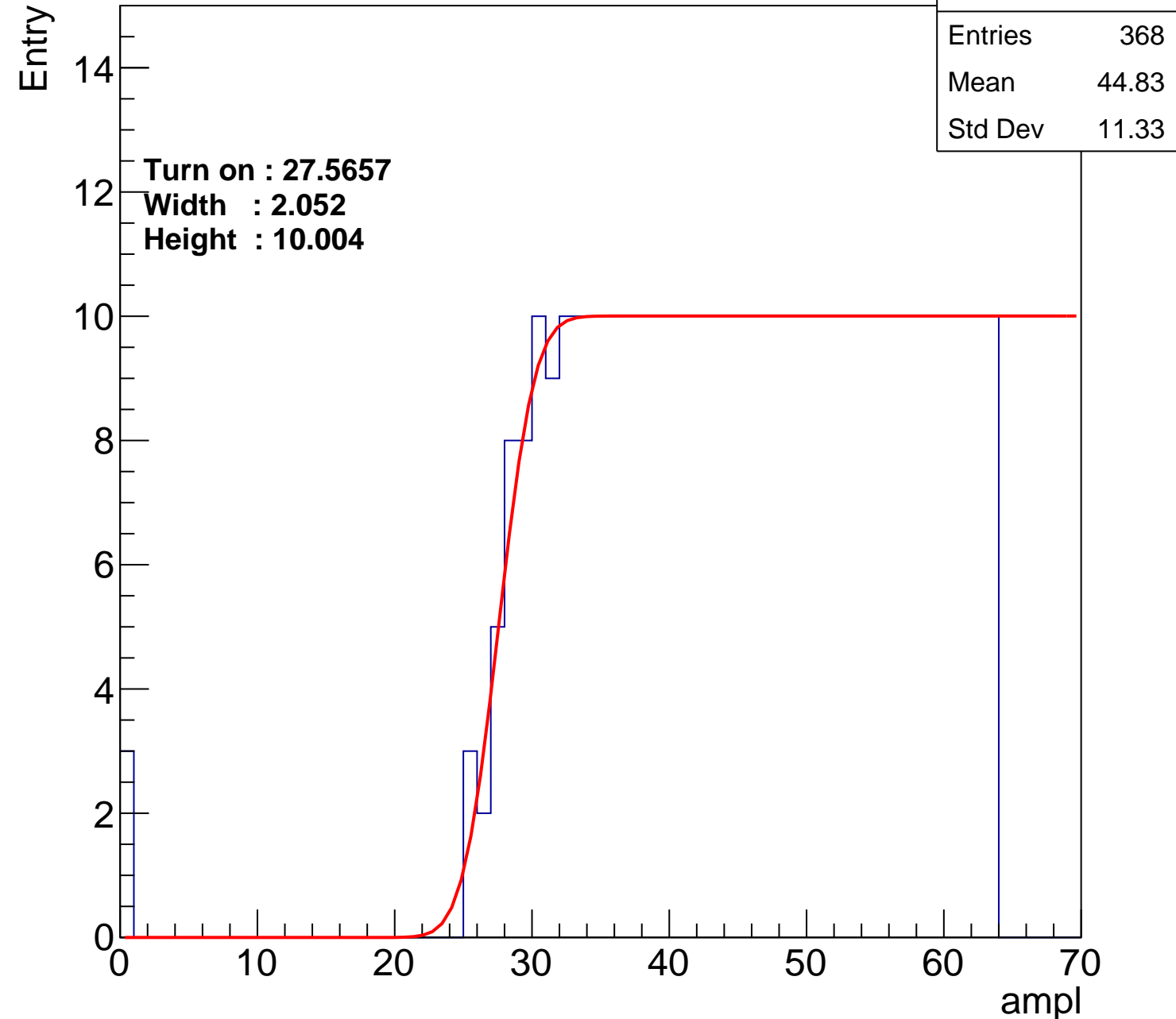
Width : 2.052

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch20

calib_packv5_042523_0143.root, FC#12, port B1

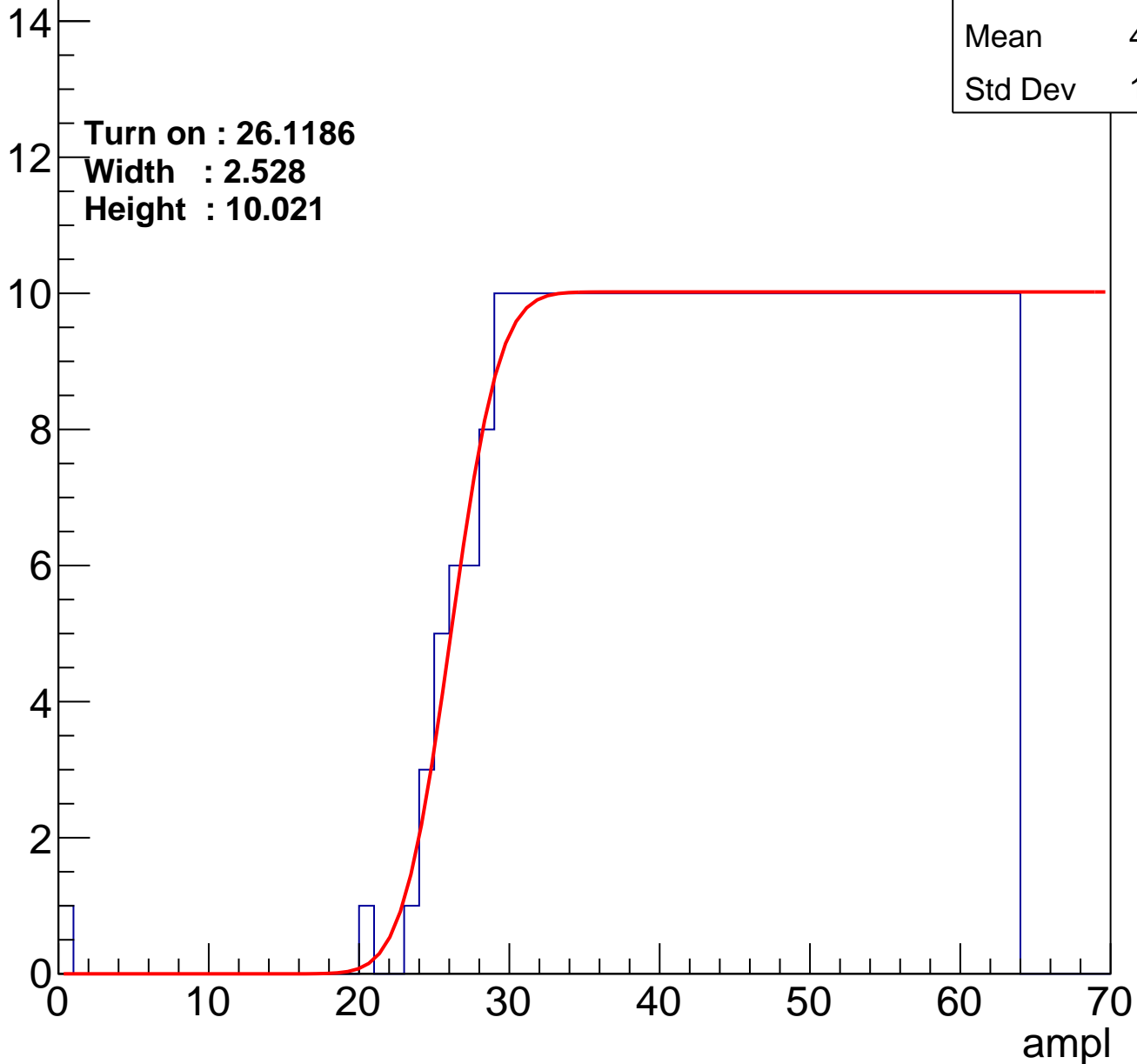
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.31 |
| Std Dev | 11.31 |

Turn on : 26.1186

Width : 2.528

Height : 10.021

Entry



B0L102S, U19-ch21

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.53 |
| Std Dev | 11.35 |

Turn on : 26.8459

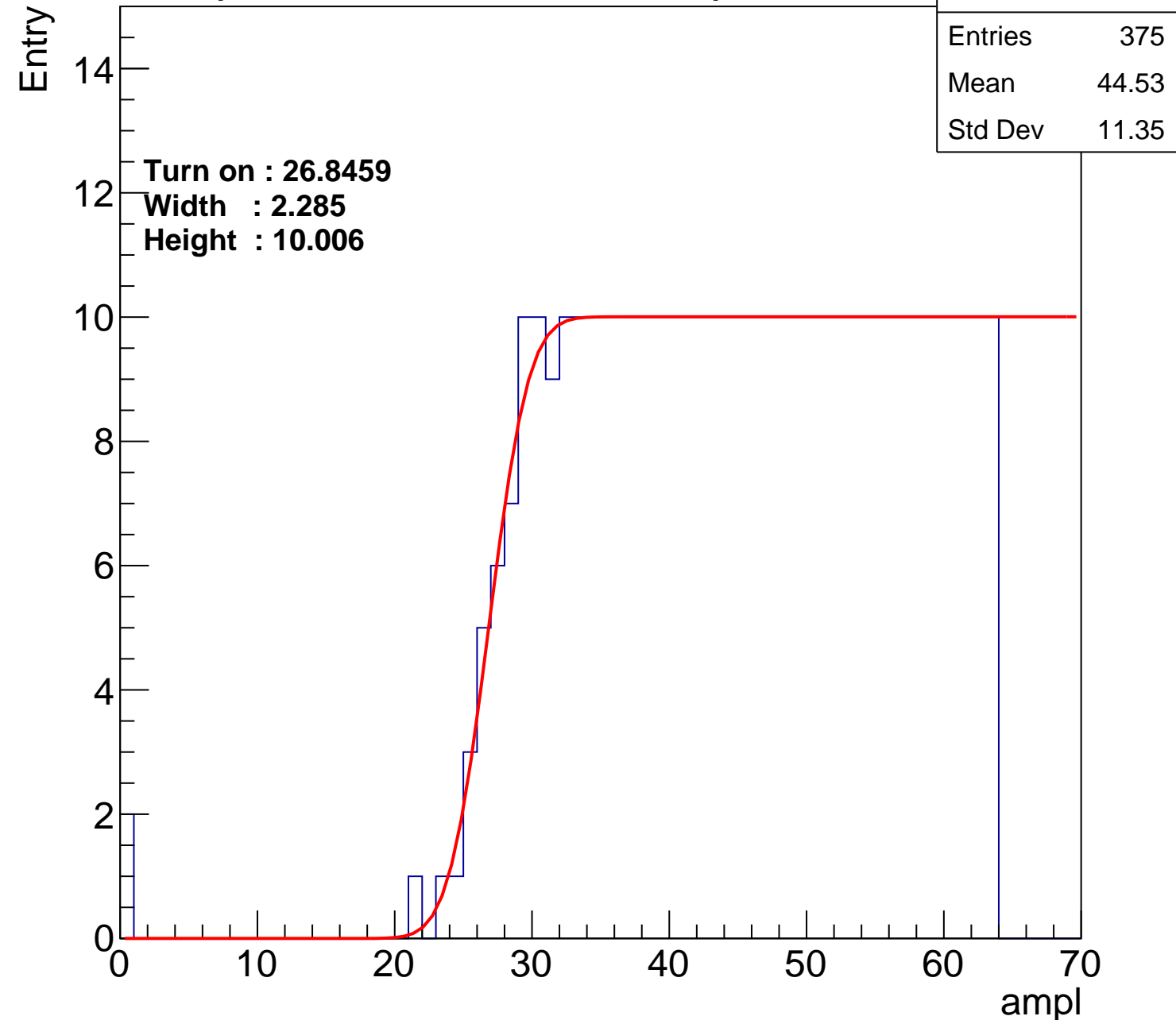
Width : 2.285

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch22

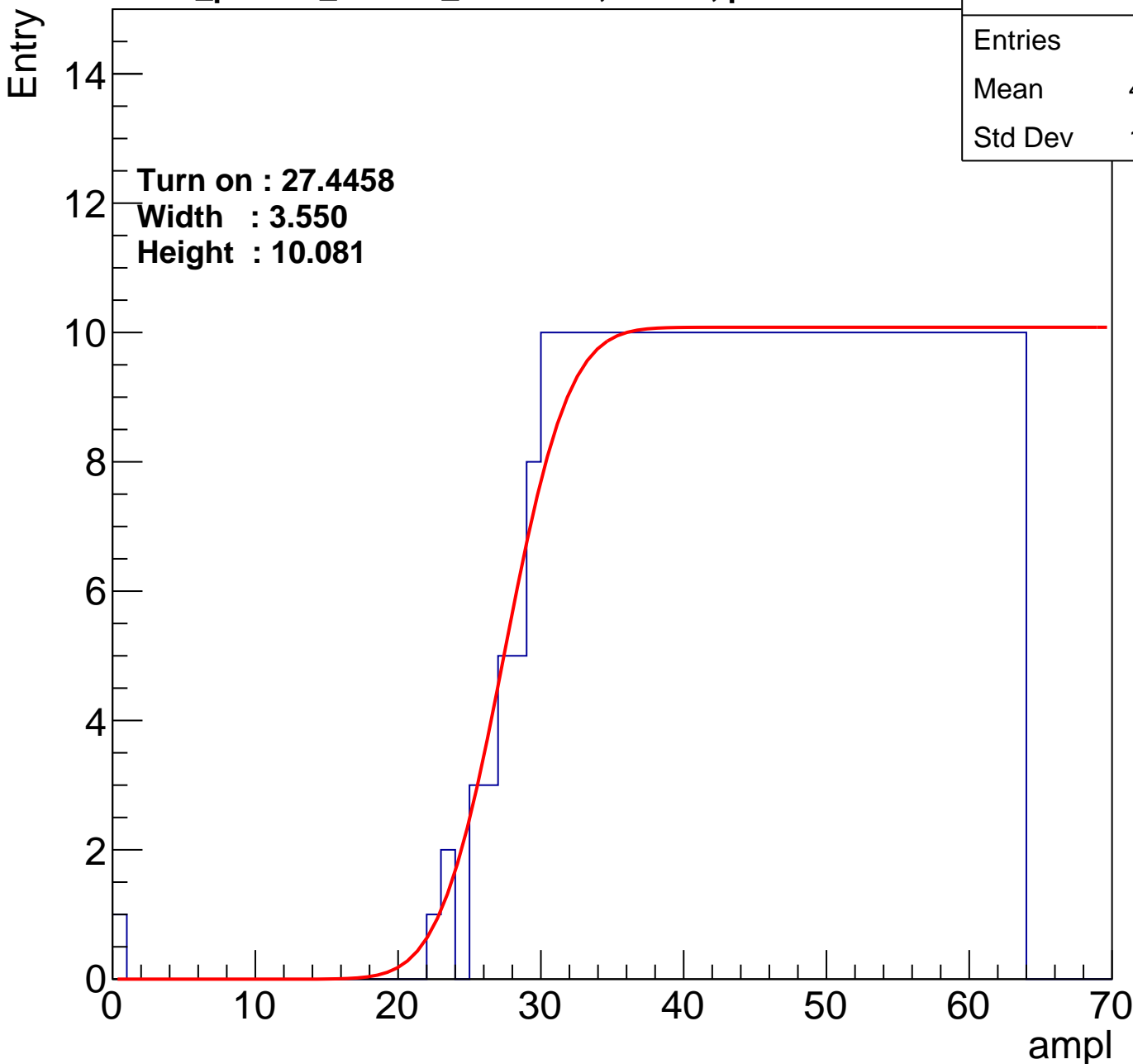
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.94 |
| Std Dev | 10.99 |

Turn on : 27.4458

Width : 3.550

Height : 10.081



B0L102S, U19-ch23

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.14 |
| Std Dev | 11.59 |

Turn on : 26.1050

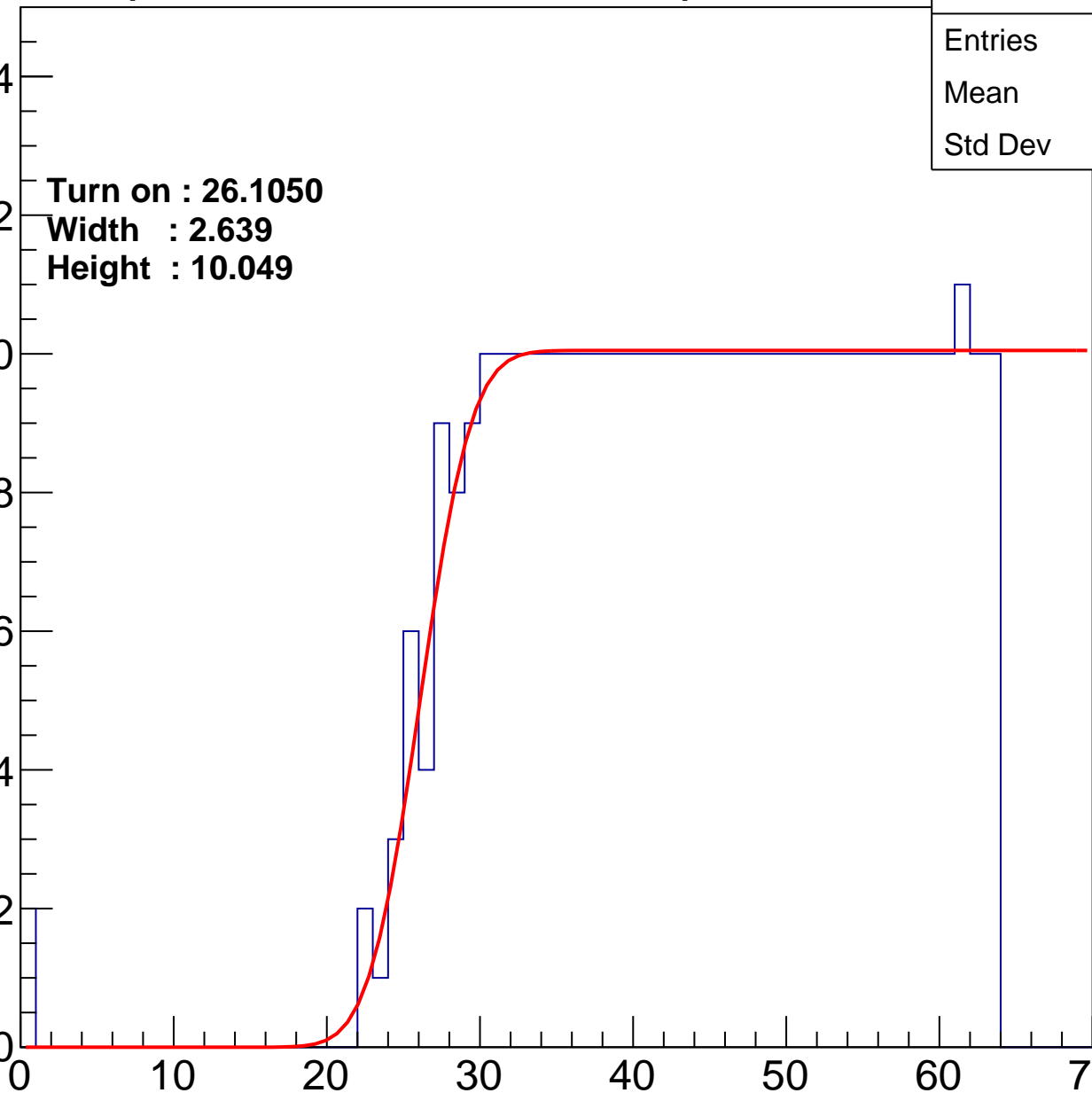
Width : 2.639

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch24

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.25 |
| Std Dev | 11.67 |

Turn on : 26.3962

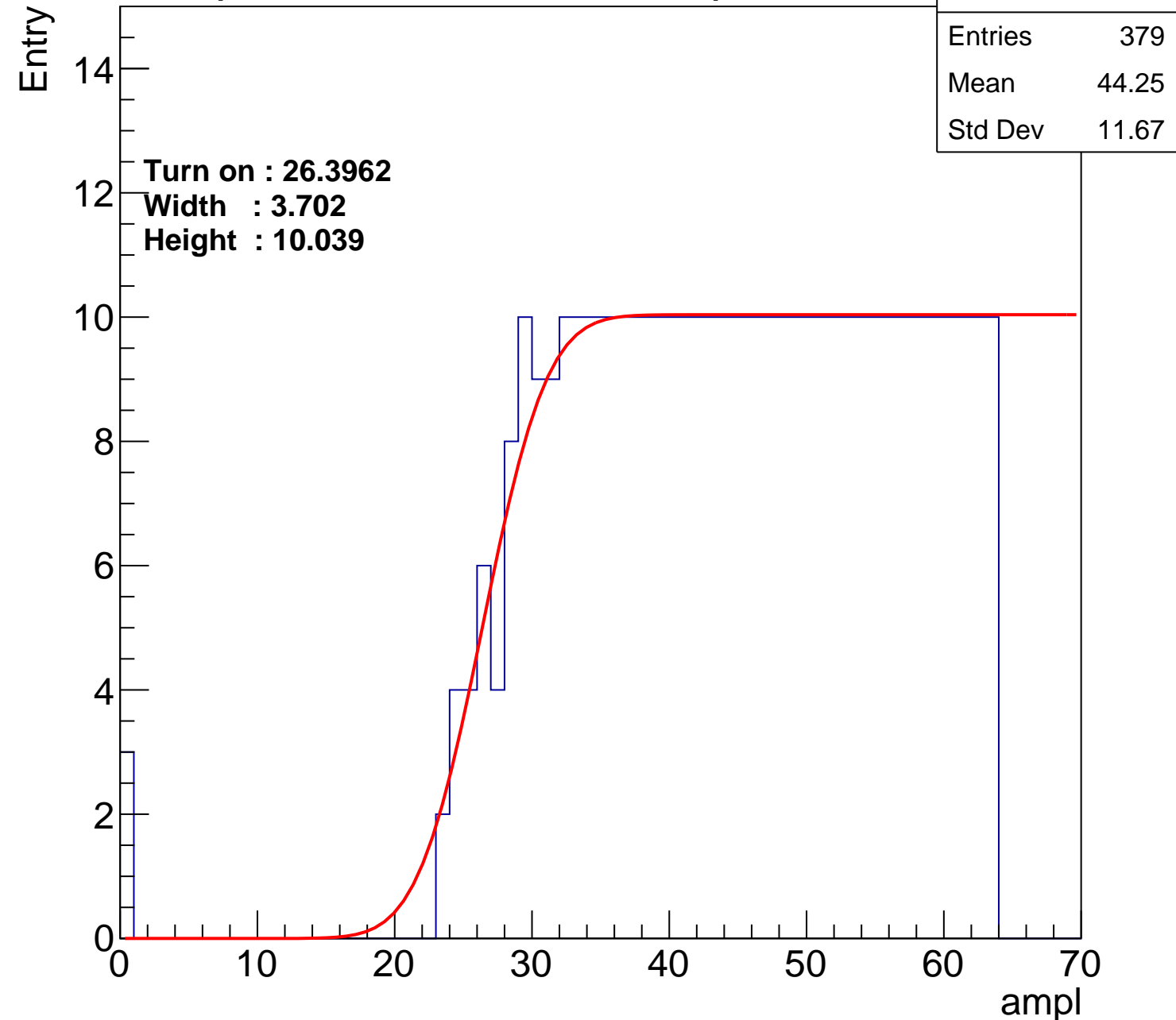
Width : 3.702

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch25

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.19 |
| Std Dev | 10.98 |

Turn on : 28.0264

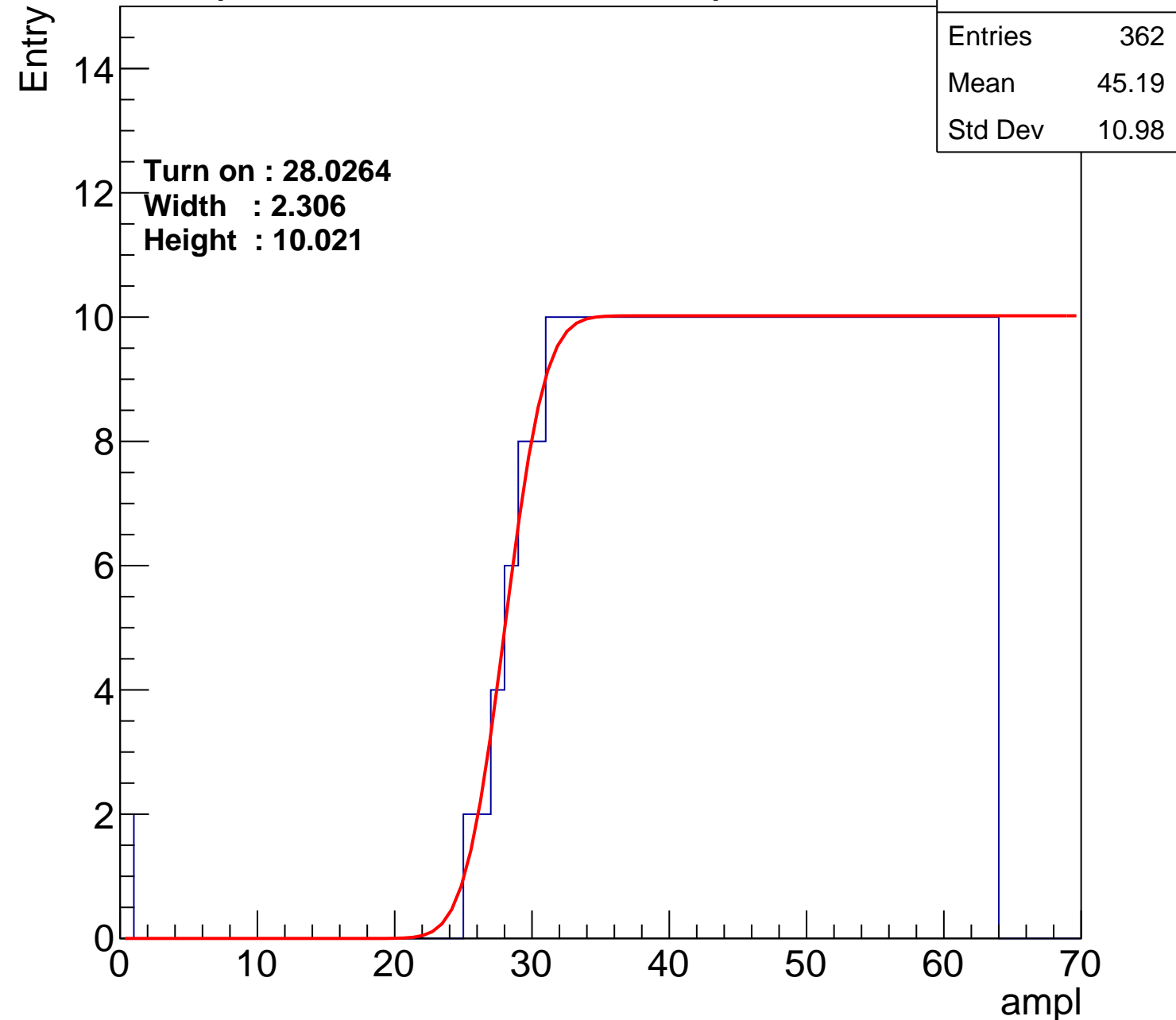
Width : 2.306

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch26

calib_packv5_042523_0143.root, FC#12, port B1

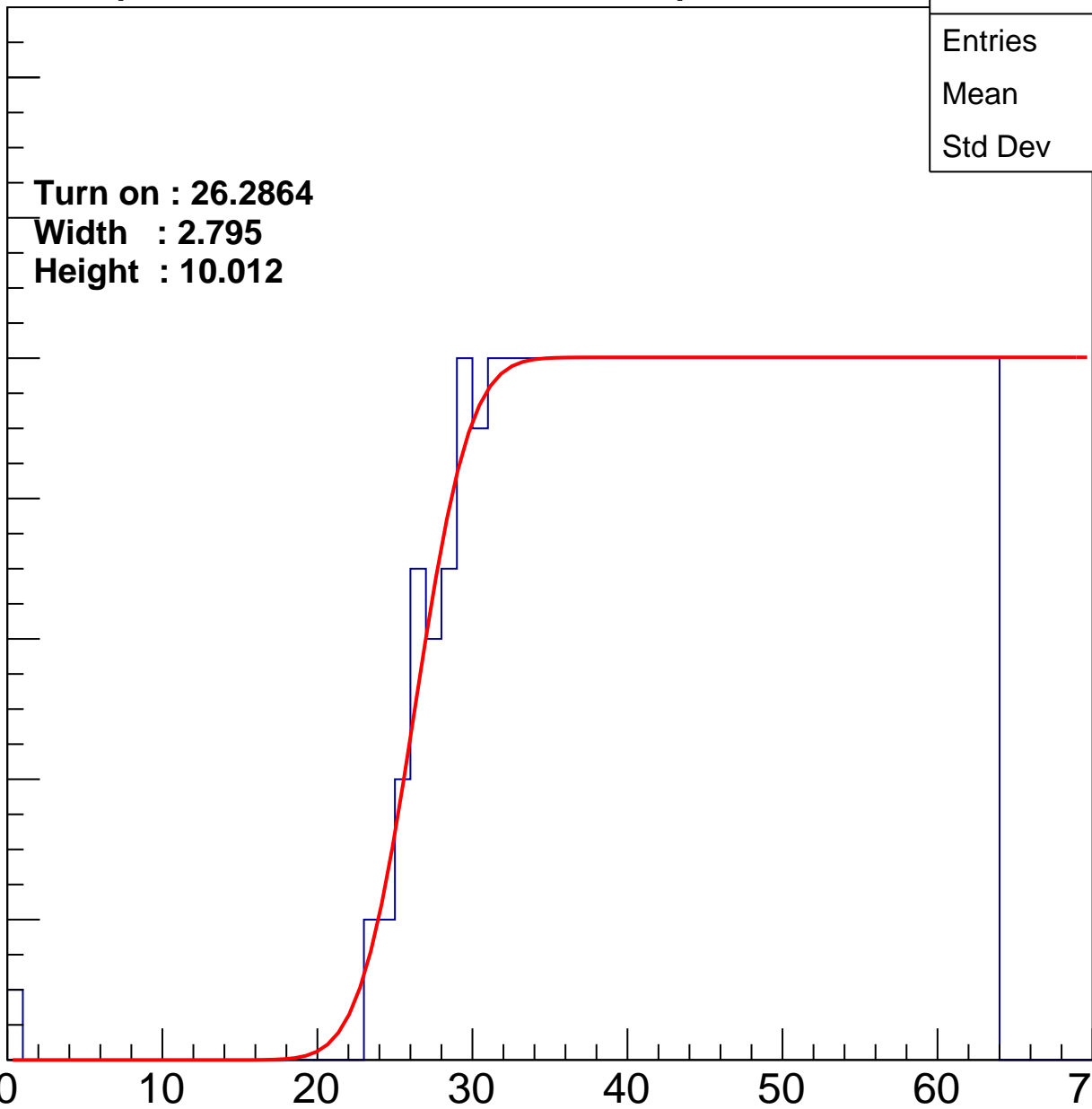
Entry

14
12
10
8
6
4
2
0

Turn on : 26.2864
Width : 2.795
Height : 10.012

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.46 |
| Std Dev | 11.23 |

ampl



B0L102S, U19-ch27

calib_packv5_042523_0143.root, FC#12, port B1

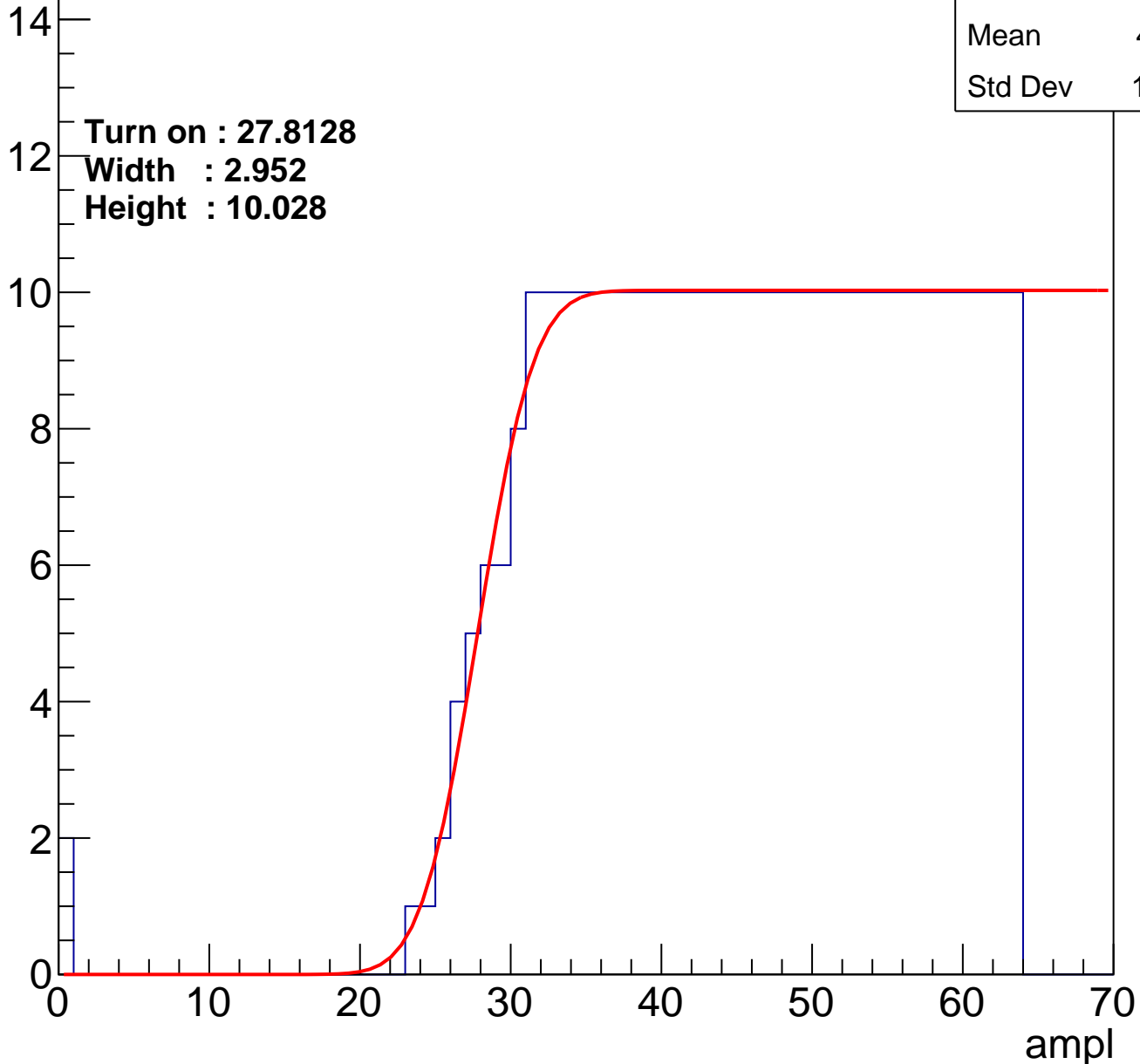
| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.01 |
| Std Dev | 11.12 |

Turn on : 27.8128

Width : 2.952

Height : 10.028

Entry



B0L102S, U19-ch28

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.26 |
| Std Dev | 11.77 |

Turn on : 26.5544

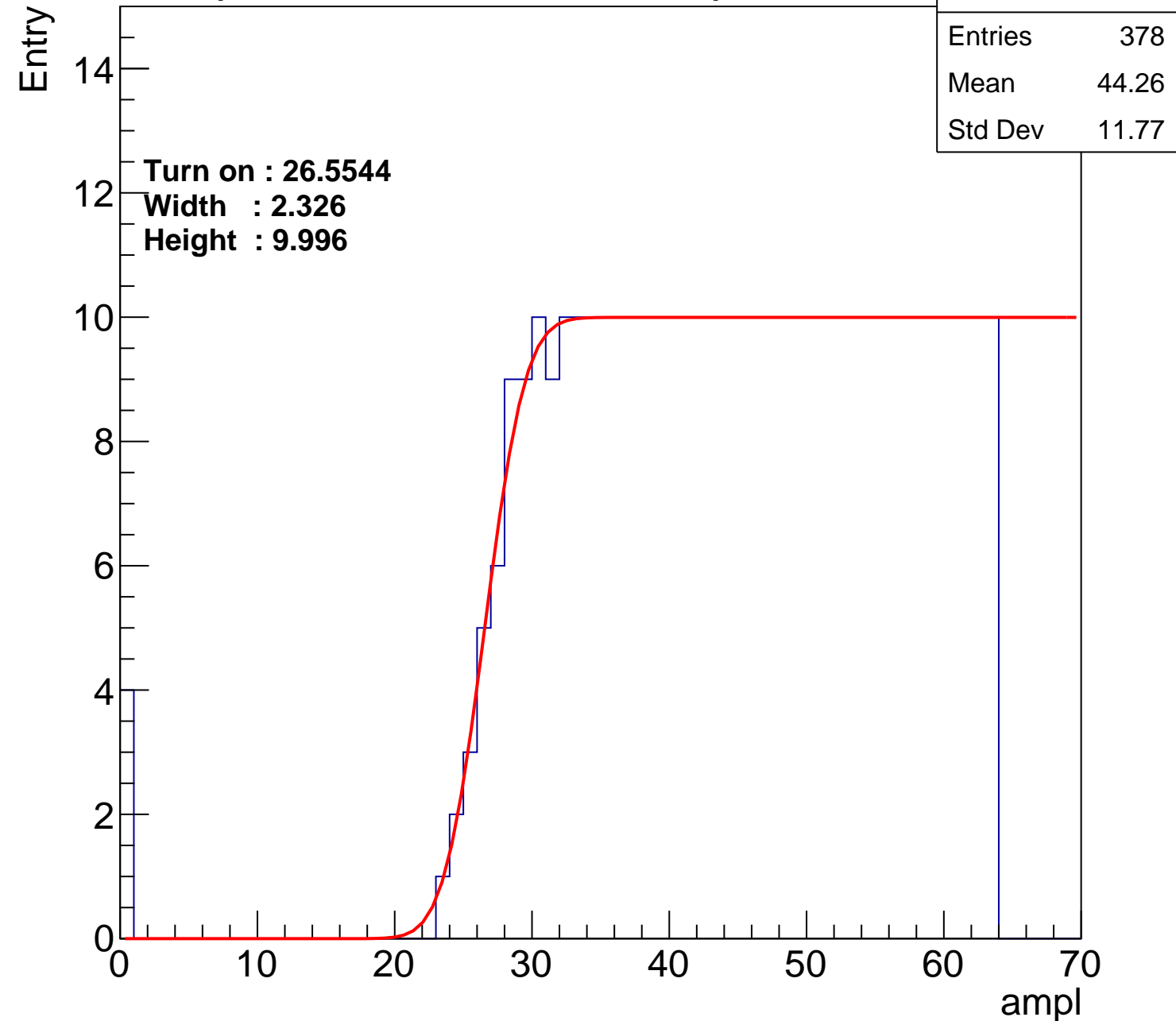
Width : 2.326

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch29

calib_packv5_042523_0143.root, FC#12, port B1

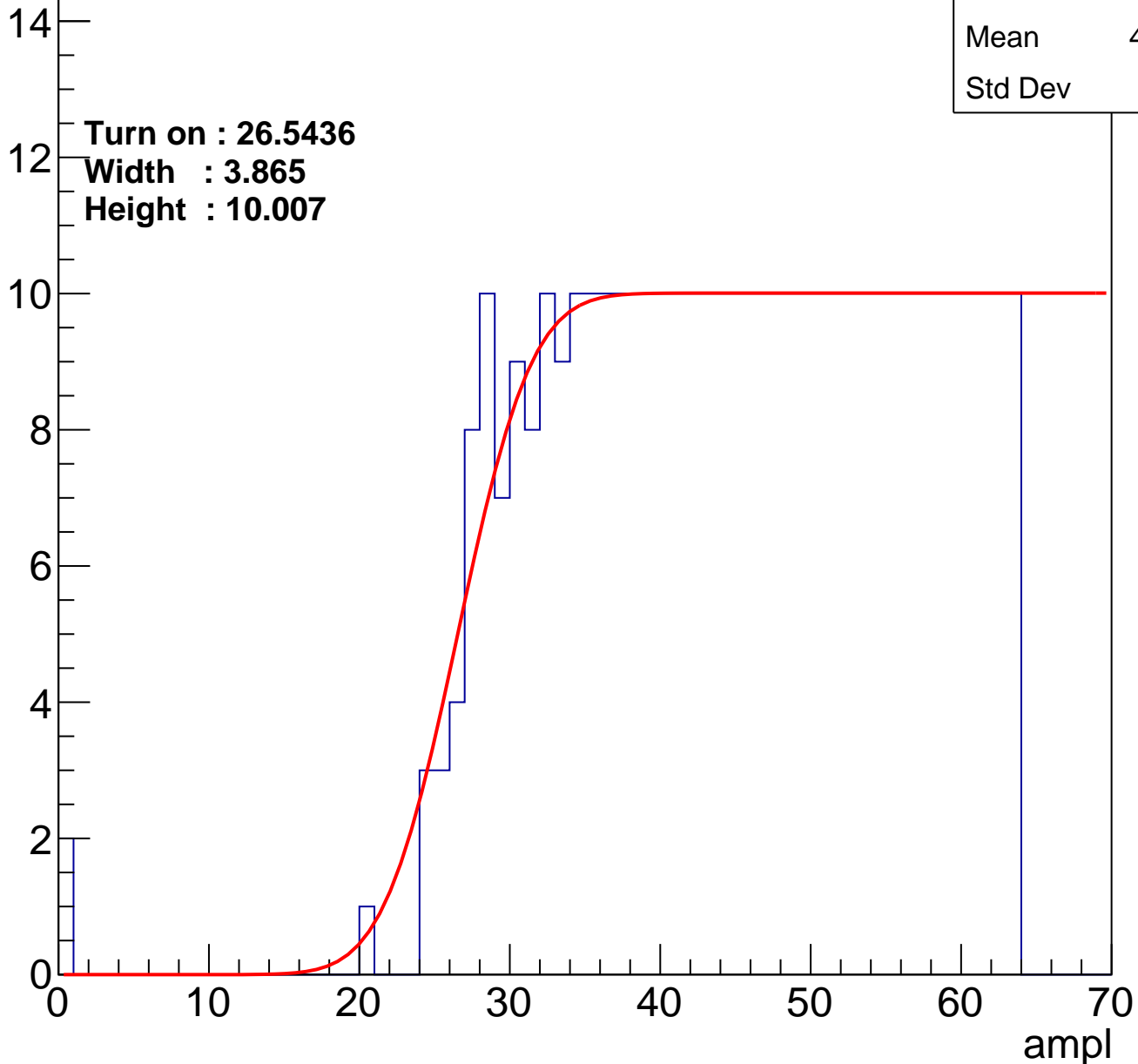
| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.53 |
| Std Dev | 11.4 |

Turn on : 26.5436

Width : 3.865

Height : 10.007

Entry



B0L102S, U19-ch30

calib_packv5_042523_0143.root, FC#12, port B1

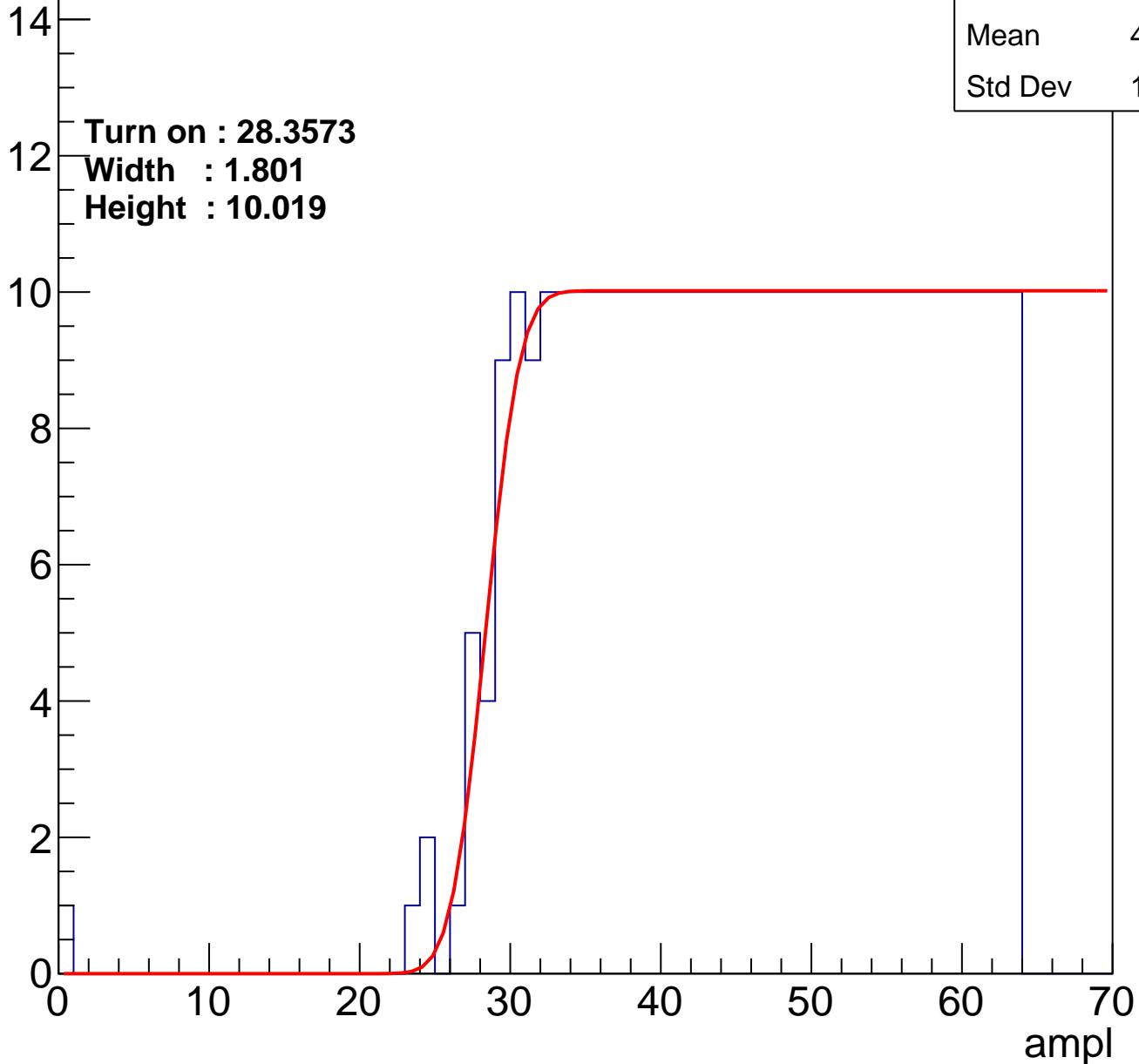
| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.26 |
| Std Dev | 10.78 |

Turn on : 28.3573

Width : 1.801

Height : 10.019

Entry



B0L102S, U19-ch31

calib_packv5_042523_0143.root, FC#12, port B1

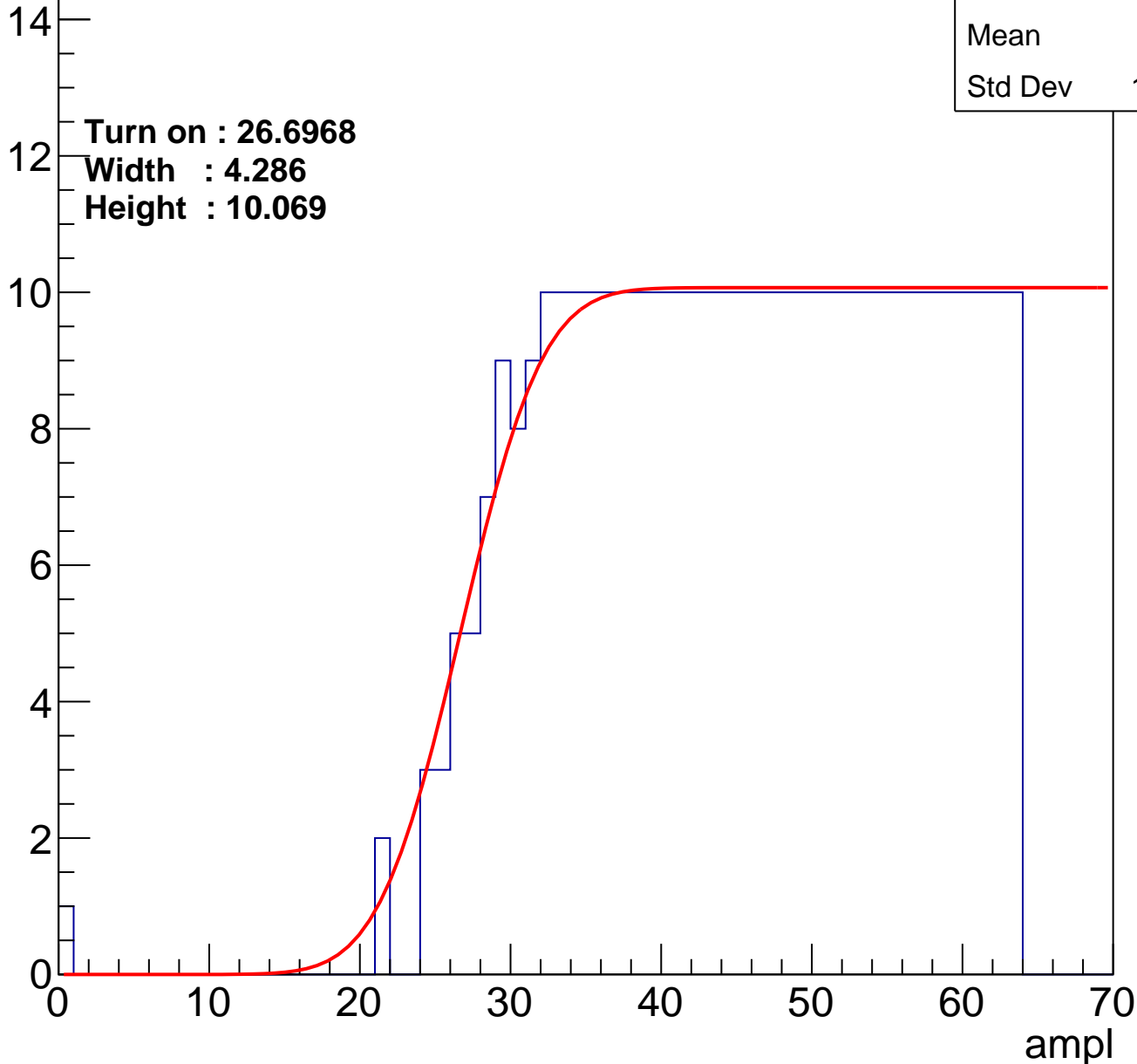
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.7 |
| Std Dev | 11.15 |

Turn on : 26.6968

Width : 4.286

Height : 10.069

Entry



B0L102S, U19-ch32

calib_packv5_042523_0143.root, FC#12, port B1

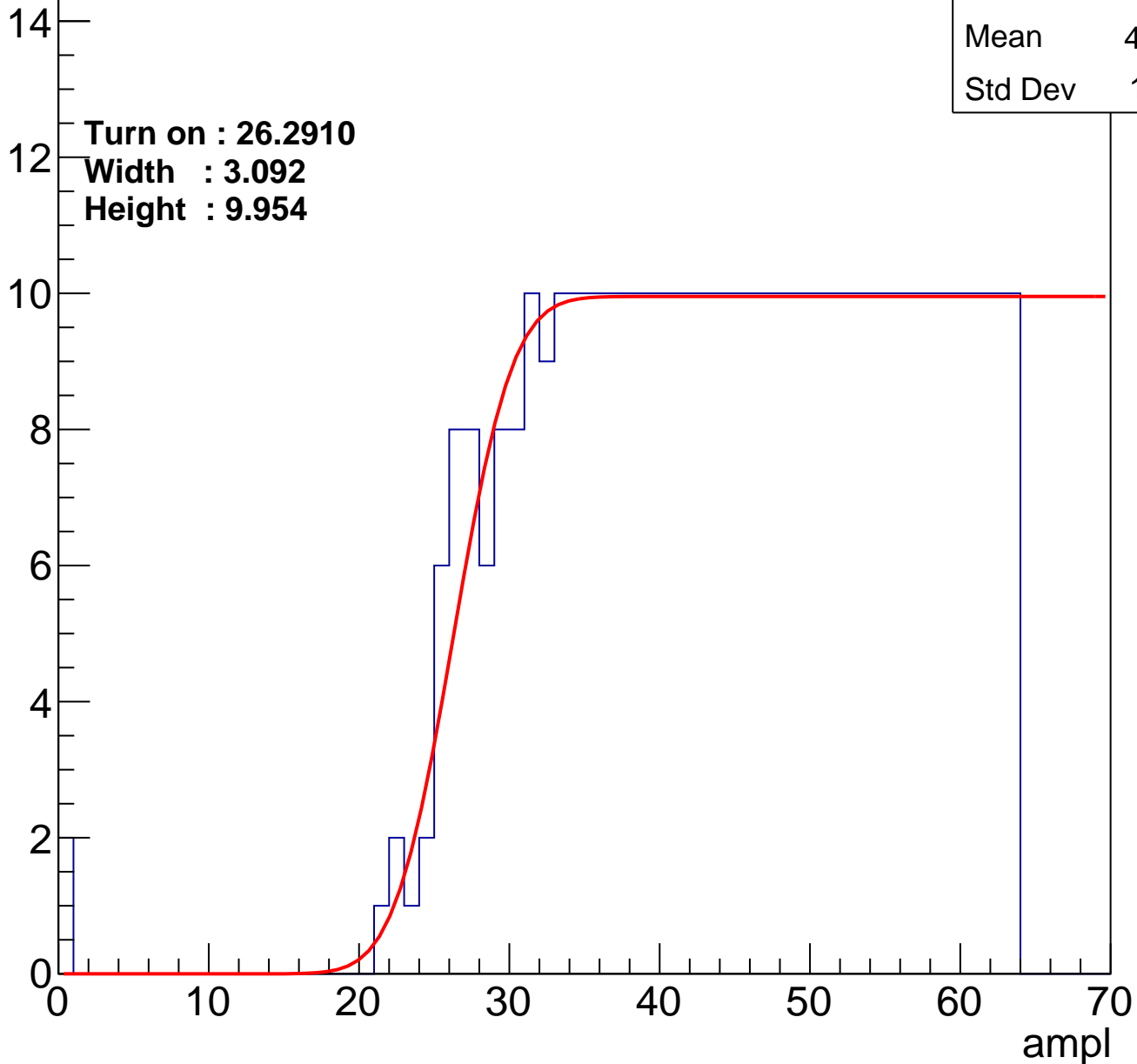
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.17 |
| Std Dev | 11.61 |

Turn on : 26.2910

Width : 3.092

Height : 9.954

Entry



B0L102S, U19-ch33

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.74 |
| Std Dev | 11.09 |

Turn on : 26.2247

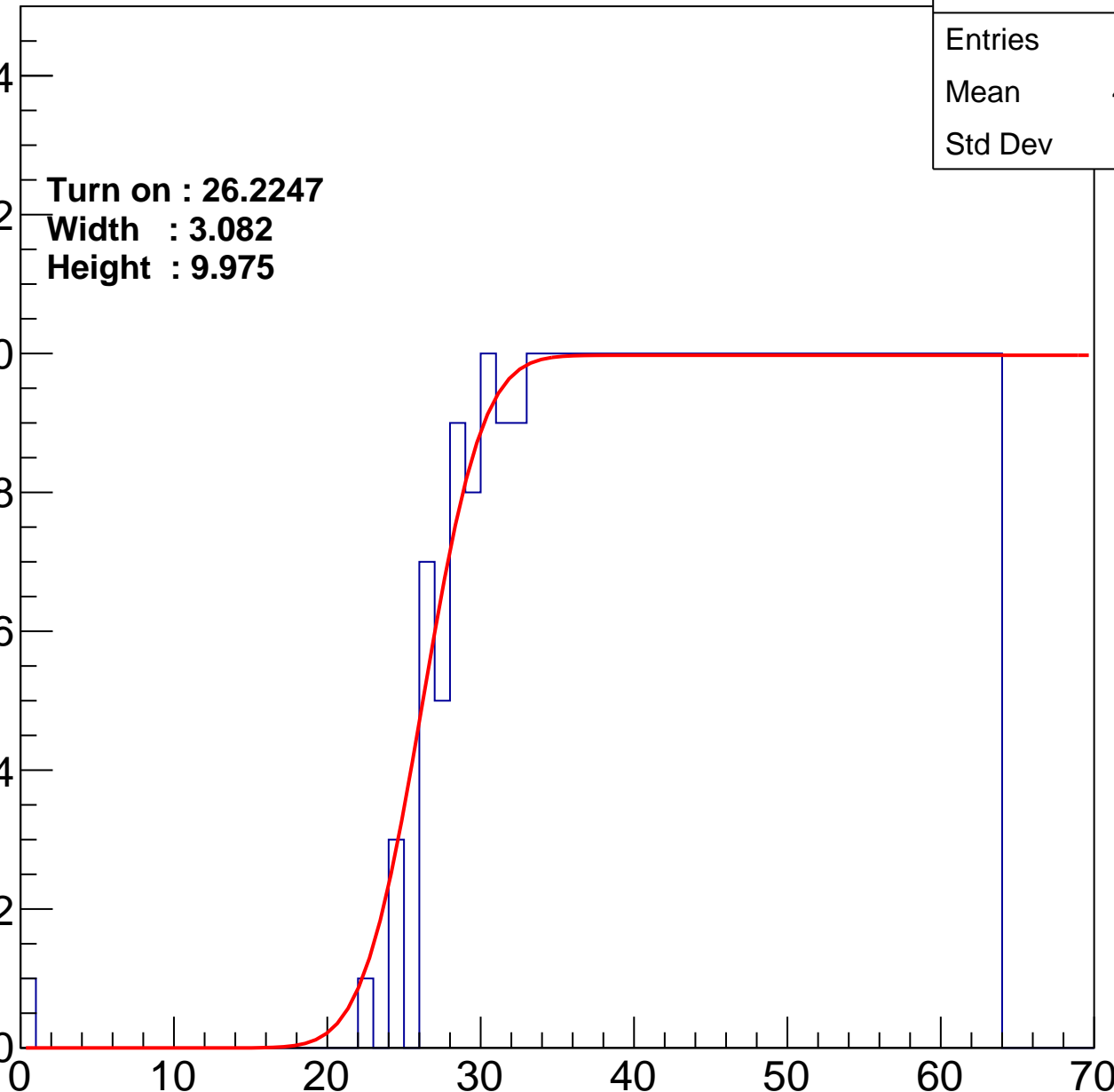
Width : 3.082

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch34

calib_packv5_042523_0143.root, FC#12, port B1

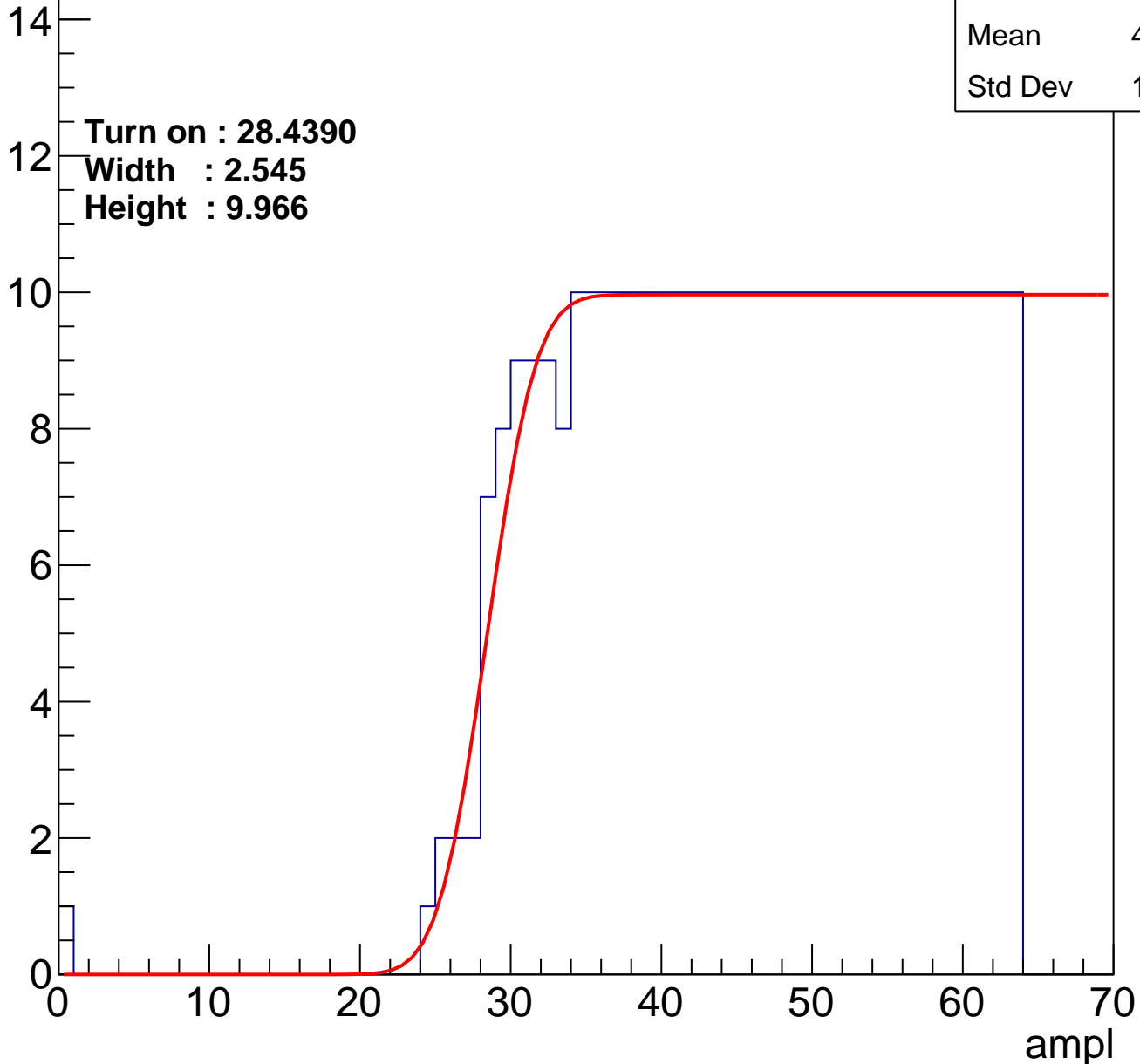
| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.42 |
| Std Dev | 10.74 |

Turn on : 28.4390

Width : 2.545

Height : 9.966

Entry



B0L102S, U19-ch35

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.19 |
| Std Dev | 11.65 |

Turn on : 25.8708

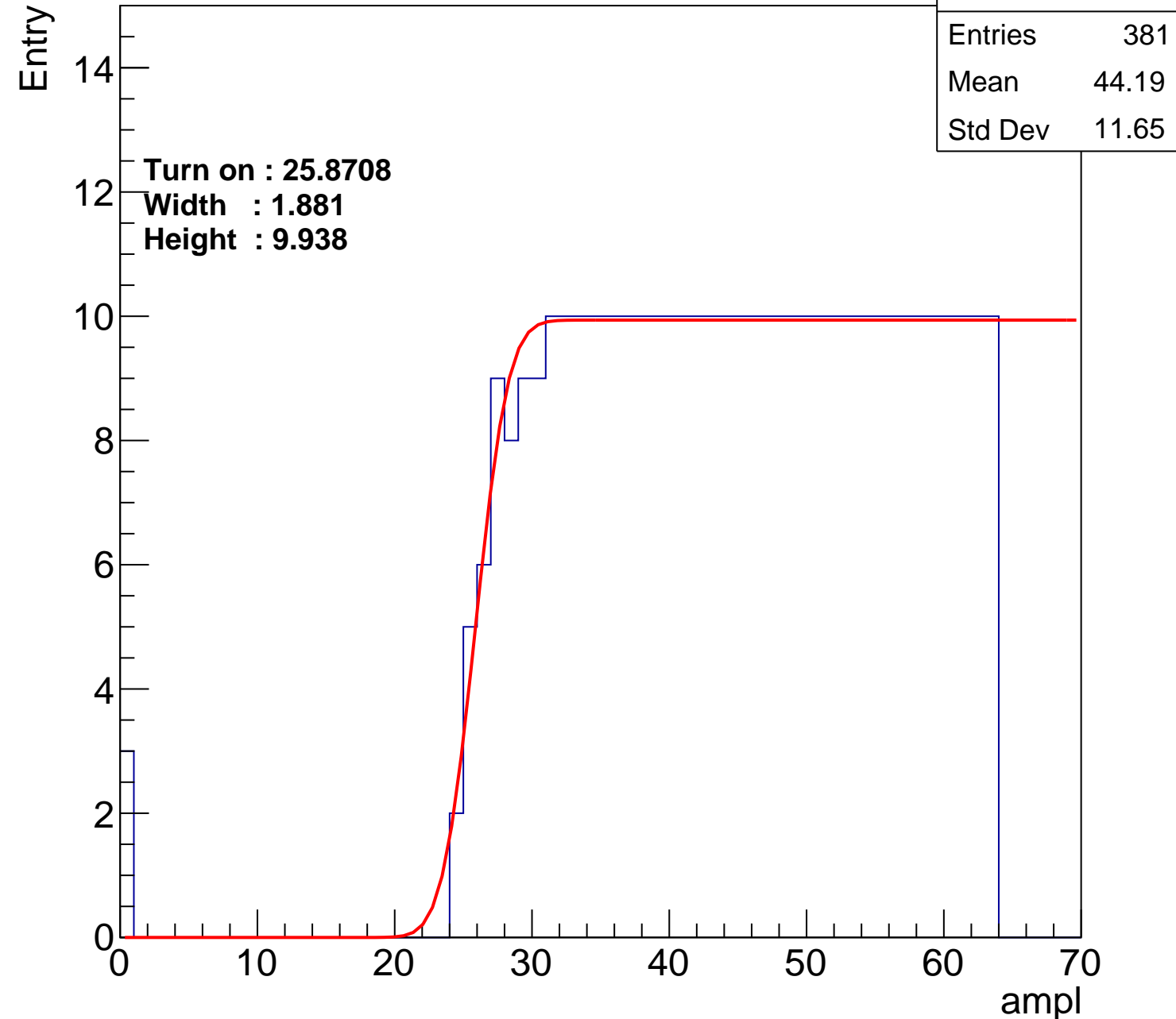
Width : 1.881

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch36

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.79 |
| Std Dev | 11.19 |

Turn on : 27.1531

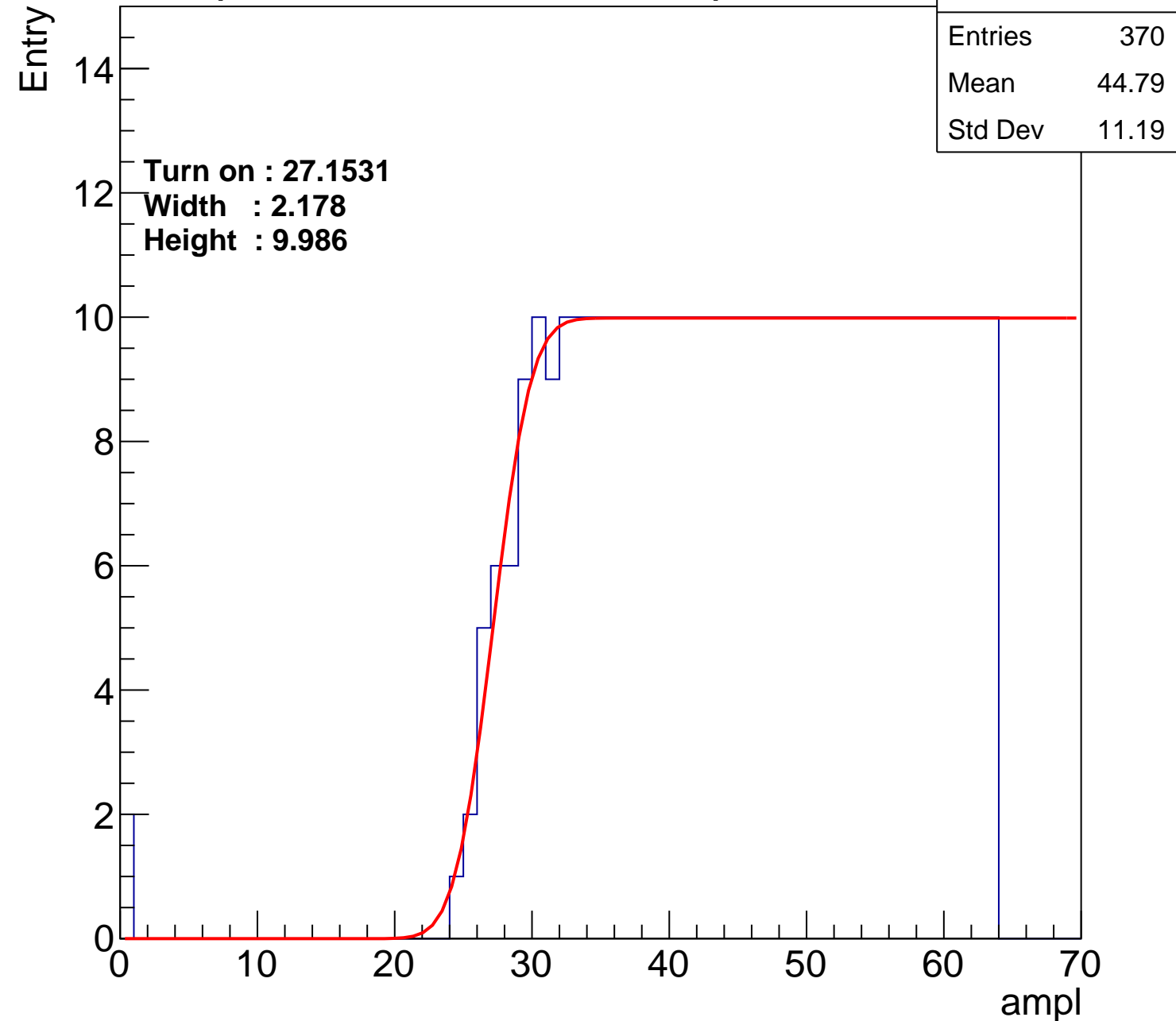
Width : 2.178

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch37

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.98 |
| Std Dev | 11.52 |

Turn on : 25.4891

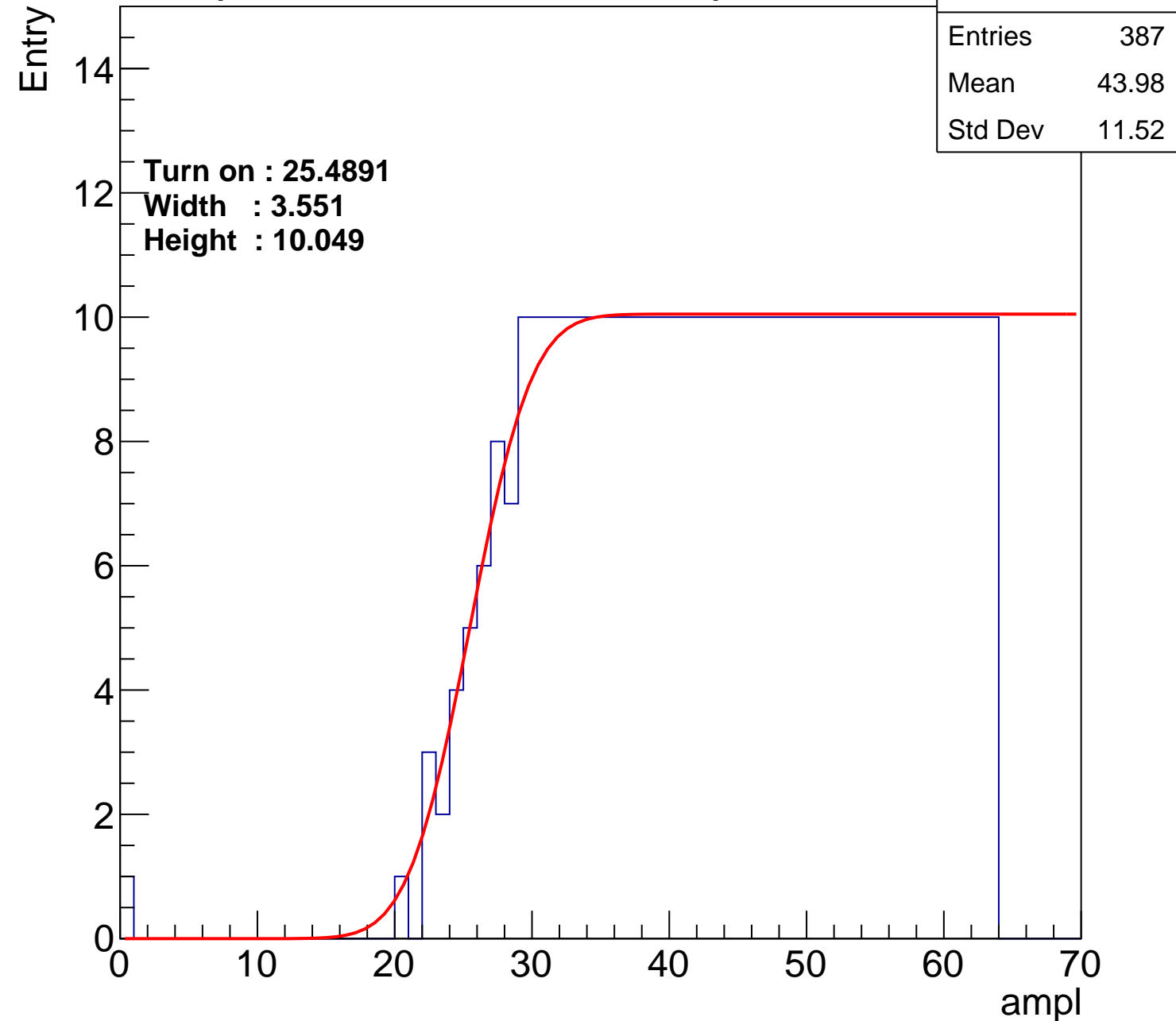
Width : 3.551

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch38

calib_packv5_042523_0143.root, FC#12, port B1

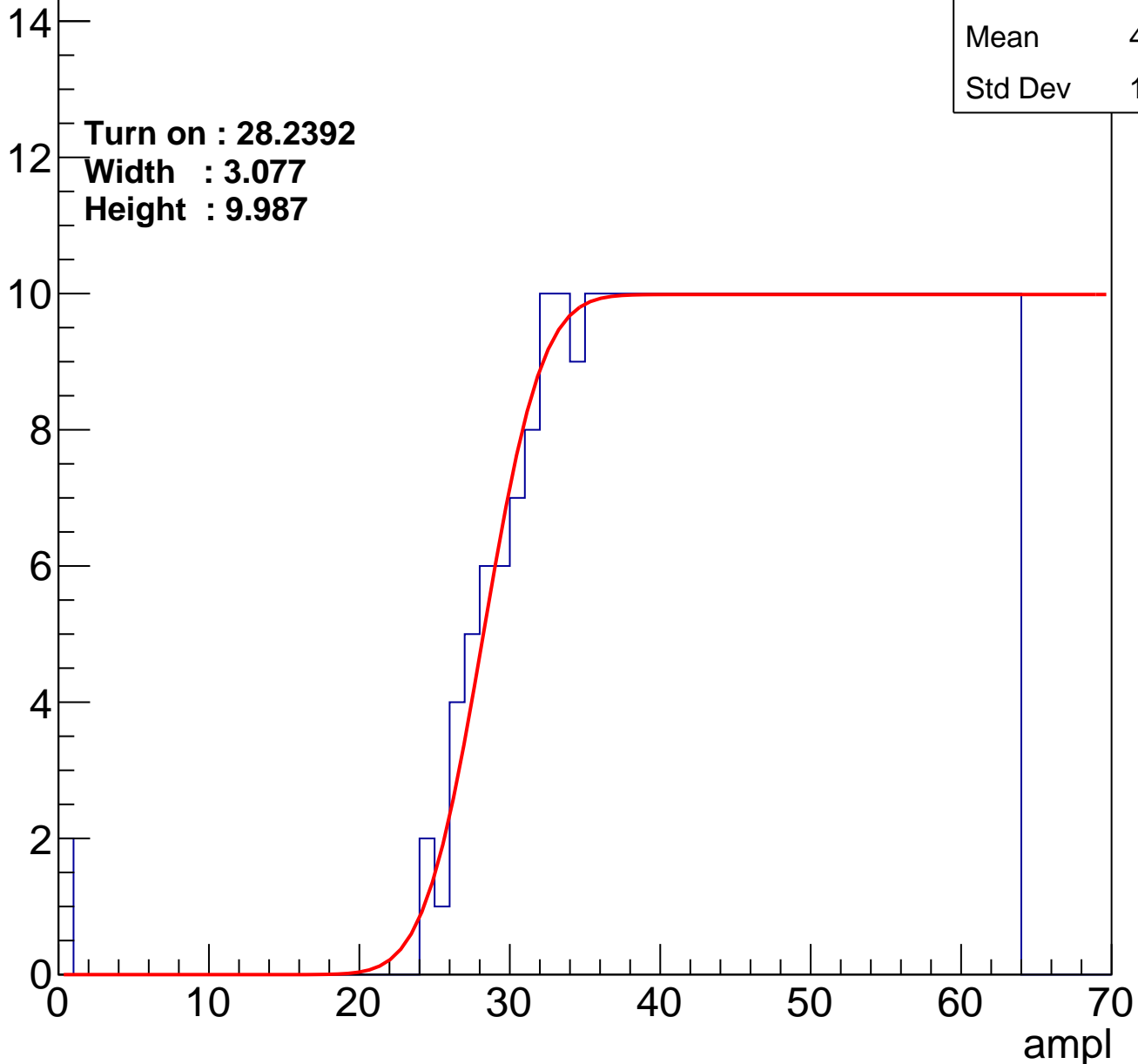
| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.22 |
| Std Dev | 11.05 |

Turn on : 28.2392

Width : 3.077

Height : 9.987

Entry



B0L102S, U19-ch39

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.59 |
| Std Dev | 11.62 |

Turn on : 27.1606

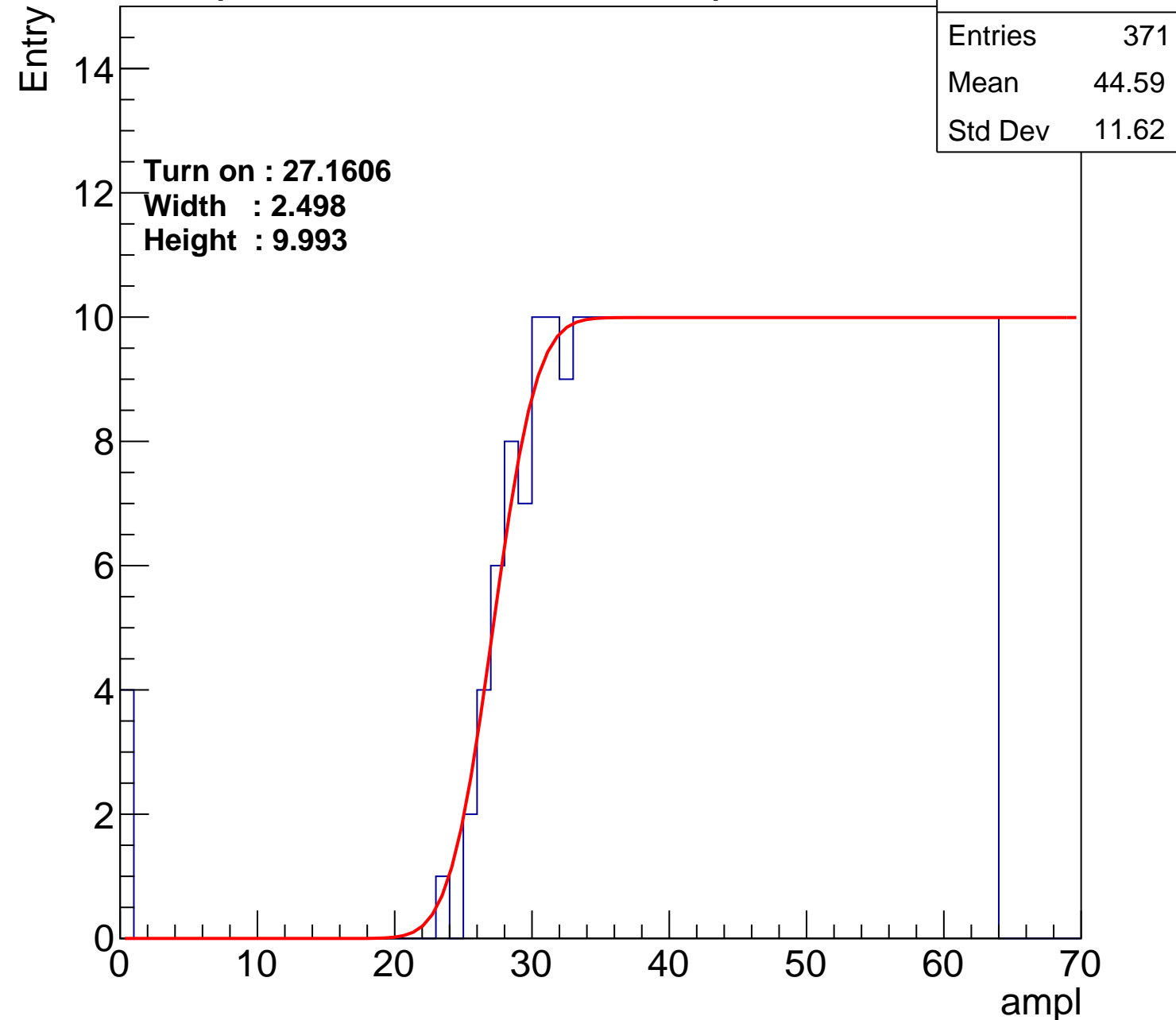
Width : 2.498

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch40

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.13 |
| Std Dev | 11.45 |

Turn on : 26.2773

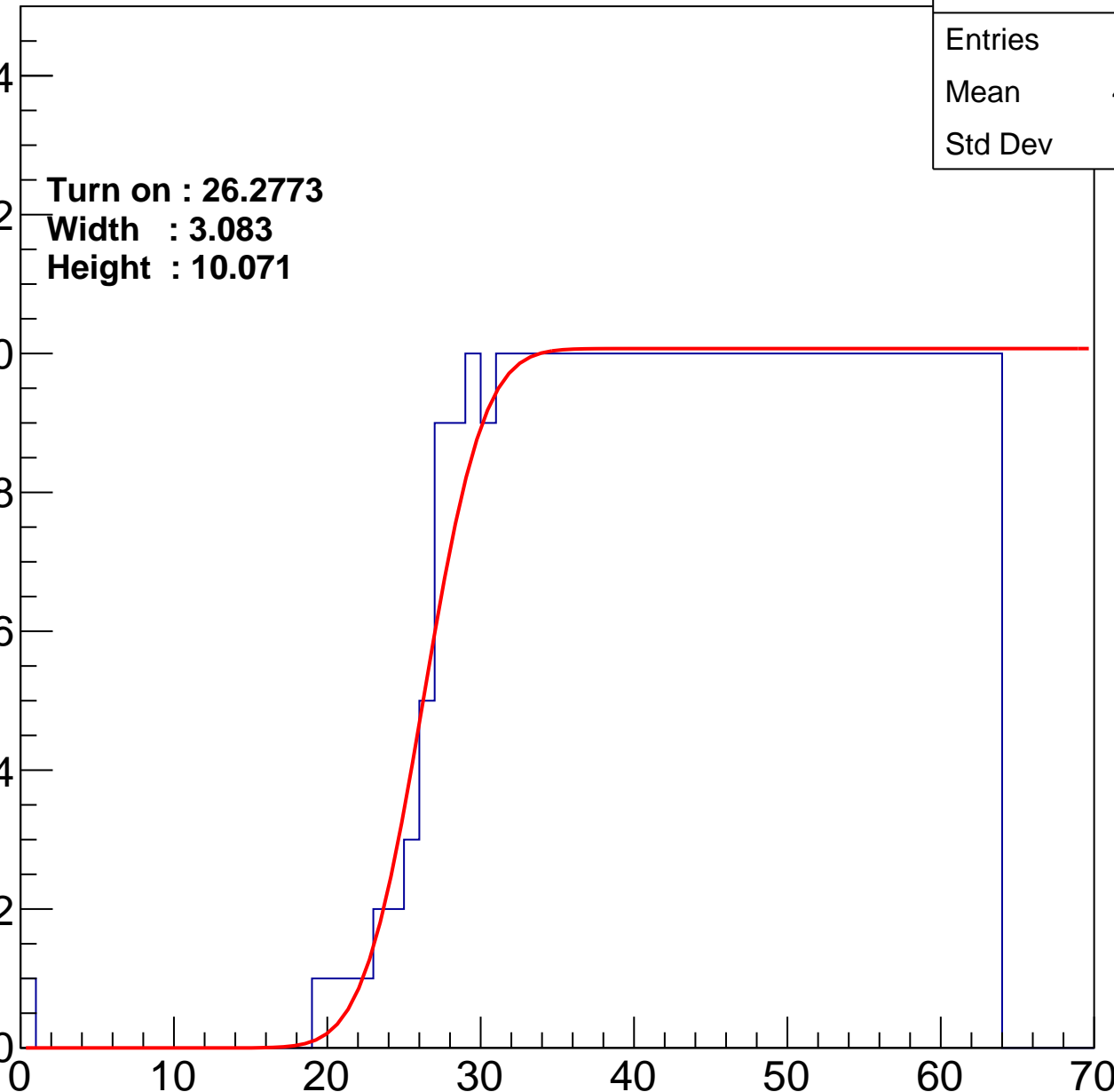
Width : 3.083

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch41

calib_packv5_042523_0143.root, FC#12, port B1

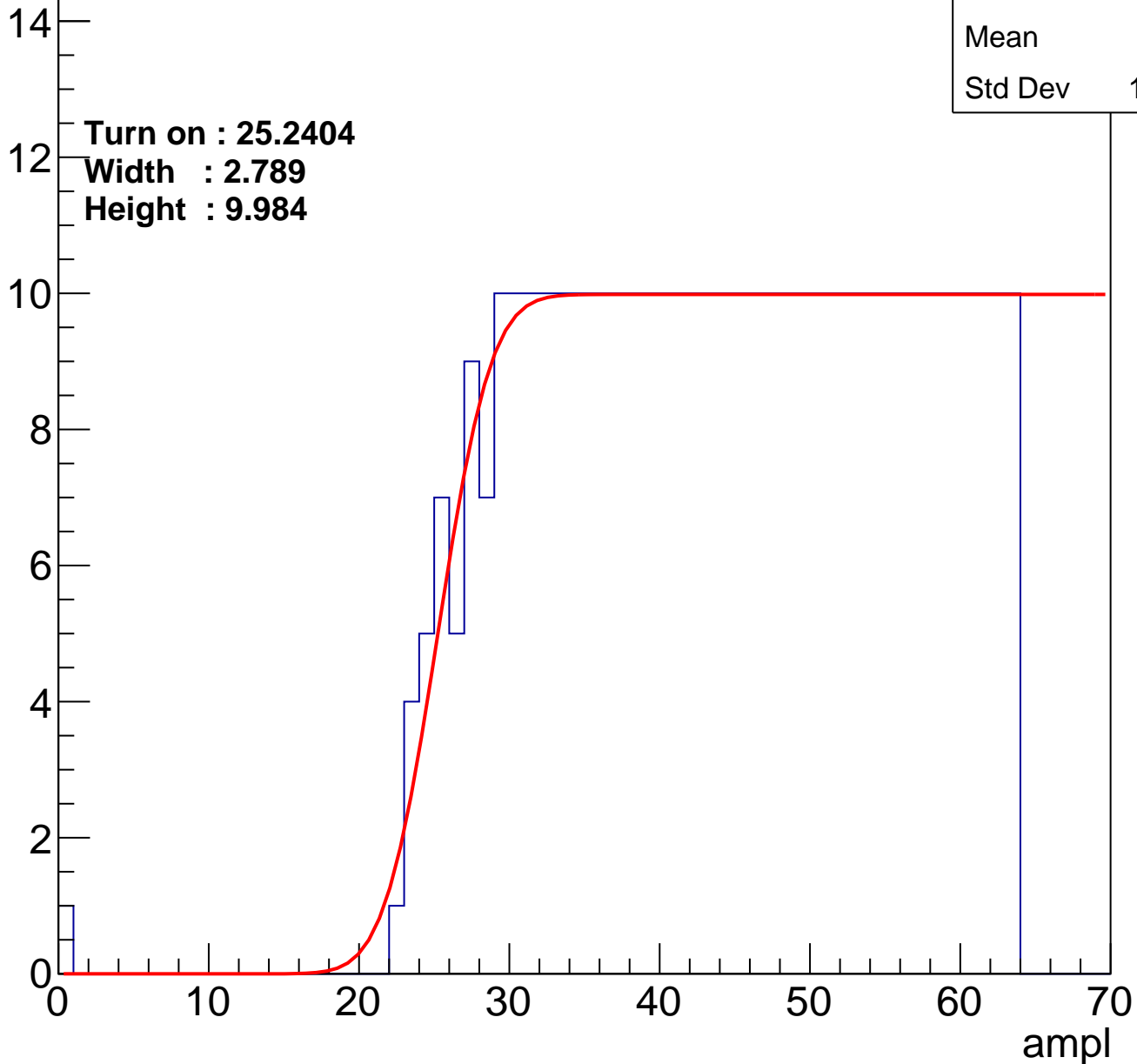
| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.9 |
| Std Dev | 11.54 |

Turn on : 25.2404

Width : 2.789

Height : 9.984

Entry



B0L102S, U19-ch42

calib_packv5_042523_0143.root, FC#12, port B1

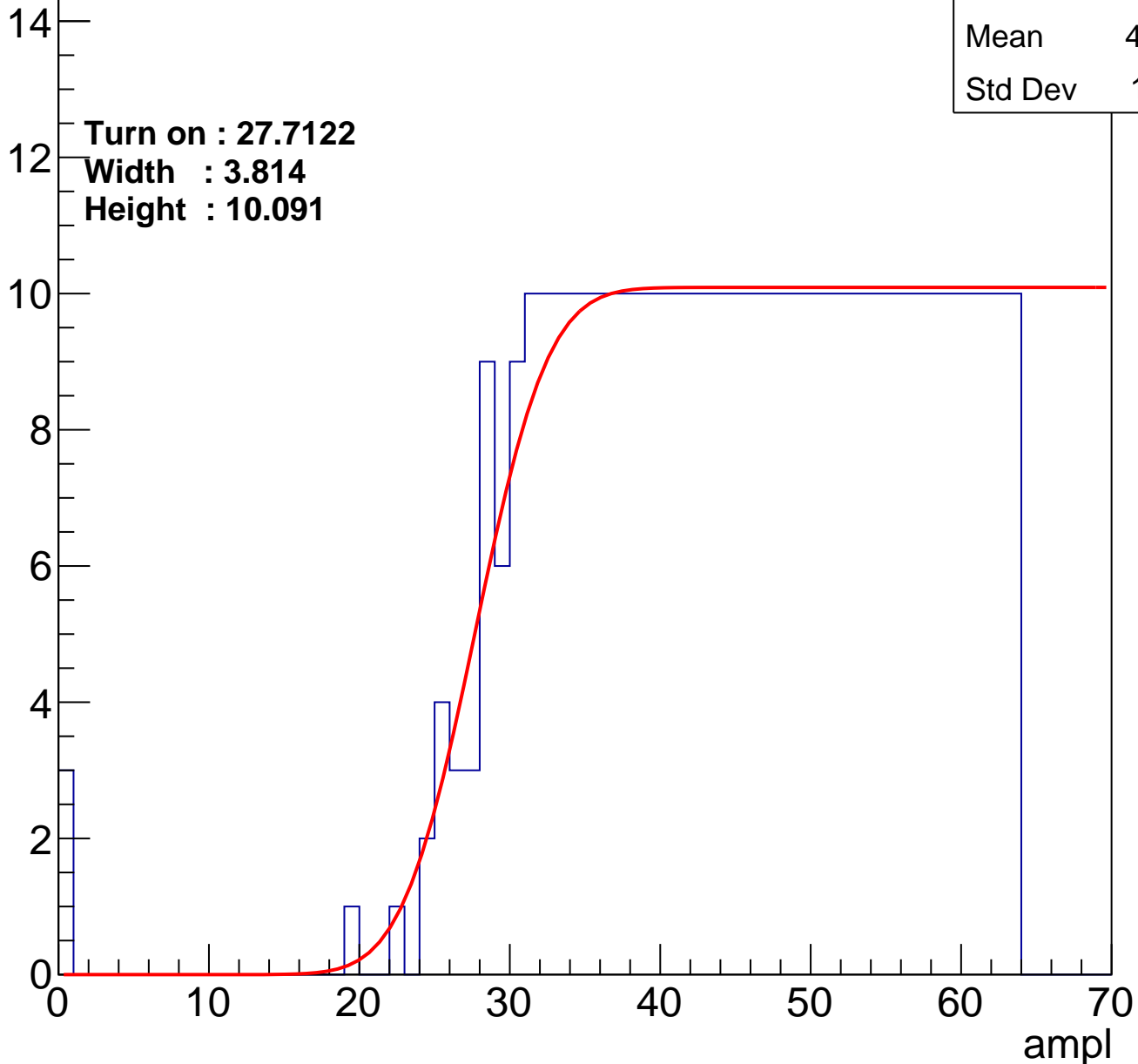
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.62 |
| Std Dev | 11.51 |

Turn on : 27.7122

Width : 3.814

Height : 10.091

Entry



B0L102S, U19-ch43

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.69 |
| Std Dev | 11.32 |

Turn on : 27.3247

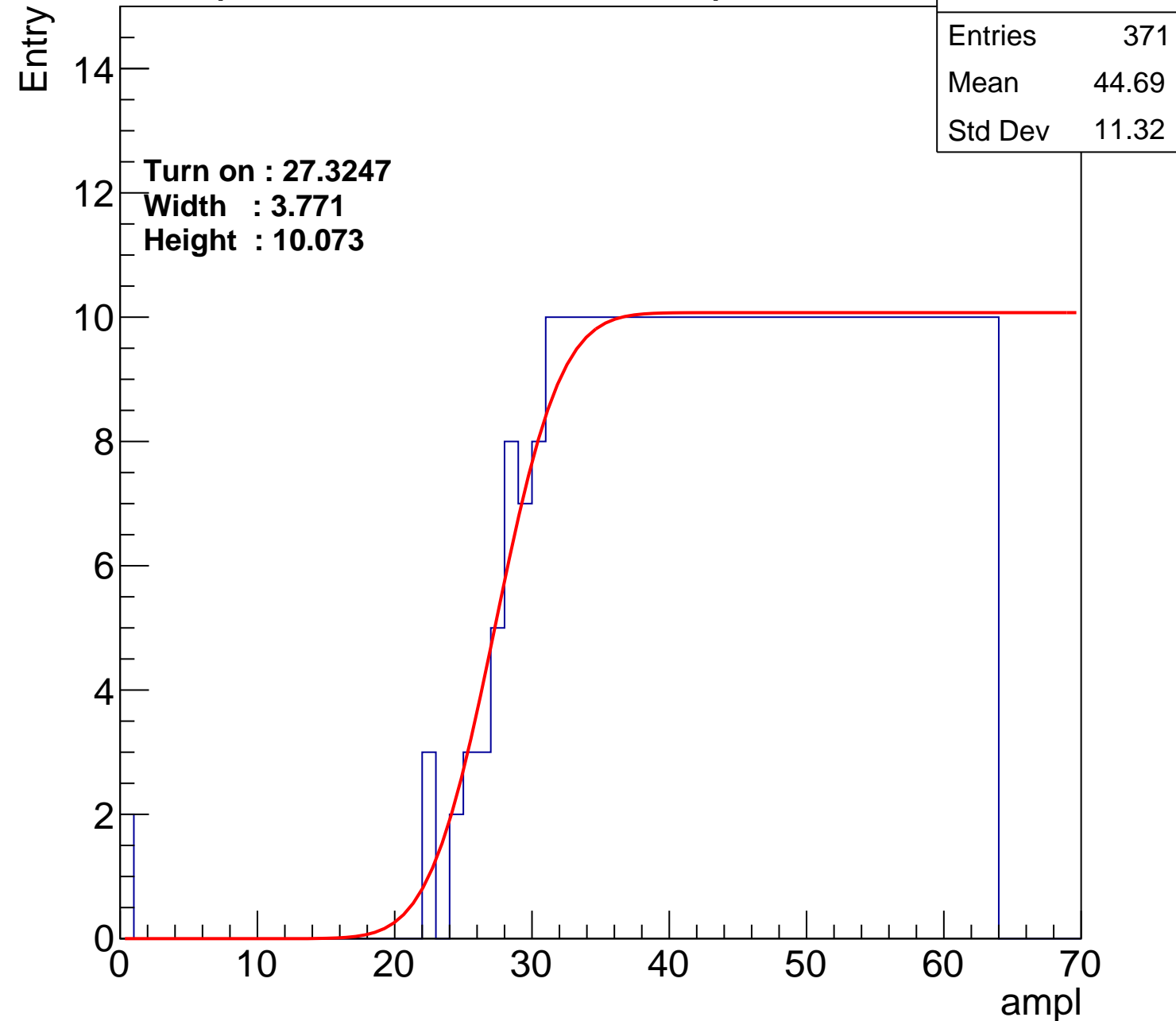
Width : 3.771

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch44

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.27 |
| Std Dev | 11.55 |

Turn on : 26.0791

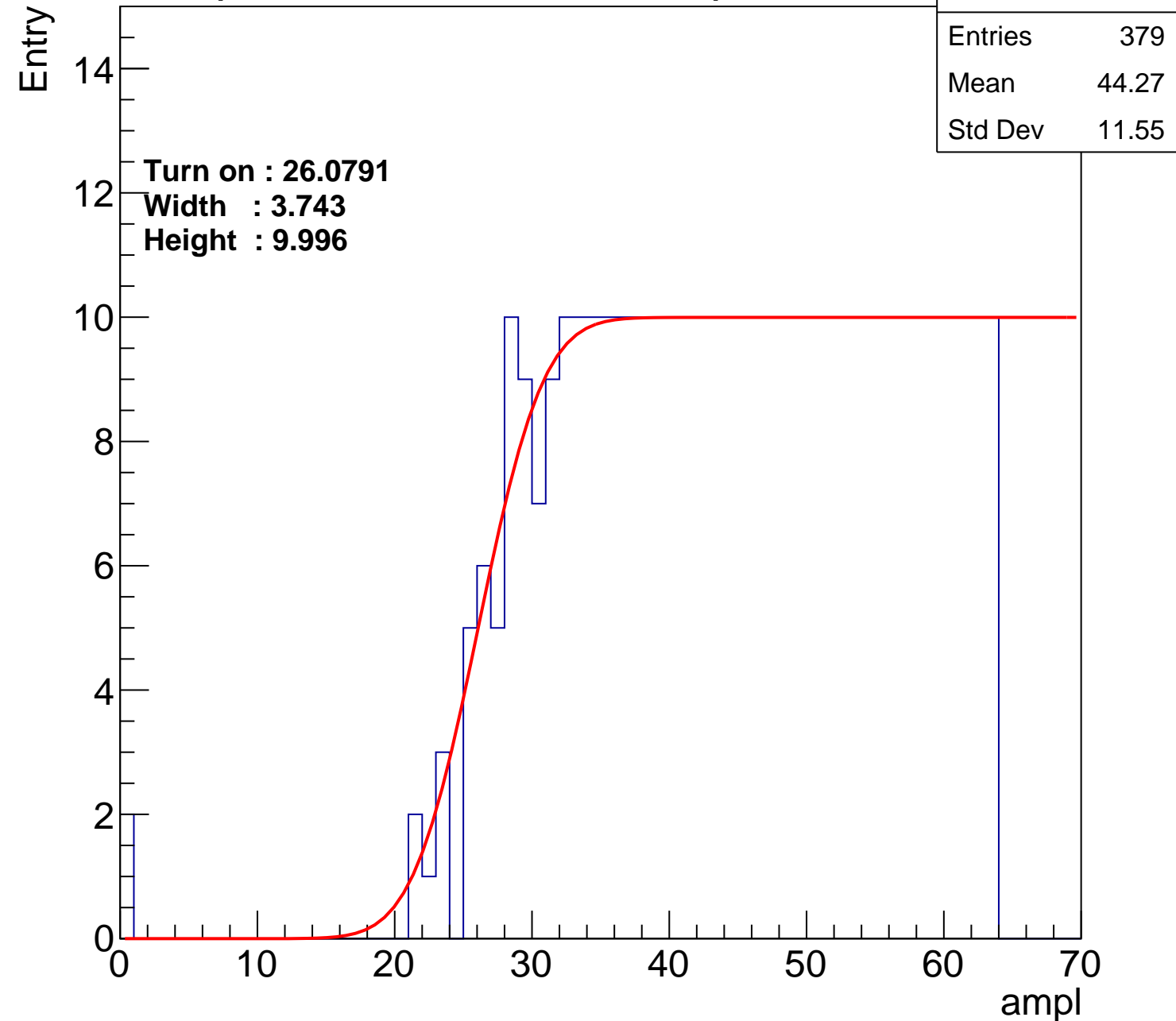
Width : 3.743

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch45

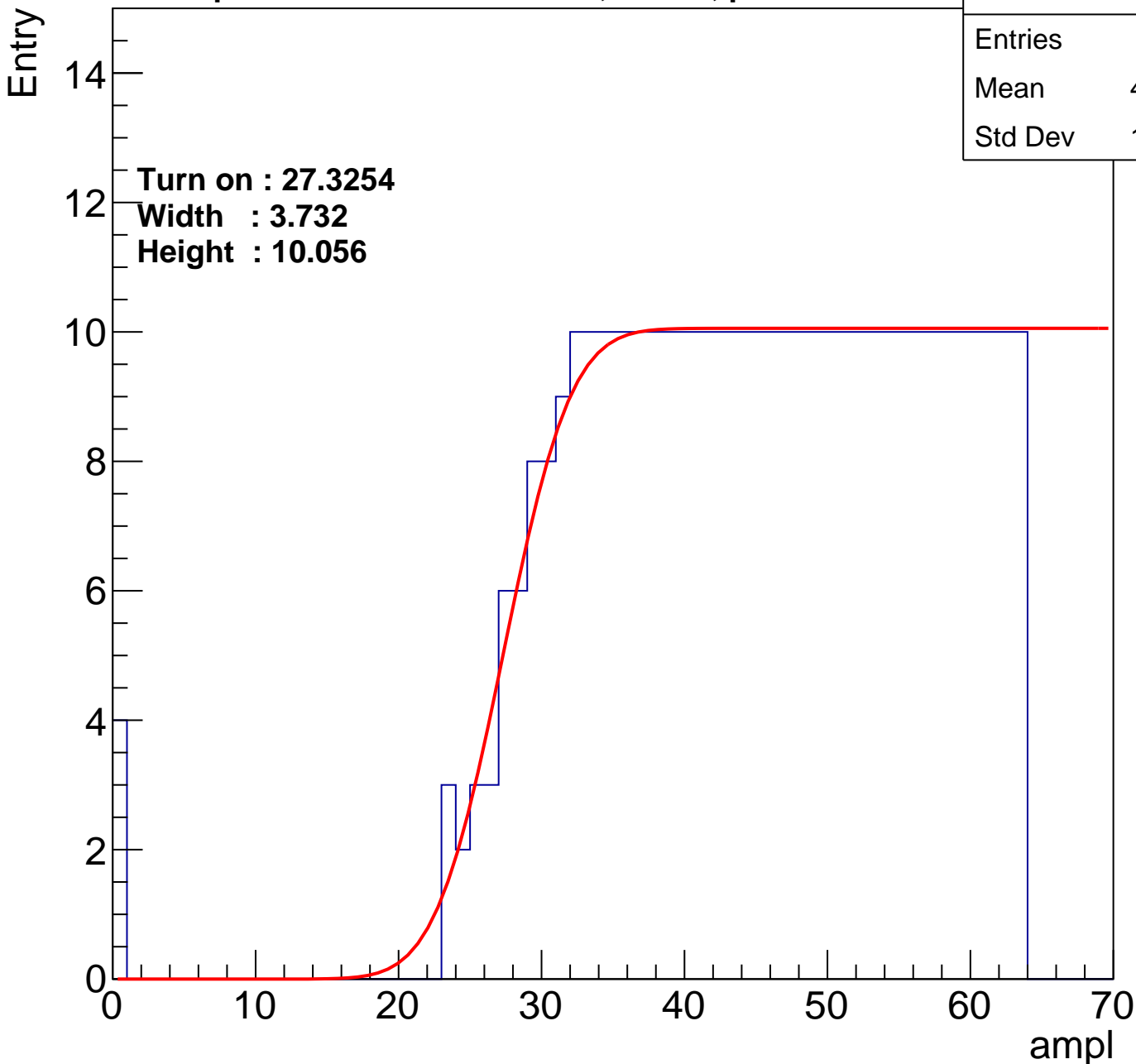
calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.49 |
| Std Dev | 11.73 |

Turn on : 27.3254

Width : 3.732

Height : 10.056



B0L102S, U19-ch46

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.08 |
| Std Dev | 10.91 |

Turn on : 27.6787

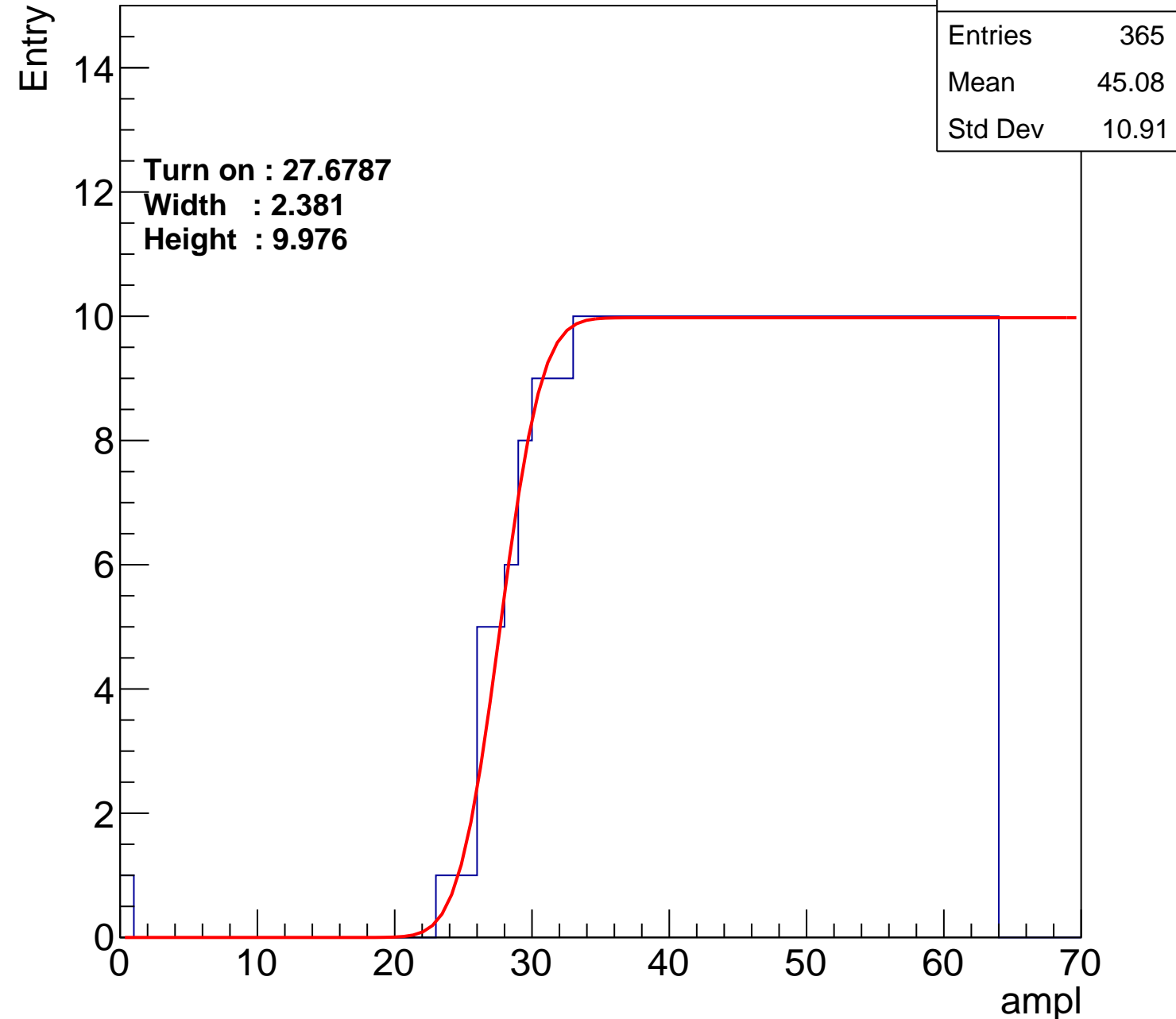
Width : 2.381

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch47

calib_packv5_042523_0143.root, FC#12, port B1

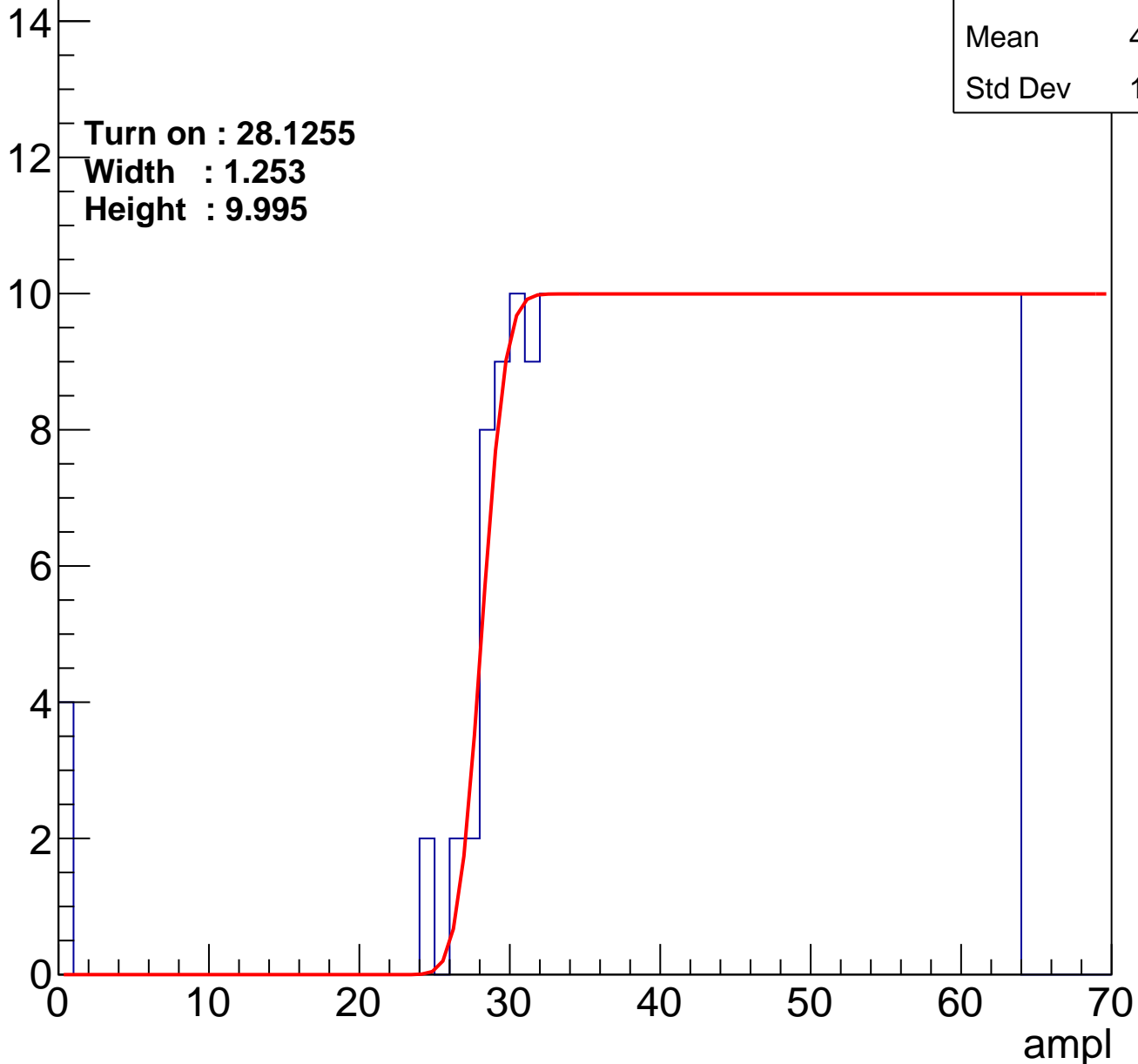
| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.86 |
| Std Dev | 11.48 |

Turn on : 28.1255

Width : 1.253

Height : 9.995

Entry



B0L102S, U19-ch48

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.3 |
| Std Dev | 11.88 |

Turn on : 24.6872

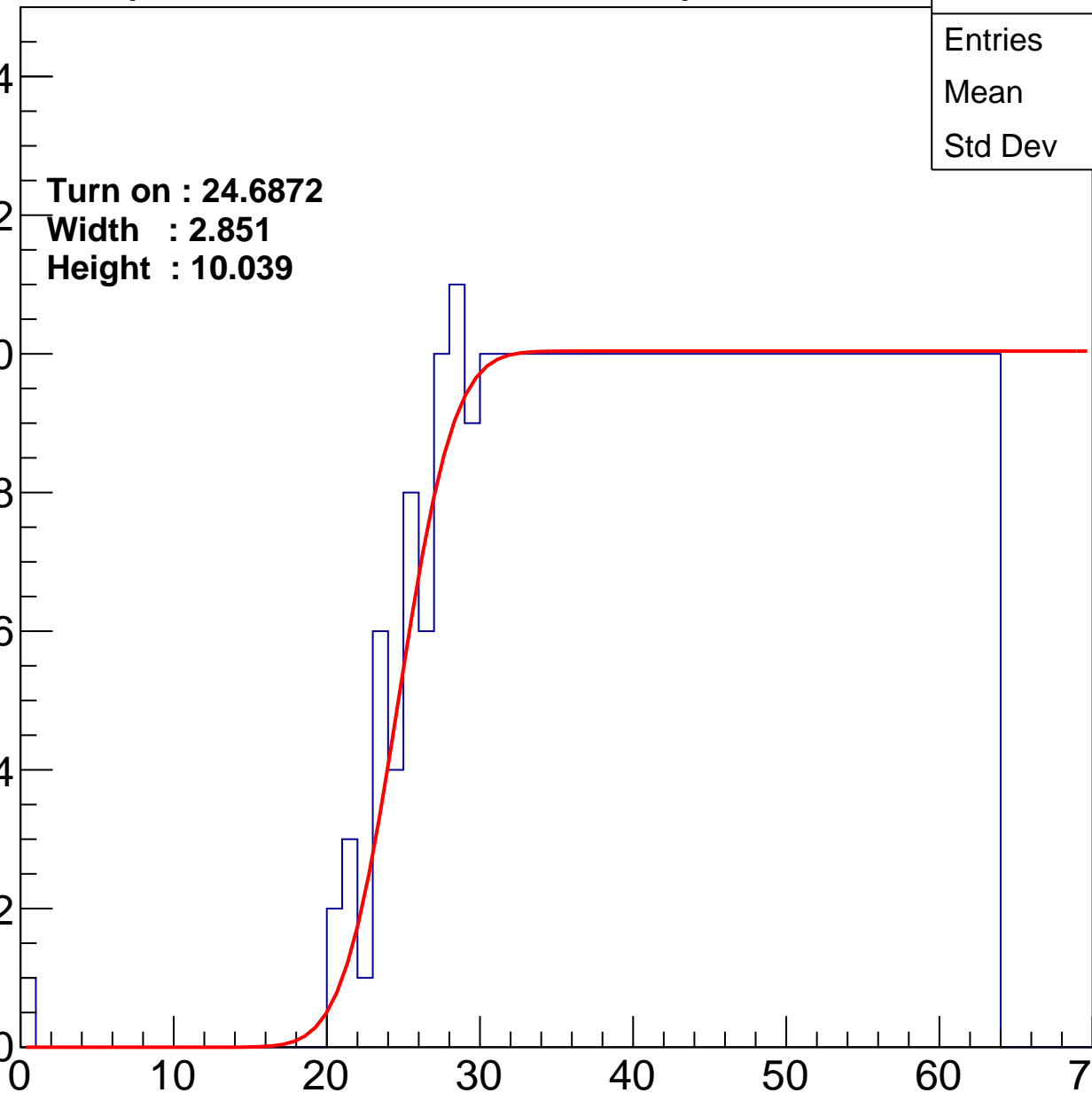
Width : 2.851

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch49

calib_packv5_042523_0143.root, FC#12, port B1

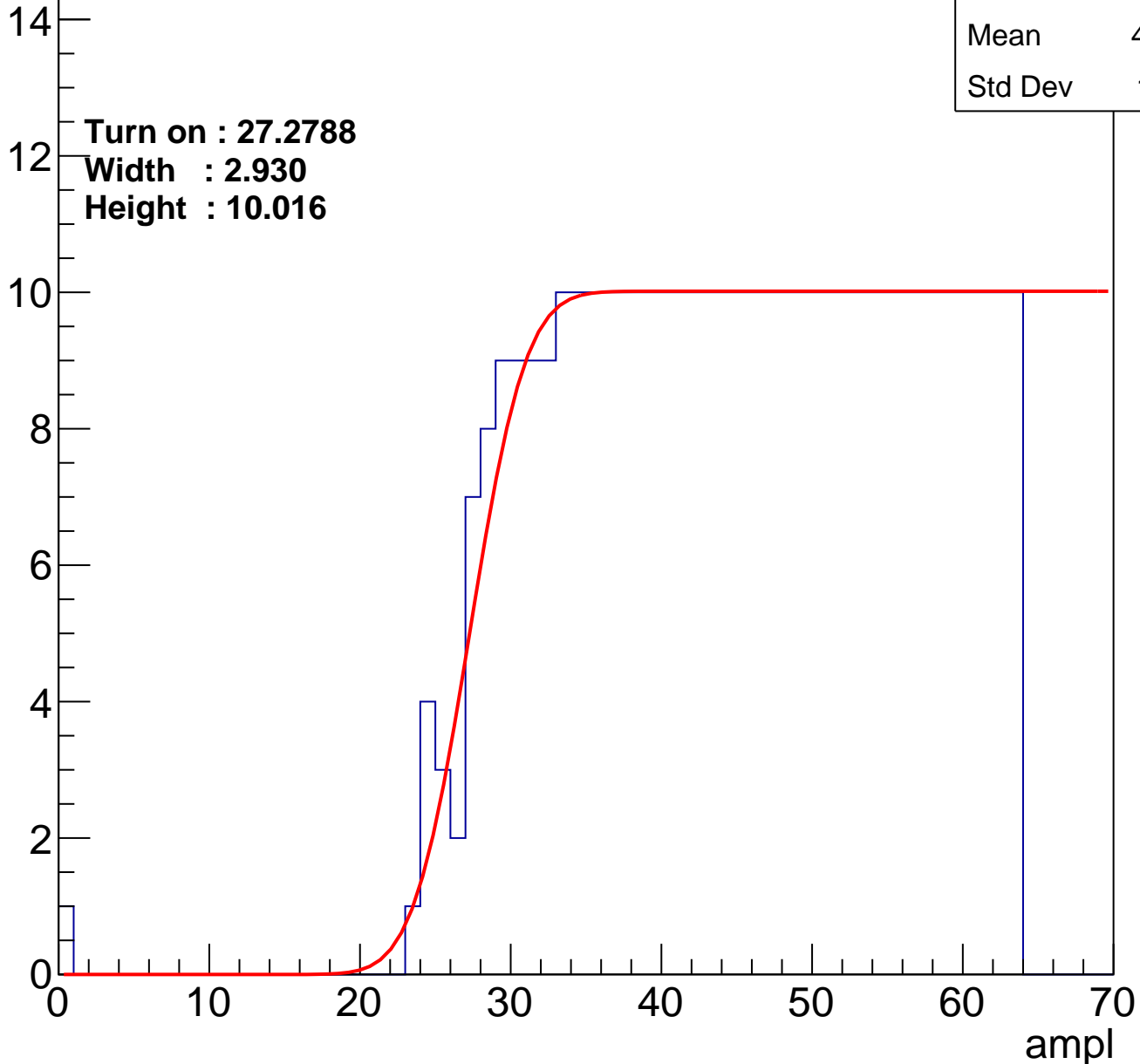
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.72 |
| Std Dev | 11.11 |

Turn on : 27.2788

Width : 2.930

Height : 10.016

Entry



B0L102S, U19-ch50

calib_packv5_042523_0143.root, FC#12, port B1

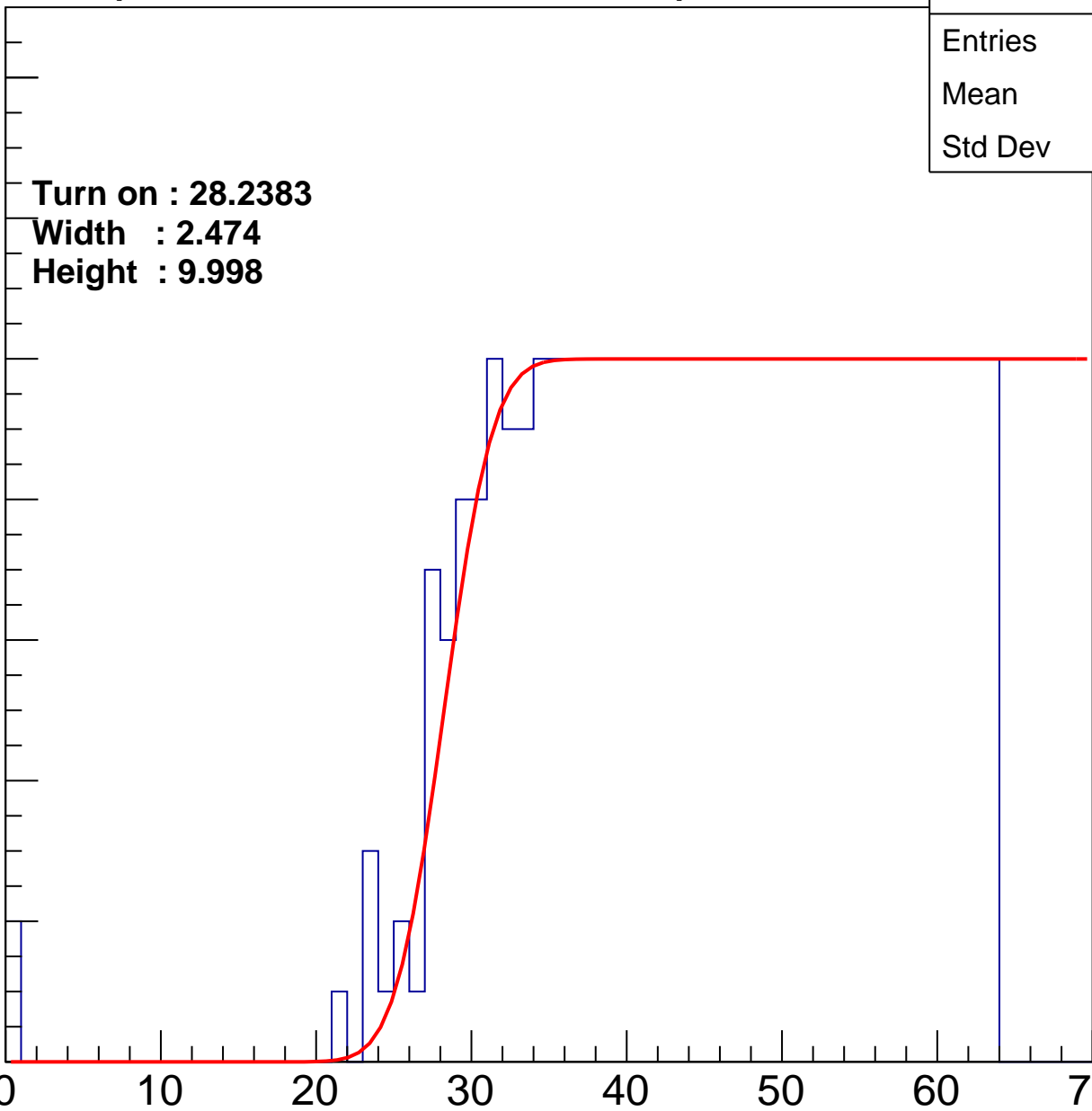
Entry

14
12
10
8
6
4
2
0

Turn on : 28.2383
Width : 2.474
Height : 9.998

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.86 |
| Std Dev | 11.25 |

ampl



B0L102S, U19-ch51

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.42 |
| Std Dev | 11.98 |

Turn on : 24.5822

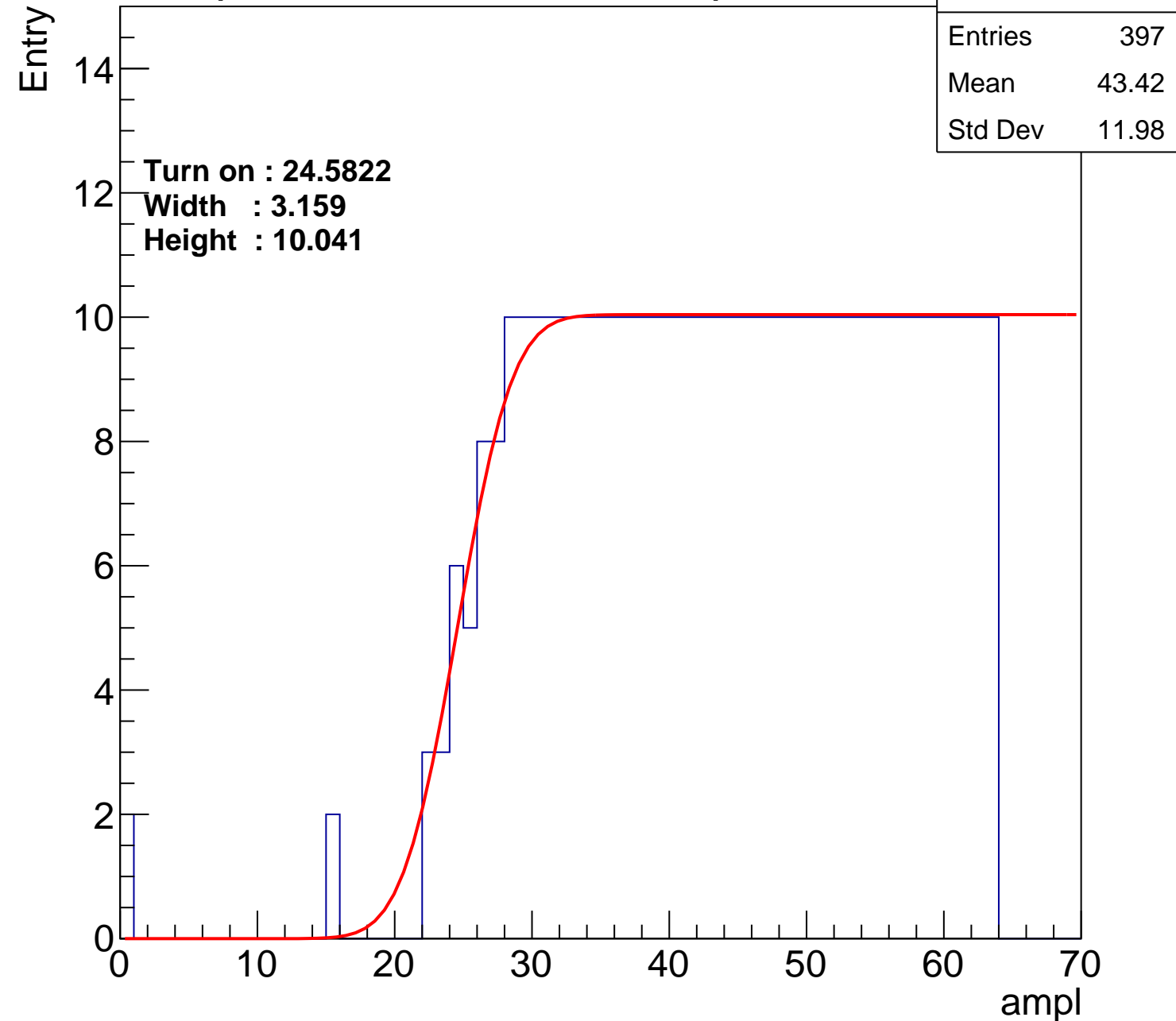
Width : 3.159

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch52

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.32 |
| Std Dev | 11.6 |

Turn on : 26.4298

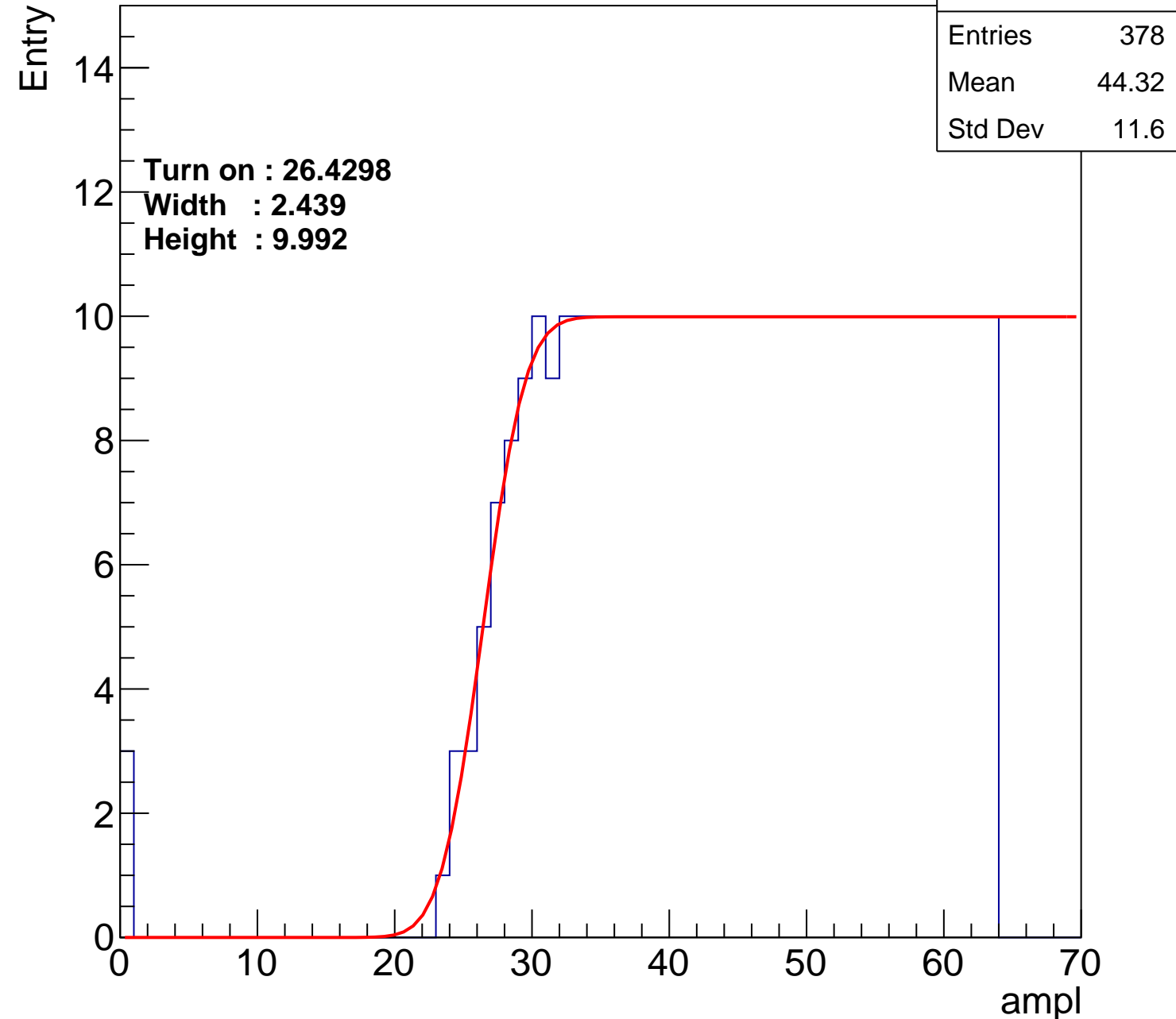
Width : 2.439

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch53

calib_packv5_042523_0143.root, FC#12, port B1

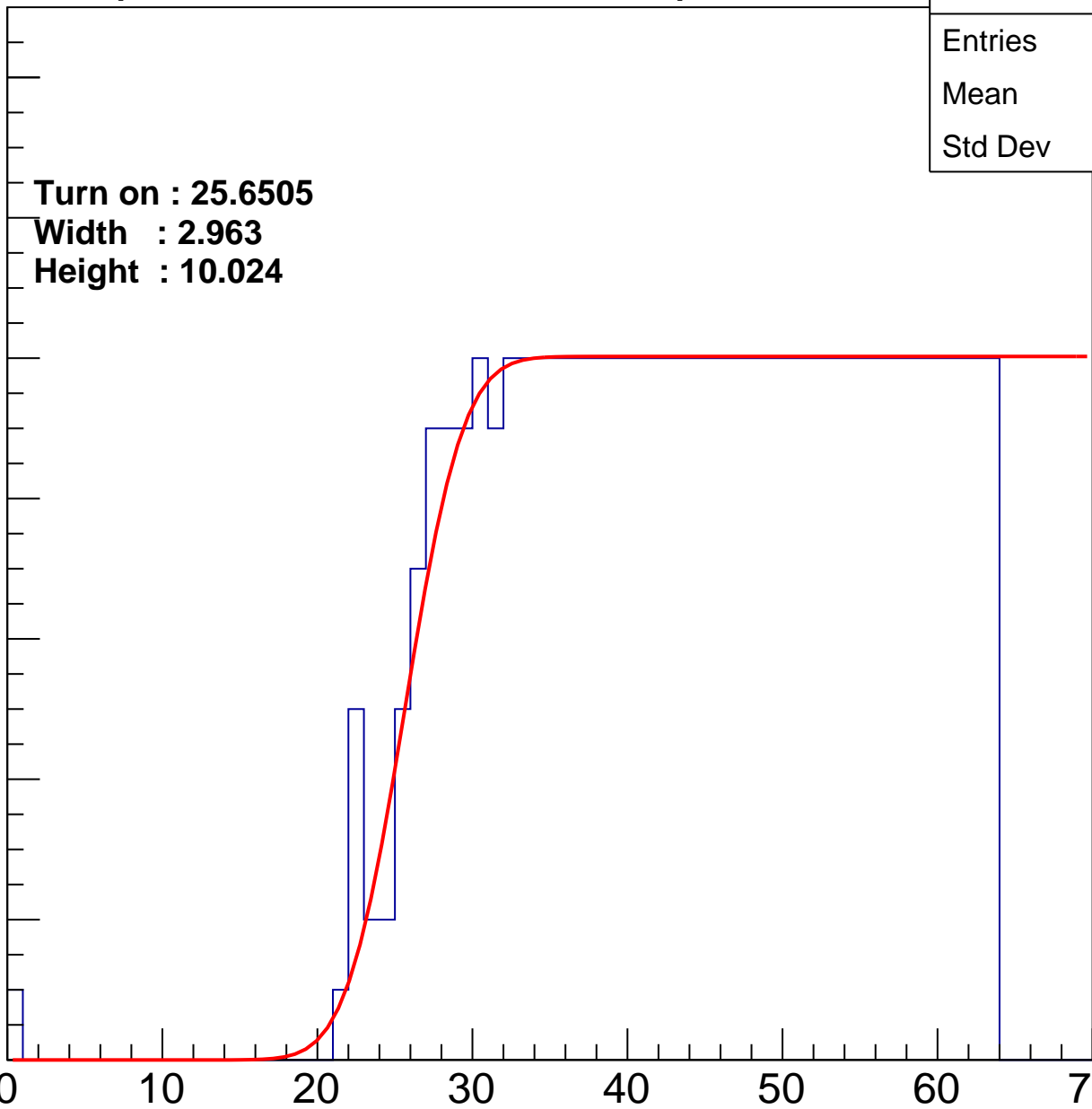
Entry

14
12
10
8
6
4
2
0

Turn on : 25.6505
Width : 2.963
Height : 10.024

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.87 |
| Std Dev | 11.59 |

ampl



B0L102S, U19-ch54

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.02 |
| Std Dev | 12.06 |

Turn on : 26.9452

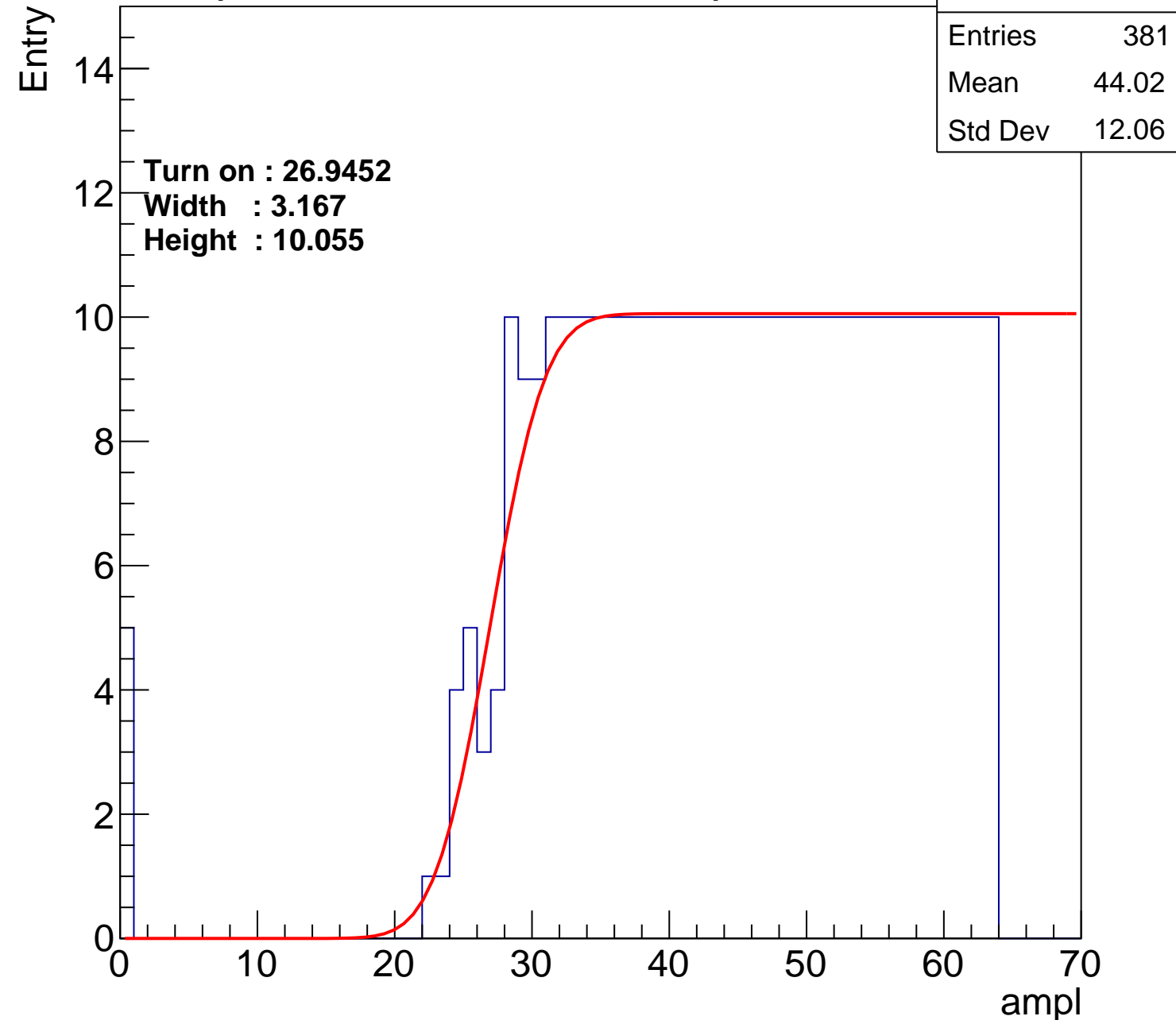
Width : 3.167

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch55

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.83 |
| Std Dev | 11.85 |

Turn on : 26.7141

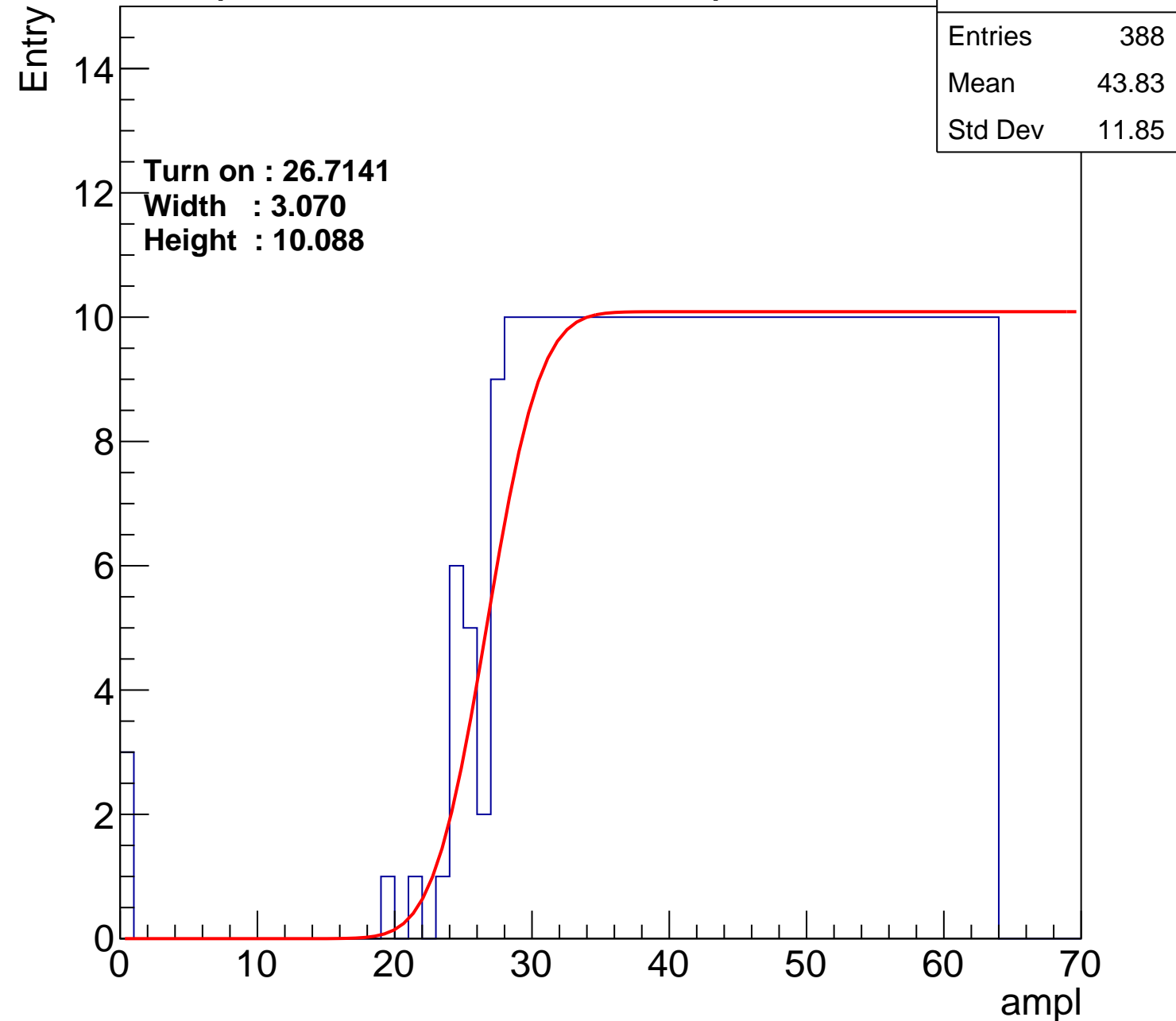
Width : 3.070

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch56

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.97 |
| Std Dev | 10.94 |

Turn on : 27.7472

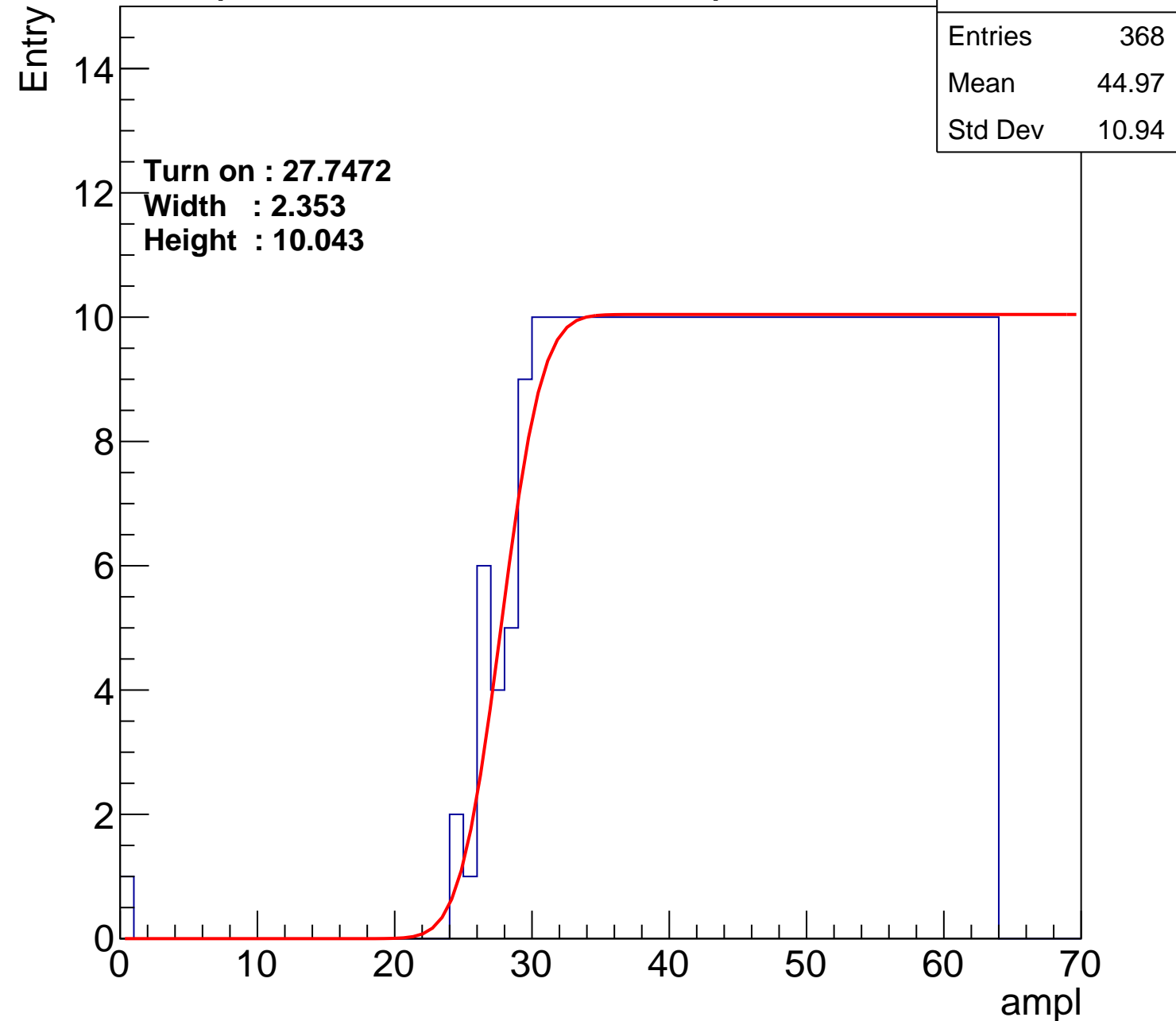
Width : 2.353

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch57

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.29 |
| Std Dev | 11.47 |

Turn on : 26.7289

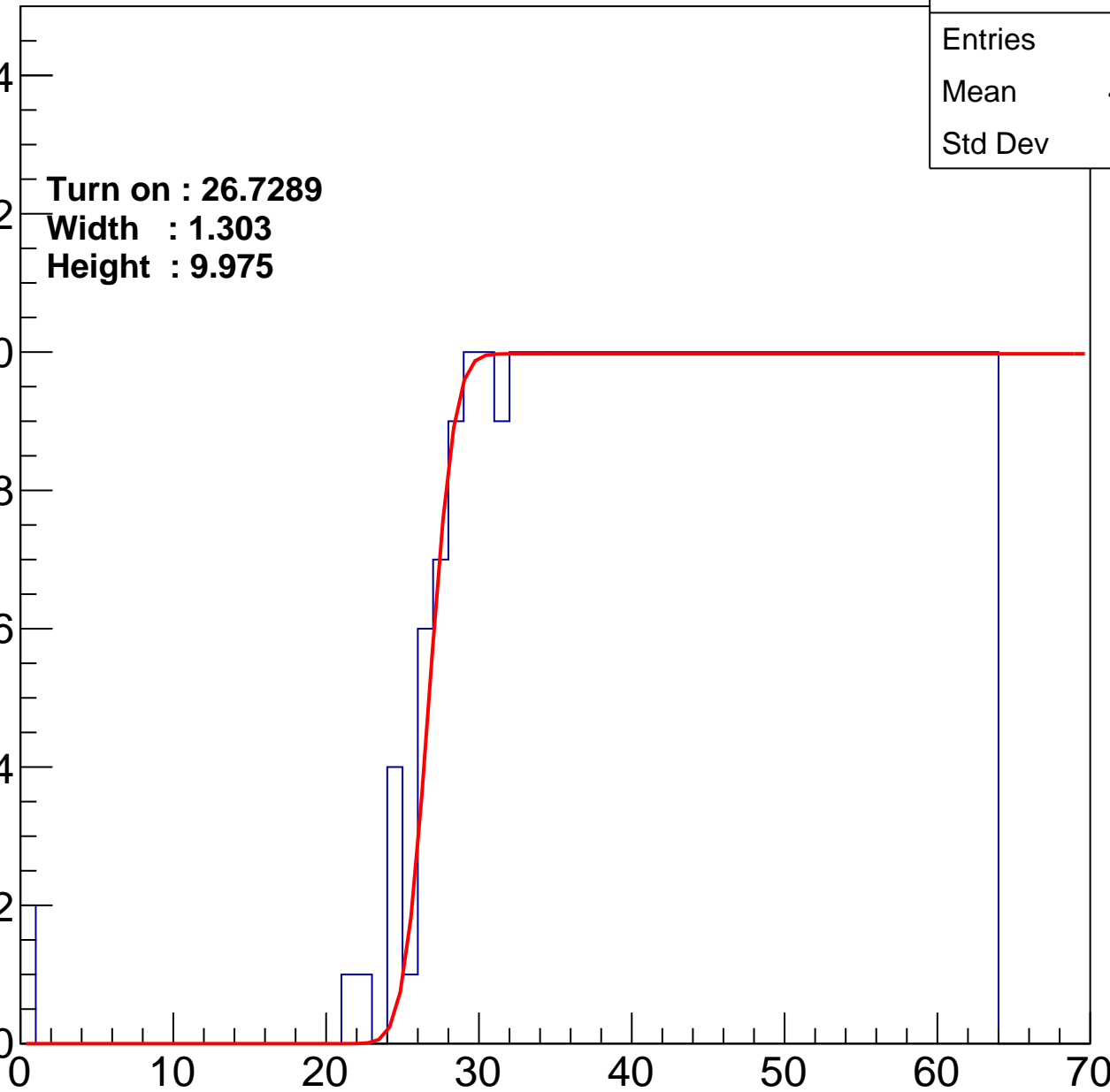
Width : 1.303

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch58

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.68 |
| Std Dev | 11.93 |

Turn on : 25.5596

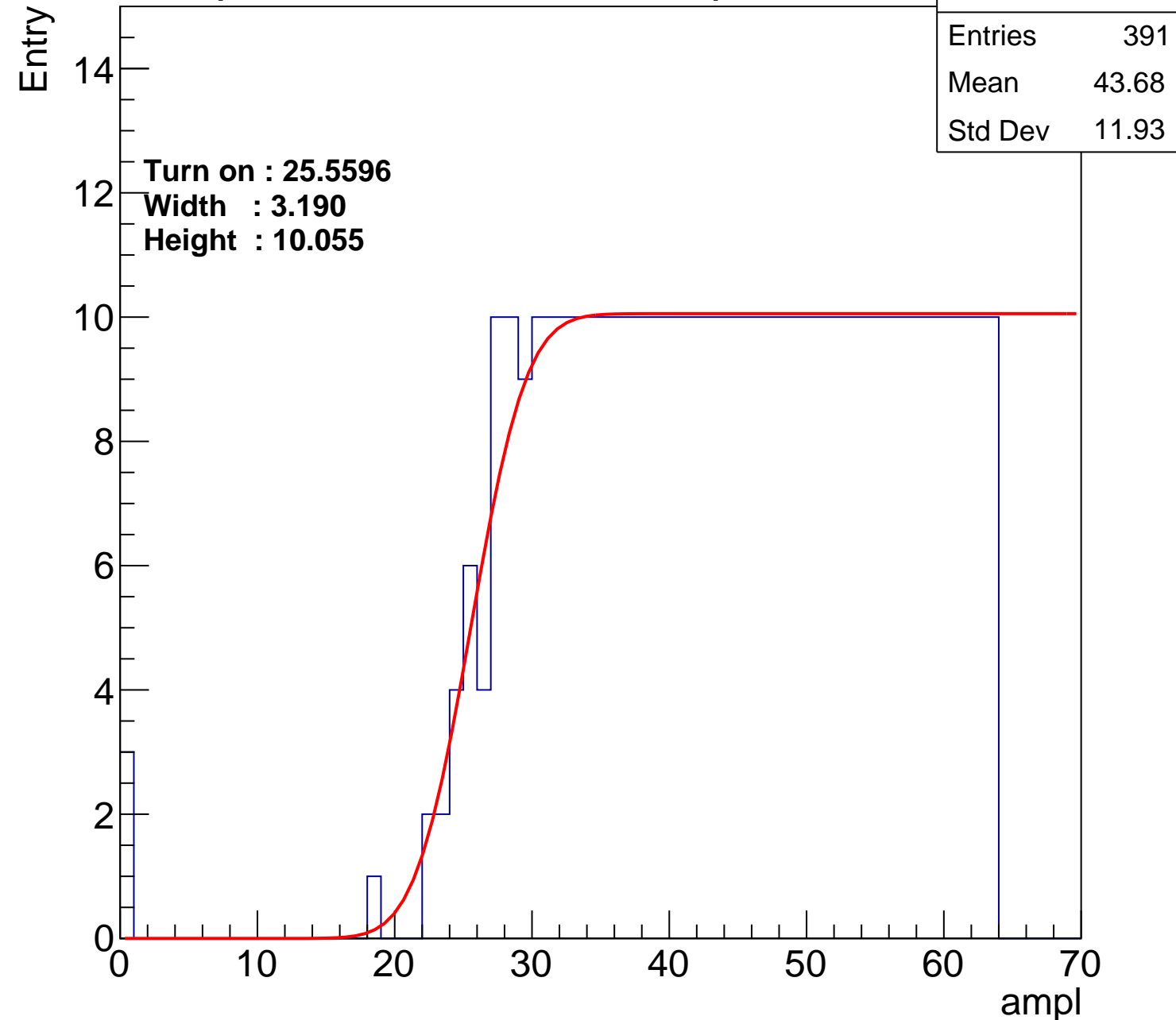
Width : 3.190

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch59

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.06 |
| Std Dev | 10.95 |

Turn on : 27.5228

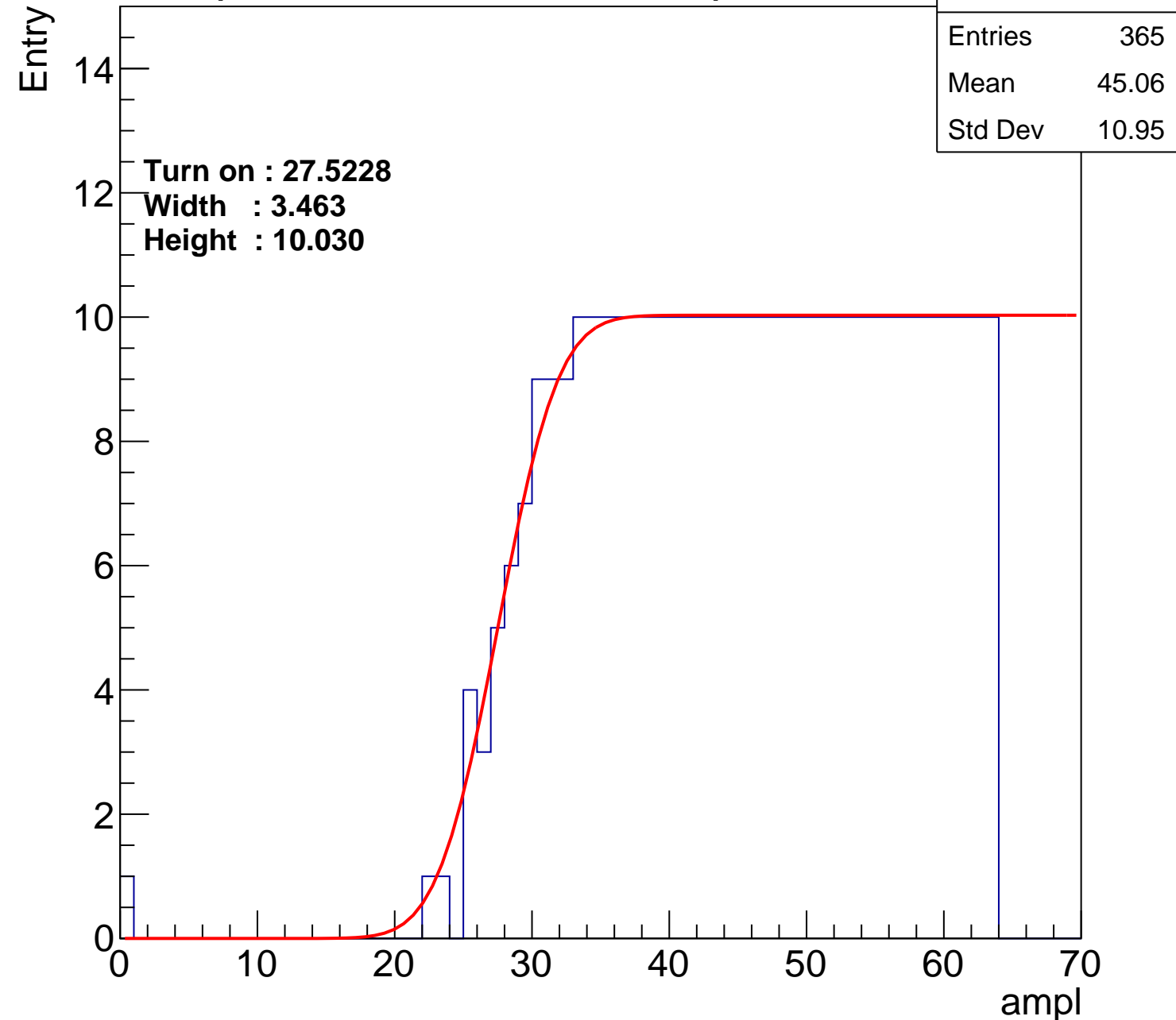
Width : 3.463

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch60

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.29 |
| Std Dev | 10.94 |

Turn on : 28.5367

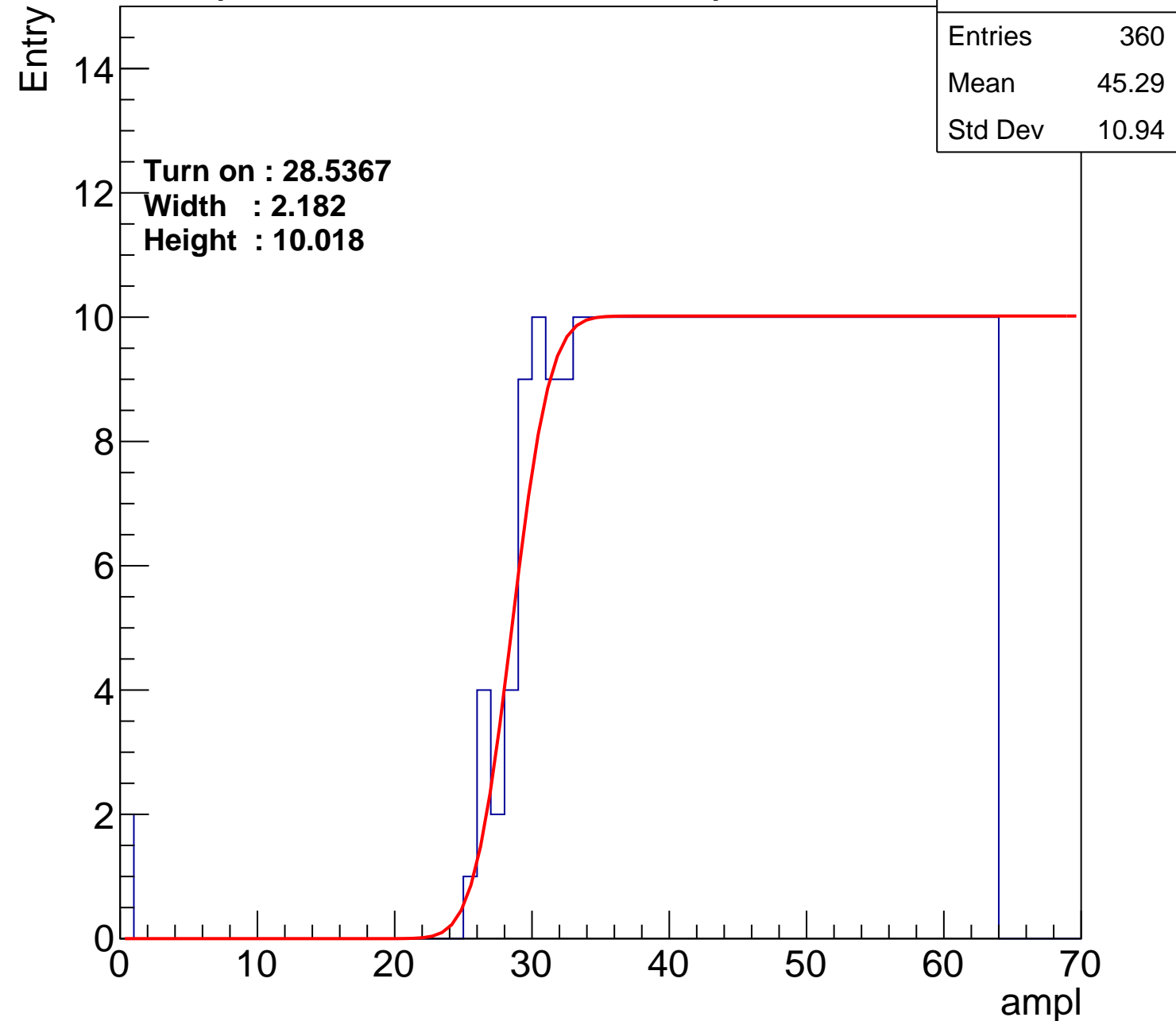
Width : 2.182

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch61

calib_packv5_042523_0143.root, FC#12, port B1

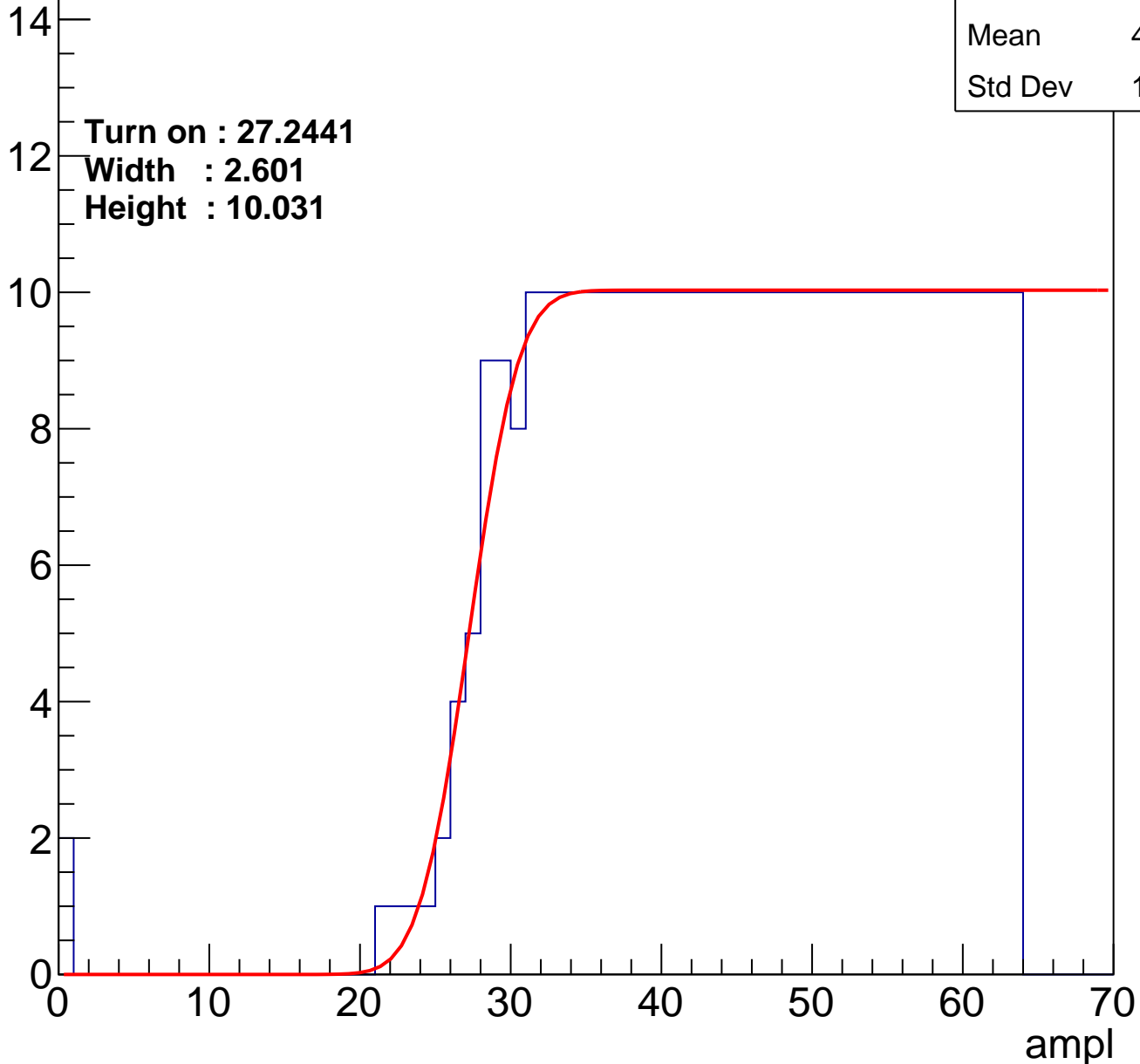
| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.62 |
| Std Dev | 11.32 |

Turn on : 27.2441

Width : 2.601

Height : 10.031

Entry



B0L102S, U19-ch62

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.53 |
| Std Dev | 11.72 |

Turn on : 27.4309

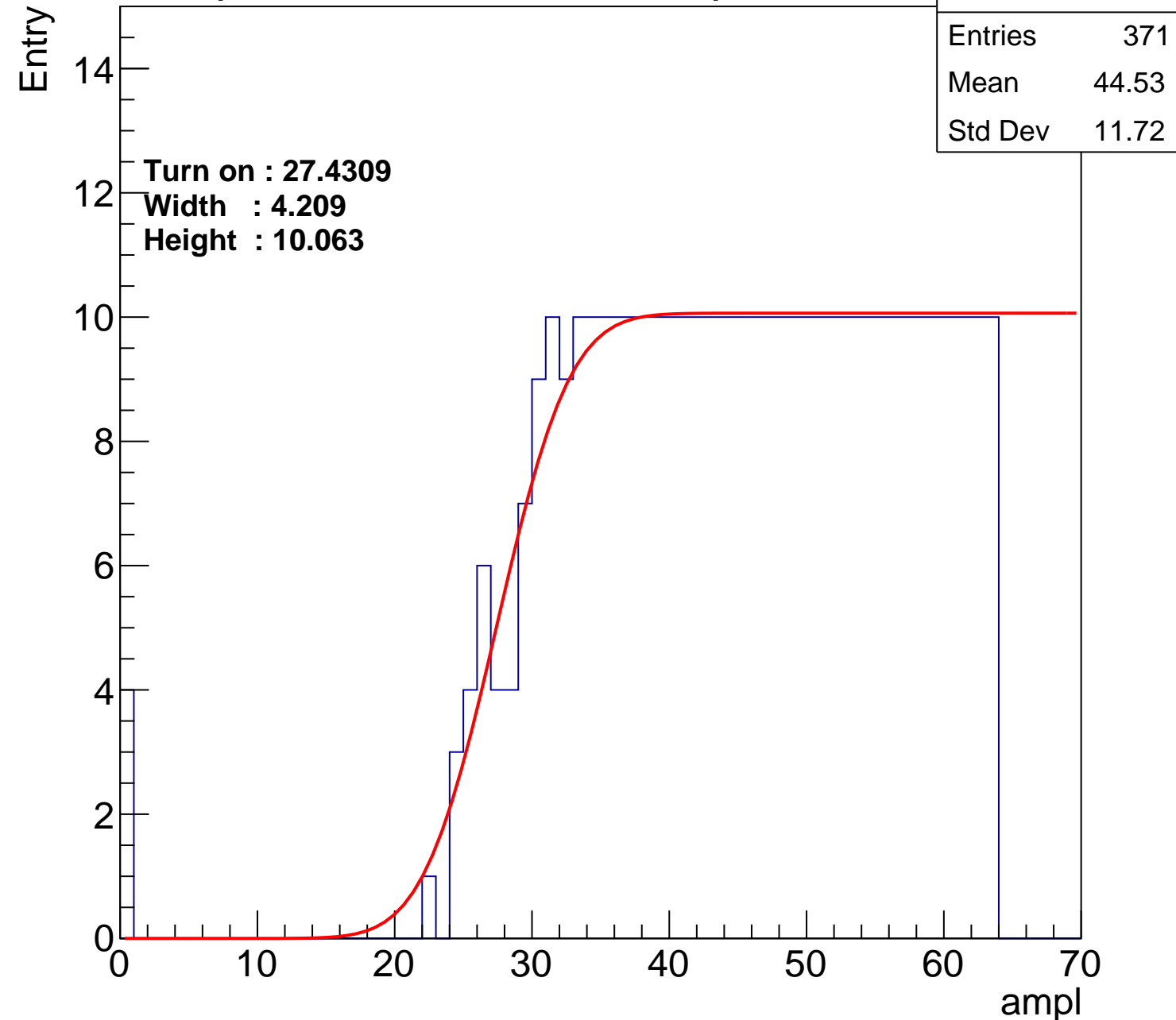
Width : 4.209

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch63

calib_packv5_042523_0143.root, FC#12, port B1

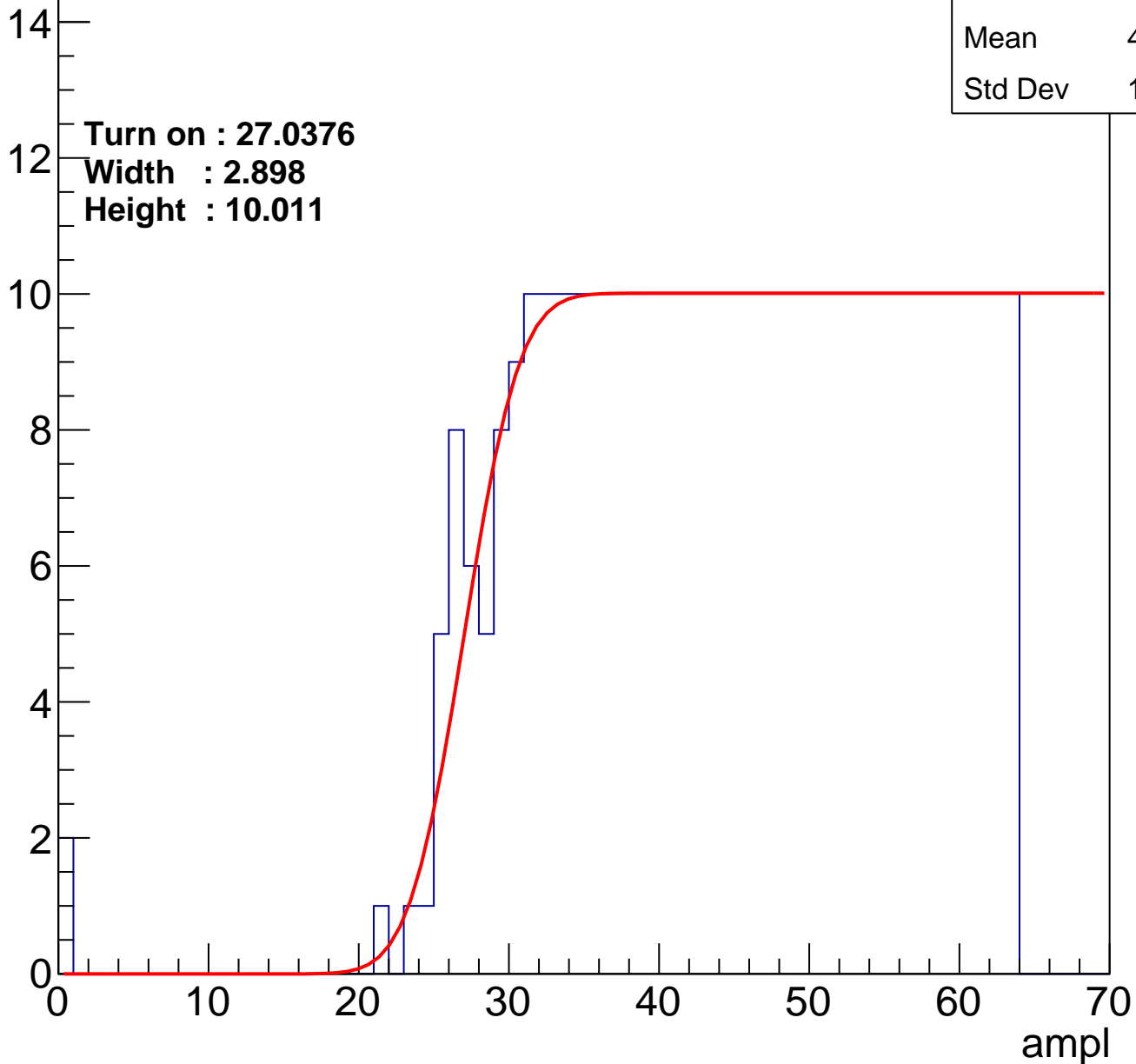
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.45 |
| Std Dev | 11.42 |

Turn on : 27.0376

Width : 2.898

Height : 10.011

Entry



B0L102S, U19-ch64

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.32 |
| Std Dev | 11.78 |

Turn on : 26.7609

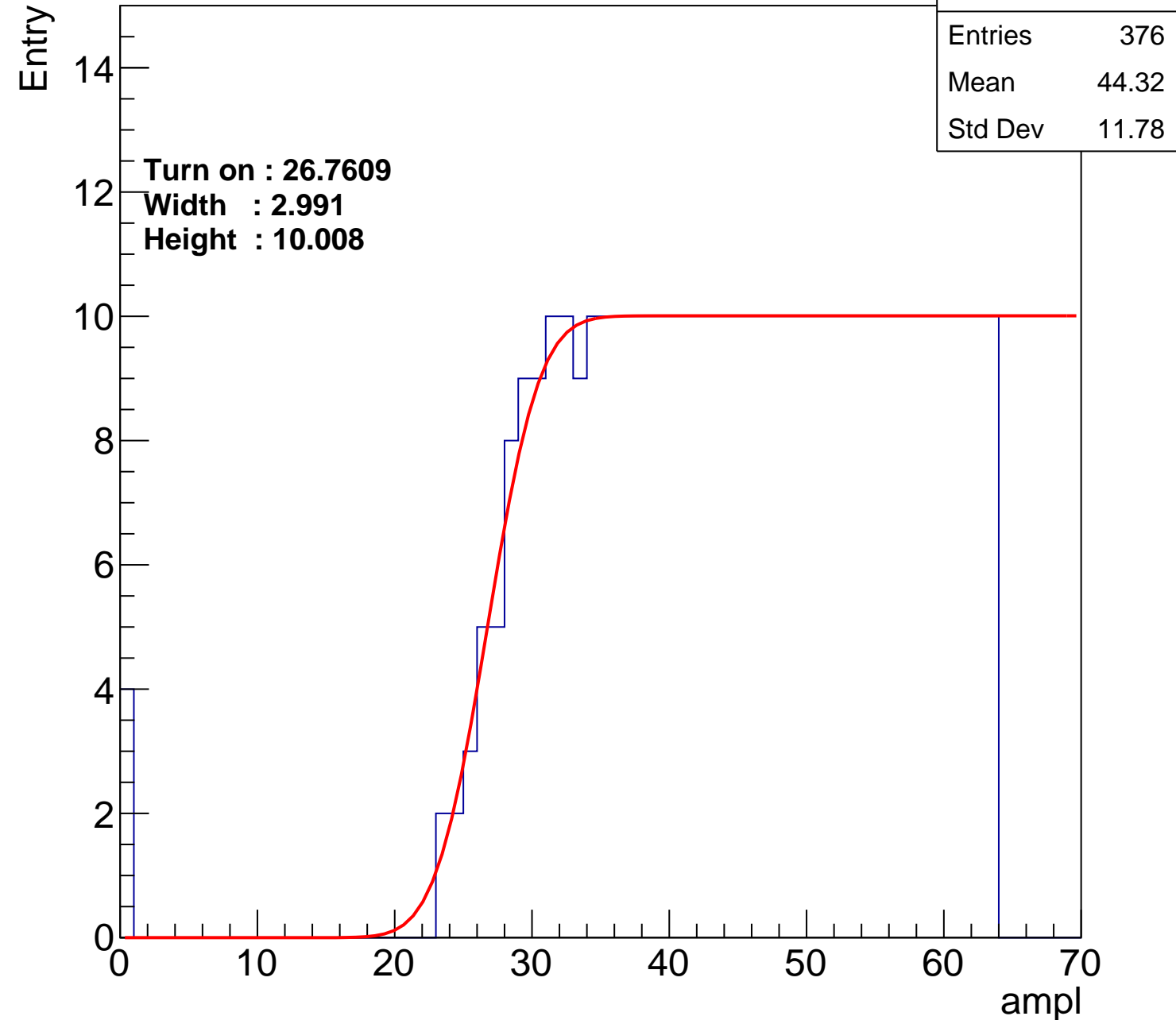
Width : 2.991

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch65

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.83 |
| Std Dev | 11.19 |

Turn on : 27.2148

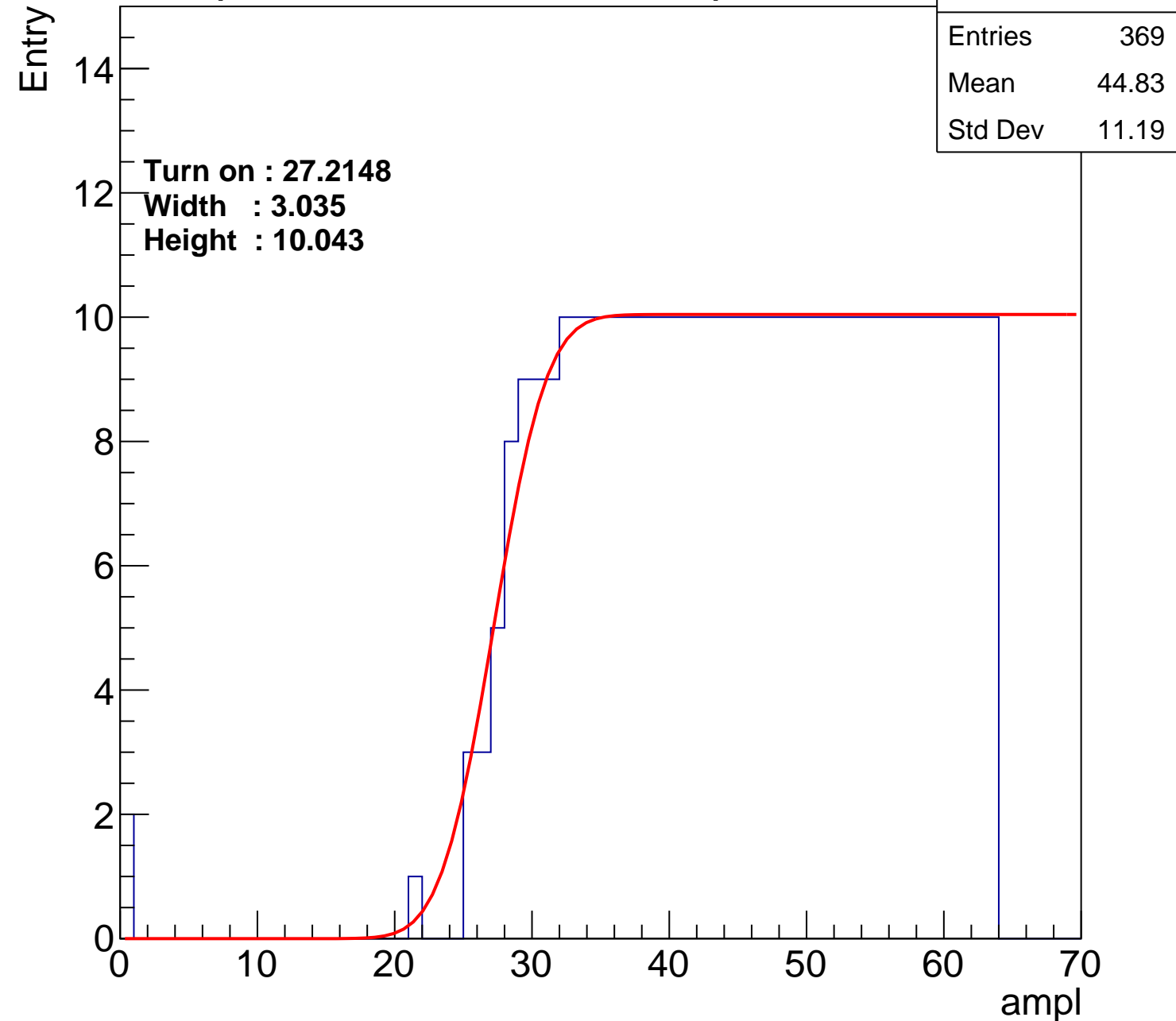
Width : 3.035

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch66

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.58 |
| Std Dev | 11.32 |

Turn on : 26.7639

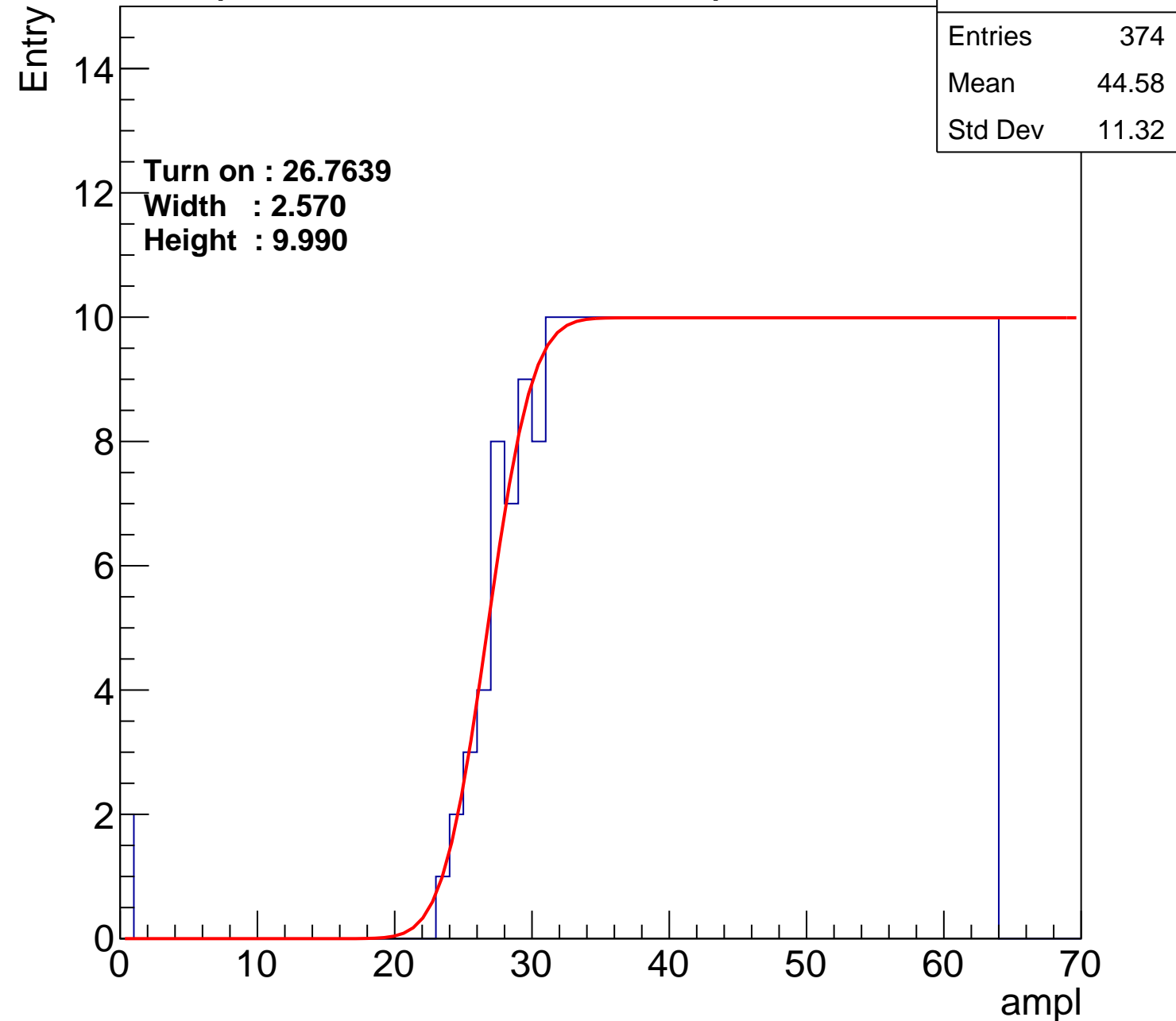
Width : 2.570

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch67

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.66 |
| Std Dev | 11.75 |

Turn on : 27.7414

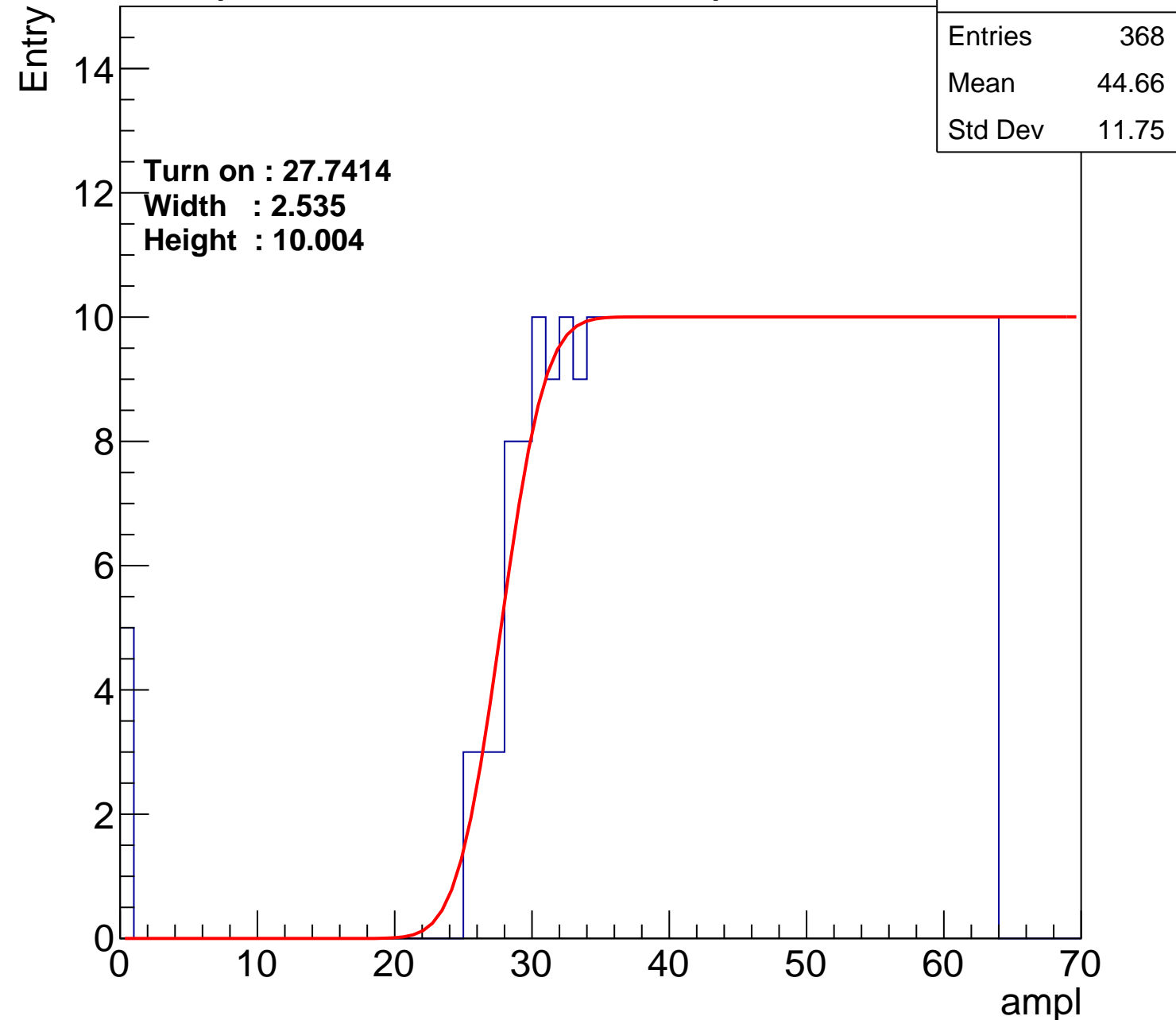
Width : 2.535

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch68

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.22 |
| Std Dev | 11.83 |

Turn on : 26.8138

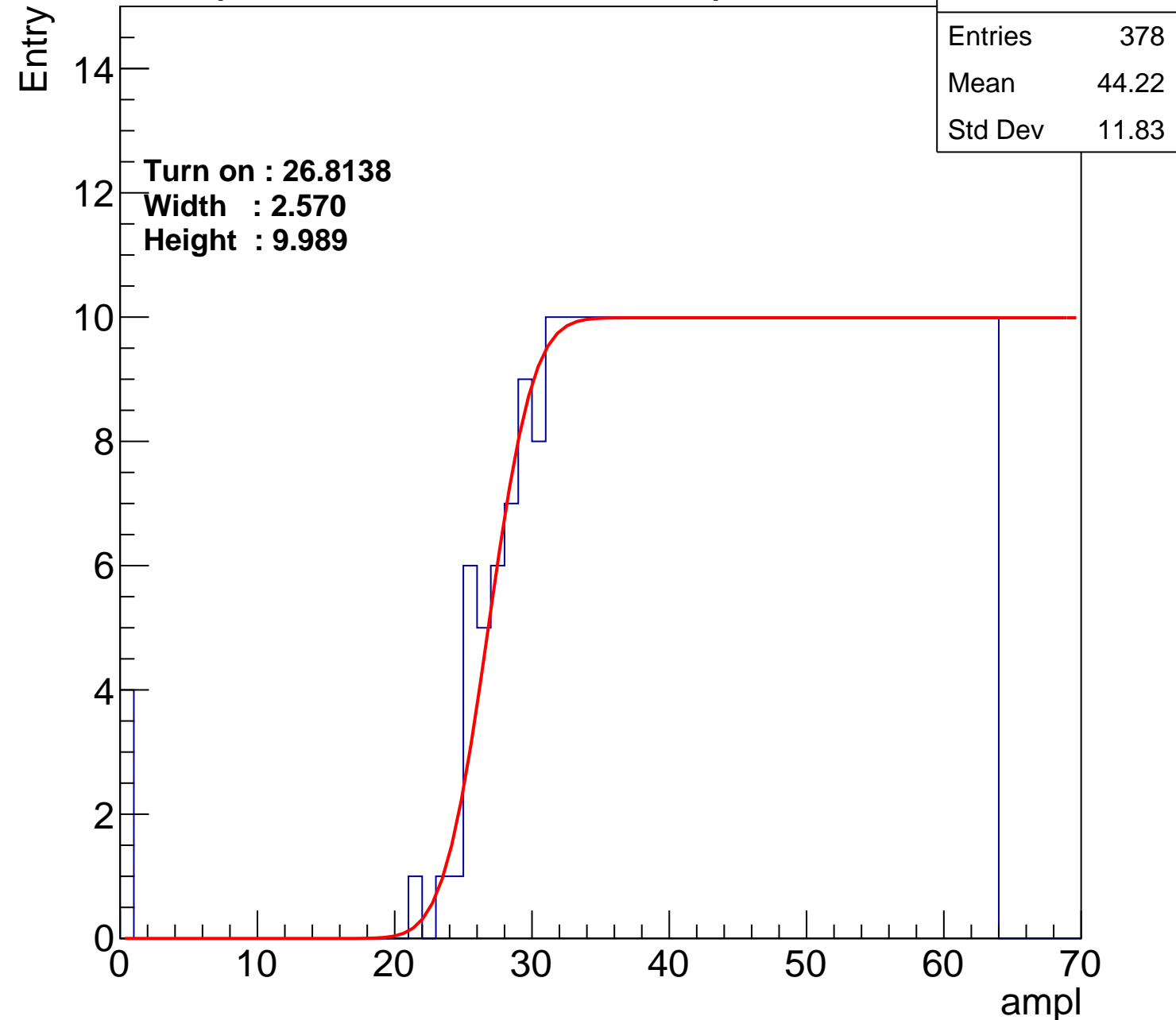
Width : 2.570

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch69

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.25 |
| Std Dev | 11.48 |

Turn on : 26.2252

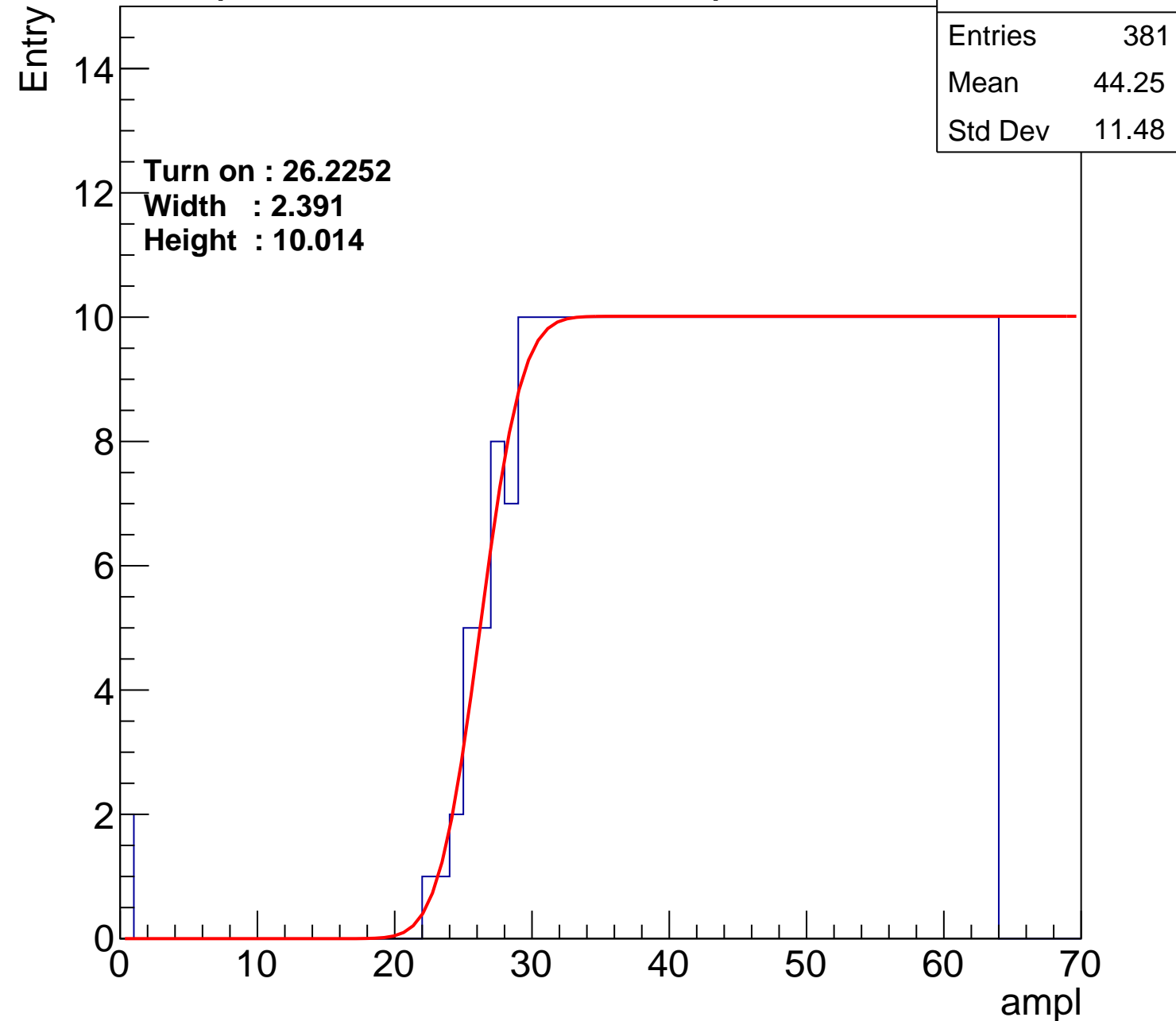
Width : 2.391

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch70

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.74 |
| Std Dev | 11.14 |

Turn on : 26.9039

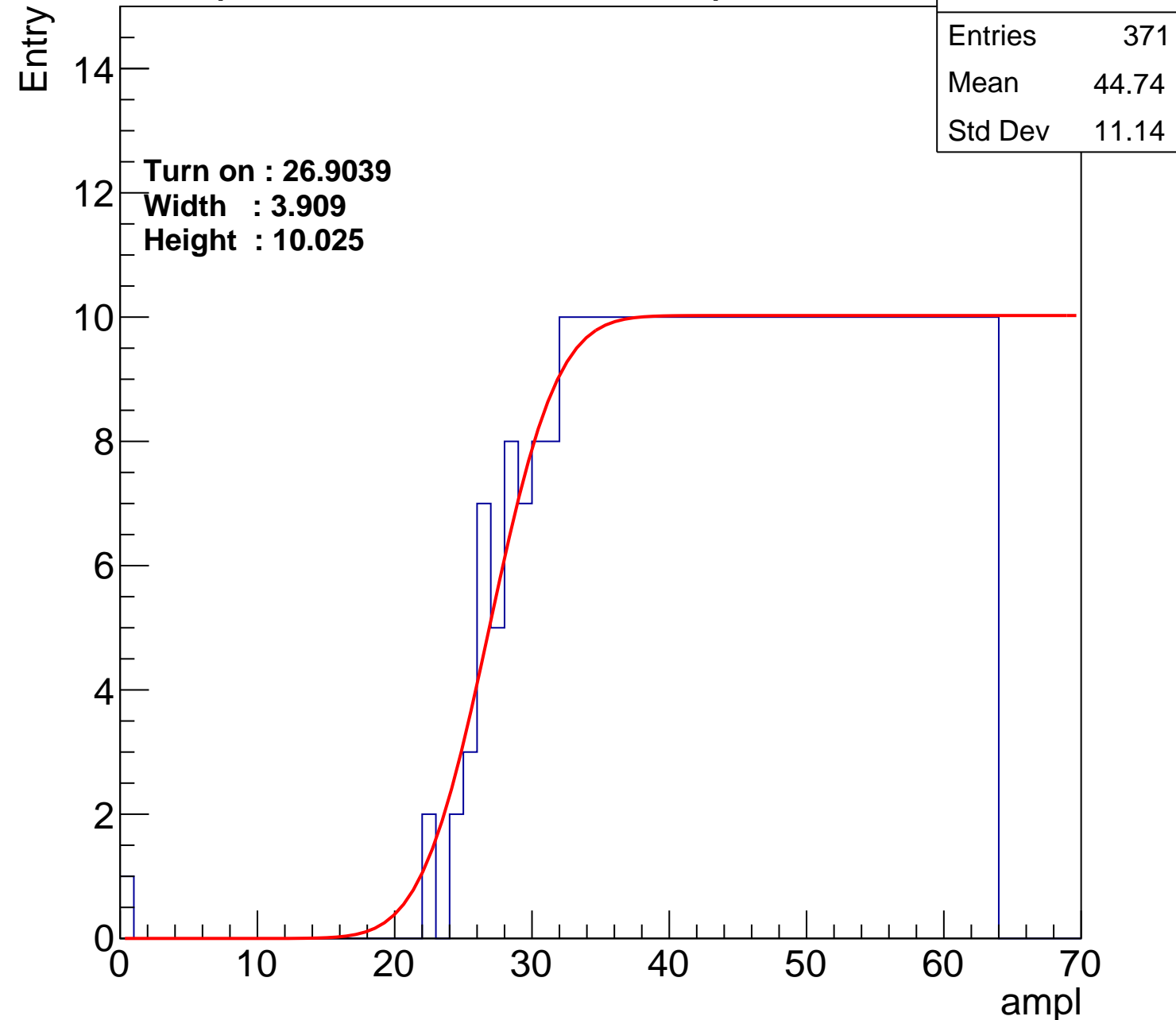
Width : 3.909

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch71

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|------|
| Entries | 382 |
| Mean | 44.1 |
| Std Dev | 11.7 |

Turn on : 26.2330

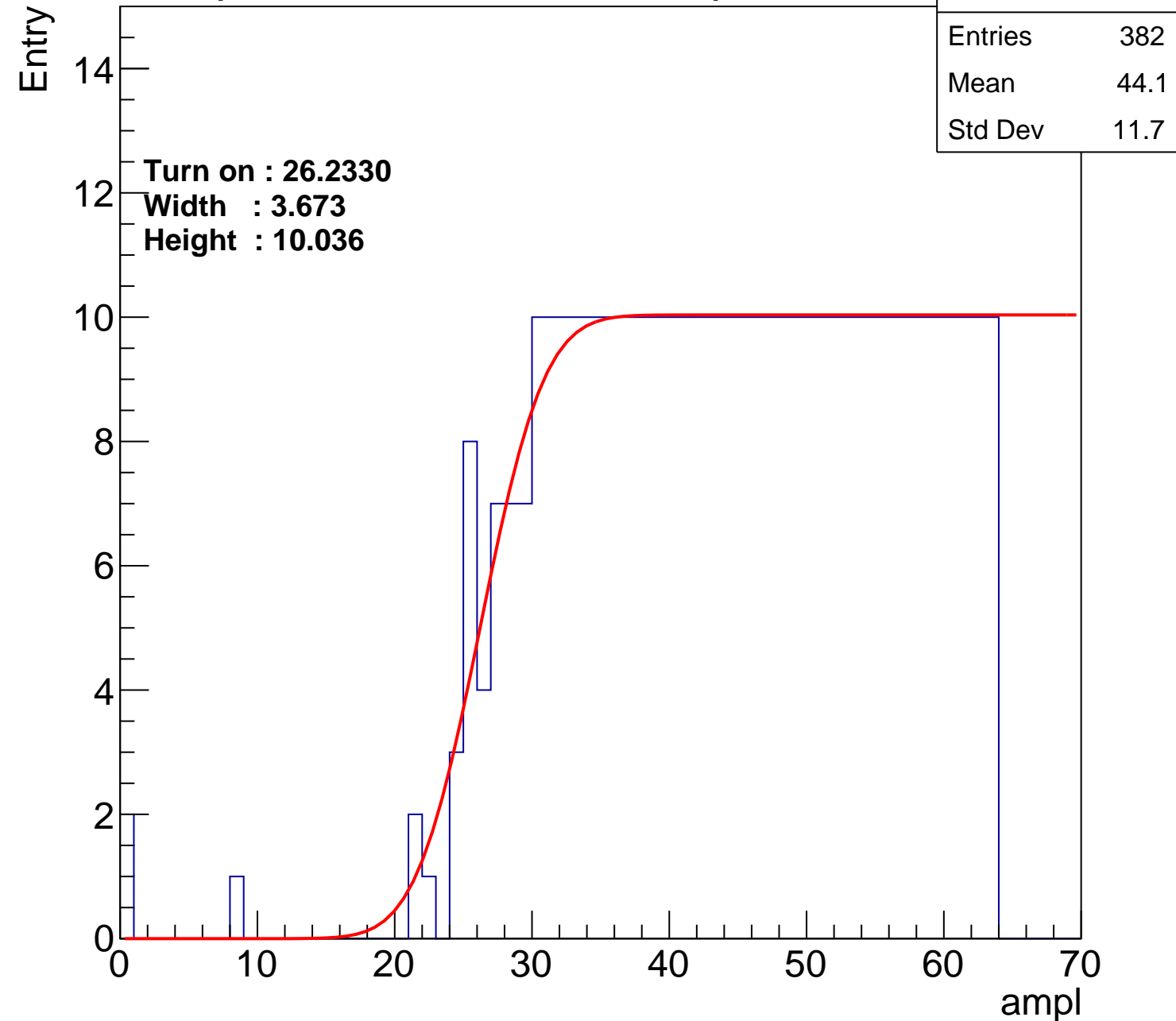
Width : 3.673

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch72

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.49 |
| Std Dev | 11.79 |

Turn on : 25.3885

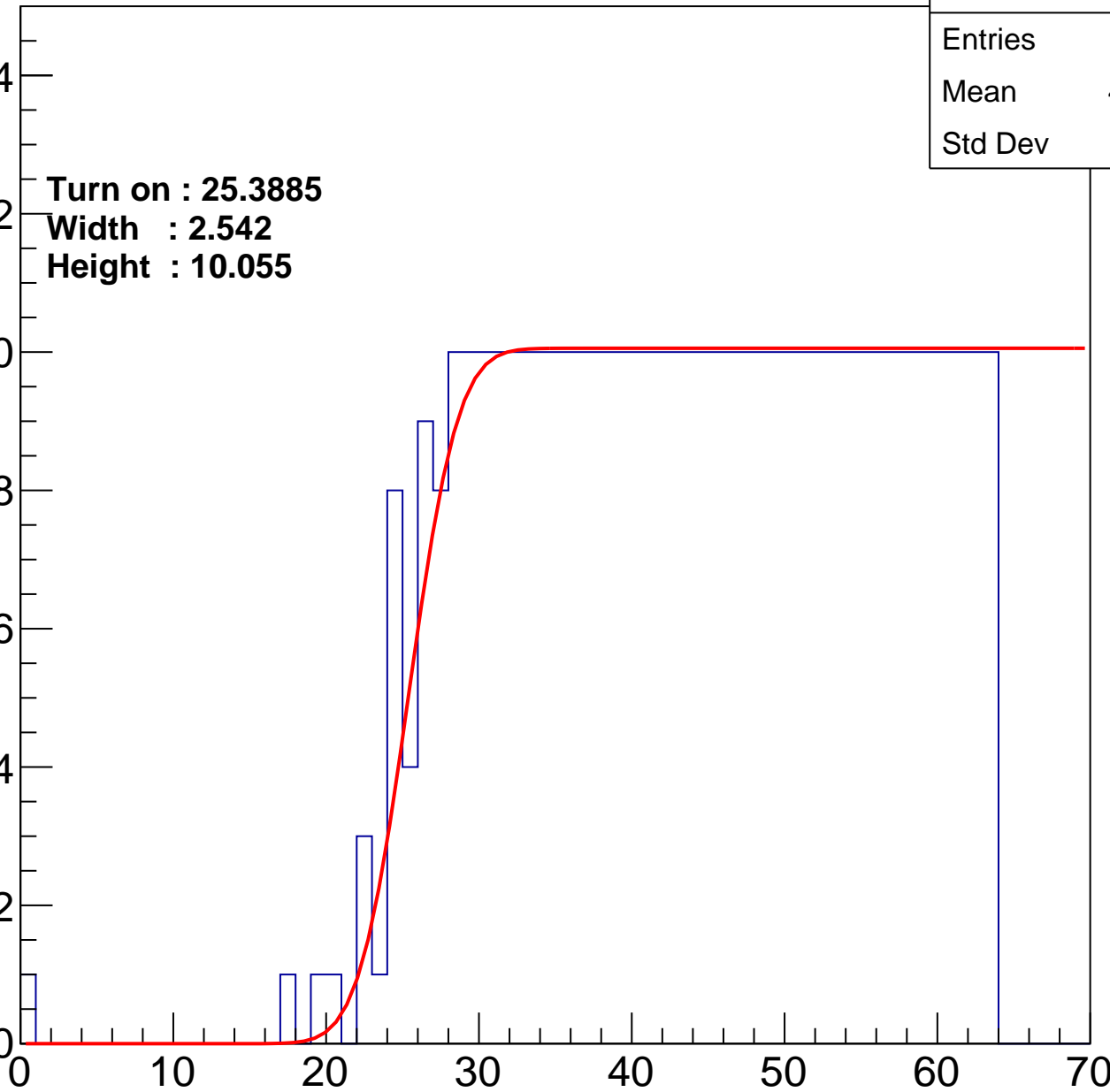
Width : 2.542

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch73

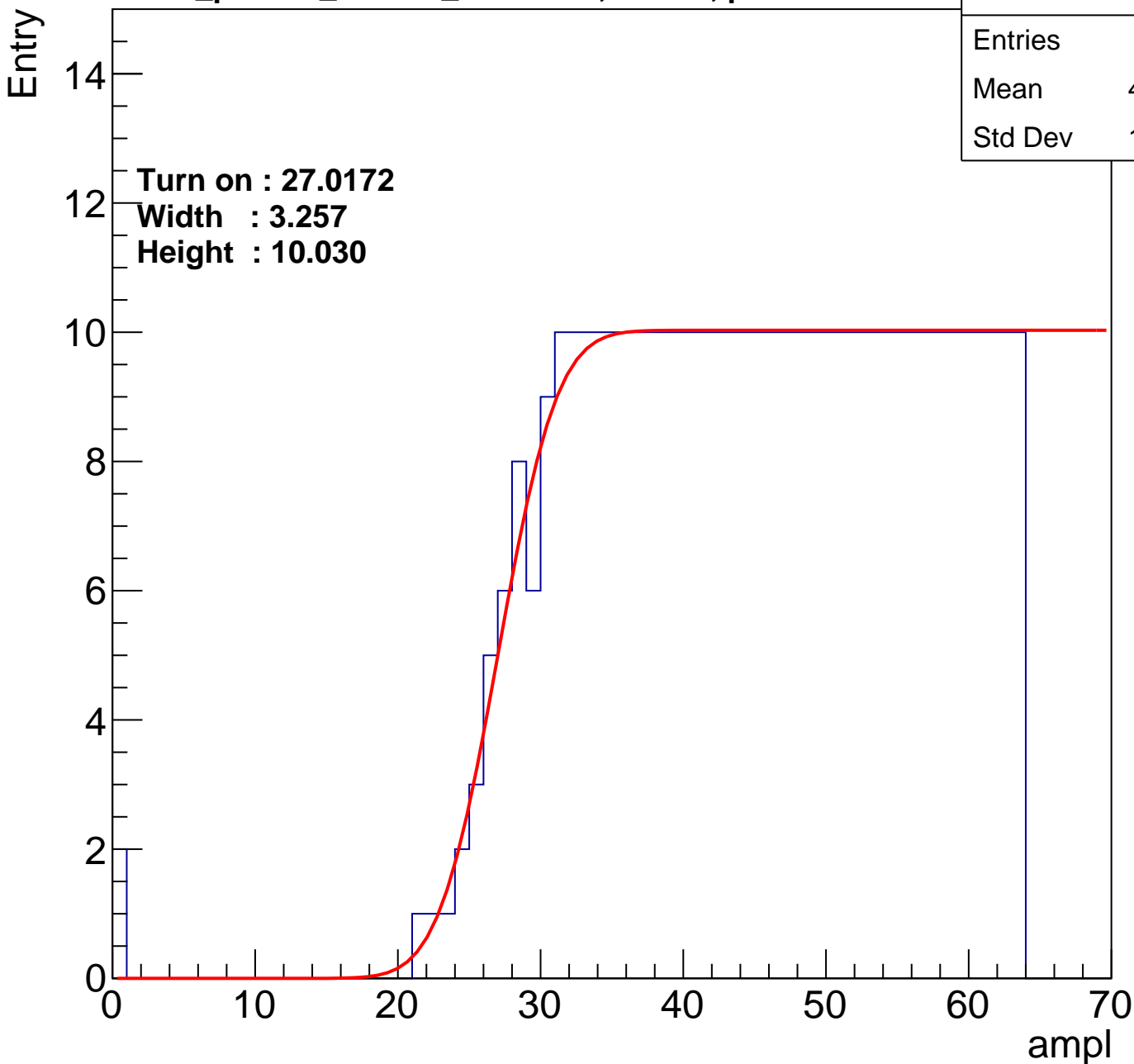
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 27.0172

Width : 3.257

Height : 10.030

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.54 |
| Std Dev | 11.39 |



B0L102S, U19-ch74

calib_packv5_042523_0143.root, FC#12, port B1

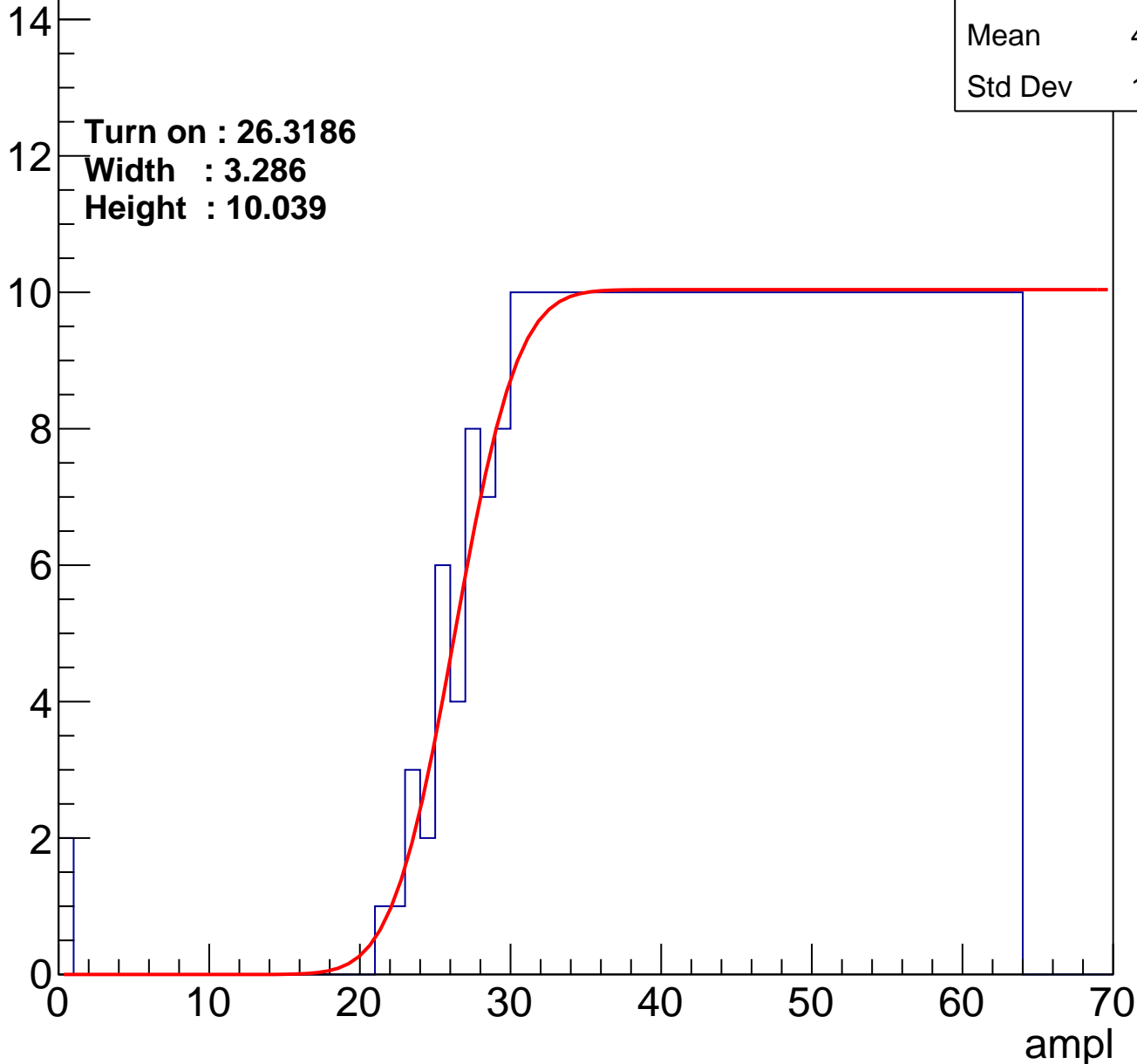
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.16 |
| Std Dev | 11.58 |

Turn on : 26.3186

Width : 3.286

Height : 10.039

Entry



B0L102S, U19-ch75

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.92 |
| Std Dev | 11.3 |

Turn on : 27.7165

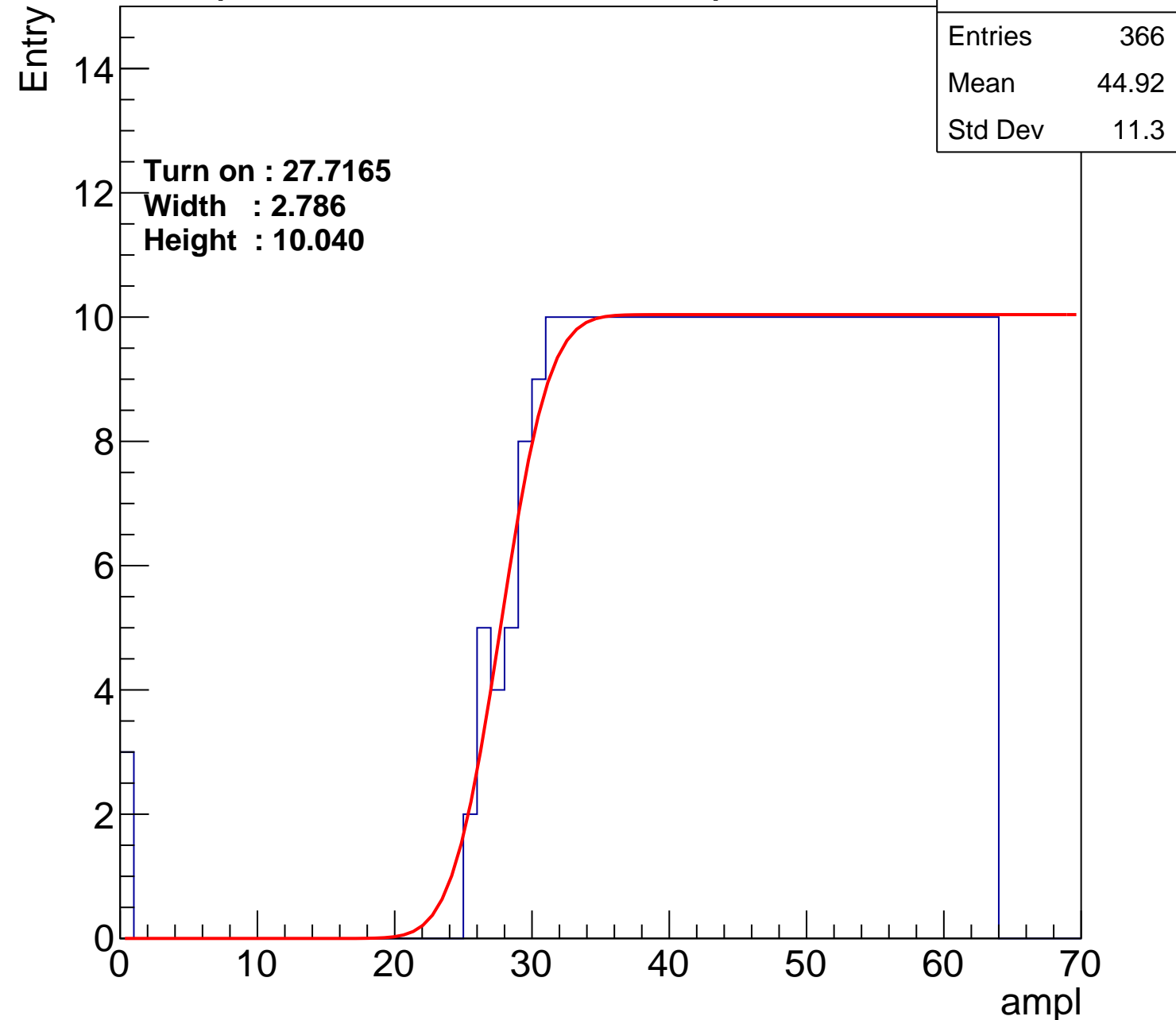
Width : 2.786

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch76

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.44 |
| Std Dev | 11.32 |

Turn on : 26.4407

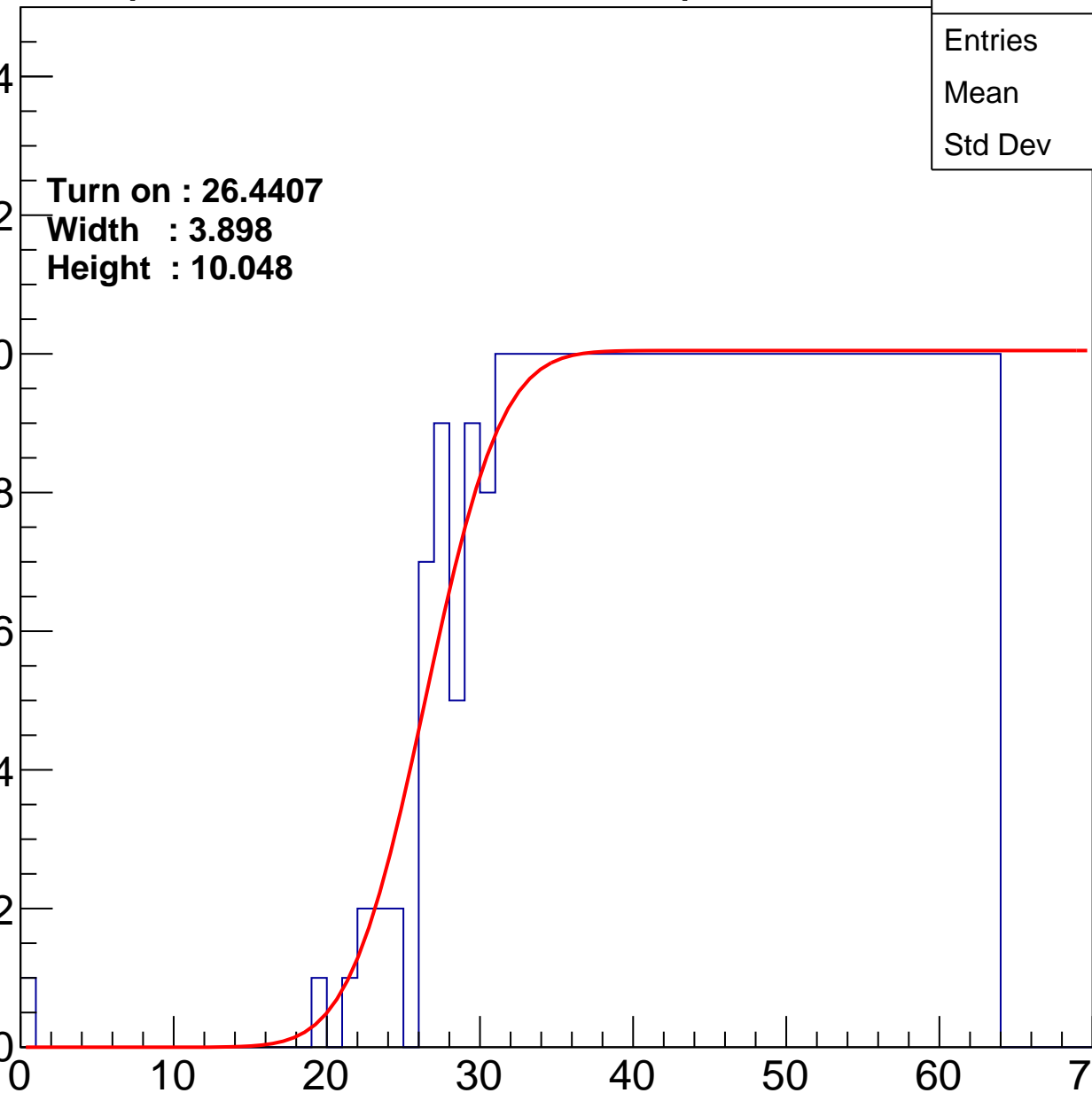
Width : 3.898

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch77

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.58 |
| Std Dev | 12.27 |

Turn on : 25.9061

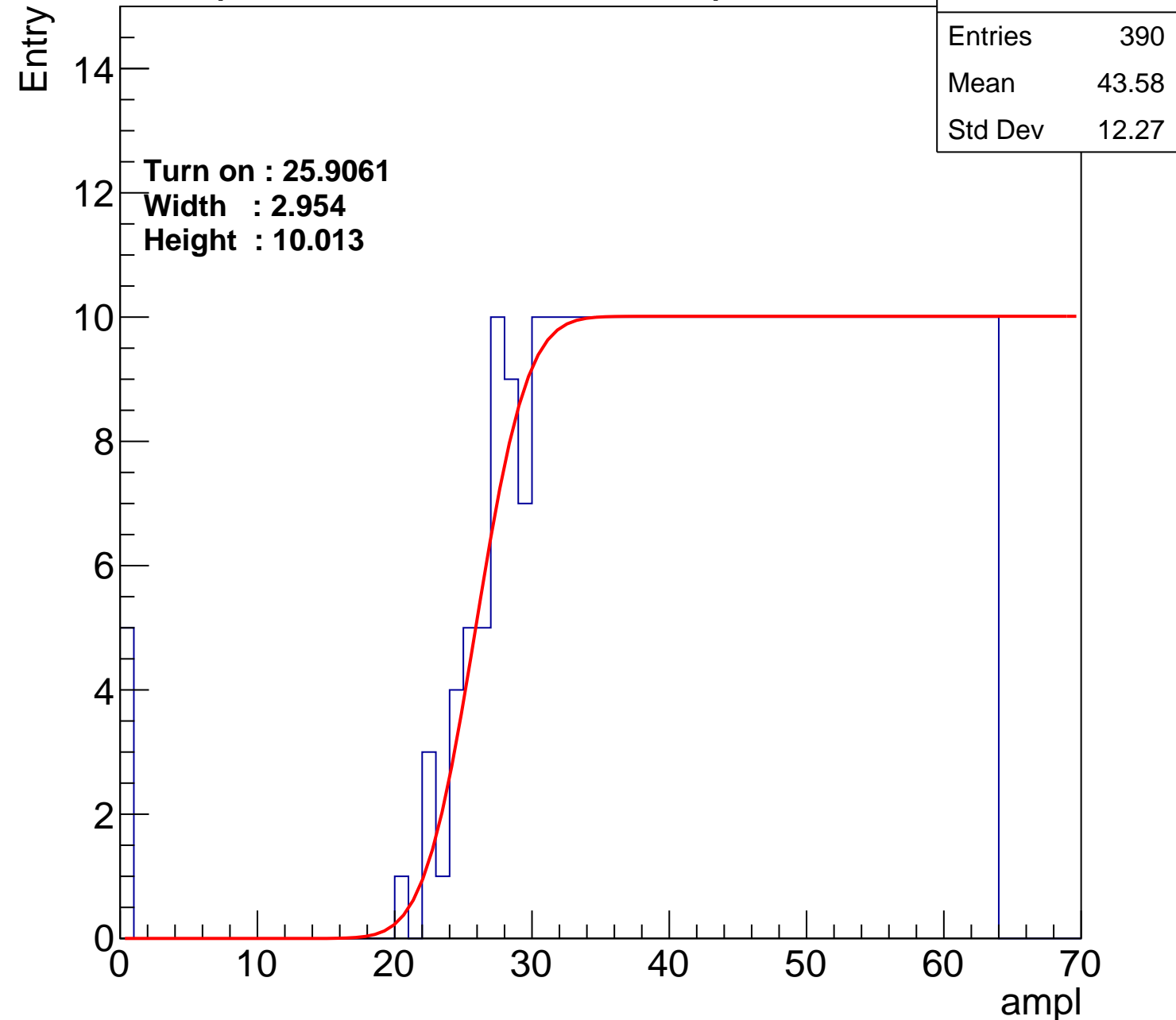
Width : 2.954

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch78

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.05 |
| Std Dev | 11.64 |

Turn on : 26.3982

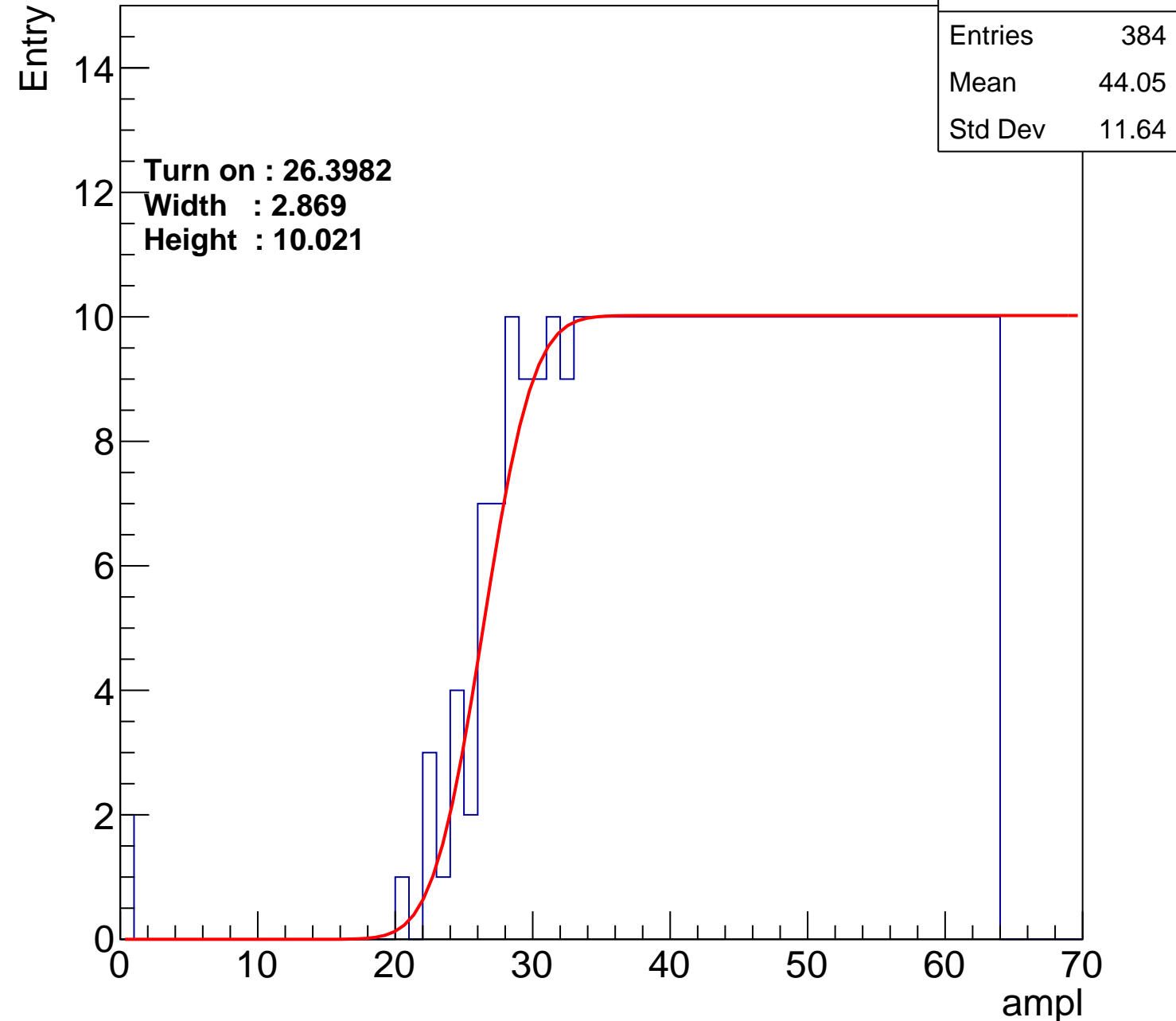
Width : 2.869

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch79

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.04 |
| Std Dev | 11.49 |

Turn on : 25.1606

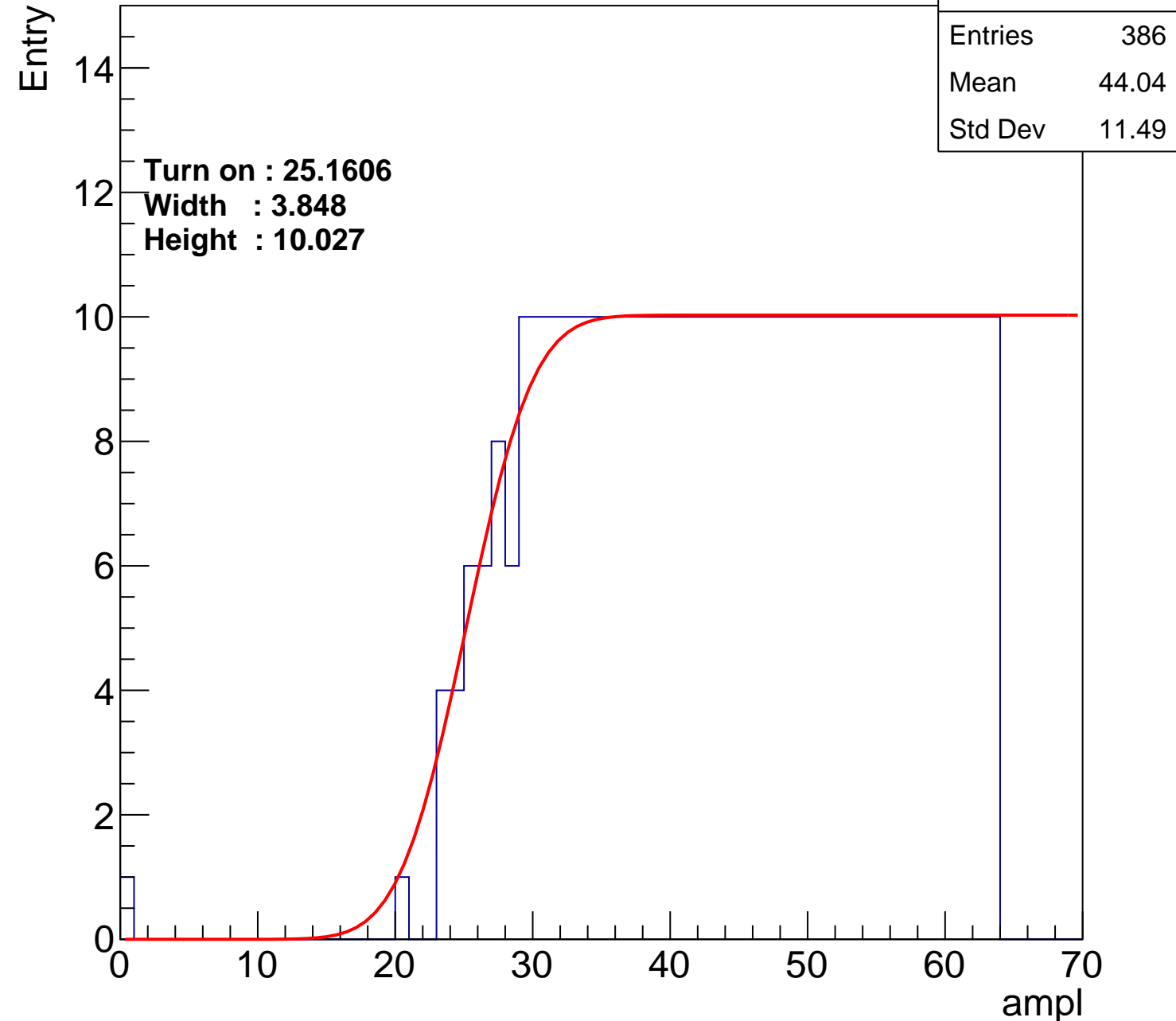
Width : 3.848

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch80

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 403 |
| Mean | 43.17 |
| Std Dev | 12.05 |

Turn on : 23.7973

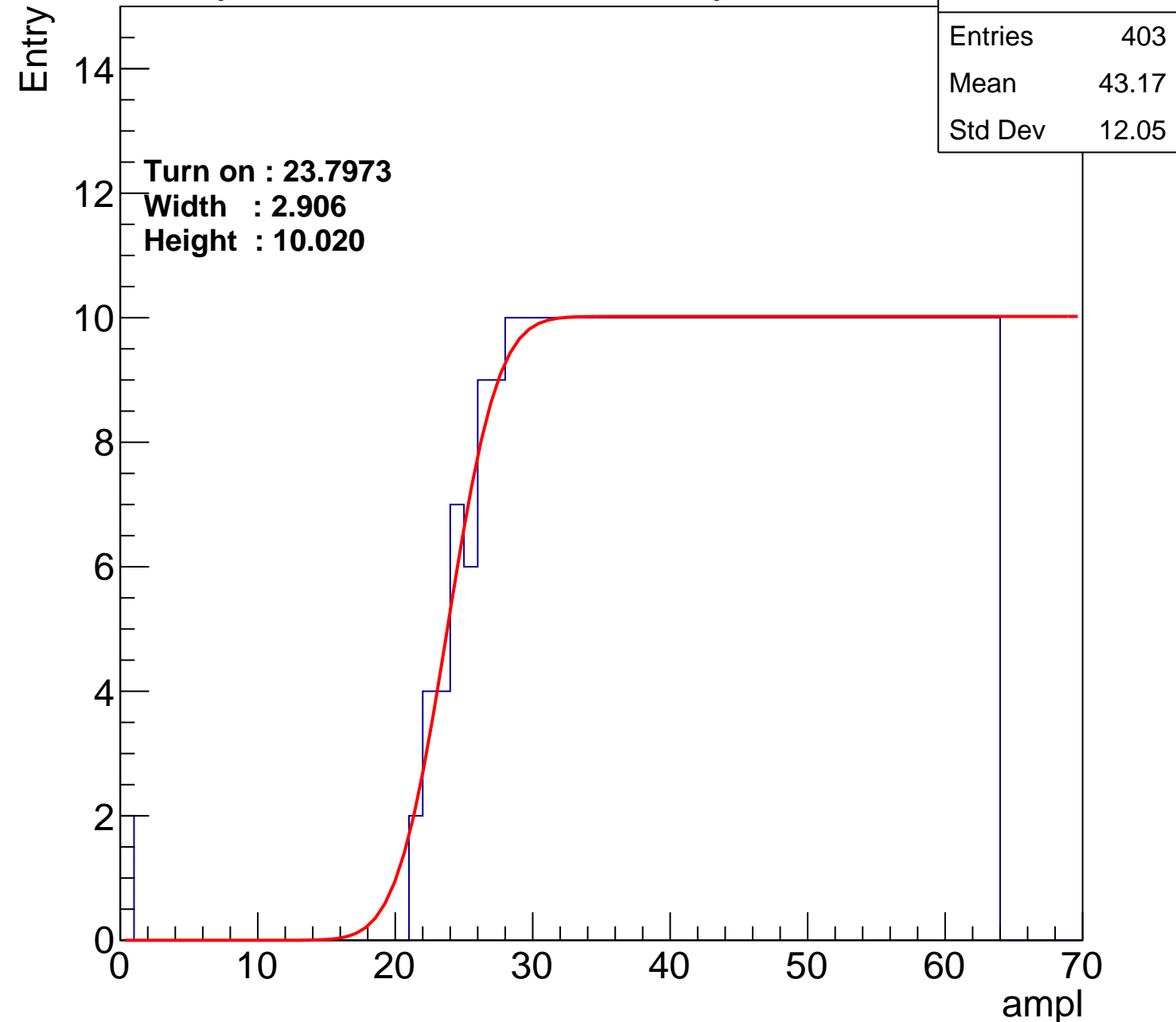
Width : 2.906

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch81

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.34 |
| Std Dev | 11.67 |

Turn on : 26.6993

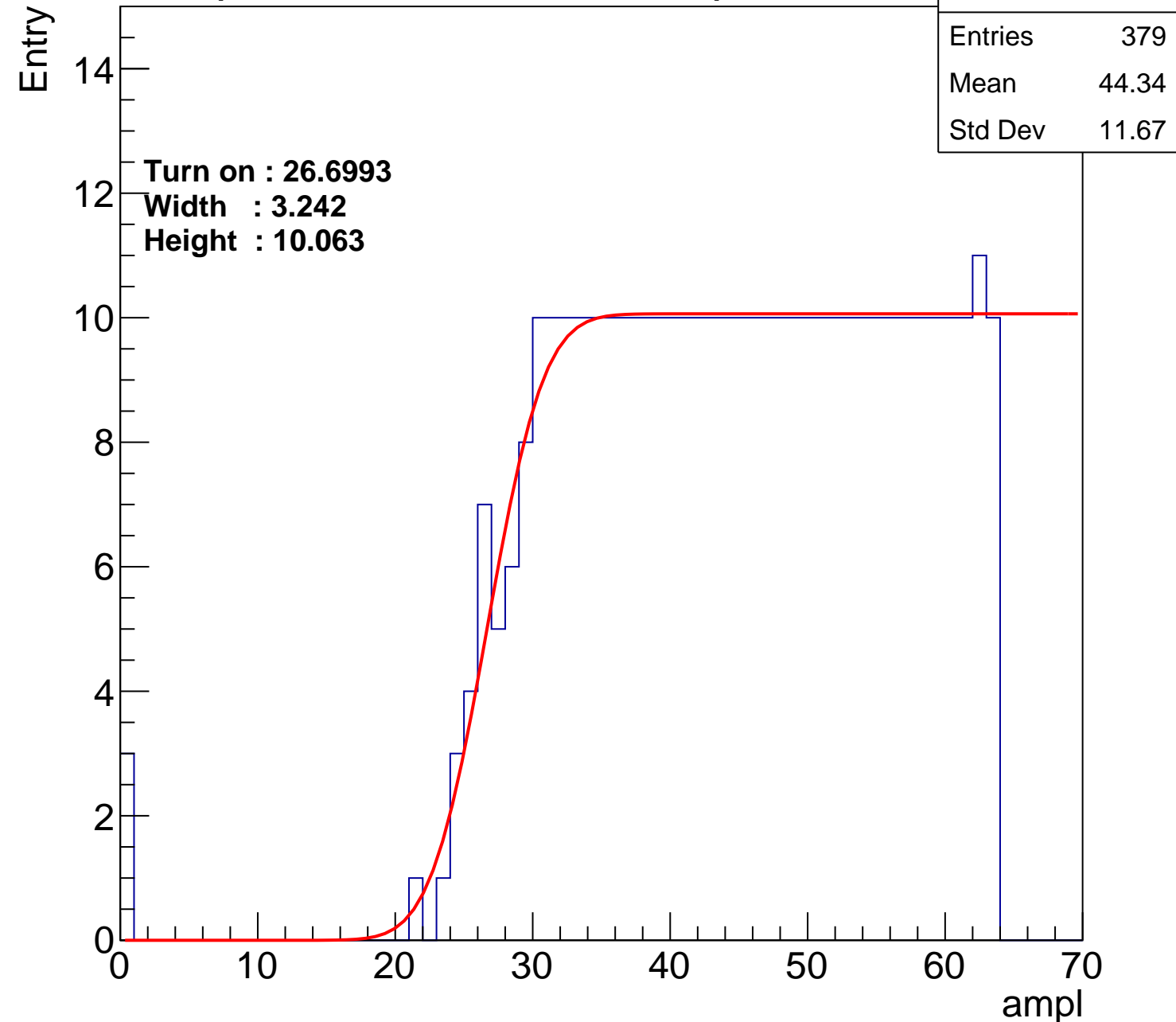
Width : 3.242

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch82

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.48 |
| Std Dev | 11.25 |

Turn on : 26.7132

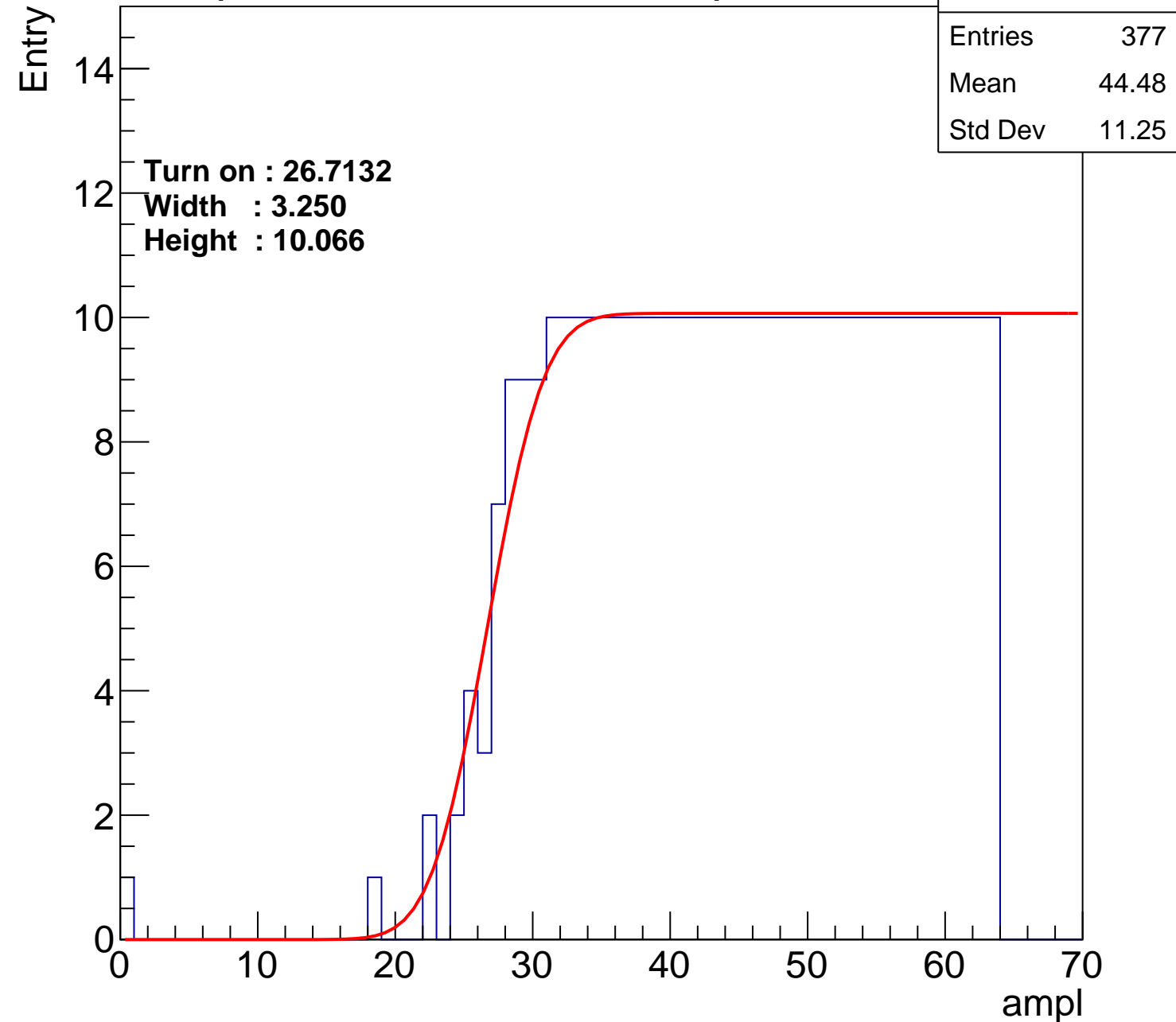
Width : 3.250

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch83

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.35 |
| Std Dev | 11.61 |

Turn on : 26.8538

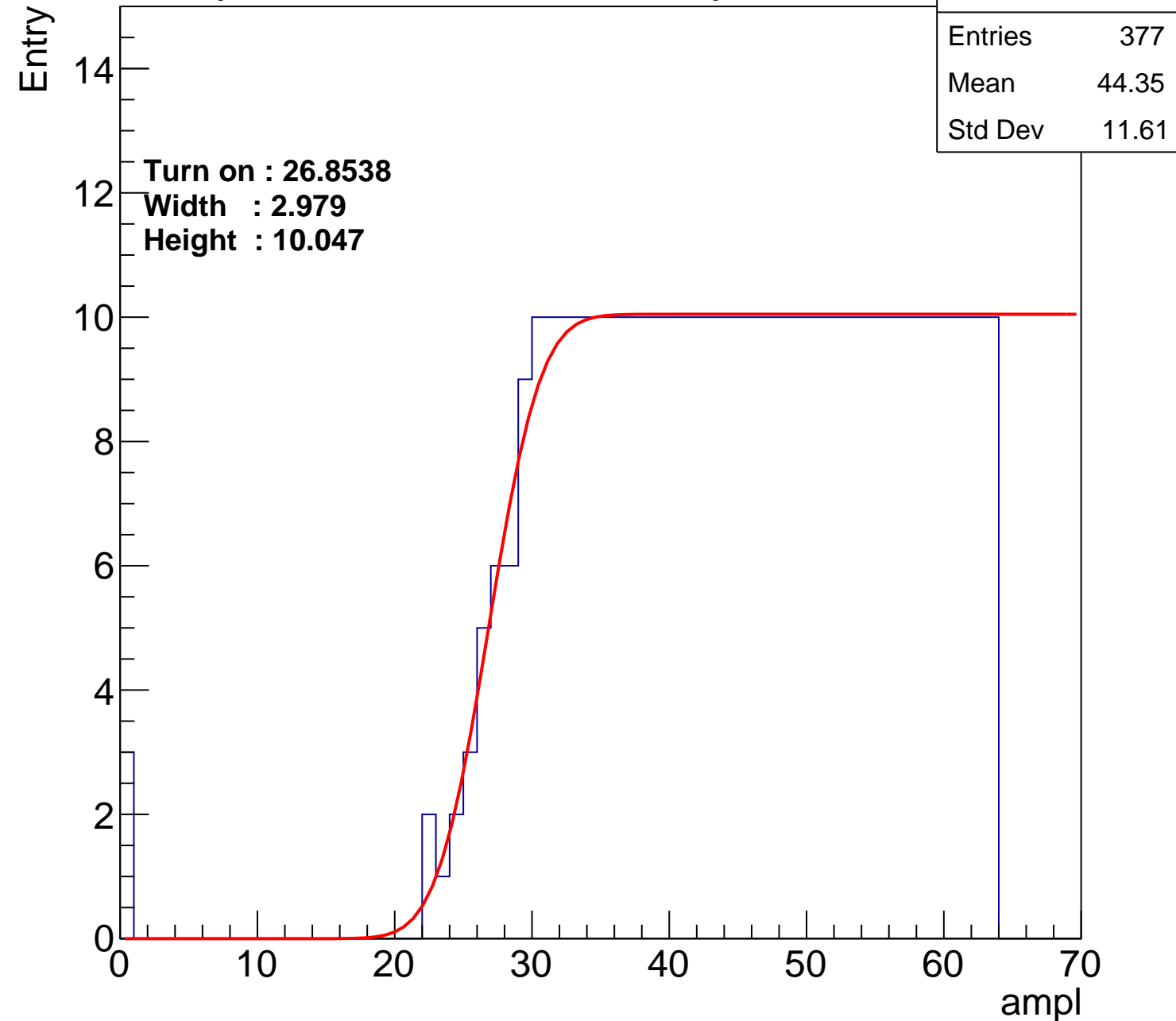
Width : 2.979

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch84

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.39 |
| Std Dev | 12.25 |

Turn on : 25.3265

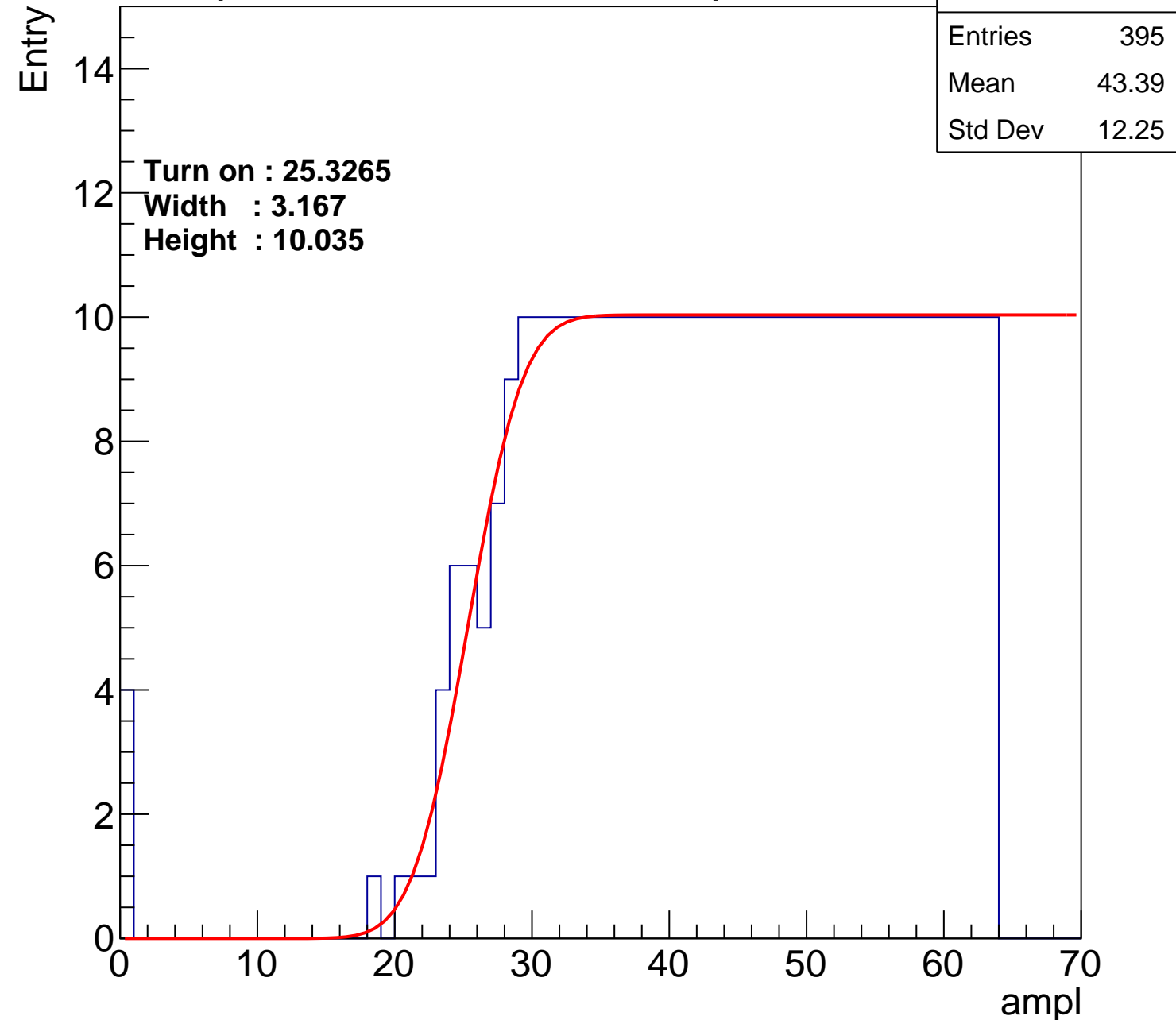
Width : 3.167

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch85

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.37 |
| Std Dev | 11.58 |

Turn on : 26.0940

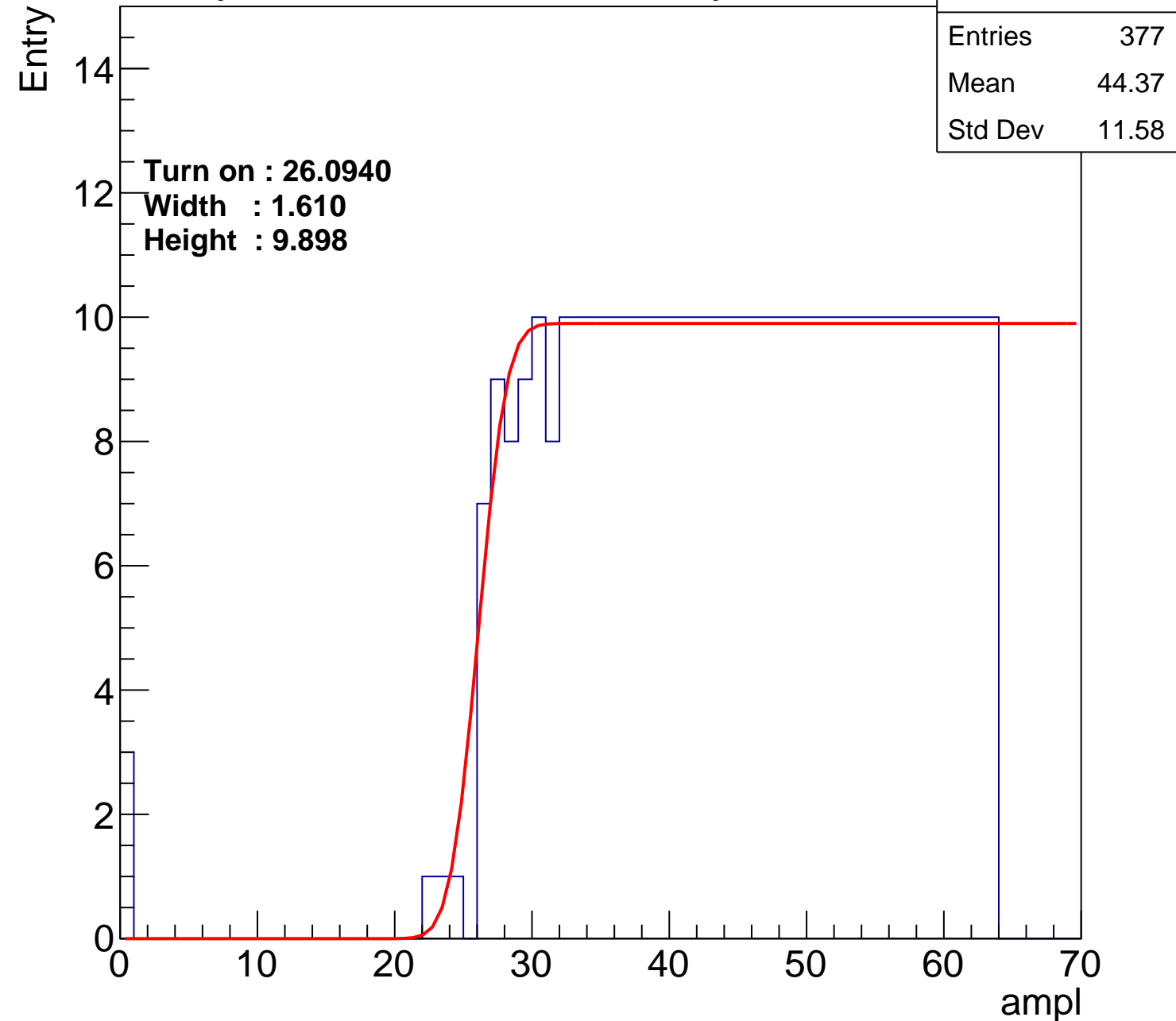
Width : 1.610

Height : 9.898

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch86

calib_packv5_042523_0143.root, FC#12, port B1

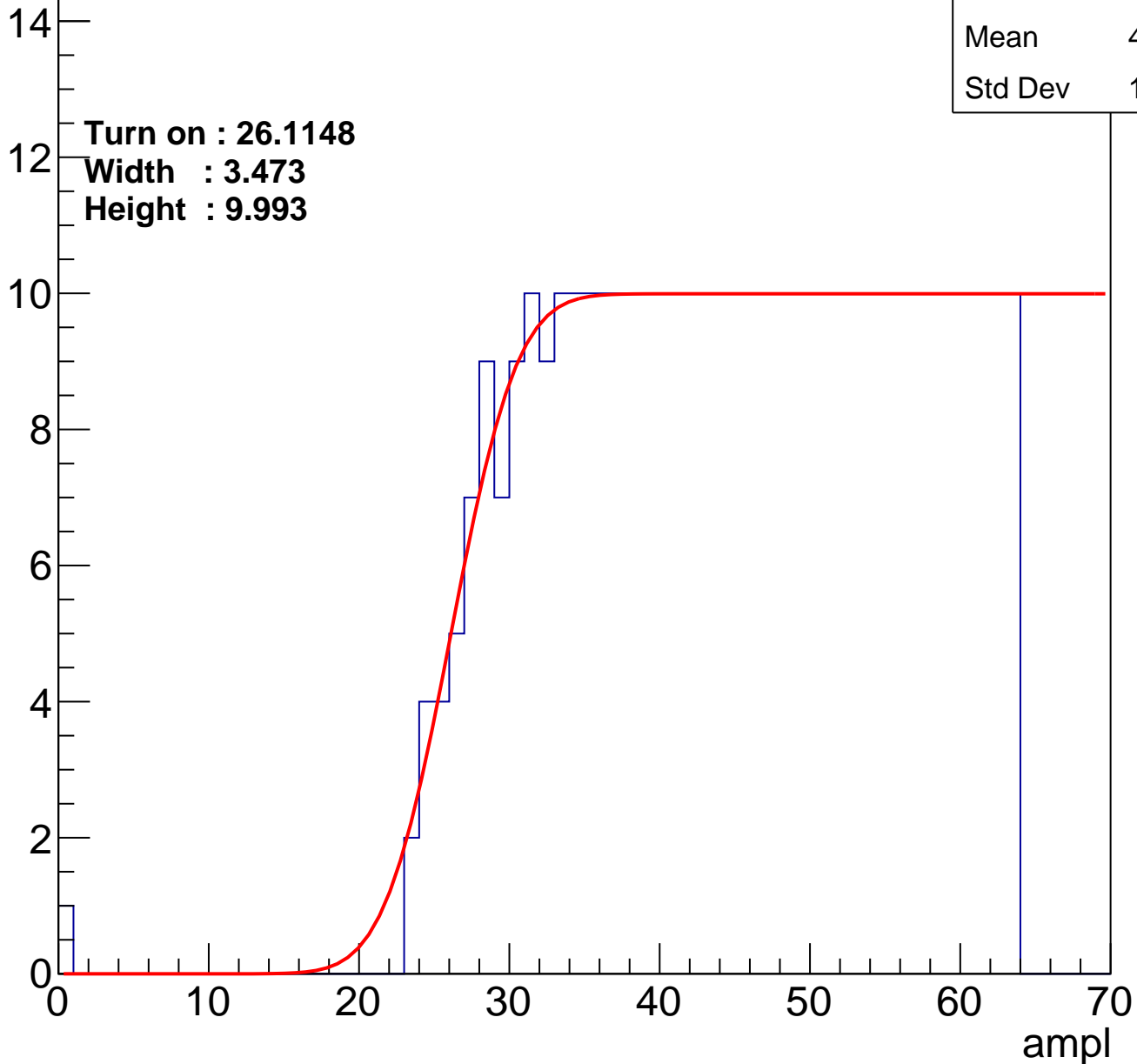
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.47 |
| Std Dev | 11.26 |

Turn on : 26.1148

Width : 3.473

Height : 9.993

Entry



B0L102S, U19-ch87

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.93 |
| Std Dev | 11.19 |

Turn on : 27.1836

Width : 3.130

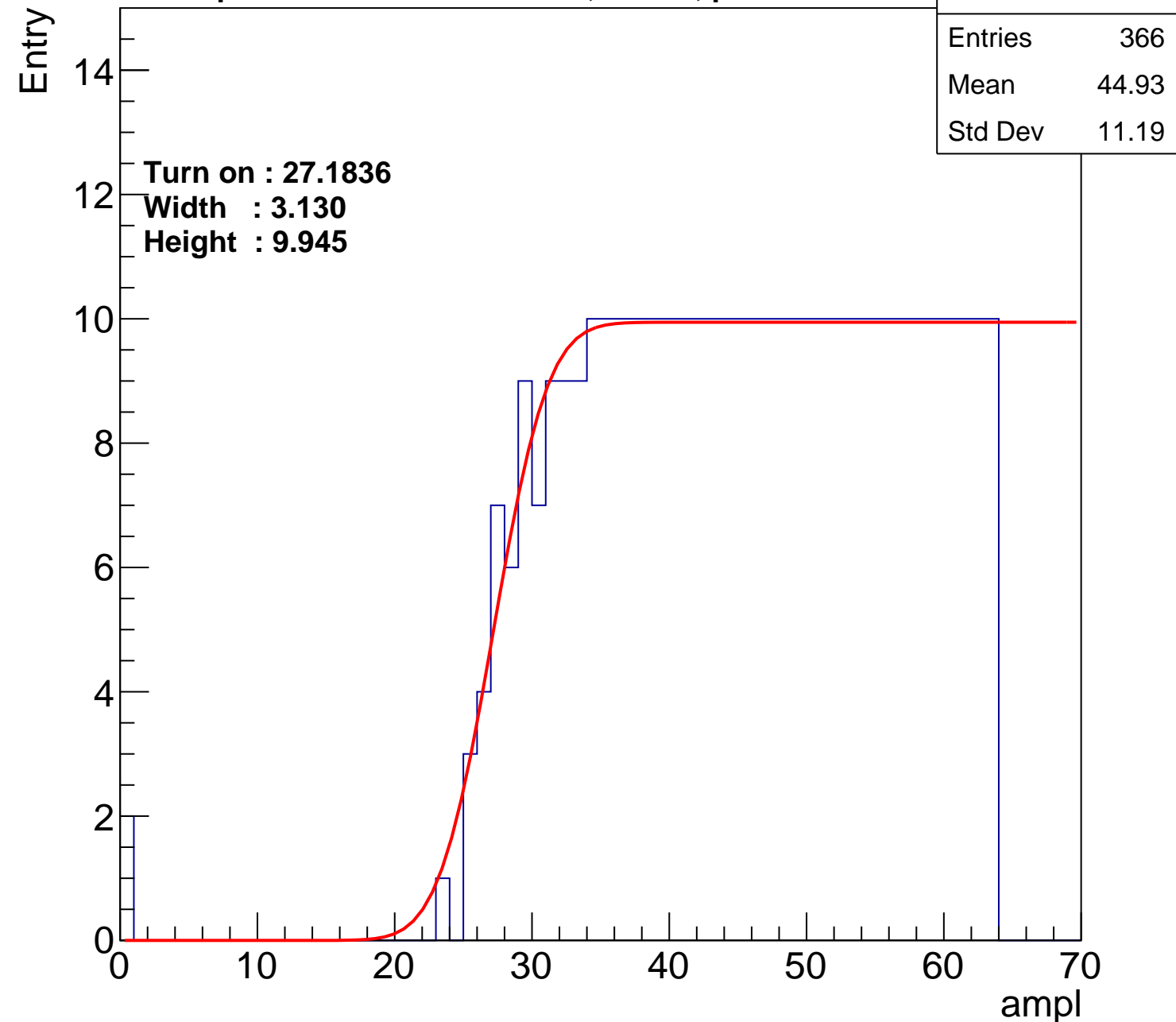
Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U19-ch88

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.44 |
| Std Dev | 11.57 |

Turn on : 26.4743

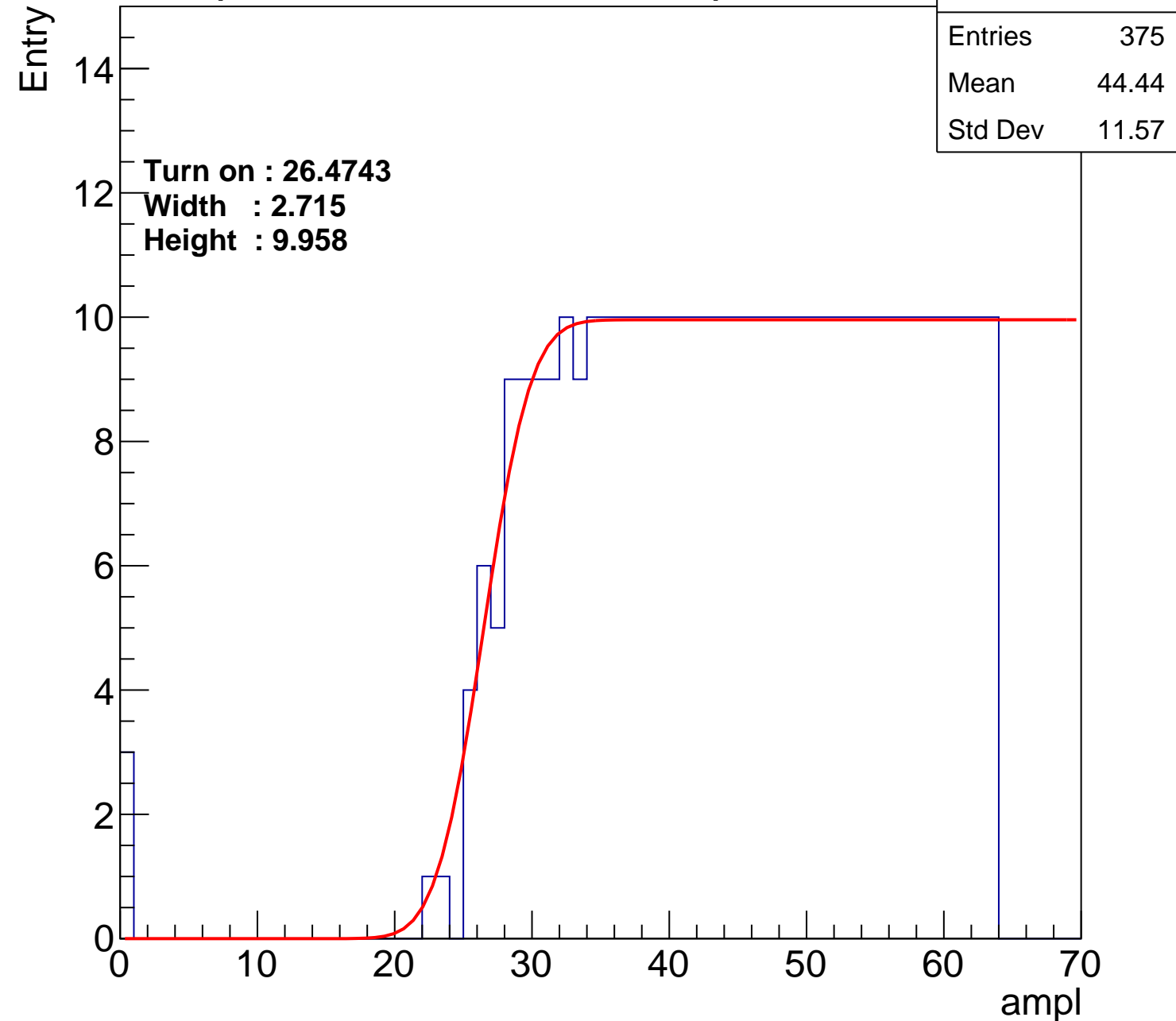
Width : 2.715

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch89

calib_packv5_042523_0143.root, FC#12, port B1

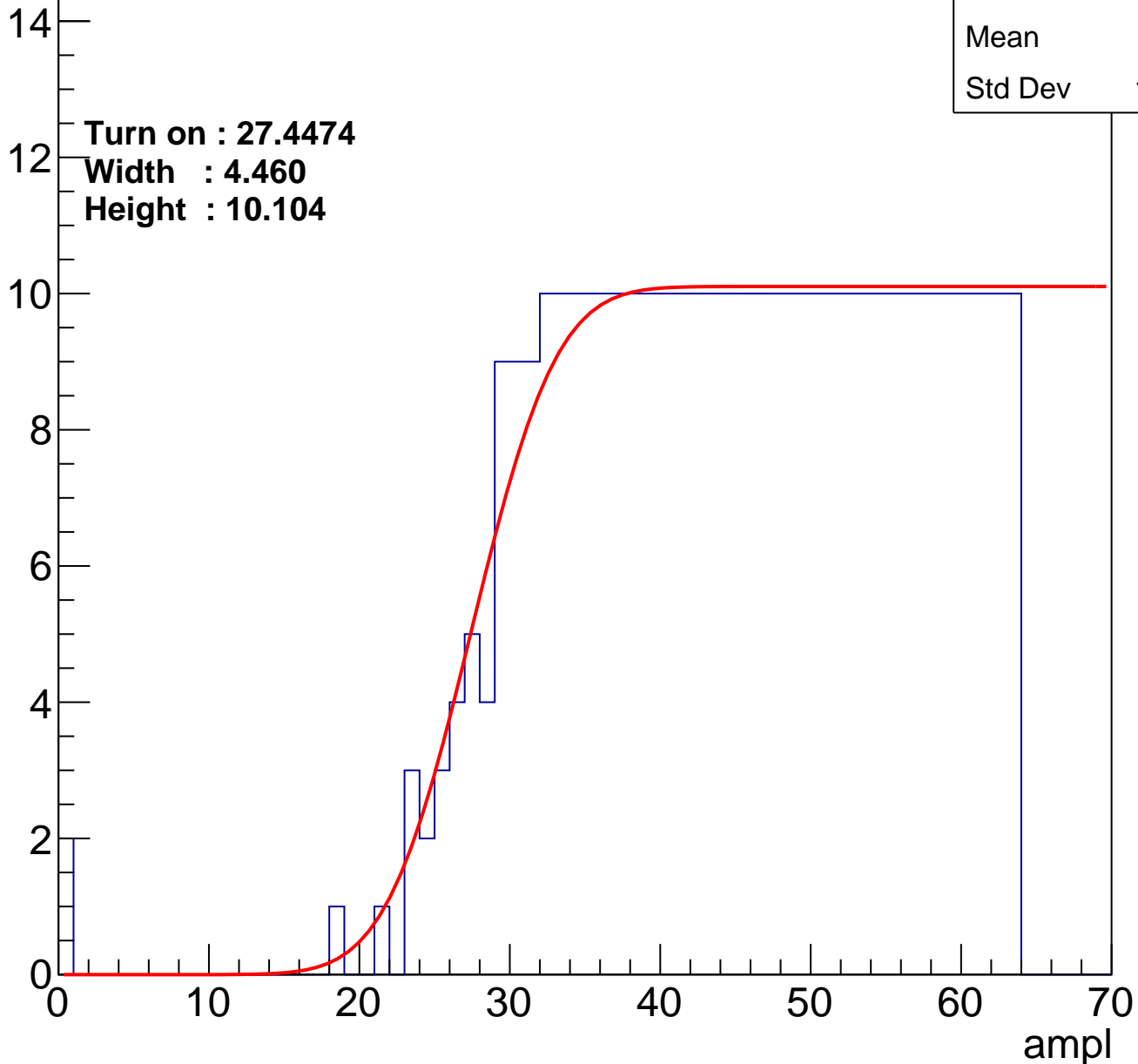
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.6 |
| Std Dev | 11.41 |

Turn on : 27.4474

Width : 4.460

Height : 10.104

Entry



B0L102S, U19-ch90

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.17 |
| Std Dev | 11.86 |

Turn on : 26.5074

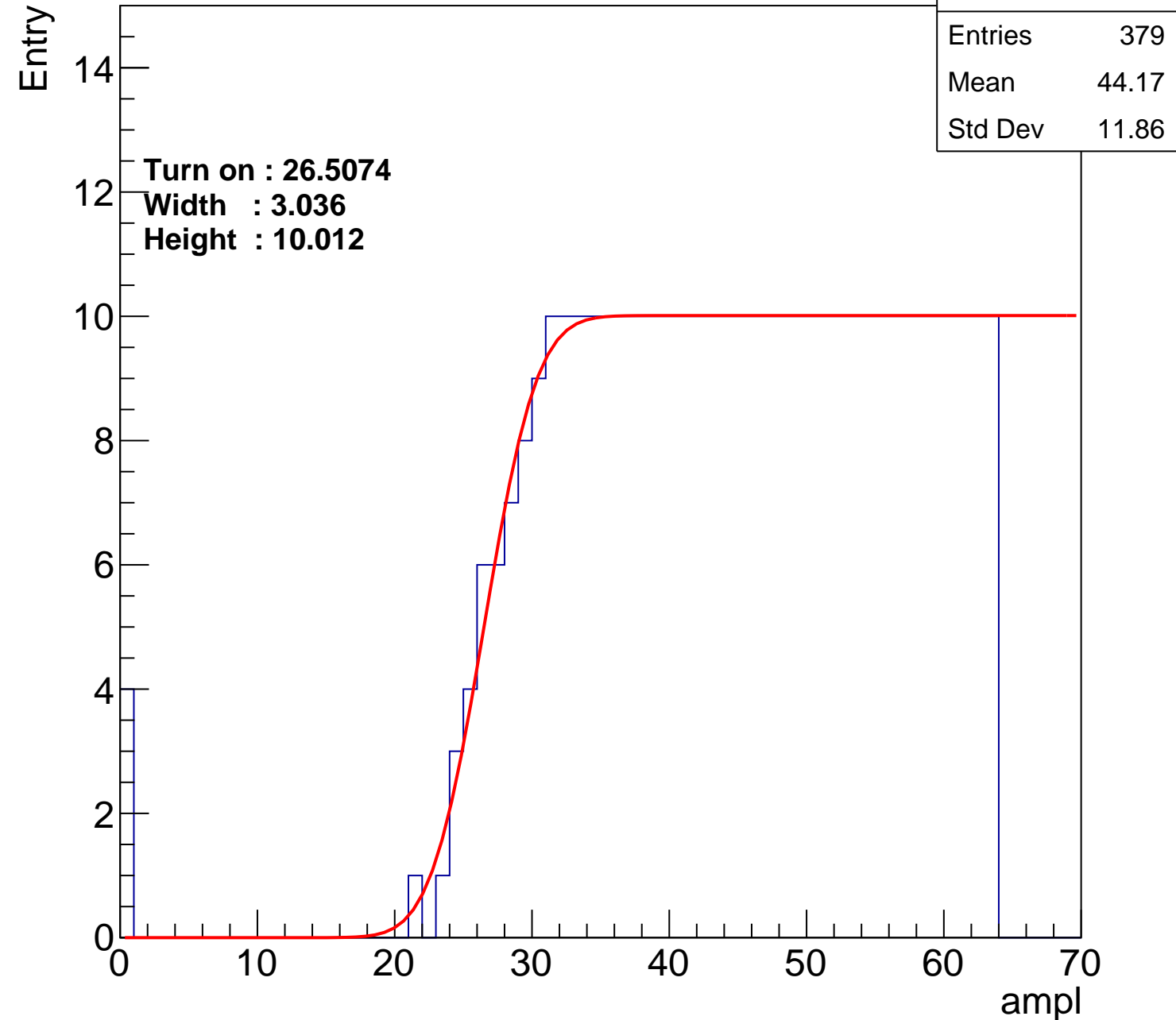
Width : 3.036

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch91

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.95 |
| Std Dev | 10.95 |

Turn on : 27.5572

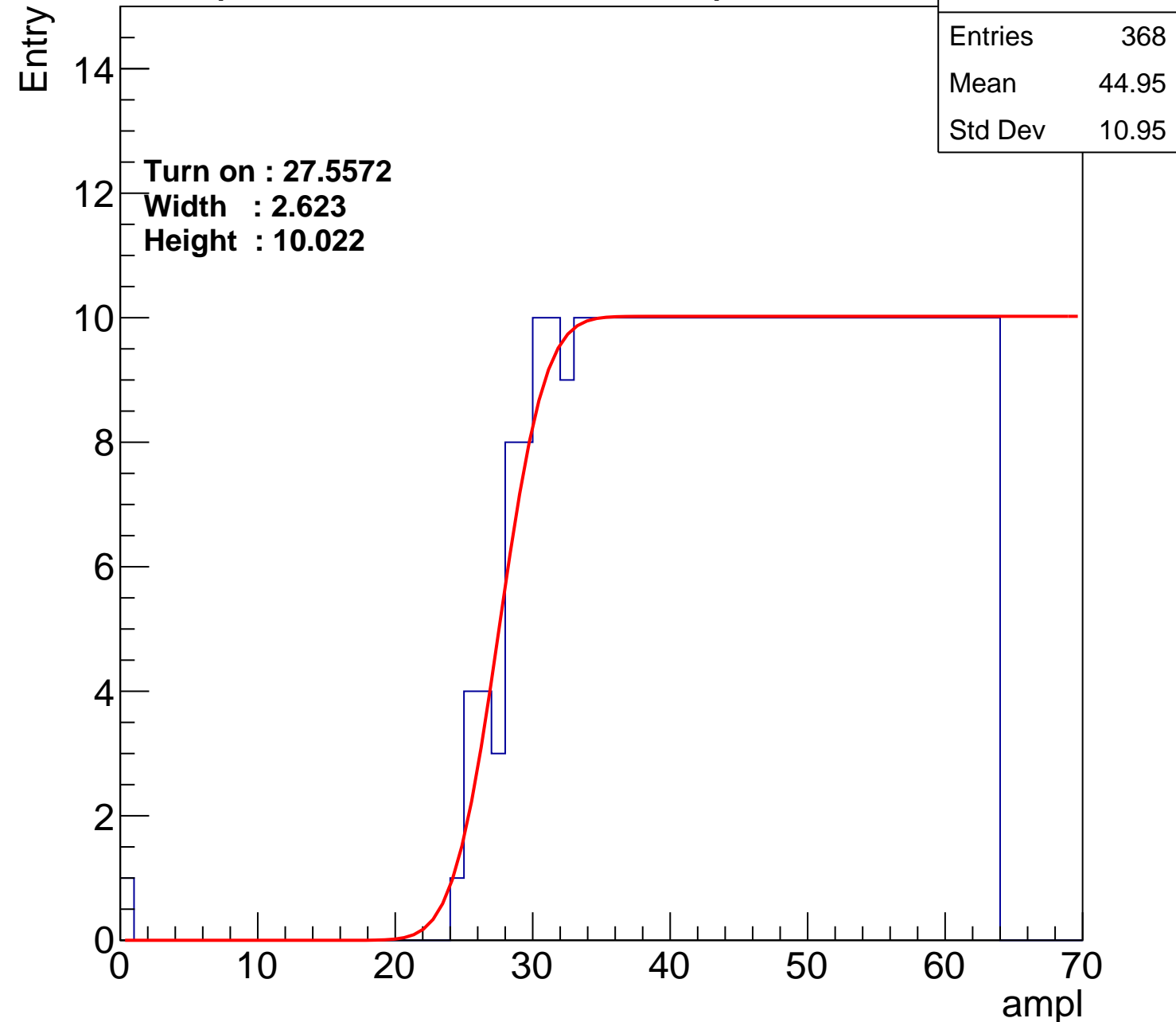
Width : 2.623

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch92

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.23 |
| Std Dev | 11.37 |

Turn on : 26.6276

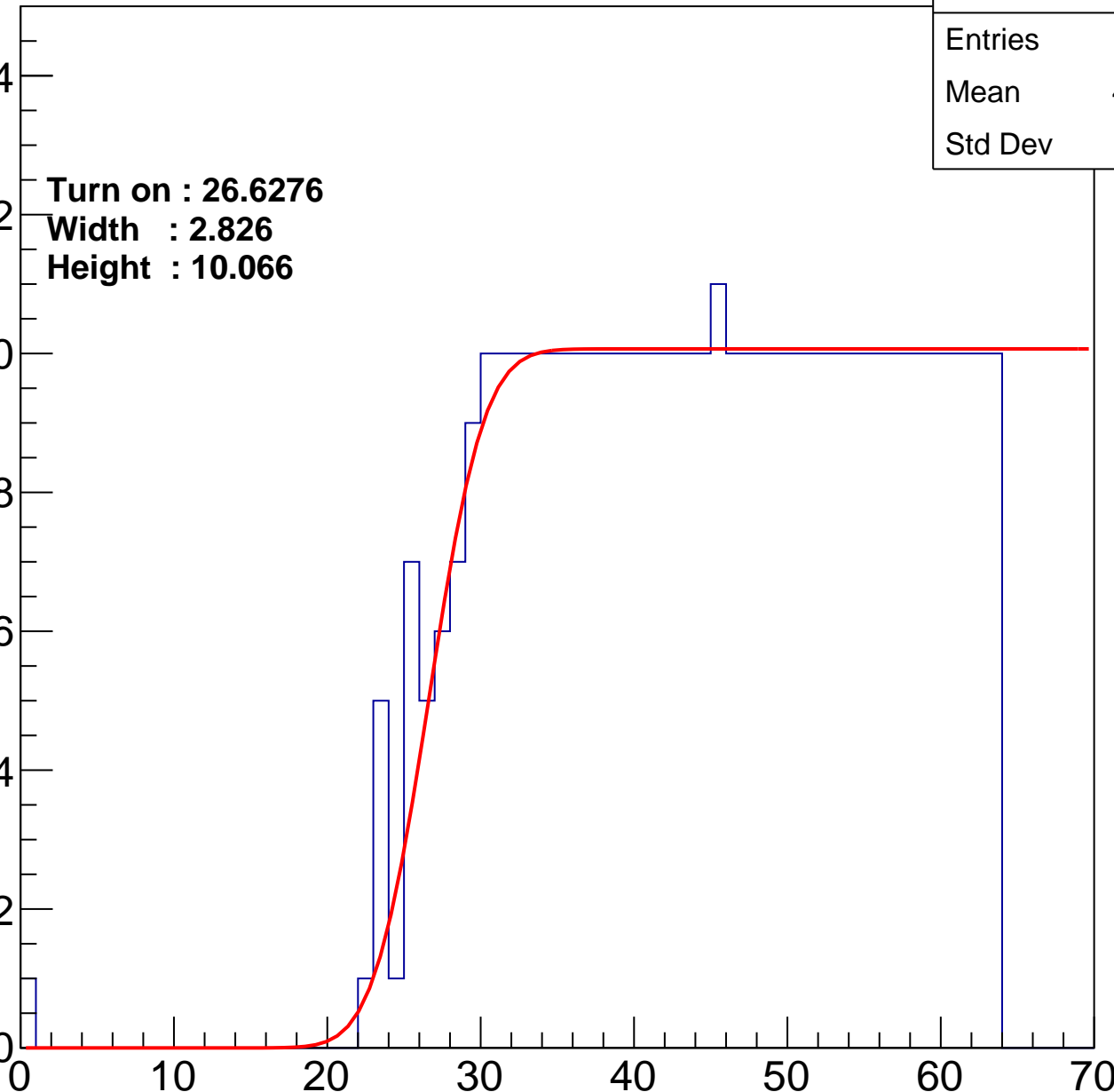
Width : 2.826

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch93

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.9 |
| Std Dev | 11.05 |

Turn on : 27.6860

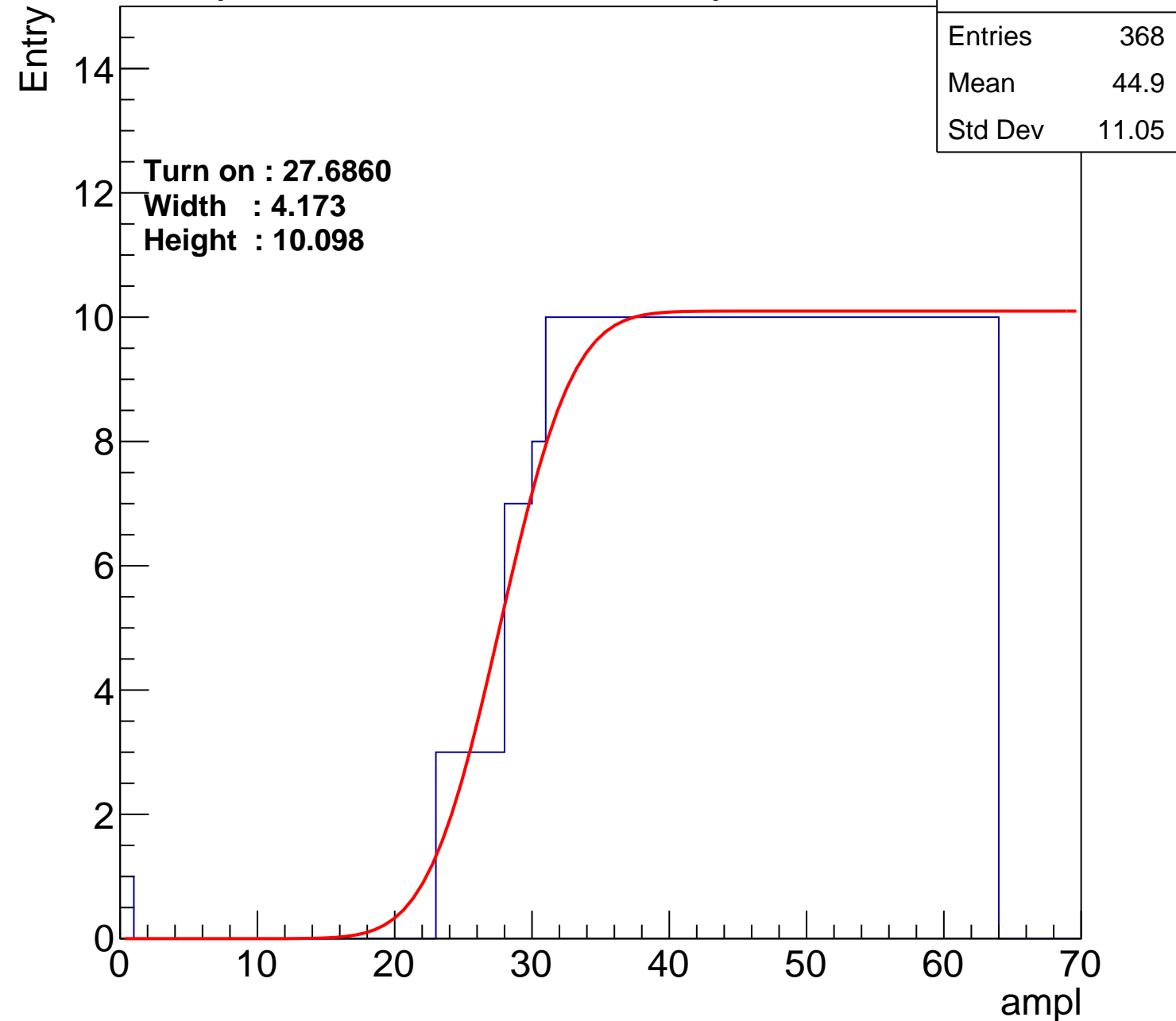
Width : 4.173

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch94

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.98 |
| Std Dev | 11.83 |

Turn on : 26.1395

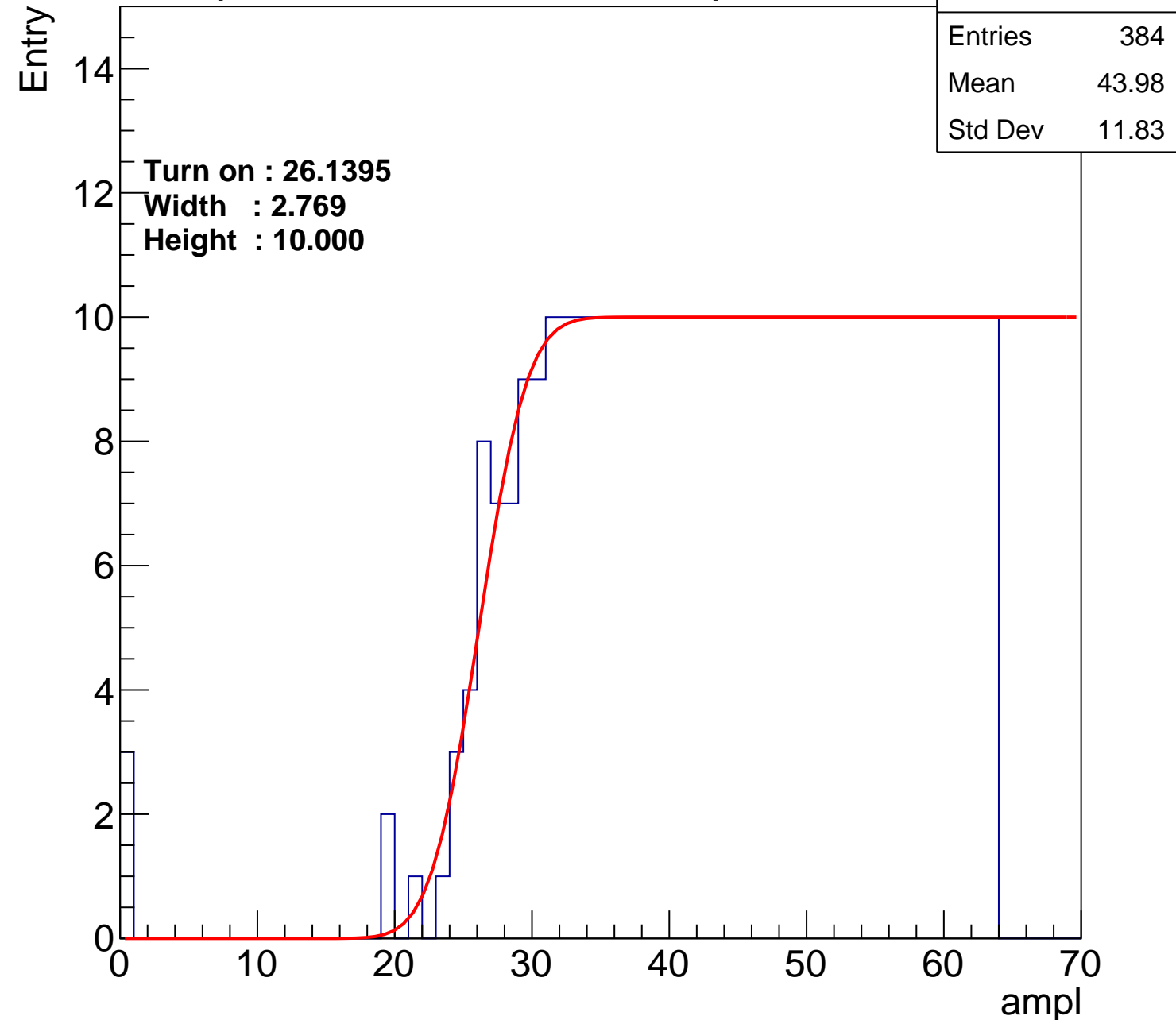
Width : 2.769

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch95

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.68 |
| Std Dev | 12.22 |

Turn on : 26.1505

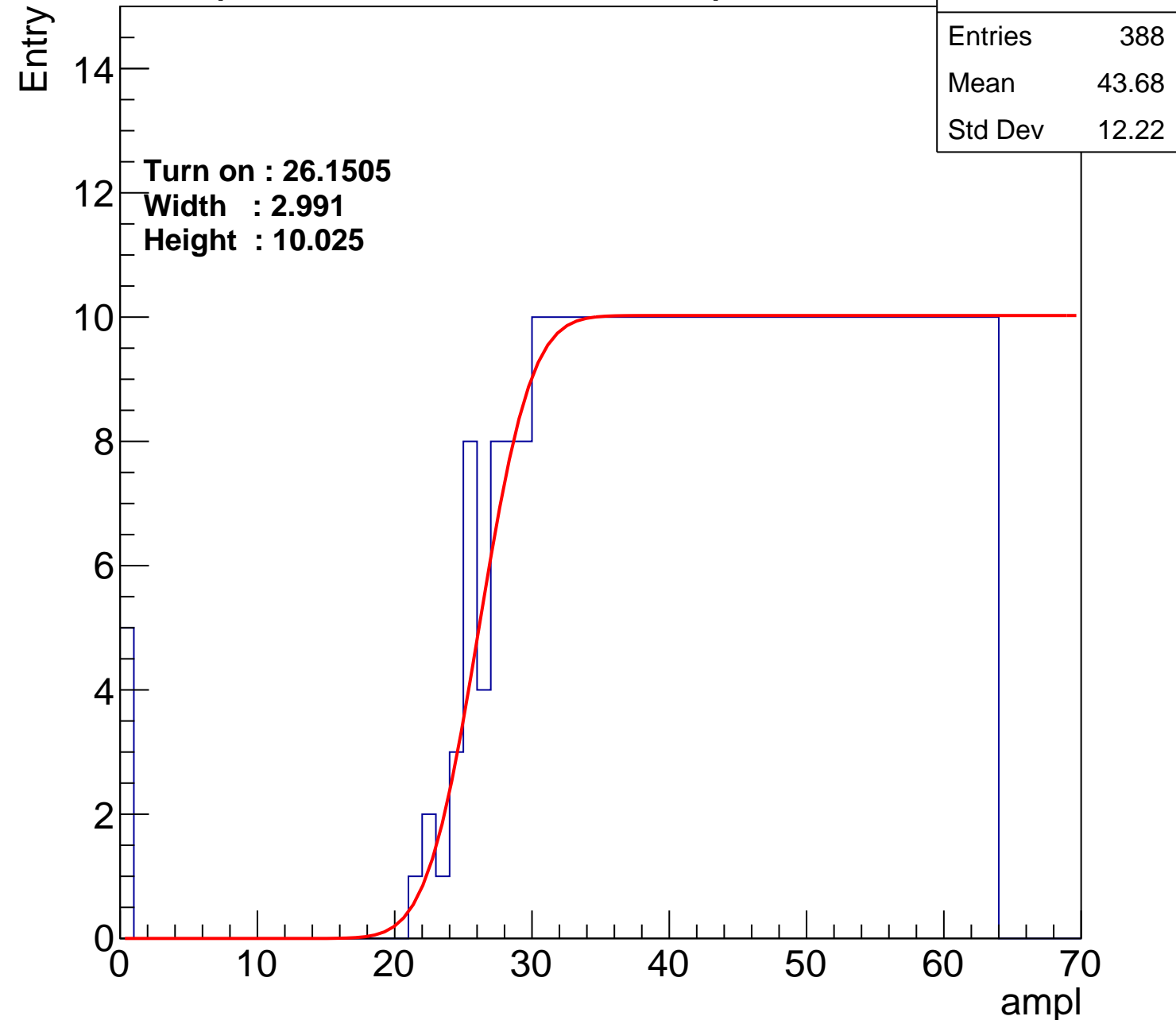
Width : 2.991

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch96

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.59 |
| Std Dev | 12.02 |

Turn on : 25.4690

Width : 2.617

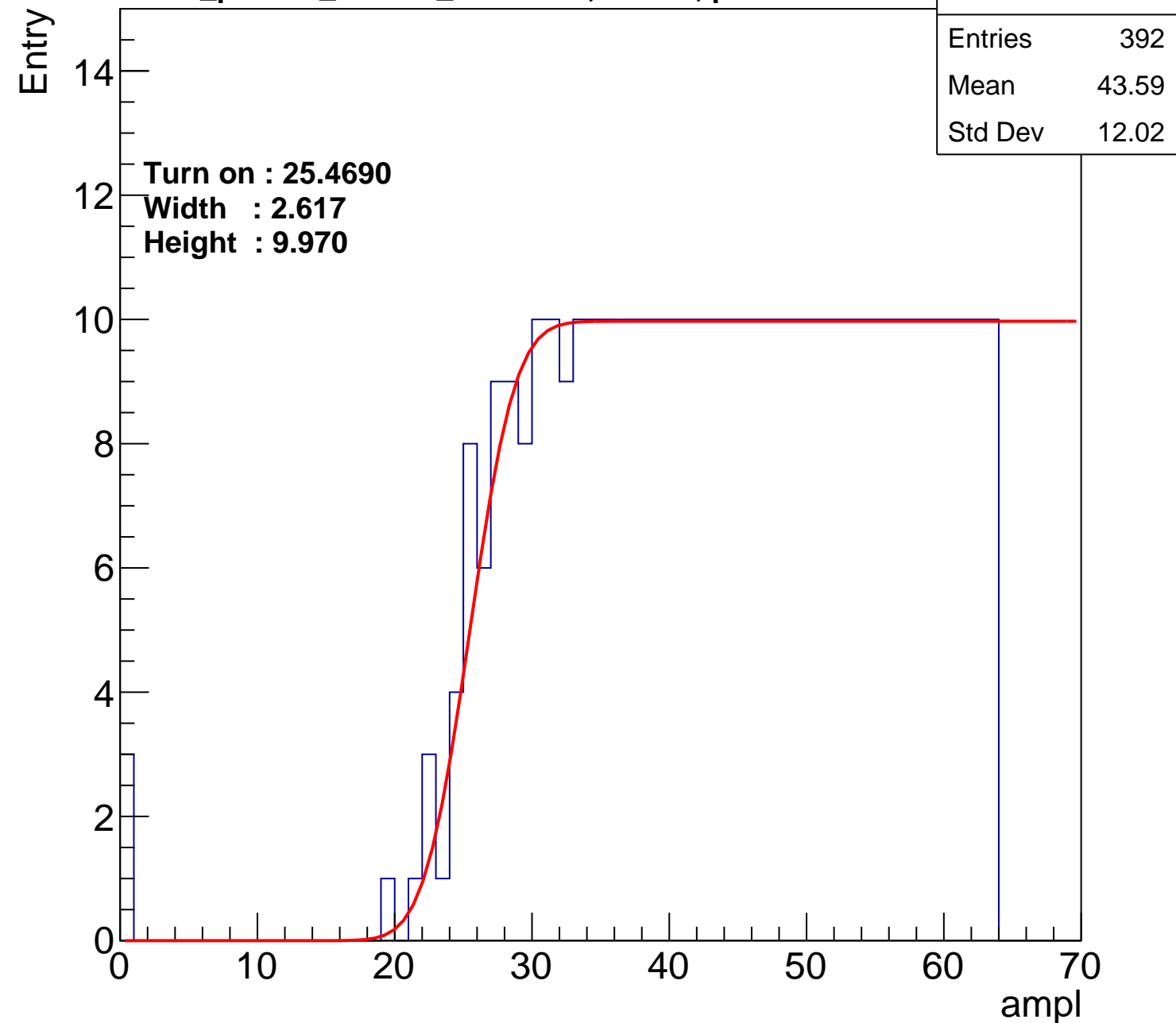
Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U19-ch97

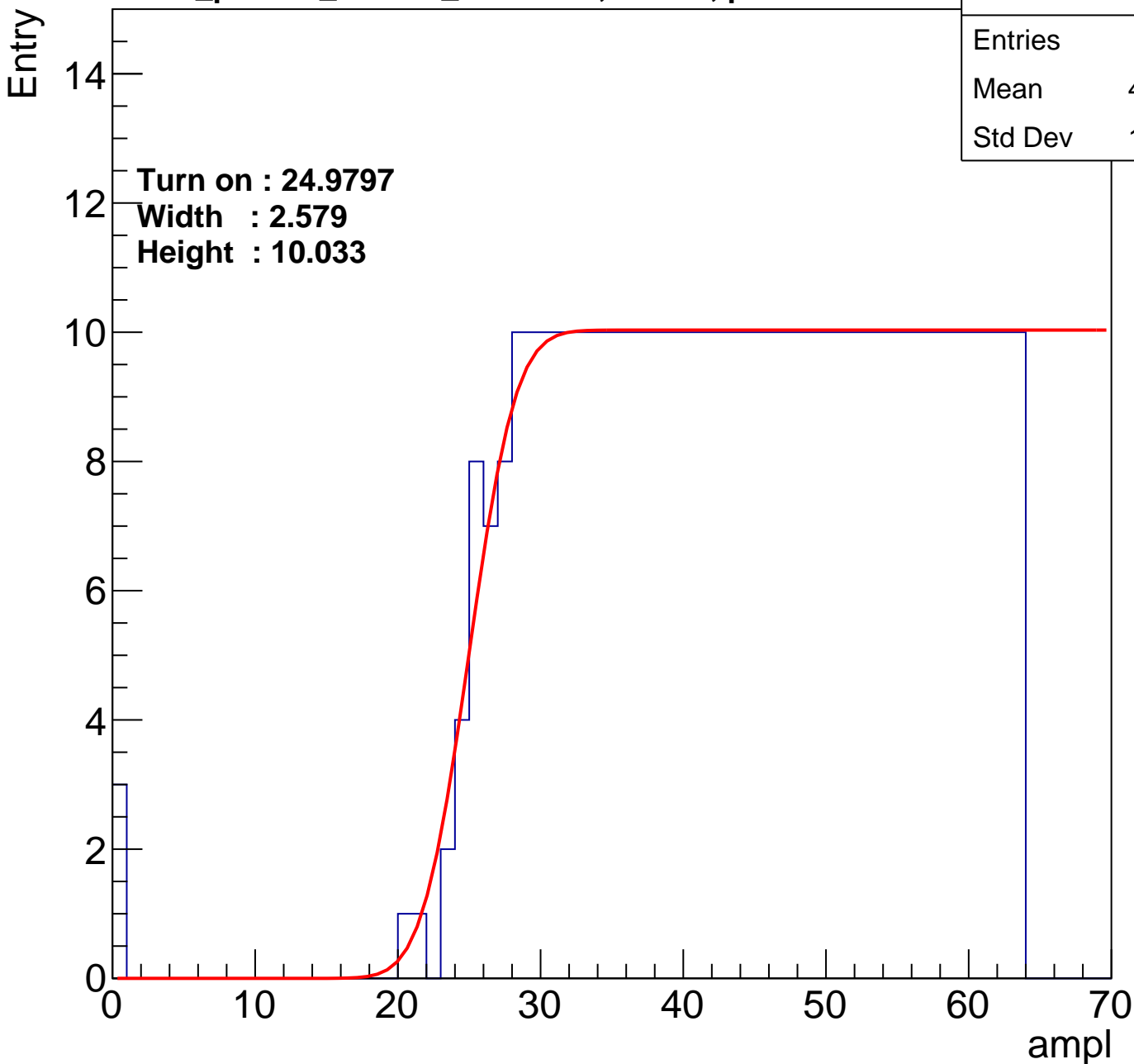
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 24.9797

Width : 2.579

Height : 10.033

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.56 |
| Std Dev | 11.97 |



B0L102S, U19-ch98

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 44.96 |
| Std Dev | 11.53 |

Turn on : 28.3342

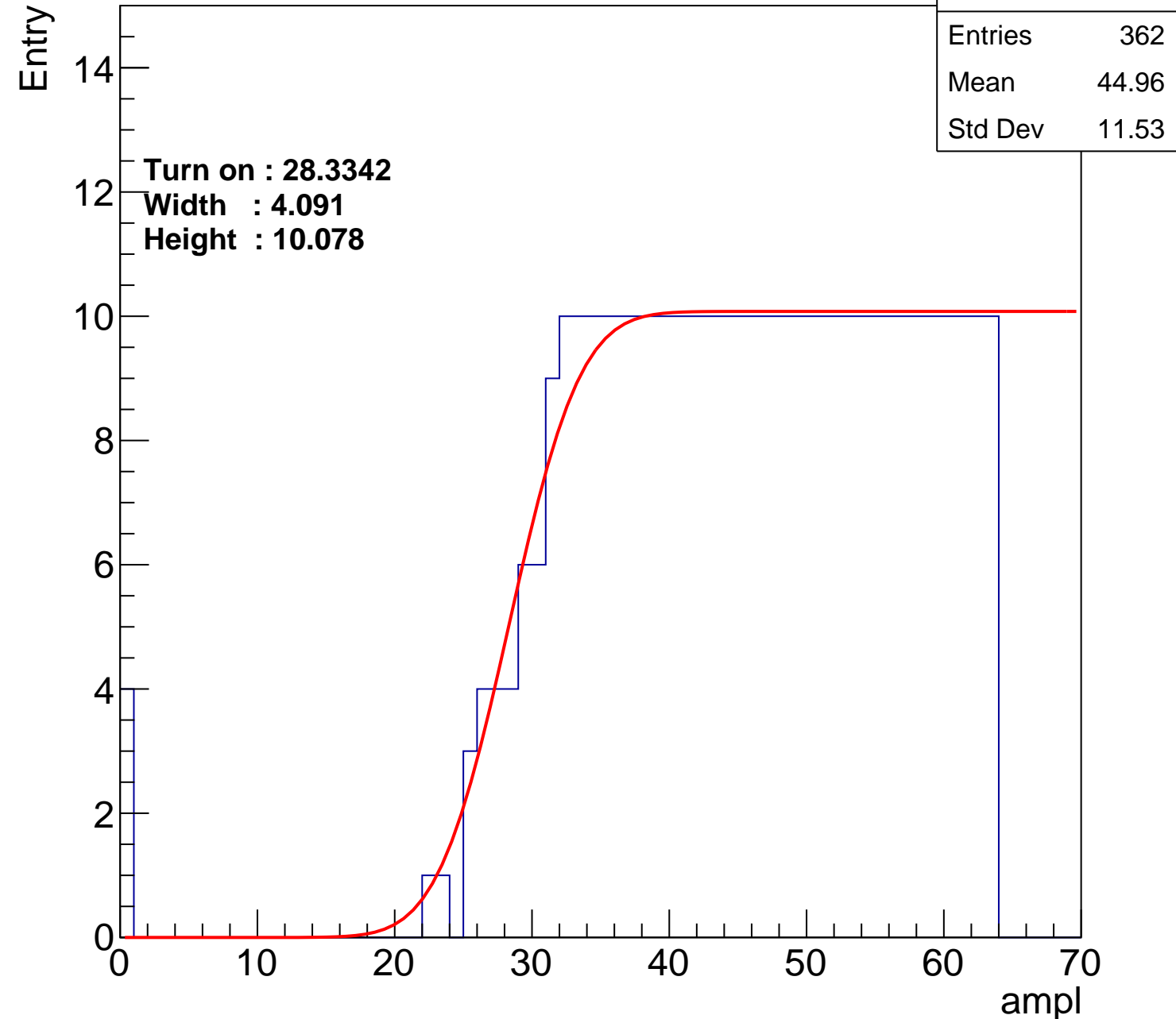
Width : 4.091

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch99

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.63 |
| Std Dev | 11.3 |

Turn on : 26.8853

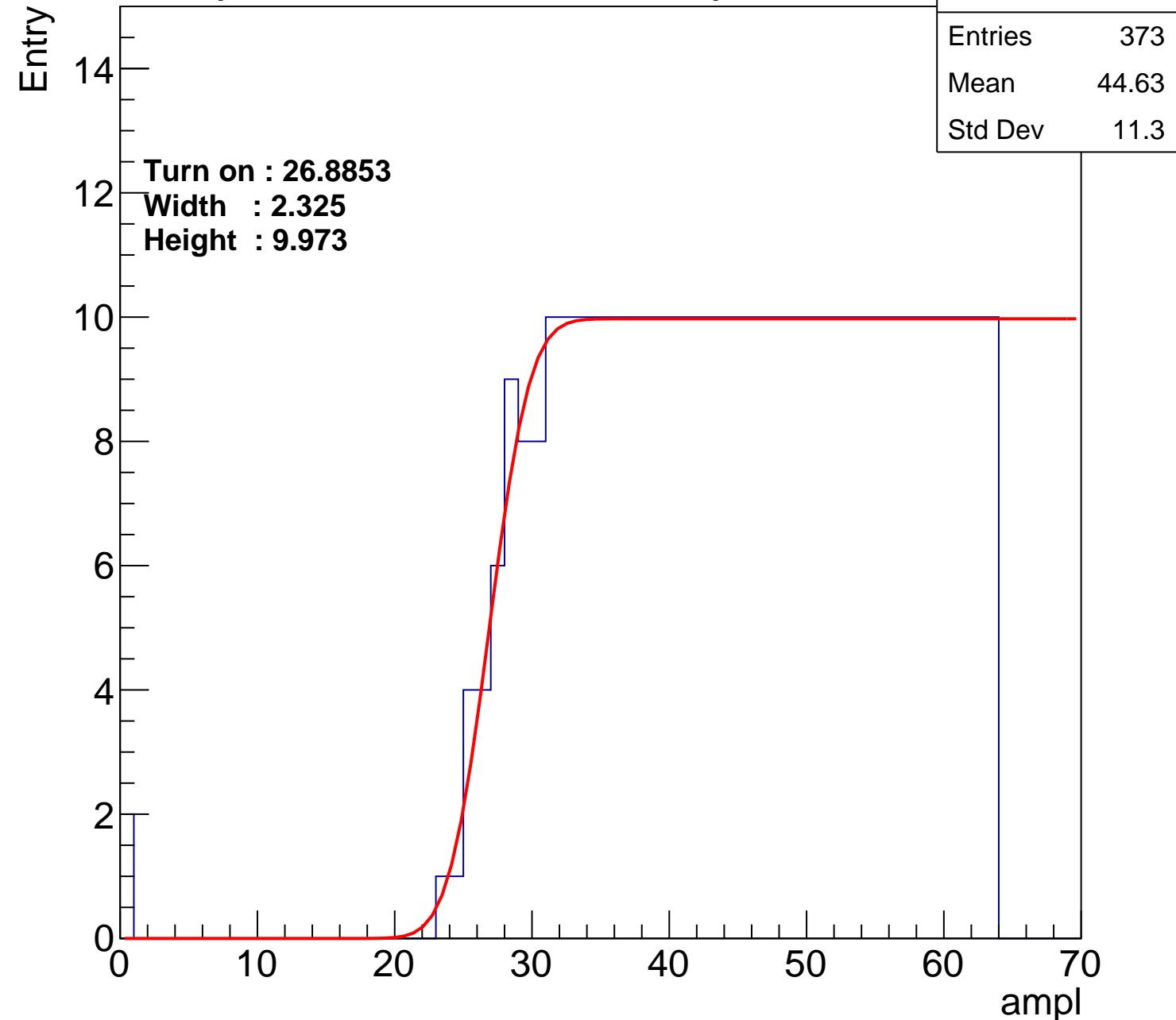
Width : 2.325

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch100

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.56 |
| Std Dev | 12.29 |

Turn on : 25.6059

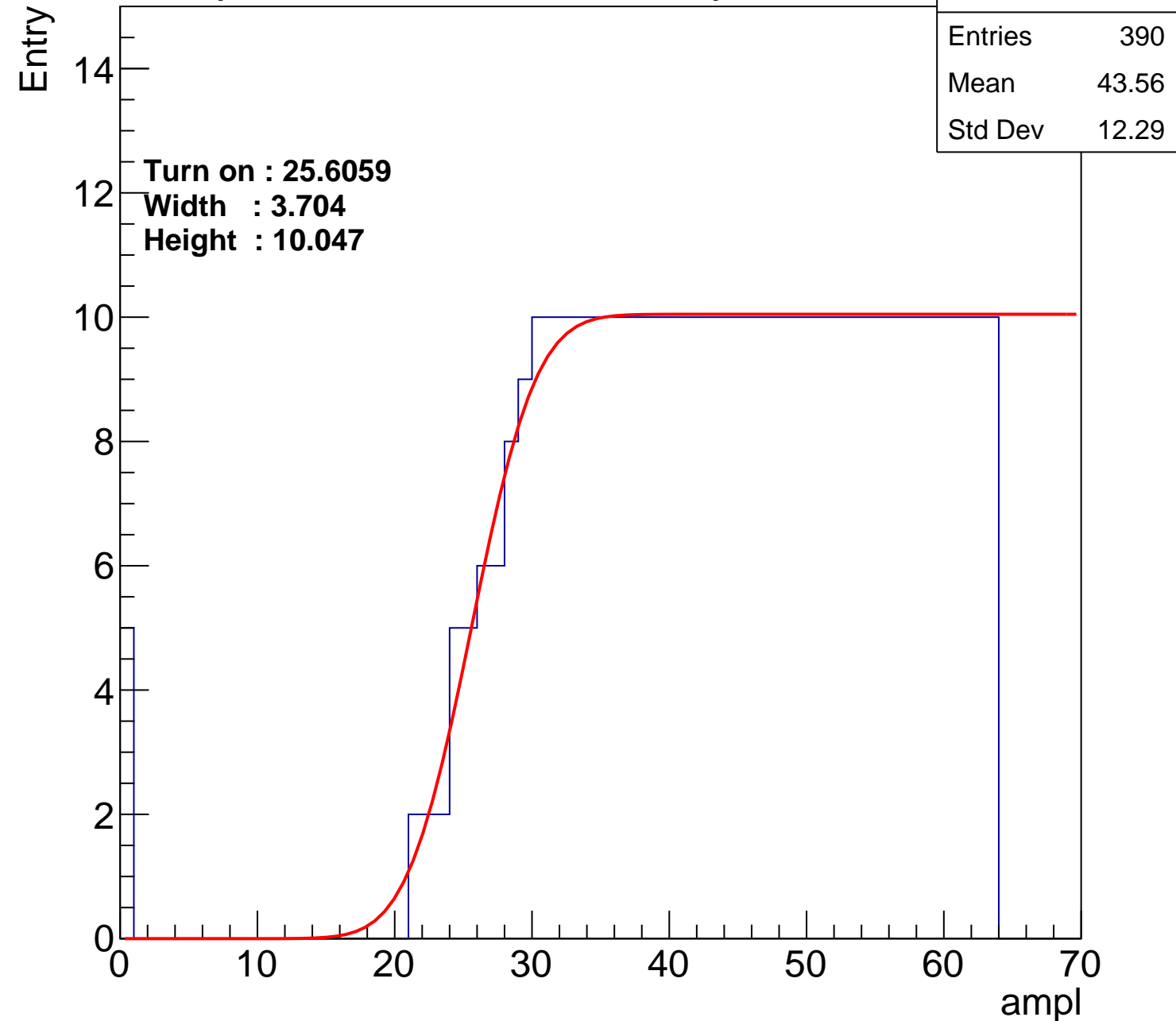
Width : 3.704

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch101

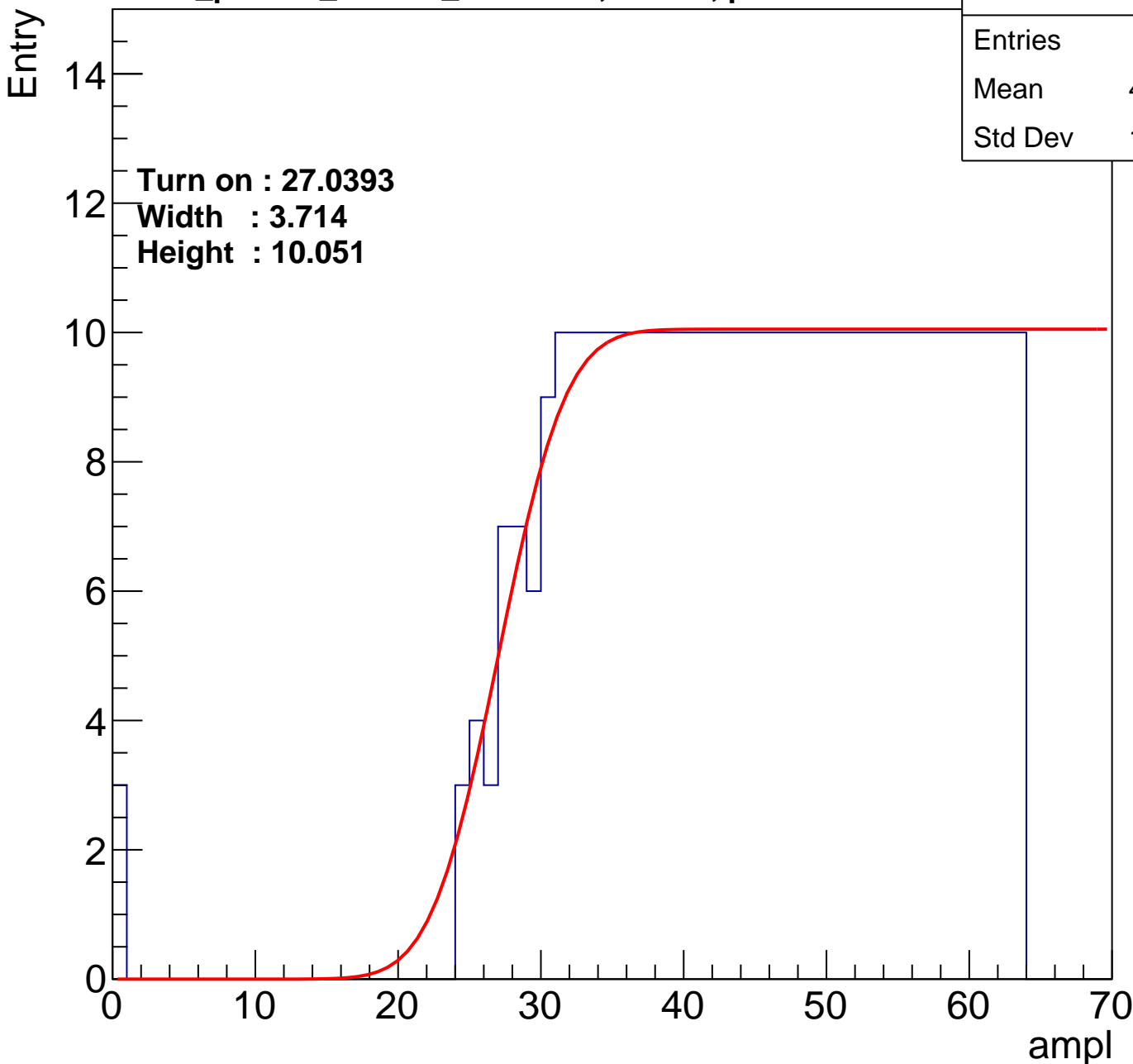
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 27.0393

Width : 3.714

Height : 10.051

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.59 |
| Std Dev | 11.49 |



B0L102S, U19-ch102

calib_packv5_042523_0143.root, FC#12, port B1

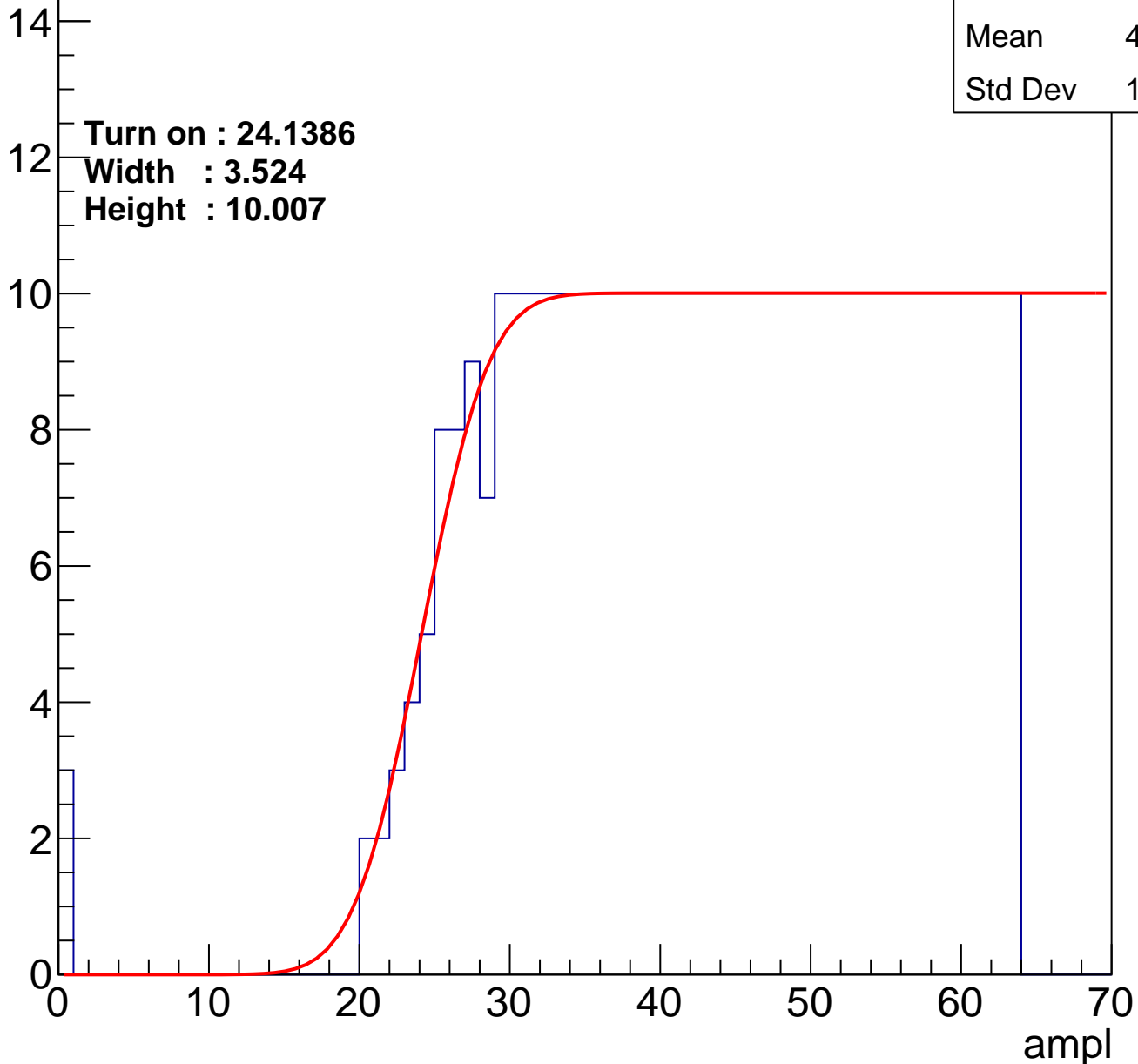
| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.16 |
| Std Dev | 12.22 |

Turn on : 24.1386

Width : 3.524

Height : 10.007

Entry



B0L102S, U19-ch103

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.89 |
| Std Dev | 11.21 |

Turn on : 27.7821

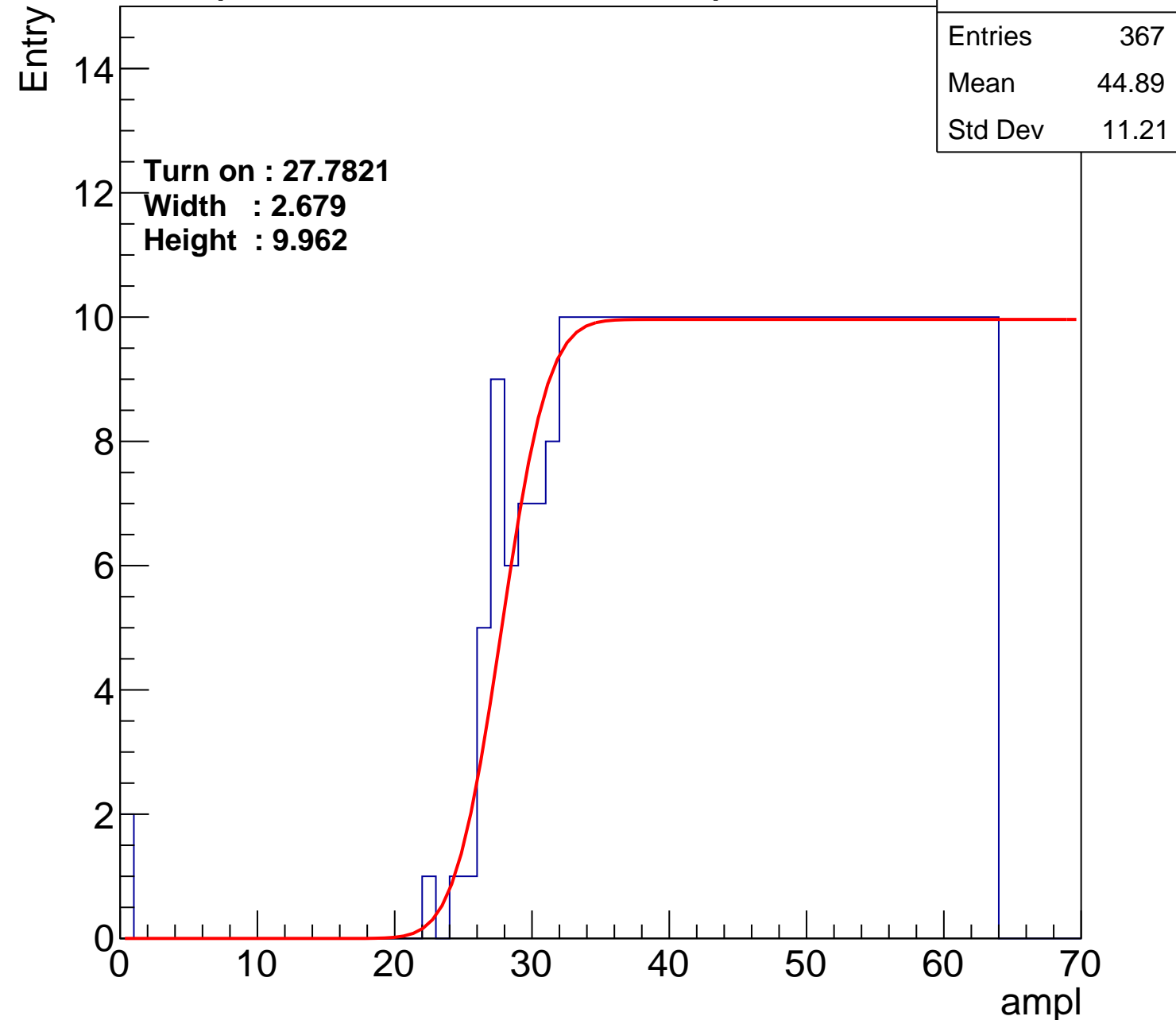
Width : 2.679

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch104

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.71 |
| Std Dev | 11.6 |

Turn on : 28.1687

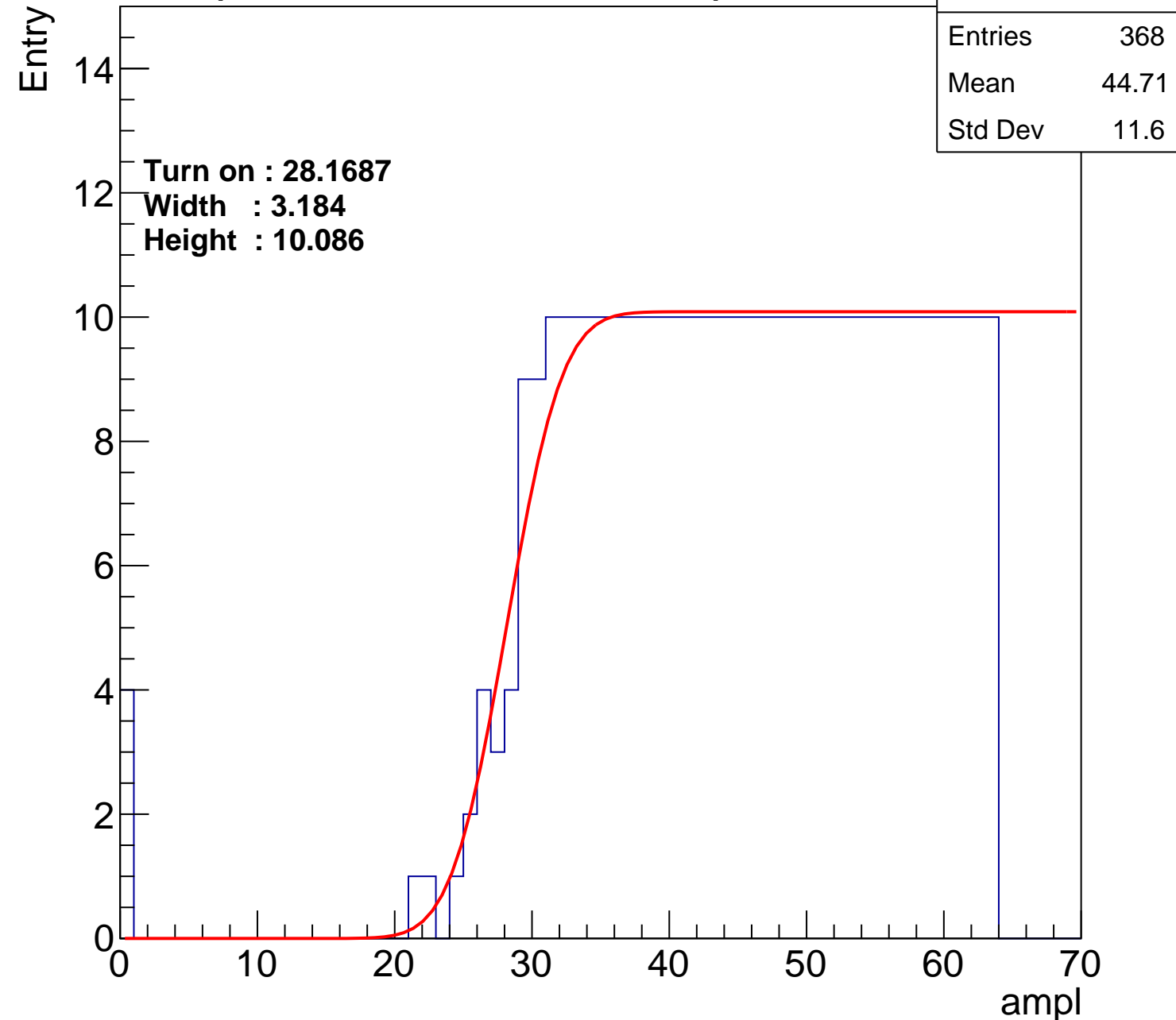
Width : 3.184

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch105

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.87 |
| Std Dev | 11.54 |

Turn on : 25.5564

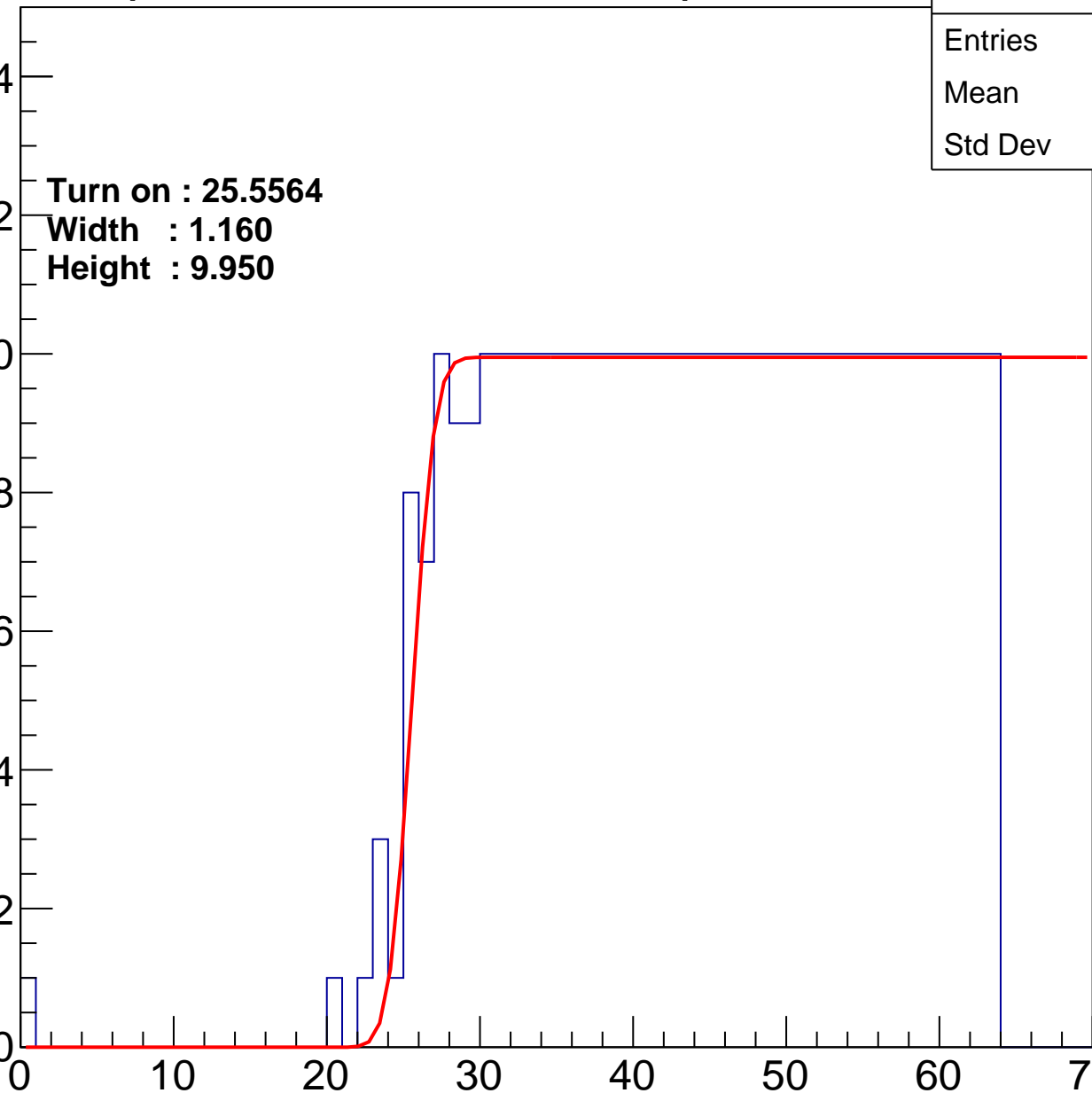
Width : 1.160

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch106

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.56 |
| Std Dev | 11.55 |

Turn on : 27.1805

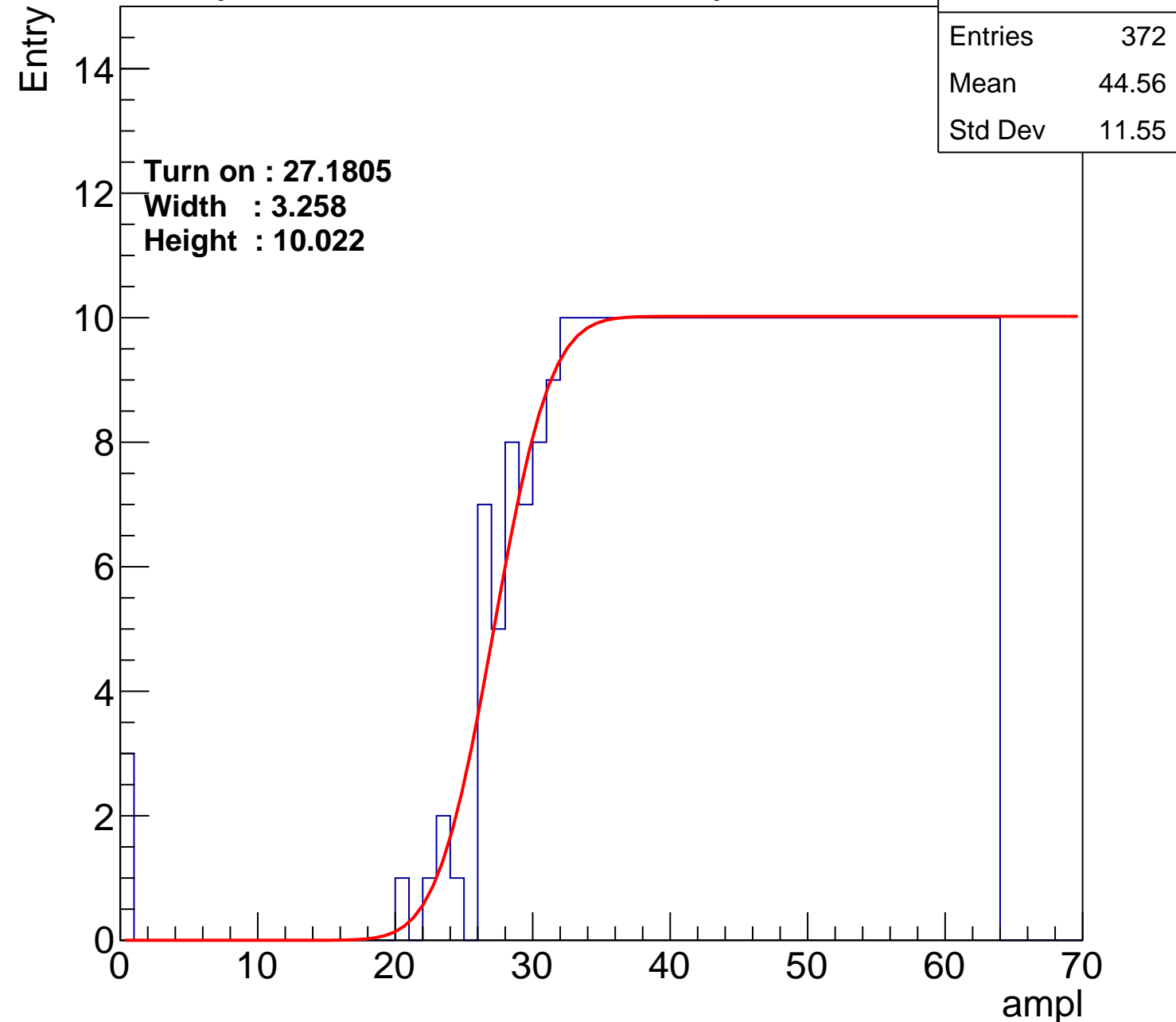
Width : 3.258

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch107

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.41 |
| Std Dev | 11.44 |

Turn on : 26.9861

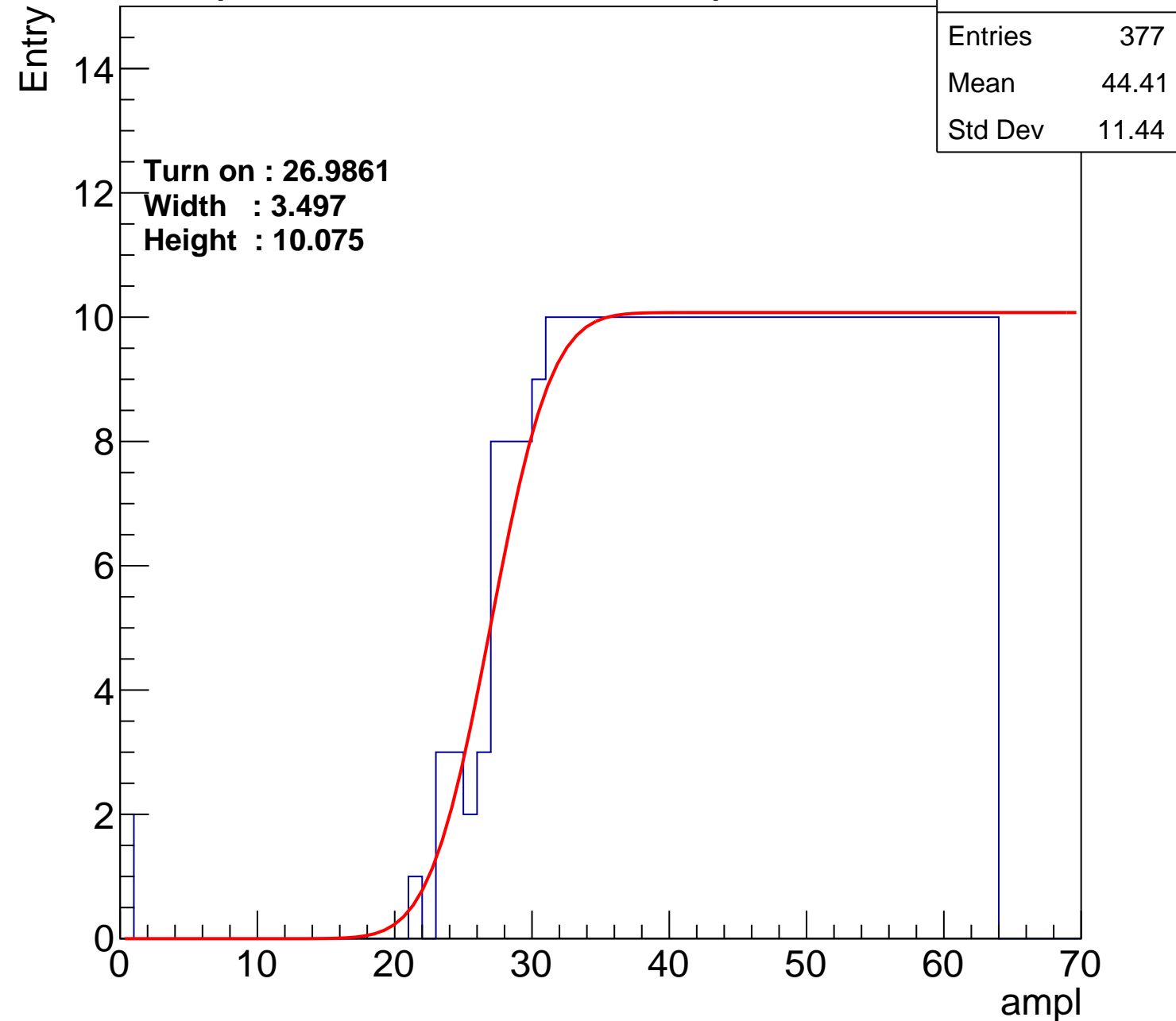
Width : 3.497

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch108

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 406 |
| Mean | 42.84 |
| Std Dev | 12.53 |

Turn on : 24.4994

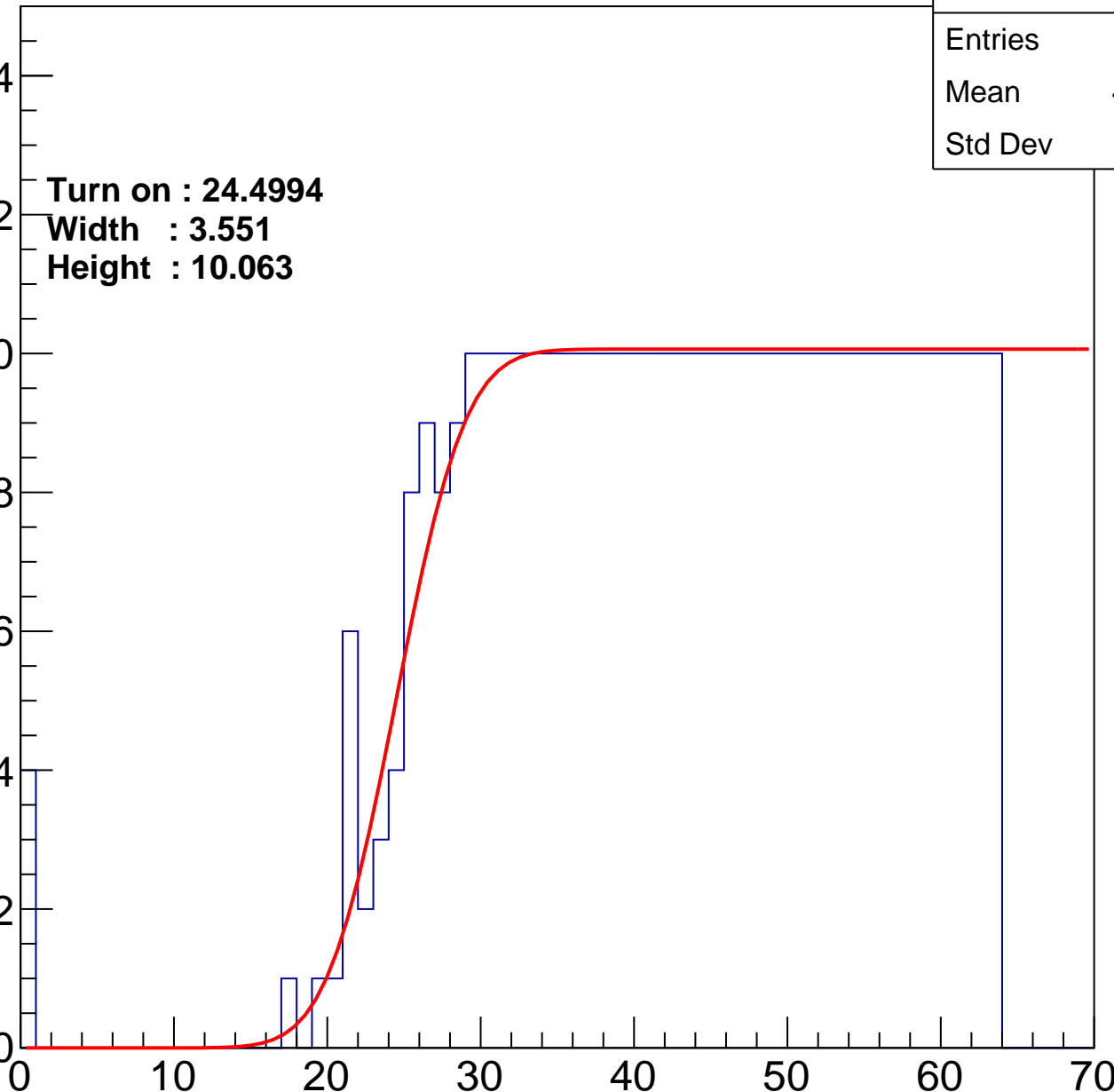
Width : 3.551

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch109

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.57 |
| Std Dev | 12.01 |

Turn on : 25.4956

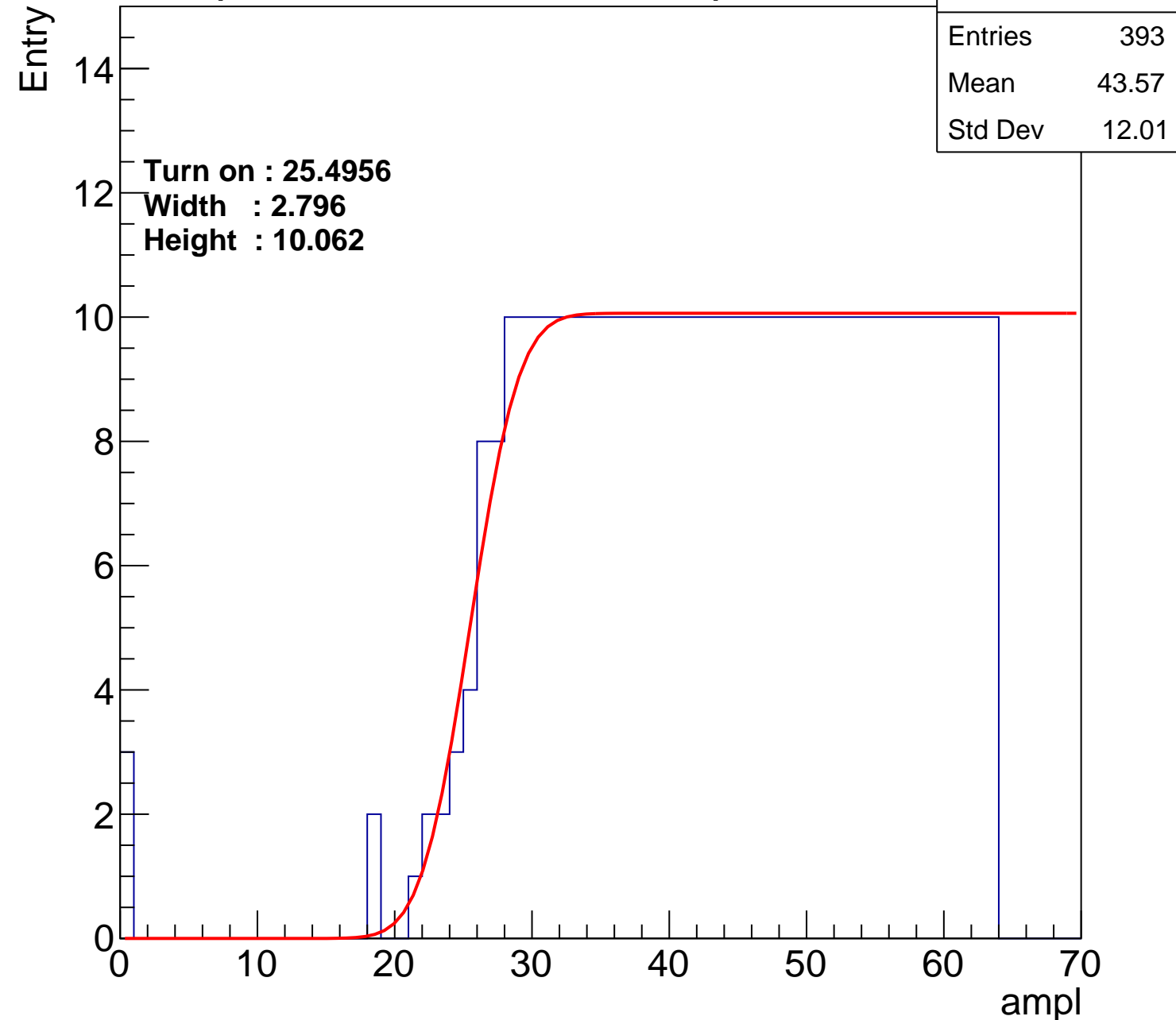
Width : 2.796

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch110

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.58 |
| Std Dev | 11.87 |

Turn on : 25.3767

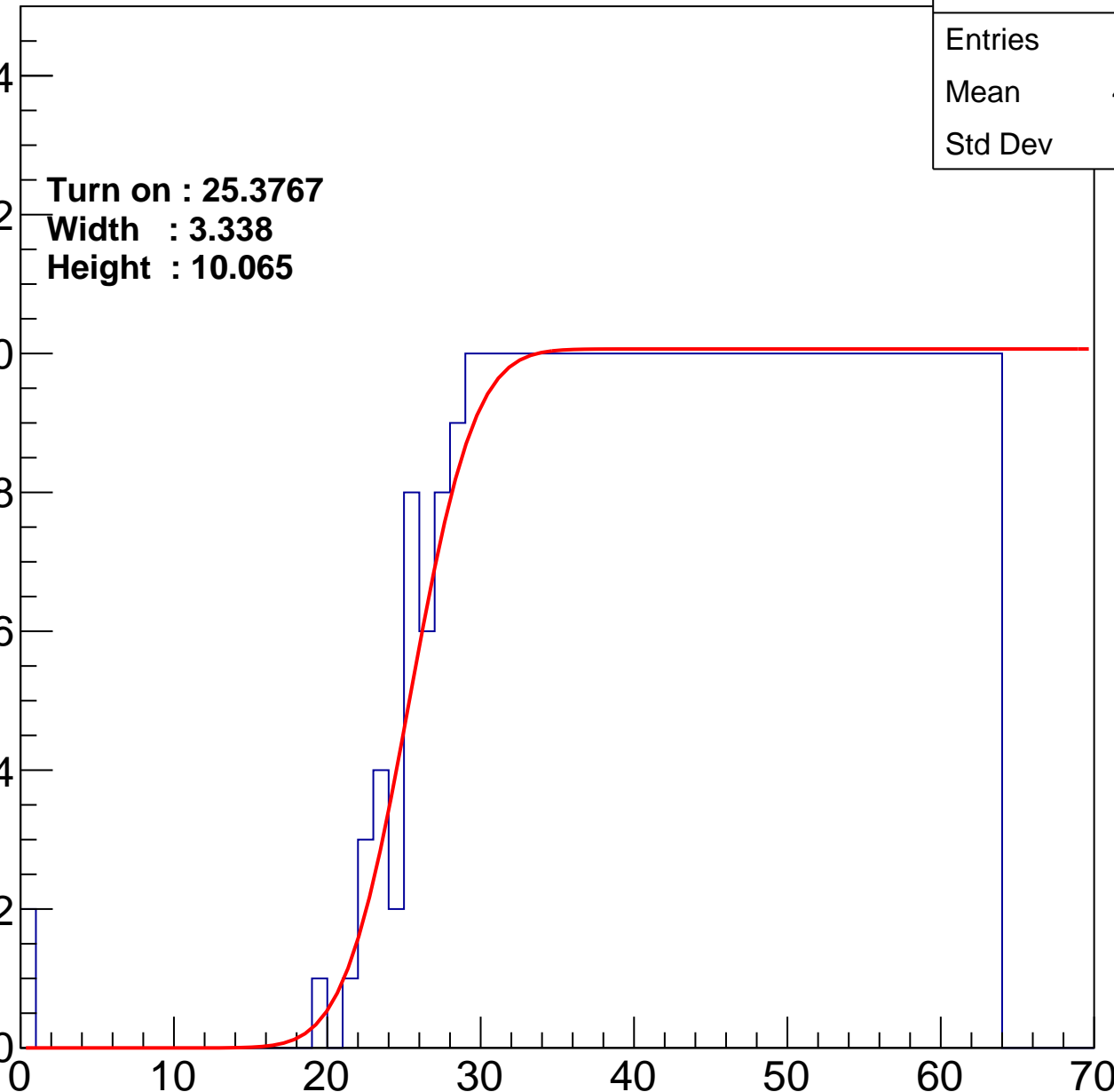
Width : 3.338

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch111

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.55 |
| Std Dev | 12.03 |

Turn on : 24.9077

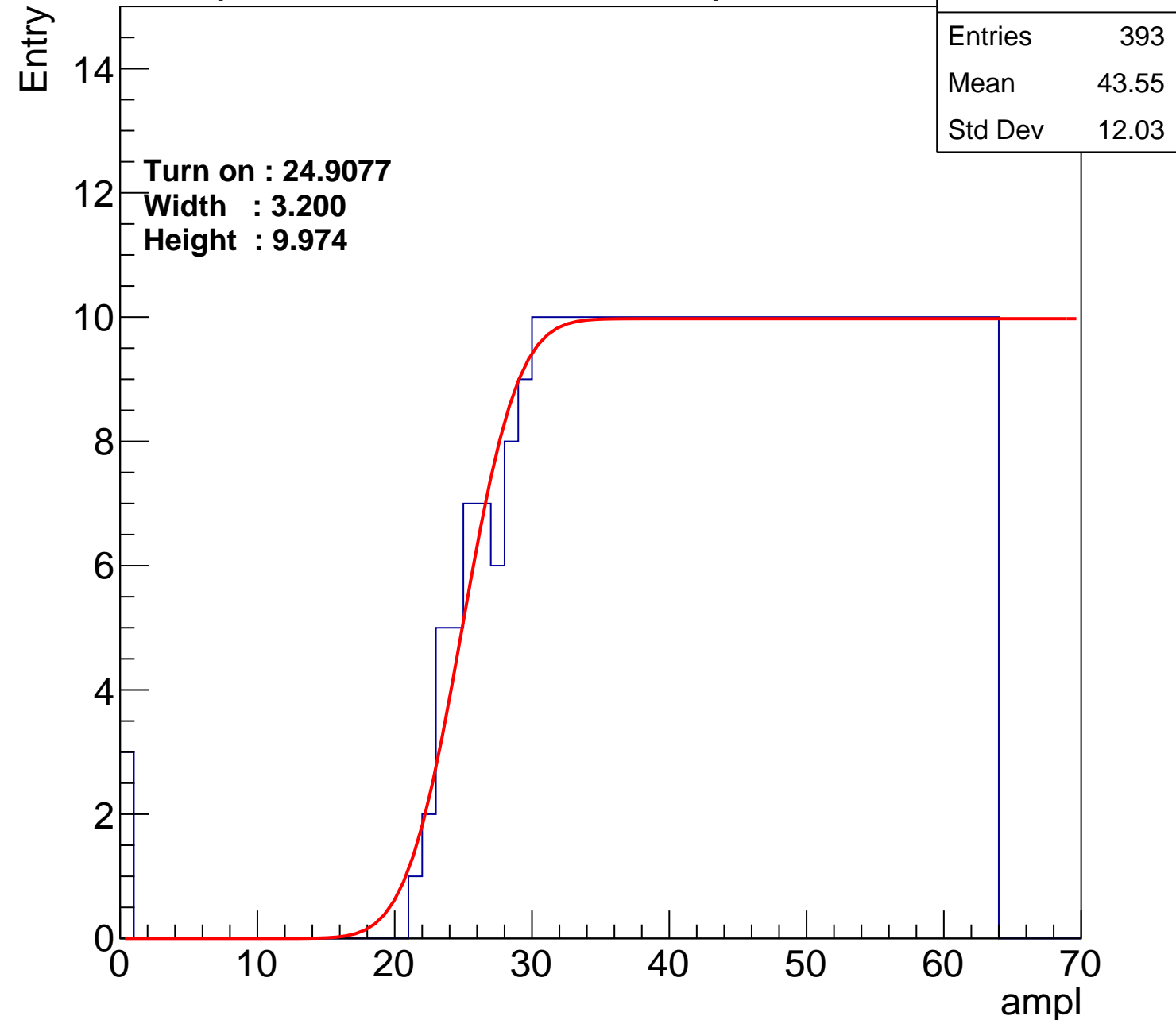
Width : 3.200

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch112

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.25 |
| Std Dev | 12.19 |

Turn on : 26.6482

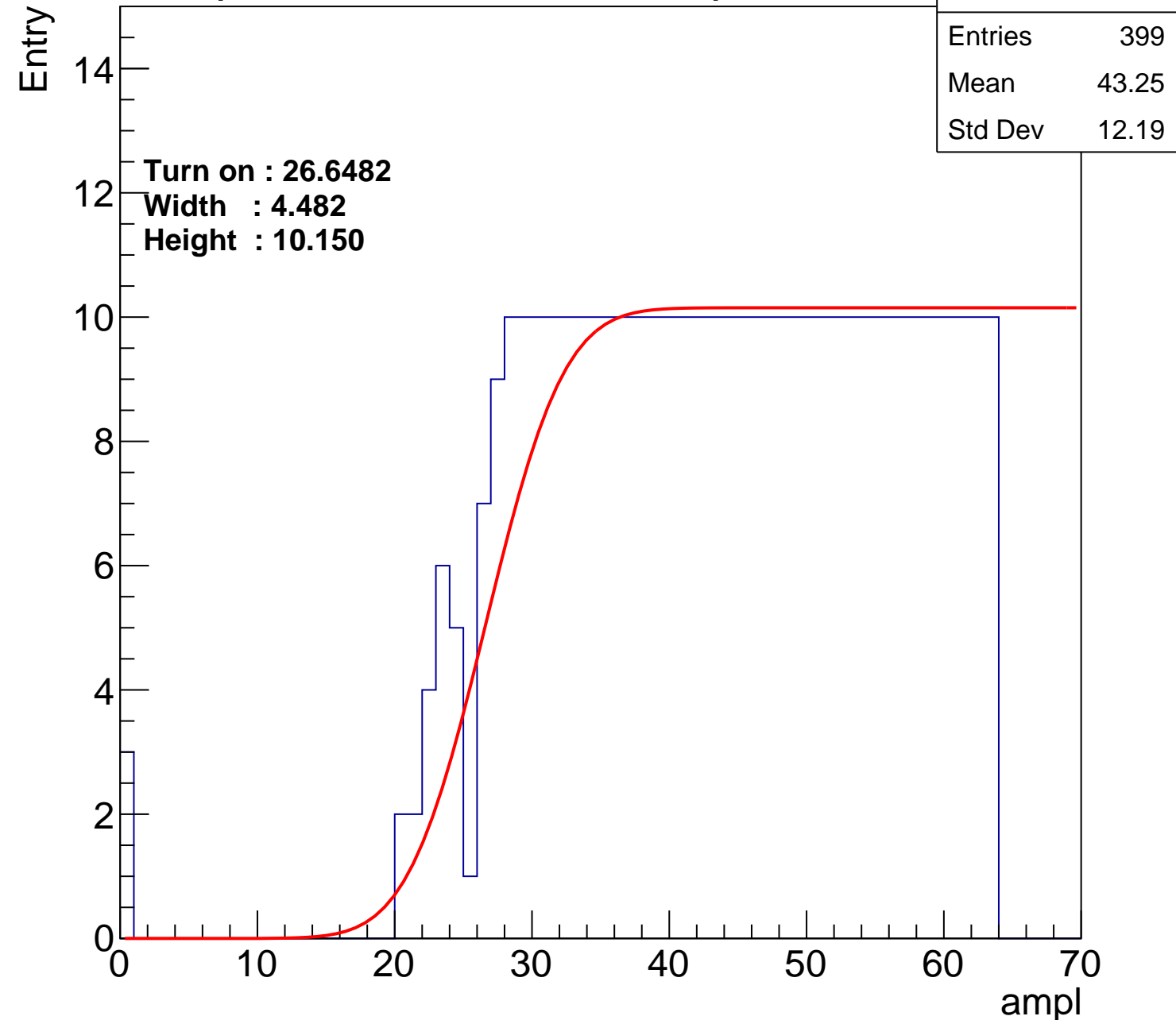
Width : 4.482

Height : 10.150

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch113

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.27 |
| Std Dev | 11.99 |

Turn on : 27.2522

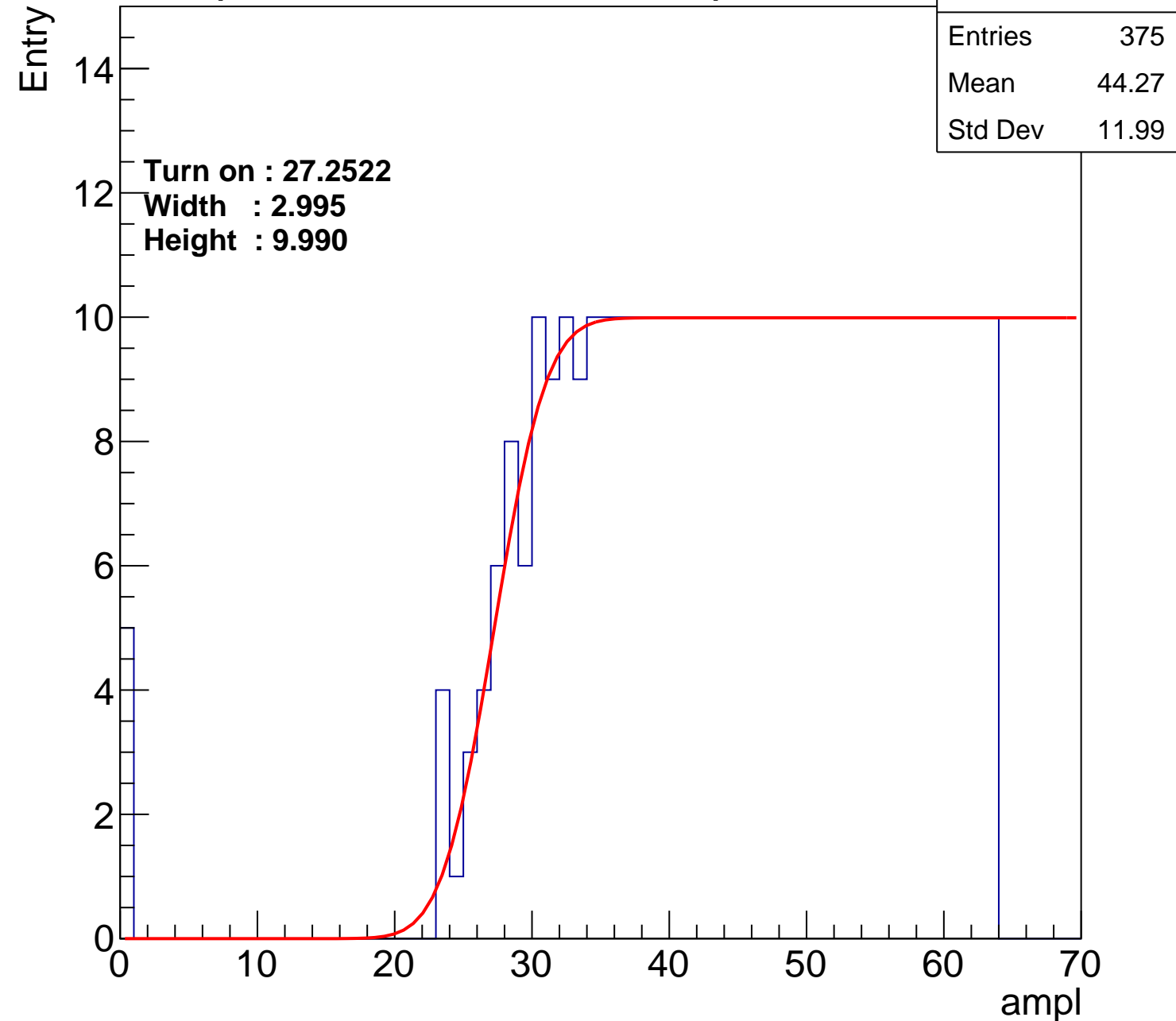
Width : 2.995

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch114

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.39 |
| Std Dev | 11.6 |

Turn on : 27.5051

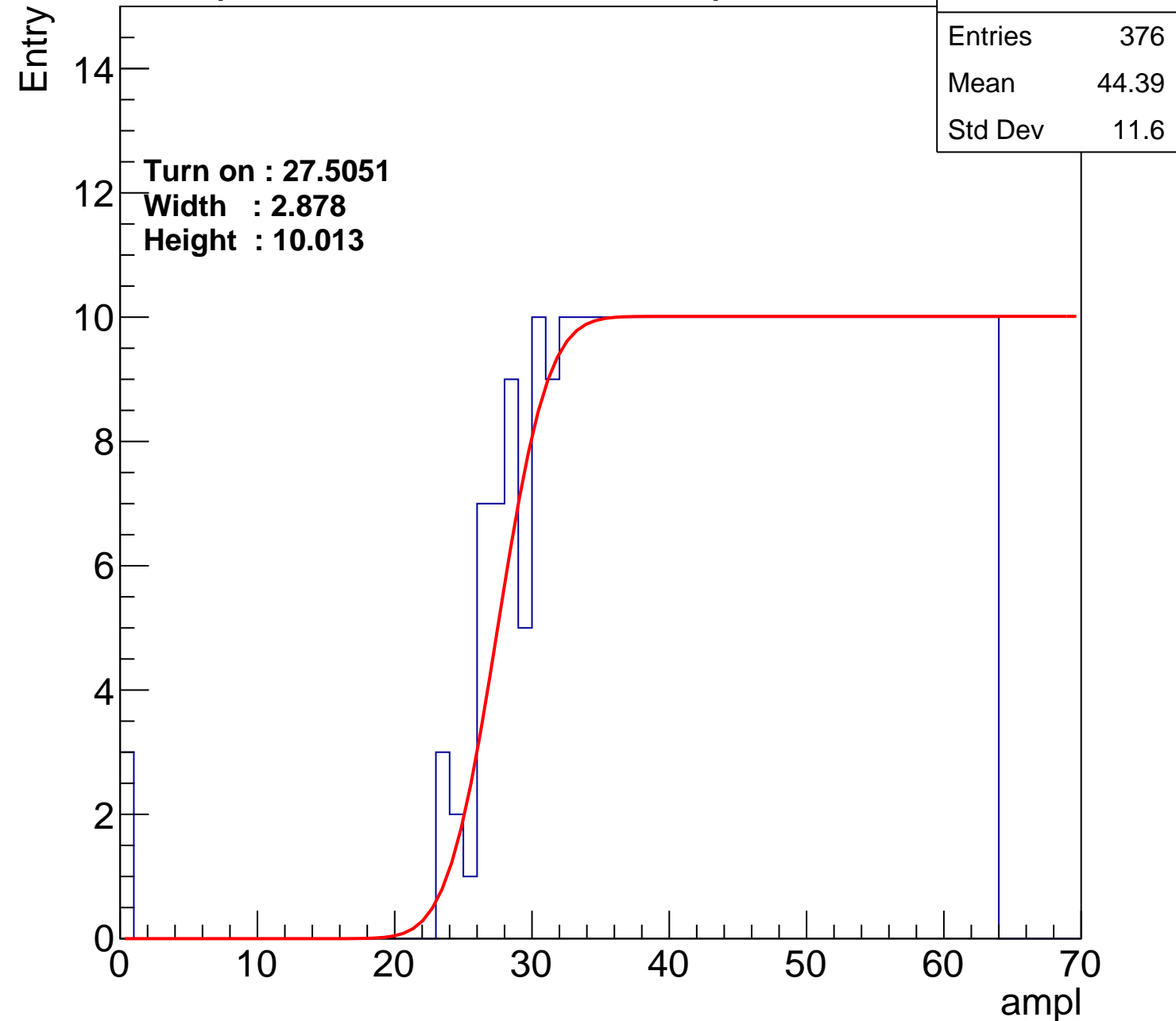
Width : 2.878

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch115

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.59 |
| Std Dev | 11.36 |

Turn on : 26.7434

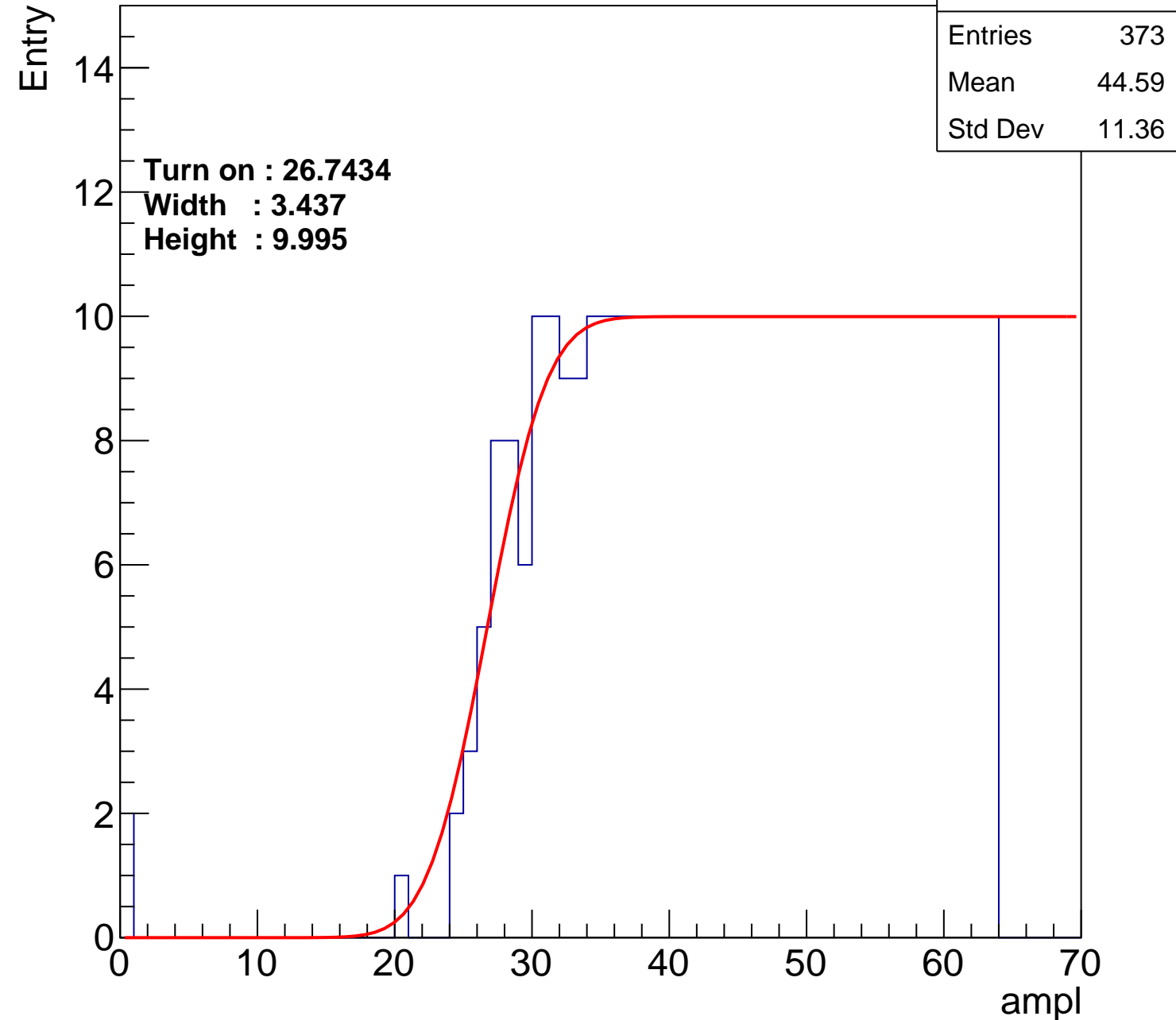
Width : 3.437

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch116

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.06 |
| Std Dev | 11.01 |

Turn on : 28.5844

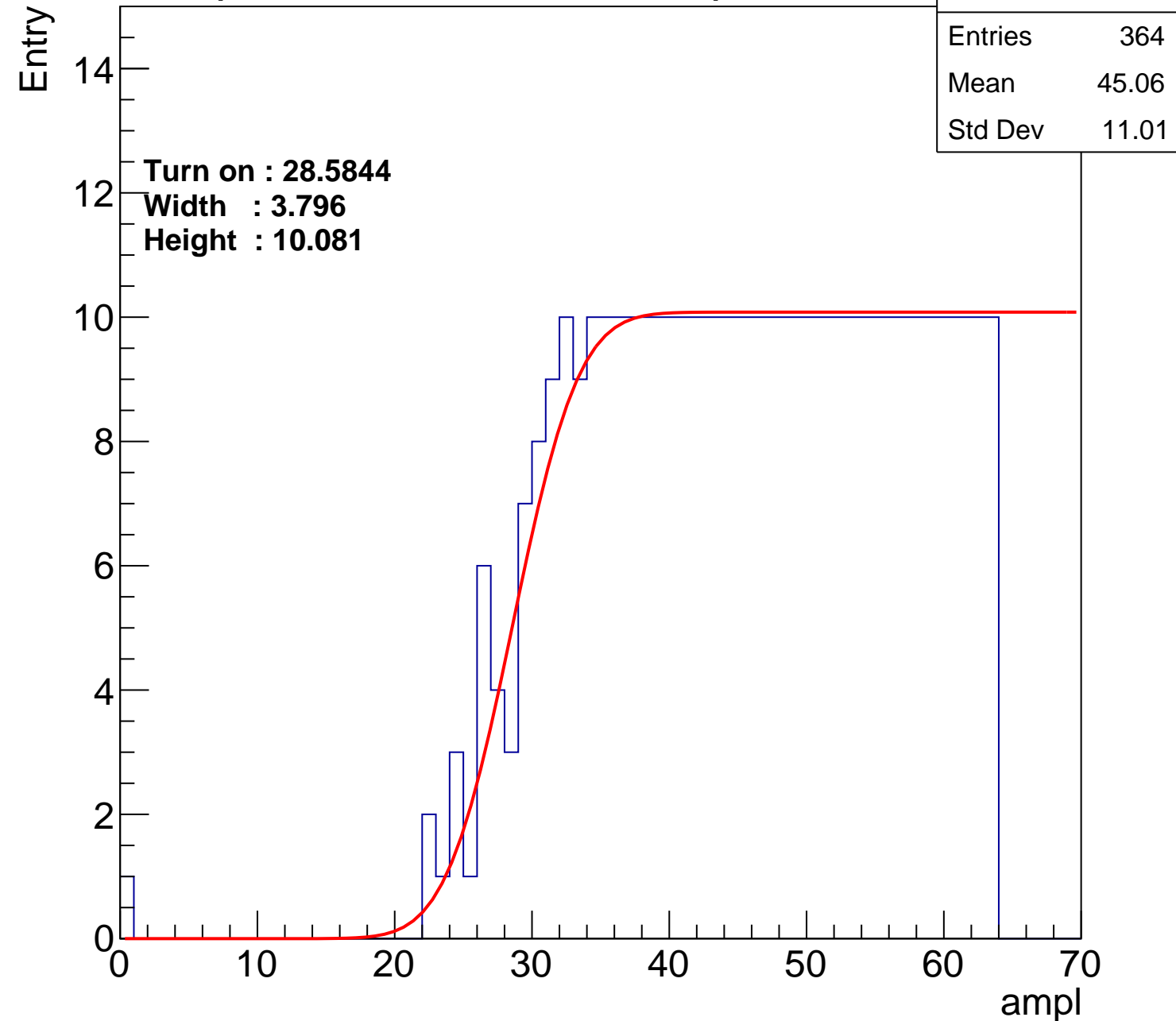
Width : 3.796

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch117

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.67 |
| Std Dev | 11.15 |

Turn on : 27.4276

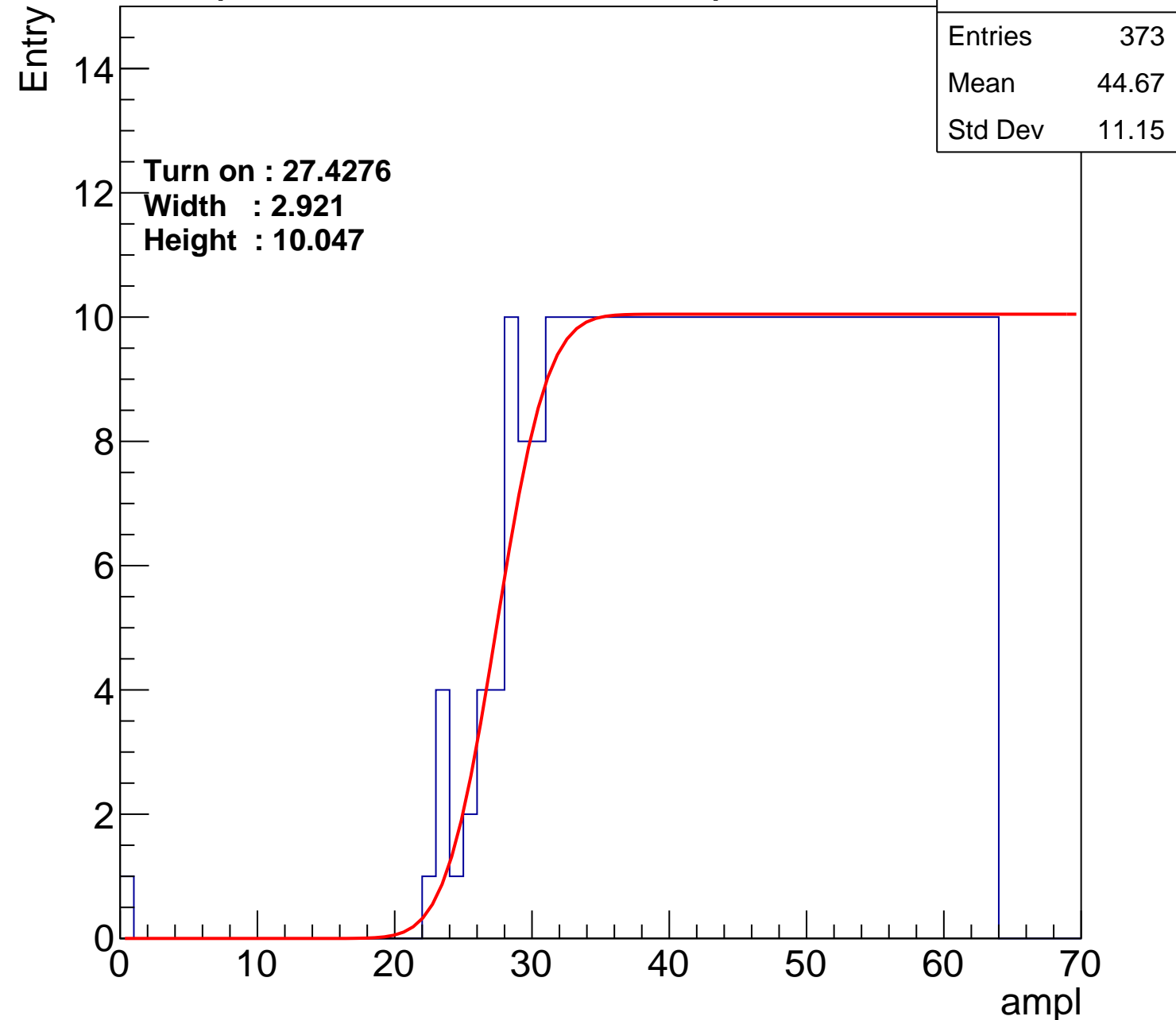
Width : 2.921

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch118

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.79 |
| Std Dev | 11.91 |

Turn on : 25.4253

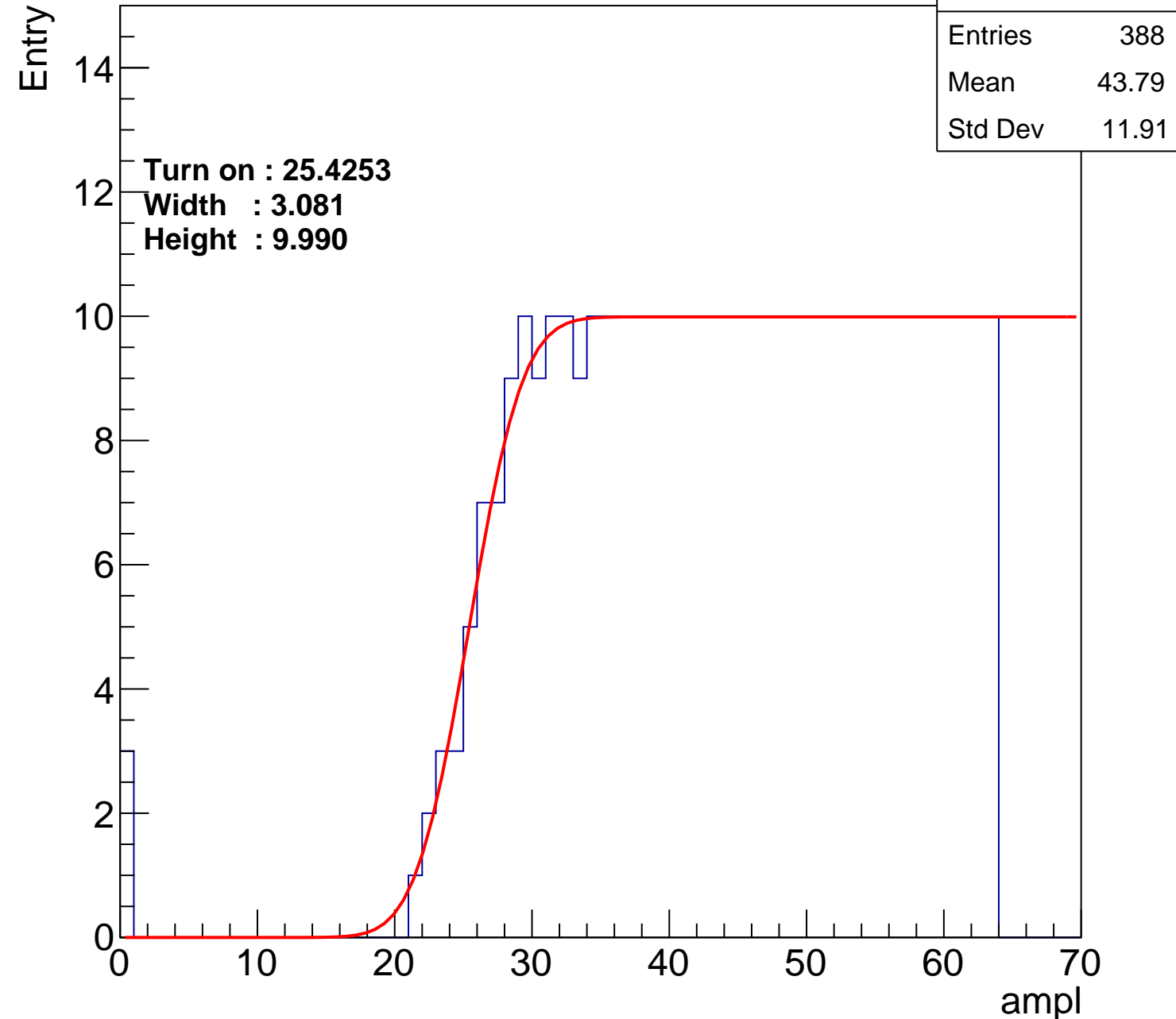
Width : 3.081

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch119

calib_packv5_042523_0143.root, FC#12, port B1

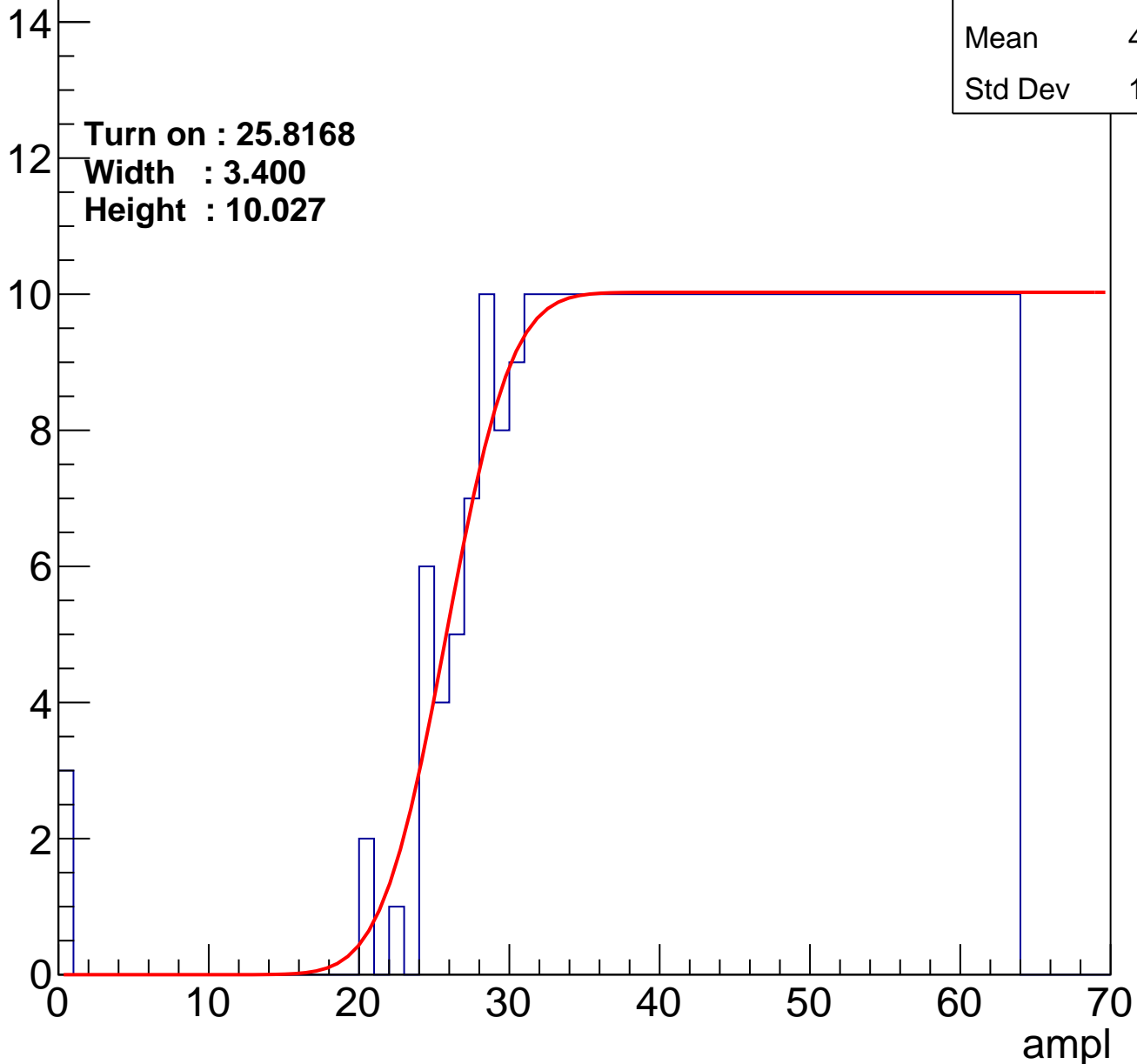
| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.94 |
| Std Dev | 11.84 |

Turn on : 25.8168

Width : 3.400

Height : 10.027

Entry



B0L102S, U19-ch120

calib_packv5_042523_0143.root, FC#12, port B1

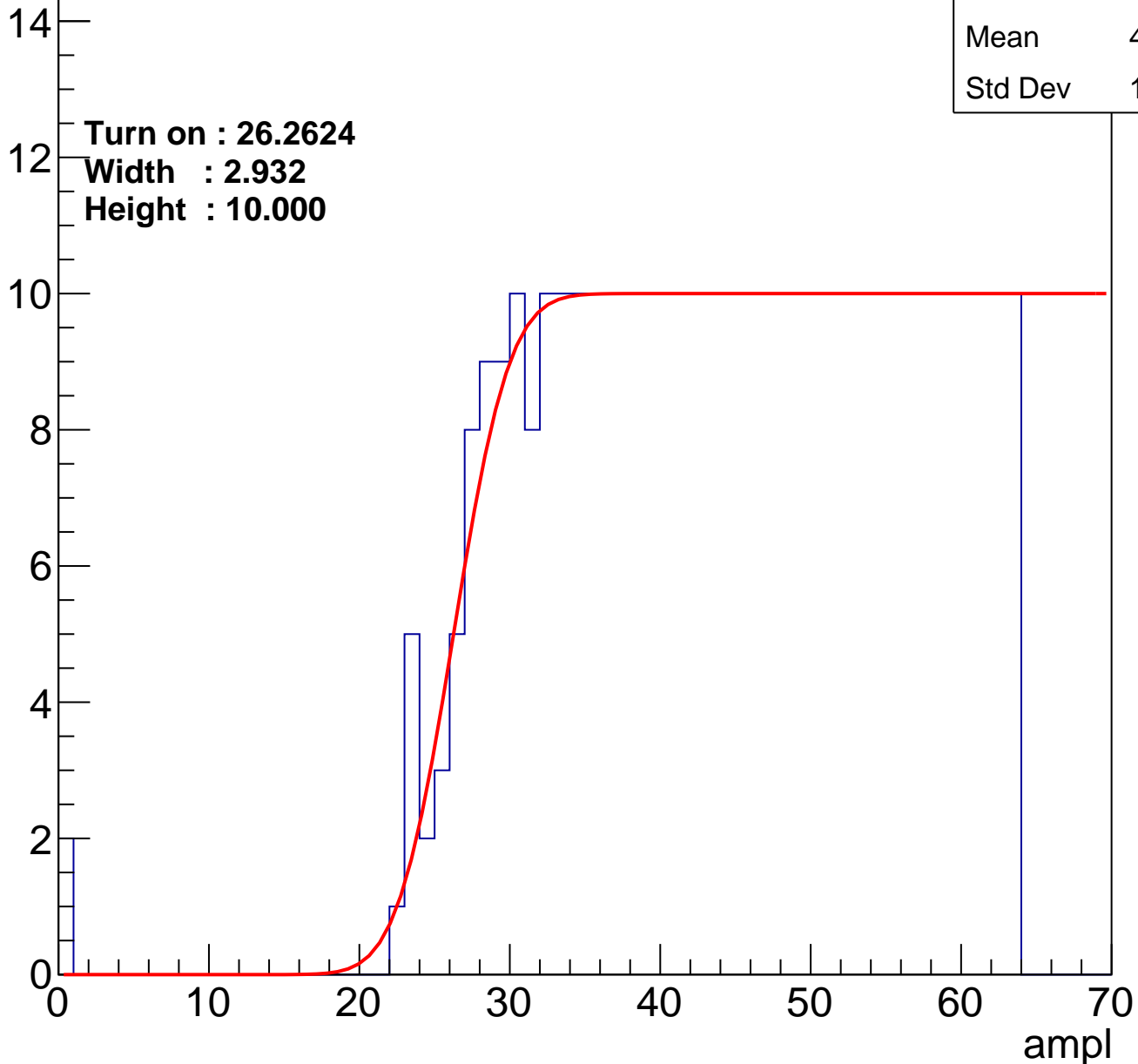
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.15 |
| Std Dev | 11.58 |

Turn on : 26.2624

Width : 2.932

Height : 10.000

Entry



B0L102S, U19-ch121

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.24 |
| Std Dev | 12.31 |

Turn on : 24.6976

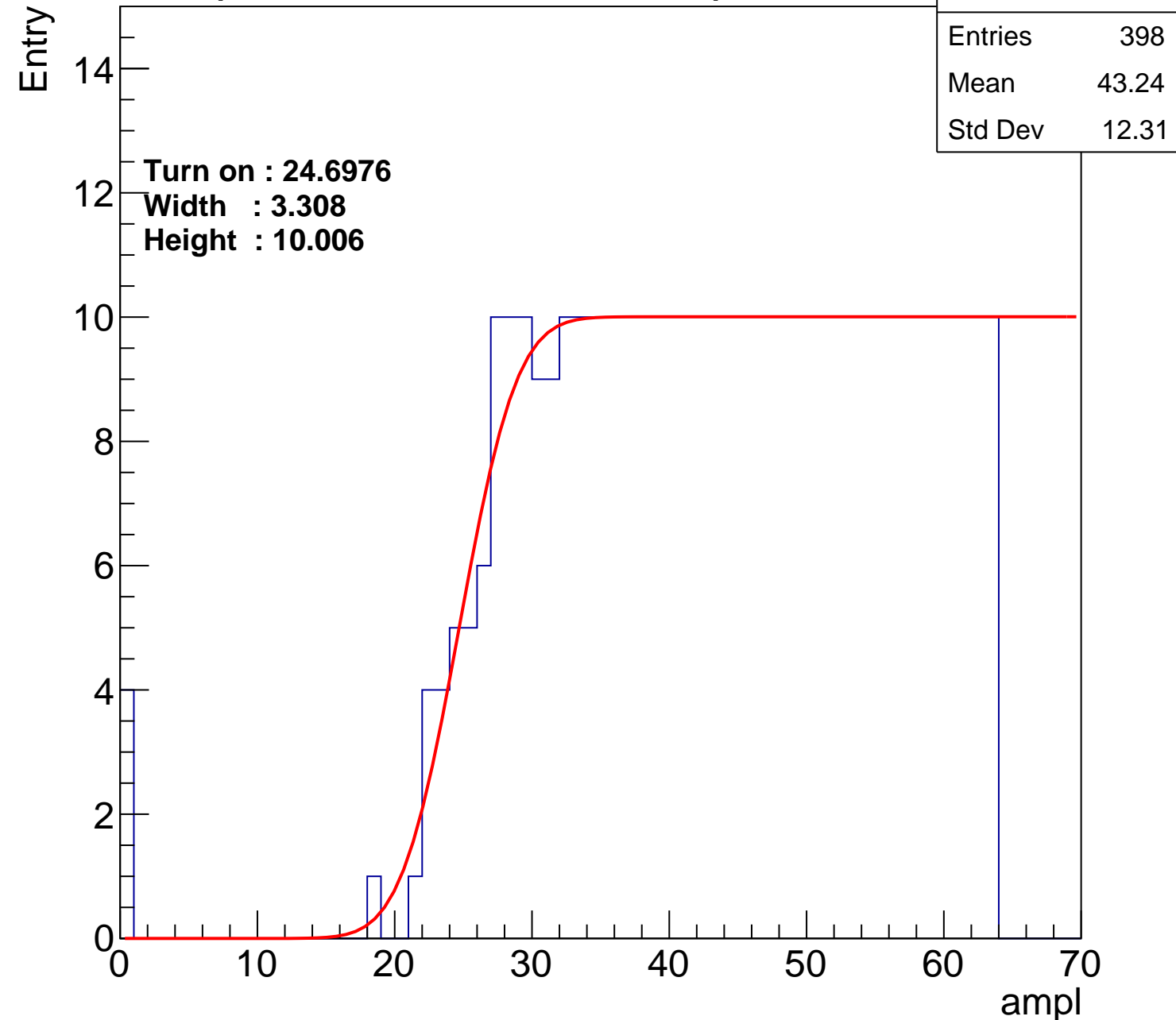
Width : 3.308

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch122

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.18 |
| Std Dev | 11.59 |

Turn on : 26.4201

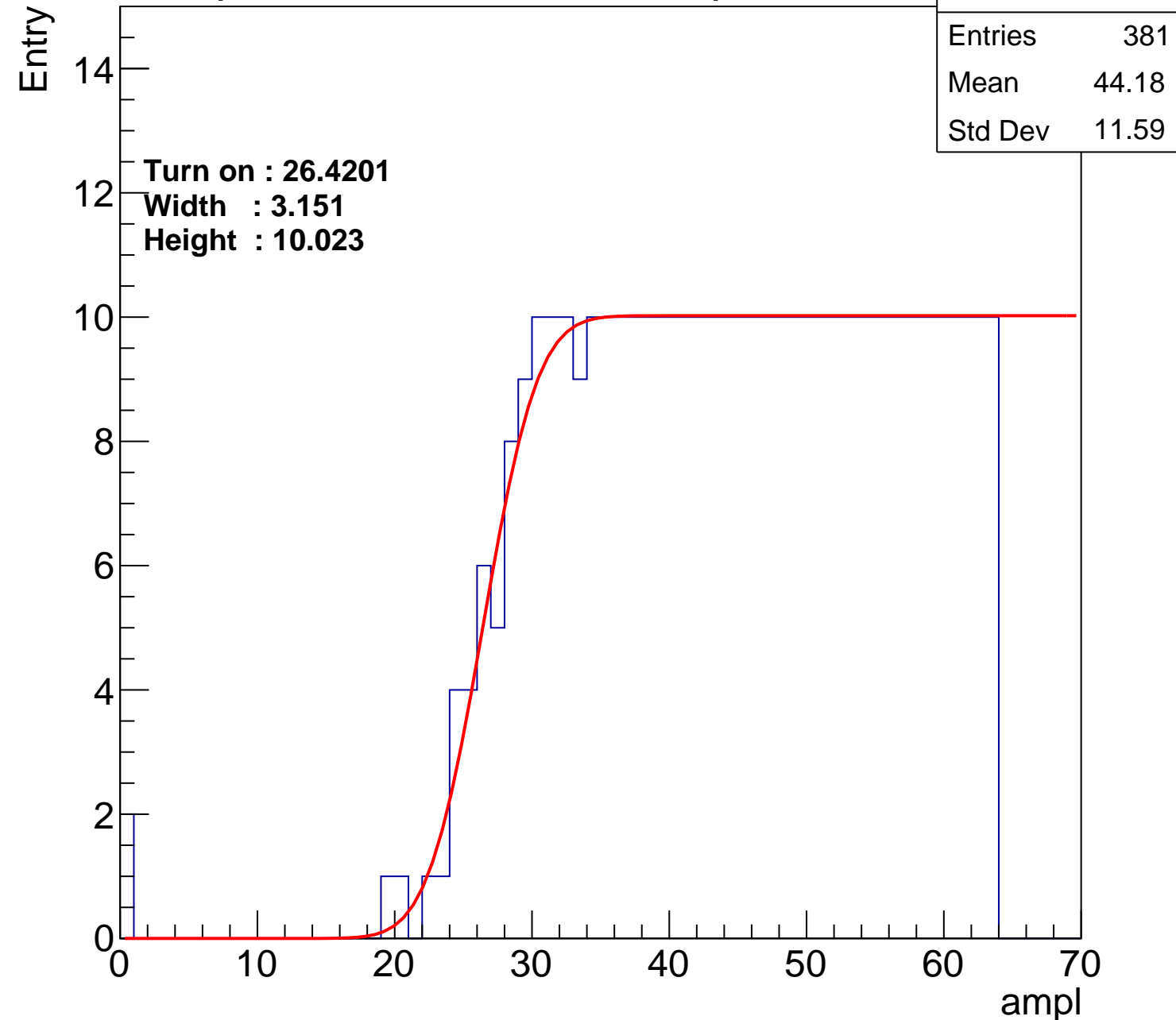
Width : 3.151

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch123

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.47 |
| Std Dev | 11.82 |

Turn on : 27.5972

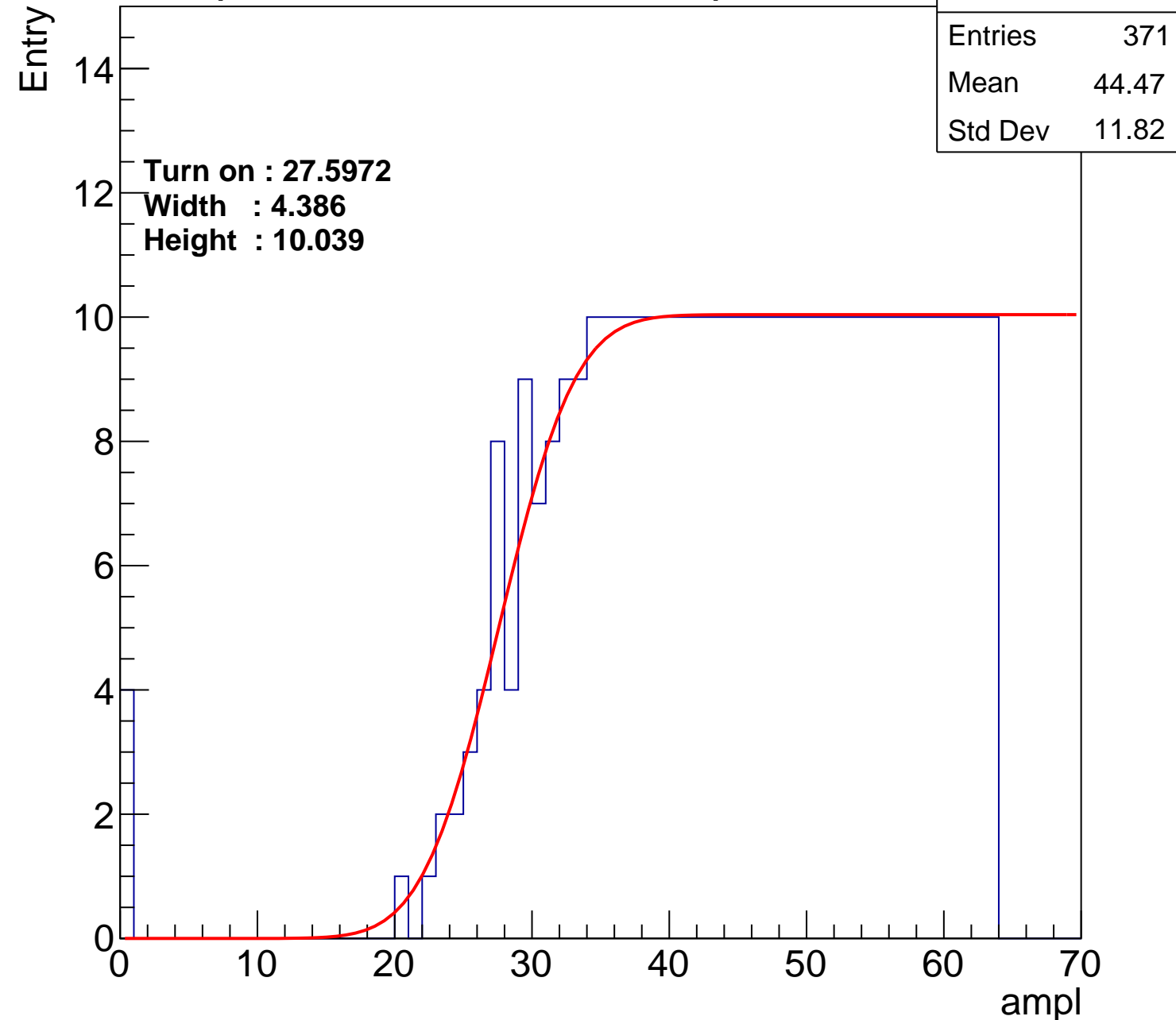
Width : 4.386

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch124

calib_packv5_042523_0143.root, FC#12, port B1

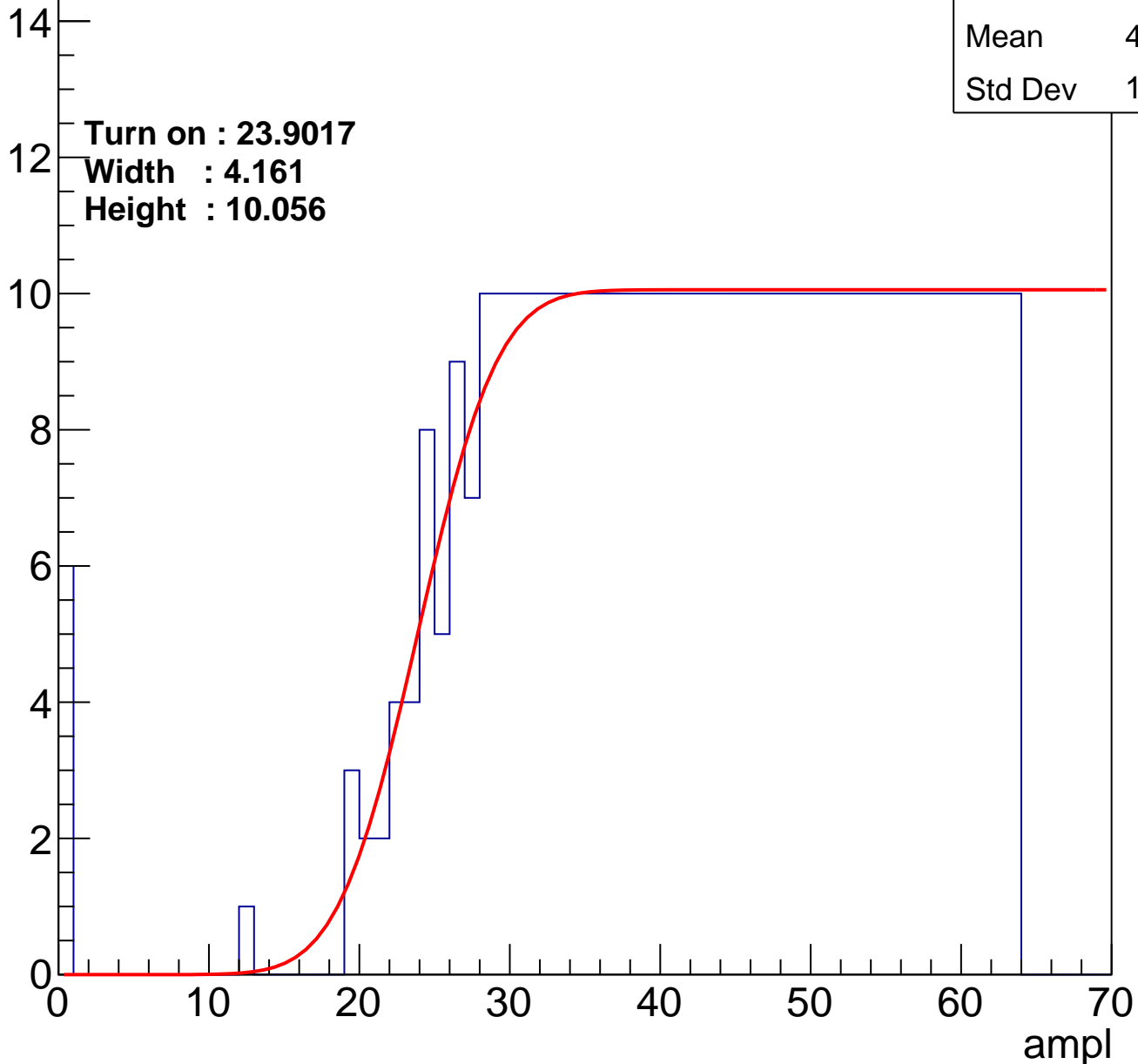
| | |
|---------|-------|
| Entries | 411 |
| Mean | 42.46 |
| Std Dev | 12.96 |

Turn on : 23.9017

Width : 4.161

Height : 10.056

Entry



B0L102S, U19-ch125

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.1 |
| Std Dev | 11.61 |

Turn on : 26.4134

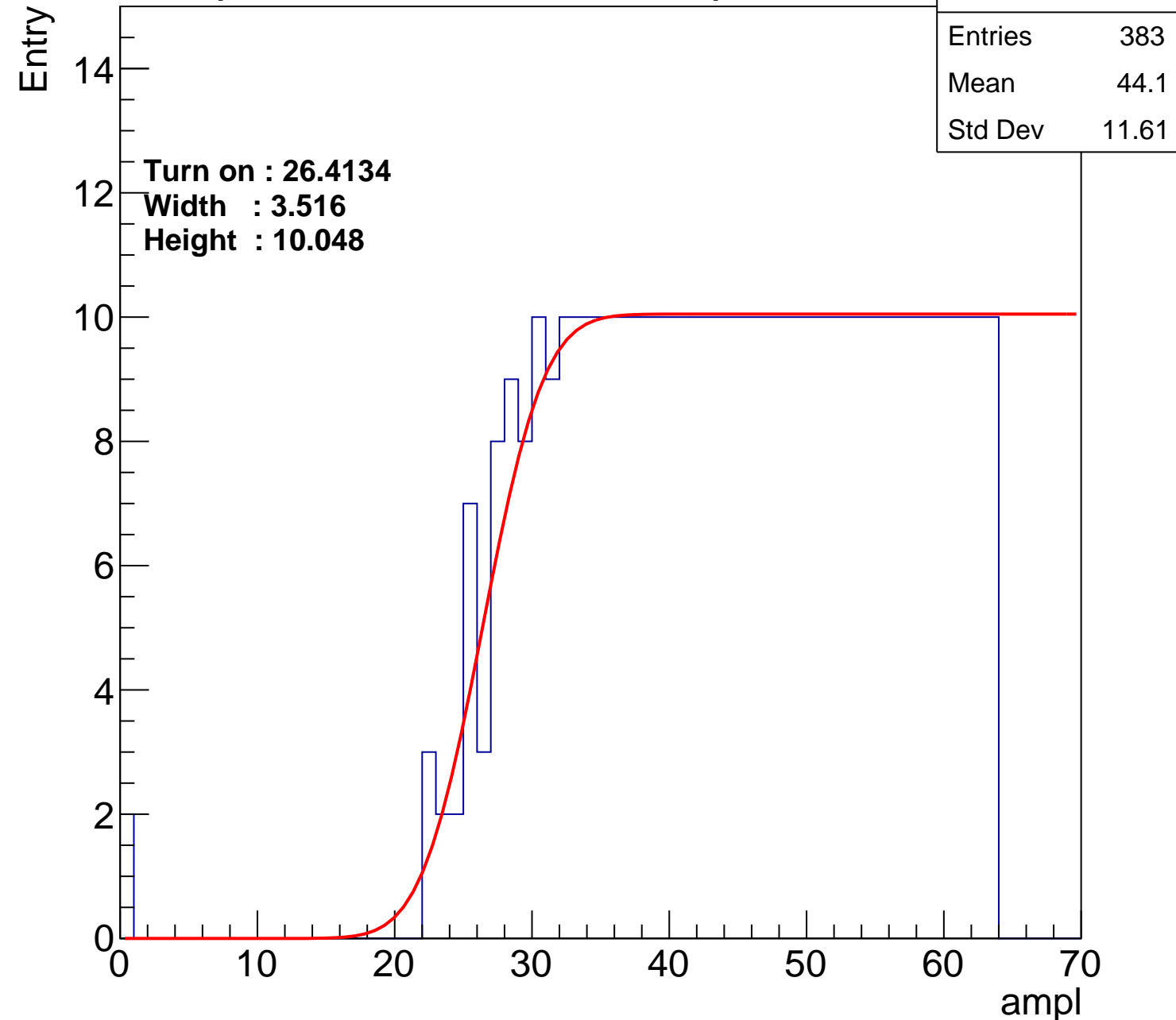
Width : 3.516

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch126

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.51 |
| Std Dev | 12.04 |

Turn on : 25.6475

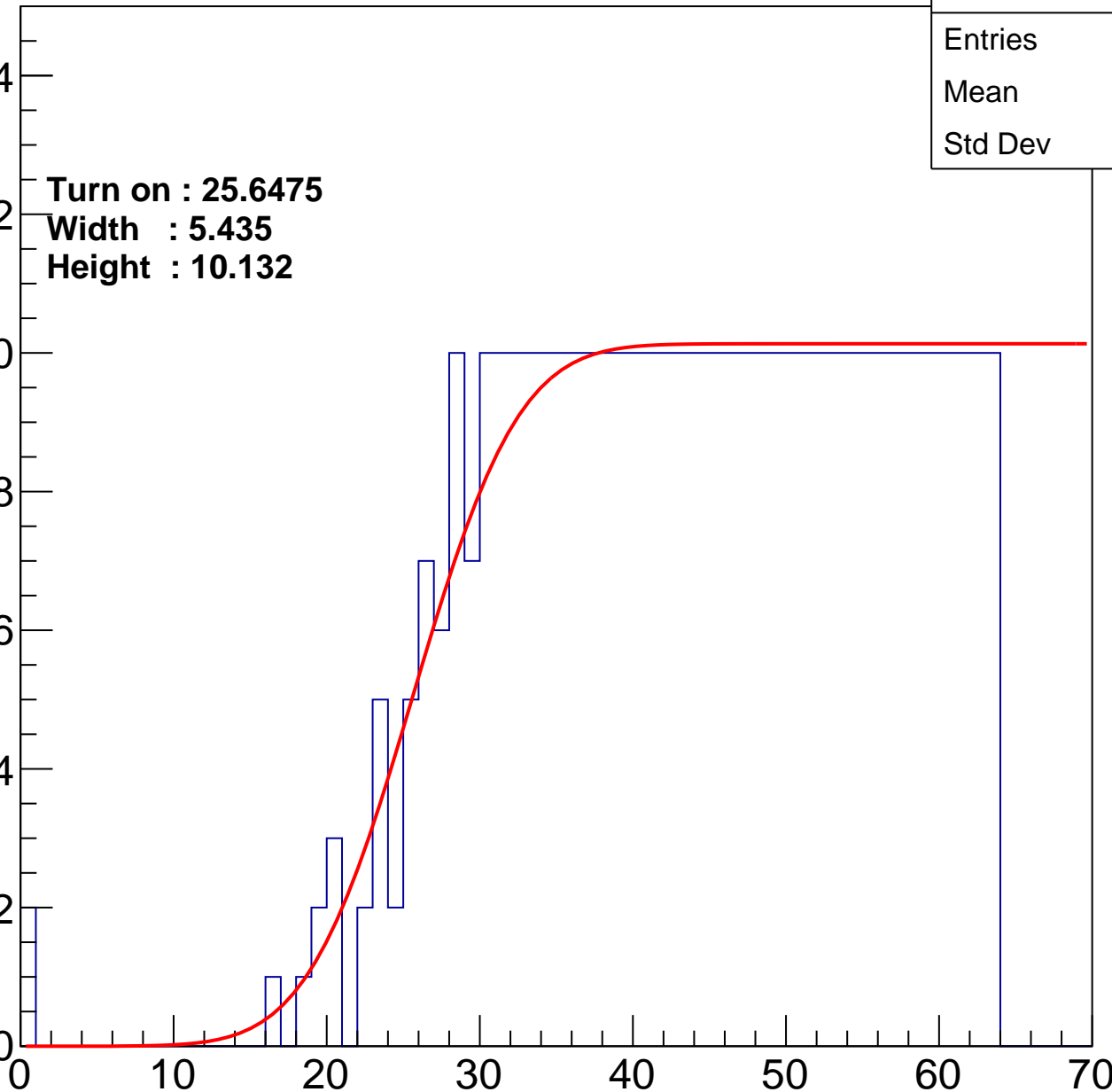
Width : 5.435

Height : 10.132

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch127

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.59 |
| Std Dev | 11.49 |

Turn on : 27.4724

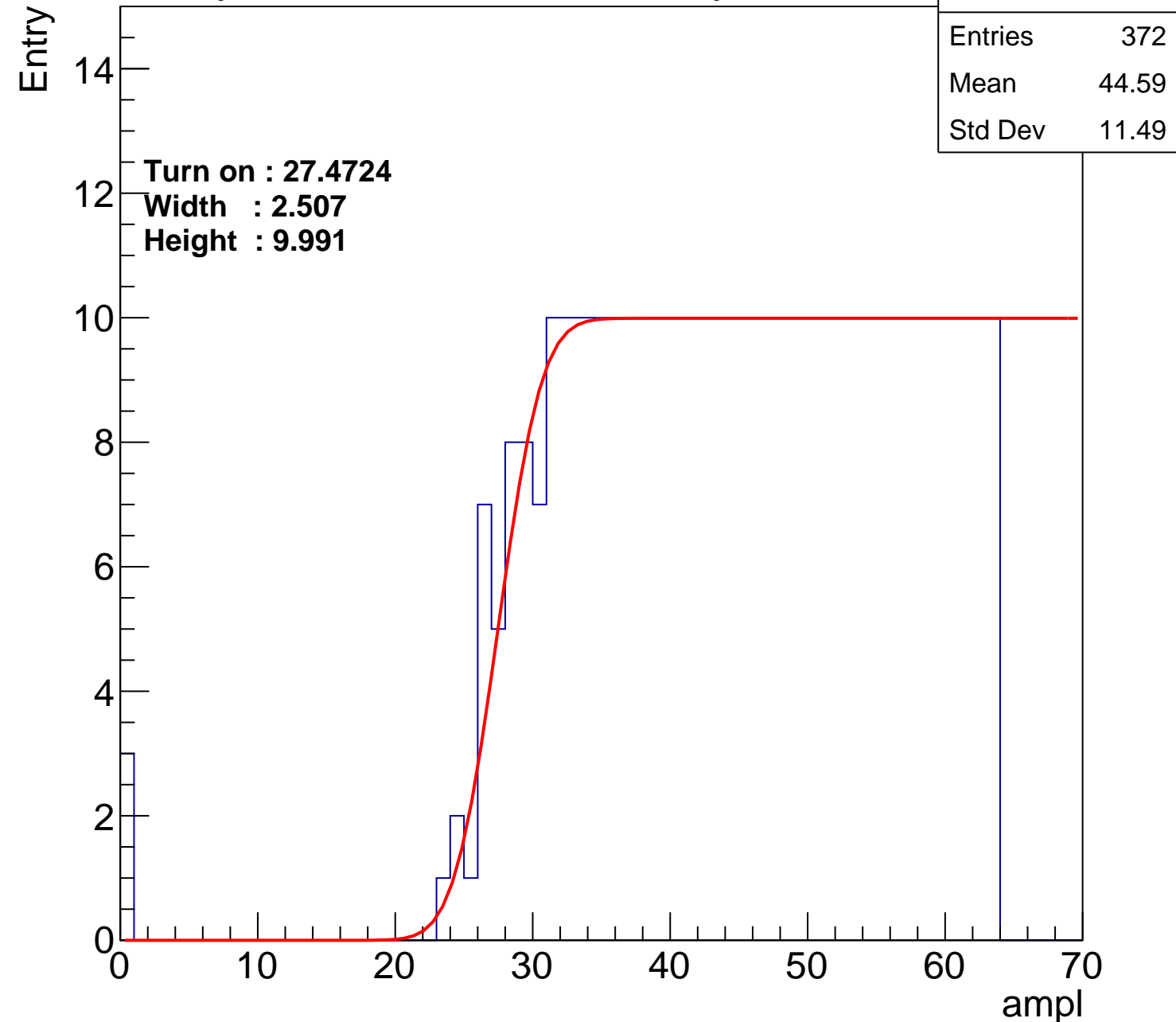
Width : 2.507

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U19-ch127

calib_packv5_042523_0143.root, FC#12, port B1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.59 |
| Std Dev | 11.49 |

Turn on : 27.4724

Width : 2.507

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl

