

B1L102S, U22-ch0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.26
Std Dev	12.07

Turn on : 24.3921

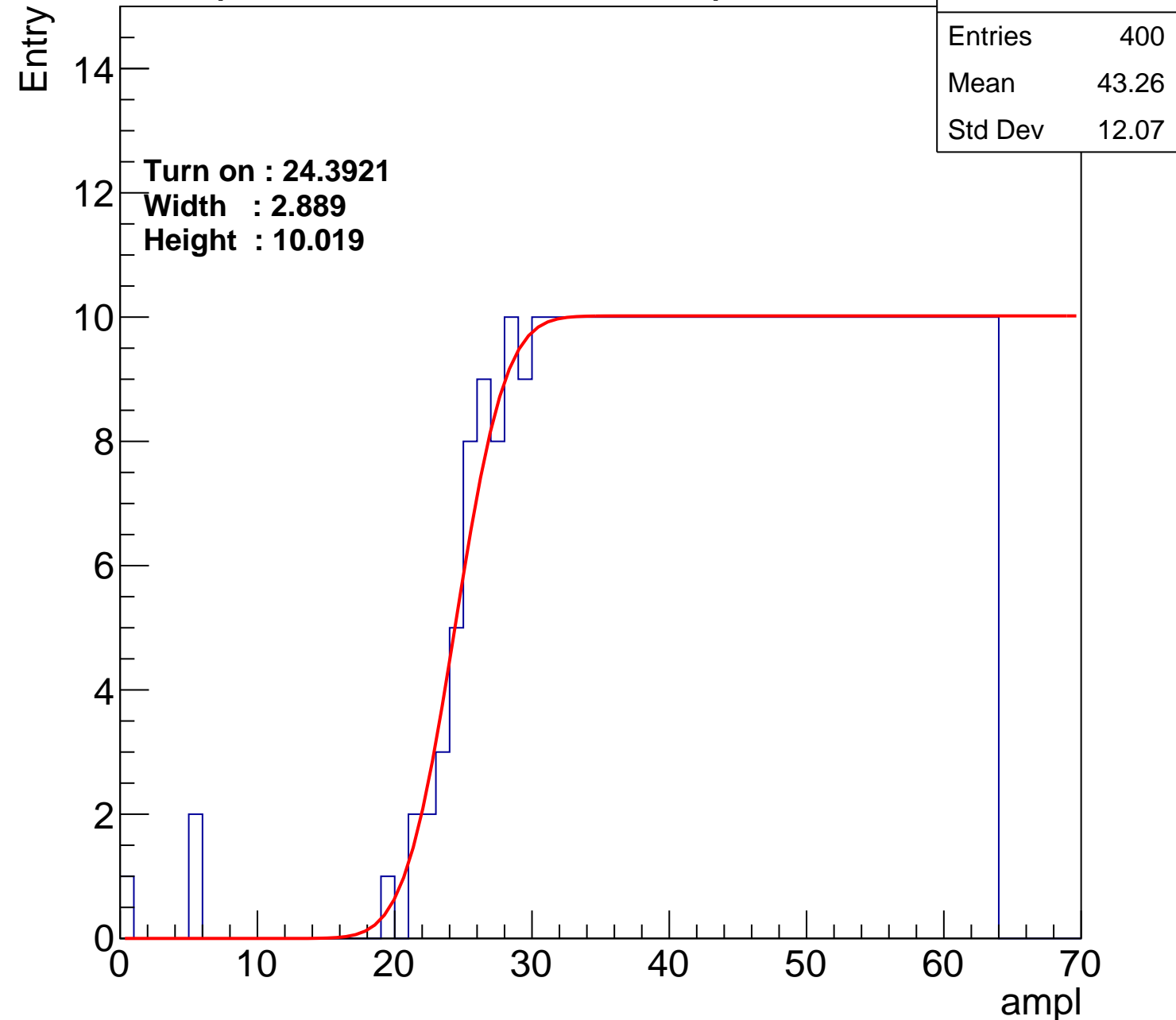
Width : 2.889

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.5696

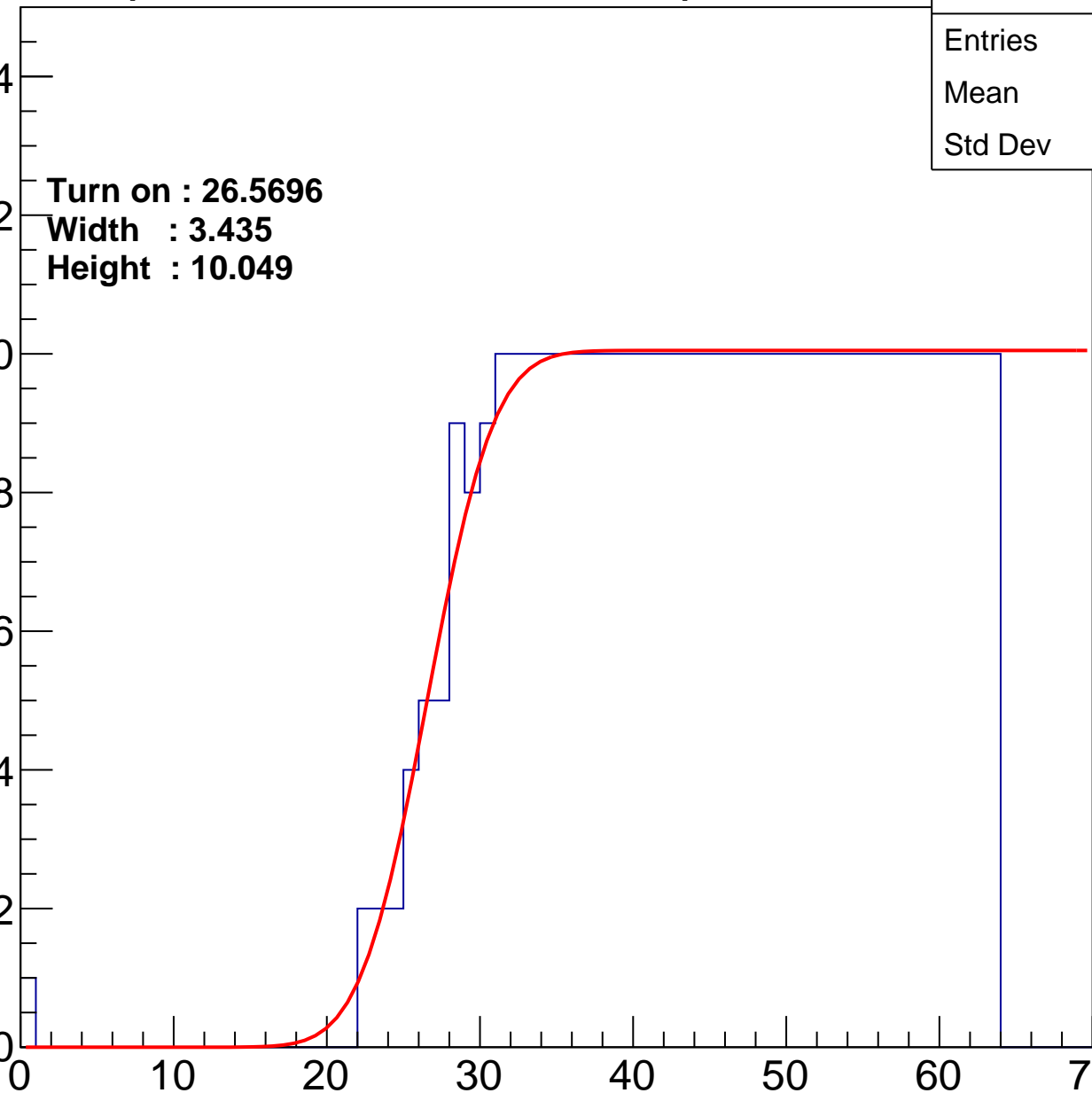
Width : 3.435

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.27
Std Dev	12.11

Turn on : 24.6075

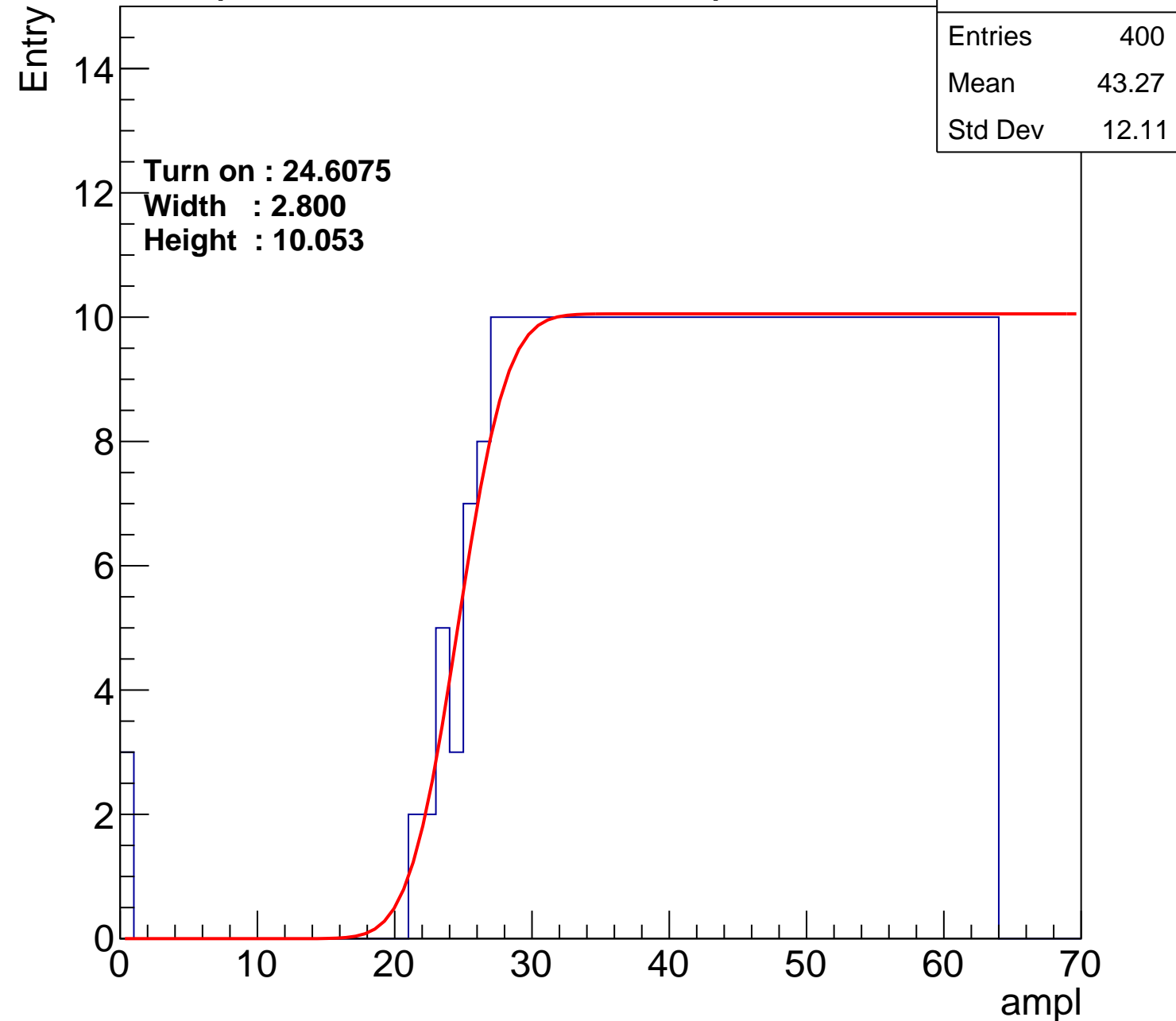
Width : 2.800

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.09
Std Dev	12.49

Turn on : 25.0928

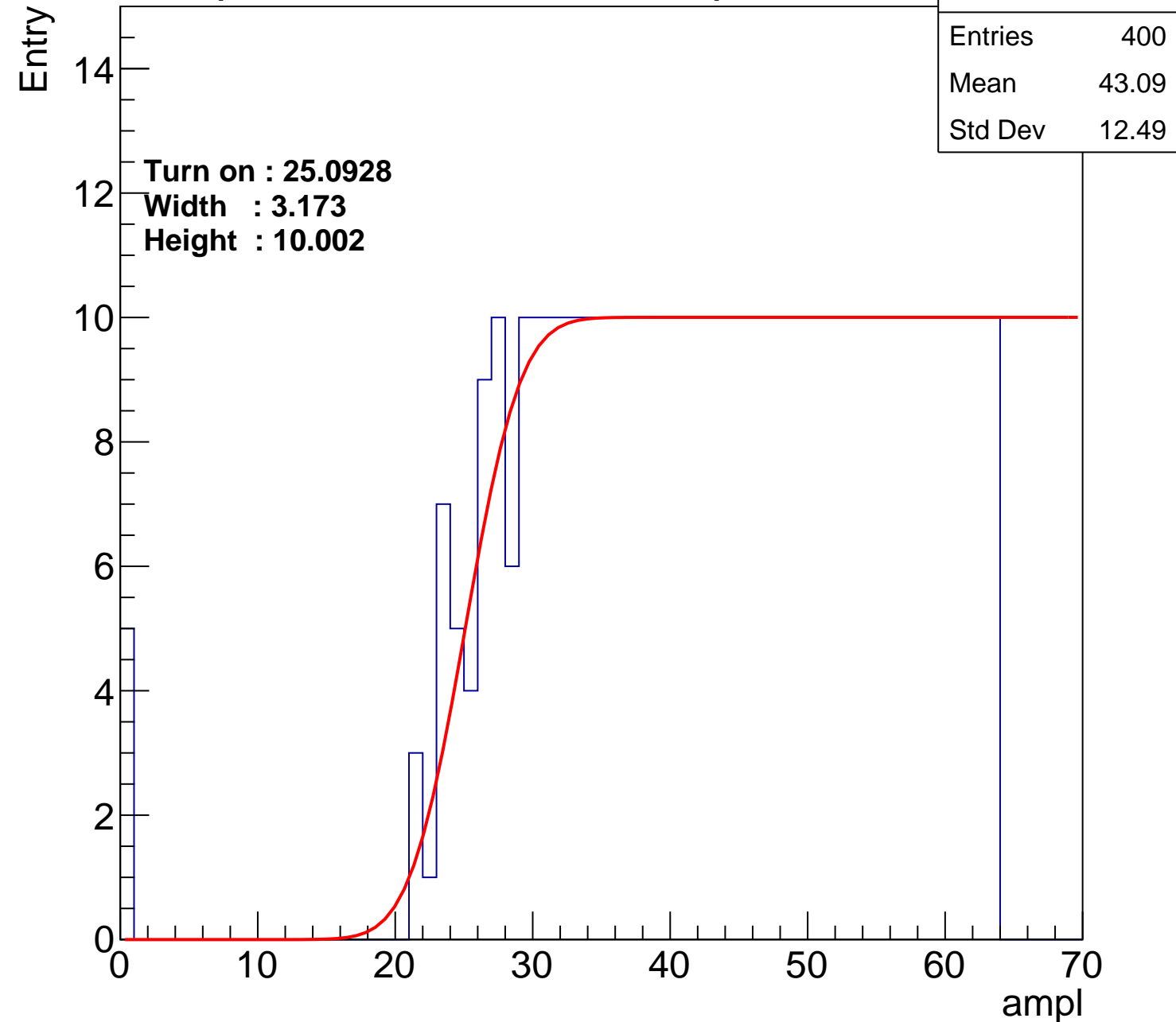
Width : 3.173

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 26.1902

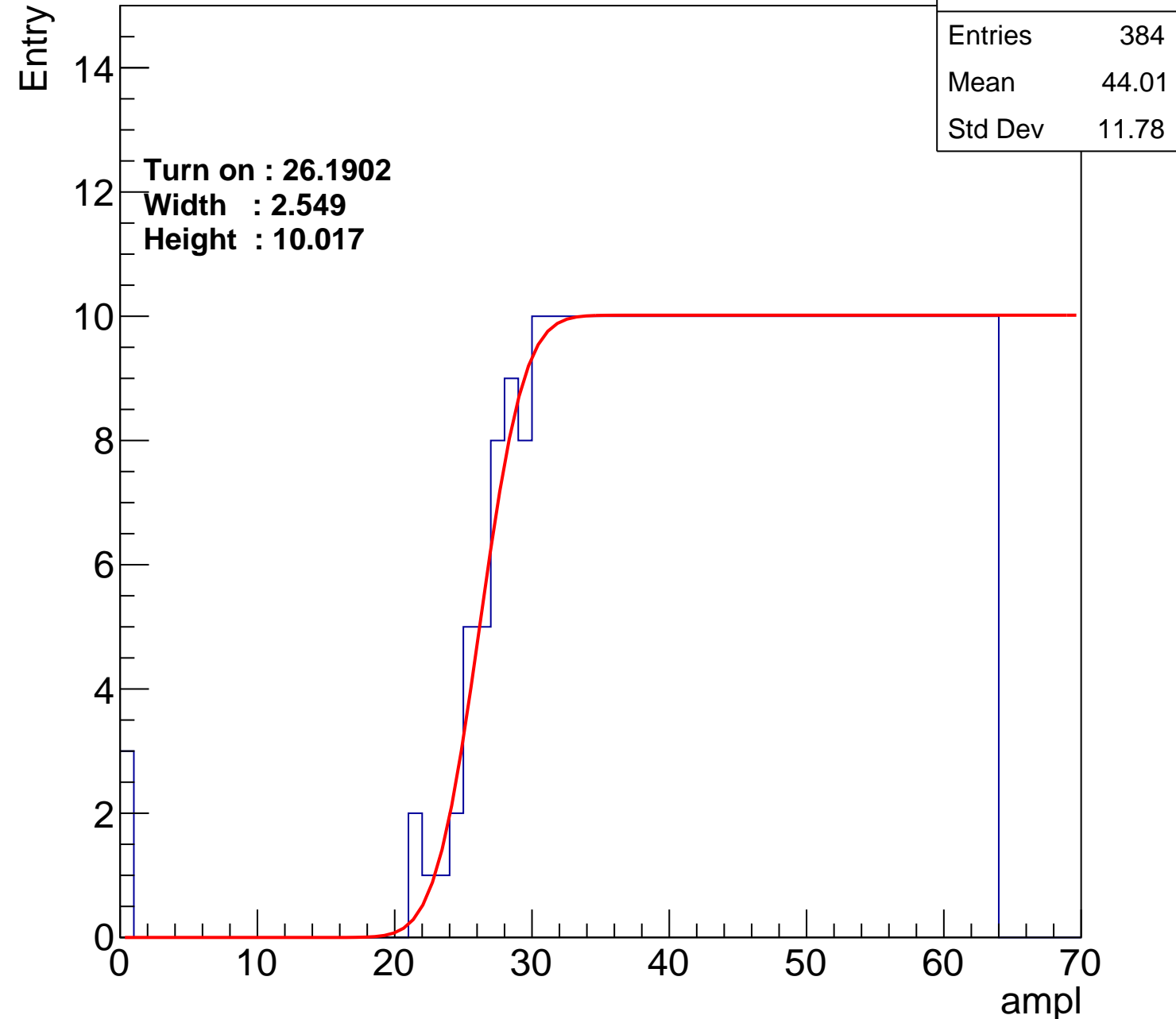
Width : 2.549

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.8
Std Dev	11.58

Turn on : 28.0897

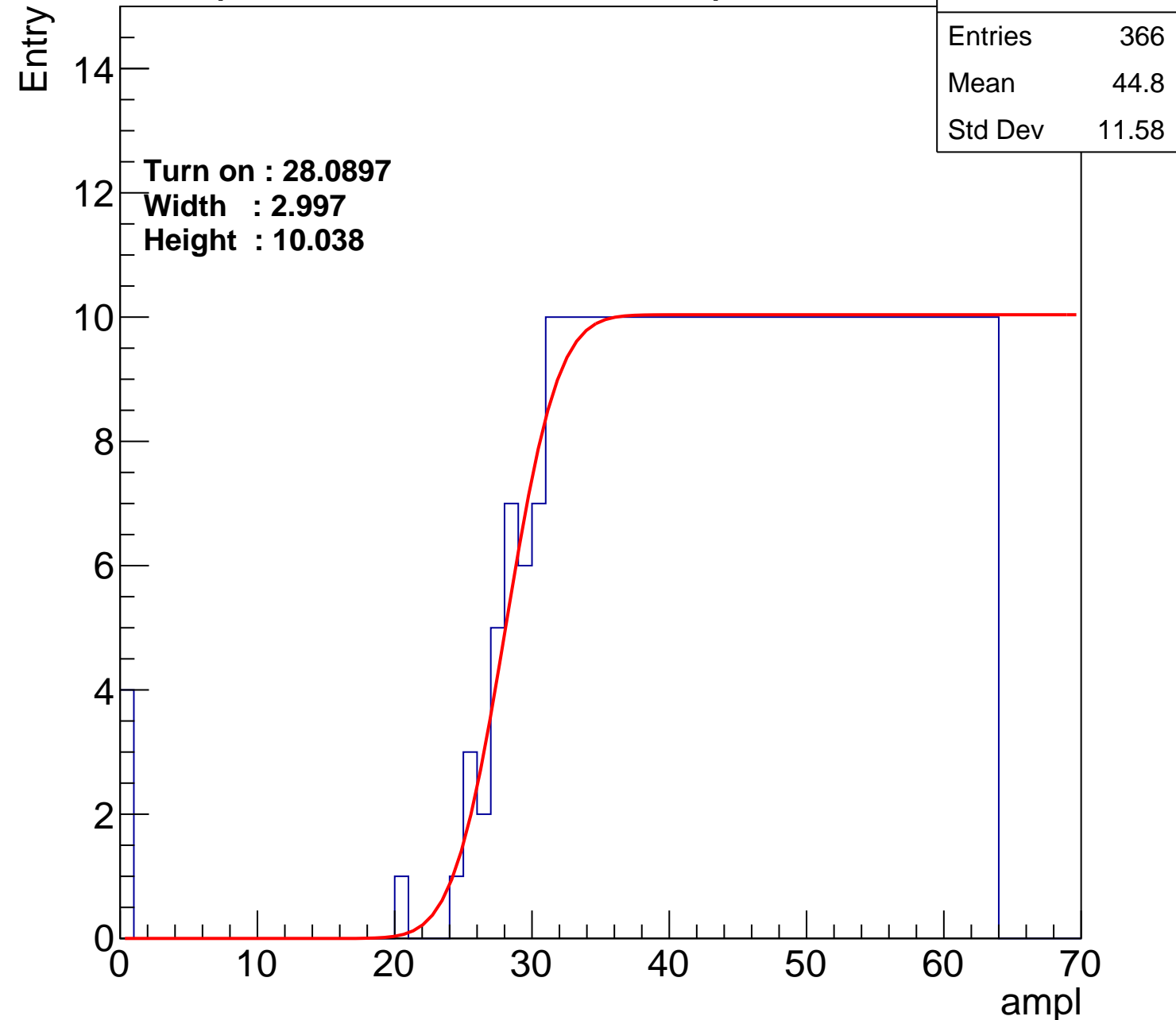
Width : 2.997

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	43.97
Std Dev	12.01

Turn on : 26.6461

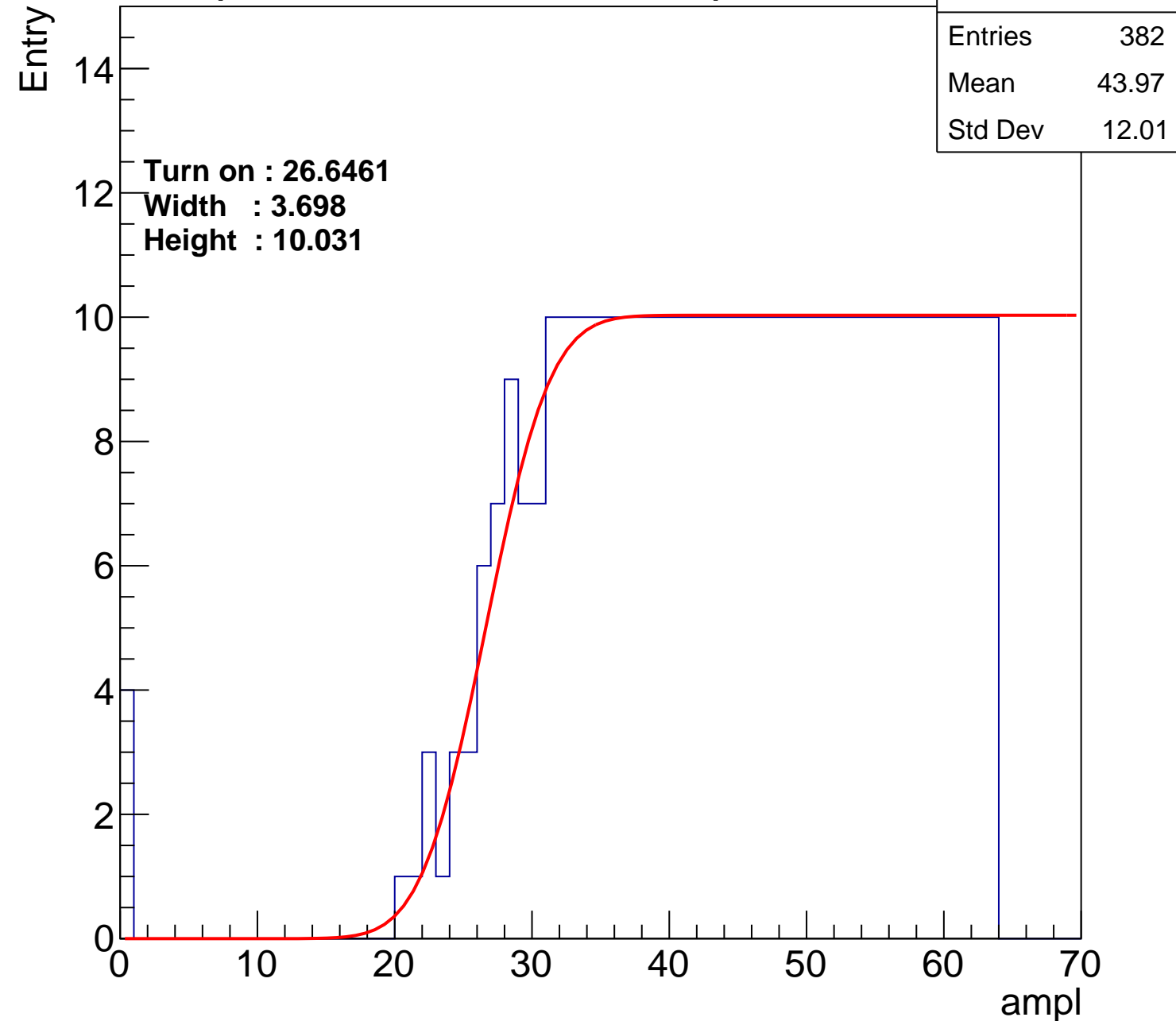
Width : 3.698

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch7

calib_packv5_042523_0143.root, FC#11, port A2

Entries	359
Mean	45.34
Std Dev	10.91

Turn on : 28.4793

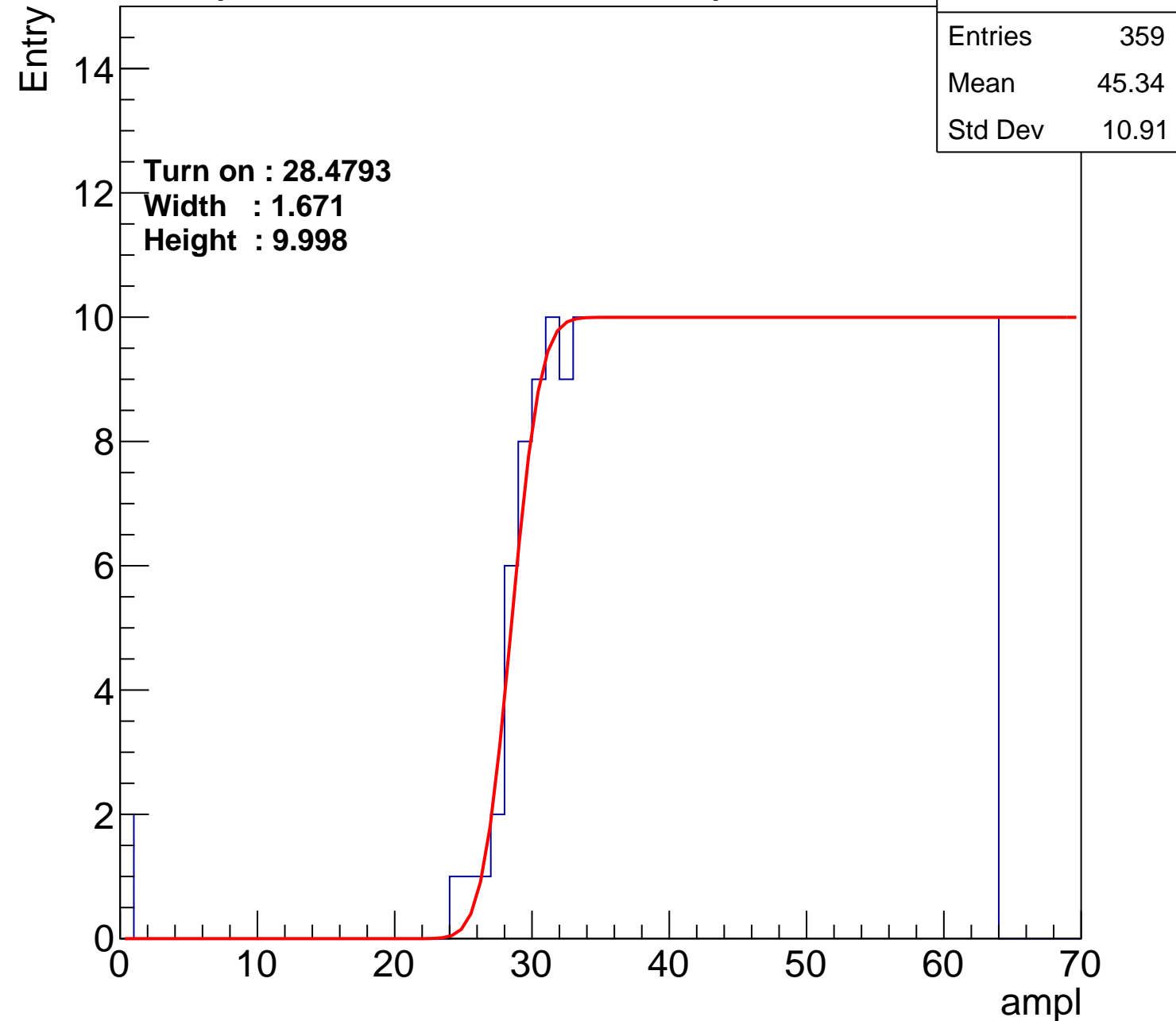
Width : 1.671

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.57
Std Dev	11.26

Turn on : 27.0064

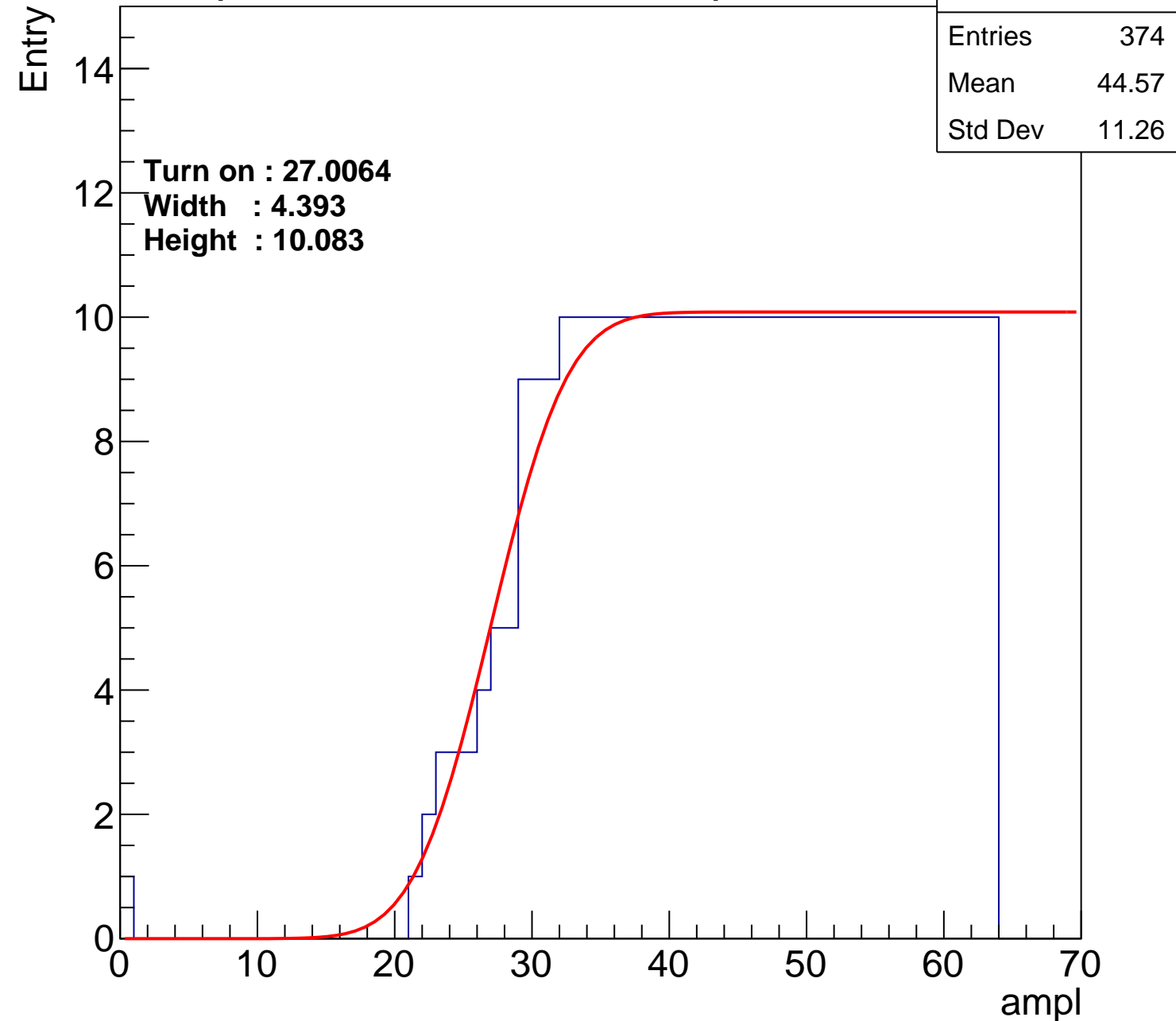
Width : 4.393

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch9

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.5949

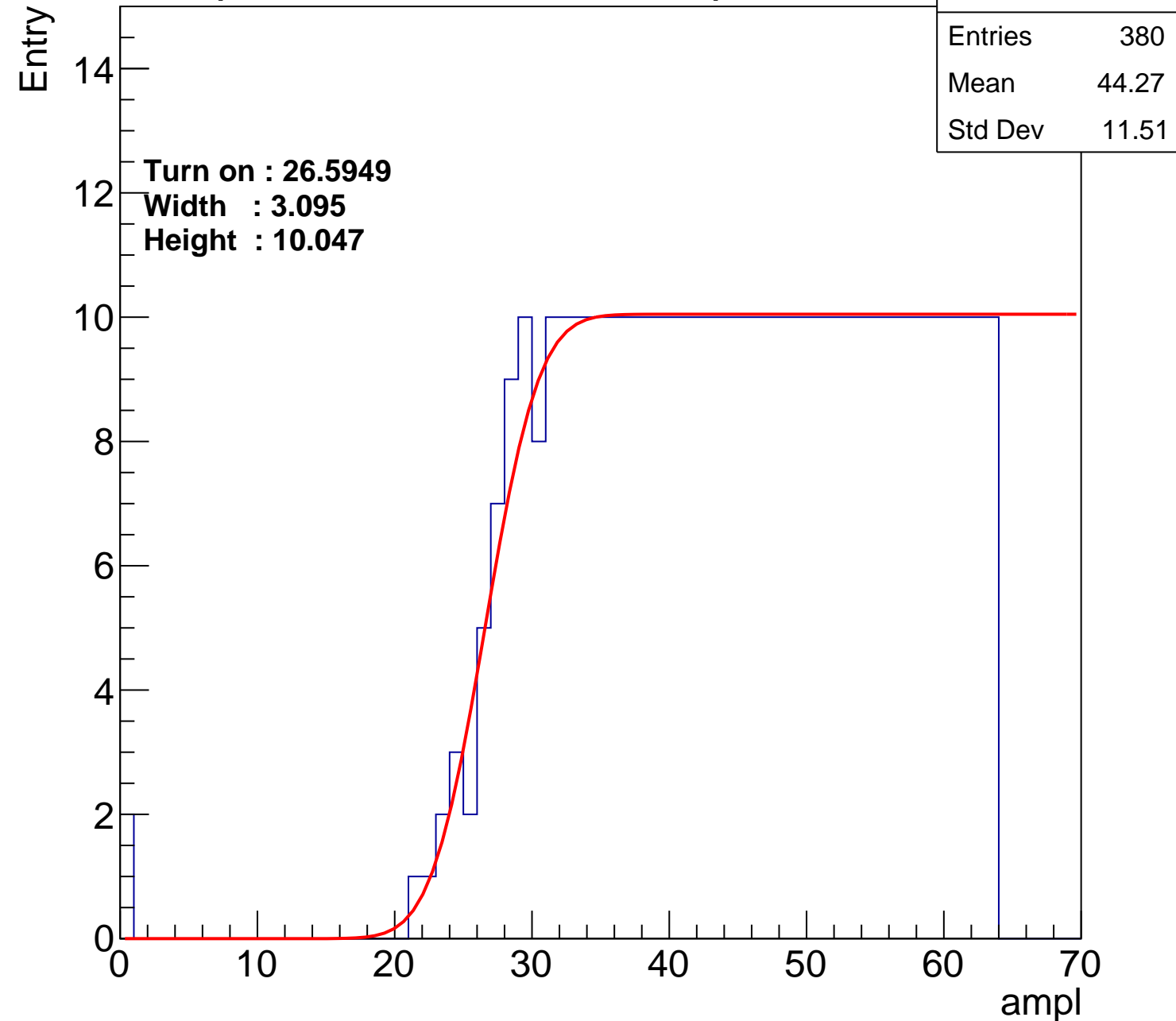
Width : 3.095

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch10

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.7
Std Dev	11.25

Turn on : 27.2152

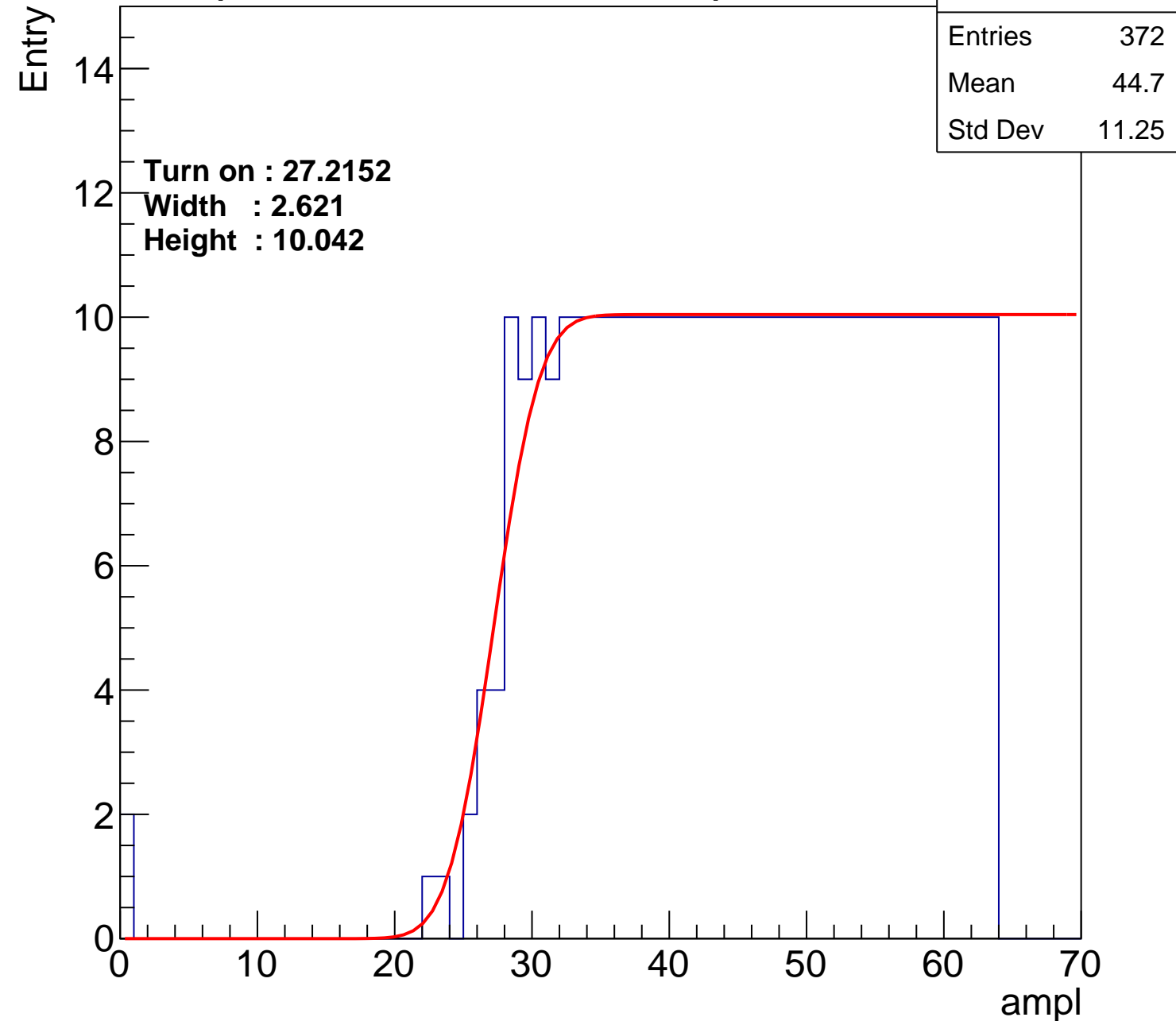
Width : 2.621

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch11

calib_packv5_042523_0143.root, FC#11, port A2

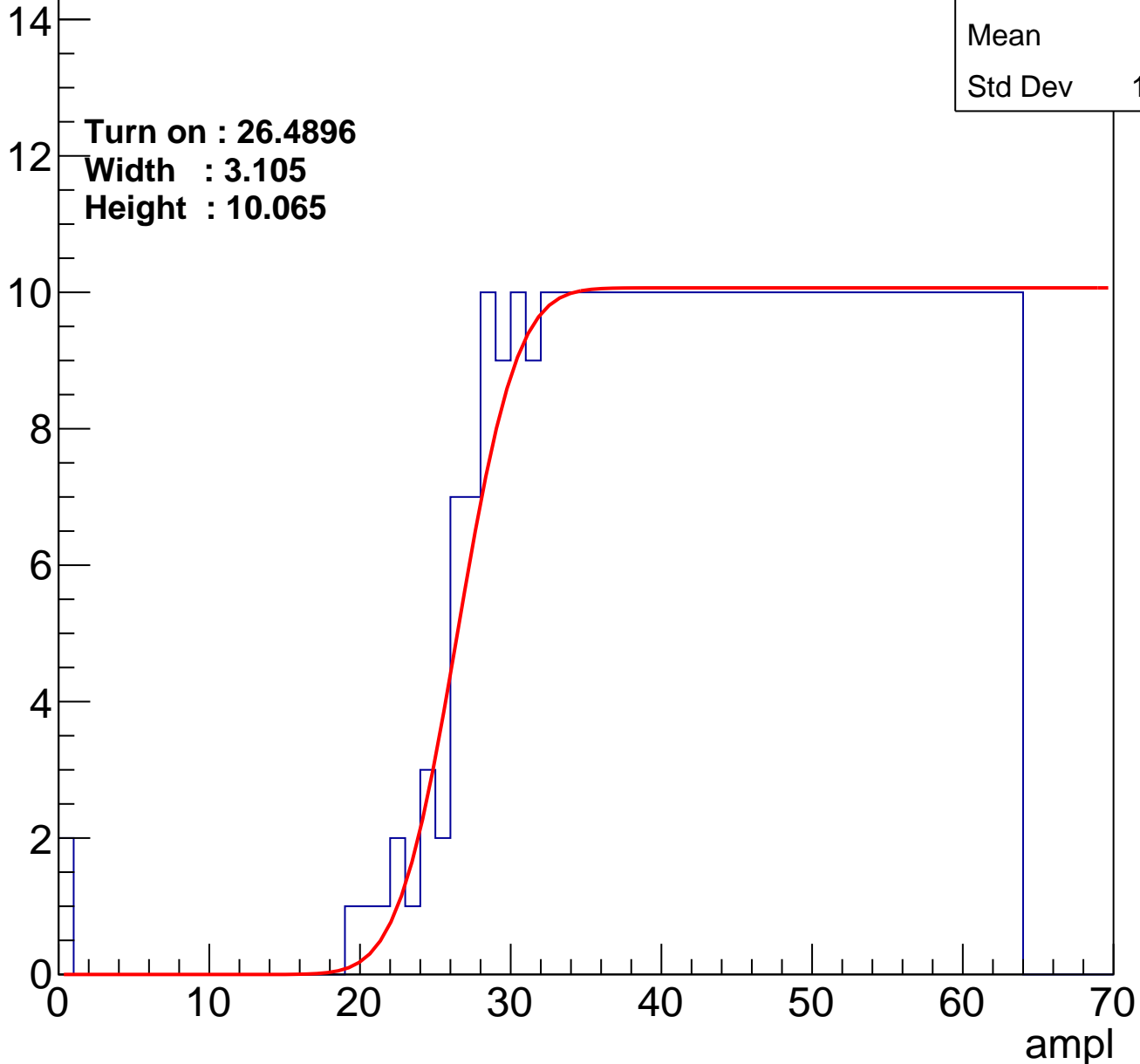
Entries	385
Mean	44
Std Dev	11.68

Turn on : 26.4896

Width : 3.105

Height : 10.065

Entry



B1L102S, U22-ch12

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.25
Std Dev	11.84

Turn on : 26.8054

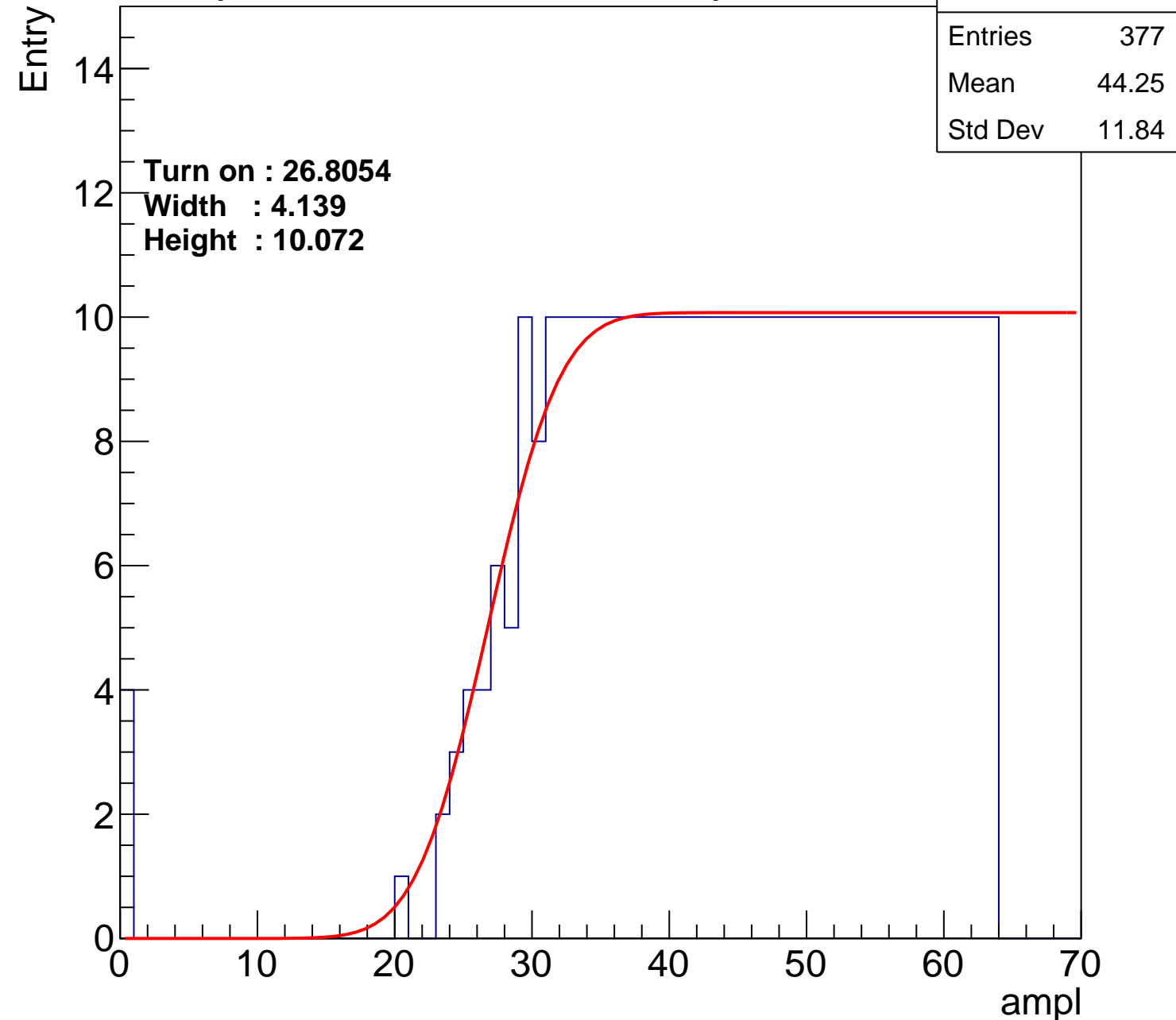
Width : 4.139

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch13

calib_packv5_042523_0143.root, FC#11, port A2

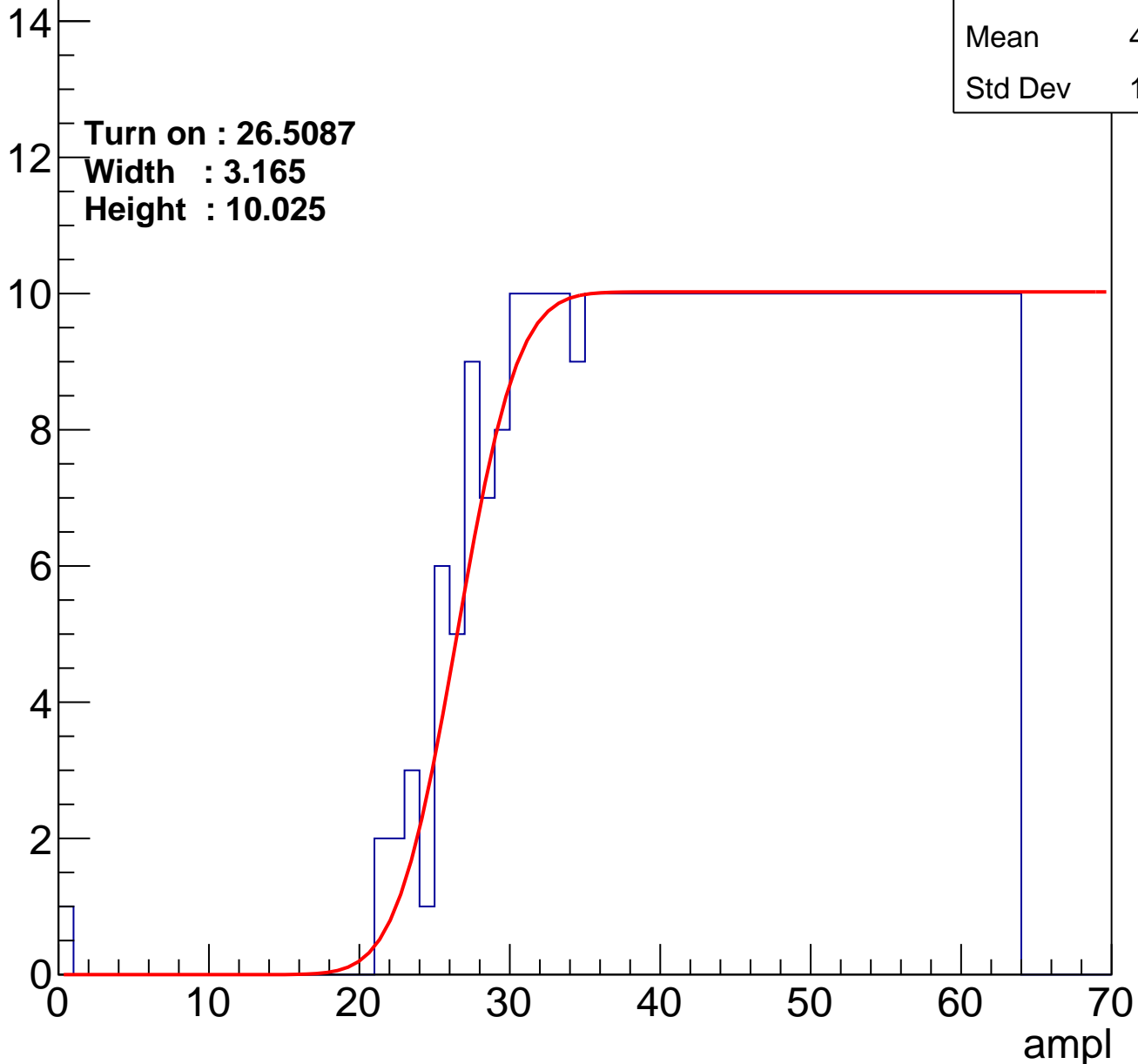
Entries	383
Mean	44.14
Std Dev	11.47

Turn on : 26.5087

Width : 3.165

Height : 10.025

Entry



B1L102S, U22-ch14

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.07
Std Dev	11.62

Turn on : 26.0178

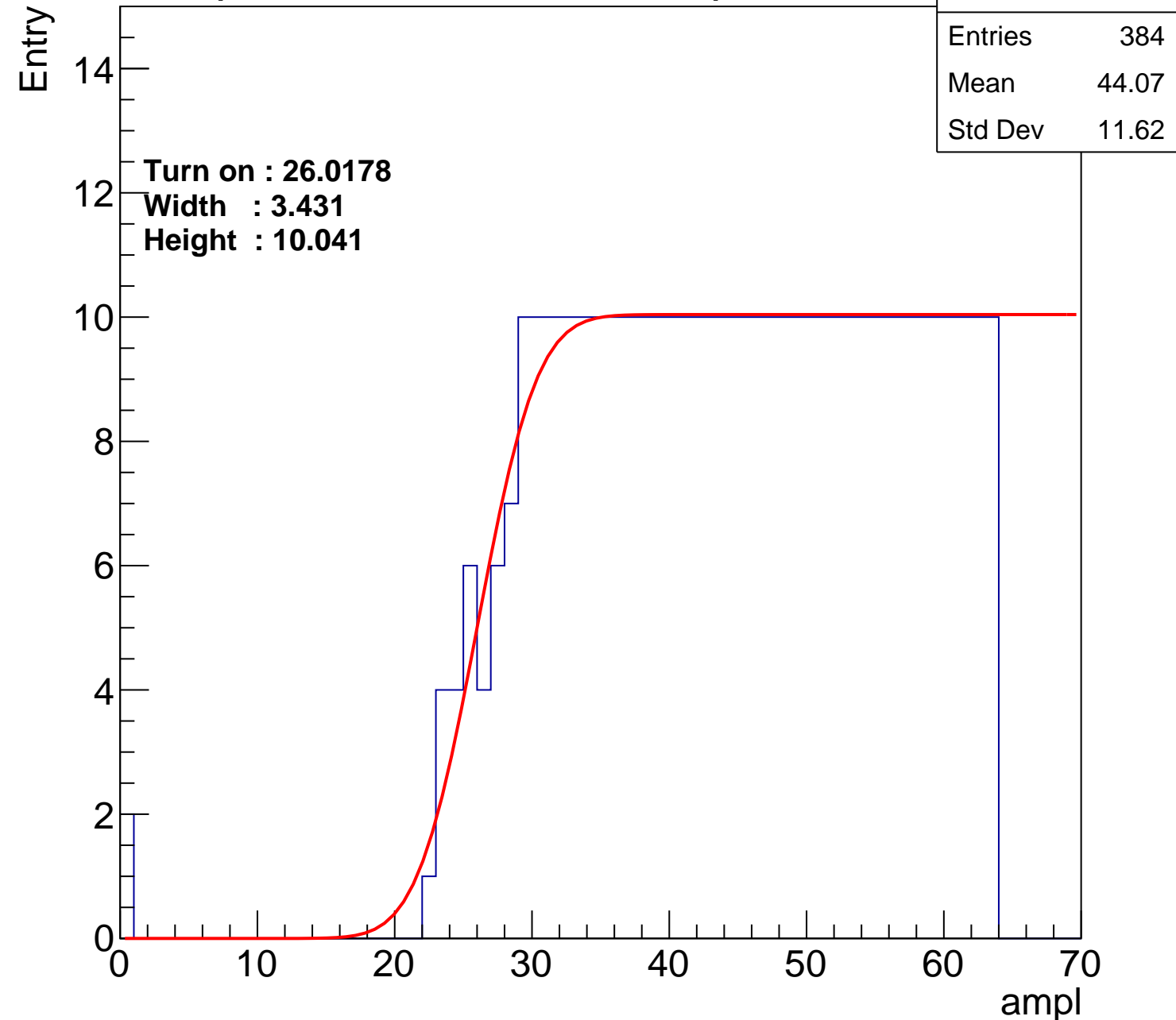
Width : 3.431

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch15

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 26.1840

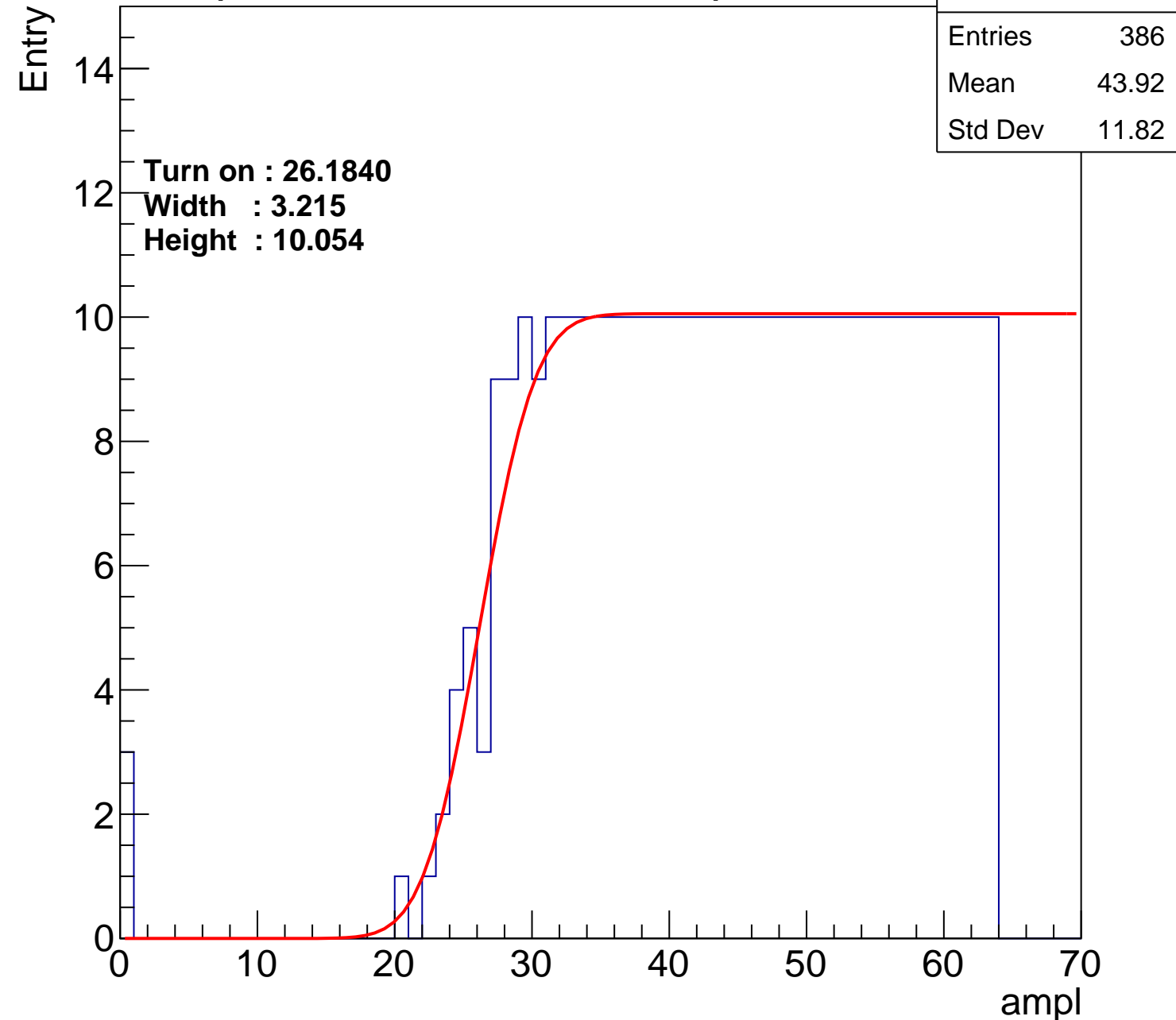
Width : 3.215

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch16

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 25.9767

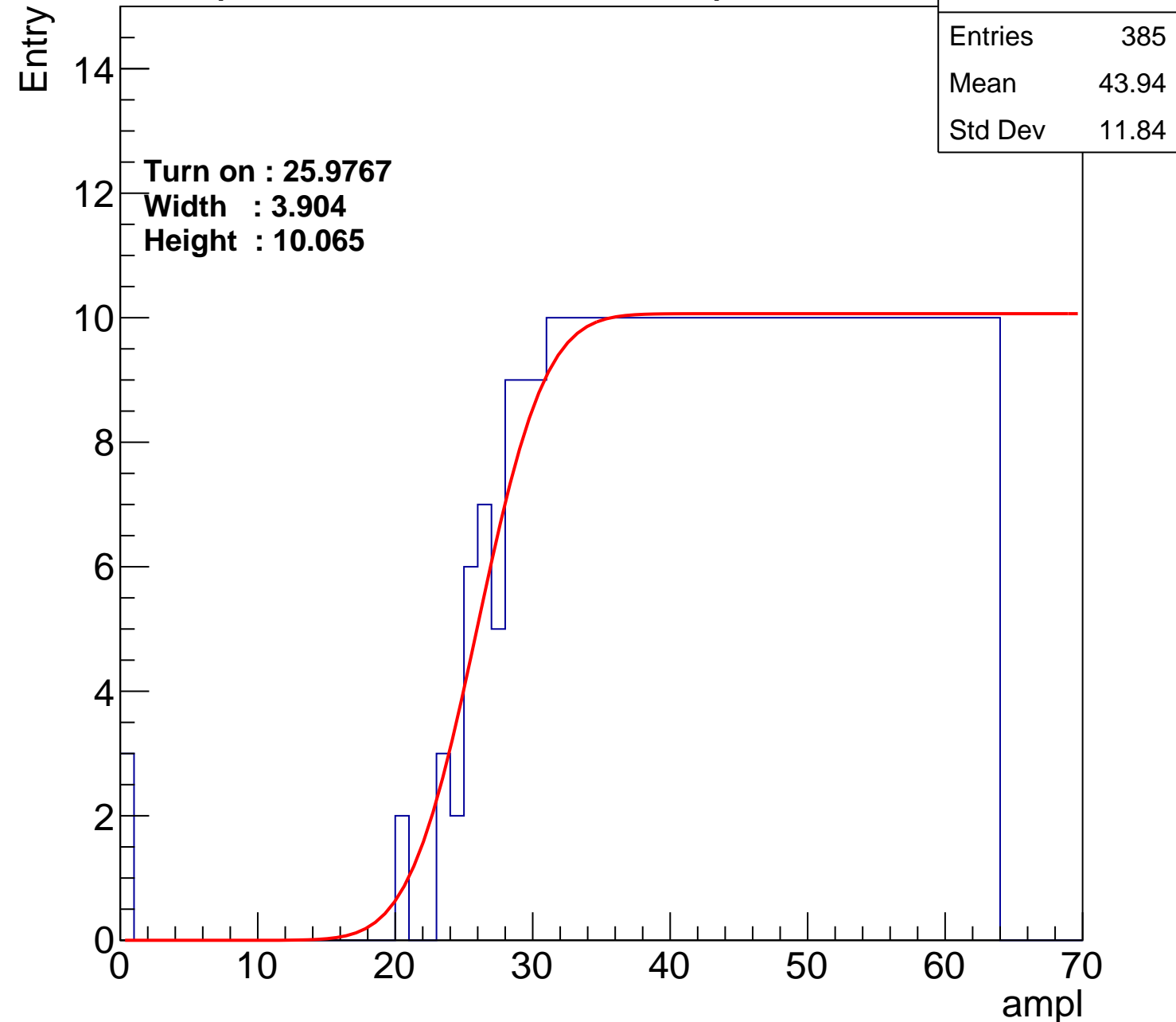
Width : 3.904

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch17

calib_packv5_042523_0143.root, FC#11, port A2

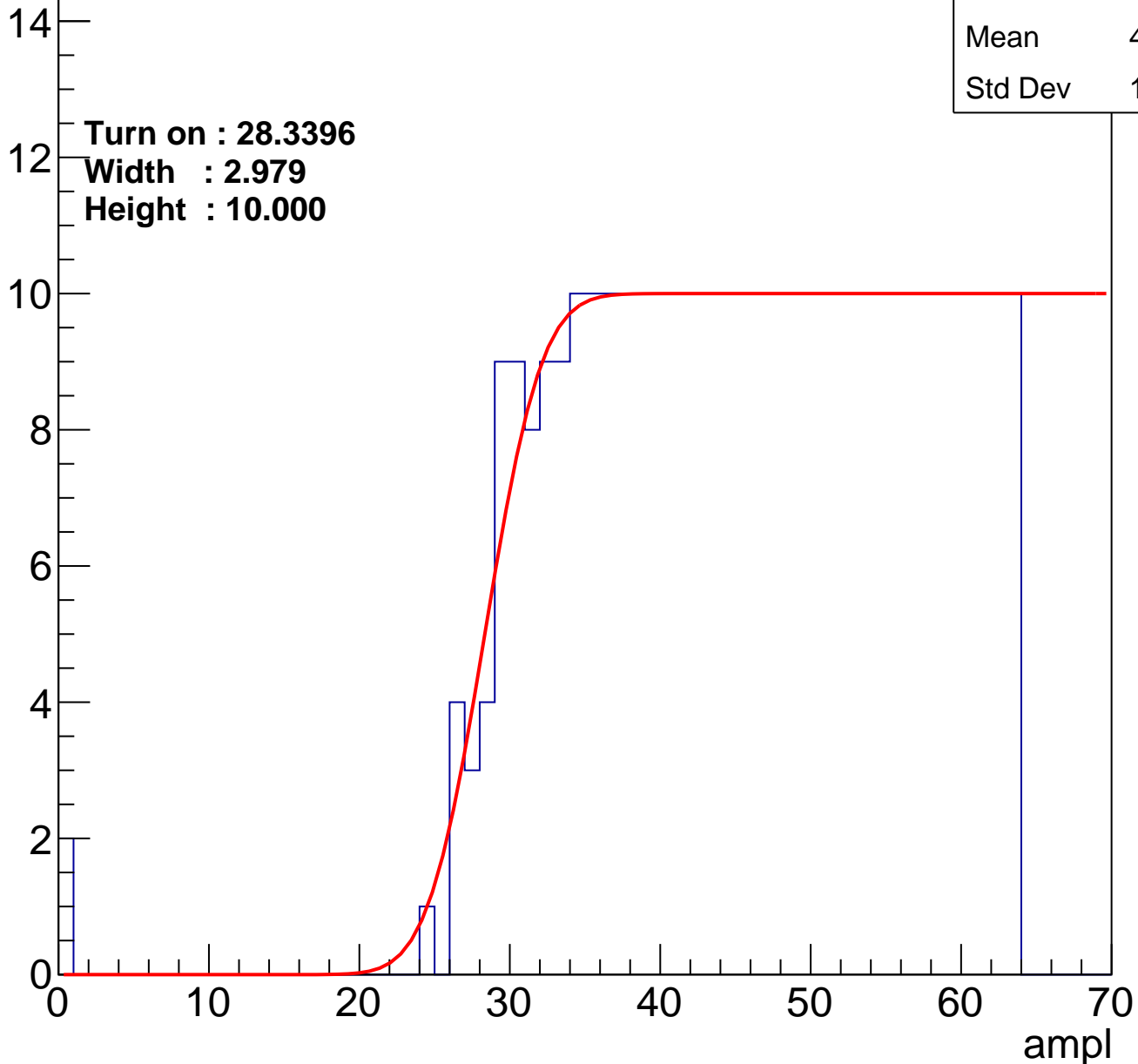
Entries	358
Mean	45.35
Std Dev	10.94

Turn on : 28.3396

Width : 2.979

Height : 10.000

Entry

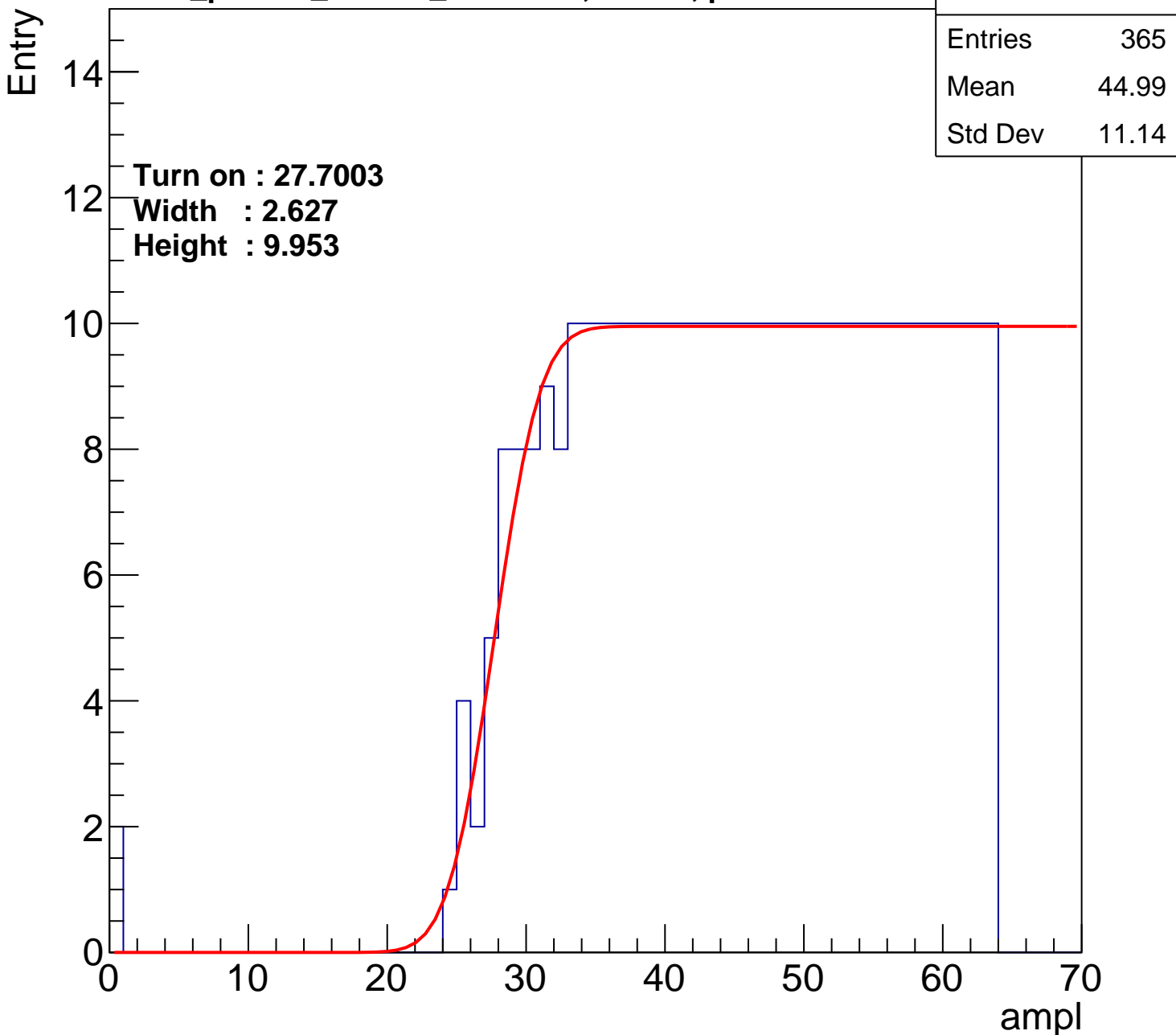


calib_packv5_042523_0143.root, FC#11, port A2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.99
Std Dev	11.14

Height : 9.953



B1L102S, U22-ch19

calib_packv5_042523_0143.root, FC#11, port A2

Entries	406
Mean	42.86
Std Dev	12.49

Turn on : 23.7305

Width : 3.387

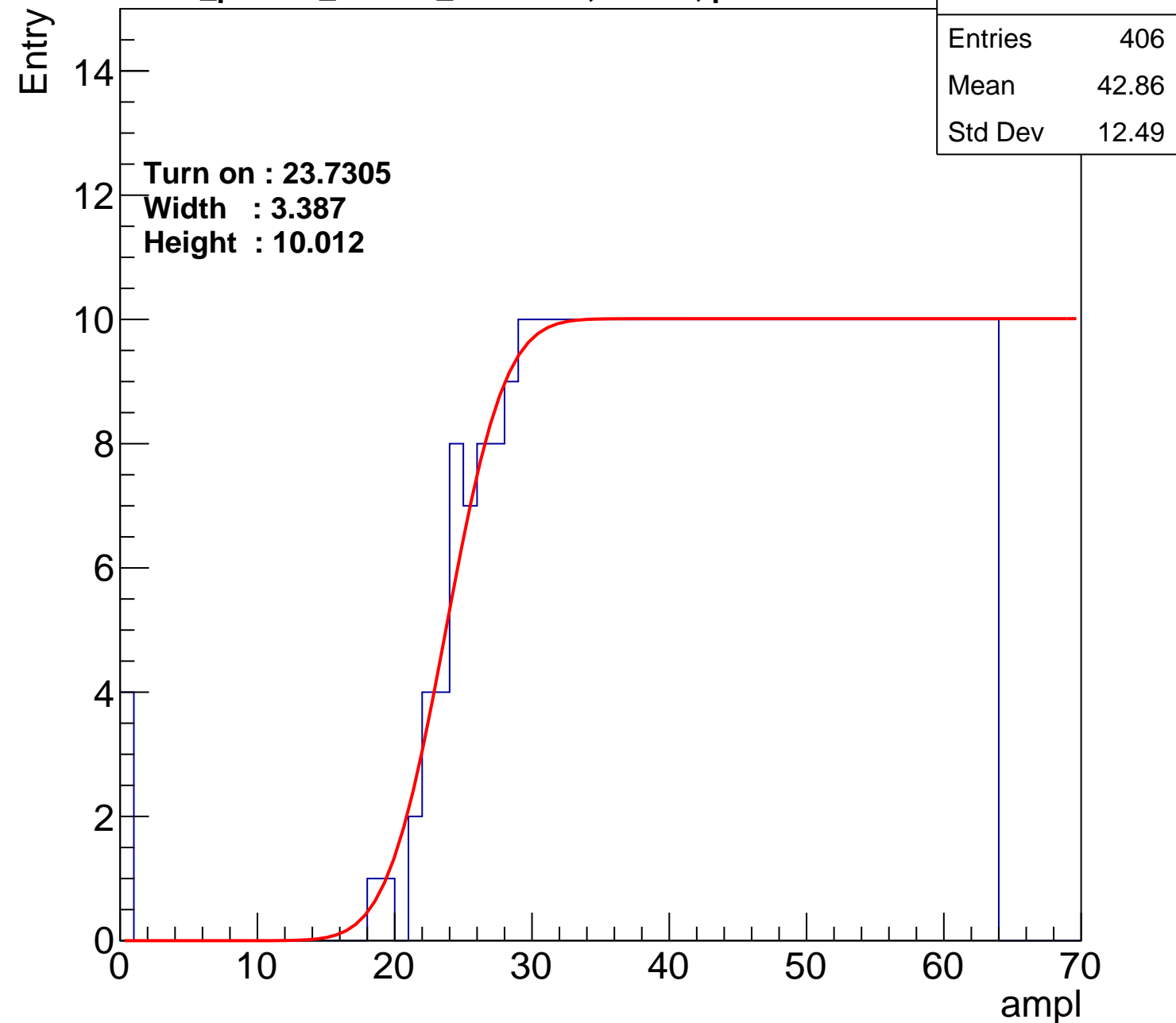
Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch20

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.36
Std Dev	11.34

Turn on : 26.7143

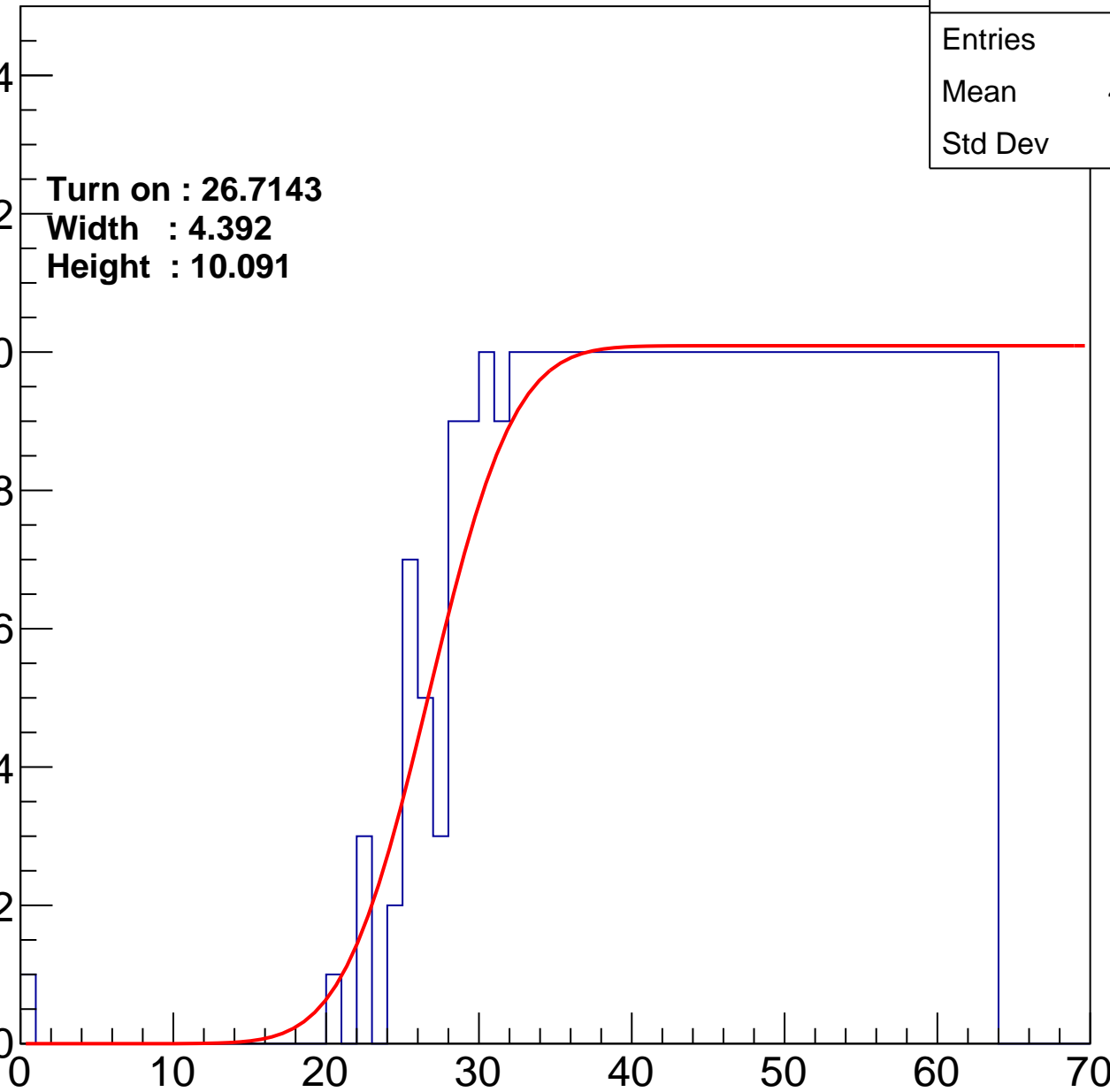
Width : 4.392

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch21

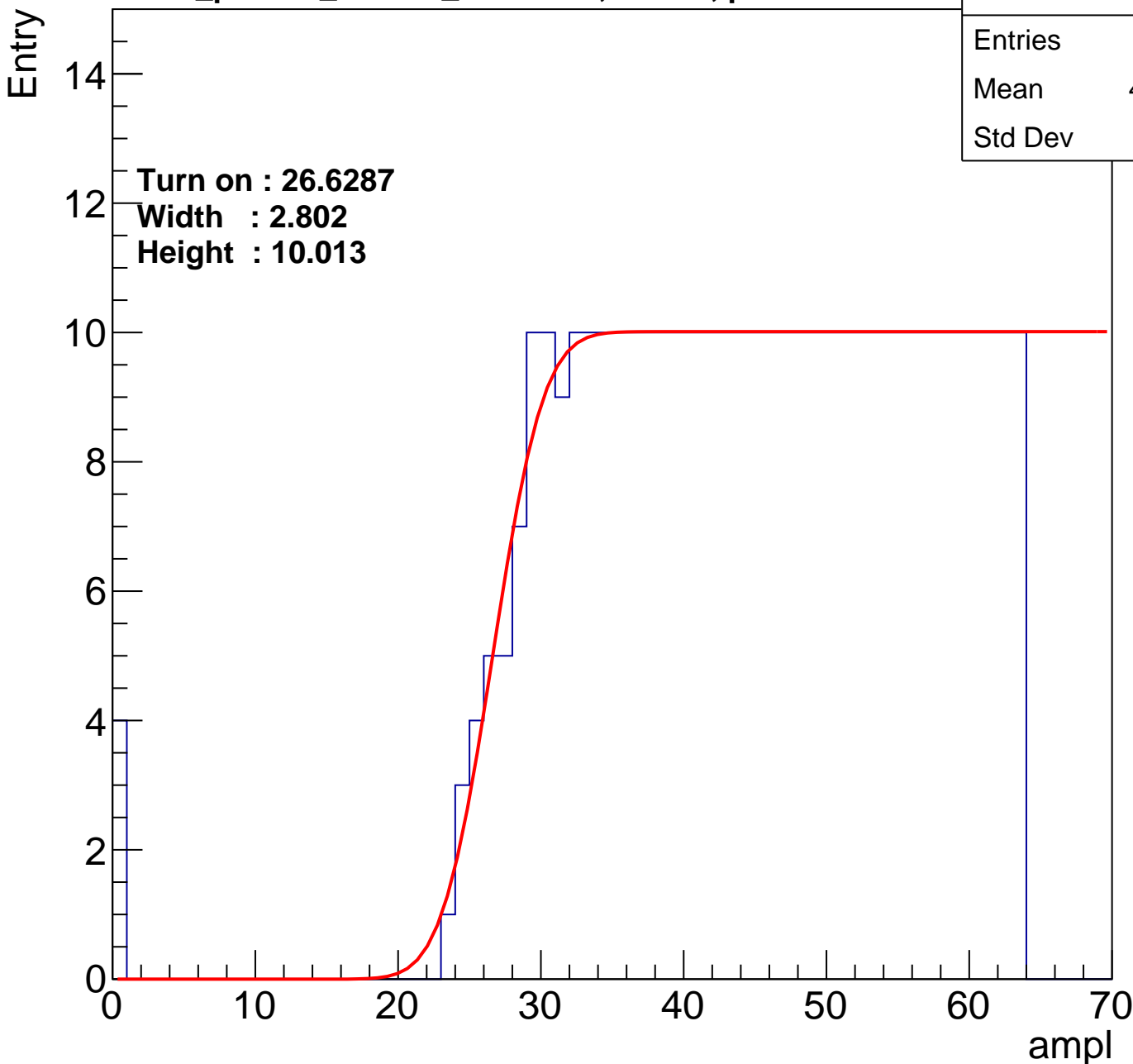
calib_packv5_042523_0143.root, FC#11, port A2

Turn on : 26.6287

Width : 2.802

Height : 10.013

Entries	378
Mean	44.25
Std Dev	11.8



B1L102S, U22-ch22

calib_packv5_042523_0143.root, FC#11, port A2

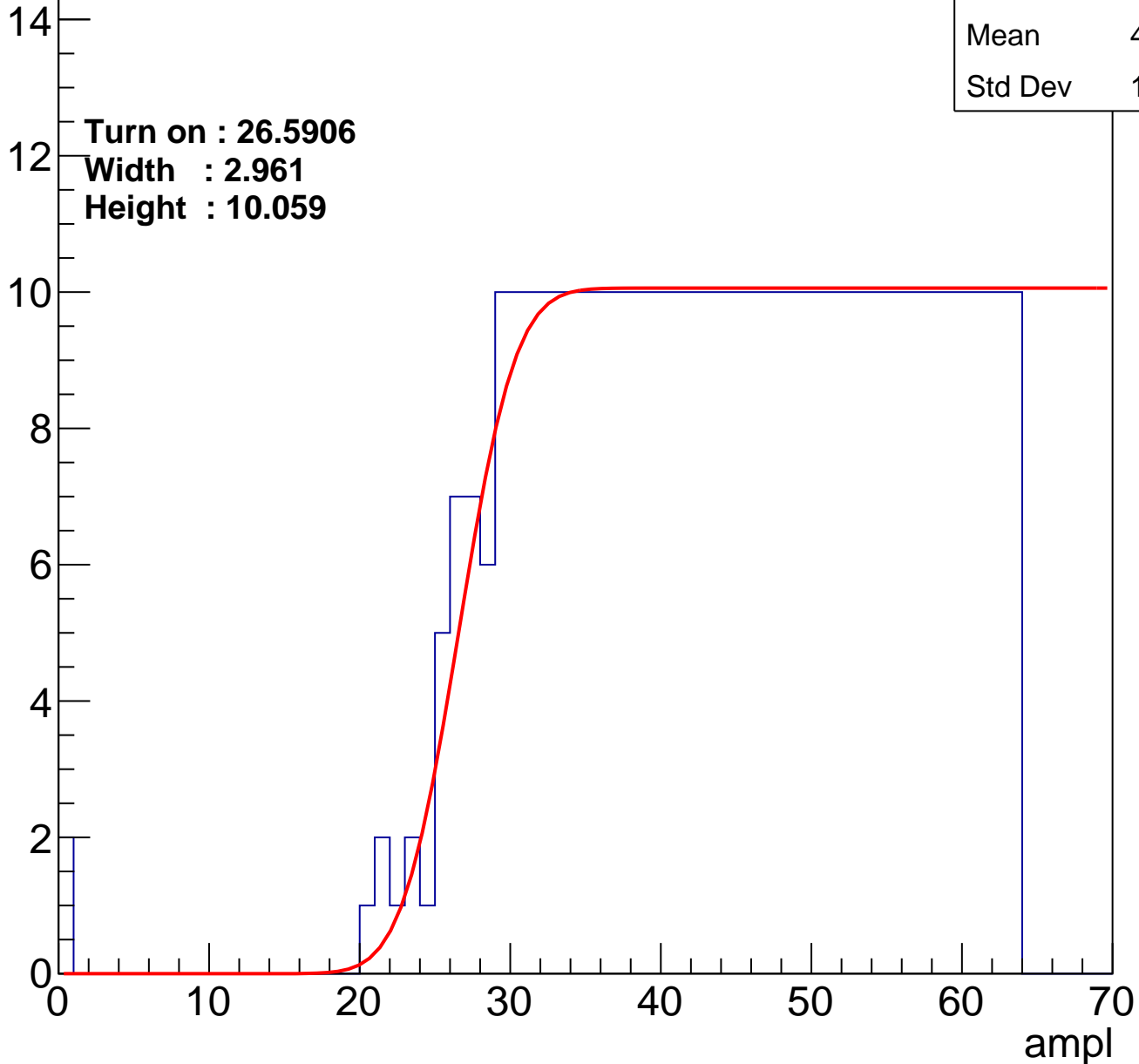
Entries	384
Mean	44.06
Std Dev	11.64

Turn on : 26.5906

Width : 2.961

Height : 10.059

Entry



B1L102S, U22-ch23

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.27
Std Dev	12.16

Turn on : 24.7622

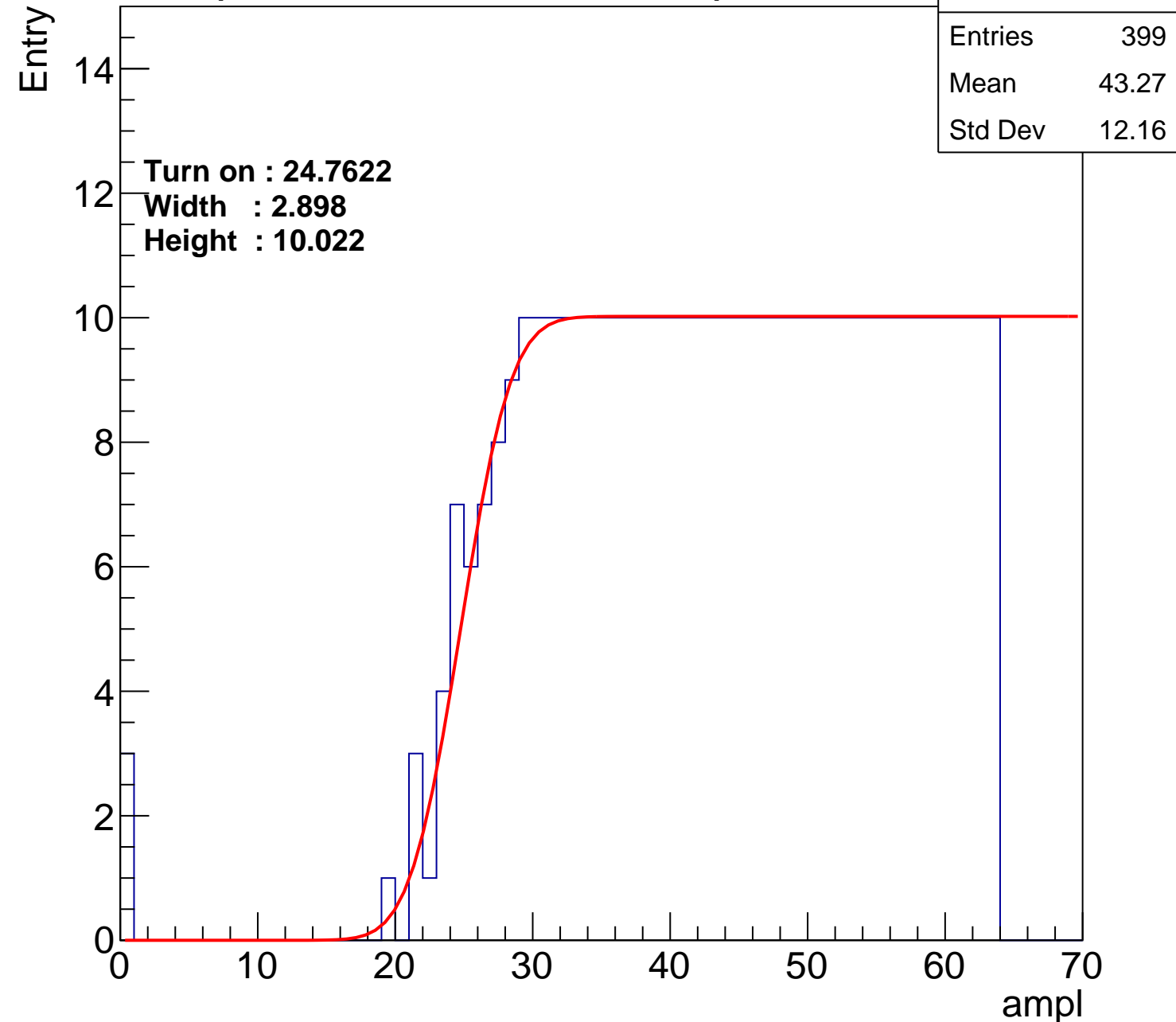
Width : 2.898

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch24

calib_packv5_042523_0143.root, FC#11, port A2

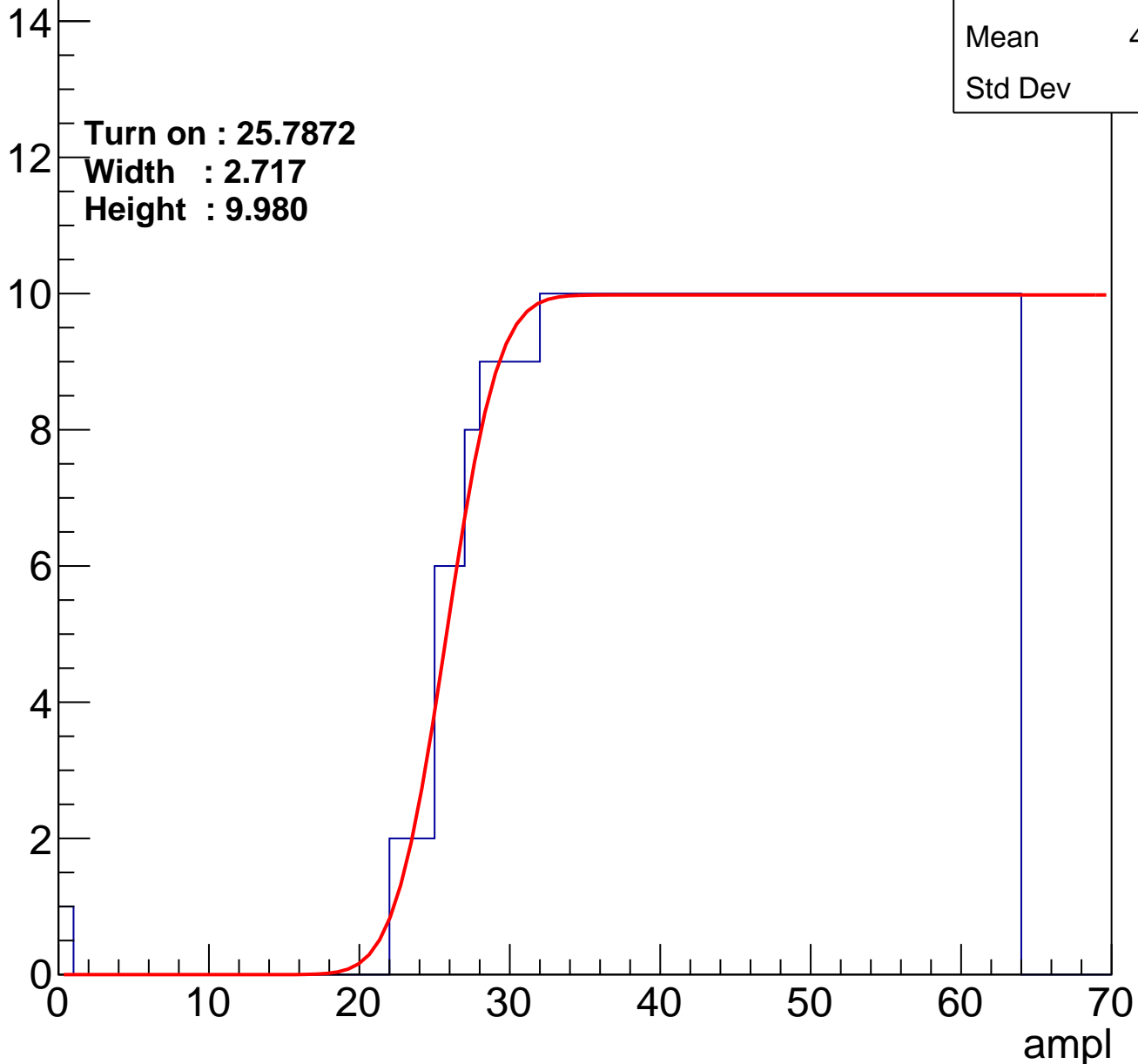
Entries	383
Mean	44.18
Std Dev	11.4

Turn on : 25.7872

Width : 2.717

Height : 9.980

Entry



B1L102S, U22-ch25

calib_packv5_042523_0143.root, FC#11, port A2

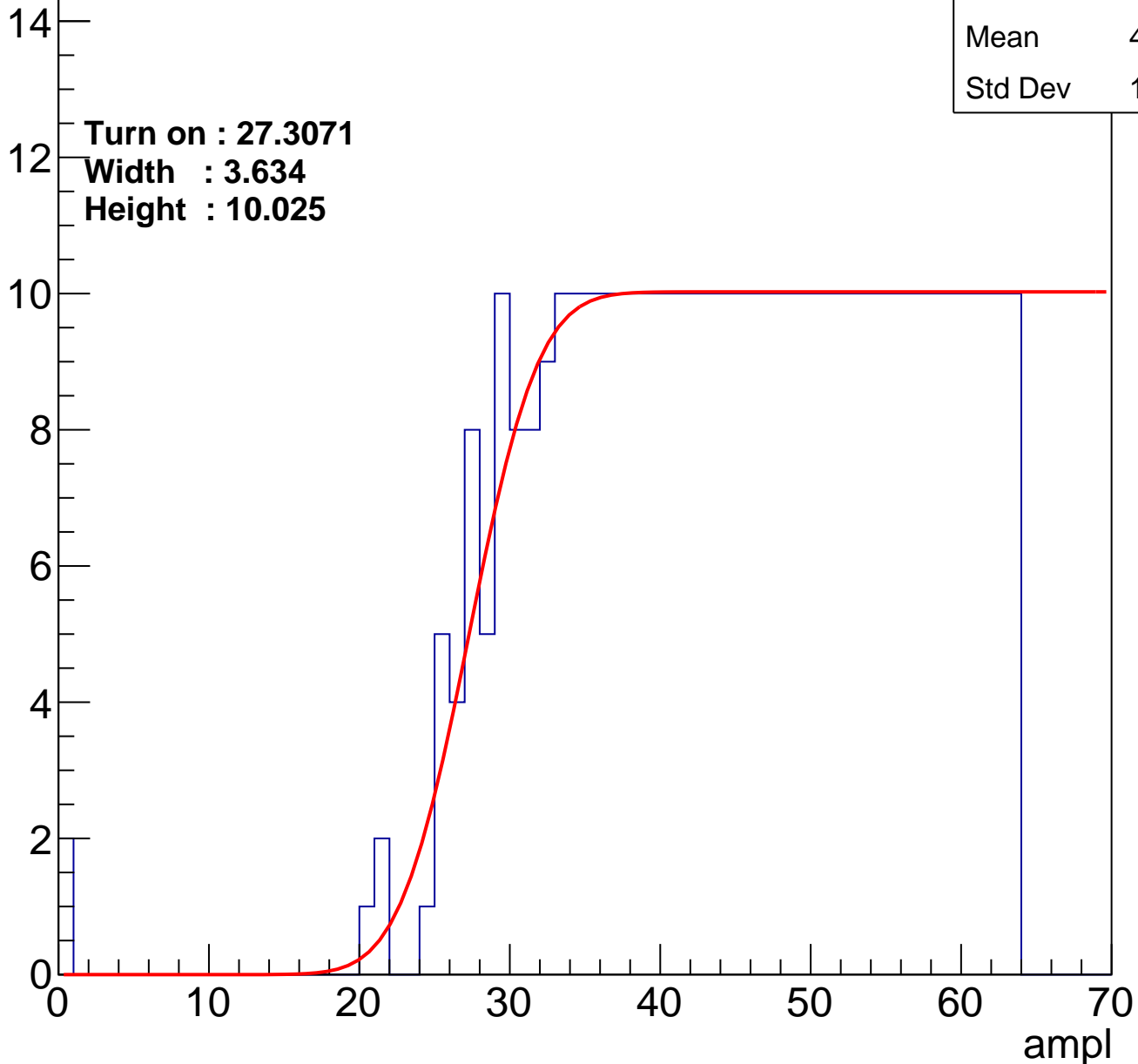
Entries	373
Mean	44.55
Std Dev	11.43

Turn on : 27.3071

Width : 3.634

Height : 10.025

Entry



B1L102S, U22-ch26

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.24
Std Dev	11.43

Turn on : 26.9478

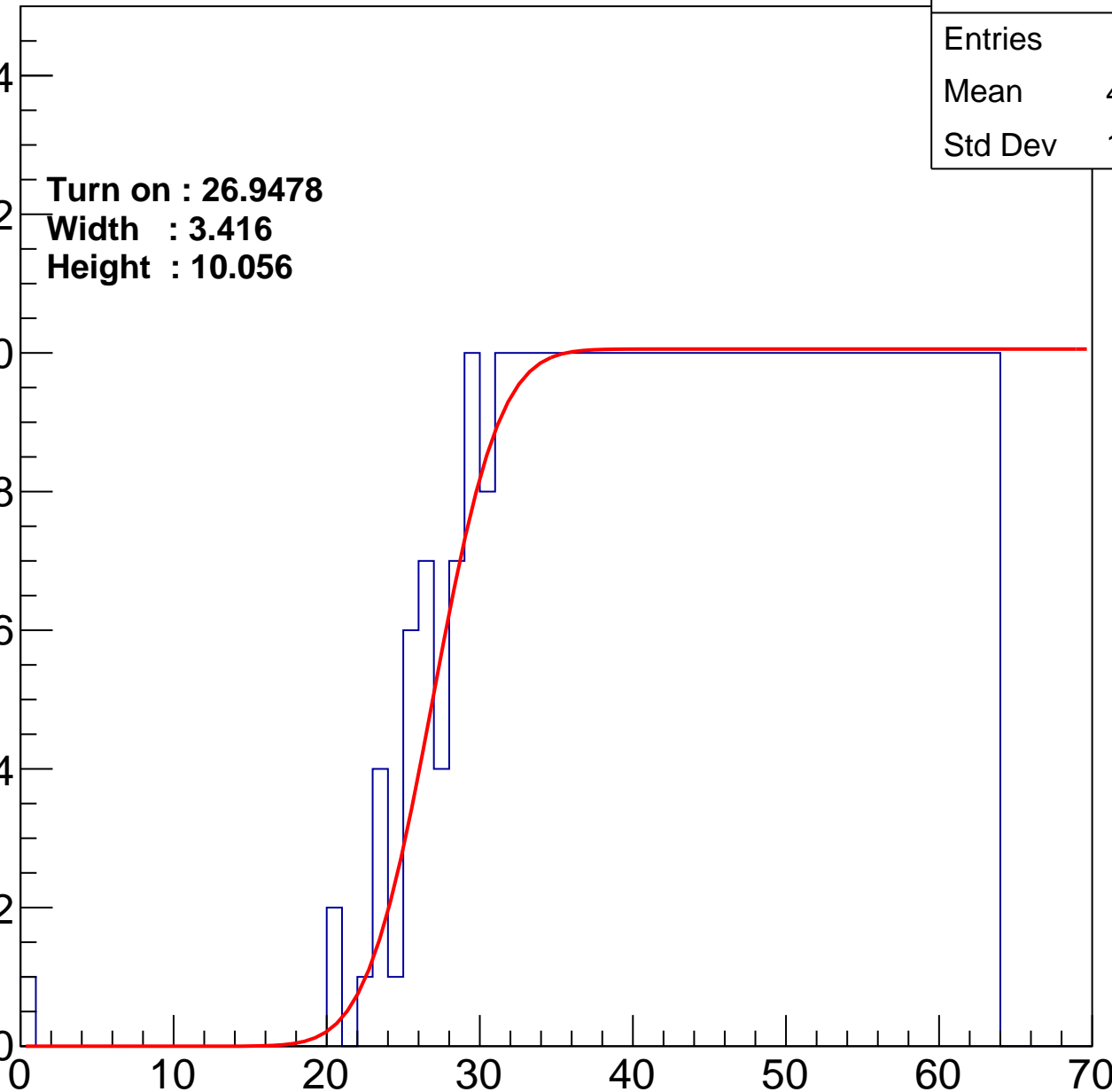
Width : 3.416

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch27

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 27.0028

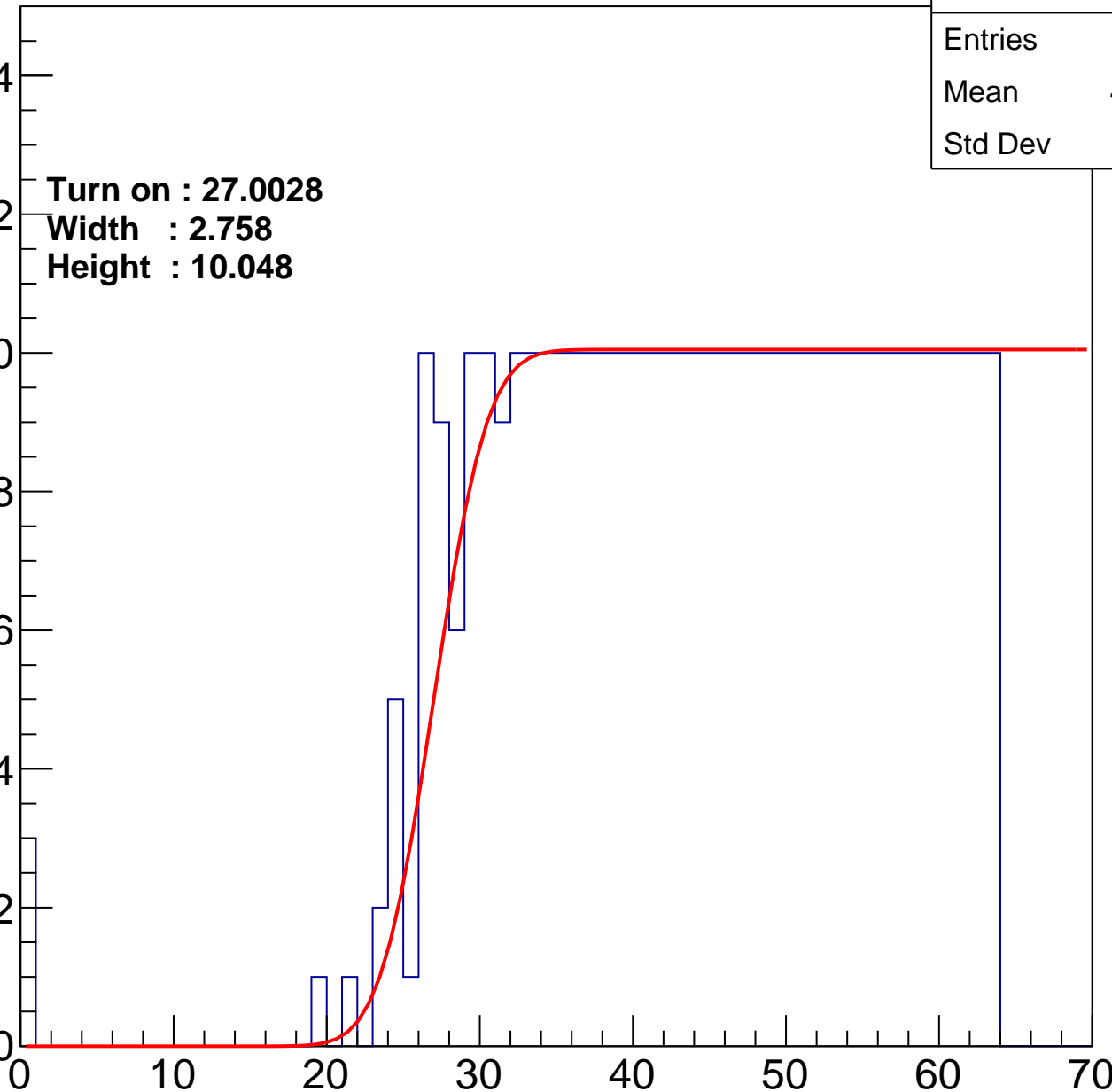
Width : 2.758

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.6204

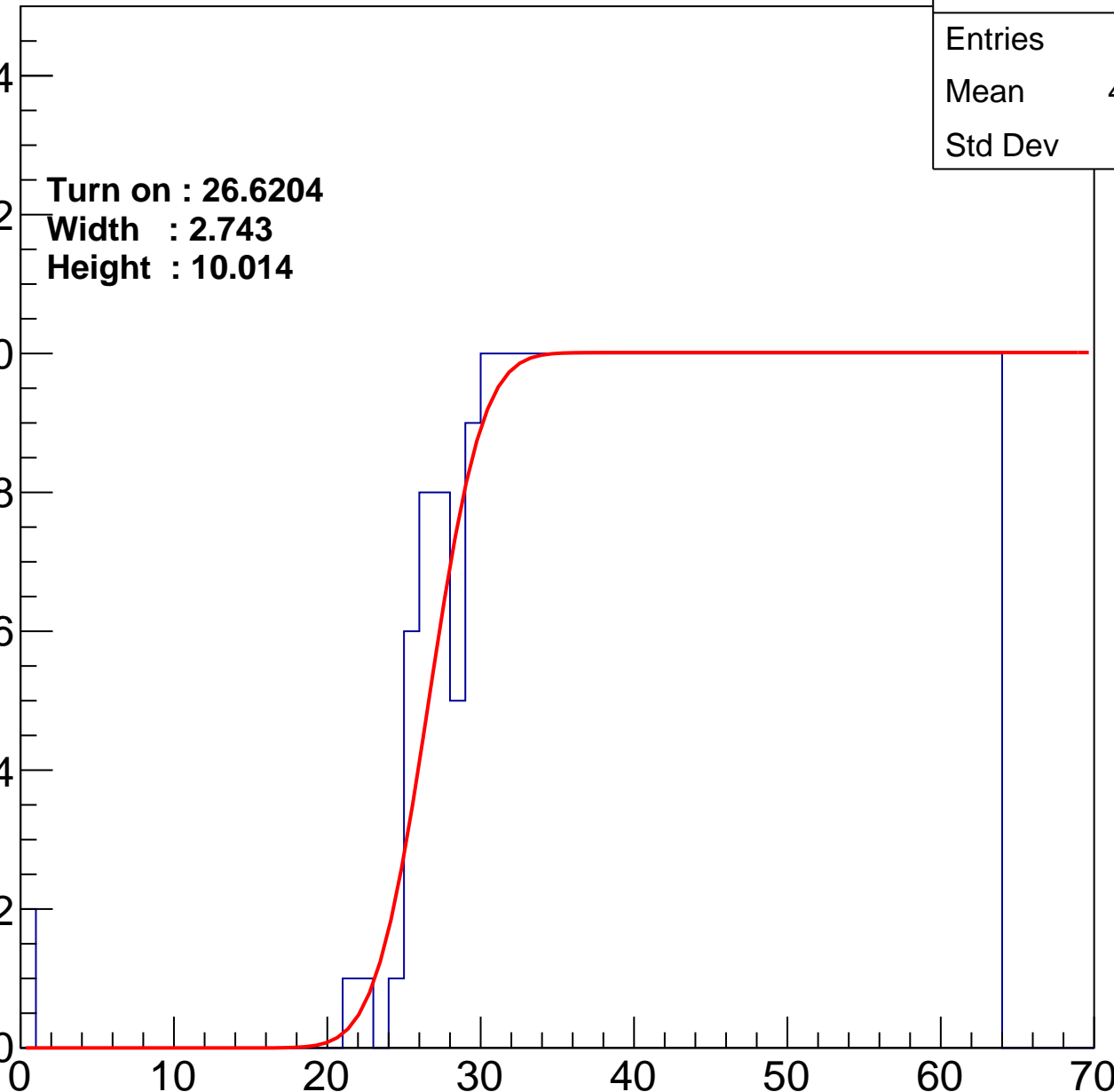
Width : 2.743

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch29

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.52
Std Dev	11.81

Turn on : 27.6676

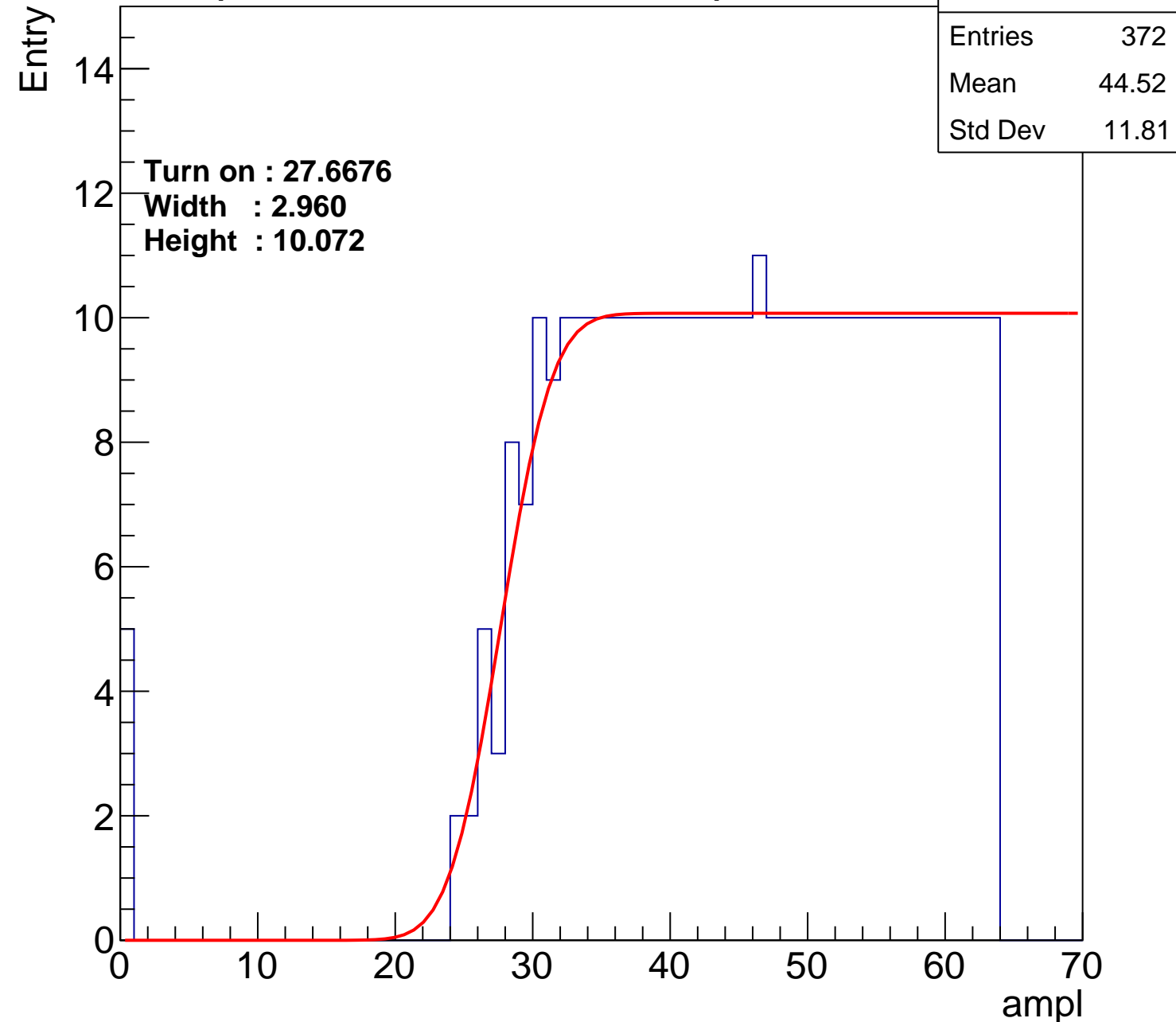
Width : 2.960

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch30

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.74
Std Dev	11.76

Turn on : 25.1134

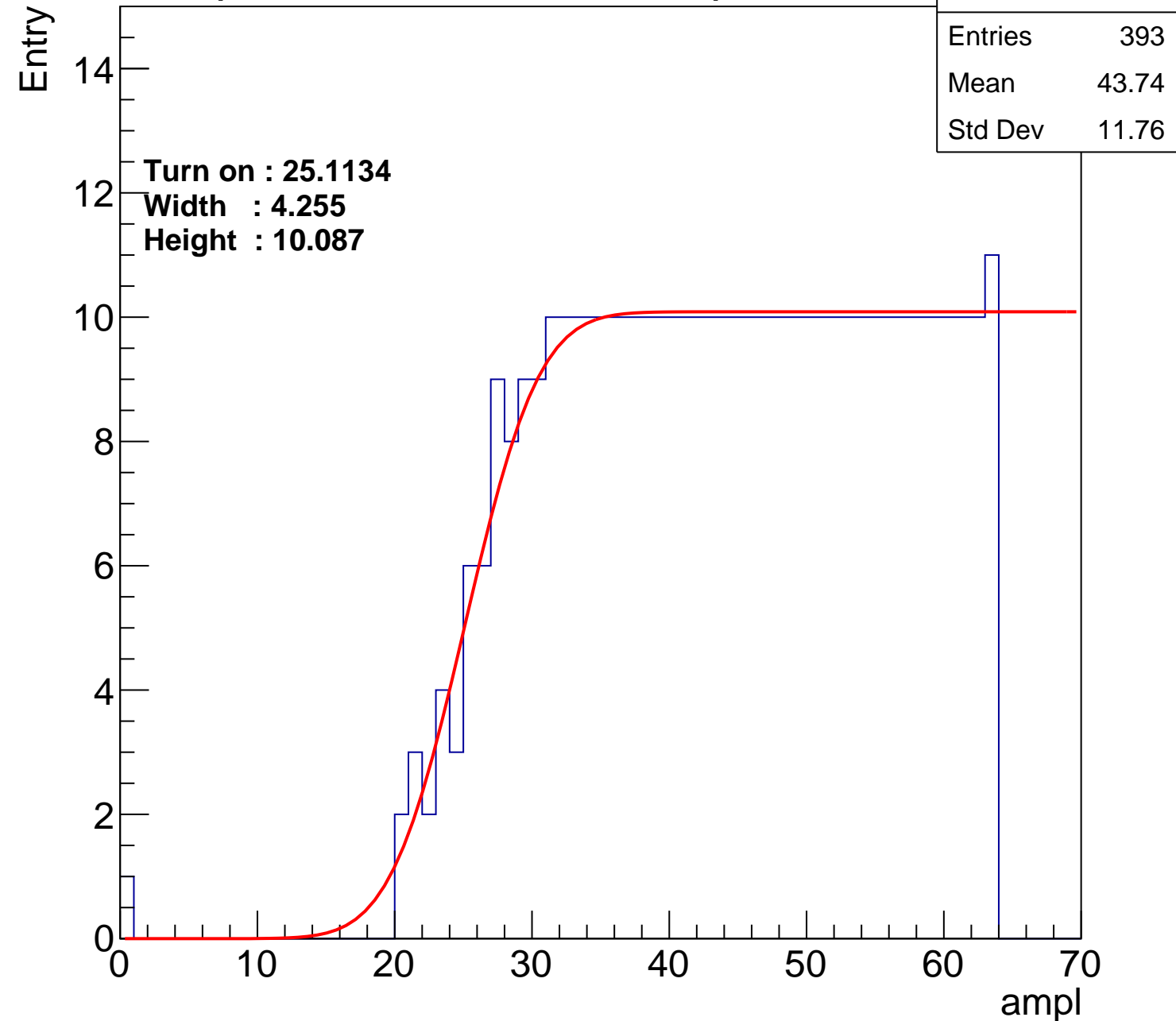
Width : 4.255

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch31

calib_packv5_042523_0143.root, FC#11, port A2

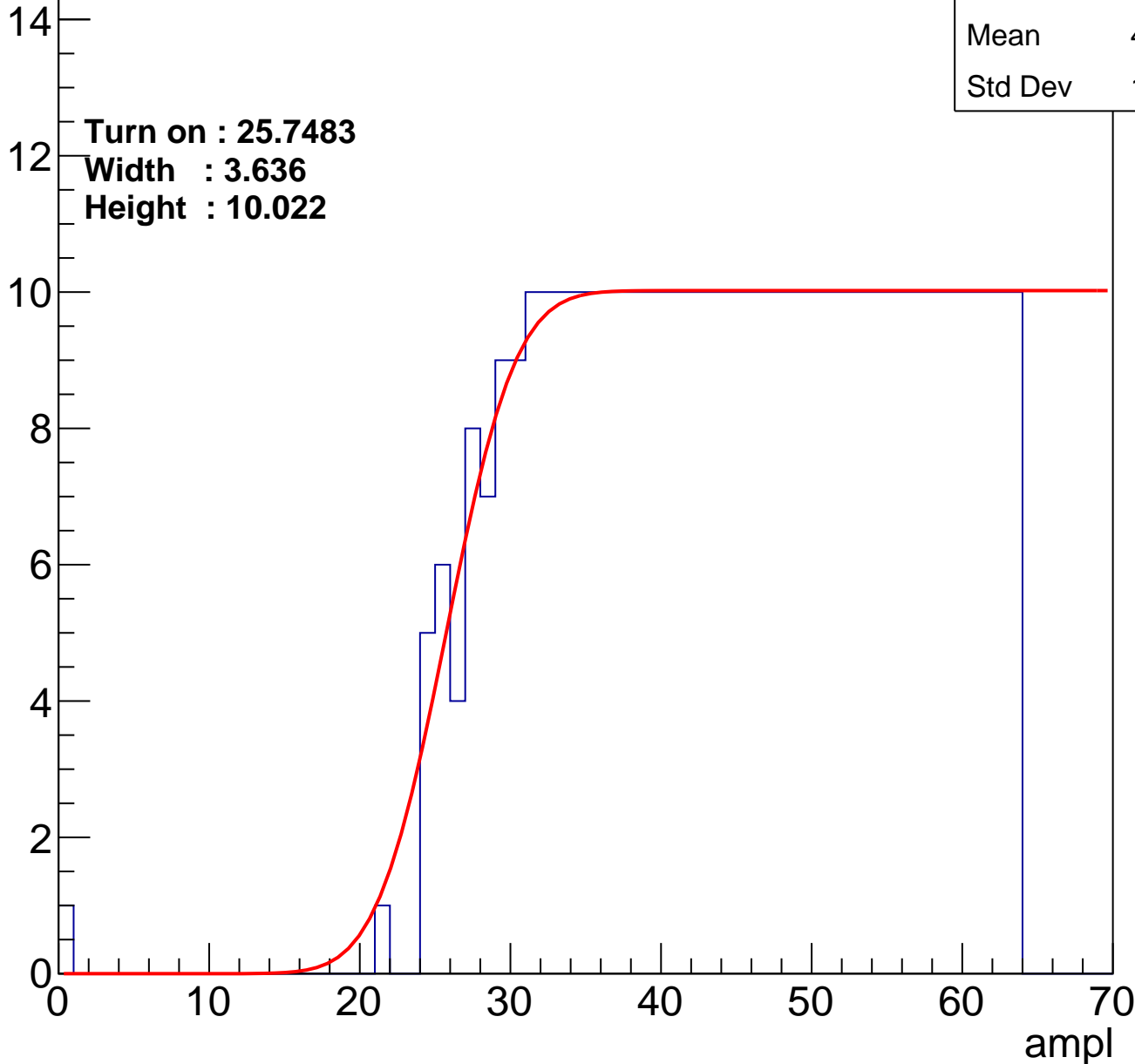
Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 25.7483

Width : 3.636

Height : 10.022

Entry



B1L102S, U22-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.51
Std Dev	11.94

Turn on : 25.5758

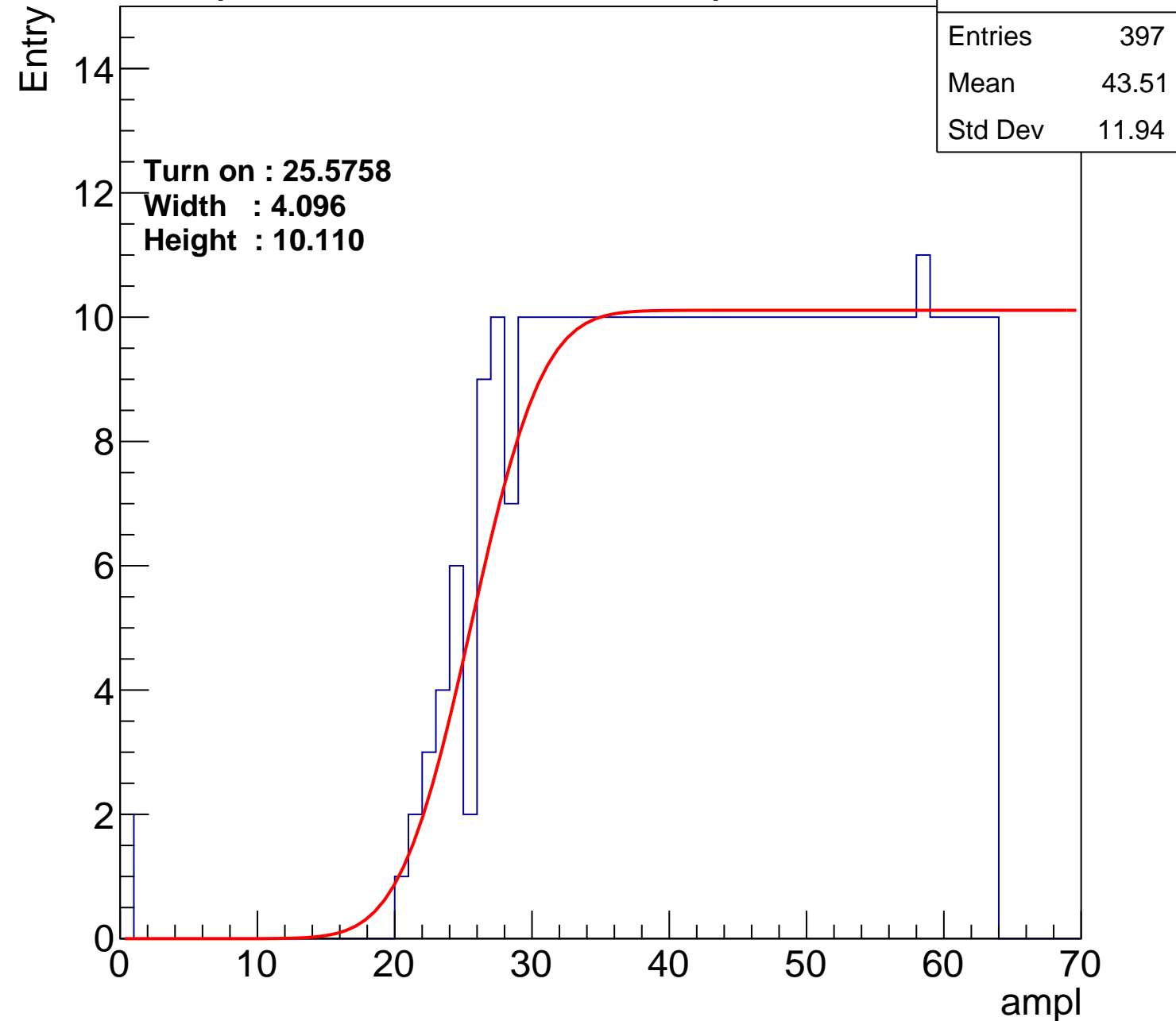
Width : 4.096

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch33

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.66
Std Dev	11.17

Turn on : 27.4497

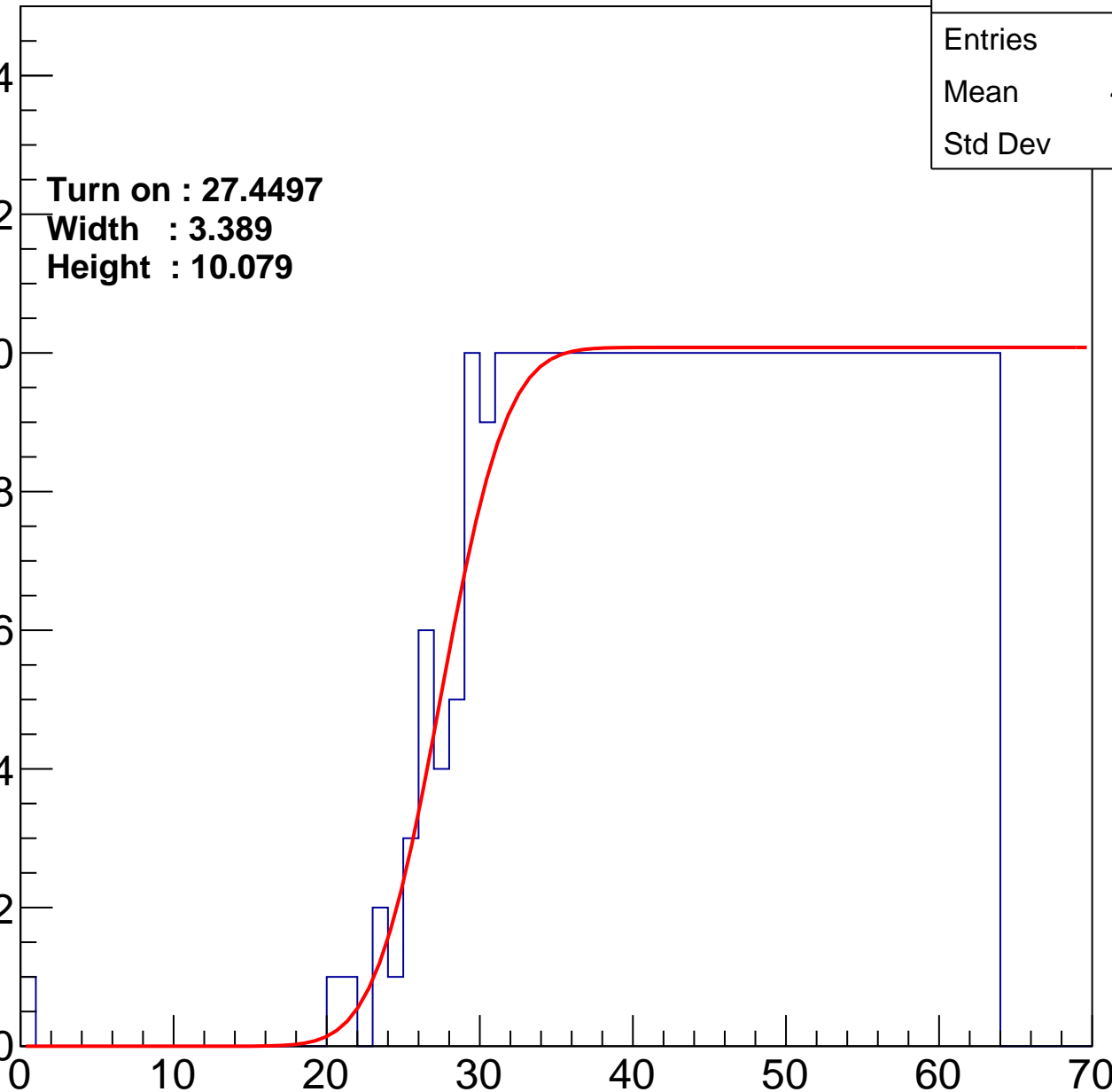
Width : 3.389

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch34

calib_packv5_042523_0143.root, FC#11, port A2

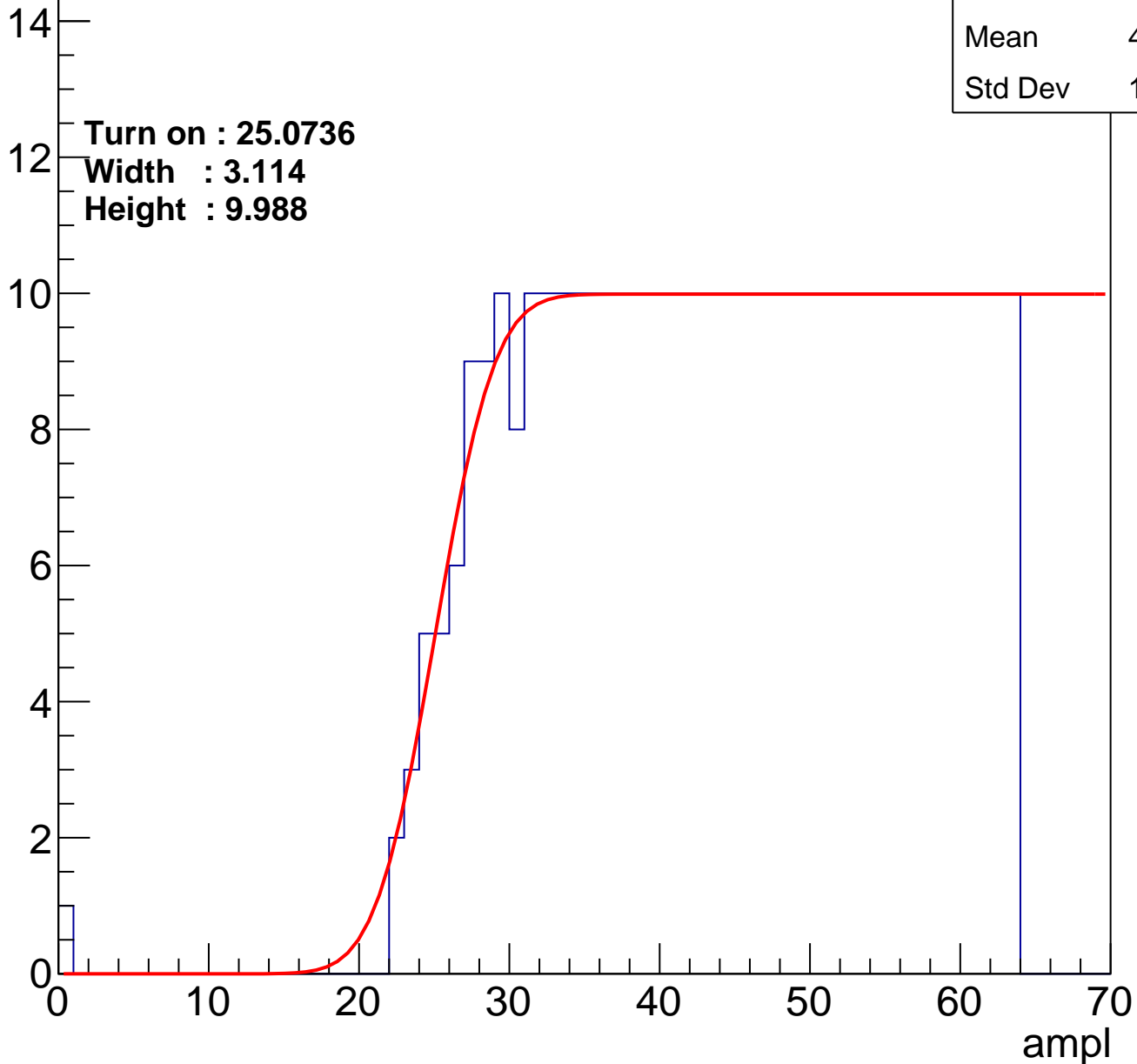
Entries	388
Mean	43.94
Std Dev	11.53

Turn on : 25.0736

Width : 3.114

Height : 9.988

Entry



B1L102S, U22-ch35

calib_packv5_042523_0143.root, FC#11, port A2

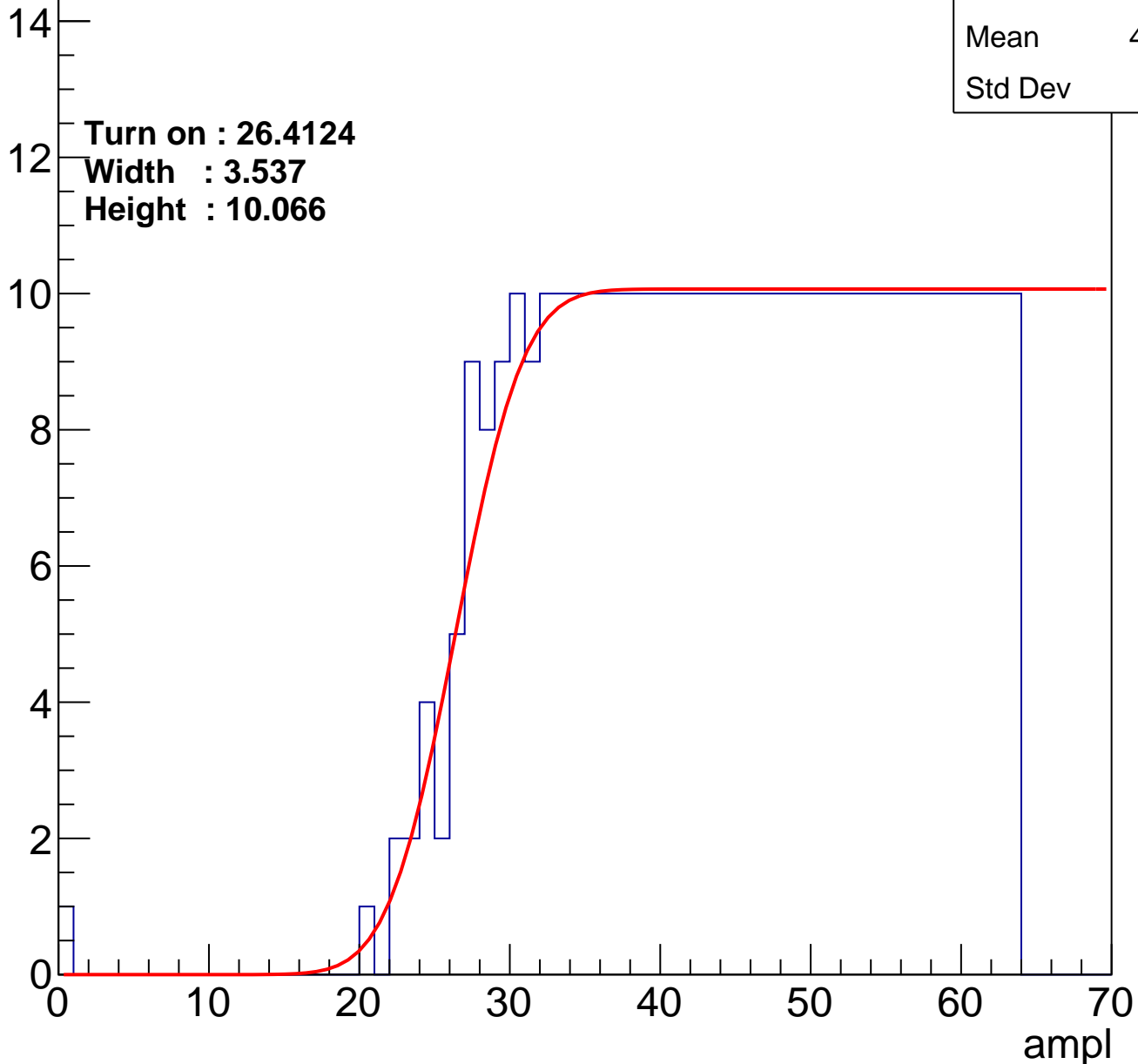
Entries	382
Mean	44.22
Std Dev	11.4

Turn on : 26.4124

Width : 3.537

Height : 10.066

Entry



B1L102S, U22-ch36

calib_packv5_042523_0143.root, FC#11, port A2

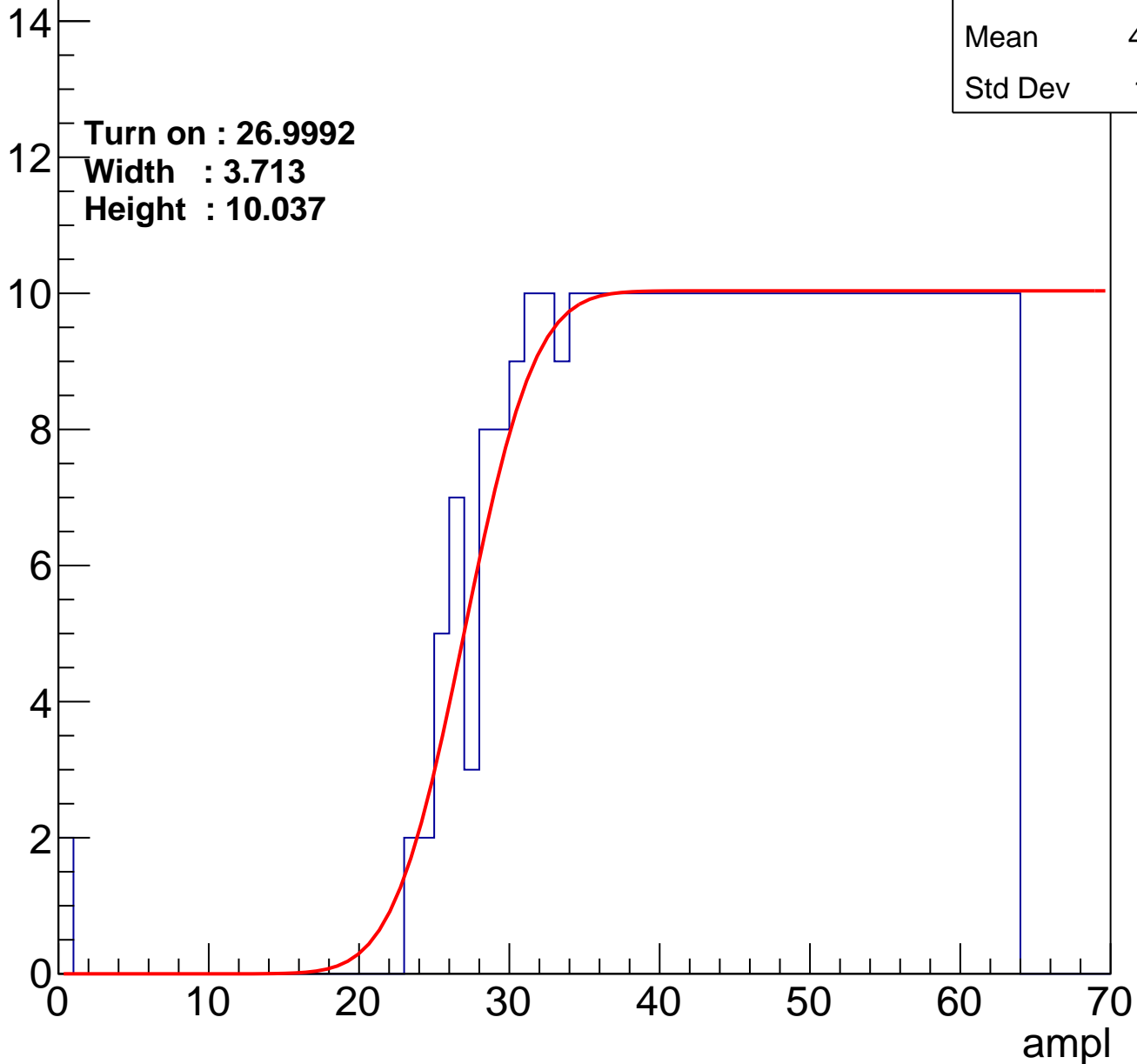
Entries	375
Mean	44.49
Std Dev	11.41

Turn on : 26.9992

Width : 3.713

Height : 10.037

Entry



B1L102S, U22-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.98
Std Dev	11.66

Turn on : 26.0400

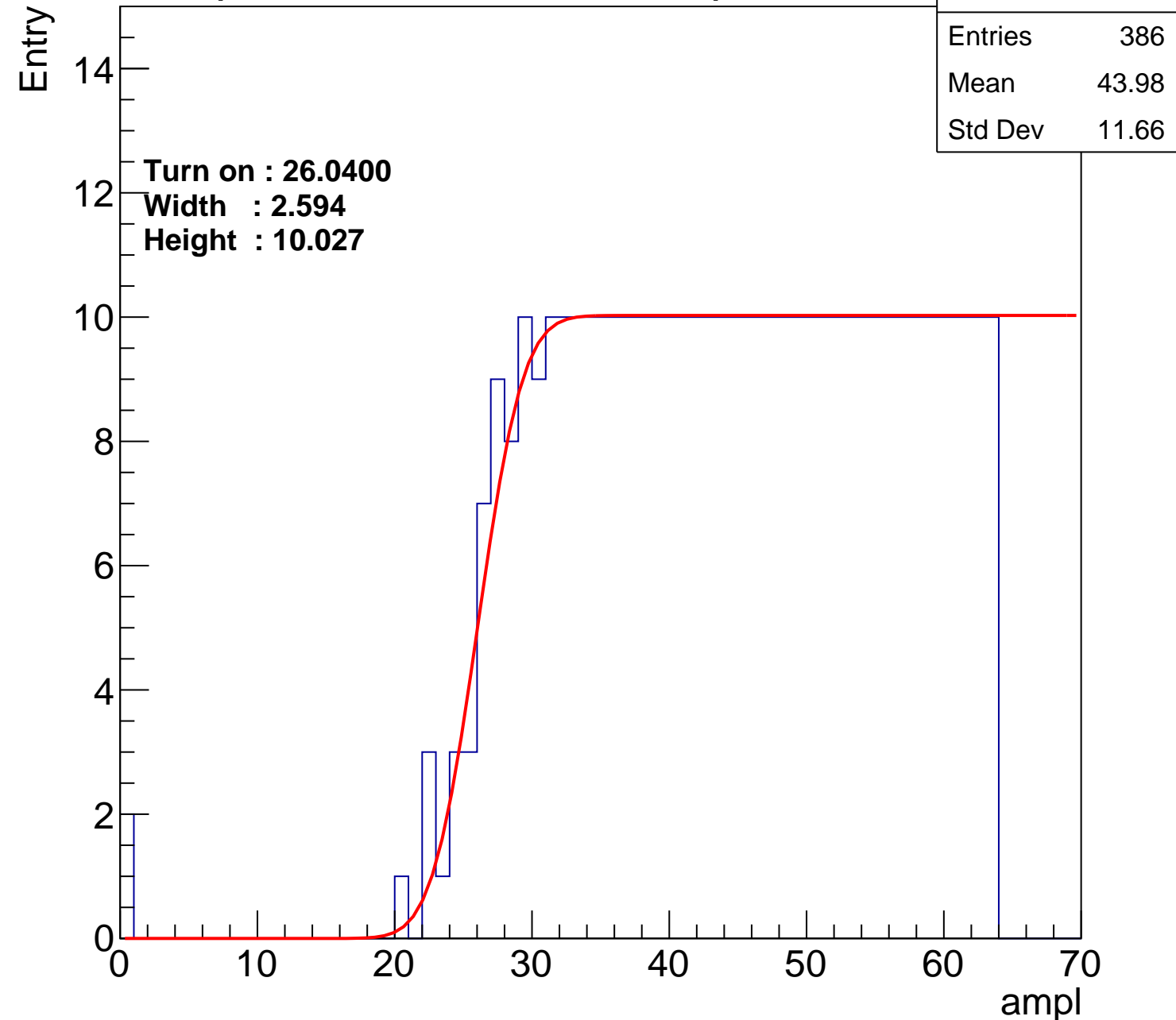
Width : 2.594

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch38

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 26.1985

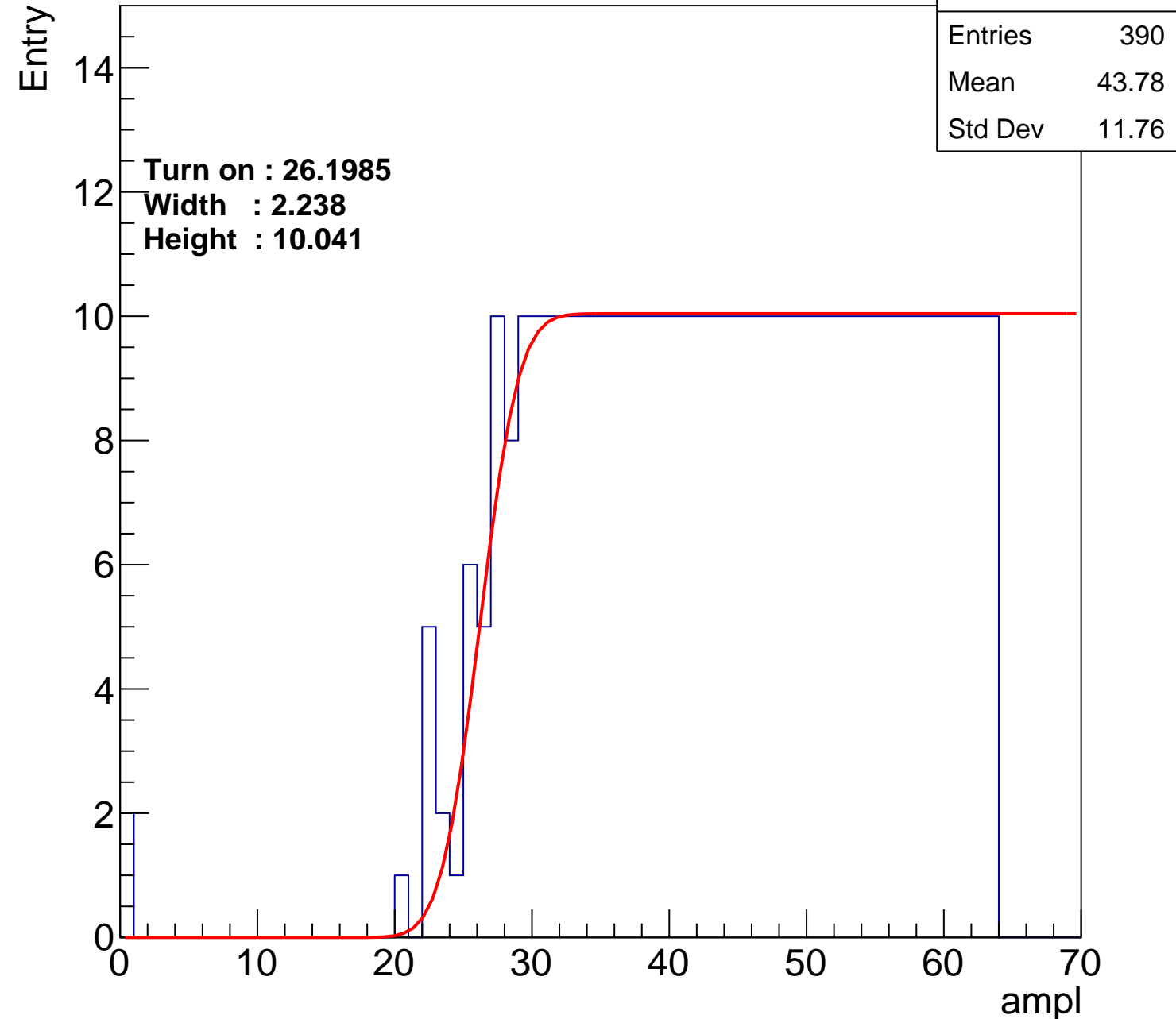
Width : 2.238

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch39

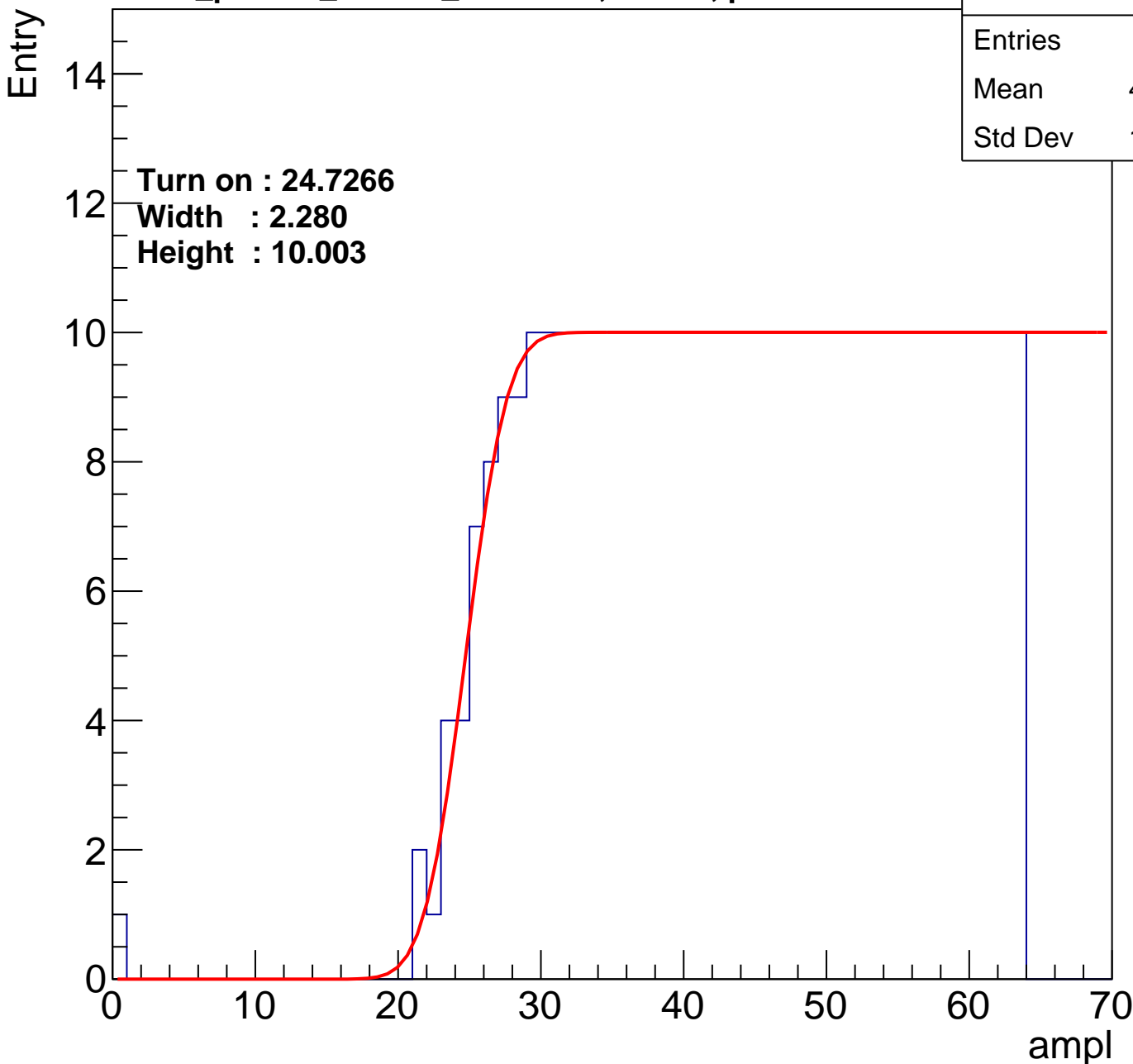
calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.62
Std Dev	11.68

Turn on : 24.7266

Width : 2.280

Height : 10.003



B1L102S, U22-ch40

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.5
Std Dev	11.98

Turn on : 24.9447

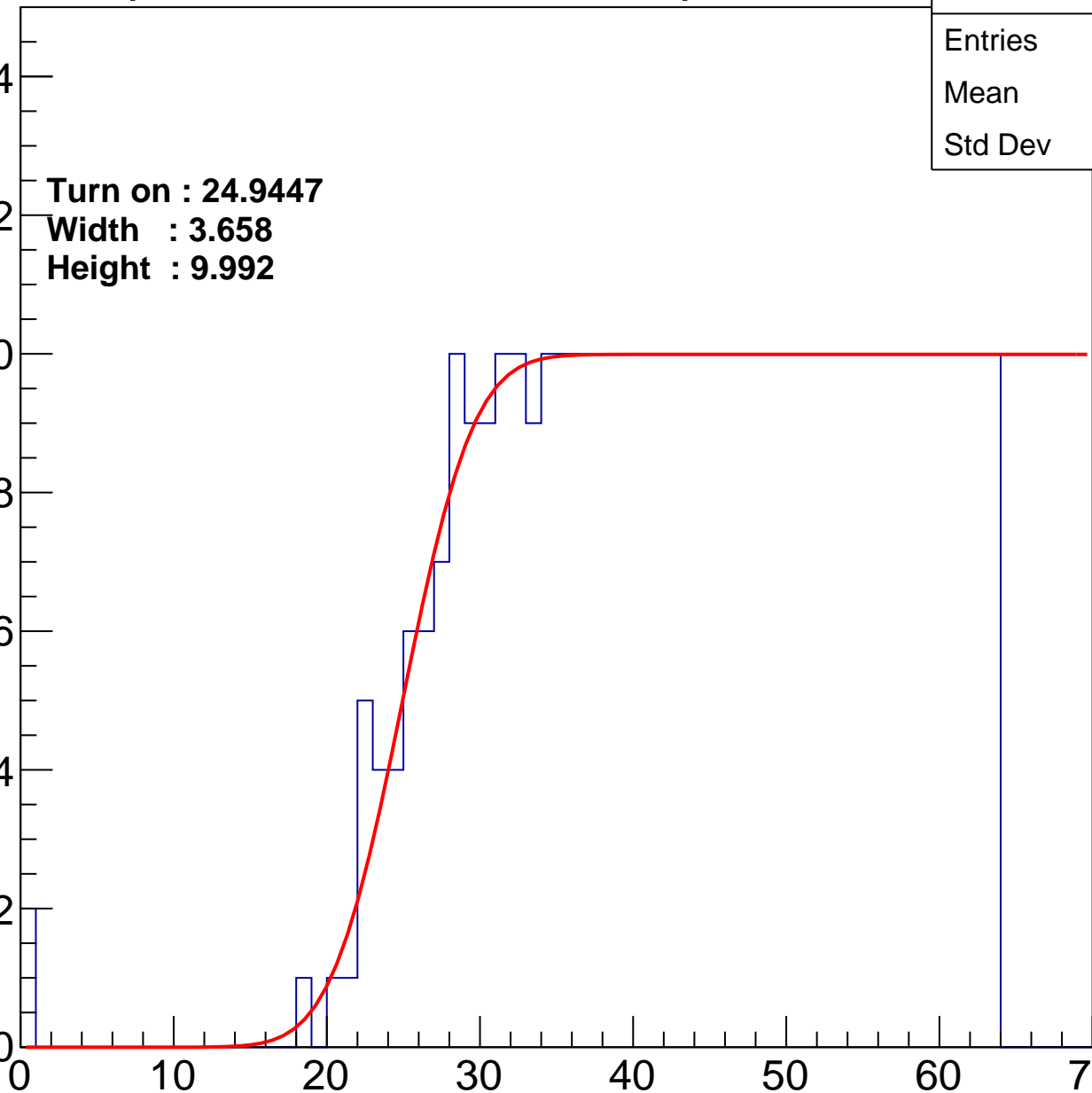
Width : 3.658

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch41

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	43.18
Std Dev	12.09

Turn on : 24.0421

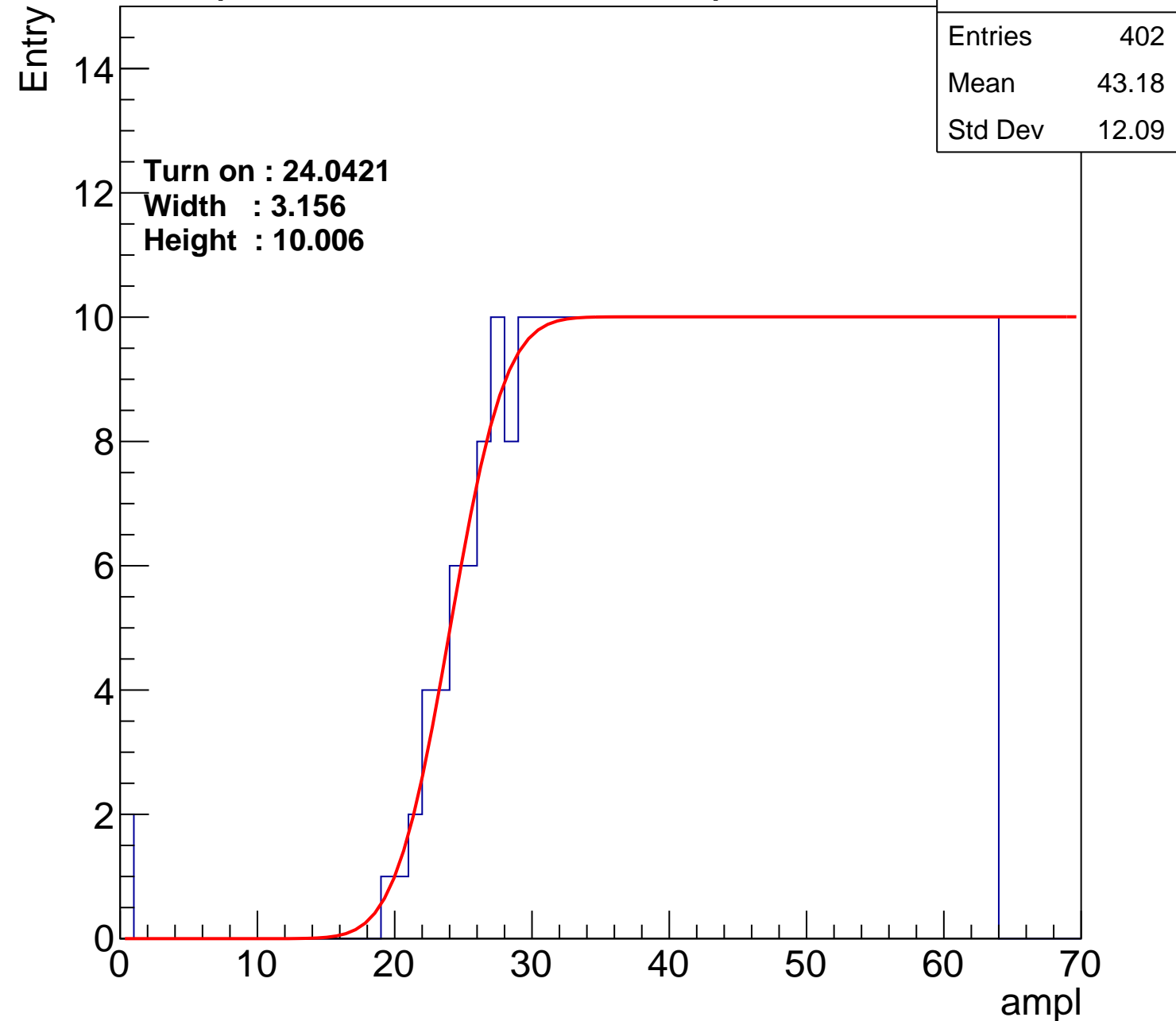
Width : 3.156

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch42

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.41
Std Dev	12.15

Turn on : 24.6310

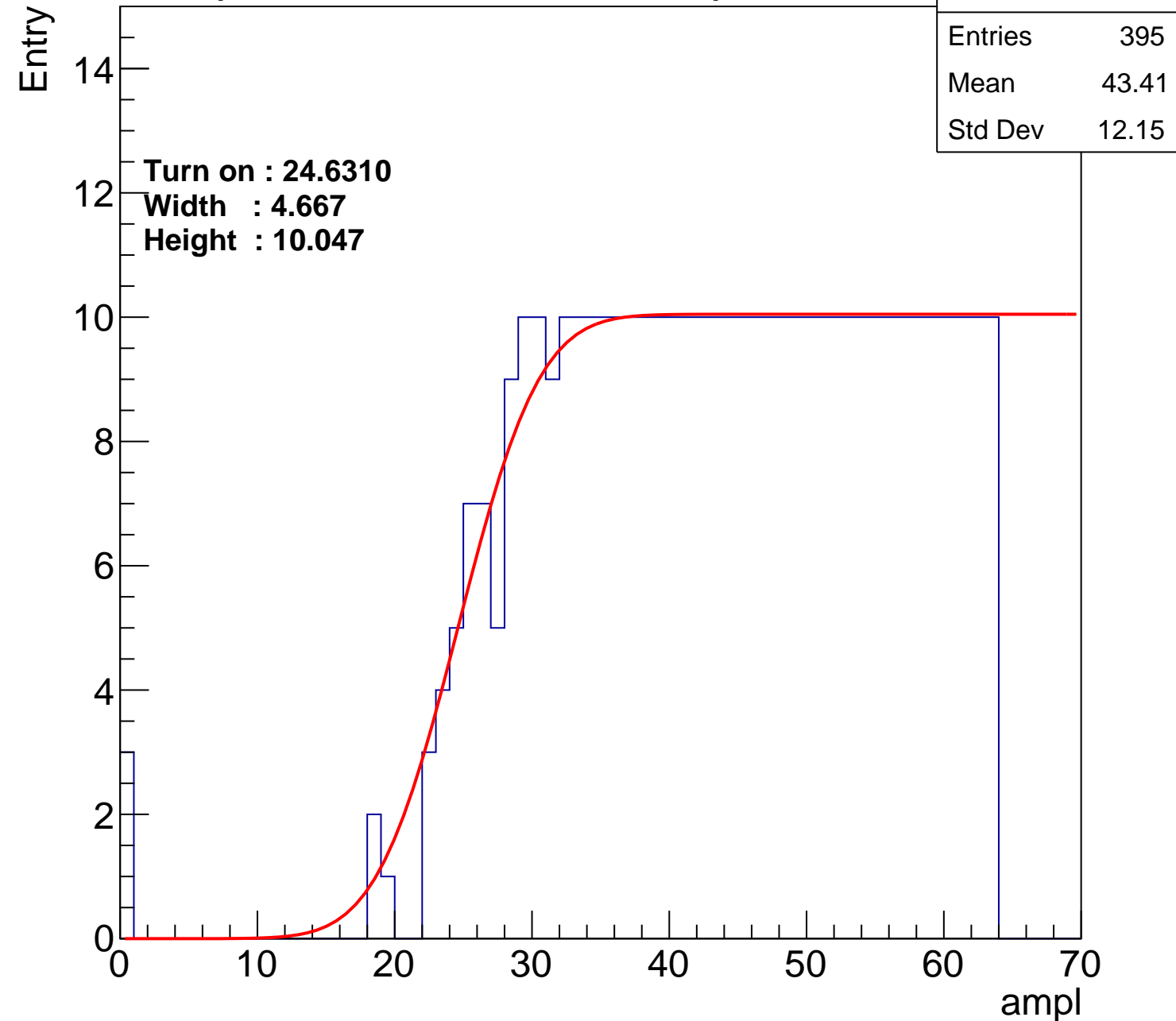
Width : 4.667

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch43

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.88
Std Dev	11.22

Turn on : 27.8733

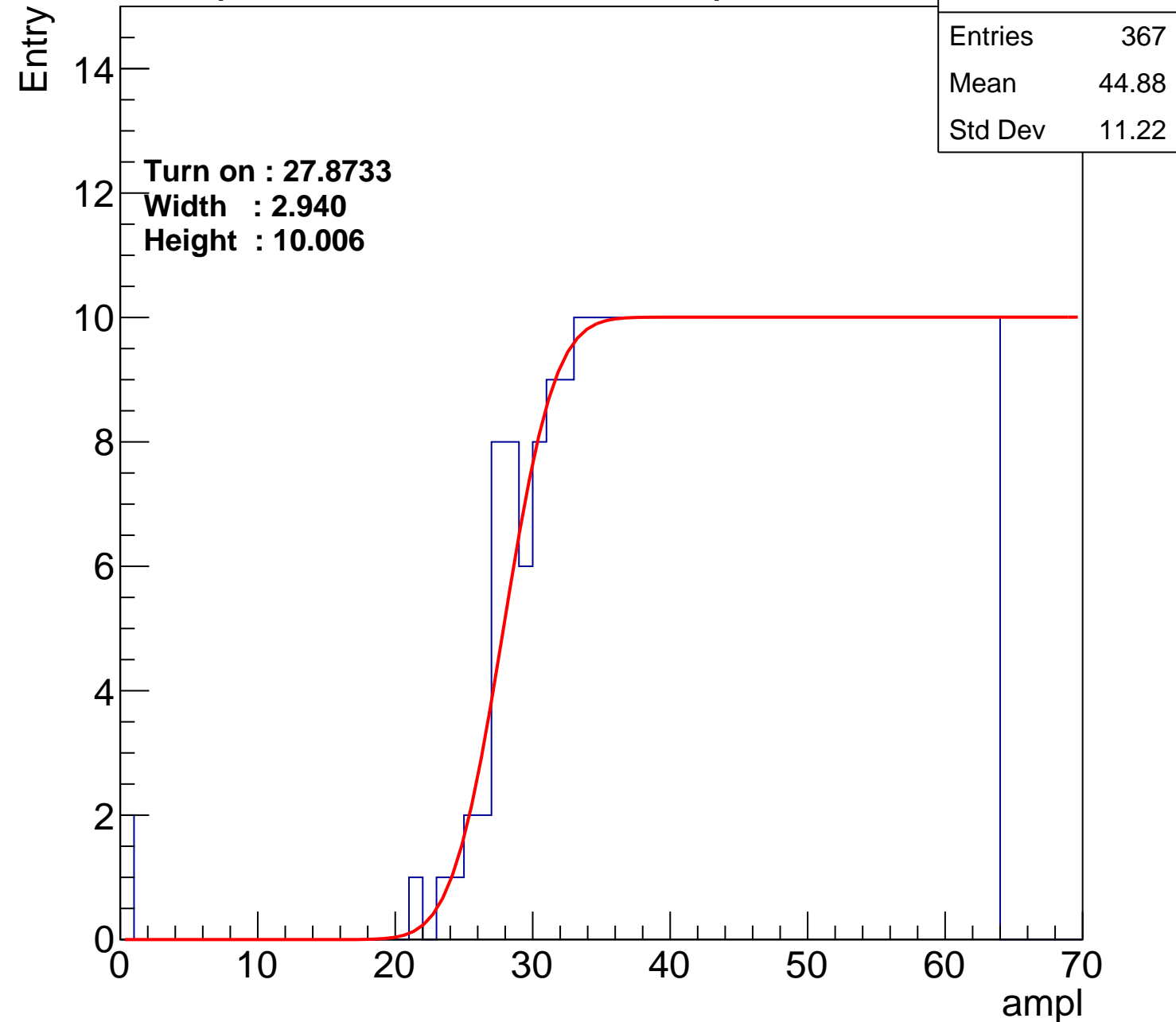
Width : 2.940

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch44

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.65
Std Dev	11.91

Turn on : 25.1238

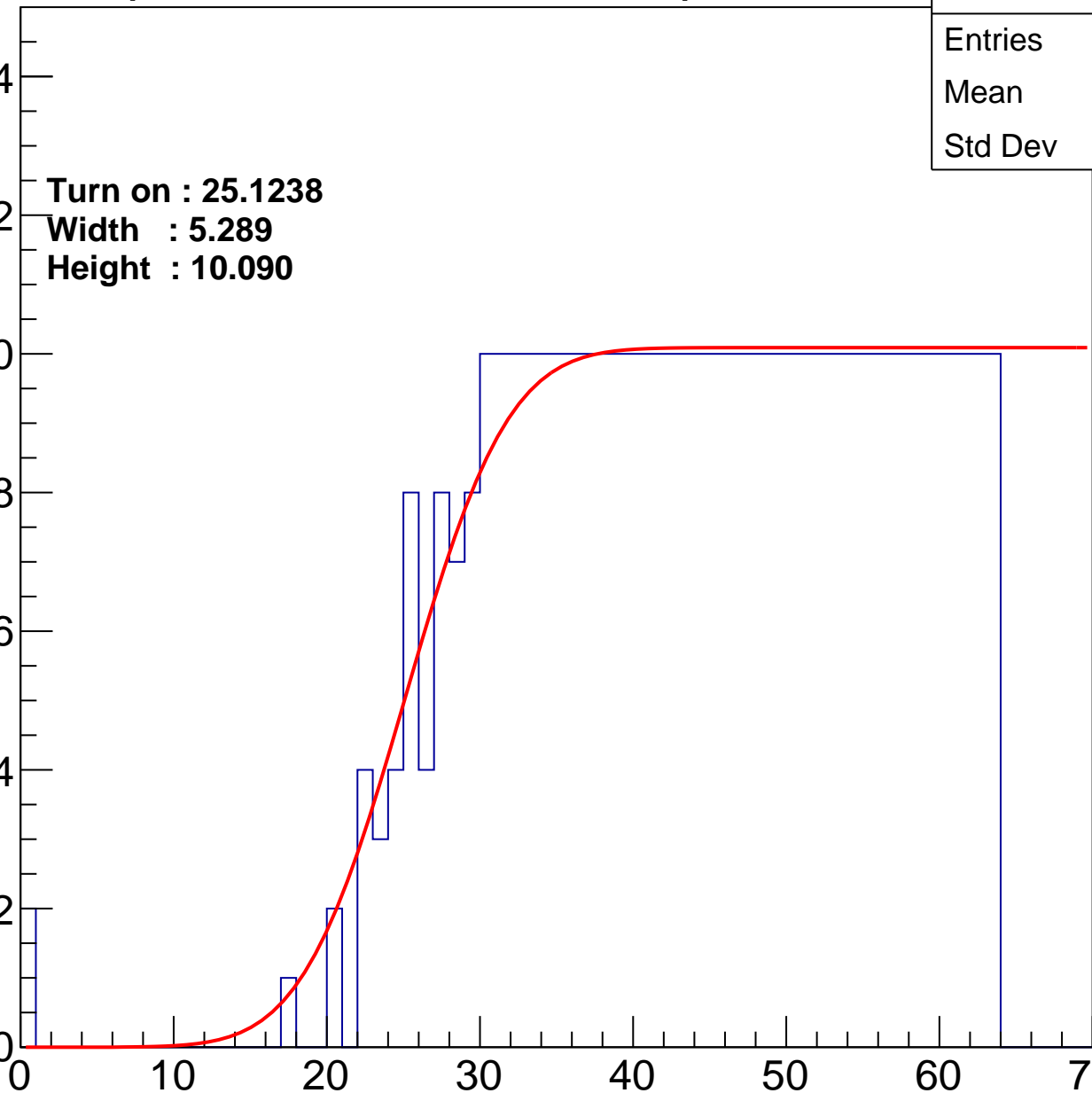
Width : 5.289

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch45

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.37
Std Dev	11.57

Turn on : 27.0599

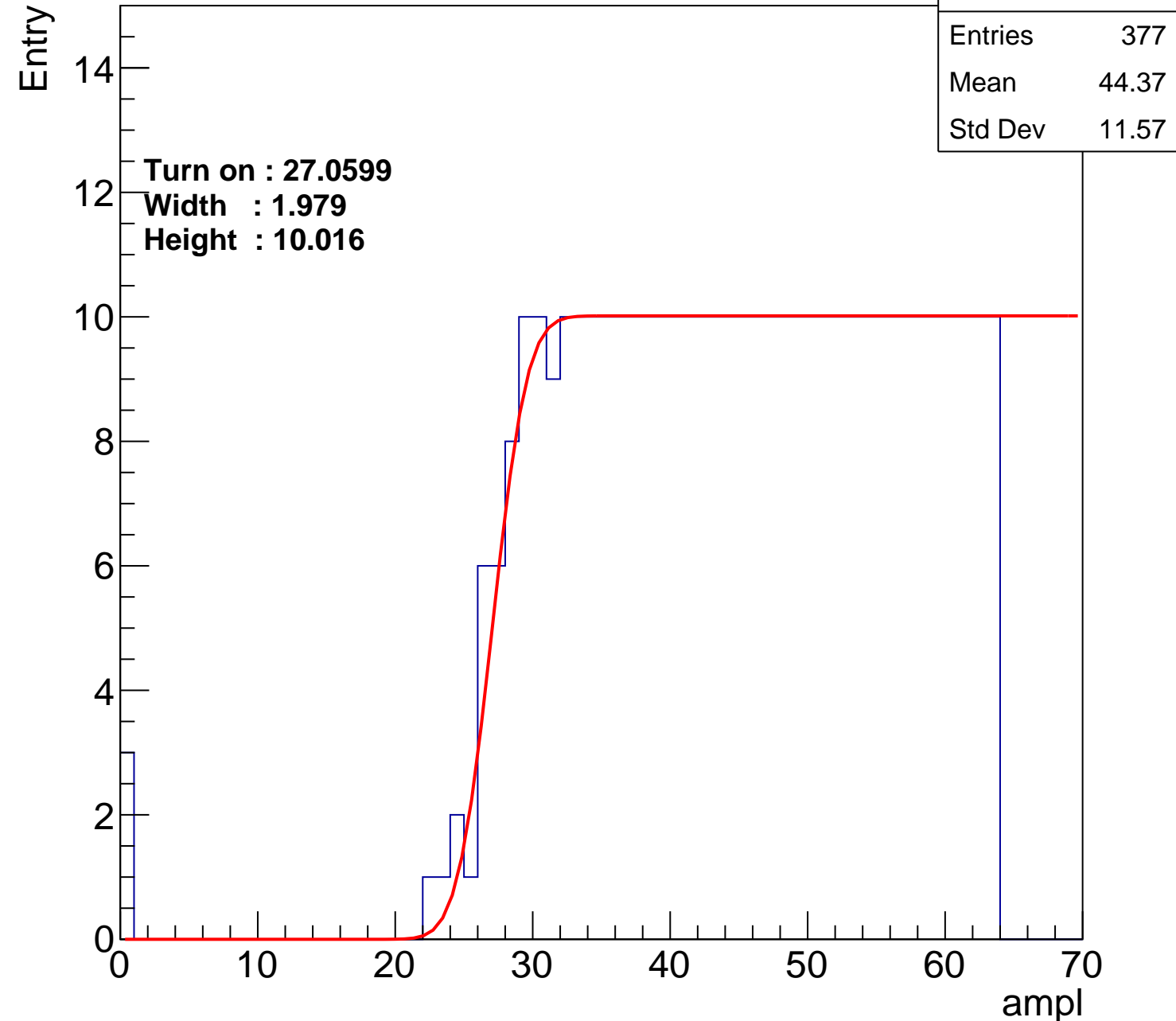
Width : 1.979

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch46

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.1860

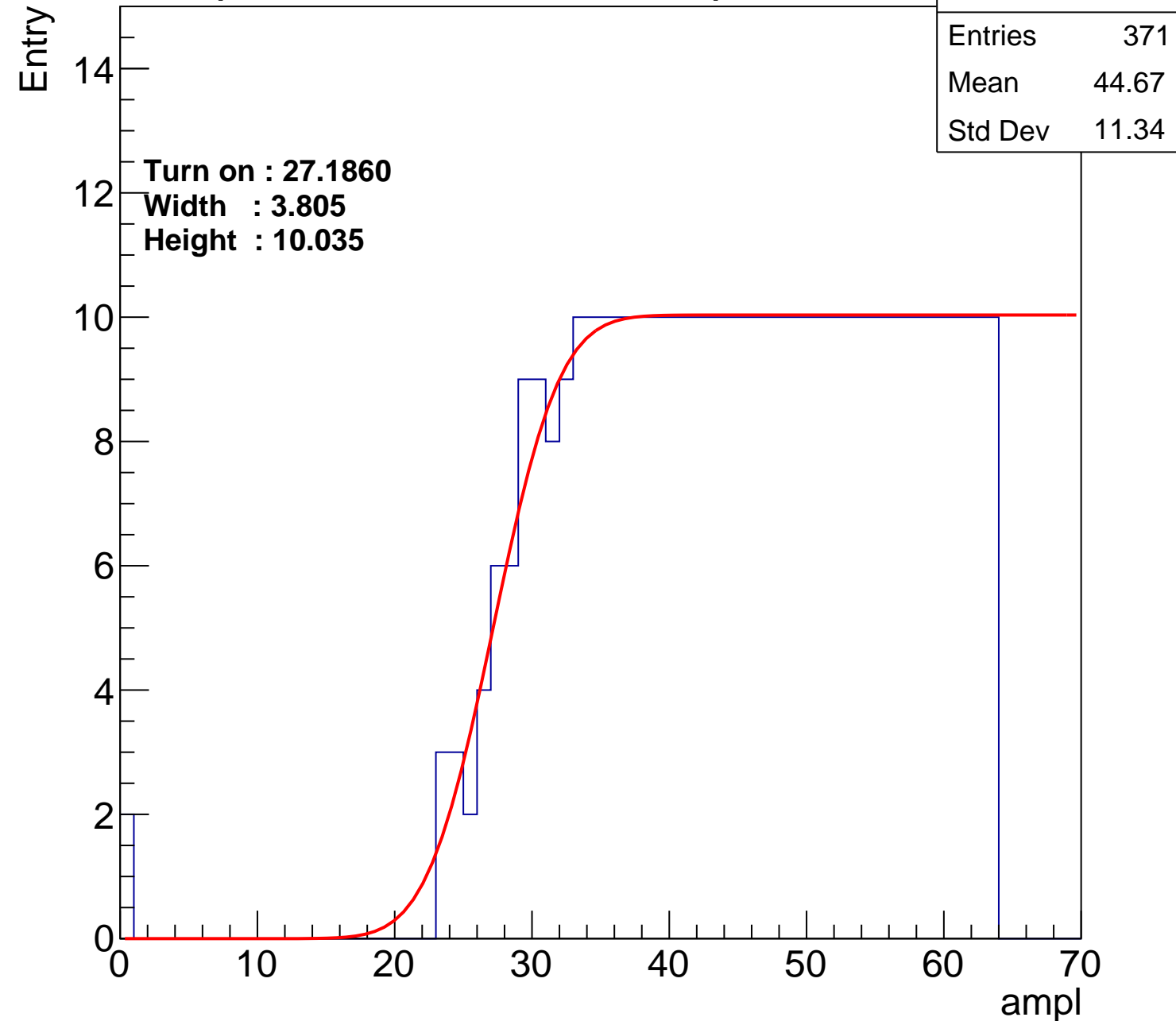
Width : 3.805

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch47

calib_packv5_042523_0143.root, FC#11, port A2

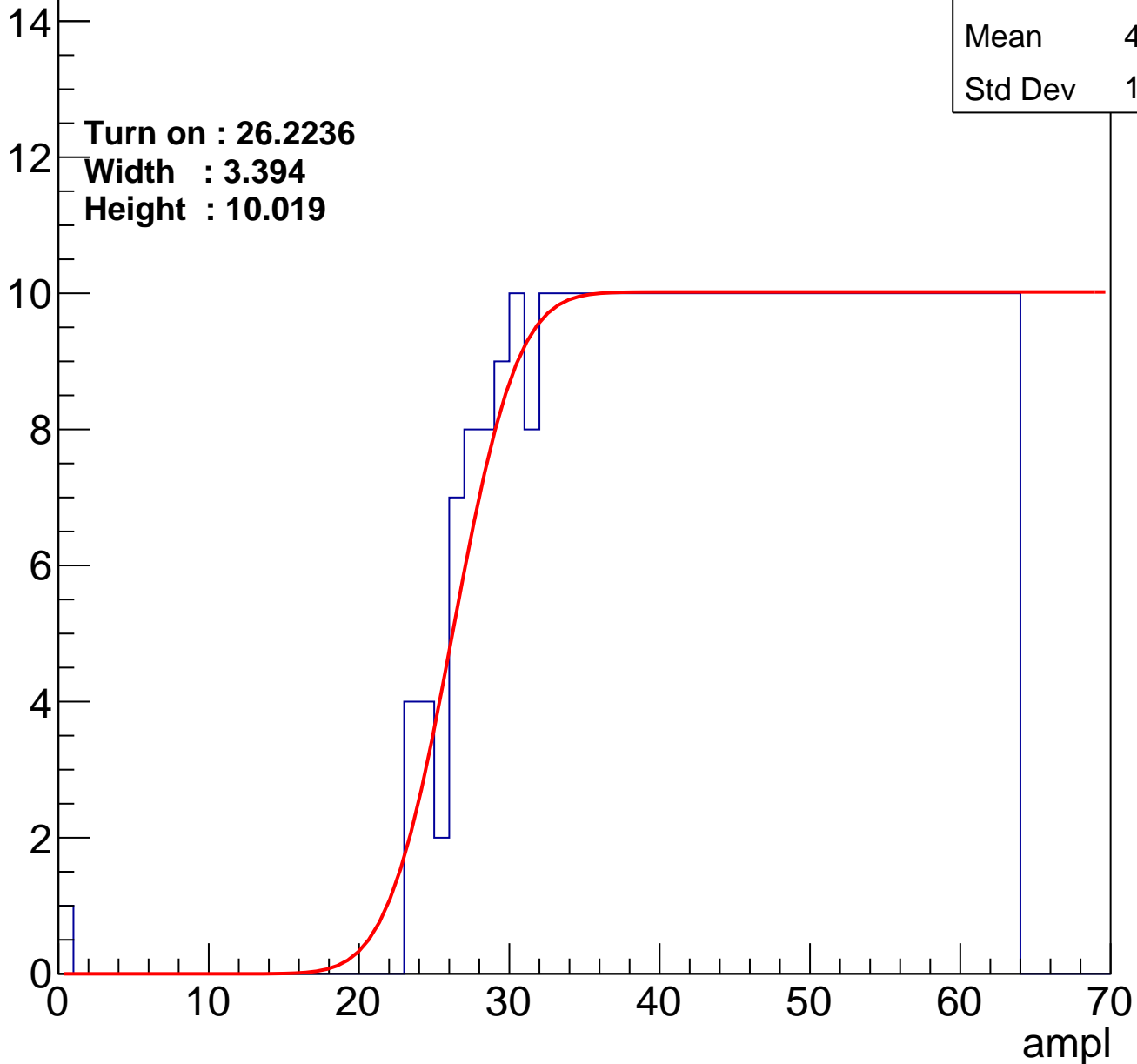
Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.2236

Width : 3.394

Height : 10.019

Entry



B1L102S, U22-ch48

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.22
Std Dev	11.38

Turn on : 26.1924

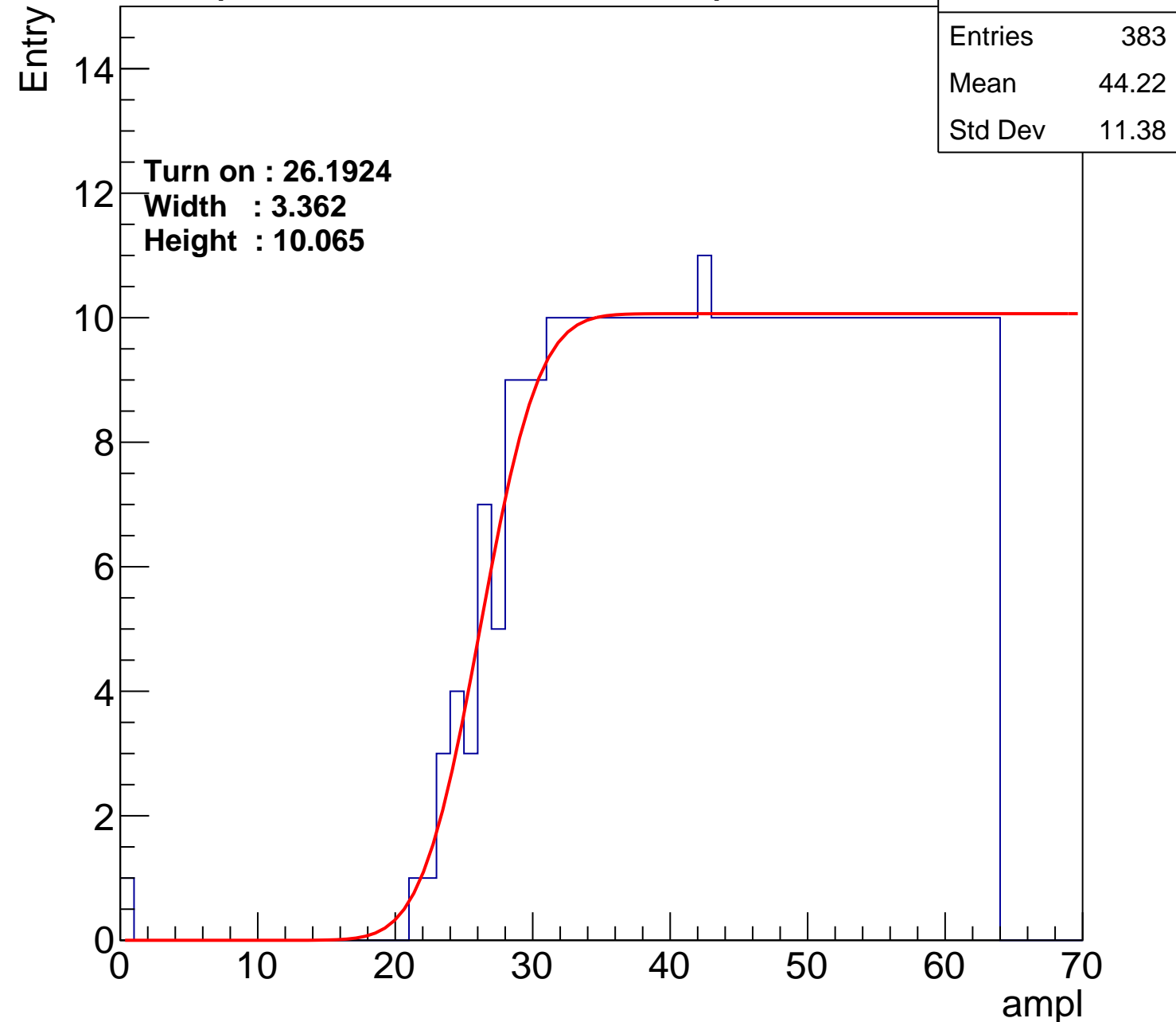
Width : 3.362

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch49

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.7610

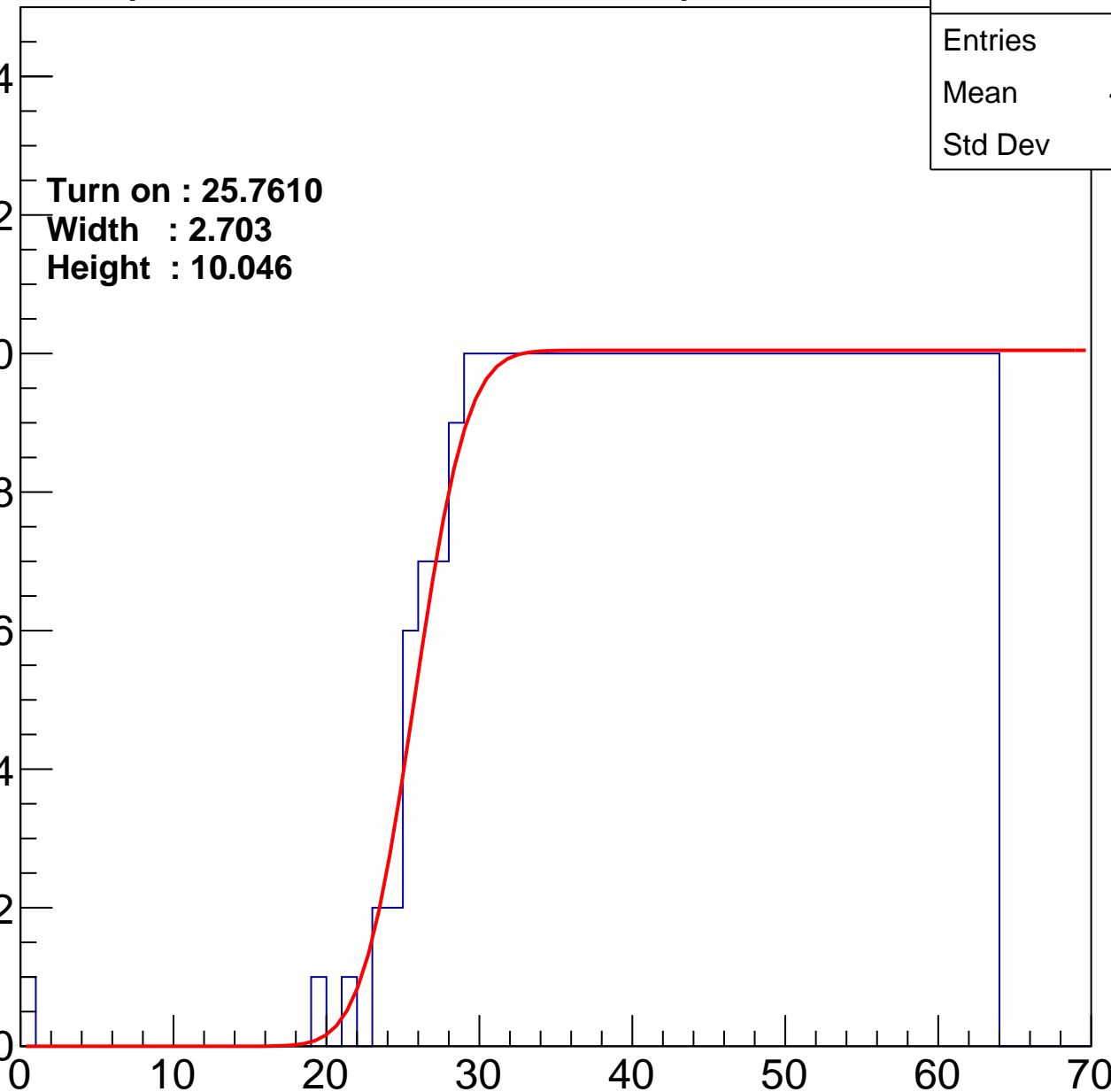
Width : 2.703

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch50

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.64
Std Dev	11.82

Turn on : 25.3400

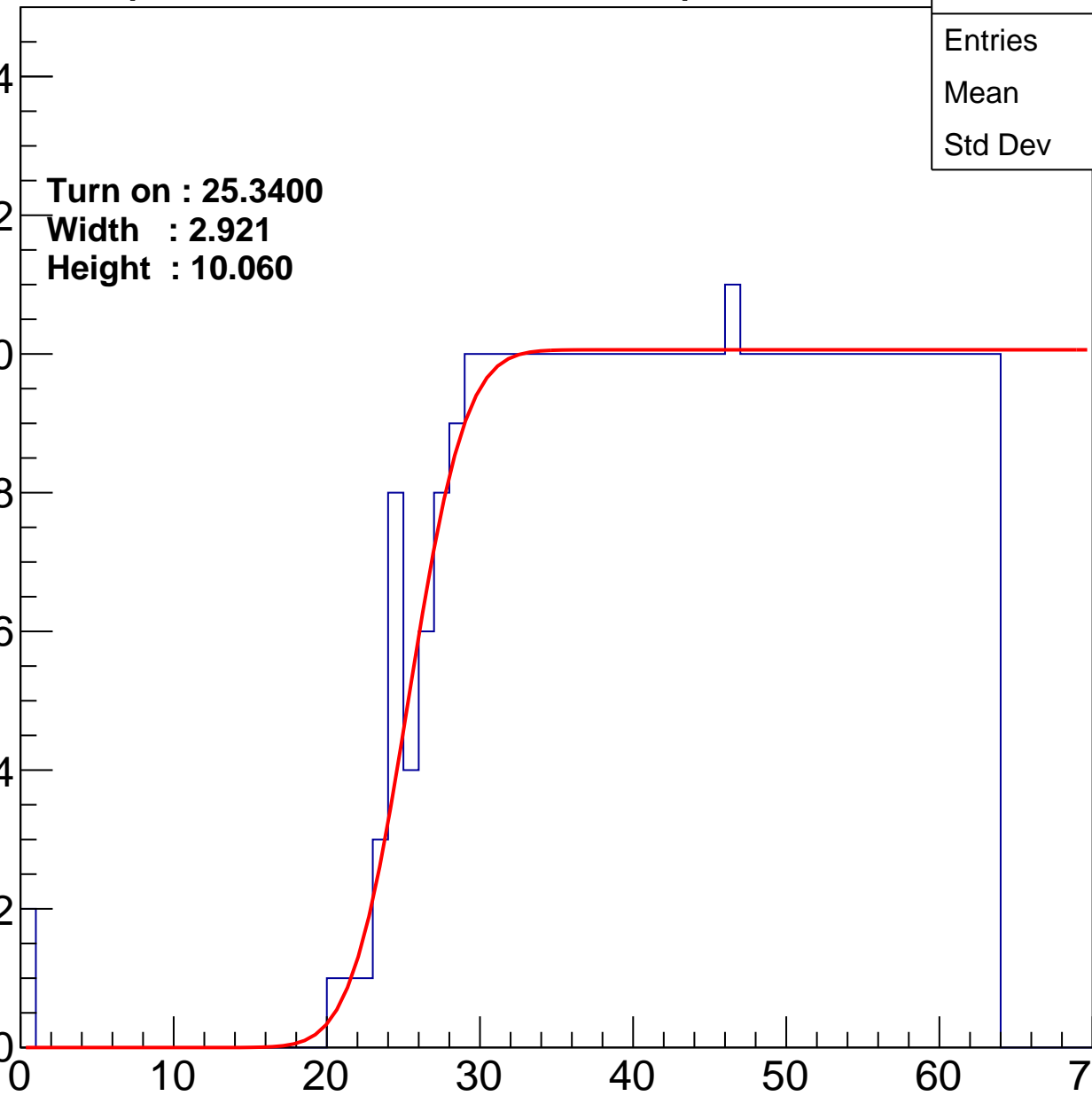
Width : 2.921

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch51

calib_packv5_042523_0143.root, FC#11, port A2

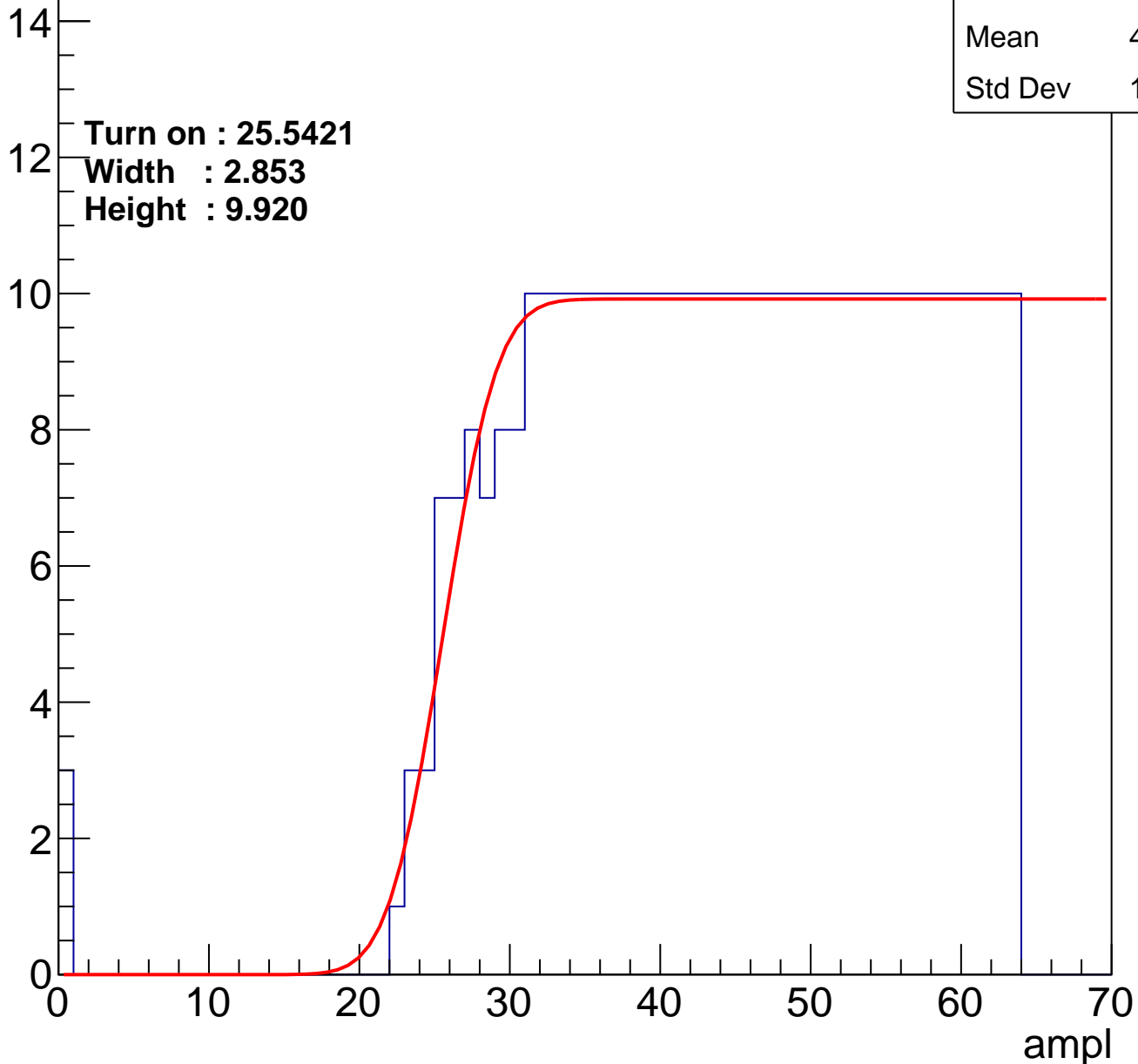
Entries	385
Mean	43.93
Std Dev	11.84

Turn on : 25.5421

Width : 2.853

Height : 9.920

Entry



B1L102S, U22-ch52

calib_packv5_042523_0143.root, FC#11, port A2

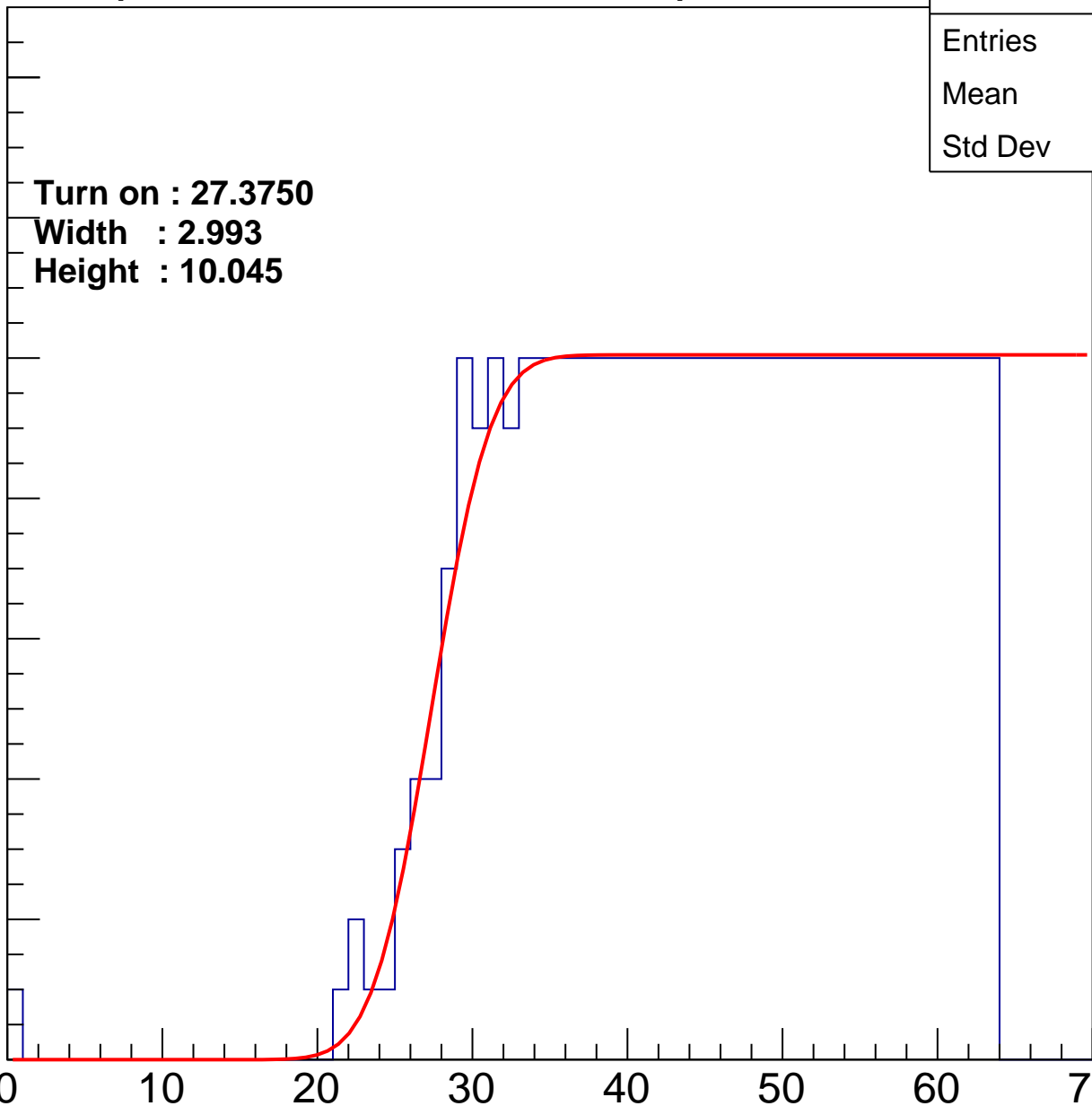
Entry

14
12
10
8
6
4
2
0

Turn on : 27.3750
Width : 2.993
Height : 10.045

Entries	372
Mean	44.71
Std Dev	11.14

ampl



B1L102S, U22-ch53

calib_packv5_042523_0143.root, FC#11, port A2

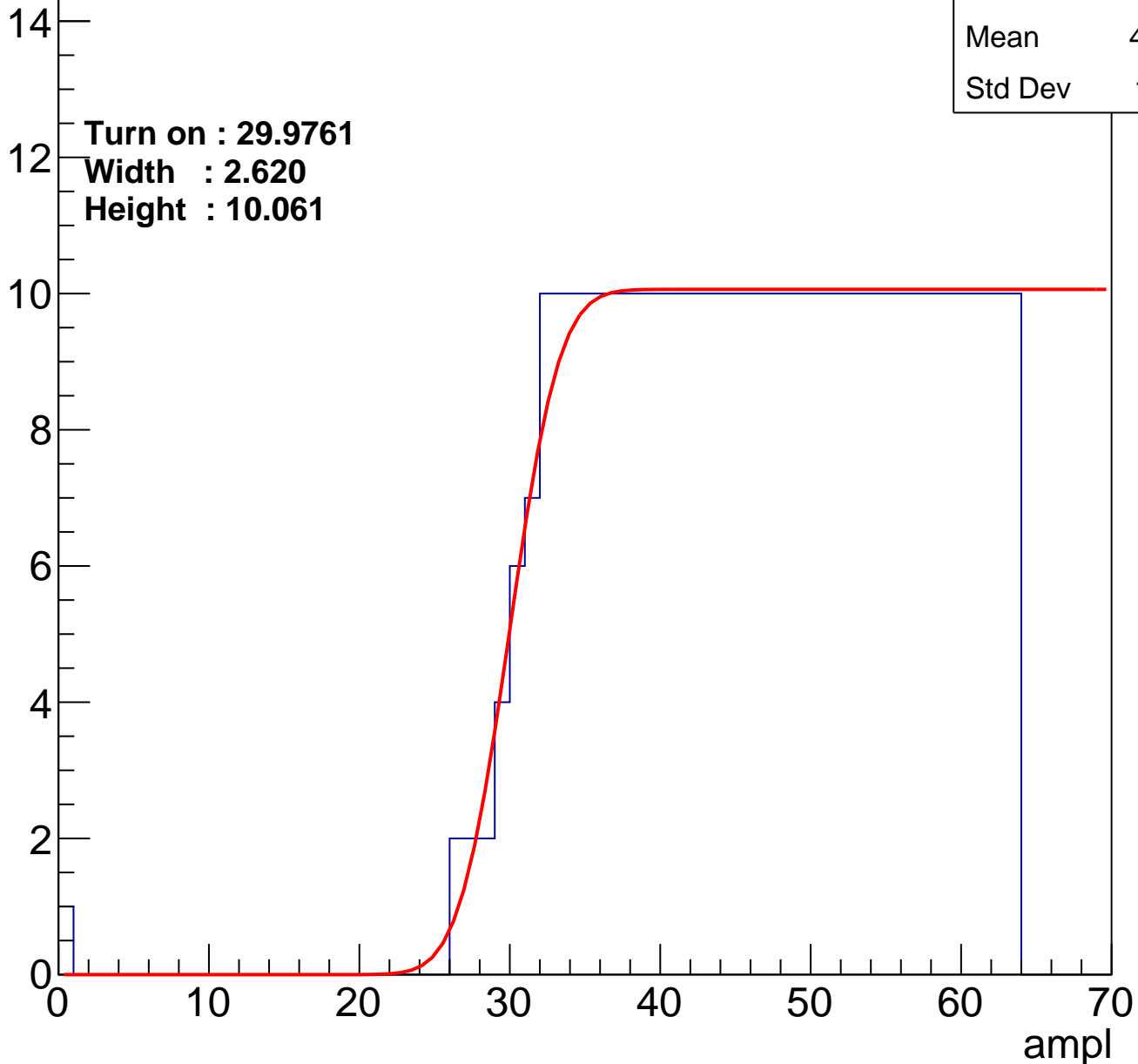
Entries	344
Mean	46.15
Std Dev	10.31

Turn on : 29.9761

Width : 2.620

Height : 10.061

Entry



B1L102S, U22-ch54

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 27.2032

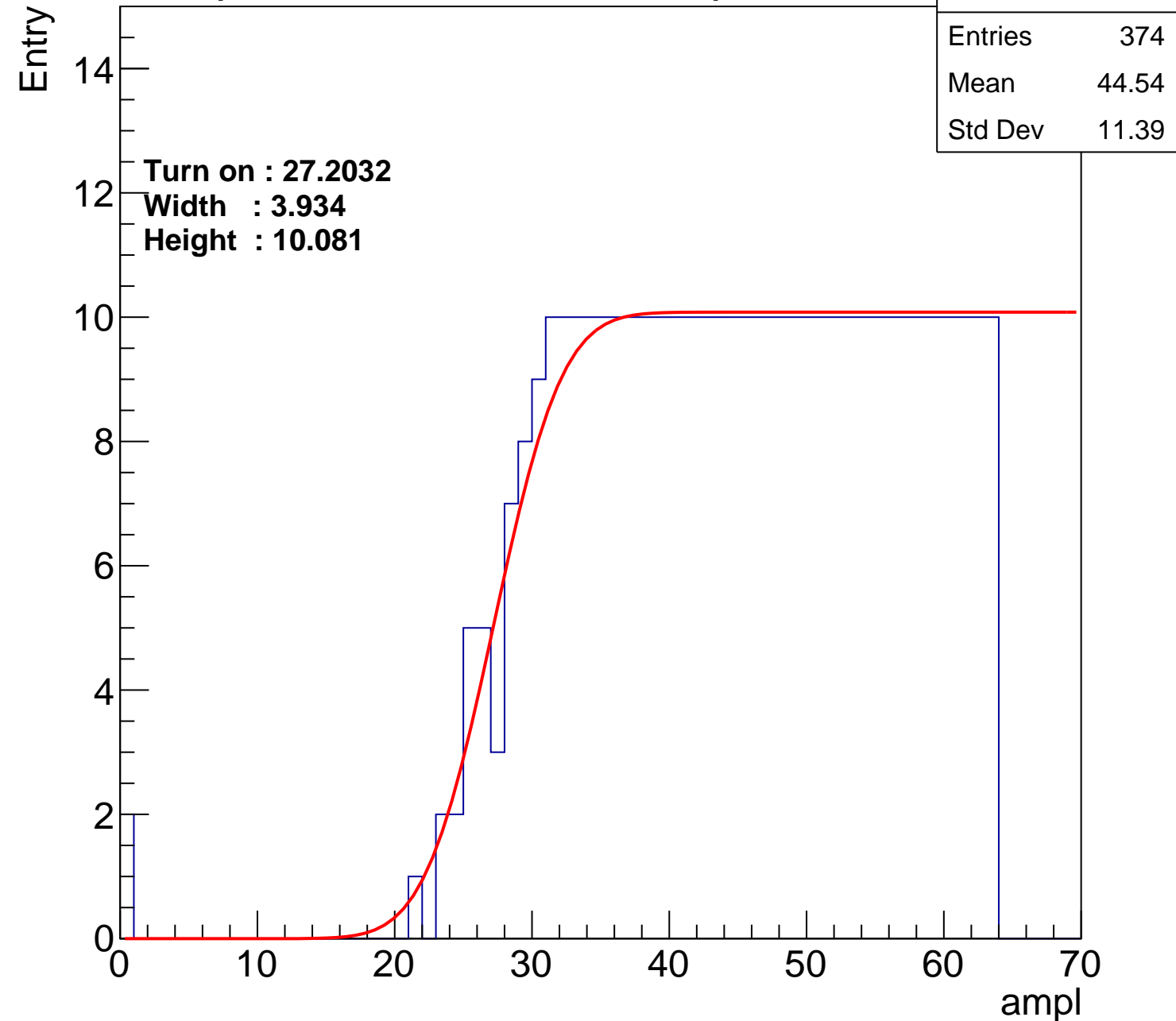
Width : 3.934

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch55

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.61
Std Dev	11.33

Turn on : 27.0178

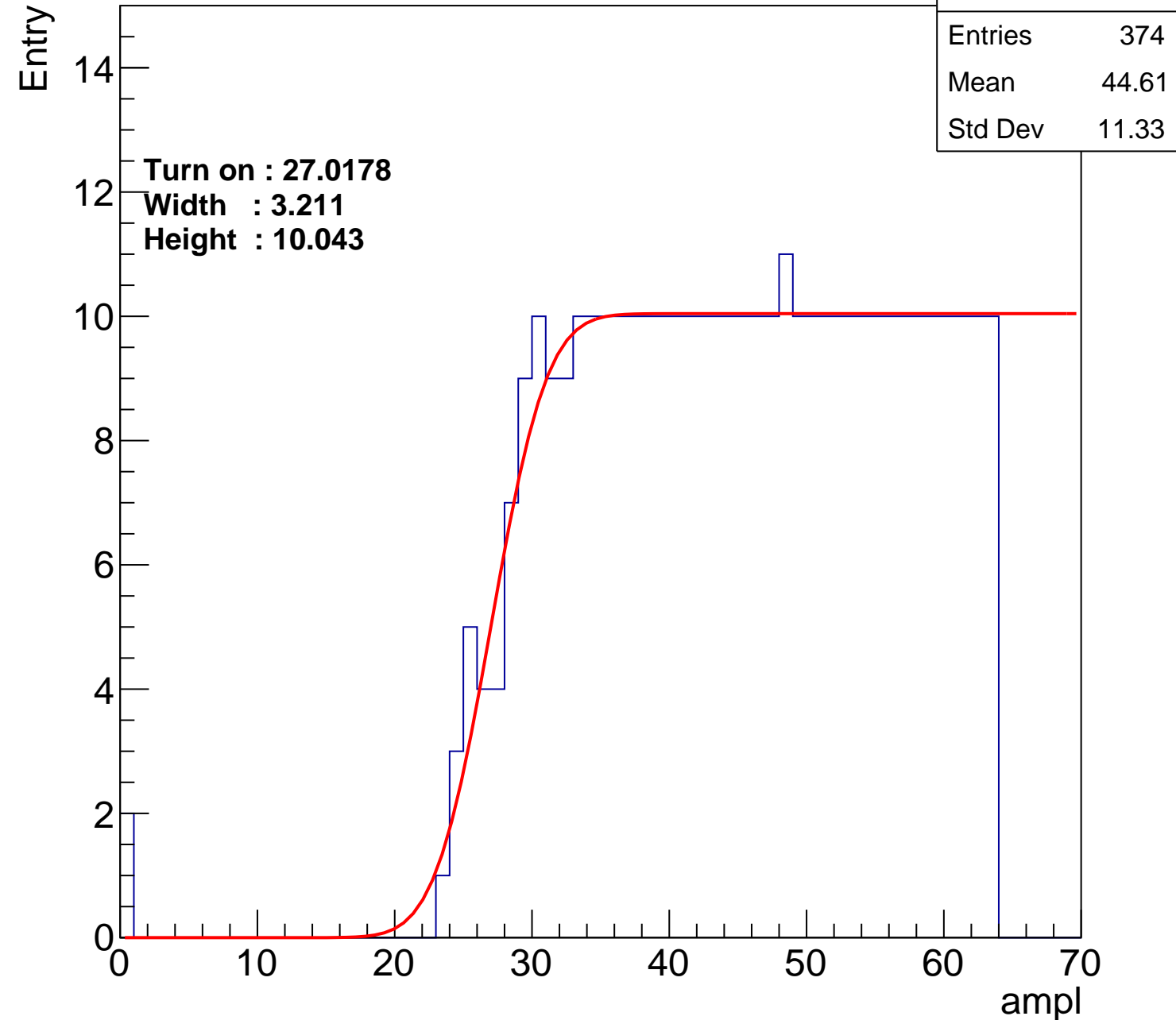
Width : 3.211

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch56

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	43.11
Std Dev	11.99

Turn on : 23.7695

Width : 3.003

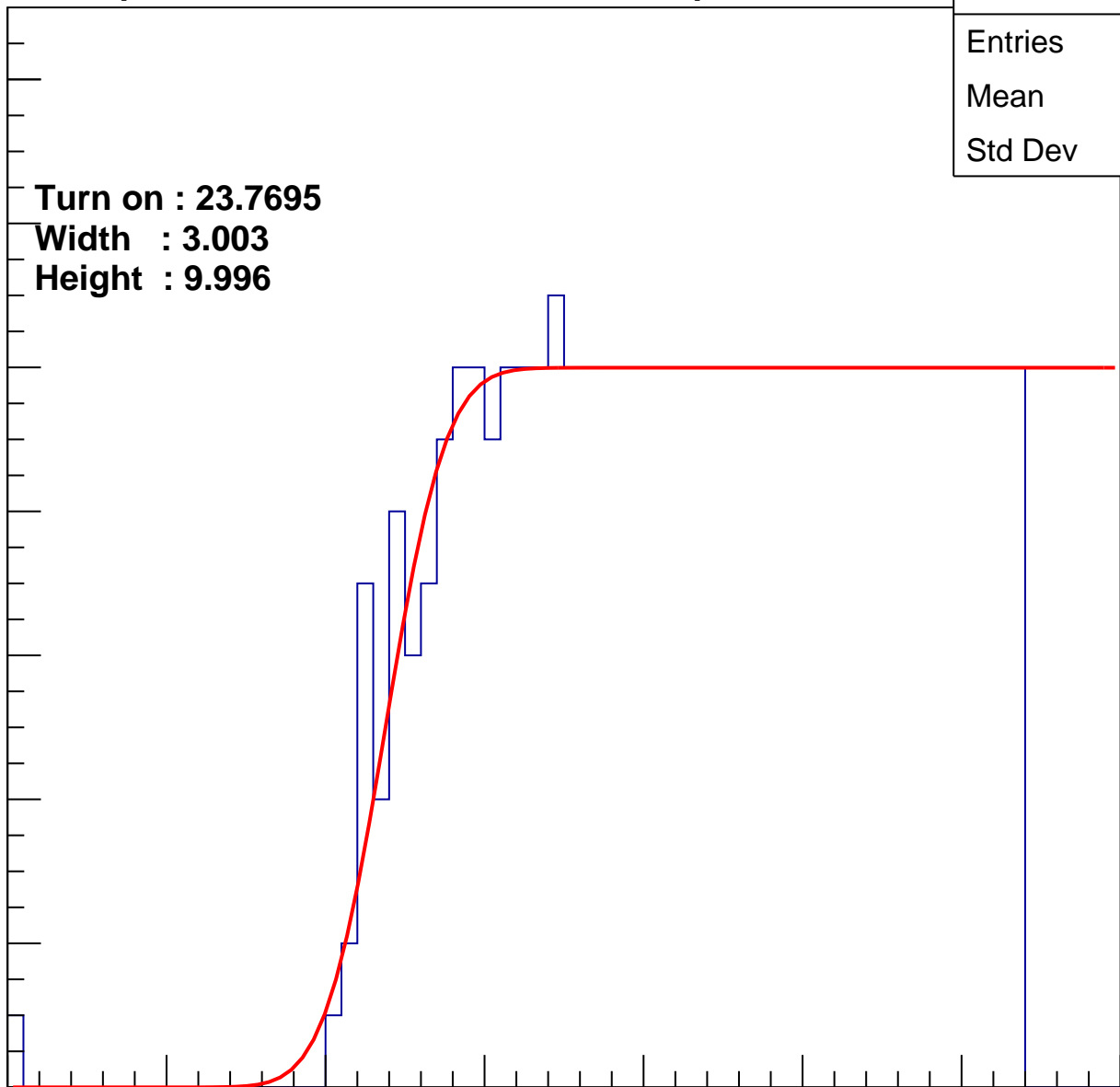
Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch57

calib_packv5_042523_0143.root, FC#11, port A2

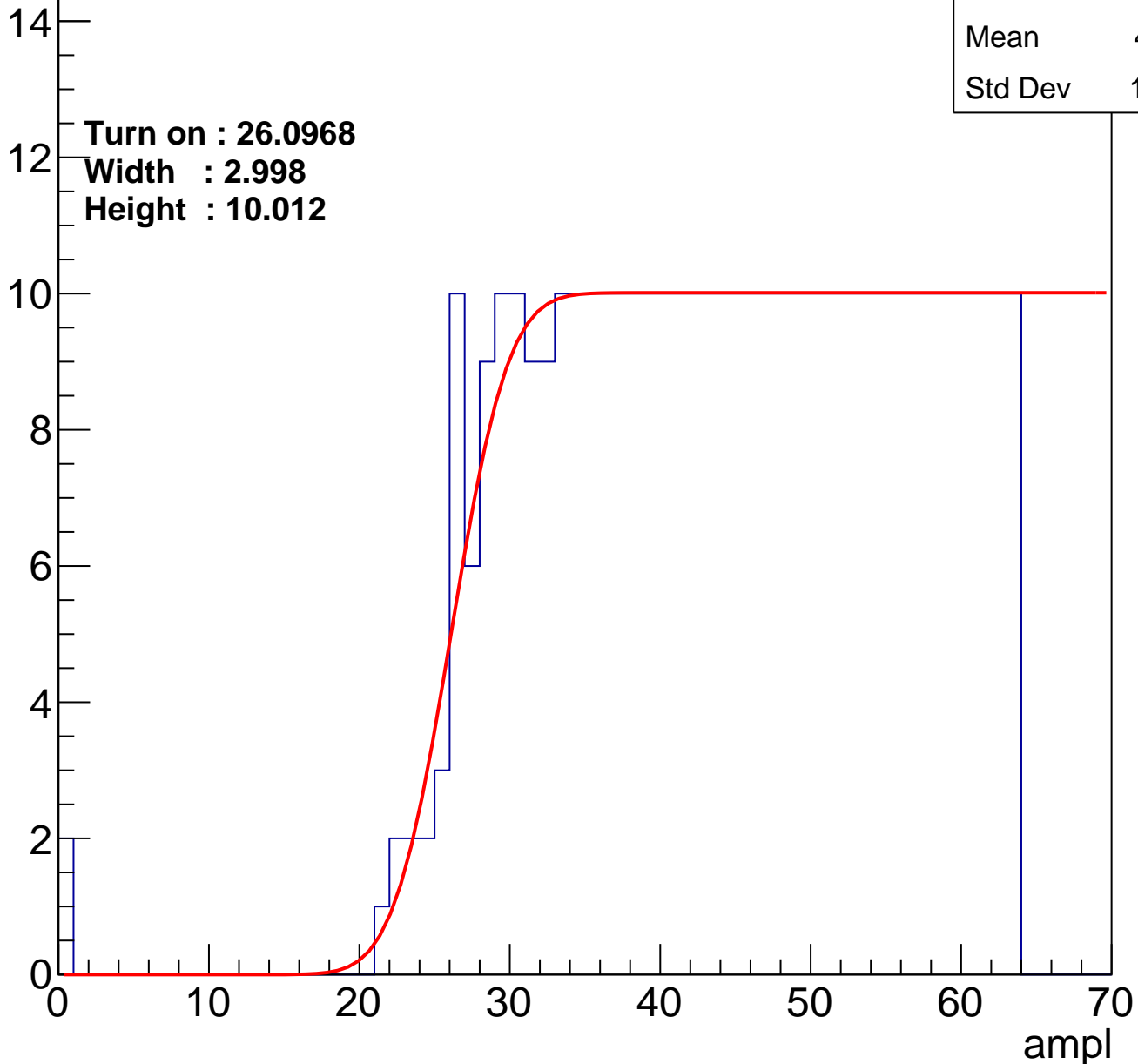
Entries	385
Mean	44.01
Std Dev	11.64

Turn on : 26.0968

Width : 2.998

Height : 10.012

Entry



B1L102S, U22-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.91
Std Dev	11.95

Turn on : 25.3825

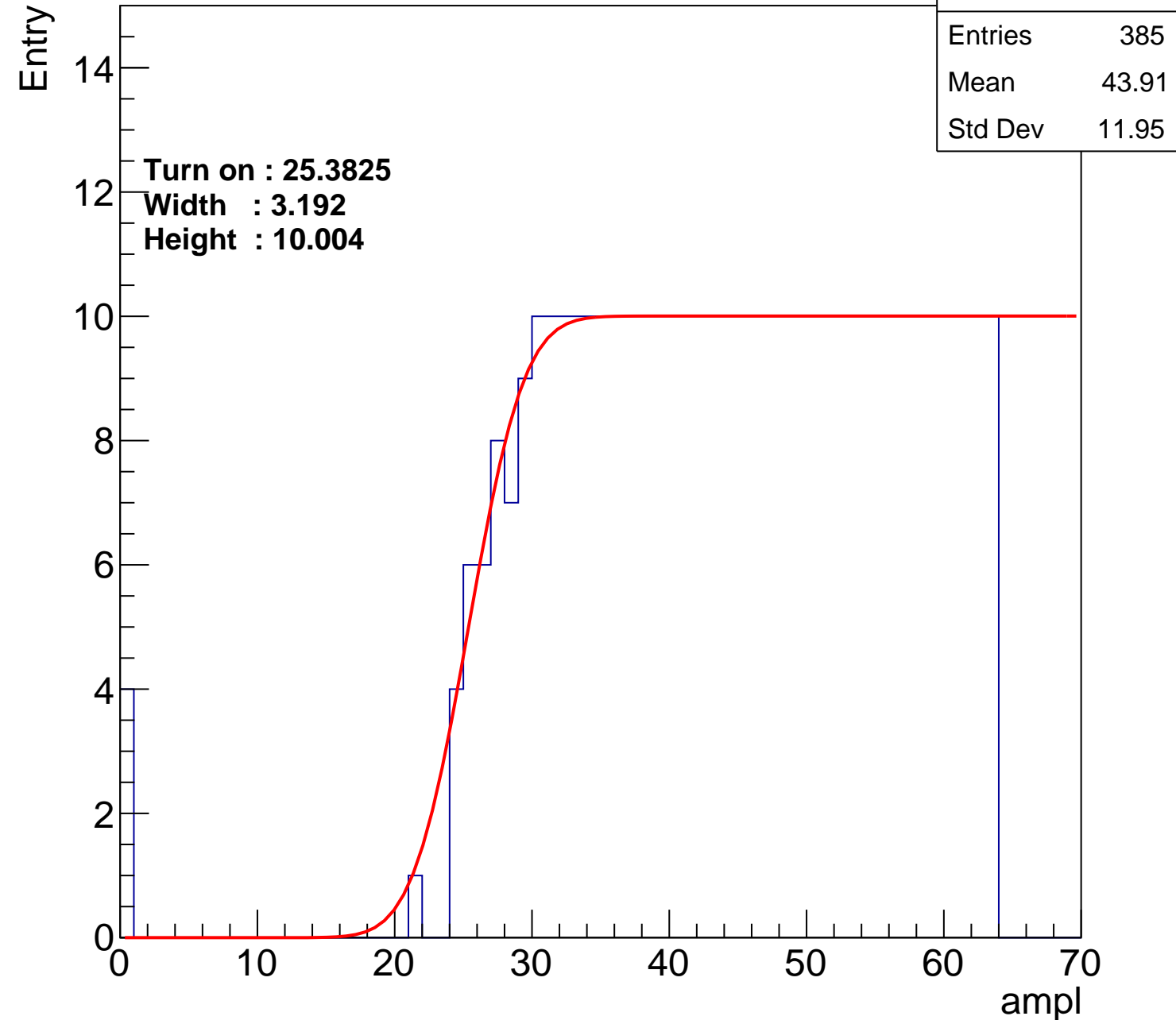
Width : 3.192

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.3323

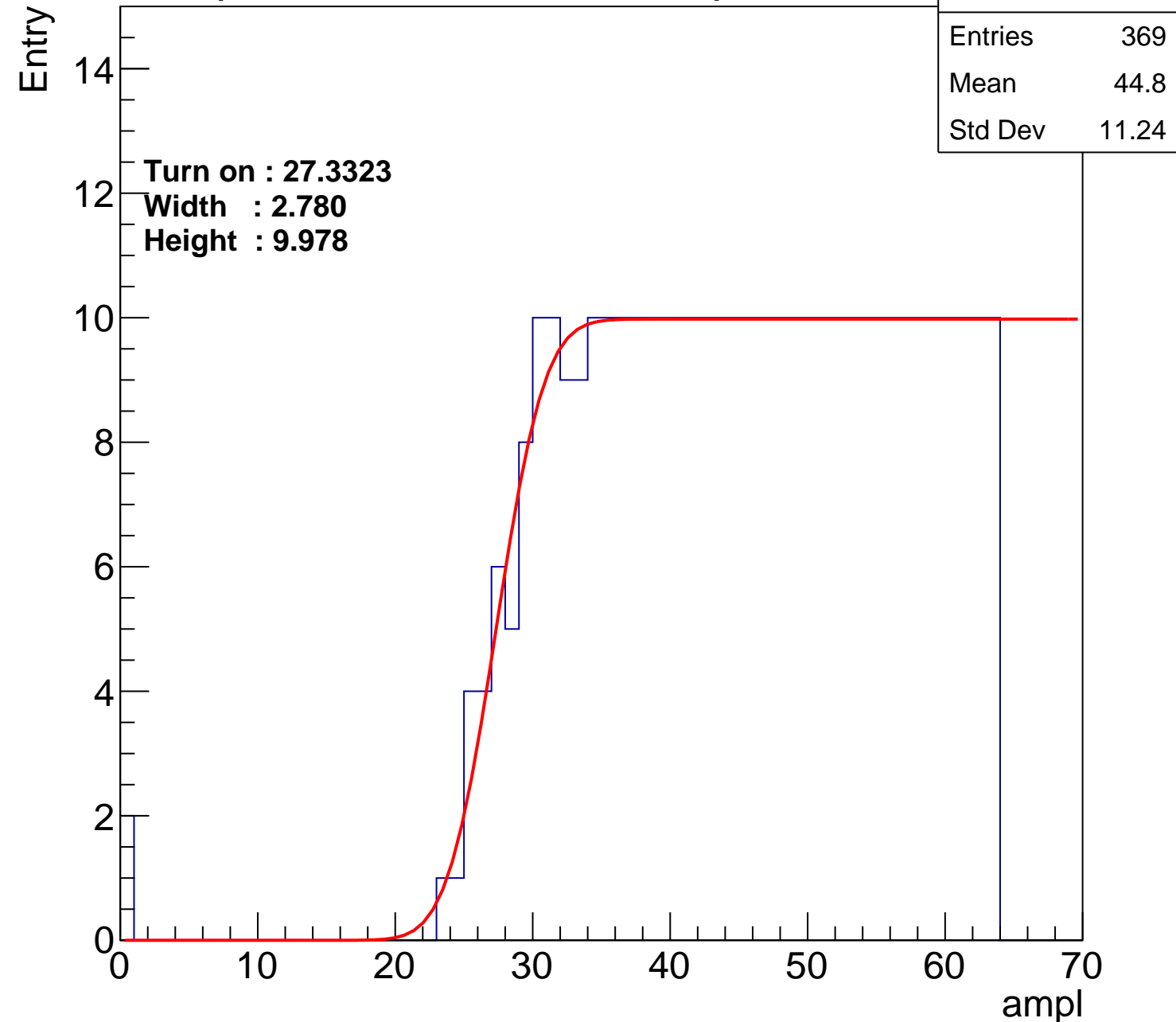
Width : 2.780

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch60

calib_packv5_042523_0143.root, FC#11, port A2

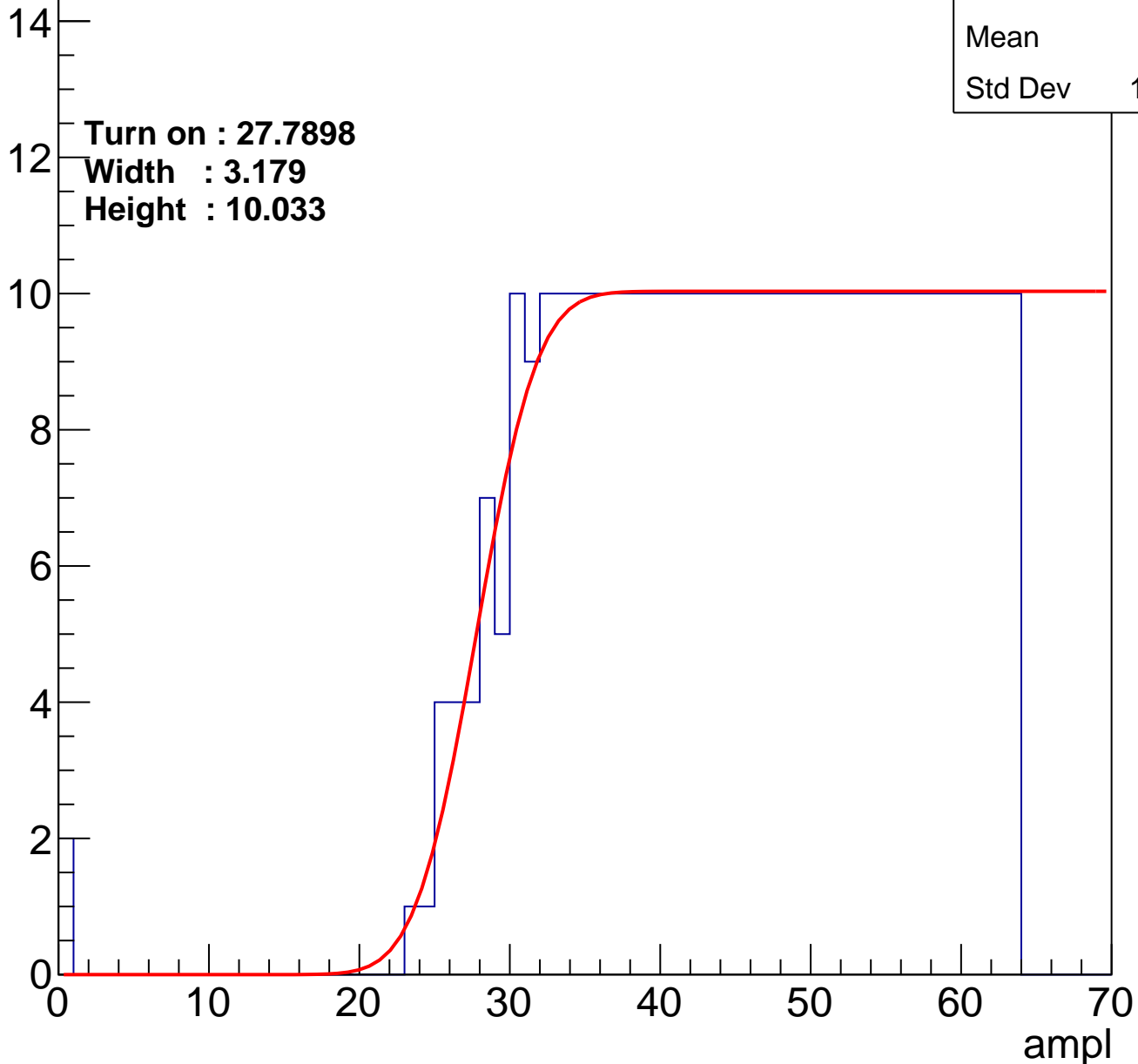
Entry

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.7898

Width : 3.179

Height : 10.033



B1L102S, U22-ch61

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.22
Std Dev	11.34

Turn on : 26.1953

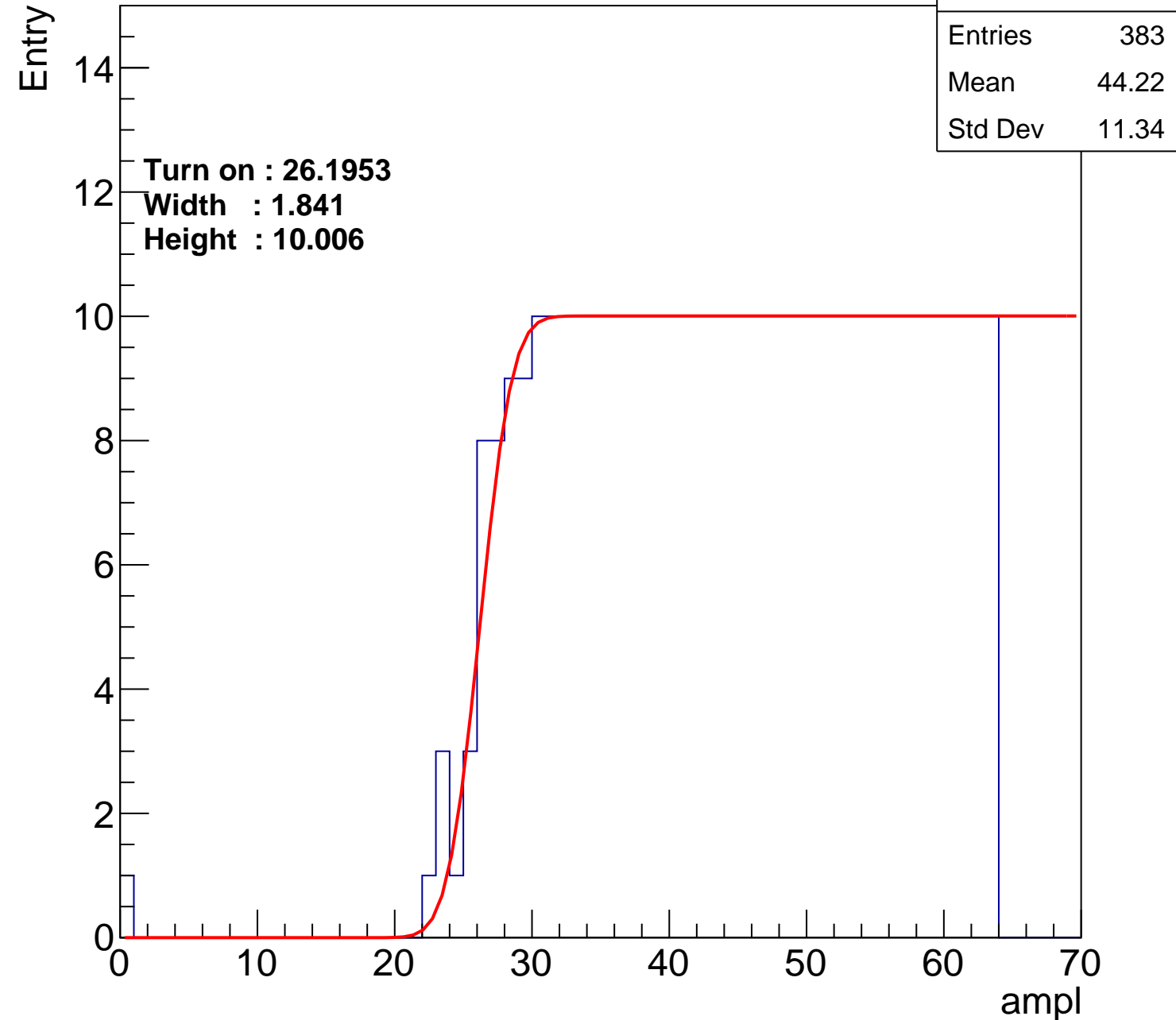
Width : 1.841

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch62

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.98
Std Dev	11.53

Turn on : 25.7830

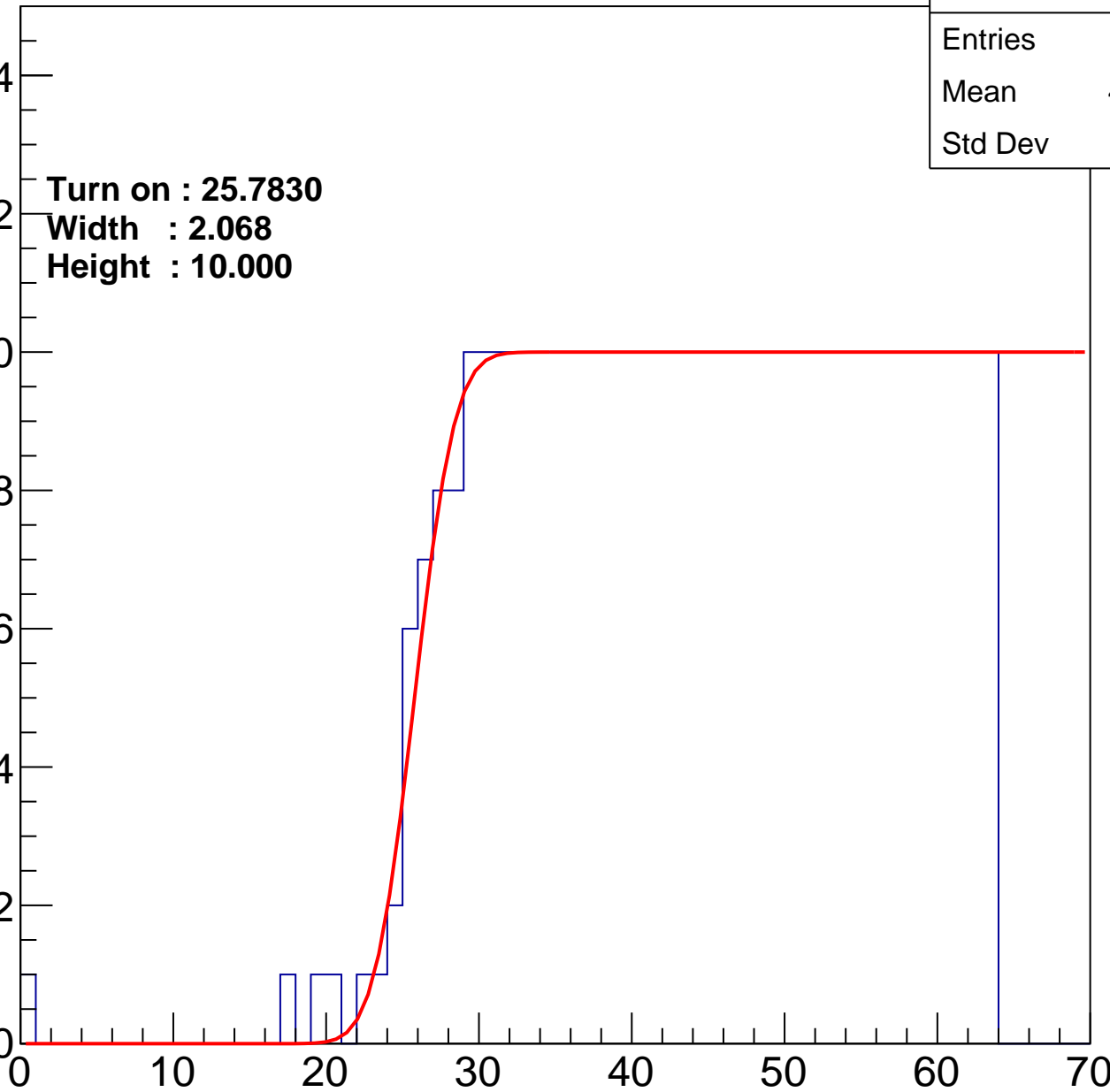
Width : 2.068

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch63

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.52
Std Dev	11.42

Turn on : 27.2477

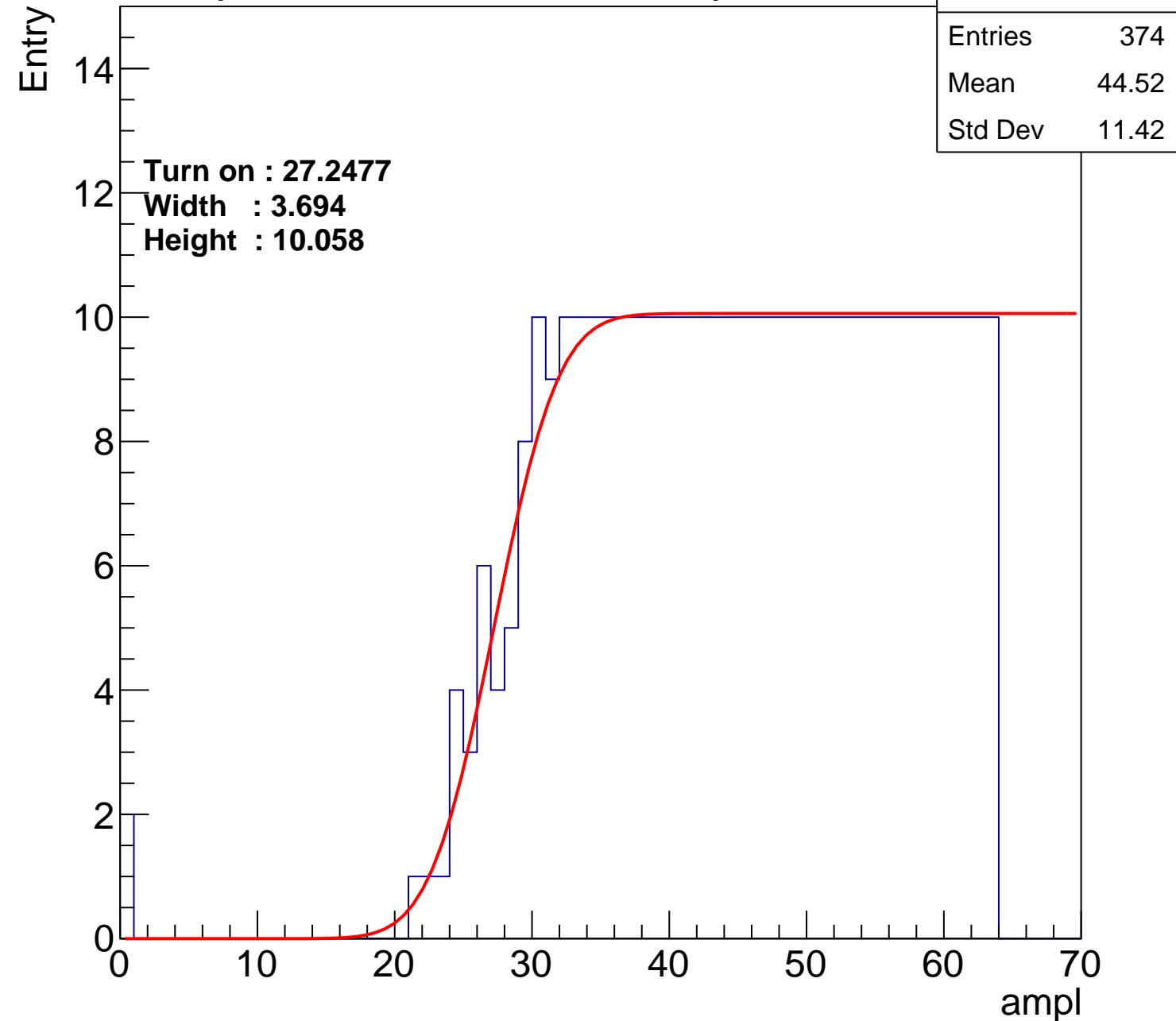
Width : 3.694

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch64

calib_packv5_042523_0143.root, FC#11, port A2

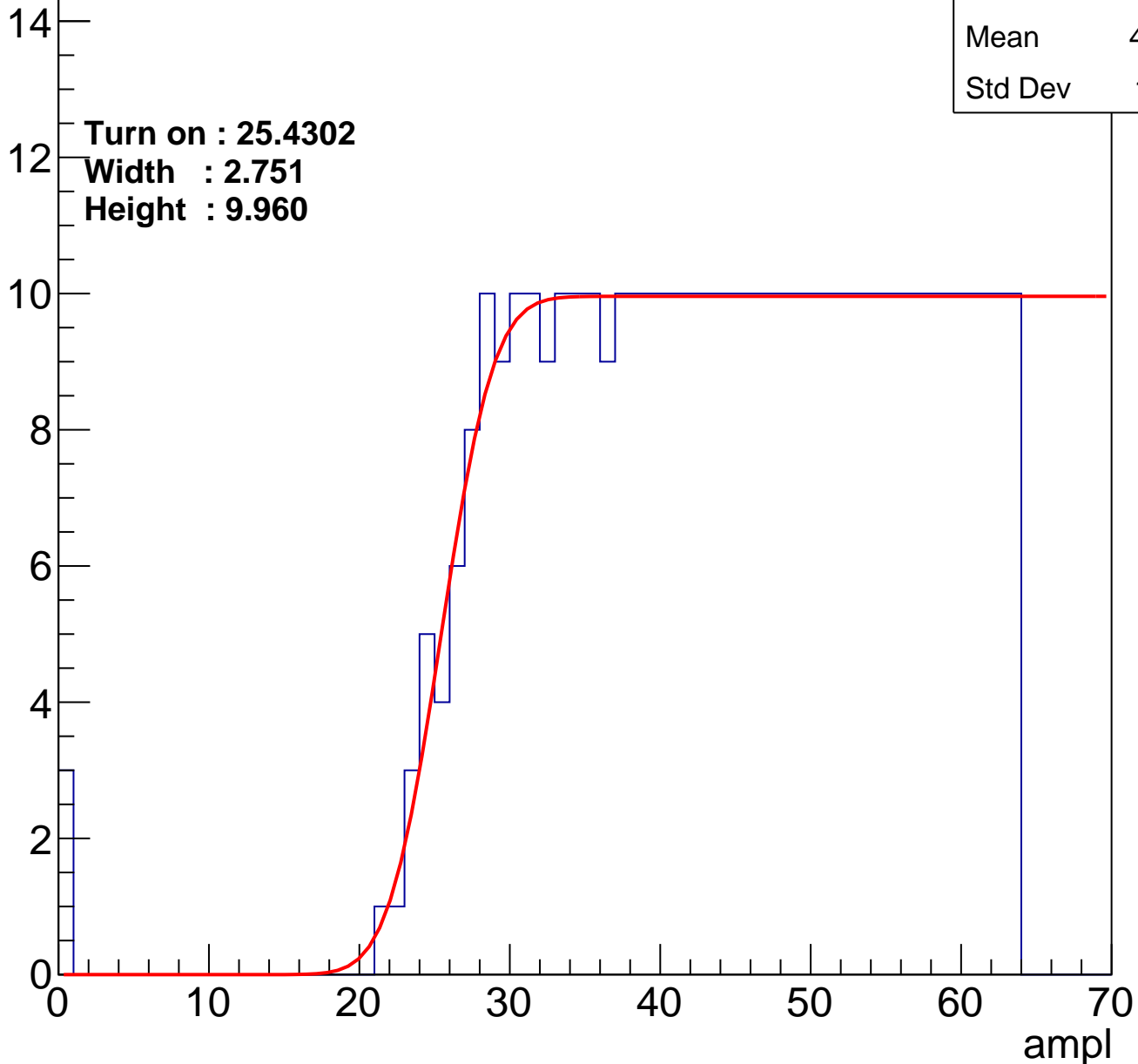
Entries	388
Mean	43.78
Std Dev	11.91

Turn on : 25.4302

Width : 2.751

Height : 9.960

Entry



B1L102S, U22-ch65

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	43.89
Std Dev	12.16

Turn on : 26.5762

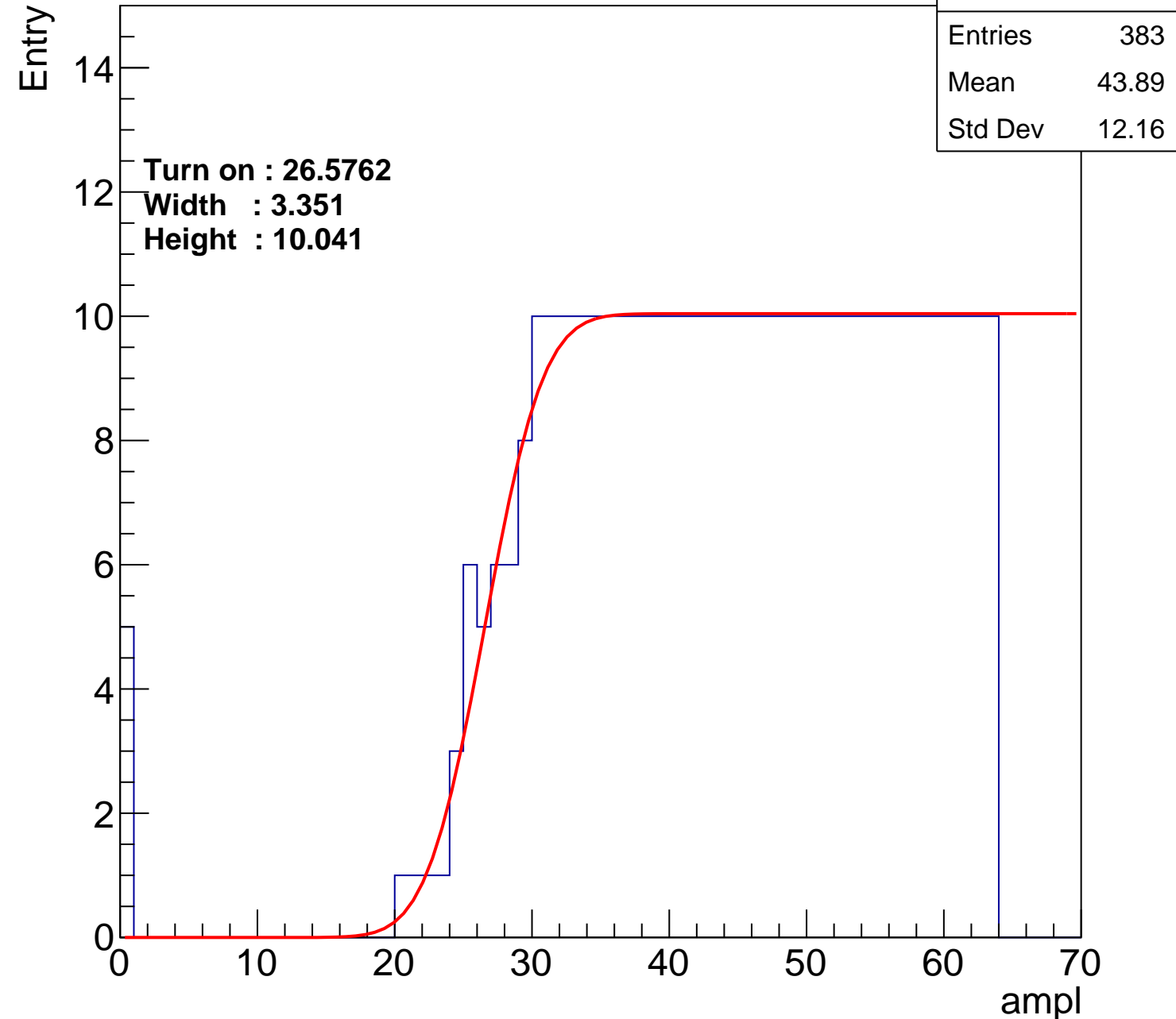
Width : 3.351

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch66

calib_packv5_042523_0143.root, FC#11, port A2

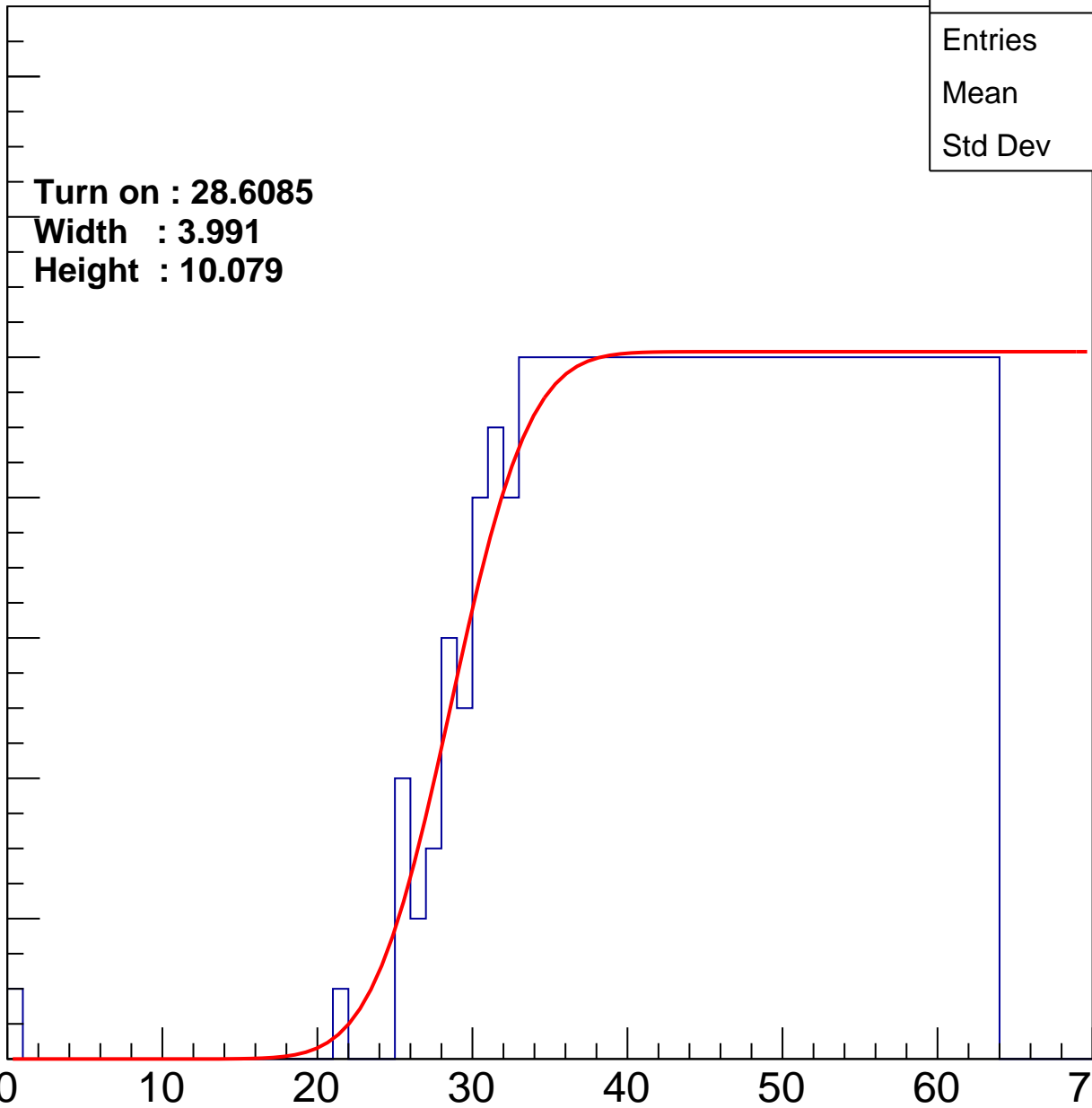
Entry

14
12
10
8
6
4
2
0

Turn on : 28.6085
Width : 3.991
Height : 10.079

Entries	357
Mean	45.44
Std Dev	10.76

ampl



B1L102S, U22-ch67

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.79
Std Dev	11.09

Turn on : 27.7376

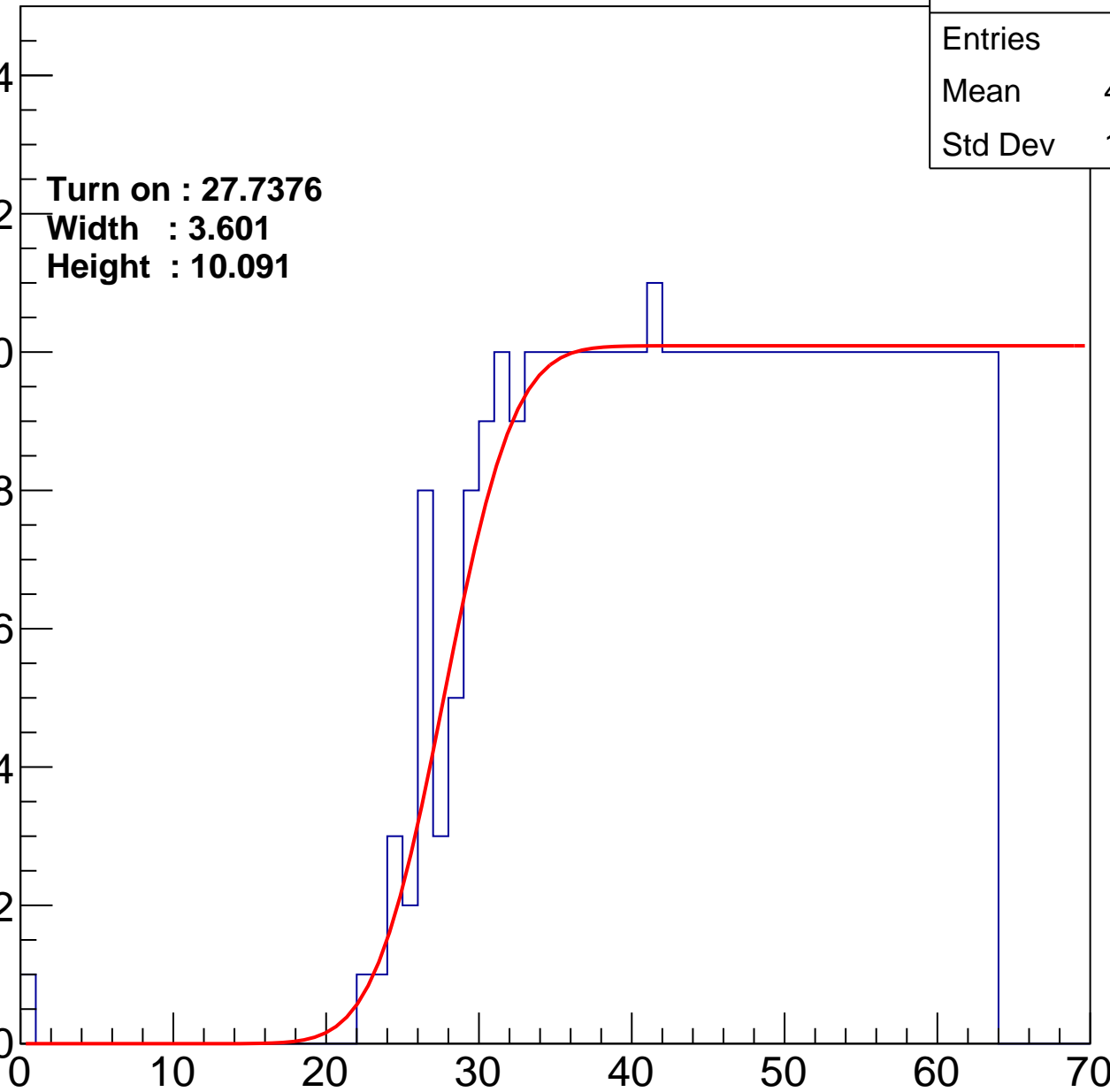
Width : 3.601

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch68

calib_packv5_042523_0143.root, FC#11, port A2

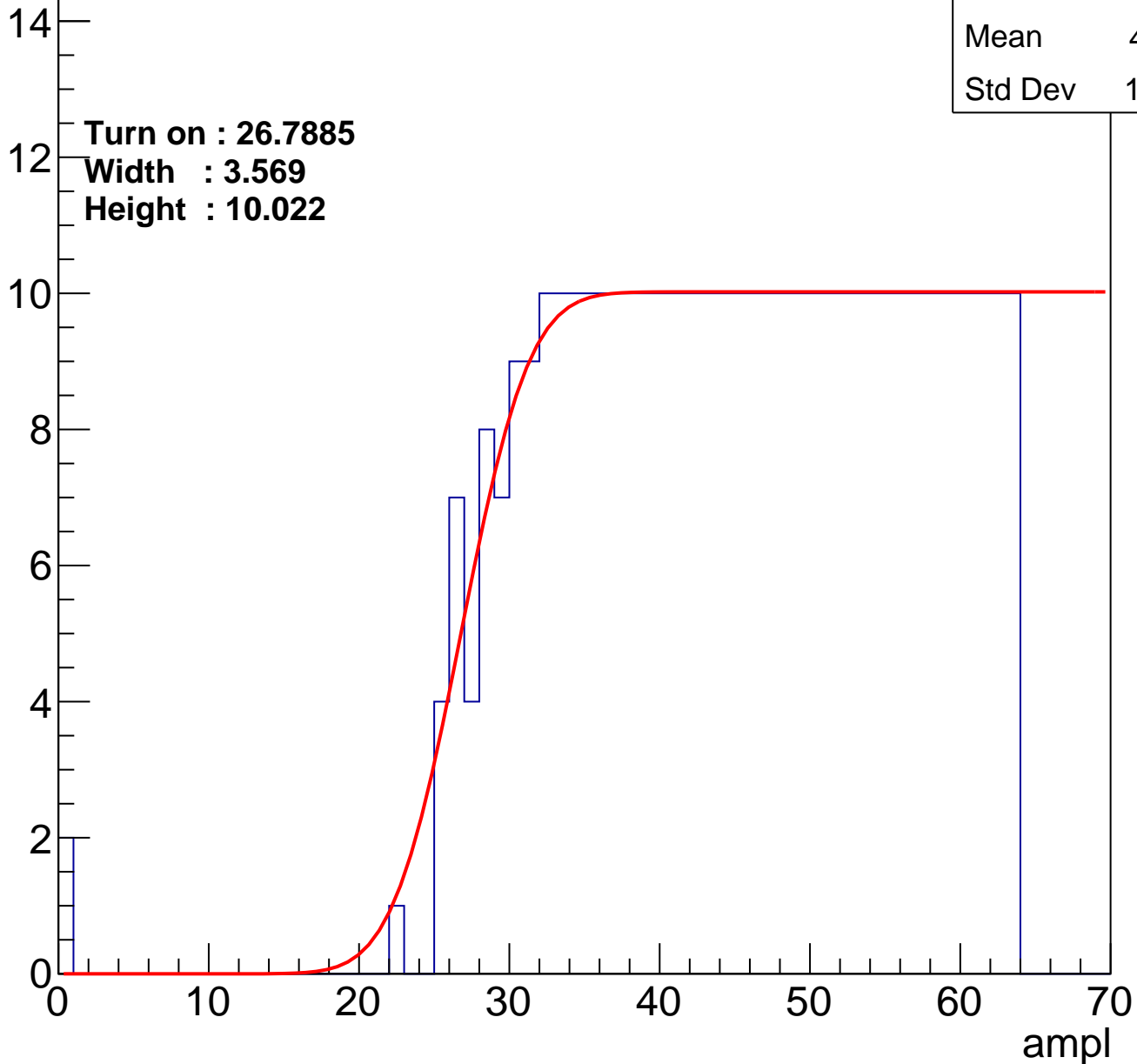
Entries	371
Mean	44.71
Std Dev	11.27

Turn on : 26.7885

Width : 3.569

Height : 10.022

Entry



B1L102S, U22-ch69

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.15
Std Dev	11.06

Turn on : 28.0880

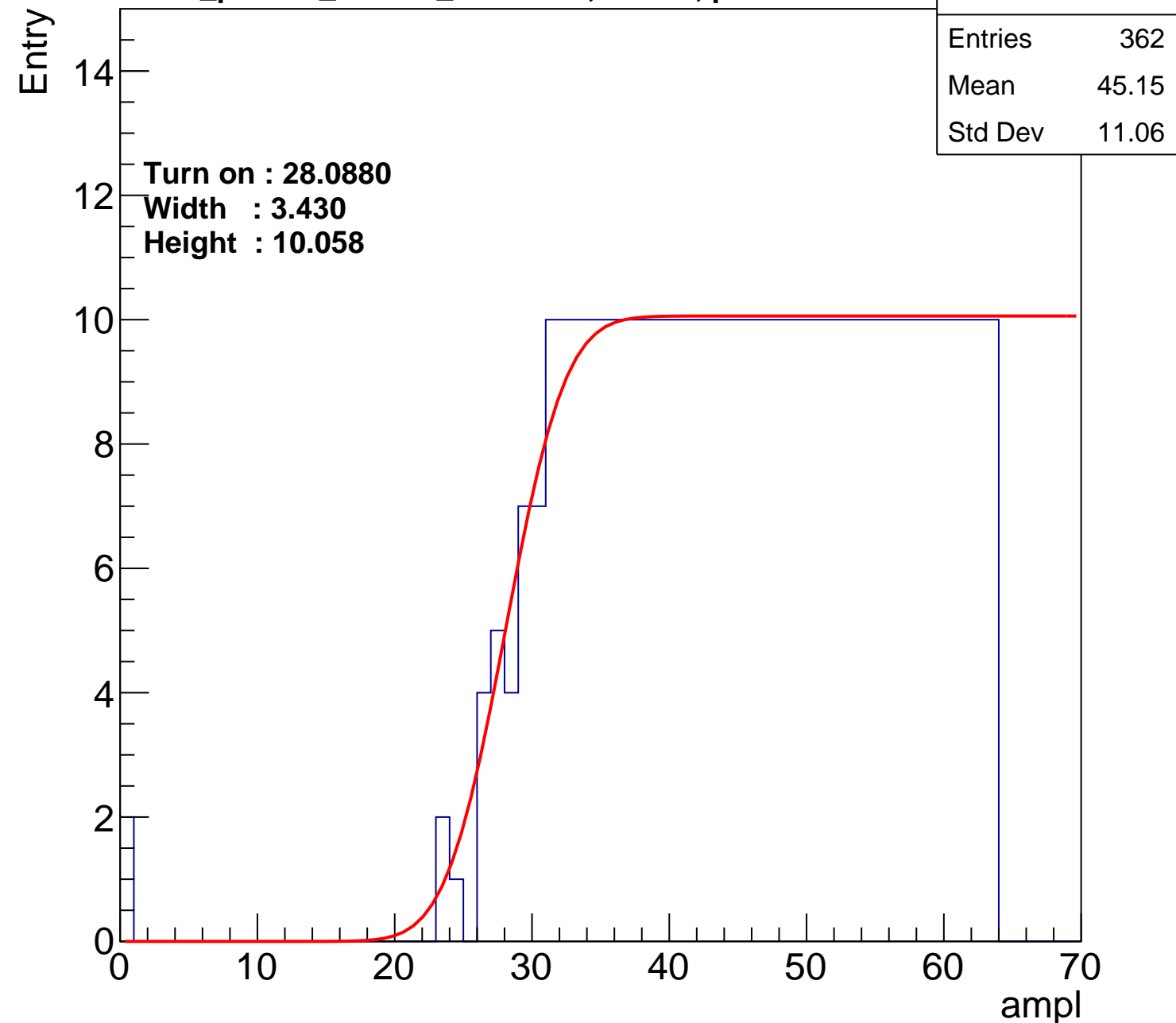
Width : 3.430

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch70

calib_packv5_042523_0143.root, FC#11, port A2

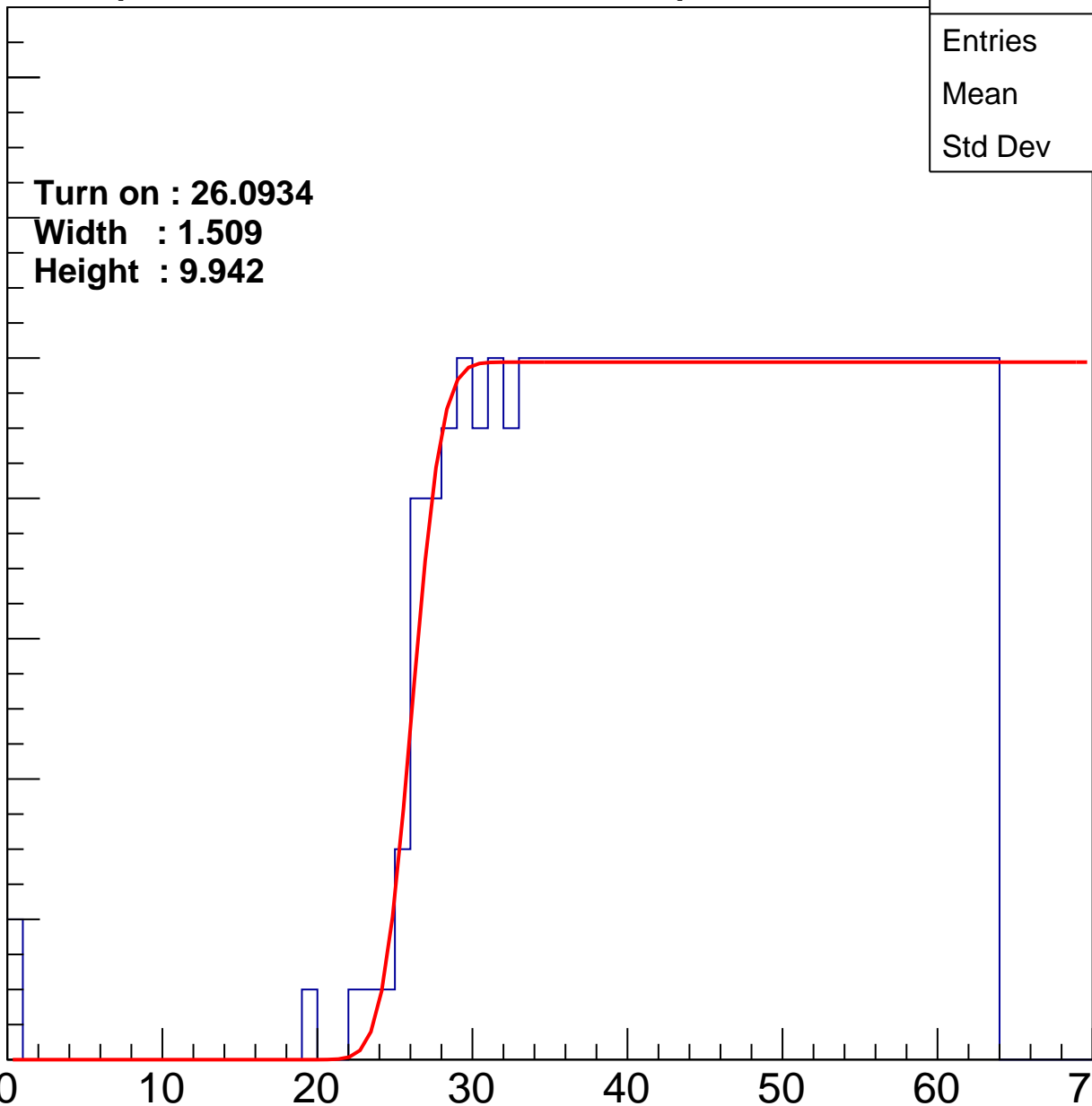
Entry

14
12
10
8
6
4
2
0

Turn on : 26.0934
Width : 1.509
Height : 9.942

Entries	382
Mean	44.18
Std Dev	11.54

ampl



B1L102S, U22-ch71

calib_packv5_042523_0143.root, FC#11, port A2

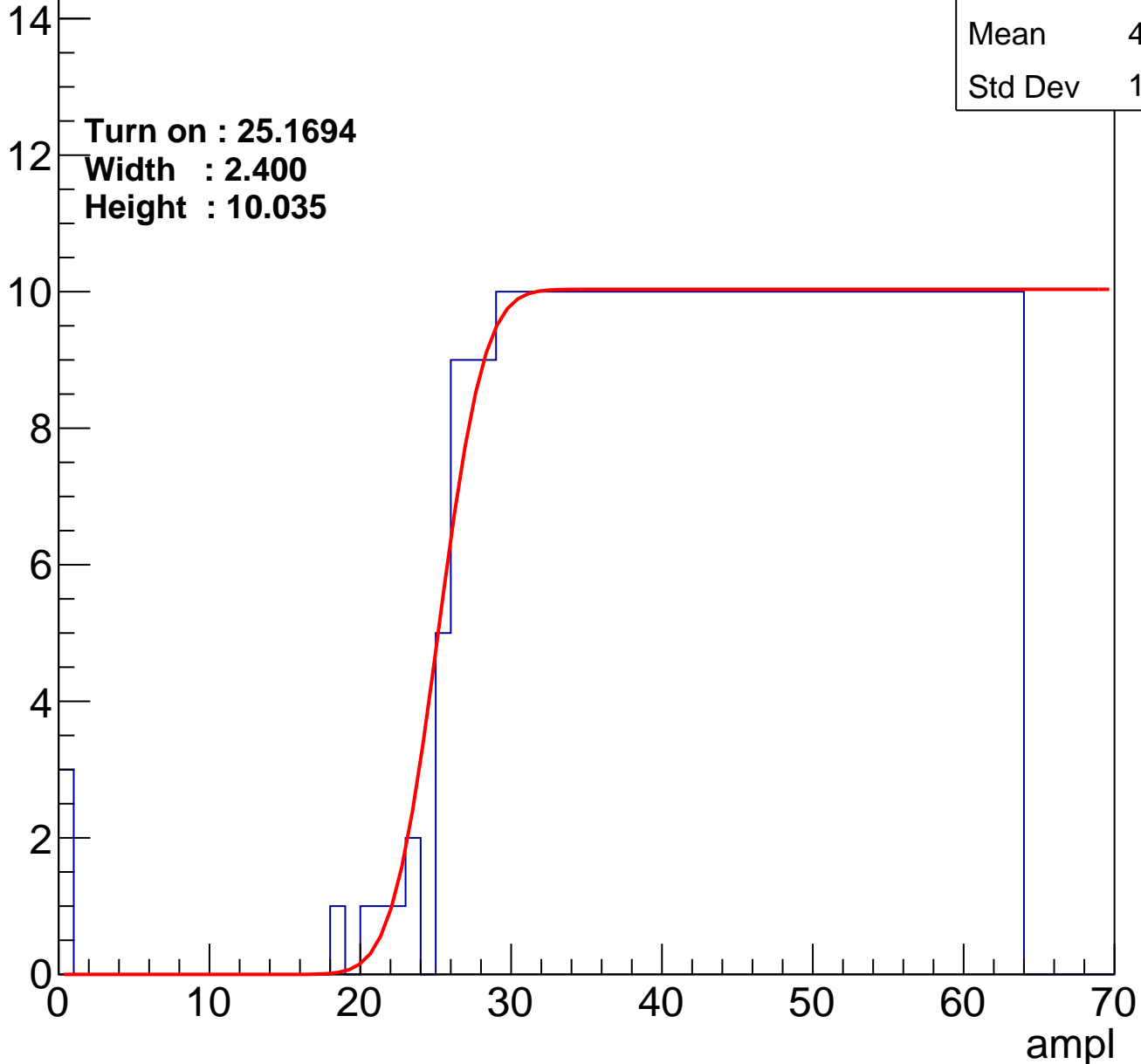
Entries	391
Mean	43.69
Std Dev	11.93

Turn on : 25.1694

Width : 2.400

Height : 10.035

Entry



B1L102S, U22-ch72

calib_packv5_042523_0143.root, FC#11, port A2

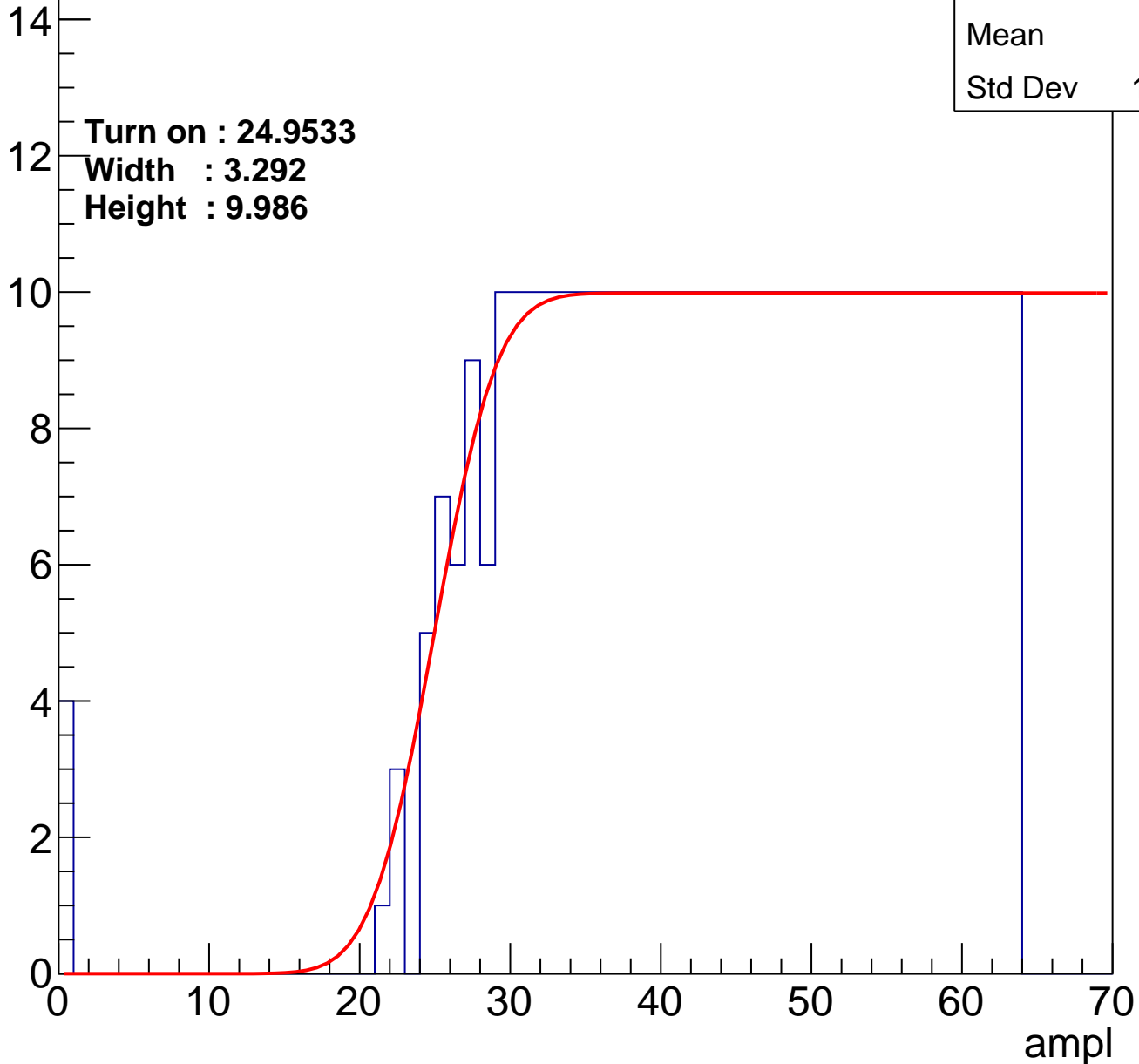
Entries	391
Mean	43.6
Std Dev	12.11

Turn on : 24.9533

Width : 3.292

Height : 9.986

Entry



B1L102S, U22-ch73

calib_packv5_042523_0143.root, FC#11, port A2

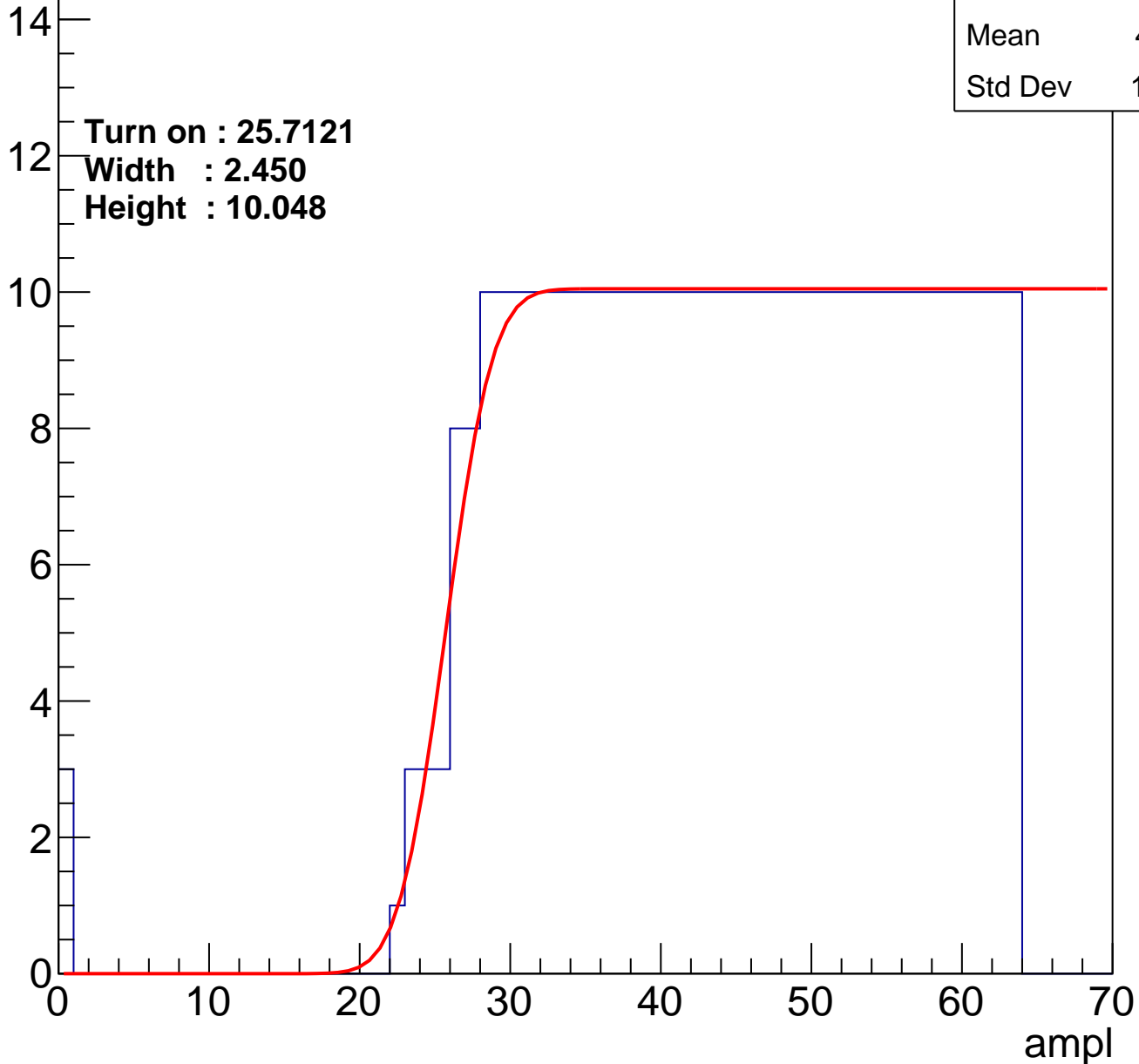
Entries	389
Mean	43.81
Std Dev	11.83

Turn on : 25.7121

Width : 2.450

Height : 10.048

Entry



B1L102S, U22-ch74

calib_packv5_042523_0143.root, FC#11, port A2

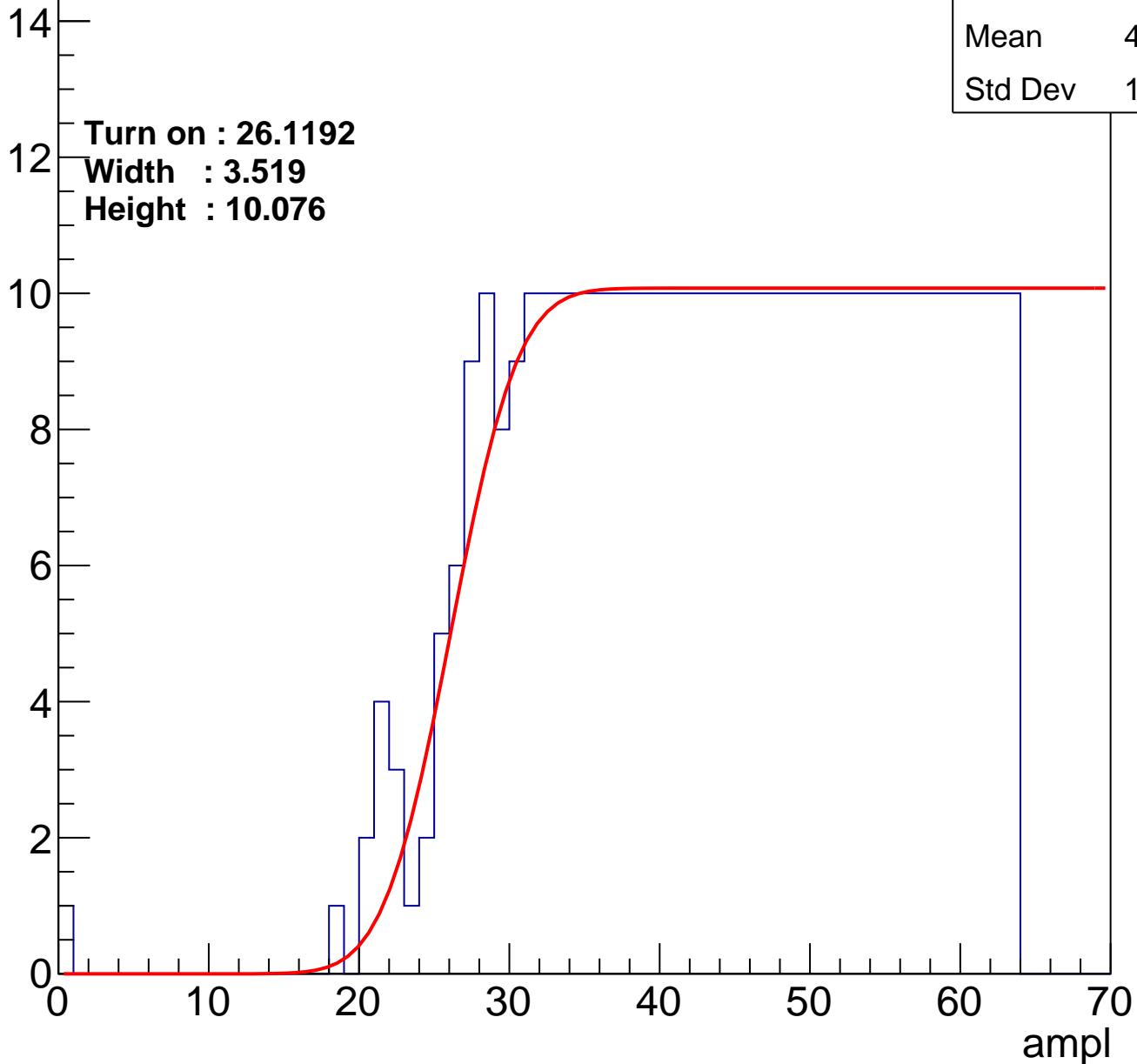
Entries	391
Mean	43.72
Std Dev	11.74

Turn on : 26.1192

Width : 3.519

Height : 10.076

Entry



B1L102S, U22-ch75

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.81
Std Dev	11.22

Turn on : 27.7894

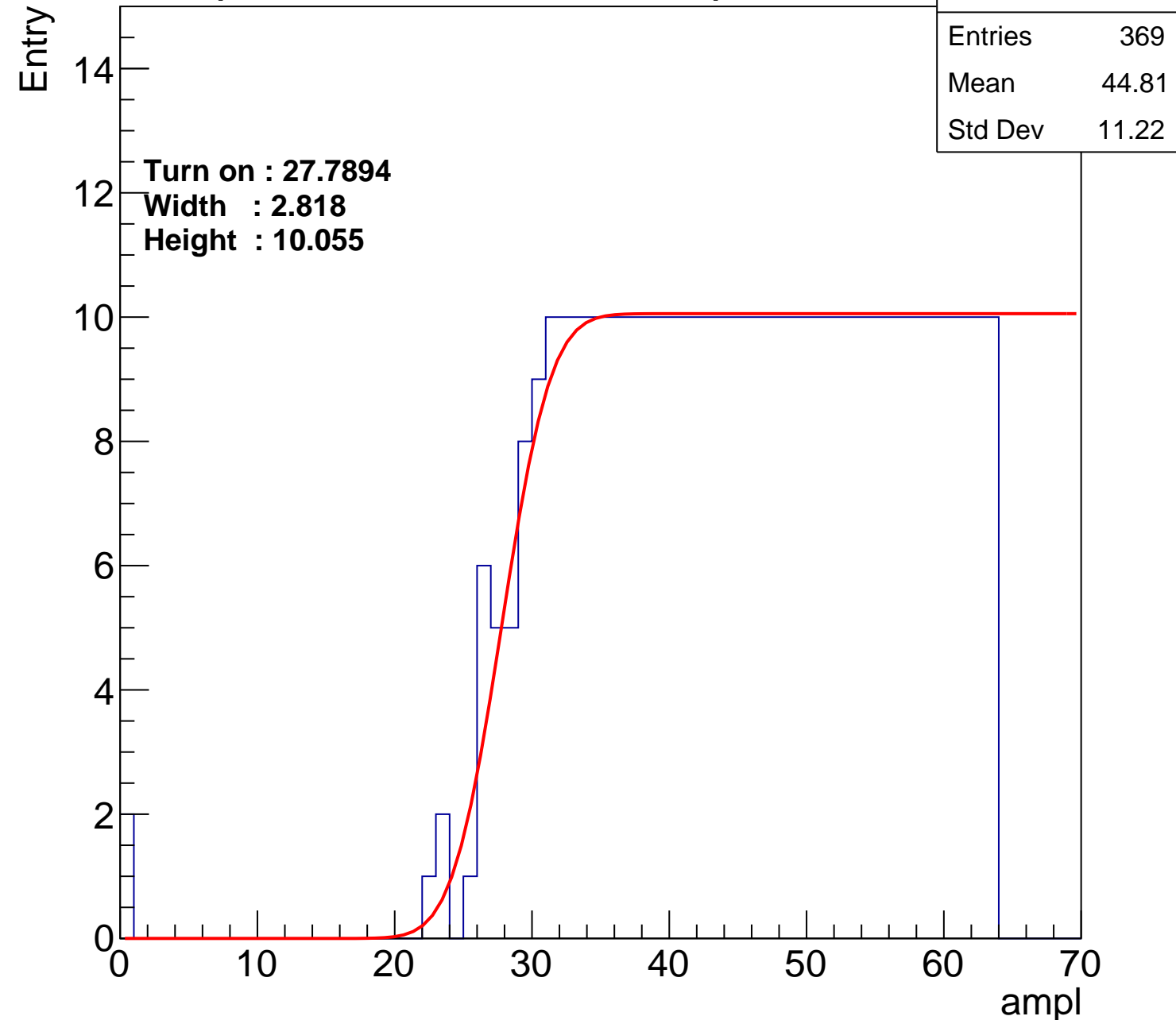
Width : 2.818

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch76

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.94
Std Dev	11.95

Turn on : 26.2079

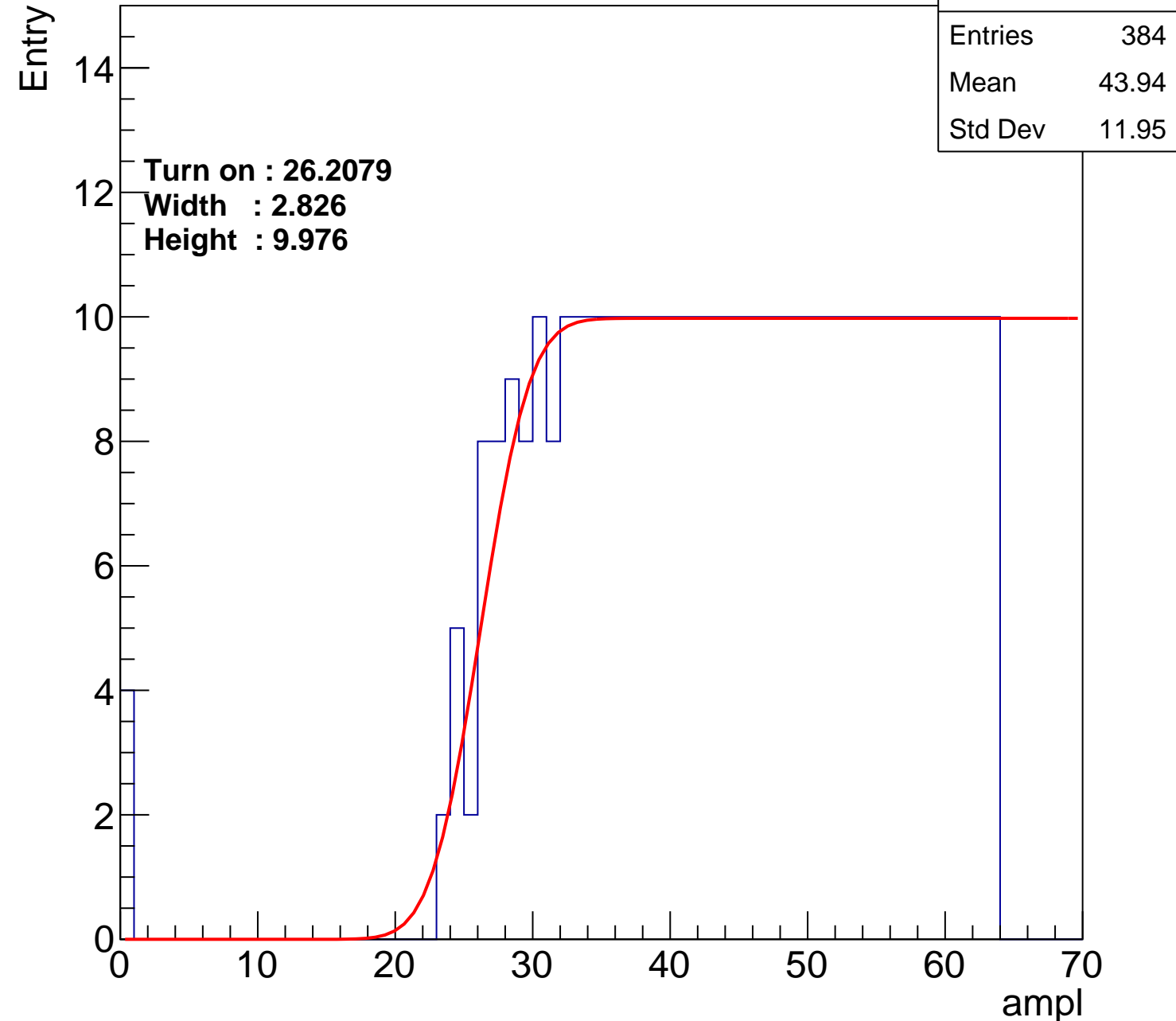
Width : 2.826

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch77

calib_packv5_042523_0143.root, FC#11, port A2

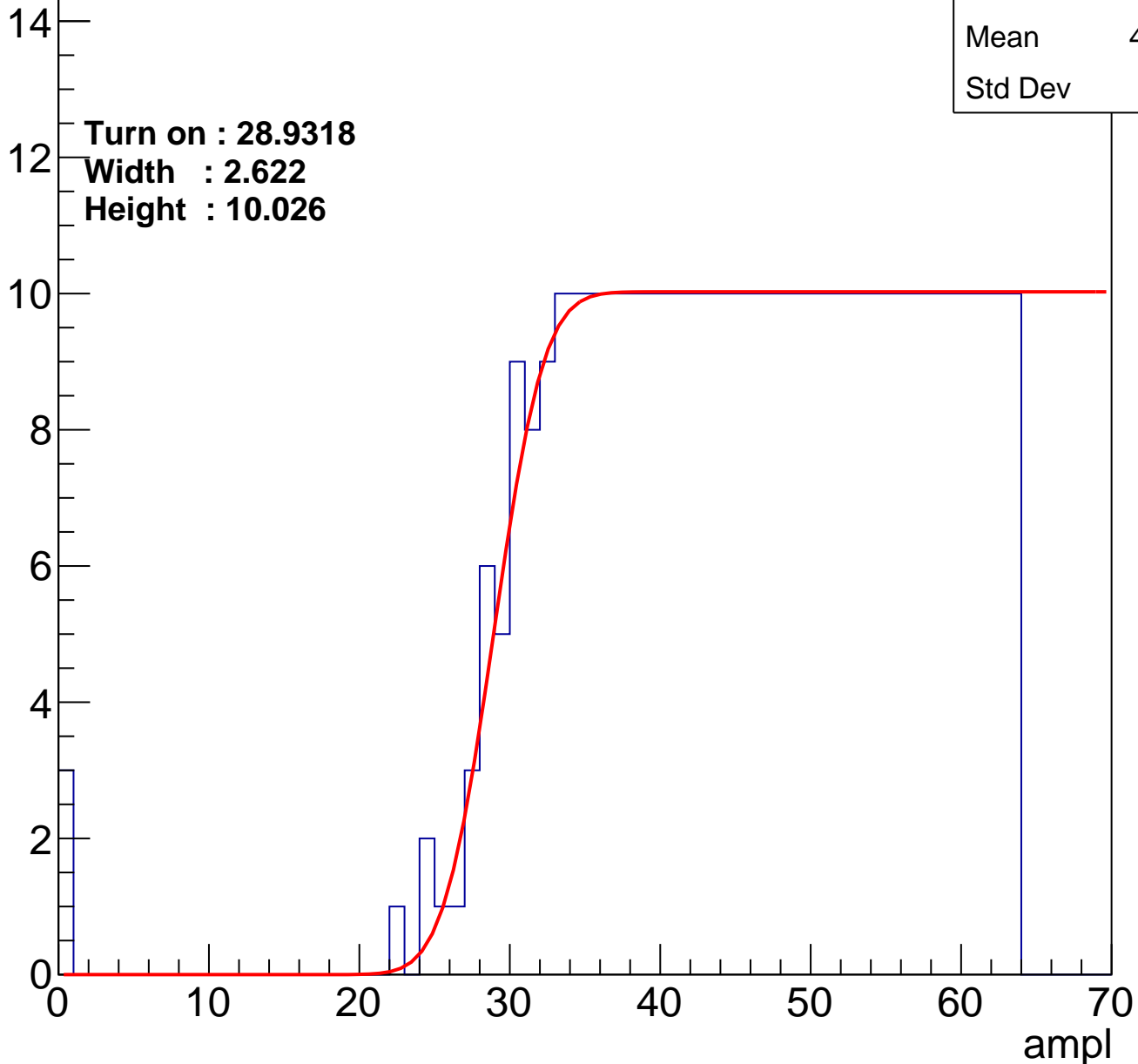
Entries	358
Mean	45.25
Std Dev	11.2

Turn on : 28.9318

Width : 2.622

Height : 10.026

Entry



B1L102S, U22-ch78

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.45
Std Dev	11.62

Turn on : 27.1507

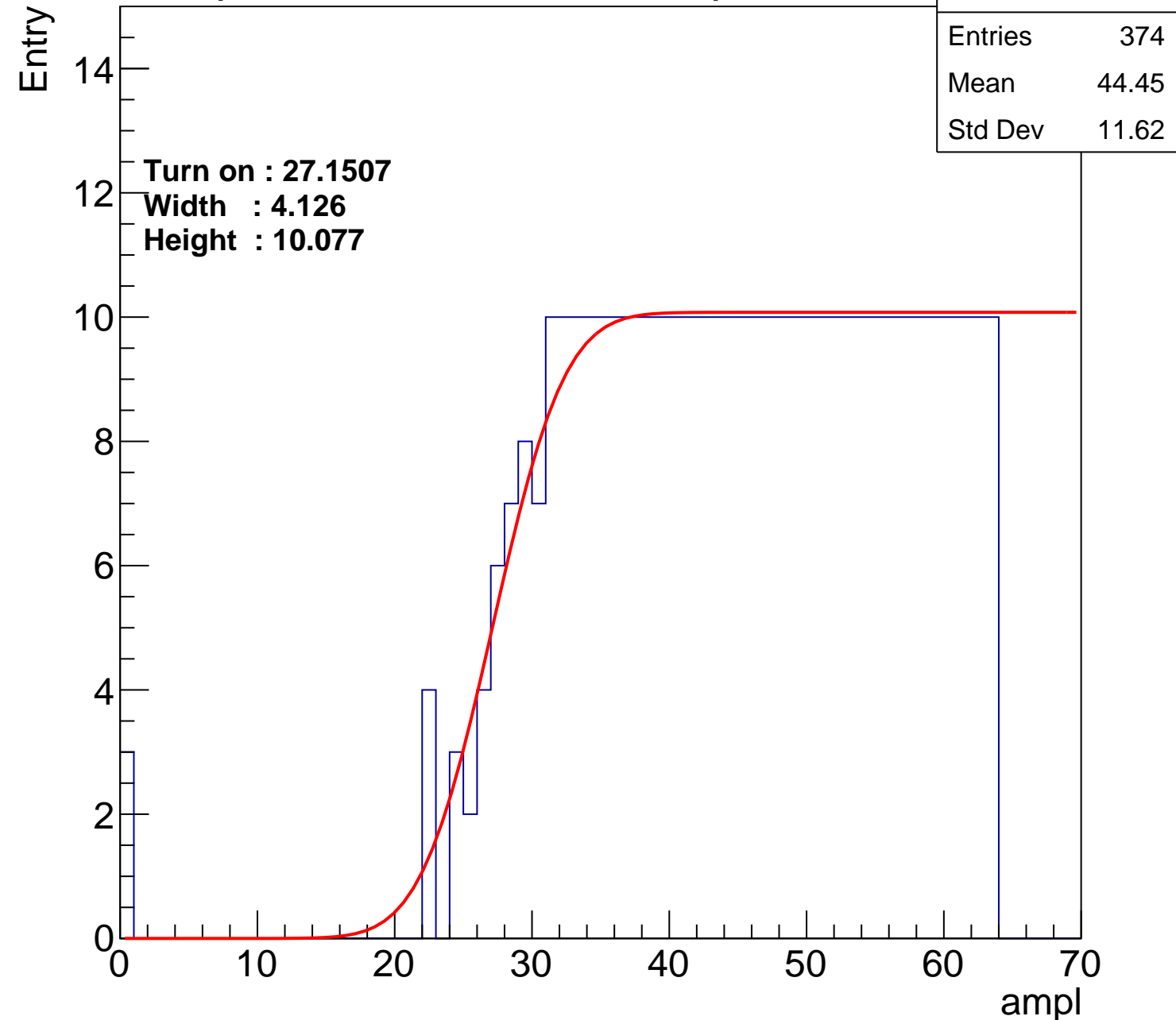
Width : 4.126

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch79

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.75
Std Dev	11.93

Turn on : 26.0875

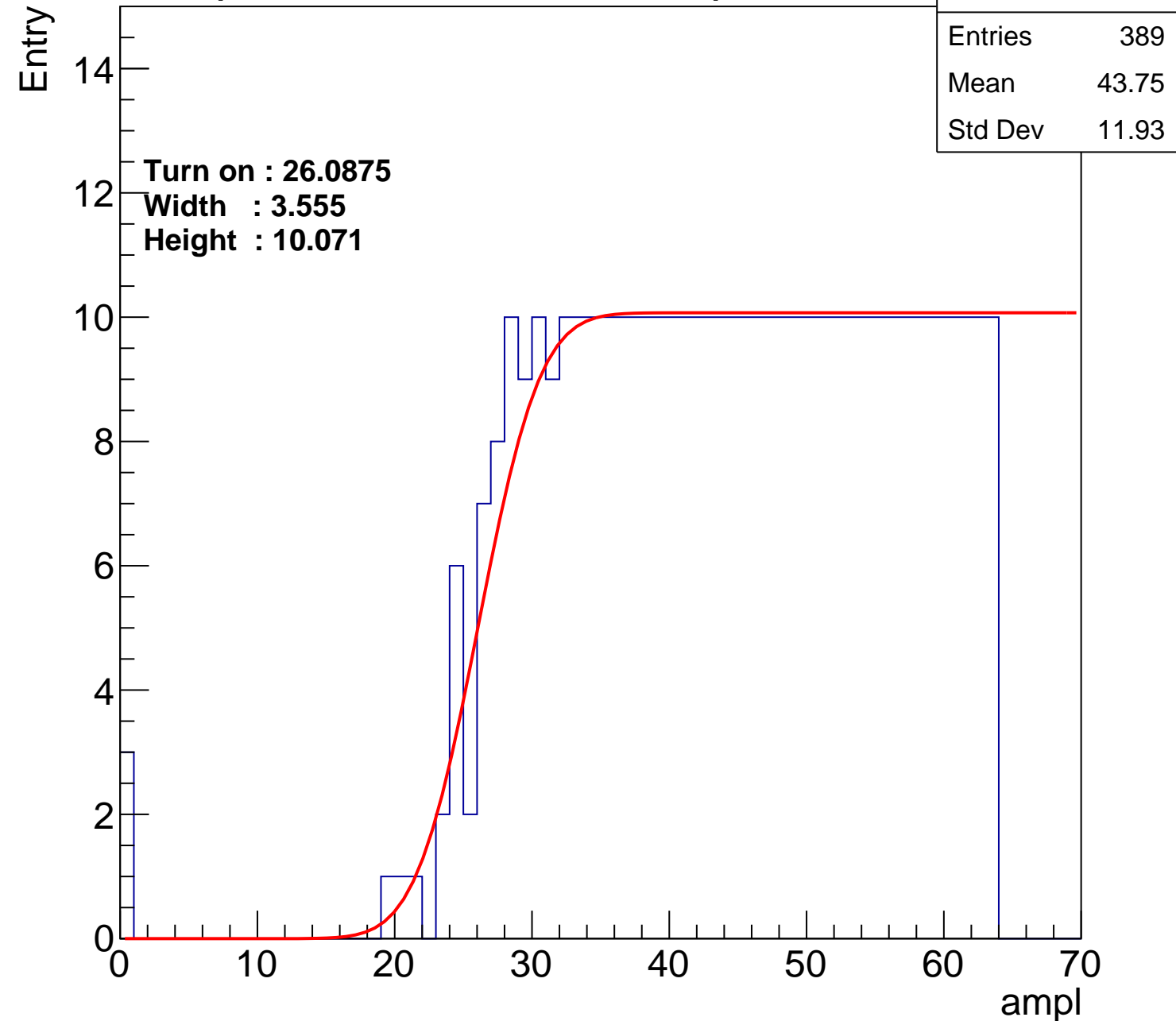
Width : 3.555

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch80

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 24.7946

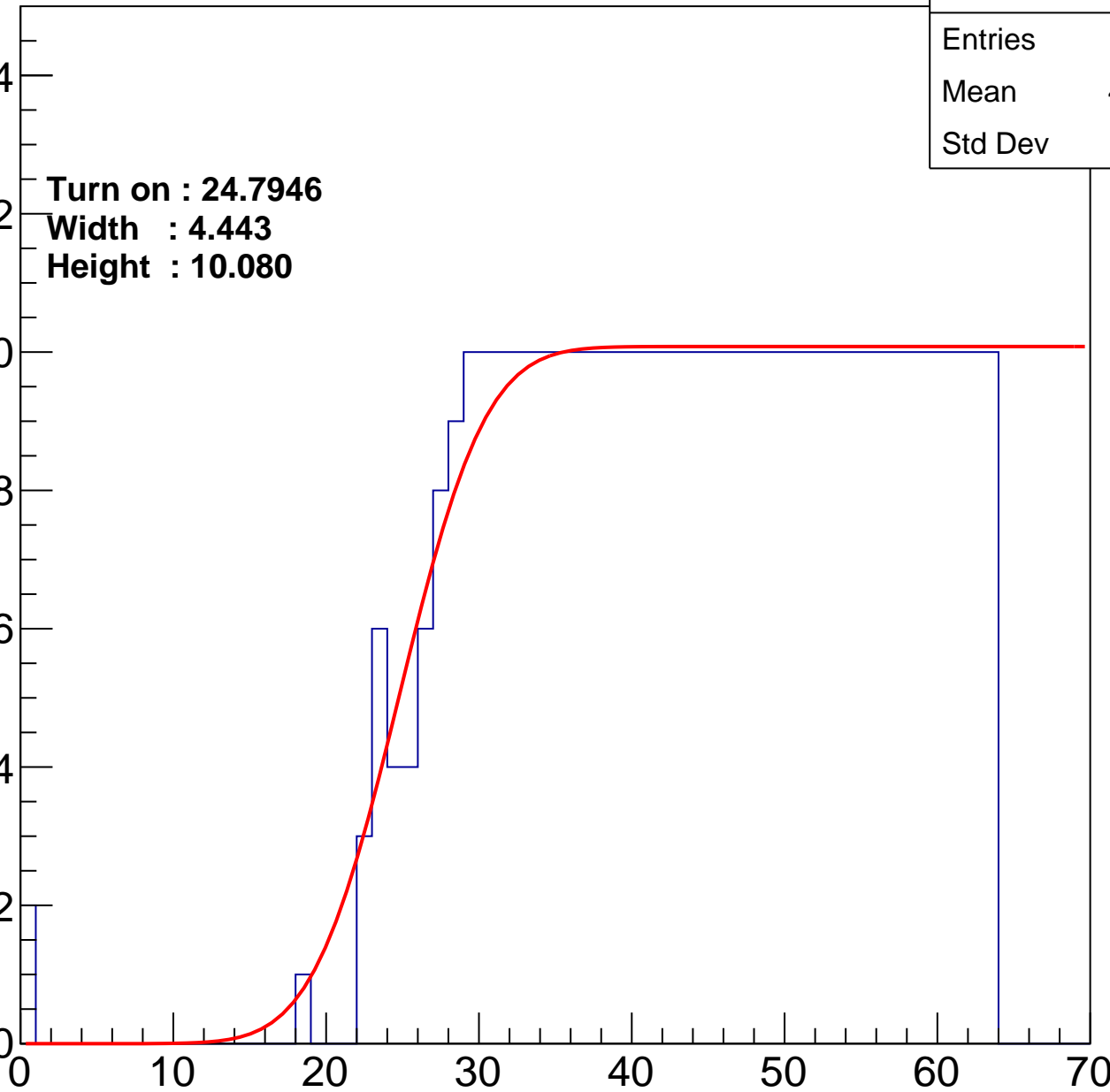
Width : 4.443

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch81

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.41
Std Dev	11.43

Turn on : 26.5840

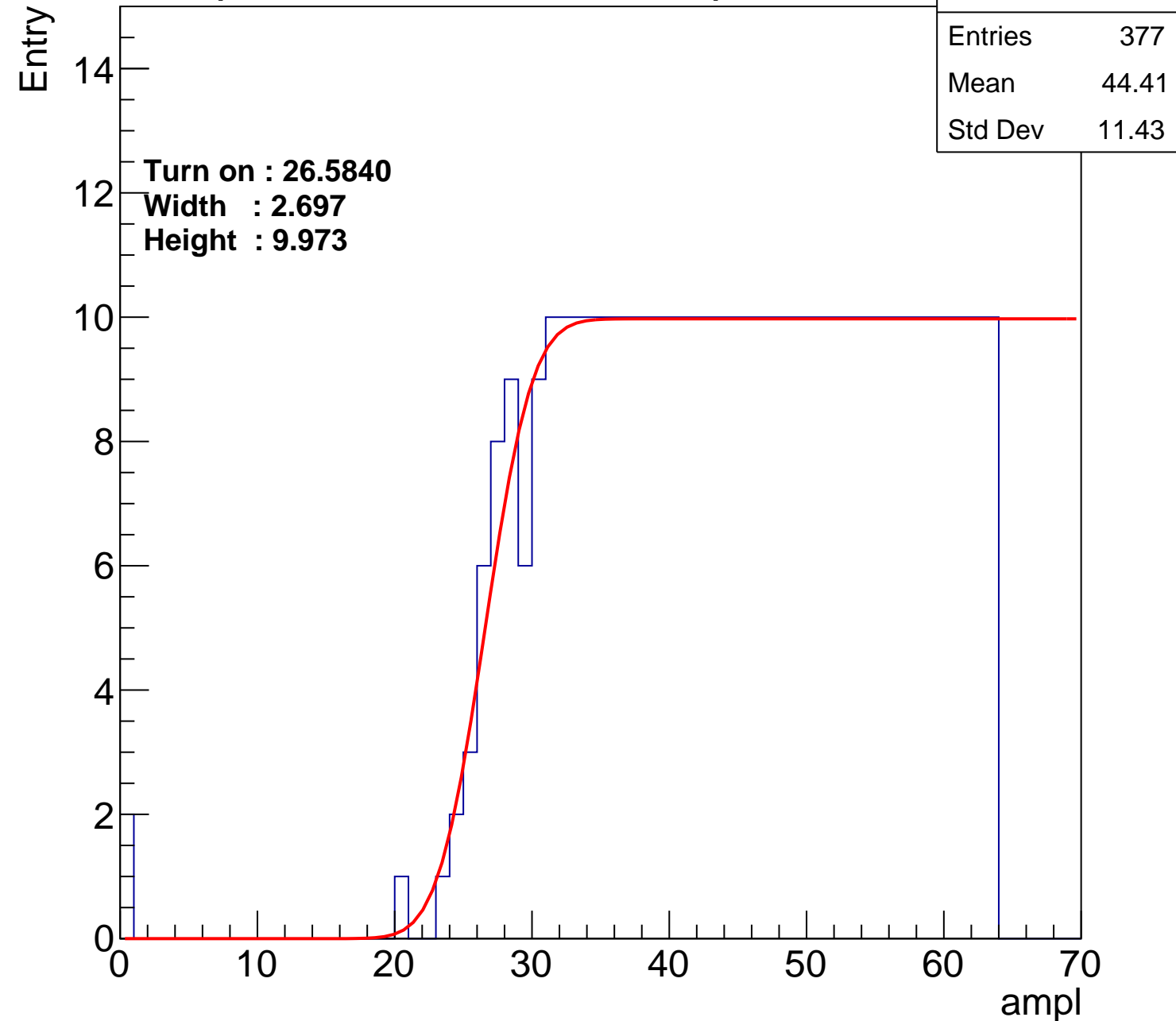
Width : 2.697

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch82

calib_packv5_042523_0143.root, FC#11, port A2

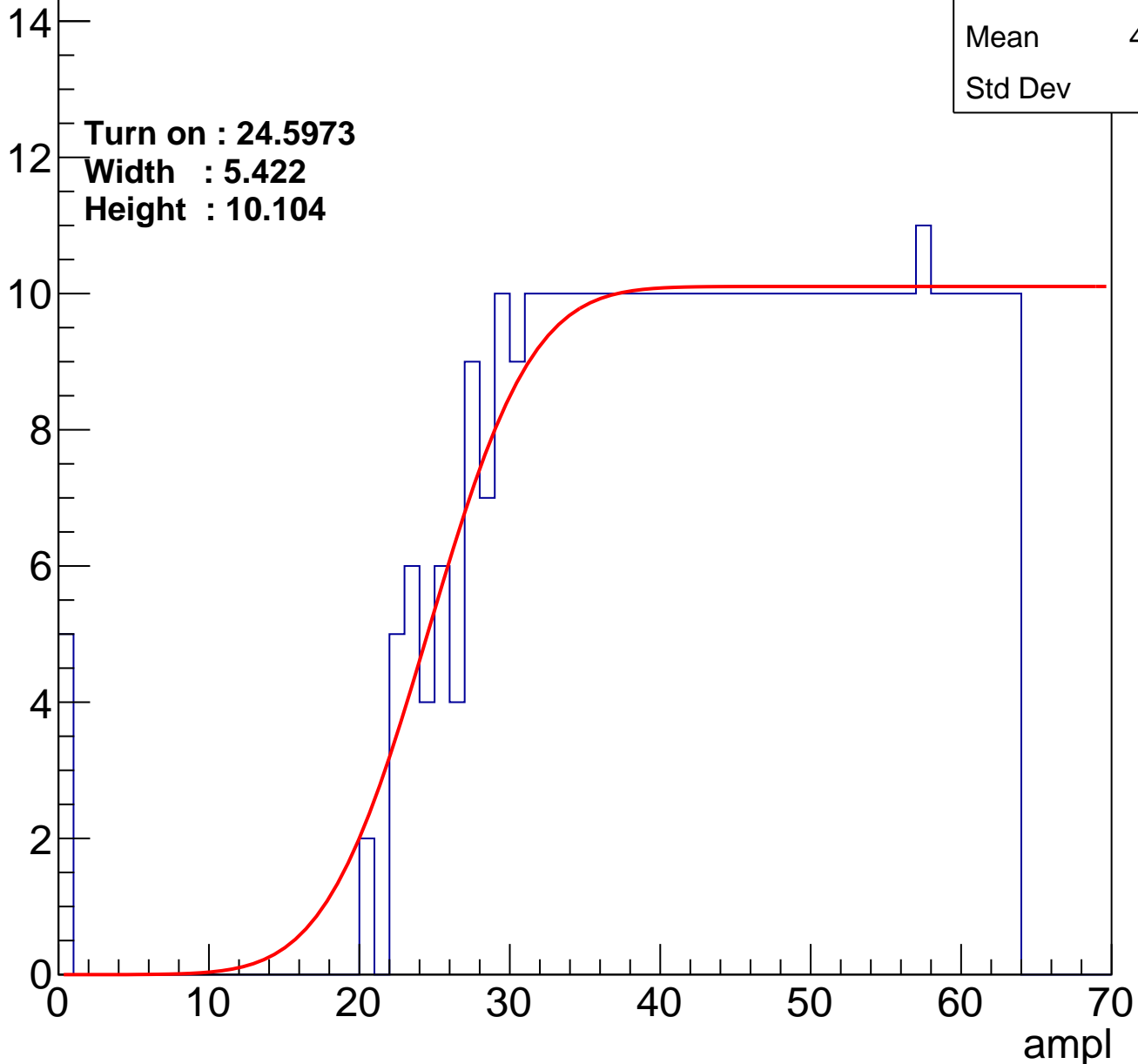
Entries	398
Mean	43.23
Std Dev	12.5

Turn on : 24.5973

Width : 5.422

Height : 10.104

Entry



B1L102S, U22-ch83

calib_packv5_042523_0143.root, FC#11, port A2

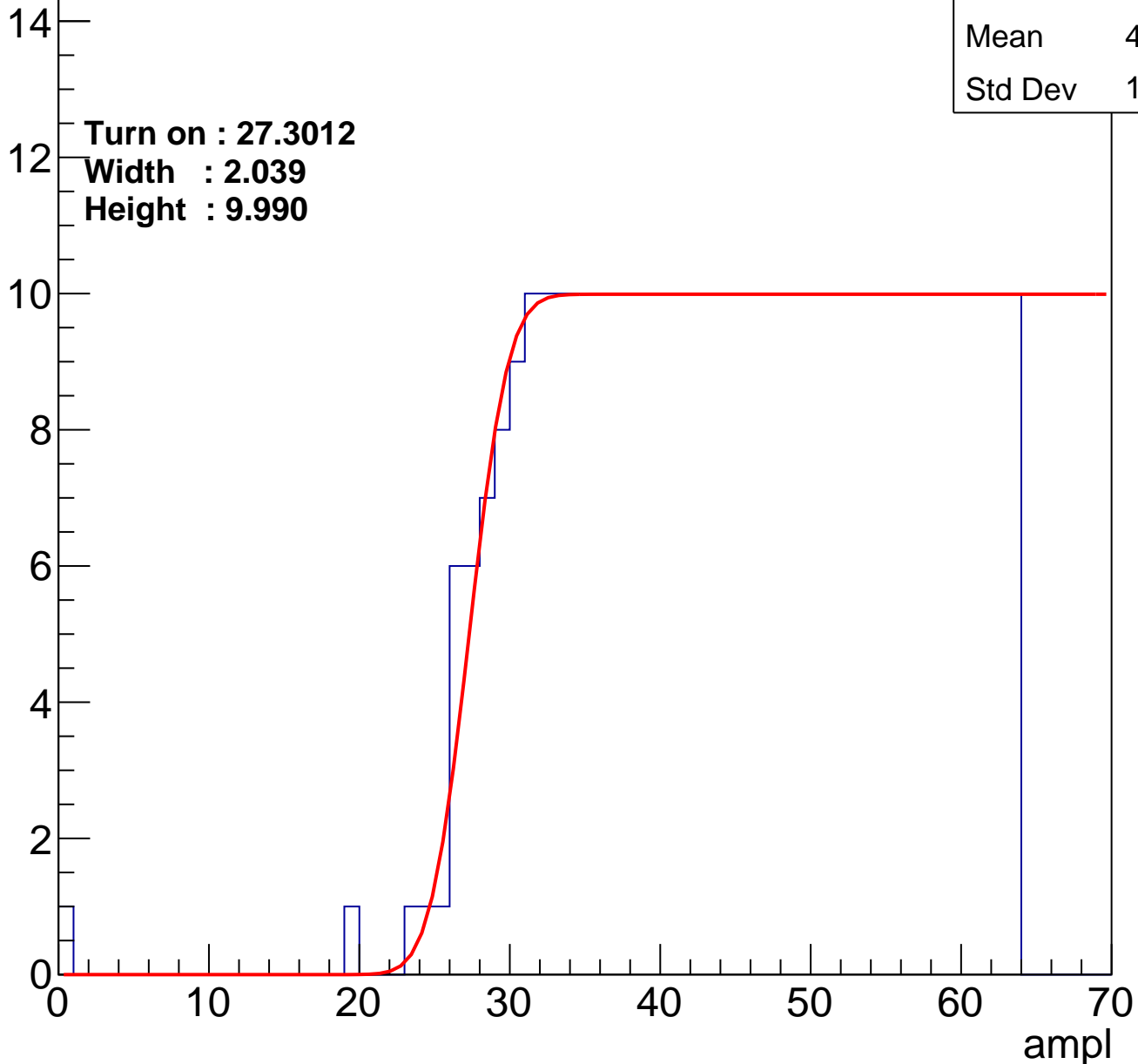
Entries	371
Mean	44.79
Std Dev	11.07

Turn on : 27.3012

Width : 2.039

Height : 9.990

Entry



B1L102S, U22-ch84

calib_packv5_042523_0143.root, FC#11, port A2

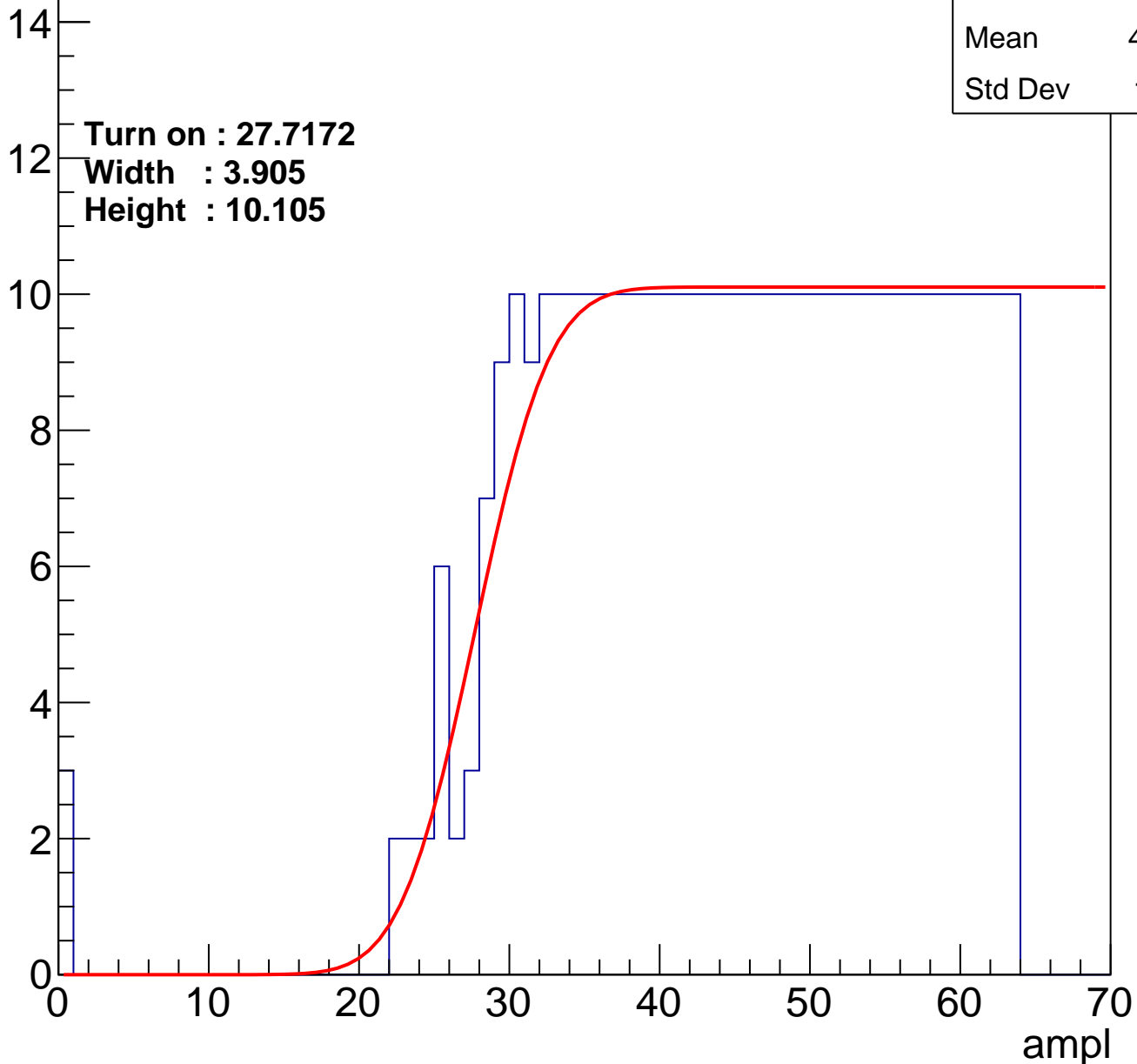
Entry

Entries	375
Mean	44.42
Std Dev	11.61

Turn on : 27.7172

Width : 3.905

Height : 10.105



B1L102S, U22-ch85

calib_packv5_042523_0143.root, FC#11, port A2

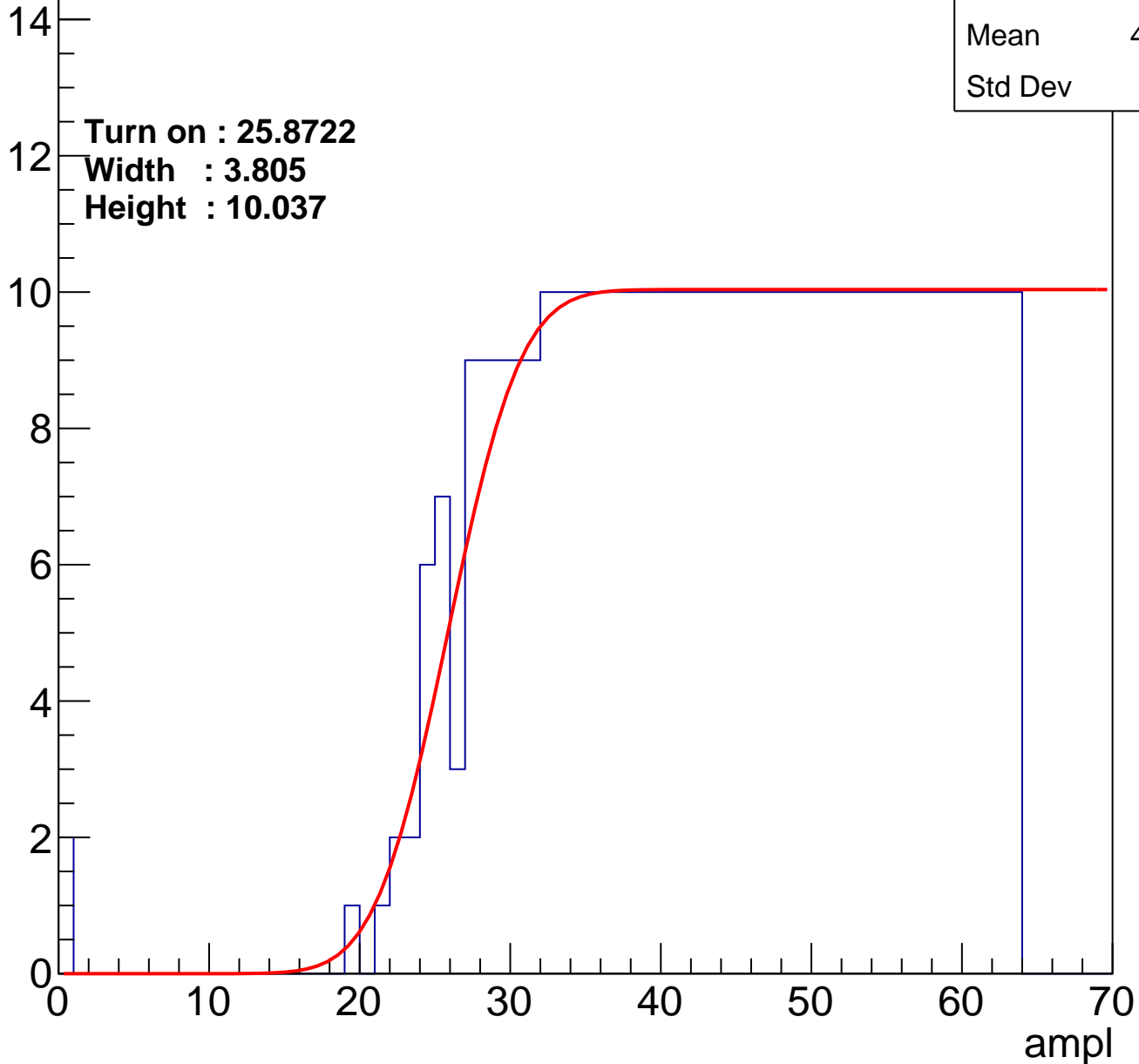
Entries	389
Mean	43.78
Std Dev	11.8

Turn on : 25.8722

Width : 3.805

Height : 10.037

Entry



B1L102S, U22-ch86

calib_packv5_042523_0143.root, FC#11, port A2

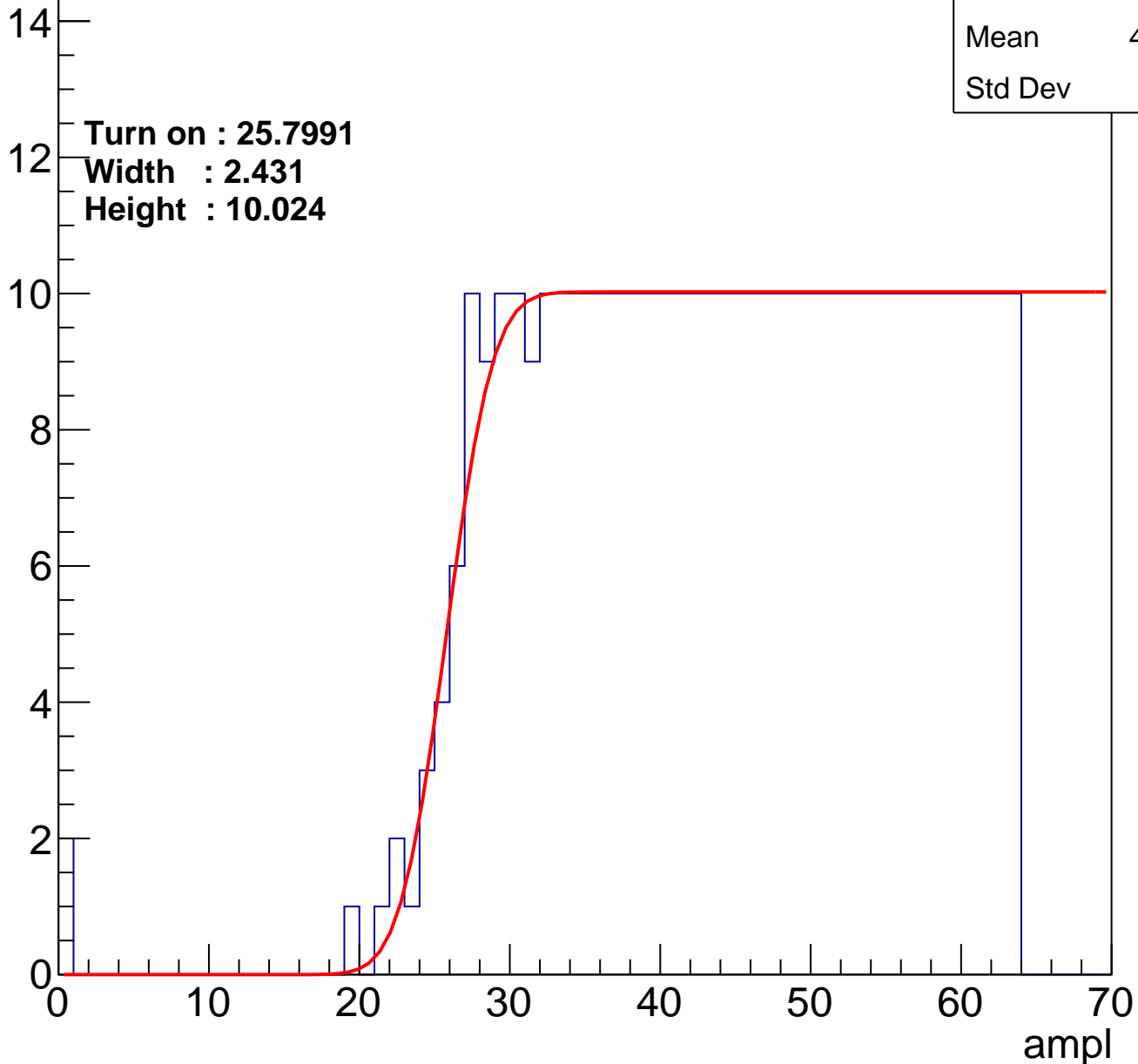
Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.7991

Width : 2.431

Height : 10.024

Entry



B1L102S, U22-ch87

calib_packv5_042523_0143.root, FC#11, port A2

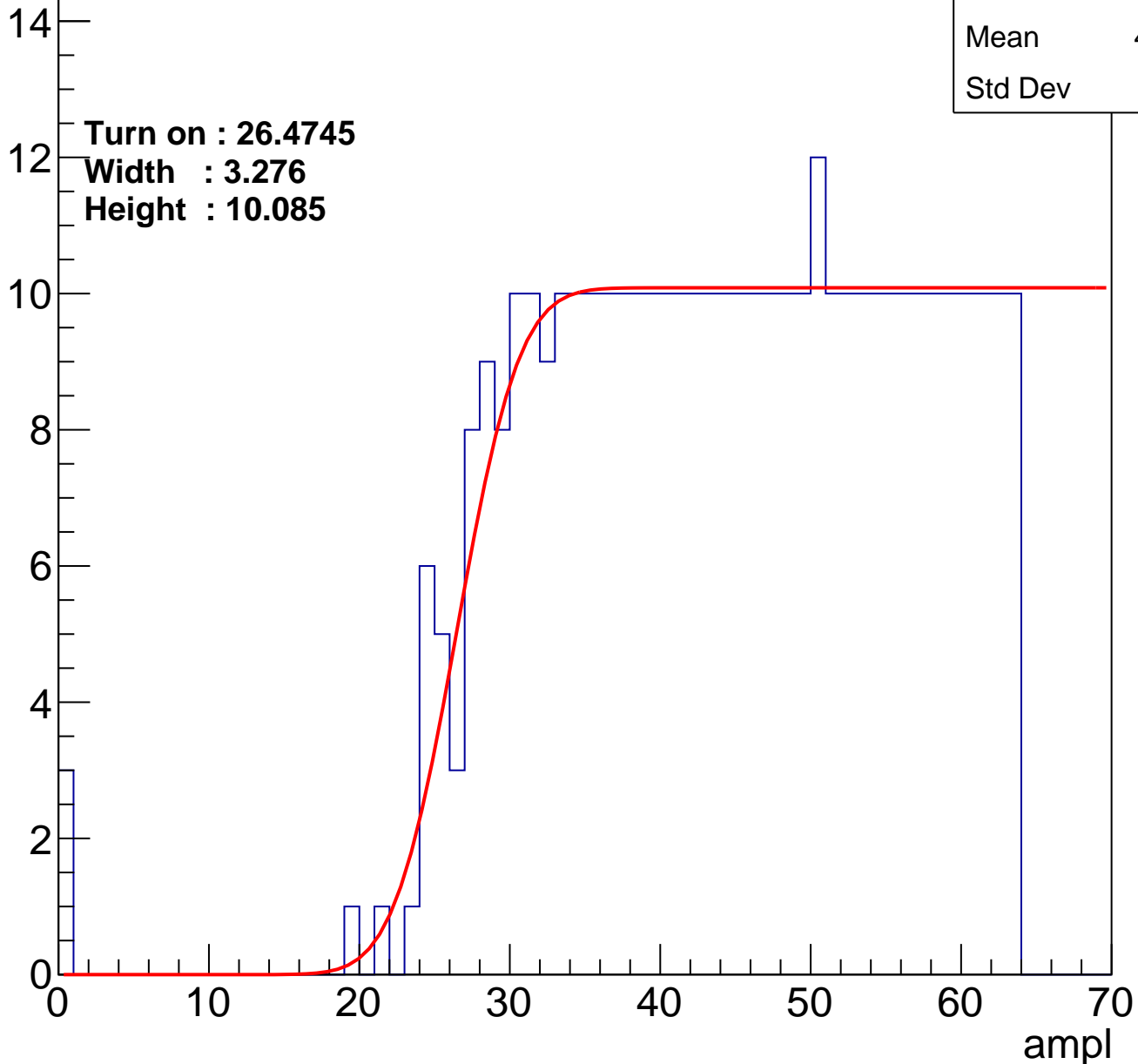
Entries	386
Mean	44.01
Std Dev	11.8

Turn on : 26.4745

Width : 3.276

Height : 10.085

Entry



B1L102S, U22-ch88

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.89
Std Dev	11.57

Turn on : 25.2398

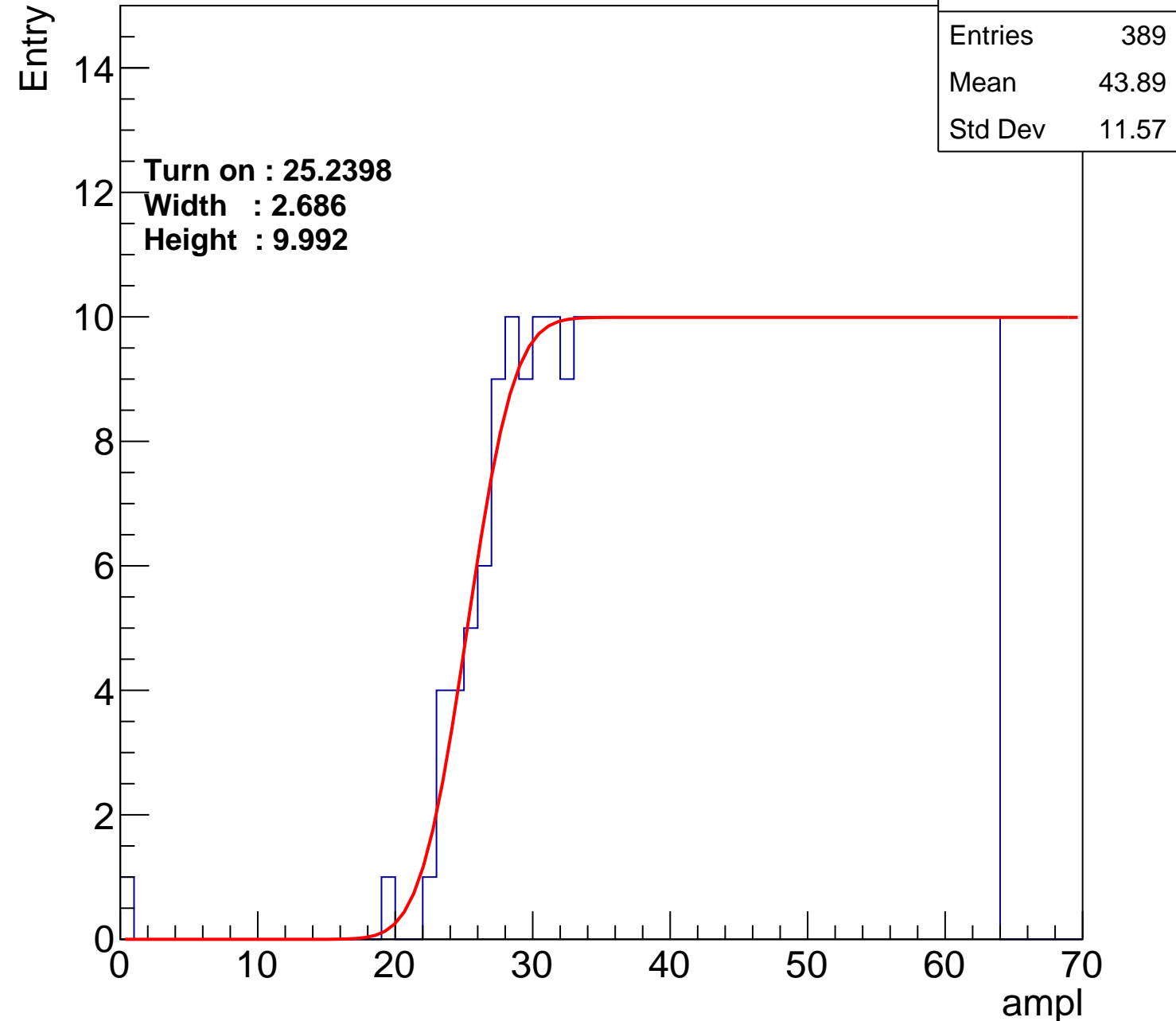
Width : 2.686

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch89

calib_packv5_042523_0143.root, FC#11, port A2

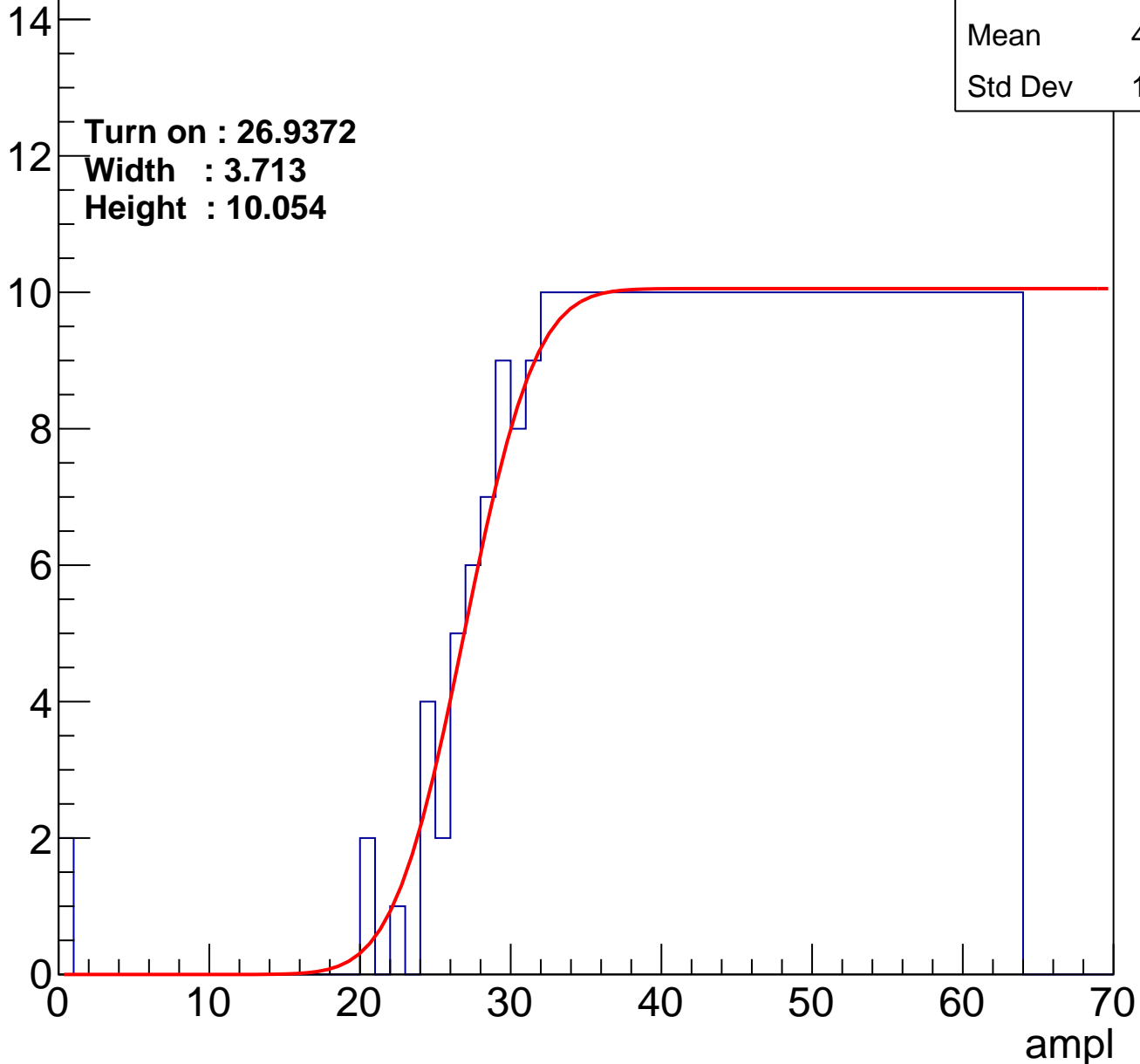
Entries	375
Mean	44.47
Std Dev	11.46

Turn on : 26.9372

Width : 3.713

Height : 10.054

Entry



B1L102S, U22-ch90

calib_packv5_042523_0143.root, FC#11, port A2

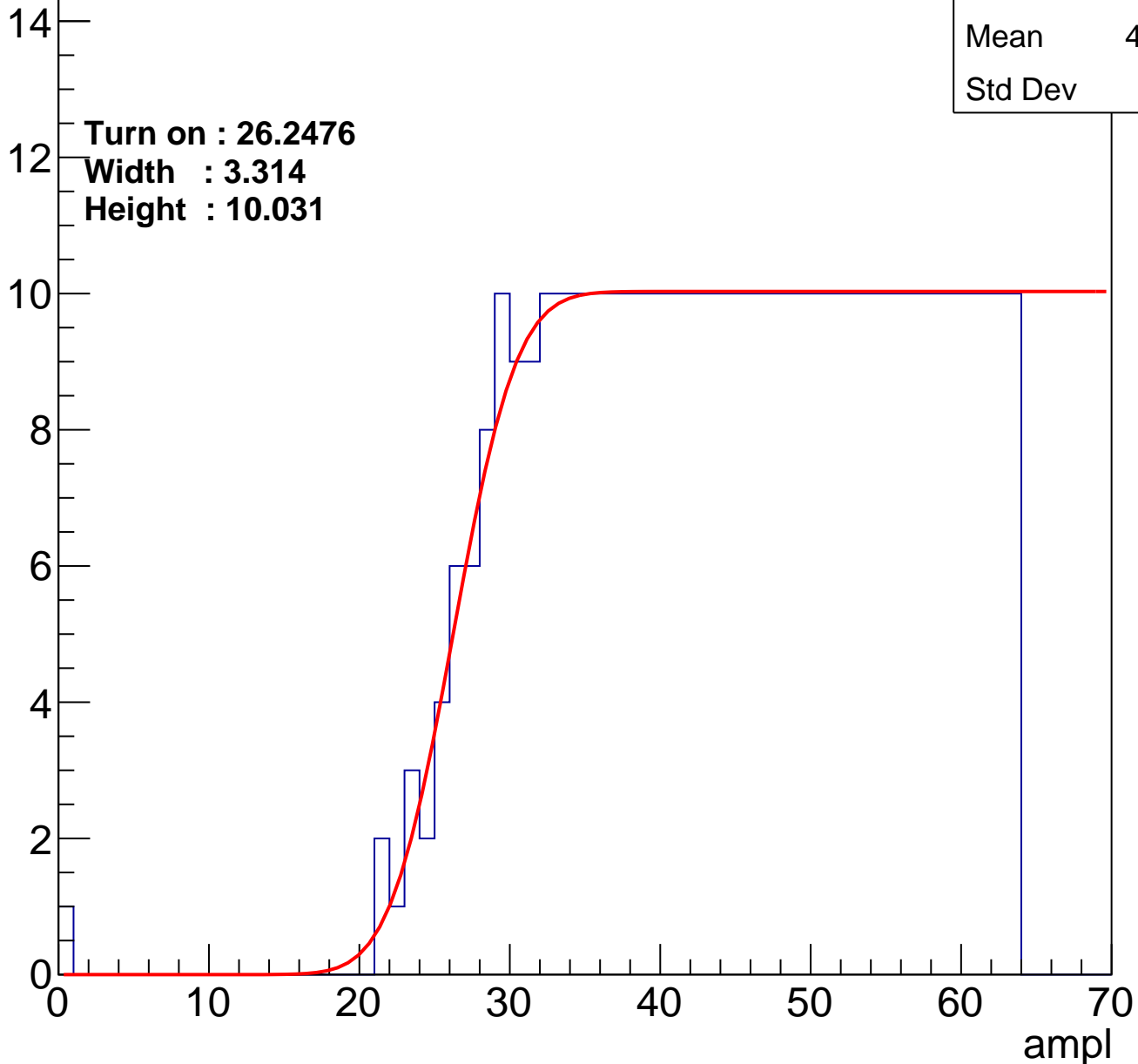
Entries	381
Mean	44.26
Std Dev	11.4

Turn on : 26.2476

Width : 3.314

Height : 10.031

Entry



B1L102S, U22-ch91

calib_packv5_042523_0143.root, FC#11, port A2

Entries	346
Mean	45.88
Std Dev	10.74

Turn on : 29.6637

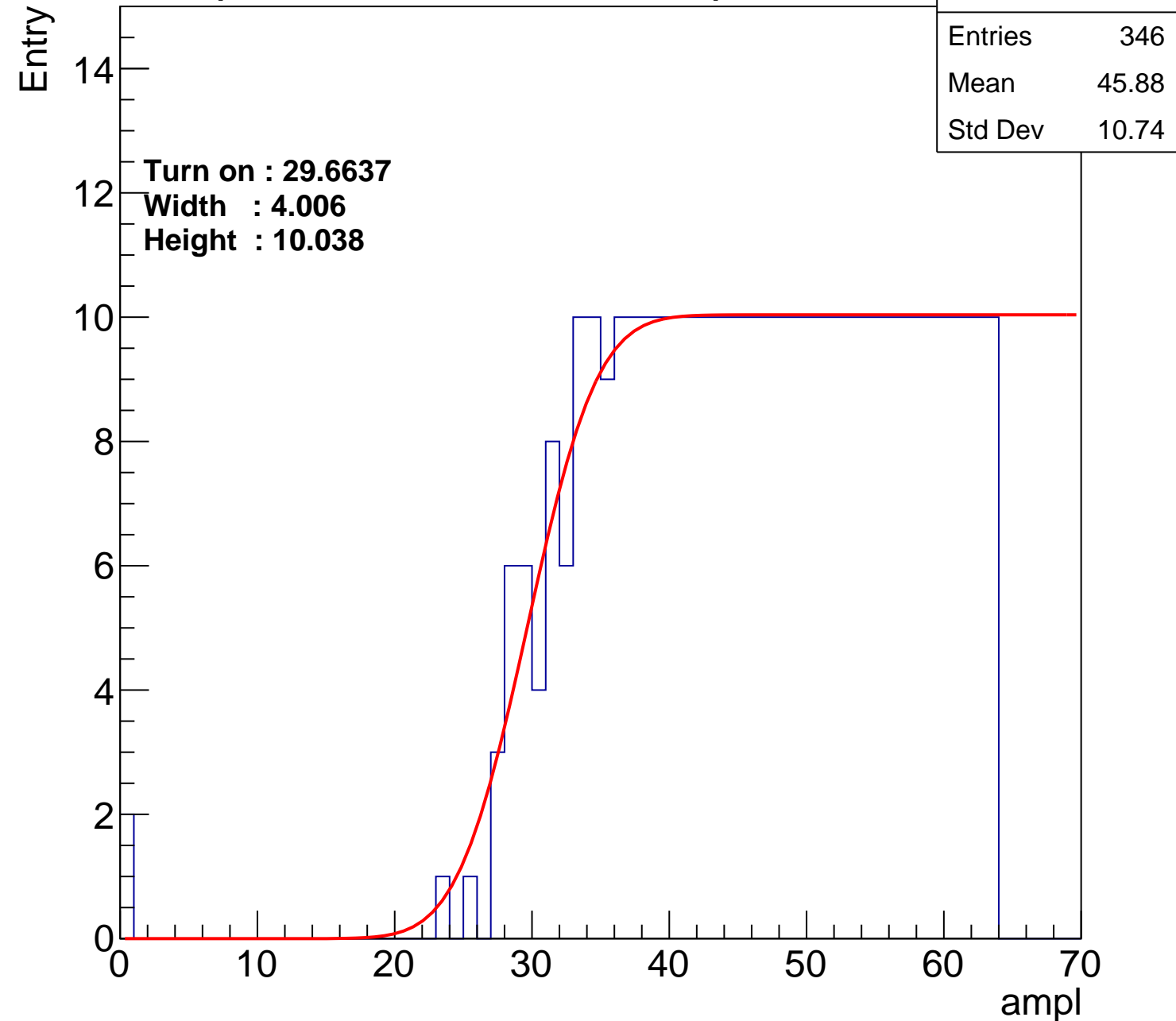
Width : 4.006

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch92

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.66
Std Dev	11.67

Turn on : 27.6429

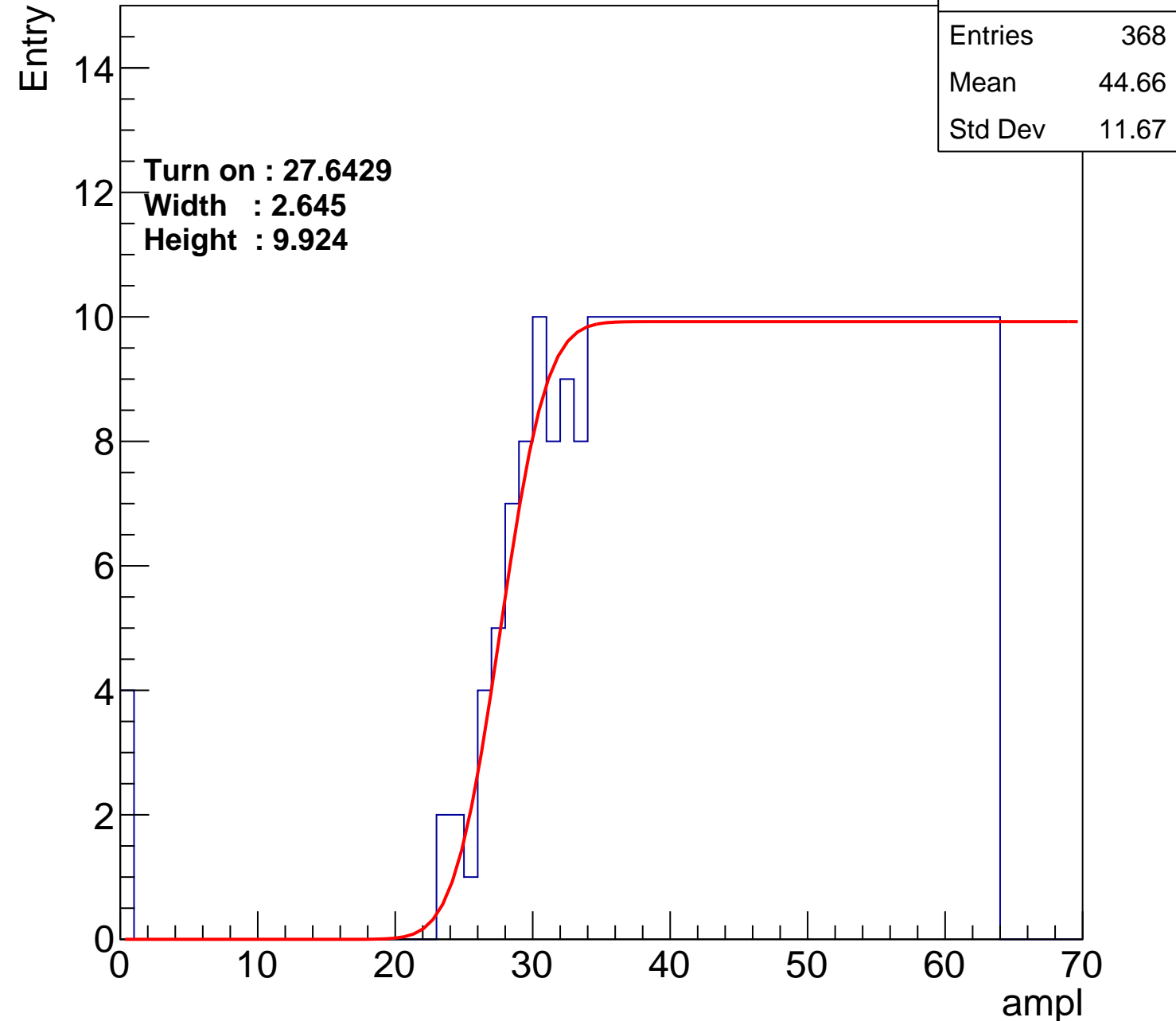
Width : 2.645

Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch93

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.89
Std Dev	11.16

Turn on : 27.6987

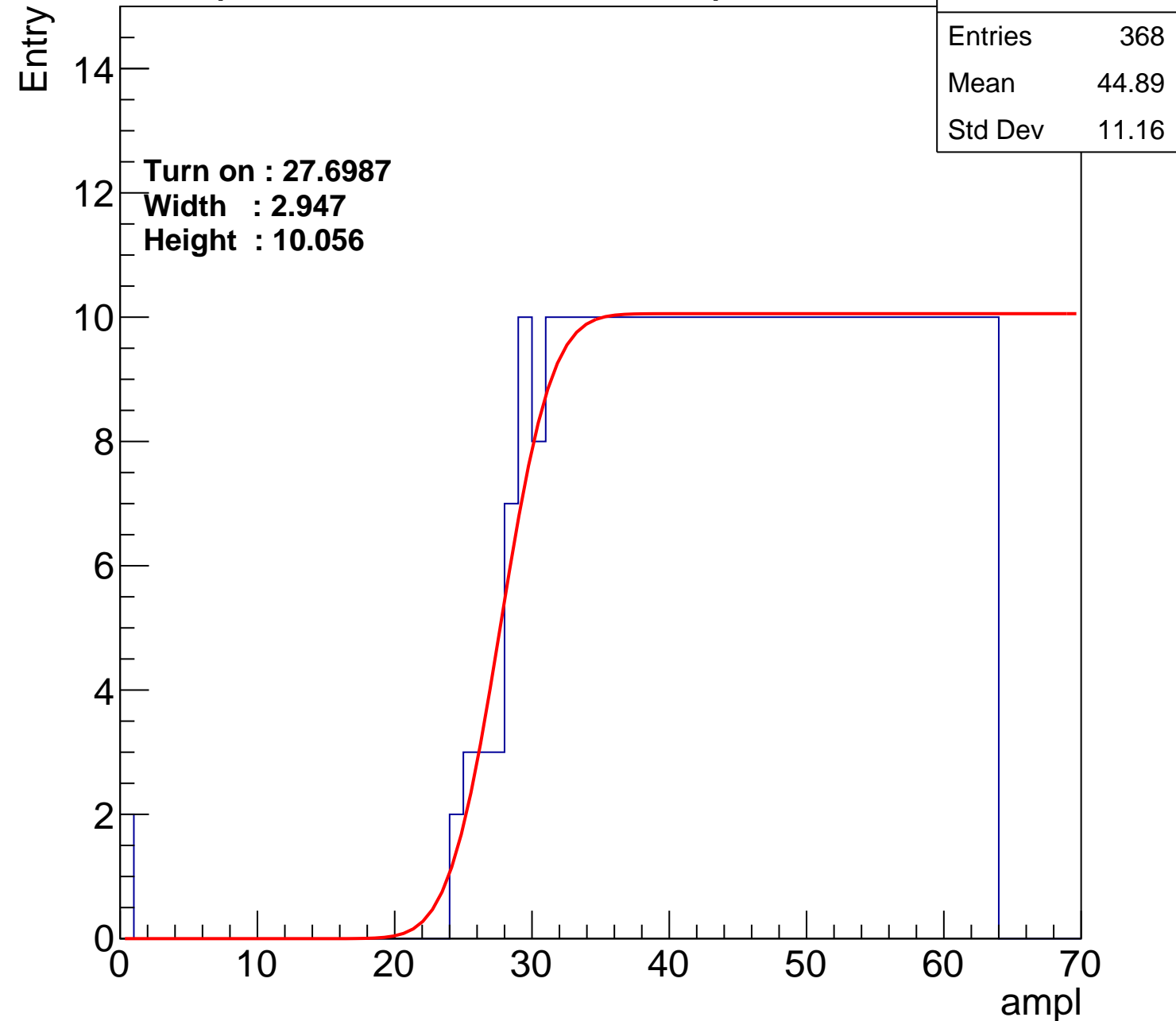
Width : 2.947

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch94

calib_packv5_042523_0143.root, FC#11, port A2

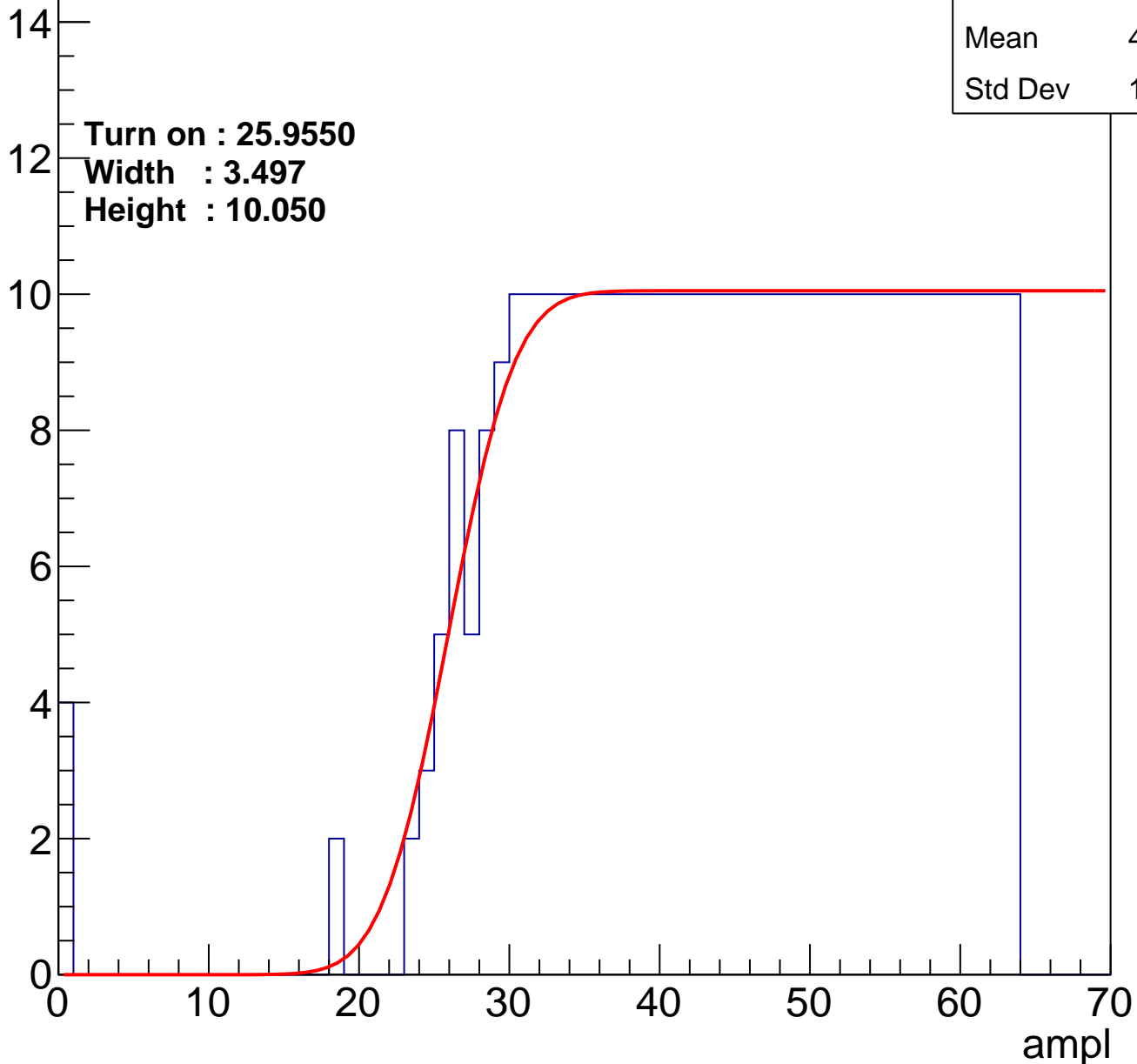
Entries	386
Mean	43.83
Std Dev	12.04

Turn on : 25.9550

Width : 3.497

Height : 10.050

Entry



B1L102S, U22-ch95

calib_packv5_042523_0143.root, FC#11, port A2

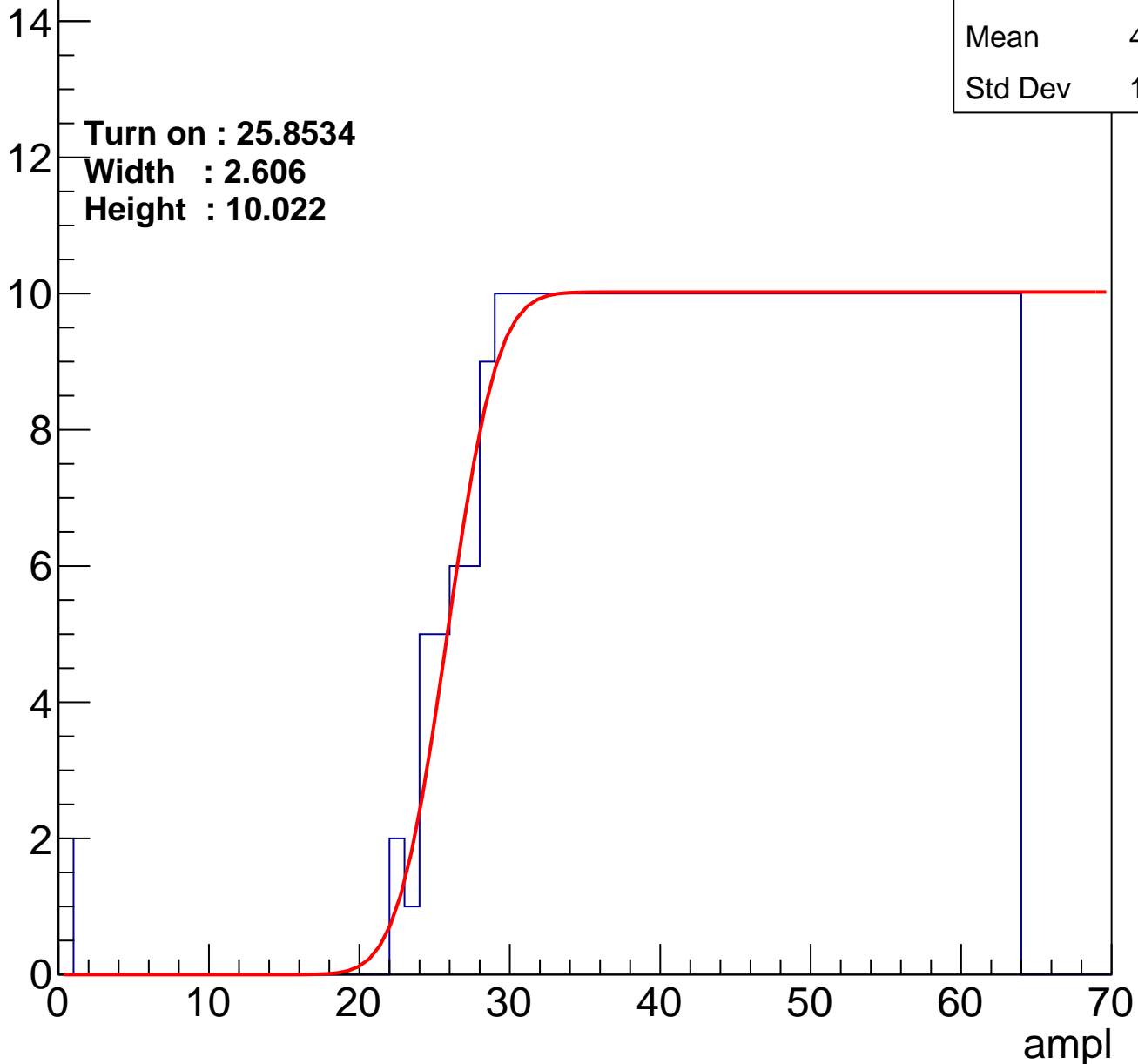
Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.8534

Width : 2.606

Height : 10.022

Entry



B1L102S, U22-ch96

calib_packv5_042523_0143.root, FC#11, port A2

Entries	406
Mean	42.87
Std Dev	12.47

Turn on : 24.0256

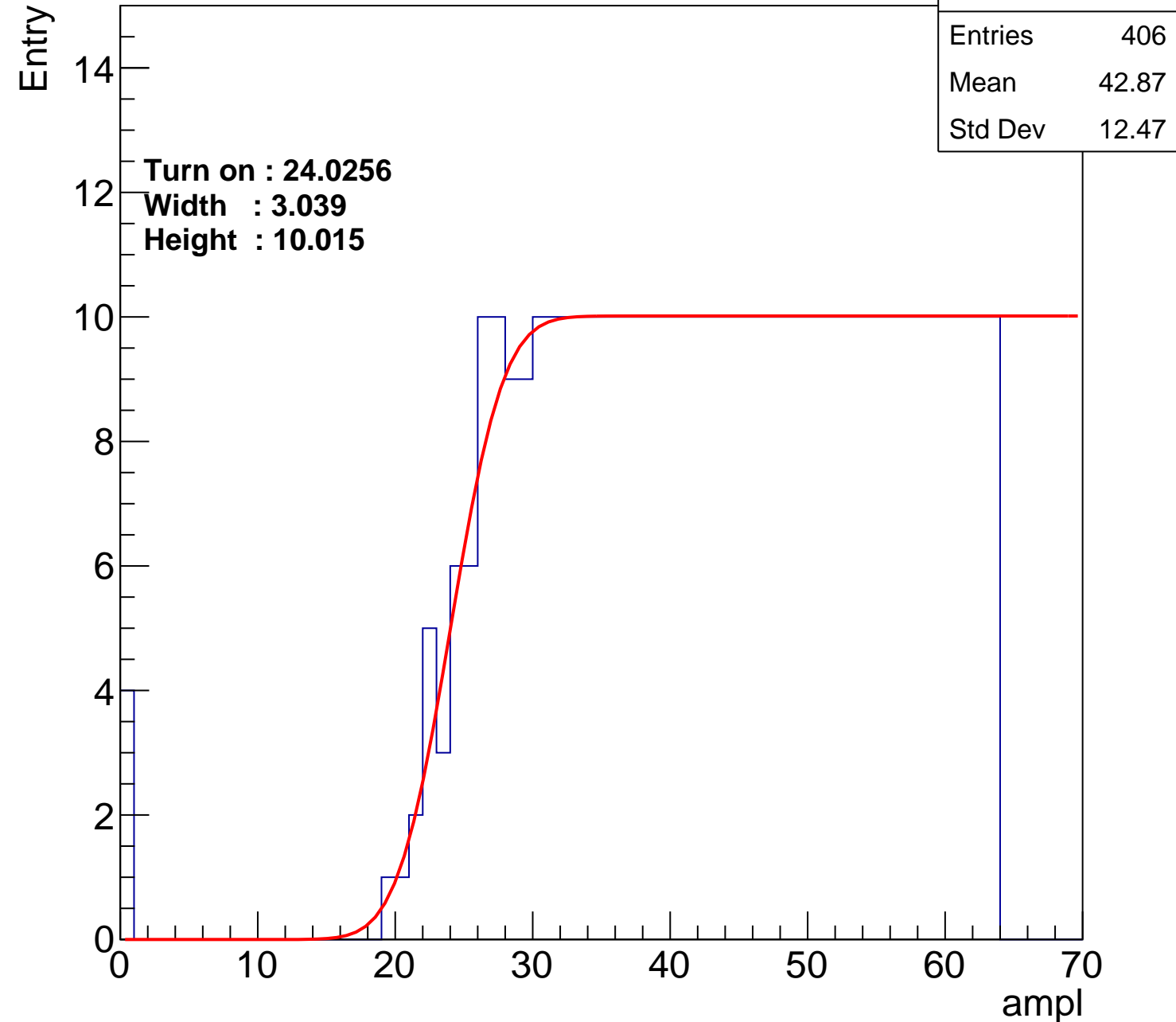
Width : 3.039

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	364
Mean	44.99
Std Dev	11.22

Turn on : 28.7279

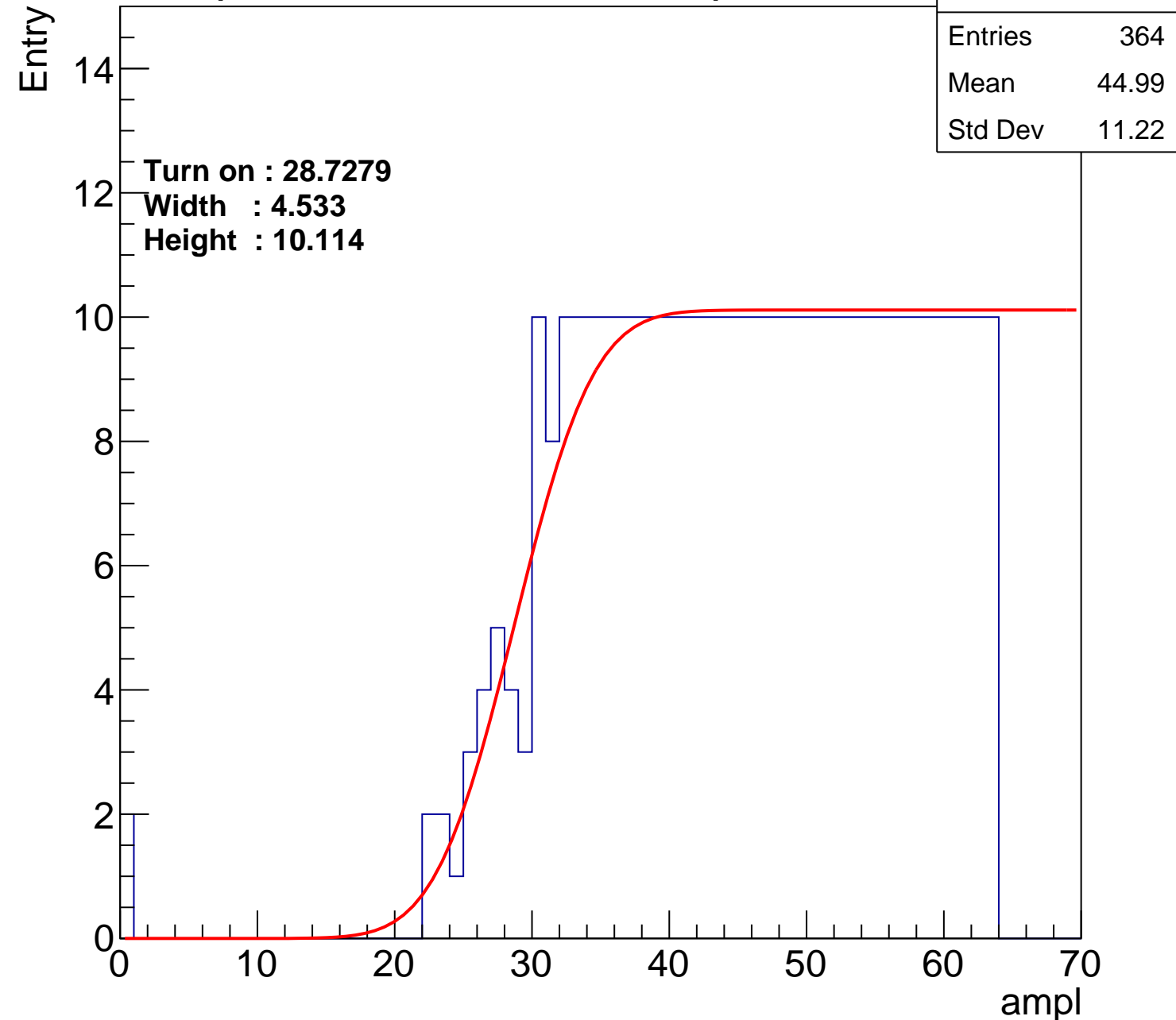
Width : 4.533

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch98

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.59
Std Dev	11.36

Turn on : 26.5094

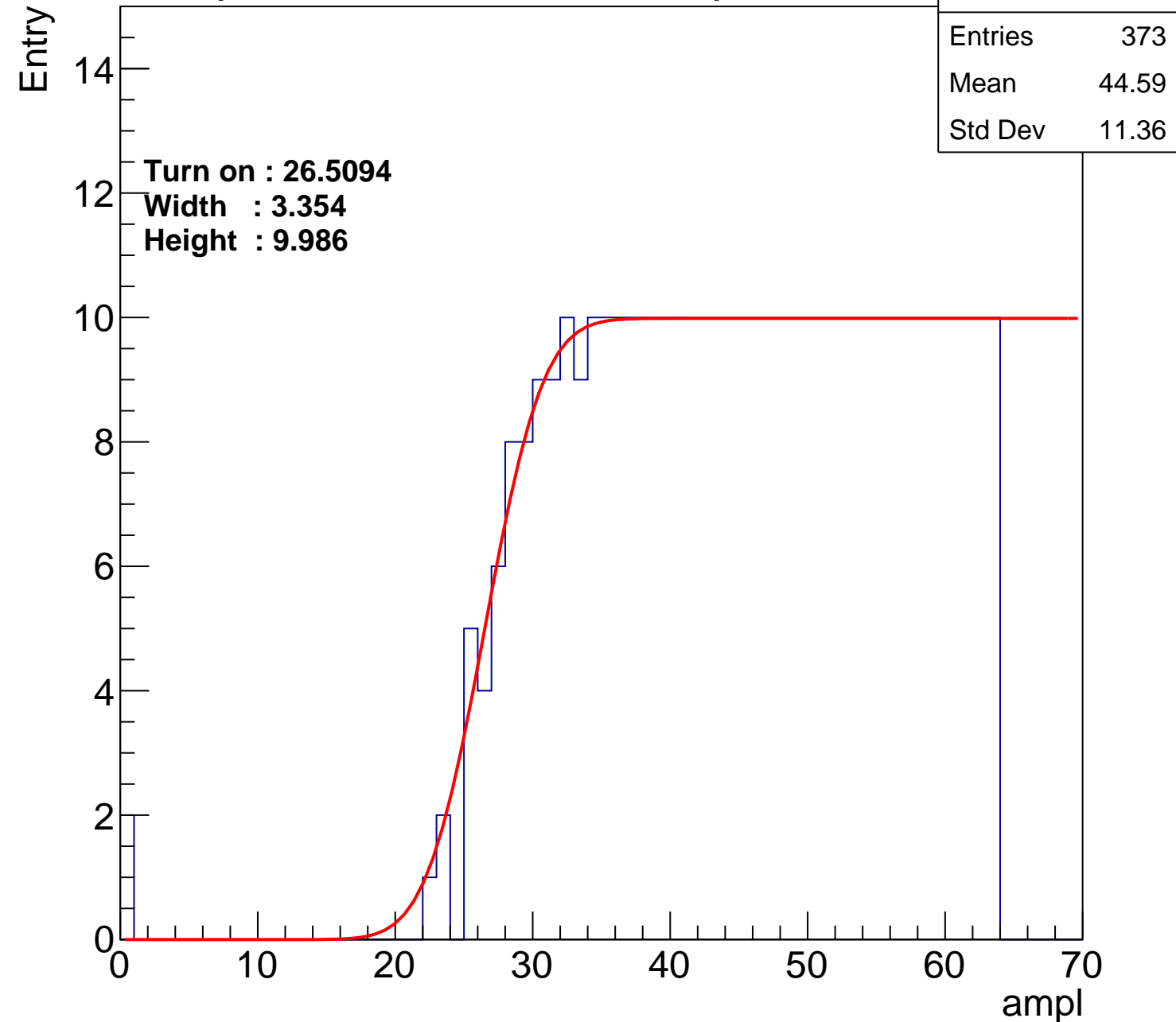
Width : 3.354

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch99

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.18
Std Dev	11.47

Turn on : 26.4917

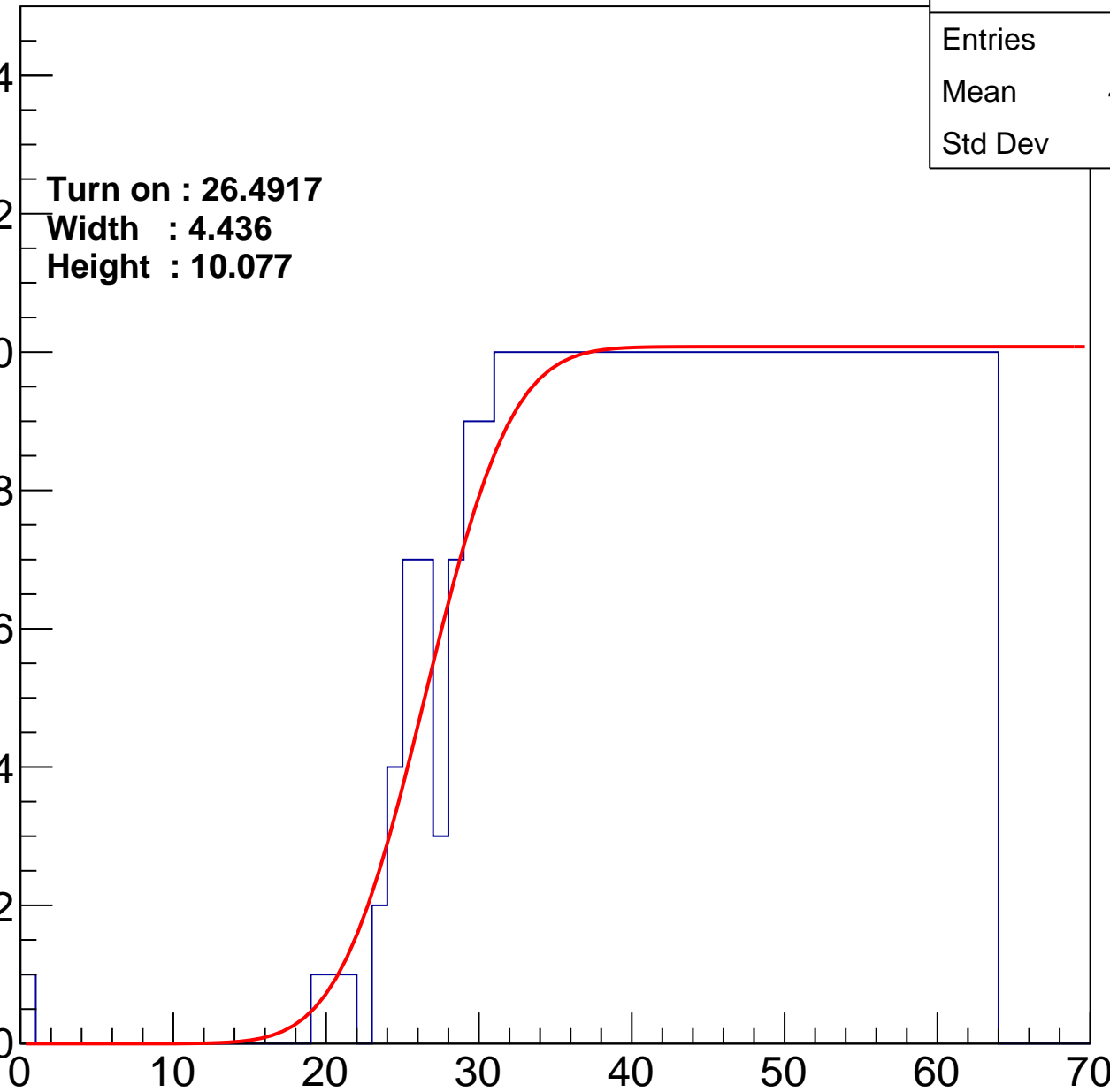
Width : 4.436

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch100

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.66
Std Dev	11.73

Turn on : 24.7488

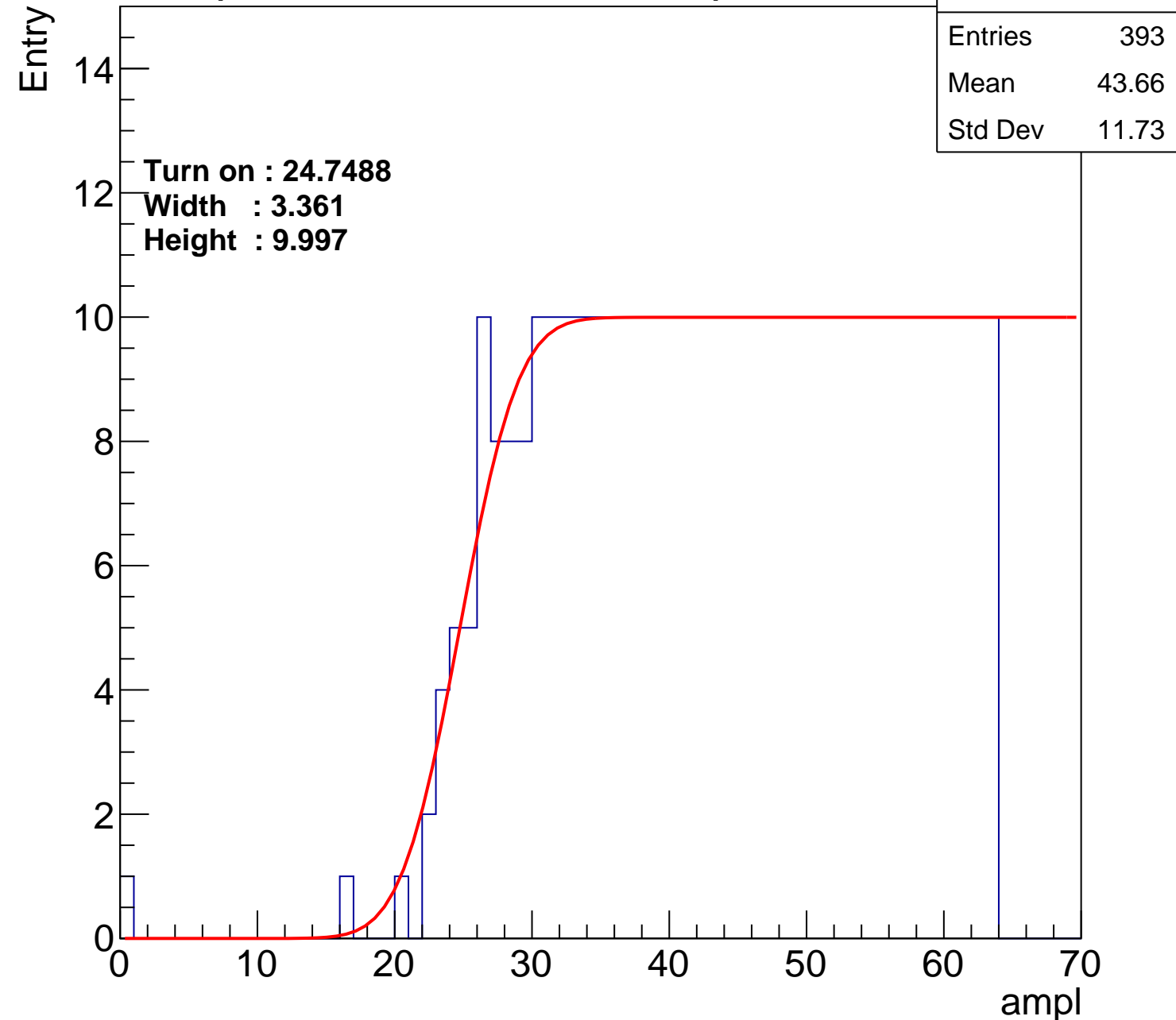
Width : 3.361

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch101

calib_packv5_042523_0143.root, FC#11, port A2

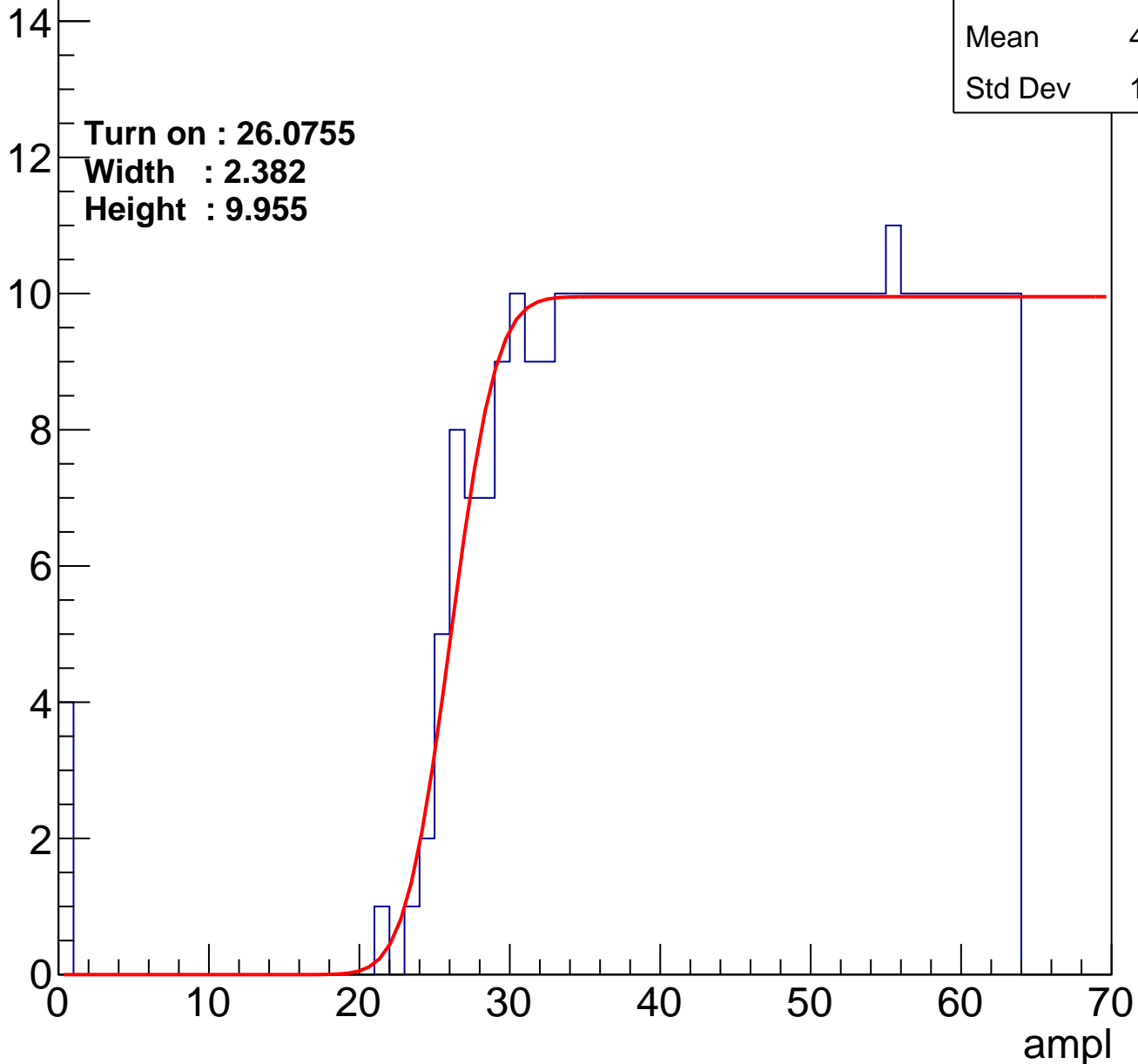
Entries	383
Mean	44.05
Std Dev	11.92

Turn on : 26.0755

Width : 2.382

Height : 9.955

Entry



B1L102S, U22-ch102

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.9
Std Dev	11.85

Turn on : 25.8720

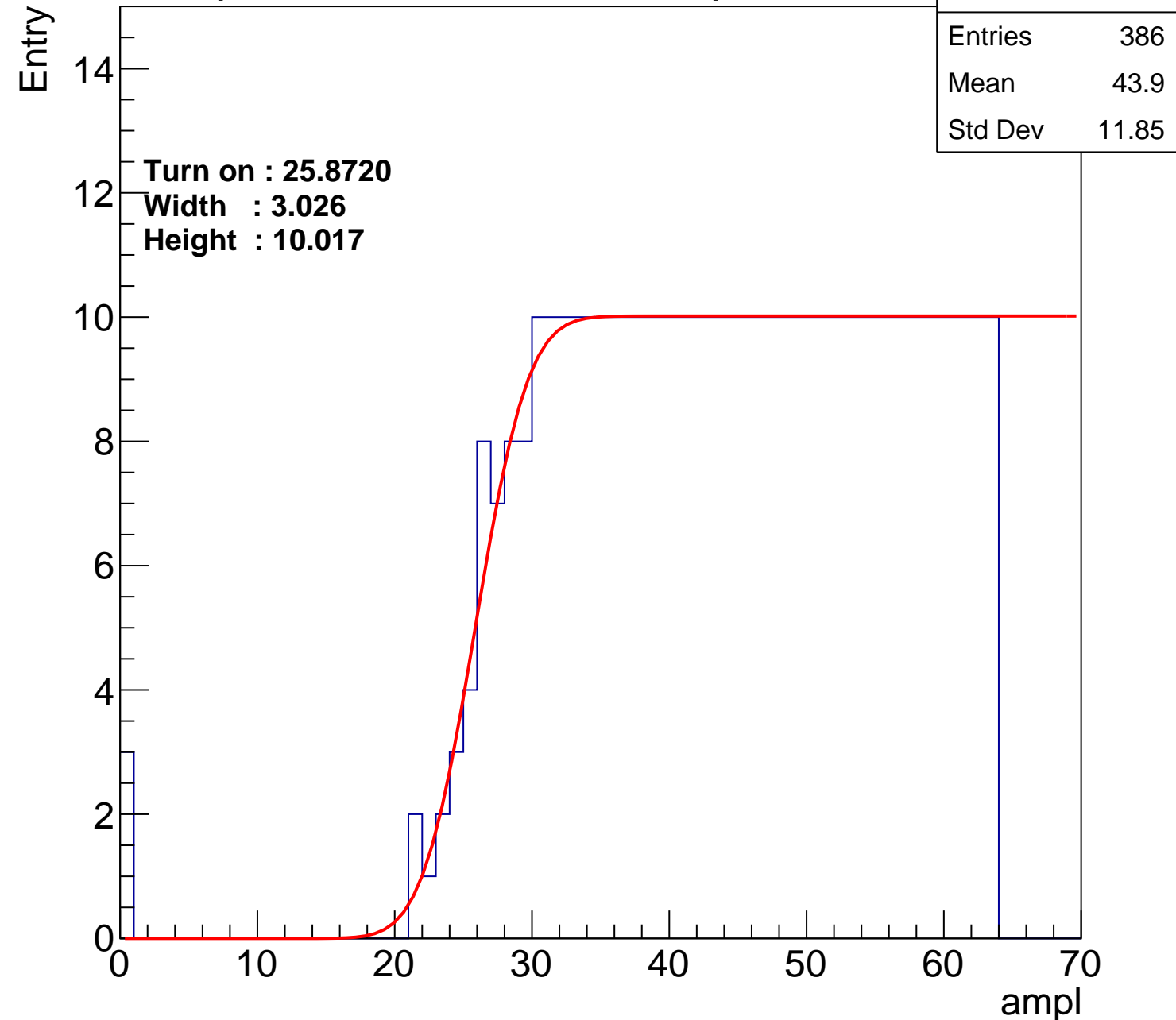
Width : 3.026

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch103

calib_packv5_042523_0143.root, FC#11, port A2

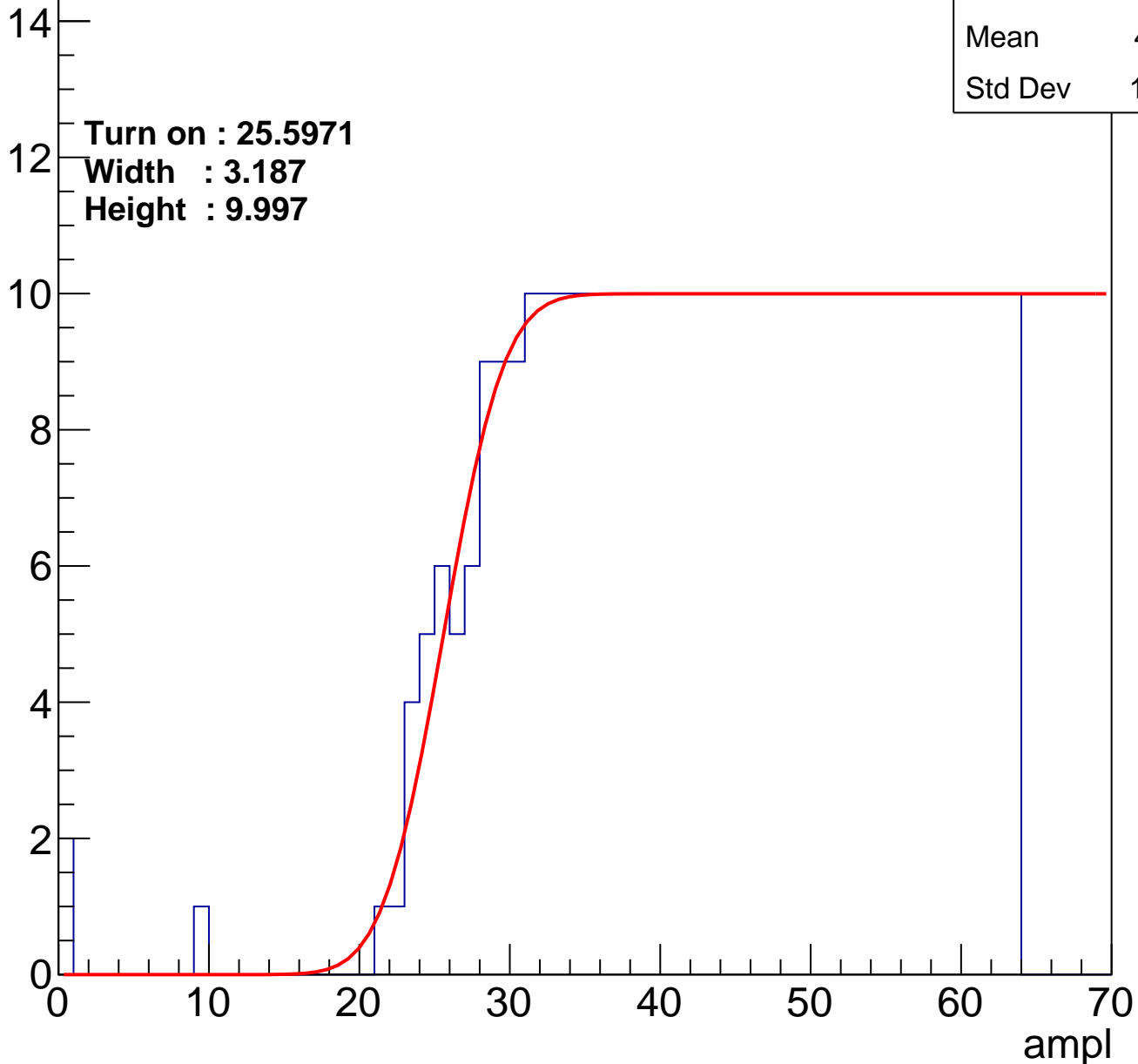
Entries	388
Mean	43.81
Std Dev	11.84

Turn on : 25.5971

Width : 3.187

Height : 9.997

Entry



B1L102S, U22-ch104

calib_packv5_042523_0143.root, FC#11, port A2

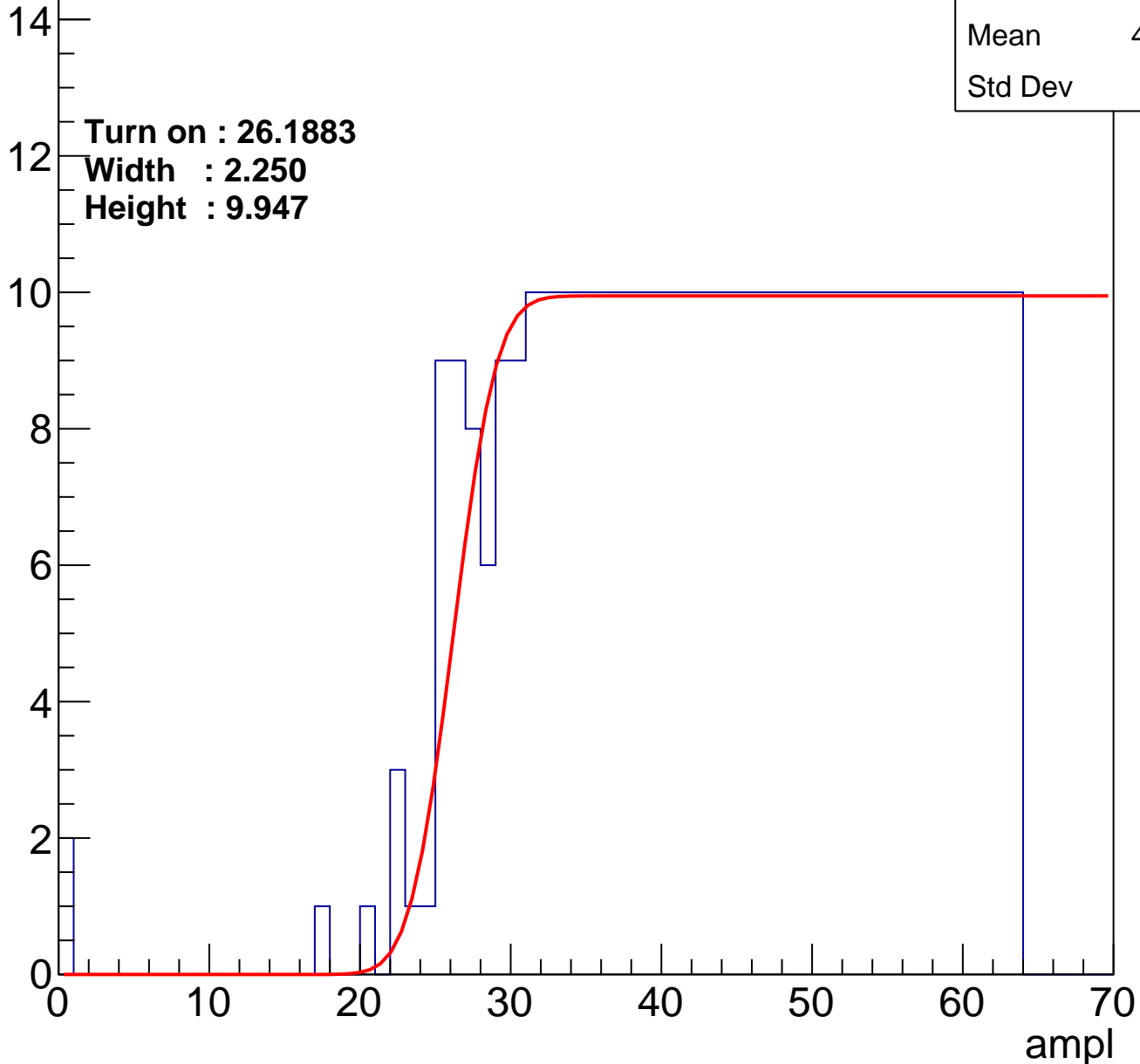
Entries	389
Mean	43.79
Std Dev	11.8

Turn on : 26.1883

Width : 2.250

Height : 9.947

Entry



B1L102S, U22-ch105

calib_packv5_042523_0143.root, FC#11, port A2

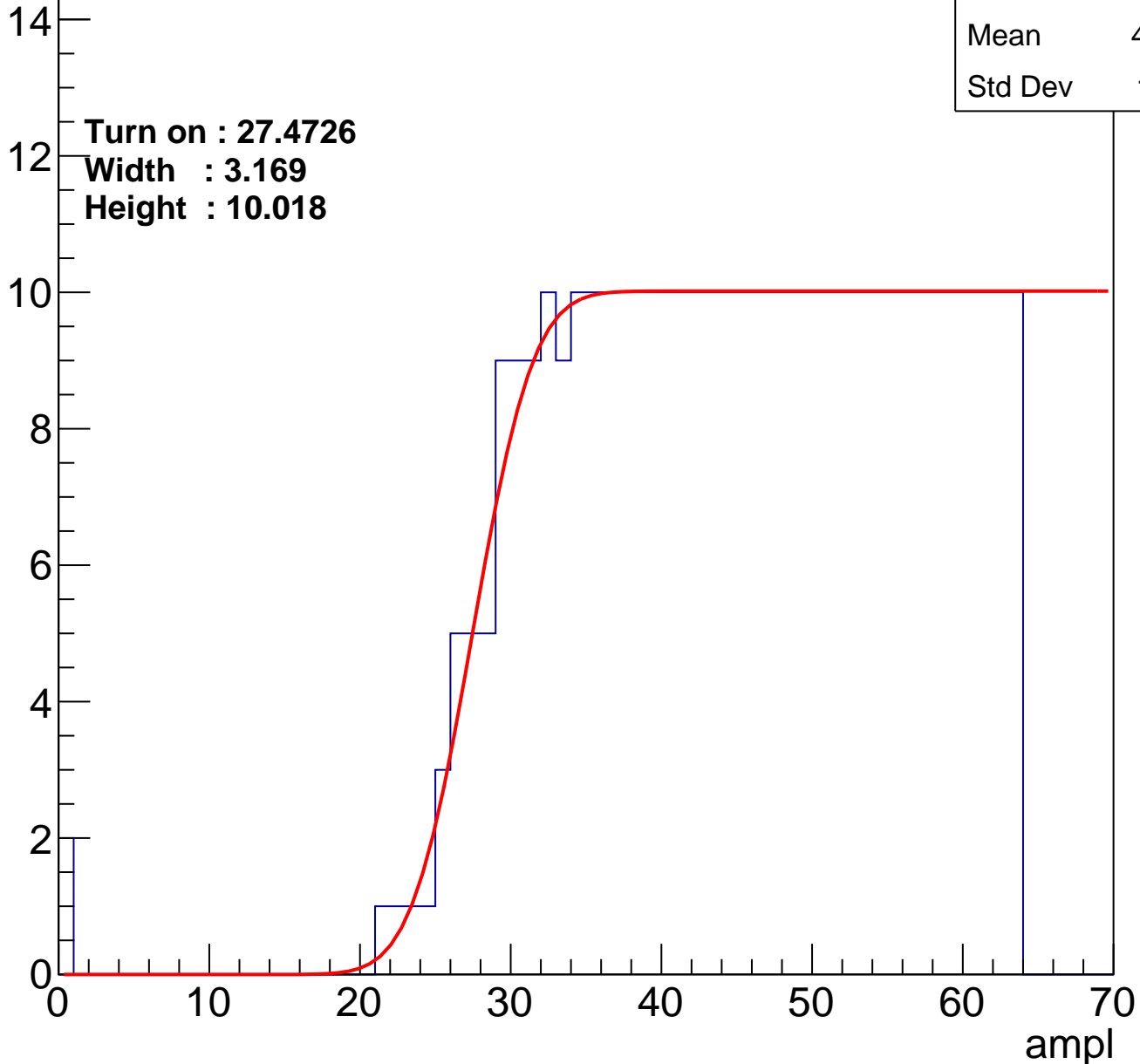
Entries	370
Mean	44.72
Std Dev	11.31

Turn on : 27.4726

Width : 3.169

Height : 10.018

Entry



B1L102S, U22-ch106

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.37
Std Dev	12.54

Turn on : 25.1137

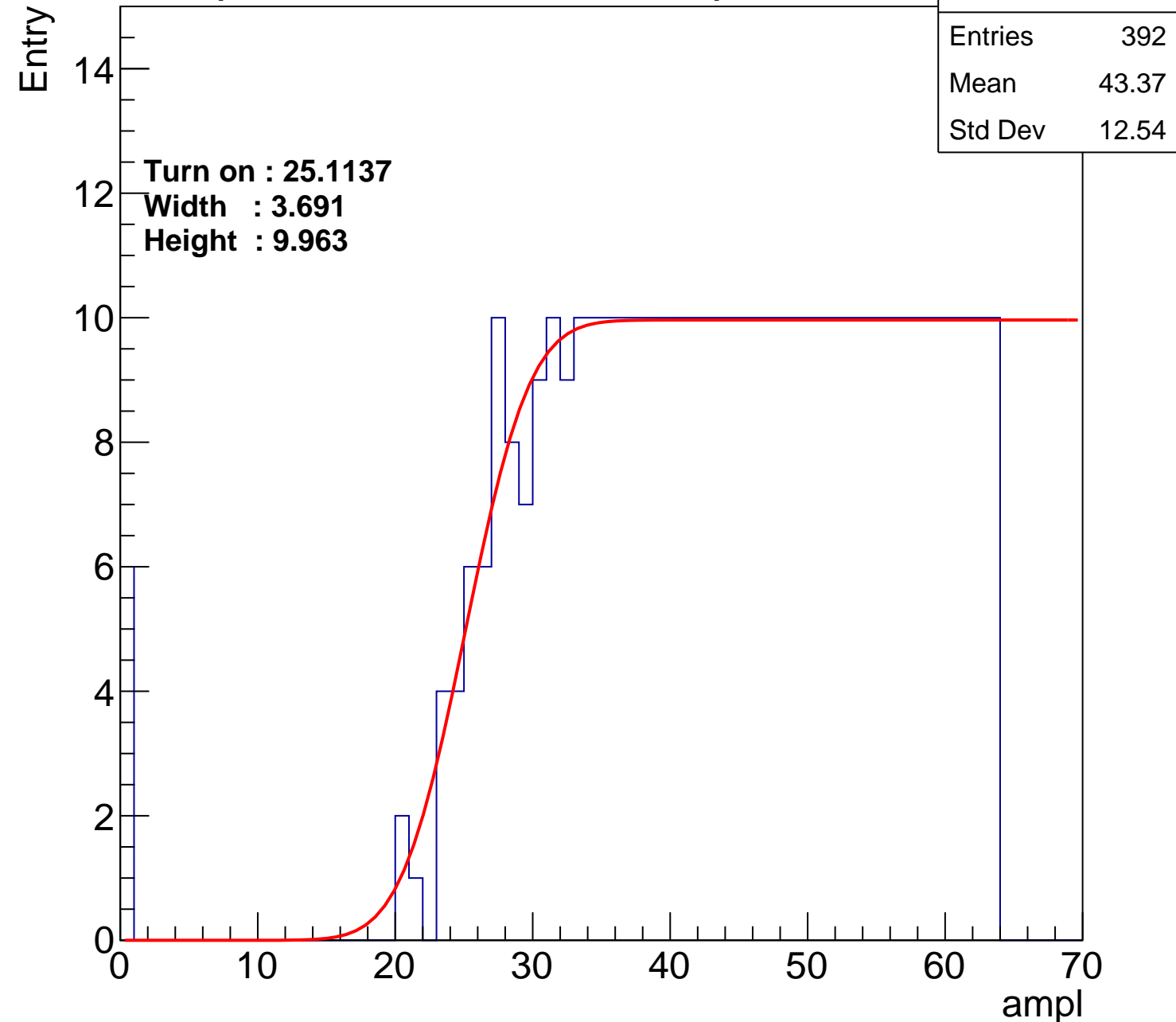
Width : 3.691

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch107

calib_packv5_042523_0143.root, FC#11, port A2

Entries	364
Mean	44.87
Std Dev	11.56

Turn on : 28.4518

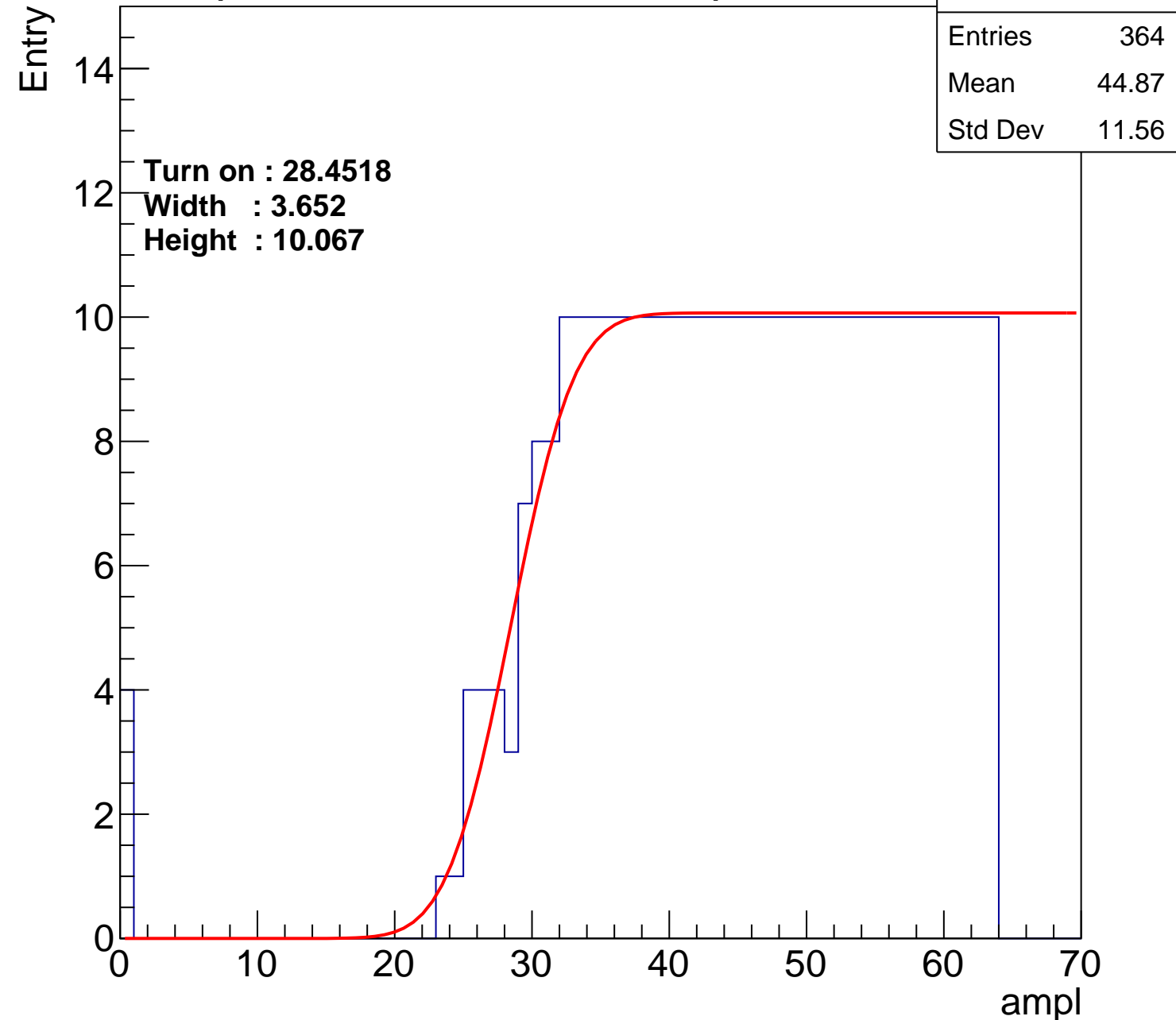
Width : 3.652

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch108

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.7080

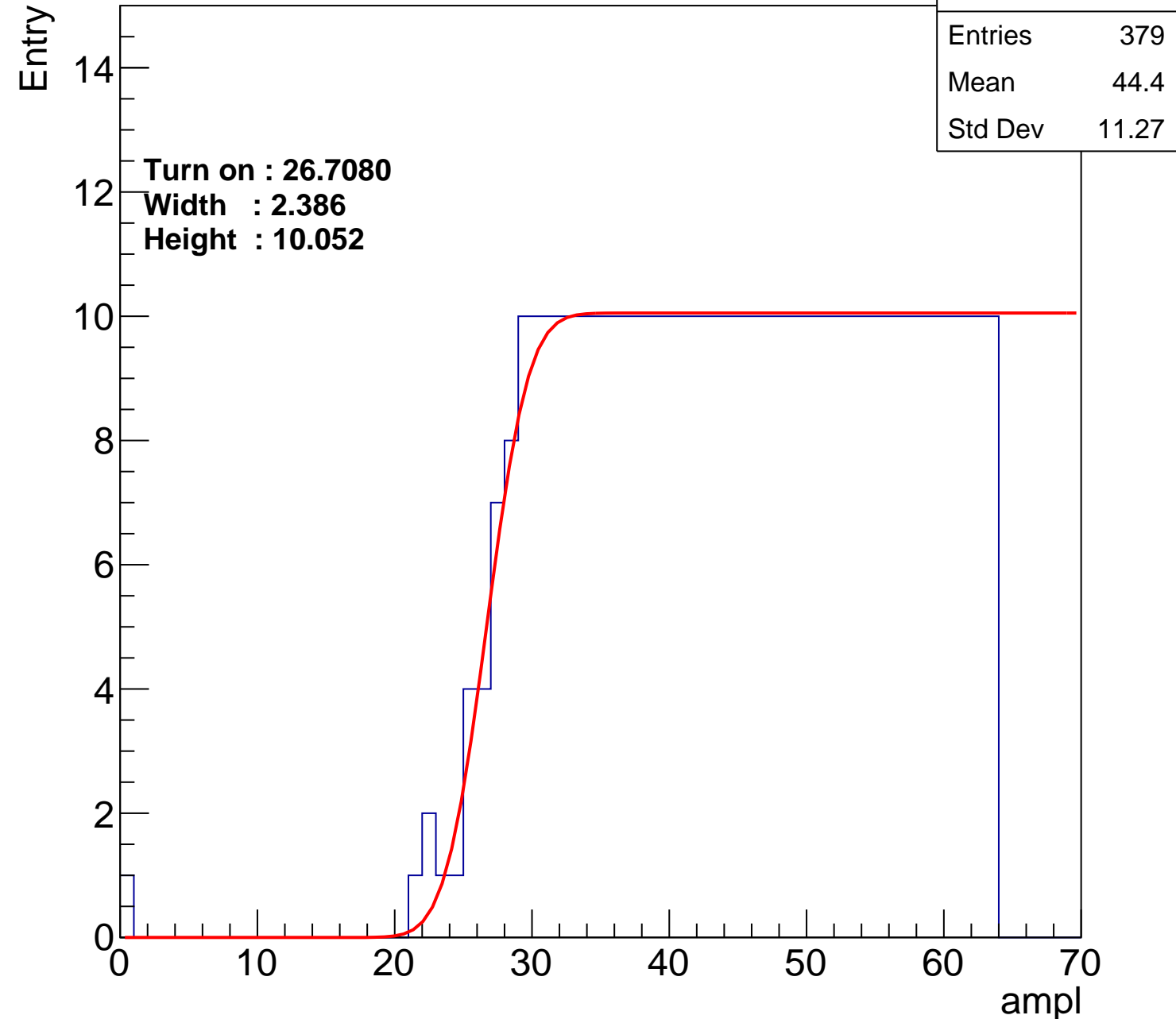
Width : 2.386

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch109

calib_packv5_042523_0143.root, FC#11, port A2

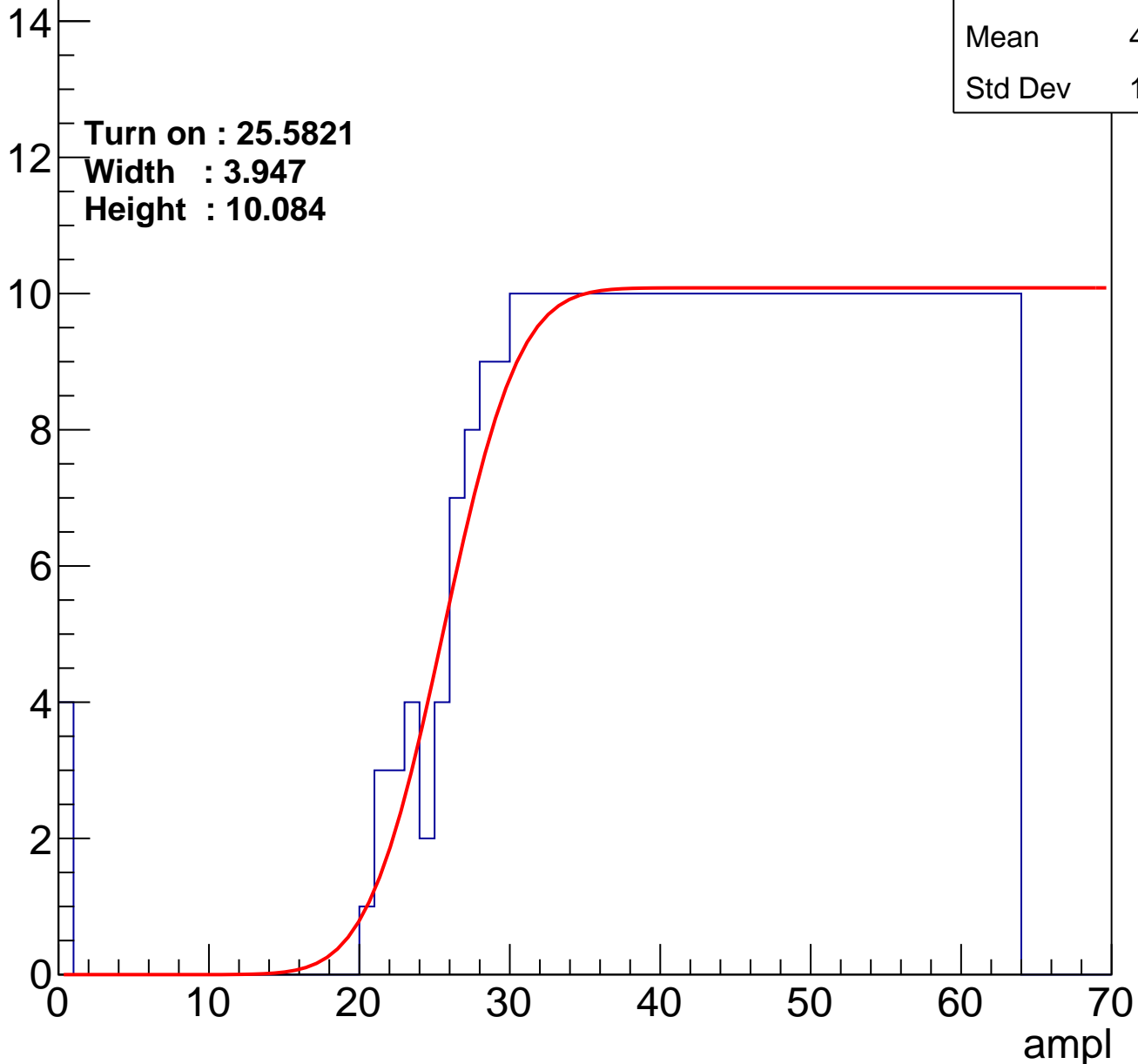
Entries	394
Mean	43.43
Std Dev	12.24

Turn on : 25.5821

Width : 3.947

Height : 10.084

Entry



B1L102S, U22-ch110

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.68
Std Dev	11.99

Turn on : 25.0973

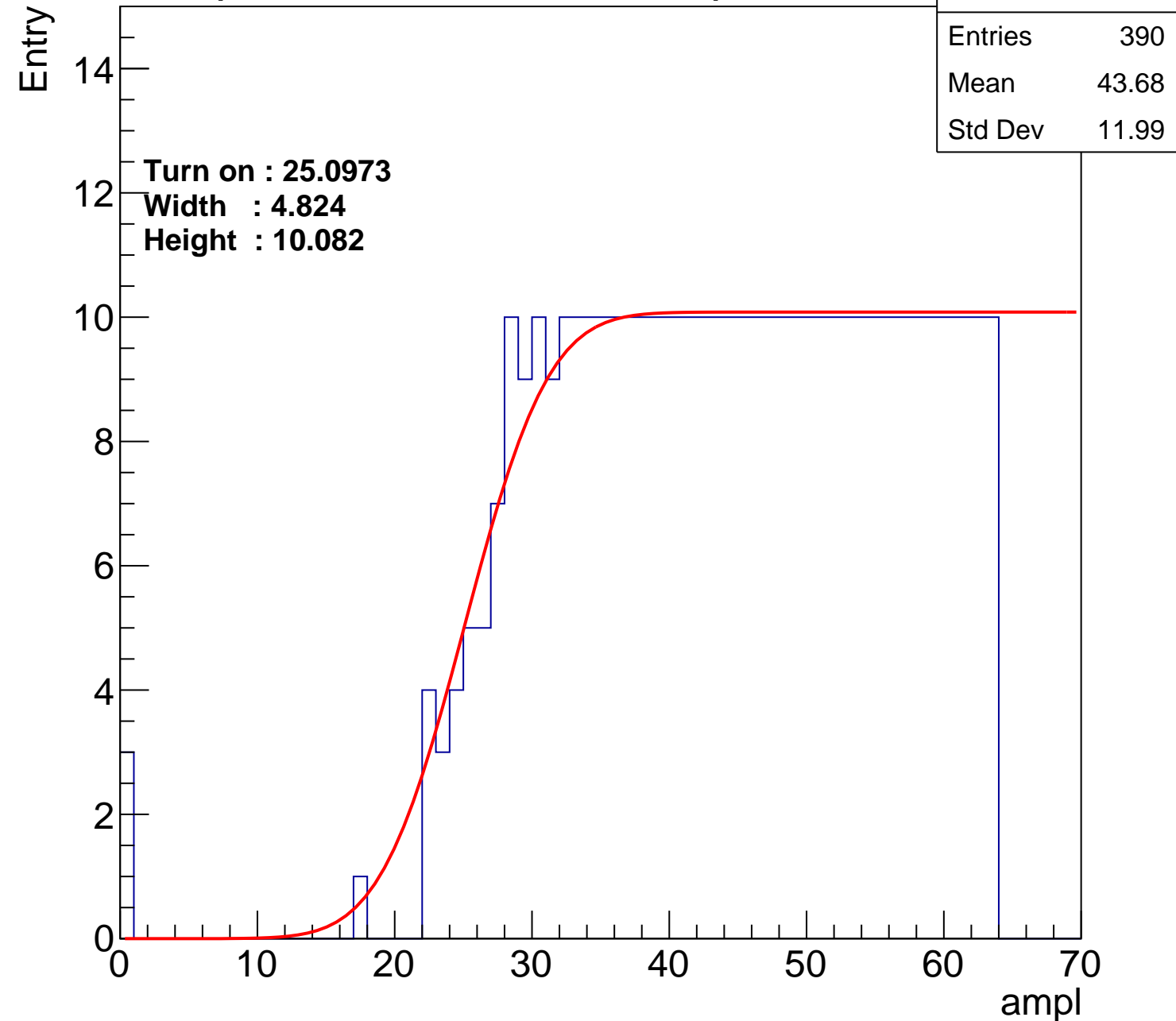
Width : 4.824

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch111

calib_packv5_042523_0143.root, FC#11, port A2

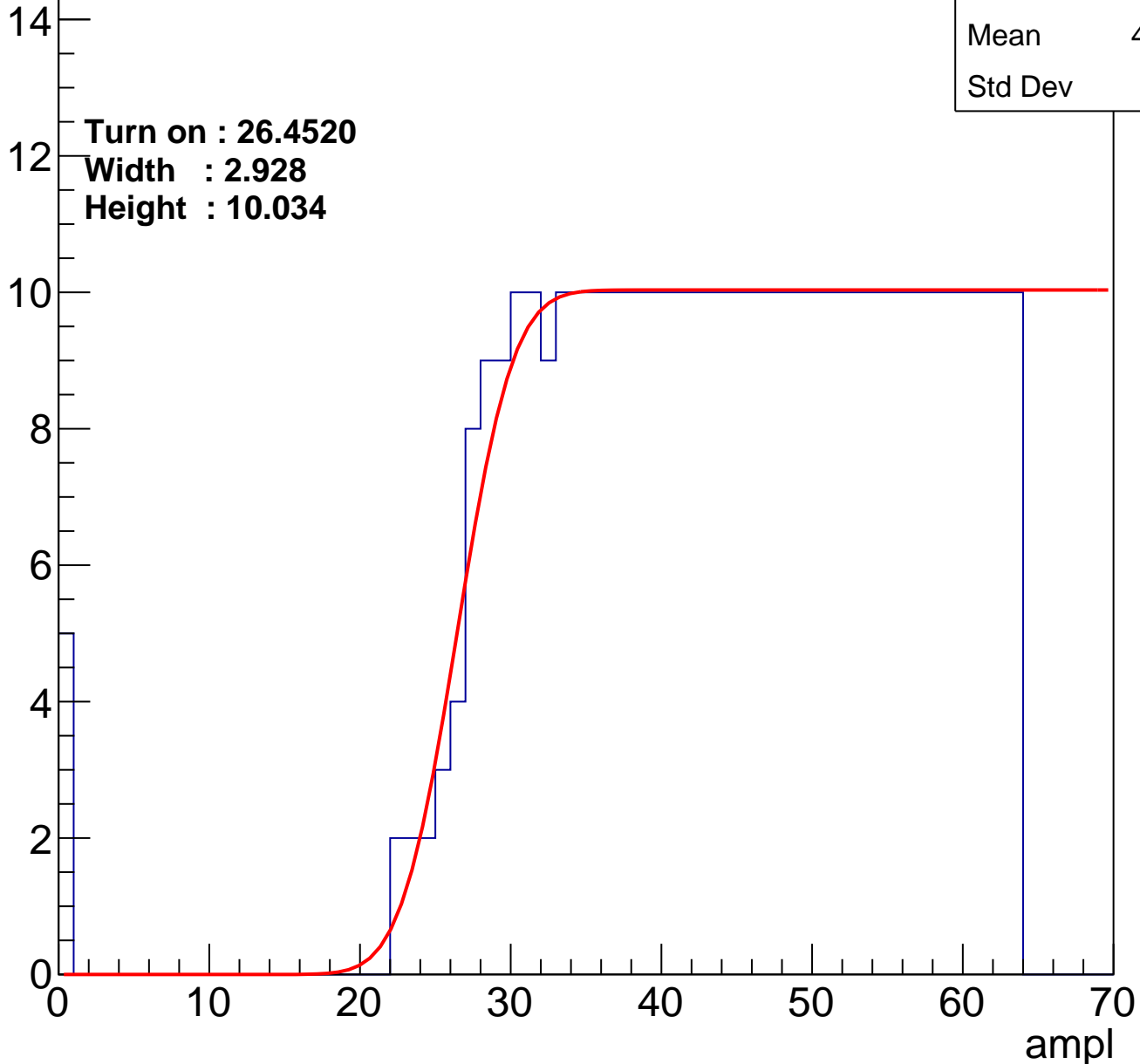
Entries	383
Mean	43.93
Std Dev	12.1

Turn on : 26.4520

Width : 2.928

Height : 10.034

Entry



B1L102S, U22-ch112

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 26.3342

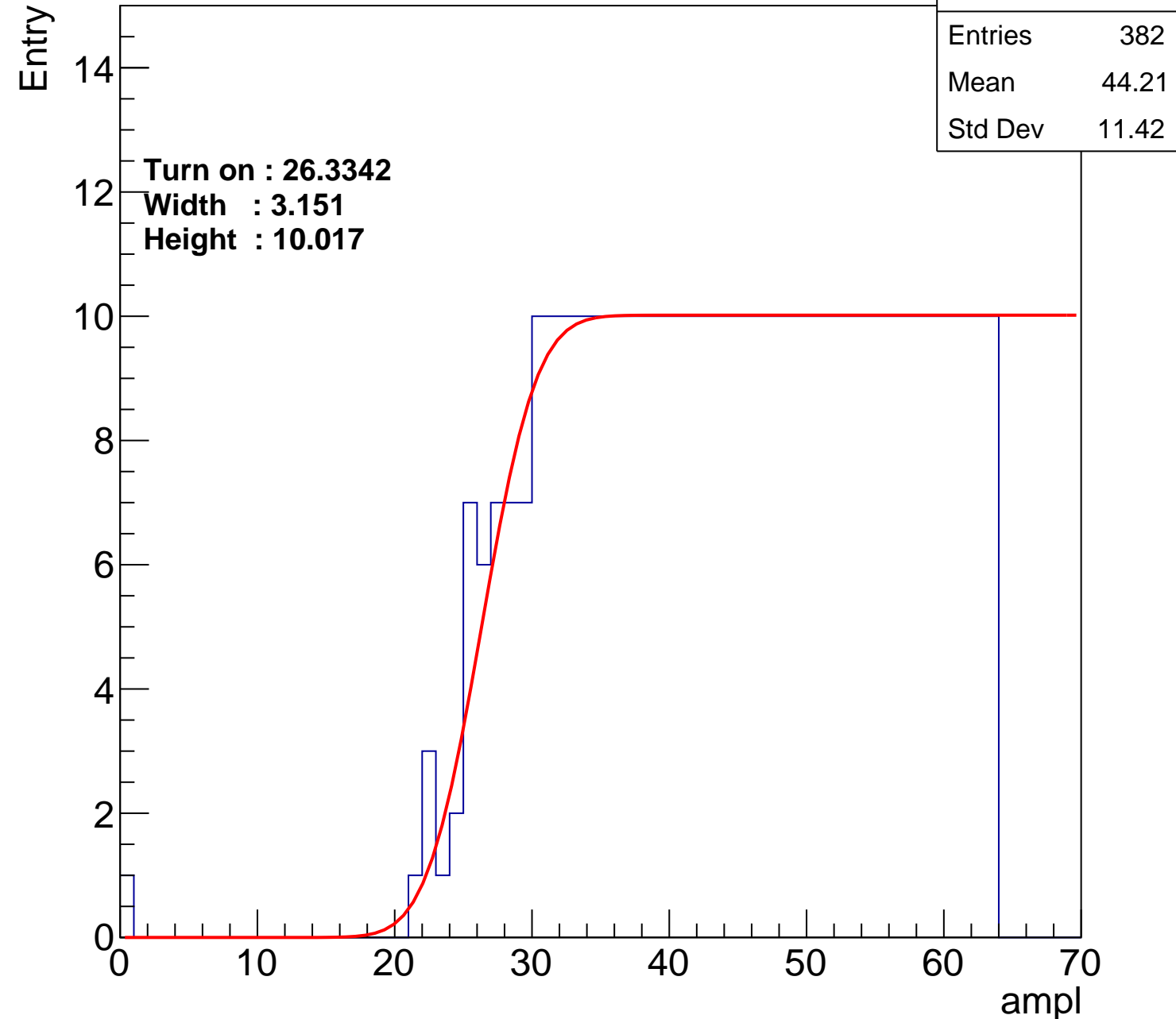
Width : 3.151

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch113

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.15
Std Dev	11.6

Turn on : 26.1517

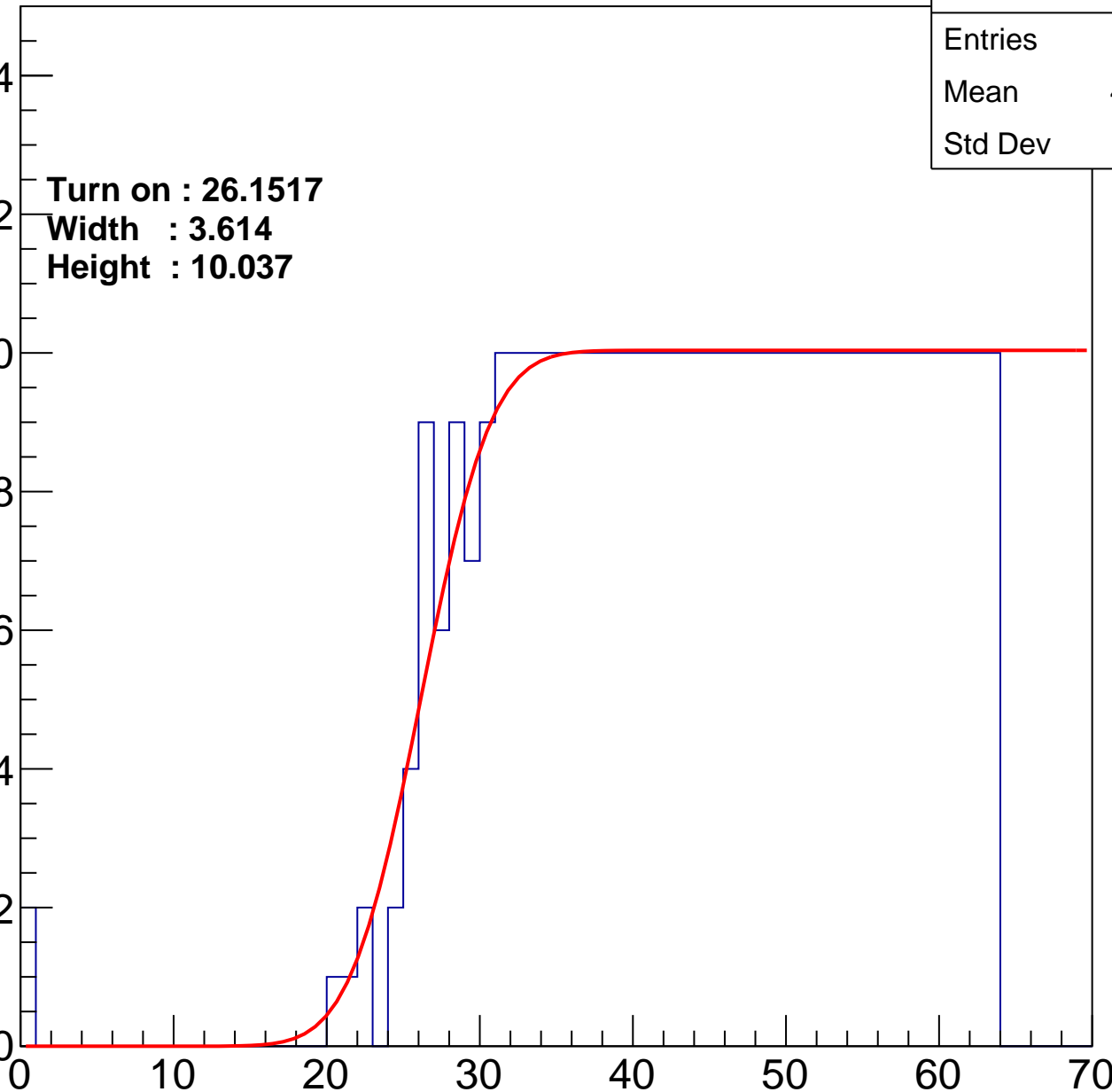
Width : 3.614

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch114

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.41
Std Dev	12.15

Turn on : 24.9836

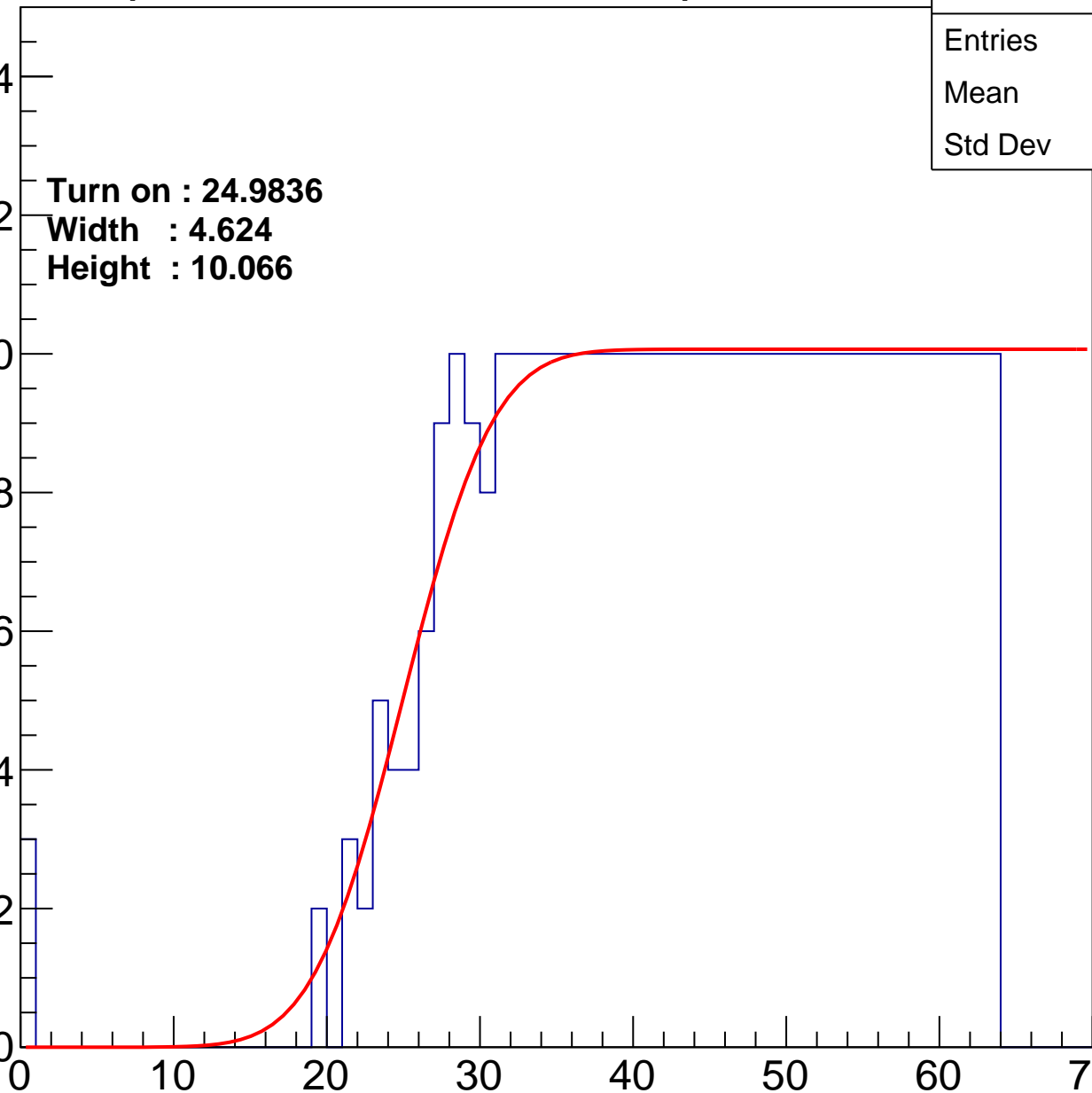
Width : 4.624

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch115

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.54
Std Dev	12.16

Turn on : 24.9844

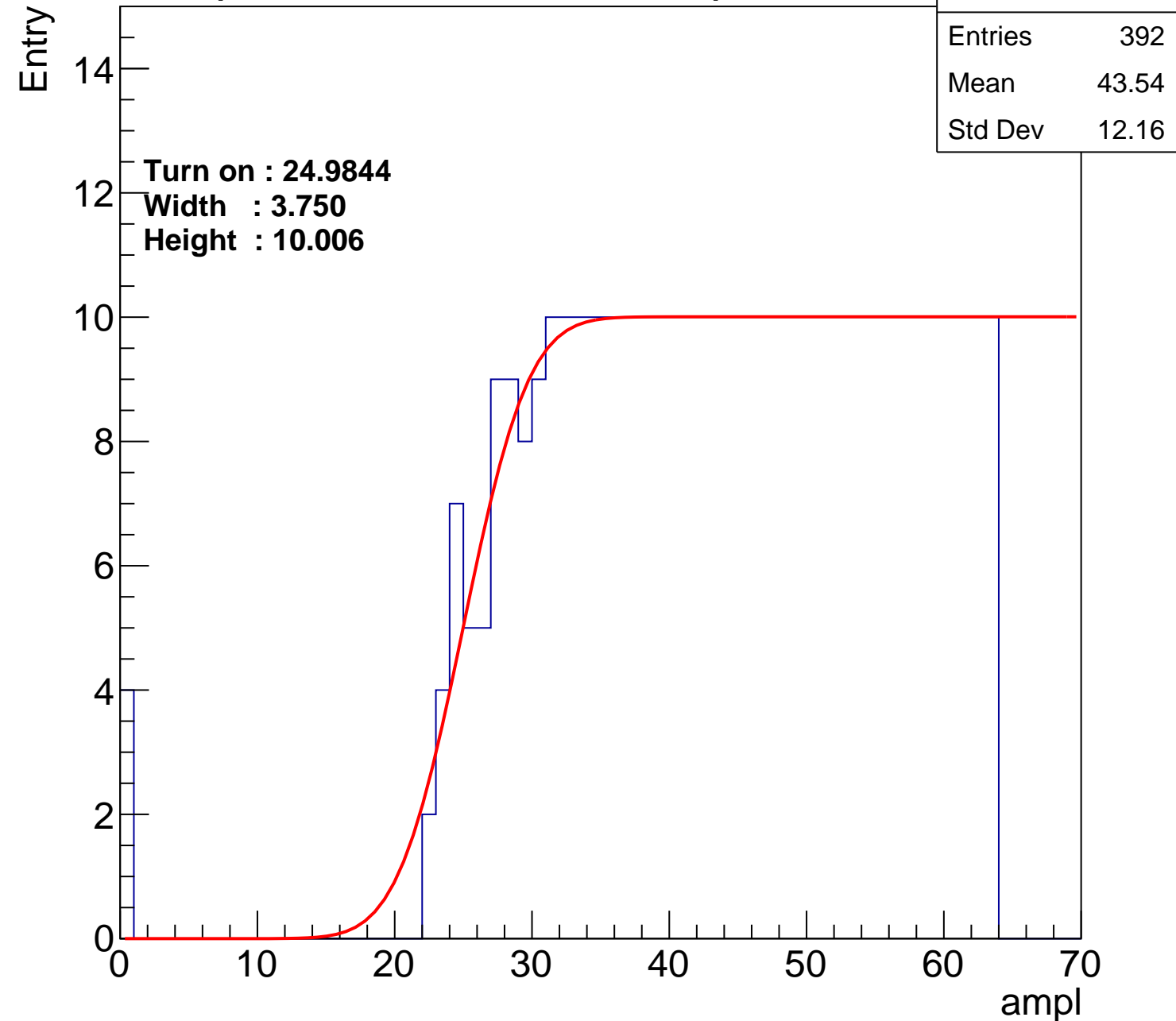
Width : 3.750

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch116

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.79
Std Dev	11.8

Turn on : 25.4473

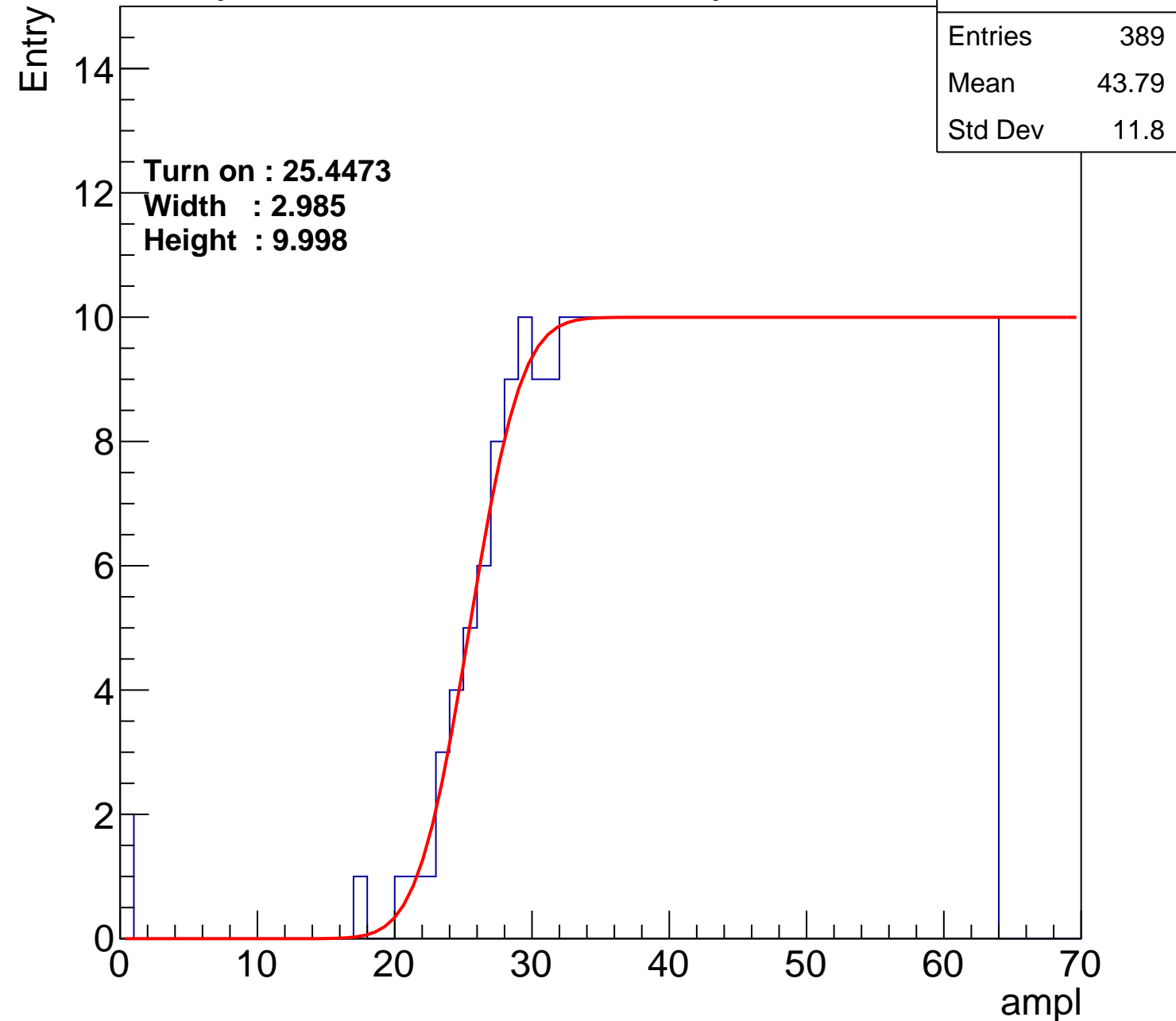
Width : 2.985

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch117

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.18
Std Dev	11.45

Turn on : 26.6019

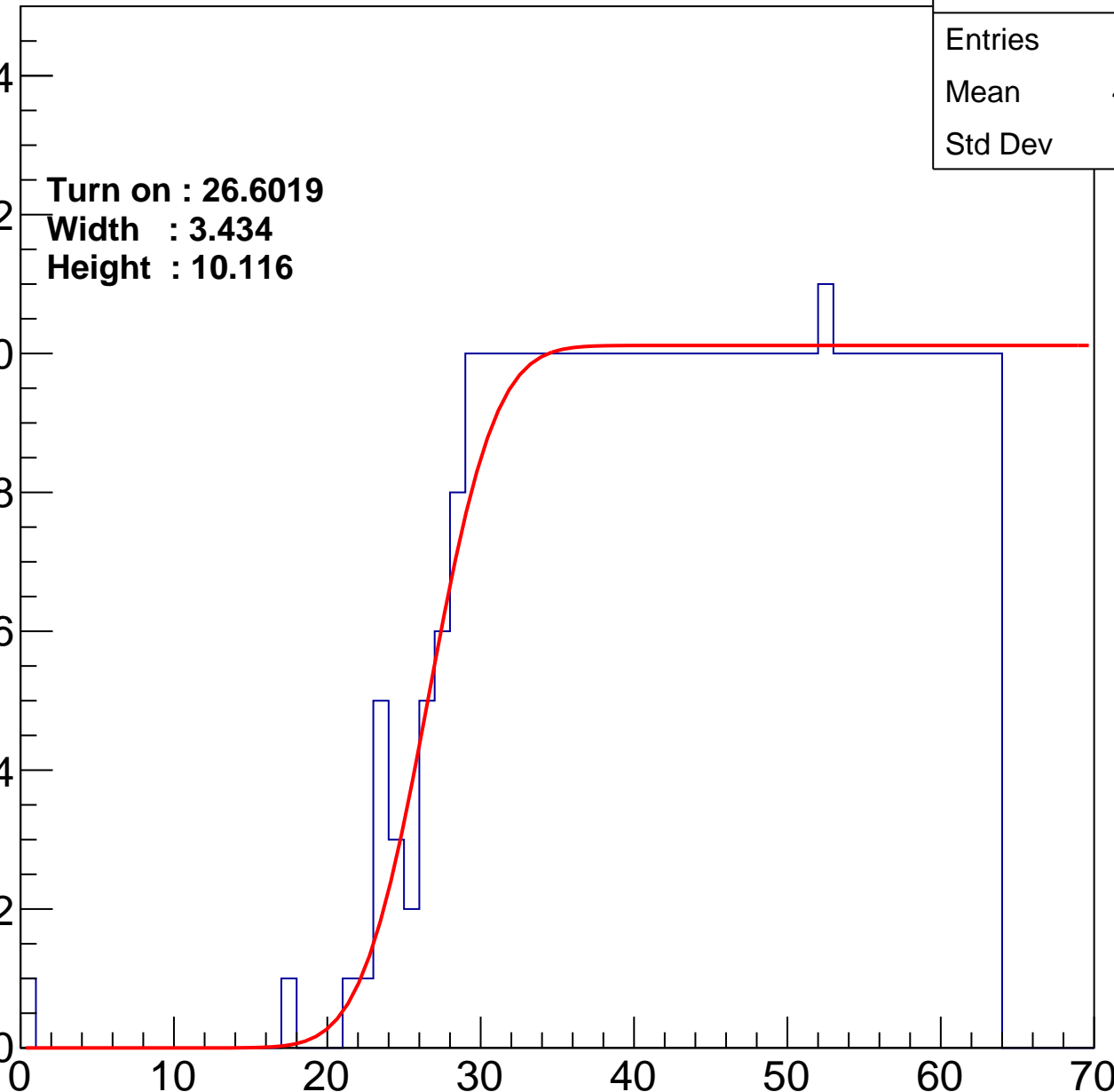
Width : 3.434

Height : 10.116

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch118

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.44
Std Dev	11.45

Turn on : 27.0229

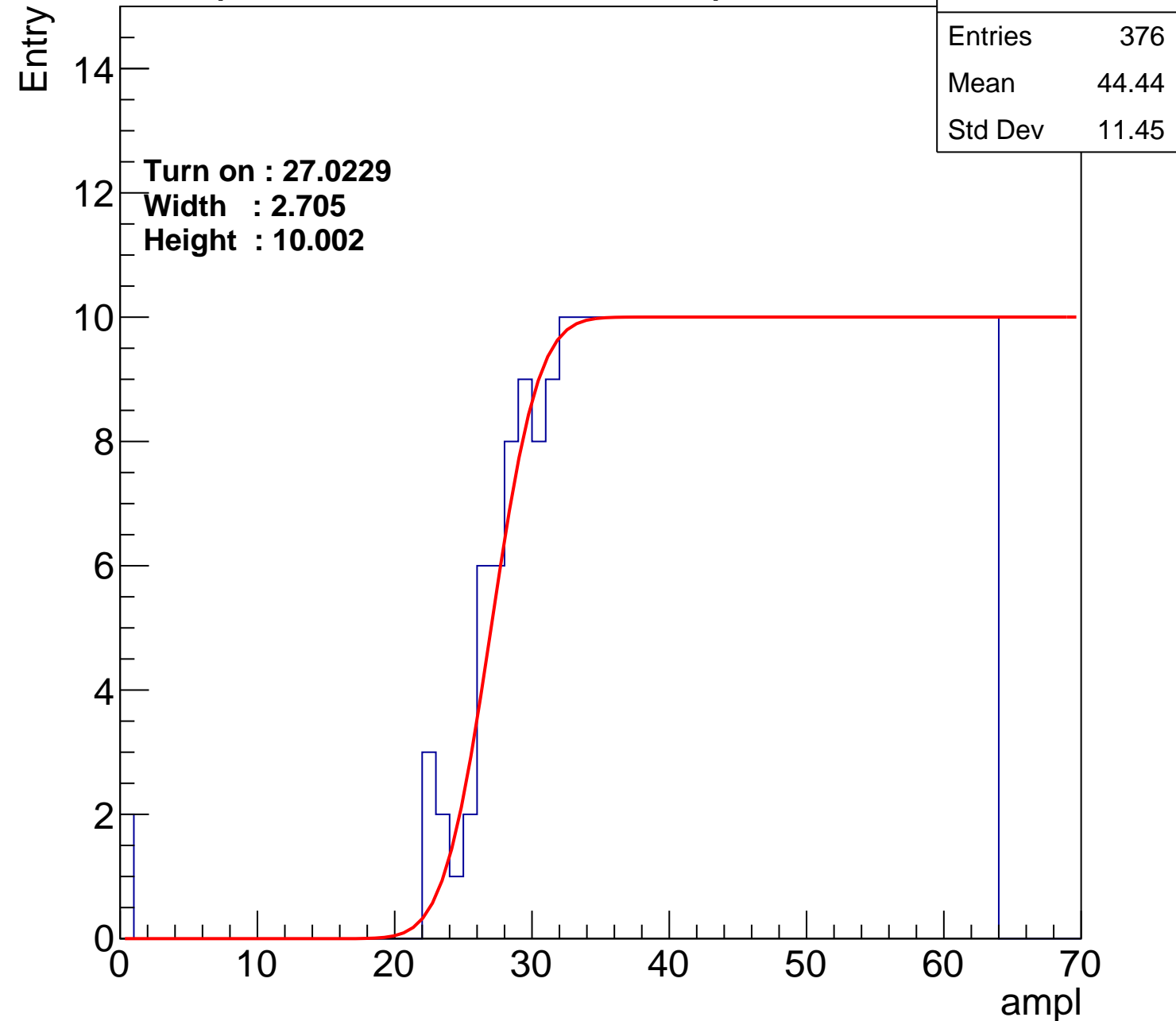
Width : 2.705

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch119

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.44
Std Dev	11.52

Turn on : 26.7725

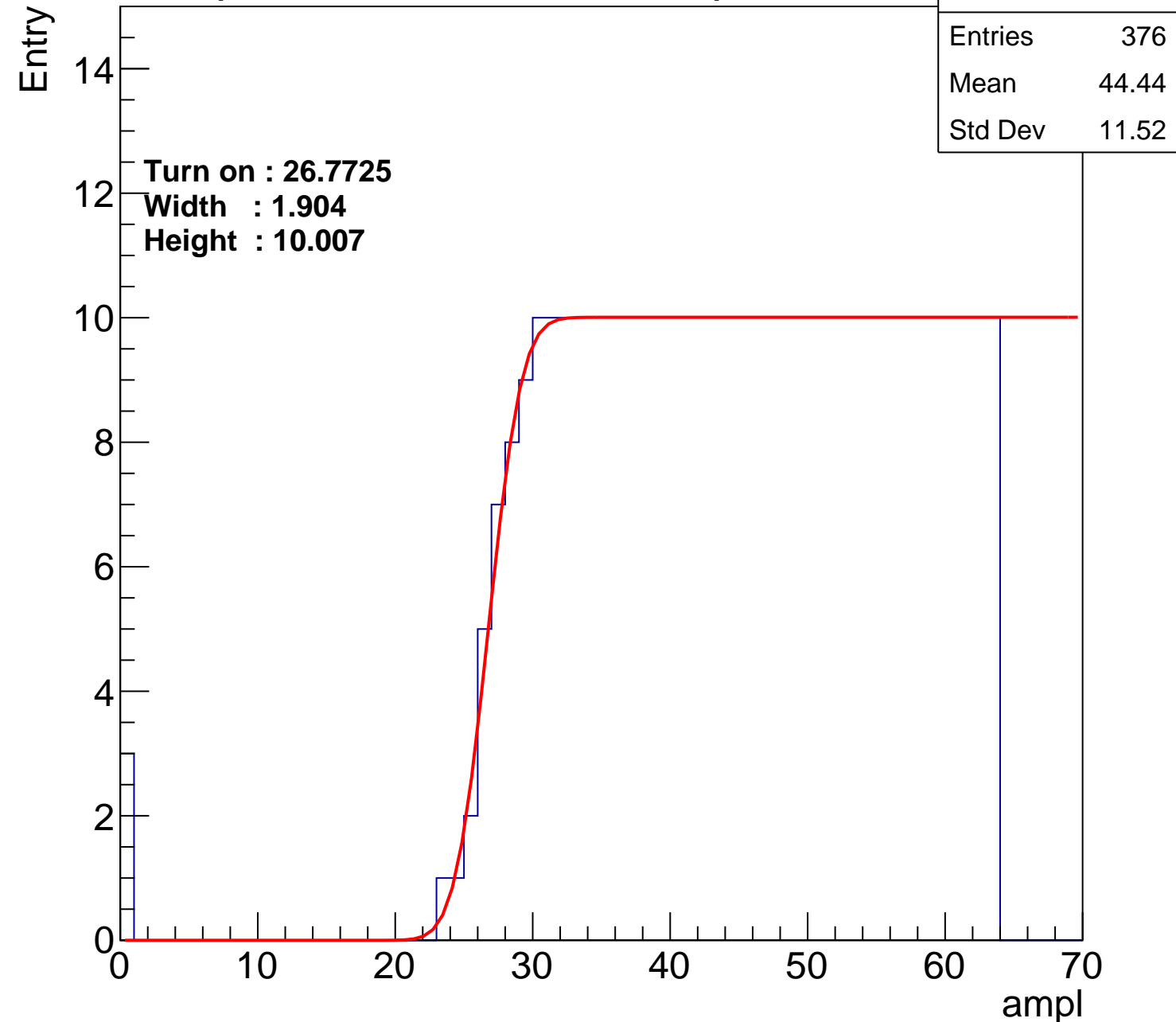
Width : 1.904

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch120

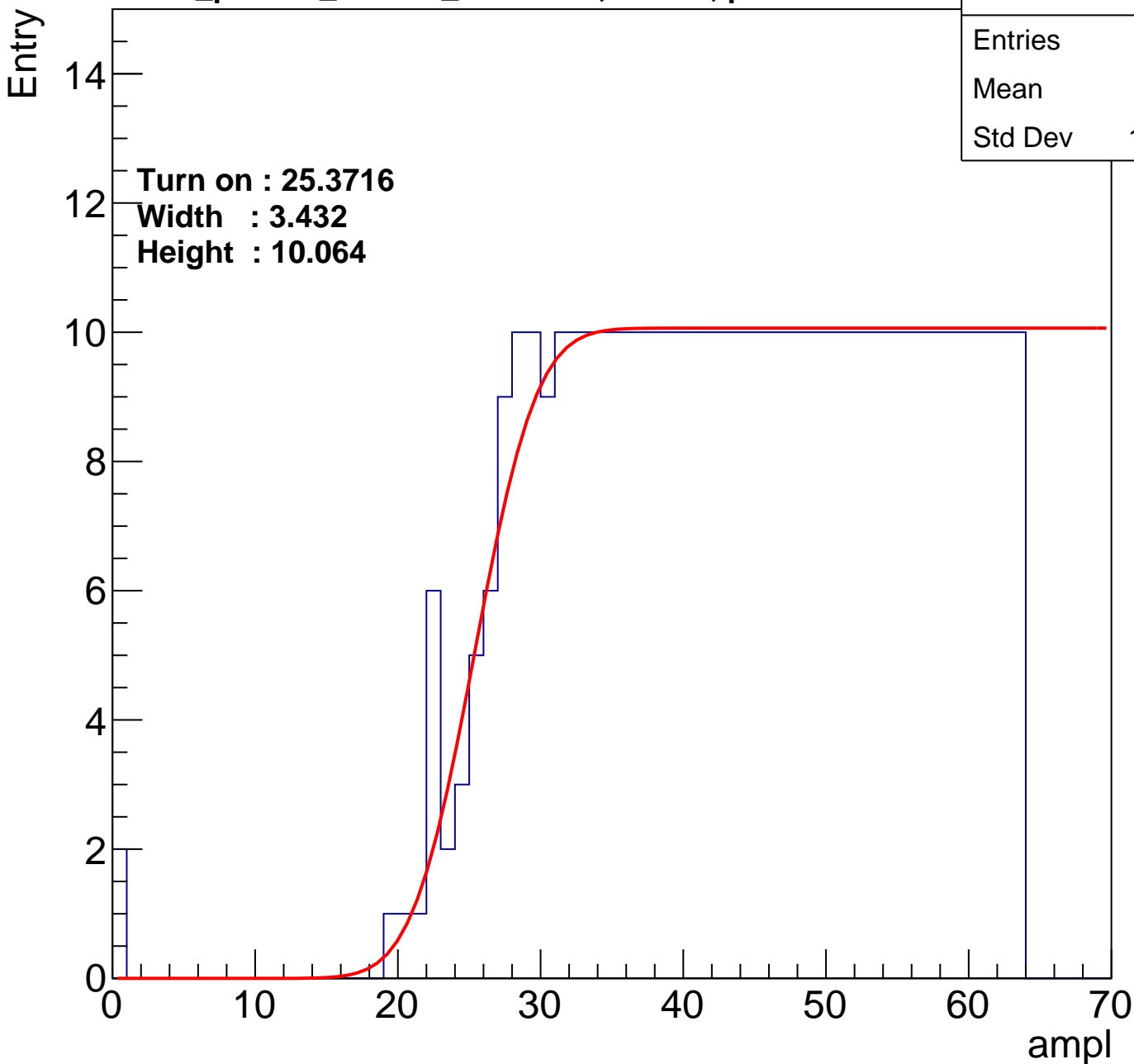
calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.5
Std Dev	11.94

Turn on : 25.3716

Width : 3.432

Height : 10.064



B1L102S, U22-ch121

calib_packv5_042523_0143.root, FC#11, port A2

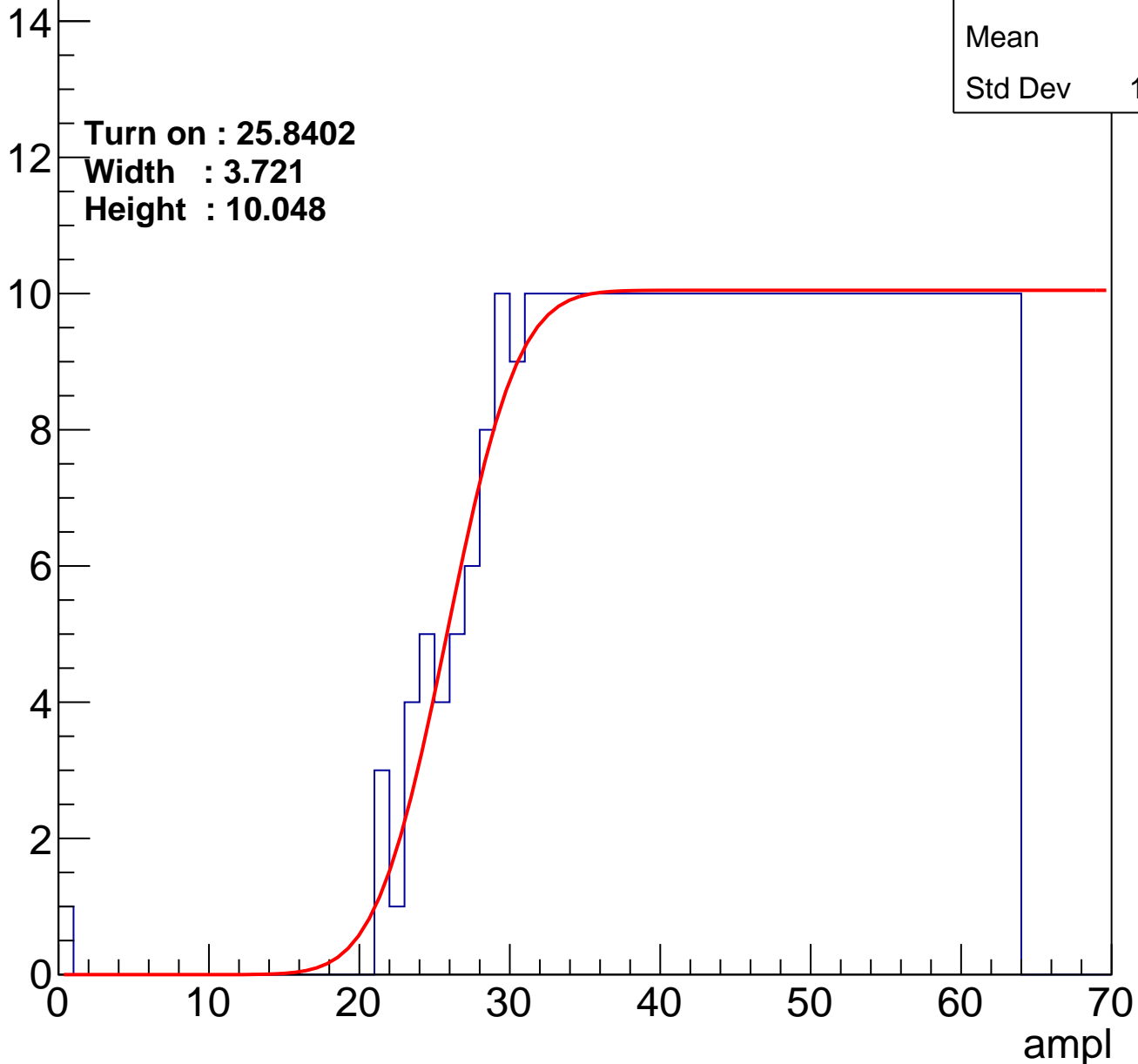
Entries	386
Mean	44
Std Dev	11.55

Turn on : 25.8402

Width : 3.721

Height : 10.048

Entry



B1L102S, U22-ch122

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.64
Std Dev	12.12

Turn on : 26.3201

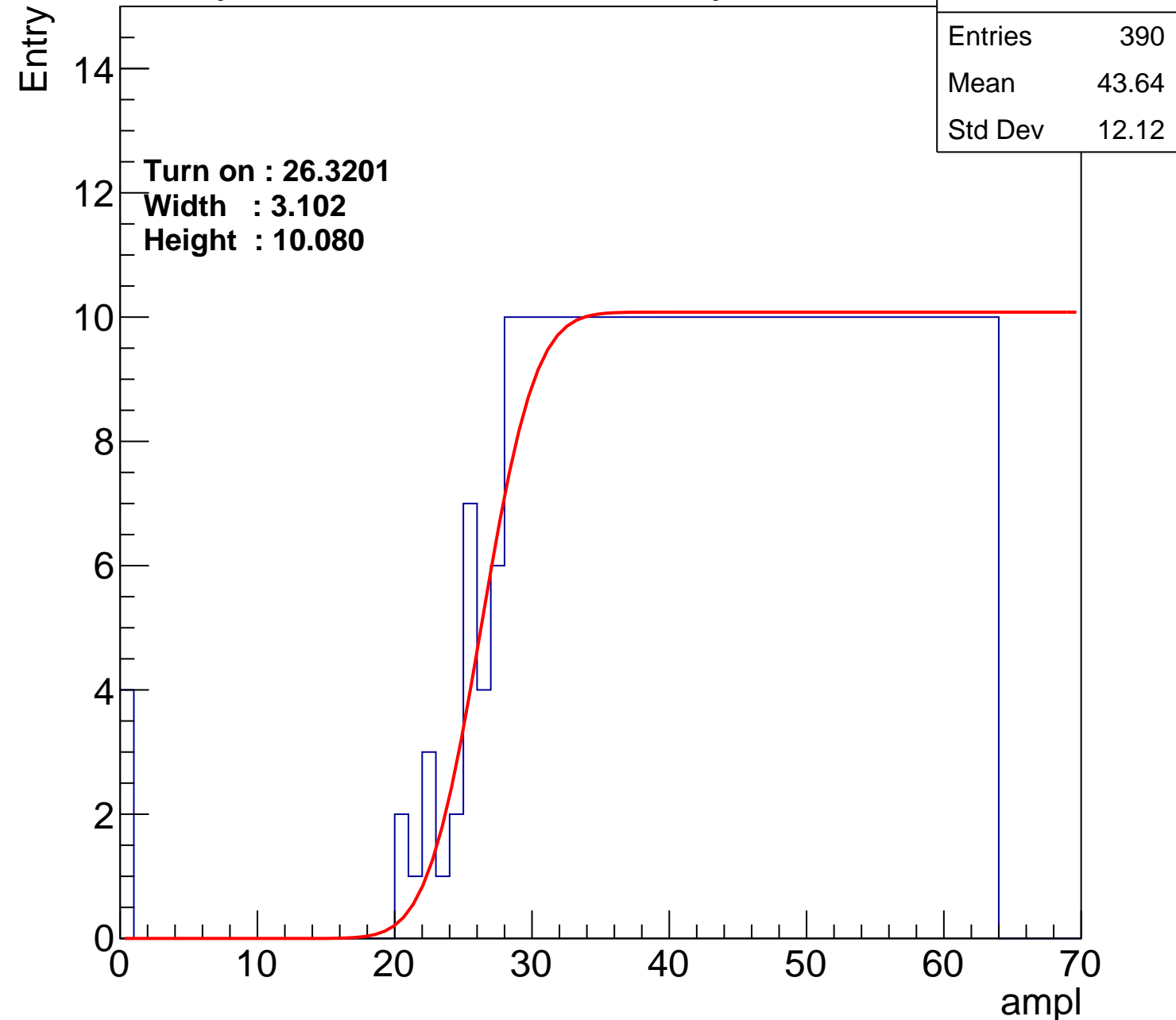
Width : 3.102

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch123

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.44
Std Dev	11.61

Turn on : 26.9707

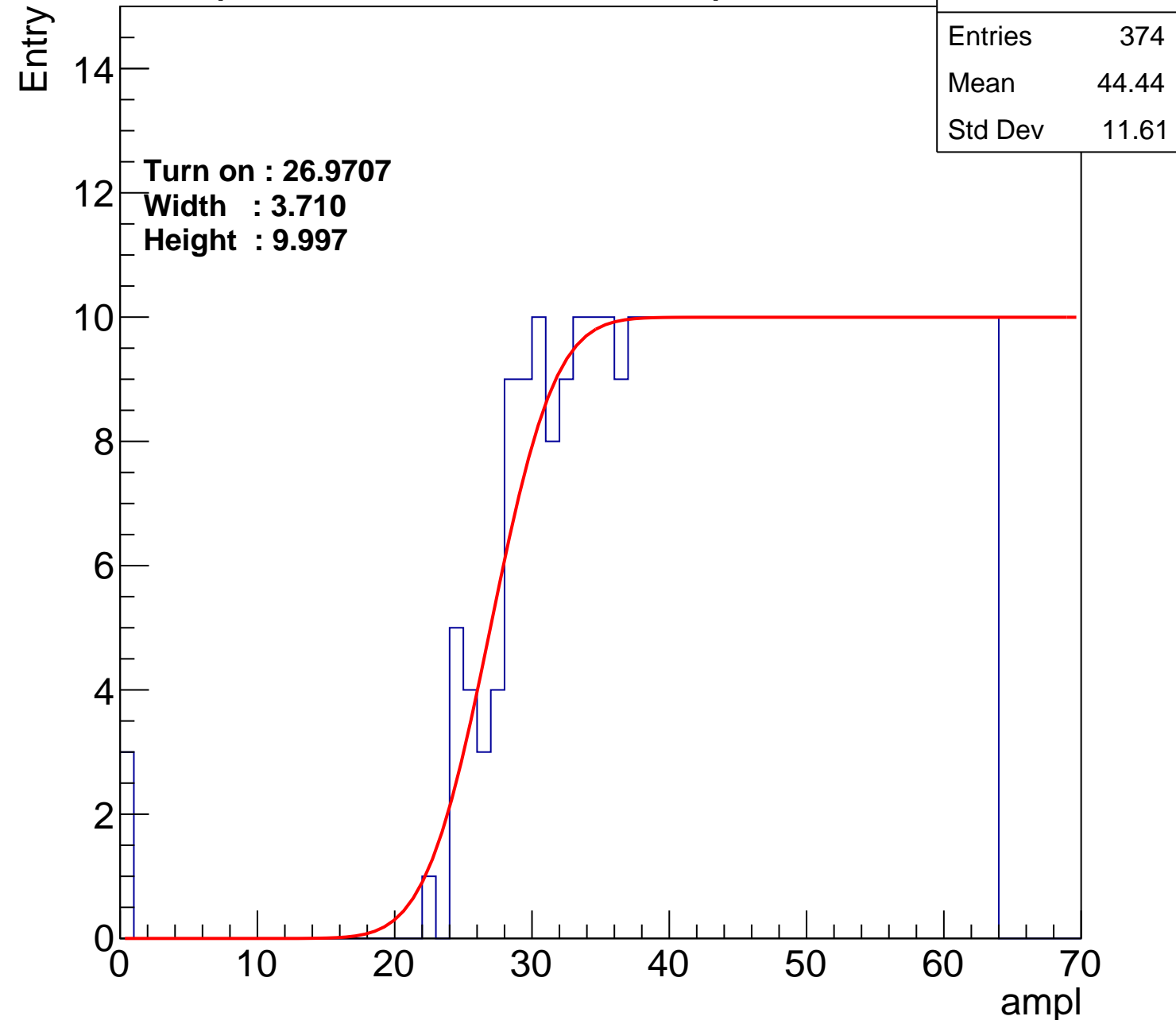
Width : 3.710

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch124

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.32
Std Dev	12.17

Turn on : 25.3057

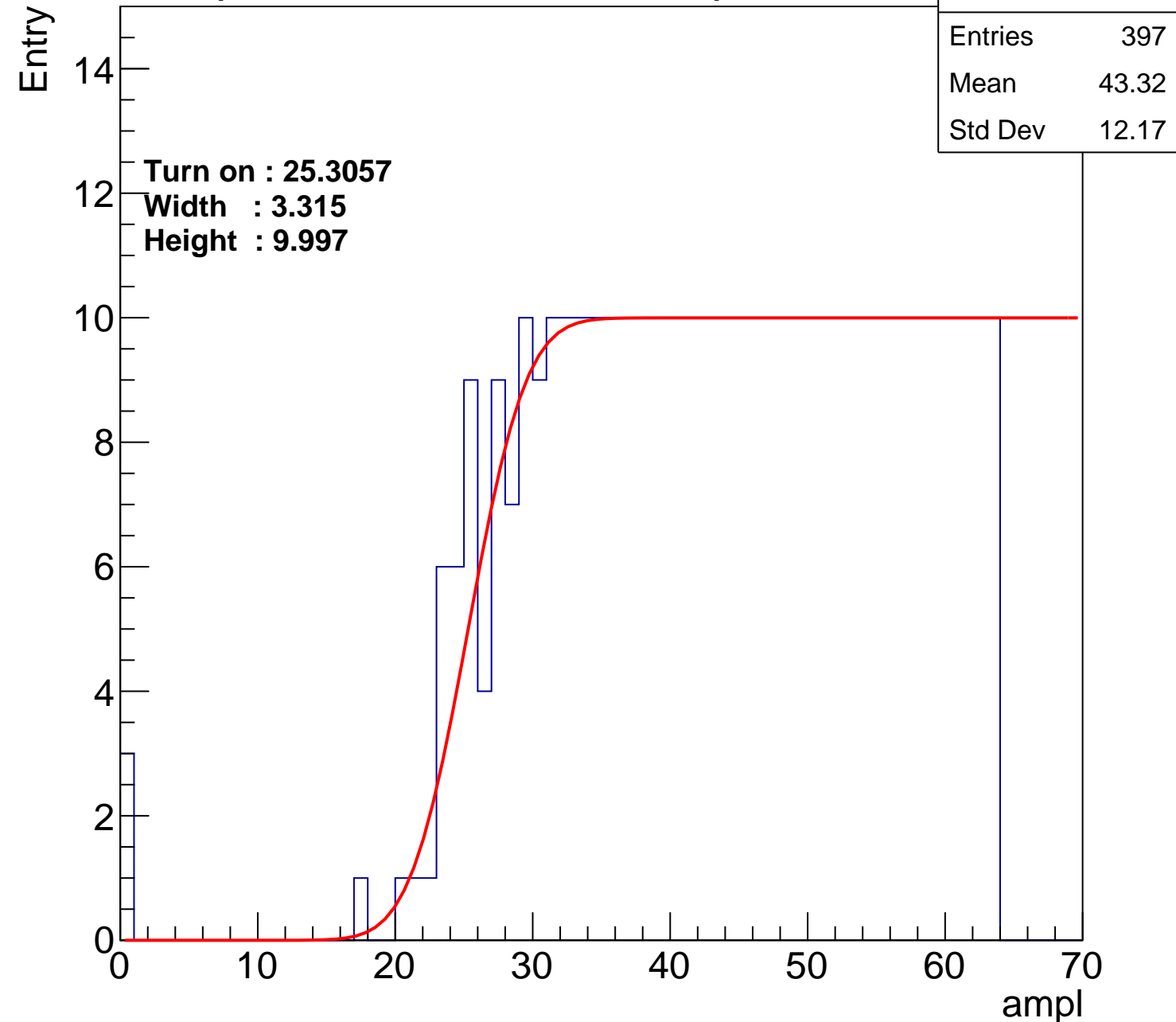
Width : 3.315

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch125

calib_packv5_042523_0143.root, FC#11, port A2

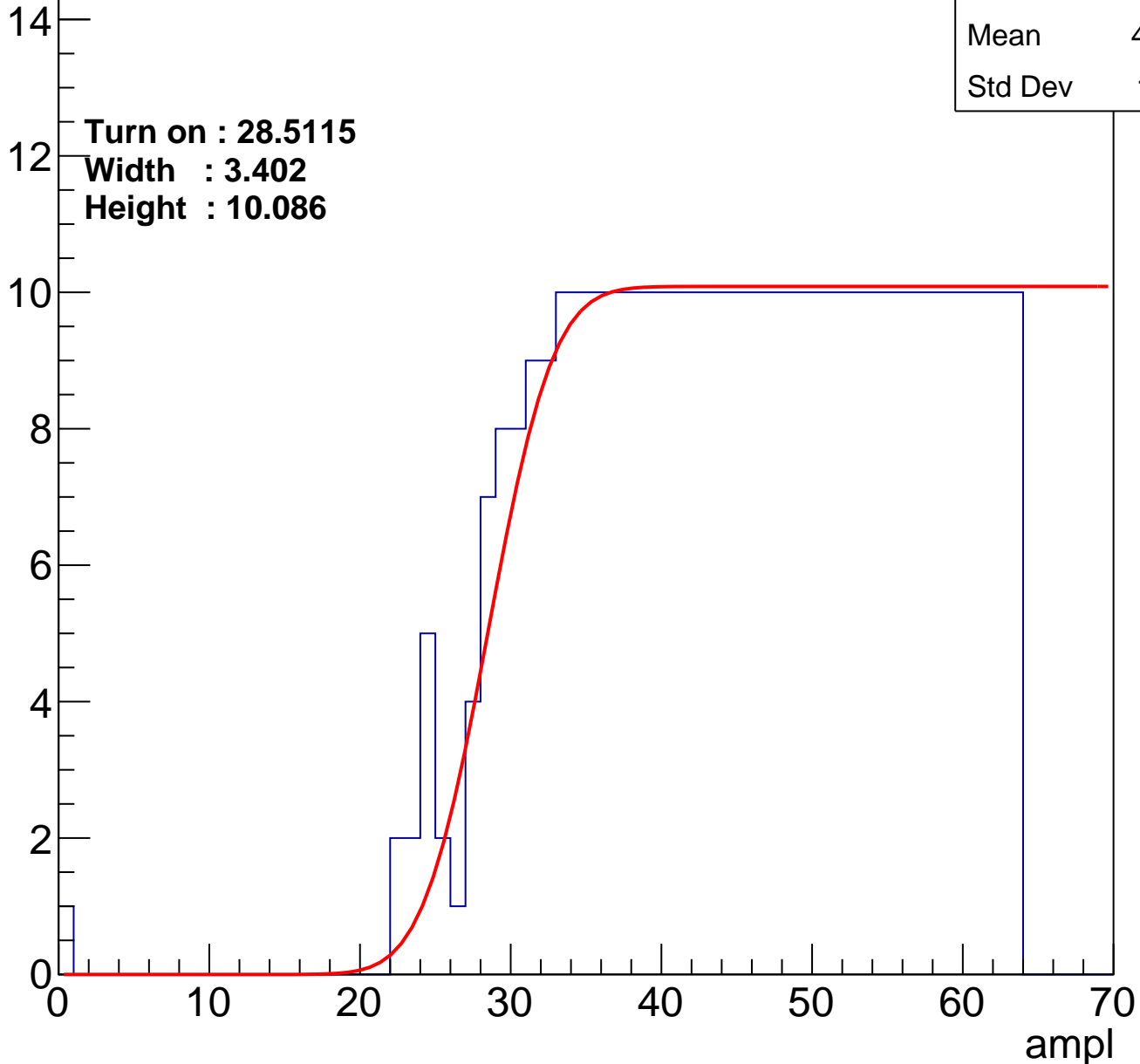
Entries	368
Mean	44.86
Std Dev	11.11

Turn on : 28.5115

Width : 3.402

Height : 10.086

Entry



B1L102S, U22-ch126

calib_packv5_042523_0143.root, FC#11, port A2

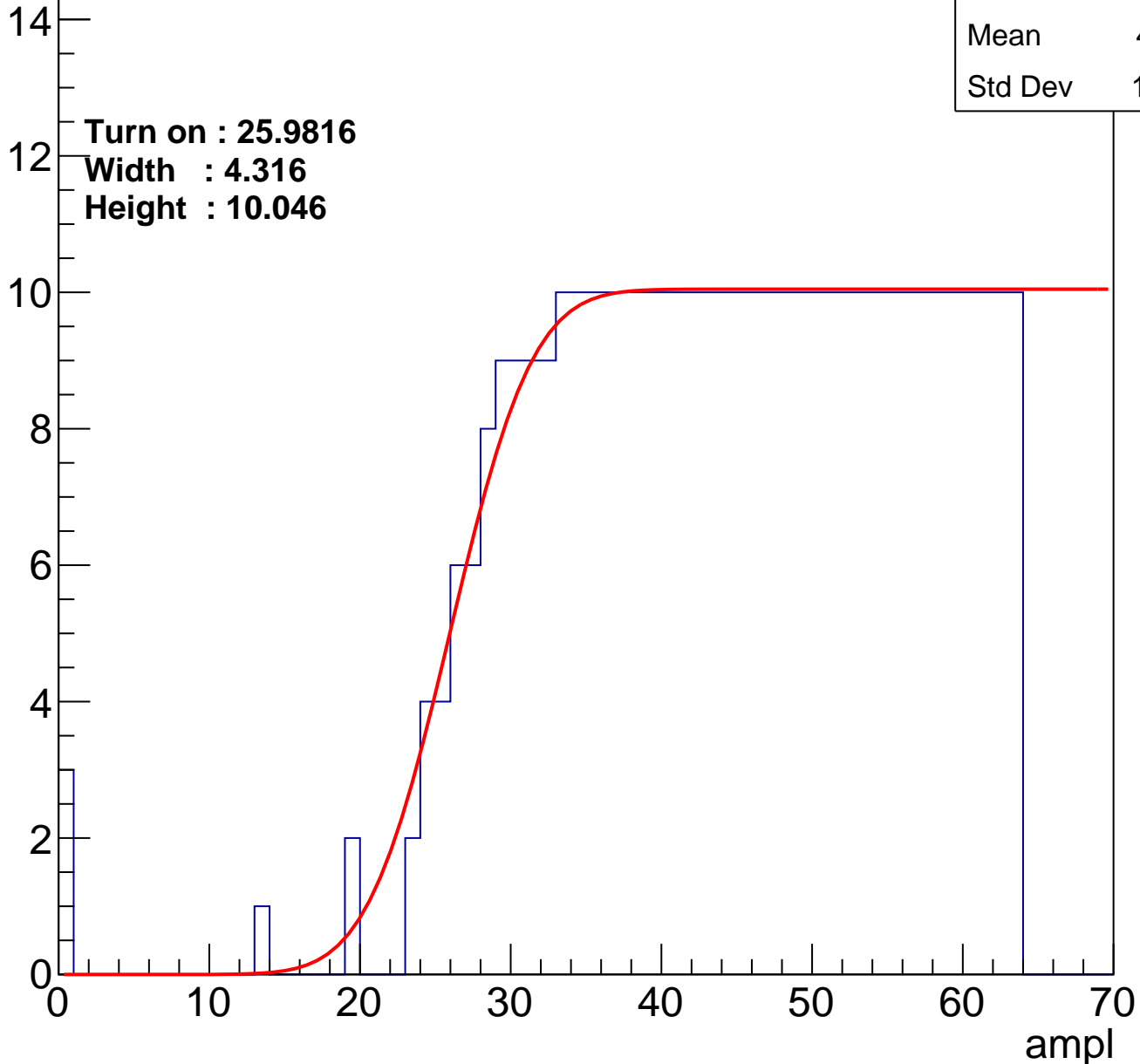
Entries	382
Mean	44.01
Std Dev	11.89

Turn on : 25.9816

Width : 4.316

Height : 10.046

Entry



B1L102S, U22-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.31
Std Dev	11.79

Turn on : 26.1177

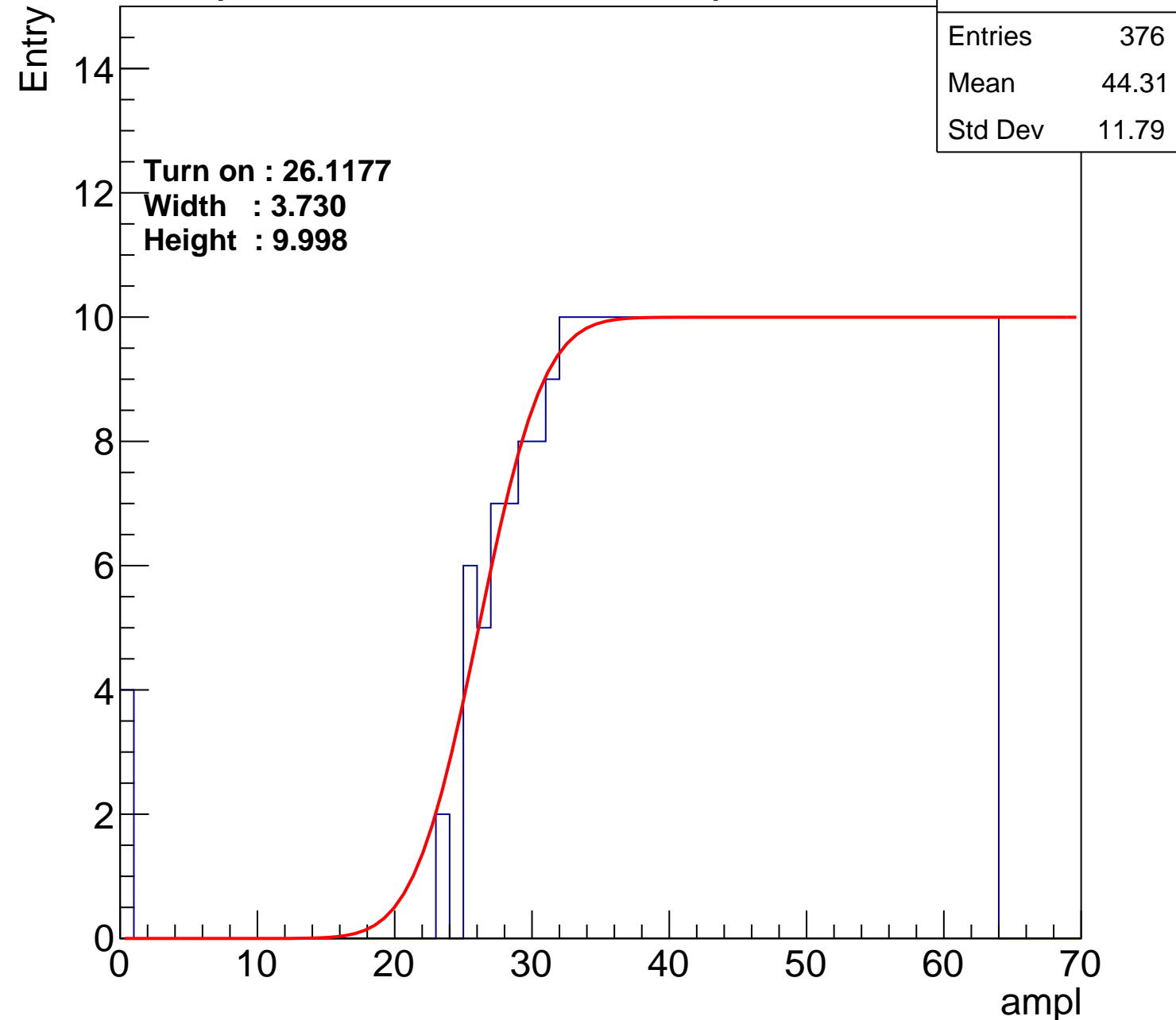
Width : 3.730

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U22-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.31
Std Dev	11.79

Turn on : 26.1177

Width : 3.730

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl

