

B1L103S, U5-ch0, adc0

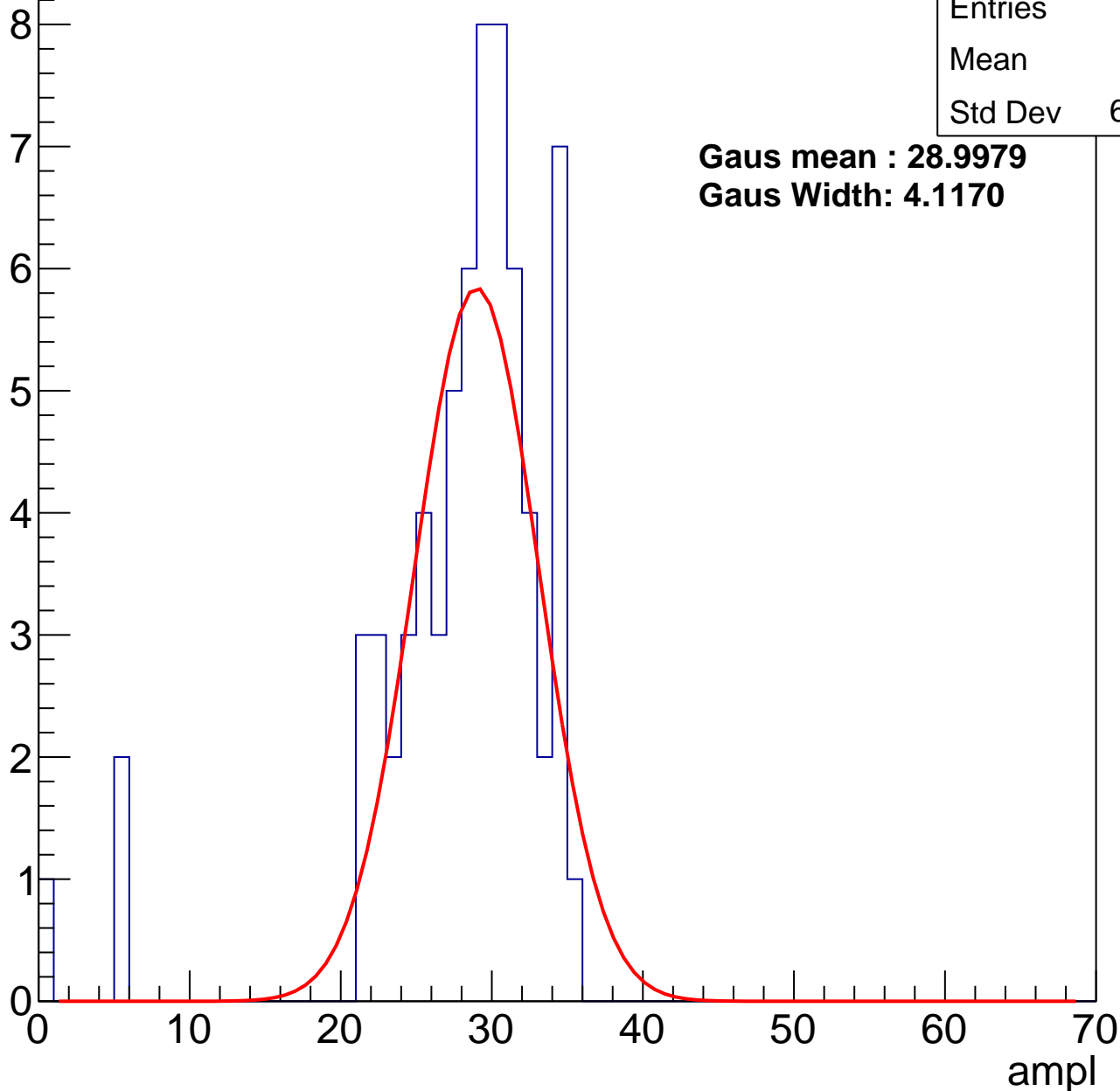
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.4
Std Dev	6.339

Gaus mean : 28.9979

Gaus Width: 4.1170



B1L103S, U5-ch0, adc1

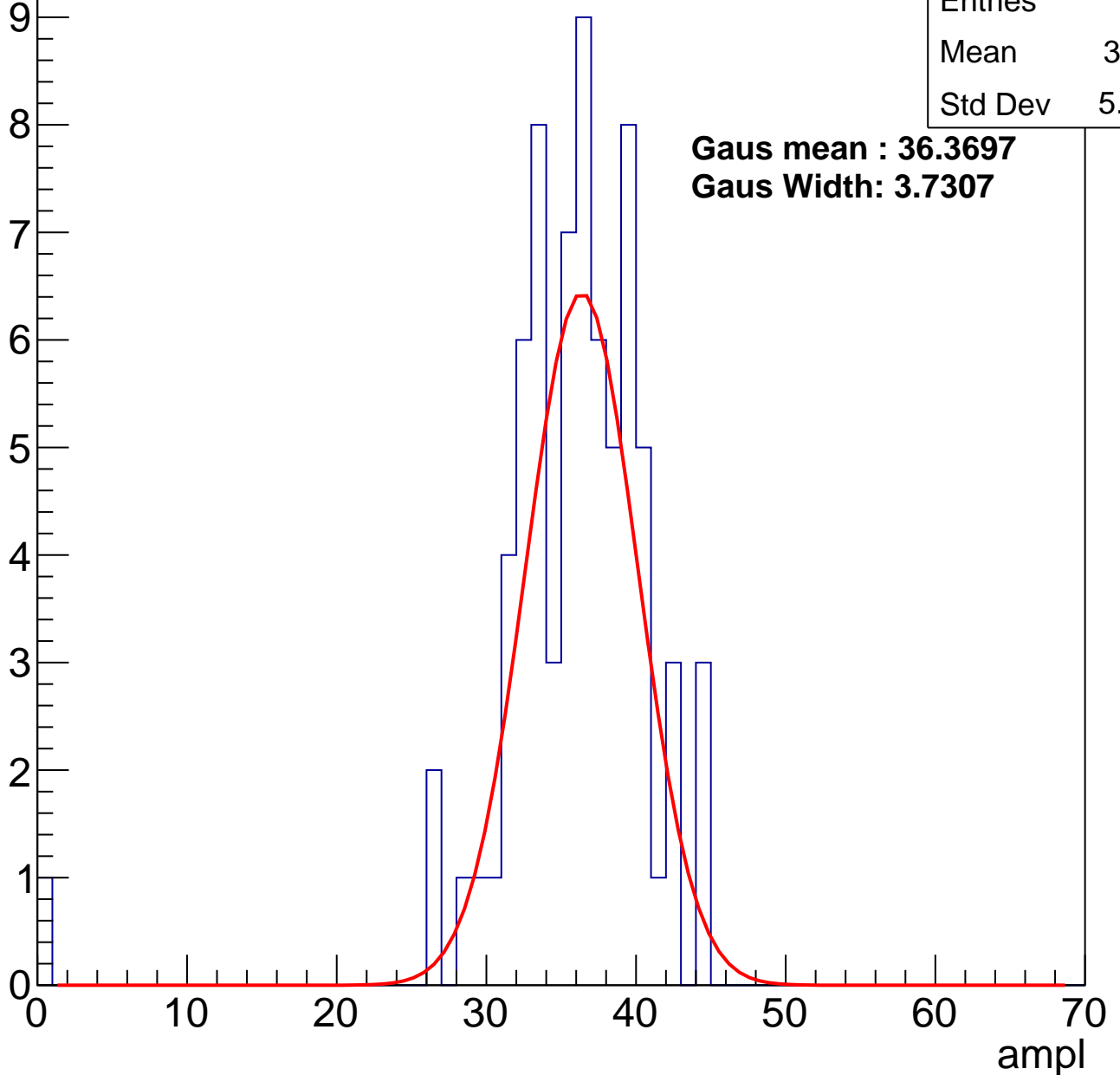
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.31
Std Dev	5.695

Gaus mean : 36.3697

Gaus Width: 3.7307



B1L103S, U5-ch0, adc2

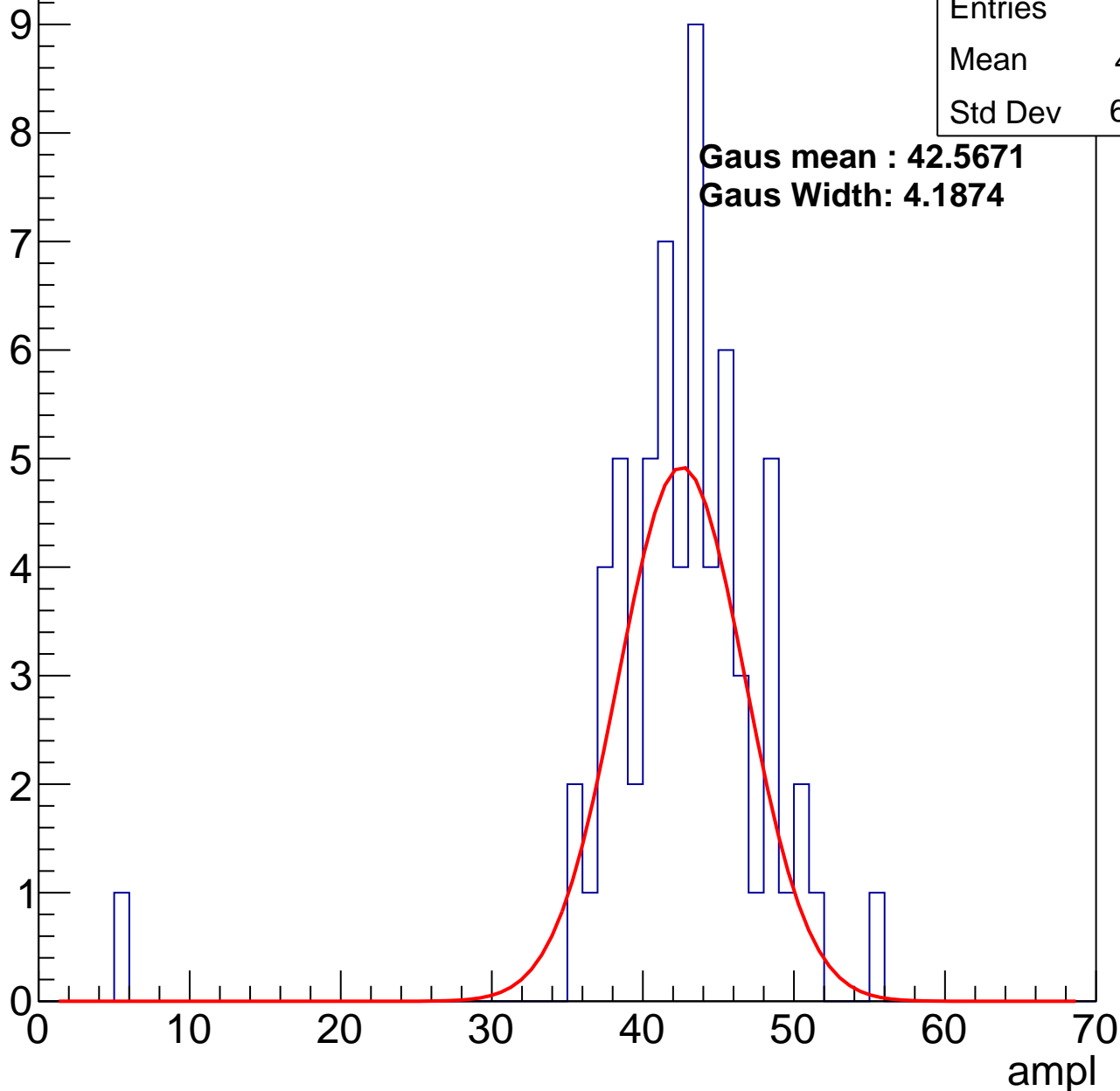
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.11
Std Dev	6.233

Gaus mean : 42.5671

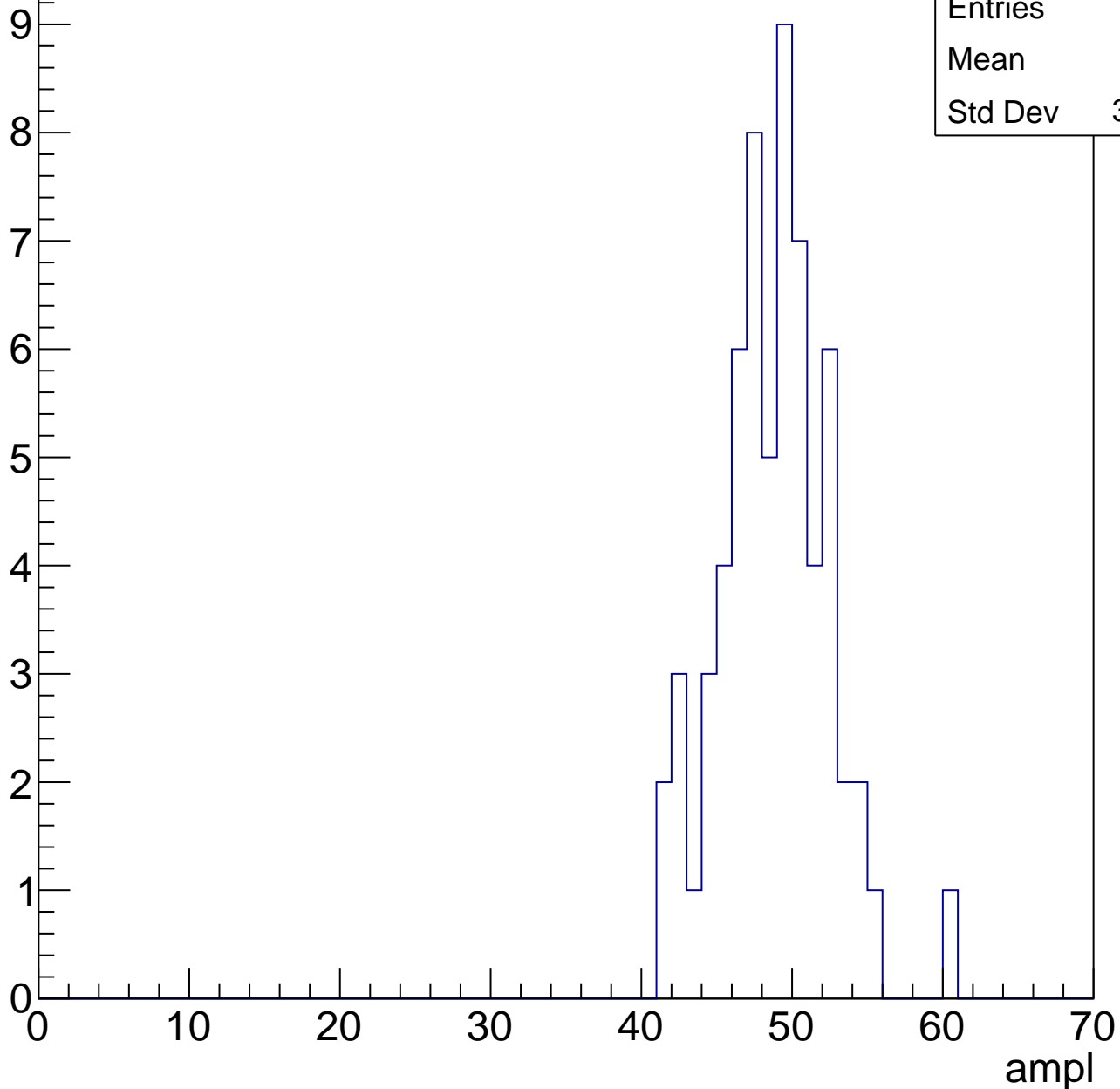
Gaus Width: 4.1874



B1L103S, U5-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

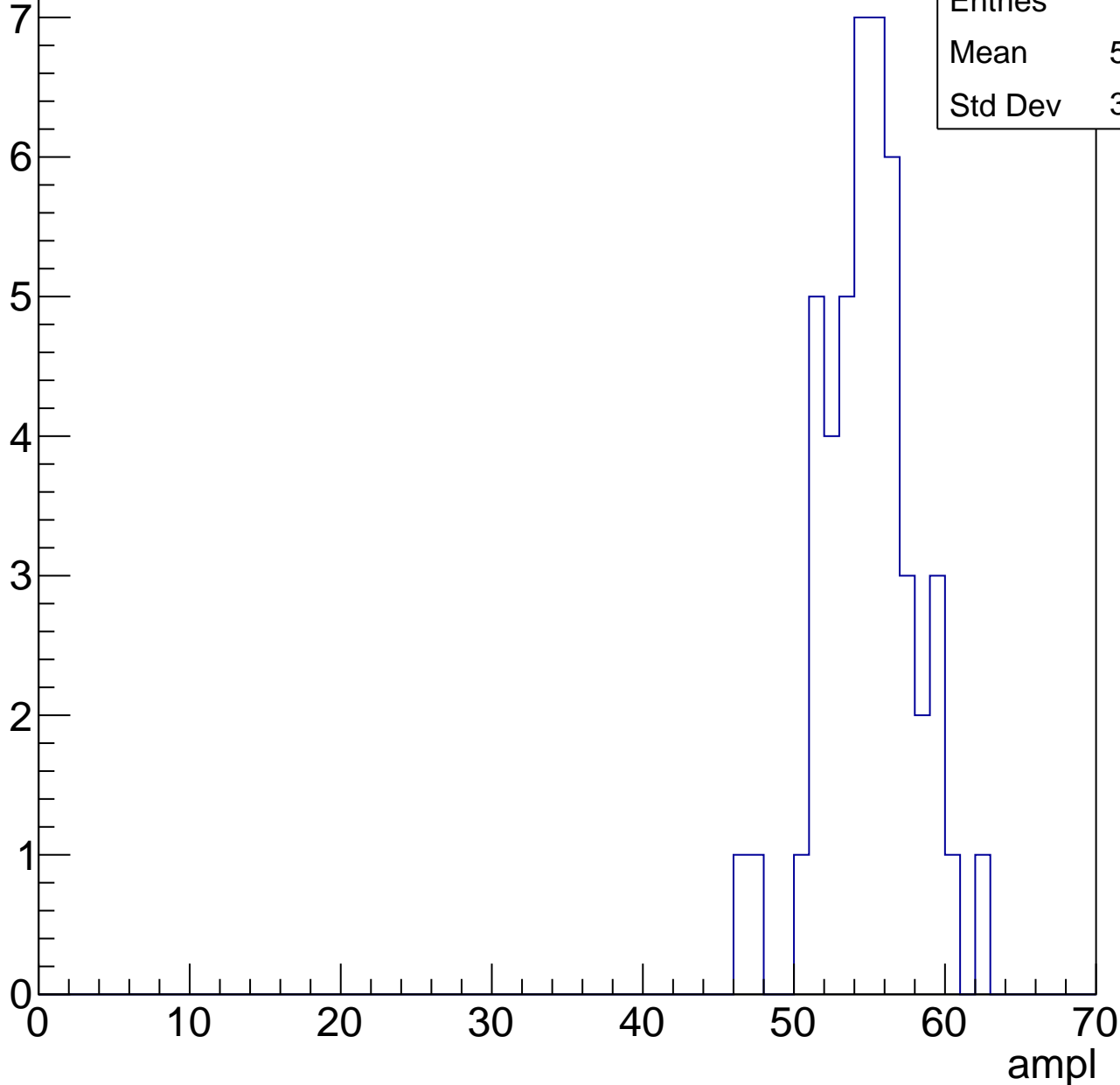


B1L103S, U5-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	54.38
Std Dev	3.105

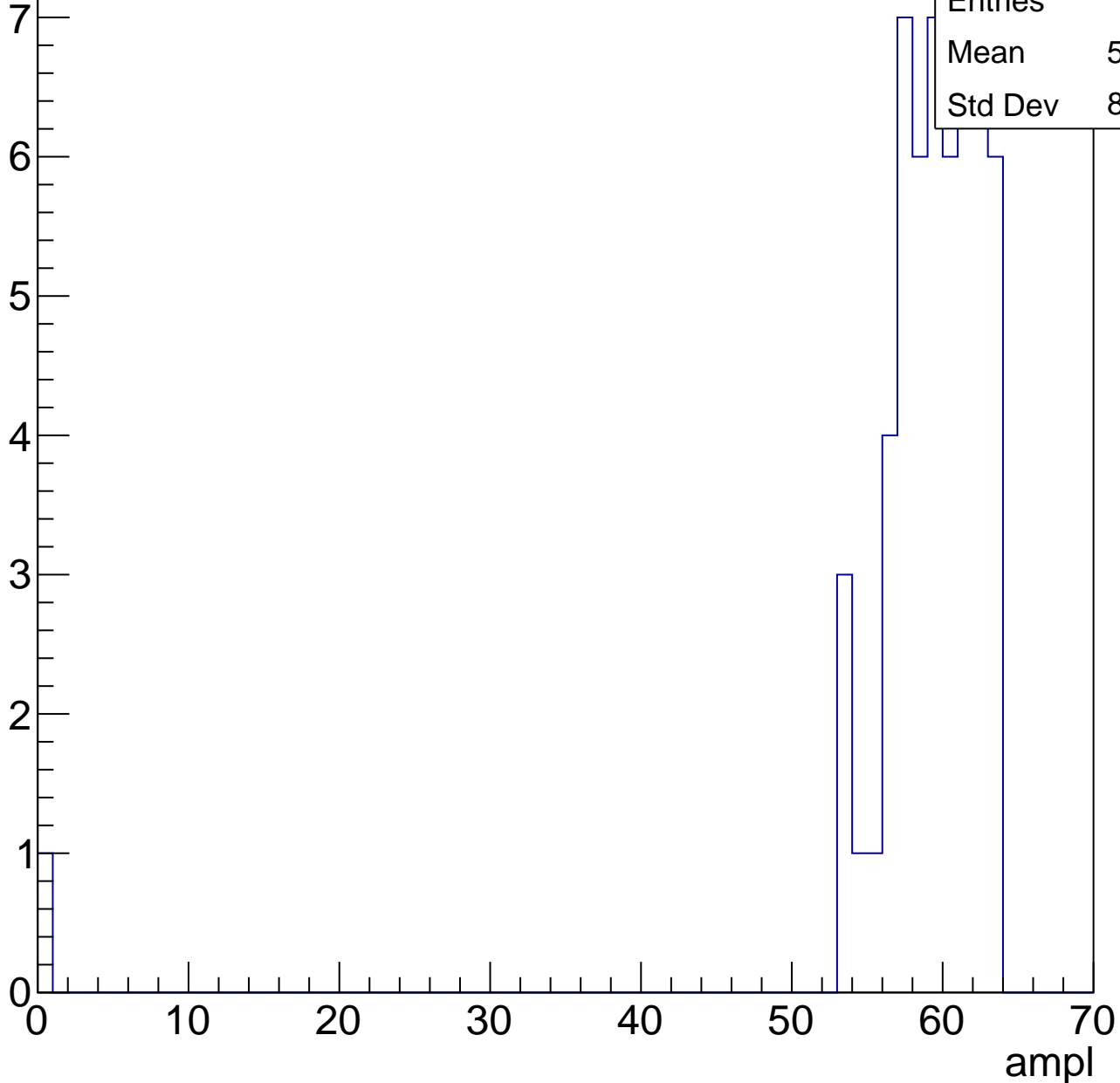


B1L103S, U5-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

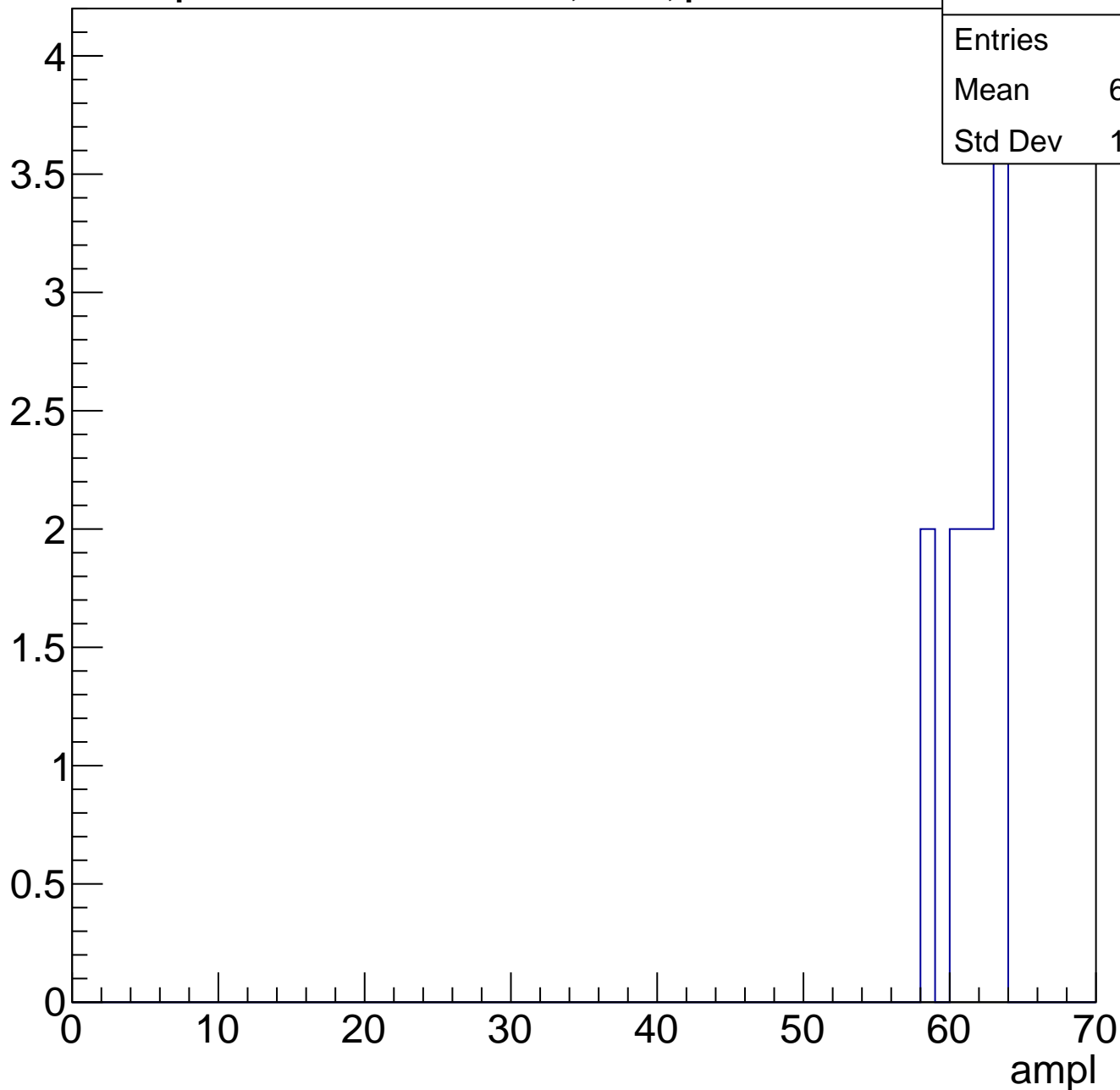
Entries	56
Mean	58.05
Std Dev	8.284



B1L103S, U5-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

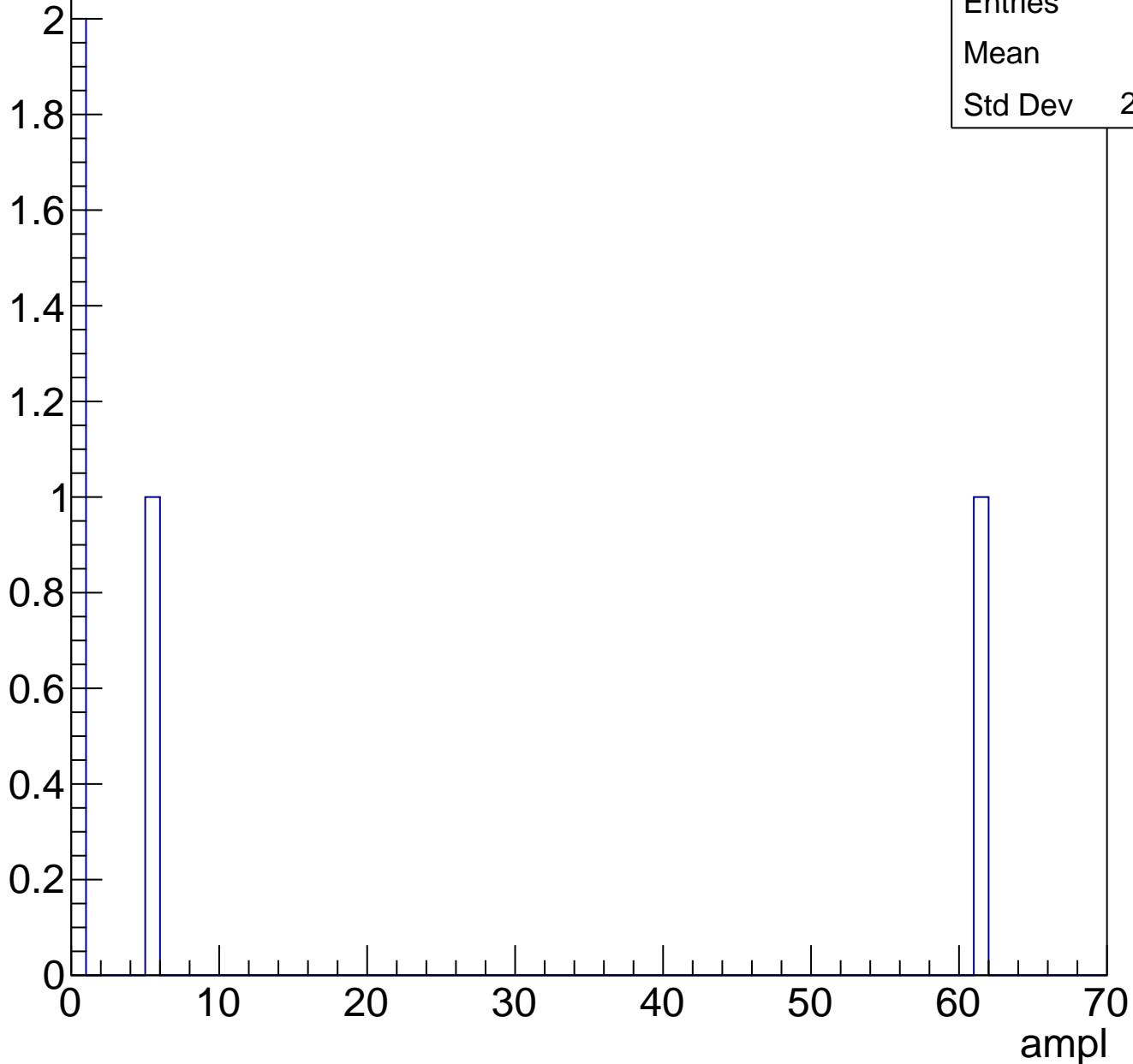
Entry



B1L103S, U5-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	16.5
Std Dev	25.77

B1L103S, U5-ch1, adc0

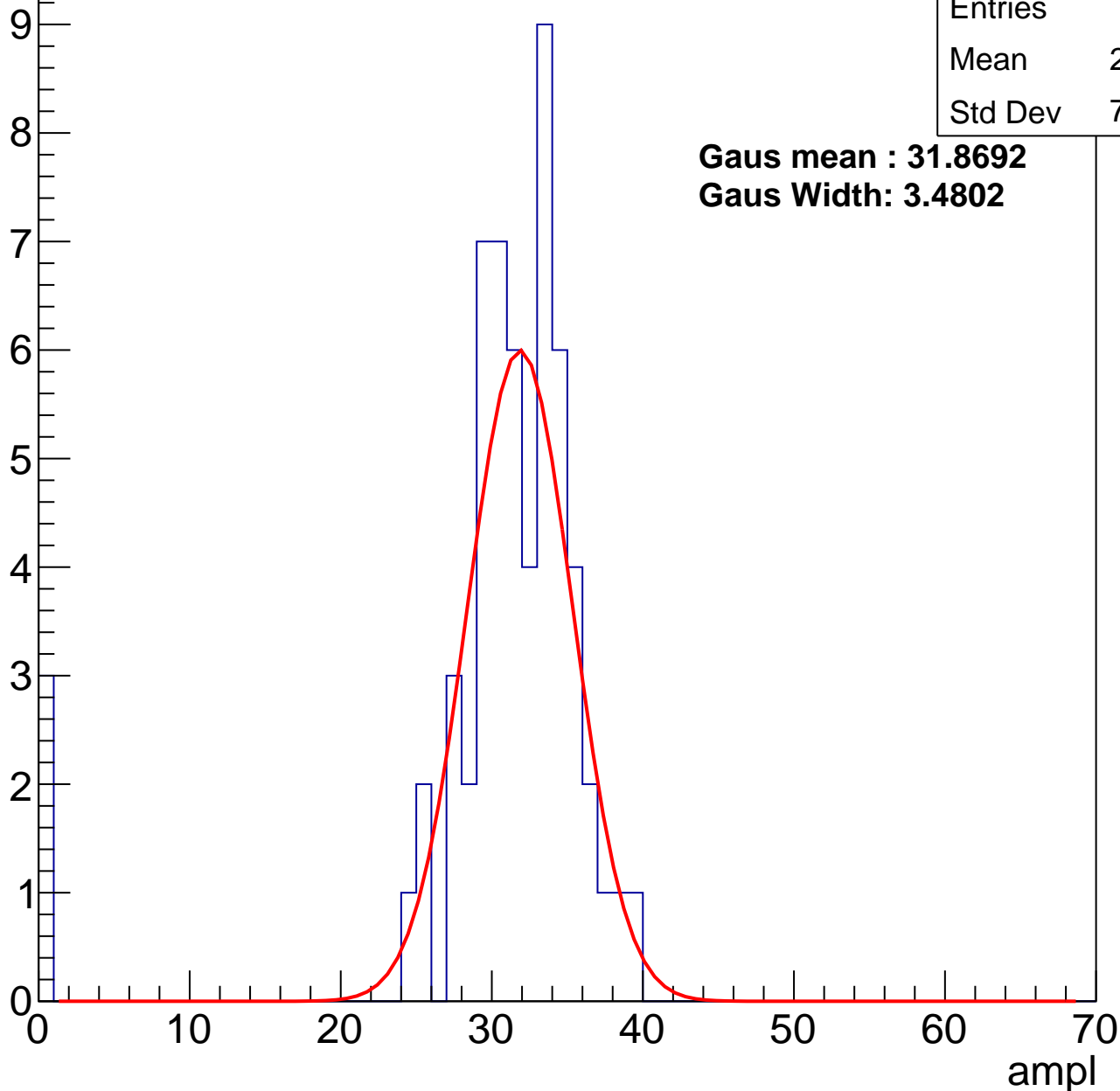
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	29.92
Std Dev	7.583

Gaus mean : 31.8692

Gaus Width: 3.4802



B1L103S, U5-ch1, adc1

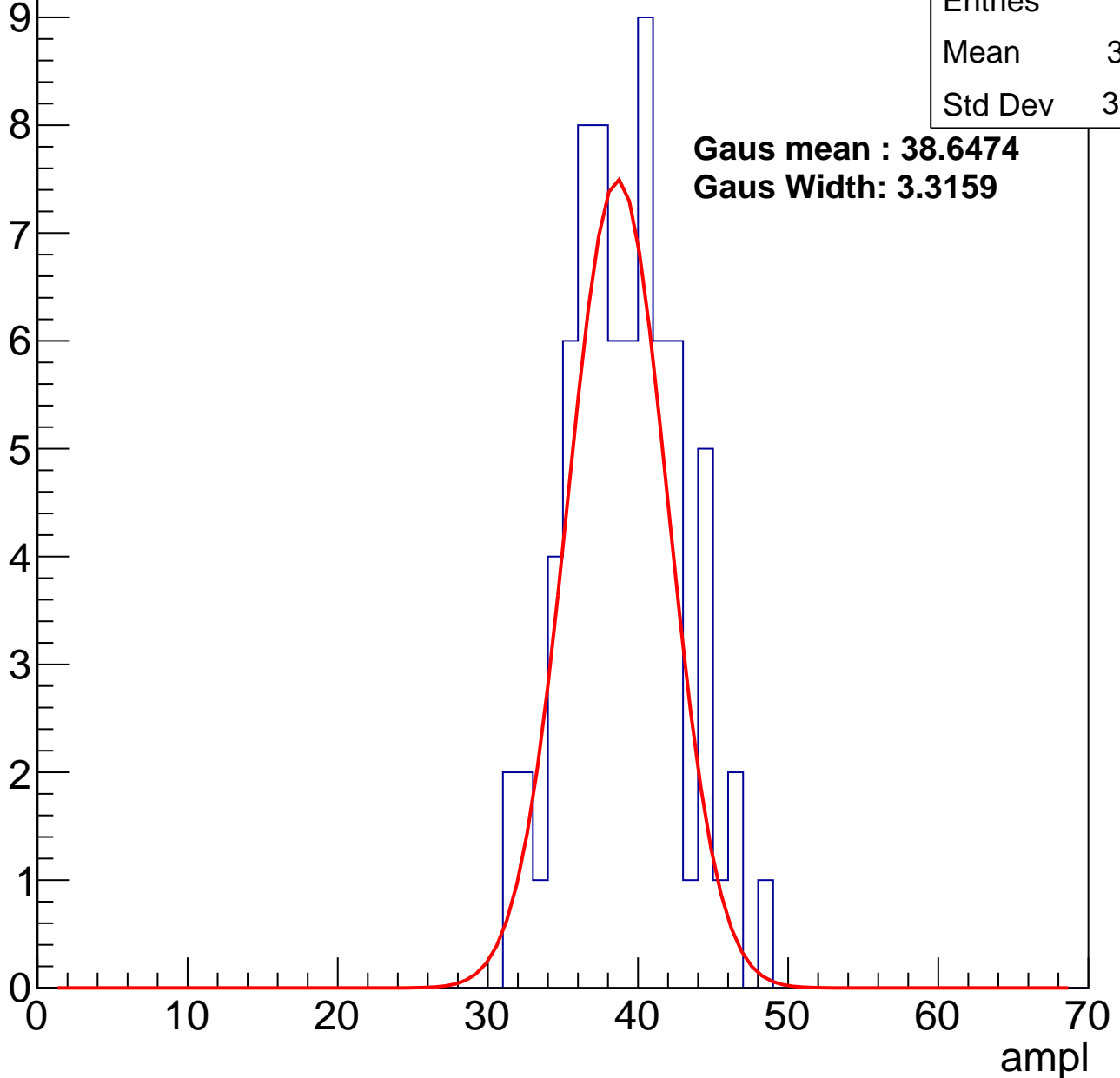
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	38.61
Std Dev	3.675

Gaus mean : 38.6474

Gaus Width: 3.3159



B1L103S, U5-ch1, adc2

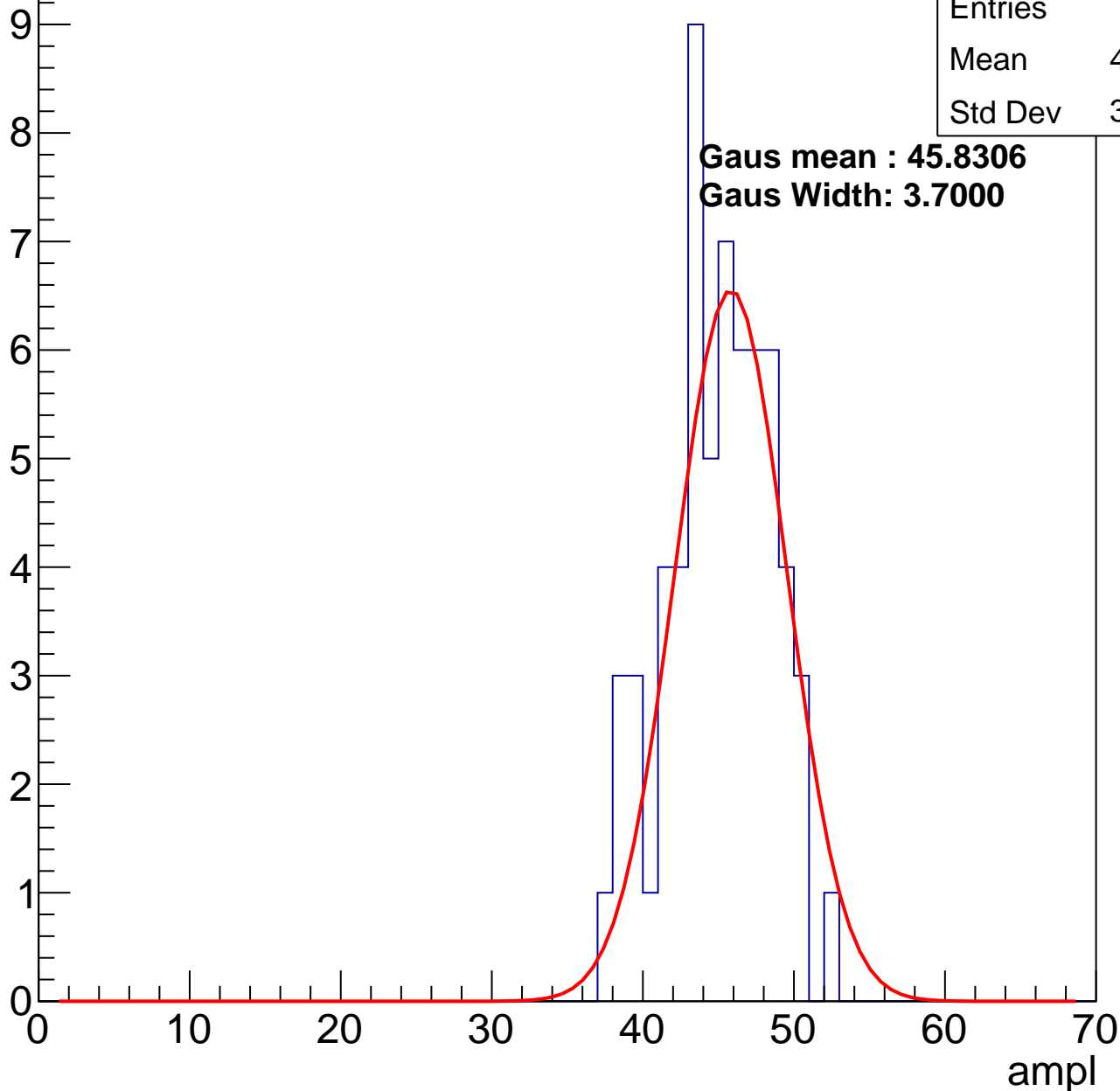
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.54
Std Dev	3.436

Gaus mean : 45.8306

Gaus Width: 3.7000

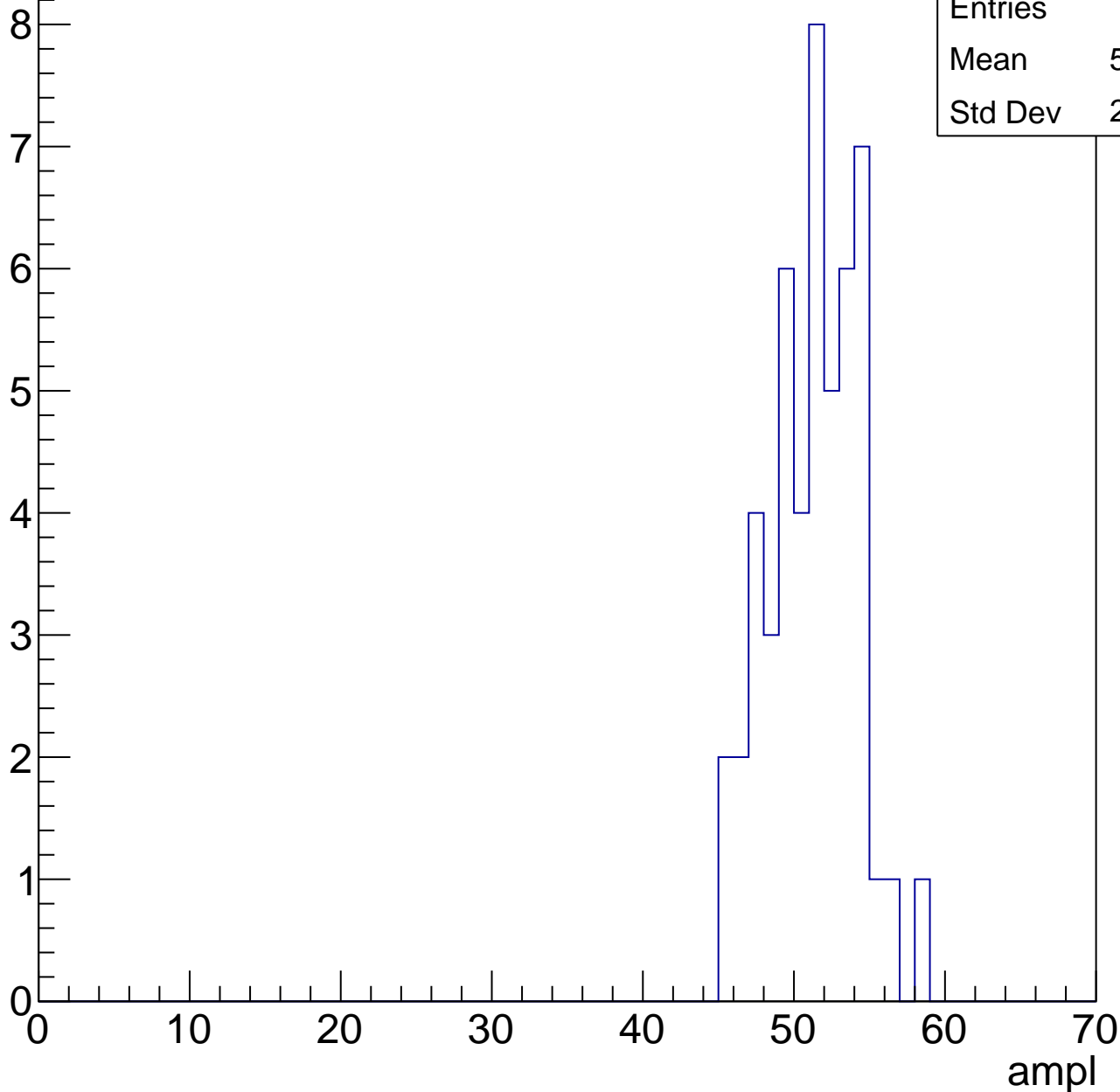


B1L103S, U5-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

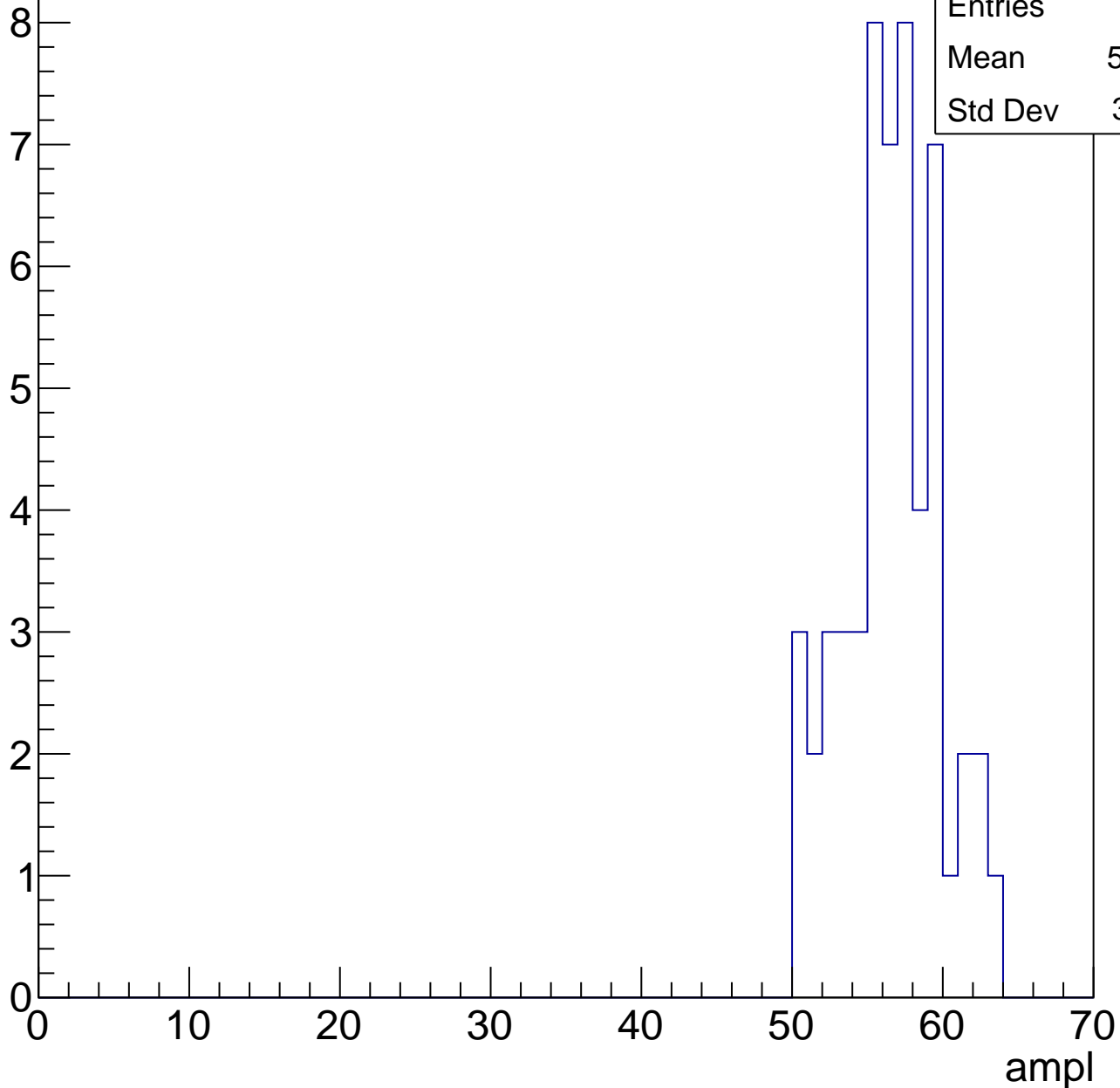
Entries	50
Mean	50.82
Std Dev	2.903



B1L103S, U5-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

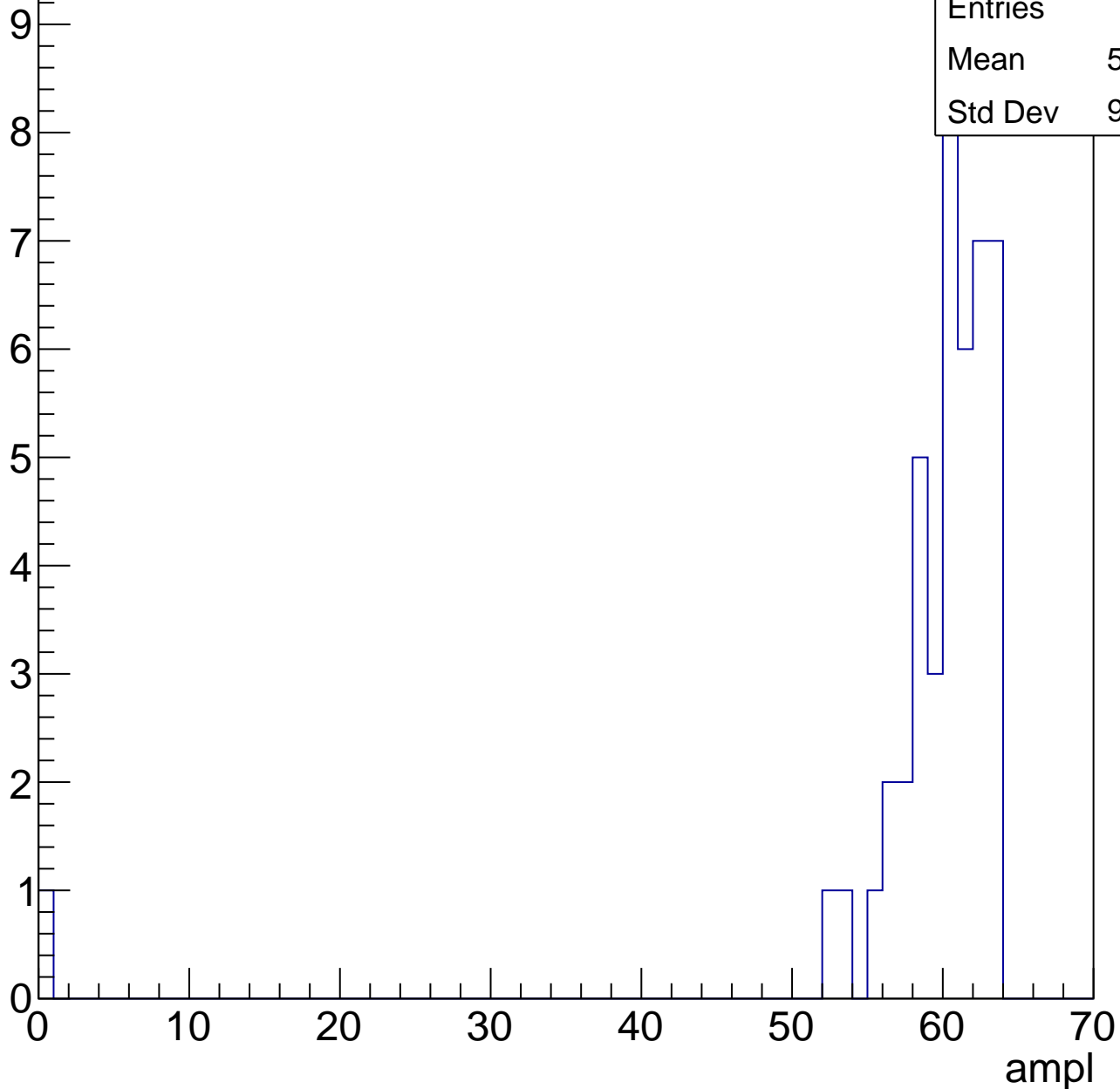


Entries	54
Mean	56.13
Std Dev	3.121

B1L103S, U5-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

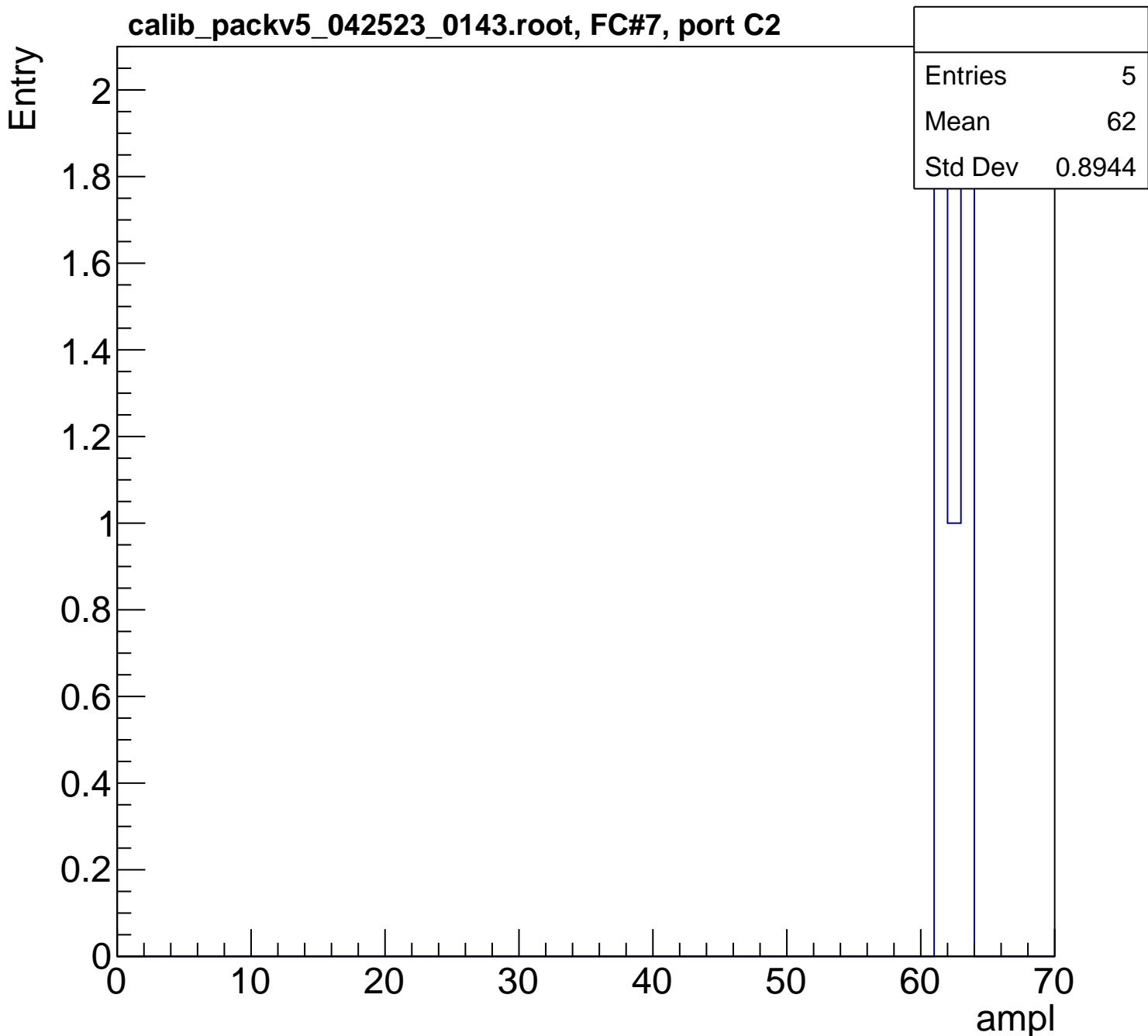
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch2, adc0

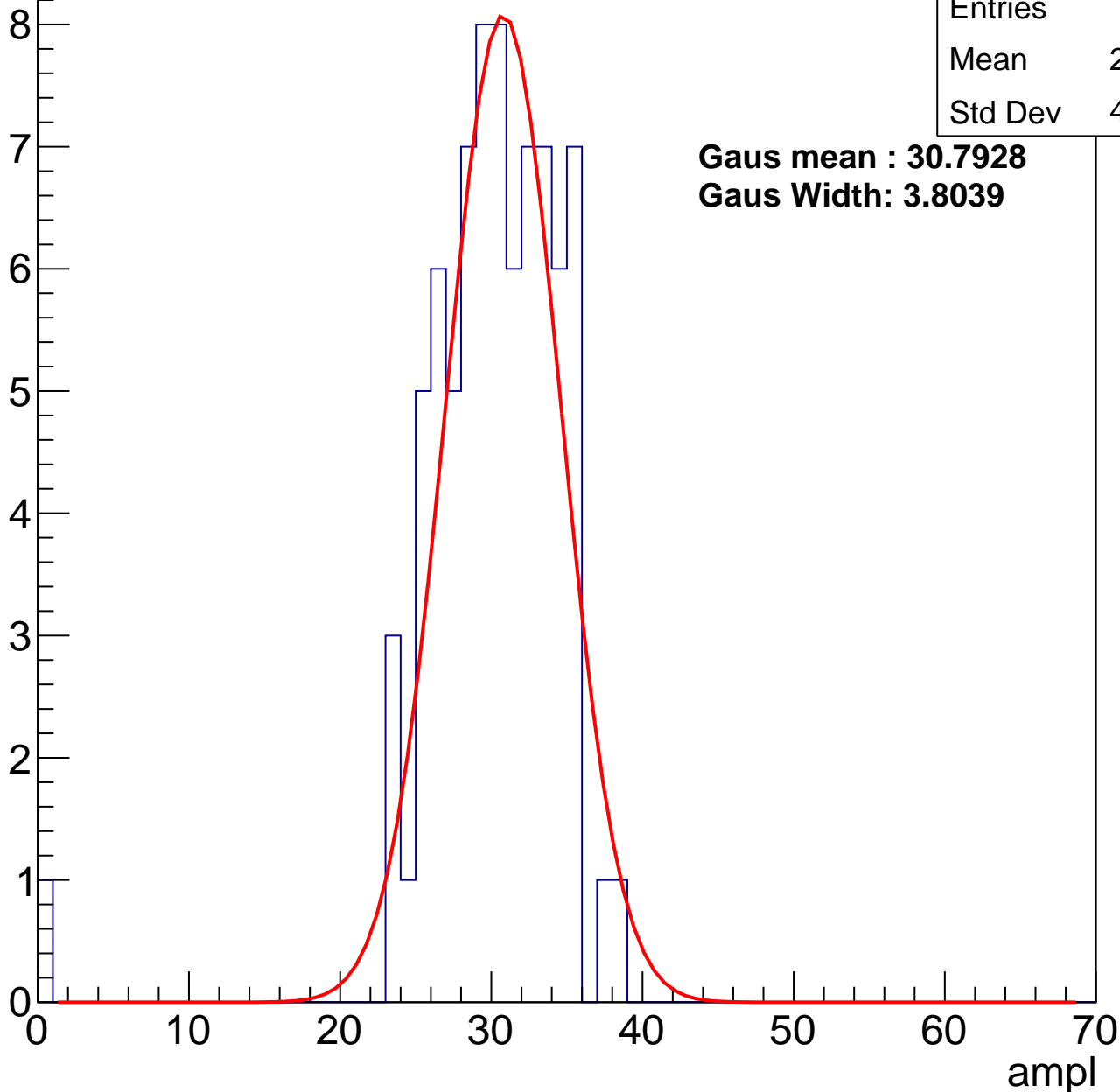
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	29.65
Std Dev	4.847

Gaus mean : 30.7928

Gaus Width: 3.8039



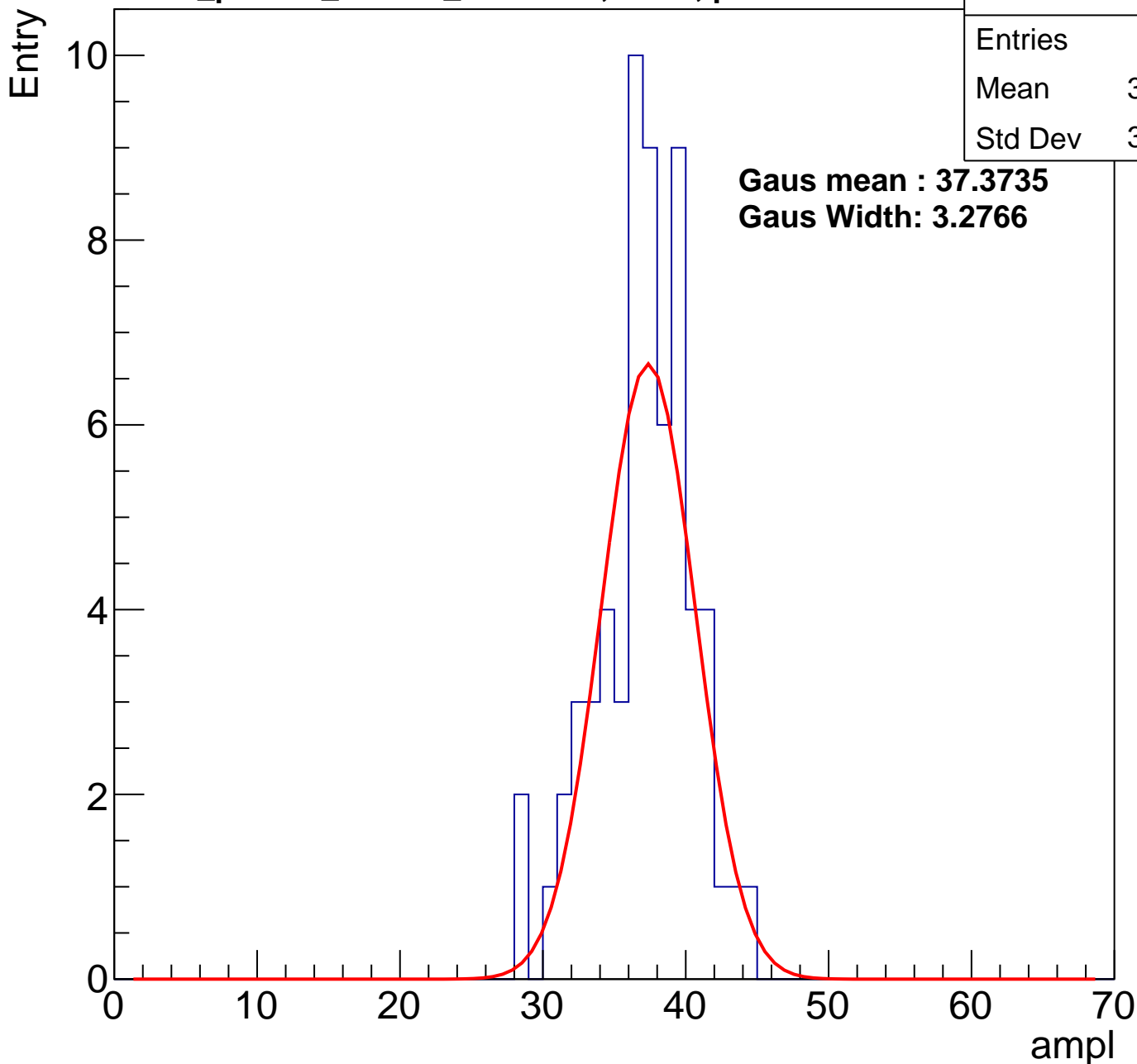
B1L103S, U5-ch2, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	63
Mean	36.65
Std Dev	3.353

Gaus mean : 37.3735

Gaus Width: 3.2766



B1L103S, U5-ch2, adc2

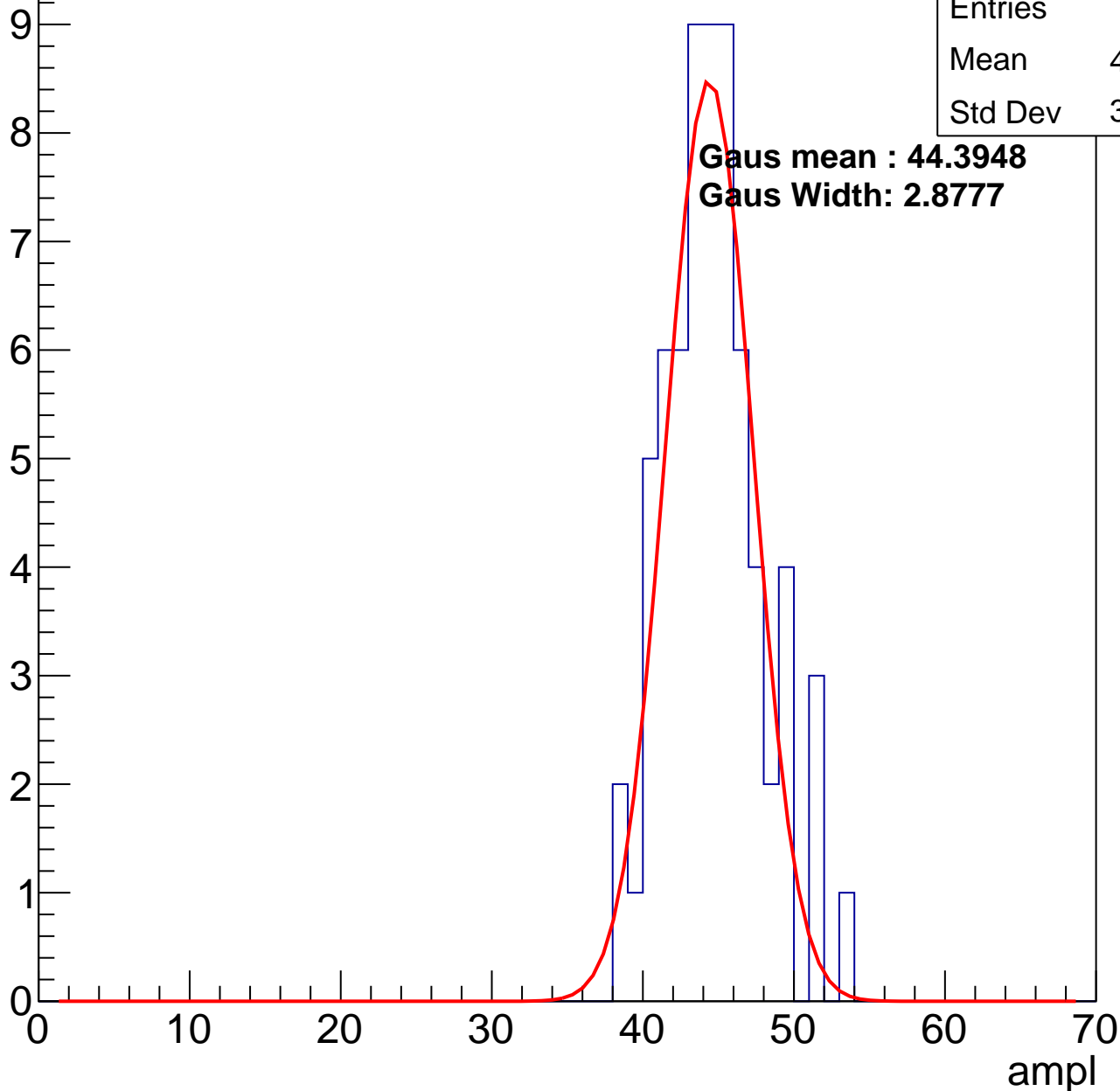
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	44.22
Std Dev	3.208

Gaus mean : 44.3948

Gaus Width: 2.8777

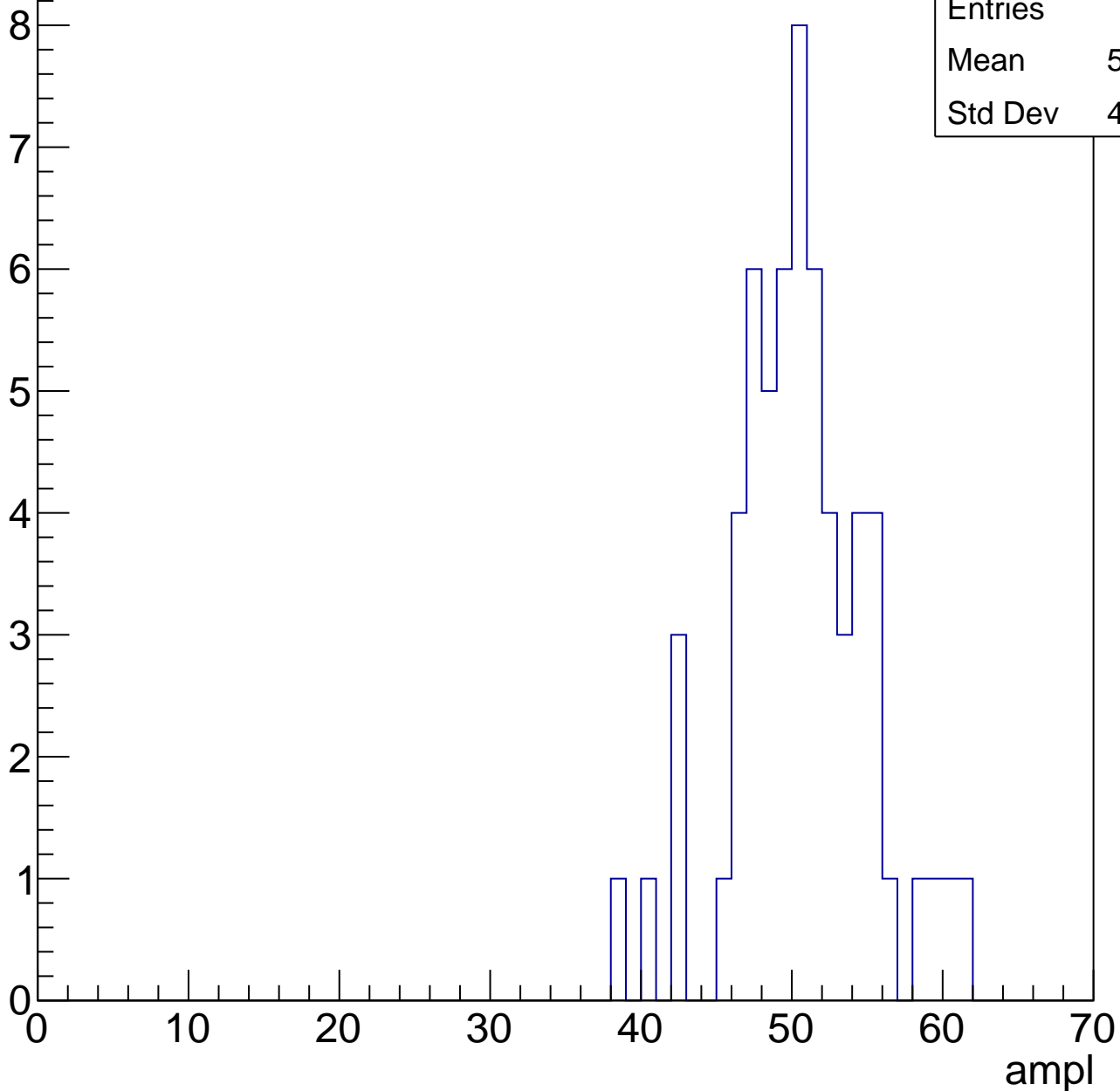


B1L103S, U5-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.03
Std Dev	4.472

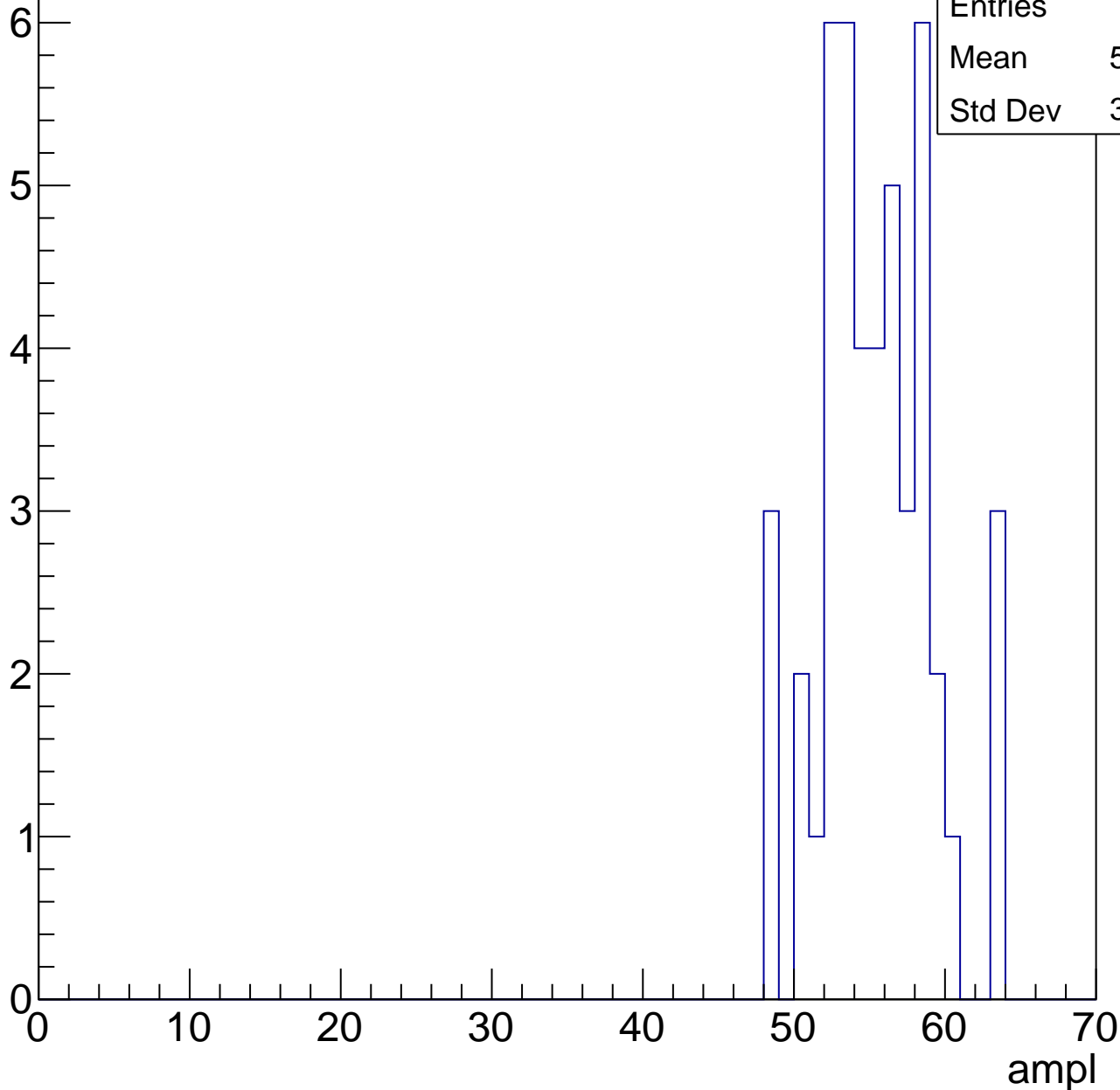


B1L103S, U5-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	54.93
Std Dev	3.656

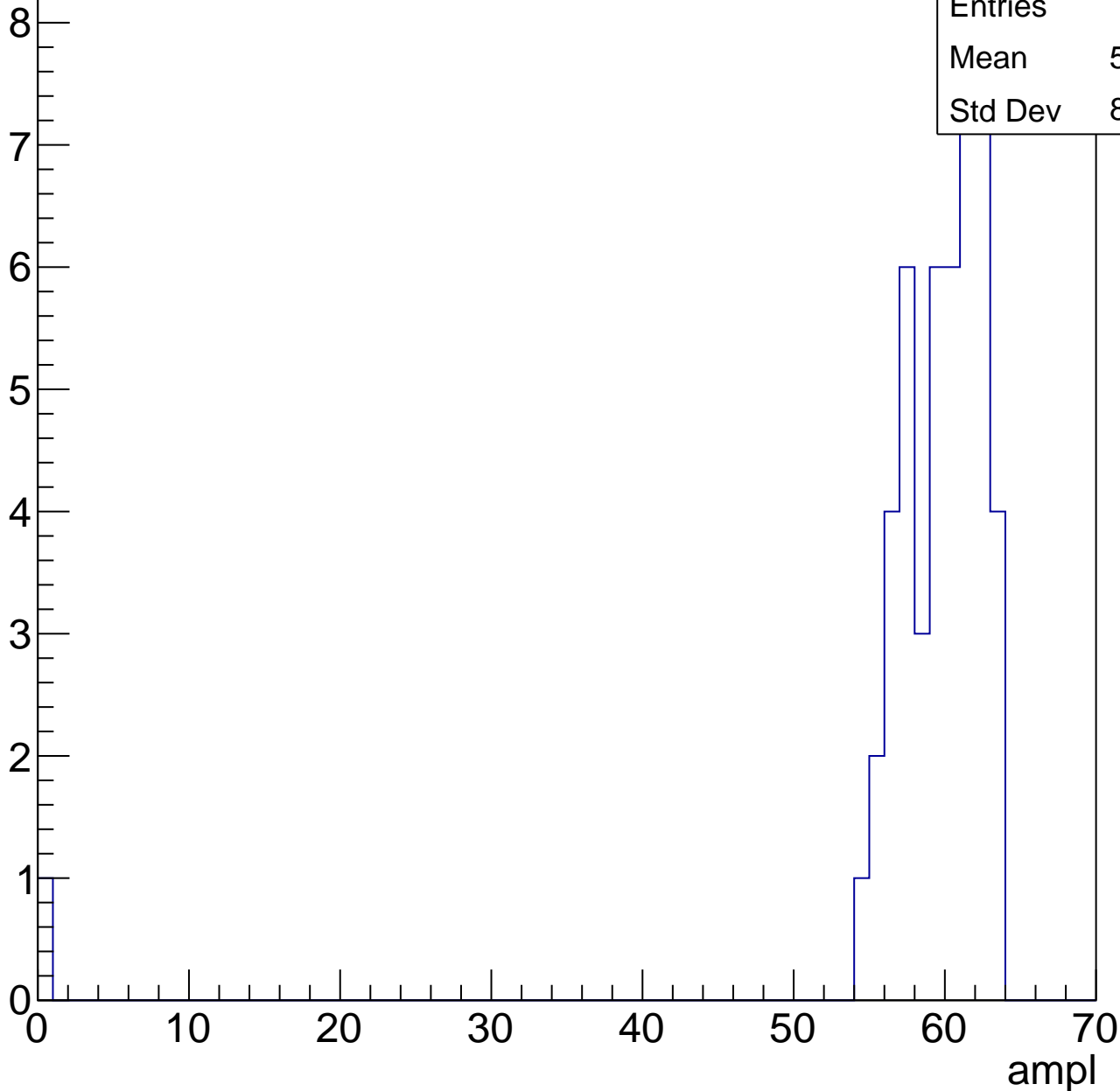


B1L103S, U5-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.24
Std Dev	8.745



B1L103S, U5-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	60.1
Std Dev	2.7

ampl

0 10 20 30 40 50 60 70

B1L103S, U5-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch3, adc0

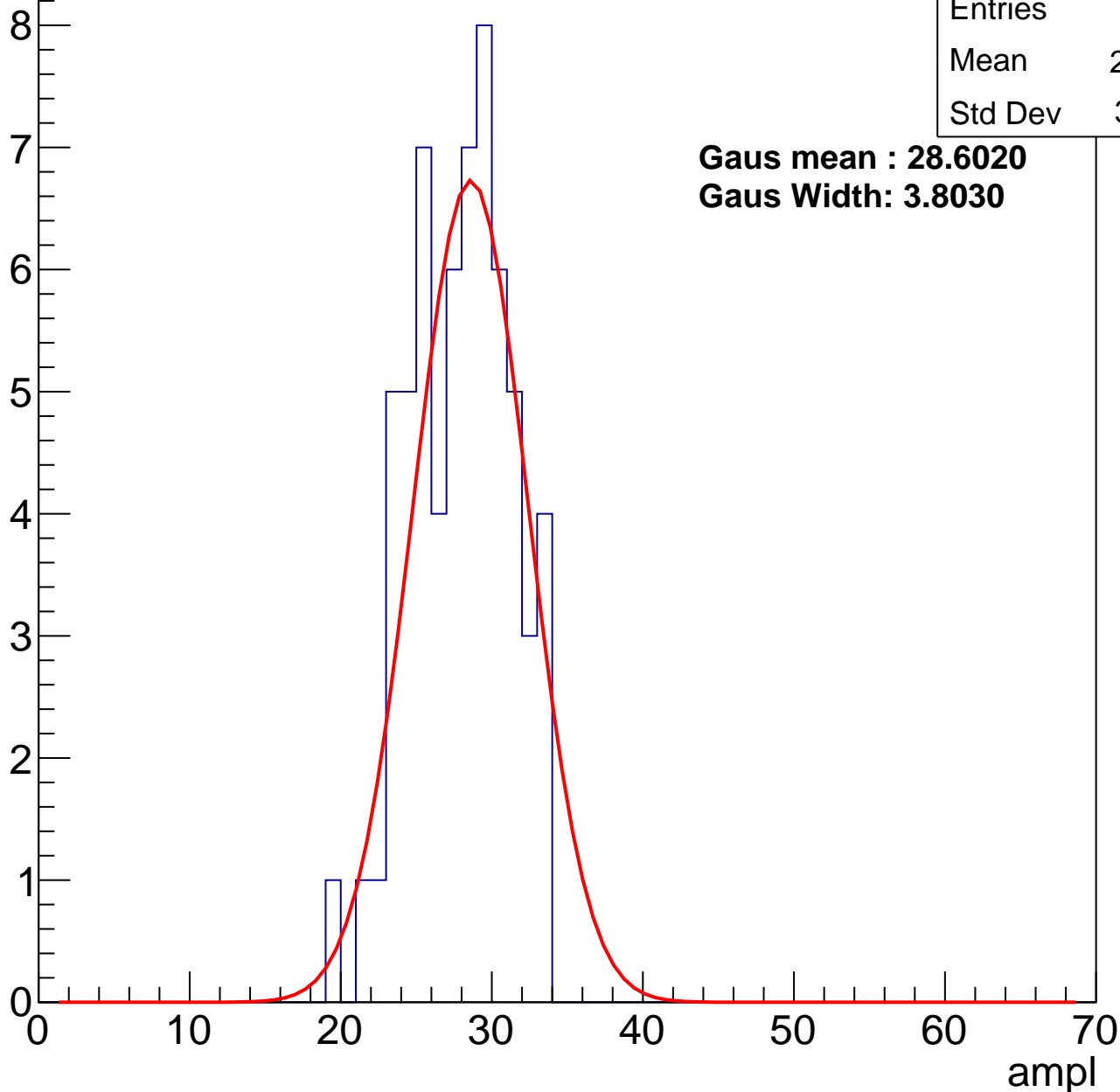
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	27.44
Std Dev	3.241

Gaus mean : 28.6020

Gaus Width: 3.8030



B1L103S, U5-ch3, adc1

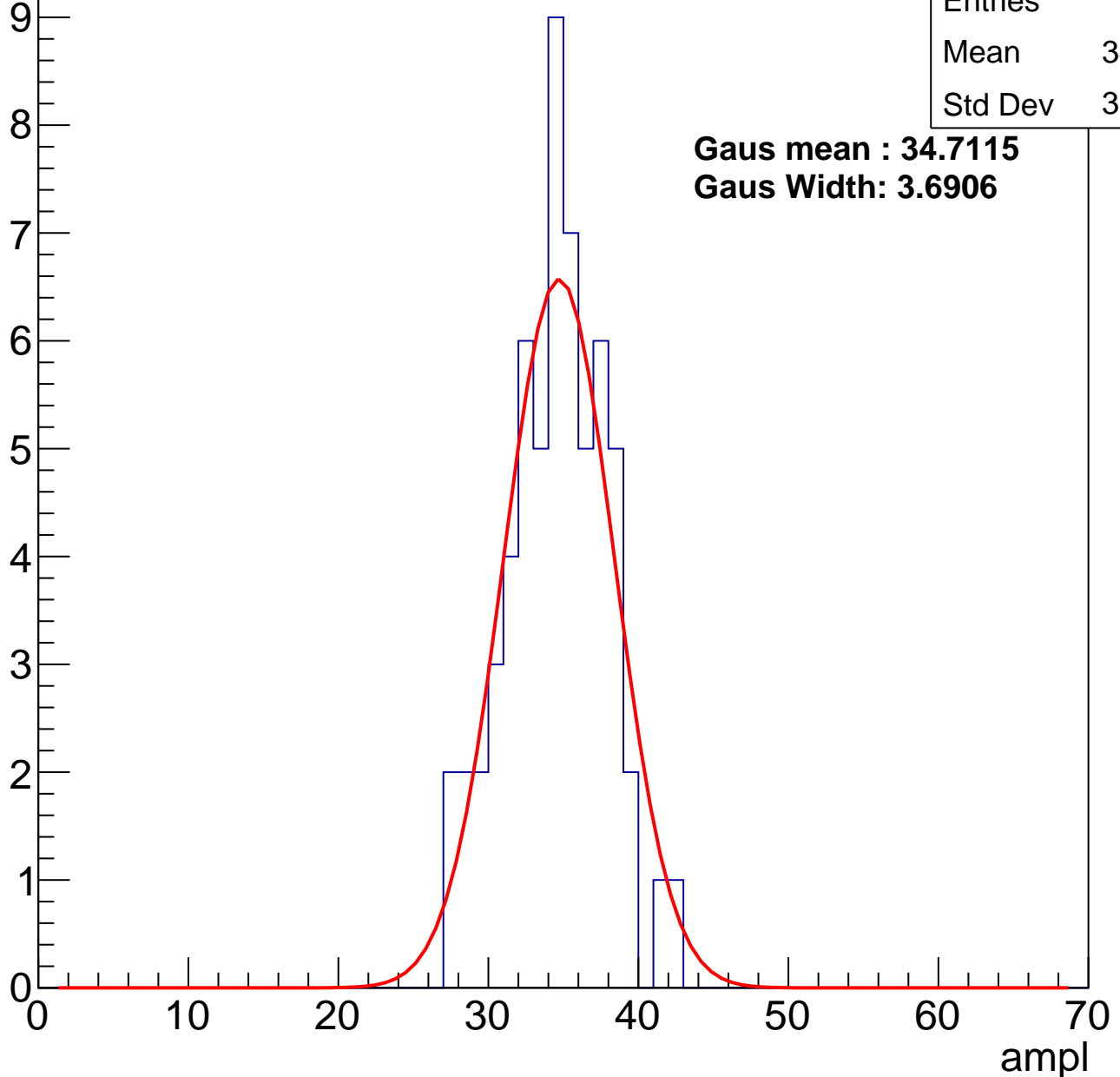
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	34.05
Std Dev	3.309

Gaus mean : 34.7115

Gaus Width: 3.6906



B1L103S, U5-ch3, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	41.18
Std Dev	4.153

Gaus mean : 41.7583

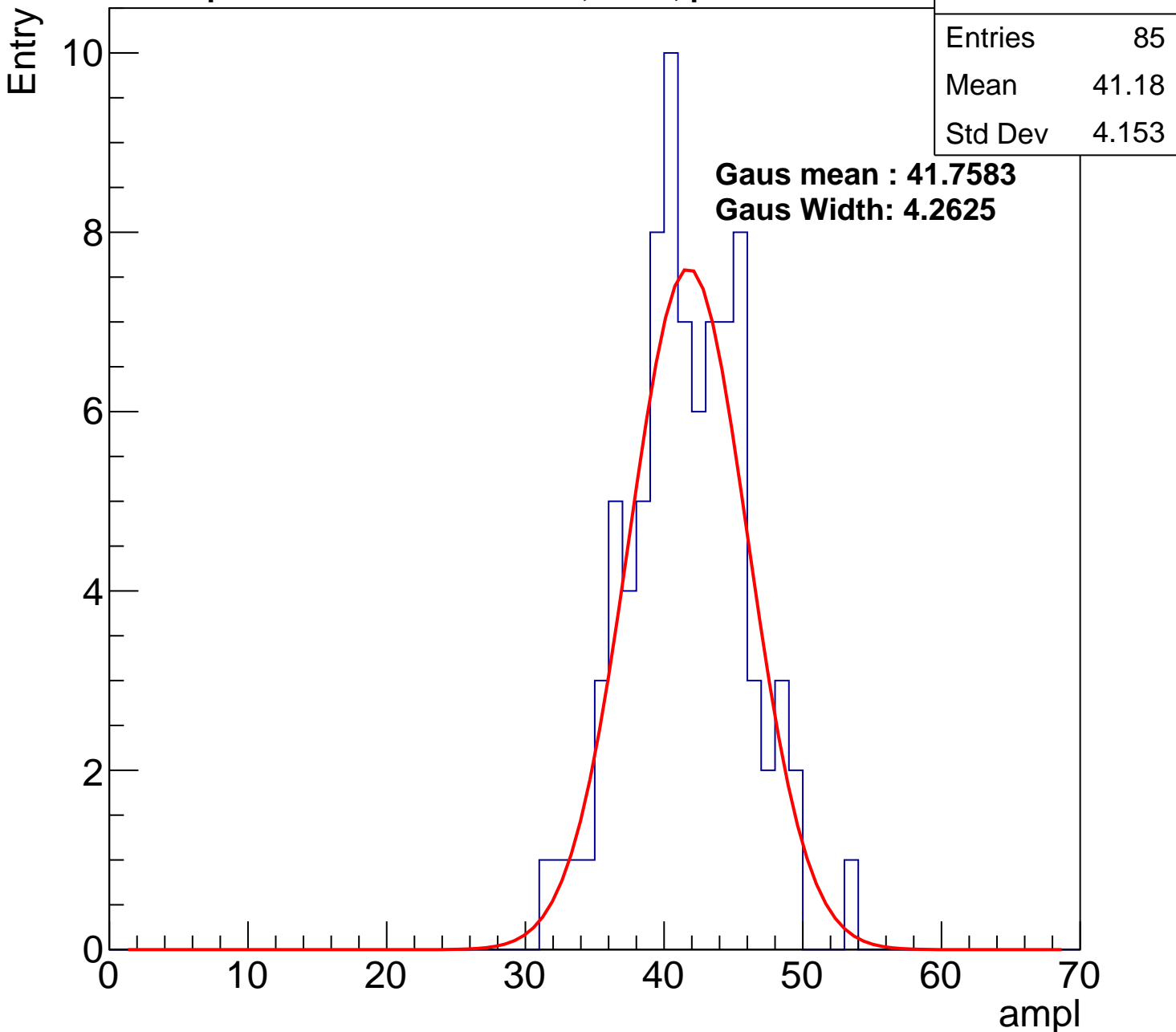
Gaus Width: 4.2625

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

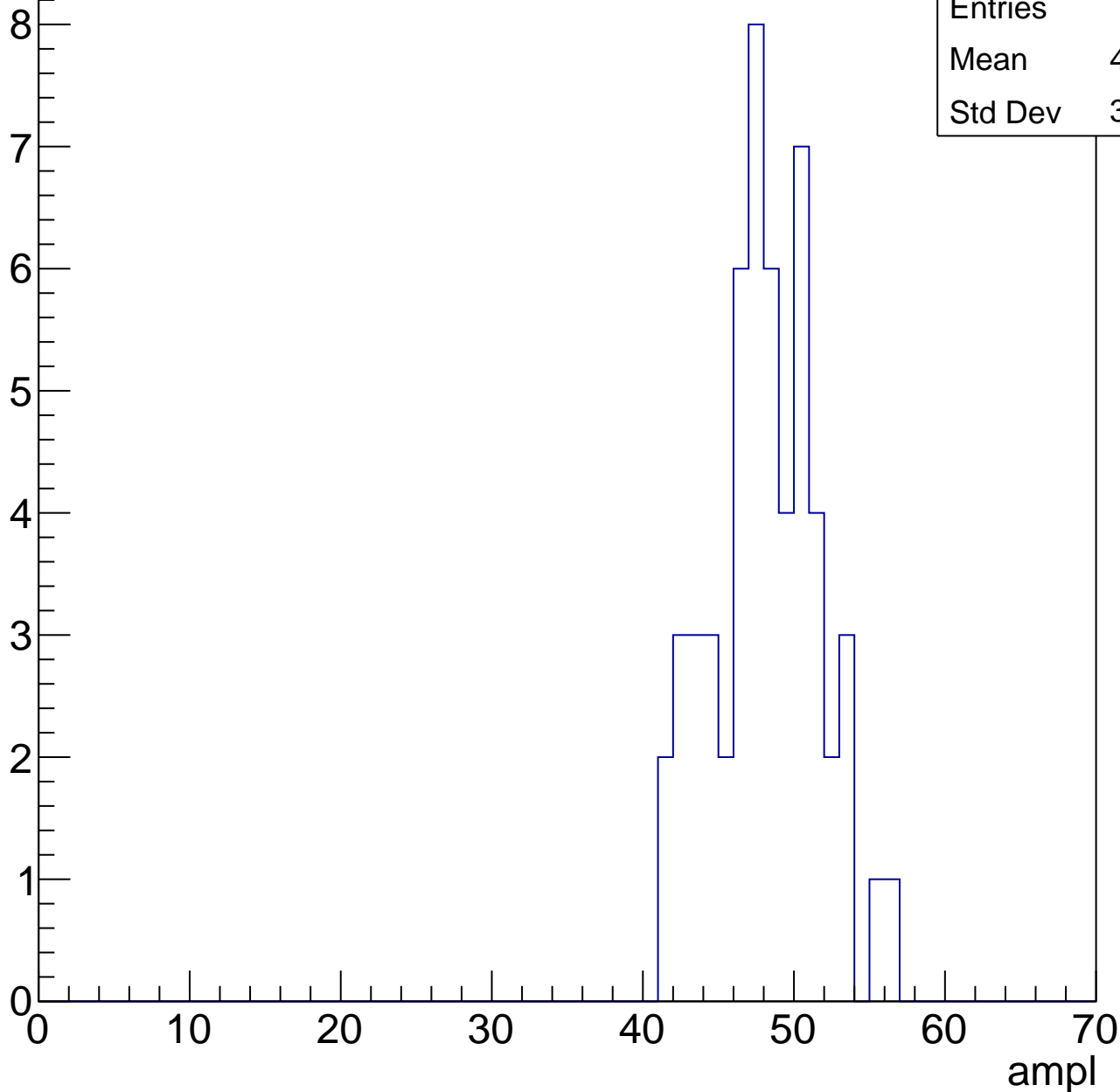


B1L103S, U5-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	47.69
Std Dev	3.469

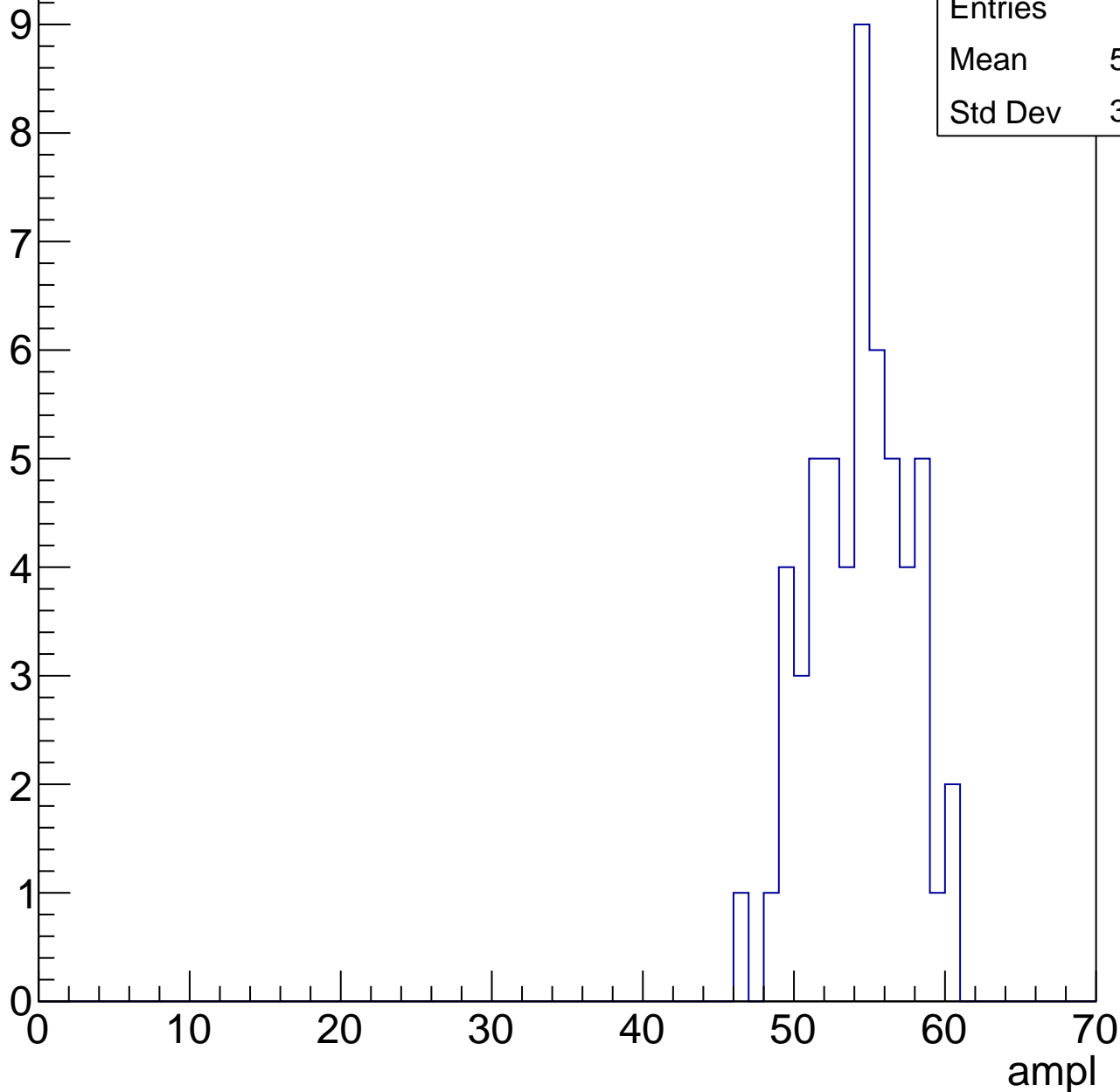


B1L103S, U5-ch3, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

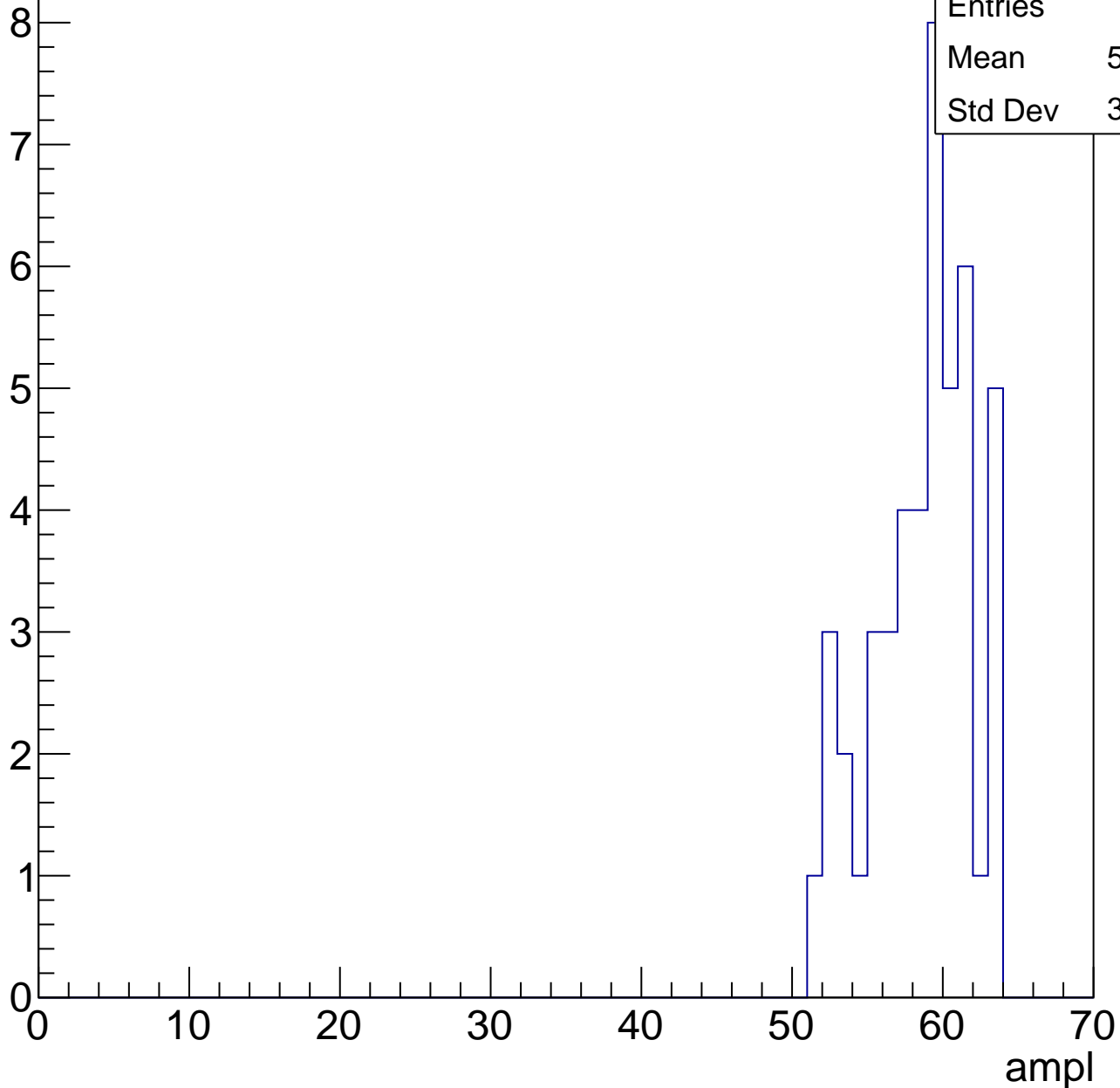
Entries	55
Mean	53.82
Std Dev	3.174



B1L103S, U5-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

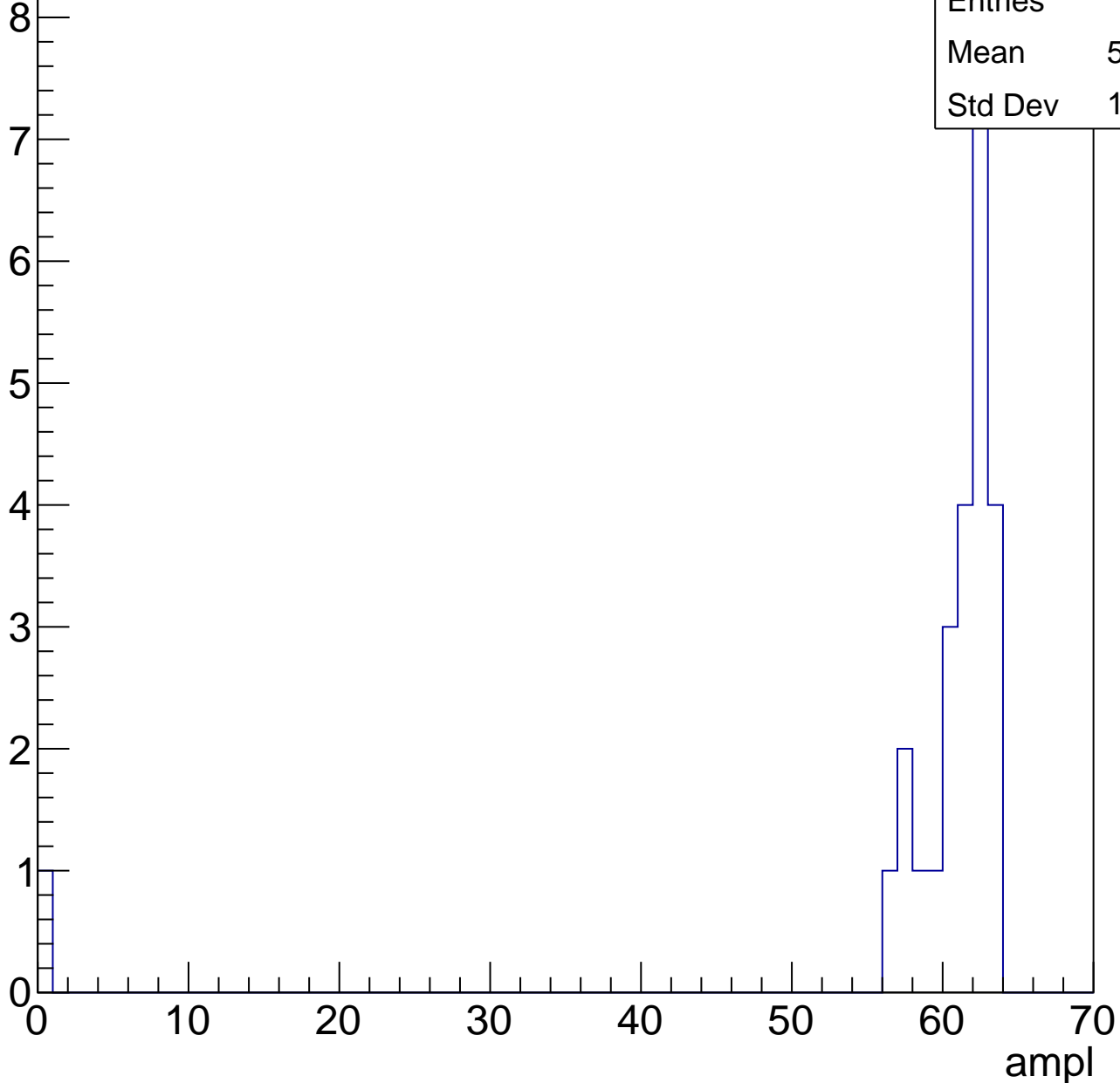


B1L103S, U5-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

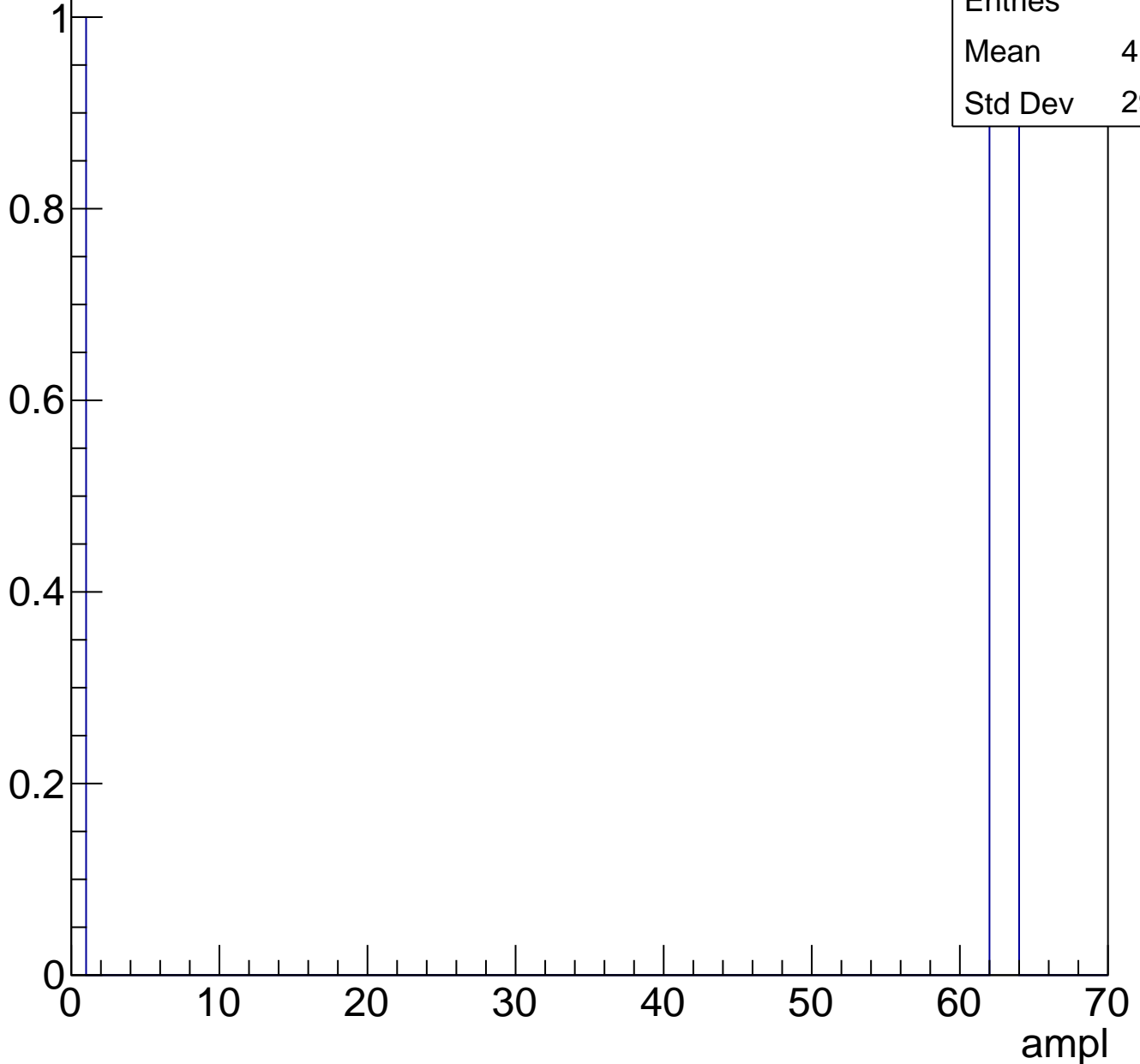
Entries	25
Mean	58.36
Std Dev	12.07



B1L103S, U5-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch4, adc0

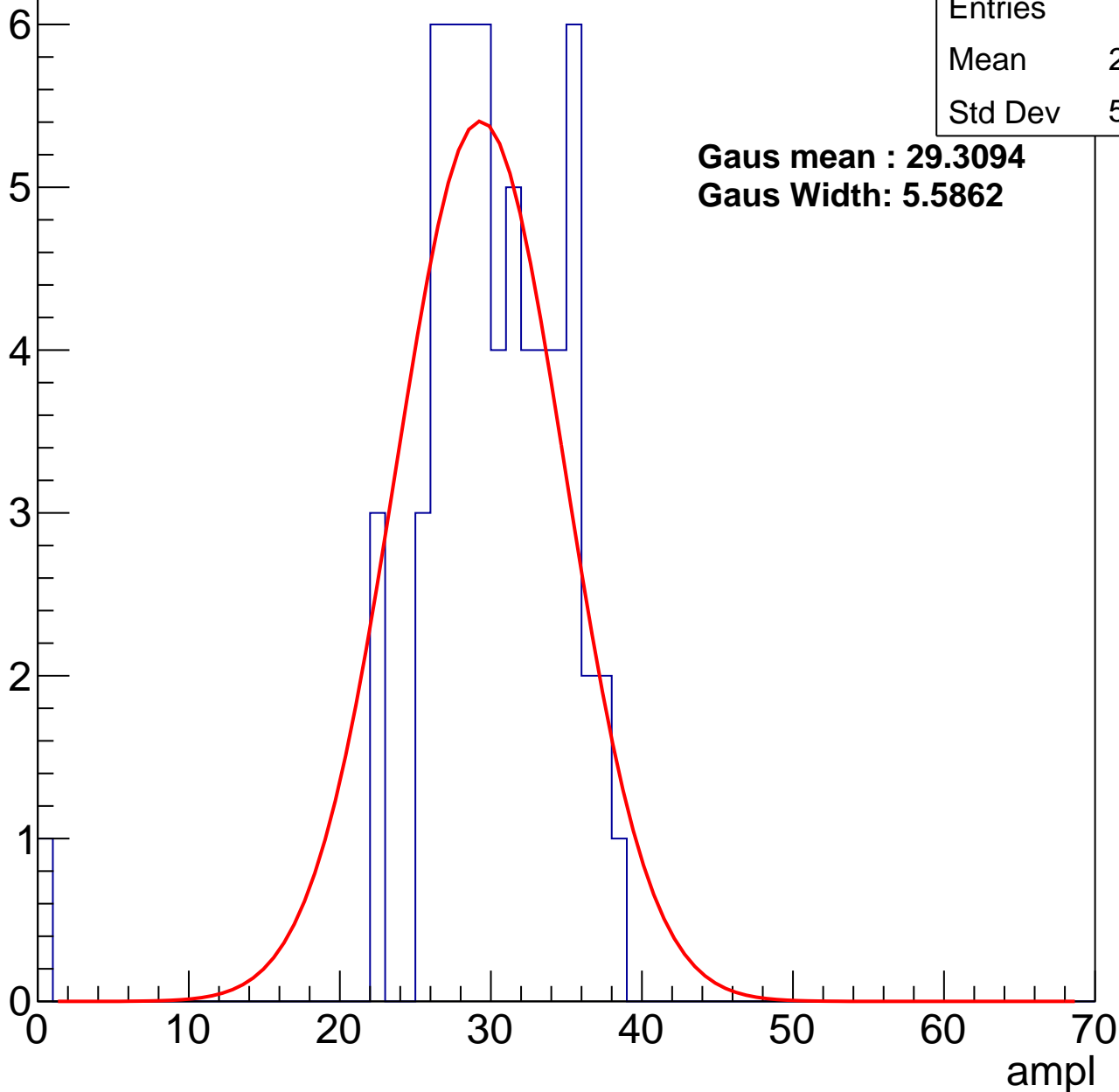
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.62
Std Dev	5.408

Gaus mean : 29.3094

Gaus Width: 5.5862



B1L103S, U5-ch4, adc1

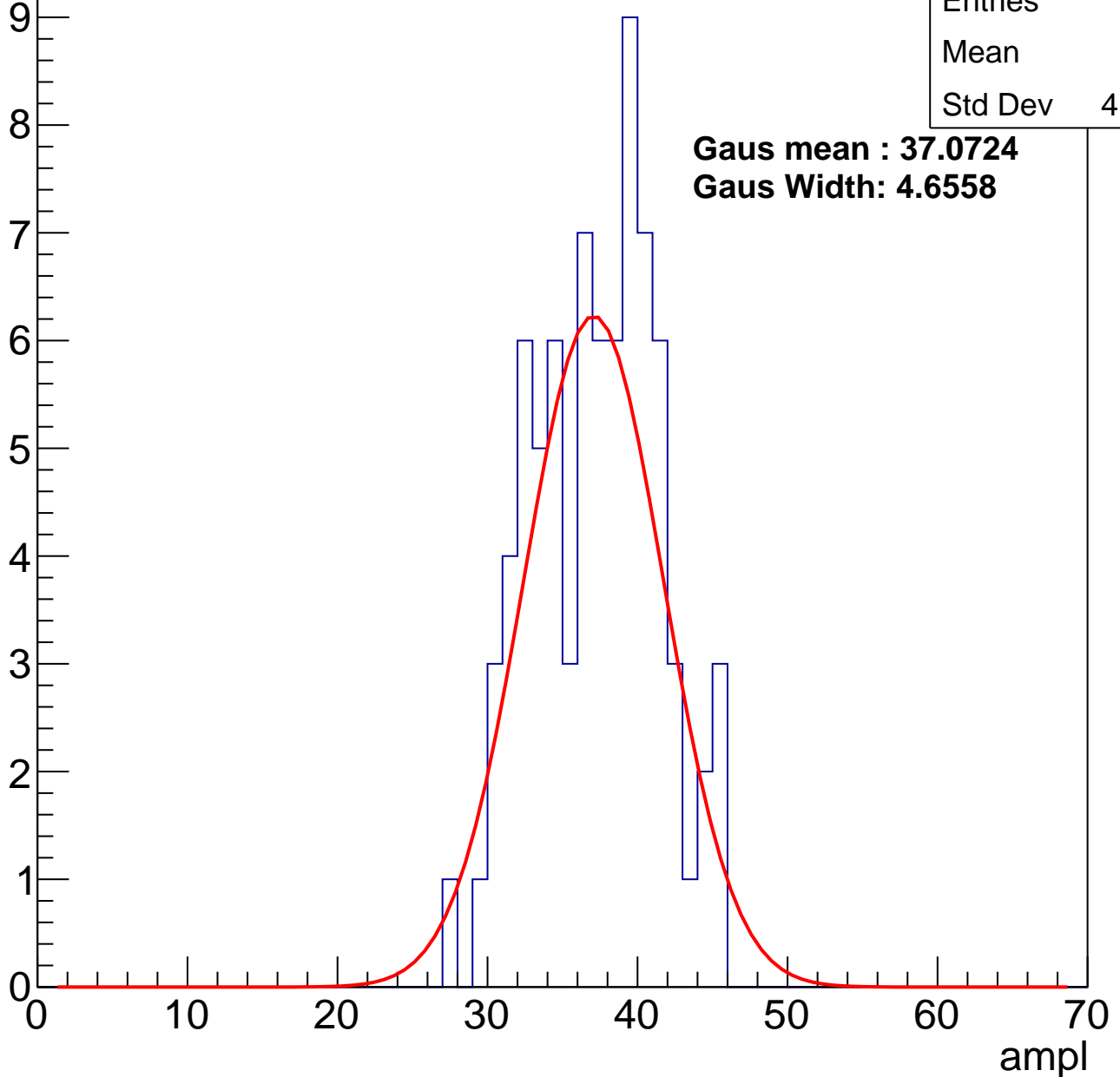
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	36.8
Std Dev	4.144

Gaus mean : 37.0724

Gaus Width: 4.6558



B1L103S, U5-ch4, adc2

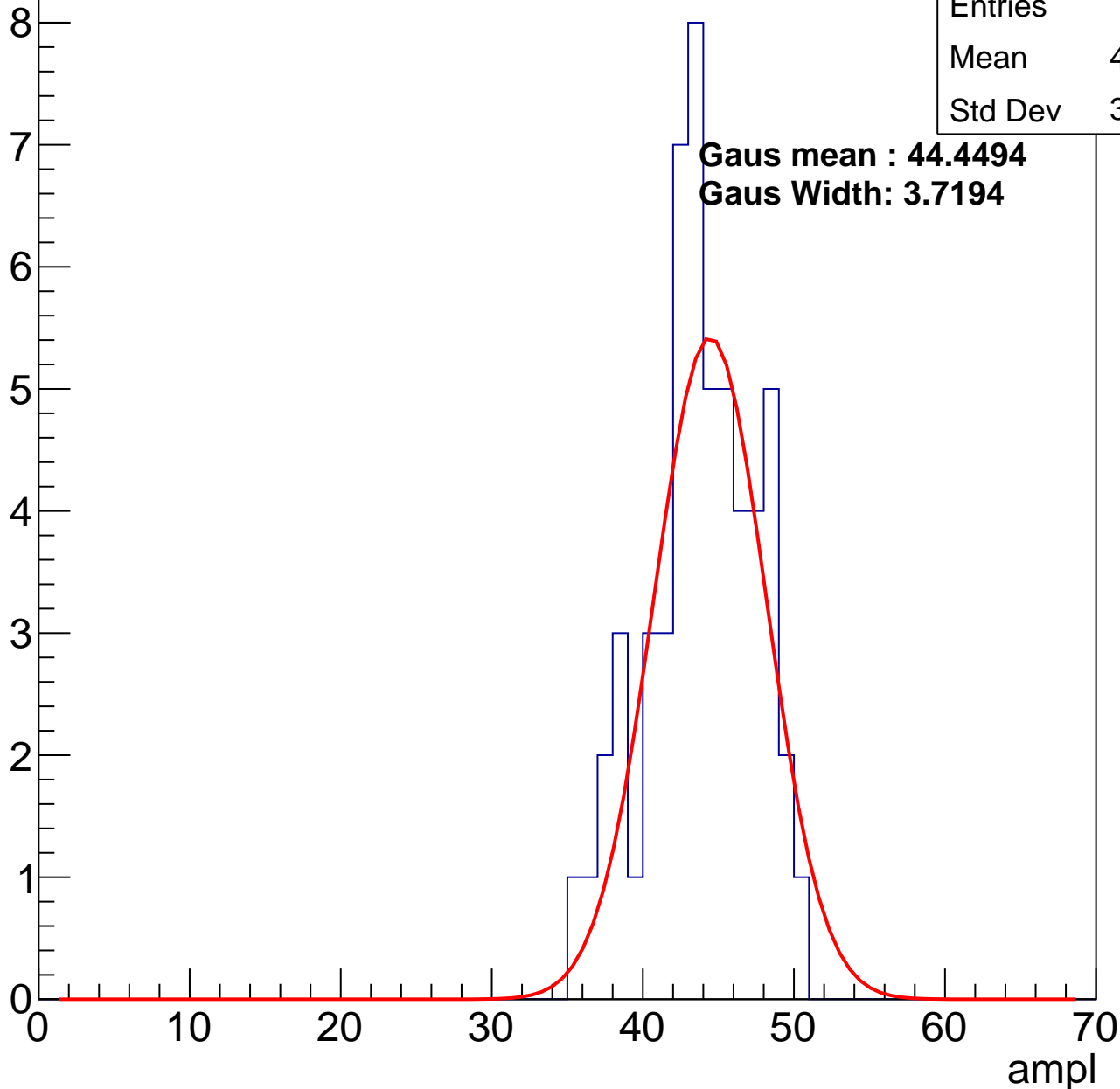
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.35
Std Dev	3.533

Gaus mean : 44.4494

Gaus Width: 3.7194

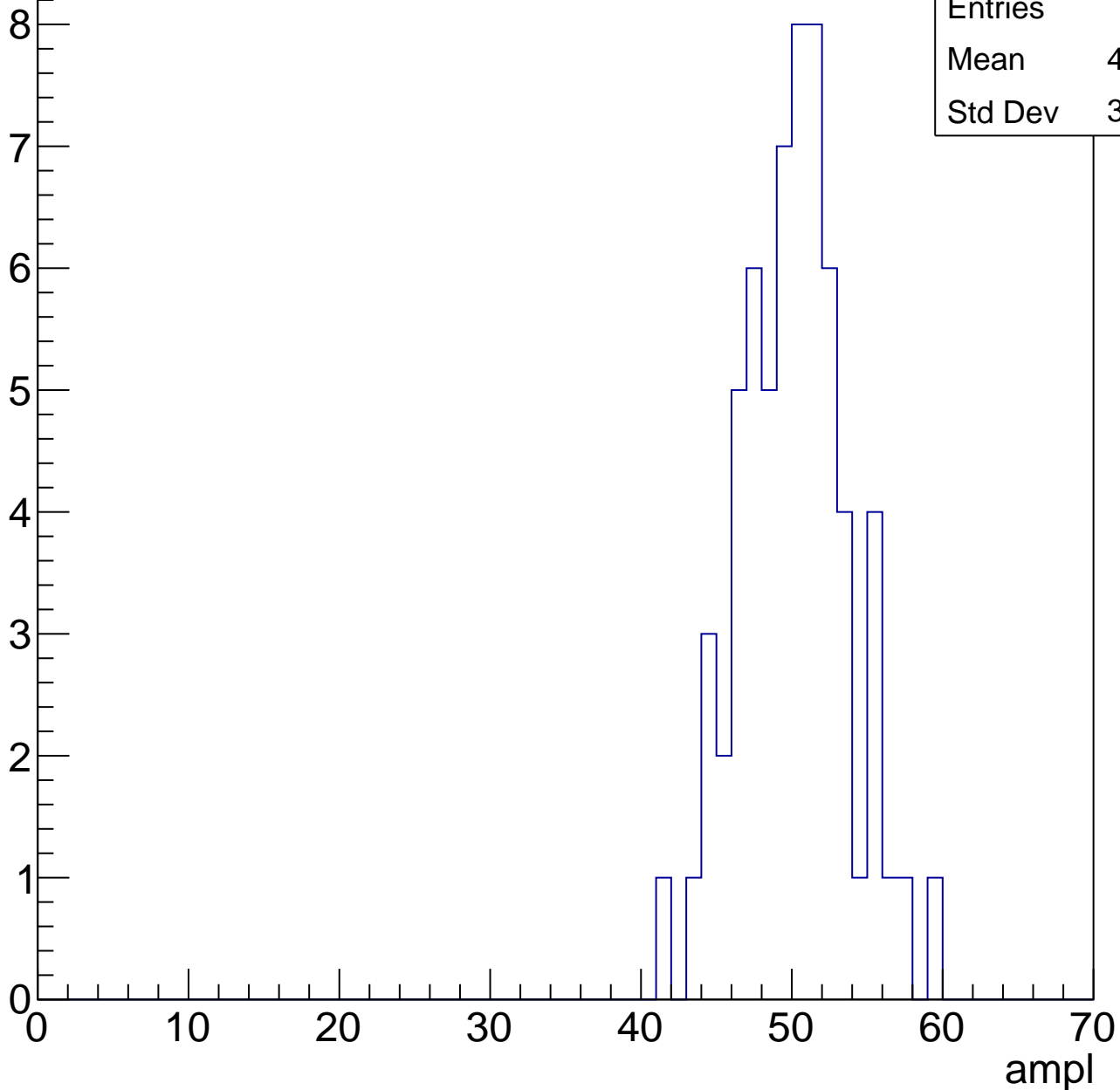


B1L103S, U5-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

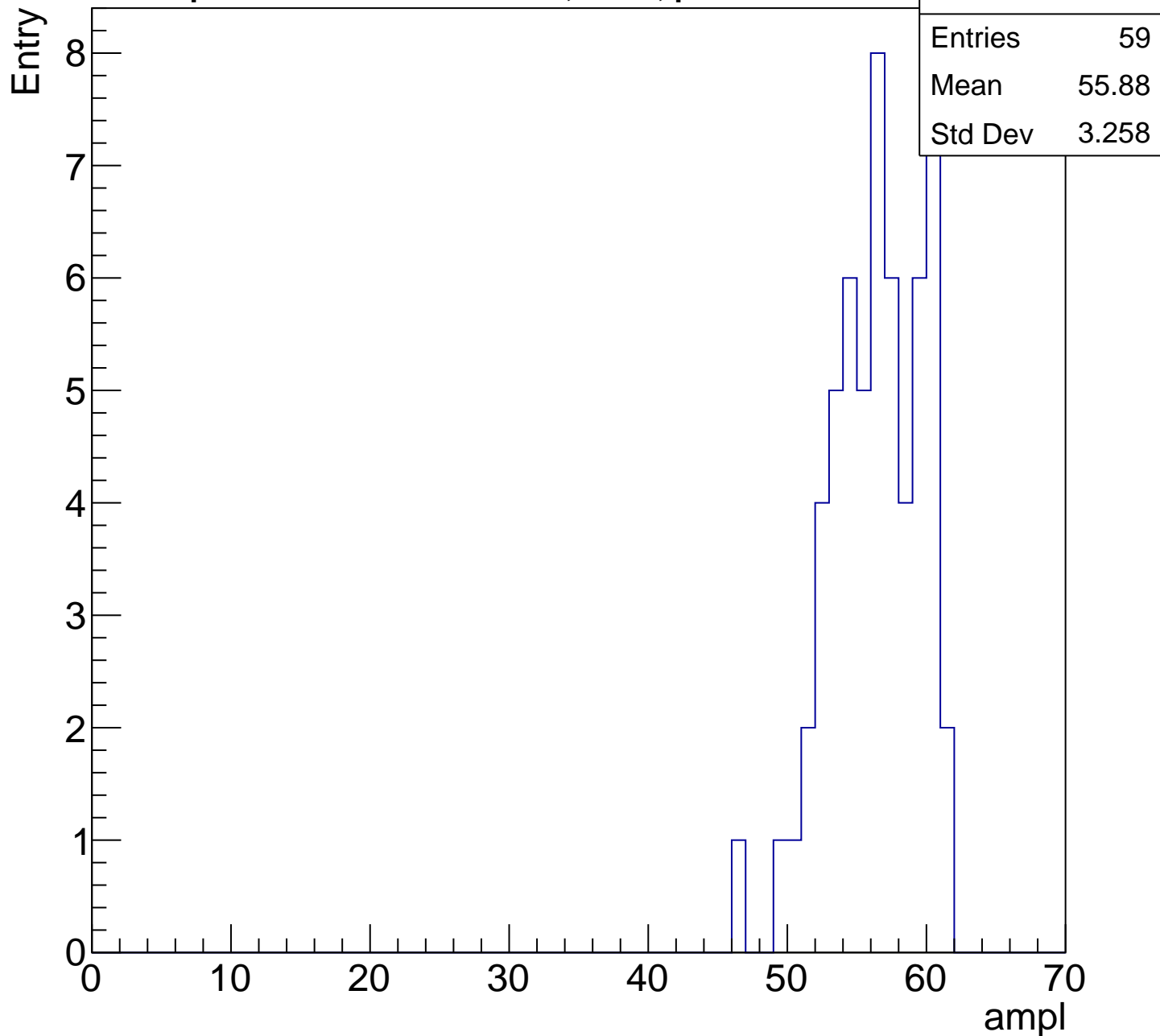
Entry

Entries	64
Mean	49.67
Std Dev	3.518



B1L103S, U5-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

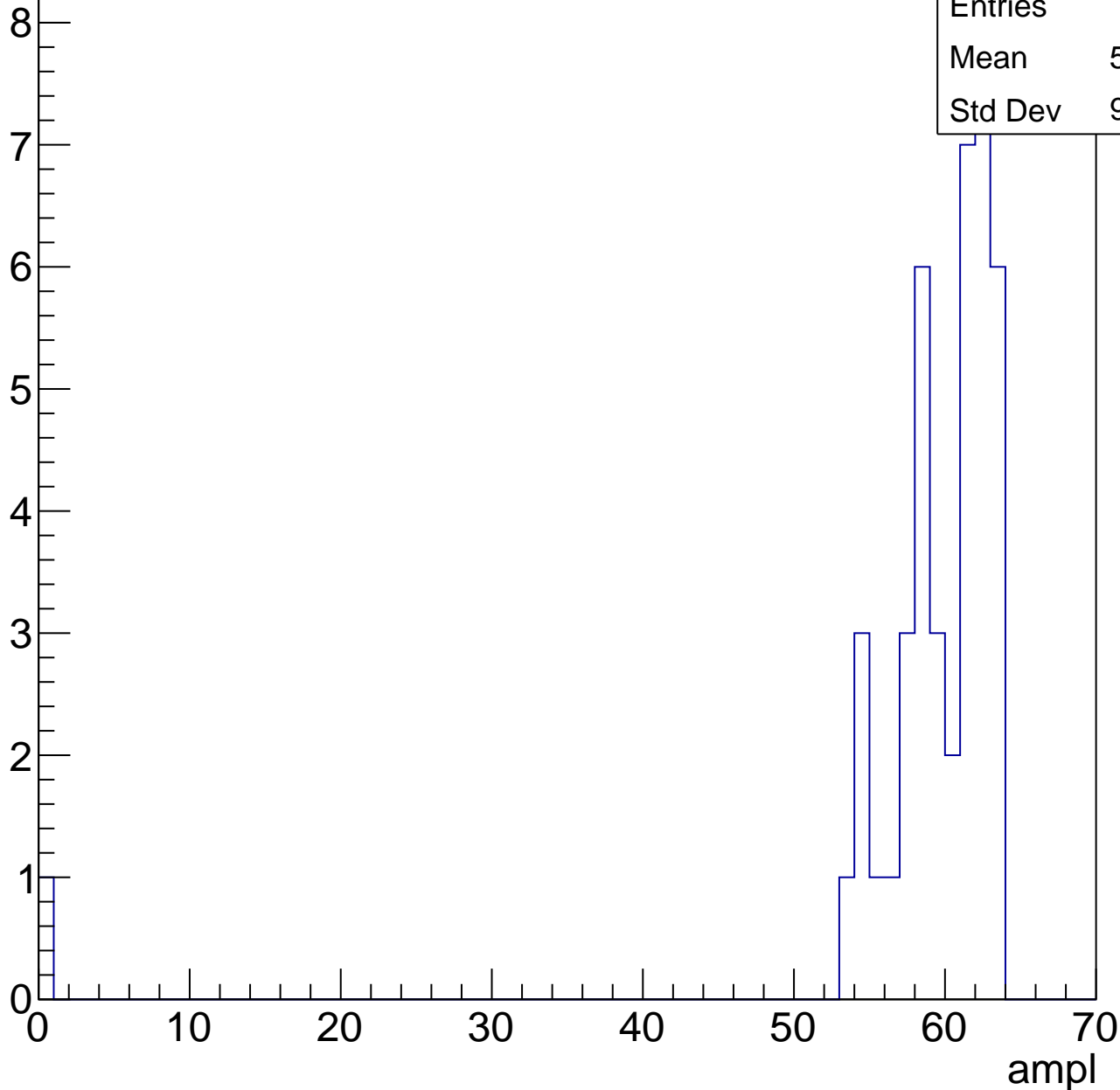


B1L103S, U5-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.17
Std Dev	9.517



B1L103S, U5-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch5, adc0

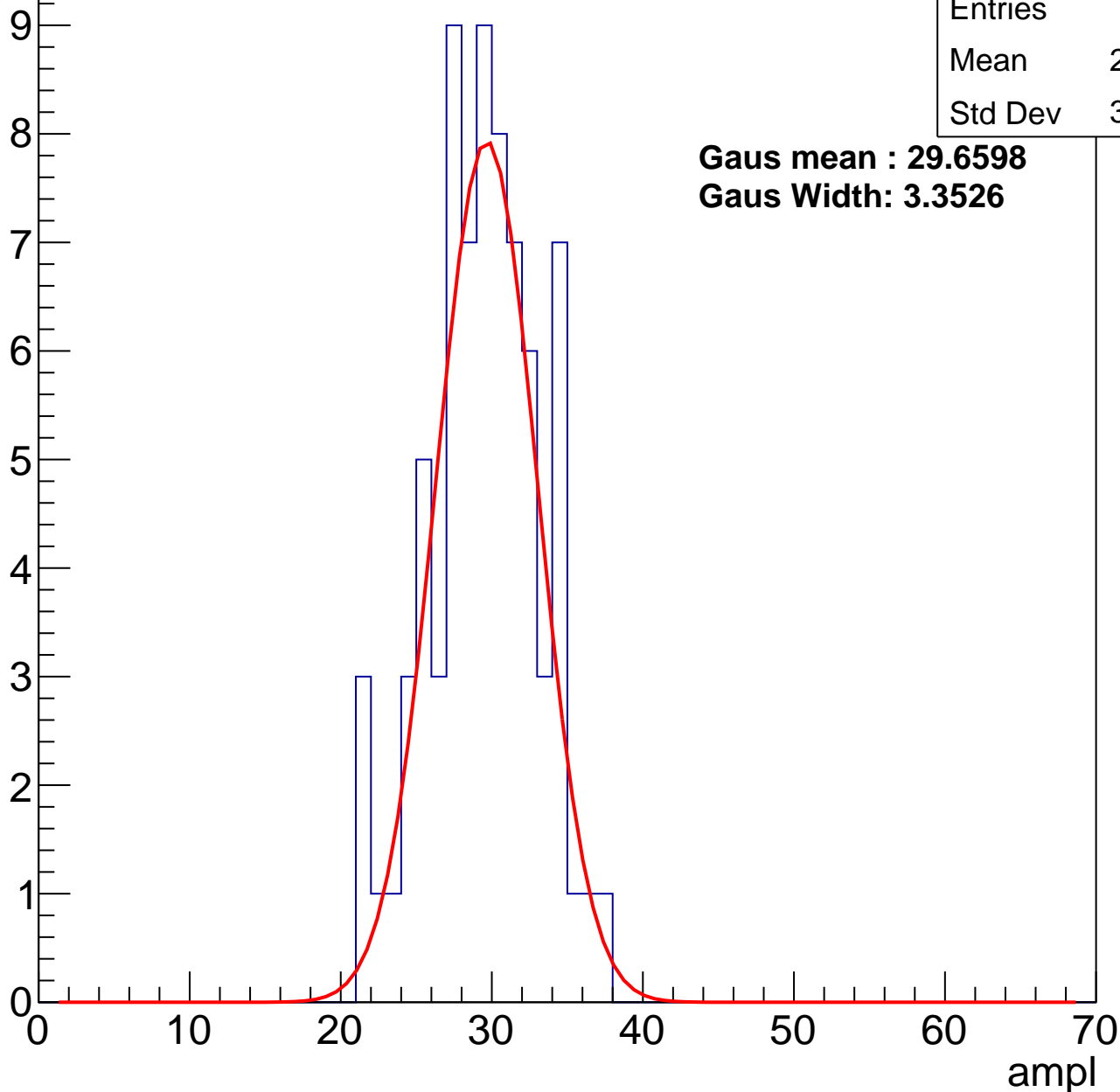
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.03
Std Dev	3.585

Gaus mean : 29.6598

Gaus Width: 3.3526



B1L103S, U5-ch5, adc1

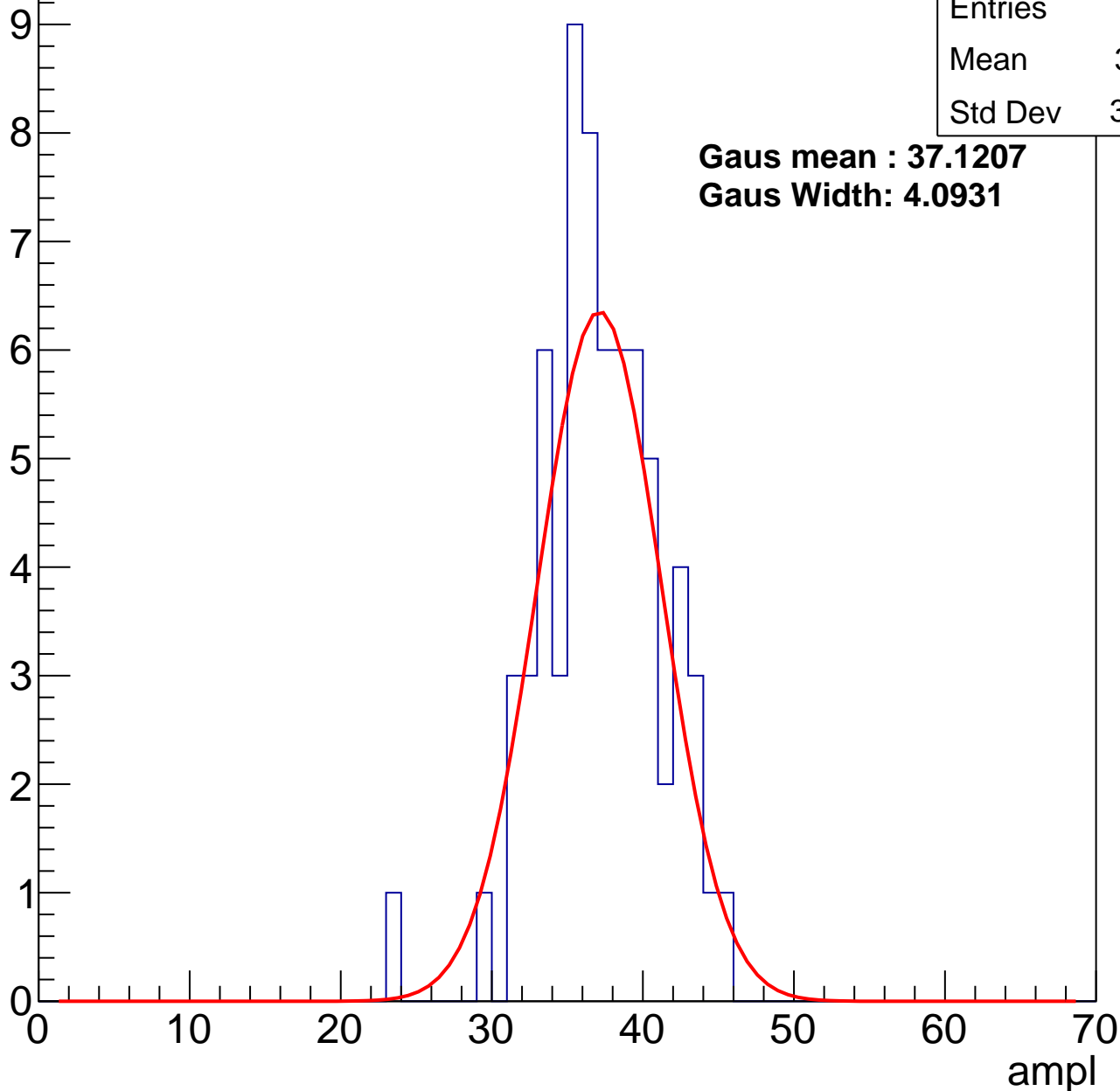
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.71
Std Dev	3.896

Gaus mean : 37.1207

Gaus Width: 4.0931



B1L103S, U5-ch5, adc2

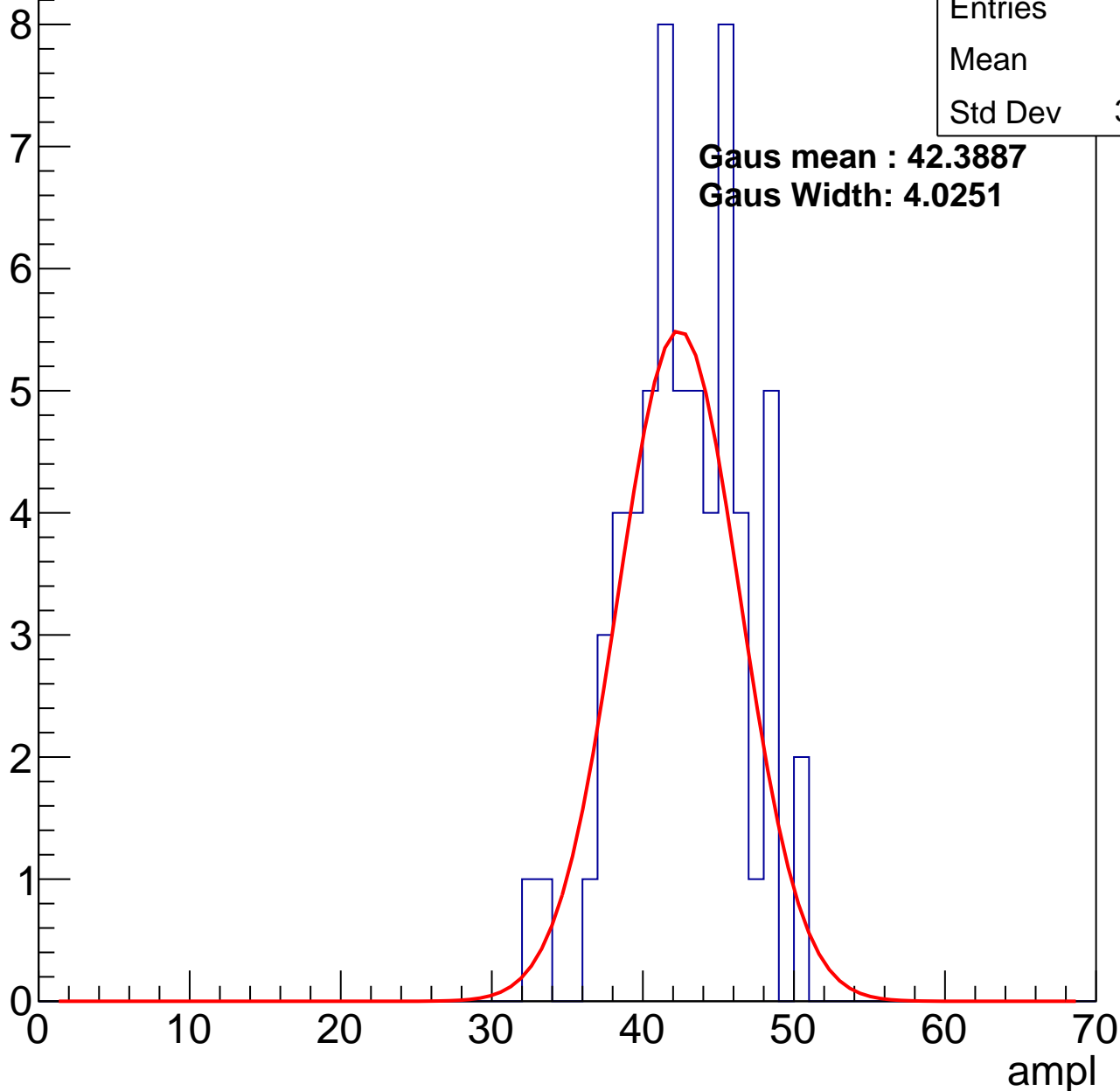
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.3
Std Dev	3.851

Gaus mean : 42.3887

Gaus Width: 4.0251



B1L103S, U5-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

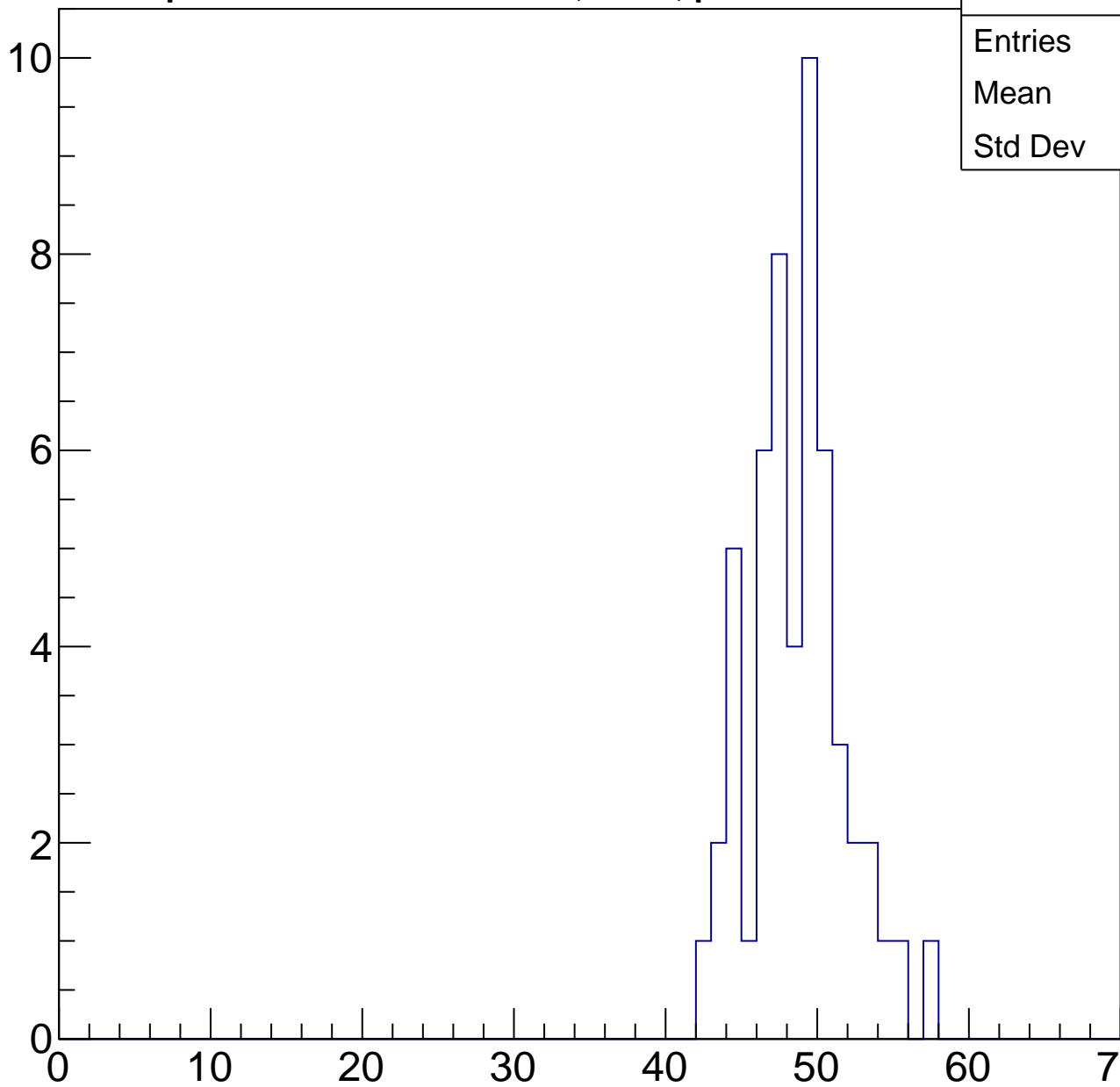
Entries	53
Mean	48.23
Std Dev	3.112

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

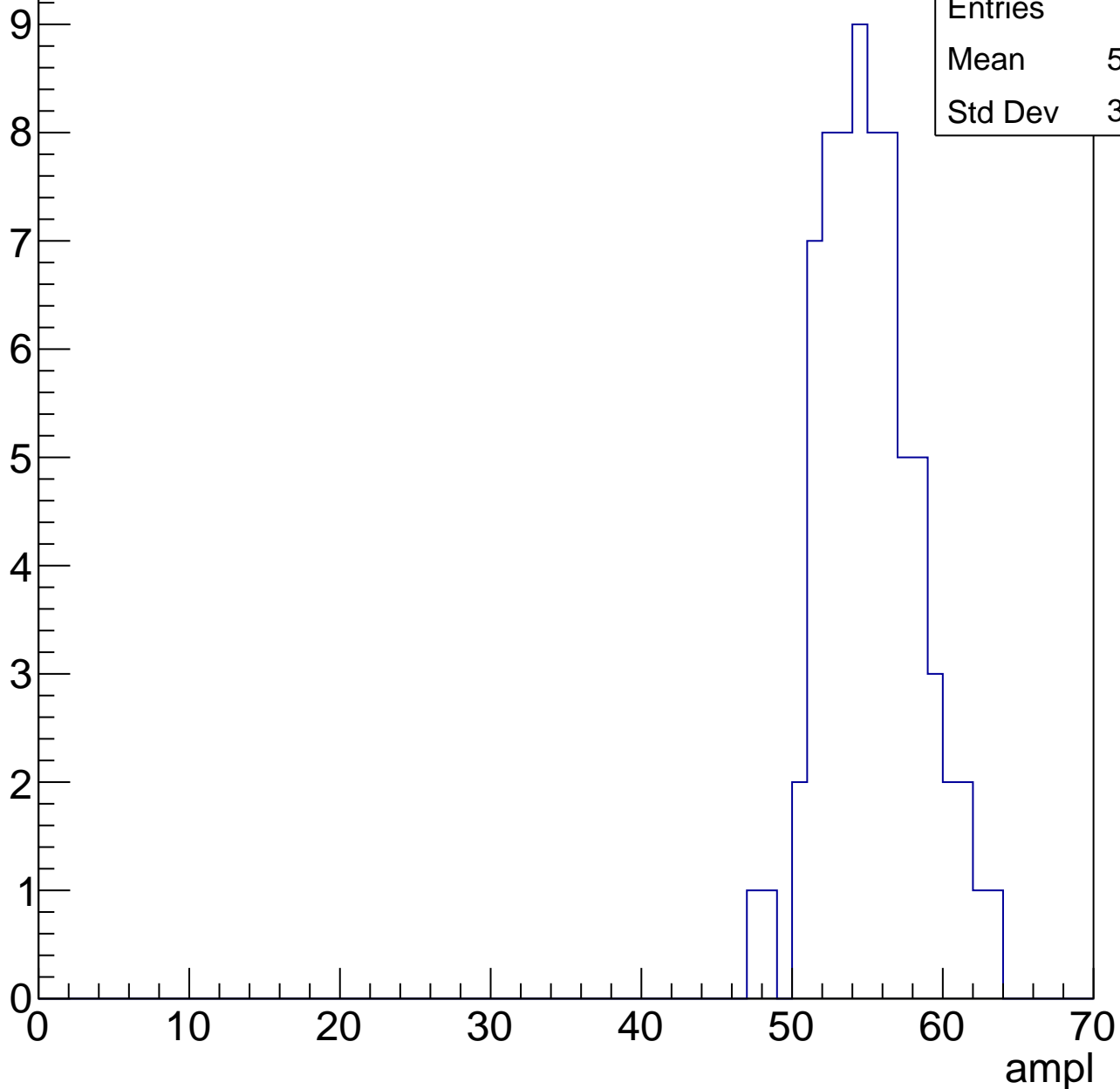
ampl



B1L103S, U5-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	71
Mean	54.72
Std Dev	3.225

B1L103S, U5-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 45

Mean 58.62

Std Dev 9.09

ampl

0

10

20

30

40

50

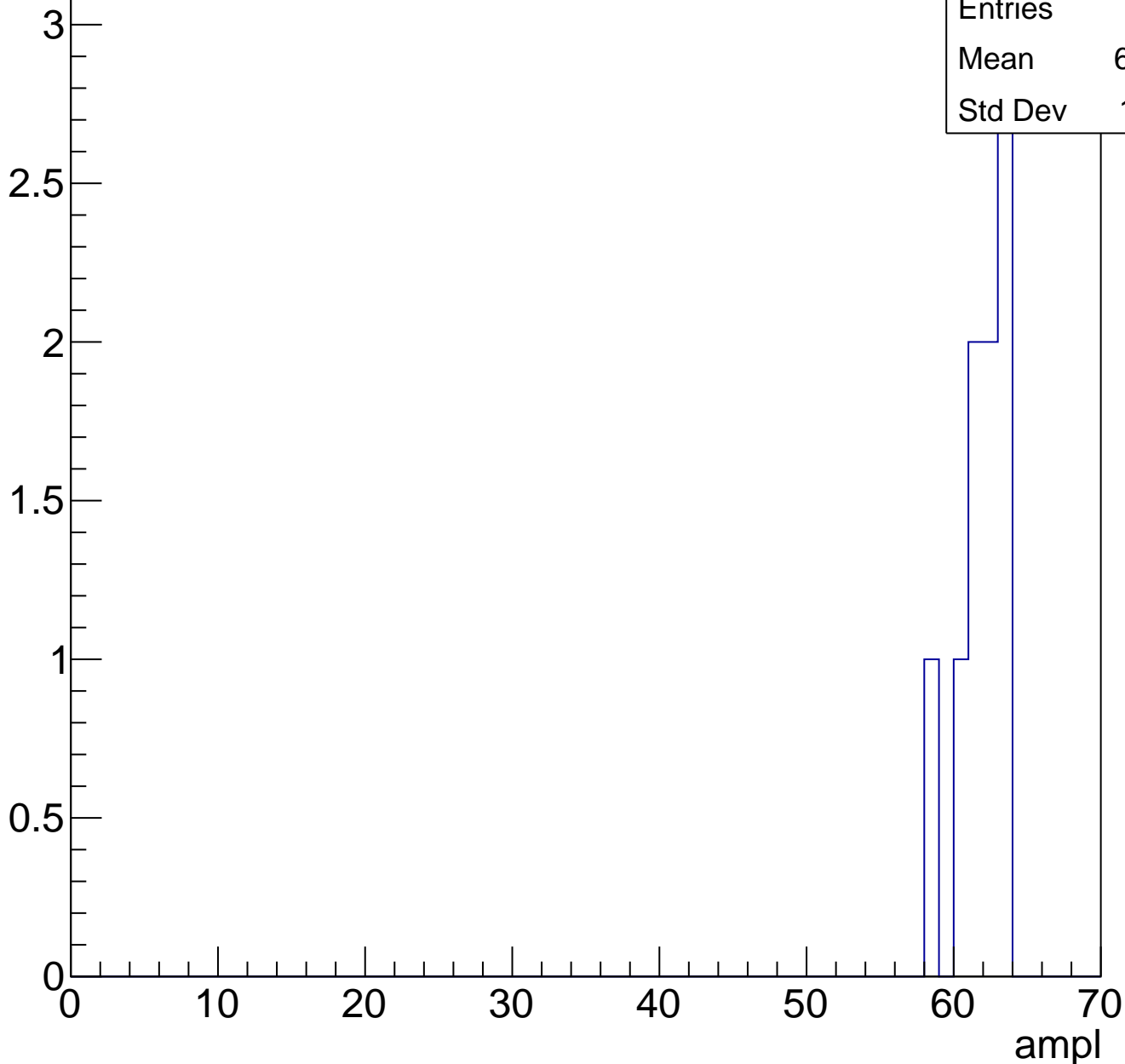
60

70

B1L103S, U5-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

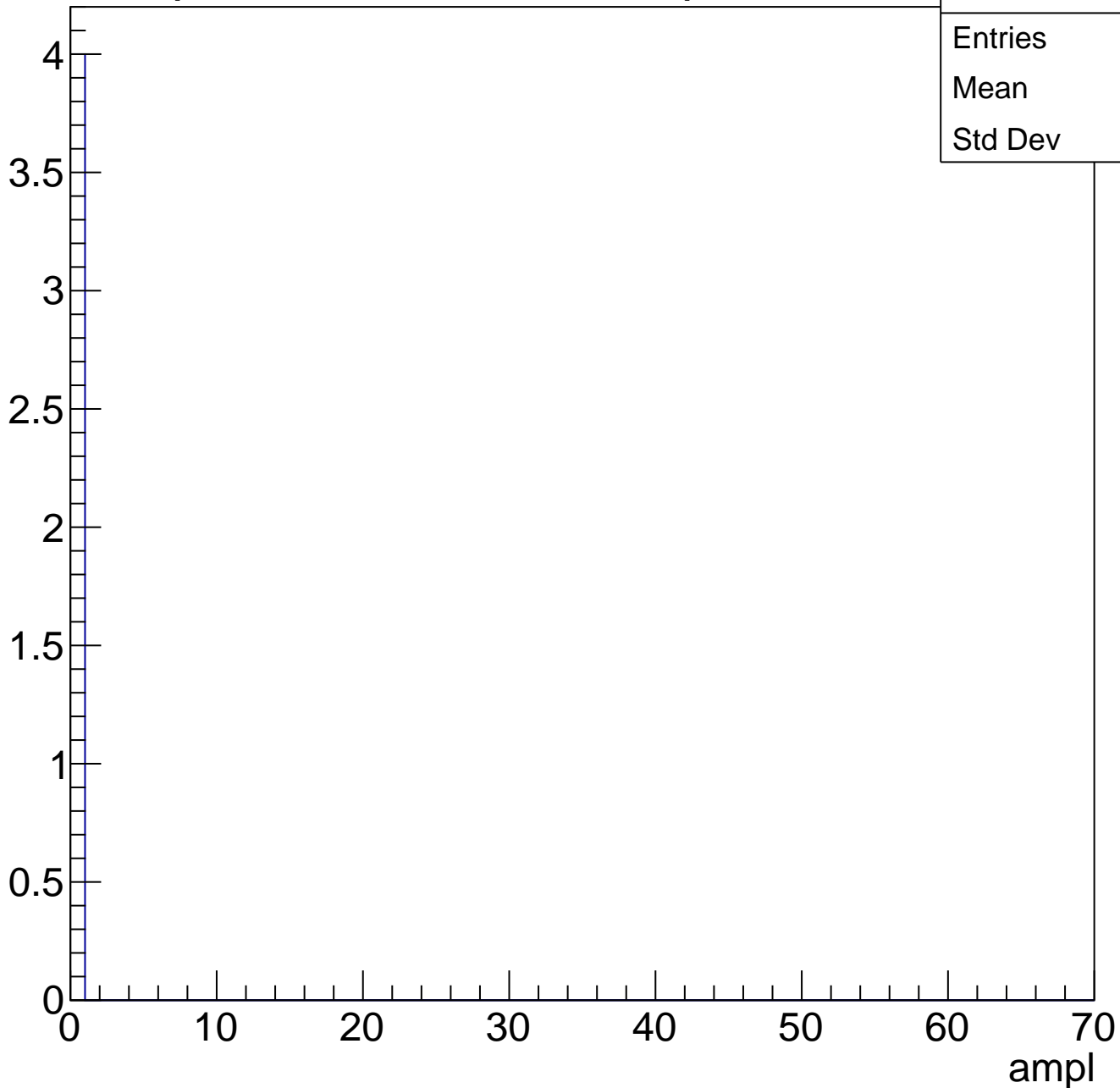
Entry



B1L103S, U5-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	0
Std Dev	0

B1L103S, U5-ch6, adc0

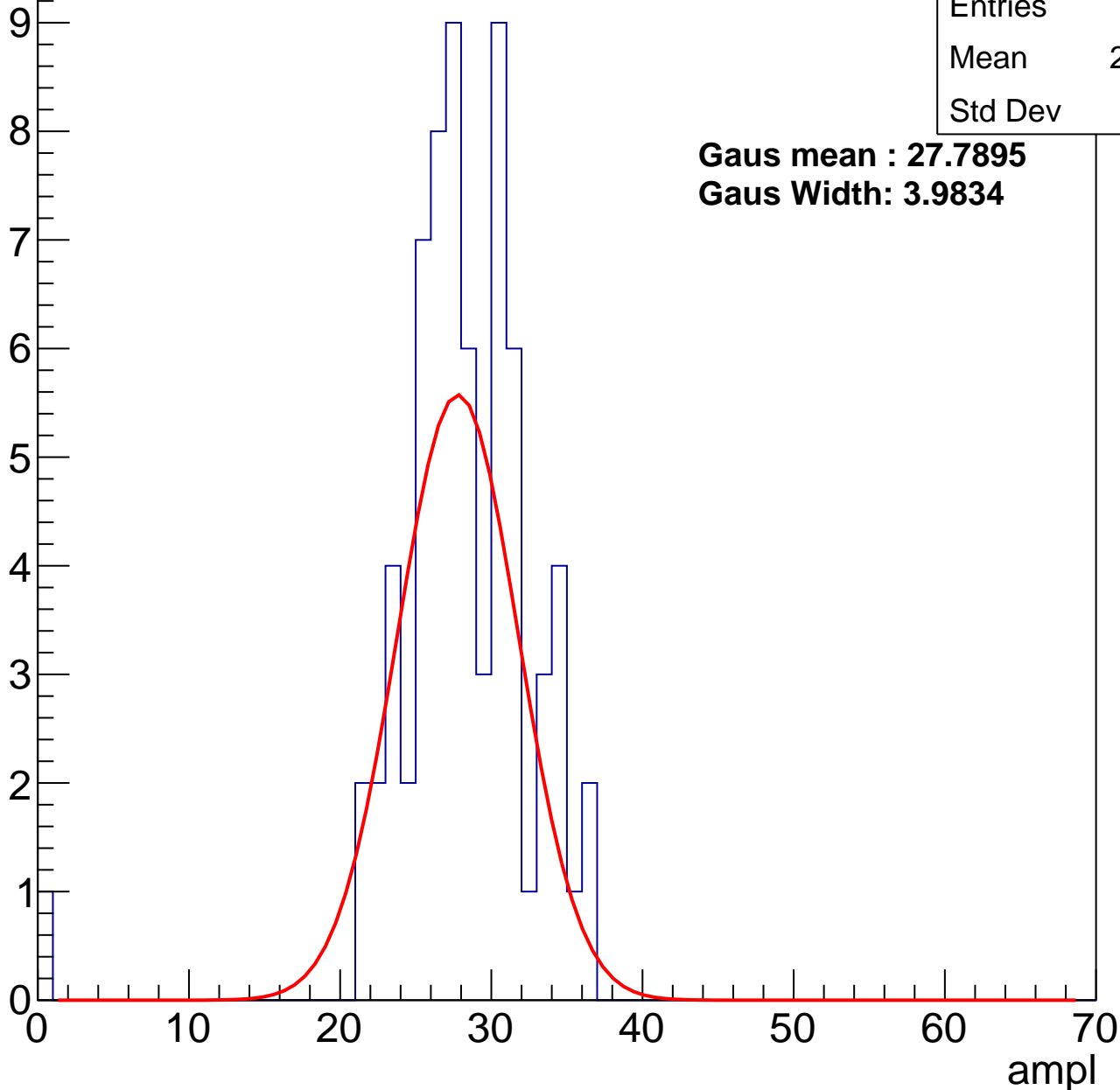
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	27.67
Std Dev	4.91

Gaus mean : 27.7895

Gaus Width: 3.9834



B1L103S, U5-ch6, adc1

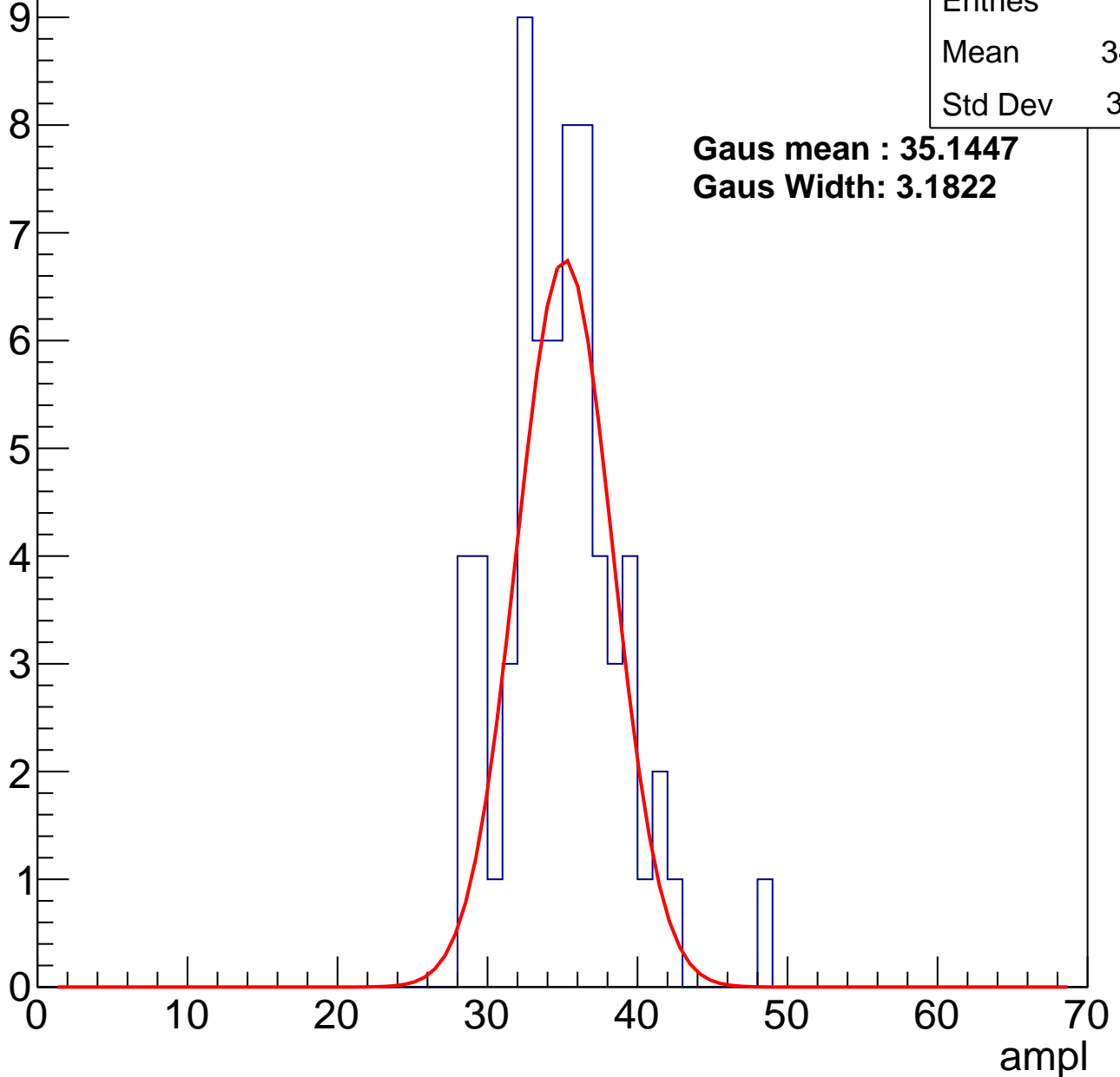
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	34.45
Std Dev	3.791

Gaus mean : 35.1447

Gaus Width: 3.1822



B1L103S, U5-ch6, adc2

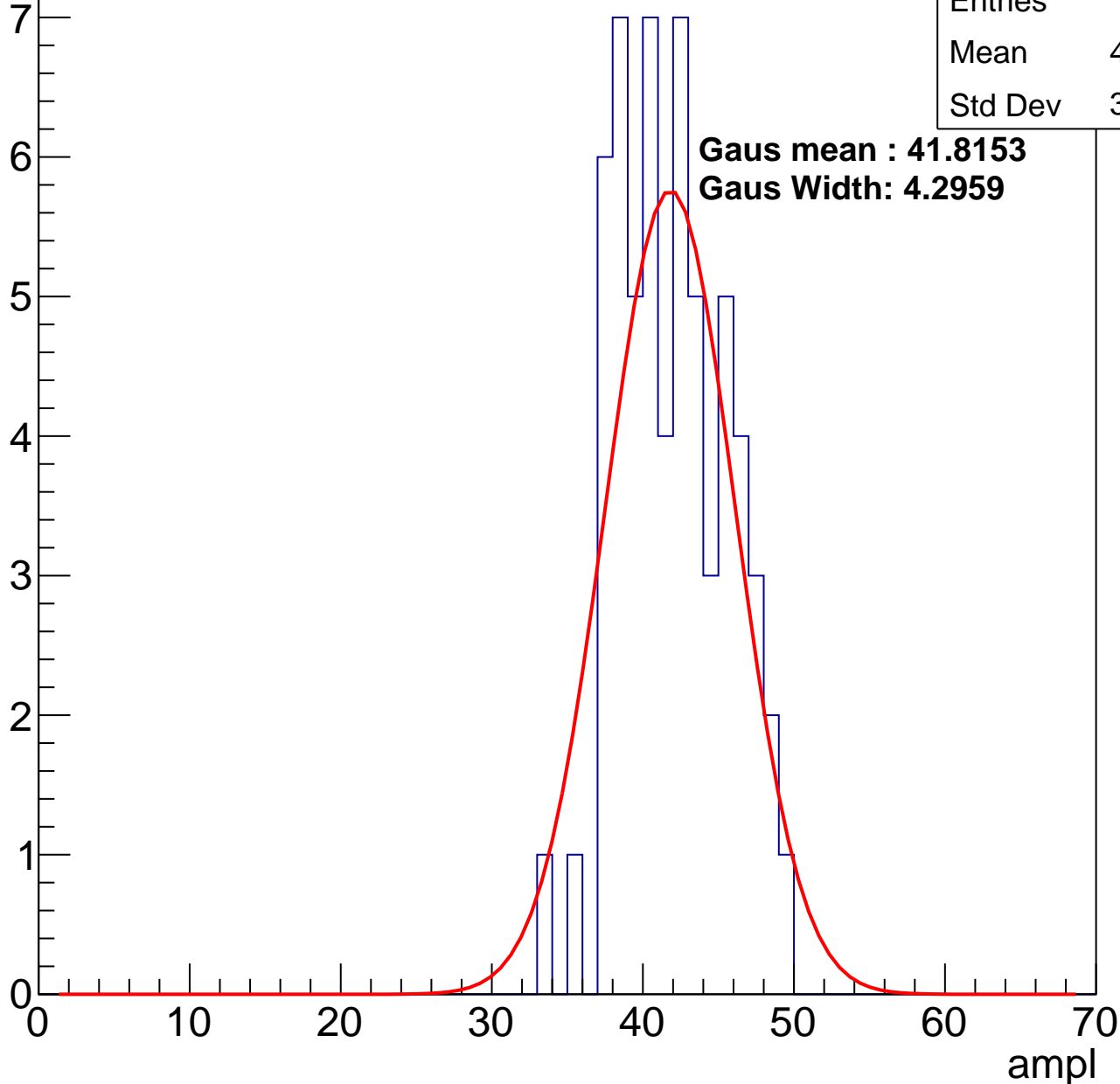
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.49
Std Dev	3.565

Gaus mean : 41.8153

Gaus Width: 4.2959

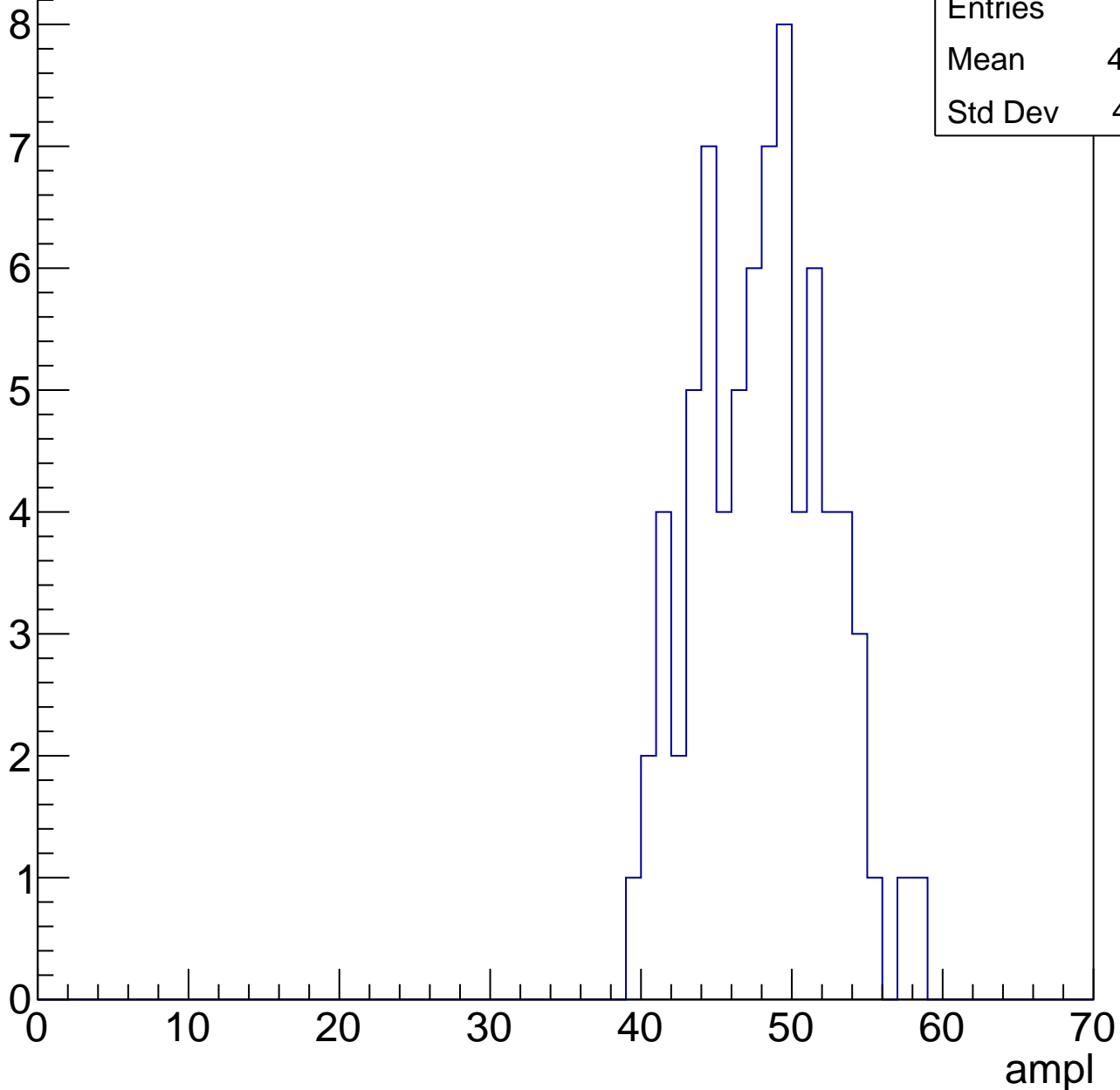


B1L103S, U5-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	47.57
Std Dev	4.231

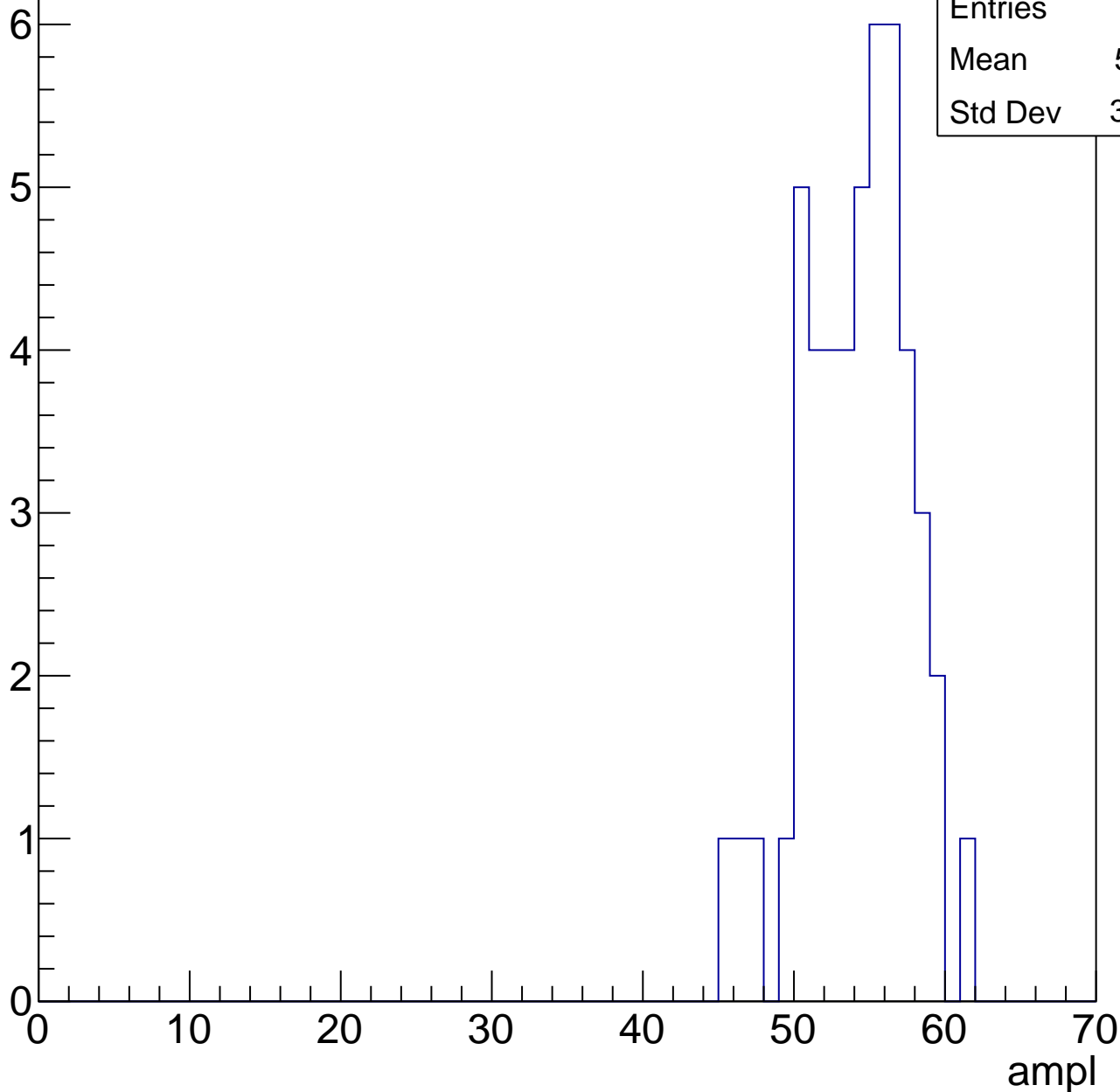


B1L103S, U5-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

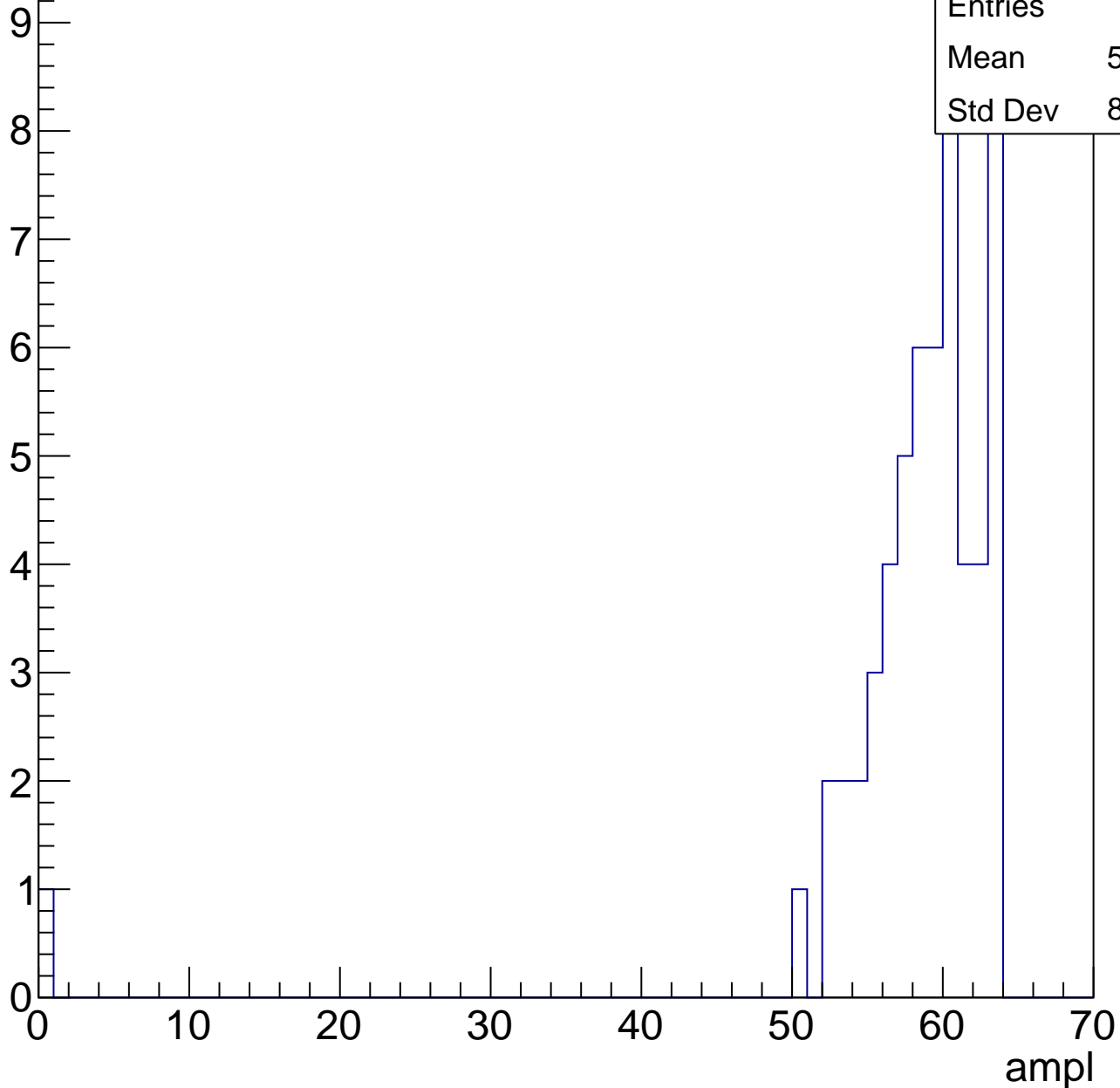
Entries	48
Mean	53.71
Std Dev	3.428



B1L103S, U5-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

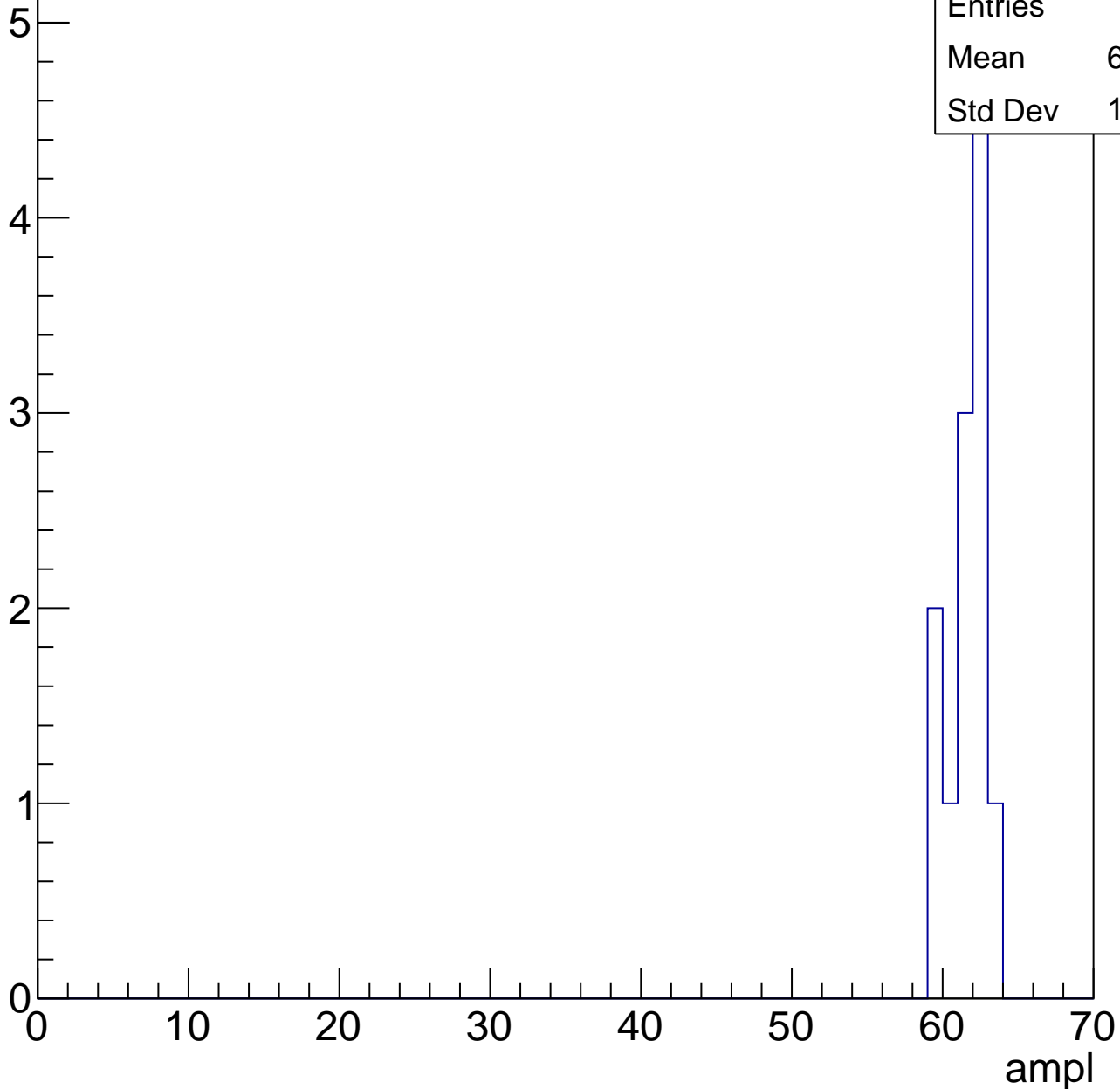


B1L103S, U5-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

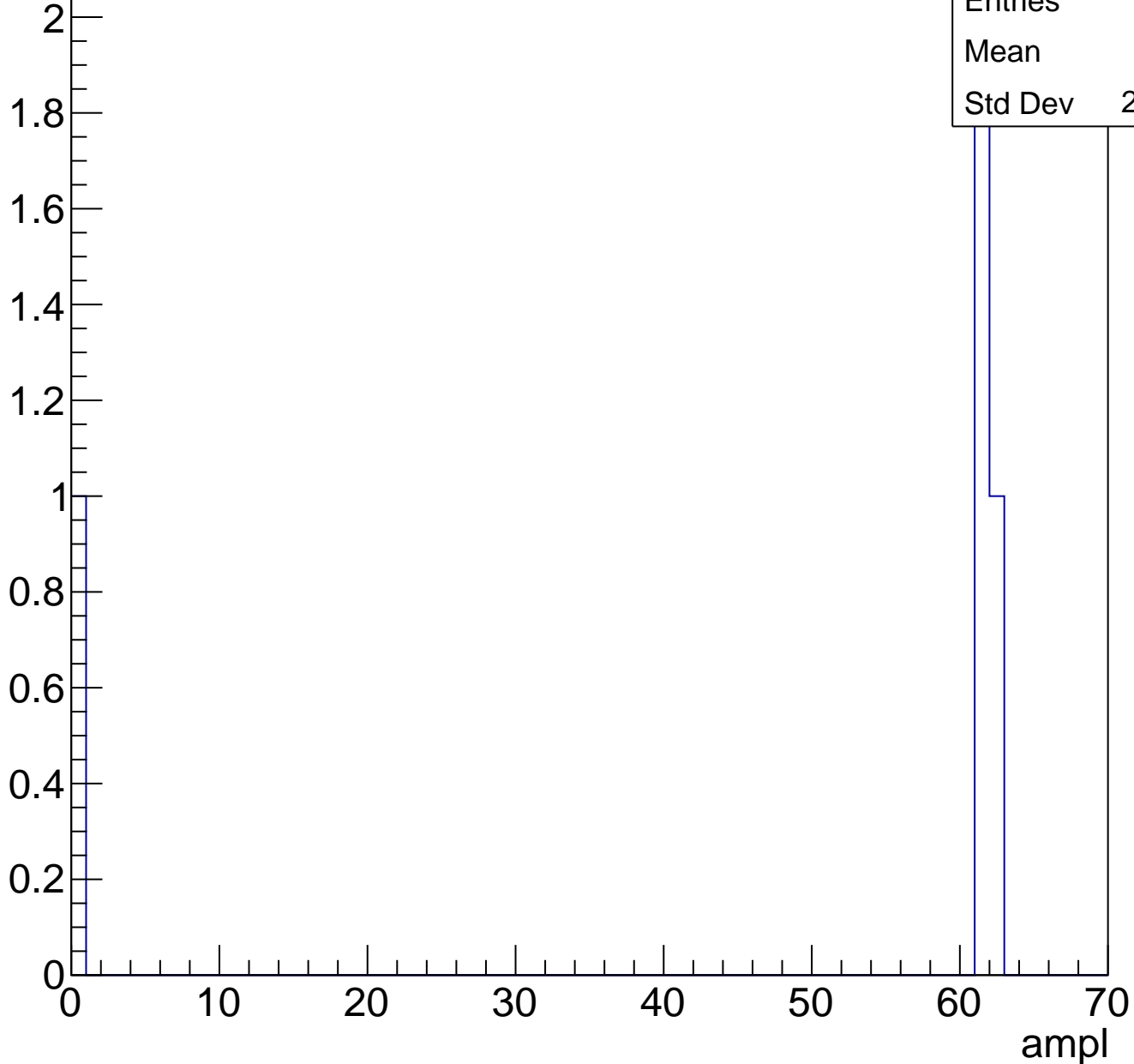
Entries	12
Mean	61.17
Std Dev	1.213



B1L103S, U5-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch7, adc0

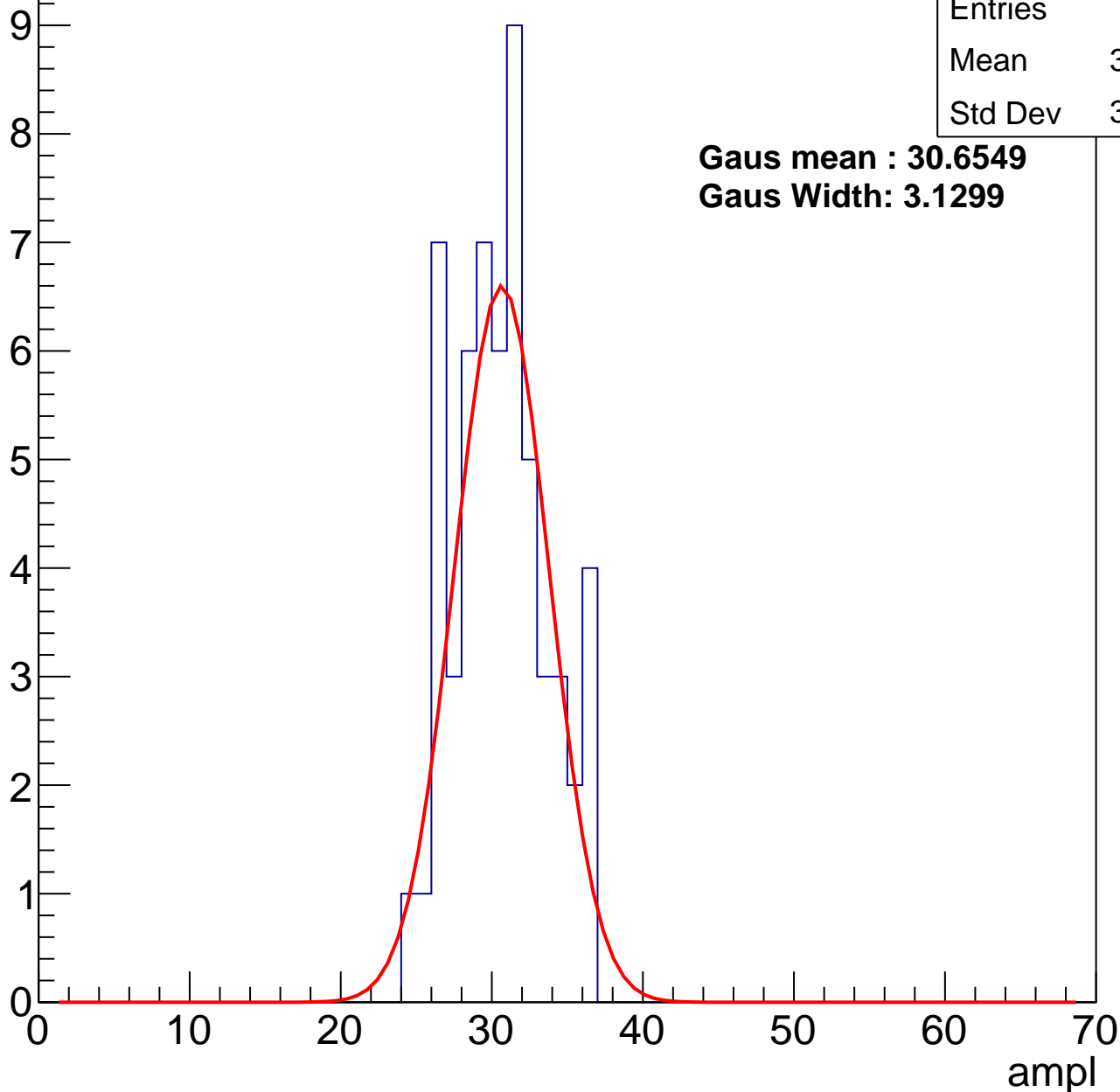
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	30.12
Std Dev	3.044

Gaus mean : 30.6549

Gaus Width: 3.1299



B1L103S, U5-ch7, adc1

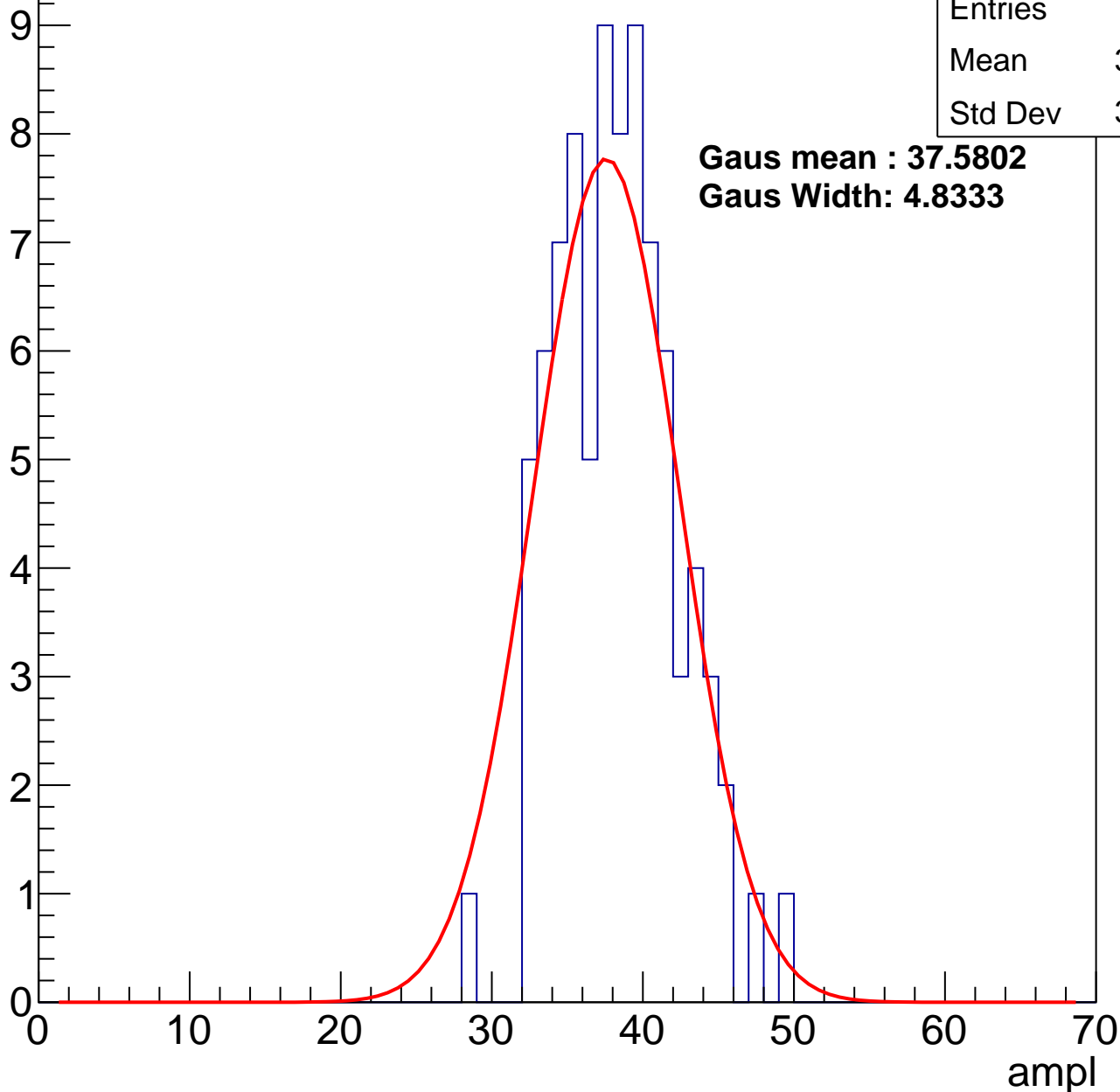
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	37.81
Std Dev	3.891

Gaus mean : 37.5802

Gaus Width: 4.8333



B1L103S, U5-ch7, adc2

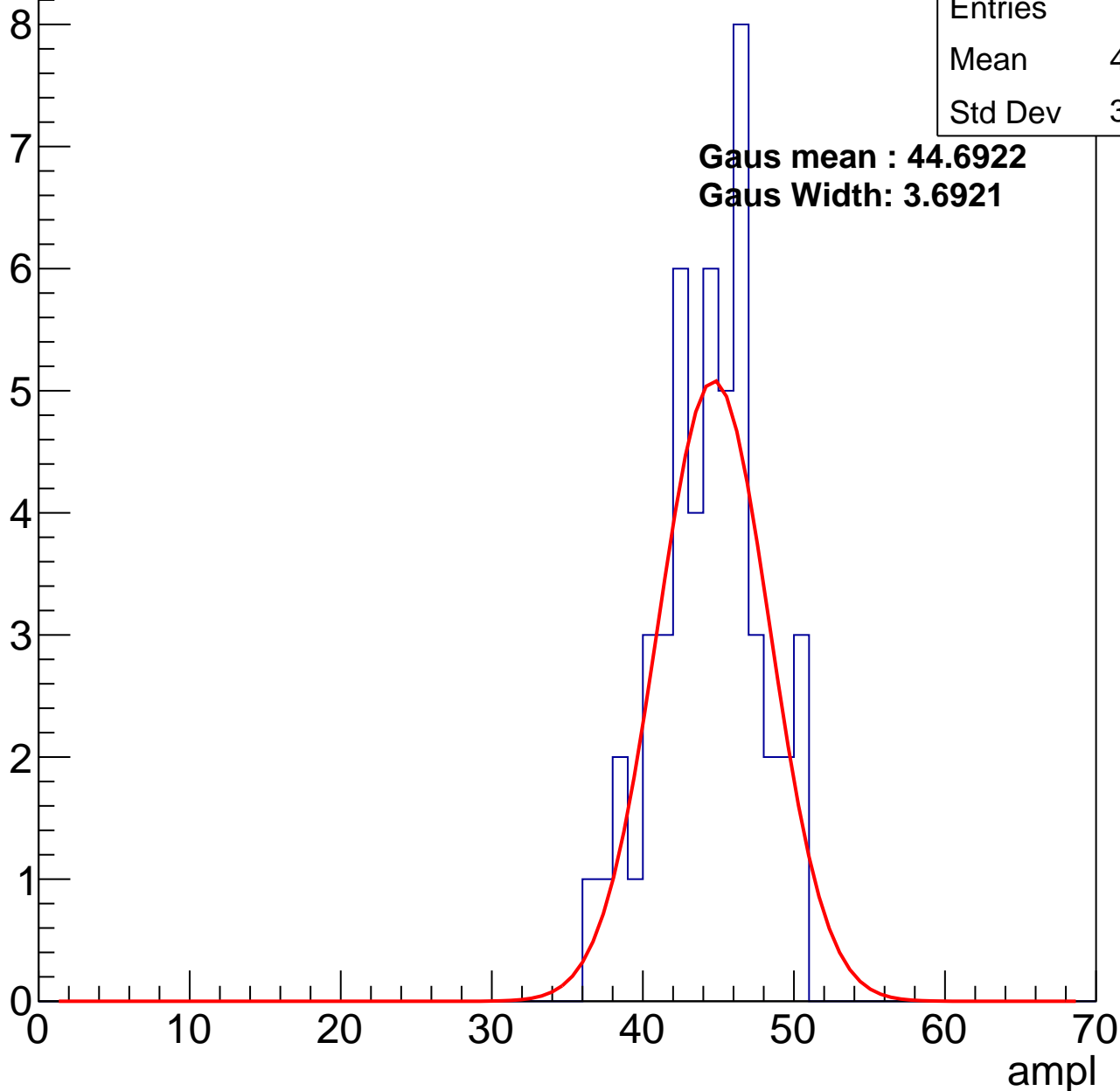
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	43.94
Std Dev	3.367

Gaus mean : 44.6922

Gaus Width: 3.6921



B1L103S, U5-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	49.99
Std Dev	3.609

Entry

10

8

6

4

2

0

0

10

20

30

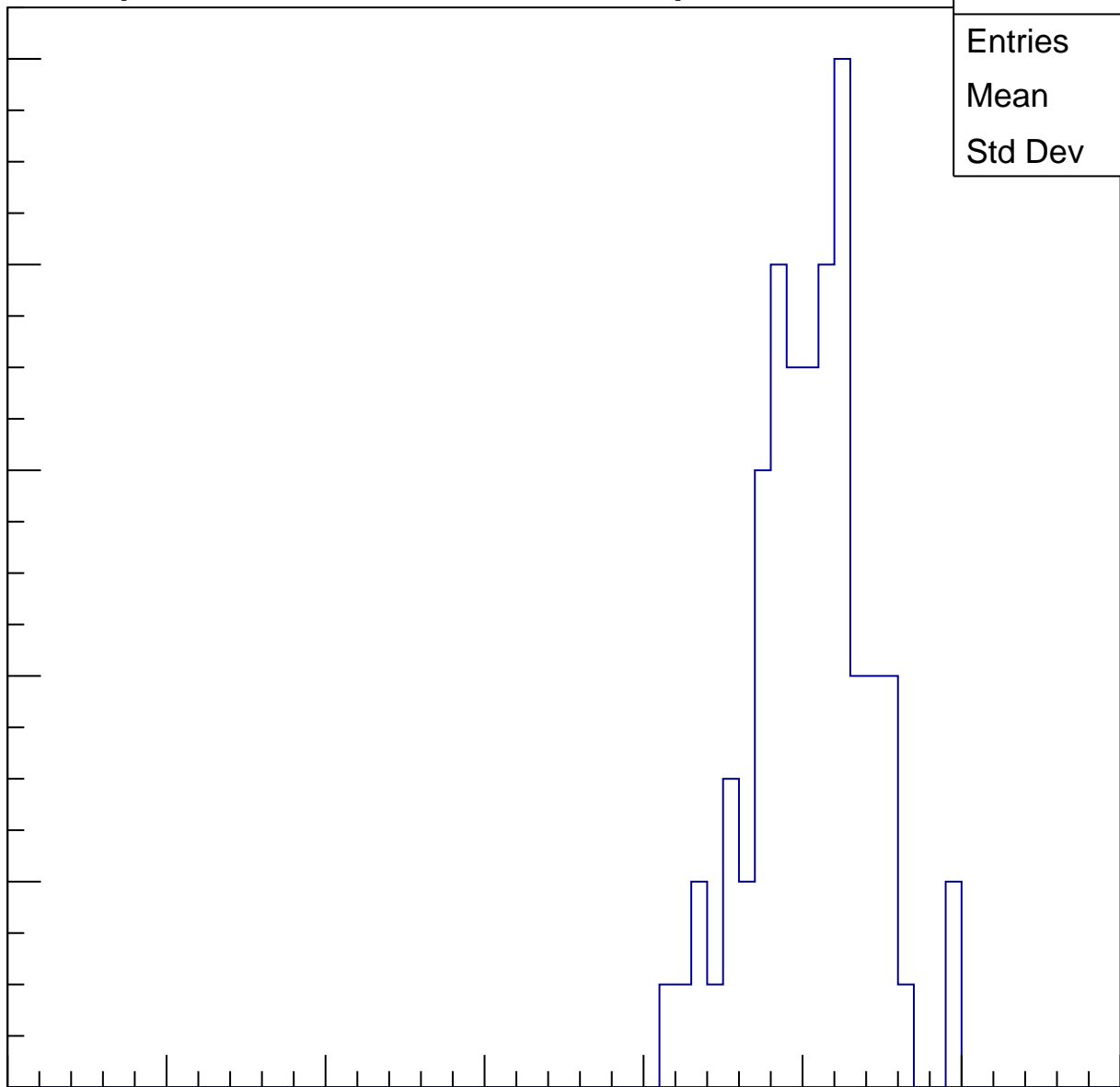
40

50

60

70

ampl

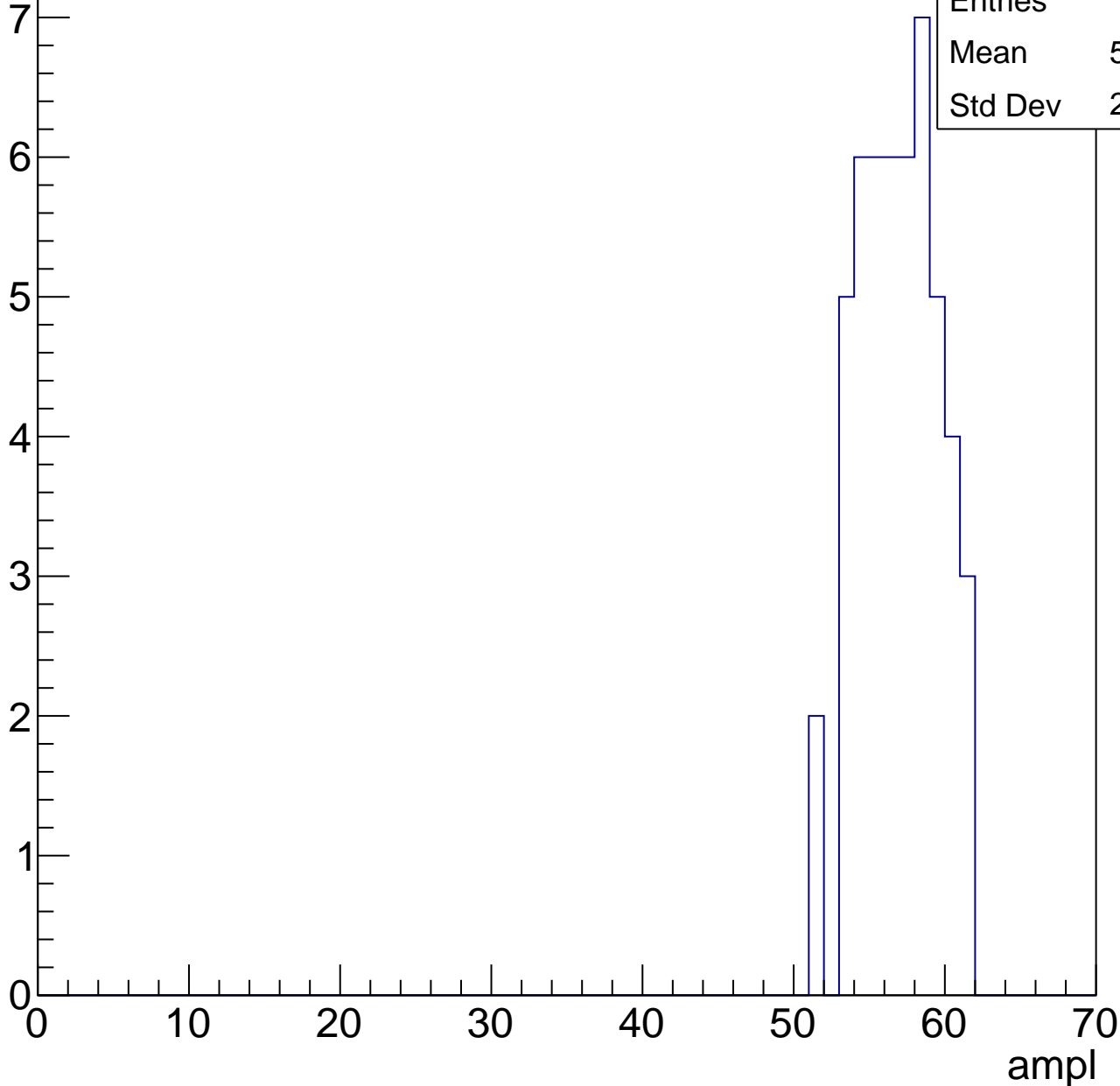


B1L103S, U5-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	56.46
Std Dev	2.578

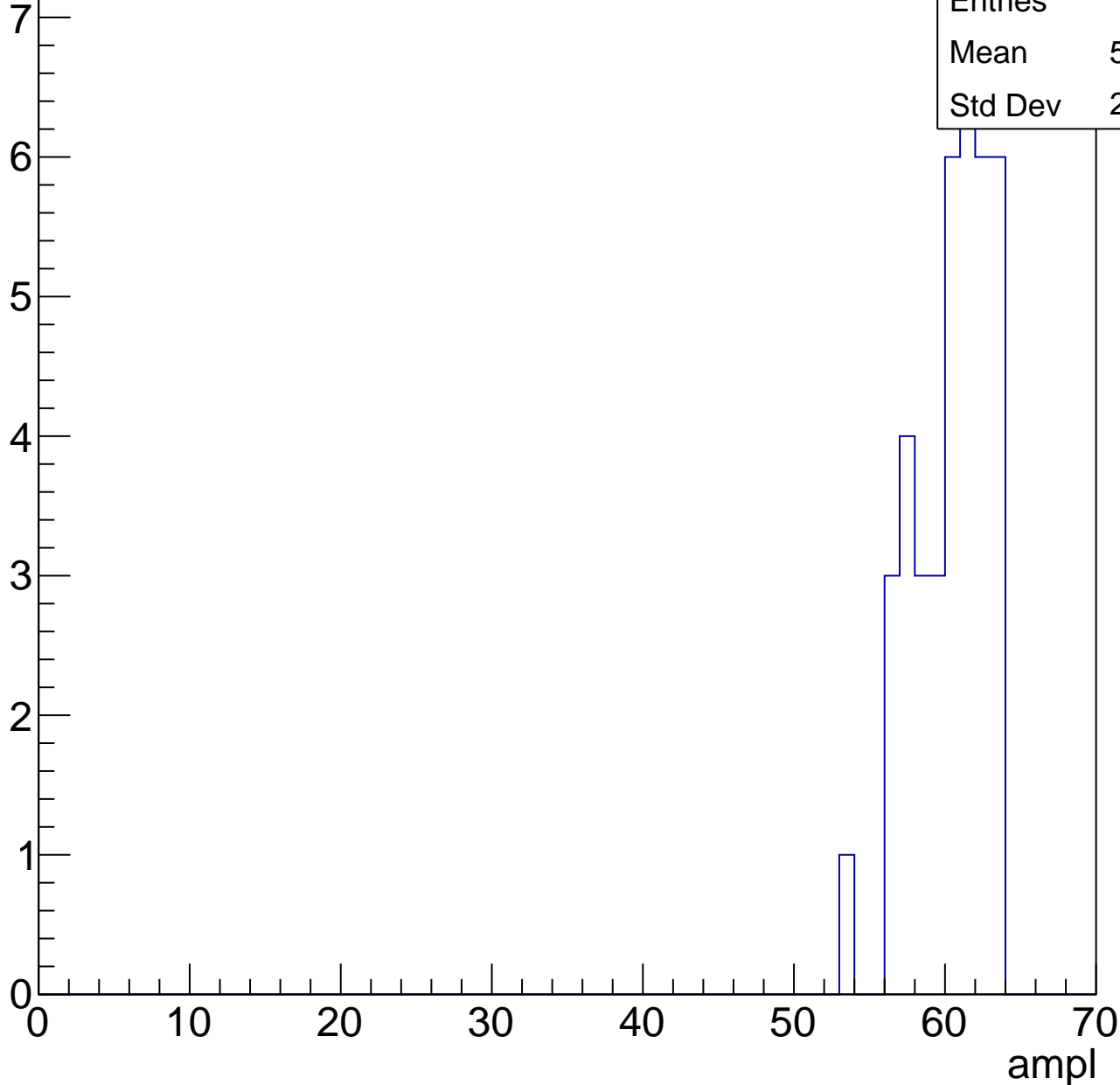


B1L103S, U5-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

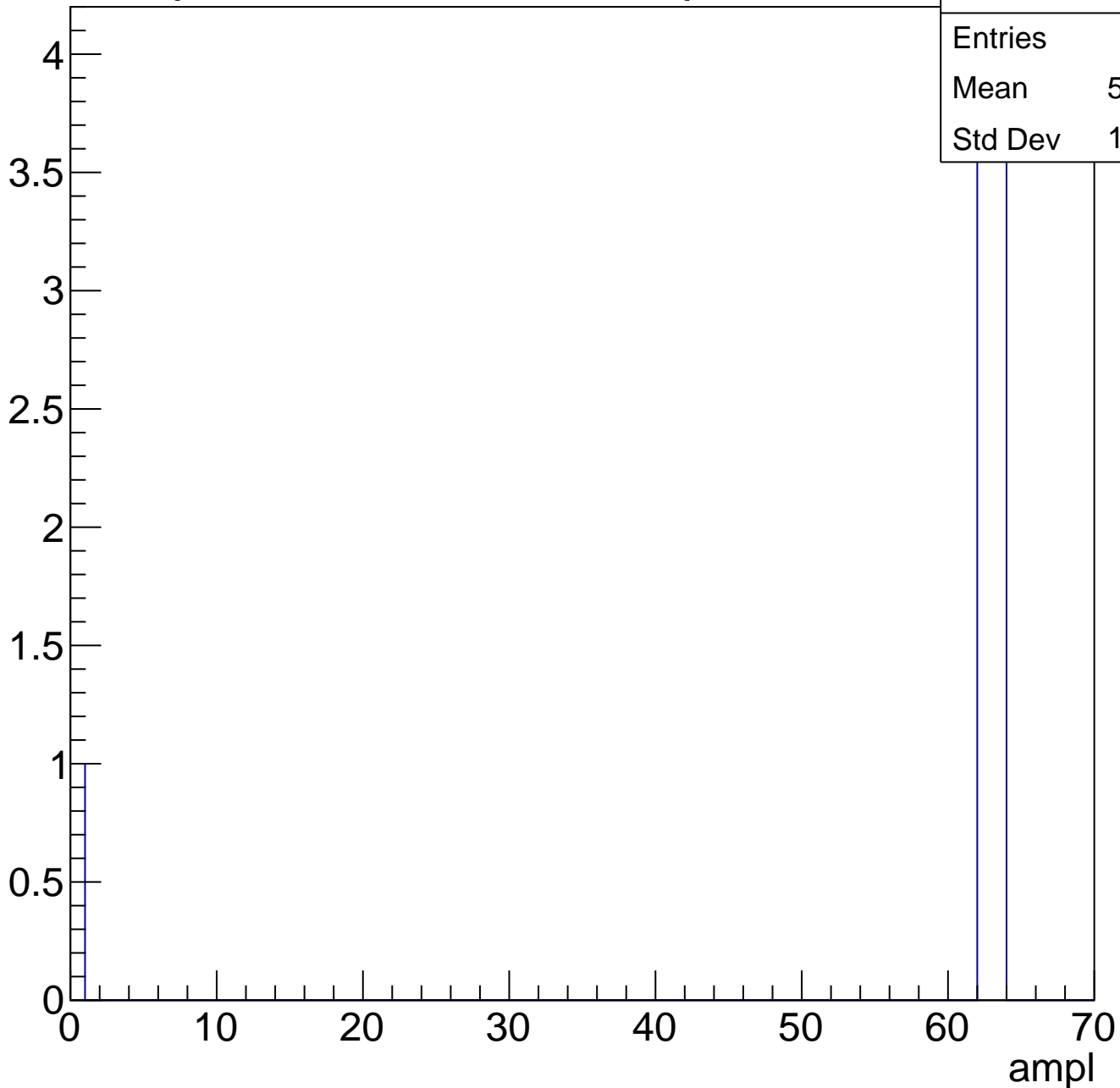
Entries	39
Mean	59.92
Std Dev	2.443



B1L103S, U5-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch8, adc0

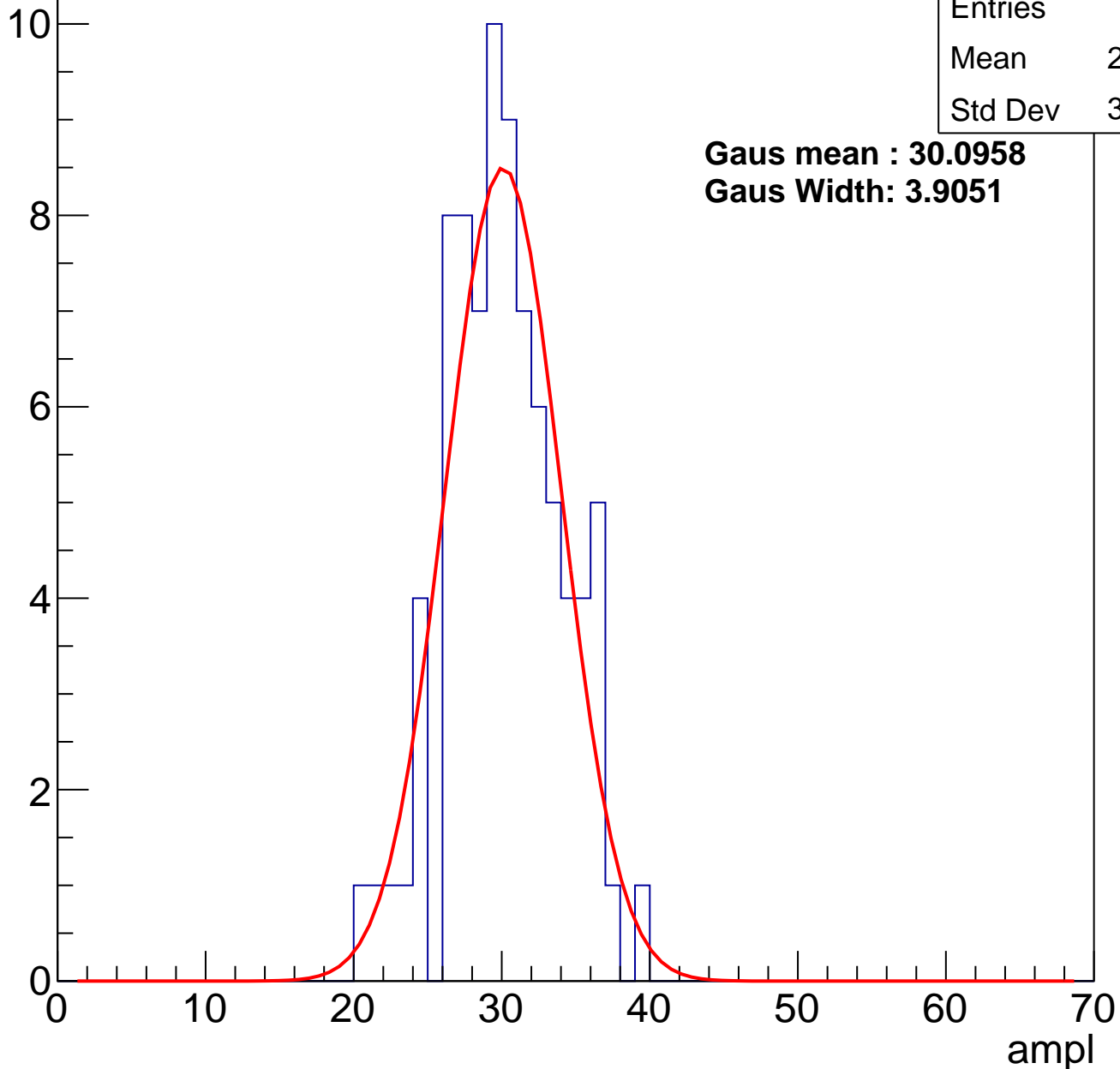
calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	29.73
Std Dev	3.834

Gaus mean : 30.0958

Gaus Width: 3.9051

Entry



B1L103S, U5-ch8, adc1

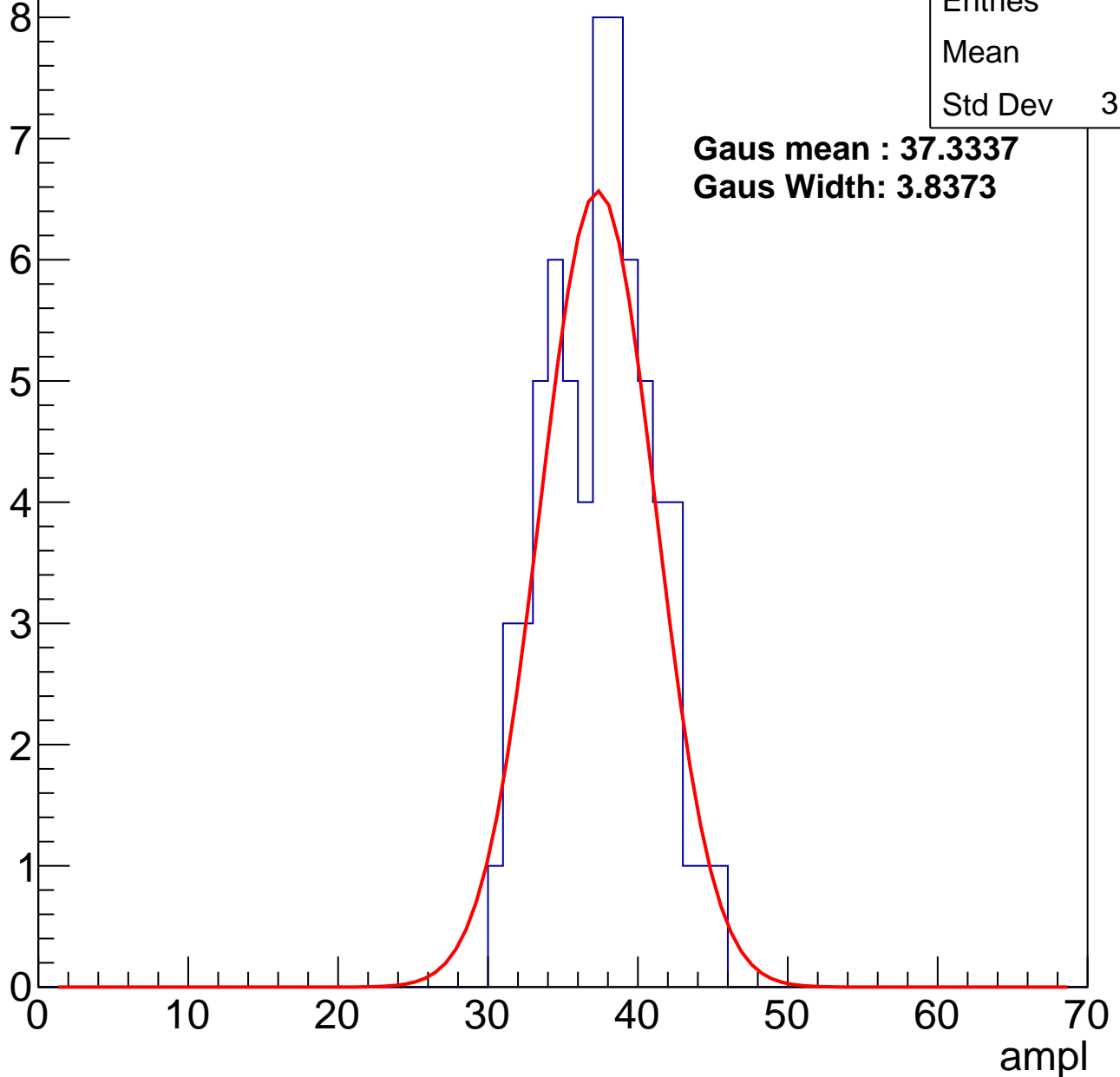
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	37
Std Dev	3.464

Gaus mean : 37.3337

Gaus Width: 3.8373



B1L103S, U5-ch8, adc2

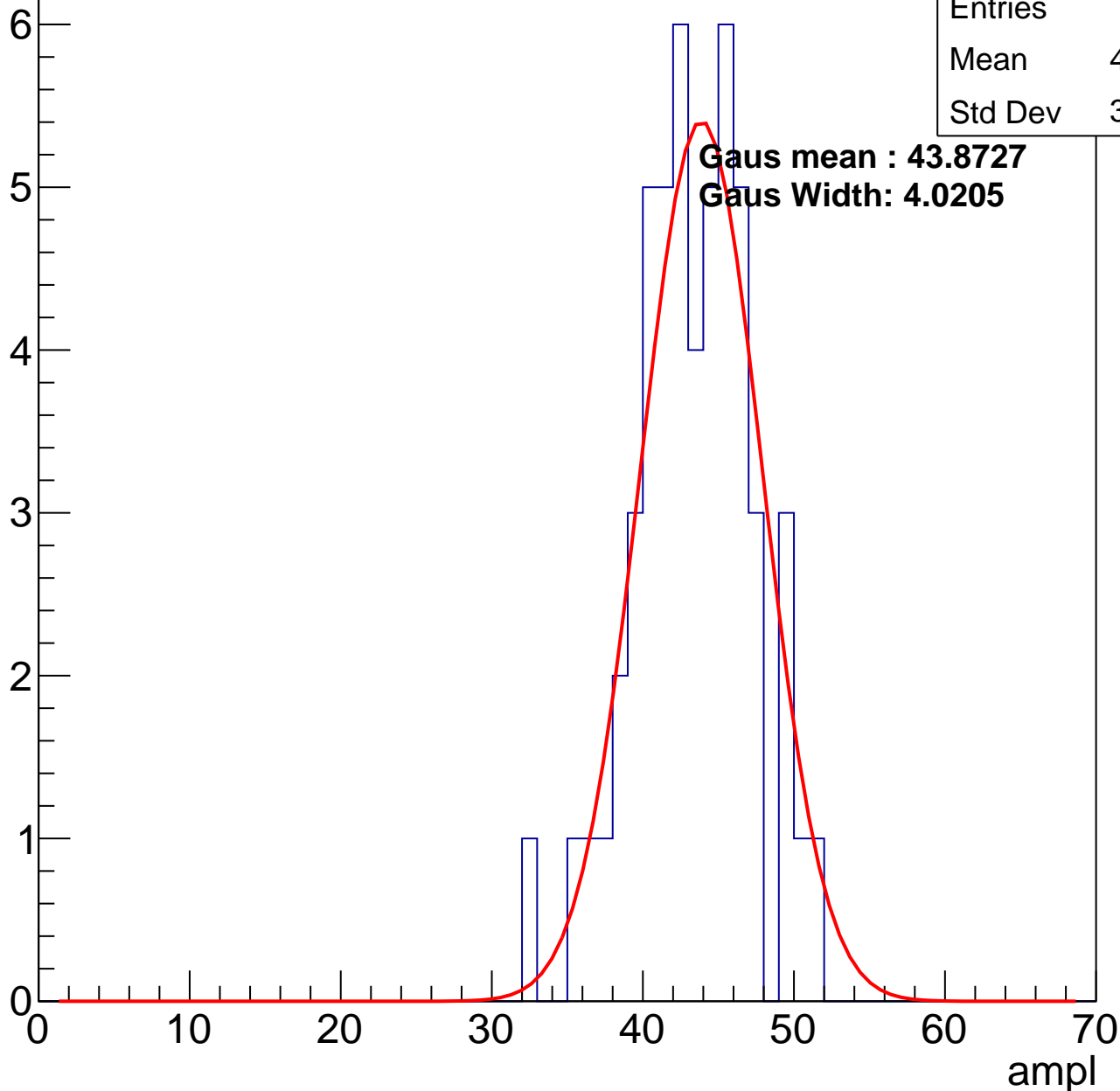
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.85
Std Dev	3.838

Gaus mean : 43.8727

Gaus Width: 4.0205

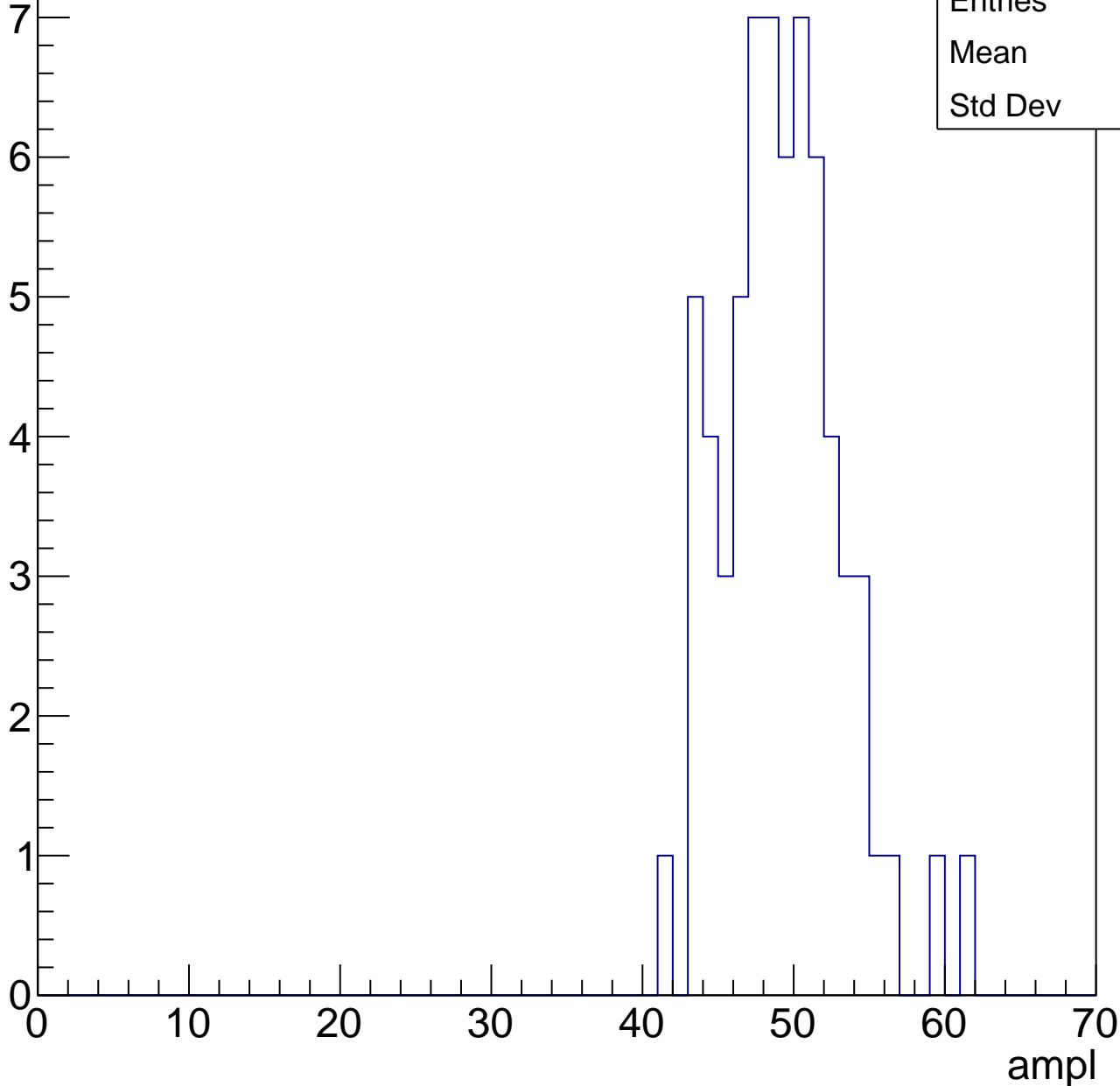


B1L103S, U5-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48.8
Std Dev	3.9

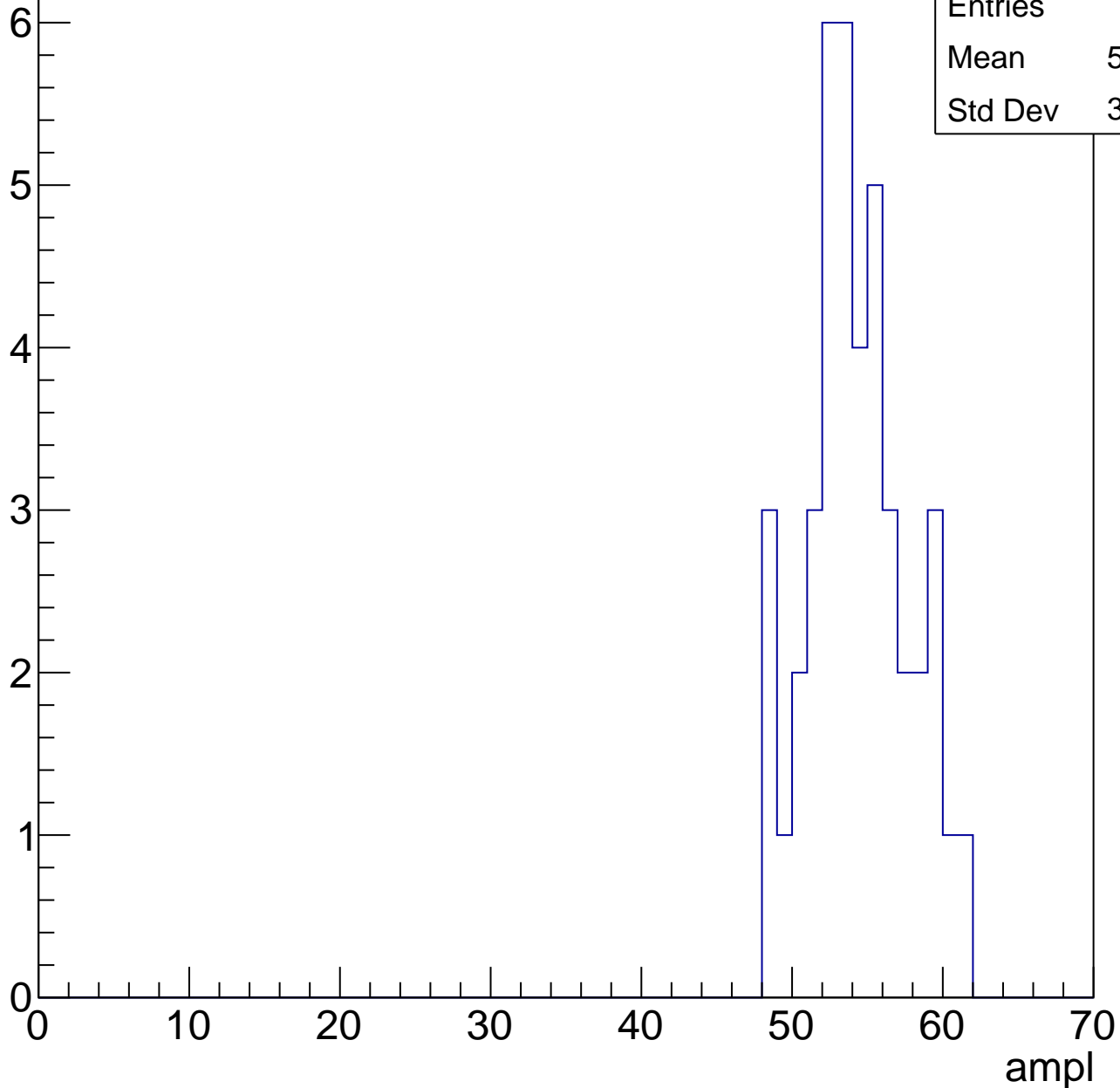


B1L103S, U5-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

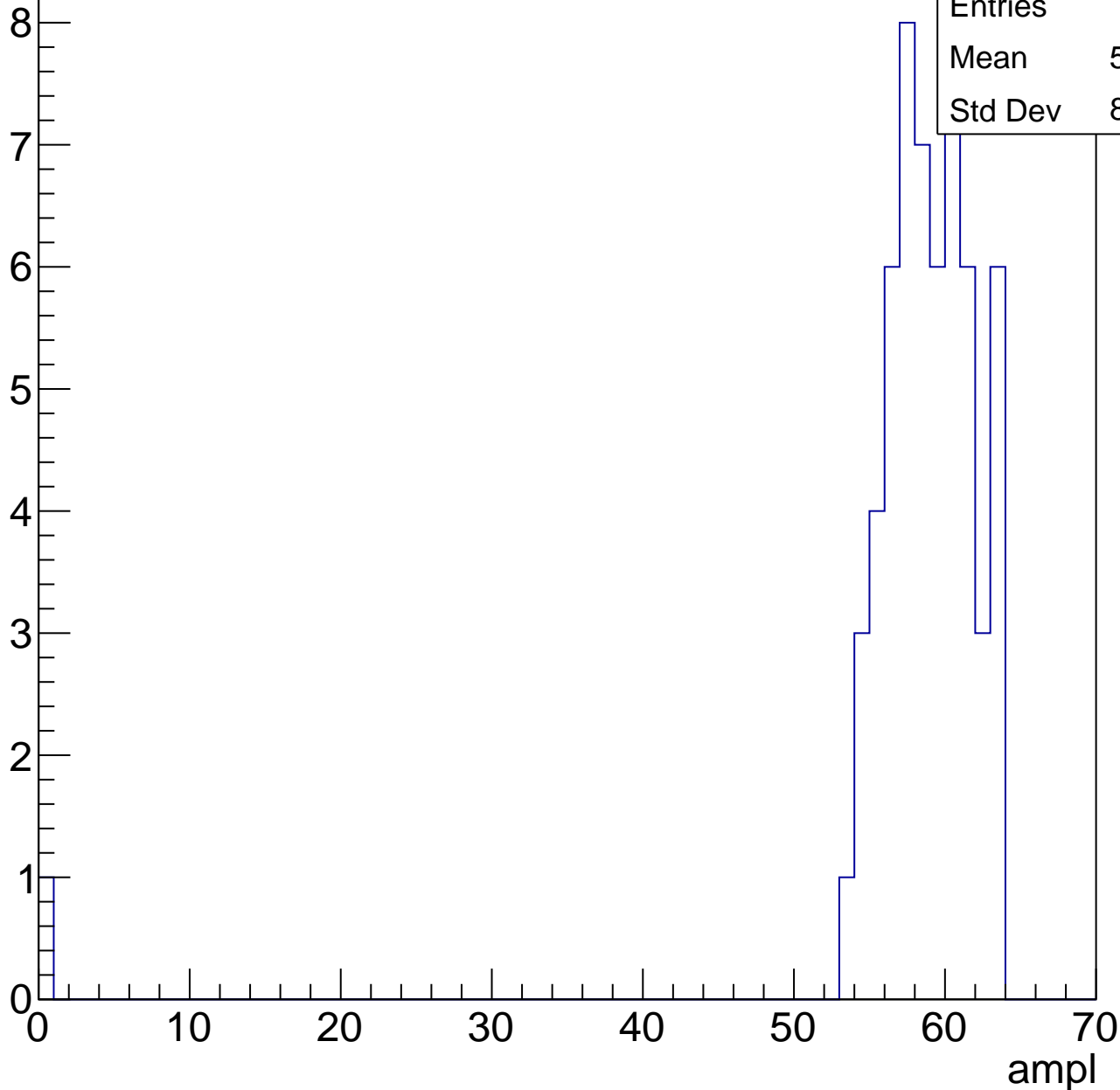
Entries	42
Mean	53.88
Std Dev	3.267



B1L103S, U5-ch8, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

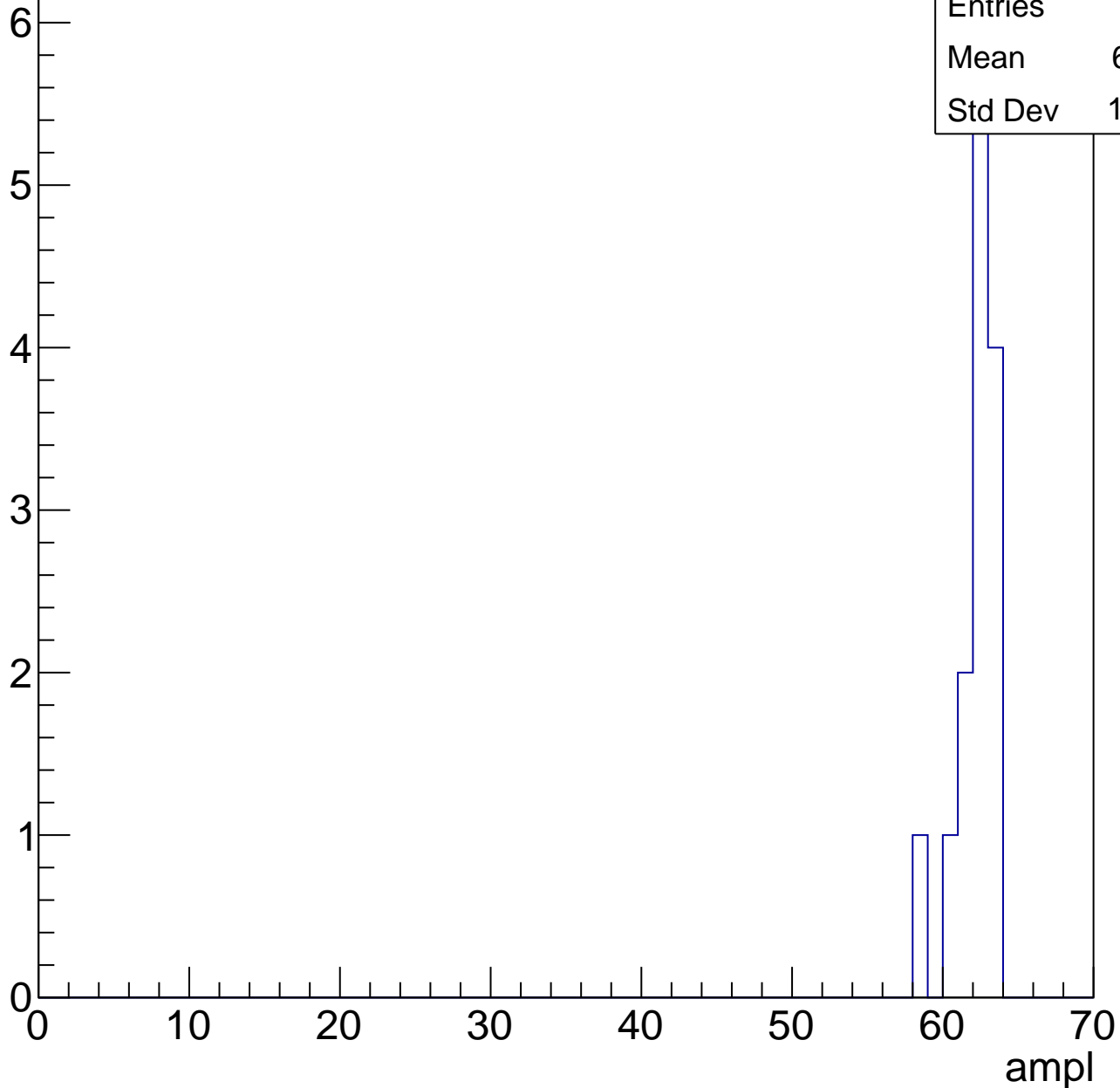


B1L103S, U5-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

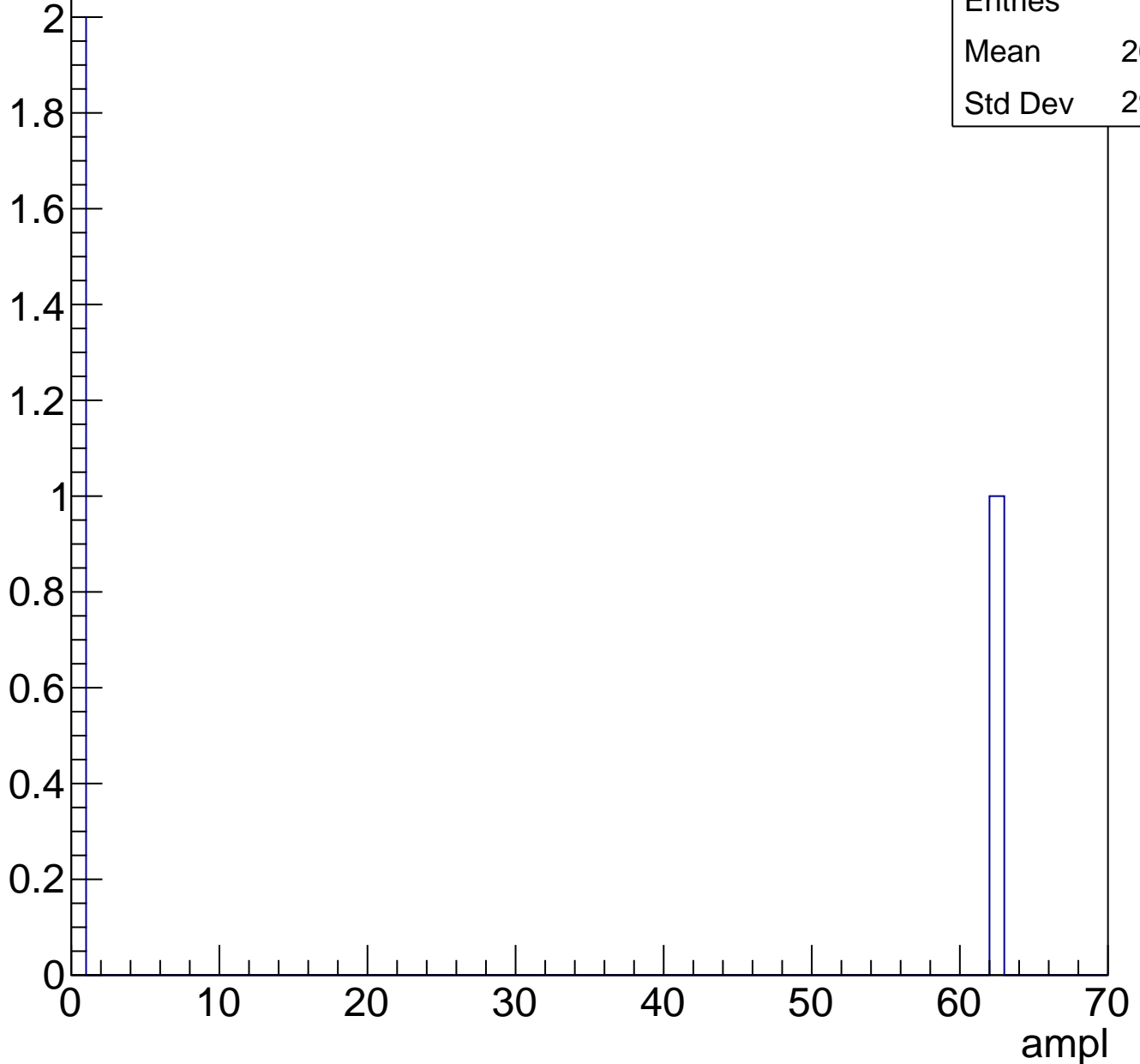
Entries	14
Mean	61.71
Std Dev	1.332



B1L103S, U5-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L103S, U5-ch9, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	28.25
Std Dev	4.697

Gaus mean : 29.2184

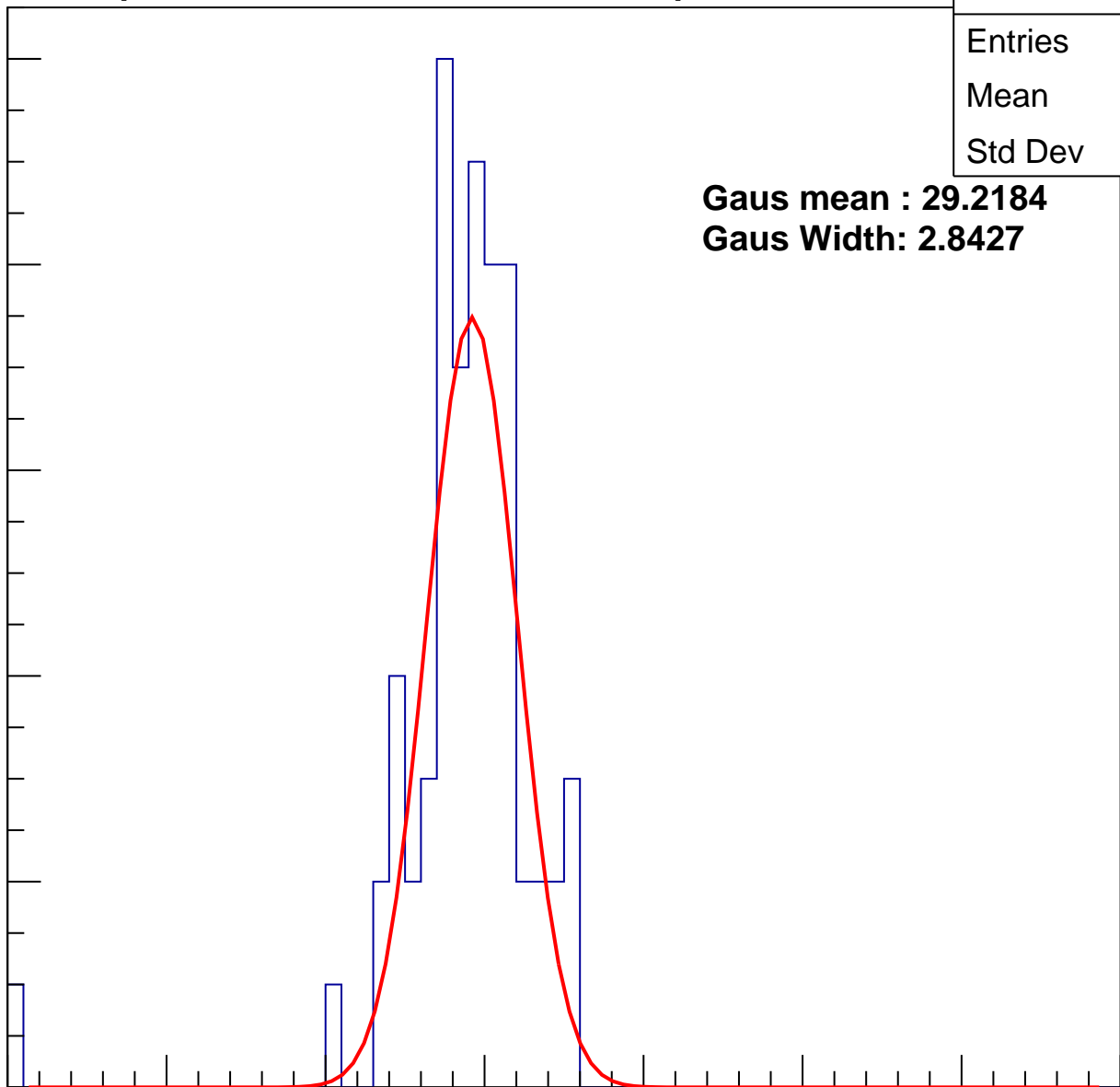
Gaus Width: 2.8427

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch9, adc1

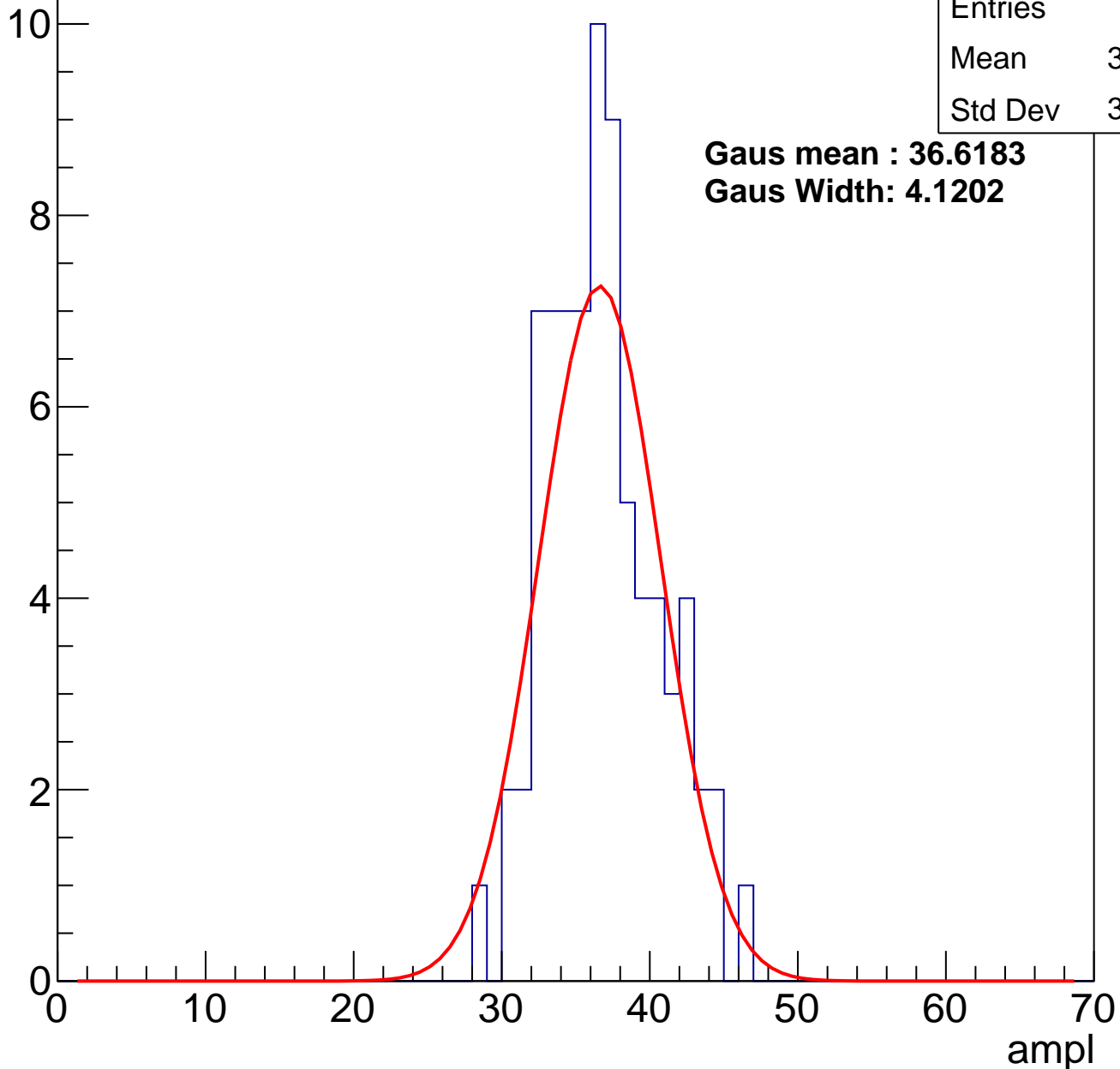
calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	36.34
Std Dev	3.712

Gaus mean : 36.6183

Gaus Width: 4.1202

Entry



B1L103S, U5-ch9, adc2

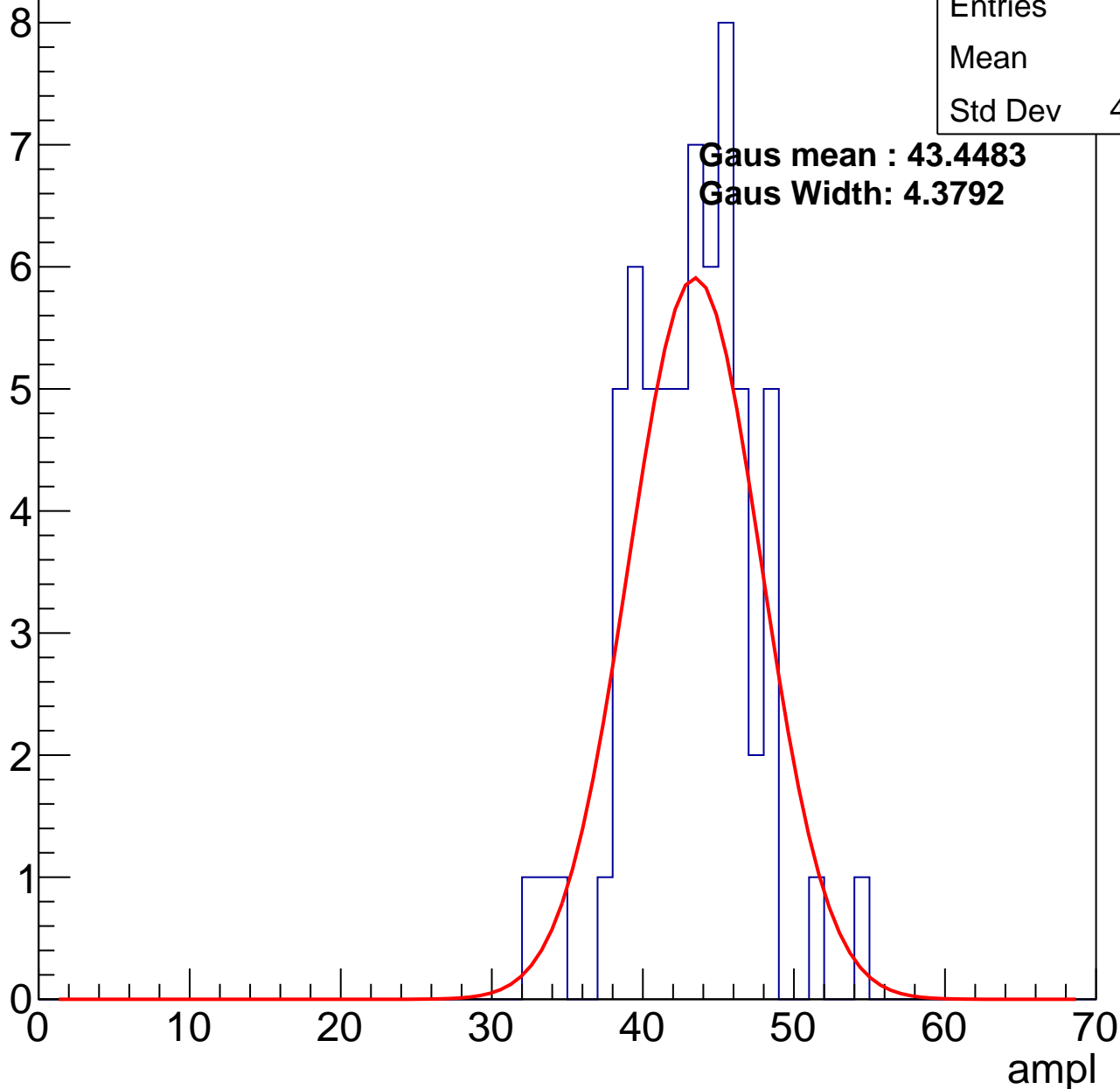
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.6
Std Dev	4.007

Gaus mean : 43.4483

Gaus Width: 4.3792



B1L103S, U5-ch9, adc3

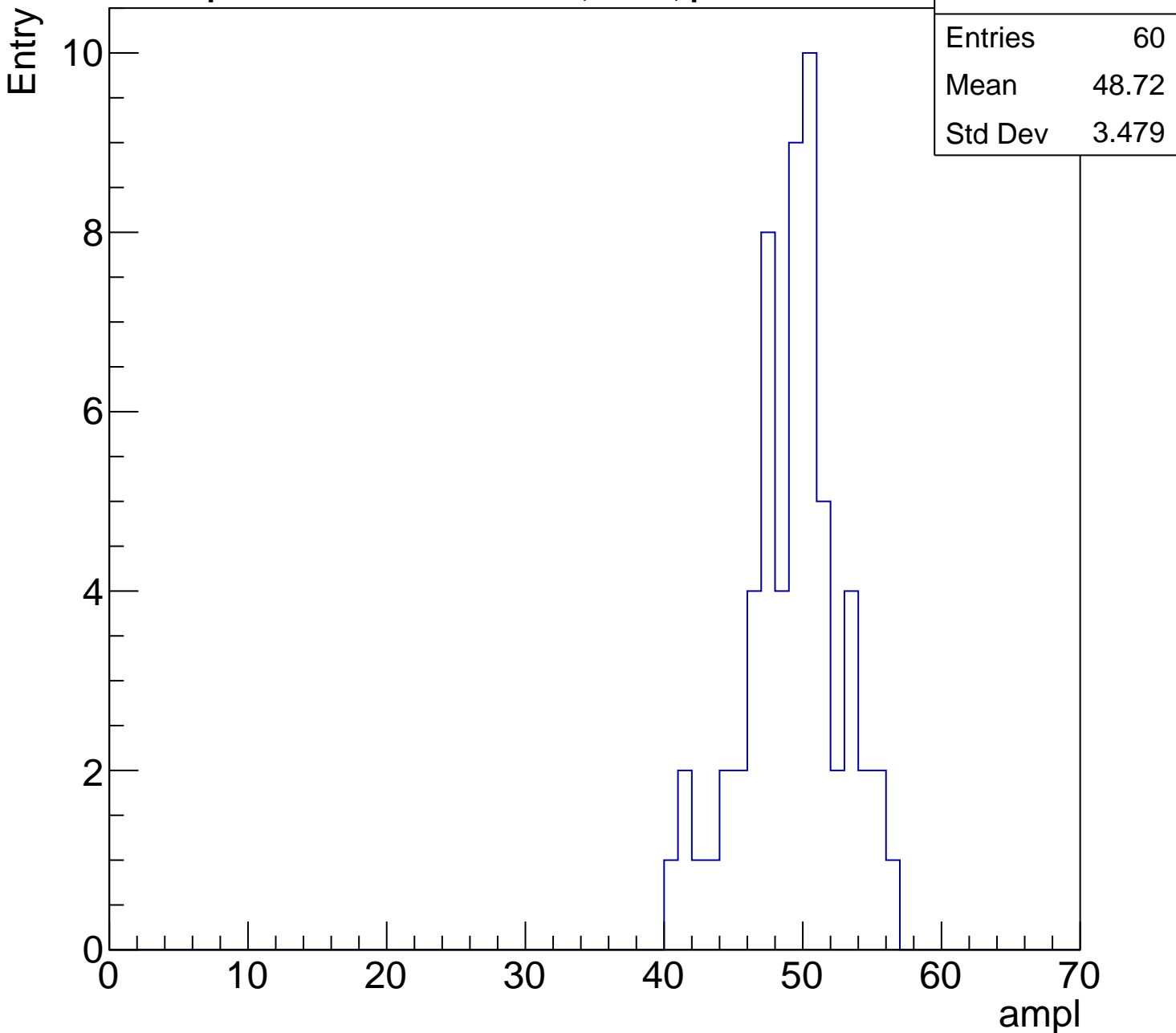
calib_packv5_042523_0143.root, FC#7, port C2

Entries	60
Mean	48.72
Std Dev	3.479

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

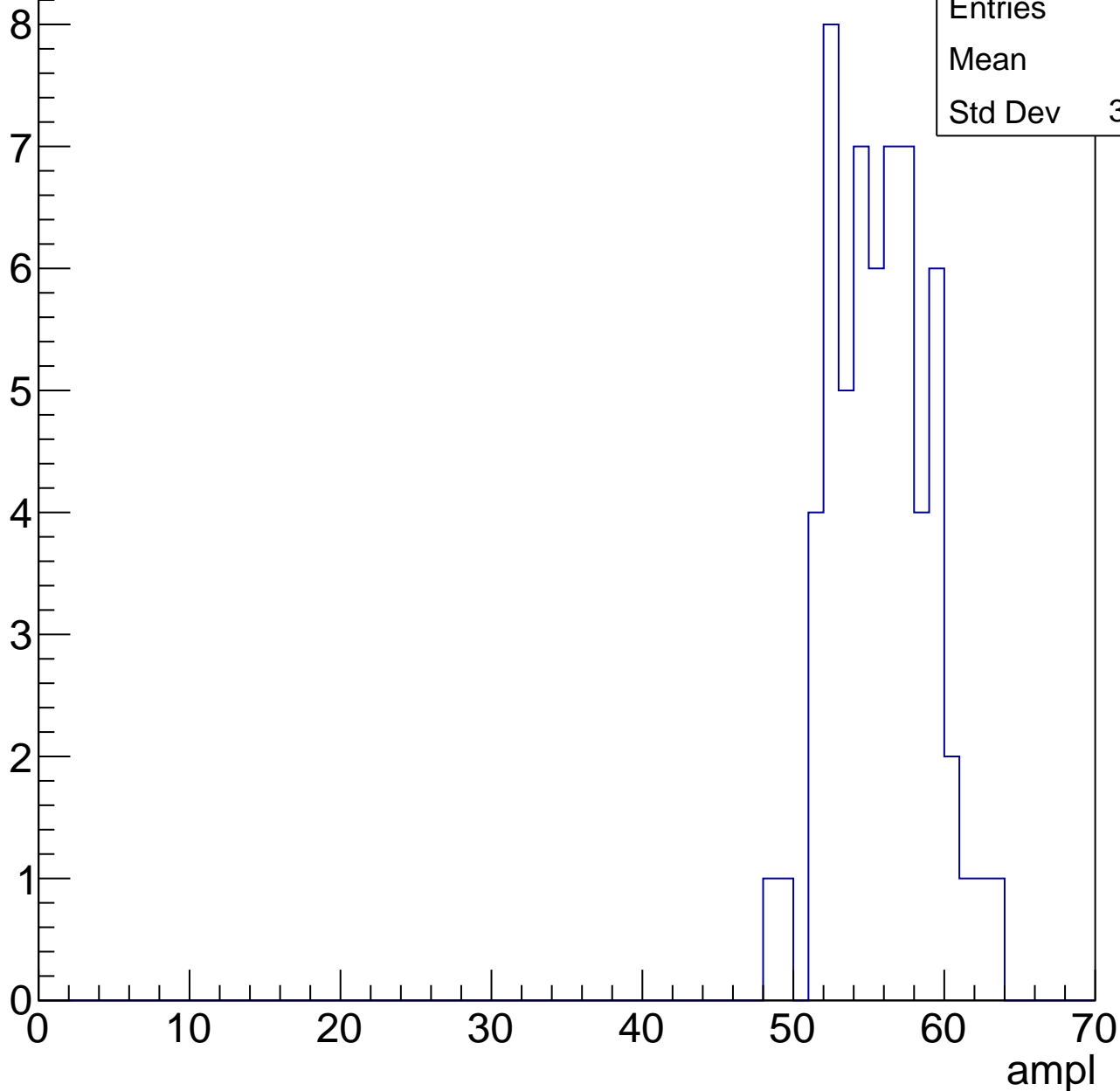


B1L103S, U5-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

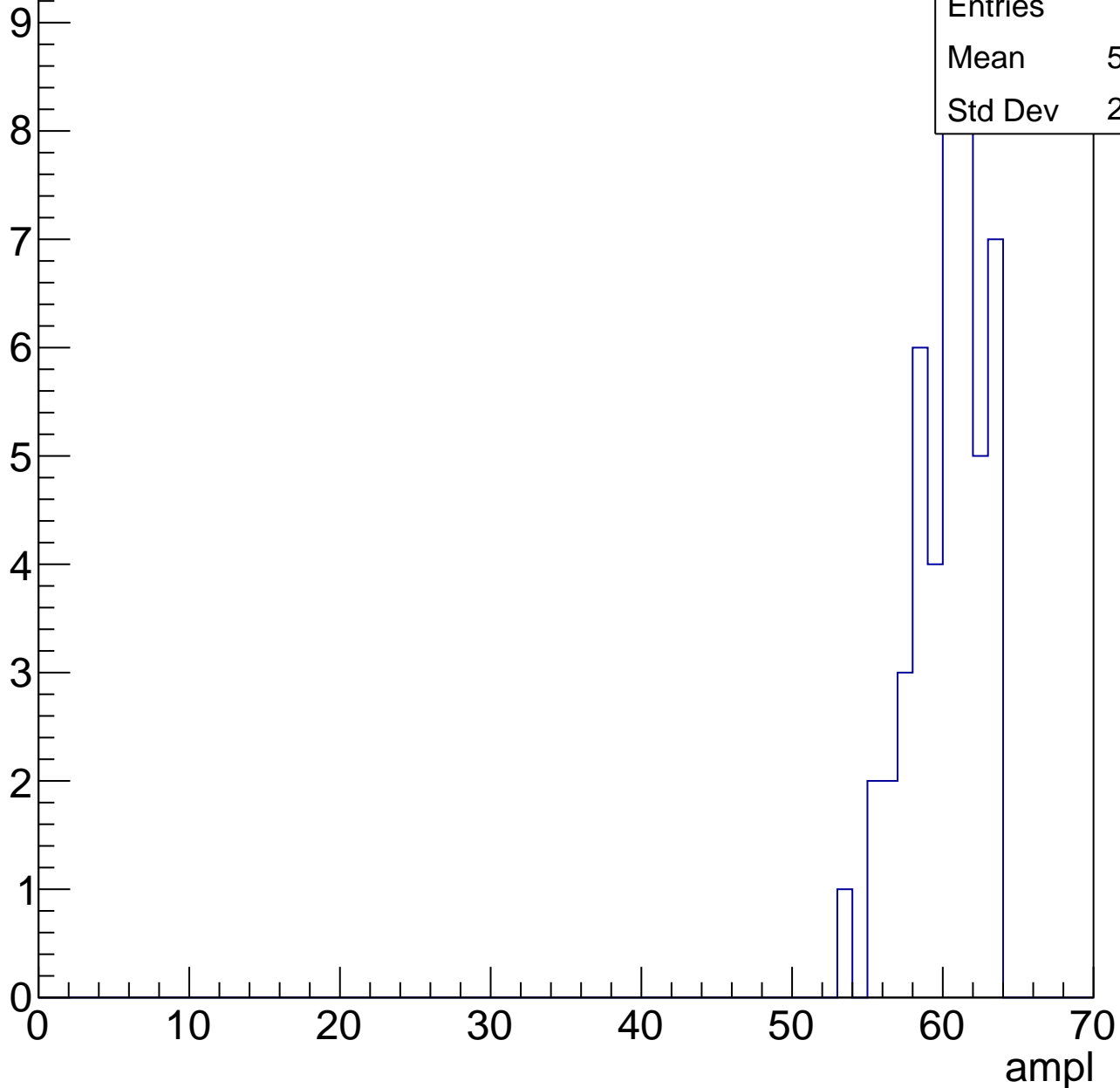
Entries	61
Mean	55.3
Std Dev	3.159



B1L103S, U5-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

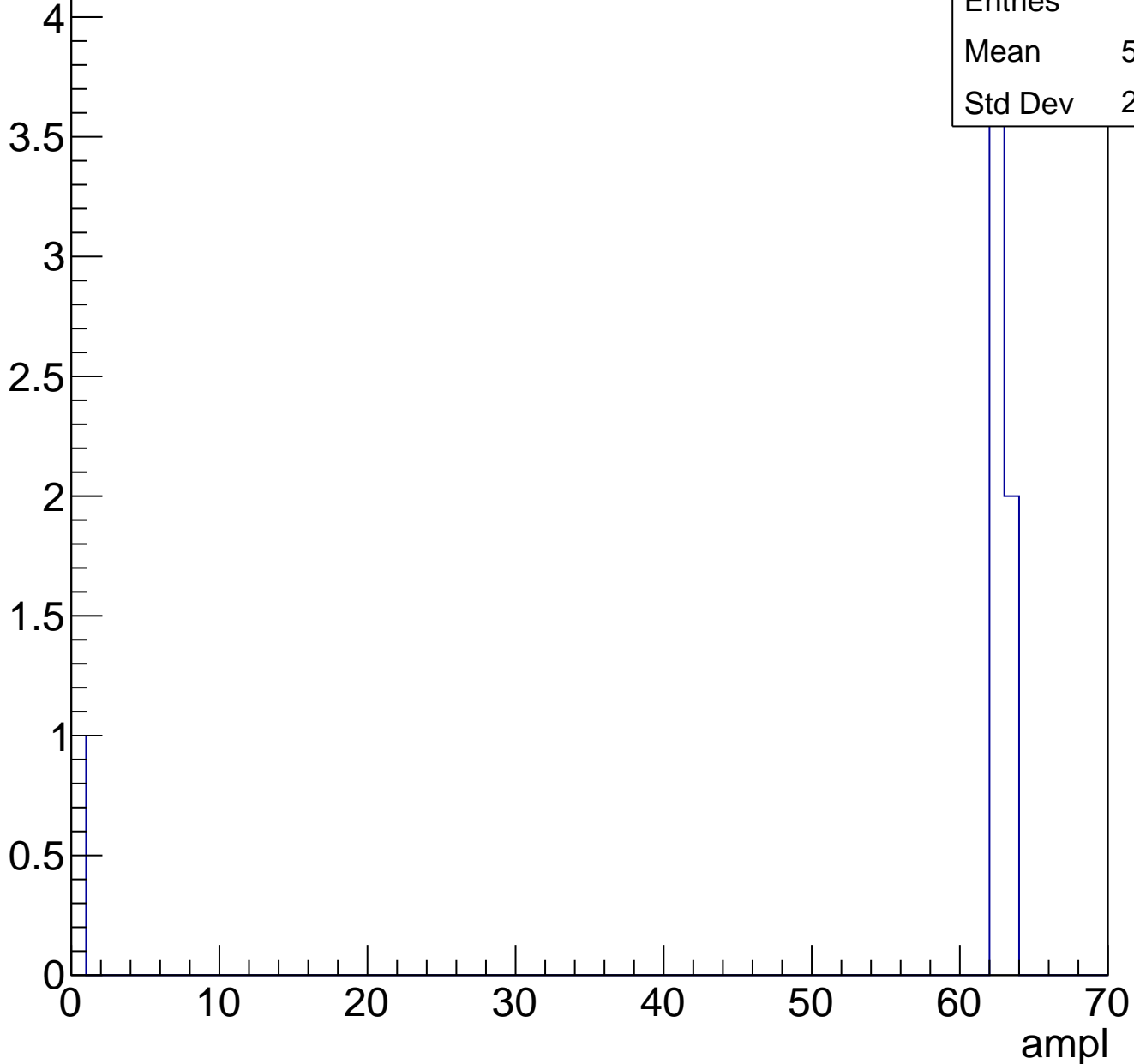
Entry



B1L103S, U5-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch10, adc0

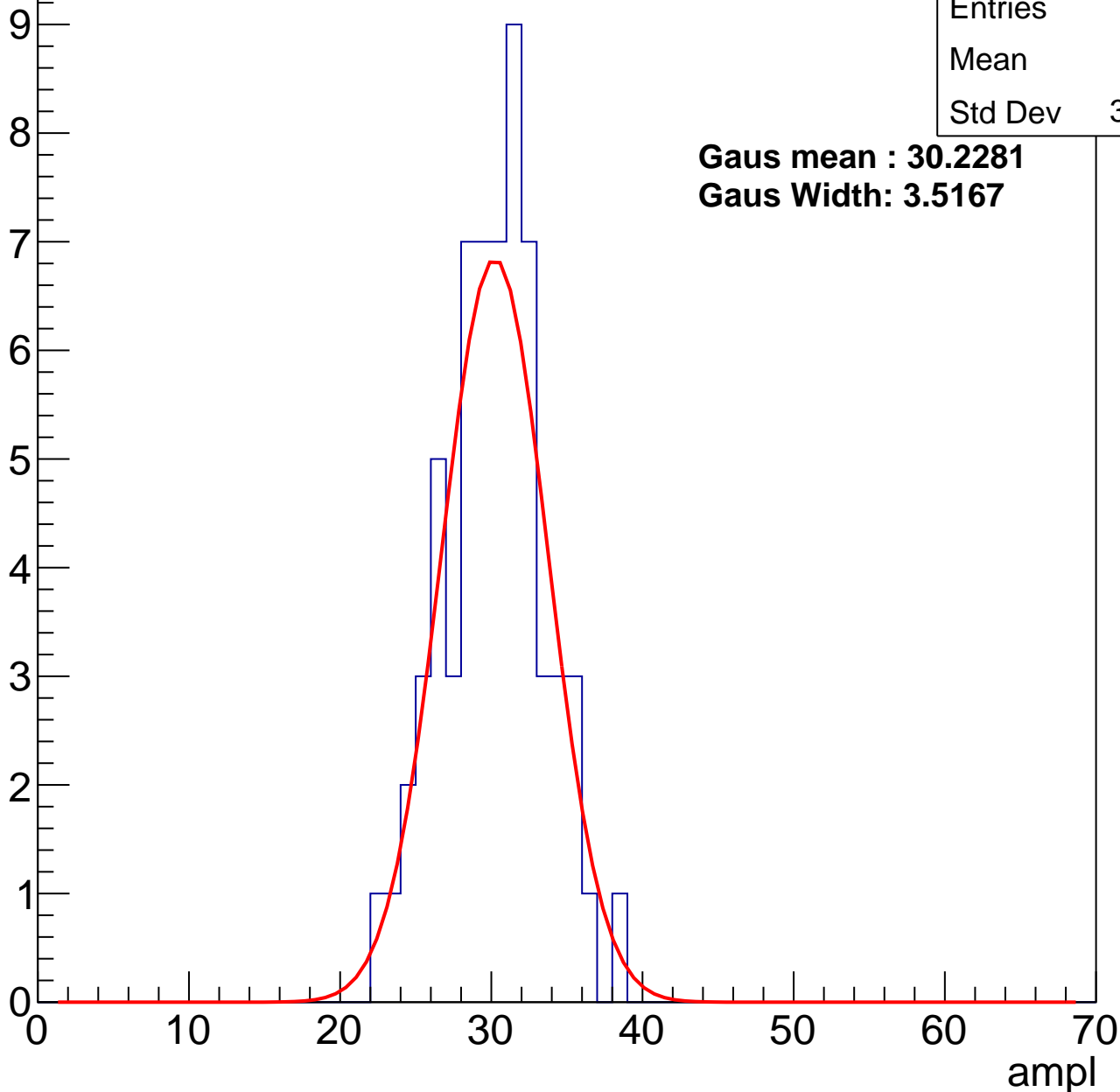
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.7
Std Dev	3.293

Gaus mean : 30.2281

Gaus Width: 3.5167



B1L103S, U5-ch10, adc1

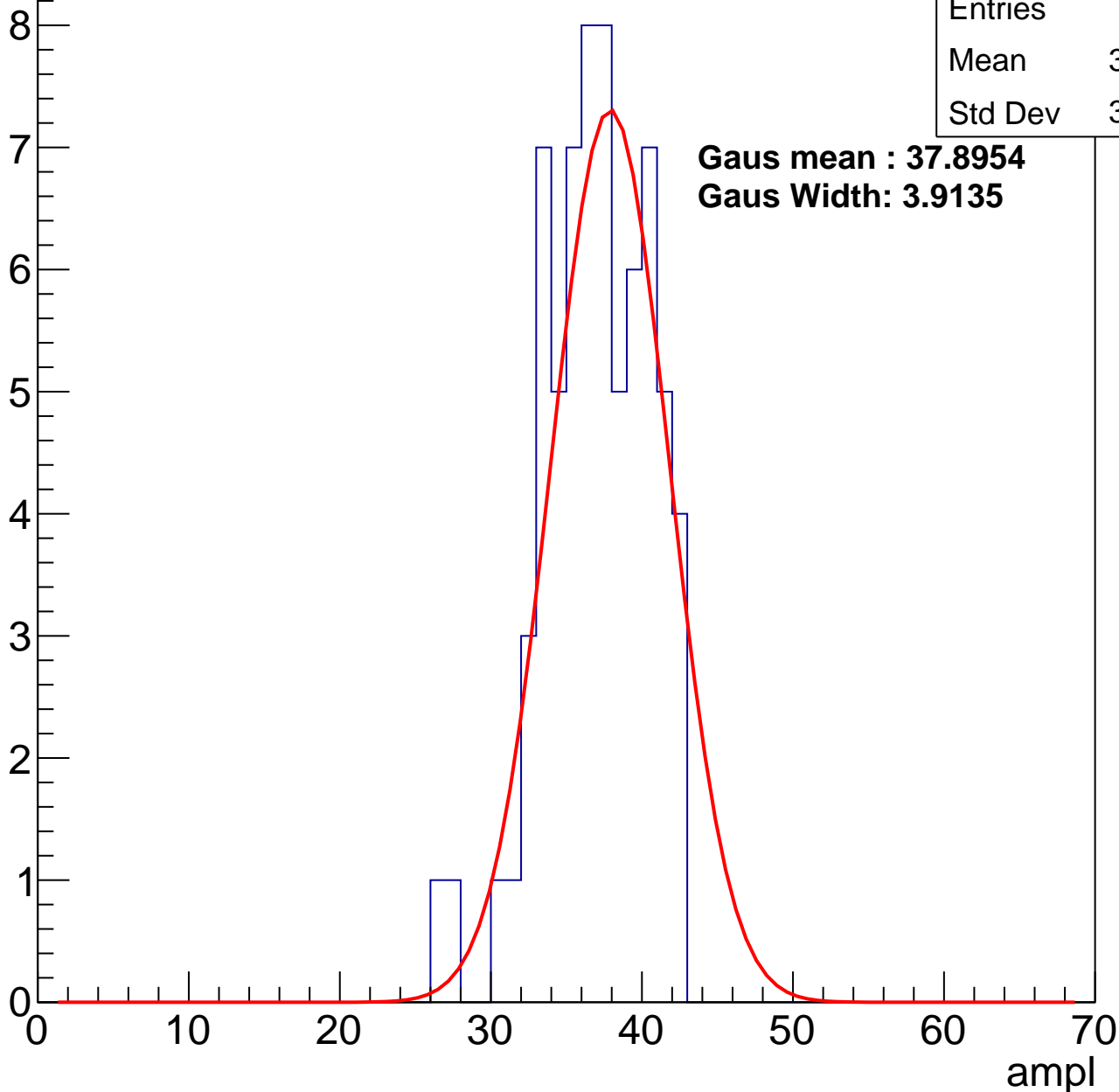
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.48
Std Dev	3.462

Gaus mean : 37.8954

Gaus Width: 3.9135



B1L103S, U5-ch10, adc2

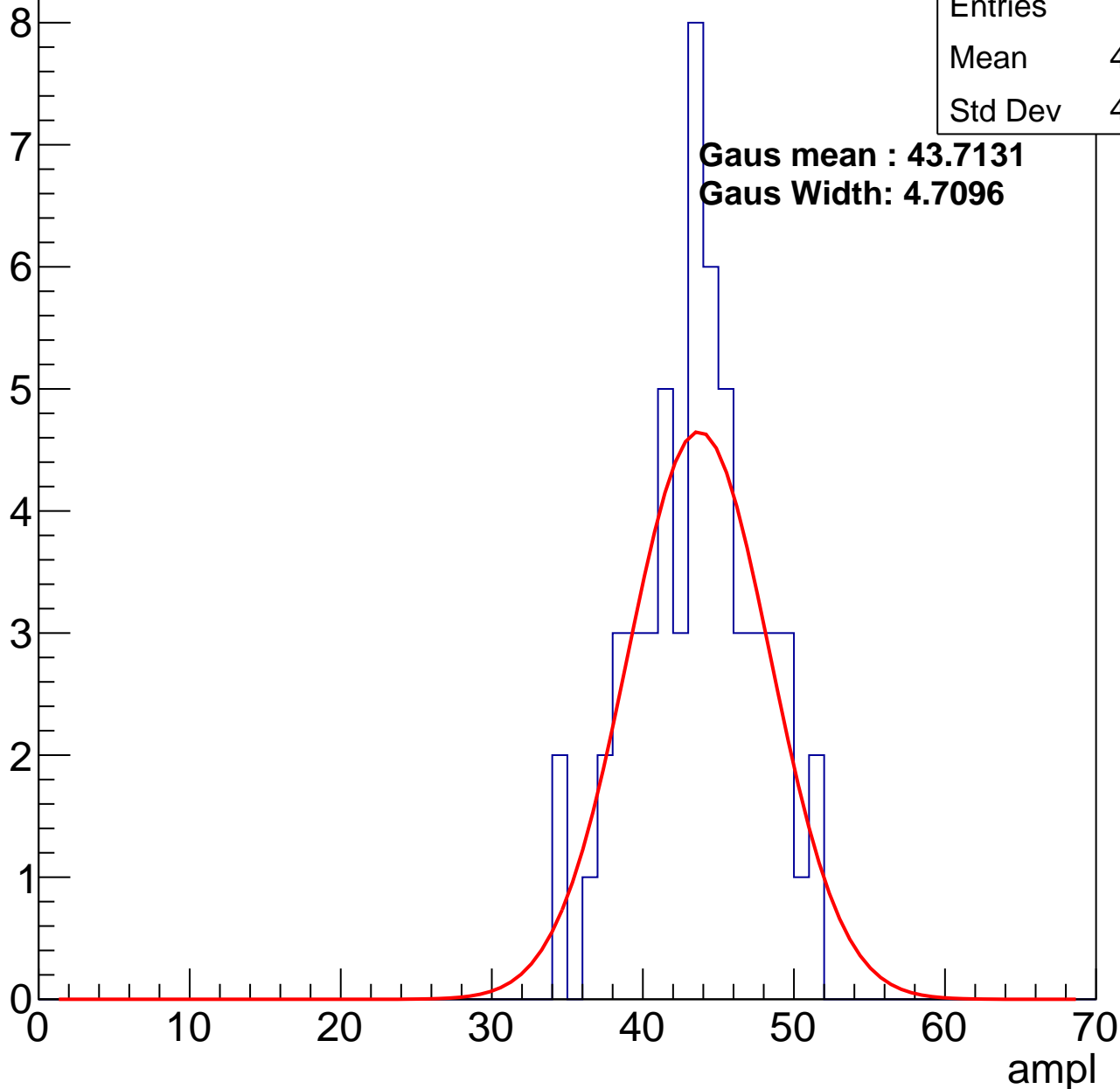
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.12
Std Dev	4.045

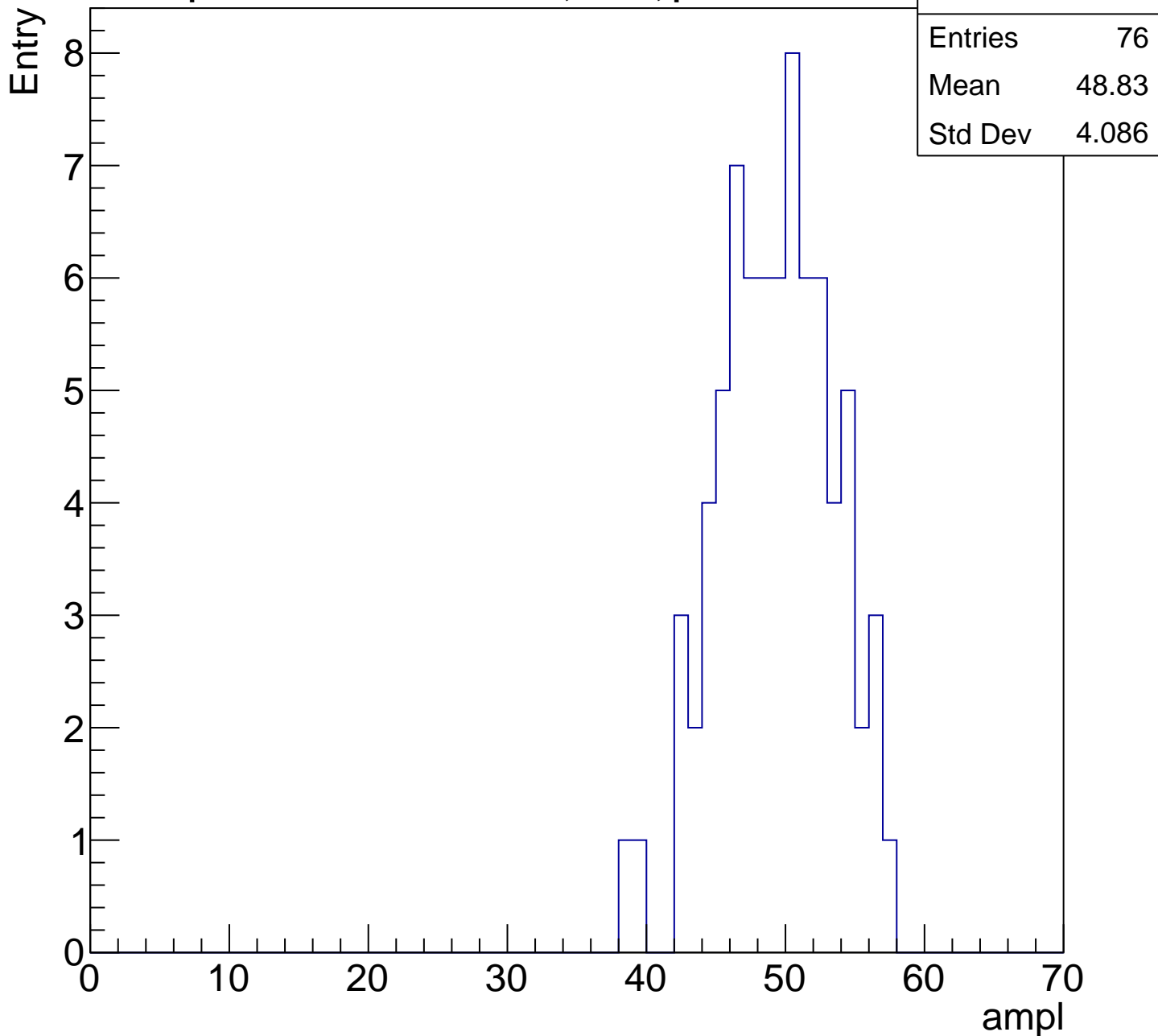
Gaus mean : 43.7131

Gaus Width: 4.7096



B1L103S, U5-ch10, adc3

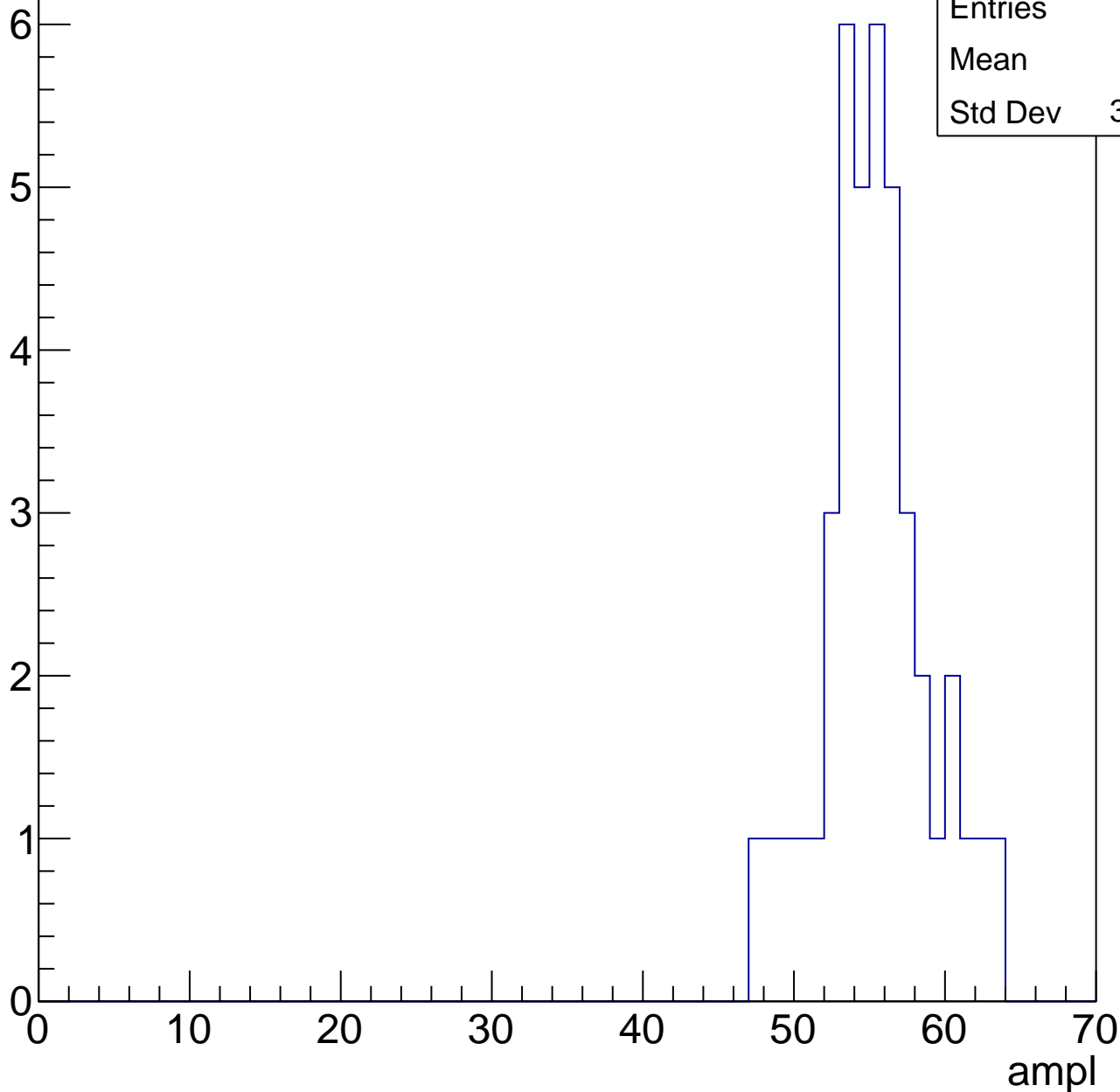
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

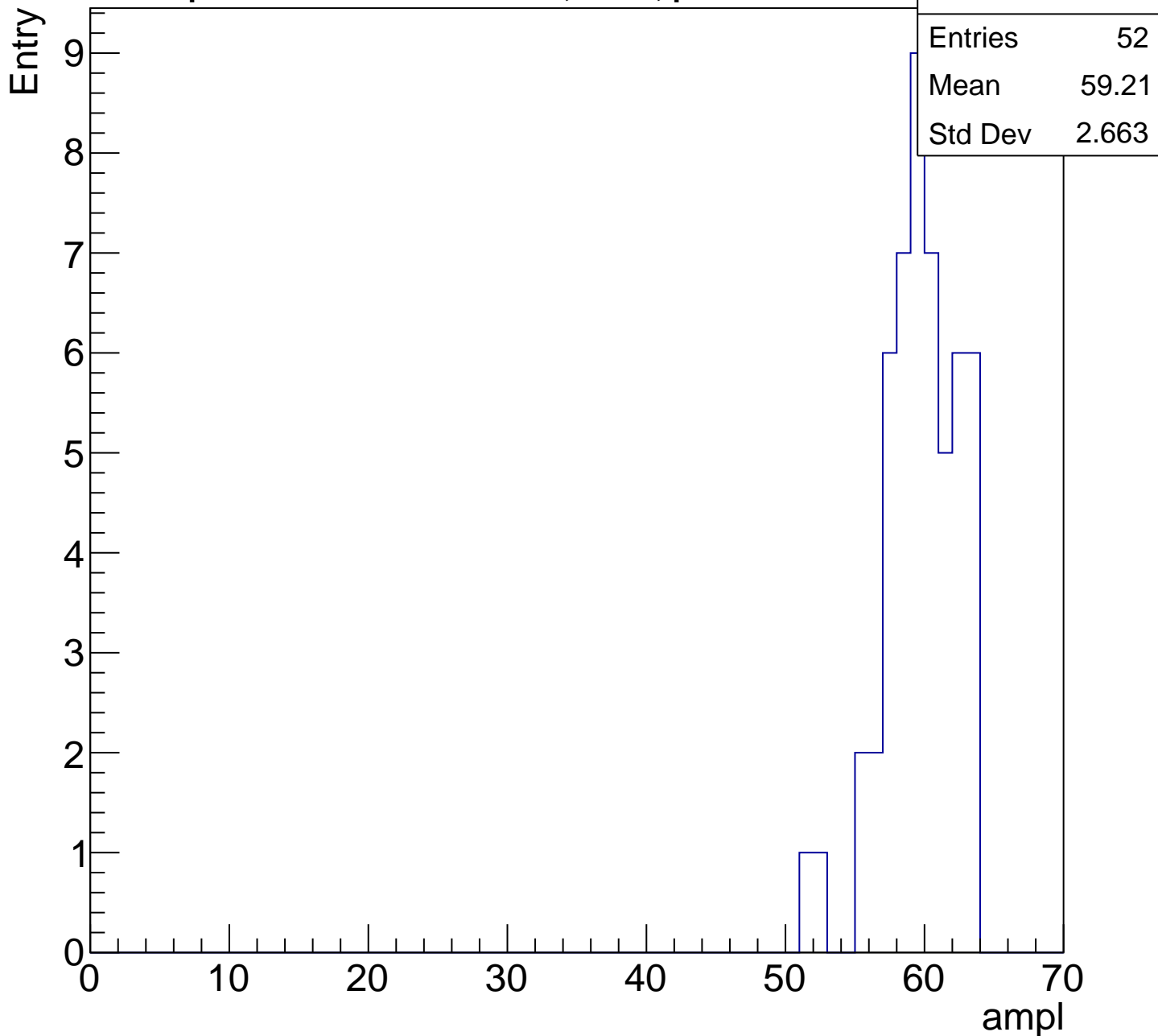
Entry



Entries	41
Mean	54.9
Std Dev	3.477

B1L103S, U5-ch10, adc5

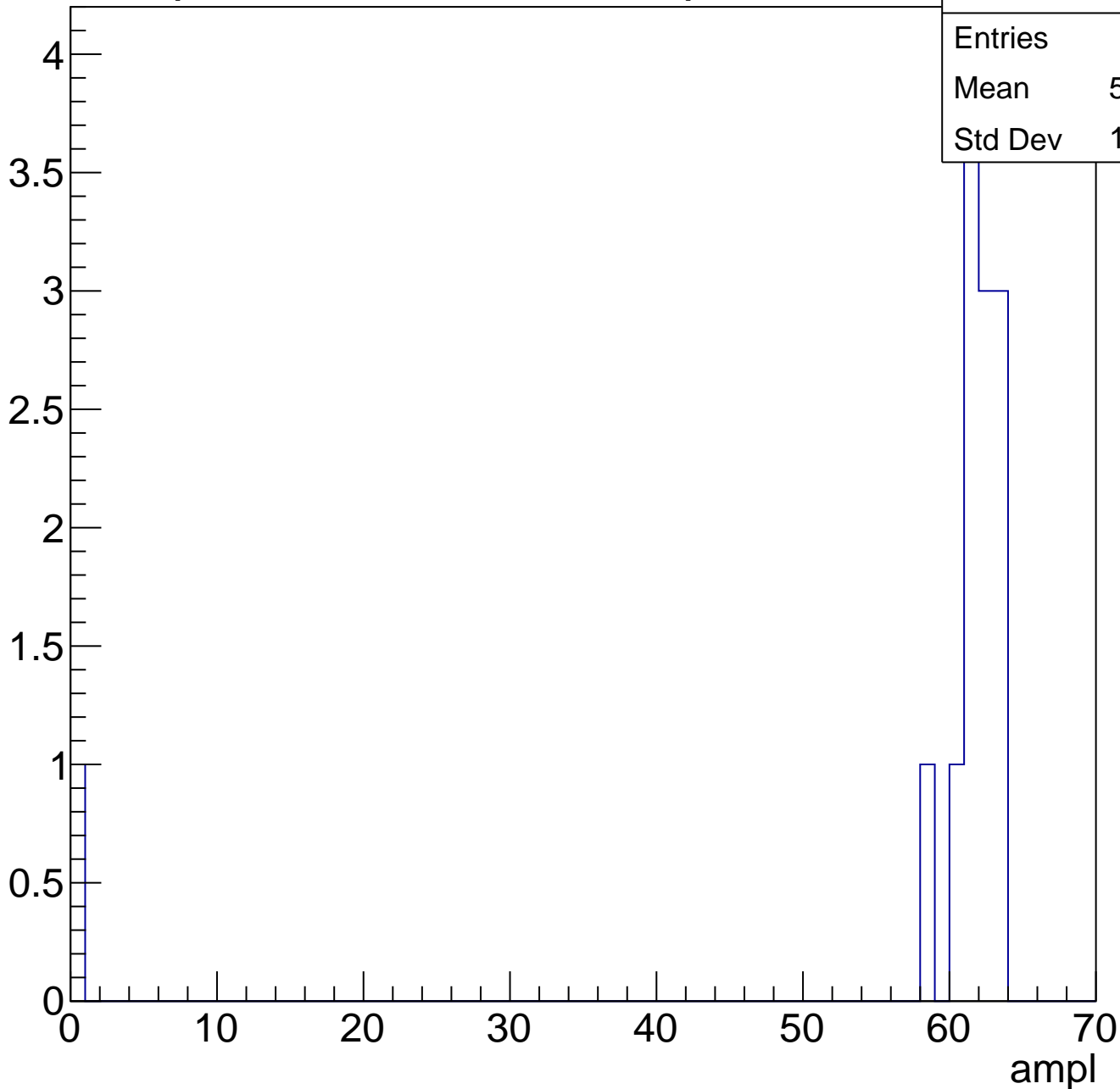
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch11, adc0

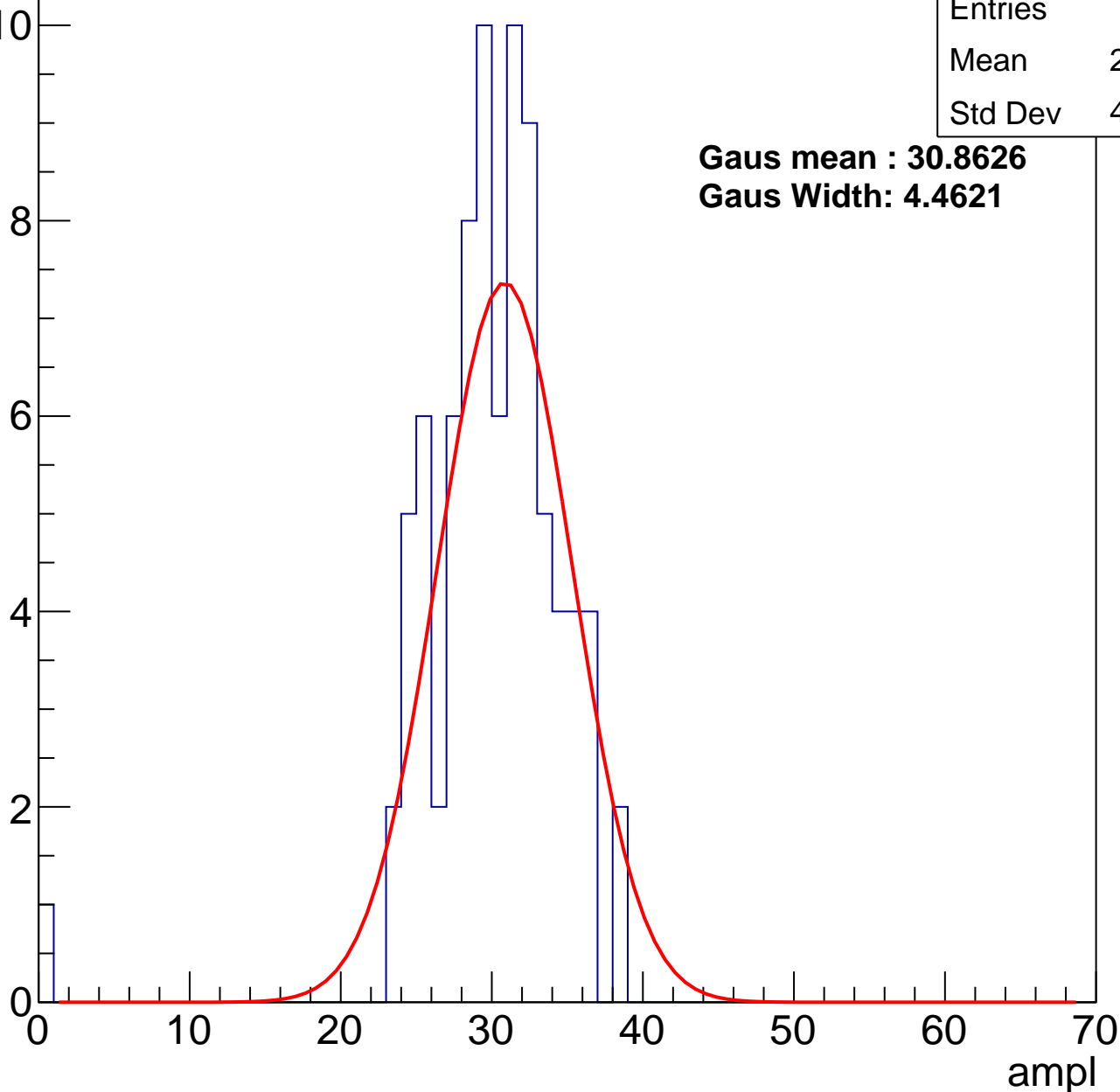
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	29.56
Std Dev	4.839

Gaus mean : 30.8626

Gaus Width: 4.4621



B1L103S, U5-ch11, adc1

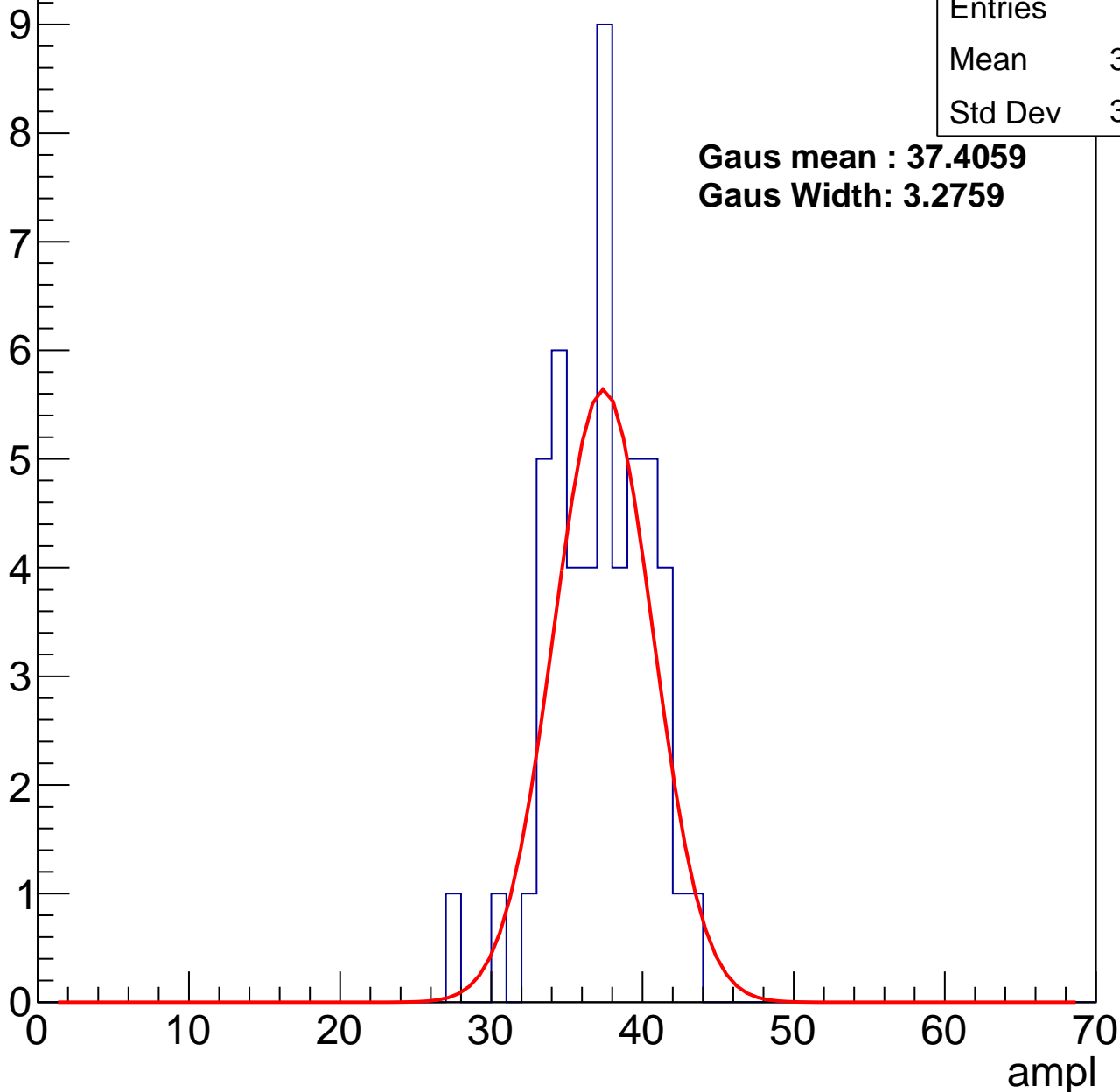
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	36.69
Std Dev	3.184

Gaus mean : 37.4059

Gaus Width: 3.2759



B1L103S, U5-ch11, adc2

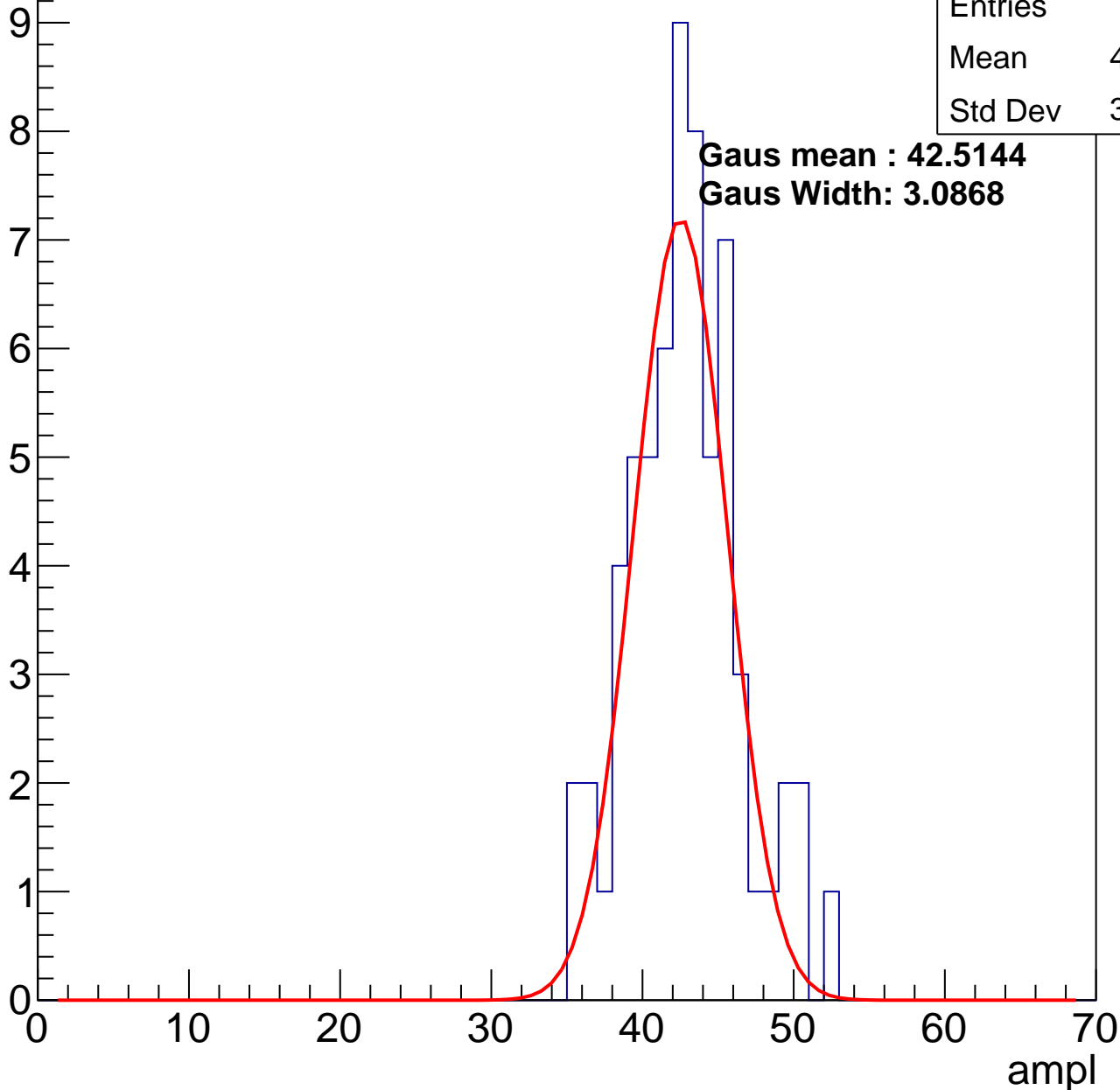
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.38
Std Dev	3.638

Gaus mean : 42.5144

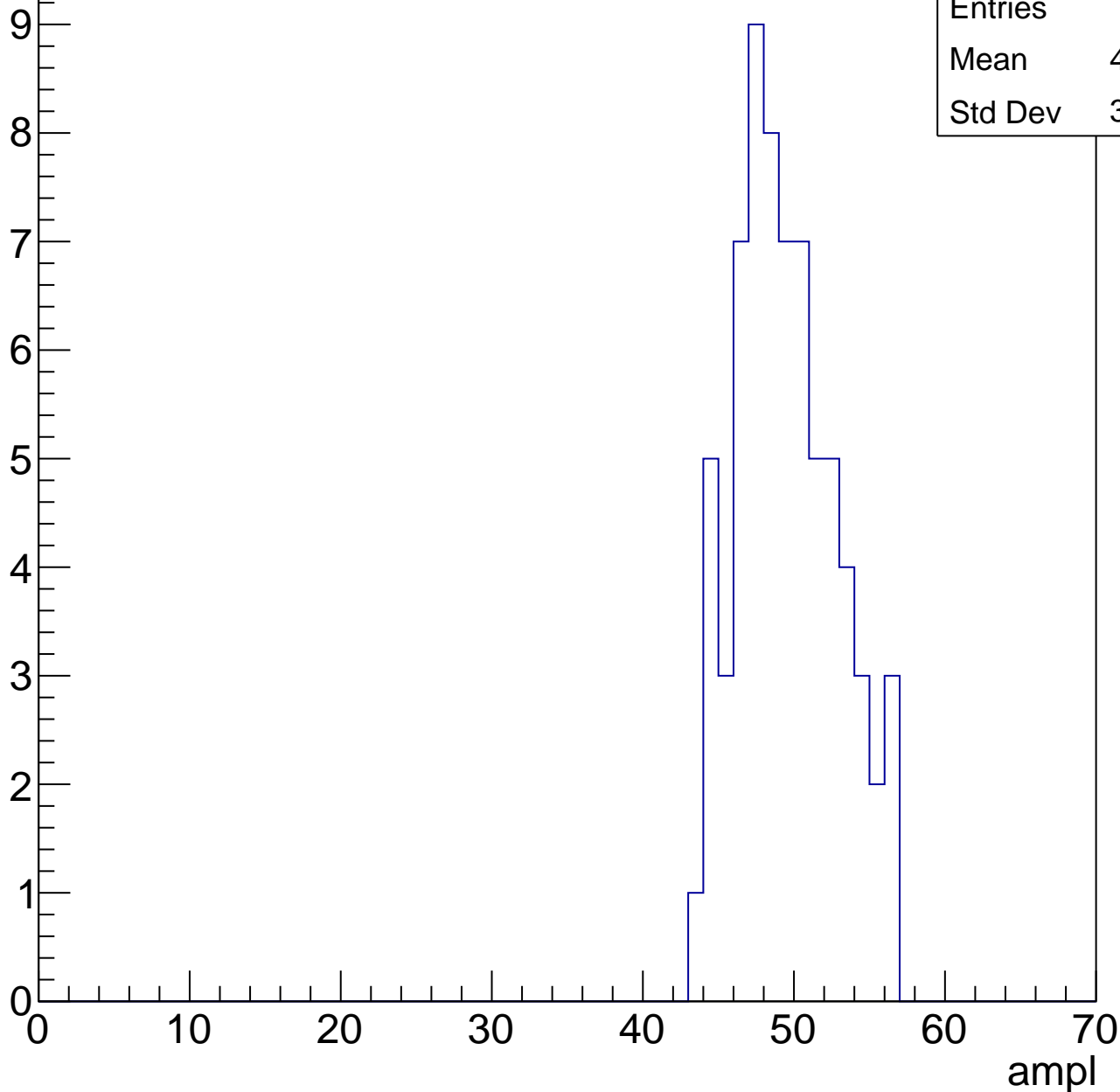
Gaus Width: 3.0868



B1L103S, U5-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

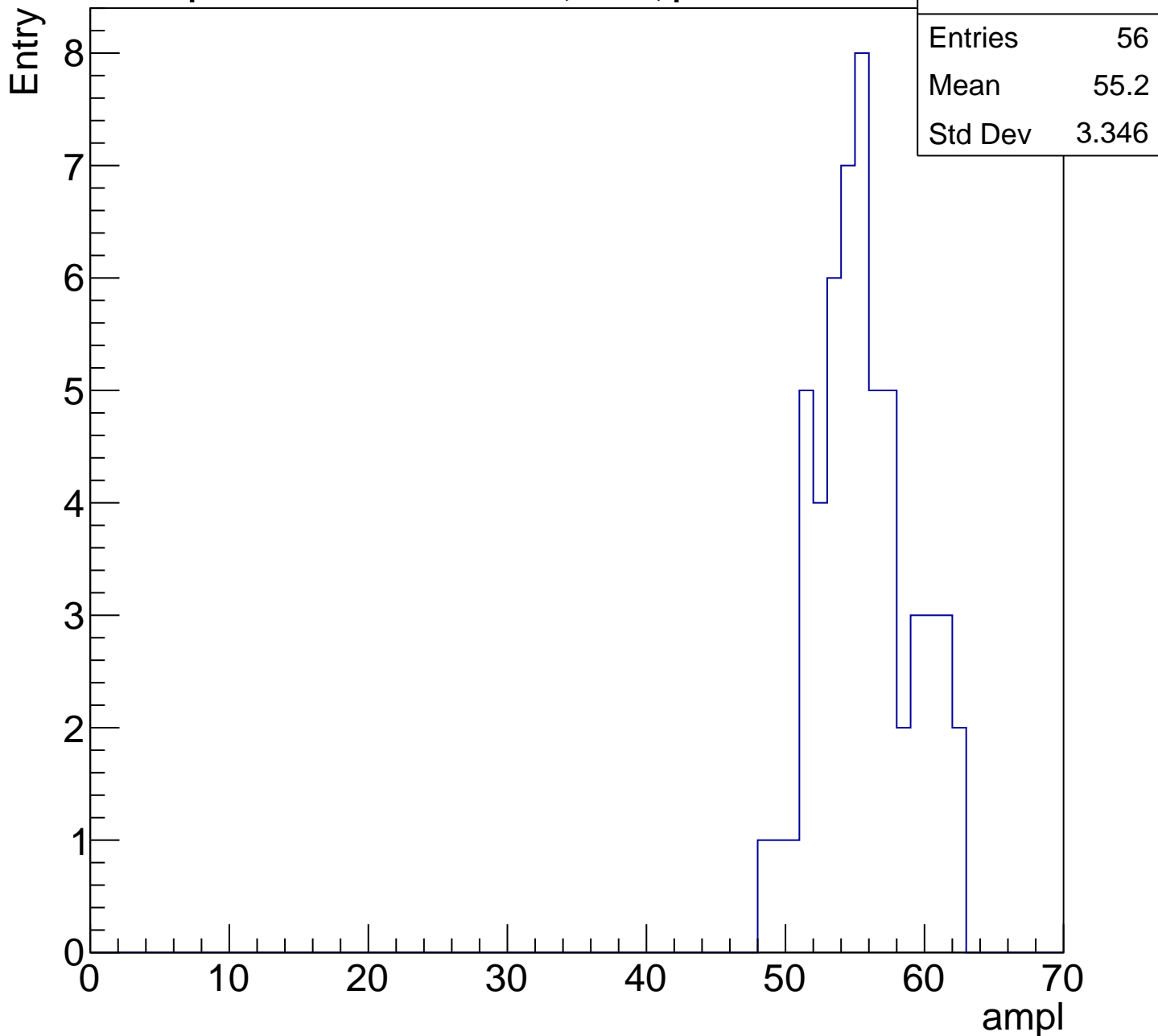
Entry



Entries	69
Mean	49.09
Std Dev	3.287

B1L103S, U5-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	45
Mean	58.44
Std Dev	9.047

ampl

10

20

30

40

50

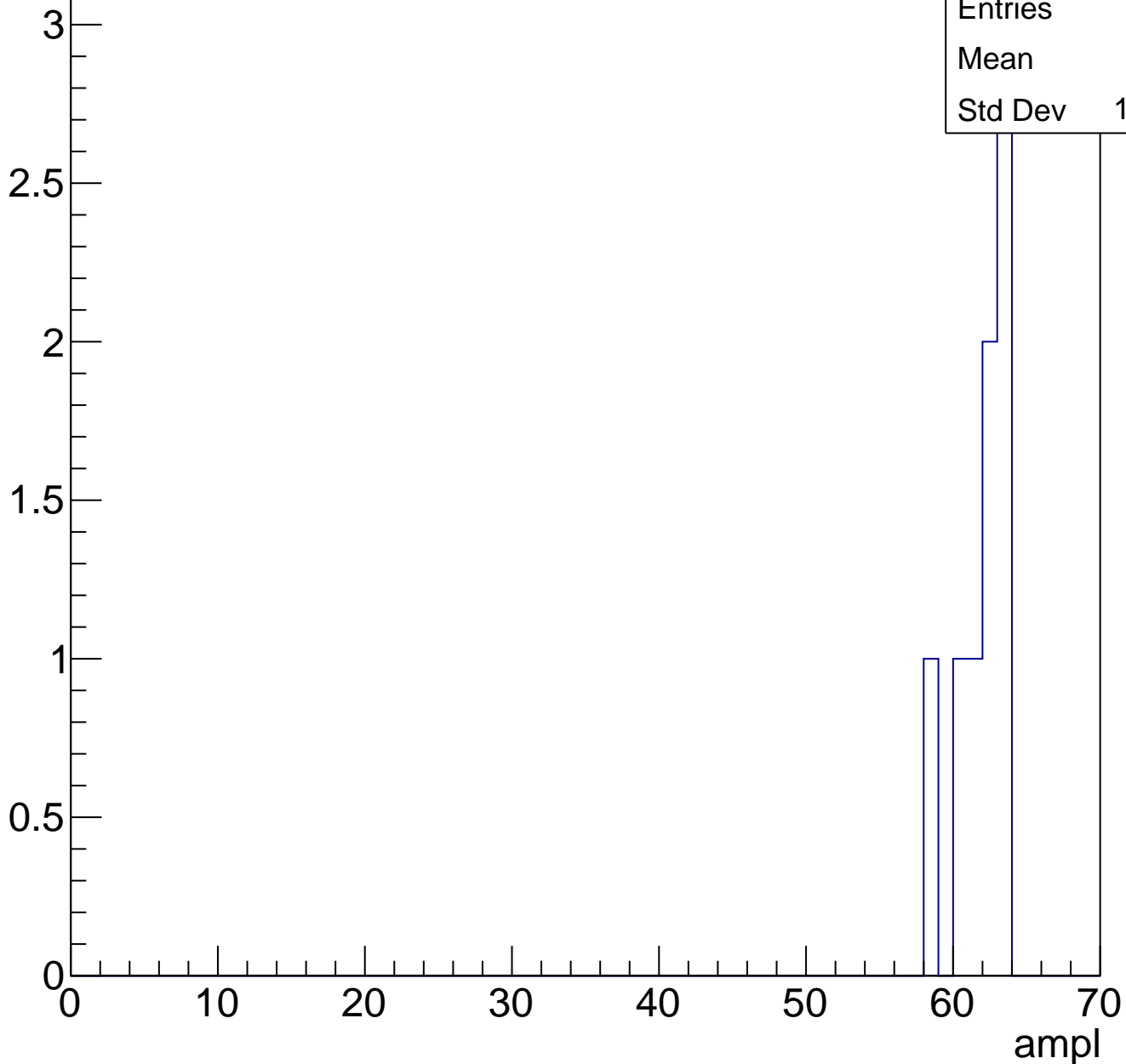
60

70

B1L103S, U5-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch12, adc0

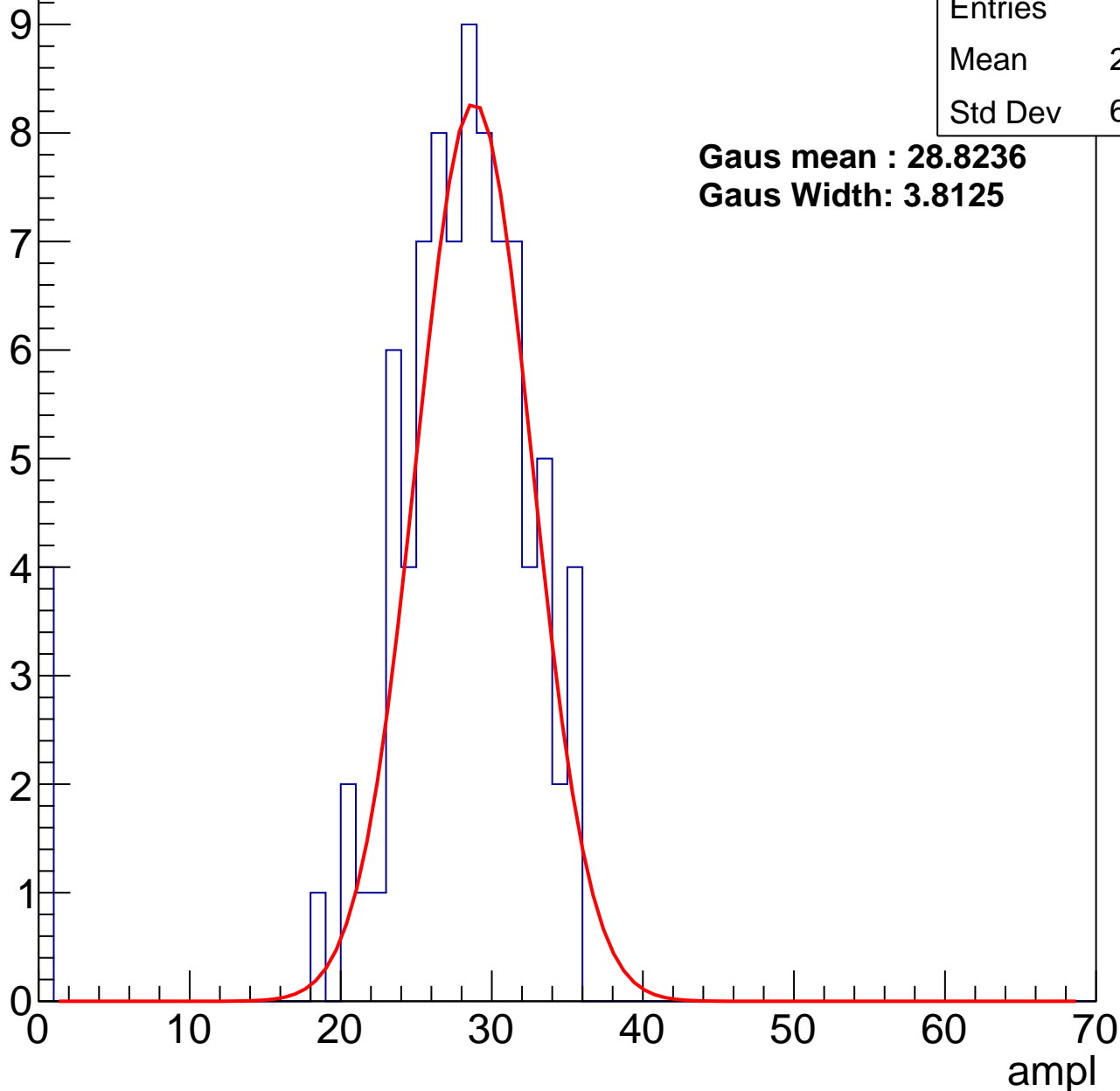
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	26.66
Std Dev	6.918

Gaus mean : 28.8236

Gaus Width: 3.8125



B1L103S, U5-ch12, adc1

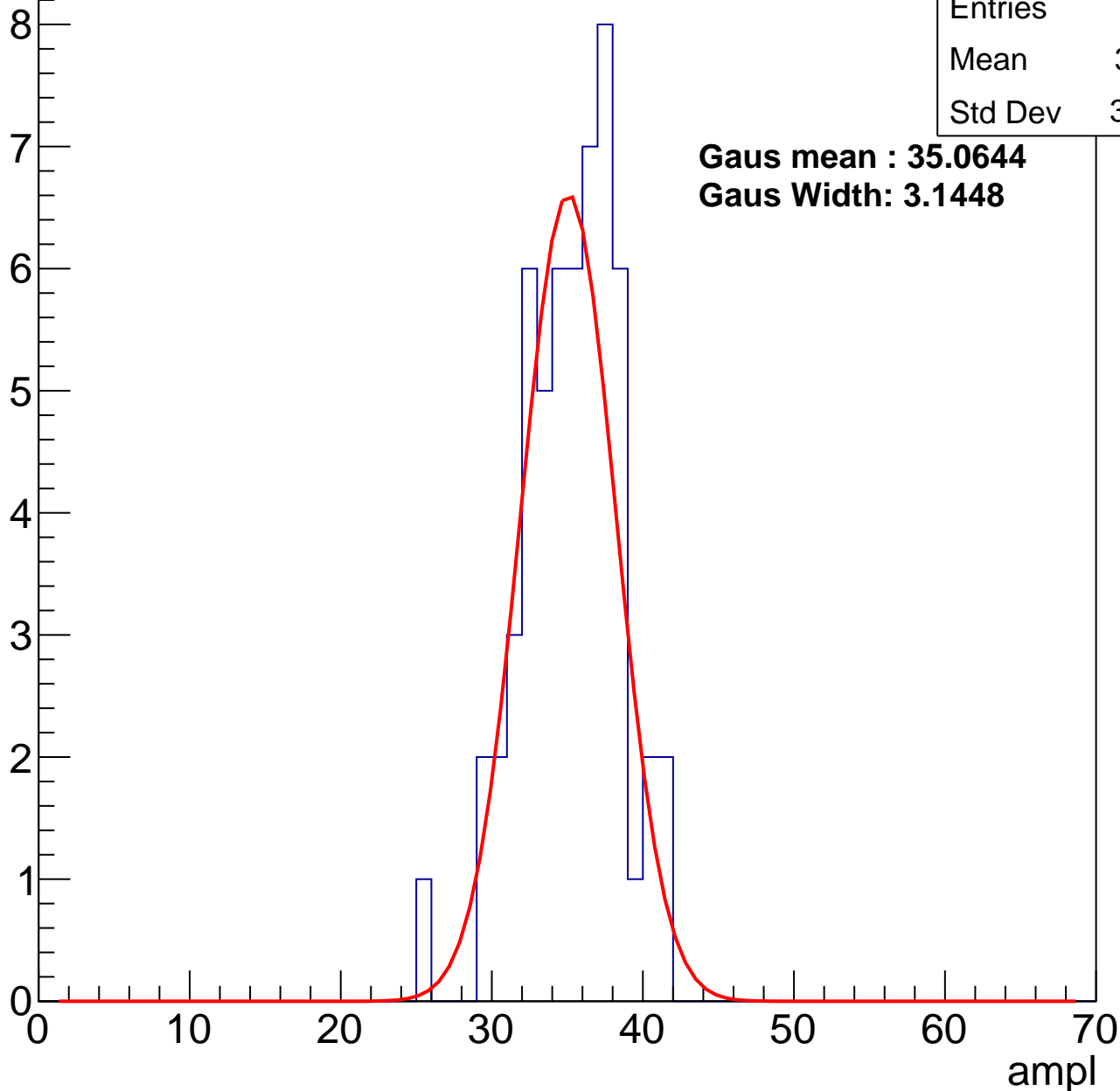
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	34.81
Std Dev	3.187

Gaus mean : 35.0644

Gaus Width: 3.1448



B1L103S, U5-ch12, adc2

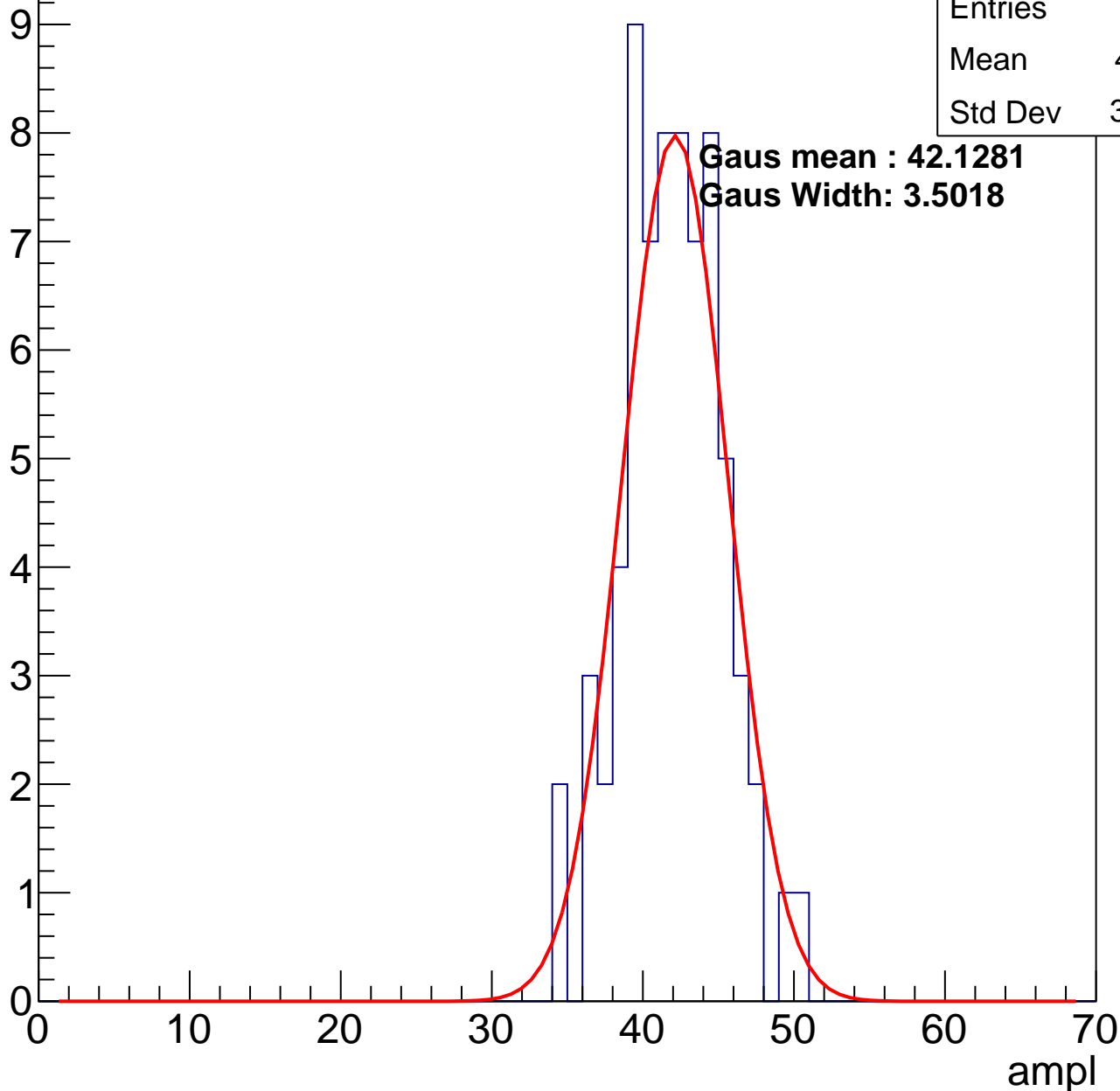
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	41.51
Std Dev	3.263

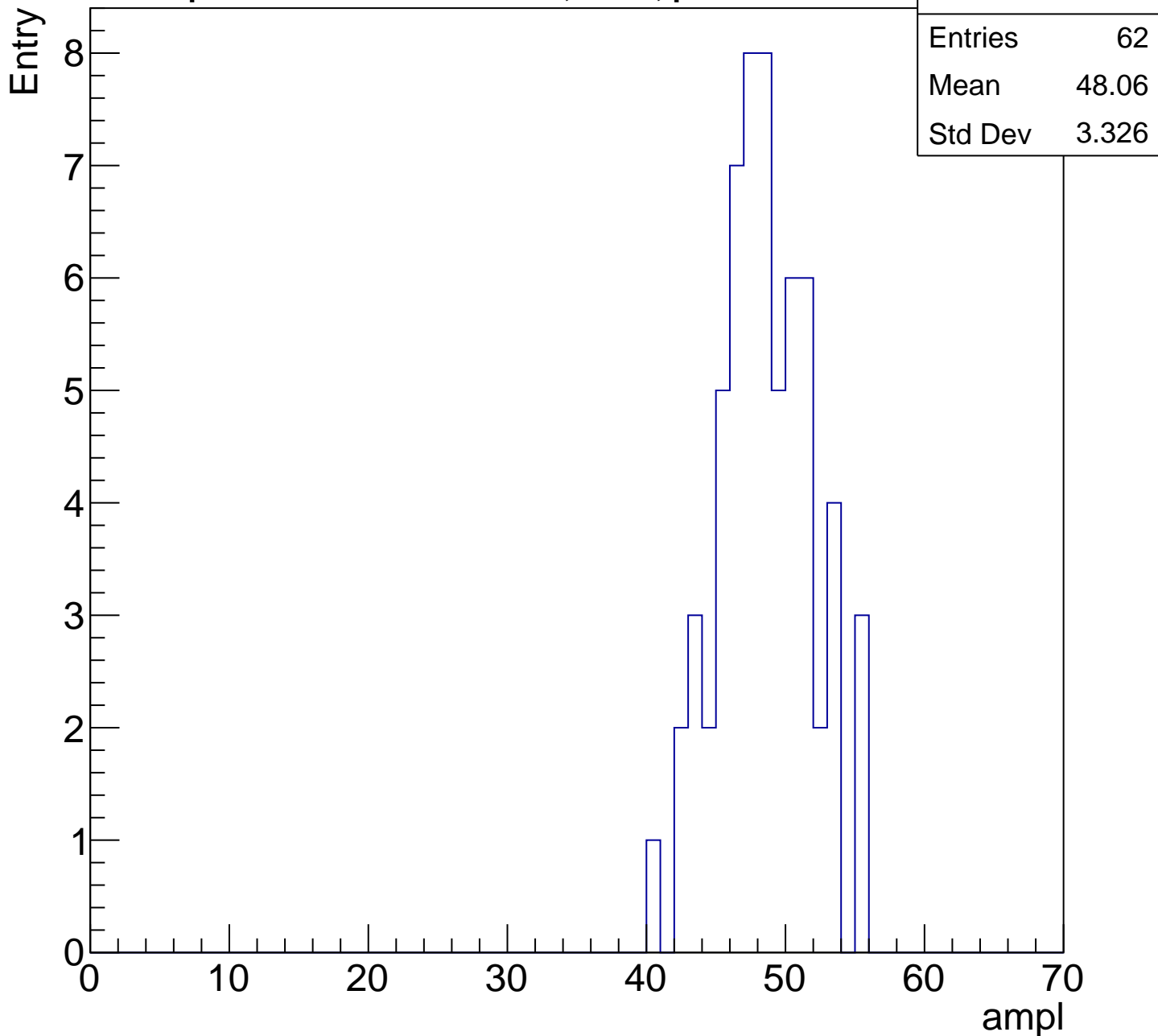
Gaus mean : 42.1281

Gaus Width: 3.5018



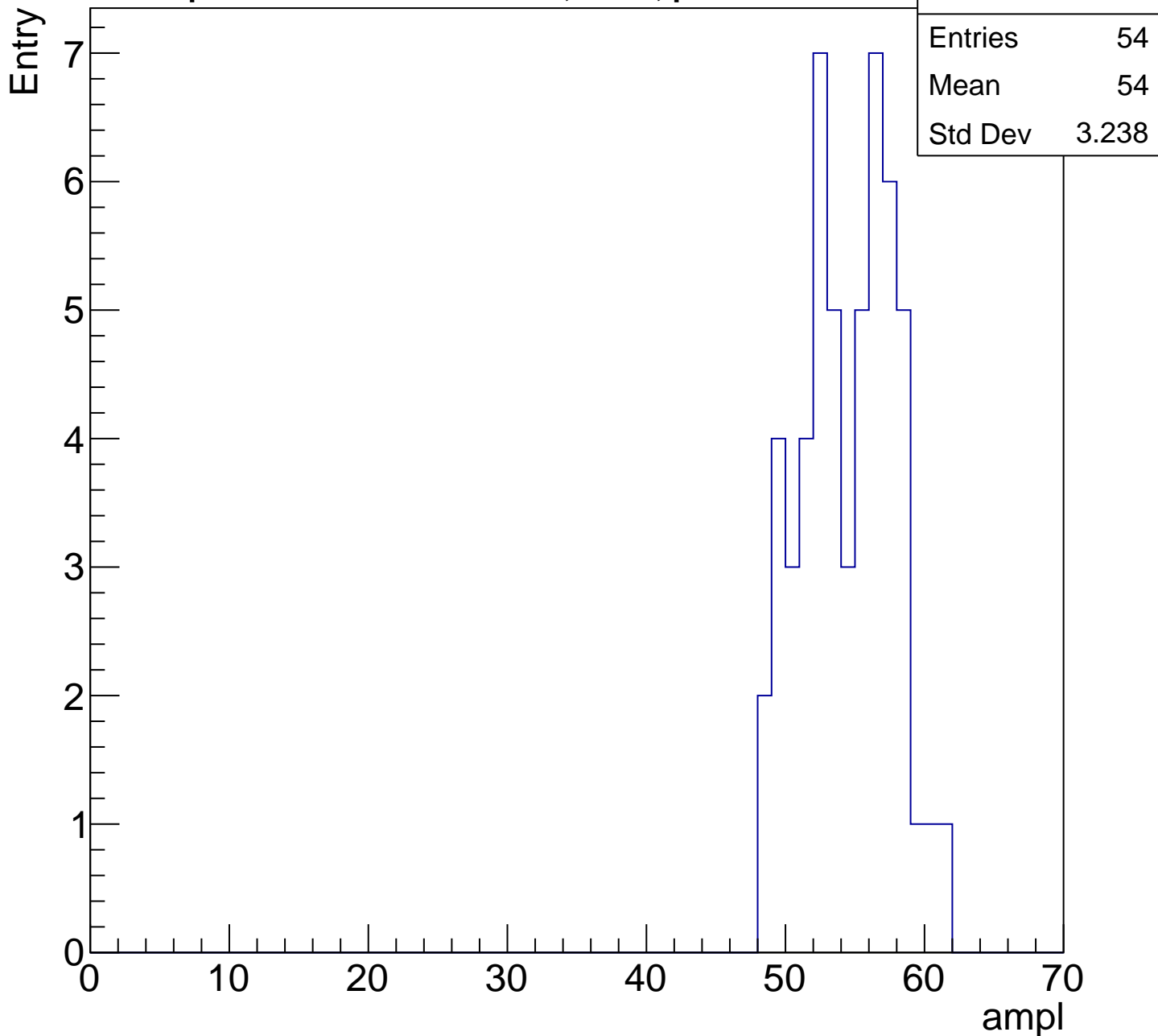
B1L103S, U5-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 47

Mean 58.3

Std Dev 3.107

ampl

0

10

20

30

40

50

60

70

B1L103S, U5-ch12, adc6

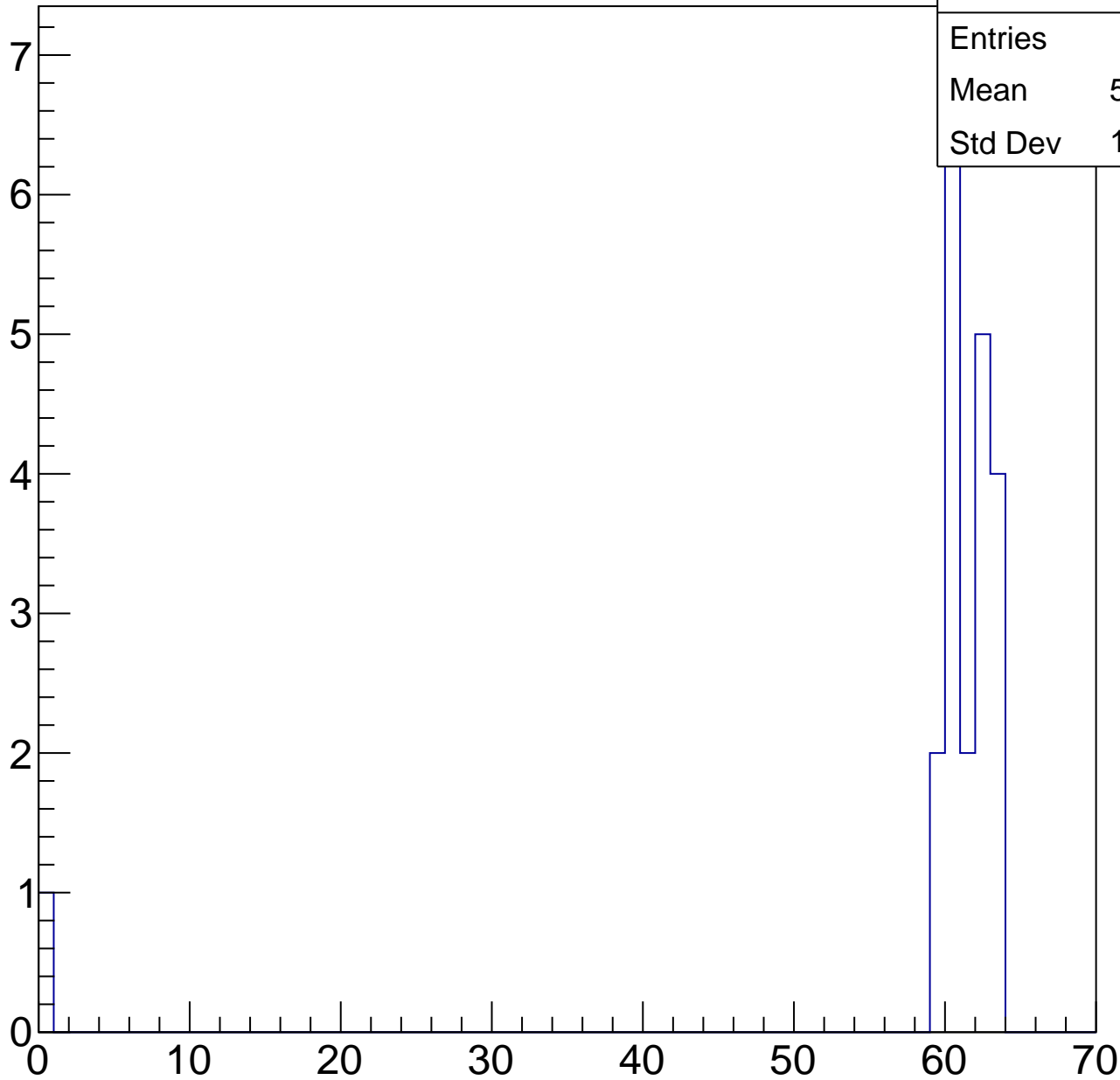
calib_packv5_042523_0143.root, FC#7, port C2

Entry

7
6
5
4
3
2
1
0

Entries	21
Mean	58.19
Std Dev	13.08

ampl



B1L103S, U5-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

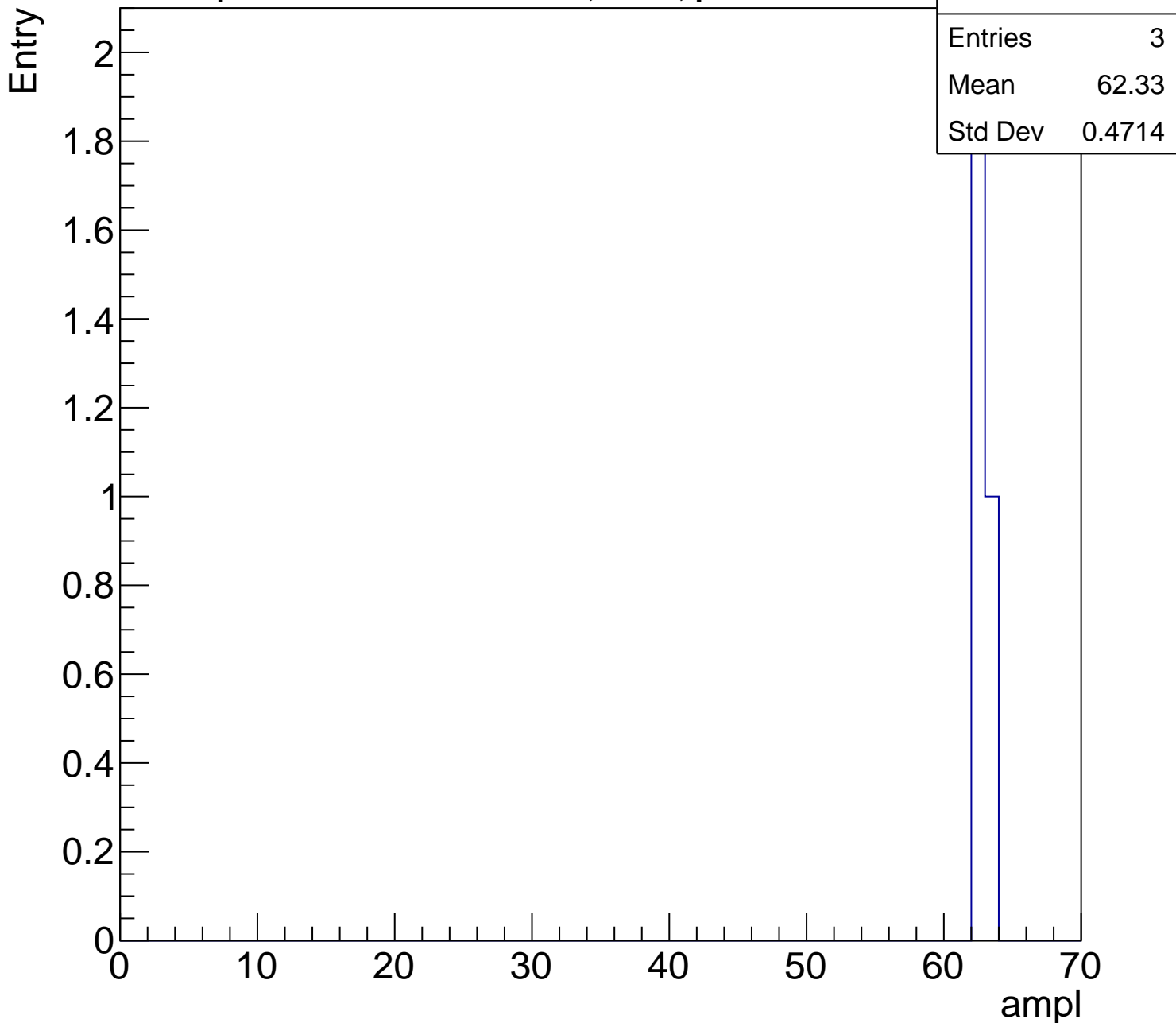
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch13, adc0

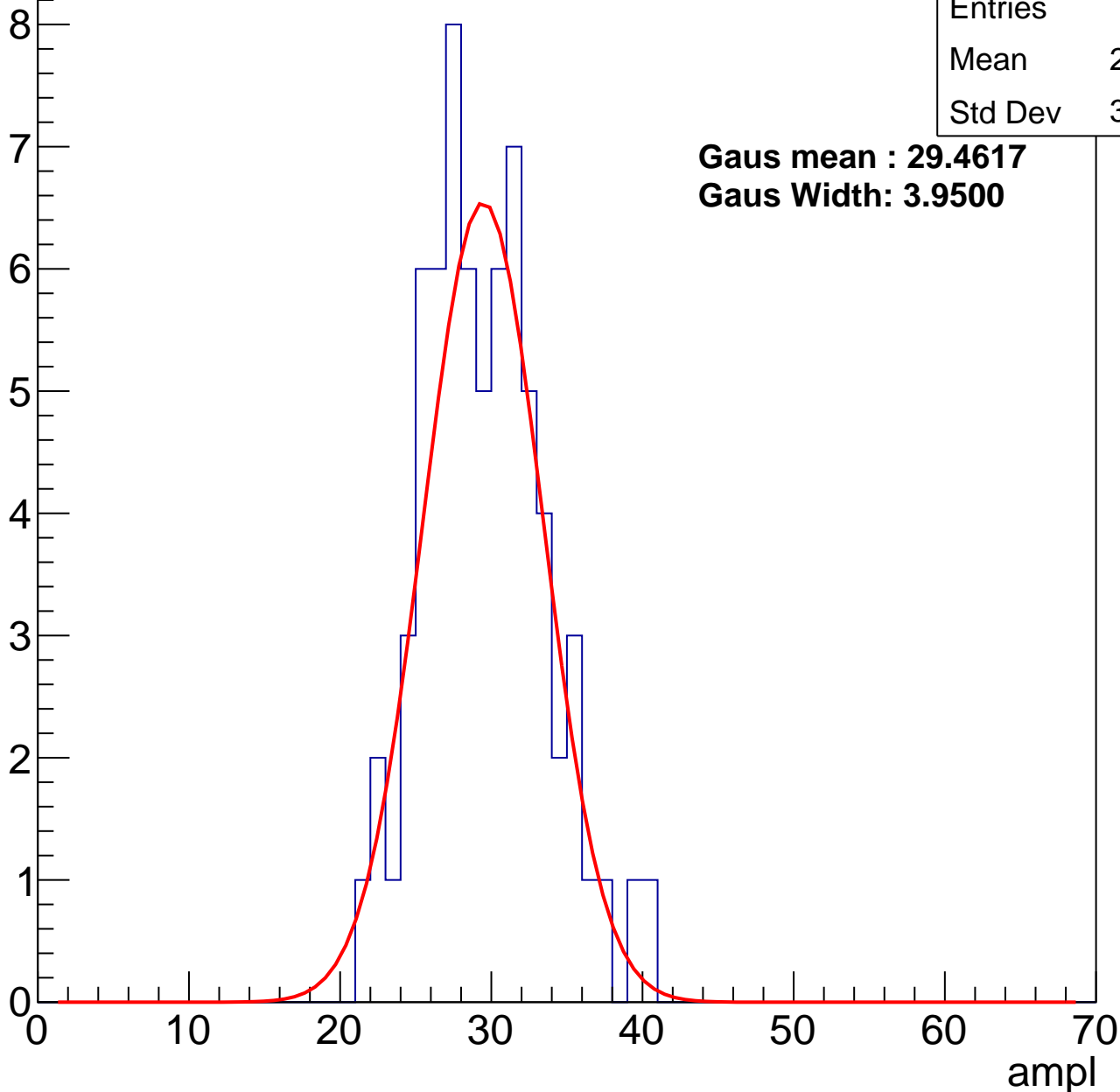
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	29.12
Std Dev	3.984

Gaus mean : 29.4617

Gaus Width: 3.9500



B1L103S, U5-ch13, adc1

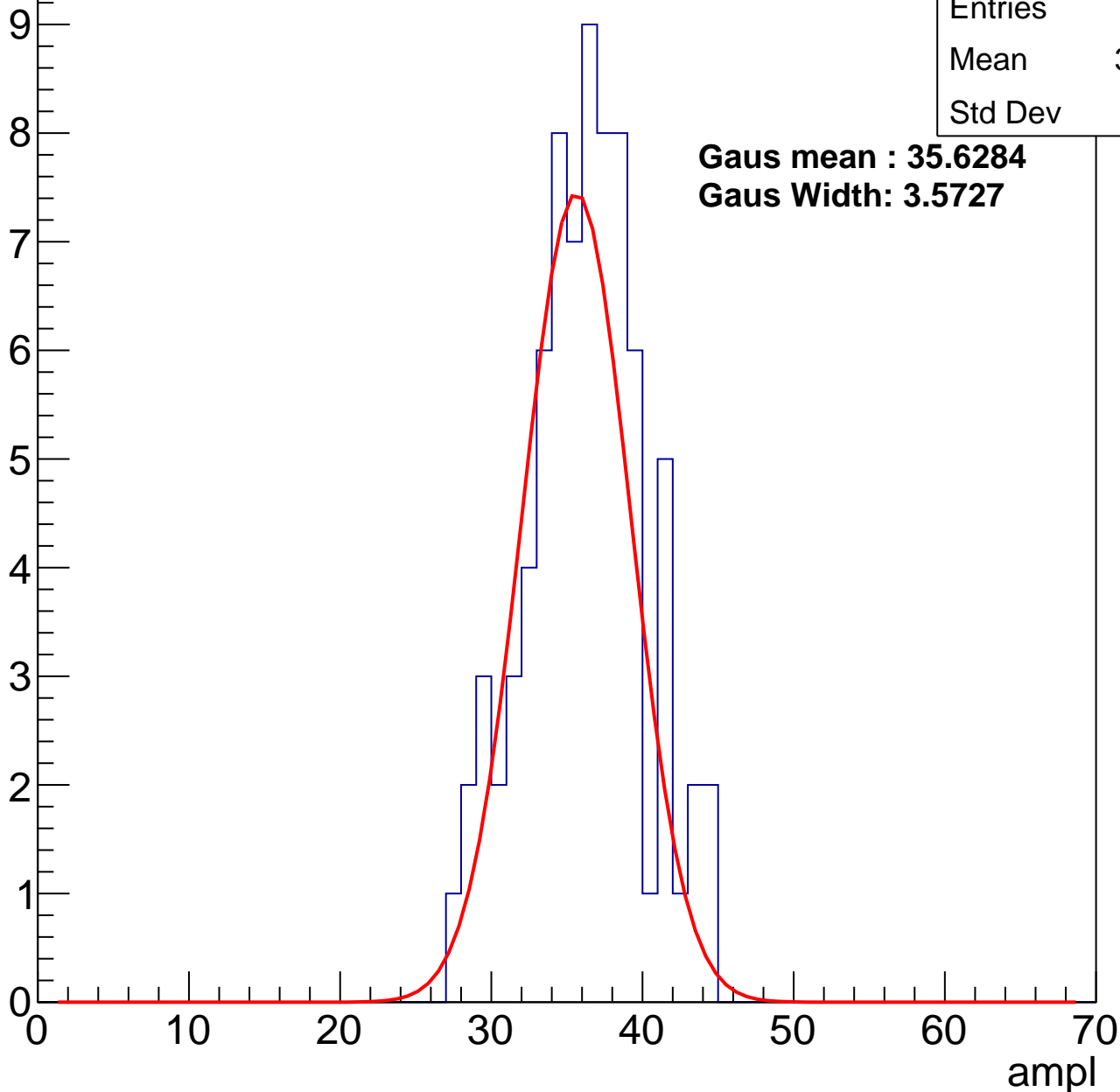
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	35.71
Std Dev	3.84

Gaus mean : 35.6284

Gaus Width: 3.5727



B1L103S, U5-ch13, adc2

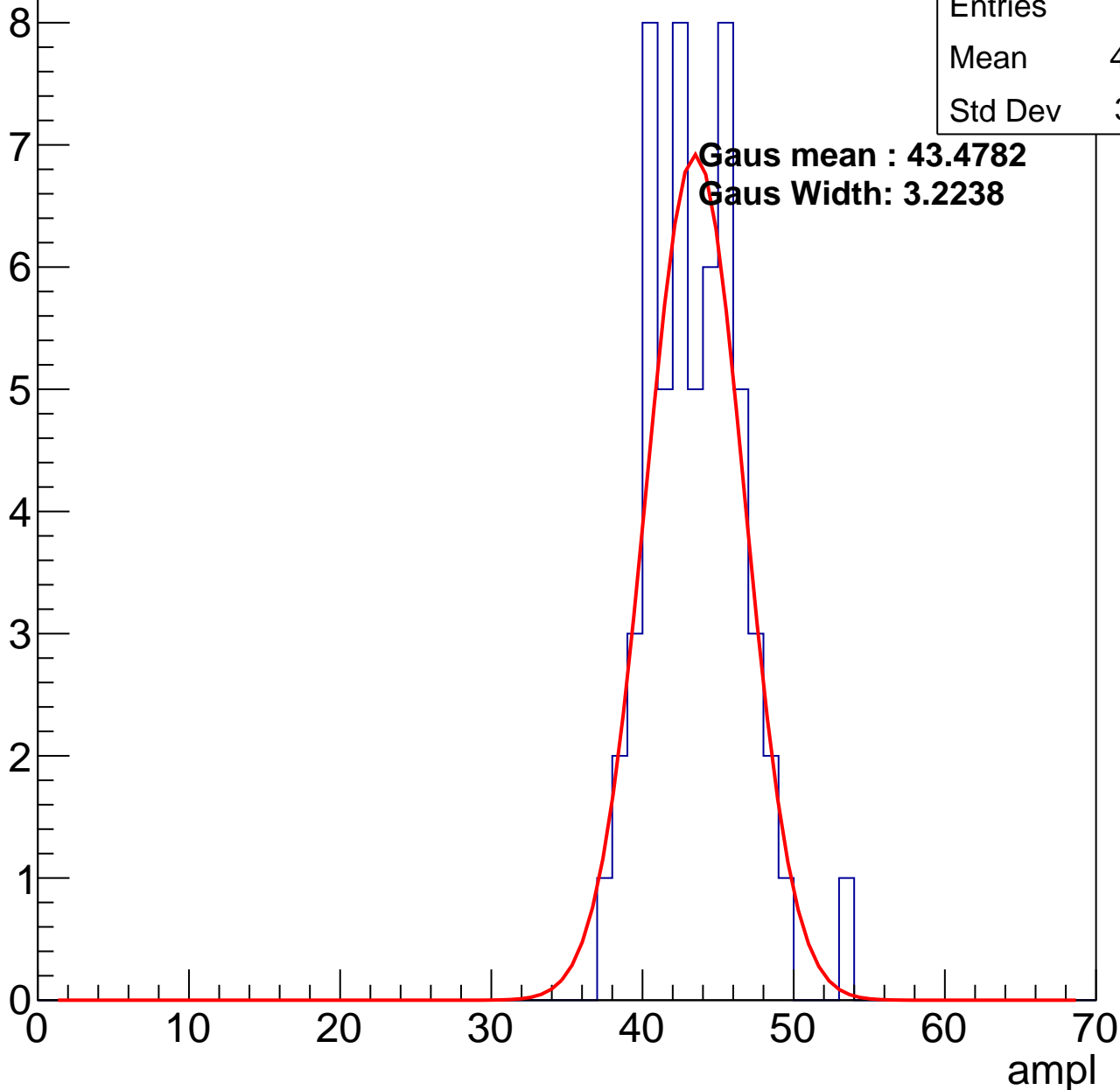
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.09
Std Dev	3.081

Gaus mean : 43.4782

Gaus Width: 3.2238

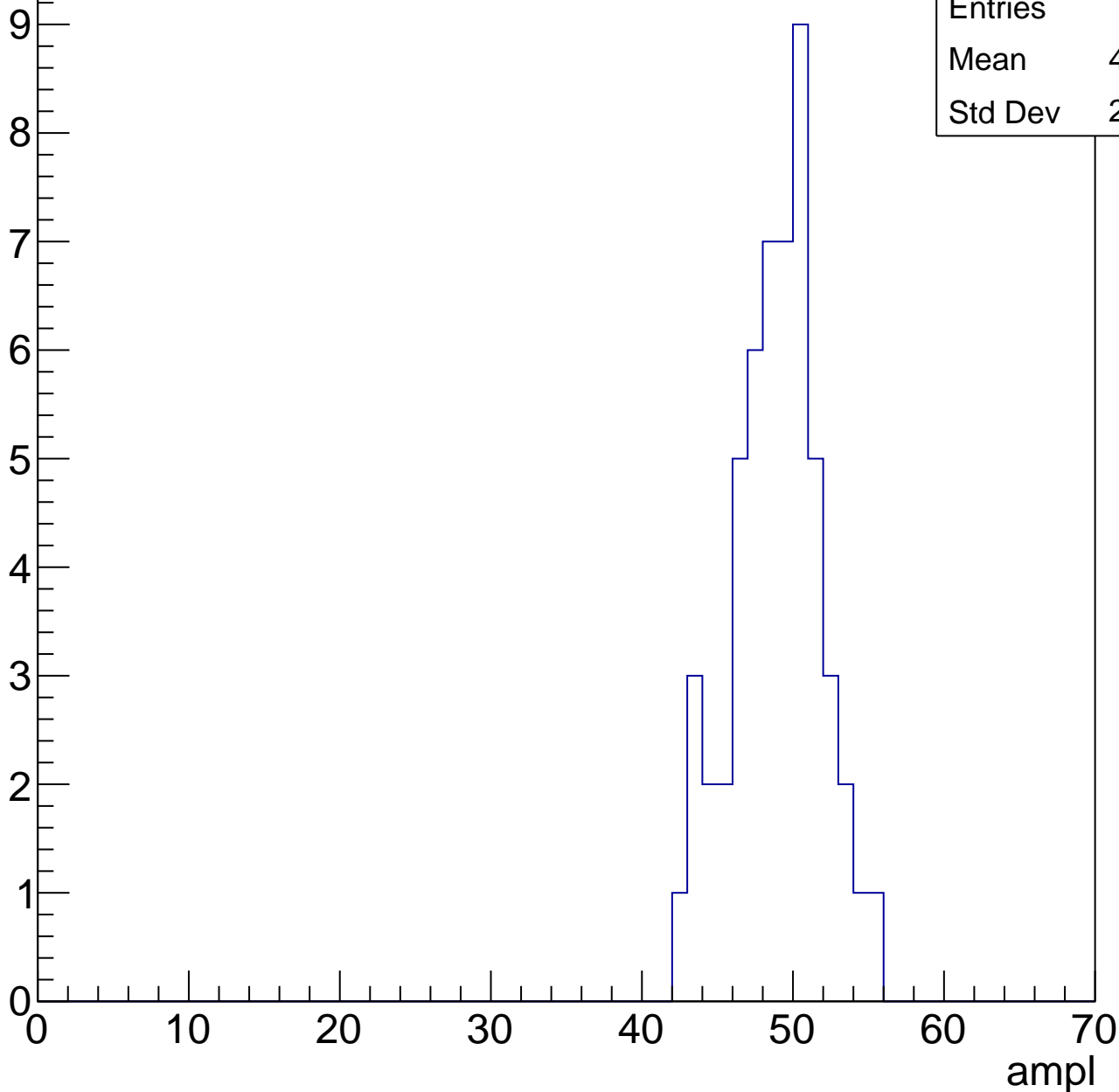


B1L103S, U5-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	48.44
Std Dev	2.878

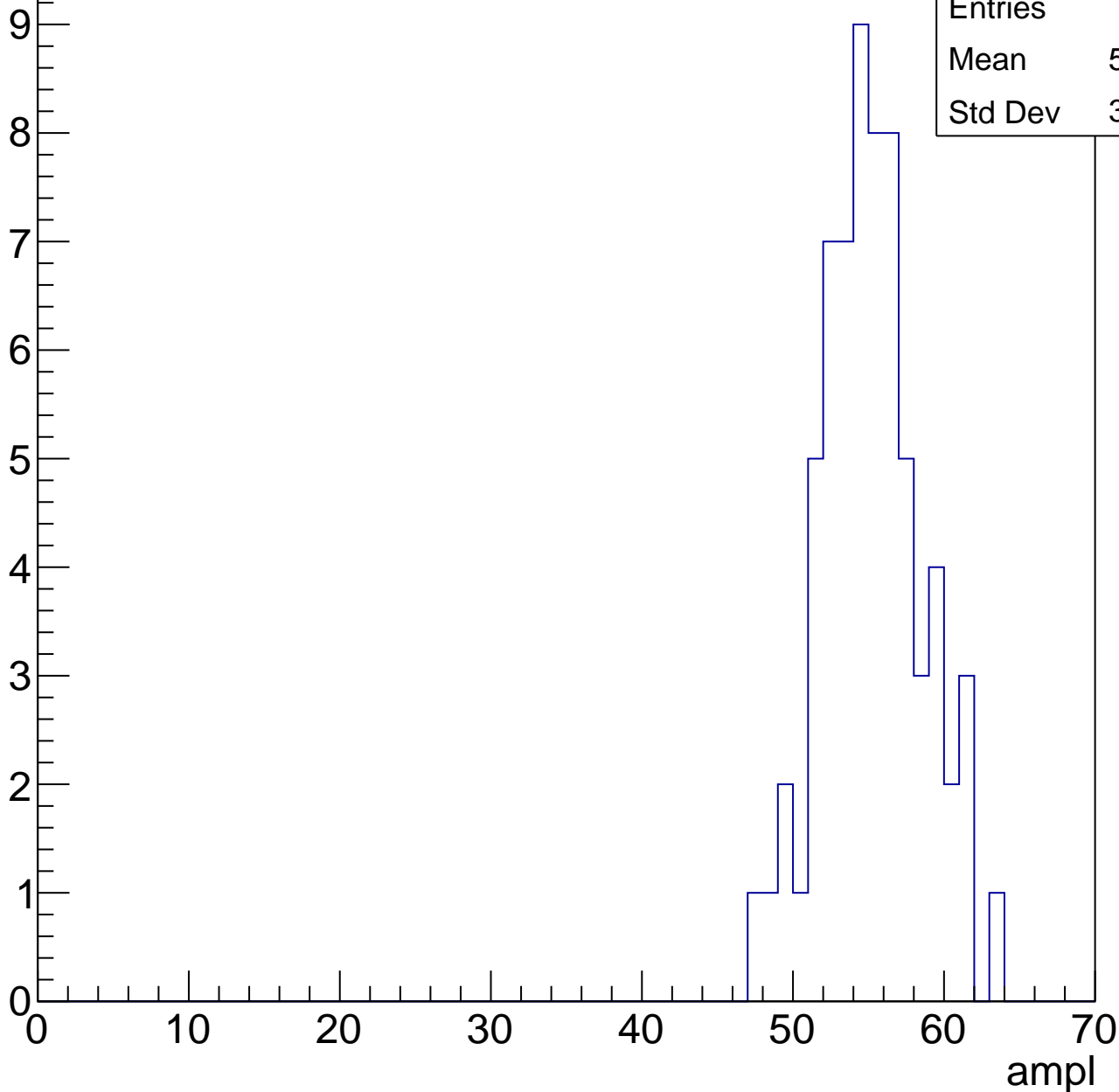


B1L103S, U5-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	54.75
Std Dev	3.293



B1L103S, U5-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 59.6

Std Dev 2.104

ampl

0

10

20

30

40

50

60

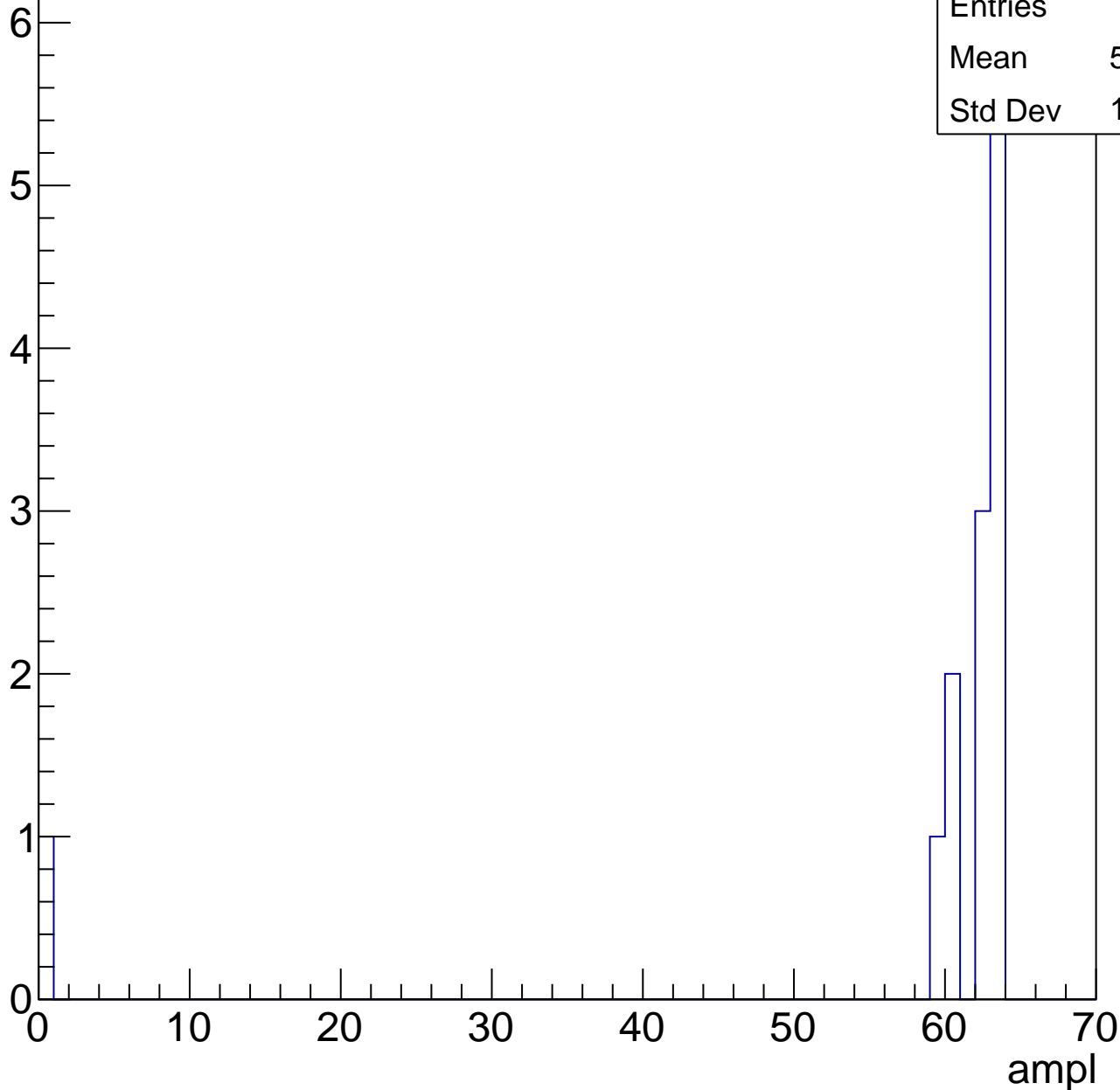
70

B1L103S, U5-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	57.15
Std Dev	16.55



B1L103S, U5-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch14, adc0

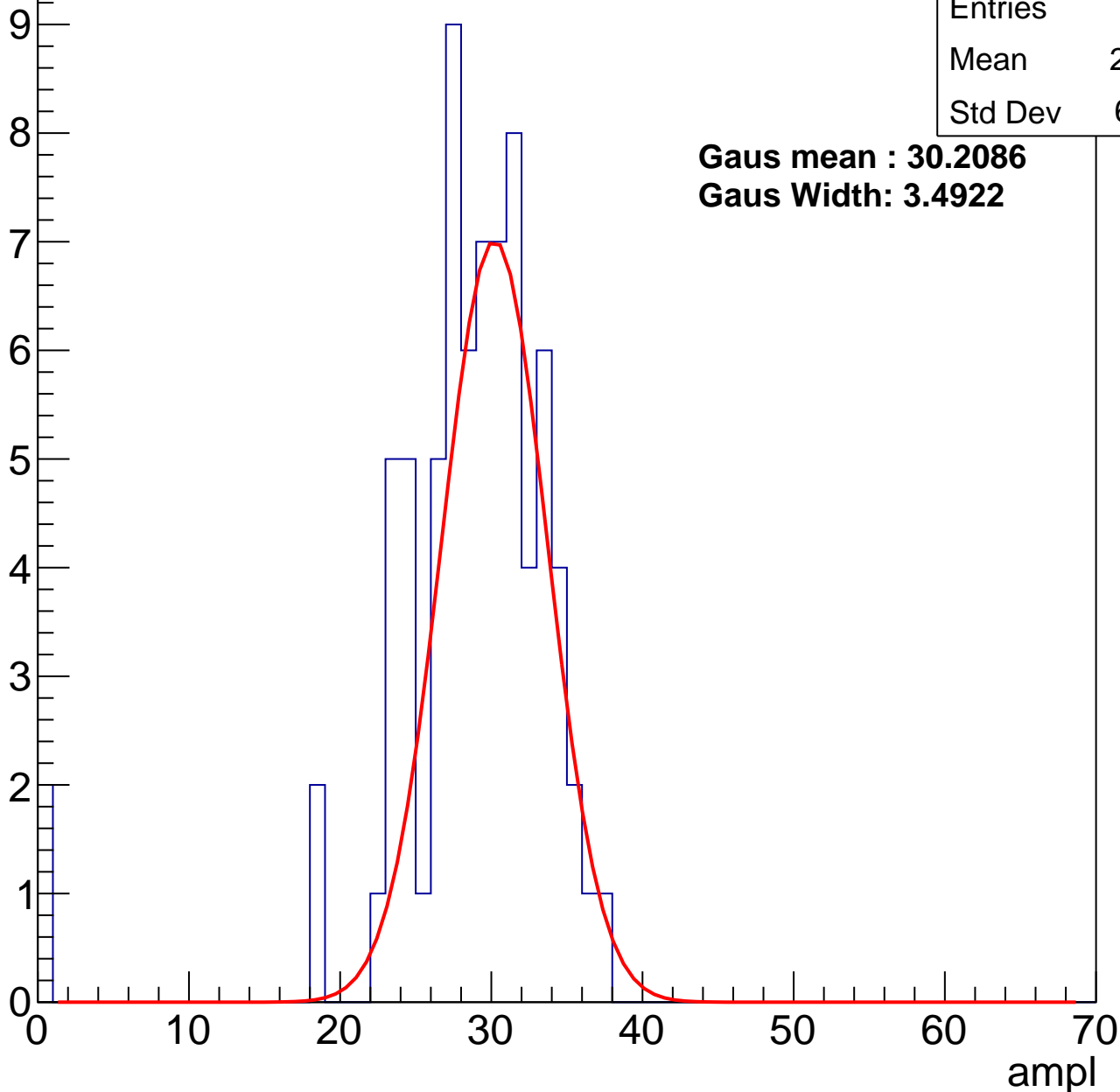
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	27.96
Std Dev	6.021

Gaus mean : 30.2086

Gaus Width: 3.4922



B1L103S, U5-ch14, adc1

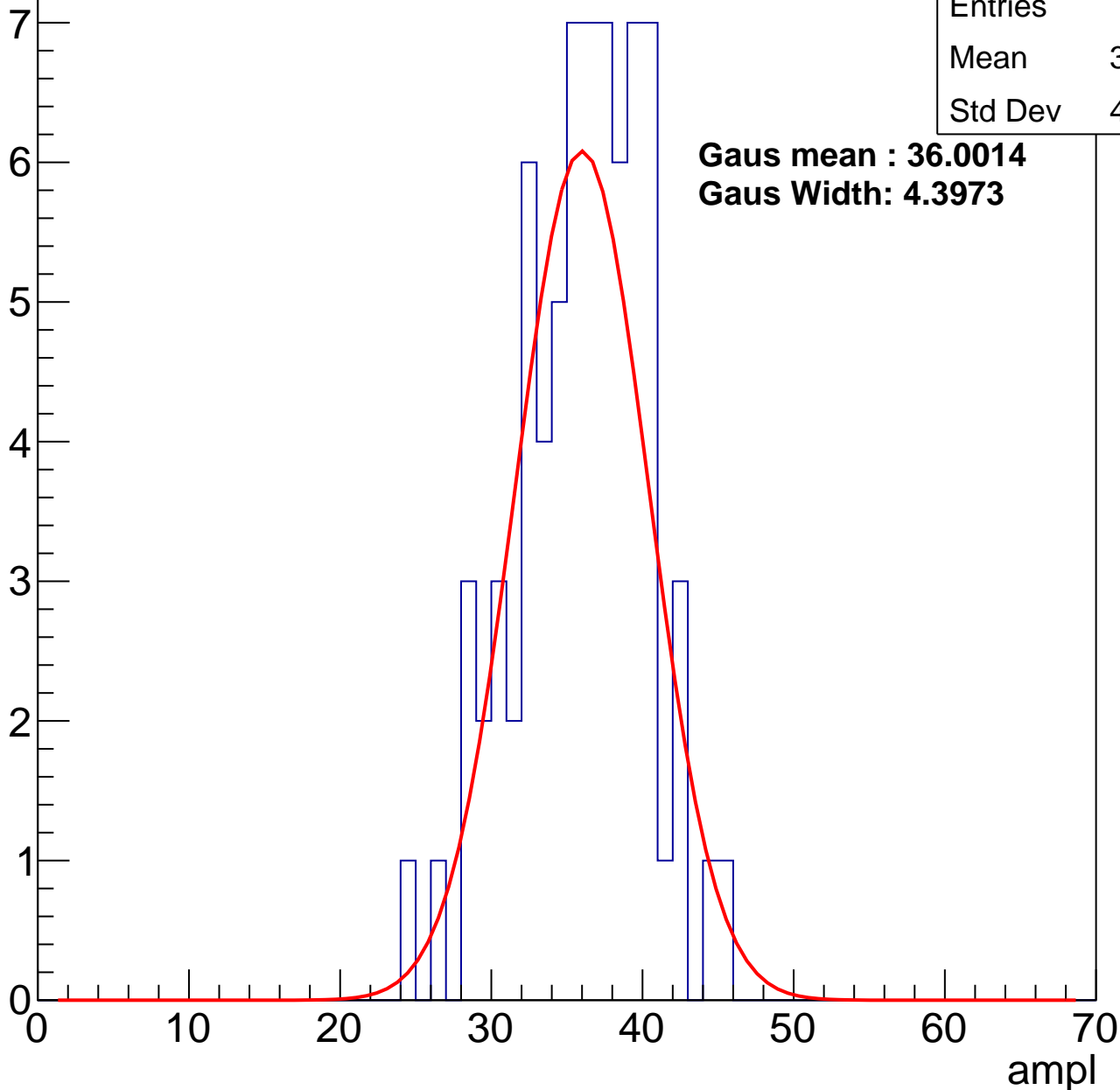
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.55
Std Dev	4.224

Gaus mean : 36.0014

Gaus Width: 4.3973



B1L103S, U5-ch14, adc2

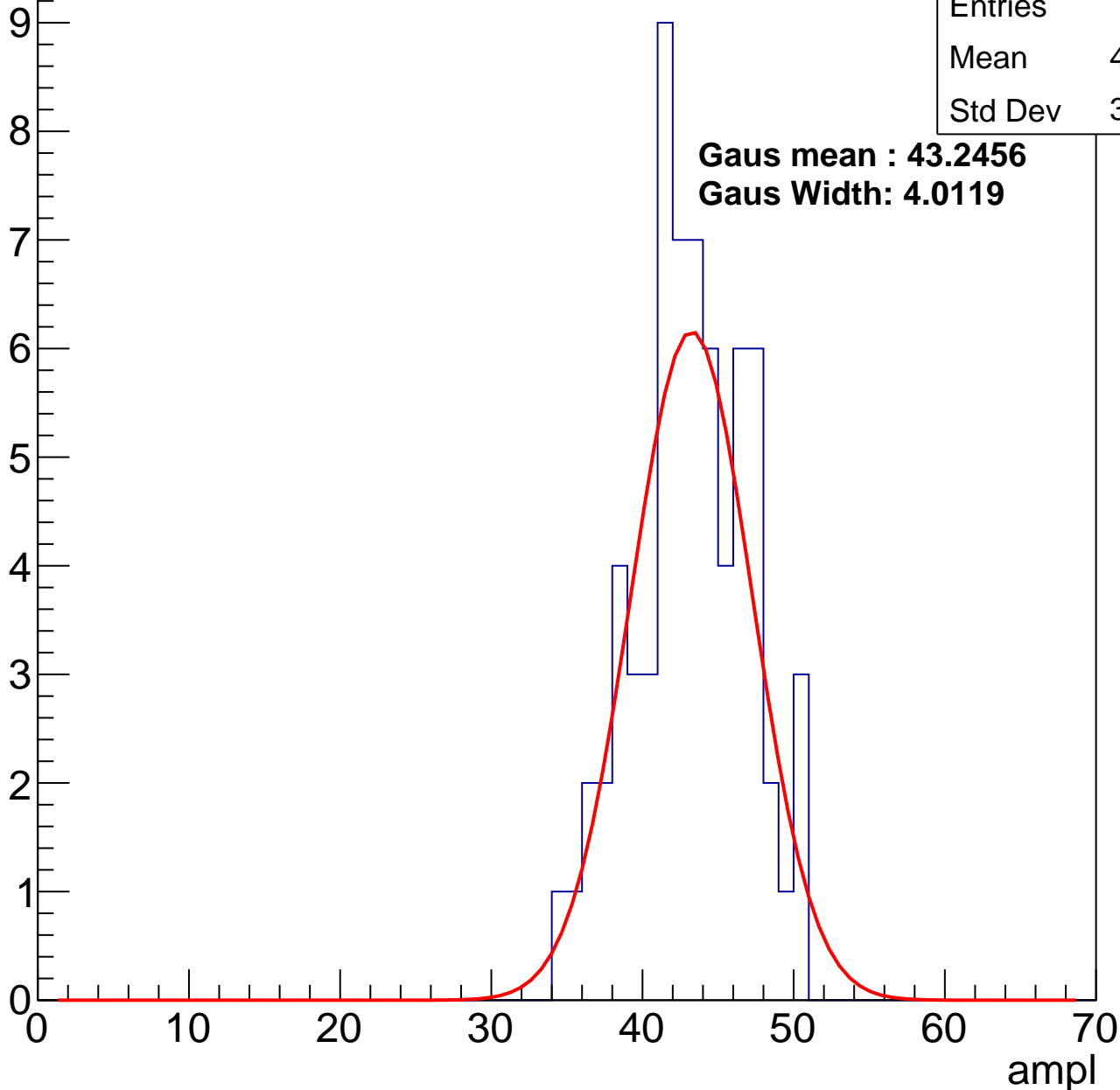
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.76
Std Dev	3.734

Gaus mean : 43.2456

Gaus Width: 4.0119

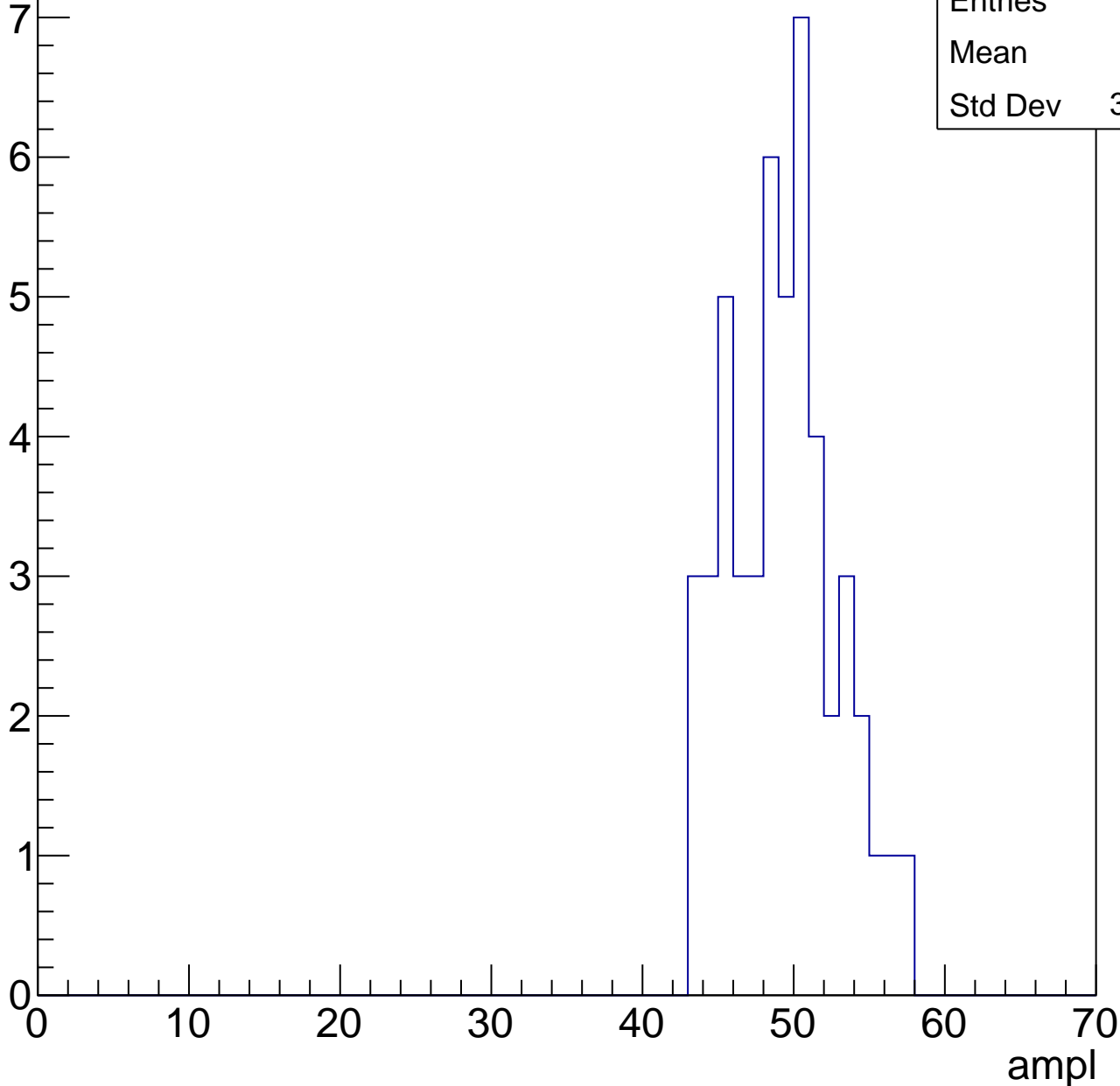


B1L103S, U5-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

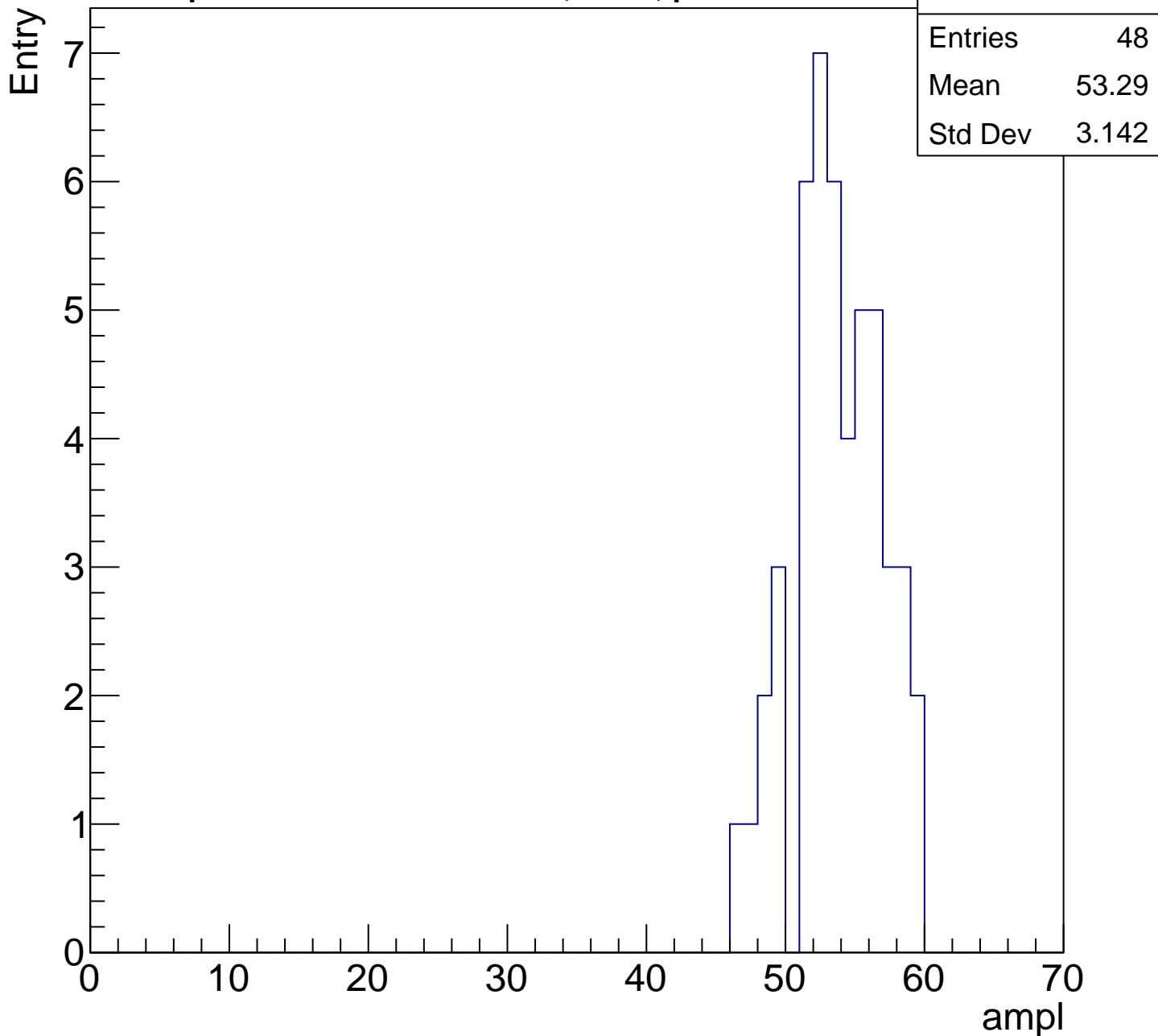
Entry

Entries	49
Mean	48.8
Std Dev	3.482



B1L103S, U5-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch14, adc5

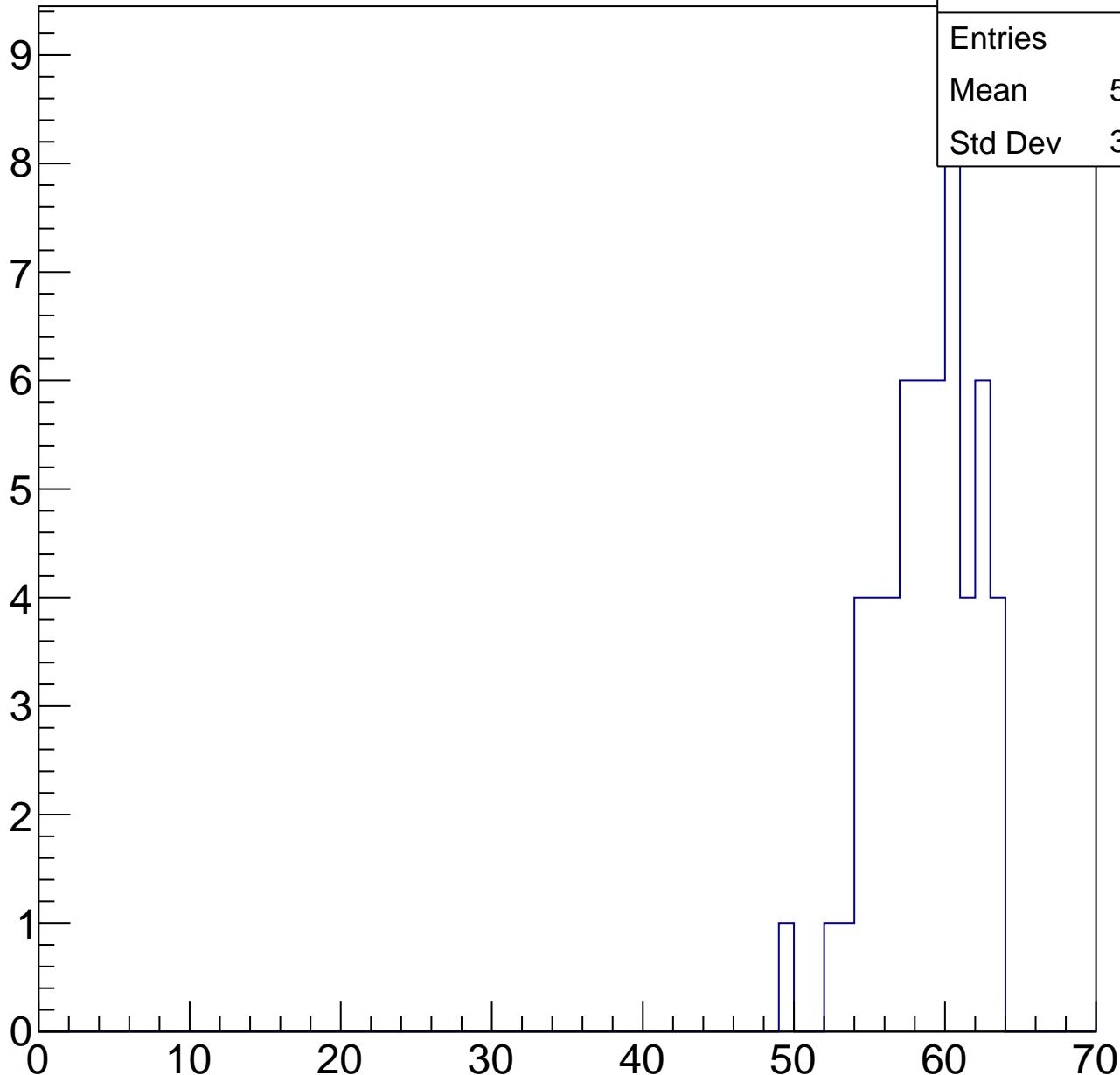
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.32
Std Dev	3.083

ampl

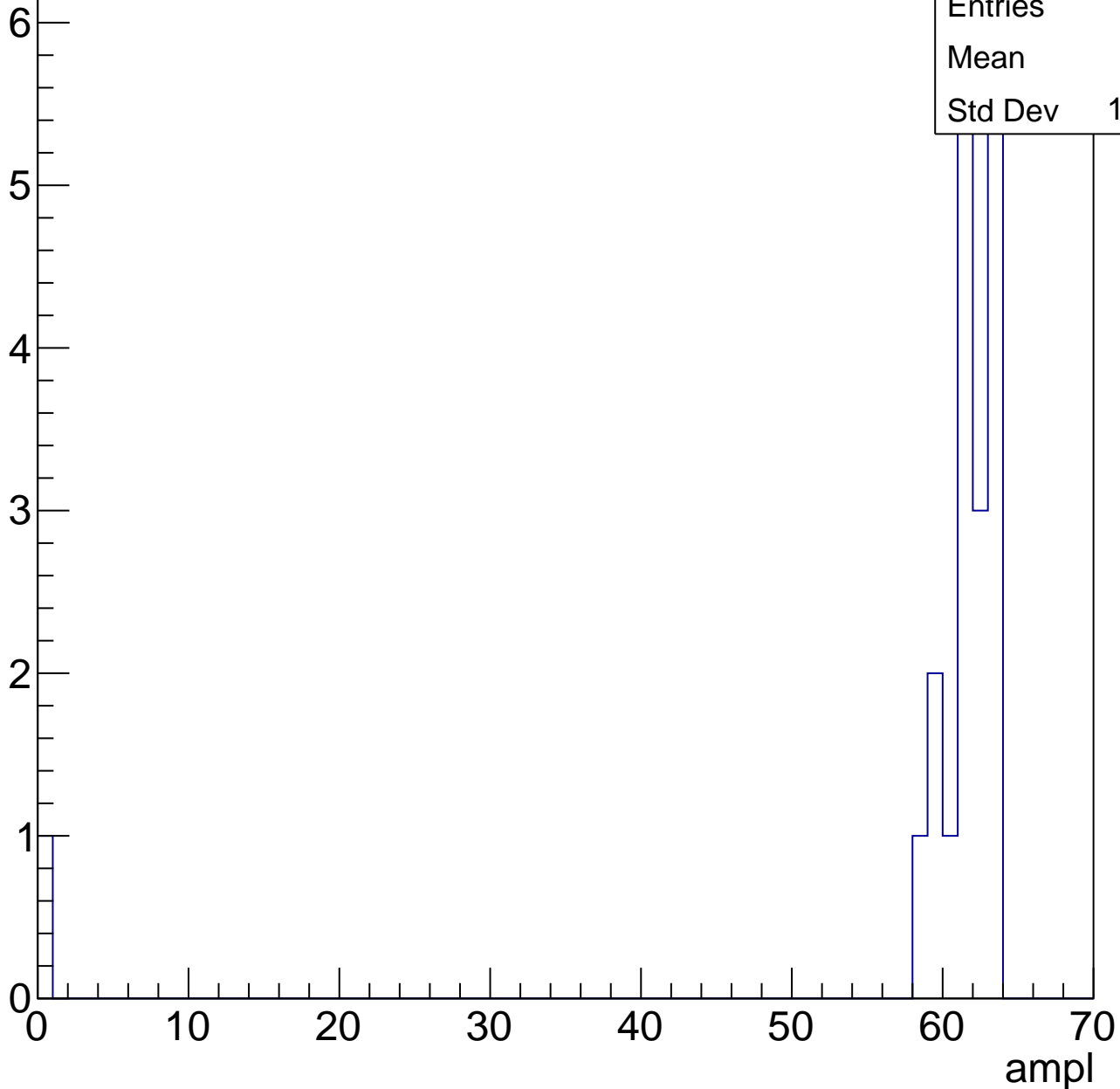


B1L103S, U5-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

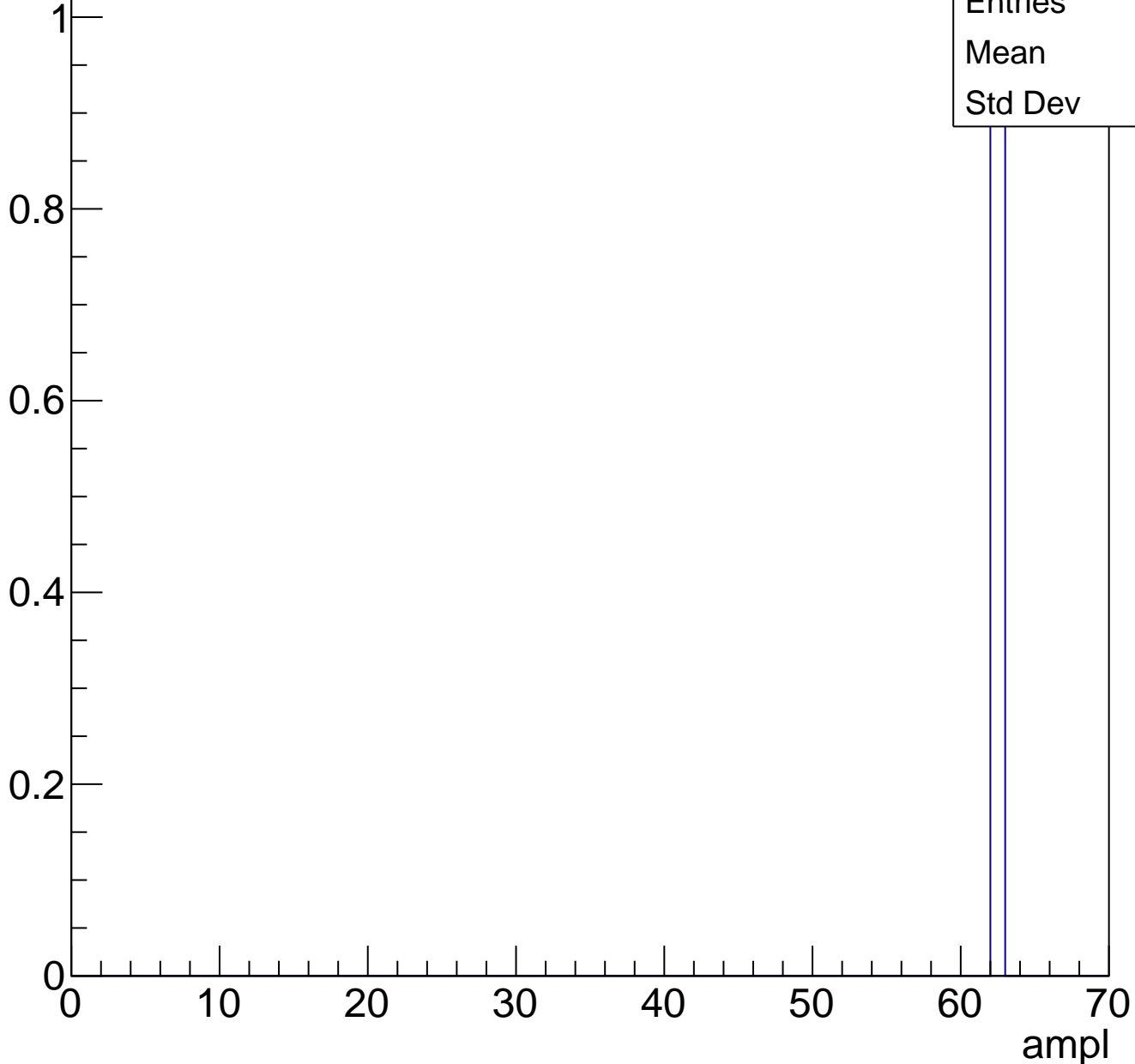
Entries	20
Mean	58.3
Std Dev	13.45



B1L103S, U5-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch15, adc0

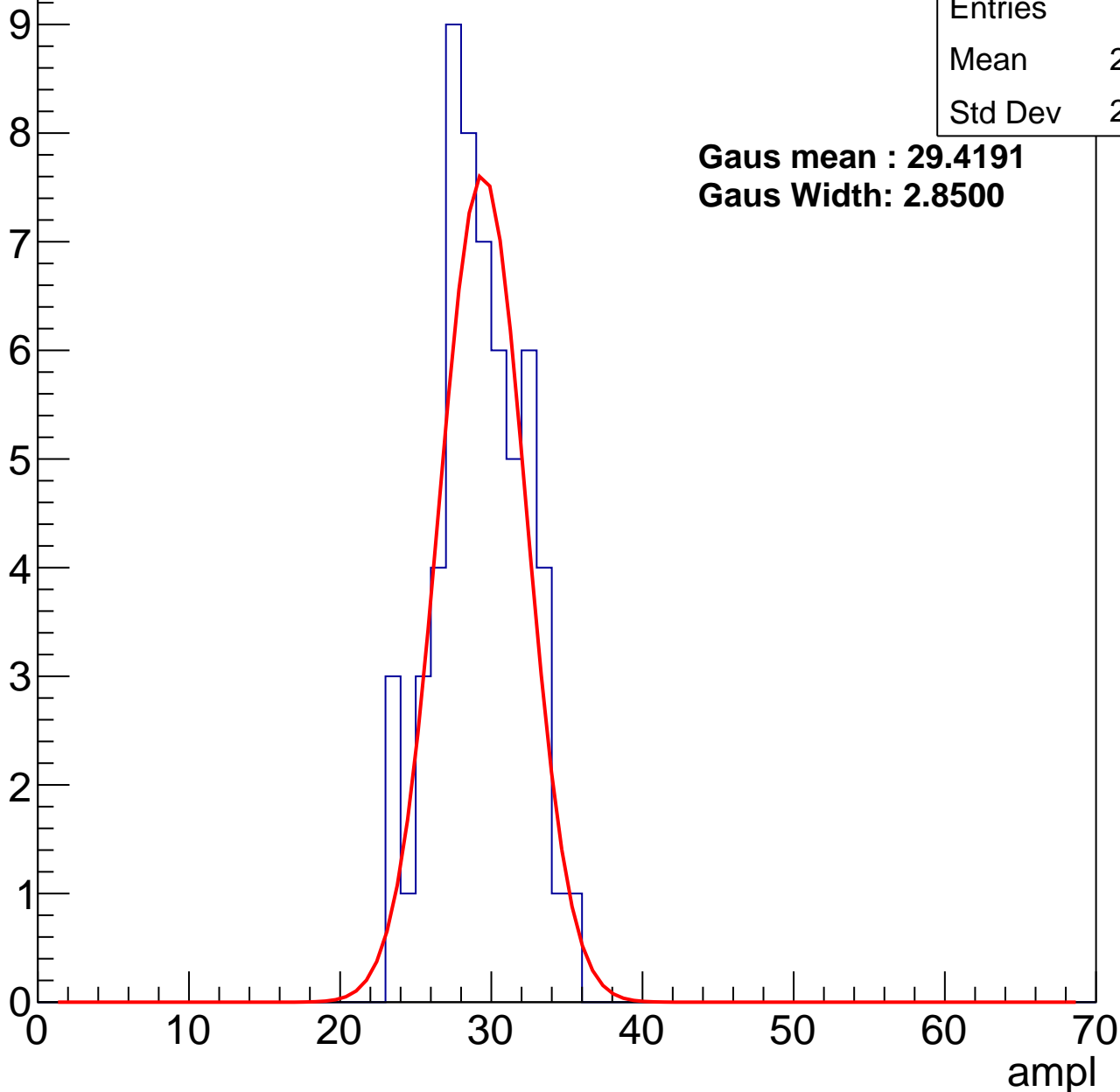
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	28.79
Std Dev	2.827

Gaus mean : 29.4191

Gaus Width: 2.8500



B1L103S, U5-ch15, adc1

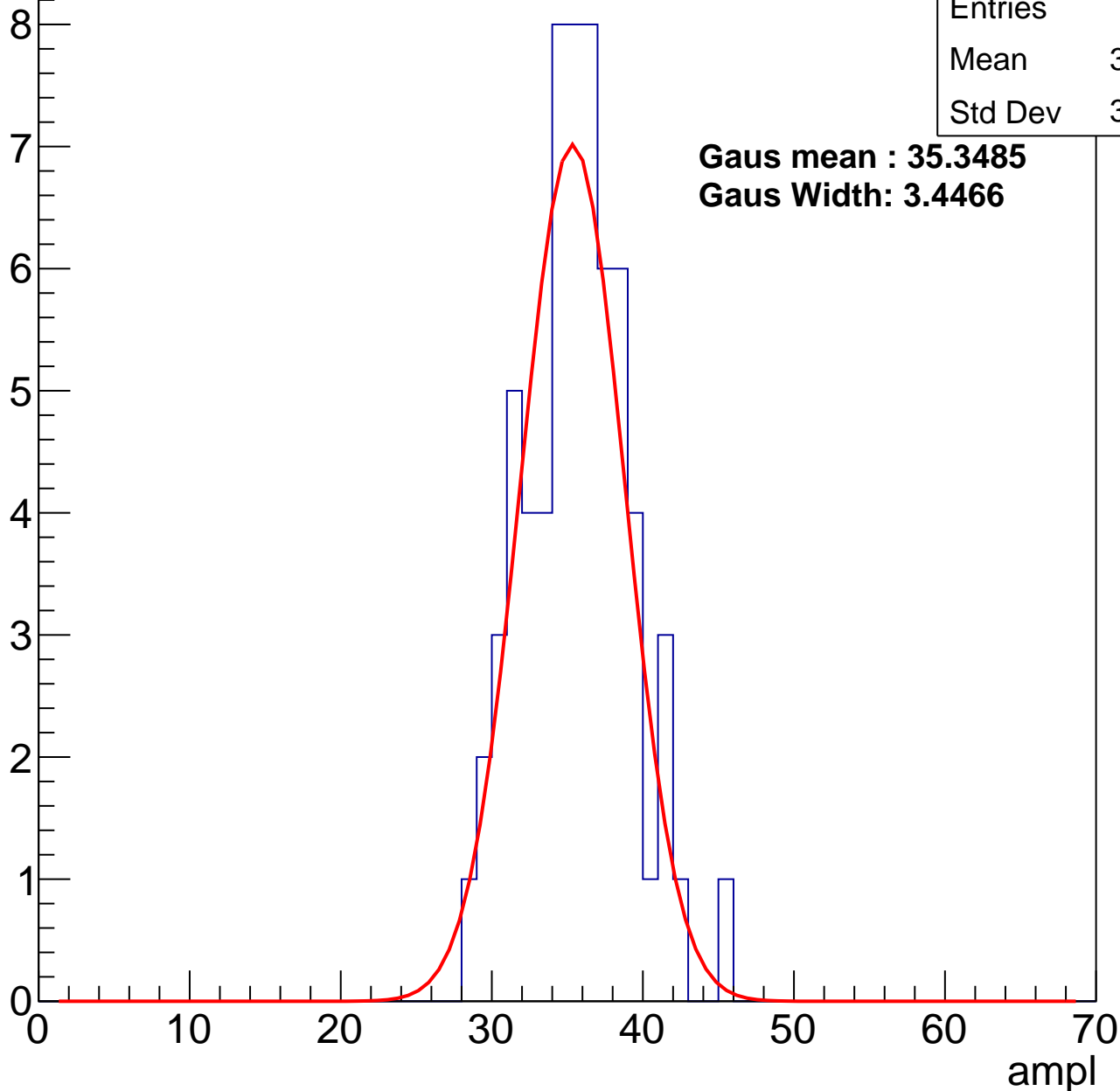
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.18
Std Dev	3.432

Gaus mean : 35.3485

Gaus Width: 3.4466

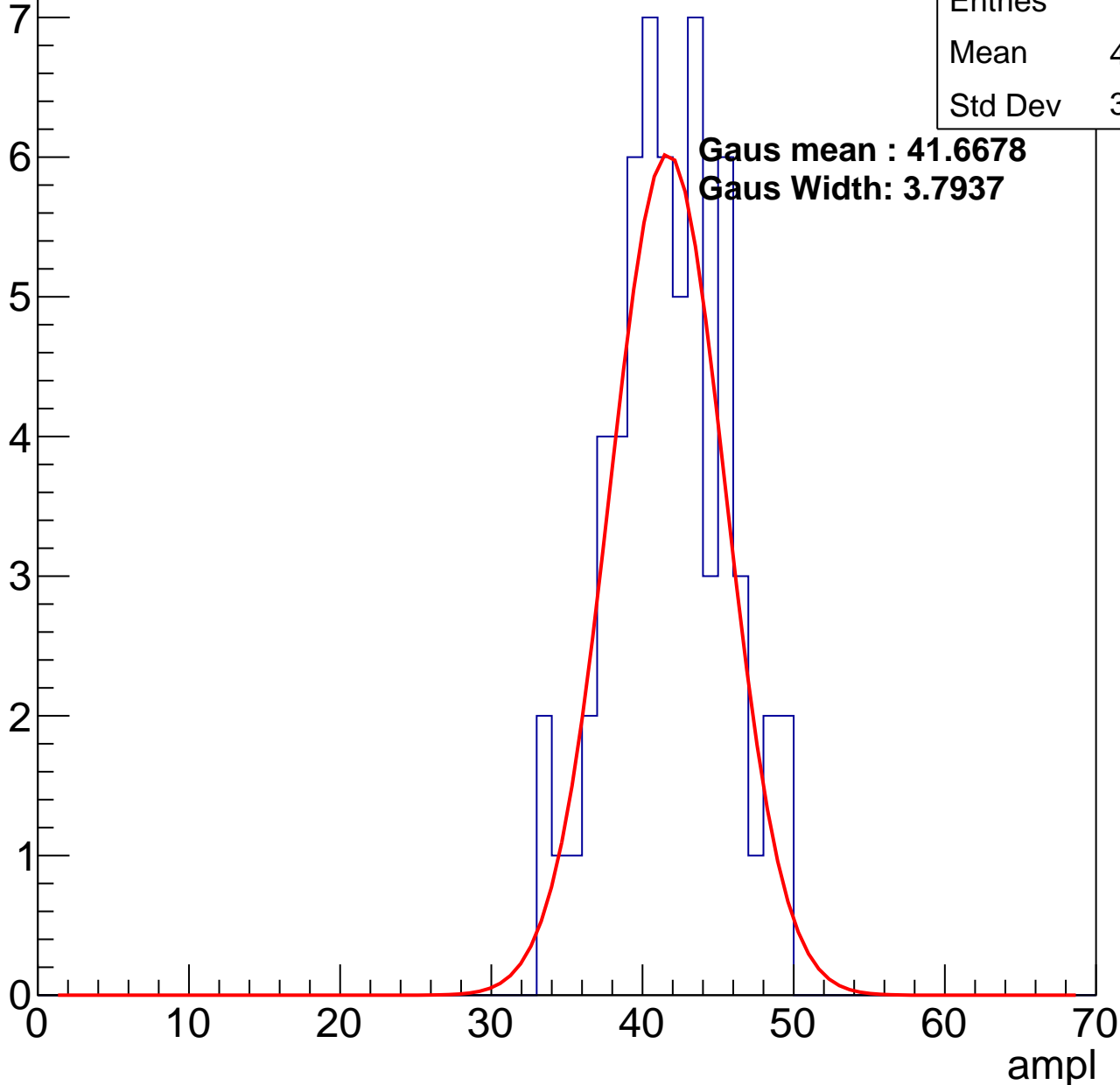


B1L103S, U5-ch15, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.27
Std Dev	3.772

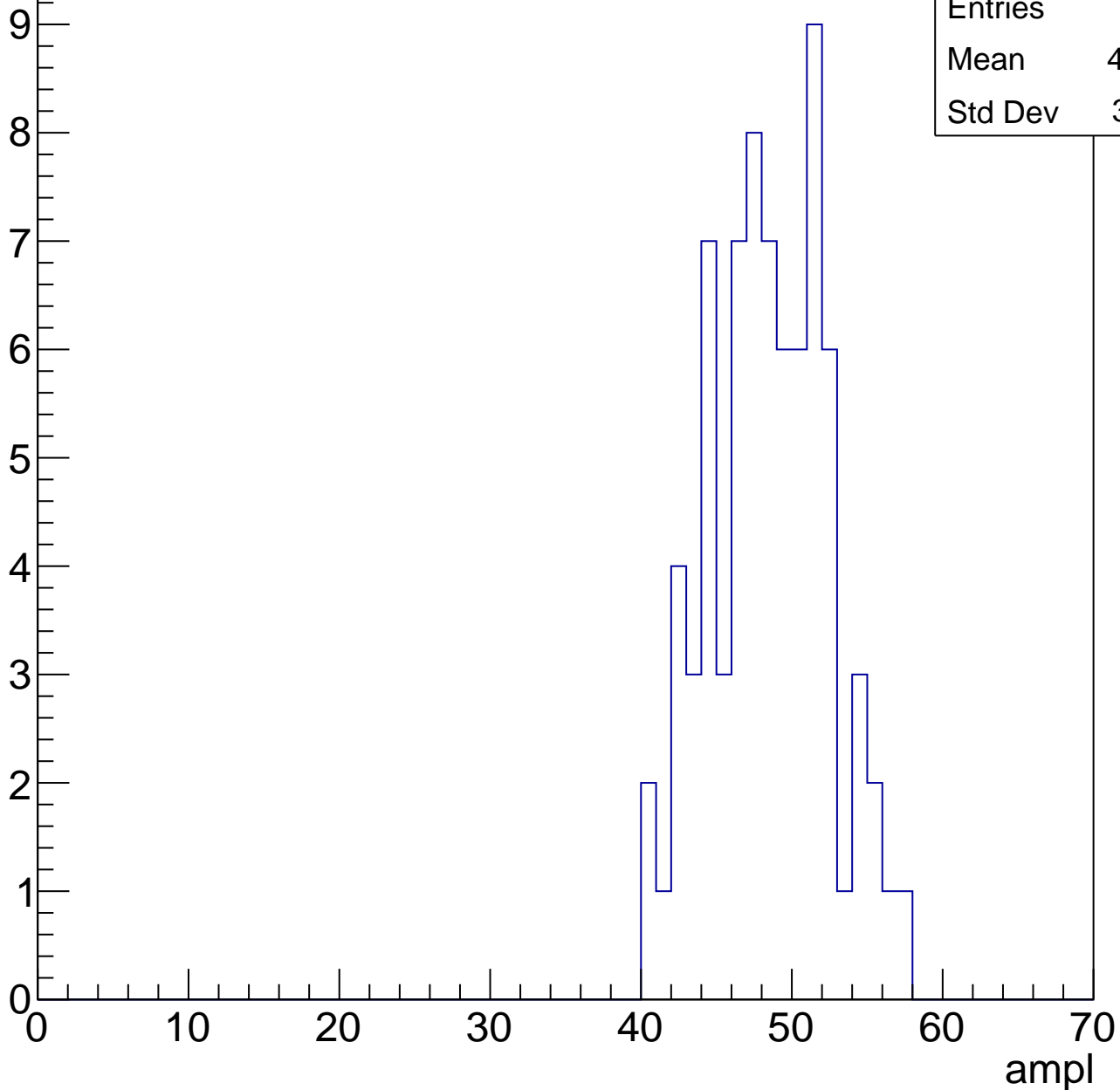


B1L103S, U5-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	48.03
Std Dev	3.871

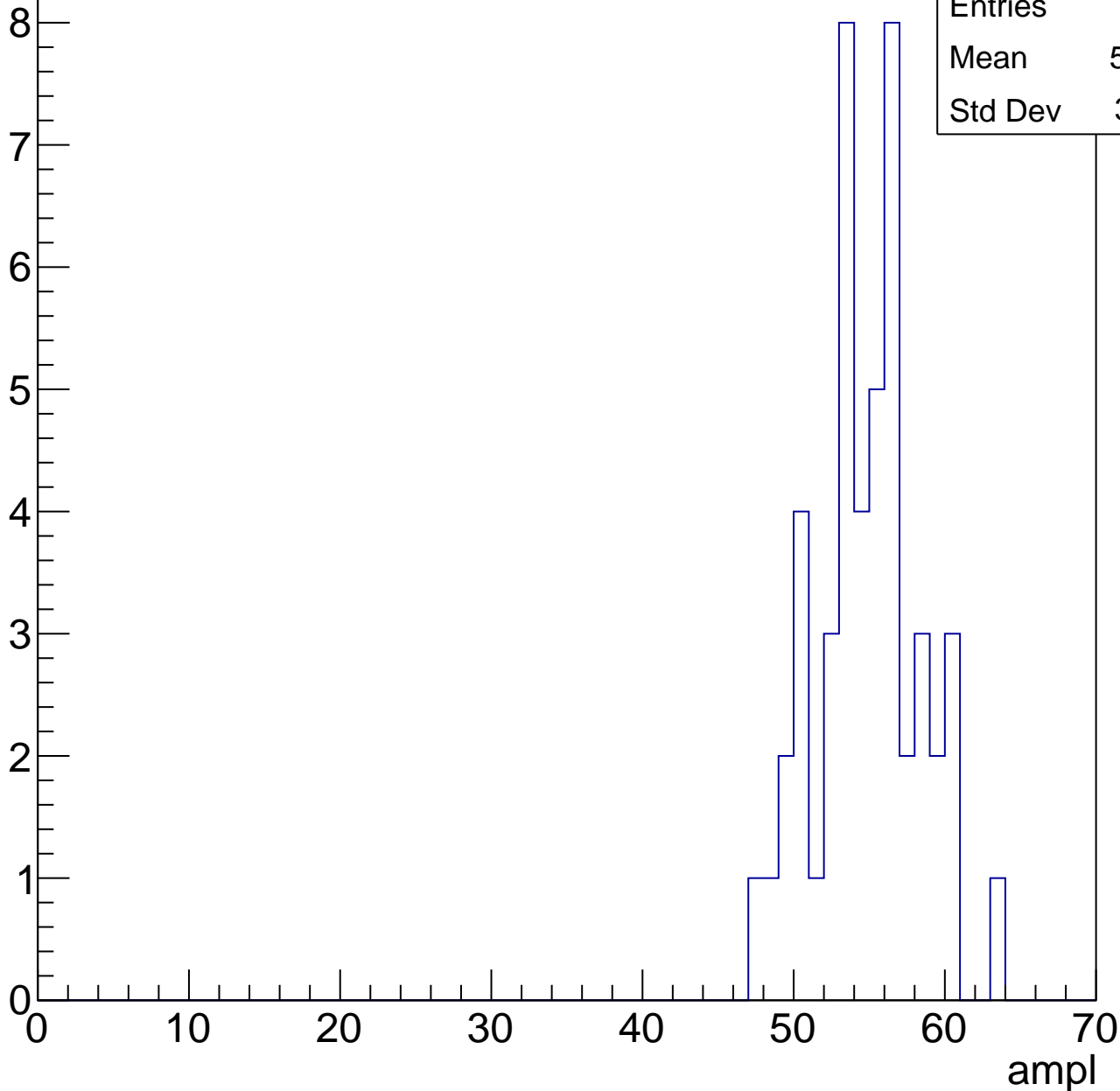


B1L103S, U5-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	54.42
Std Dev	3.421

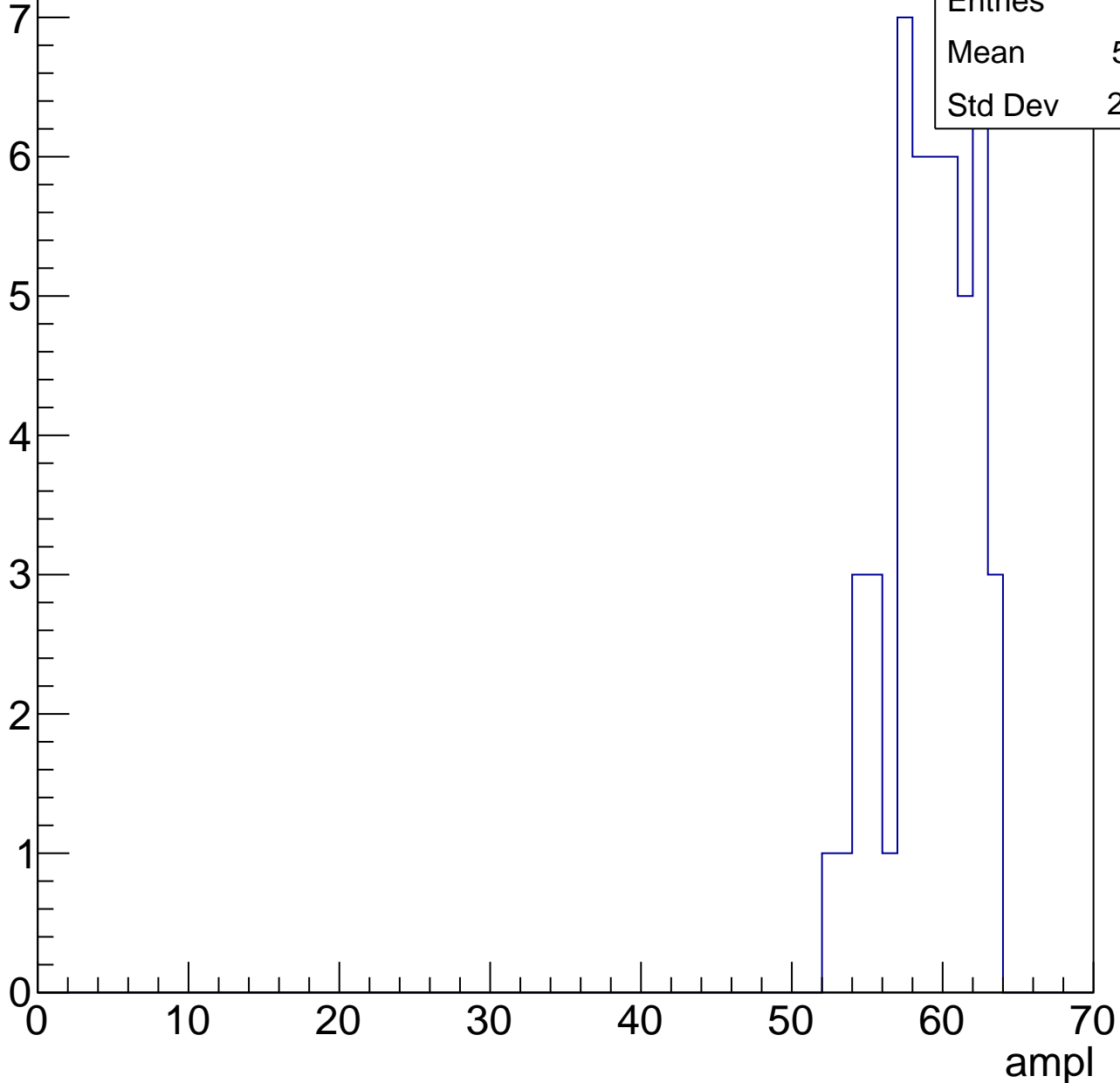


B1L103S, U5-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.71
Std Dev	2.799

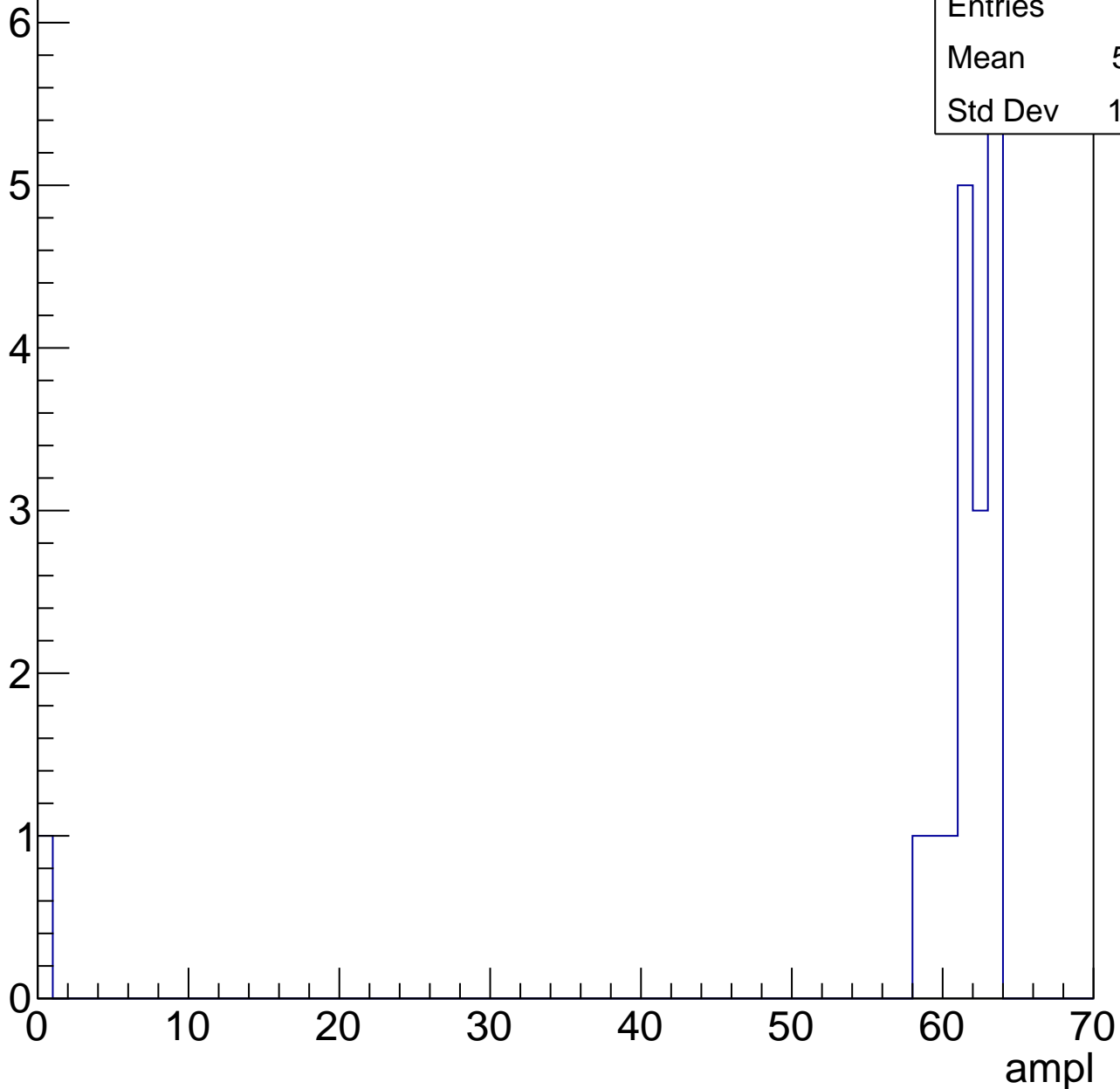


B1L103S, U5-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

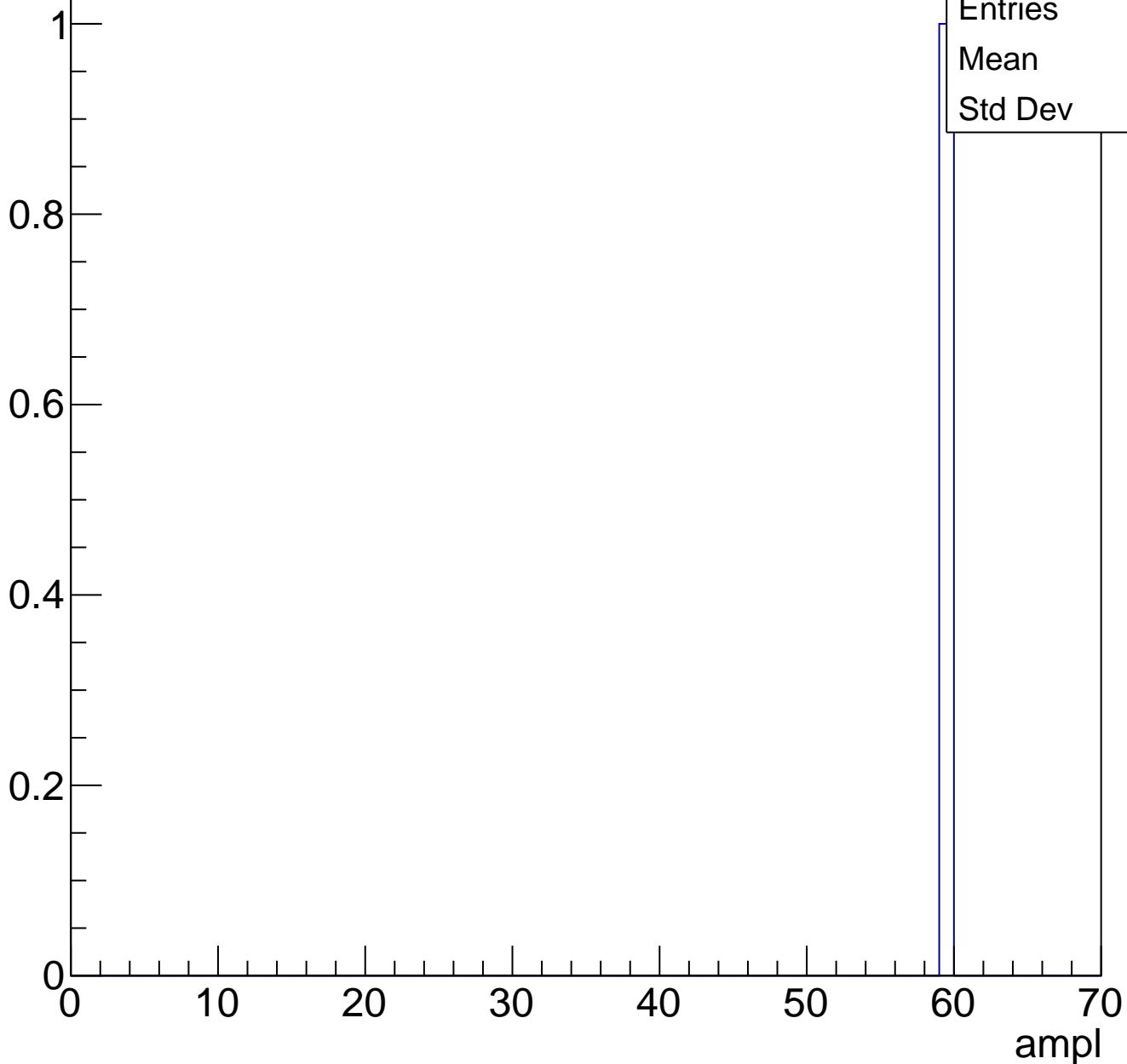
Entries	18
Mean	58.11
Std Dev	14.17



B1L103S, U5-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	59
Std Dev	0

B1L103S, U5-ch16, adc0

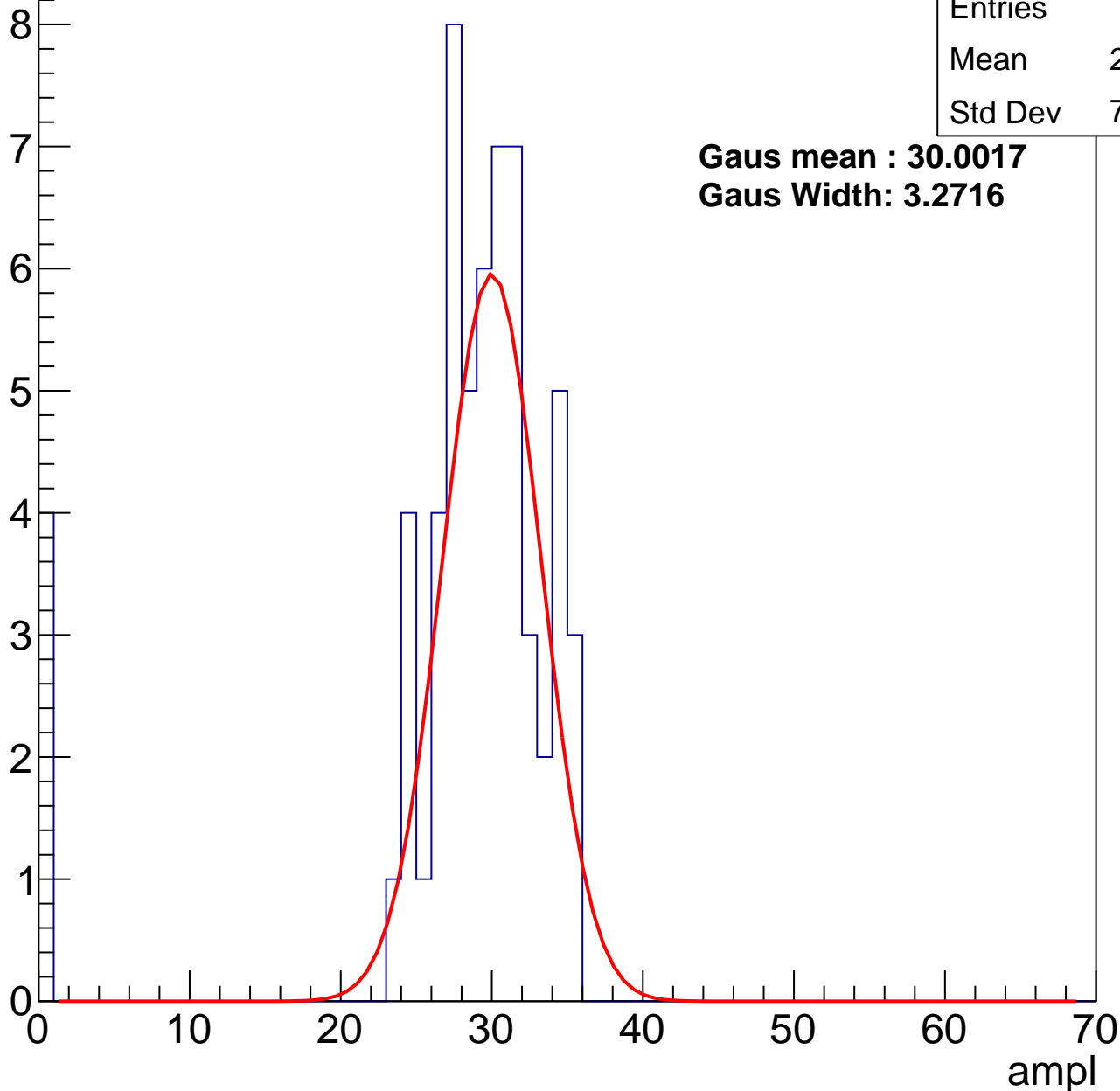
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	27.37
Std Dev	7.912

Gaus mean : 30.0017

Gaus Width: 3.2716



B1L103S, U5-ch16, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	36.15
Std Dev	3.787

Gaus mean : 37.0574

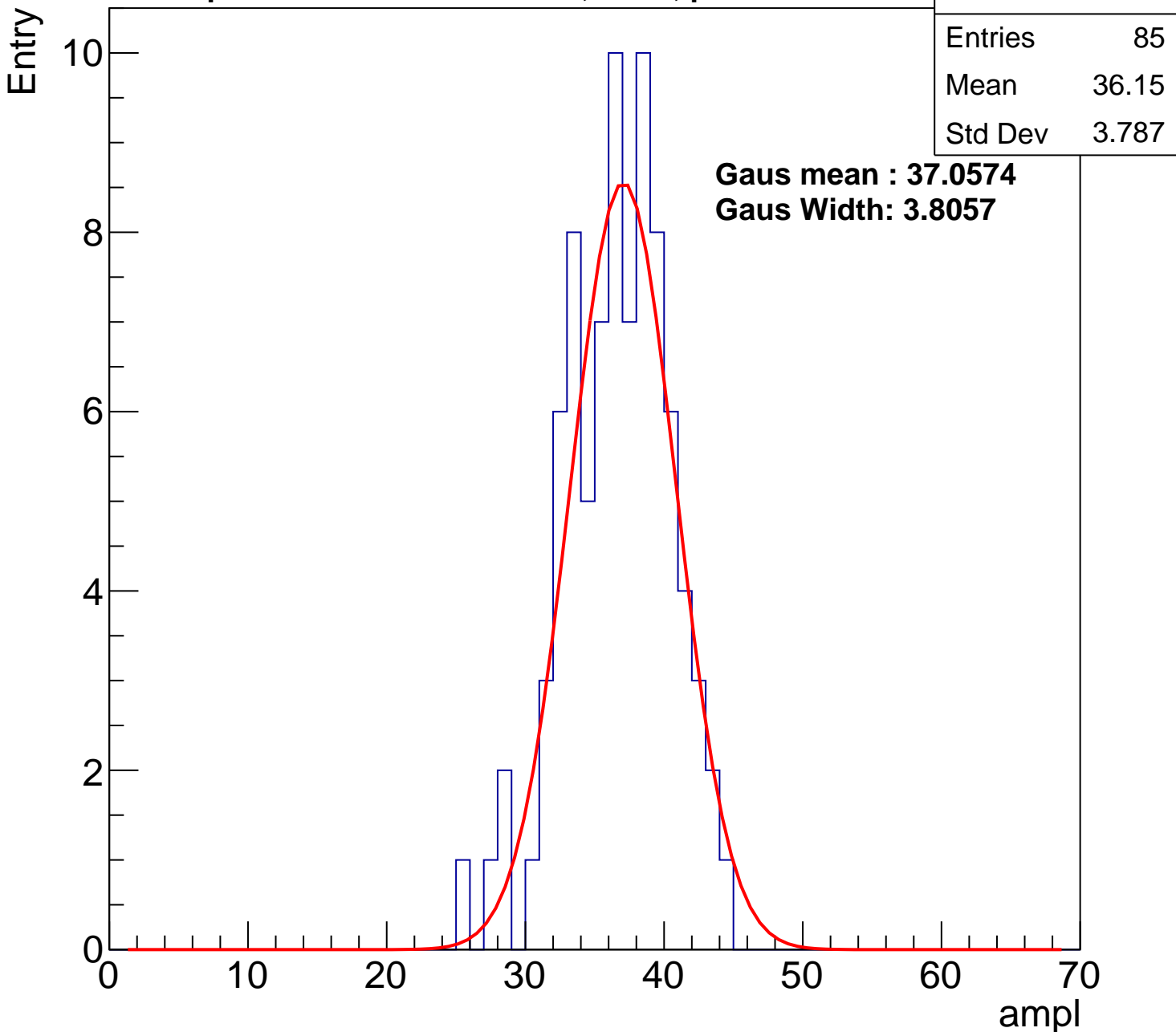
Gaus Width: 3.8057

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

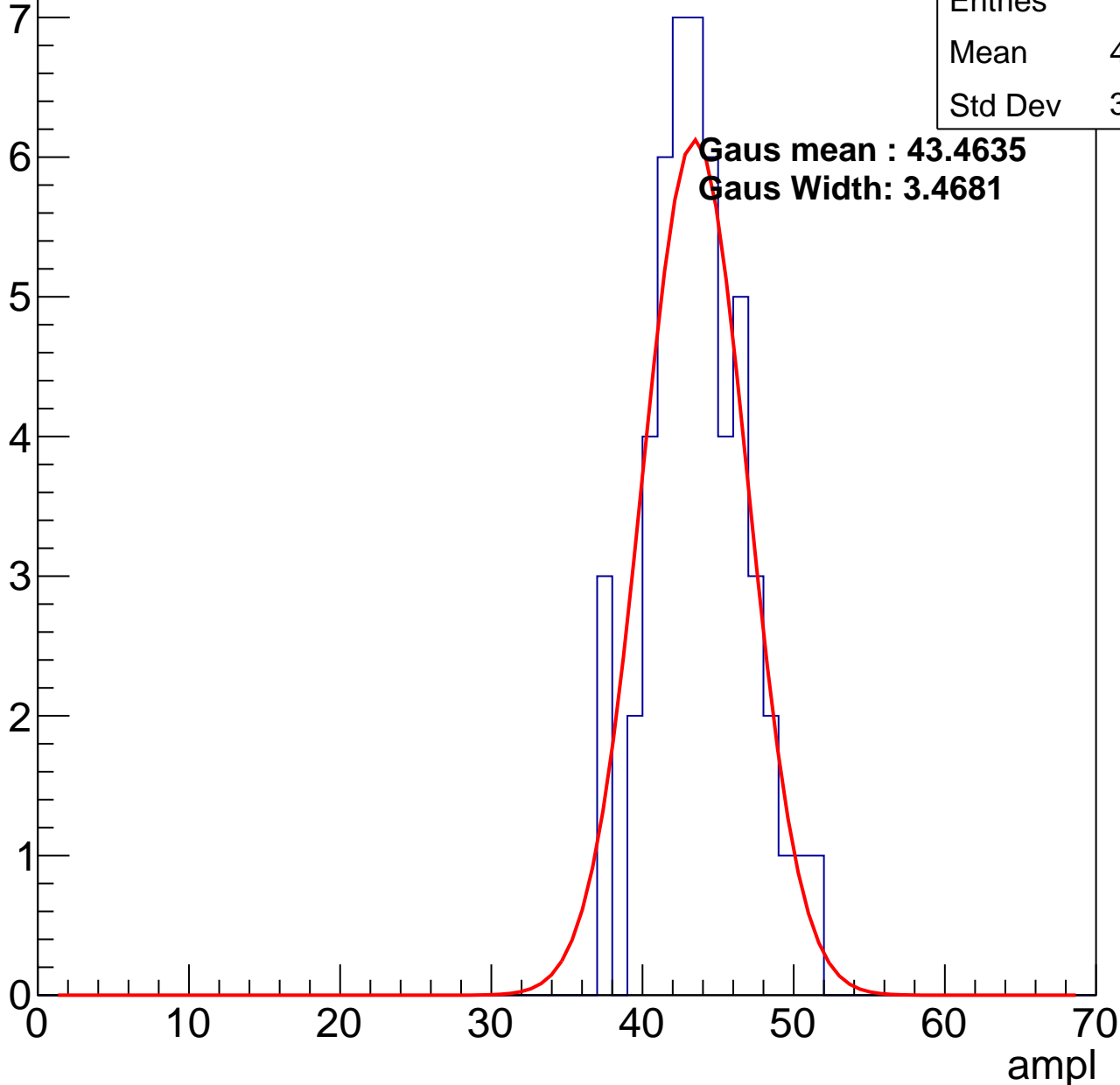


B1L103S, U5-ch16, adc2

calib_packv5_042523_0143.root, FC#7, port C2

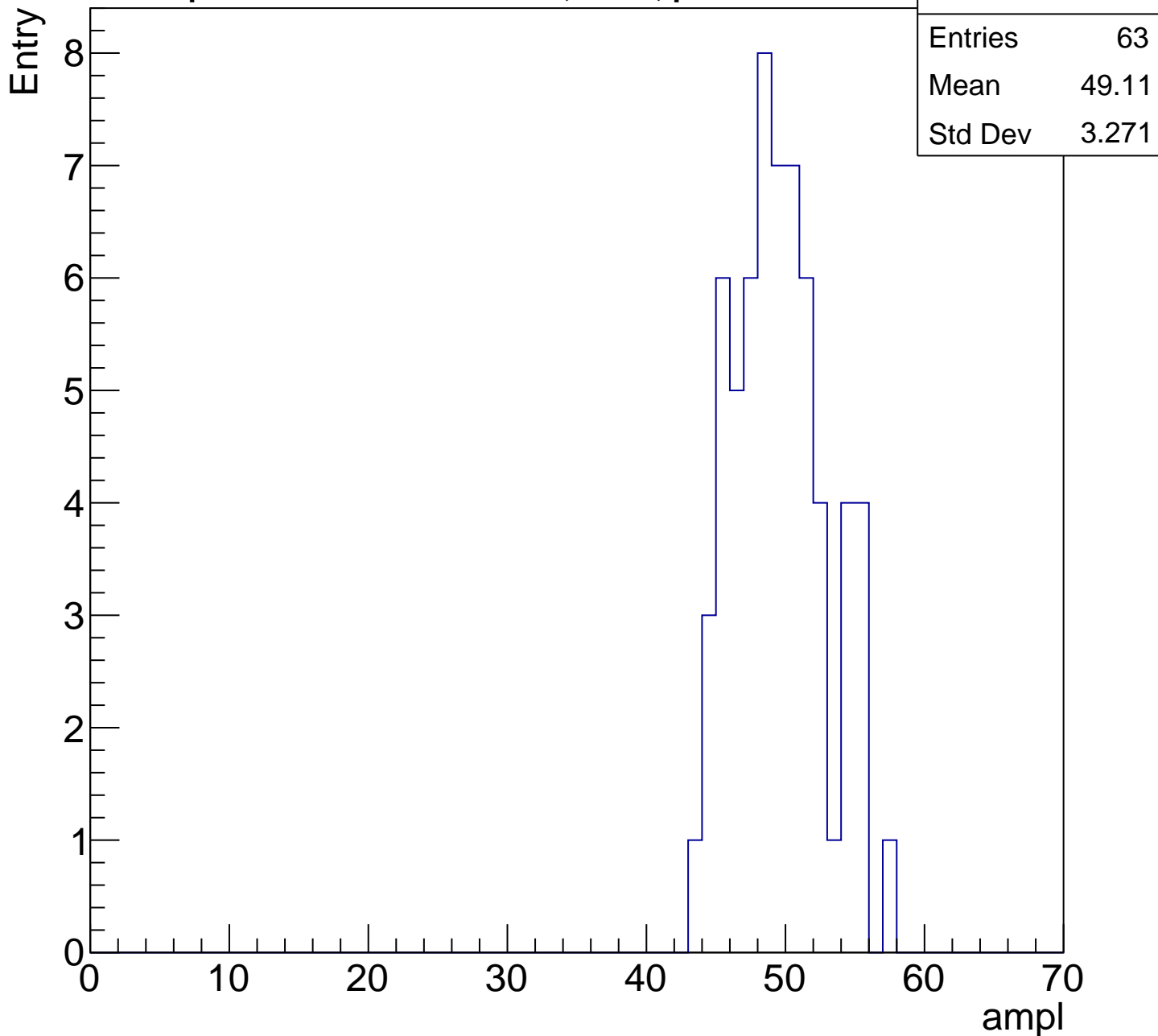
Entry

Entries	52
Mean	43.29
Std Dev	3.152



B1L103S, U5-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

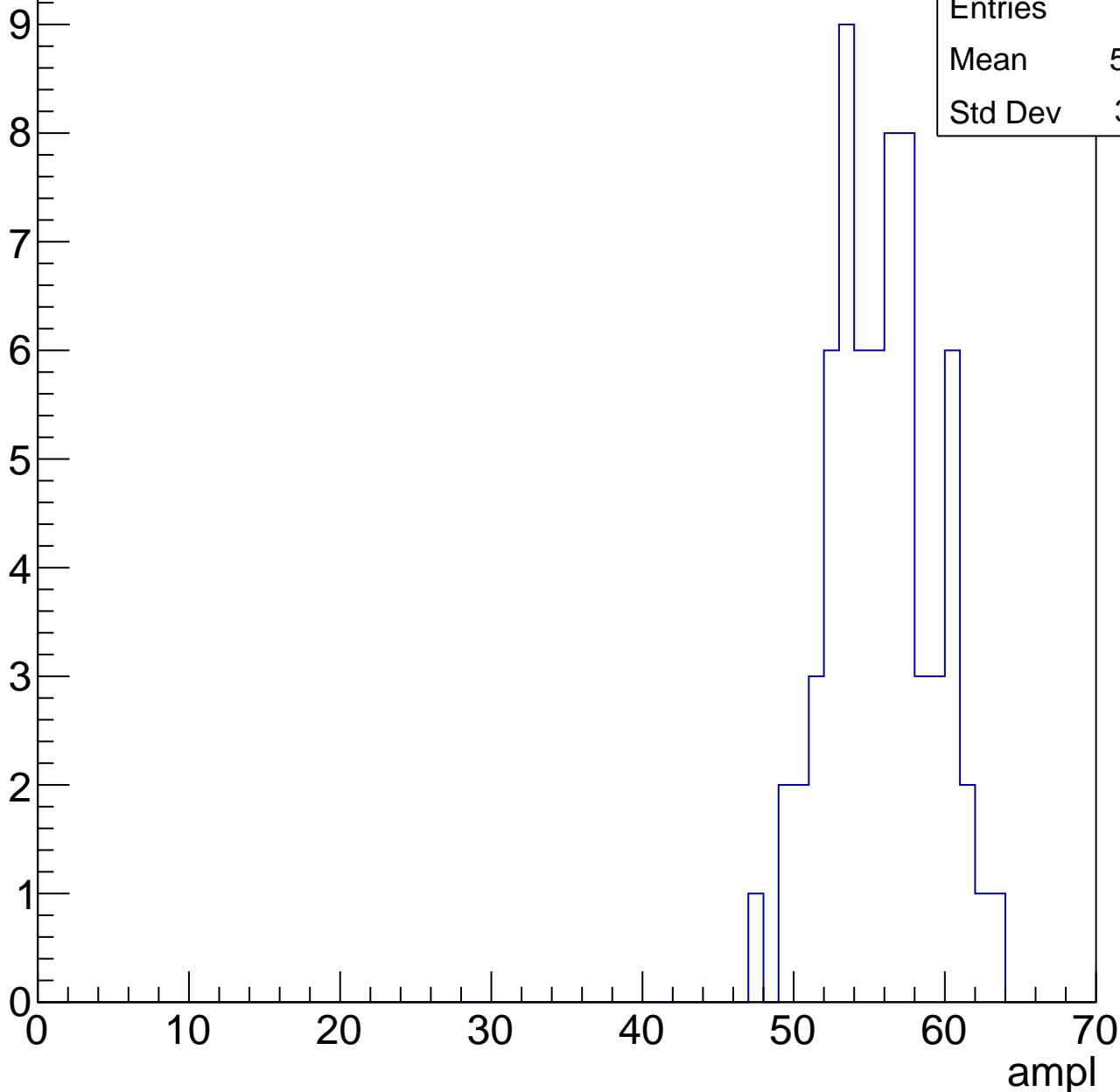


B1L103S, U5-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	55.27
Std Dev	3.401

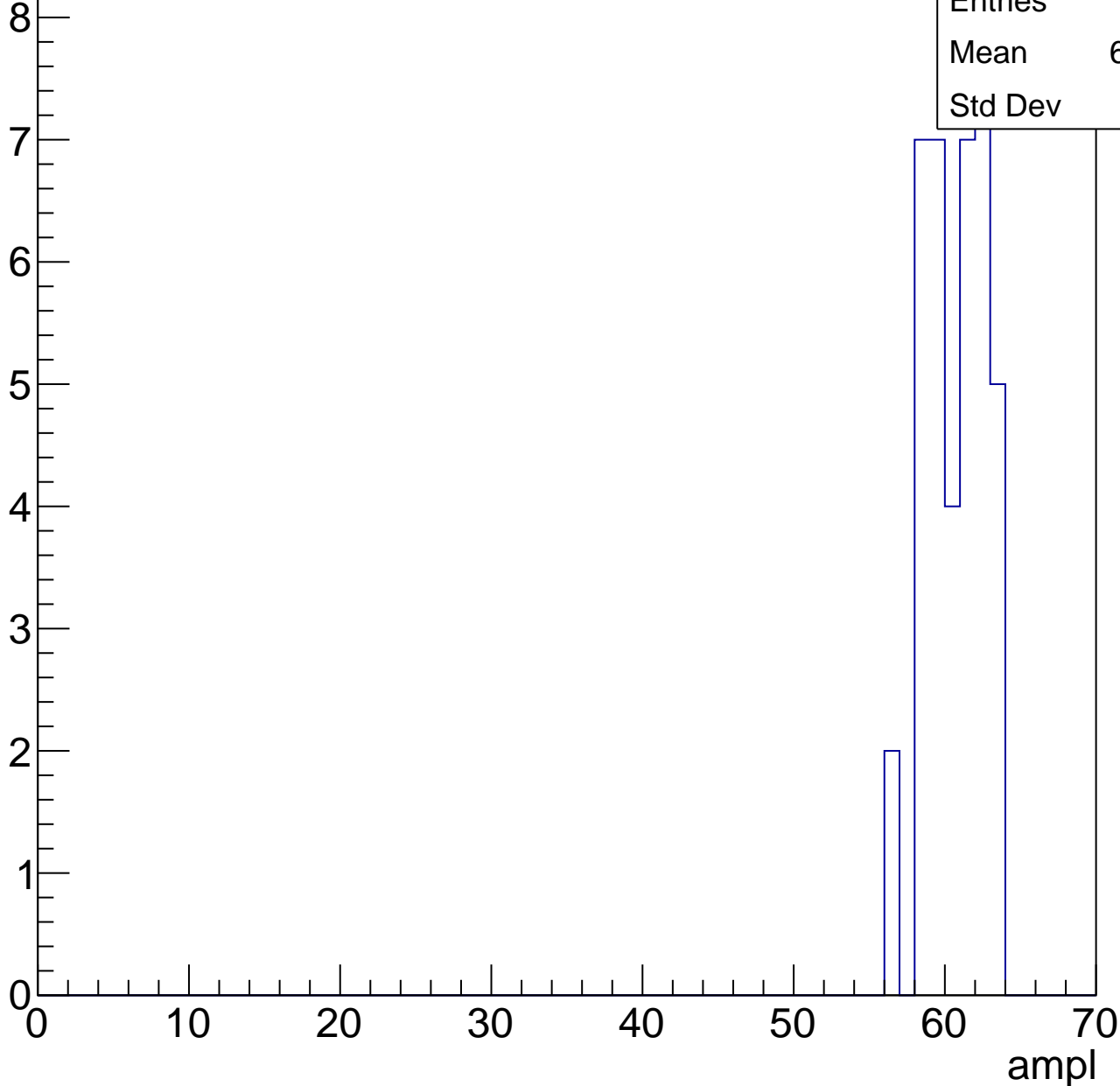


B1L103S, U5-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

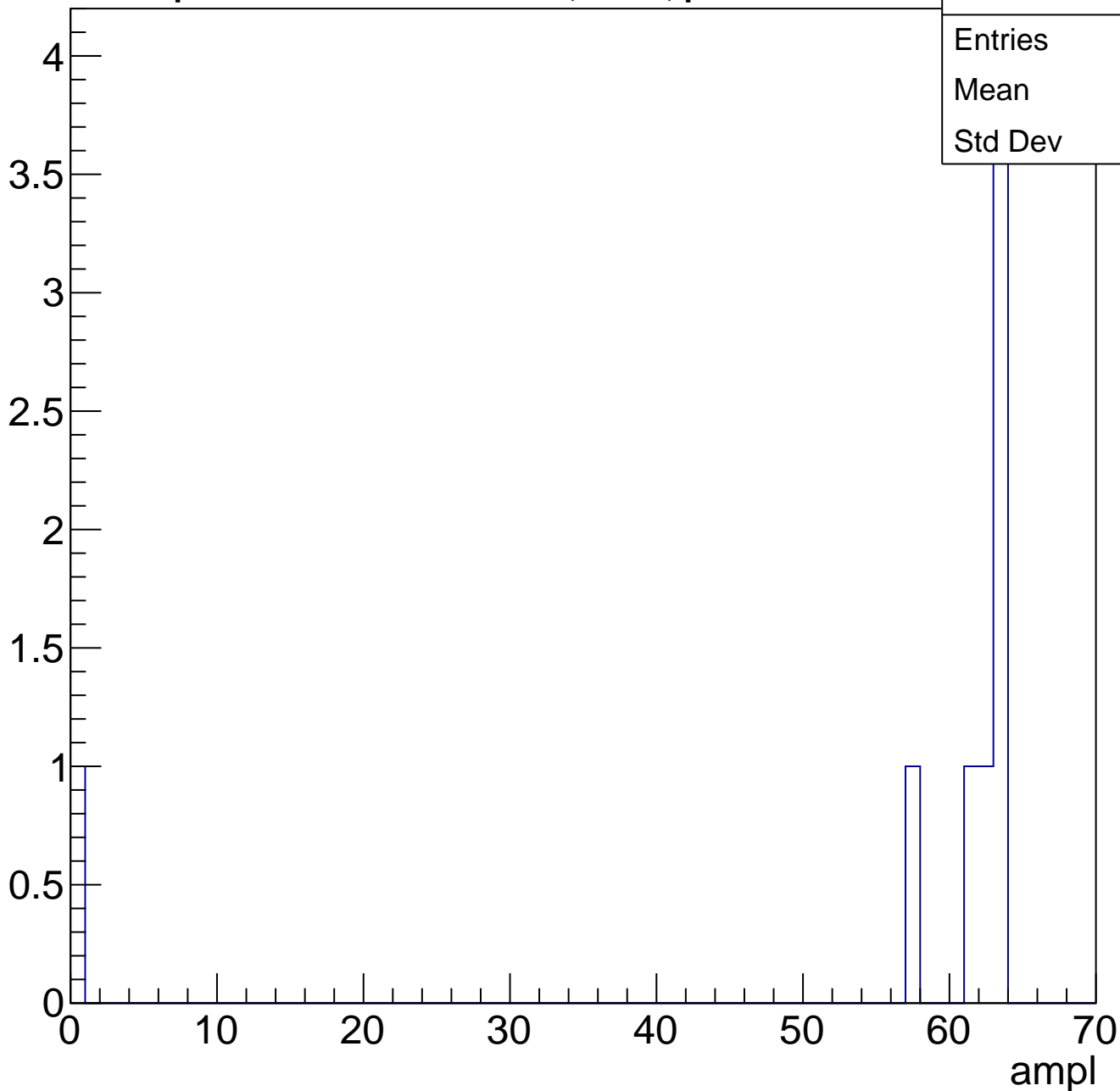
Entries	40
Mean	60.23
Std Dev	1.93



B1L103S, U5-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch17, adc0

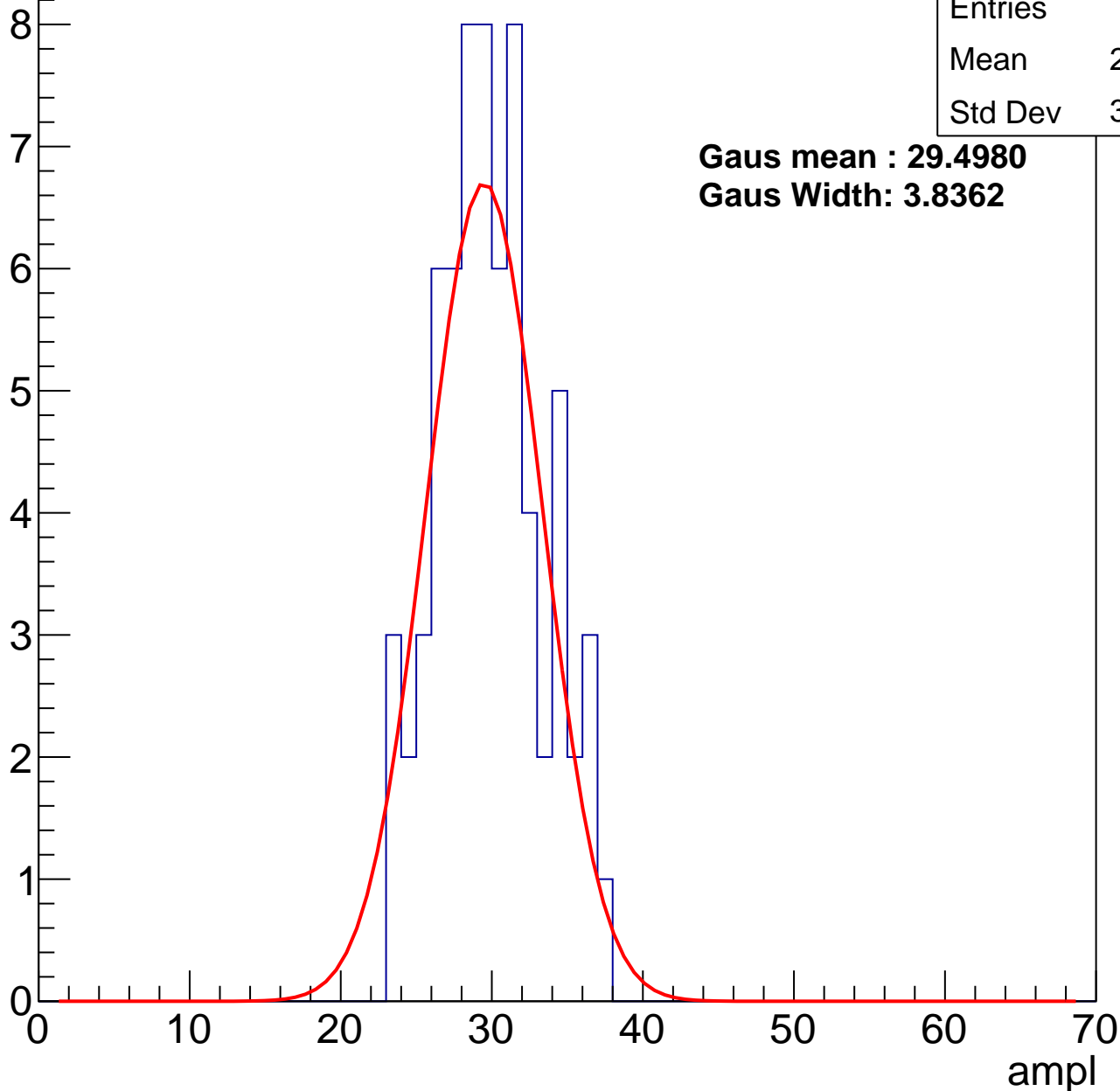
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.45
Std Dev	3.439

Gaus mean : 29.4980

Gaus Width: 3.8362



B1L103S, U5-ch17, adc1

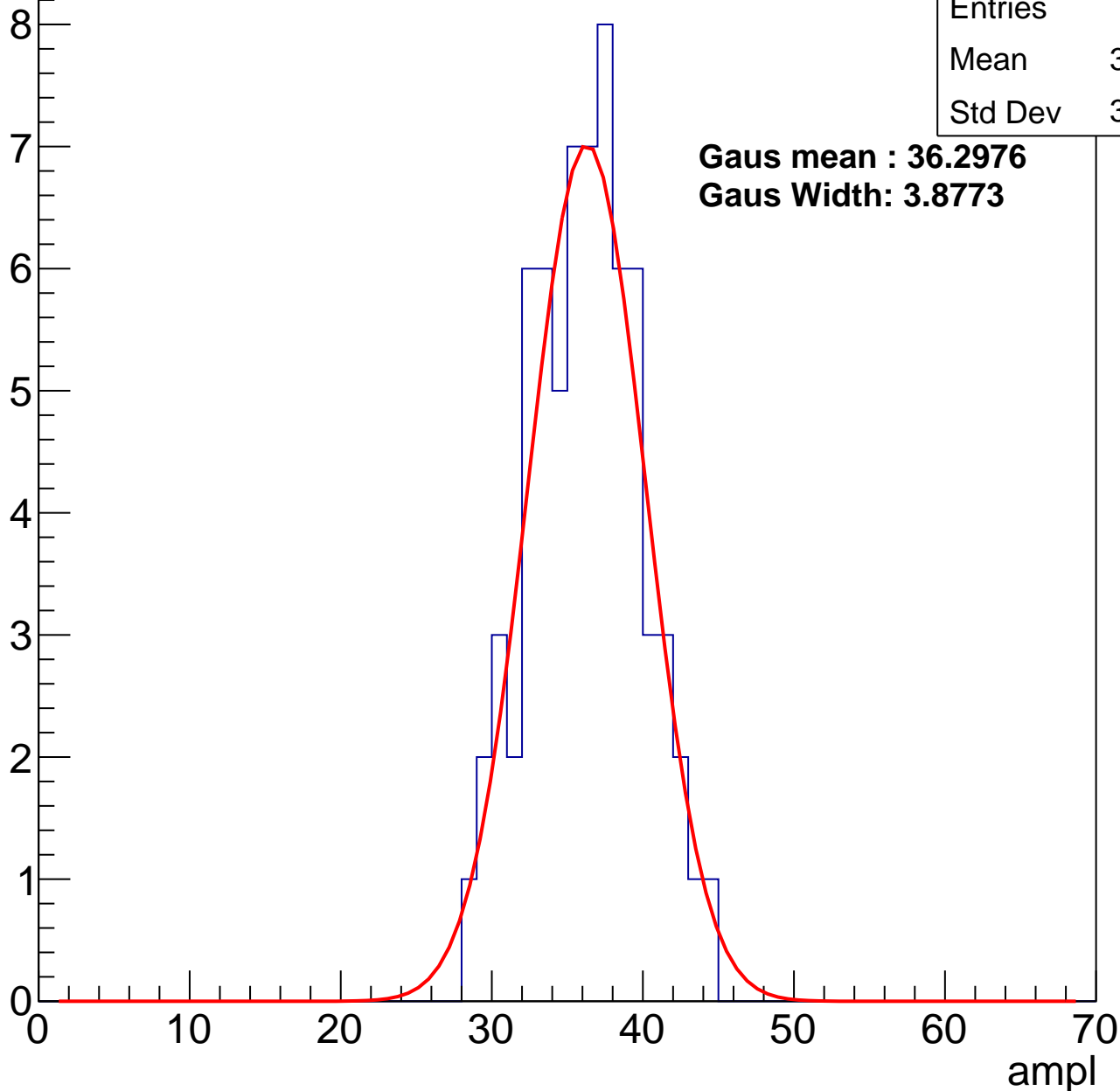
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.75
Std Dev	3.585

Gaus mean : 36.2976

Gaus Width: 3.8773



B1L103S, U5-ch17, adc2

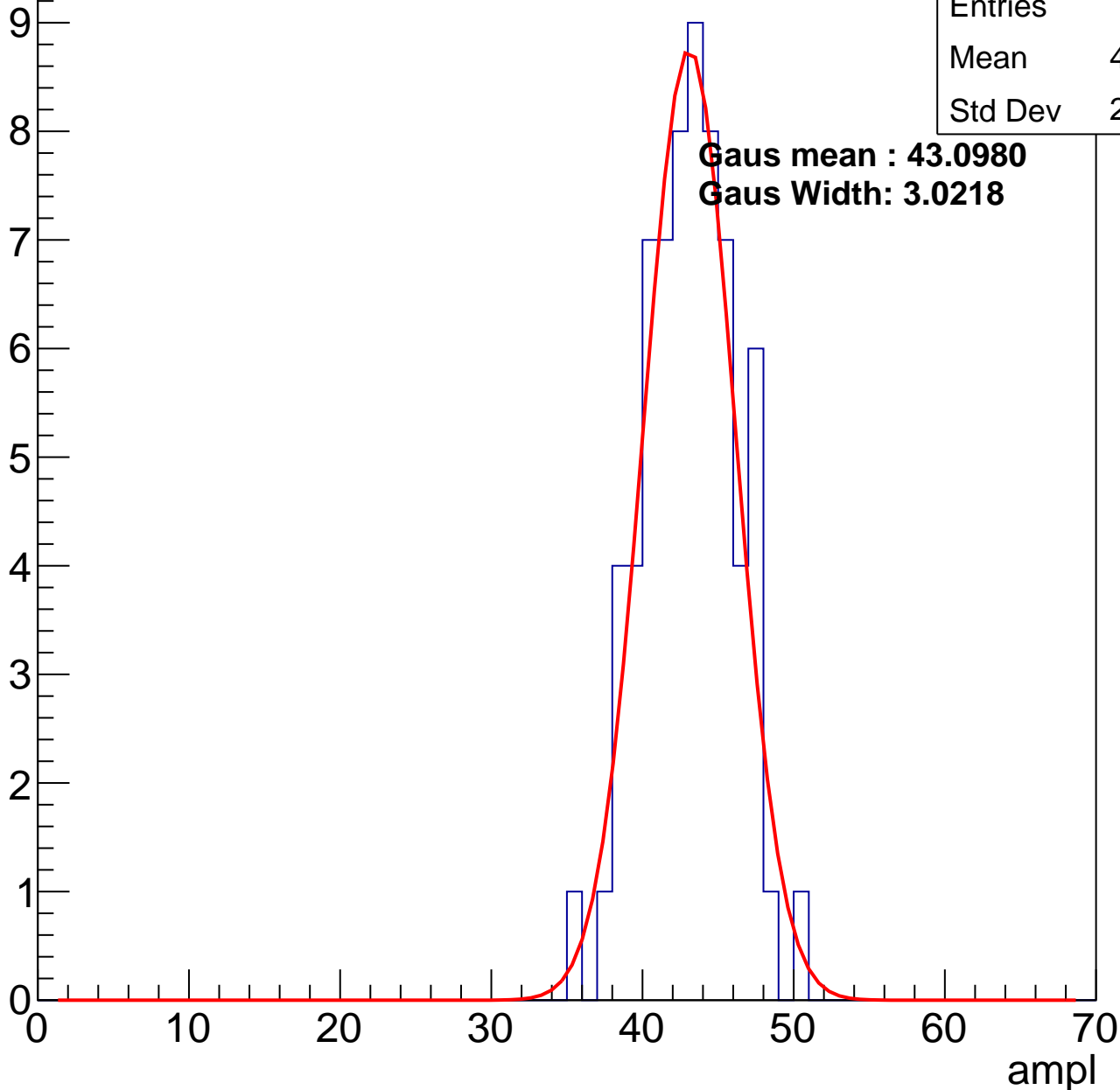
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.66
Std Dev	2.964

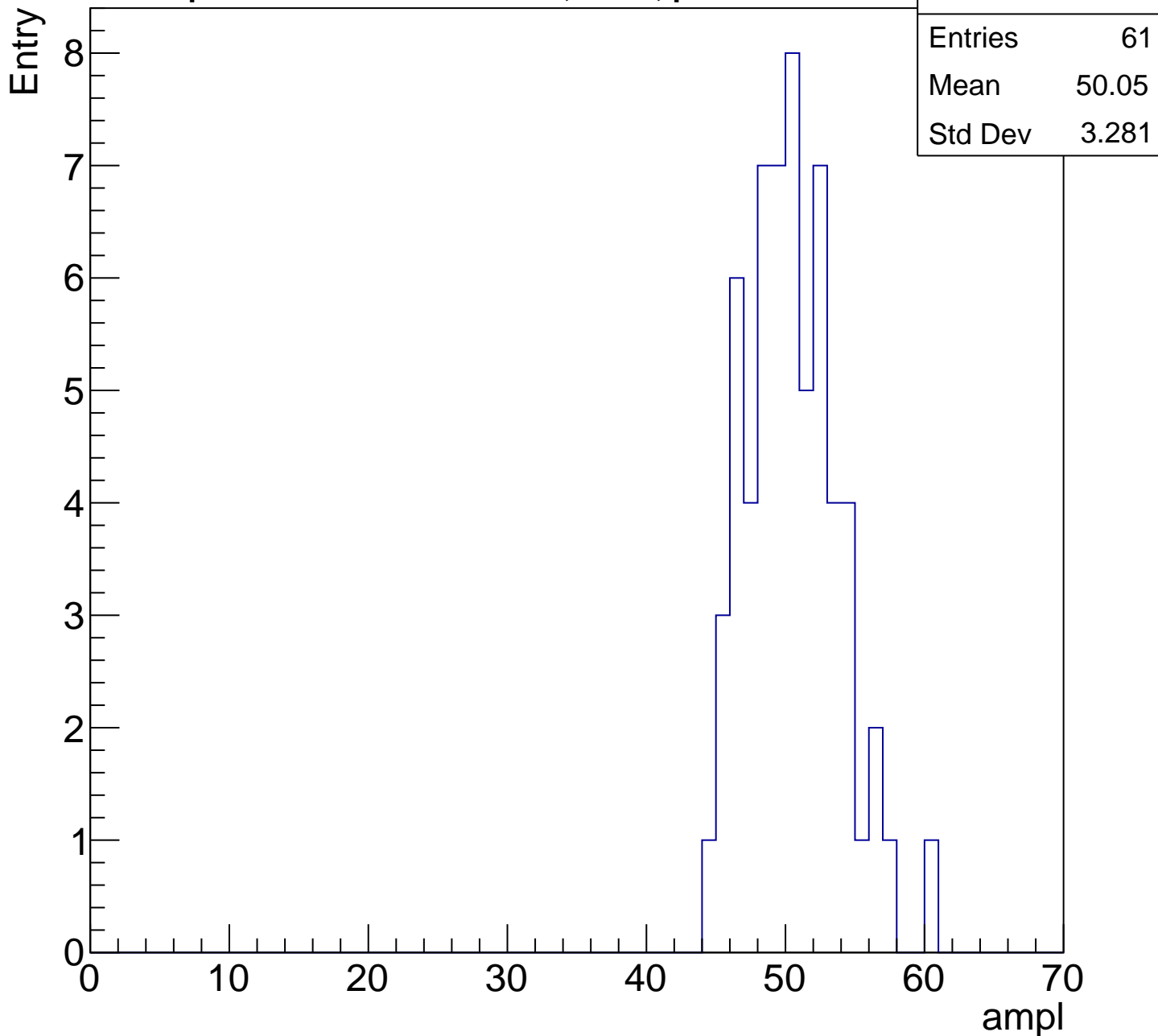
Gaus mean : 43.0980

Gaus Width: 3.0218



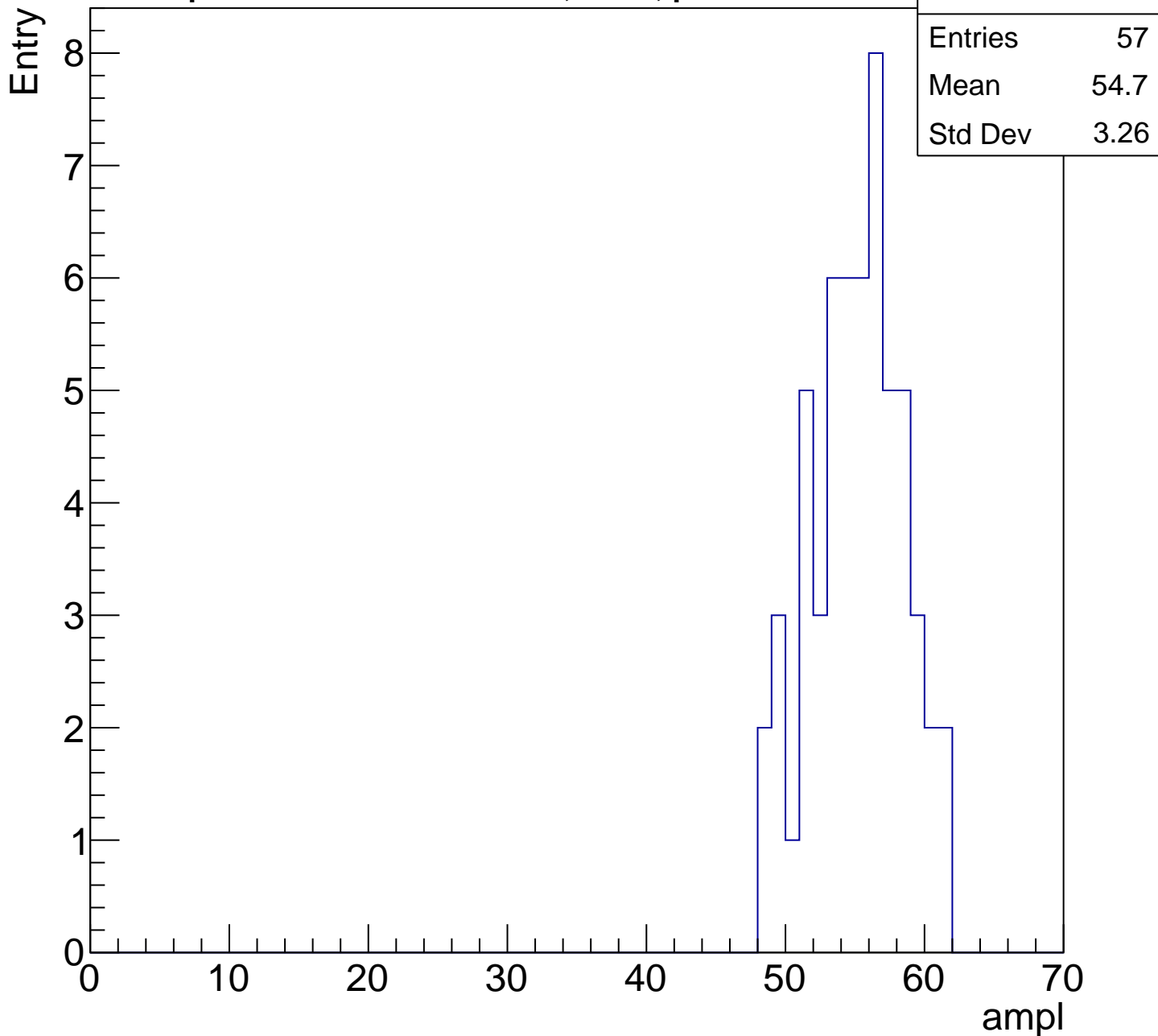
B1L103S, U5-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

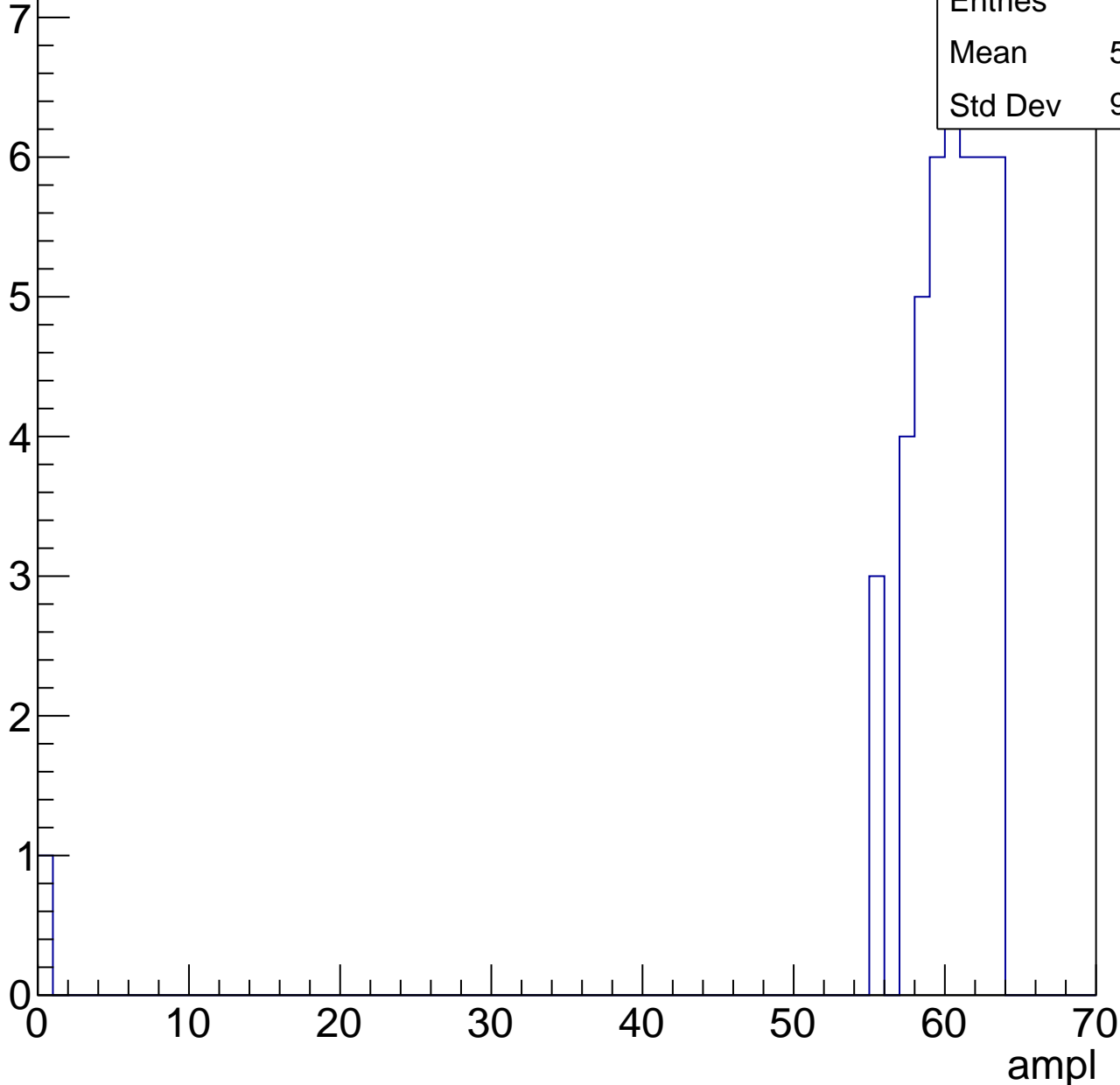


B1L103S, U5-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

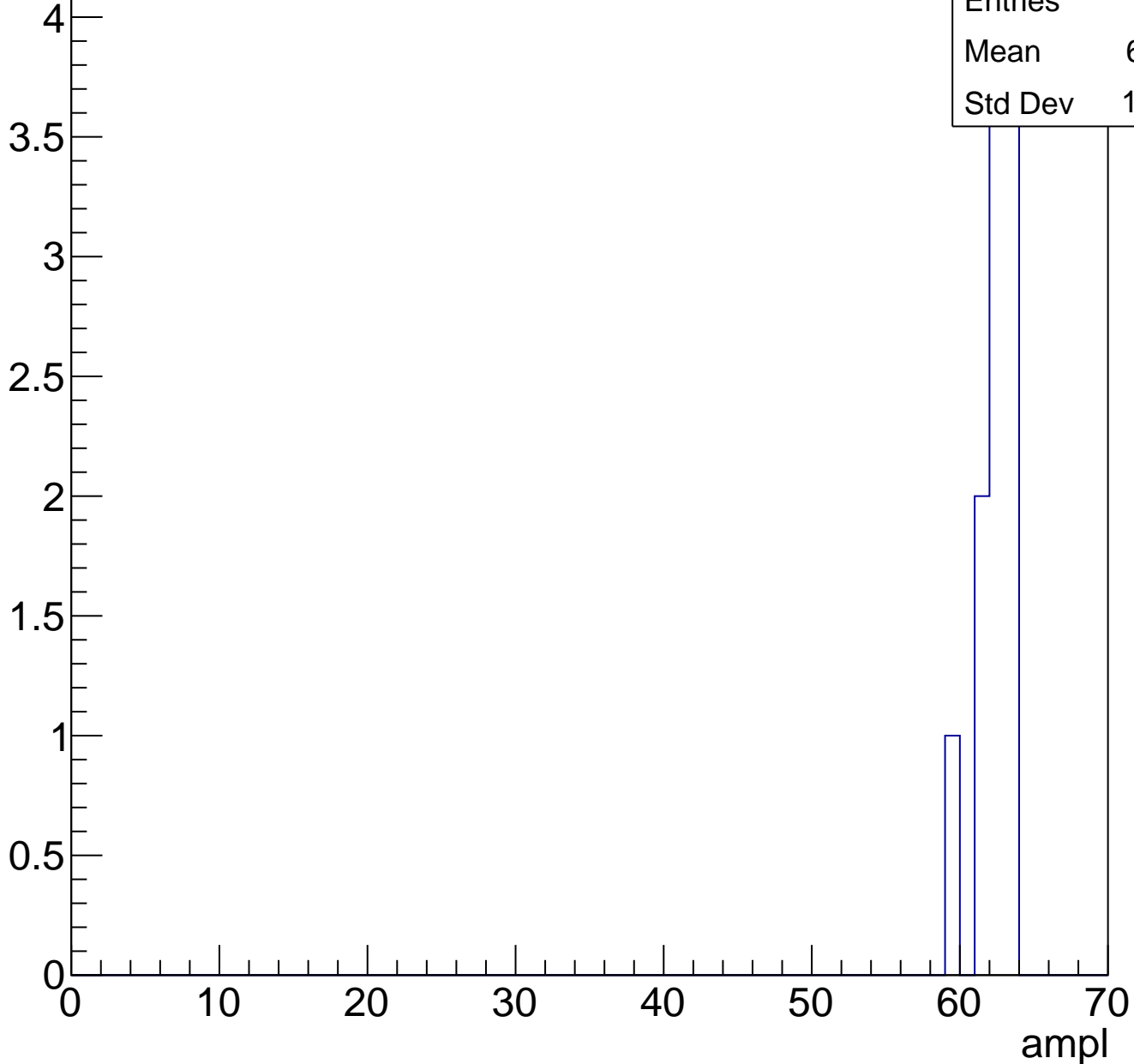
Entries	44
Mean	58.48
Std Dev	9.194



B1L103S, U5-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

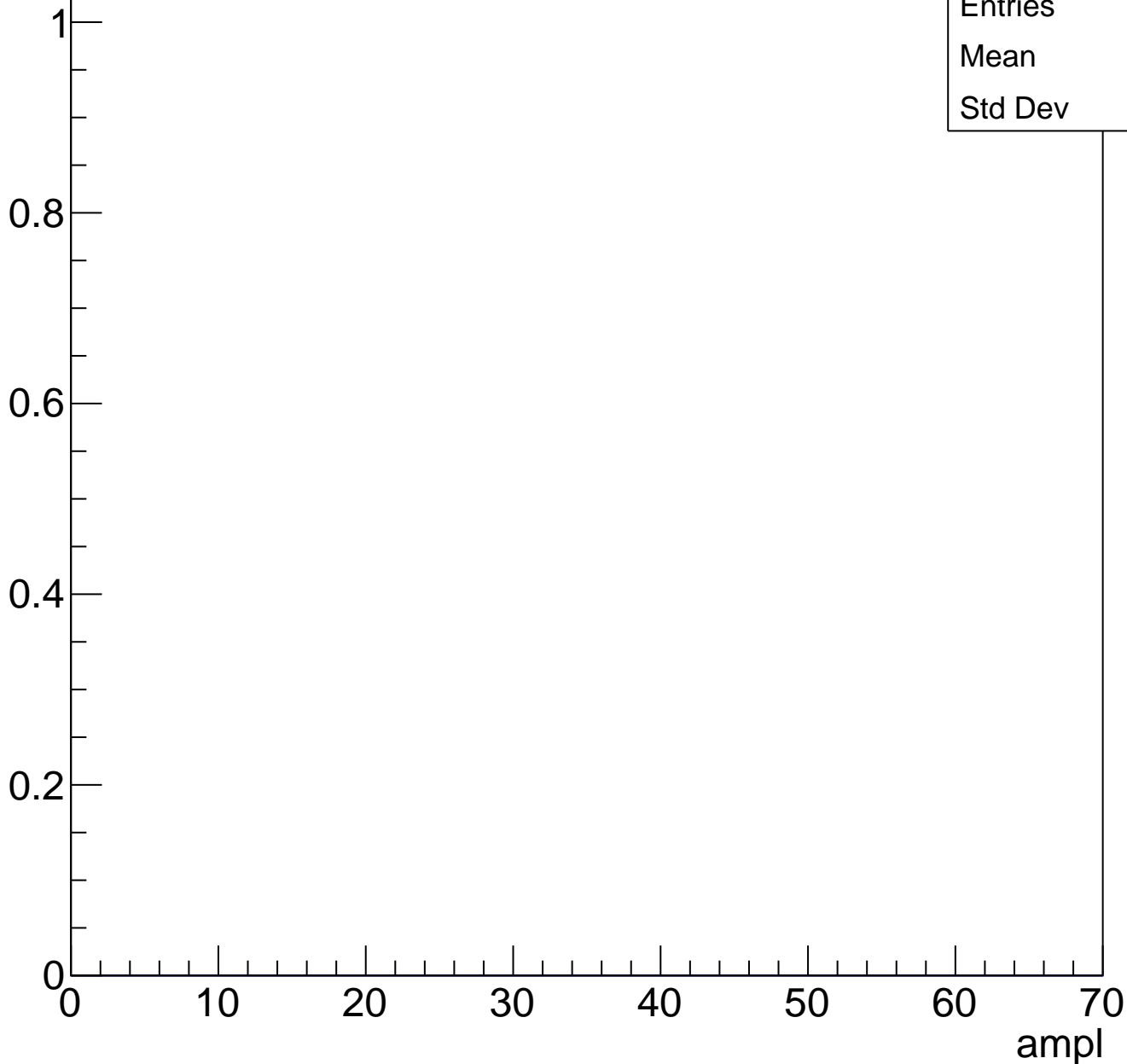
Entry



B1L103S, U5-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch18, adc0

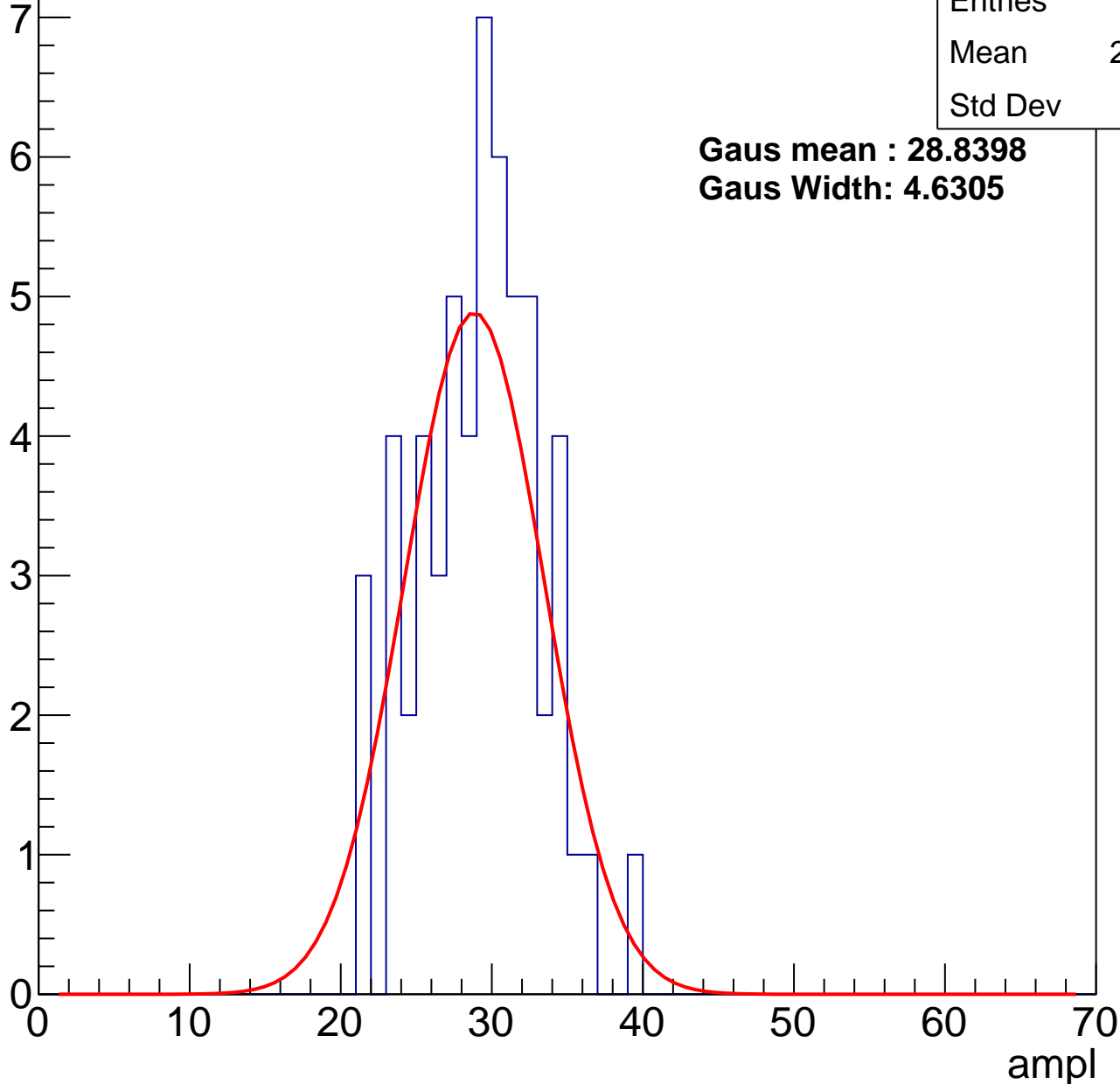
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	28.74
Std Dev	3.94

Gaus mean : 28.8398

Gaus Width: 4.6305



B1L103S, U5-ch18, adc1

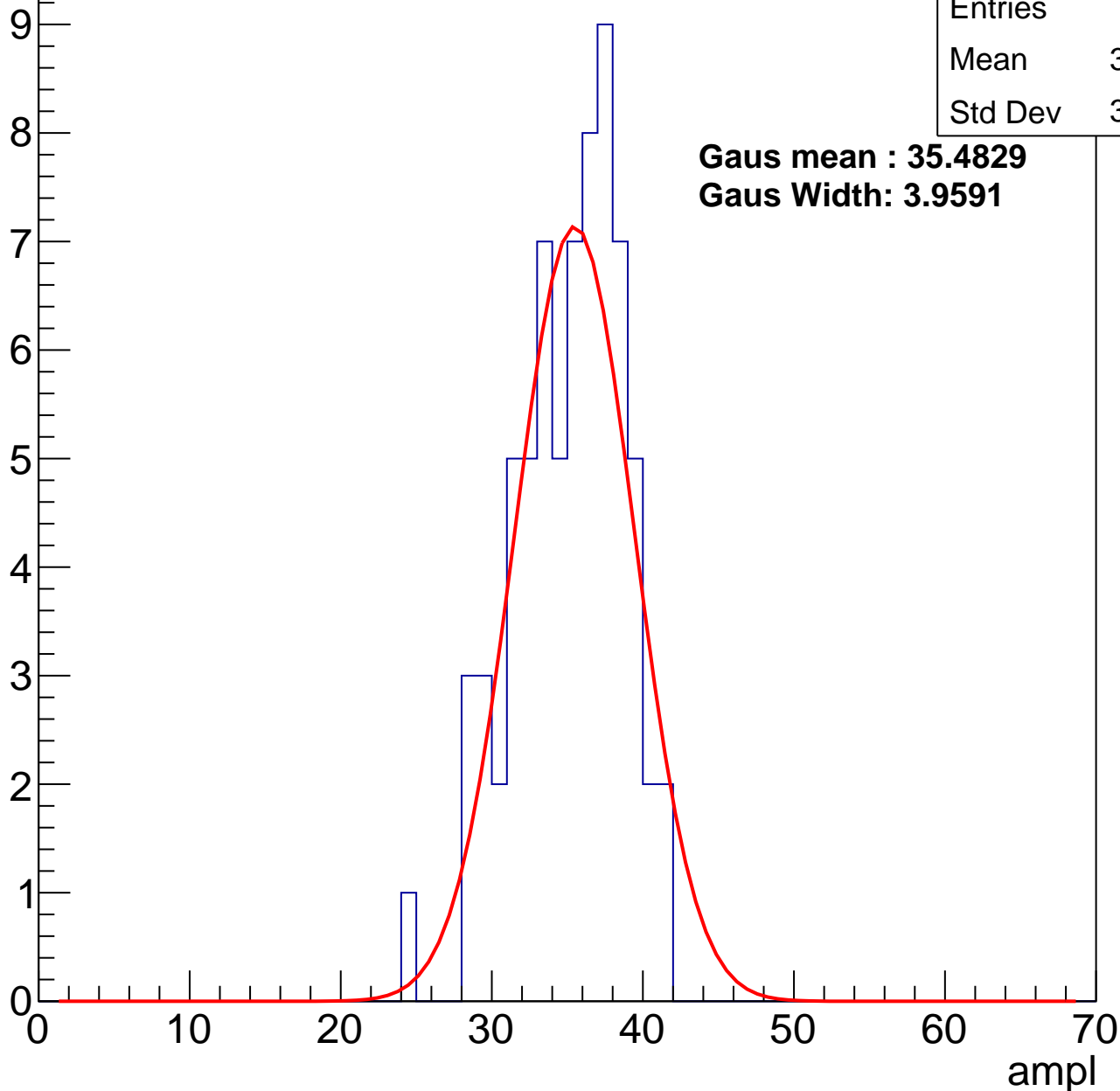
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	34.65
Std Dev	3.529

Gaus mean : 35.4829

Gaus Width: 3.9591



B1L103S, U5-ch18, adc2

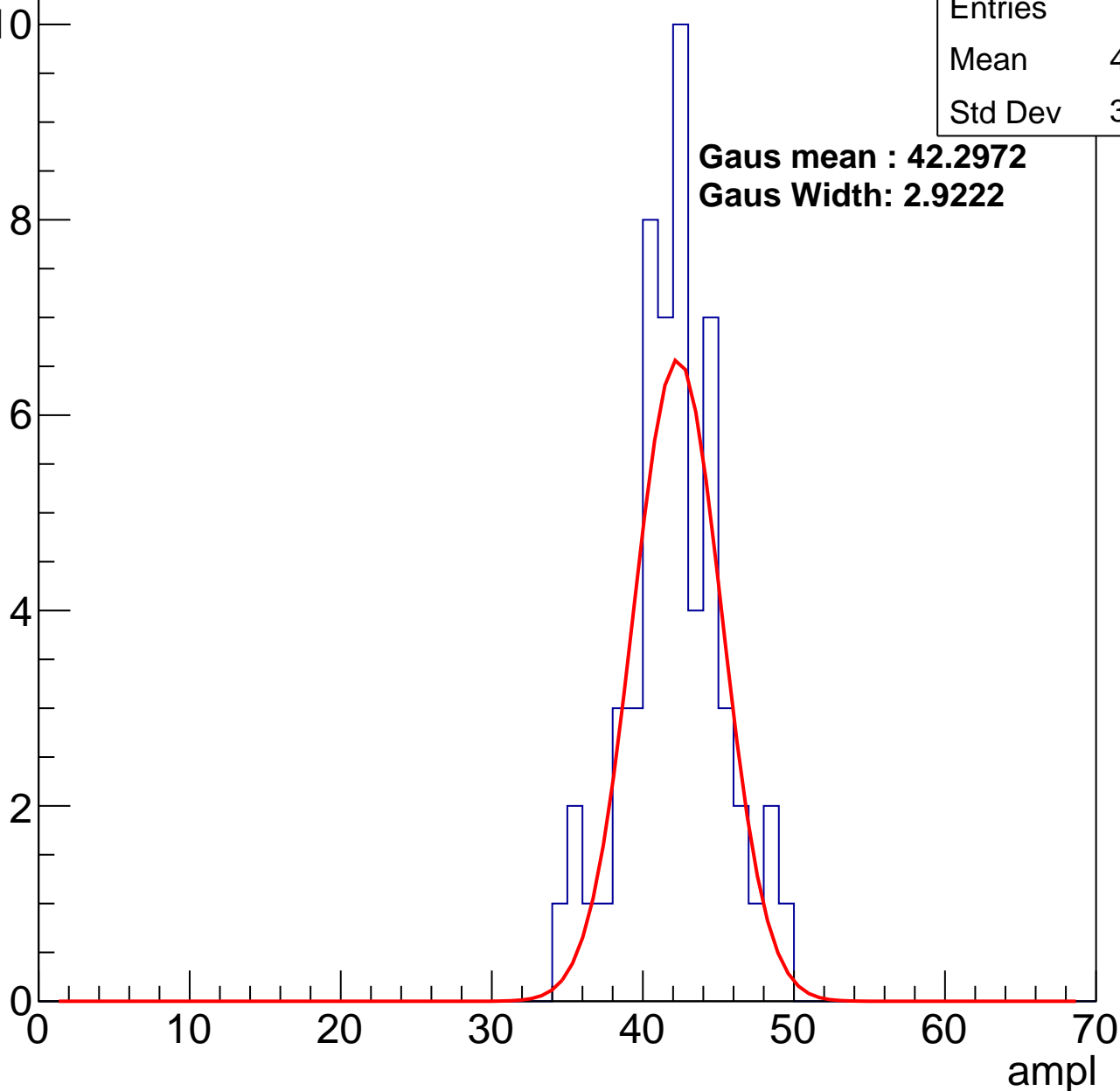
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	41.68
Std Dev	3.185

Gaus mean : 42.2972

Gaus Width: 2.9222



B1L103S, U5-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

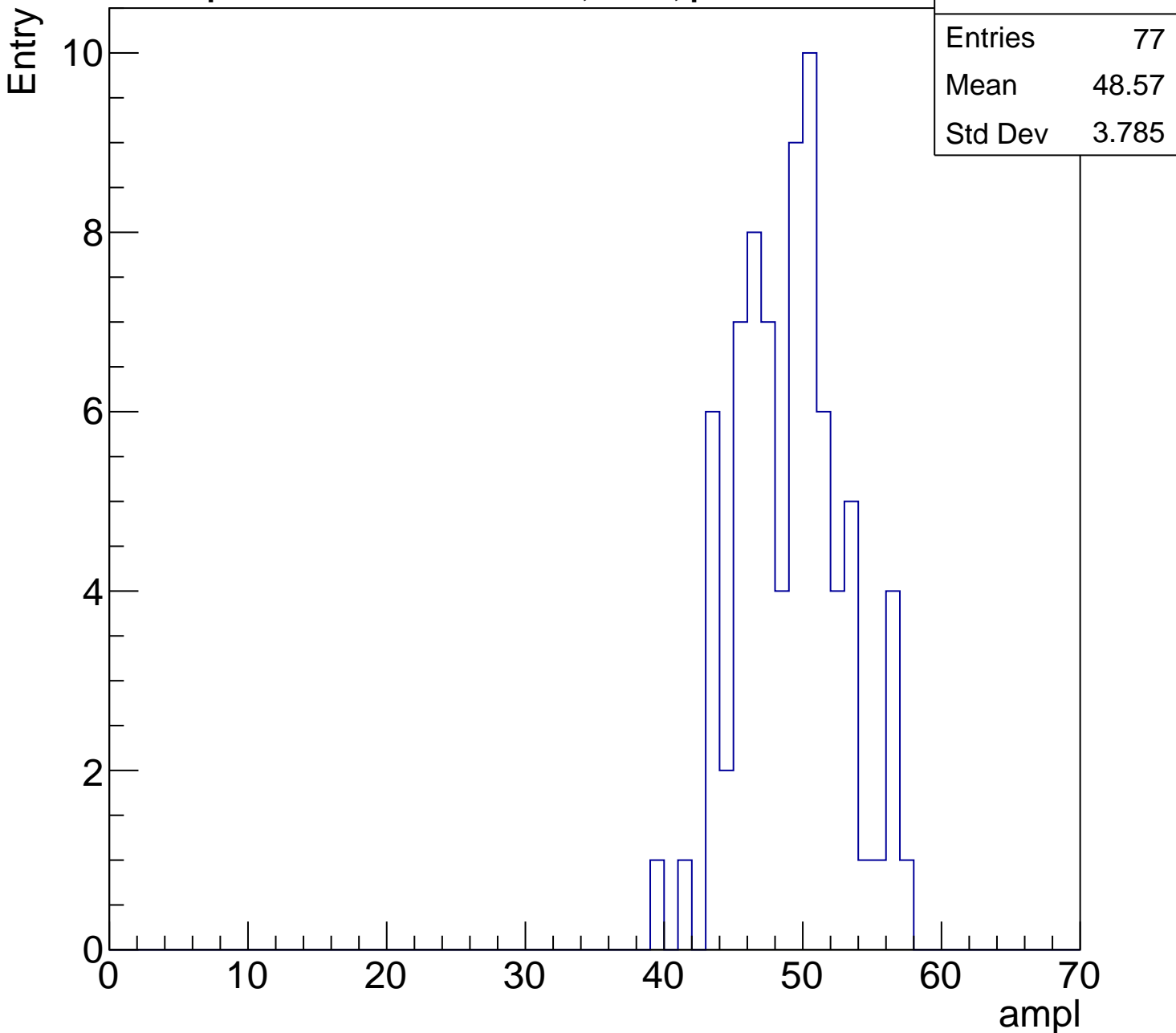
Entries	77
Mean	48.57
Std Dev	3.785

Entry

10
8
6
4
2
0

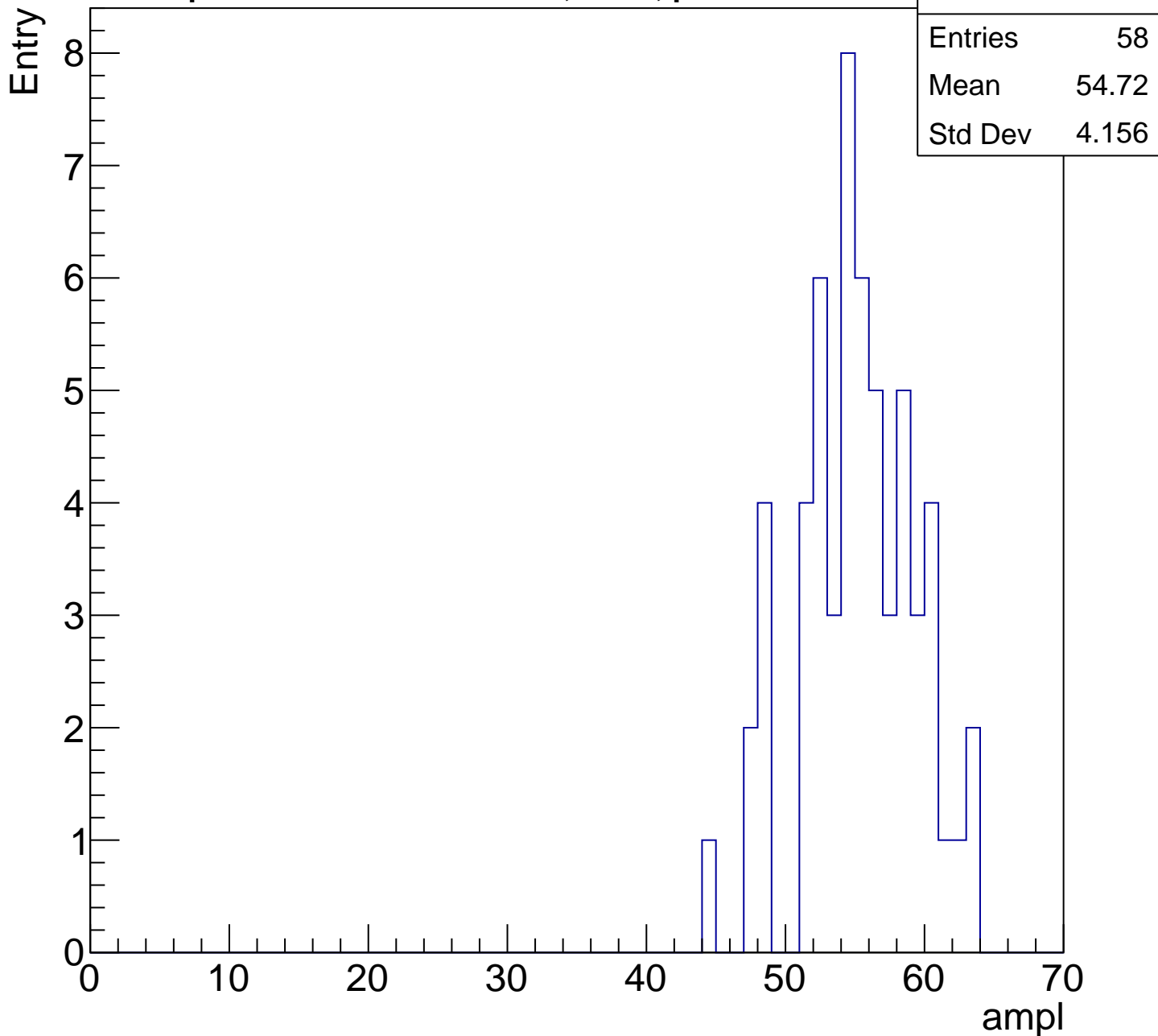
0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

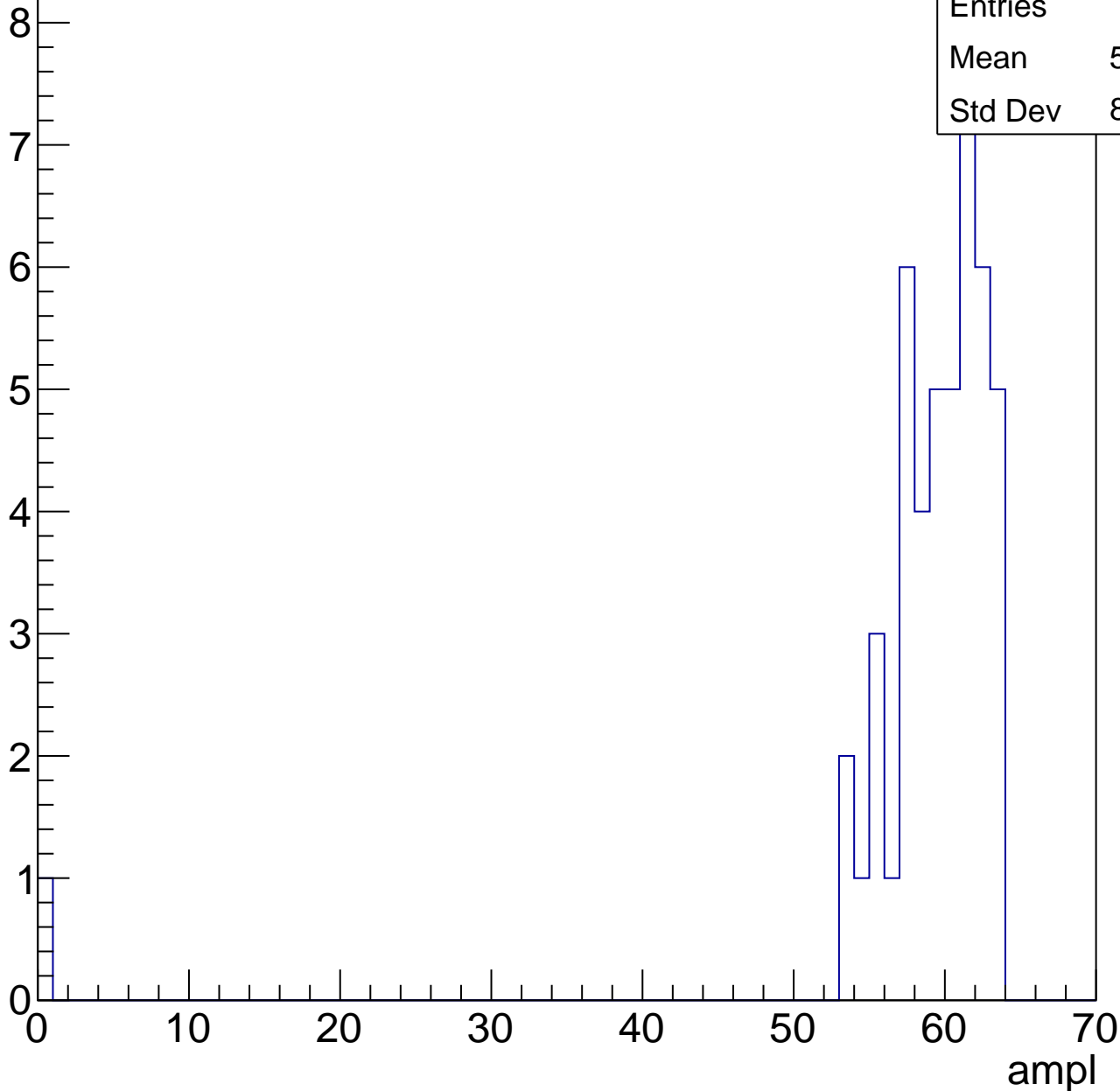


B1L103S, U5-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

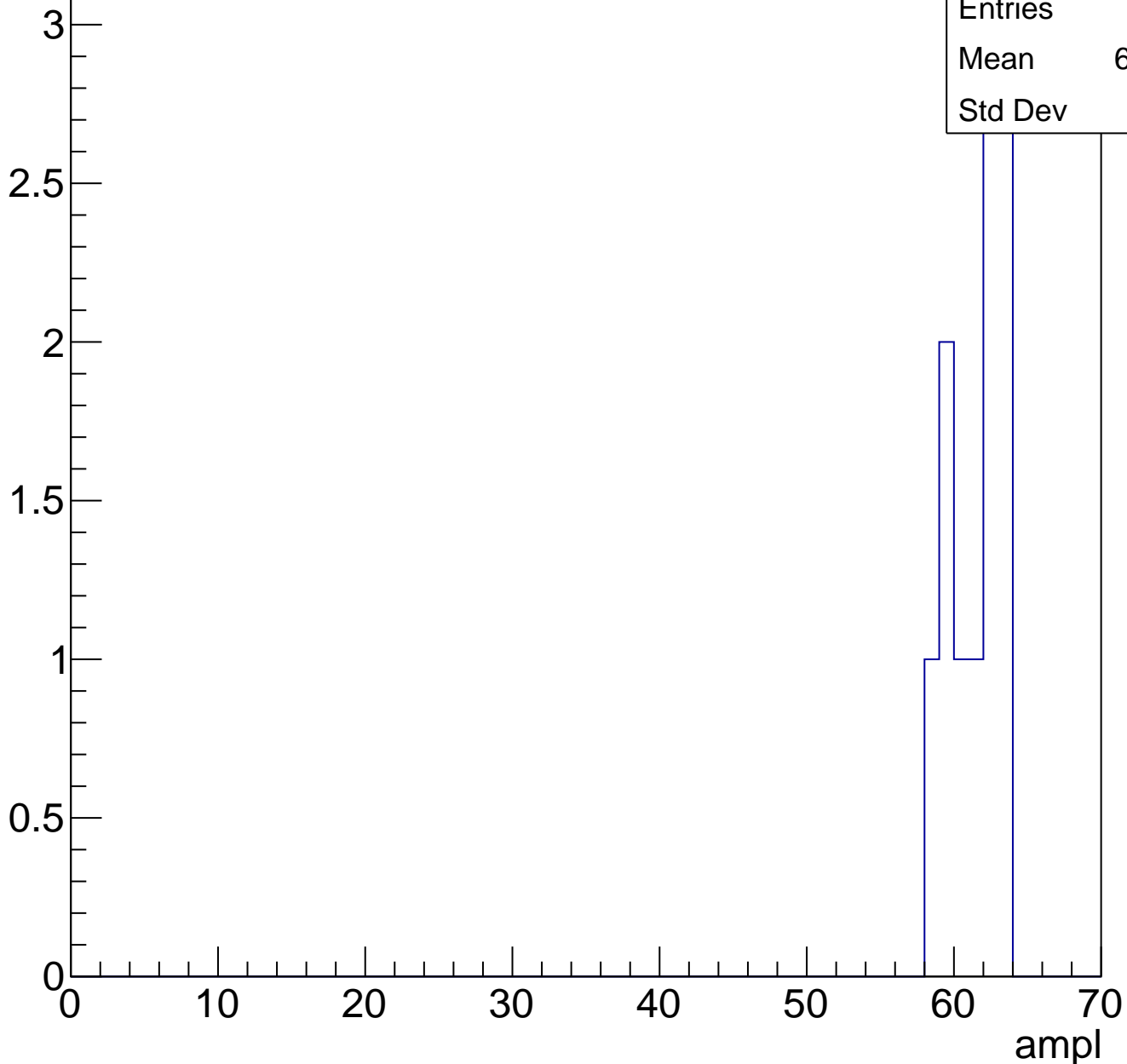
Entries	47
Mean	57.98
Std Dev	8.974



B1L103S, U5-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch19, adc0

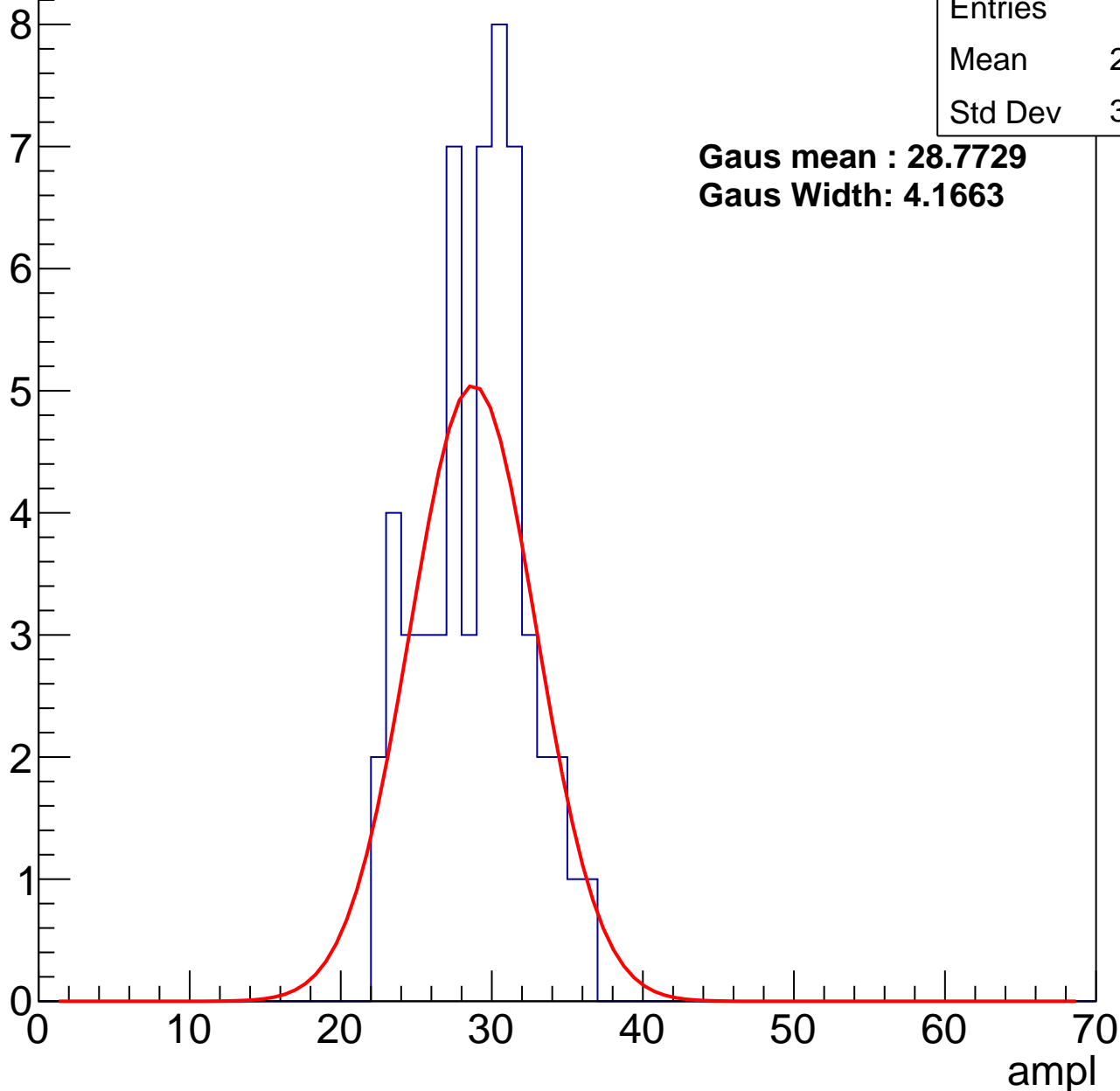
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	28.48
Std Dev	3.375

Gaus mean : 28.7729

Gaus Width: 4.1663



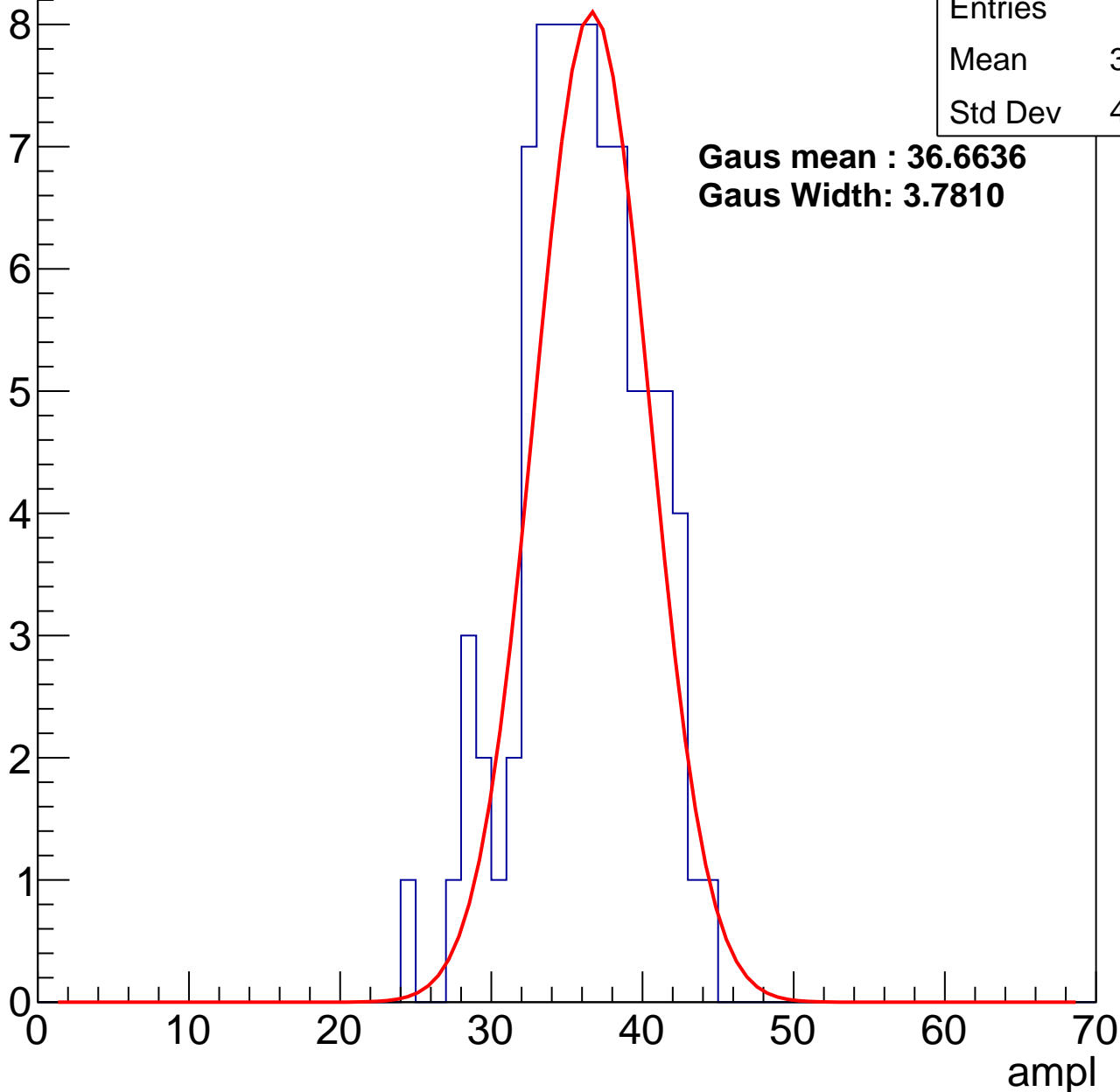
B1L103S, U5-ch19, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	35.63
Std Dev	4.017

Gaus mean : 36.6636
Gaus Width: 3.7810



B1L103S, U5-ch19, adc2

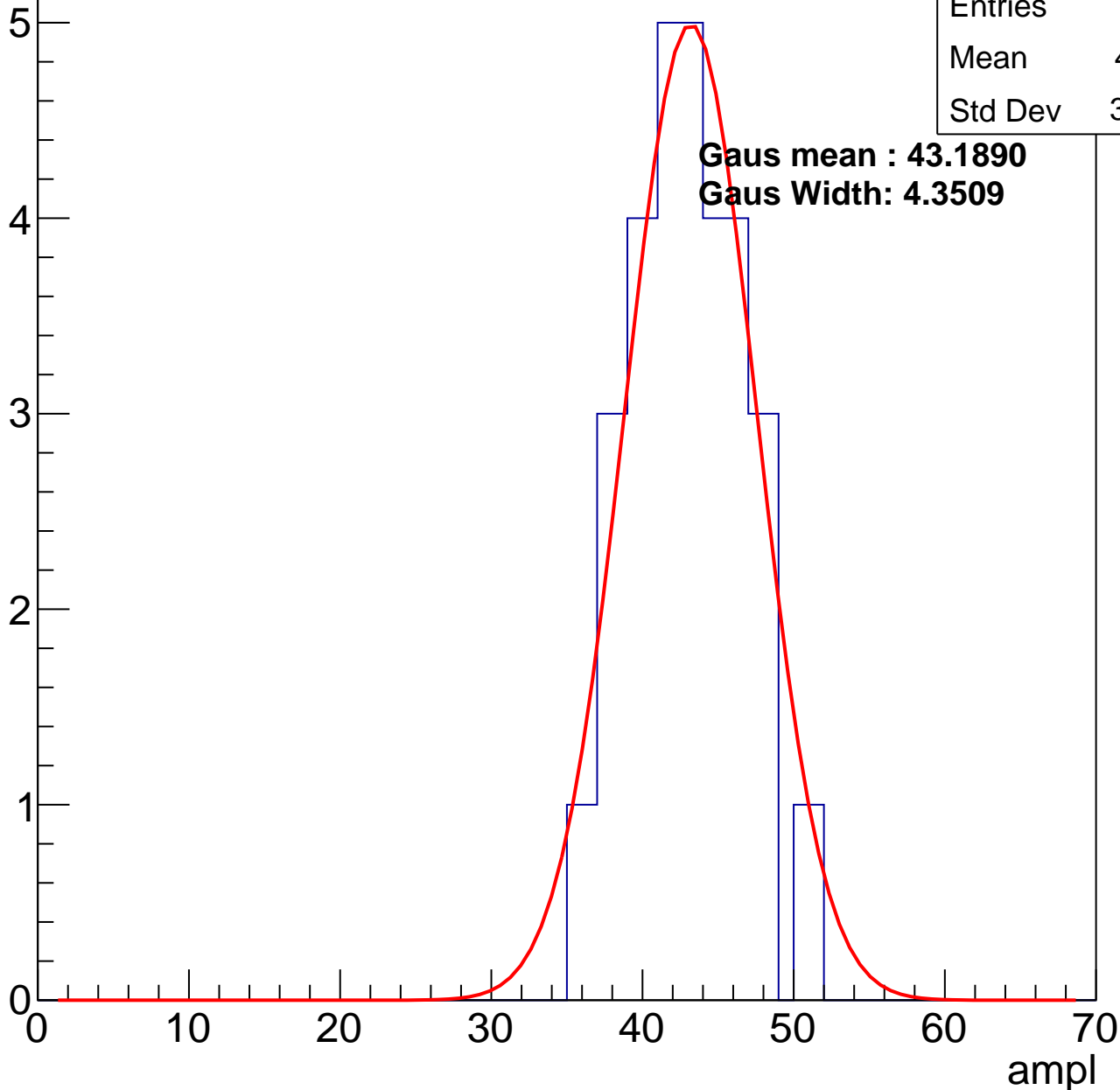
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	42.51
Std Dev	3.707

Gaus mean : 43.1890

Gaus Width: 4.3509

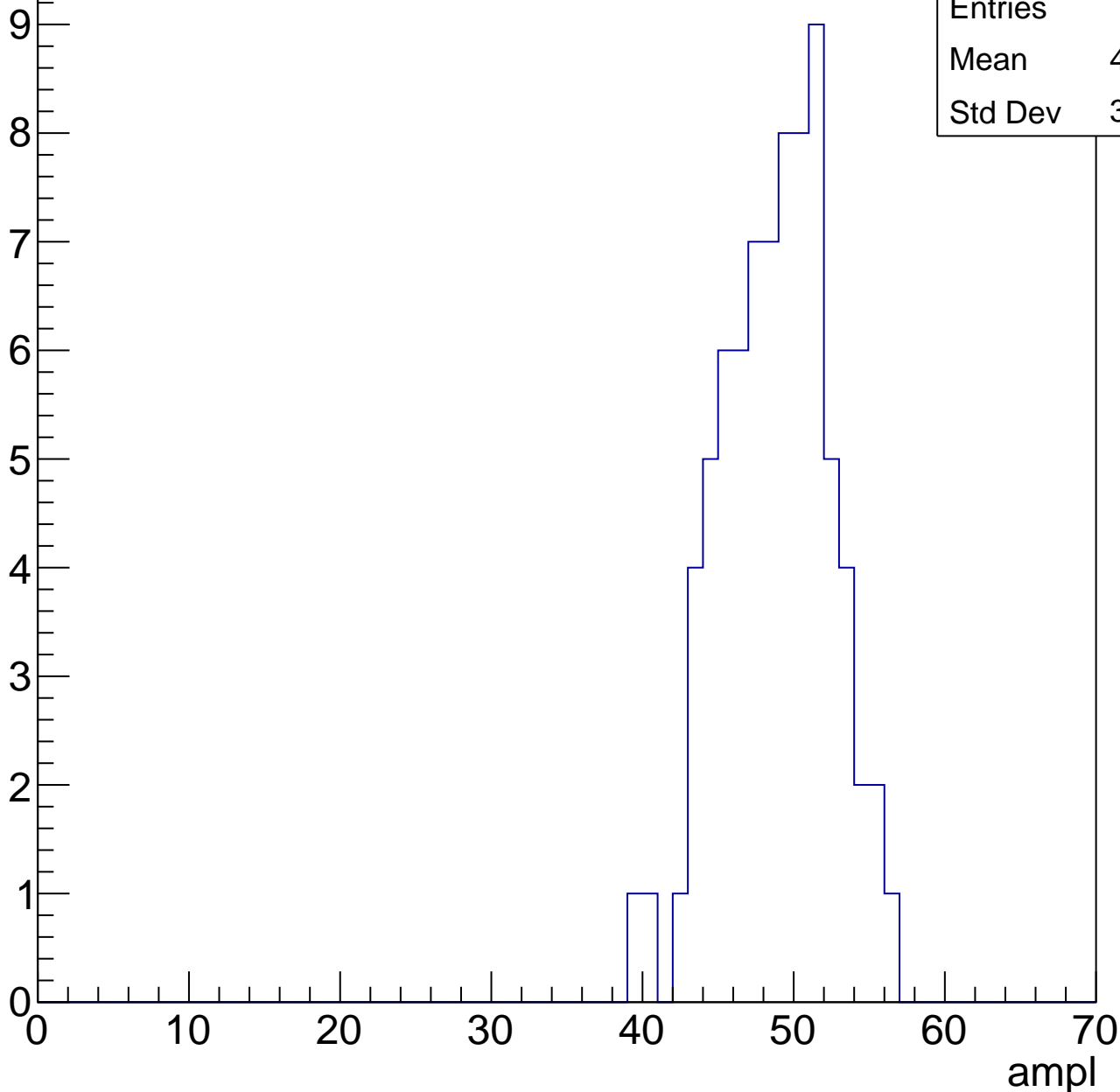


B1L103S, U5-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

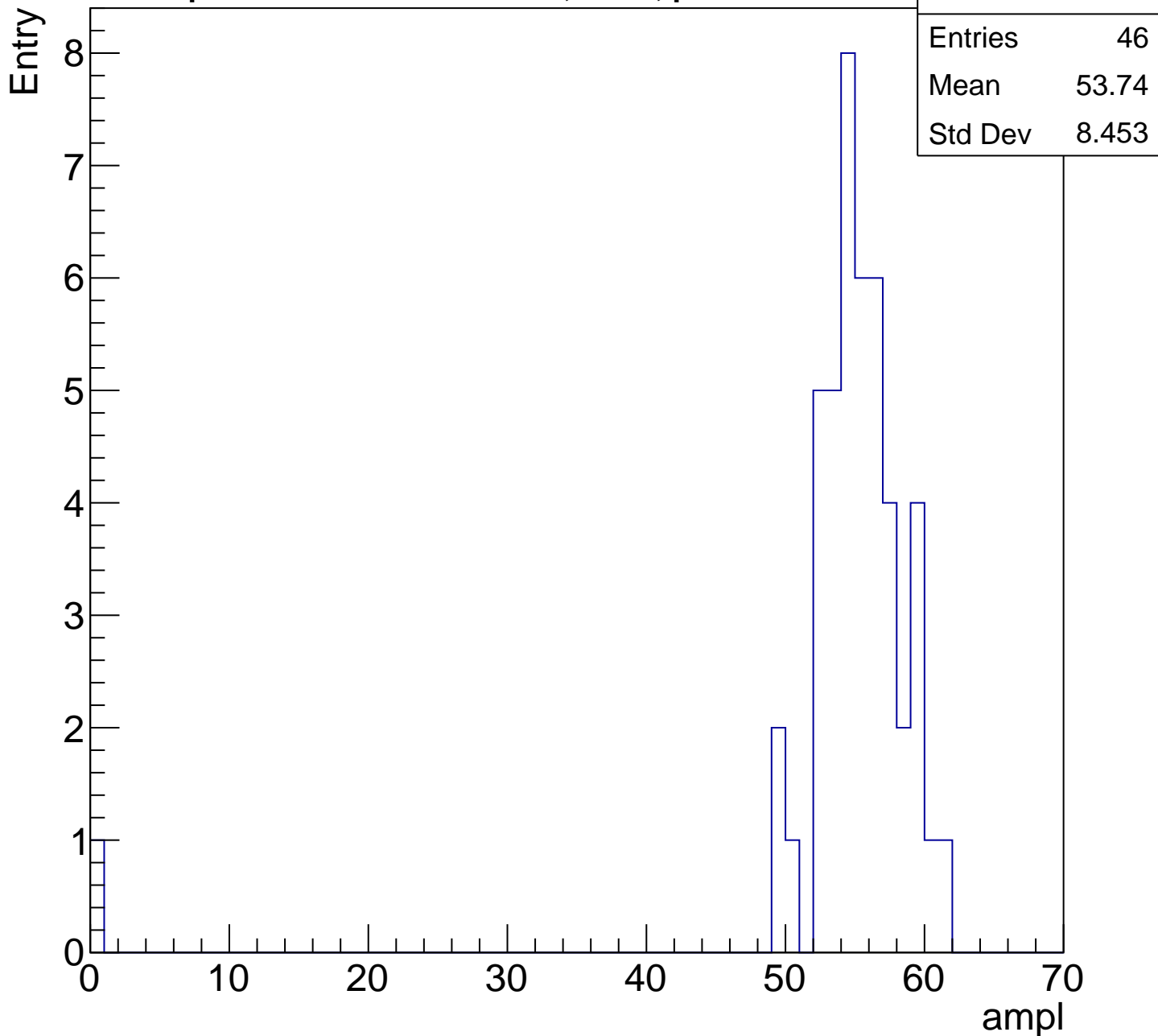
Entry

Entries	77
Mean	48.32
Std Dev	3.555



B1L103S, U5-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch19, adc5

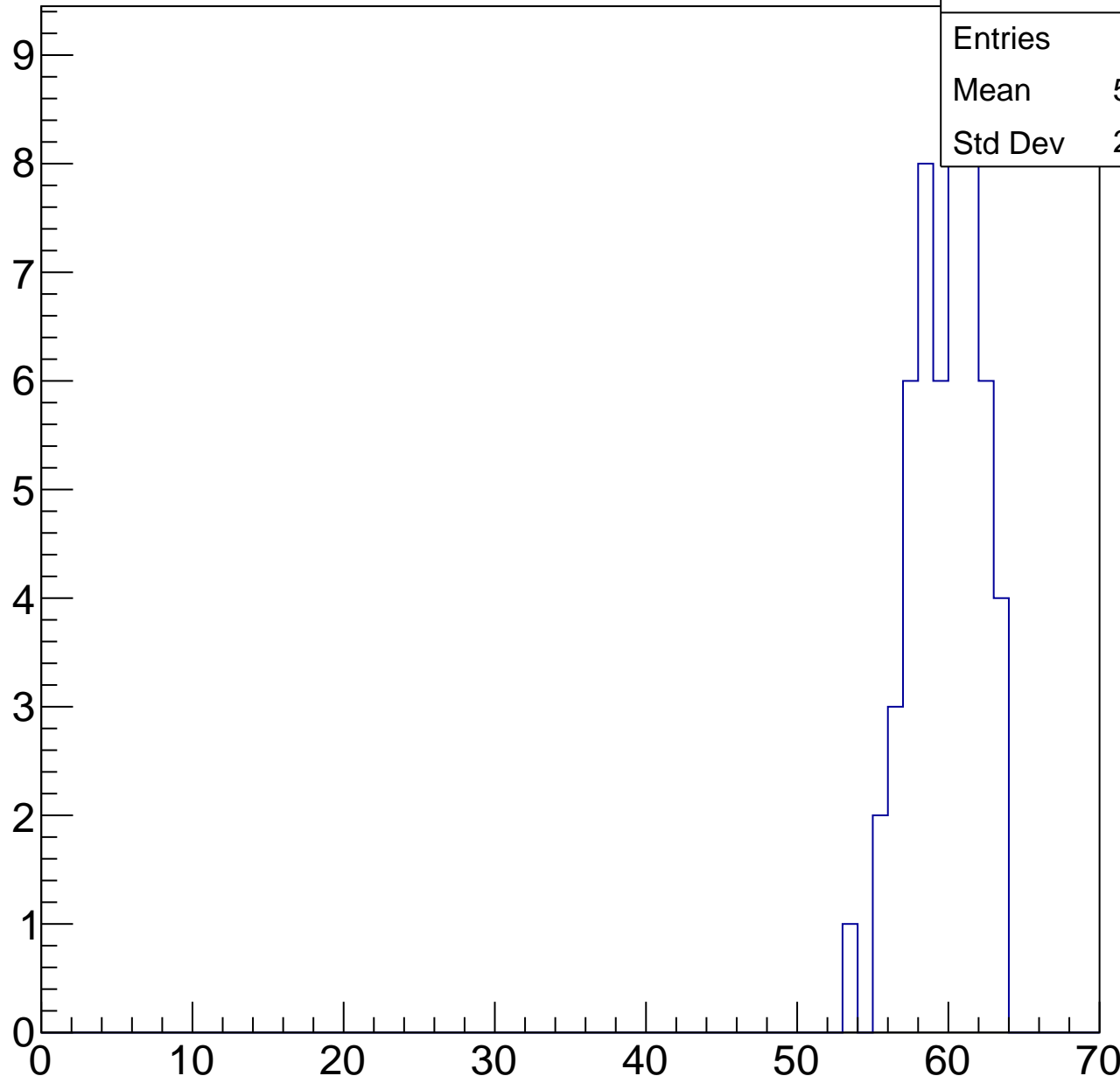
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.32
Std Dev	2.313

ampl

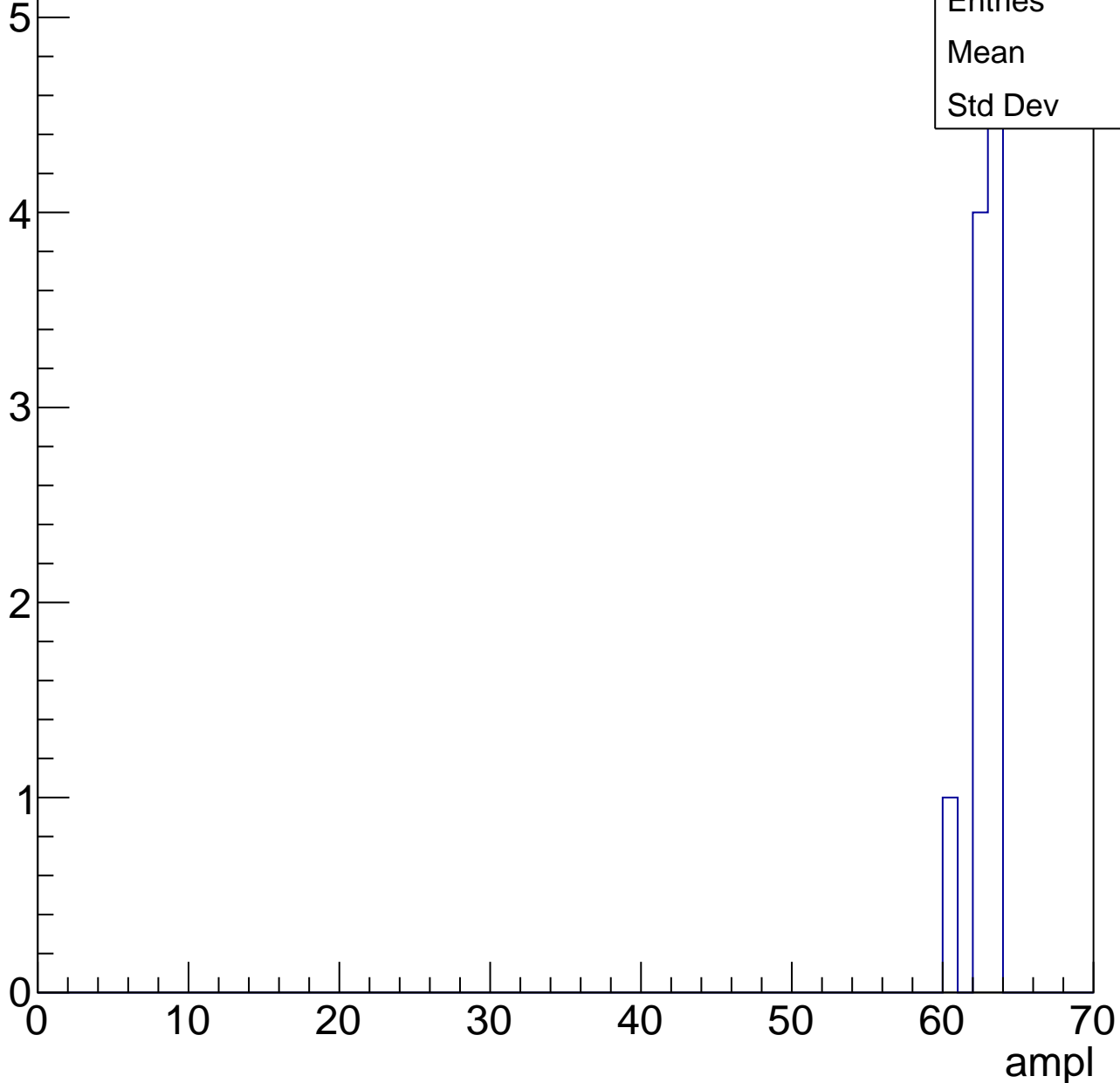


B1L103S, U5-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

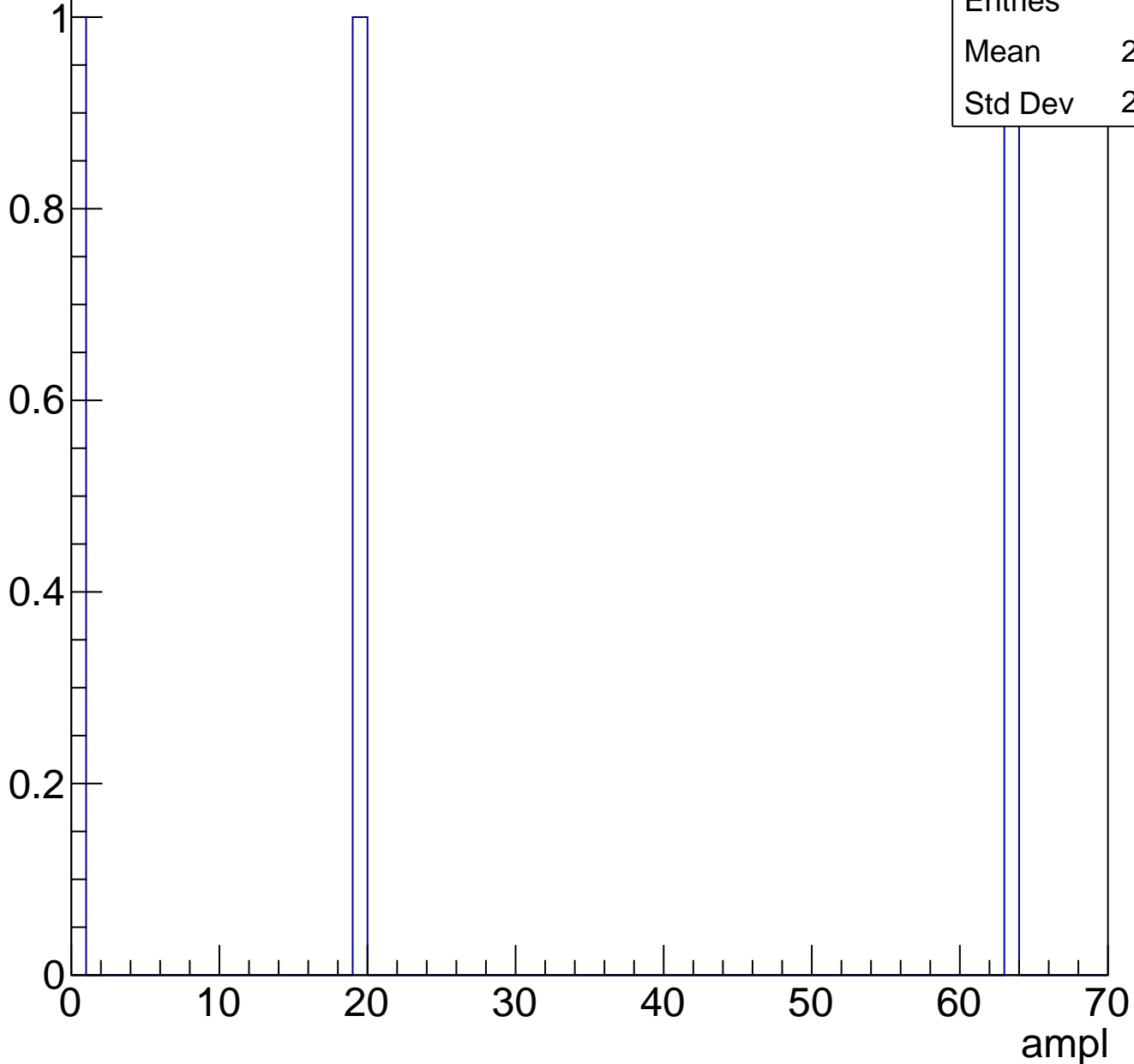
Entries	10
Mean	62.3
Std Dev	0.9



B1L103S, U5-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B1L103S, U5-ch20, adc0

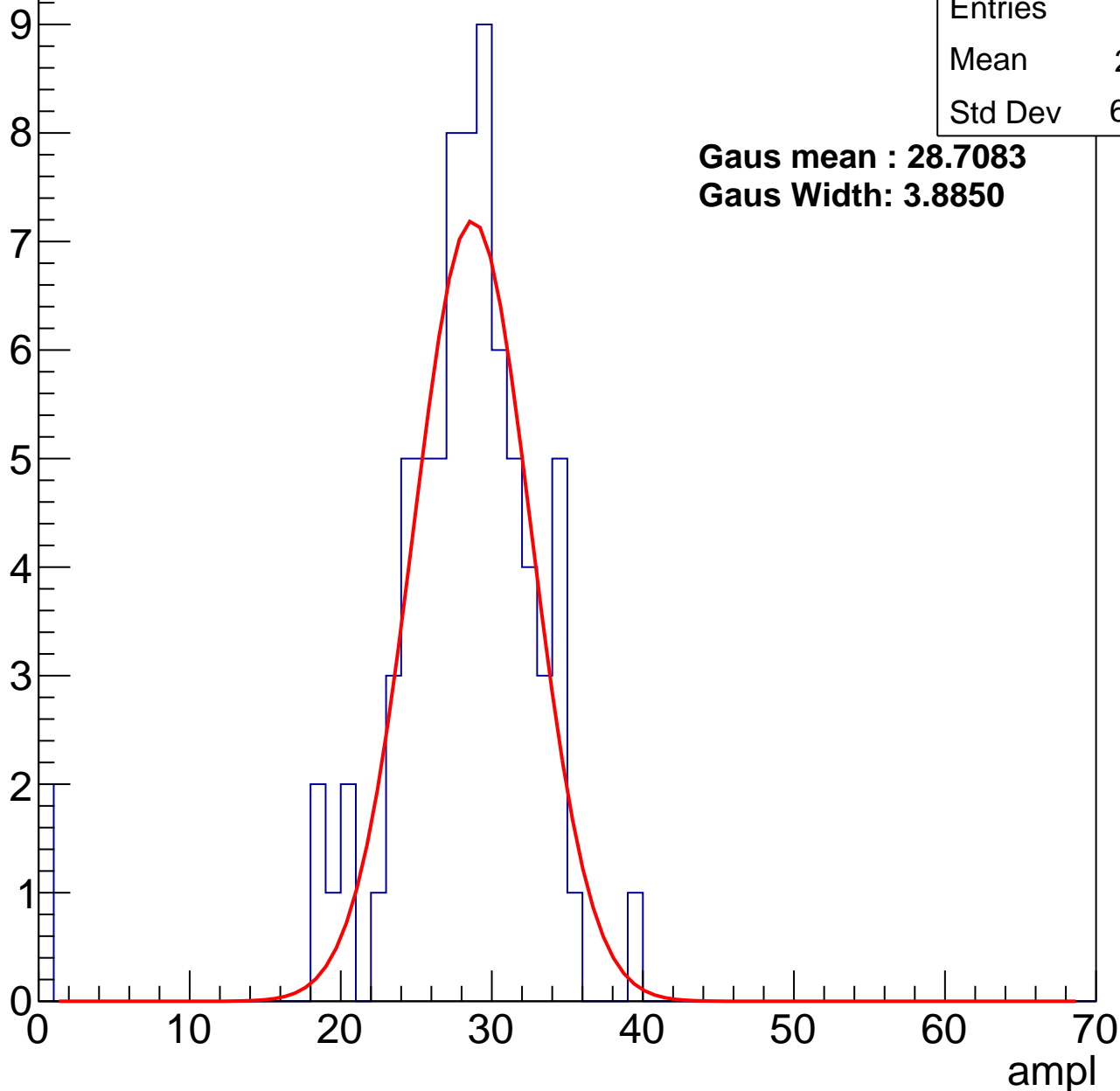
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	27.21
Std Dev	6.027

Gaus mean : 28.7083

Gaus Width: 3.8850



B1L103S, U5-ch20, adc1

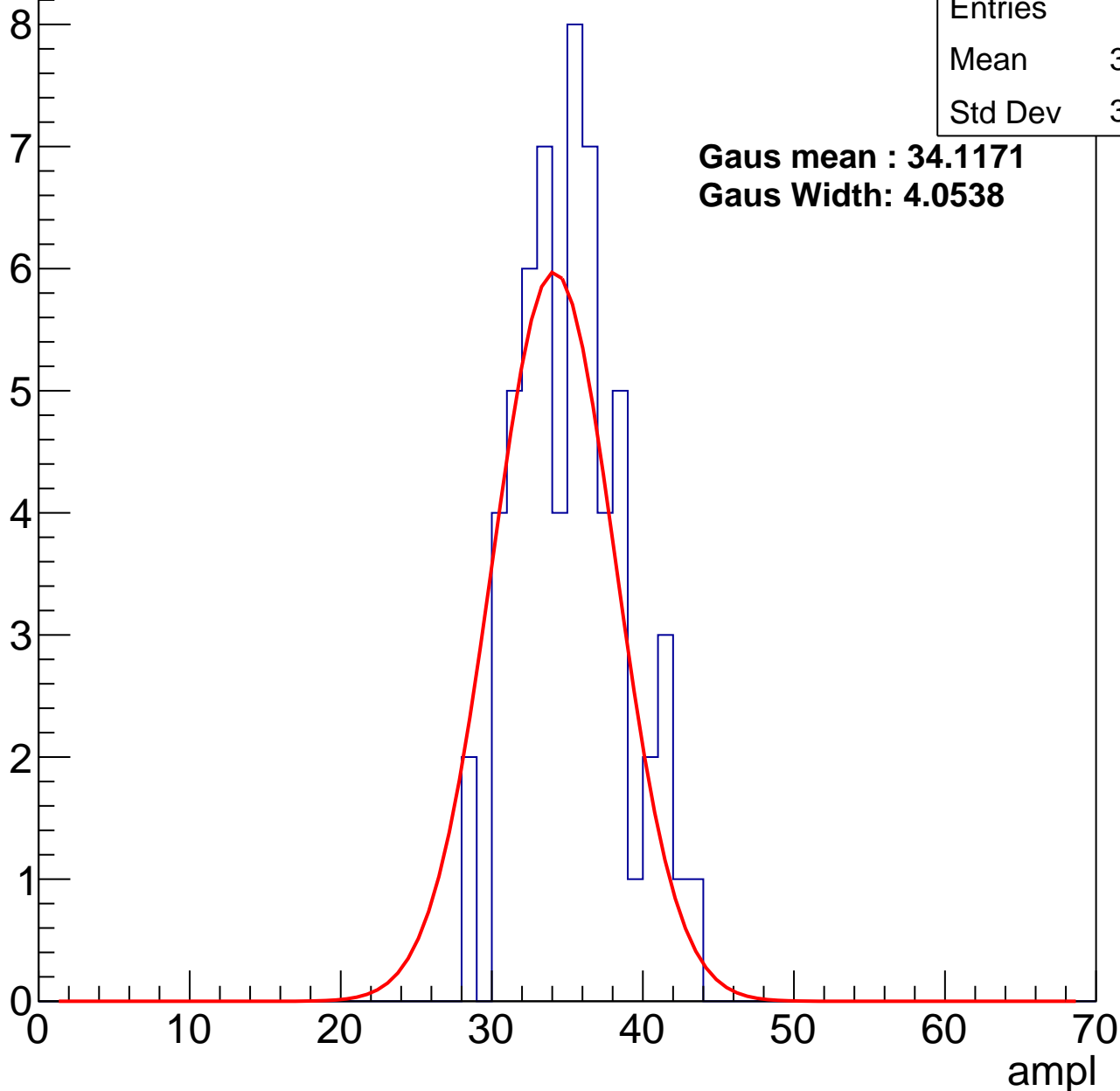
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	34.78
Std Dev	3.455

Gaus mean : 34.1171

Gaus Width: 4.0538



B1L103S, U5-ch20, adc2

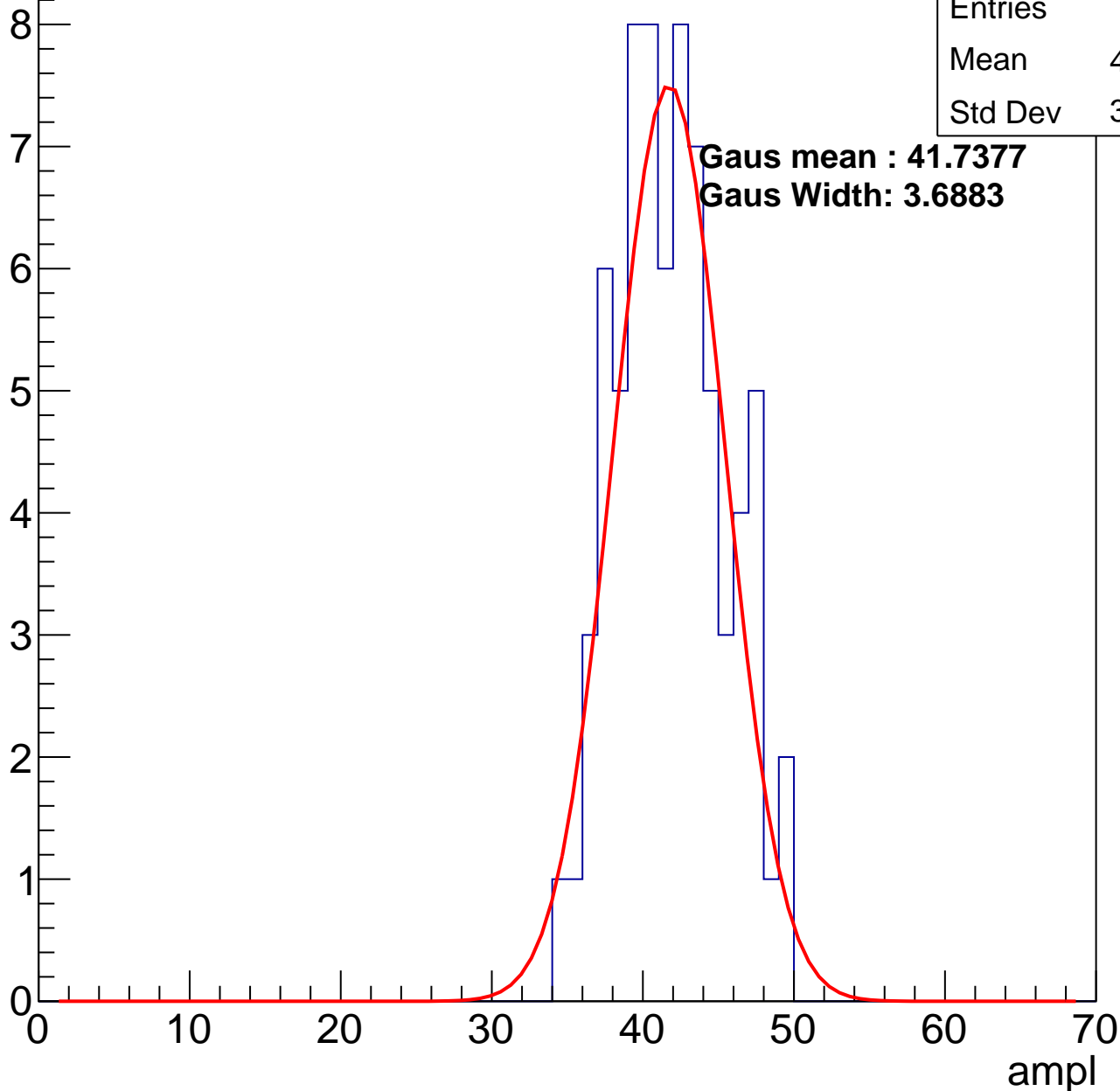
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	41.42
Std Dev	3.538

Gaus mean : 41.7377

Gaus Width: 3.6883

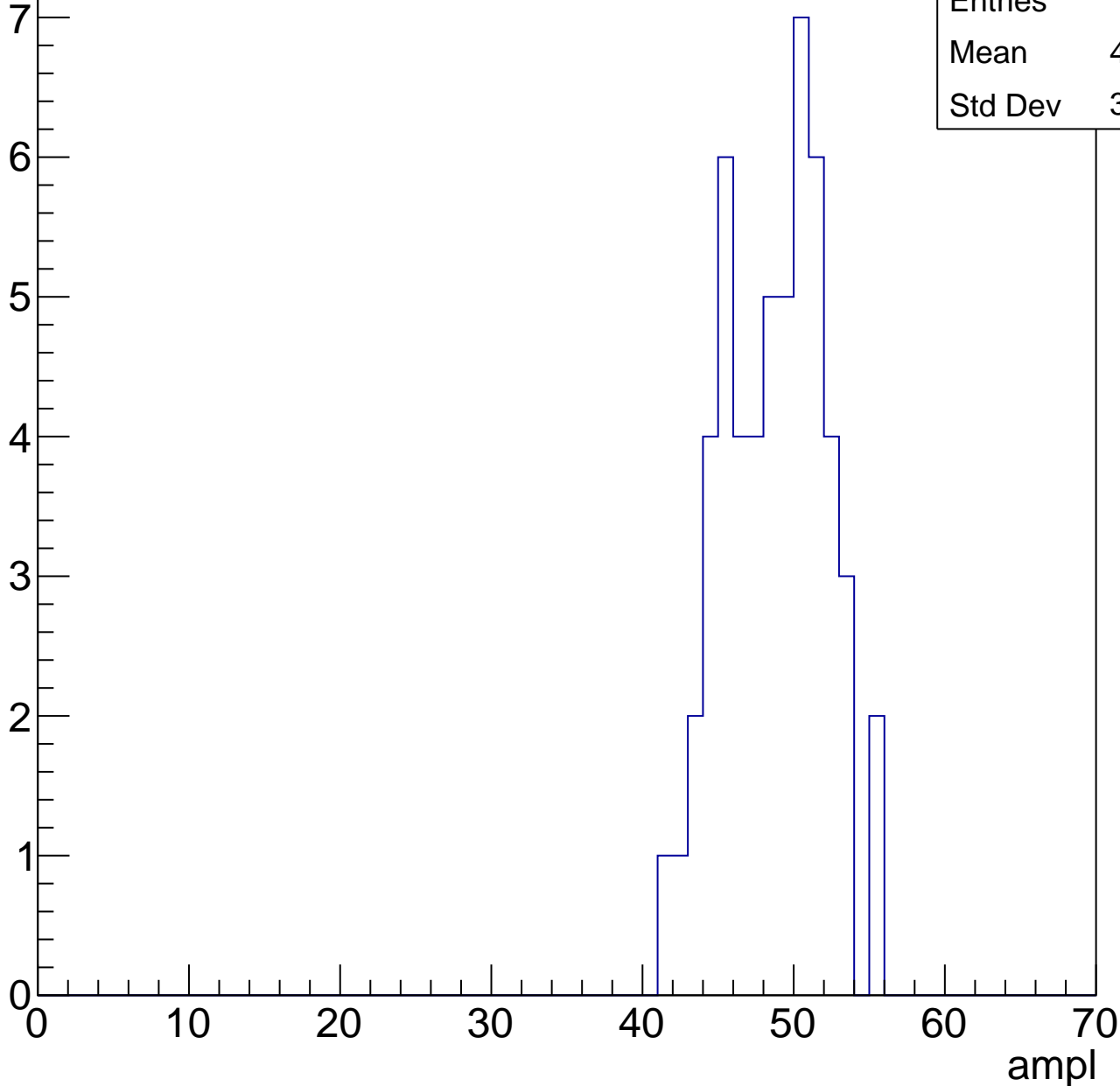


B1L103S, U5-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

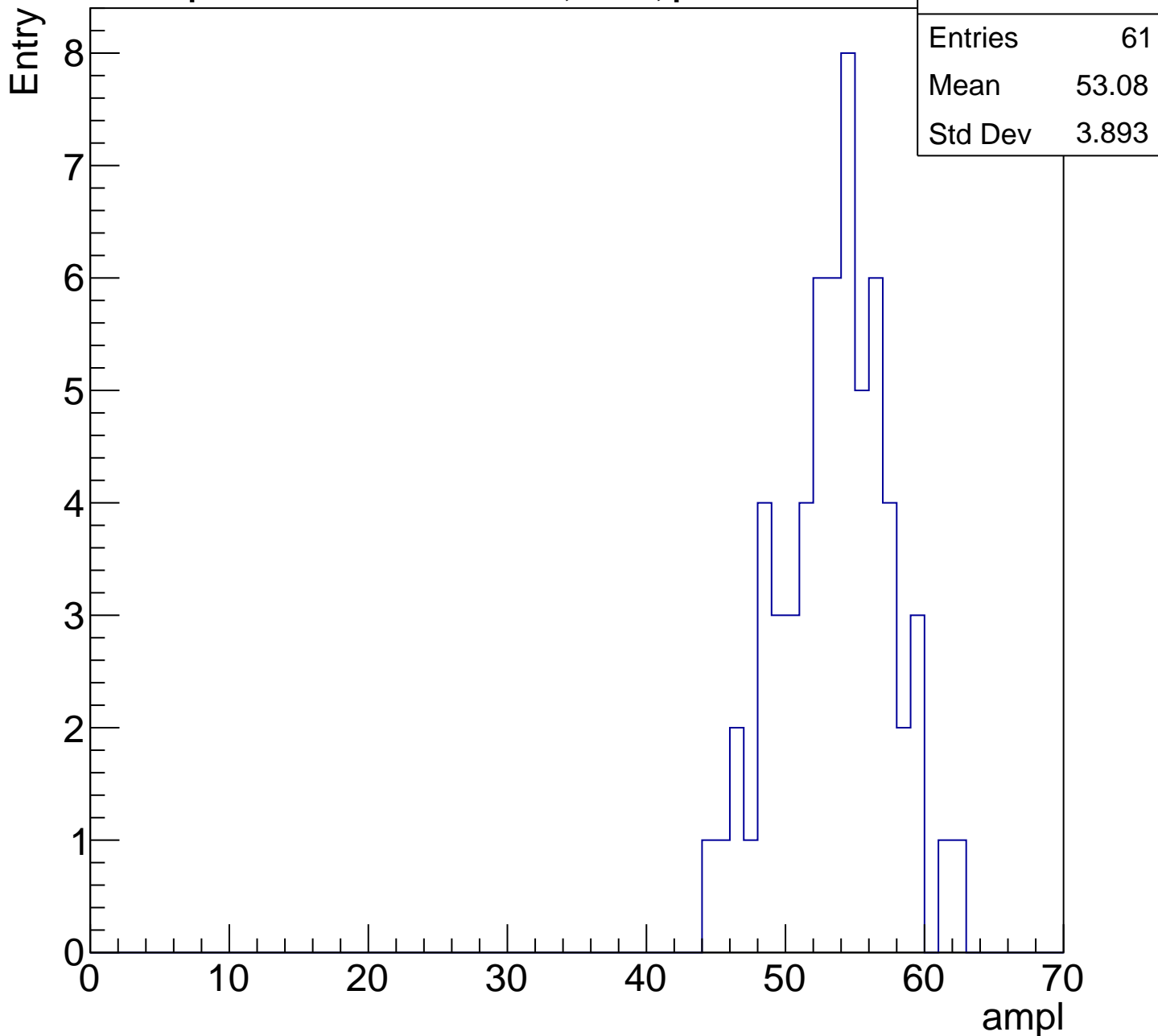
Entry

Entries	54
Mean	48.24
Std Dev	3.316



B1L103S, U5-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

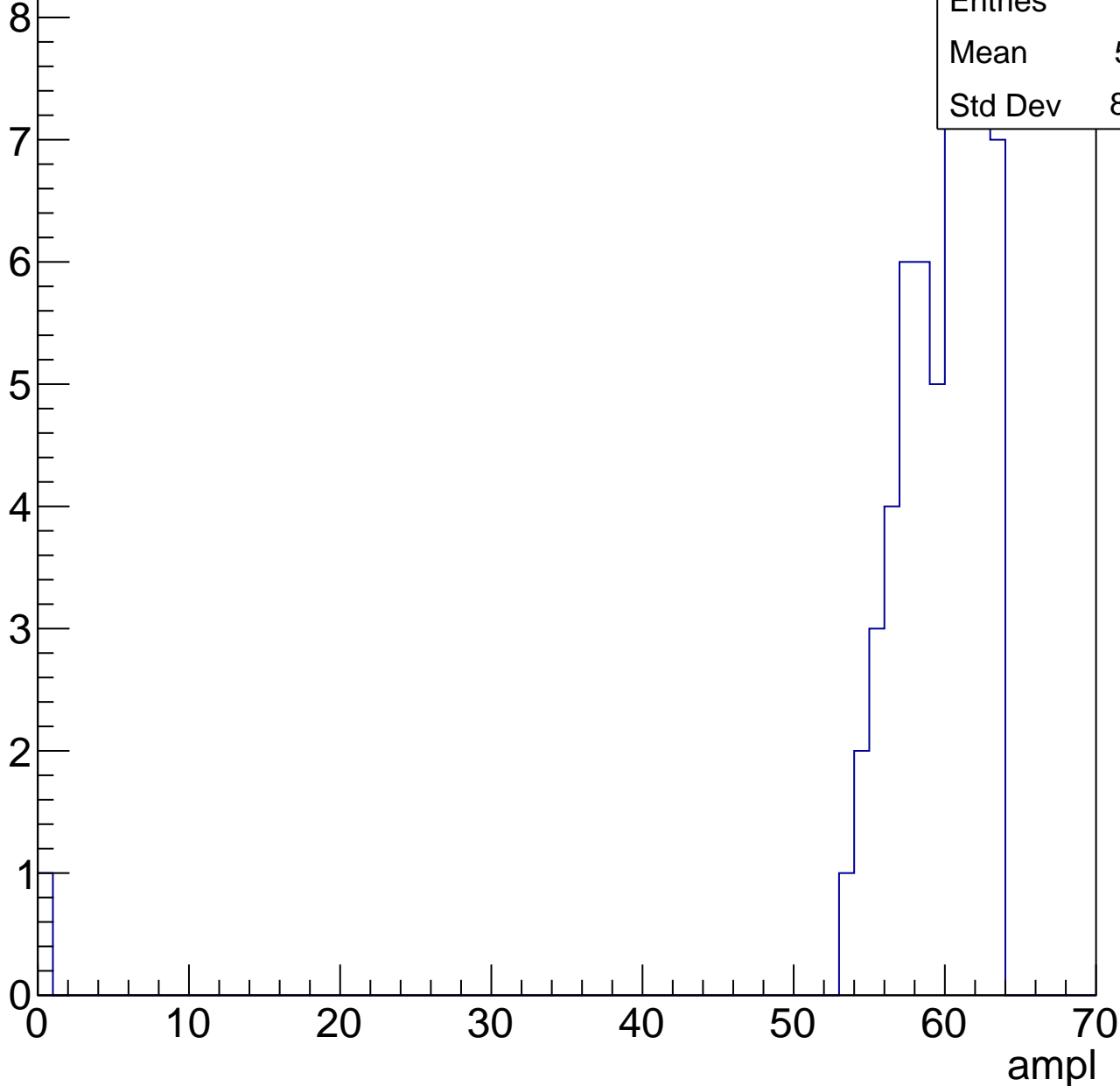


B1L103S, U5-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	58.31
Std Dev	8.106



B1L103S, U5-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	10
Mean	60.3
Std Dev	1.792

B1L103S, U5-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch21, adc0

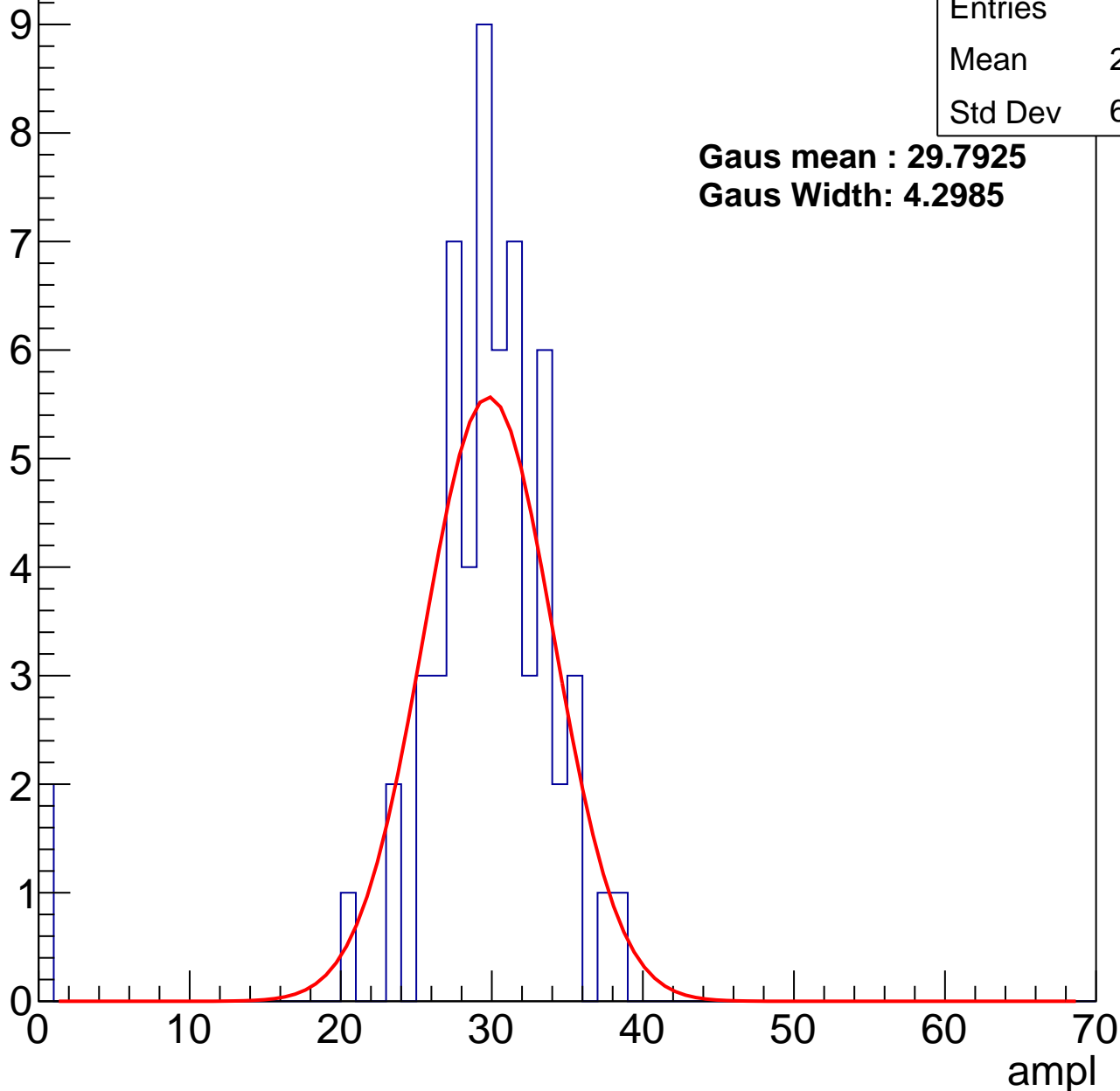
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.67
Std Dev	6.318

Gaus mean : 29.7925

Gaus Width: 4.2985



B1L103S, U5-ch21, adc1

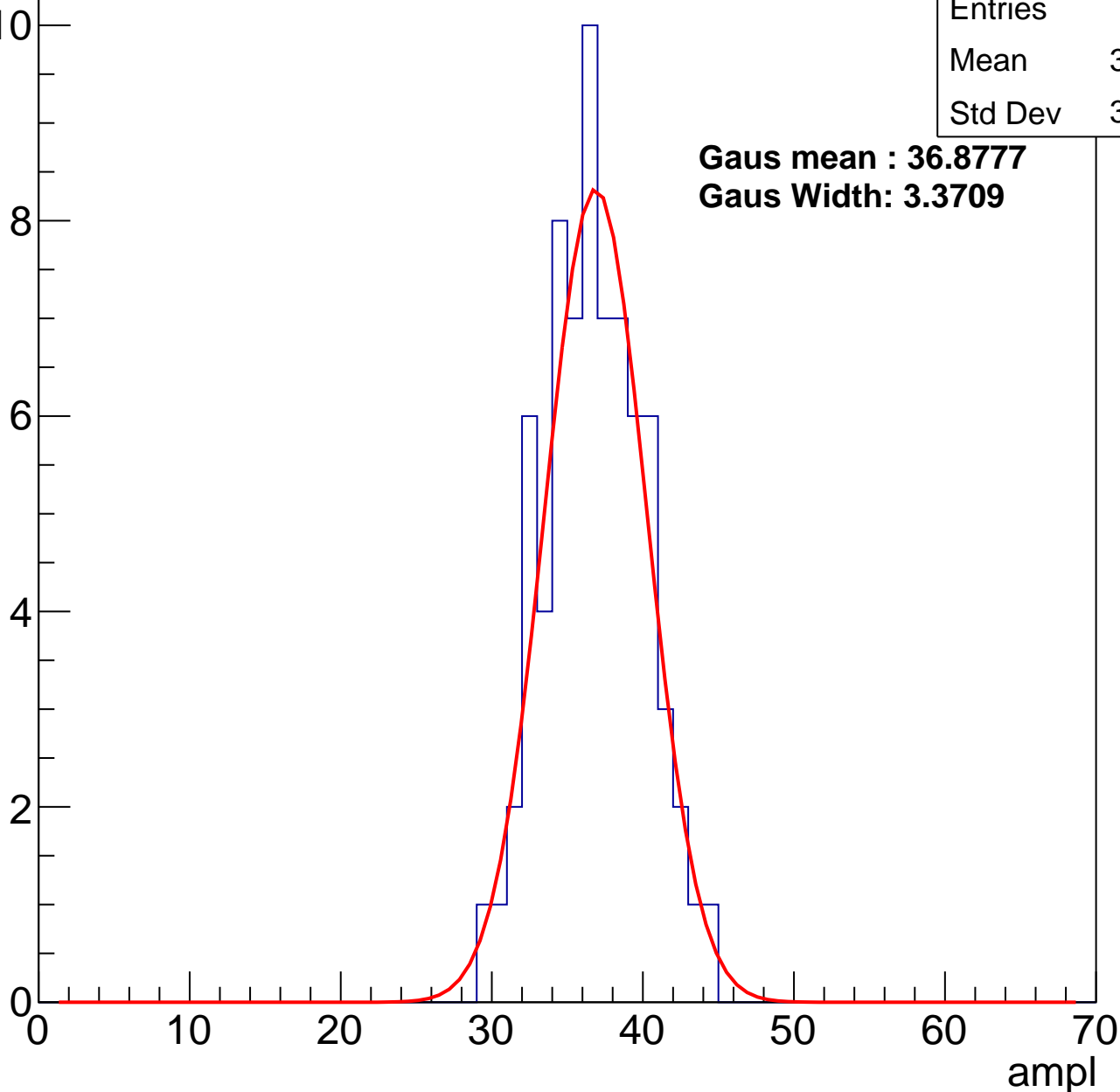
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	36.32
Std Dev	3.214

Gaus mean : 36.8777

Gaus Width: 3.3709



B1L103S, U5-ch21, adc2

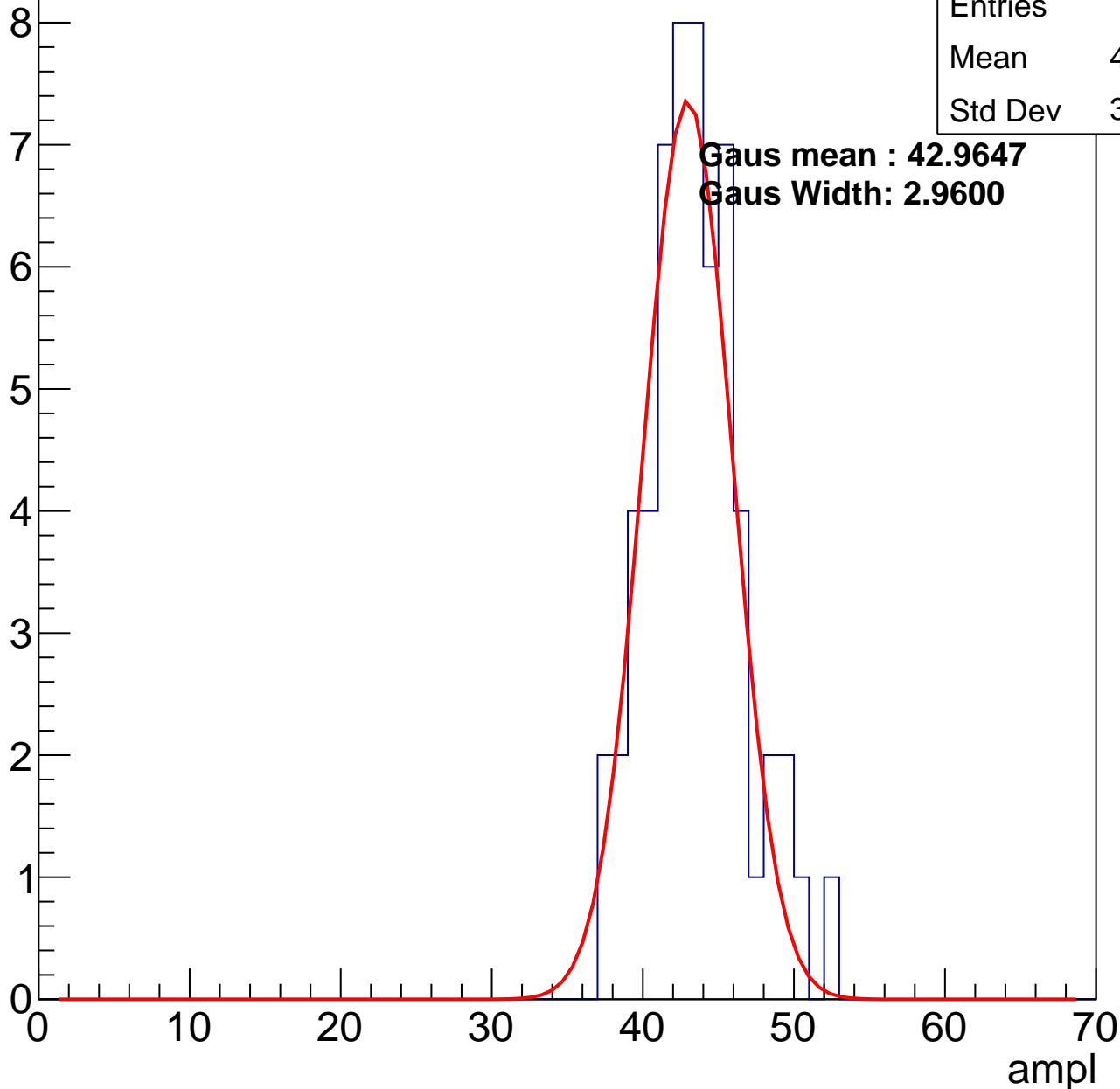
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.03
Std Dev	3.178

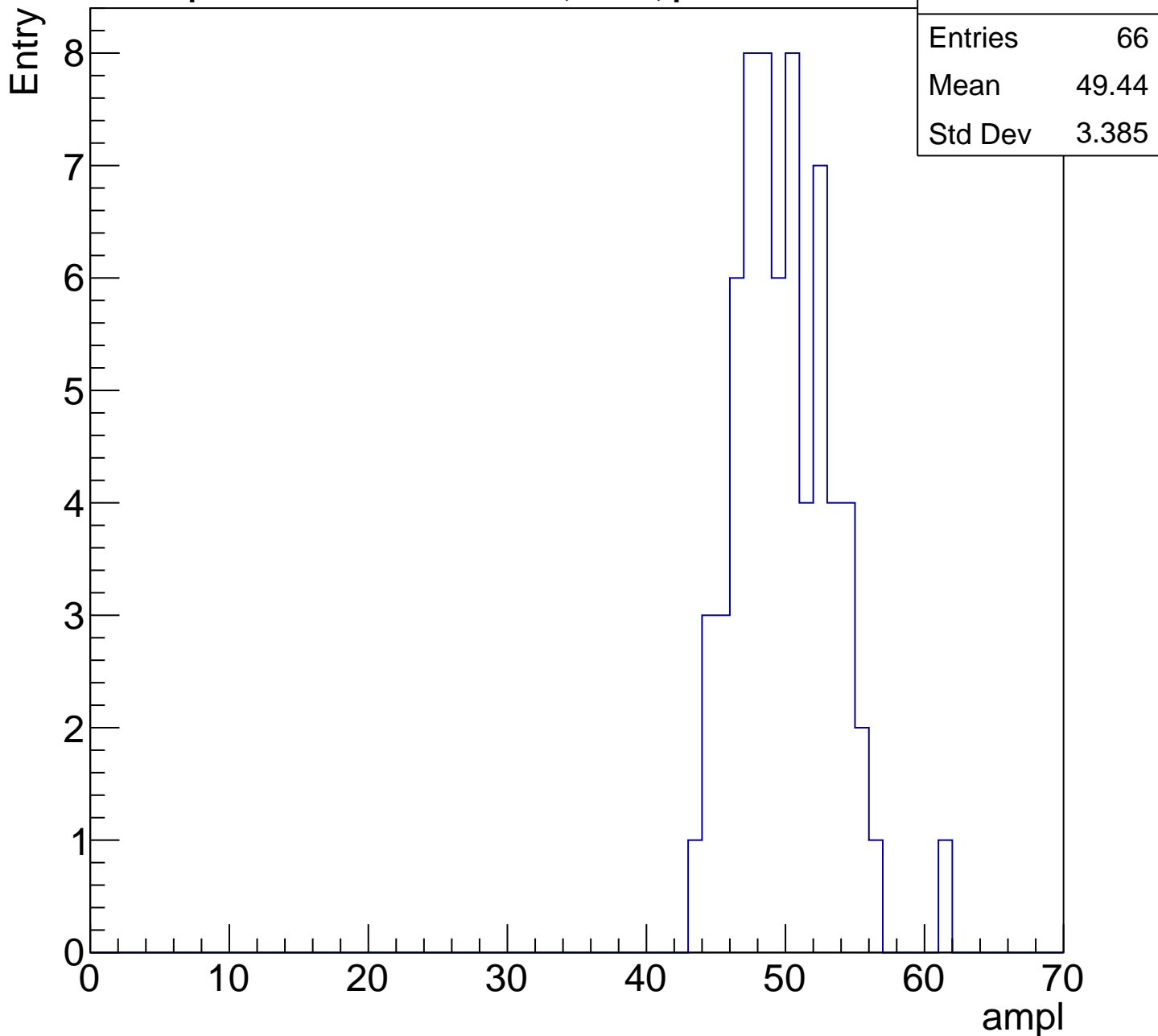
Gaus mean : 42.9647

Gaus Width: 2.9600



B1L103S, U5-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

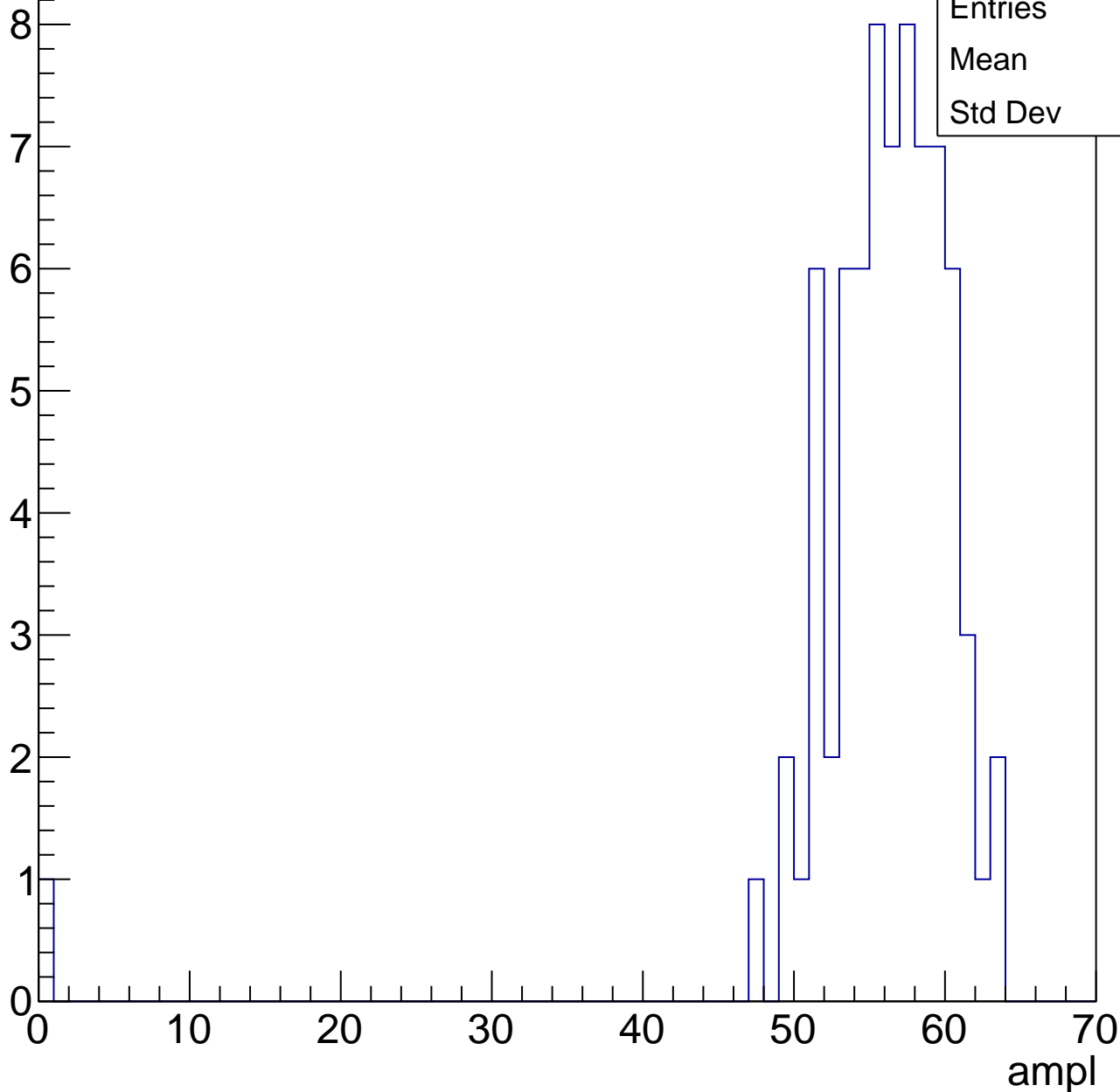


B1L103S, U5-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	55.2
Std Dev	7.33

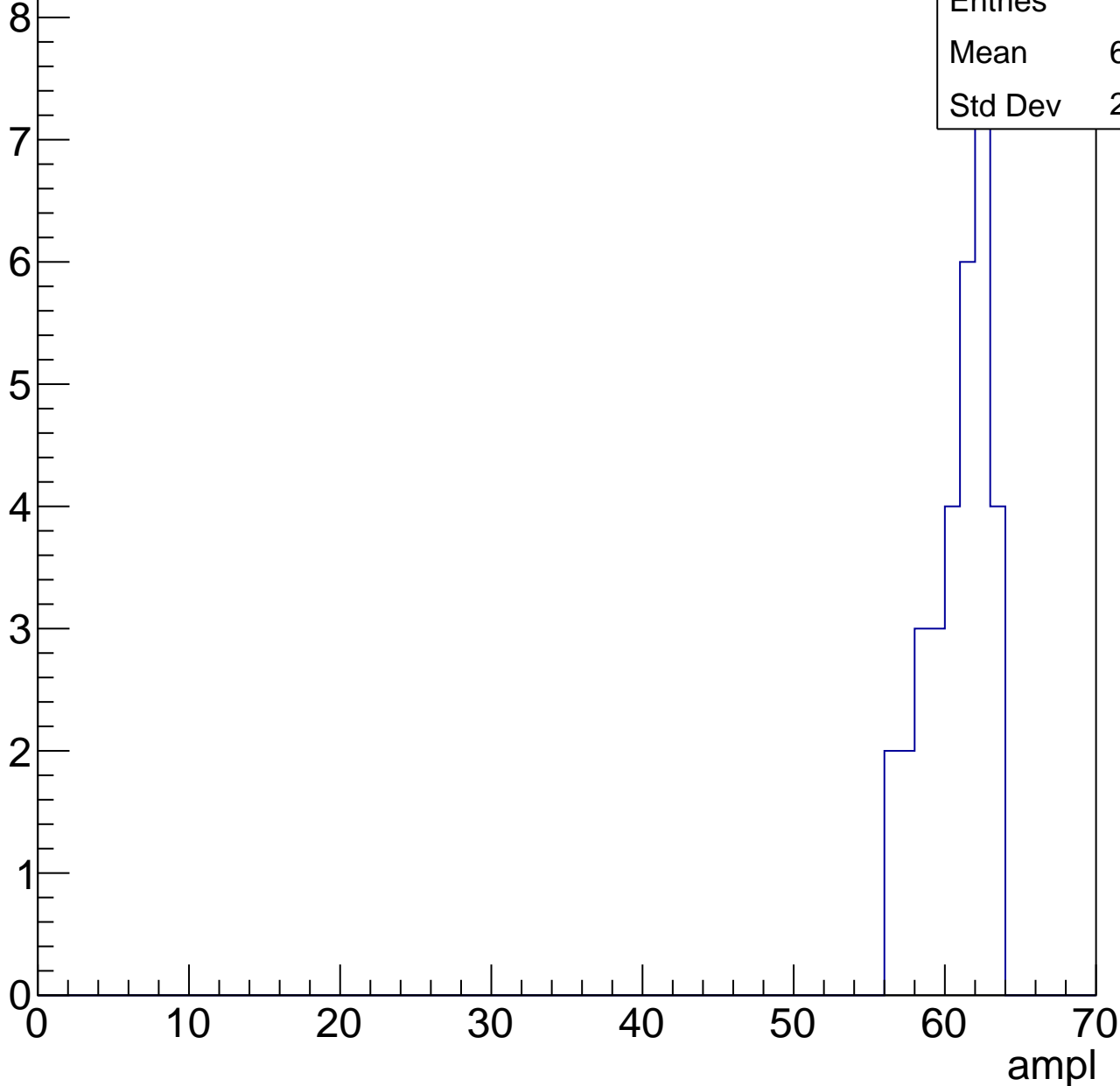


B1L103S, U5-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

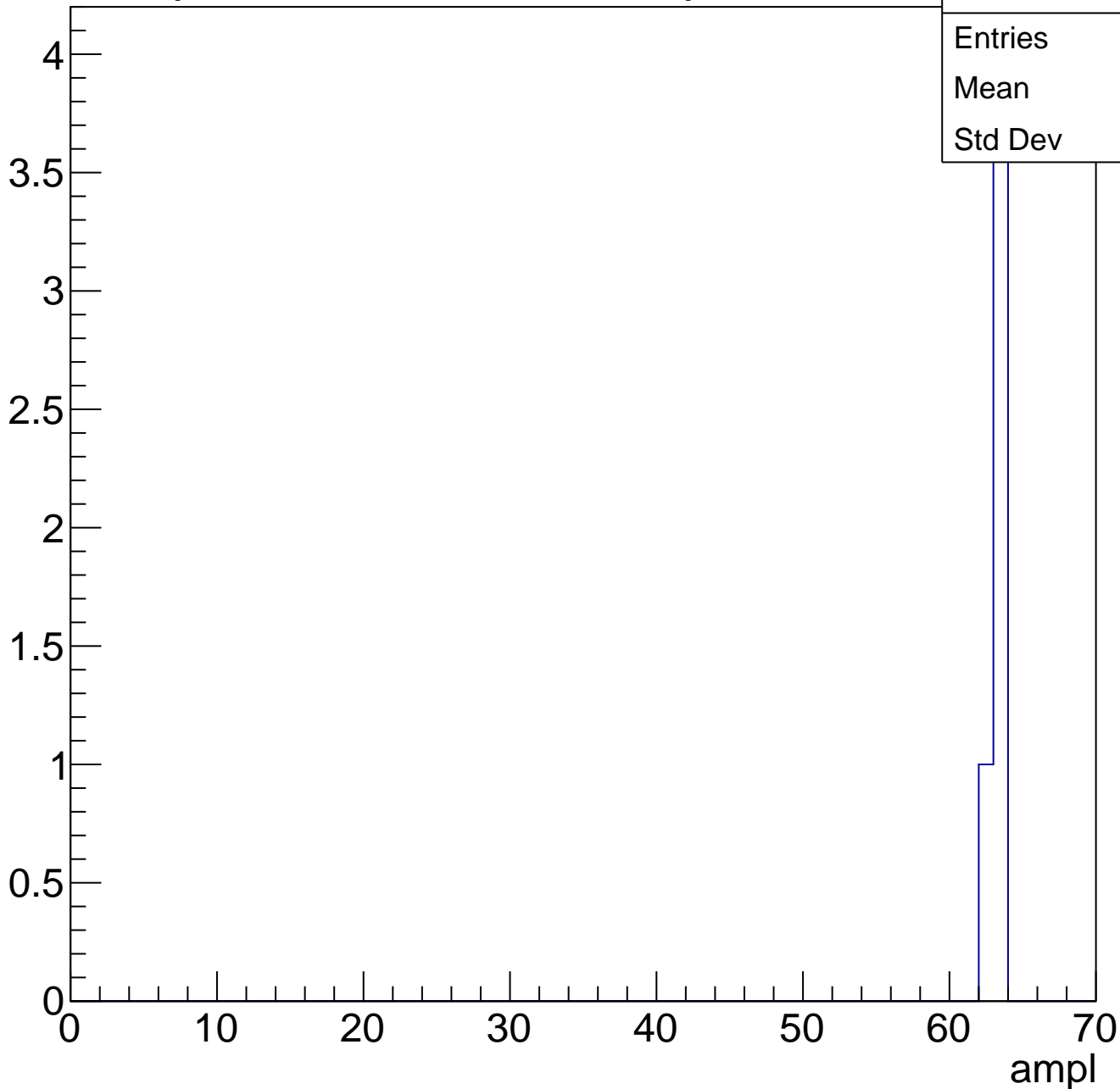
Entries	32
Mean	60.34
Std Dev	2.056



B1L103S, U5-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch22, adc0

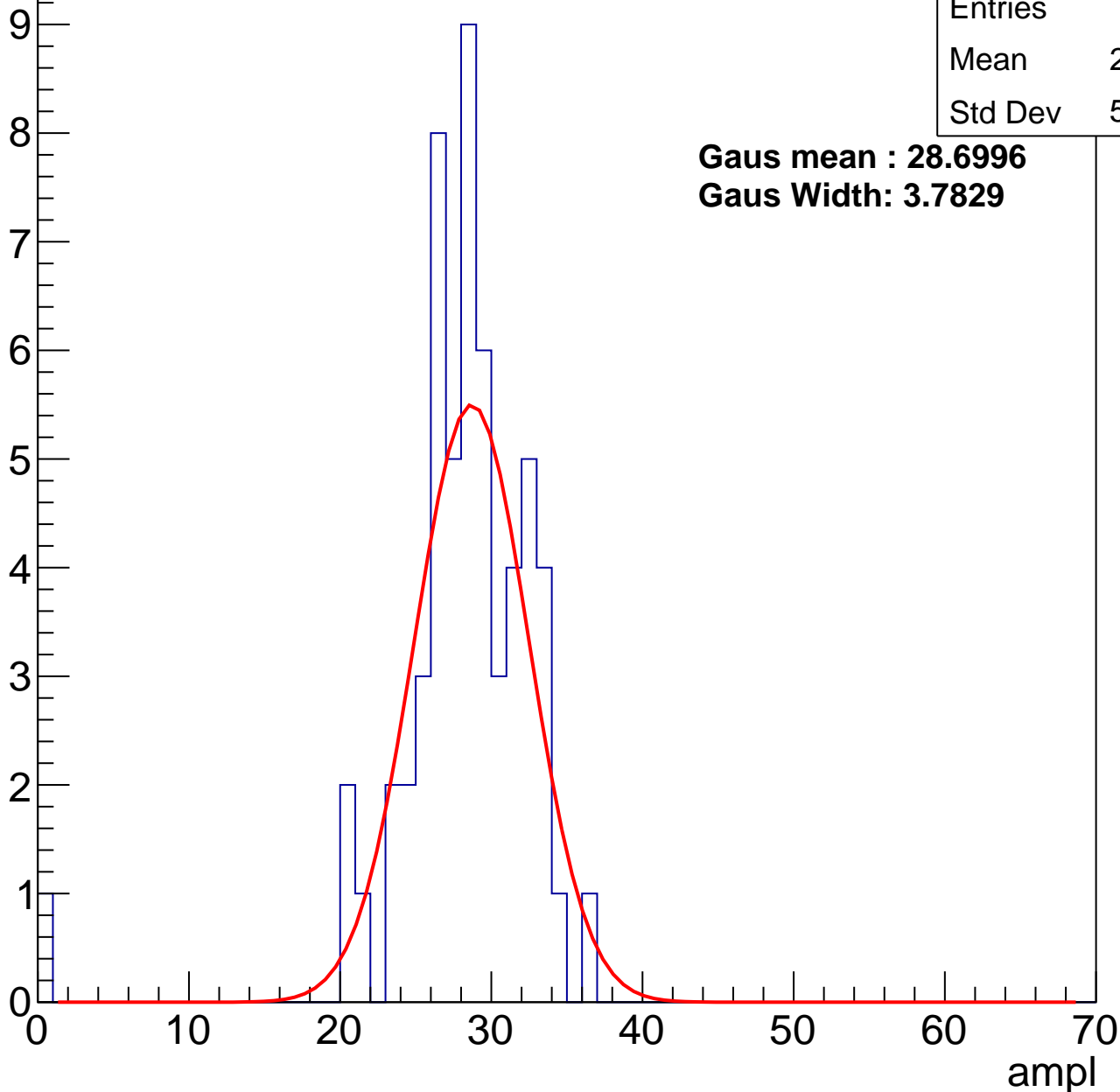
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	27.63
Std Dev	5.014

Gaus mean : 28.6996

Gaus Width: 3.7829



B1L103S, U5-ch22, adc1

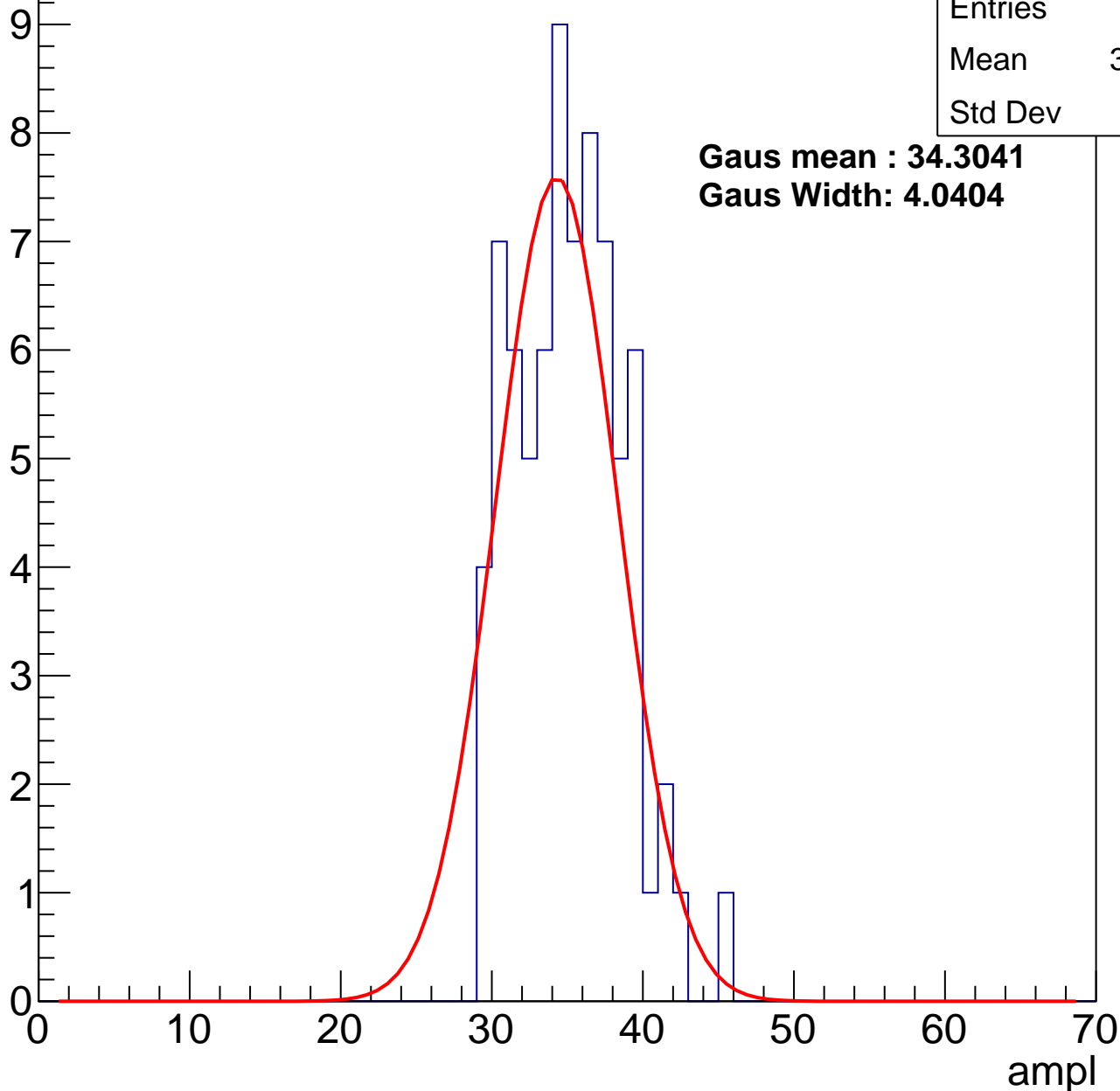
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	34.68
Std Dev	3.48

Gaus mean : 34.3041

Gaus Width: 4.0404



B1L103S, U5-ch22, adc2

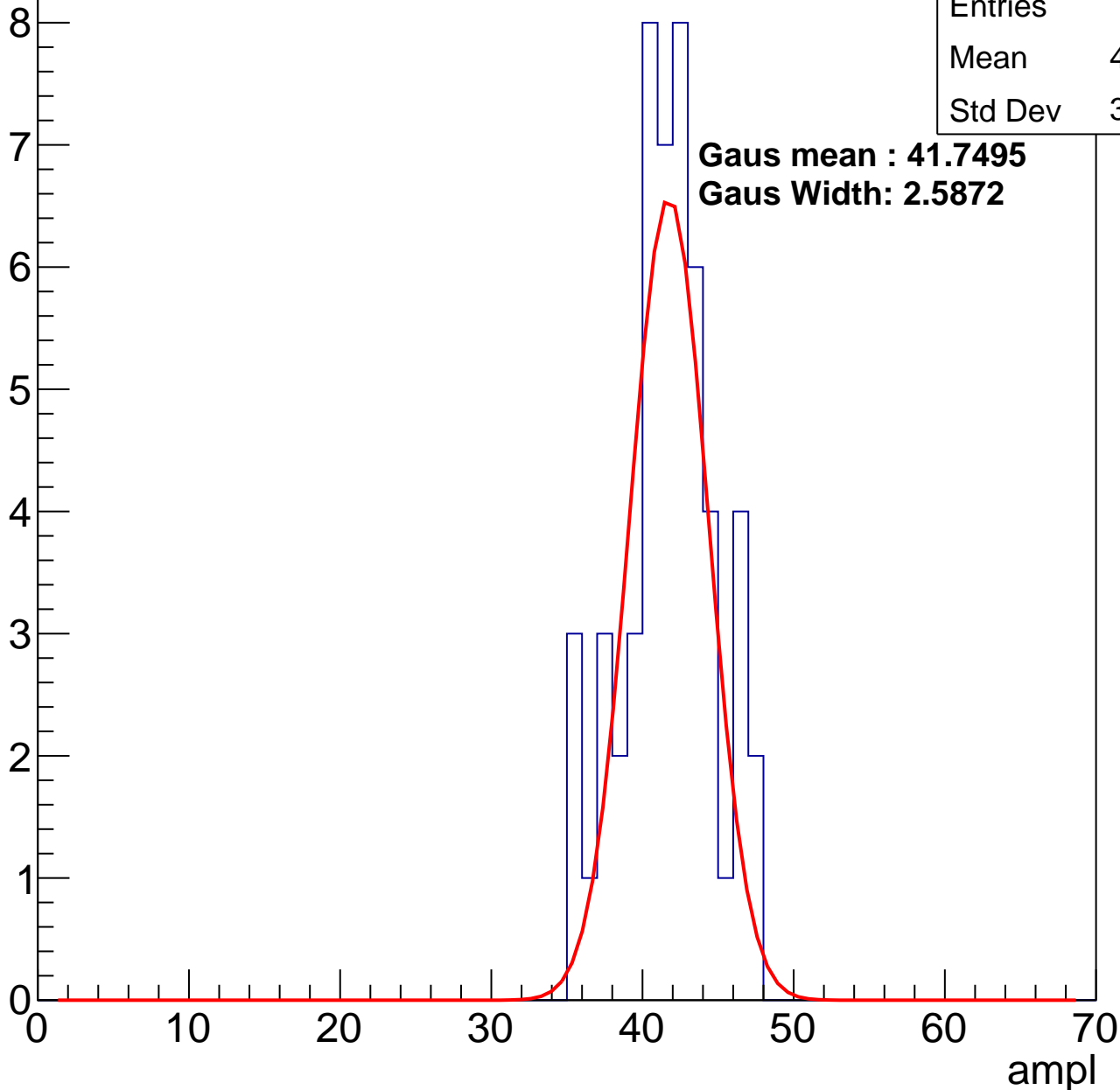
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	41.25
Std Dev	3.012

Gaus mean : 41.7495

Gaus Width: 2.5872

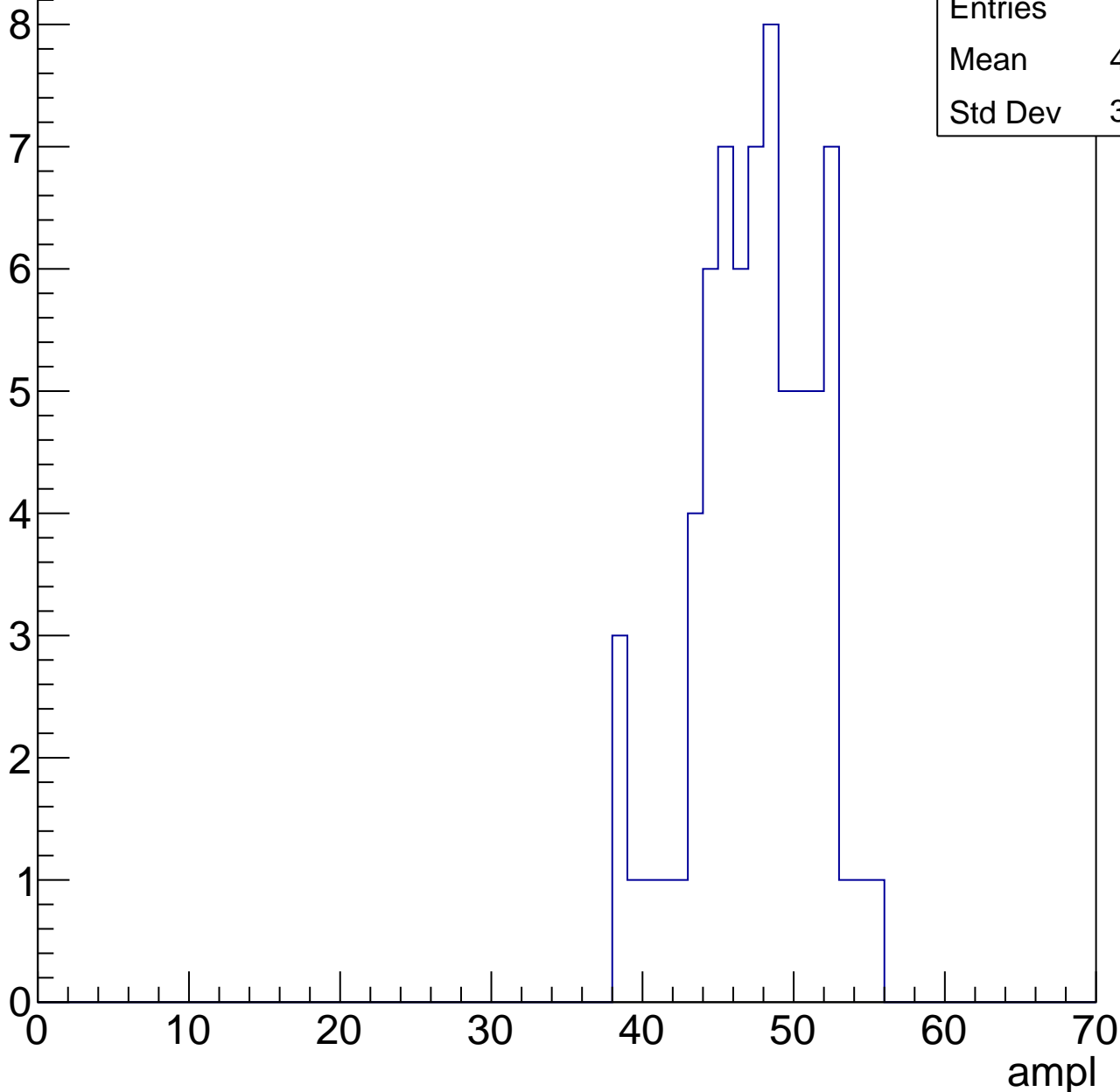


B1L103S, U5-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	47.03
Std Dev	3.865

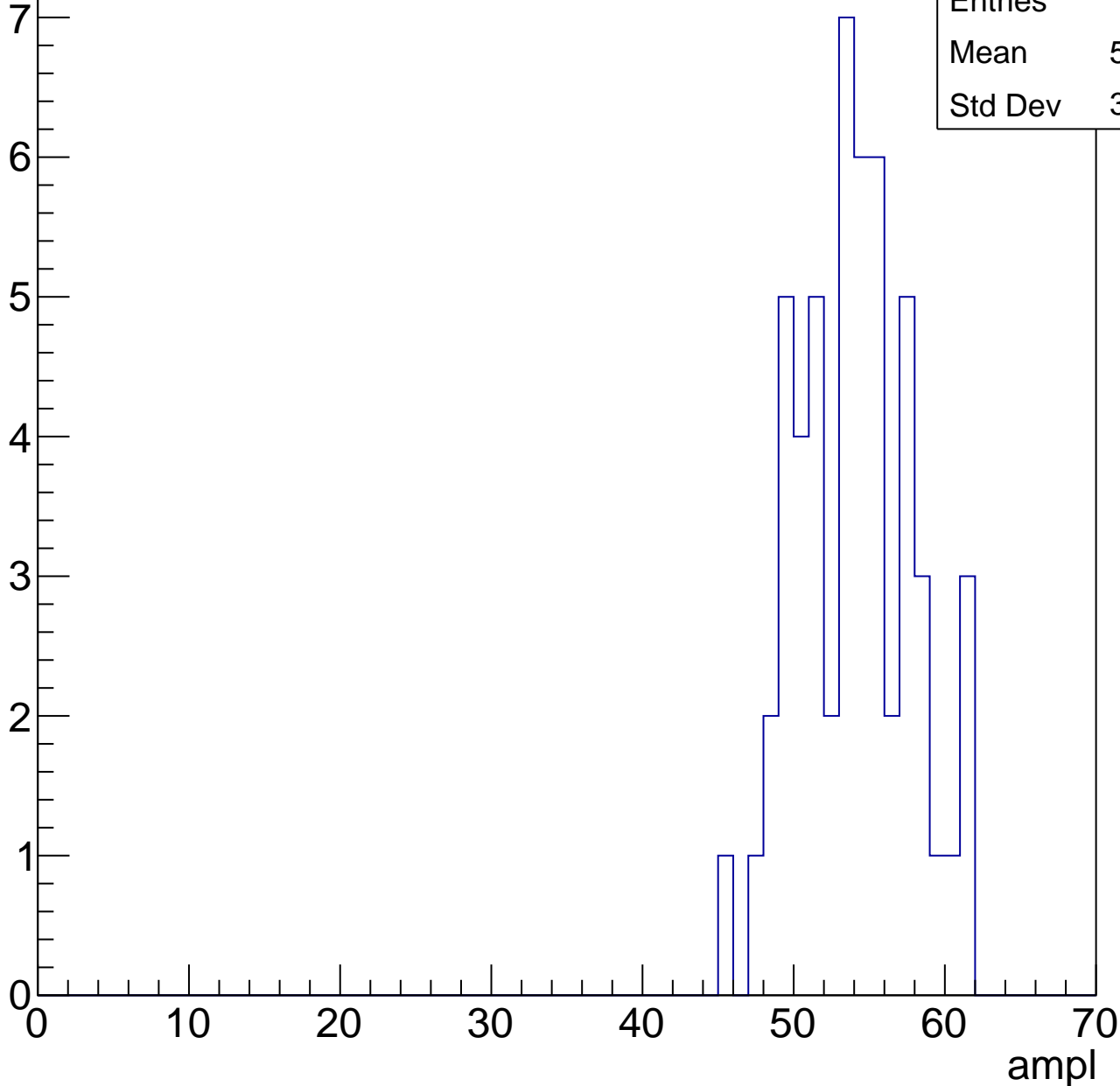


B1L103S, U5-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

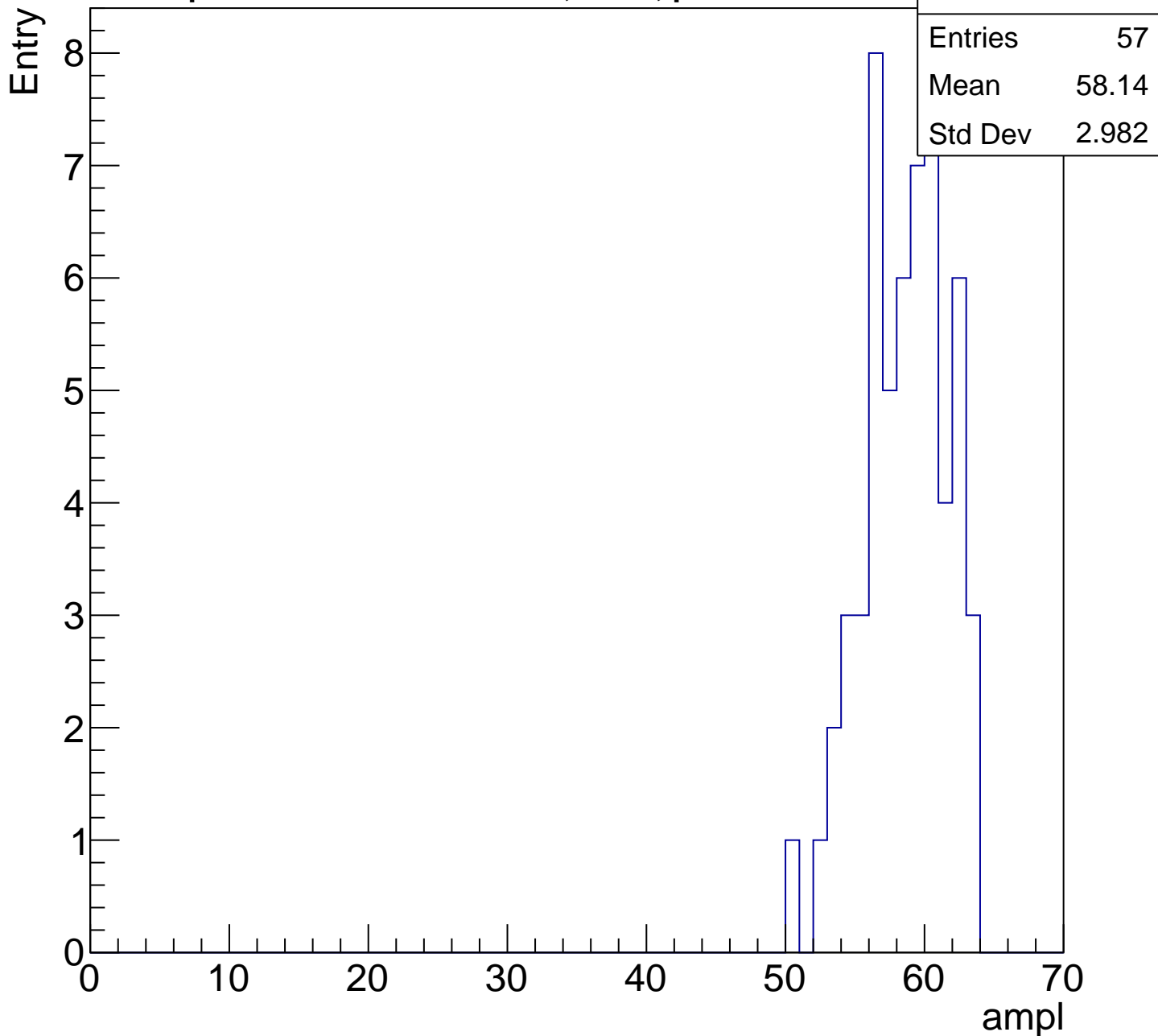
Entry

Entries	54
Mean	53.52
Std Dev	3.735



B1L103S, U5-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

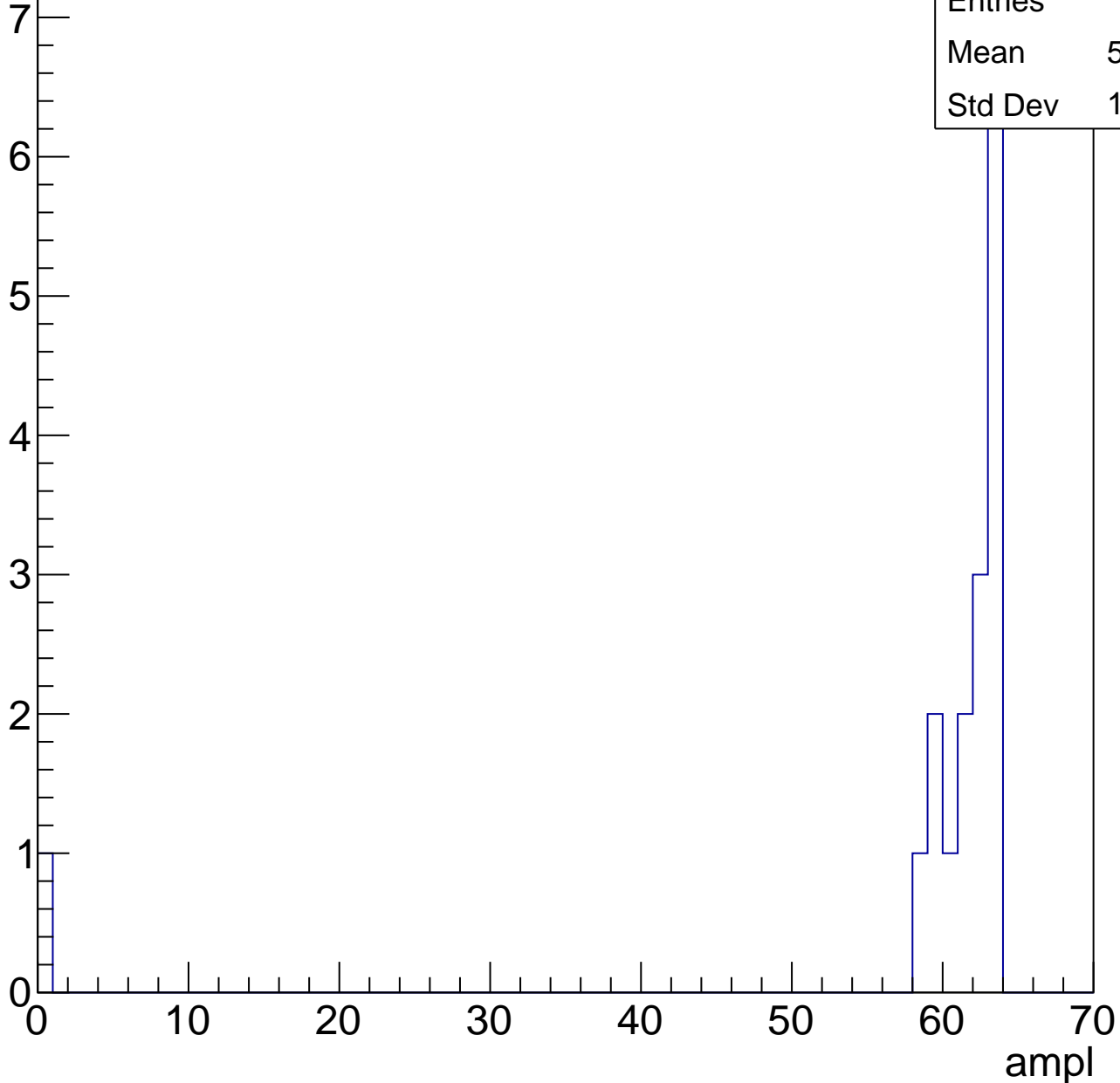


B1L103S, U5-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

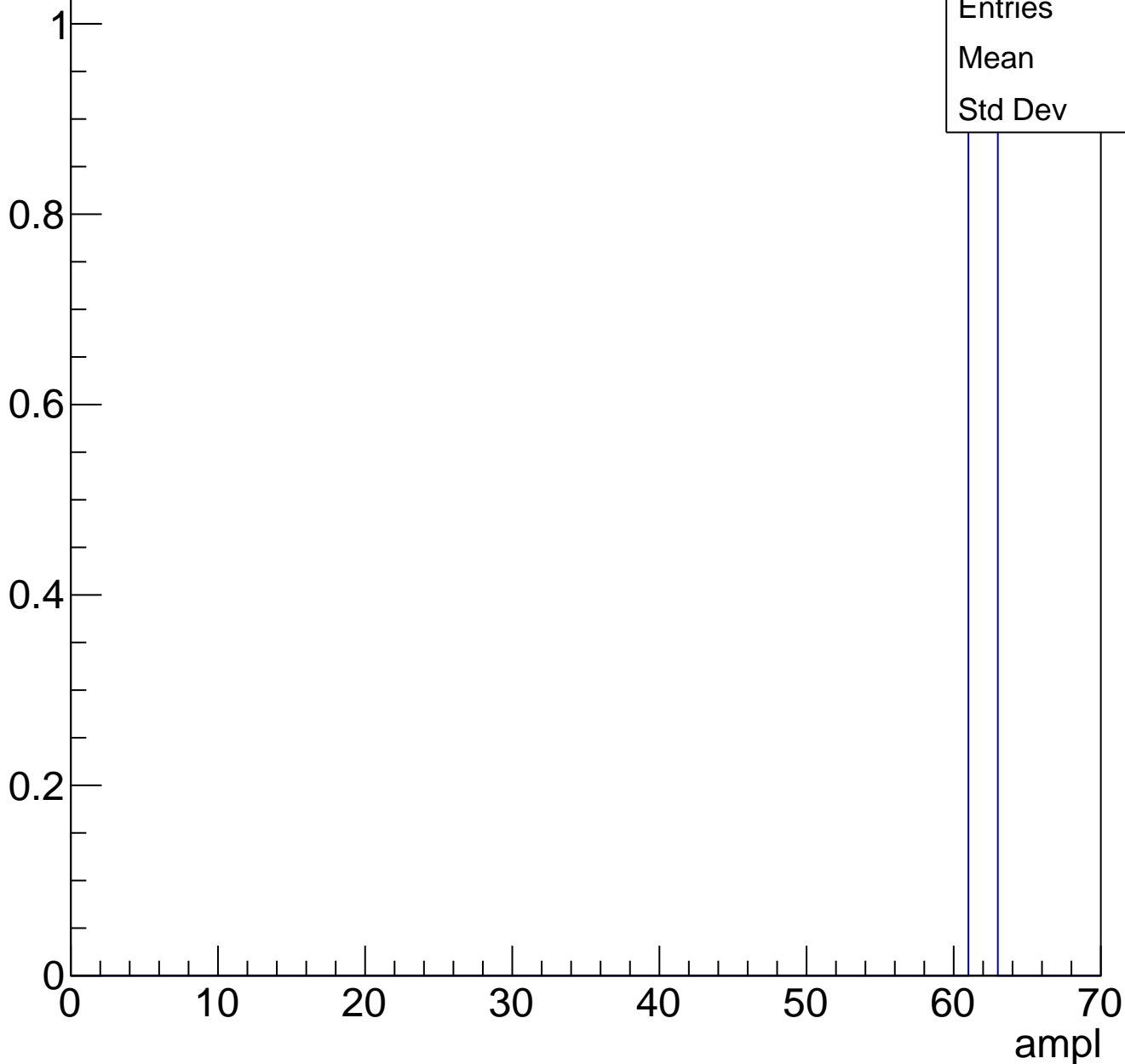
Entries	17
Mean	57.94
Std Dev	14.57



B1L103S, U5-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch23, adc0

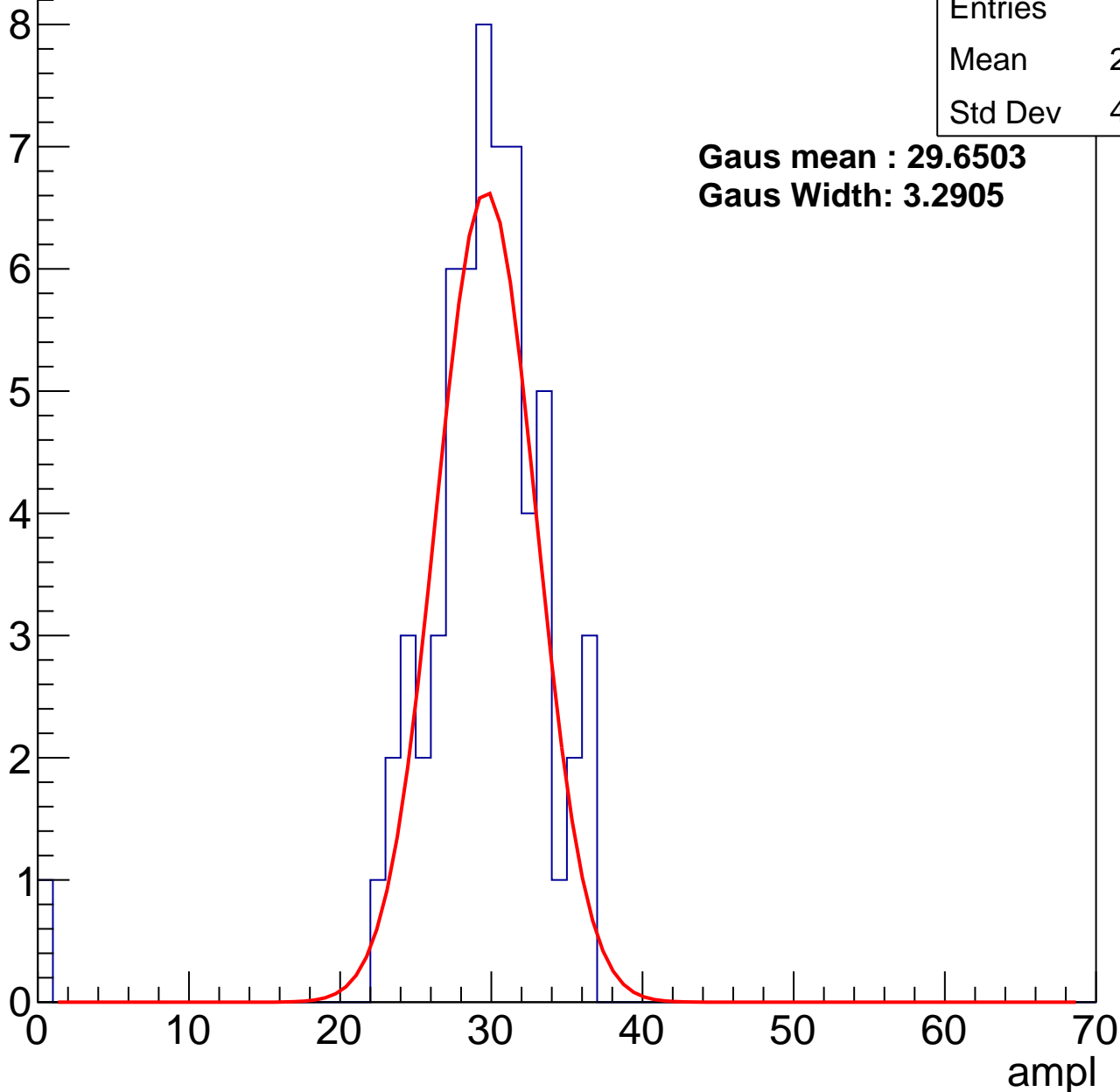
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.89
Std Dev	4.992

Gaus mean : 29.6503

Gaus Width: 3.2905



B1L103S, U5-ch23, adc1

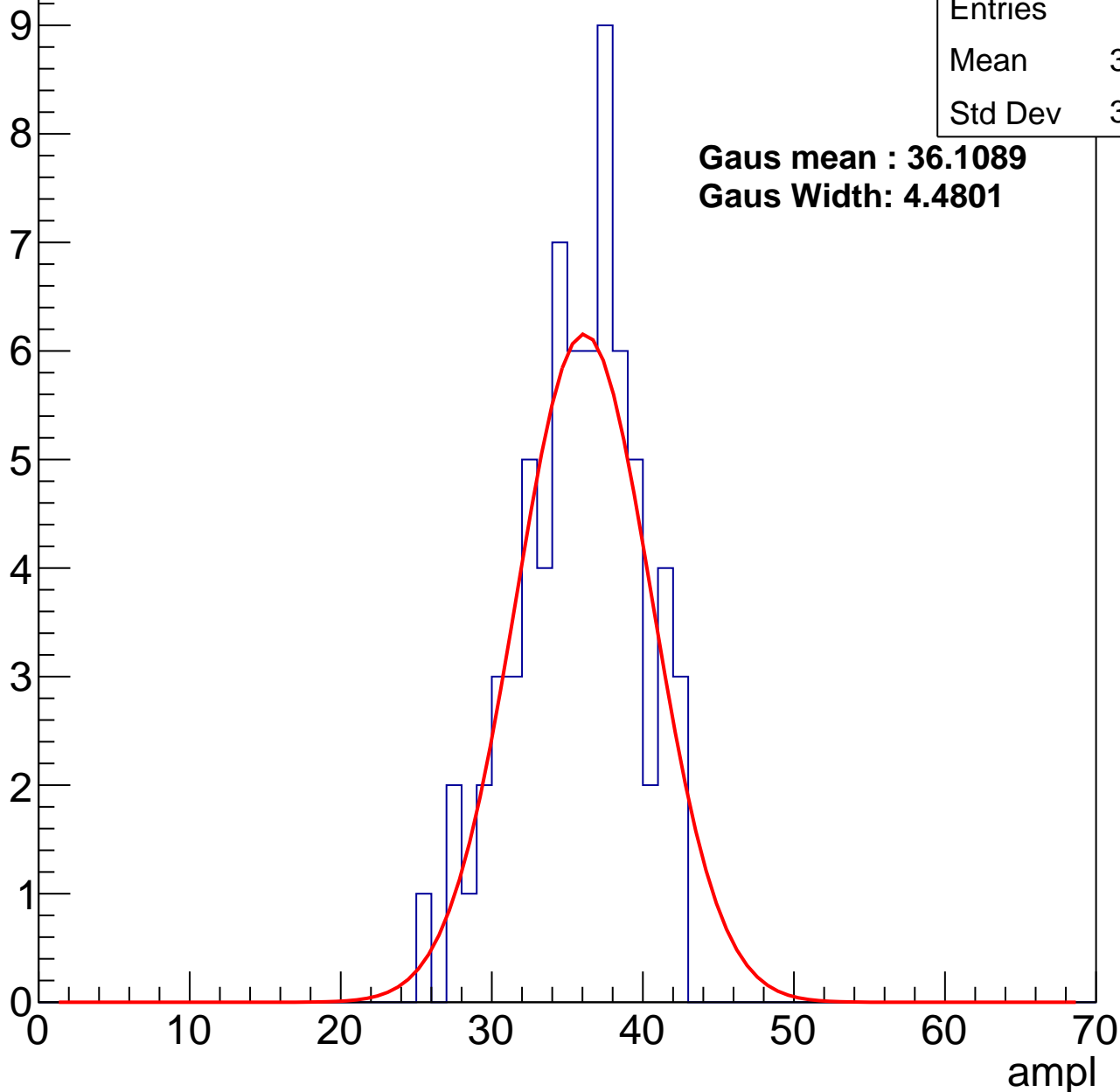
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.22
Std Dev	3.908

Gaus mean : 36.1089

Gaus Width: 4.4801



B1L103S, U5-ch23, adc2

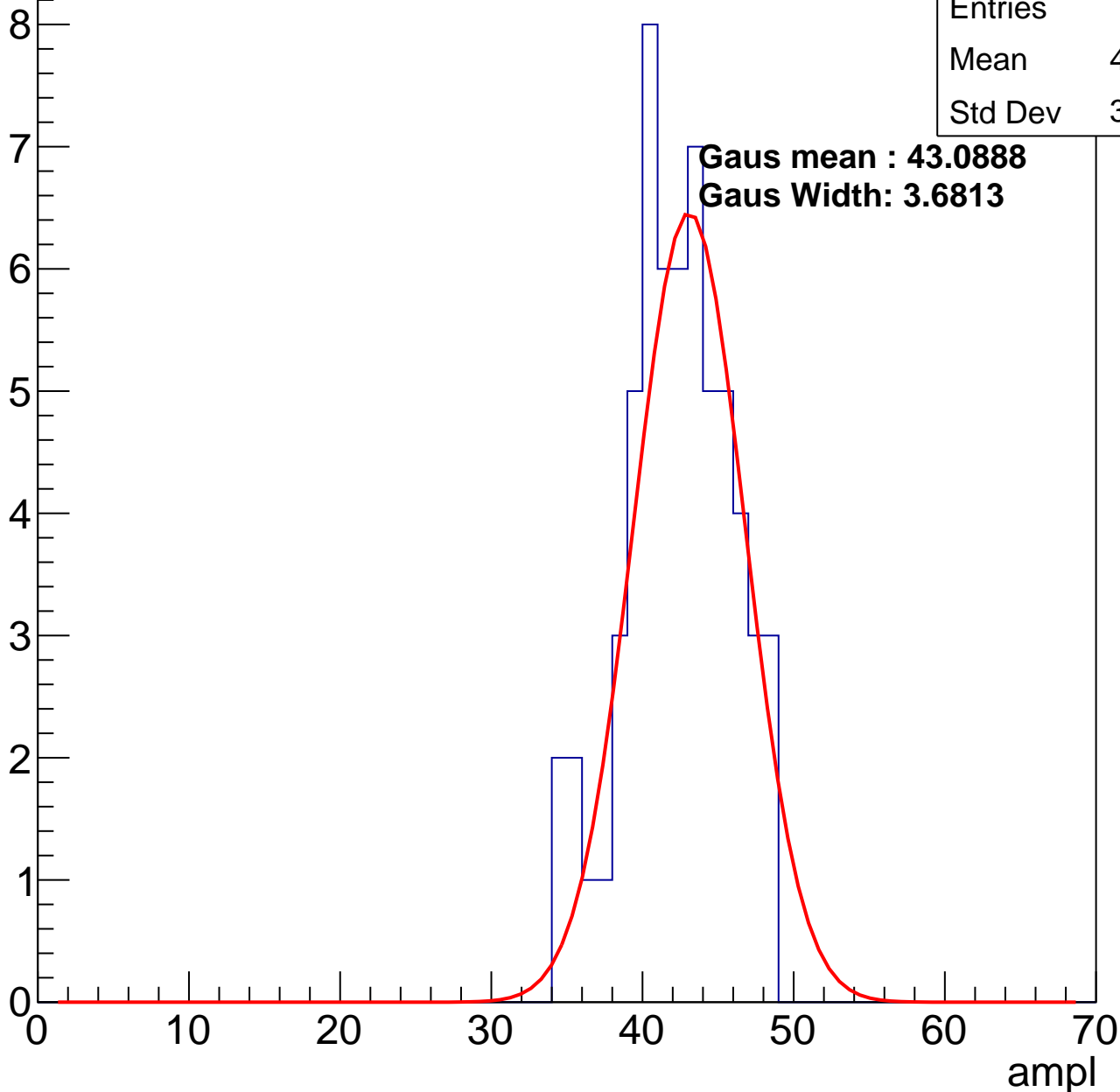
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.85
Std Dev	3.473

Gaus mean : 43.0888

Gaus Width: 3.6813

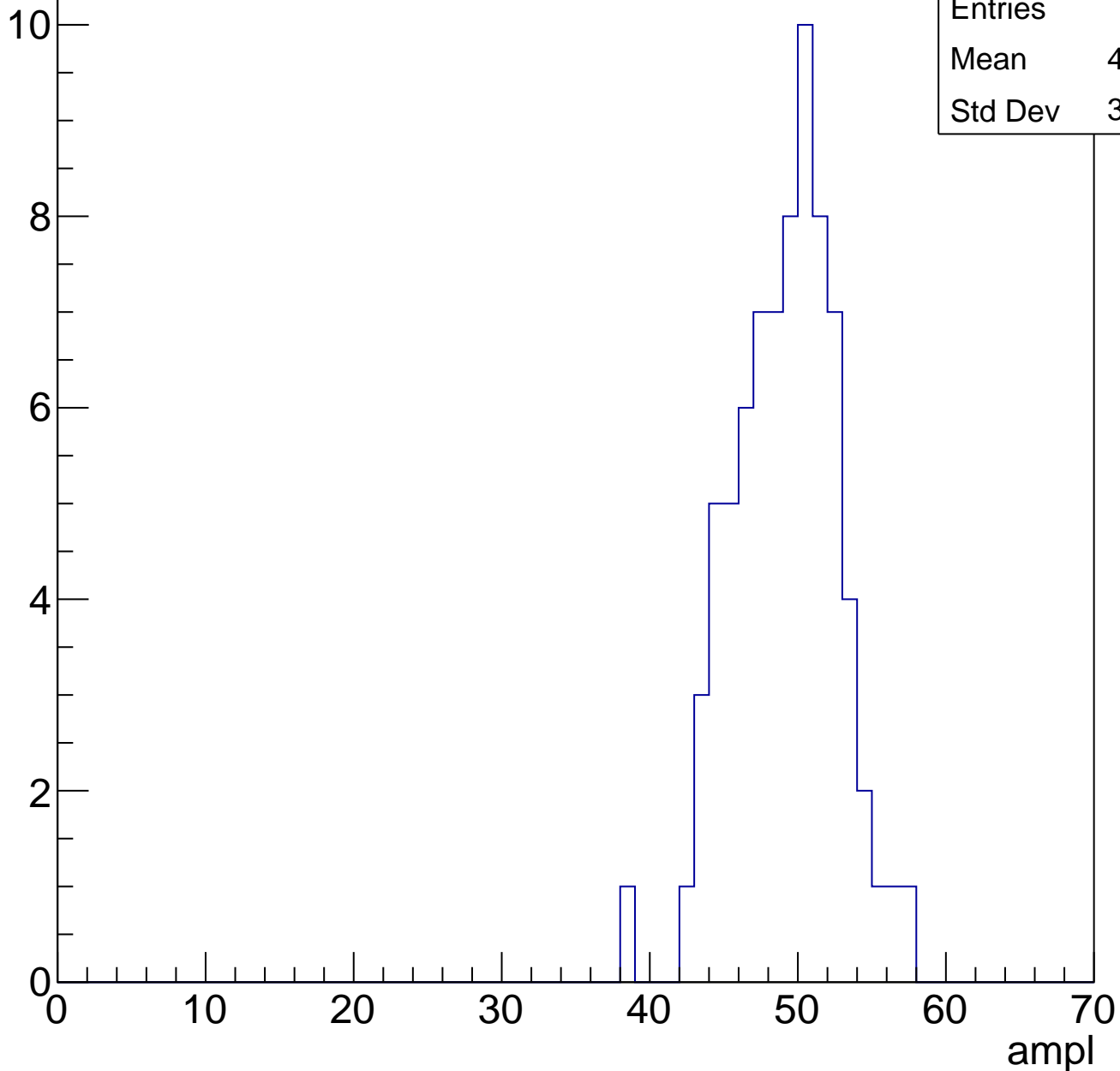


B1L103S, U5-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

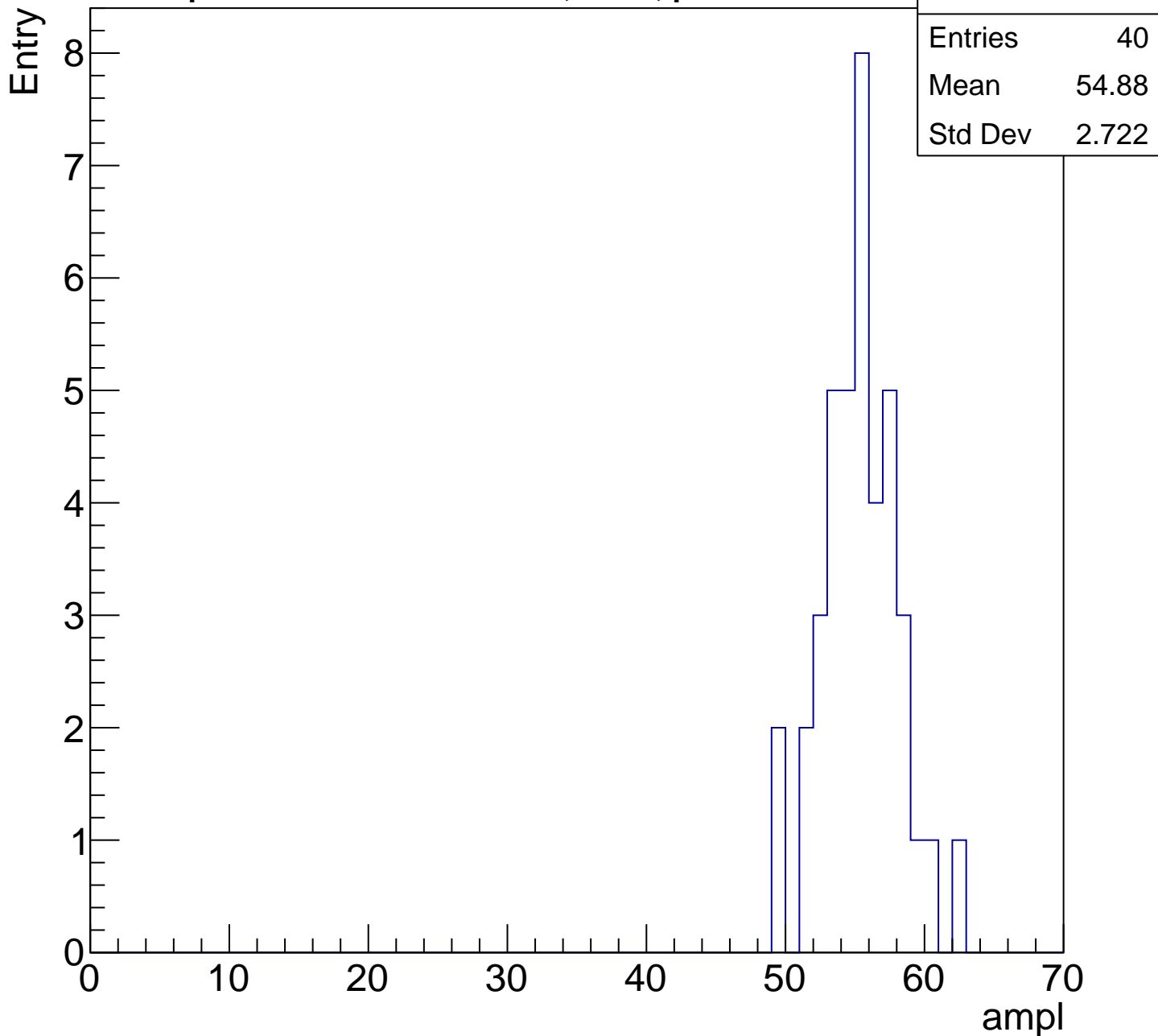
Entries	77
Mean	48.66
Std Dev	3.478

Entry



B1L103S, U5-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

Entries 61

Mean 59.31

Std Dev 2.571

8

6

4

2

0

0

10

20

30

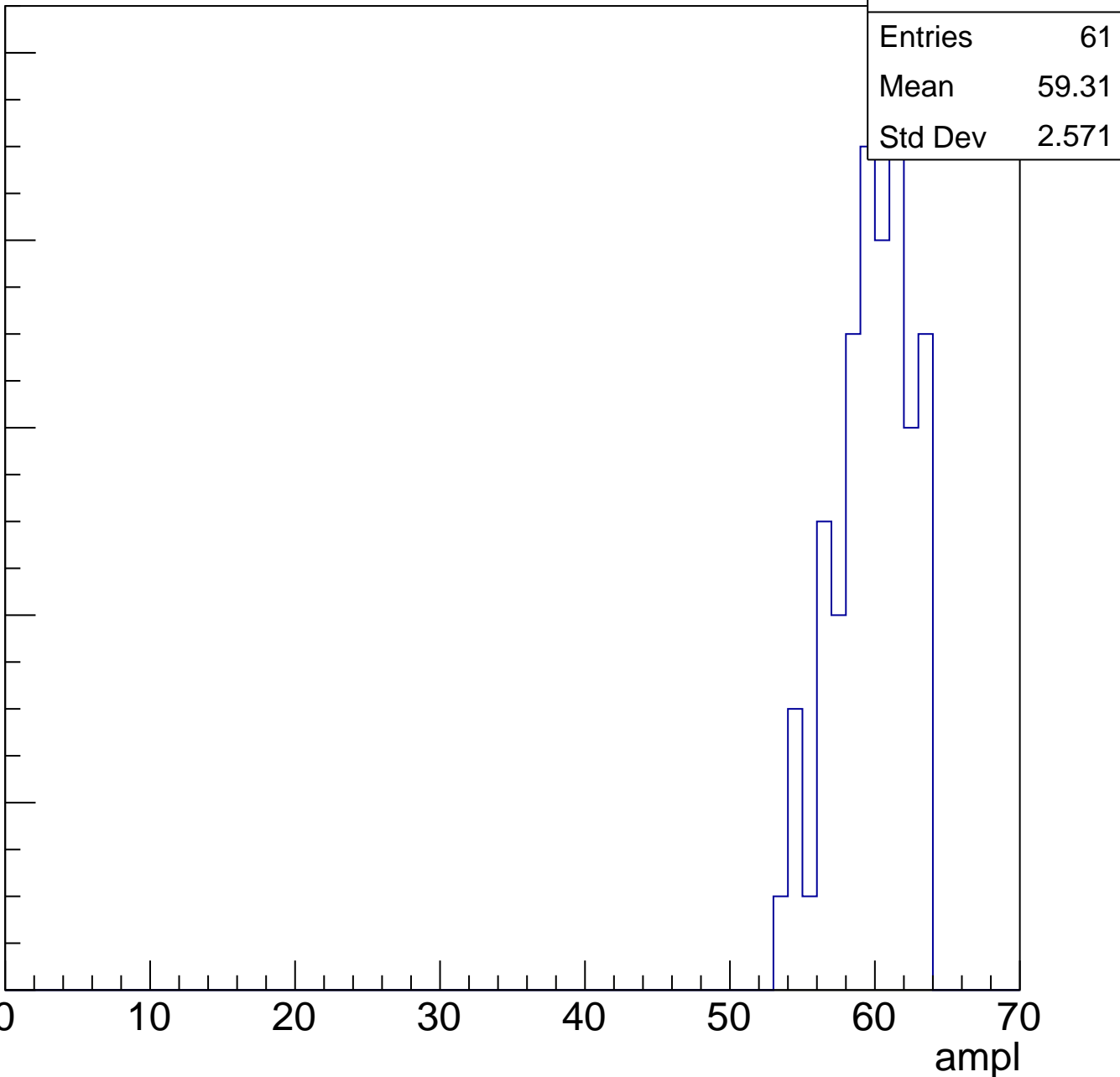
40

50

60

70

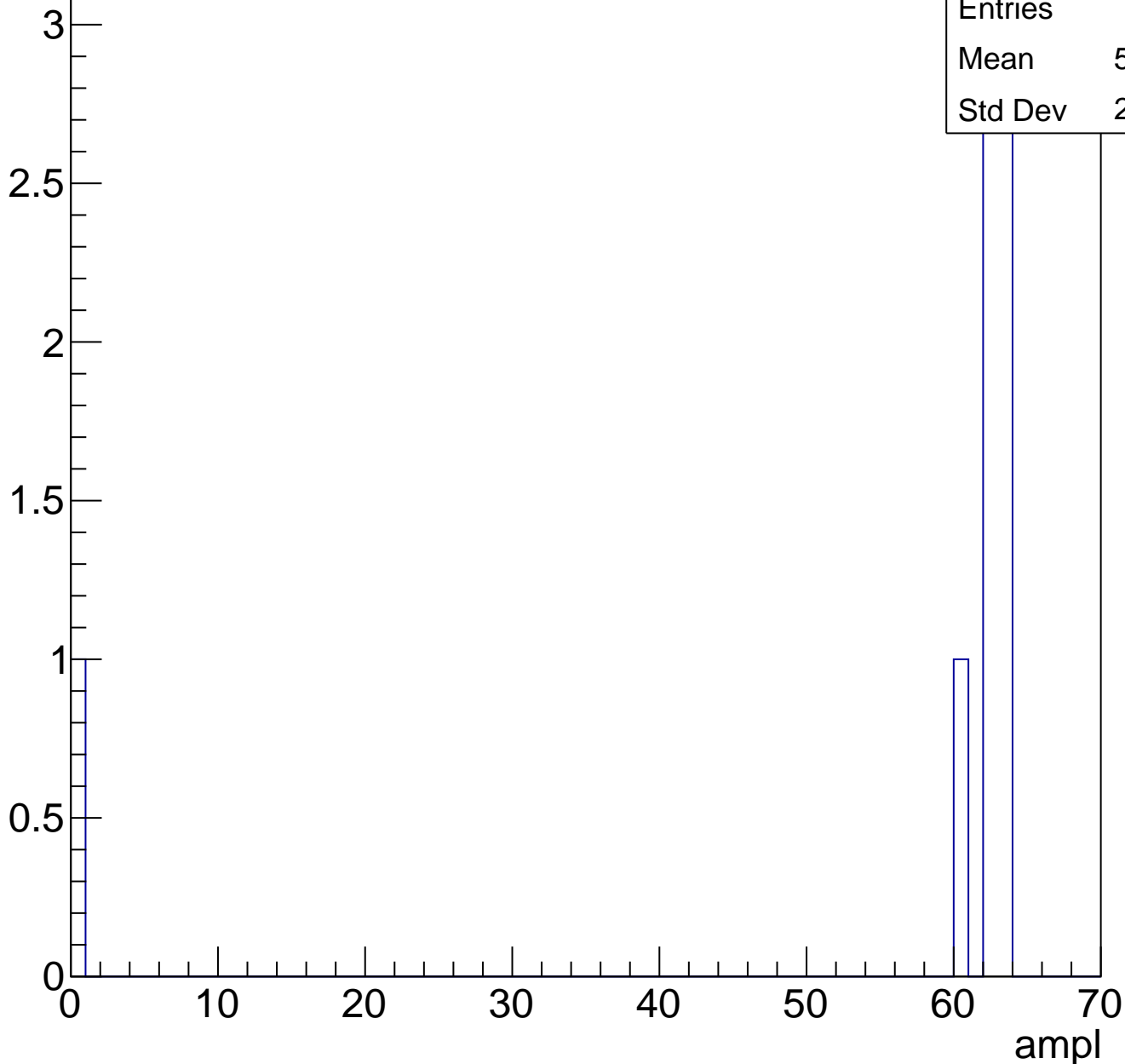
ampl



B1L103S, U5-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch24, adc0

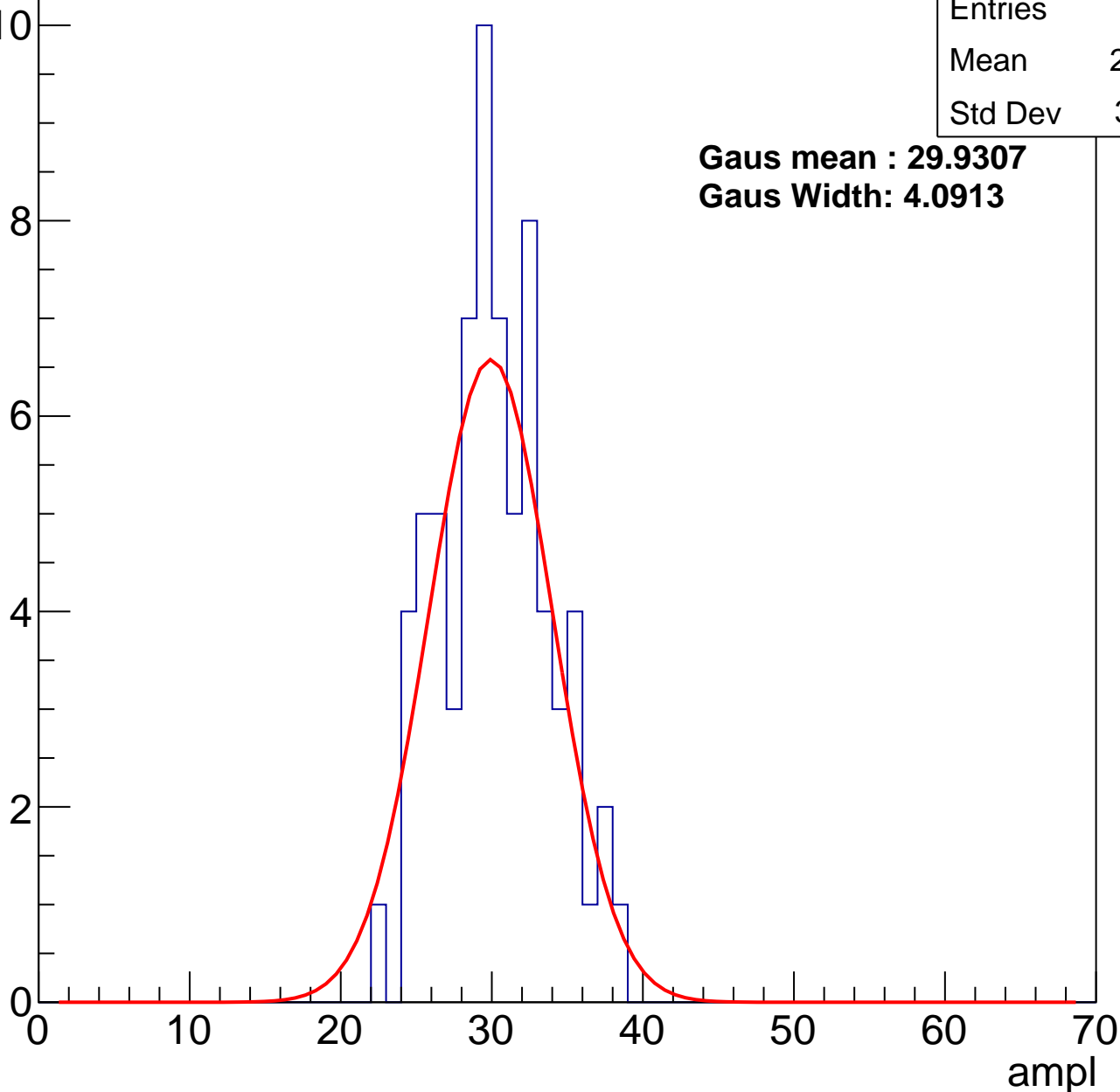
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.76
Std Dev	3.571

Gaus mean : 29.9307

Gaus Width: 4.0913



B1L103S, U5-ch24, adc1

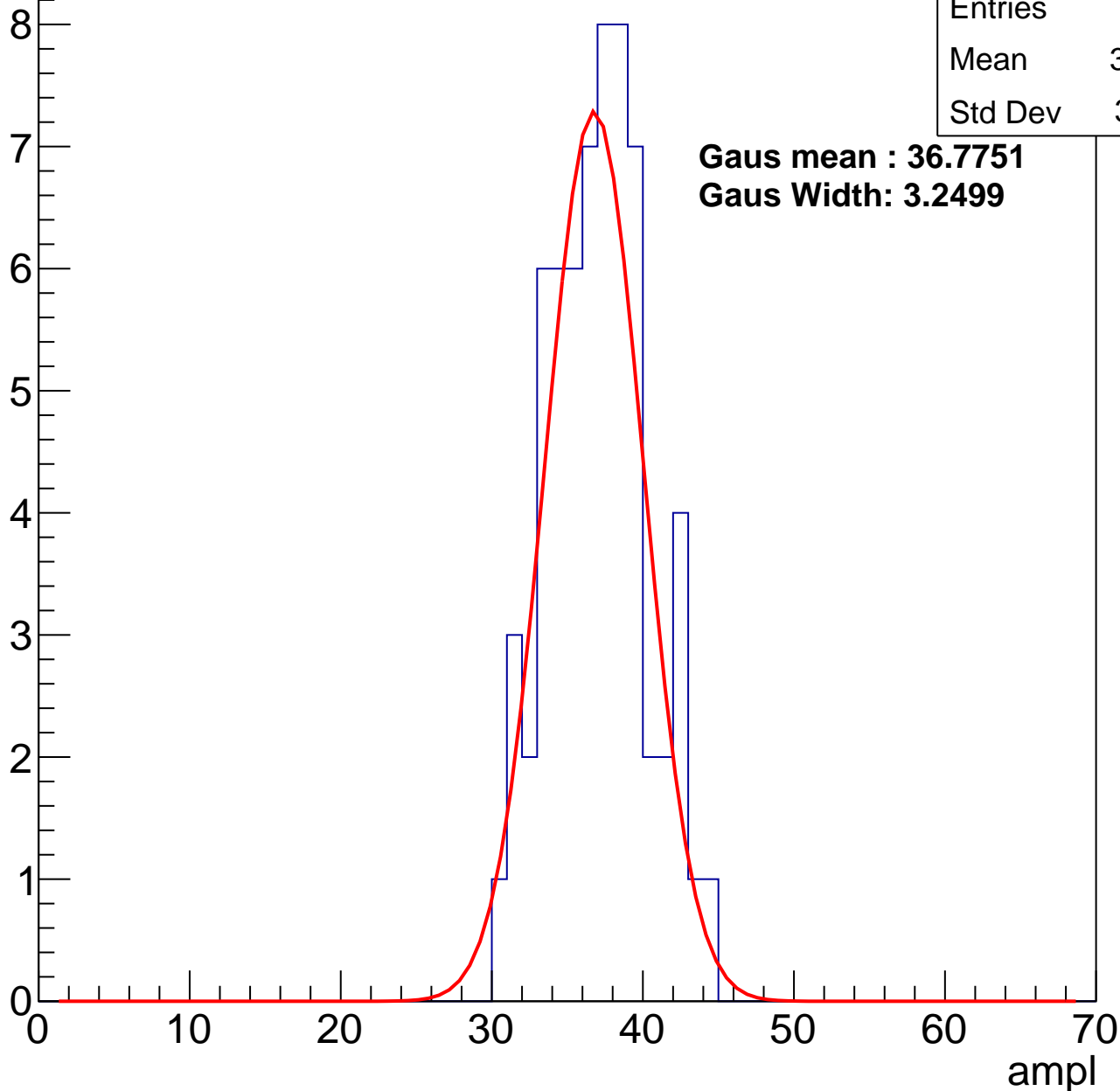
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.58
Std Dev	3.181

Gaus mean : 36.7751

Gaus Width: 3.2499



B1L103S, U5-ch24, adc2

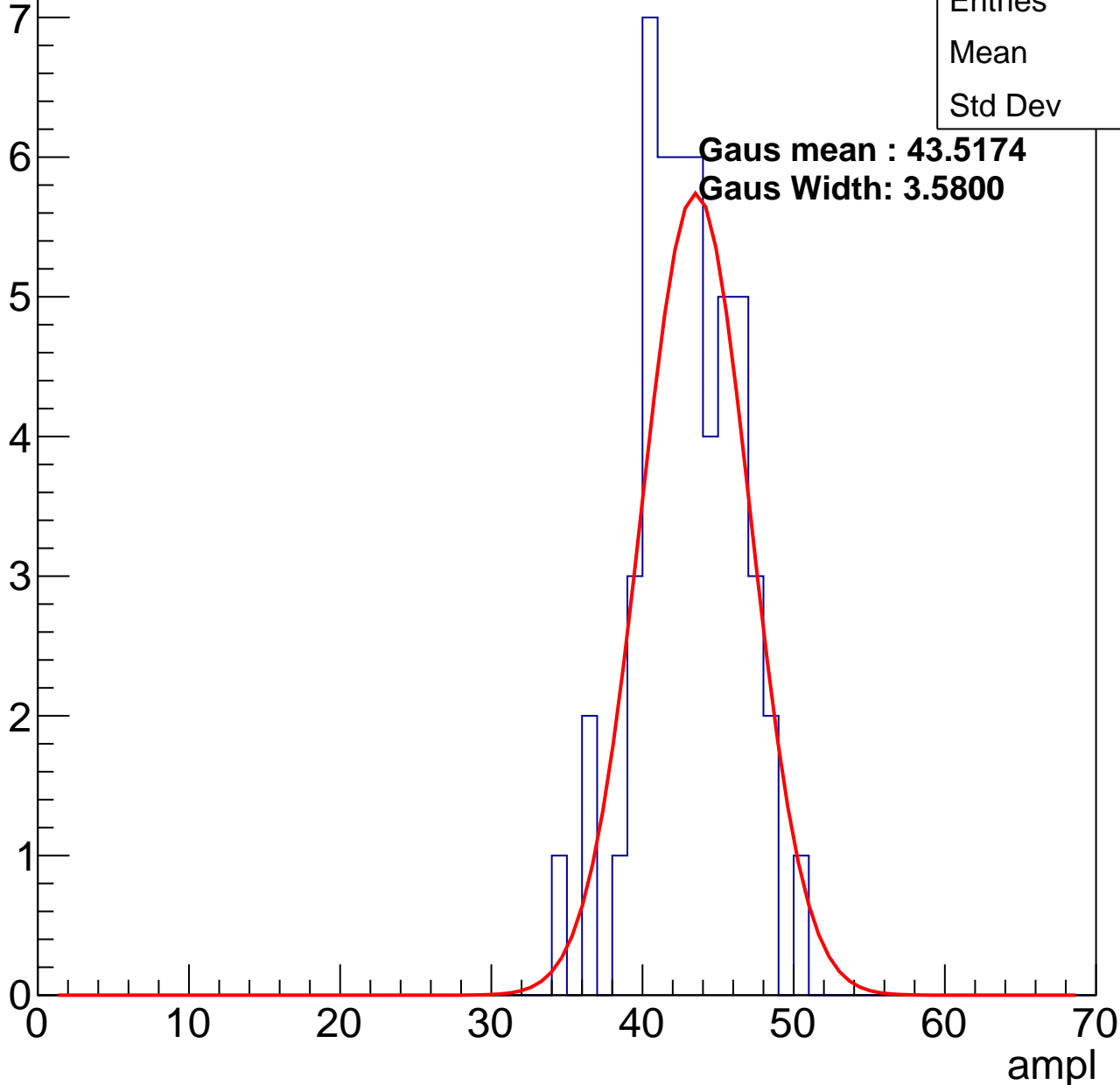
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	42.6
Std Dev	3.26

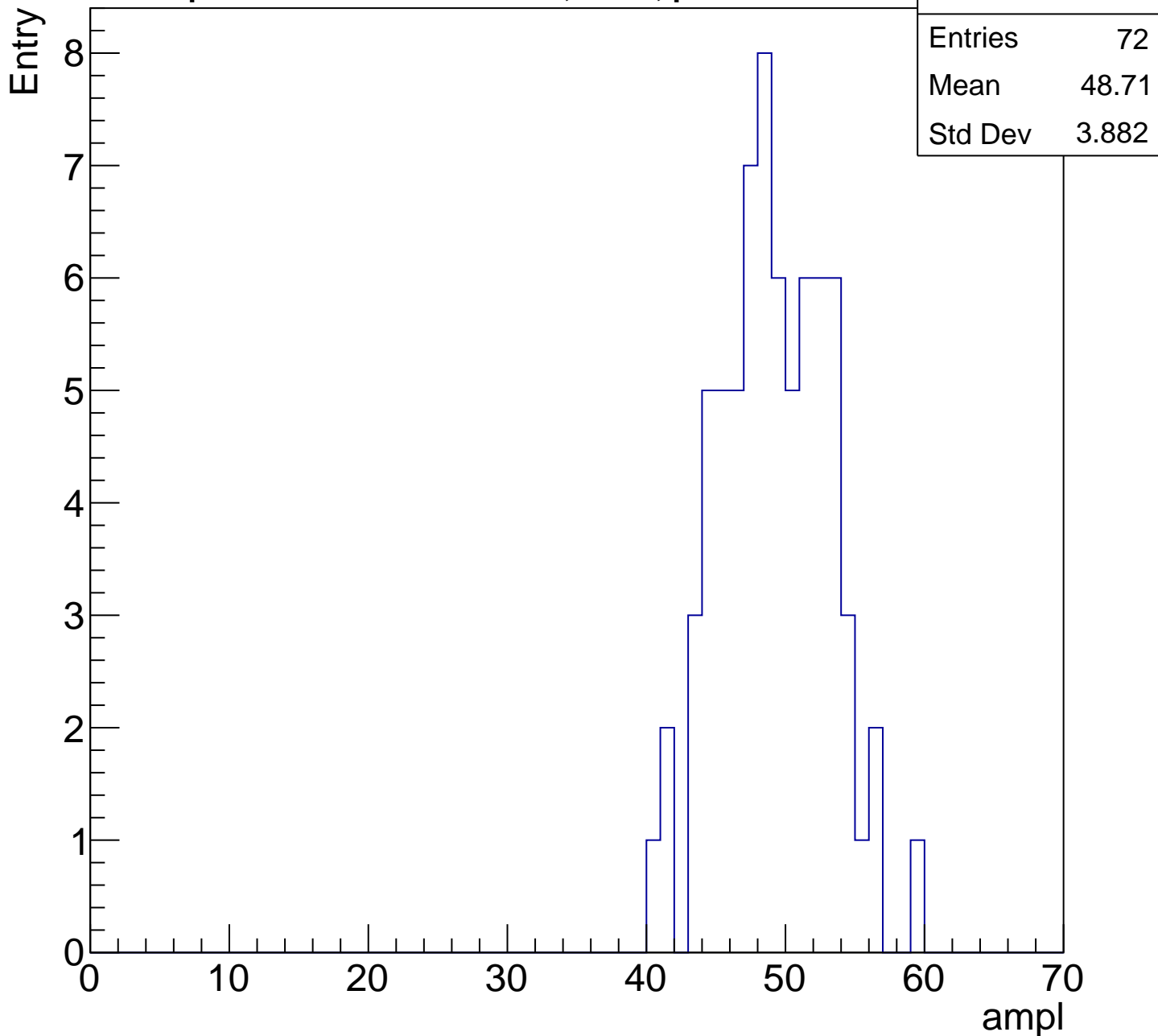
Gaus mean : 43.5174

Gaus Width: 3.5800



B1L103S, U5-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

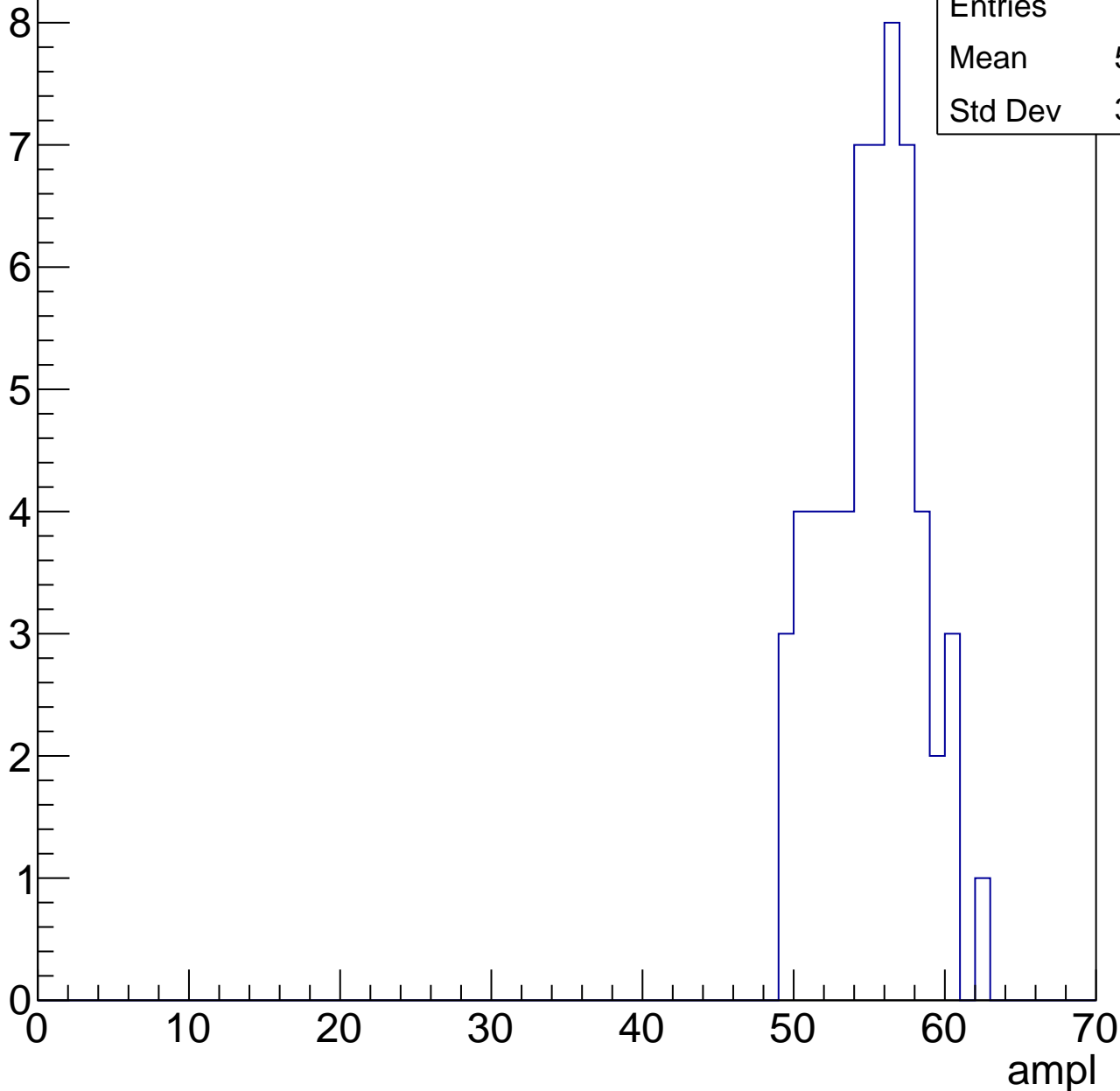


B1L103S, U5-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.71
Std Dev	3.091

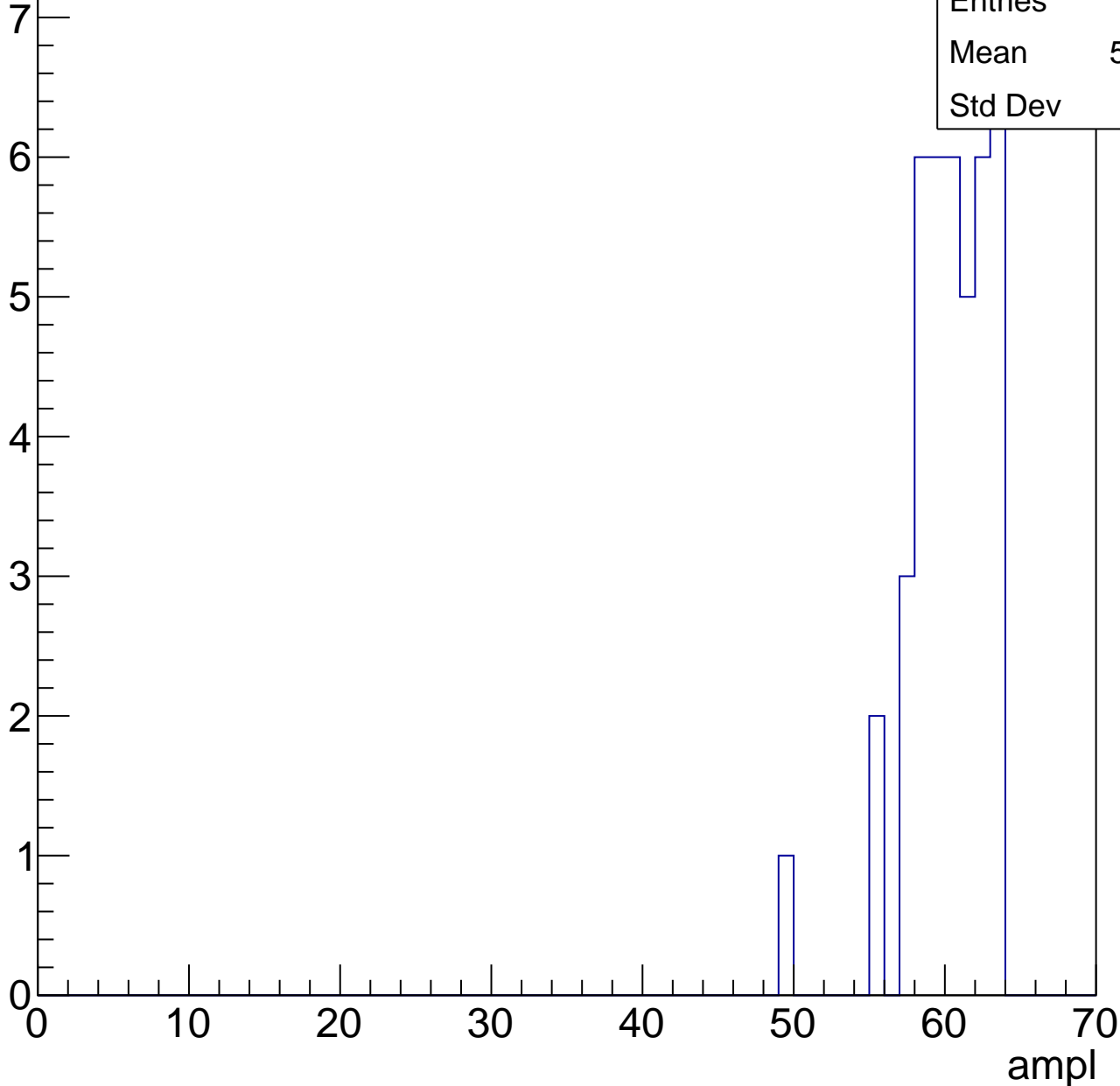


B1L103S, U5-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59.76
Std Dev	2.75

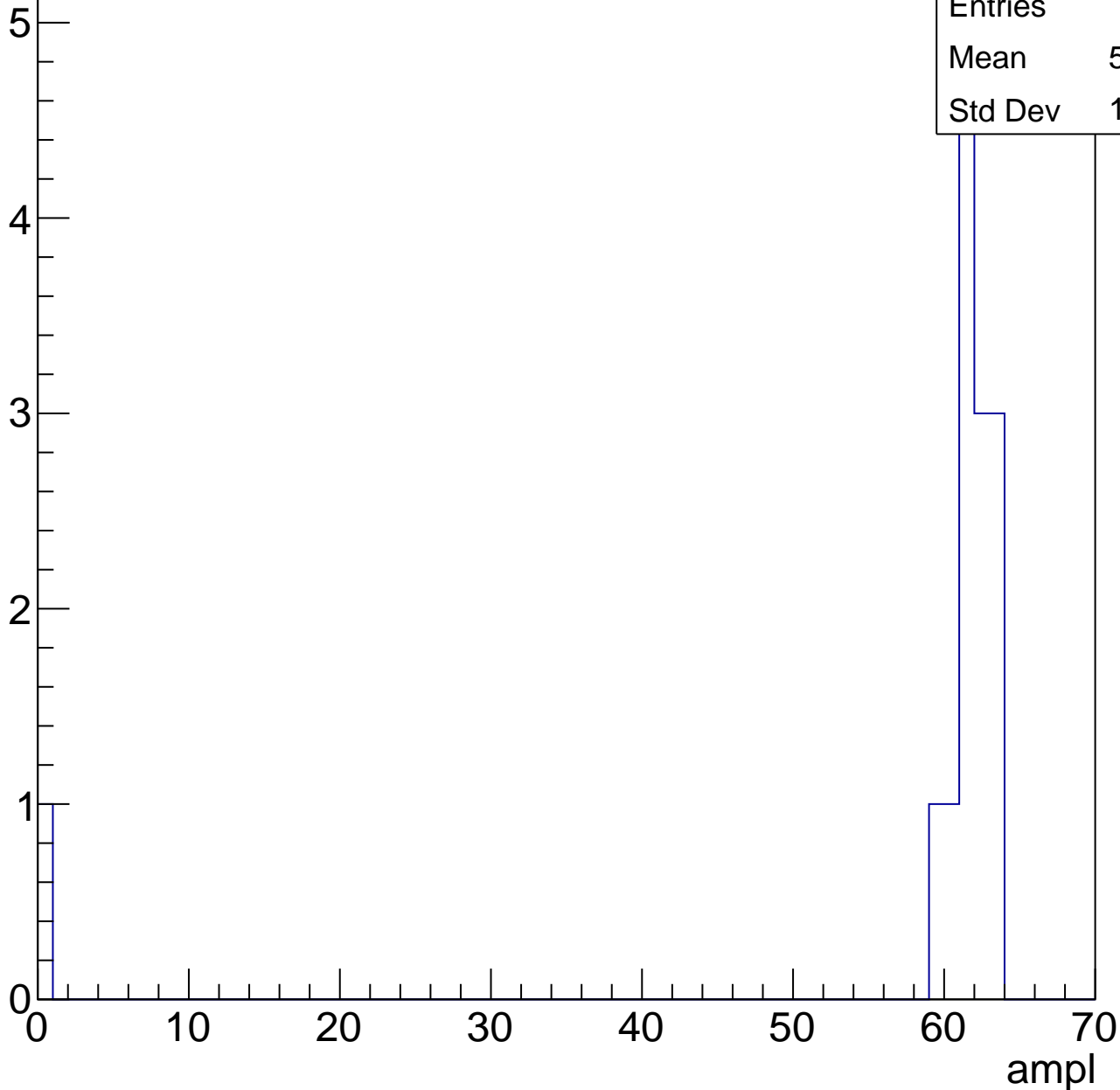


B1L103S, U5-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	57.07
Std Dev	15.87



B1L103S, U5-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch25, adc0

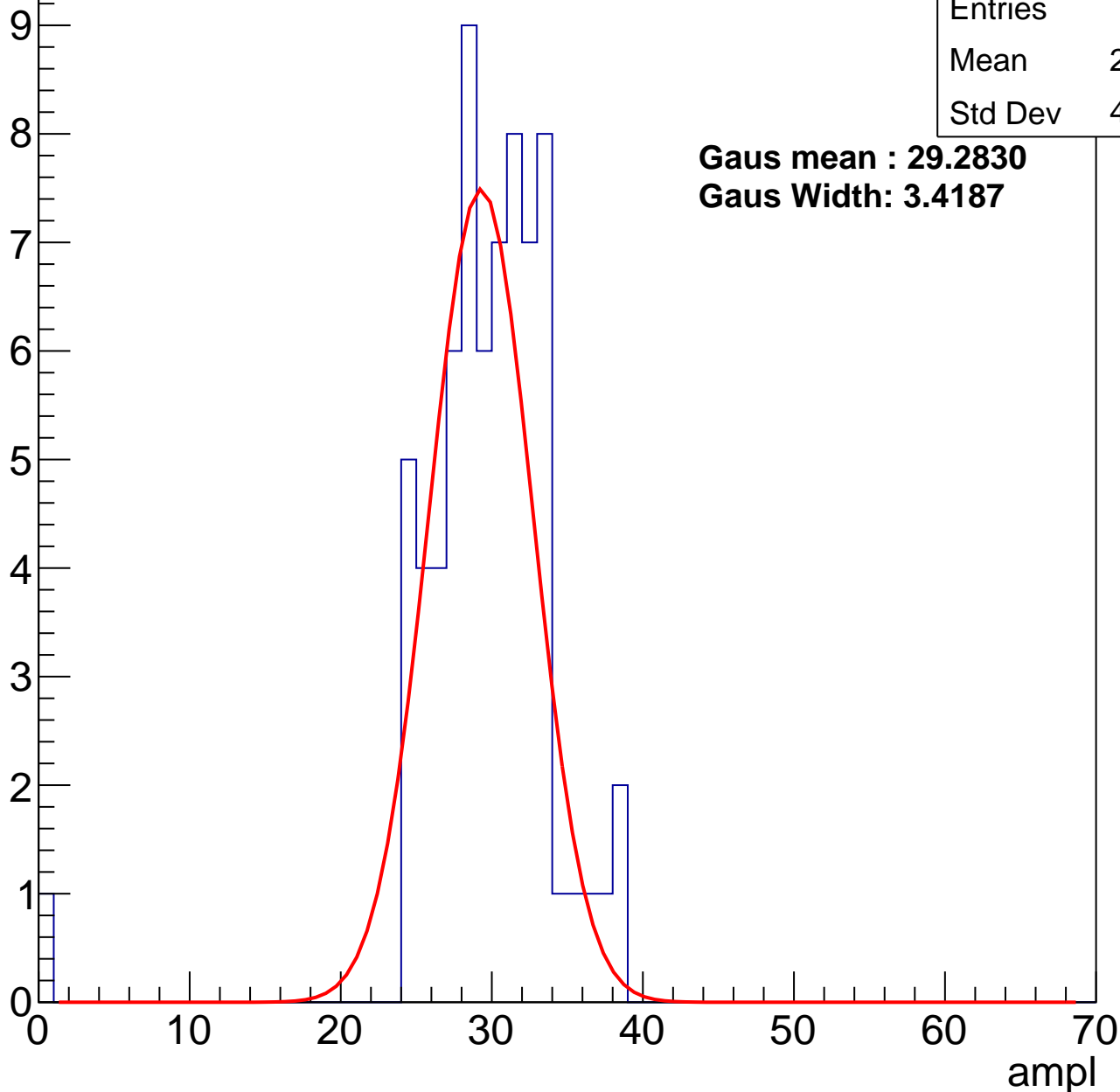
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.24
Std Dev	4.837

Gaus mean : 29.2830

Gaus Width: 3.4187



B1L103S, U5-ch25, adc1

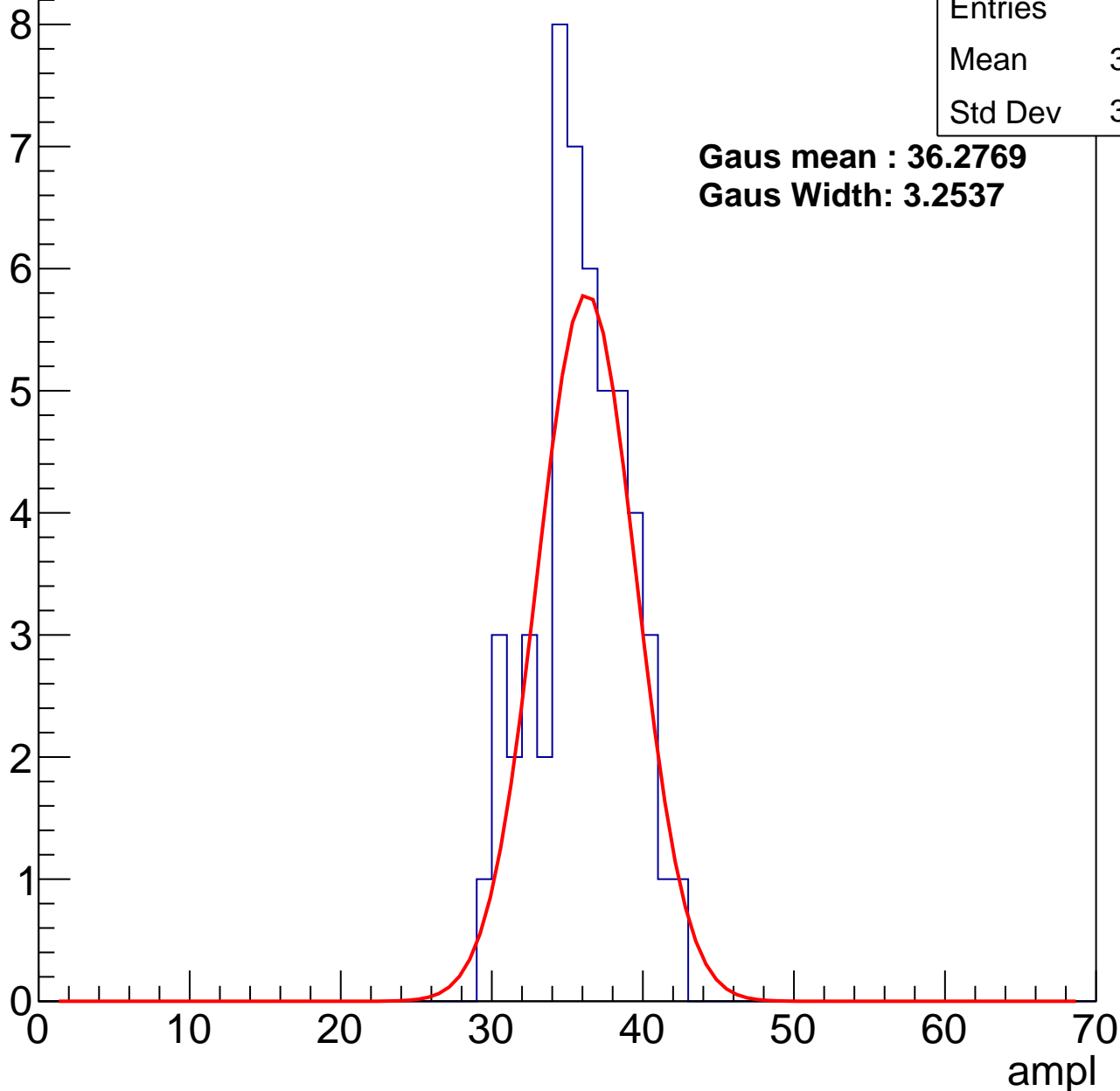
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	35.49
Std Dev	3.032

Gaus mean : 36.2769

Gaus Width: 3.2537



B1L103S, U5-ch25, adc2

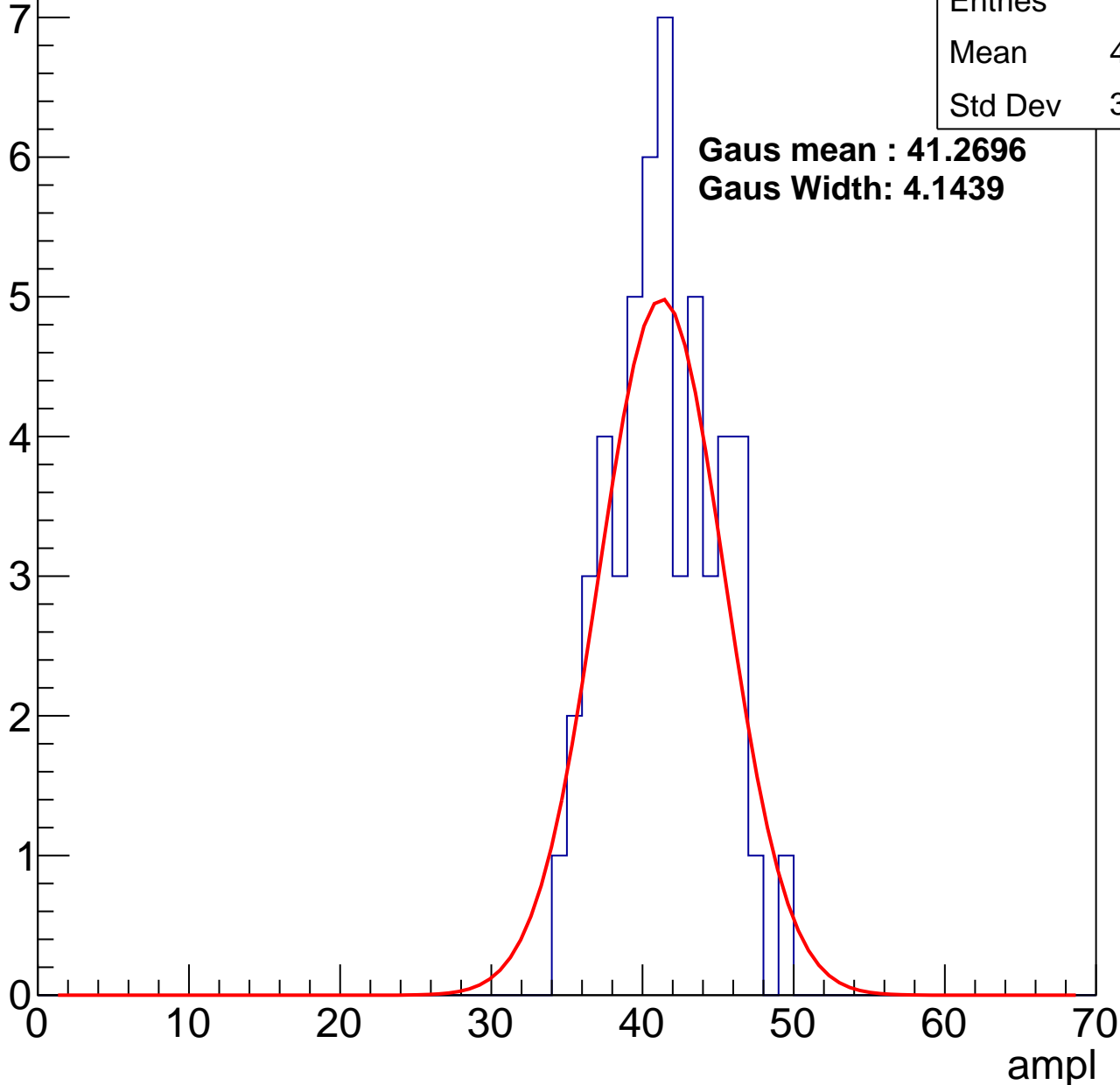
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	40.94
Std Dev	3.472

Gaus mean : 41.2696

Gaus Width: 4.1439

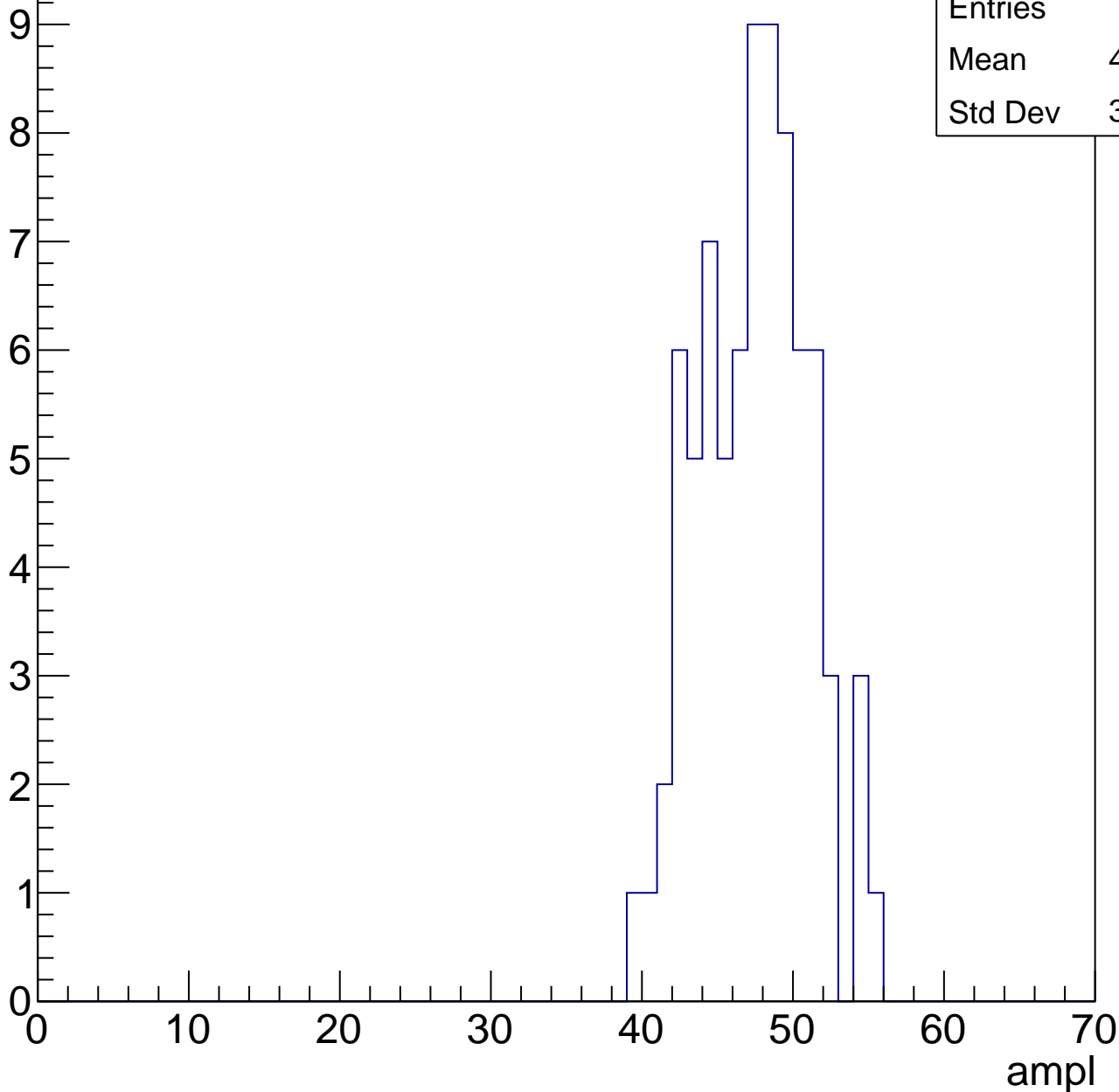


B1L103S, U5-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

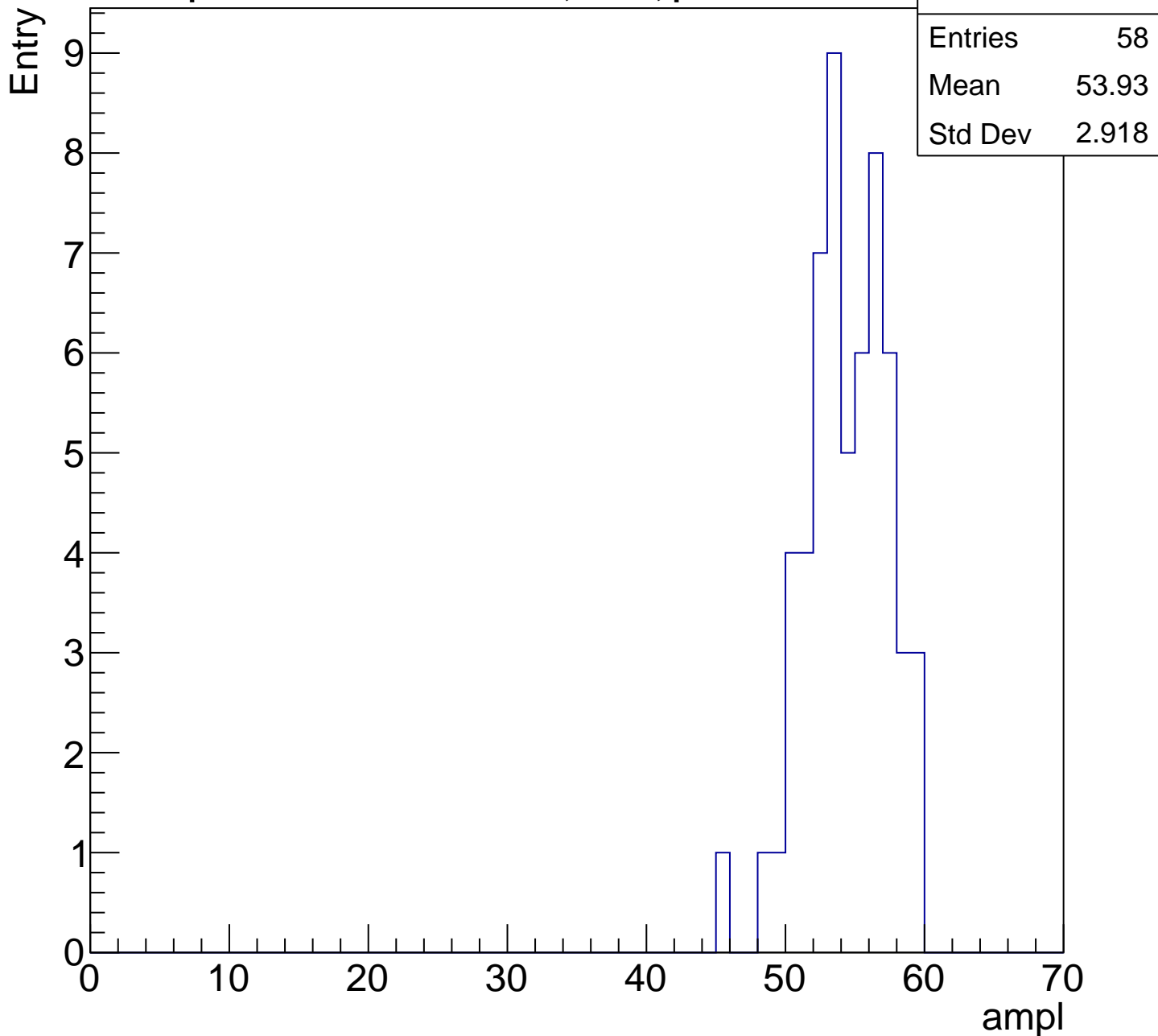
Entry

Entries	78
Mean	46.96
Std Dev	3.546



B1L103S, U5-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

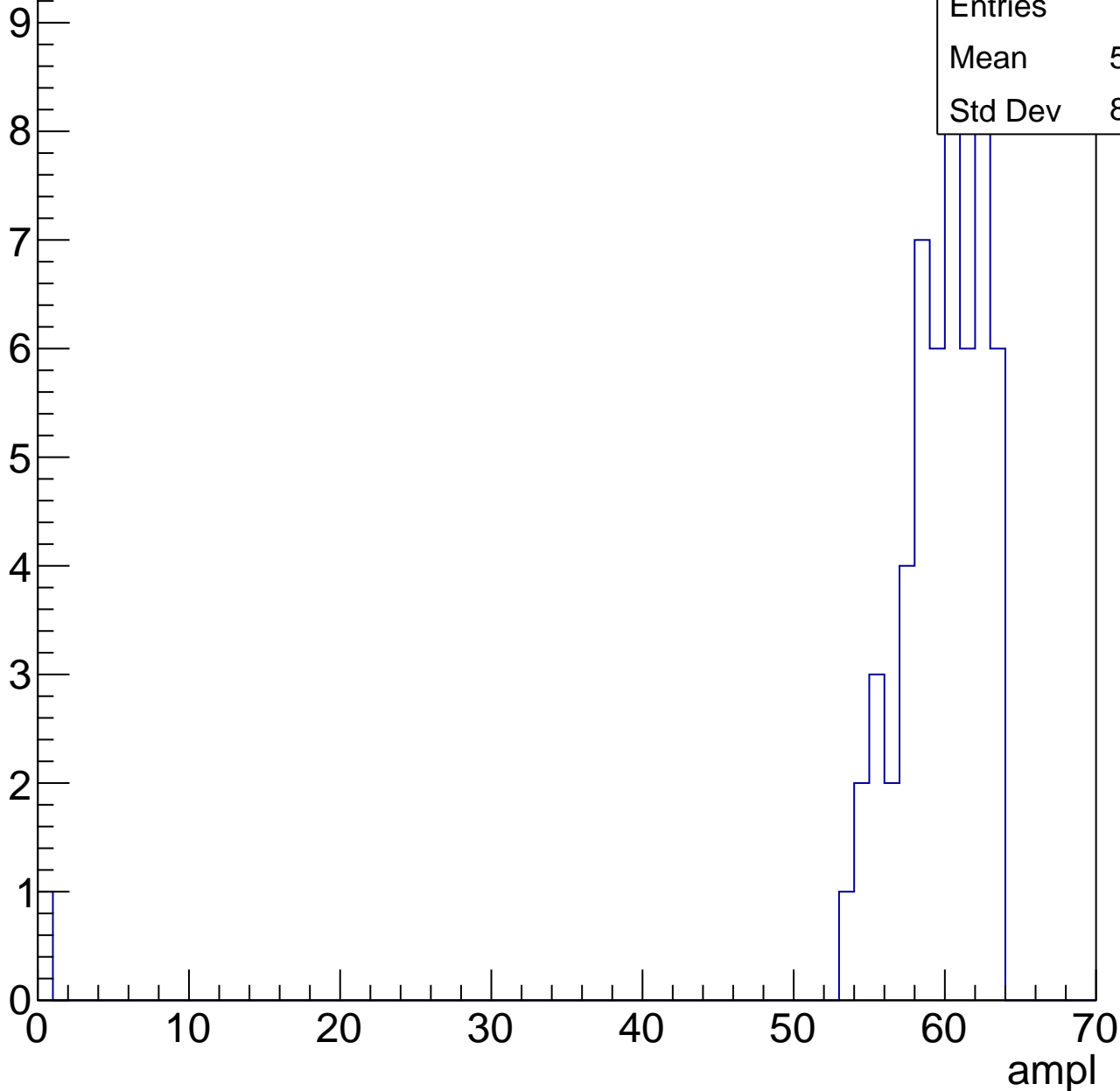


B1L103S, U5-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

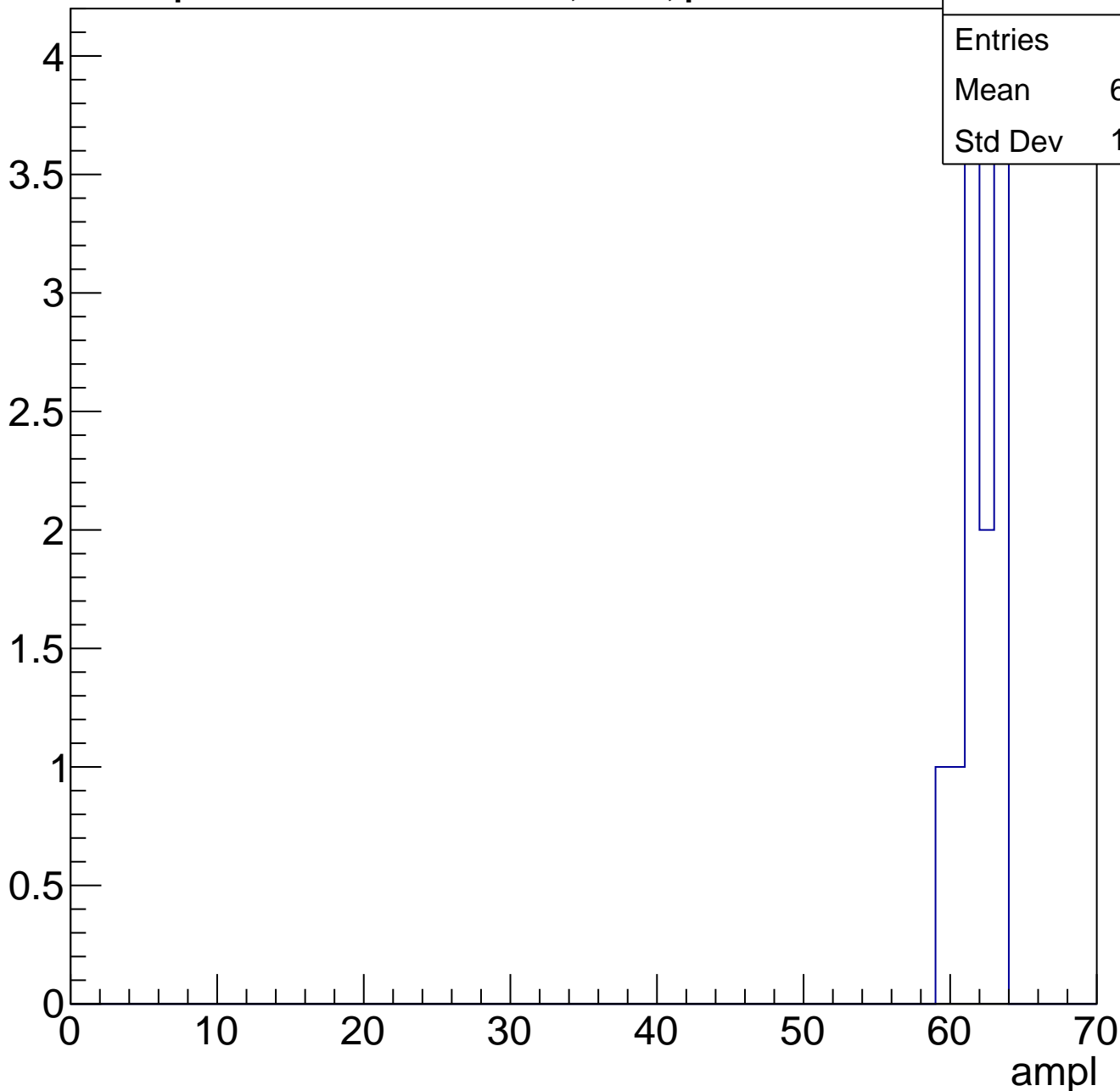
Entries	55
Mean	58.29
Std Dev	8.344



B1L103S, U5-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L103S, U5-ch26, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	30.05
Std Dev	3.62

Gaus mean : 30.7493

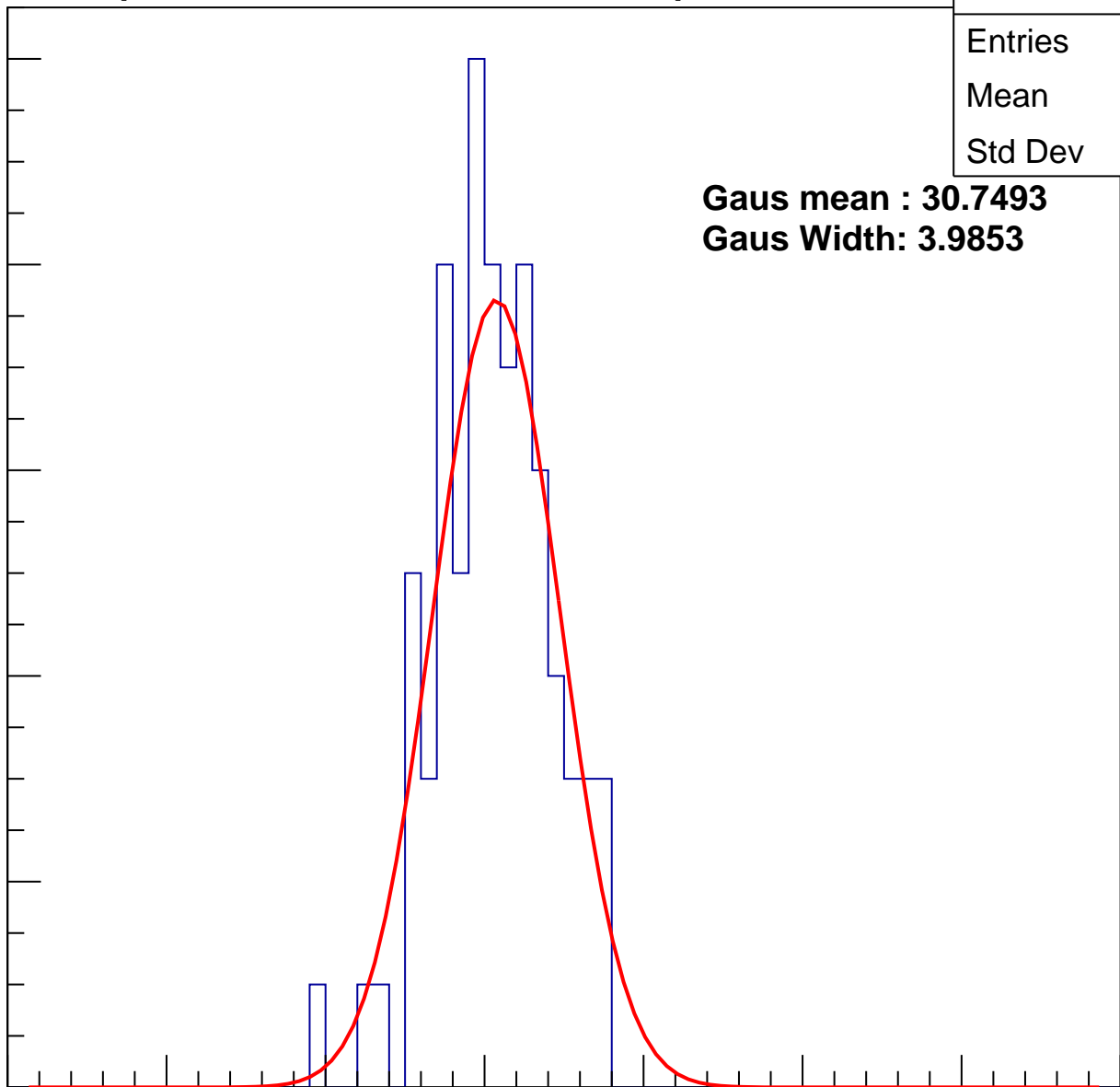
Gaus Width: 3.9853

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch26, adc1

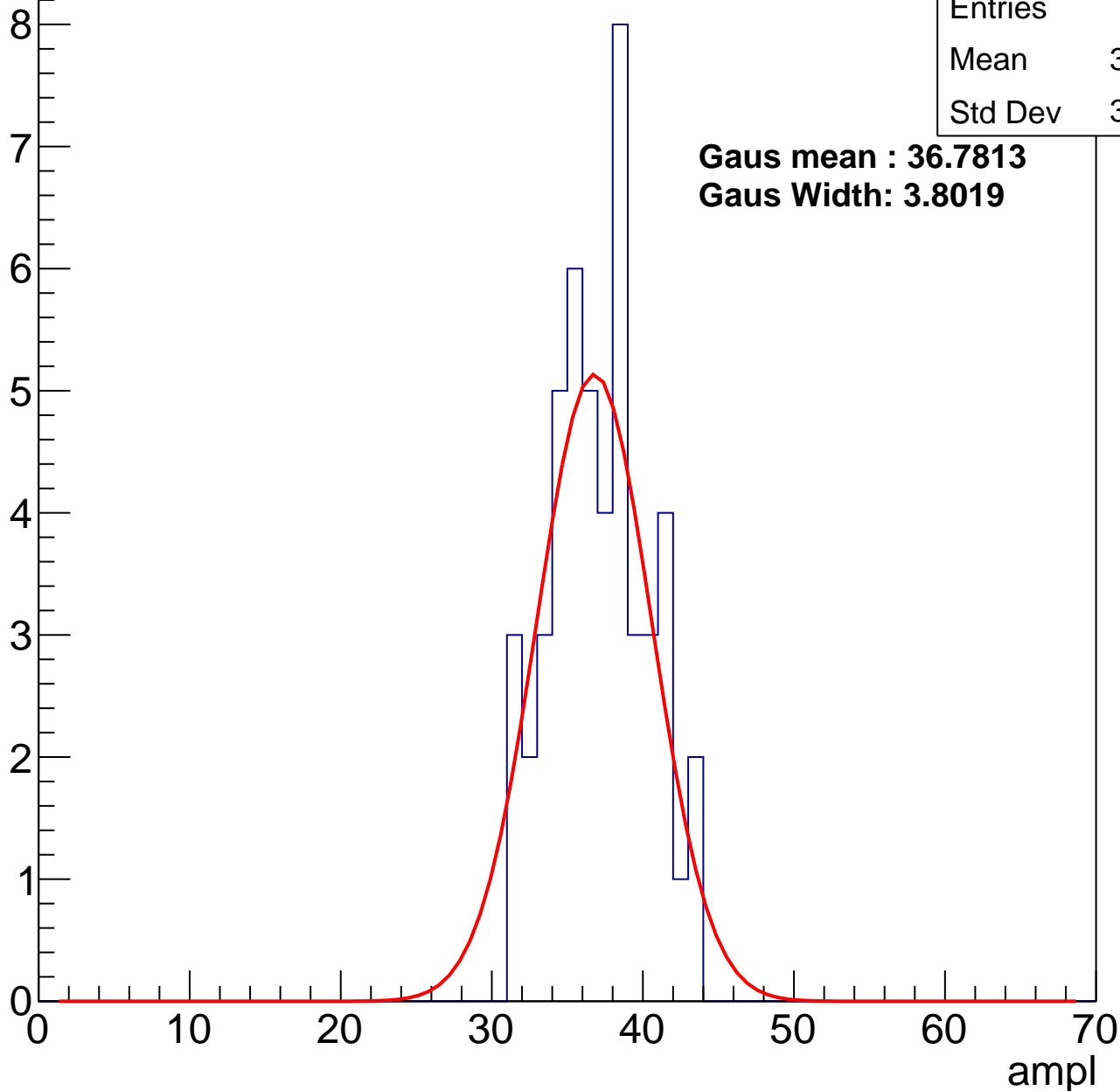
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	36.67
Std Dev	3.139

Gaus mean : 36.7813

Gaus Width: 3.8019



B1L103S, U5-ch26, adc2

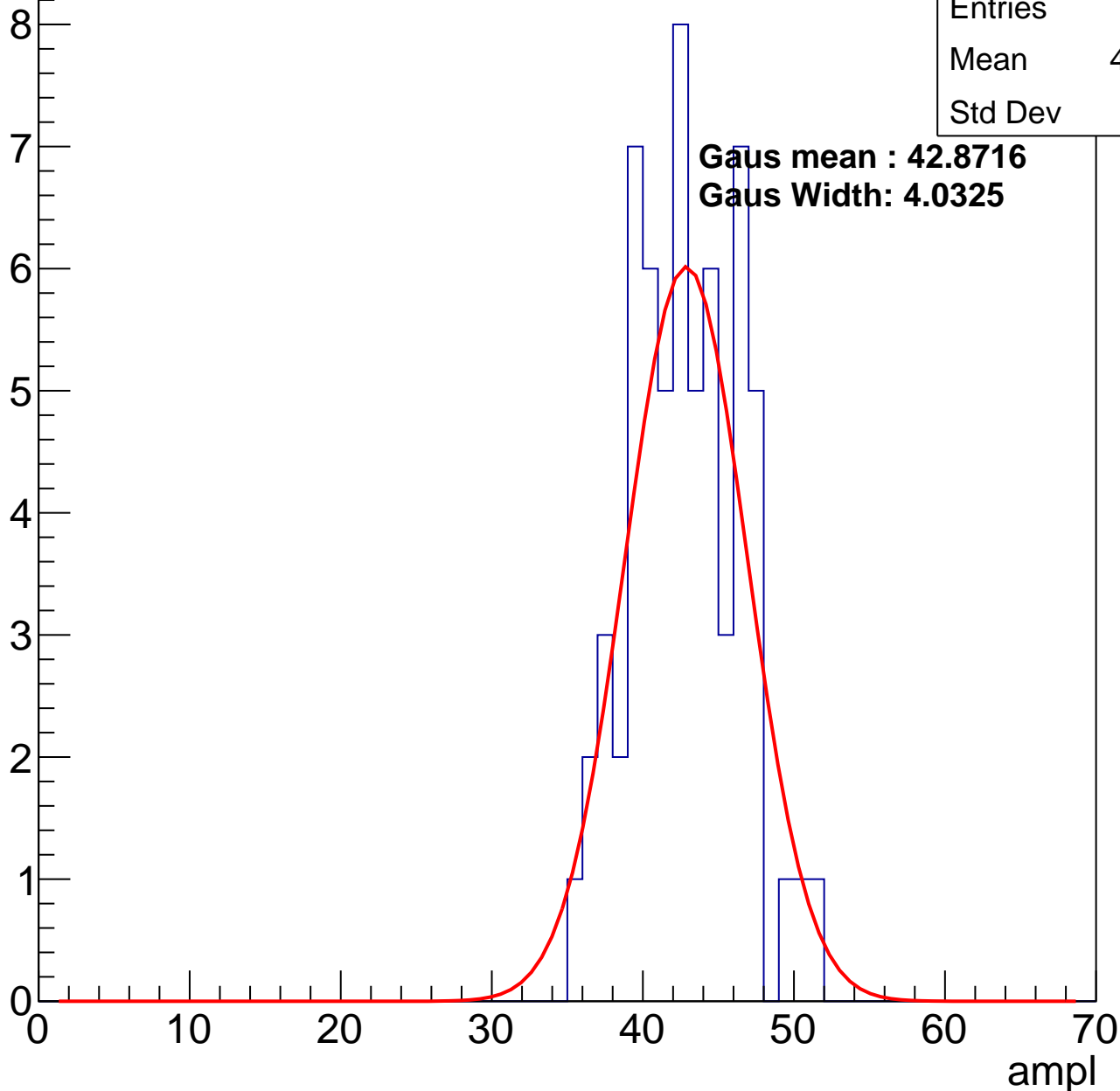
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.37
Std Dev	3.56

Gaus mean : 42.8716

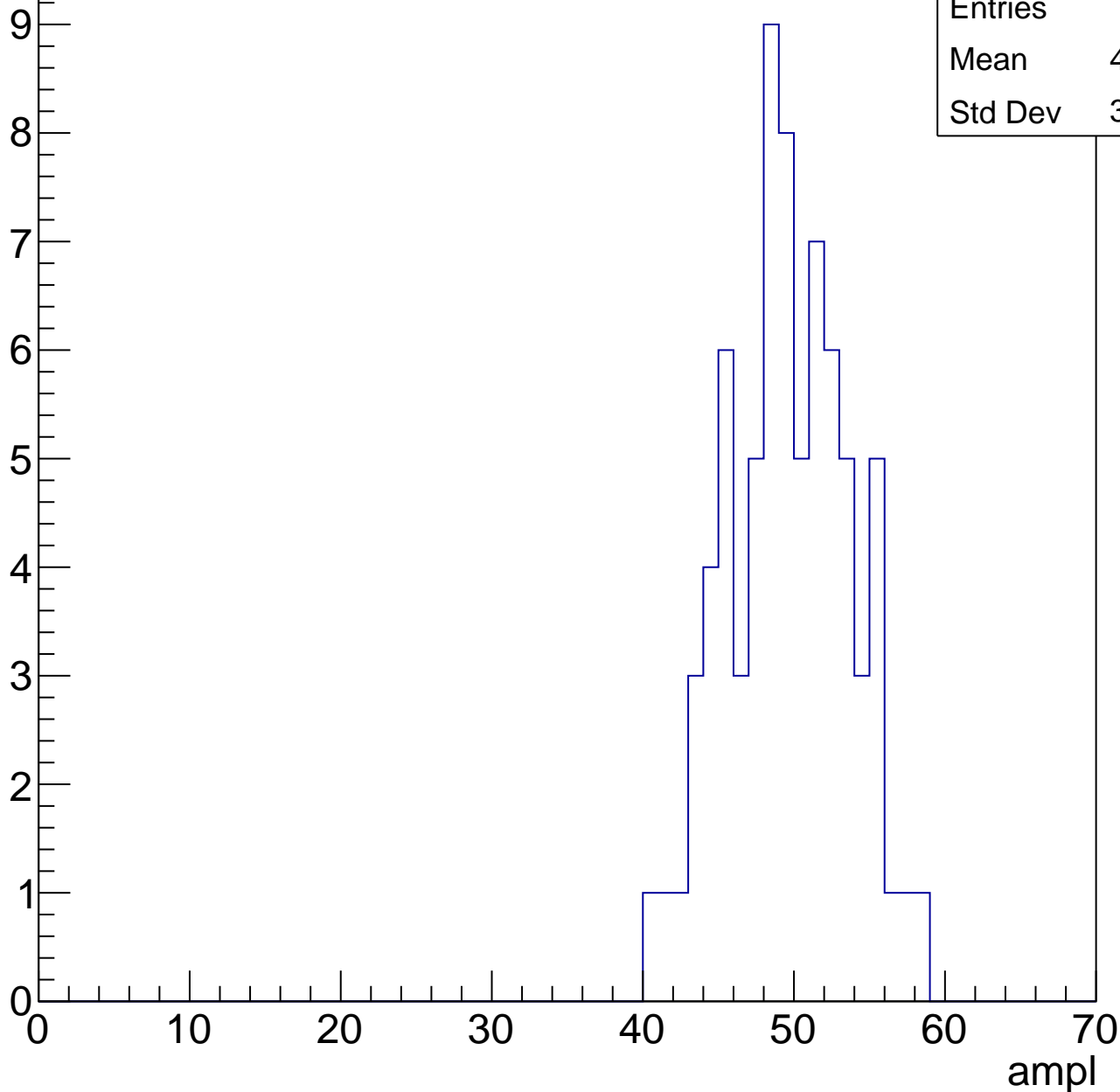
Gaus Width: 4.0325



B1L103S, U5-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

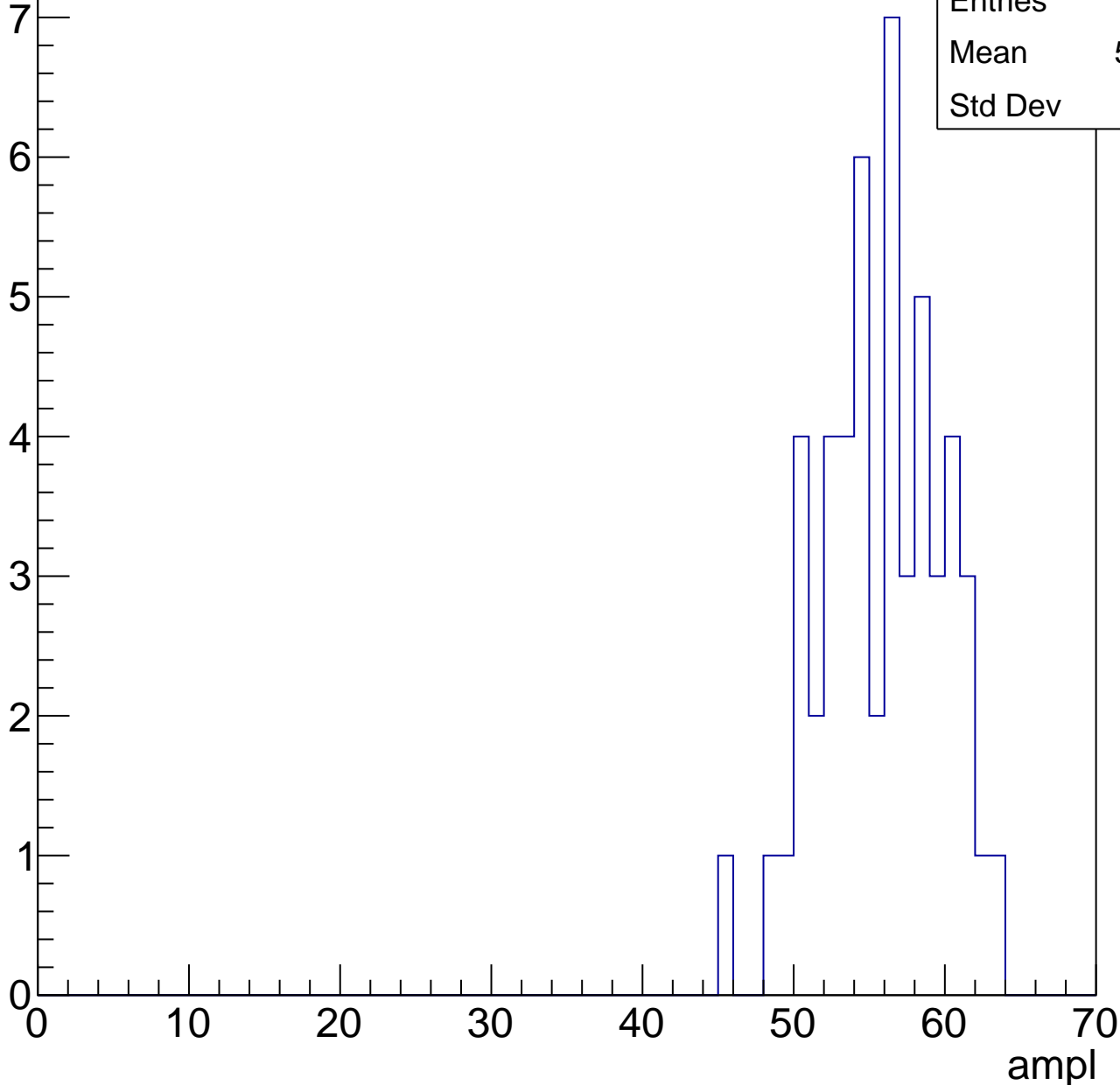


B1L103S, U5-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	55.31
Std Dev	3.93

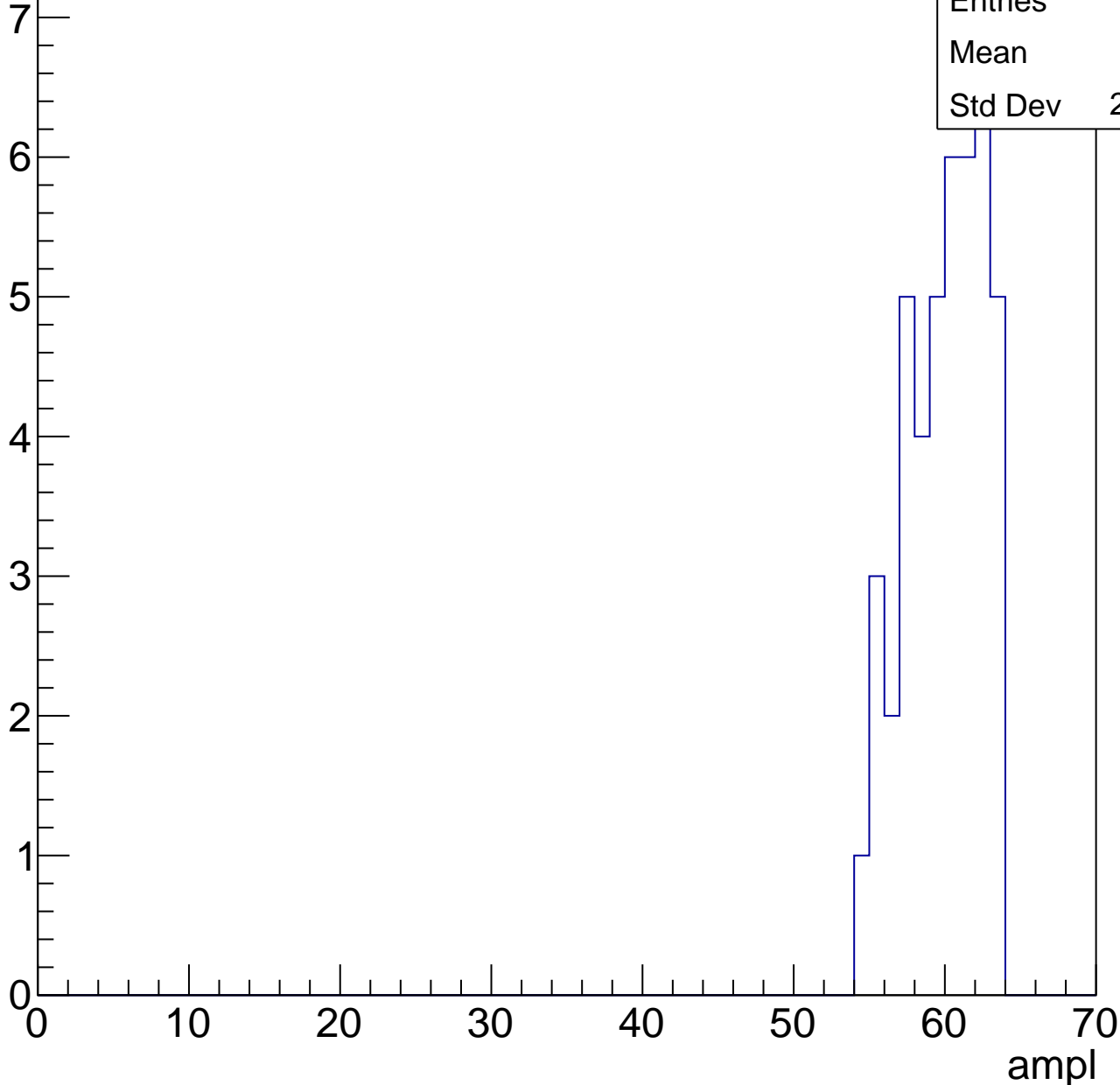


B1L103S, U5-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

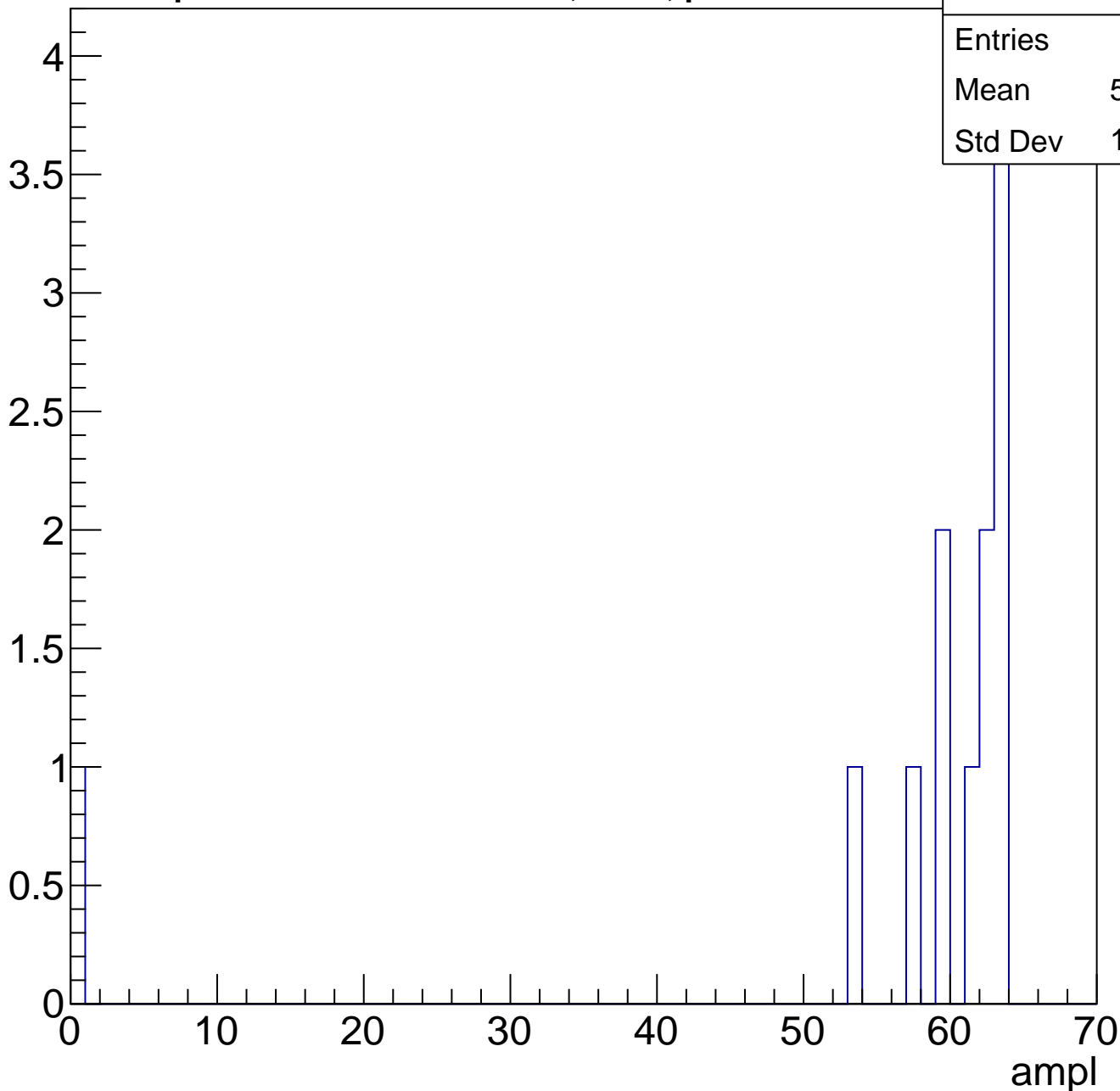
Entries	44
Mean	59.5
Std Dev	2.509



B1L103S, U5-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch27, adc0

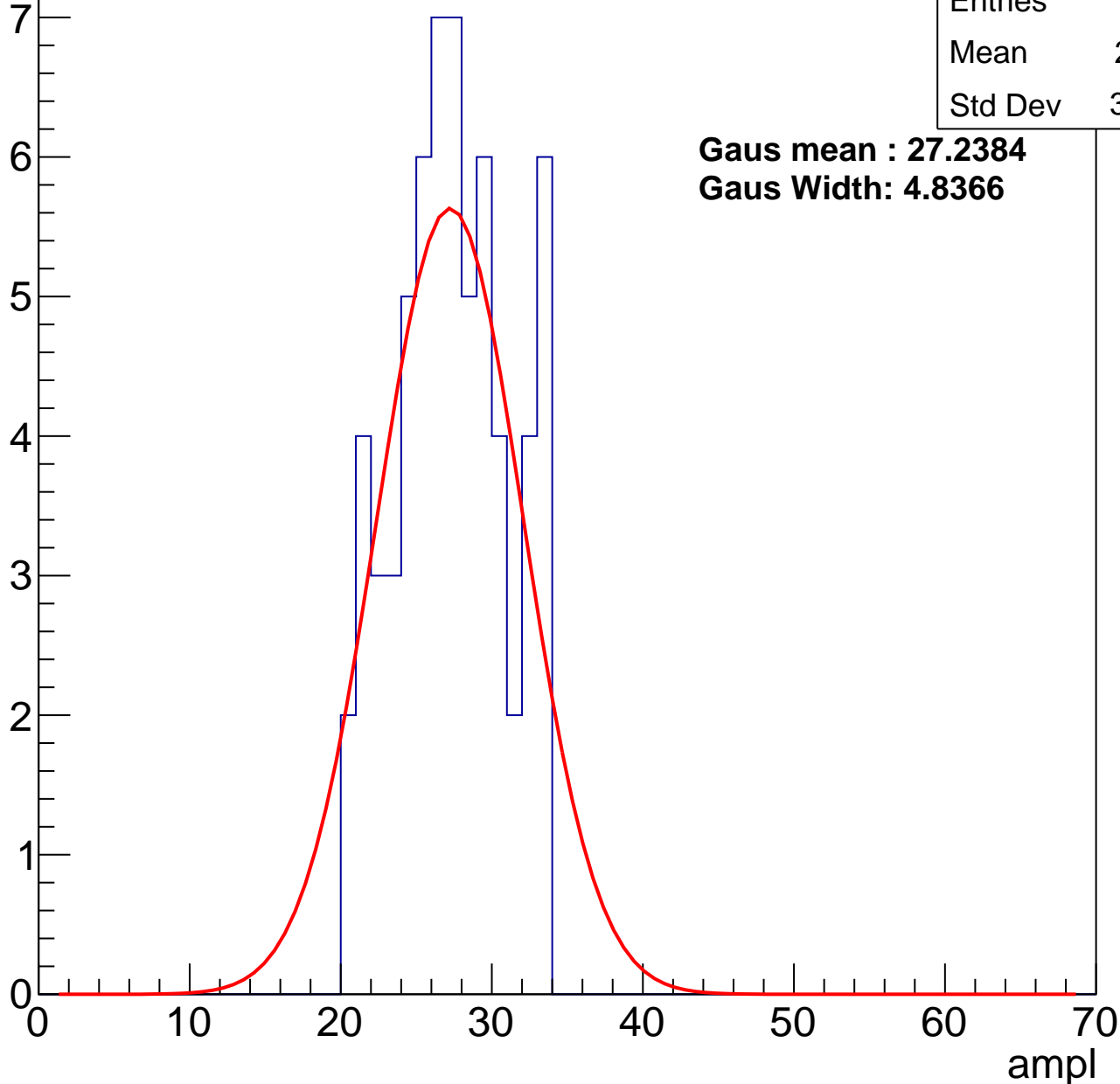
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	26.91
Std Dev	3.652

Gaus mean : 27.2384

Gaus Width: 4.8366



B1L103S, U5-ch27, adc1

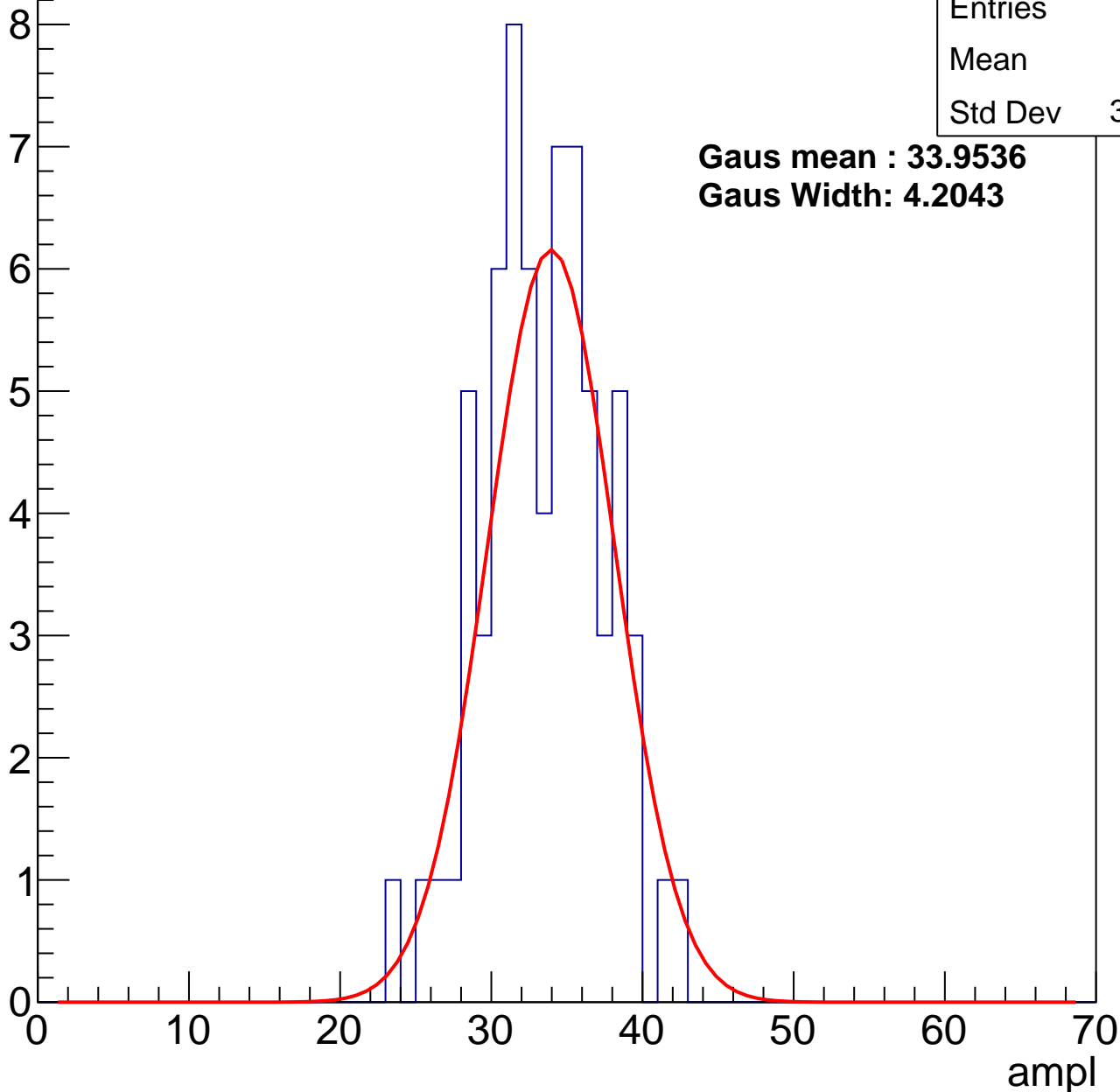
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	33
Std Dev	3.873

Gaus mean : 33.9536

Gaus Width: 4.2043



B1L103S, U5-ch27, adc2

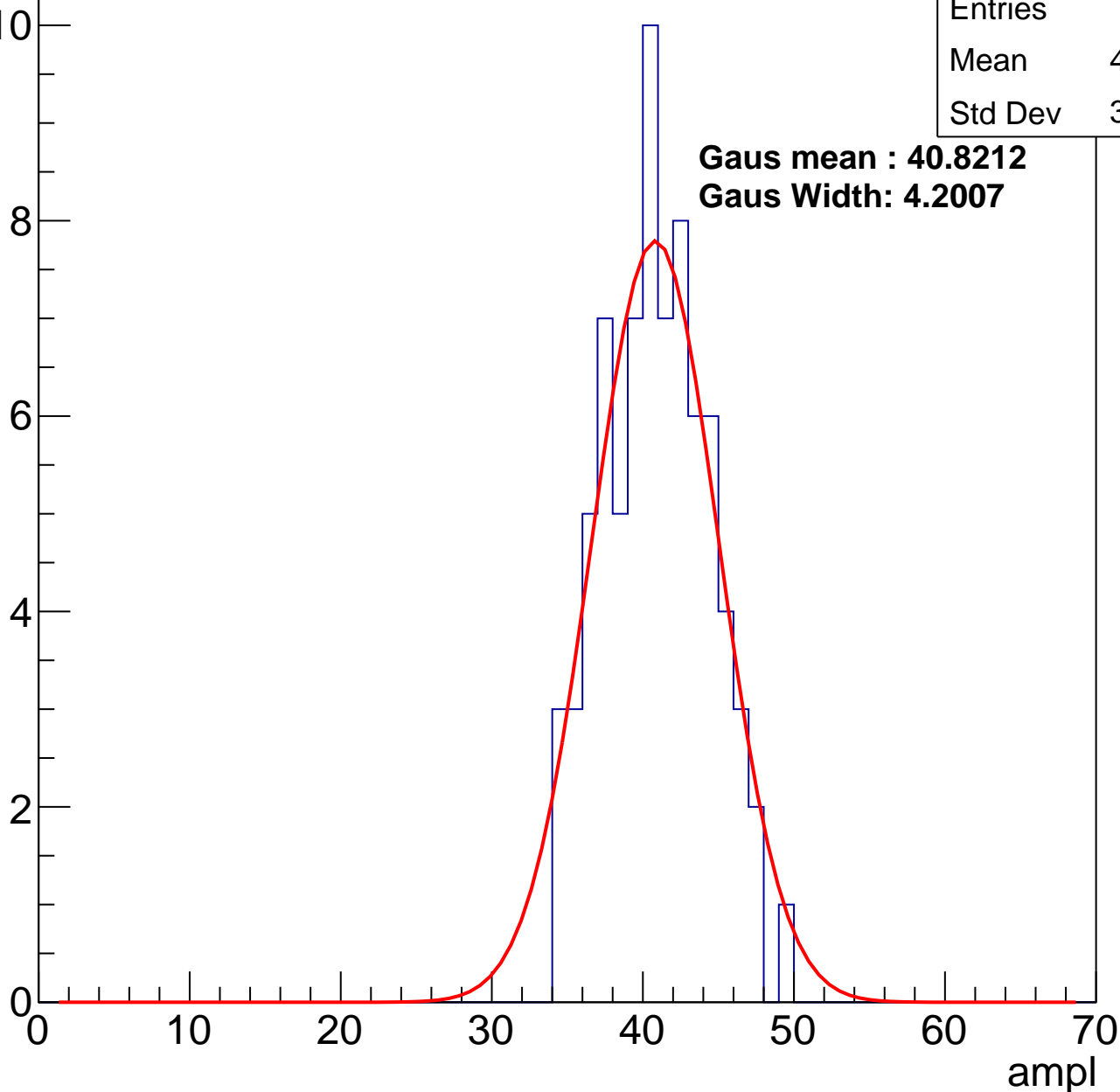
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	40.45
Std Dev	3.444

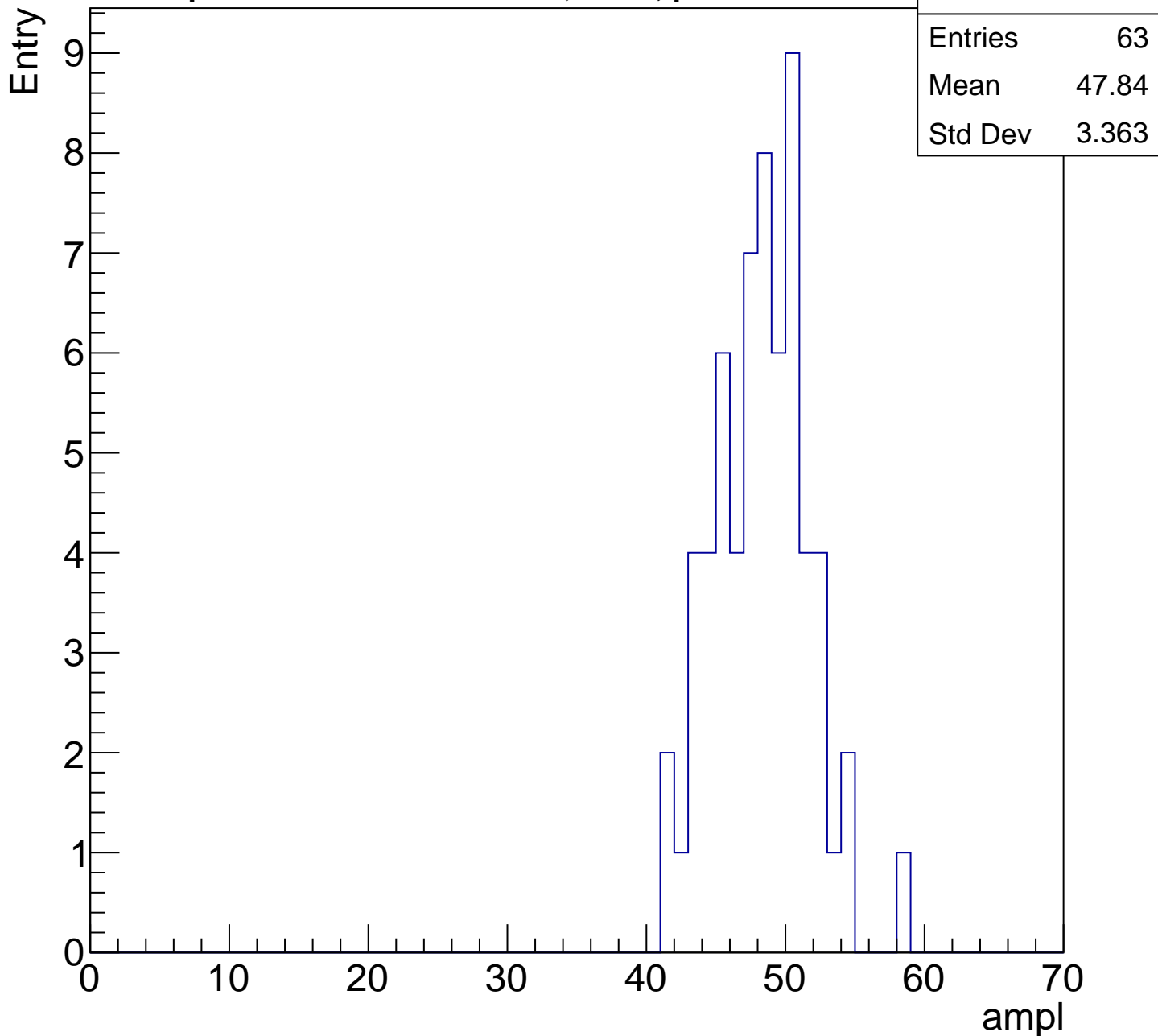
Gaus mean : 40.8212

Gaus Width: 4.2007



B1L103S, U5-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

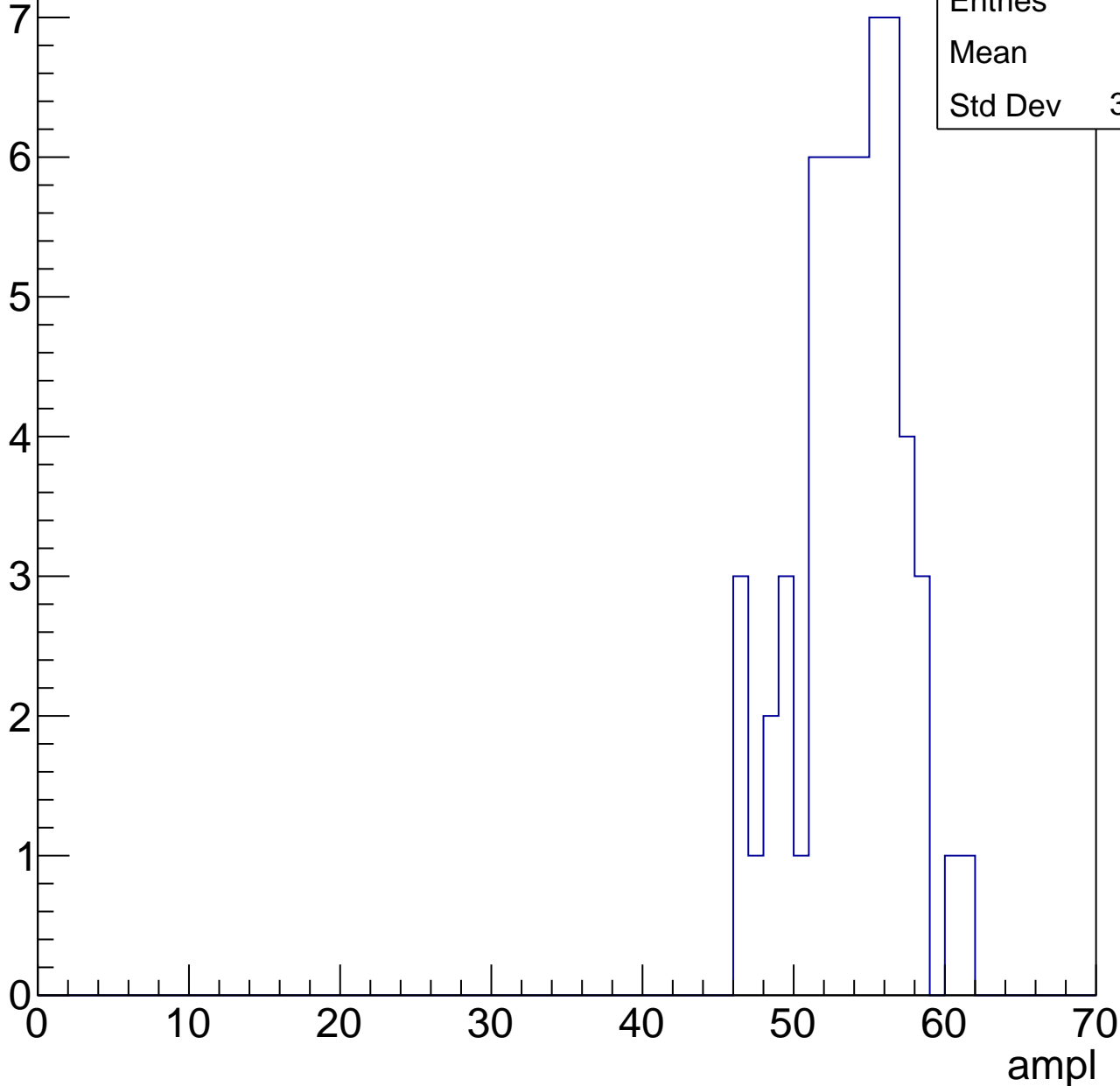


B1L103S, U5-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

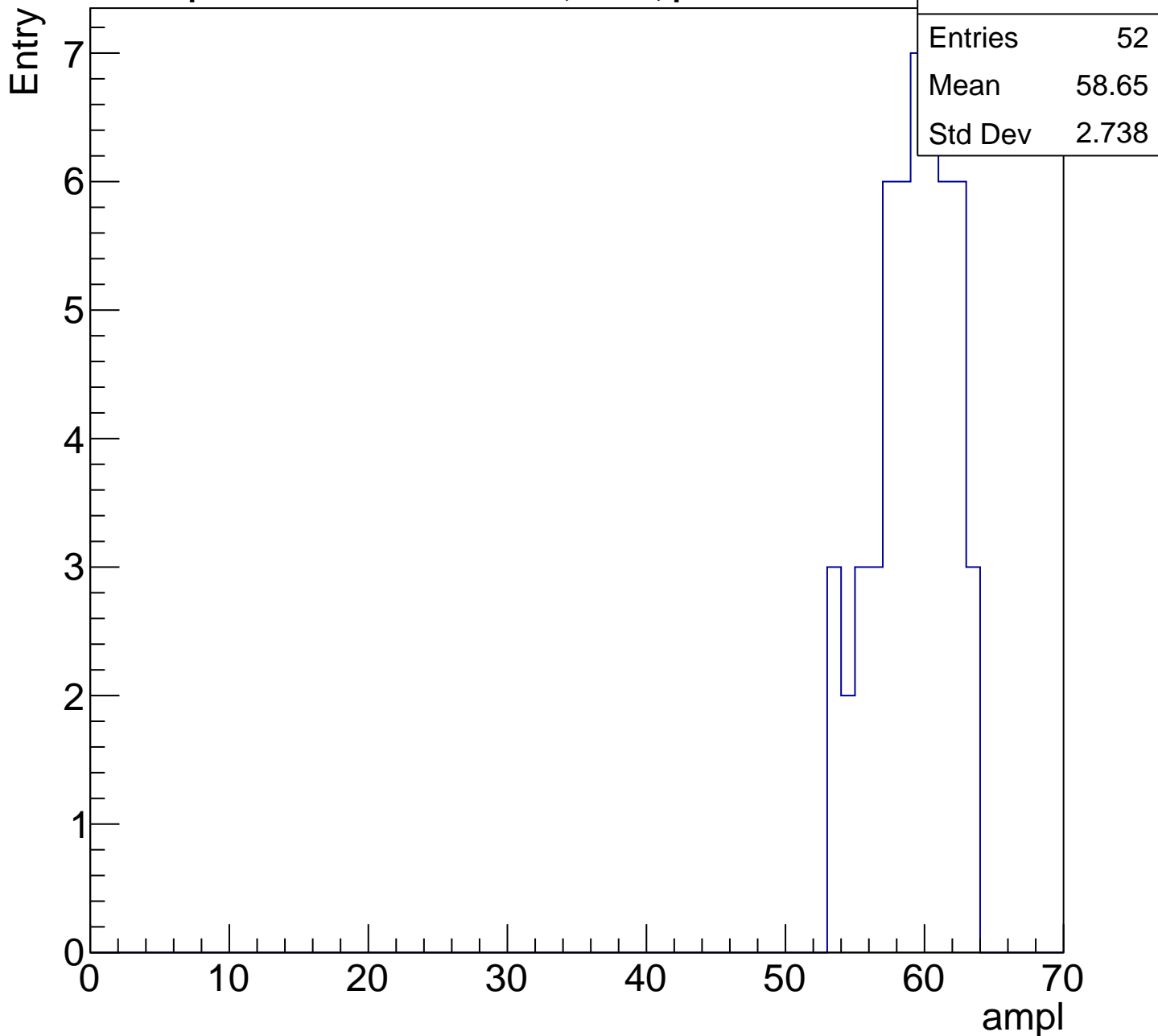
Entry

Entries	57
Mean	53.3
Std Dev	3.413



B1L103S, U5-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

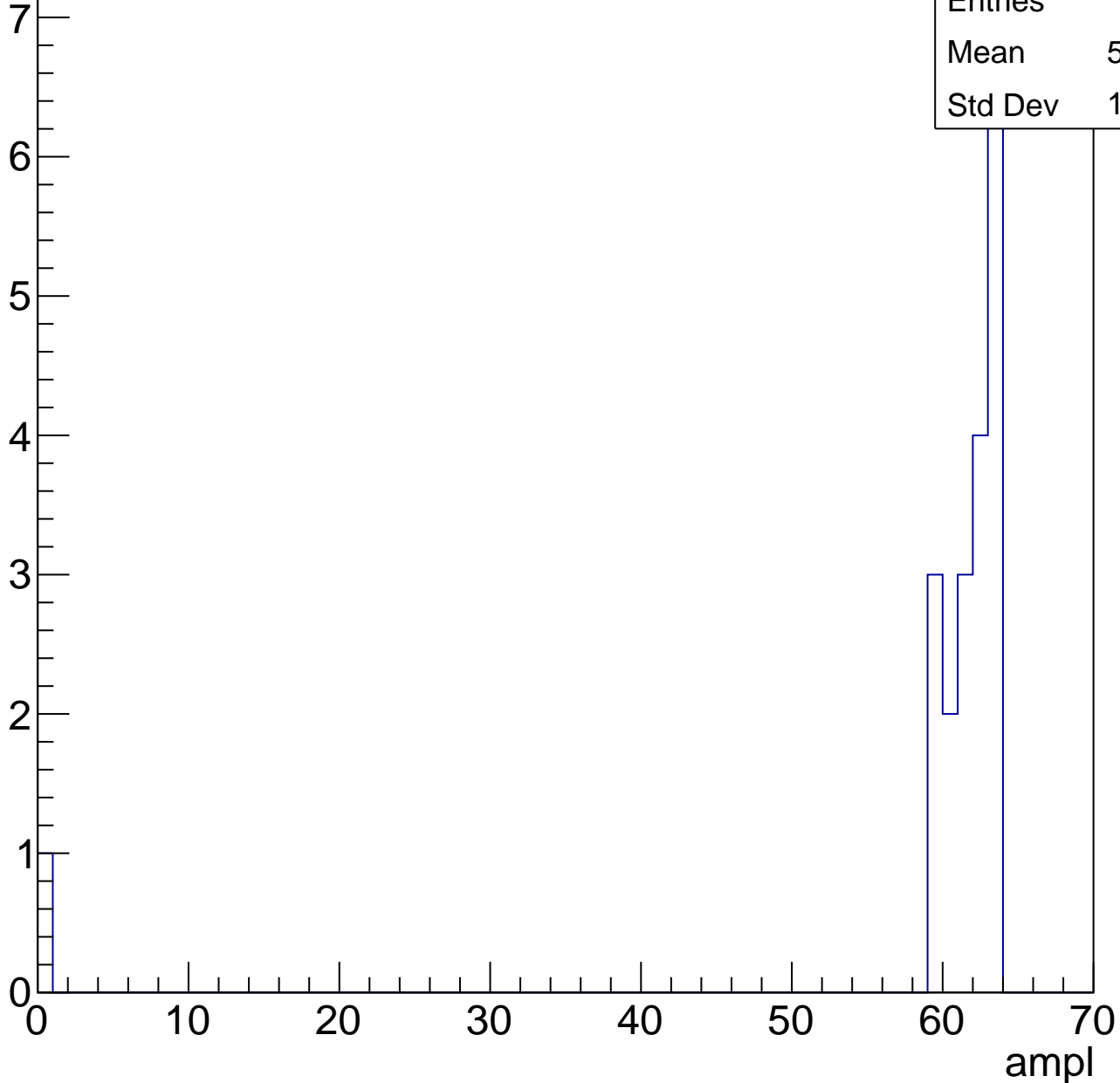


B1L103S, U5-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	20
Mean	58.45
Std Dev	13.49



B1L103S, U5-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch28, adc0

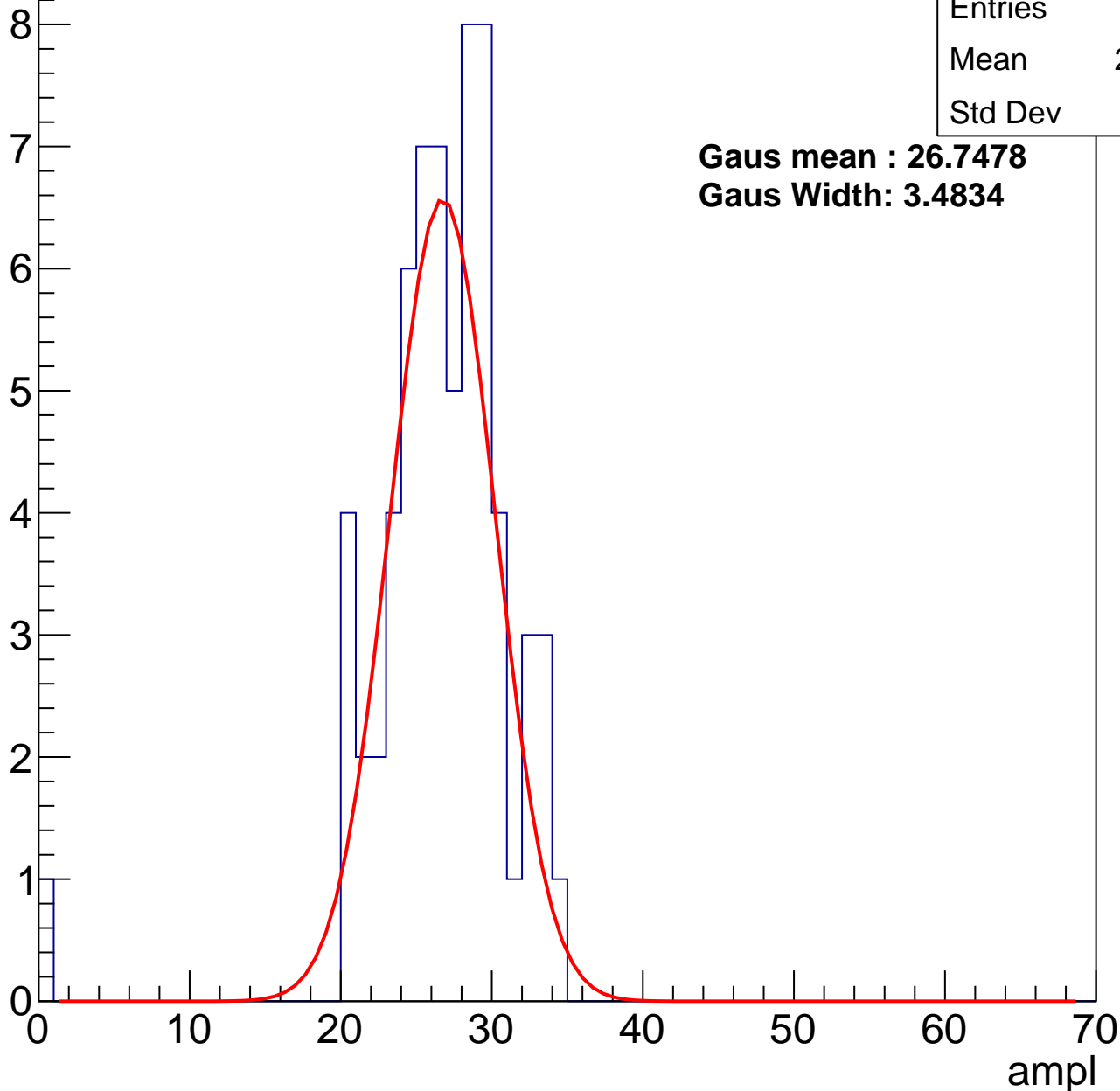
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	26.21
Std Dev	4.74

Gaus mean : 26.7478

Gaus Width: 3.4834



B1L103S, U5-ch28, adc1

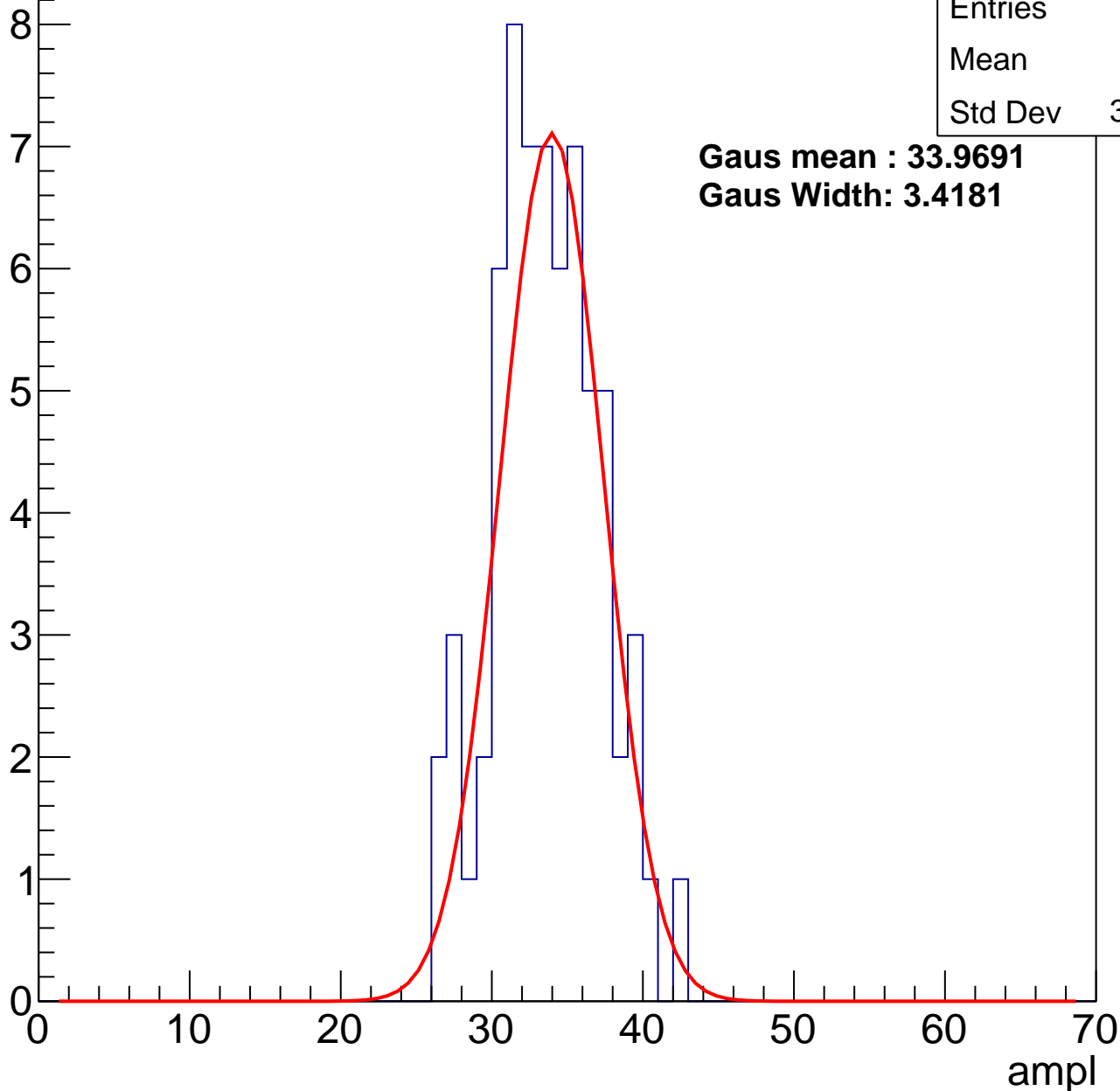
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	33.2
Std Dev	3.483

Gaus mean : 33.9691

Gaus Width: 3.4181



B1L103S, U5-ch28, adc2

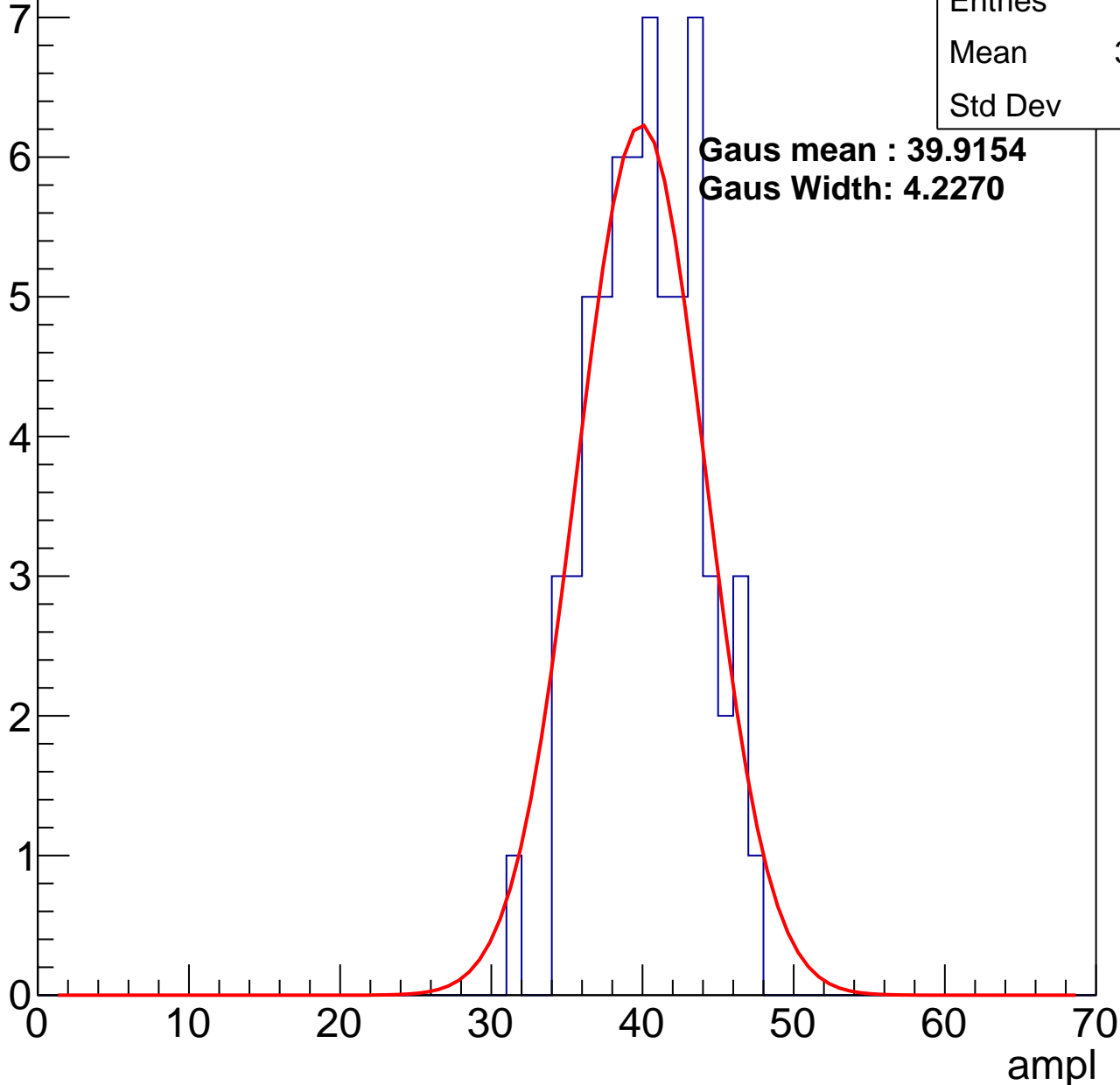
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	39.81
Std Dev	3.5

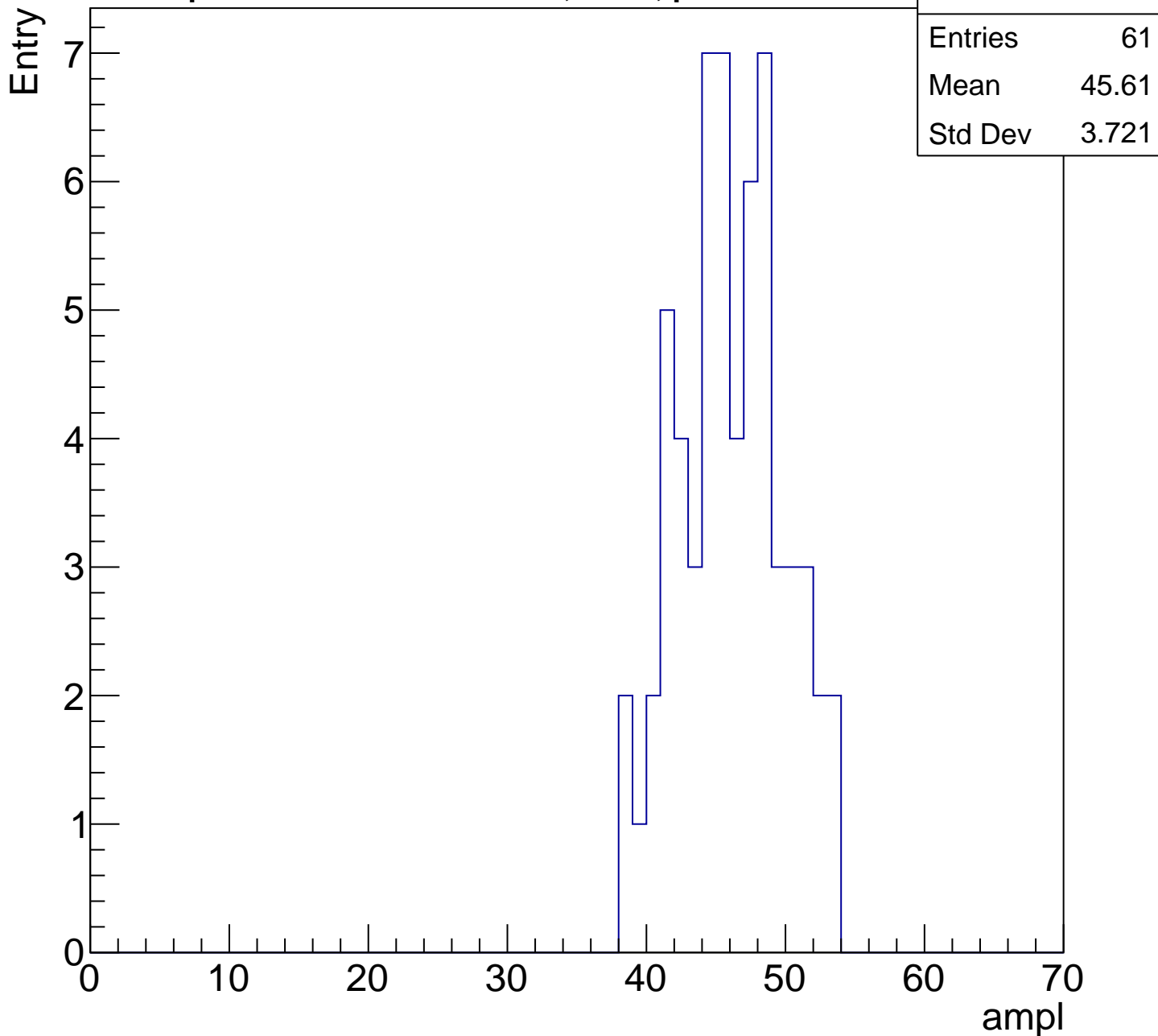
Gaus mean : 39.9154

Gaus Width: 4.2270



B1L103S, U5-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

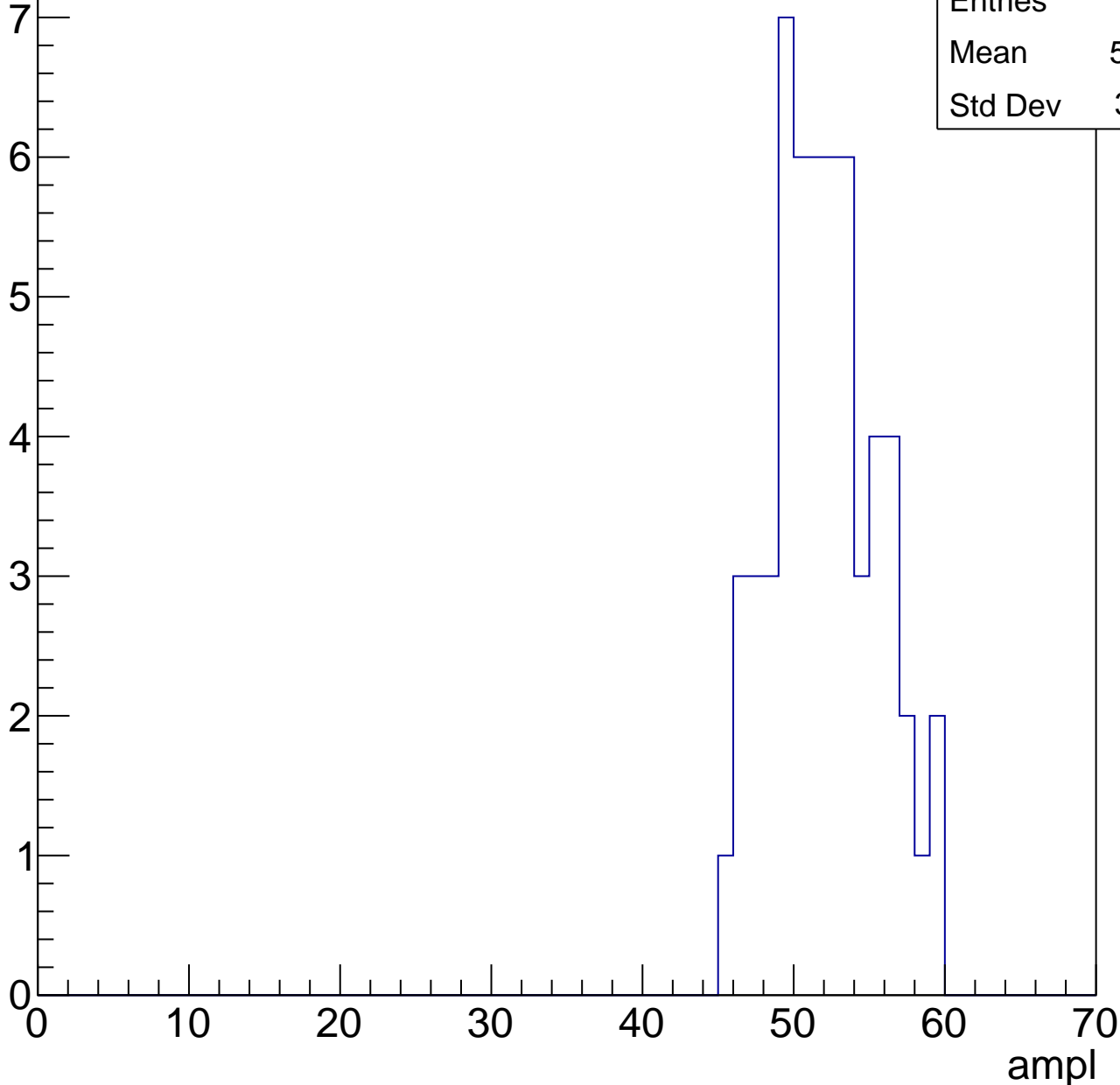


B1L103S, U5-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	51.63
Std Dev	3.421

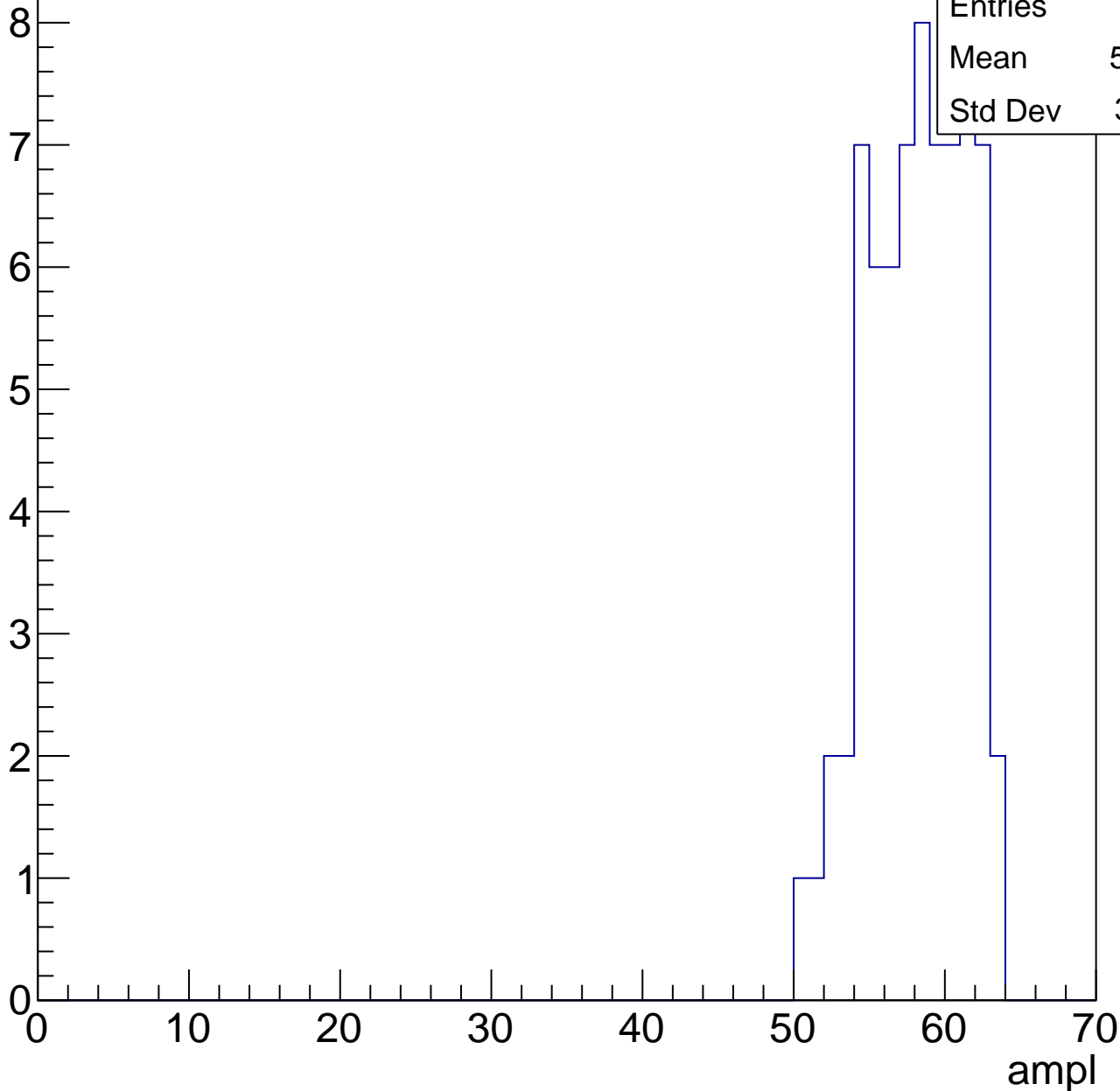


B1L103S, U5-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	57.73
Std Dev	3.131

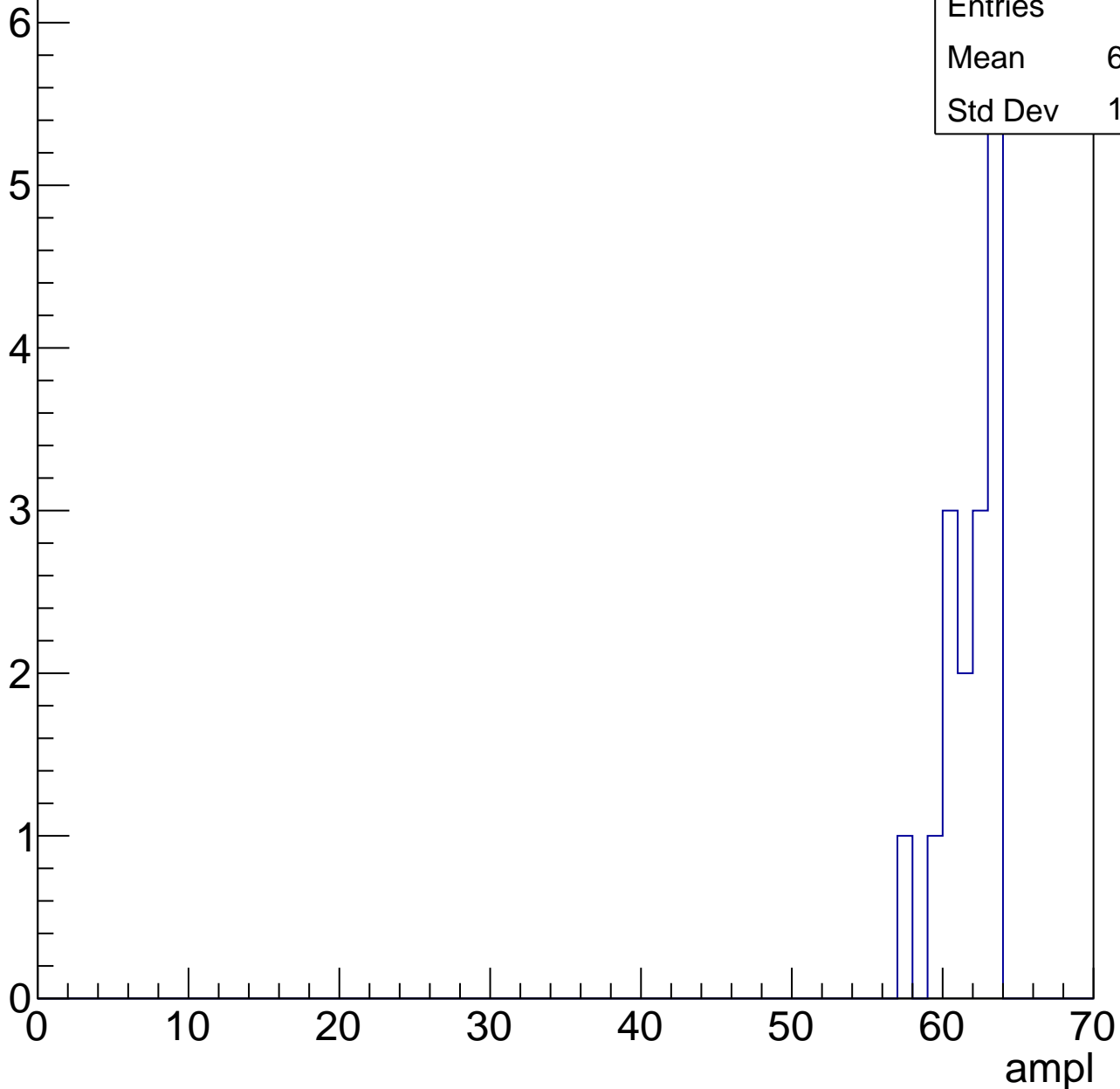


B1L103S, U5-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

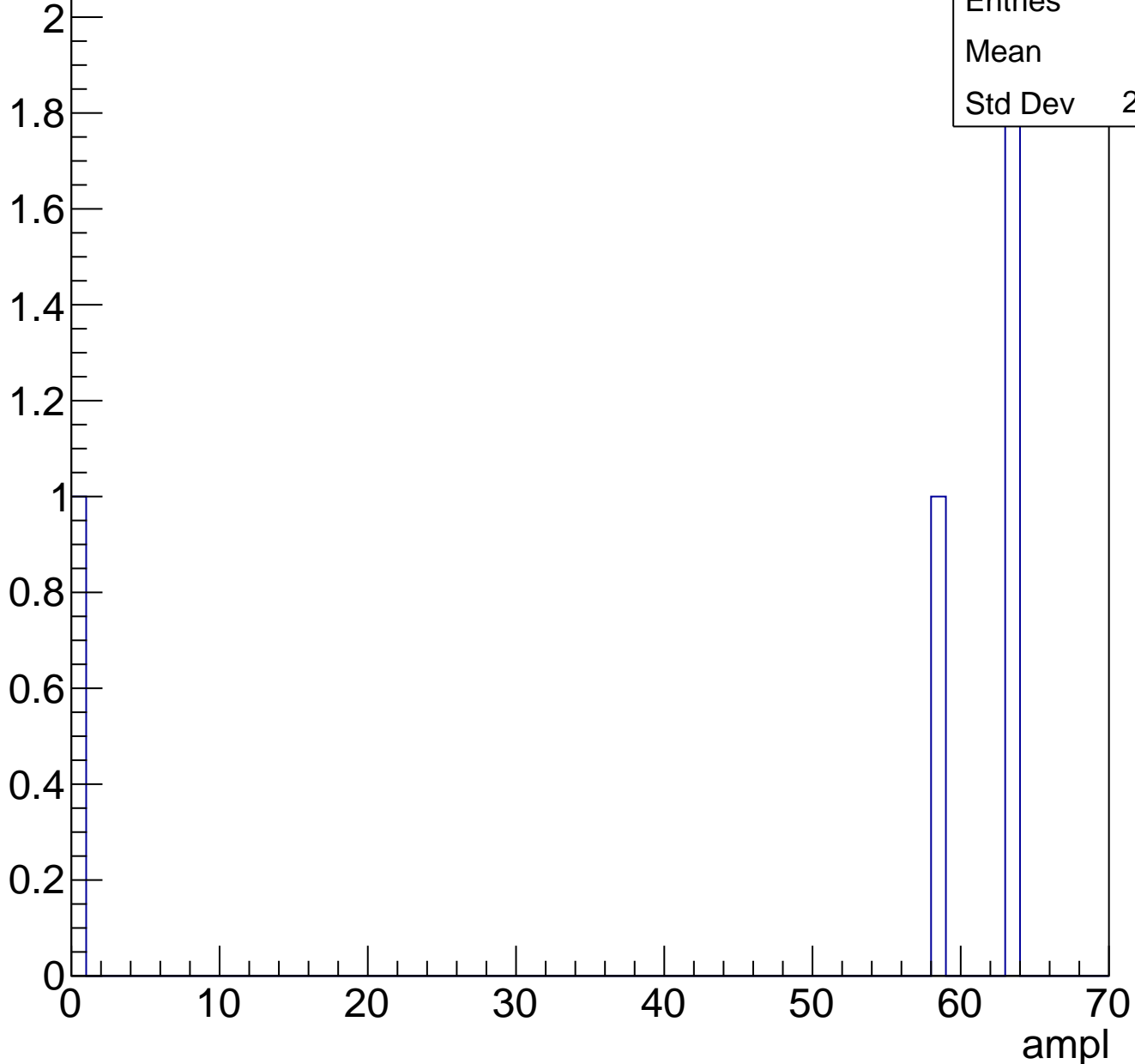
Entries	16
Mean	61.38
Std Dev	1.728



B1L103S, U5-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch29, adc0

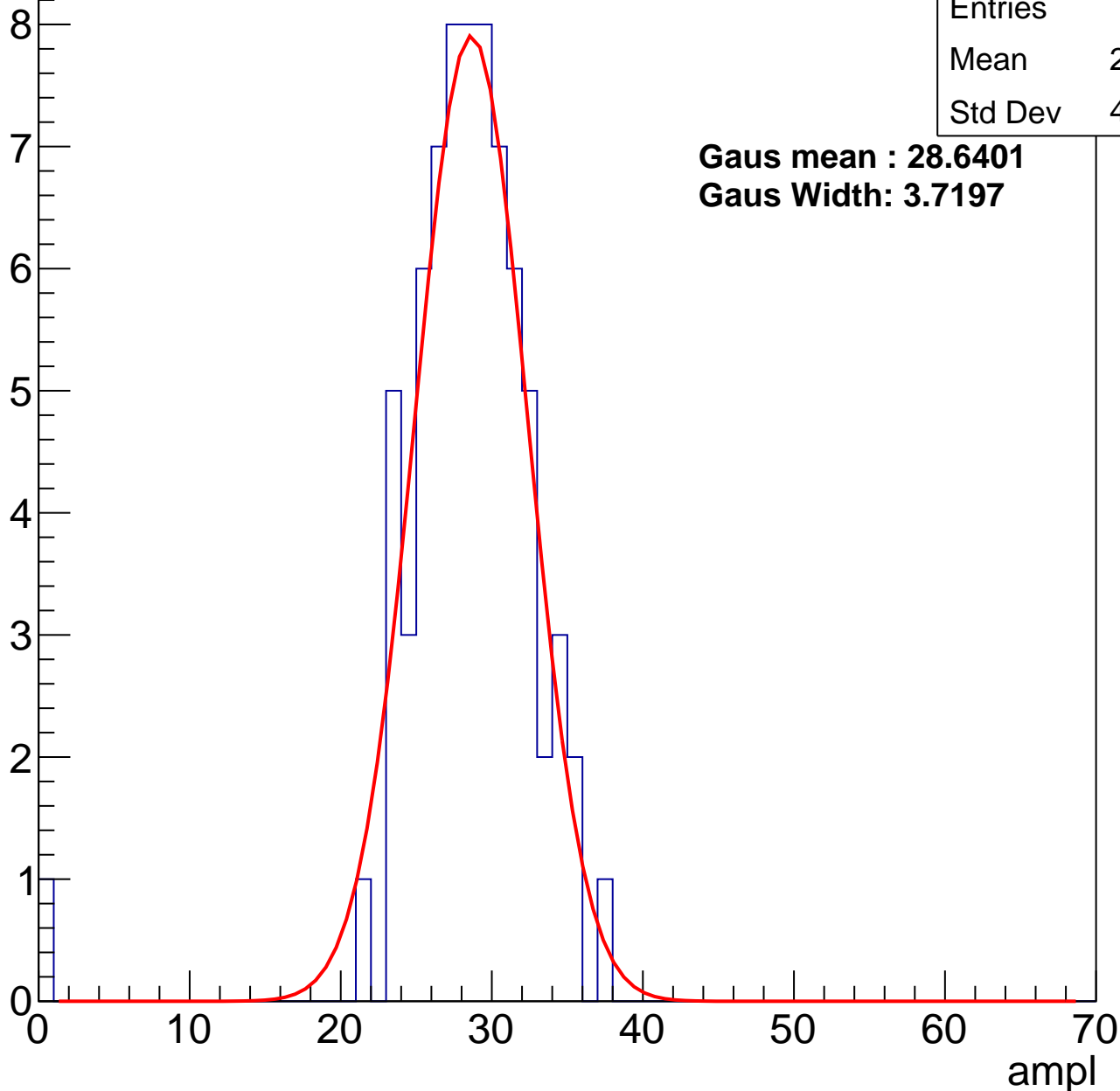
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	27.99
Std Dev	4.686

Gaus mean : 28.6401

Gaus Width: 3.7197



B1L103S, U5-ch29, adc1

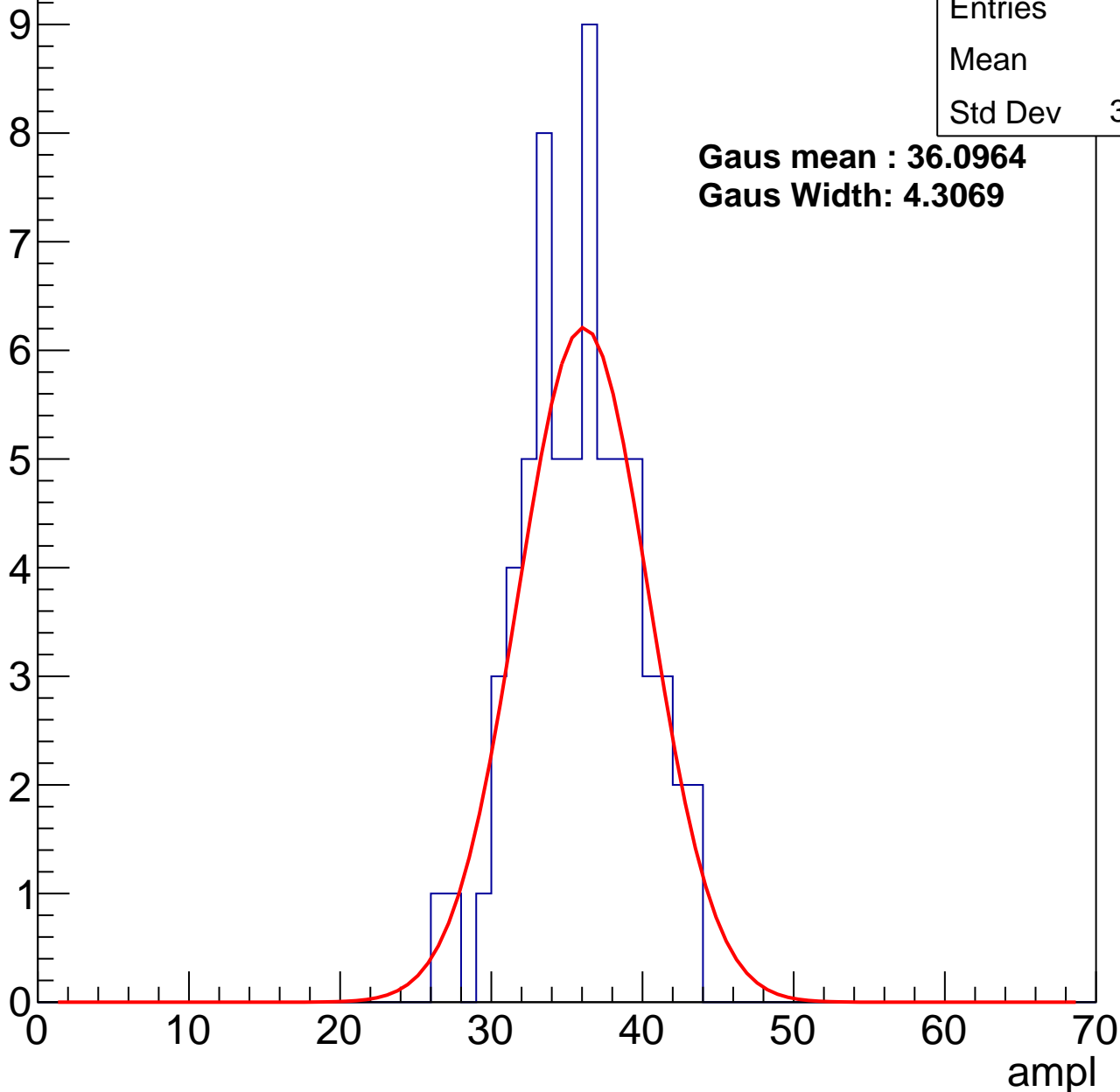
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.4
Std Dev	3.774

Gaus mean : 36.0964

Gaus Width: 4.3069



B1L103S, U5-ch29, adc2

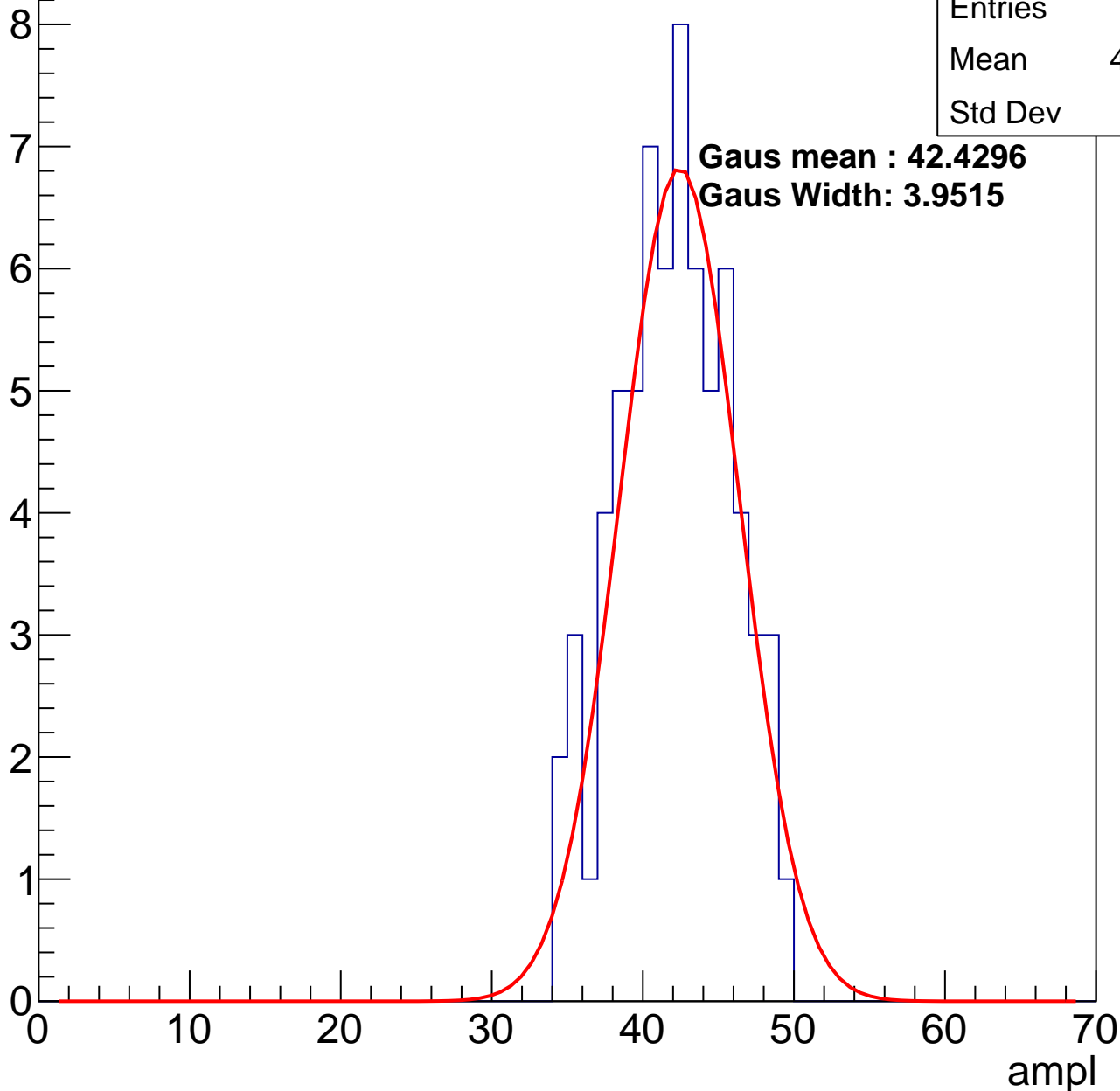
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	41.59
Std Dev	3.68

Gaus mean : 42.4296

Gaus Width: 3.9515

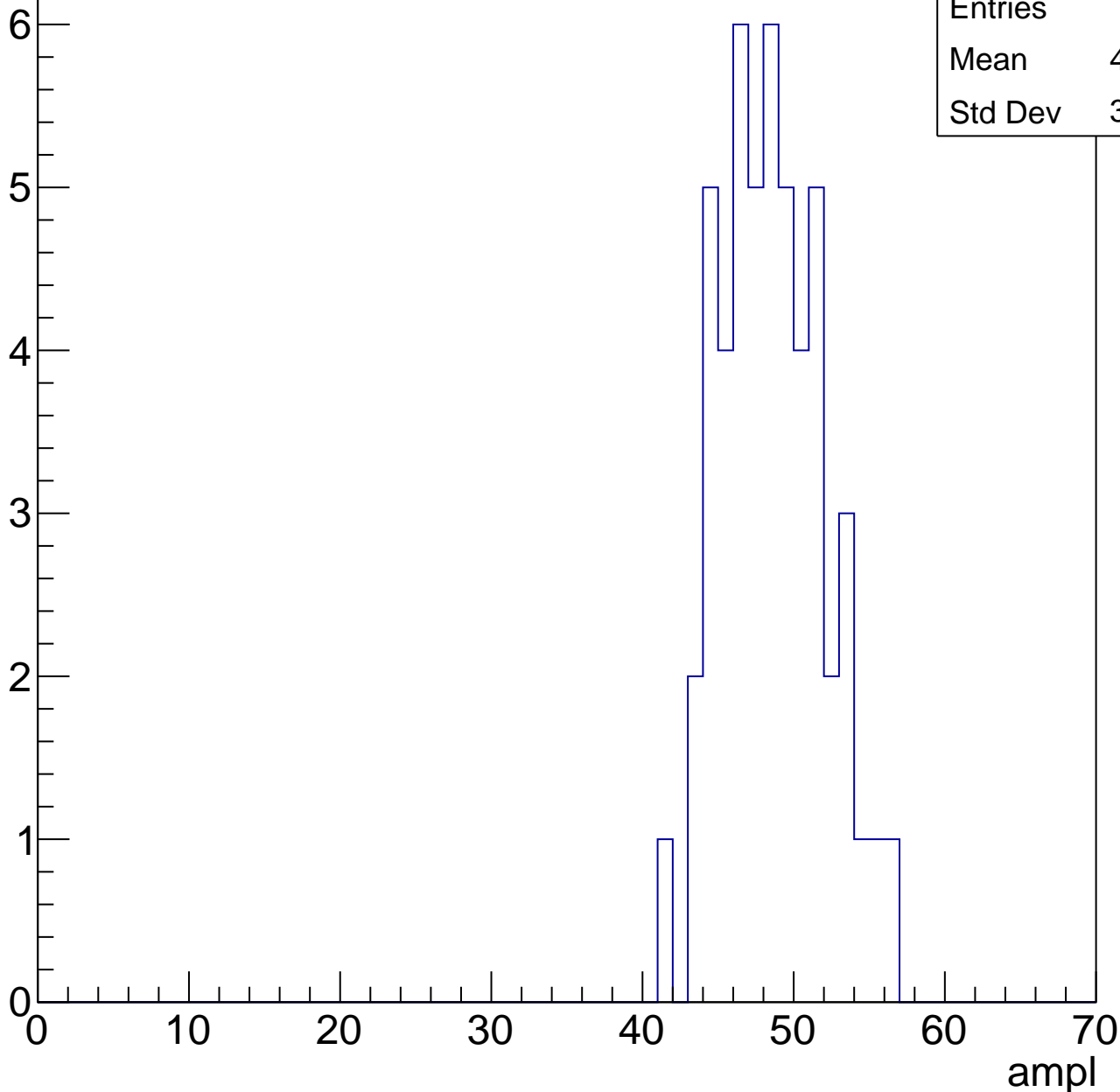


B1L103S, U5-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	48.12
Std Dev	3.329

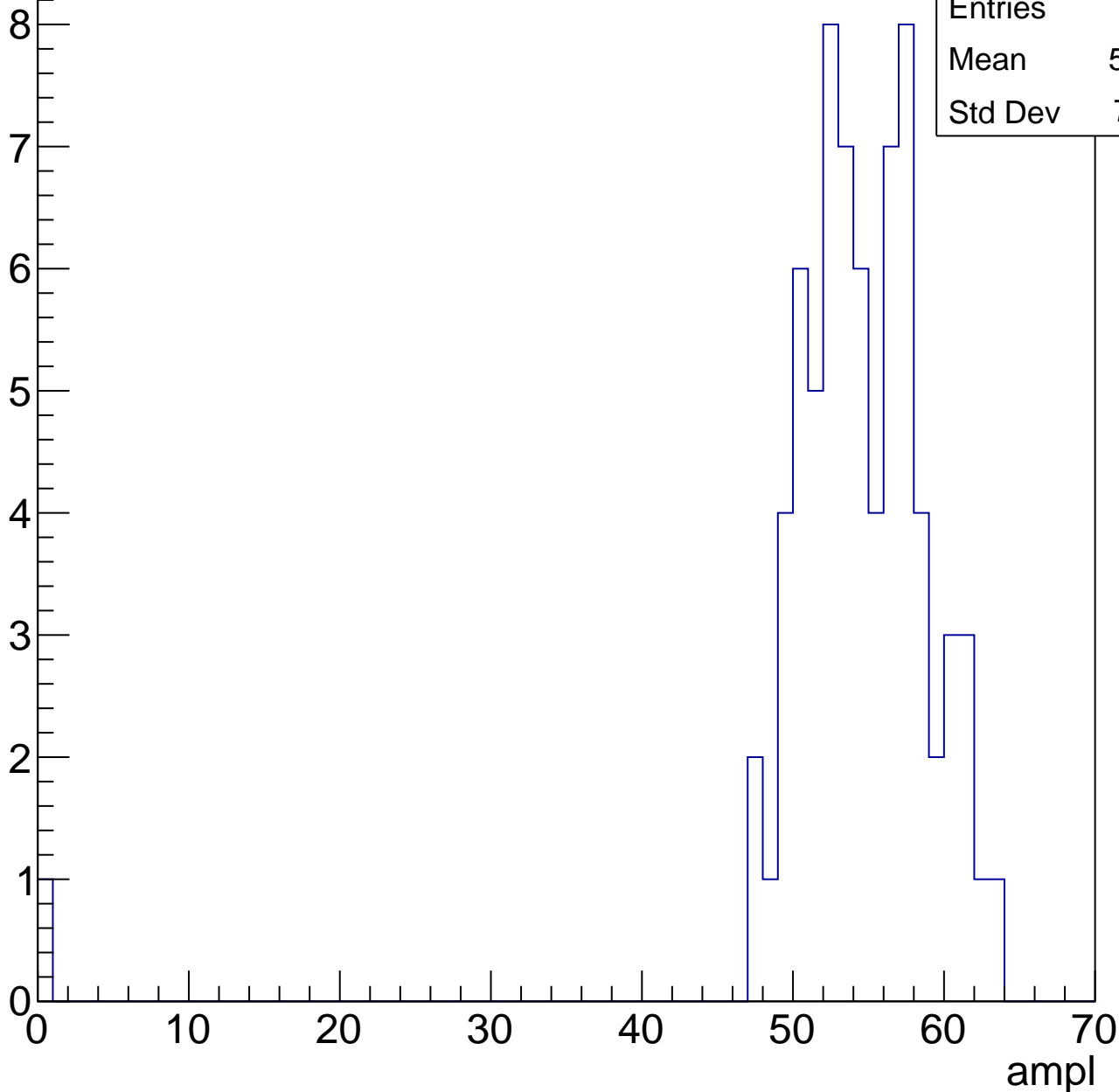


B1L103S, U5-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	53.56
Std Dev	7.341



B1L103S, U5-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

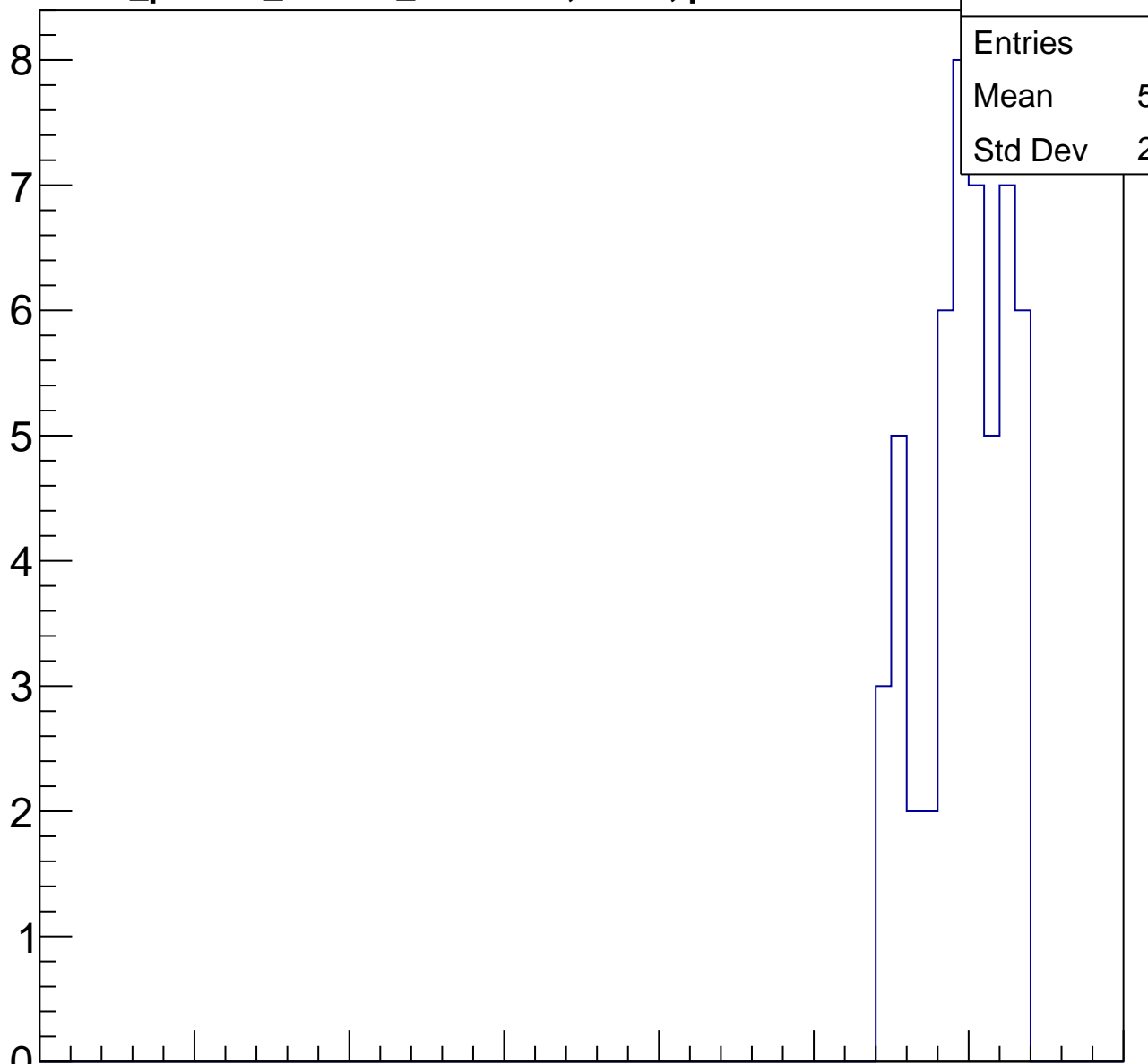
Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.22
Std Dev	2.696

ampl

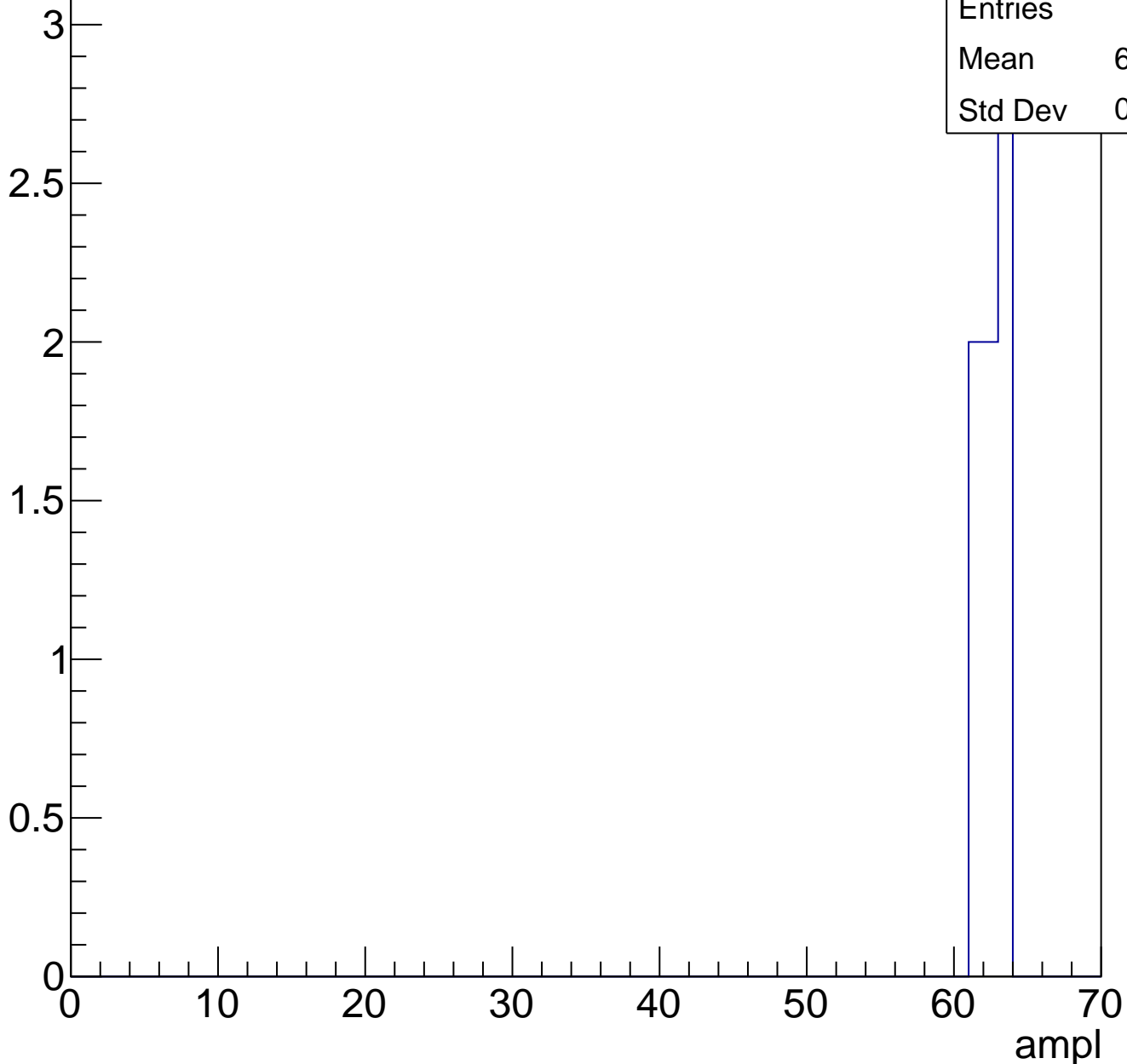
0 10 20 30 40 50 60 70



B1L103S, U5-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch30, adc0

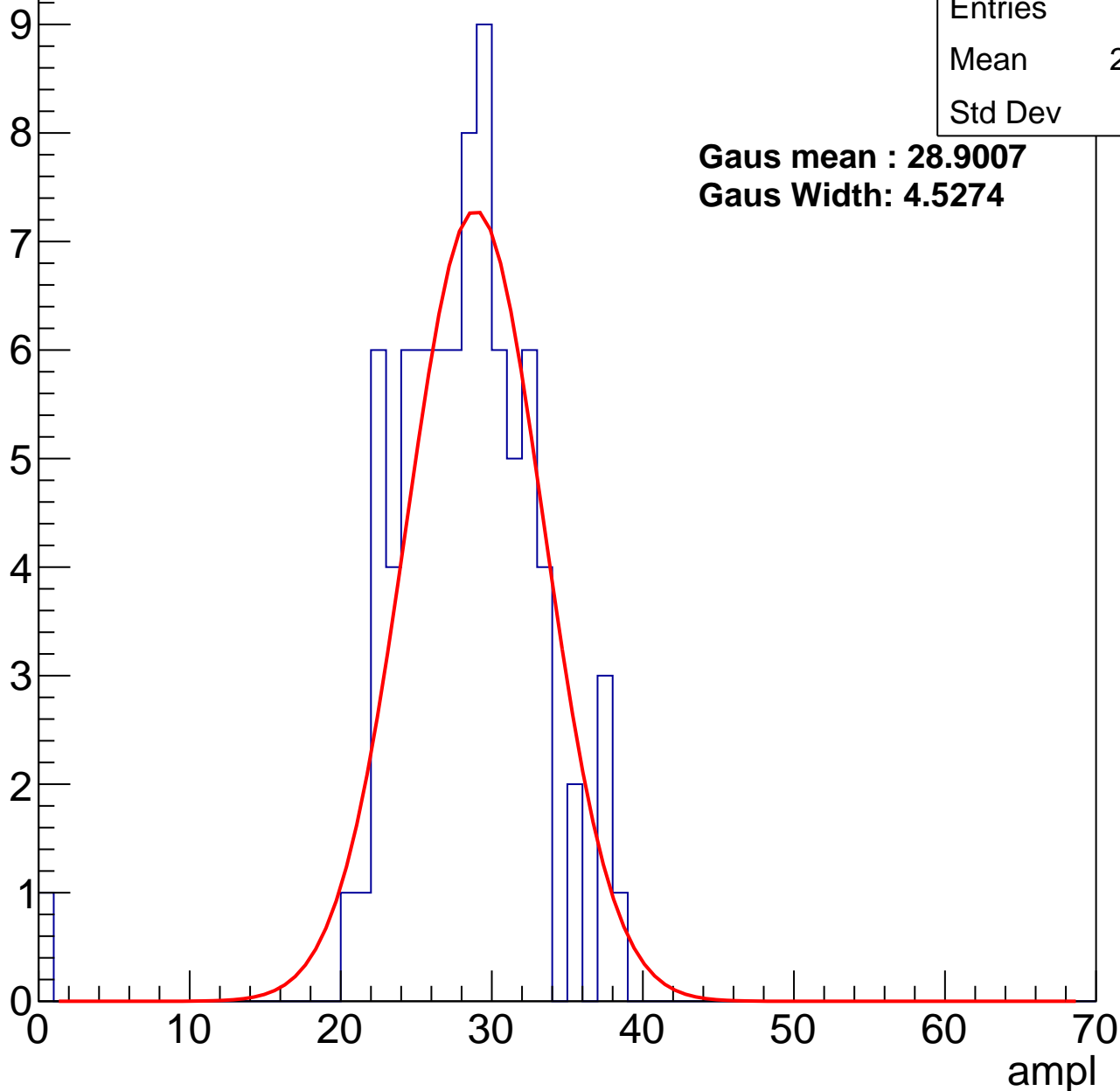
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	27.65
Std Dev	5.09

Gaus mean : 28.9007

Gaus Width: 4.5274



B1L103S, U5-ch30, adc1

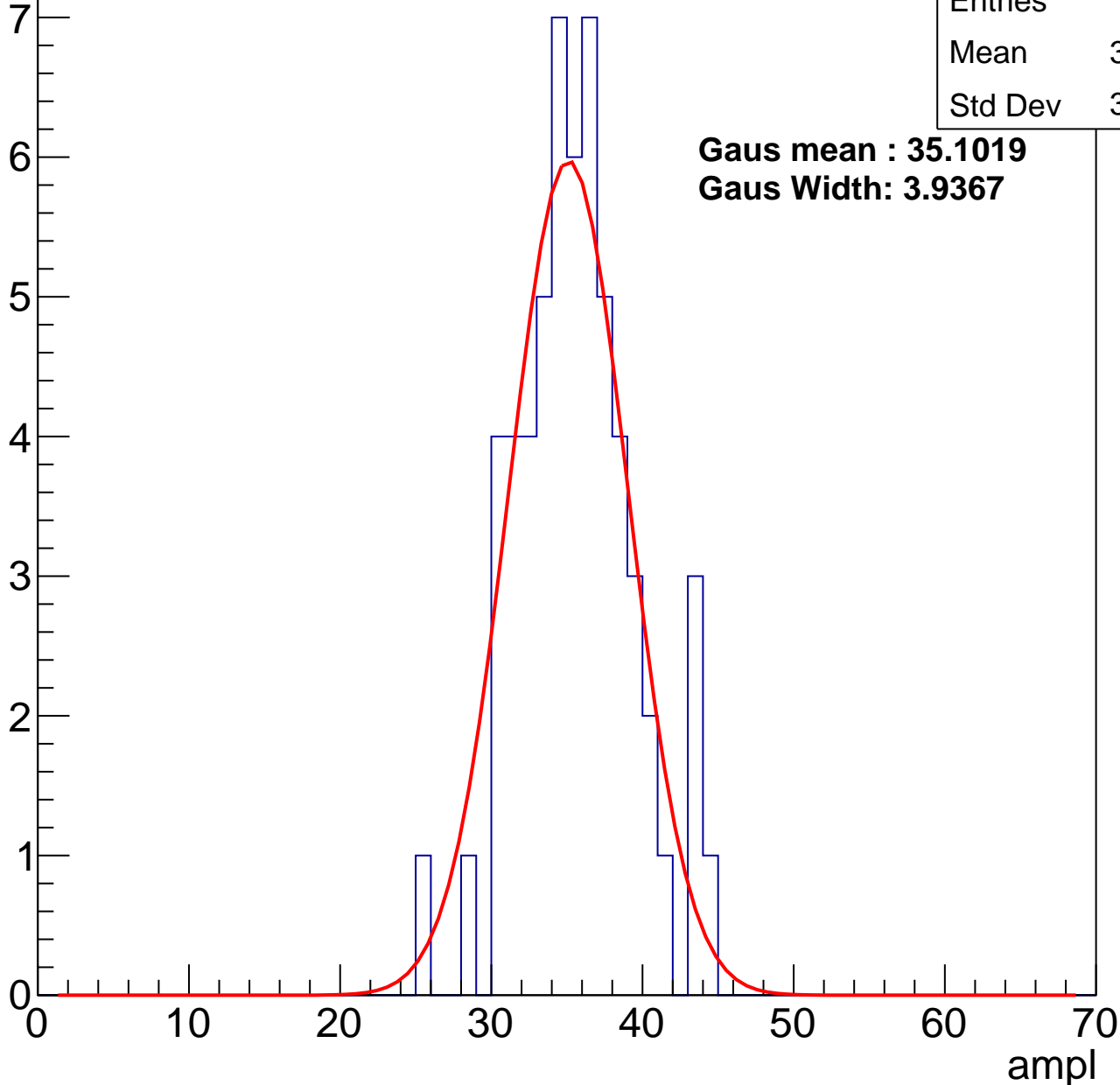
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.14
Std Dev	3.817

Gaus mean : 35.1019

Gaus Width: 3.9367



B1L103S, U5-ch30, adc2

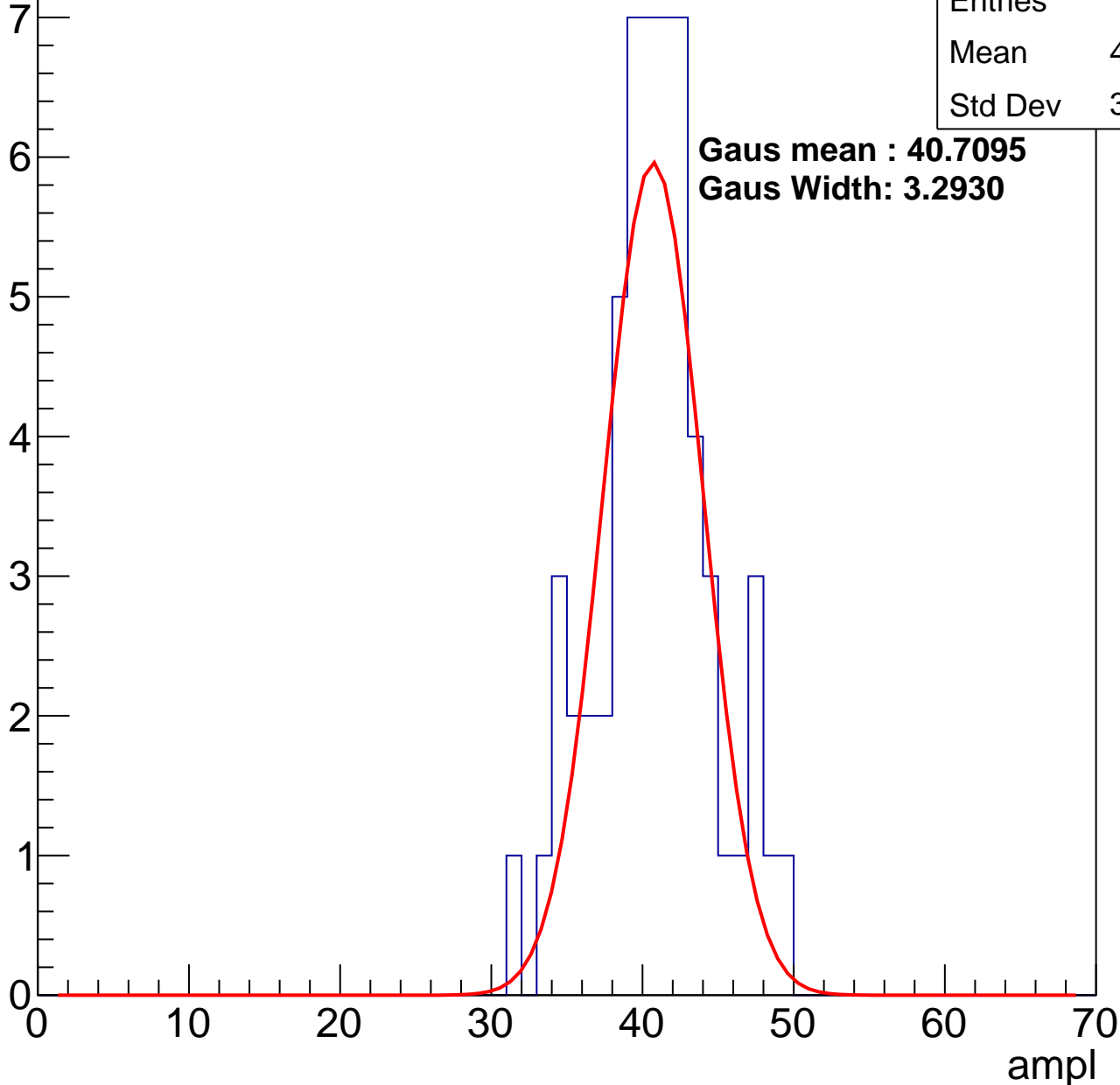
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	40.33
Std Dev	3.785

Gaus mean : 40.7095

Gaus Width: 3.2930

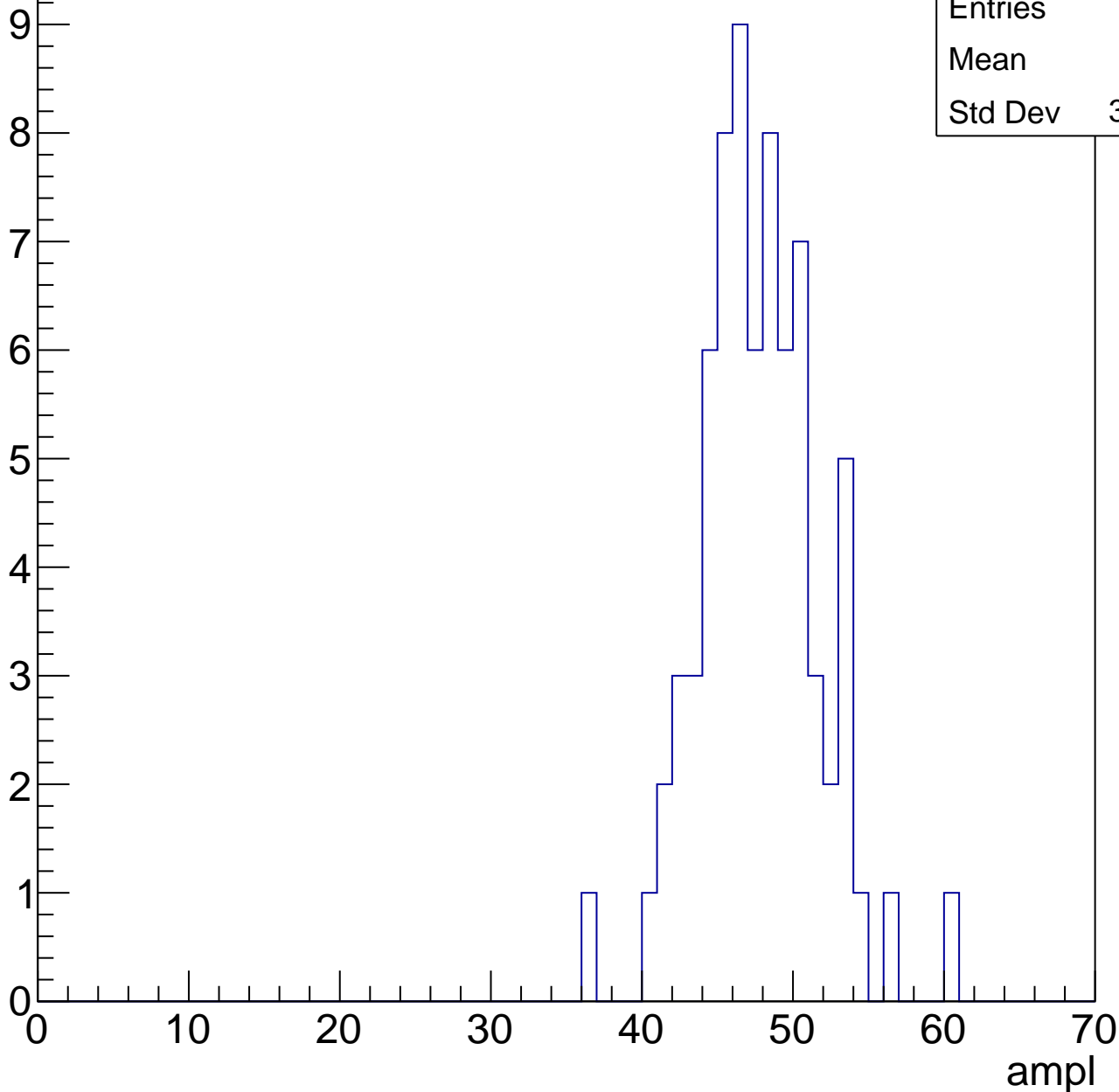


B1L103S, U5-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	47.3
Std Dev	3.926

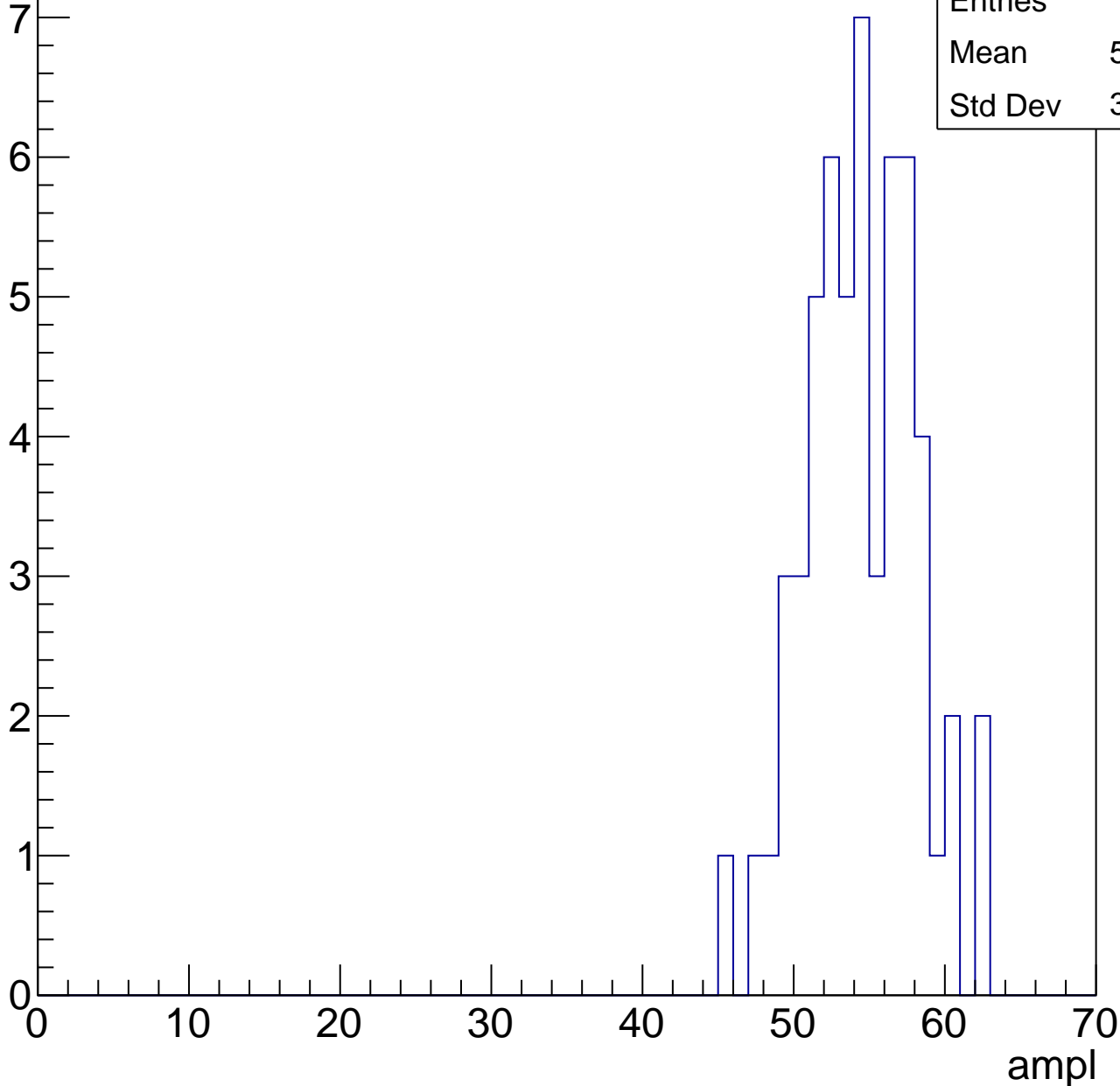


B1L103S, U5-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

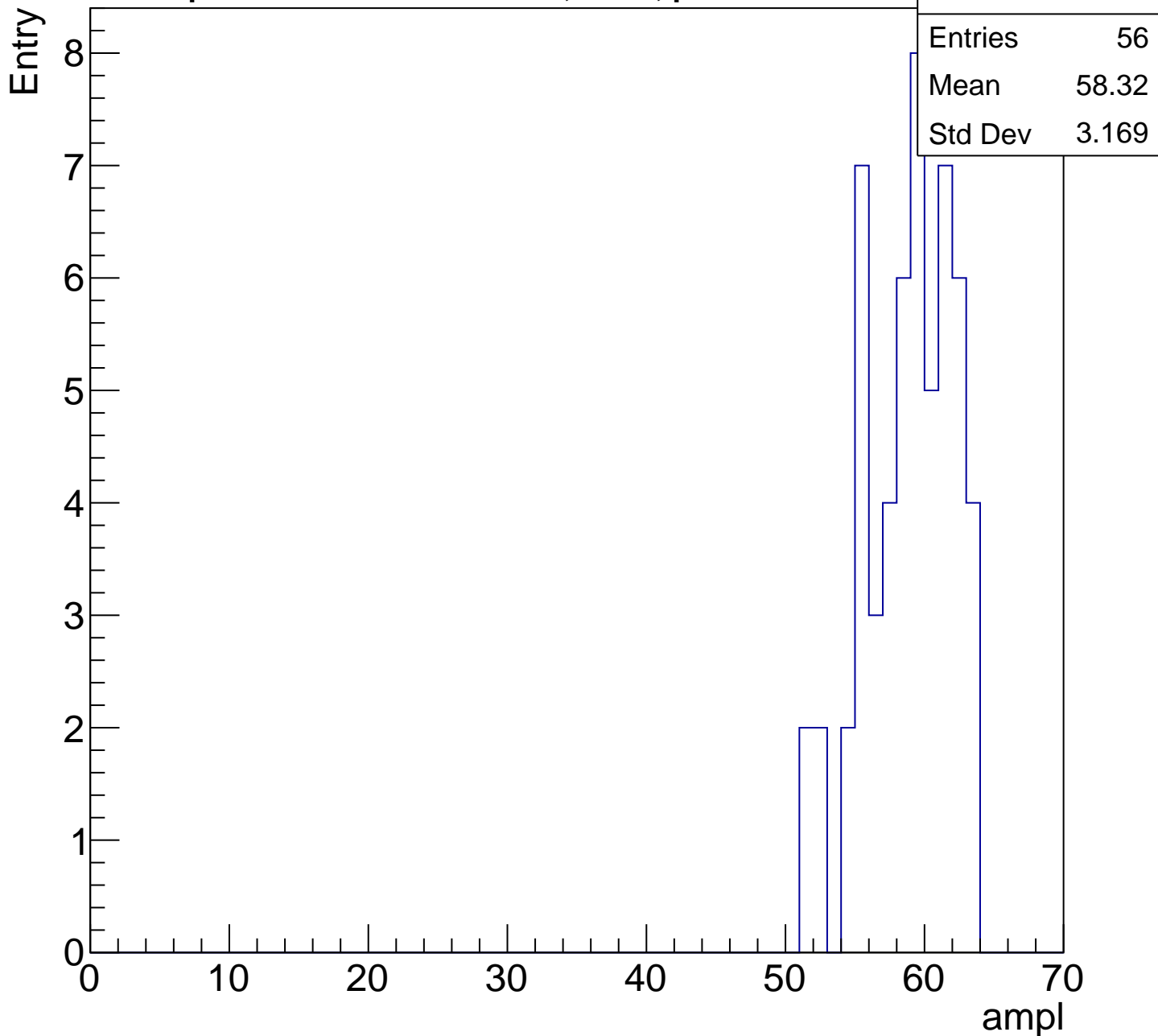
Entry

Entries	56
Mean	54.02
Std Dev	3.618



B1L103S, U5-ch30, adc5

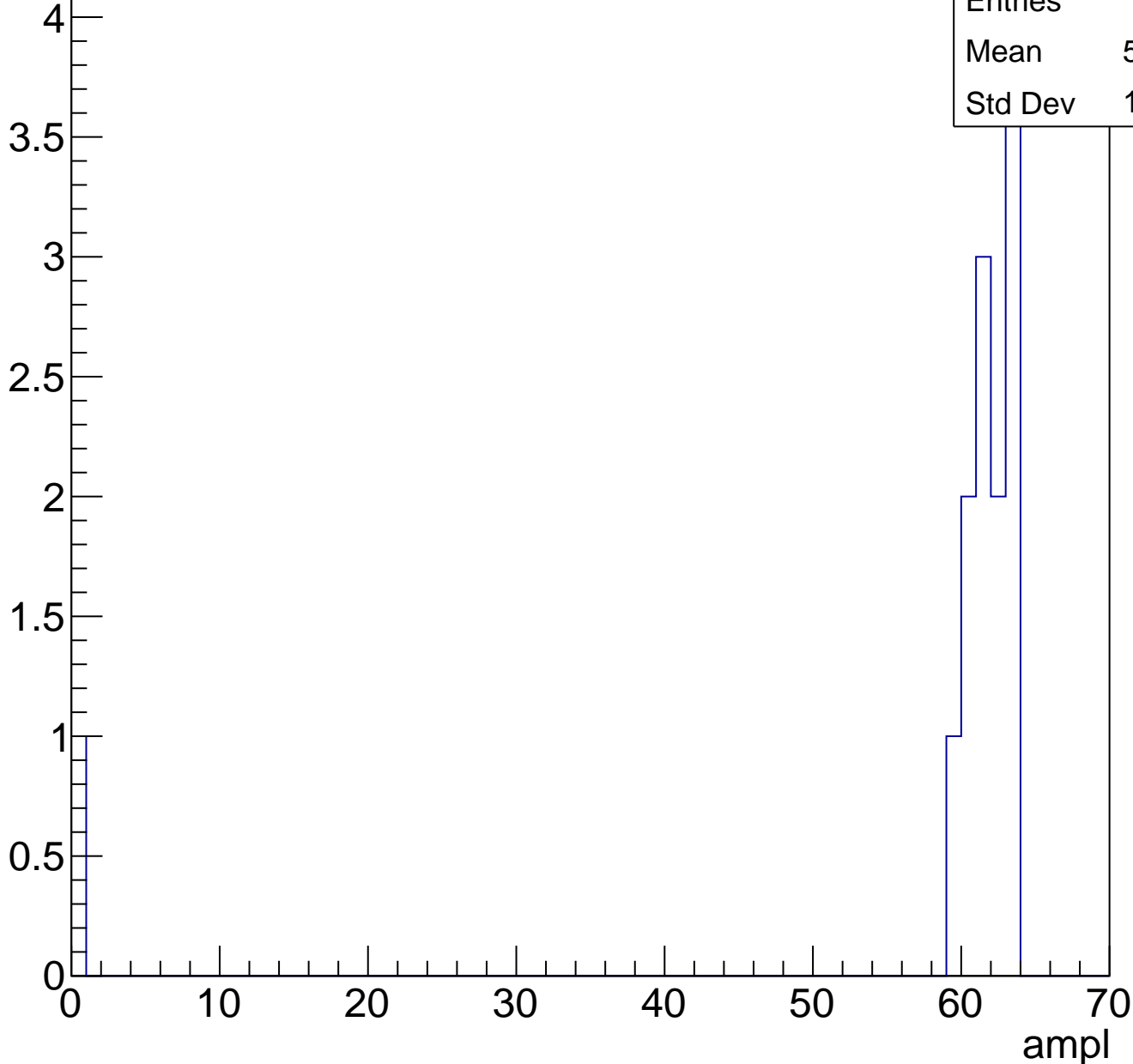
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch31, adc0

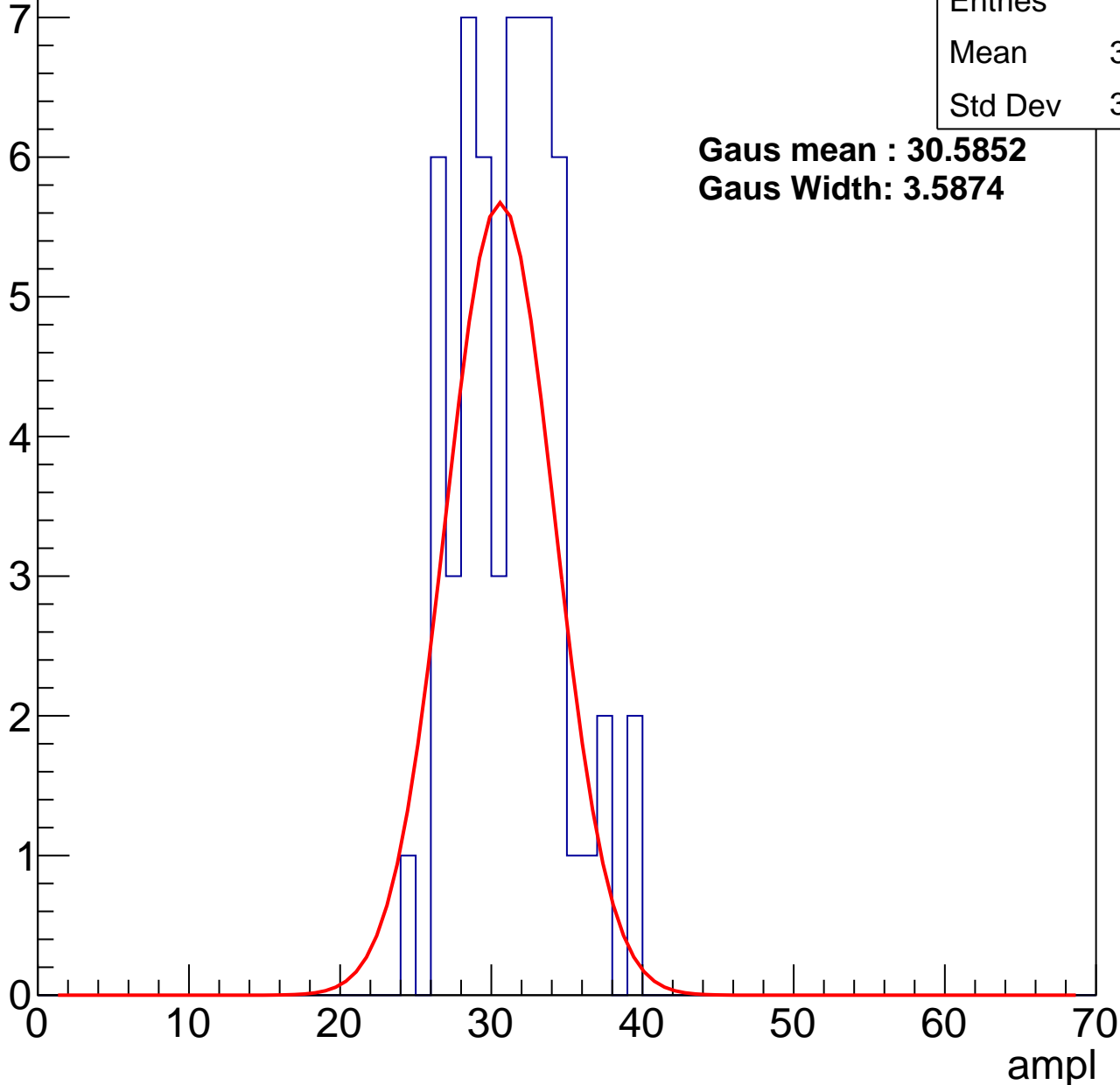
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	30.85
Std Dev	3.359

Gaus mean : 30.5852

Gaus Width: 3.5874



B1L103S, U5-ch31, adc1

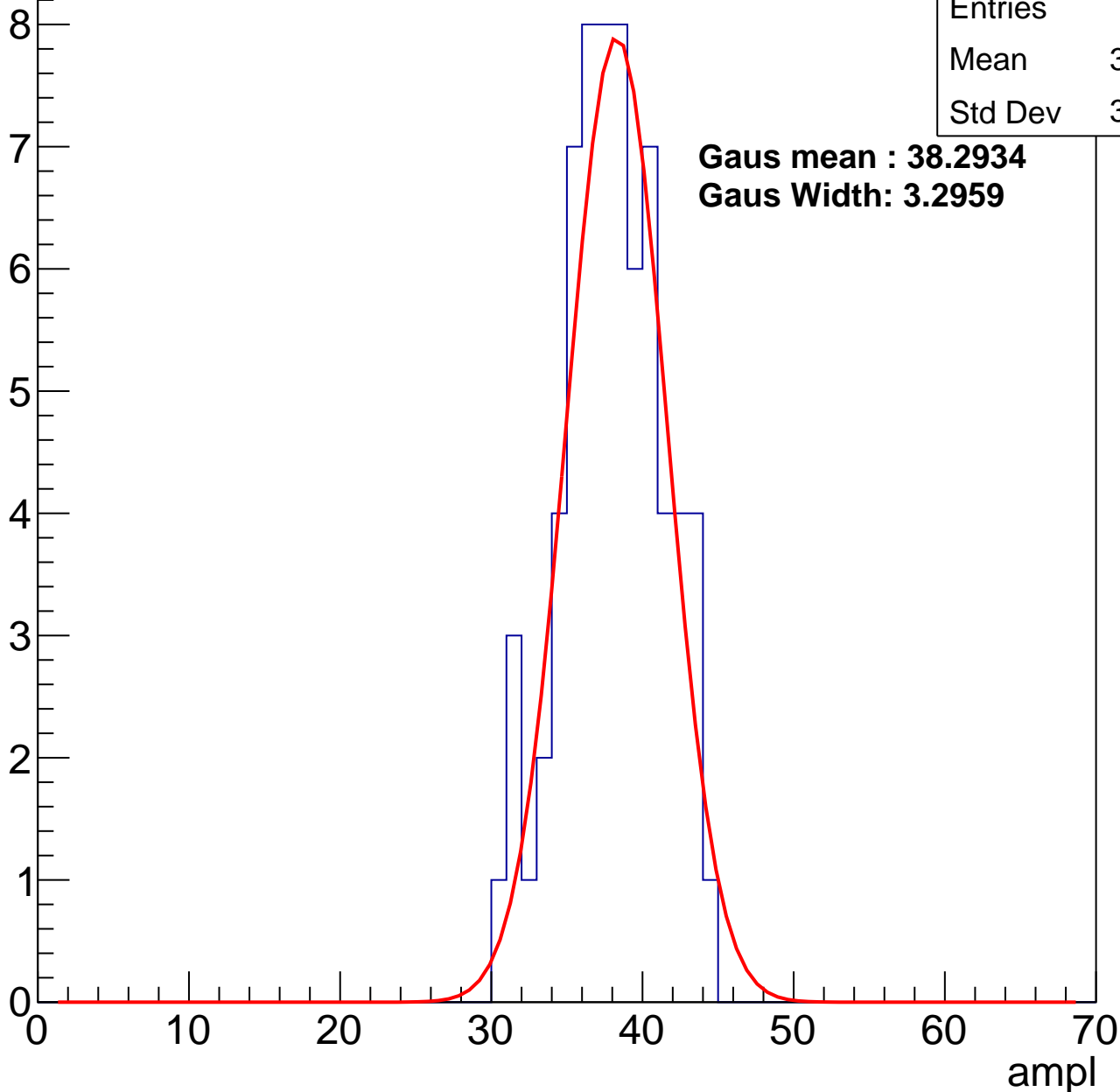
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.53
Std Dev	3.252

Gaus mean : 38.2934

Gaus Width: 3.2959



B1L103S, U5-ch31, adc2

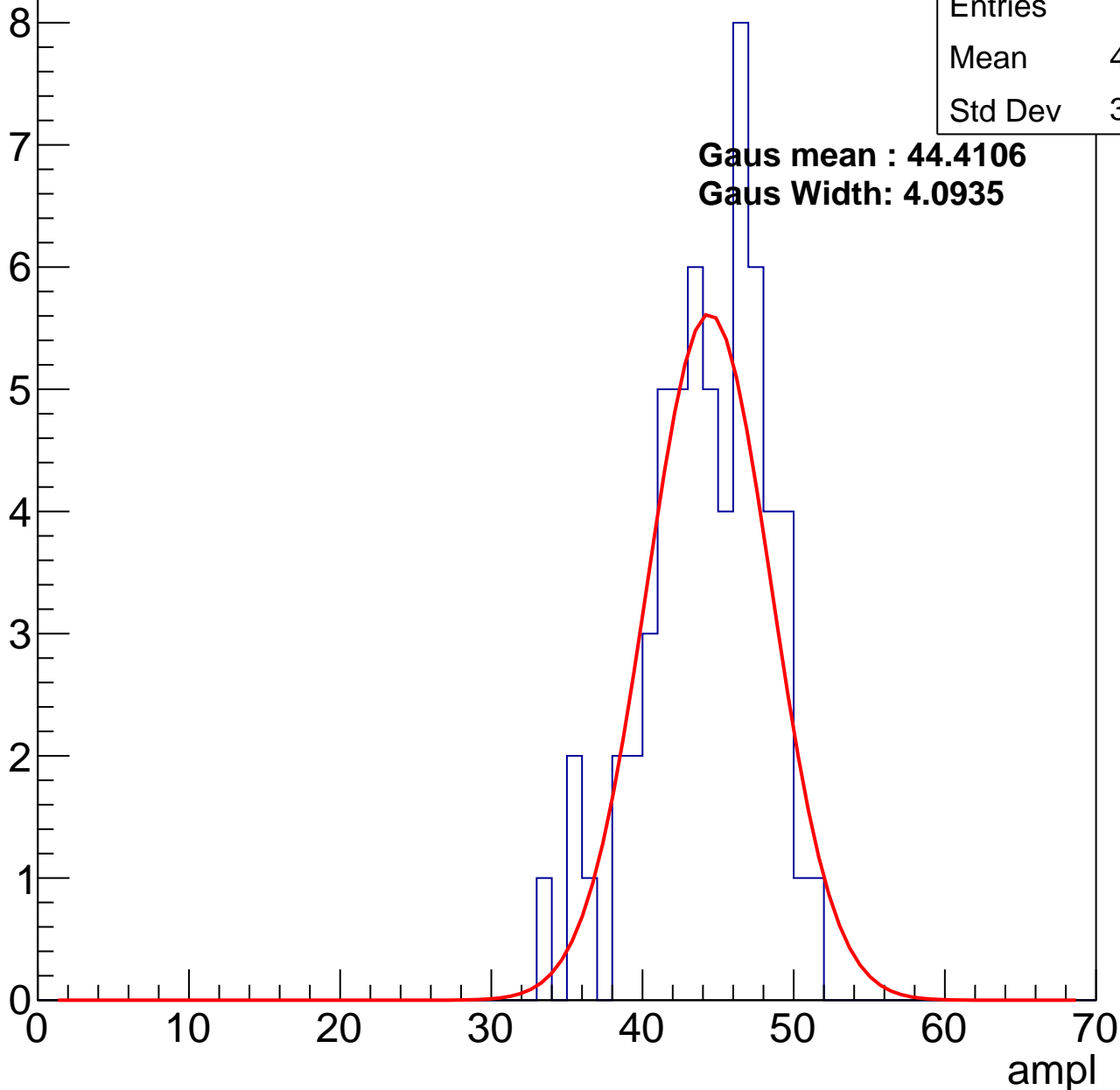
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.75
Std Dev	3.923

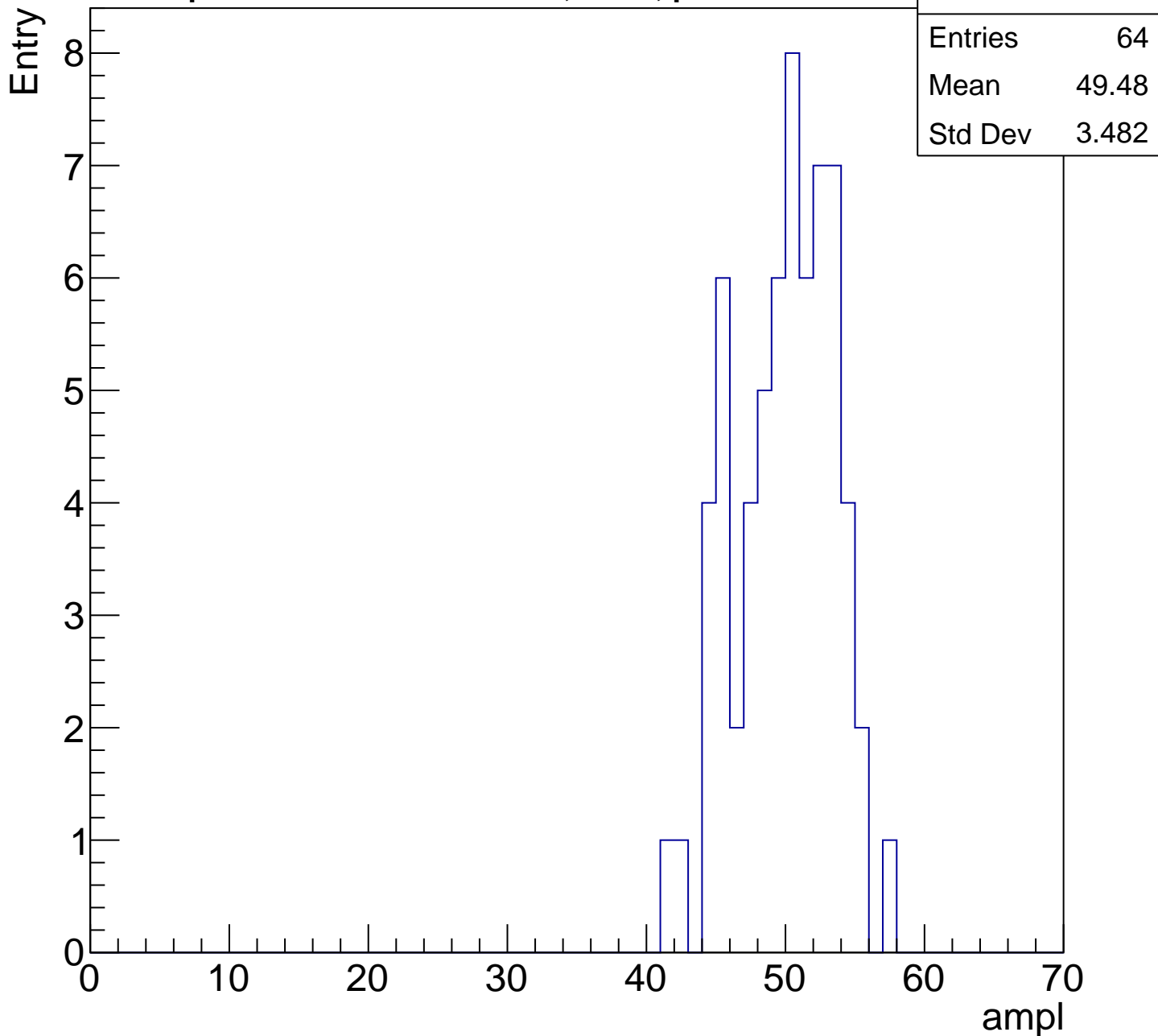
Gaus mean : 44.4106

Gaus Width: 4.0935



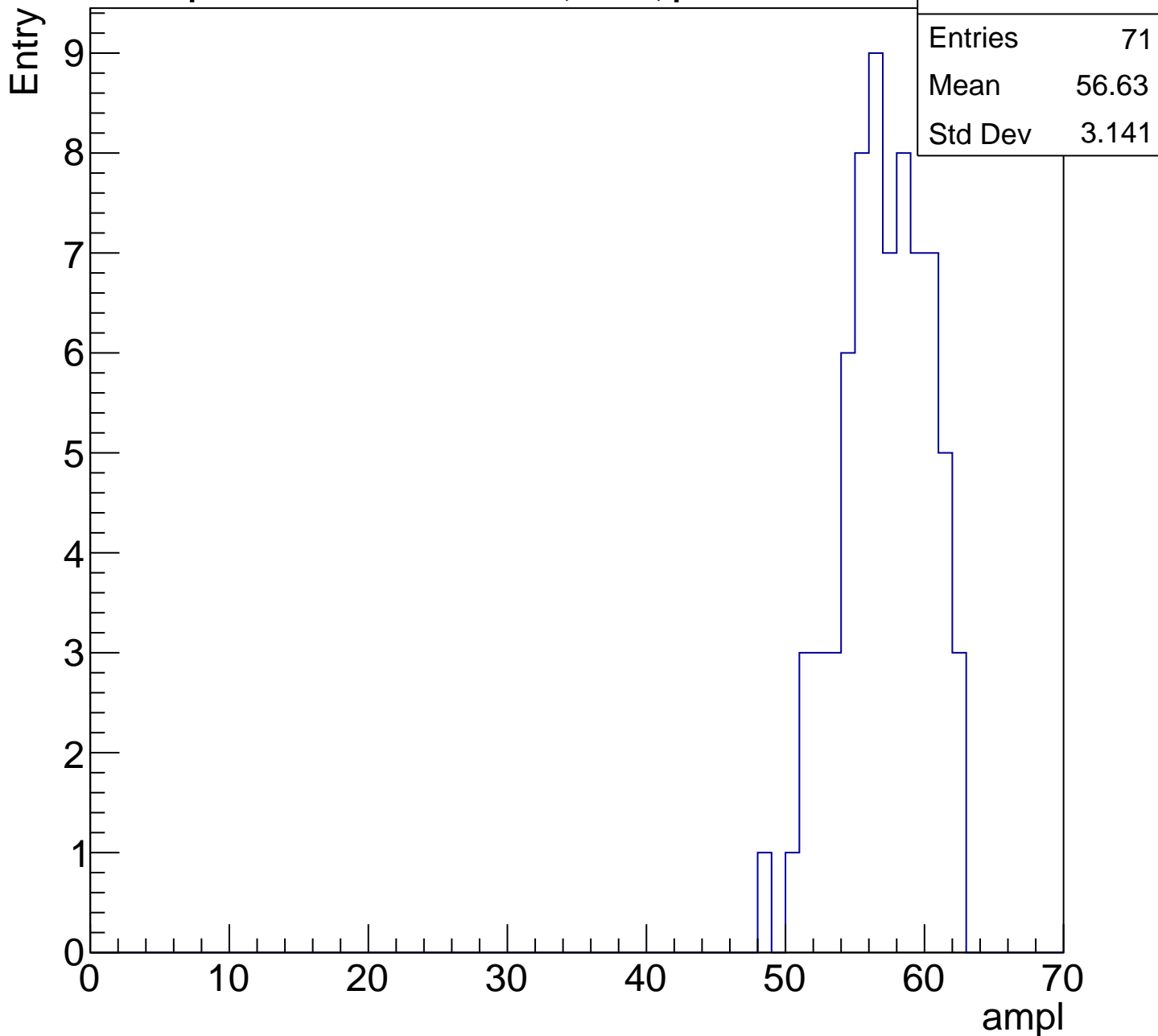
B1L103S, U5-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

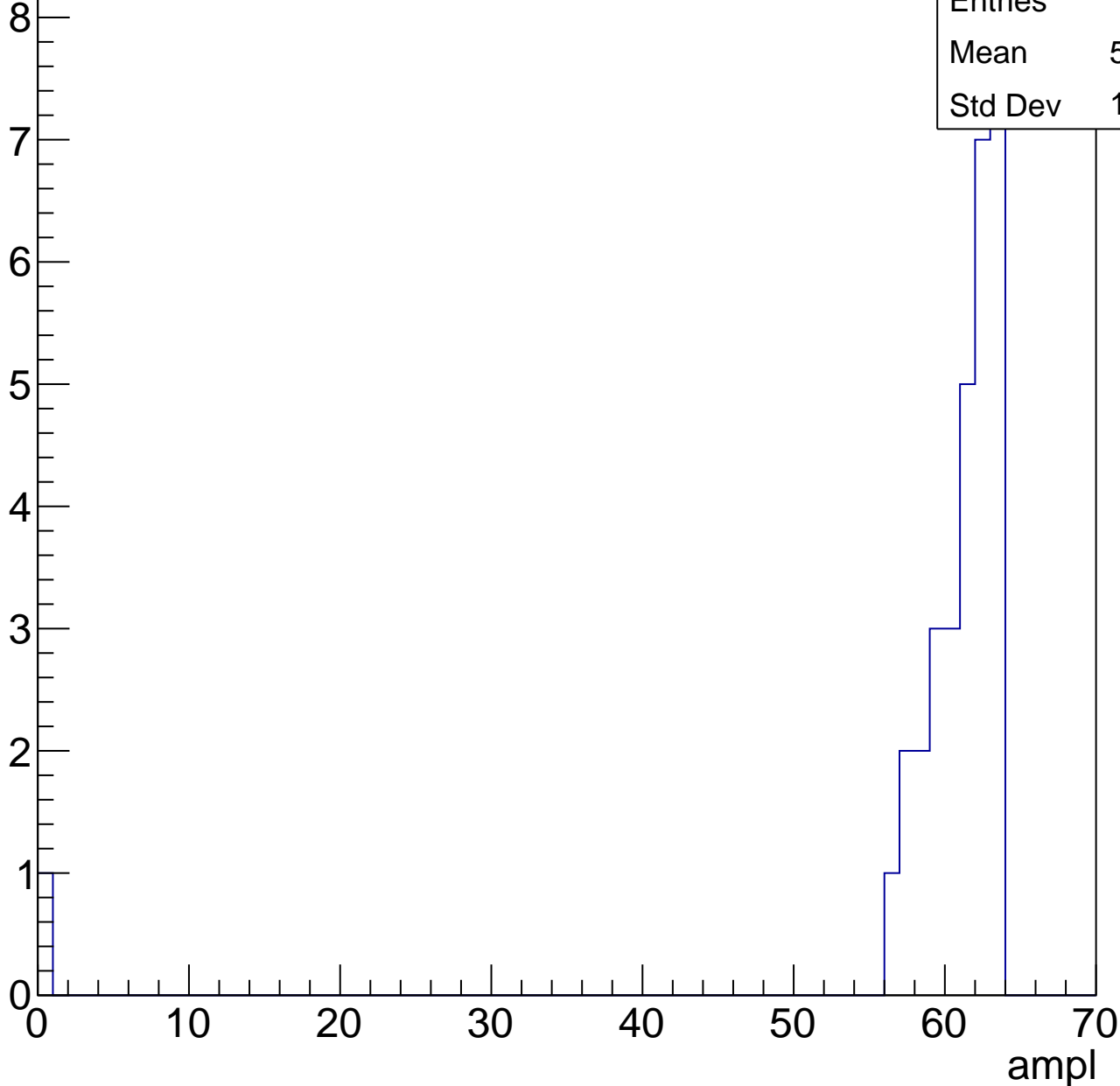


B1L103S, U5-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	58.94
Std Dev	10.77



B1L103S, U5-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L103S, U5-ch32, adc0

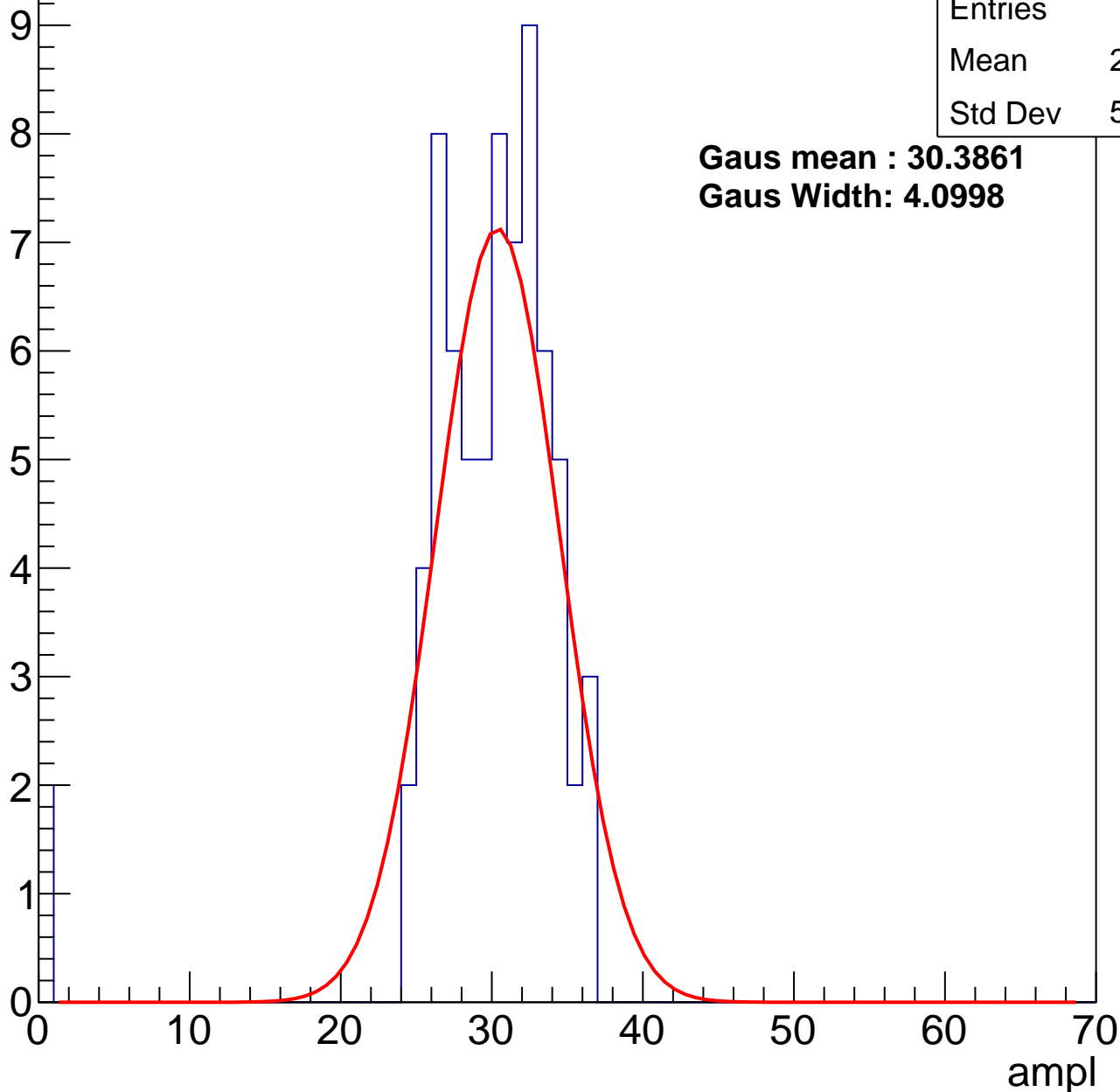
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.08
Std Dev	5.837

Gaus mean : 30.3861

Gaus Width: 4.0998



B1L103S, U5-ch32, adc1

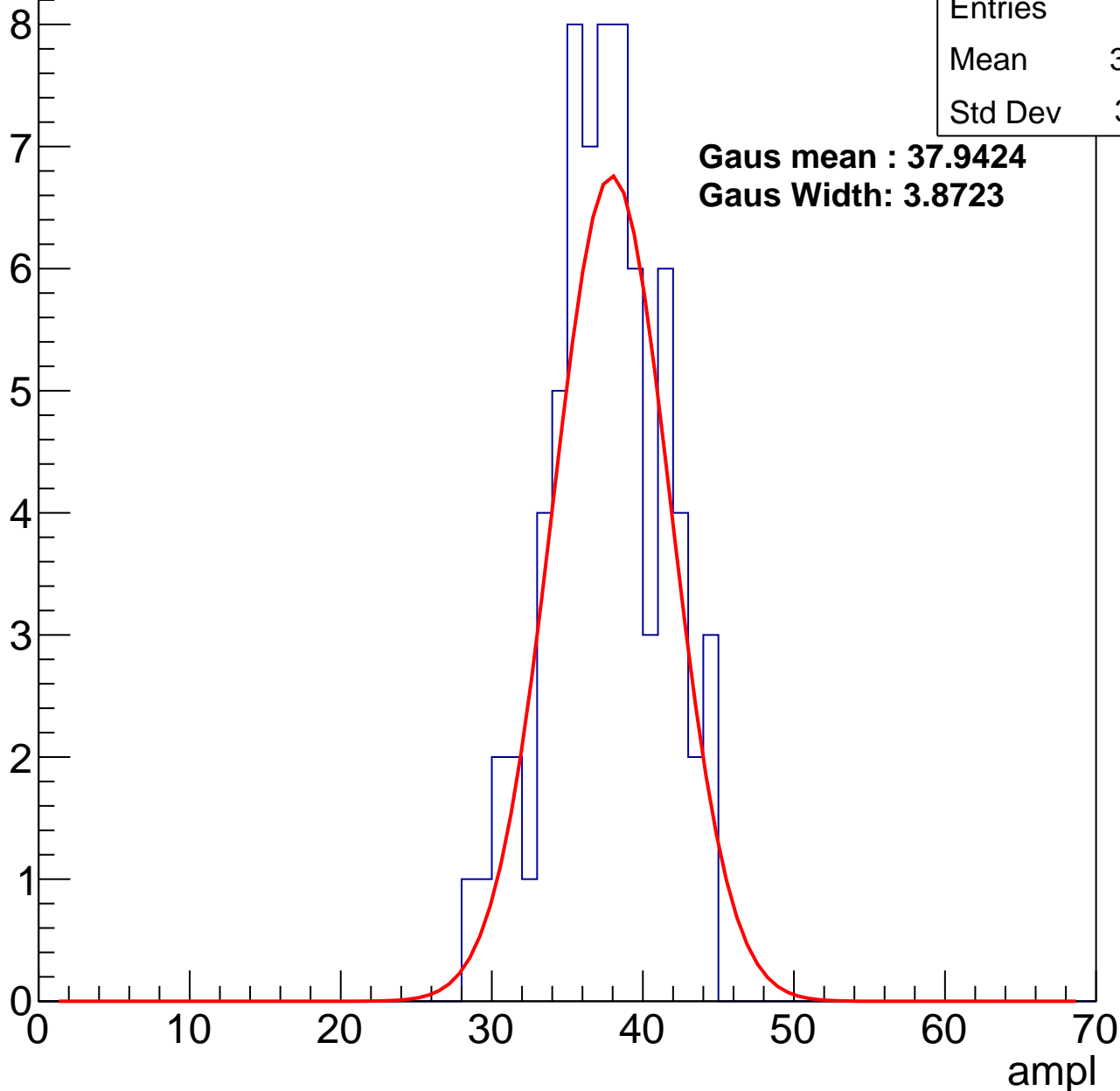
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	37.06
Std Dev	3.681

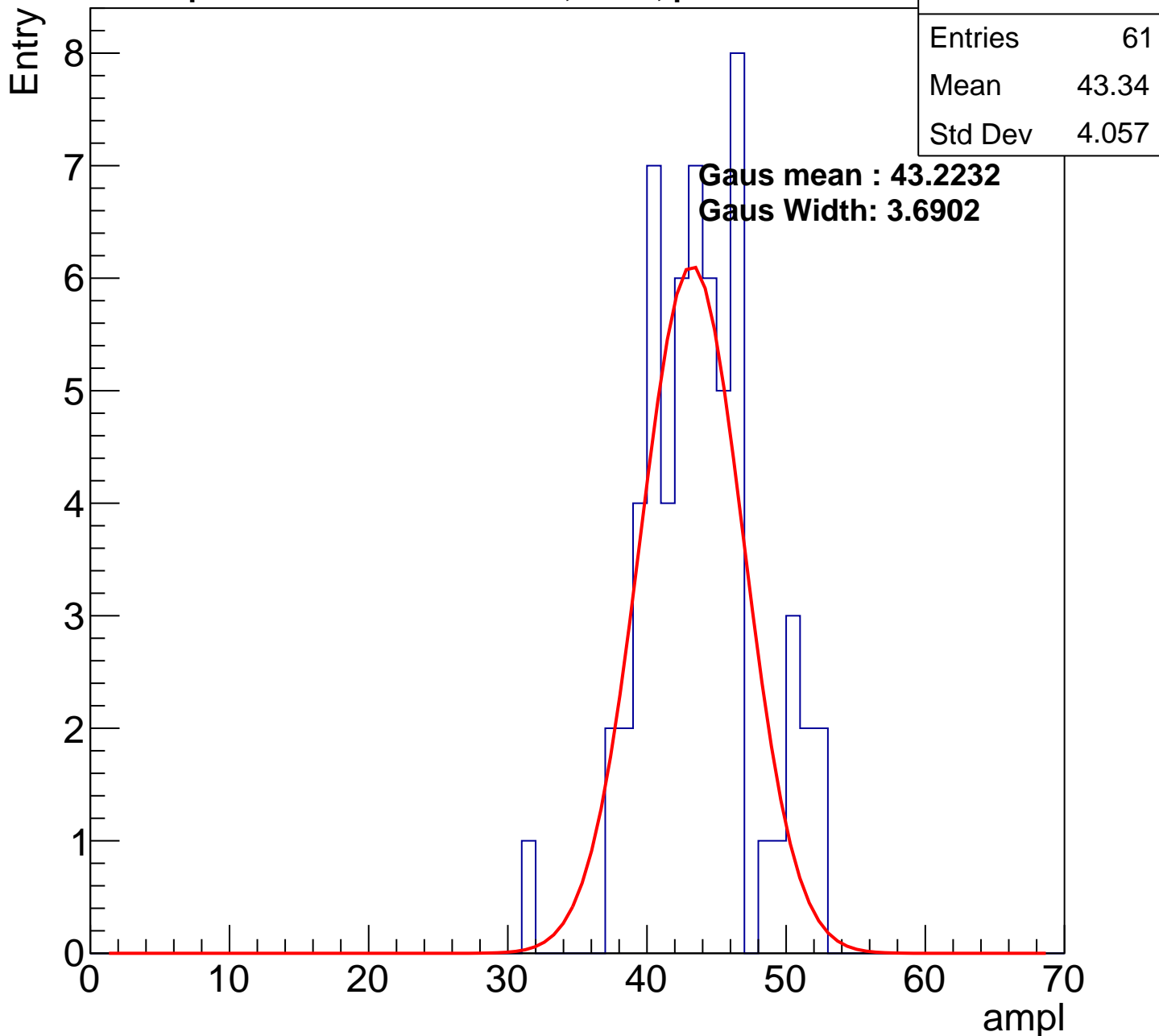
Gaus mean : 37.9424

Gaus Width: 3.8723



B1L103S, U5-ch32, adc2

calib_packv5_042523_0143.root, FC#7, port C2

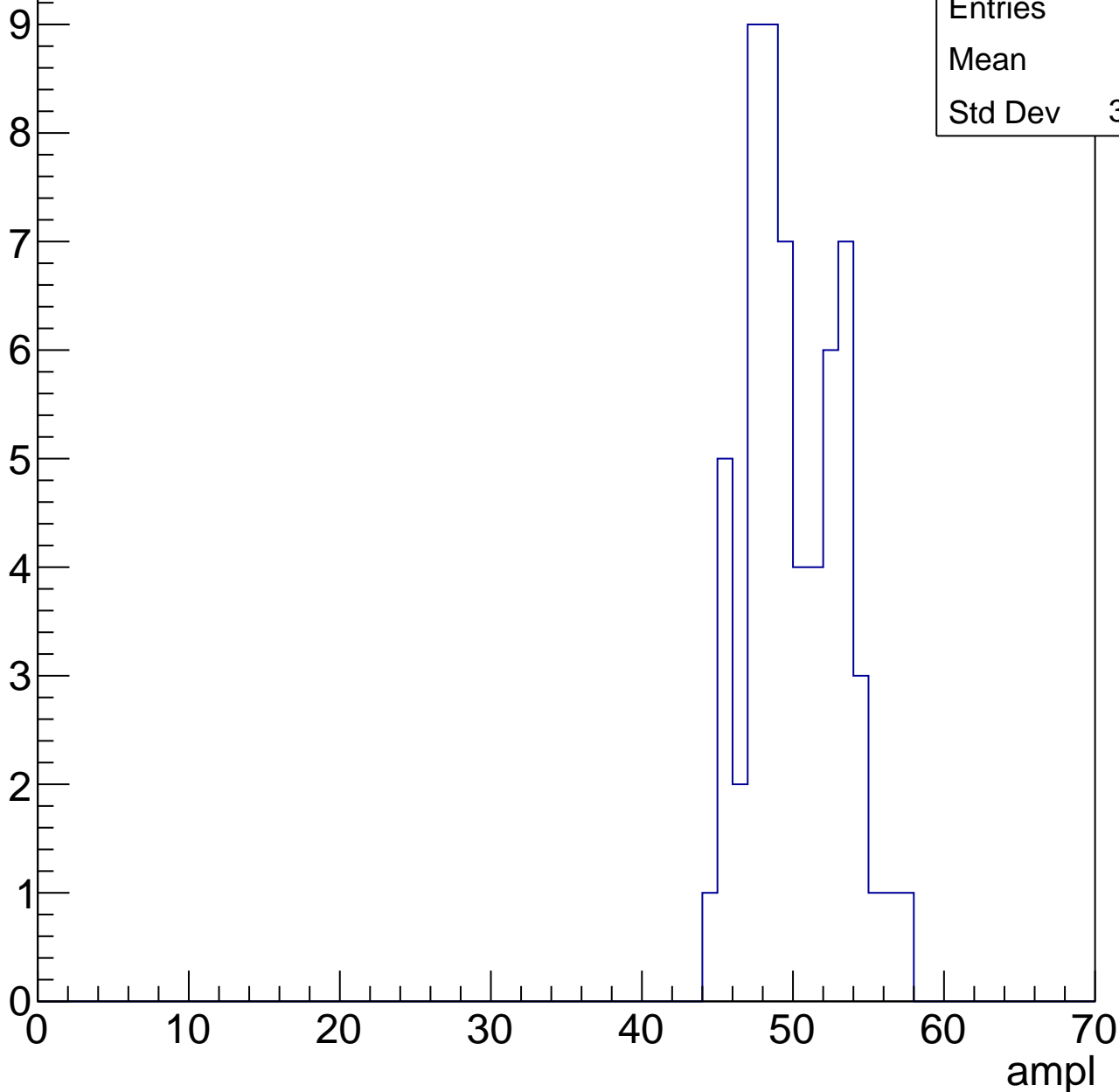


B1L103S, U5-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.6
Std Dev	3.045

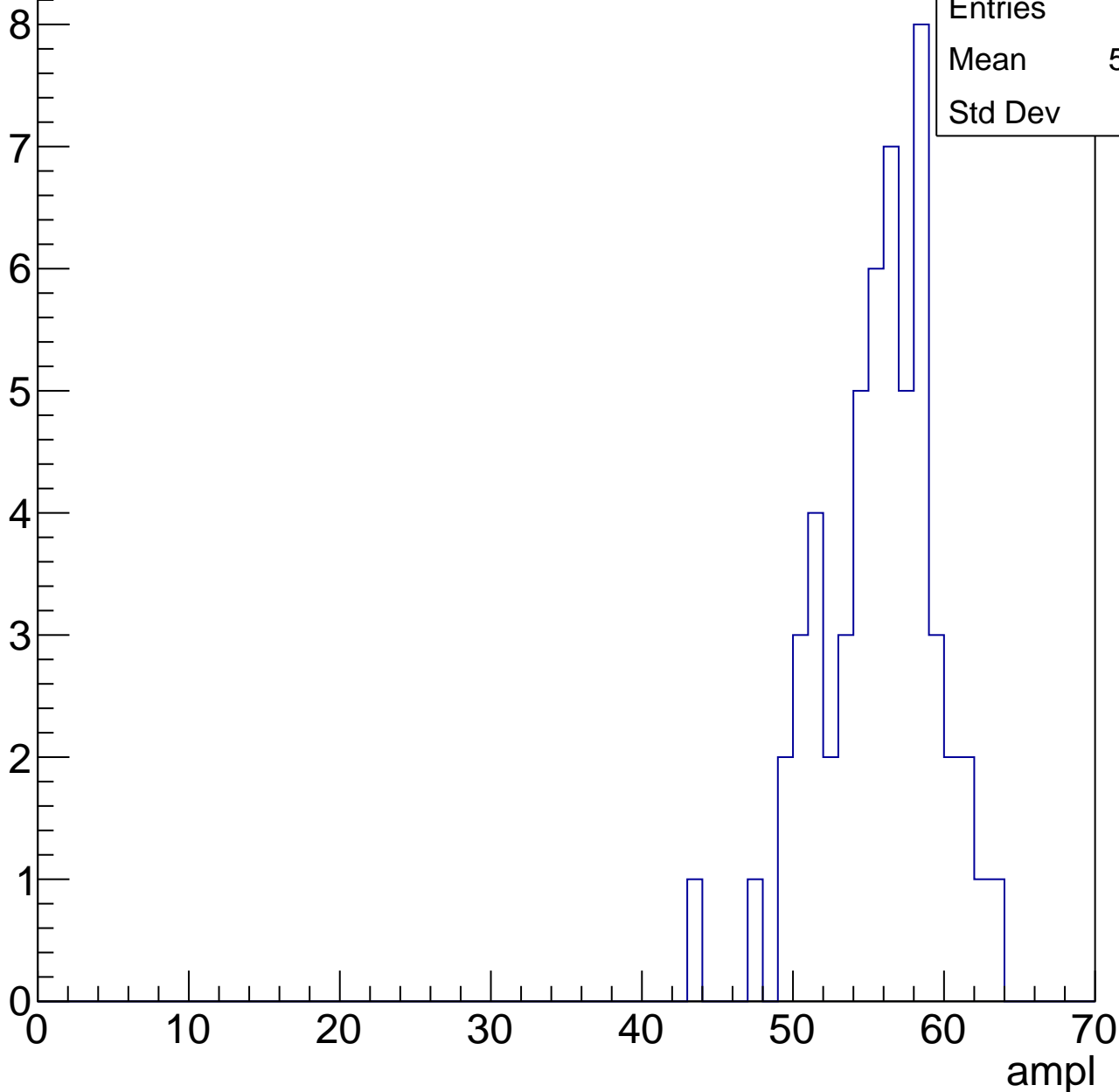


B1L103S, U5-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

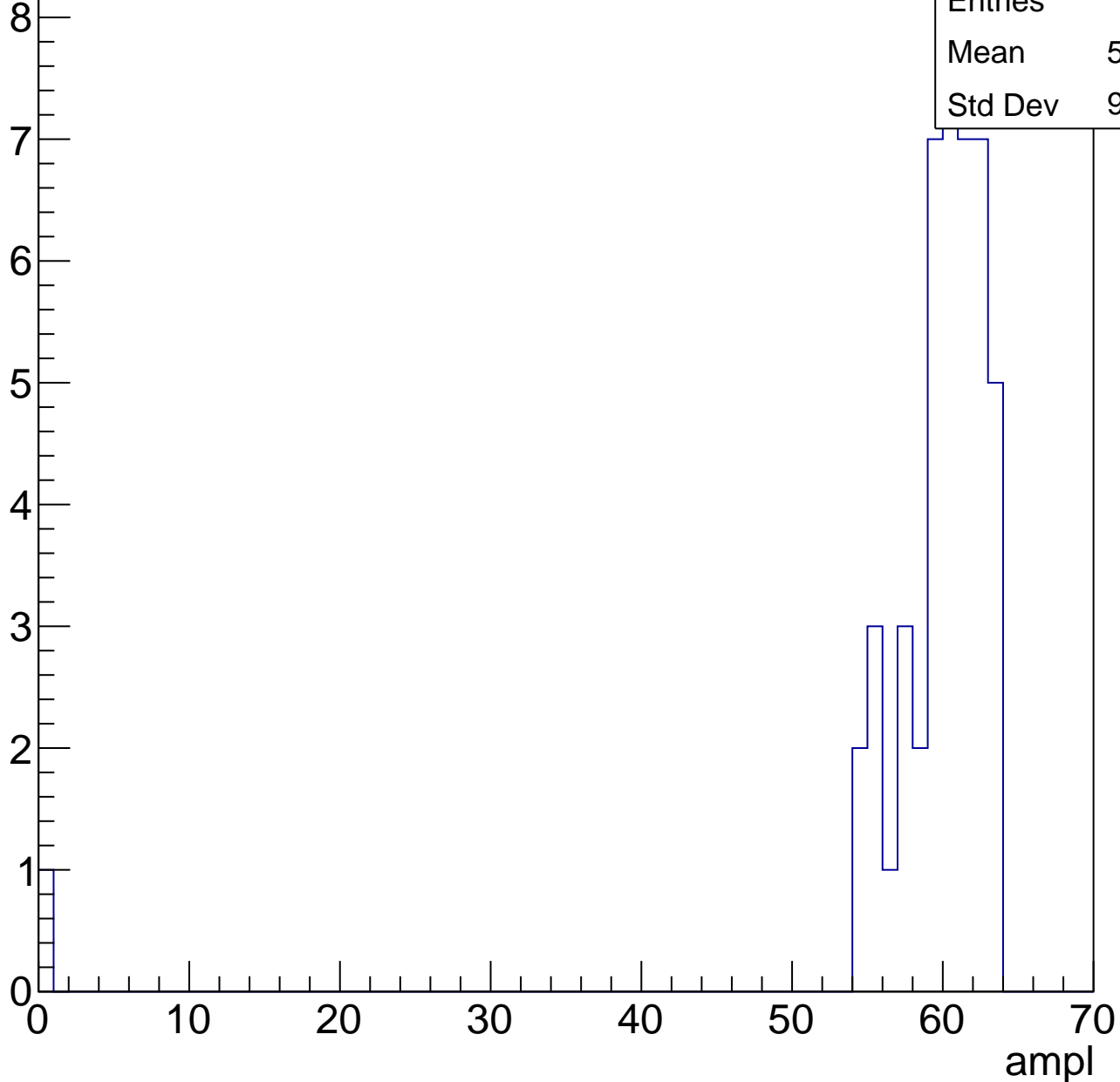
Entries	56
Mean	55.18
Std Dev	3.86



B1L103S, U5-ch32, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

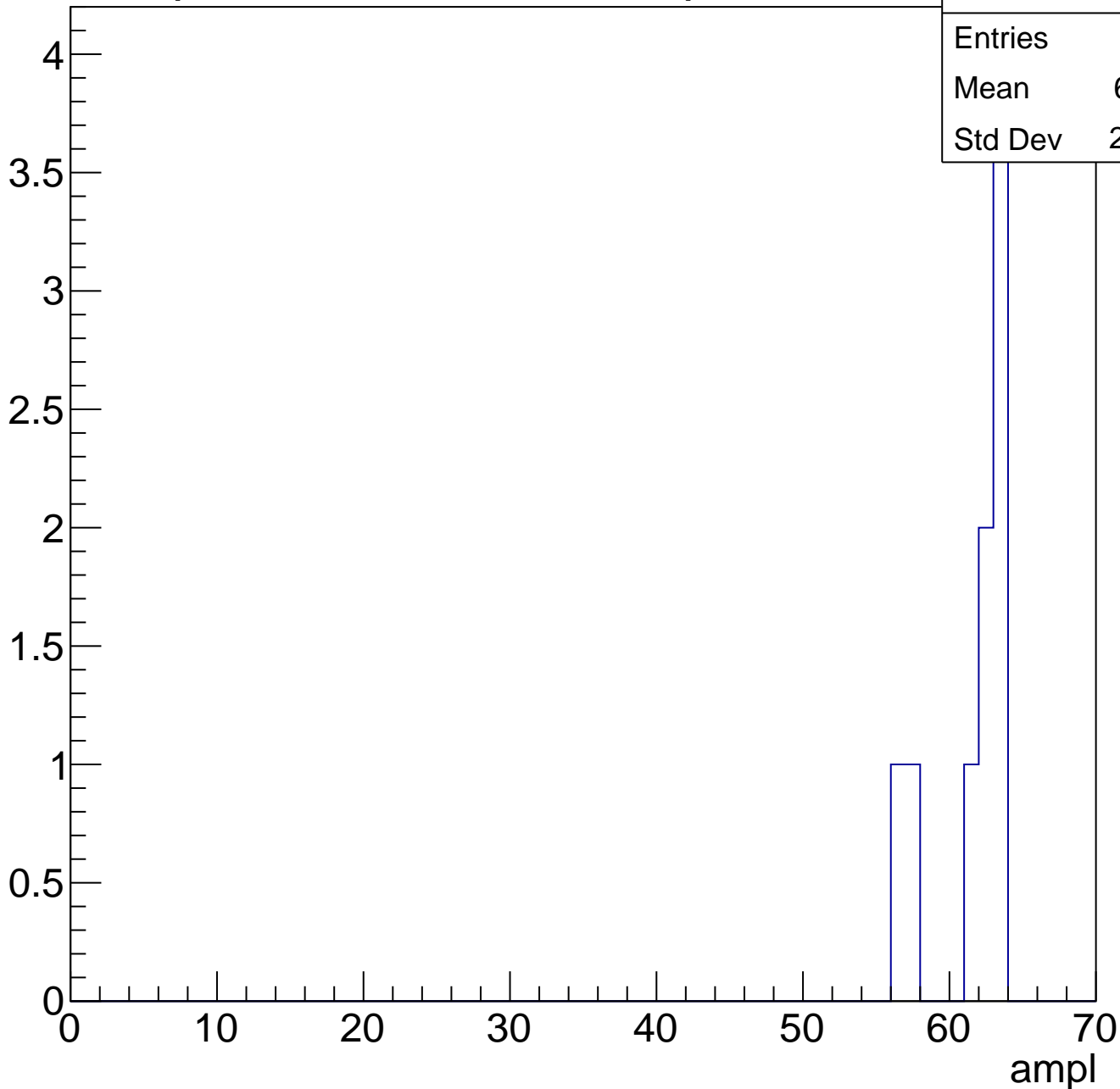


Entries	46
Mean	58.37
Std Dev	9.044

B1L103S, U5-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch33, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	30.11
Std Dev	3.878

Gaus mean : 30.7285

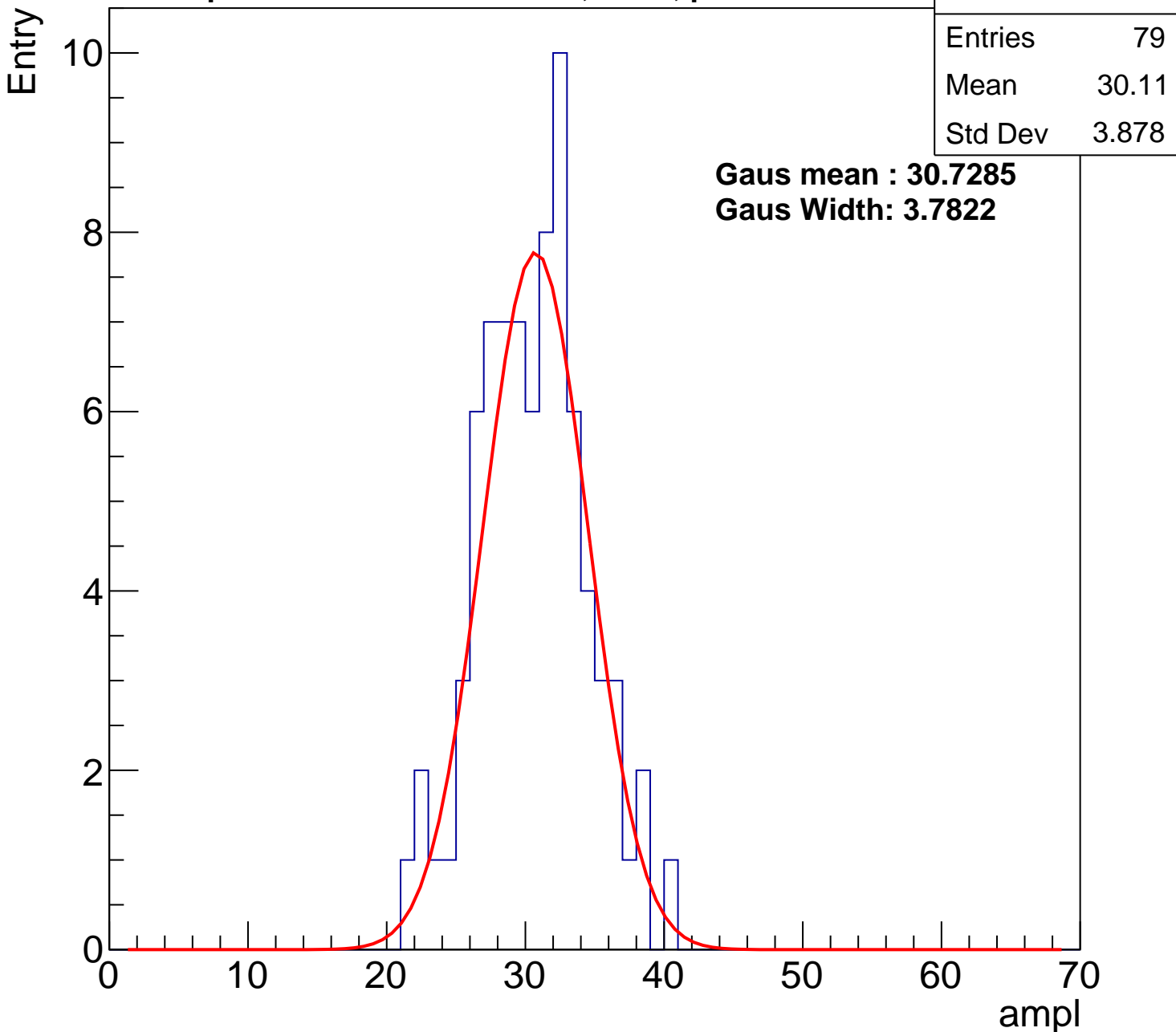
Gaus Width: 3.7822

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch33, adc1

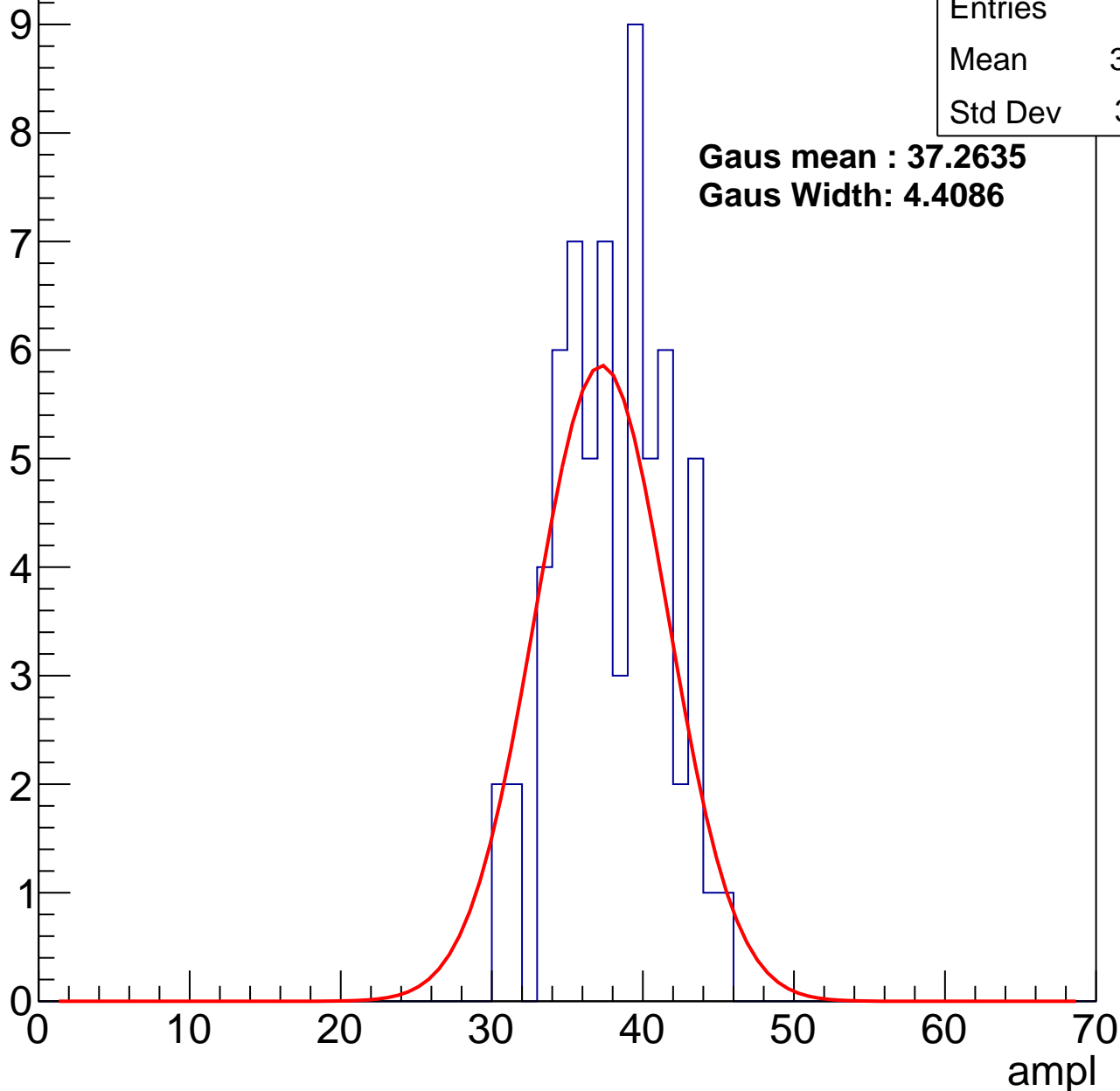
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	37.55
Std Dev	3.561

Gaus mean : 37.2635

Gaus Width: 4.4086



B1L103S, U5-ch33, adc2

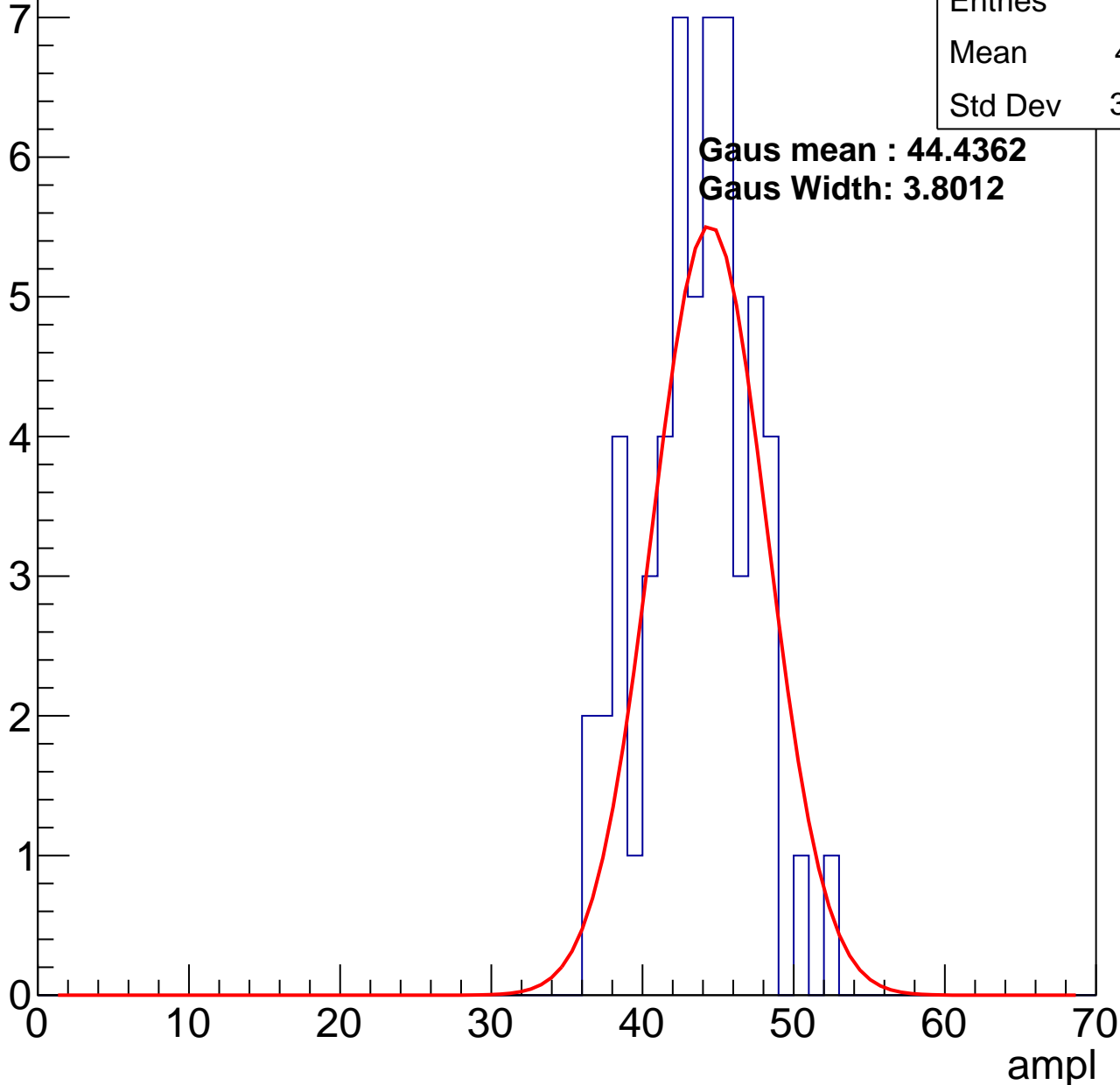
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.21
Std Dev	3.549

Gaus mean : 44.4362

Gaus Width: 3.8012

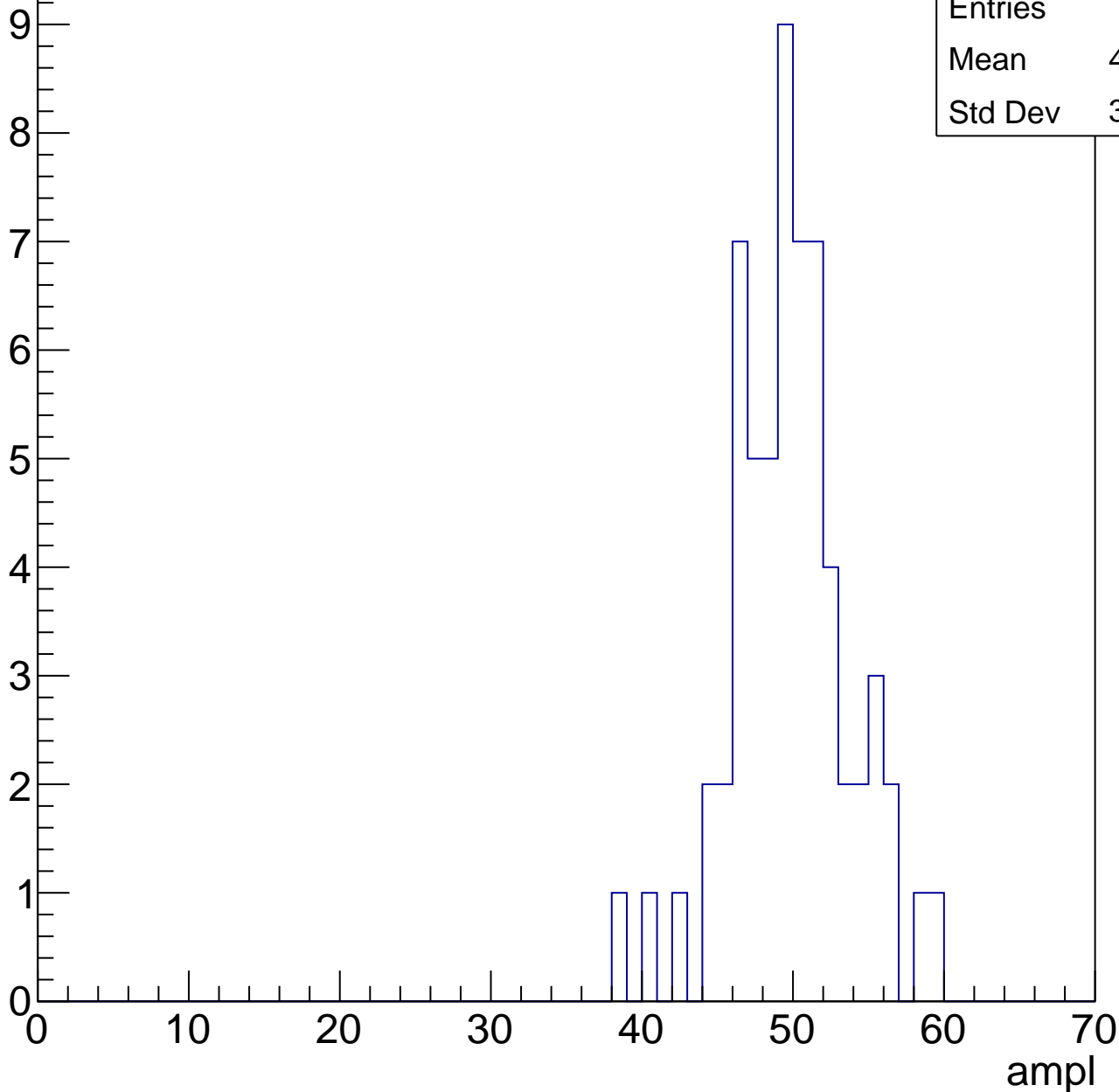


B1L103S, U5-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

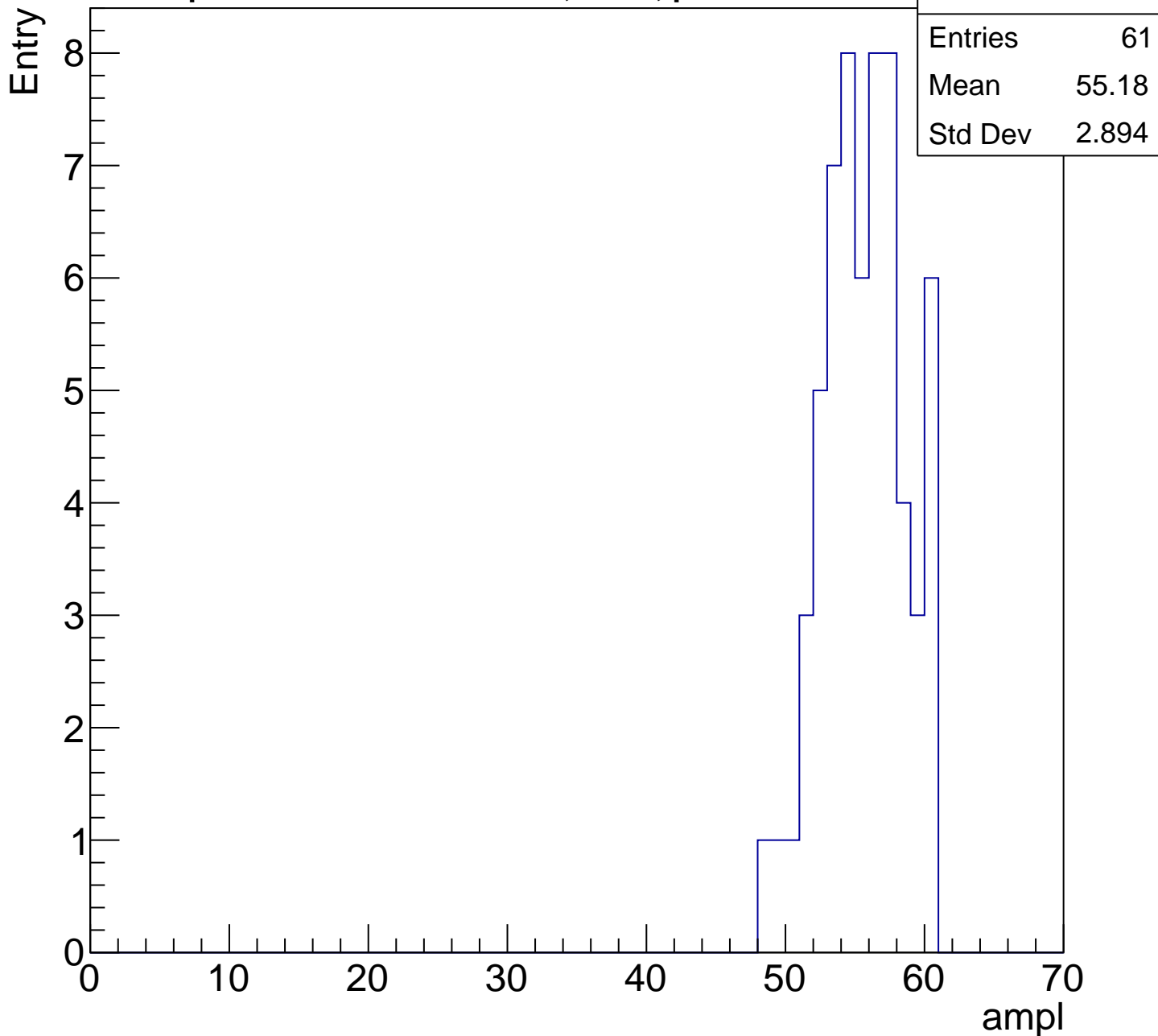
Entry

Entries	62
Mean	49.34
Std Dev	3.922



B1L103S, U5-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

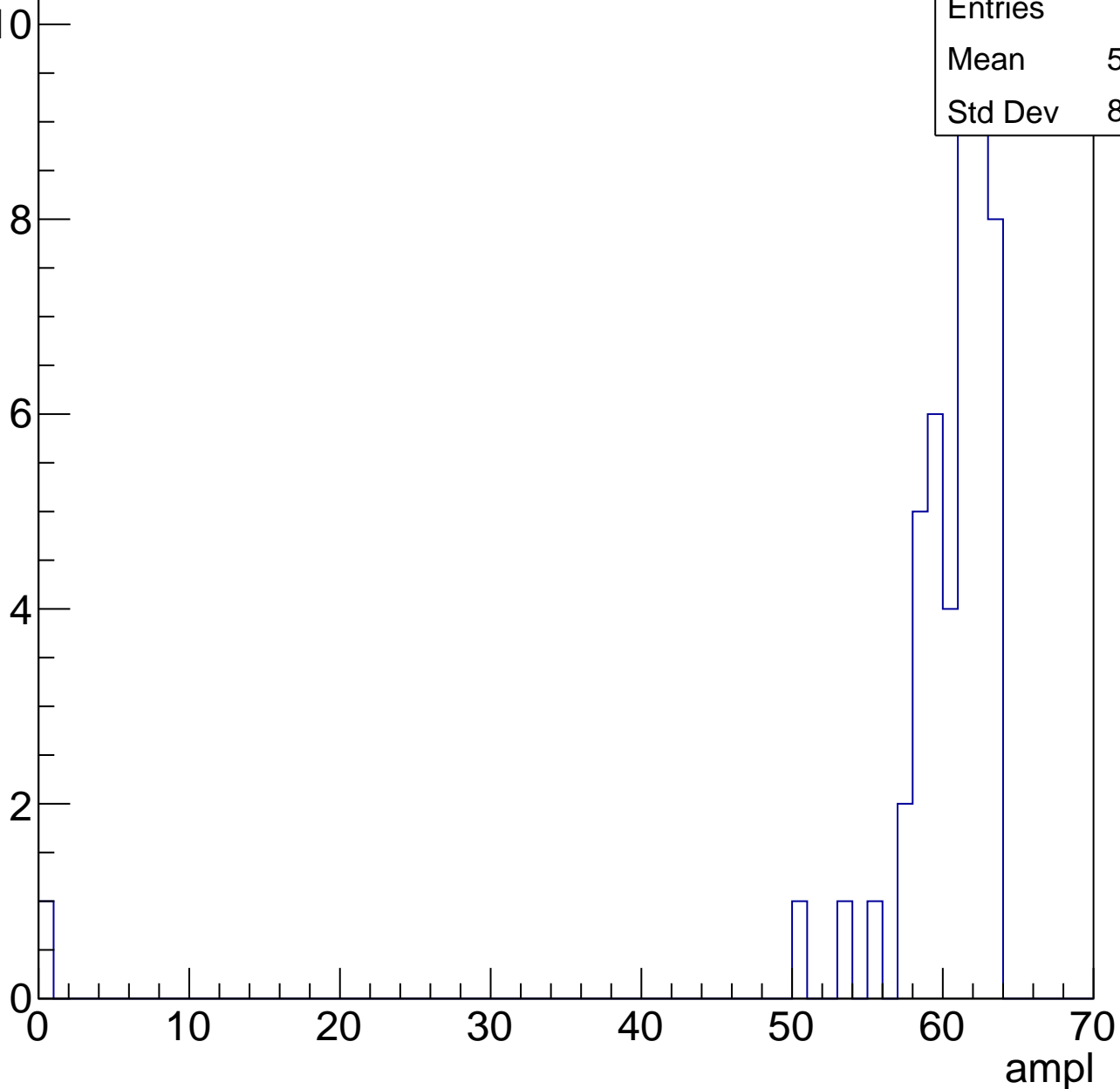


B1L103S, U5-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.94
Std Dev	8.996



B1L103S, U5-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

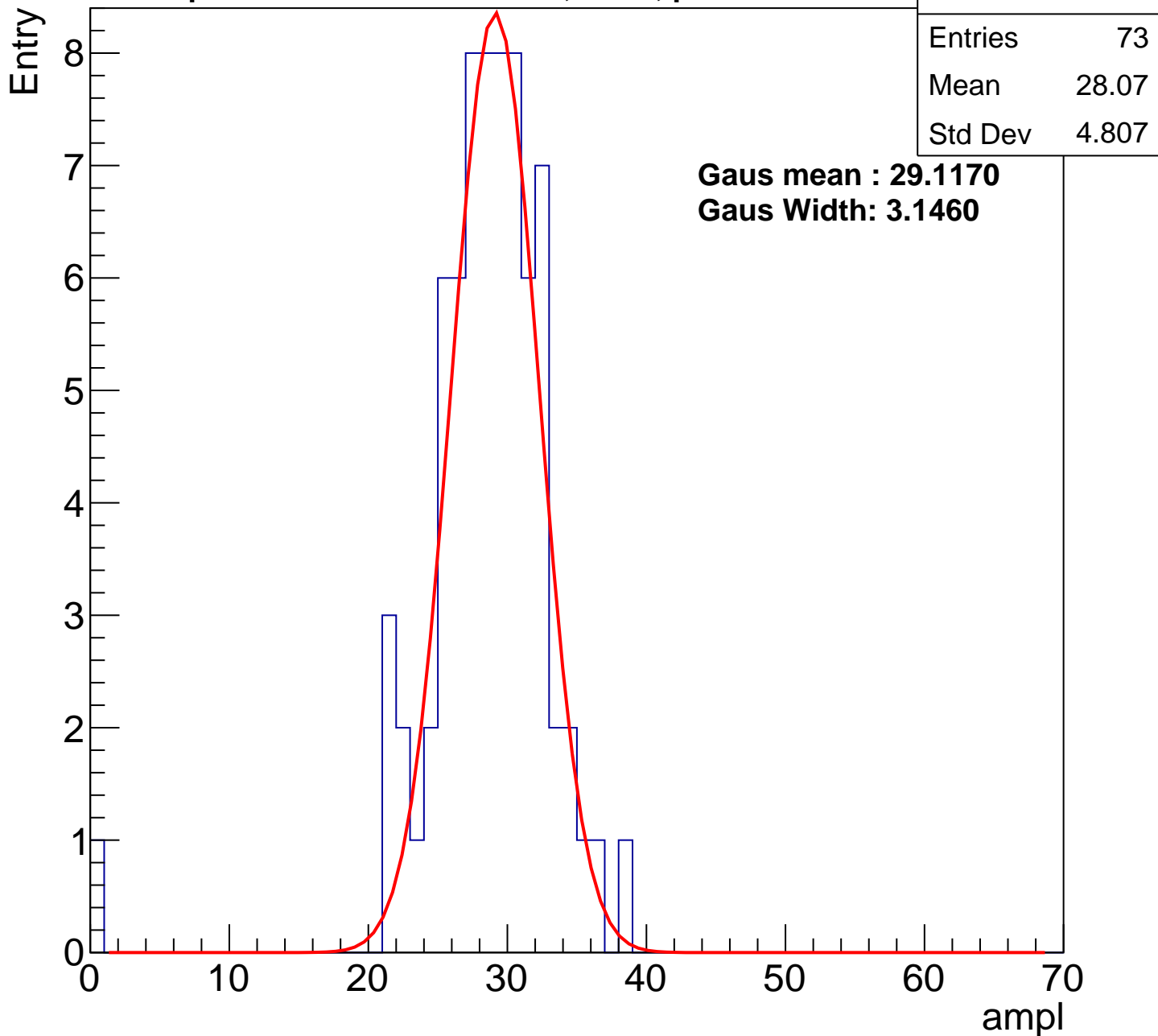
Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U5-ch34, adc0

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch34, adc1

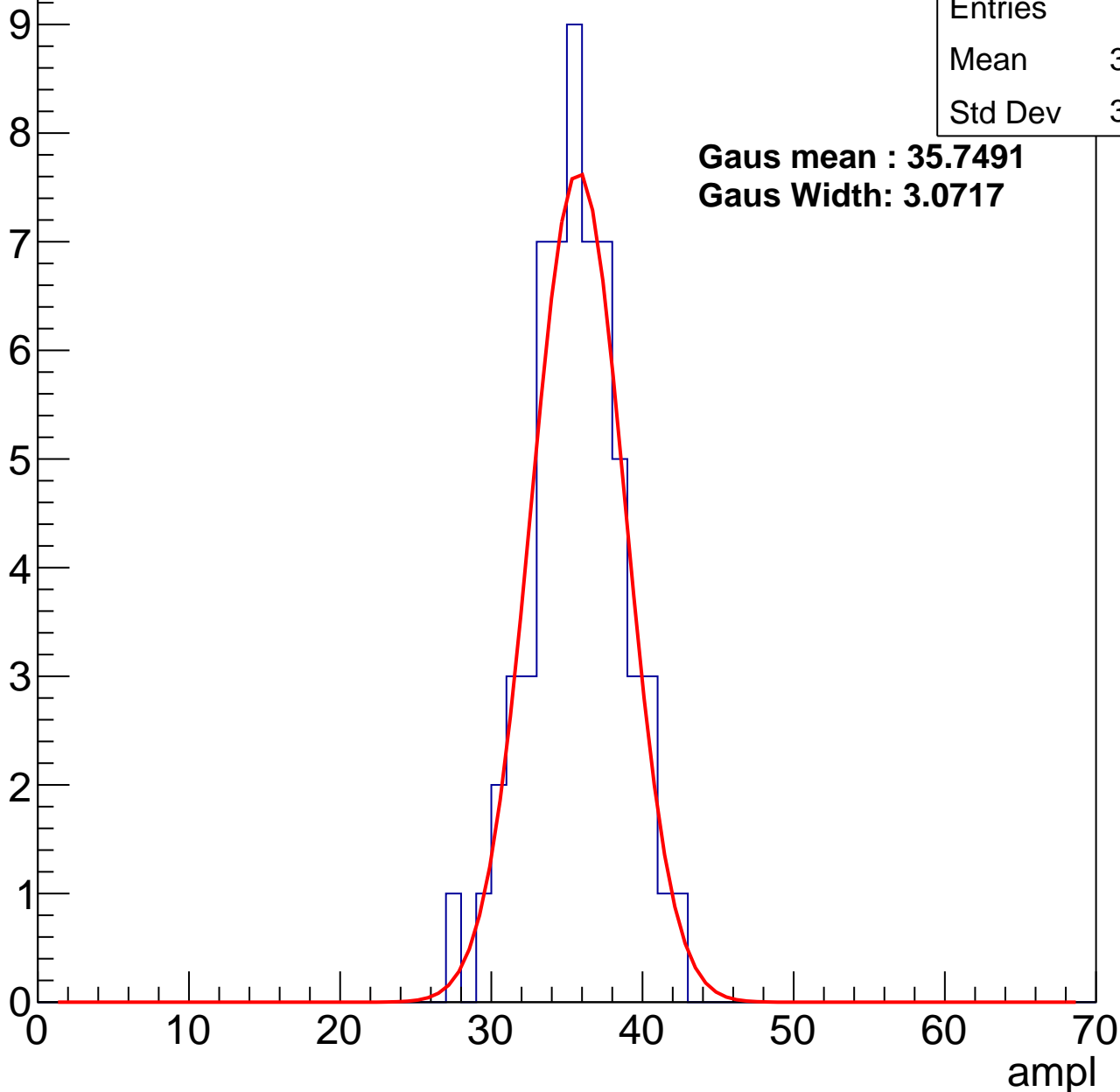
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.17
Std Dev	3.018

Gaus mean : 35.7491

Gaus Width: 3.0717



B1L103S, U5-ch34, adc2

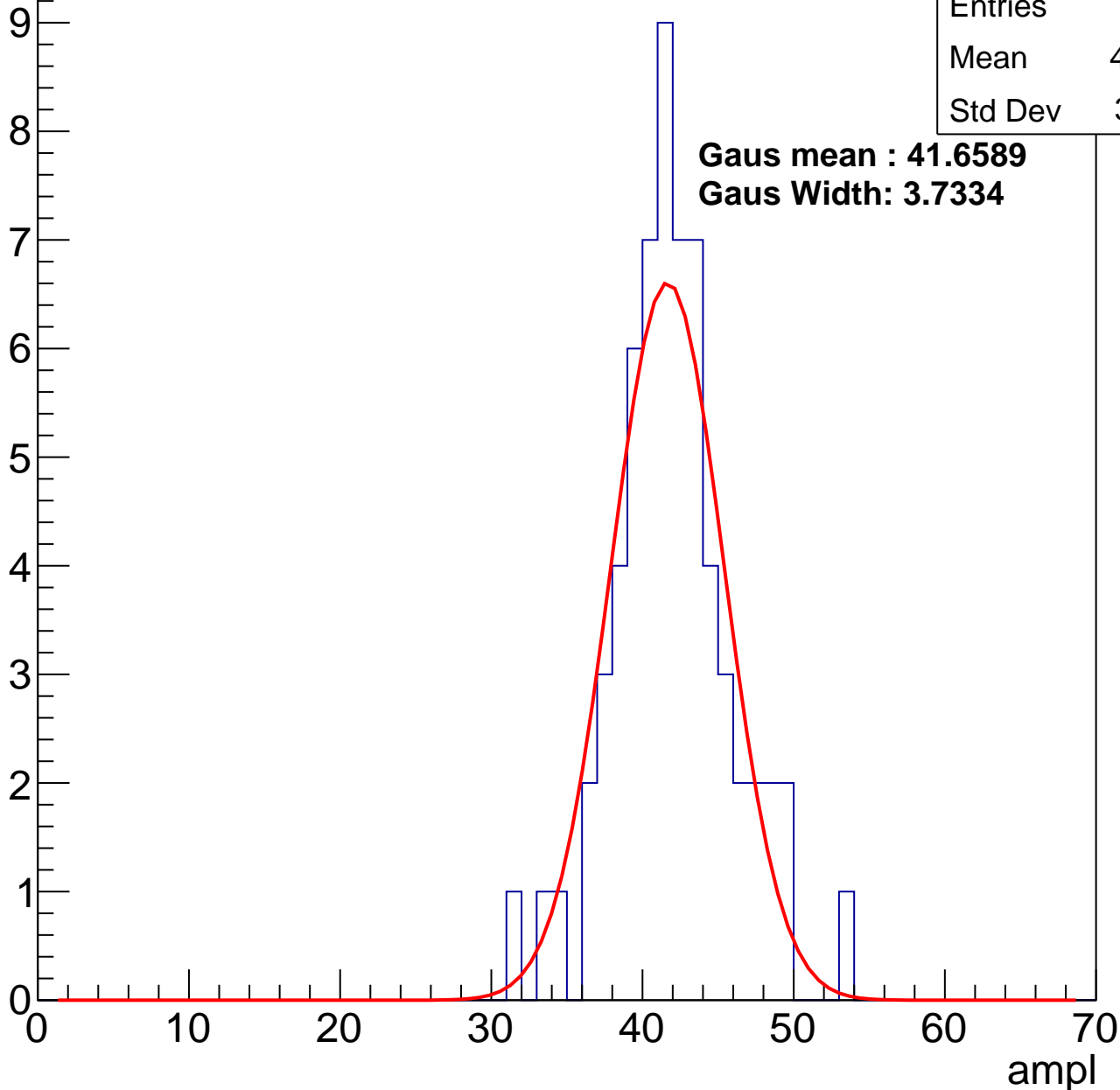
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	41.48
Std Dev	3.901

Gaus mean : 41.6589

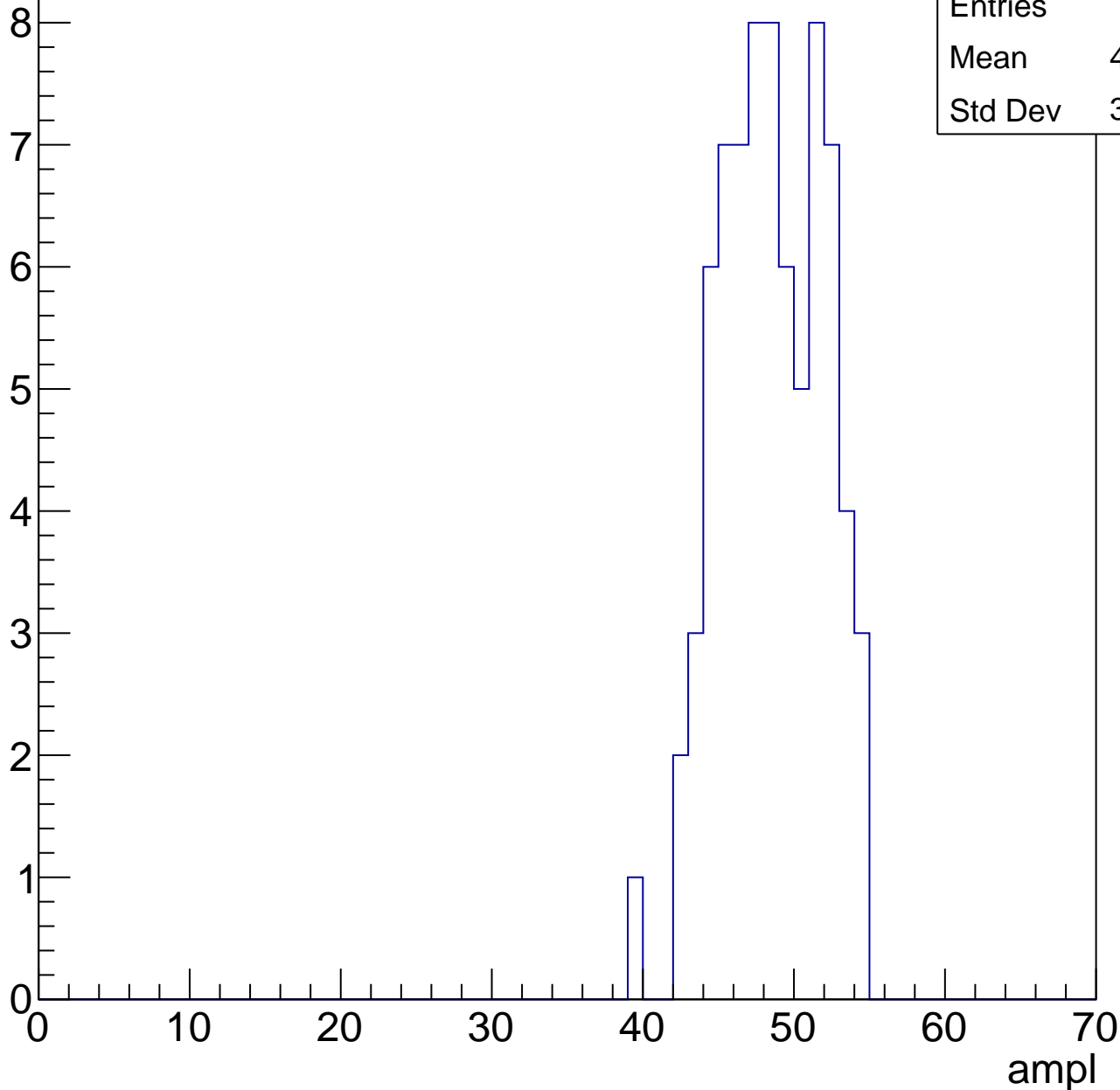
Gaus Width: 3.7334



B1L103S, U5-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



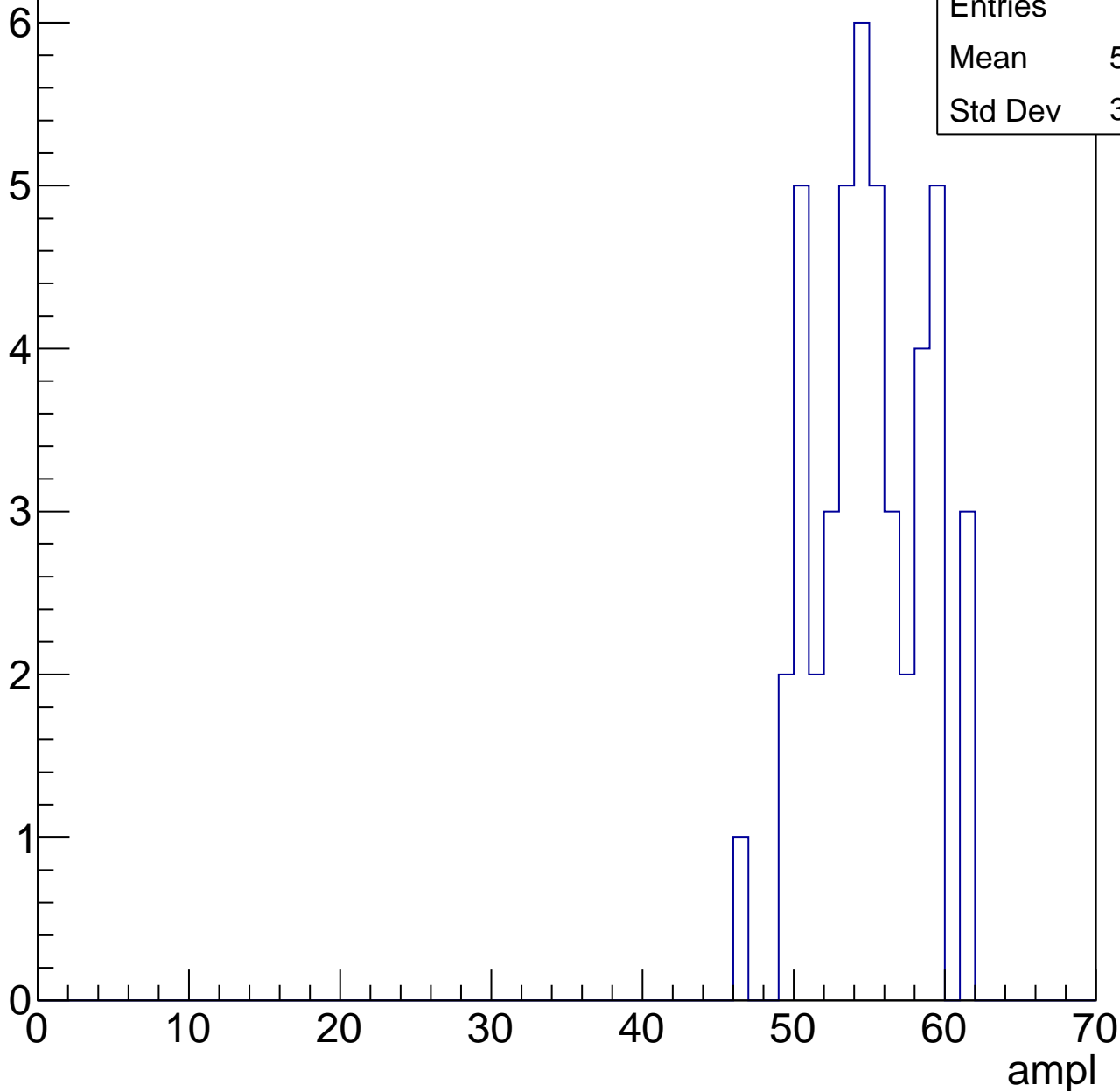
Entries	75
Mean	48.04
Std Dev	3.348

B1L103S, U5-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	54.52
Std Dev	3.574



B1L103S, U5-ch34, adc5

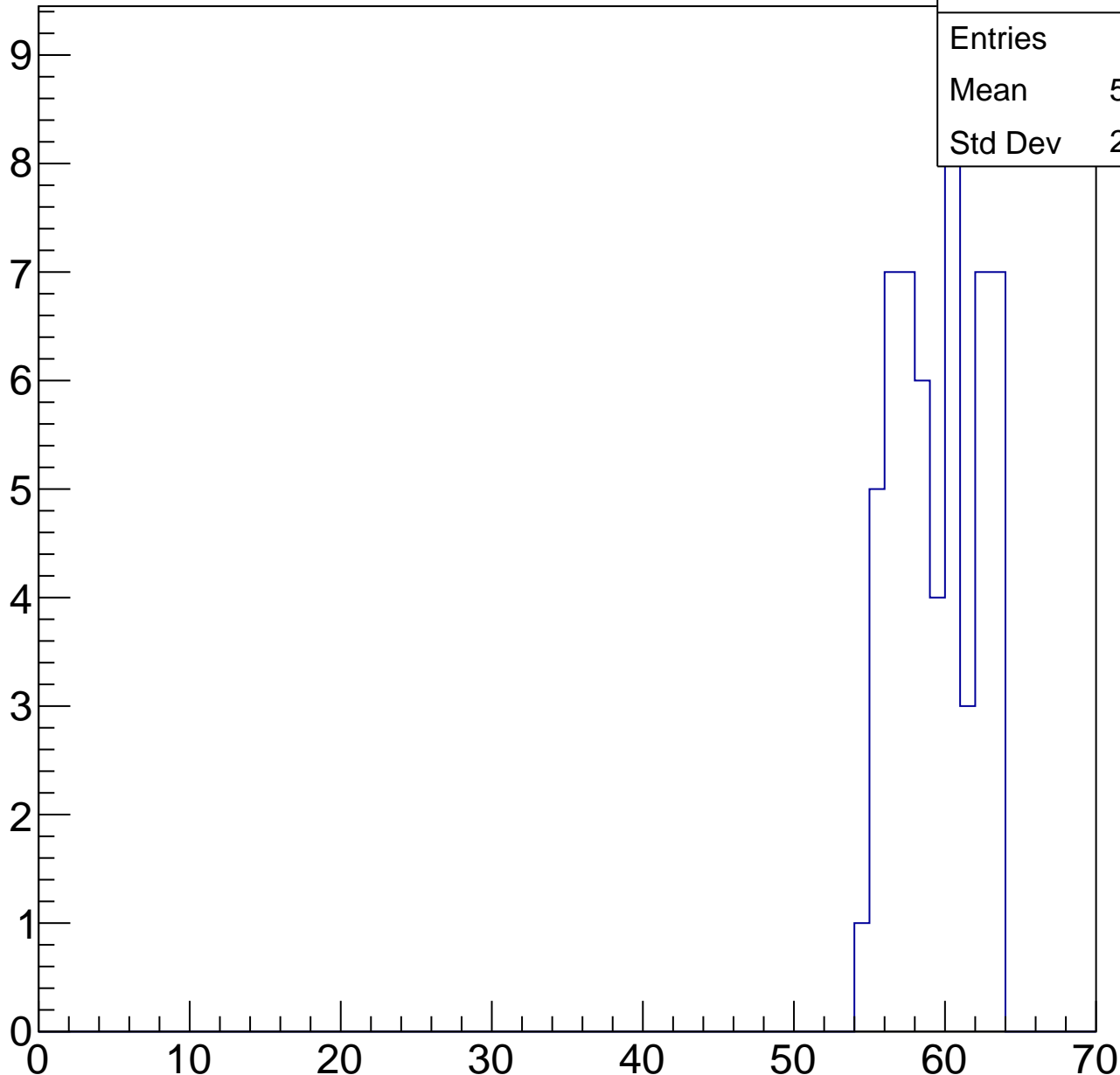
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.96
Std Dev	2.666

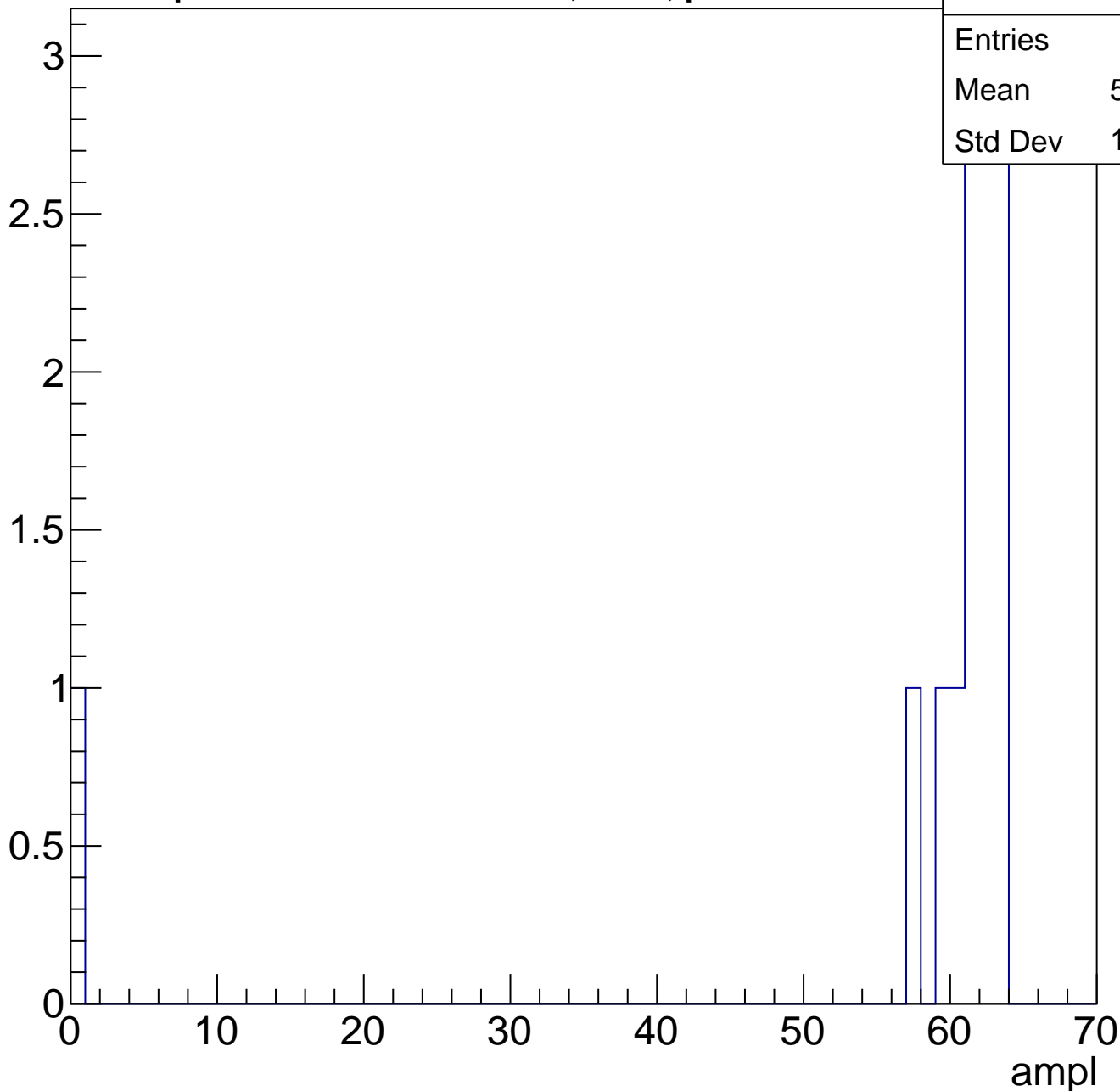
ampl



B1L103S, U5-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	13
Mean	56.46
Std Dev	16.38

B1L103S, U5-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch35, adc0

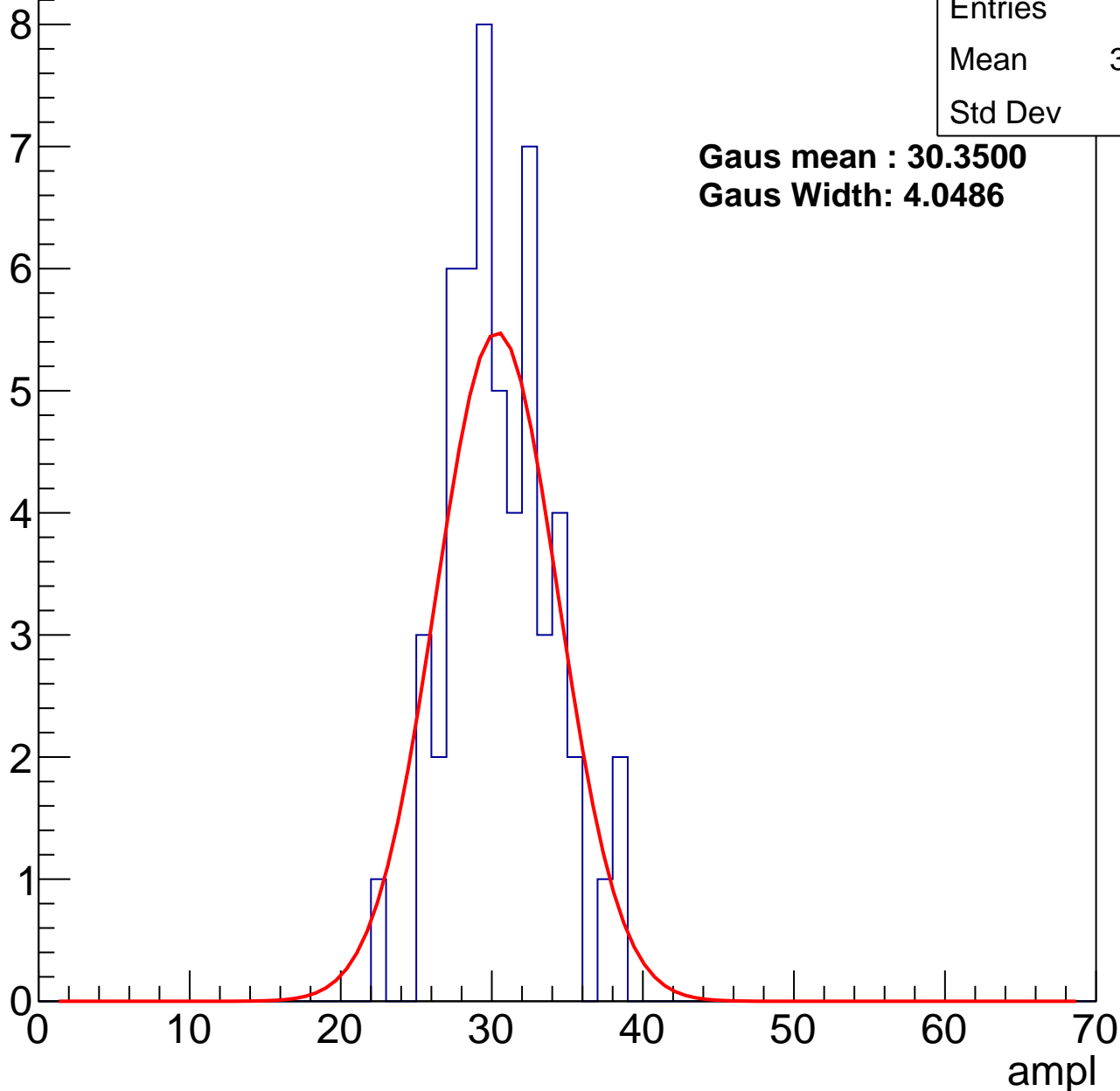
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	30.13
Std Dev	3.35

Gaus mean : 30.3500

Gaus Width: 4.0486



B1L103S, U5-ch35, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	88
Mean	36.75
Std Dev	3.788

Gaus mean : 37.2729

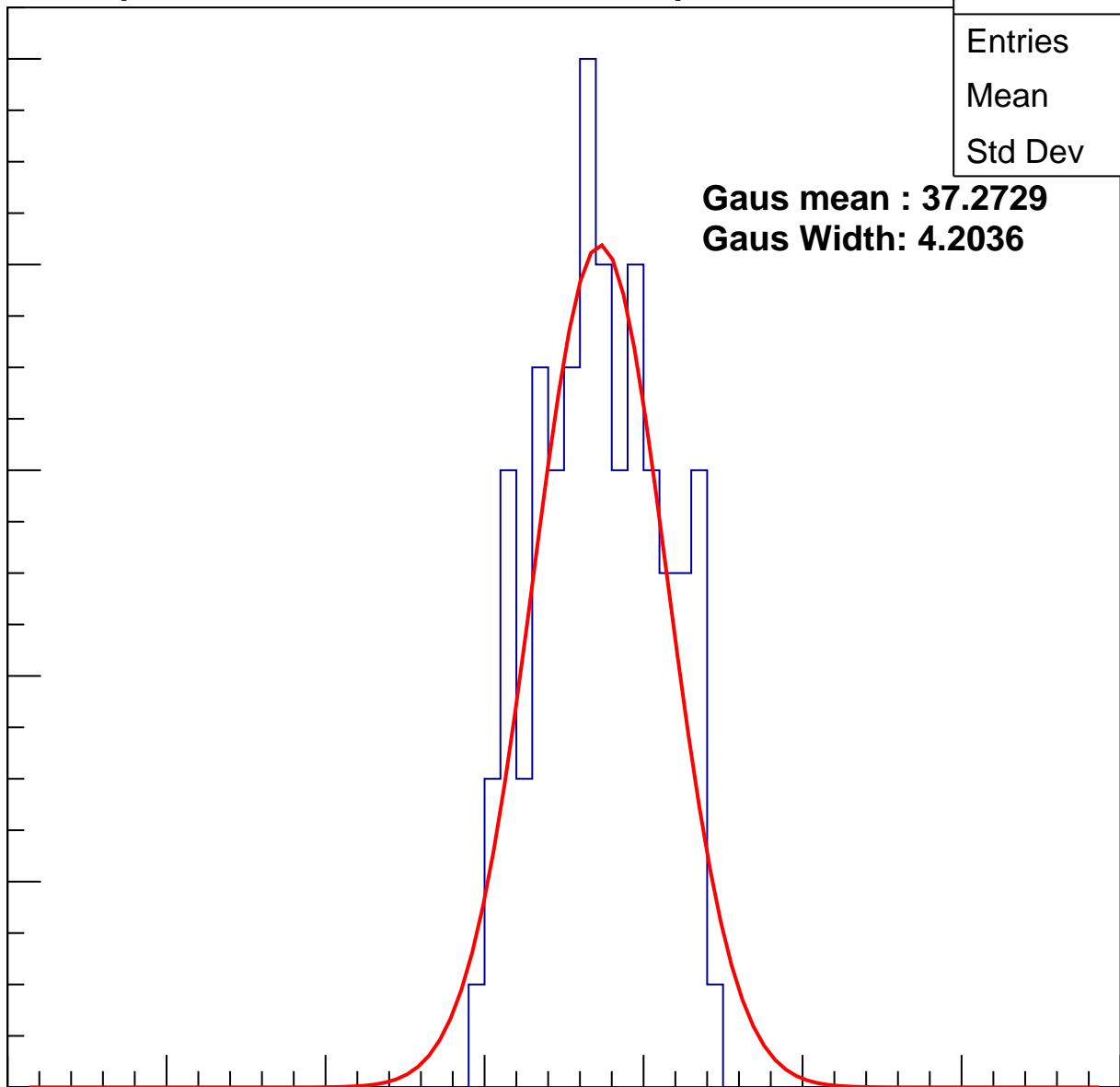
Gaus Width: 4.2036

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch35, adc2

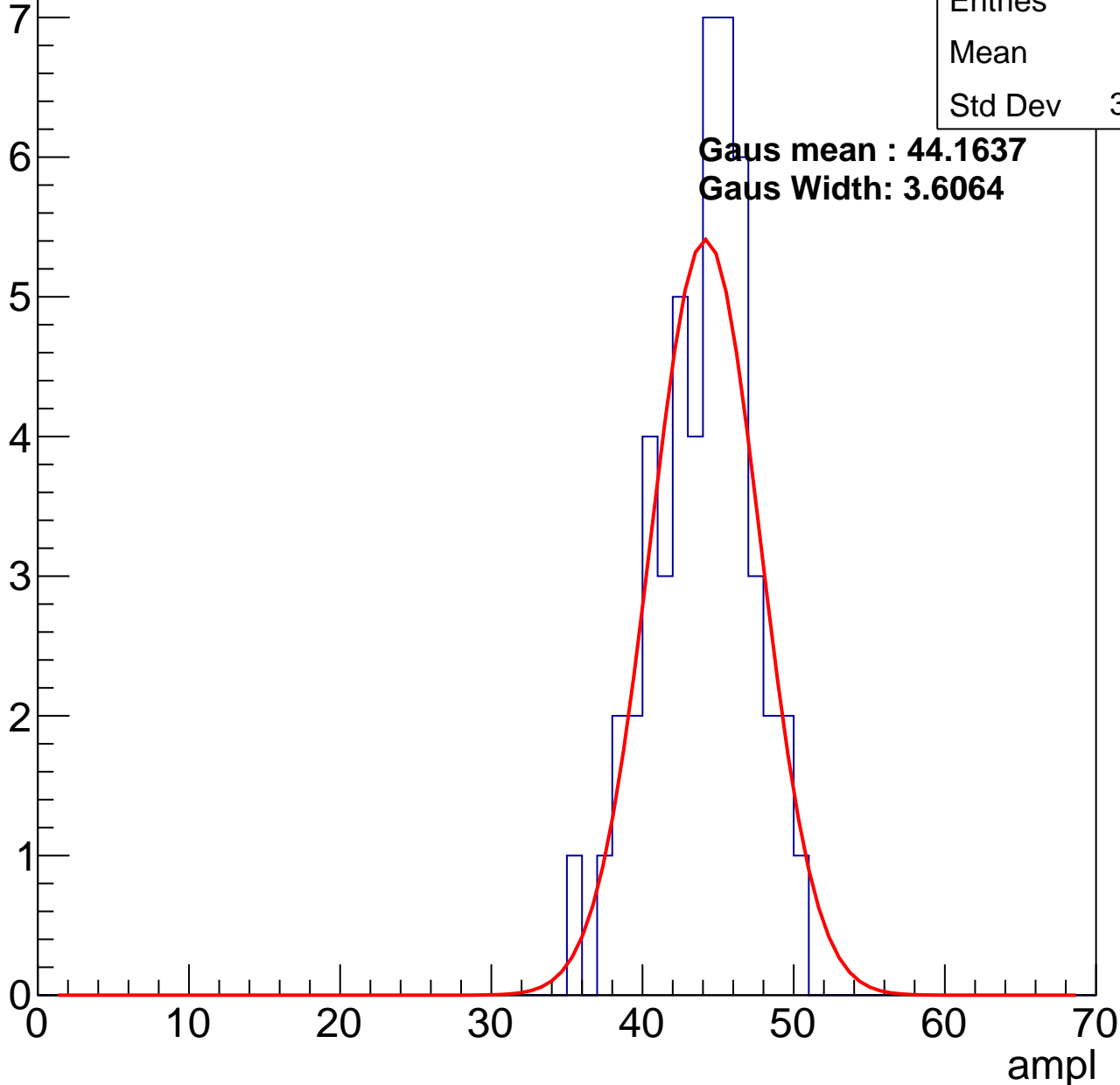
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	43.5
Std Dev	3.257

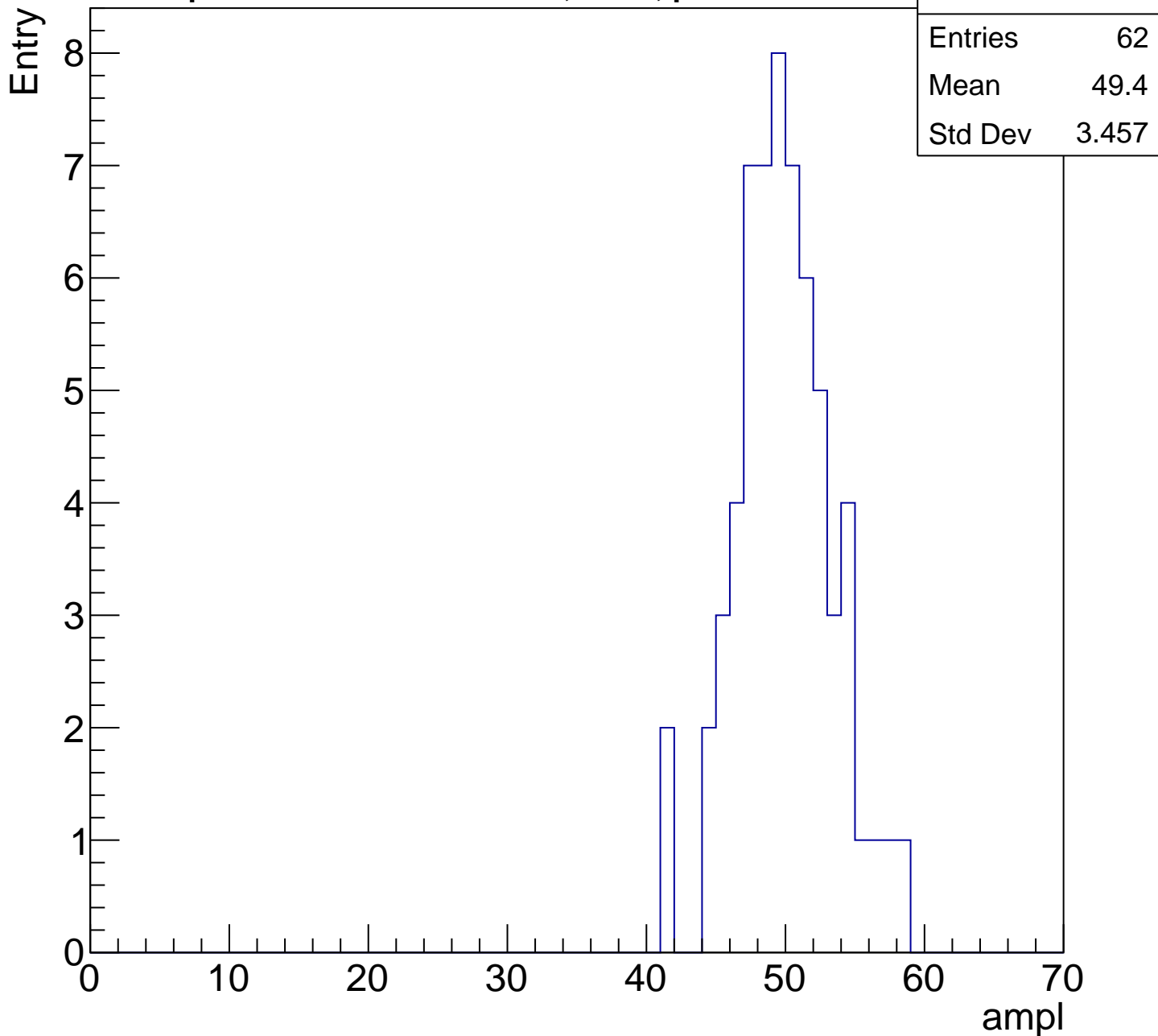
Gaus mean : 44.1637

Gaus Width: 3.6064



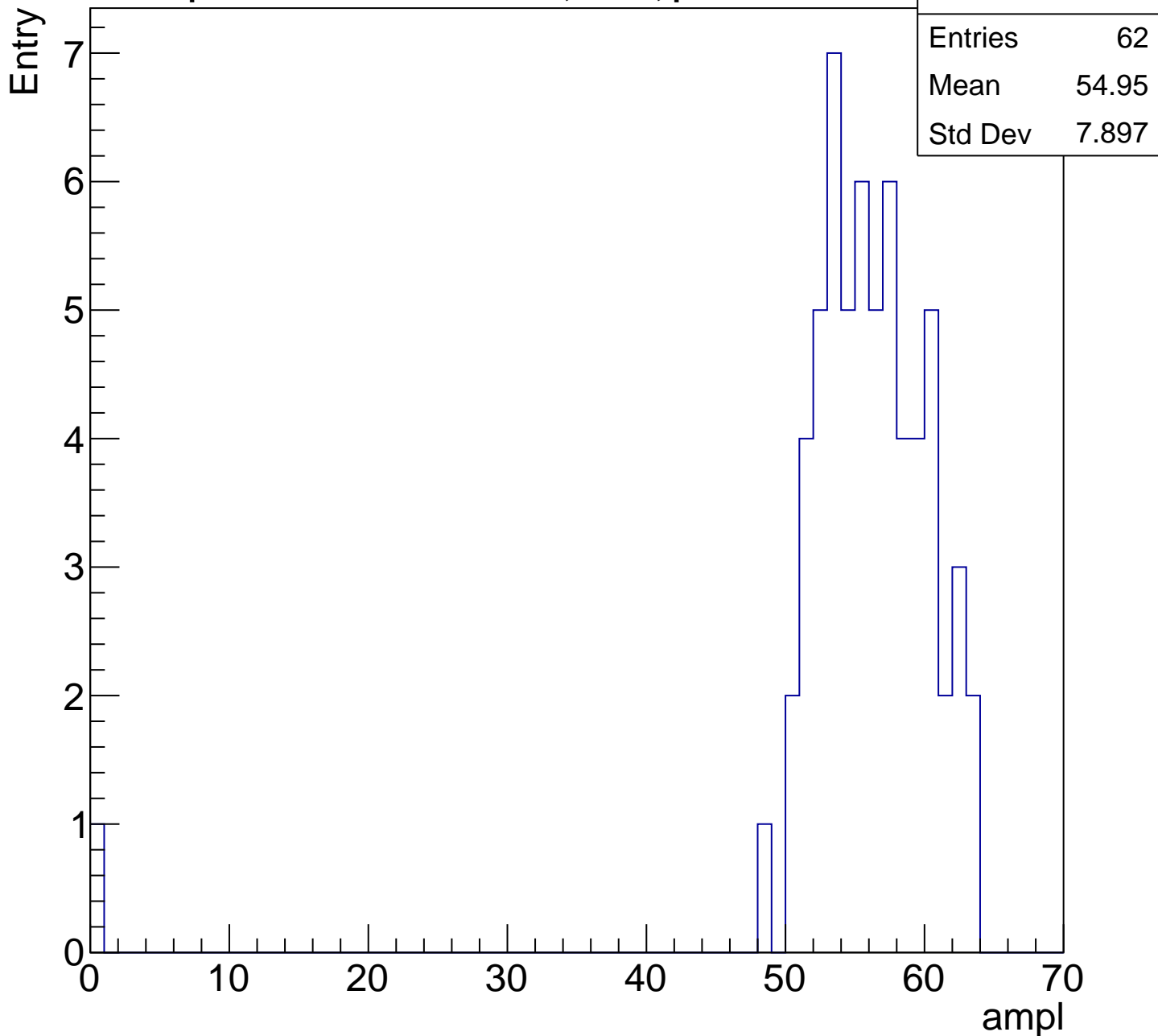
B1L103S, U5-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

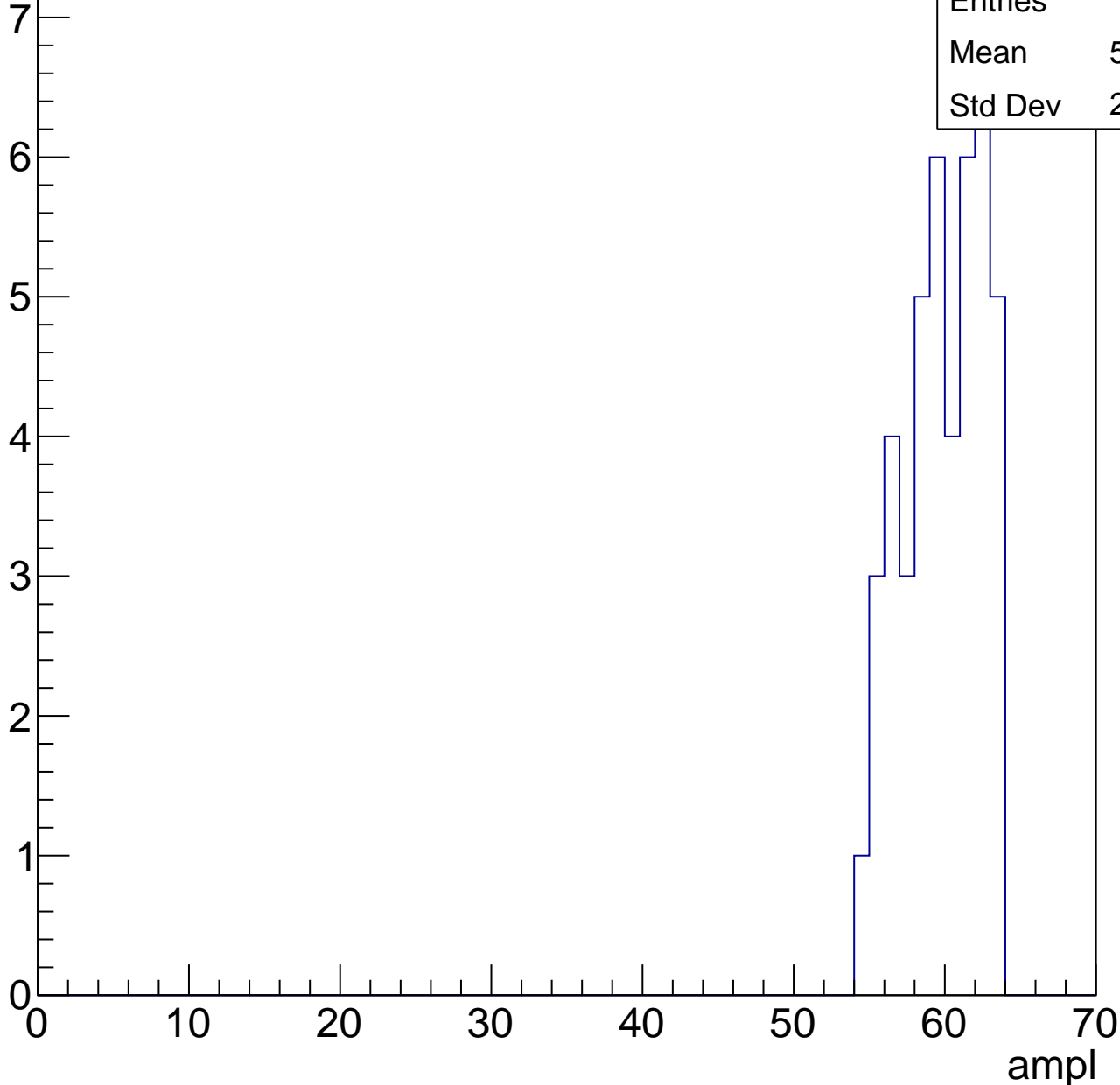


B1L103S, U5-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

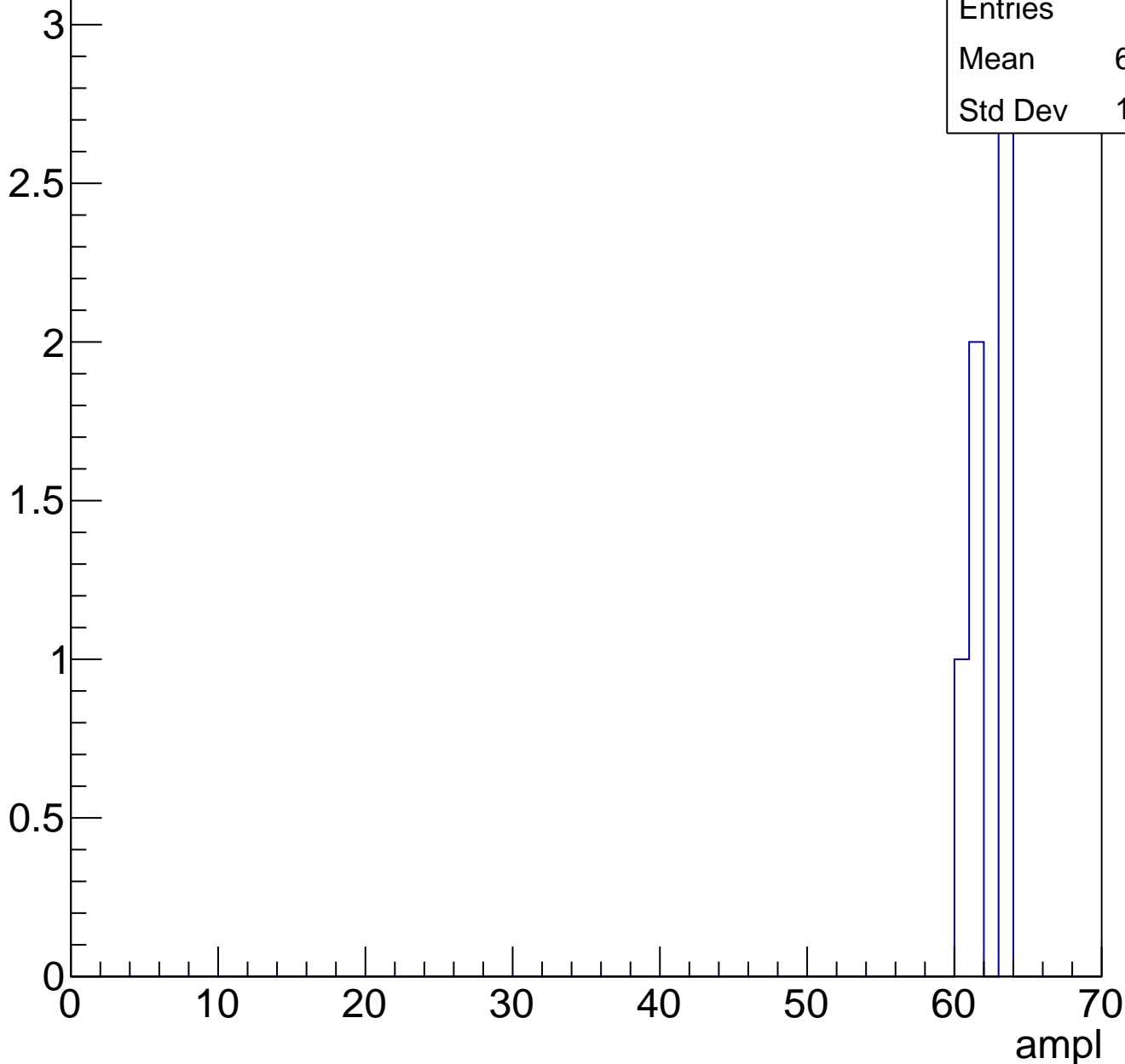
Entries	44
Mean	59.39
Std Dev	2.569



B1L103S, U5-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch36, adc0

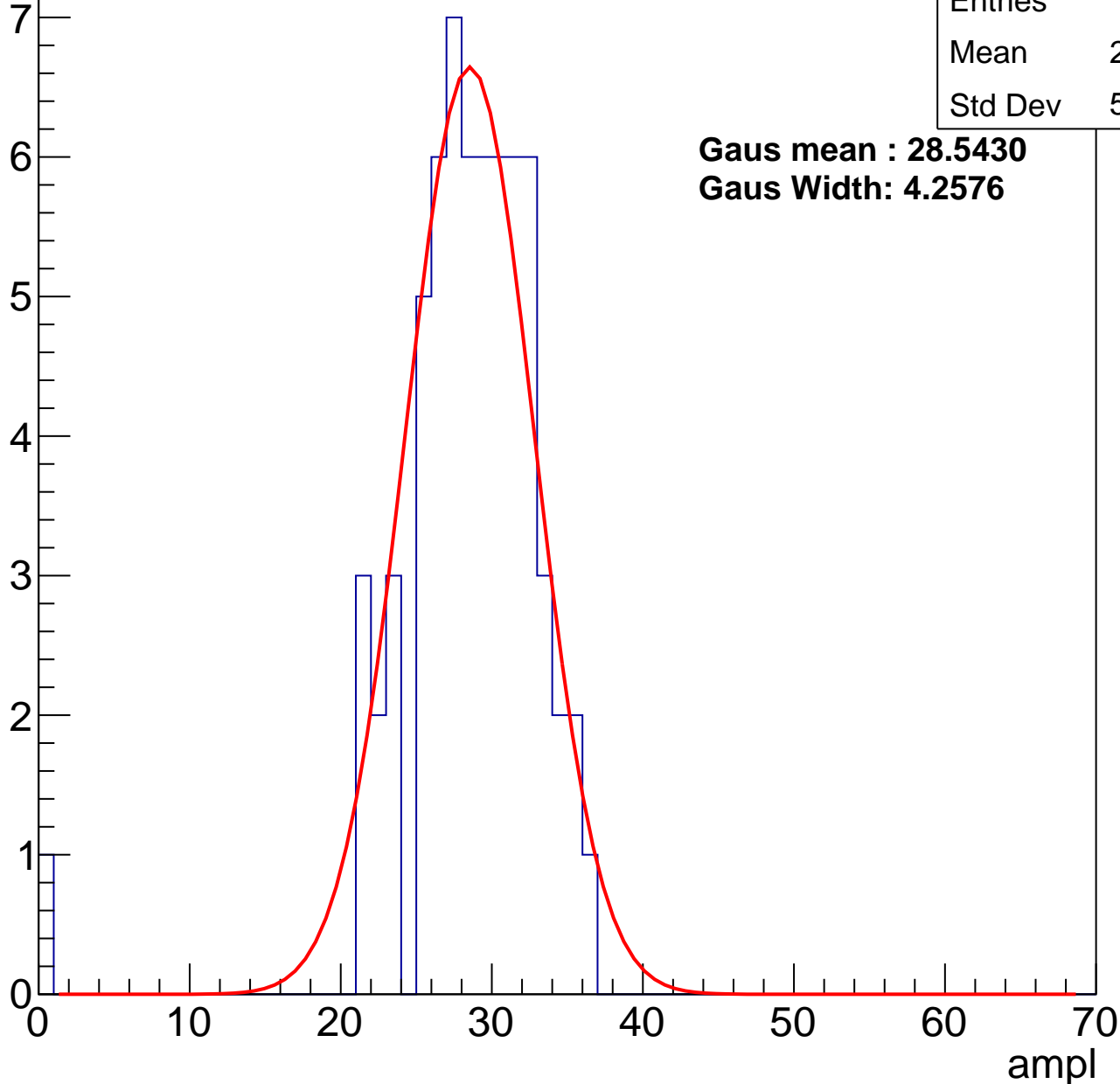
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	27.98
Std Dev	5.028

Gaus mean : 28.5430

Gaus Width: 4.2576



B1L103S, U5-ch36, adc1

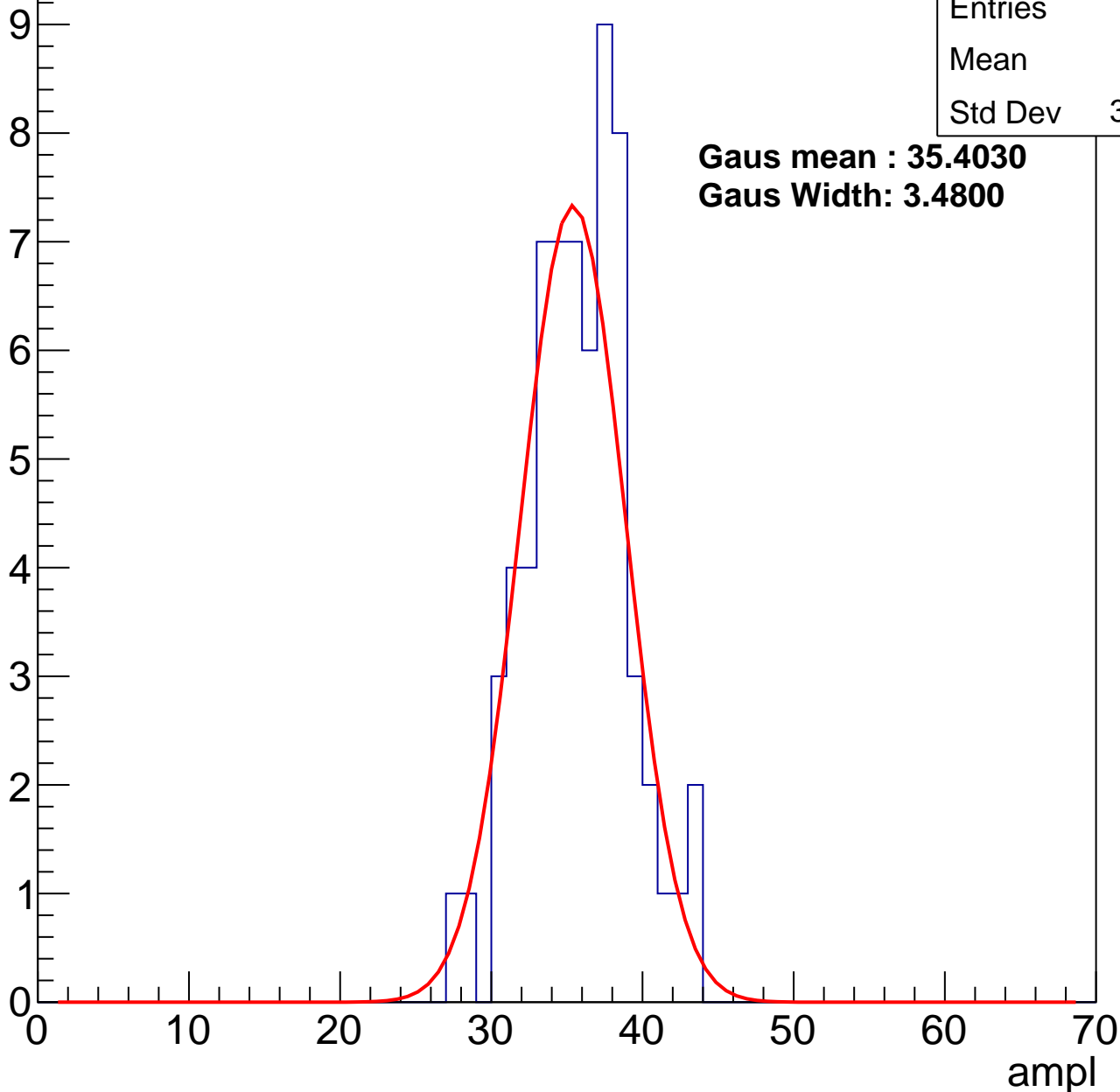
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.3
Std Dev	3.353

Gaus mean : 35.4030

Gaus Width: 3.4800



B1L103S, U5-ch36, adc2

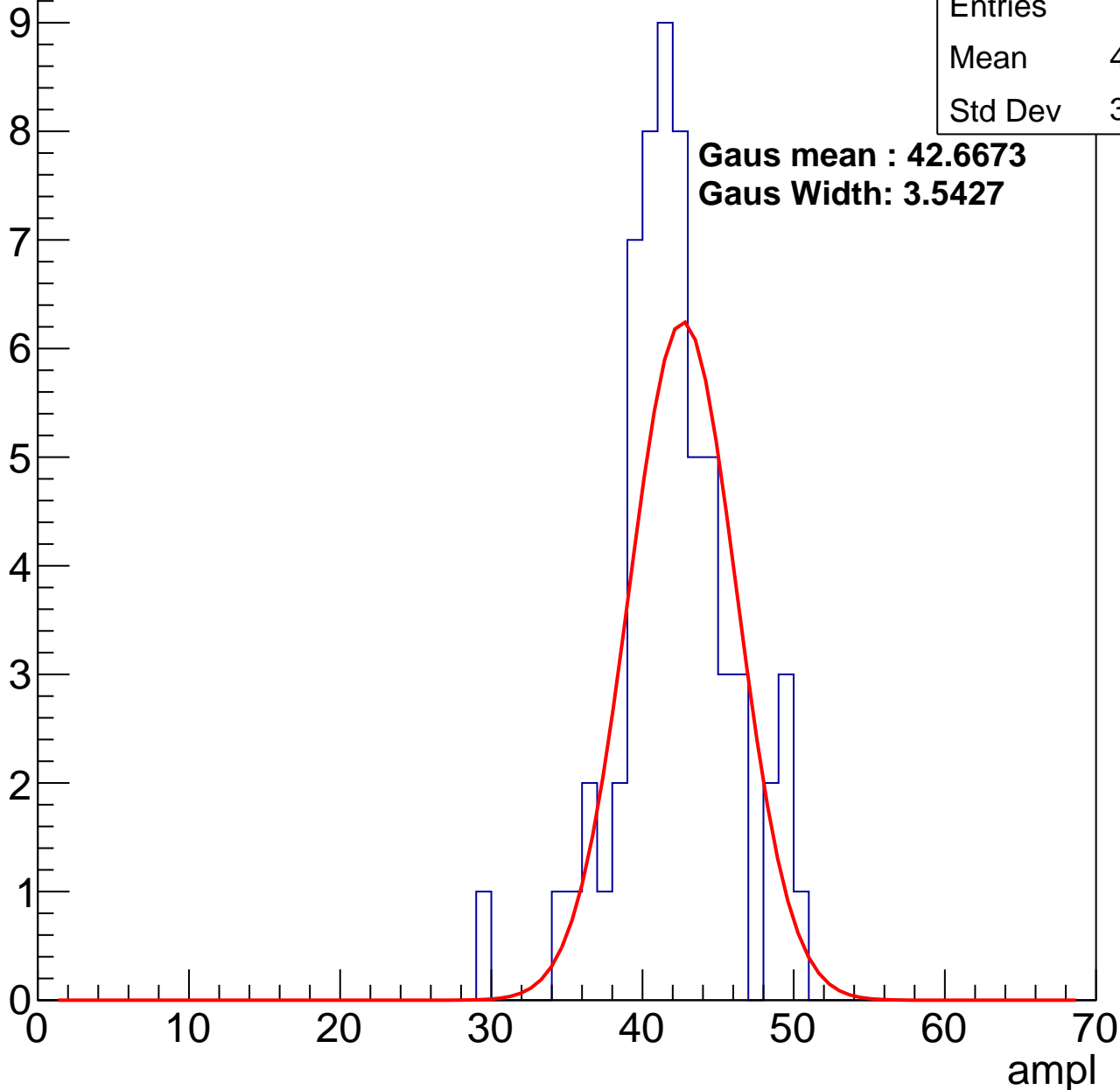
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.65
Std Dev	3.789

Gaus mean : 42.6673

Gaus Width: 3.5427

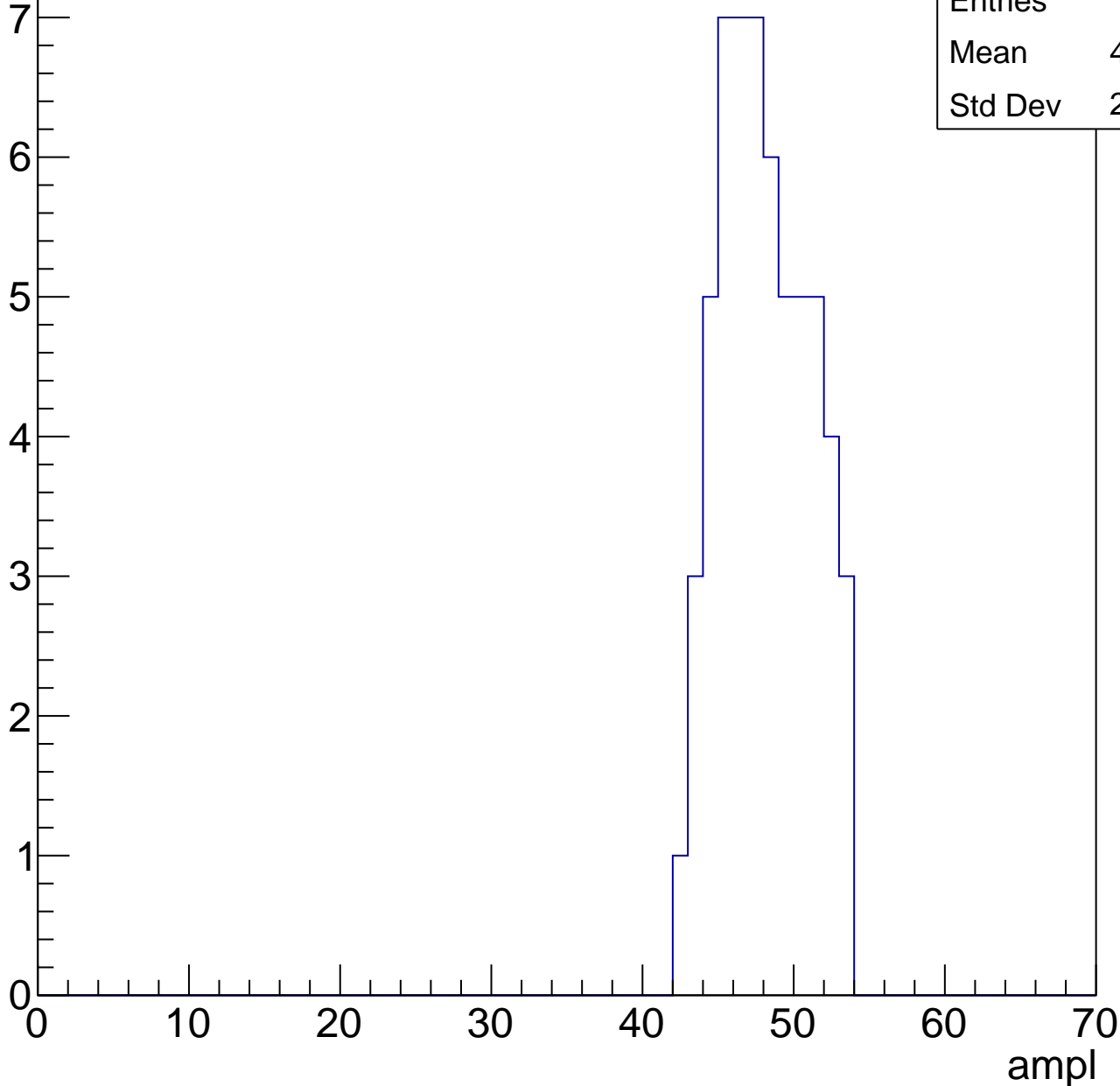


B1L103S, U5-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	47.62
Std Dev	2.906

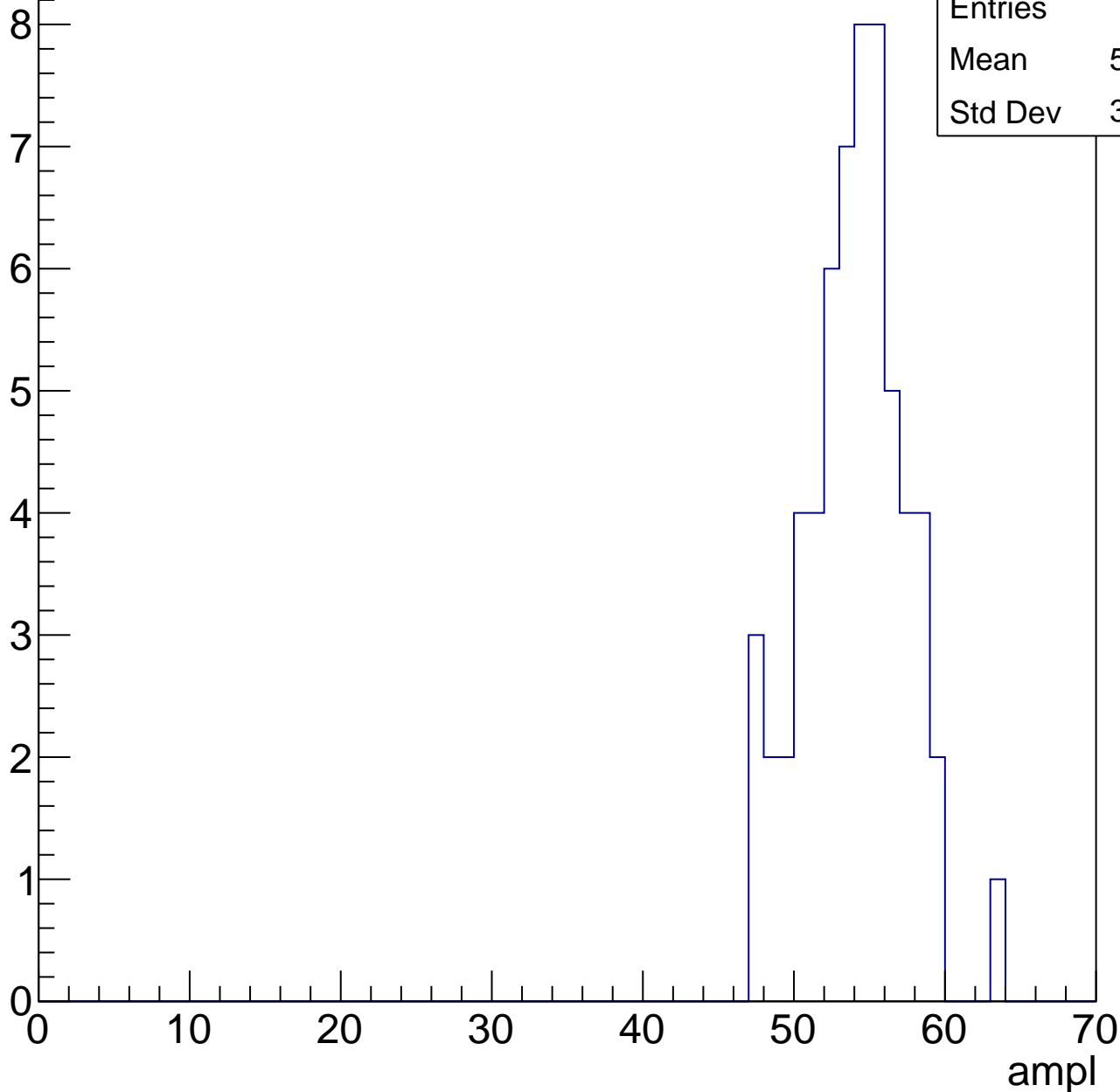


B1L103S, U5-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	53.58
Std Dev	3.288

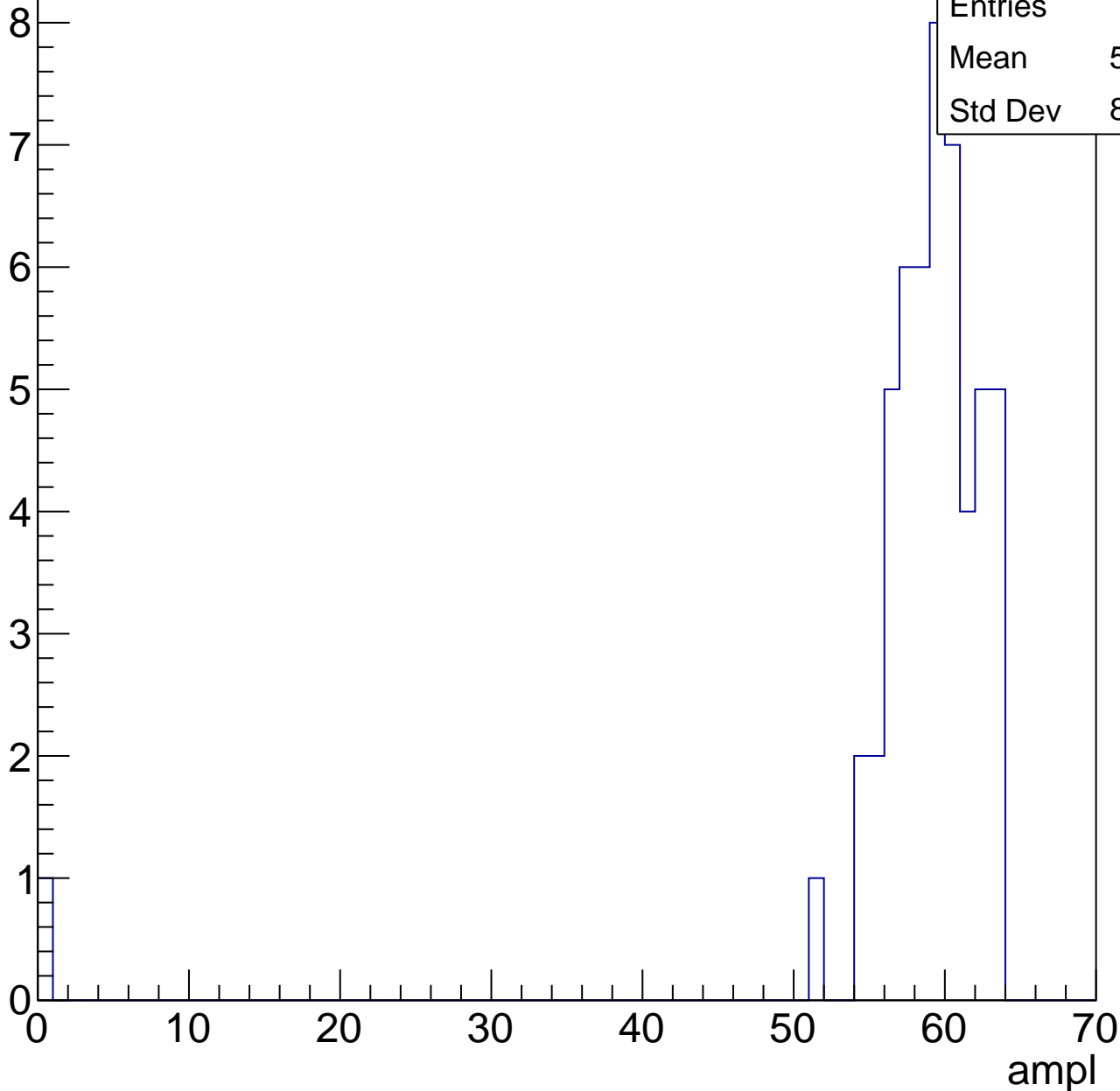


B1L103S, U5-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	57.69
Std Dev	8.505



B1L103S, U5-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

6

5

4

3

2

1

0

Entries

17

Mean

61.47

Std Dev

0.9772

ampl

0

10

20

30

40

50

60

70

B1L103S, U5-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch37, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	26.99
Std Dev	4.371

Gaus mean : 27.8396

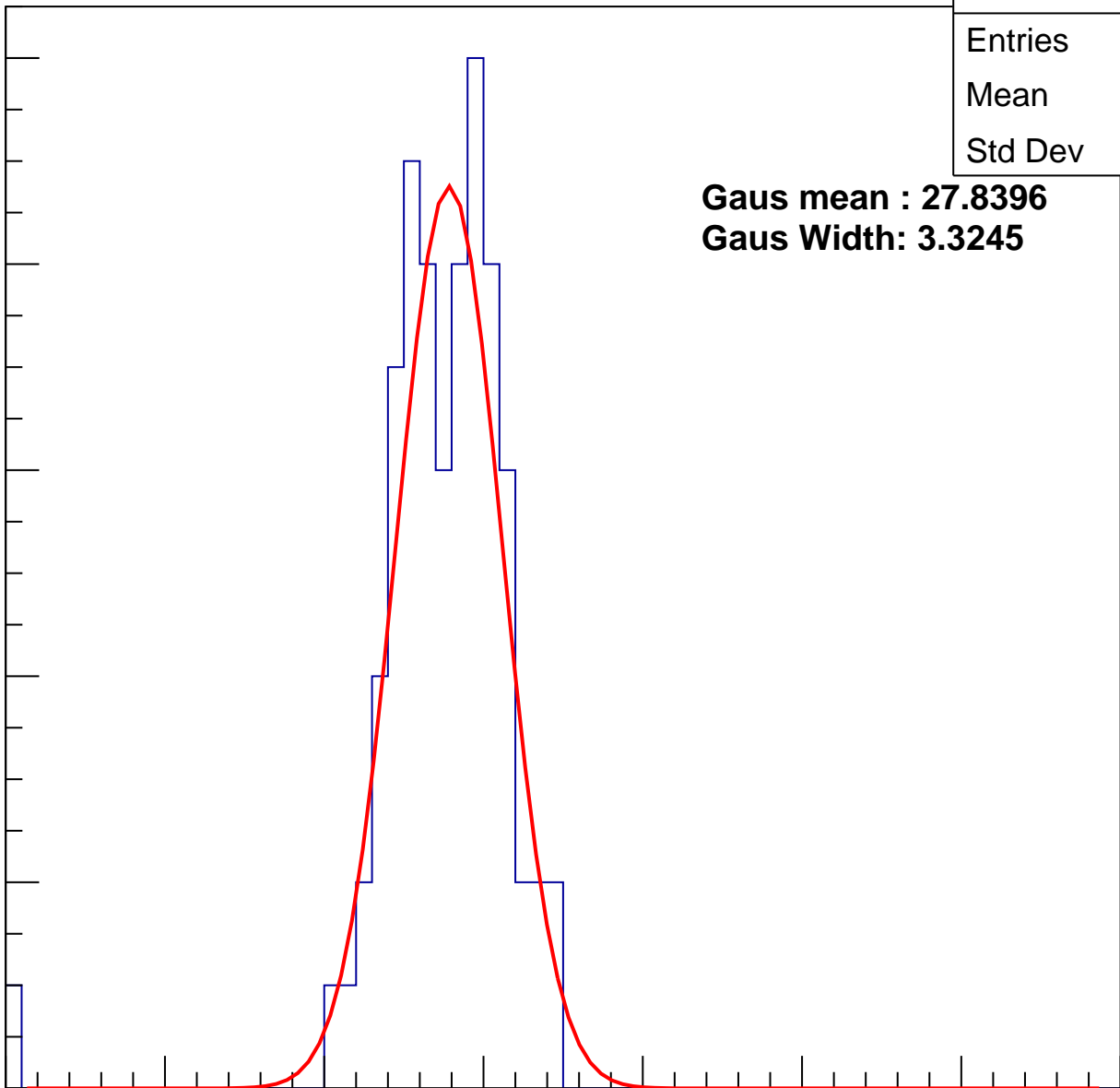
Gaus Width: 3.3245

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch37, adc1

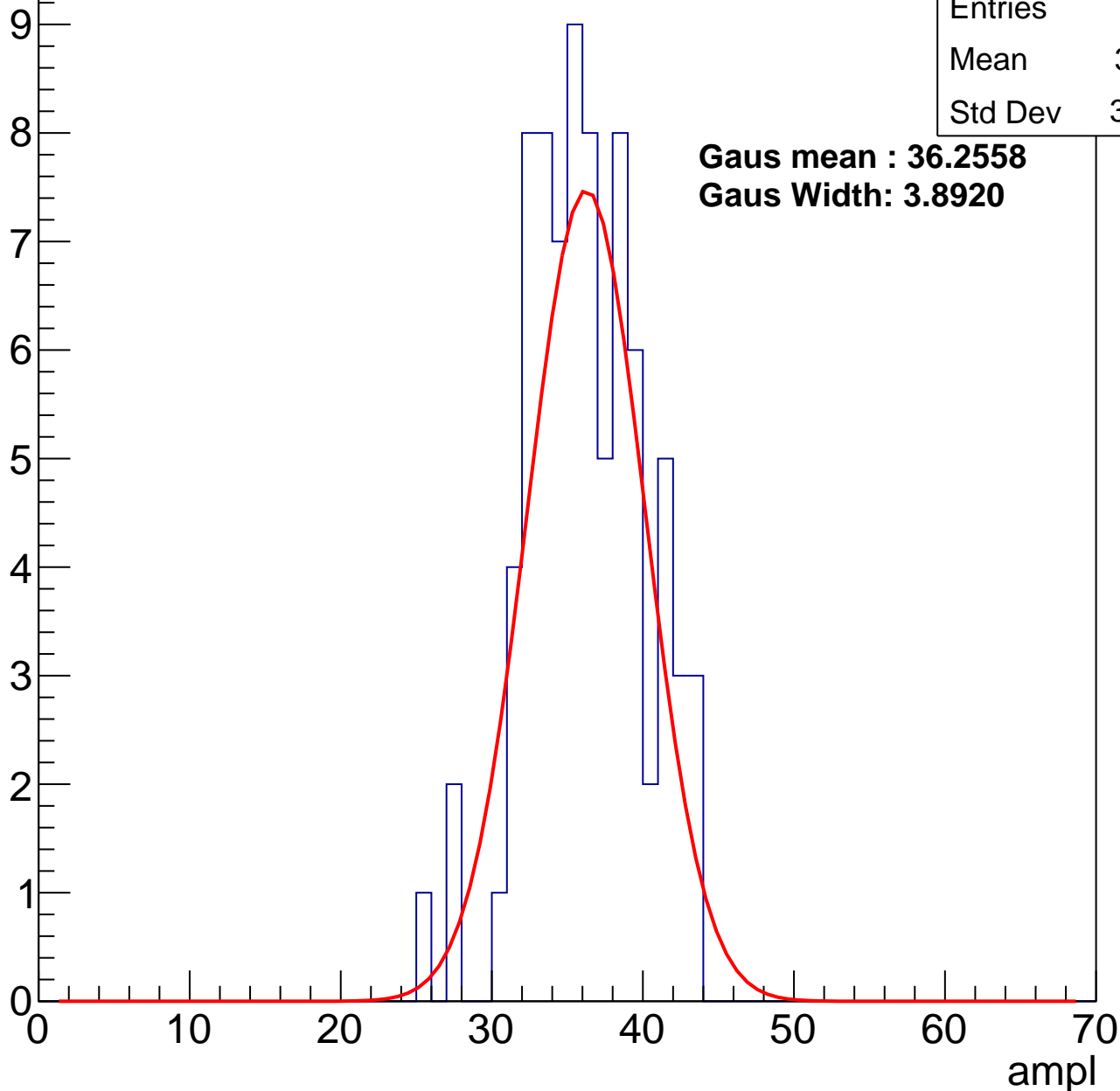
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	35.71
Std Dev	3.795

Gaus mean : 36.2558

Gaus Width: 3.8920



B1L103S, U5-ch37, adc2

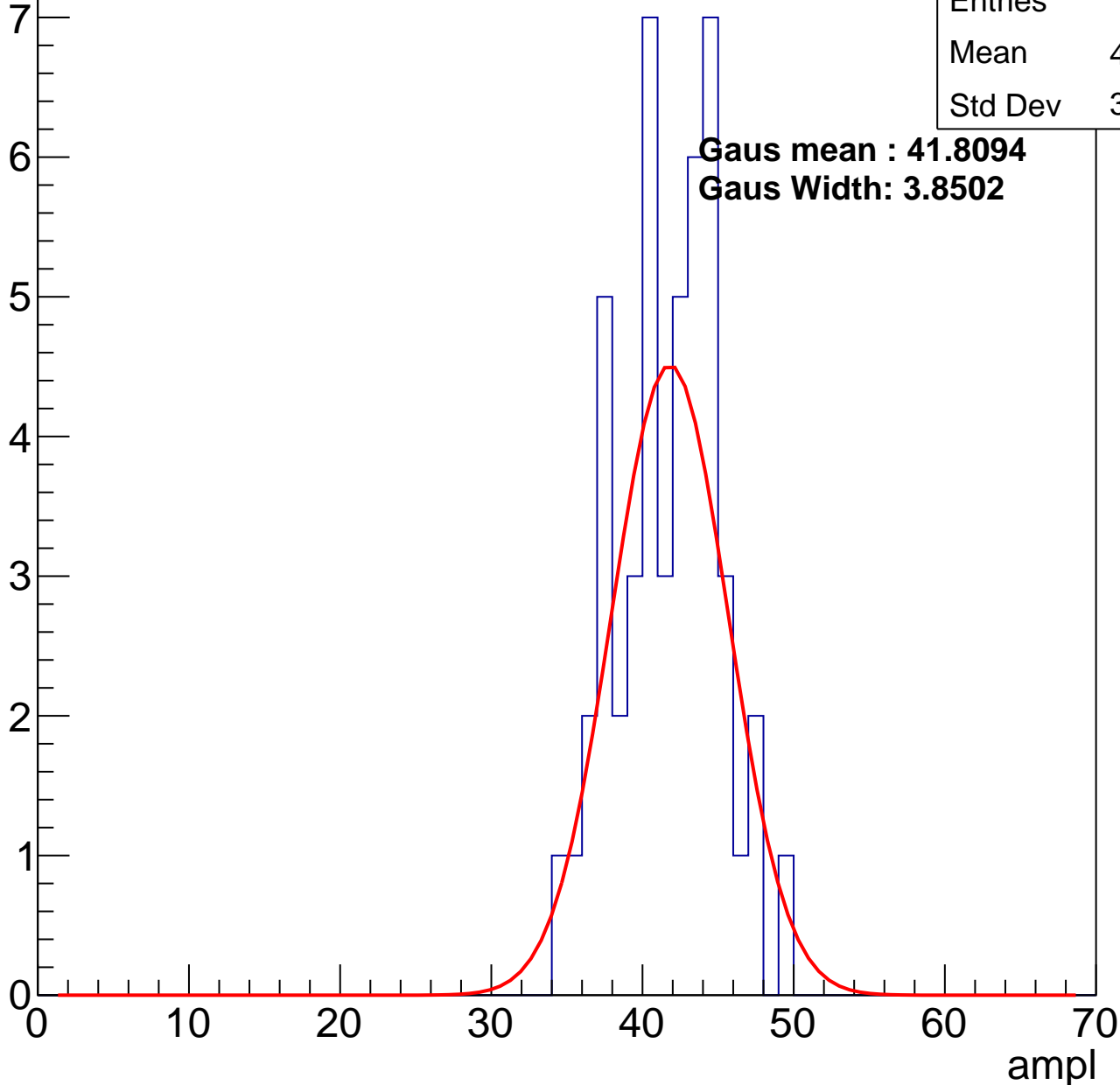
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	41.27
Std Dev	3.349

Gaus mean : 41.8094

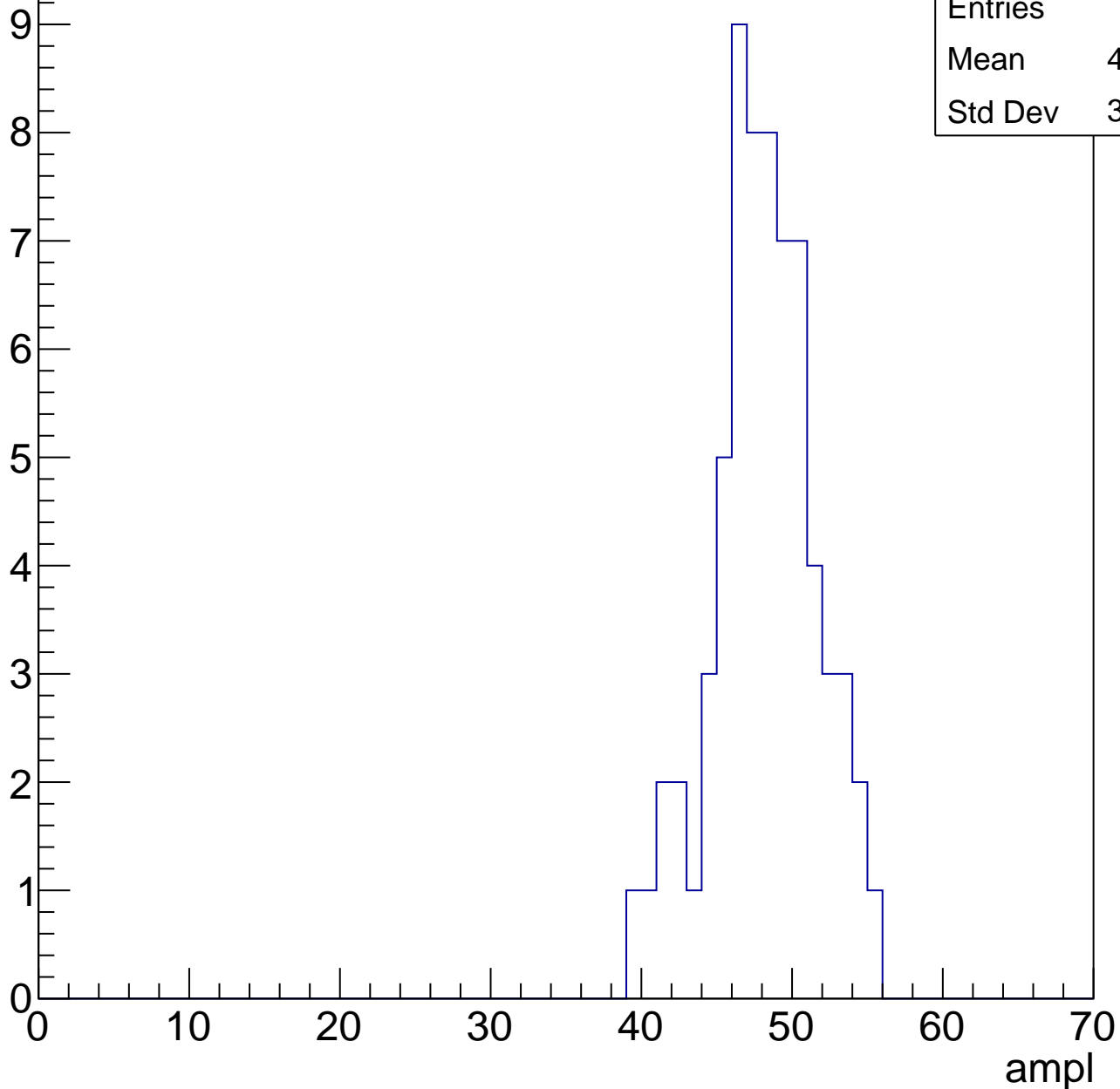
Gaus Width: 3.8502



B1L103S, U5-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



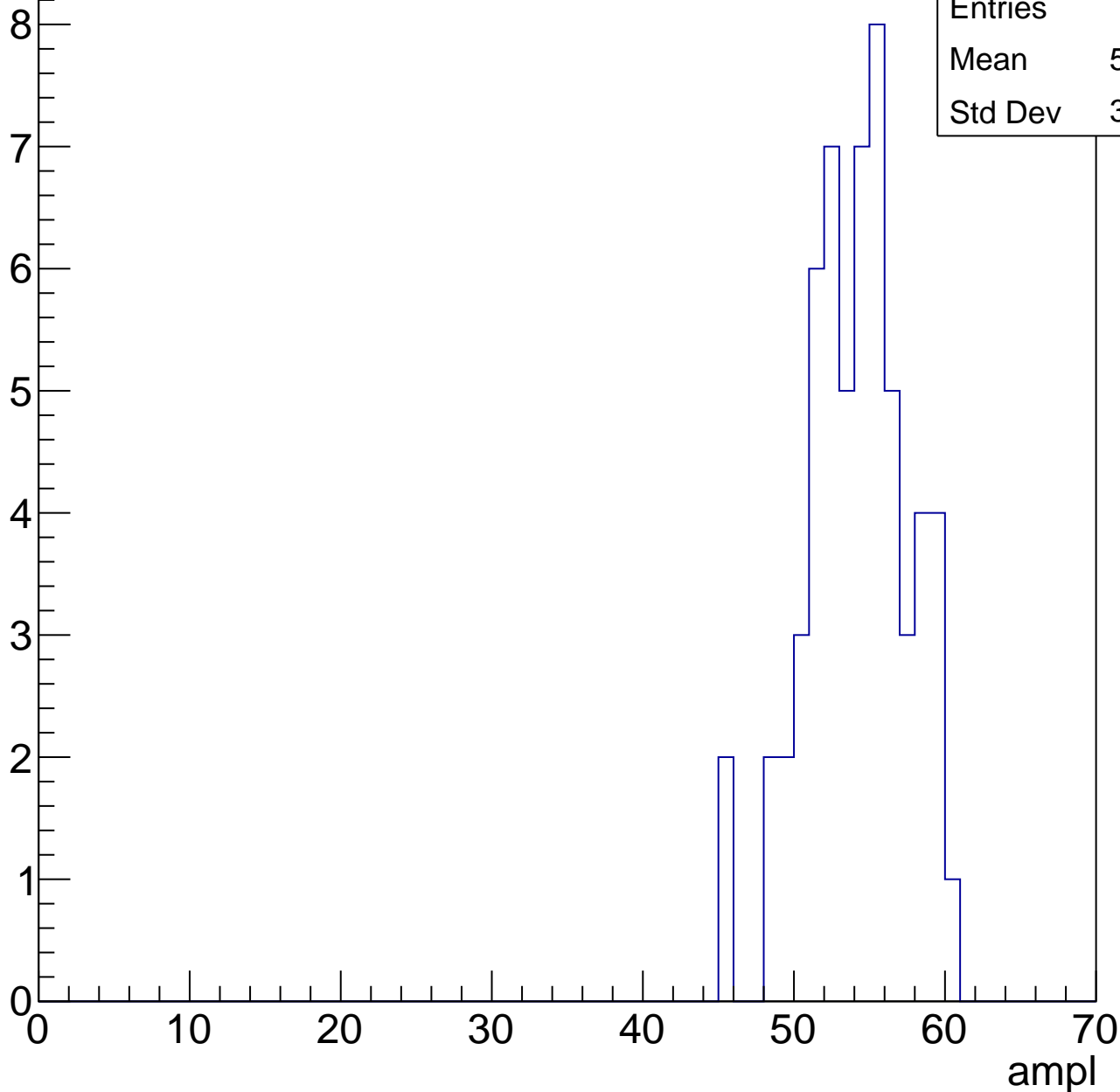
Entries	67
Mean	47.67
Std Dev	3.422

B1L103S, U5-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

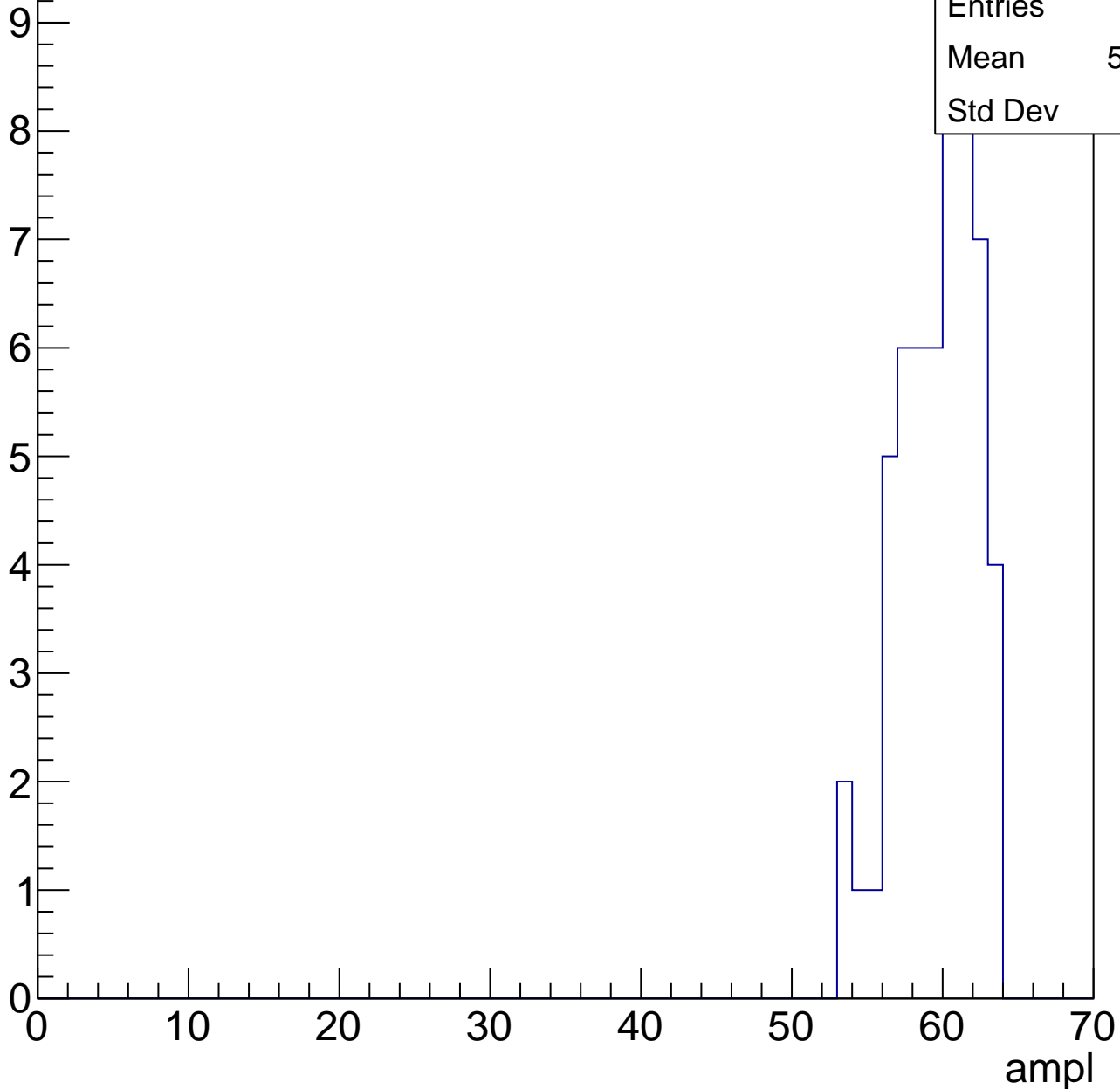
Entries	59
Mean	53.66
Std Dev	3.363



B1L103S, U5-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

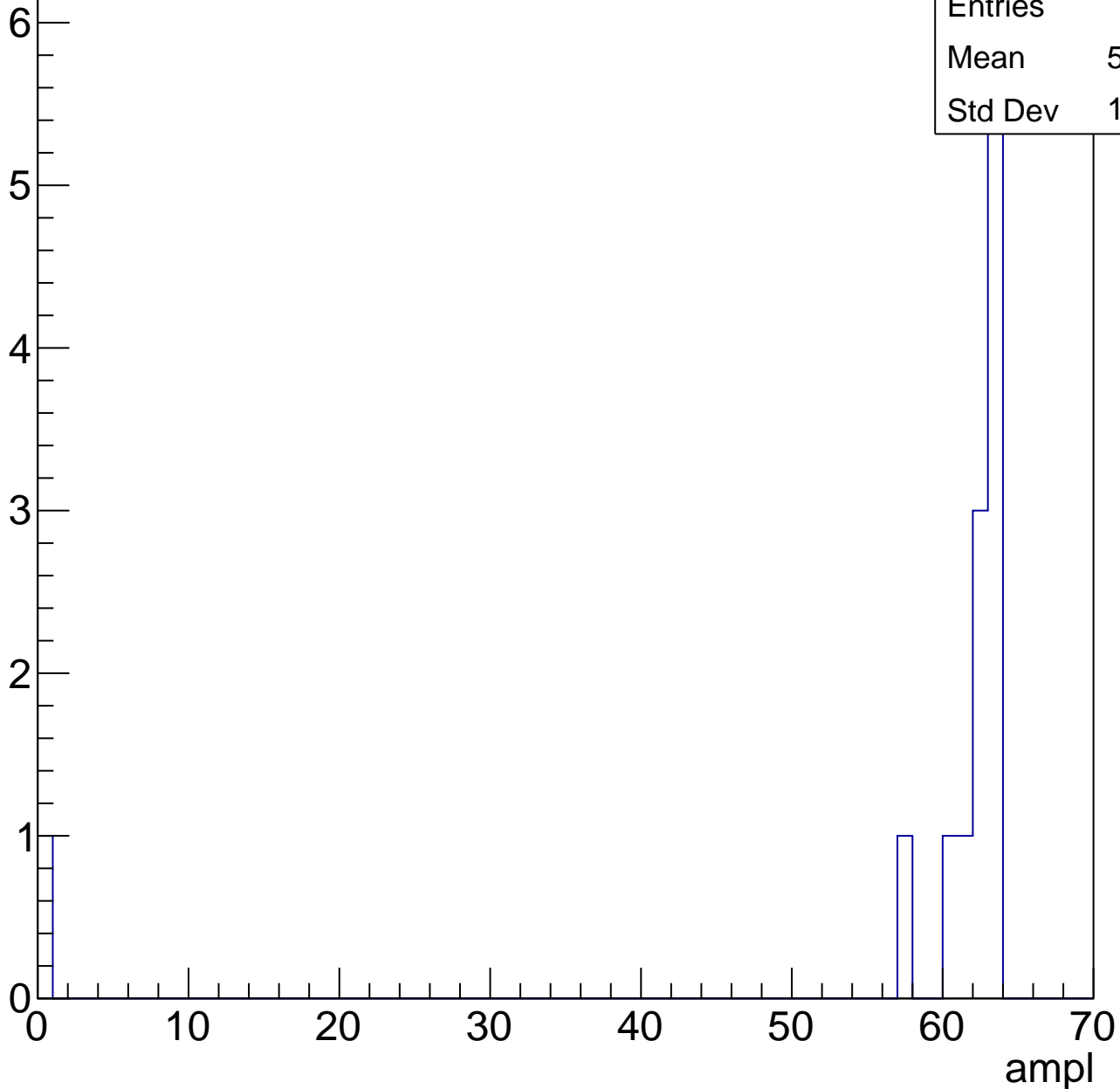


B1L103S, U5-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	57.08
Std Dev	16.56



B1L103S, U5-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch38, adc0

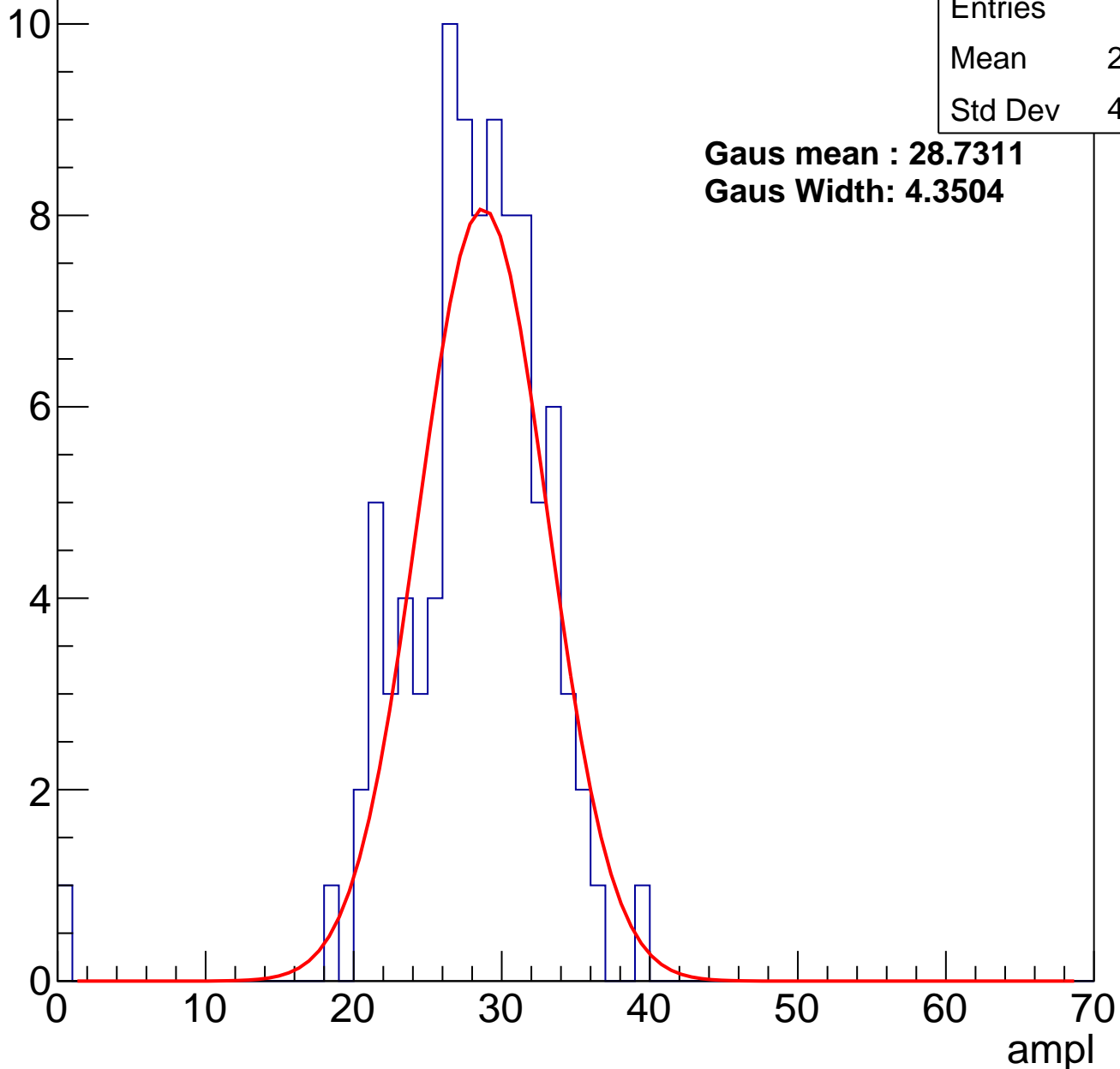
calib_packv5_042523_0143.root, FC#7, port C2

Entries	93
Mean	27.68
Std Dev	4.997

Gaus mean : 28.7311

Gaus Width: 4.3504

Entry



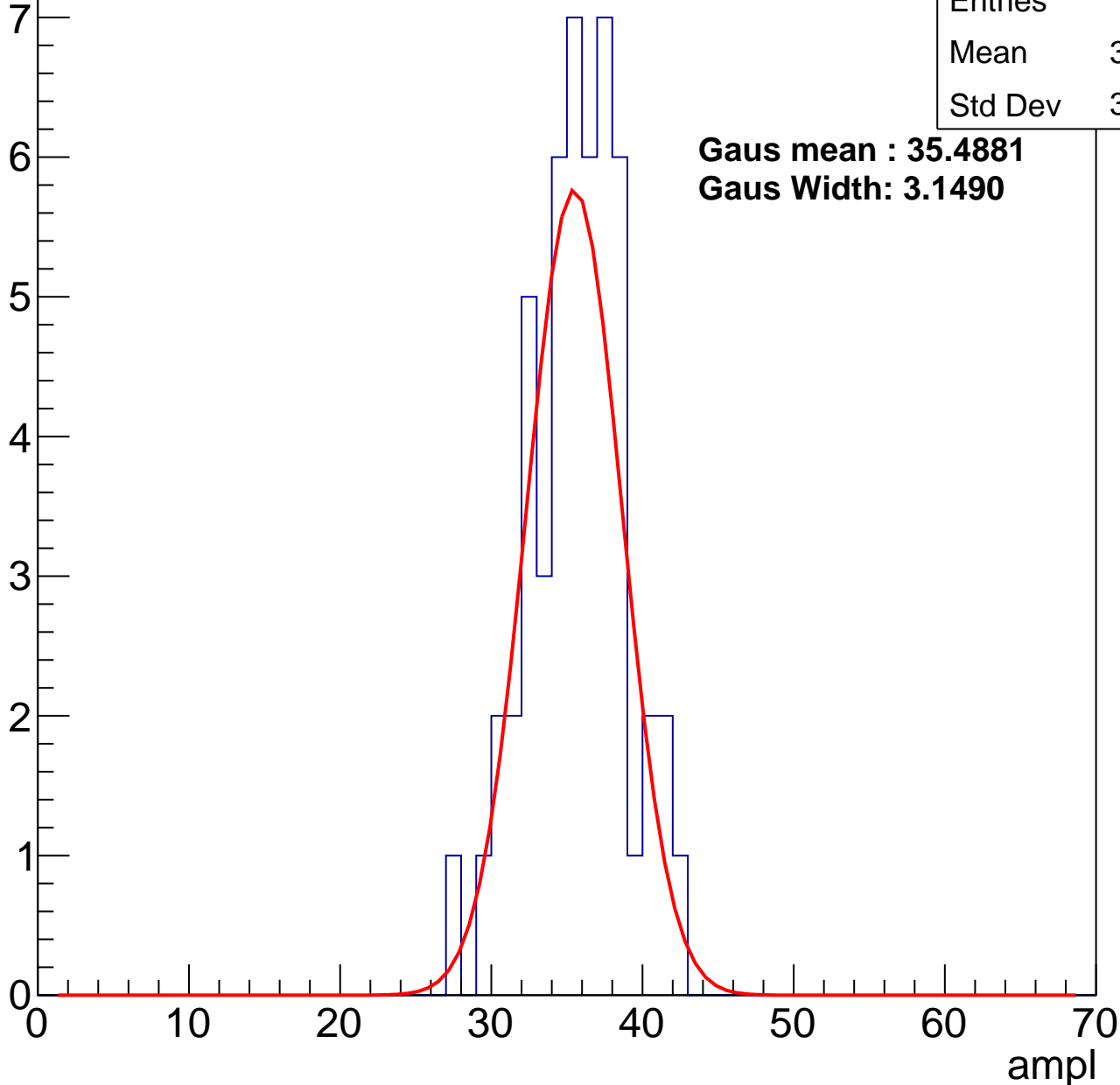
B1L103S, U5-ch38, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	35.23
Std Dev	3.154

Gaus mean : 35.4881
Gaus Width: 3.1490



B1L103S, U5-ch38, adc2

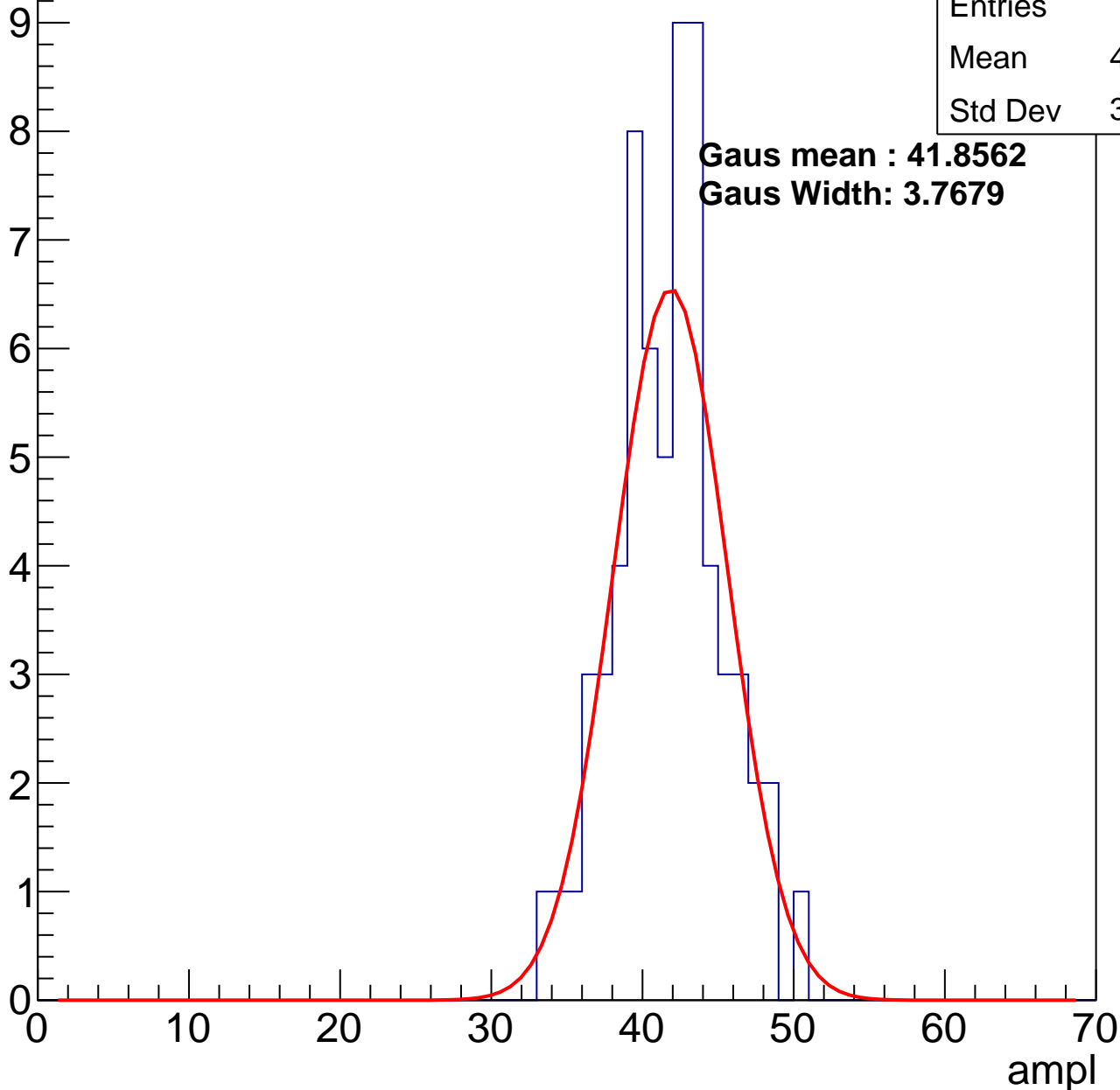
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.29
Std Dev	3.498

Gaus mean : 41.8562

Gaus Width: 3.7679

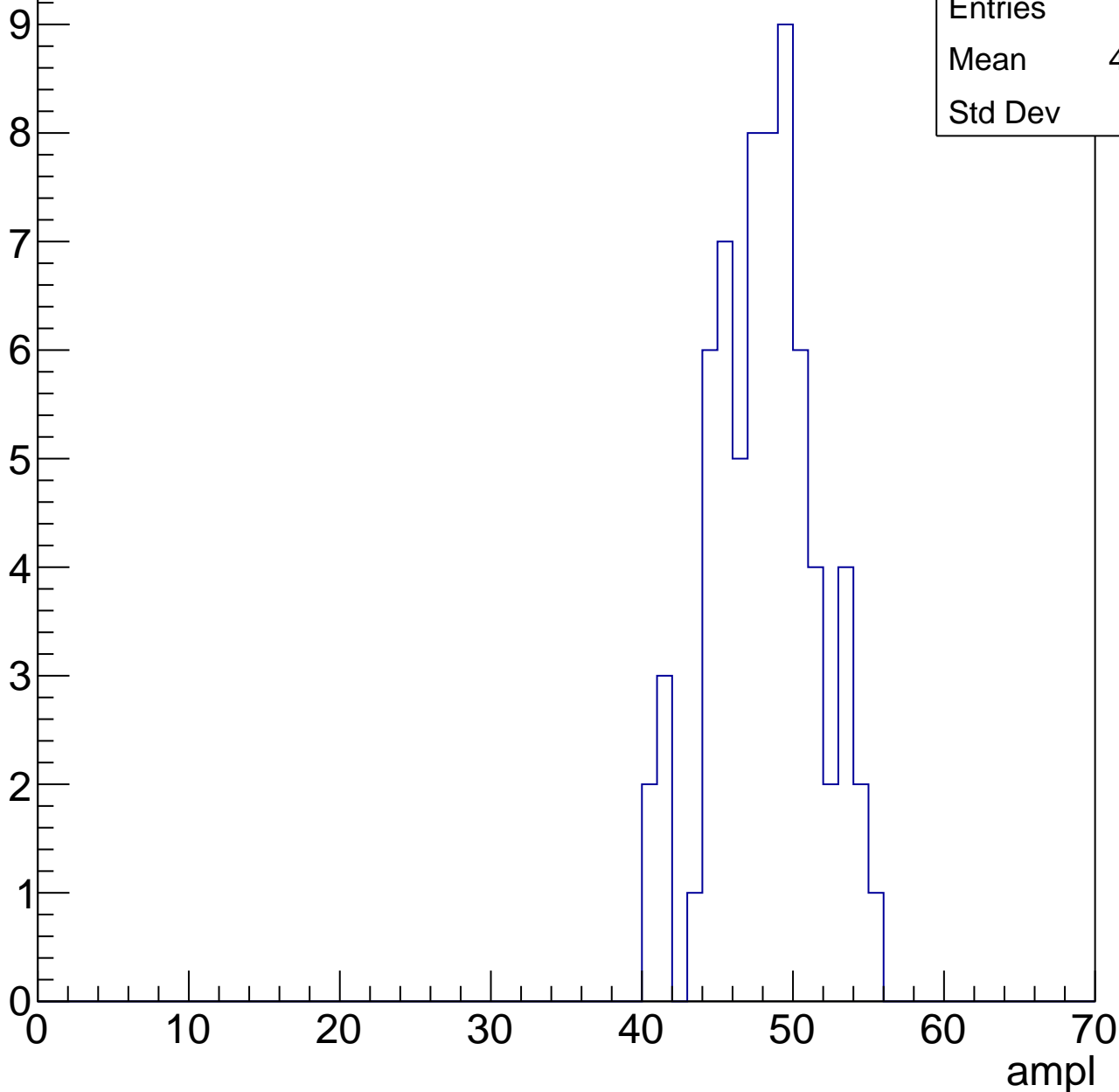


B1L103S, U5-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	47.63
Std Dev	3.43

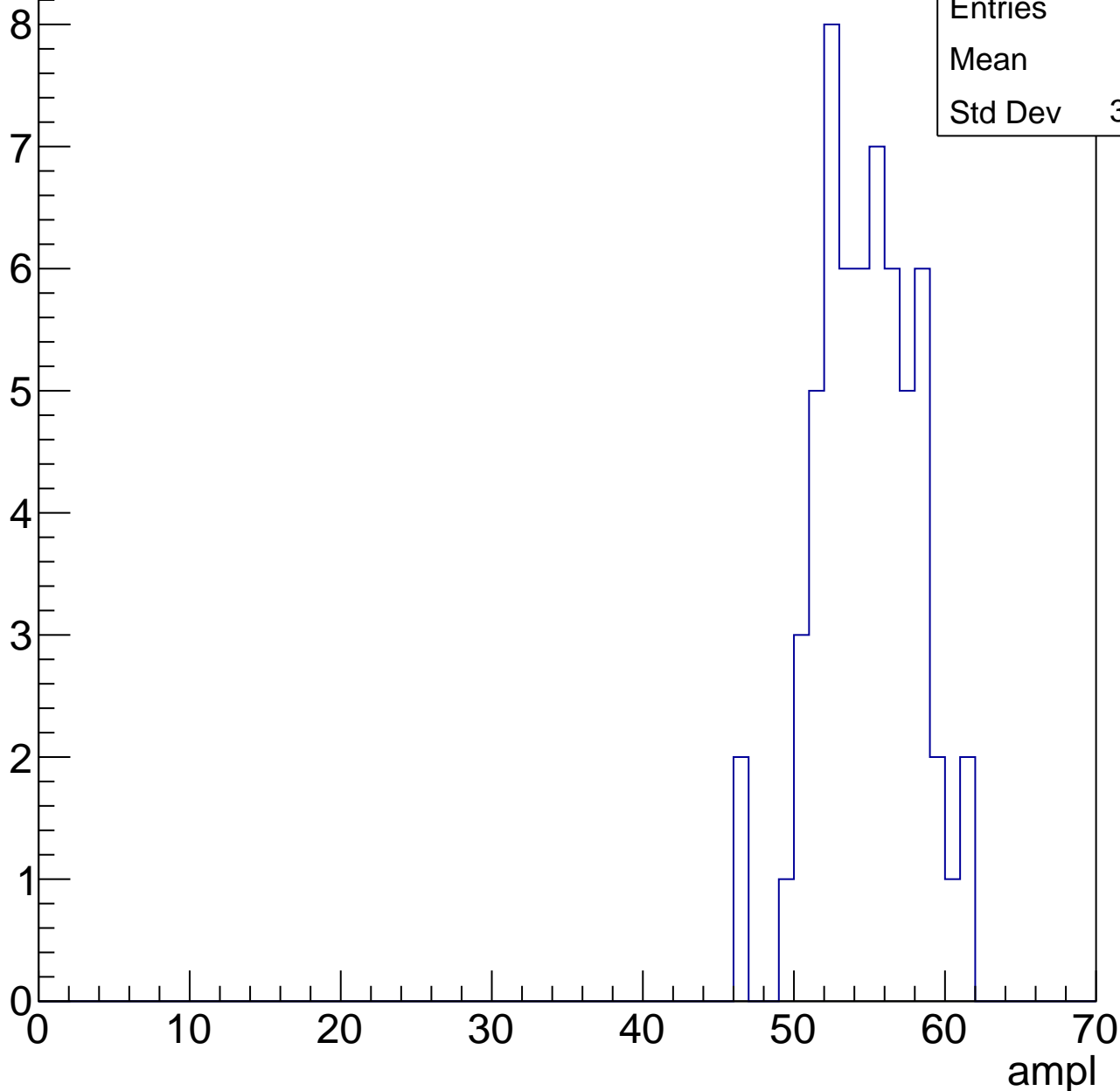


B1L103S, U5-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

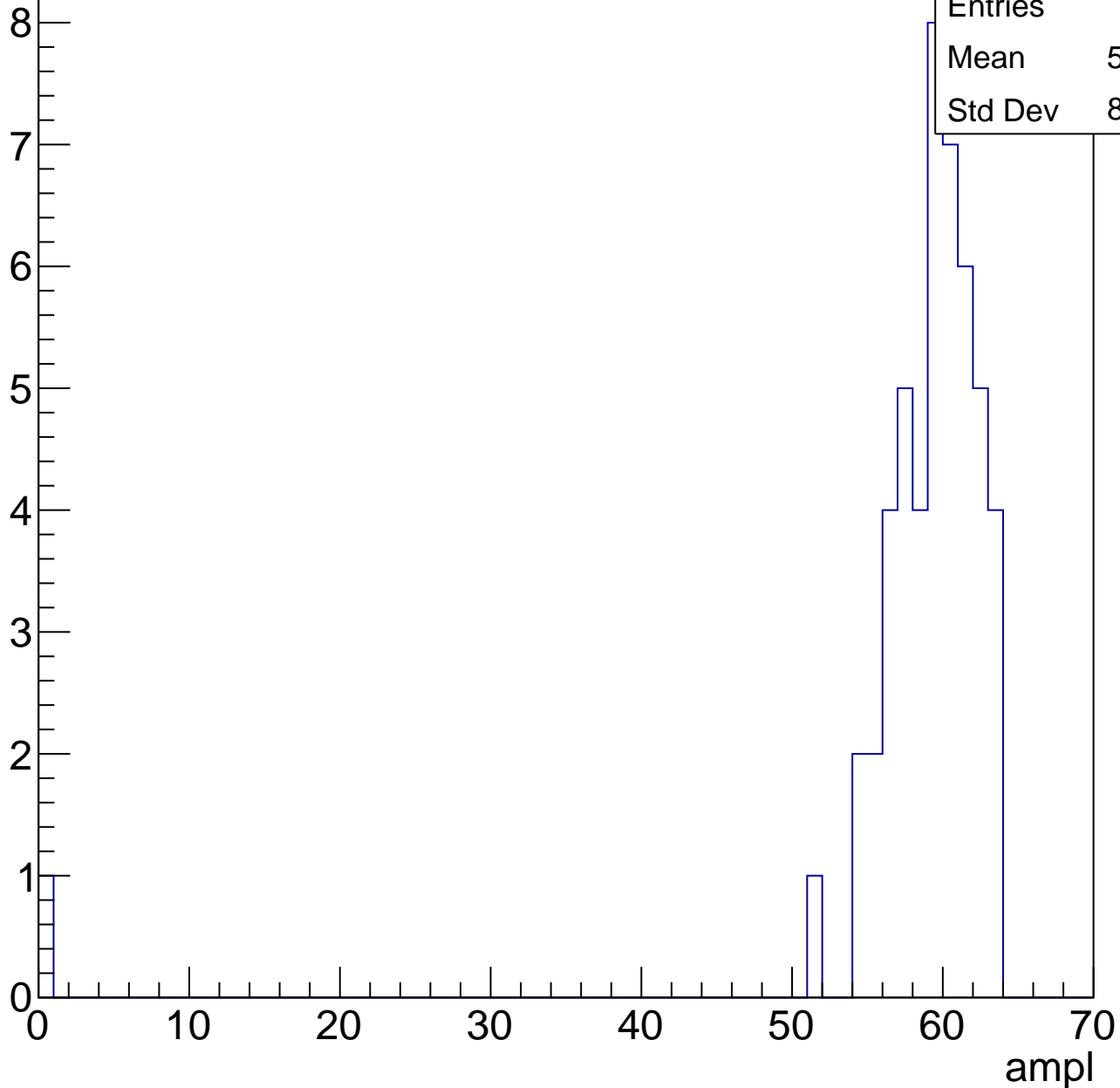
Entries	60
Mean	54.3
Std Dev	3.252



B1L103S, U5-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

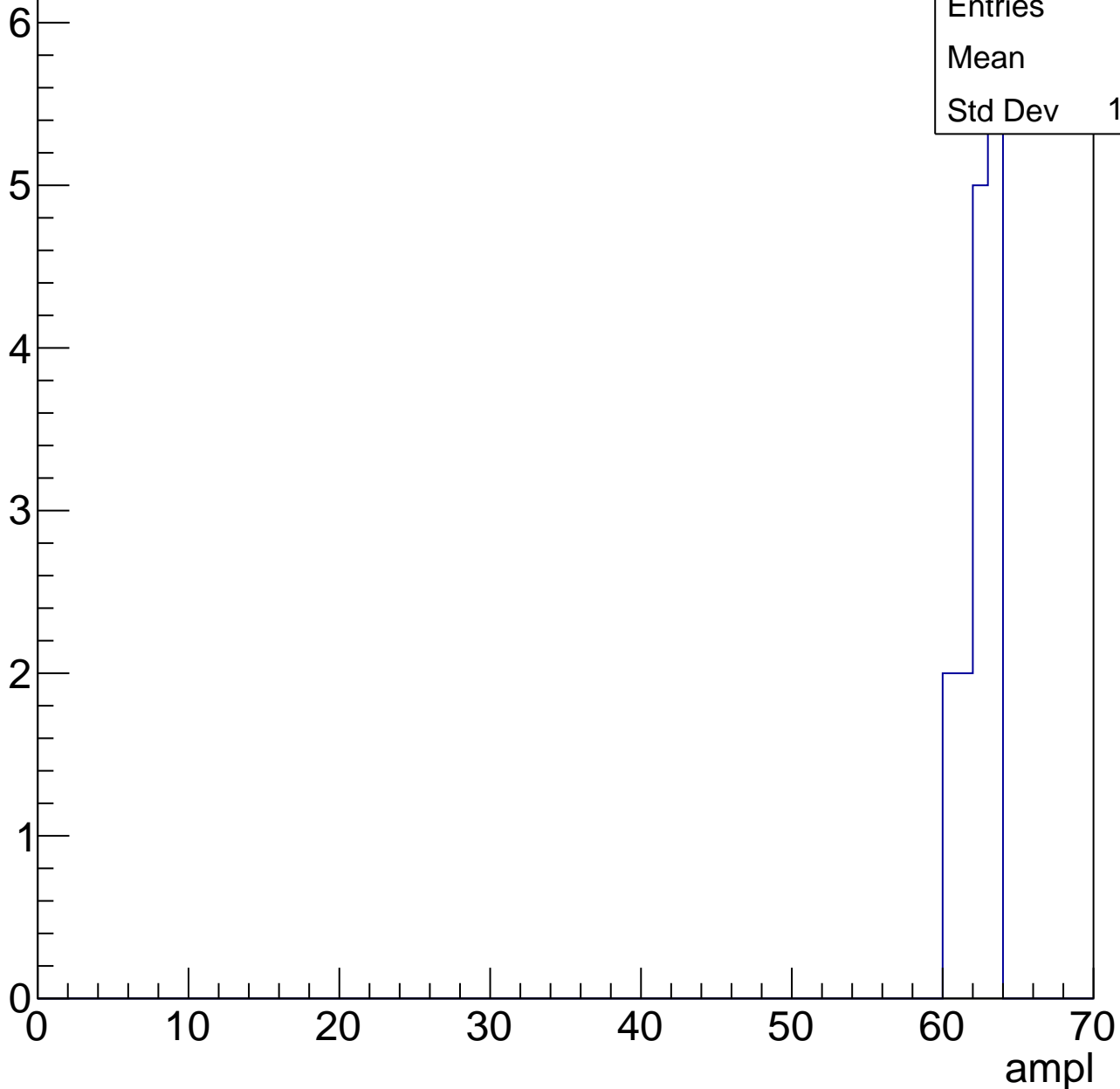


B1L103S, U5-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	62
Std Dev	1.033



B1L103S, U5-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch39, adc0

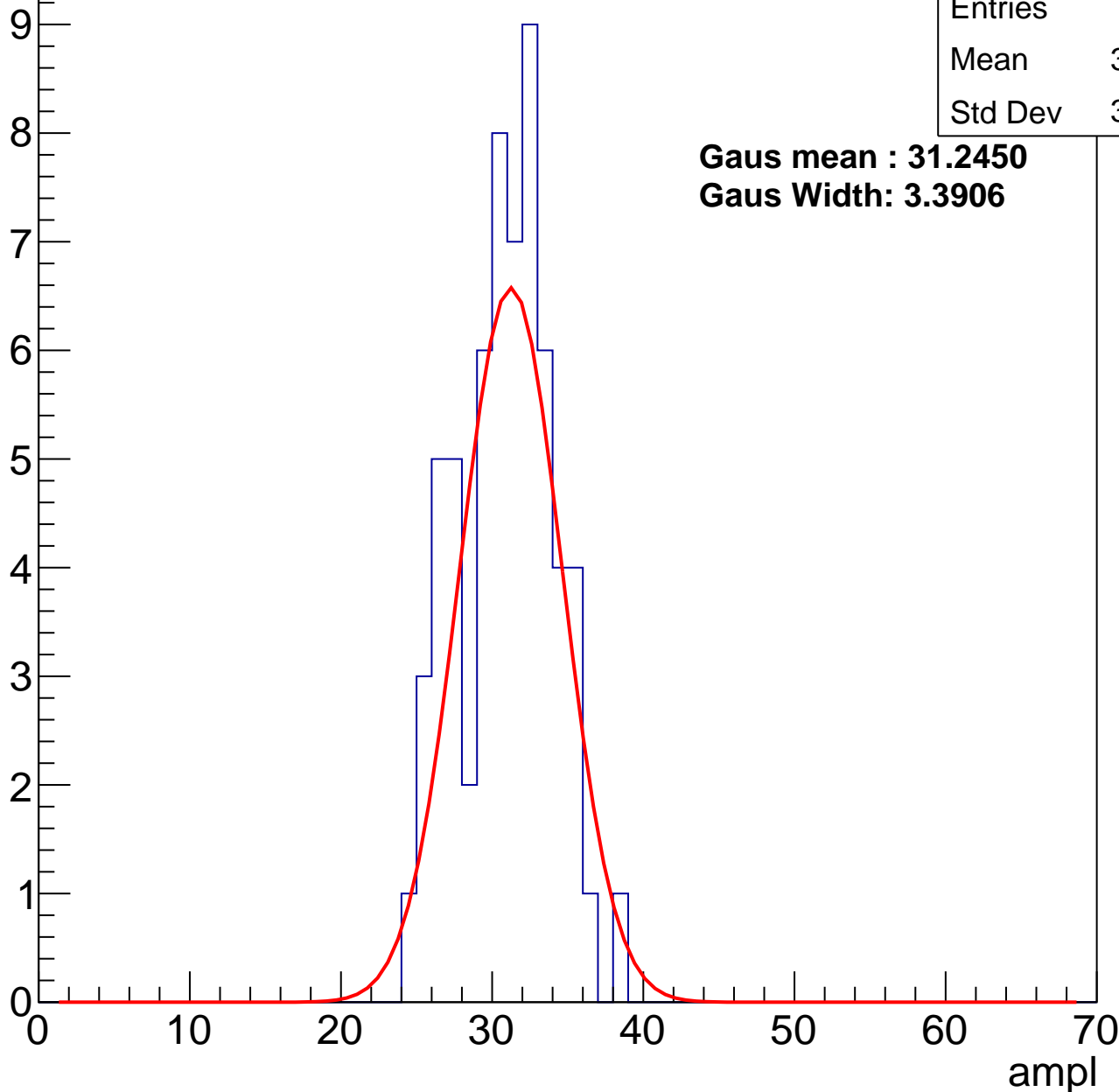
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	30.44
Std Dev	3.109

Gaus mean : 31.2450

Gaus Width: 3.3906



B1L103S, U5-ch39, adc1

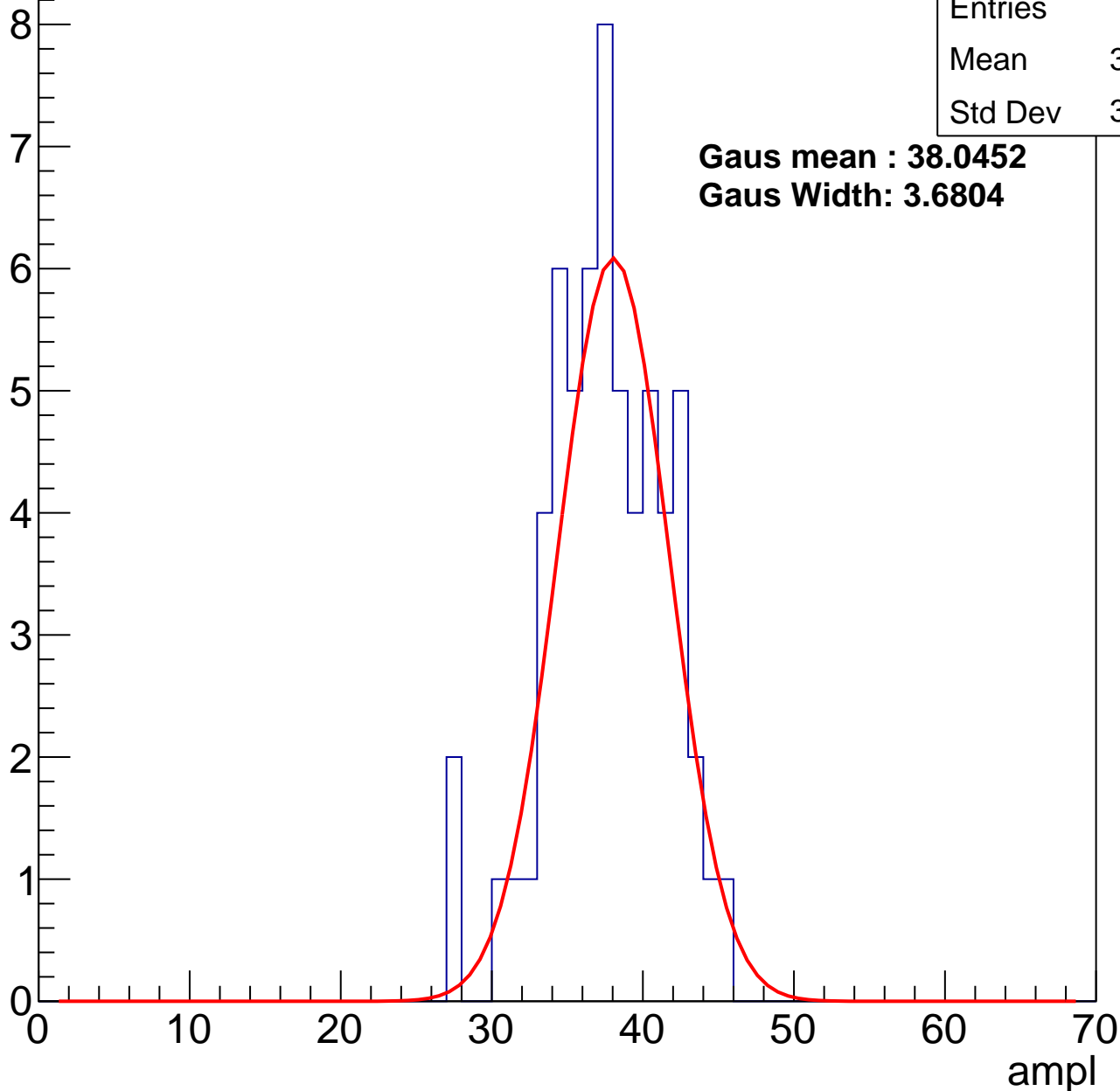
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.13
Std Dev	3.826

Gaus mean : 38.0452

Gaus Width: 3.6804



B1L103S, U5-ch39, adc2

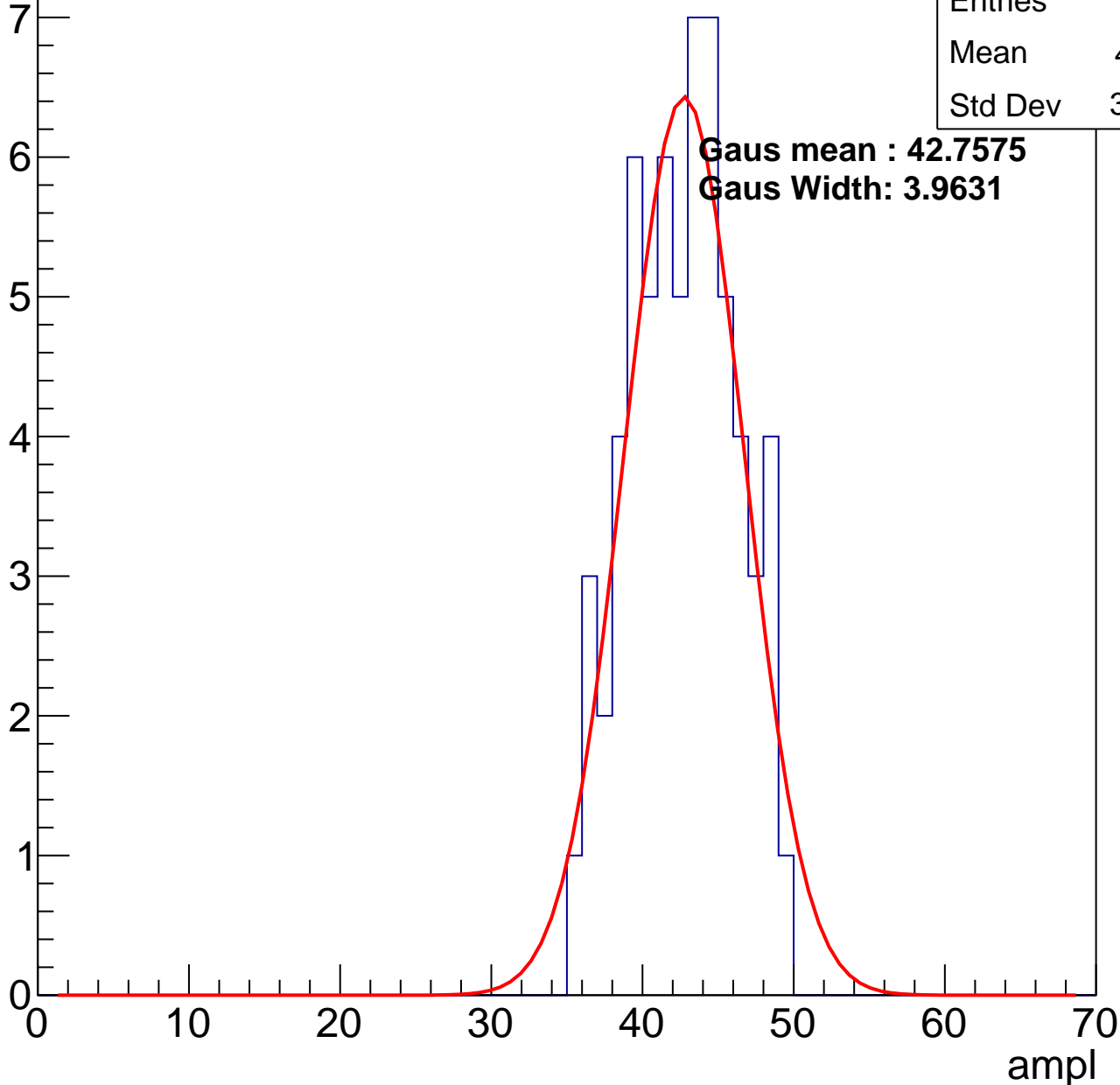
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.21
Std Dev	3.474

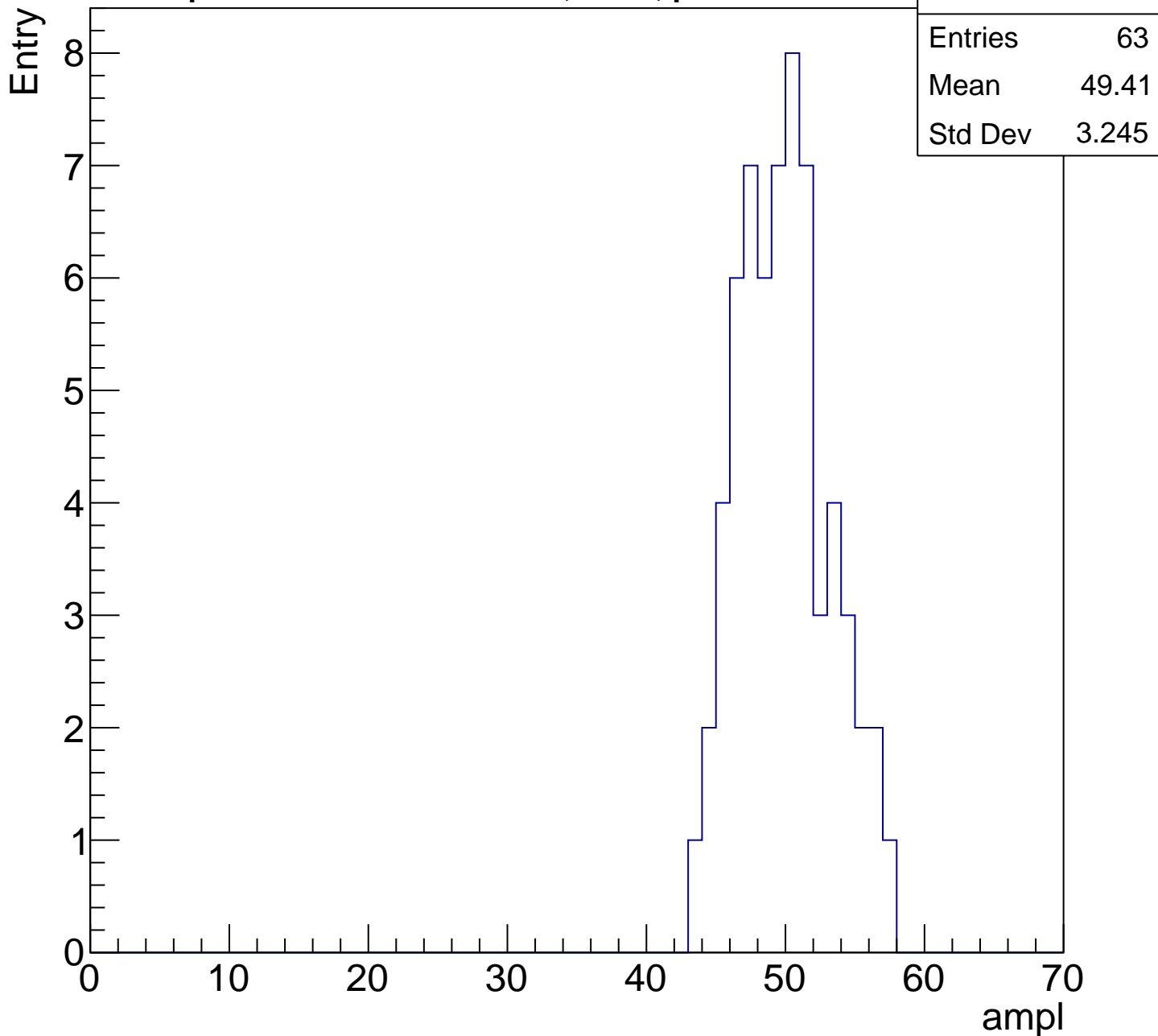
Gaus mean : 42.7575

Gaus Width: 3.9631



B1L103S, U5-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

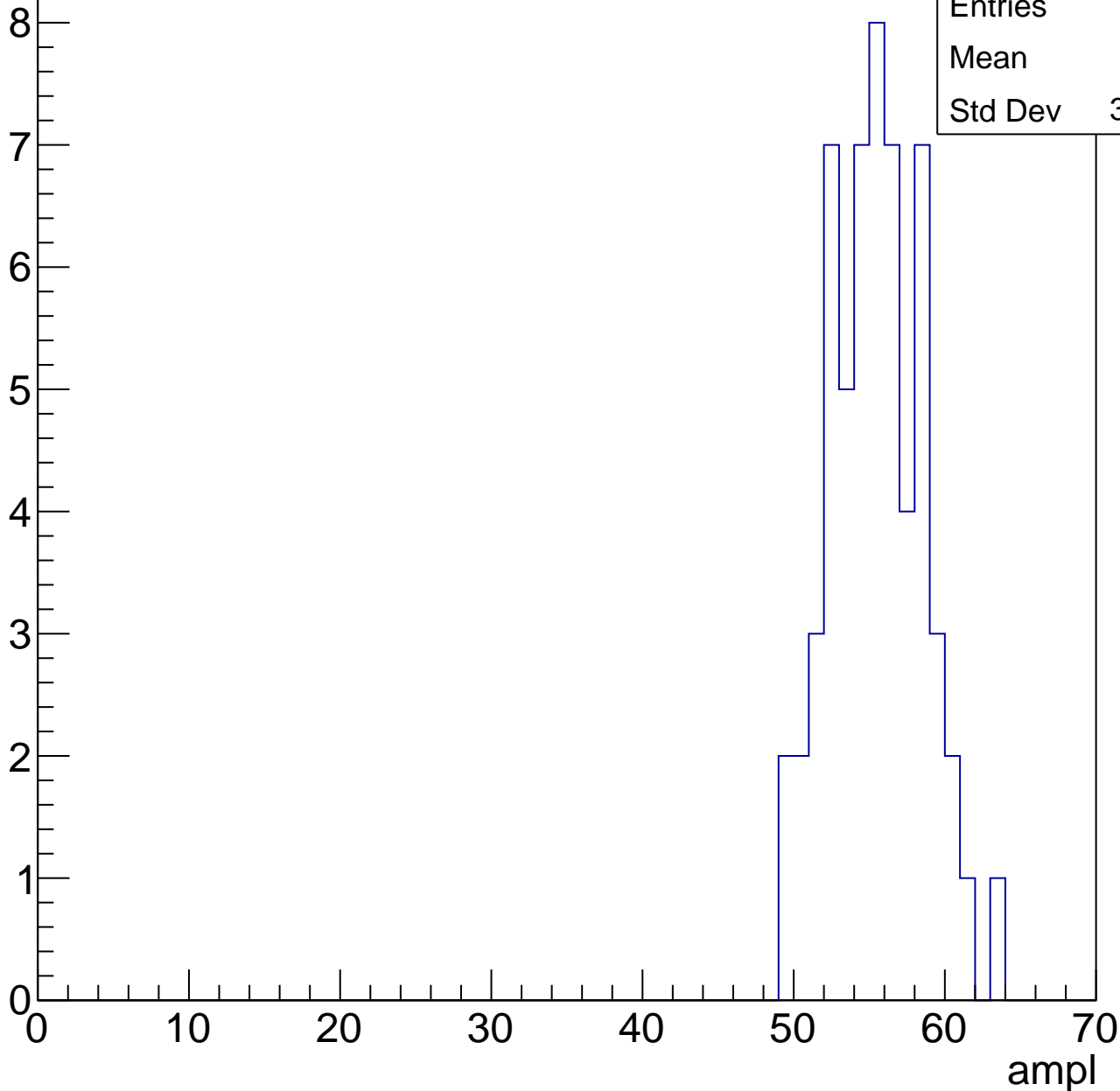


B1L103S, U5-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55
Std Dev	3.037

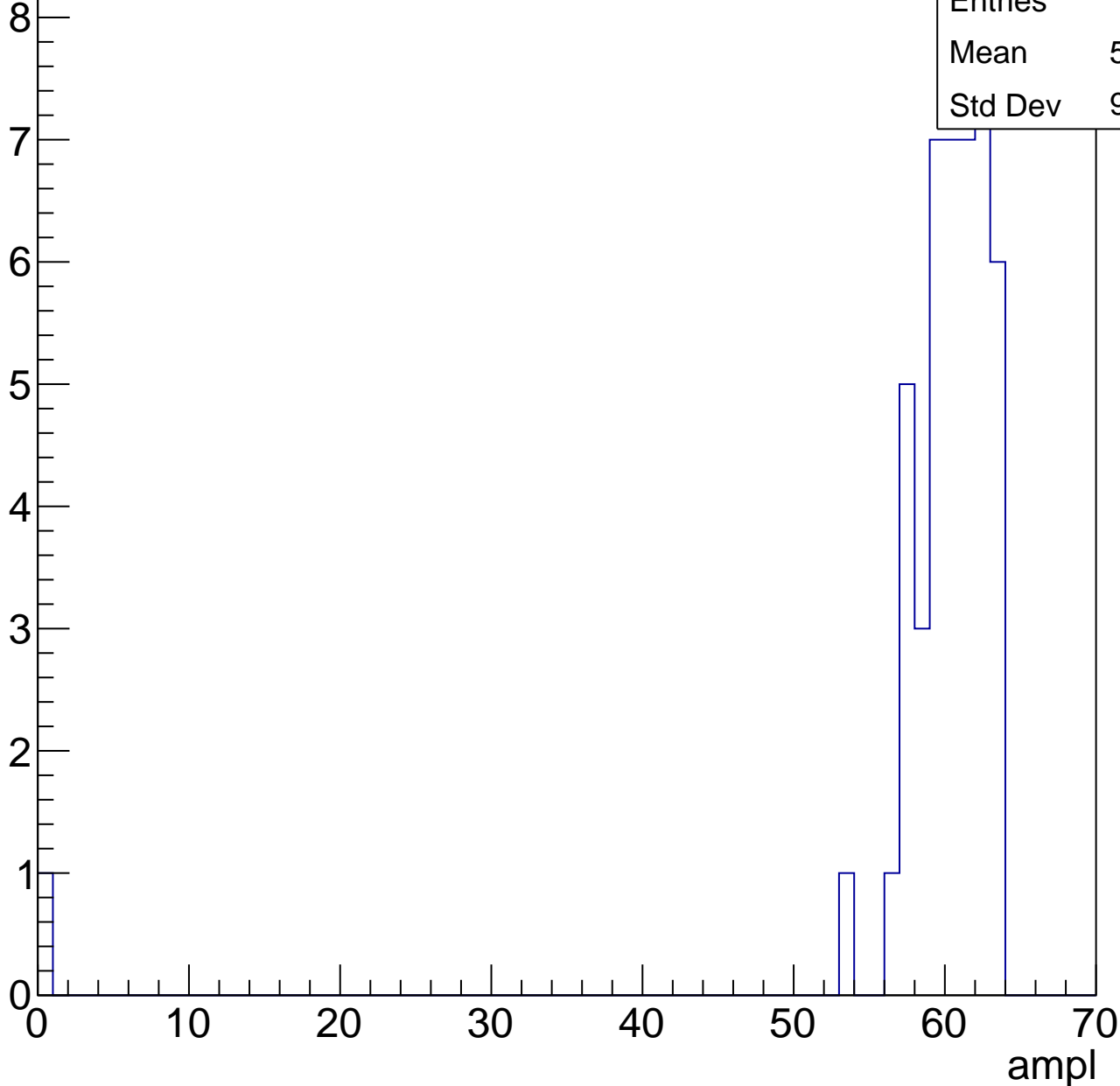


B1L103S, U5-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.74
Std Dev	9.028



B1L103S, U5-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch40, adc0

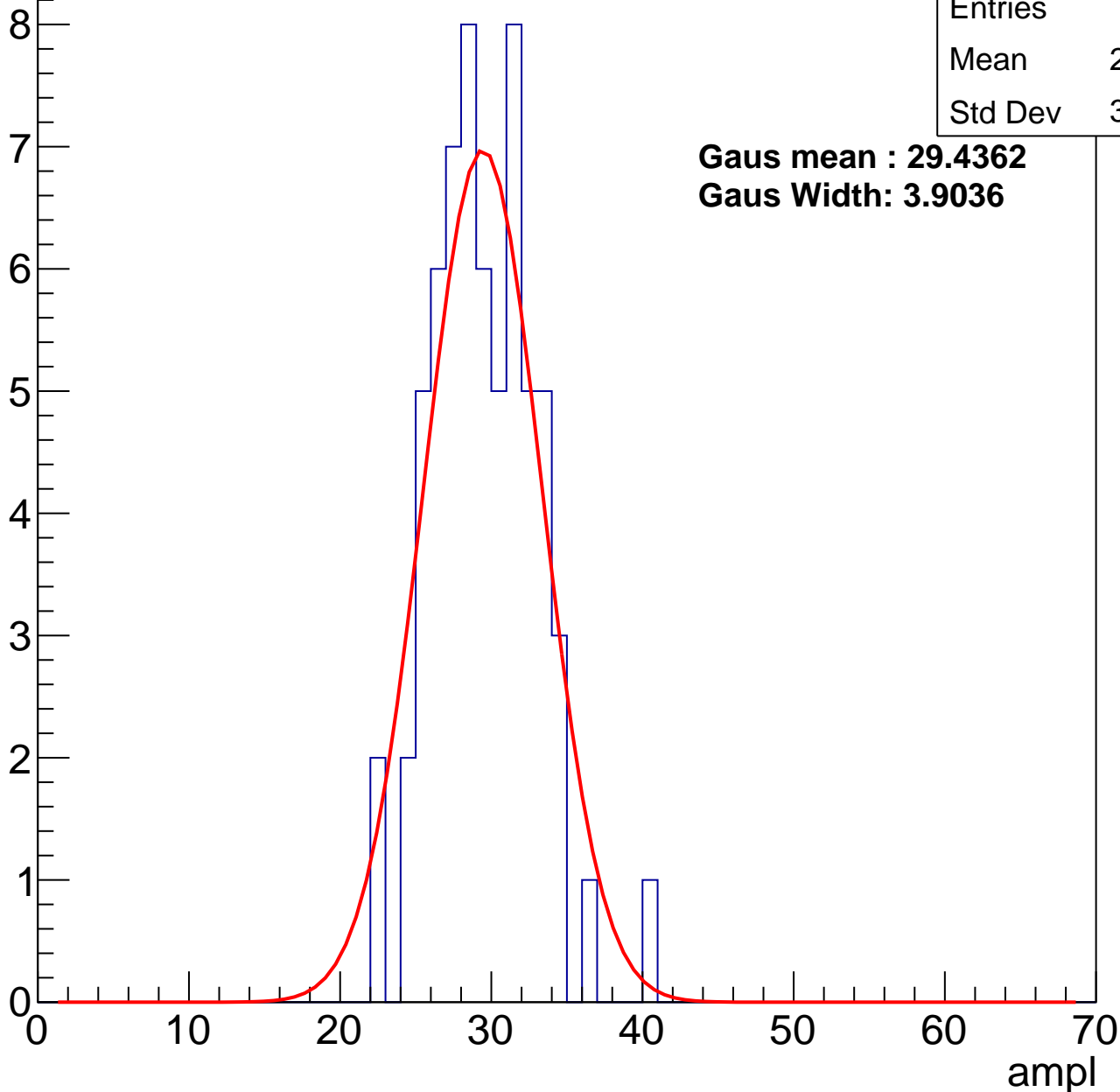
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	29.08
Std Dev	3.365

Gaus mean : 29.4362

Gaus Width: 3.9036



B1L103S, U5-ch40, adc1

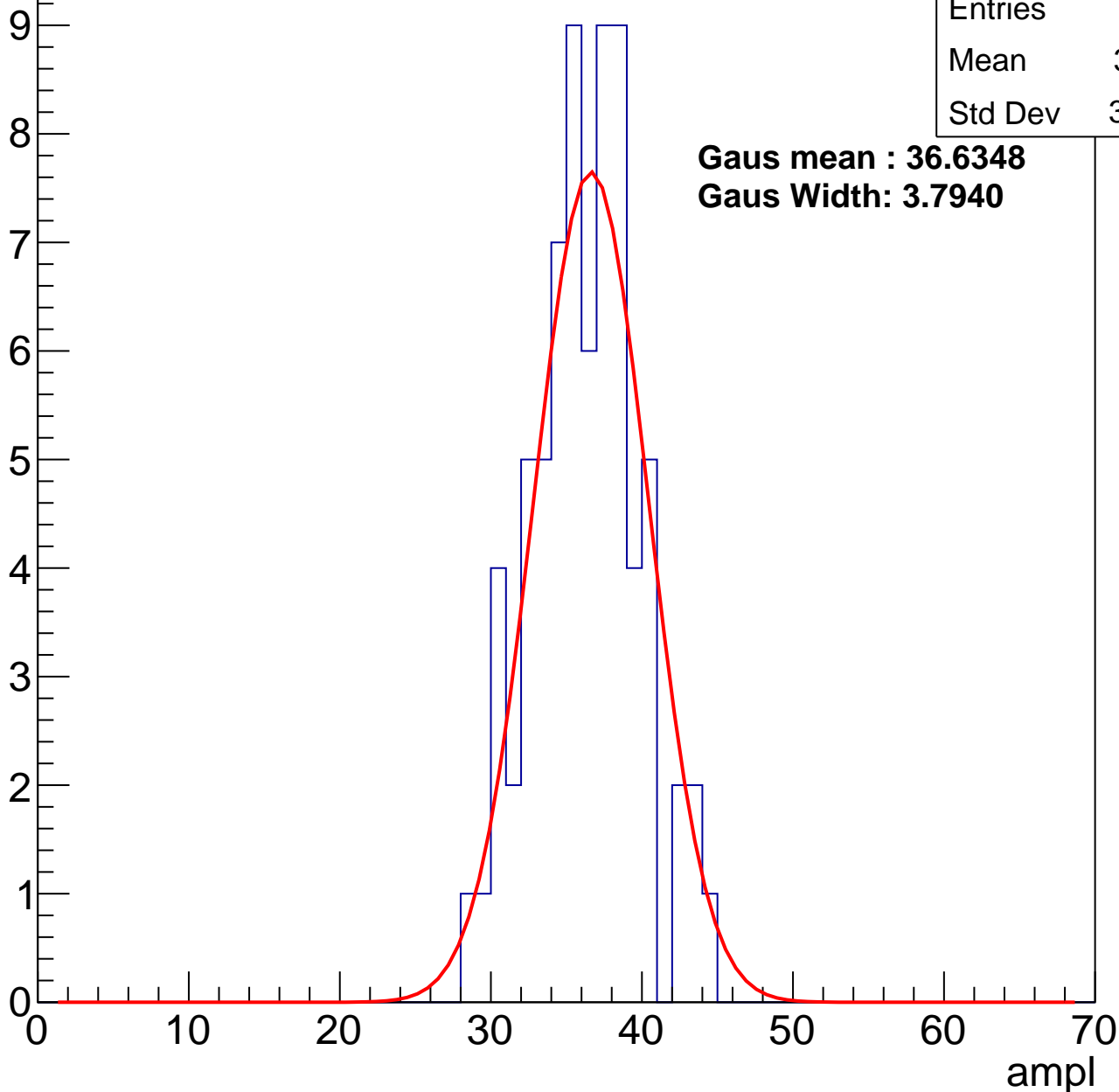
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	35.81
Std Dev	3.459

Gaus mean : 36.6348

Gaus Width: 3.7940



B1L103S, U5-ch40, adc2

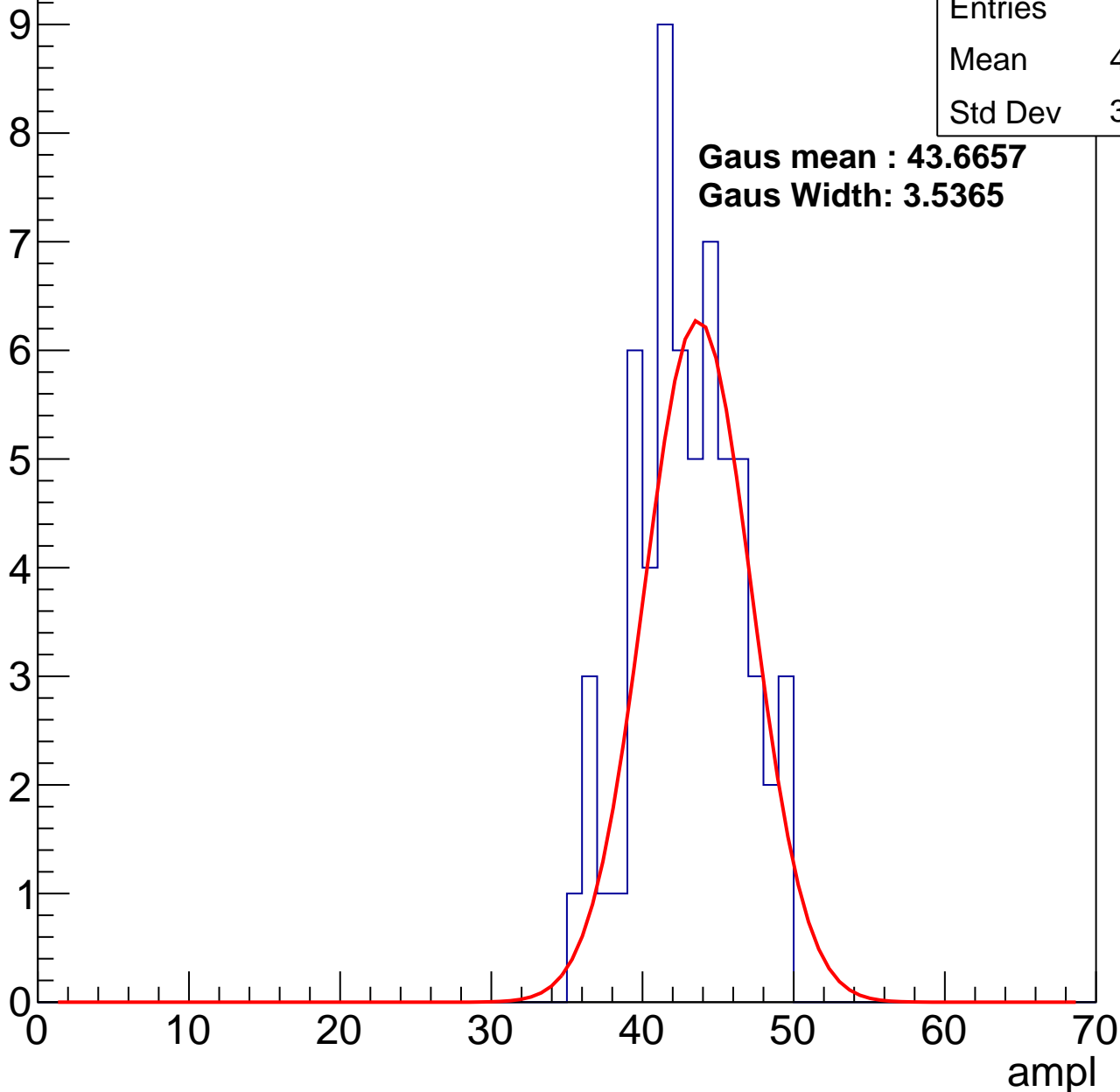
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.54
Std Dev	3.414

Gaus mean : 43.6657

Gaus Width: 3.5365

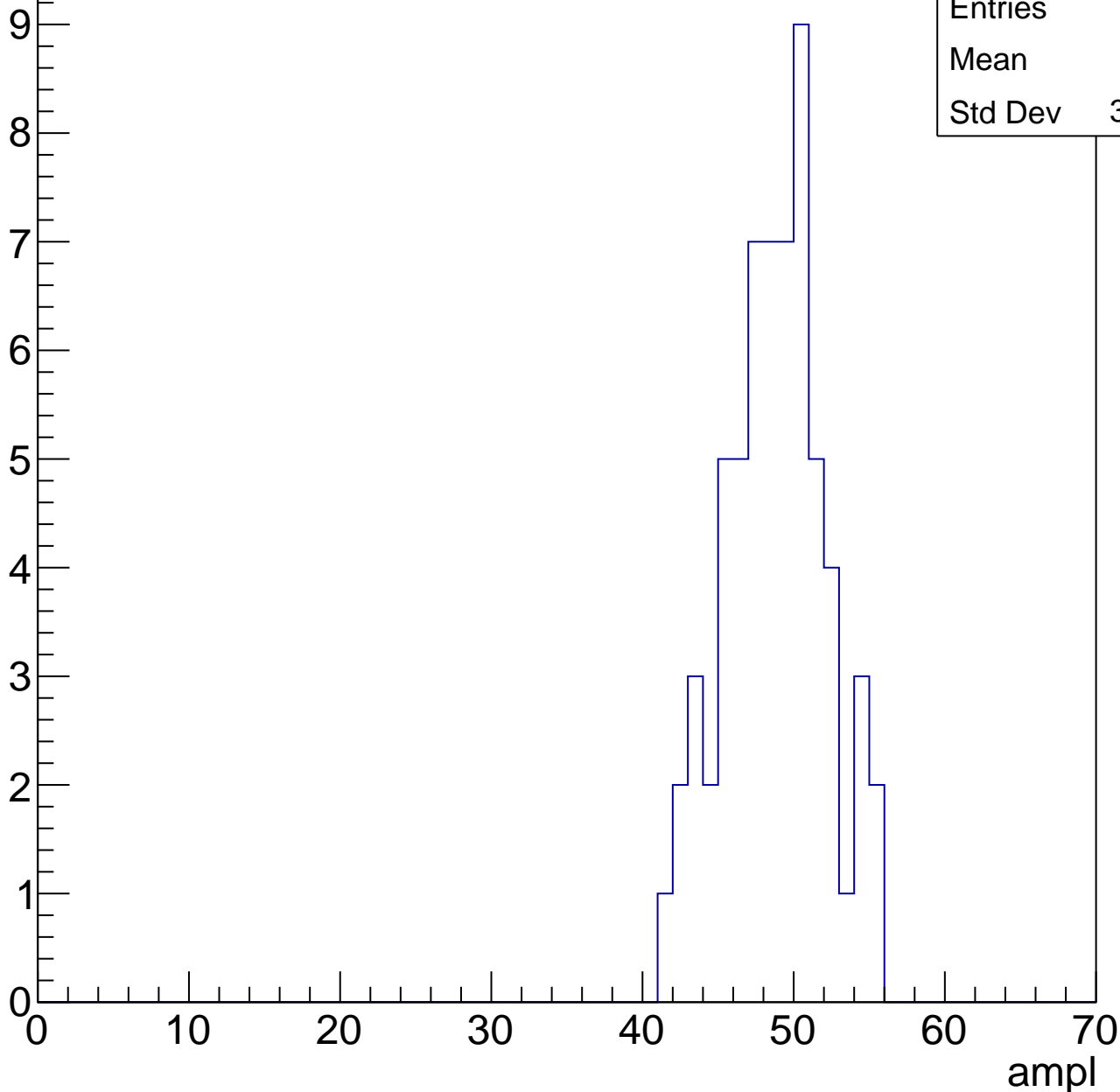


B1L103S, U5-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48.3
Std Dev	3.279

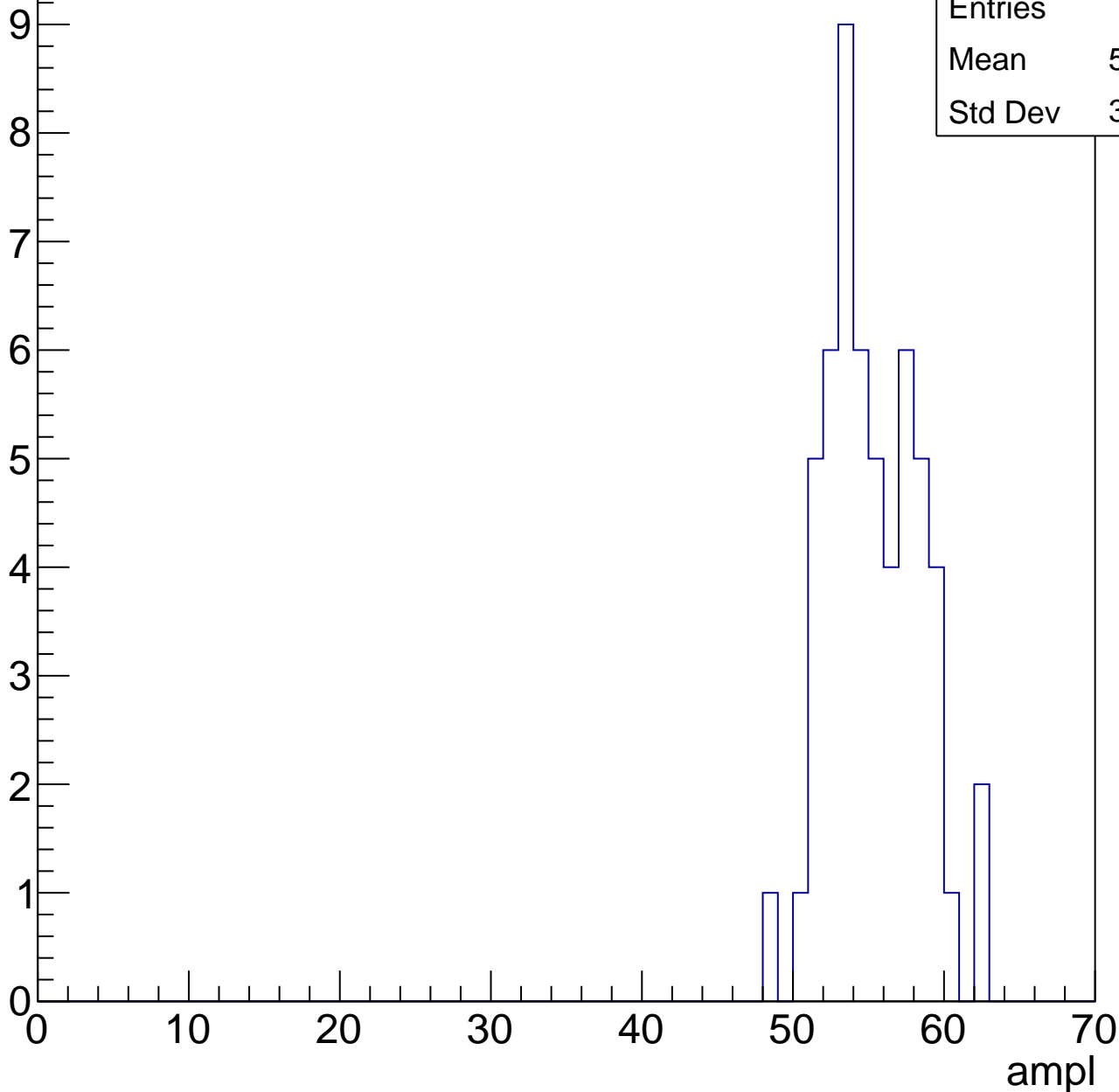


B1L103S, U5-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

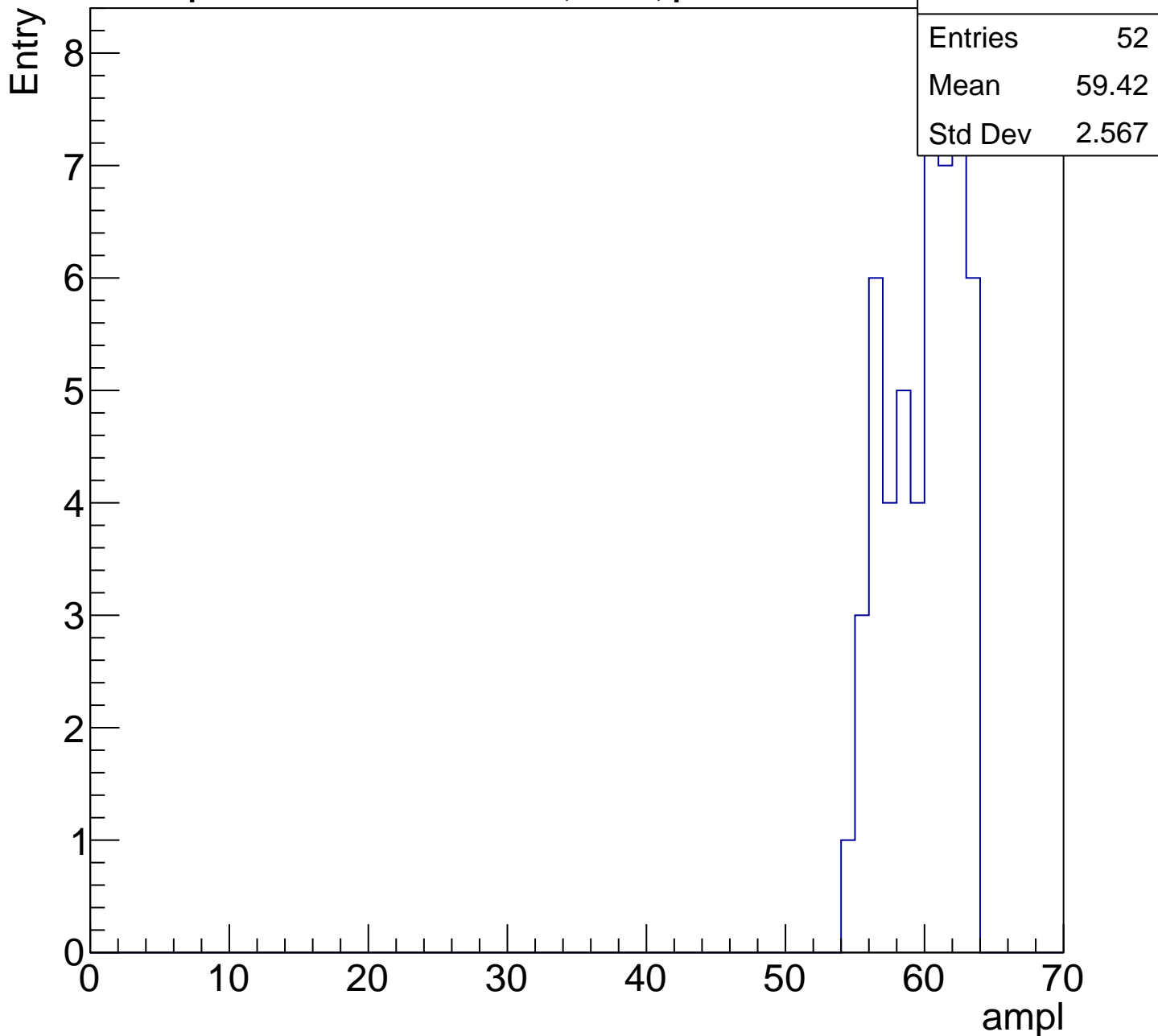
Entry

Entries	55
Mean	54.85
Std Dev	3.042



B1L103S, U5-ch40, adc5

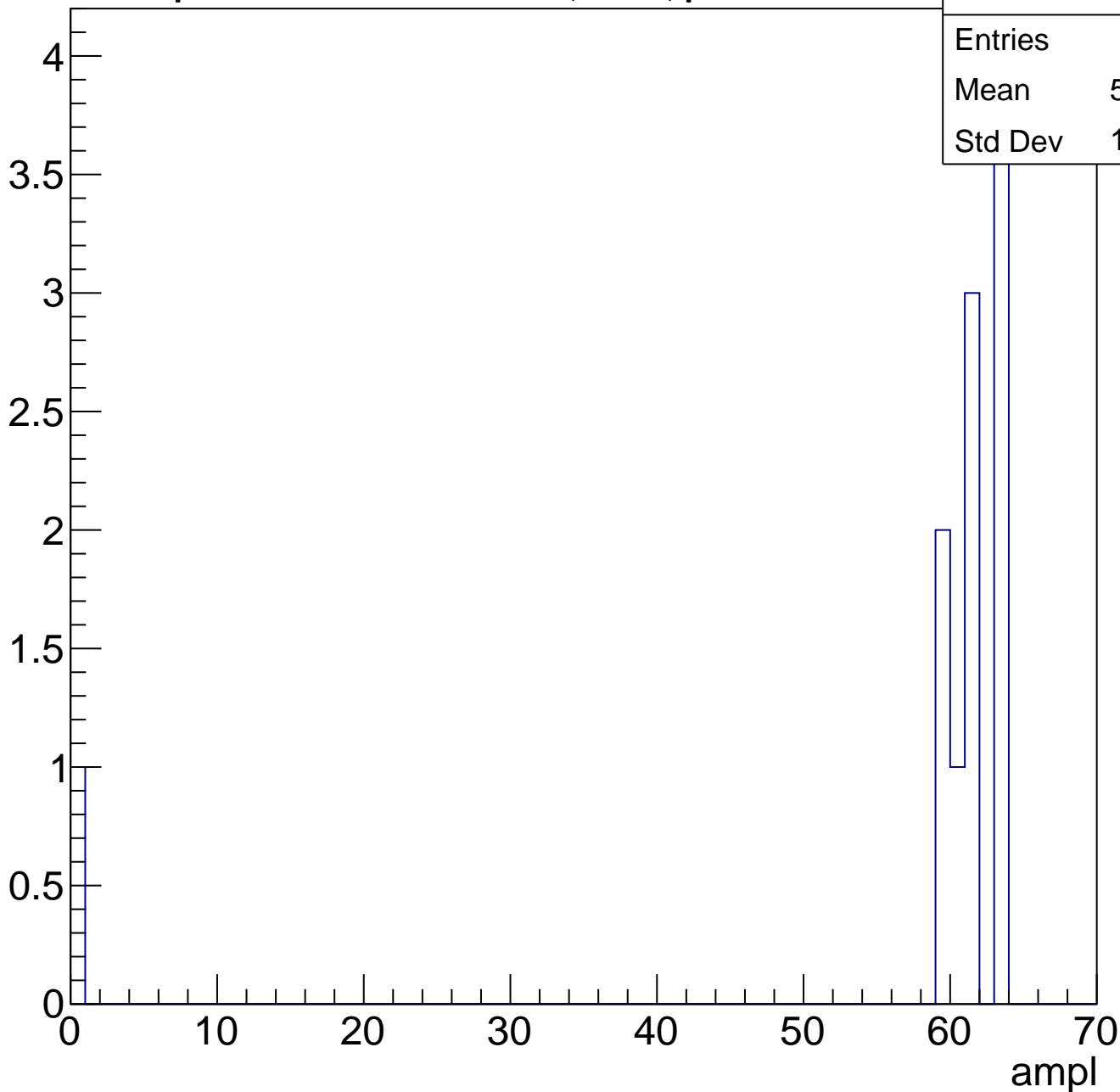
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch41, adc0

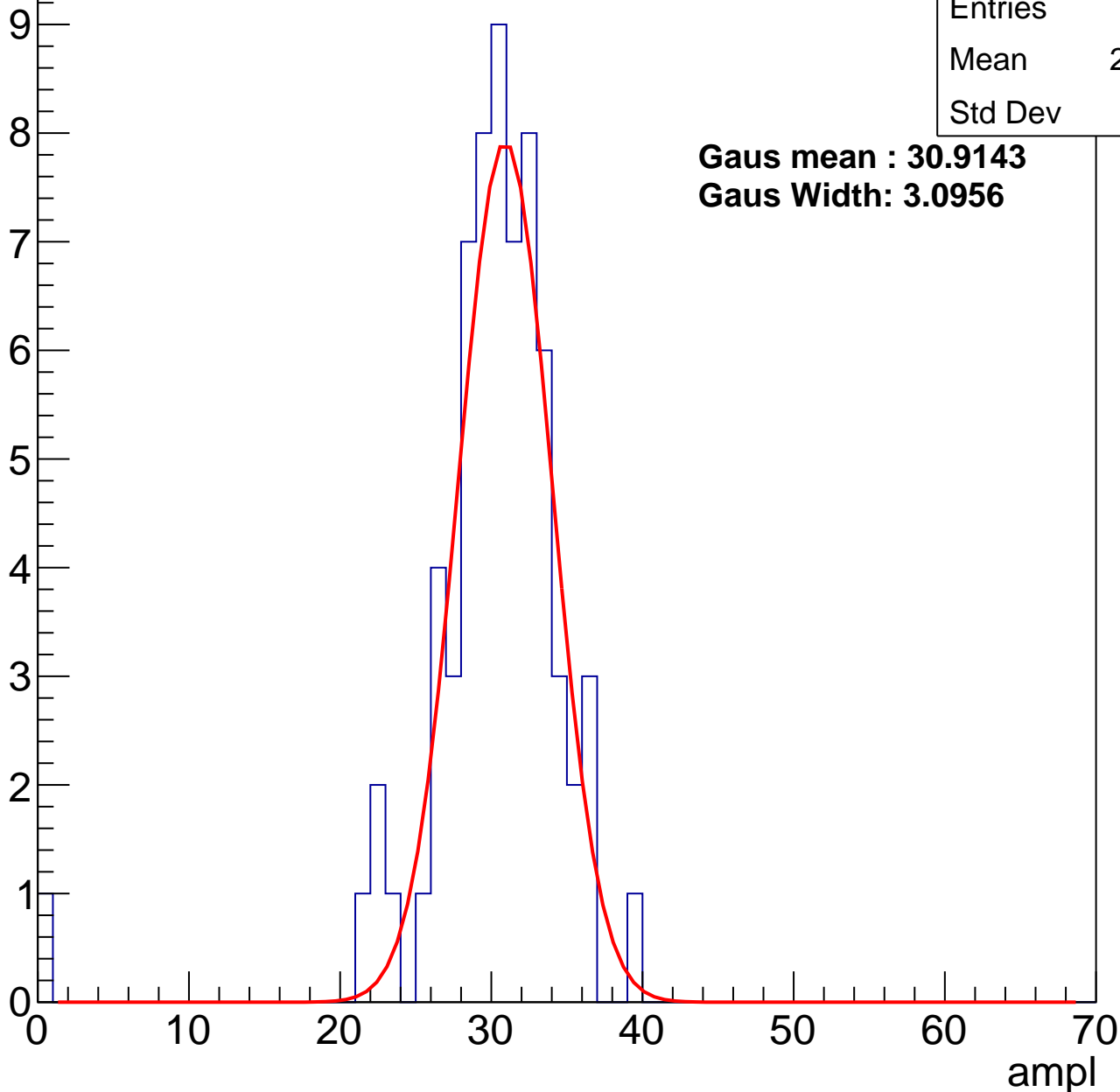
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.64
Std Dev	5.01

Gaus mean : 30.9143

Gaus Width: 3.0956



B1L103S, U5-ch41, adc1

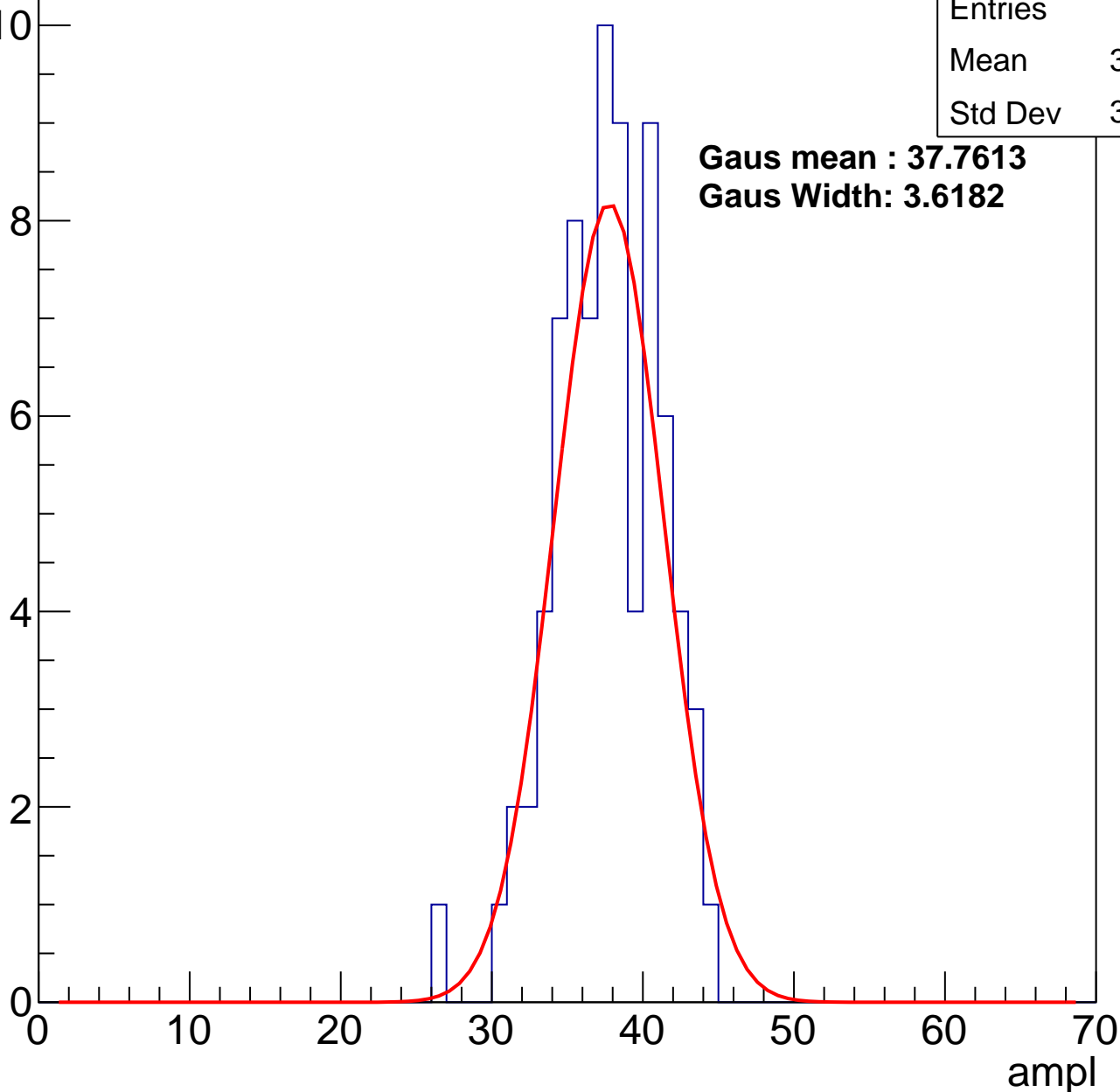
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	37.17
Std Dev	3.425

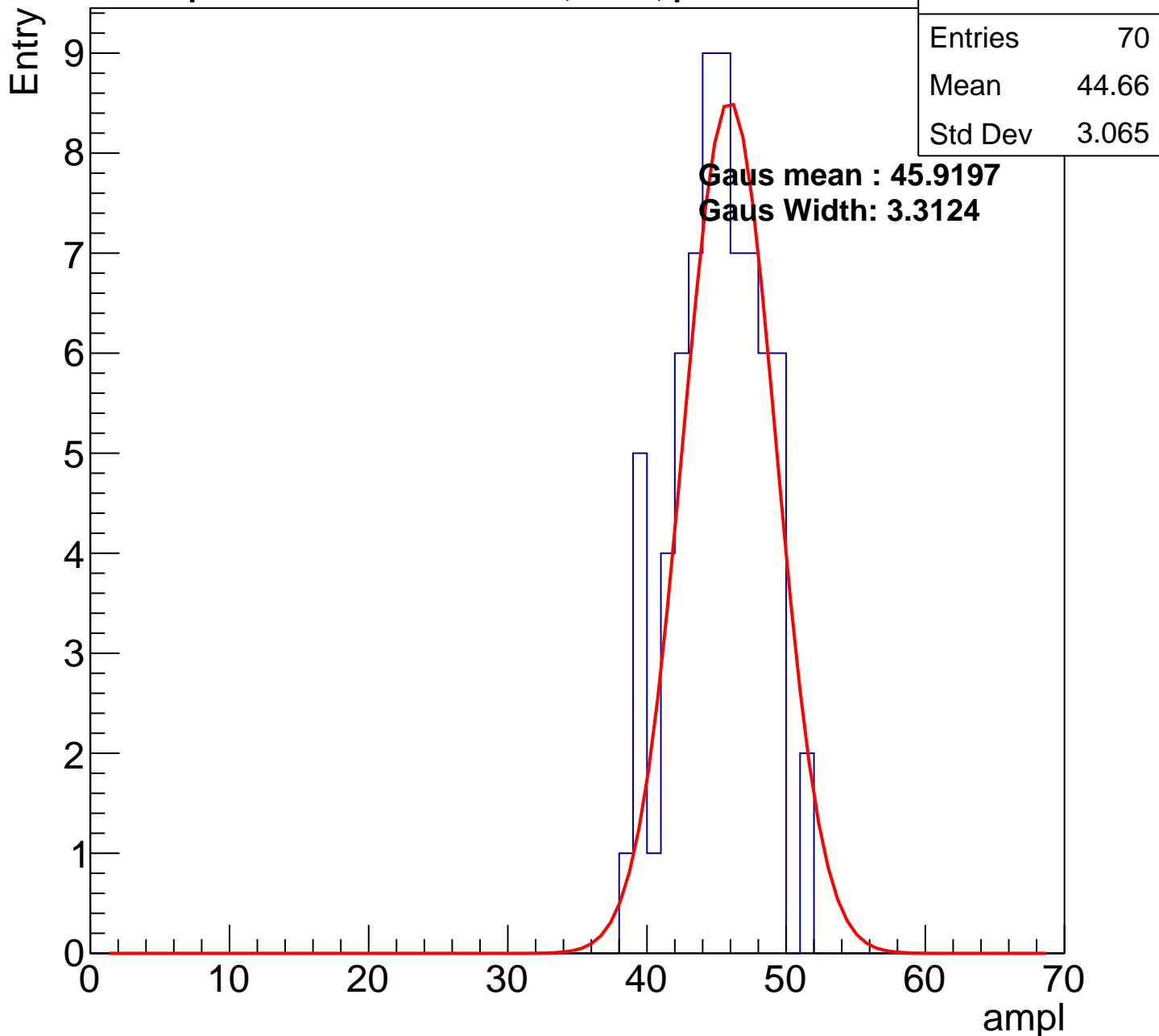
Gaus mean : 37.7613

Gaus Width: 3.6182



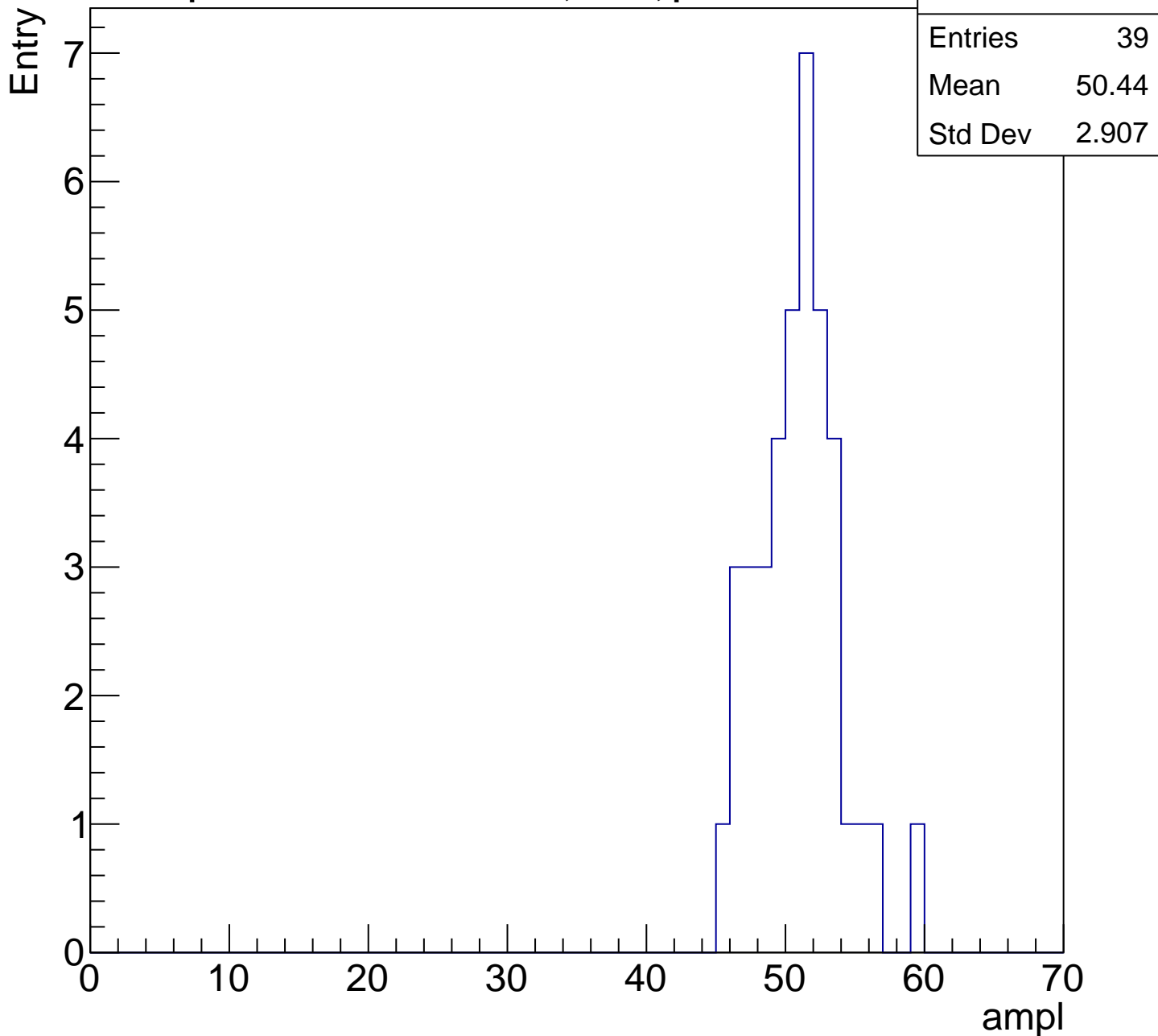
B1L103S, U5-ch41, adc2

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

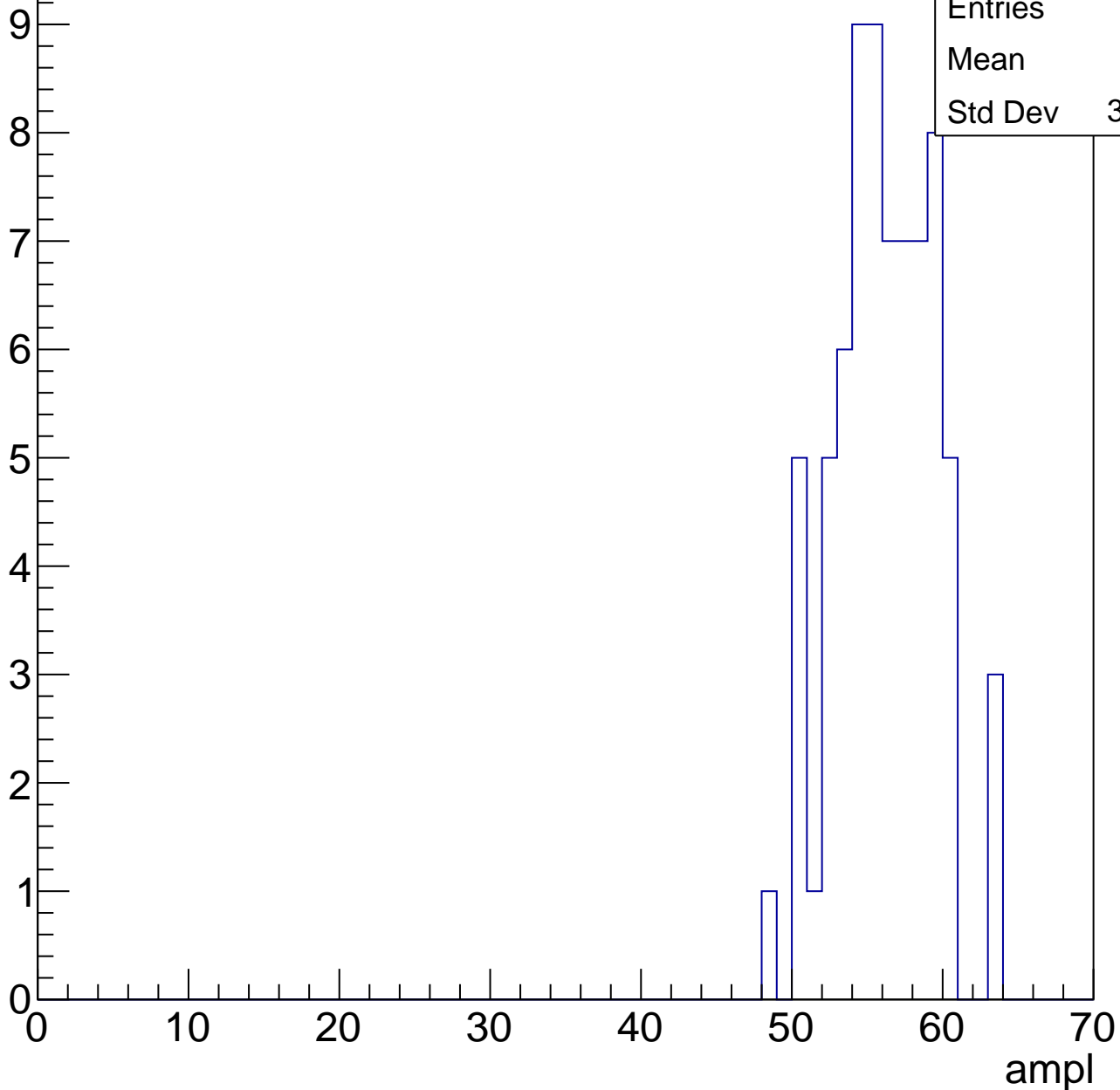


B1L103S, U5-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	55.7
Std Dev	3.259

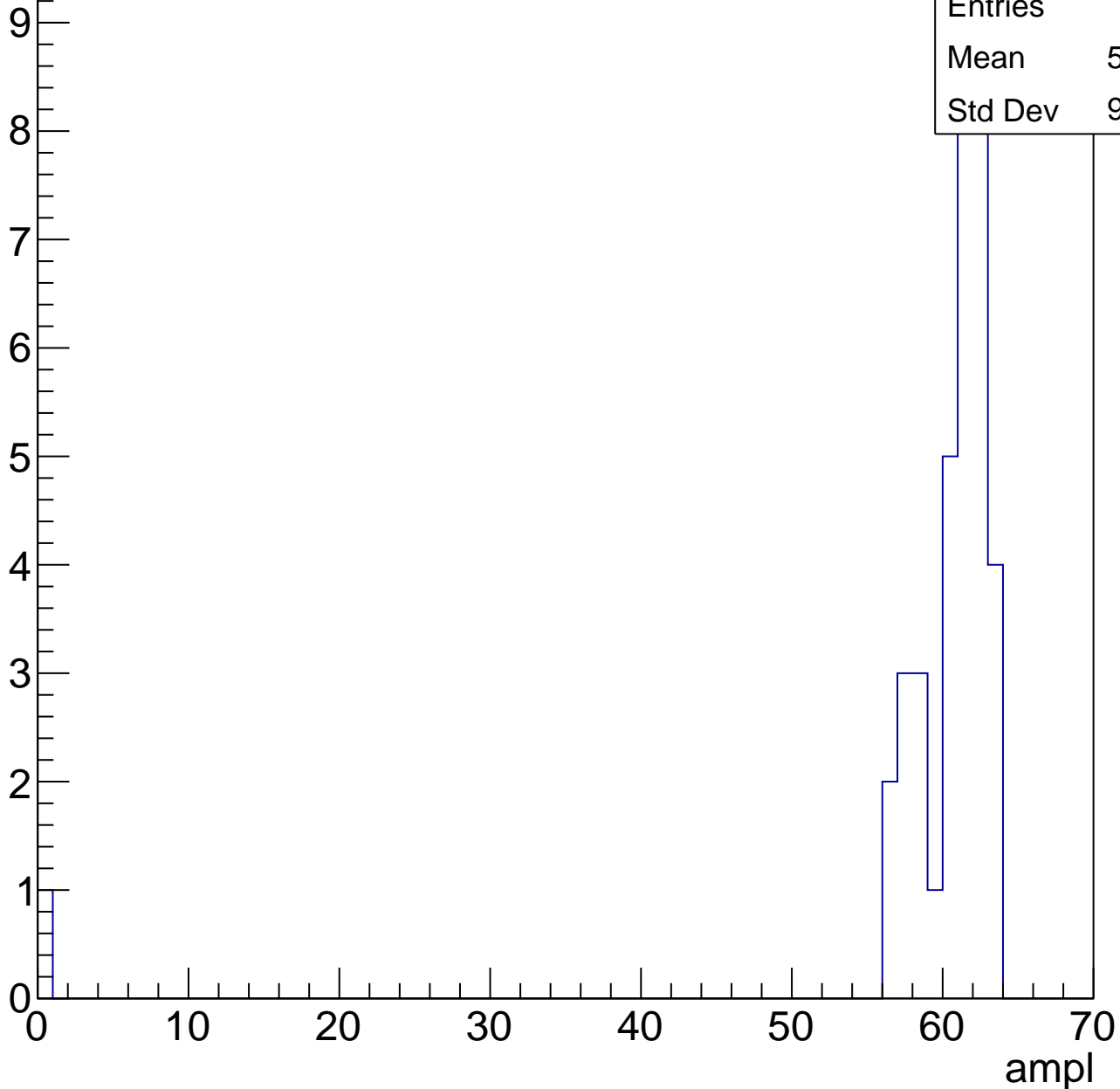


B1L103S, U5-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

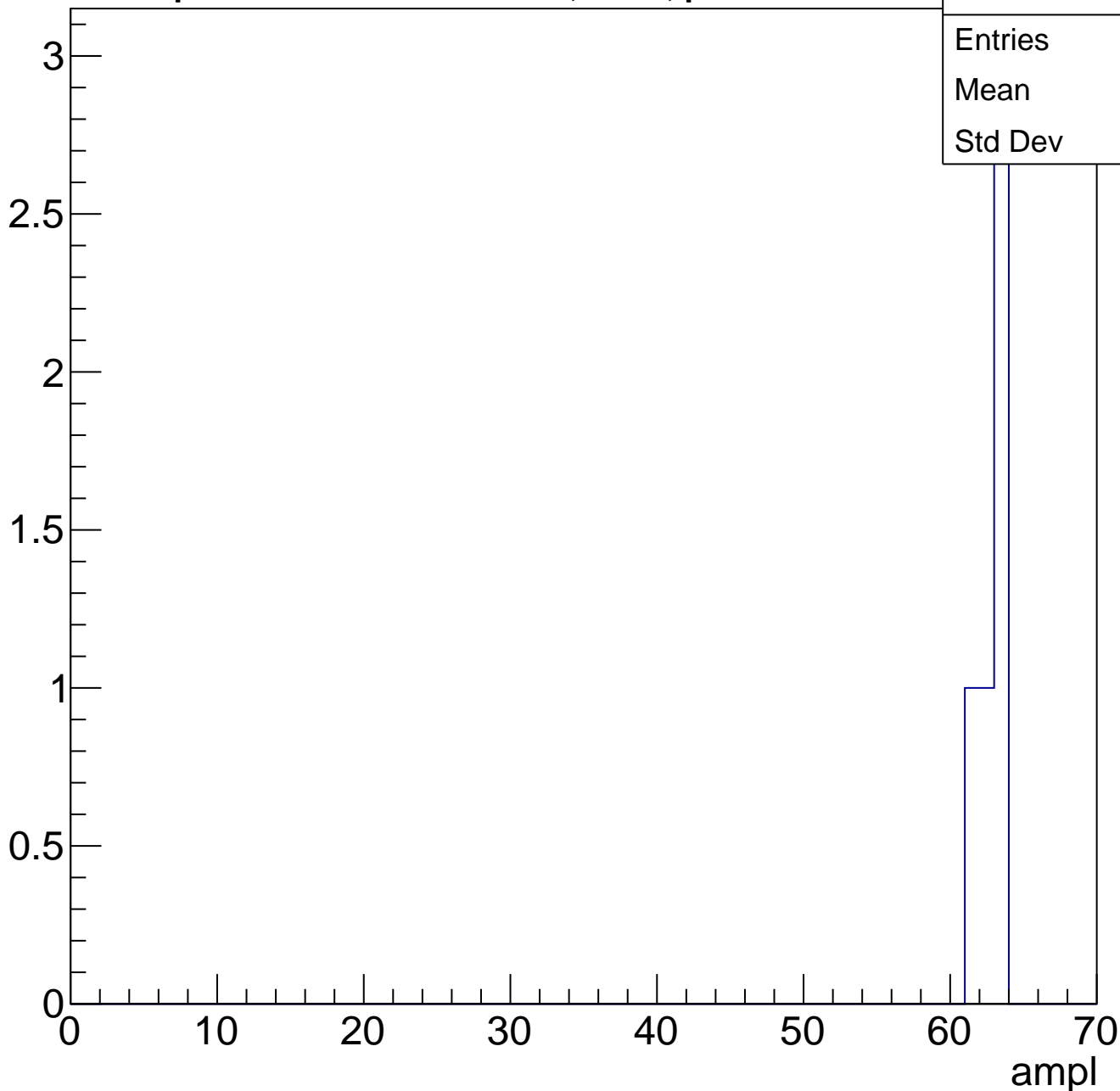
Entries	37
Mean	58.78
Std Dev	9.998



B1L103S, U5-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch42, adc0

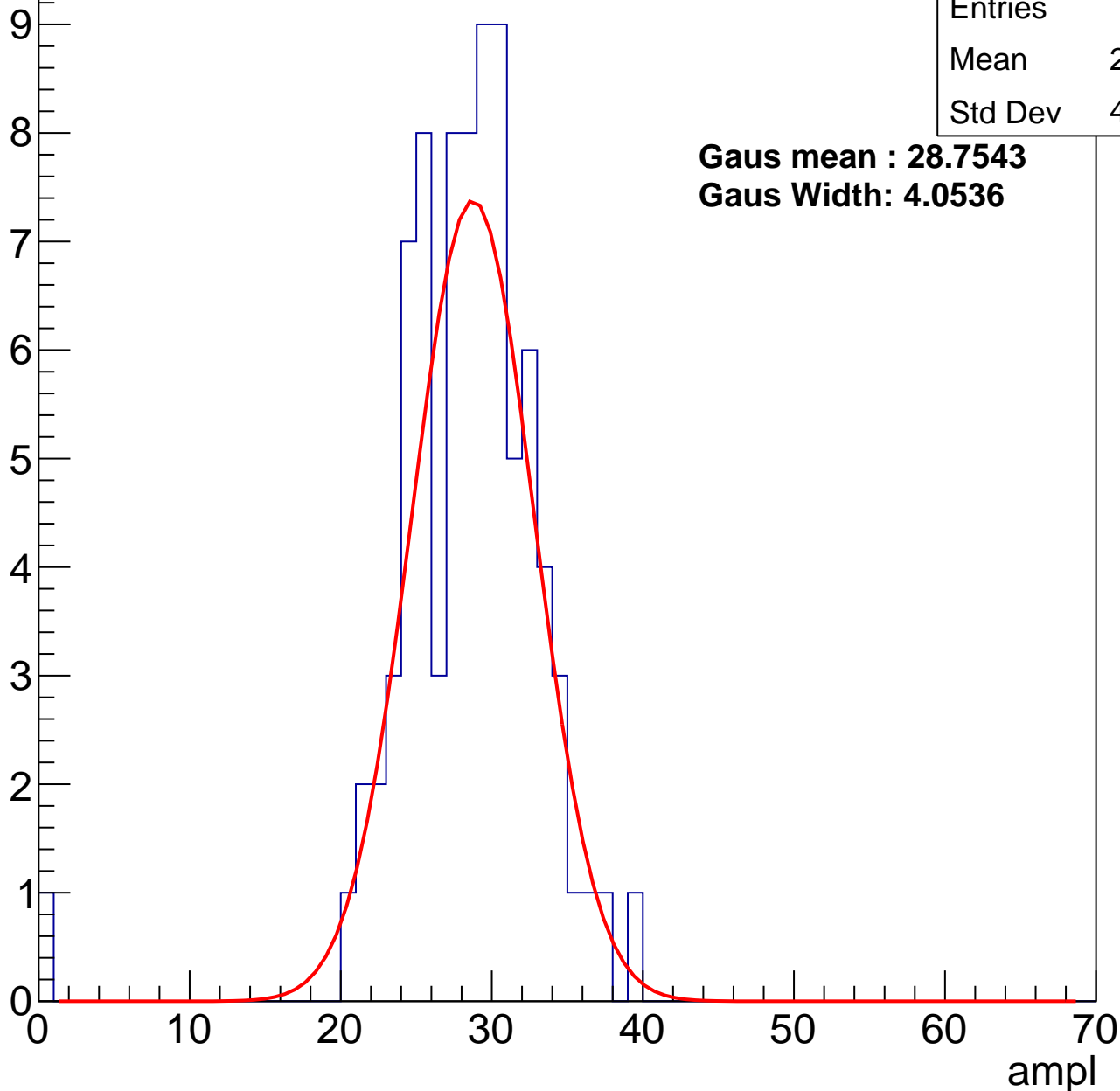
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	27.95
Std Dev	4.916

Gaus mean : 28.7543

Gaus Width: 4.0536



B1L103S, U5-ch42, adc1

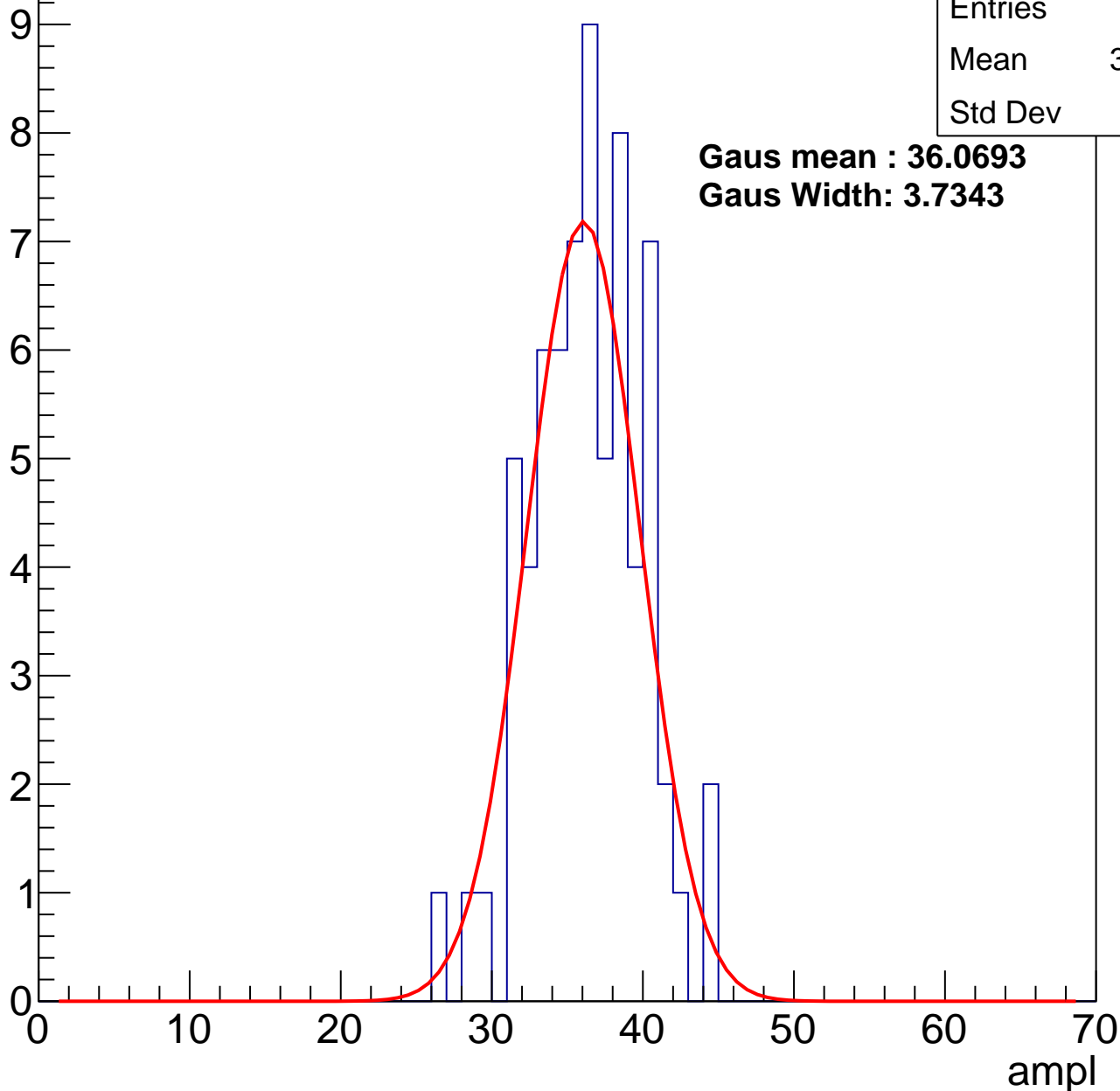
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.86
Std Dev	3.58

Gaus mean : 36.0693

Gaus Width: 3.7343



B1L103S, U5-ch42, adc2

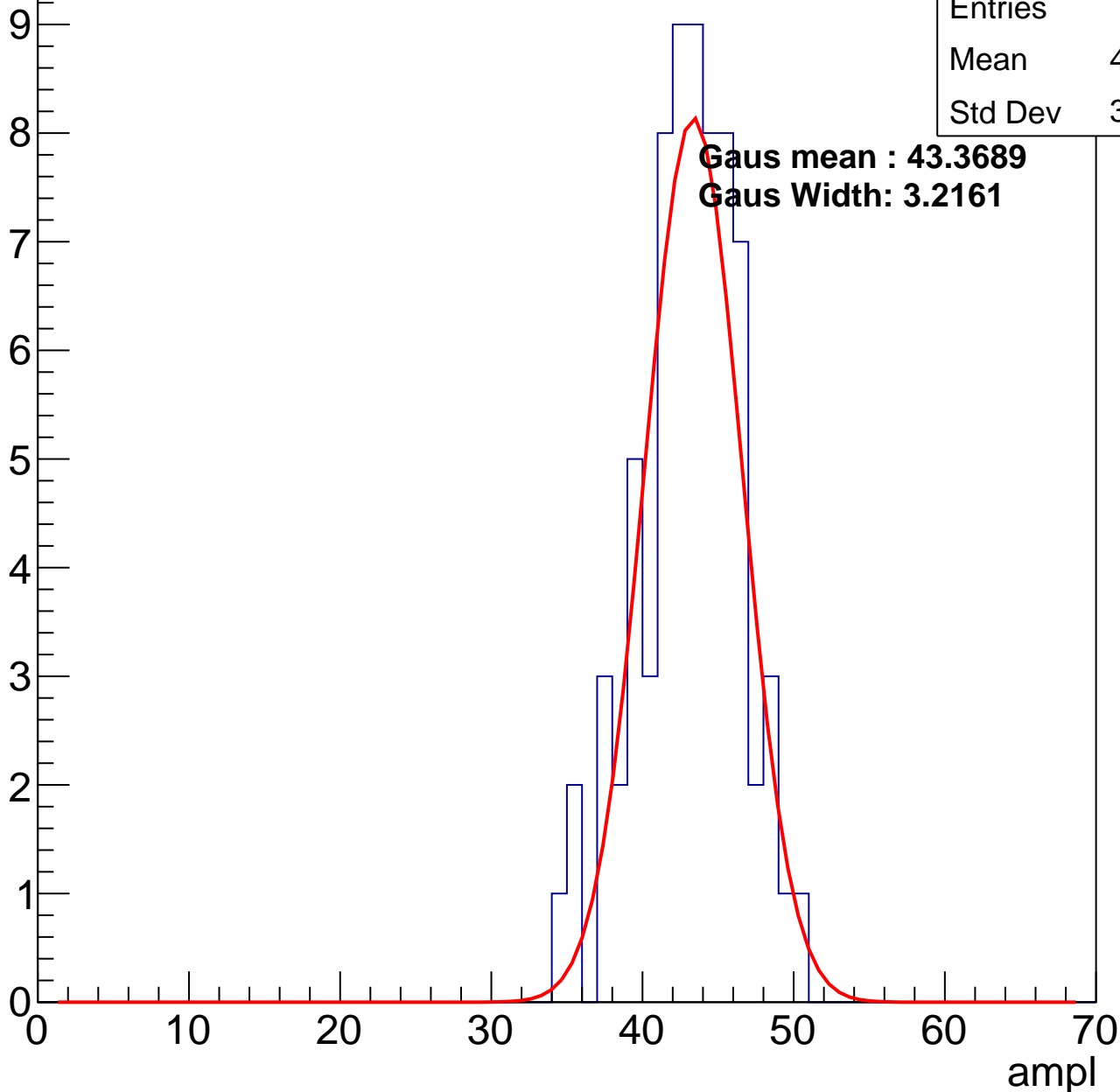
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.64
Std Dev	3.335

Gaus mean : 43.3689

Gaus Width: 3.2161

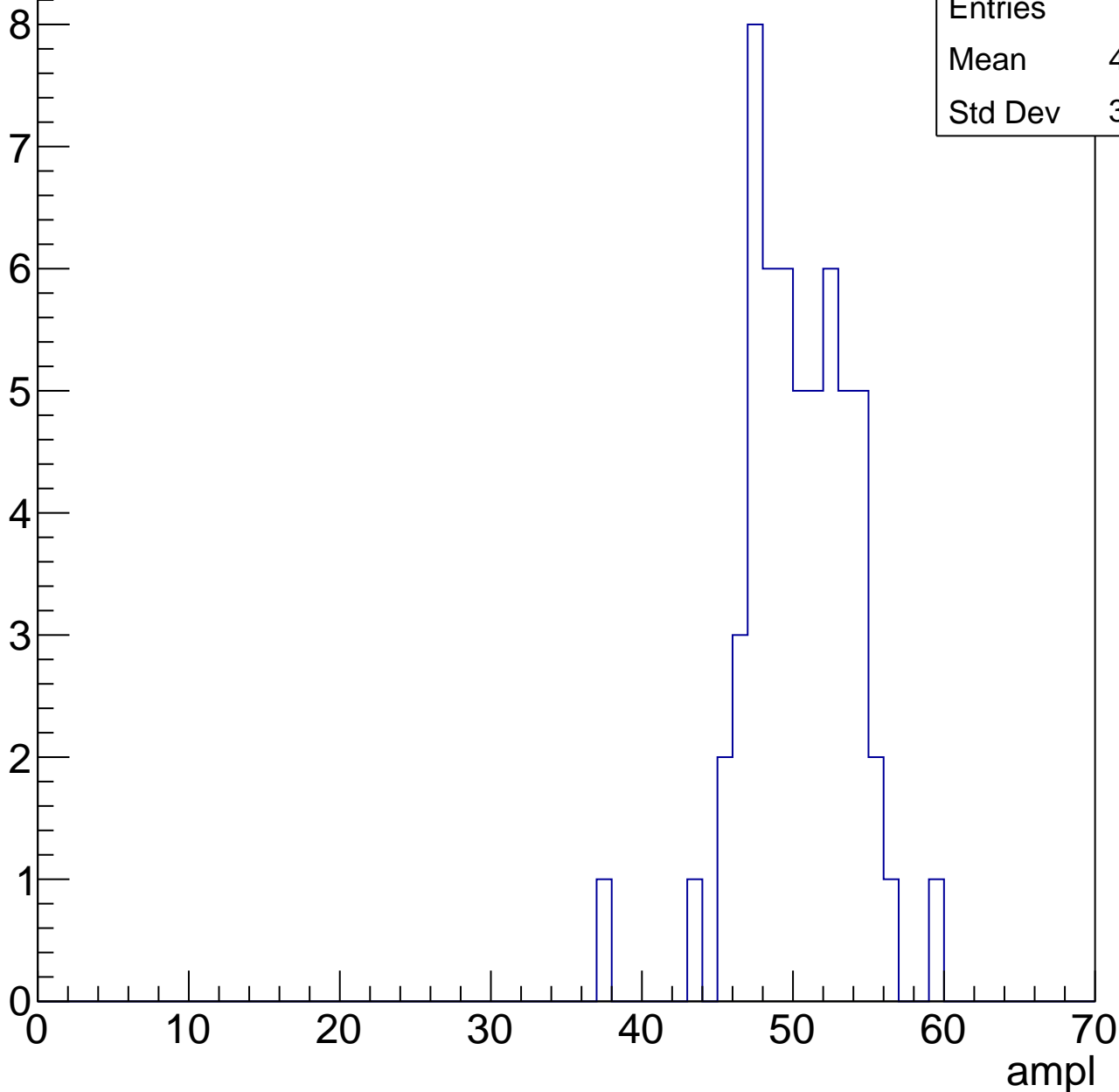


B1L103S, U5-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

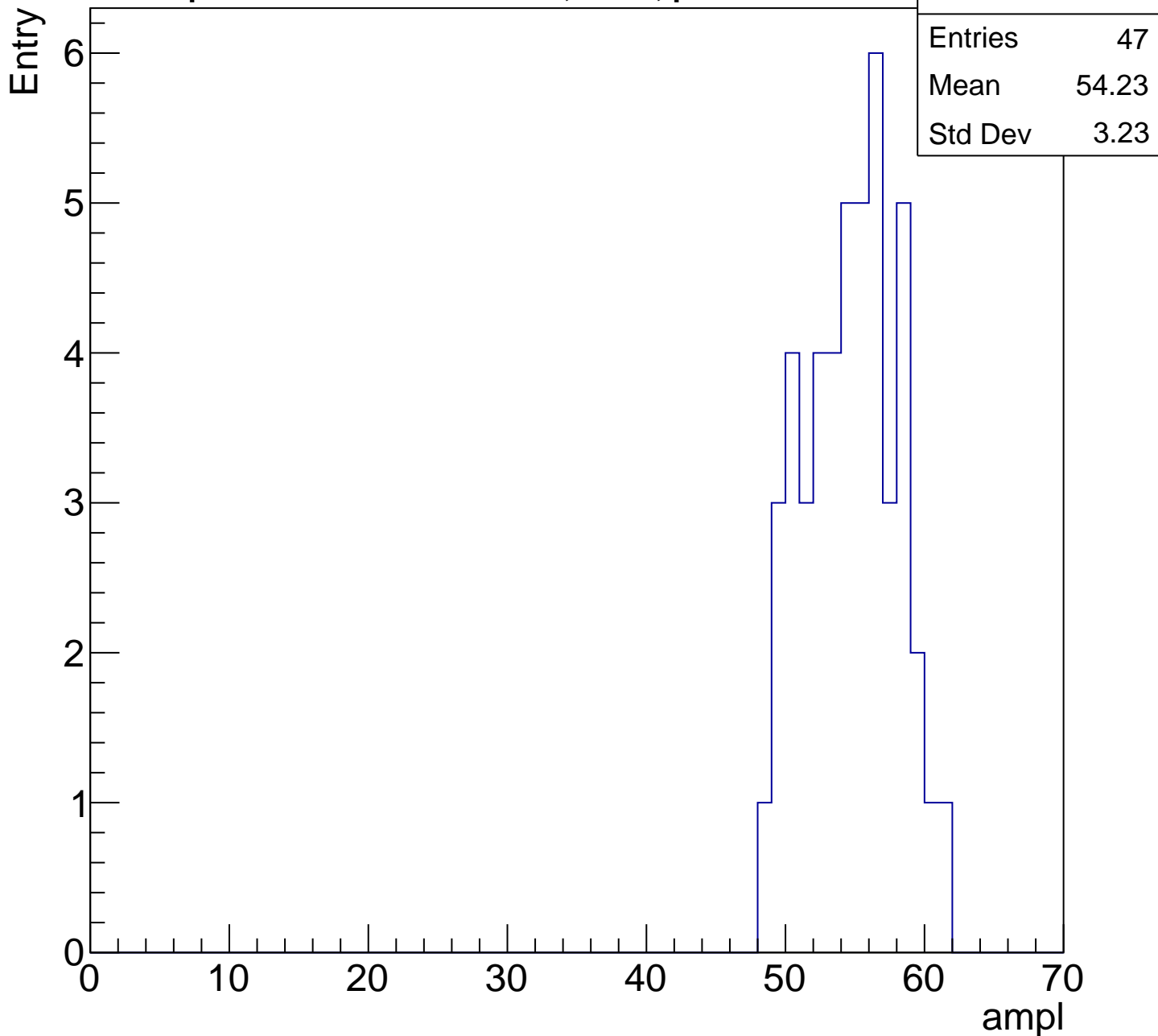
Entry

Entries	57
Mean	49.88
Std Dev	3.599



B1L103S, U5-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

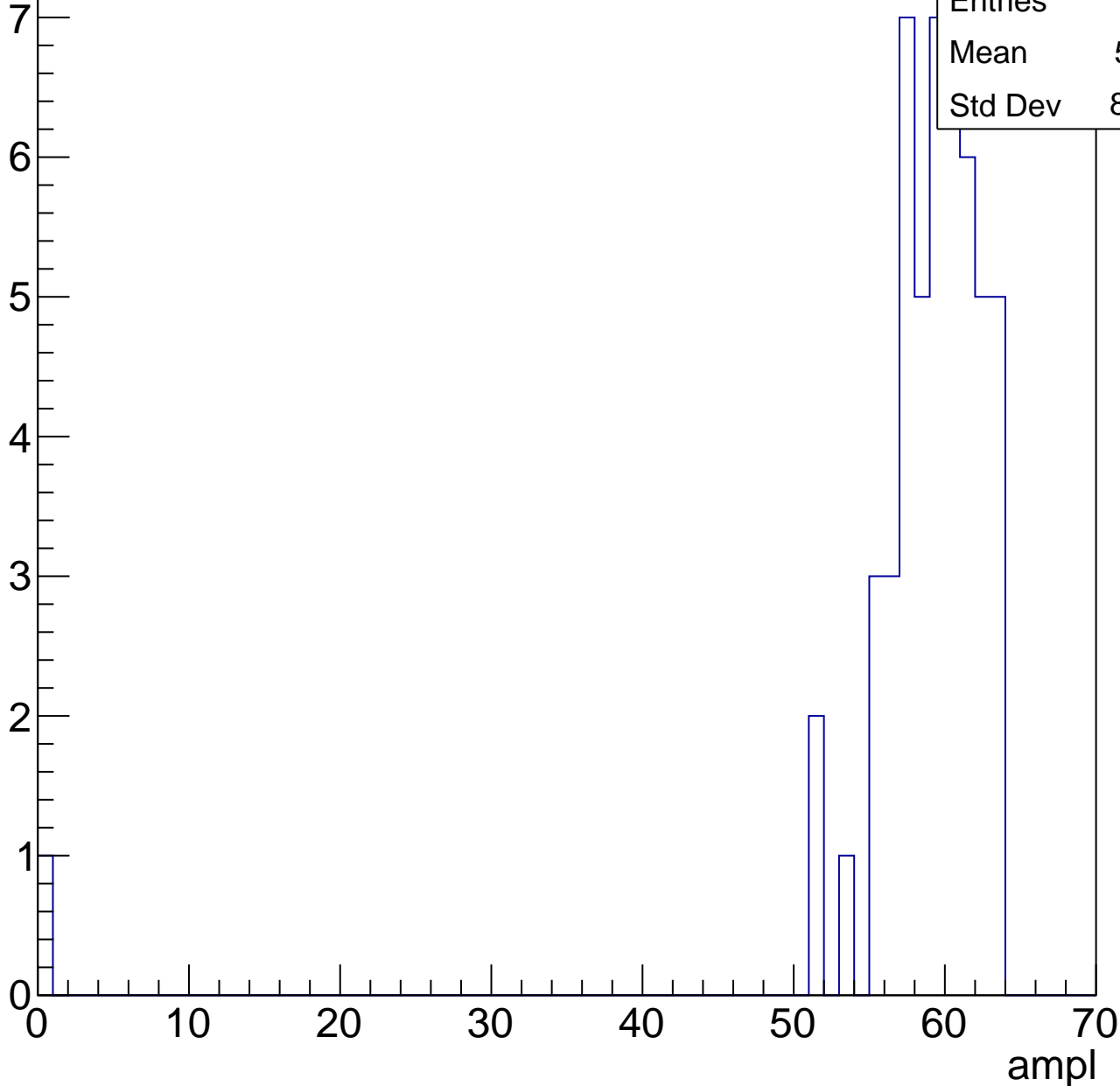


B1L103S, U5-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	57.71
Std Dev	8.574

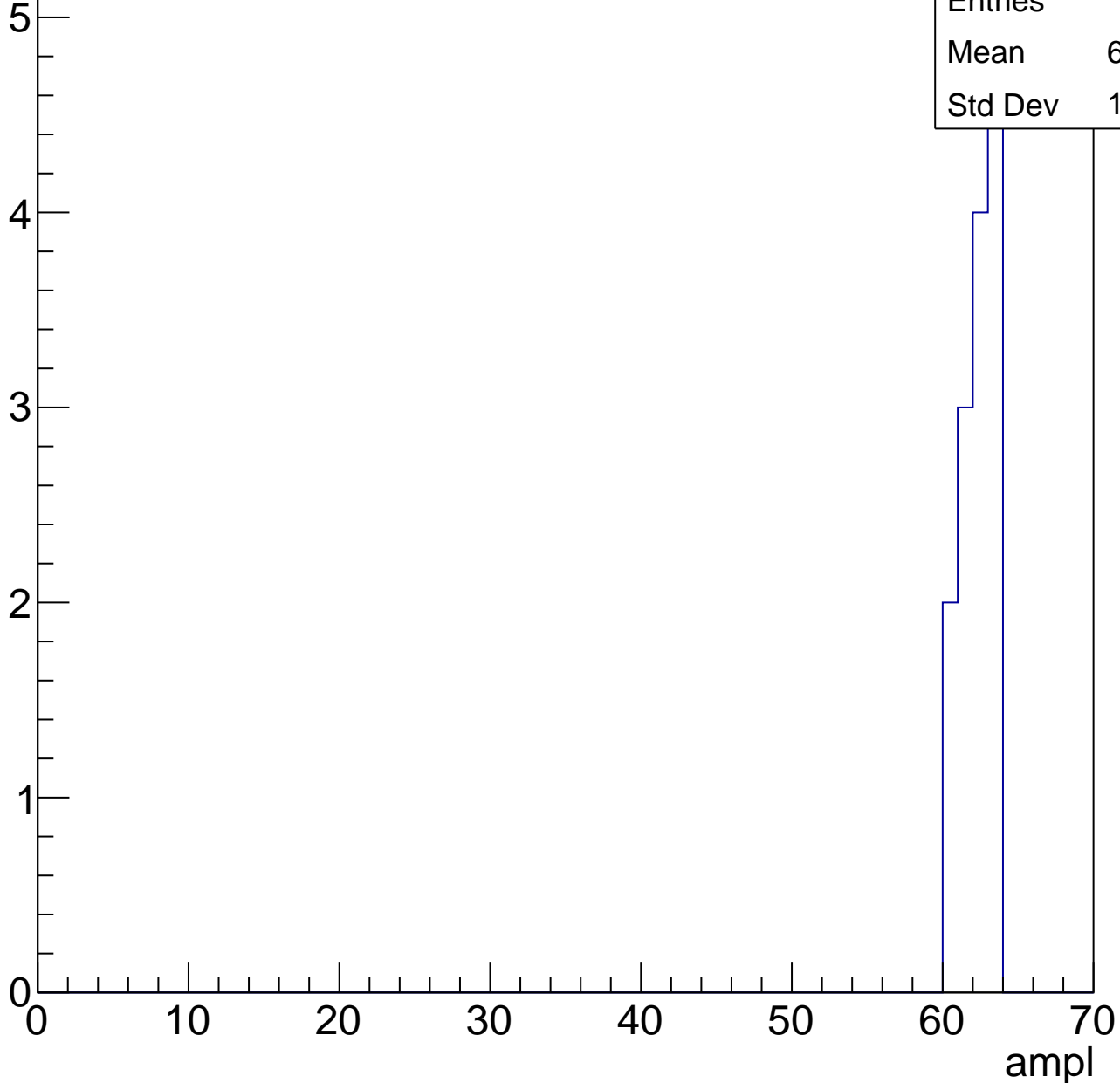


B1L103S, U5-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	61.86
Std Dev	1.059



B1L103S, U5-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch43, adc0

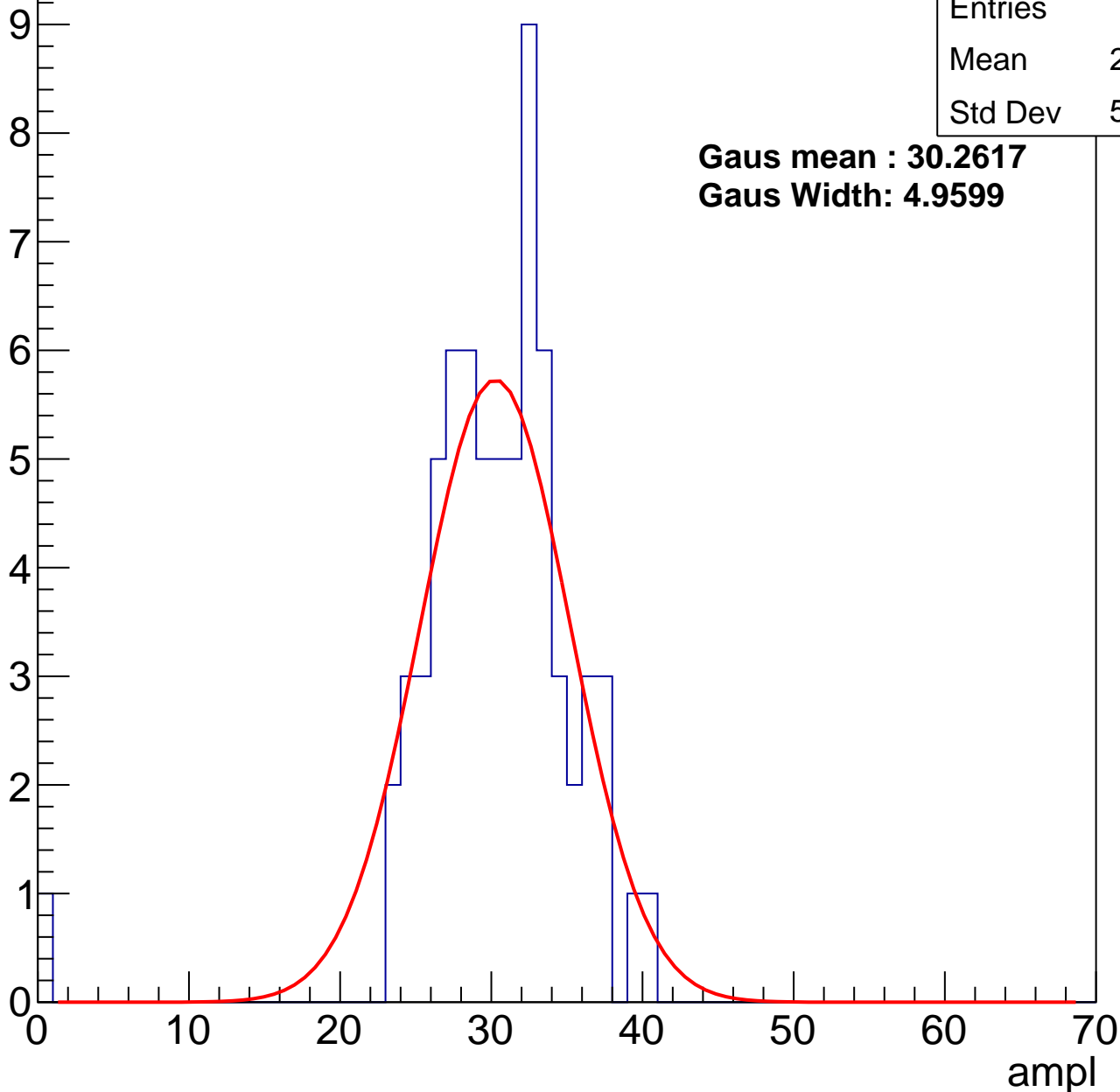
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	29.84
Std Dev	5.345

Gaus mean : 30.2617

Gaus Width: 4.9599



B1L103S, U5-ch43, adc1

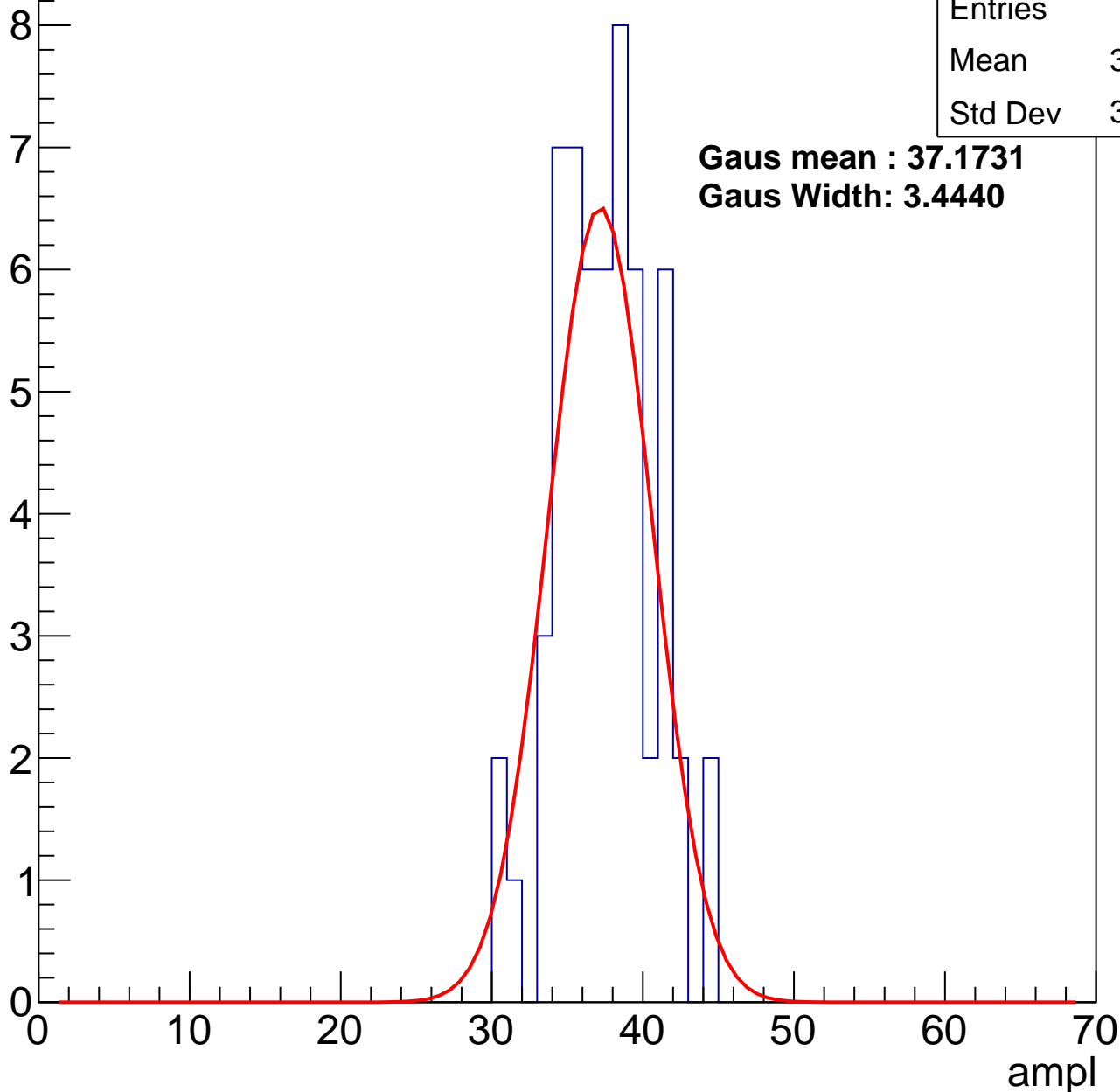
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	37.02
Std Dev	3.143

Gaus mean : 37.1731

Gaus Width: 3.4440



B1L103S, U5-ch43, adc2

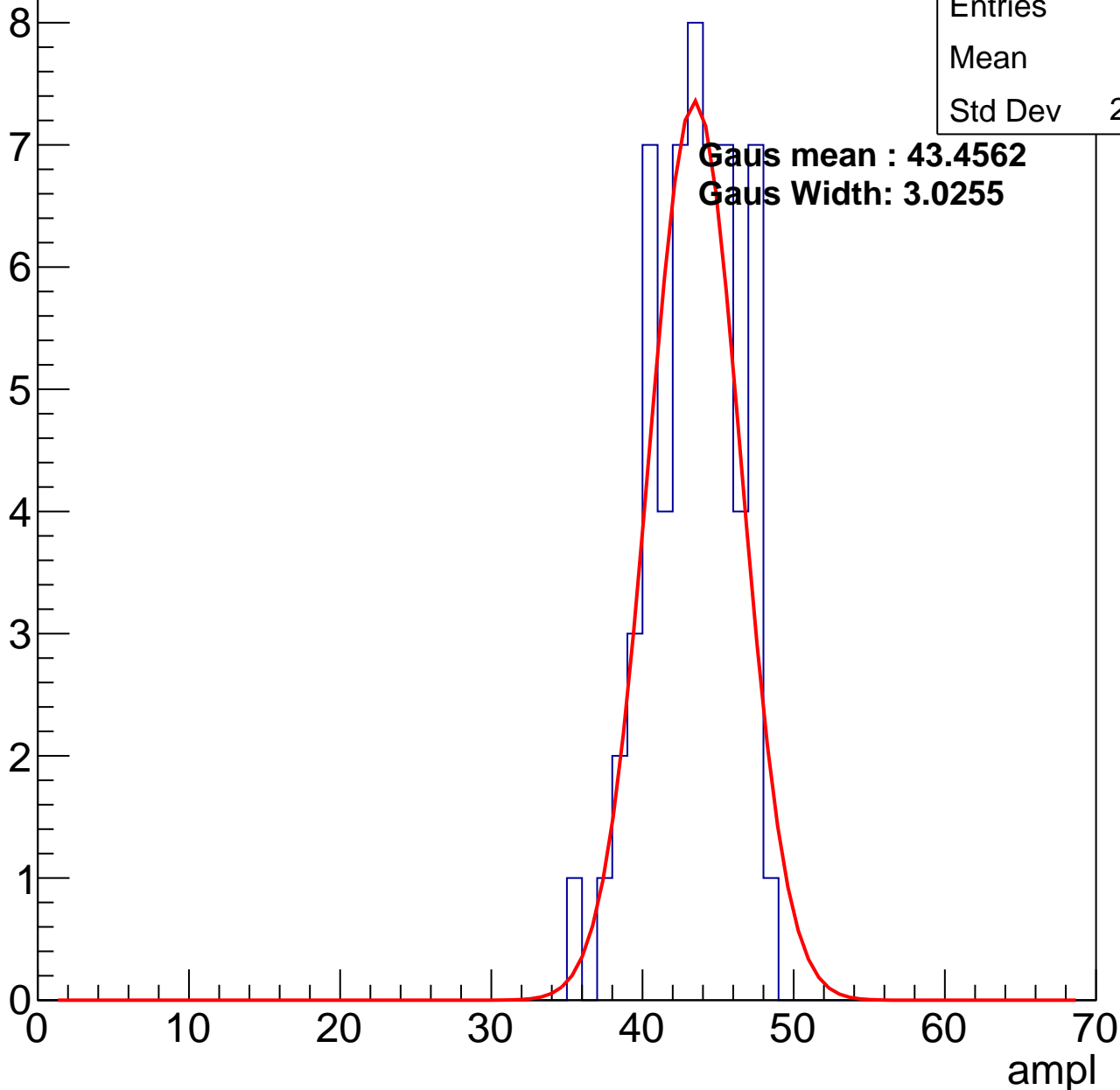
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.9
Std Dev	2.886

Gaus mean : 43.4562

Gaus Width: 3.0255

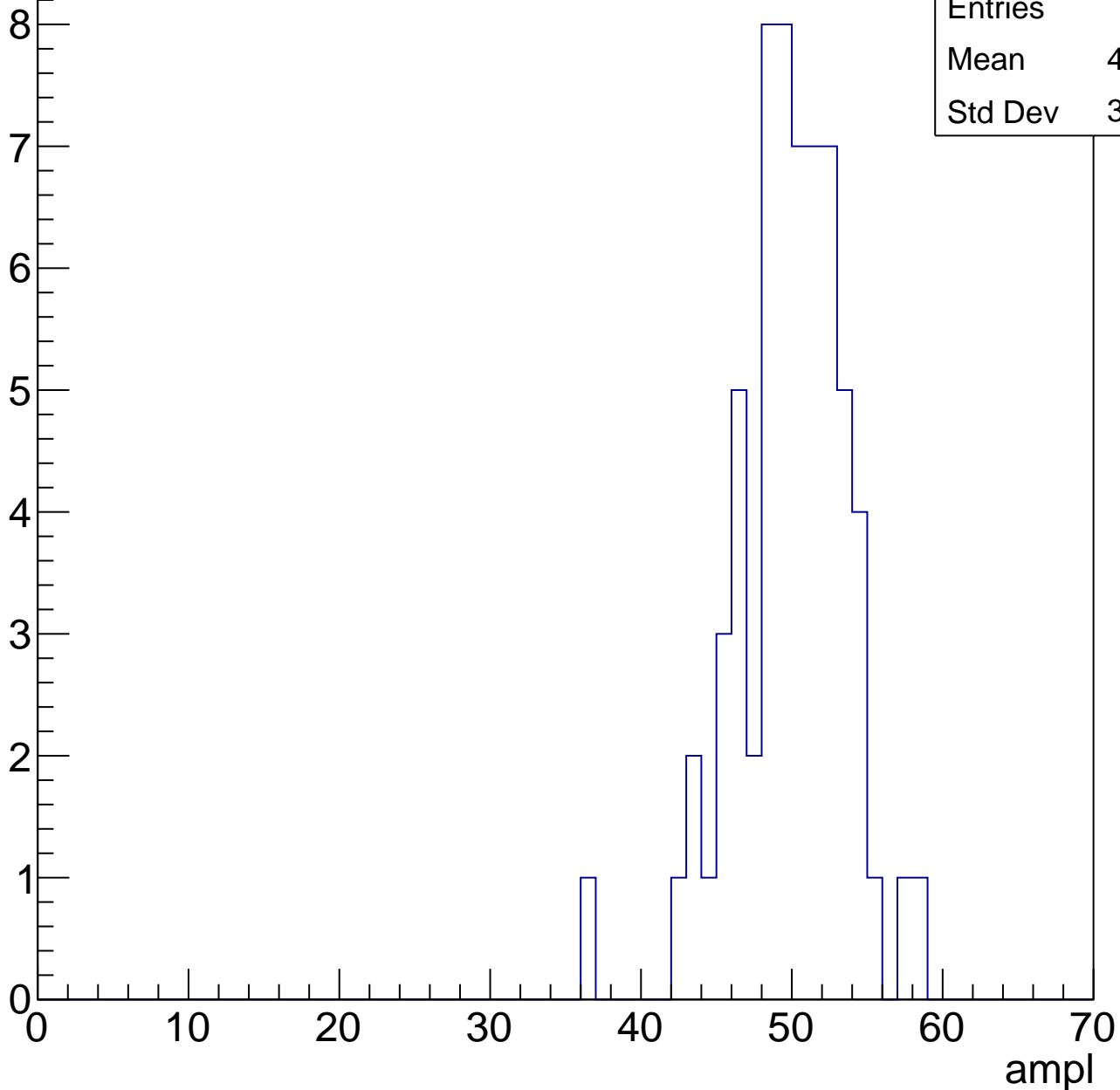


B1L103S, U5-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

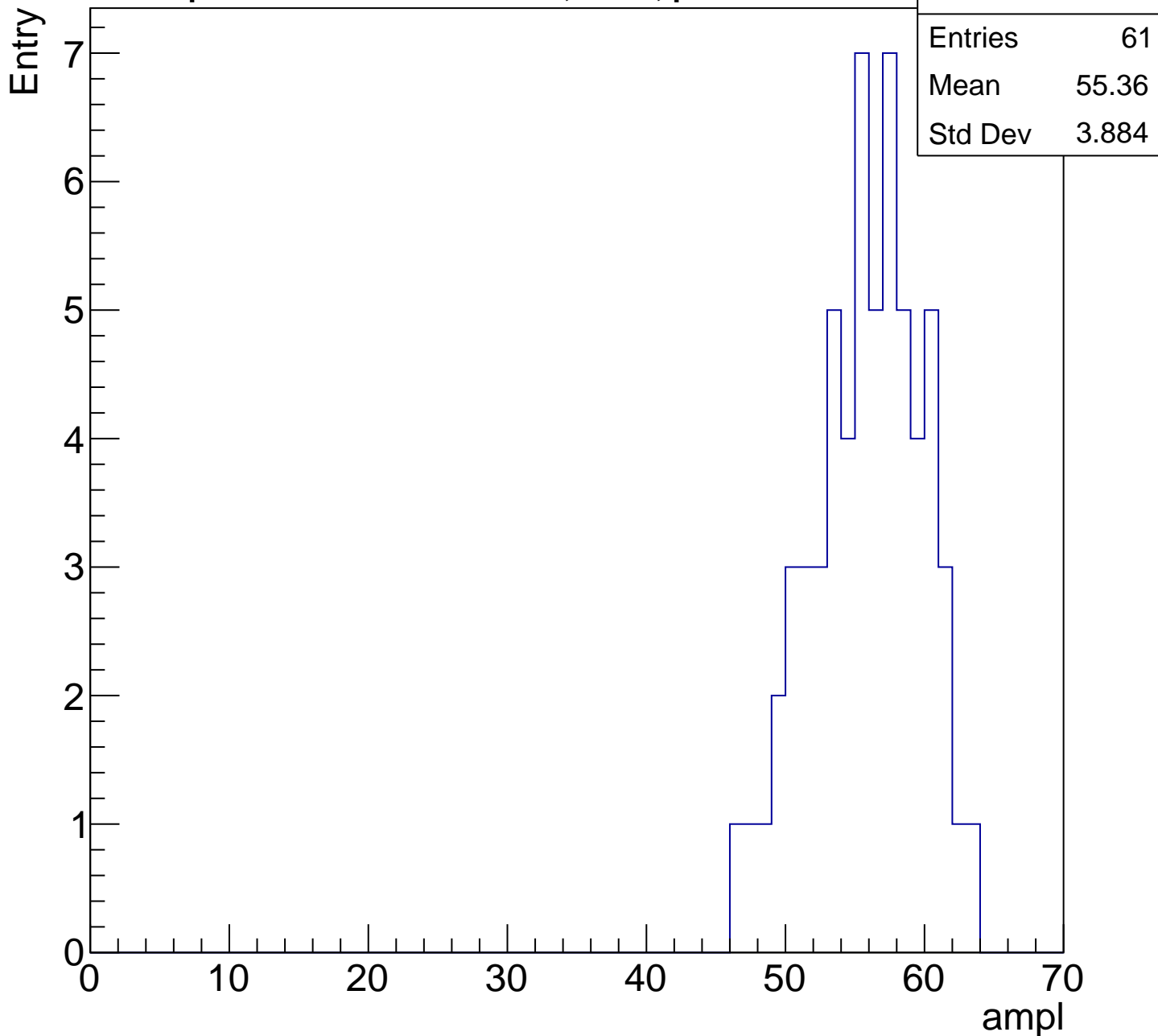
Entry

Entries	64
Mean	49.45
Std Dev	3.687



B1L103S, U5-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

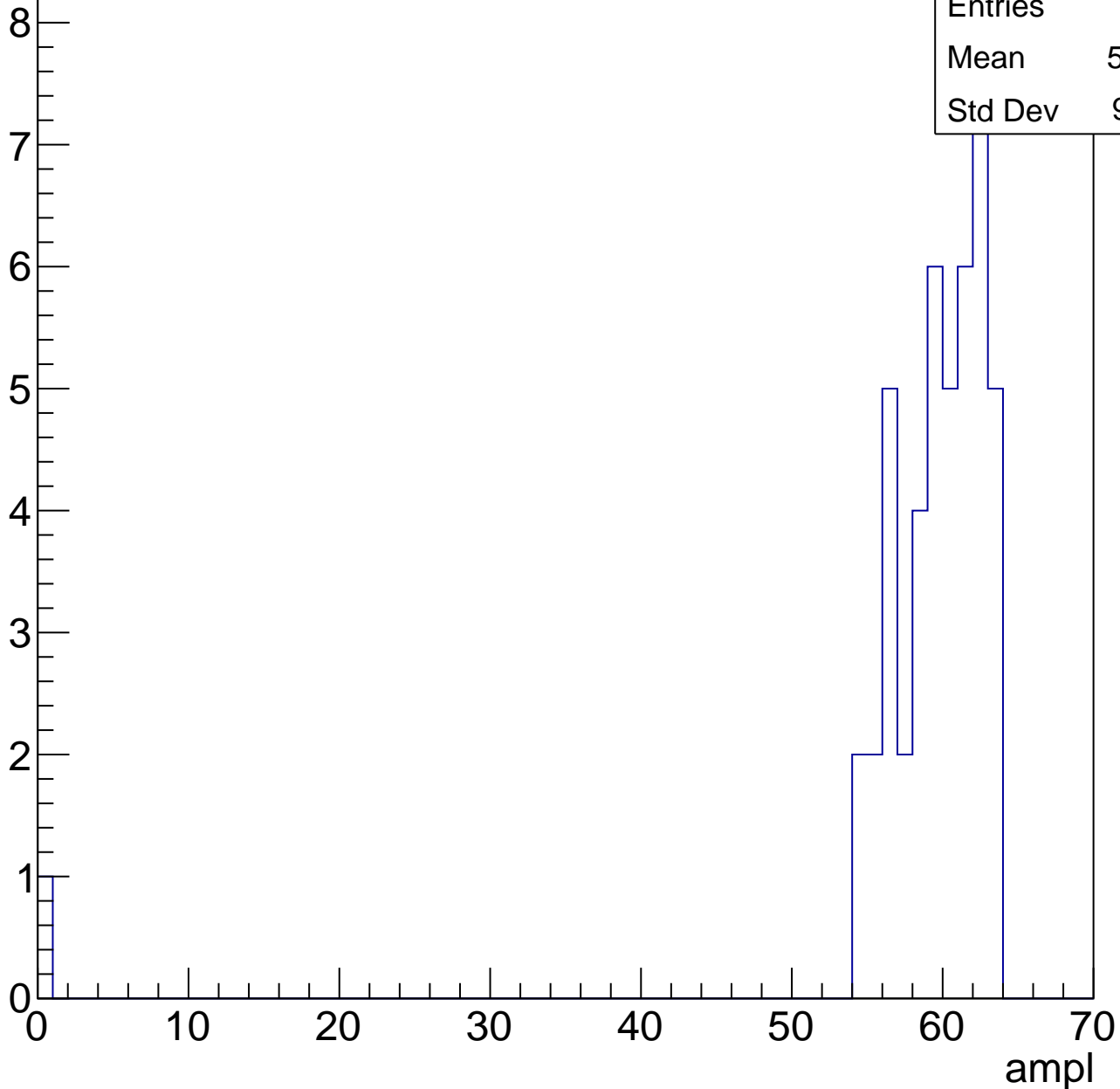


B1L103S, U5-ch43, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

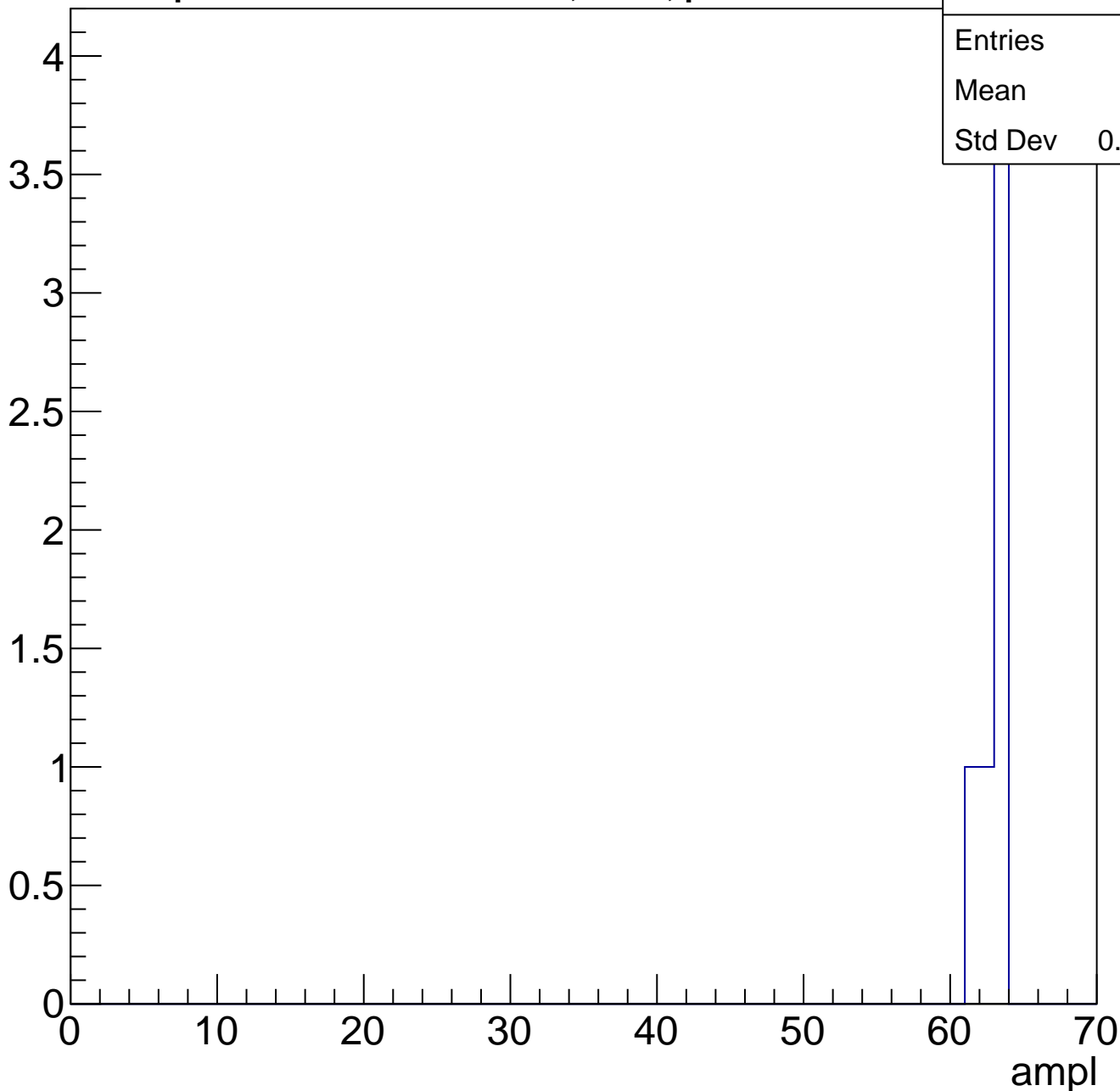
Entries	46
Mean	58.15
Std Dev	9.051



B1L103S, U5-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch44, adc0

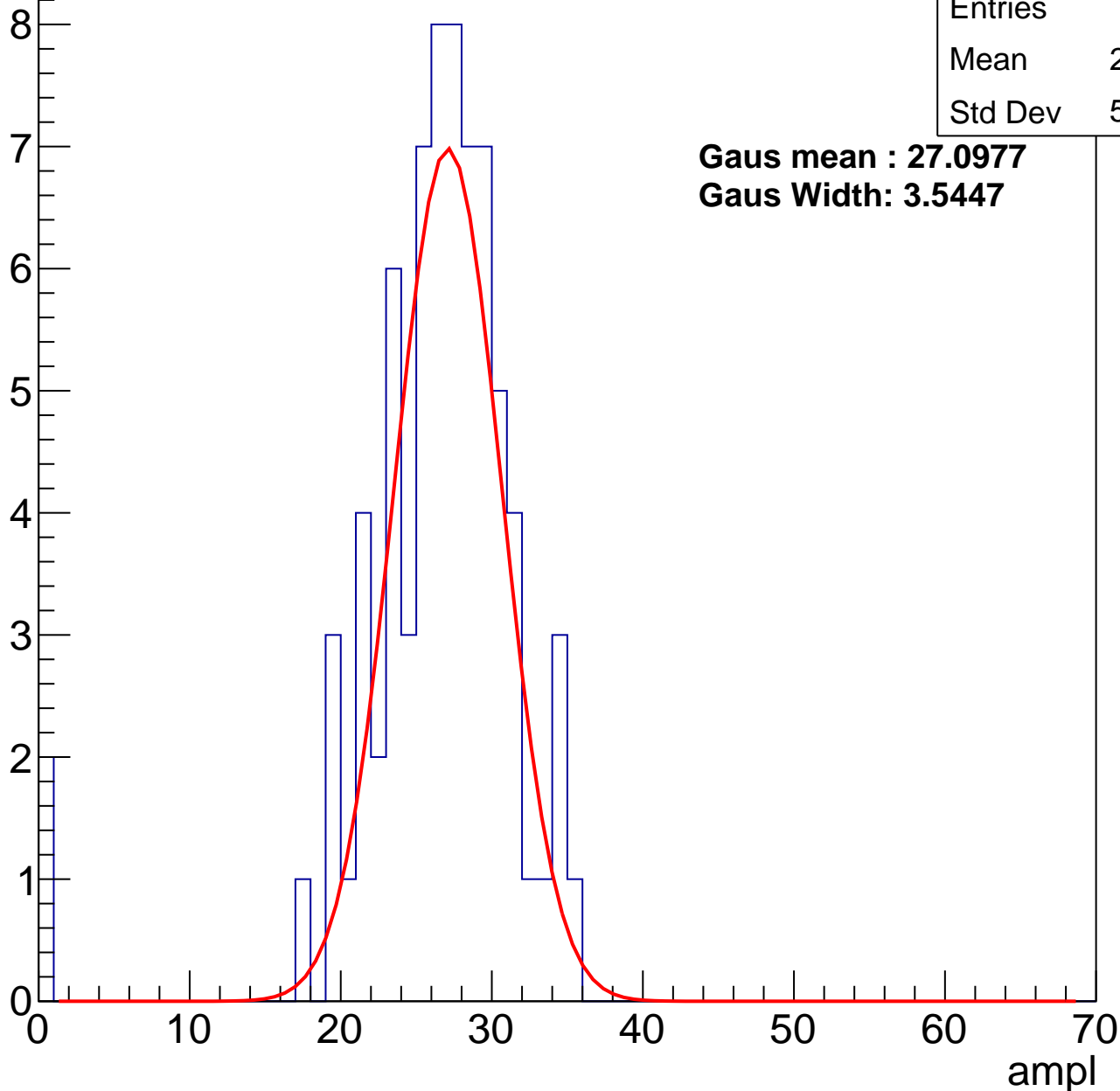
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	25.76
Std Dev	5.758

Gaus mean : 27.0977

Gaus Width: 3.5447



B1L103S, U5-ch44, adc1

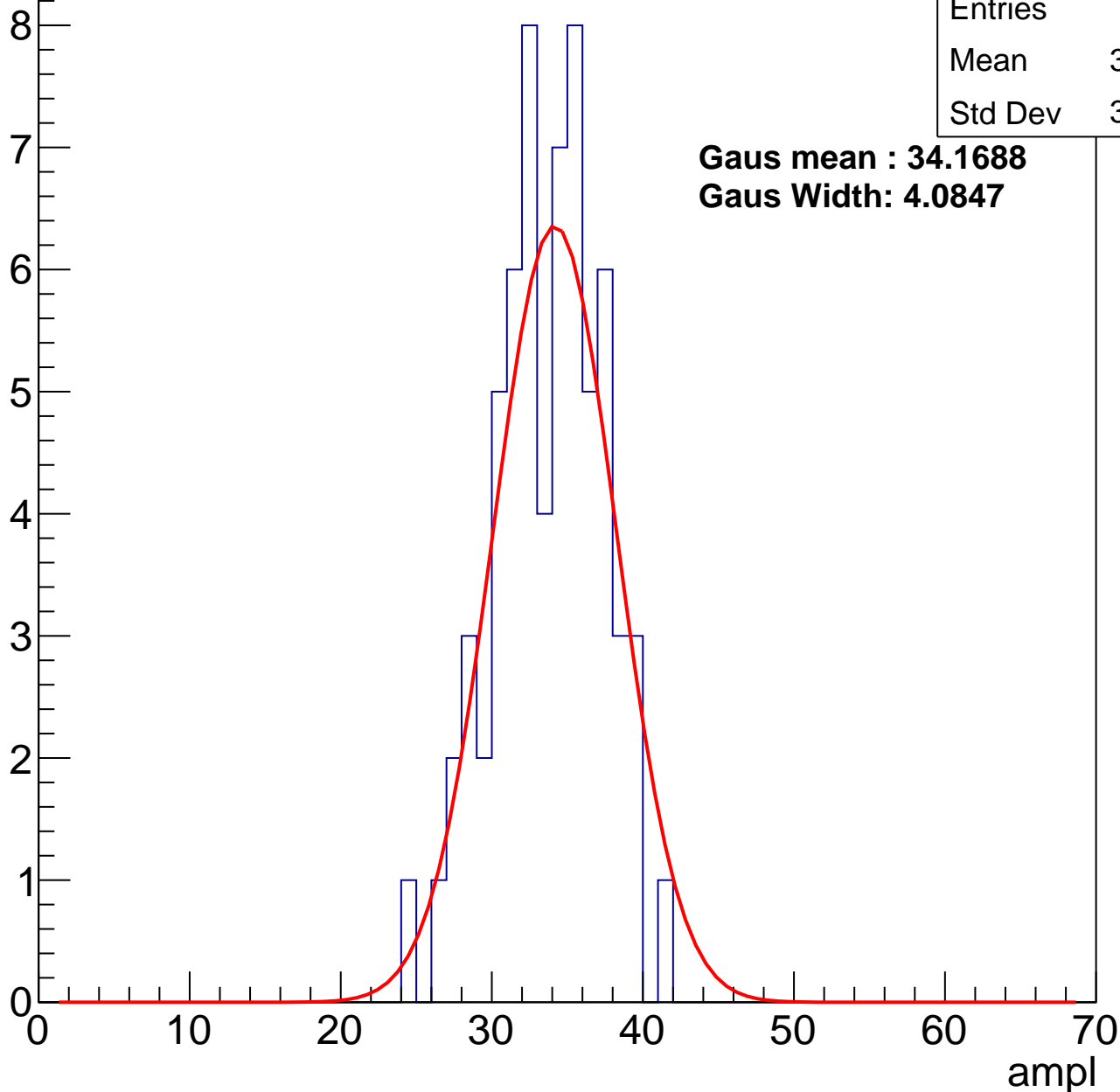
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	33.26
Std Dev	3.527

Gaus mean : 34.1688

Gaus Width: 4.0847



B1L103S, U5-ch44, adc2

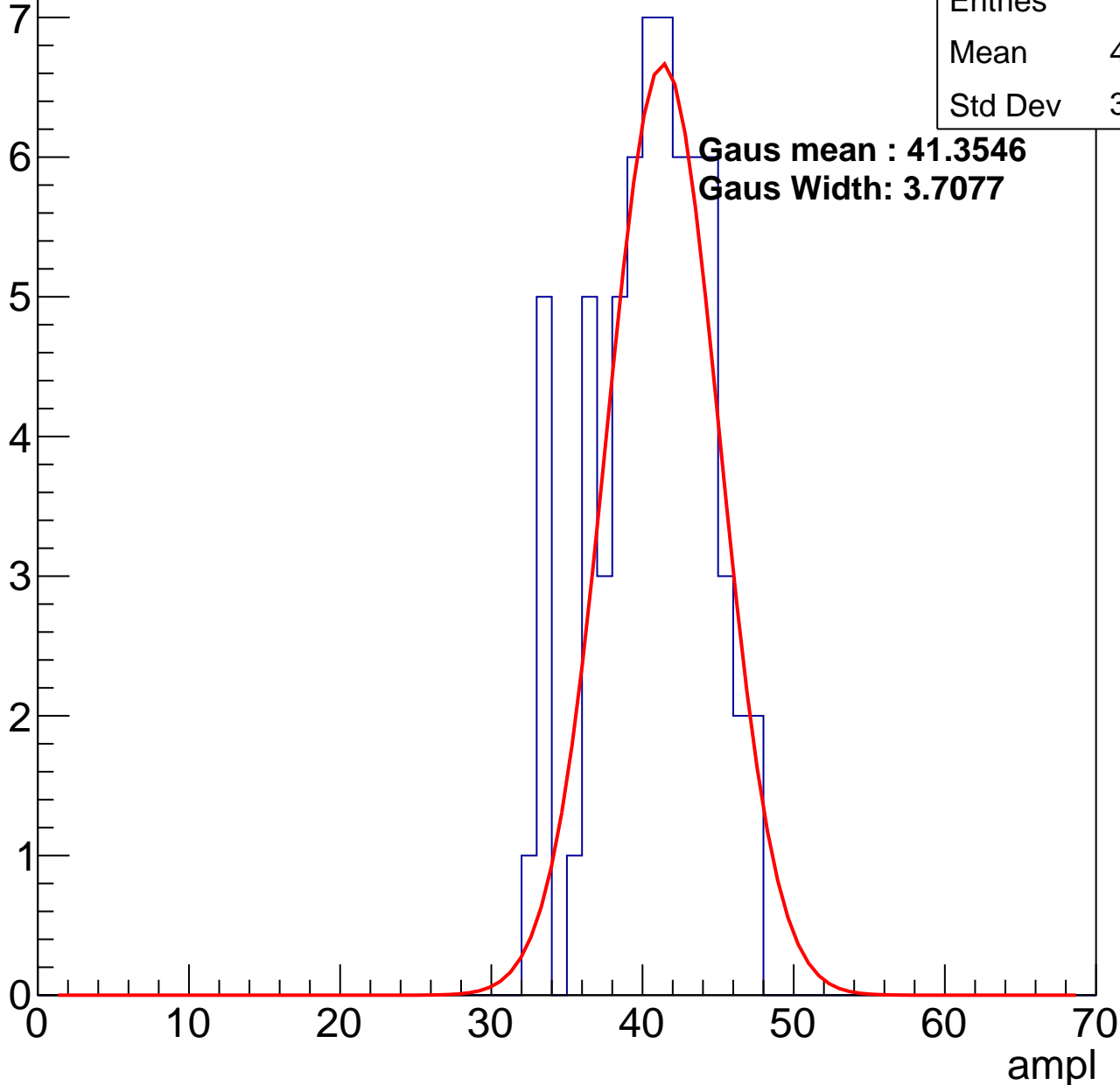
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	40.14
Std Dev	3.704

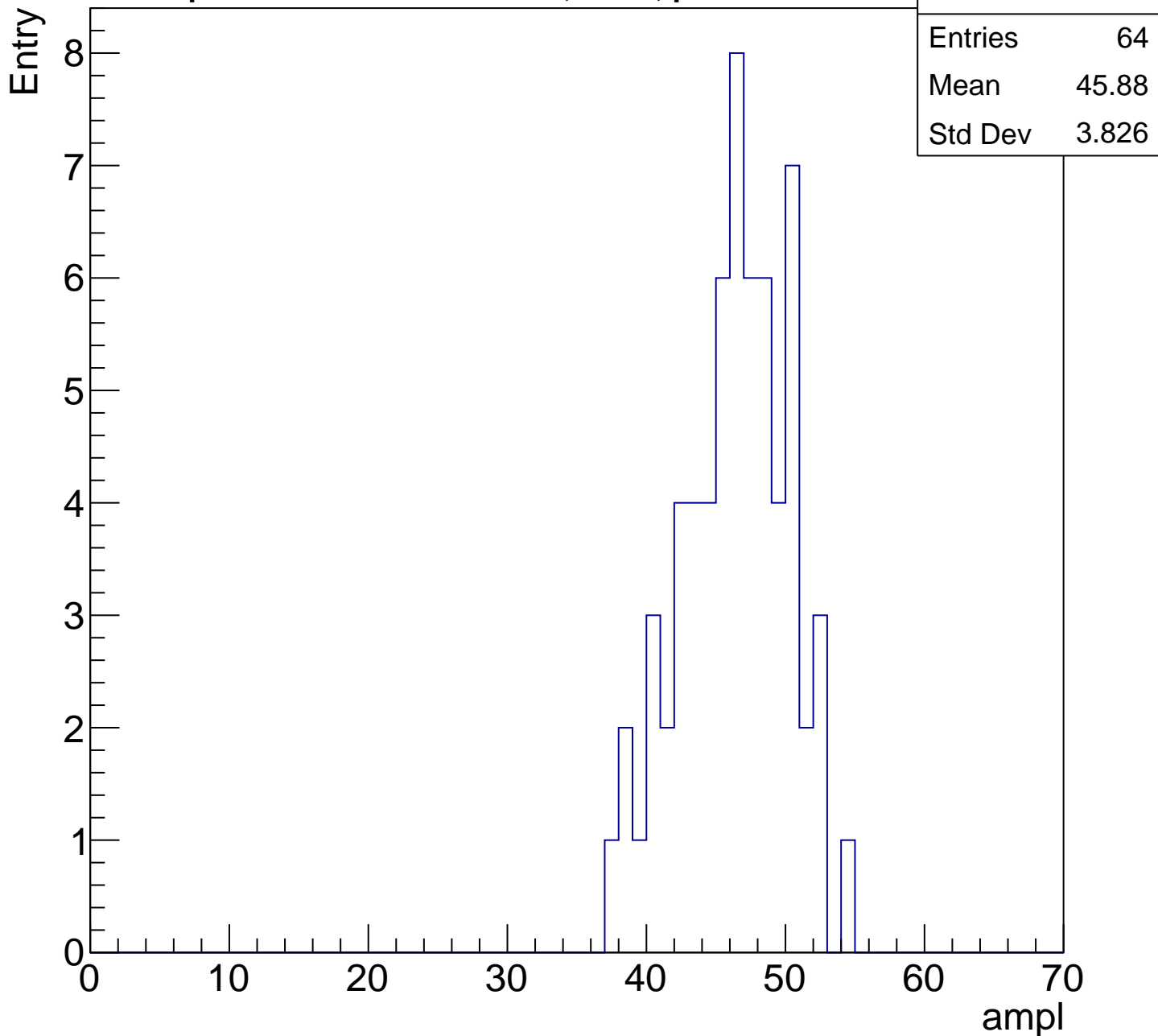
Gaus mean : 41.3546

Gaus Width: 3.7077



B1L103S, U5-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

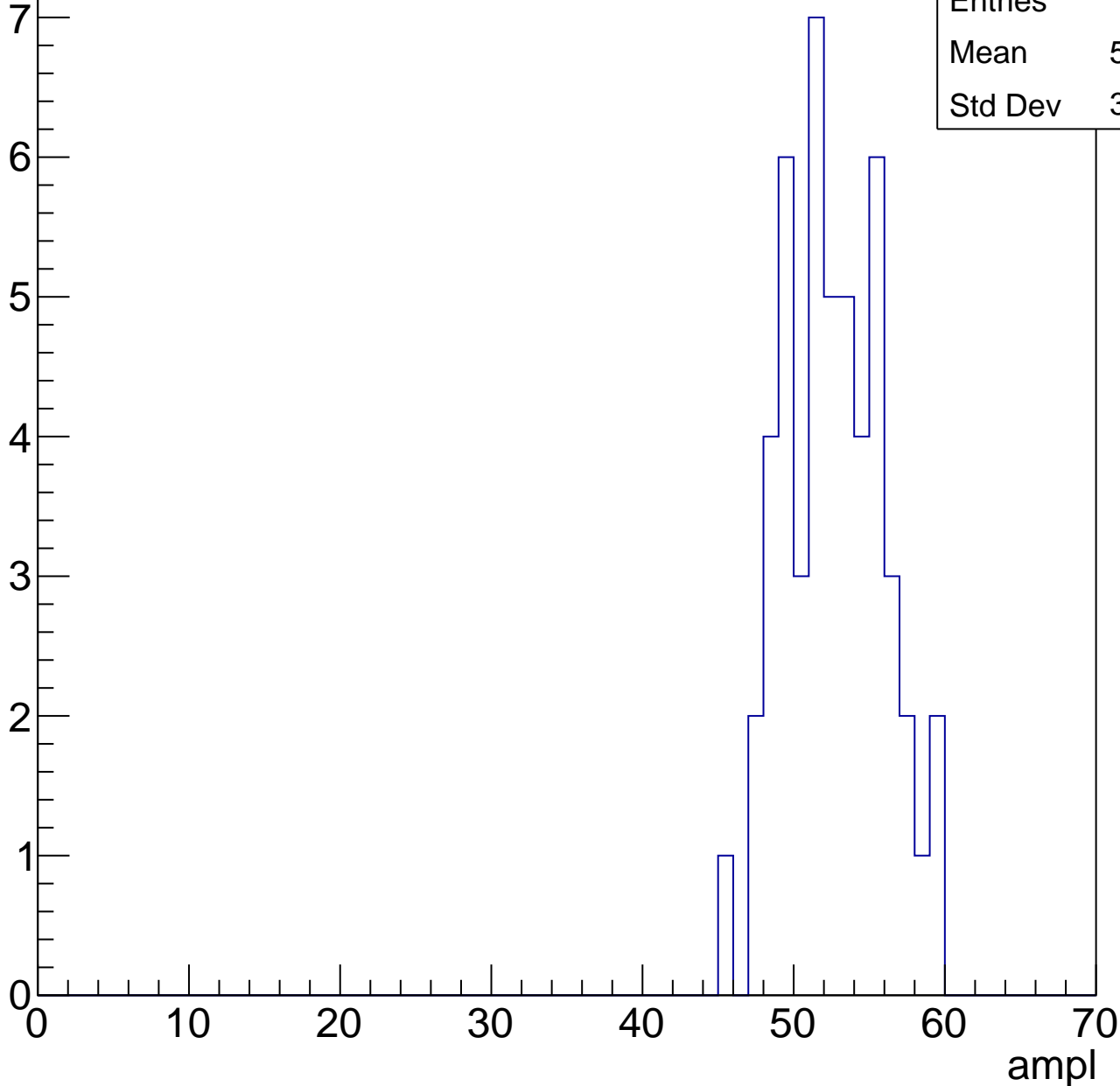


B1L103S, U5-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	52.18
Std Dev	3.258



B1L103S, U5-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

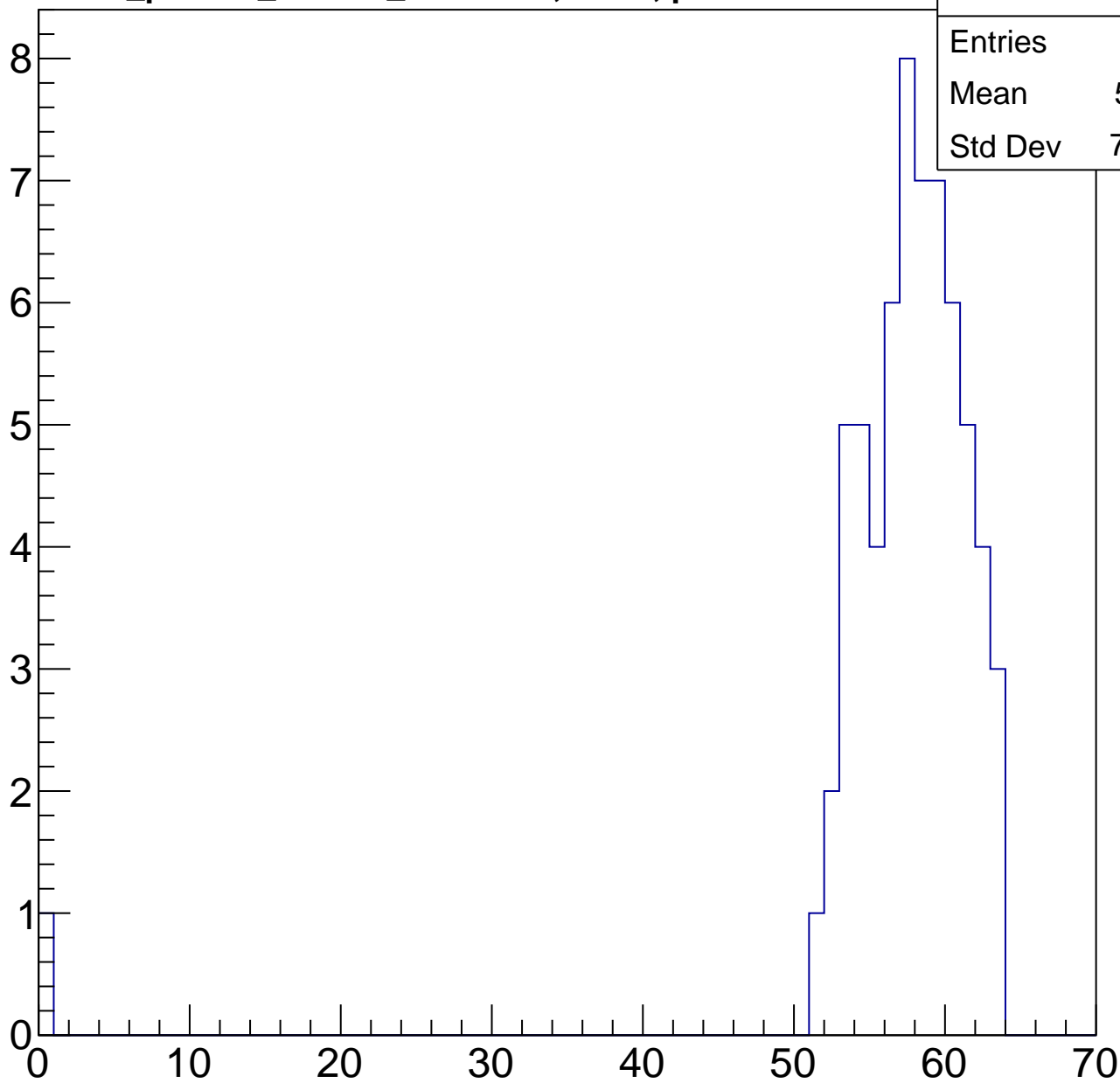
Entry

8
7
6
5
4
3
2
1
0

Entries	64
Mean	56.61
Std Dev	7.755

ampl

0 10 20 30 40 50 60 70

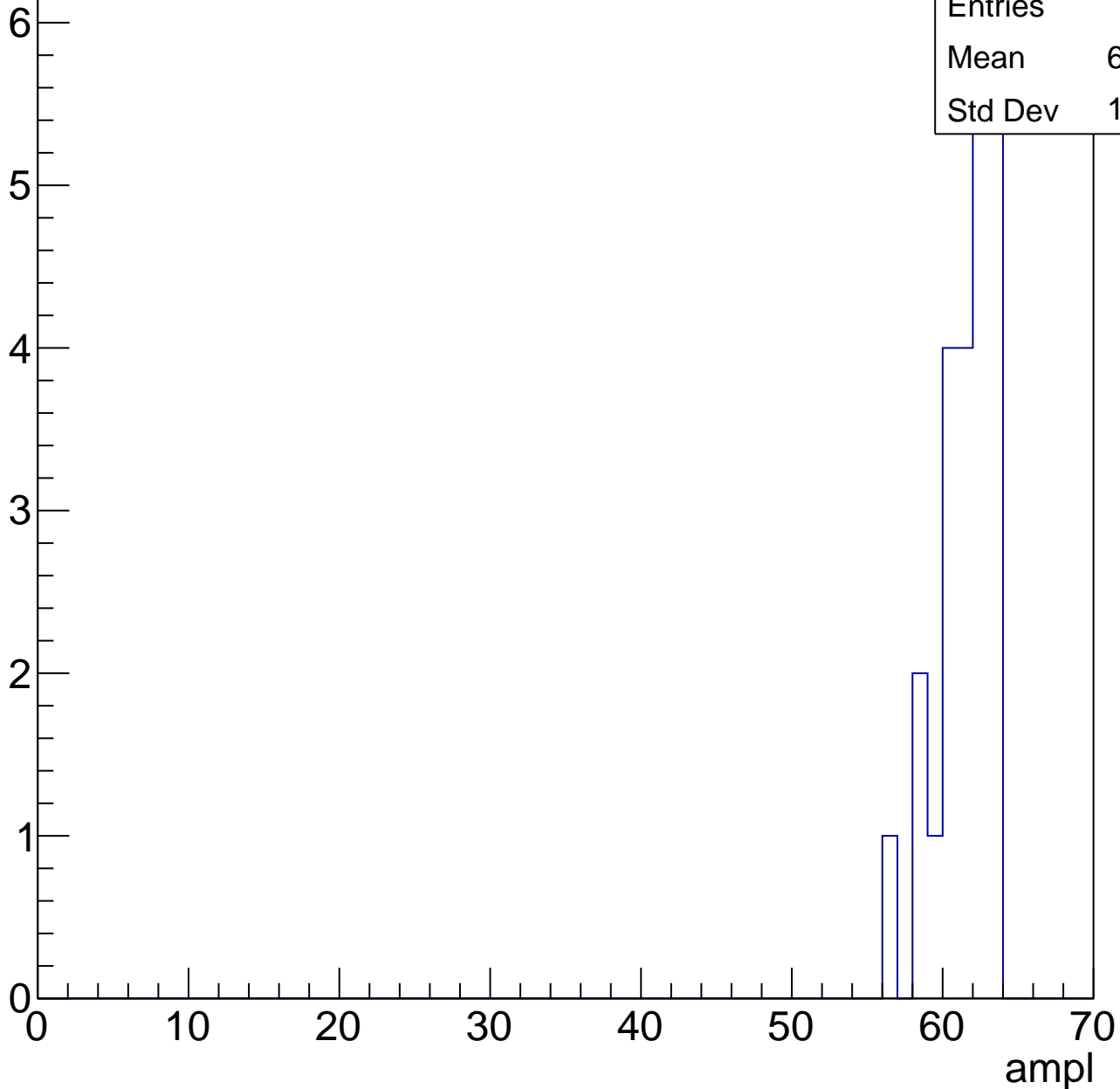


B1L103S, U5-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

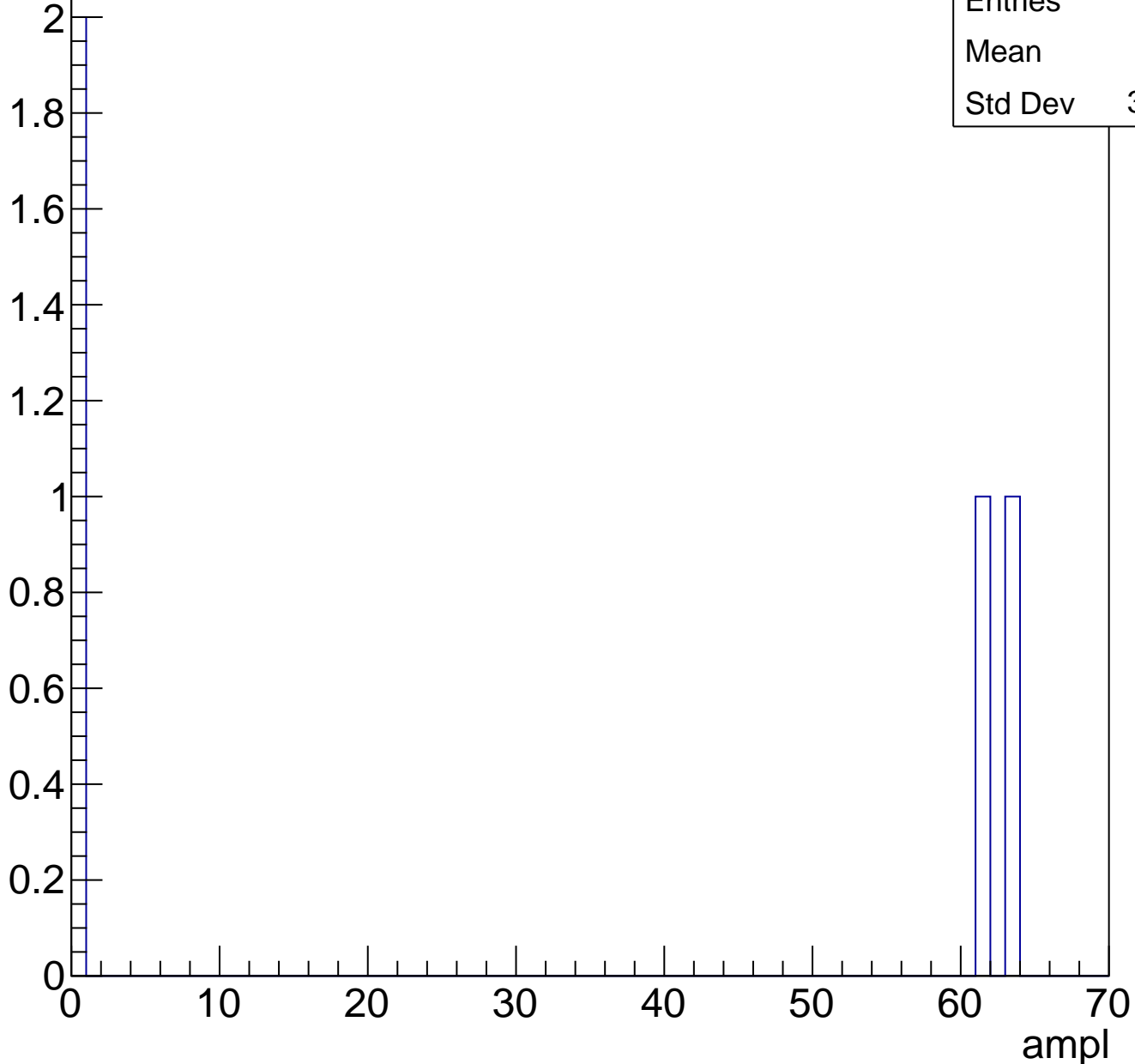
Entries	24
Mean	61.04
Std Dev	1.837



B1L103S, U5-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	31
Std Dev	31.01

B1L103S, U5-ch45, adc0

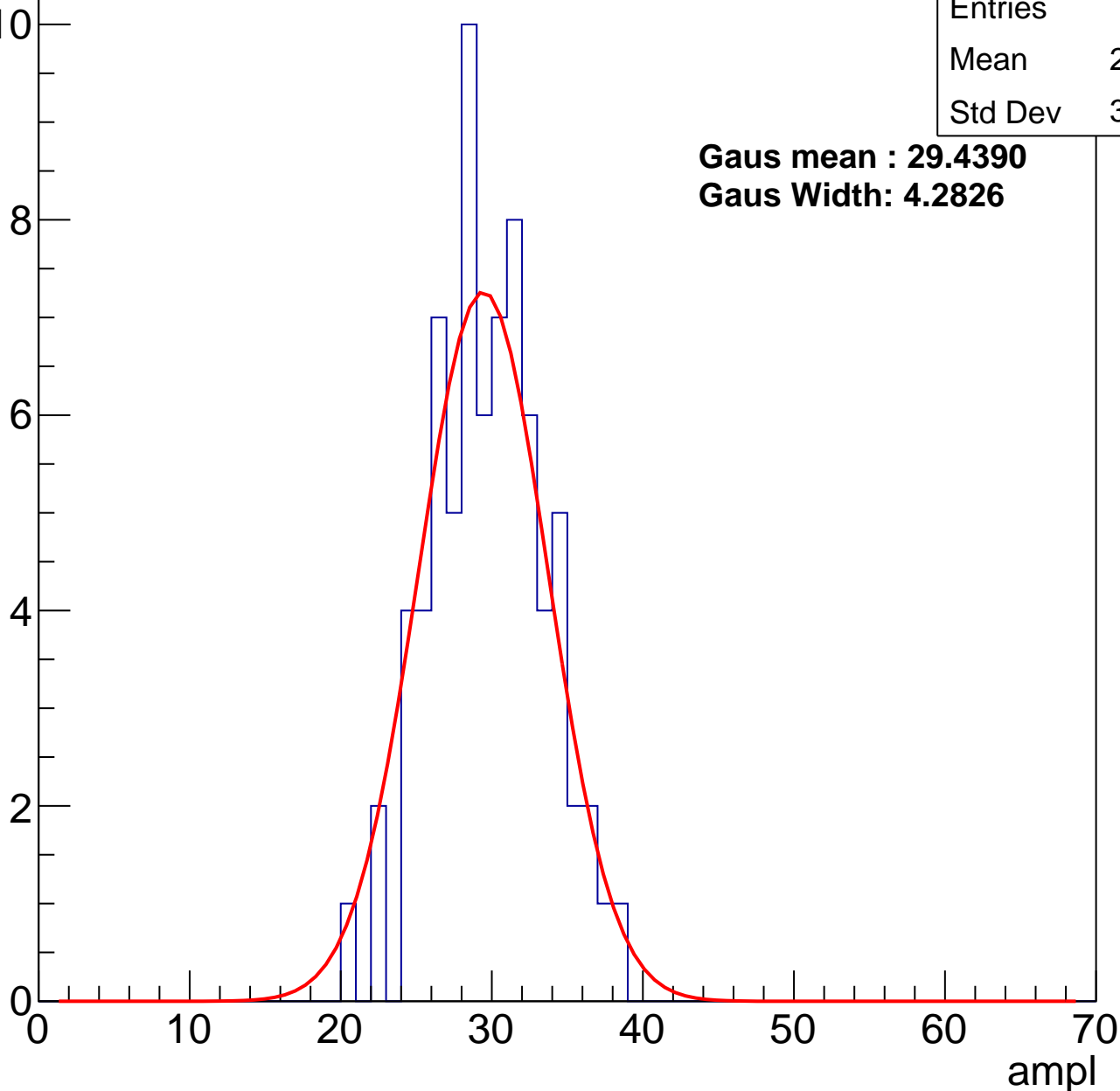
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.33
Std Dev	3.696

Gaus mean : 29.4390

Gaus Width: 4.2826



B1L103S, U5-ch45, adc1

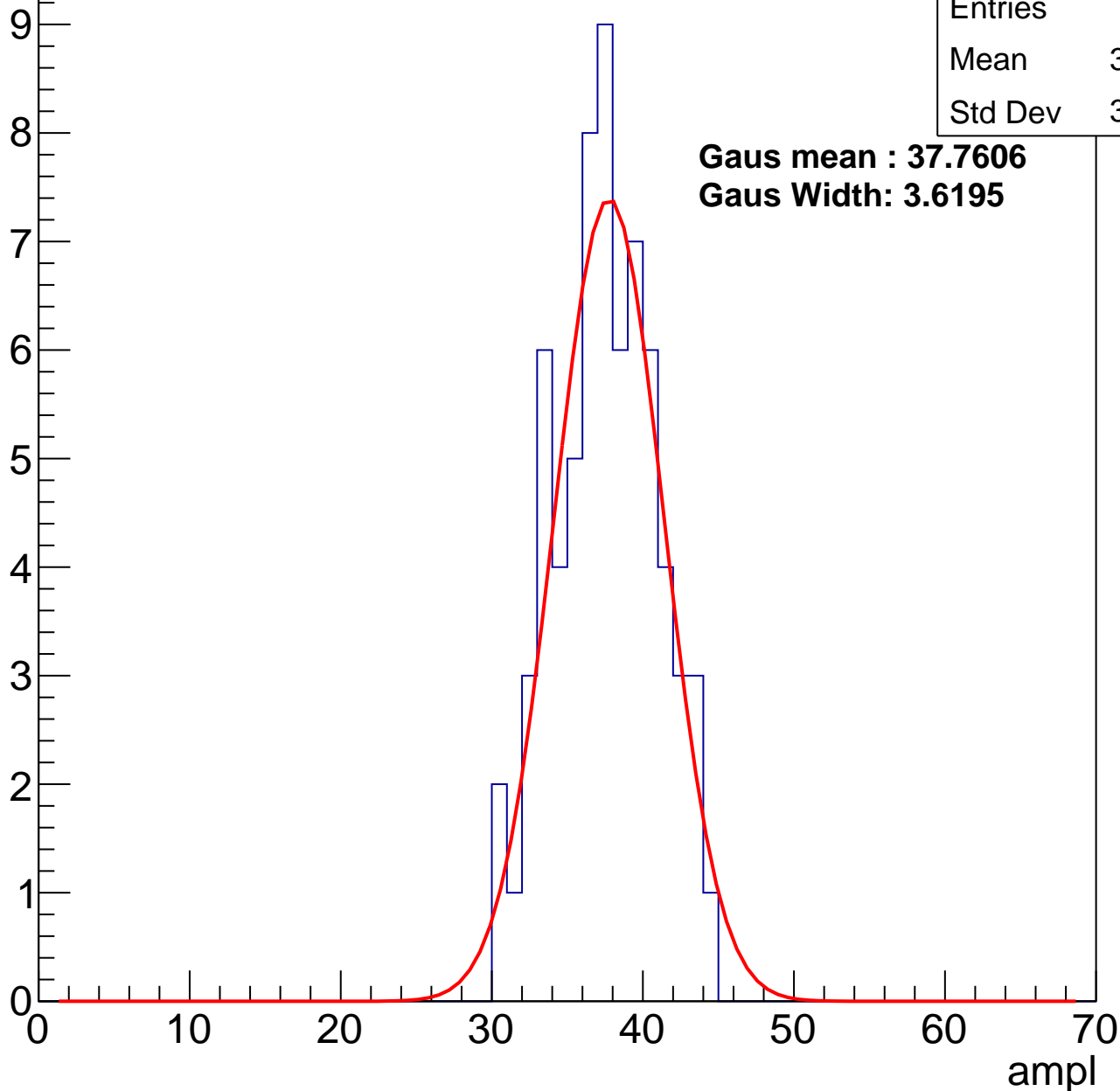
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.07
Std Dev	3.327

Gaus mean : 37.7606

Gaus Width: 3.6195



B1L103S, U5-ch45, adc2

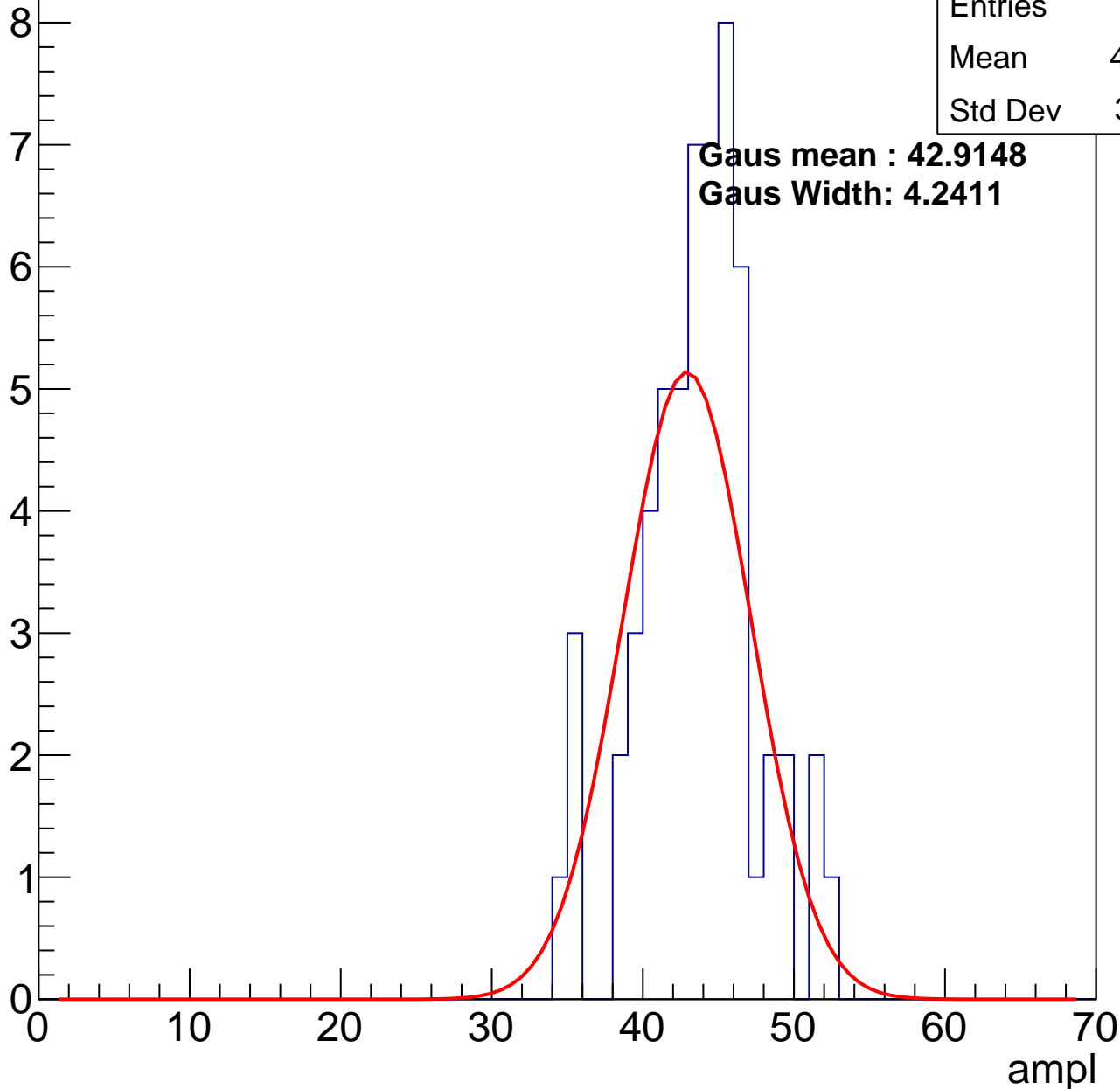
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.17
Std Dev	3.841

Gaus mean : 42.9148

Gaus Width: 4.2411

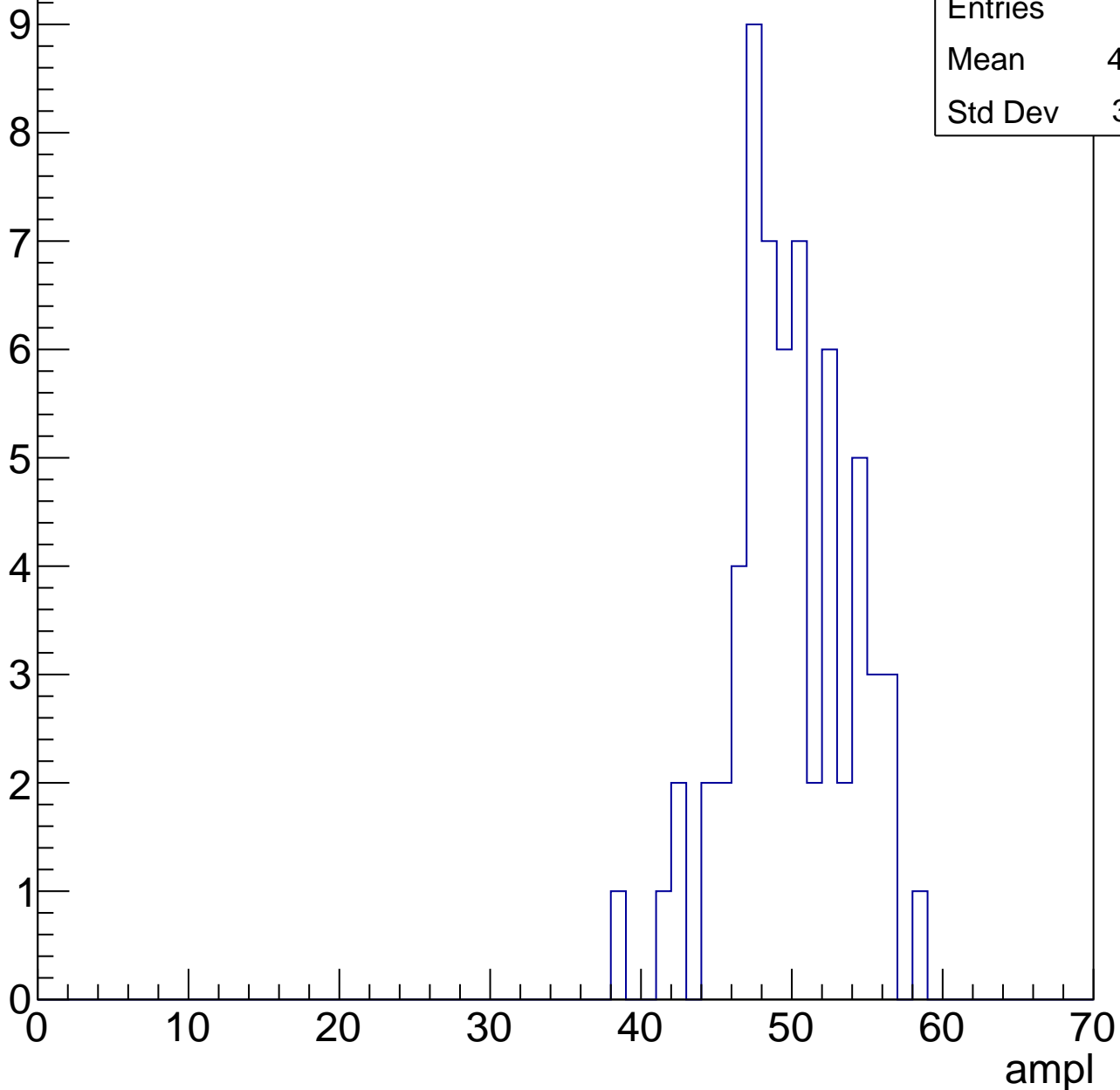


B1L103S, U5-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.35
Std Dev	3.981

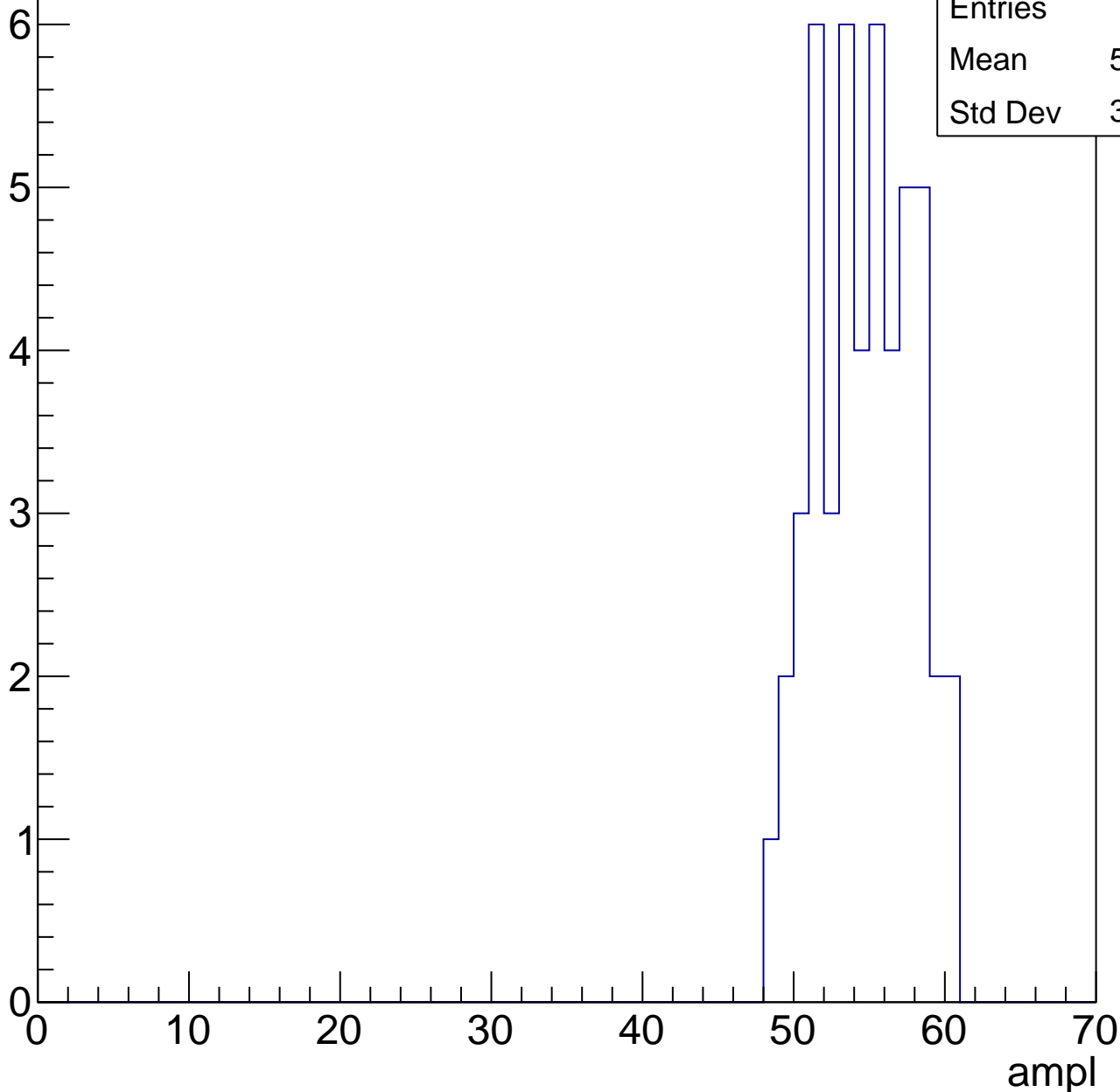


B1L103S, U5-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	54.27
Std Dev	3.102

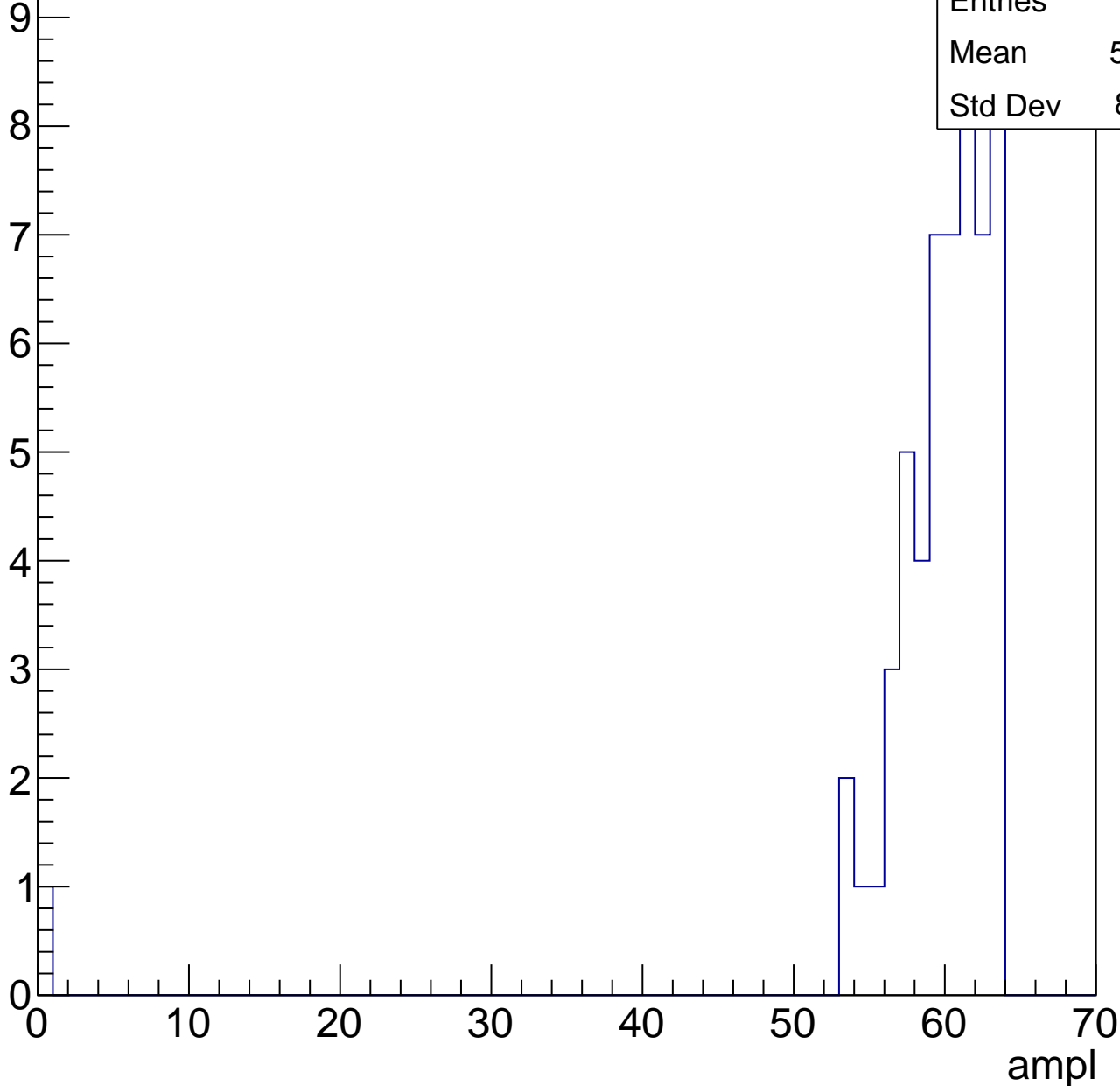


B1L103S, U5-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

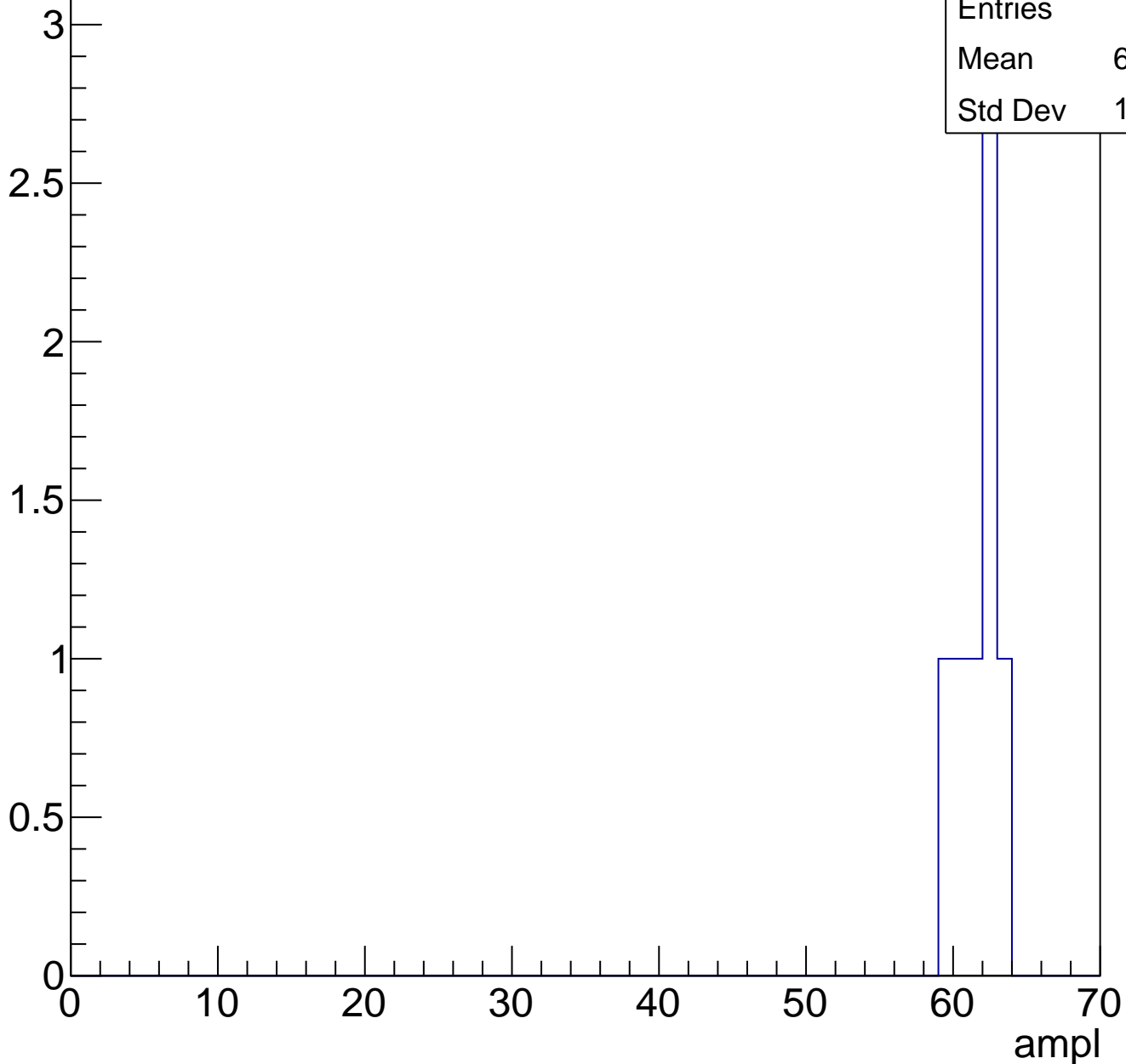
Entries	56
Mean	58.62
Std Dev	8.331



B1L103S, U5-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch46, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	27.68
Std Dev	4.871

Gaus mean : 28.8993

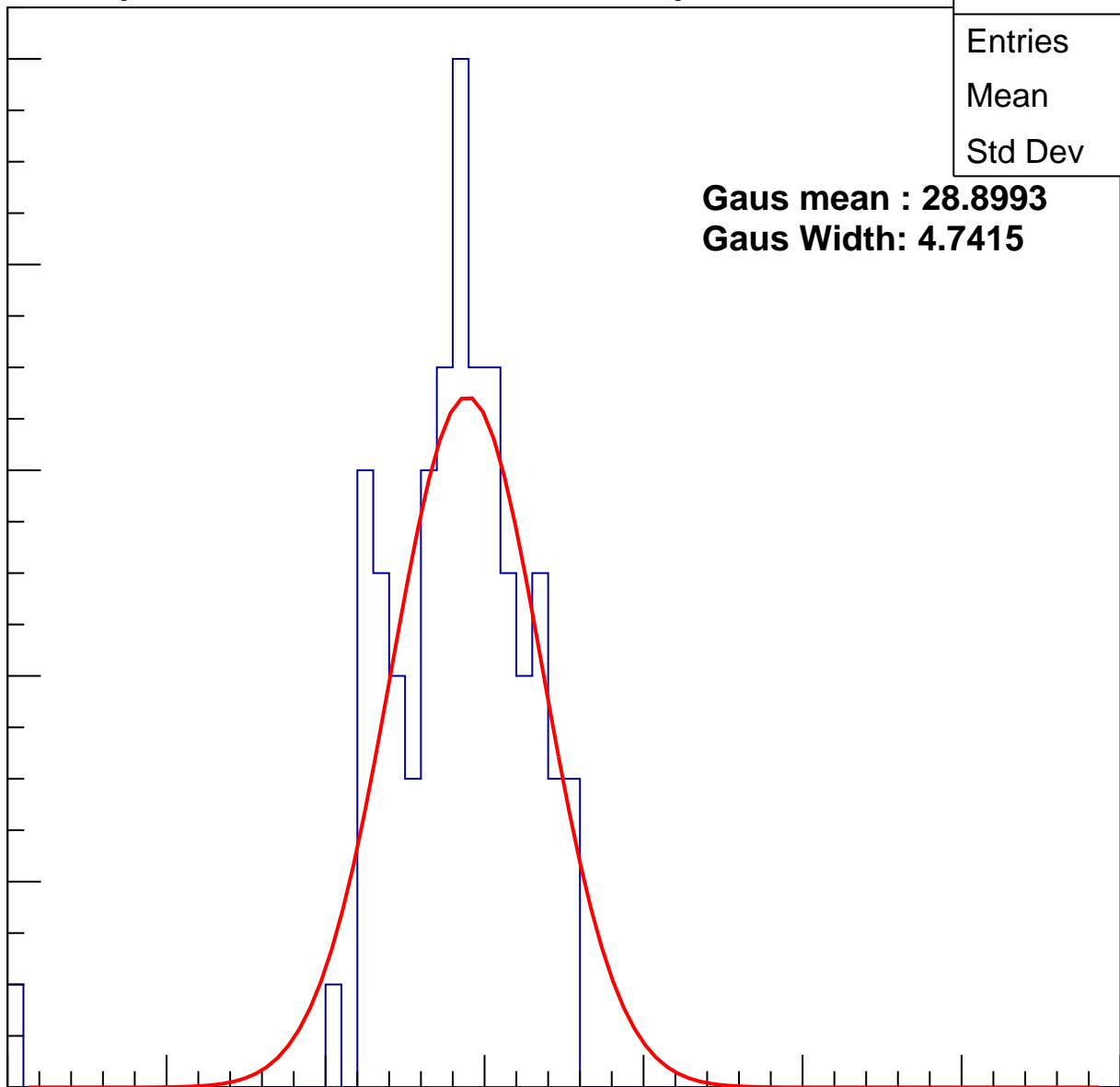
Gaus Width: 4.7415

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch46, adc1

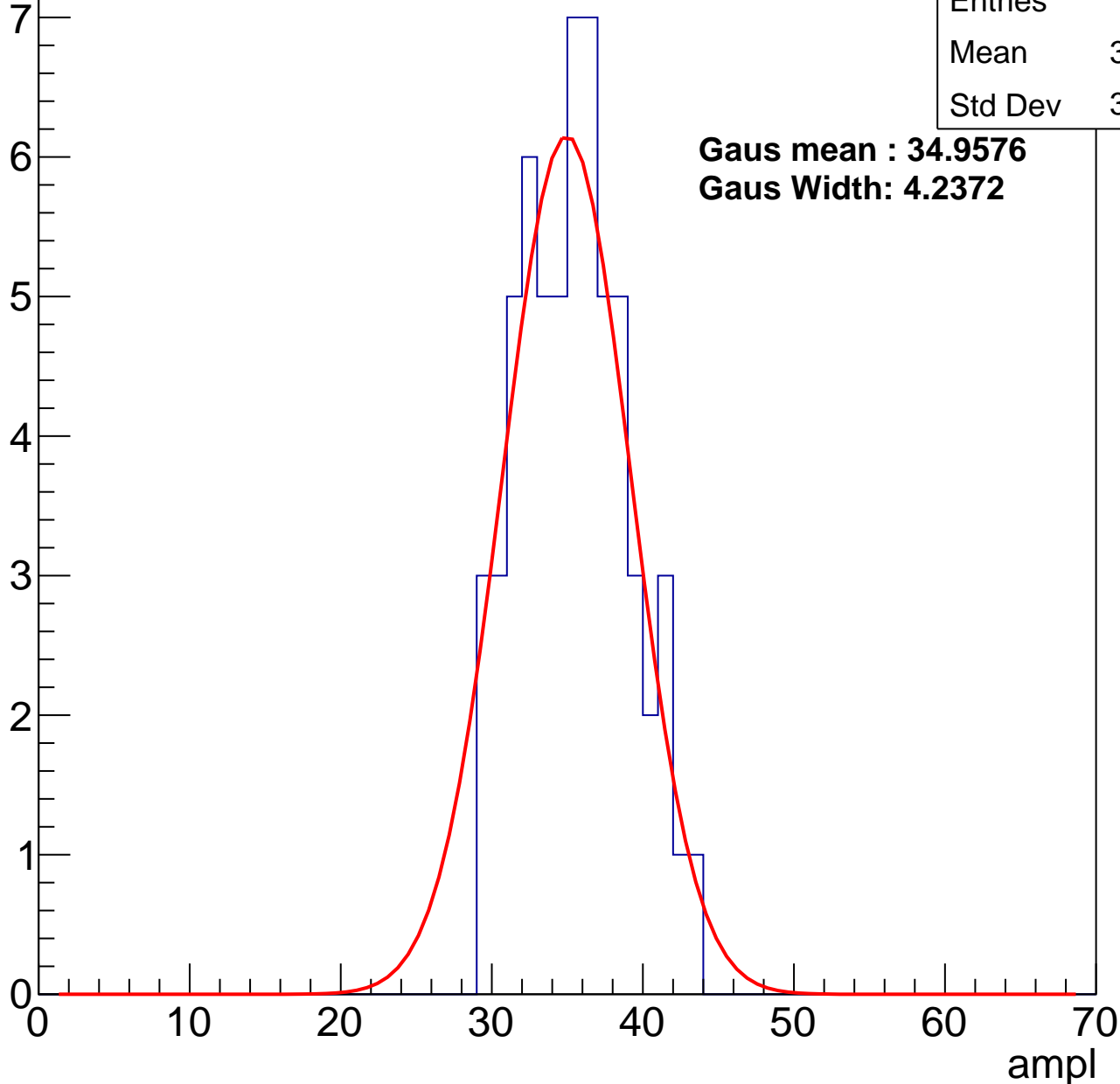
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.02
Std Dev	3.466

Gaus mean : 34.9576

Gaus Width: 4.2372



B1L103S, U5-ch46, adc2

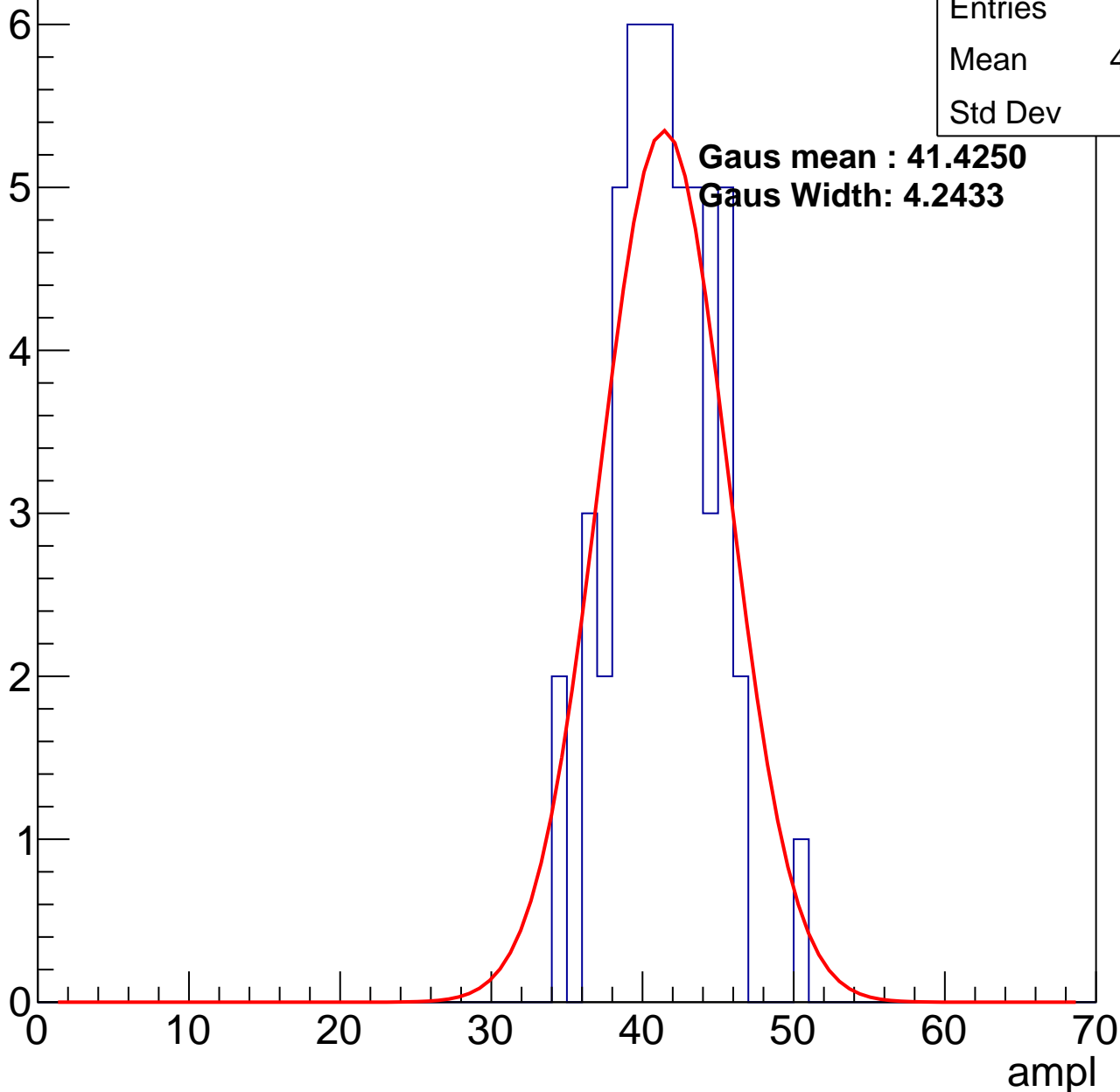
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	40.86
Std Dev	3.26

Gaus mean : 41.4250

Gaus Width: 4.2433

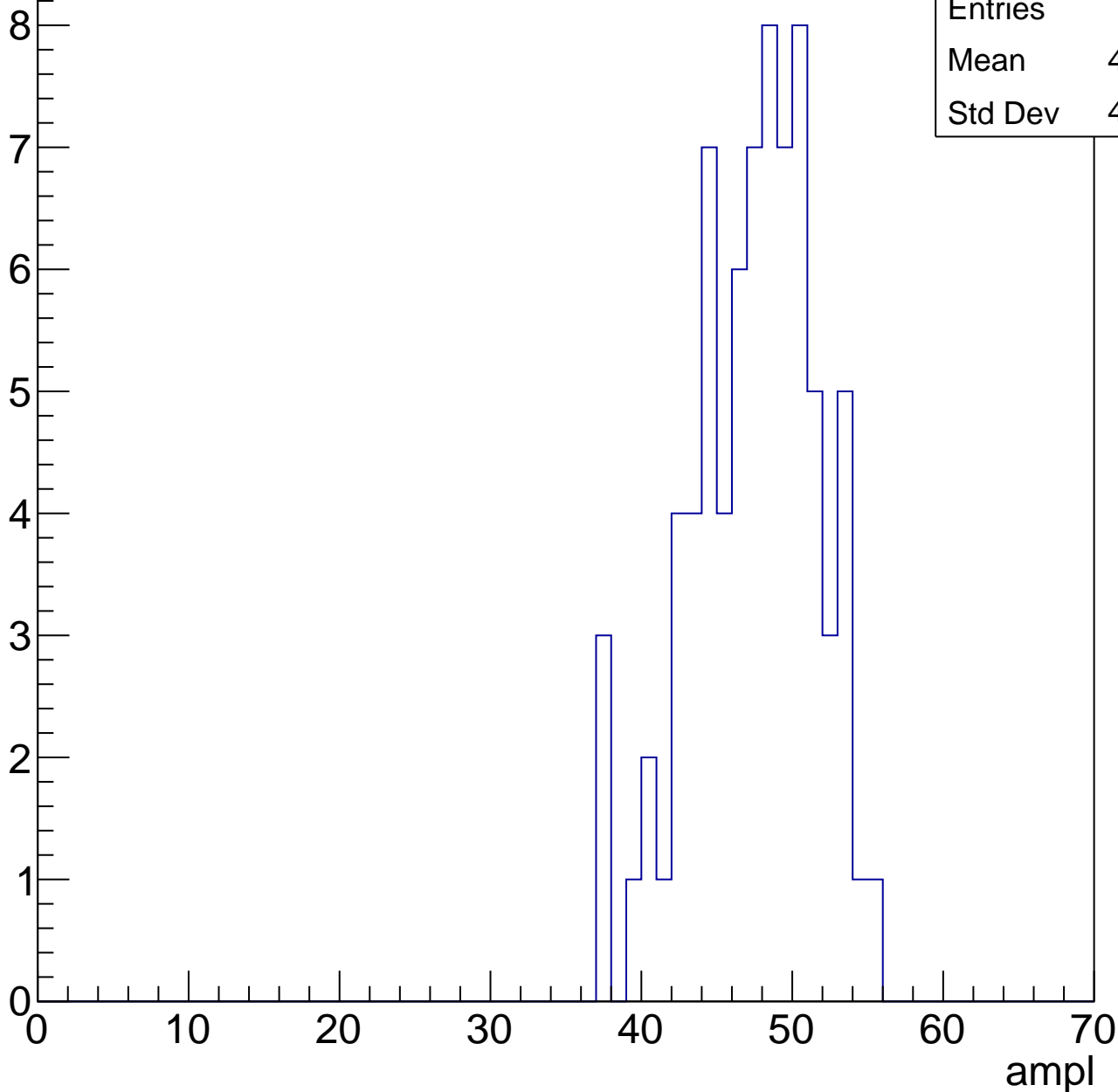


B1L103S, U5-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

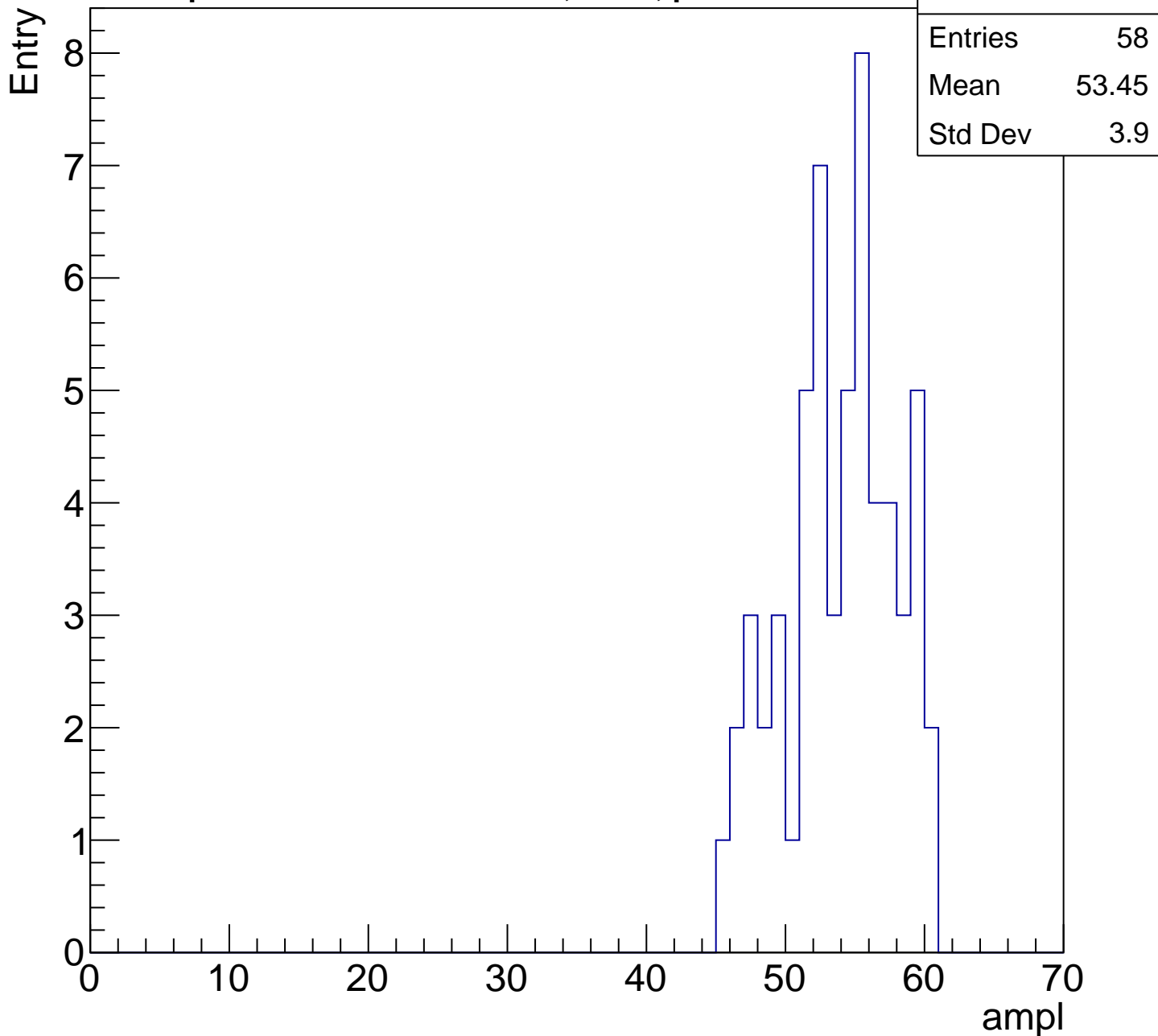
Entry

Entries	77
Mean	46.96
Std Dev	4.113



B1L103S, U5-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 55

Mean 57.65

Std Dev 8.341

ampl

0

10

20

30

40

50

60

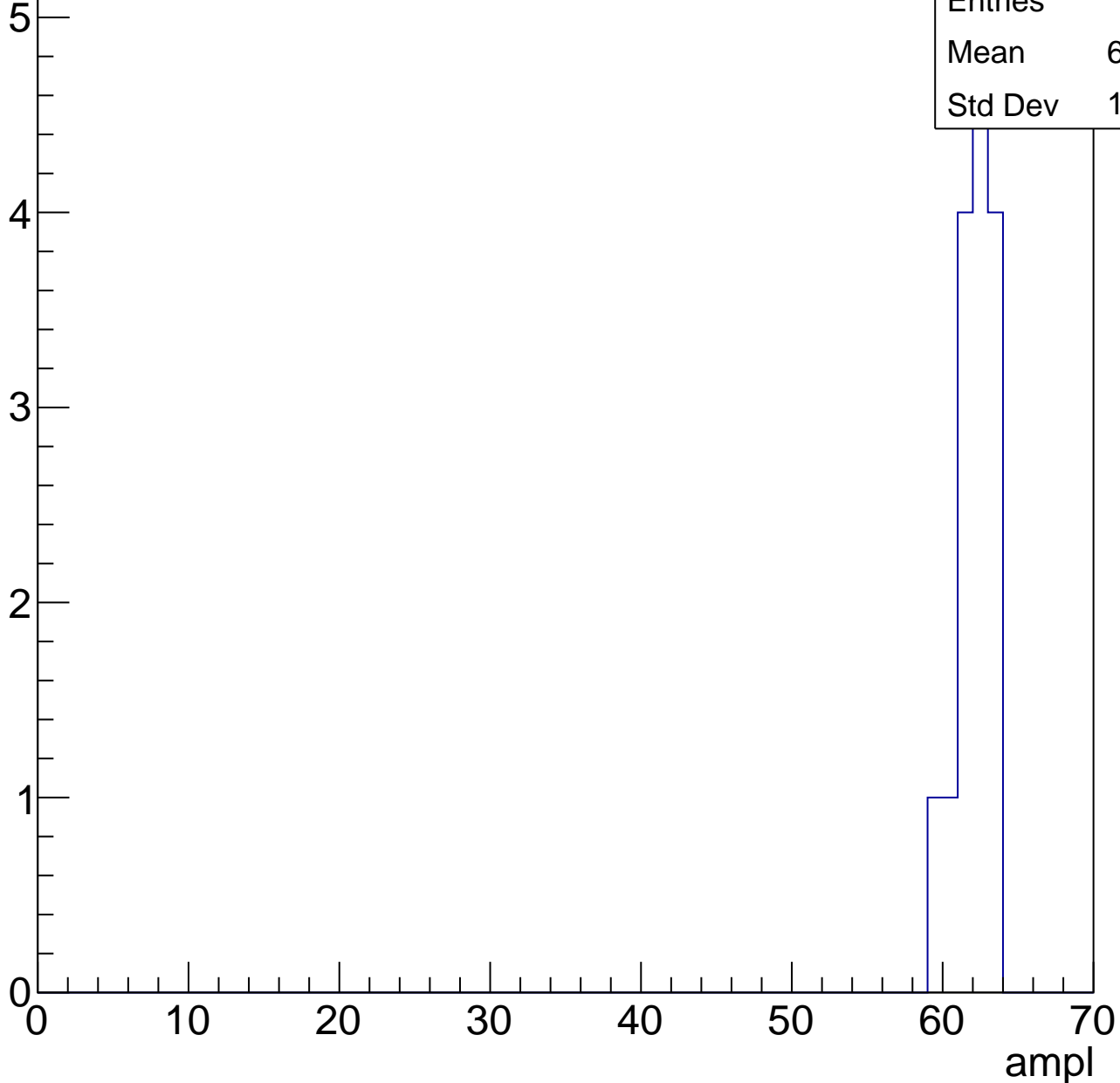
70

B1L103S, U5-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

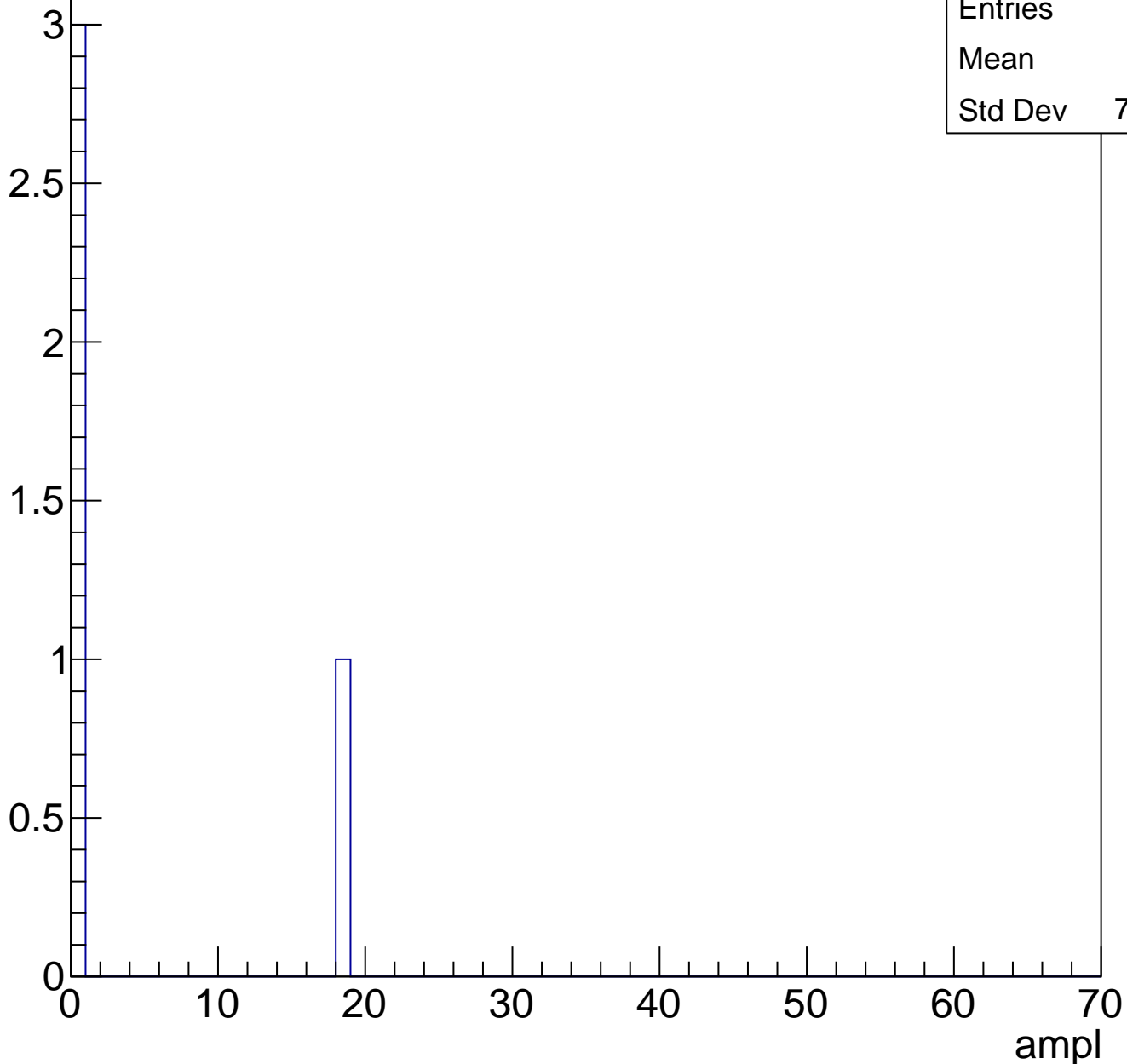
Entries	15
Mean	61.67
Std Dev	1.135



B1L103S, U5-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	4.5
Std Dev	7.794

B1L103S, U5-ch47, adc0

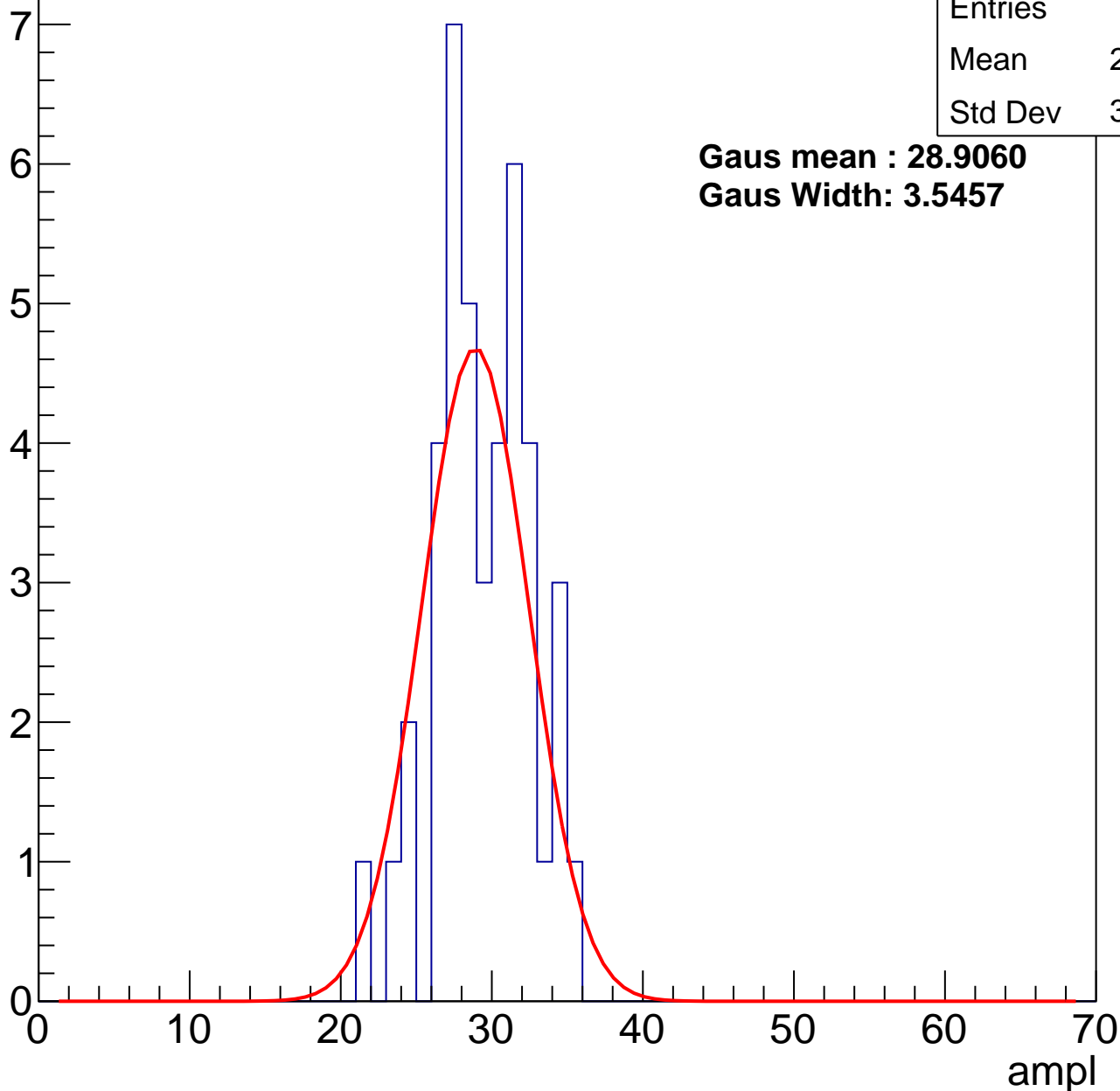
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	28.95
Std Dev	3.124

Gaus mean : 28.9060

Gaus Width: 3.5457



B1L103S, U5-ch47, adc1

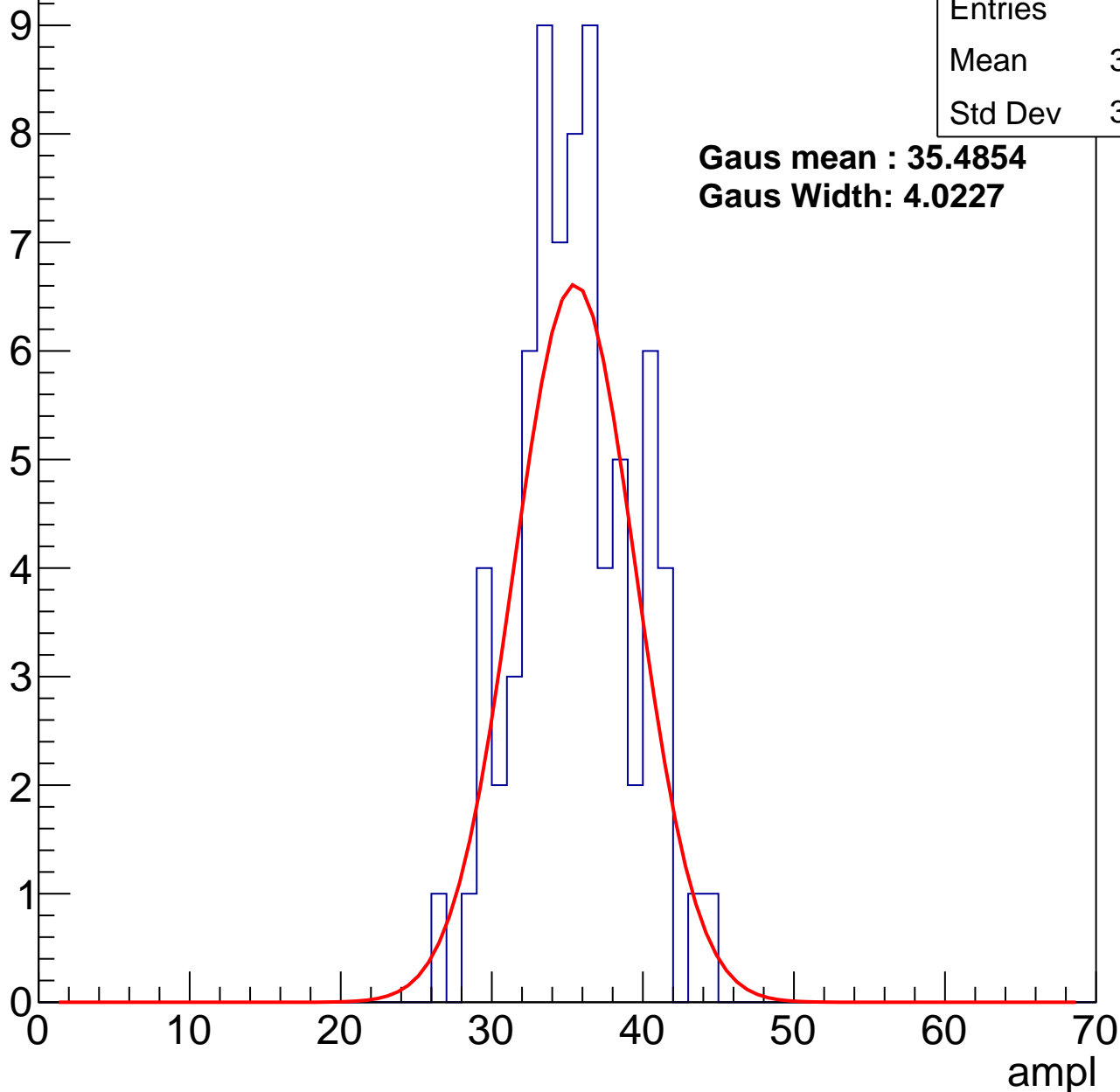
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.08
Std Dev	3.719

Gaus mean : 35.4854

Gaus Width: 4.0227



B1L103S, U5-ch47, adc2

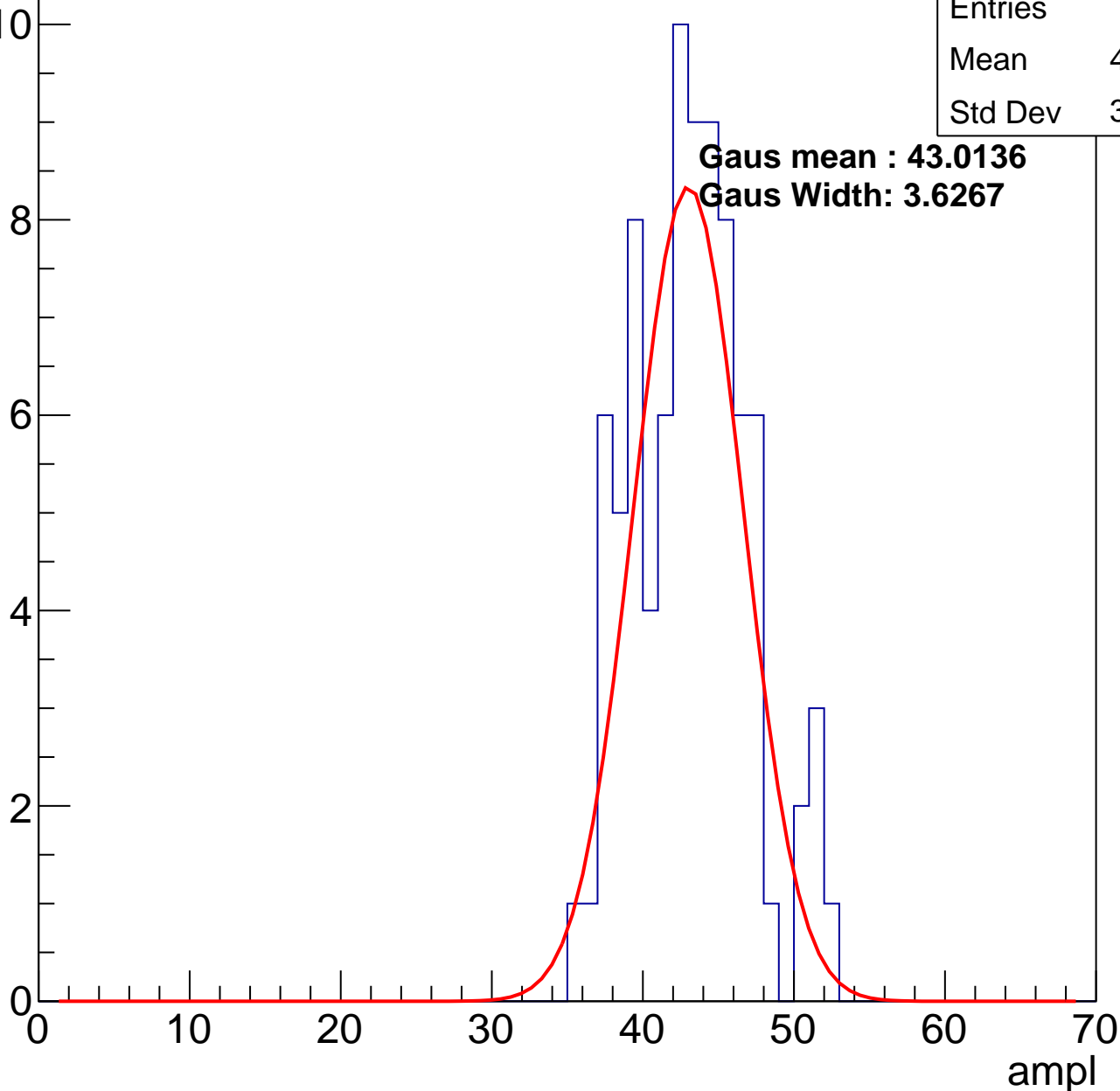
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	42.73
Std Dev	3.786

Gaus mean : 43.0136

Gaus Width: 3.6267

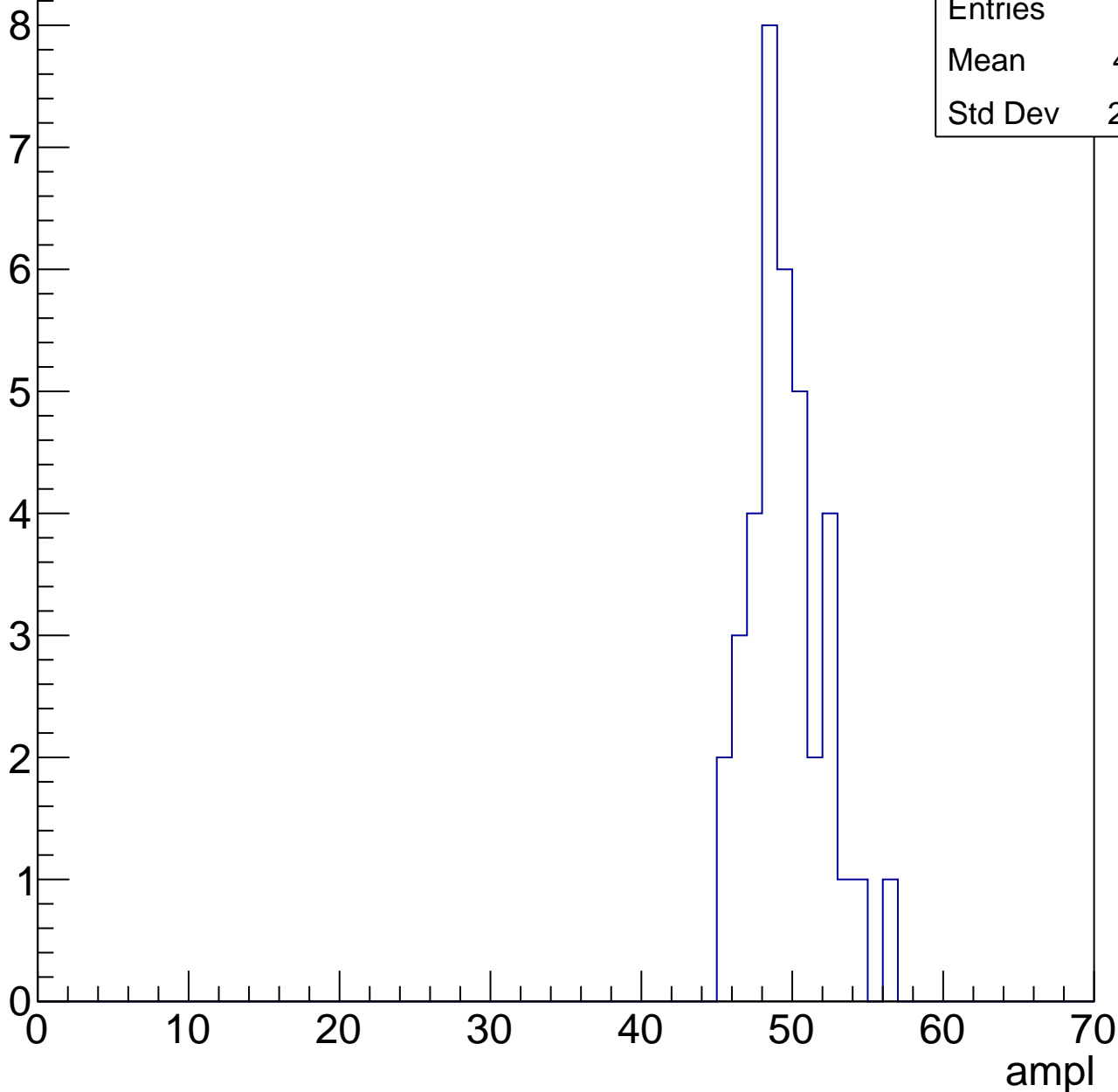


B1L103S, U5-ch47, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	49.11
Std Dev	2.447

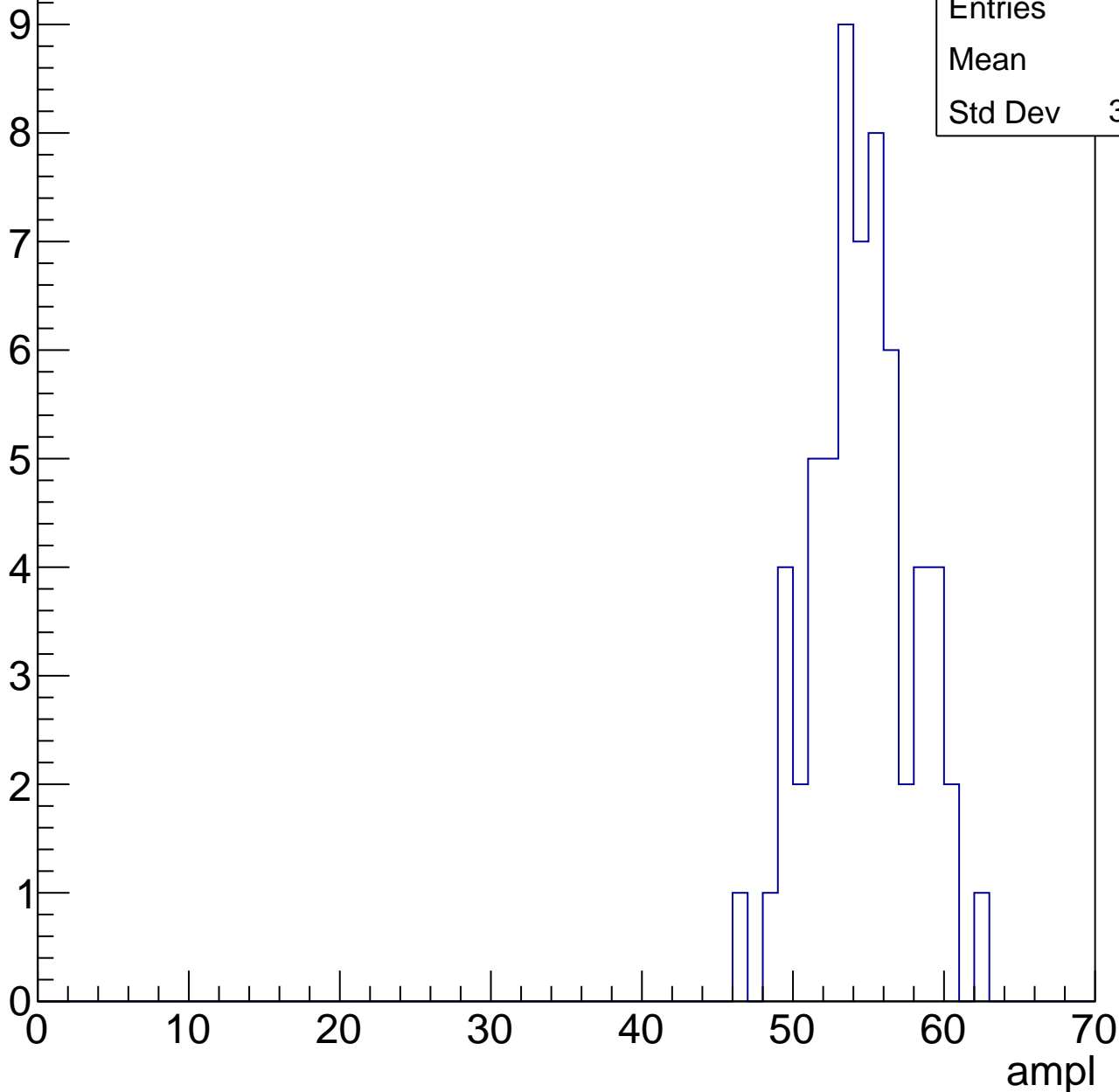


B1L103S, U5-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

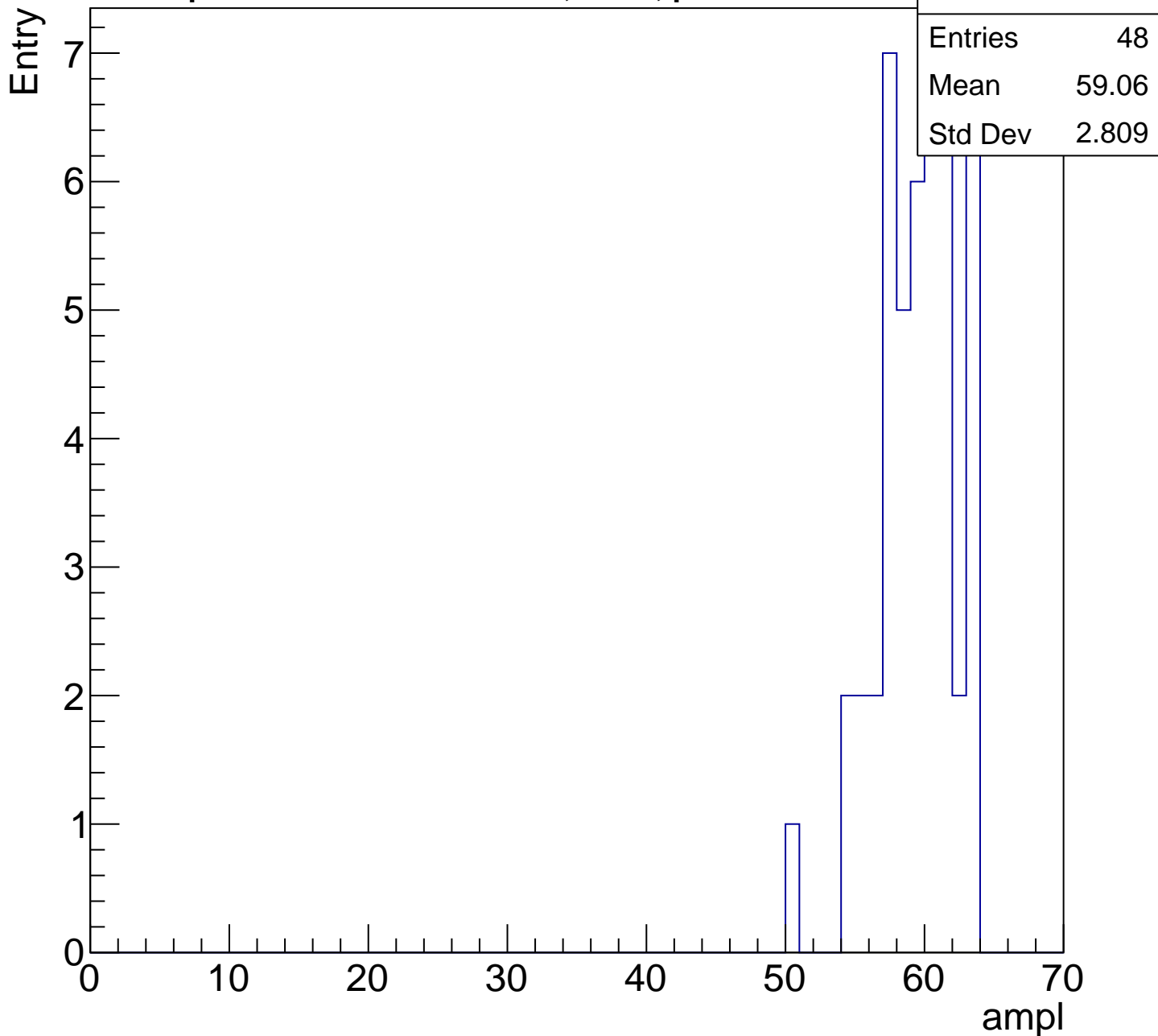
Entry

Entries	61
Mean	54.1
Std Dev	3.278



B1L103S, U5-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

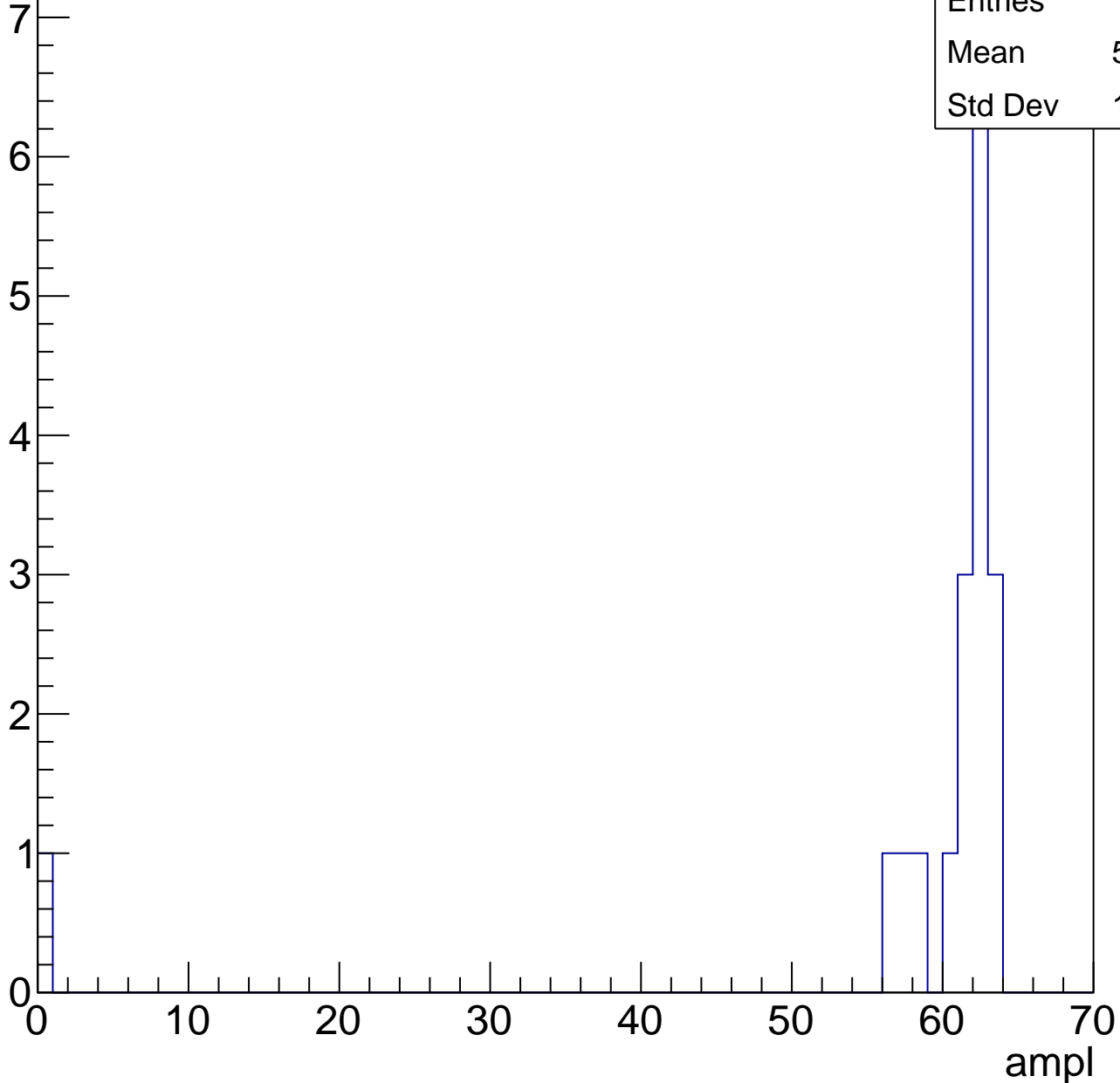


B1L103S, U5-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	18
Mean	57.61
Std Dev	14.11



B1L103S, U5-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch48, adc0

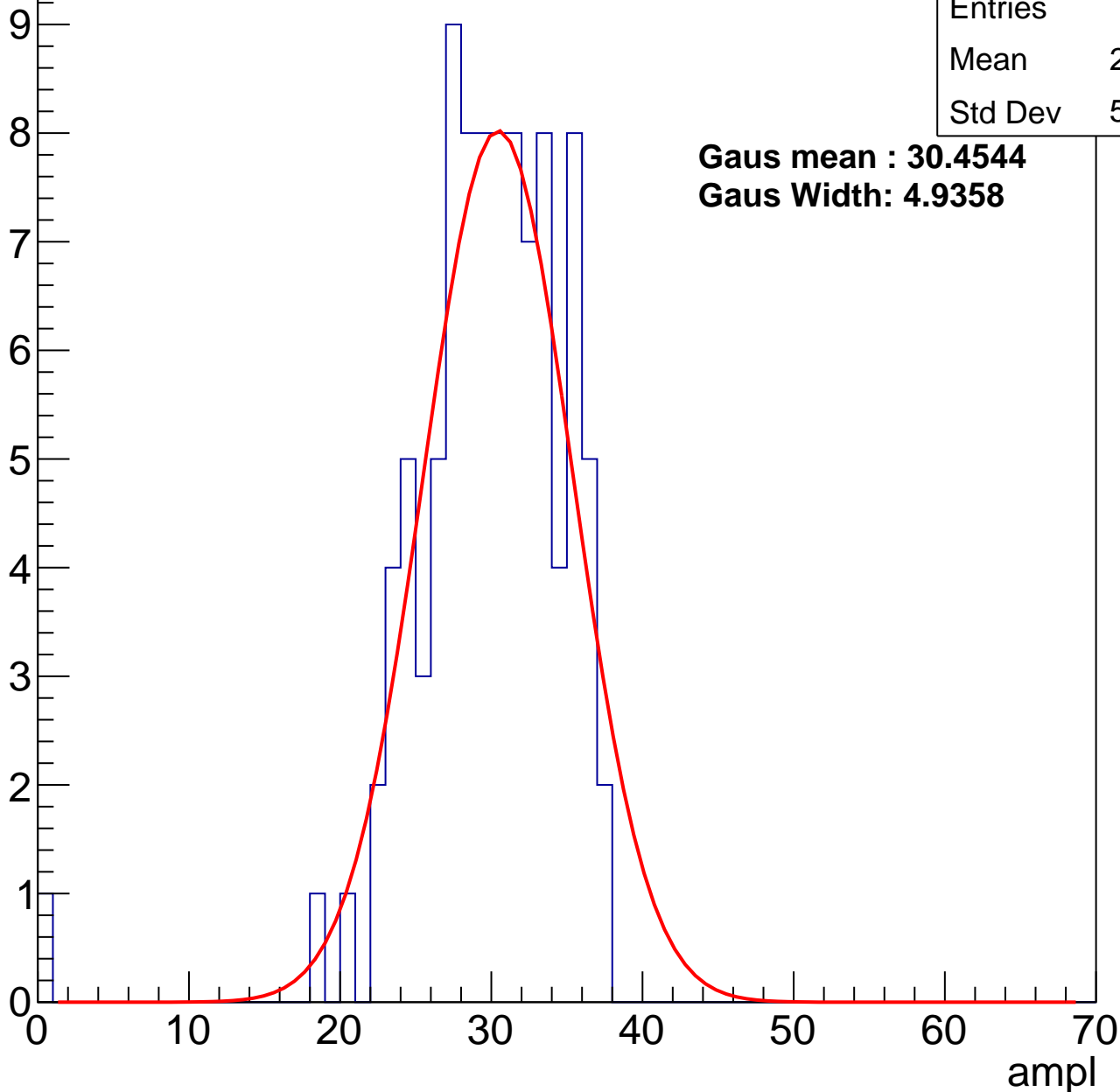
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	97
Mean	29.32
Std Dev	5.106

Gaus mean : 30.4544

Gaus Width: 4.9358



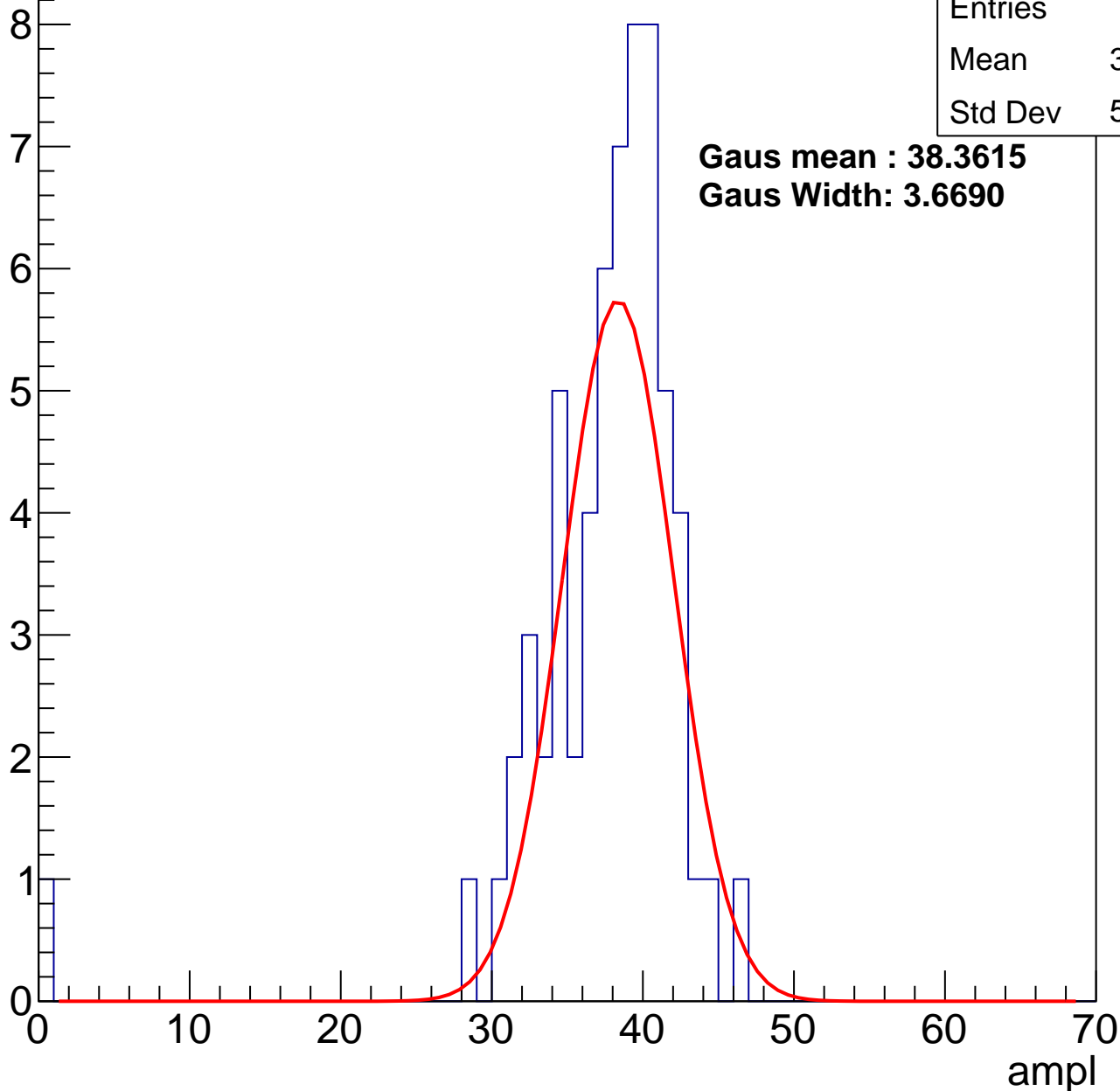
B1L103S, U5-ch48, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	36.97
Std Dev	5.946

Gaus mean : 38.3615
Gaus Width: 3.6690



B1L103S, U5-ch48, adc2

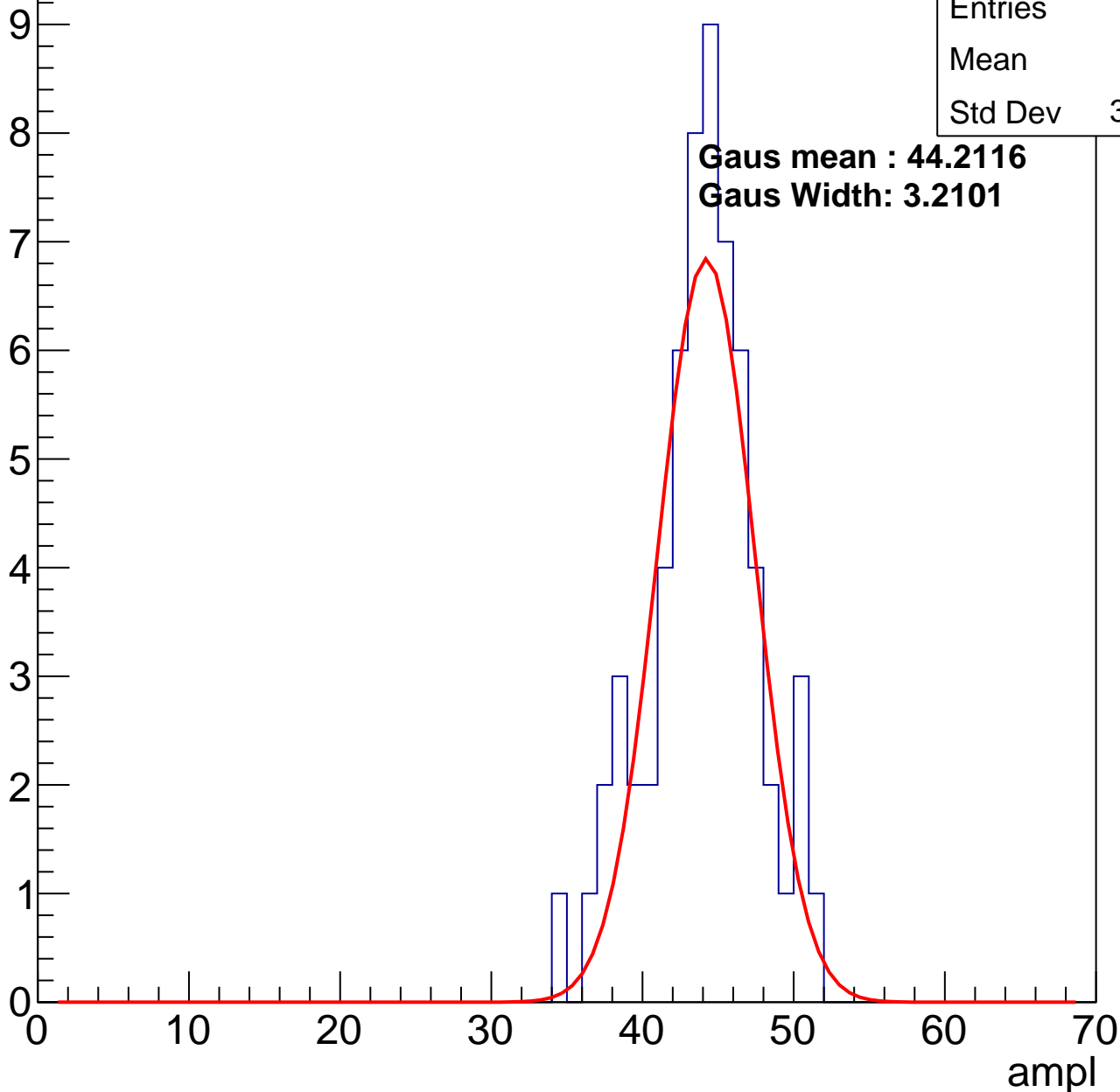
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.5
Std Dev	3.559

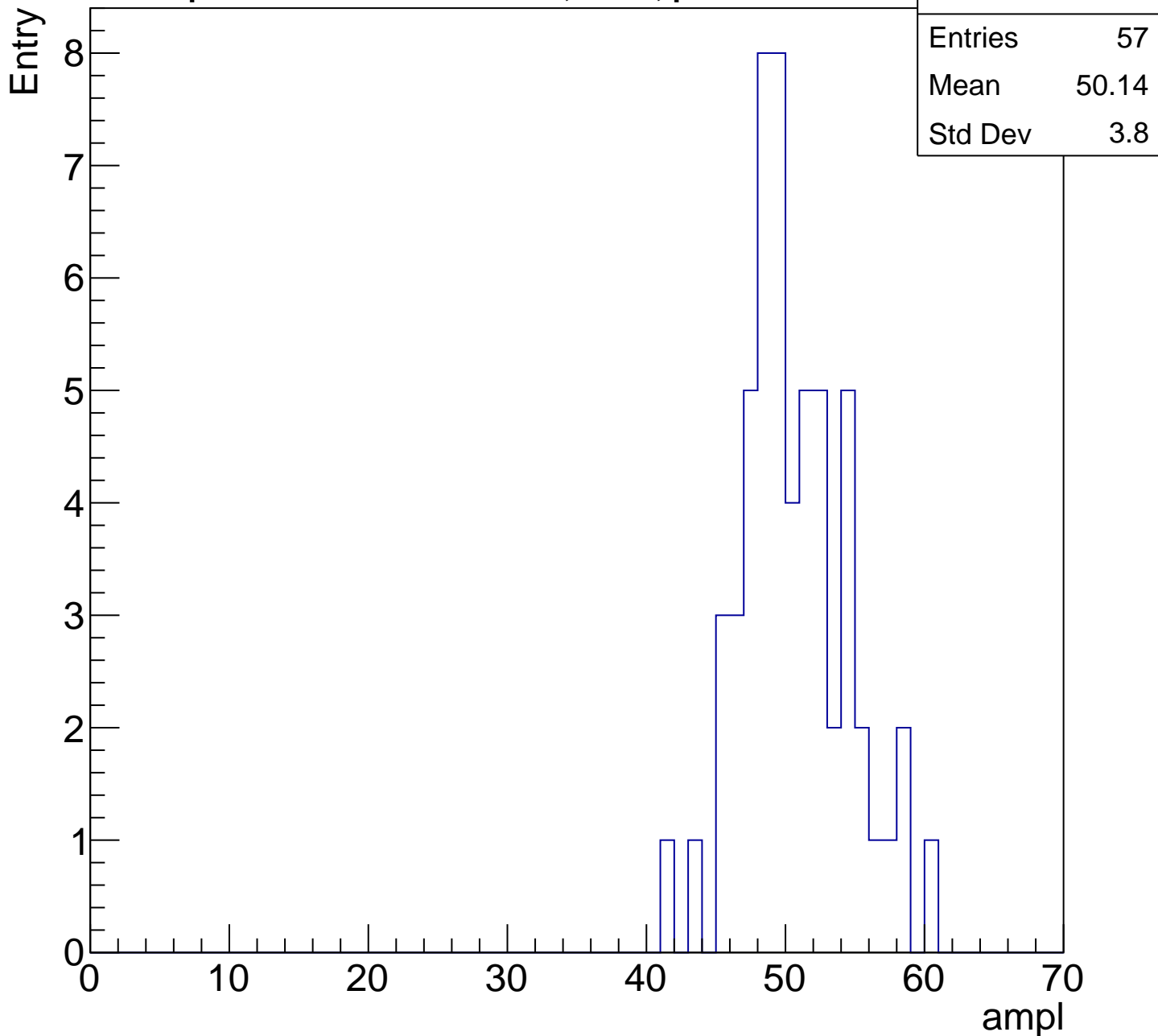
Gaus mean : 44.2116

Gaus Width: 3.2101



B1L103S, U5-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

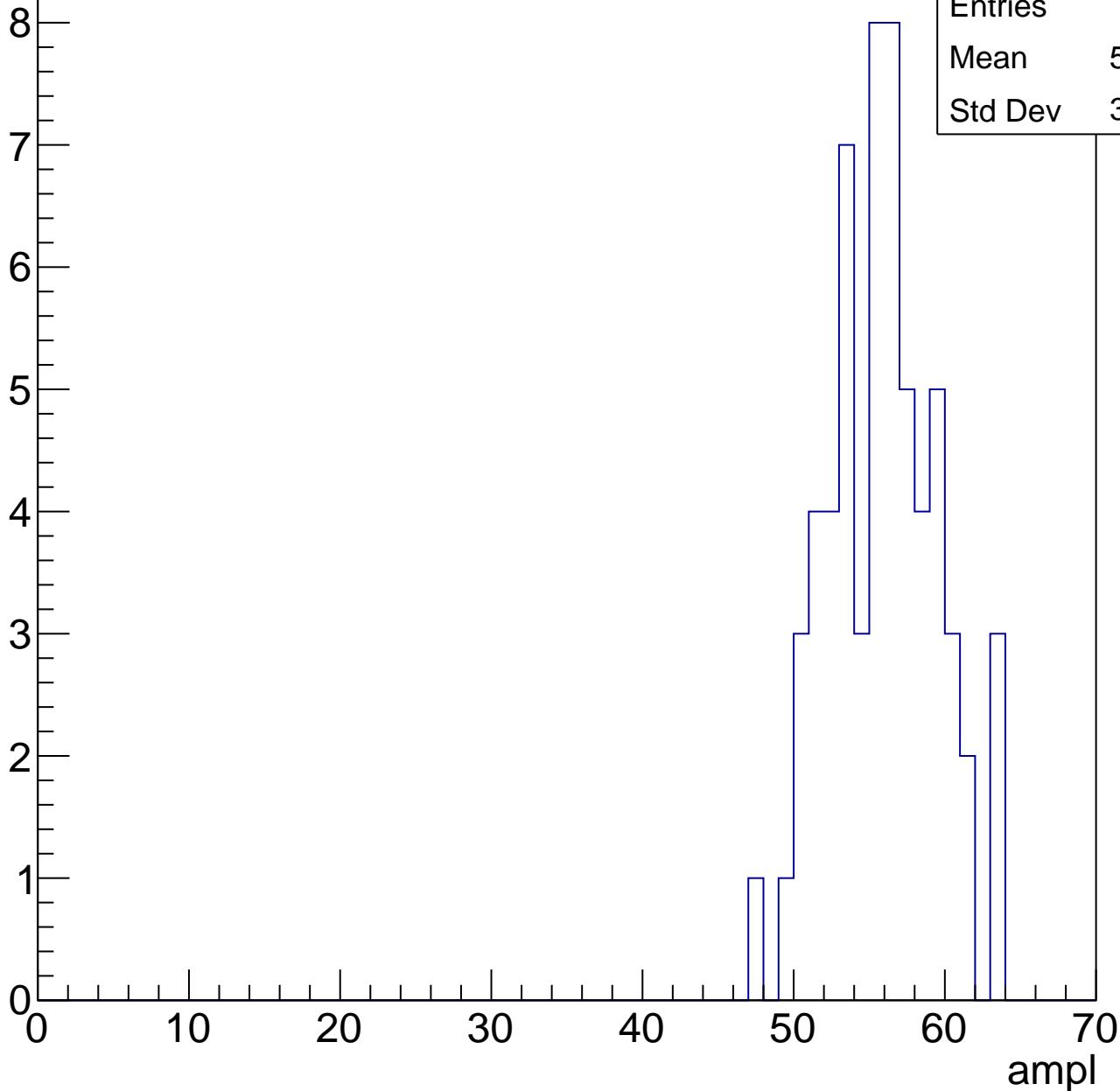


B1L103S, U5-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.44
Std Dev	3.569

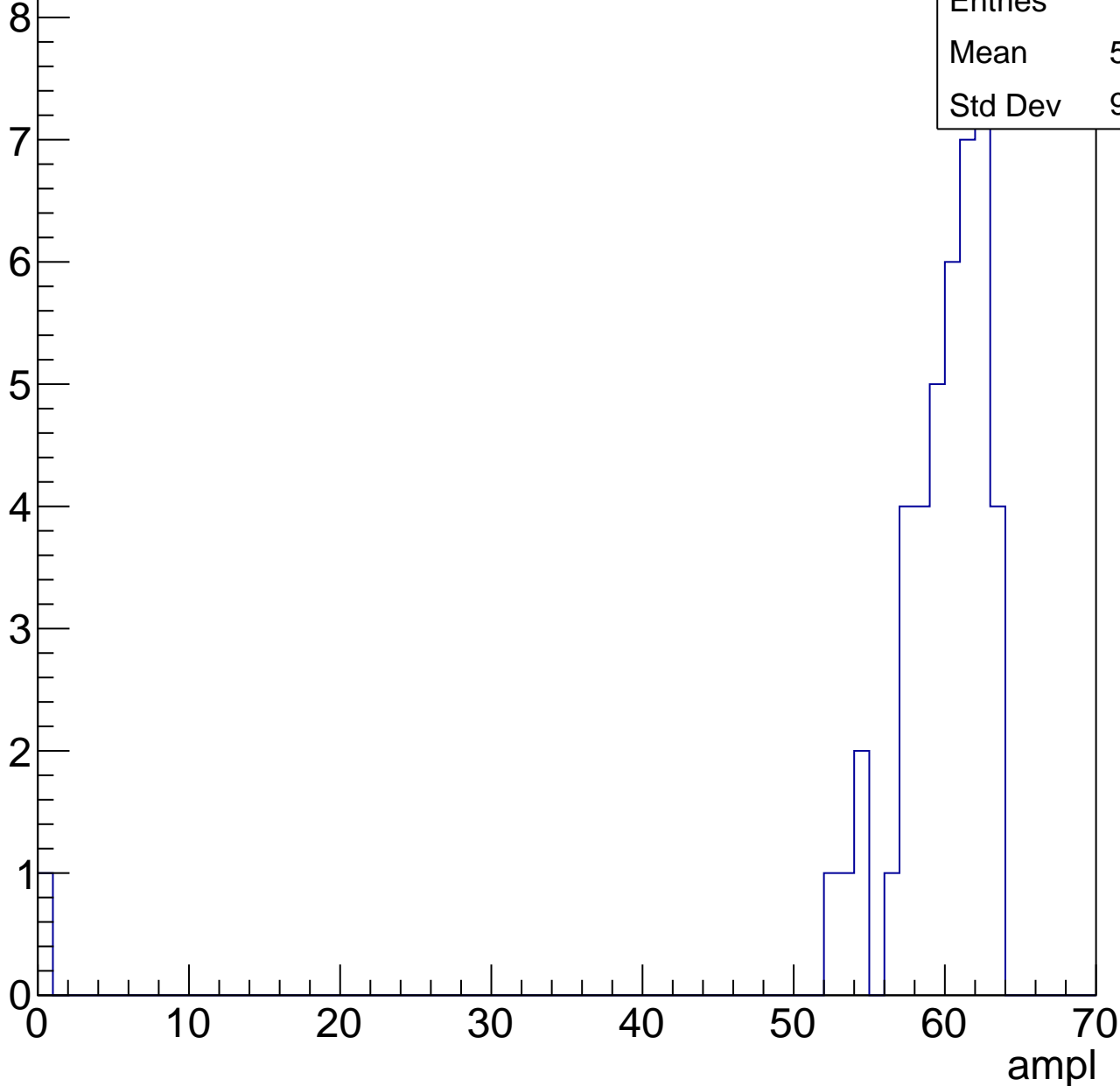


B1L103S, U5-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

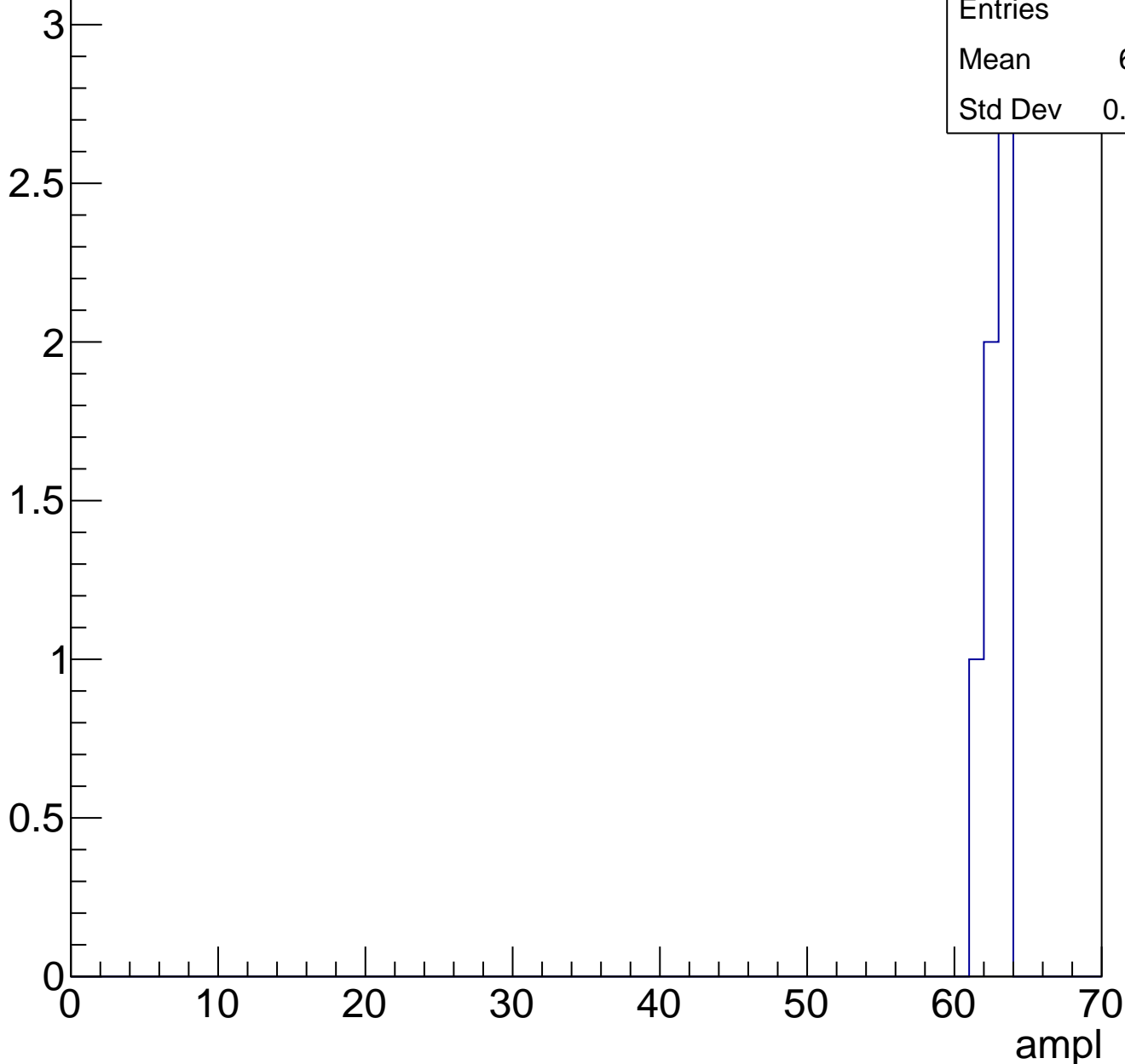
Entries	44
Mean	58.16
Std Dev	9.273



B1L103S, U5-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch49, adc0

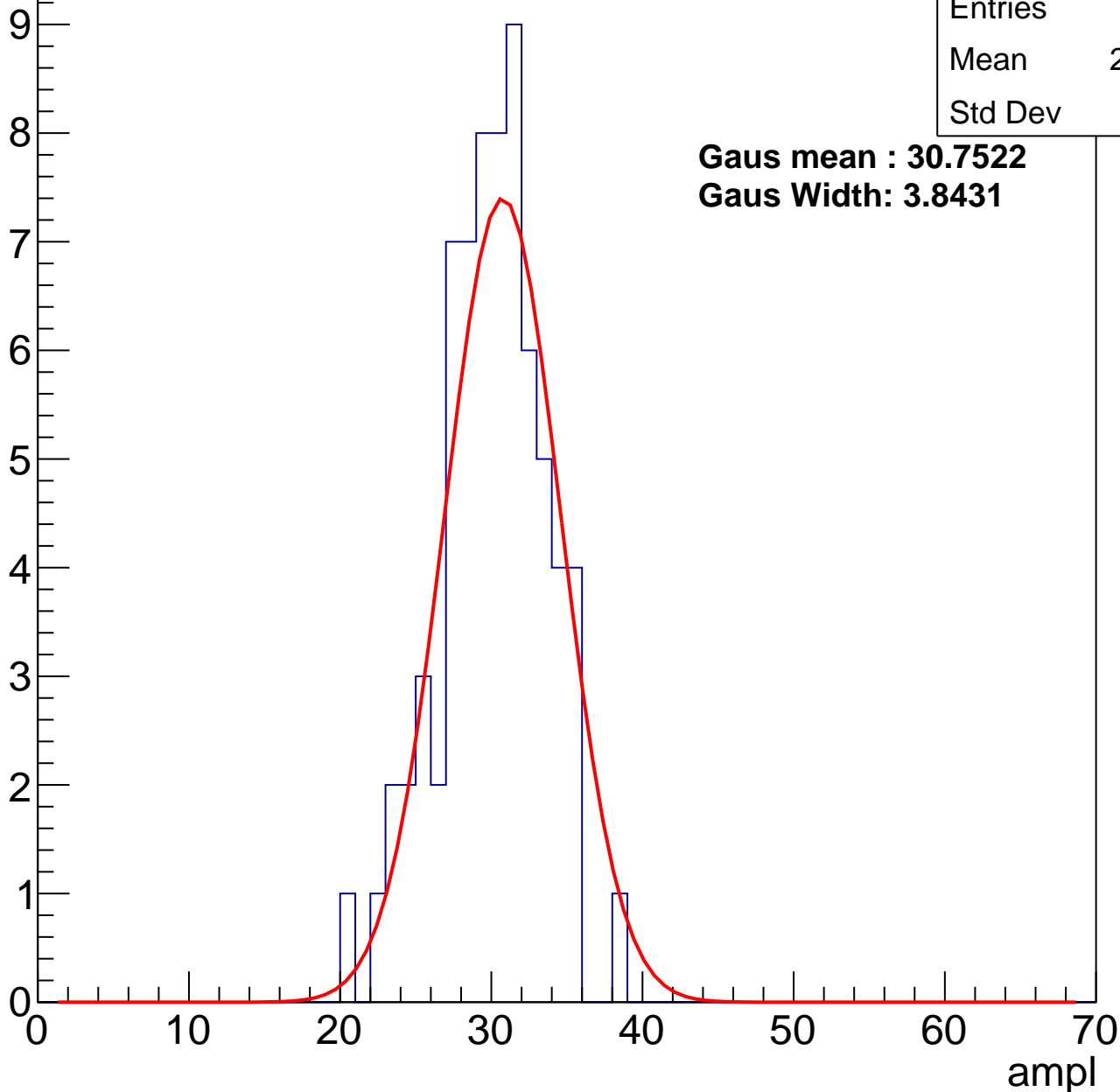
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.57
Std Dev	3.45

Gaus mean : 30.7522

Gaus Width: 3.8431



B1L103S, U5-ch49, adc1

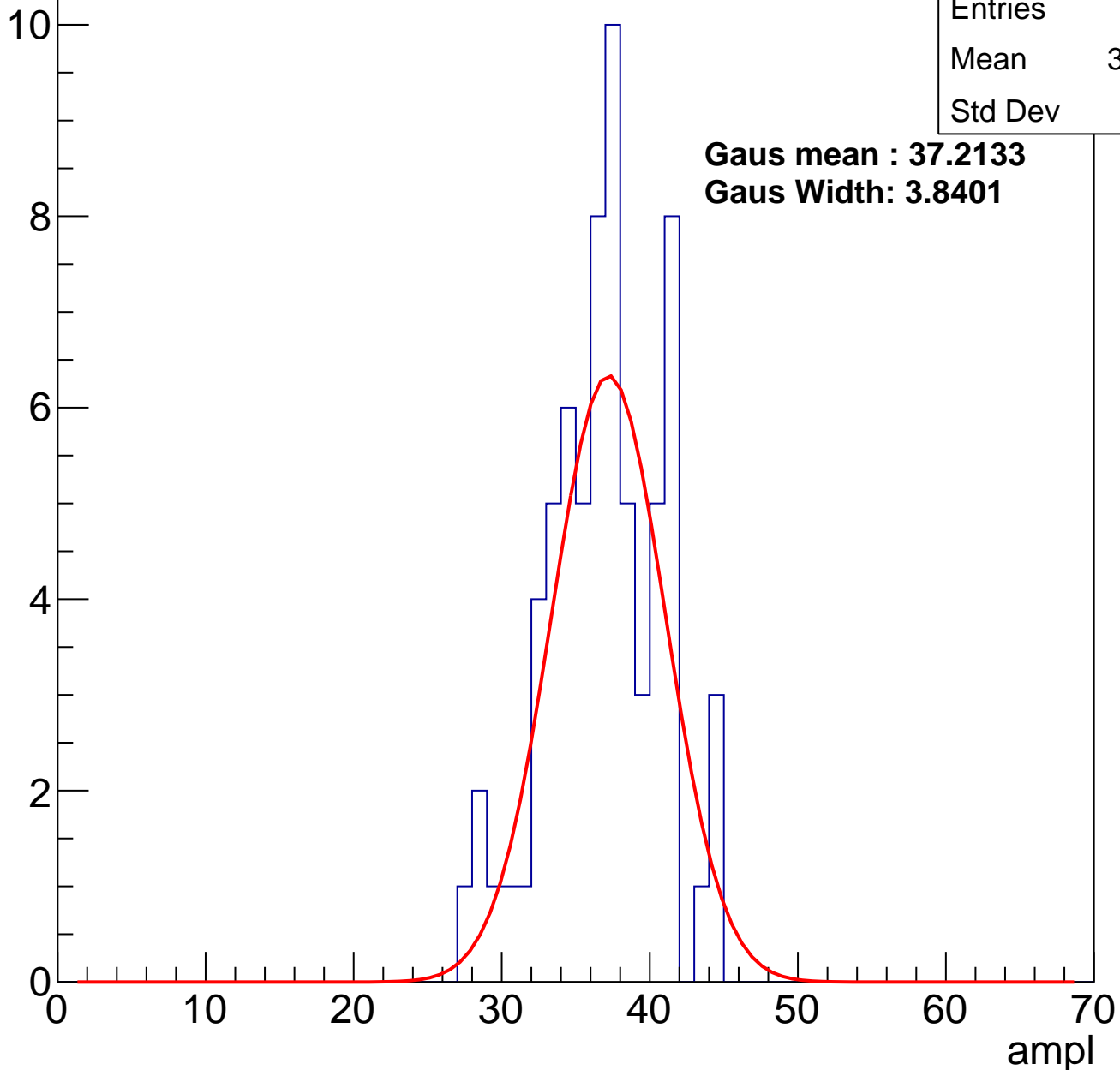
calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	36.42
Std Dev	3.85

Gaus mean : 37.2133

Gaus Width: 3.8401

Entry



B1L103S, U5-ch49, adc2

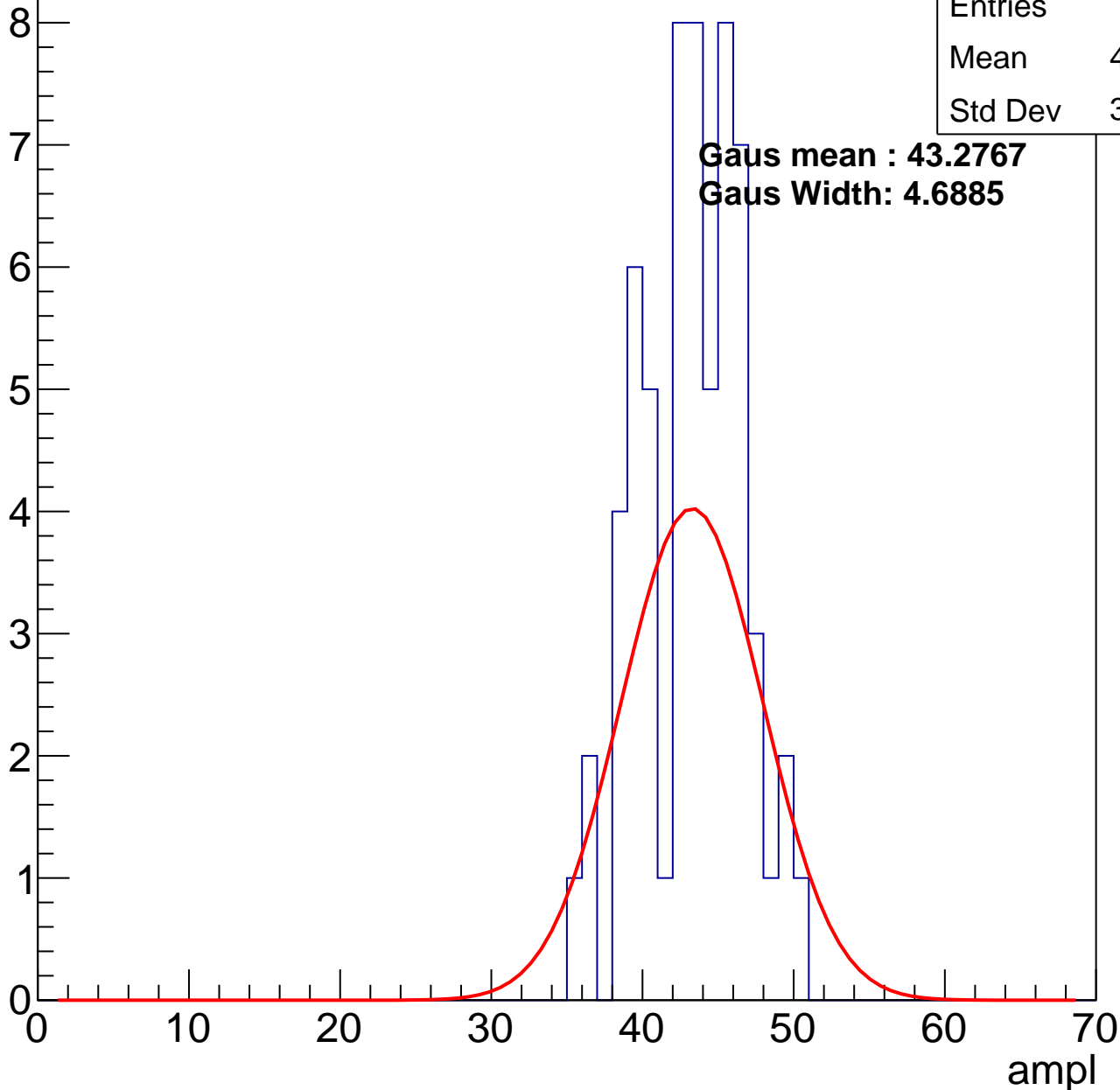
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.79
Std Dev	3.375

Gaus mean : 43.2767

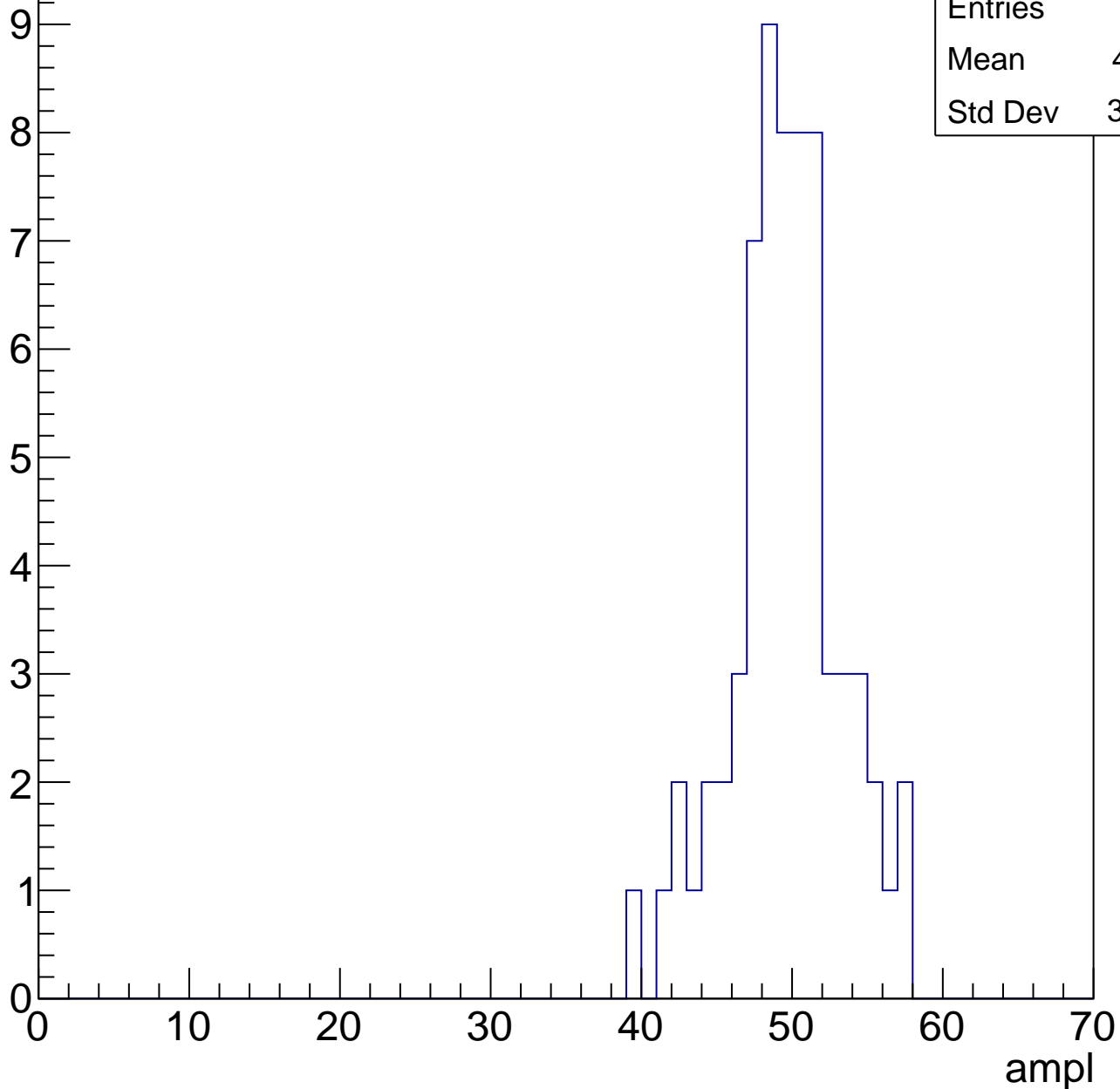
Gaus Width: 4.6885



B1L103S, U5-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

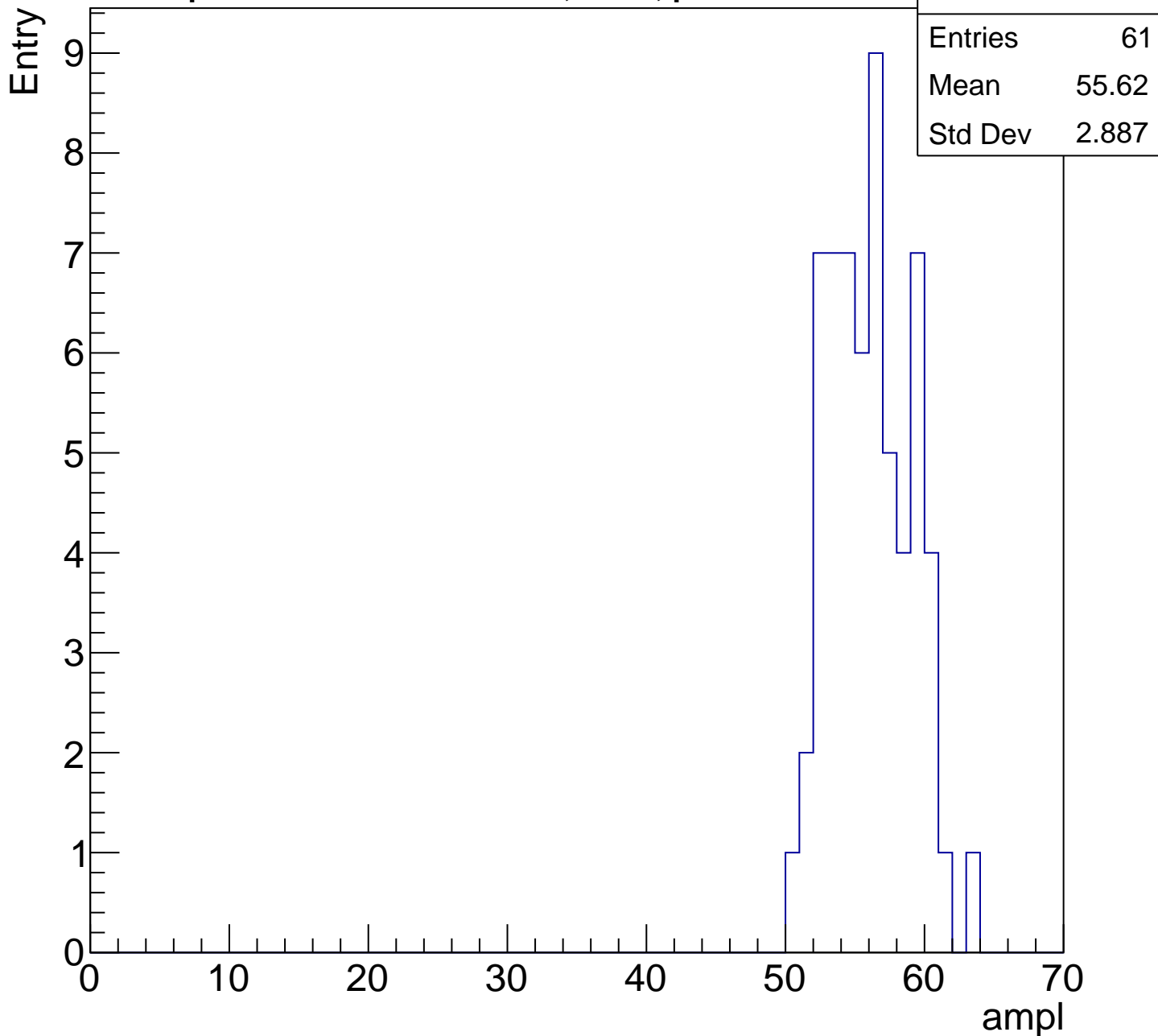
Entry



Entries	66
Mean	49.11
Std Dev	3.656

B1L103S, U5-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

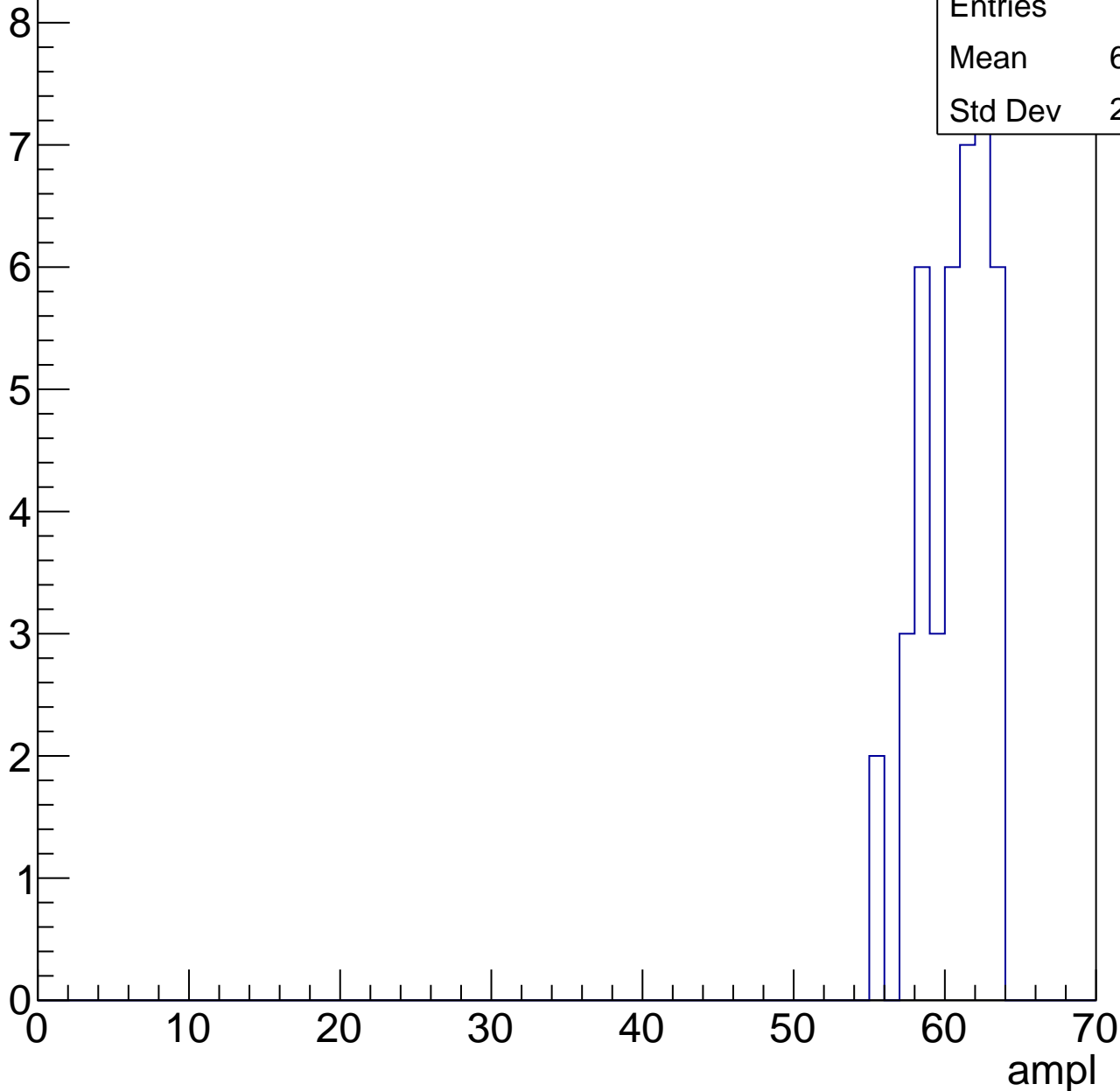


B1L103S, U5-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

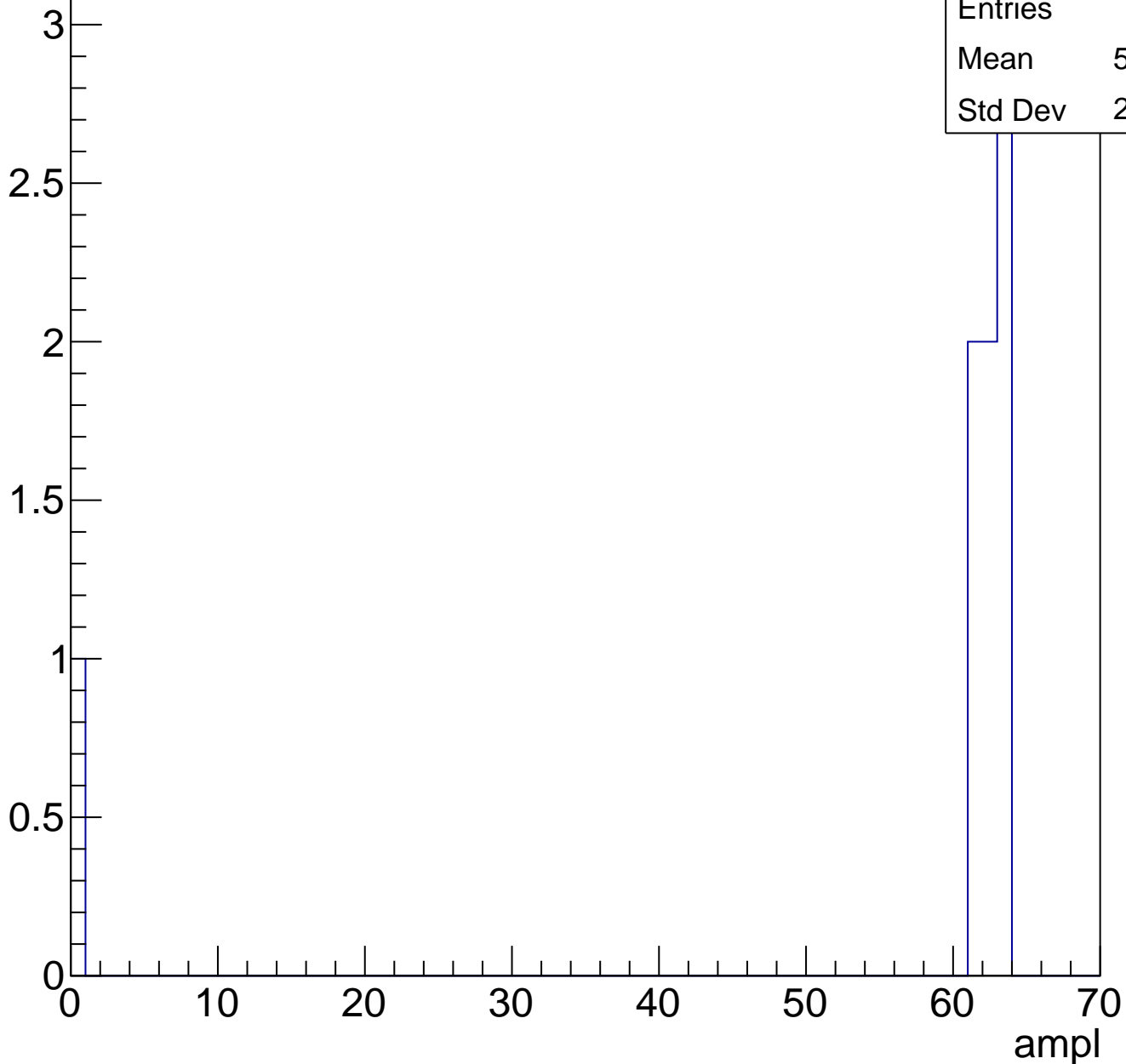
Entries	41
Mean	60.17
Std Dev	2.185



B1L103S, U5-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

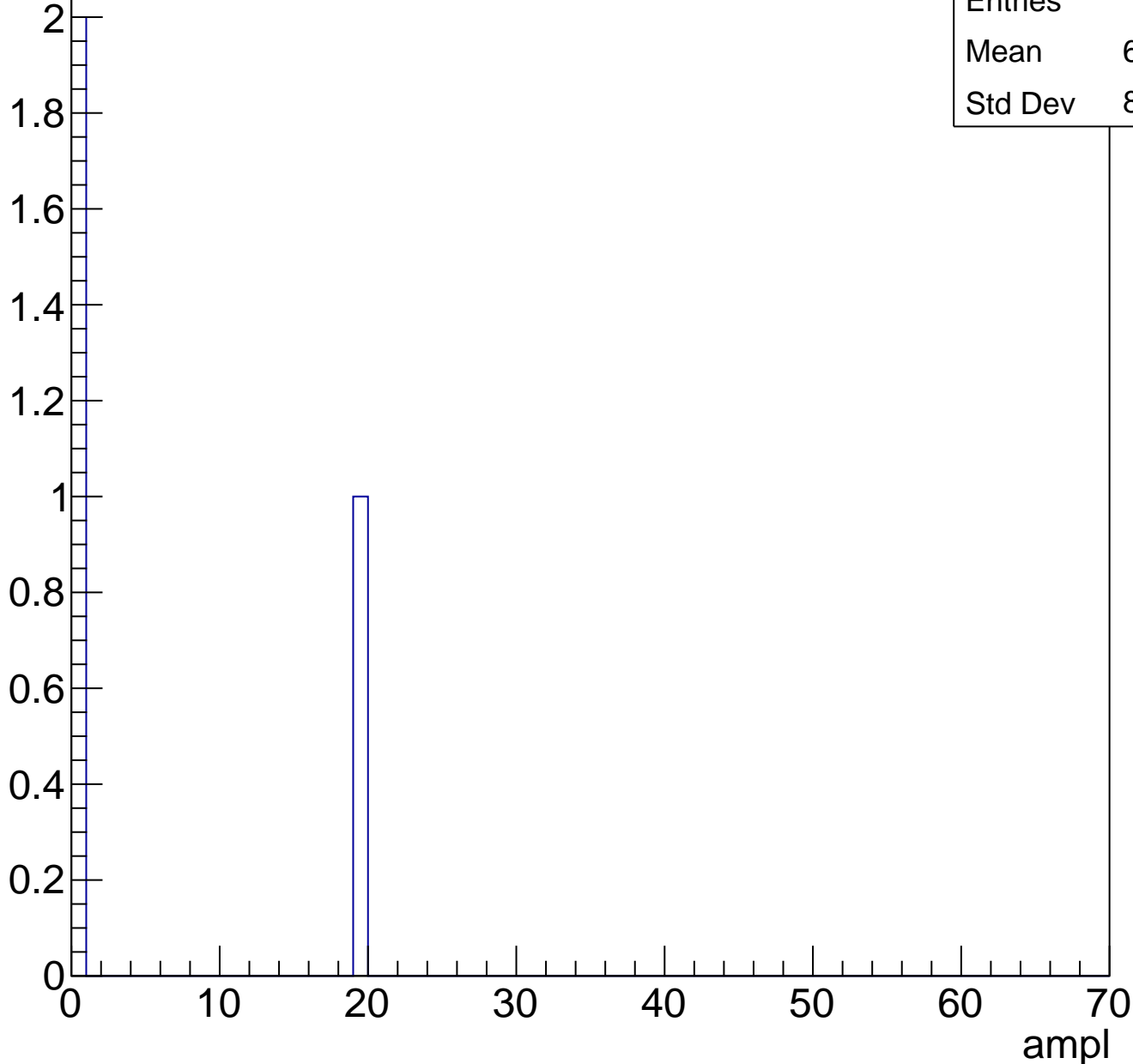
Entry



B1L103S, U5-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L103S, U5-ch50, adc0

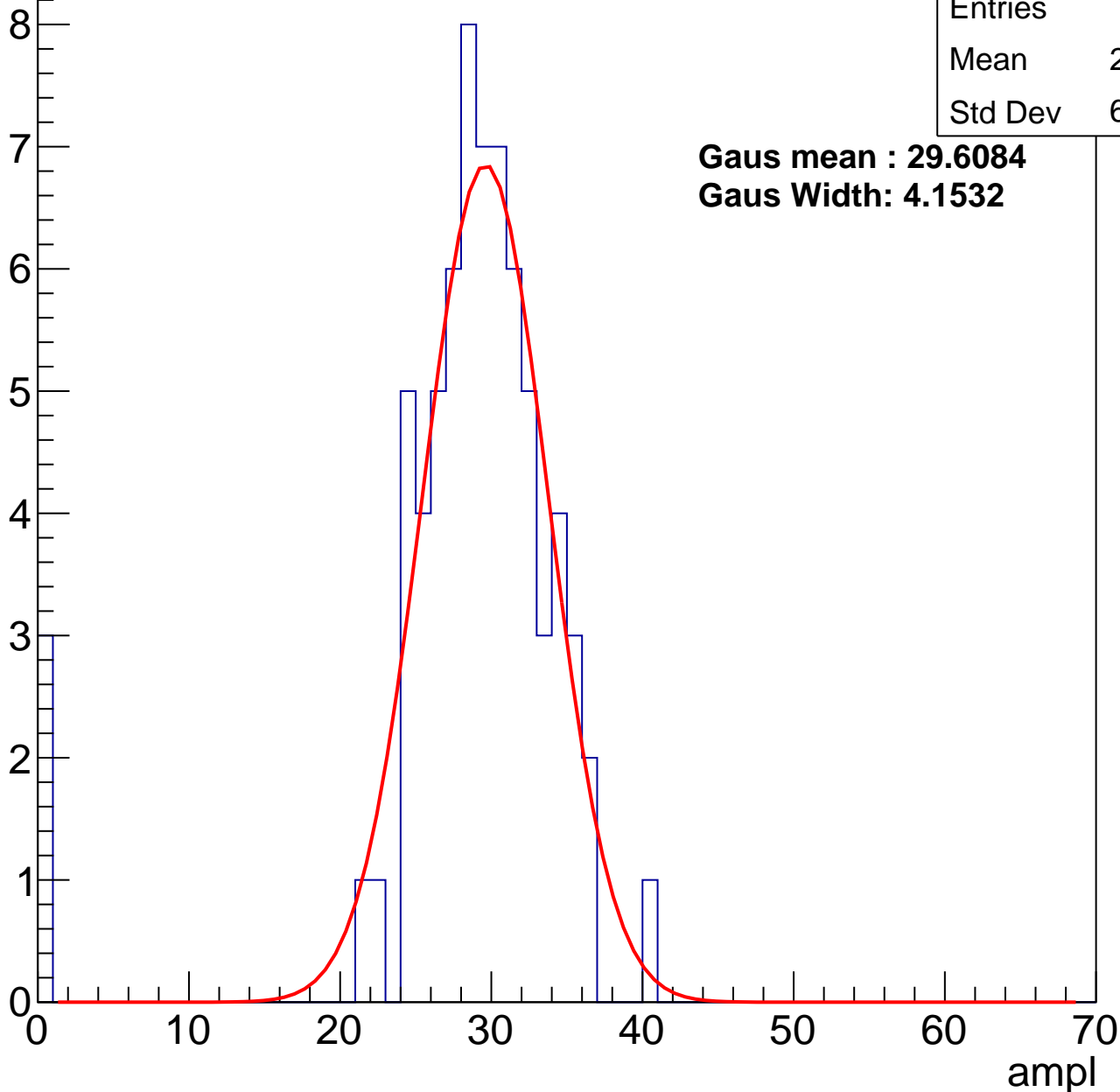
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.03
Std Dev	6.906

Gaus mean : 29.6084

Gaus Width: 4.1532



B1L103S, U5-ch50, adc1

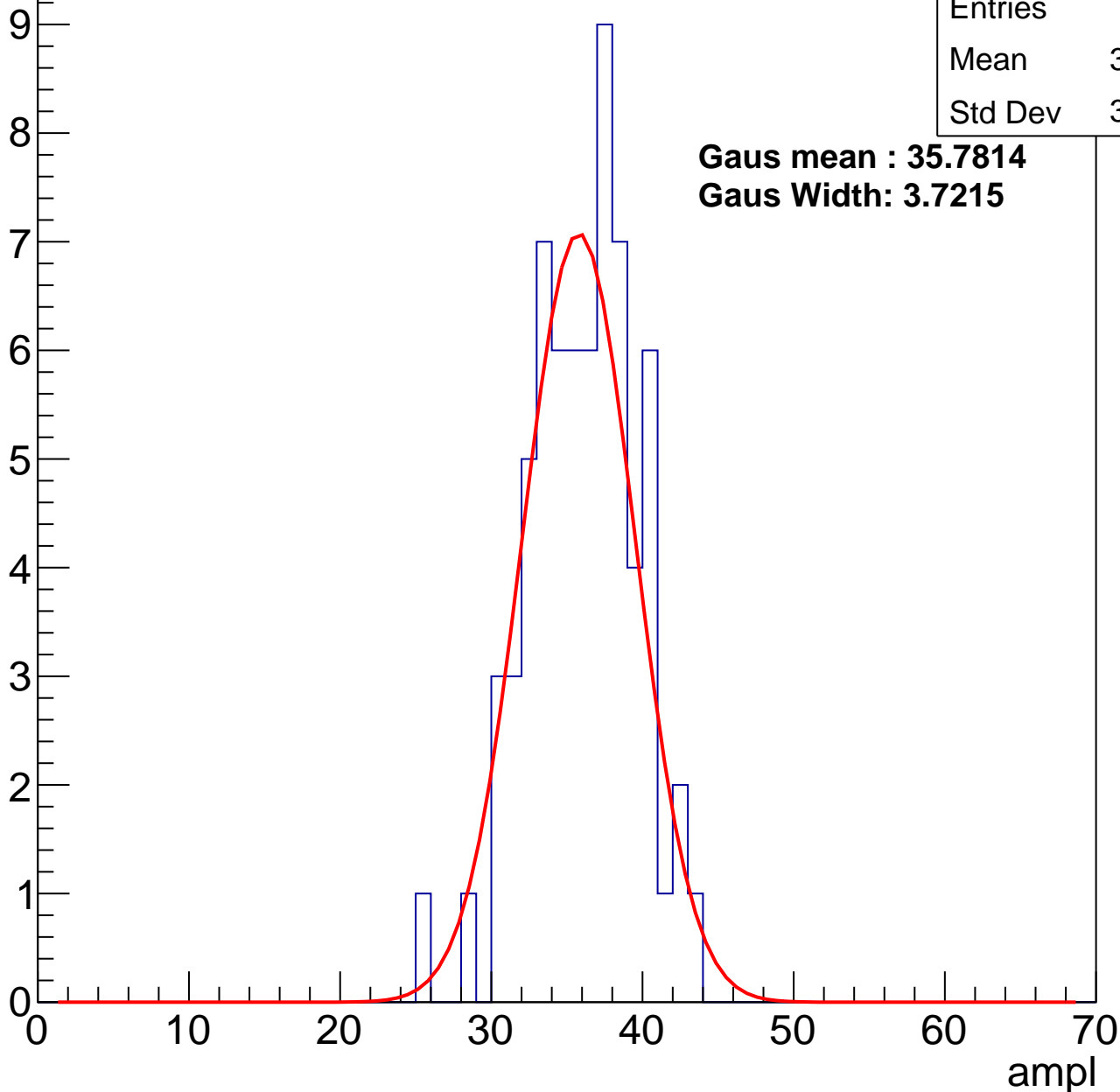
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.59
Std Dev	3.524

Gaus mean : 35.7814

Gaus Width: 3.7215



B1L103S, U5-ch50, adc2

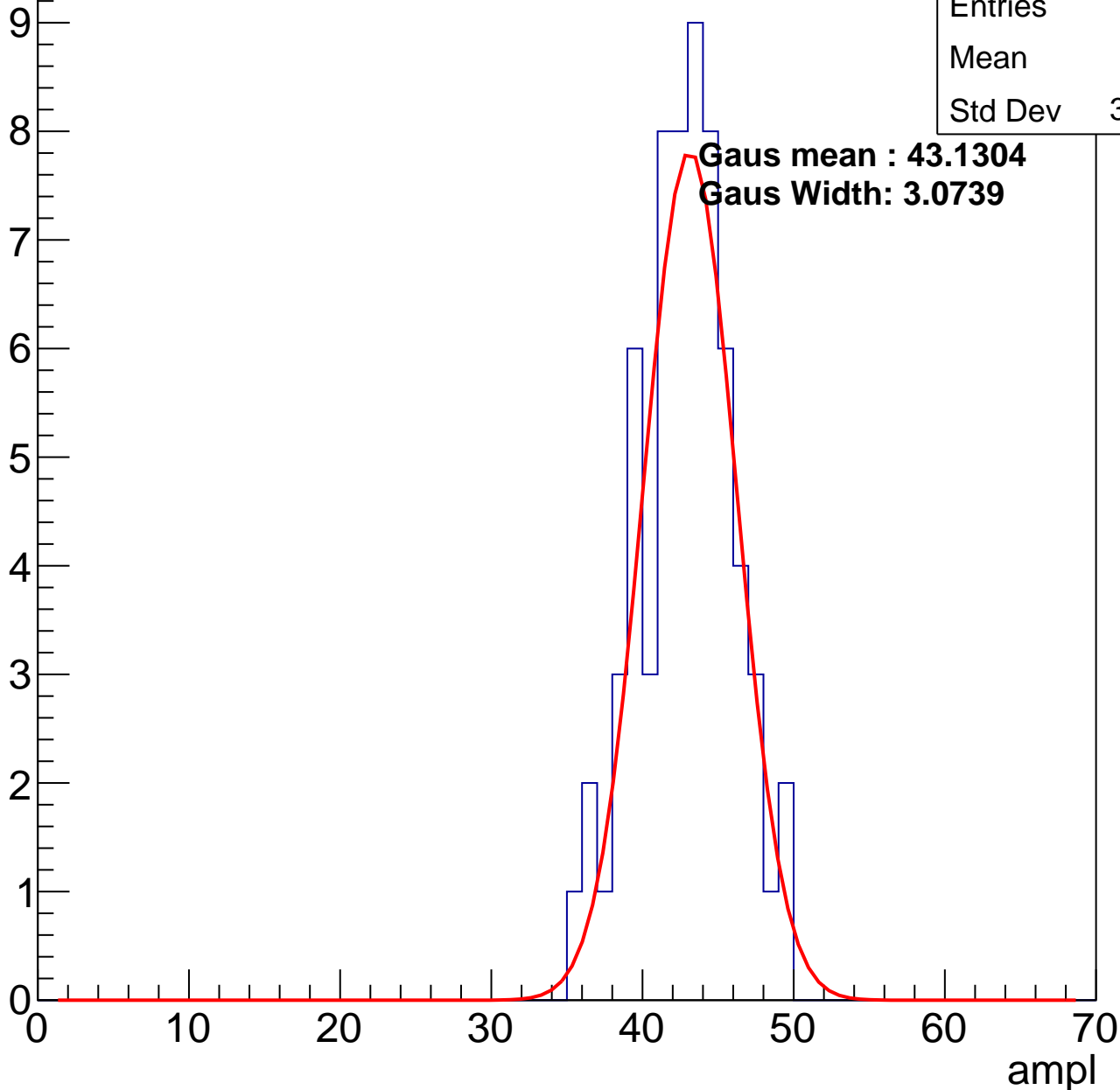
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.4
Std Dev	3.102

Gaus mean : 43.1304

Gaus Width: 3.0739

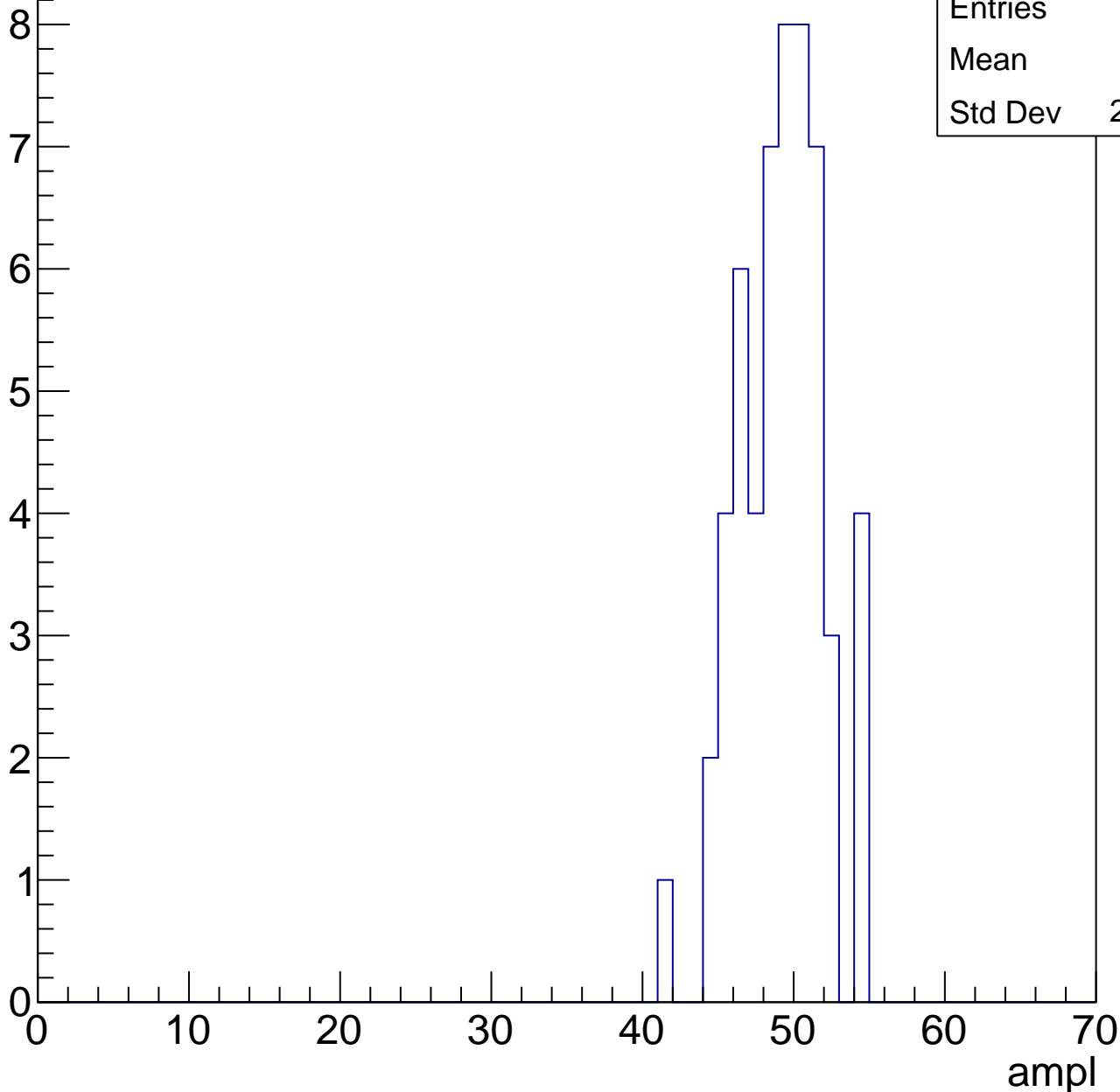


B1L103S, U5-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

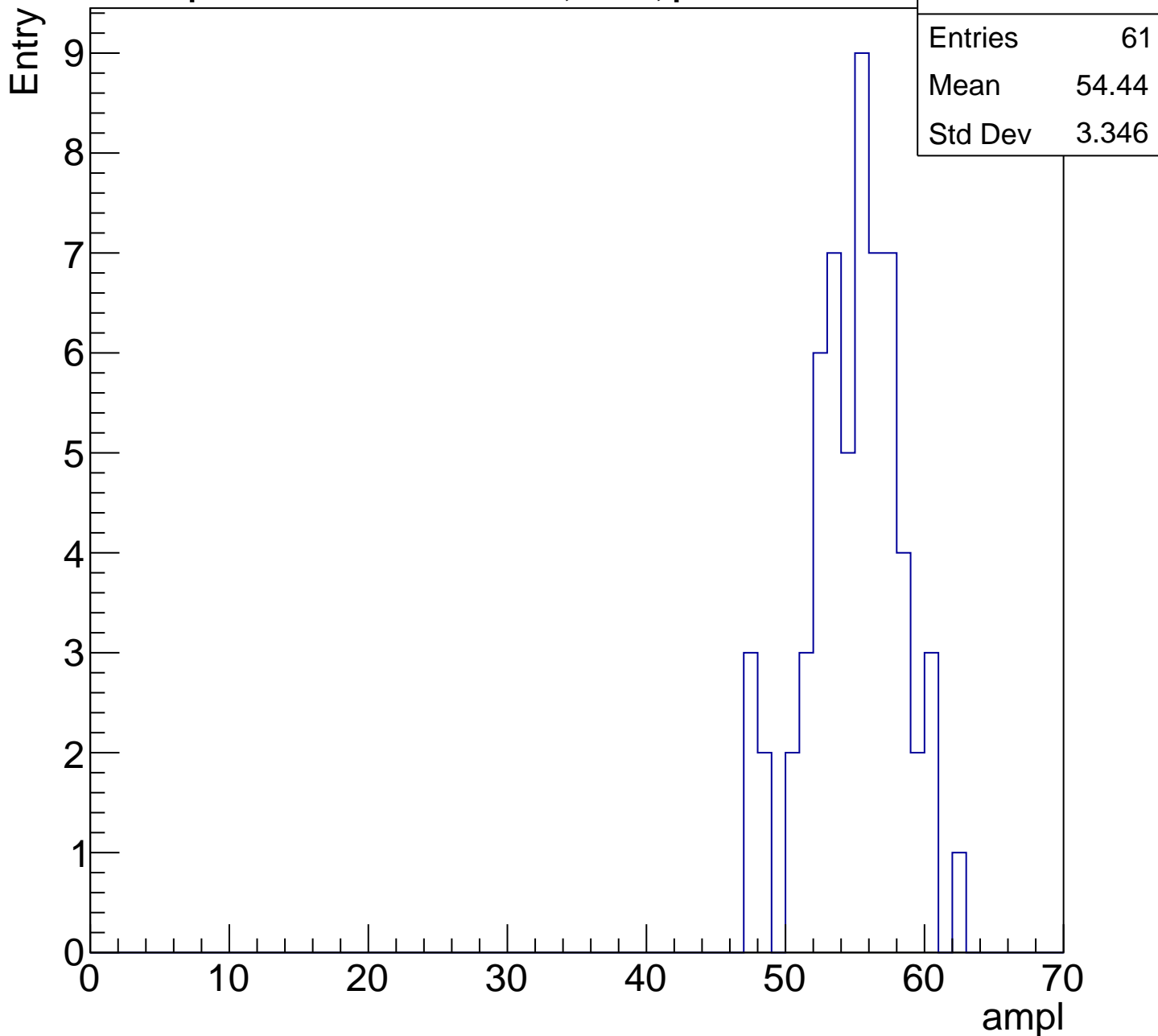
Entry

Entries	54
Mean	48.7
Std Dev	2.766



B1L103S, U5-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

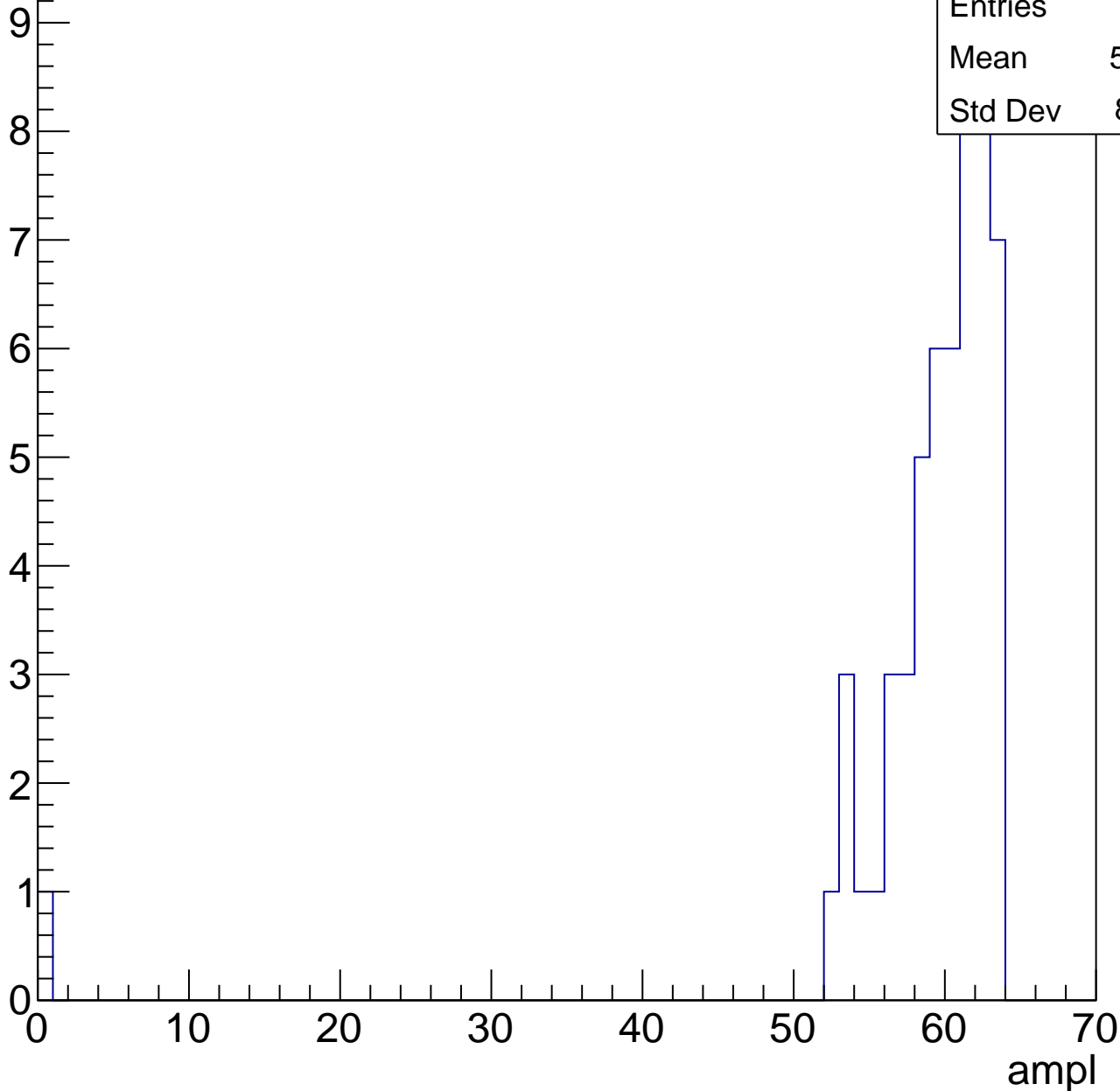


B1L103S, U5-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	58.33
Std Dev	8.531



B1L103S, U5-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

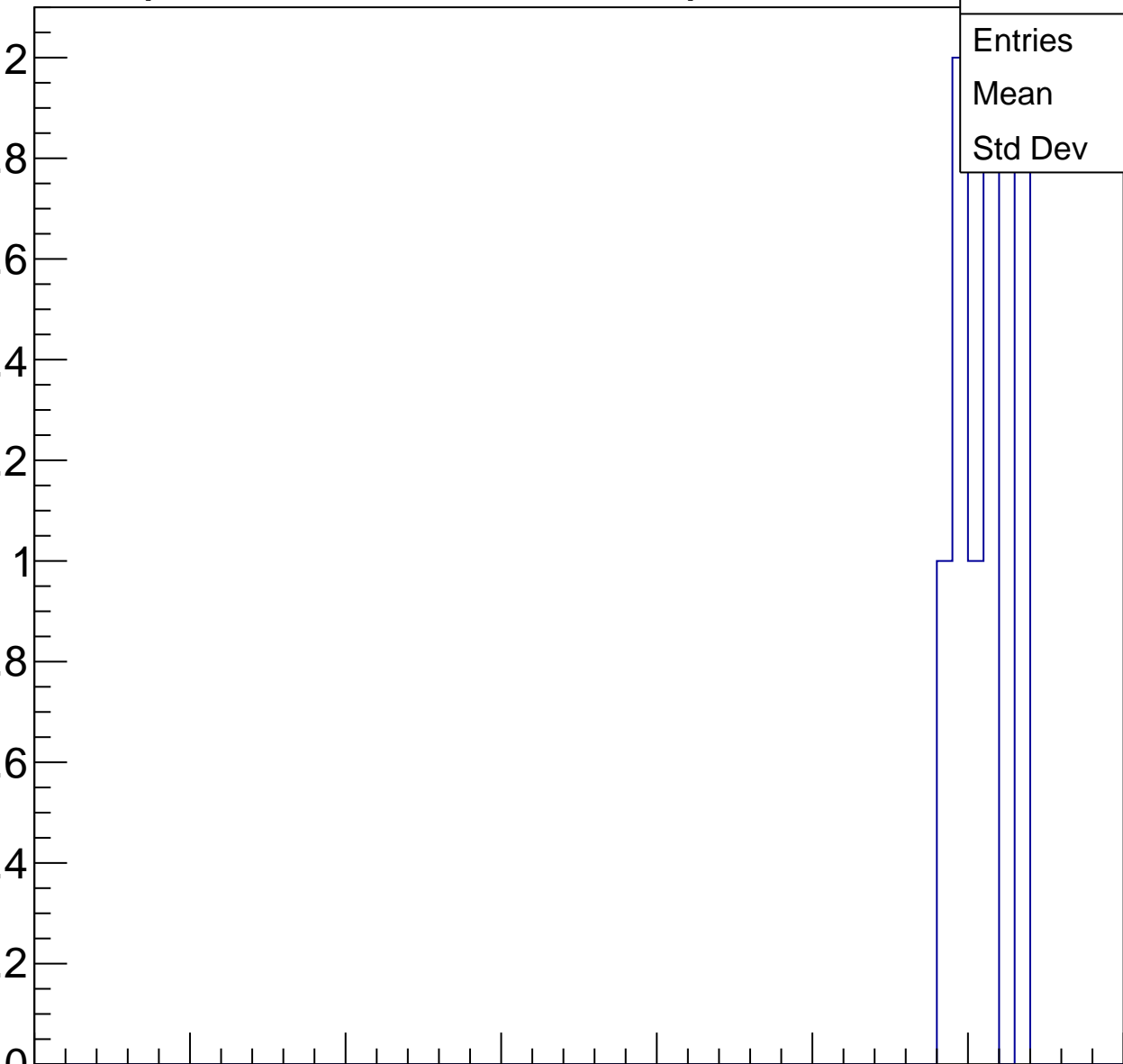
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	60.5
Std Dev	1.732

0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch51, adc0

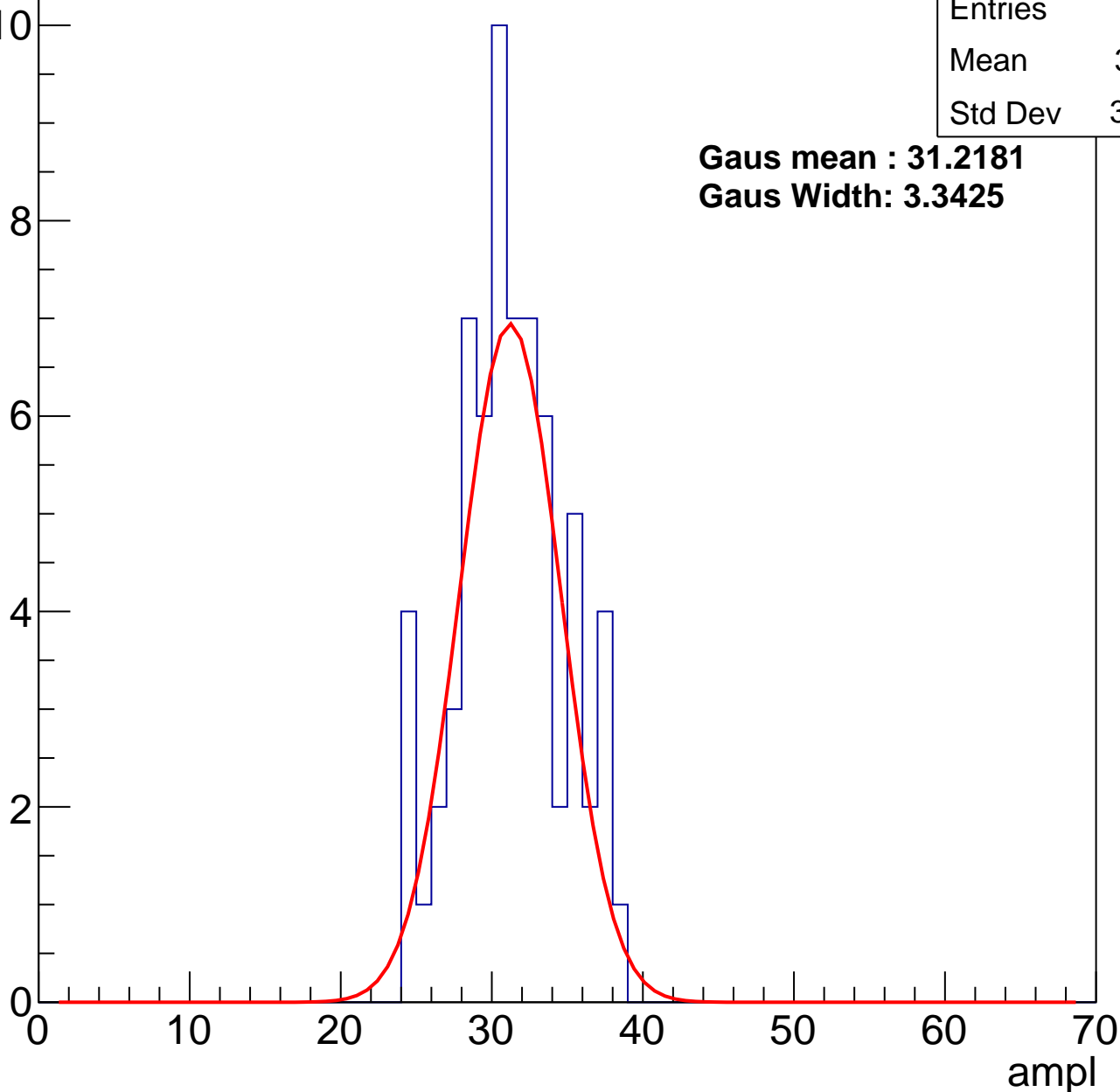
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	30.81
Std Dev	3.448

Gaus mean : 31.2181

Gaus Width: 3.3425



B1L103S, U5-ch51, adc1

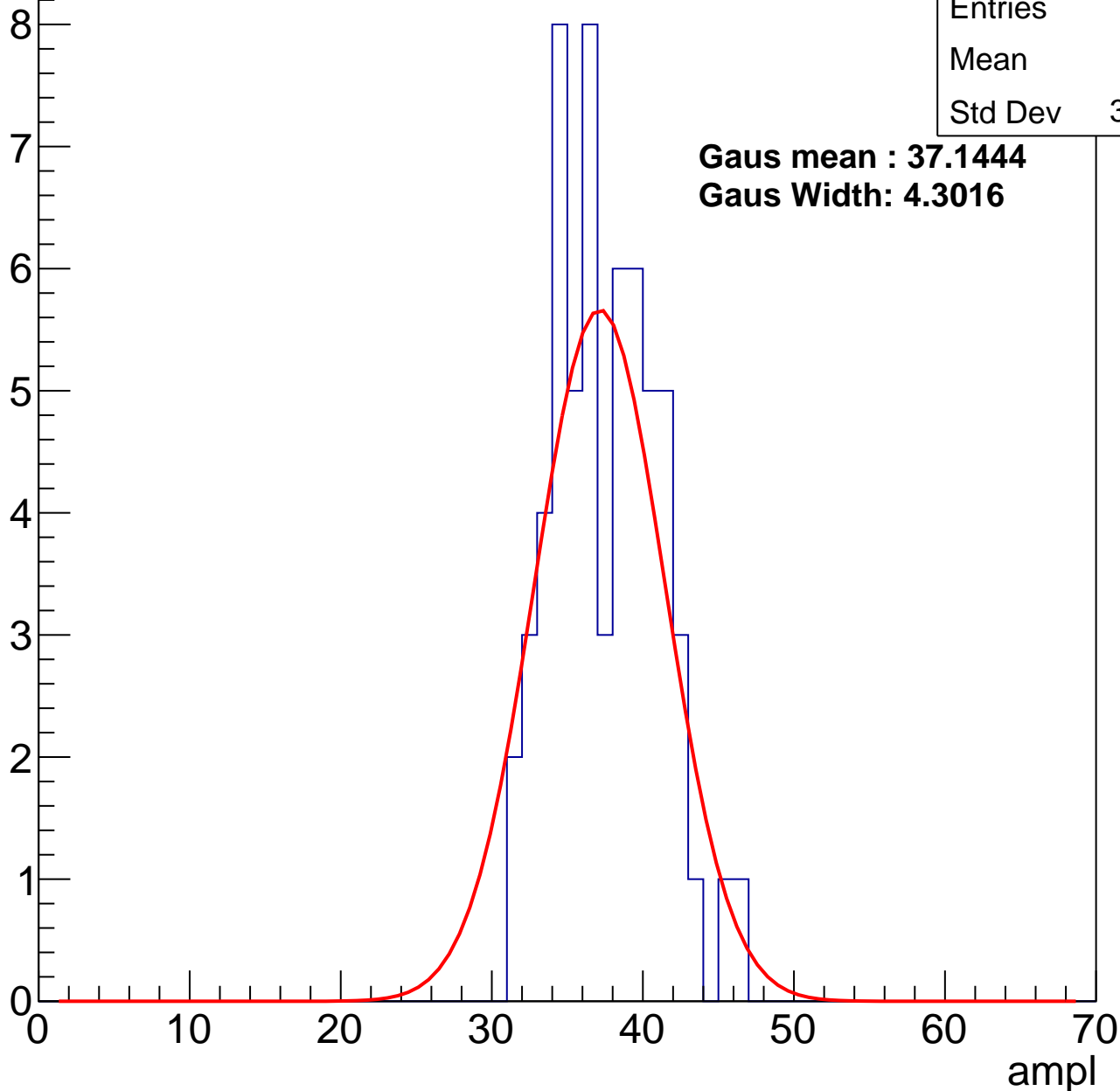
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.1
Std Dev	3.439

Gaus mean : 37.1444

Gaus Width: 4.3016



B1L103S, U5-ch51, adc2

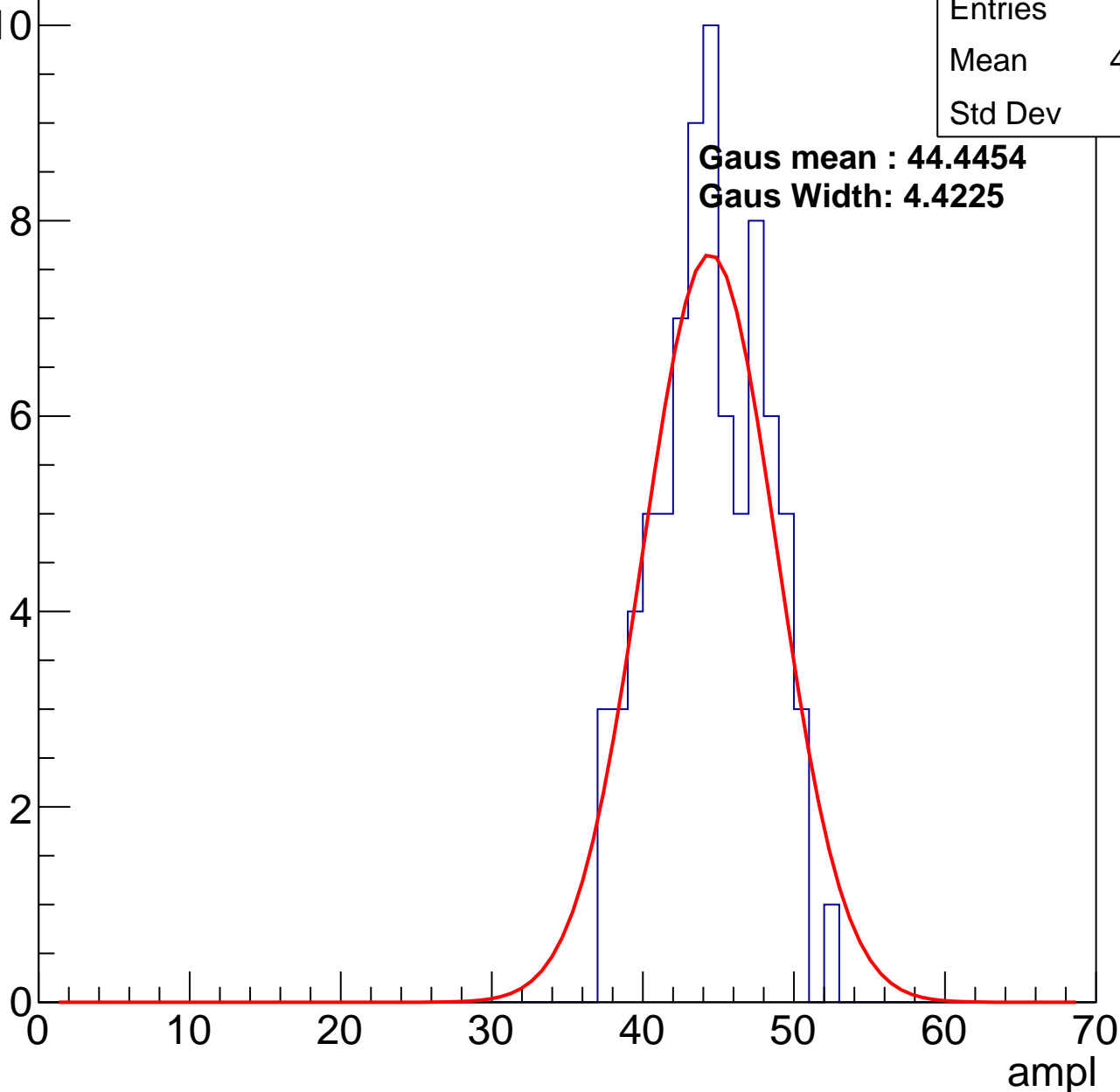
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	43.98
Std Dev	3.55

Gaus mean : 44.4454

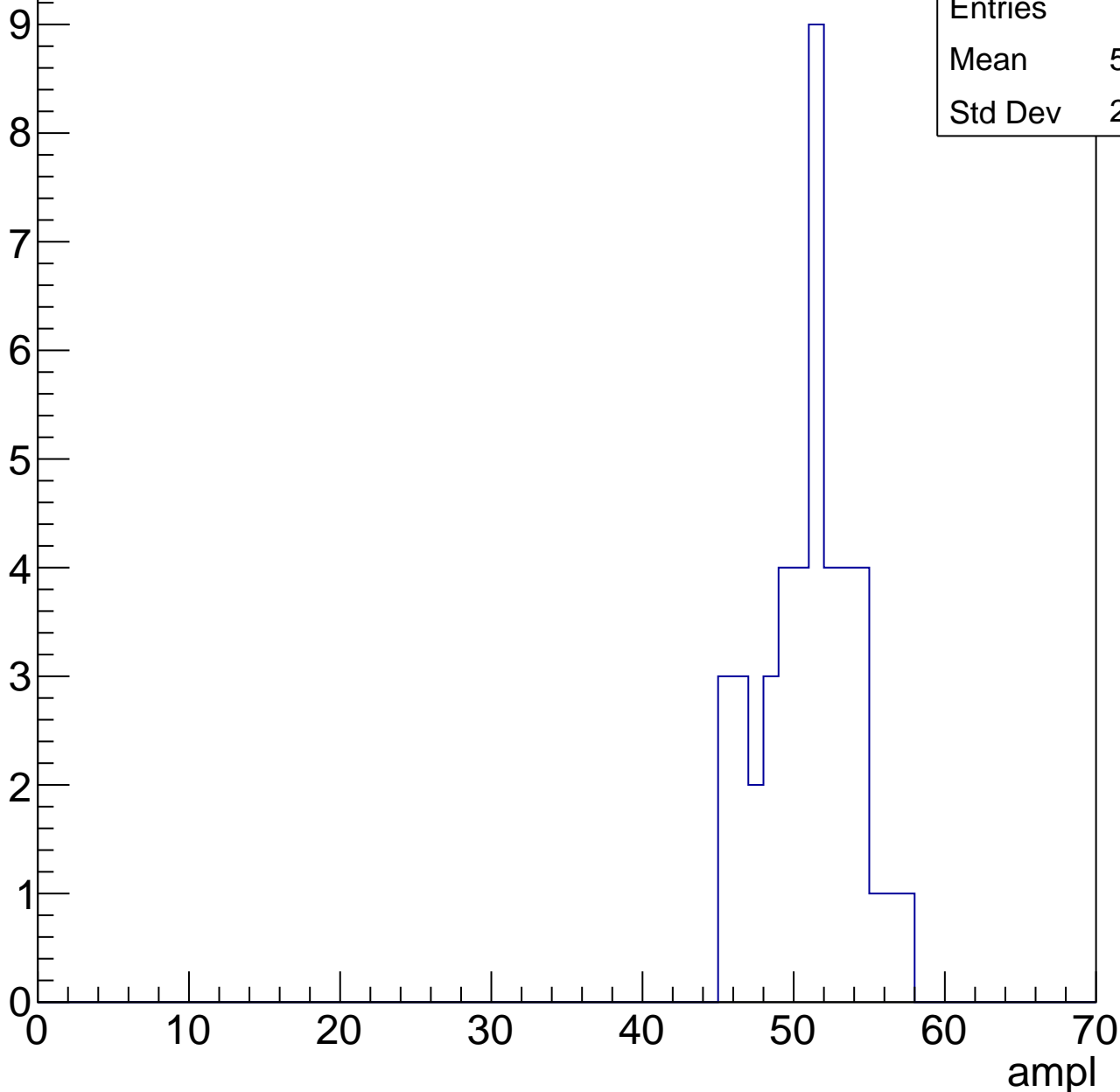
Gaus Width: 4.4225



B1L103S, U5-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

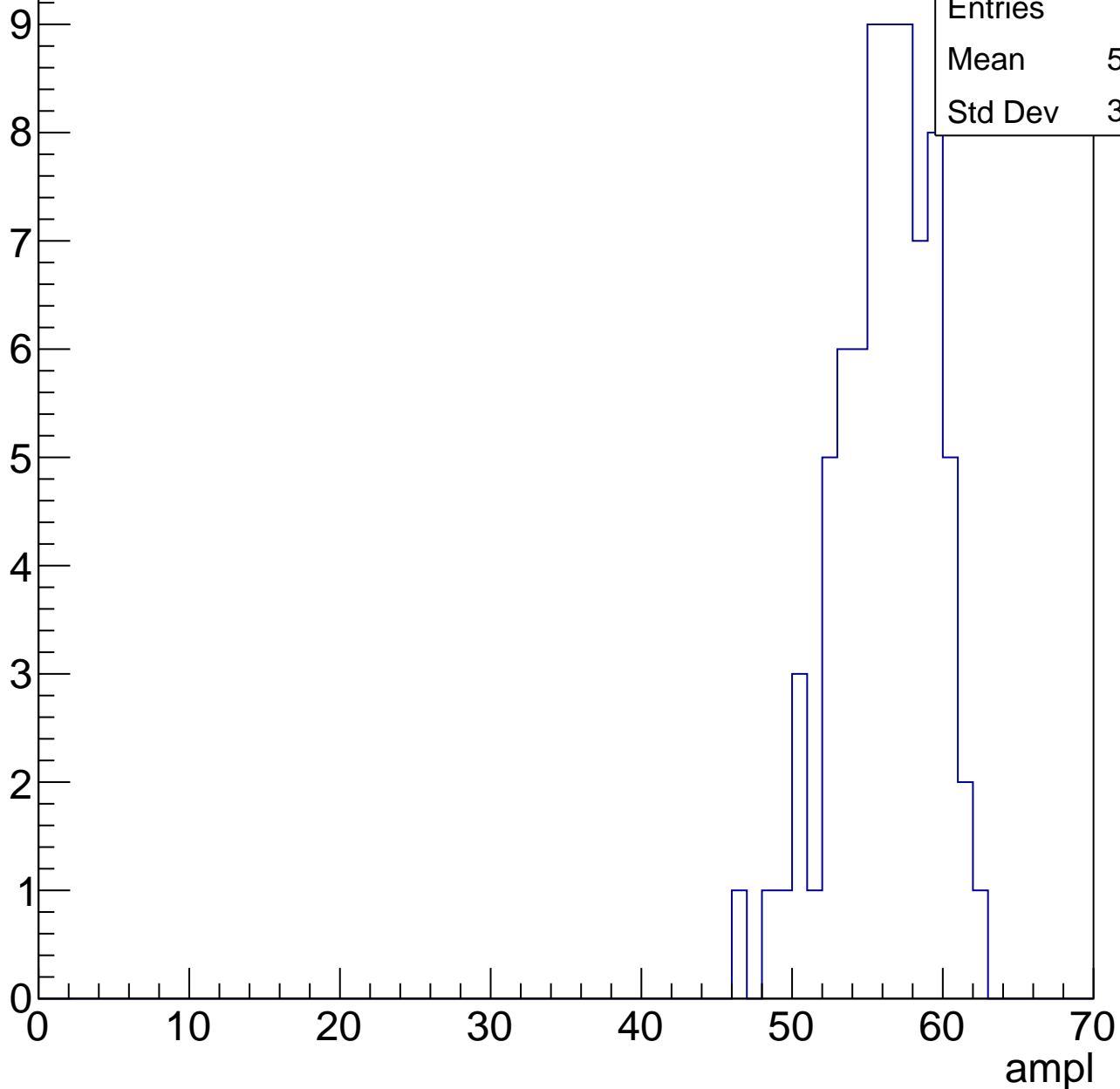


Entries	43
Mean	50.47
Std Dev	2.983

B1L103S, U5-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

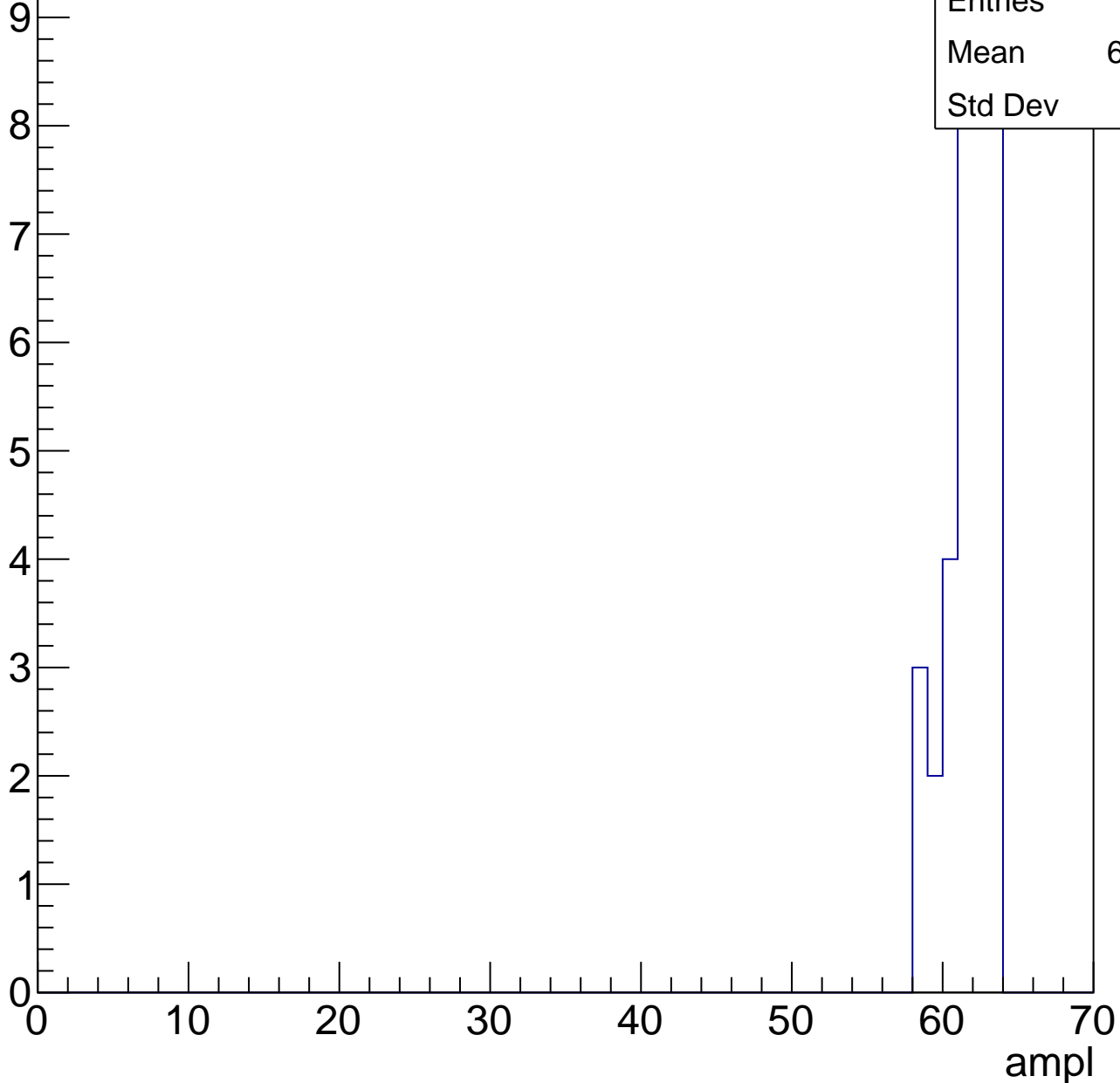
Entry



B1L103S, U5-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

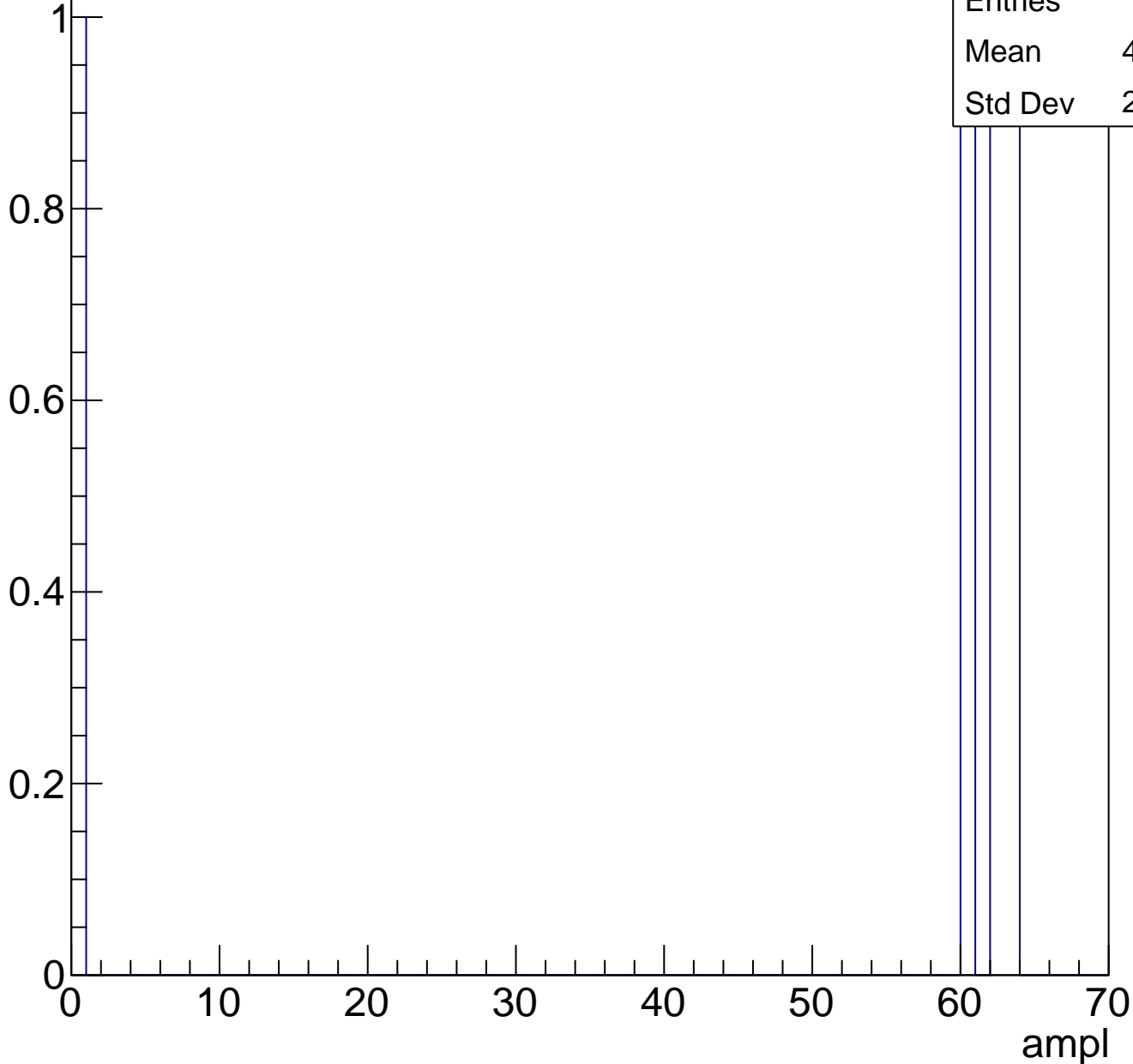
Entry



B1L103S, U5-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

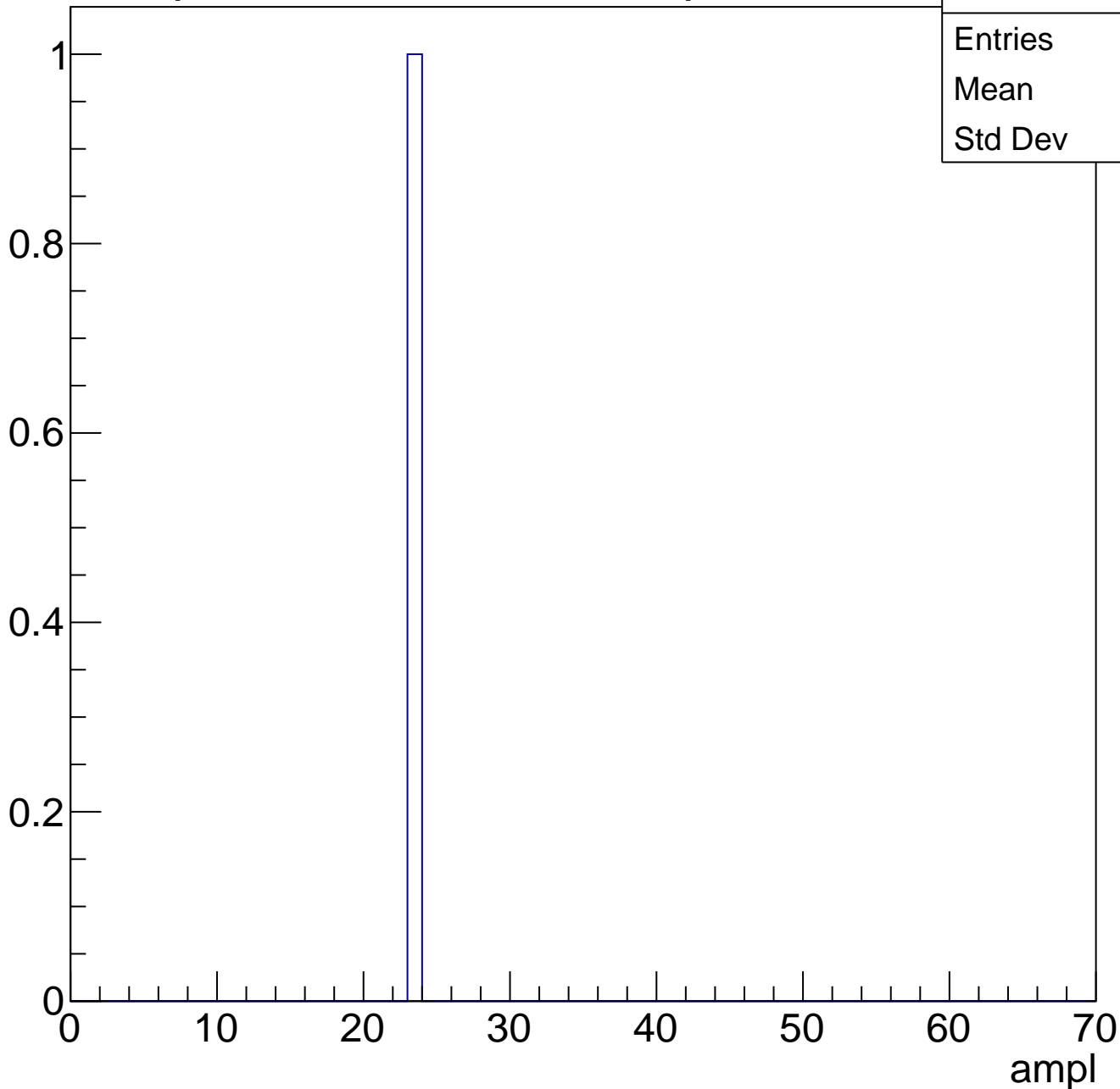
Entry



B1L103S, U5-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L103S, U5-ch52, adc0

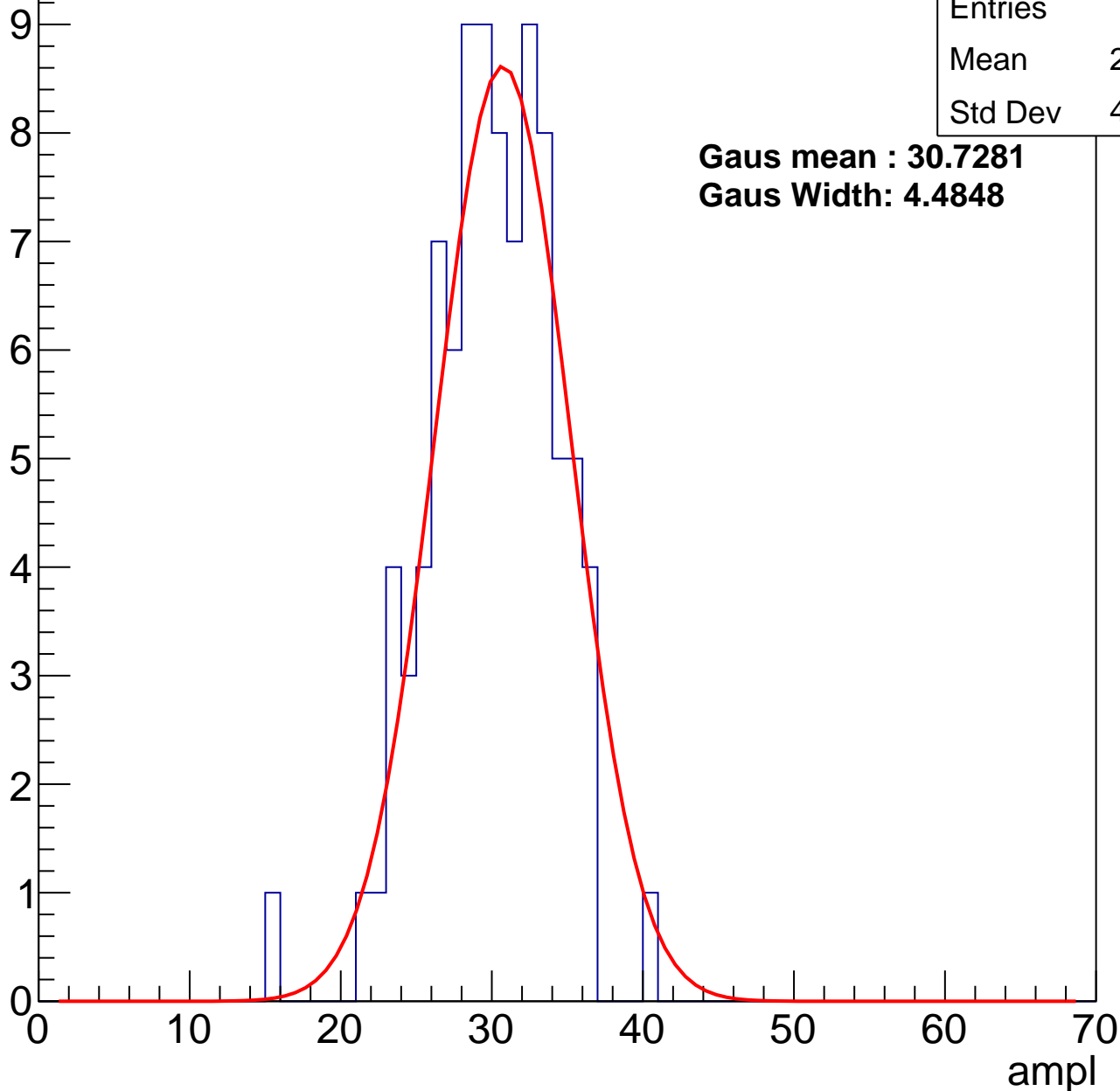
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	92
Mean	29.53
Std Dev	4.077

Gaus mean : 30.7281

Gaus Width: 4.4848



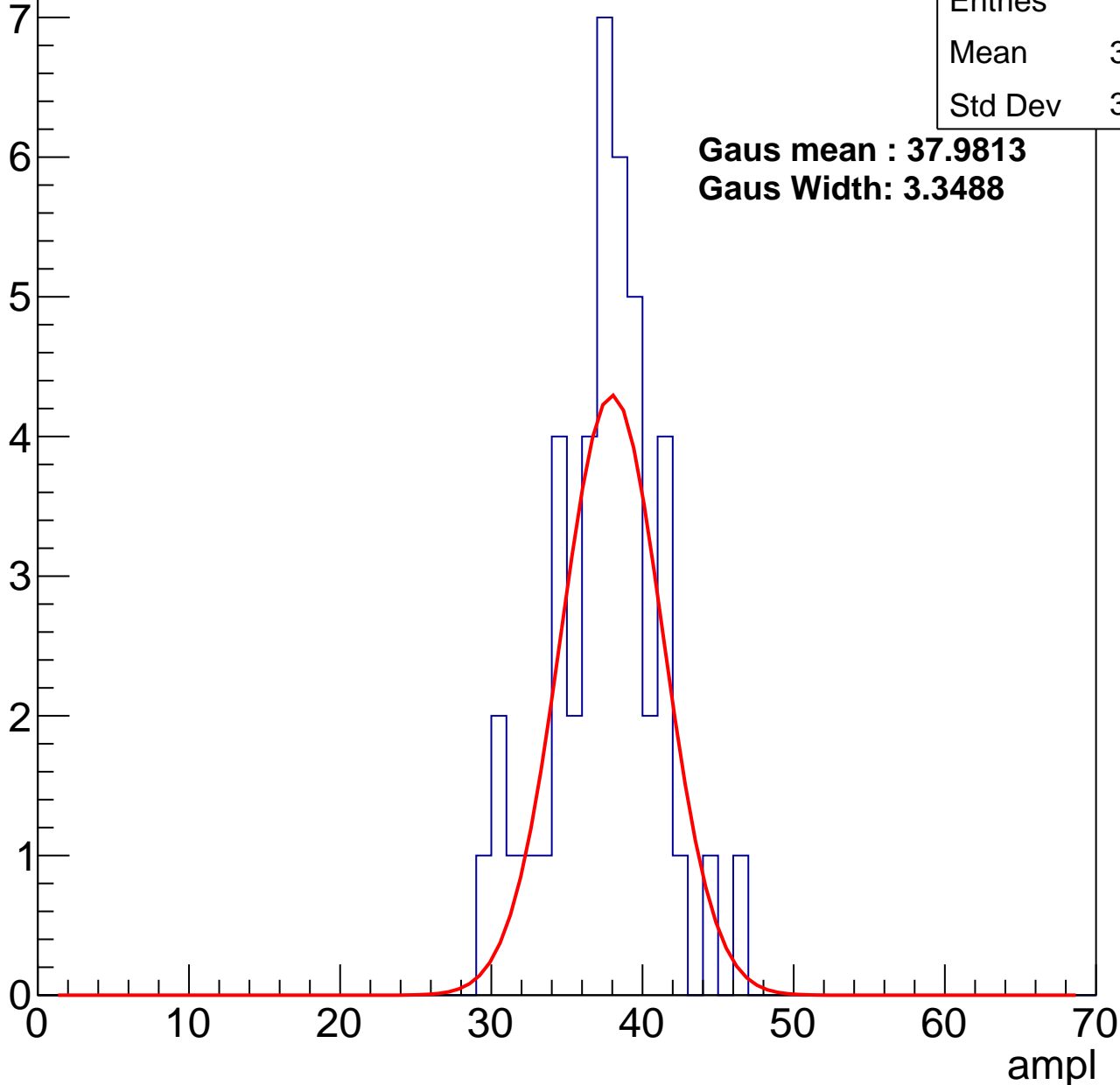
B1L103S, U5-ch52, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	37.05
Std Dev	3.576

Gaus mean : 37.9813
Gaus Width: 3.3488



B1L103S, U5-ch52, adc2

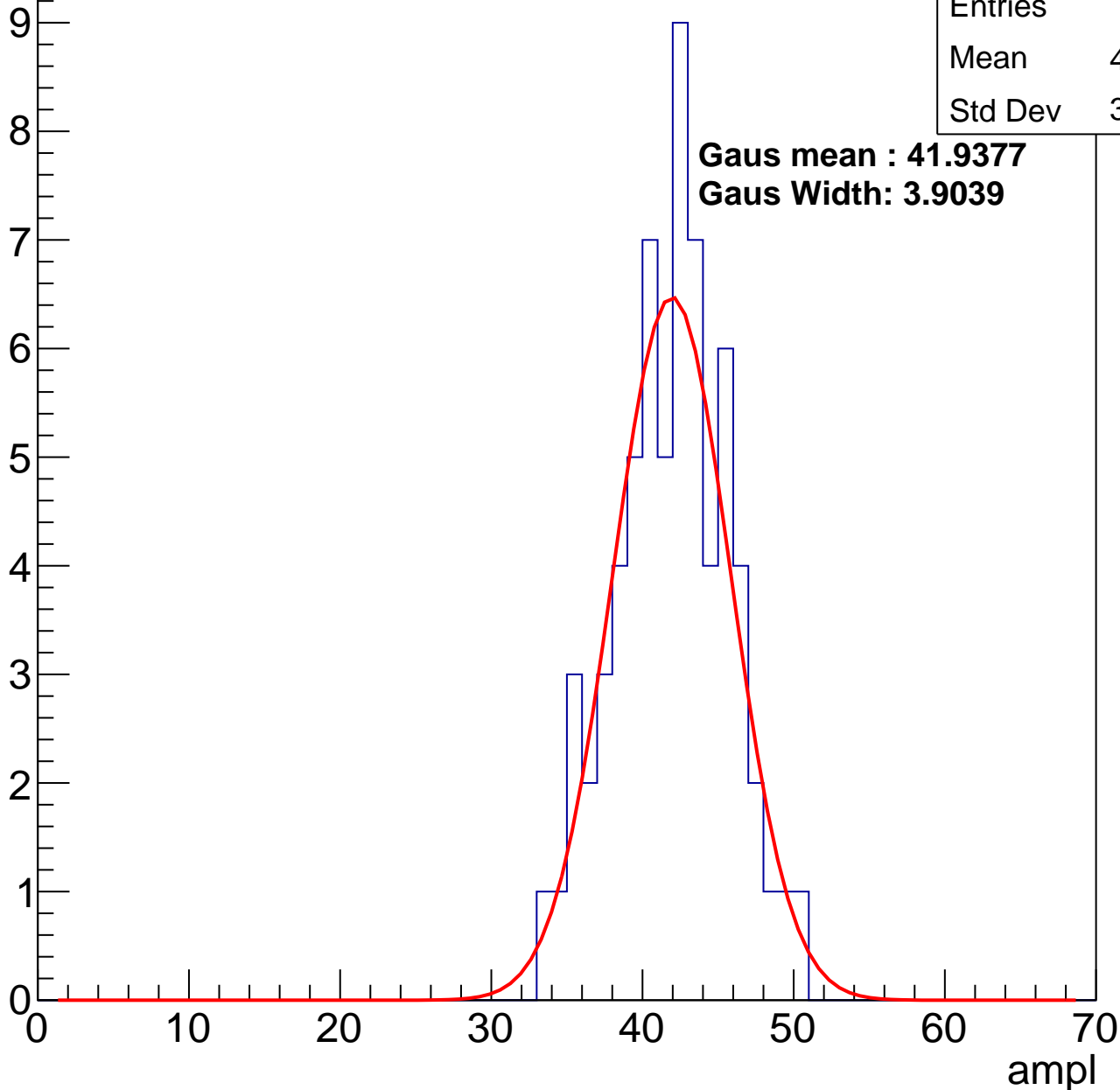
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	41.47
Std Dev	3.698

Gaus mean : 41.9377

Gaus Width: 3.9039

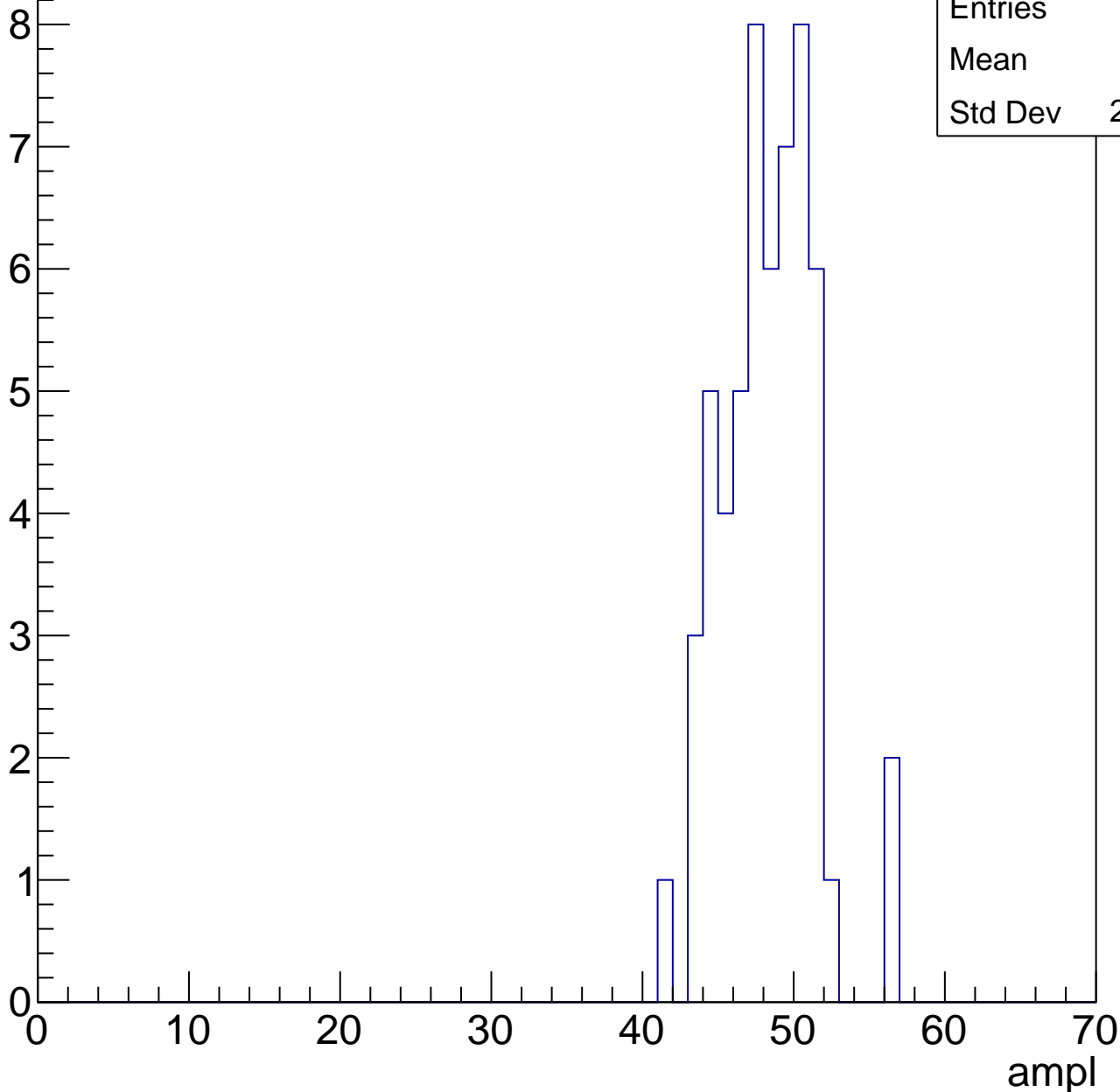


B1L103S, U5-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	47.8
Std Dev	2.997

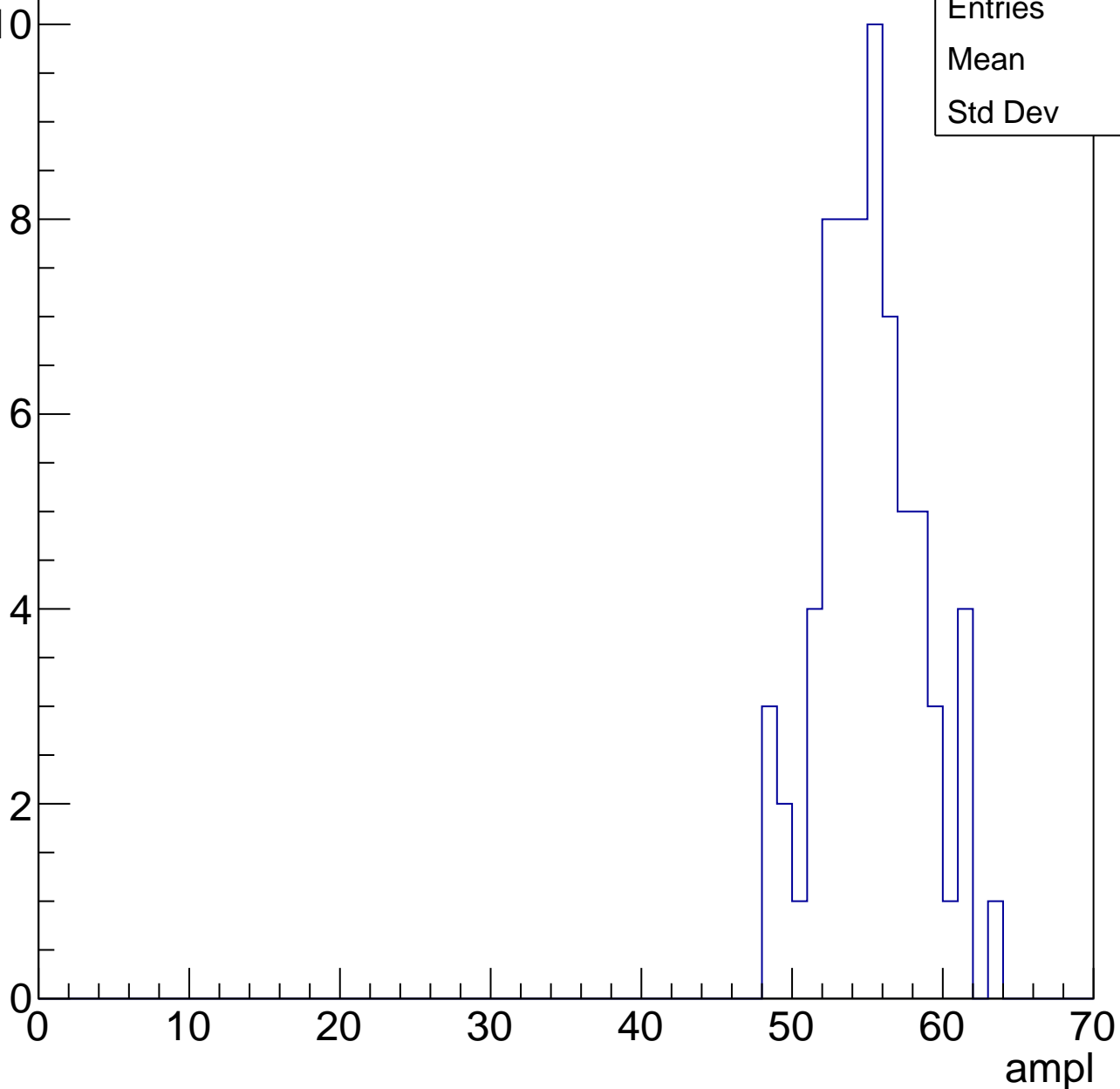


B1L103S, U5-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	54.7
Std Dev	3.31

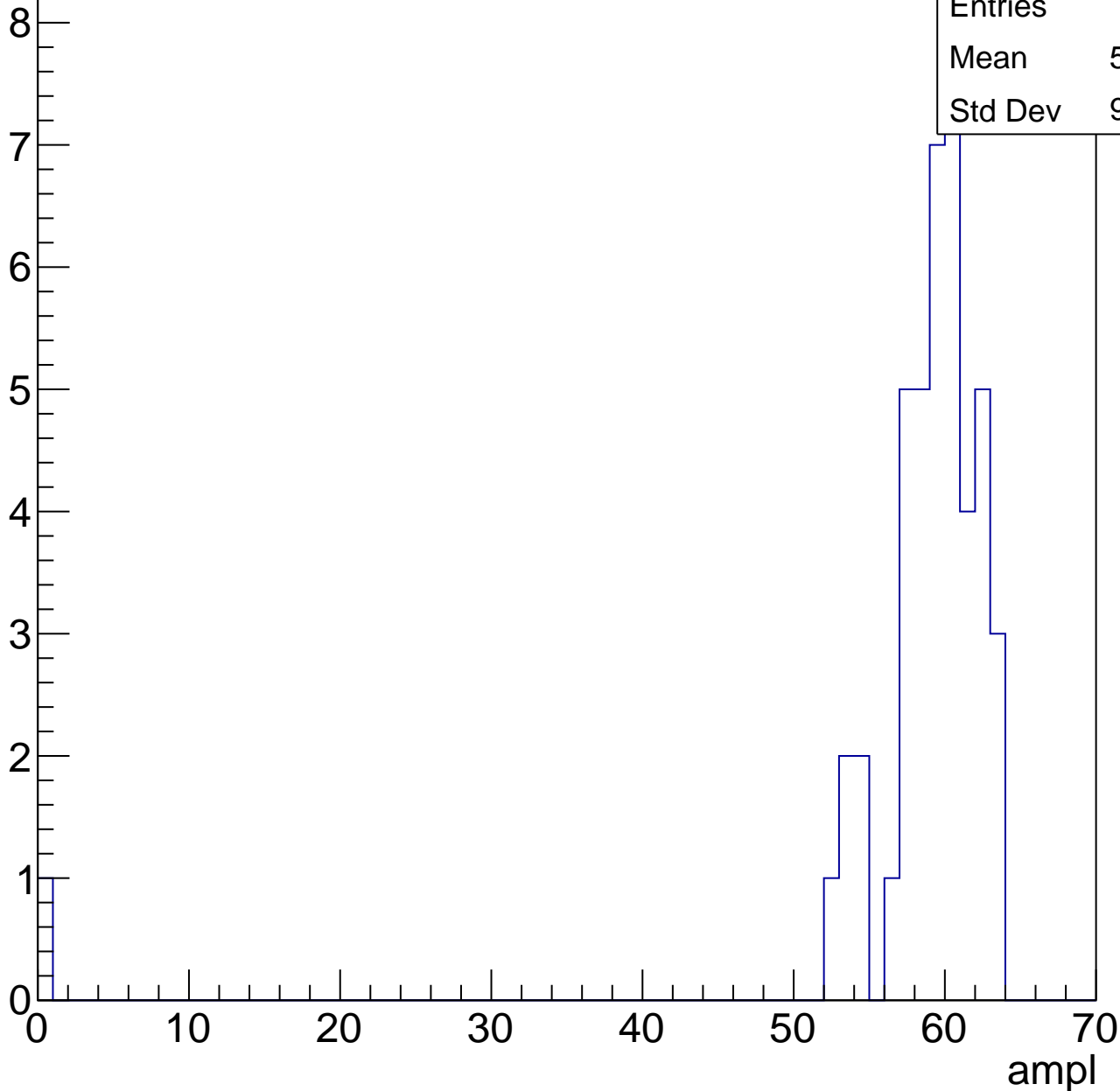


B1L103S, U5-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	57.57
Std Dev	9.186

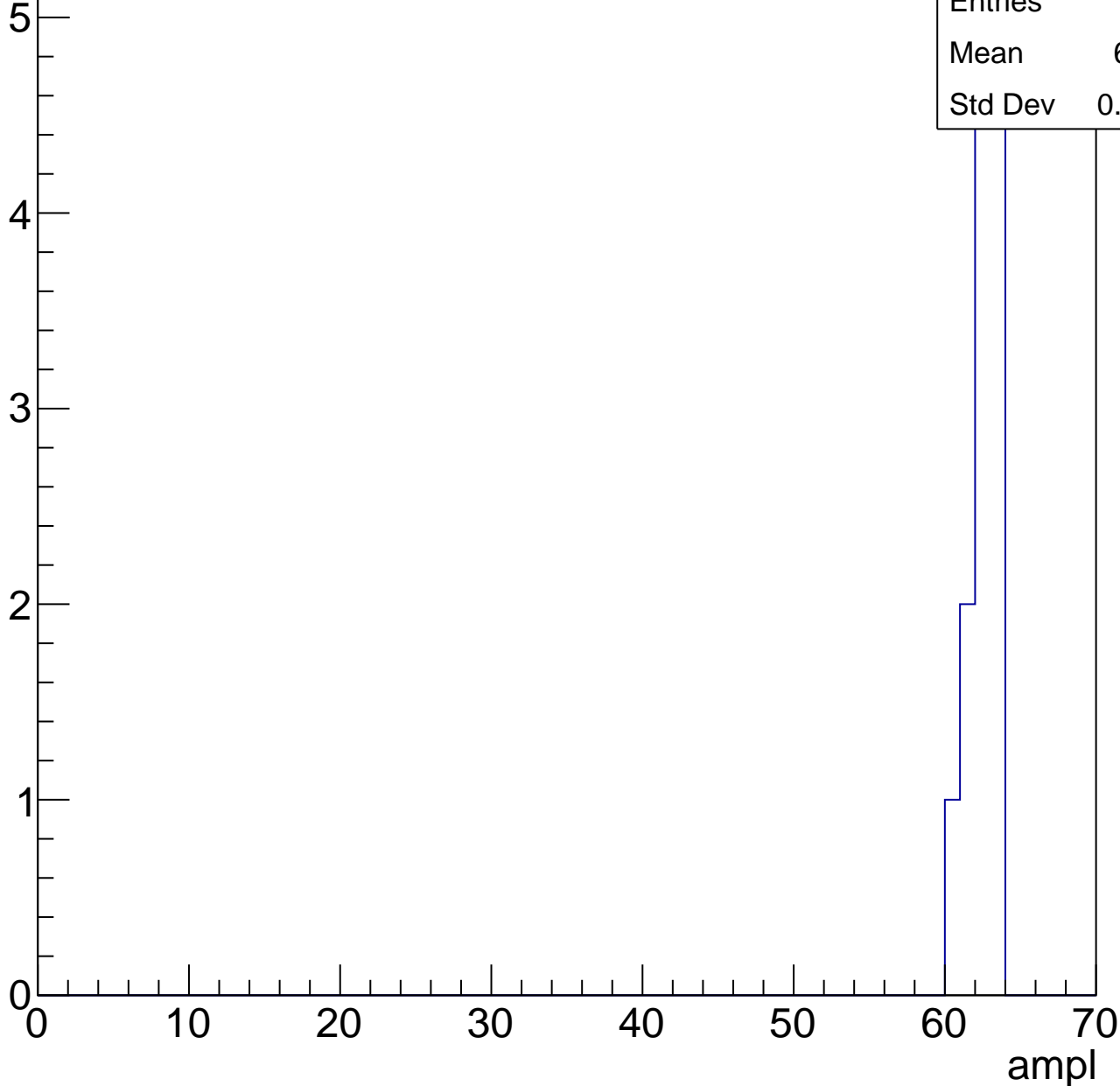


B1L103S, U5-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	62.08
Std Dev	0.9166



B1L103S, U5-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch53, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	63
Mean	28.33
Std Dev	5.933

Gaus mean : 29.9389

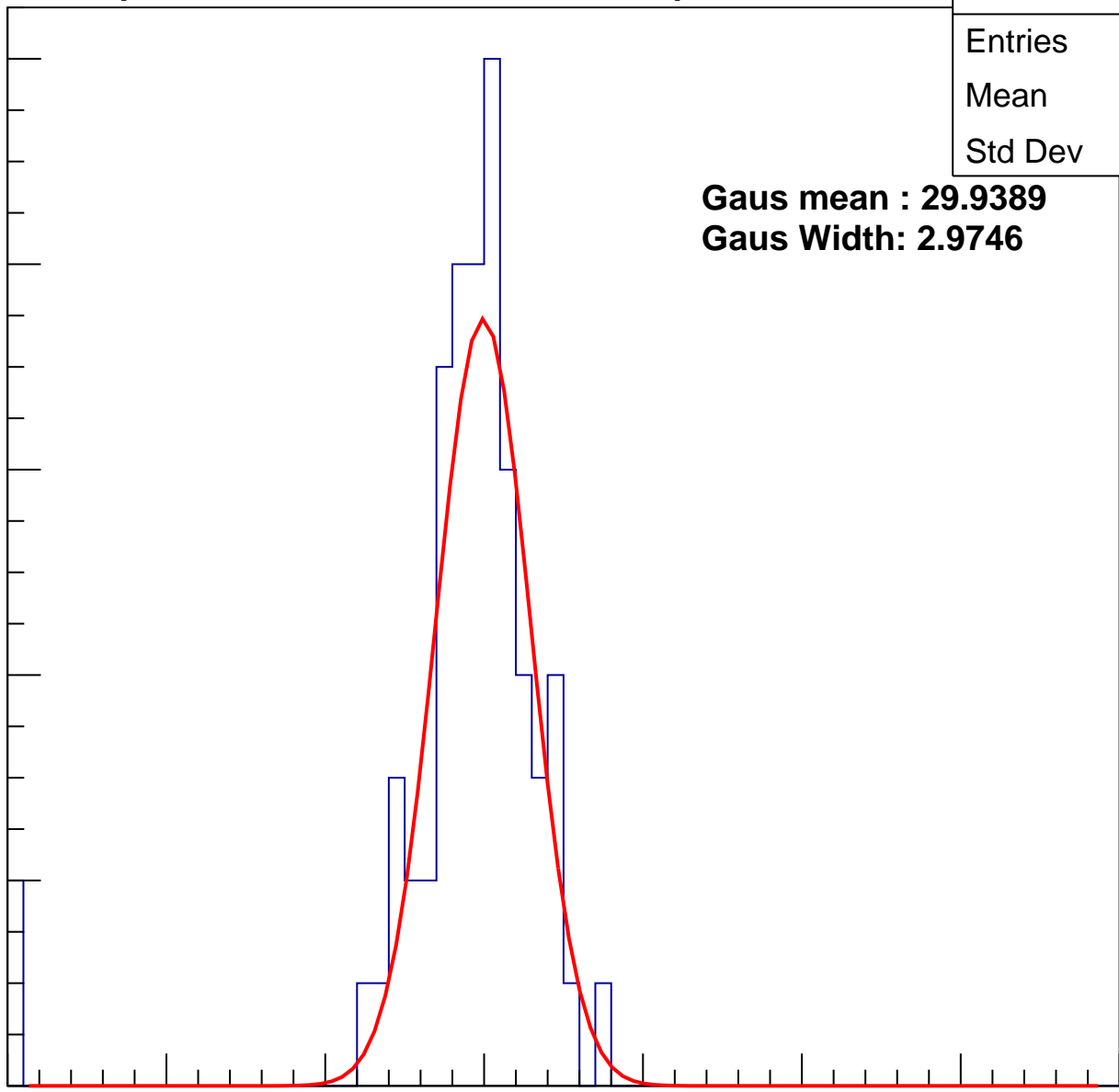
Gaus Width: 2.9746

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch53, adc1

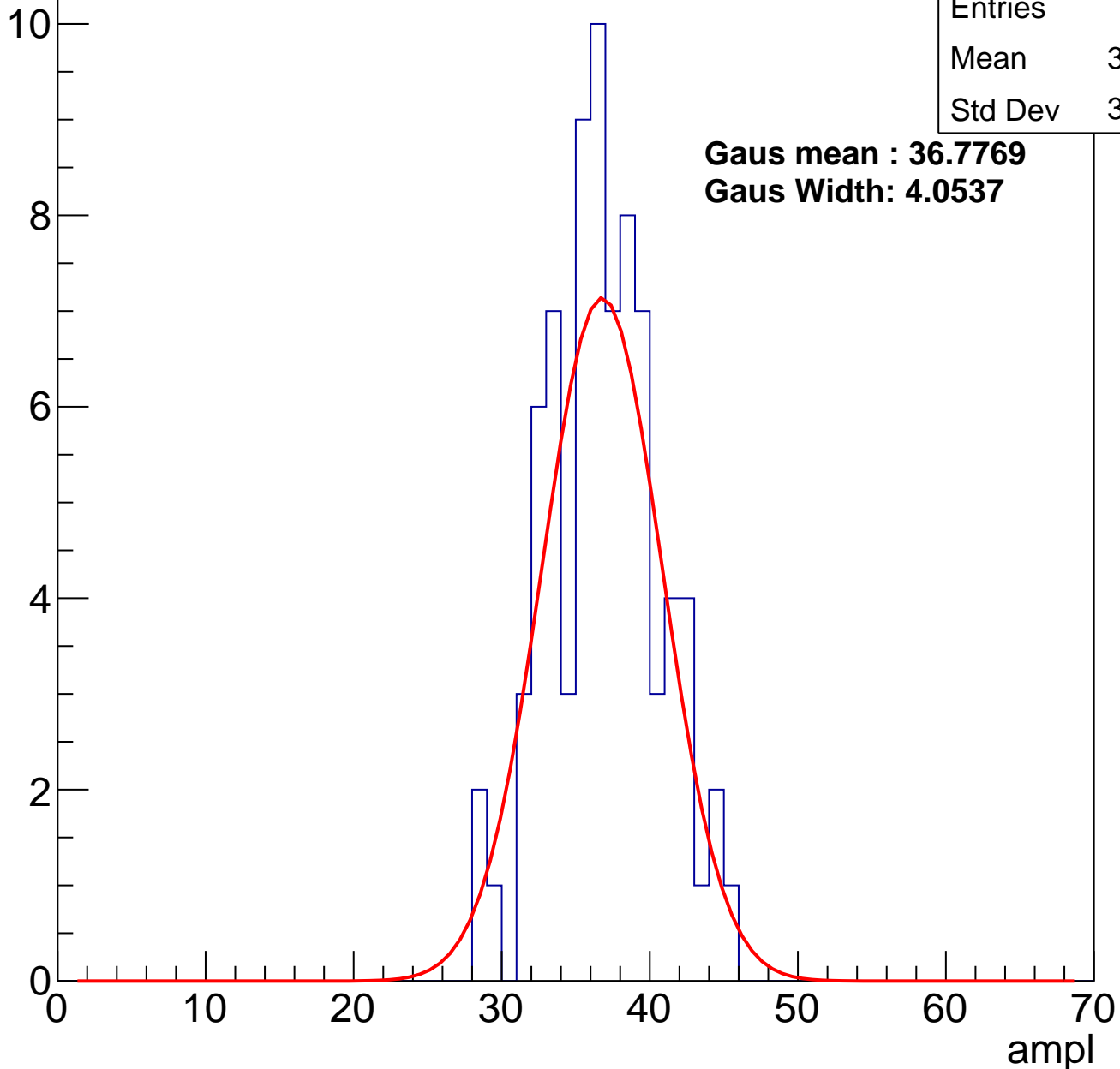
calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	36.44
Std Dev	3.702

Gaus mean : 36.7769

Gaus Width: 4.0537

Entry



B1L103S, U5-ch53, adc2

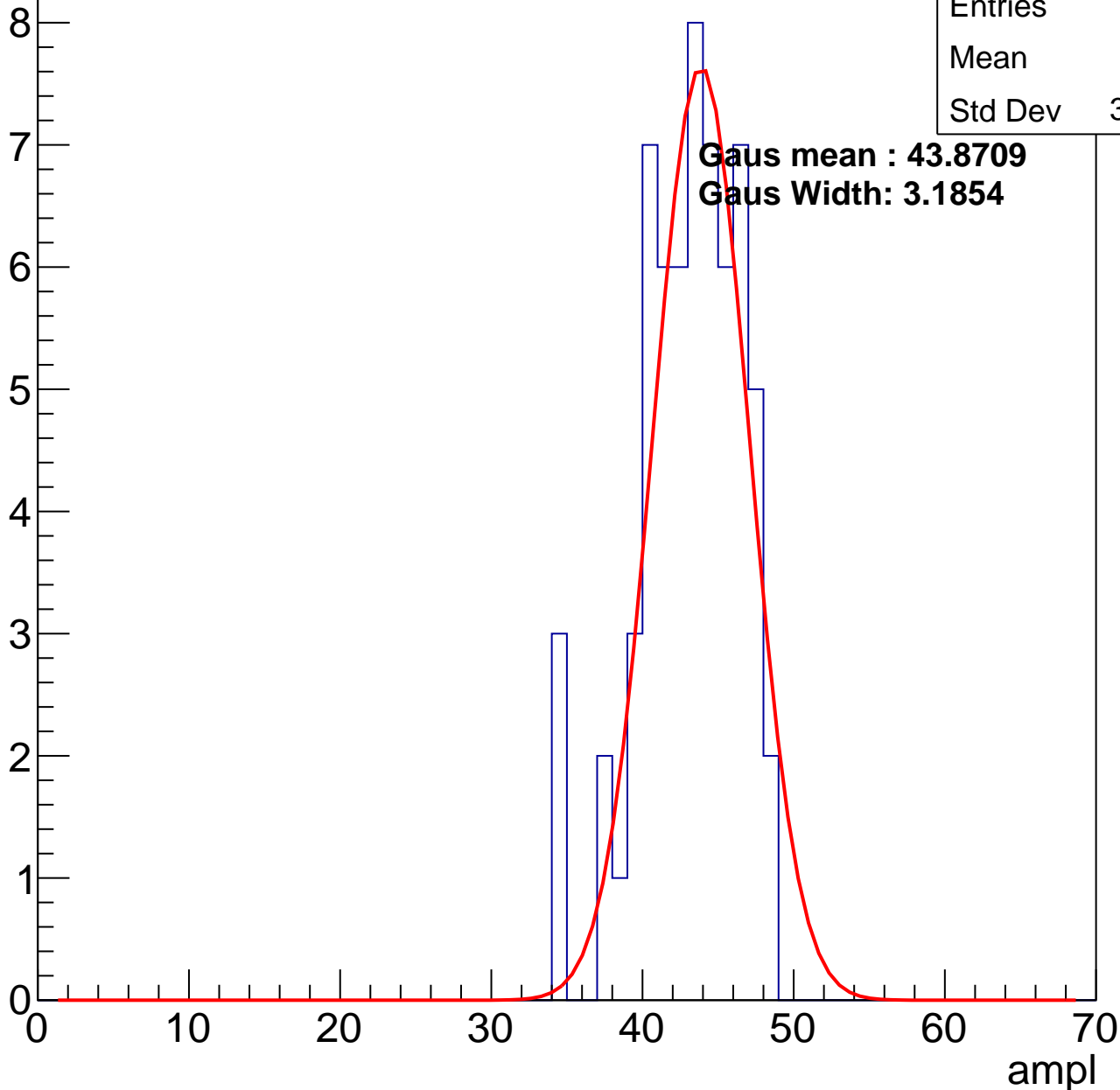
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.6
Std Dev	3.322

Gaus mean : 43.8709

Gaus Width: 3.1854

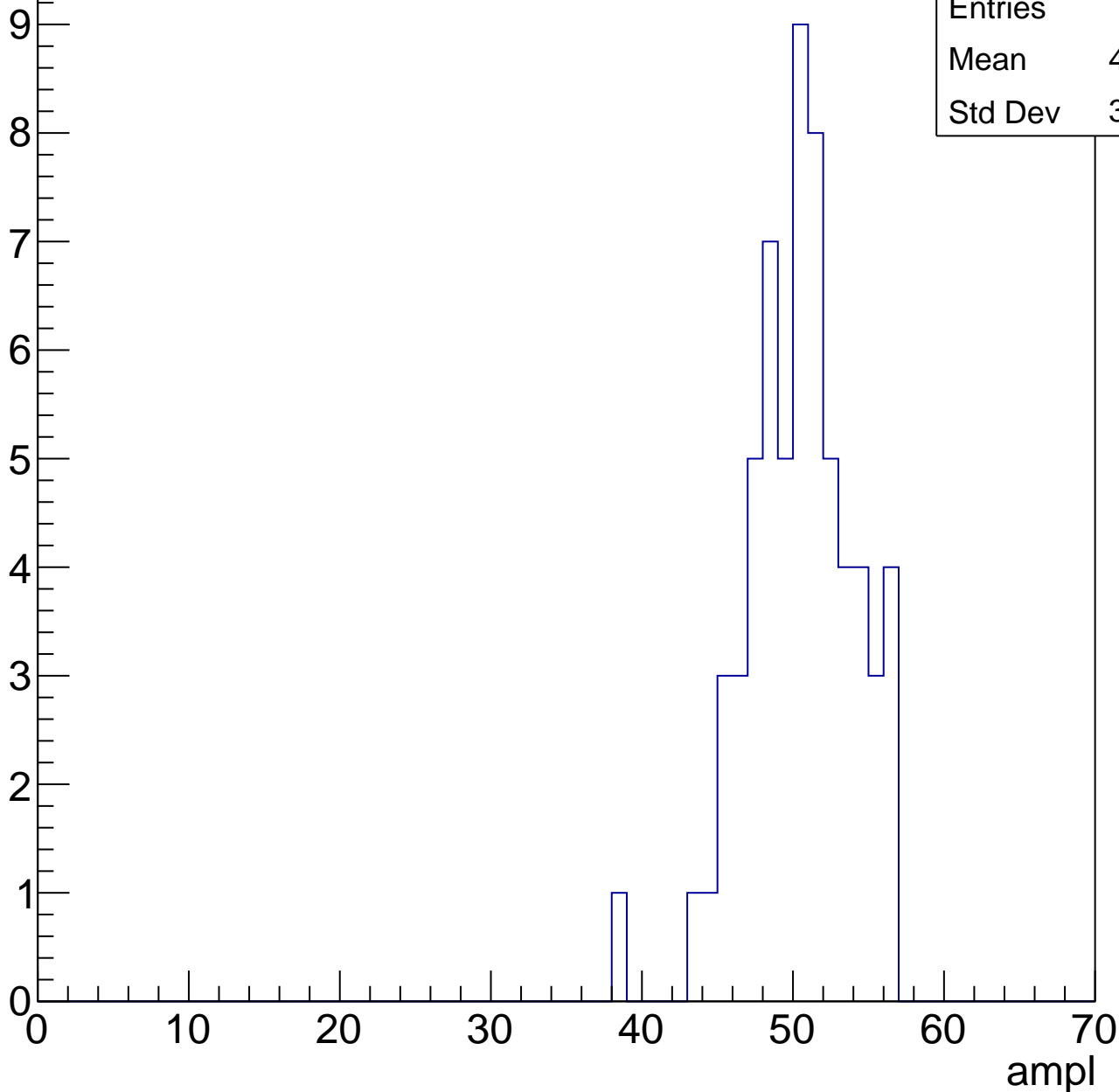


B1L103S, U5-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.98
Std Dev	3.512

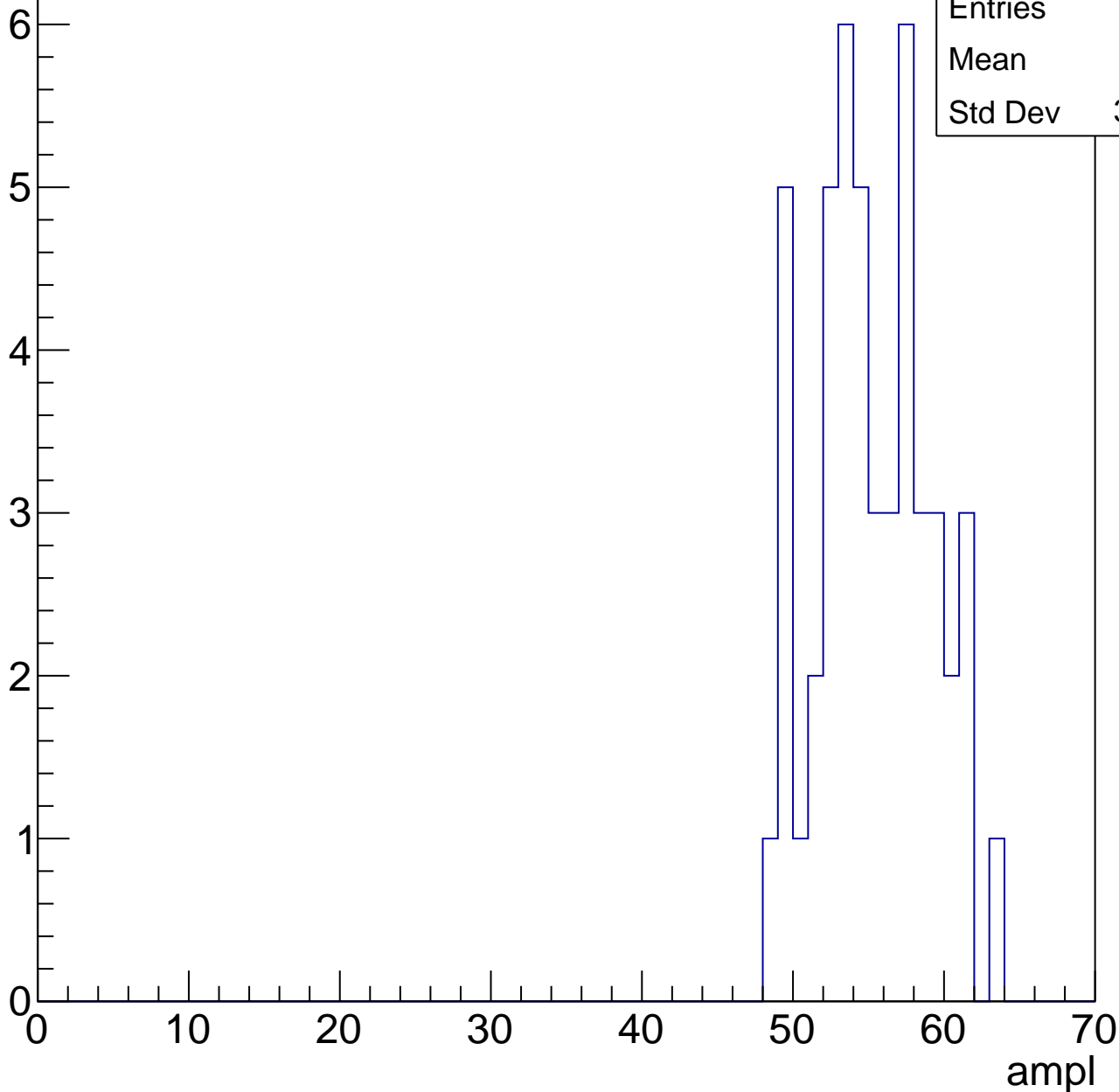


B1L103S, U5-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	54.8
Std Dev	3.731

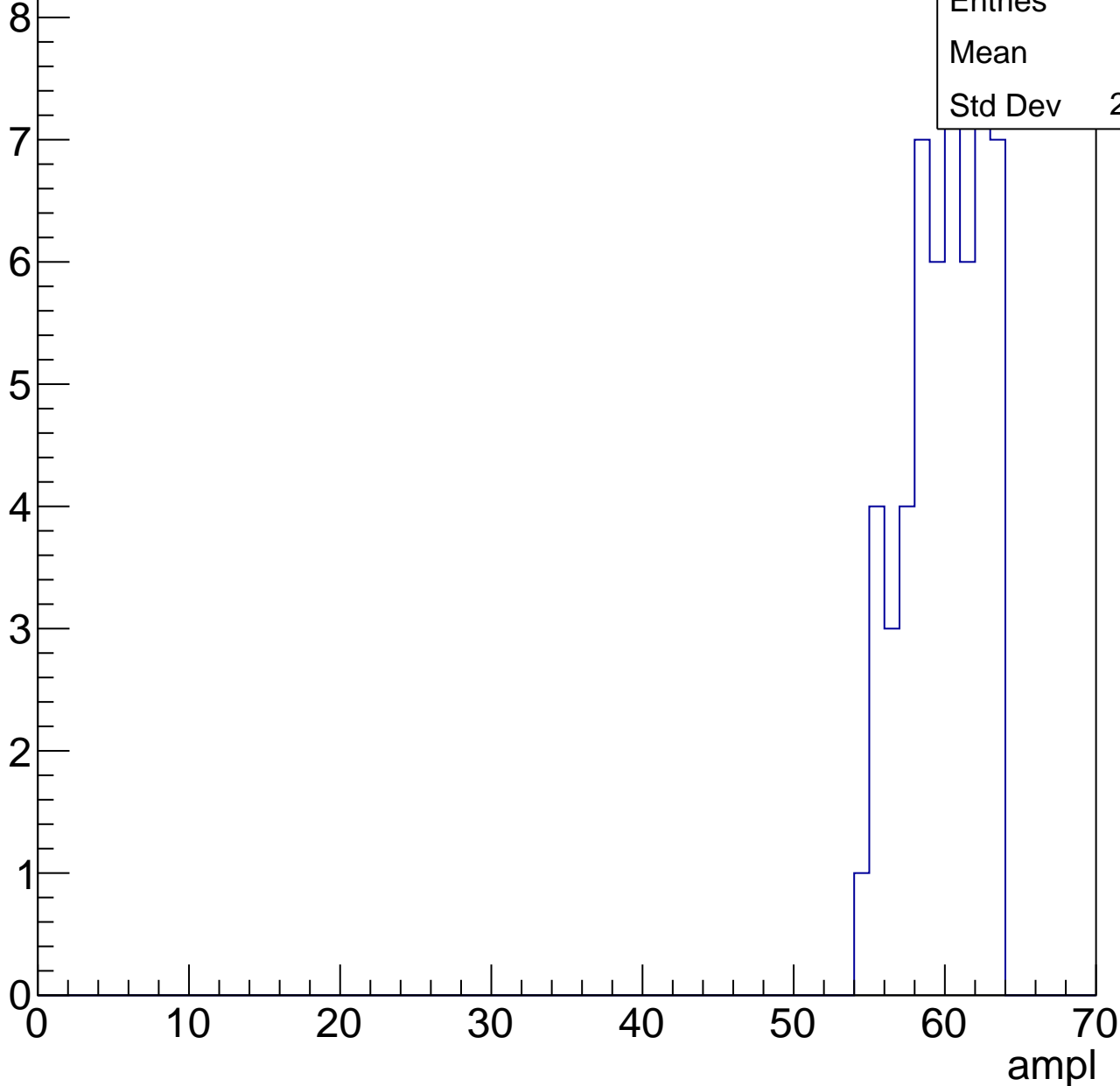


B1L103S, U5-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	59.5
Std Dev	2.515



B1L103S, U5-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

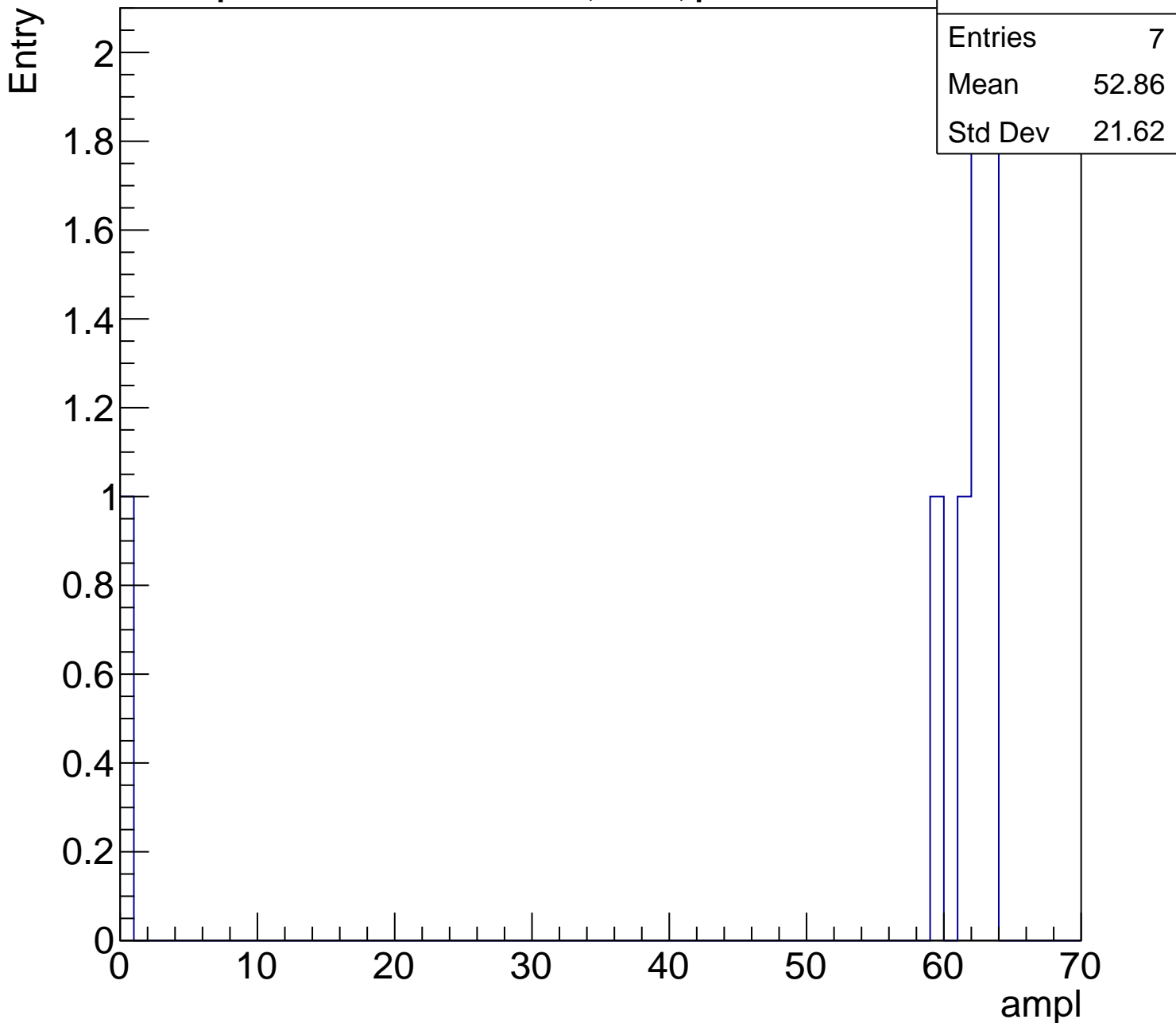
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.62

0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch54, adc0

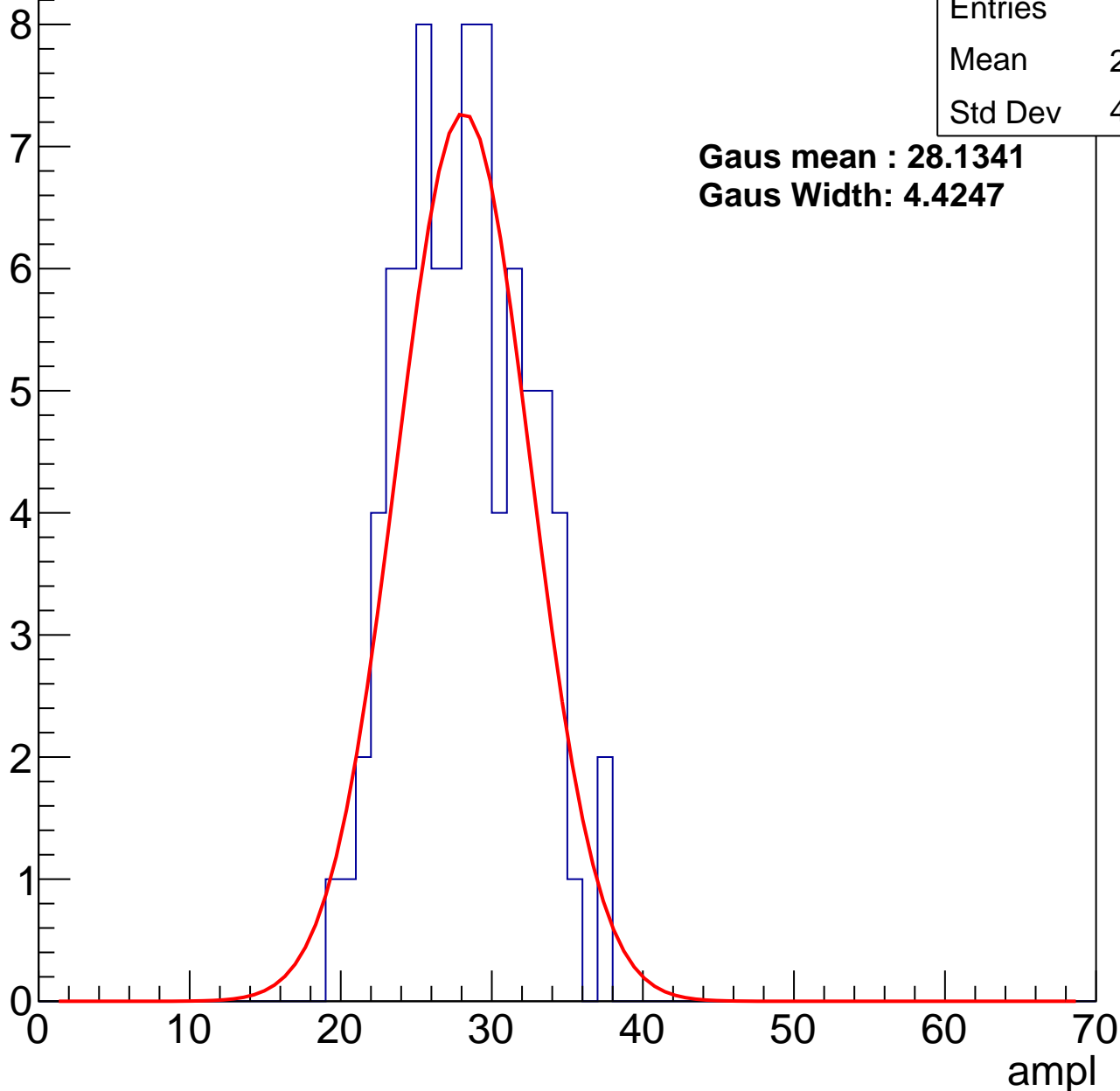
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	27.72
Std Dev	4.058

Gaus mean : 28.1341

Gaus Width: 4.4247



B1L103S, U5-ch54, adc1

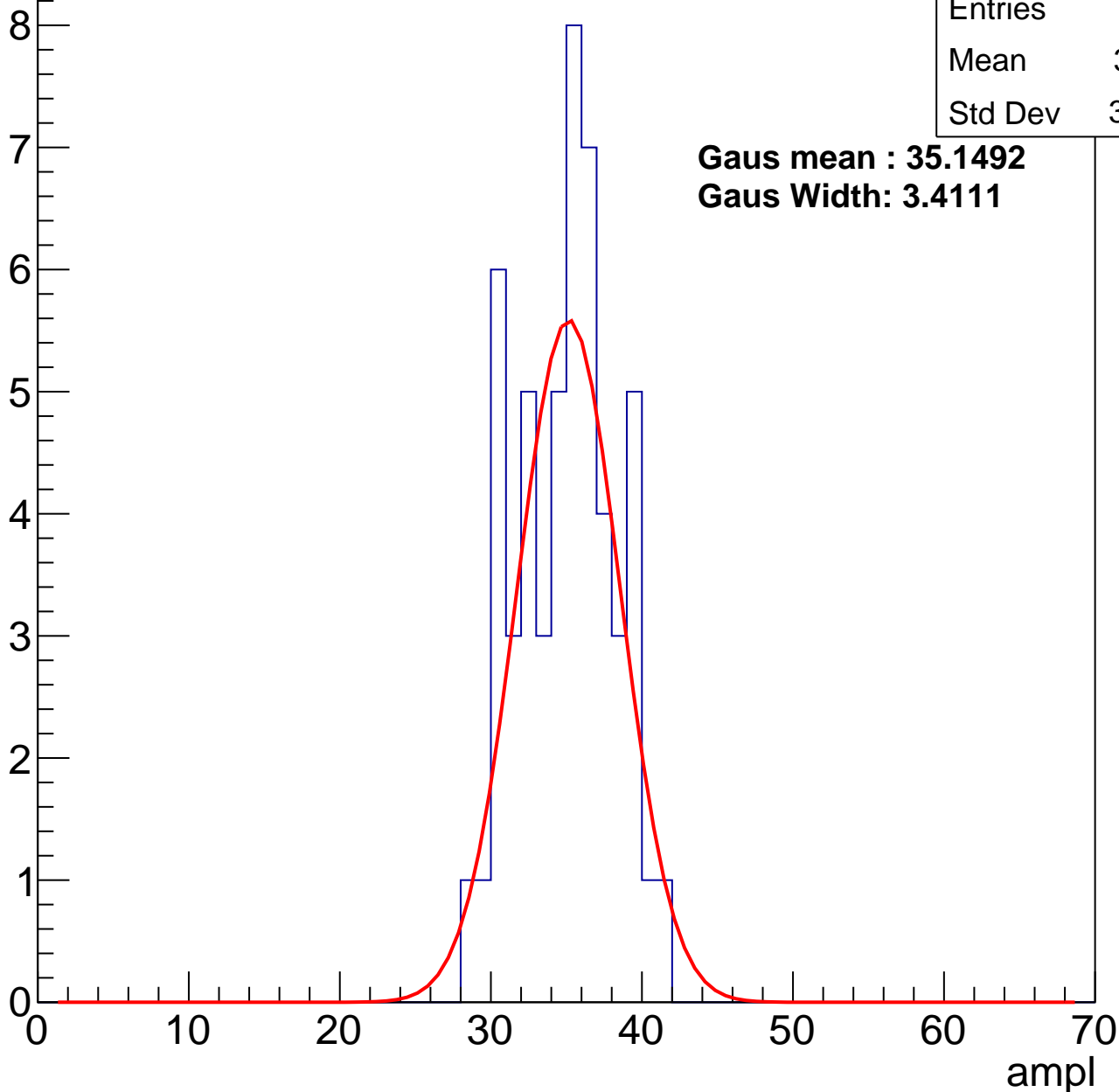
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	34.51
Std Dev	3.142

Gaus mean : 35.1492

Gaus Width: 3.4111



B1L103S, U5-ch54, adc2

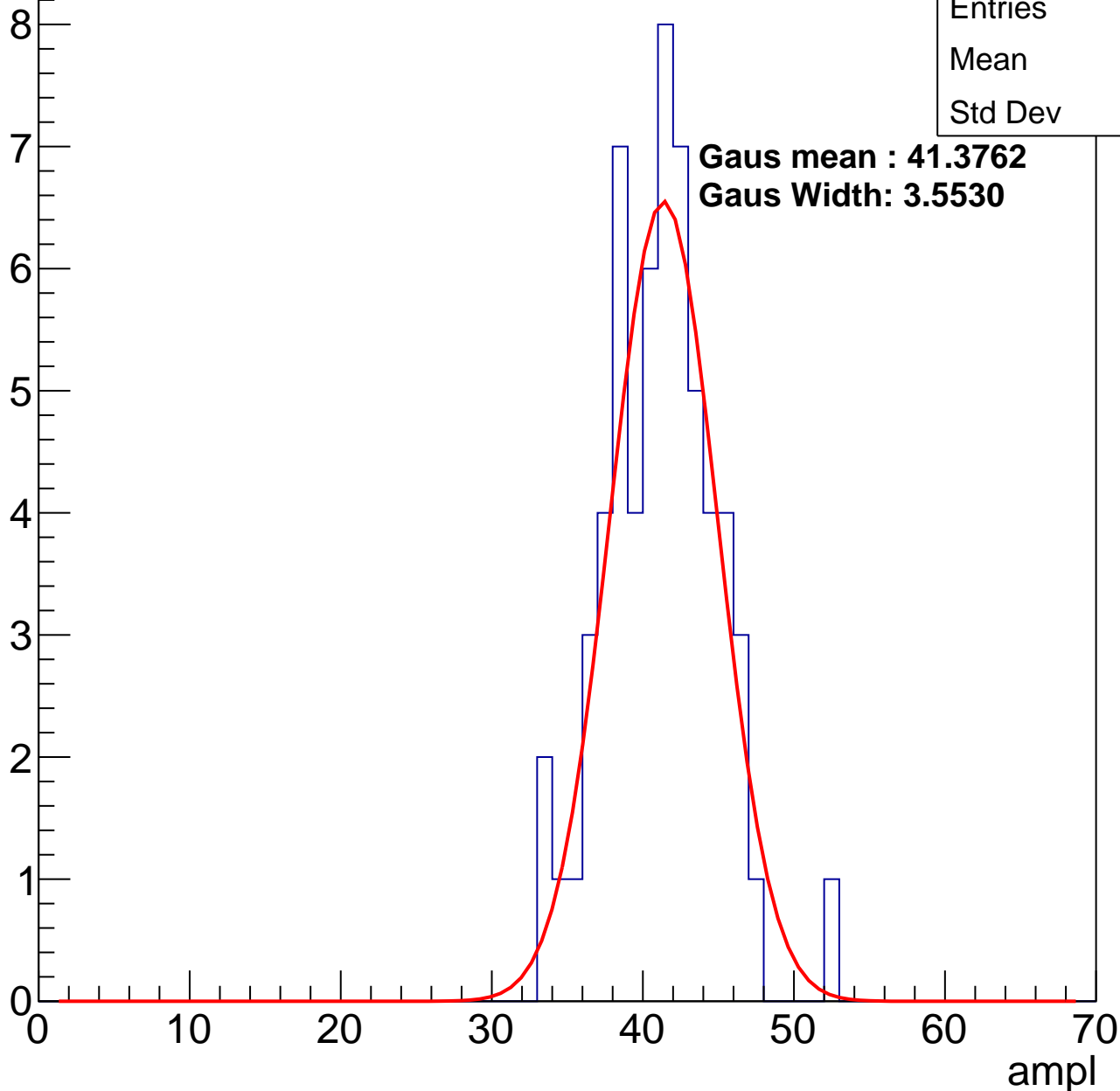
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	40.7
Std Dev	3.6

Gaus mean : 41.3762

Gaus Width: 3.5530

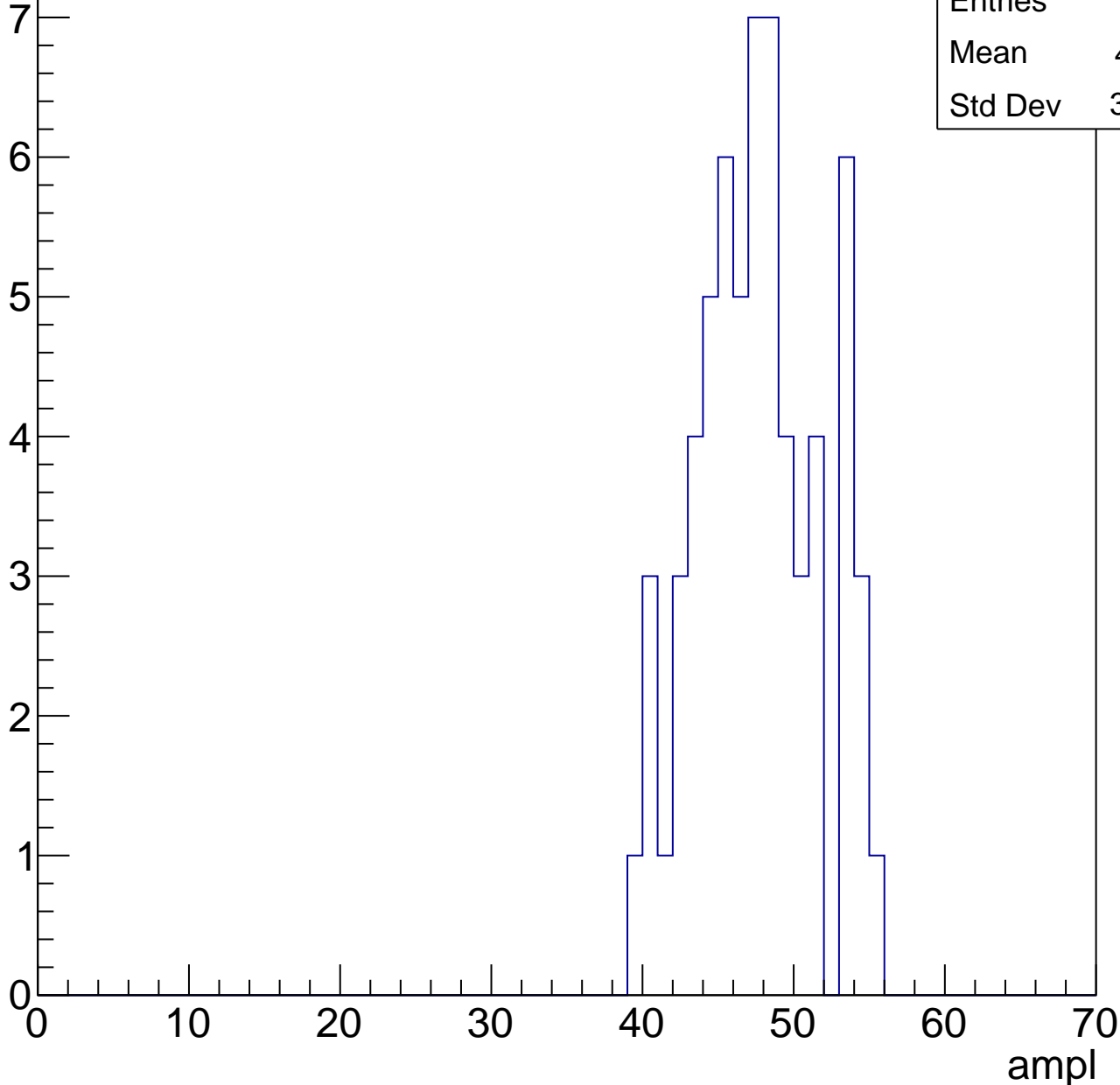


B1L103S, U5-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	47.11
Std Dev	3.985

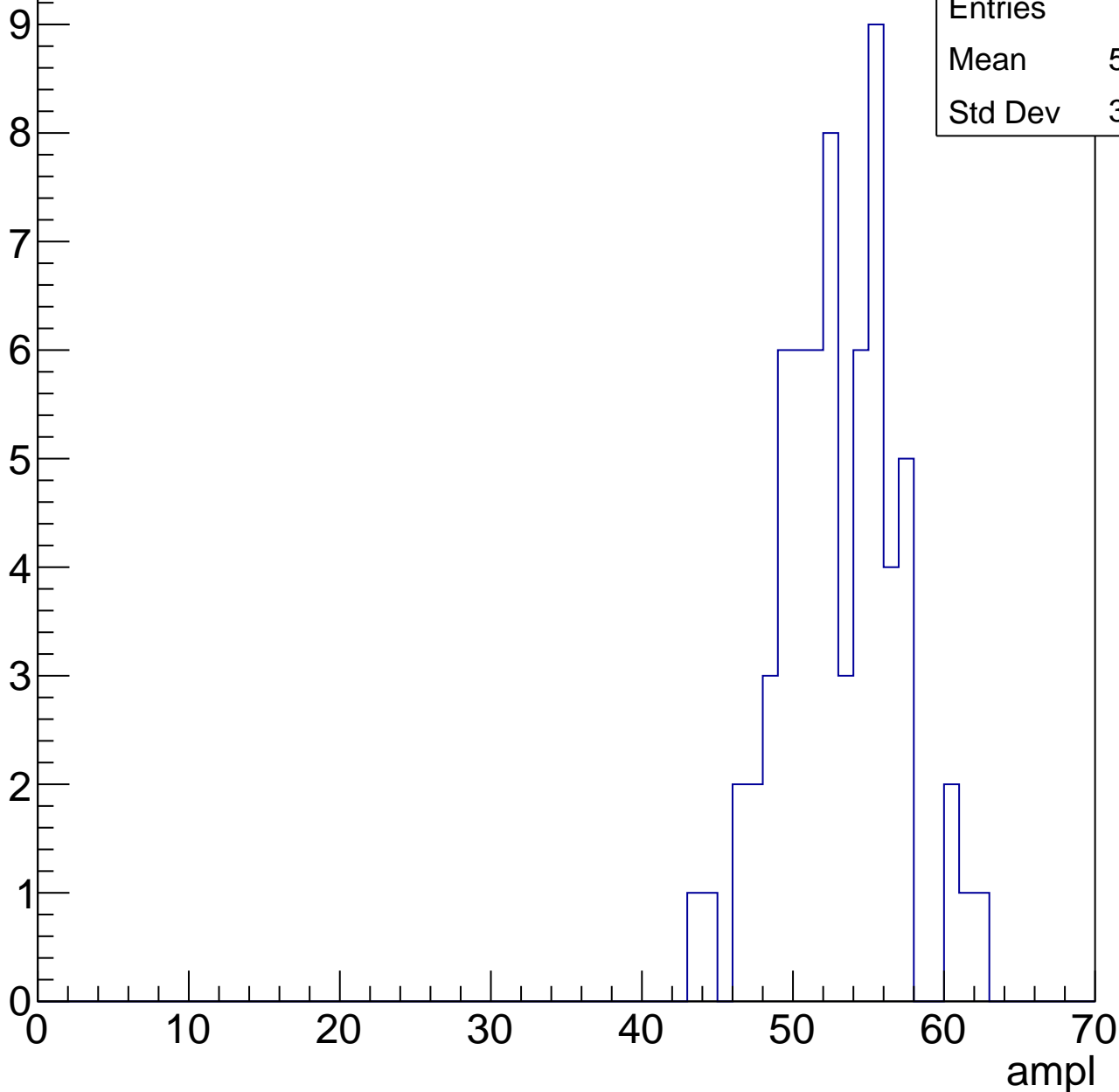


B1L103S, U5-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	52.47
Std Dev	3.874

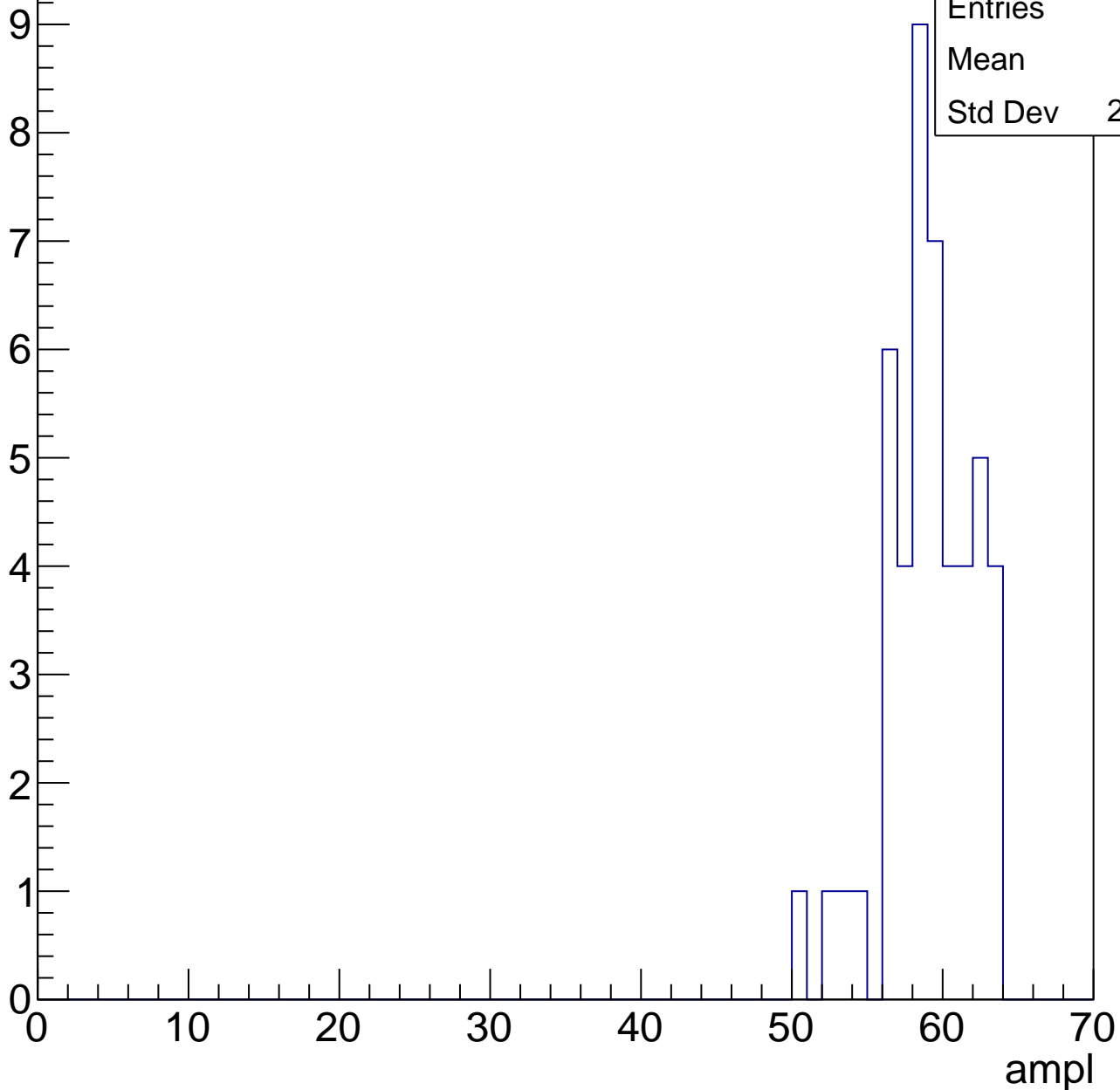


B1L103S, U5-ch54, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	58.6
Std Dev	2.885

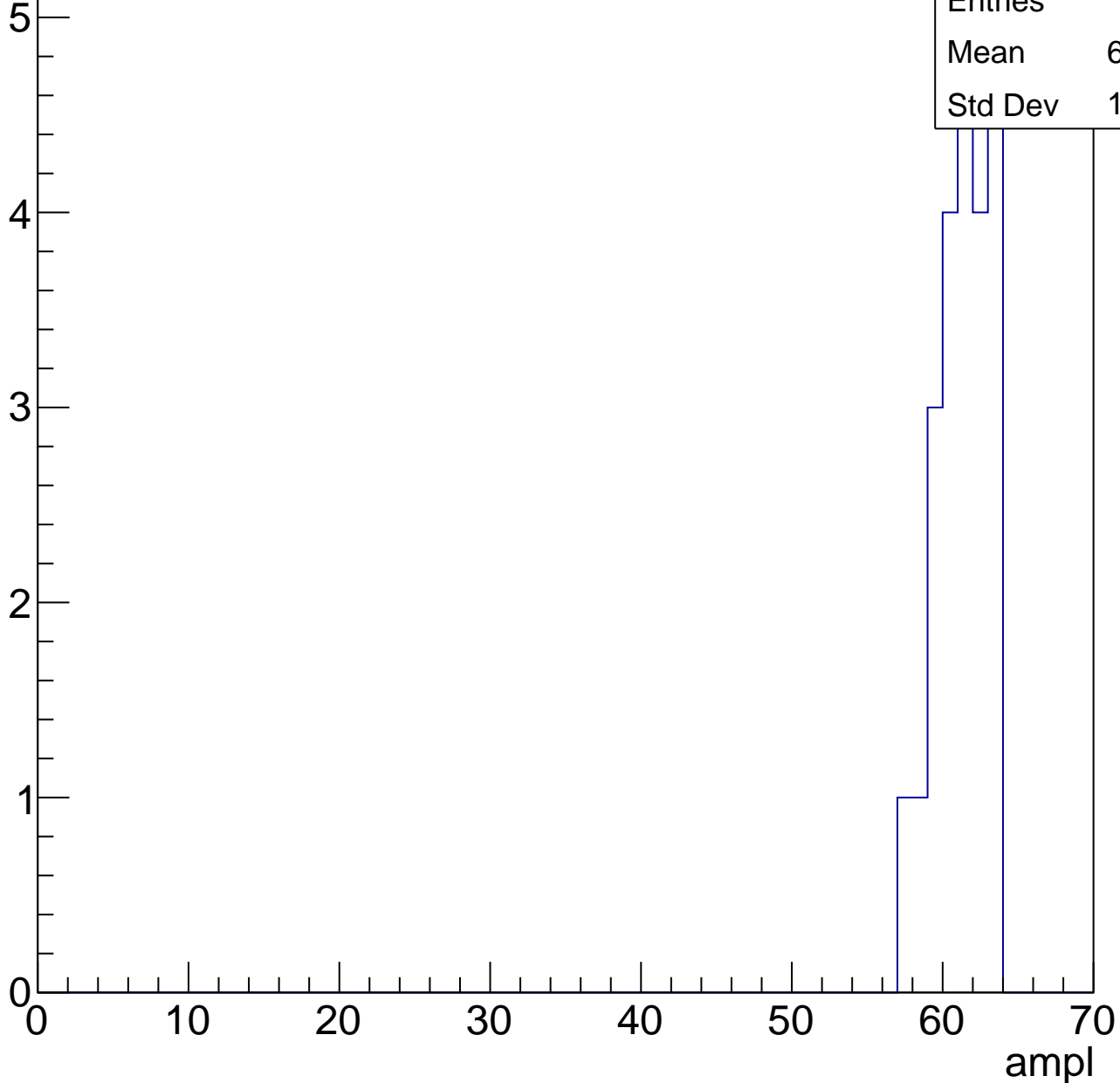


B1L103S, U5-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

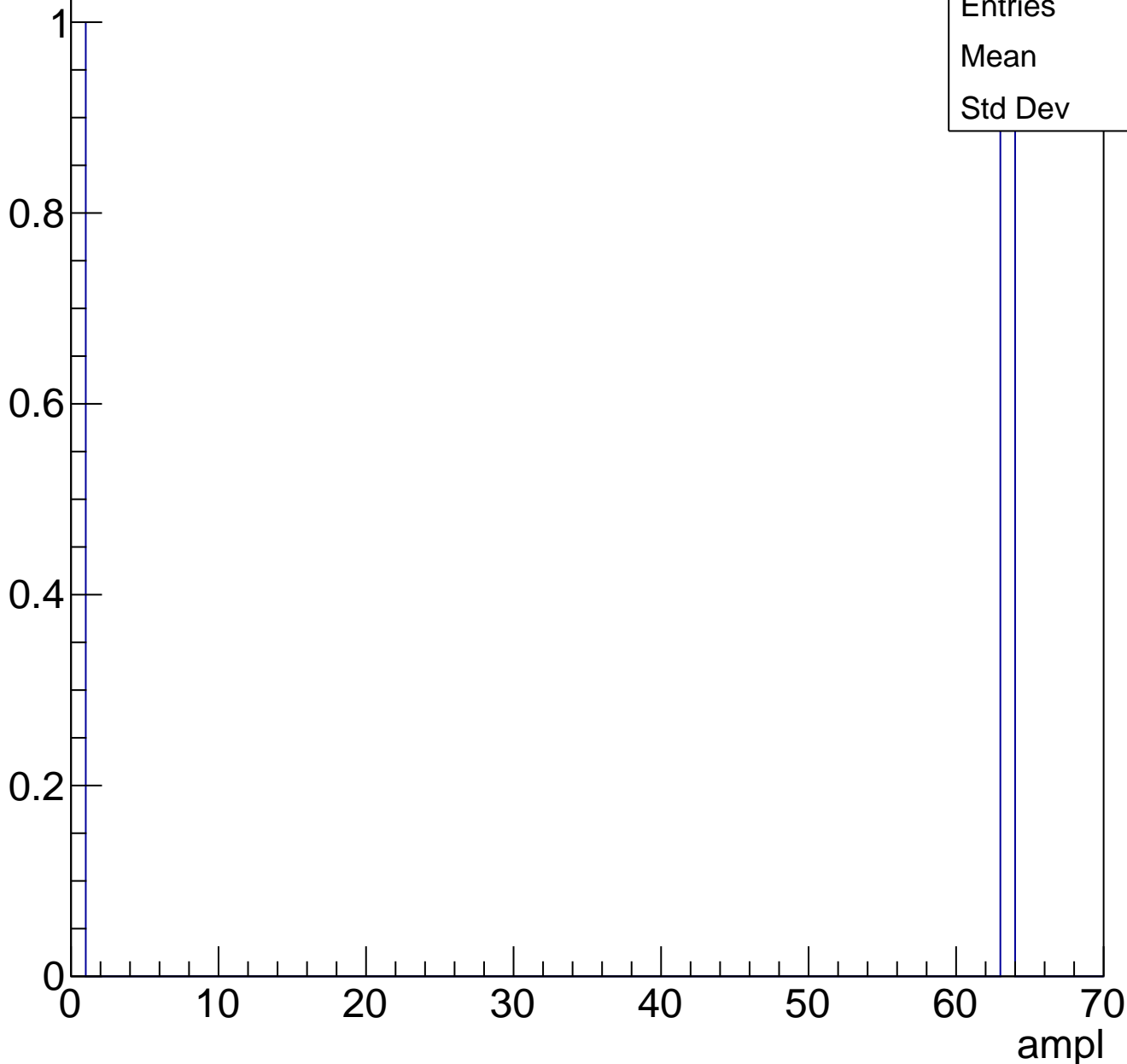
Entries	23
Mean	60.87
Std Dev	1.676



B1L103S, U5-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch55, adc0

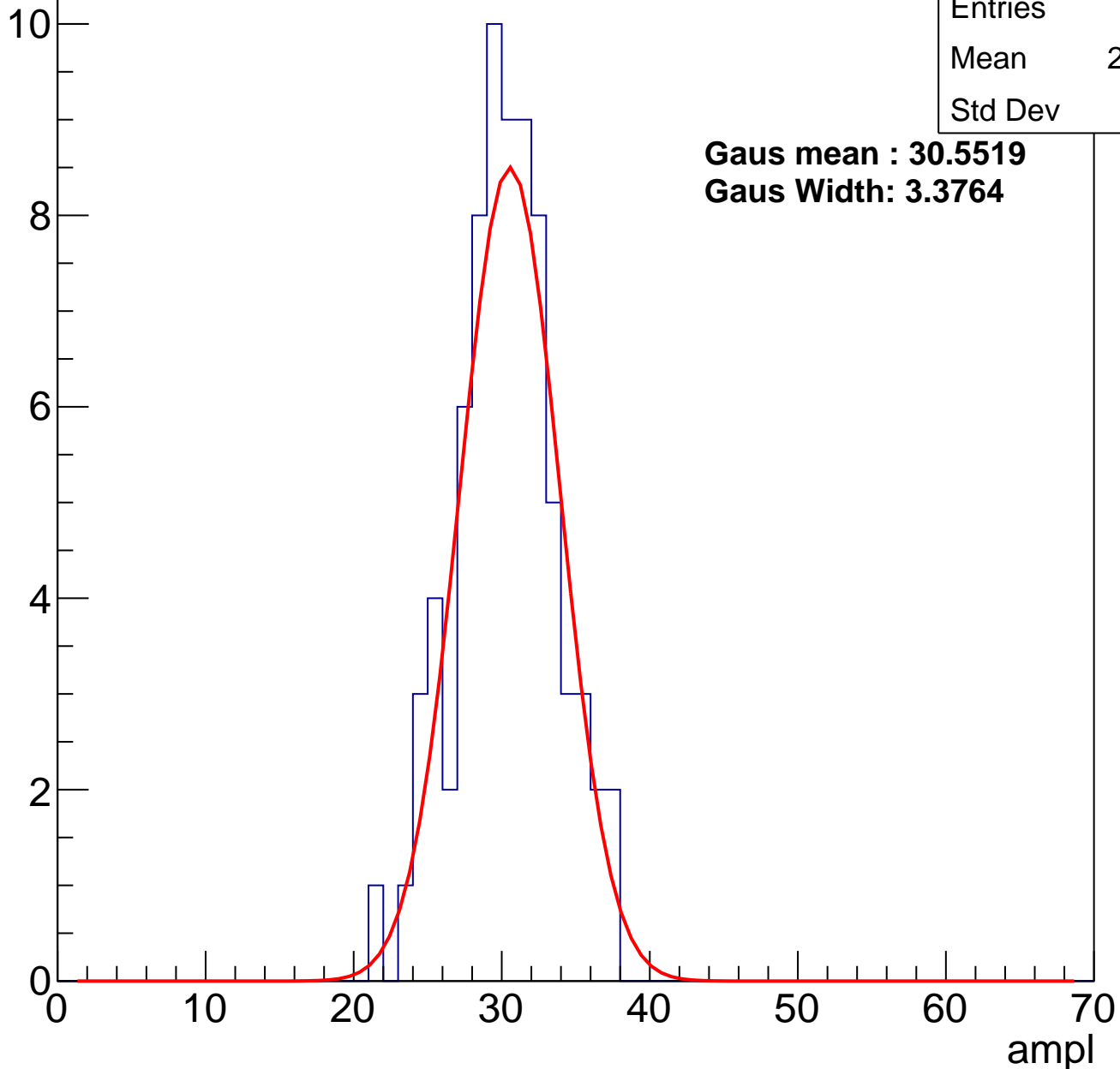
calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	29.83
Std Dev	3.33

Gaus mean : 30.5519

Gaus Width: 3.3764

Entry



B1L103S, U5-ch55, adc1

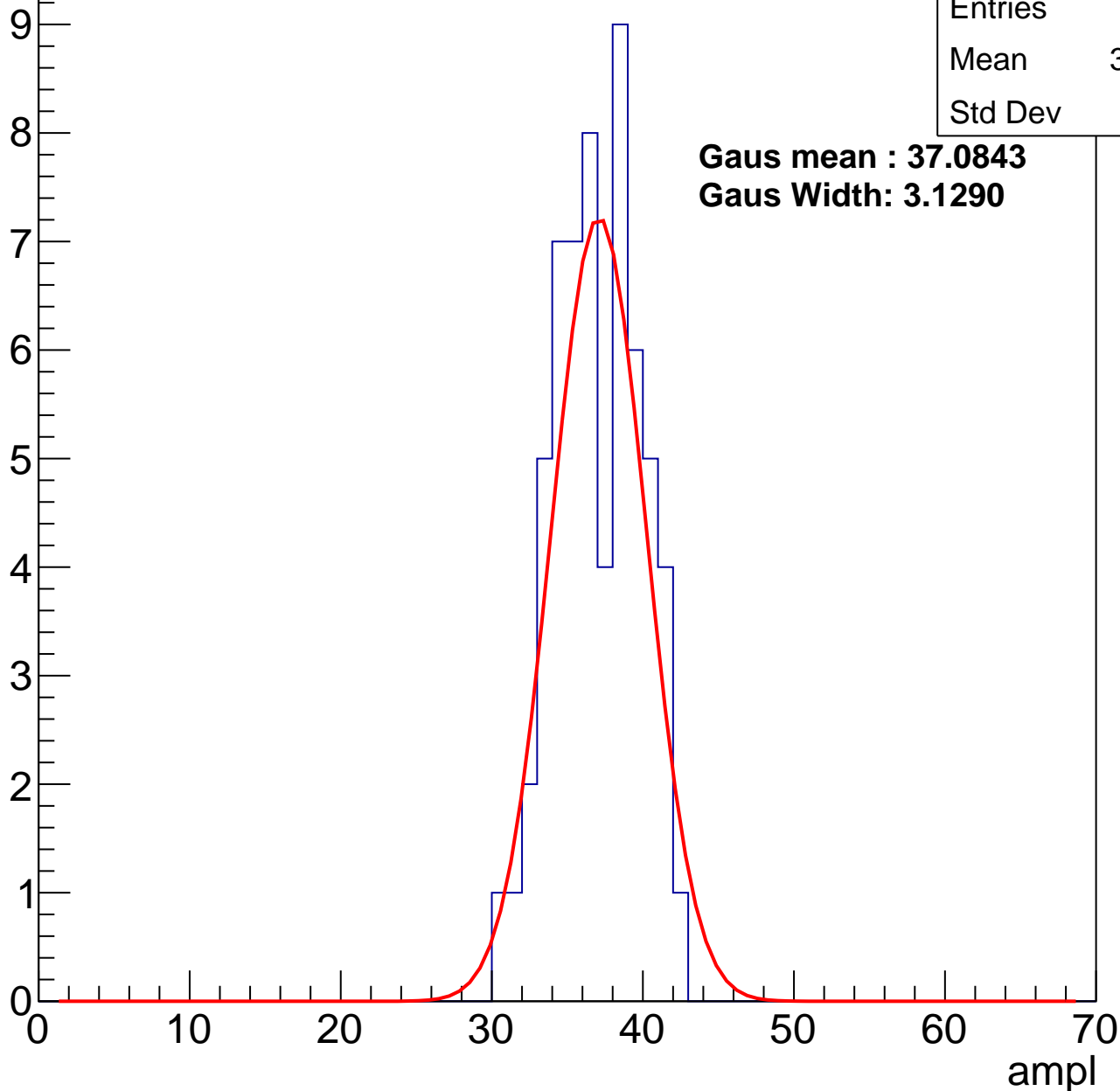
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.52
Std Dev	2.79

Gaus mean : 37.0843

Gaus Width: 3.1290



B1L103S, U5-ch55, adc2

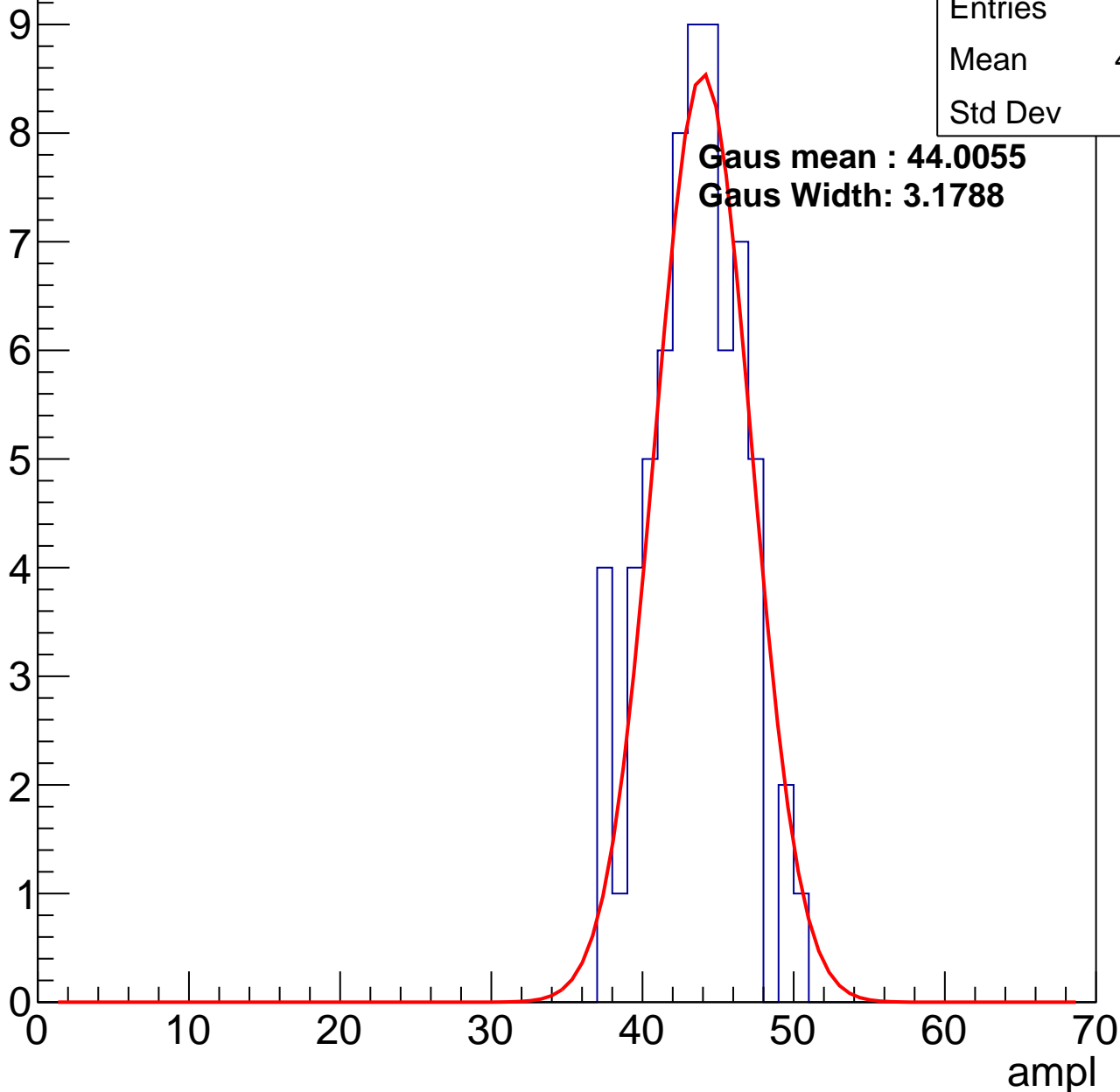
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.01
Std Dev	3.01

Gaus mean : 44.0055

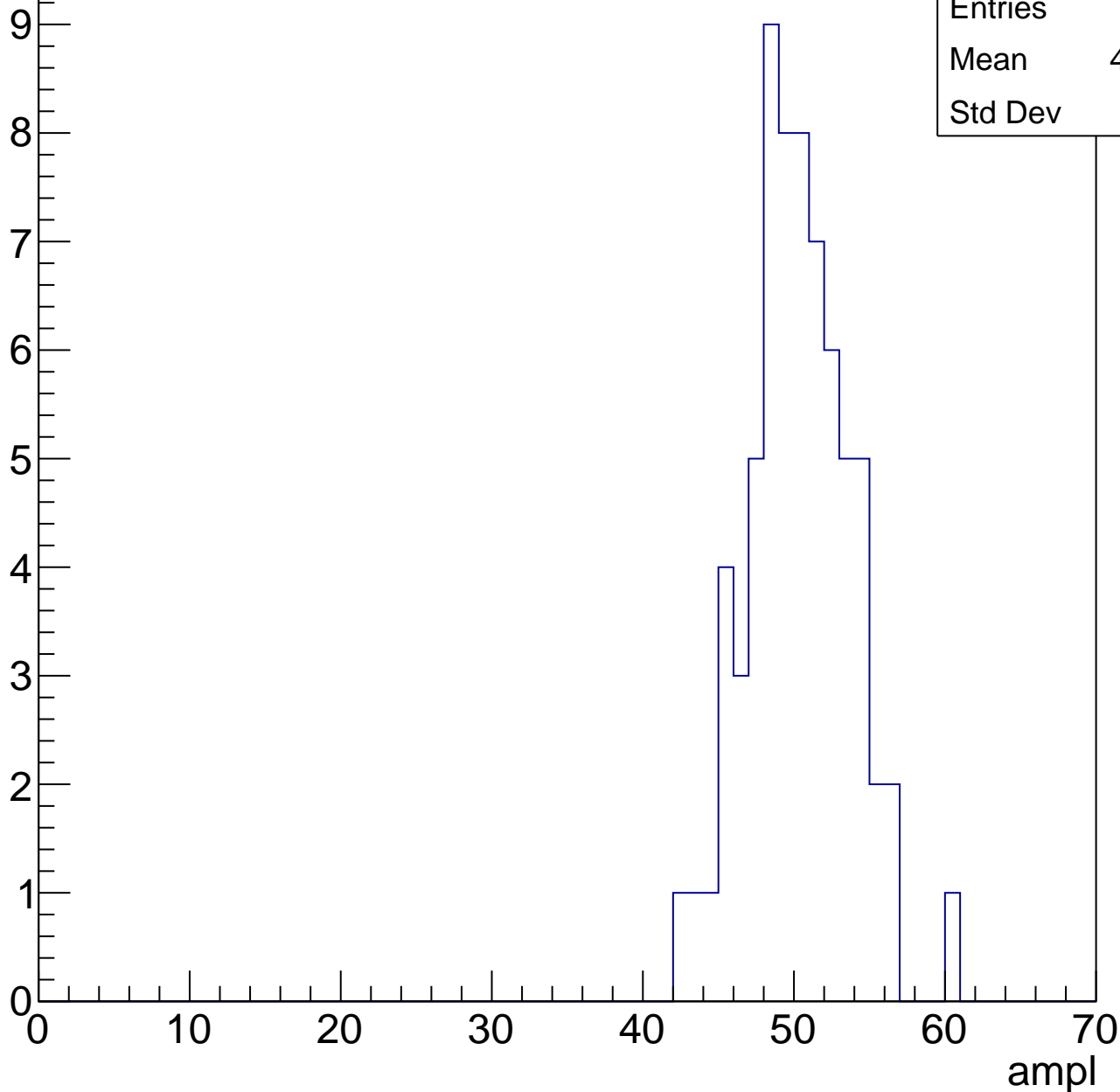
Gaus Width: 3.1788



B1L103S, U5-ch55, adc3

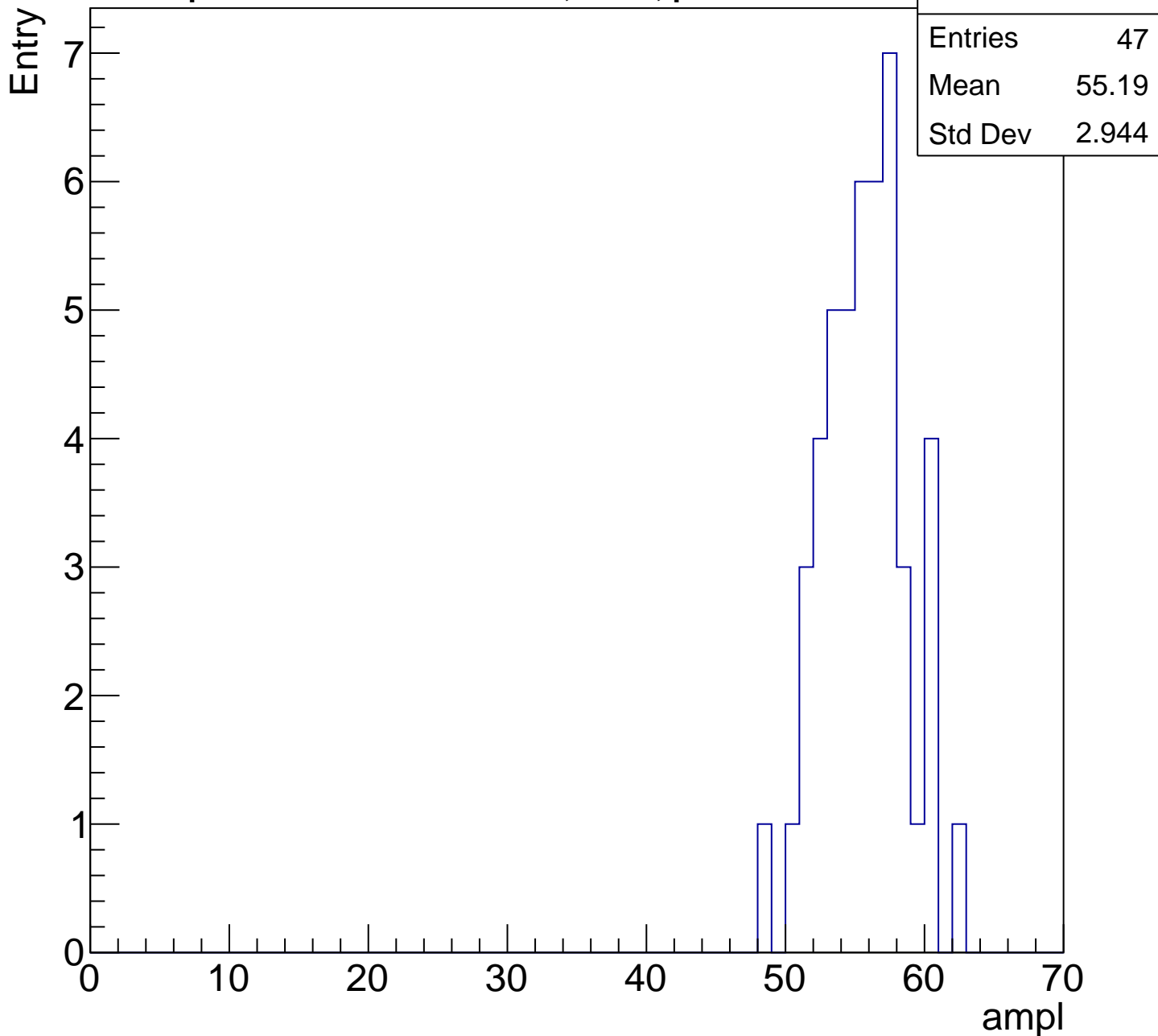
calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch55, adc4

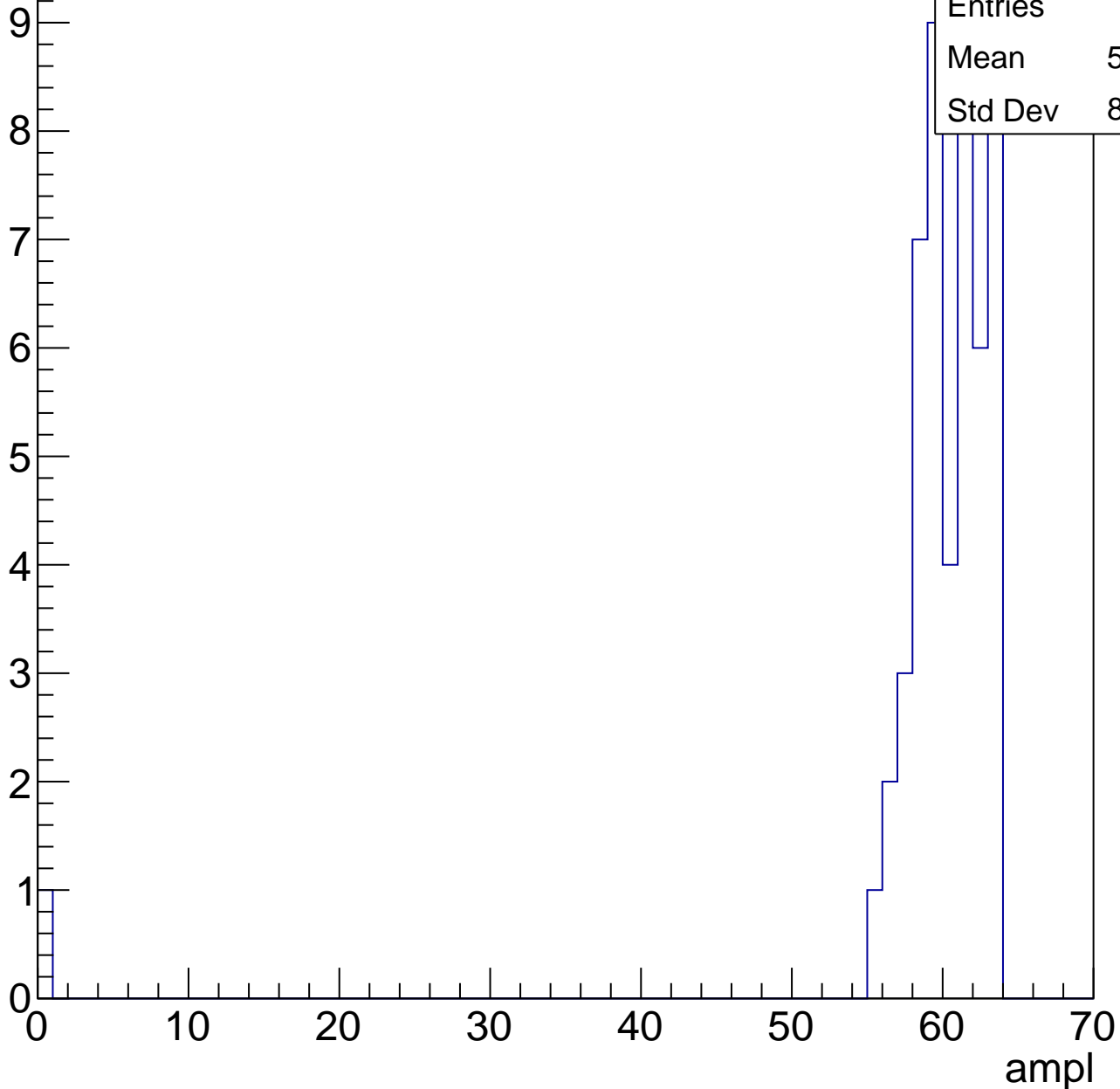
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

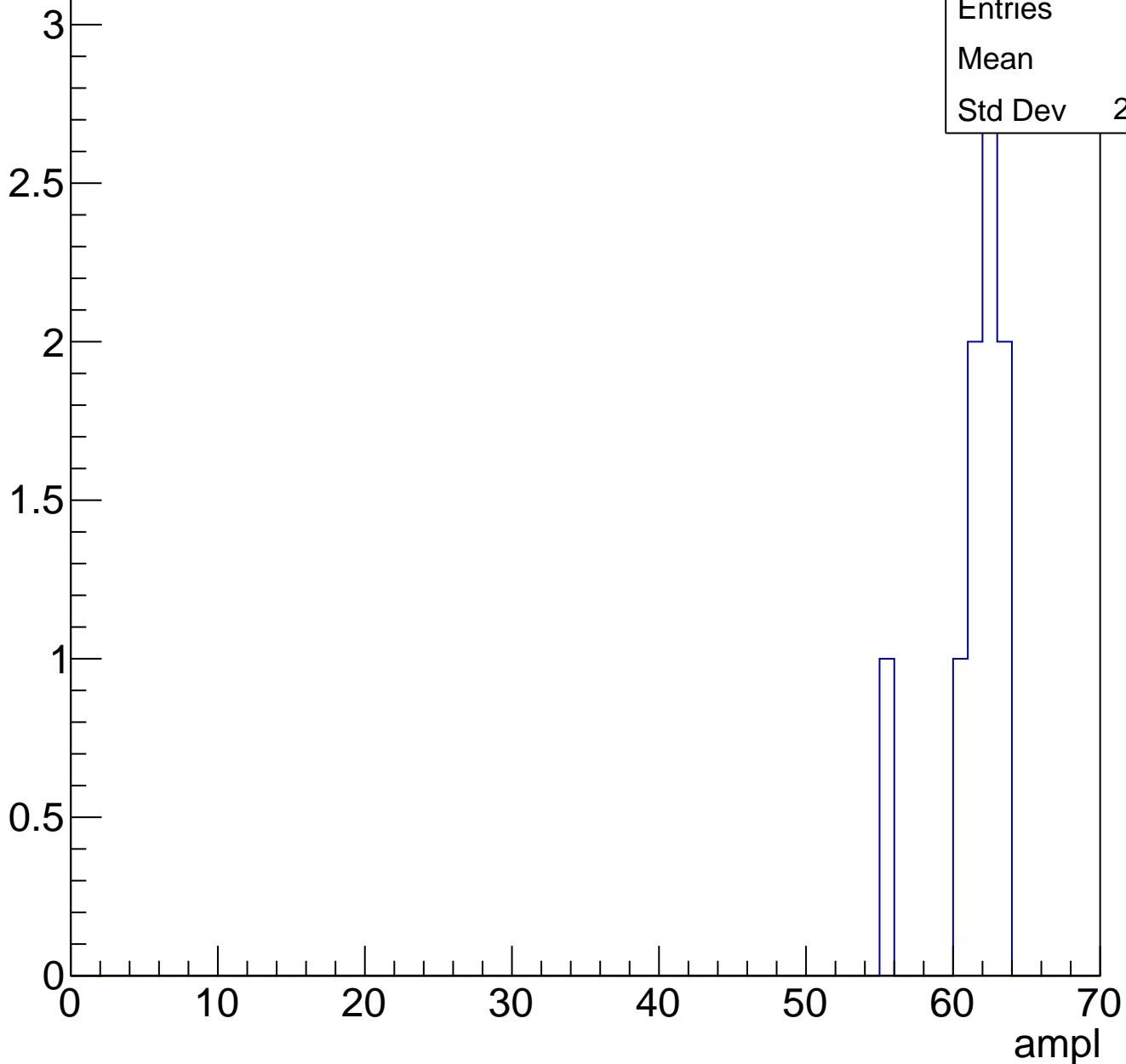
Entry



B1L103S, U5-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch56, adc0

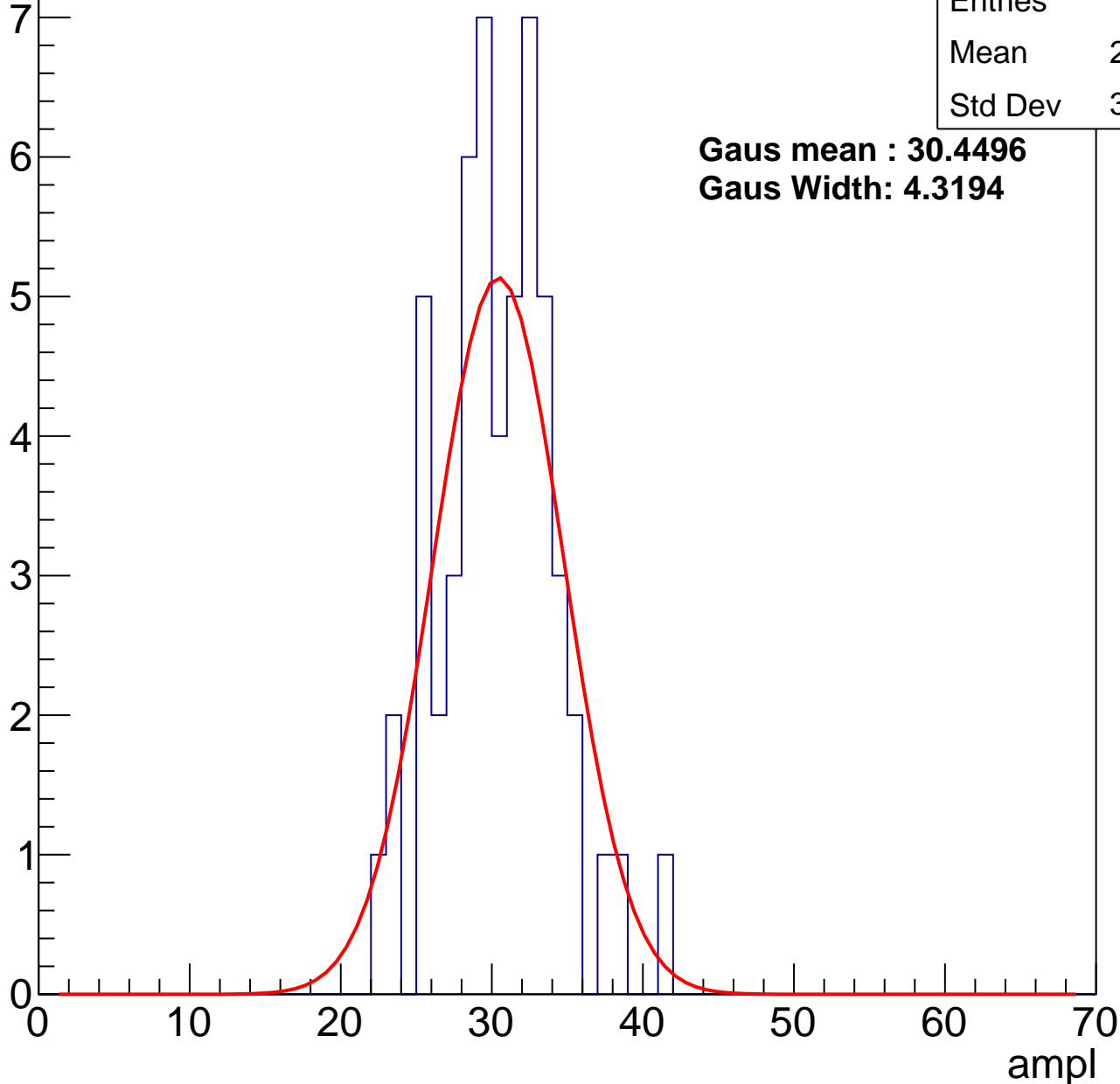
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	29.98
Std Dev	3.783

Gaus mean : 30.4496

Gaus Width: 4.3194



B1L103S, U5-ch56, adc1

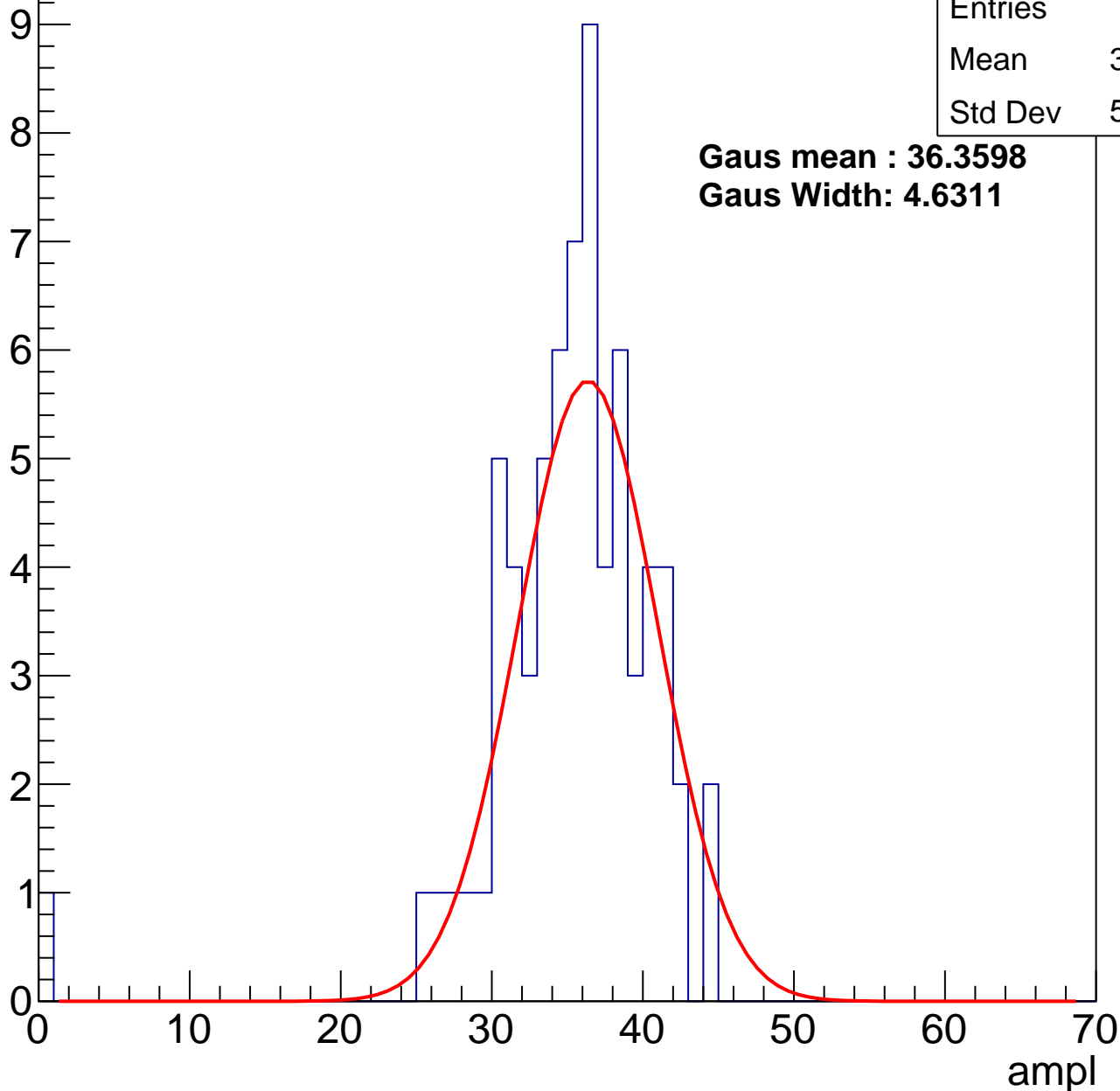
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	34.74
Std Dev	5.884

Gaus mean : 36.3598

Gaus Width: 4.6311



B1L103S, U5-ch56, adc2

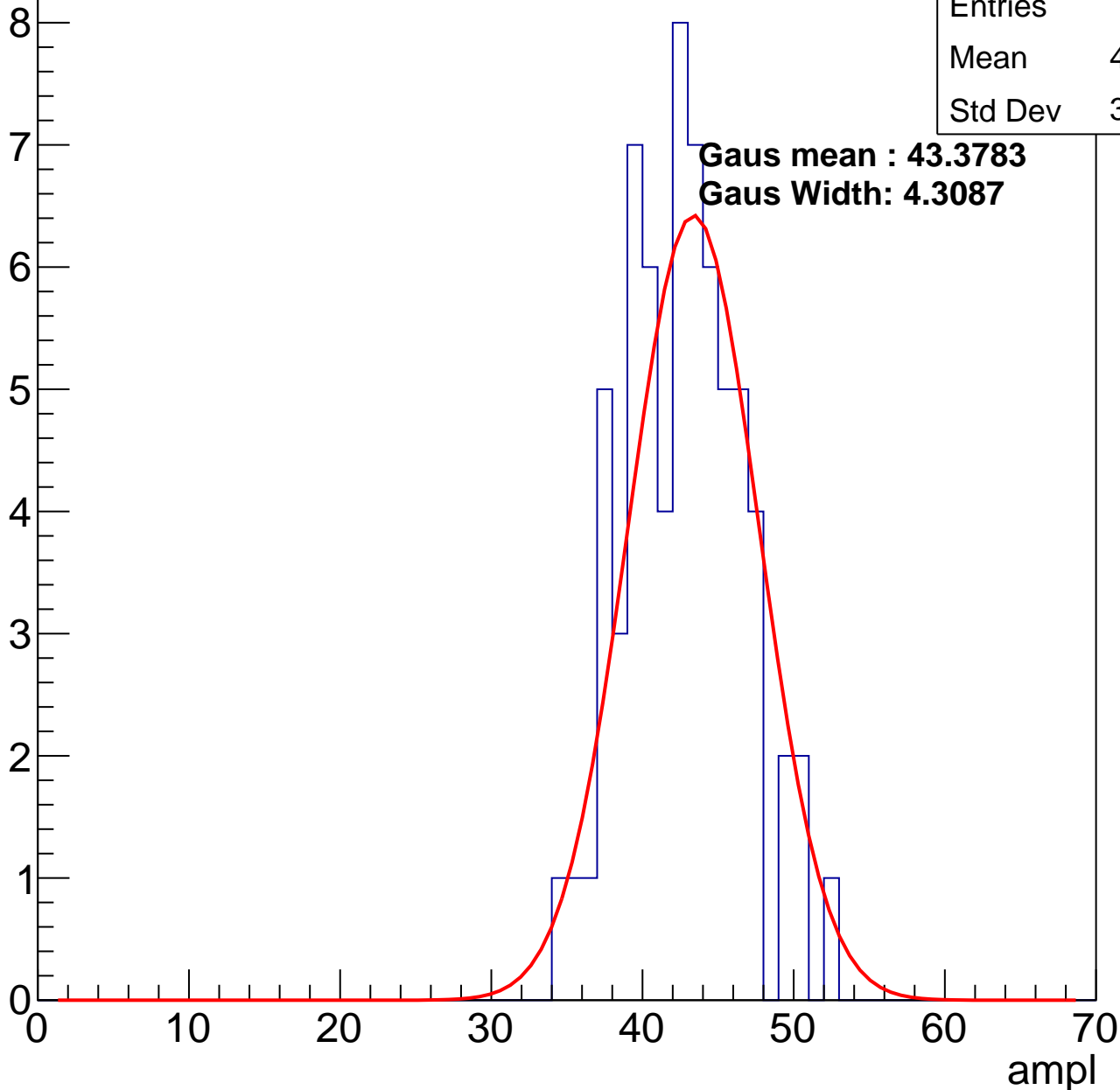
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.28
Std Dev	3.815

Gaus mean : 43.3783

Gaus Width: 4.3087

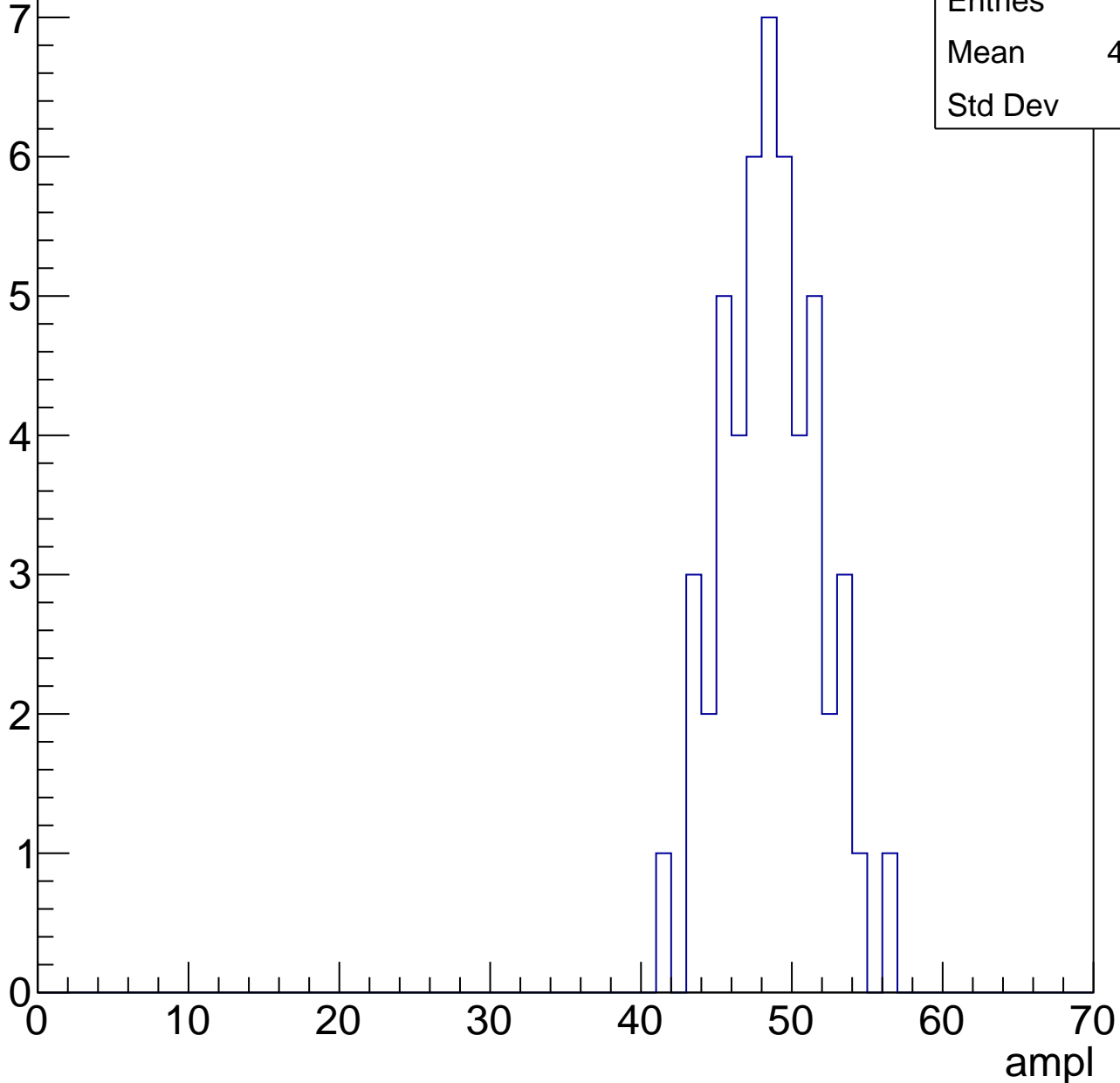


B1L103S, U5-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	48.14
Std Dev	3.15

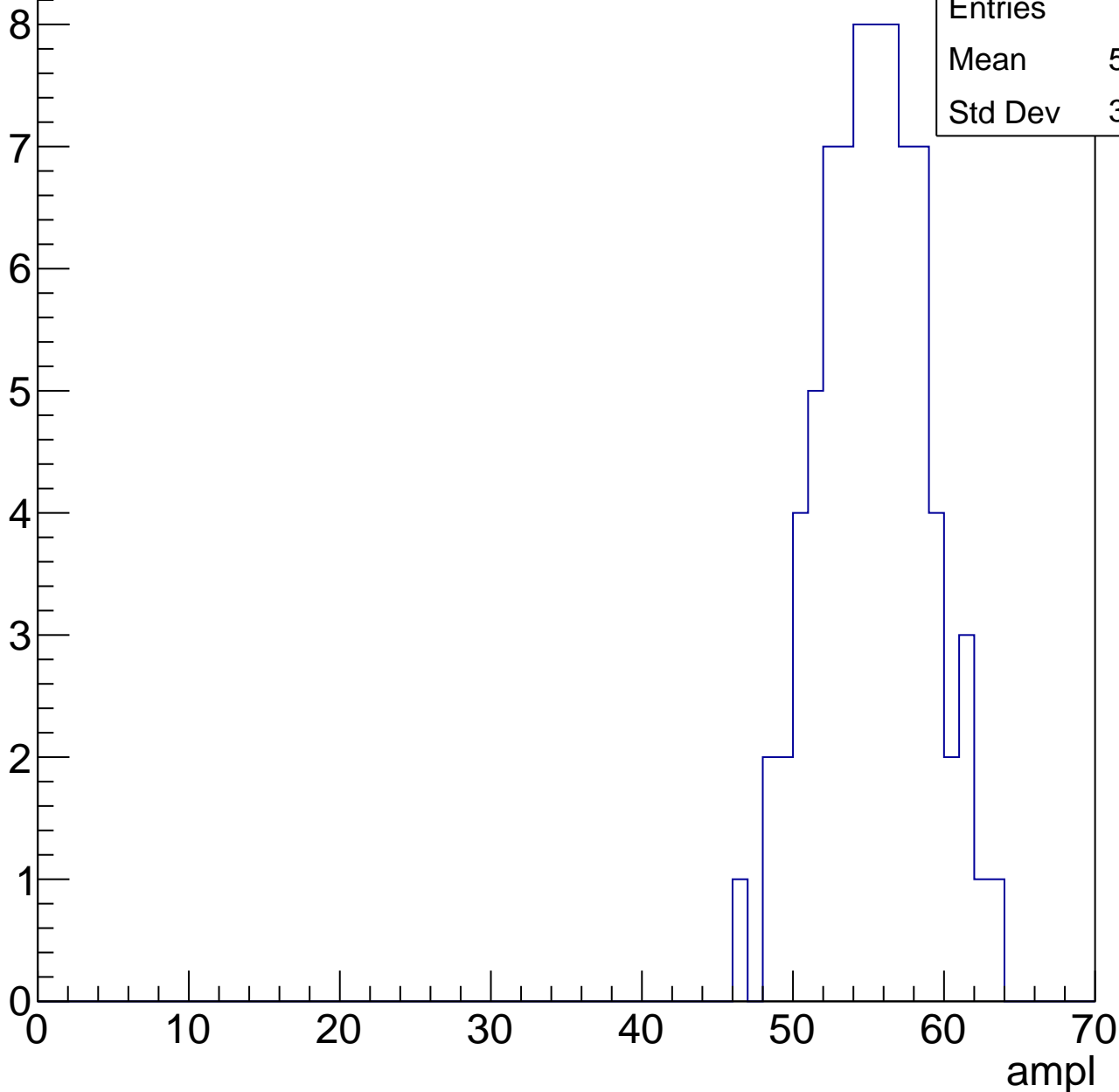


B1L103S, U5-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	54.79
Std Dev	3.532

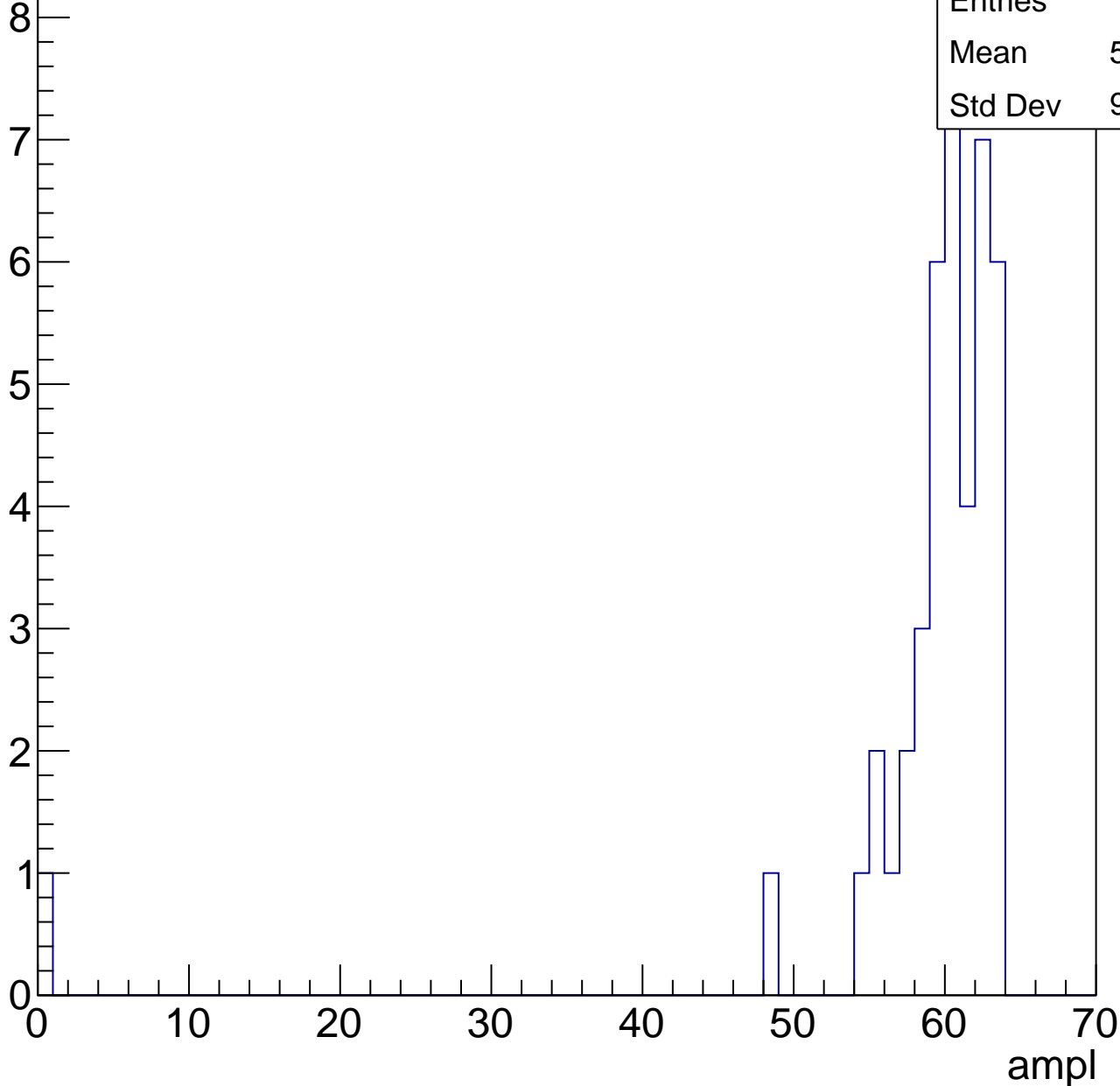


B1L103S, U5-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

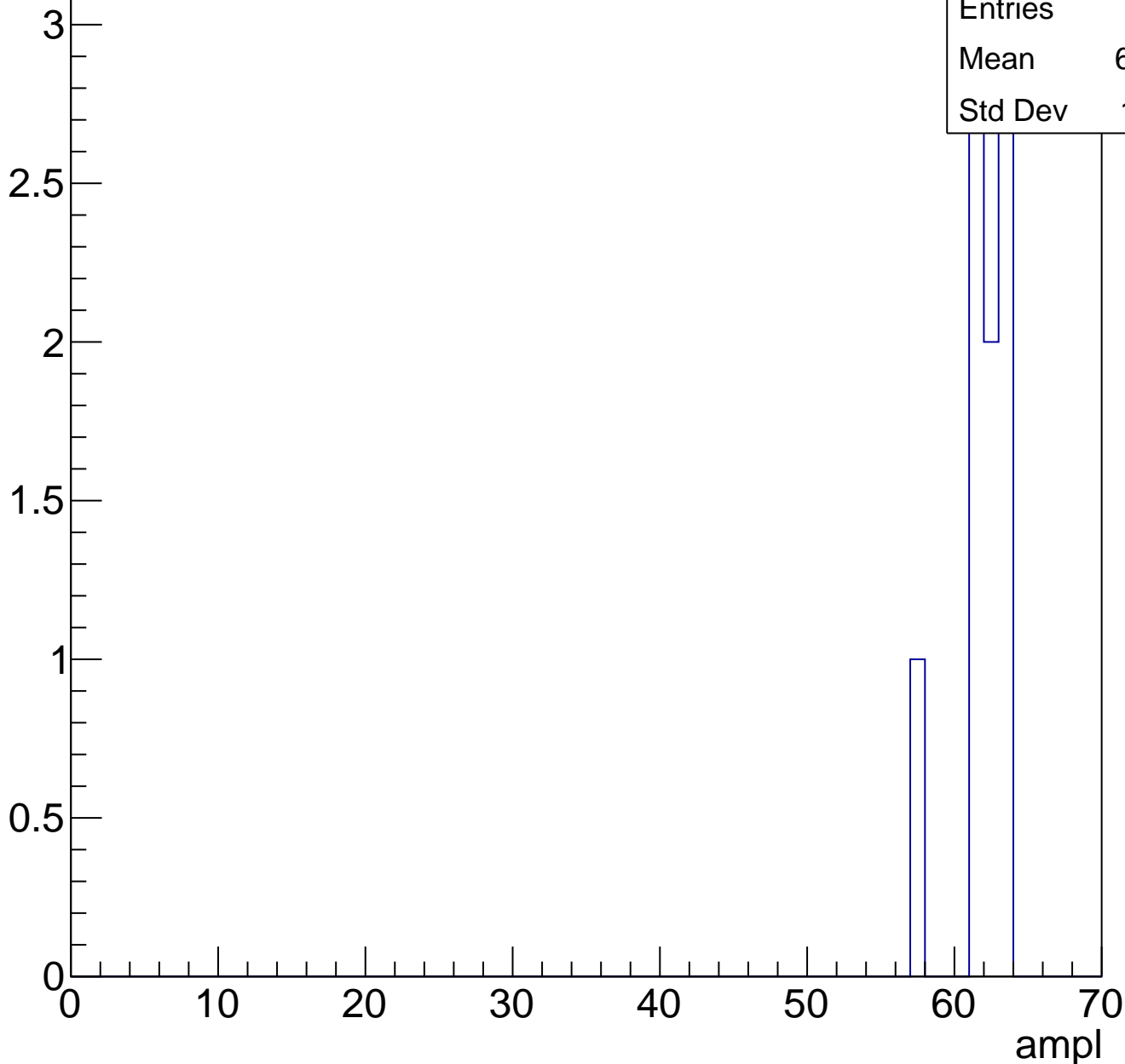
Entries	42
Mean	58.24
Std Dev	9.559



B1L103S, U5-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch57, adc0

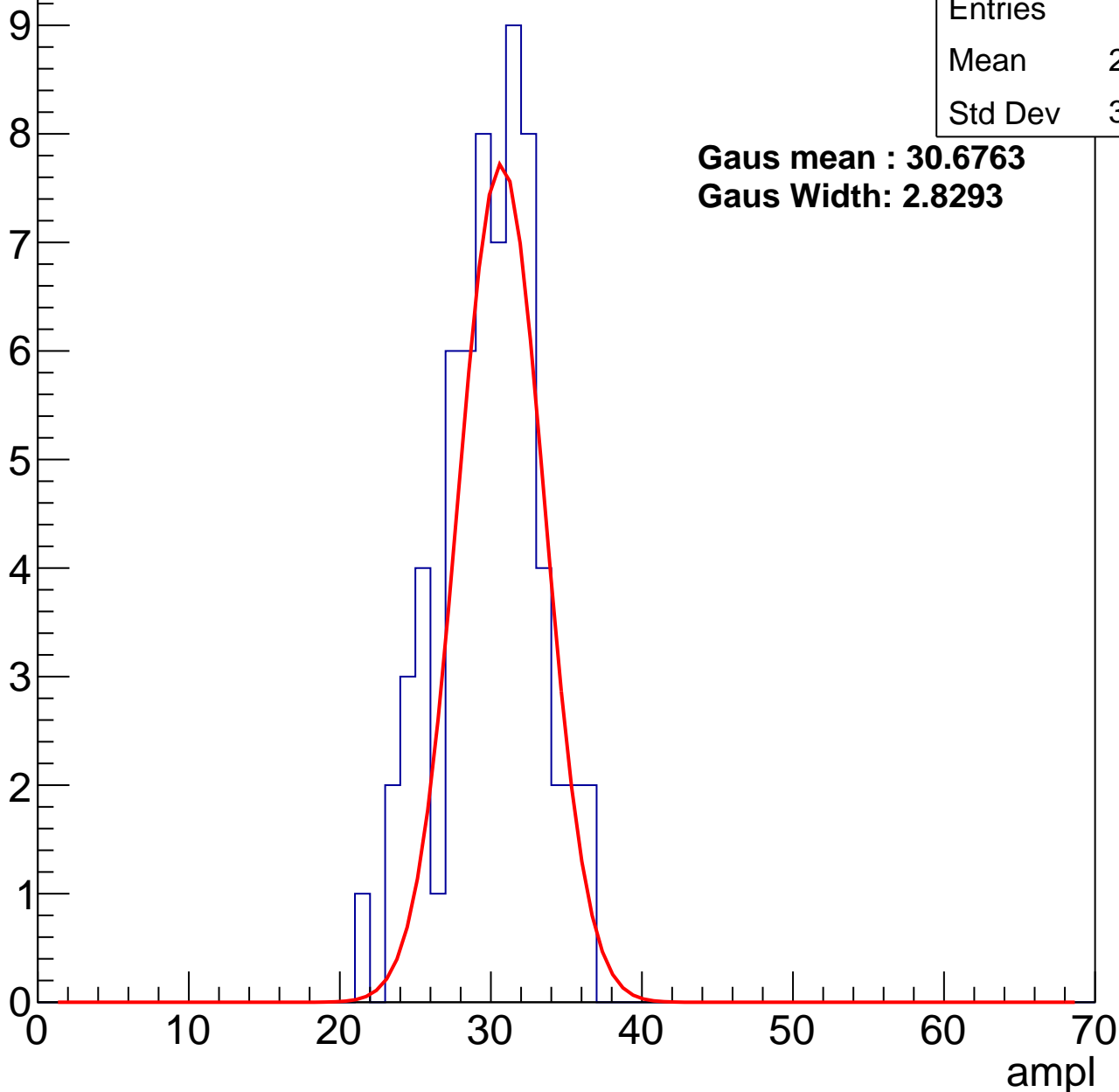
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	29.45
Std Dev	3.282

Gaus mean : 30.6763

Gaus Width: 2.8293



B1L103S, U5-ch57, adc1

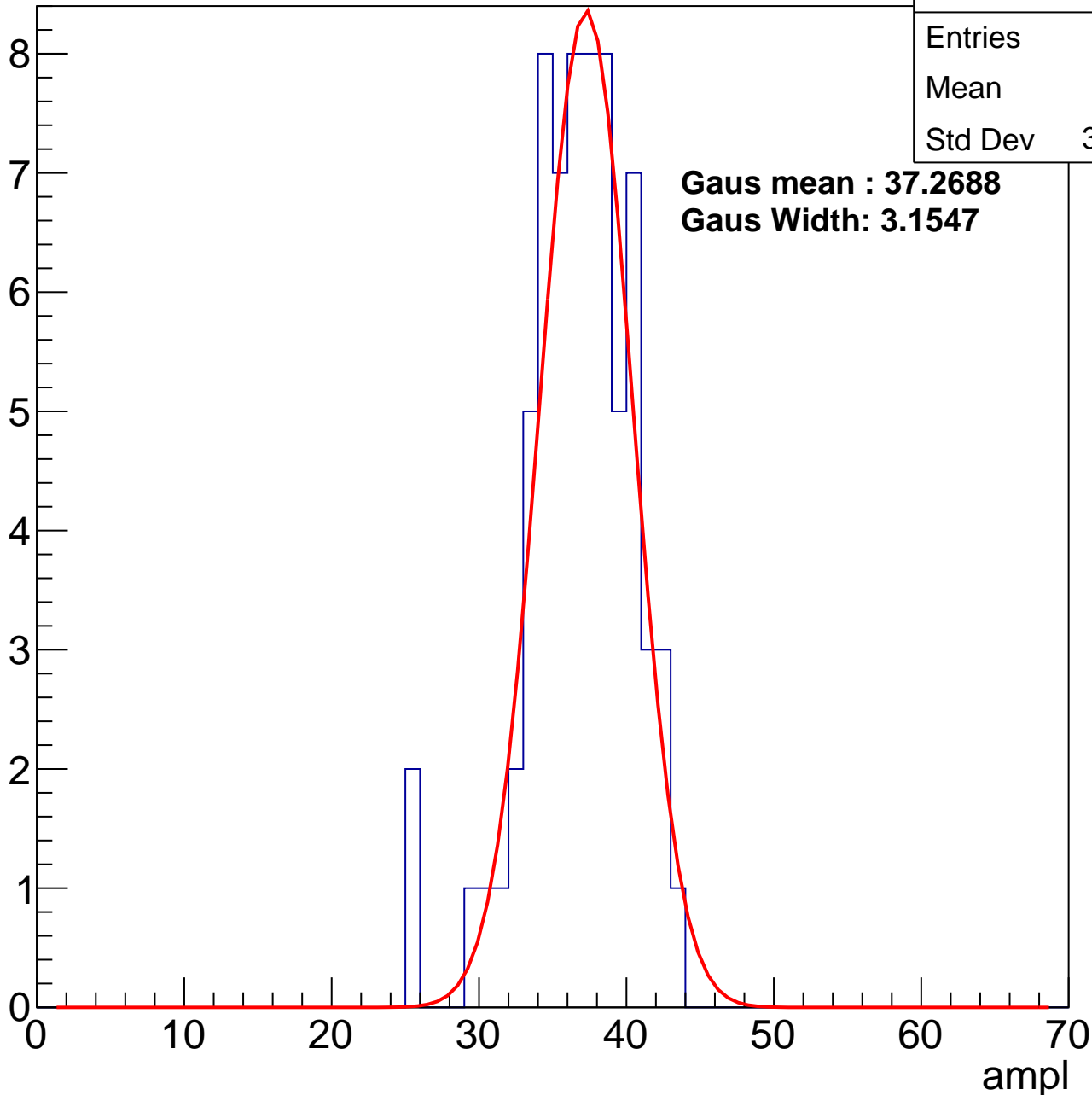
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.3
Std Dev	3.567

Gaus mean : 37.2688

Gaus Width: 3.1547



B1L103S, U5-ch57, adc2

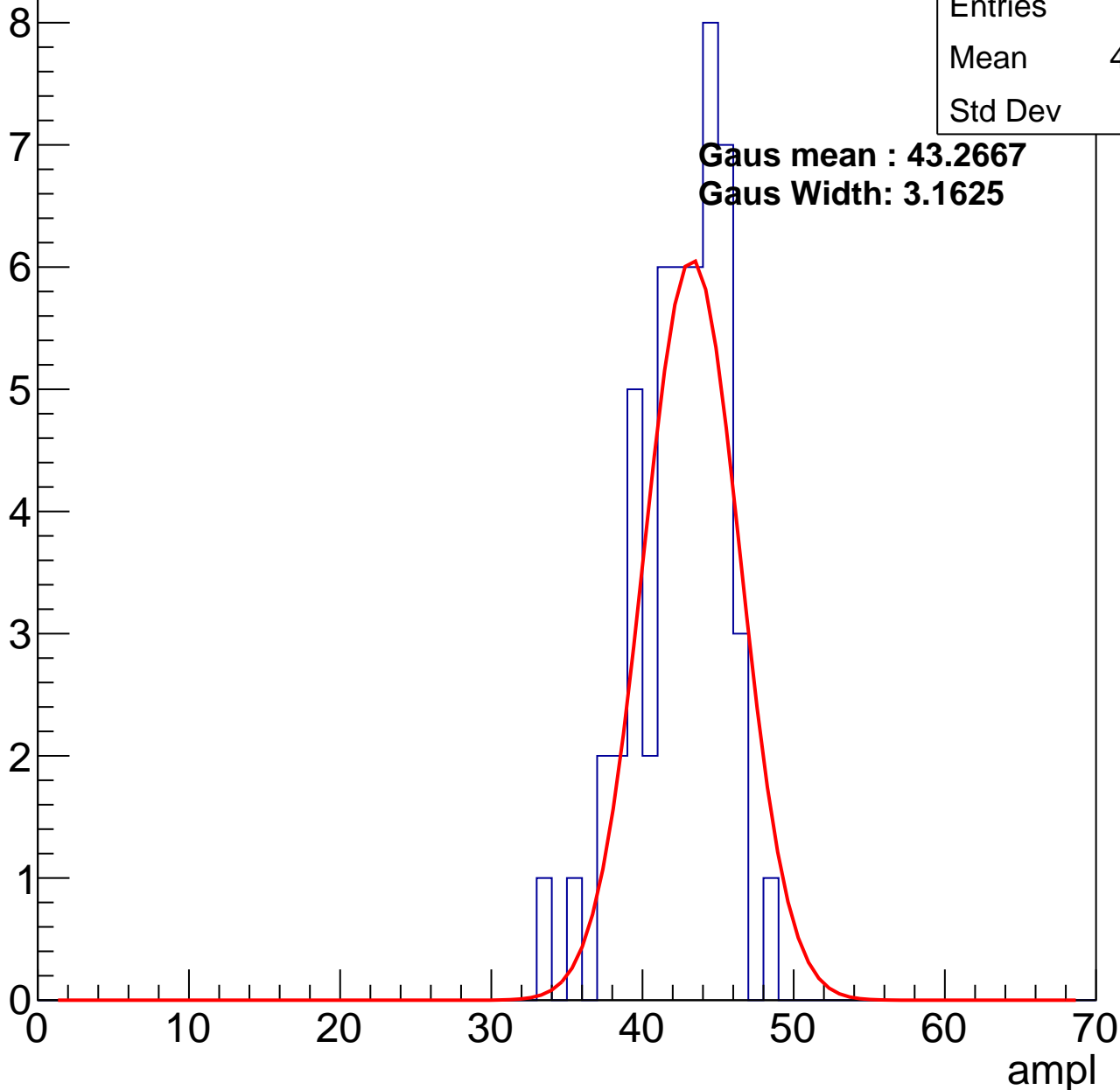
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.04
Std Dev	3.02

Gaus mean : 43.2667

Gaus Width: 3.1625

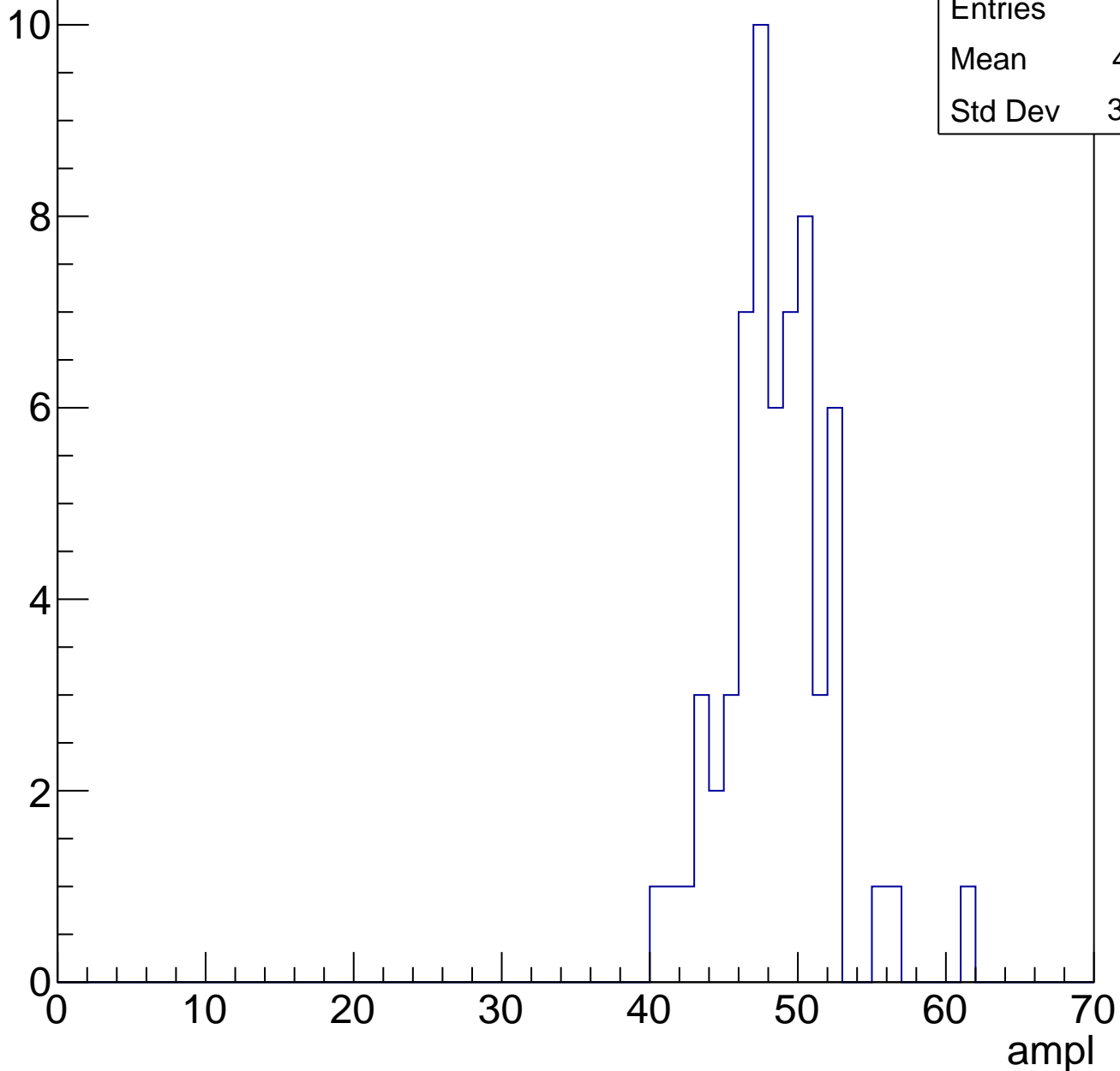


B1L103S, U5-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

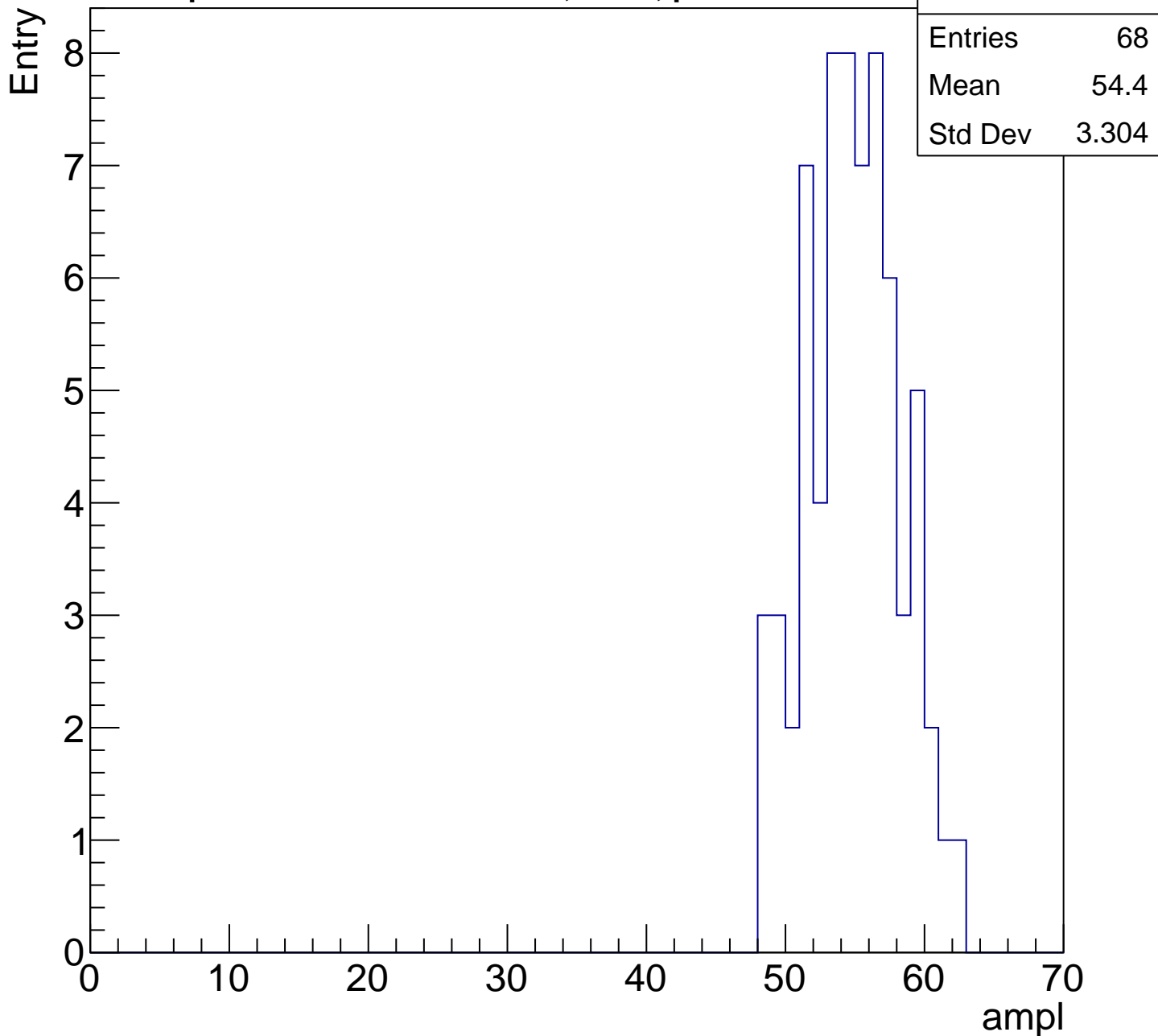
Entries	61
Mean	48.11
Std Dev	3.539

Entry



B1L103S, U5-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

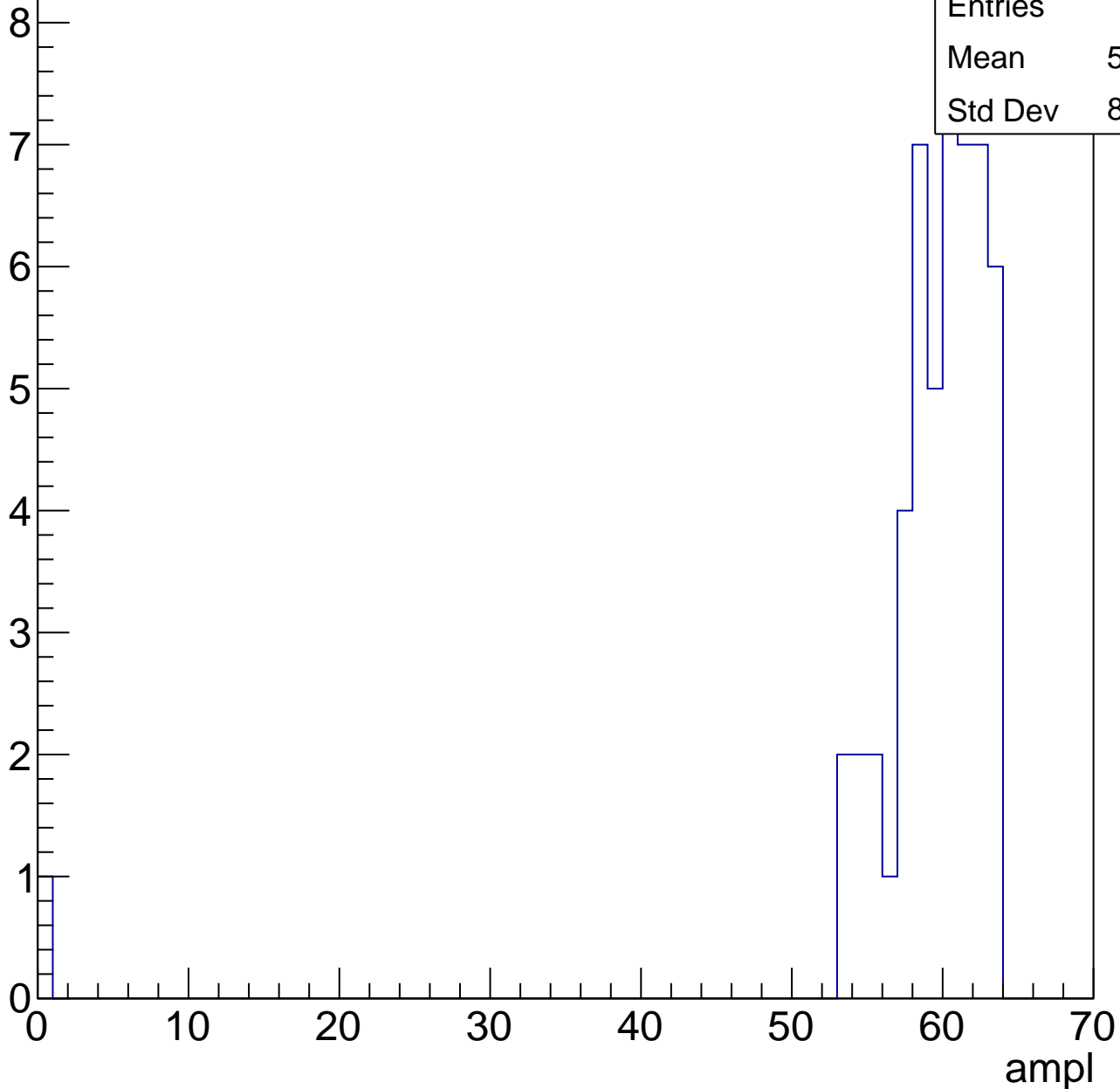


B1L103S, U5-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	58.23
Std Dev	8.584



B1L103S, U5-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

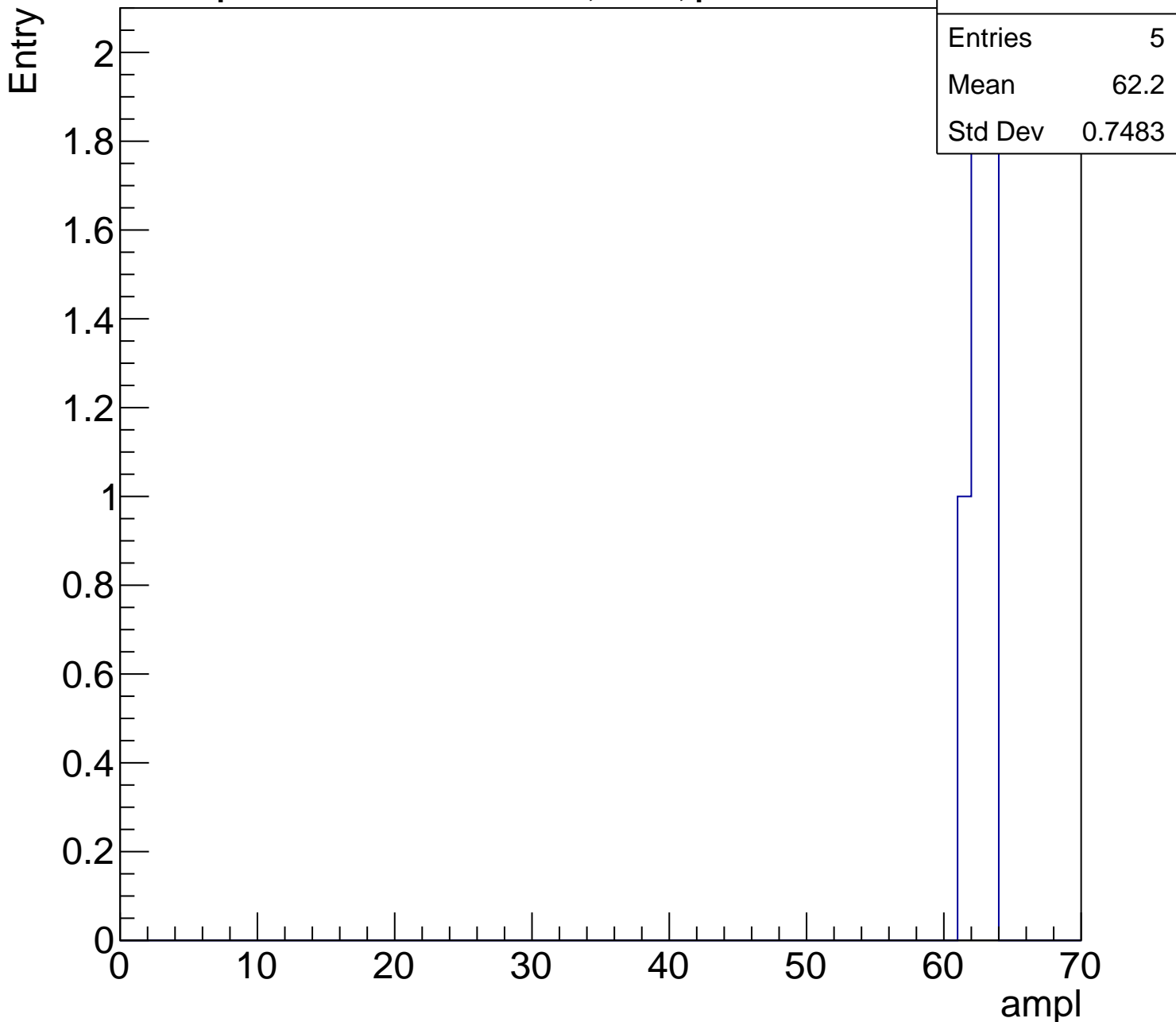
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch58, adc0

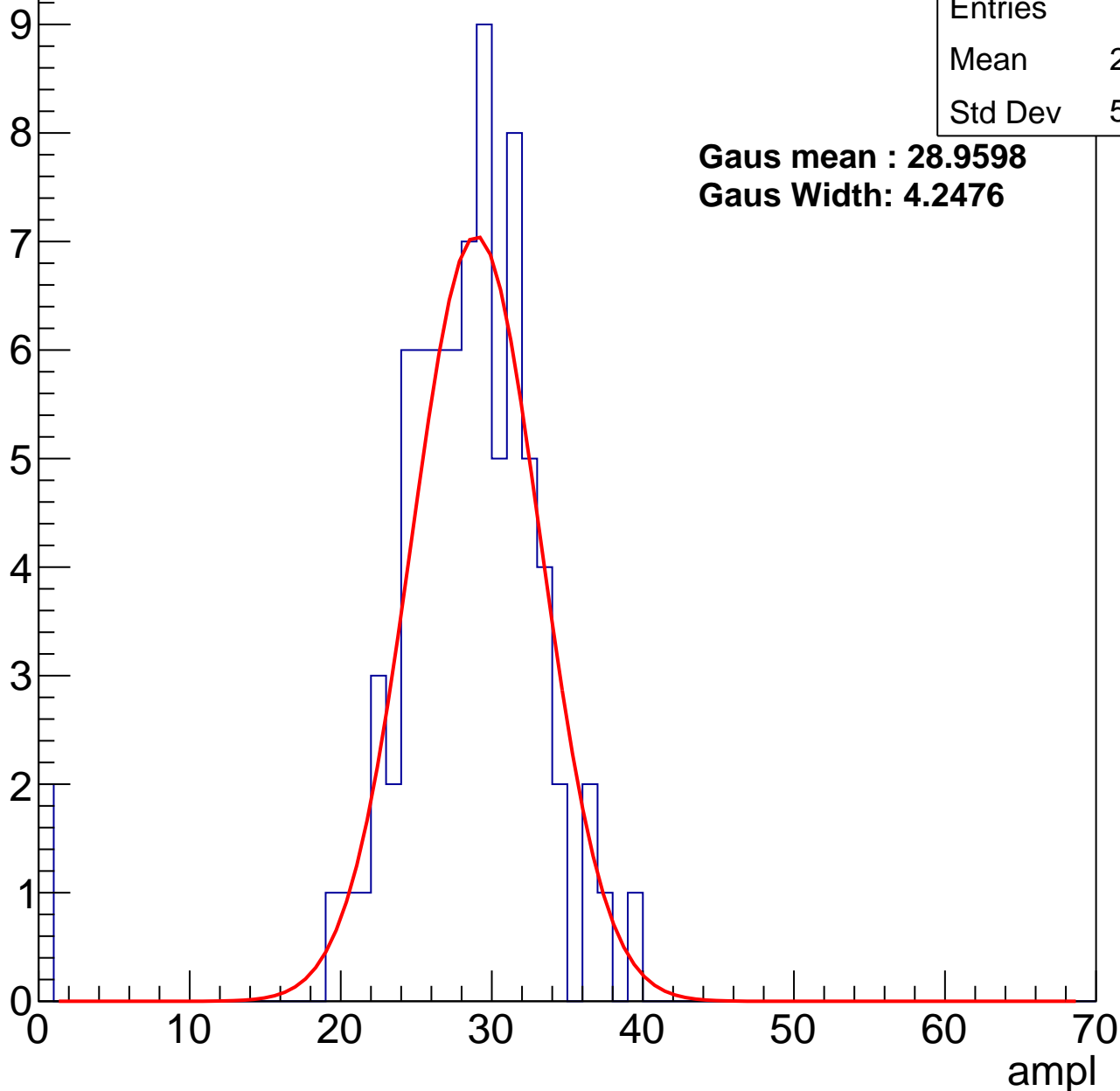
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	27.53
Std Dev	5.954

Gaus mean : 28.9598

Gaus Width: 4.2476



B1L103S, U5-ch58, adc1

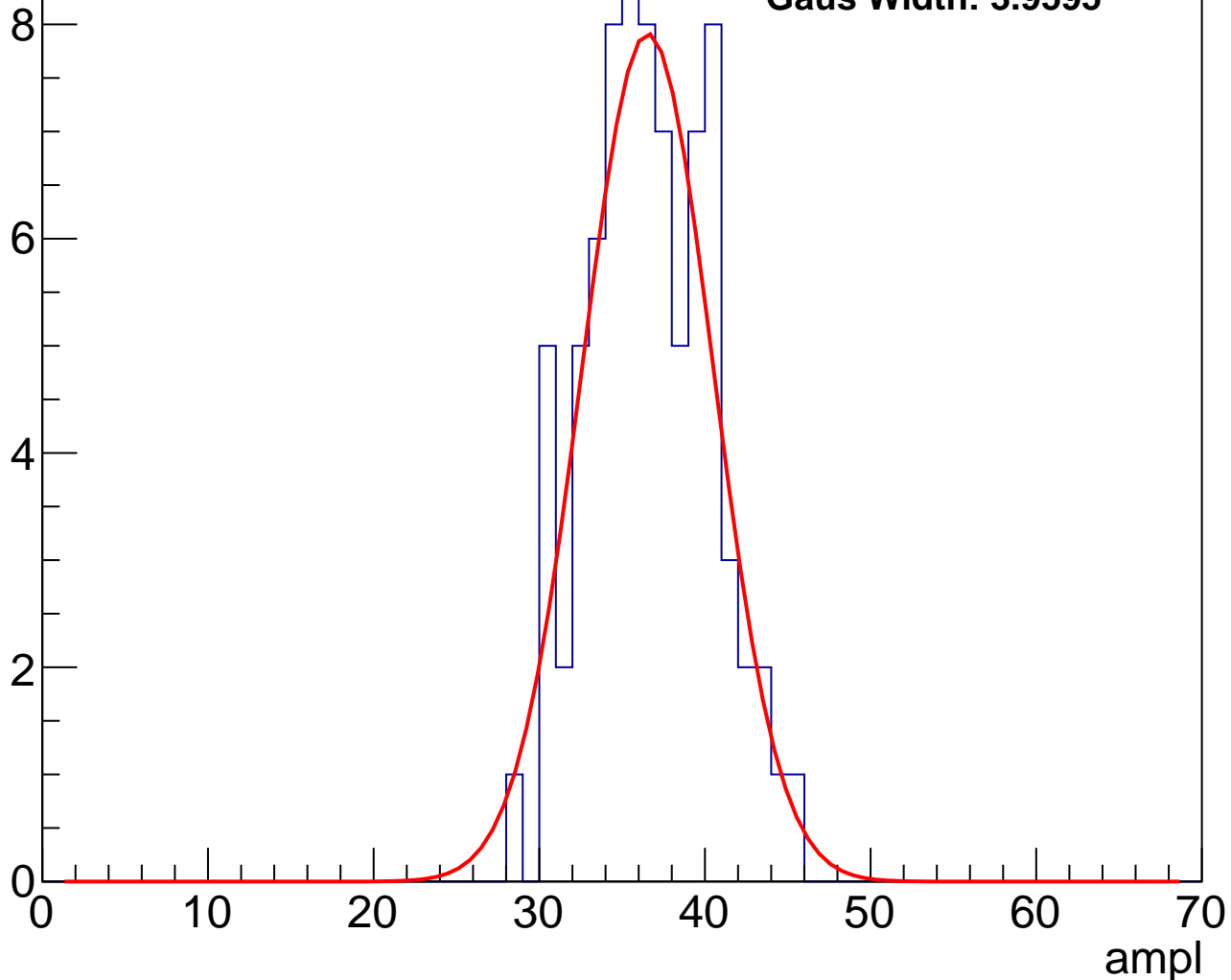
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	36.2
Std Dev	3.643

Gaus mean : 36.5477

Gaus Width: 3.9595



B1L103S, U5-ch58, adc2

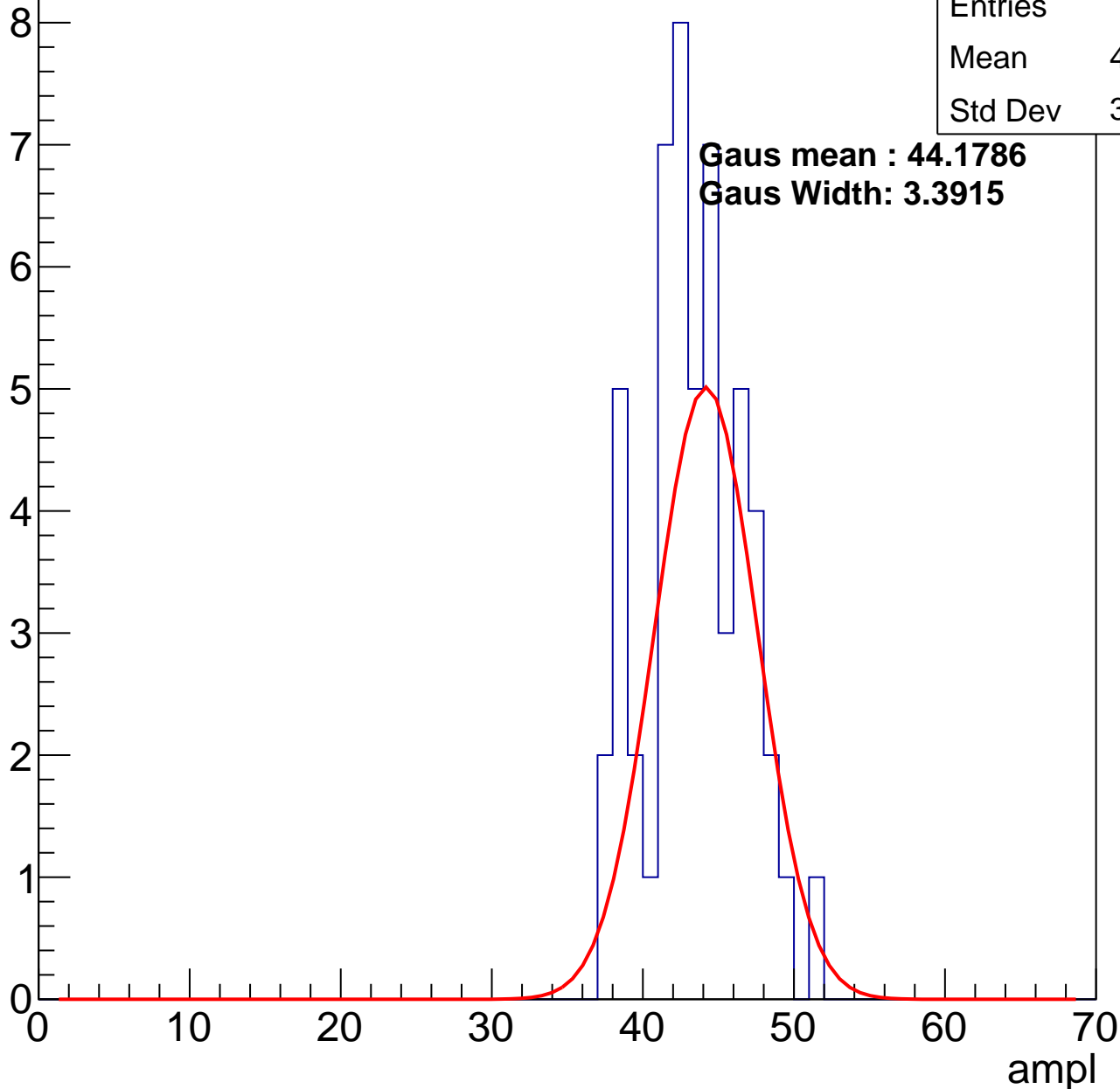
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.96
Std Dev	3.227

Gaus mean : 44.1786

Gaus Width: 3.3915

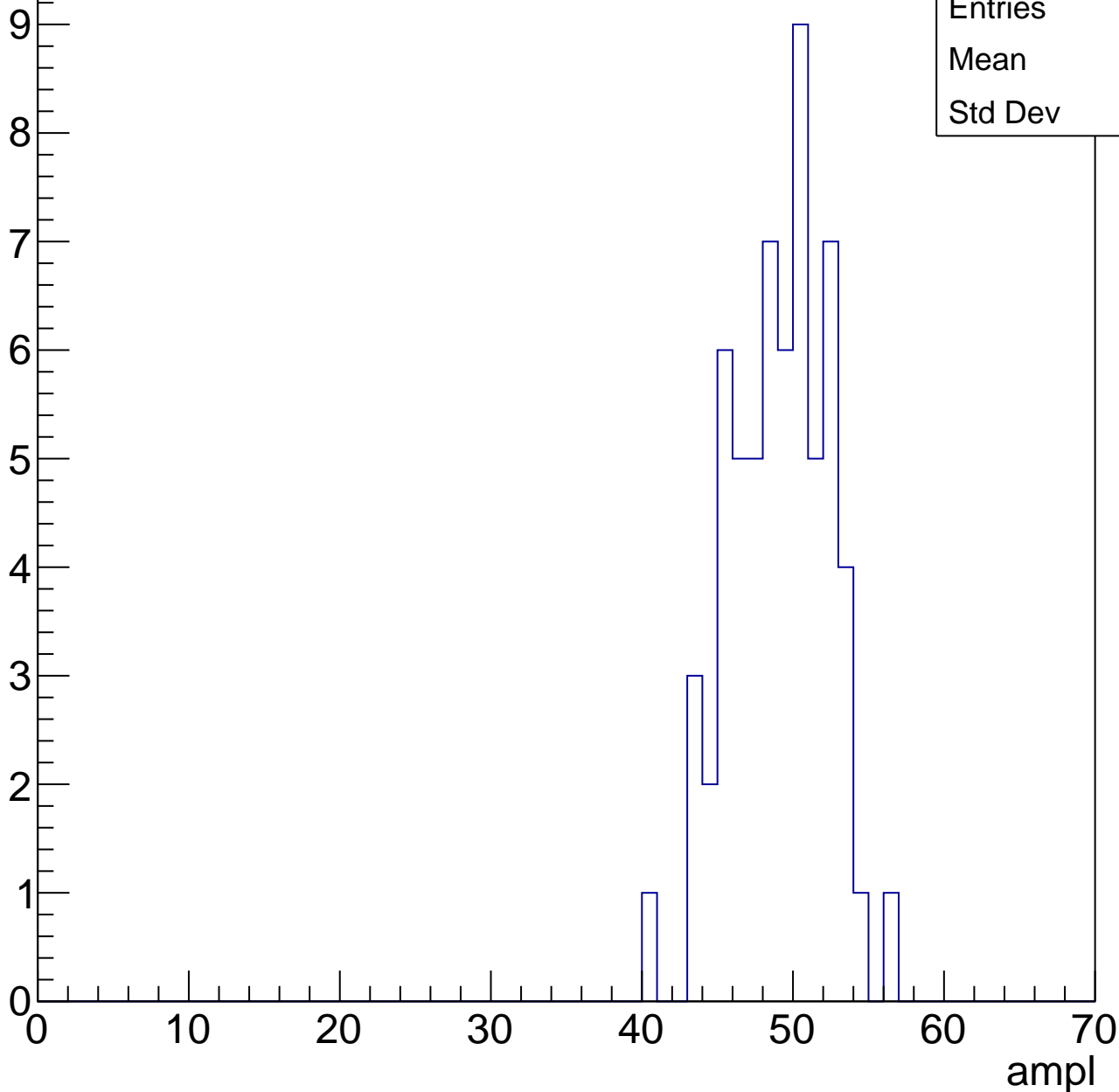


B1L103S, U5-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	48.6
Std Dev	3.18

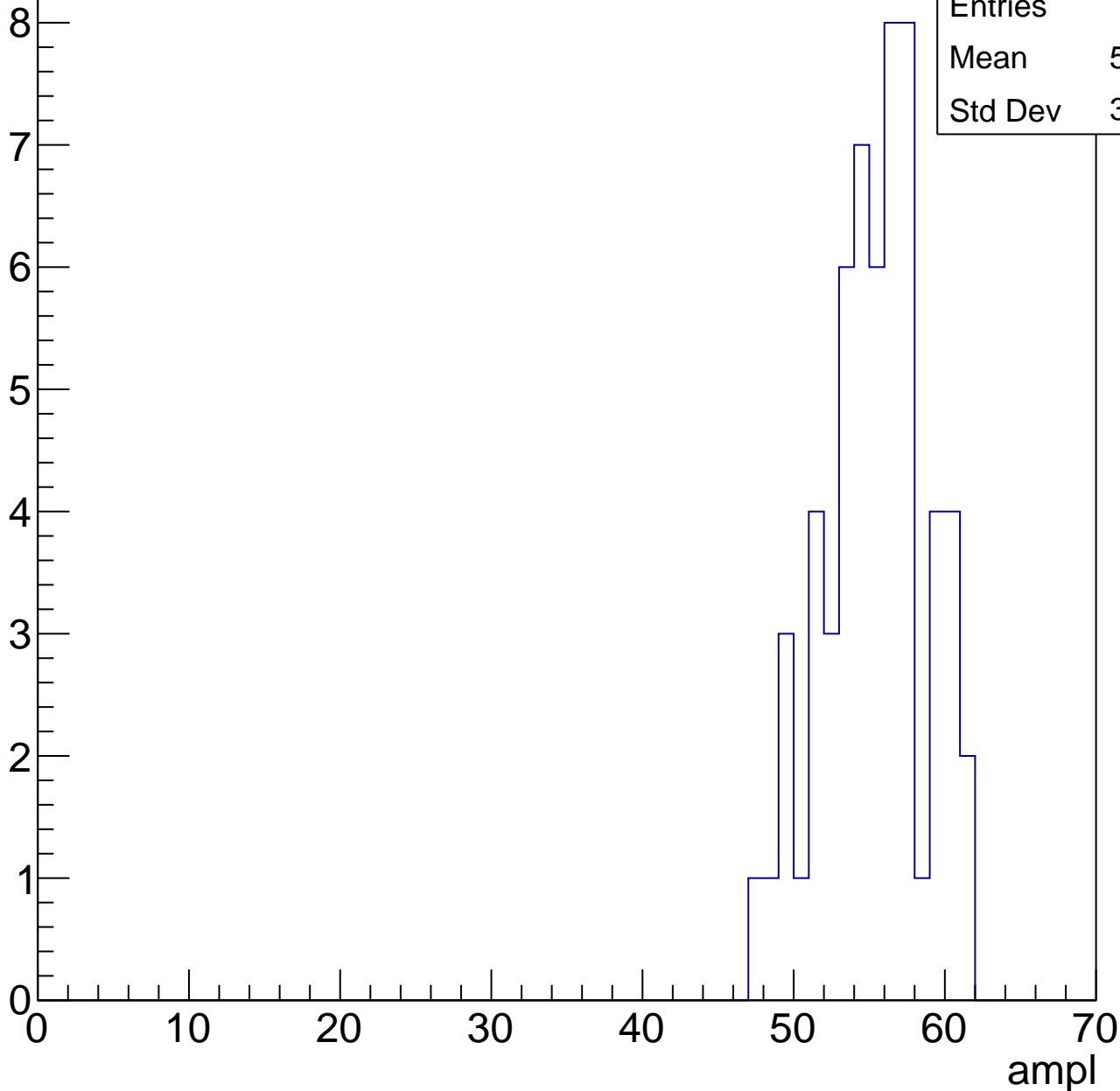


B1L103S, U5-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

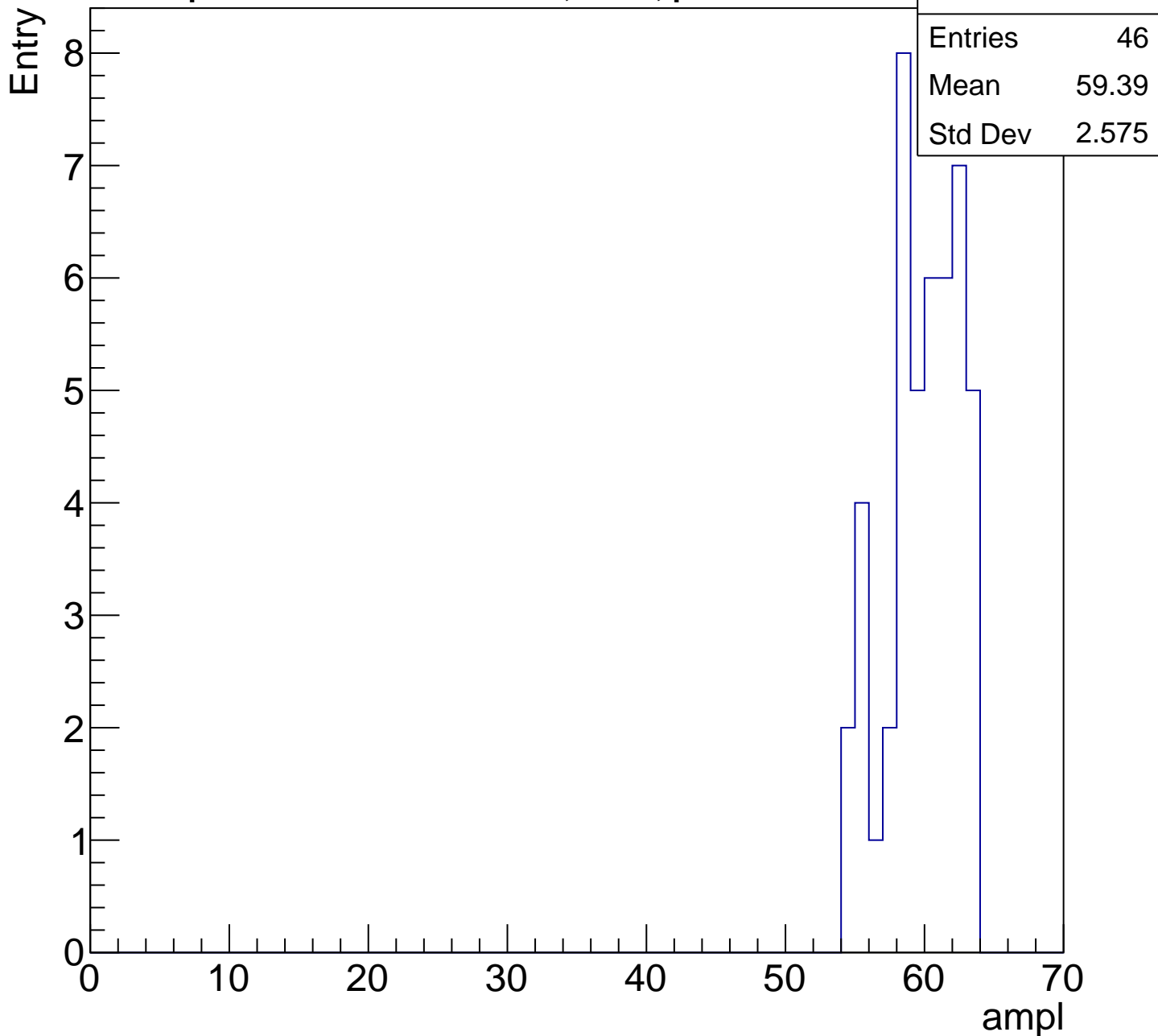
Entry

Entries	59
Mean	54.88
Std Dev	3.325



B1L103S, U5-ch58, adc5

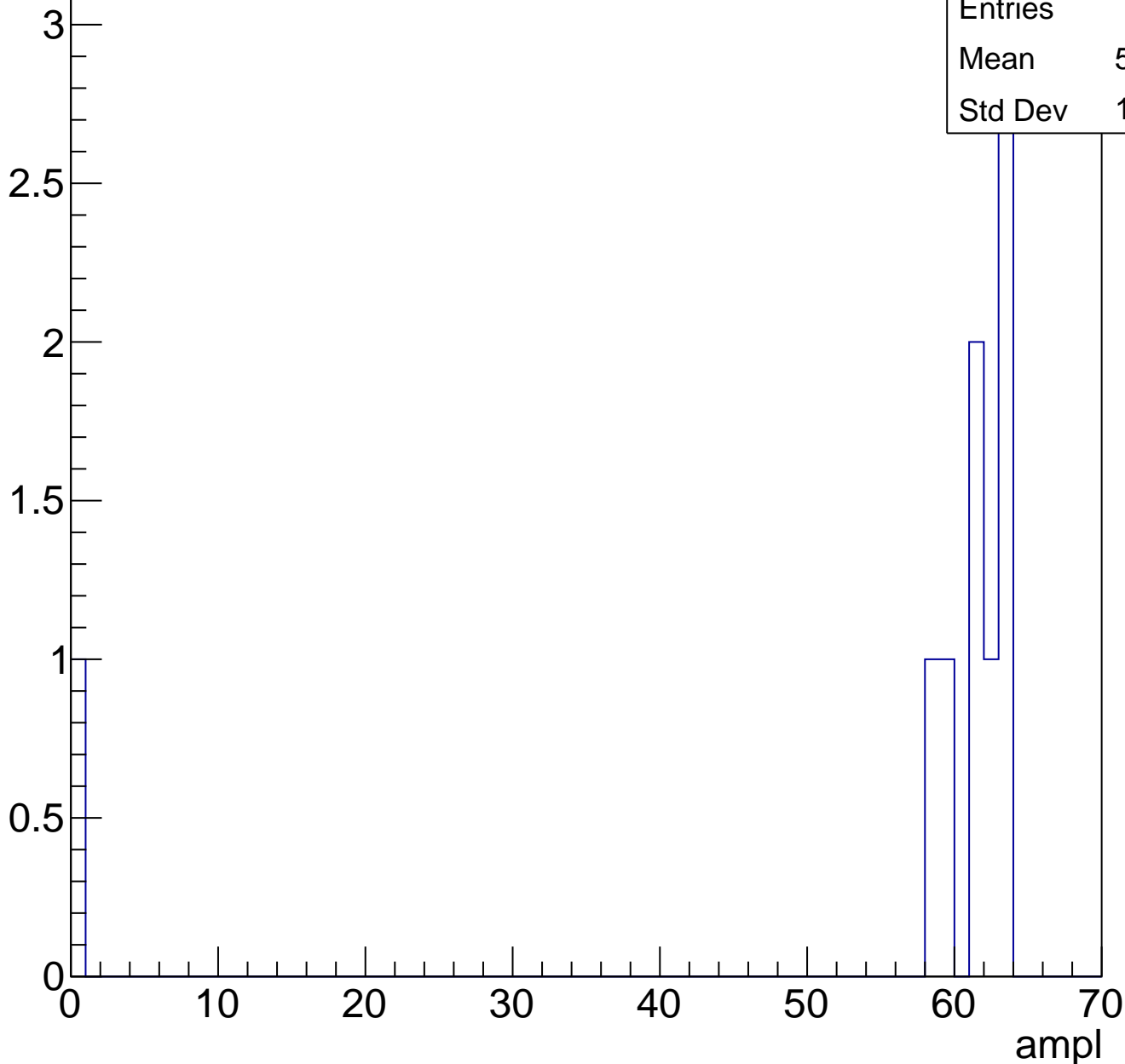
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

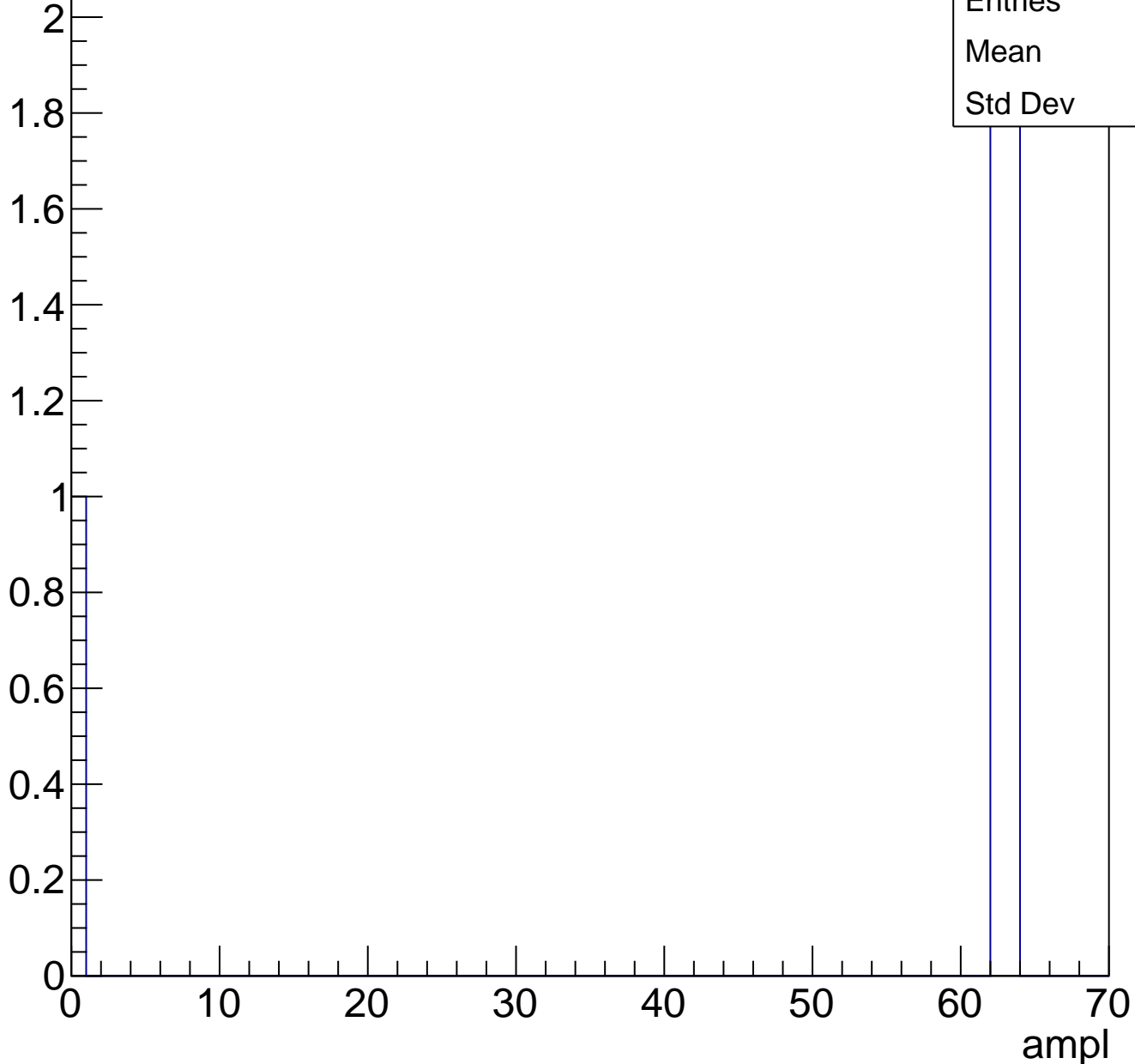
Entry



B1L103S, U5-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	50
Std Dev	25

B1L103S, U5-ch59, adc0

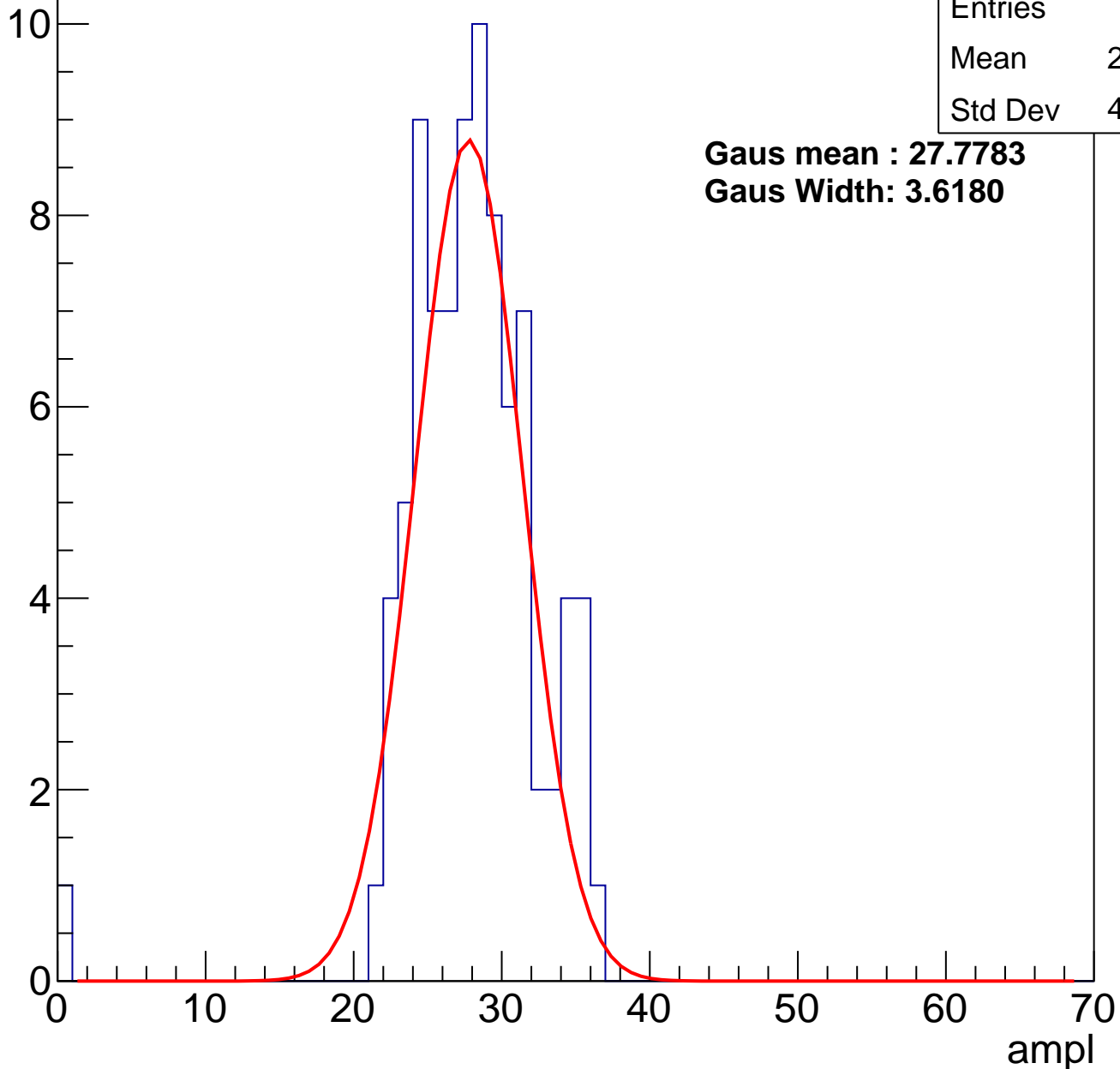
calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	27.48
Std Dev	4.678

Gaus mean : 27.7783

Gaus Width: 3.6180

Entry



B1L103S, U5-ch59, adc1

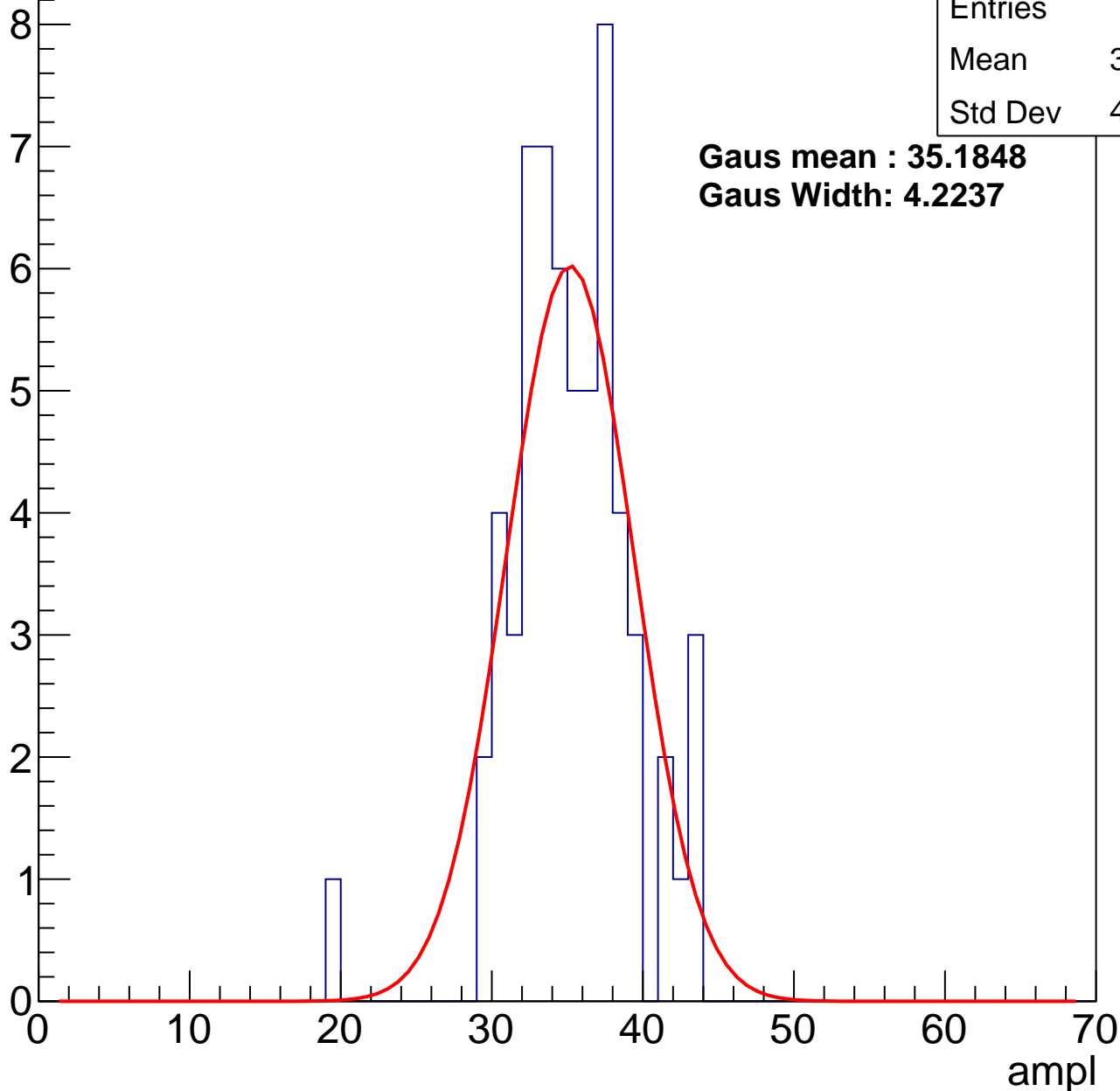
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	34.79
Std Dev	4.057

Gaus mean : 35.1848

Gaus Width: 4.2237



B1L103S, U5-ch59, adc2

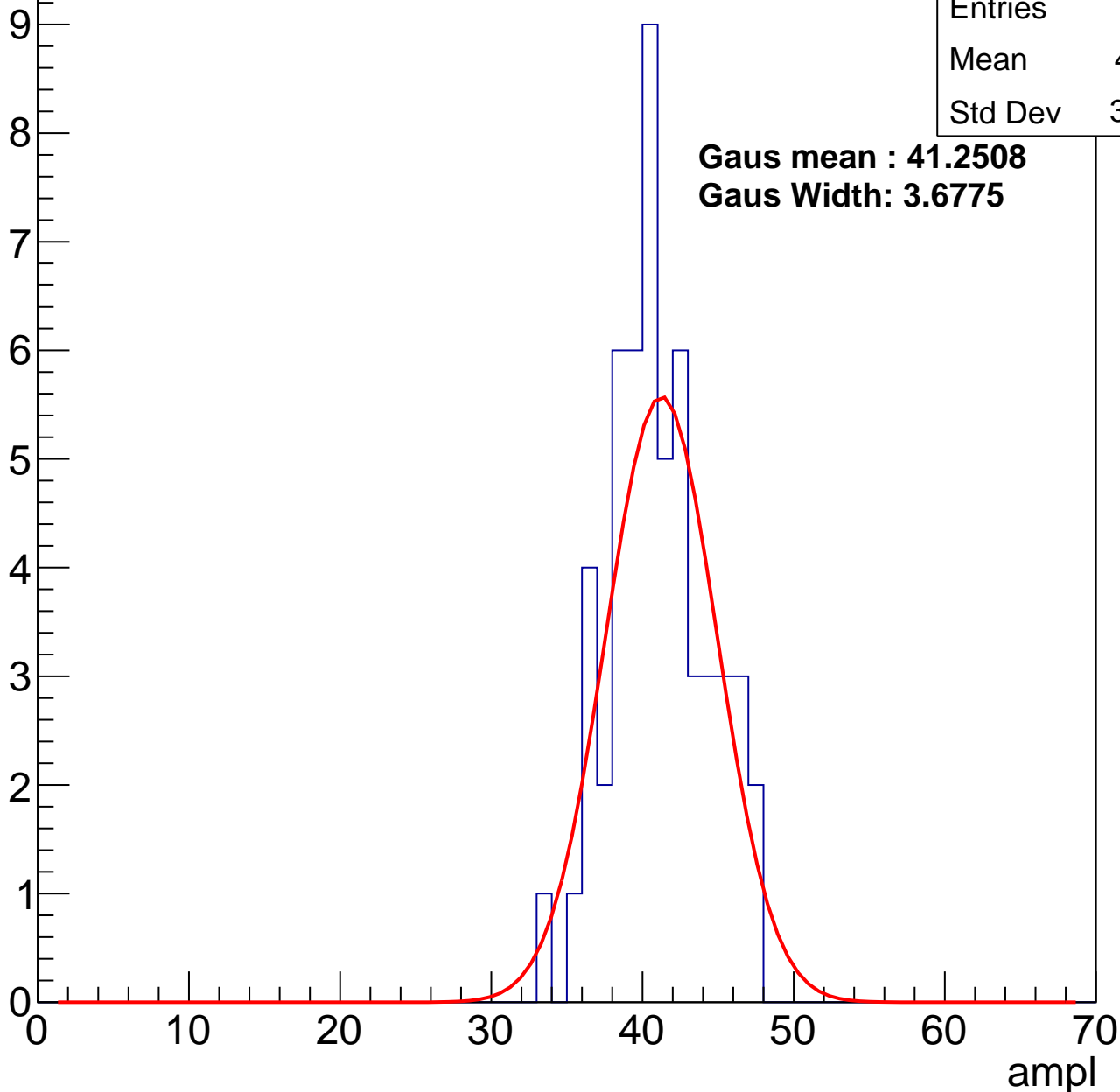
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	40.61
Std Dev	3.194

Gaus mean : 41.2508

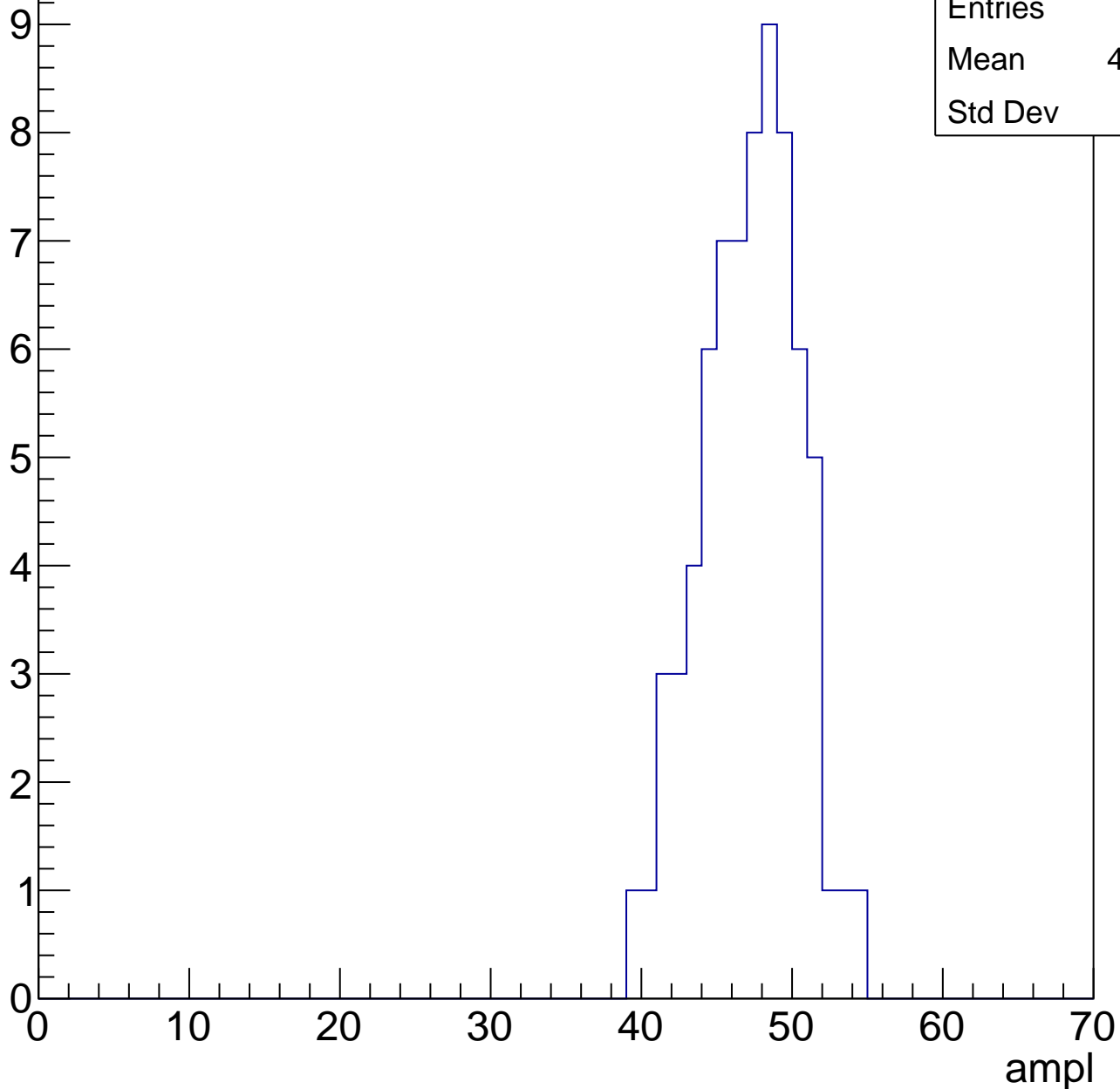
Gaus Width: 3.6775



B1L103S, U5-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

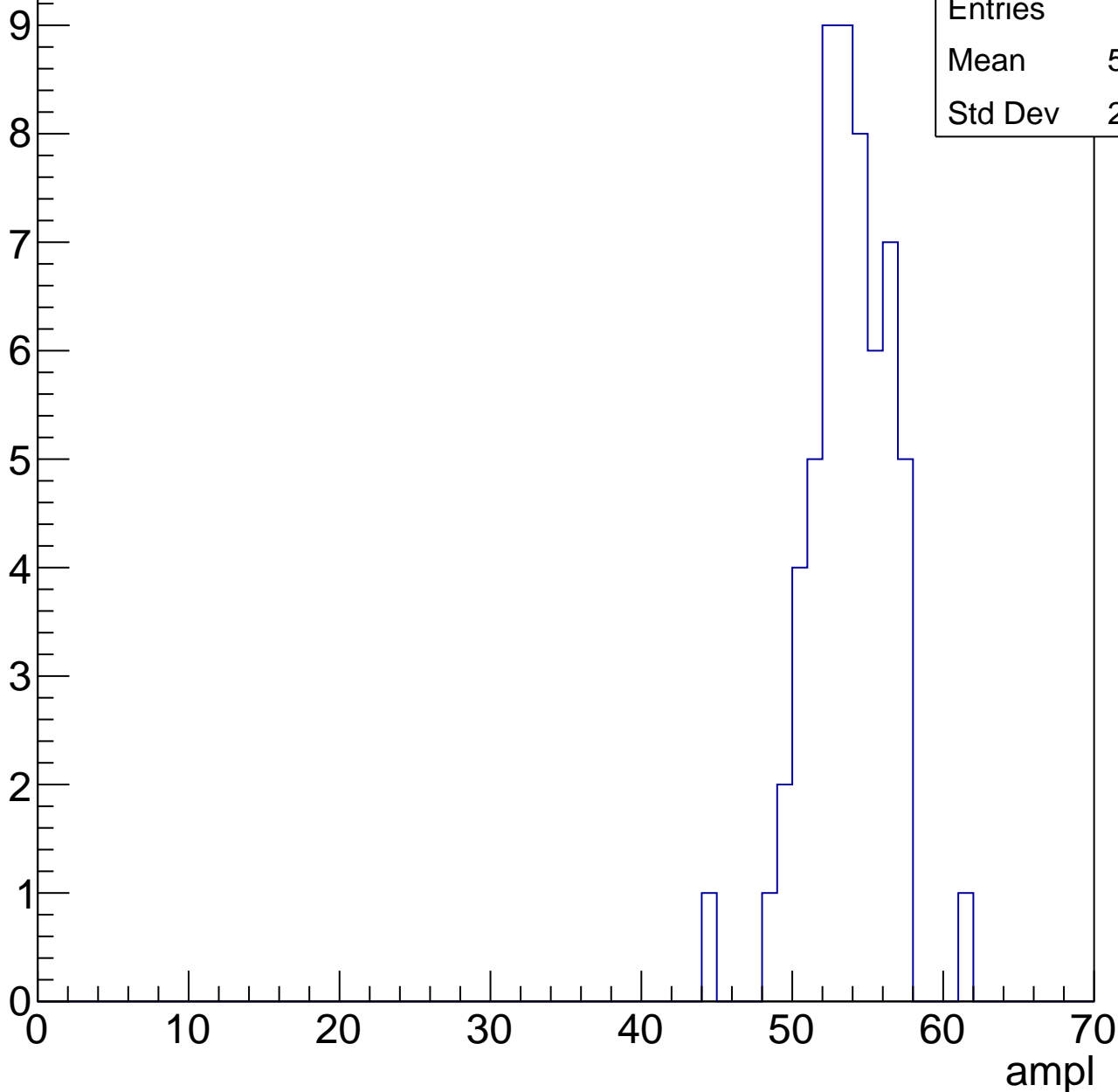


B1L103S, U5-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	53.28
Std Dev	2.747

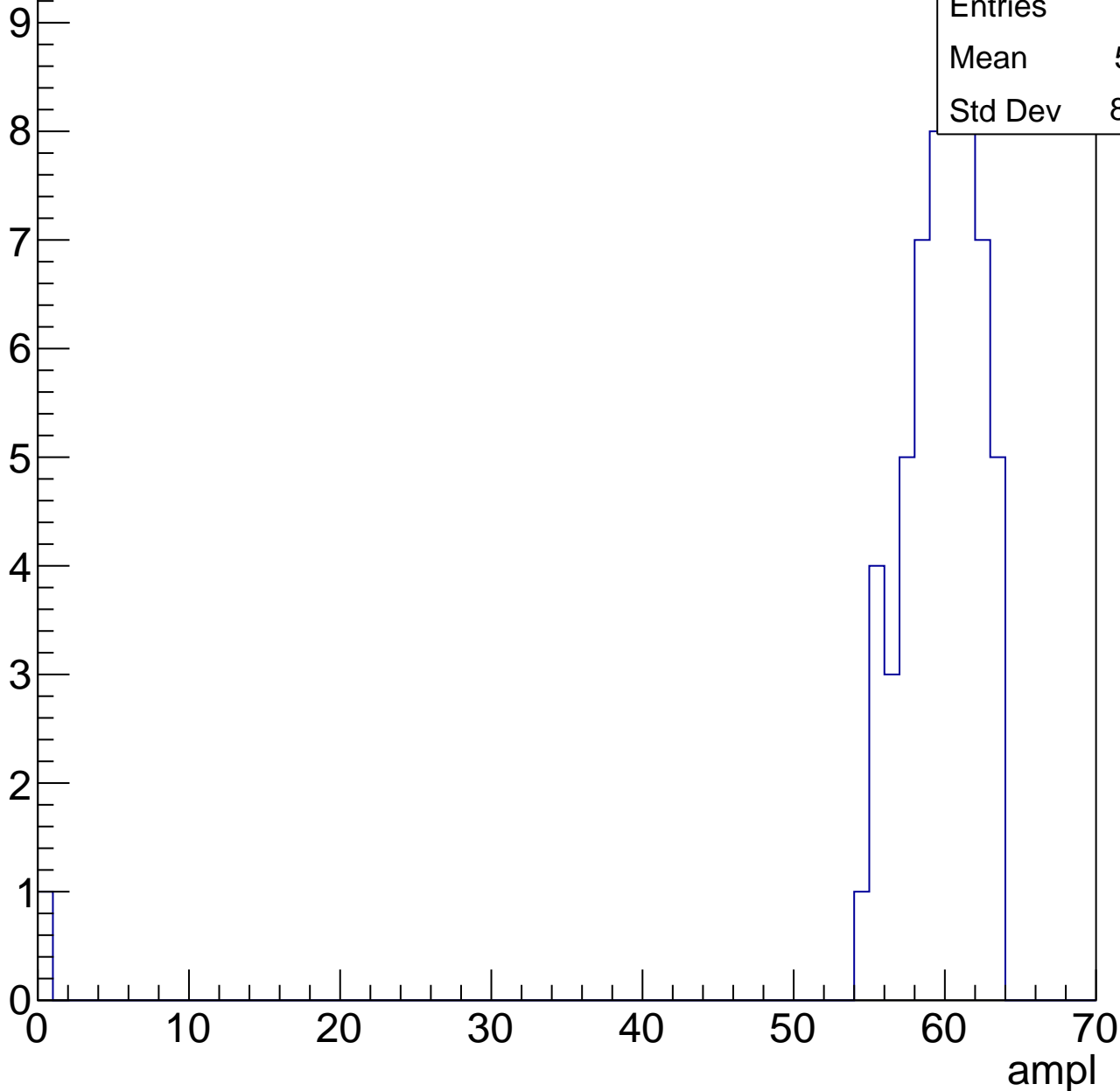


B1L103S, U5-ch59, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

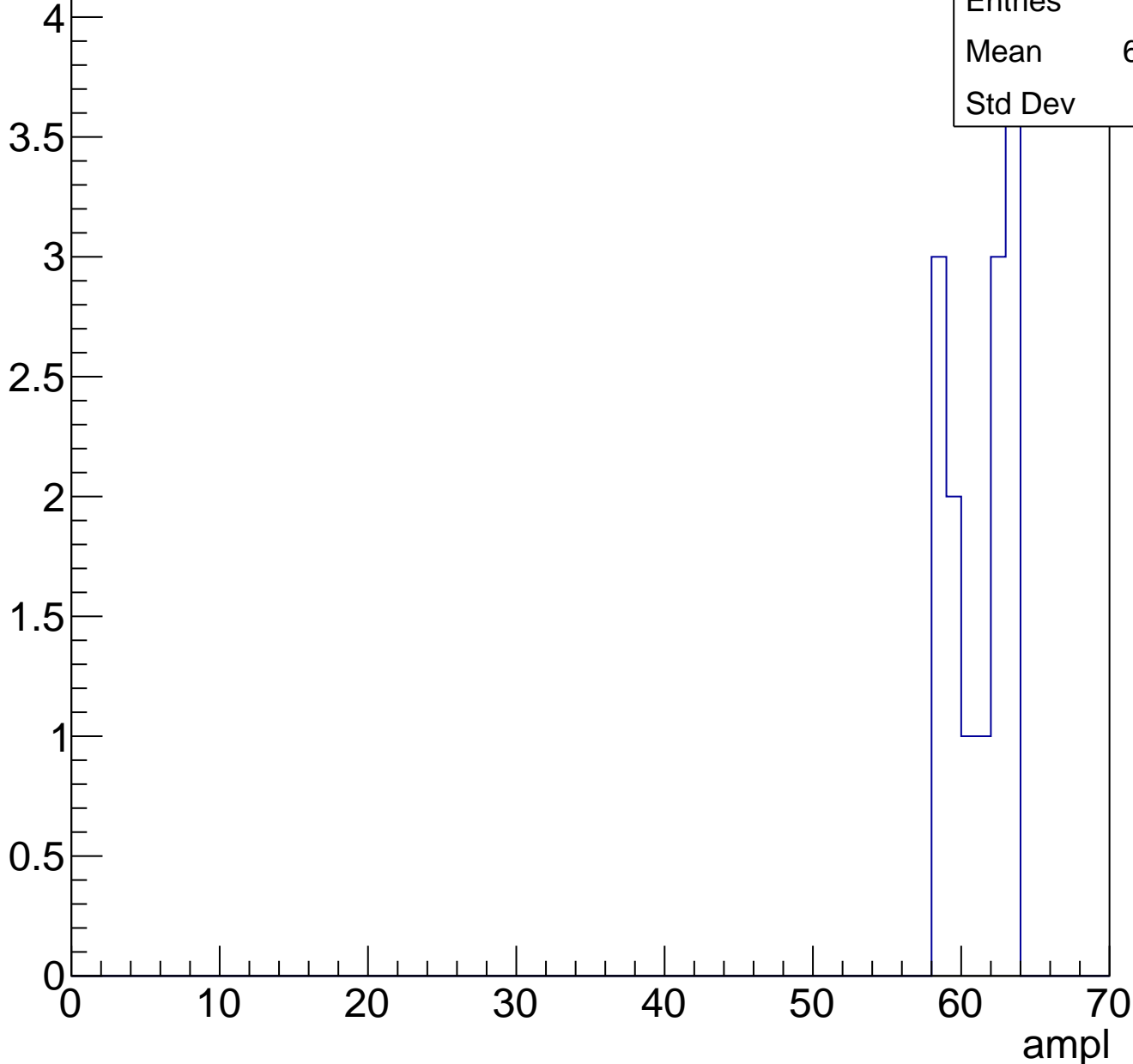
Entries	58
Mean	58.31
Std Dev	8.073



B1L103S, U5-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	14
Mean	60.79
Std Dev	1.97

B1L103S, U5-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch60, adc0

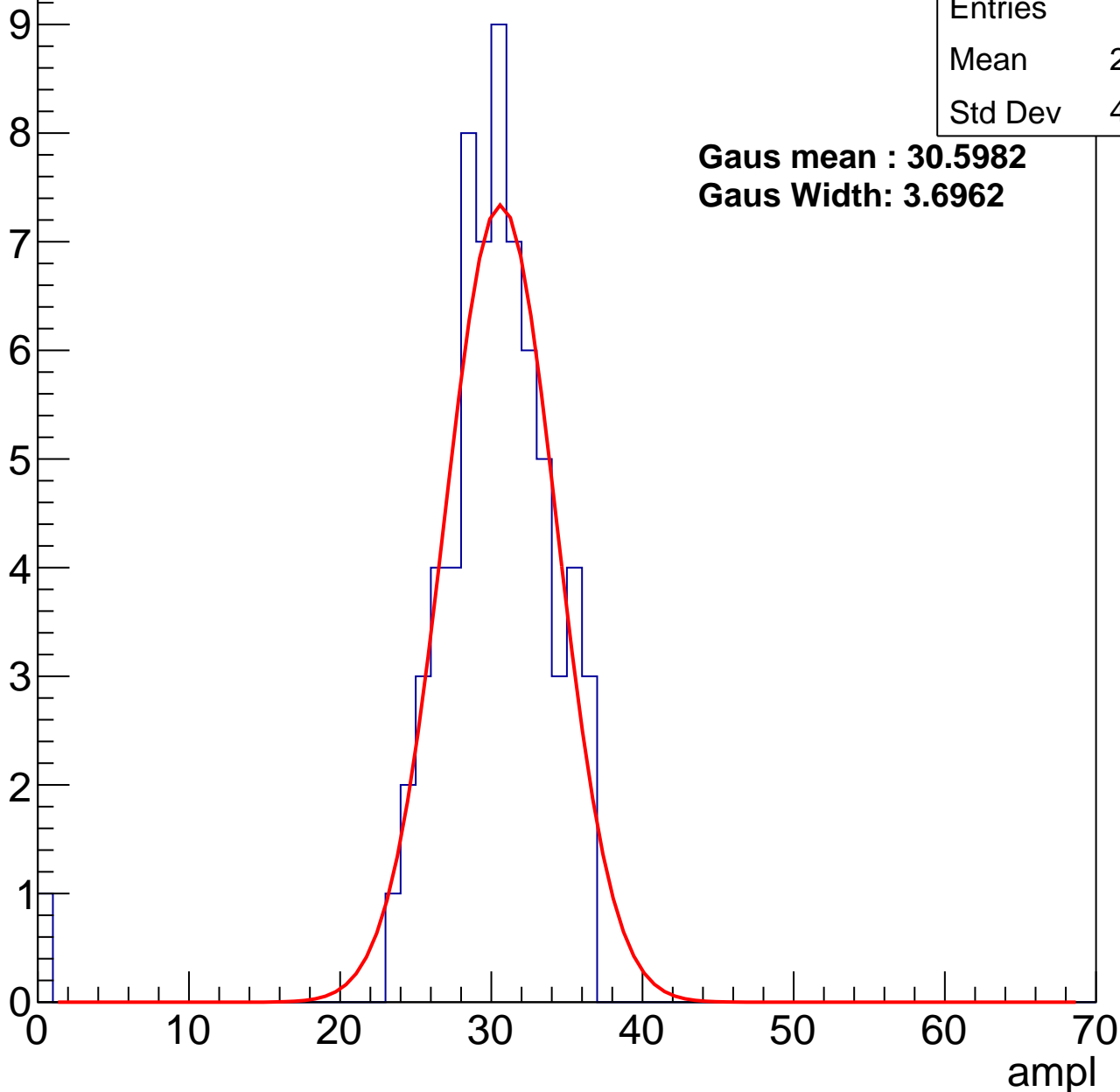
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.54
Std Dev	4.814

Gaus mean : 30.5982

Gaus Width: 3.6962



B1L103S, U5-ch60, adc1

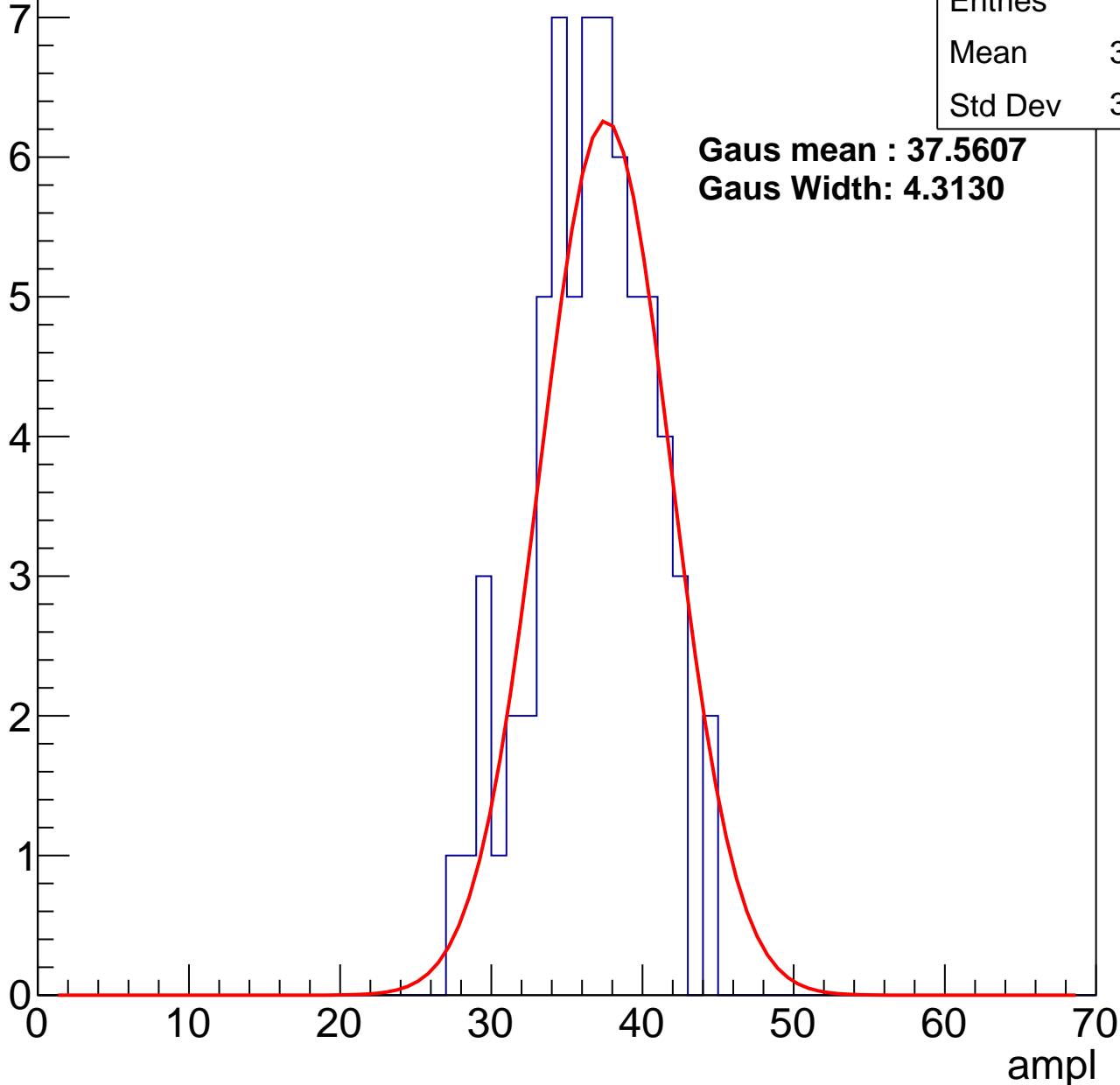
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.18
Std Dev	3.849

Gaus mean : 37.5607

Gaus Width: 4.3130



B1L103S, U5-ch60, adc2

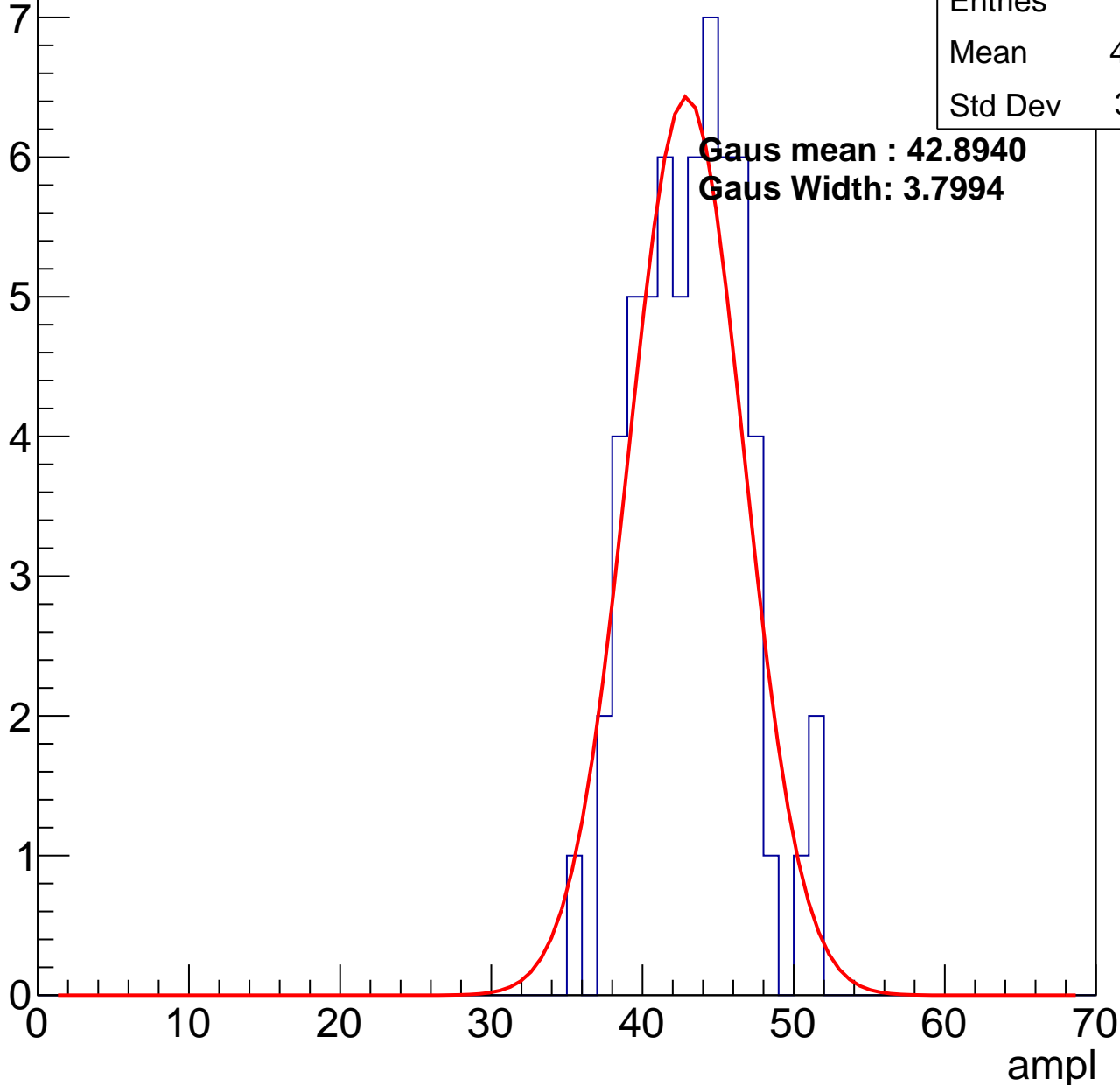
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.82
Std Dev	3.481

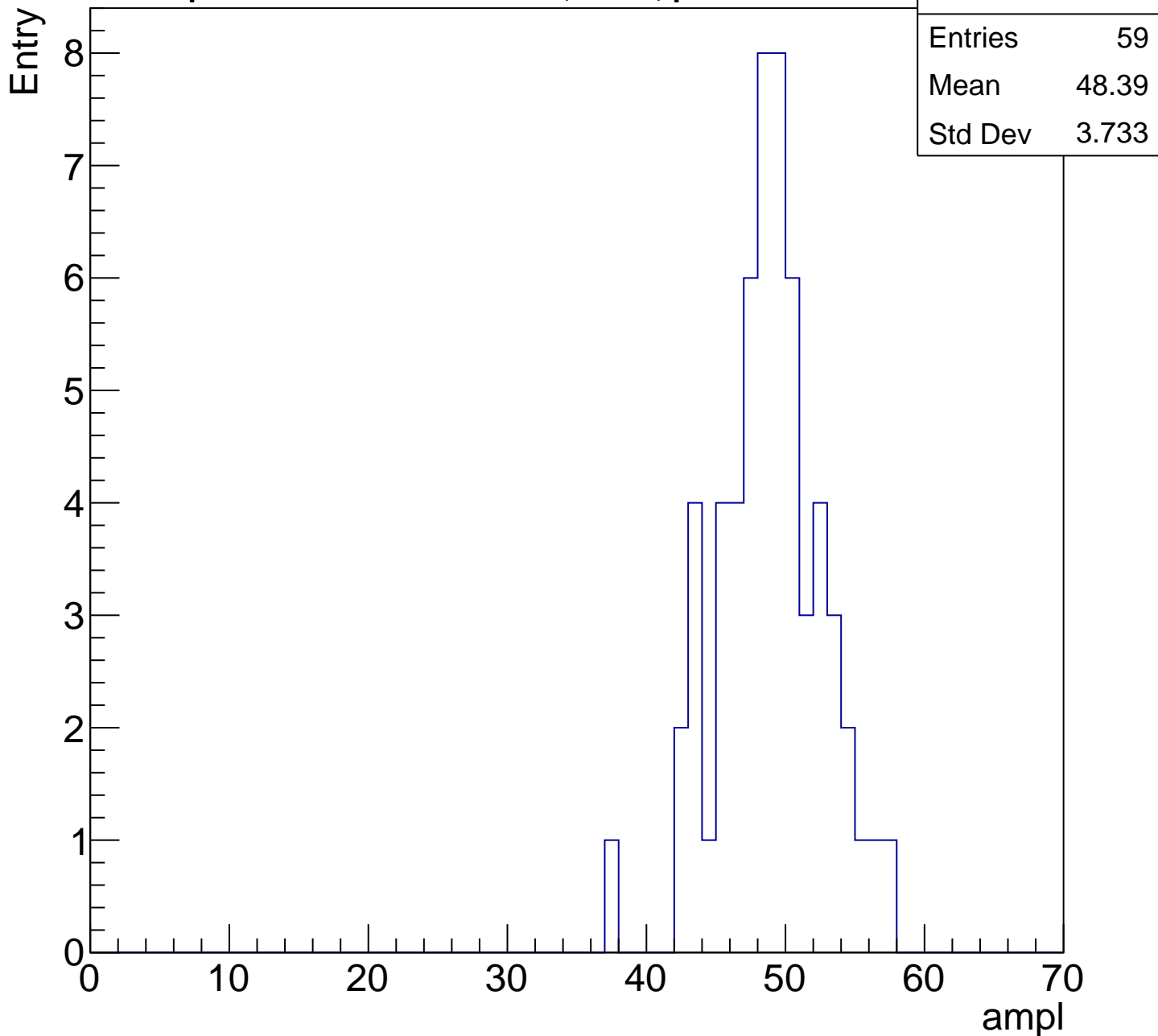
Gaus mean : 42.8940

Gaus Width: 3.7994



B1L103S, U5-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

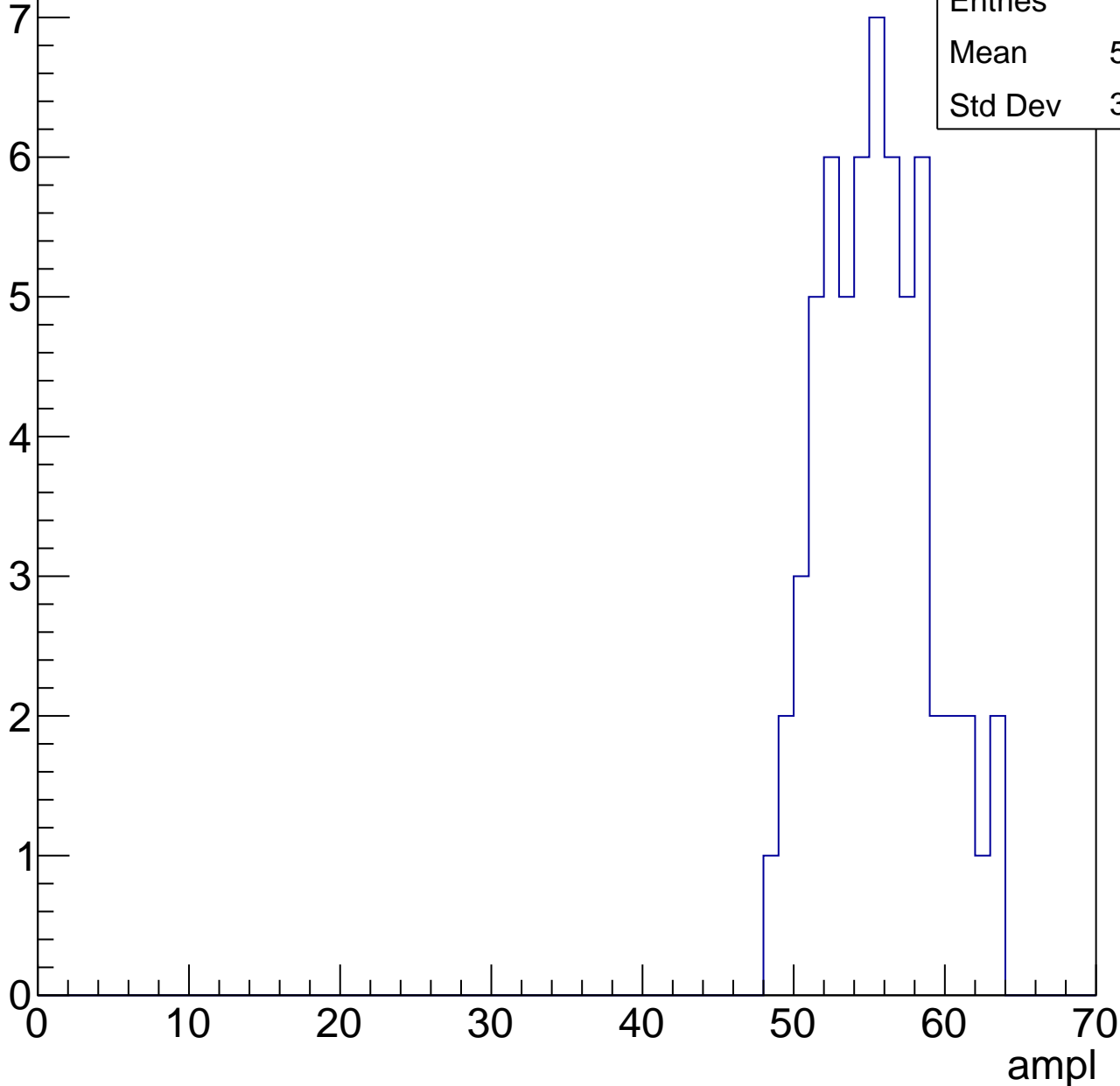


B1L103S, U5-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	54.98
Std Dev	3.546



B1L103S, U5-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	48
Mean	57.85
Std Dev	8.865

ampl

0

10

20

30

40

50

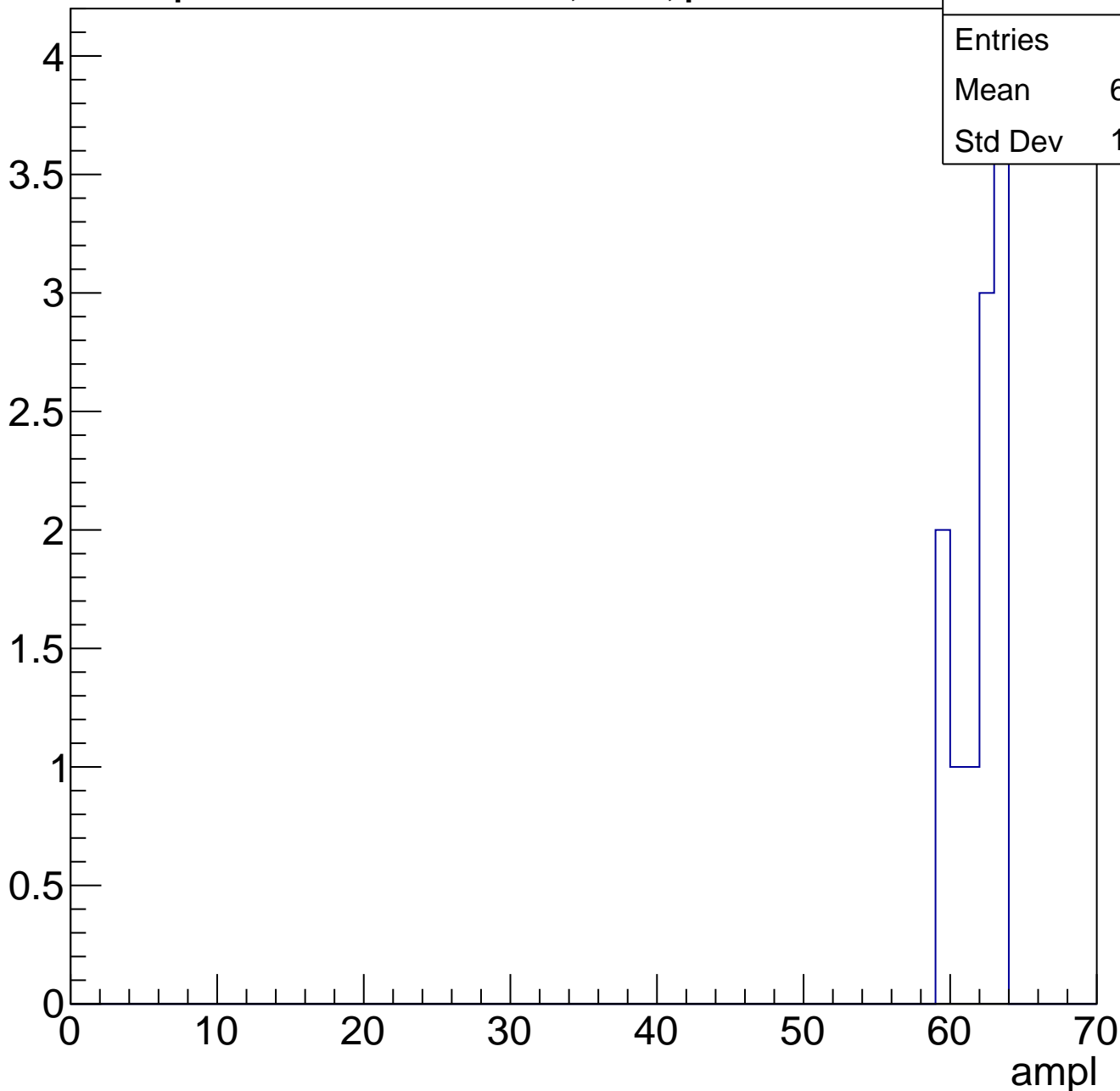
60

70

B1L103S, U5-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	11
Mean	61.55
Std Dev	1.499

B1L103S, U5-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch61, adc0

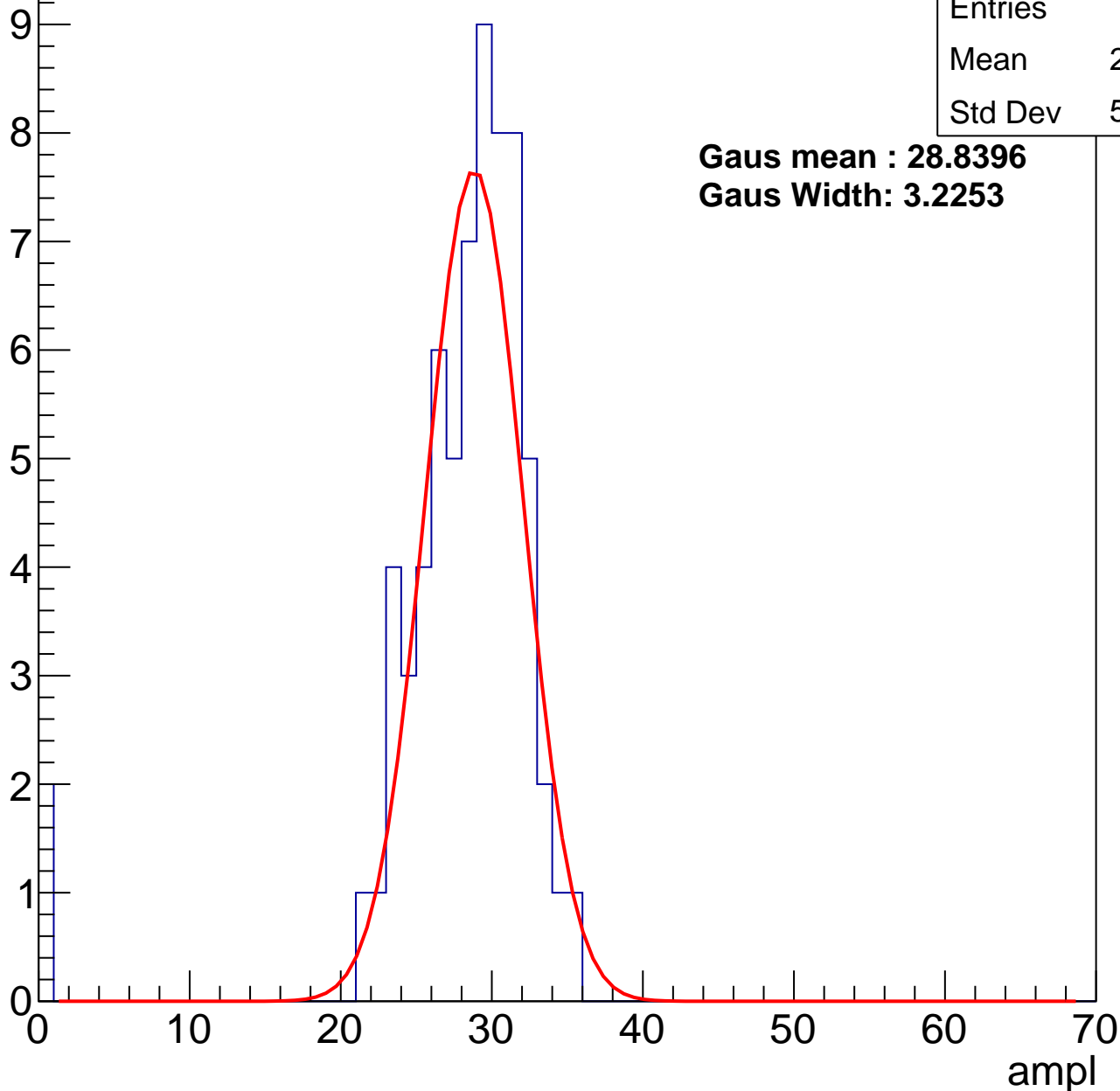
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.43
Std Dev	5.694

Gaus mean : 28.8396

Gaus Width: 3.2253



B1L103S, U5-ch61, adc1

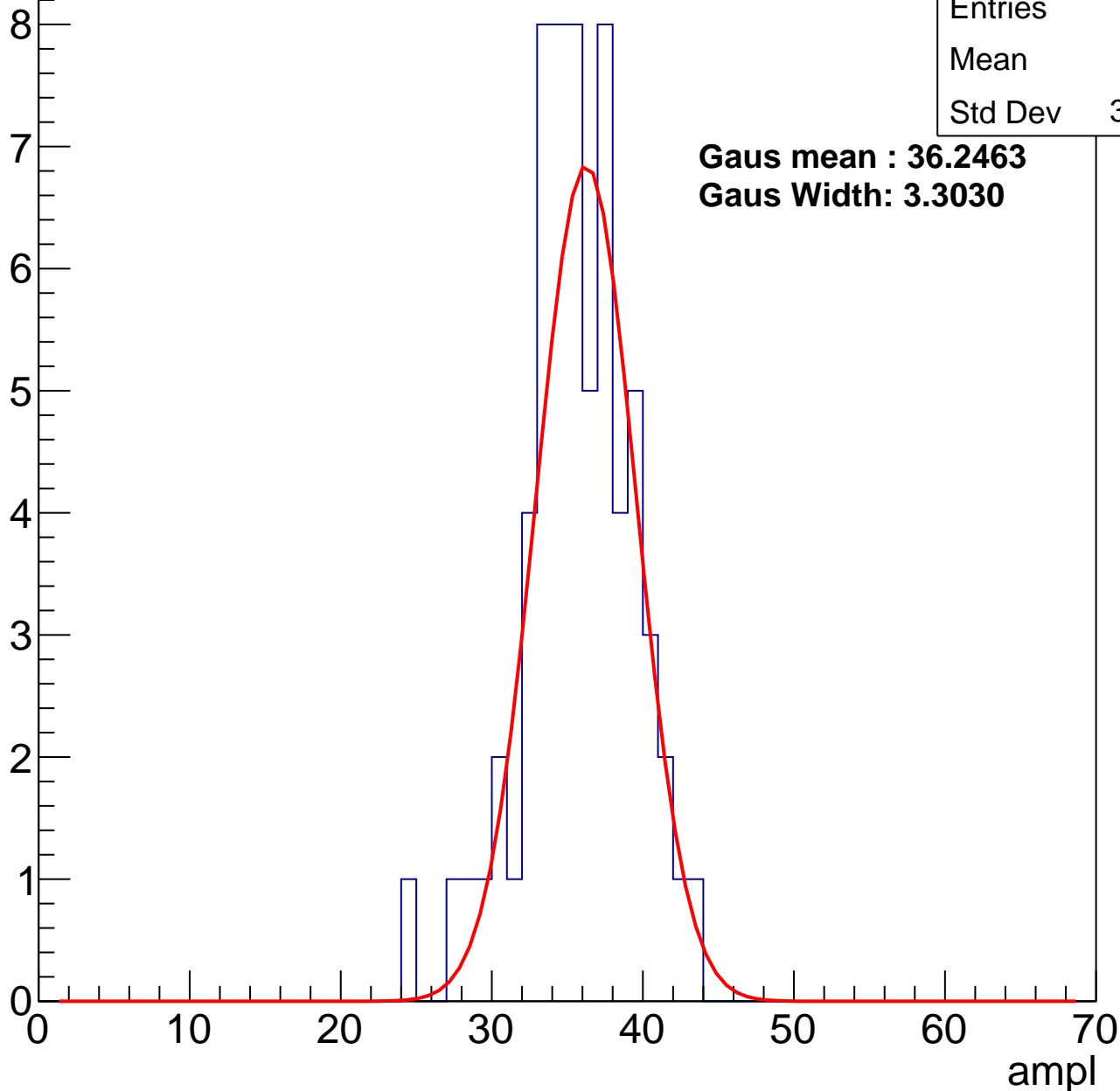
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.2
Std Dev	3.585

Gaus mean : 36.2463

Gaus Width: 3.3030



B1L103S, U5-ch61, adc2

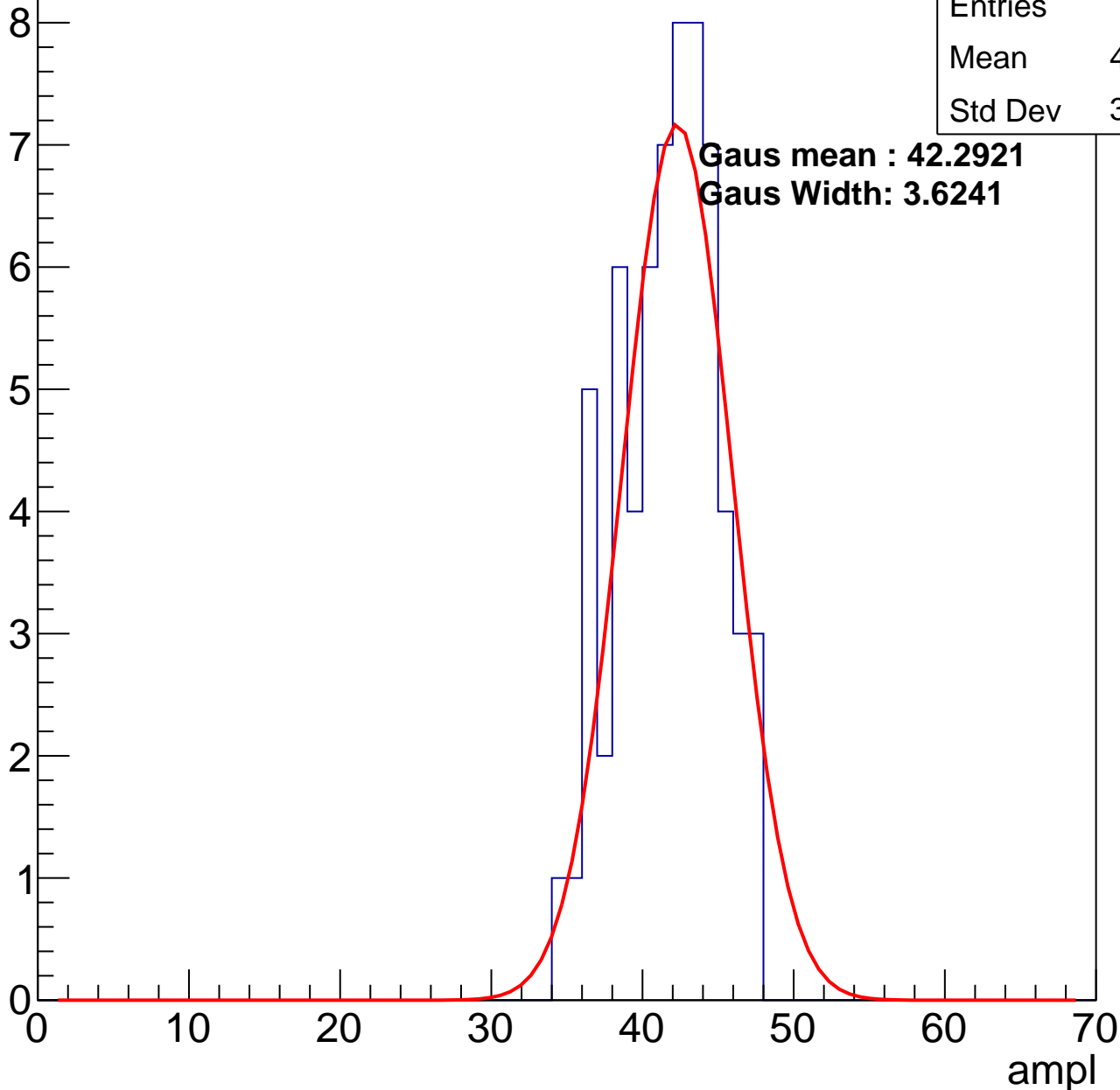
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.25
Std Dev	3.196

Gaus mean : 42.2921

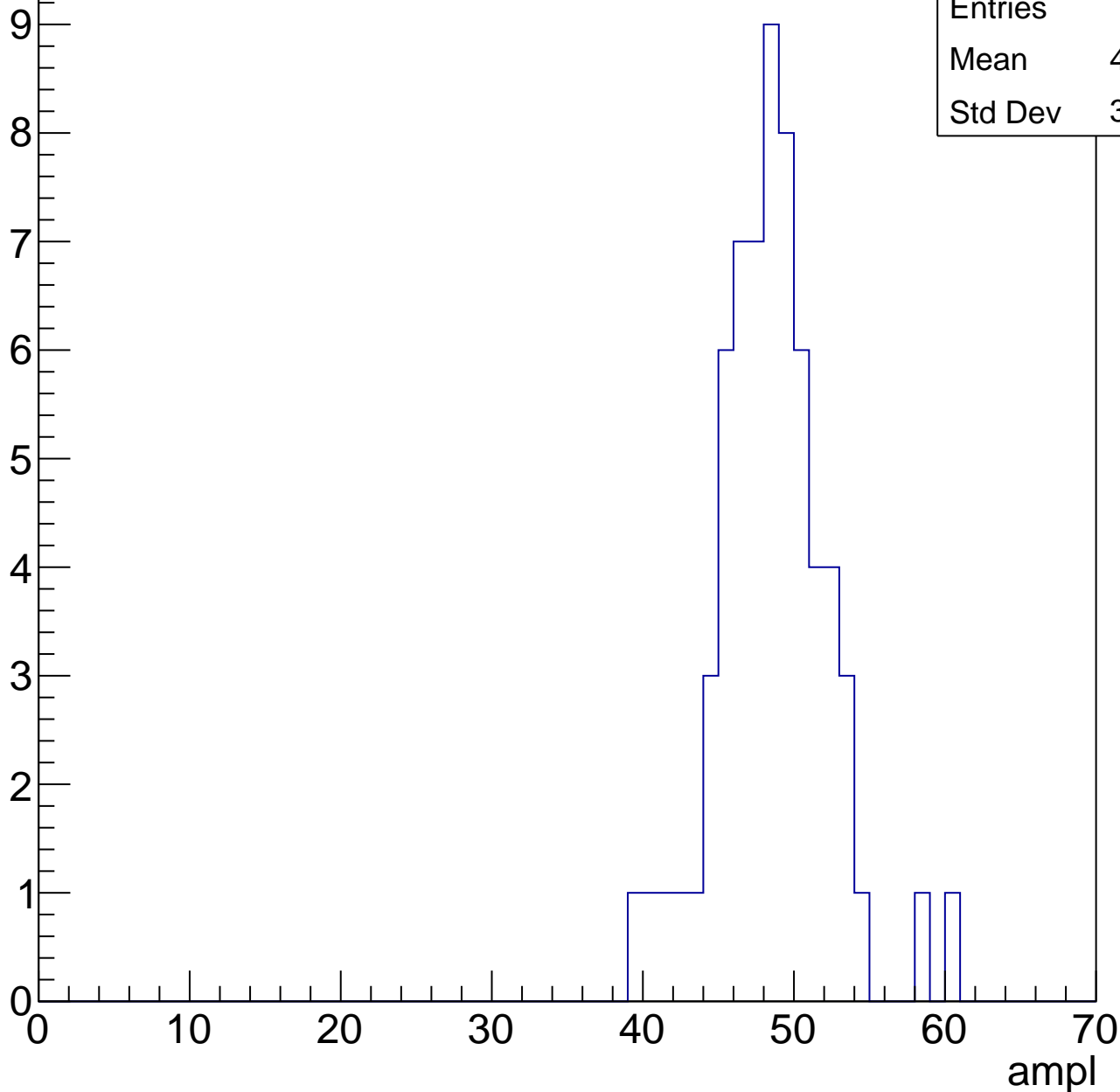
Gaus Width: 3.6241



B1L103S, U5-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

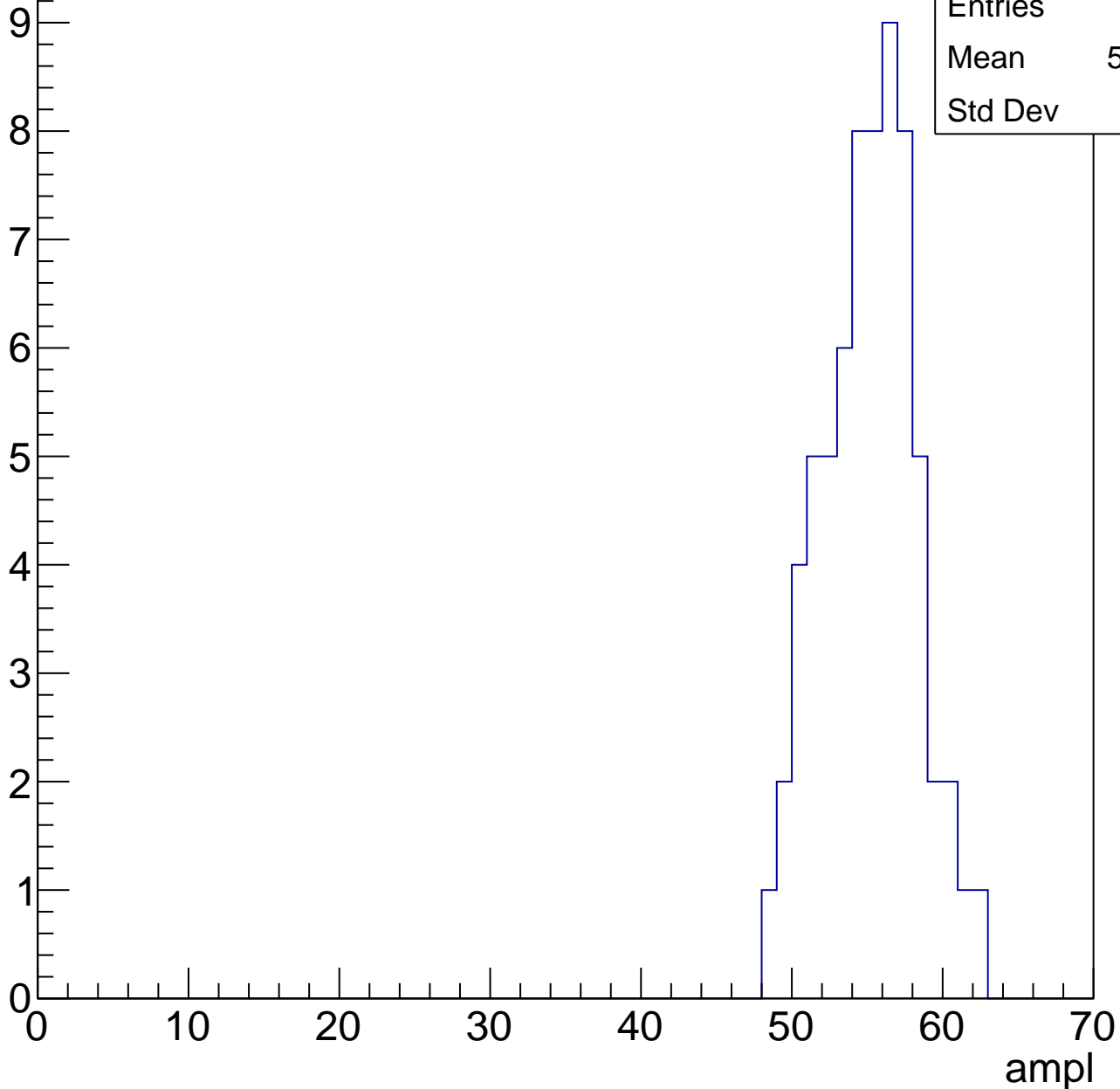


B1L103S, U5-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

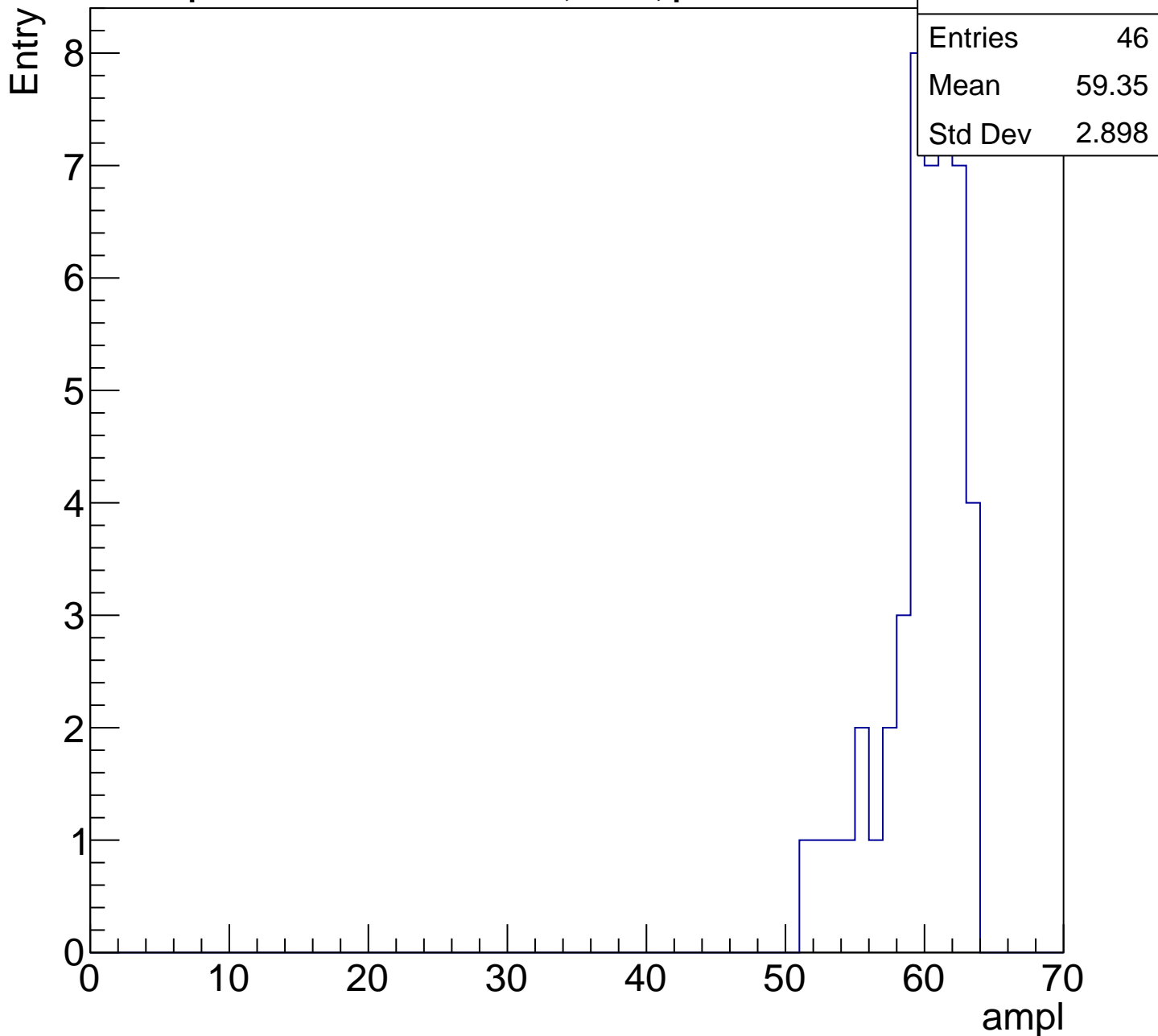
Entry

Entries	67
Mean	54.66
Std Dev	3.05



B1L103S, U5-ch61, adc5

calib_packv5_042523_0143.root, FC#7, port C2

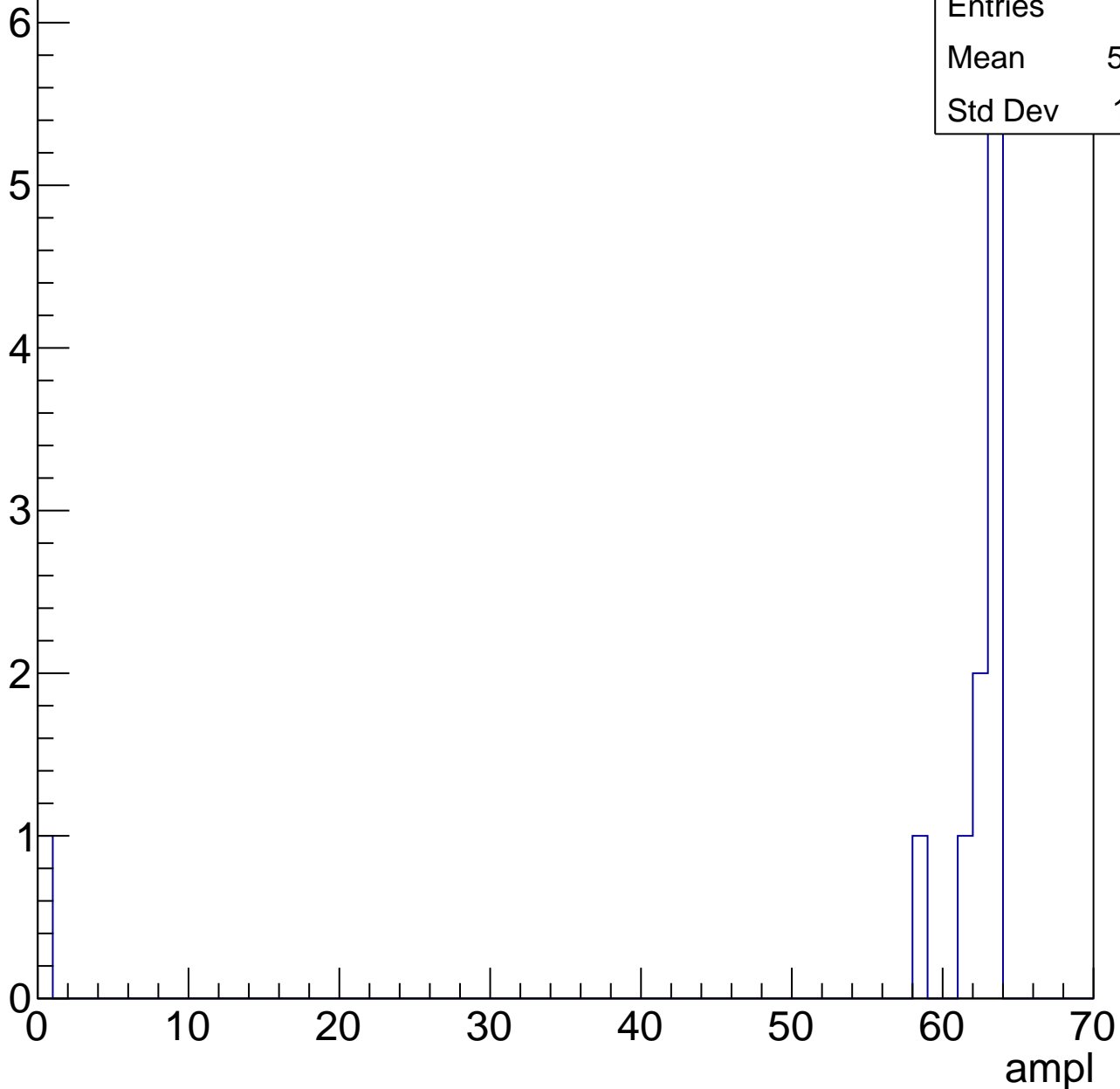


B1L103S, U5-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	56.45
Std Dev	17.91



B1L103S, U5-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch62, adc0

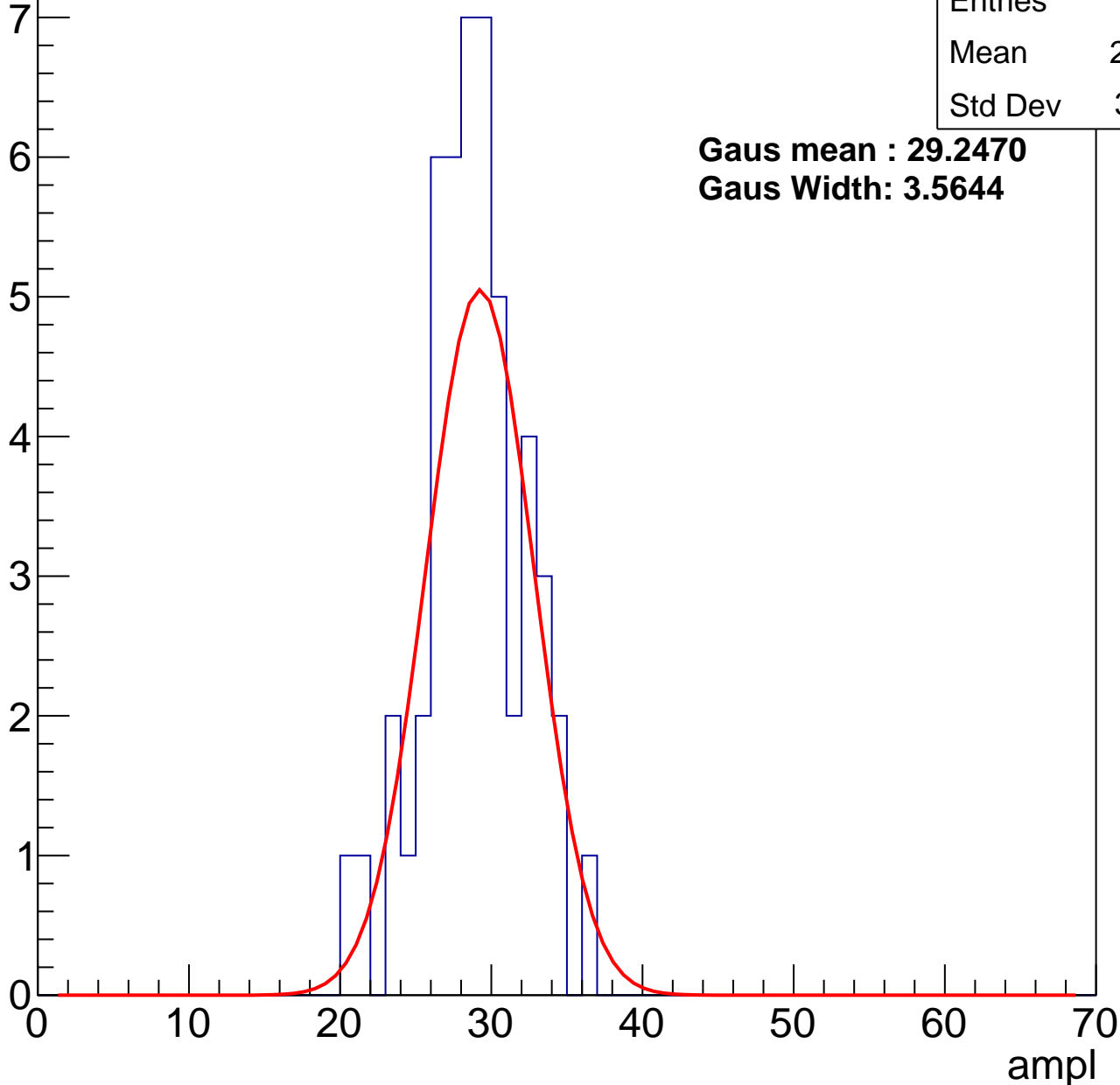
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	28.42
Std Dev	3.281

Gaus mean : 29.2470

Gaus Width: 3.5644



B1L103S, U5-ch62, adc1

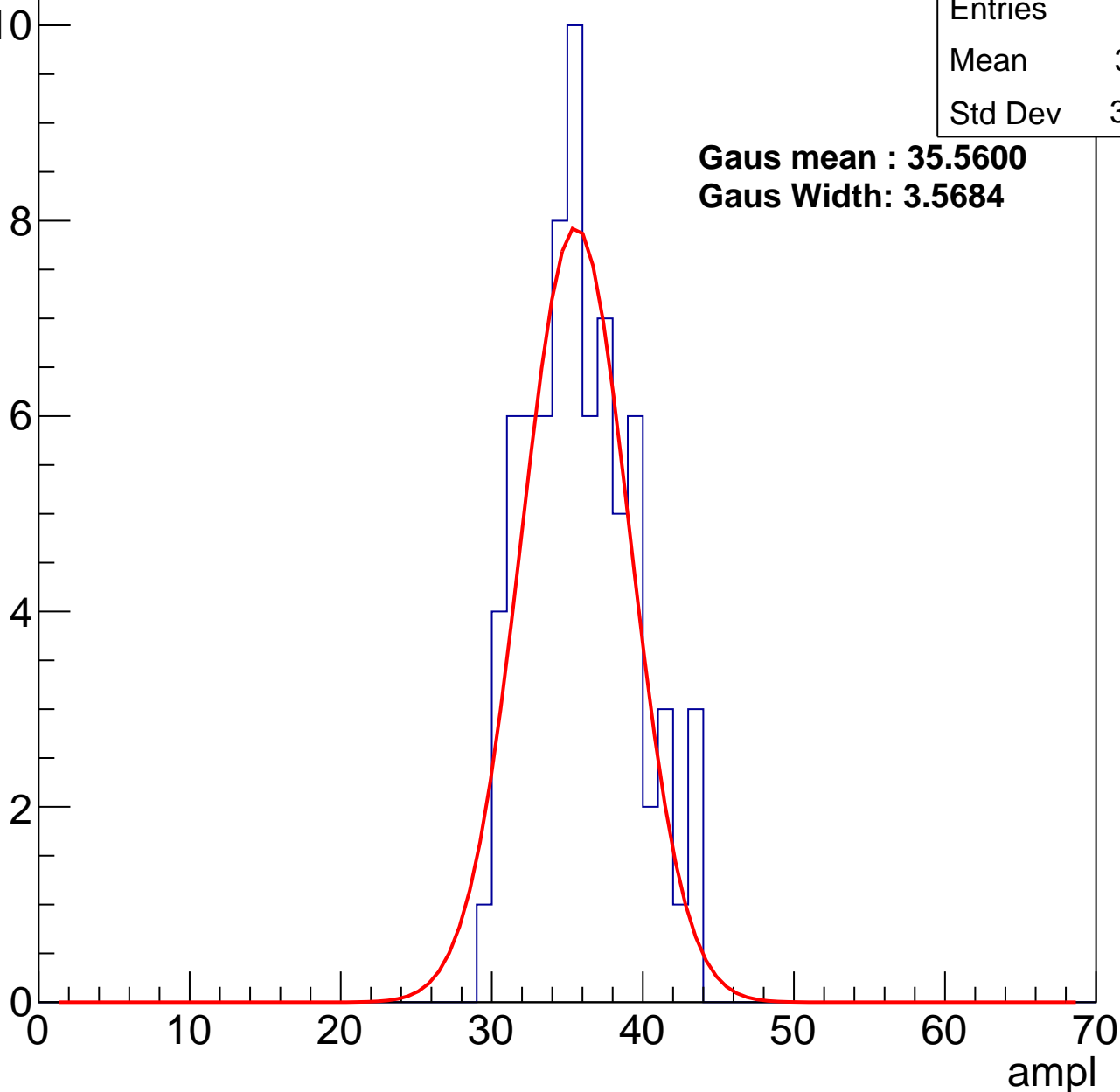
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.41
Std Dev	3.448

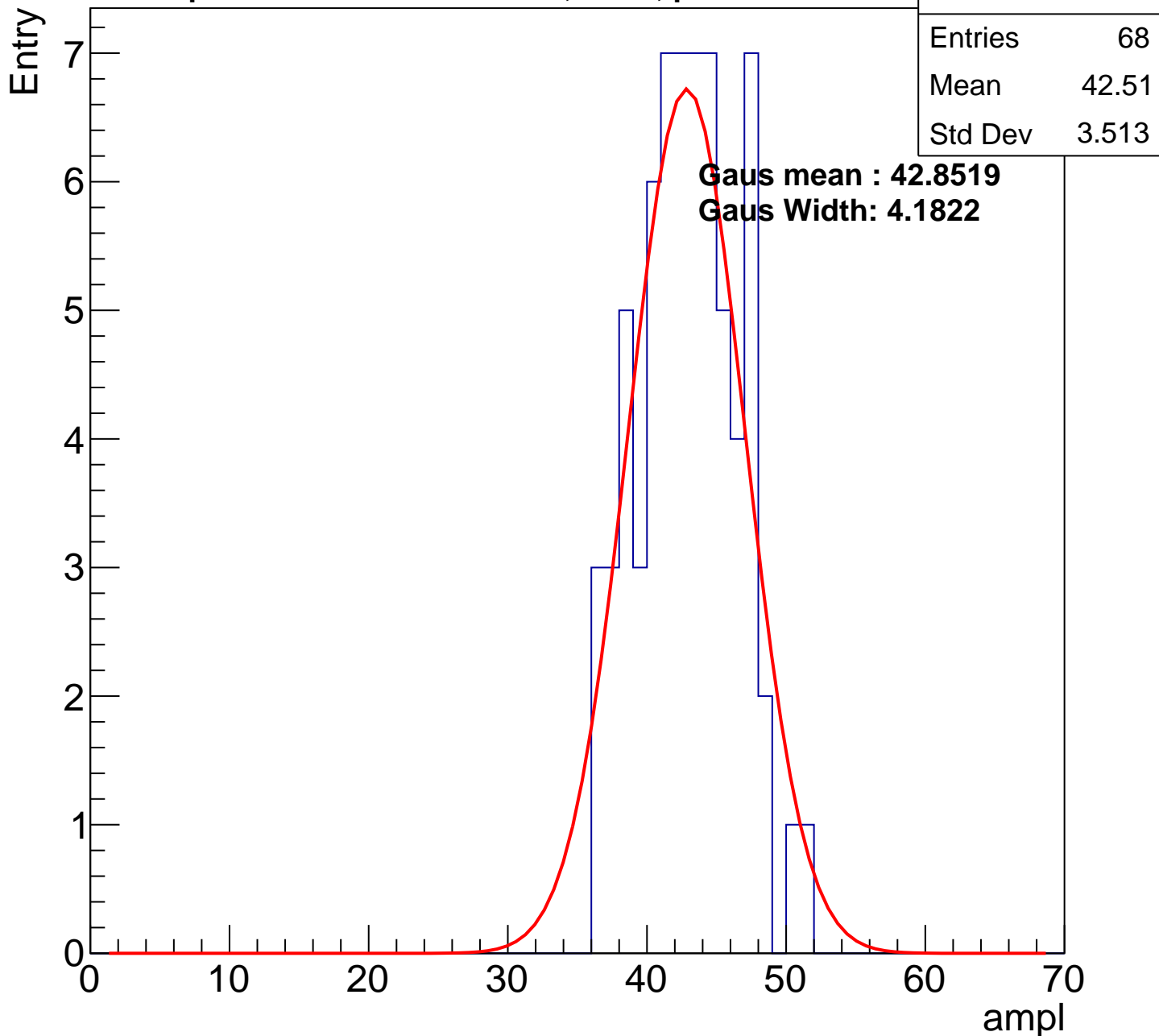
Gaus mean : 35.5600

Gaus Width: 3.5684



B1L103S, U5-ch62, adc2

calib_packv5_042523_0143.root, FC#7, port C2

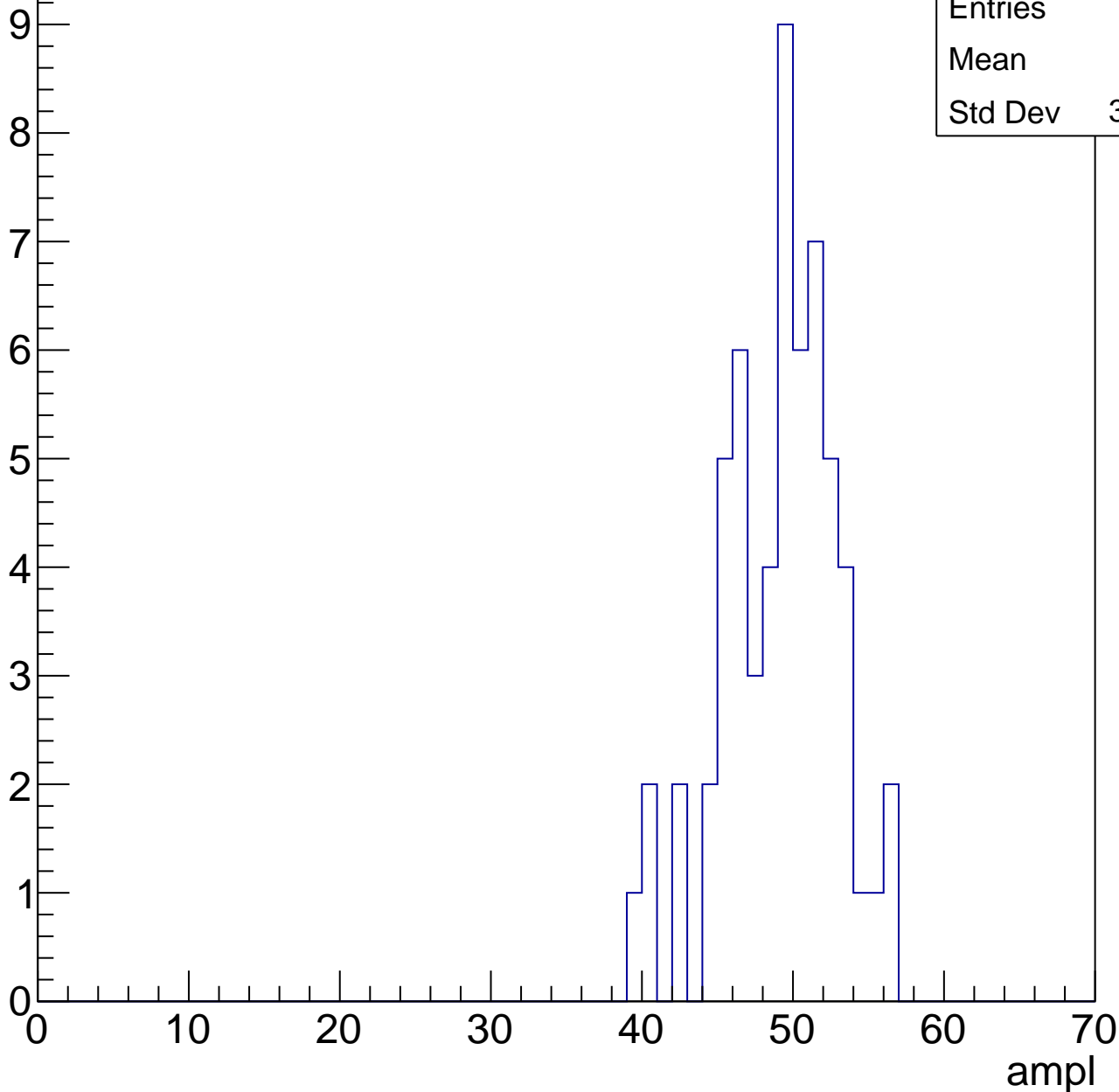


B1L103S, U5-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	48.6
Std Dev	3.769

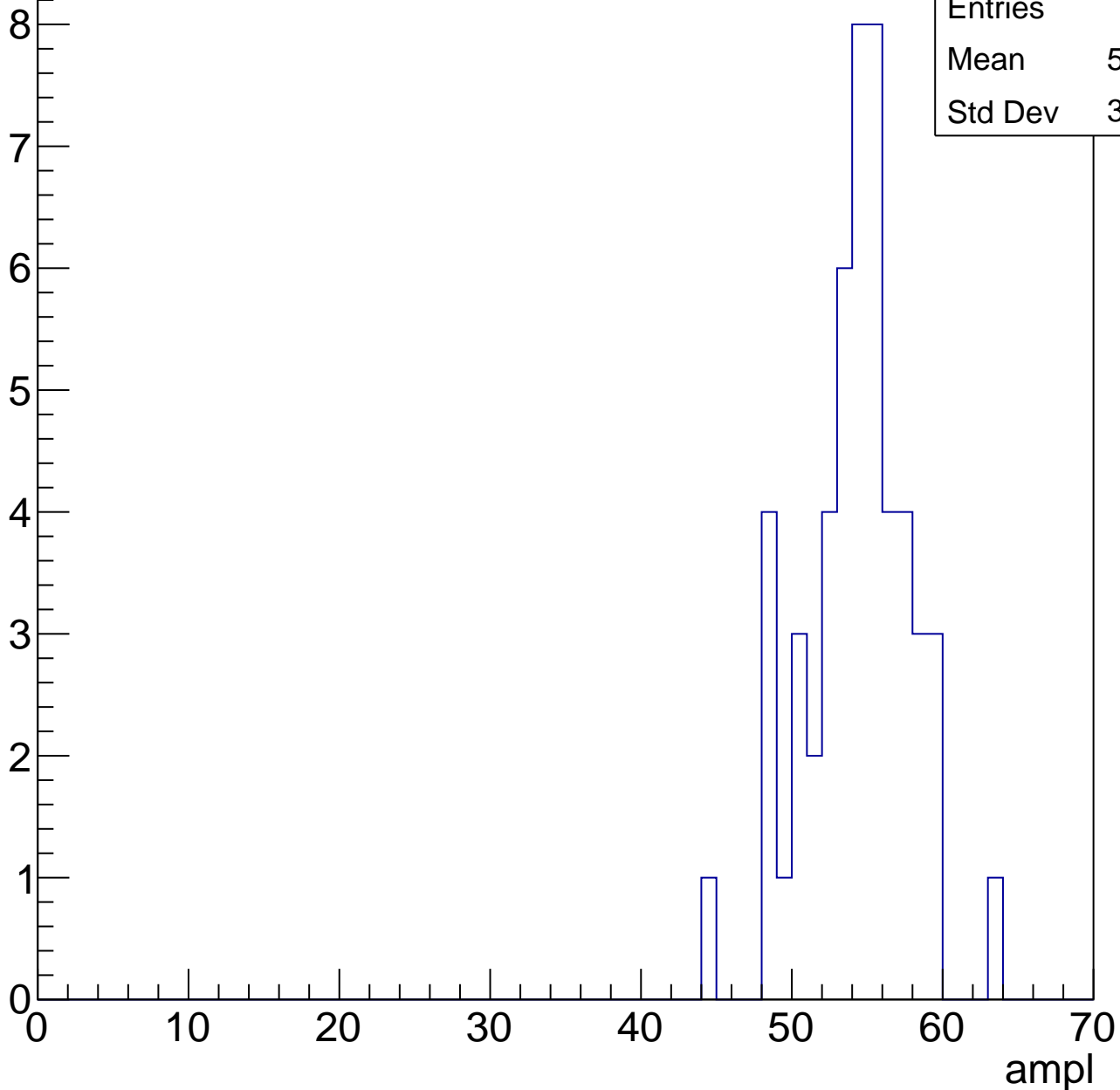


B1L103S, U5-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	53.87
Std Dev	3.453

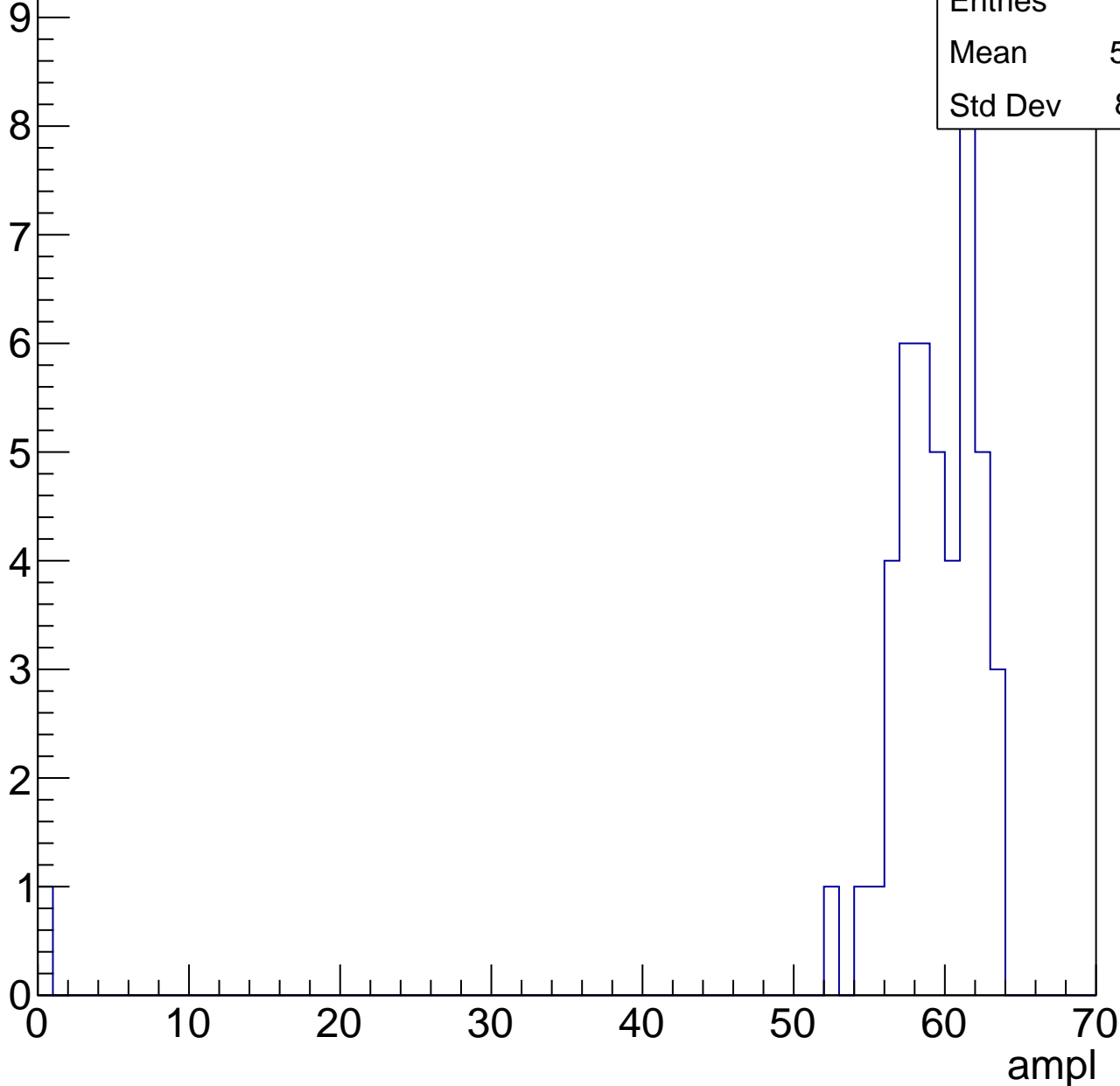


B1L103S, U5-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

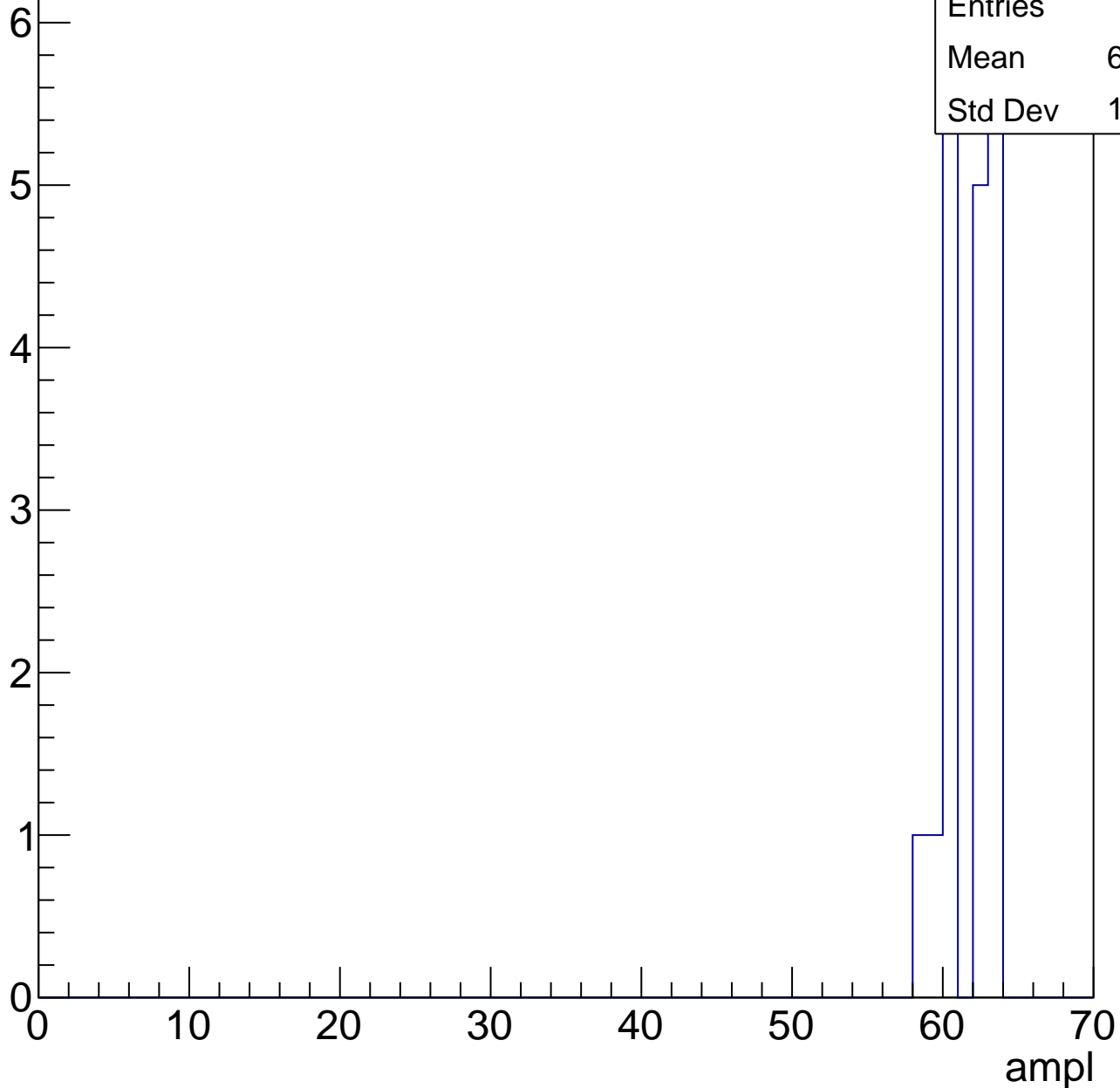
Entries	46
Mean	57.78
Std Dev	8.971



B1L103S, U5-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

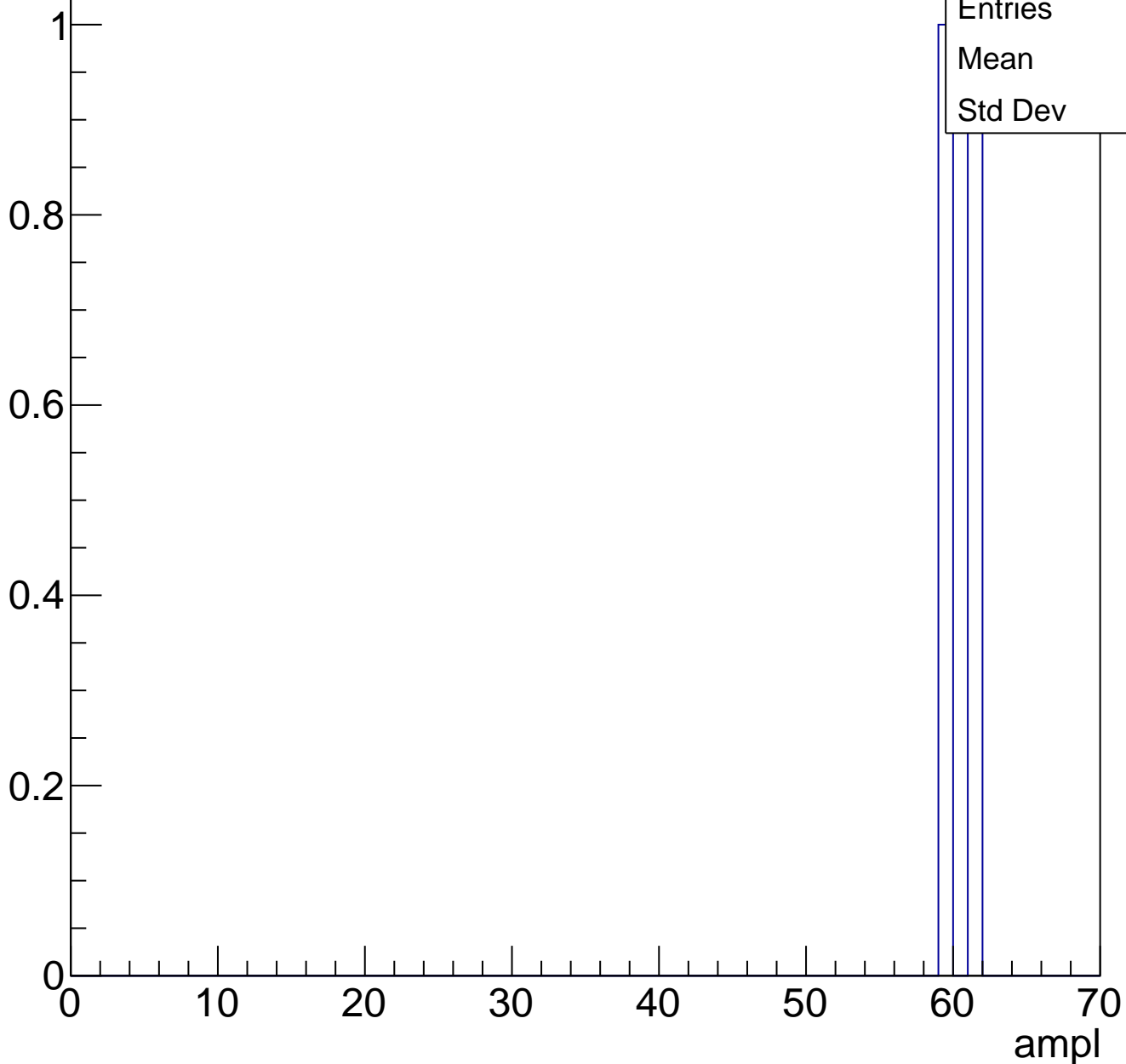
Entry



B1L103S, U5-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch63, adc0

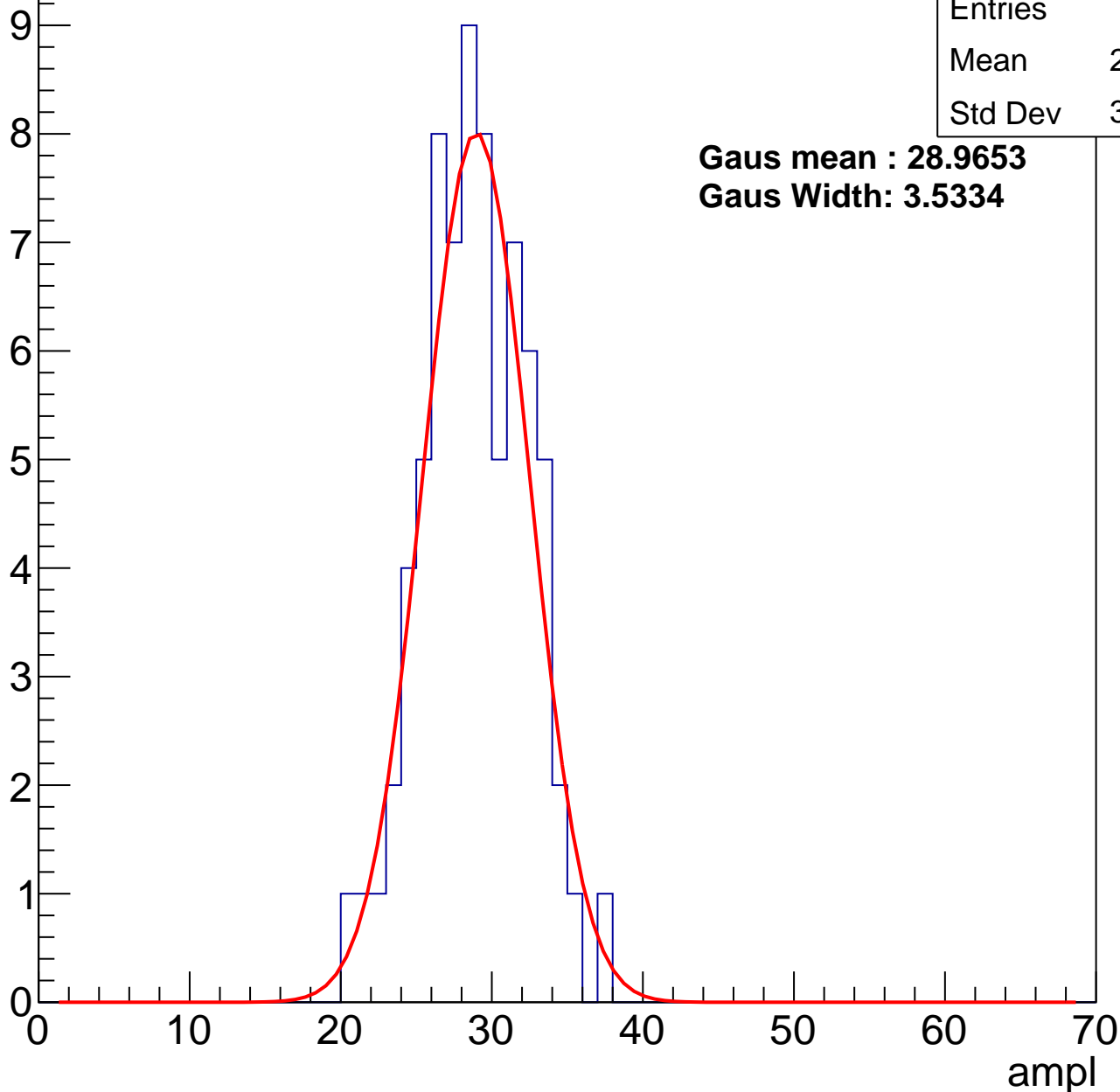
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.42
Std Dev	3.408

Gaus mean : 28.9653

Gaus Width: 3.5334

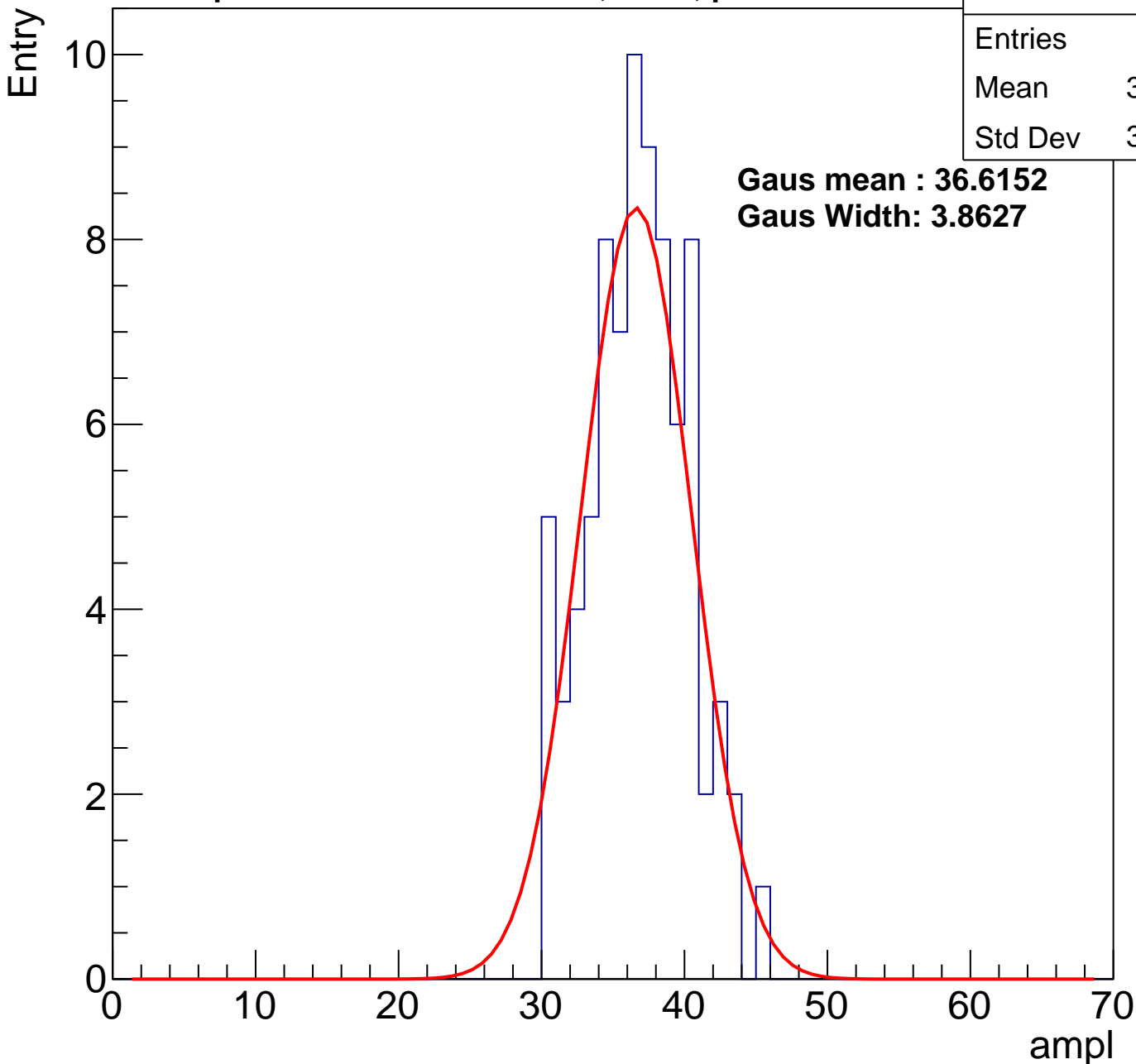


B1L103S, U5-ch63, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	36.33
Std Dev	3.432

Gaus mean : 36.6152
Gaus Width: 3.8627



B1L103S, U5-ch63, adc2

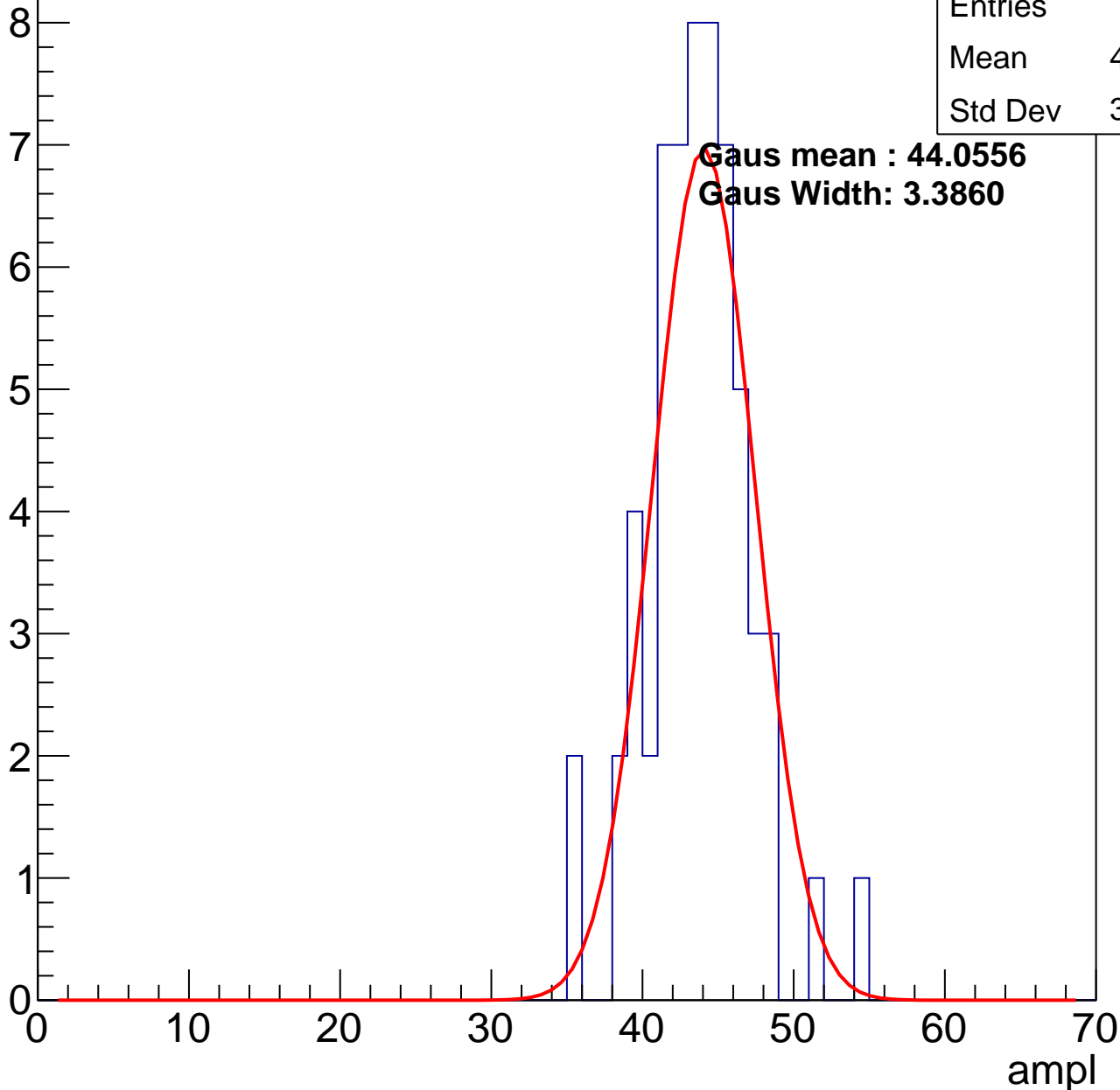
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.23
Std Dev	3.368

Gaus mean : 44.0556

Gaus Width: 3.3860

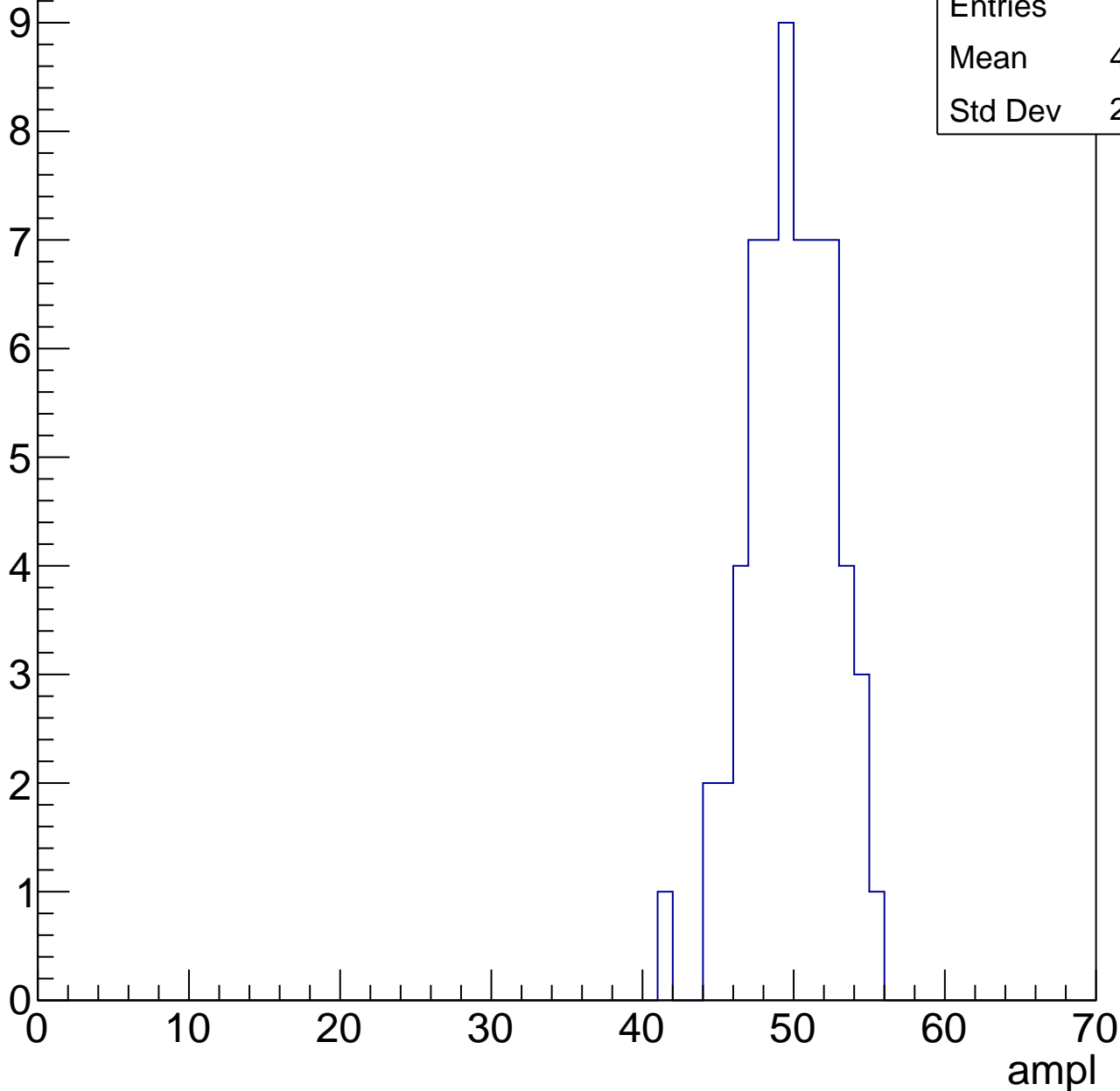


B1L103S, U5-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.33
Std Dev	2.815



B1L103S, U5-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

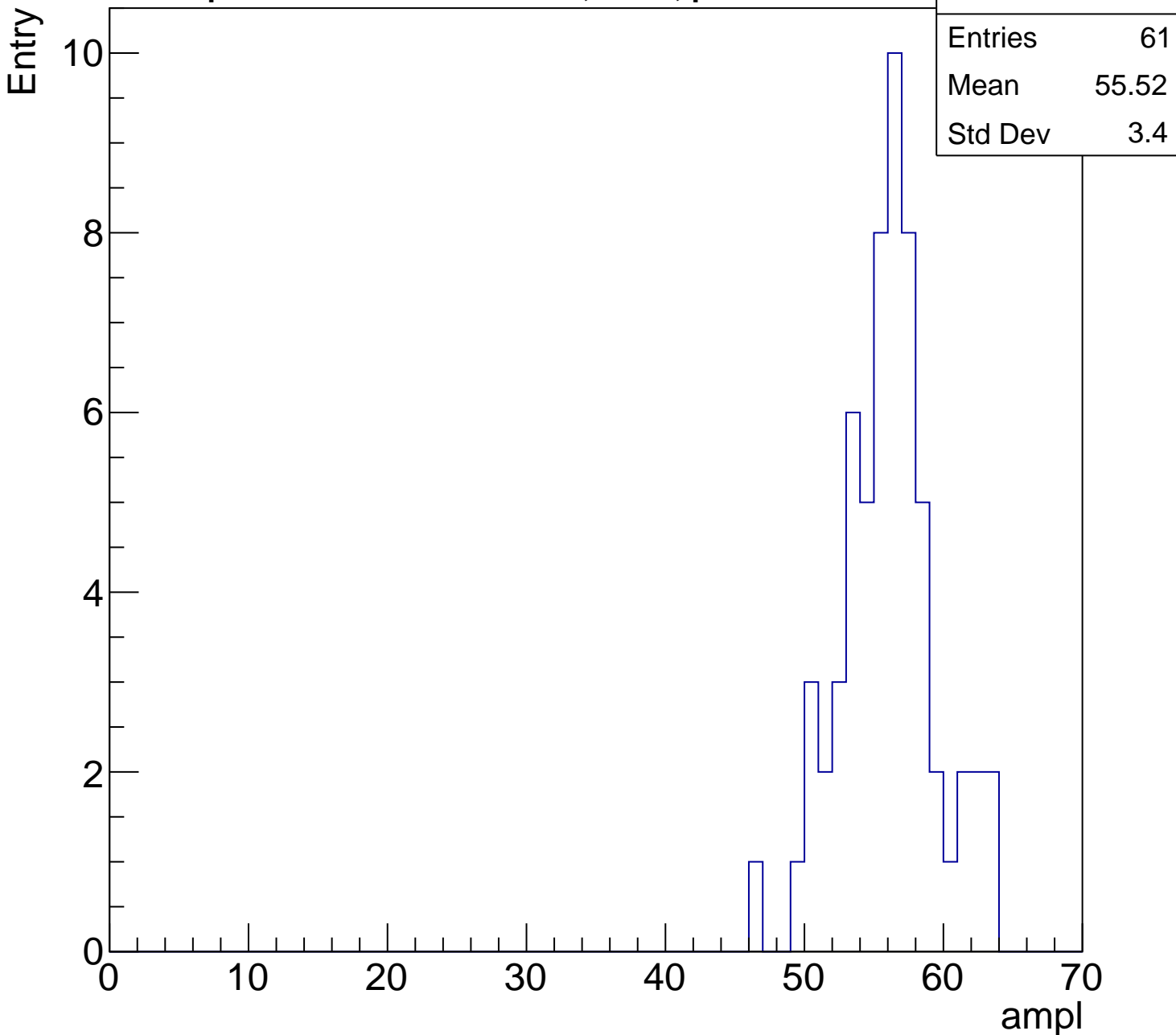
Entries	61
Mean	55.52
Std Dev	3.4

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

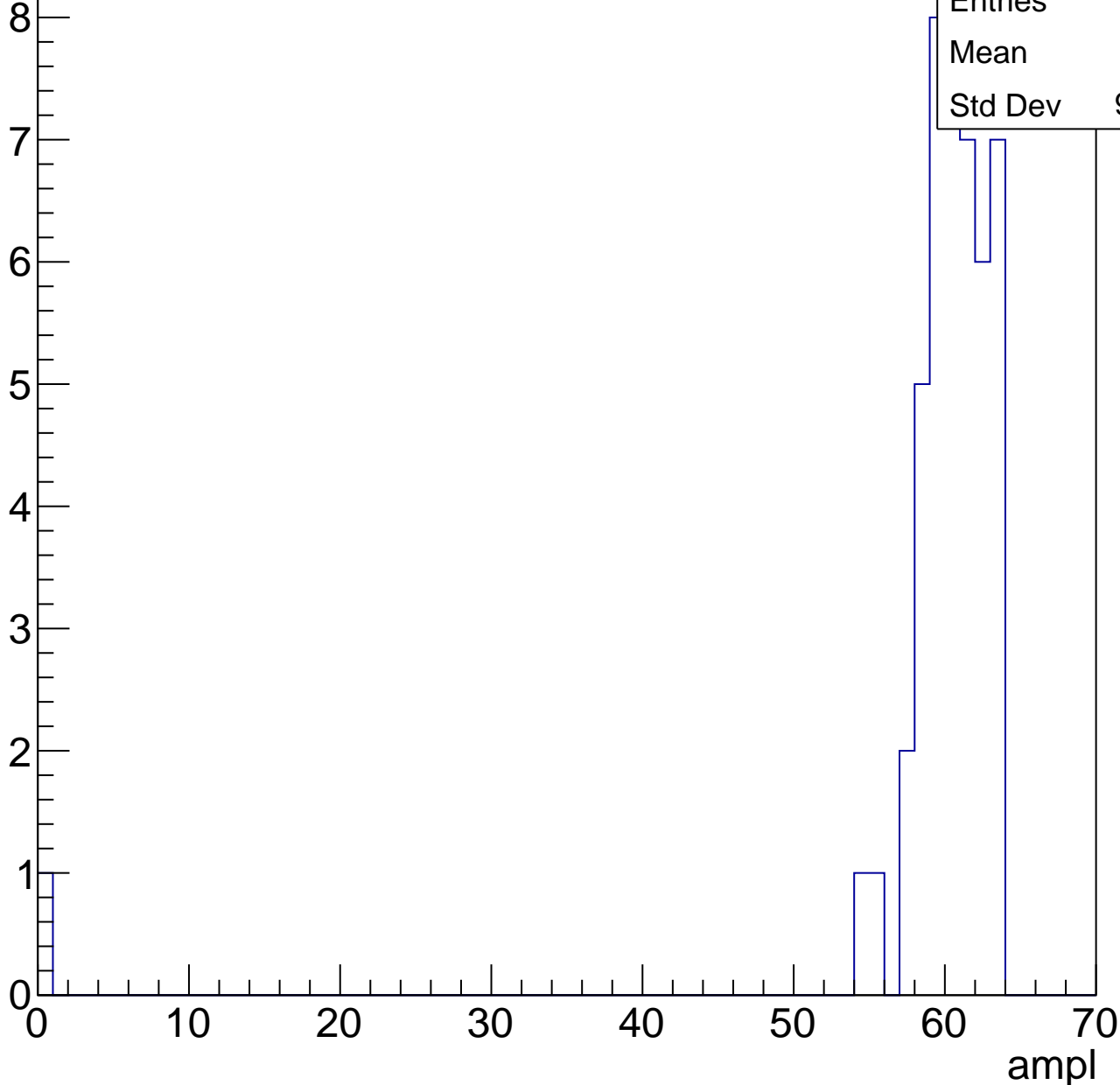


B1L103S, U5-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.8
Std Dev	9.011



B1L103S, U5-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch64, adc0

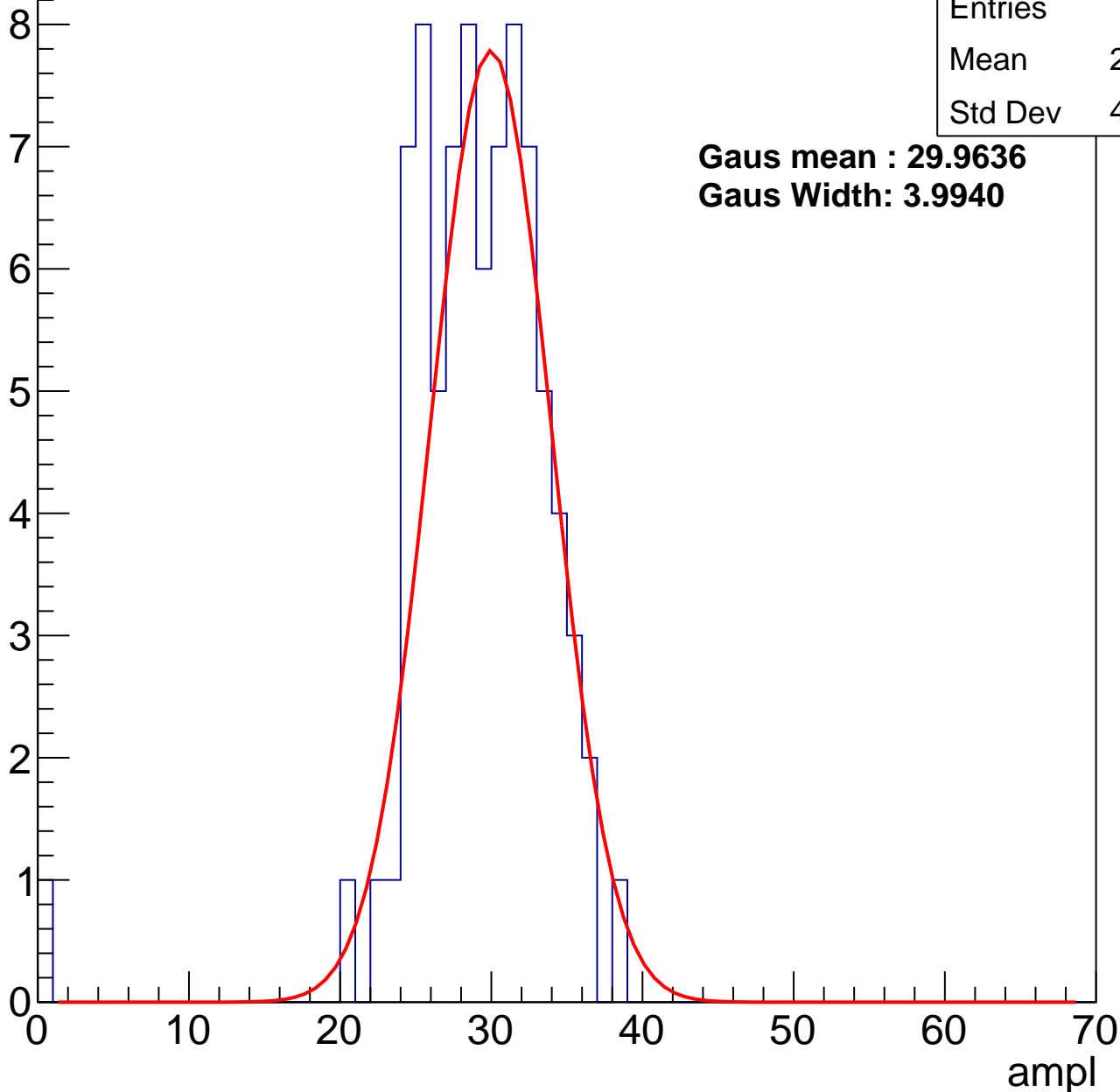
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	28.63
Std Dev	4.875

Gaus mean : 29.9636

Gaus Width: 3.9940



B1L103S, U5-ch64, adc1

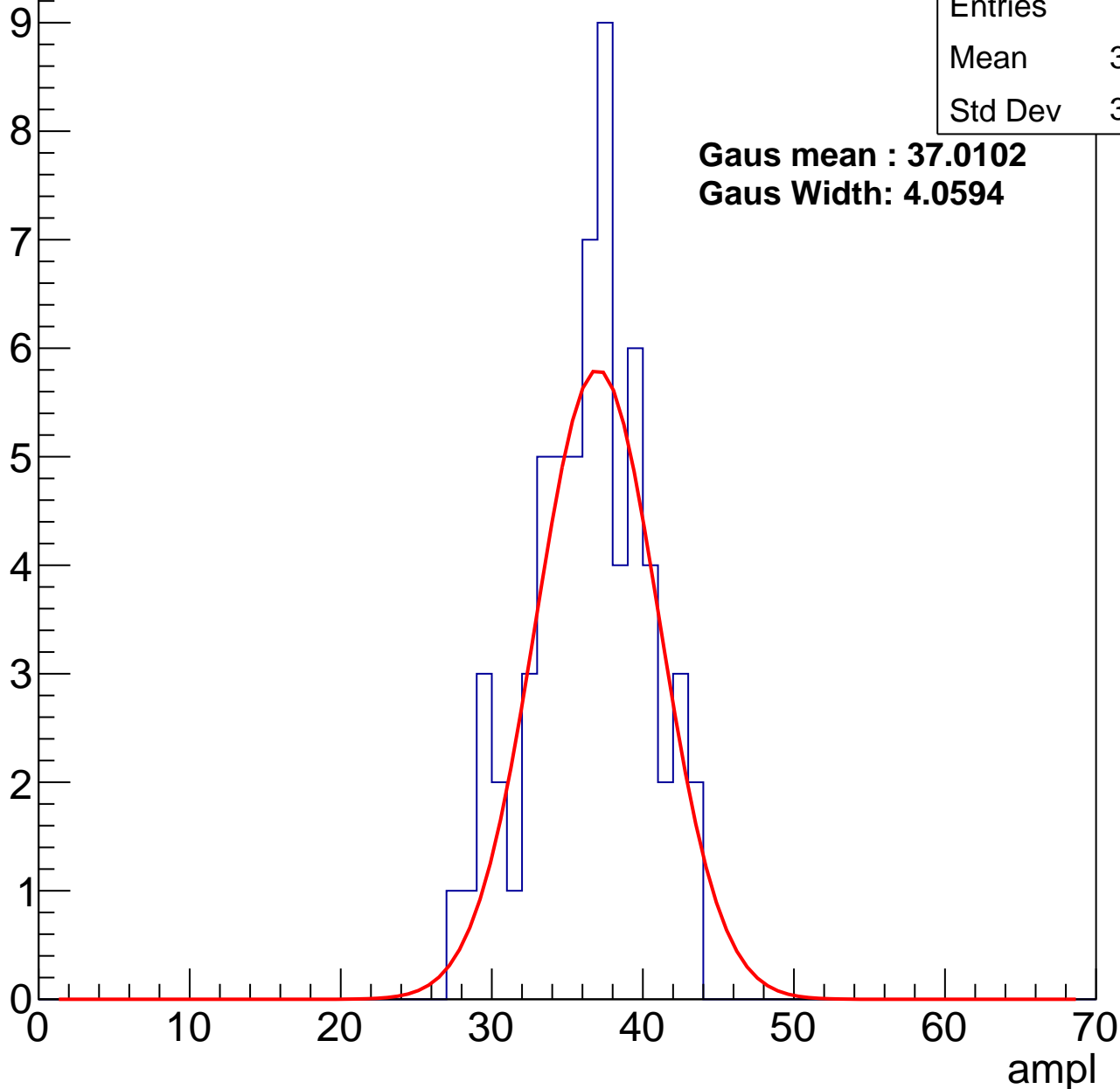
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.94
Std Dev	3.792

Gaus mean : 37.0102

Gaus Width: 4.0594



B1L103S, U5-ch64, adc2

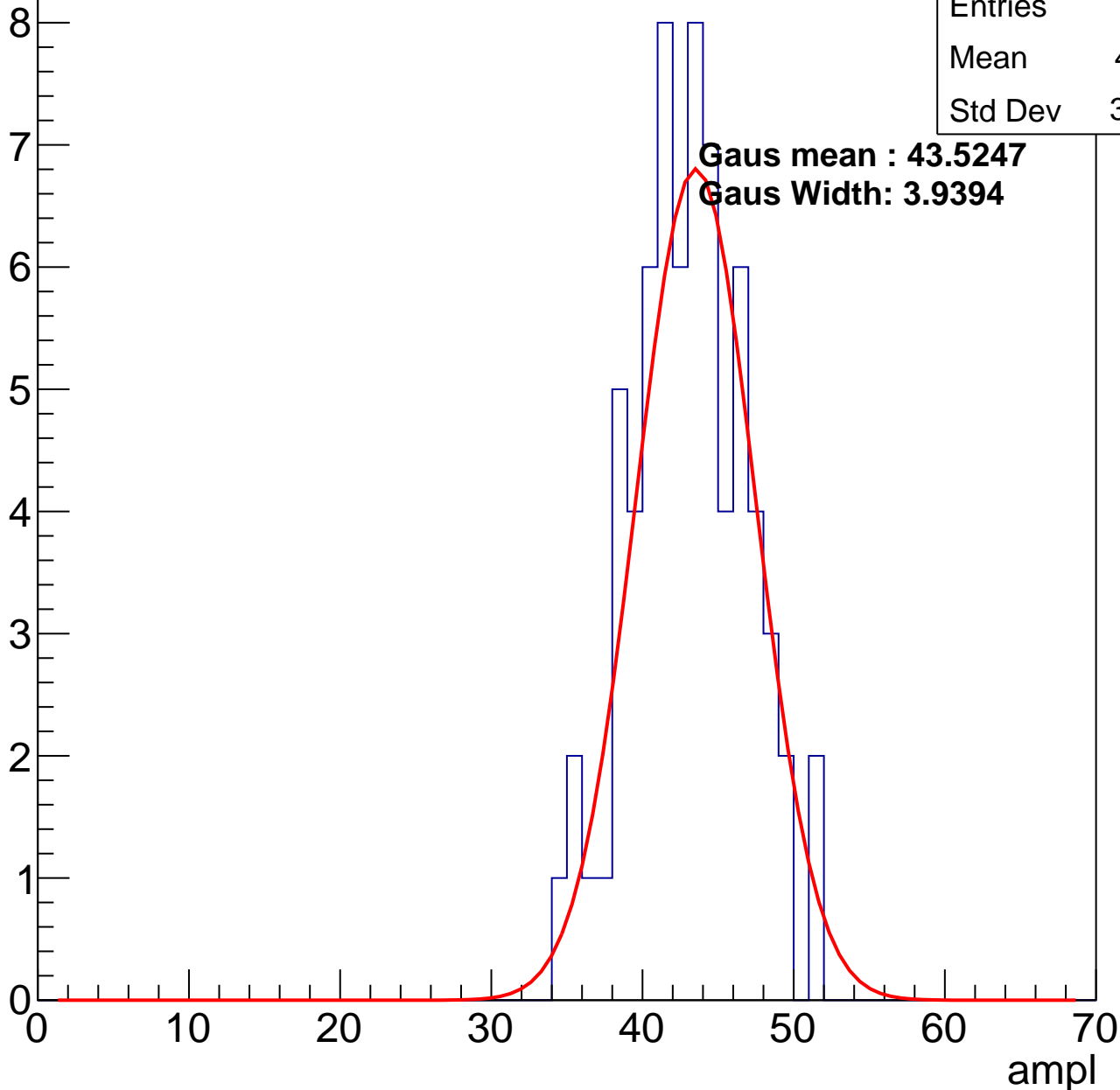
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.61
Std Dev	3.743

Gaus mean : 43.5247

Gaus Width: 3.9394

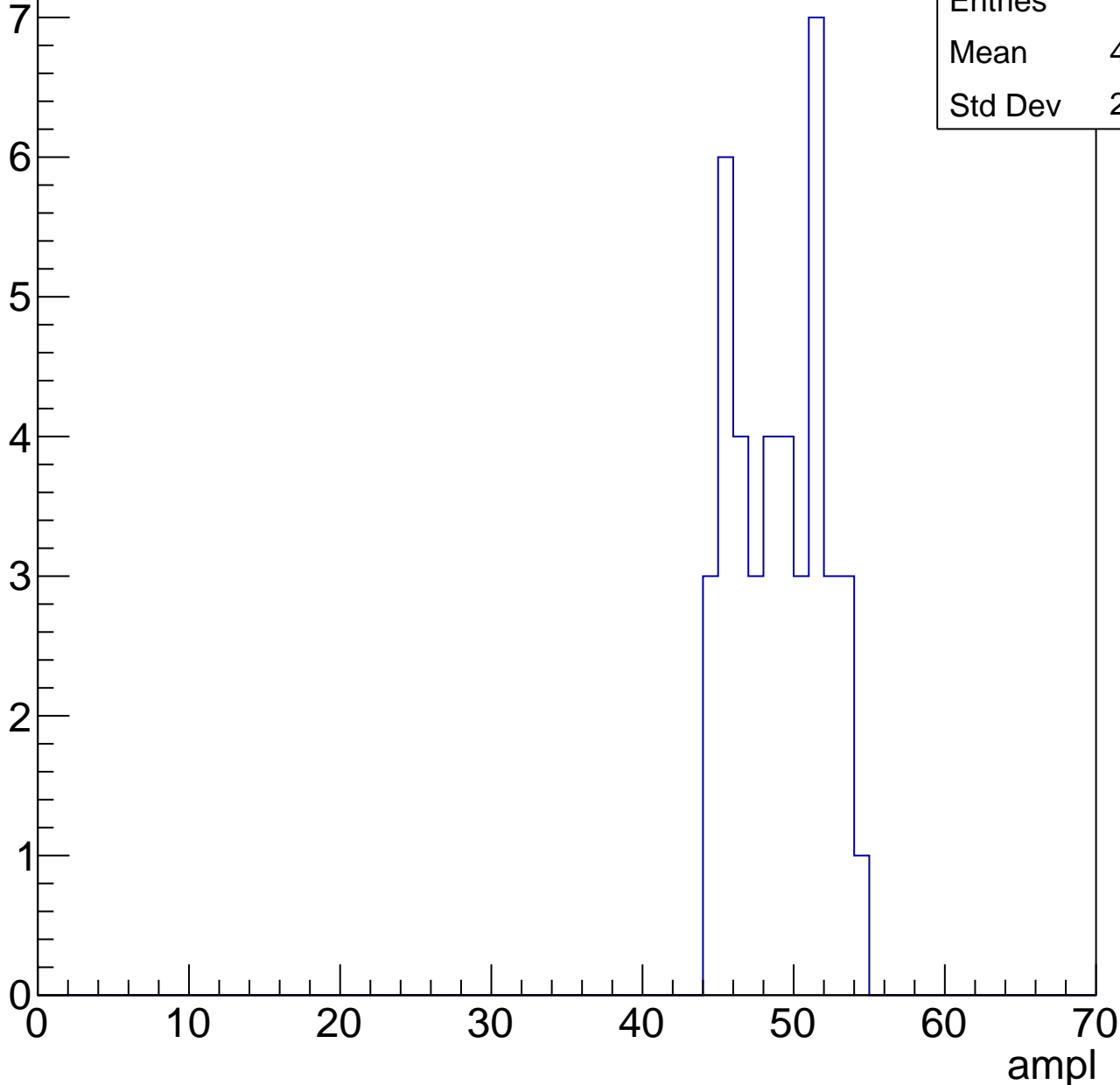


B1L103S, U5-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

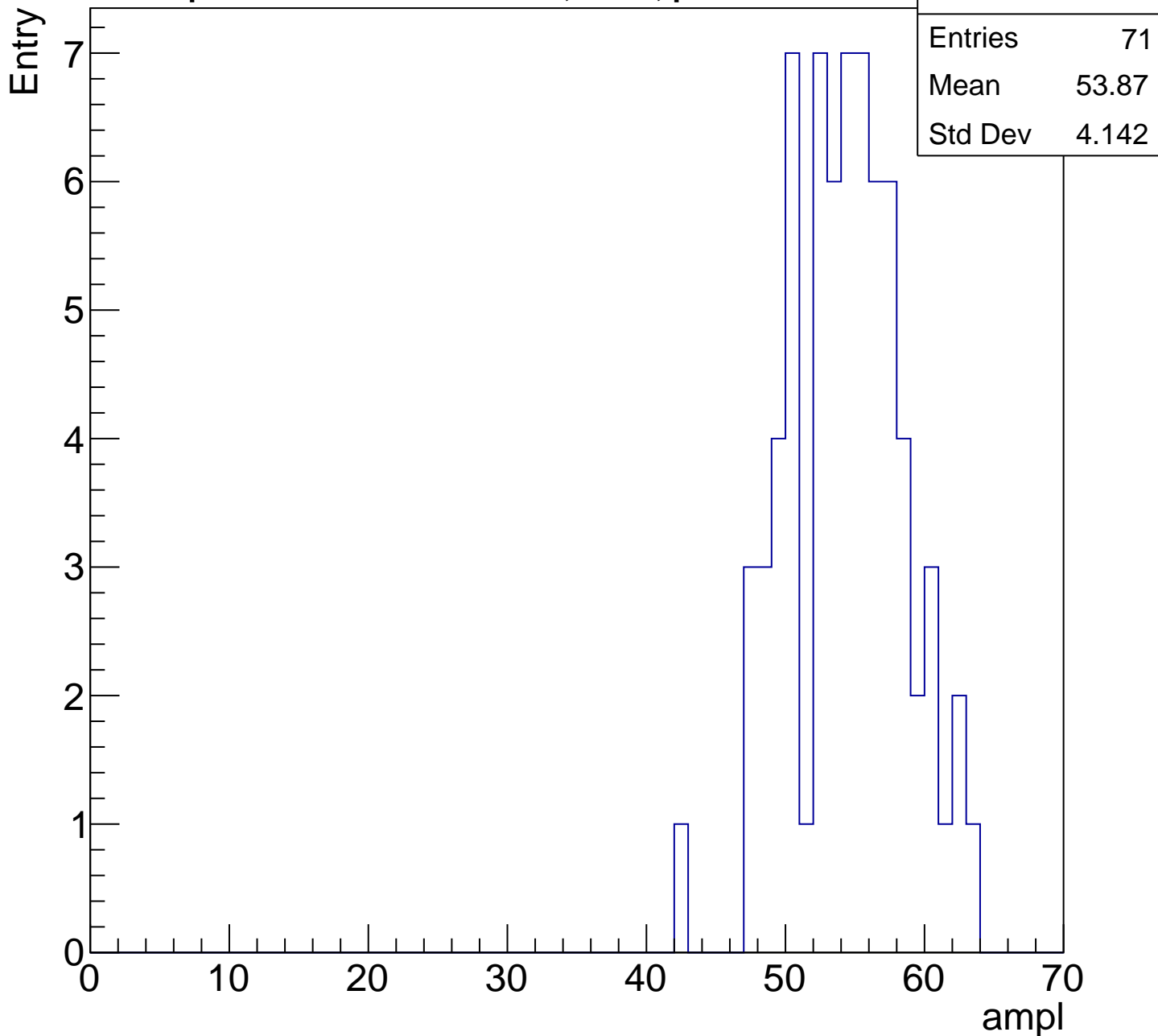
Entry

Entries	41
Mean	48.56
Std Dev	2.905



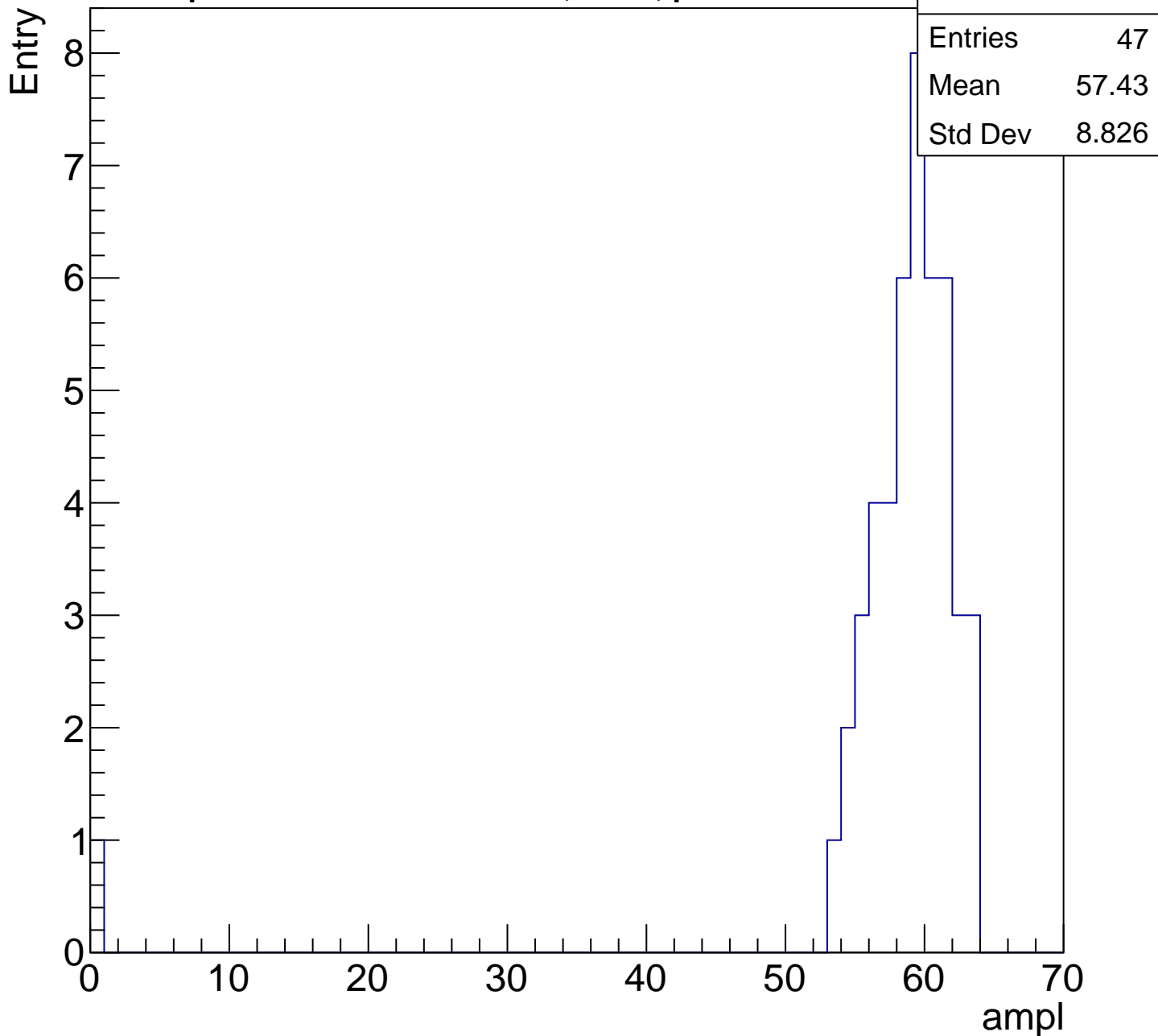
B1L103S, U5-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

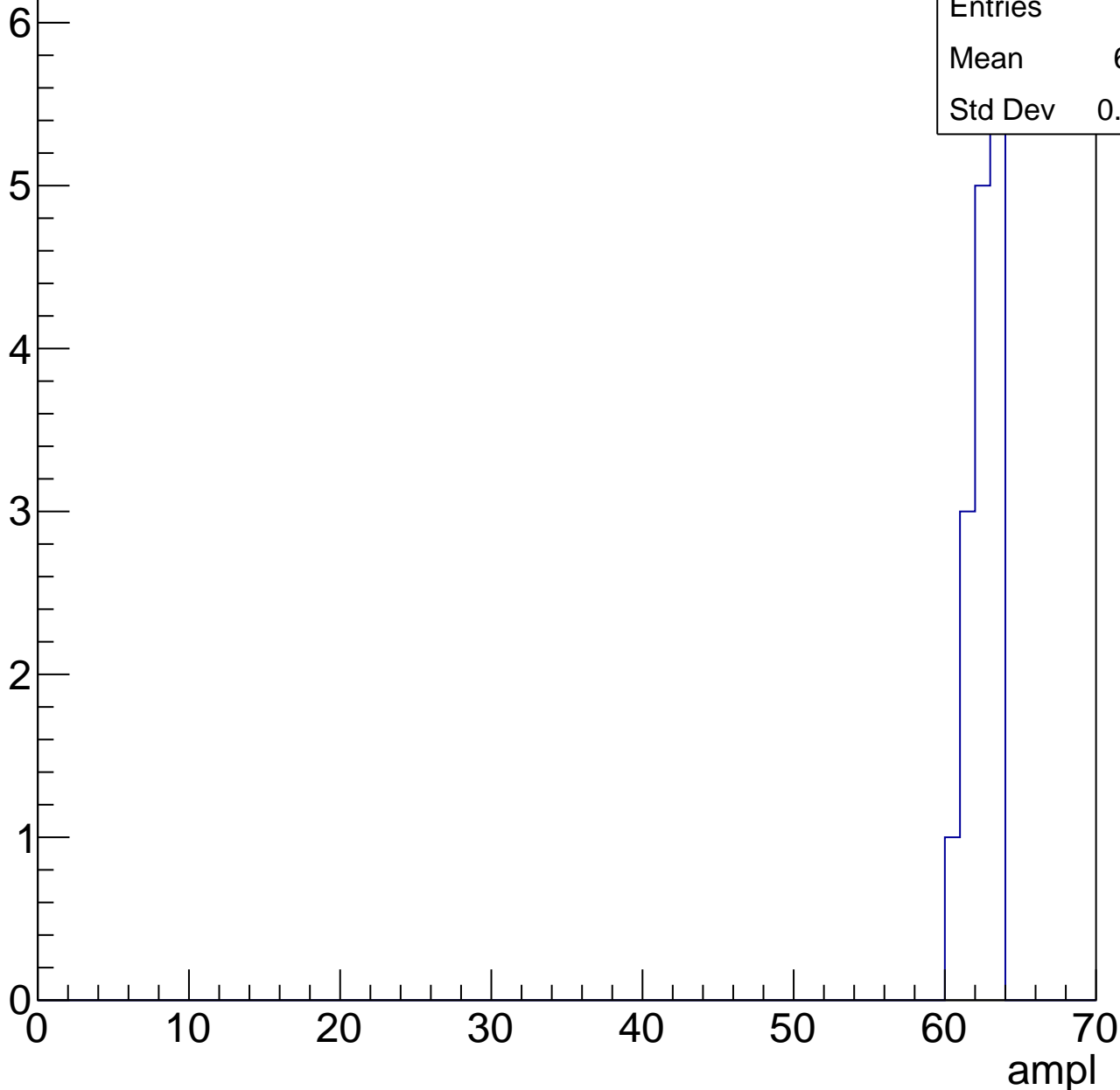


B1L103S, U5-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	62.07
Std Dev	0.9286



B1L103S, U5-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch65, adc0

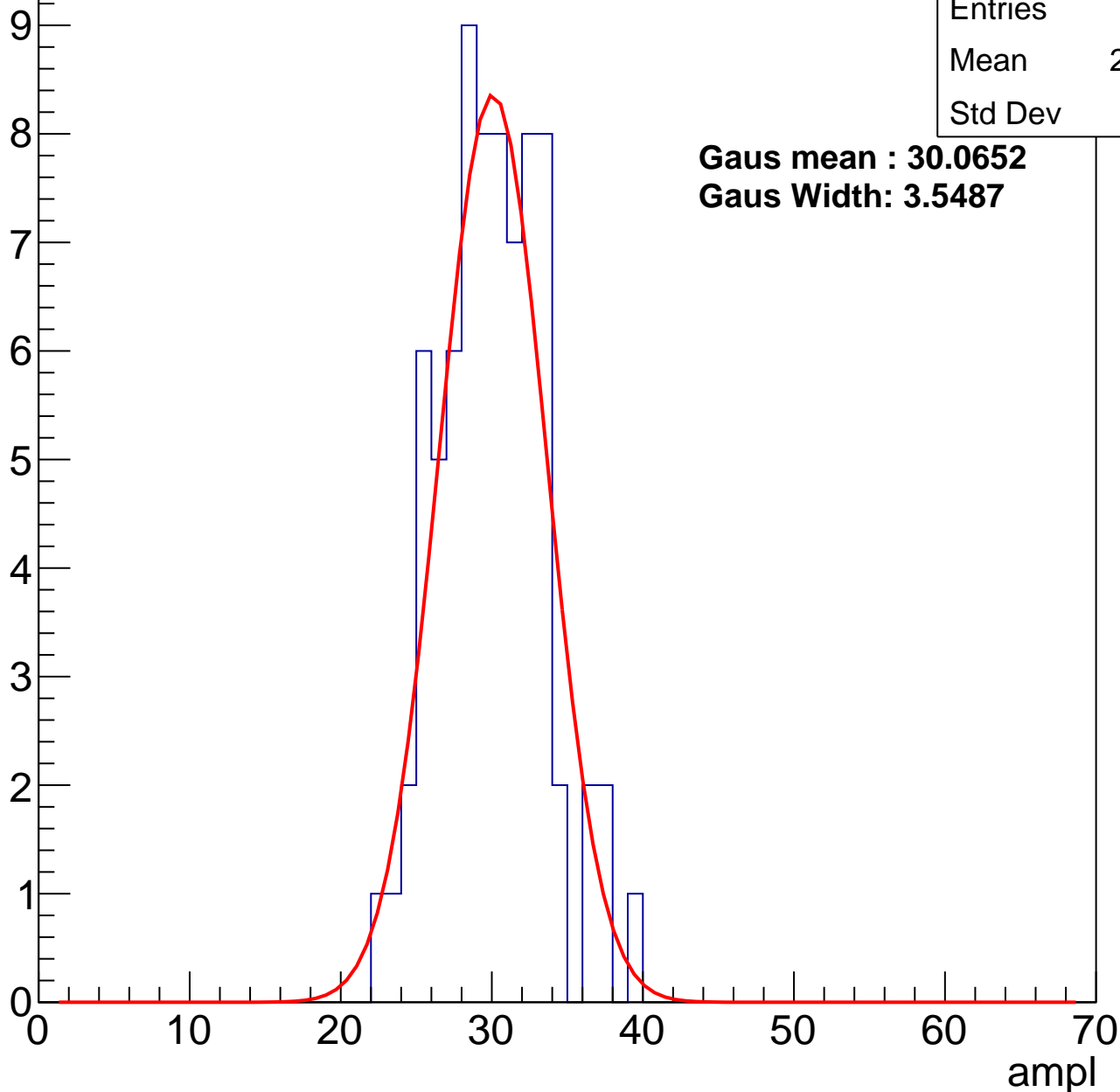
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	29.59
Std Dev	3.43

Gaus mean : 30.0652

Gaus Width: 3.5487



B1L103S, U5-ch65, adc1

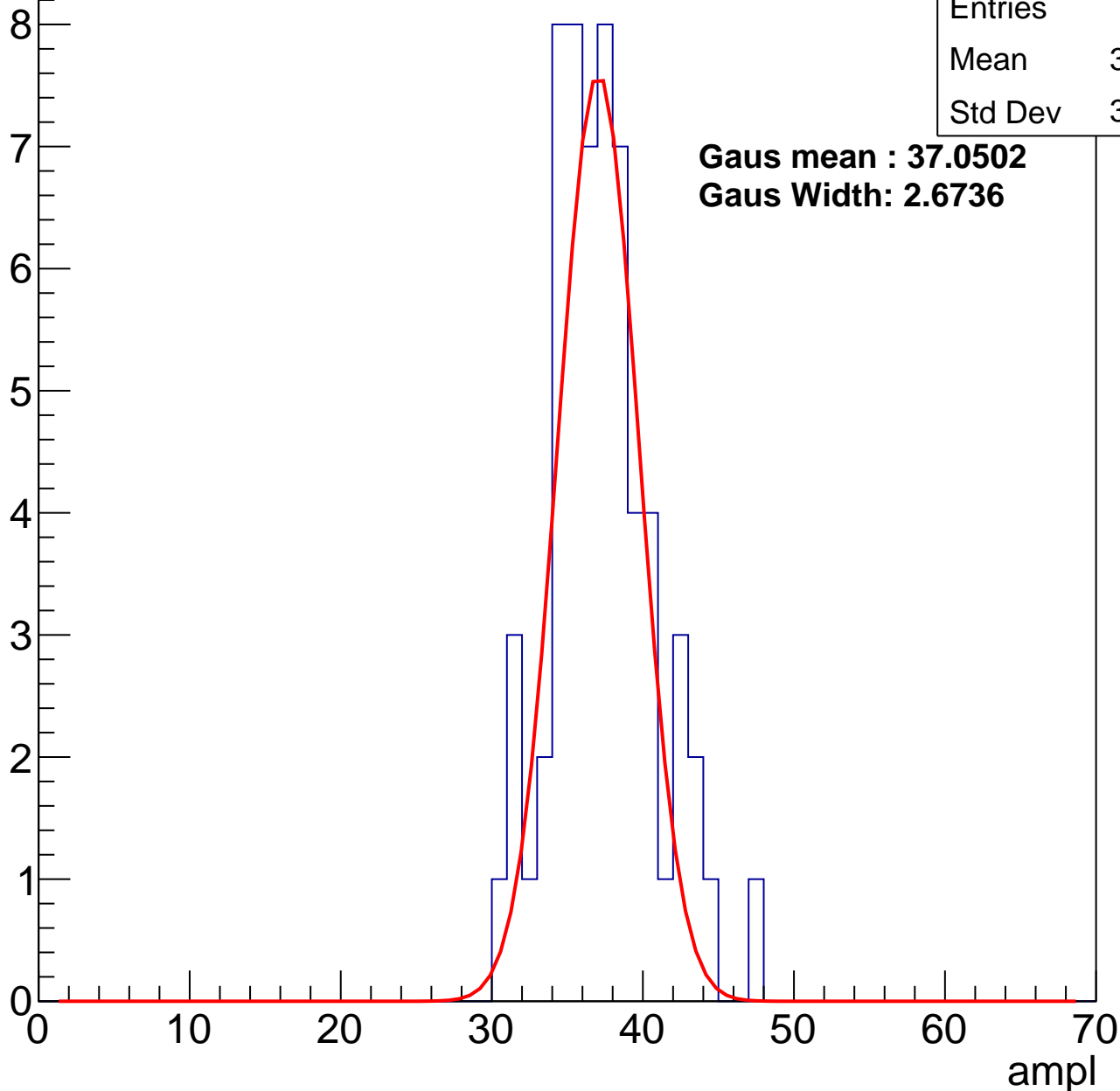
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	36.84
Std Dev	3.374

Gaus mean : 37.0502

Gaus Width: 2.6736



B1L103S, U5-ch65, adc2

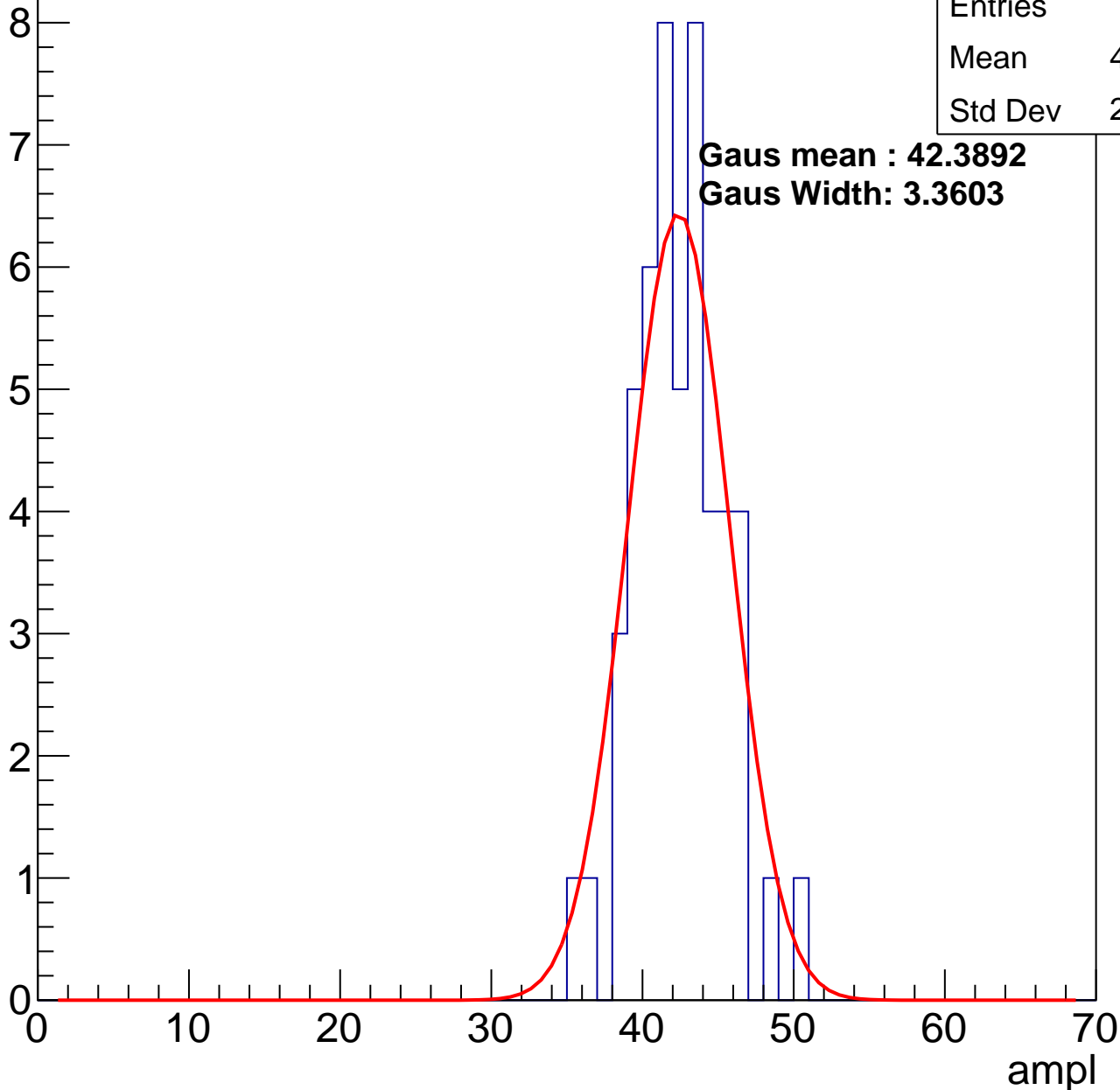
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.96
Std Dev	2.917

Gaus mean : 42.3892

Gaus Width: 3.3603

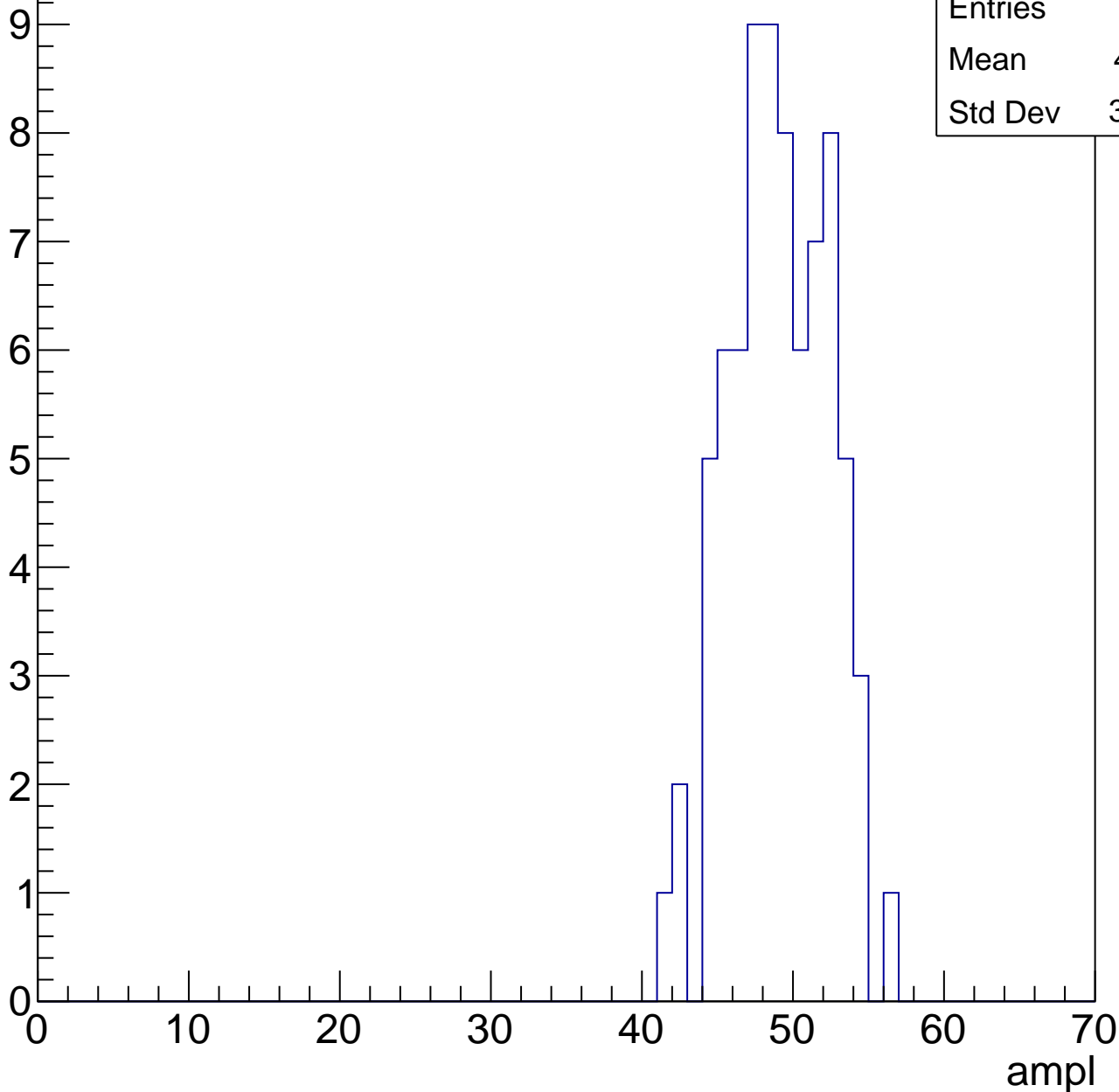


B1L103S, U5-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

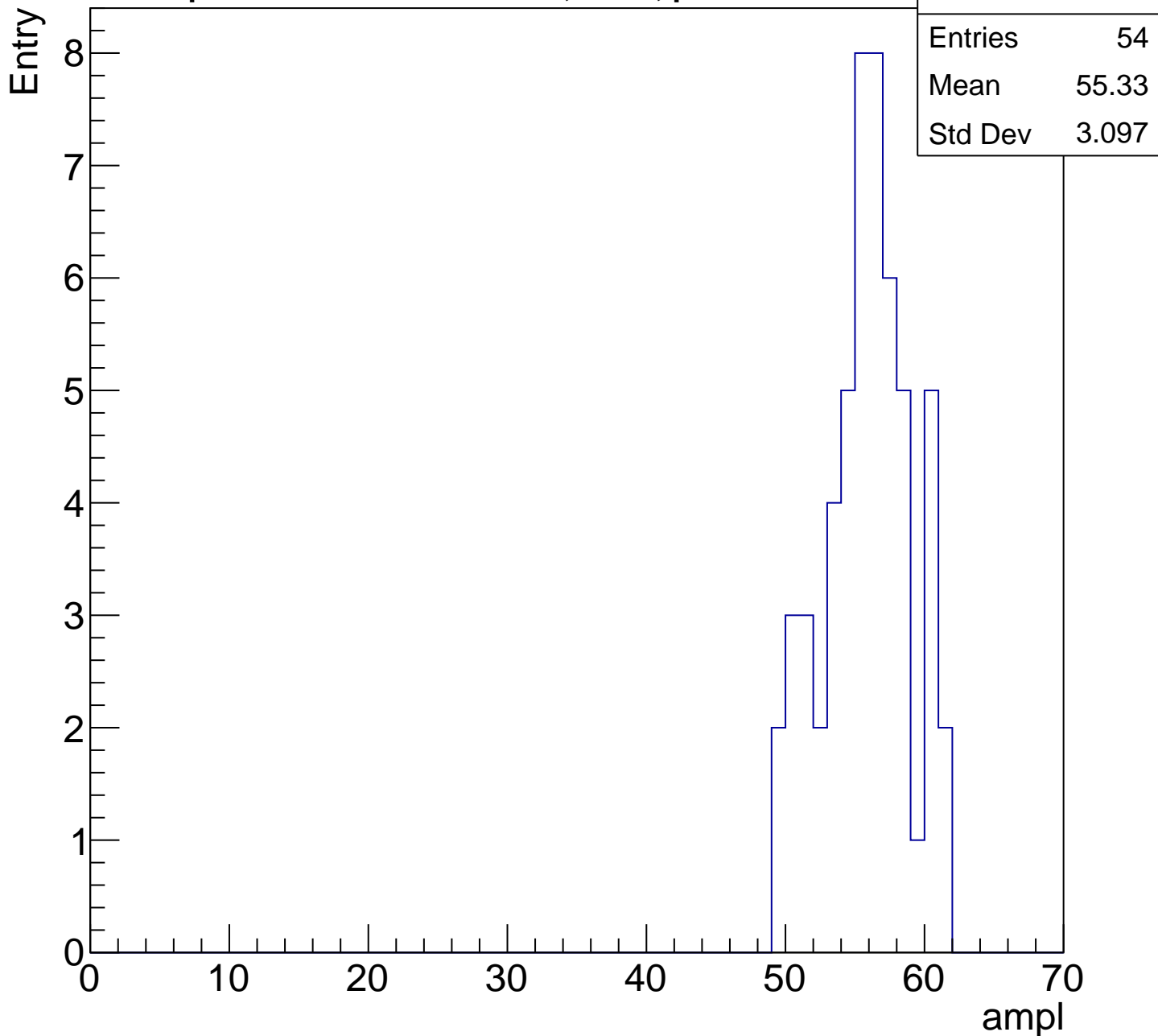
Entry

Entries	76
Mean	48.61
Std Dev	3.204



B1L103S, U5-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

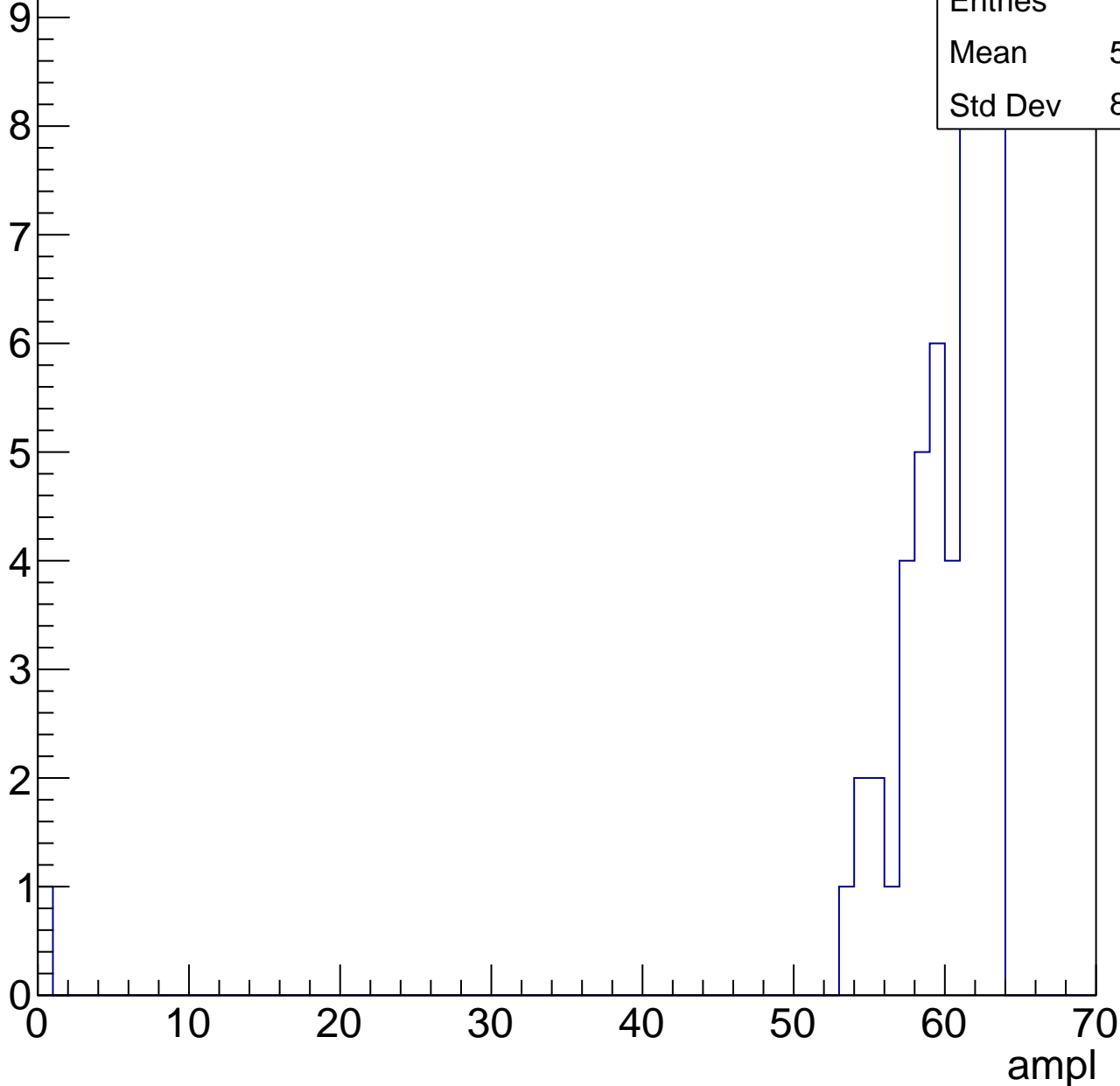


B1L103S, U5-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

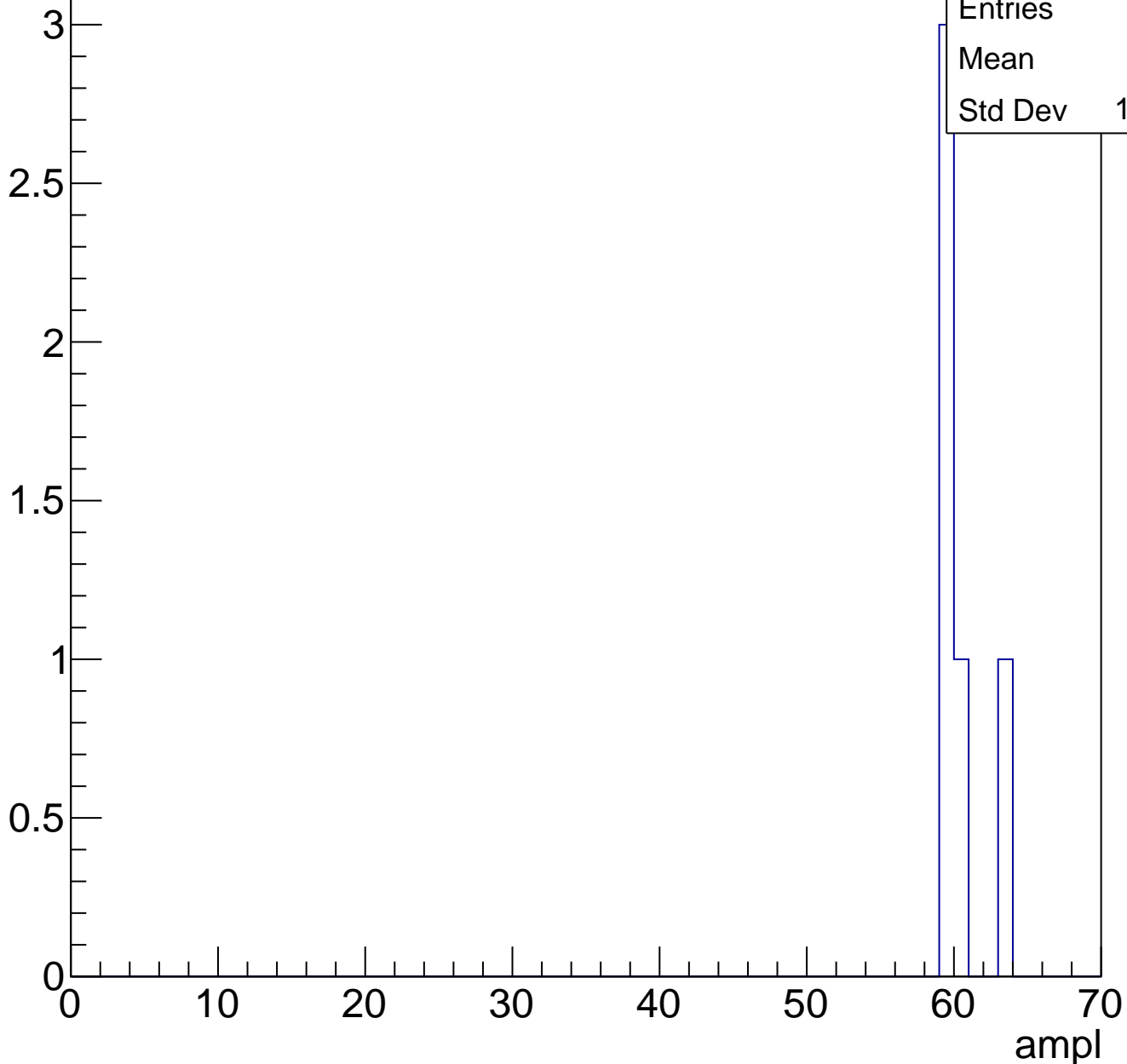
Entries	52
Mean	58.69
Std Dev	8.644



B1L103S, U5-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

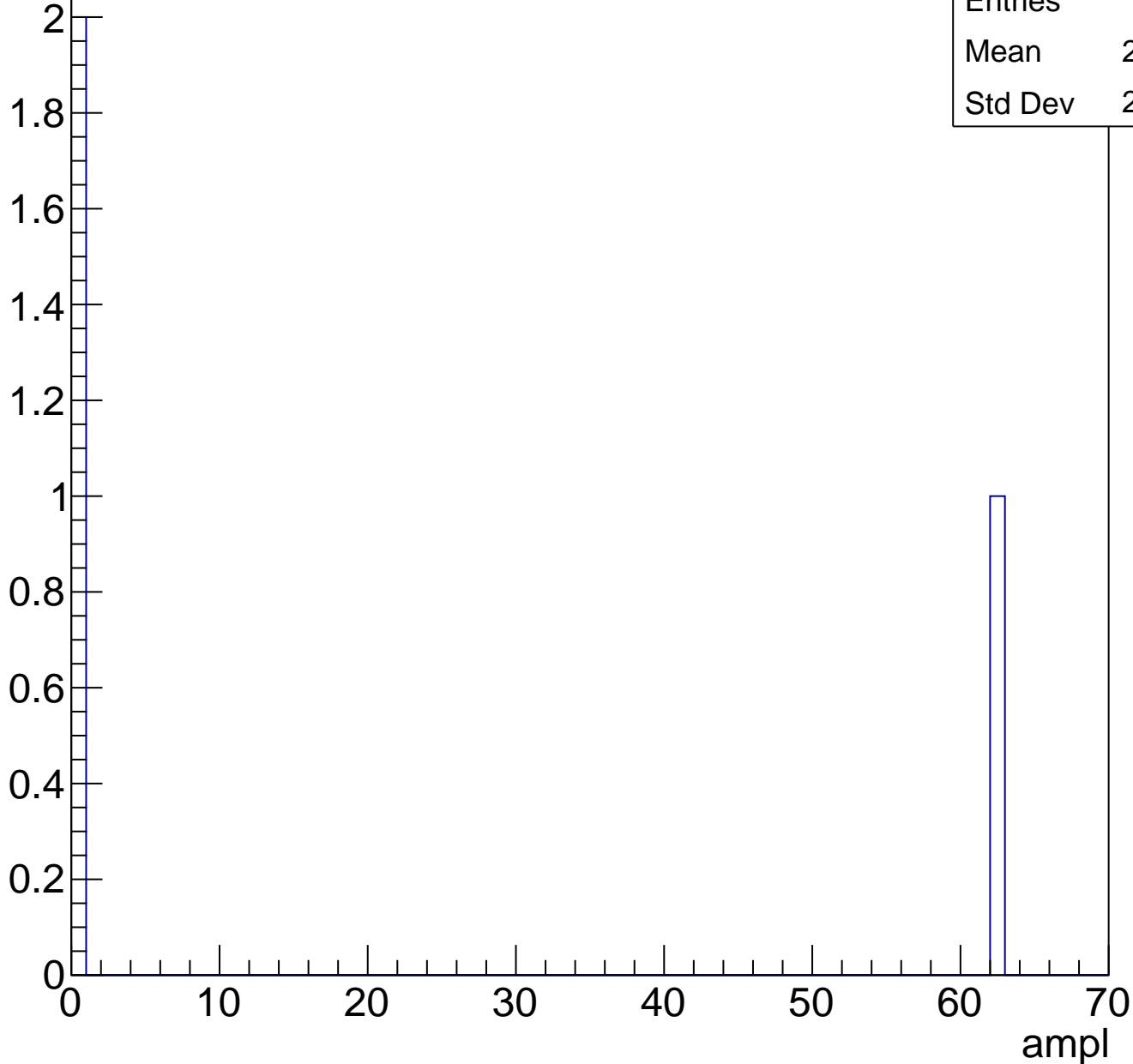
Entry



B1L103S, U5-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch66, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	26.71
Std Dev	5.038

Gaus mean : 27.6066

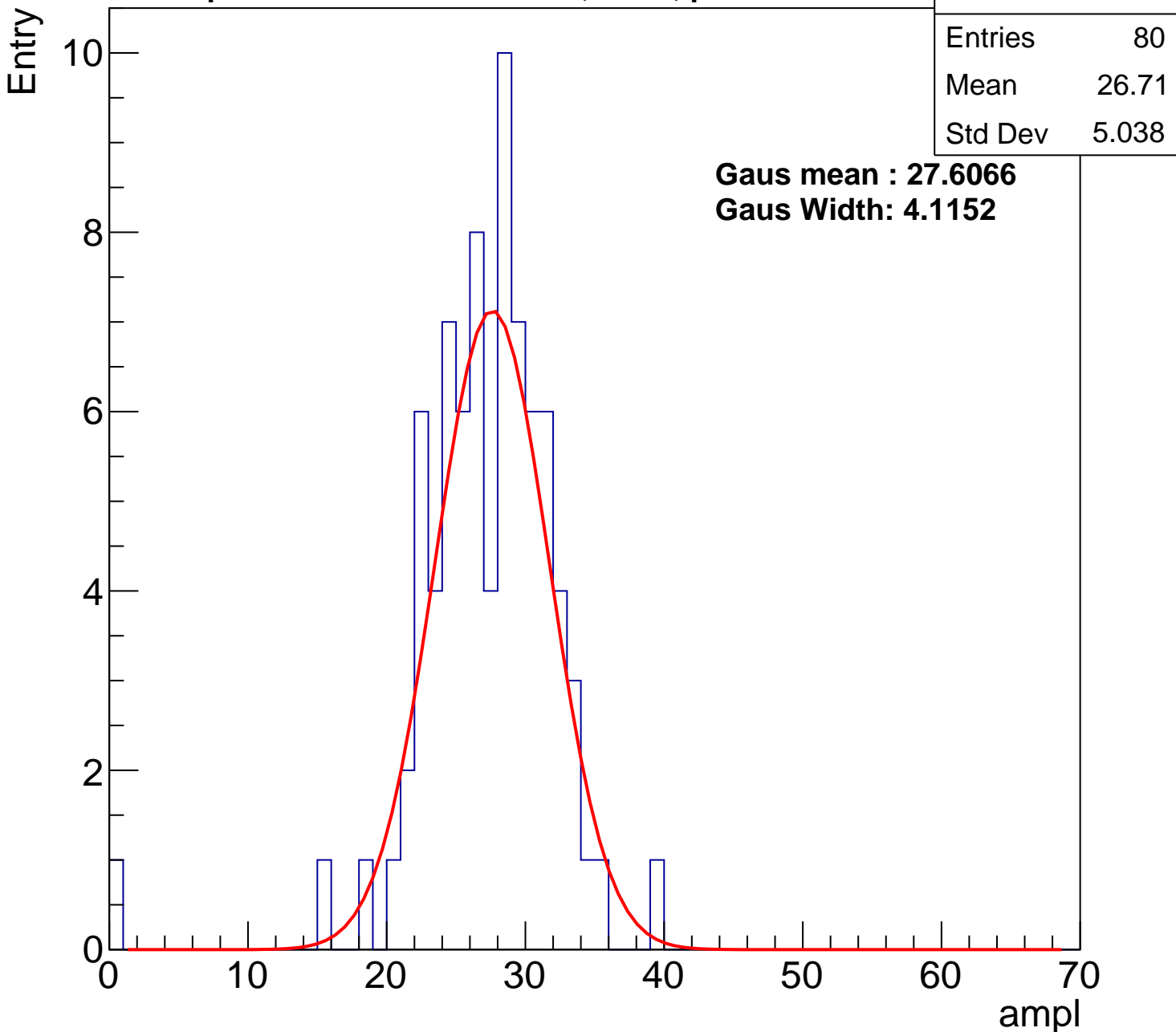
Gaus Width: 4.1152

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch66, adc1

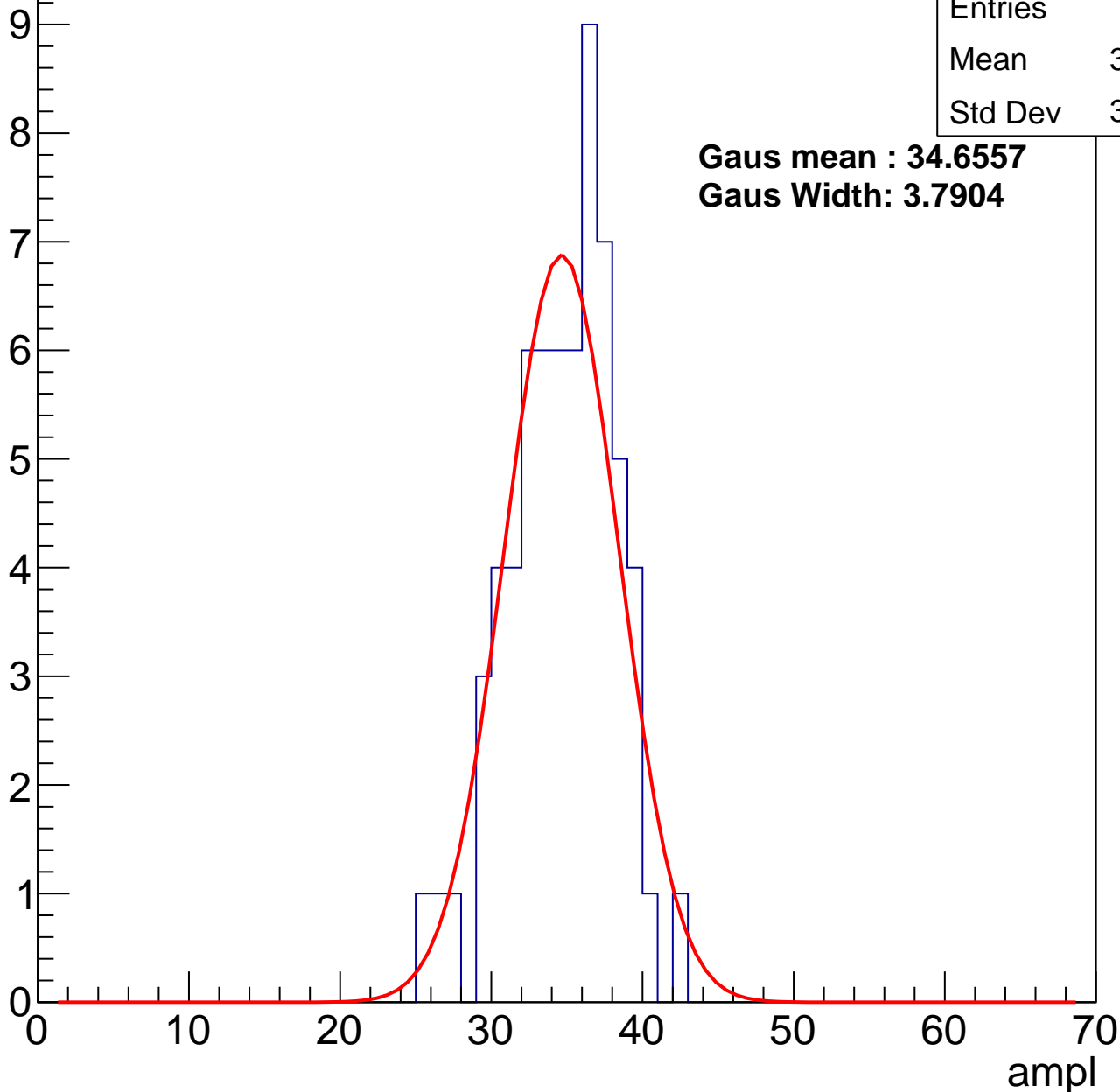
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	34.22
Std Dev	3.466

Gaus mean : 34.6557

Gaus Width: 3.7904



B1L103S, U5-ch66, adc2

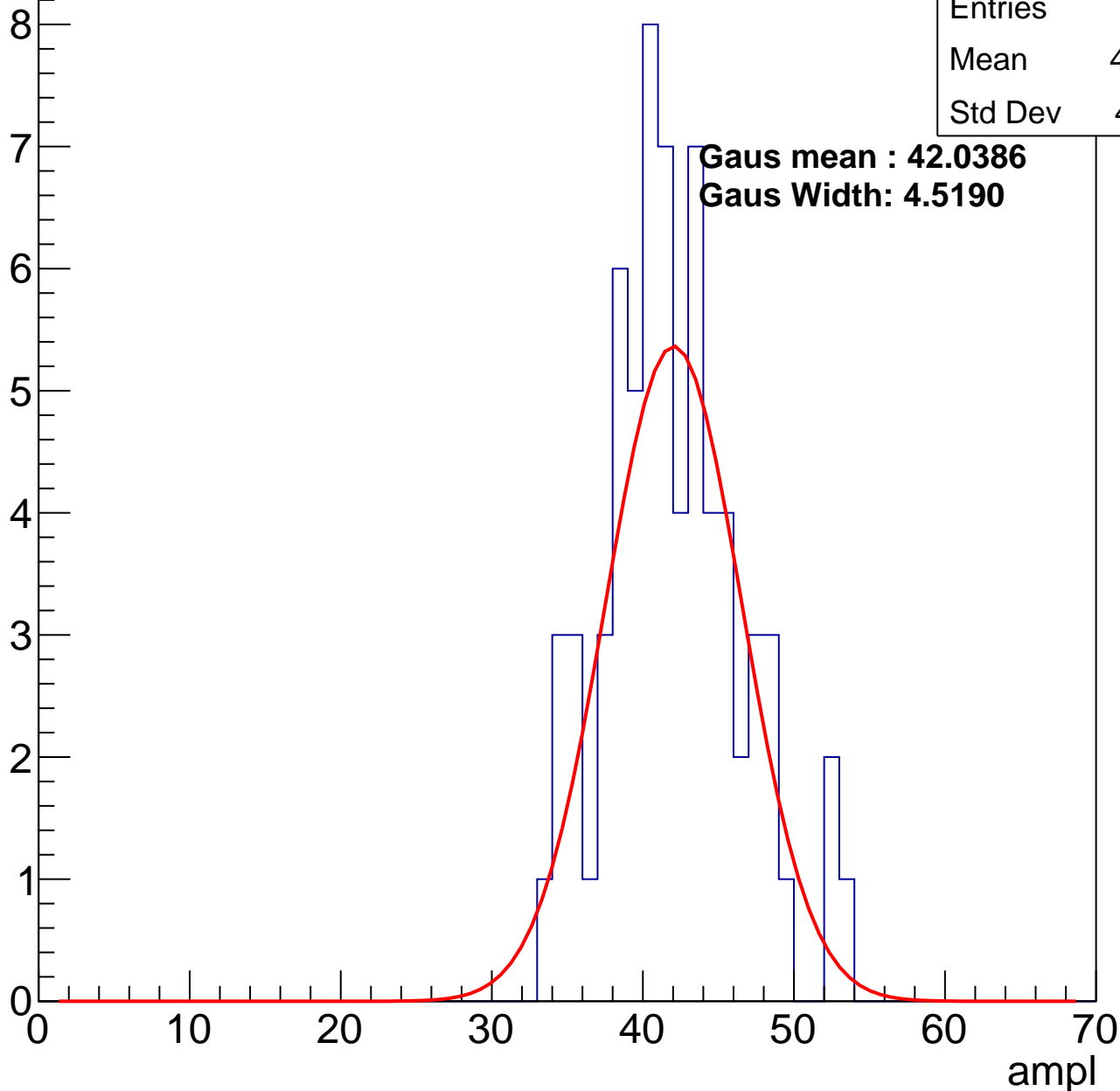
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	41.54
Std Dev	4.441

Gaus mean : 42.0386

Gaus Width: 4.5190

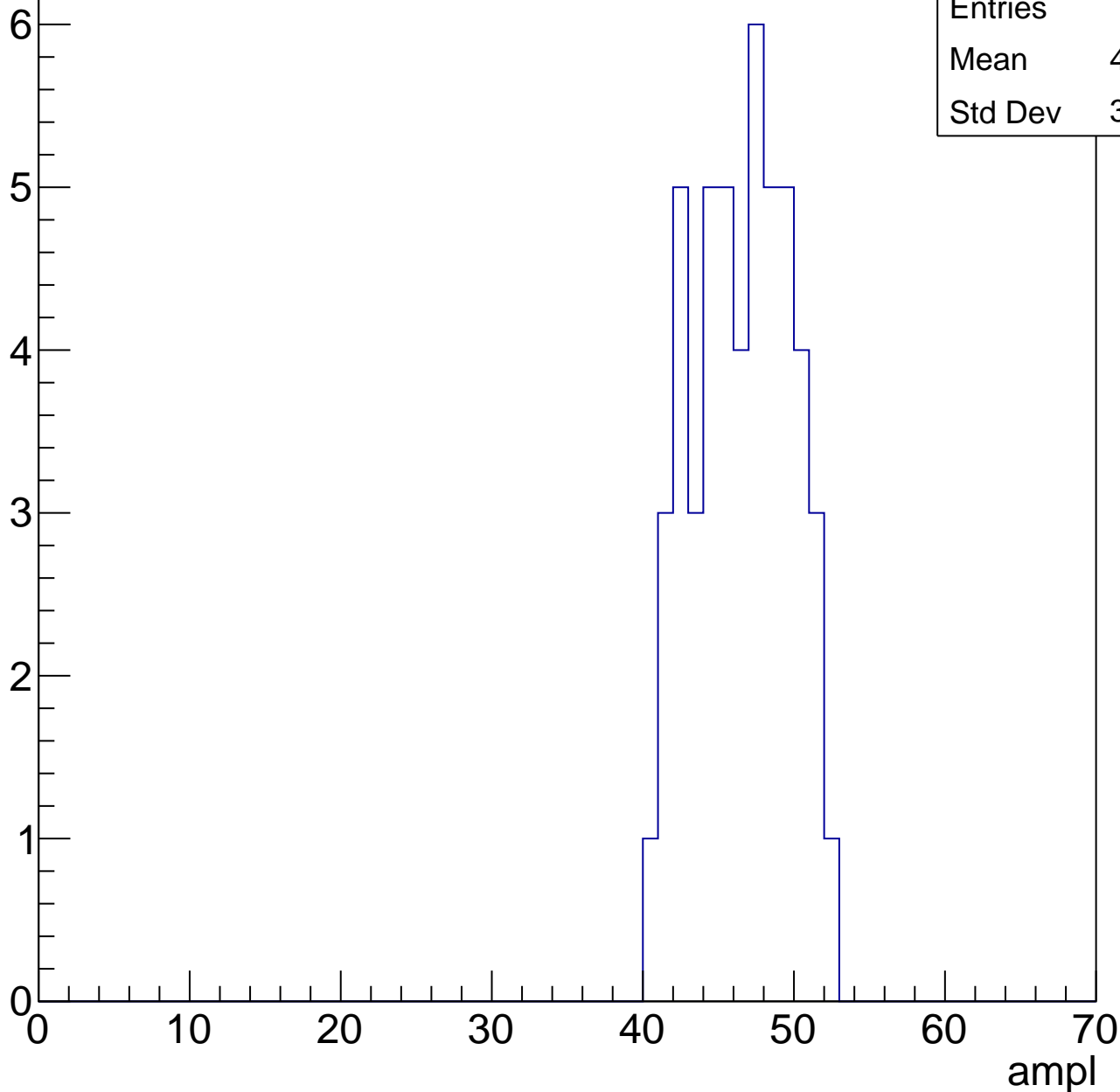


B1L103S, U5-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

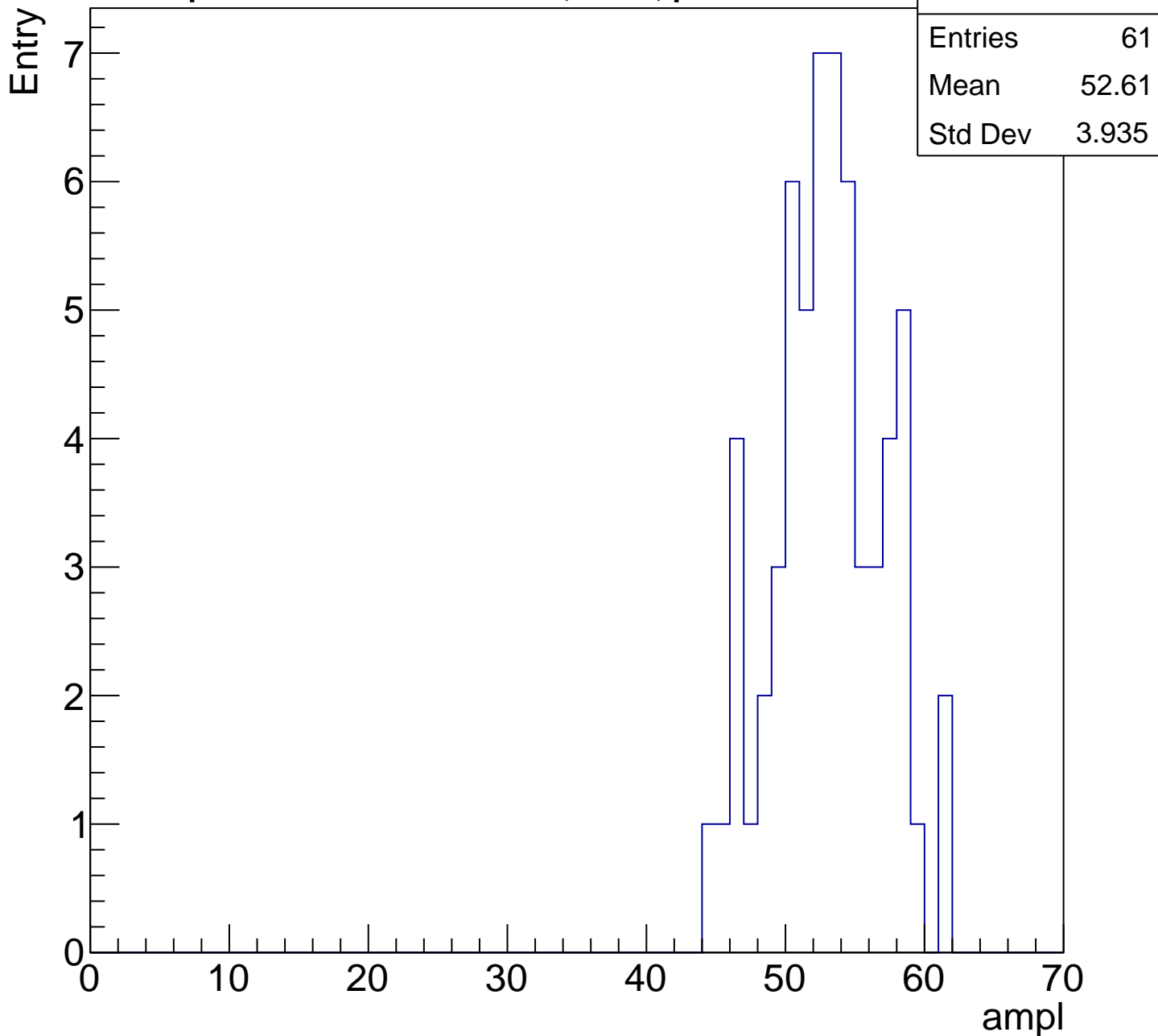
Entry

Entries	50
Mean	46.06
Std Dev	3.127



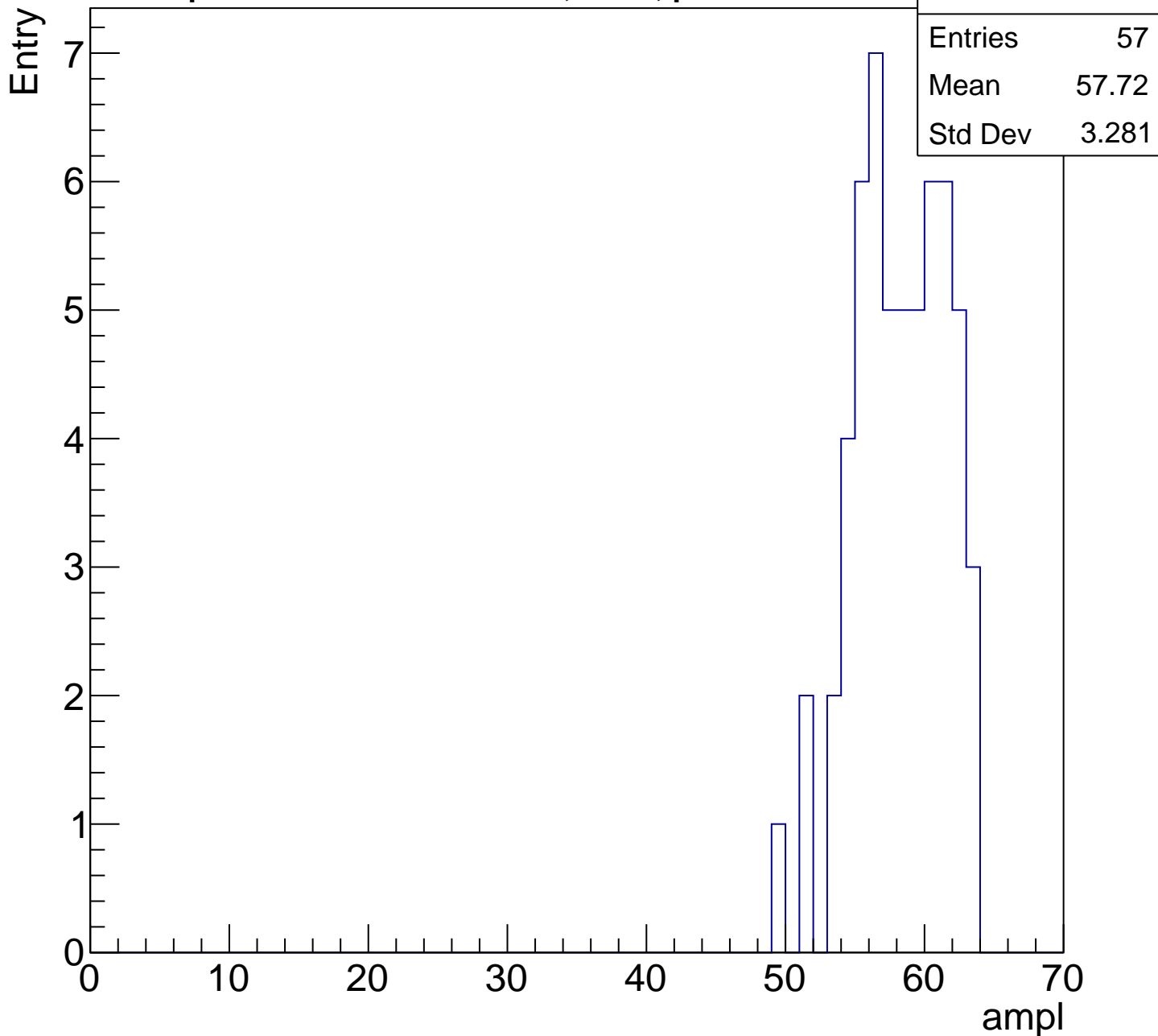
B1L103S, U5-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

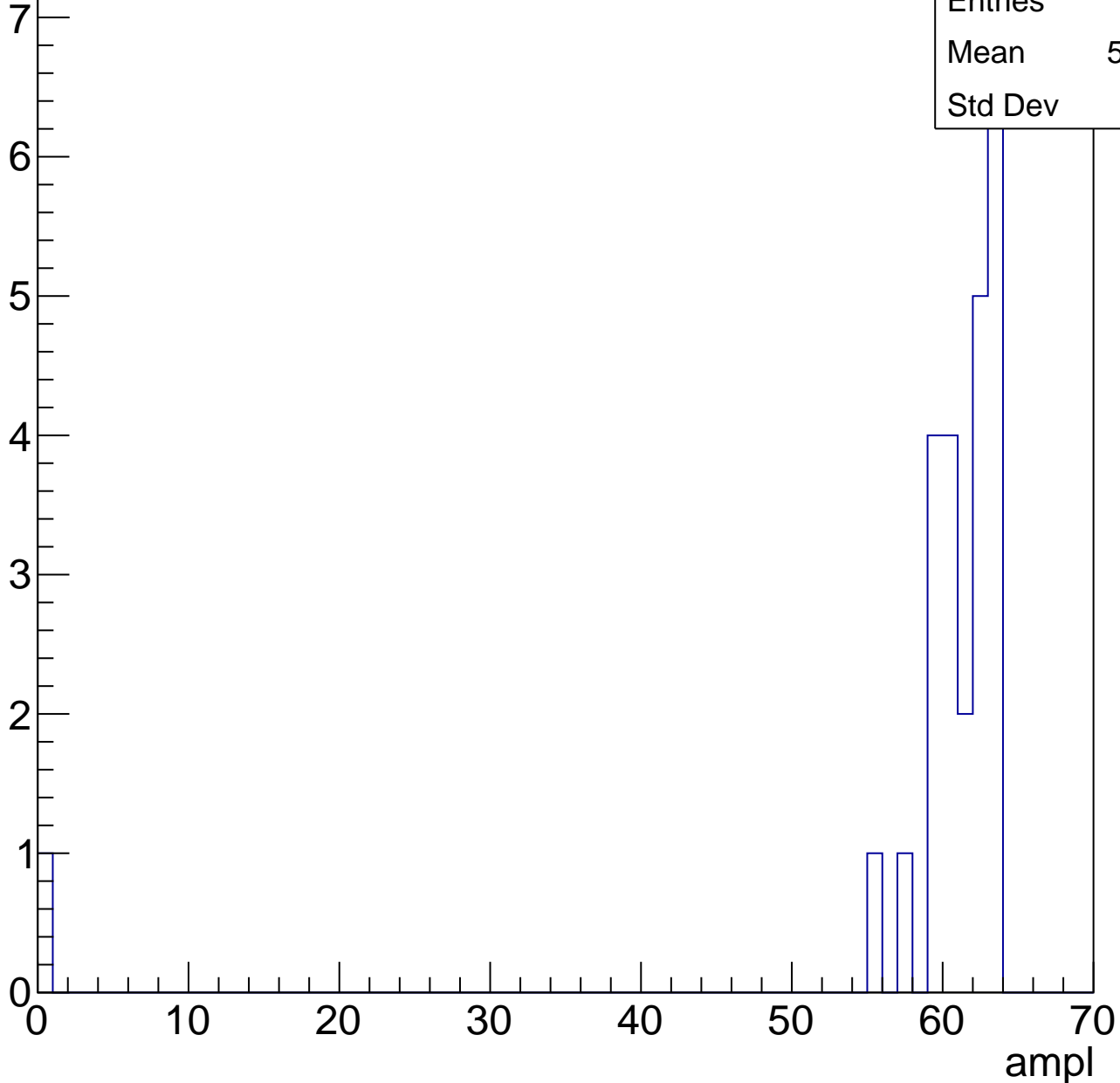


B1L103S, U5-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	58.44
Std Dev	12.1



B1L103S, U5-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch67, adc0

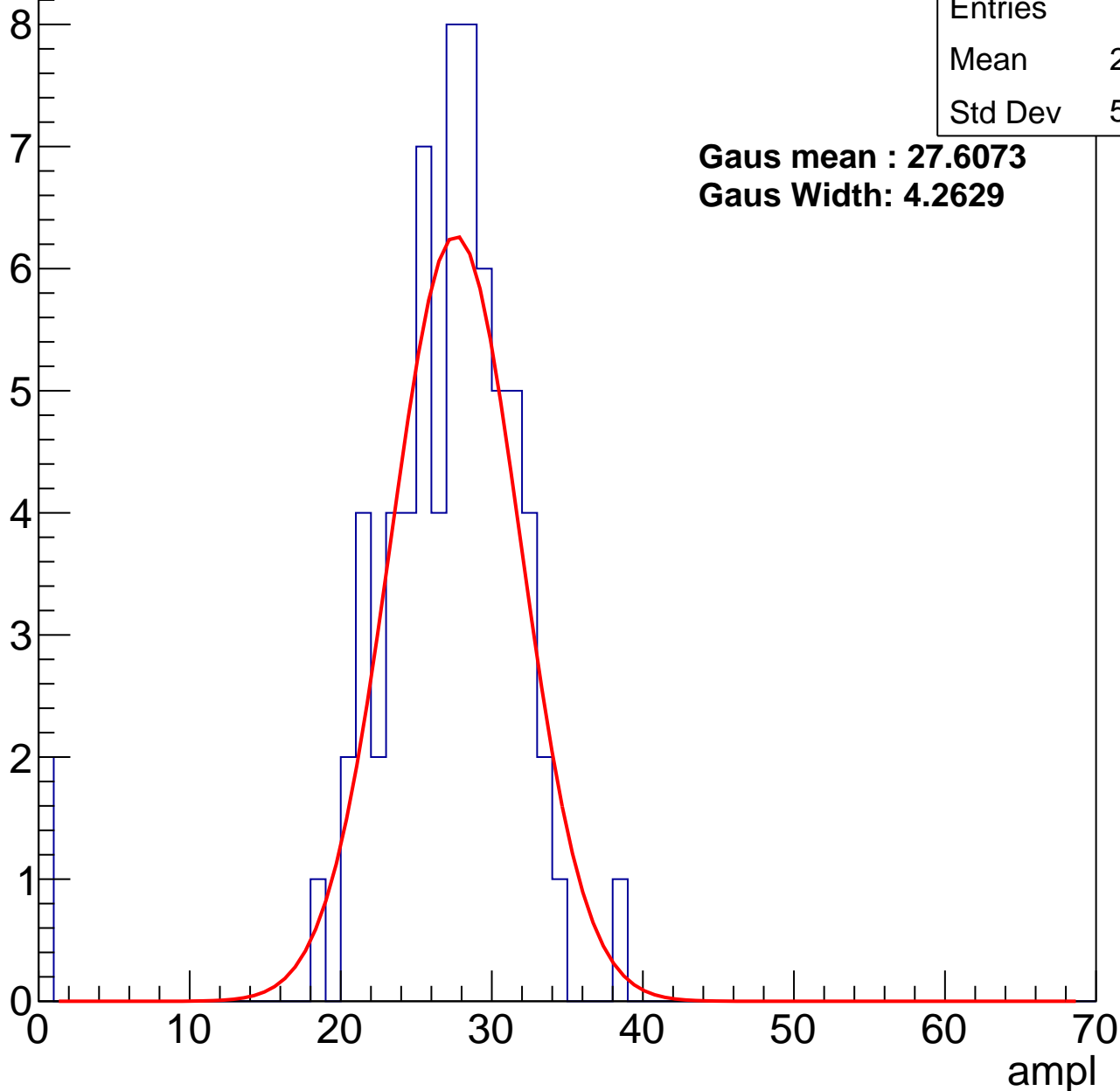
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	26.26
Std Dev	5.884

Gaus mean : 27.6073

Gaus Width: 4.2629



B1L103S, U5-ch67, adc1

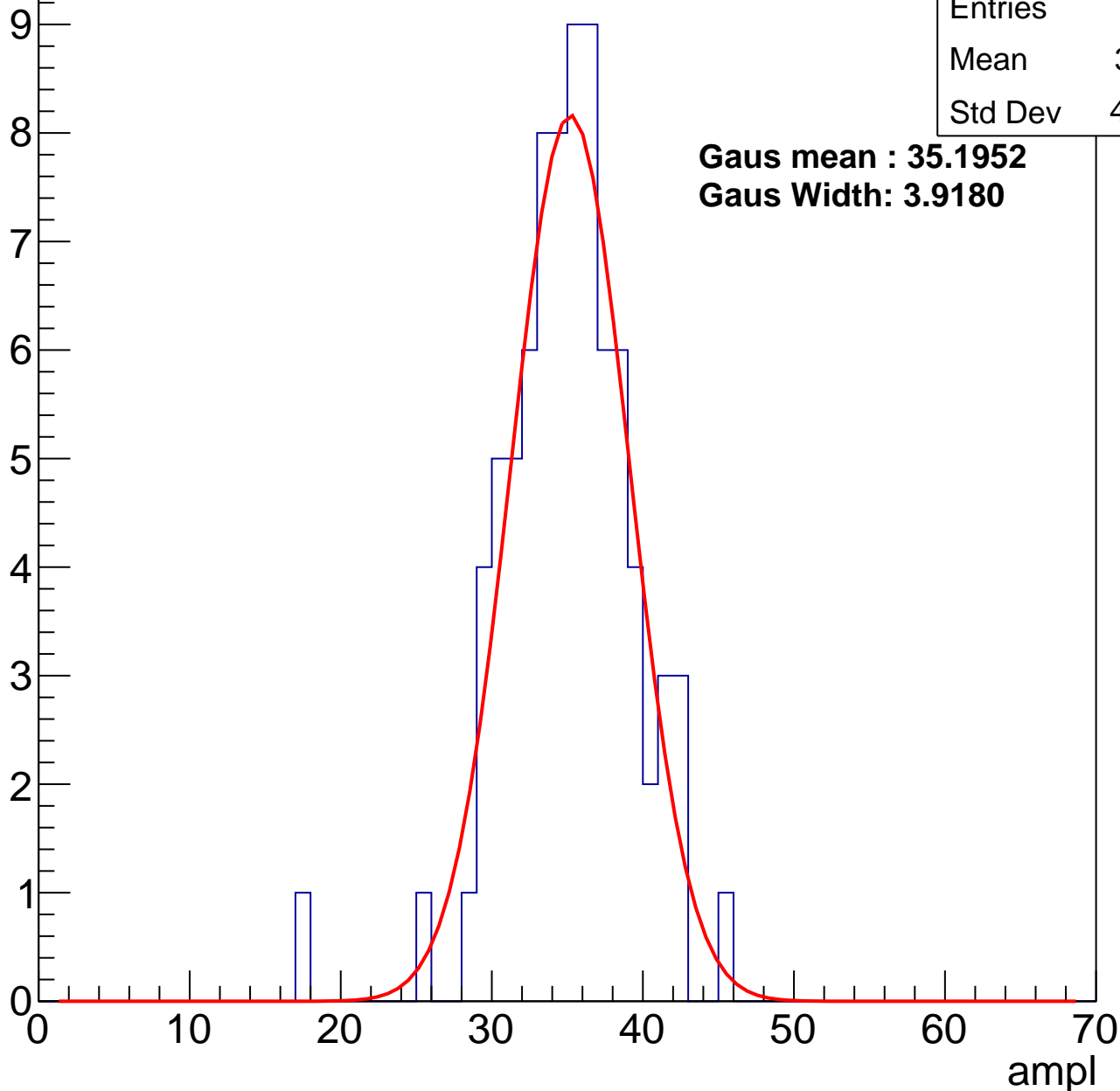
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	34.61
Std Dev	4.219

Gaus mean : 35.1952

Gaus Width: 3.9180



B1L103S, U5-ch67, adc2

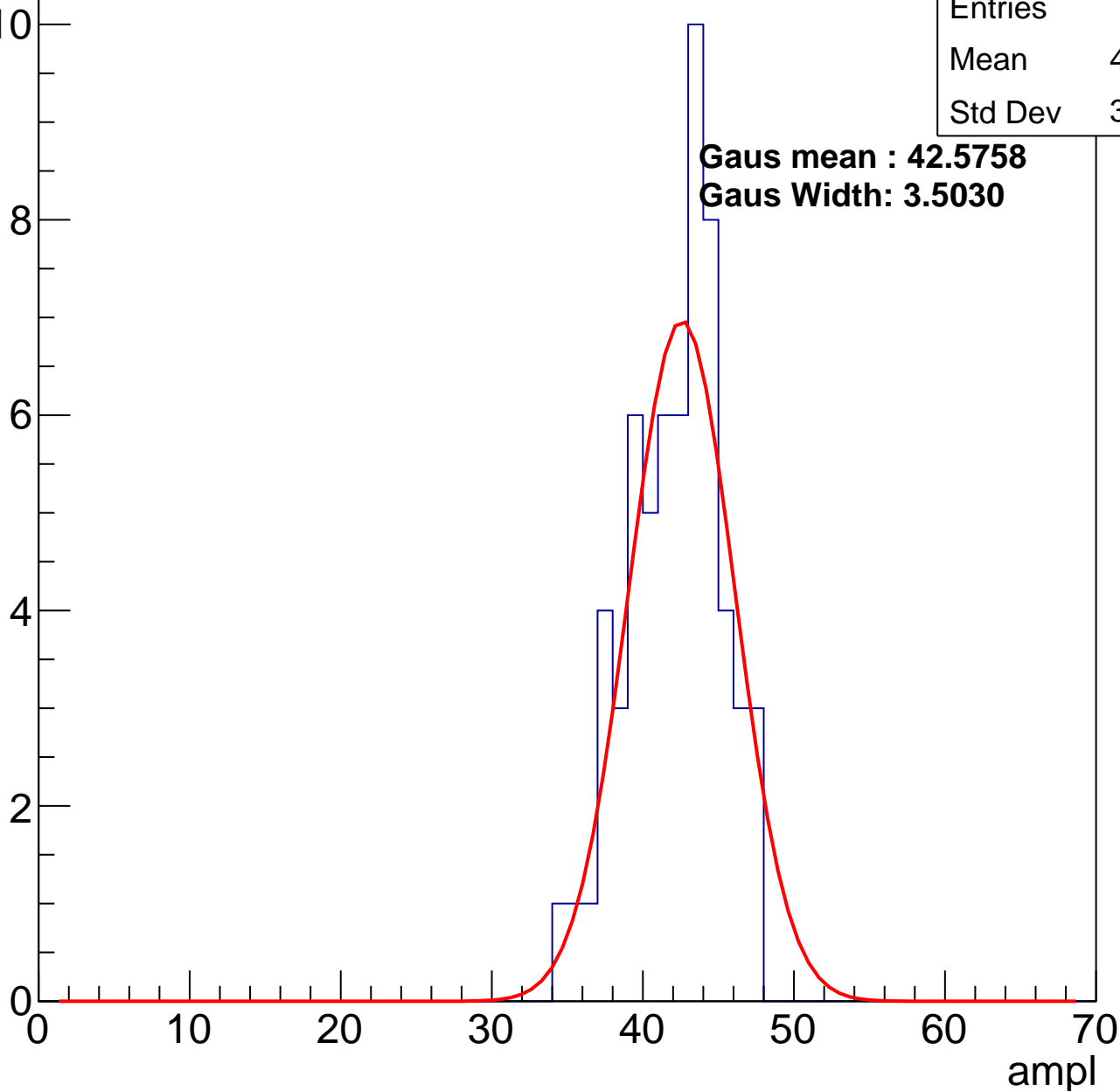
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.64
Std Dev	3.057

Gaus mean : 42.5758

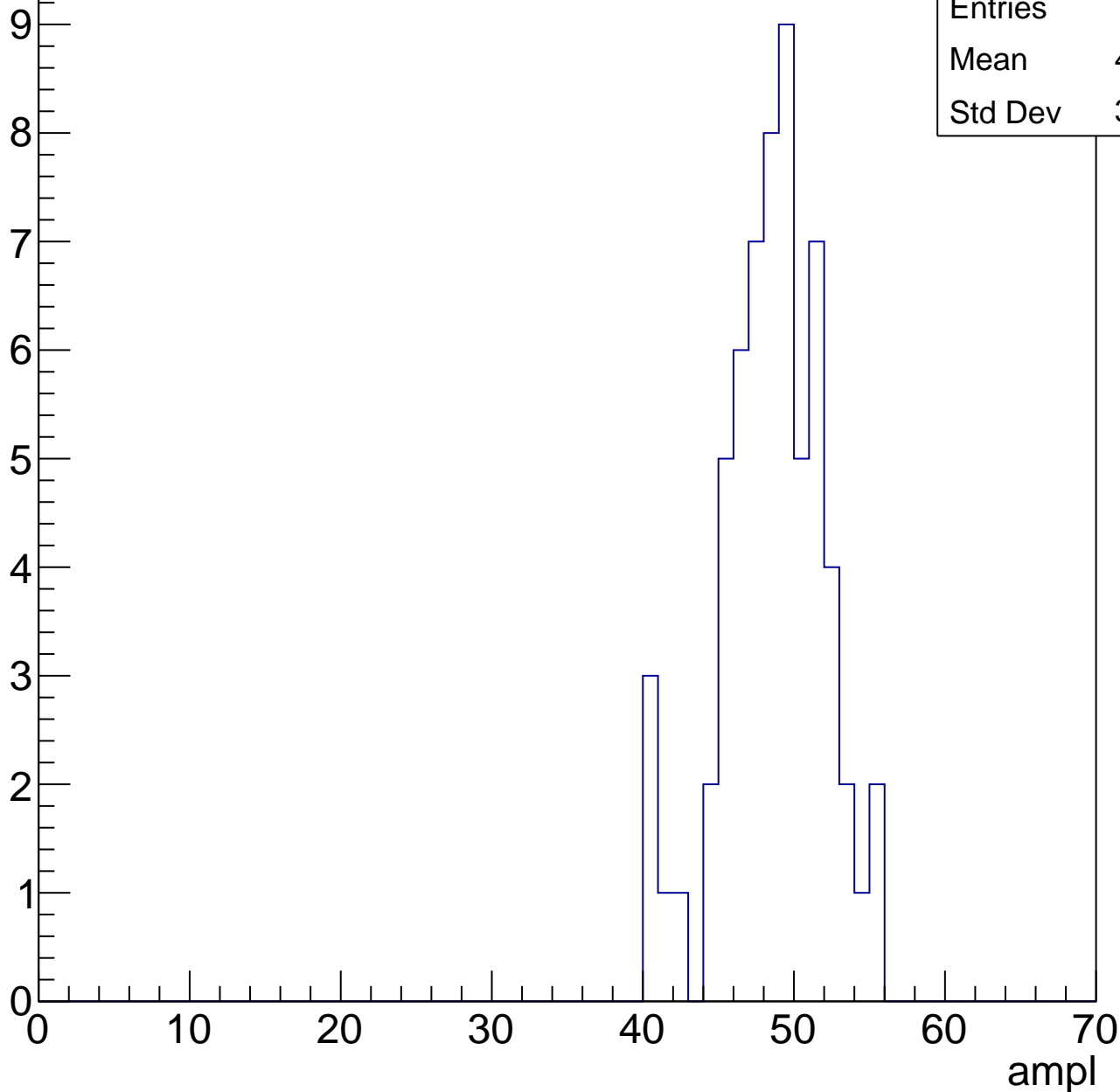
Gaus Width: 3.5030



B1L103S, U5-ch67, adc3

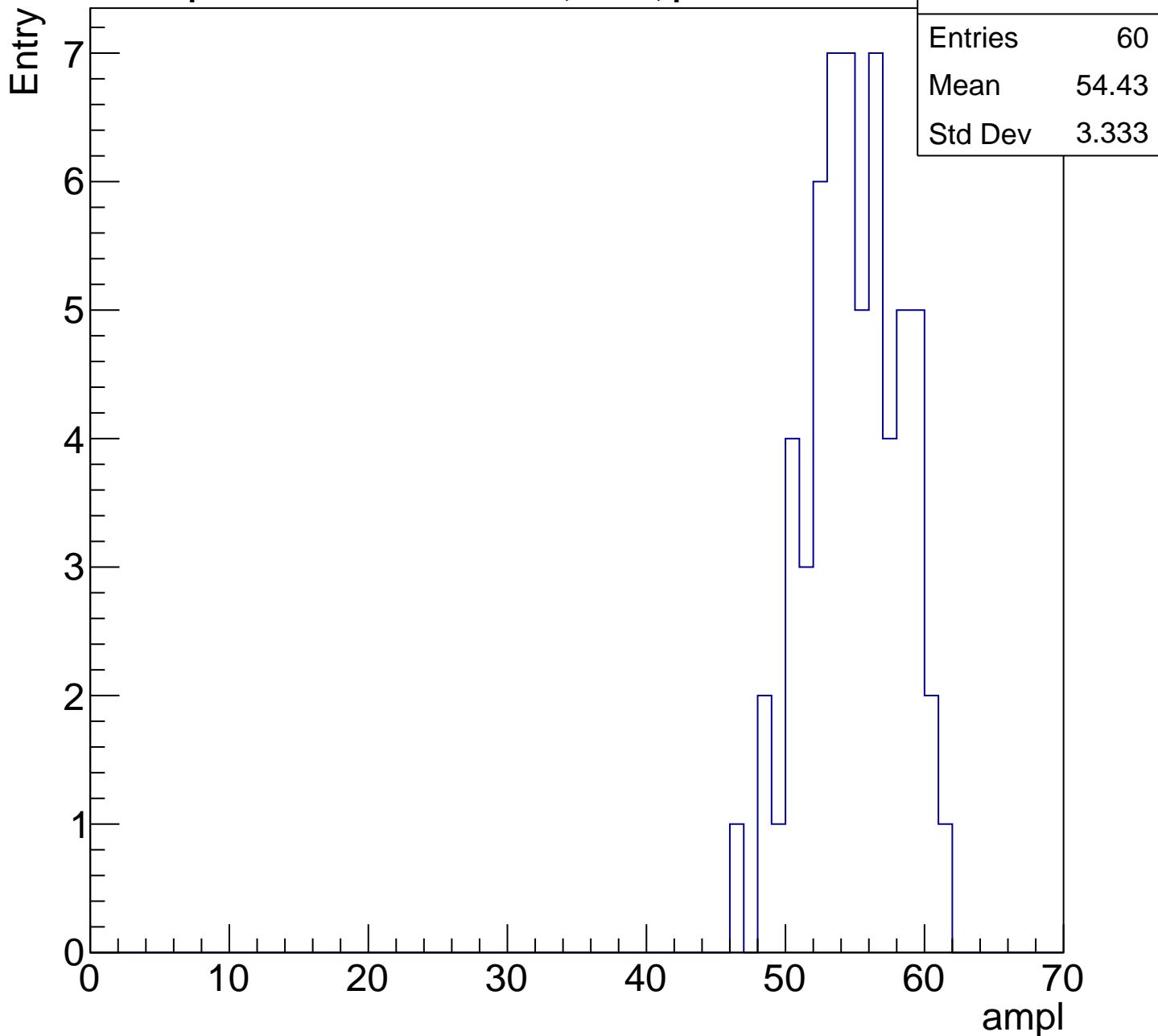
calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

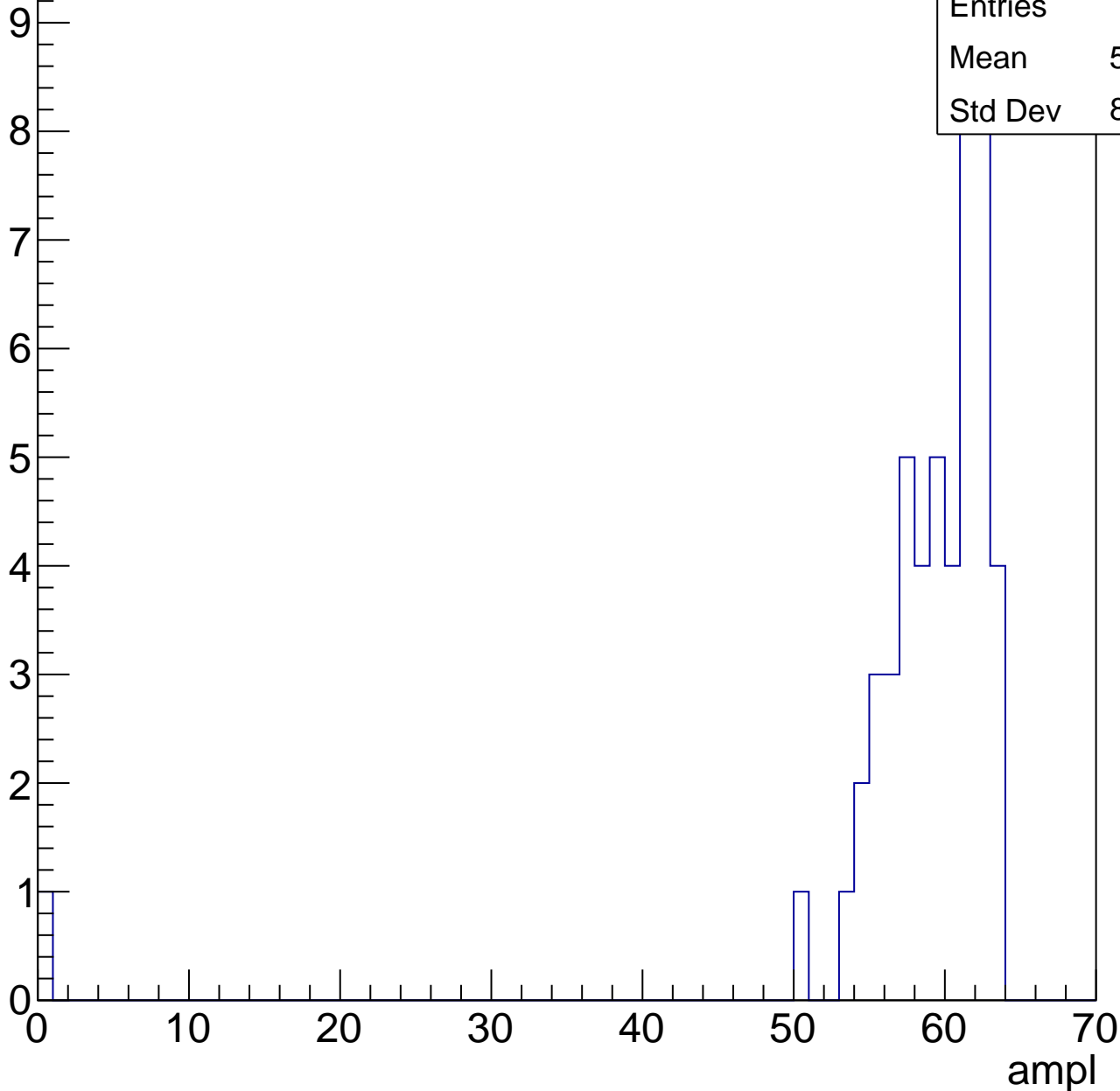


B1L103S, U5-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	57.86
Std Dev	8.784

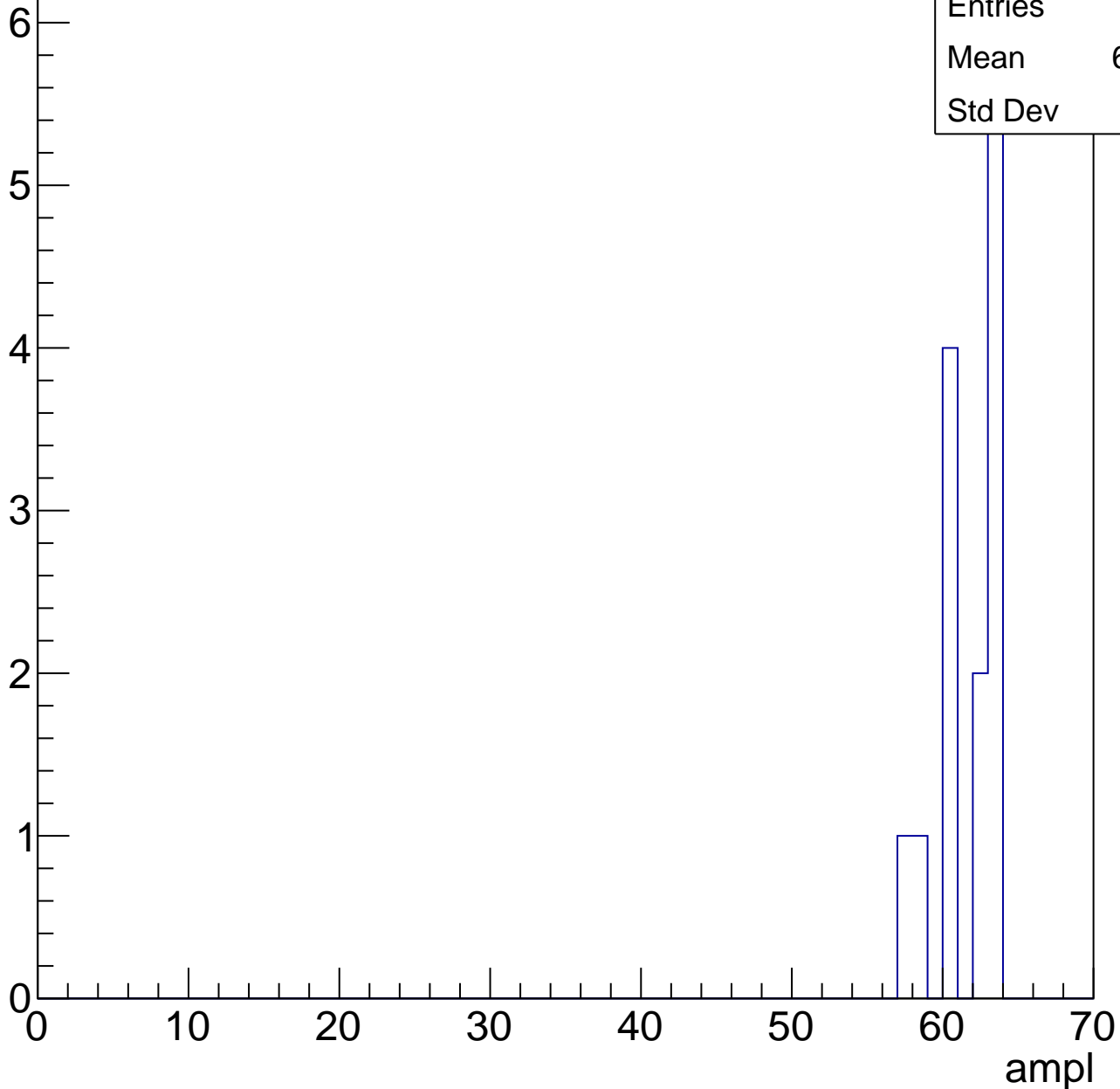


B1L103S, U5-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	61.21
Std Dev	1.97



B1L103S, U5-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch68, adc0

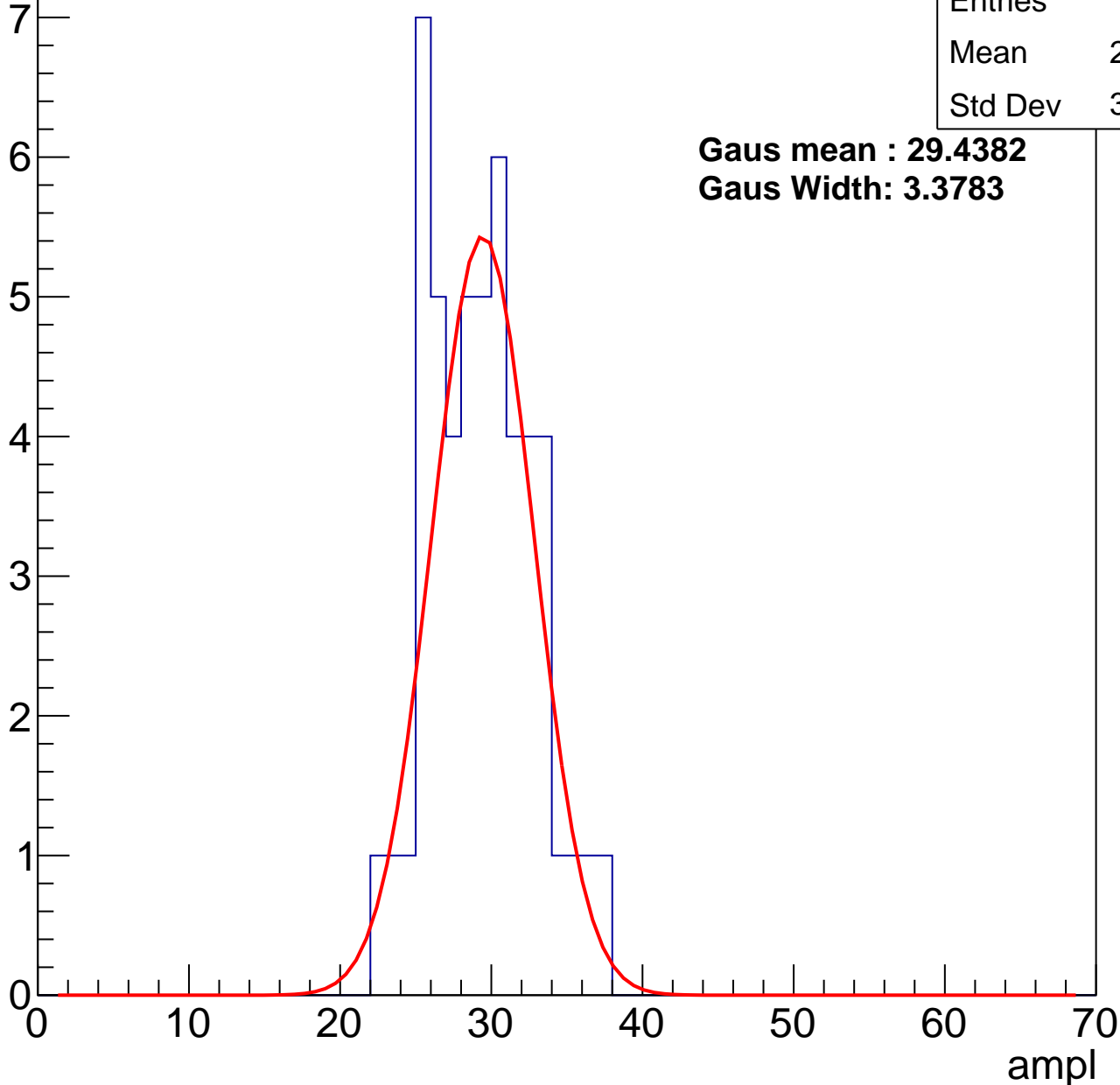
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	28.88
Std Dev	3.382

Gaus mean : 29.4382

Gaus Width: 3.3783



B1L103S, U5-ch68, adc1

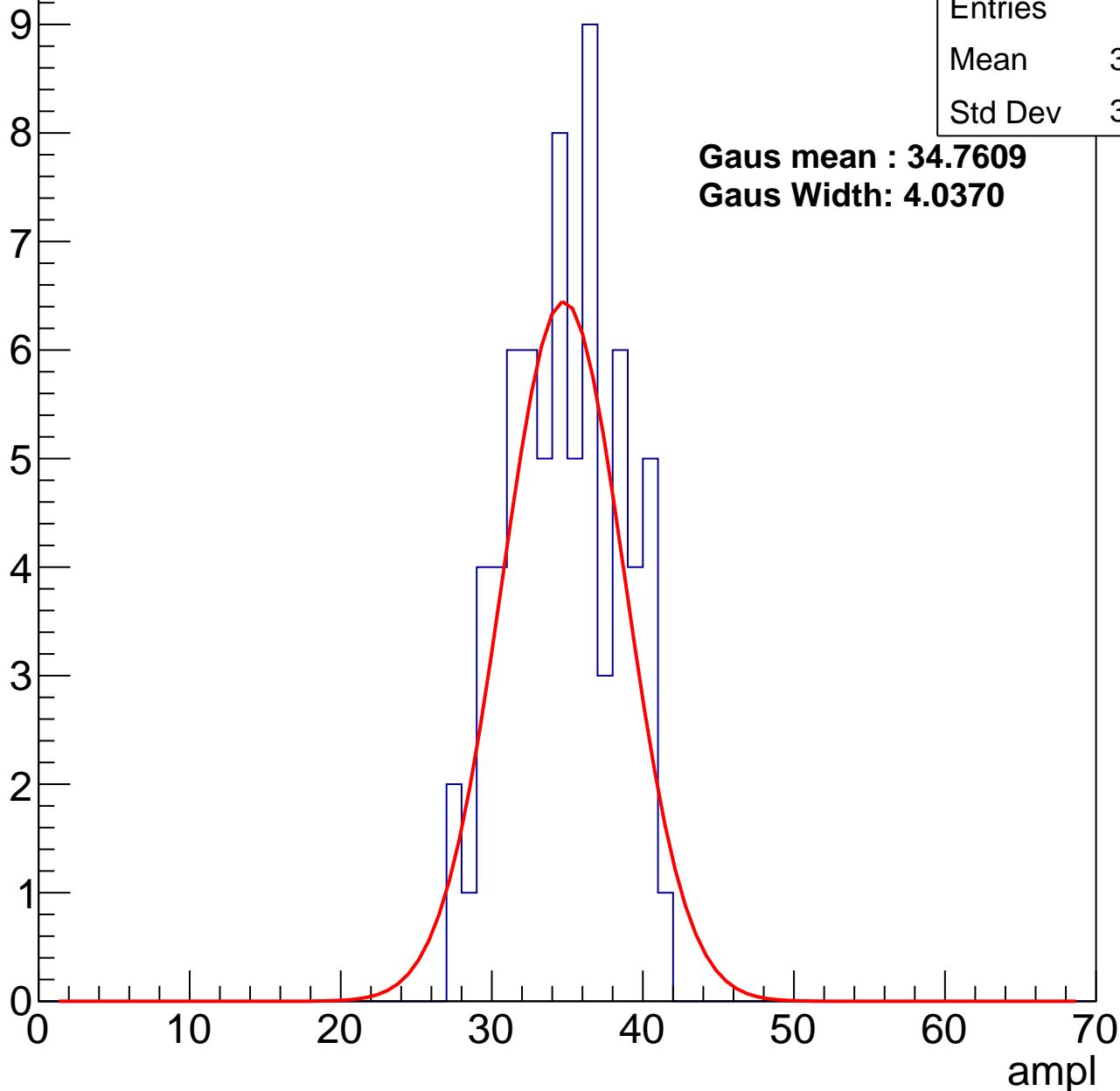
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	34.32
Std Dev	3.545

Gaus mean : 34.7609

Gaus Width: 4.0370



B1L103S, U5-ch68, adc2

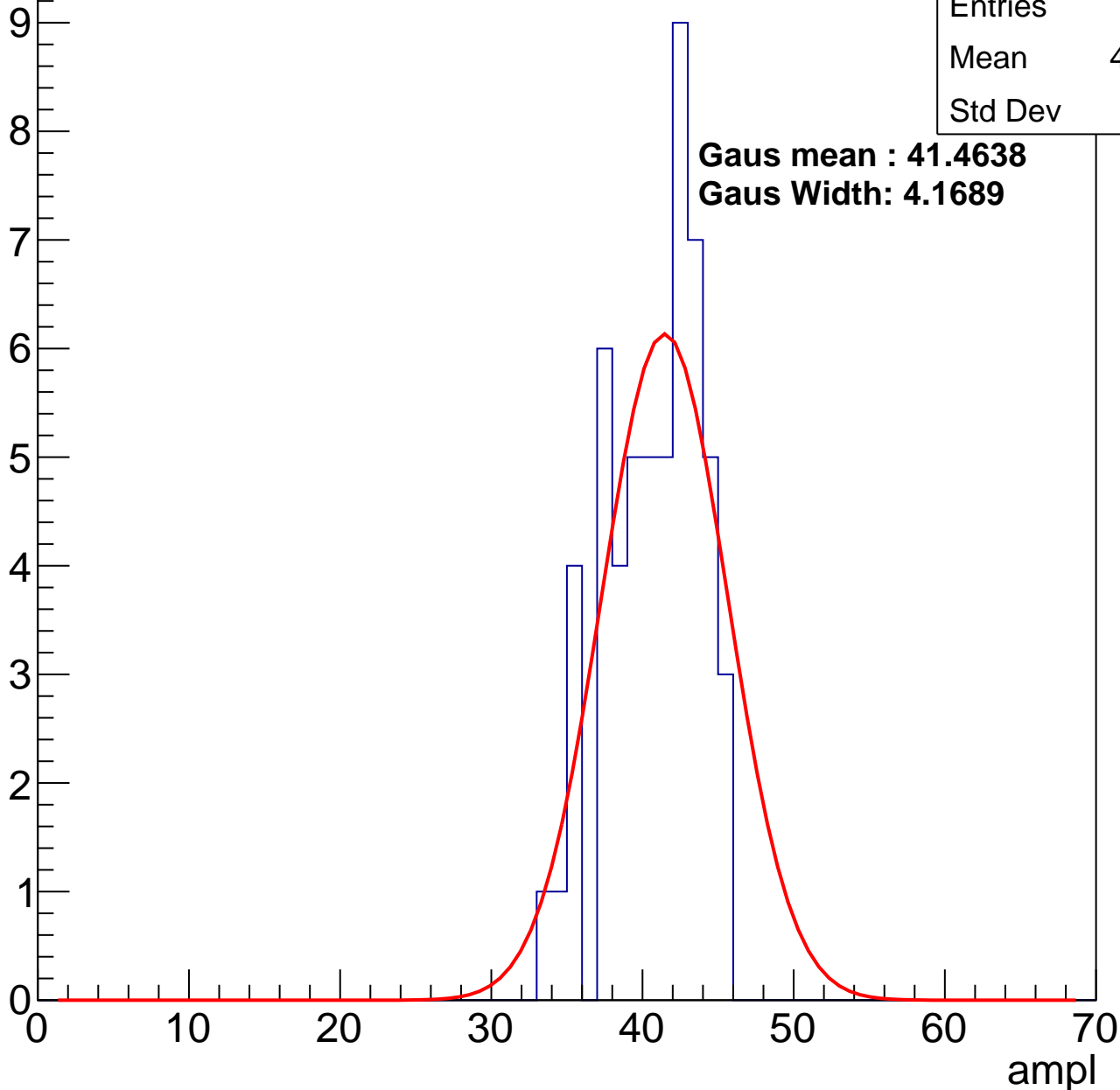
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	40.27
Std Dev	3.06

Gaus mean : 41.4638

Gaus Width: 4.1689

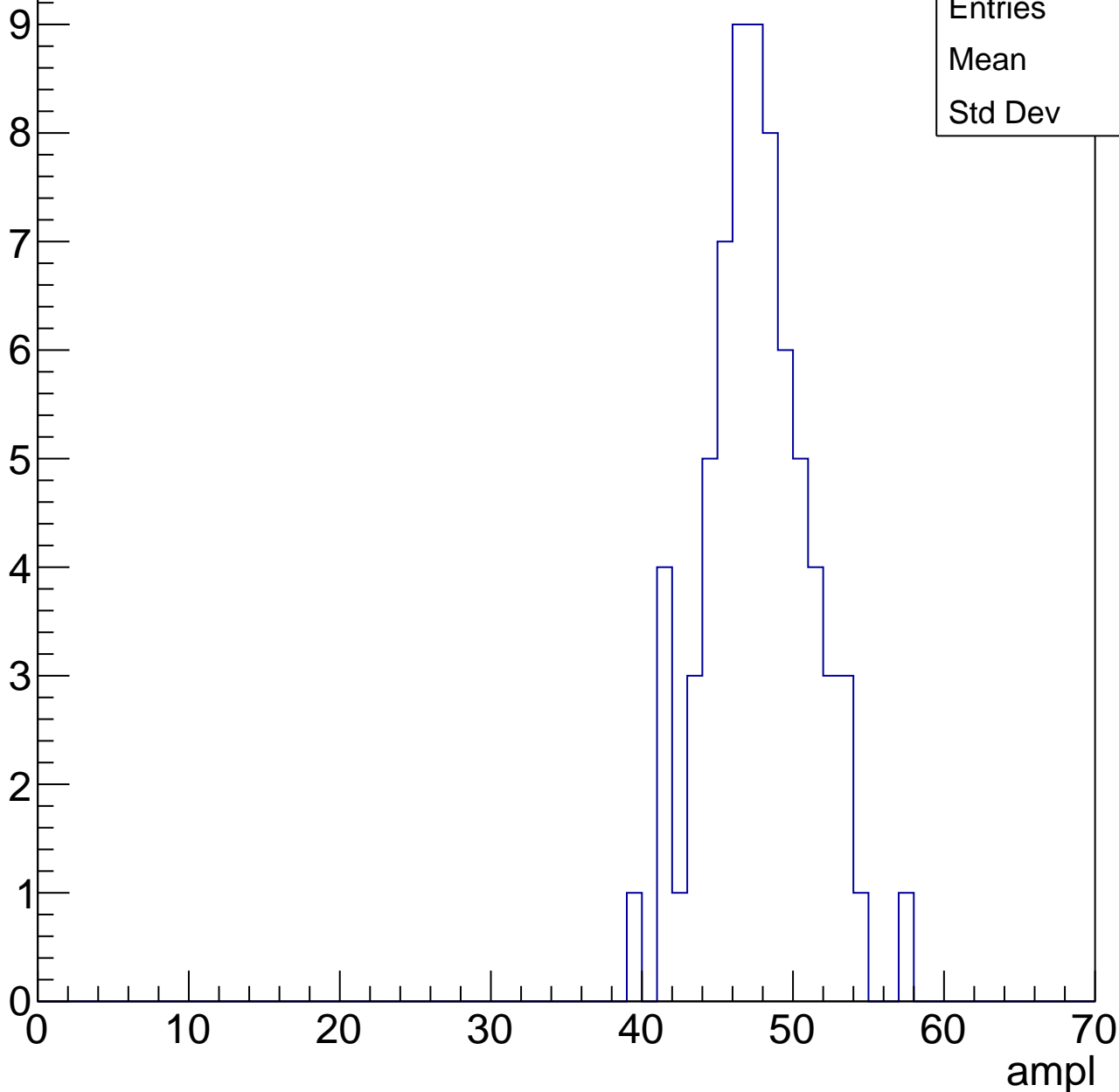


B1L103S, U5-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	47.2
Std Dev	3.45

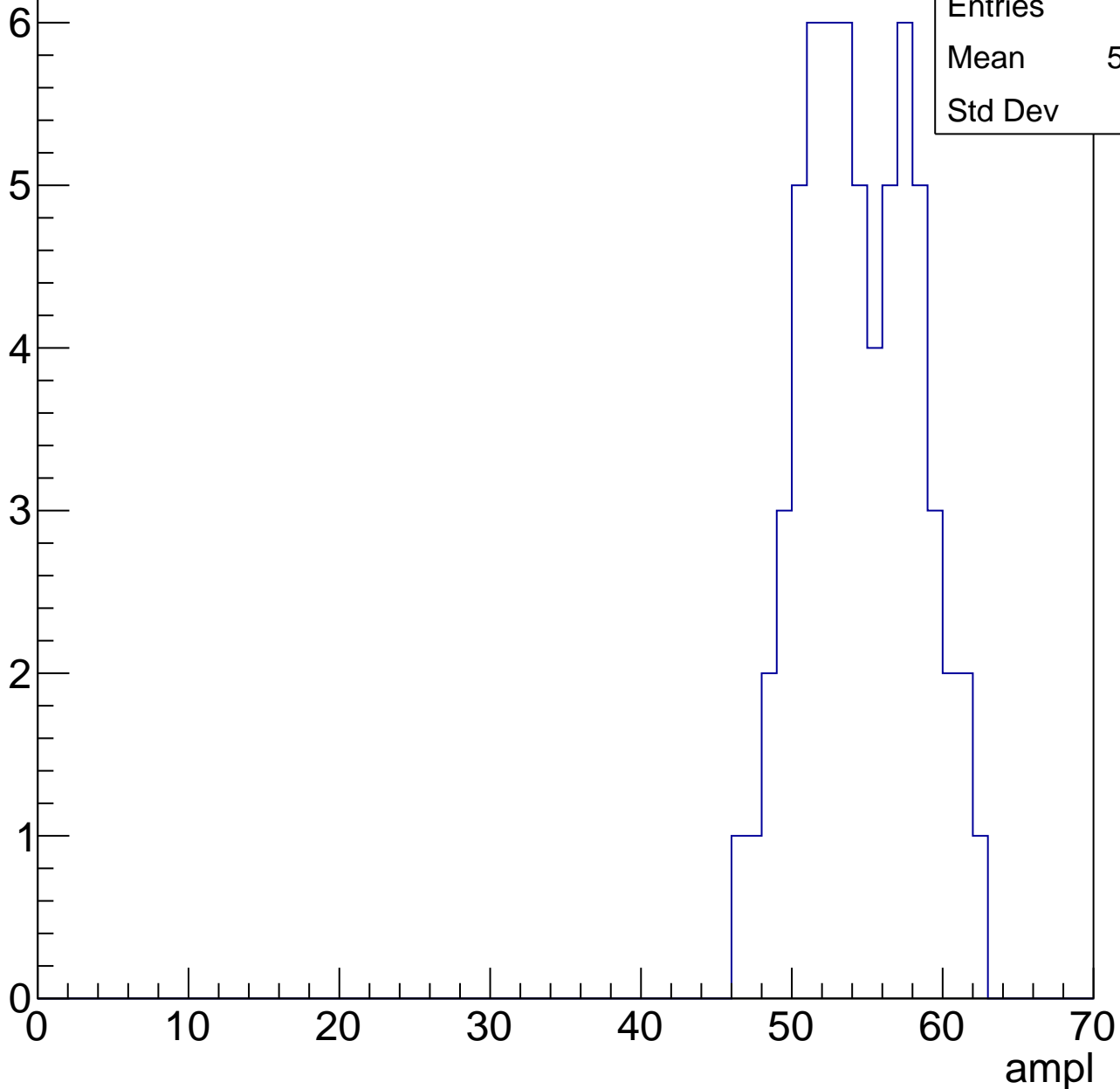


B1L103S, U5-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

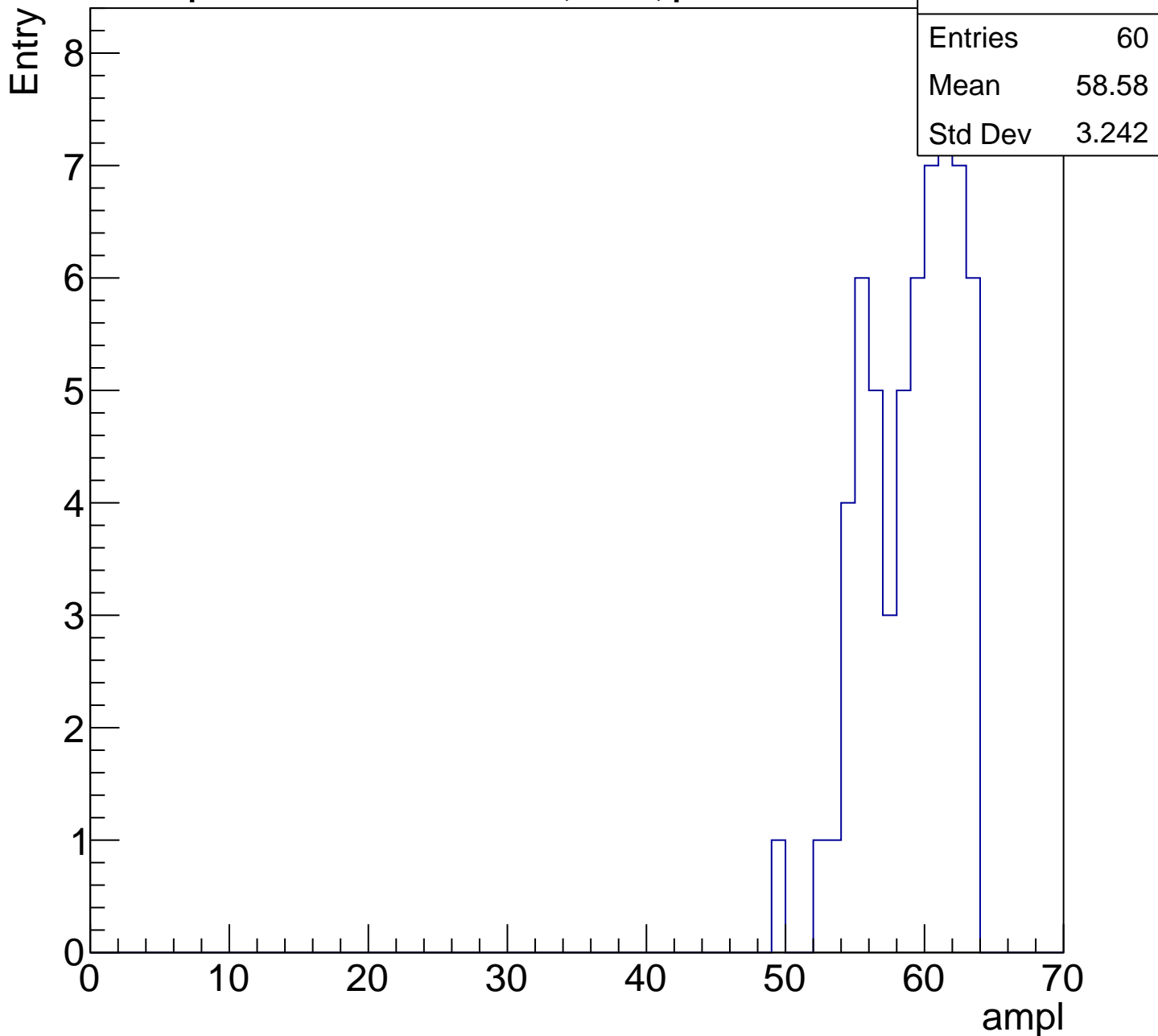
Entry

Entries	63
Mean	54.05
Std Dev	3.76



B1L103S, U5-ch68, adc5

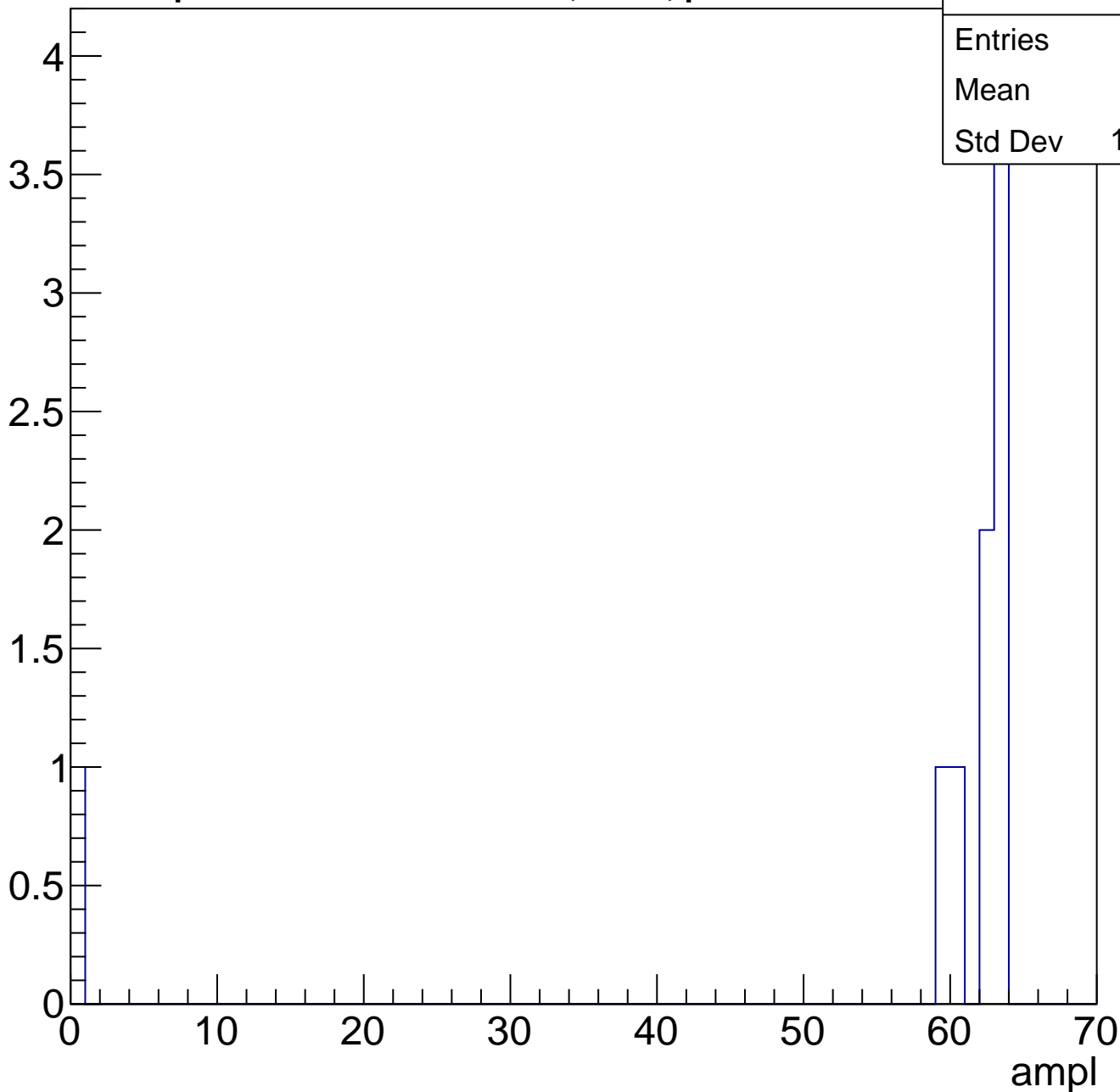
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch69, adc0

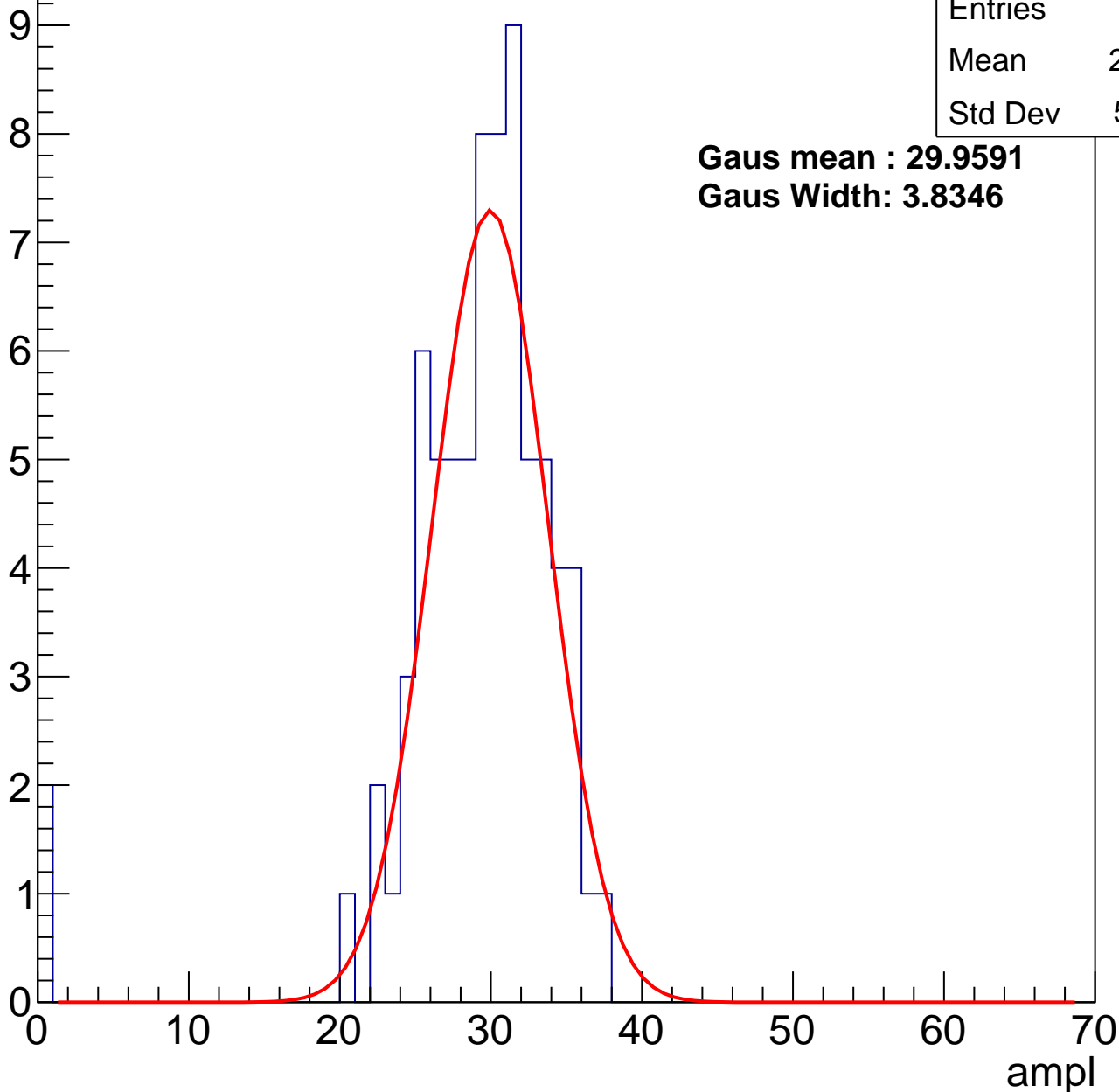
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.52
Std Dev	5.941

Gaus mean : 29.9591

Gaus Width: 3.8346



B1L103S, U5-ch69, adc1

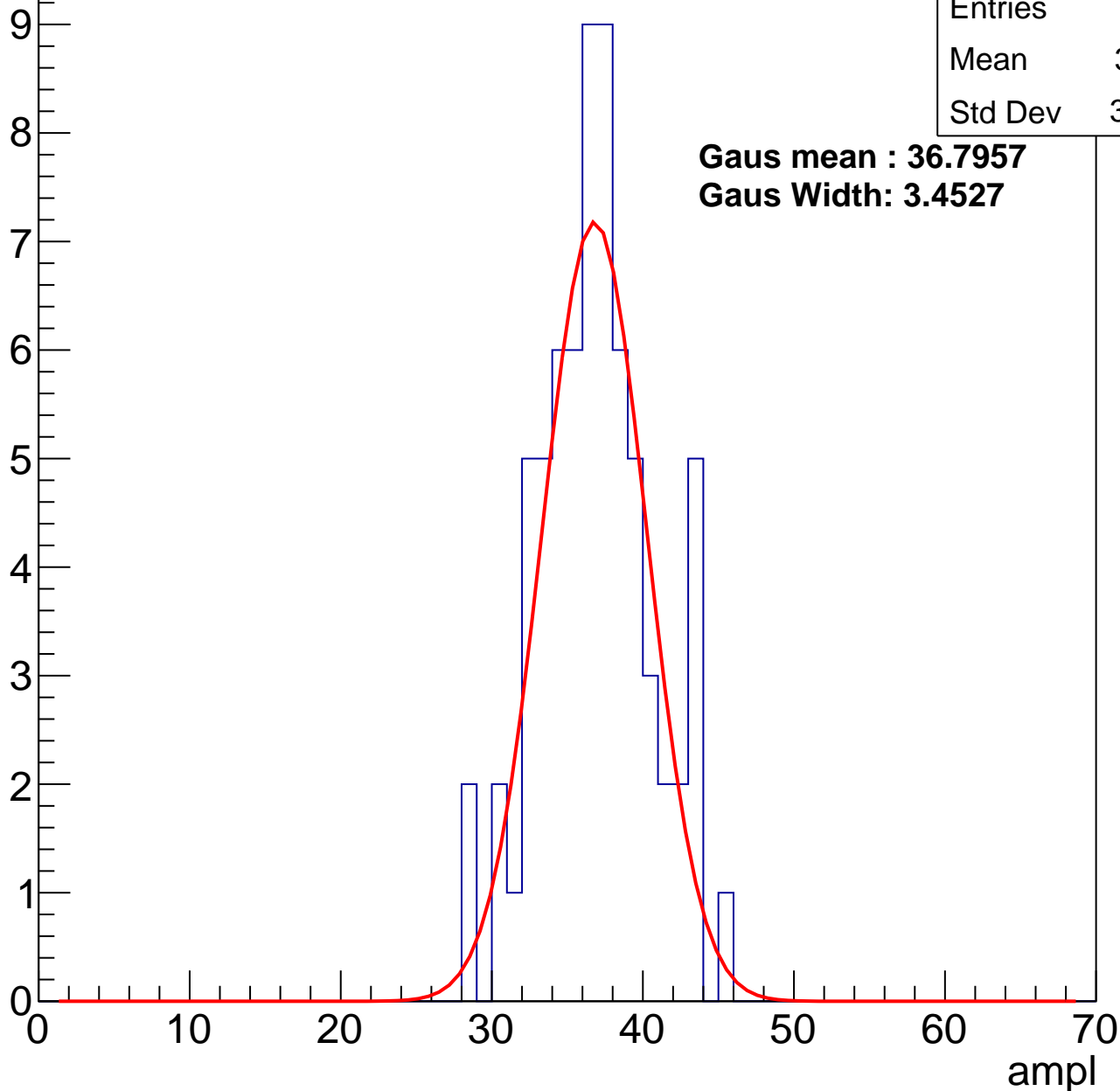
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.41
Std Dev	3.696

Gaus mean : 36.7957

Gaus Width: 3.4527



B1L103S, U5-ch69, adc2

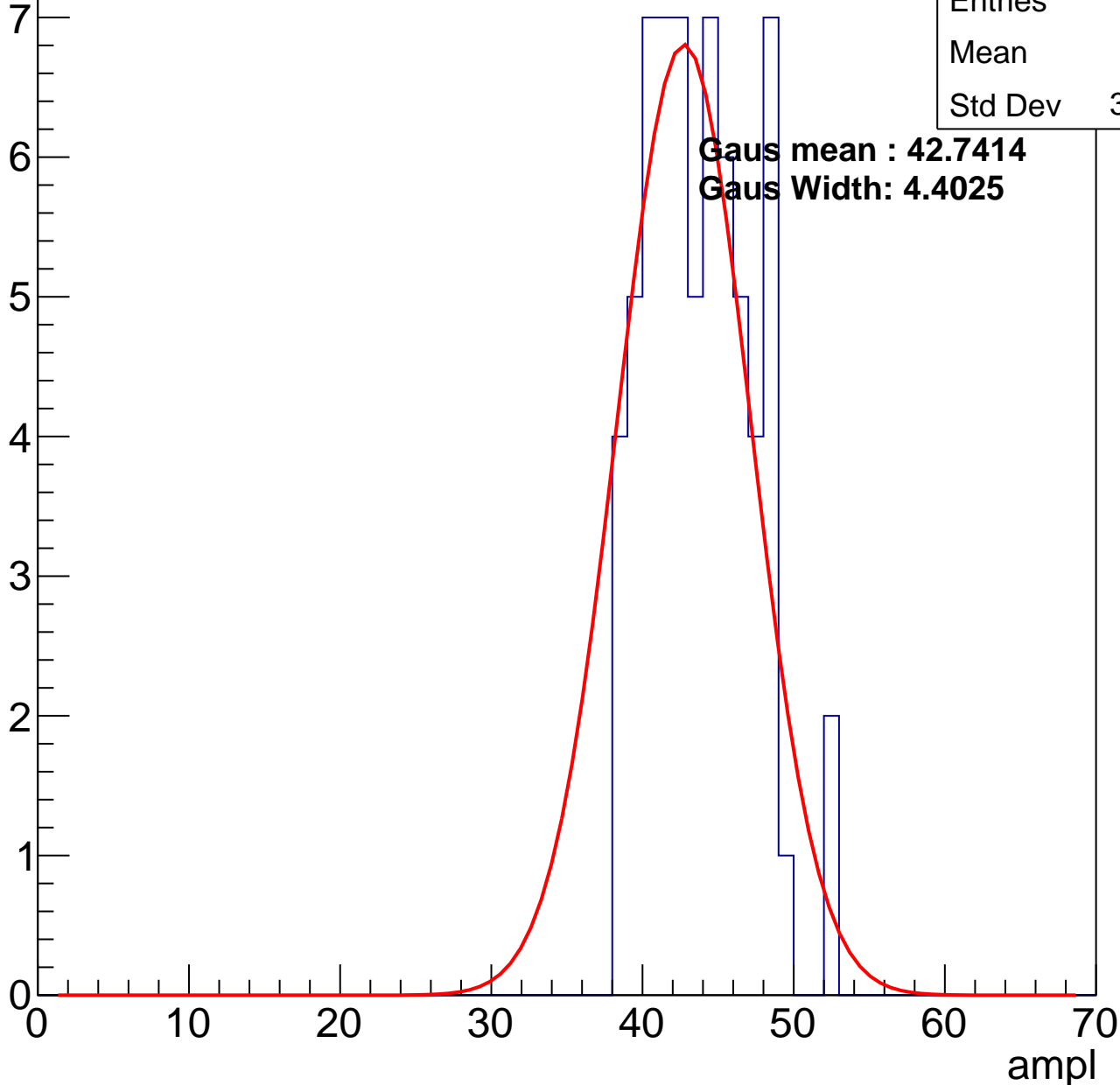
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.4
Std Dev	3.412

Gaus mean : 42.7414

Gaus Width: 4.4025

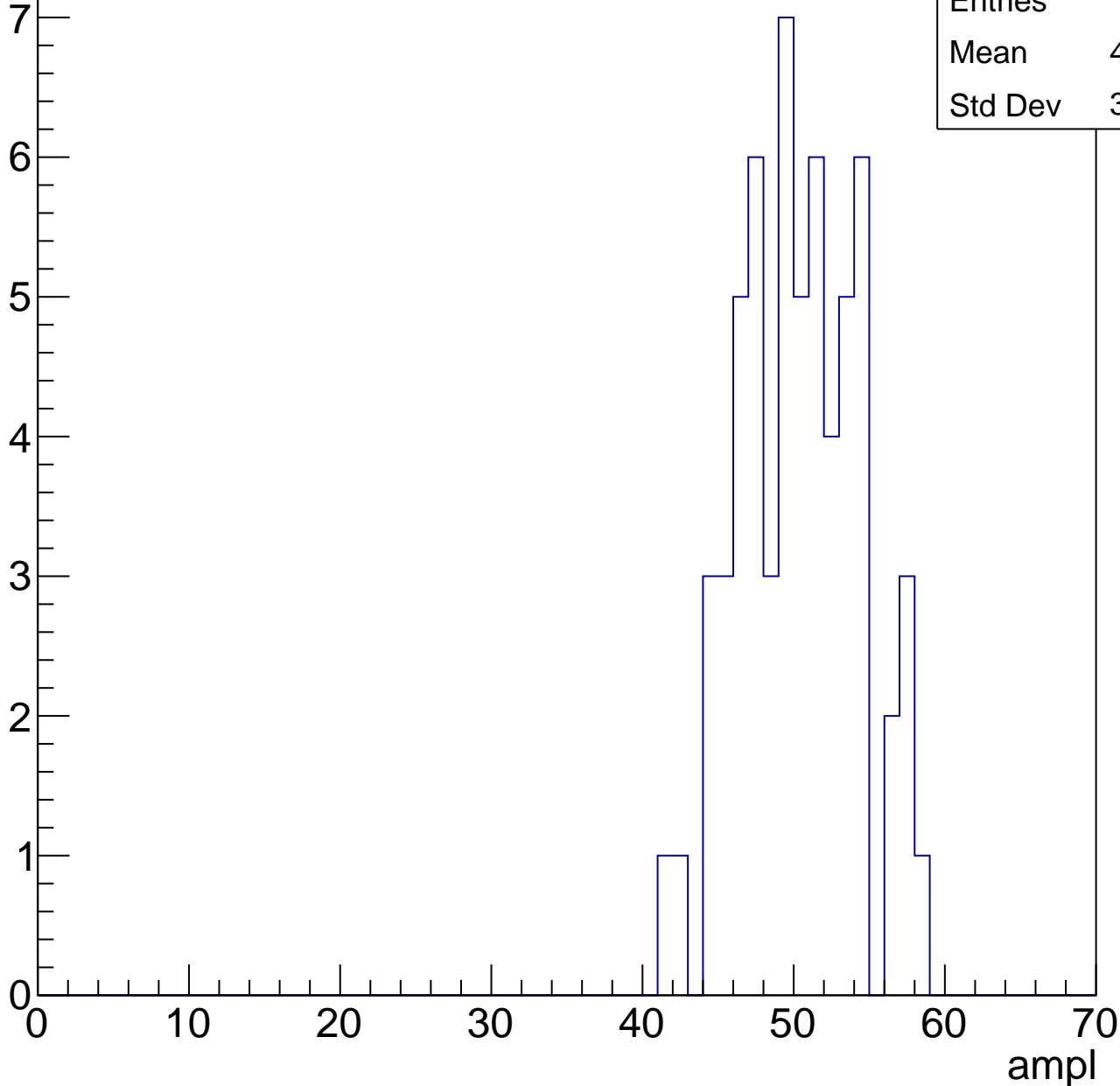


B1L103S, U5-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

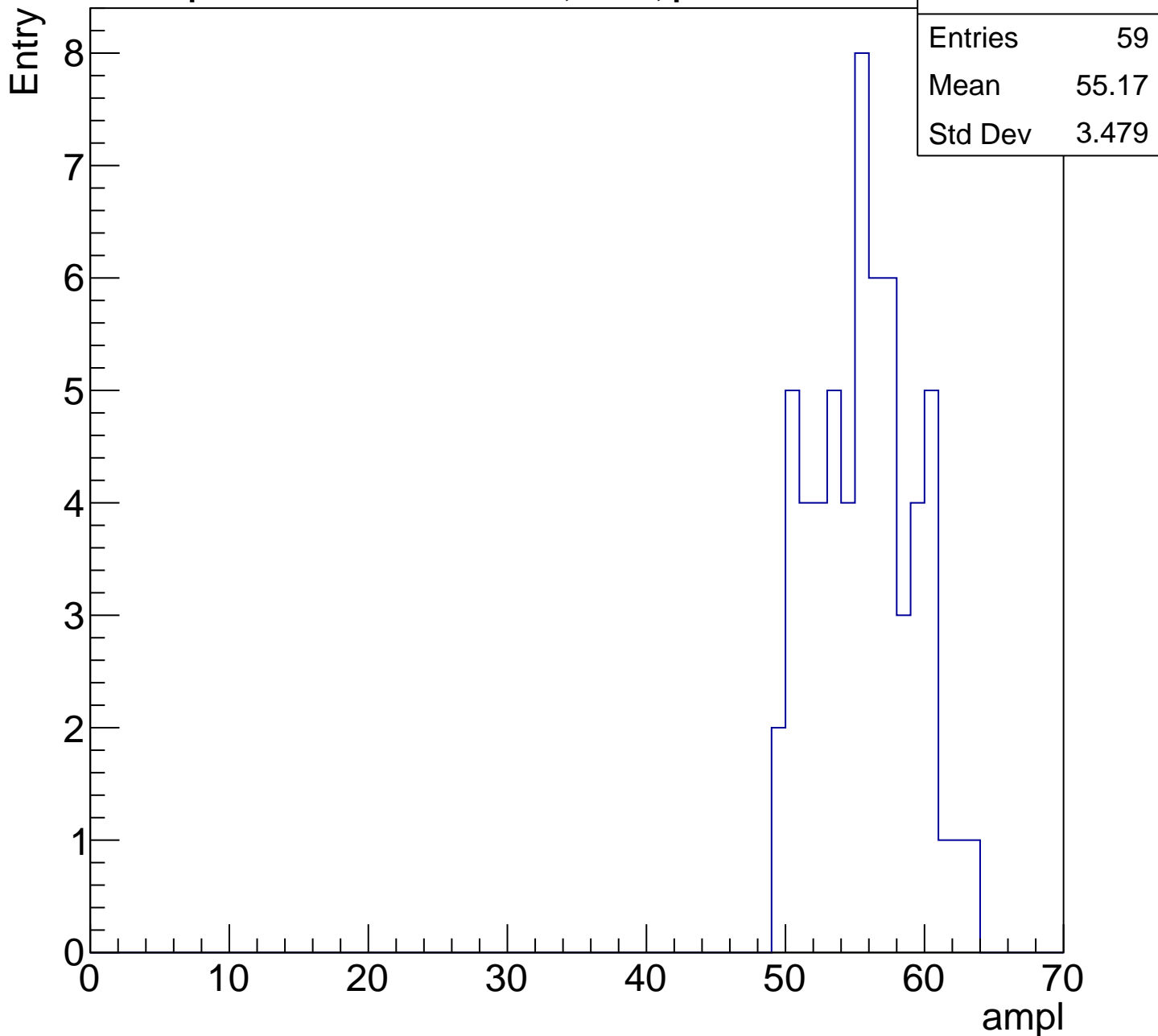
Entry

Entries	61
Mean	49.89
Std Dev	3.897



B1L103S, U5-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 58.6

Std Dev 9.309

ampl

0

10

20

30

40

50

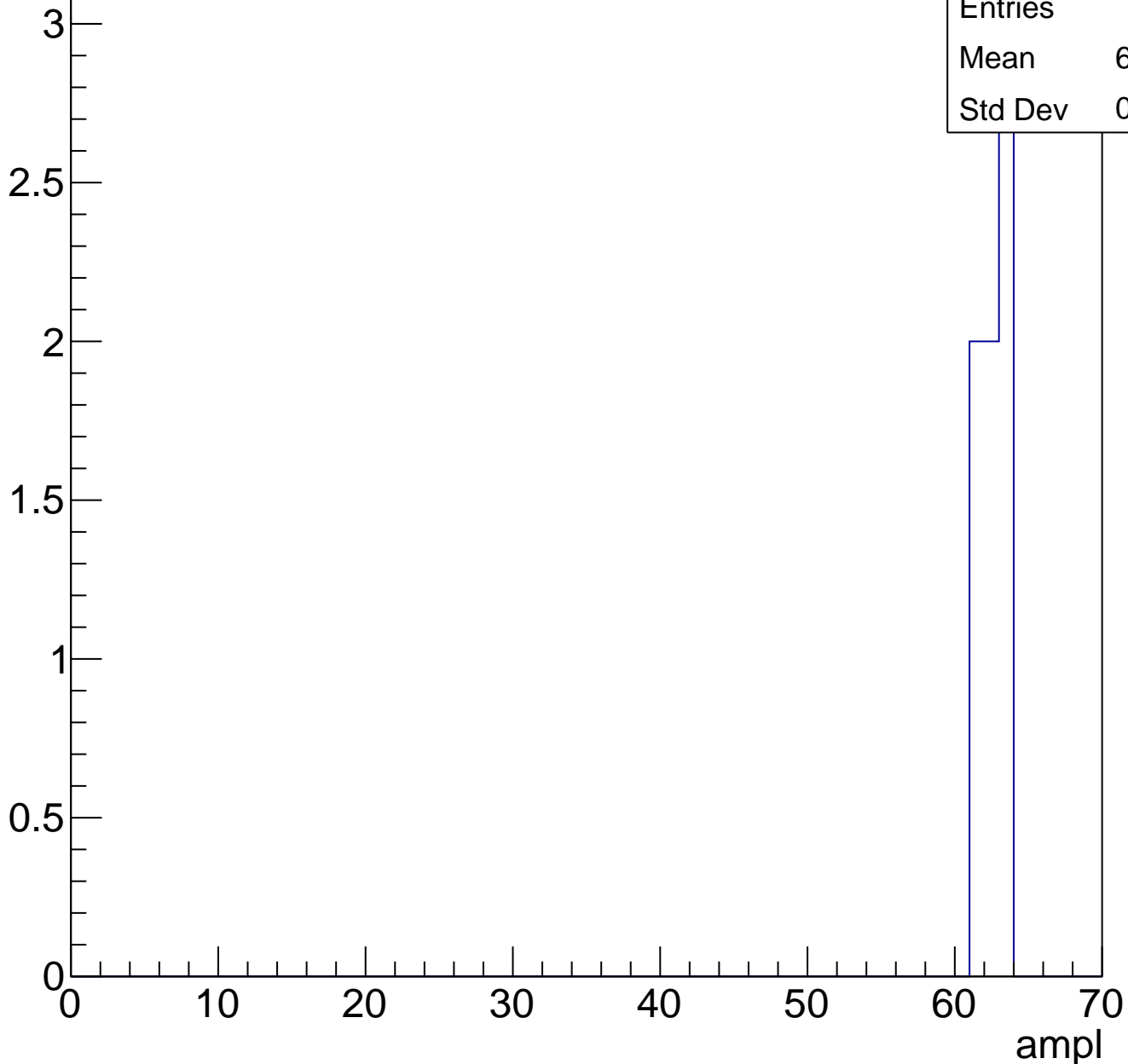
60

70

B1L103S, U5-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch70, adc0

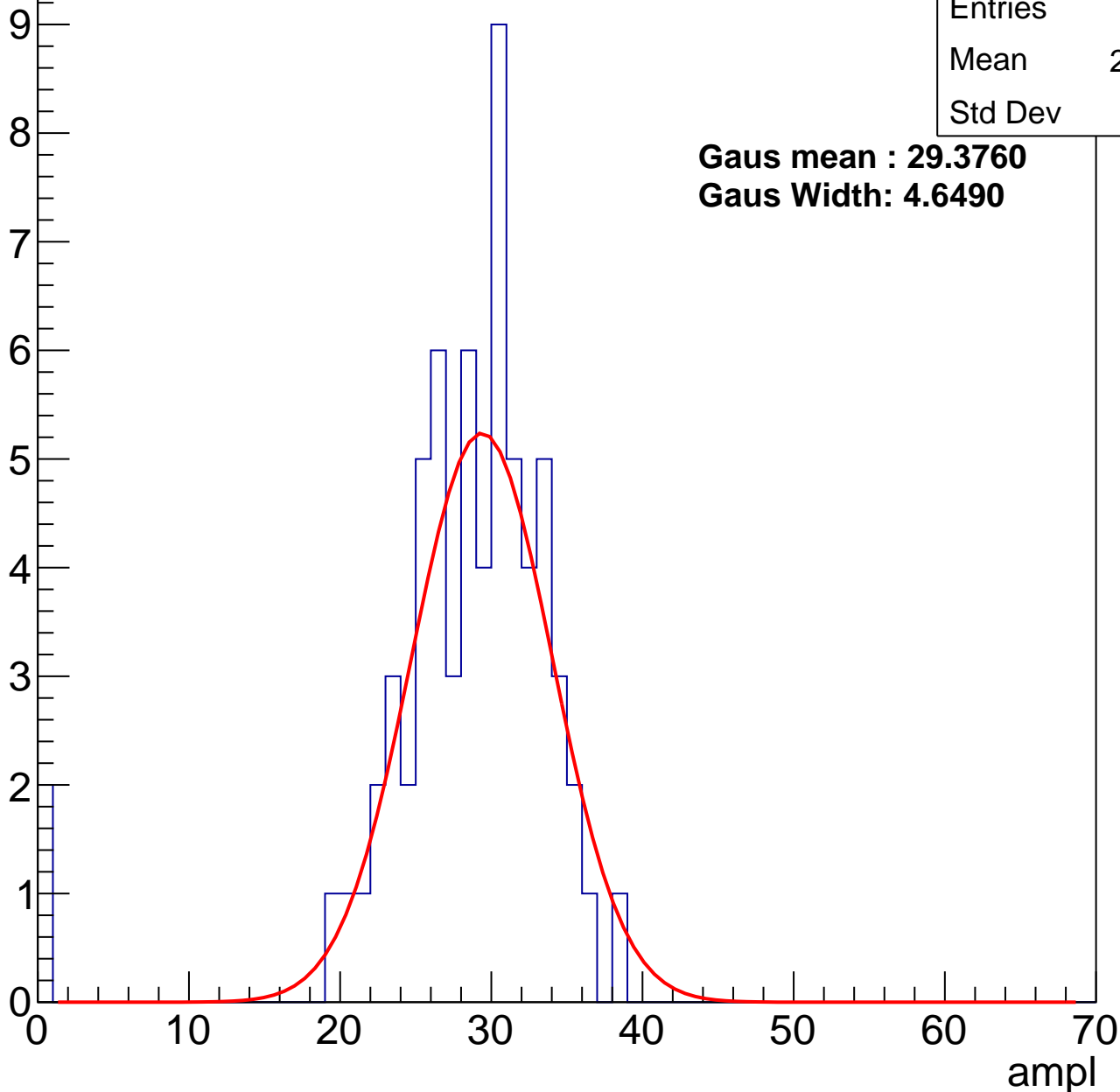
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	27.74
Std Dev	6.34

Gaus mean : 29.3760

Gaus Width: 4.6490



B1L103S, U5-ch70, adc1

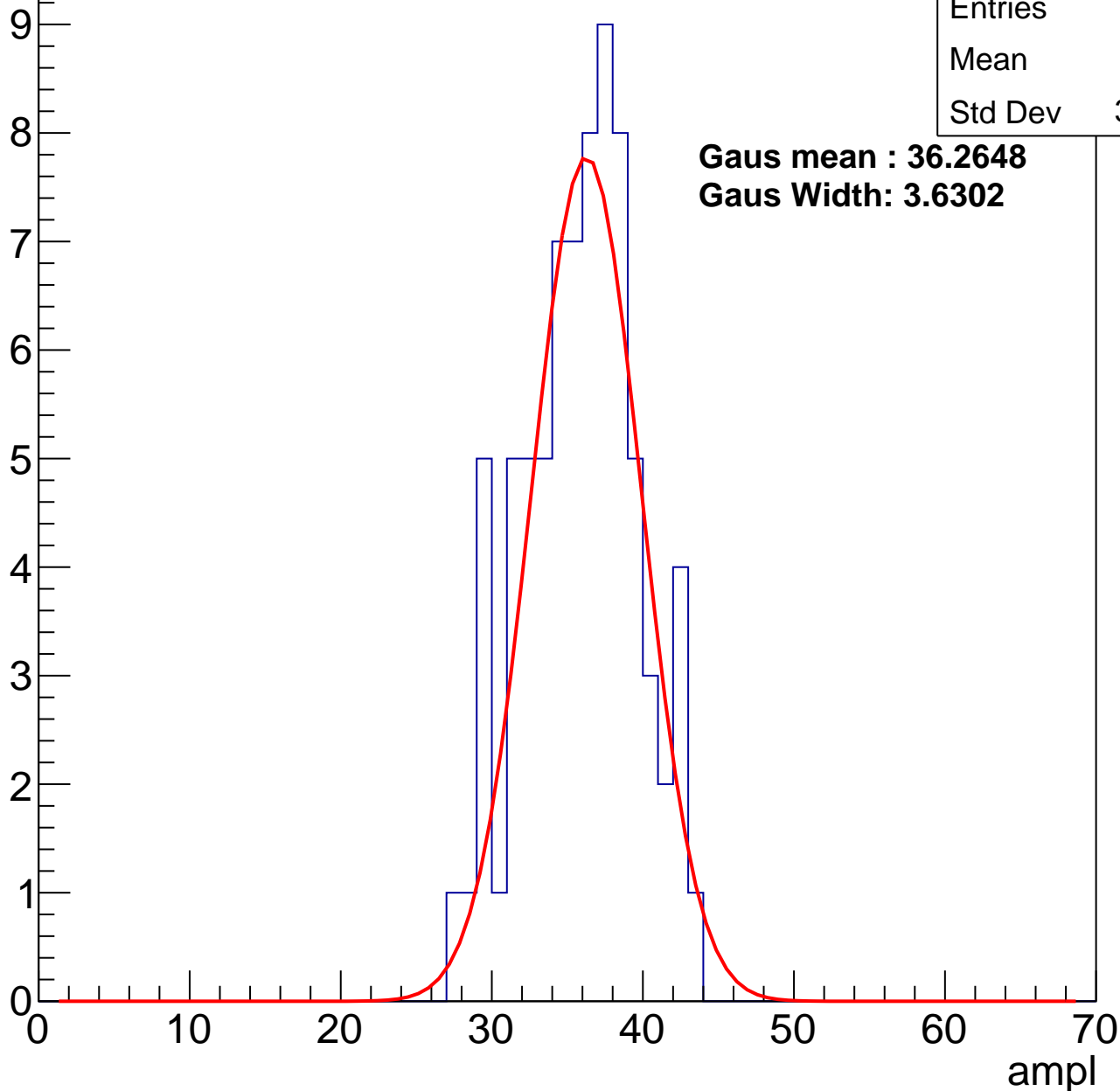
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	35.4
Std Dev	3.711

Gaus mean : 36.2648

Gaus Width: 3.6302



B1L103S, U5-ch70, adc2

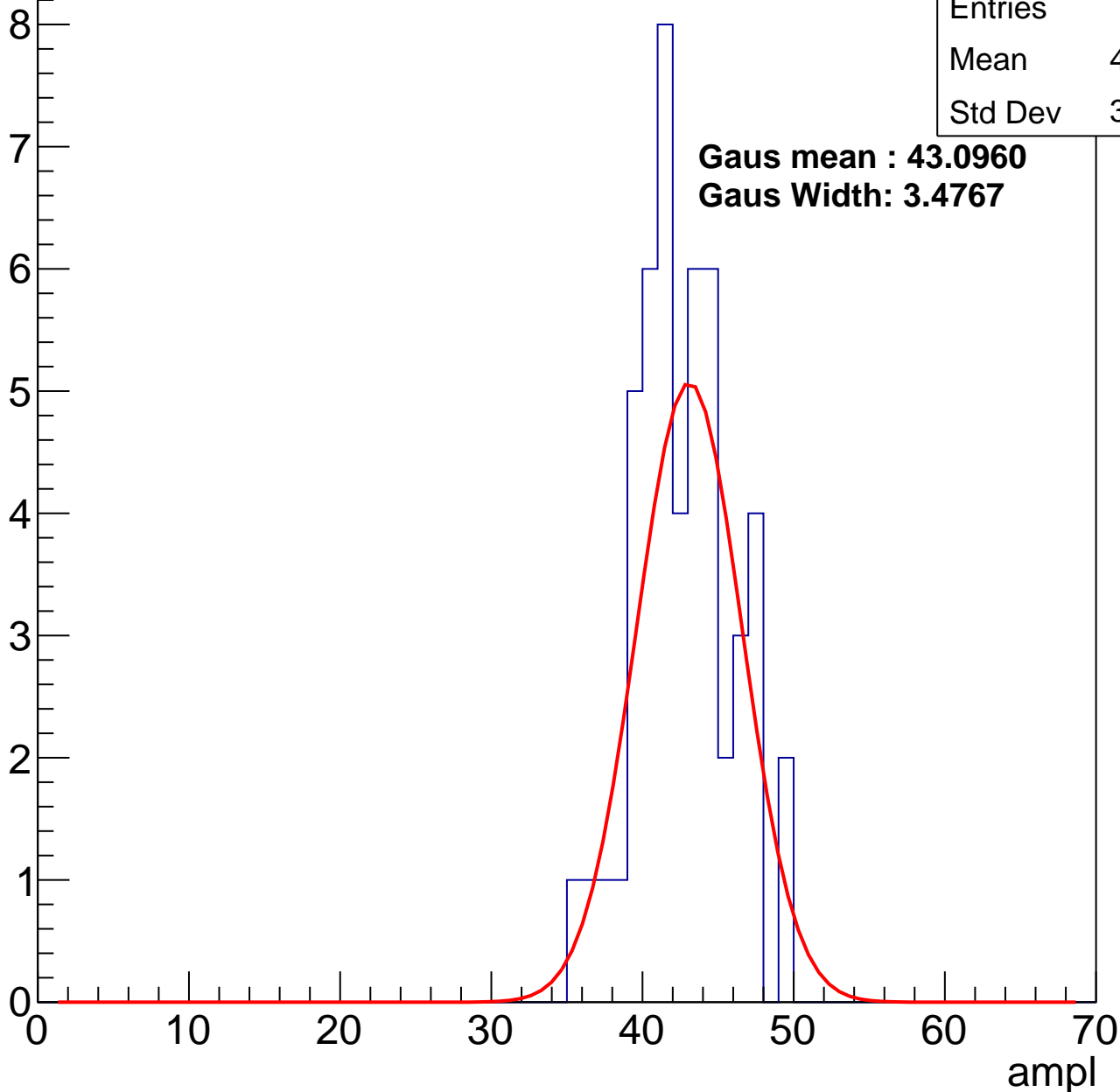
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.26
Std Dev	3.142

Gaus mean : 43.0960

Gaus Width: 3.4767

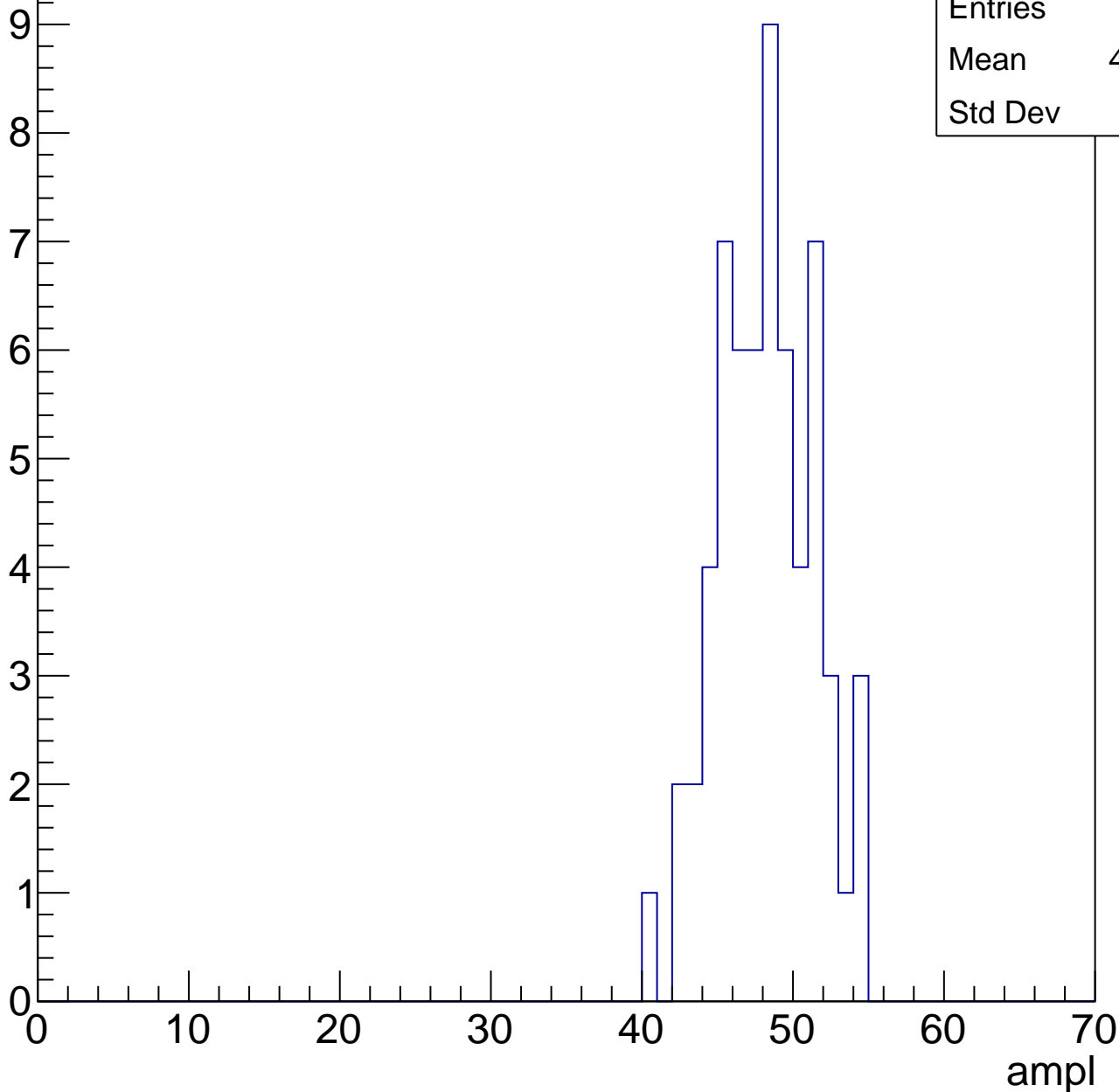


B1L103S, U5-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	47.75
Std Dev	3.15

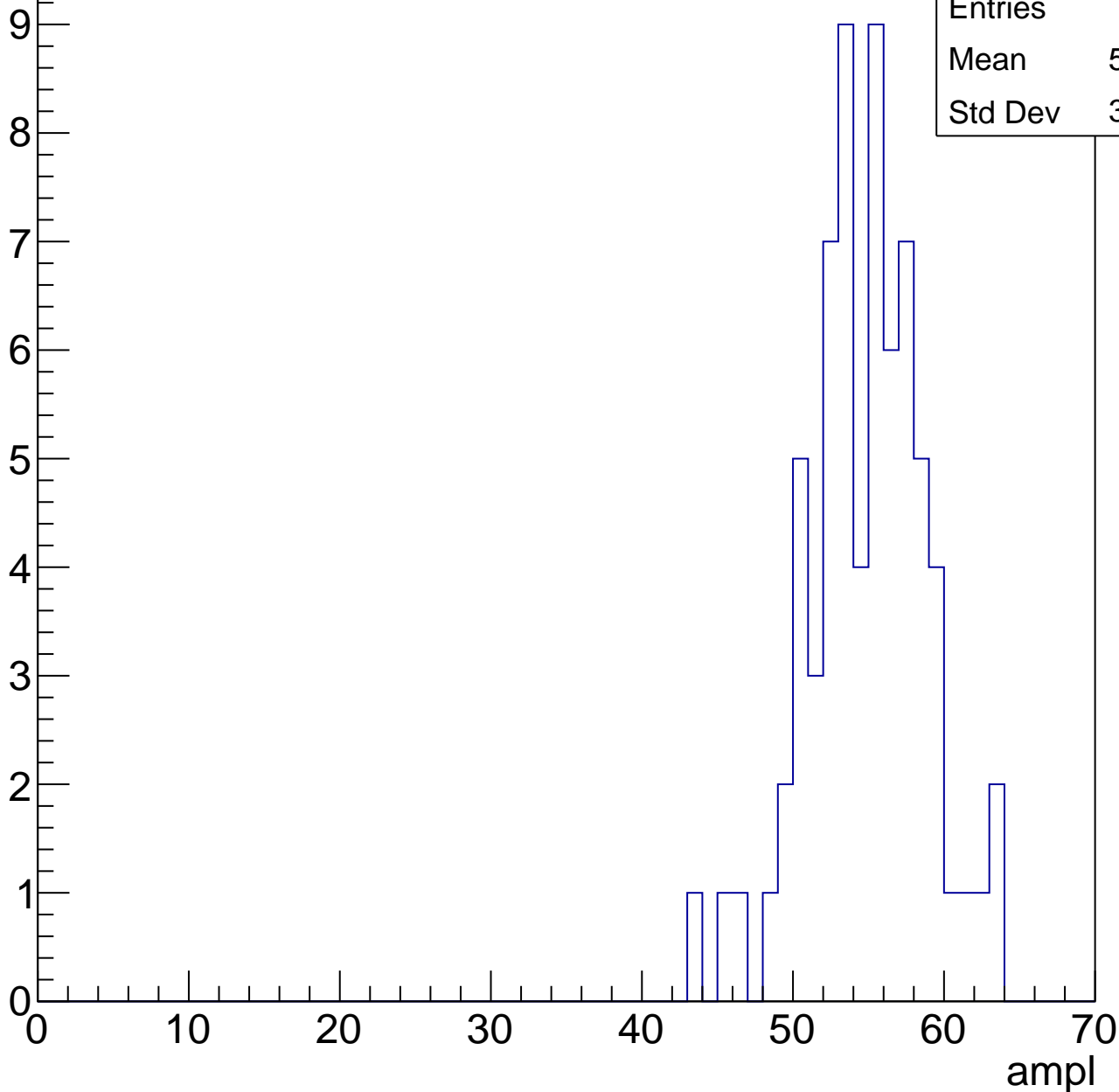


B1L103S, U5-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	54.36
Std Dev	3.917



B1L103S, U5-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

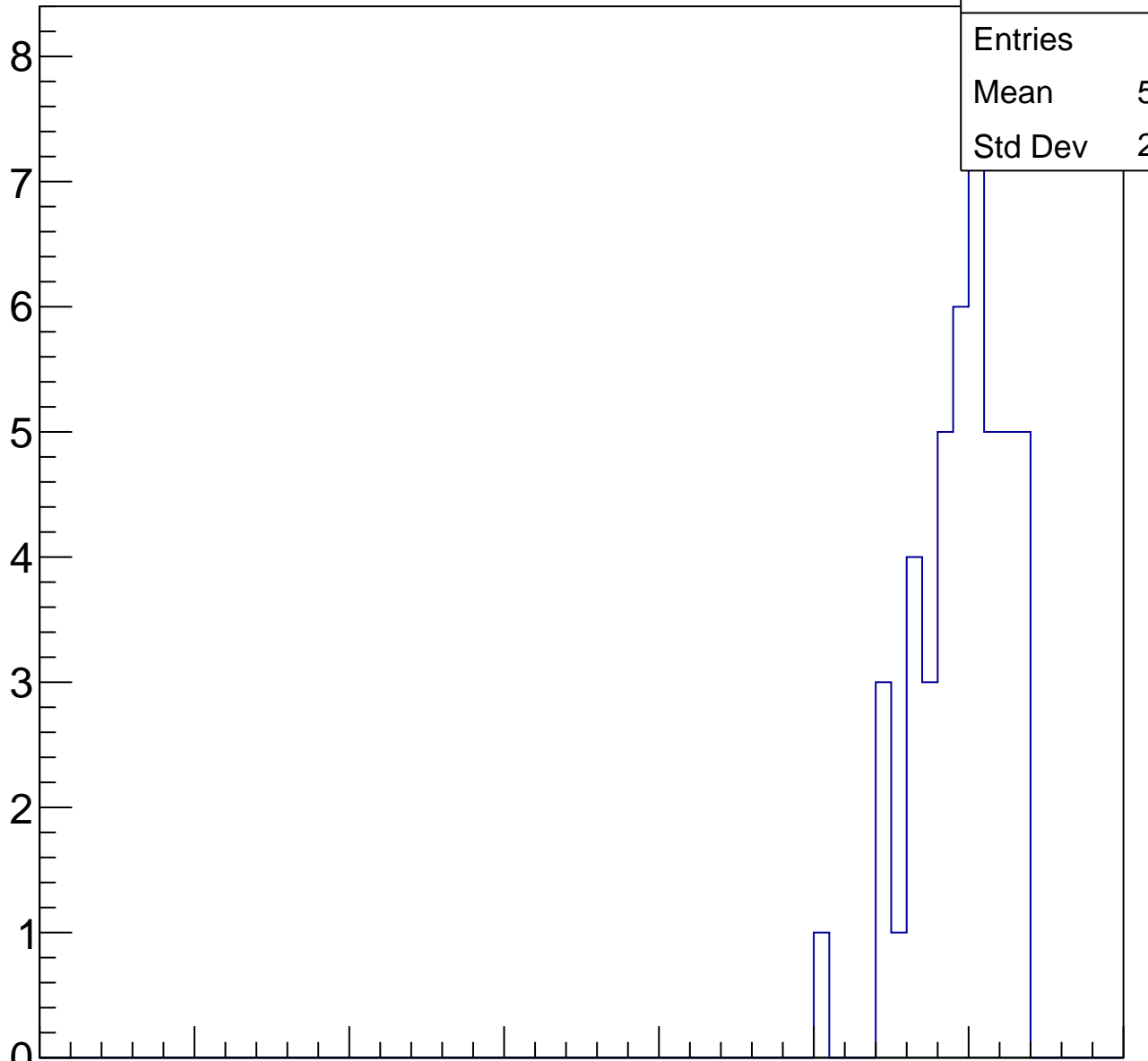
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.04
Std Dev	2.866

ampl

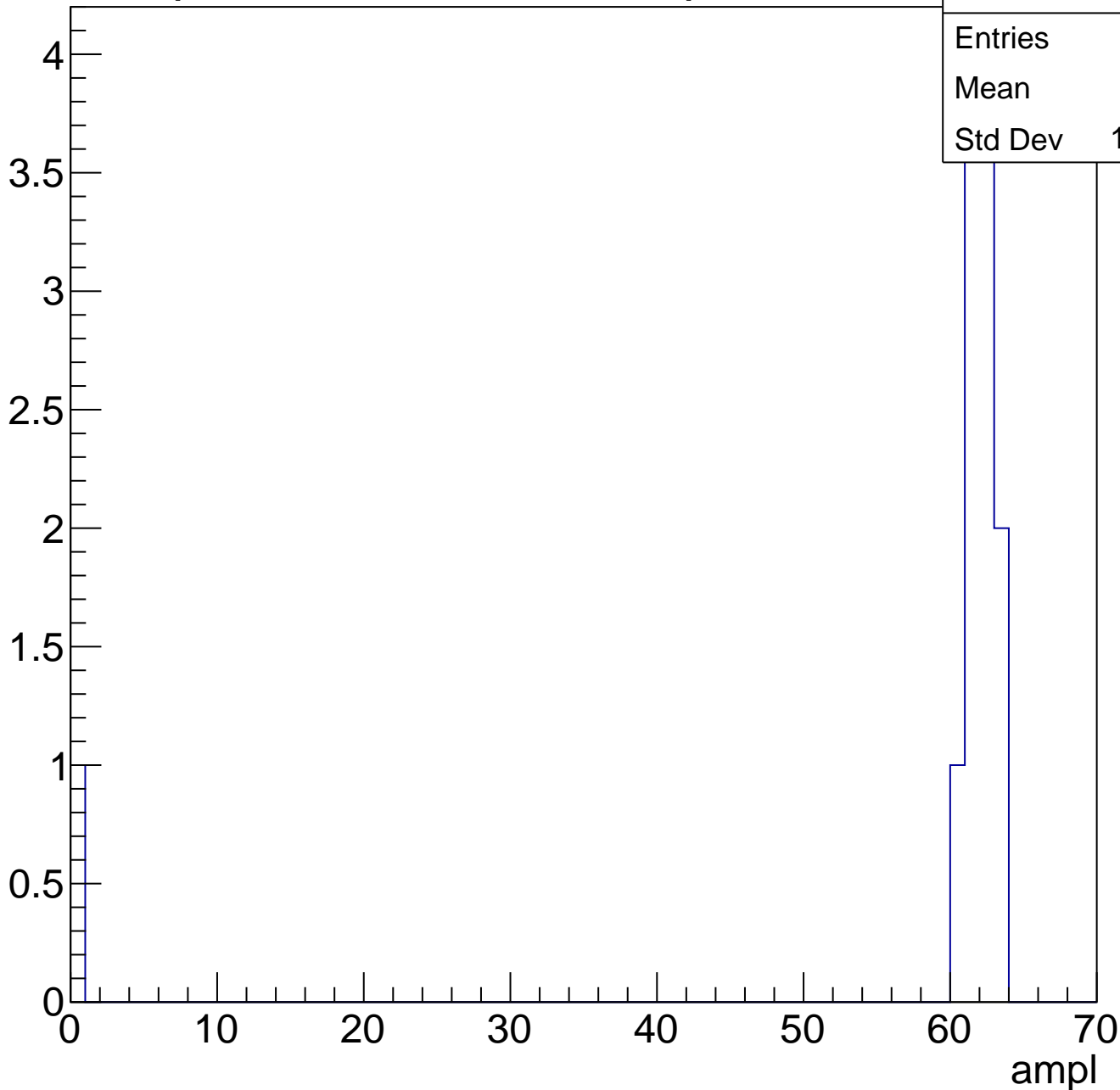
0 10 20 30 40 50 60 70



B1L103S, U5-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	12
Mean	56.5
Std Dev	17.06

B1L103S, U5-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch71, adc0

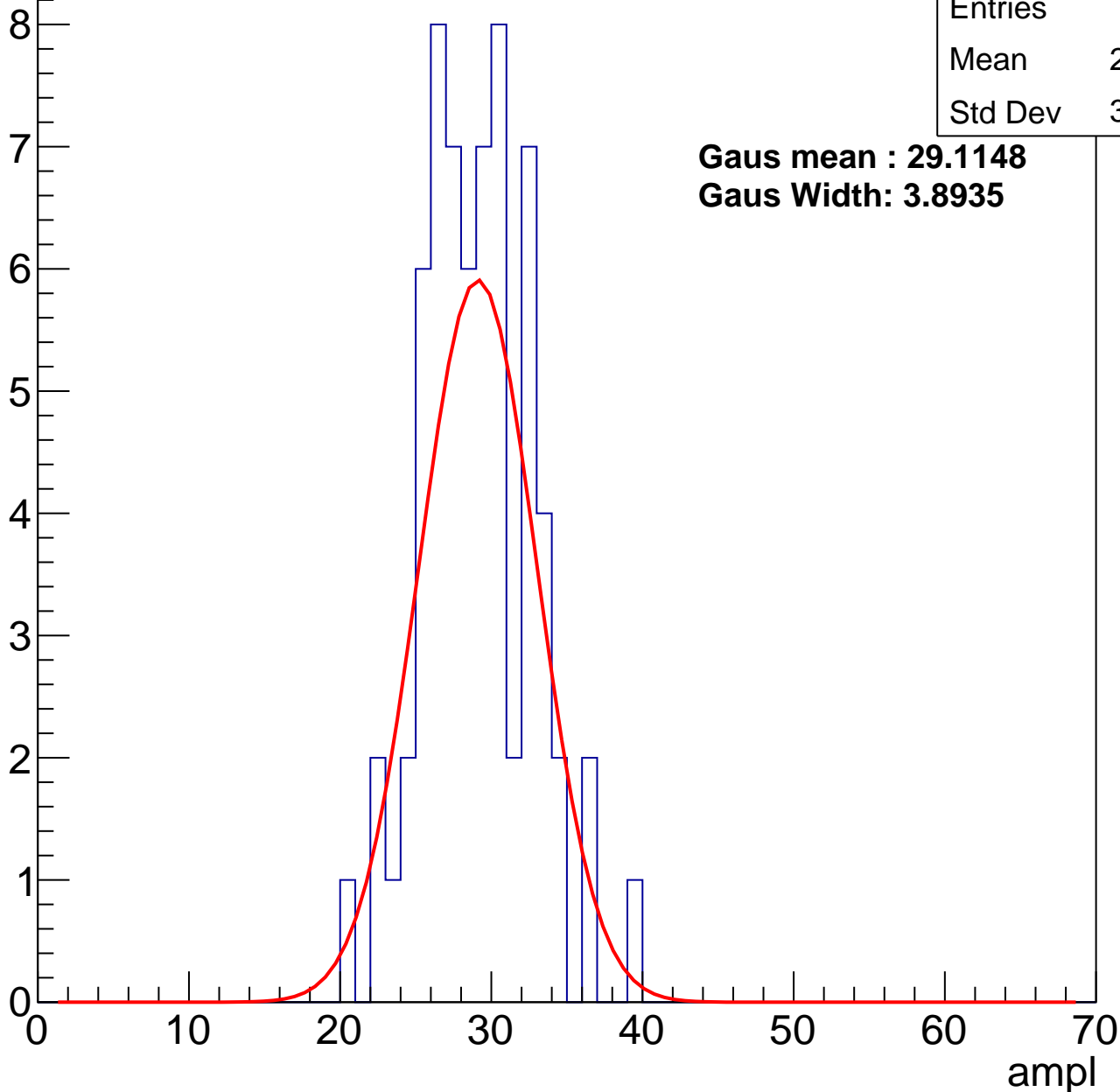
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.64
Std Dev	3.587

Gaus mean : 29.1148

Gaus Width: 3.8935



B1L103S, U5-ch71, adc1

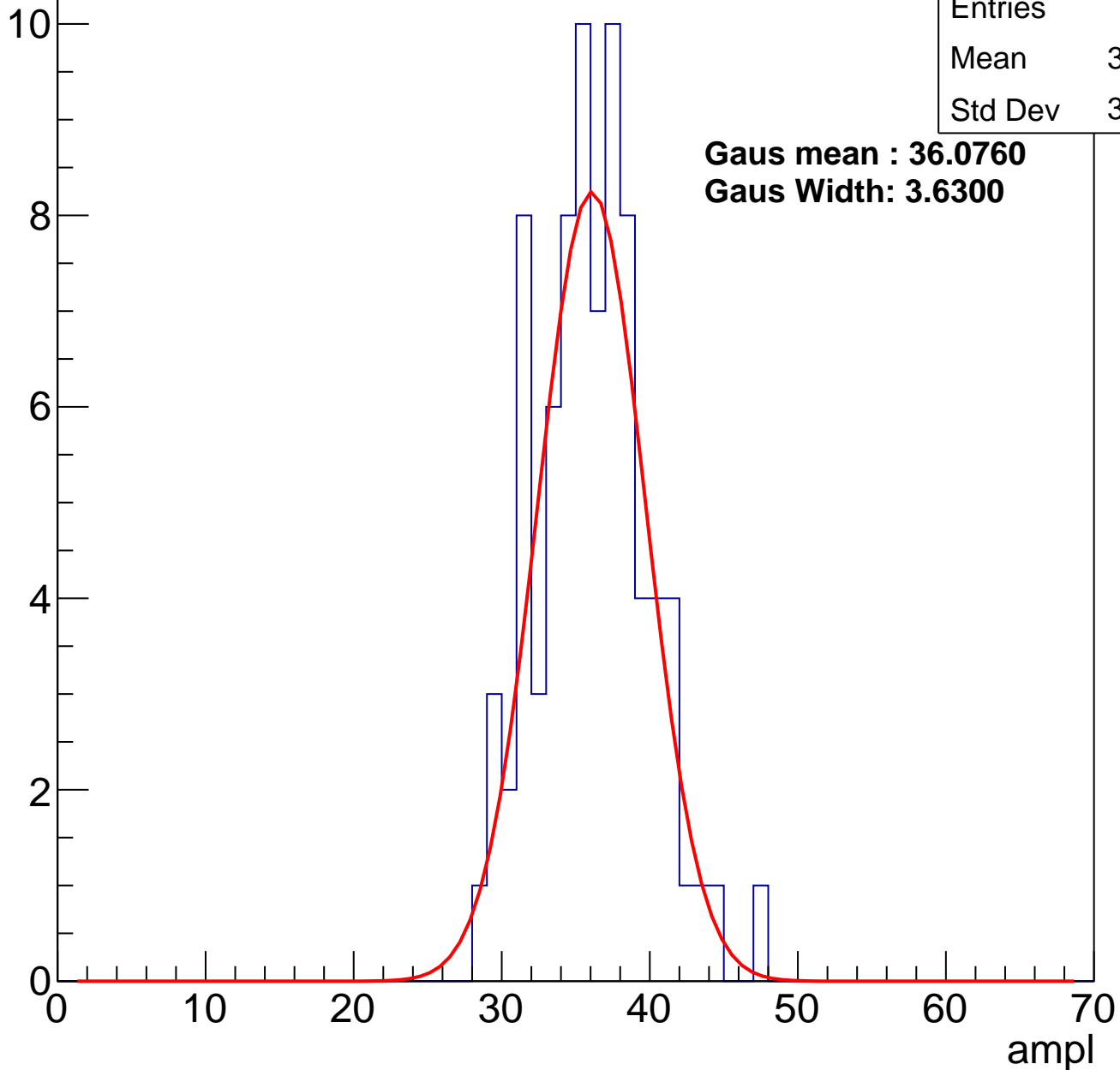
calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	35.62
Std Dev	3.708

Gaus mean : 36.0760

Gaus Width: 3.6300

Entry



B1L103S, U5-ch71, adc2

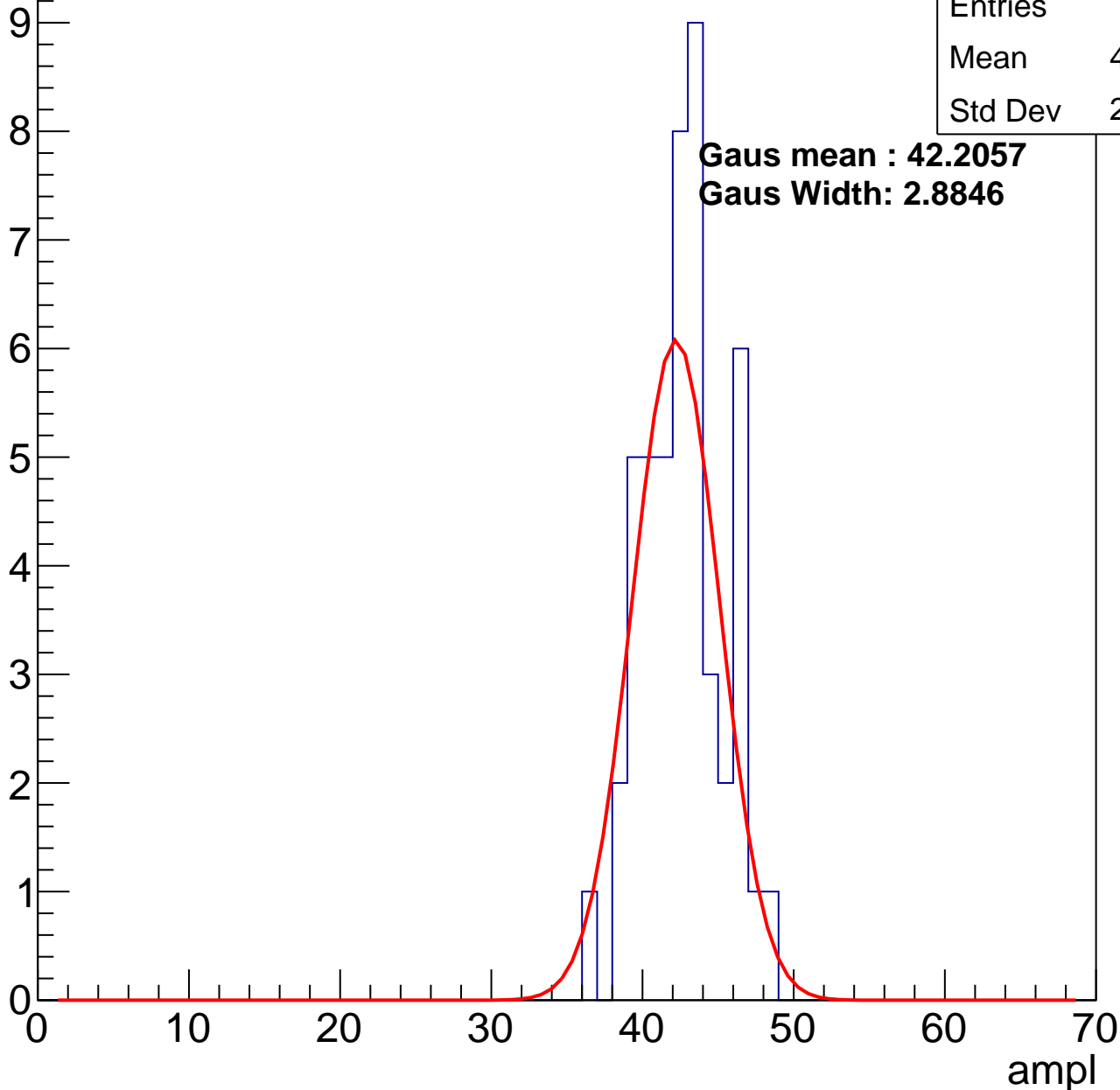
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	42.25
Std Dev	2.626

Gaus mean : 42.2057

Gaus Width: 2.8846

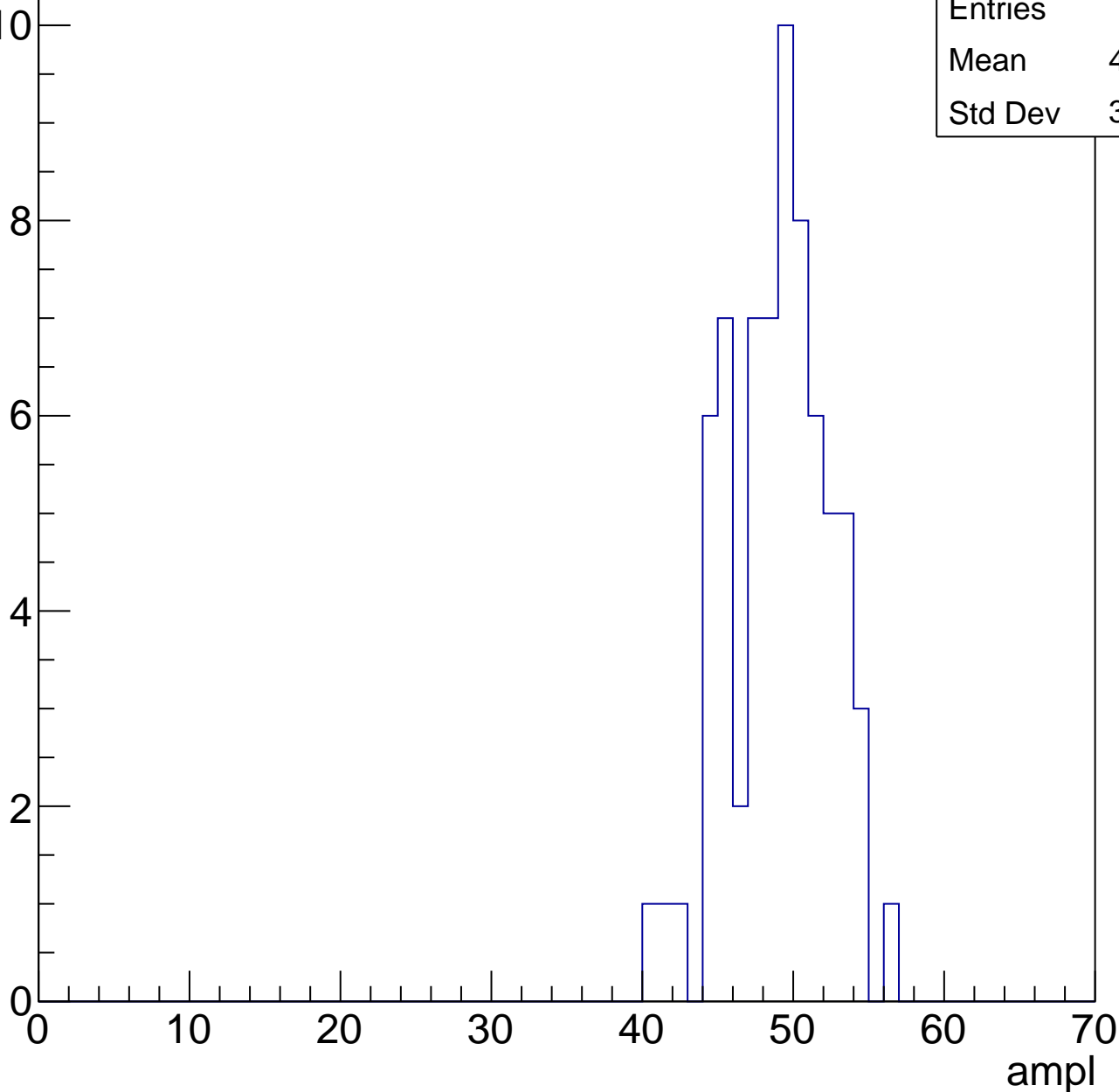


B1L103S, U5-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	48.54
Std Dev	3.328

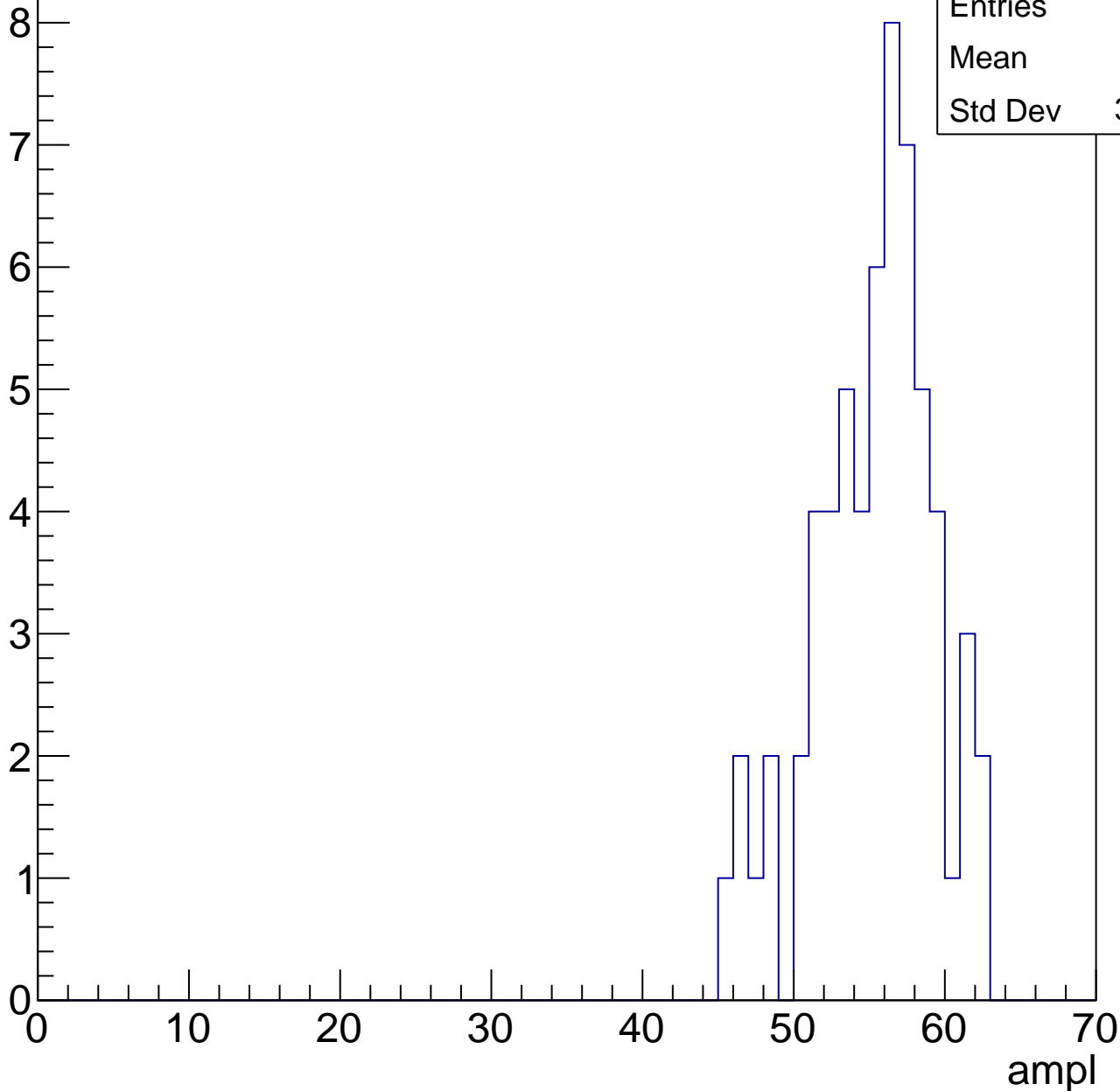


B1L103S, U5-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	54.8
Std Dev	3.991



B1L103S, U5-ch71, adc5

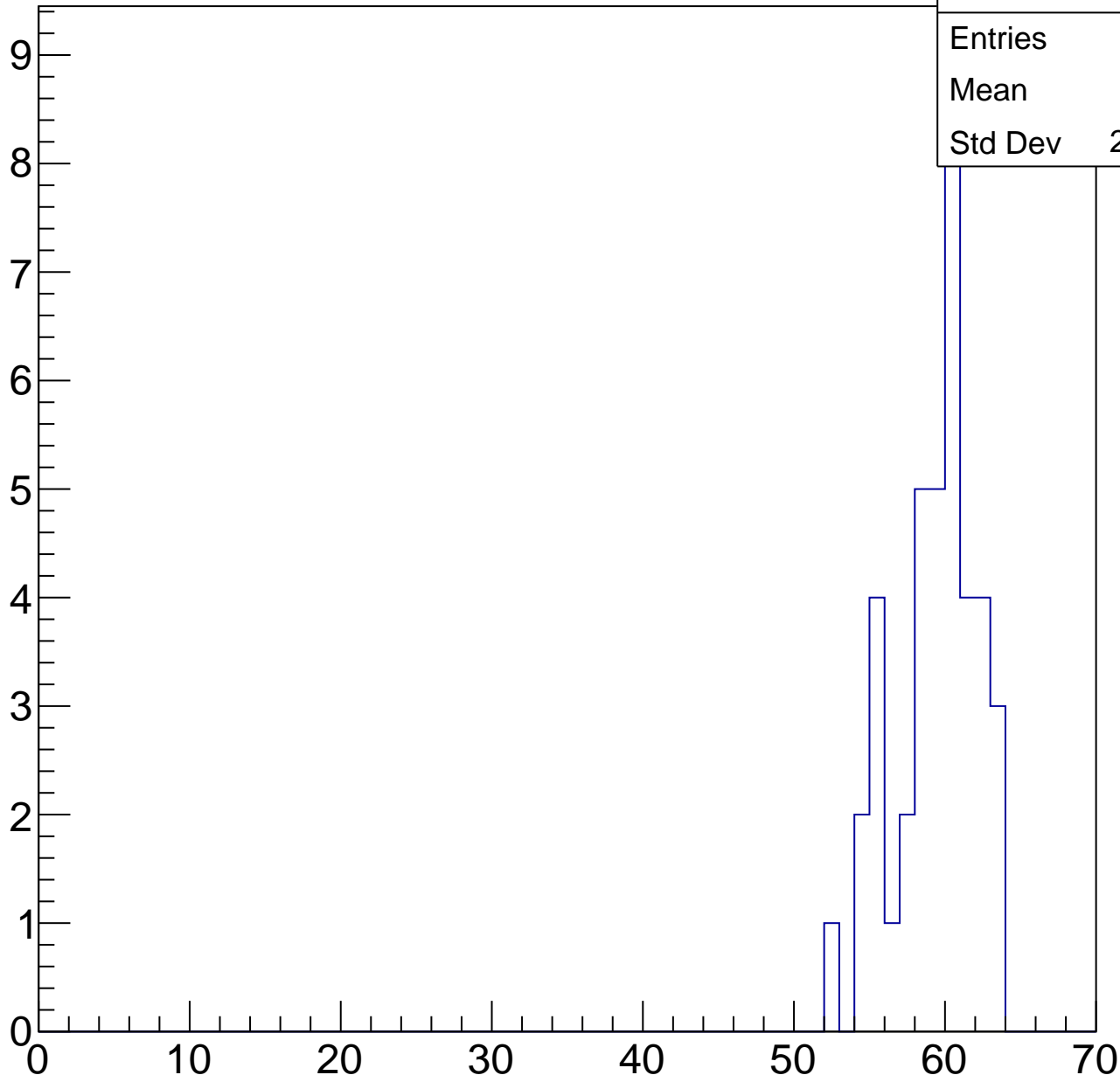
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.9
Std Dev	2.709

ampl

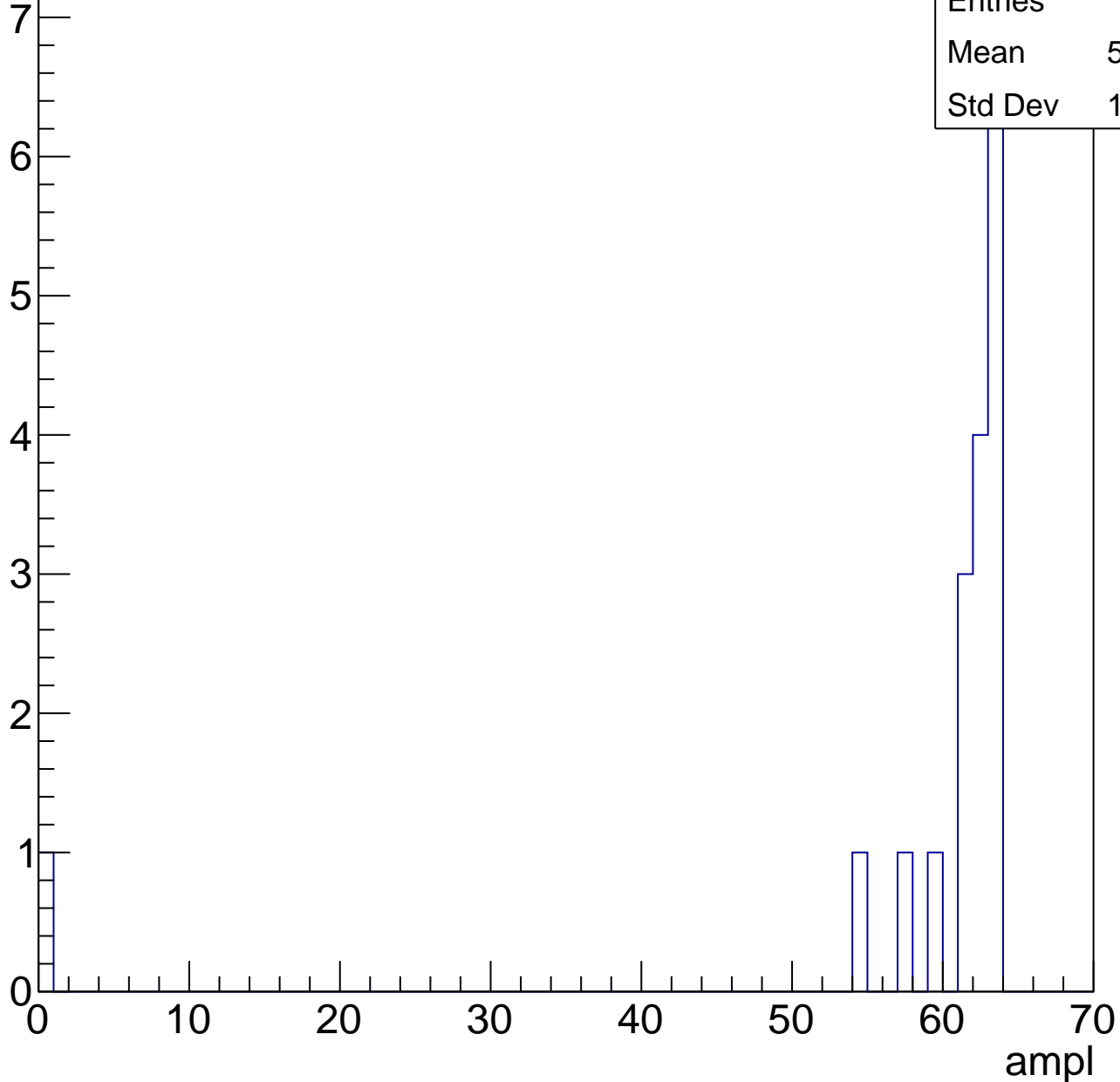


B1L103S, U5-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	18
Mean	57.89
Std Dev	14.24



B1L103S, U5-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch72, adc0

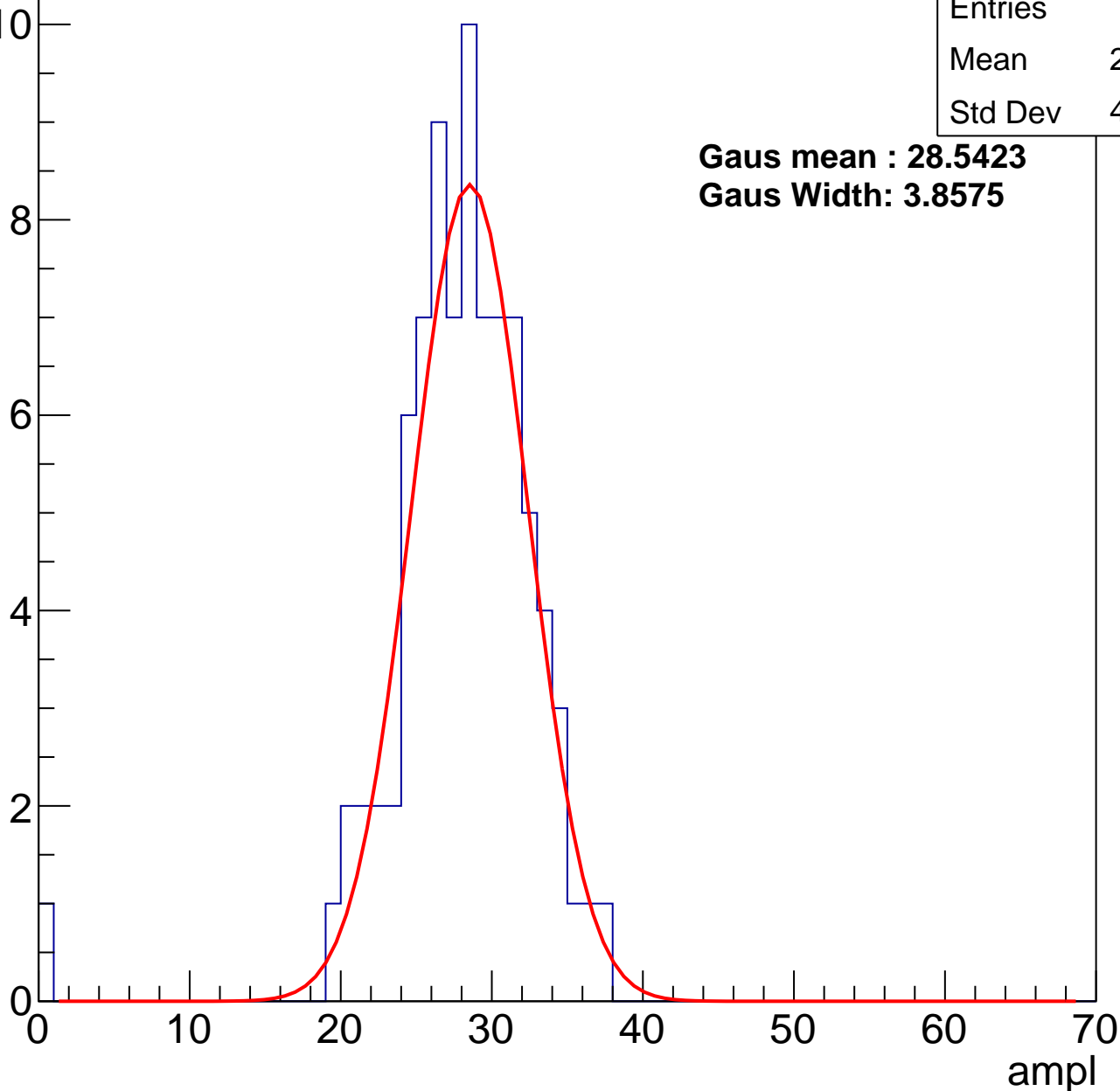
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	27.59
Std Dev	4.827

Gaus mean : 28.5423

Gaus Width: 3.8575



B1L103S, U5-ch72, adc1

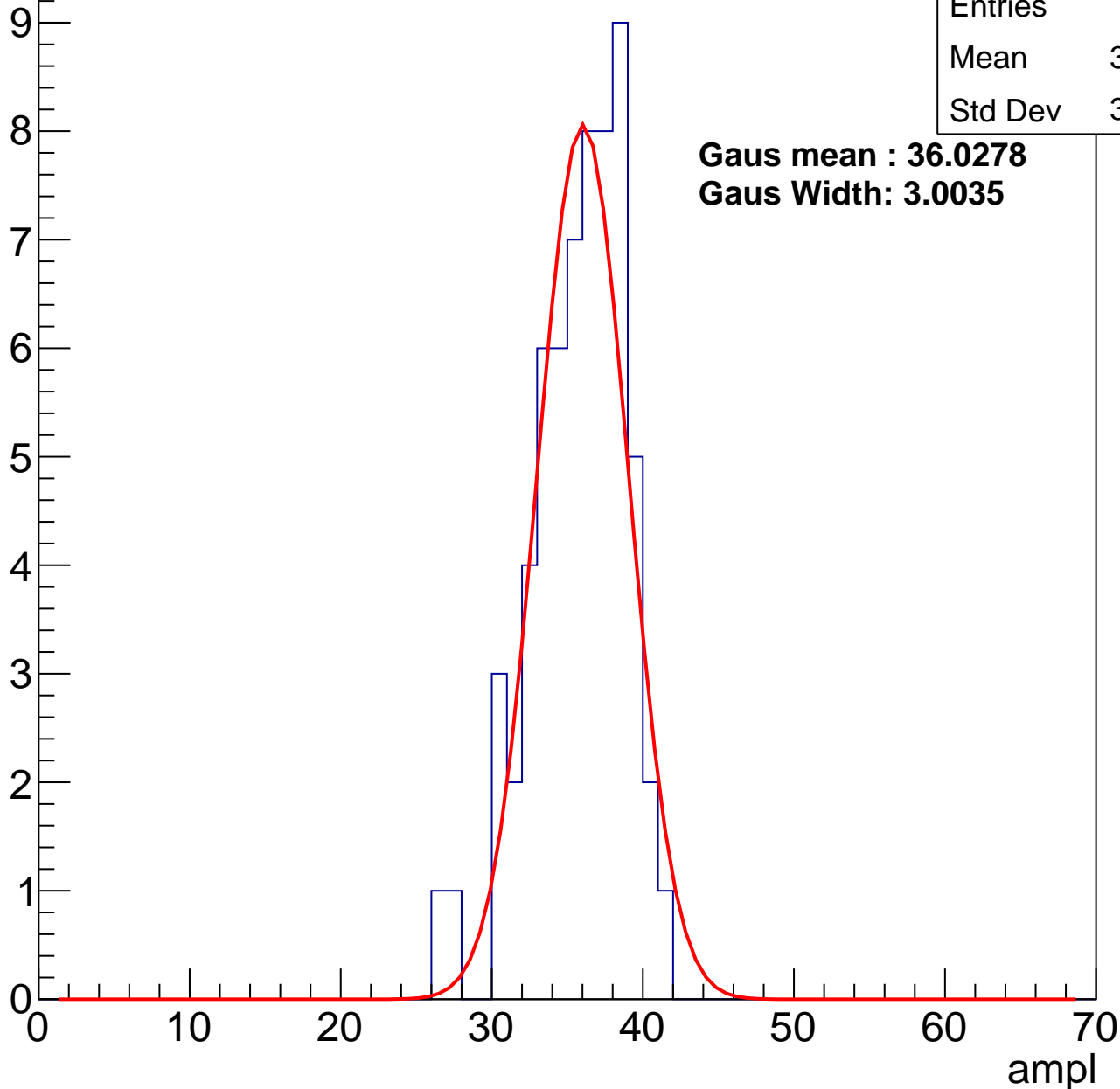
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.27
Std Dev	3.087

Gaus mean : 36.0278

Gaus Width: 3.0035



B1L103S, U5-ch72, adc2

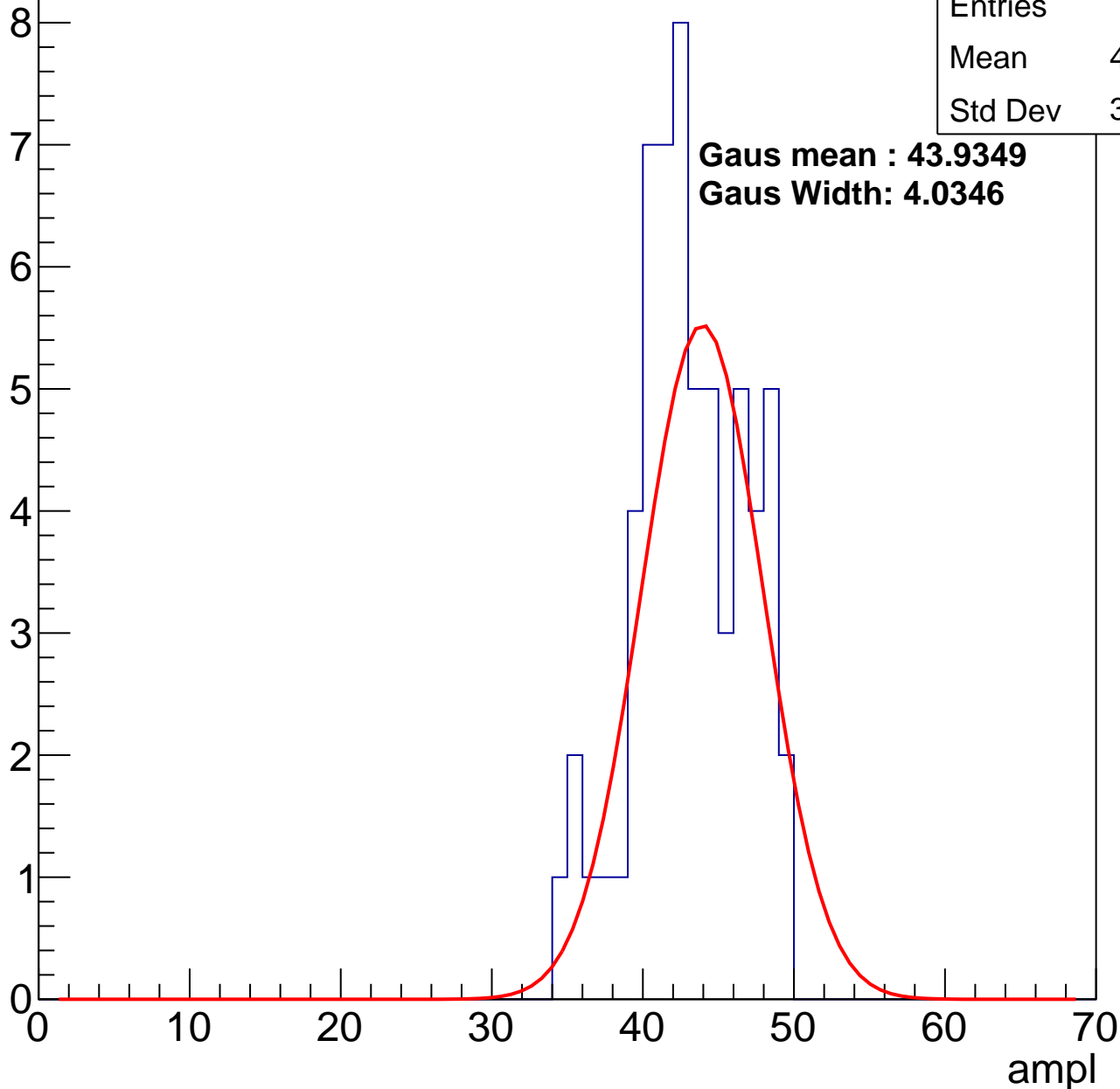
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.62
Std Dev	3.613

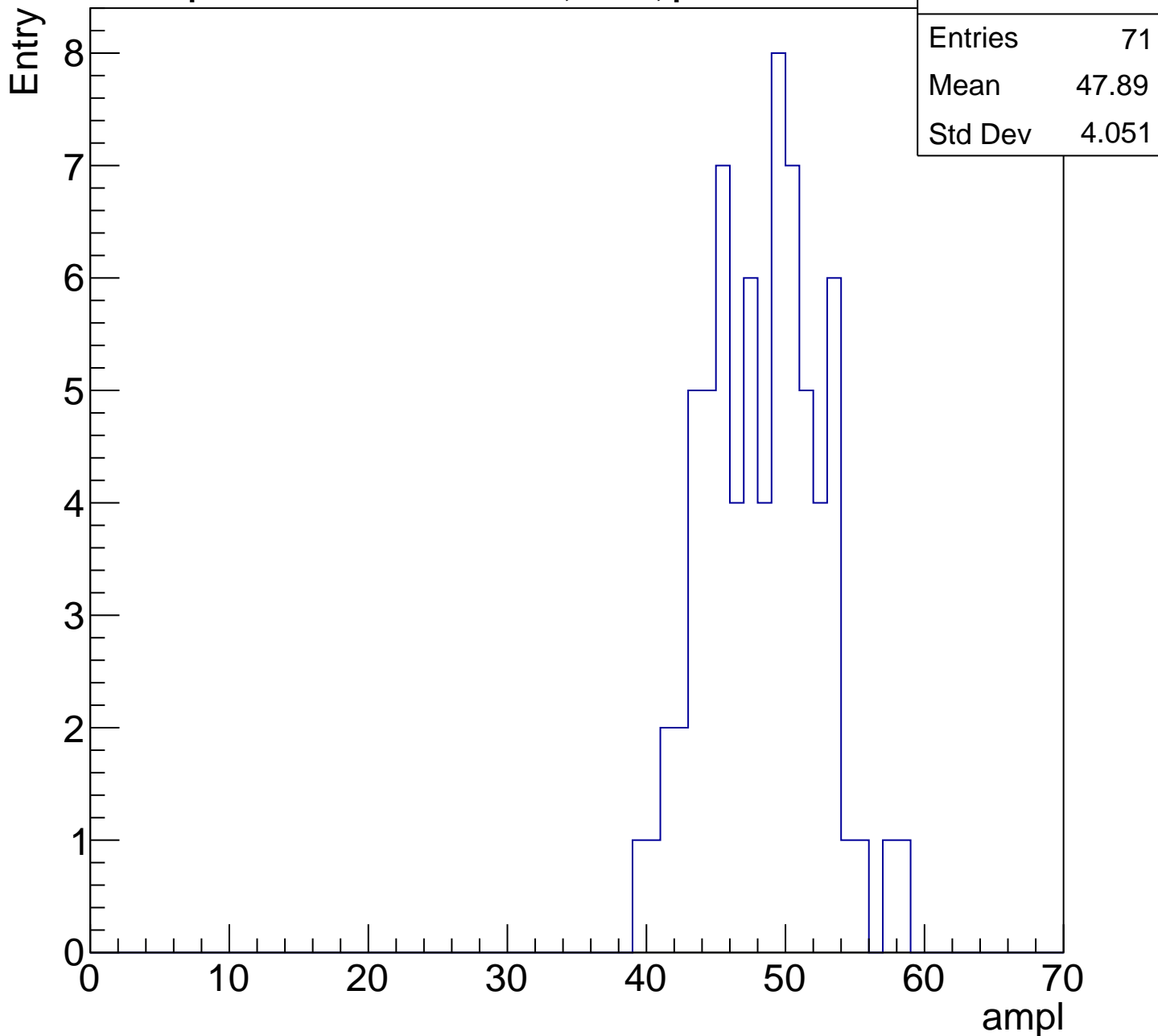
Gaus mean : 43.9349

Gaus Width: 4.0346



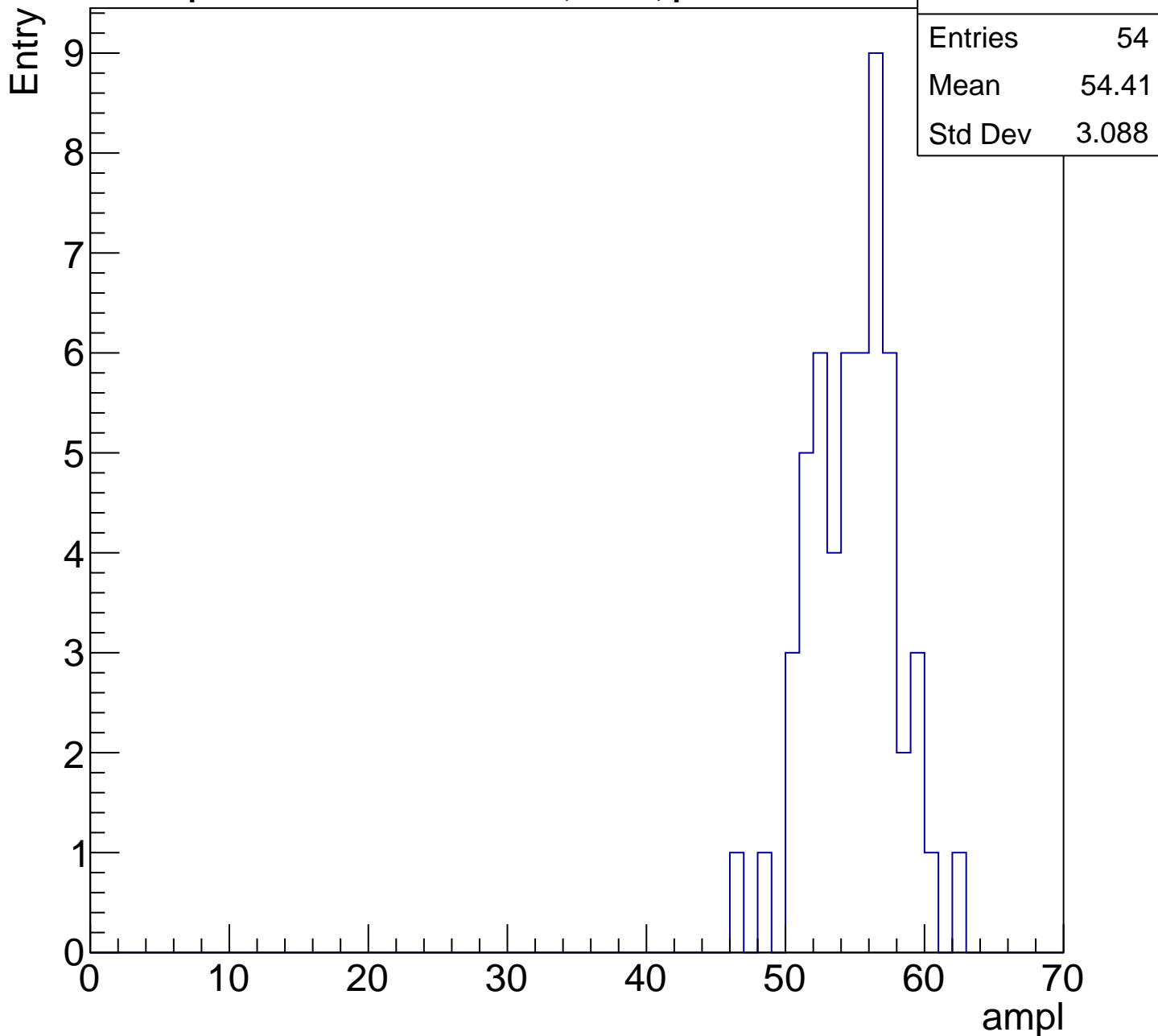
B1L103S, U5-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

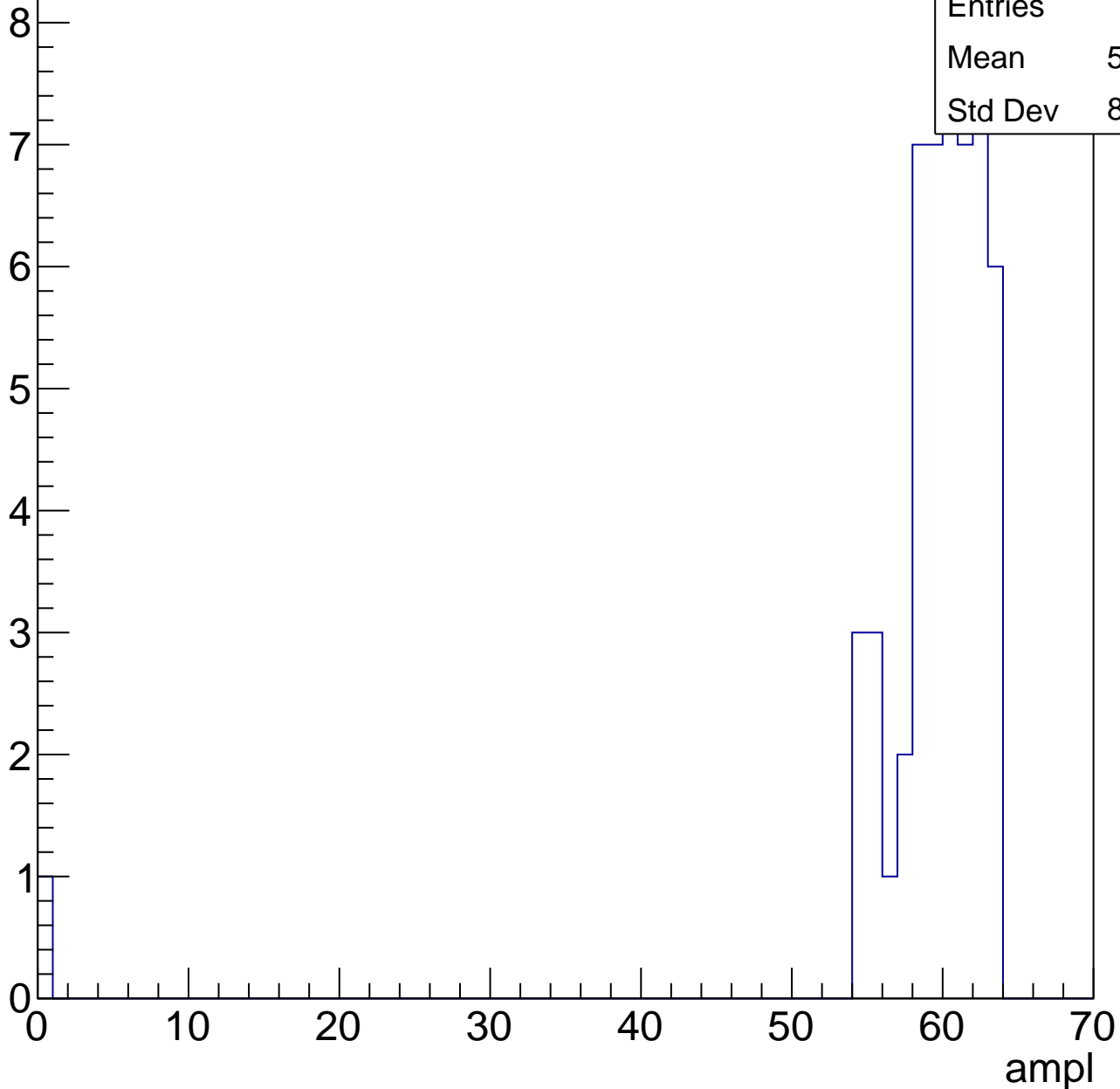


B1L103S, U5-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

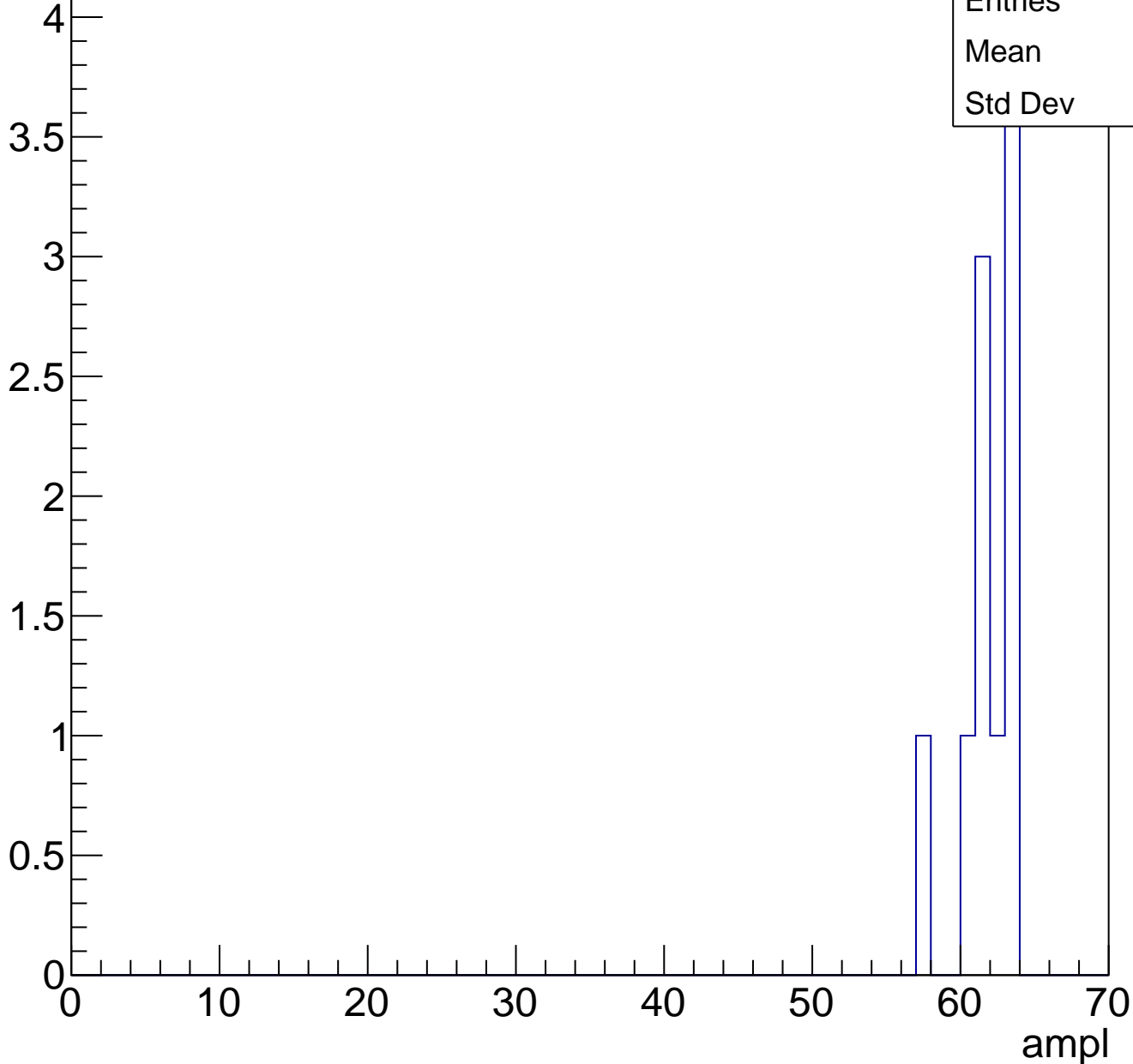
Entries	53
Mean	58.43
Std Dev	8.484



B1L103S, U5-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch73, adc0

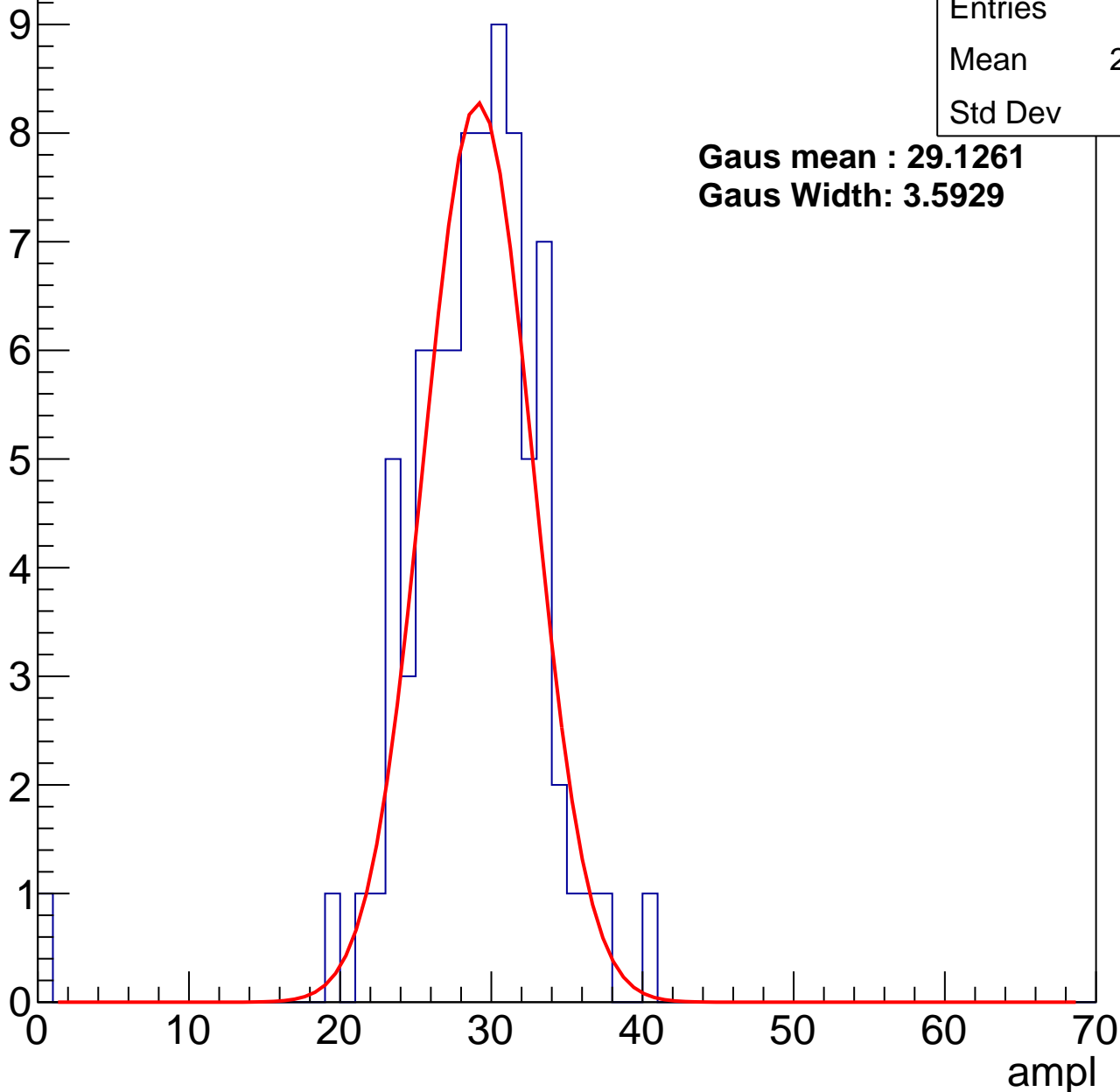
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	28.37
Std Dev	4.94

Gaus mean : 29.1261

Gaus Width: 3.5929



B1L103S, U5-ch73, adc1

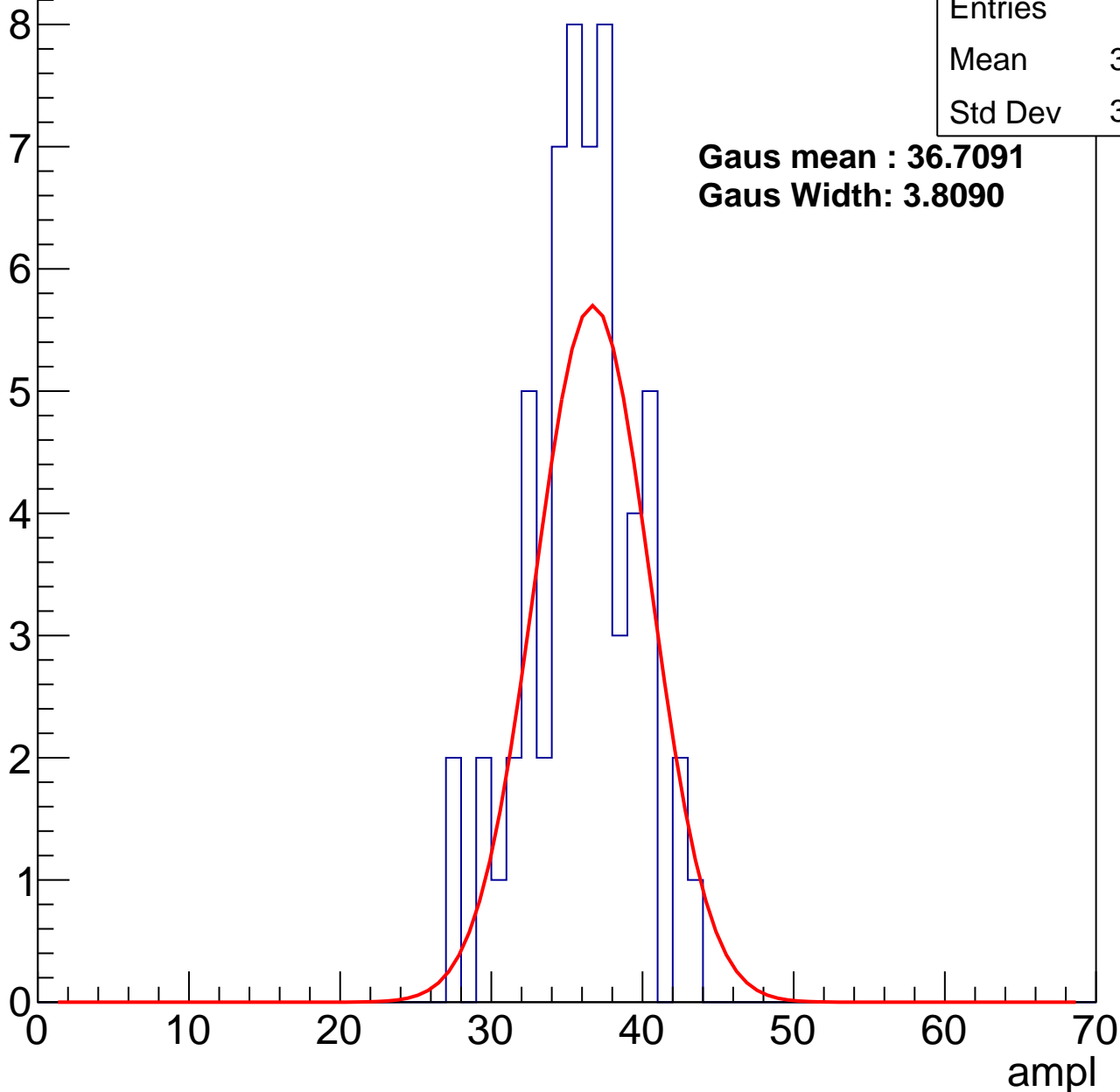
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	35.47
Std Dev	3.495

Gaus mean : 36.7091

Gaus Width: 3.8090



B1L103S, U5-ch73, adc2

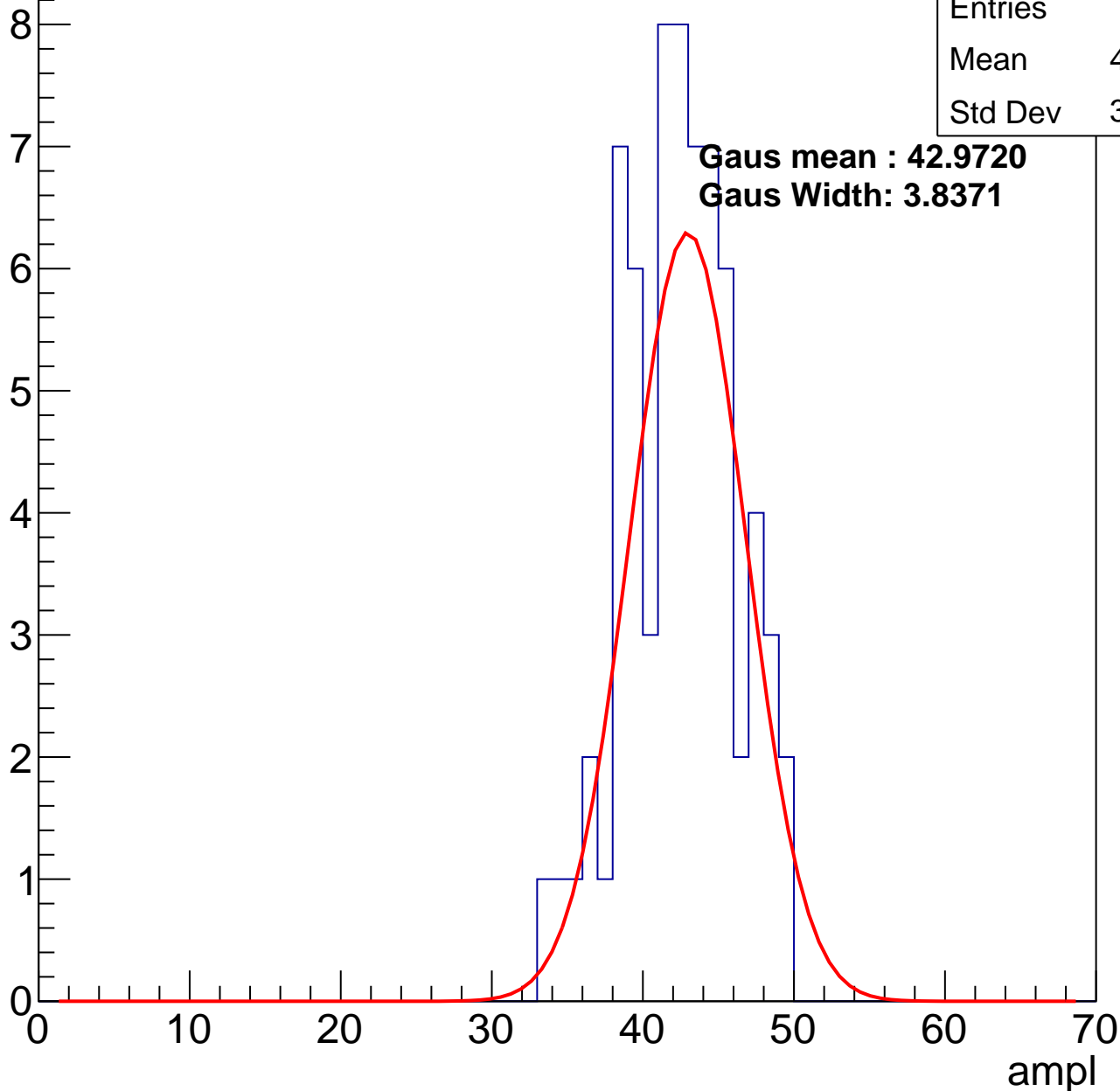
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	41.97
Std Dev	3.619

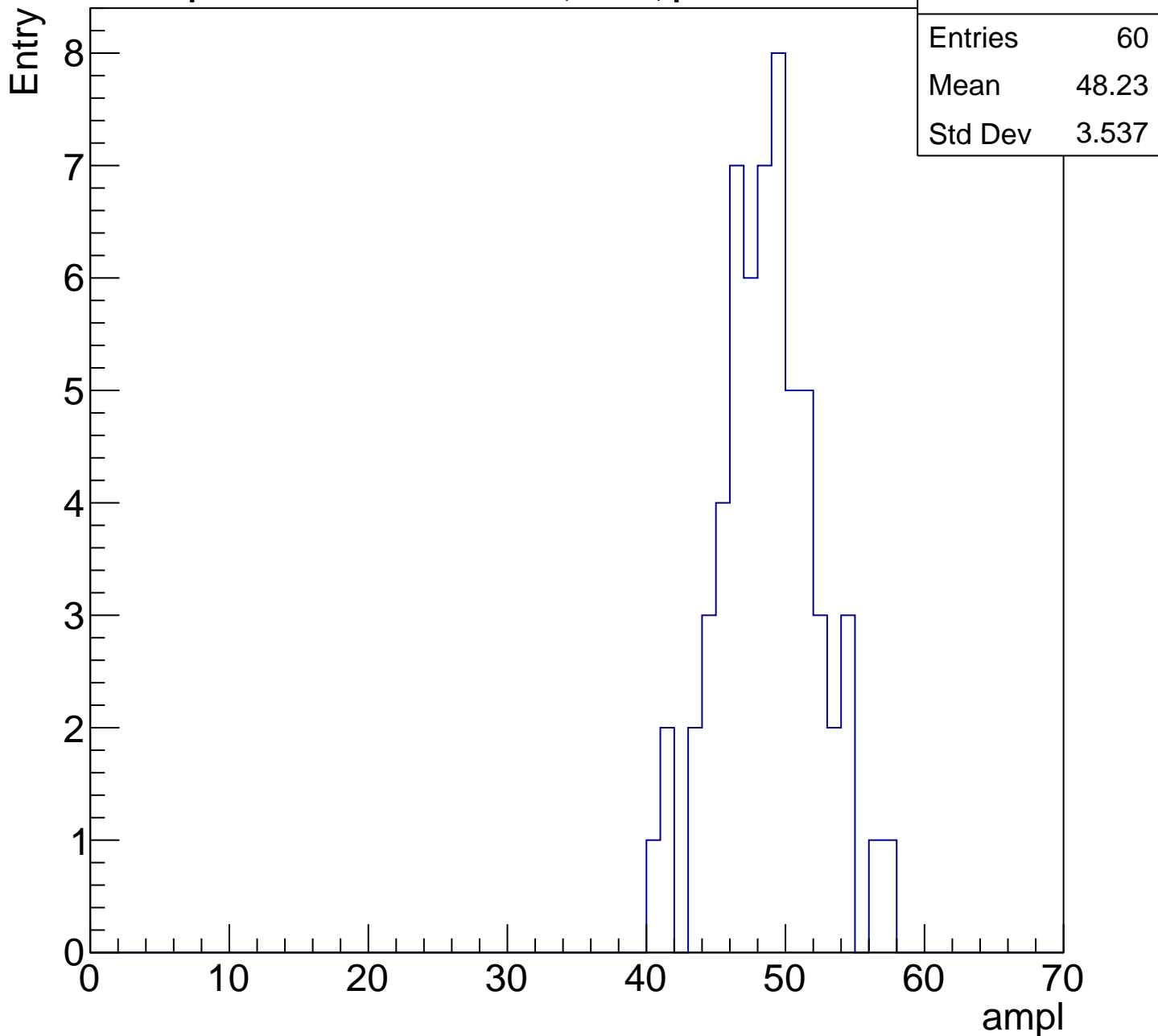
Gaus mean : 42.9720

Gaus Width: 3.8371



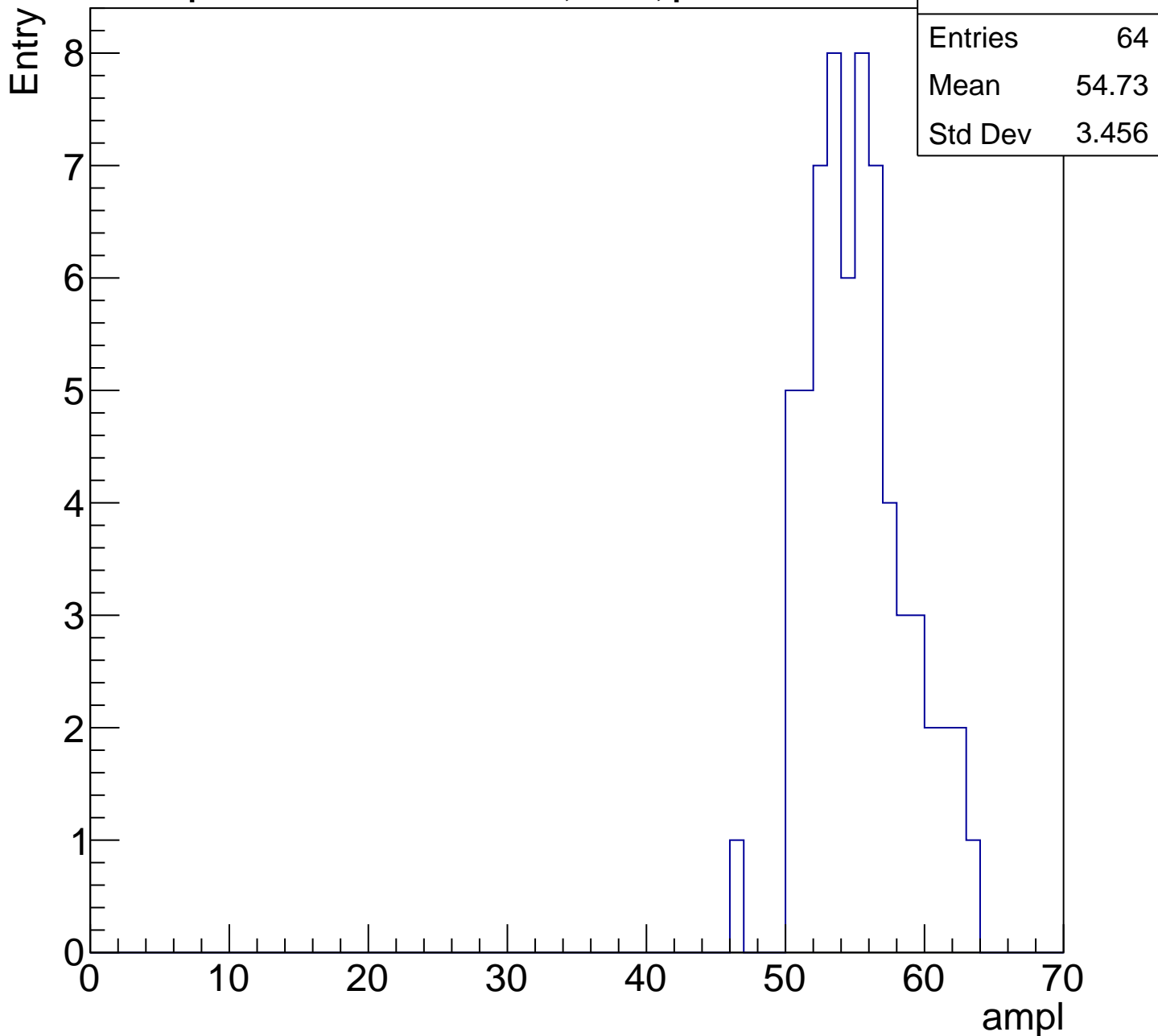
B1L103S, U5-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries

45

Mean

59.51

Std Dev

2.455

0

0

10

20

30

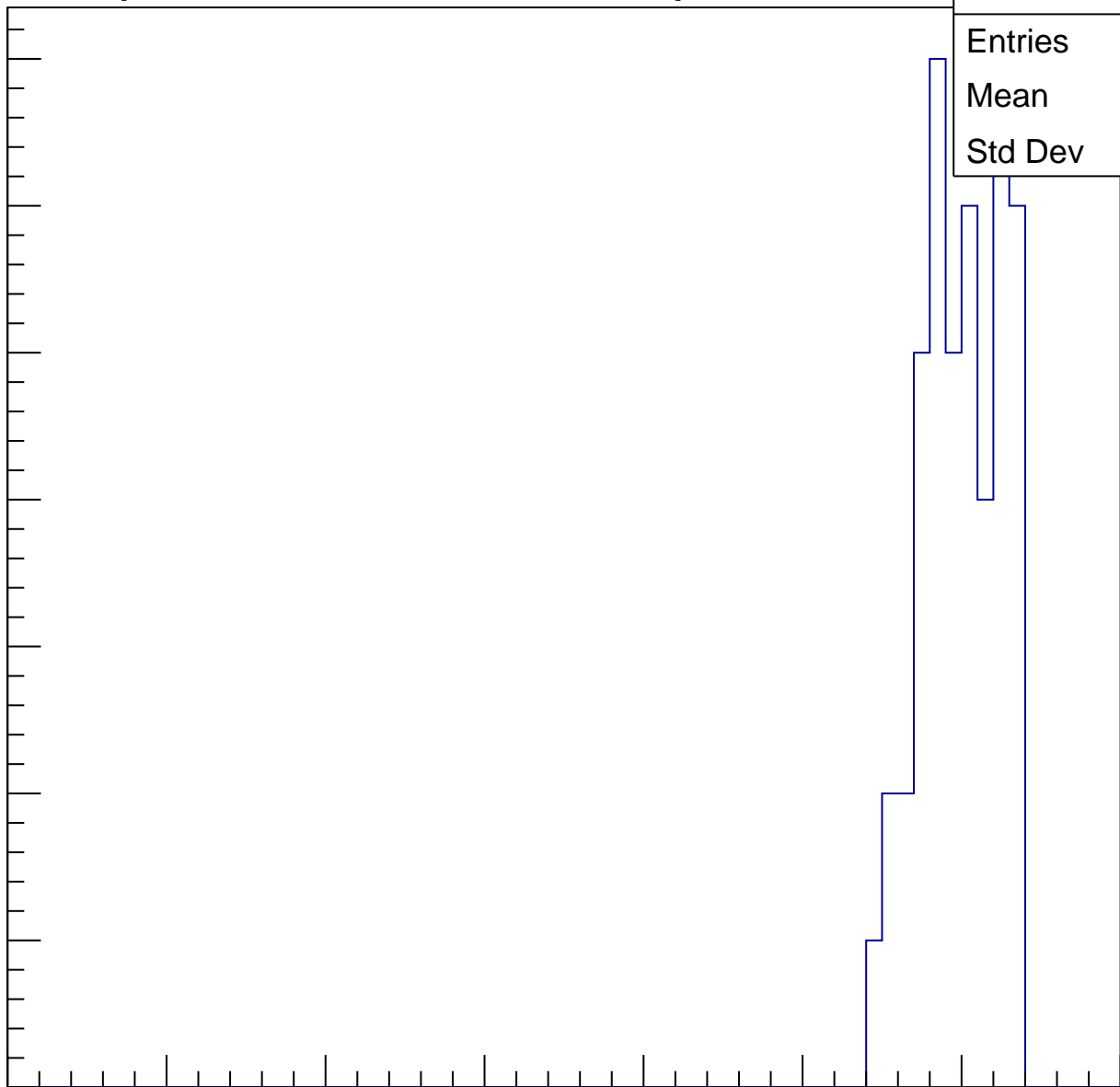
40

50

60

70

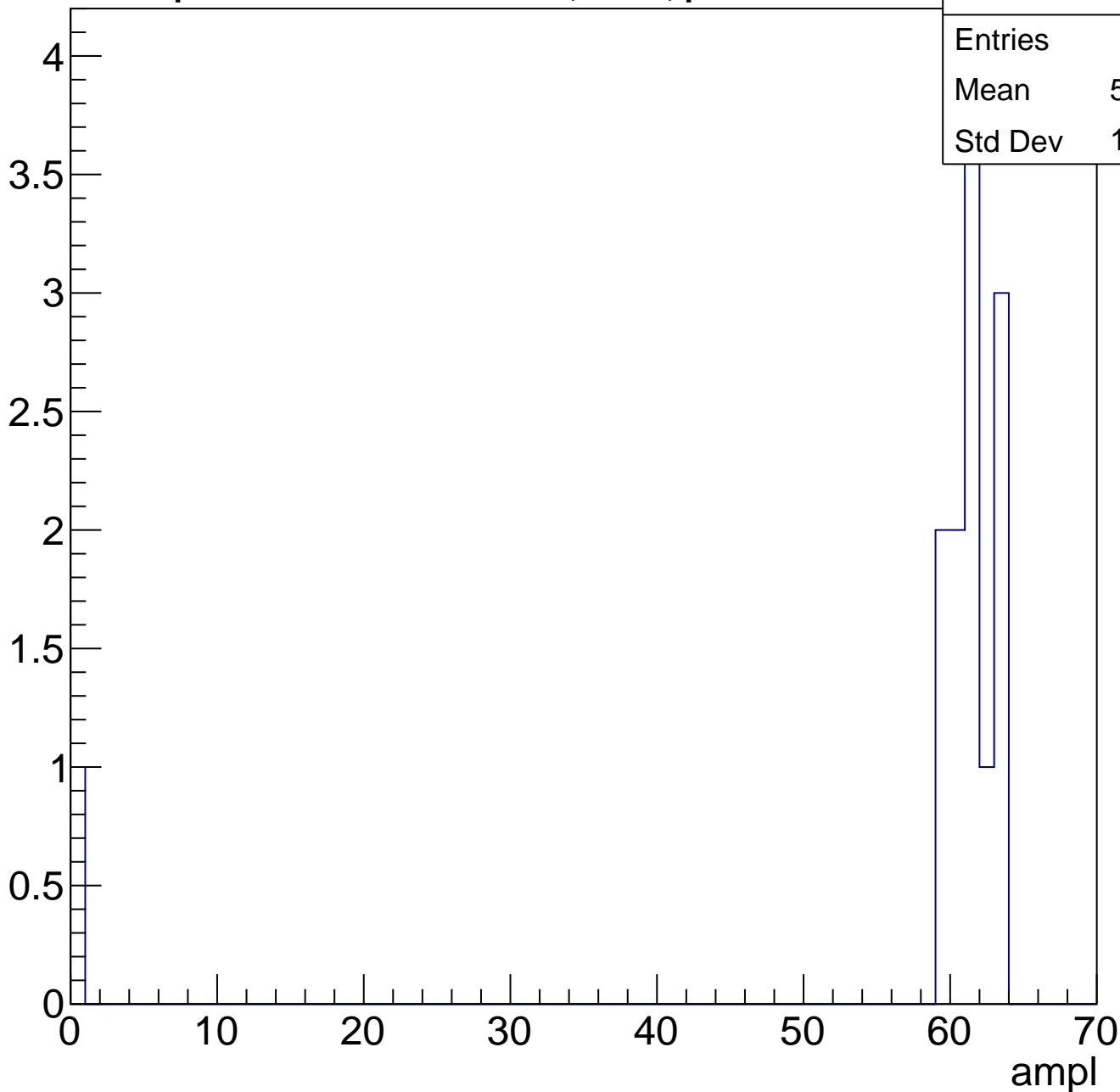
ampl



B1L103S, U5-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch74, adc0

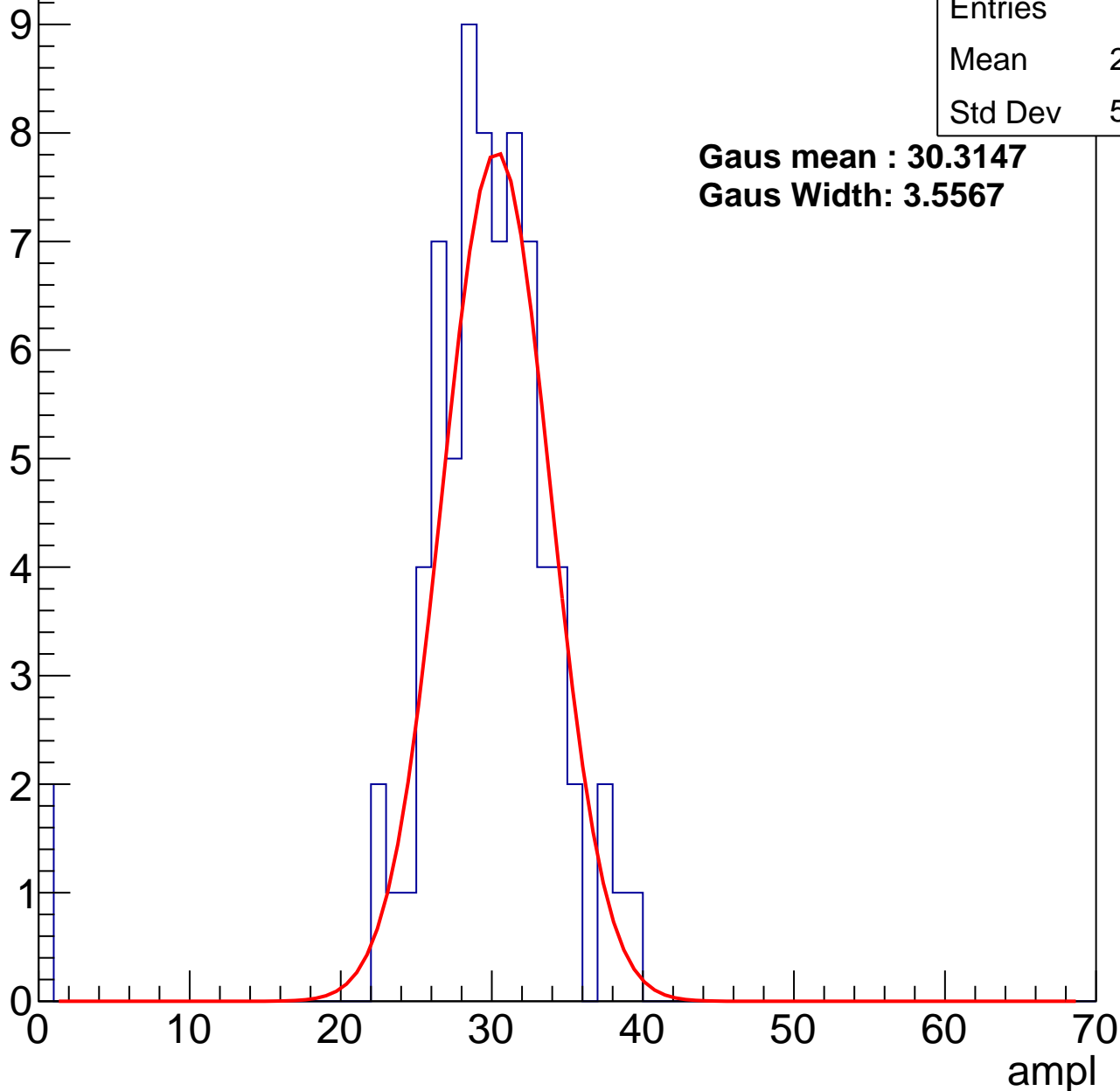
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.84
Std Dev	5.935

Gaus mean : 30.3147

Gaus Width: 3.5567



B1L103S, U5-ch74, adc1

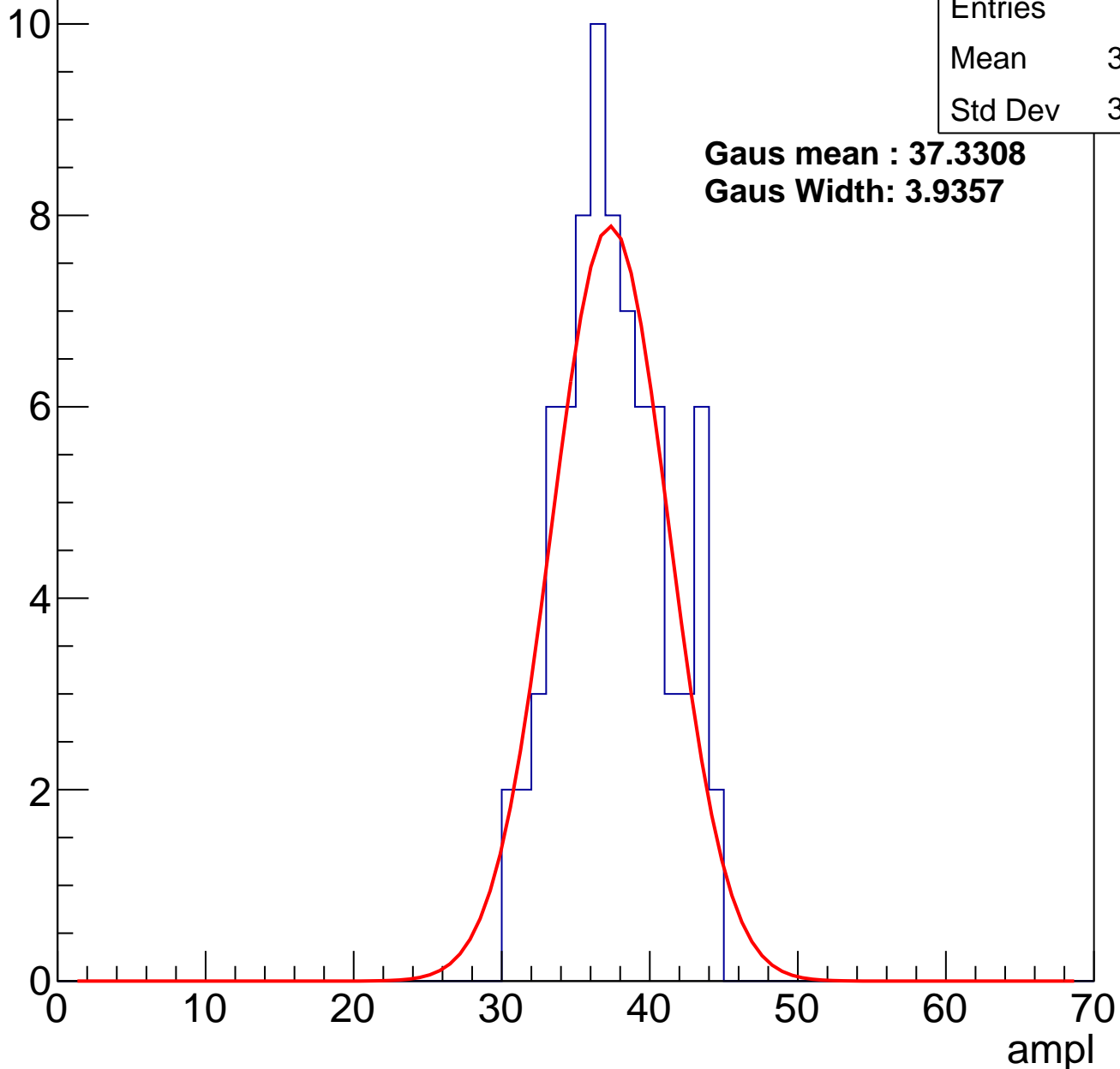
calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	37.06
Std Dev	3.506

Gaus mean : 37.3308

Gaus Width: 3.9357

Entry



B1L103S, U5-ch74, adc2

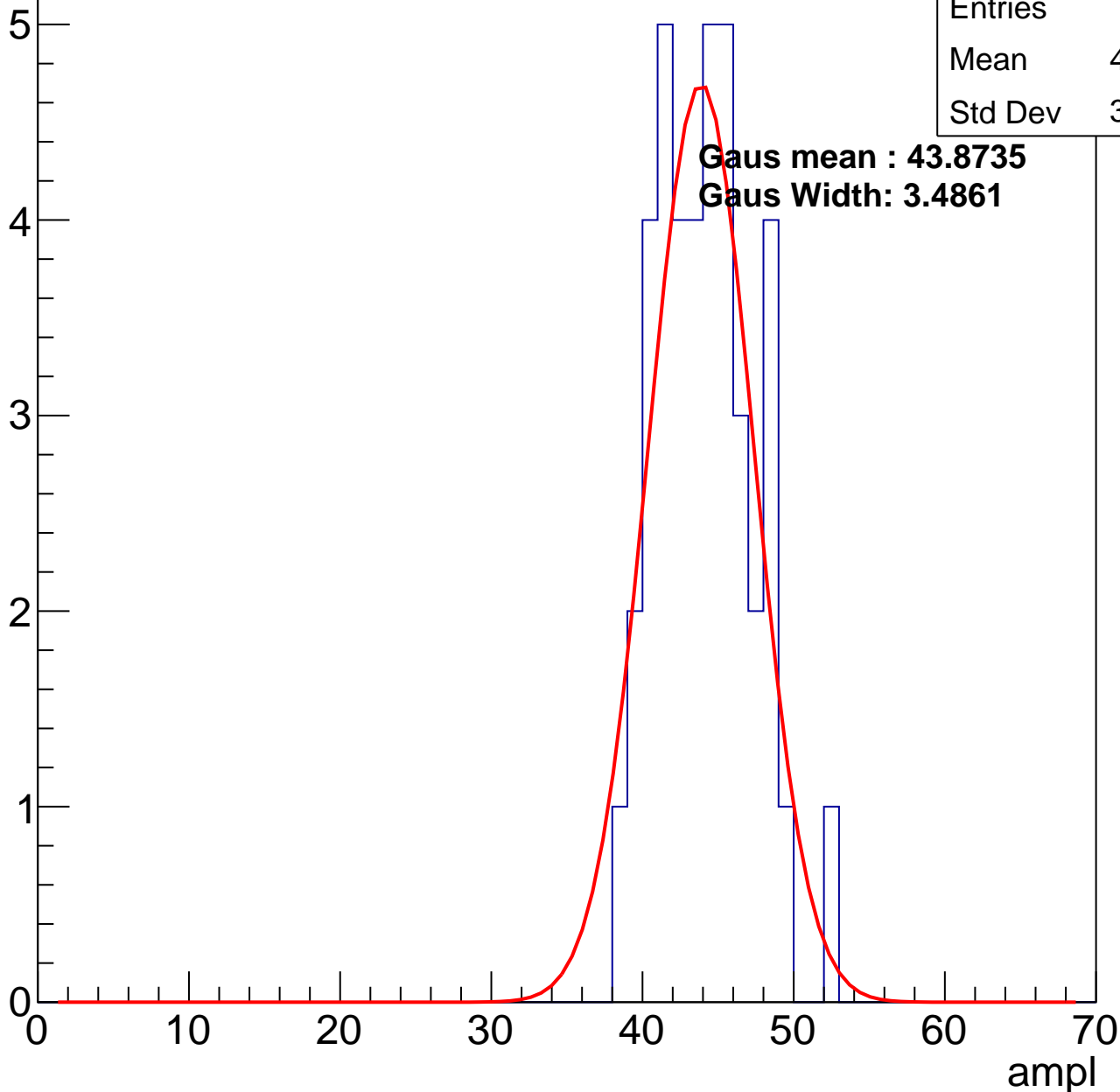
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	43.68
Std Dev	3.119

Gaus mean : 43.8735

Gaus Width: 3.4861

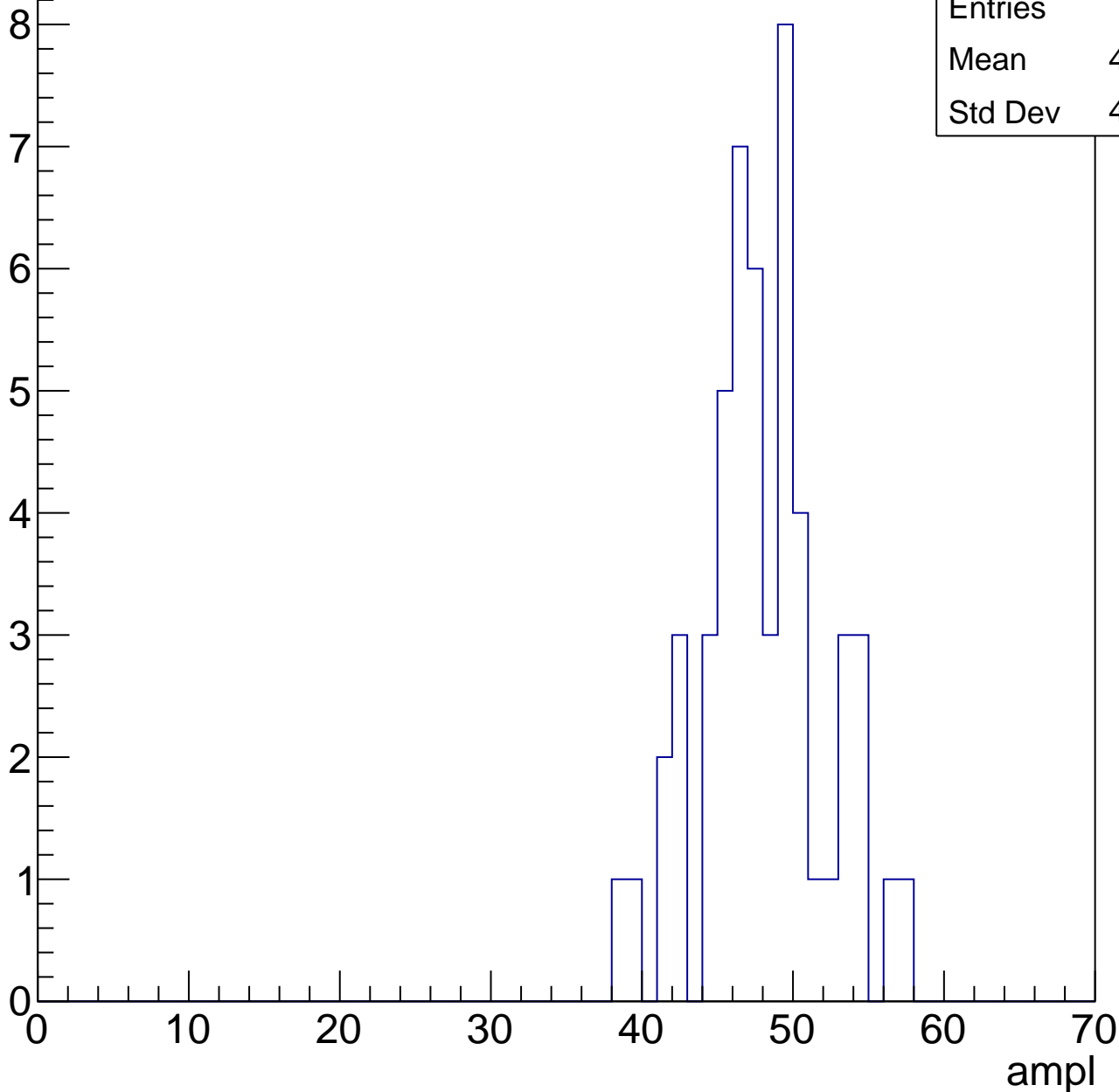


B1L103S, U5-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	47.53
Std Dev	4.073



B1L103S, U5-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

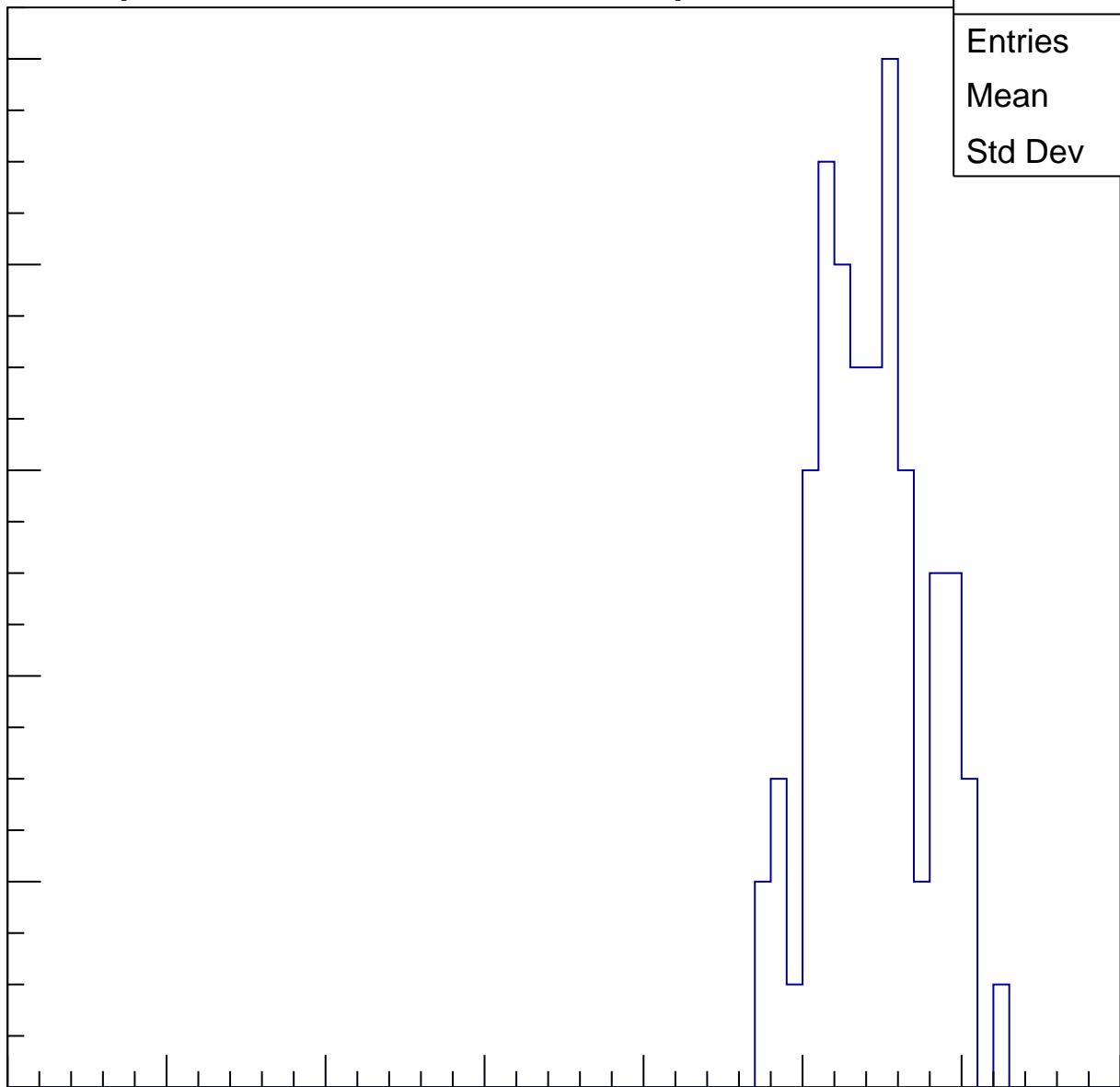
Entries	75
Mean	53.84
Std Dev	3.414

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

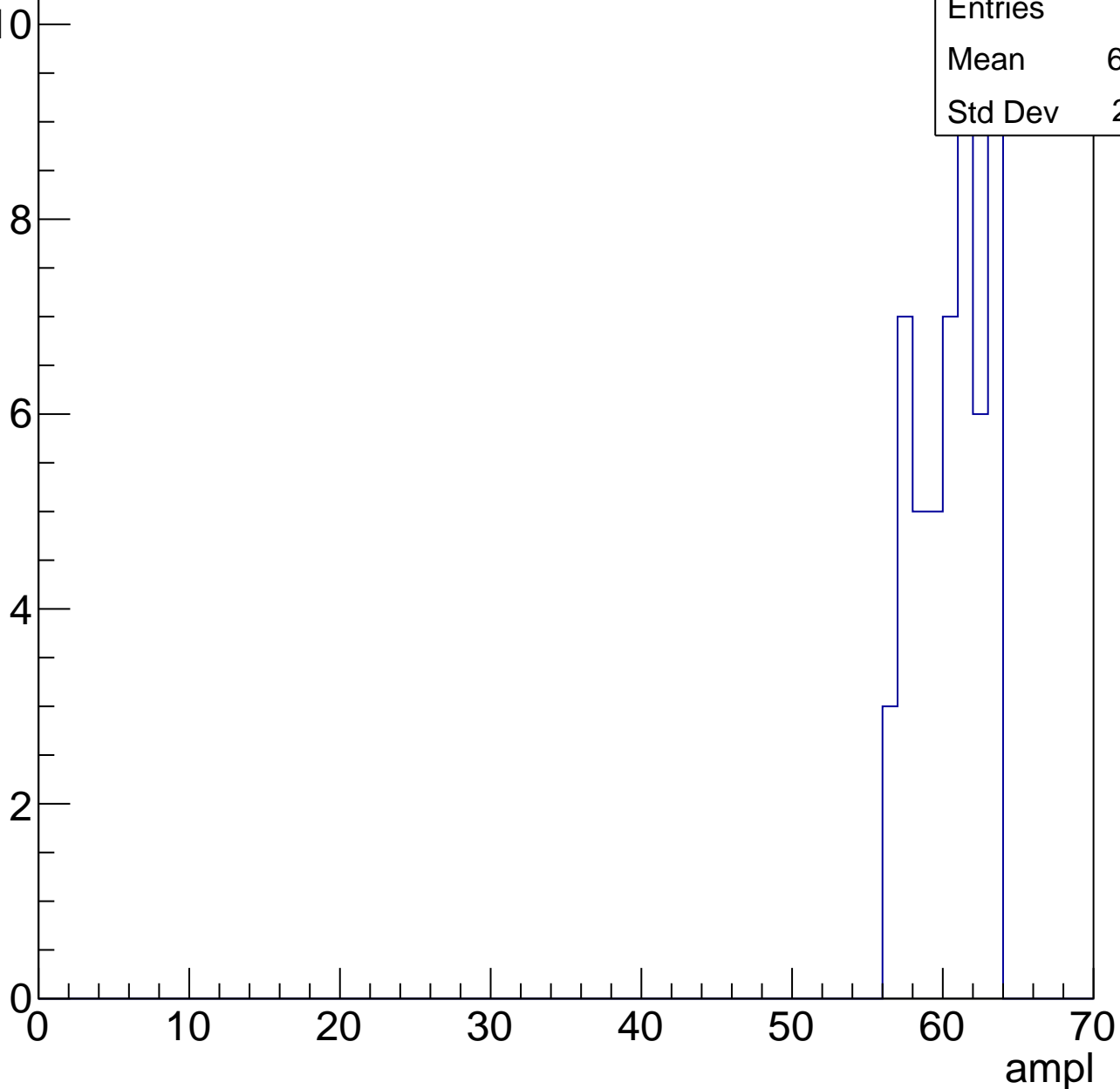


B1L103S, U5-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

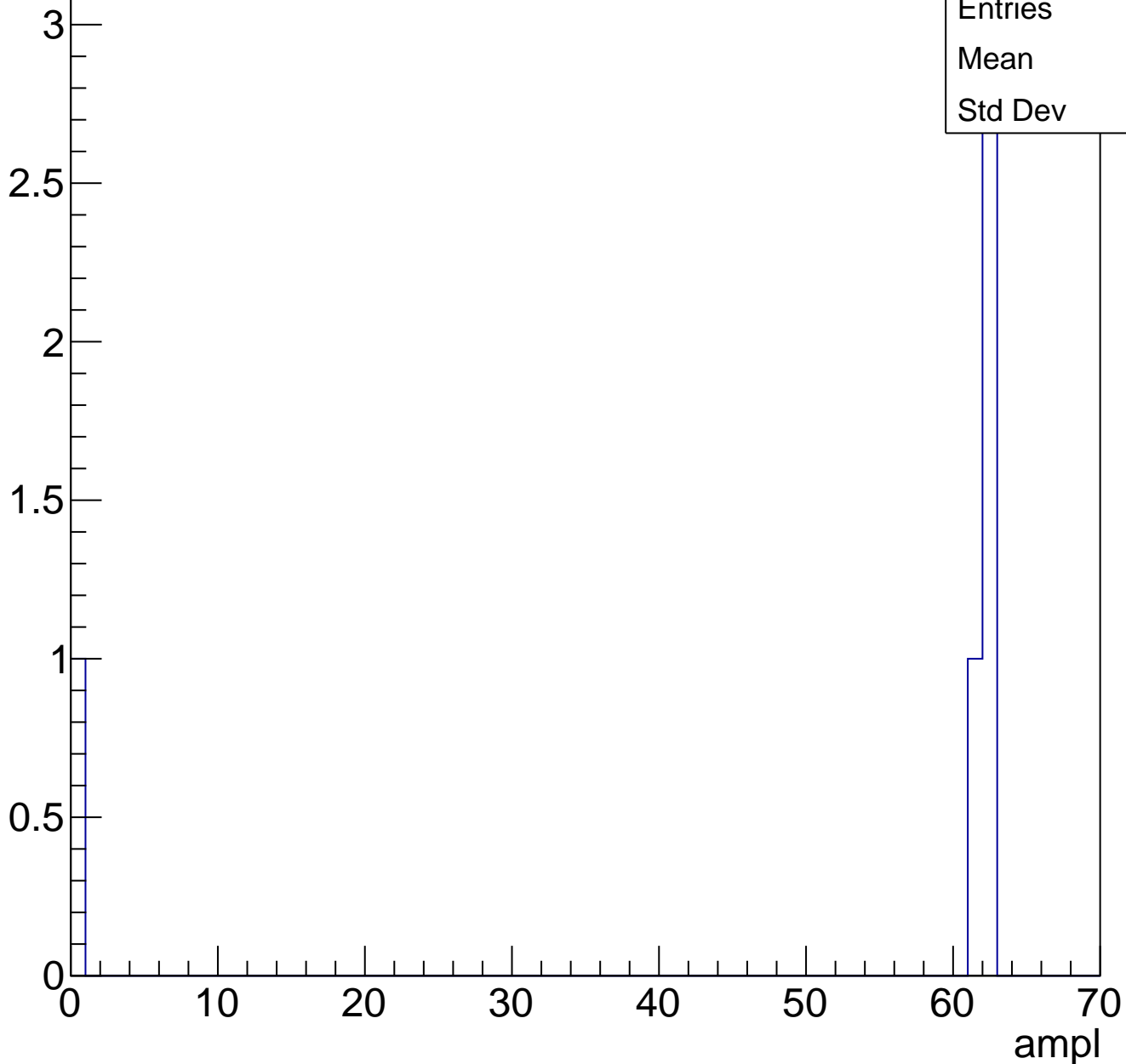
Entries	52
Mean	60.06
Std Dev	2.231



B1L103S, U5-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch75, adc0

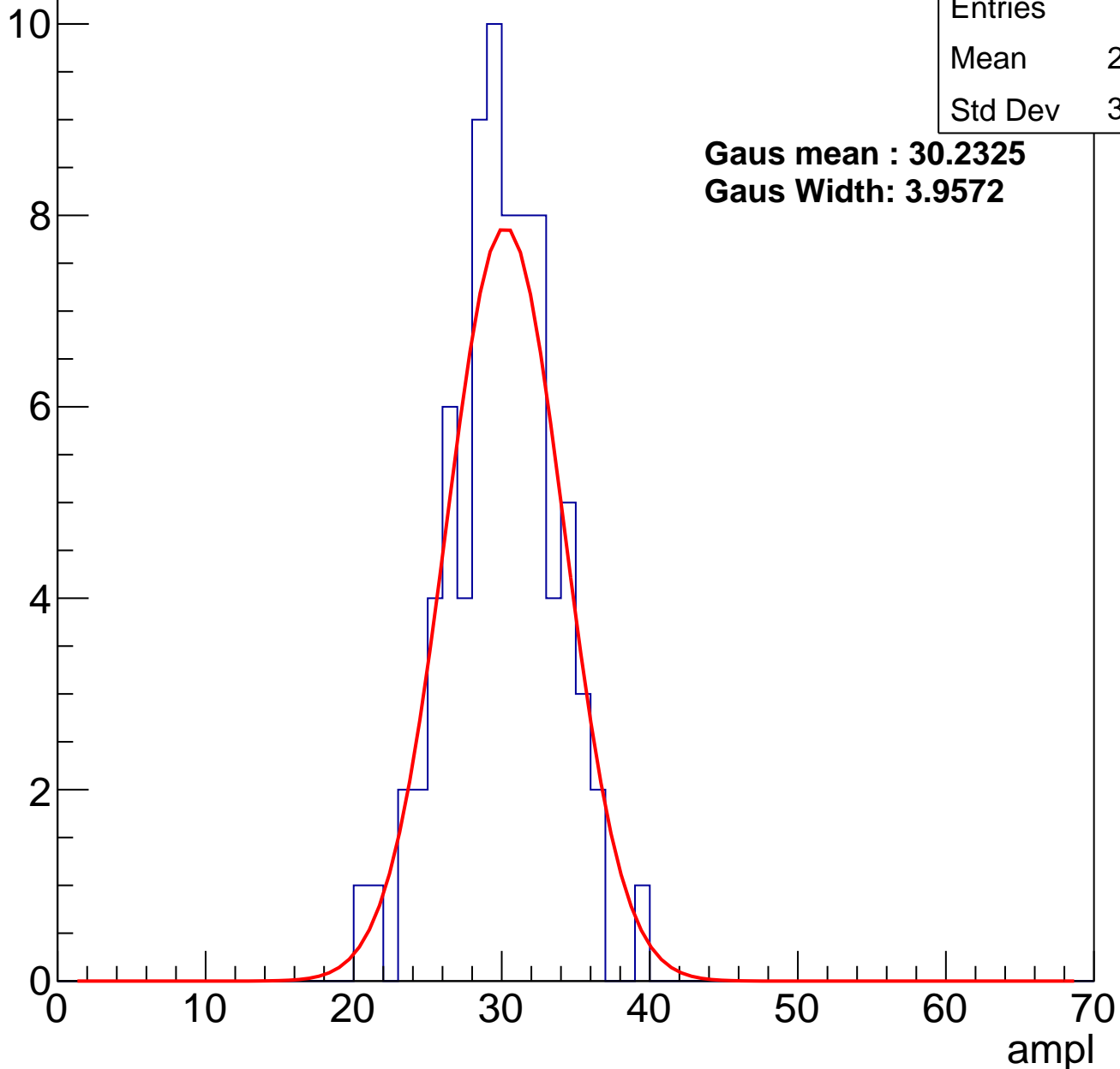
calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	29.53
Std Dev	3.572

Gaus mean : 30.2325

Gaus Width: 3.9572

Entry



B1L103S, U5-ch75, adc1

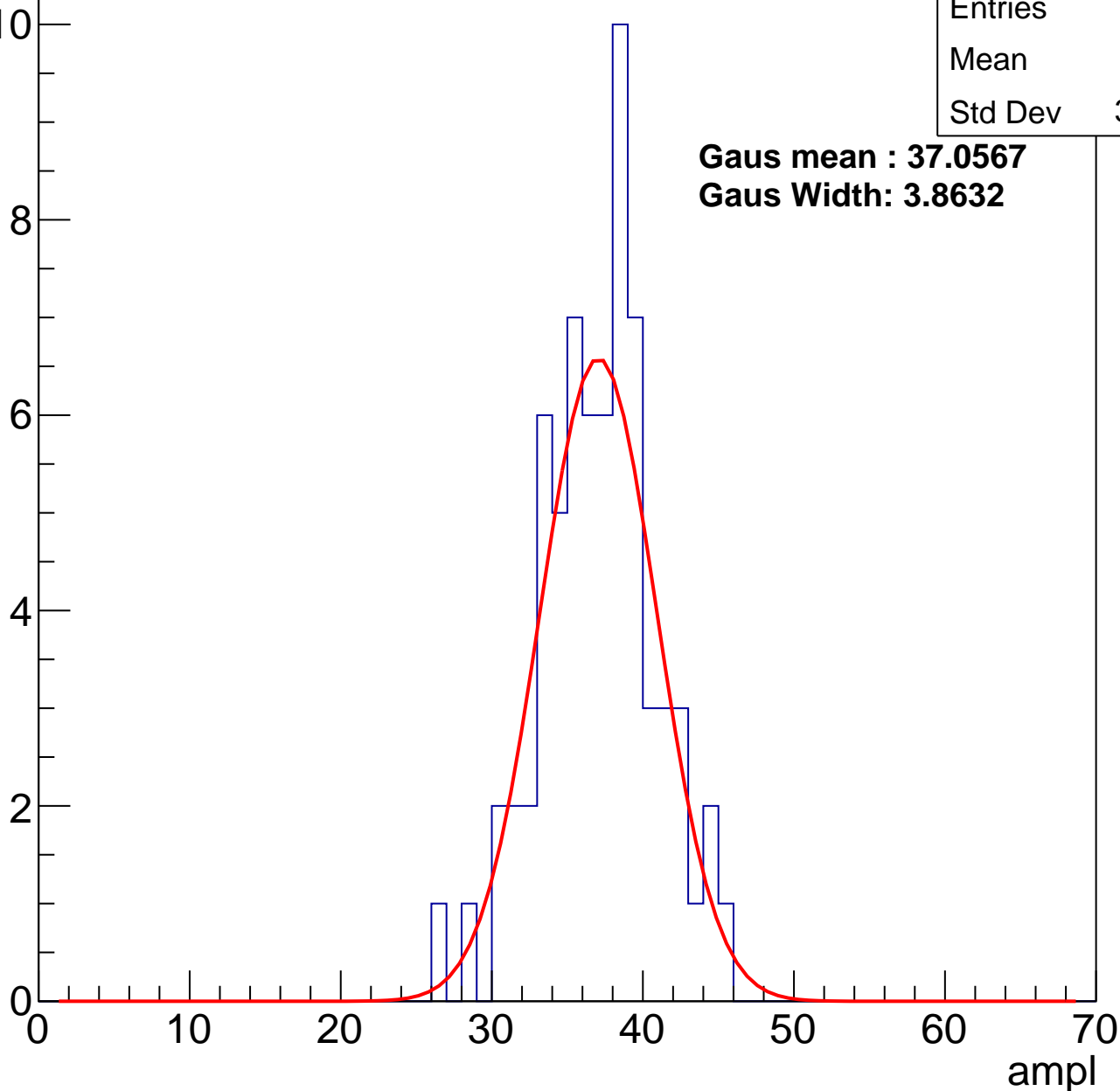
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.6
Std Dev	3.781

Gaus mean : 37.0567

Gaus Width: 3.8632



B1L103S, U5-ch75, adc2

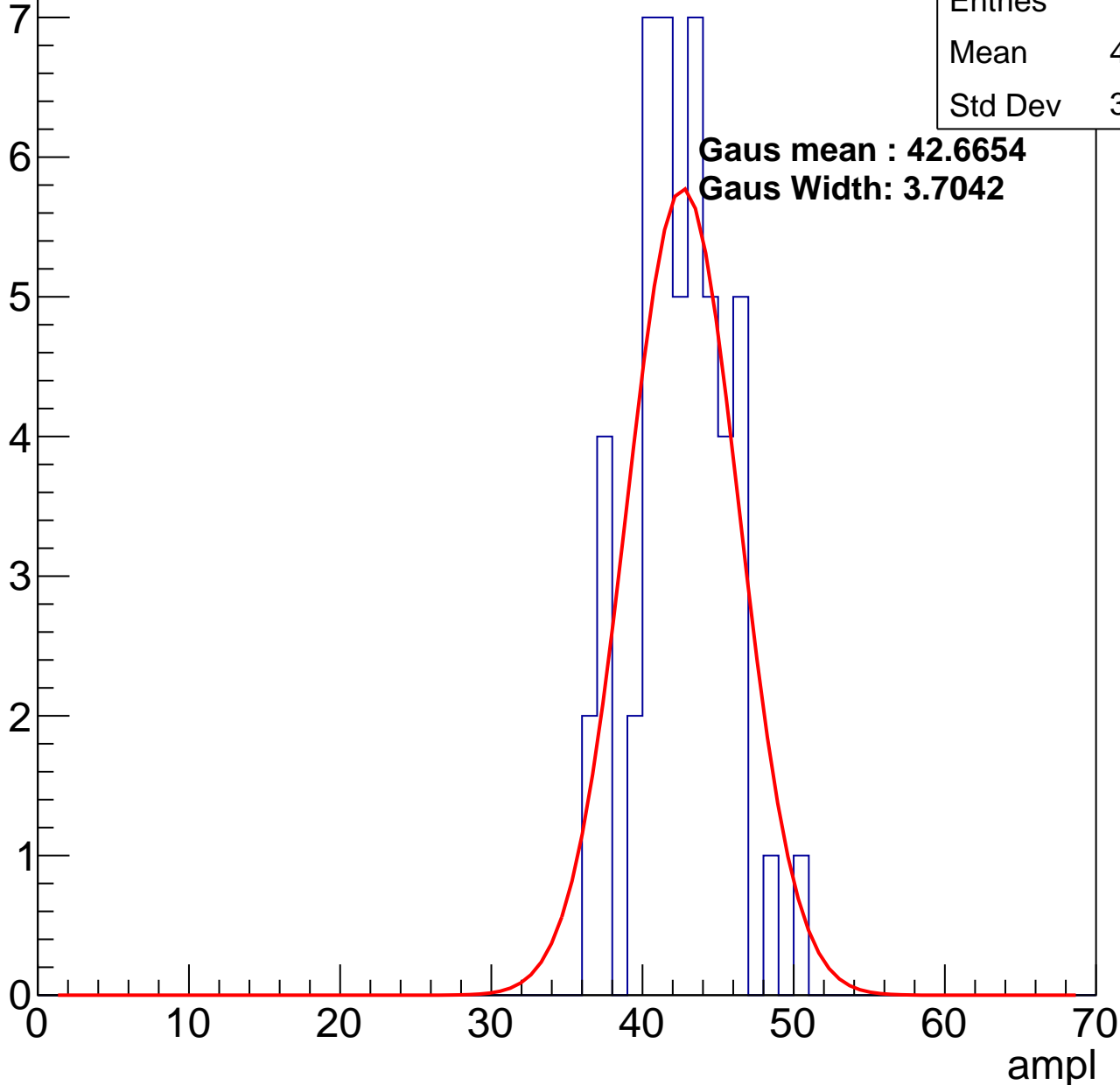
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.08
Std Dev	3.058

Gaus mean : 42.6654

Gaus Width: 3.7042

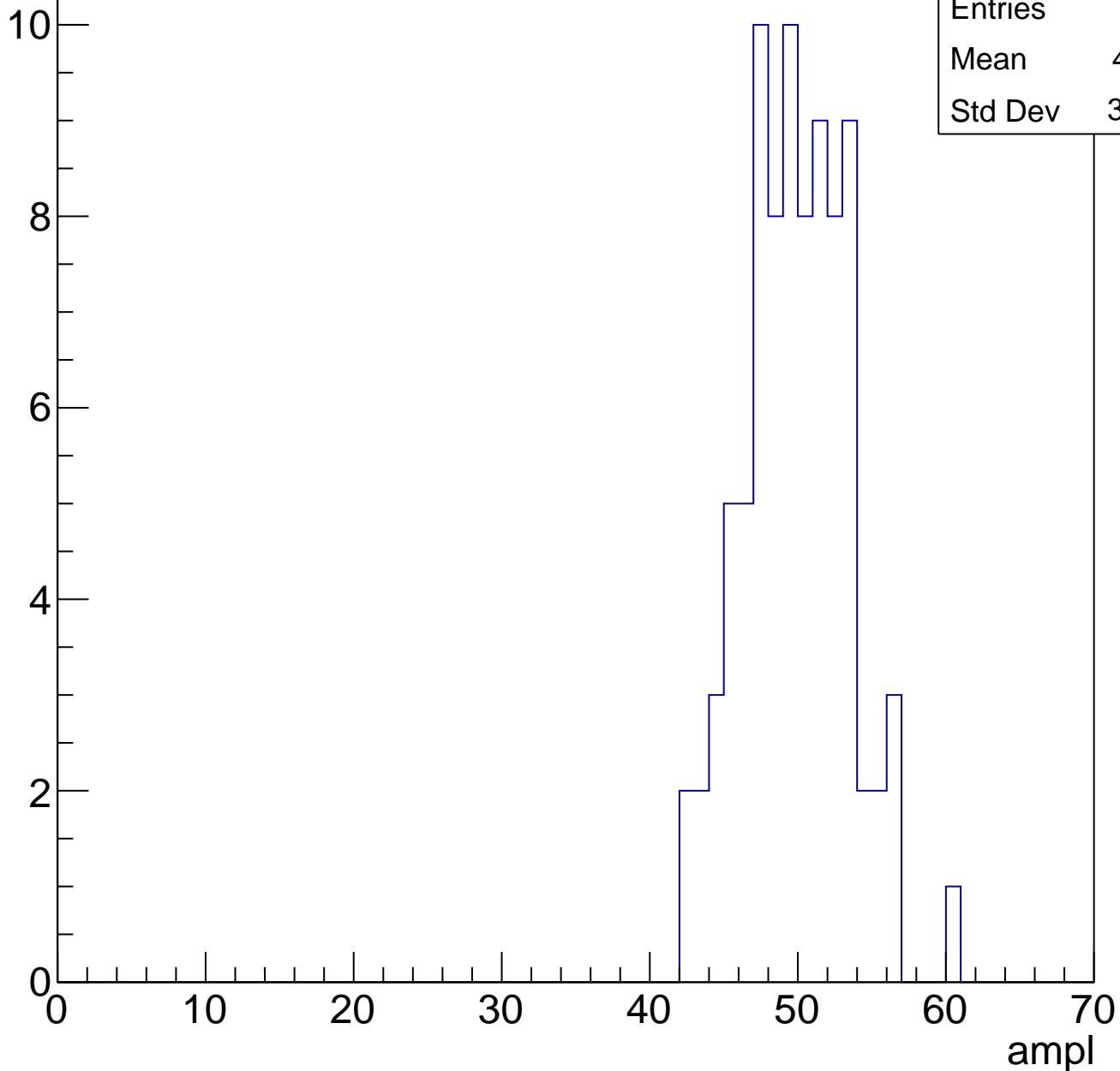


B1L103S, U5-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	49.41
Std Dev	3.479

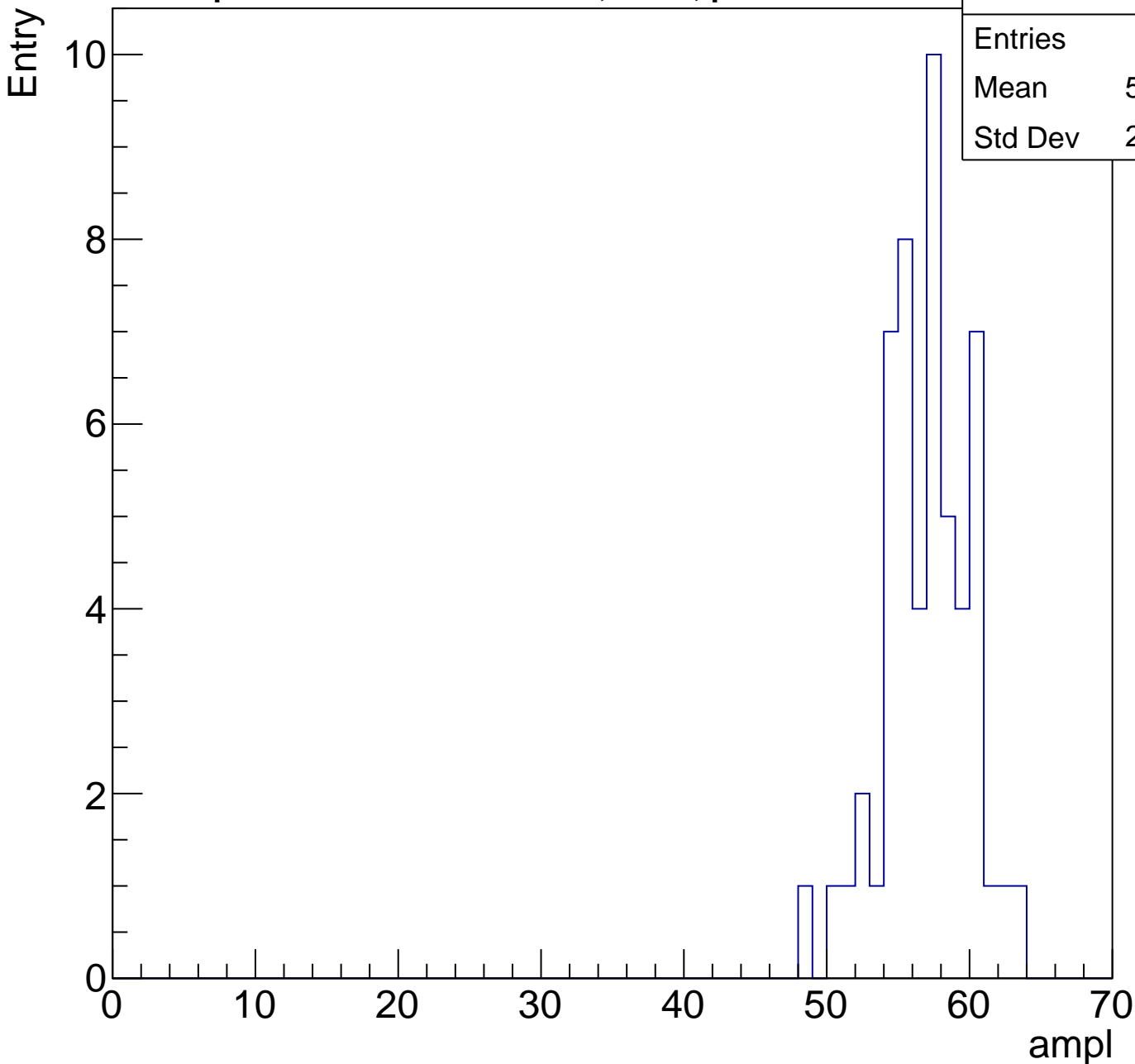
Entry



B1L103S, U5-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	54
Mean	56.48
Std Dev	2.974



B1L103S, U5-ch75, adc5

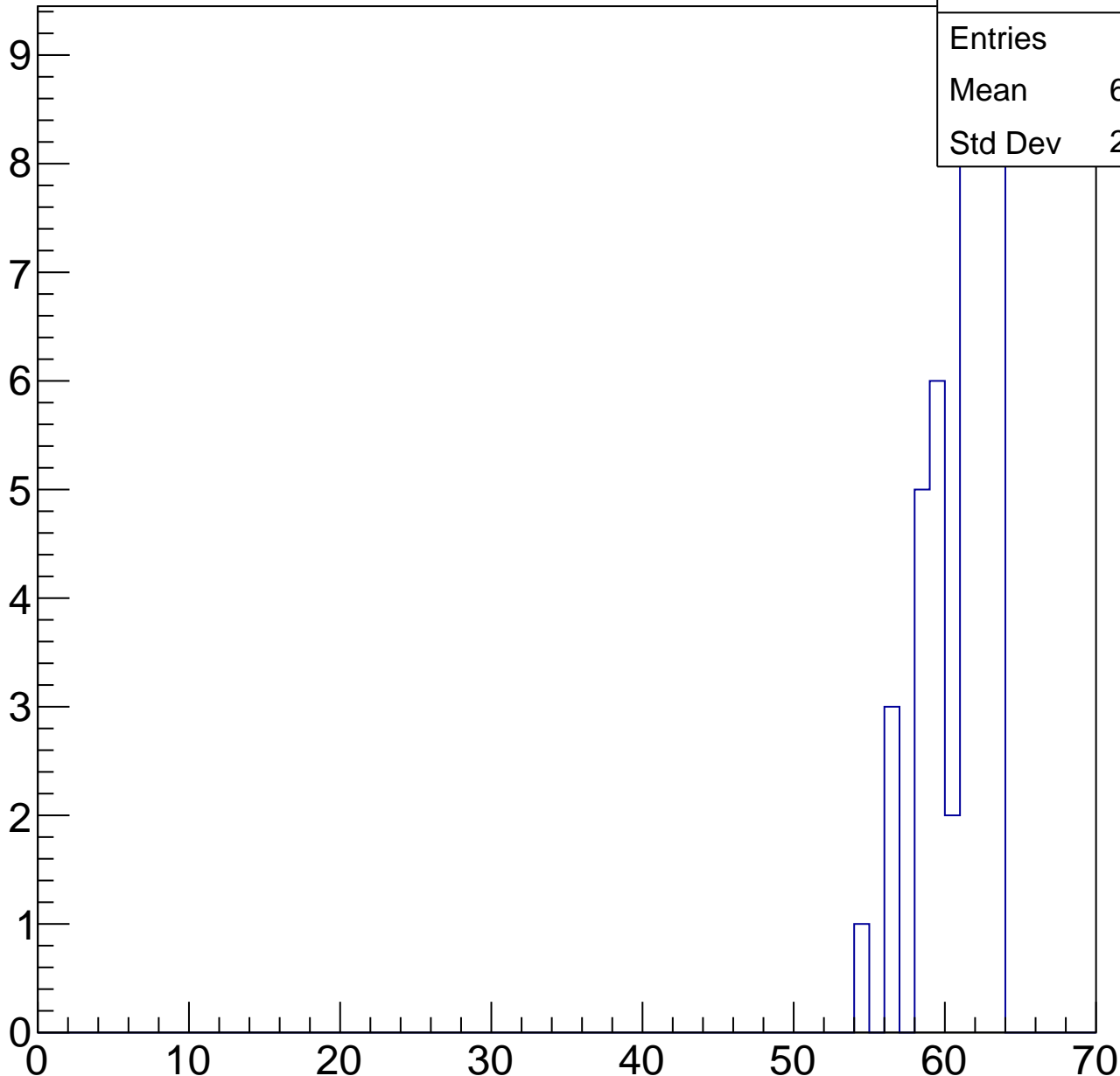
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.38
Std Dev	2.288

ampl



B1L103S, U5-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch76, adc0

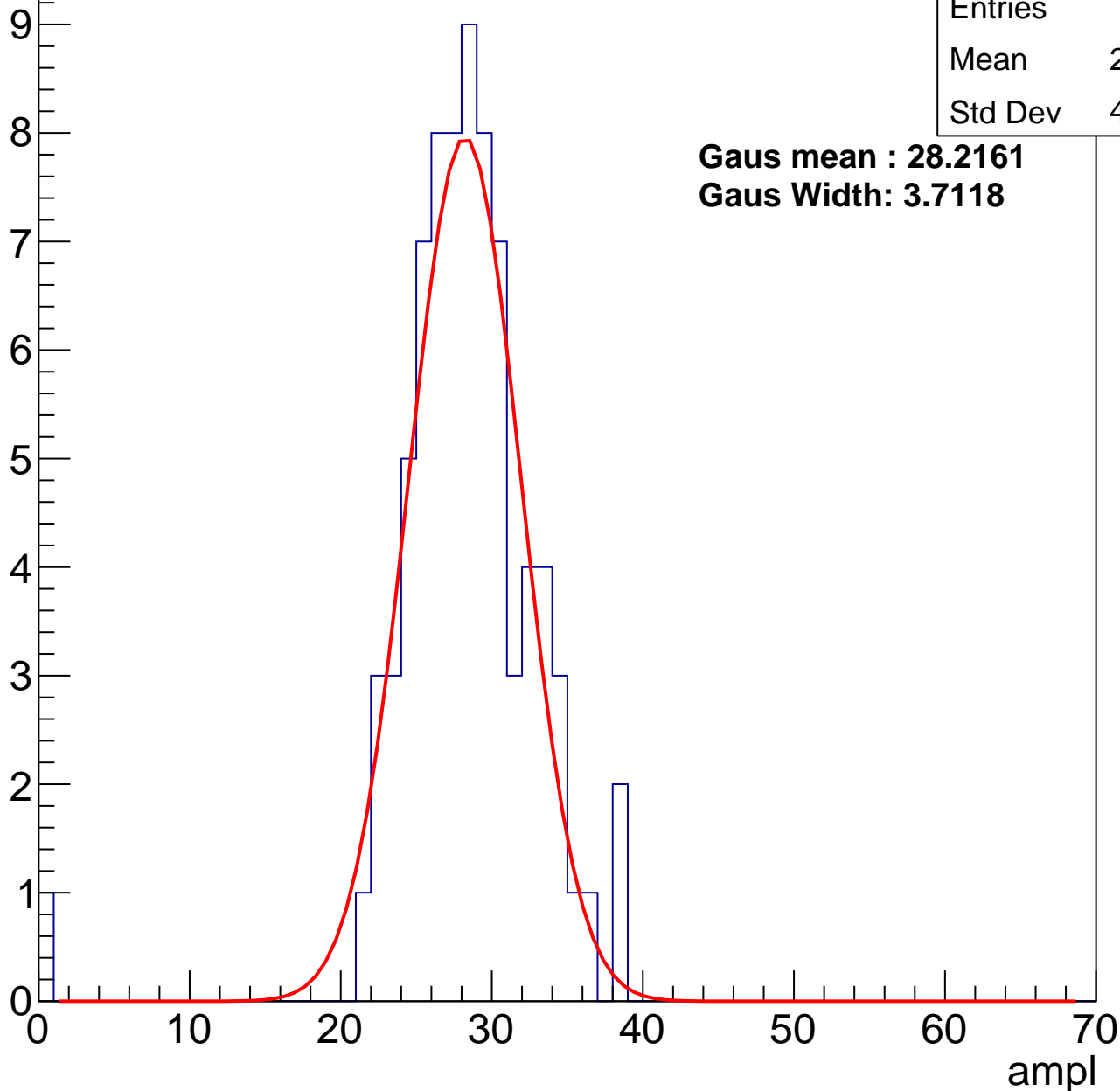
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	27.83
Std Dev	4.863

Gaus mean : 28.2161

Gaus Width: 3.7118



B1L103S, U5-ch76, adc1

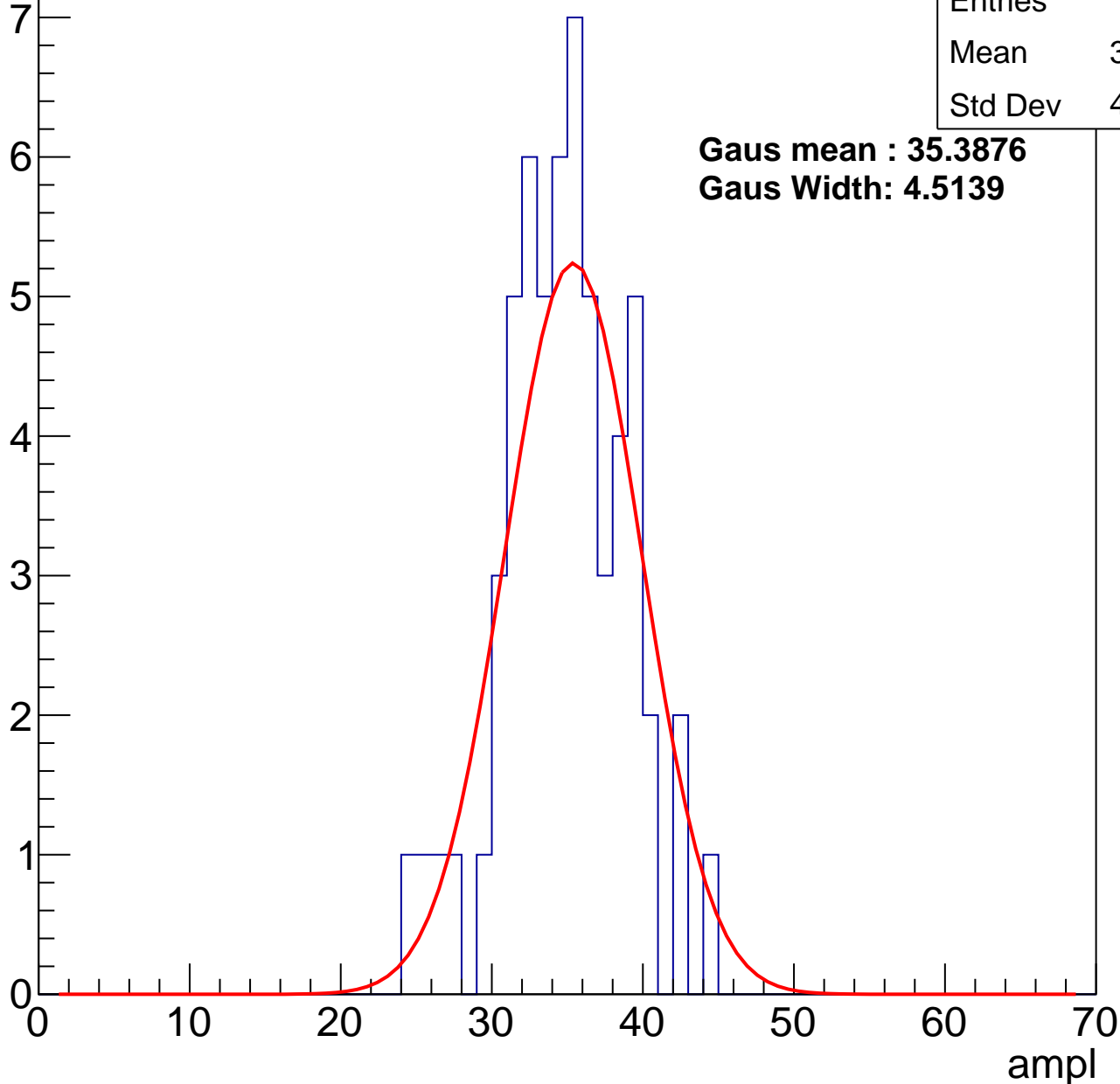
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	34.37
Std Dev	4.067

Gaus mean : 35.3876

Gaus Width: 4.5139



B1L103S, U5-ch76, adc2

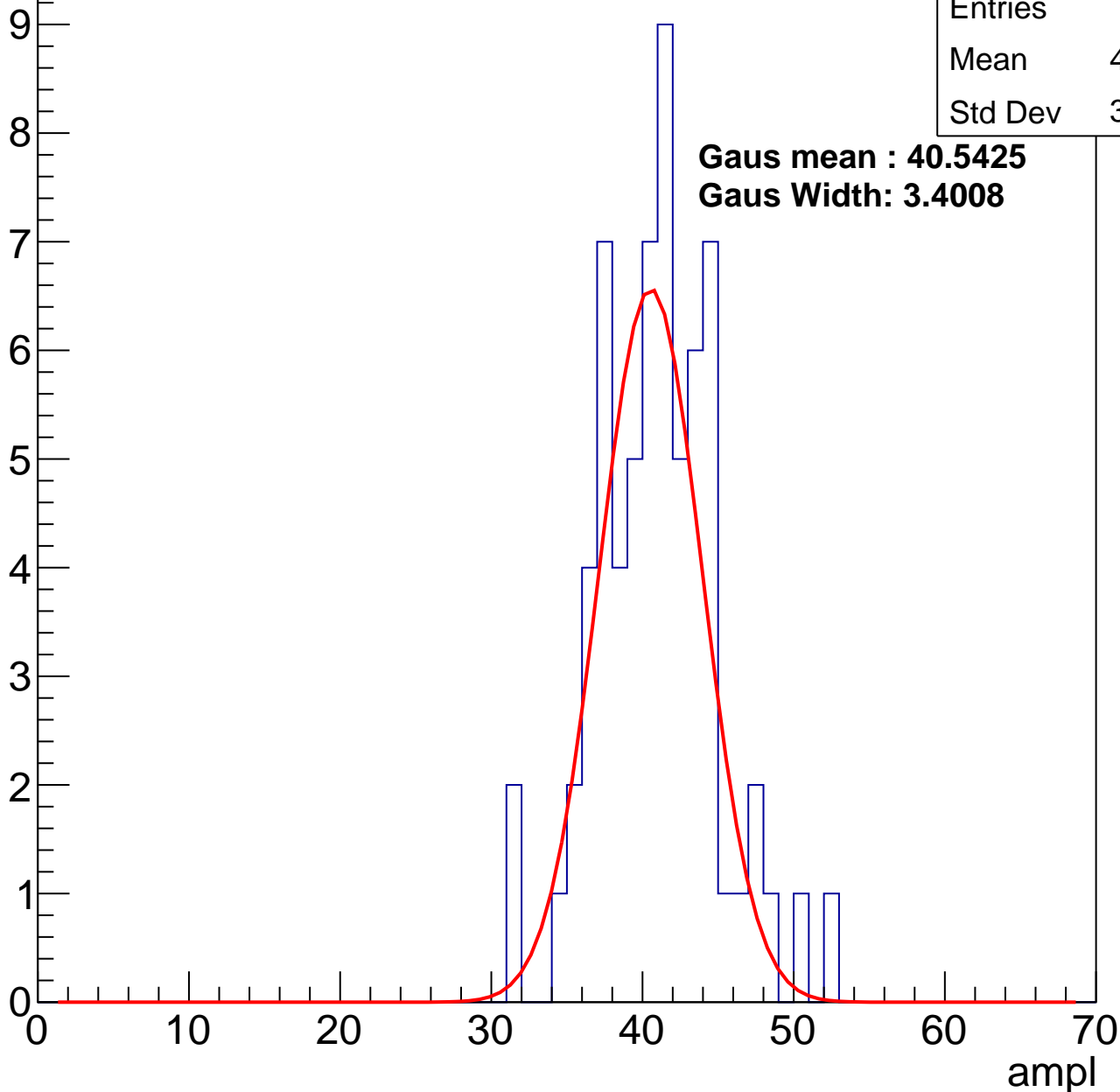
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	40.55
Std Dev	3.966

Gaus mean : 40.5425

Gaus Width: 3.4008

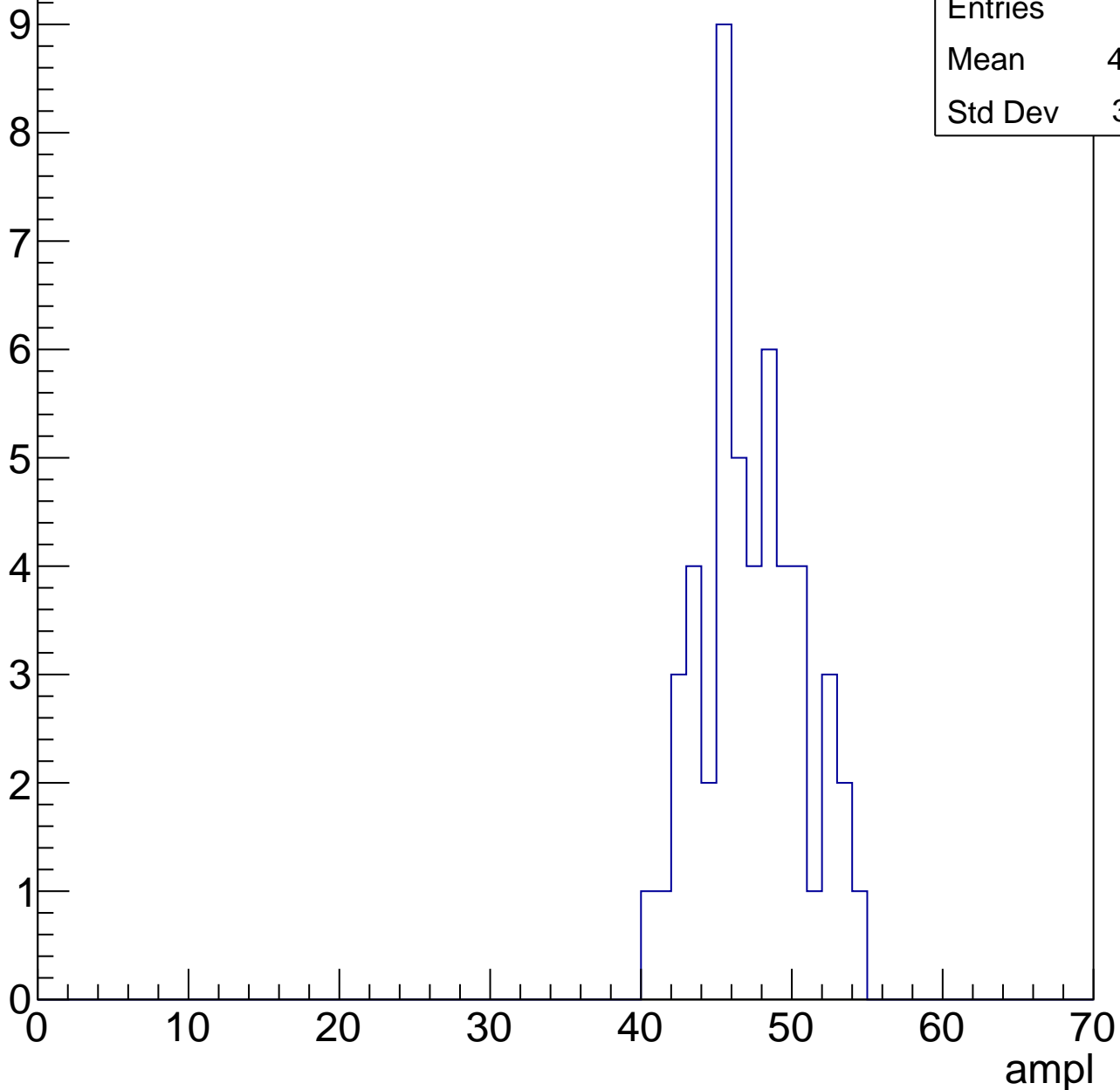


B1L103S, U5-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

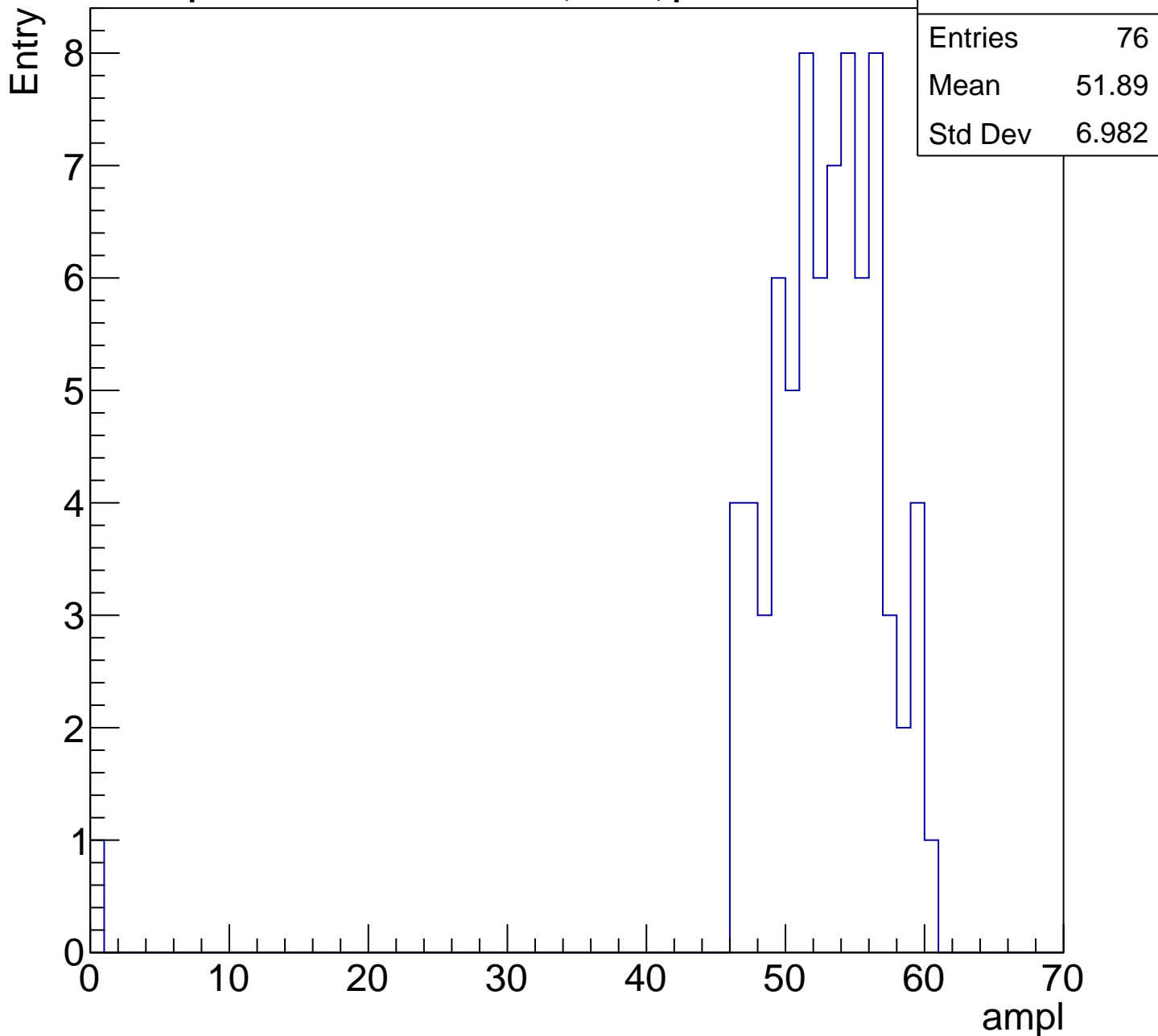
Entry

Entries	50
Mean	46.82
Std Dev	3.321



B1L103S, U5-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

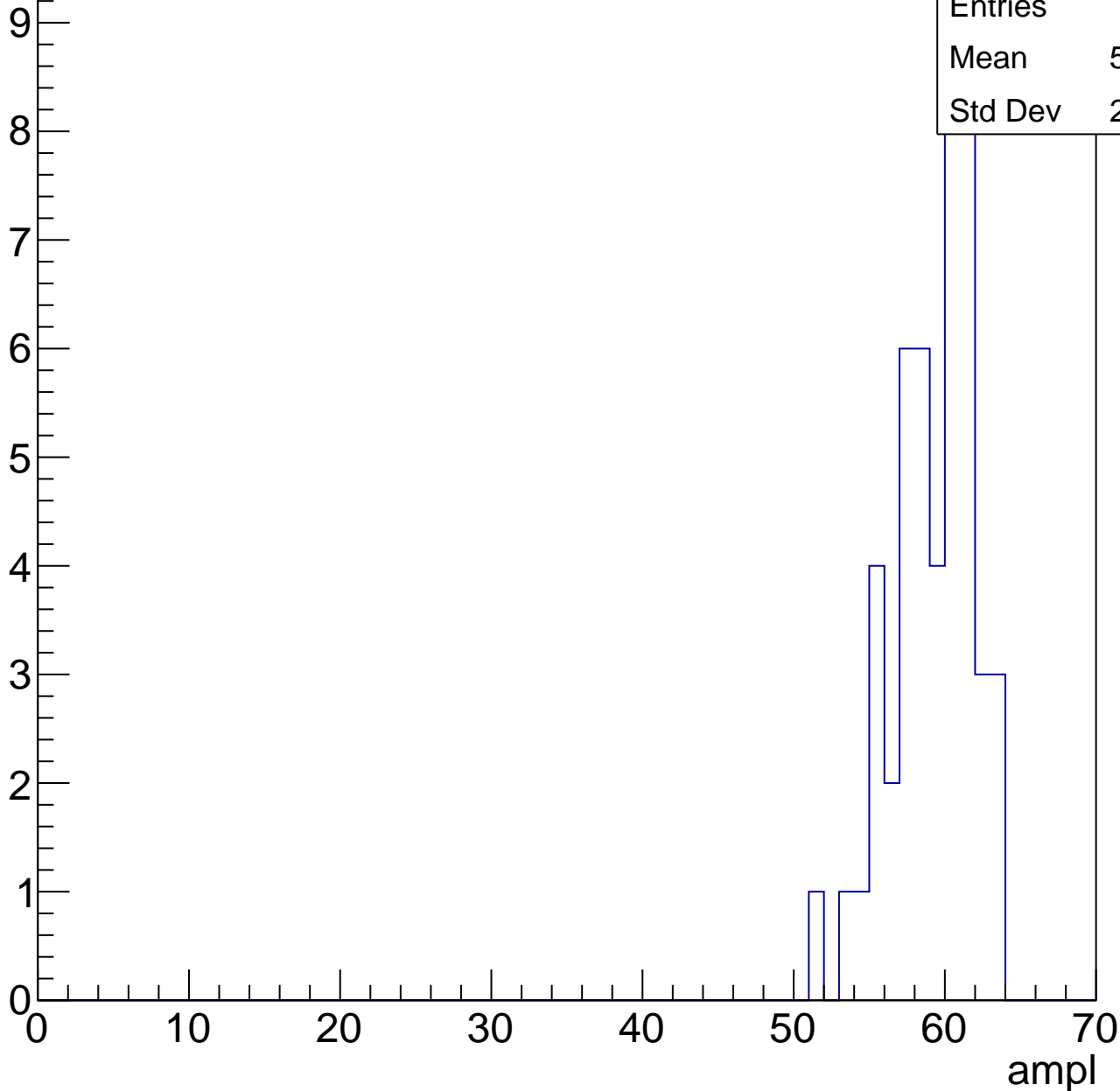


B1L103S, U5-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.75
Std Dev	2.712

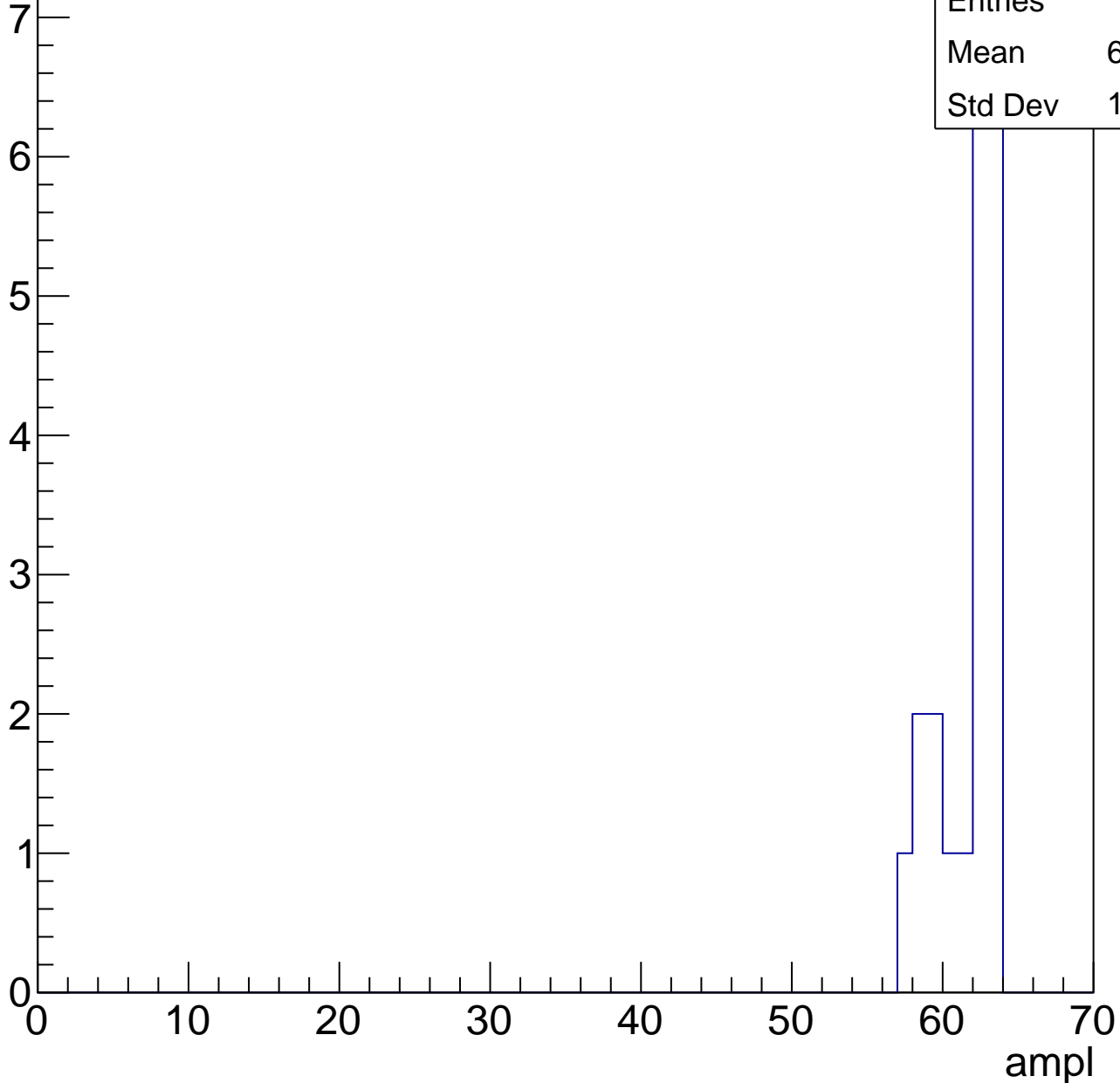


B1L103S, U5-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	61.29
Std Dev	1.906



B1L103S, U5-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch77, adc0

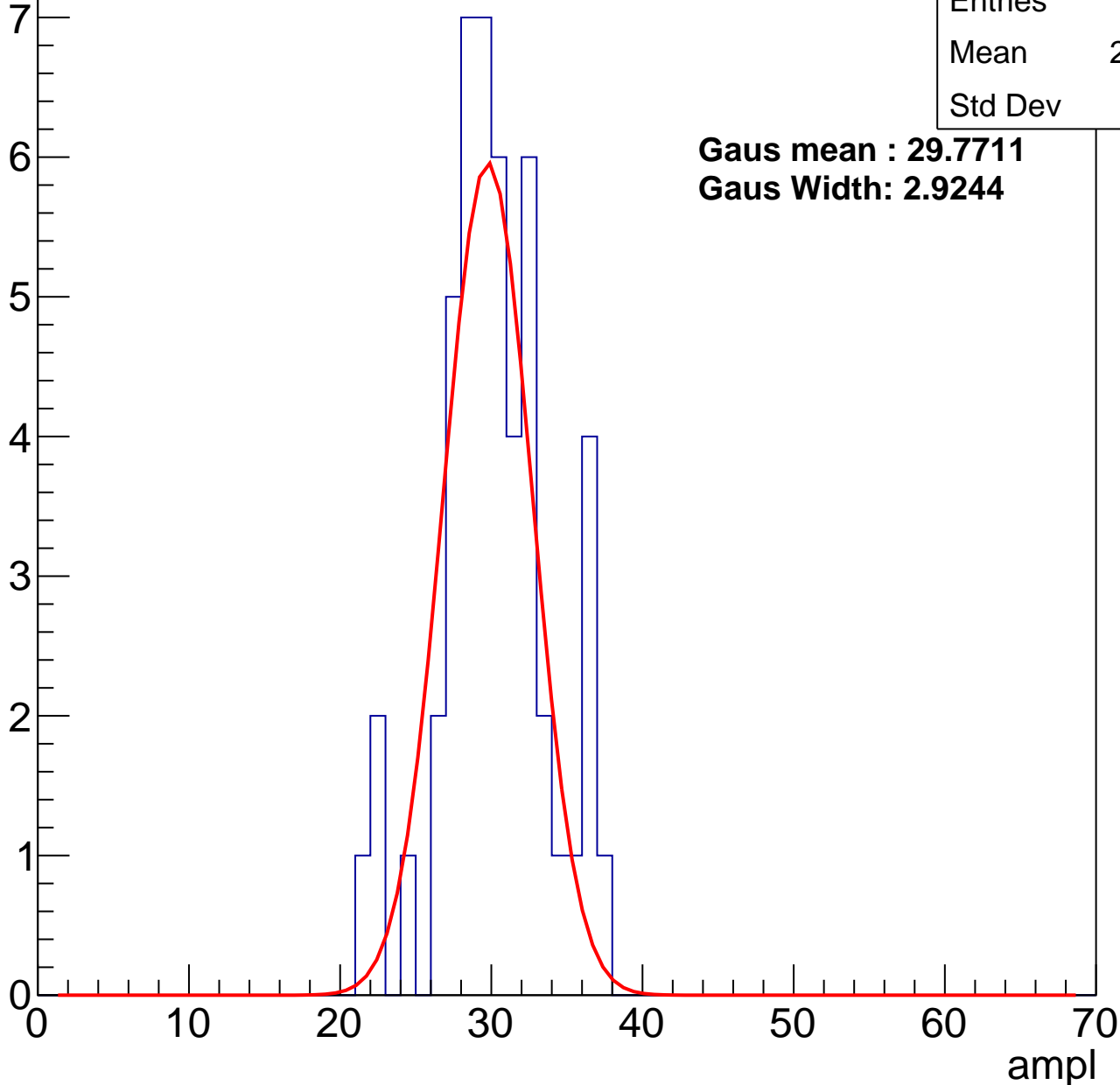
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	29.74
Std Dev	3.56

Gaus mean : 29.7711

Gaus Width: 2.9244



B1L103S, U5-ch77, adc1

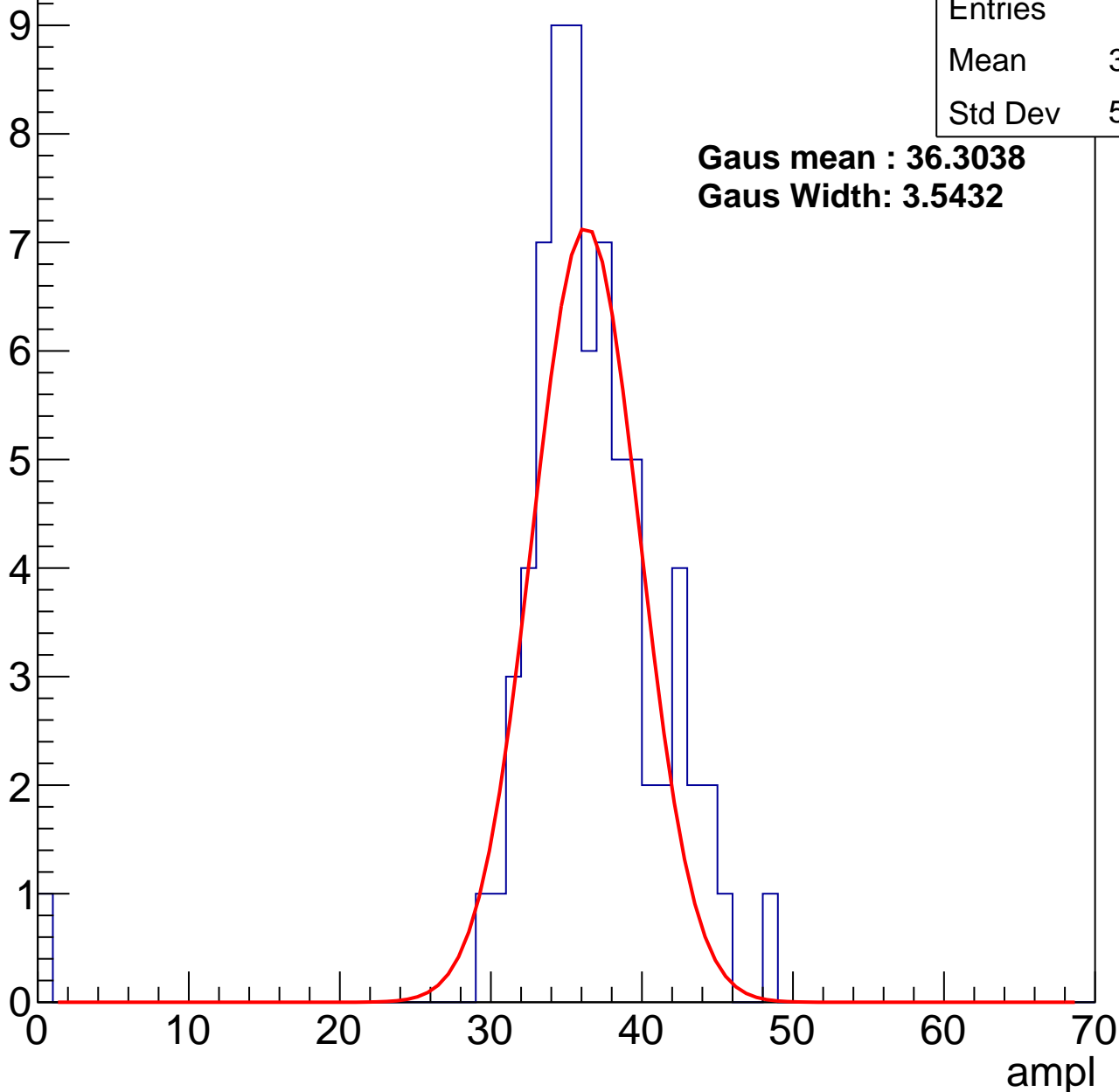
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	35.96
Std Dev	5.746

Gaus mean : 36.3038

Gaus Width: 3.5432



B1L103S, U5-ch77, adc2

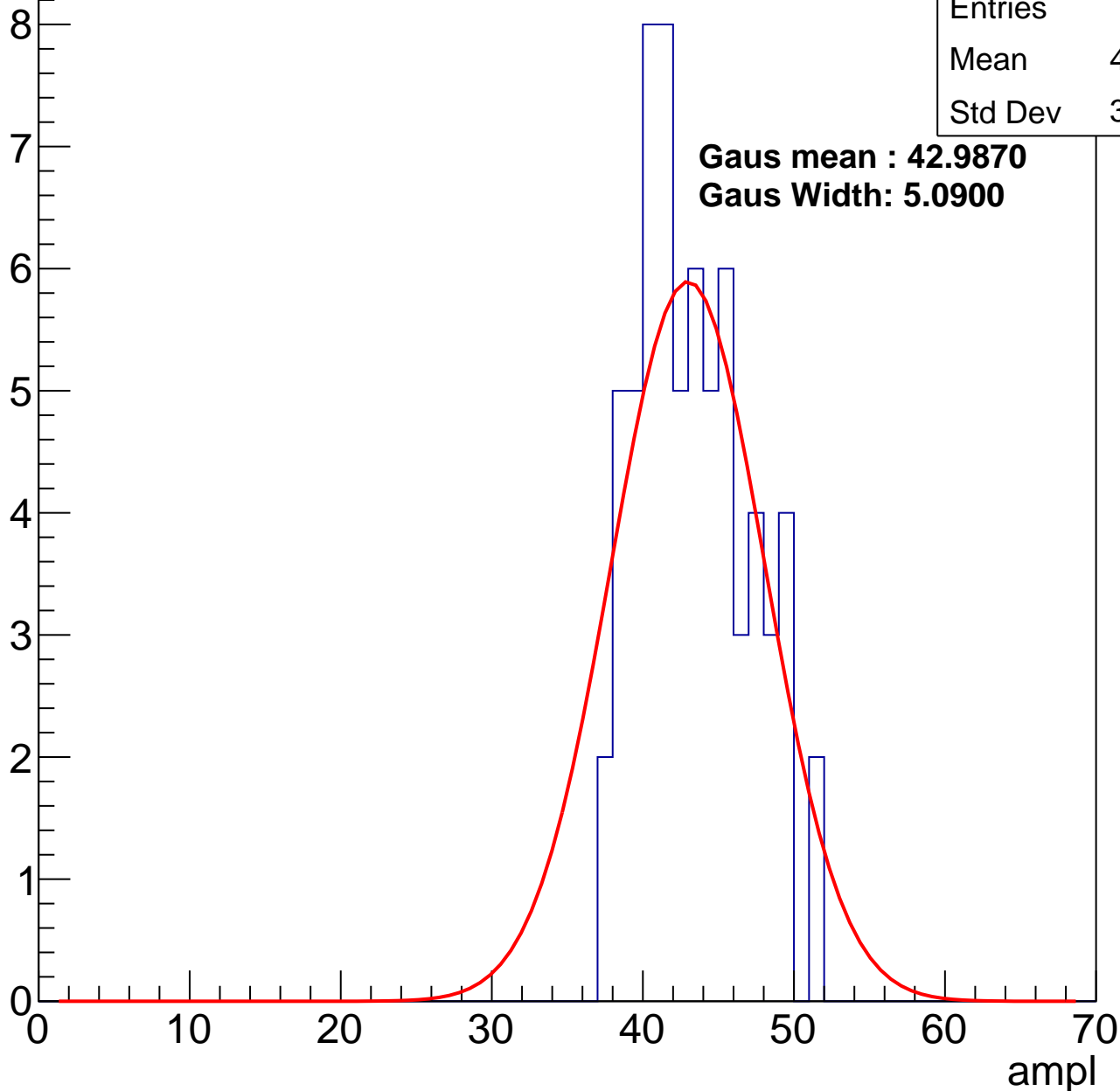
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.92
Std Dev	3.594

Gaus mean : 42.9870

Gaus Width: 5.0900

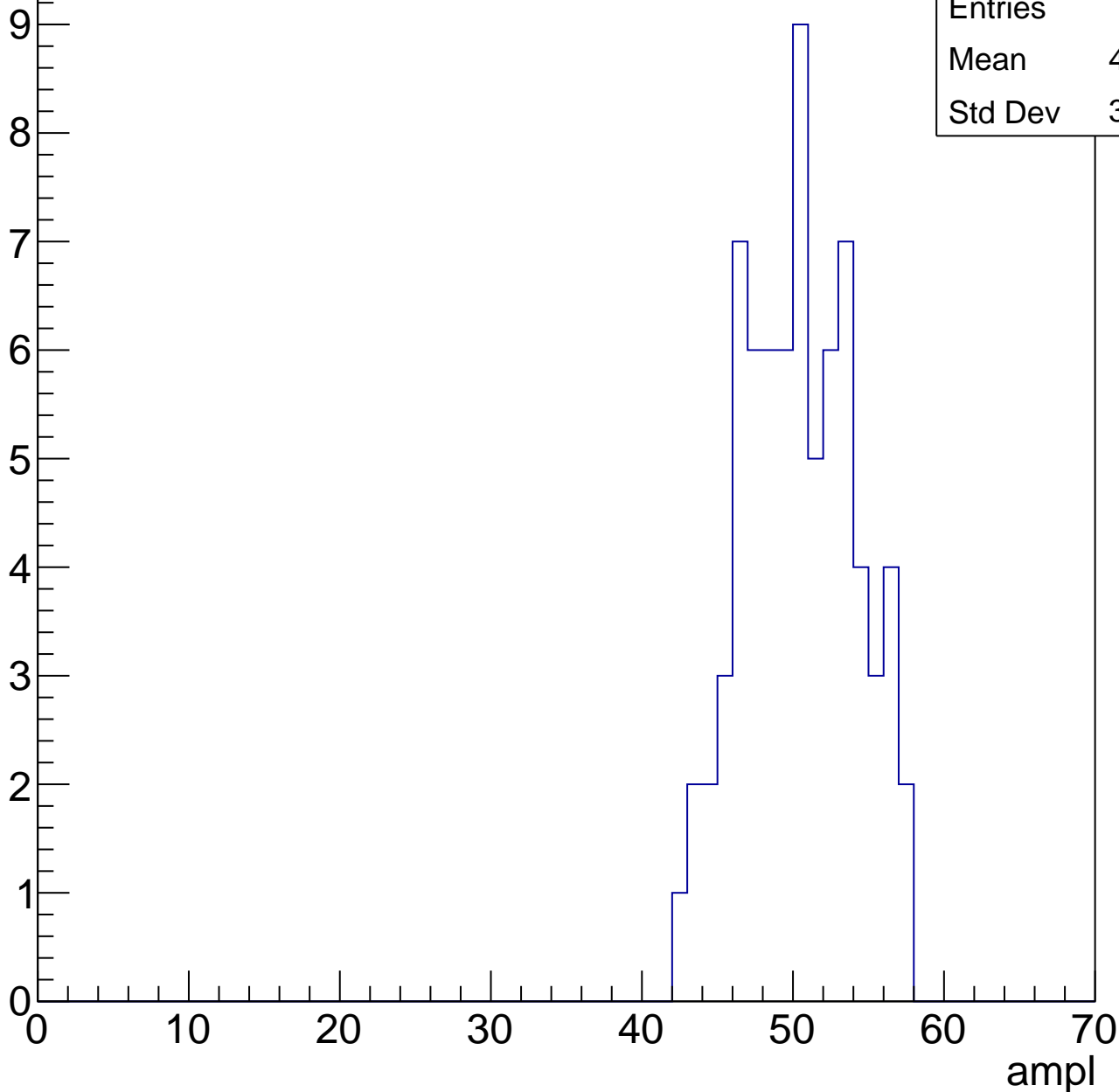


B1L103S, U5-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	49.92
Std Dev	3.659

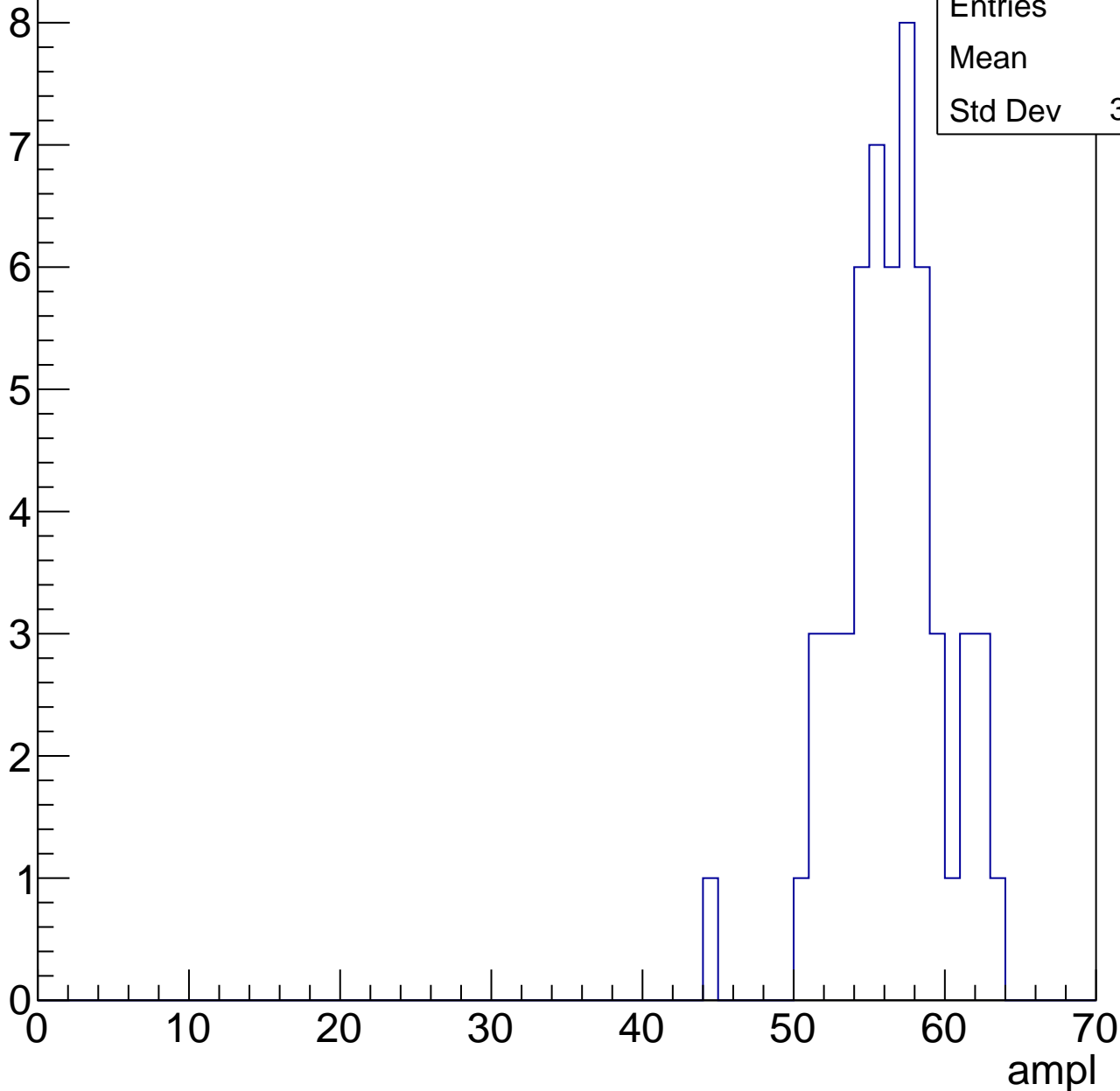


B1L103S, U5-ch77, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56
Std Dev	3.485



B1L103S, U5-ch77, adc5

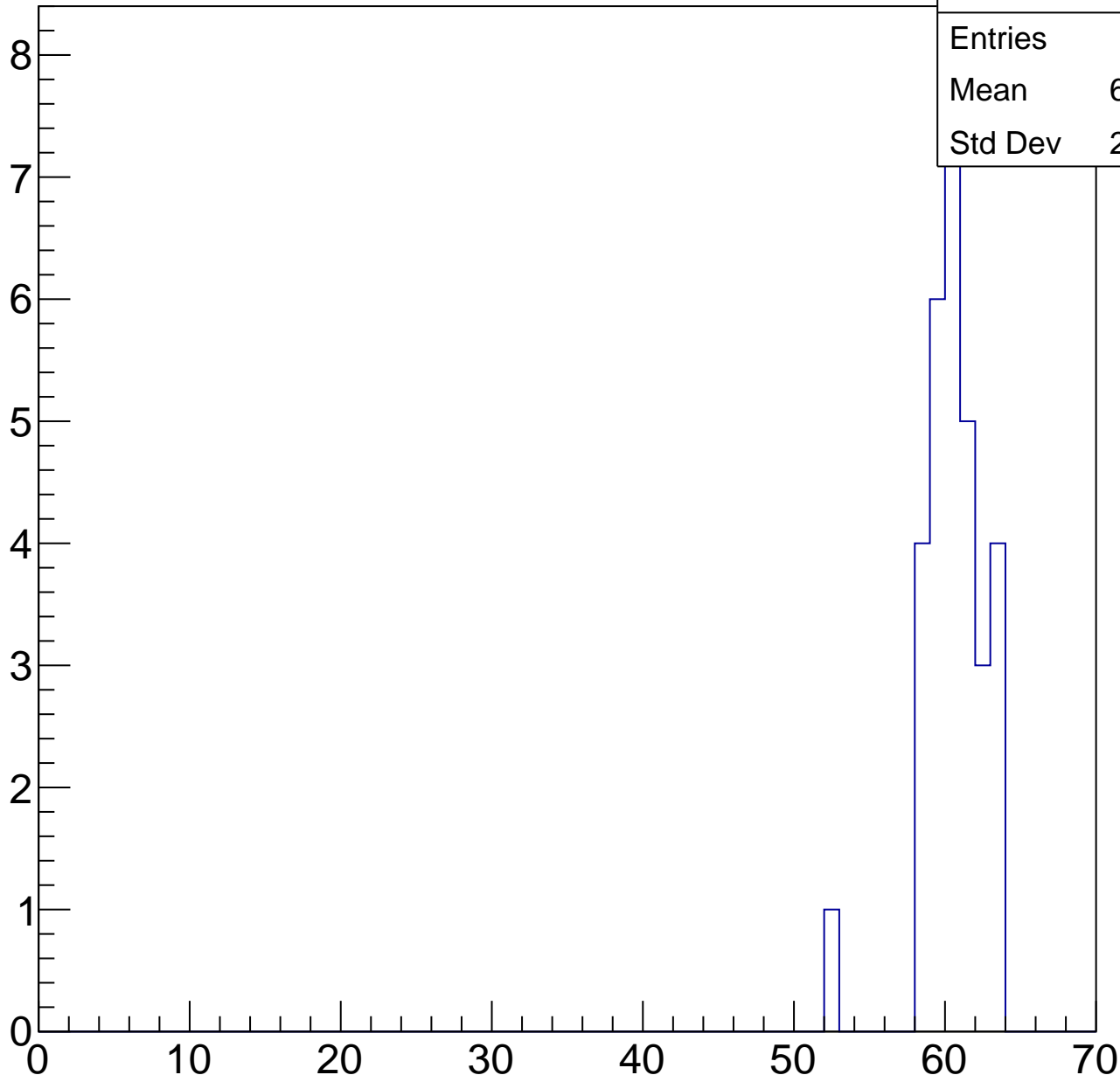
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	31
Mean	60.03
Std Dev	2.117

ampl

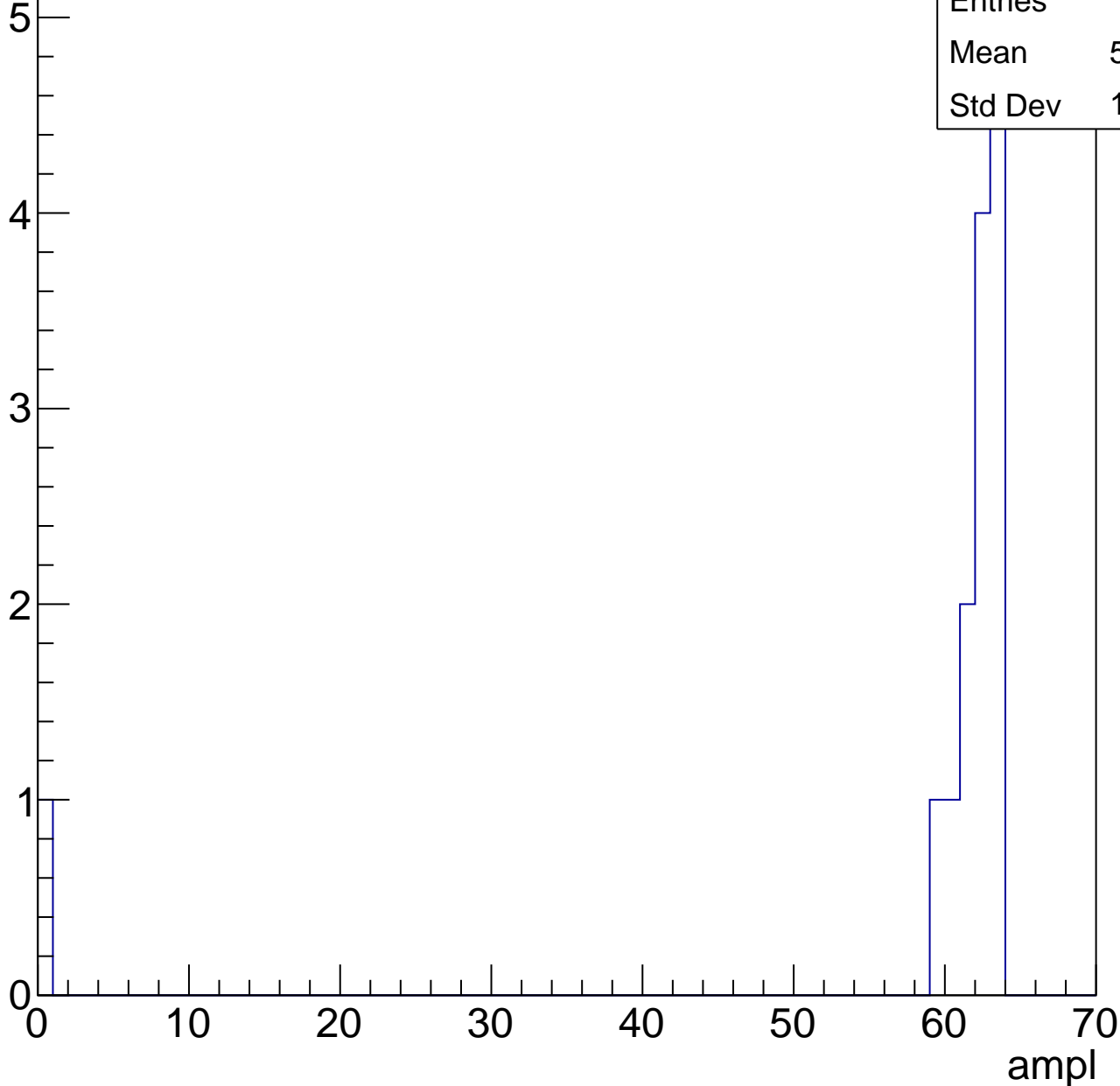


B1L103S, U5-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	57.43
Std Dev	15.97



B1L103S, U5-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch78, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	27.06
Std Dev	5.602

Gaus mean : 28.1569

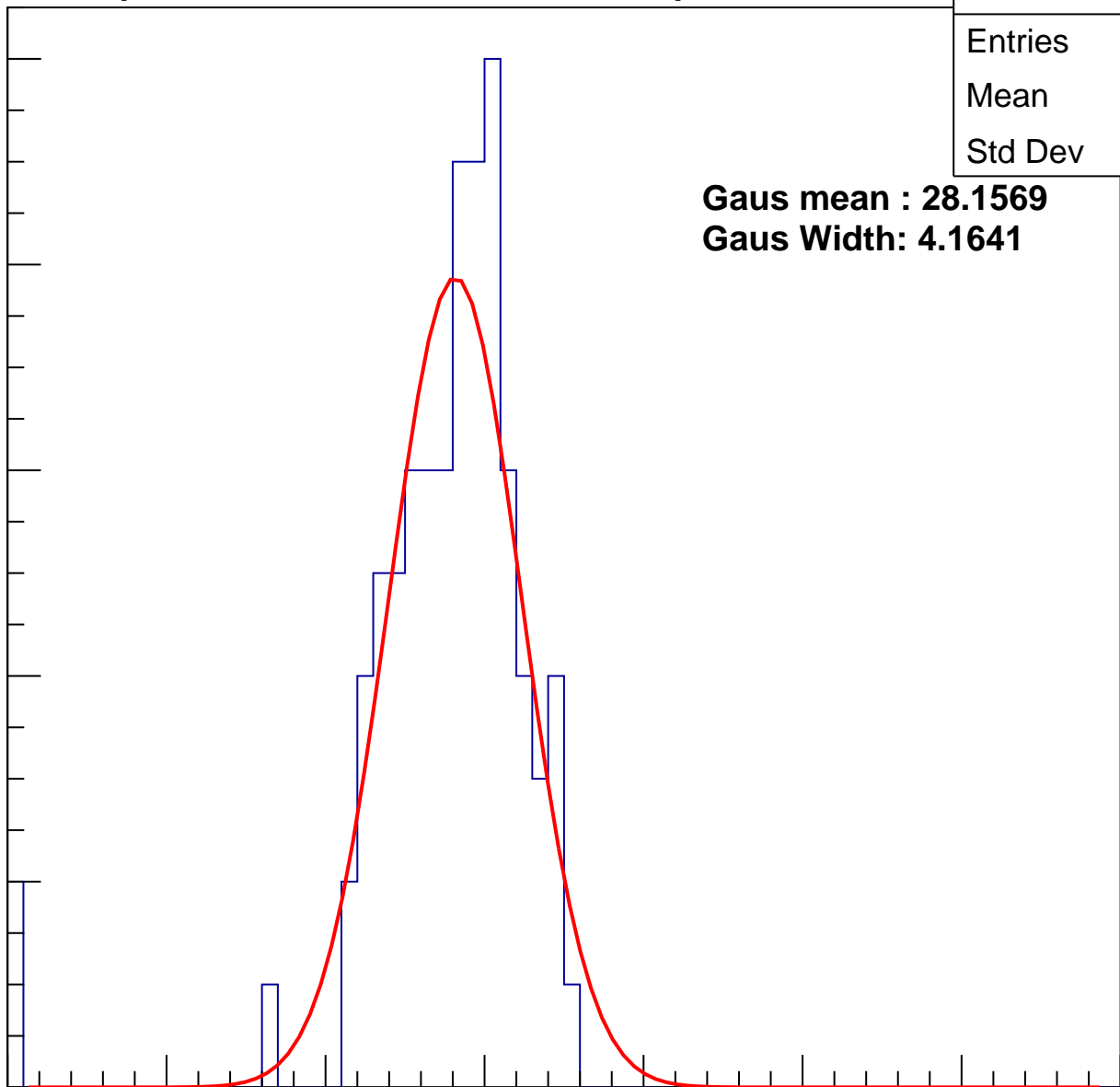
Gaus Width: 4.1641

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch78, adc1

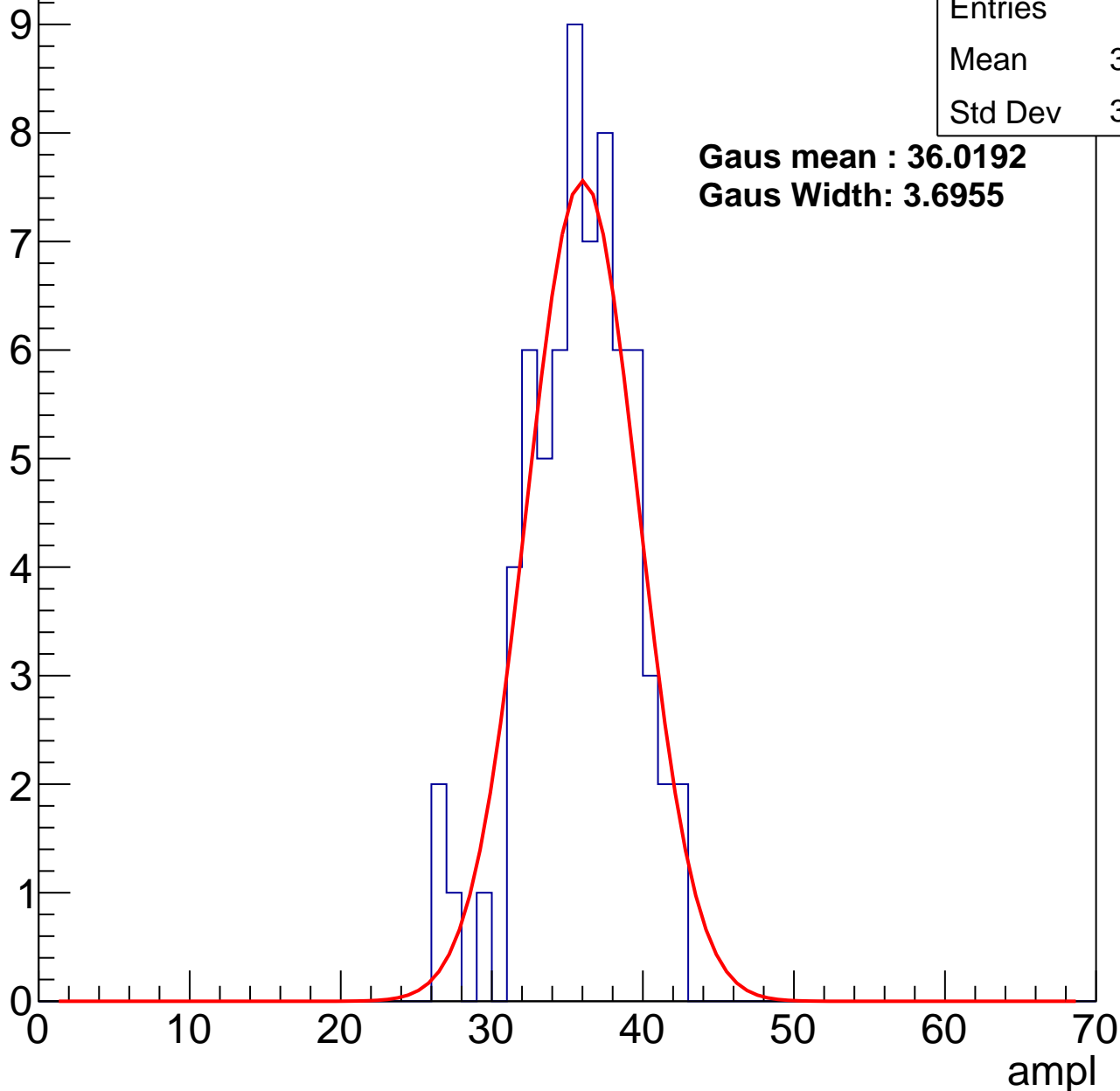
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.35
Std Dev	3.497

Gaus mean : 36.0192

Gaus Width: 3.6955



B1L103S, U5-ch78, adc2

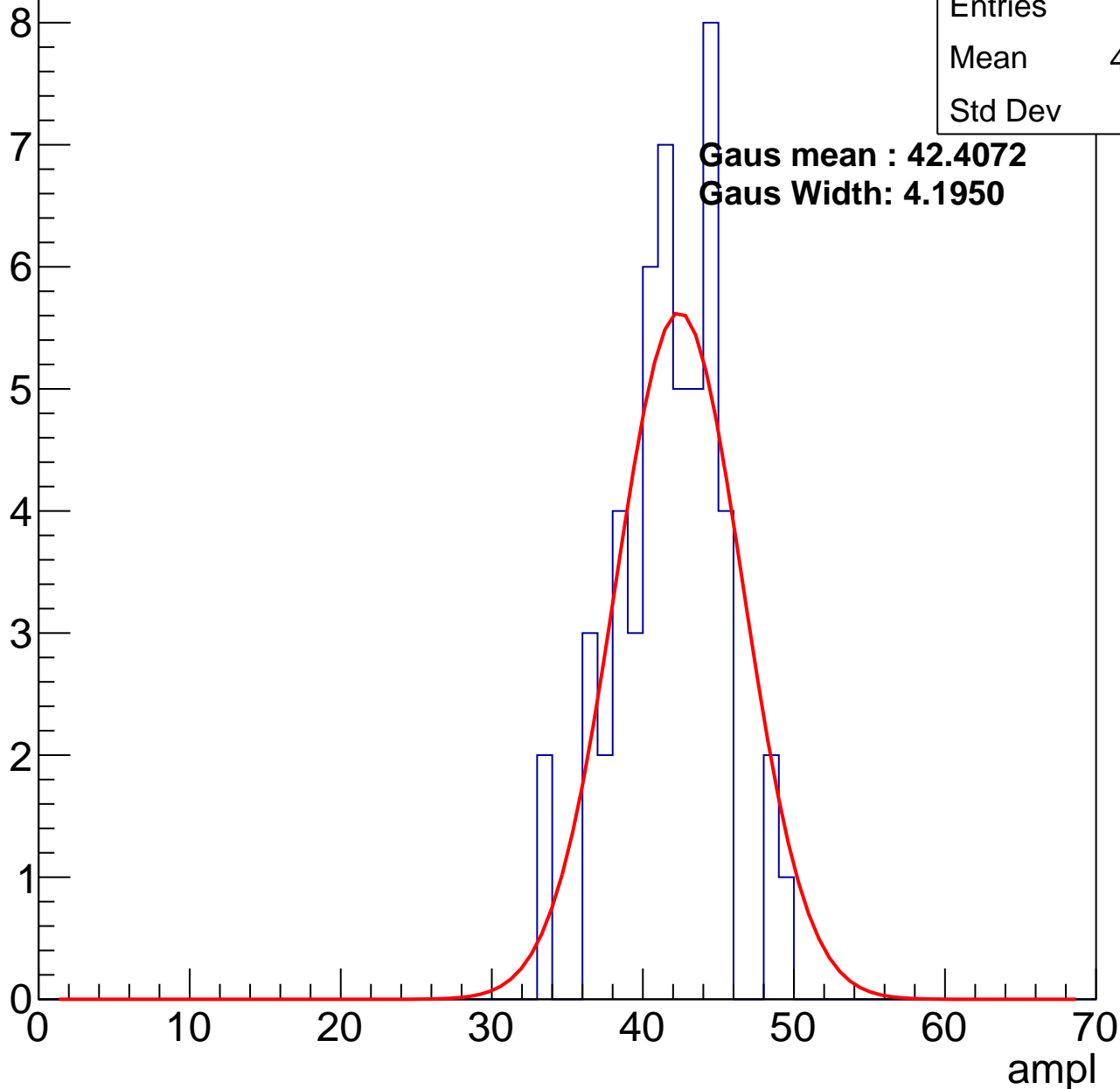
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	41.27
Std Dev	3.42

Gaus mean : 42.4072

Gaus Width: 4.1950

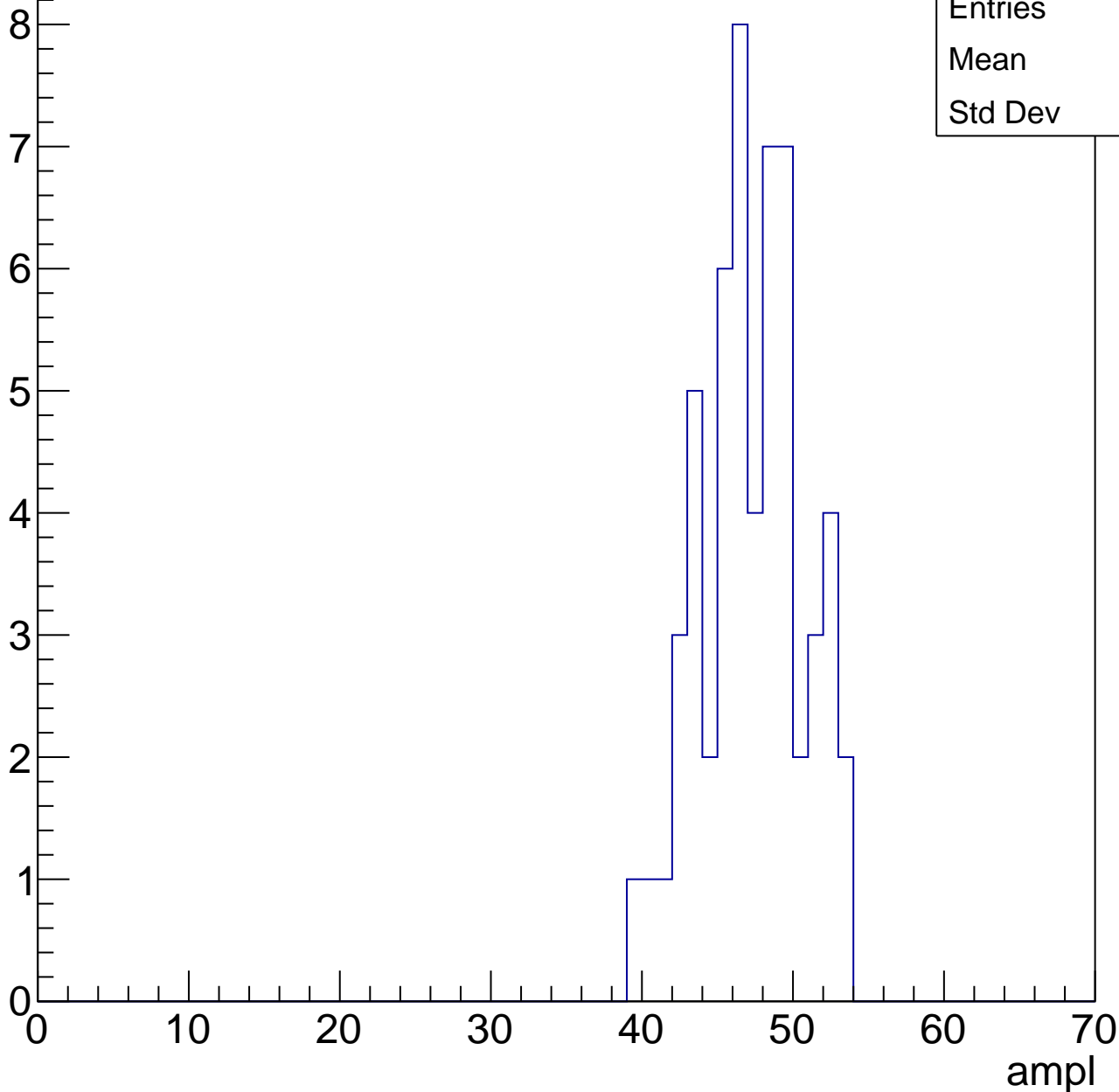


B1L103S, U5-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

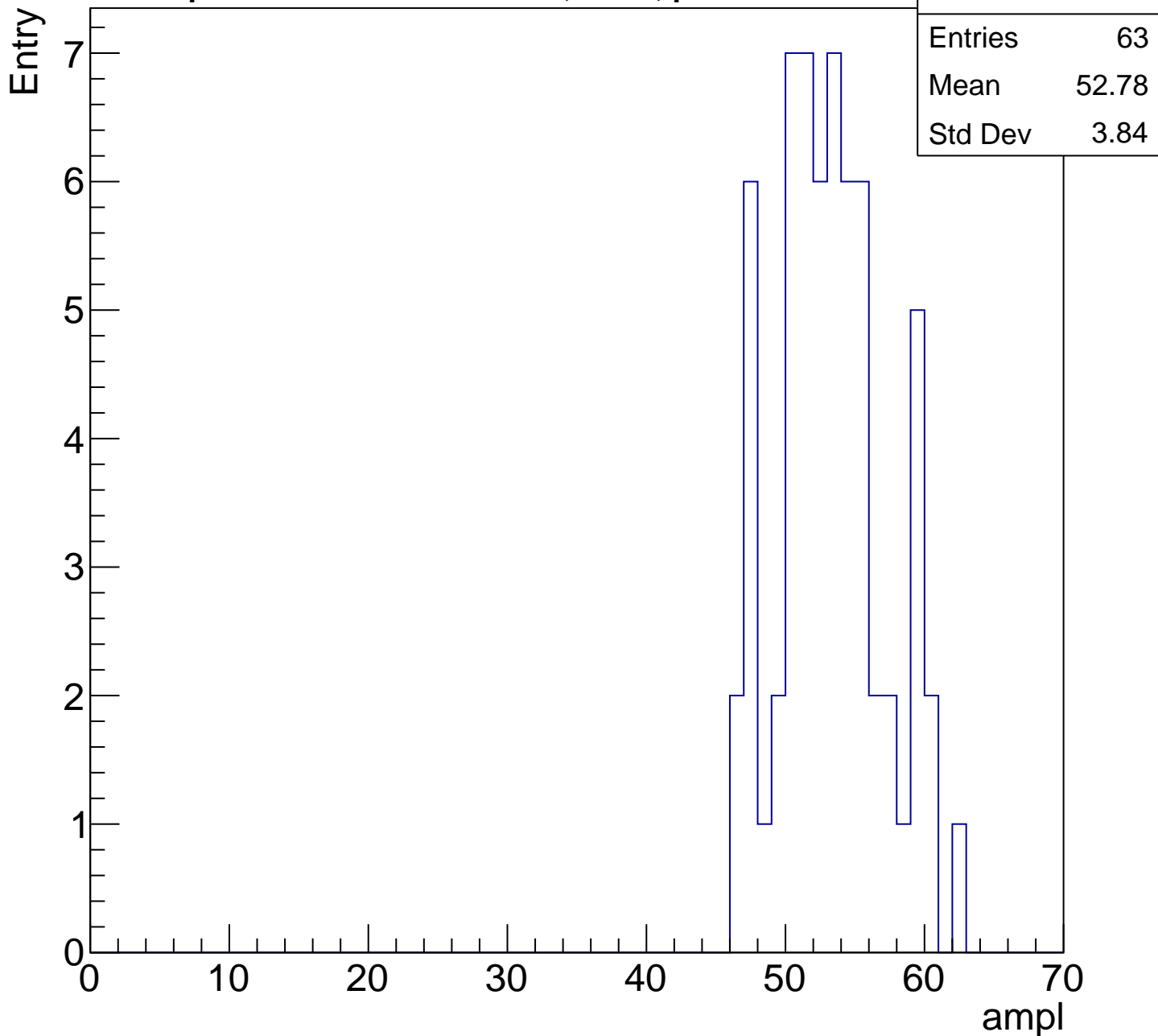
Entry

Entries	56
Mean	46.8
Std Dev	3.34



B1L103S, U5-ch78, adc4

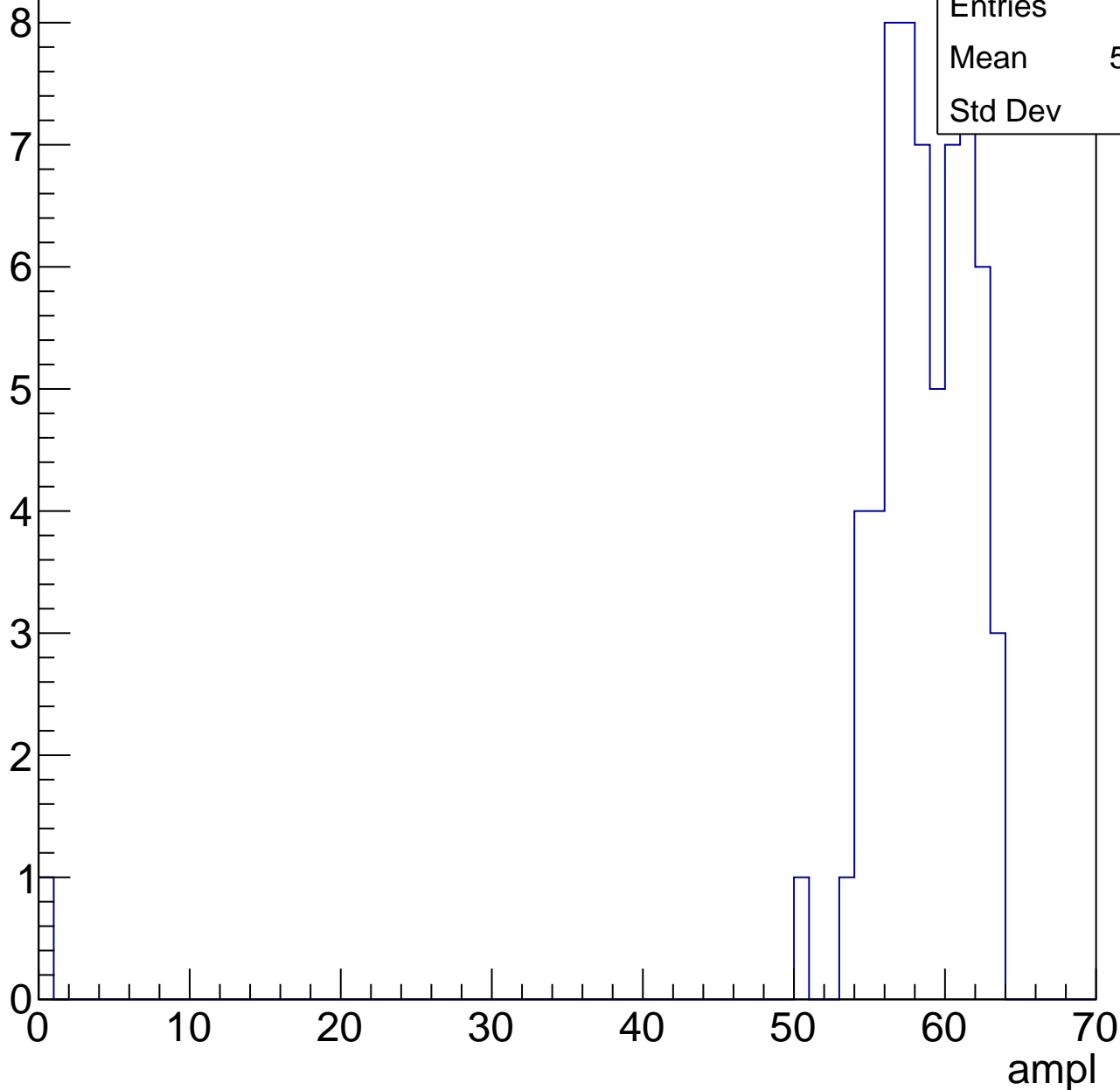
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

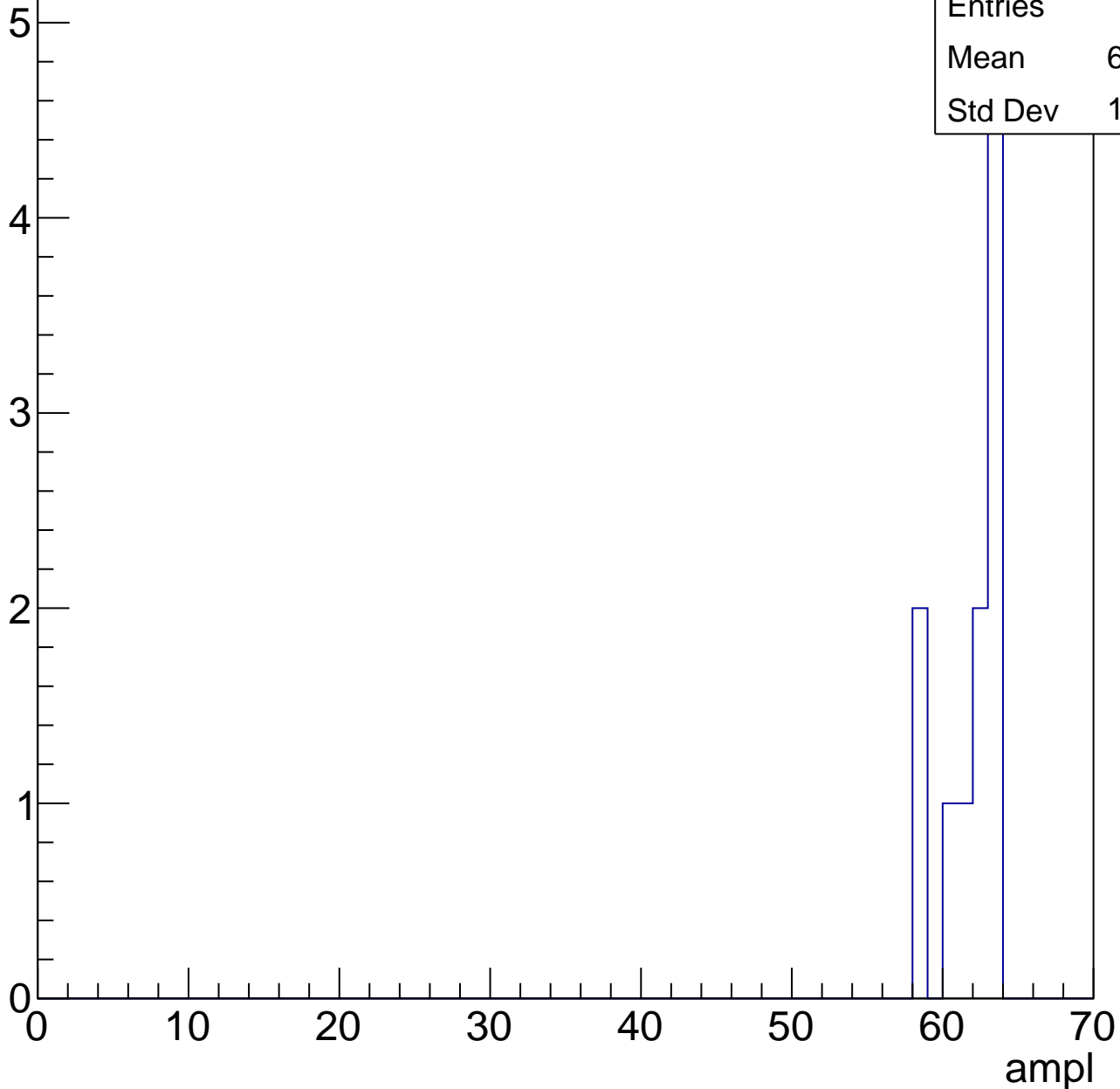


B1L103S, U5-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

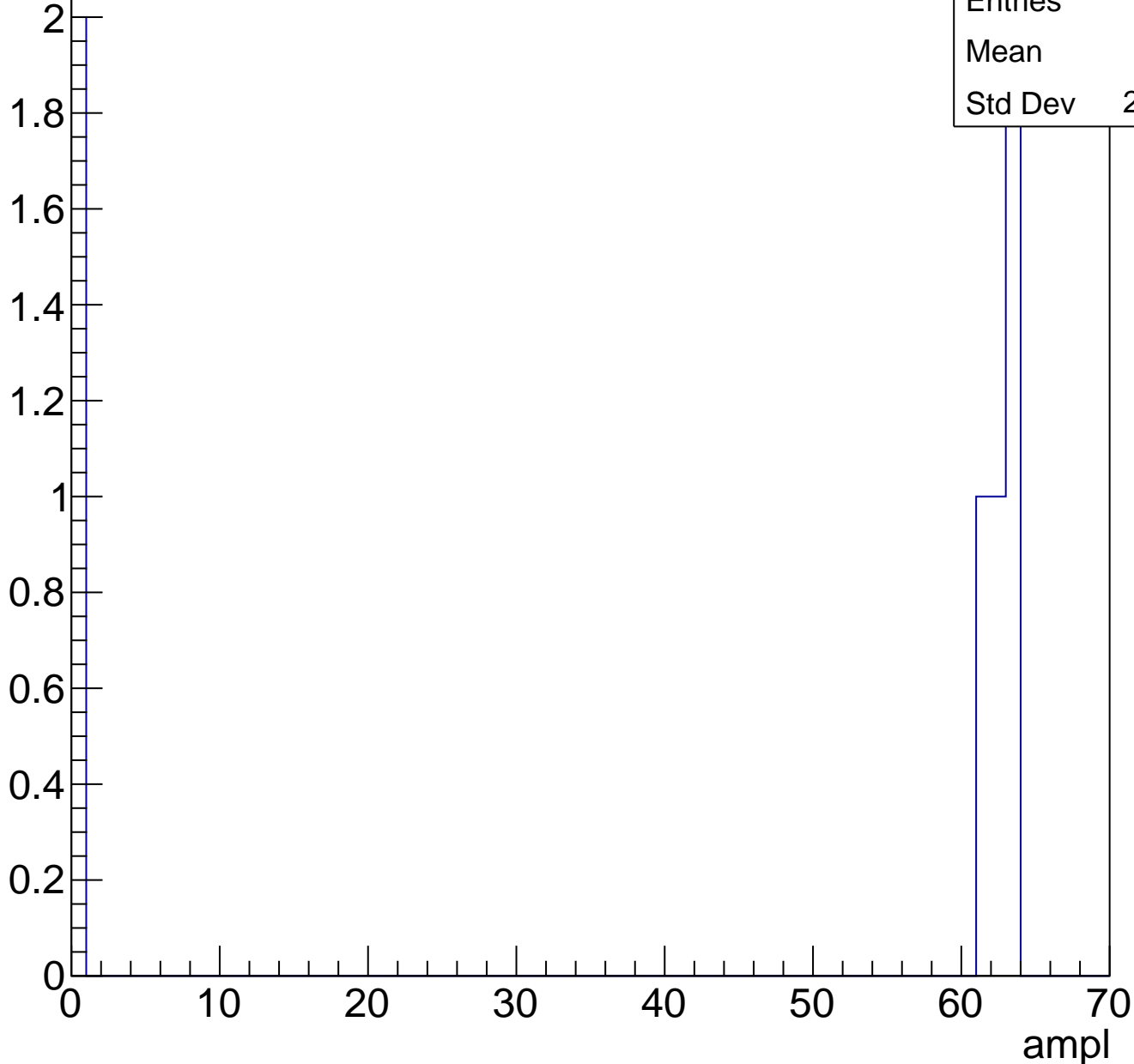
Entries	11
Mean	61.45
Std Dev	1.876



B1L103S, U5-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch79, adc0

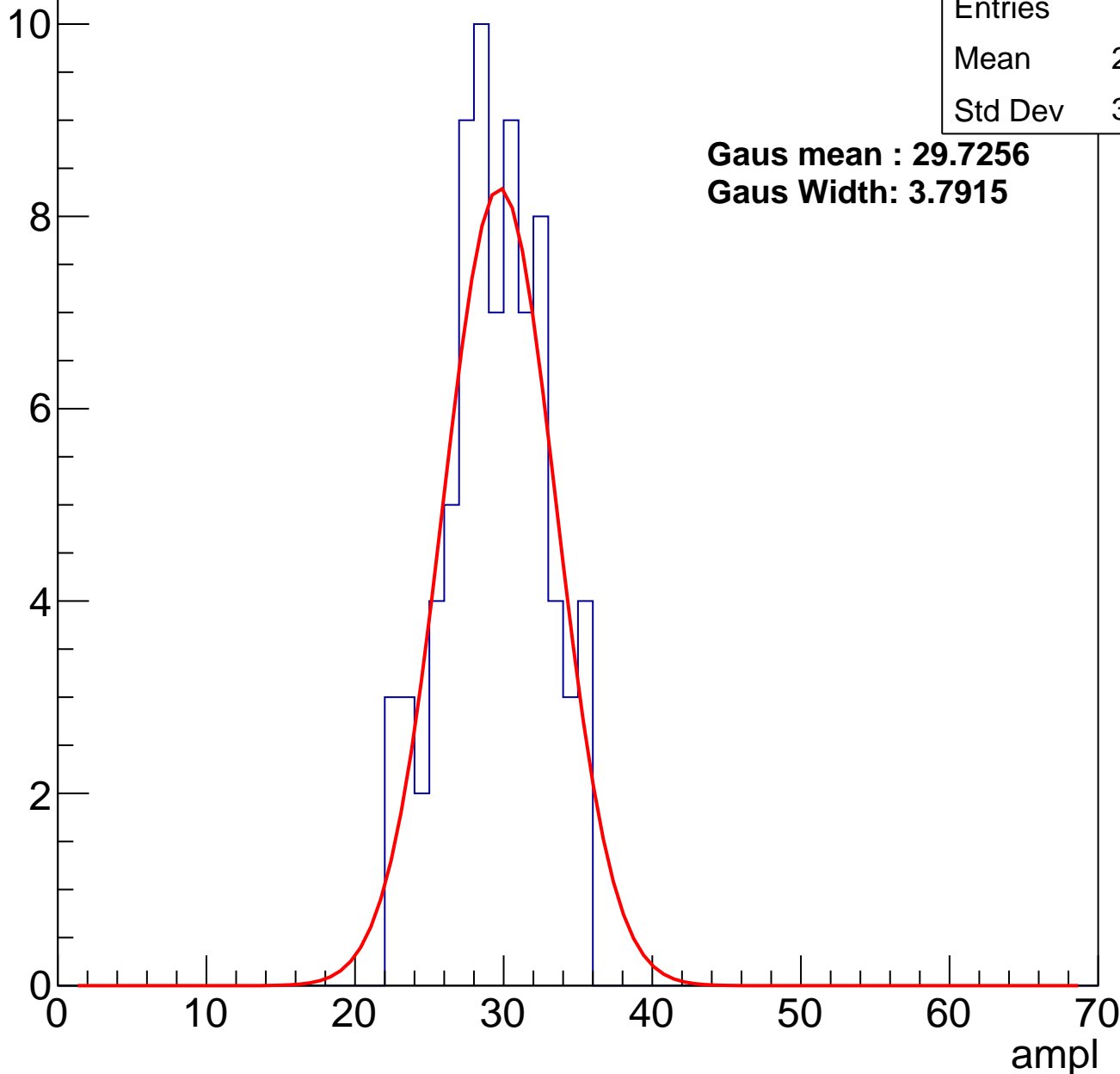
calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	28.92
Std Dev	3.304

Gaus mean : 29.7256

Gaus Width: 3.7915

Entry



B1L103S, U5-ch79, adc1

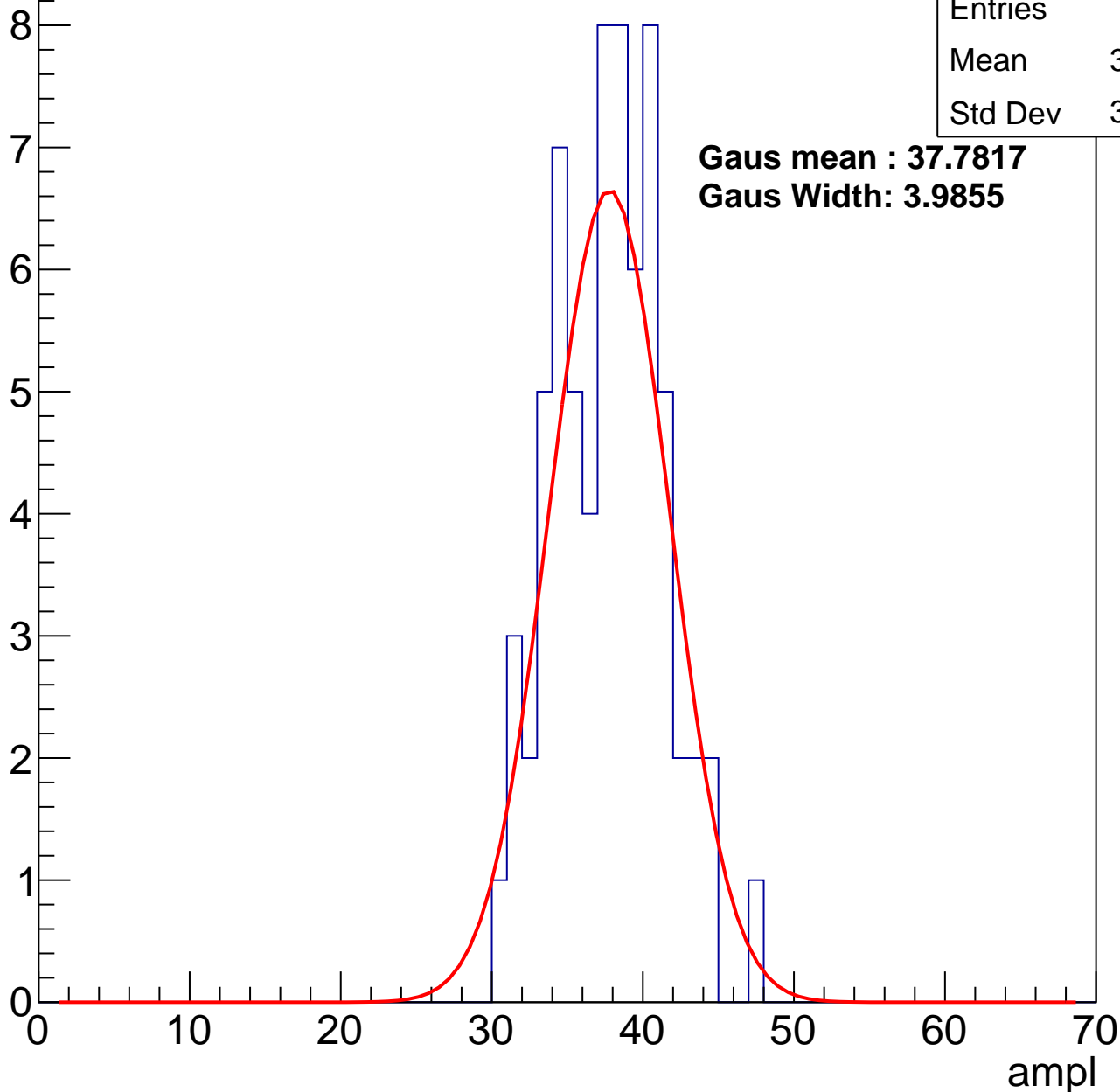
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	37.29
Std Dev	3.555

Gaus mean : 37.7817

Gaus Width: 3.9855



B1L103S, U5-ch79, adc2

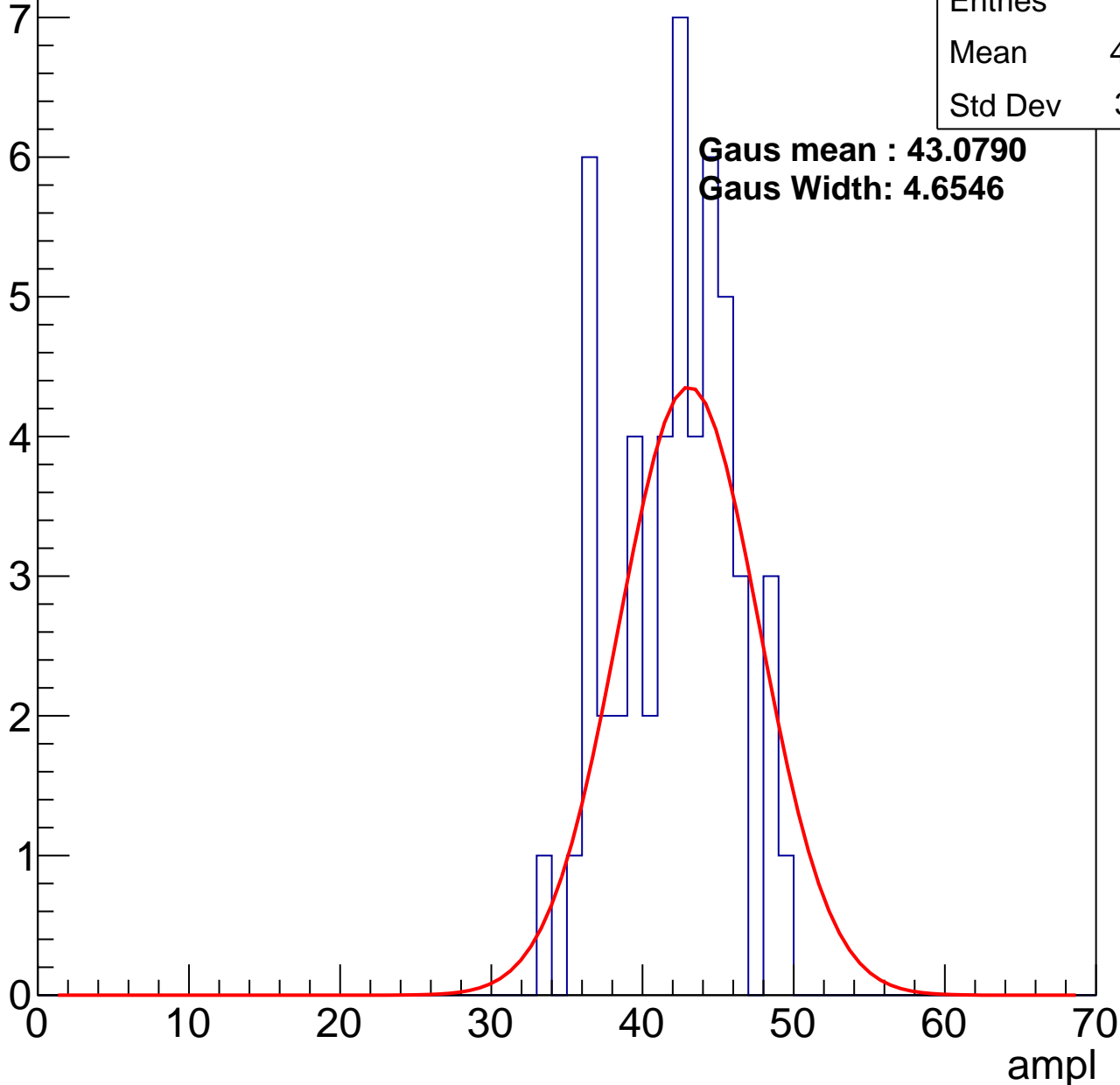
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.57
Std Dev	3.821

Gaus mean : 43.0790

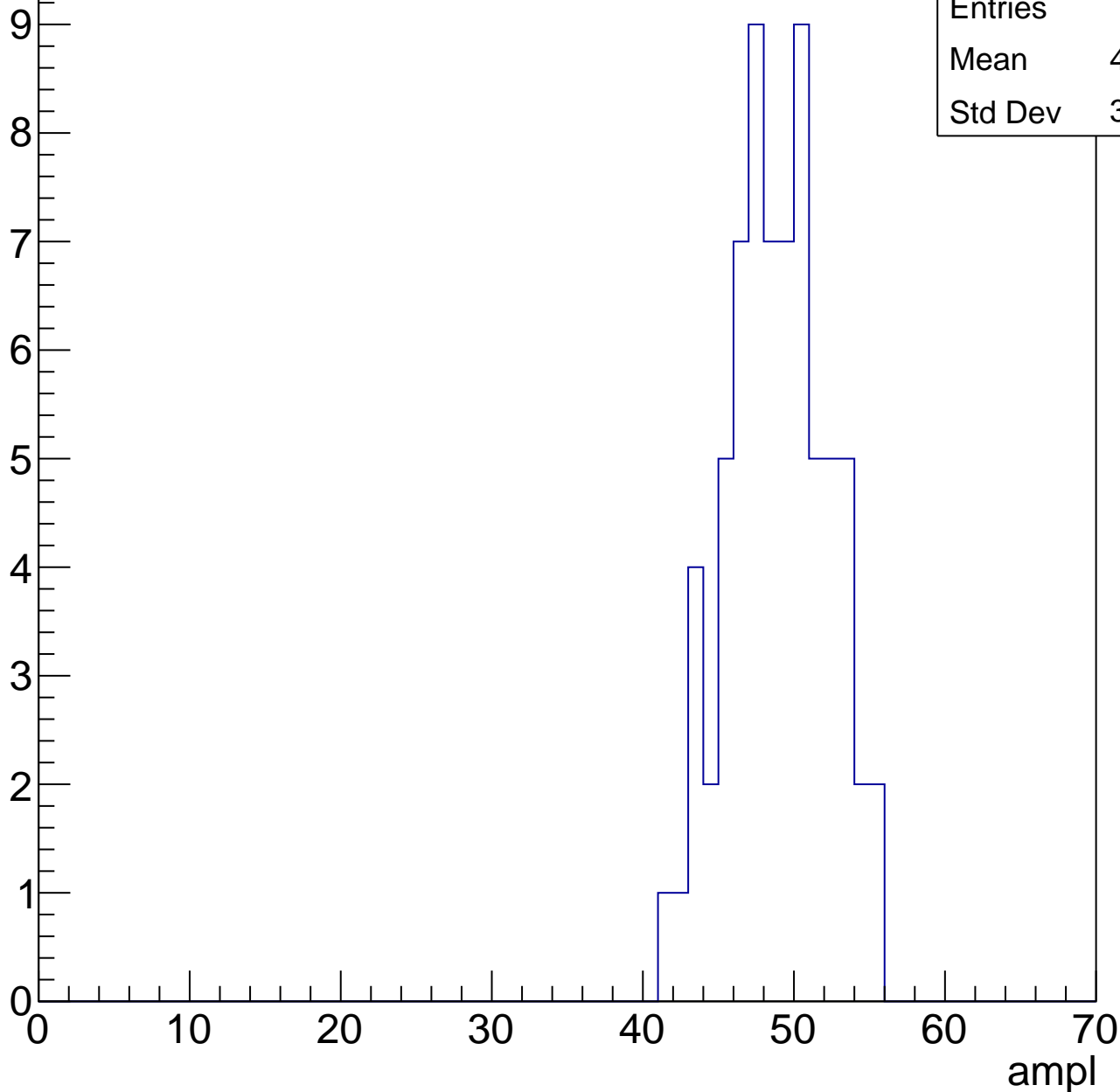
Gaus Width: 4.6546



B1L103S, U5-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



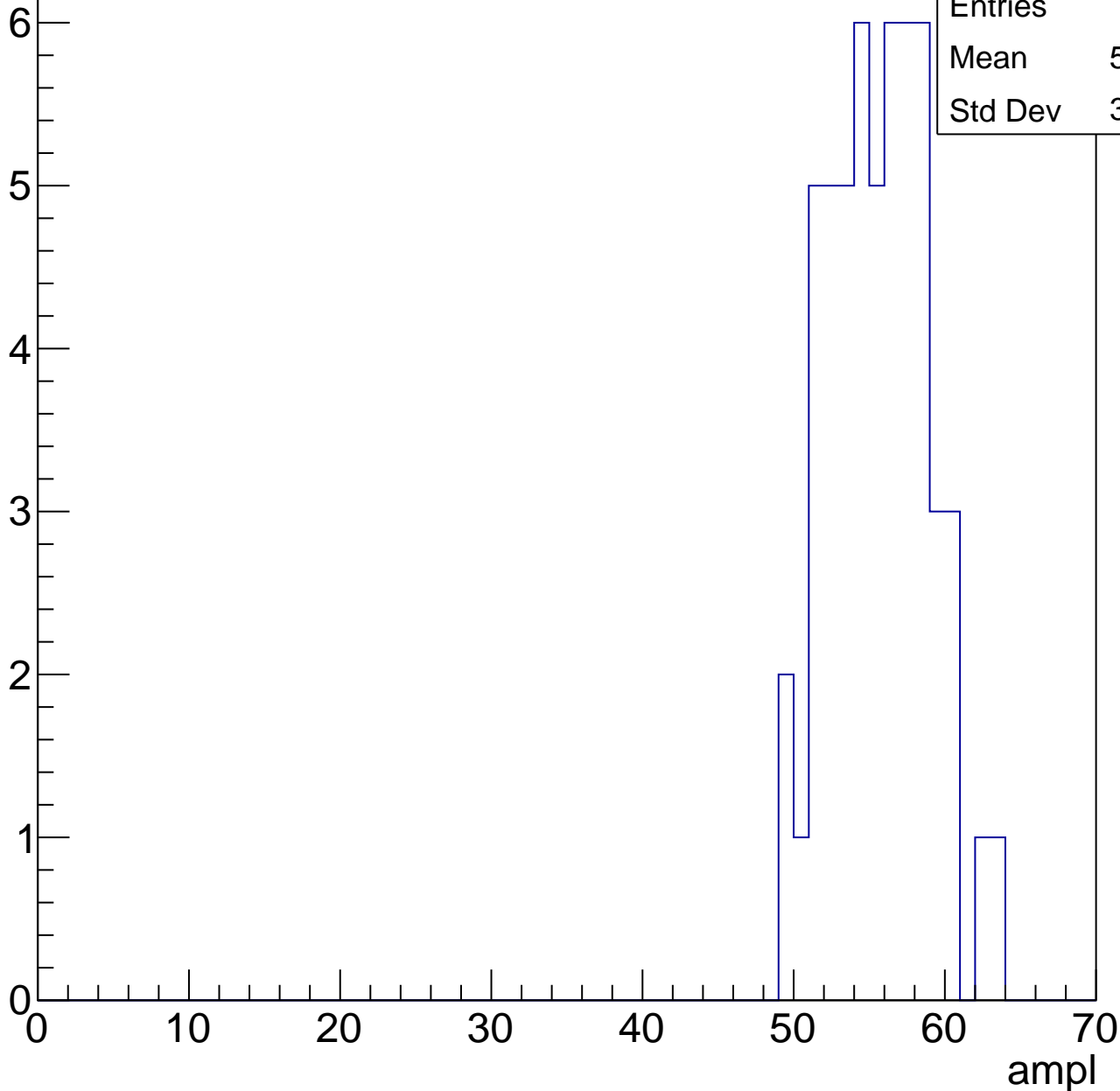
Entries	71
Mean	48.45
Std Dev	3.245

B1L103S, U5-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	55.18
Std Dev	3.208



B1L103S, U5-ch79, adc5

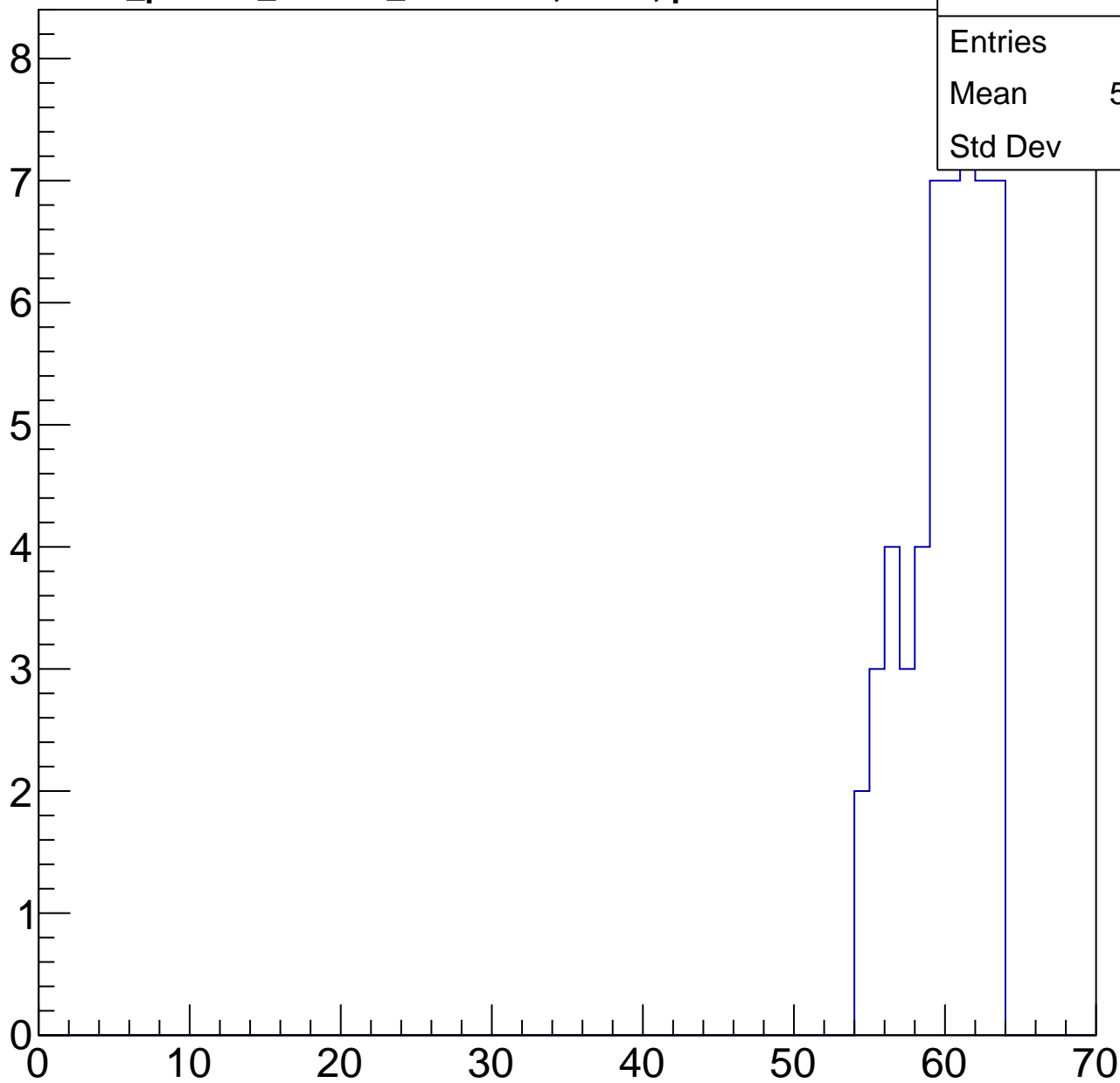
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.54
Std Dev	2.59

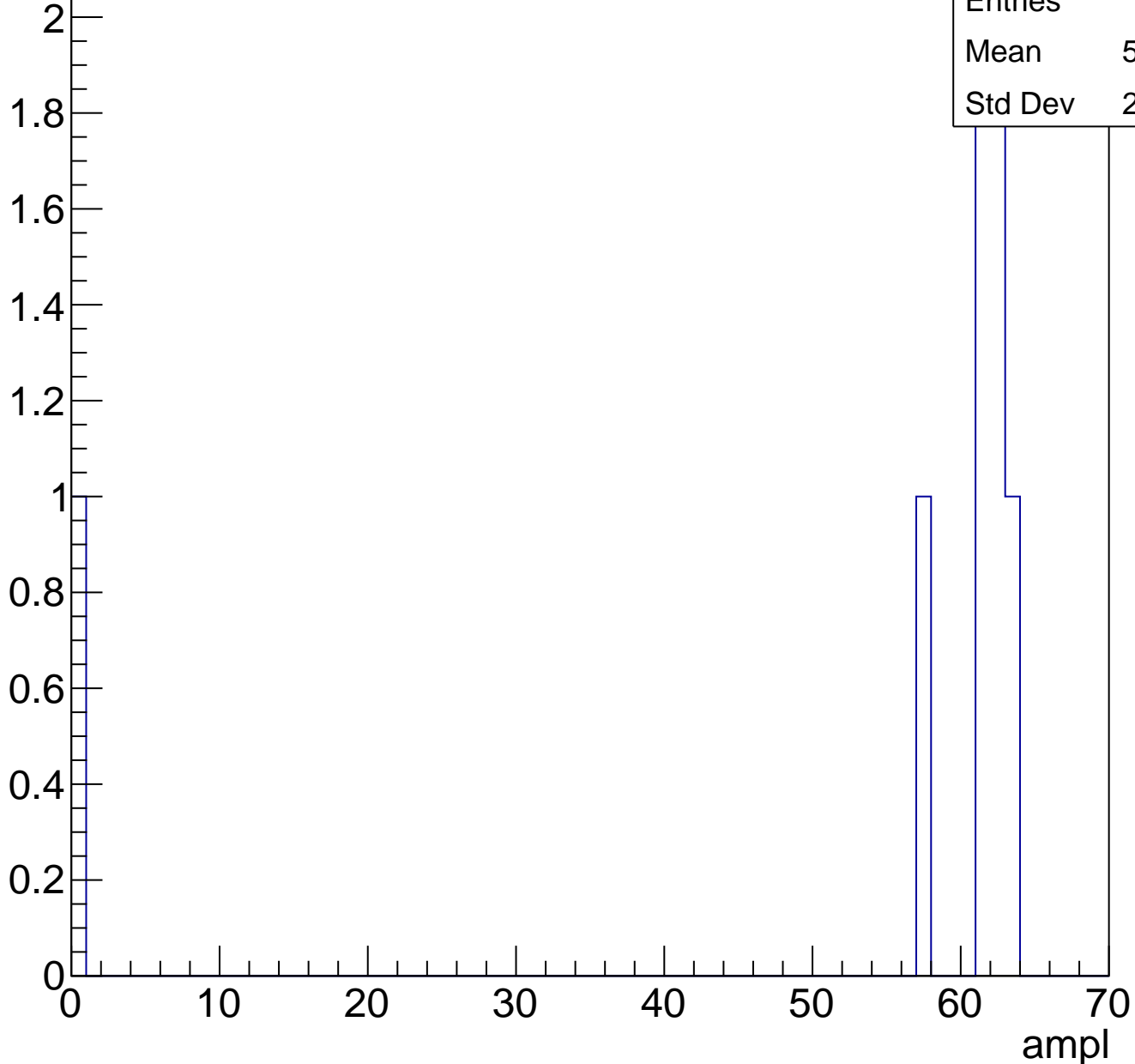
ampl



B1L103S, U5-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch80, adc0

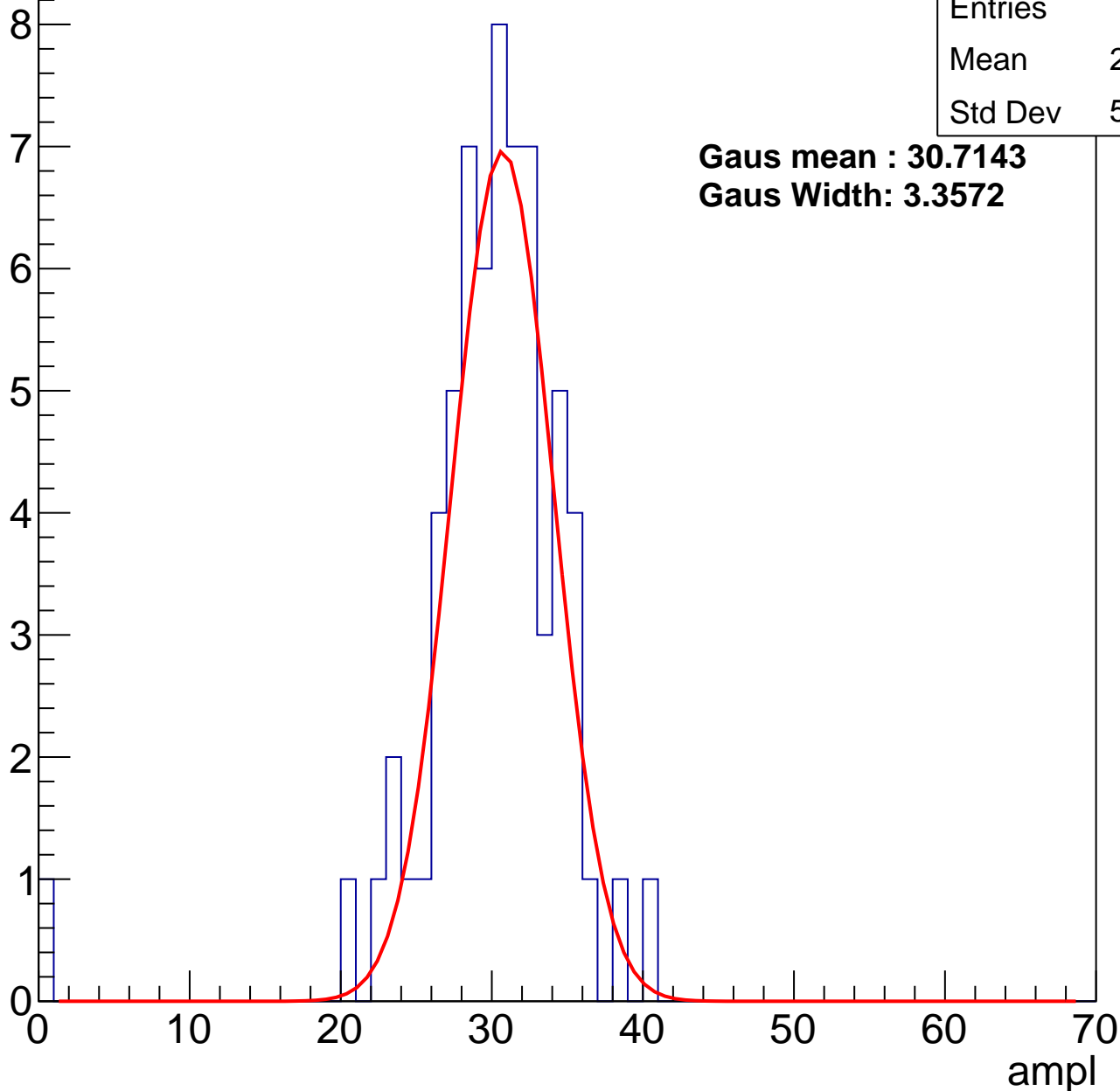
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	29.55
Std Dev	5.214

Gaus mean : 30.7143

Gaus Width: 3.3572



B1L103S, U5-ch80, adc1

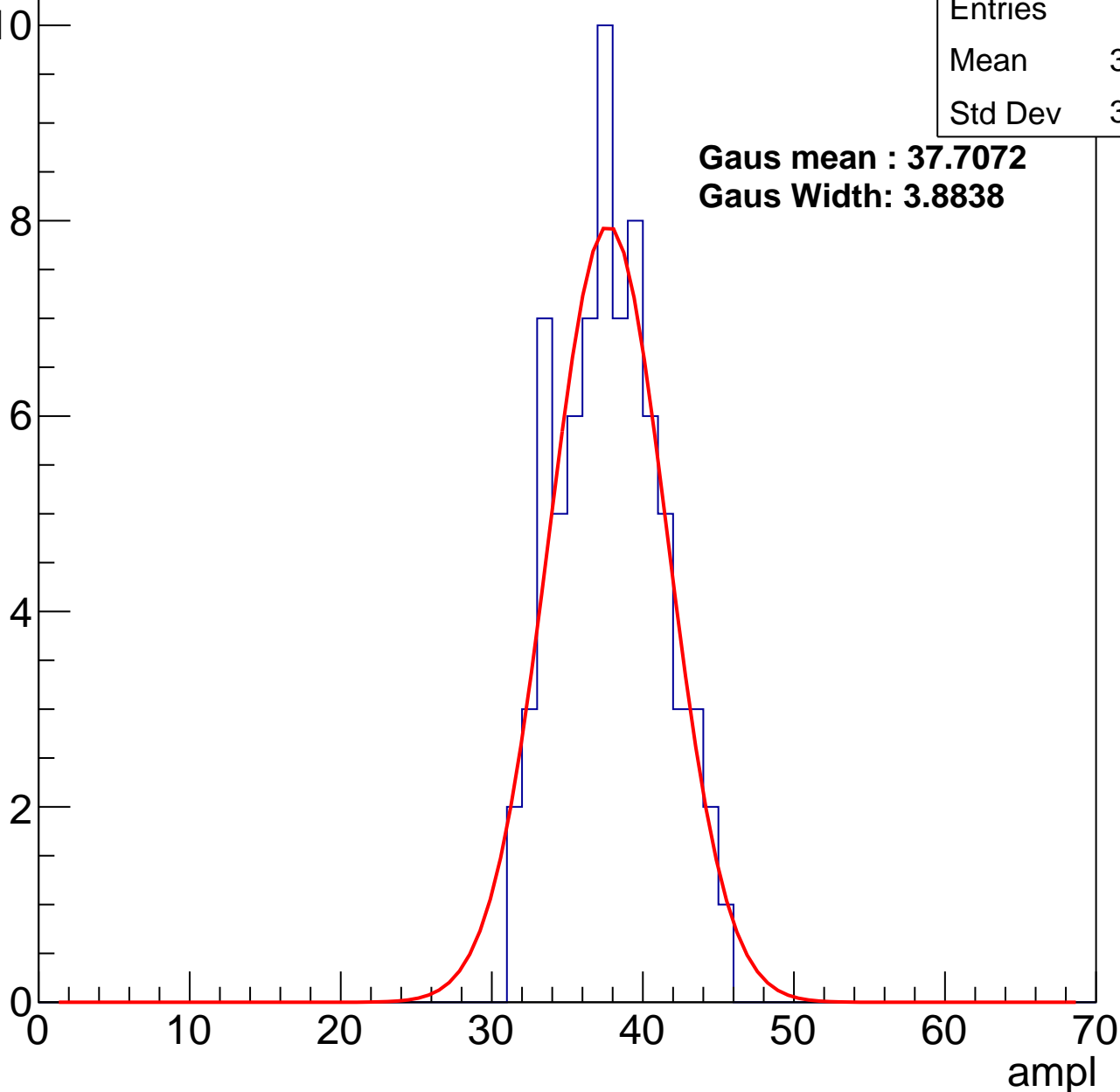
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.36
Std Dev	3.353

Gaus mean : 37.7072

Gaus Width: 3.8838



B1L103S, U5-ch80, adc2

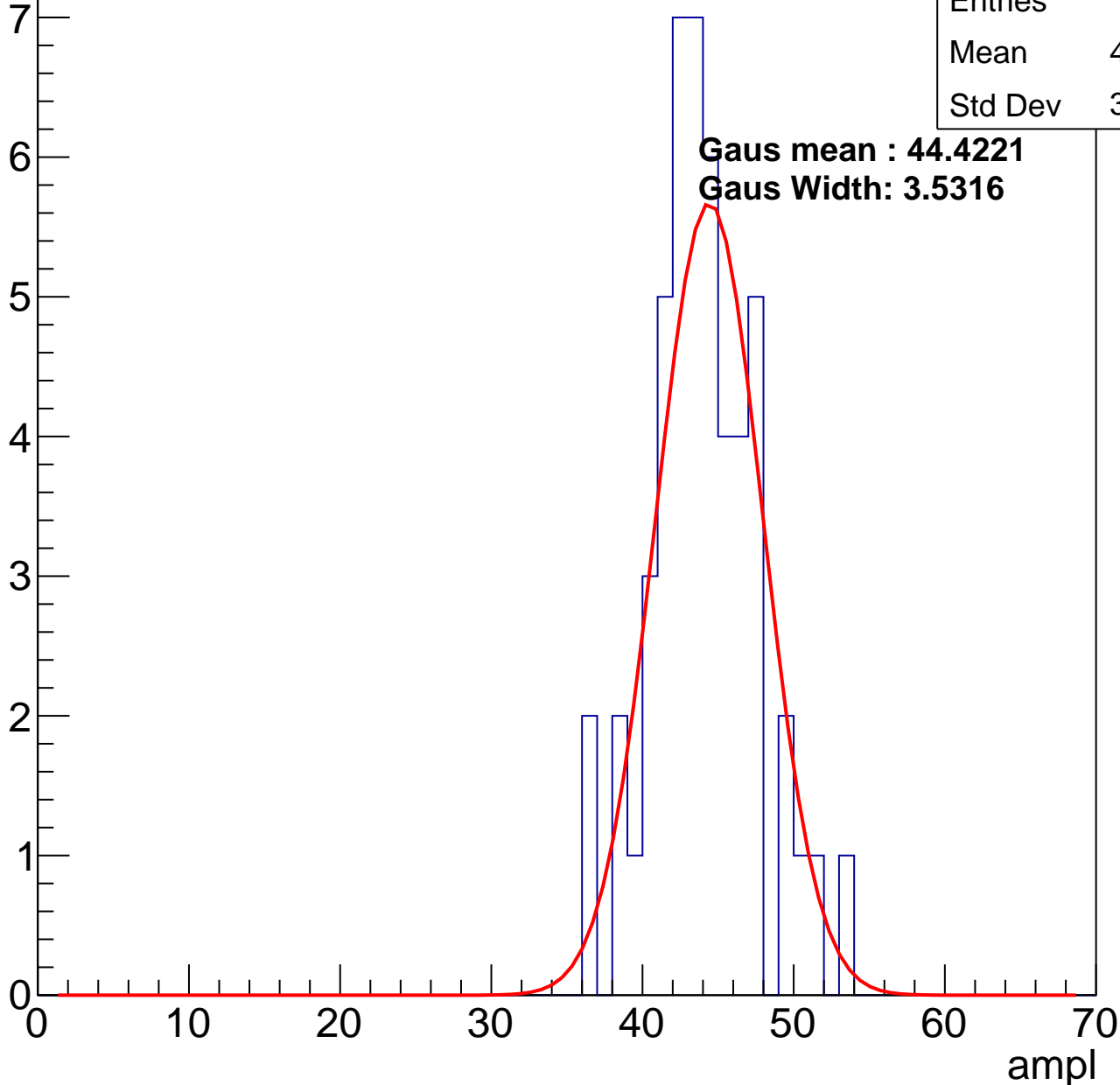
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	43.57
Std Dev	3.499

Gaus mean : 44.4221

Gaus Width: 3.5316

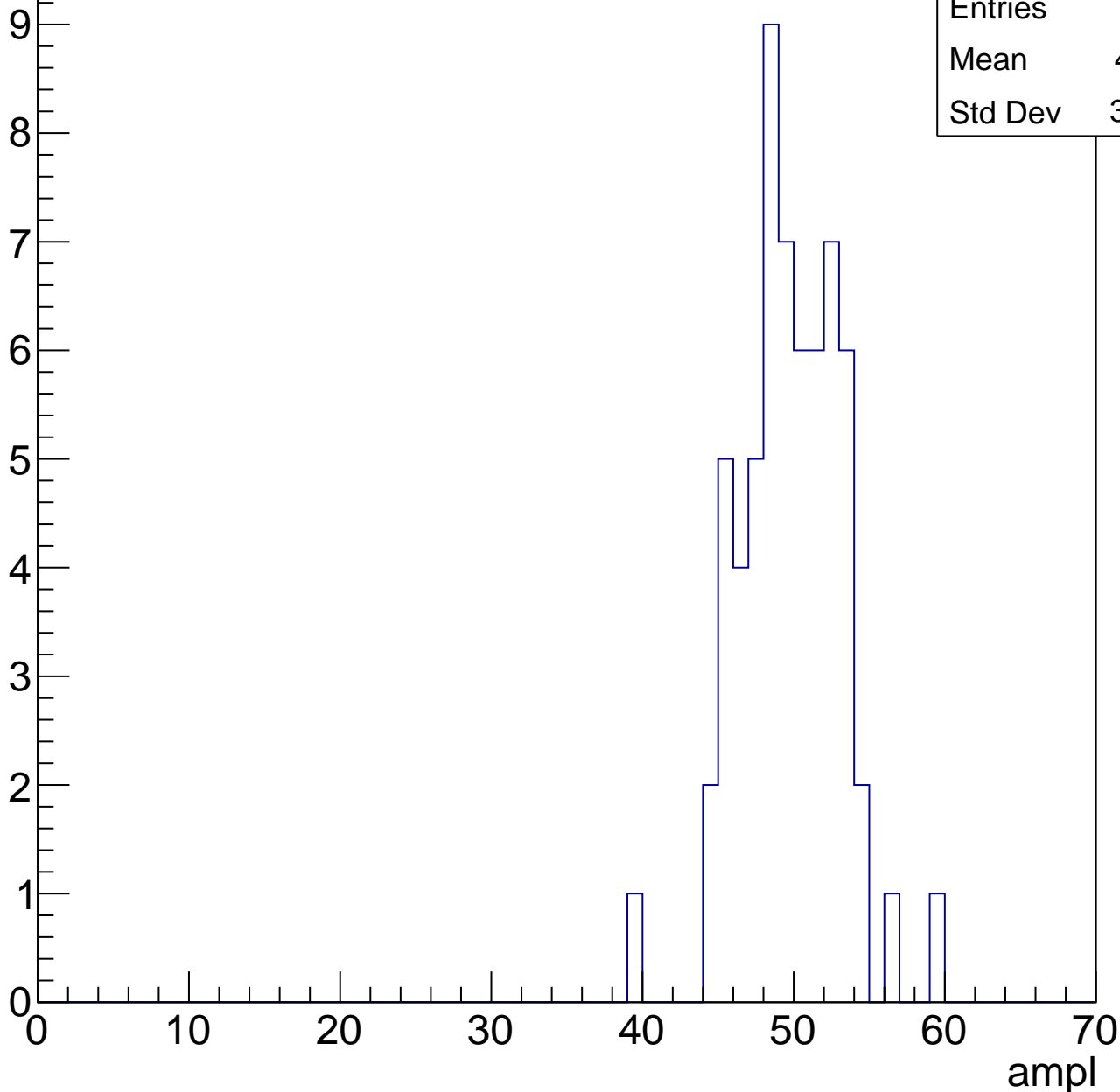


B1L103S, U5-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

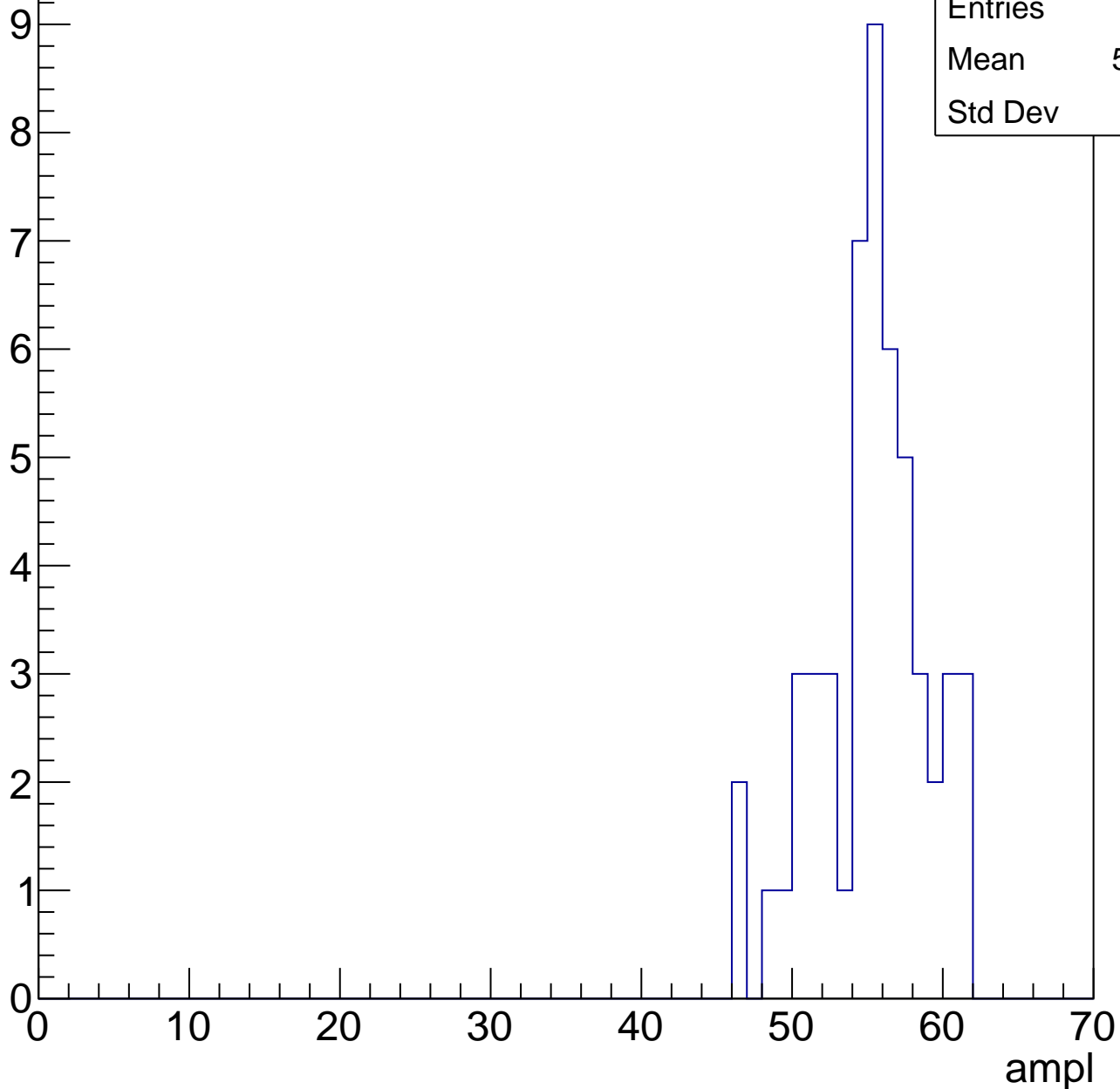
Entries	62
Mean	49.31
Std Dev	3.305



B1L103S, U5-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

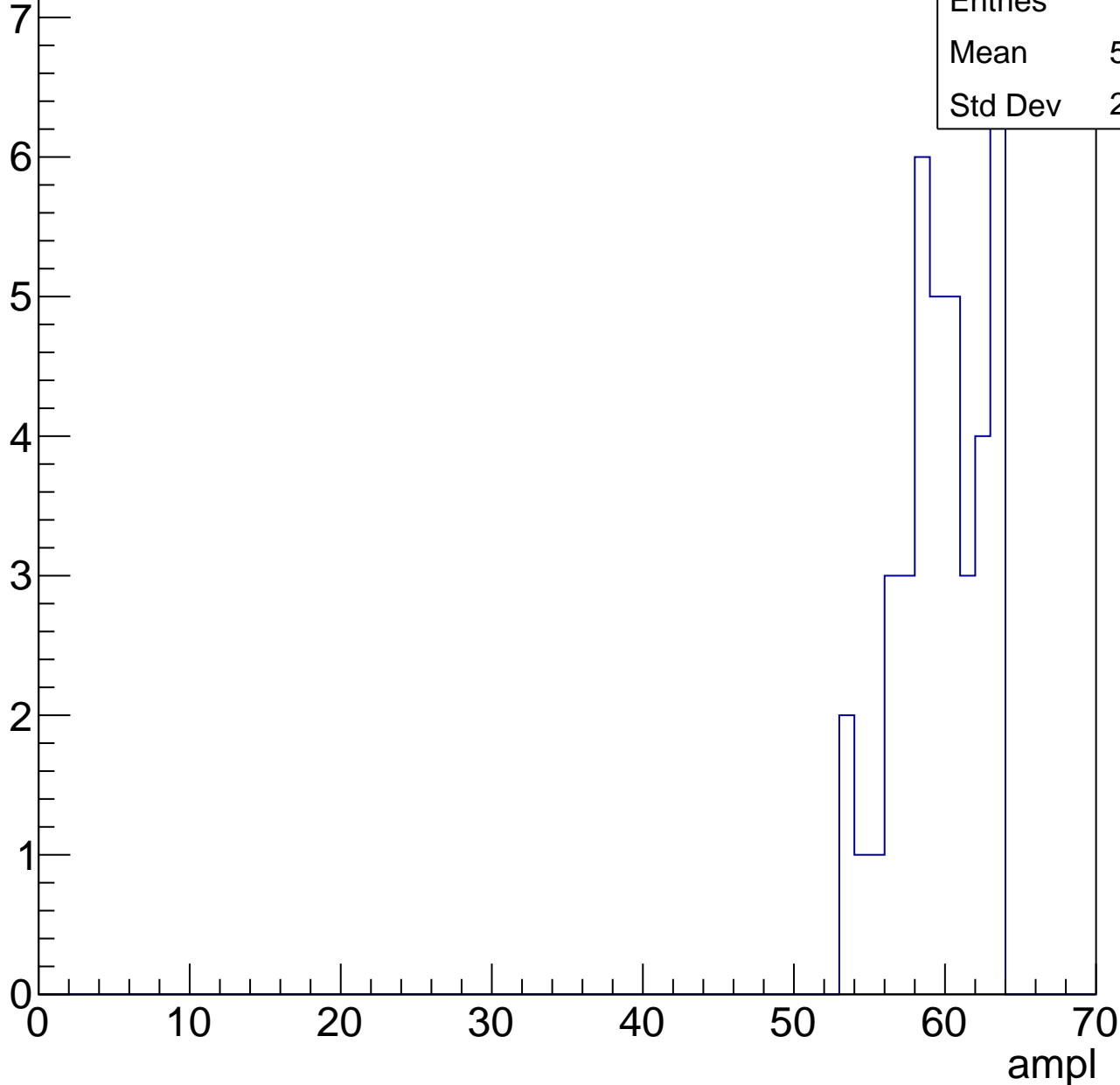


B1L103S, U5-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	59.23
Std Dev	2.833

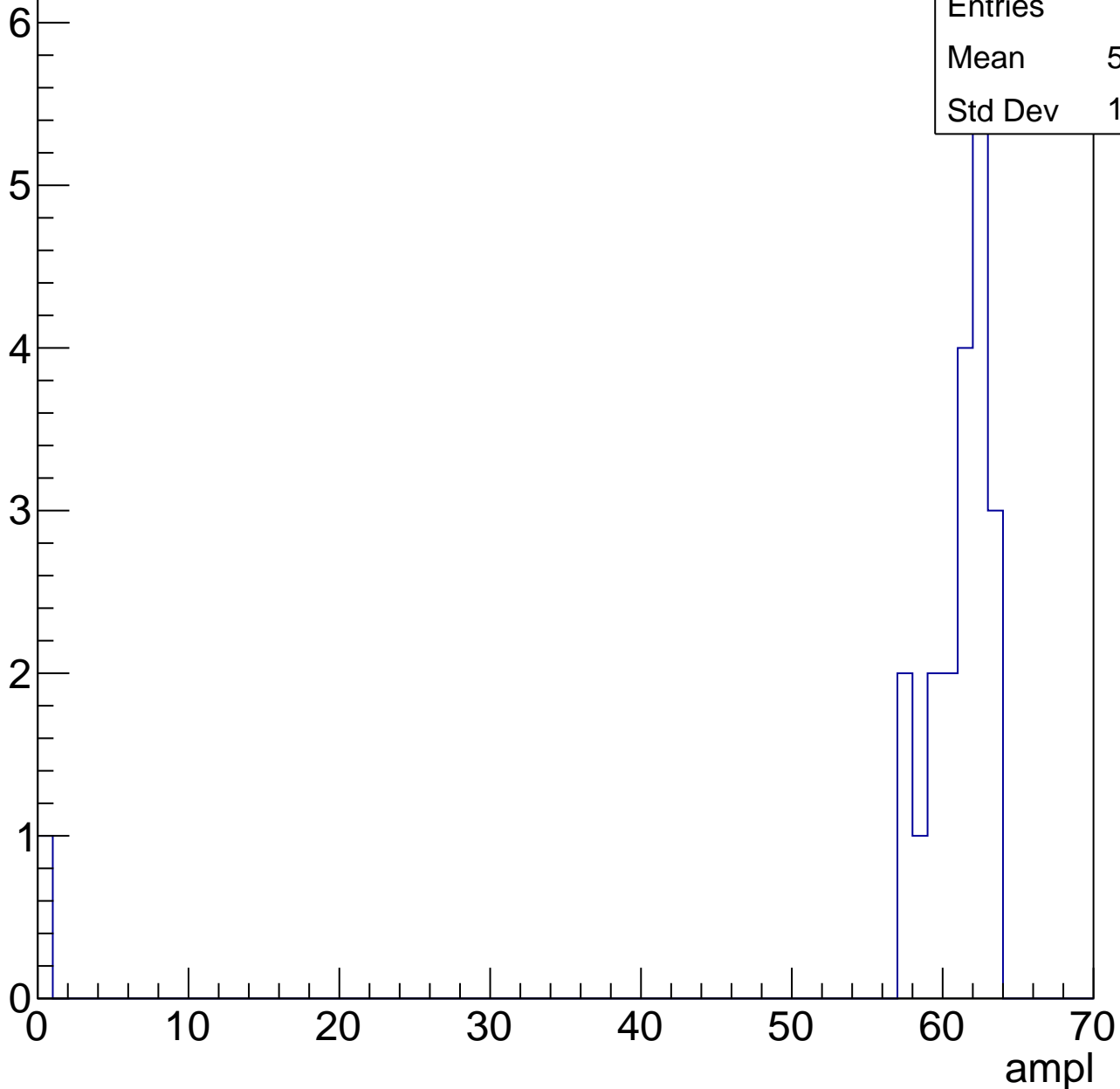


B1L103S, U5-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	57.86
Std Dev	13.06



B1L103S, U5-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch81, adc0

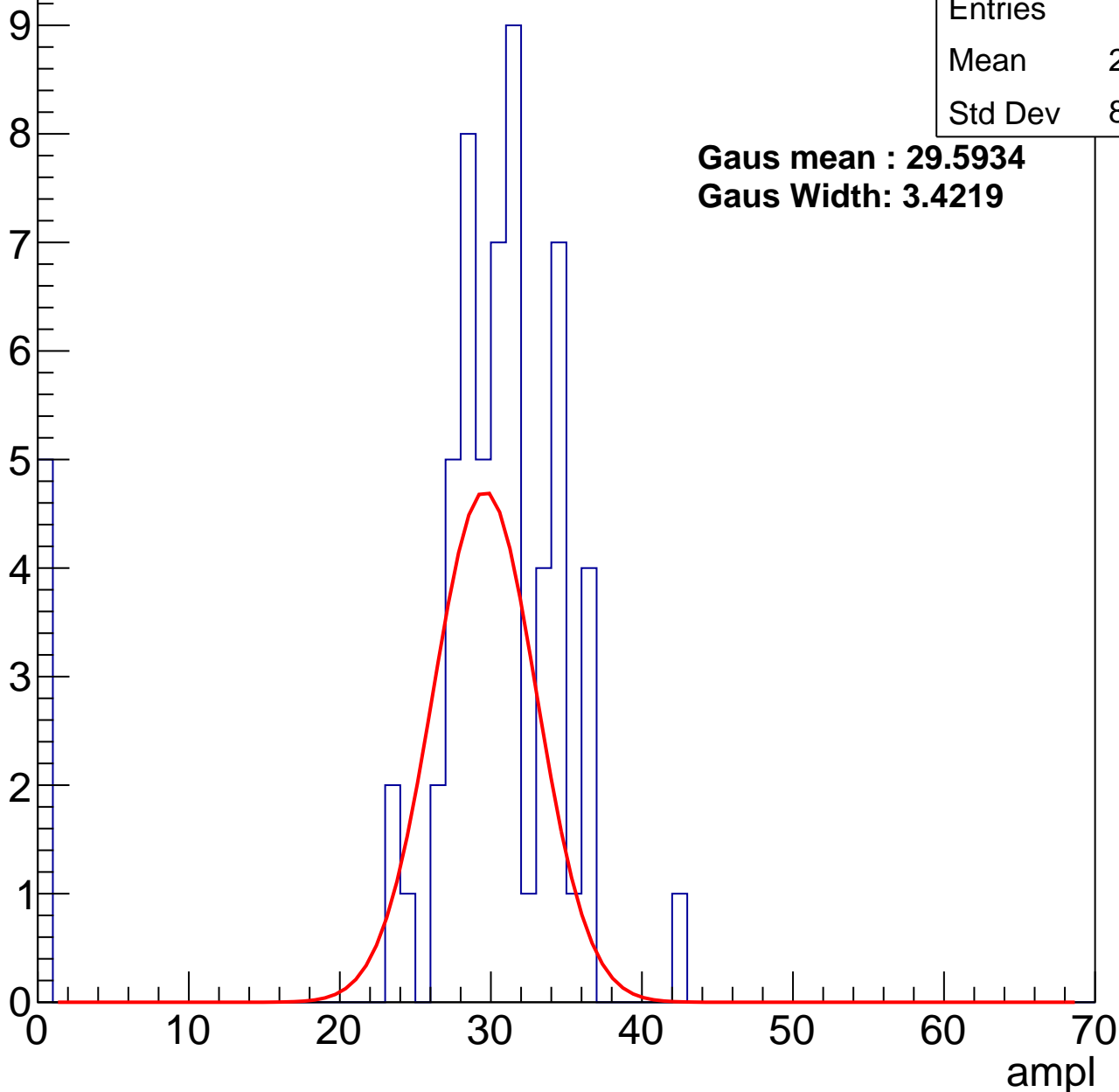
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.03
Std Dev	8.964

Gaus mean : 29.5934

Gaus Width: 3.4219



B1L103S, U5-ch81, adc1

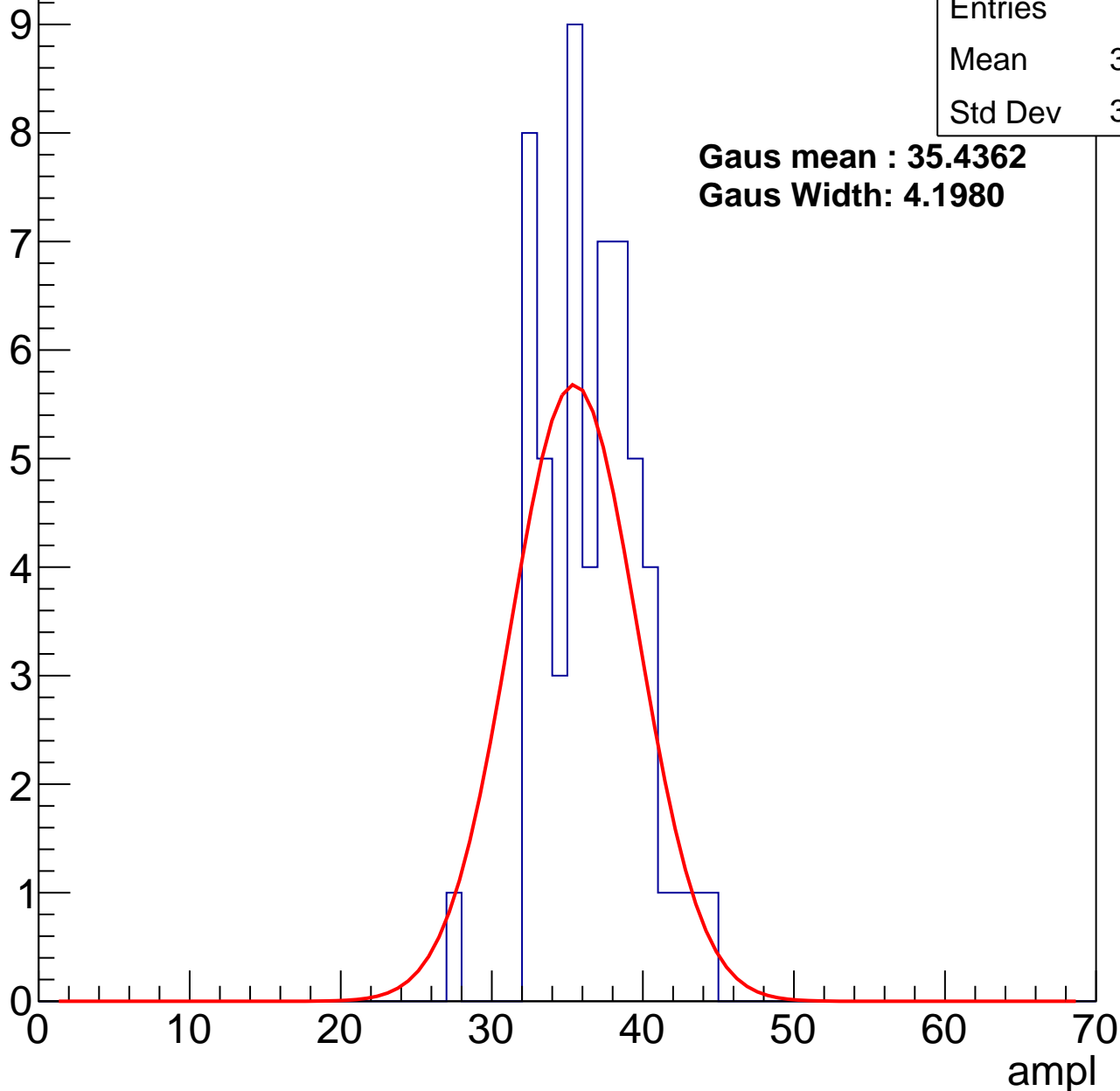
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.12
Std Dev	3.223

Gaus mean : 35.4362

Gaus Width: 4.1980



B1L103S, U5-ch81, adc2

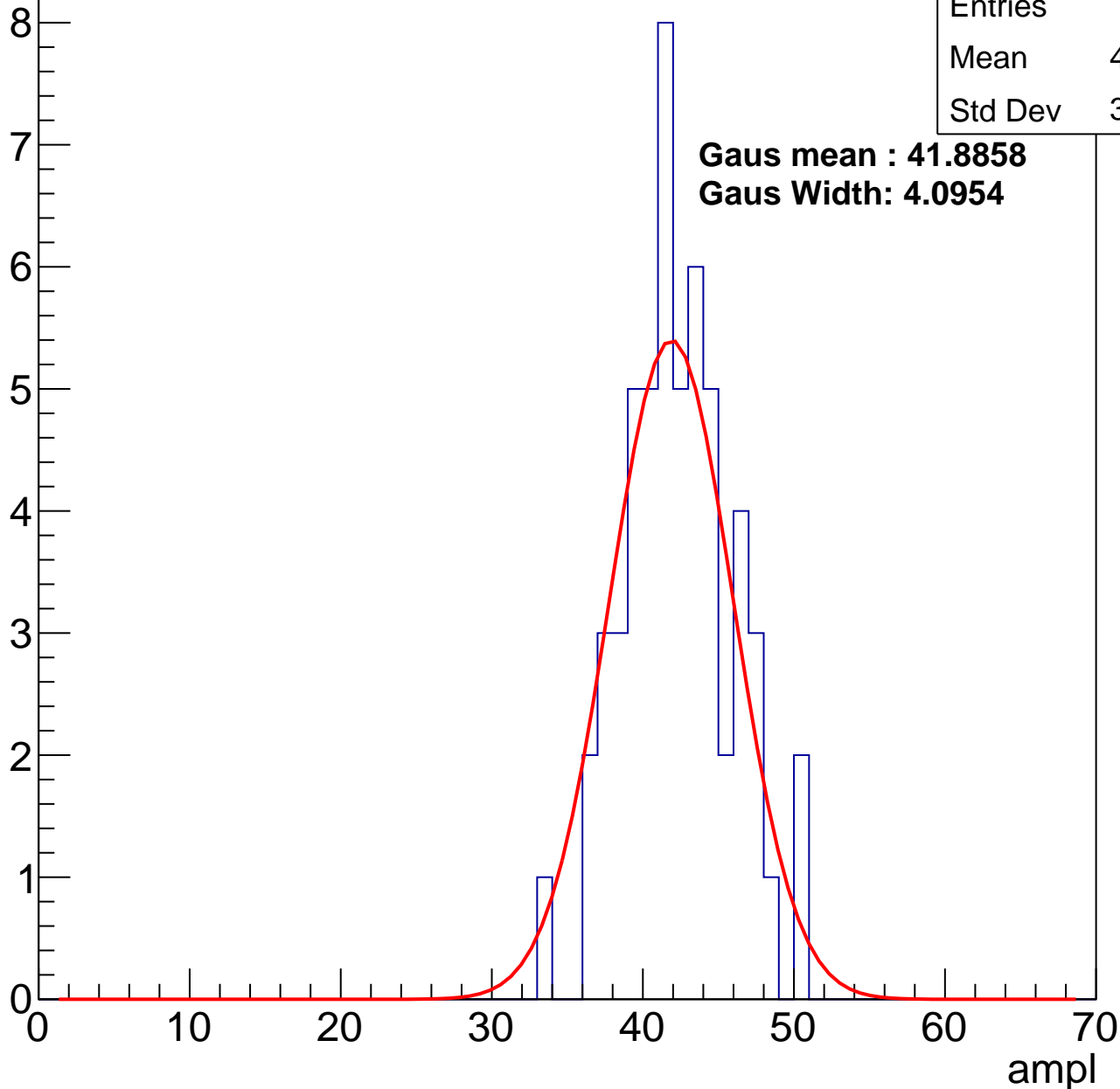
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	41.89
Std Dev	3.556

Gaus mean : 41.8858

Gaus Width: 4.0954

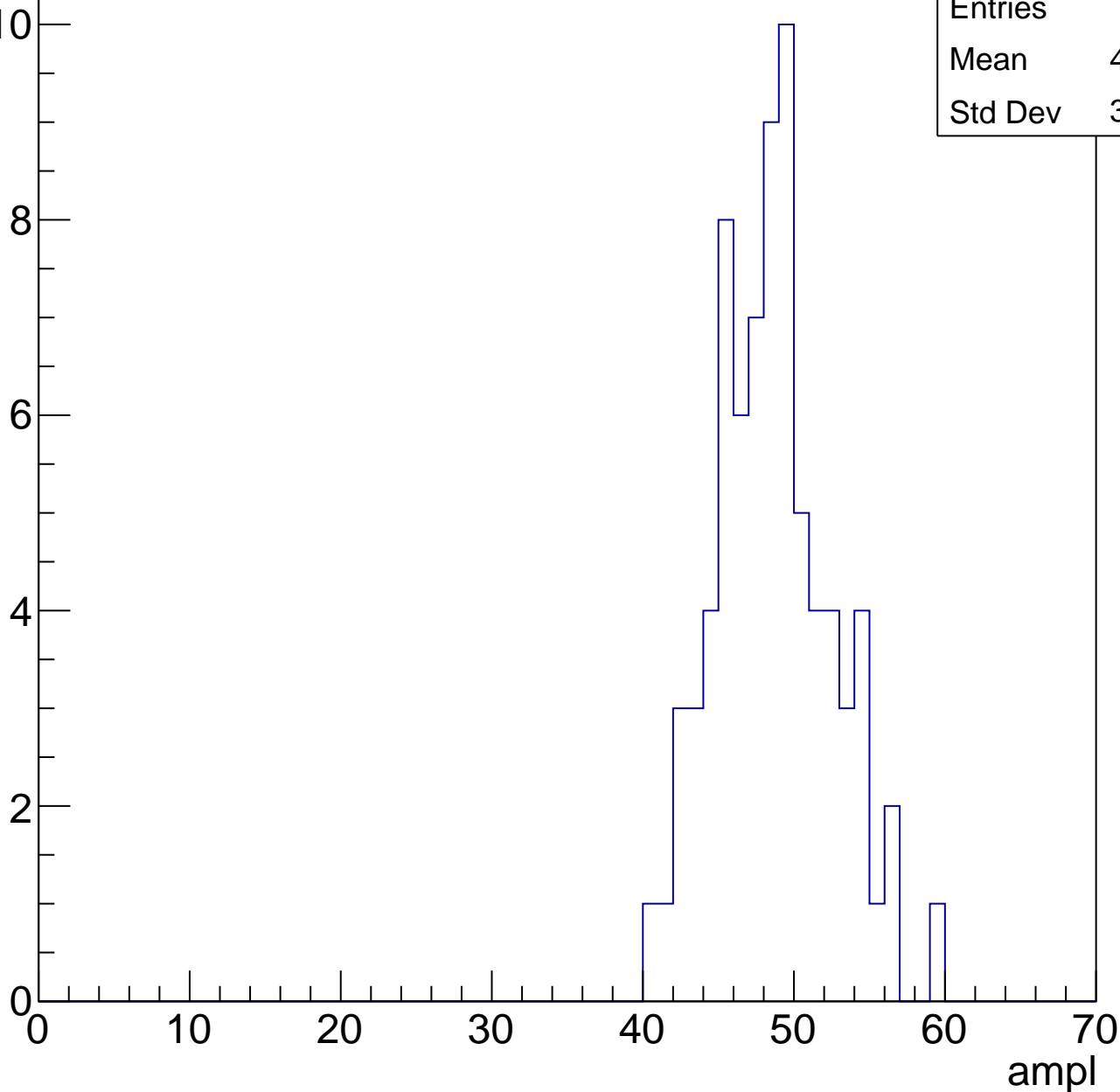


B1L103S, U5-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	48.18
Std Dev	3.817

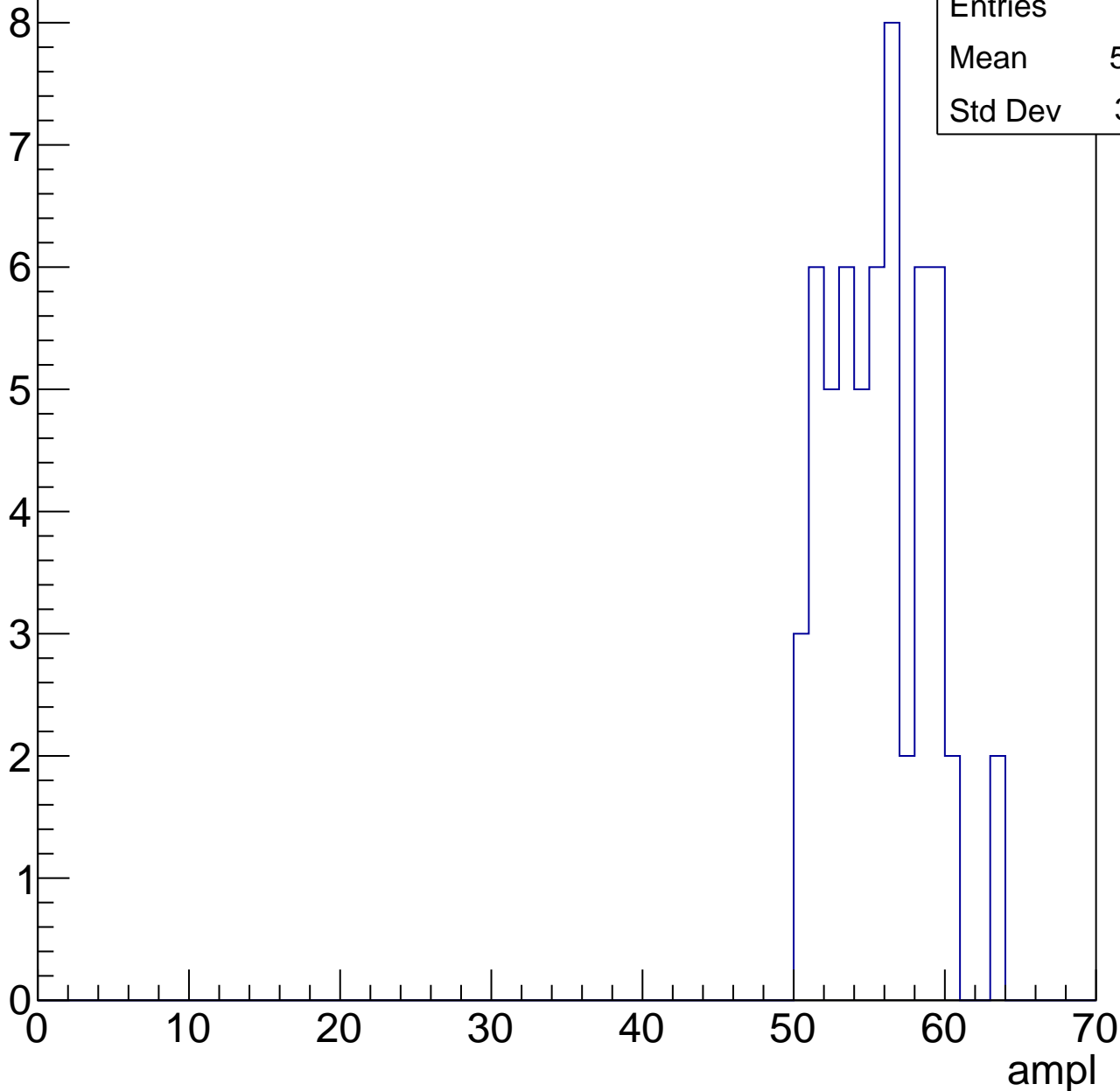


B1L103S, U5-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

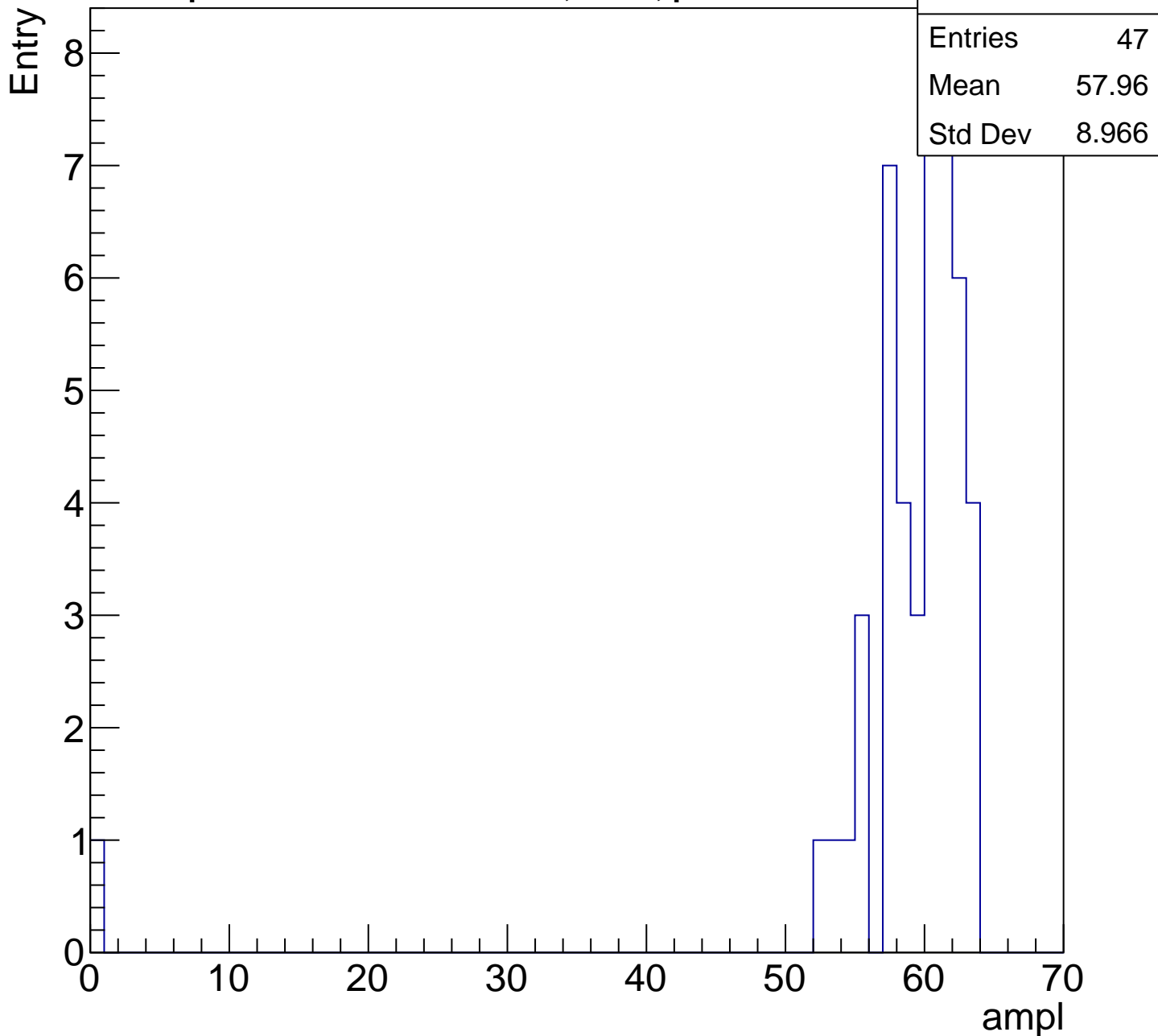
Entry

Entries	57
Mean	55.16
Std Dev	3.211



B1L103S, U5-ch81, adc5

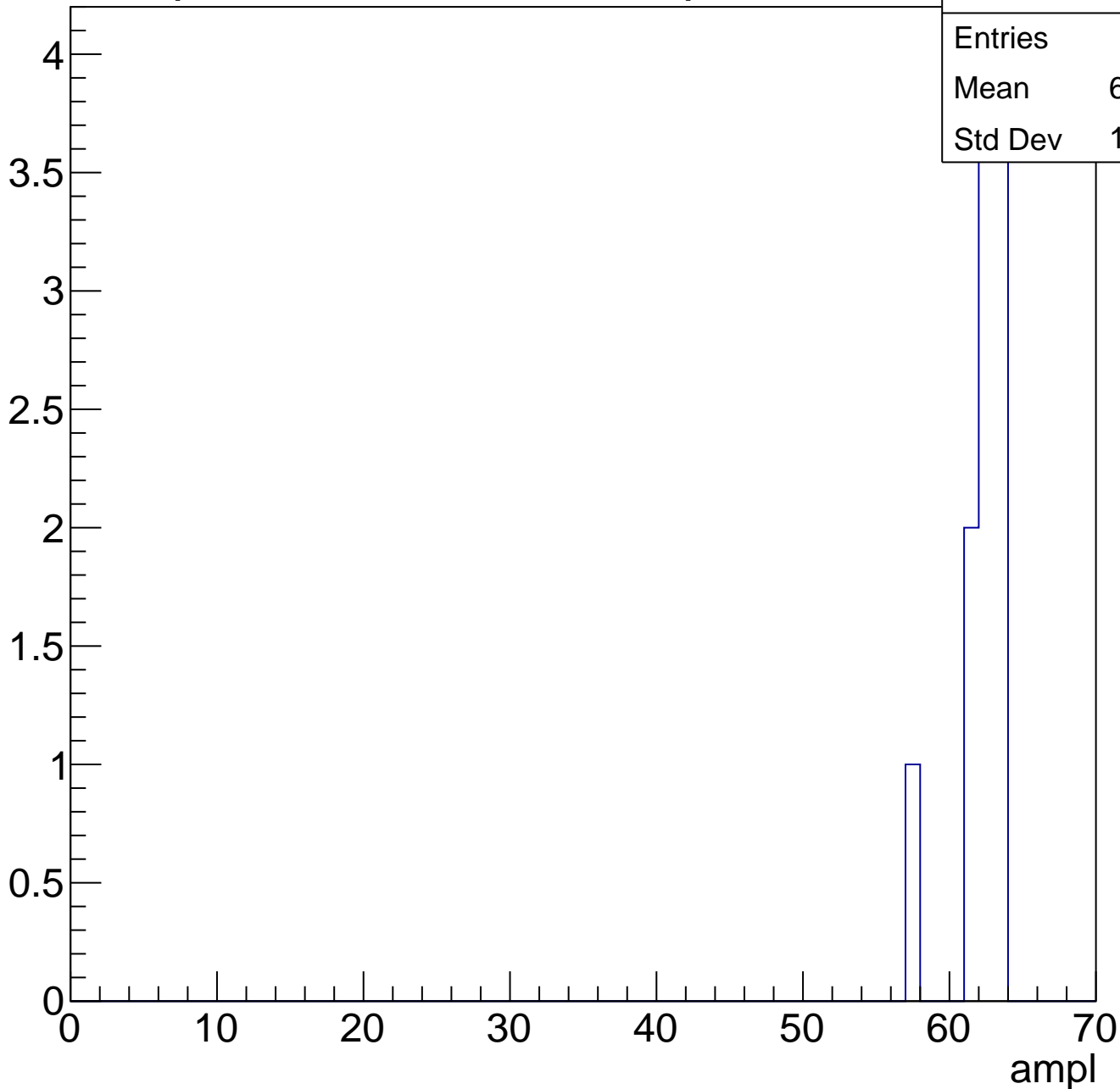
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch82, adc0

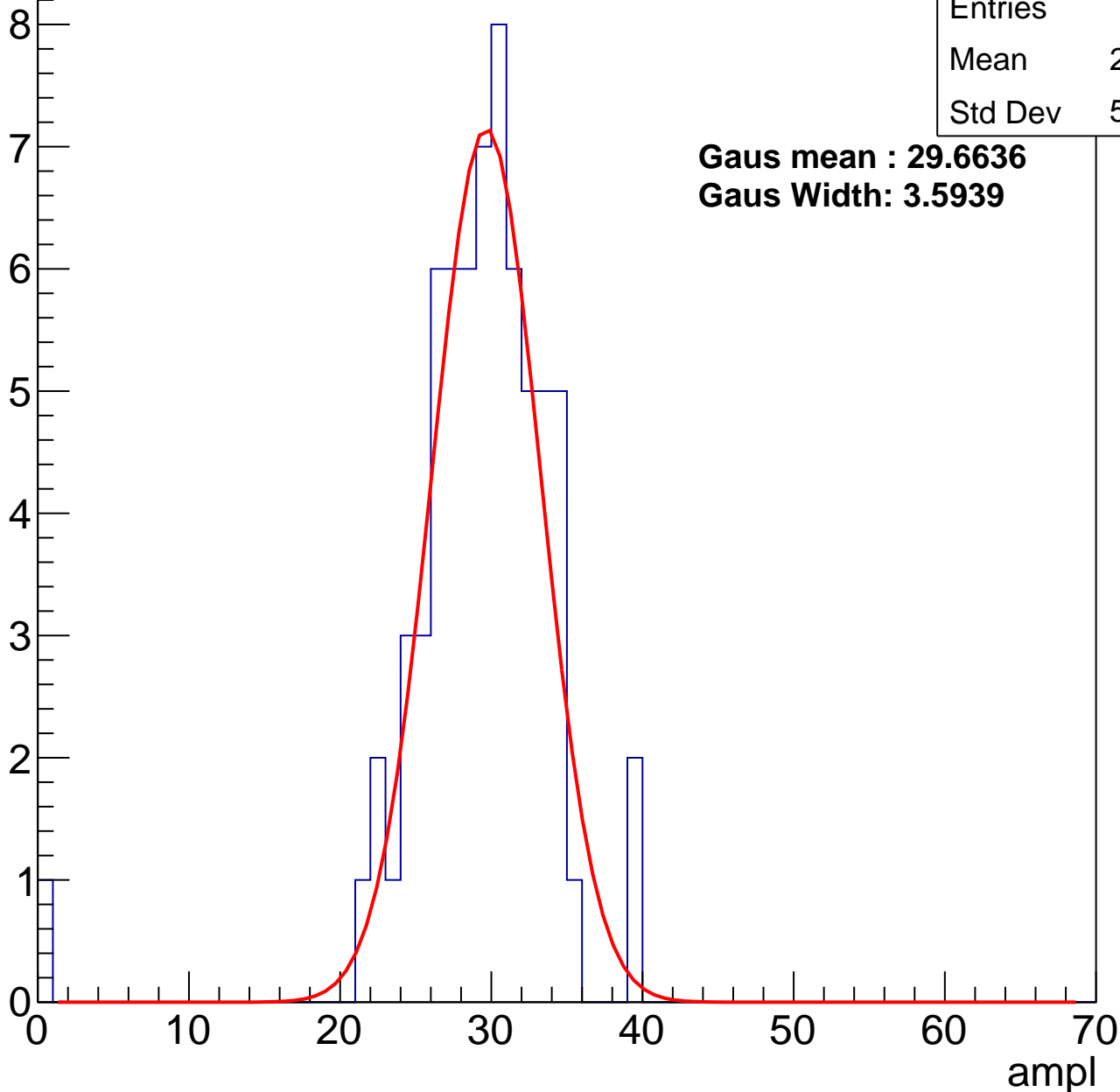
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.79
Std Dev	5.089

Gaus mean : 29.6636

Gaus Width: 3.5939



B1L103S, U5-ch82, adc1

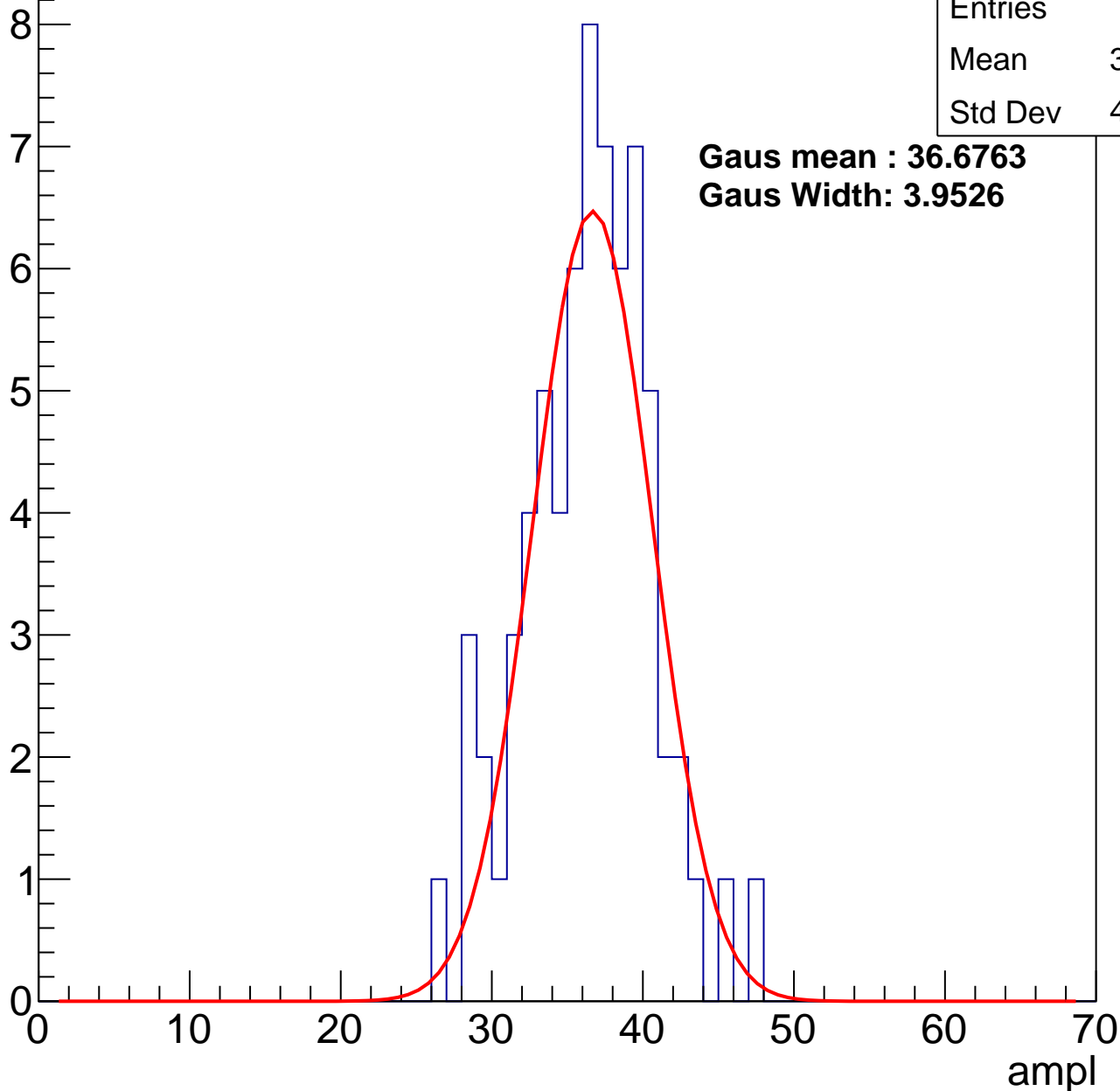
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.93
Std Dev	4.133

Gaus mean : 36.6763

Gaus Width: 3.9526



B1L103S, U5-ch82, adc2

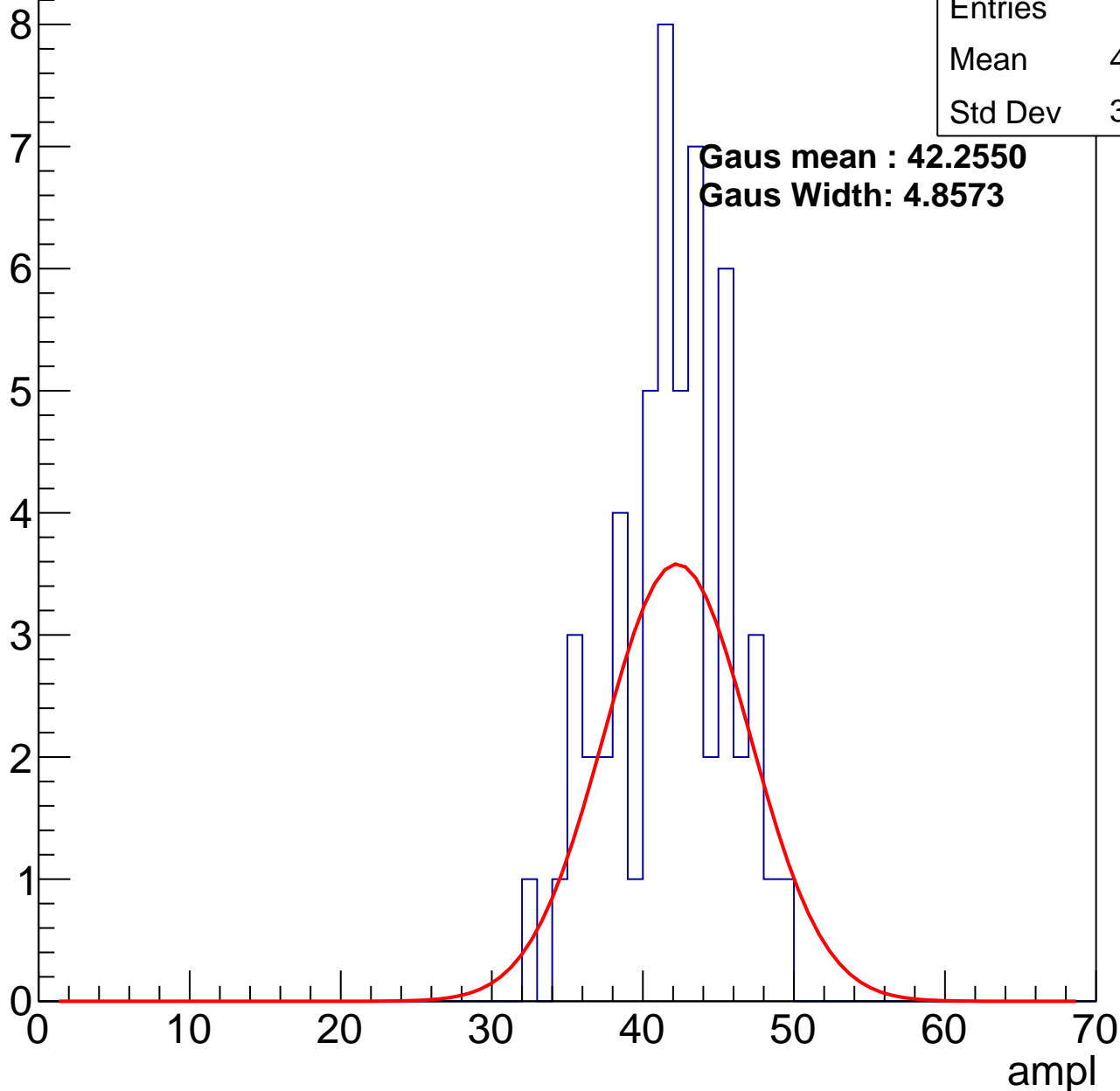
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	41.39
Std Dev	3.788

Gaus mean : 42.2550

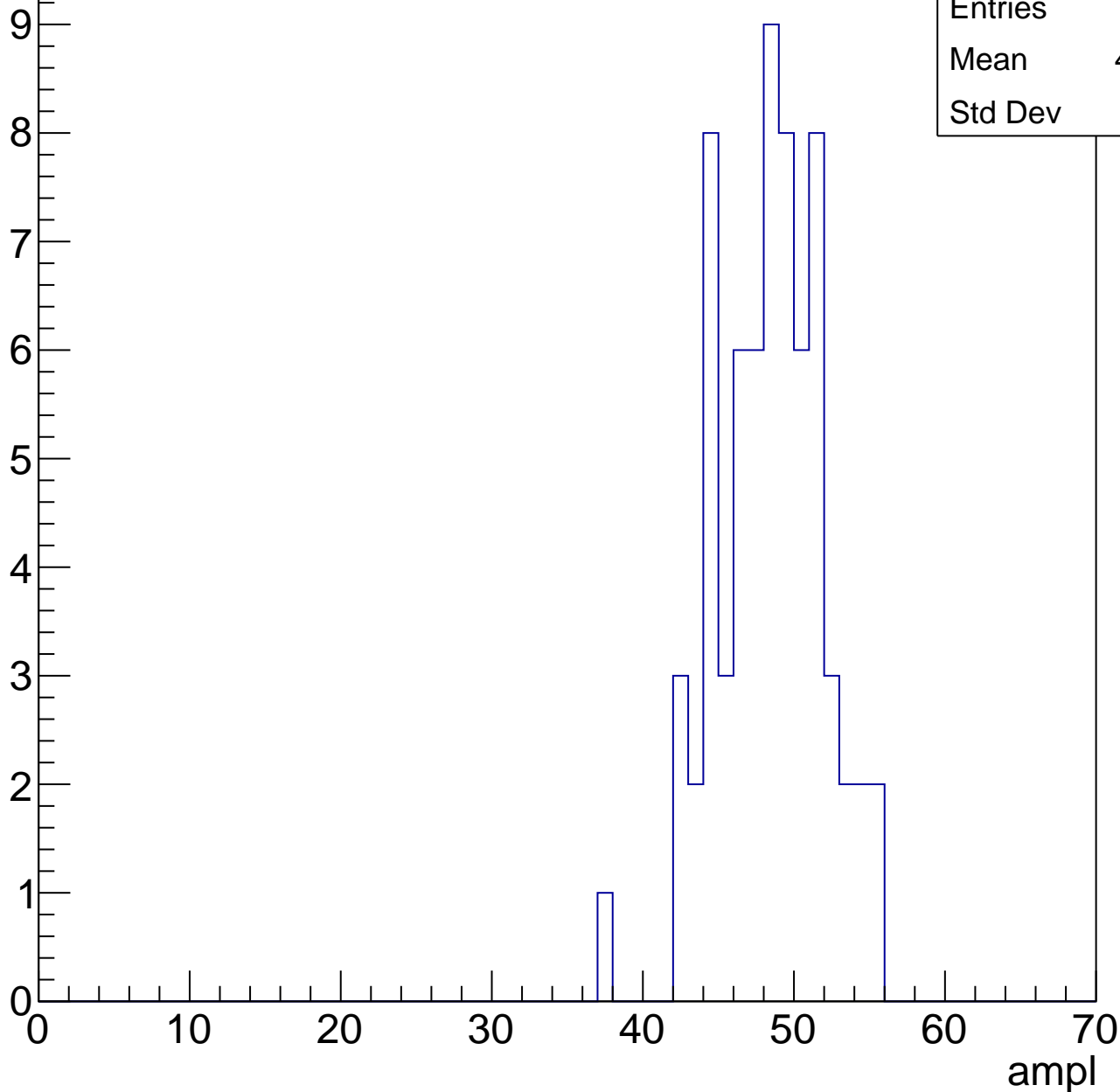
Gaus Width: 4.8573



B1L103S, U5-ch82, adc3

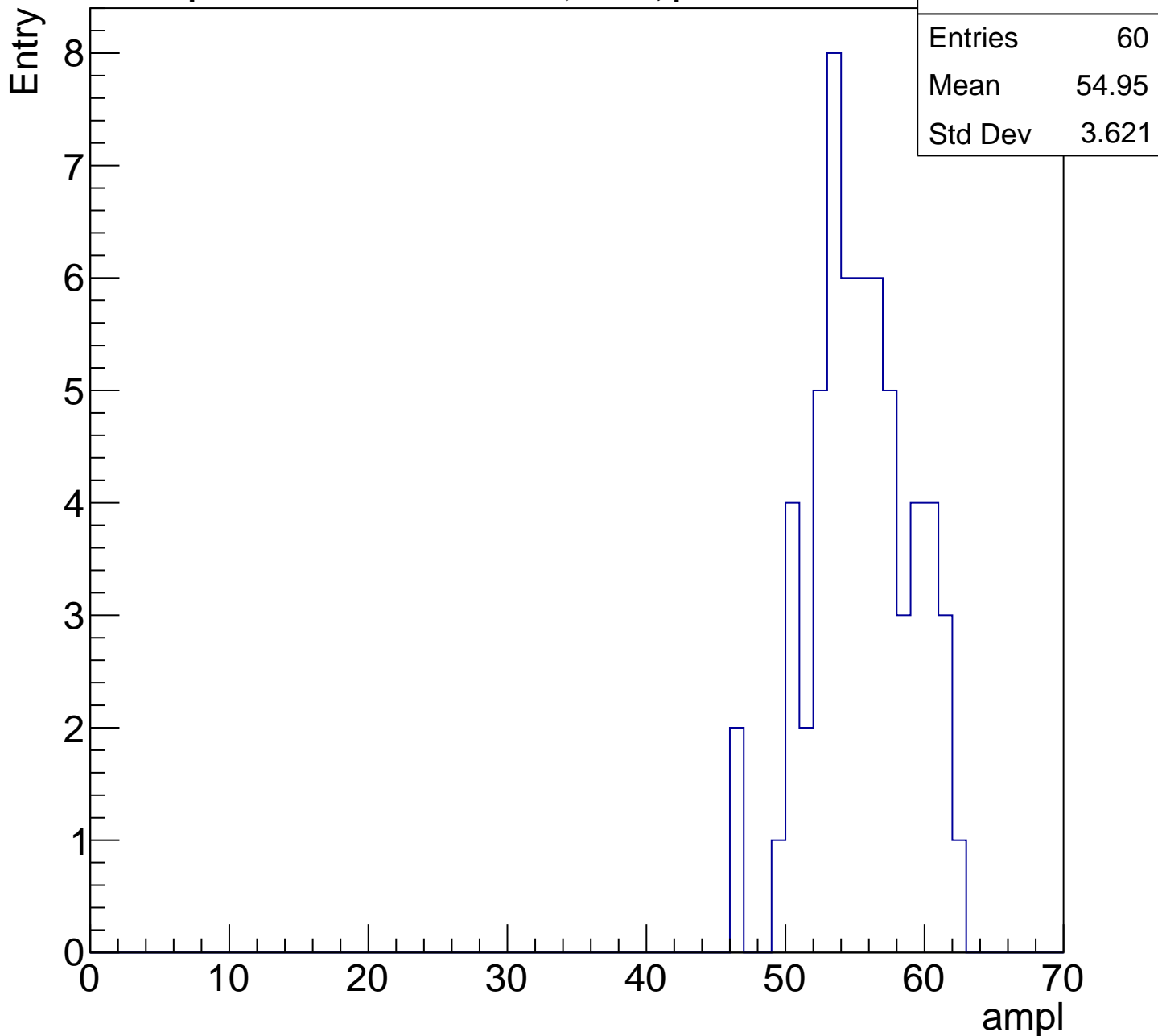
calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

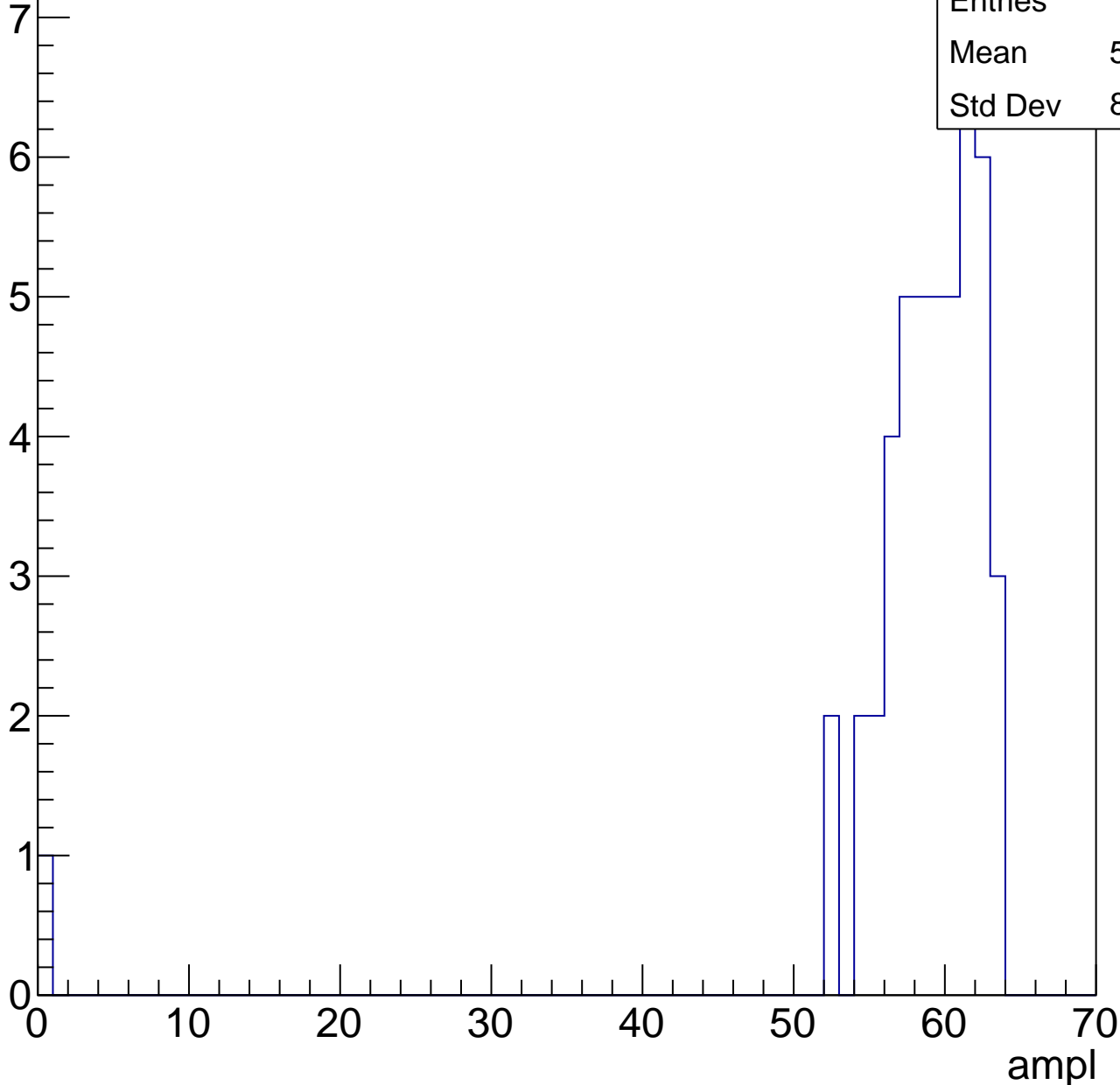


B1L103S, U5-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	57.53
Std Dev	8.939

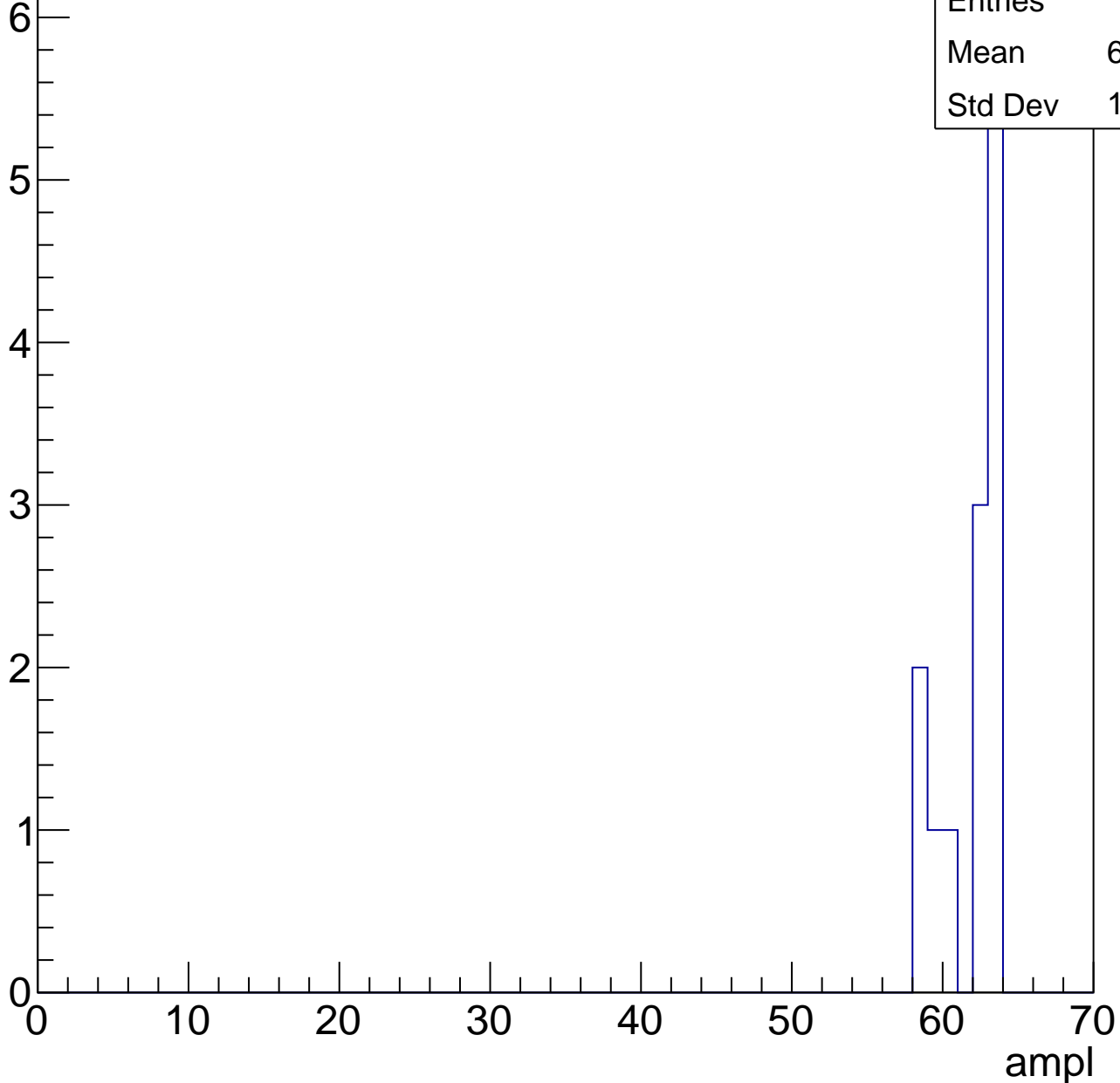


B1L103S, U5-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	61.46
Std Dev	1.906



B1L103S, U5-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch83, adc0

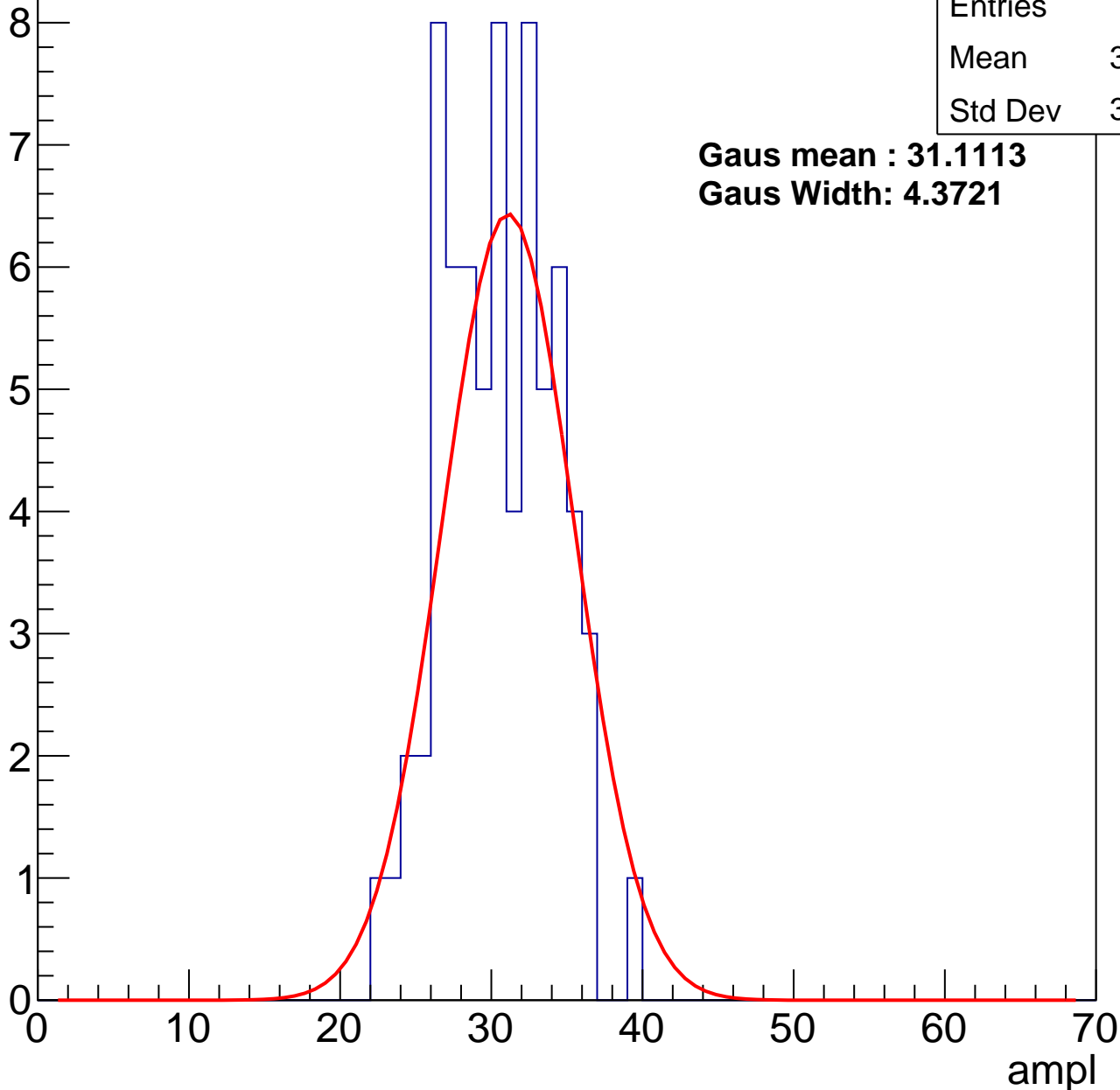
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	30.03
Std Dev	3.609

Gaus mean : 31.1113

Gaus Width: 4.3721



B1L103S, U5-ch83, adc1

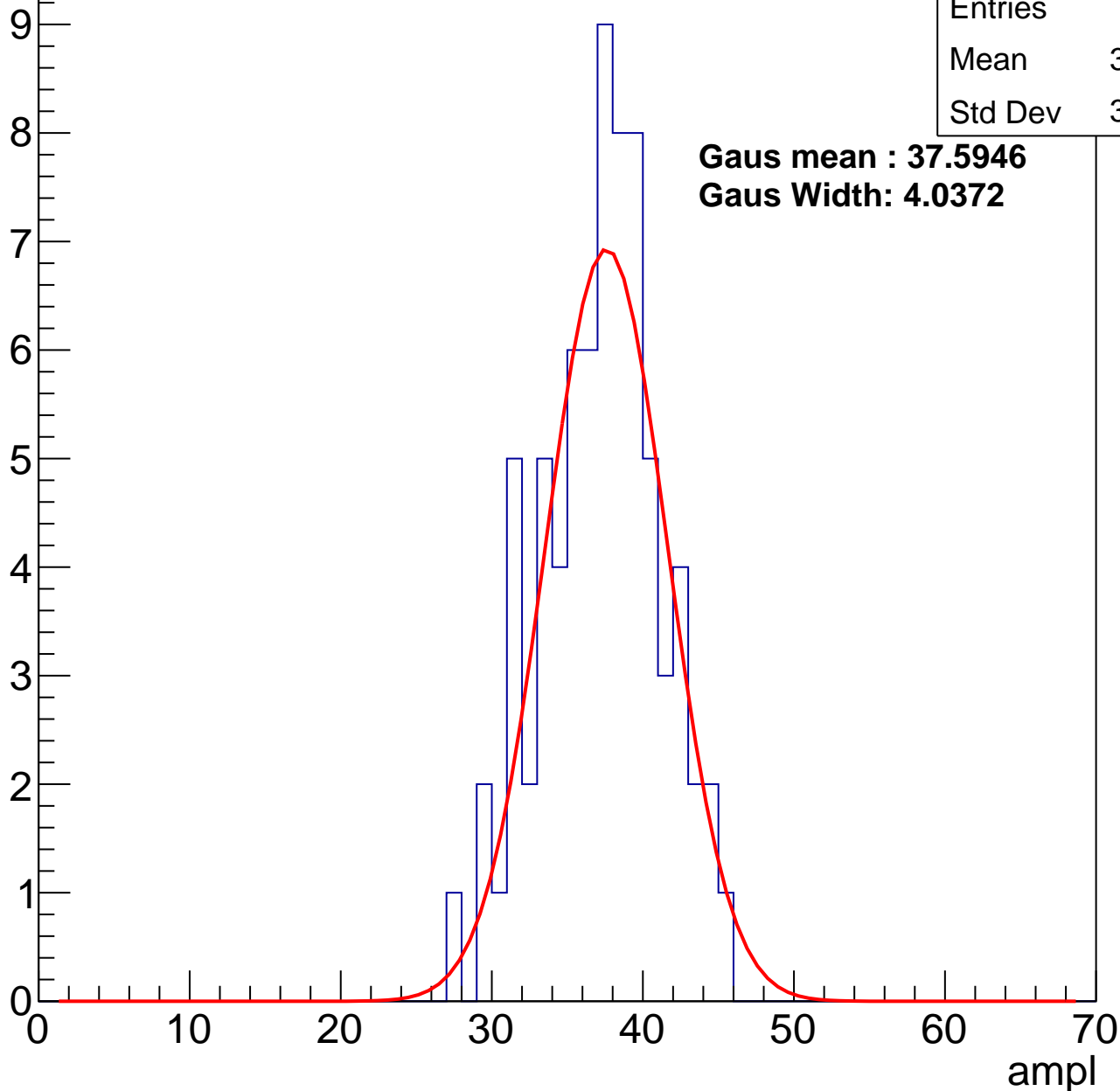
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.76
Std Dev	3.883

Gaus mean : 37.5946

Gaus Width: 4.0372



B1L103S, U5-ch83, adc2

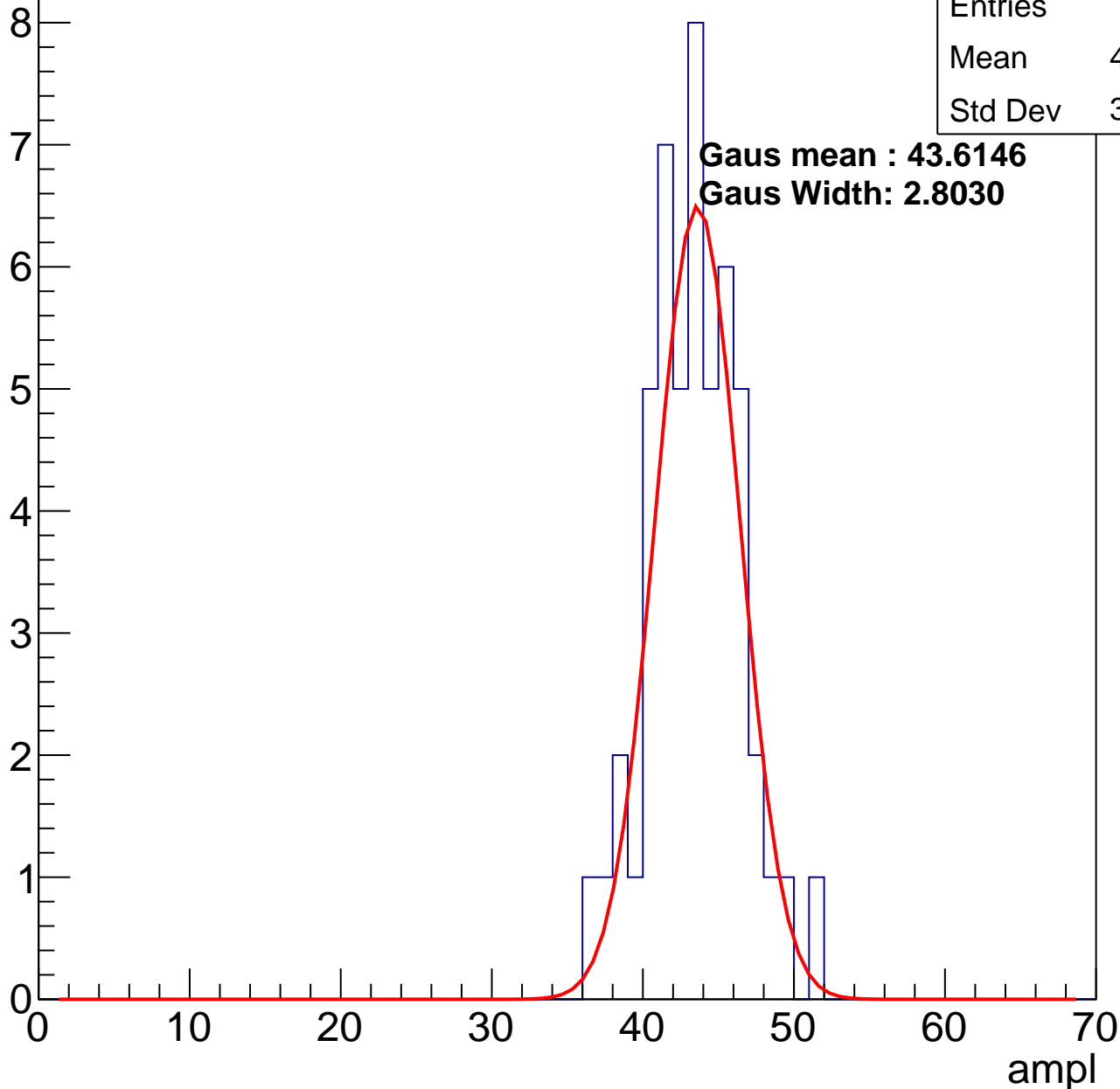
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	42.96
Std Dev	3.003

Gaus mean : 43.6146

Gaus Width: 2.8030

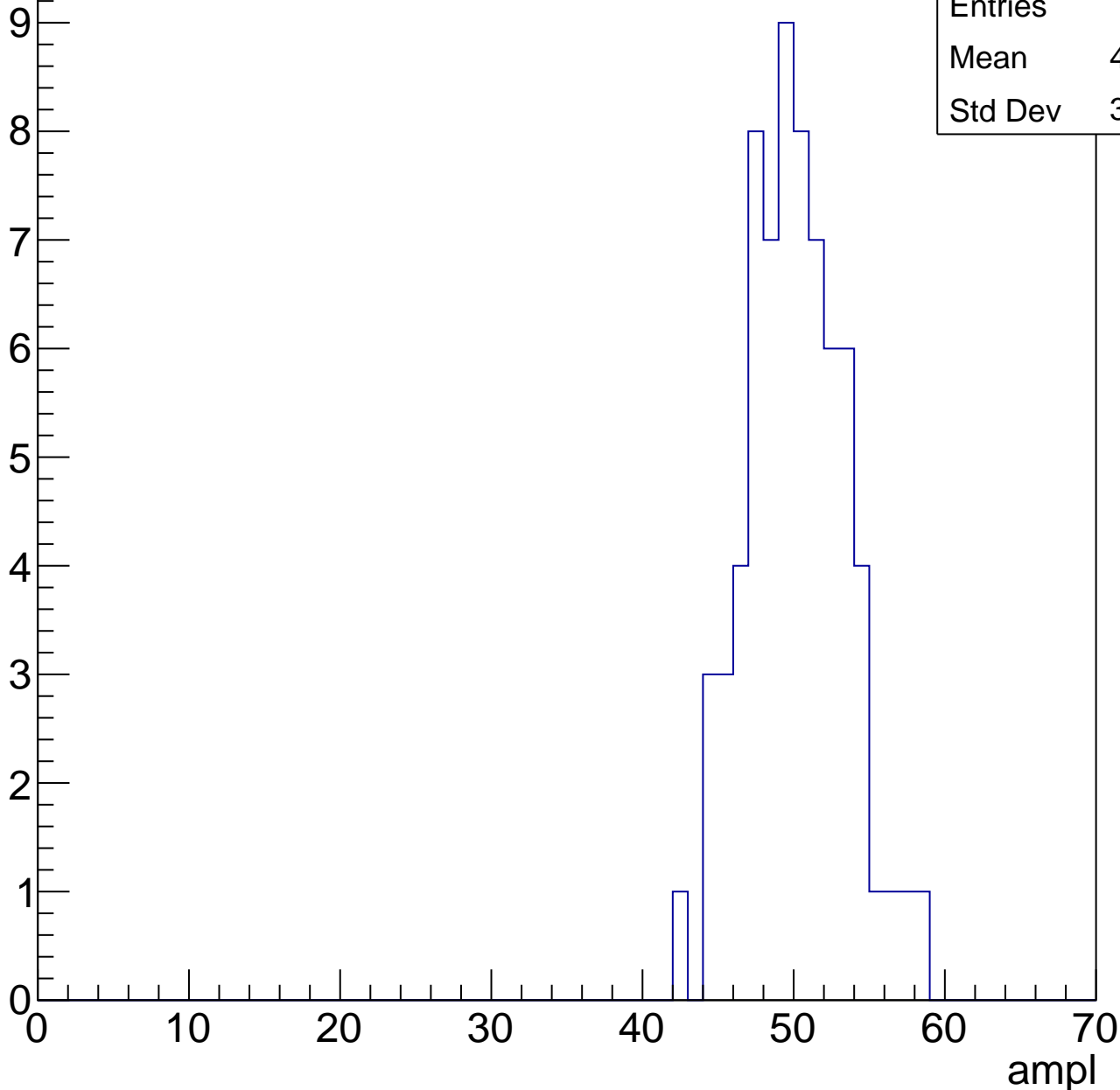


B1L103S, U5-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

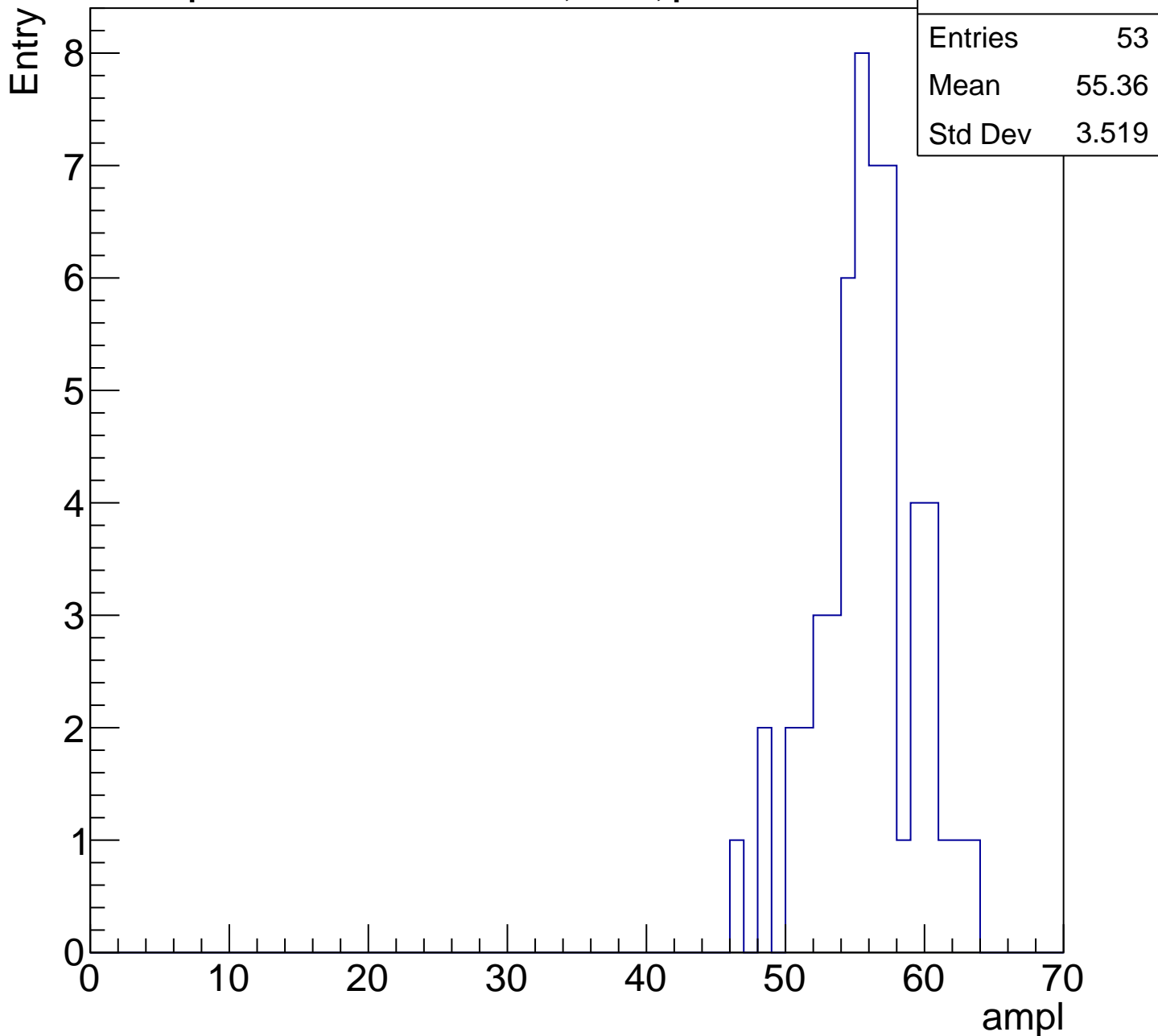
Entry

Entries	70
Mean	49.64
Std Dev	3.238



B1L103S, U5-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

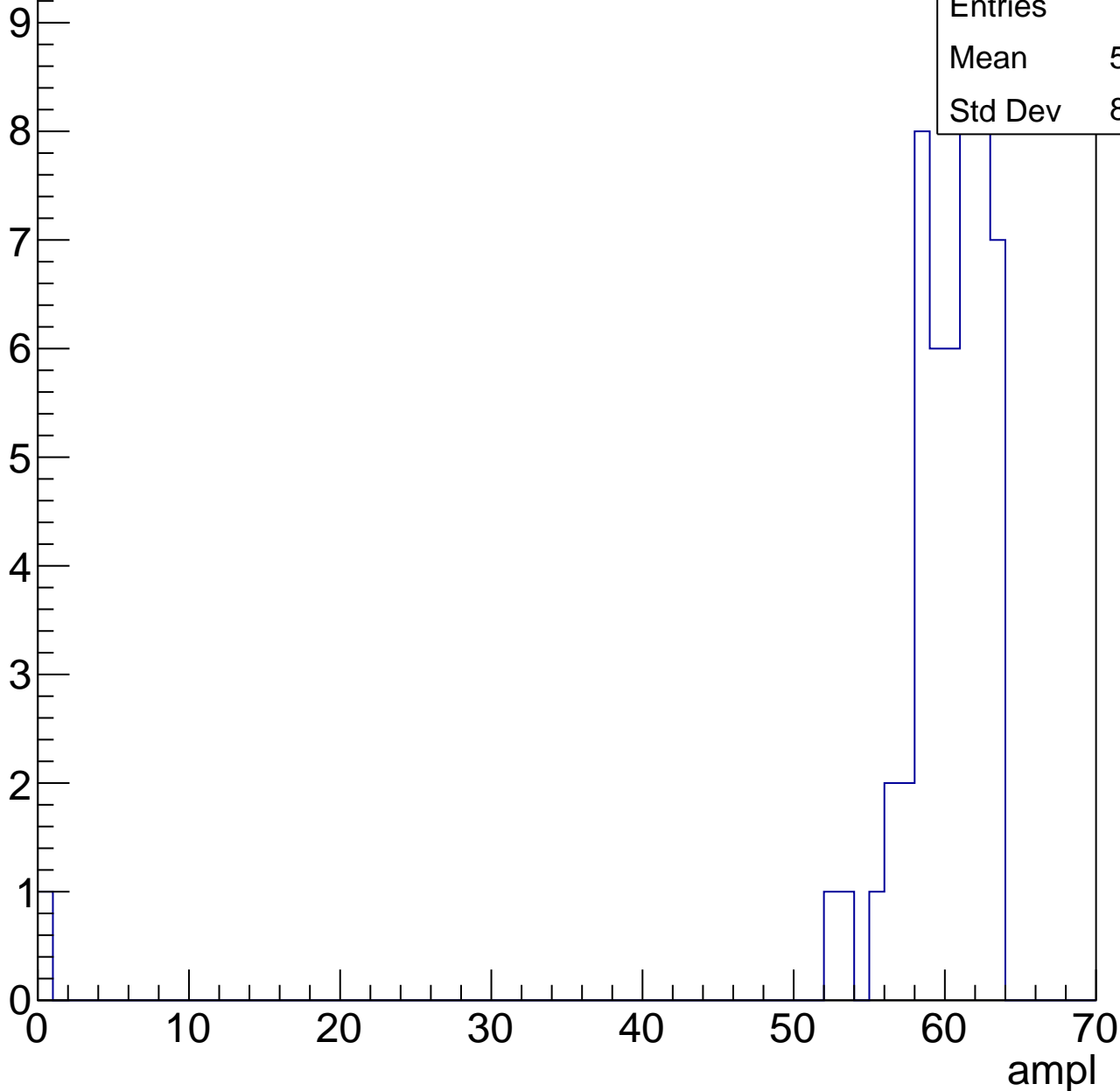


B1L103S, U5-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

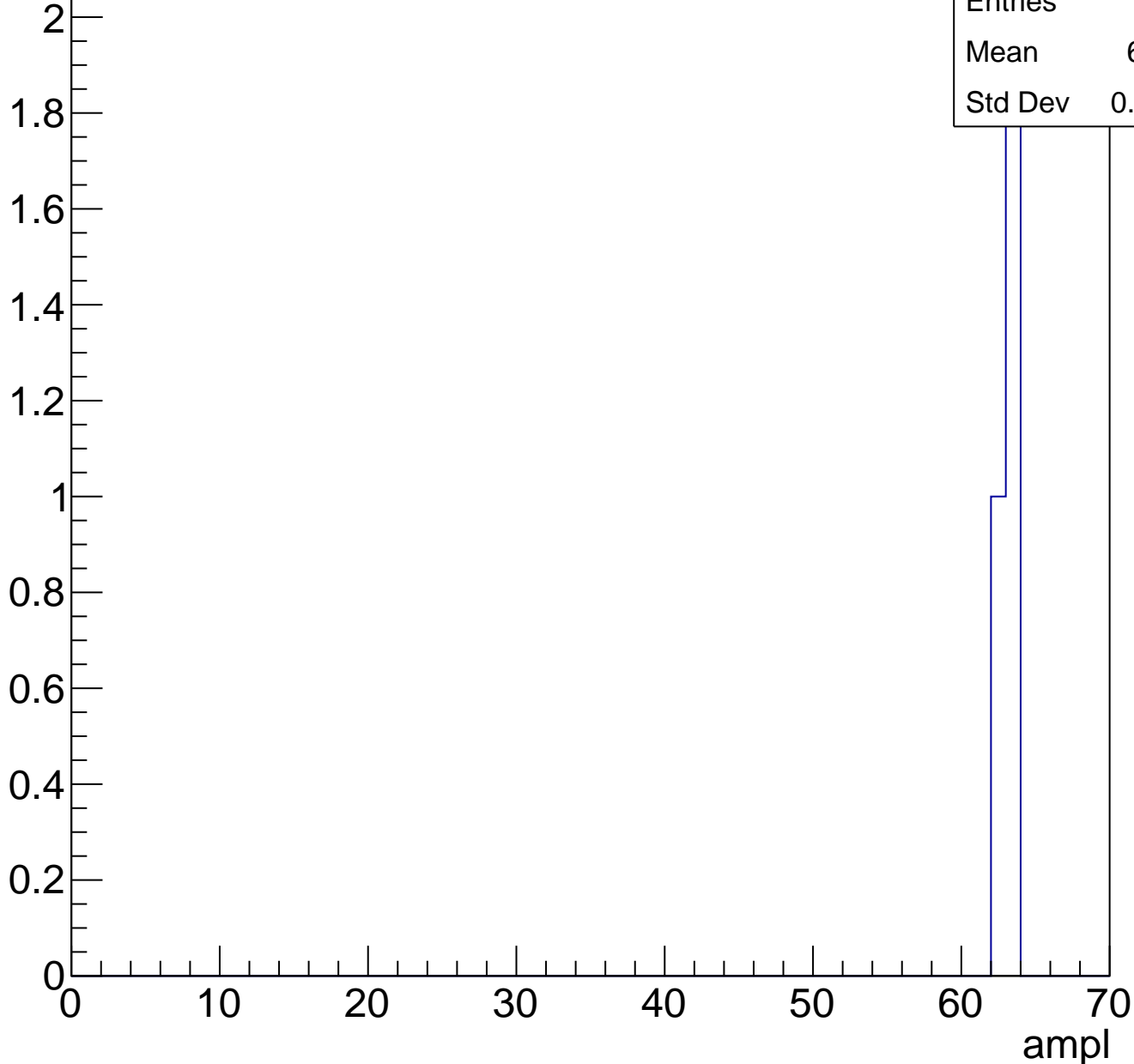
Entries	52
Mean	58.65
Std Dev	8.589



B1L103S, U5-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch84, adc0

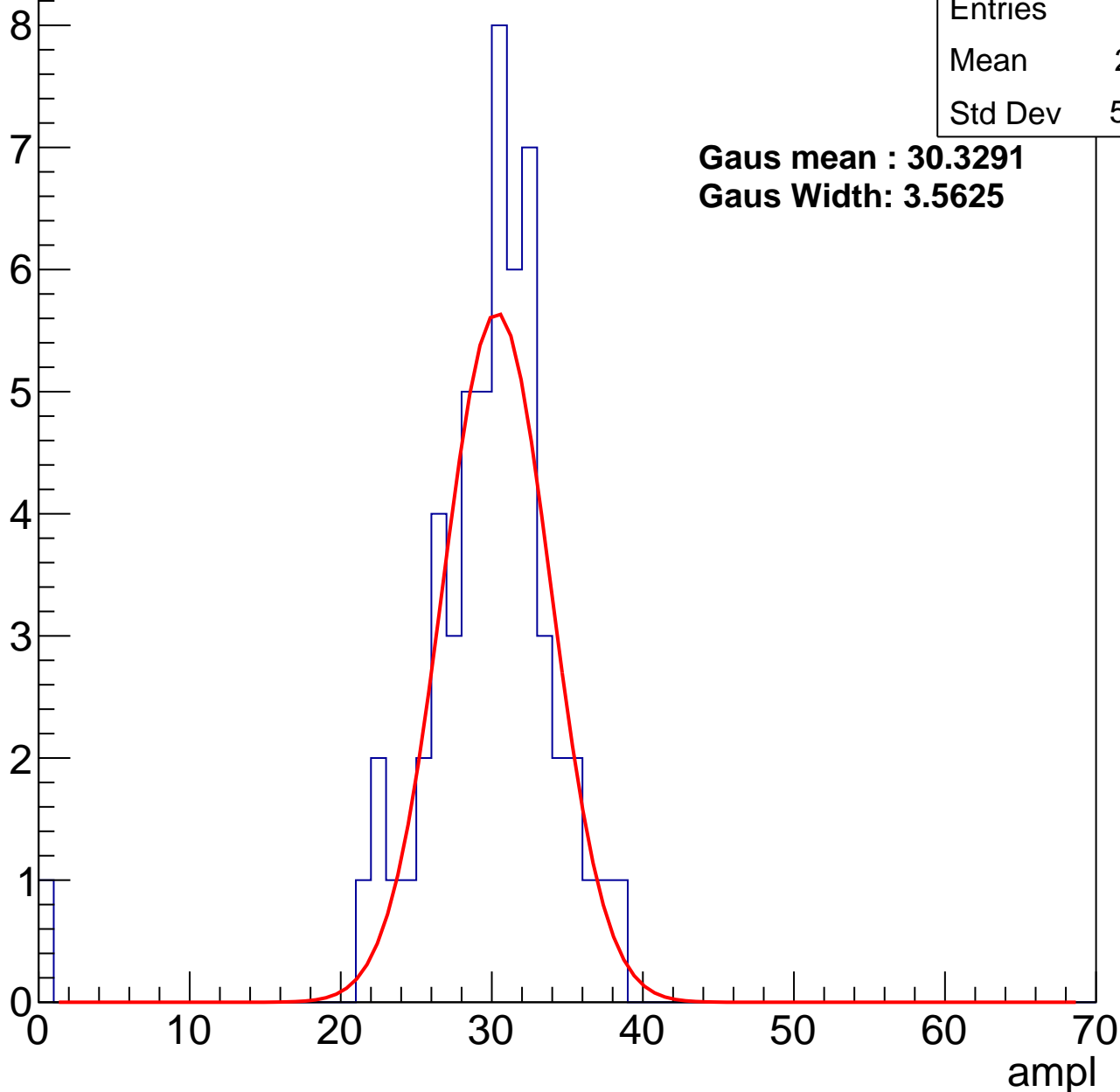
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	29.11
Std Dev	5.357

Gaus mean : 30.3291

Gaus Width: 3.5625



B1L103S, U5-ch84, adc1

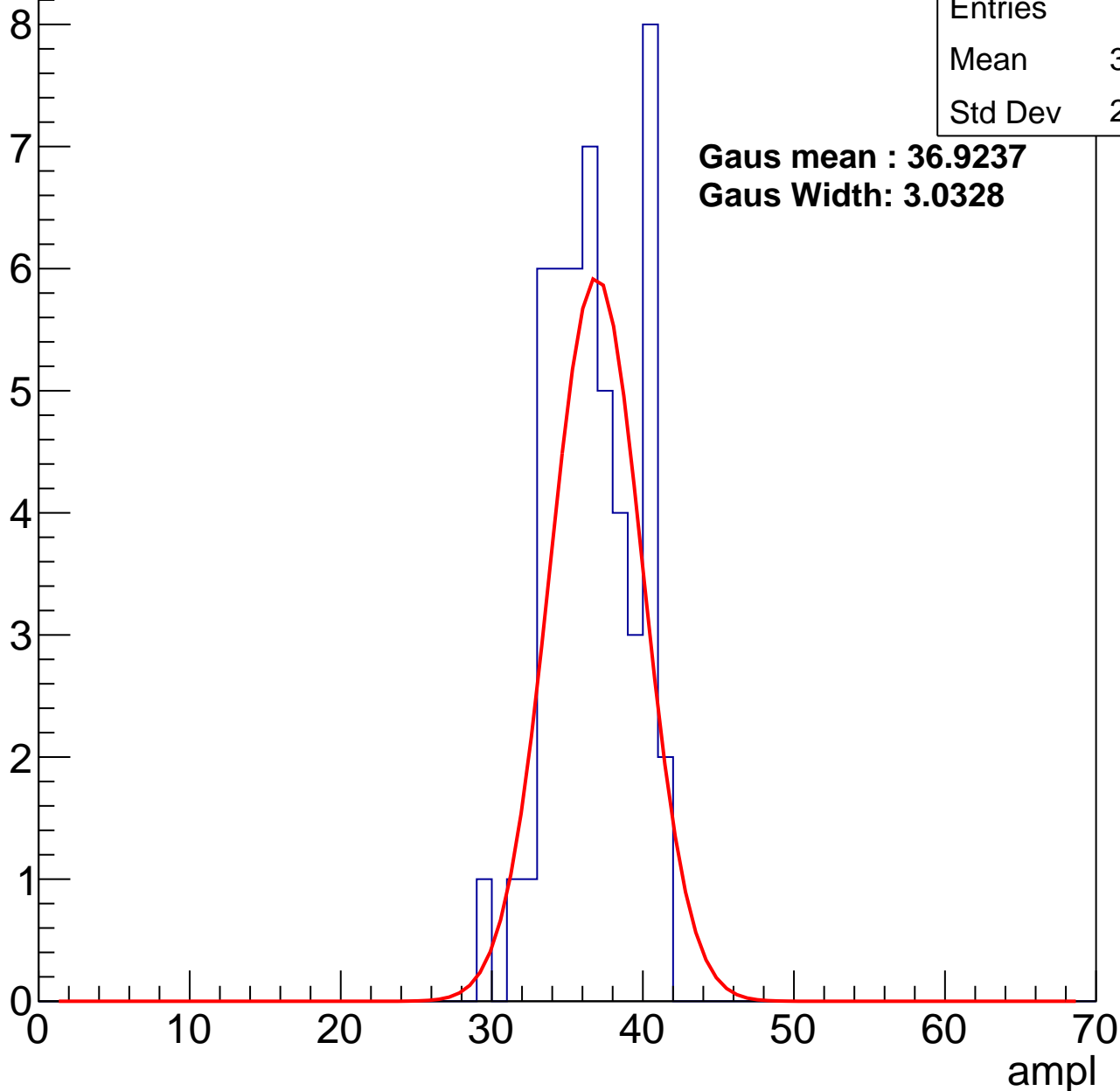
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	36.24
Std Dev	2.818

Gaus mean : 36.9237

Gaus Width: 3.0328



B1L103S, U5-ch84, adc2

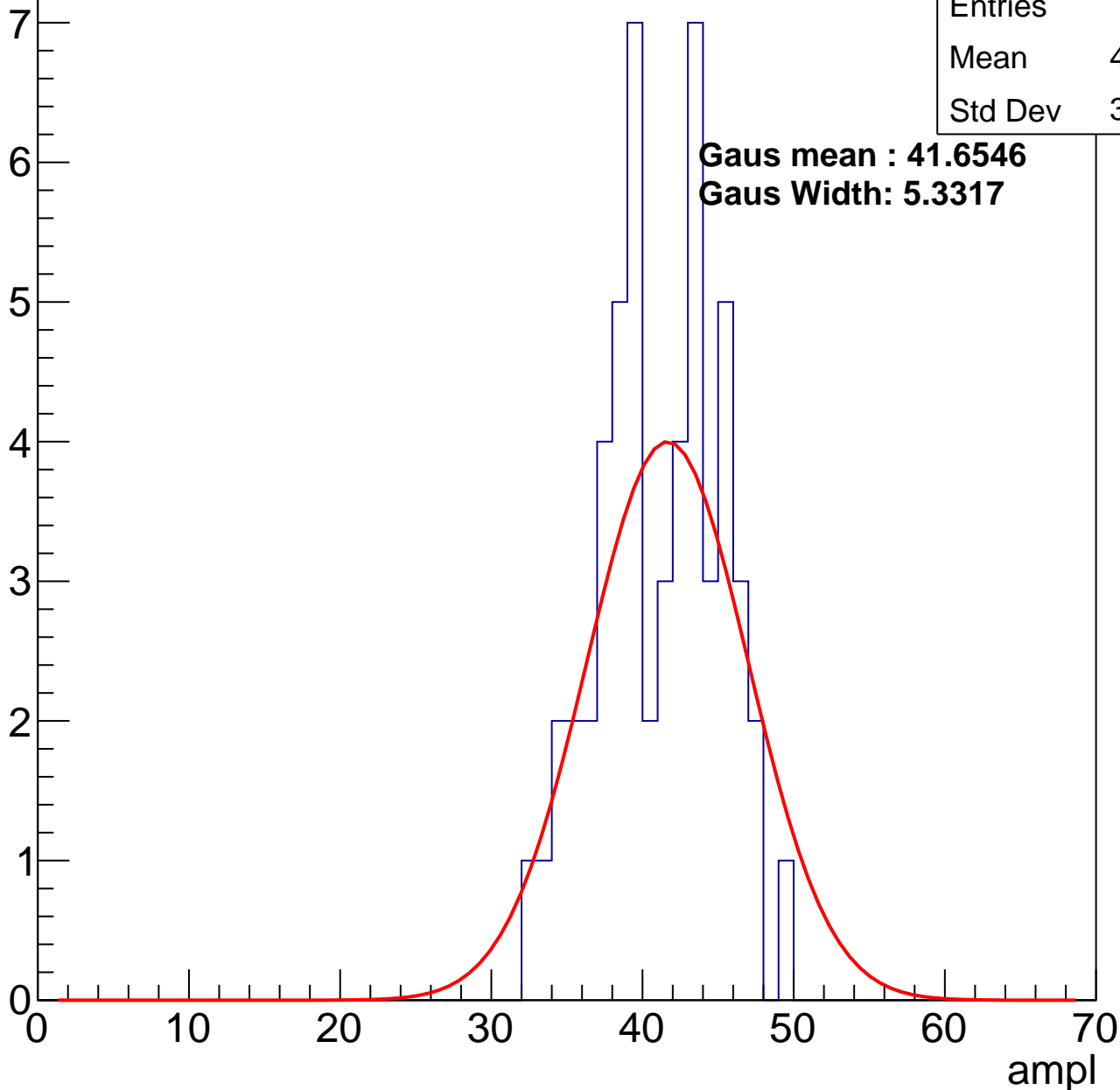
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	40.67
Std Dev	3.963

Gaus mean : 41.6546

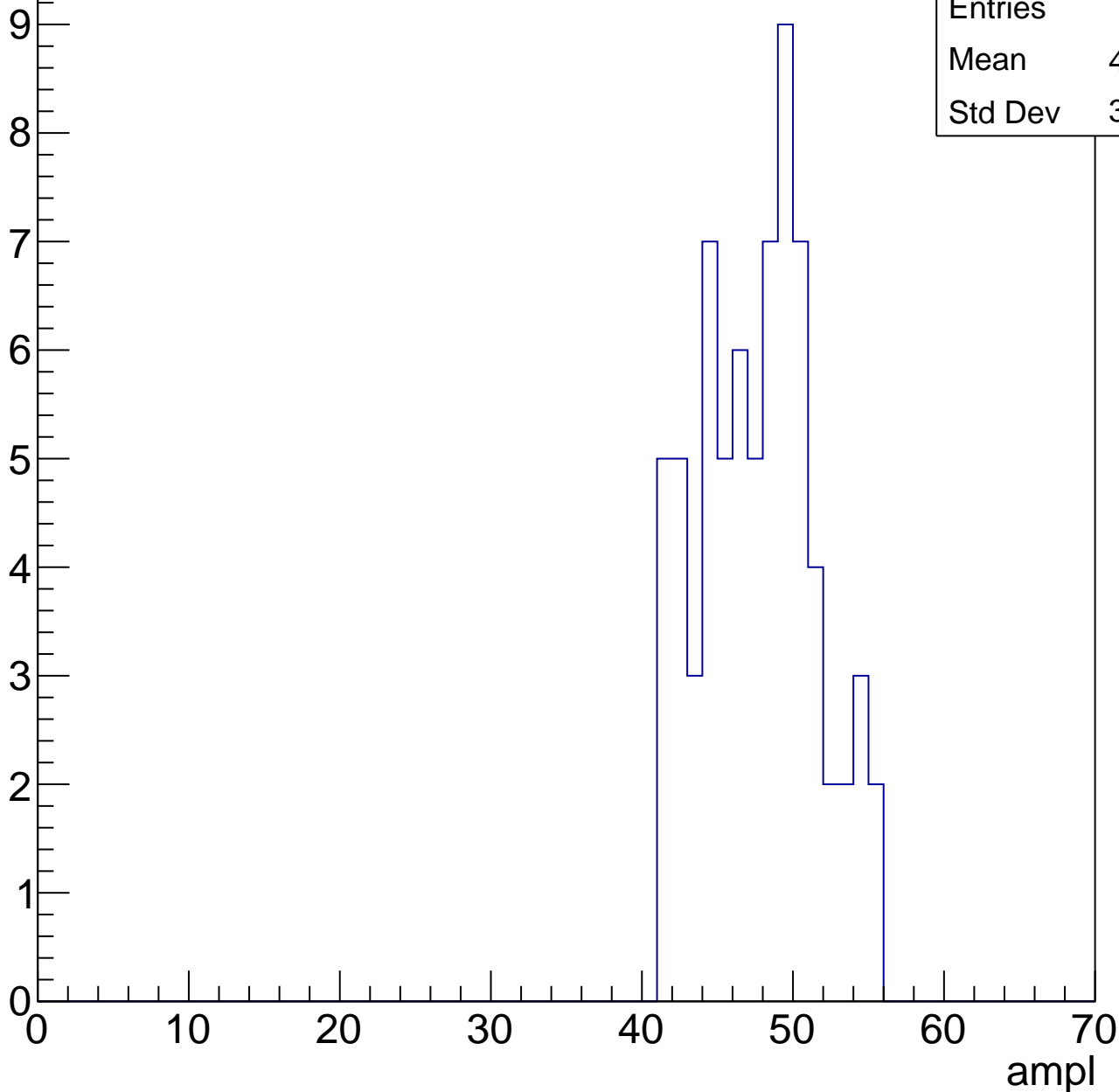
Gaus Width: 5.3317



B1L103S, U5-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

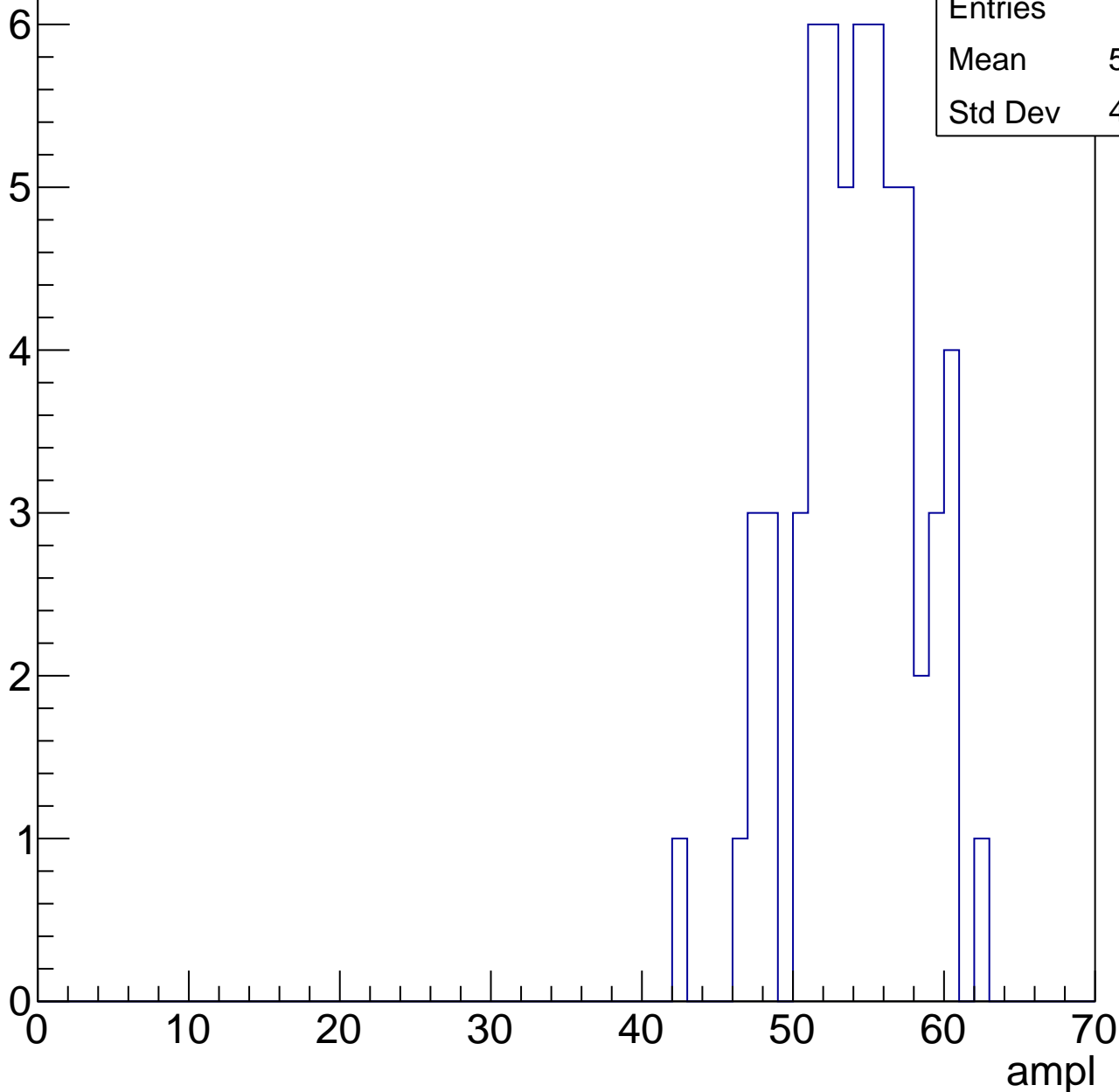


B1L103S, U5-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

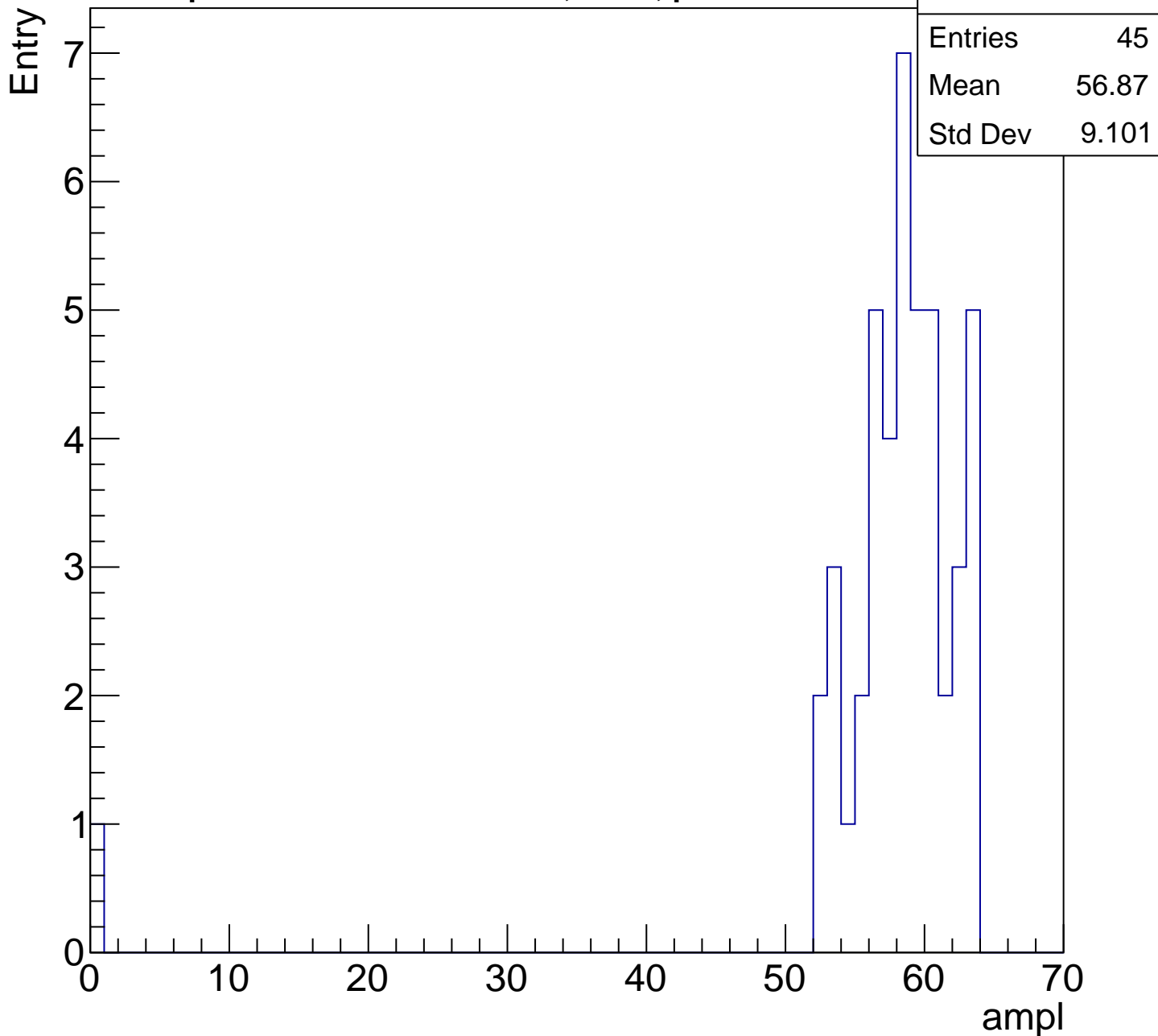
Entry

Entries	60
Mean	53.67
Std Dev	4.028



B1L103S, U5-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

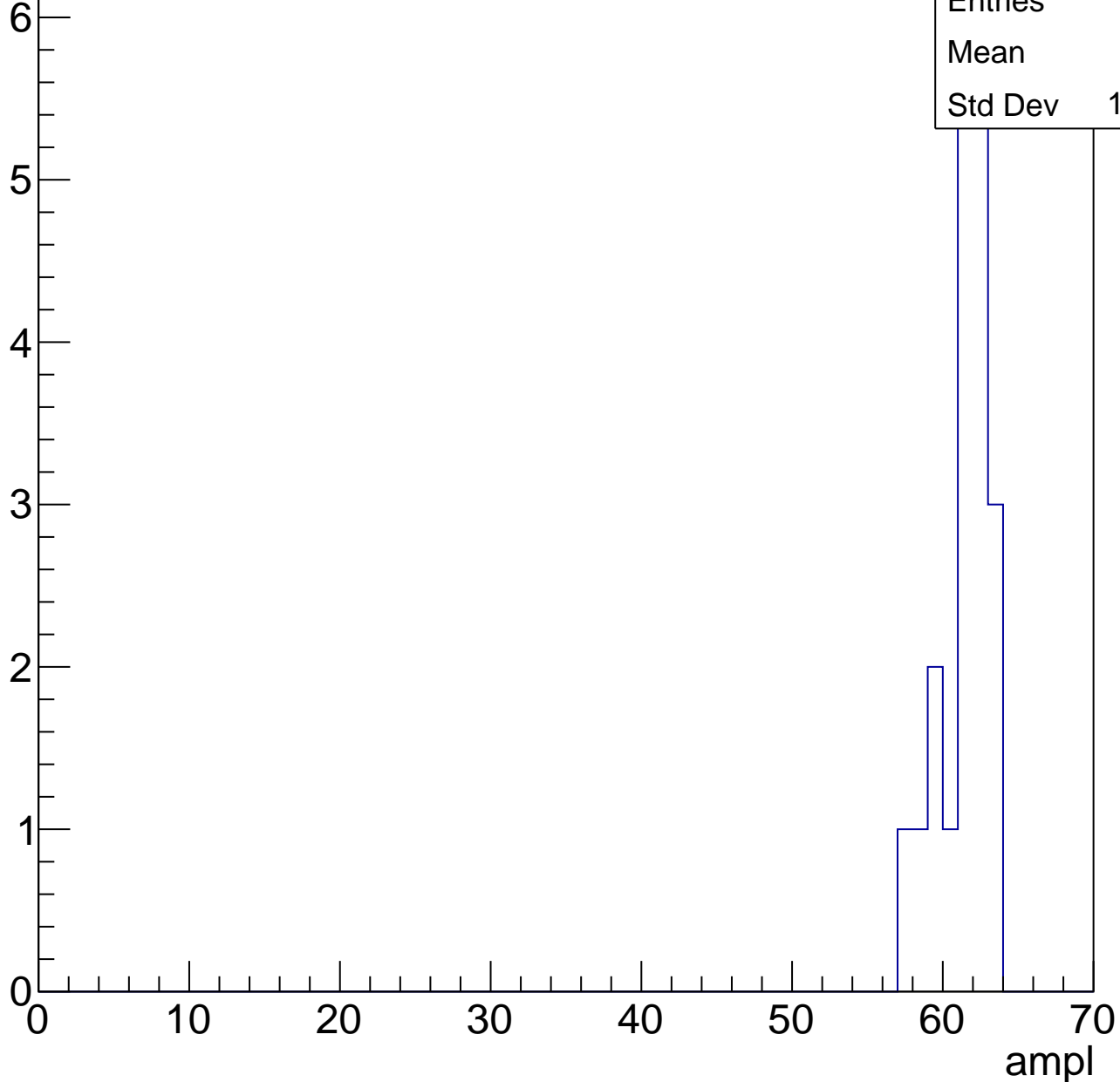


B1L103S, U5-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	20
Mean	61
Std Dev	1.612



B1L103S, U5-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

0 10 20 30 40 50 60 70

ampl

B1L103S, U5-ch85, adc0

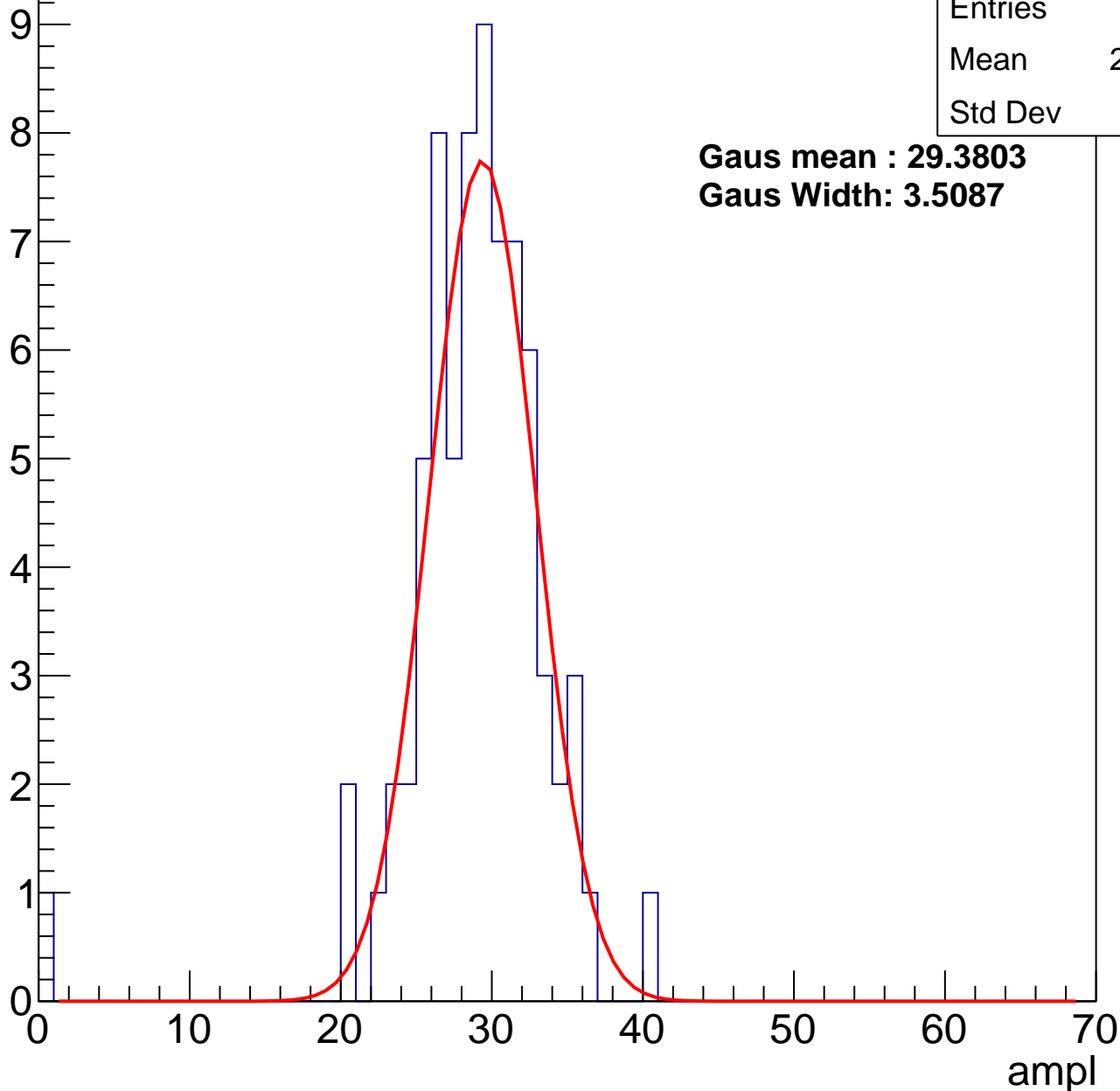
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.44
Std Dev	4.96

Gaus mean : 29.3803

Gaus Width: 3.5087



B1L103S, U5-ch85, adc1

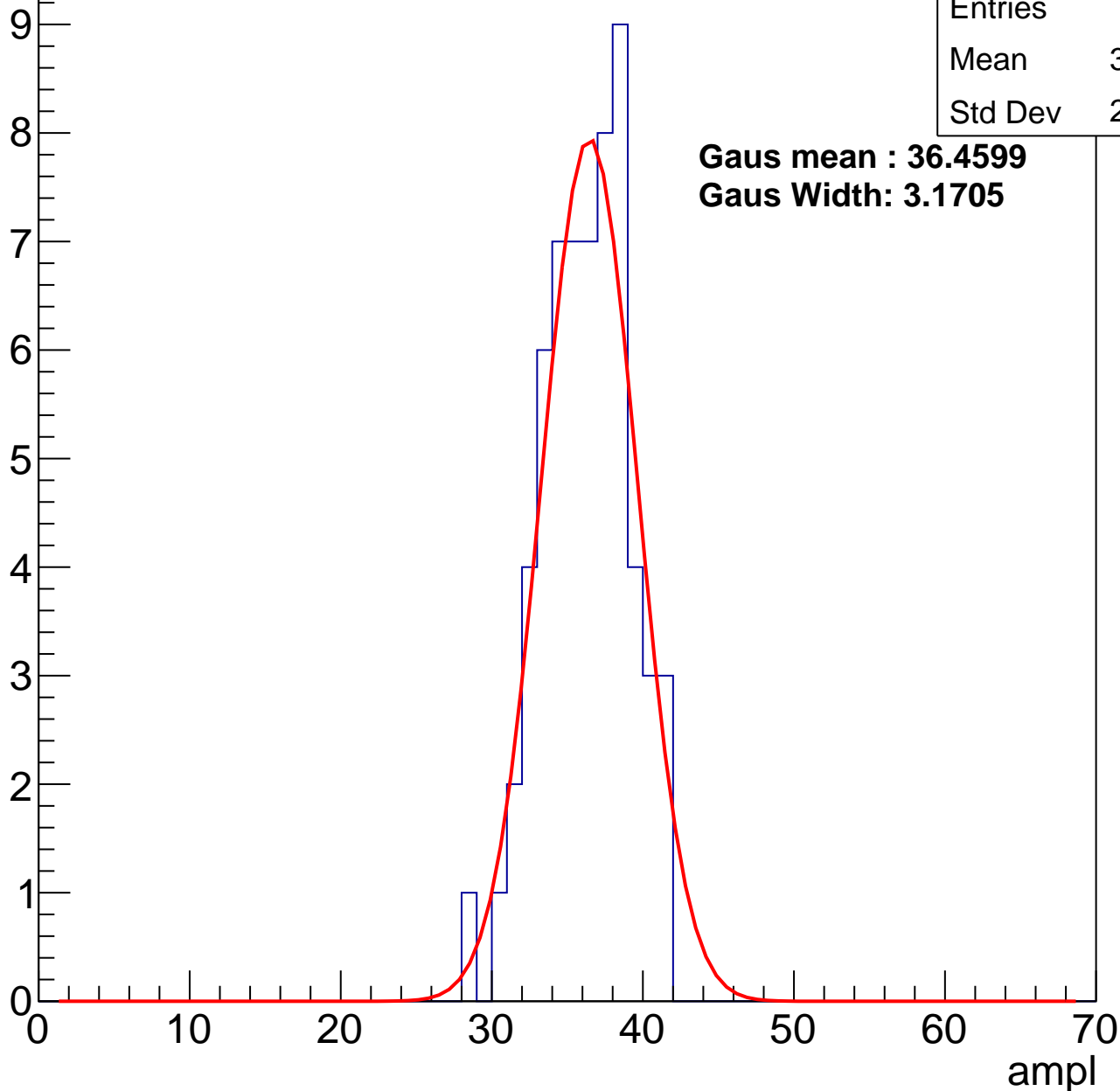
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.77
Std Dev	2.848

Gaus mean : 36.4599

Gaus Width: 3.1705



B1L103S, U5-ch85, adc2

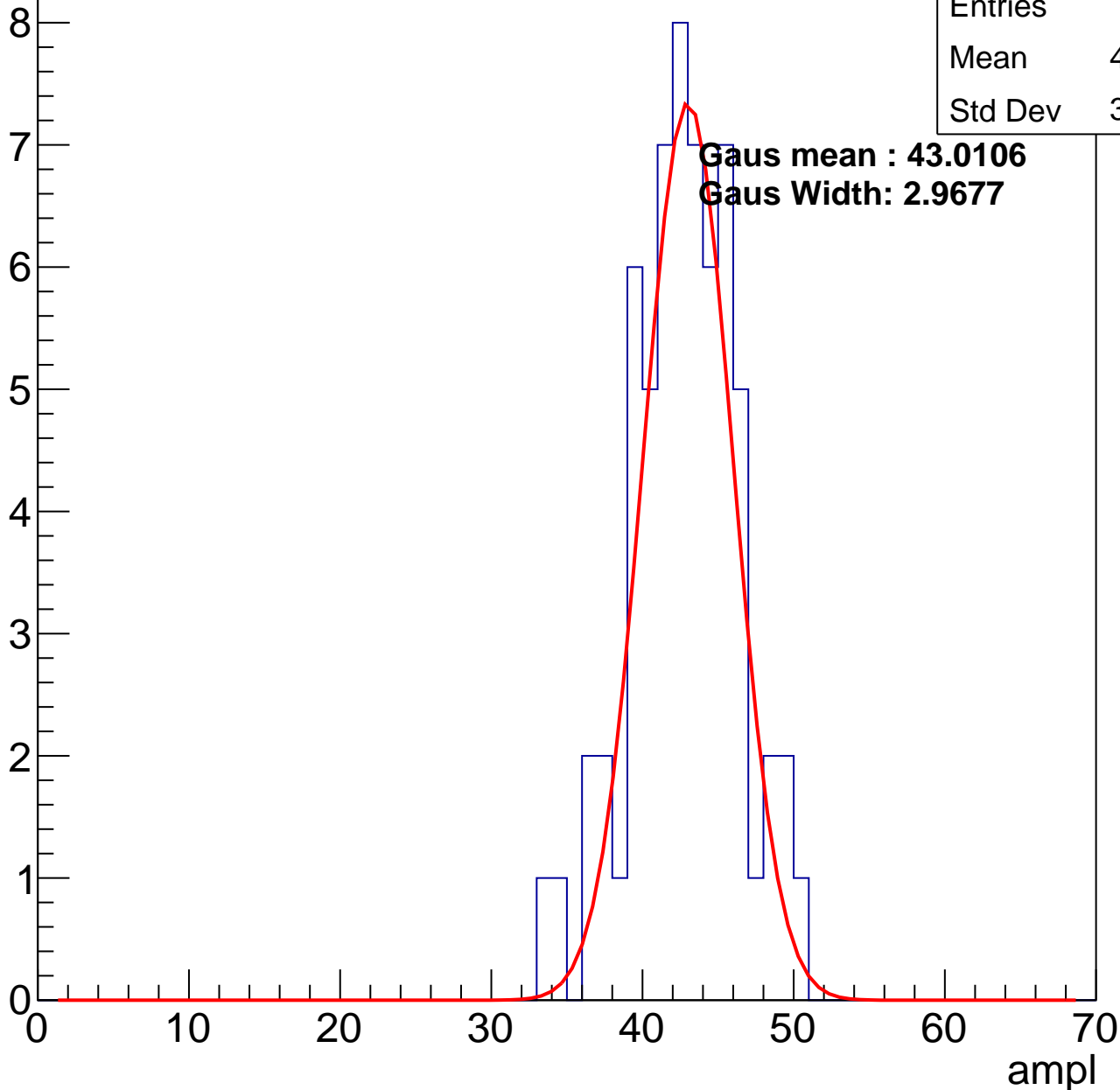
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.33
Std Dev	3.514

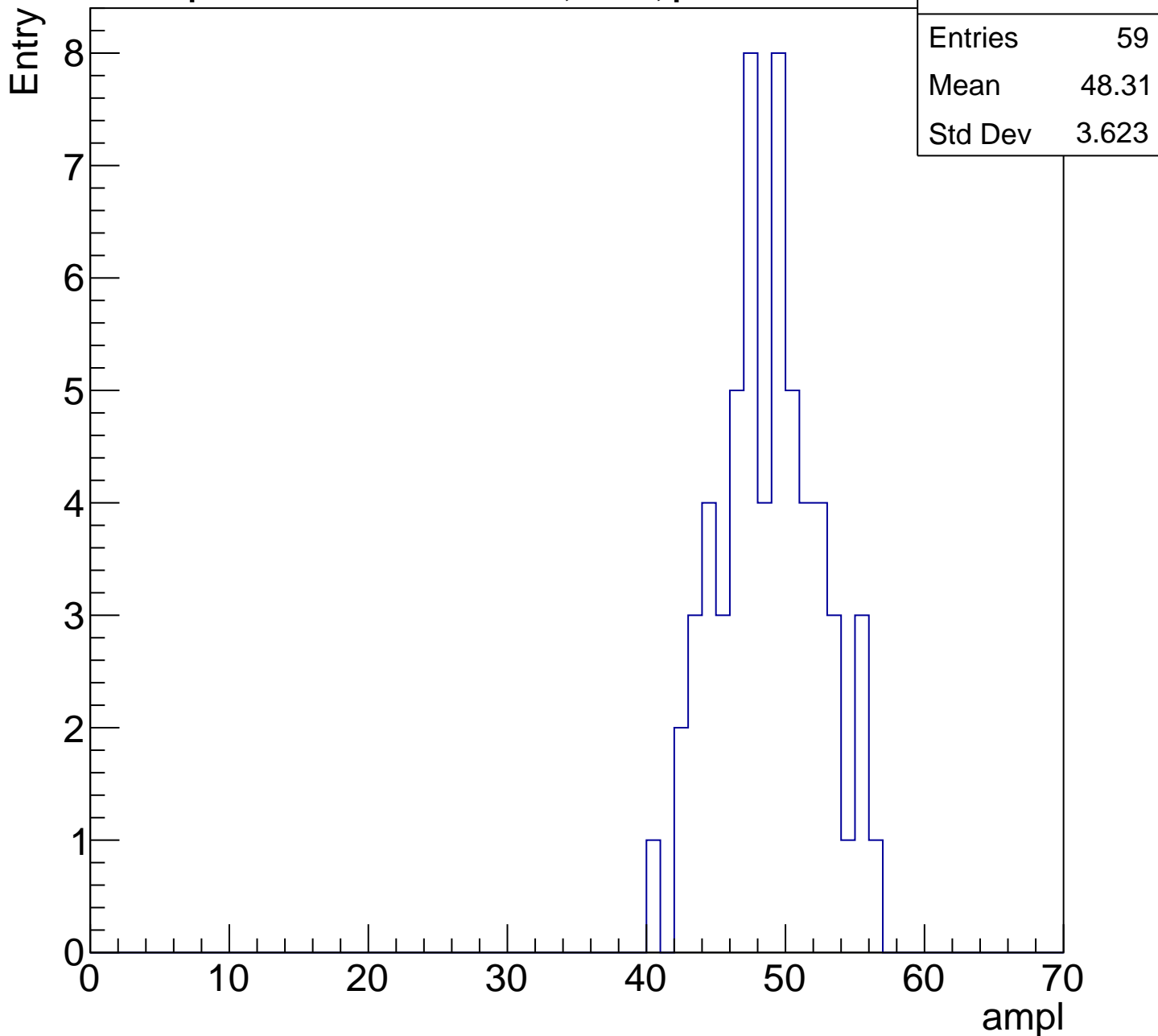
Gaus mean : 43.0106

Gaus Width: 2.9677



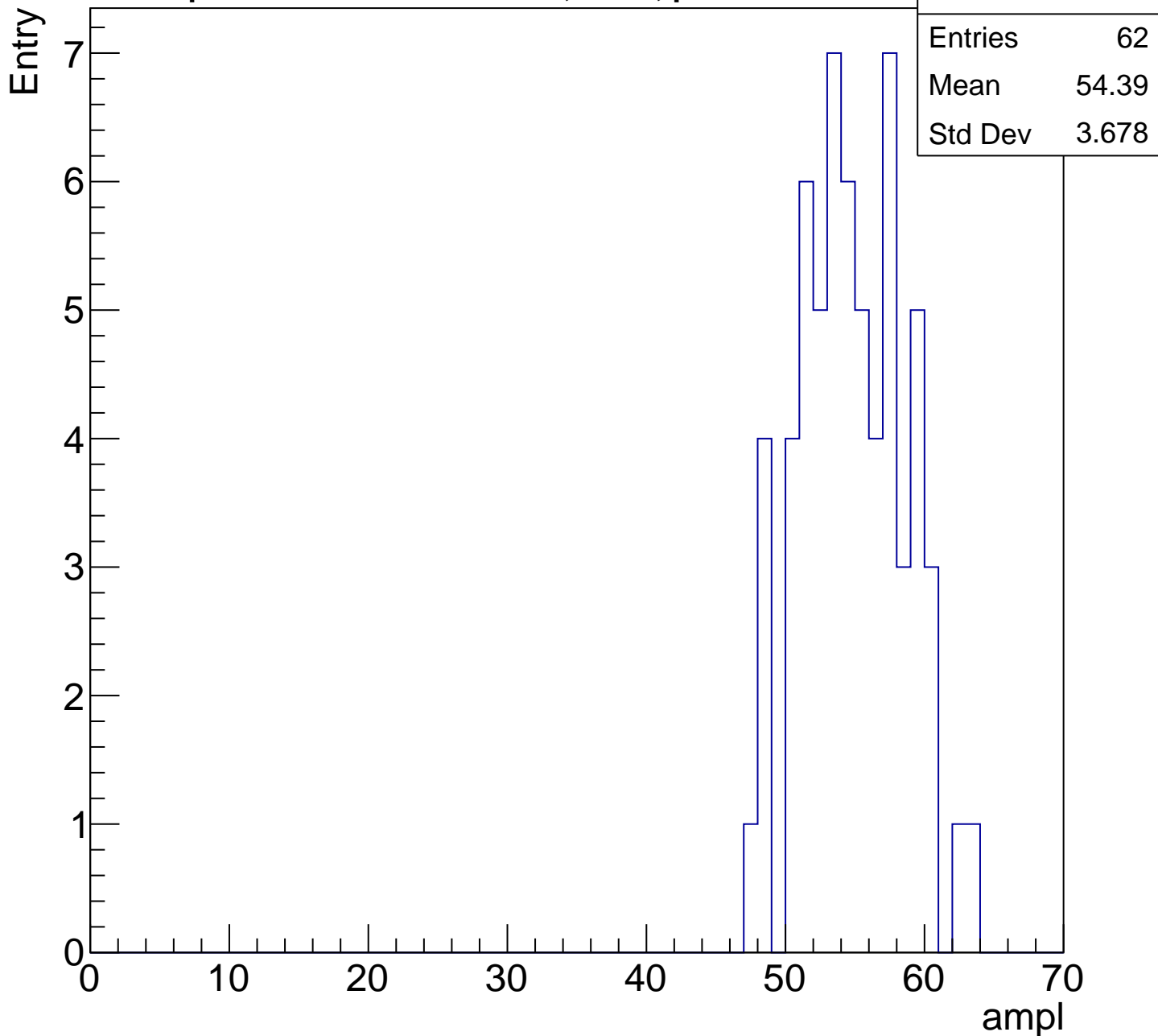
B1L103S, U5-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch85, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

Entries 57

Mean 59.39

Std Dev 2.808

8

6

4

2

0

0

10

20

30

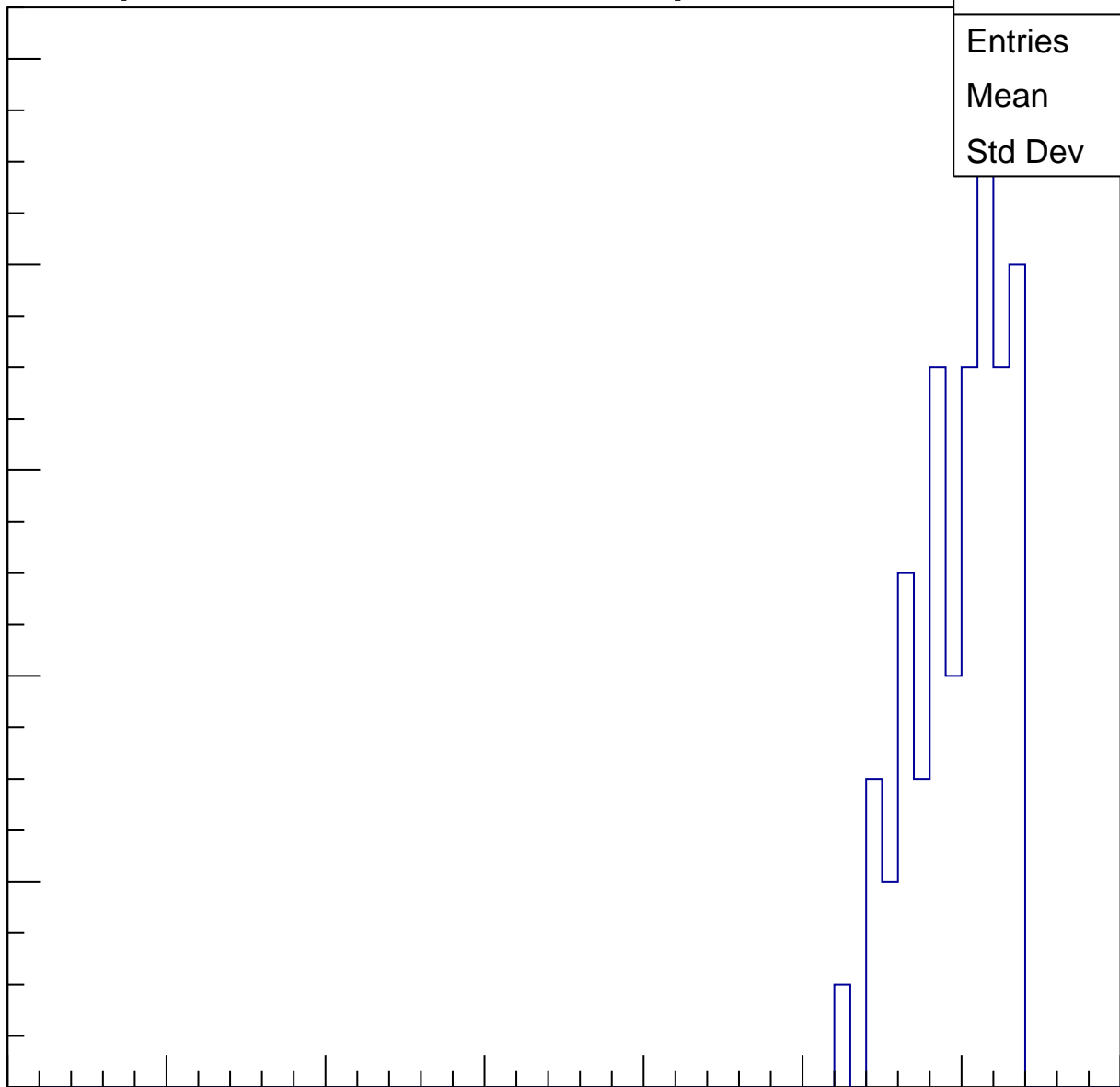
40

50

60

70

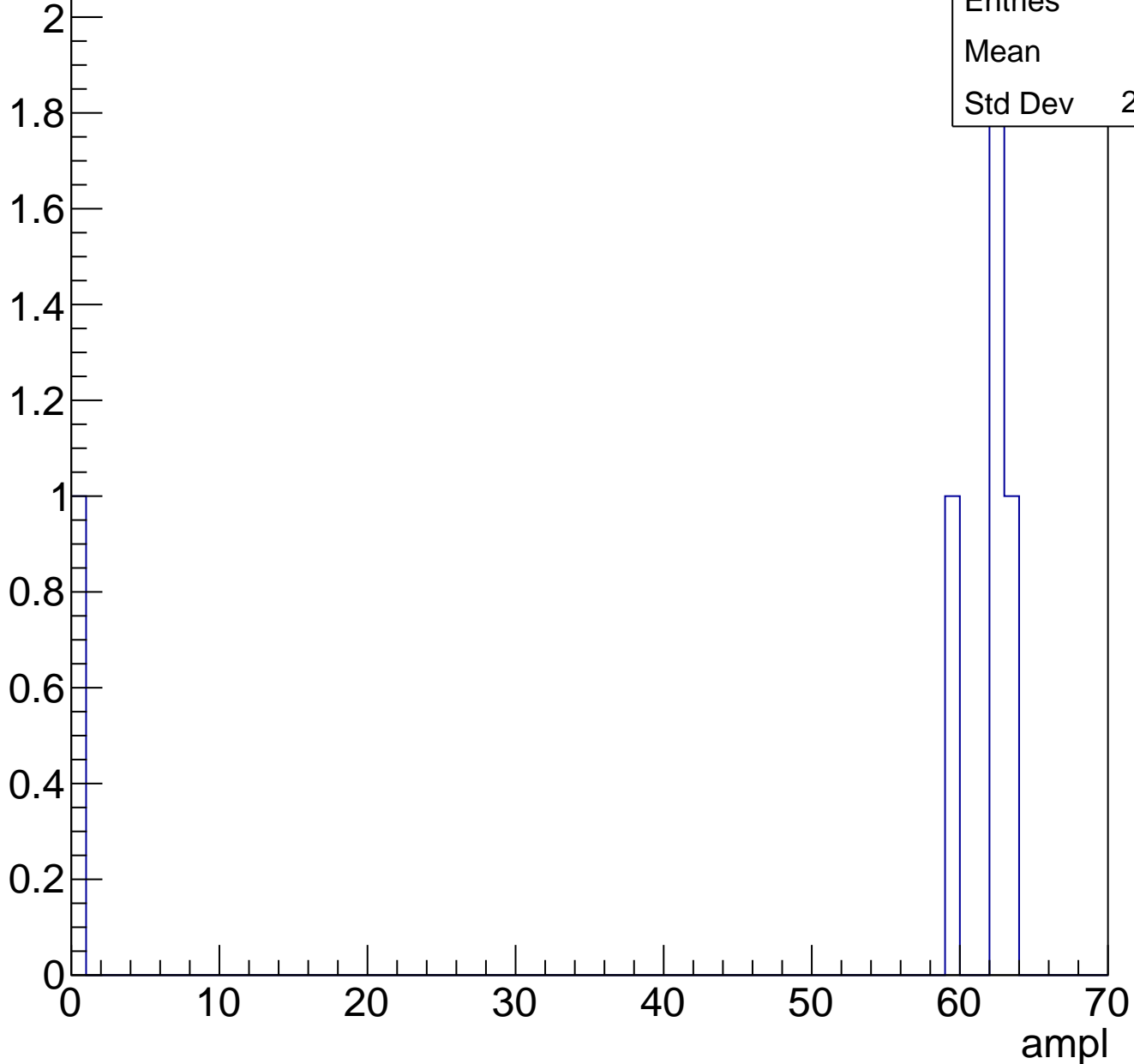
ampl



B1L103S, U5-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch86, adc0

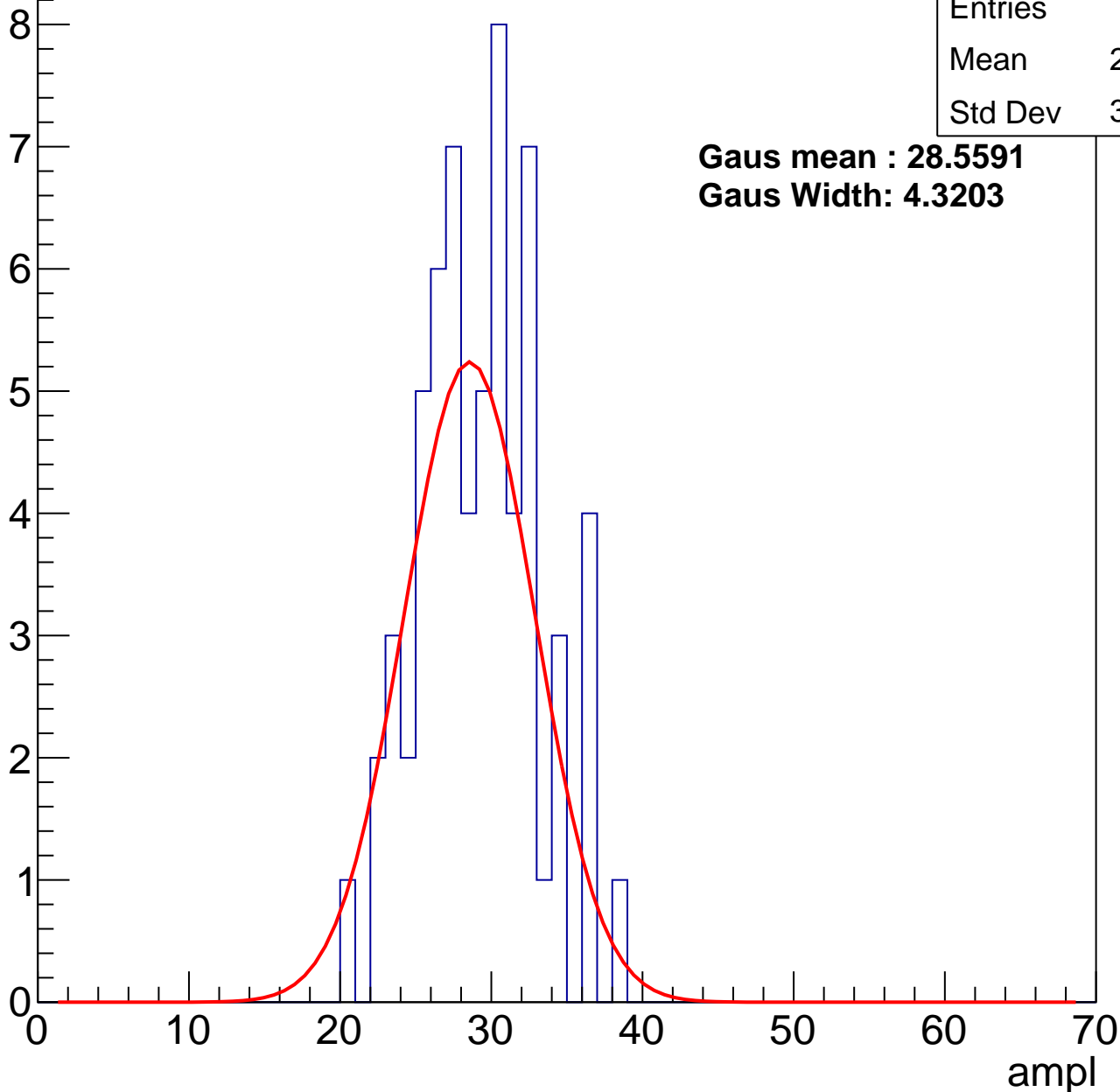
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.78
Std Dev	3.893

Gaus mean : 28.5591

Gaus Width: 4.3203



B1L103S, U5-ch86, adc1

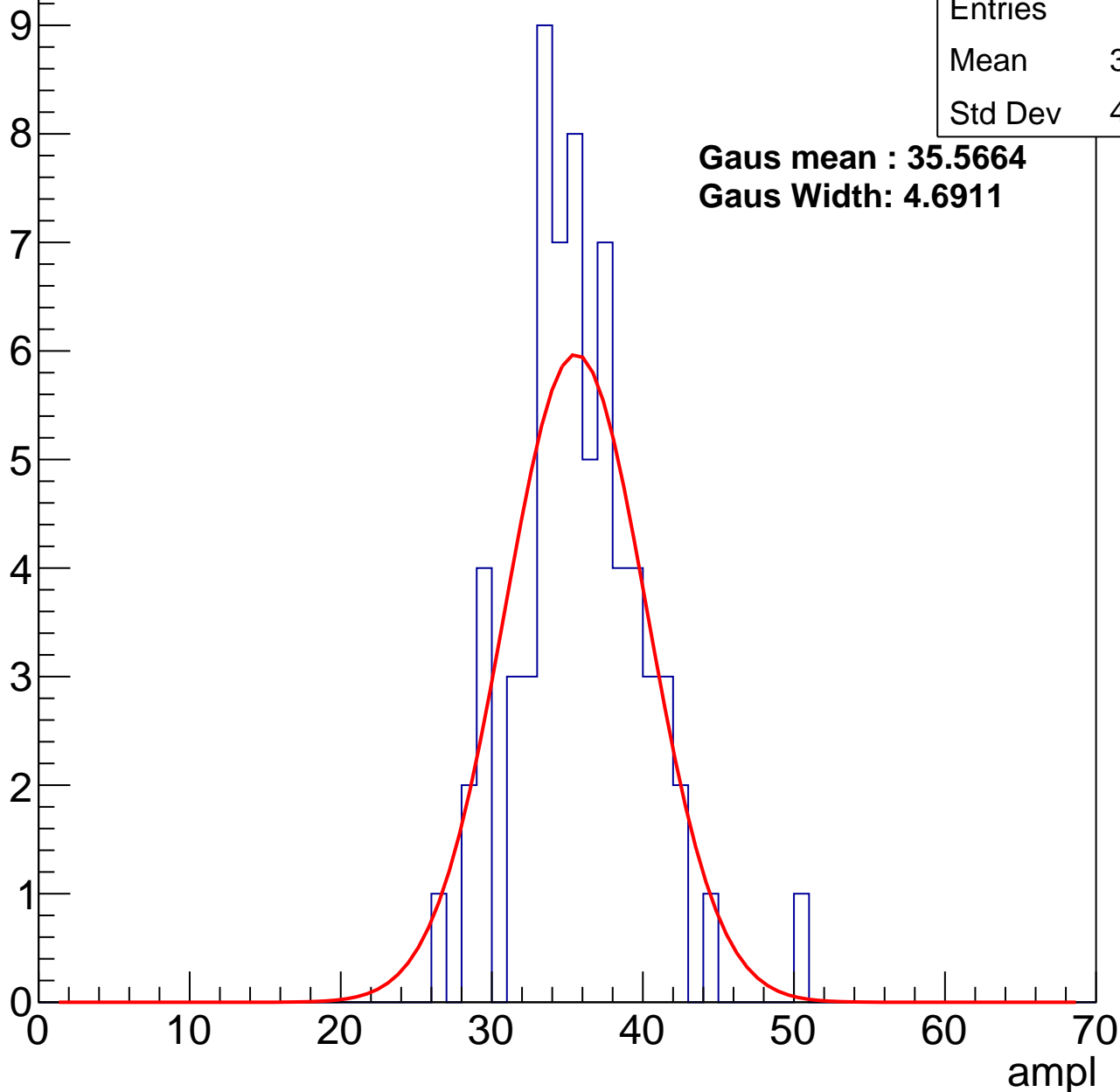
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.37
Std Dev	4.146

Gaus mean : 35.5664

Gaus Width: 4.6911



B1L103S, U5-ch86, adc2

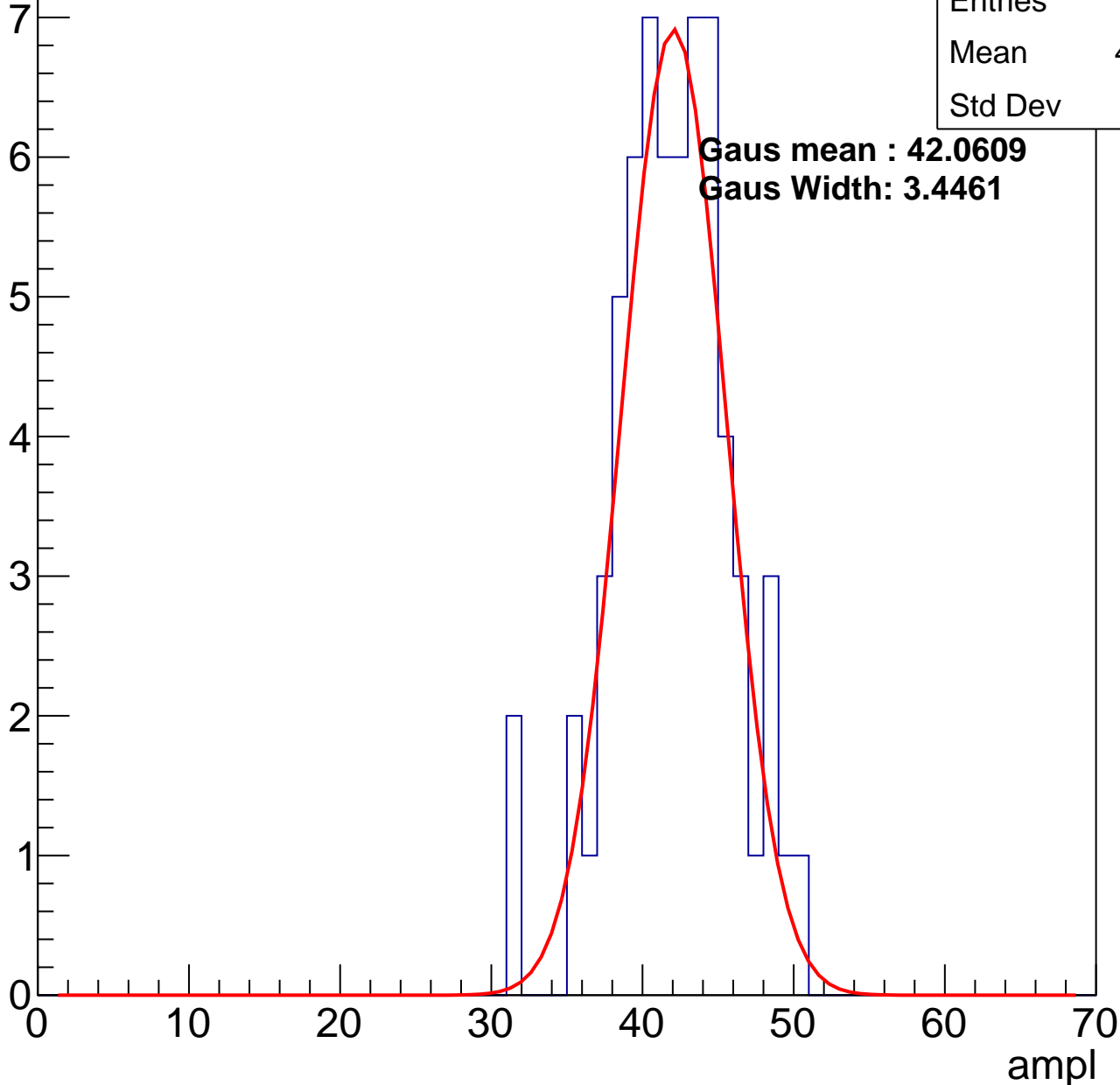
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.51
Std Dev	3.86

Gaus mean : 42.0609

Gaus Width: 3.4461

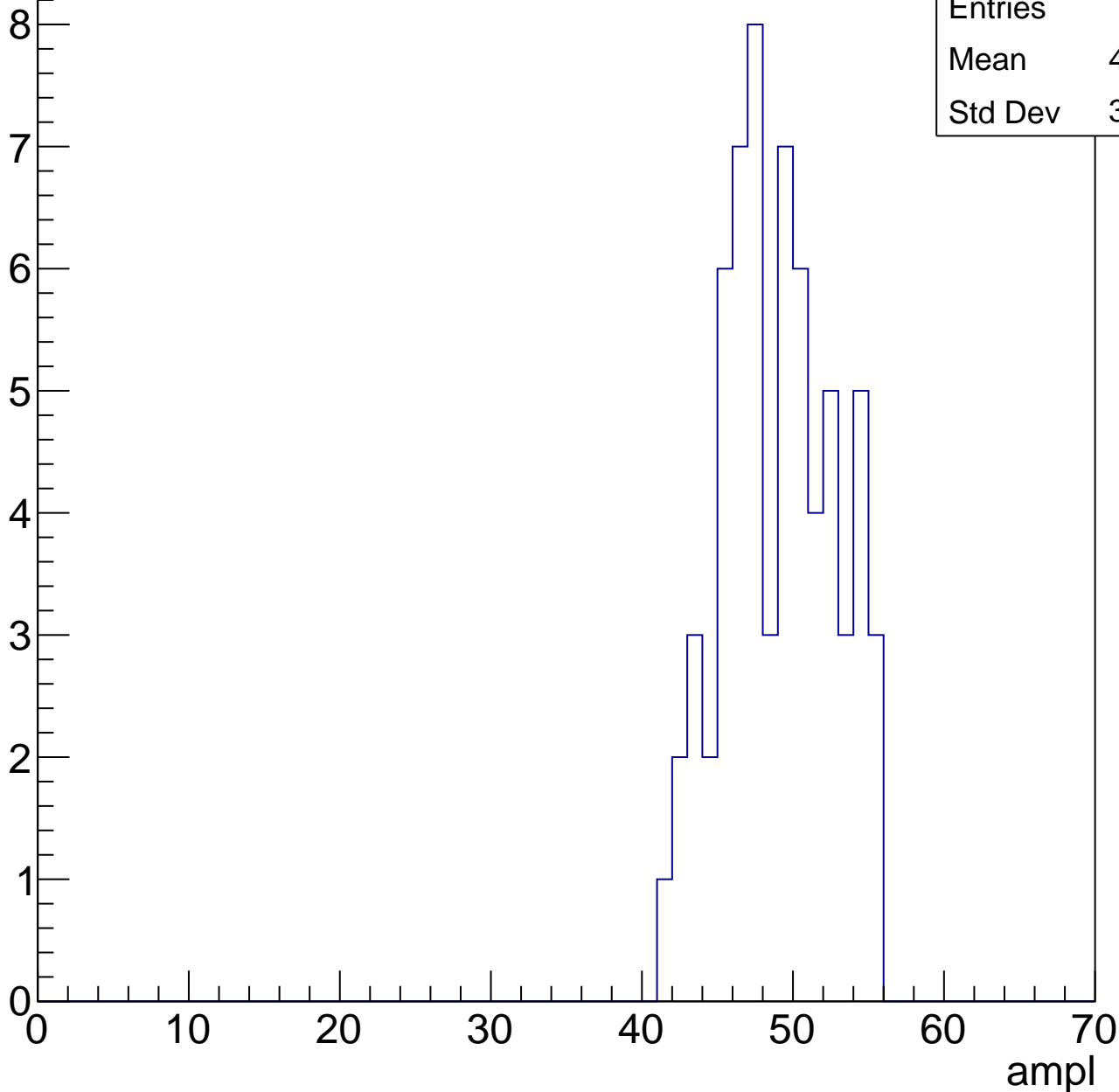


B1L103S, U5-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

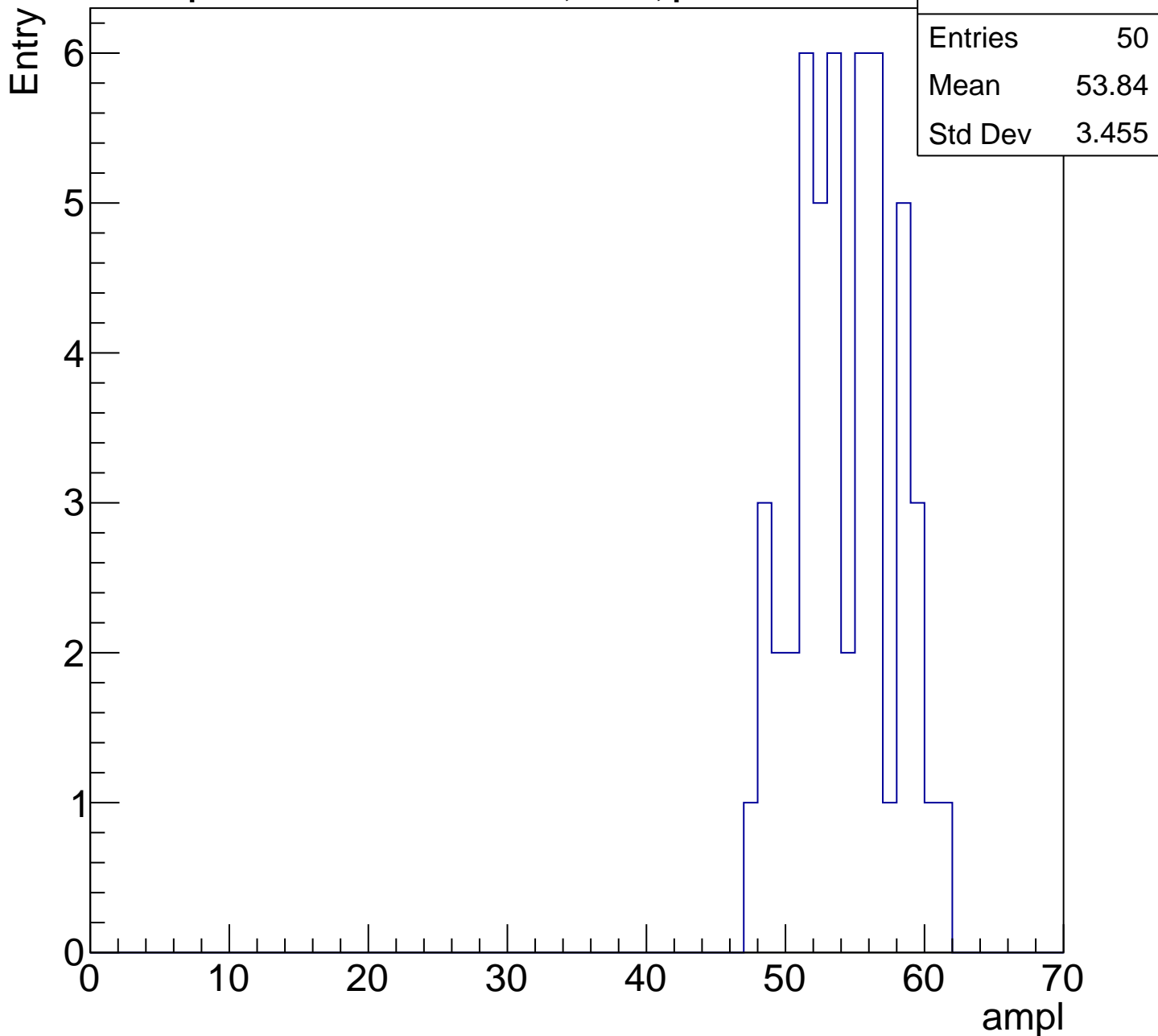
Entry

Entries	65
Mean	48.54
Std Dev	3.612



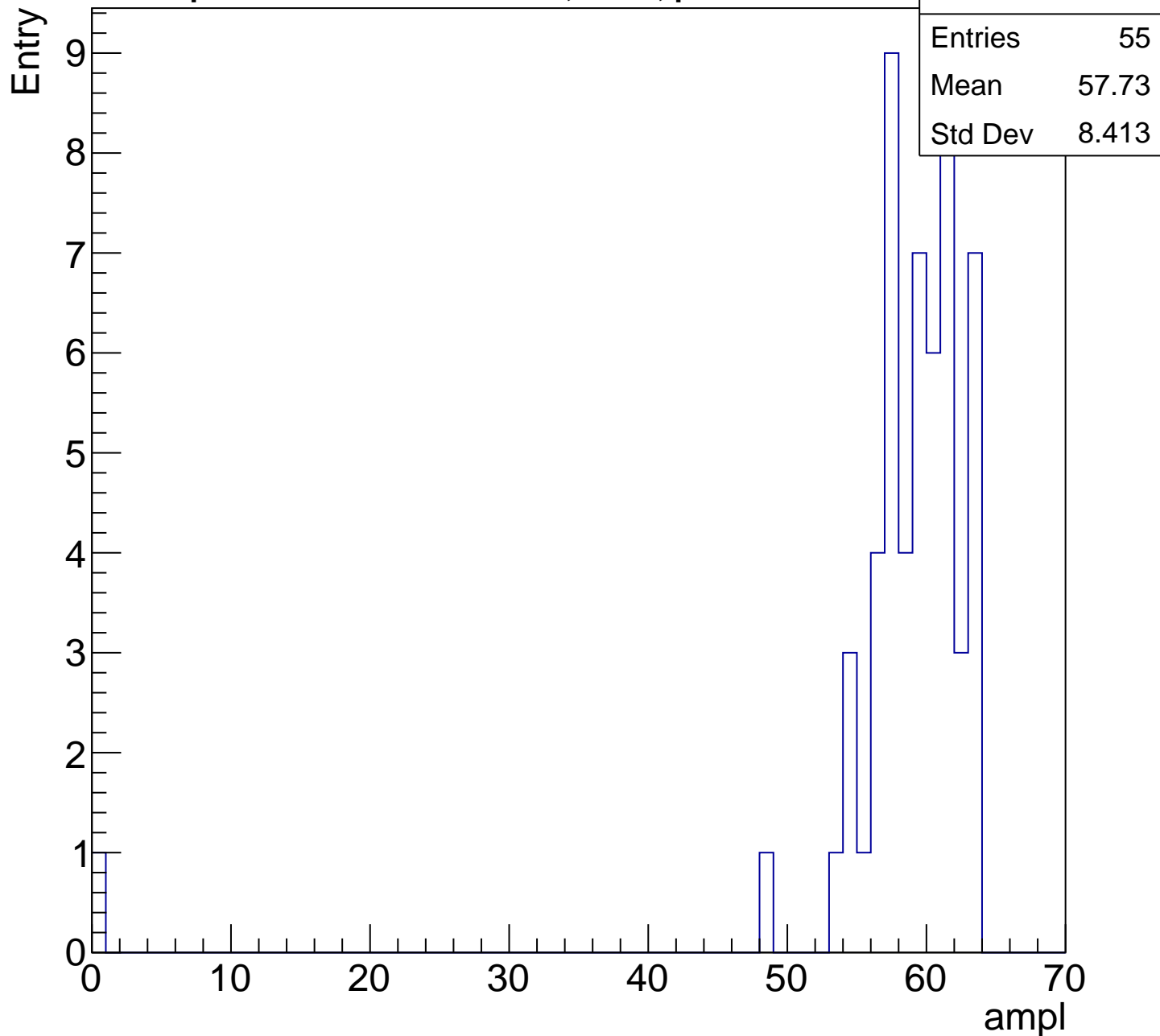
B1L103S, U5-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

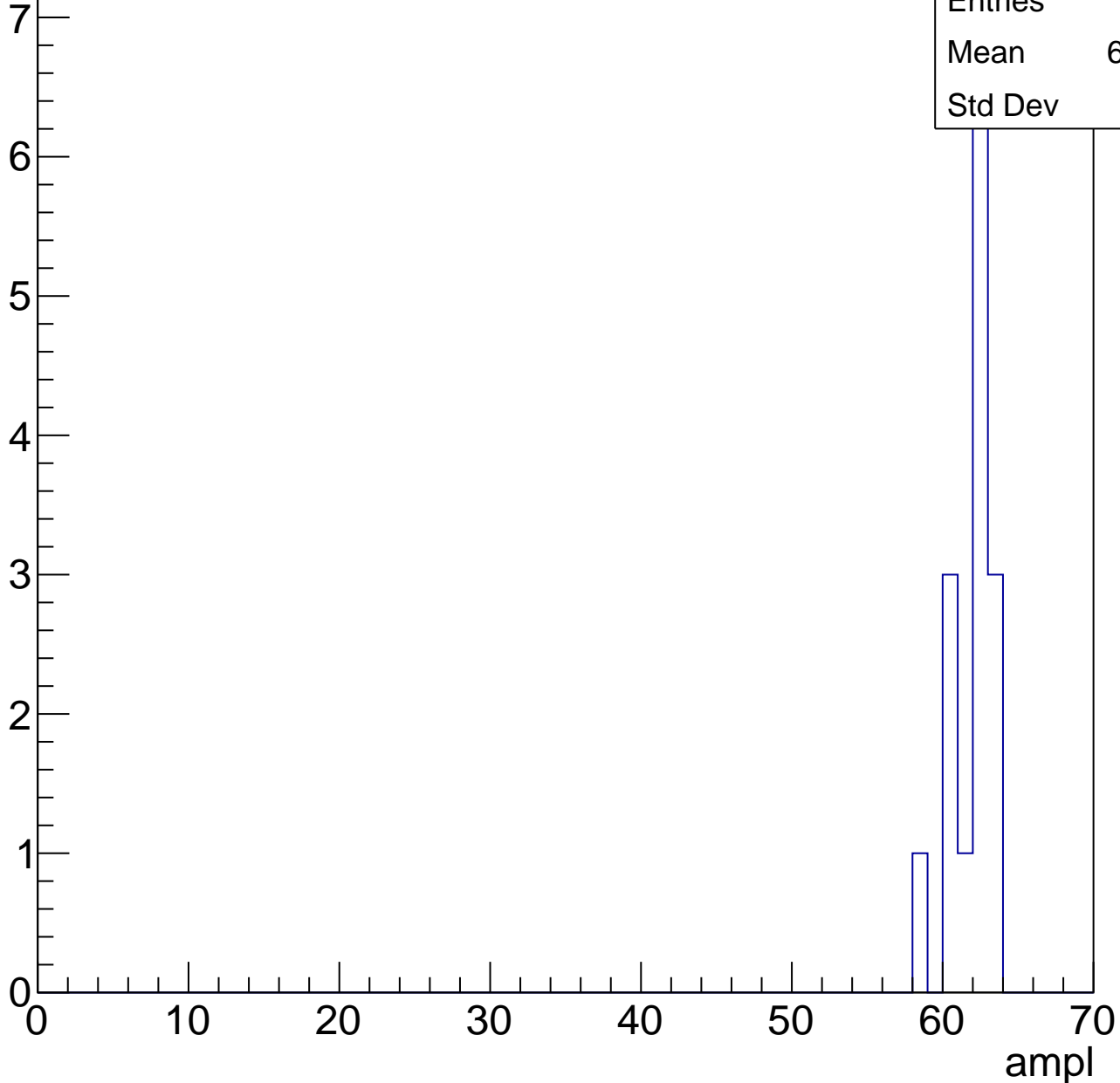


B1L103S, U5-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.47
Std Dev	1.36



B1L103S, U5-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch87, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	28.77
Std Dev	6.077

Gaus mean : 30.3762

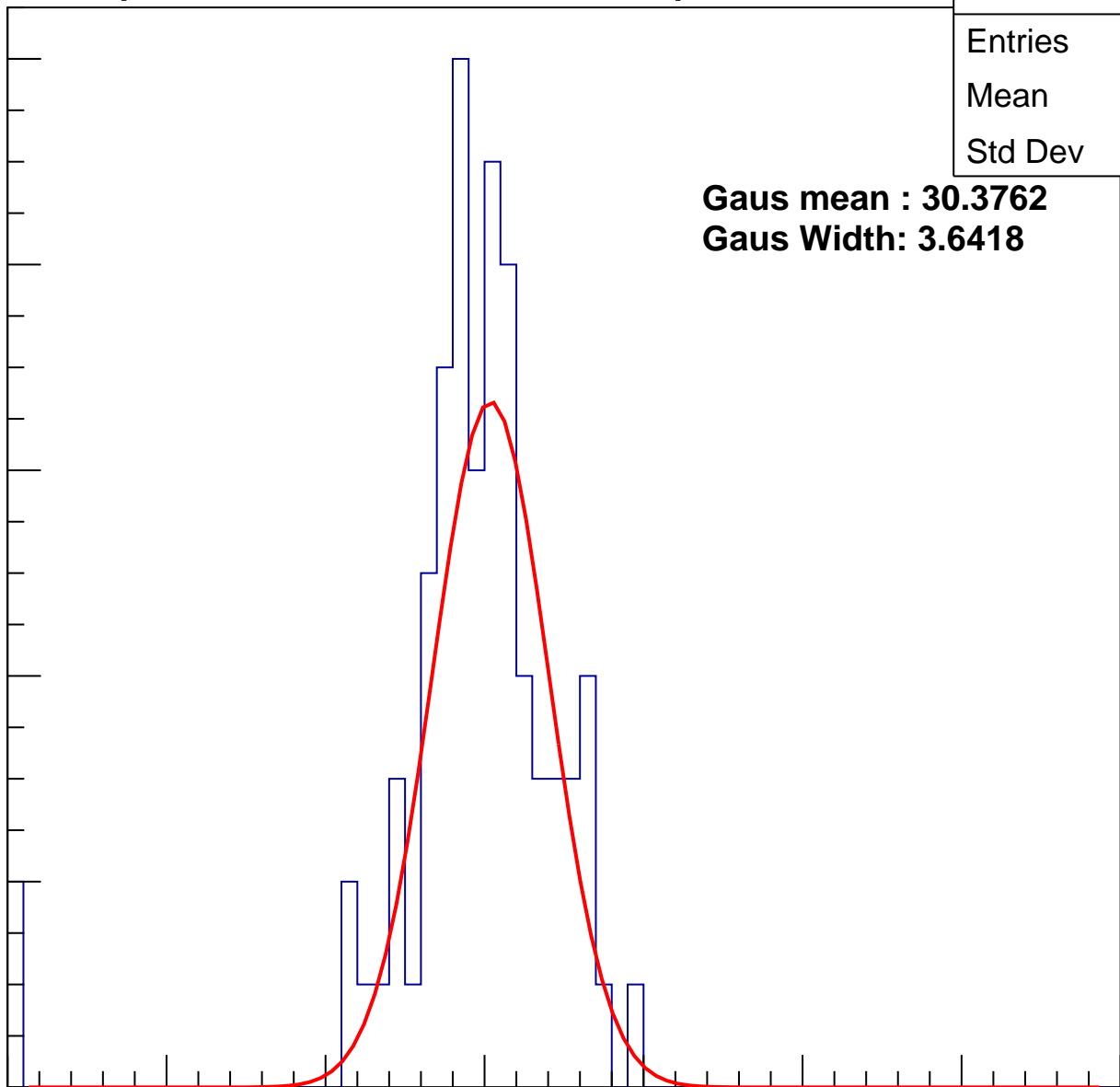
Gaus Width: 3.6418

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch87, adc1

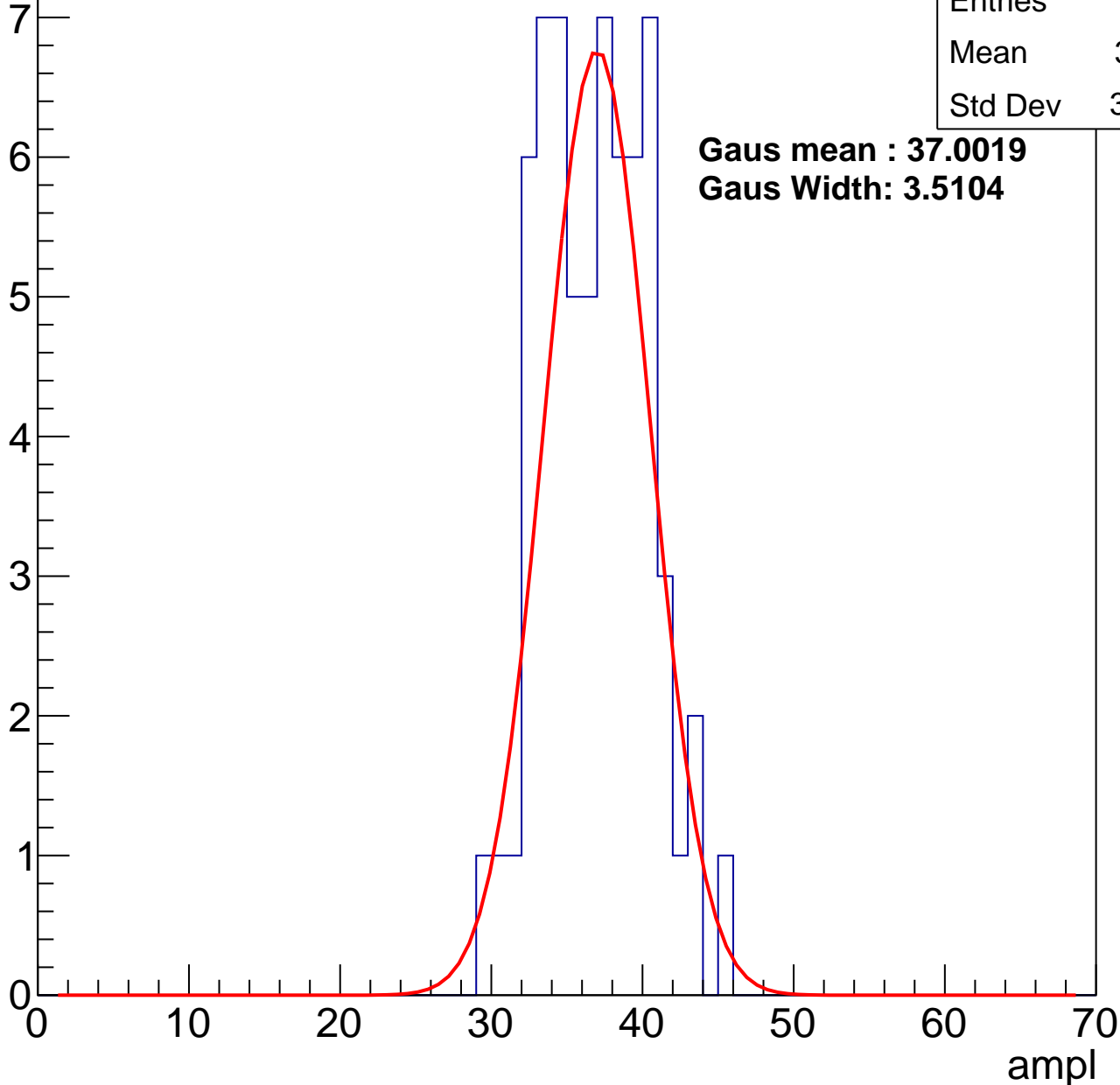
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.41
Std Dev	3.433

Gaus mean : 37.0019

Gaus Width: 3.5104



B1L103S, U5-ch87, adc2

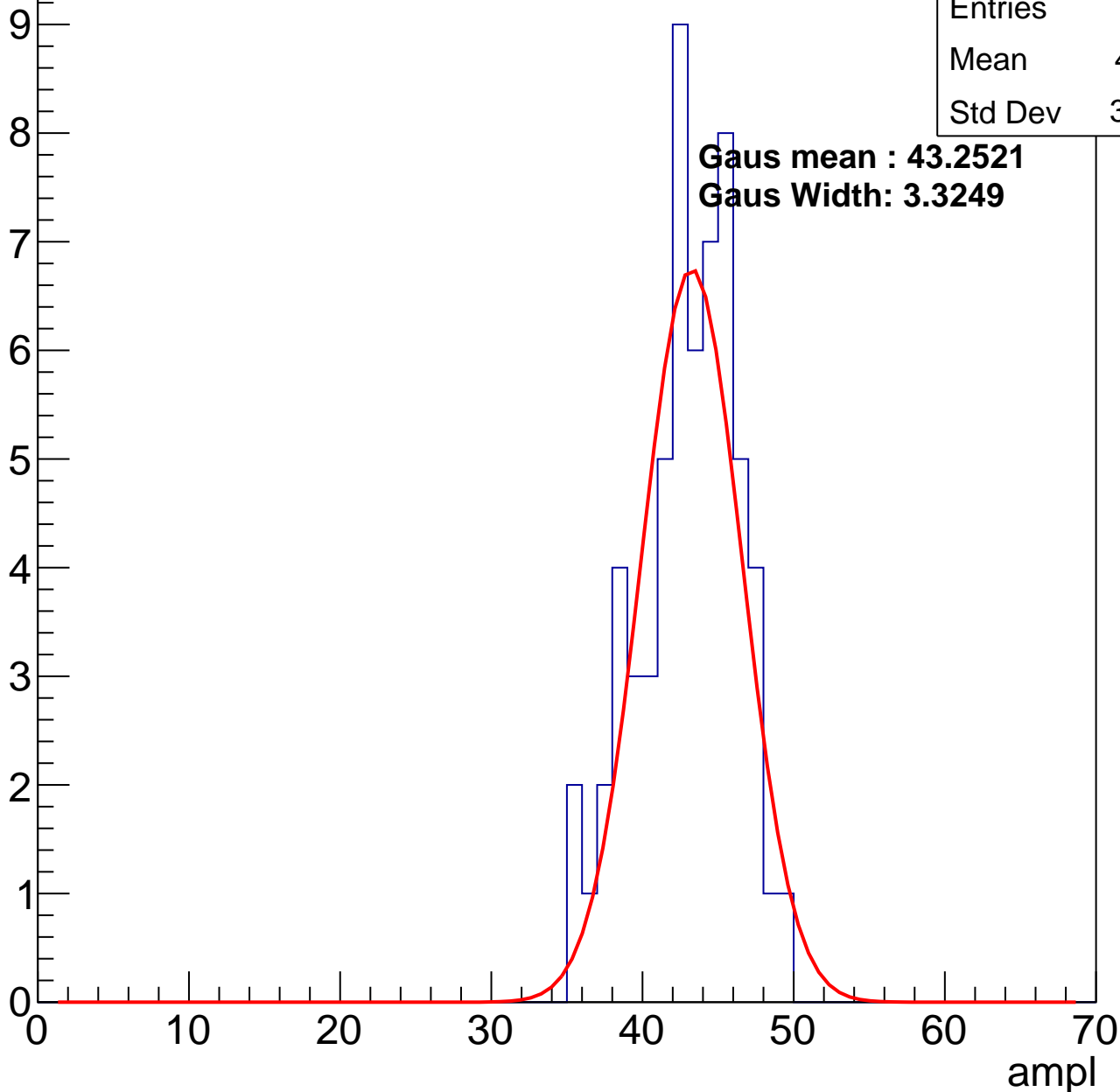
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.51
Std Dev	3.257

Gaus mean : 43.2521

Gaus Width: 3.3249

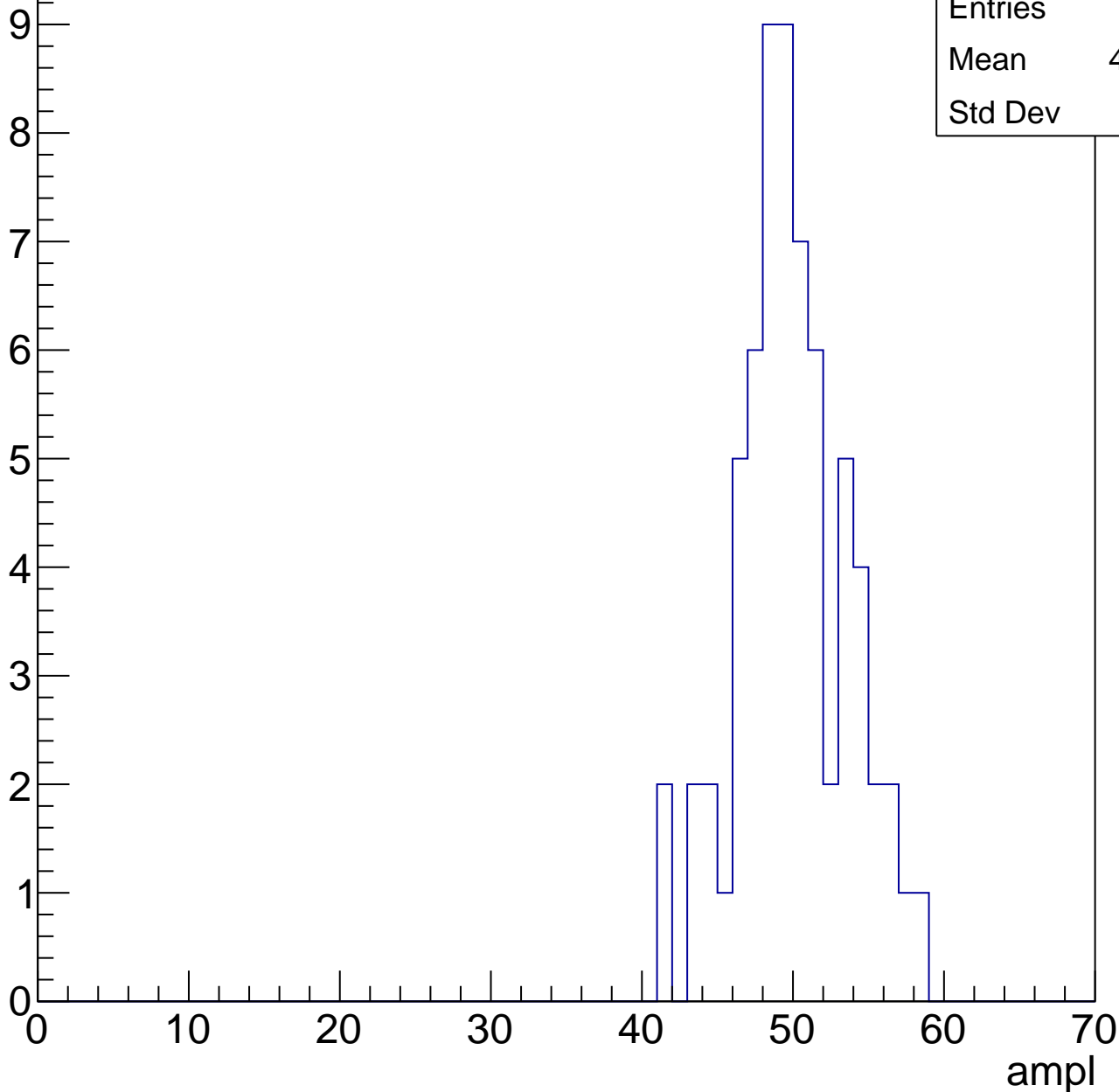


B1L103S, U5-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

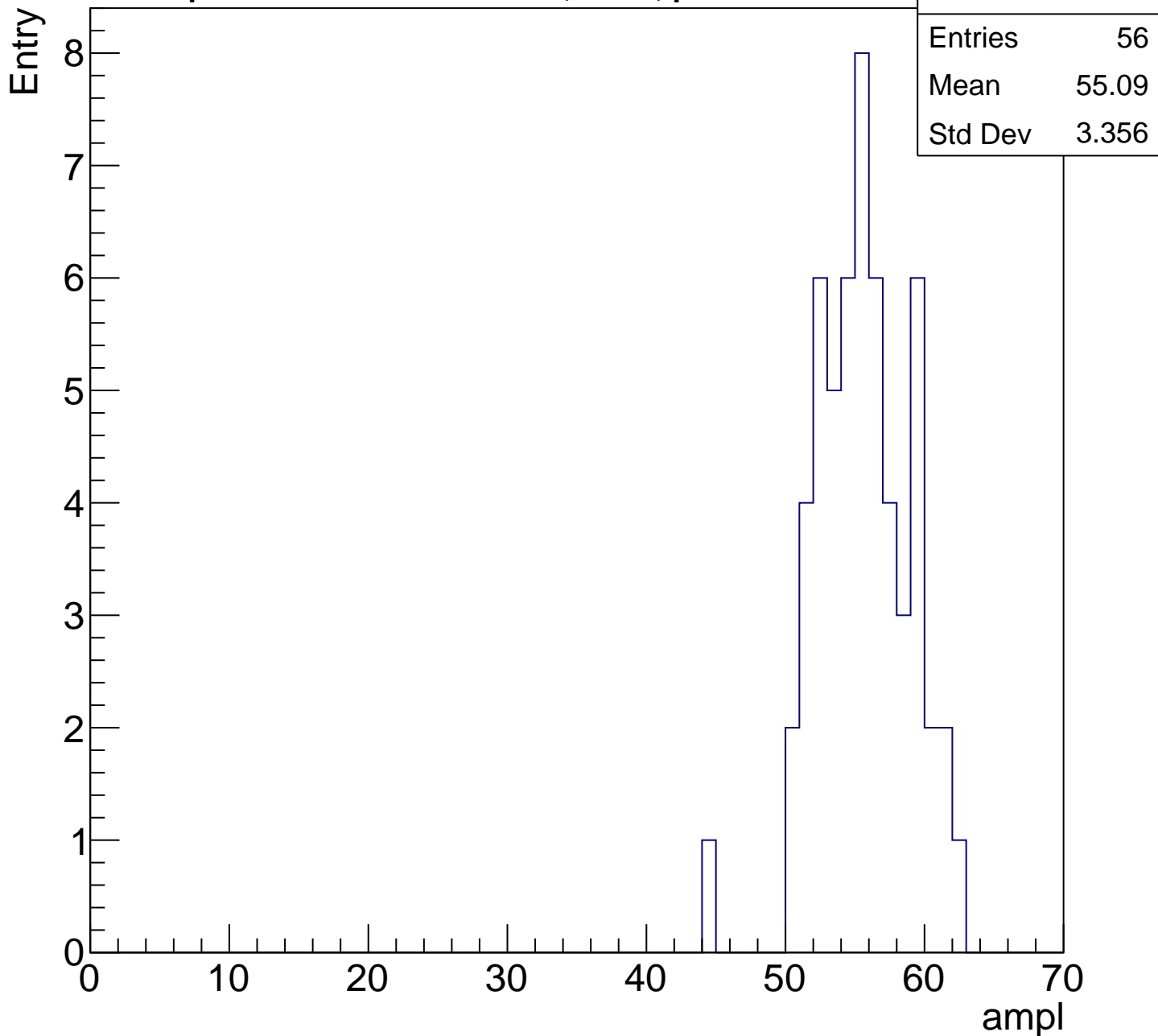
Entry

Entries	66
Mean	49.45
Std Dev	3.64



B1L103S, U5-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

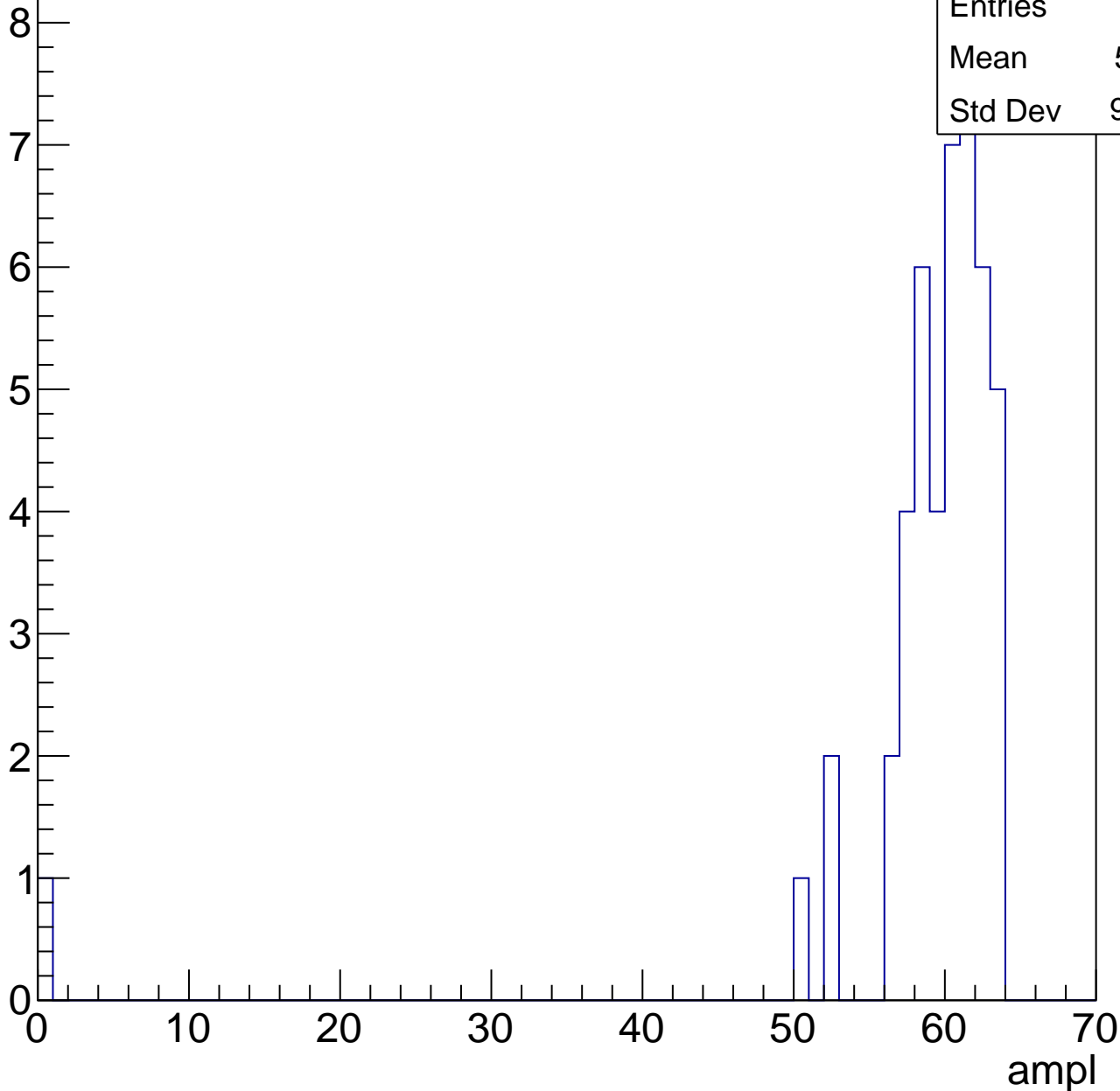


B1L103S, U5-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

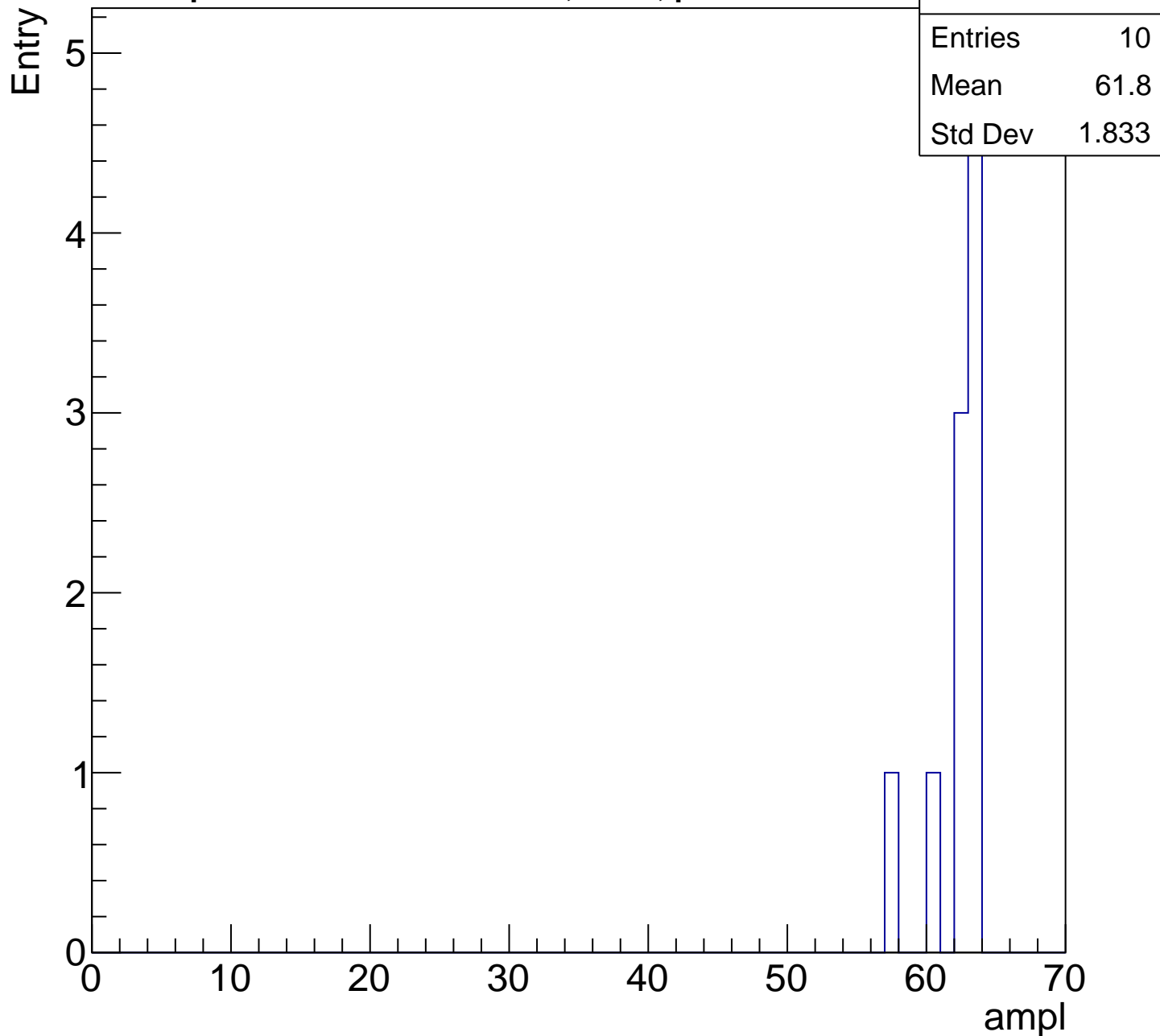
Entry

Entries	46
Mean	58.11
Std Dev	9.132



B1L103S, U5-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch88, adc0

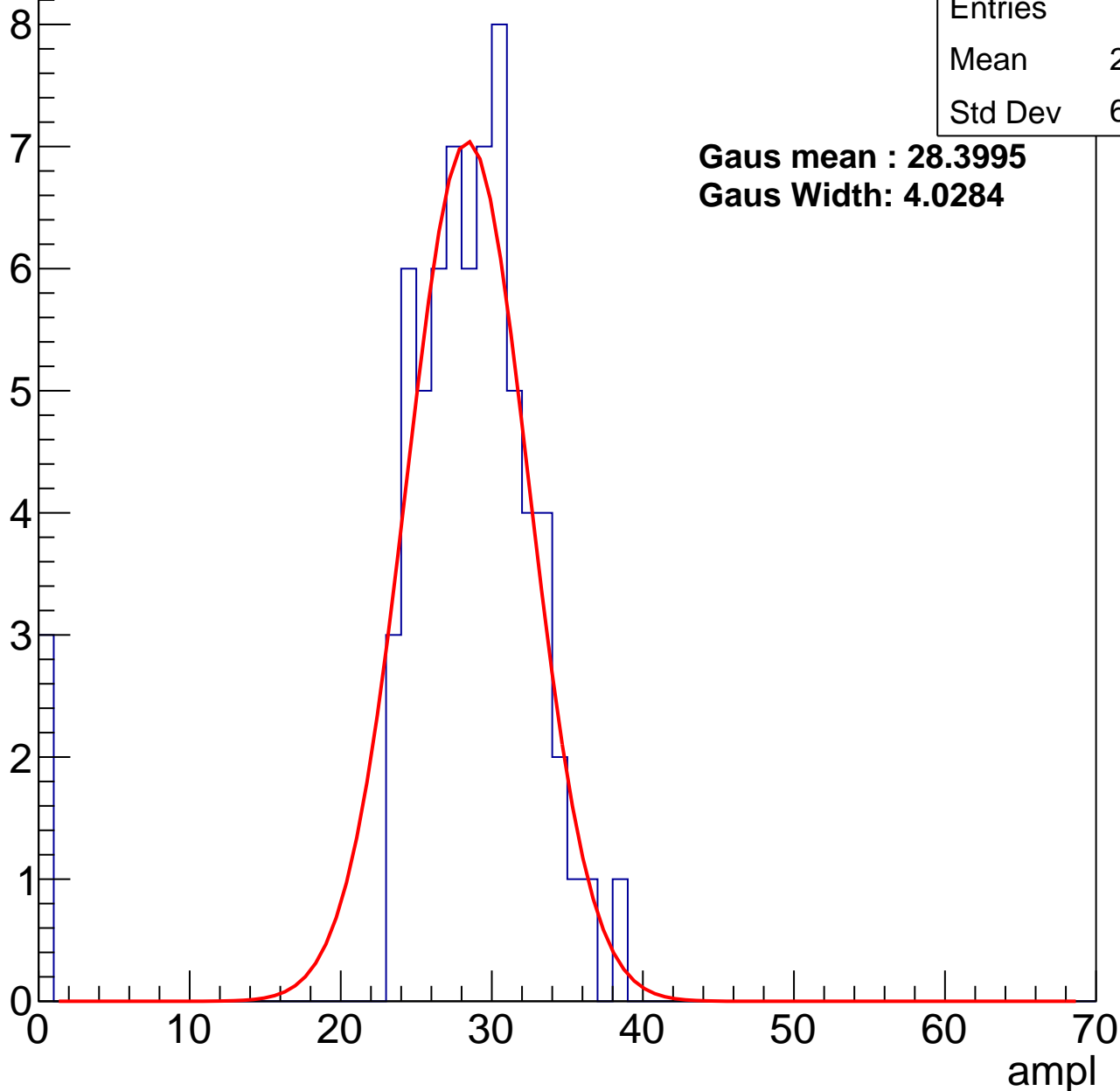
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	27.33
Std Dev	6.704

Gaus mean : 28.3995

Gaus Width: 4.0284



B1L103S, U5-ch88, adc1

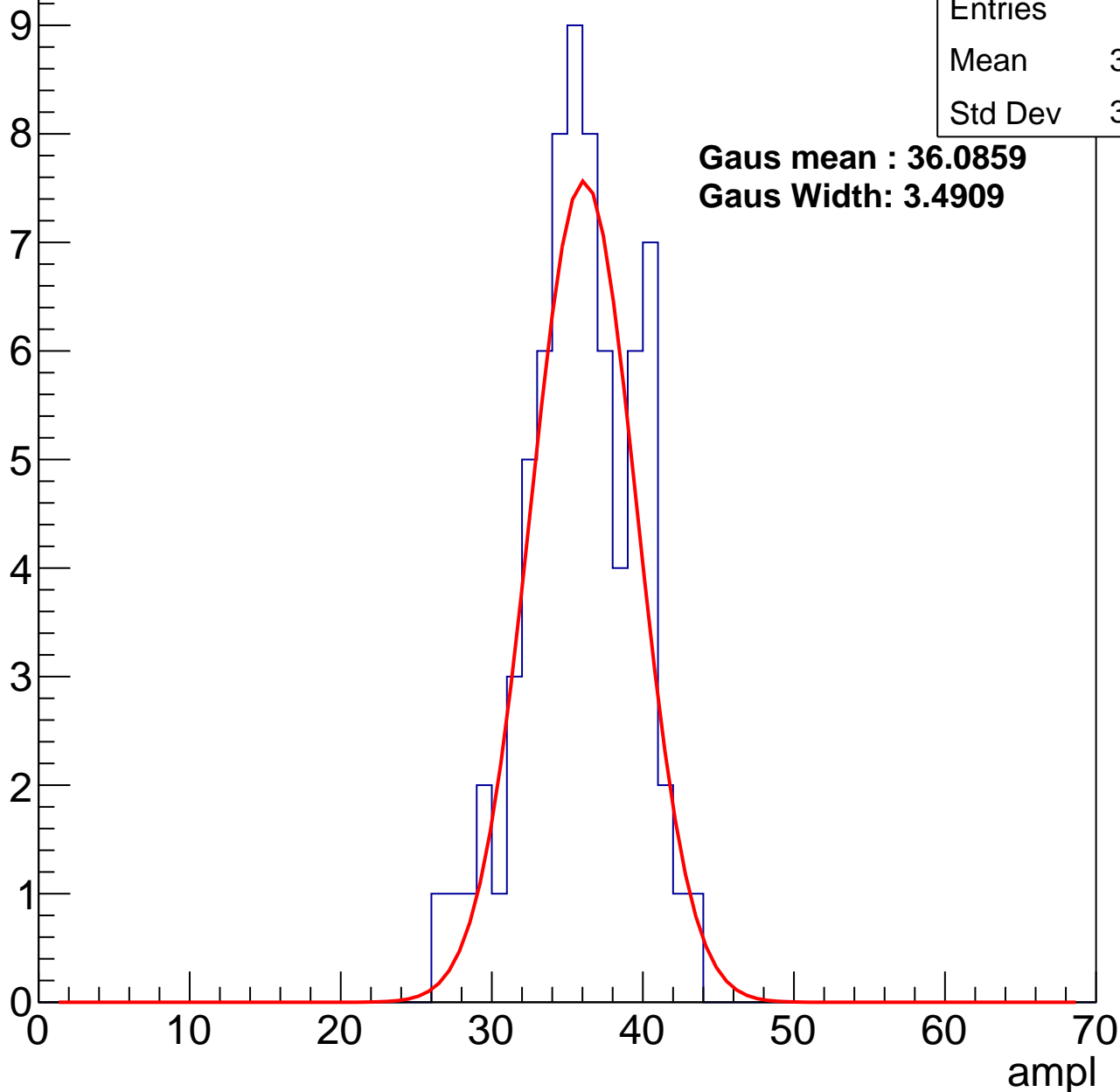
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	35.42
Std Dev	3.589

Gaus mean : 36.0859

Gaus Width: 3.4909



B1L103S, U5-ch88, adc2

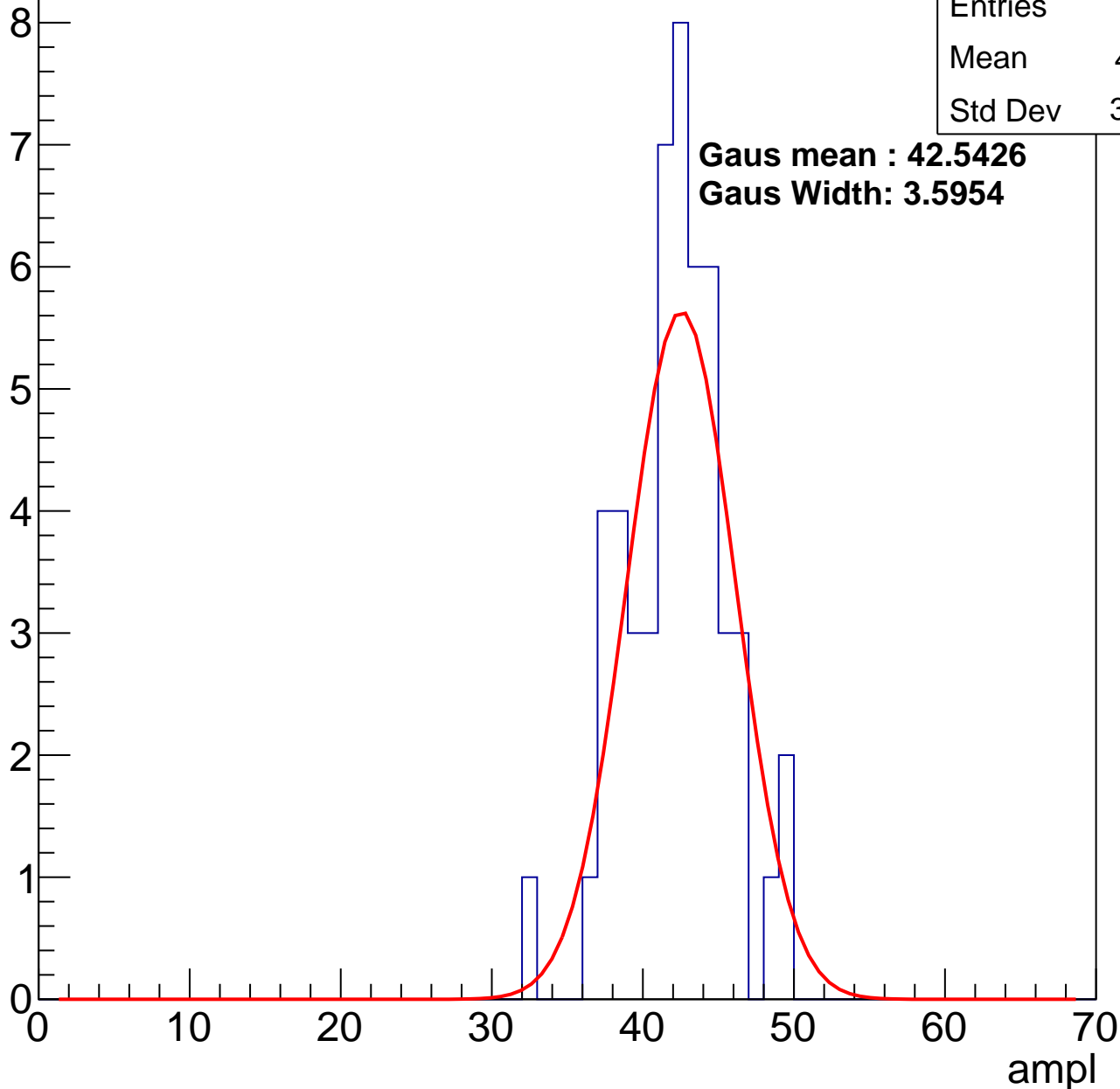
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	41.71
Std Dev	3.342

Gaus mean : 42.5426

Gaus Width: 3.5954

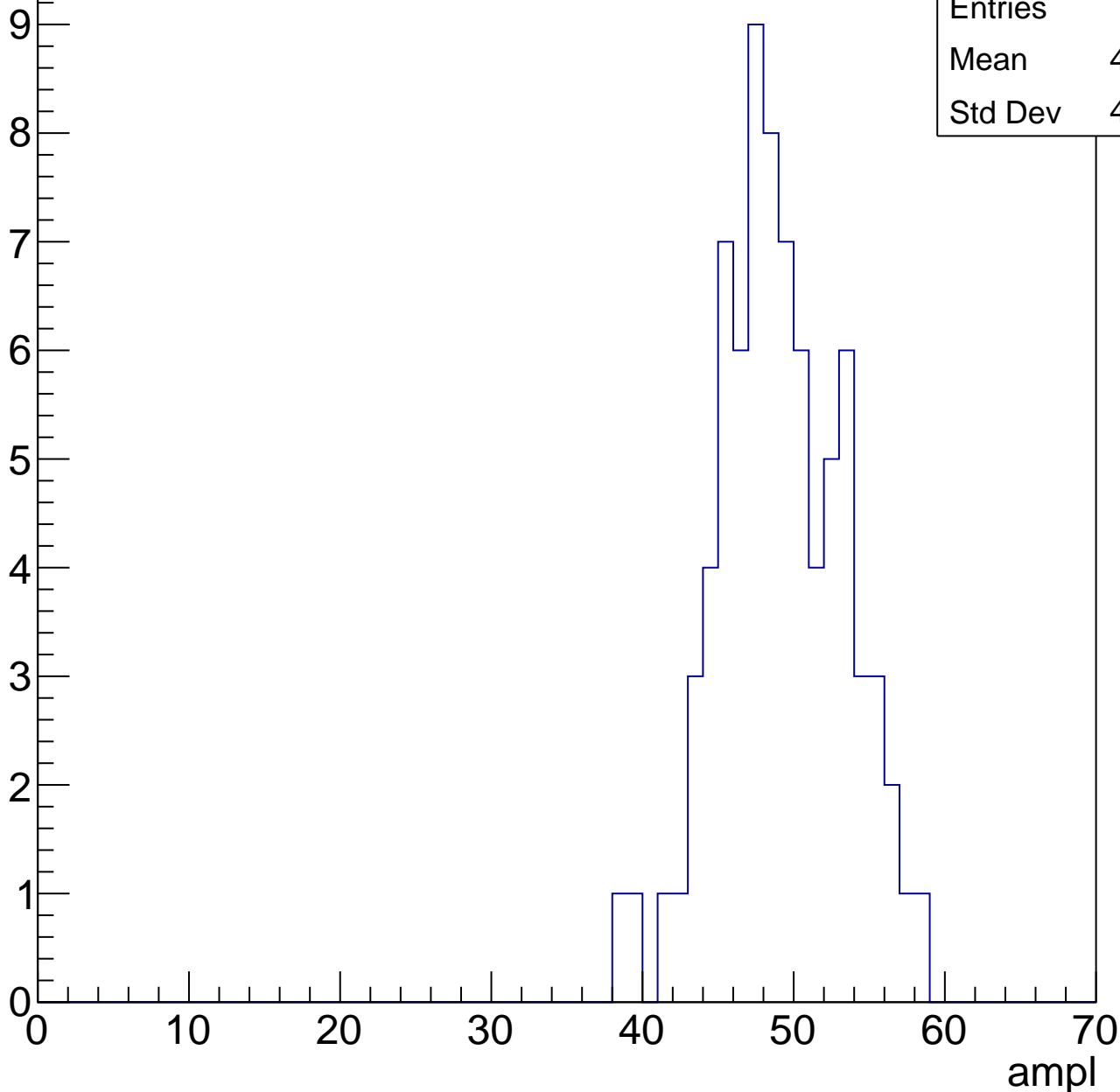


B1L103S, U5-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	48.63
Std Dev	4.104

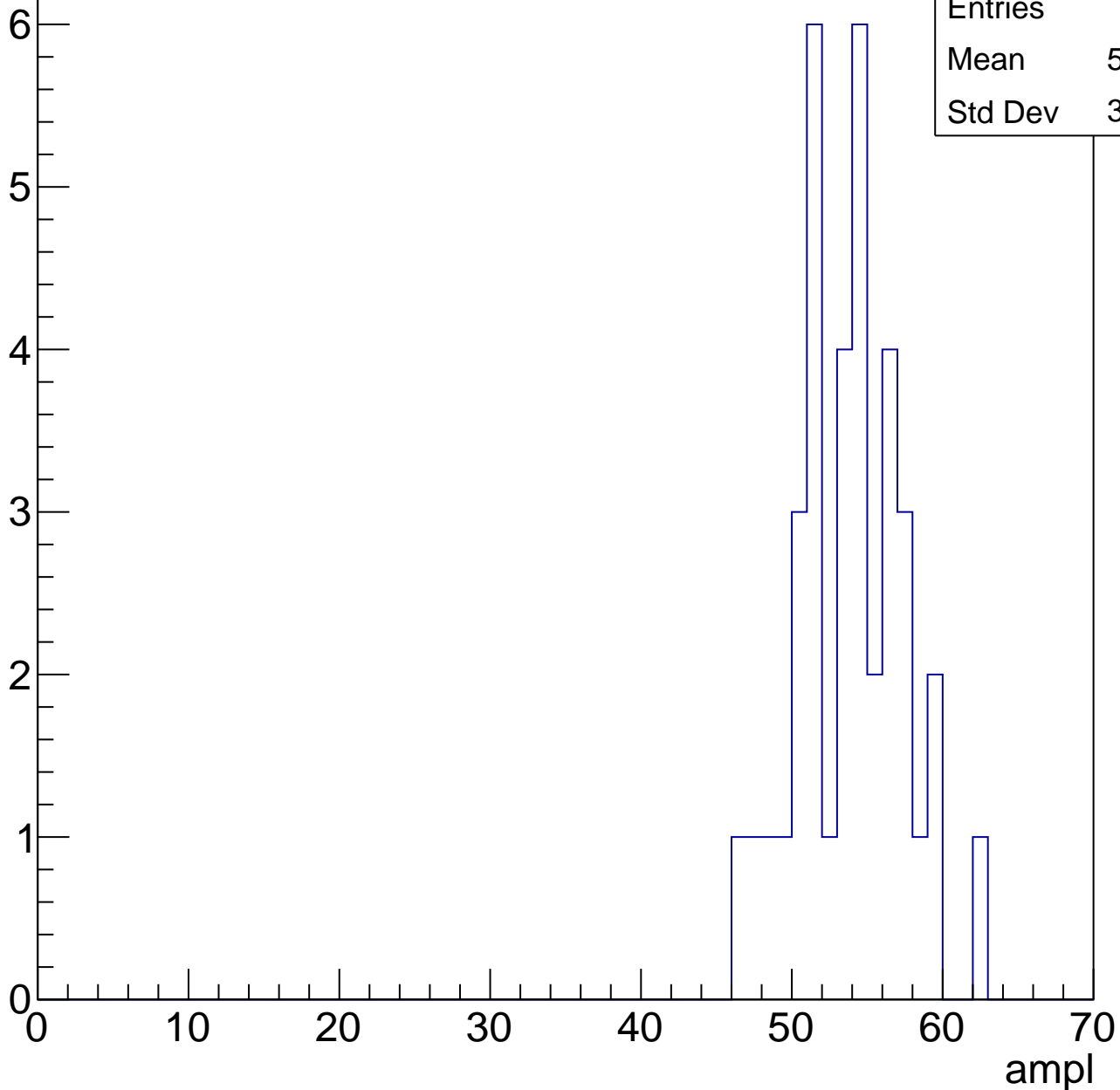


B1L103S, U5-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	53.43
Std Dev	3.476



B1L103S, U5-ch88, adc5

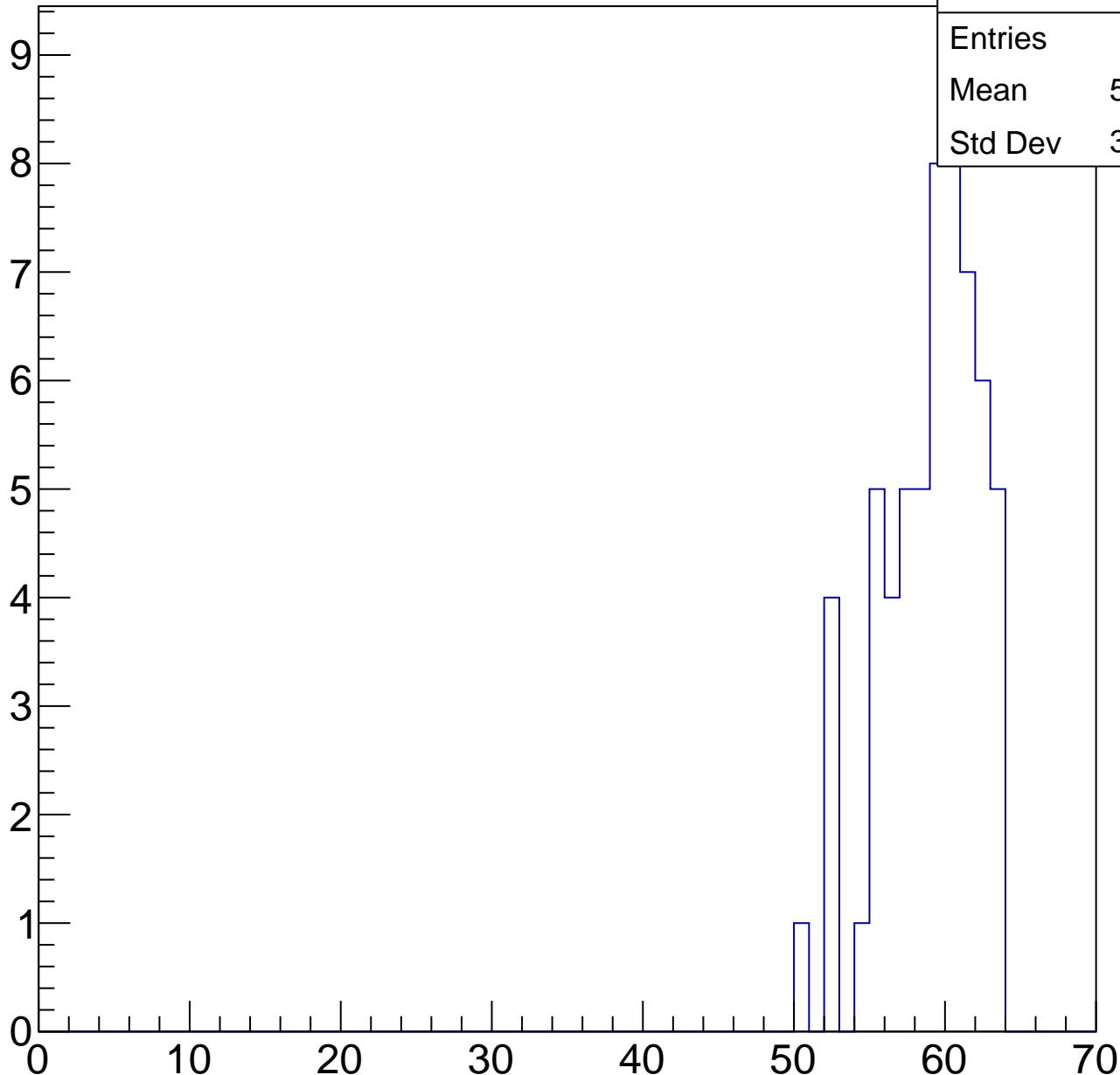
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.53
Std Dev	3.165

ampl

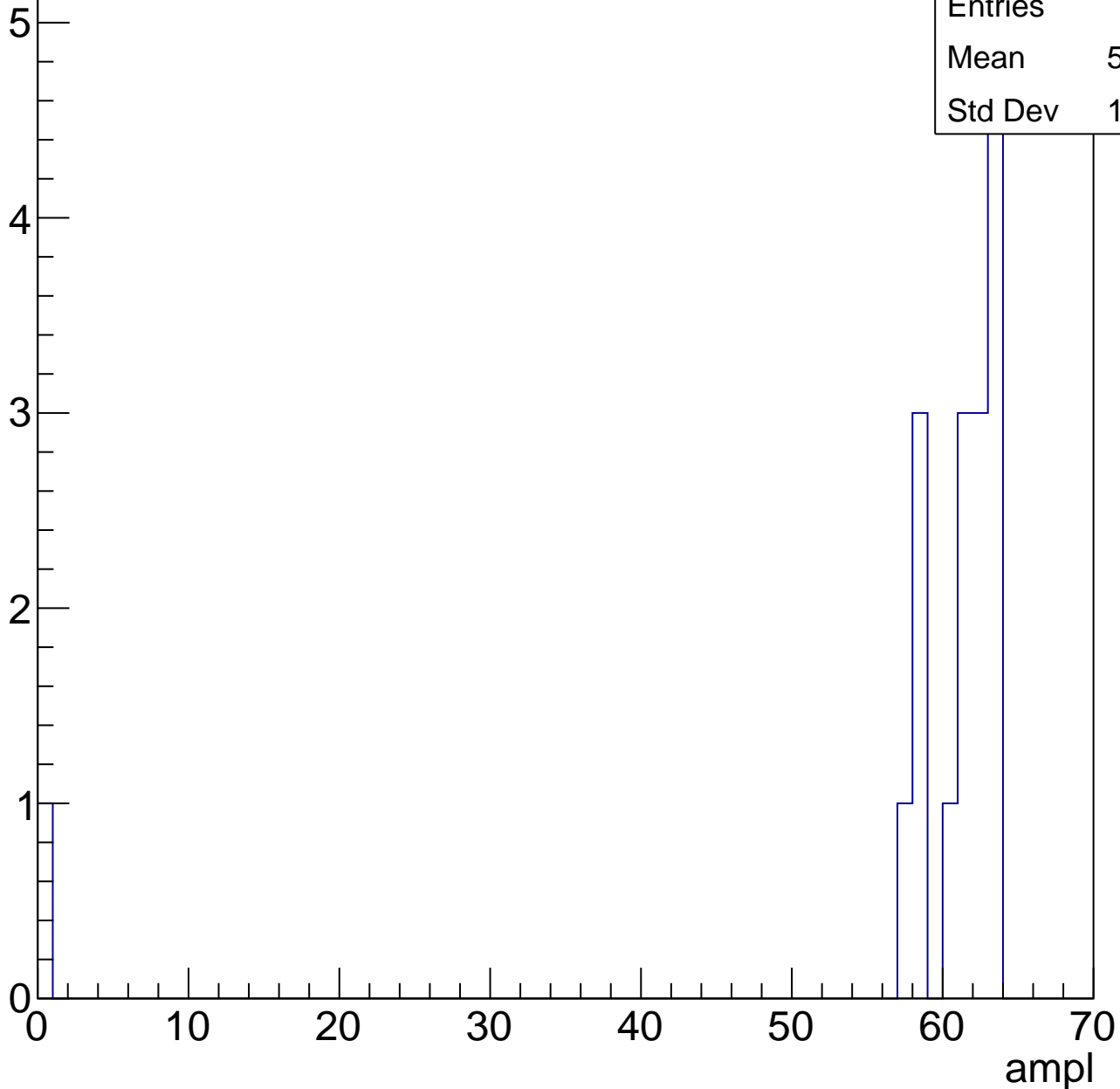


B1L103S, U5-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	57.35
Std Dev	14.47



B1L103S, U5-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch89, adc0

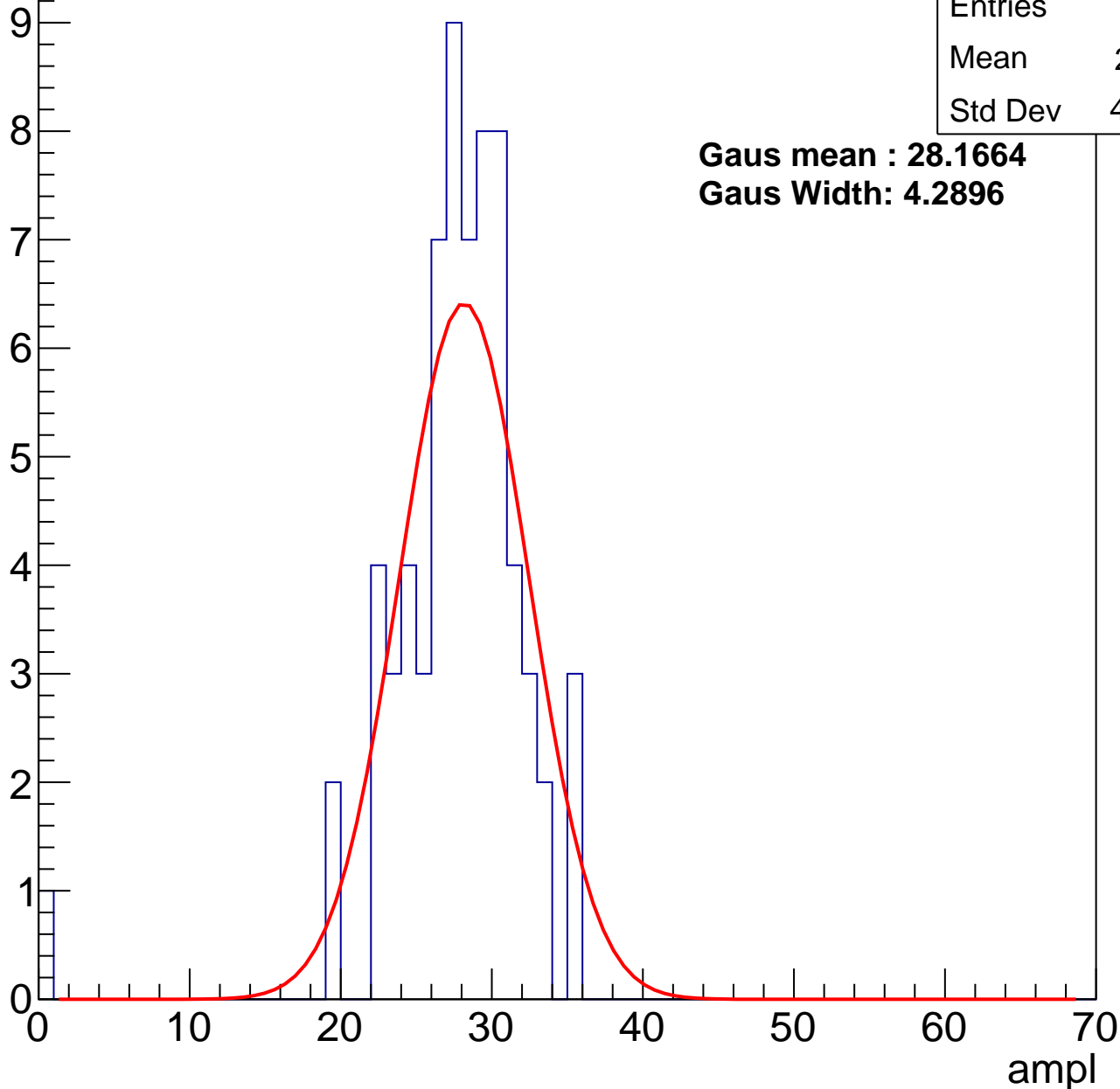
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.21
Std Dev	4.807

Gaus mean : 28.1664

Gaus Width: 4.2896



B1L103S, U5-ch89, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	35.09
Std Dev	3.757

Gaus mean : 35.5563

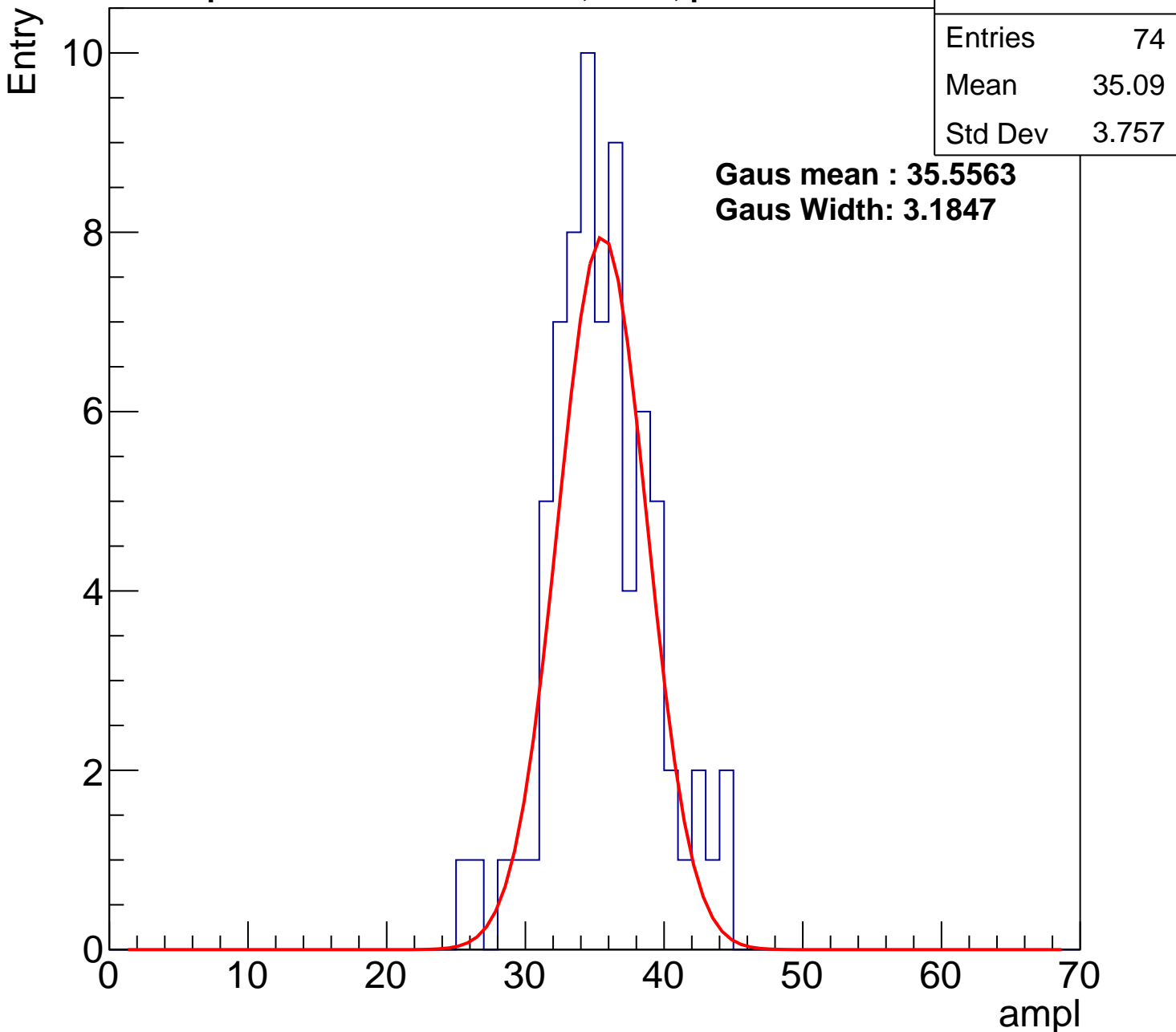
Gaus Width: 3.1847

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch89, adc2

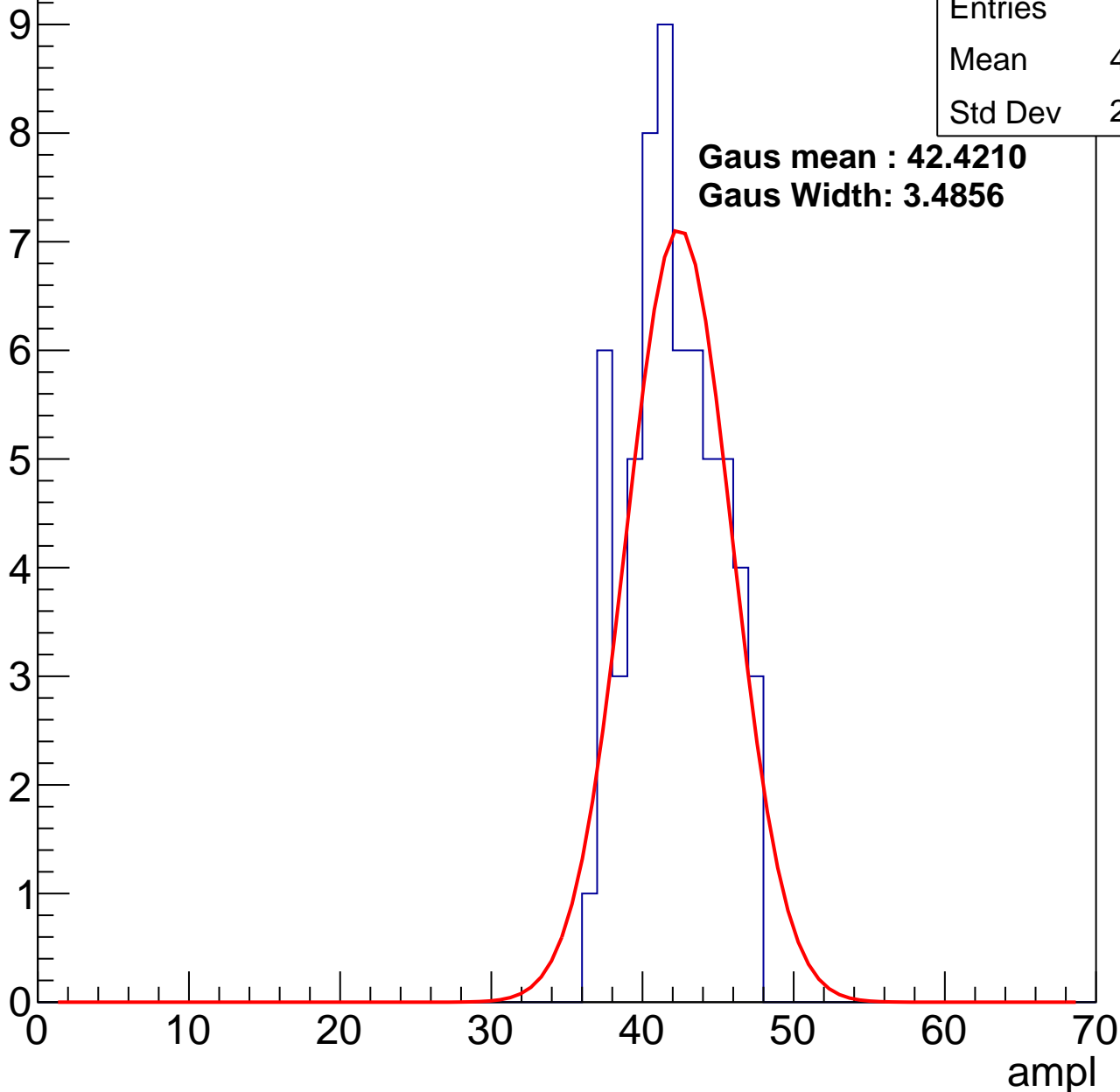
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.57
Std Dev	2.917

Gaus mean : 42.4210

Gaus Width: 3.4856

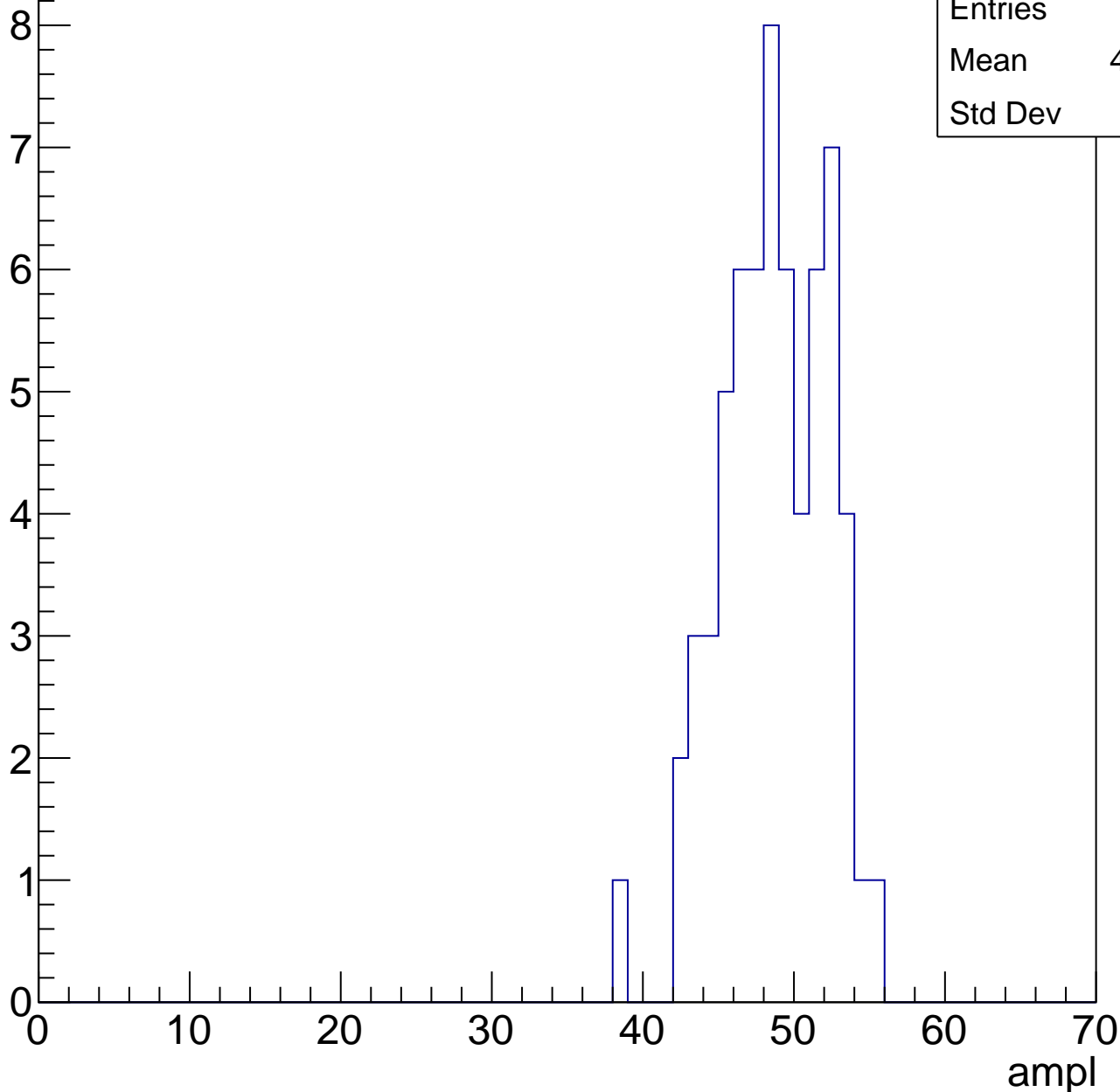


B1L103S, U5-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

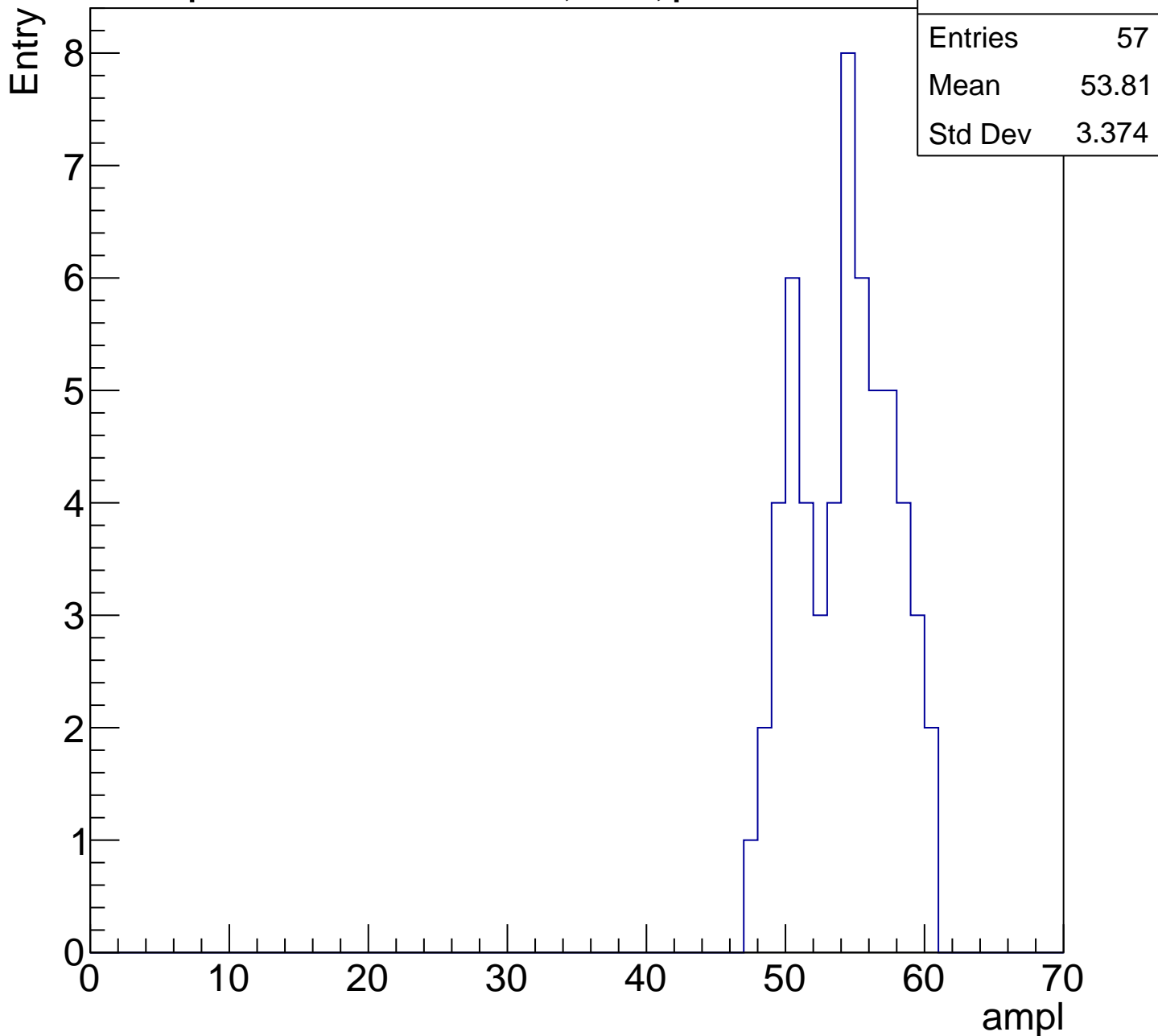
Entry

Entries	63
Mean	48.17
Std Dev	3.43



B1L103S, U5-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch89, adc5

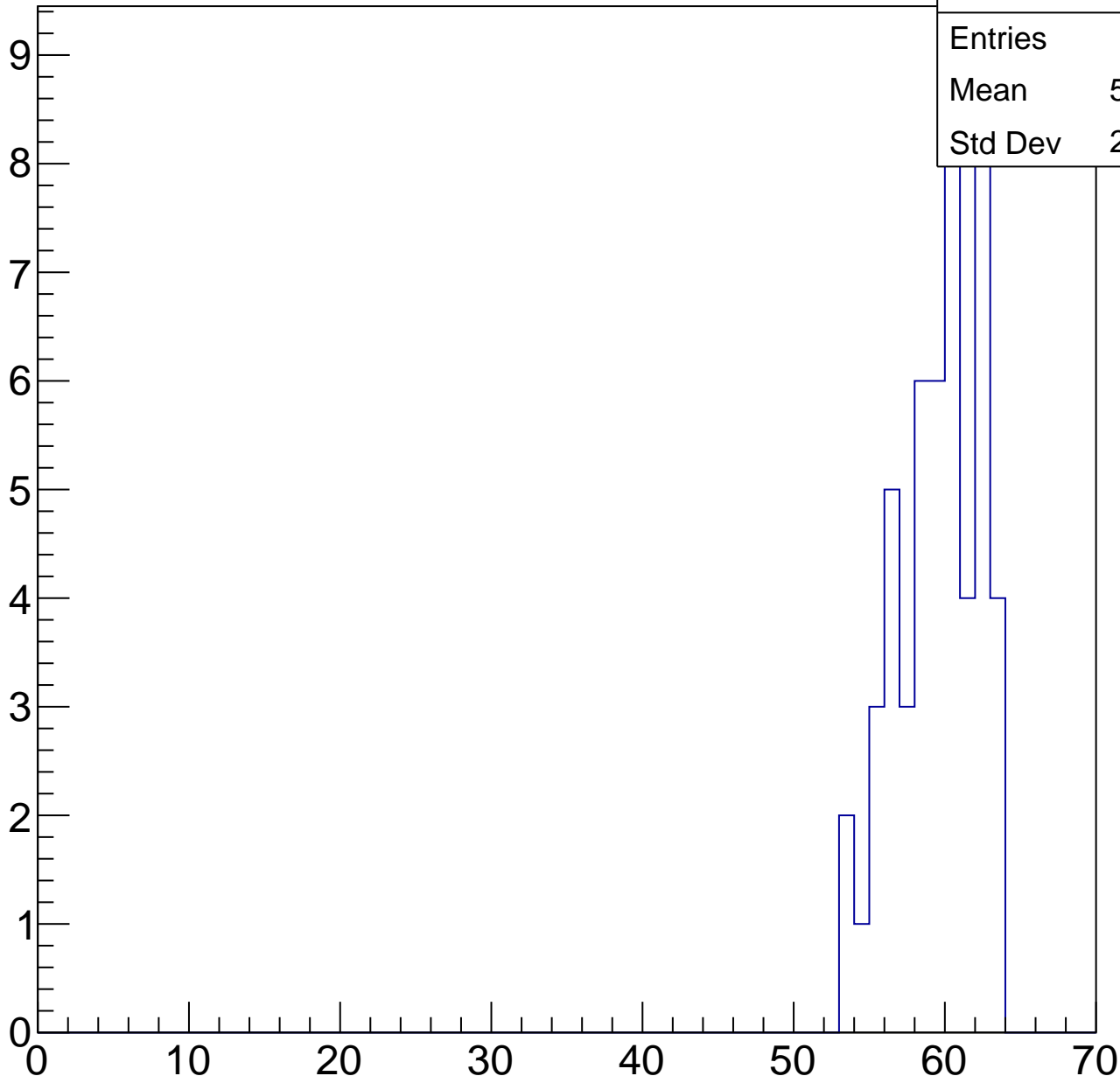
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.06
Std Dev	2.718

ampl

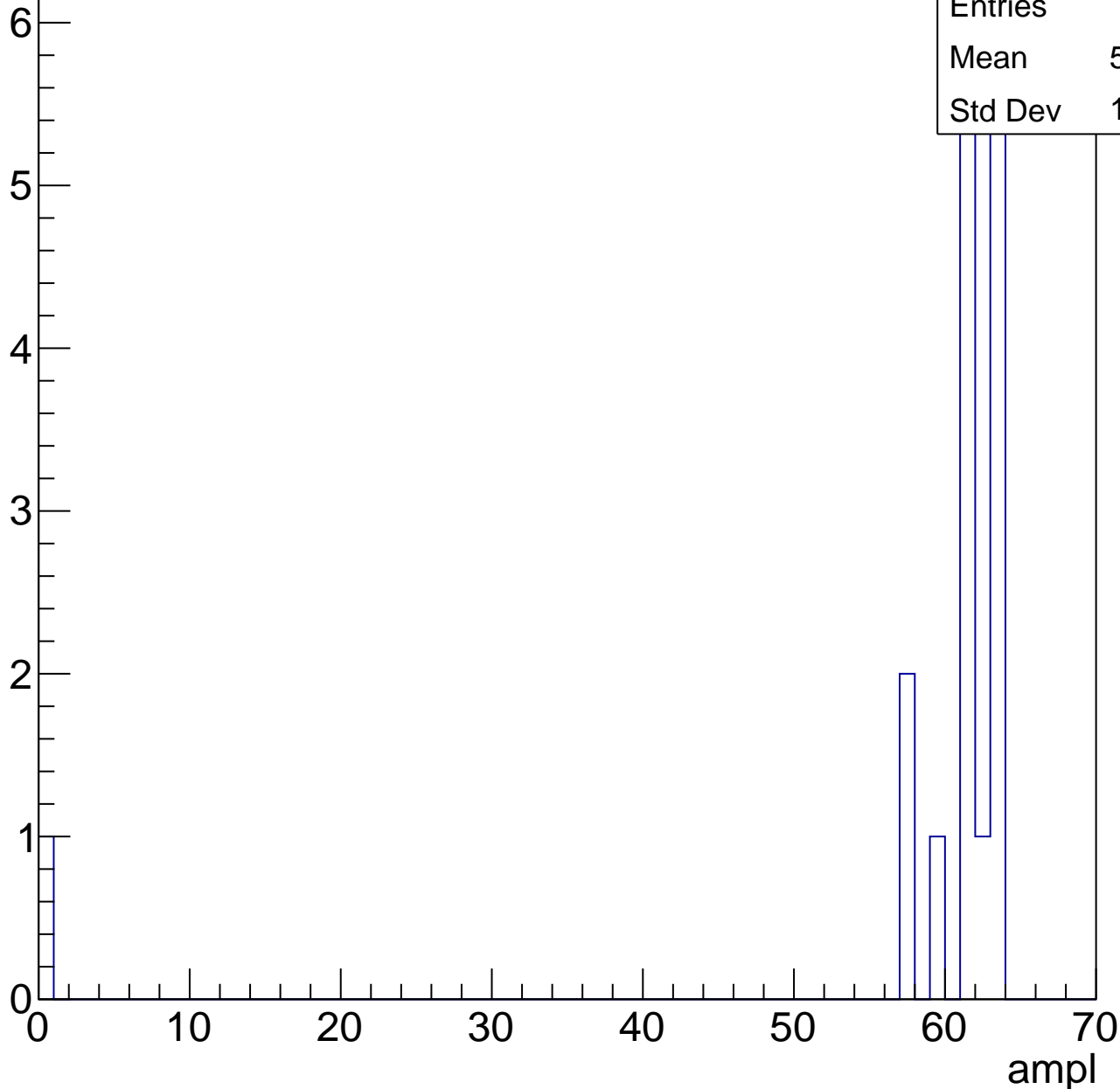


B1L103S, U5-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	57.59
Std Dev	14.52



B1L103S, U5-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch90, adc0

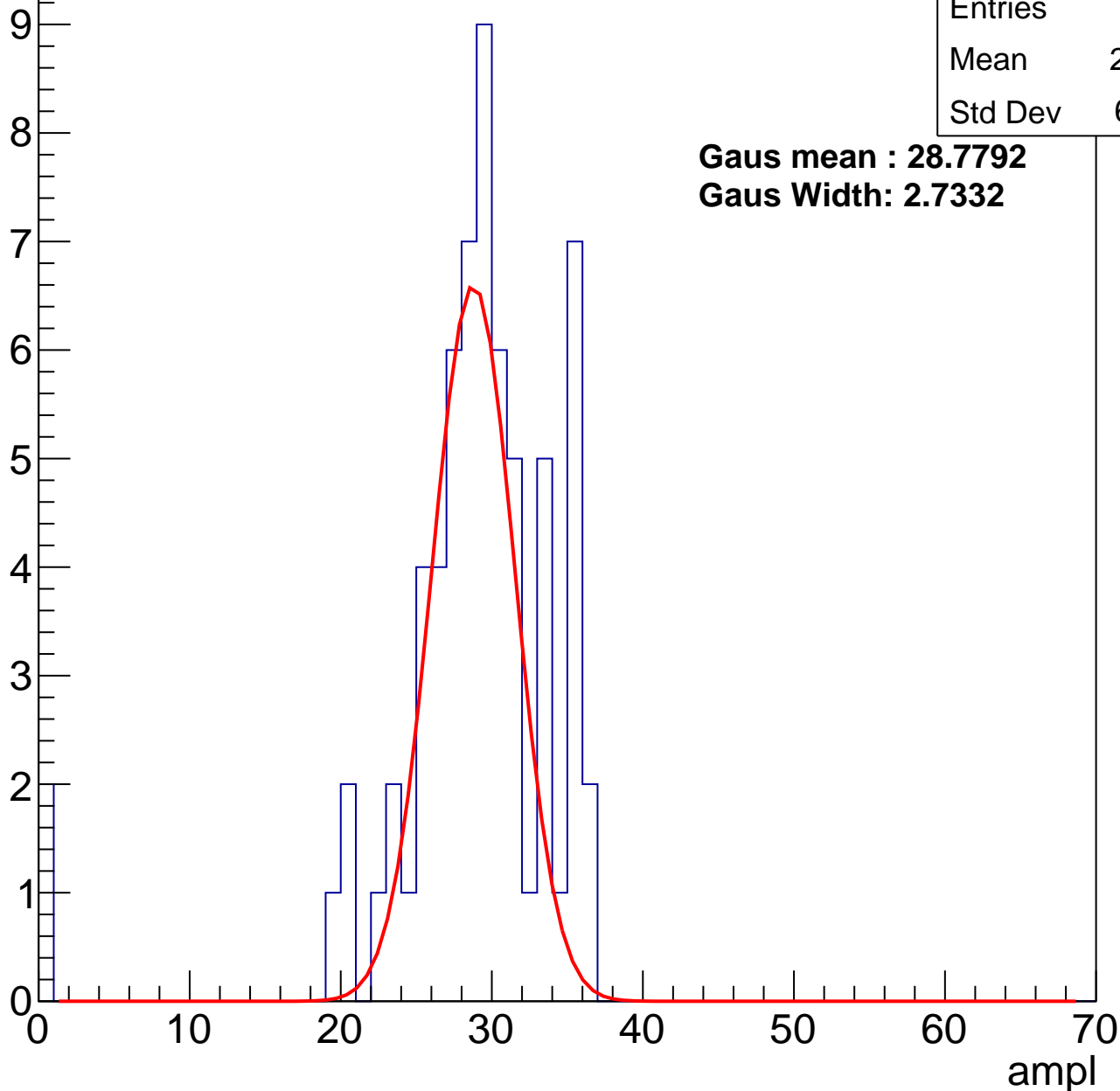
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.14
Std Dev	6.351

Gaus mean : 28.7792

Gaus Width: 2.7332



B1L103S, U5-ch90, adc1

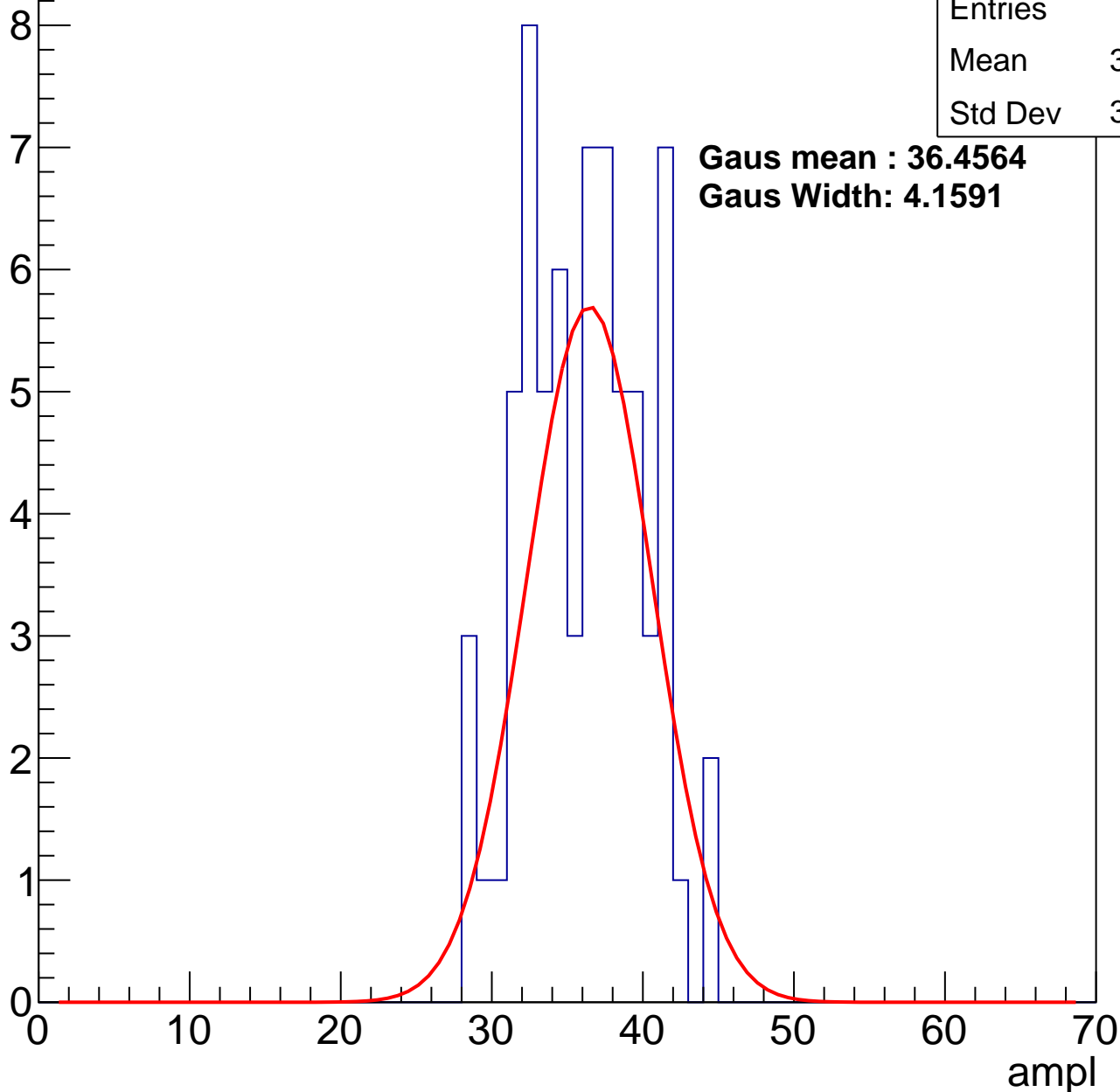
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.67
Std Dev	3.907

Gaus mean : 36.4564

Gaus Width: 4.1591



B1L103S, U5-ch90, adc2

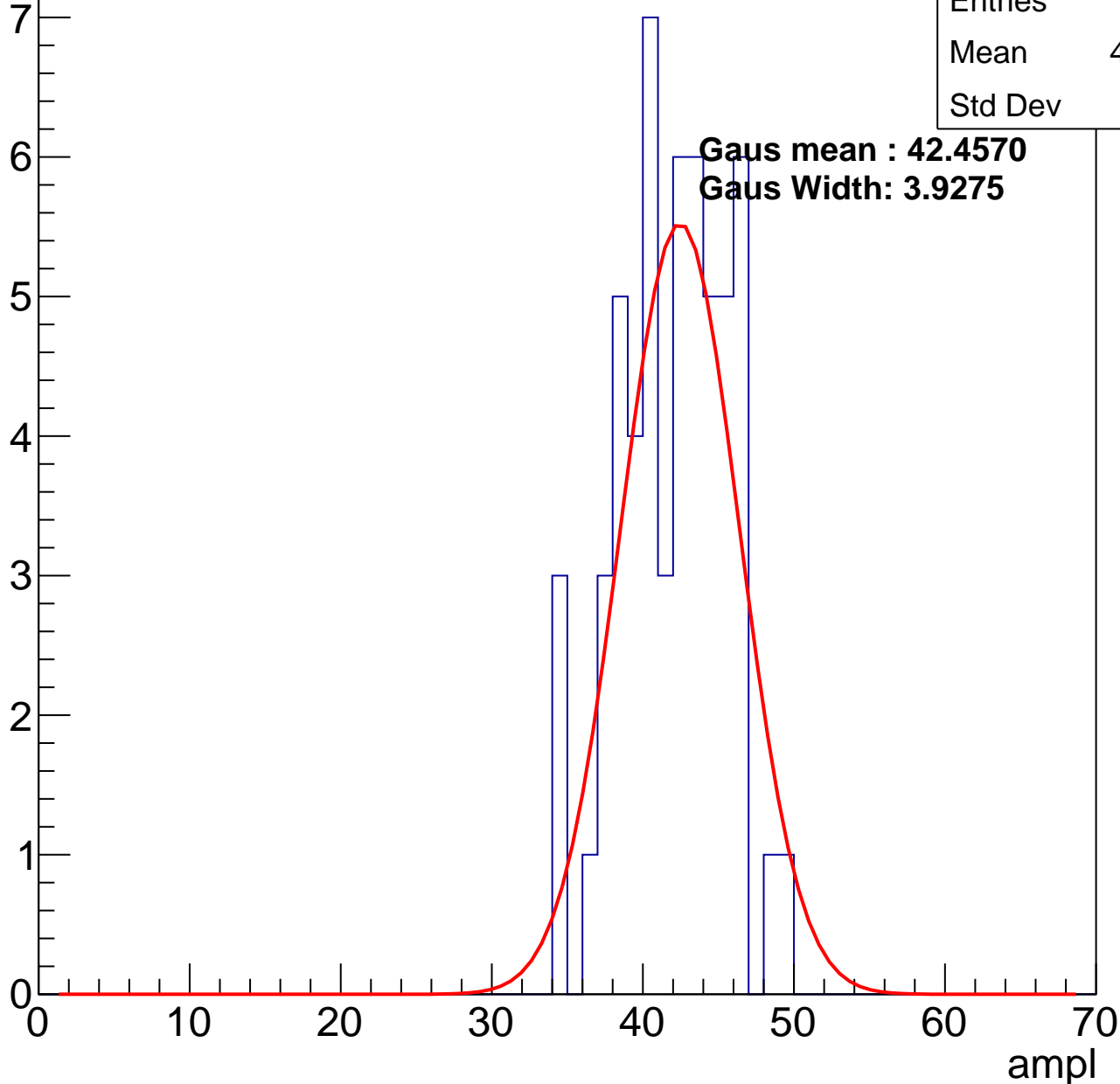
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	41.54
Std Dev	3.51

Gaus mean : 42.4570

Gaus Width: 3.9275

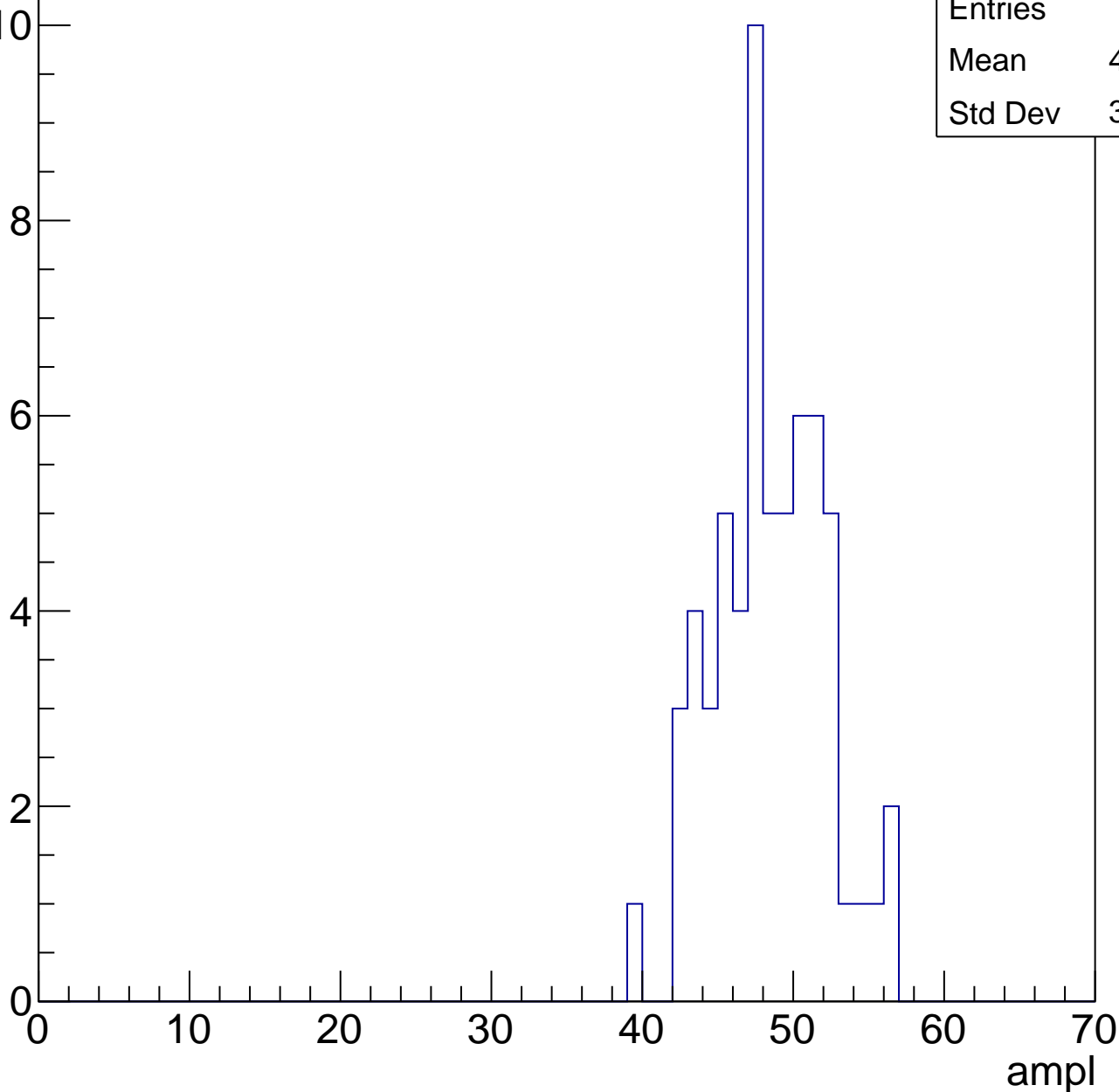


B1L103S, U5-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	47.95
Std Dev	3.612

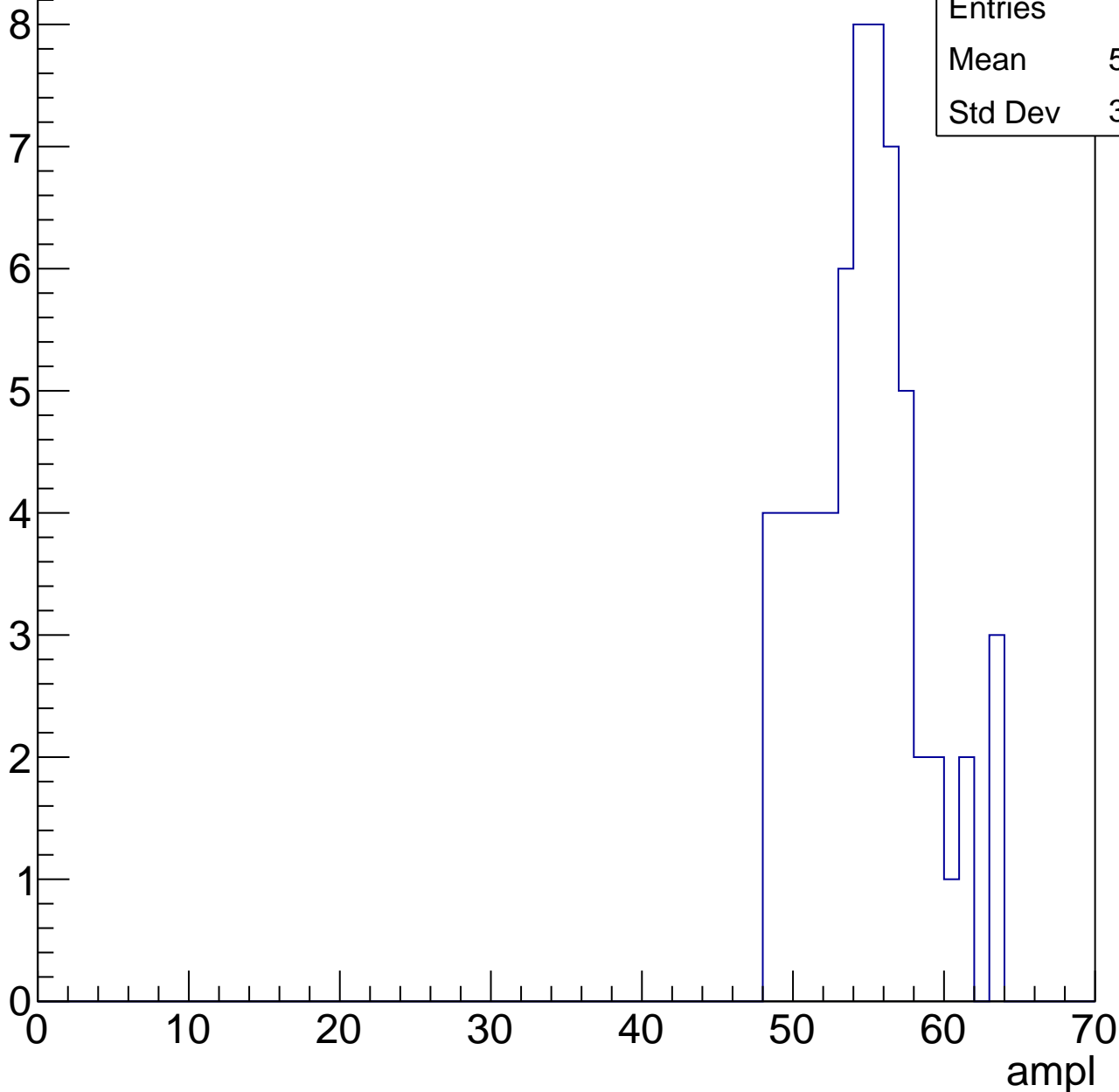


B1L103S, U5-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

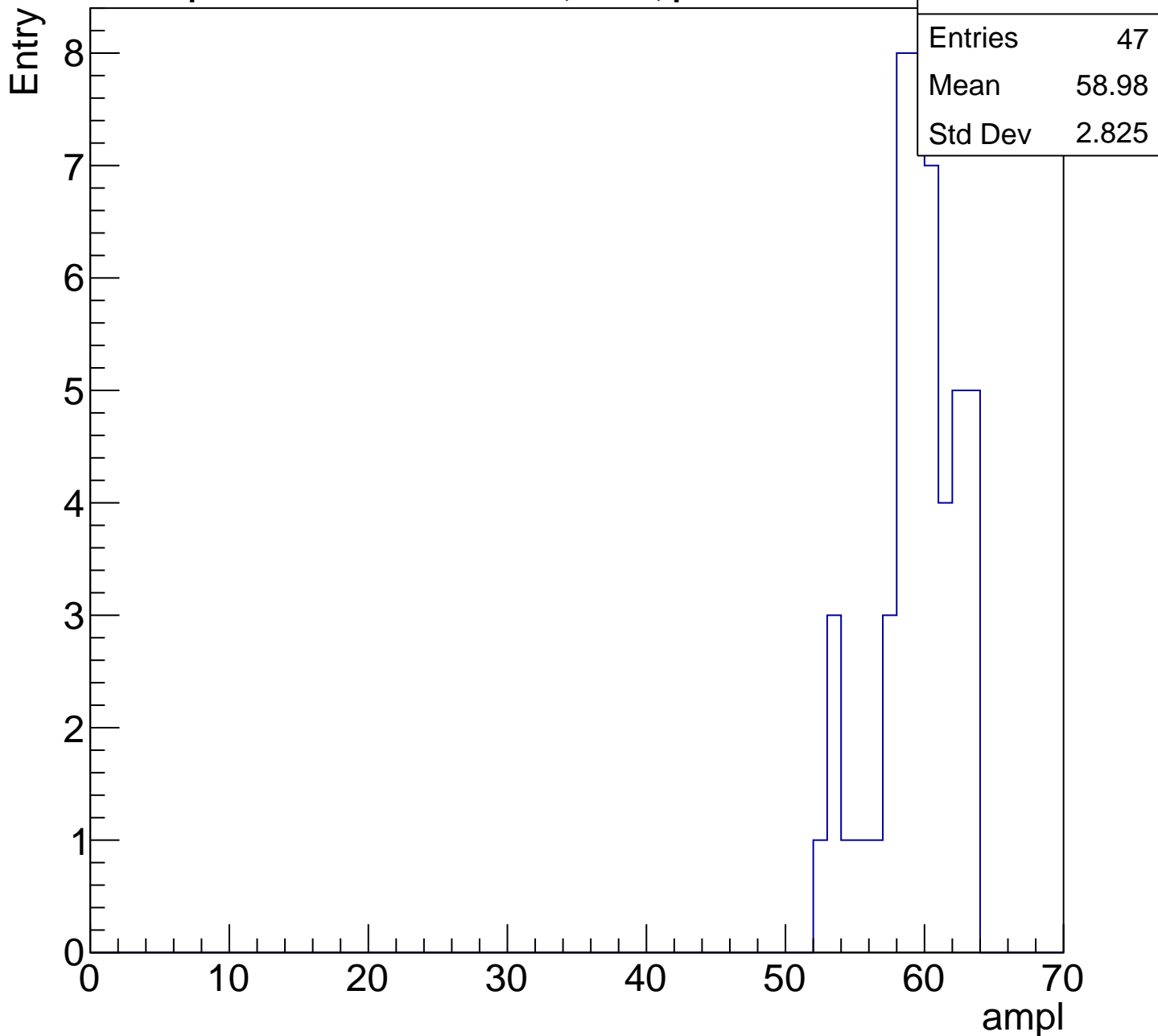
Entry

Entries	64
Mean	54.25
Std Dev	3.754



B1L103S, U5-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

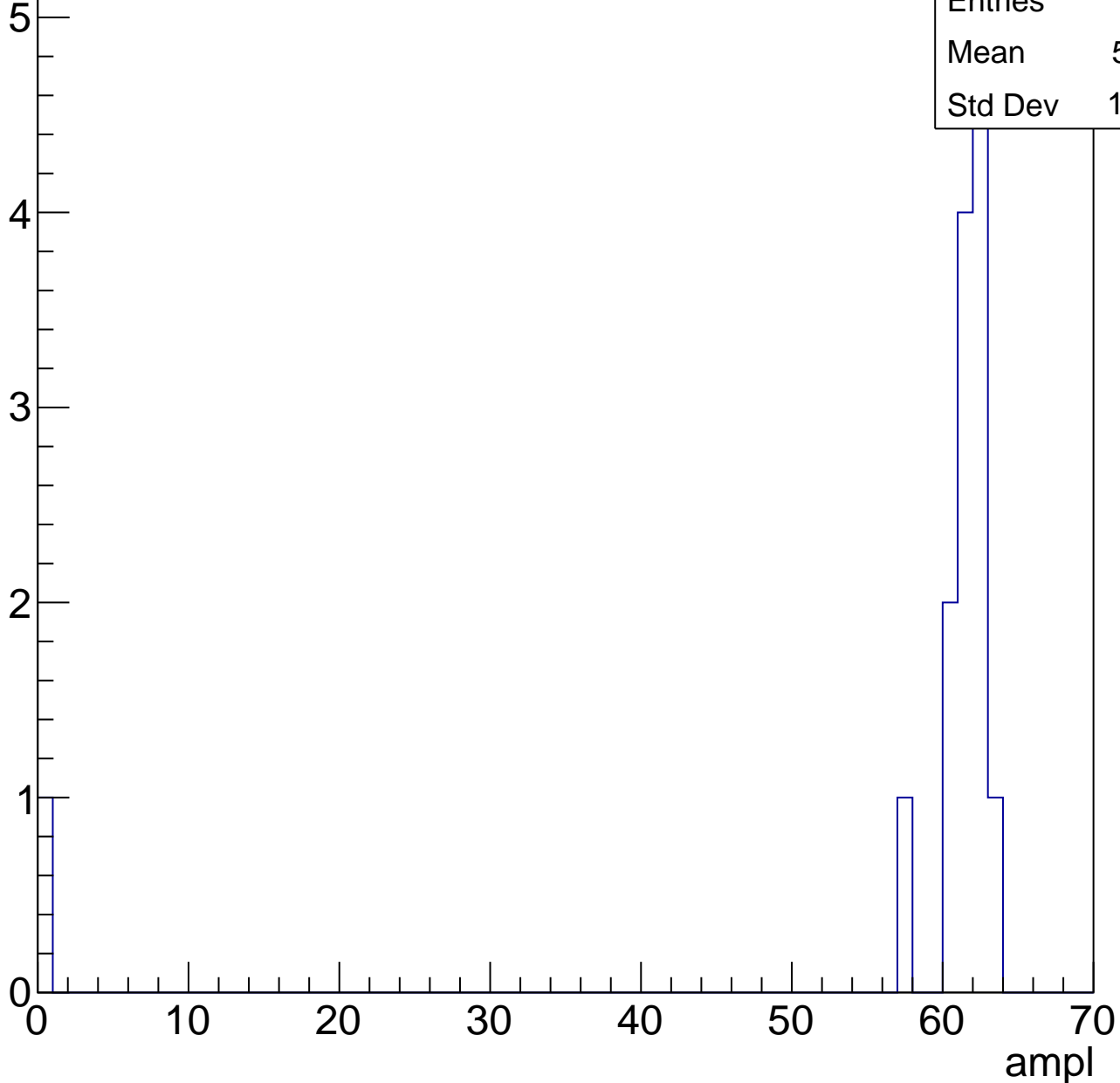


B1L103S, U5-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

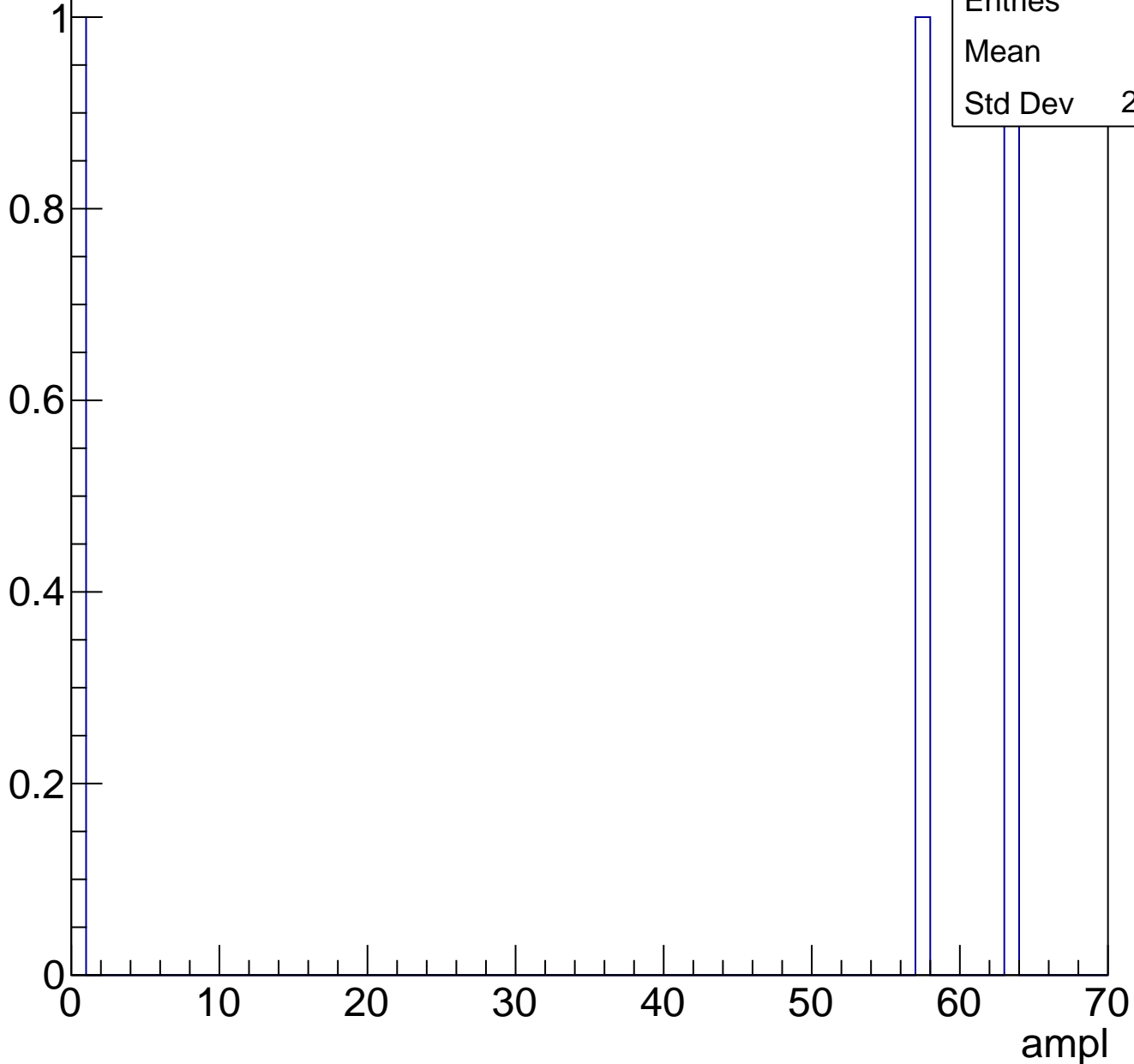
Entries	14
Mean	56.71
Std Dev	15.79



B1L103S, U5-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch91, adc0

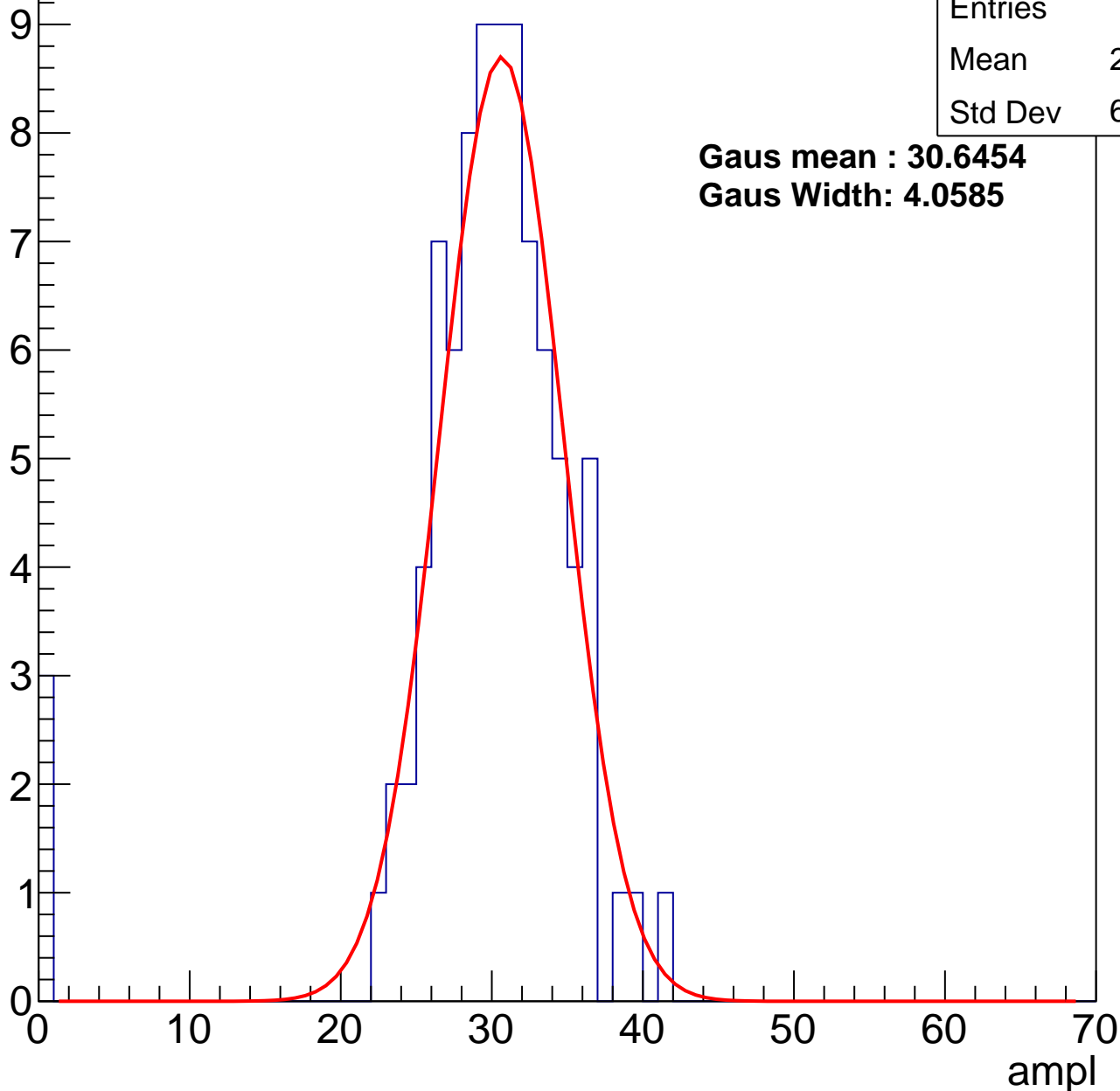
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	29.16
Std Dev	6.578

Gaus mean : 30.6454

Gaus Width: 4.0585



B1L103S, U5-ch91, adc1

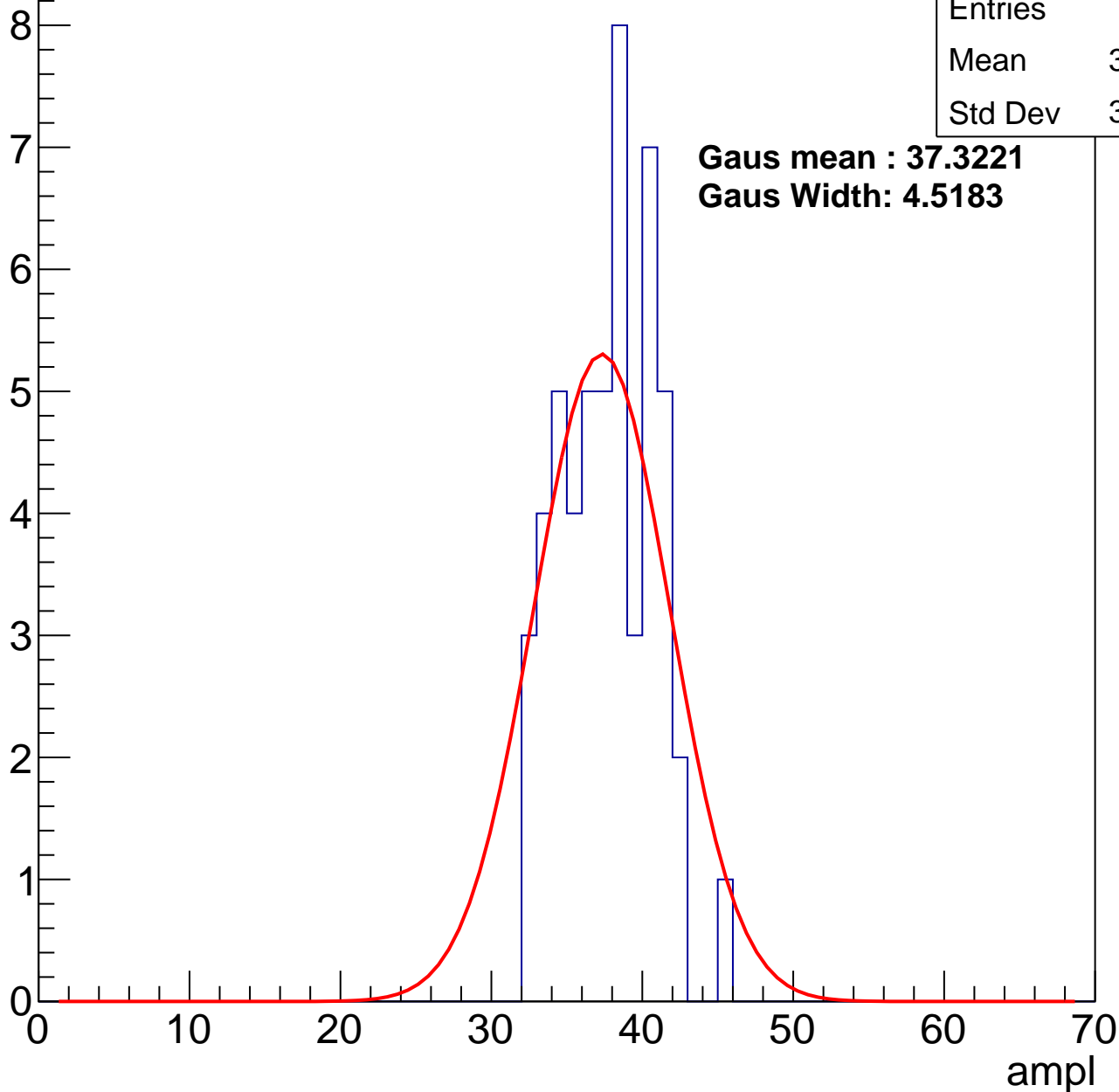
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	37.27
Std Dev	3.033

Gaus mean : 37.3221

Gaus Width: 4.5183



B1L103S, U5-ch91, adc2

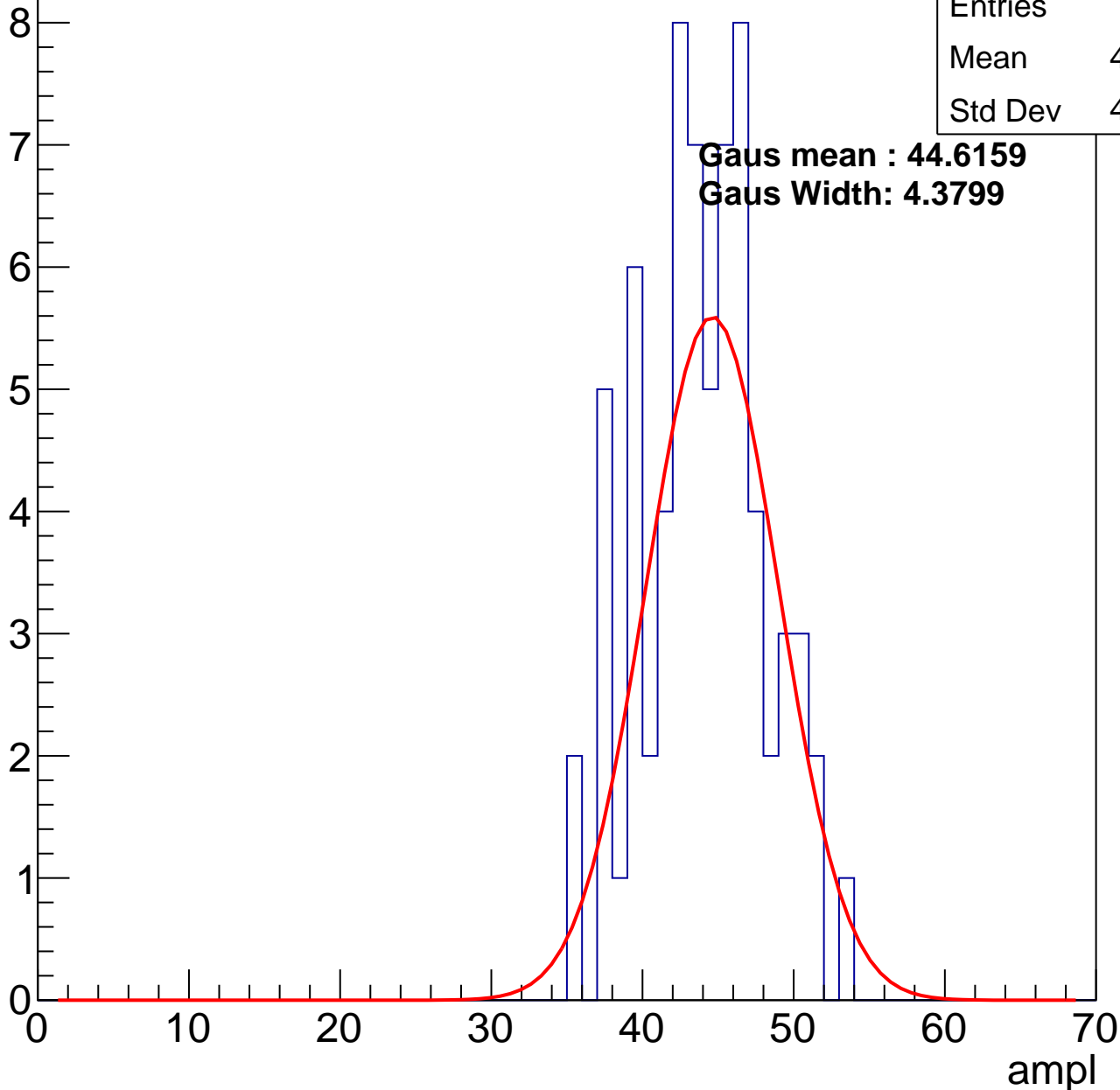
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.53
Std Dev	4.059

Gaus mean : 44.6159

Gaus Width: 4.3799

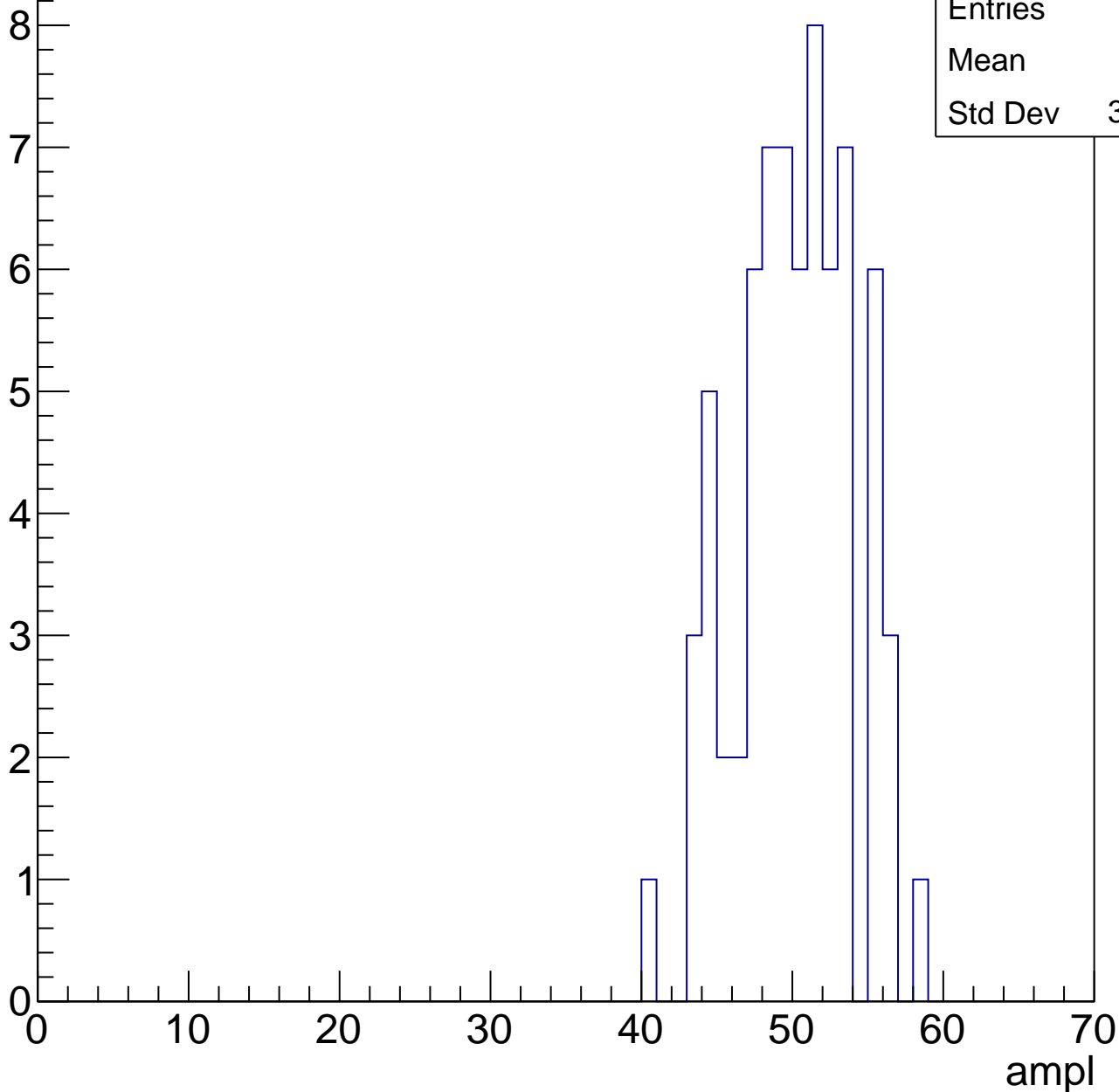


B1L103S, U5-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.7
Std Dev	3.796

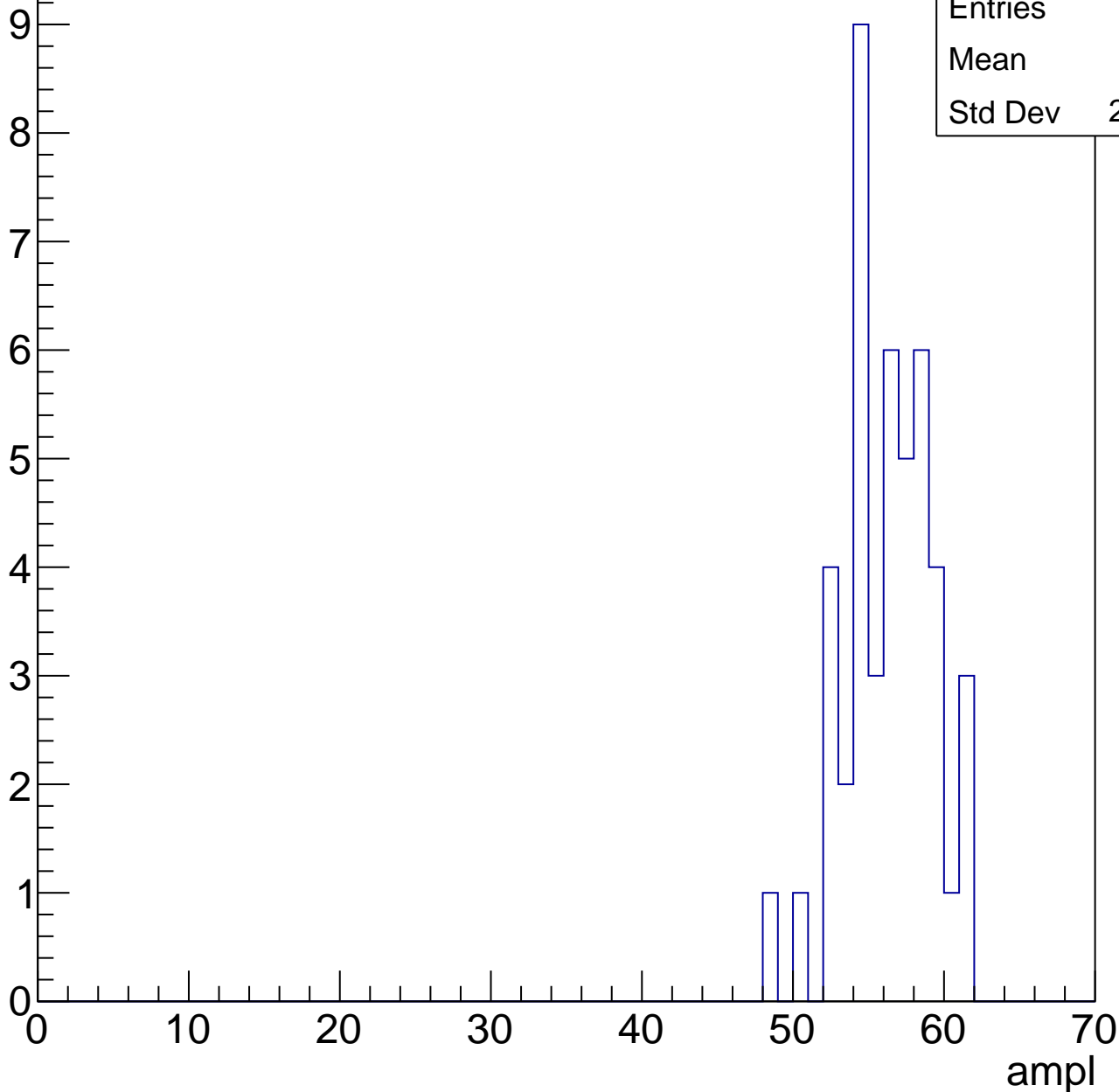


B1L103S, U5-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

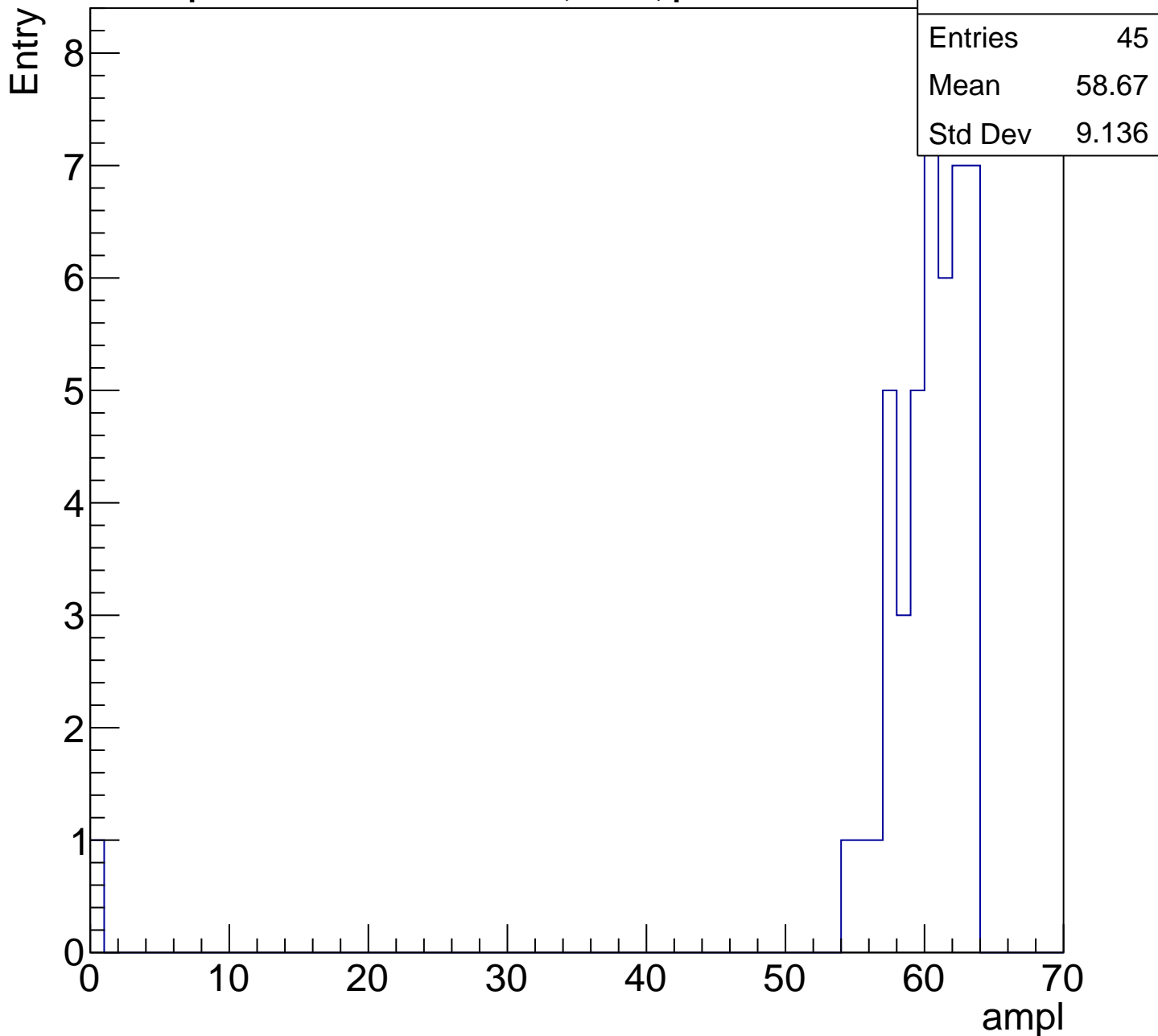
Entry

Entries	45
Mean	55.8
Std Dev	2.888



B1L103S, U5-ch91, adc5

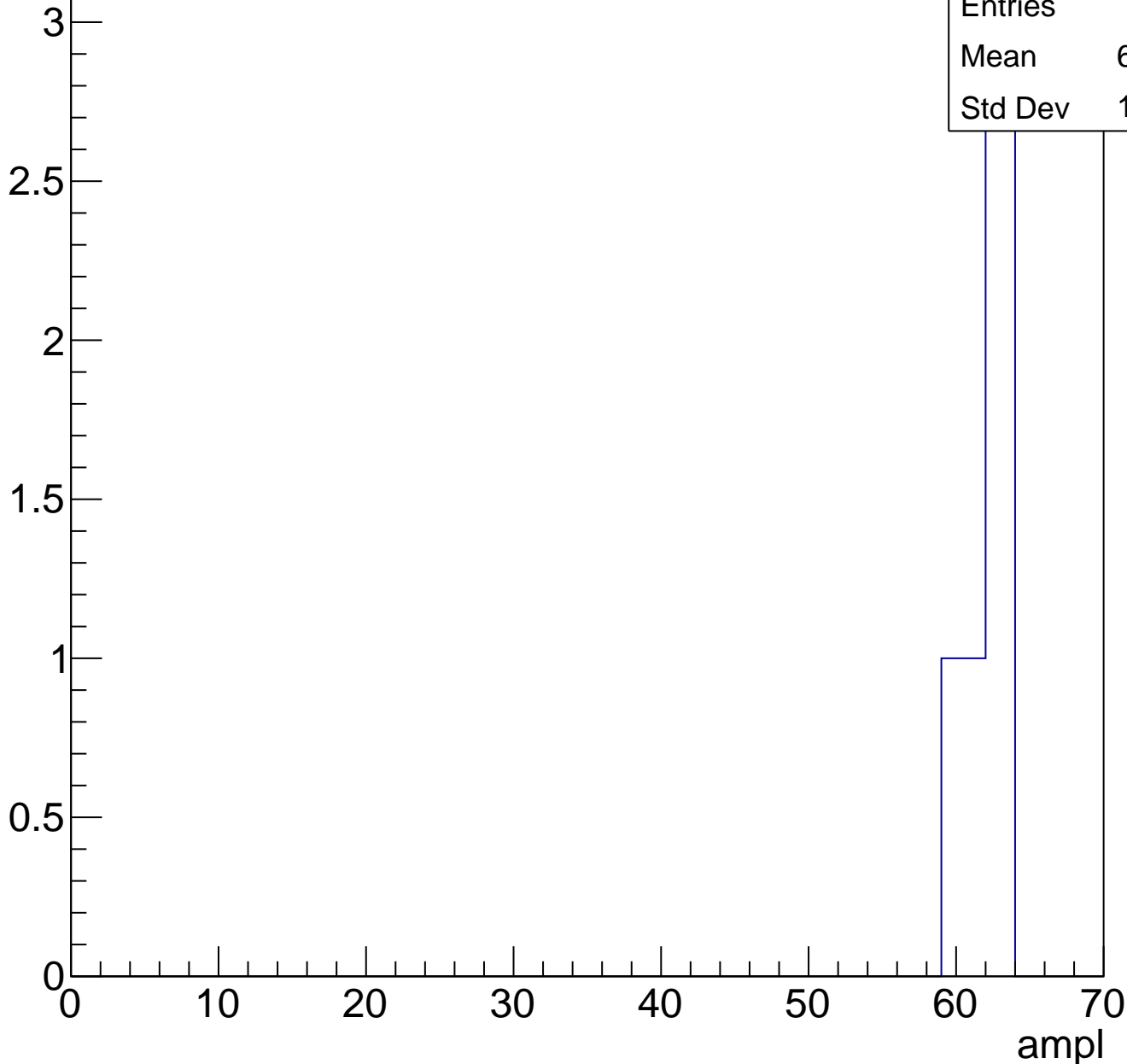
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch92, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	27.06
Std Dev	4.621

Gaus mean : 27.7454

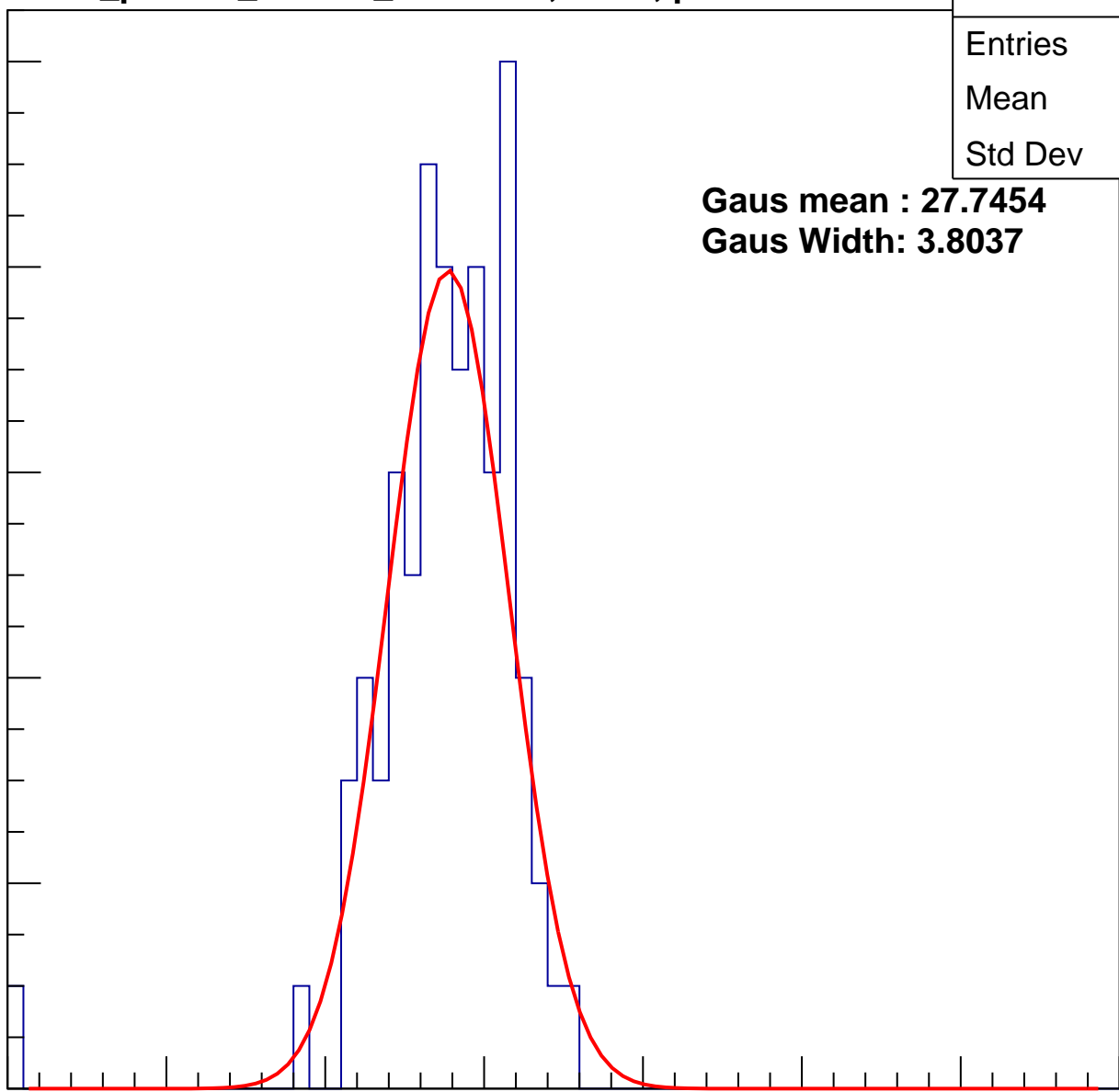
Gaus Width: 3.8037

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch92, adc1

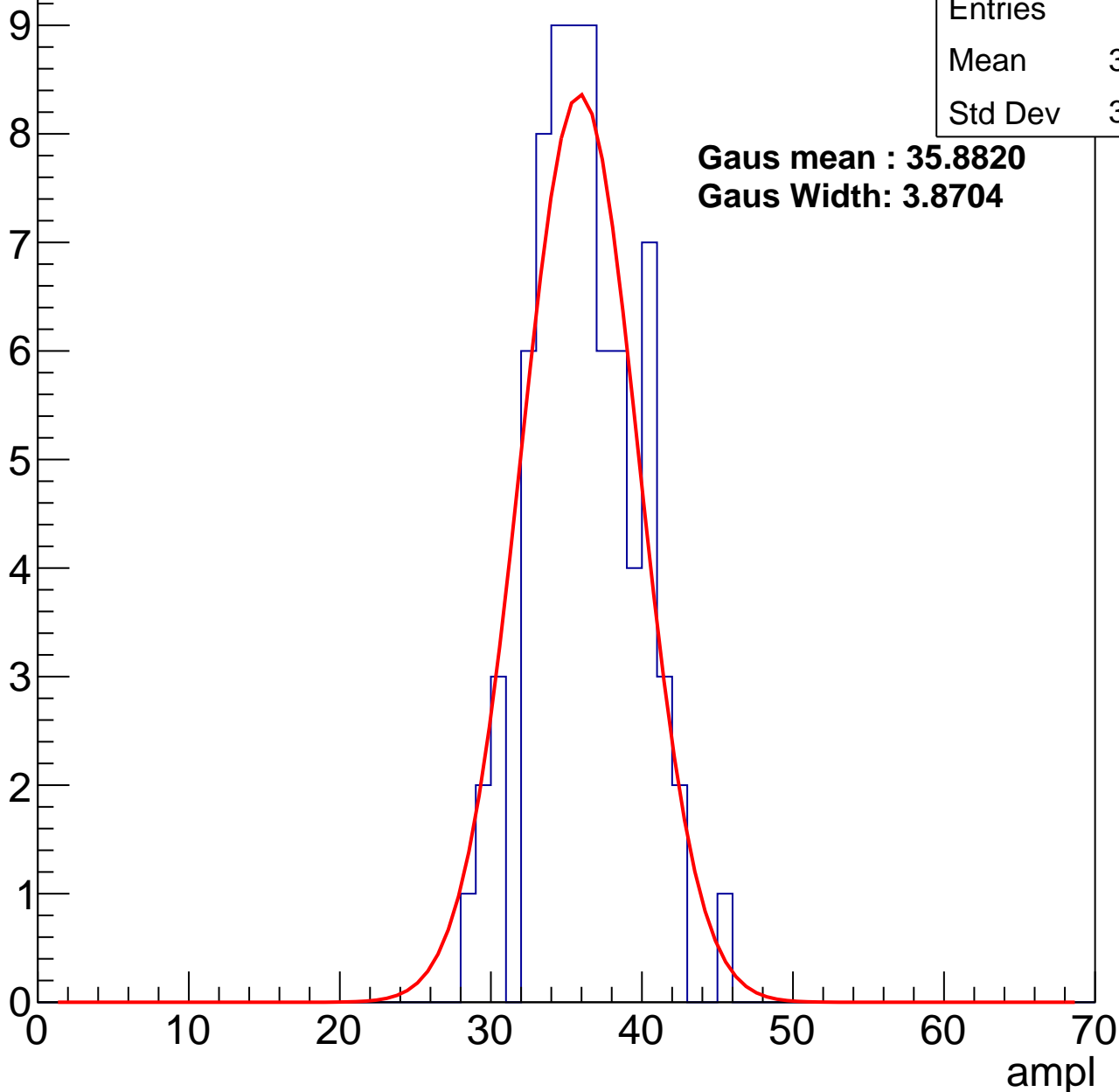
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.72
Std Dev	3.409

Gaus mean : 35.8820

Gaus Width: 3.8704



B1L103S, U5-ch92, adc2

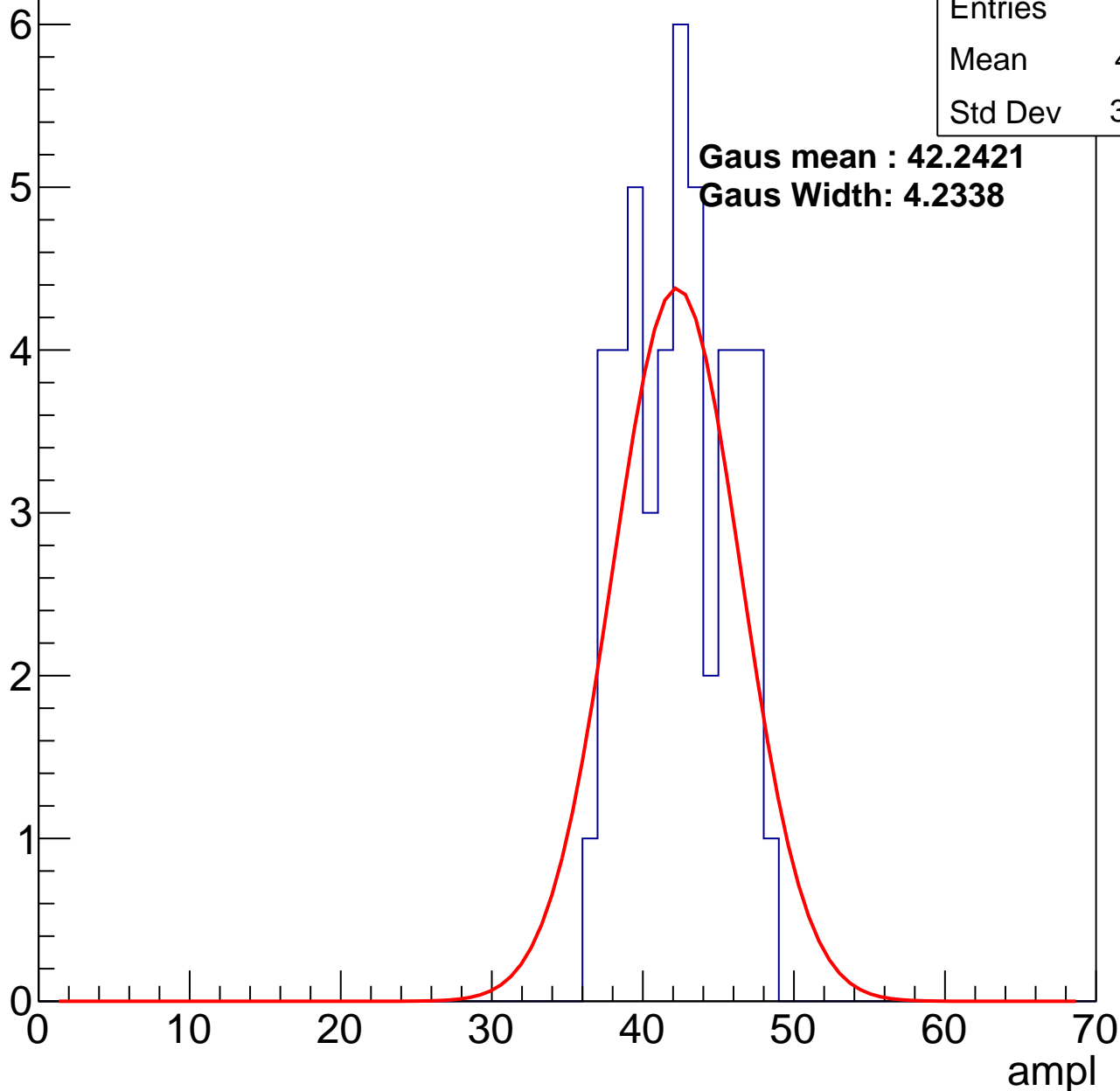
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	41.91
Std Dev	3.293

Gaus mean : 42.2421

Gaus Width: 4.2338

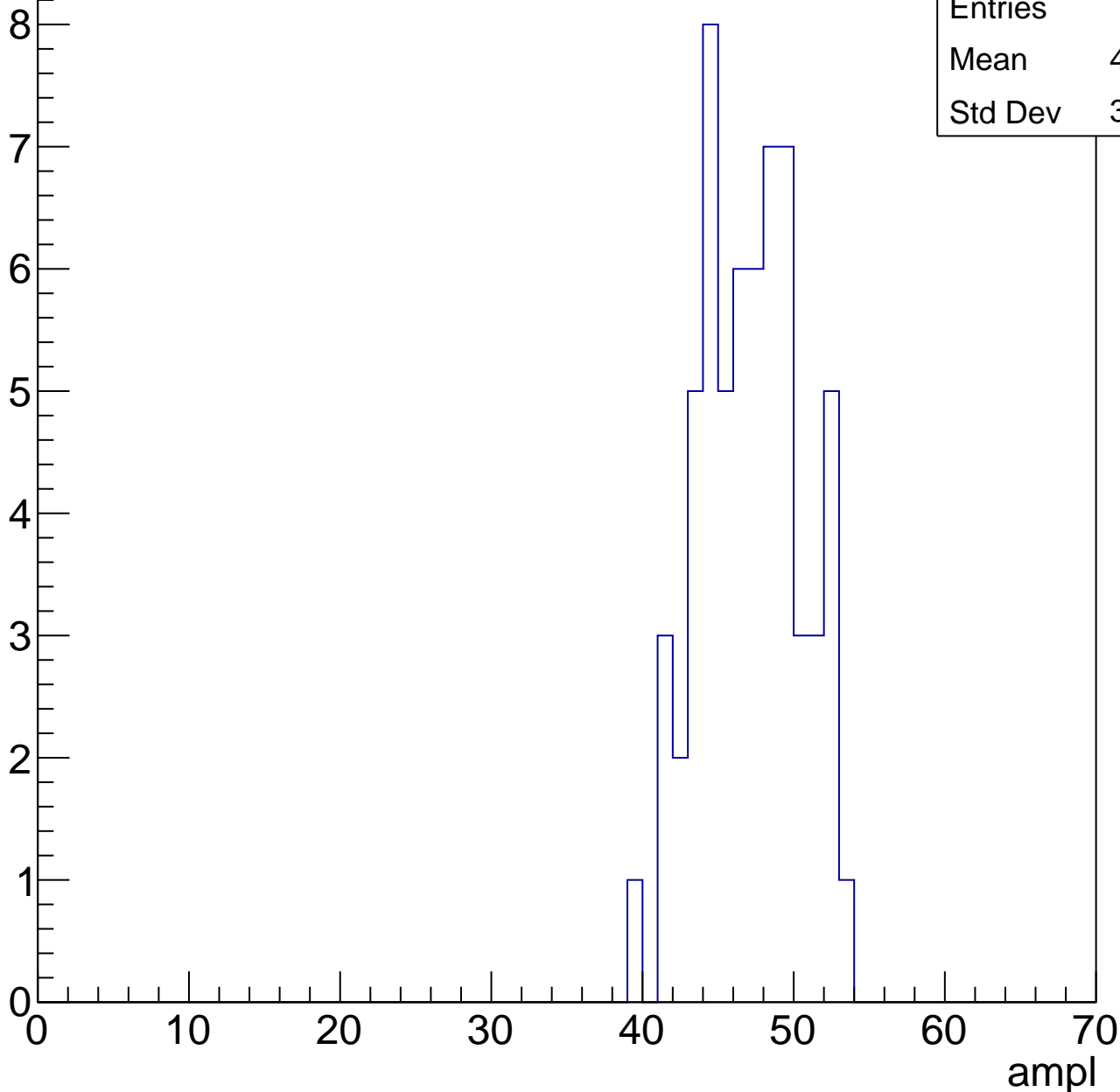


B1L103S, U5-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	46.63
Std Dev	3.269

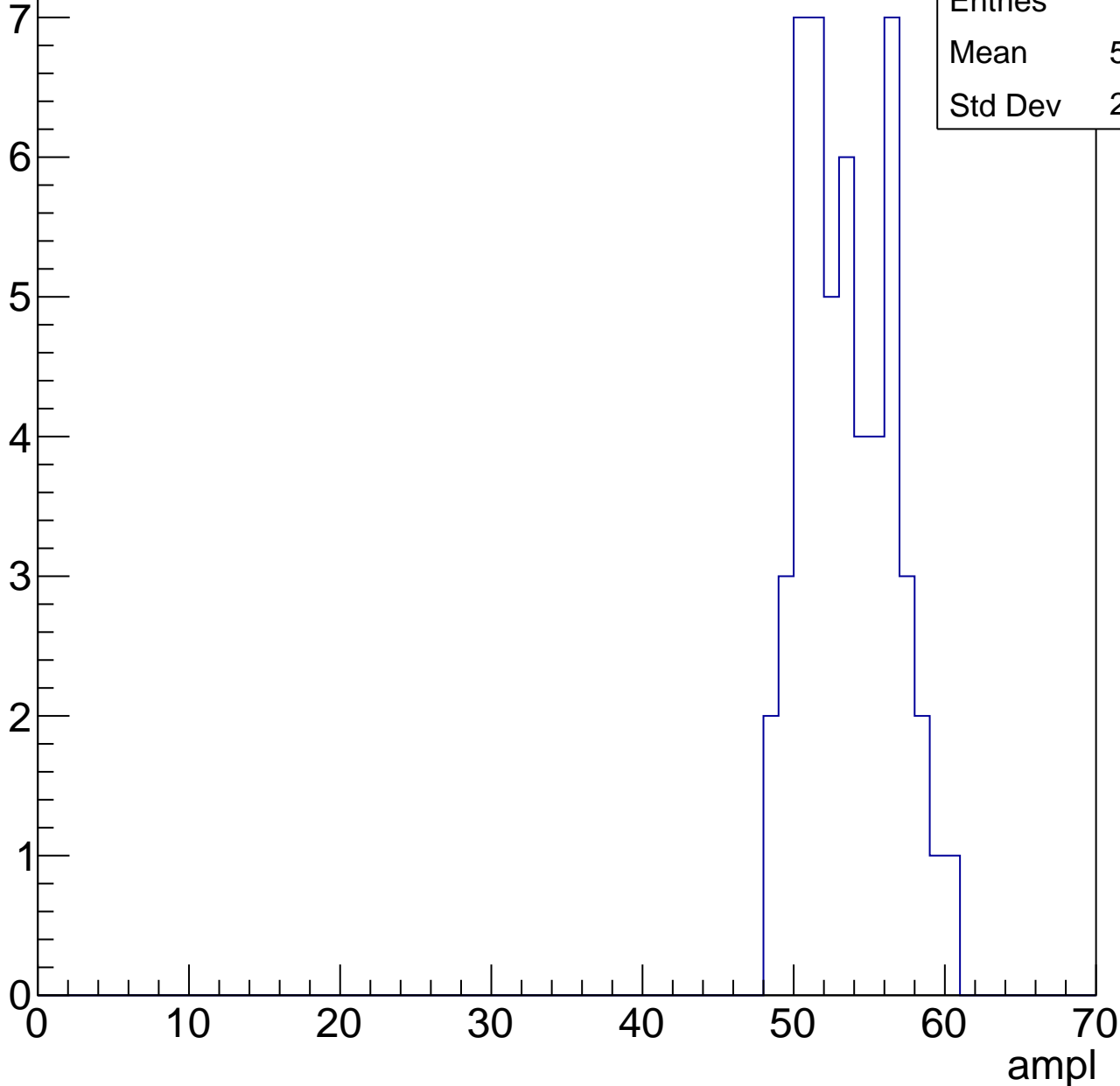


B1L103S, U5-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	53.12
Std Dev	2.972

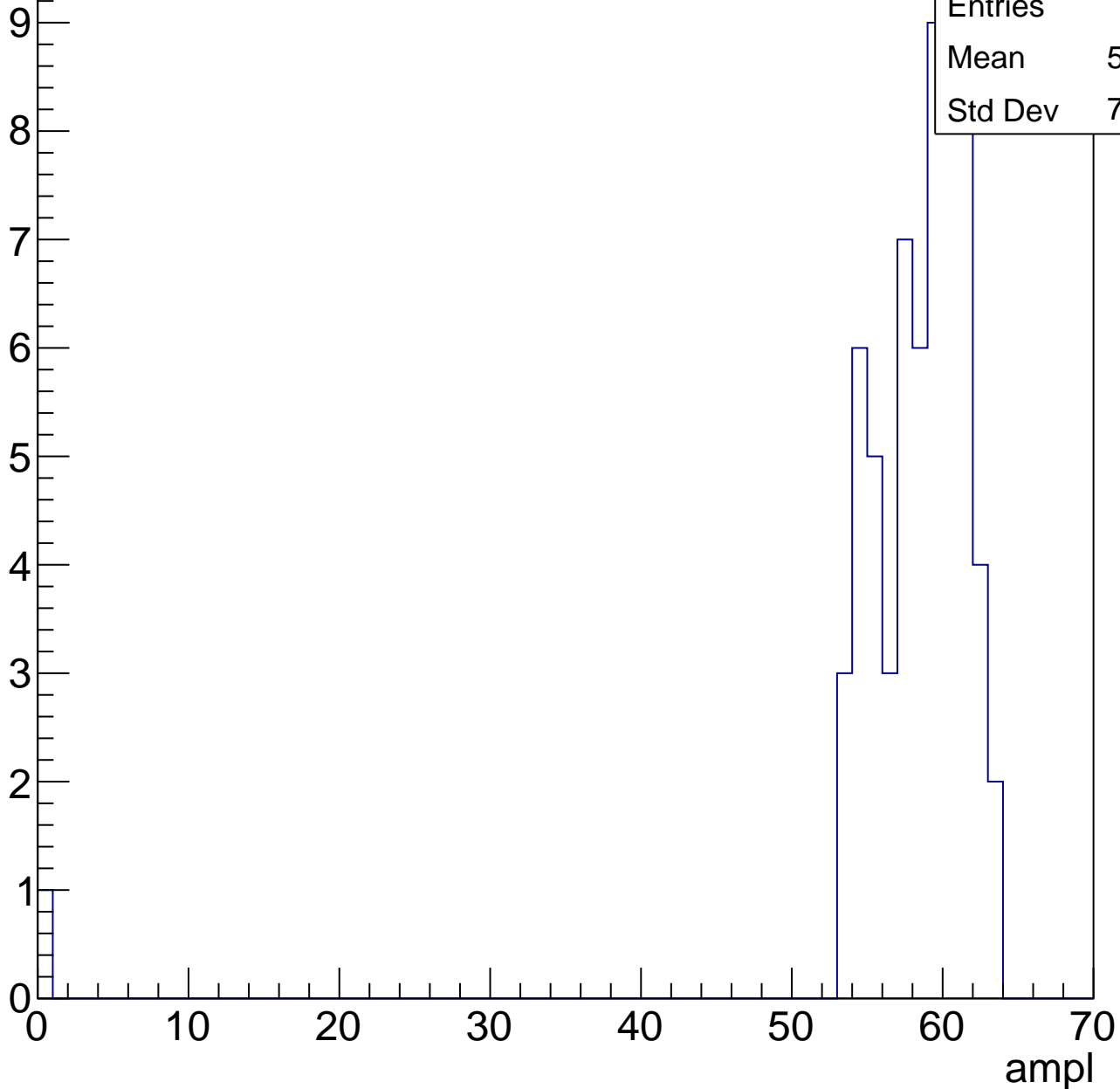


B1L103S, U5-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	57.19
Std Dev	7.814

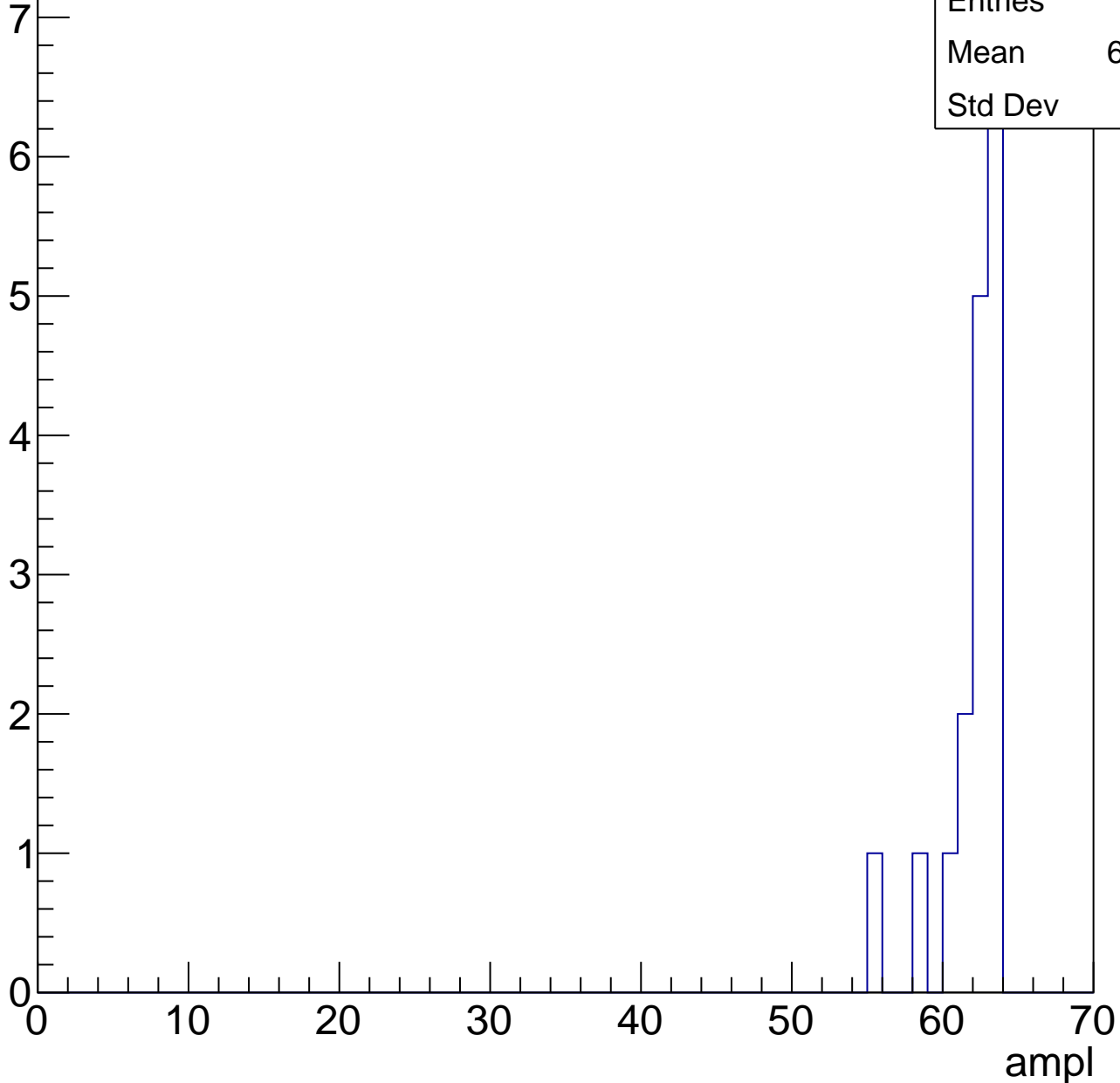


B1L103S, U5-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

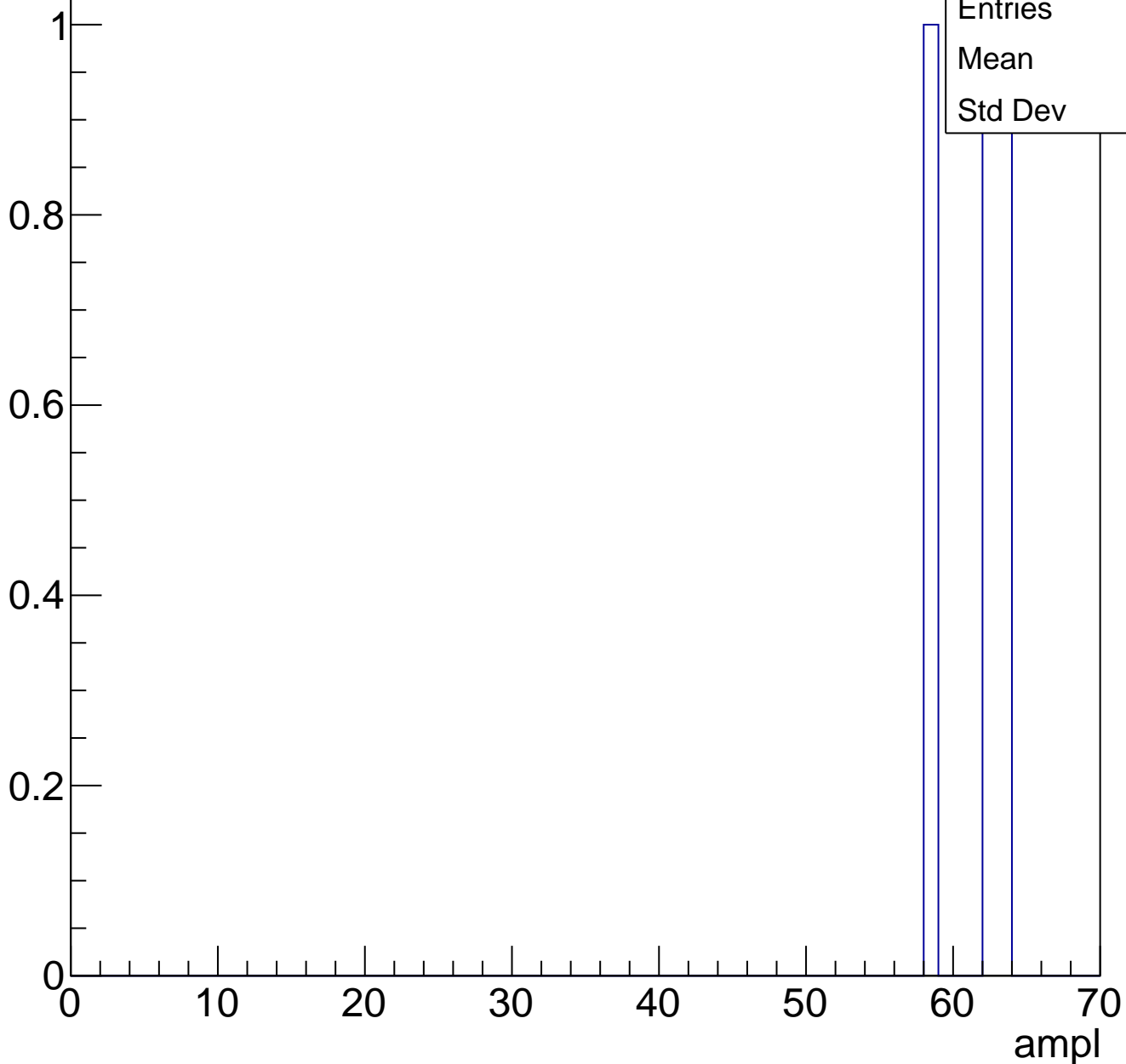
Entries	17
Mean	61.53
Std Dev	2.09



B1L103S, U5-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch93, adc0

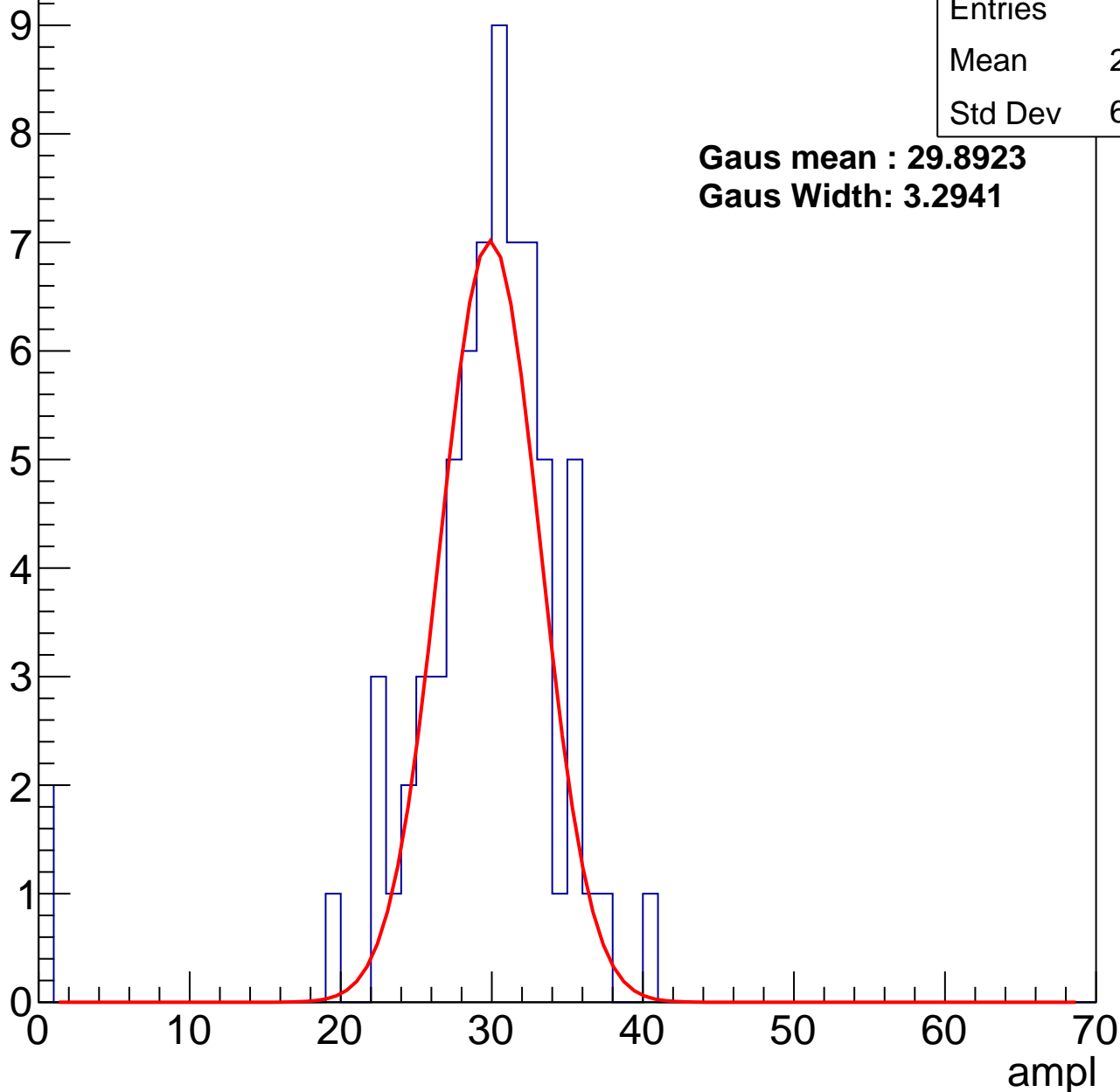
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.76
Std Dev	6.246

Gaus mean : 29.8923

Gaus Width: 3.2941



B1L103S, U5-ch93, adc1

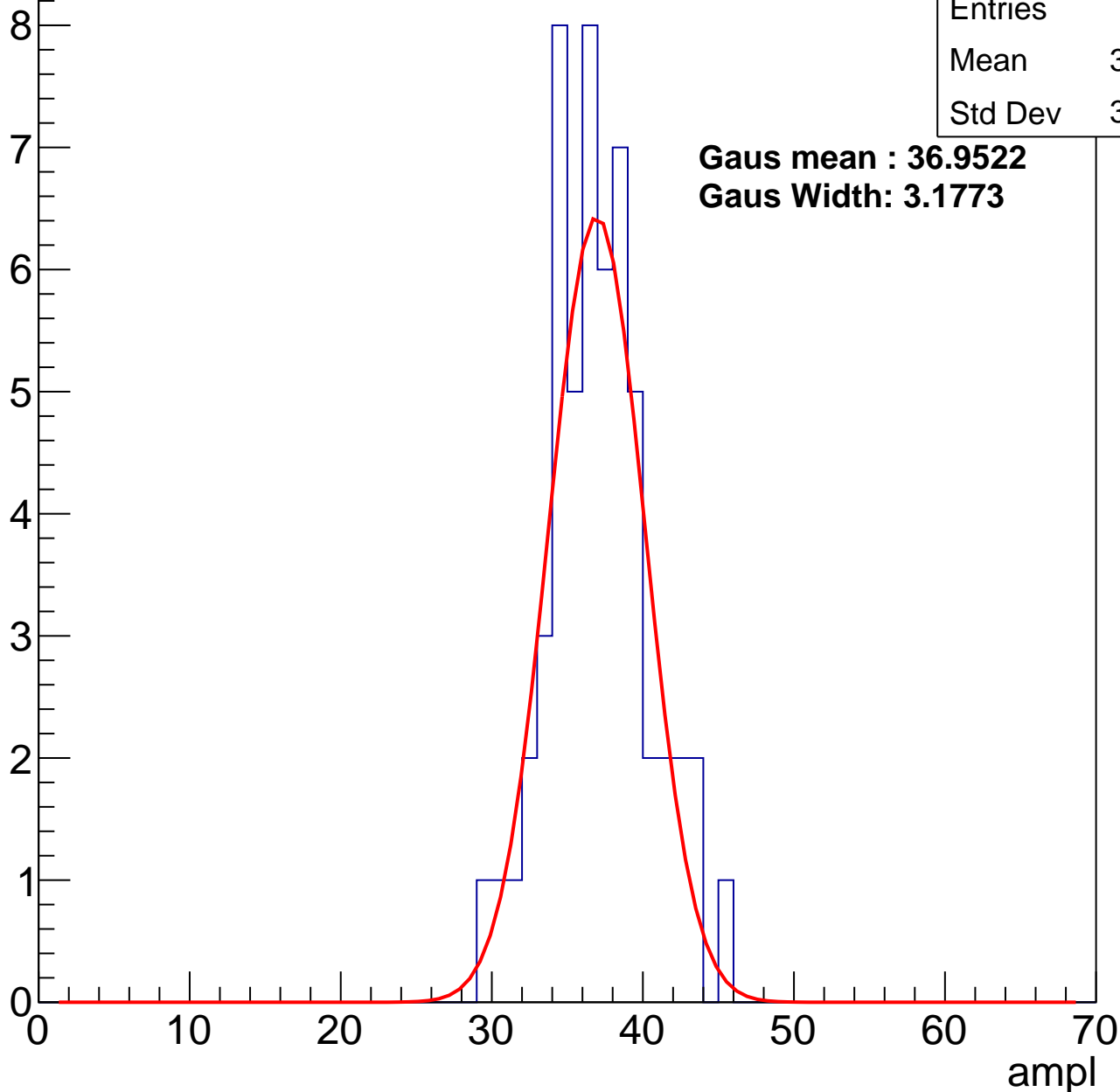
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	36.57
Std Dev	3.272

Gaus mean : 36.9522

Gaus Width: 3.1773



B1L103S, U5-ch93, adc2

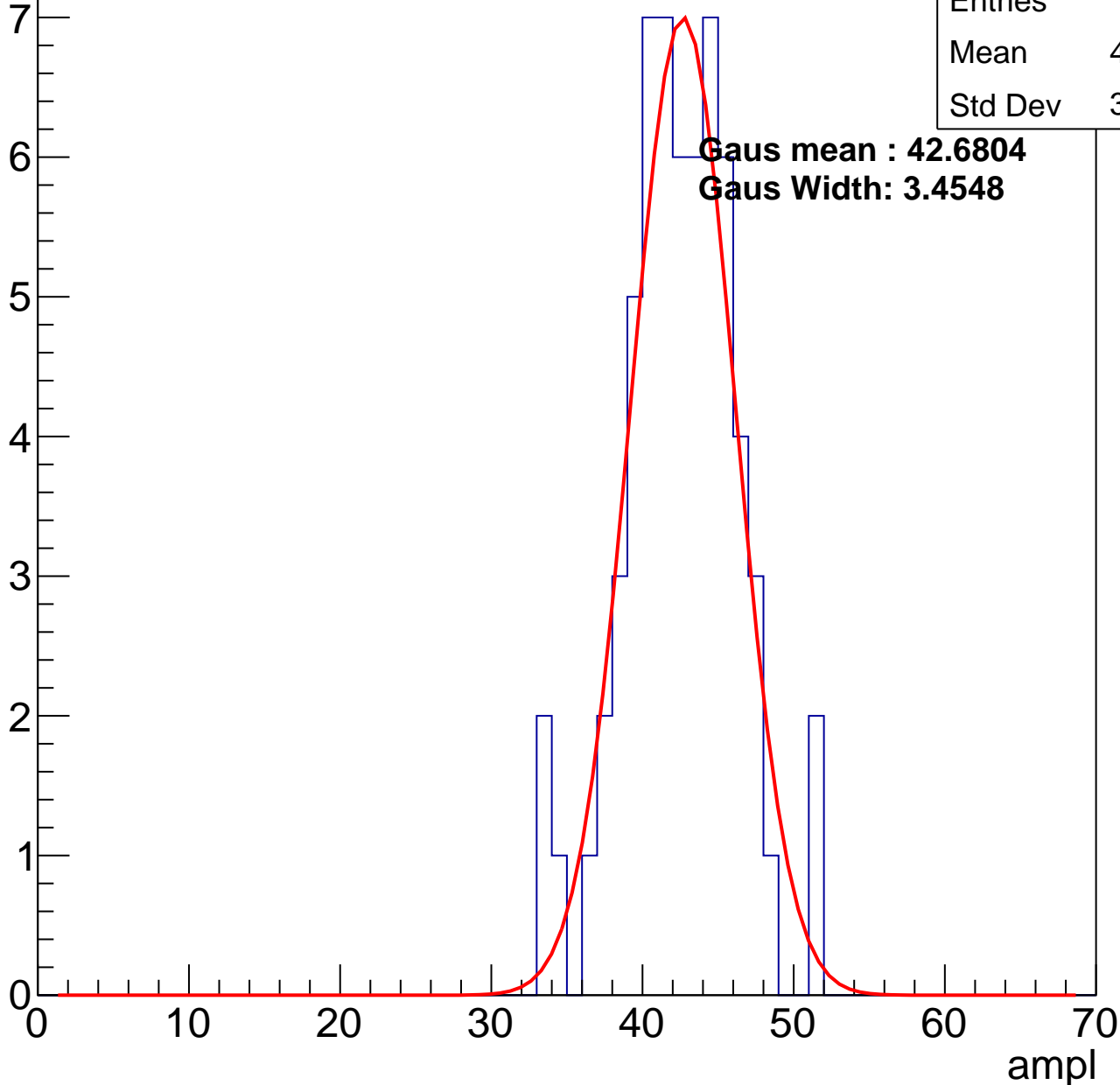
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.05
Std Dev	3.705

Gaus mean : 42.6804

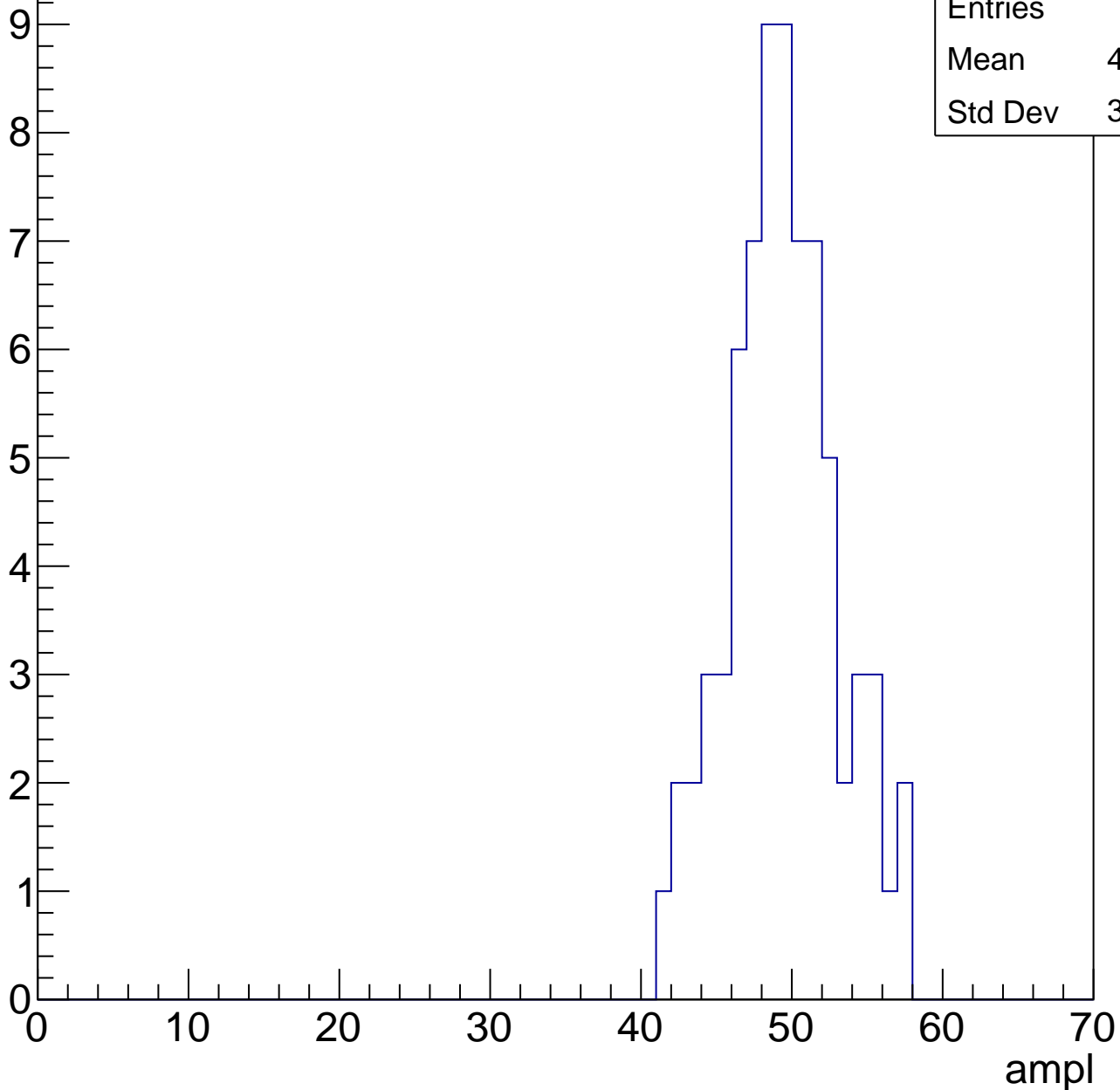
Gaus Width: 3.4548



B1L103S, U5-ch93, adc3

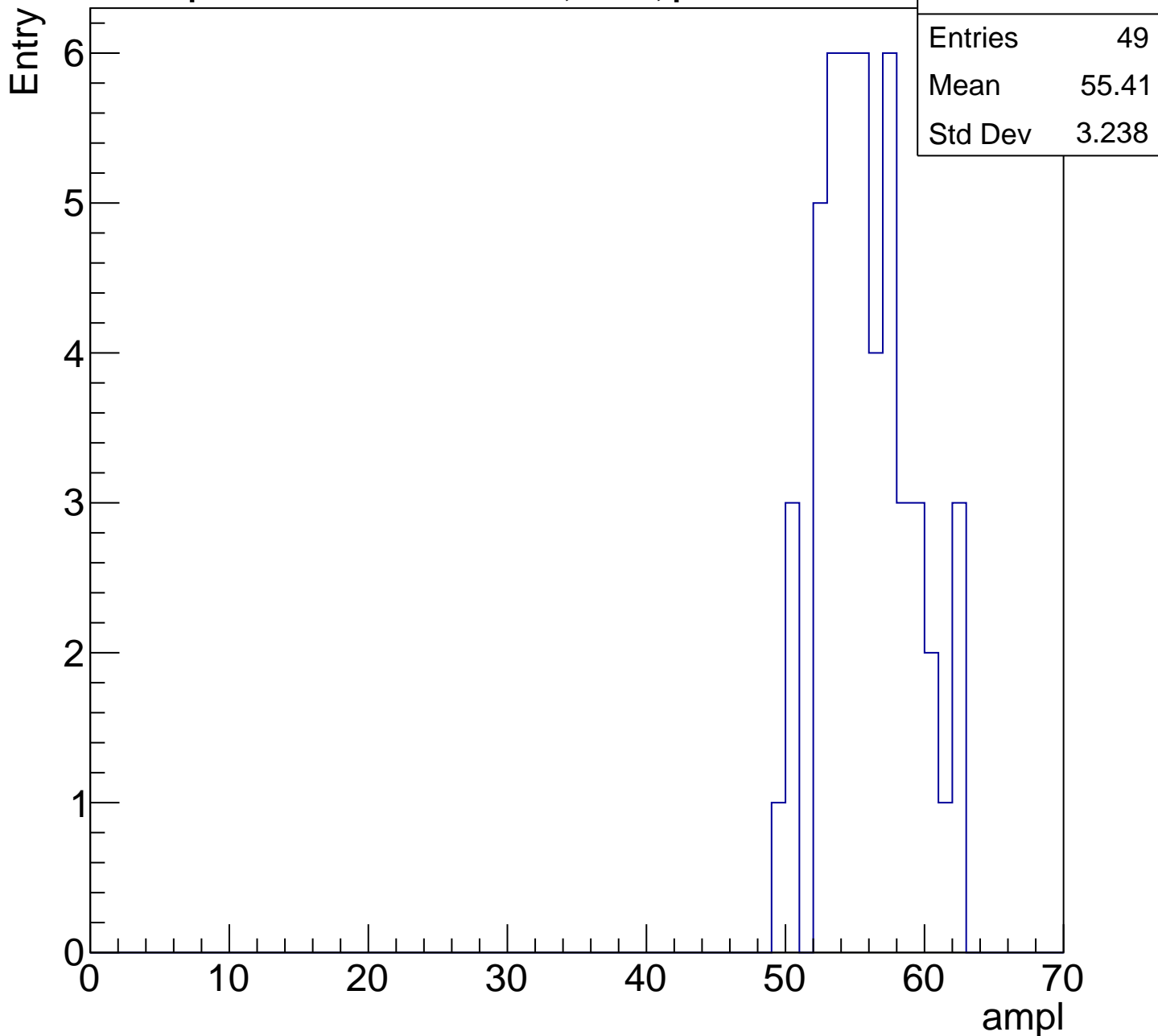
calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

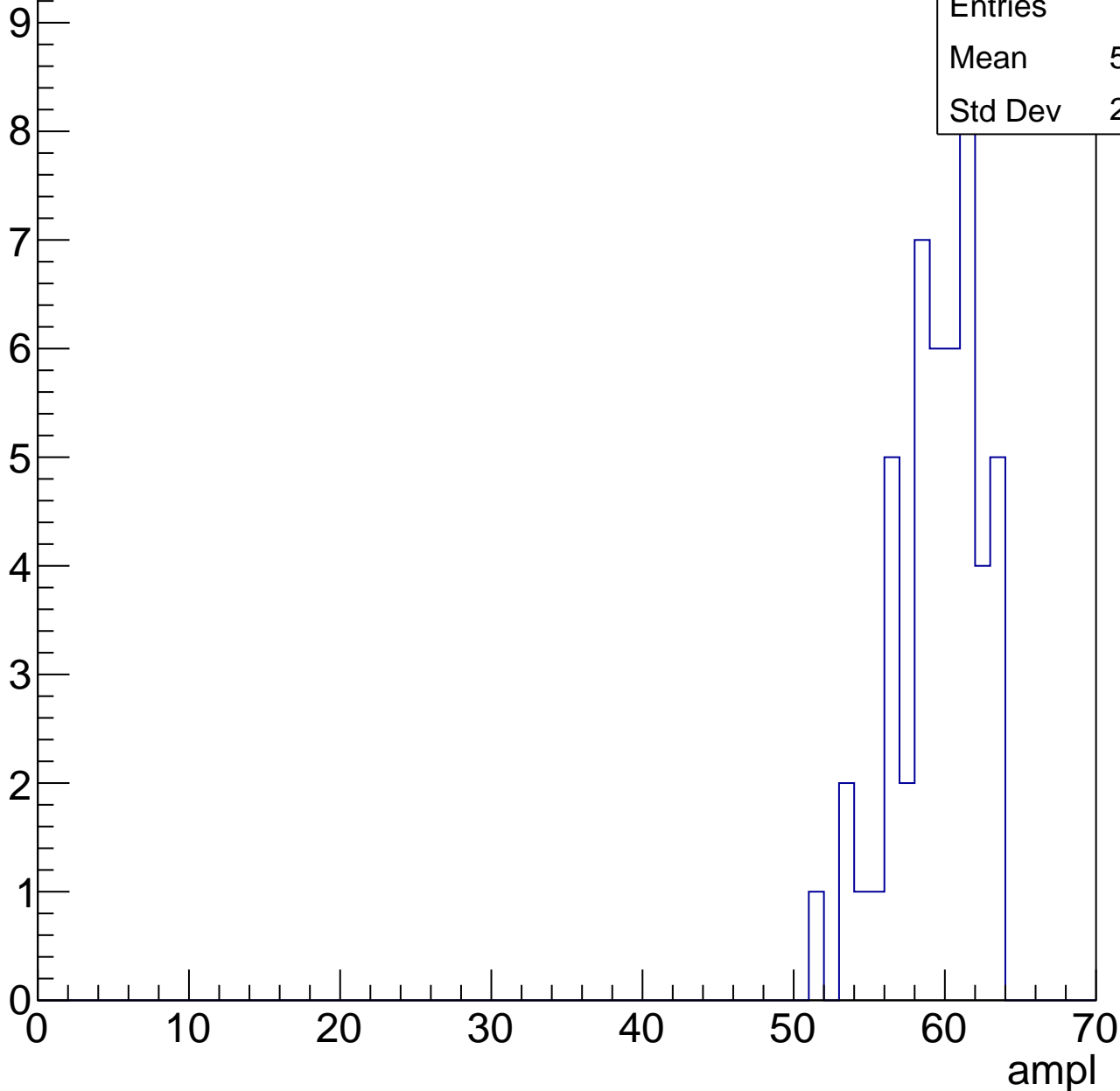


B1L103S, U5-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	59.02
Std Dev	2.839

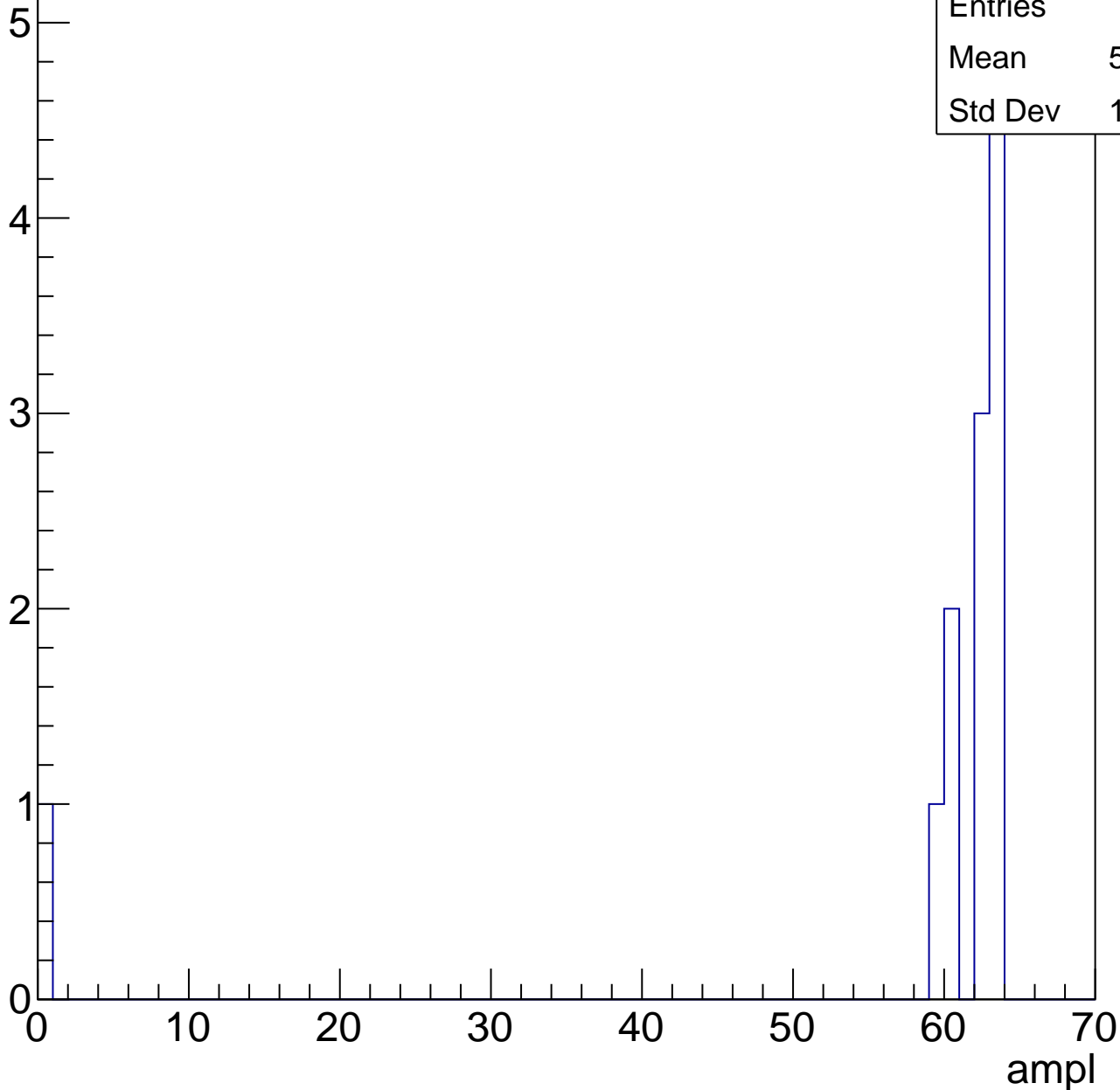


B1L103S, U5-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	56.67
Std Dev	17.14



B1L103S, U5-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch94, adc0

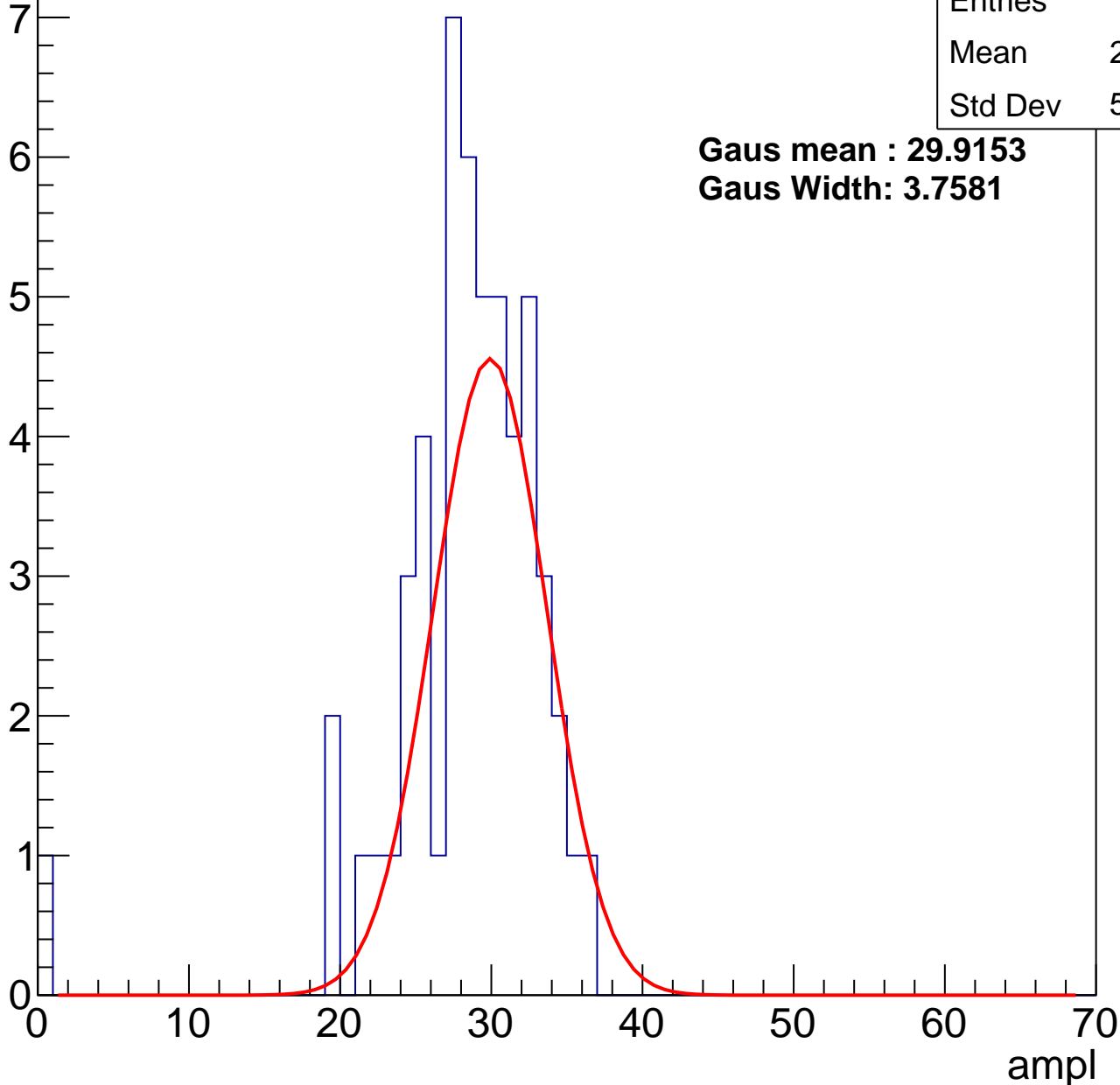
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	27.85
Std Dev	5.409

Gaus mean : 29.9153

Gaus Width: 3.7581



B1L103S, U5-ch94, adc1

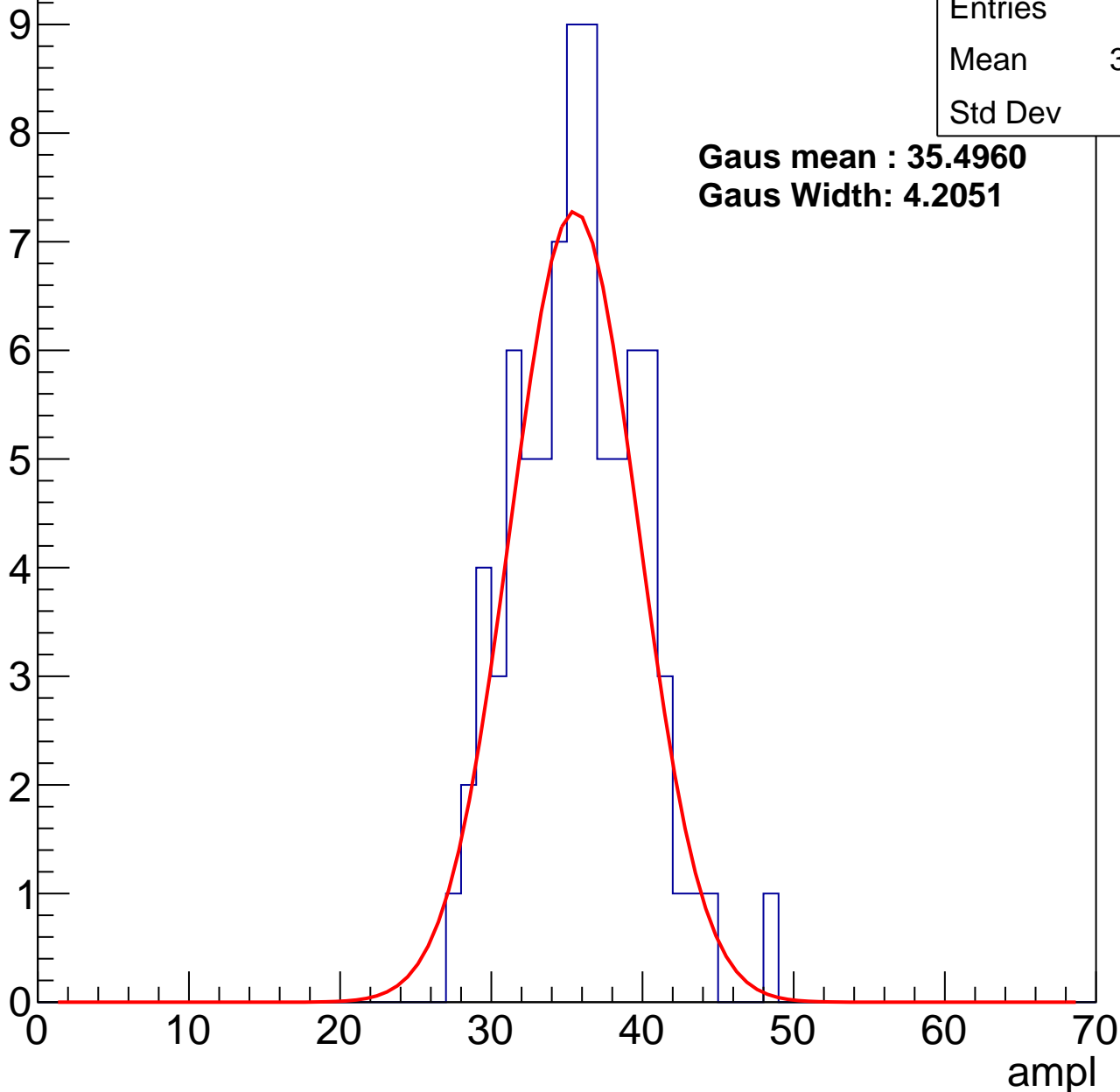
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	35.33
Std Dev	4.08

Gaus mean : 35.4960

Gaus Width: 4.2051



B1L103S, U5-ch94, adc2

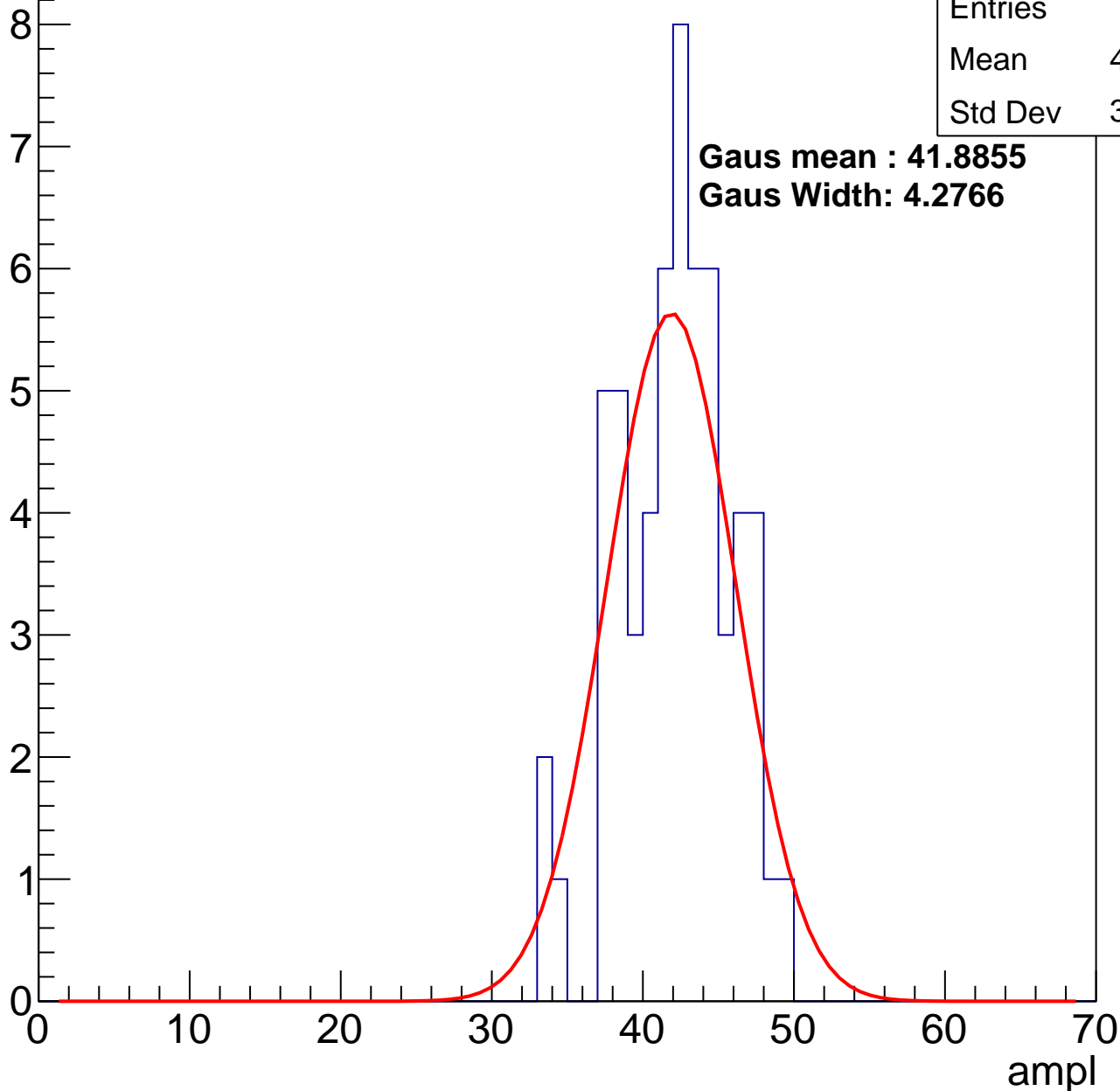
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	41.69
Std Dev	3.637

Gaus mean : 41.8855

Gaus Width: 4.2766

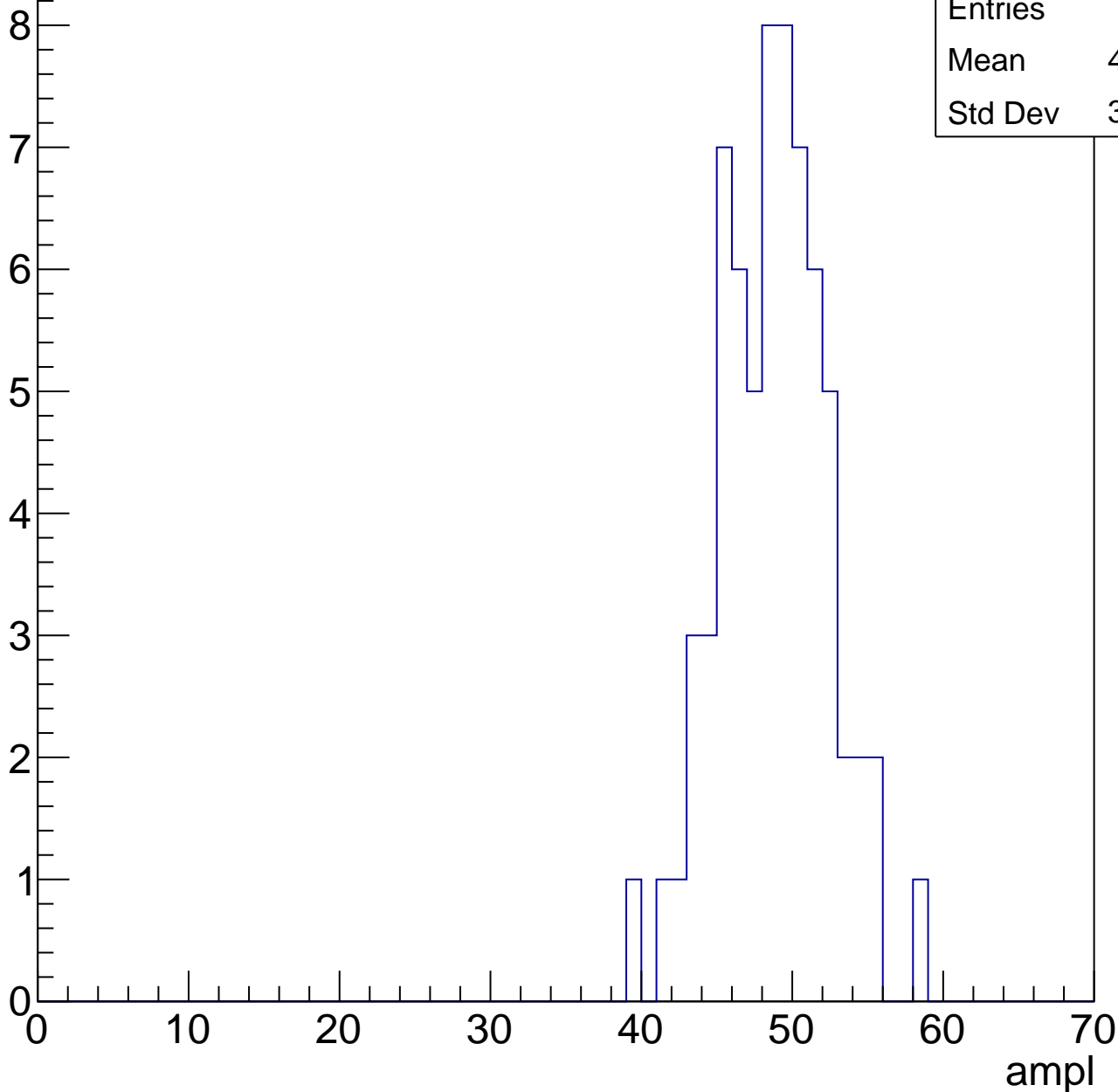


B1L103S, U5-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	48.28
Std Dev	3.568

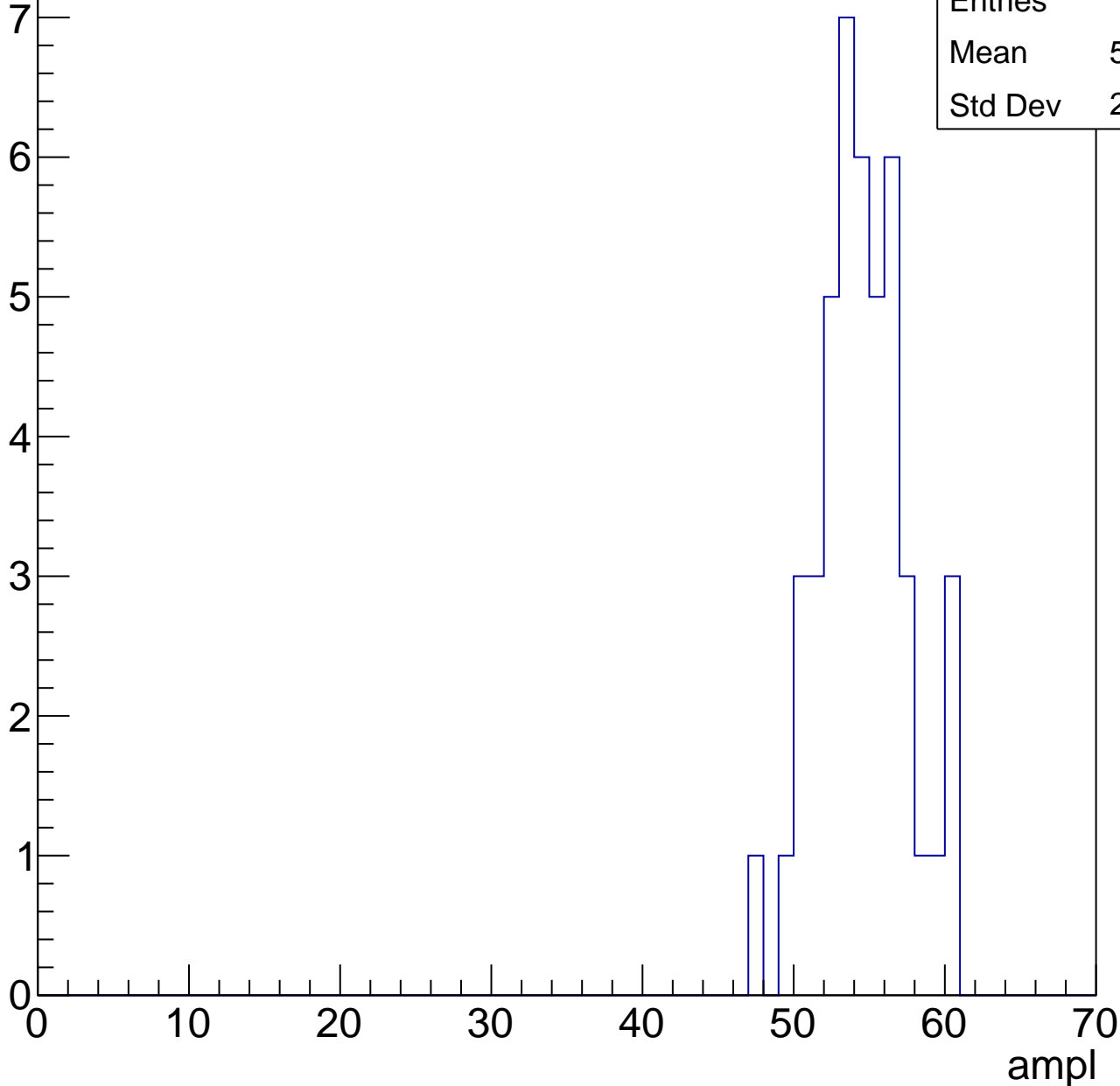


B1L103S, U5-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

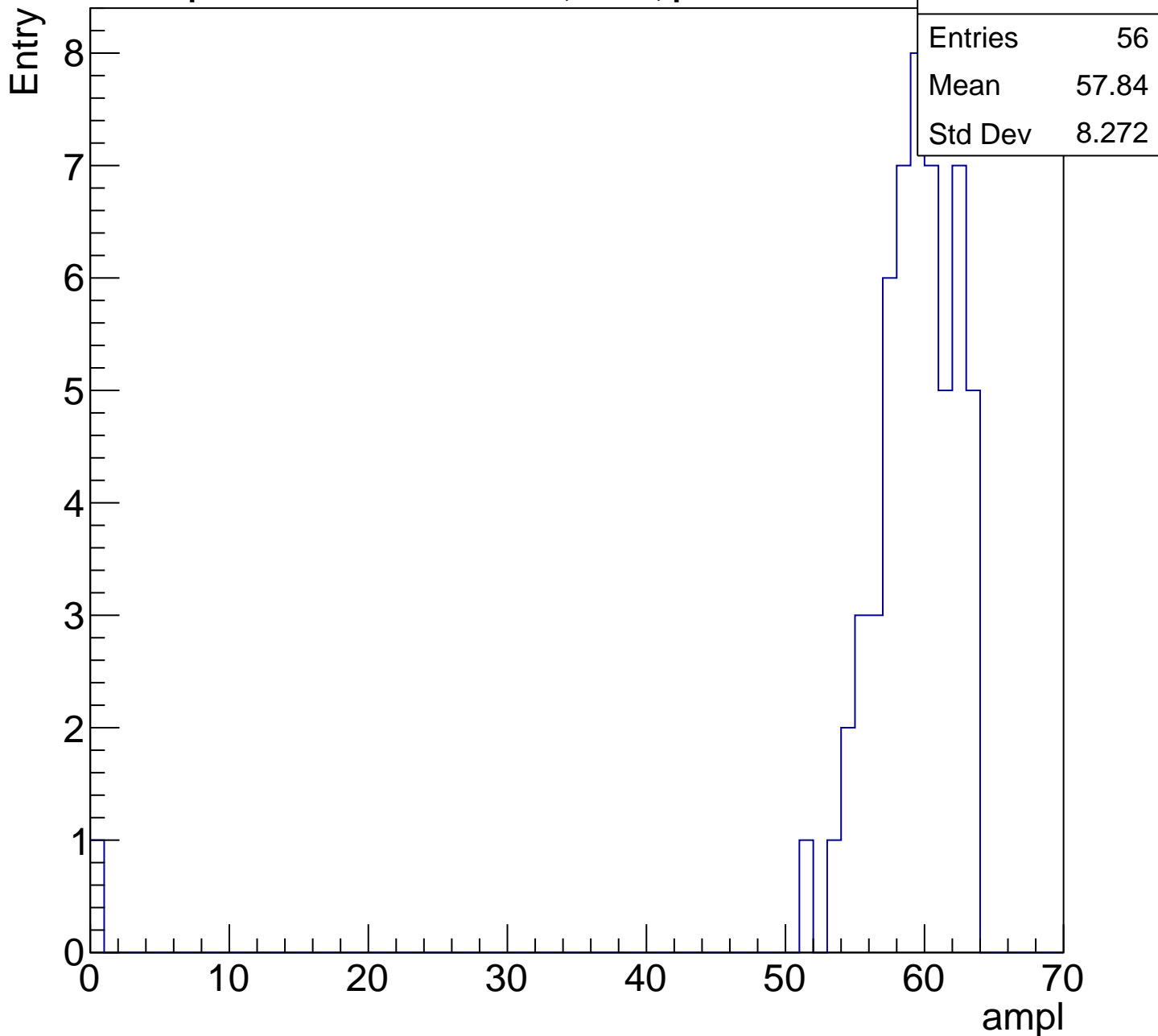
Entry

Entries	45
Mean	54.07
Std Dev	2.909



B1L103S, U5-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

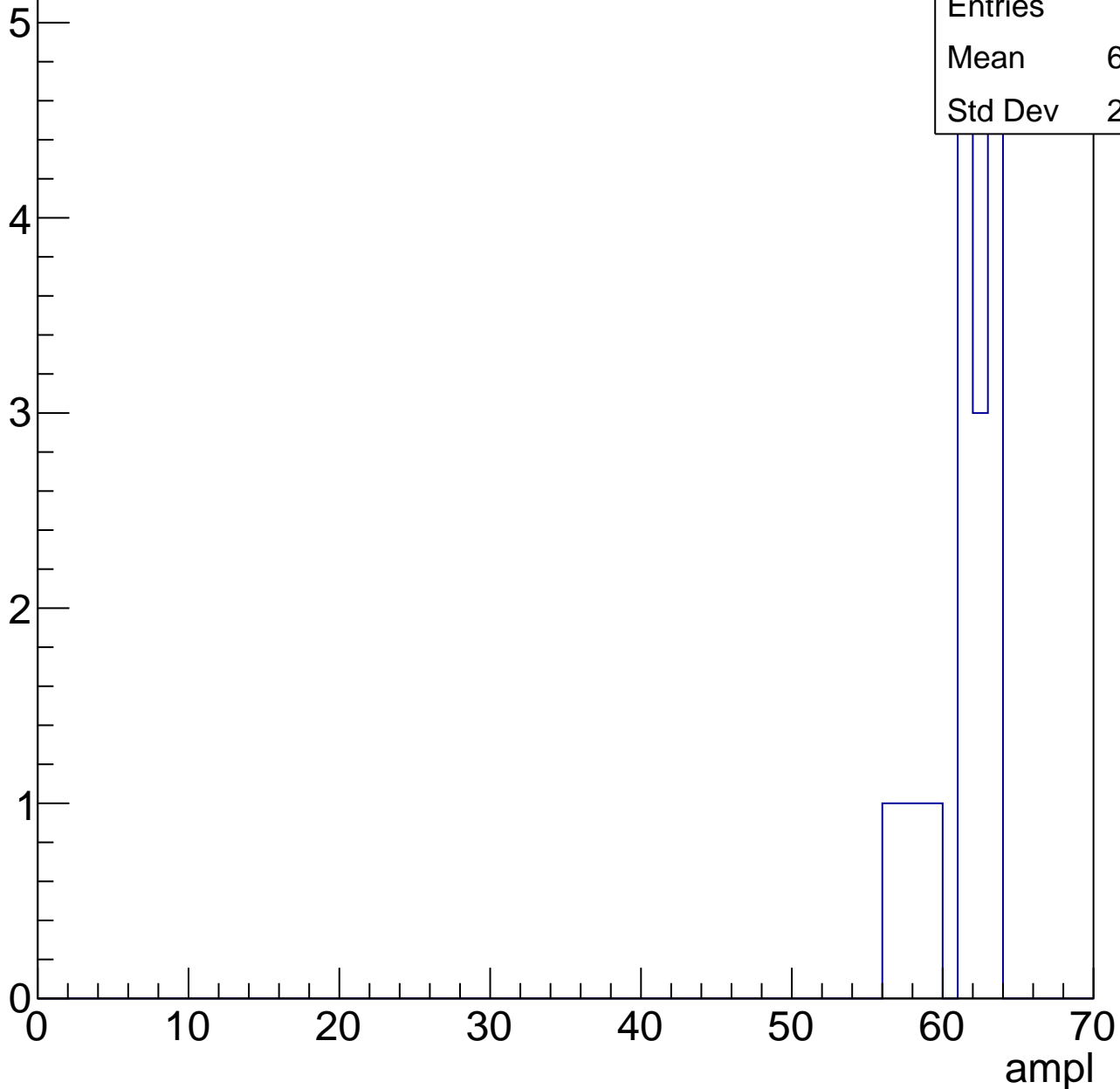


B1L103S, U5-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	60.94
Std Dev	2.127



B1L103S, U5-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch95, adc0

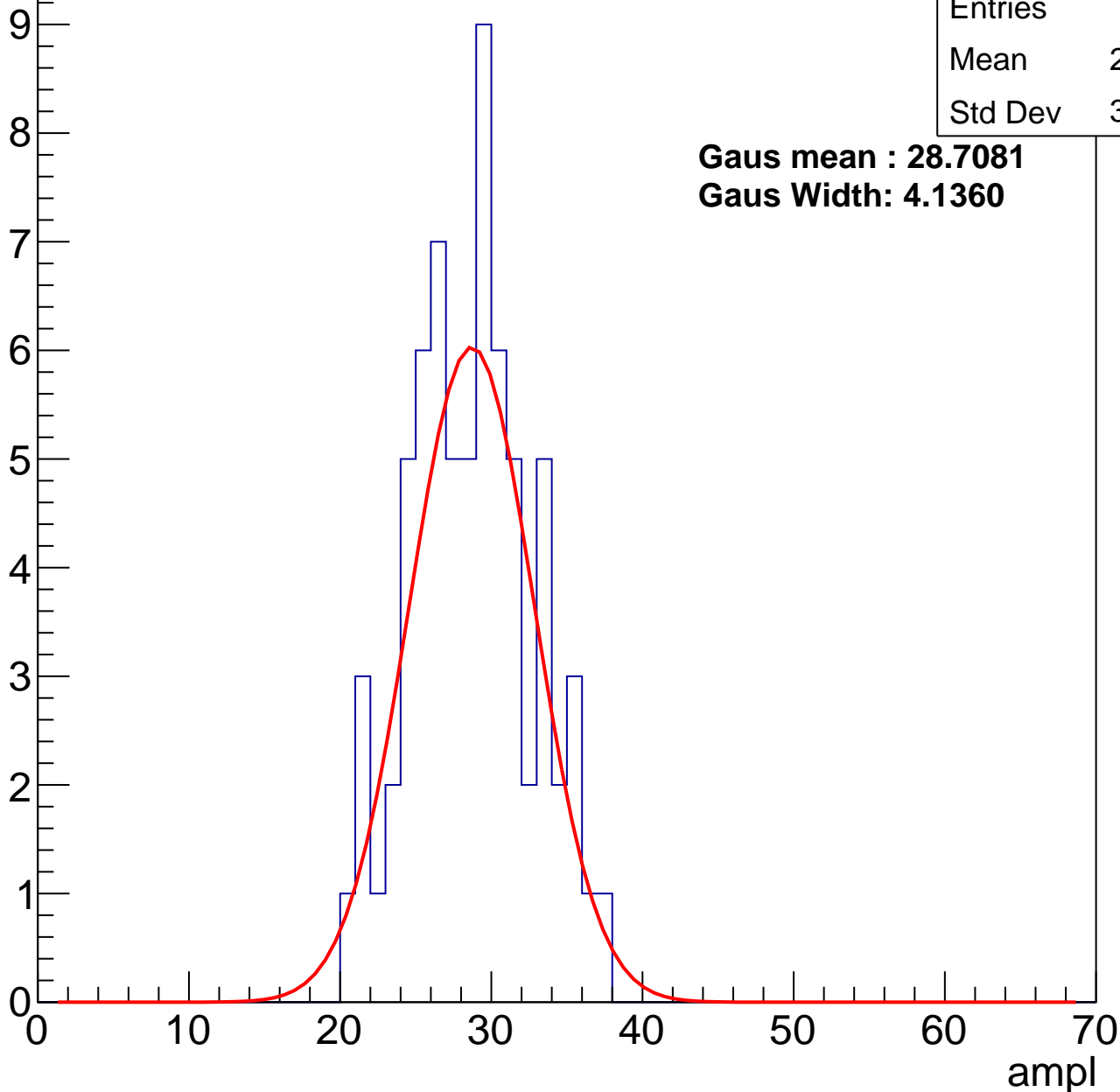
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	28.25
Std Dev	3.917

Gaus mean : 28.7081

Gaus Width: 4.1360



B1L103S, U5-ch95, adc1

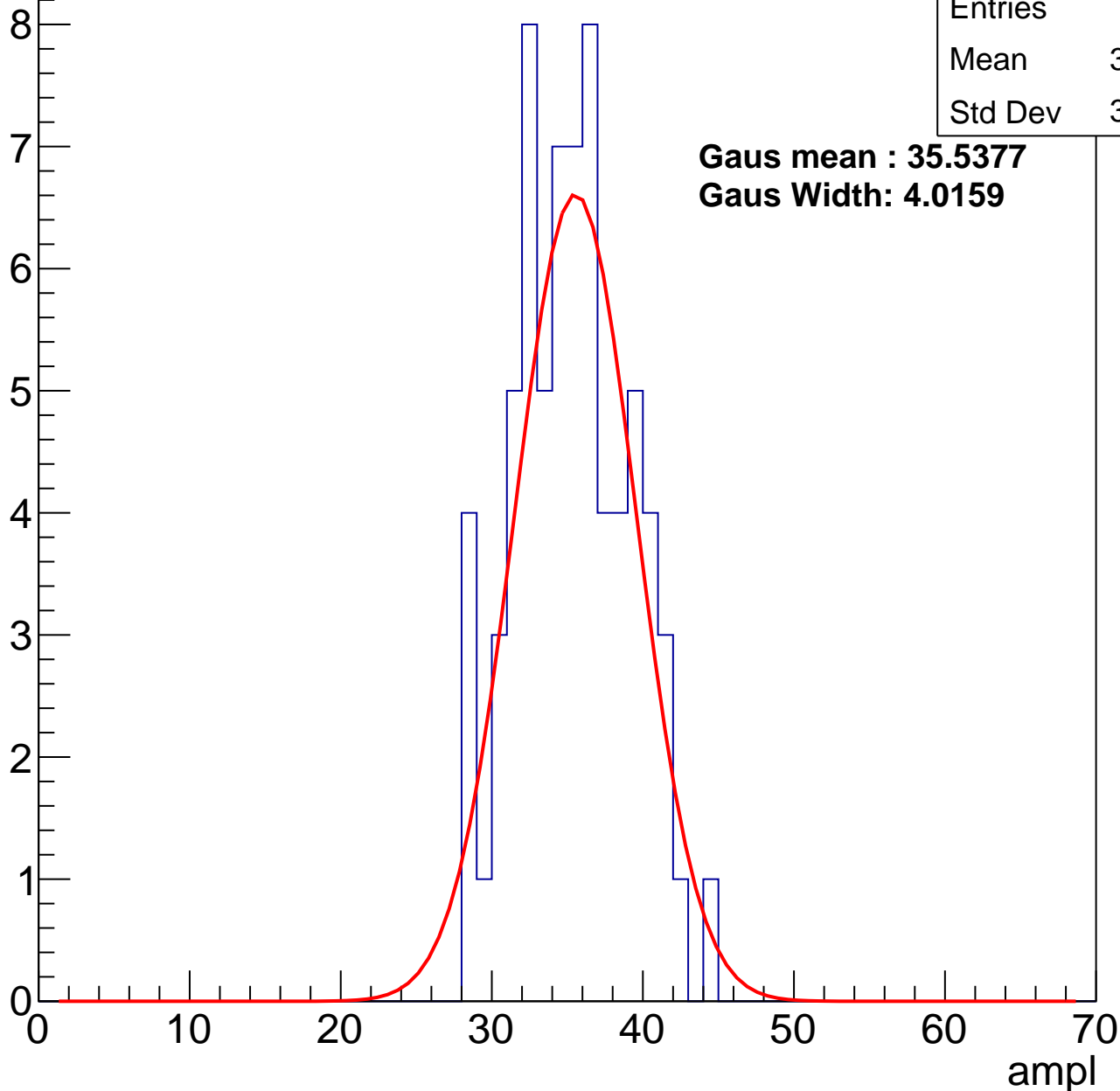
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	34.89
Std Dev	3.709

Gaus mean : 35.5377

Gaus Width: 4.0159



B1L103S, U5-ch95, adc2

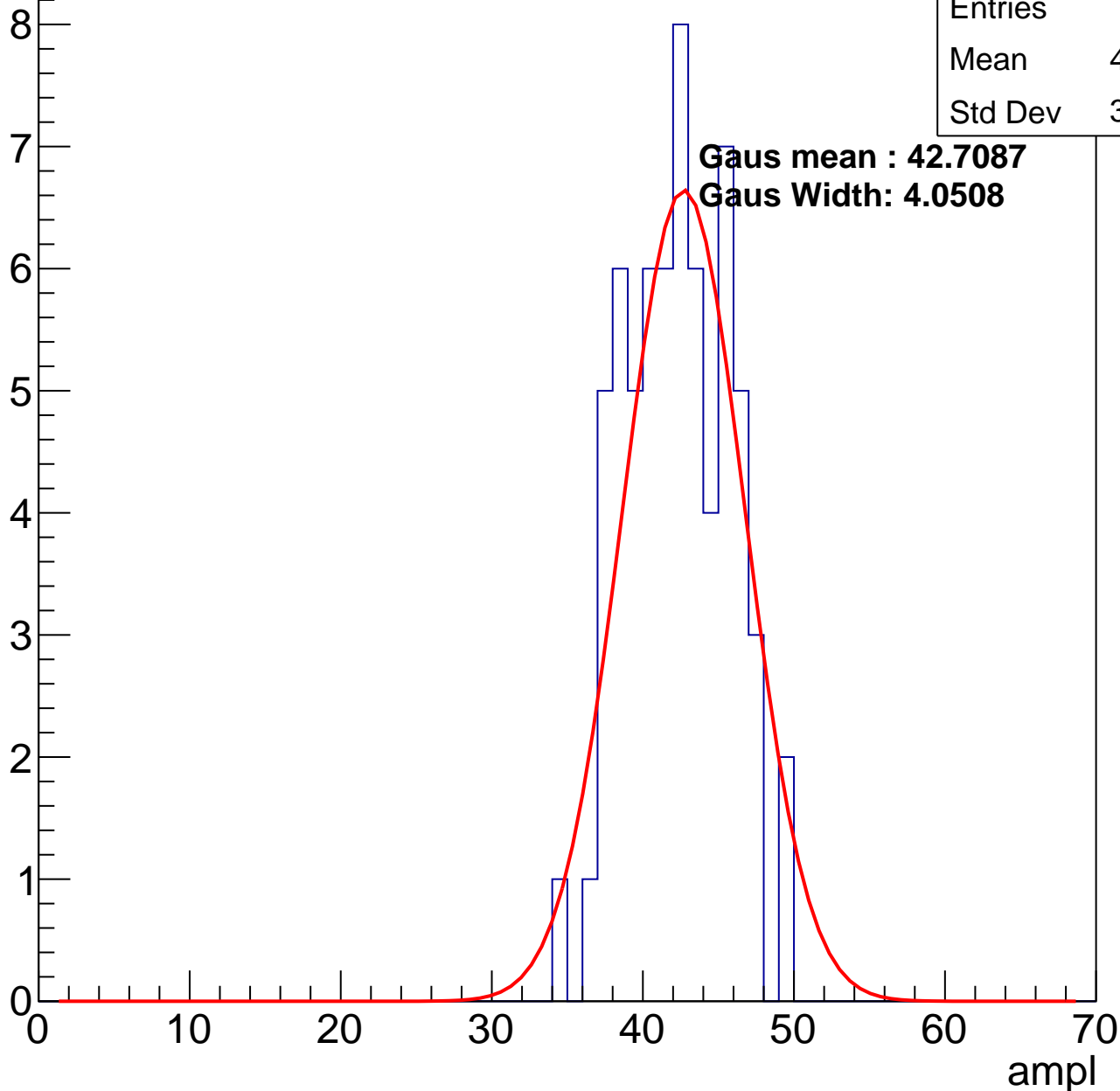
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.82
Std Dev	3.355

Gaus mean : 42.7087

Gaus Width: 4.0508

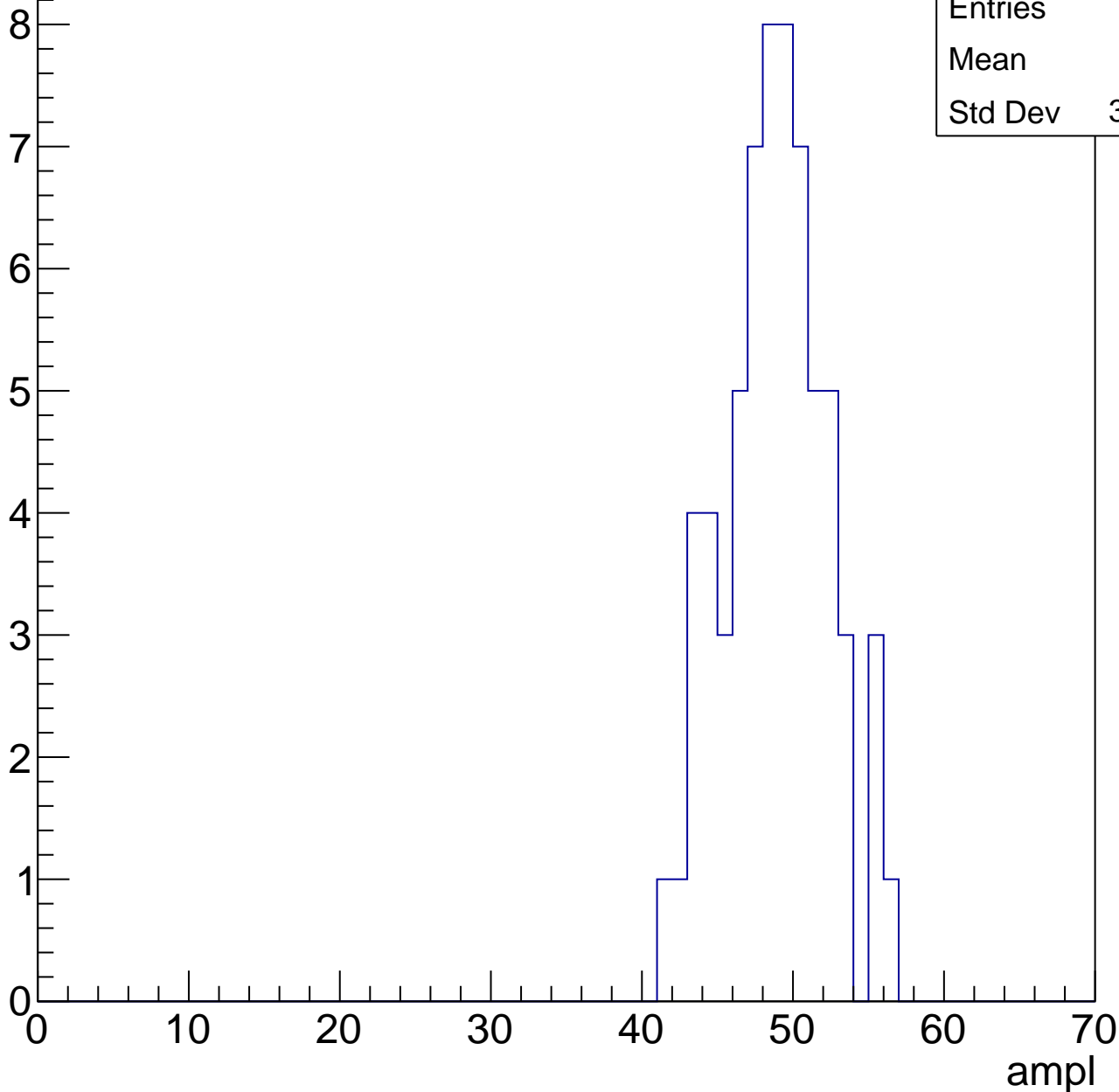


B1L103S, U5-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48.4
Std Dev	3.373

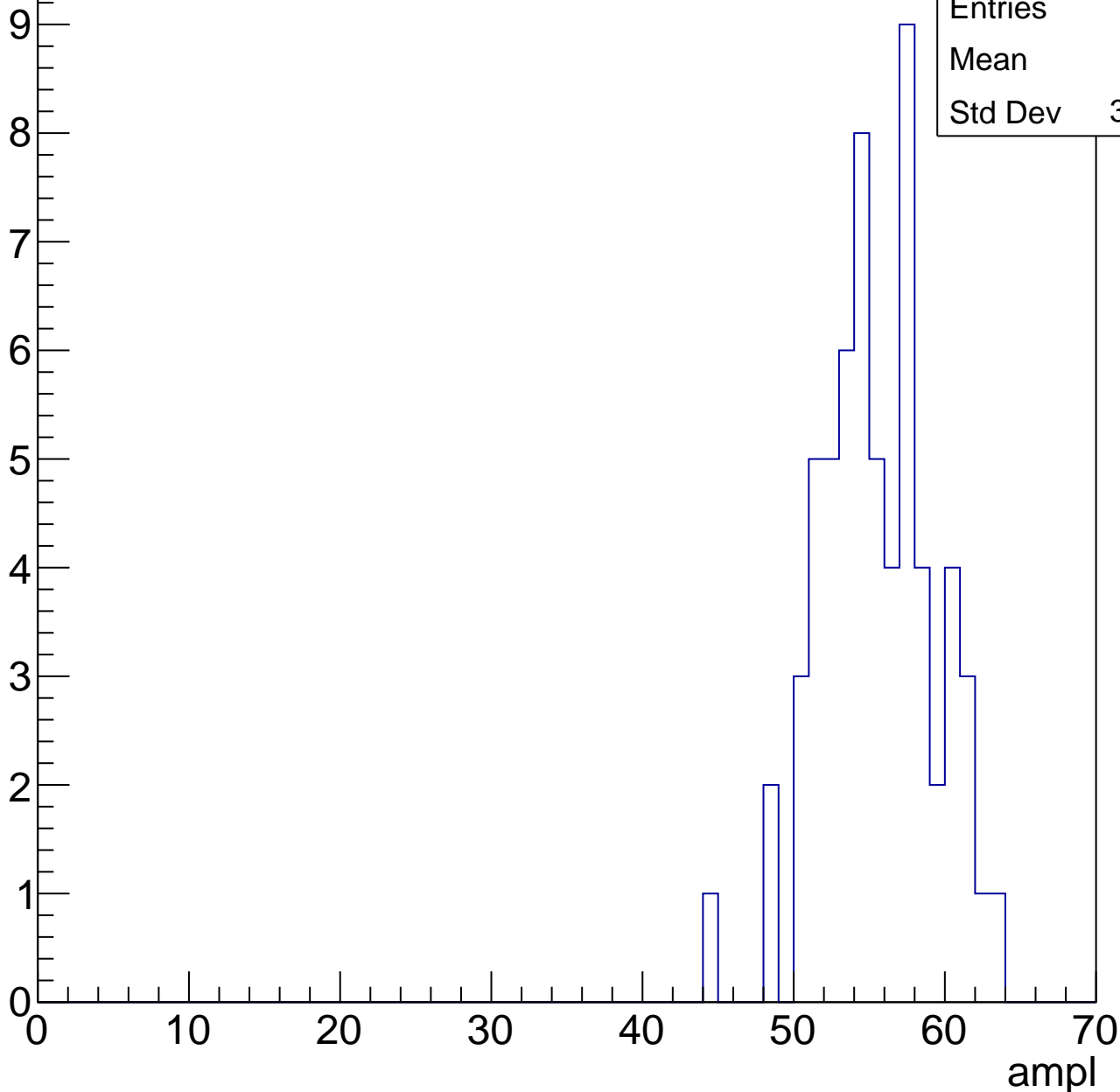


B1L103S, U5-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

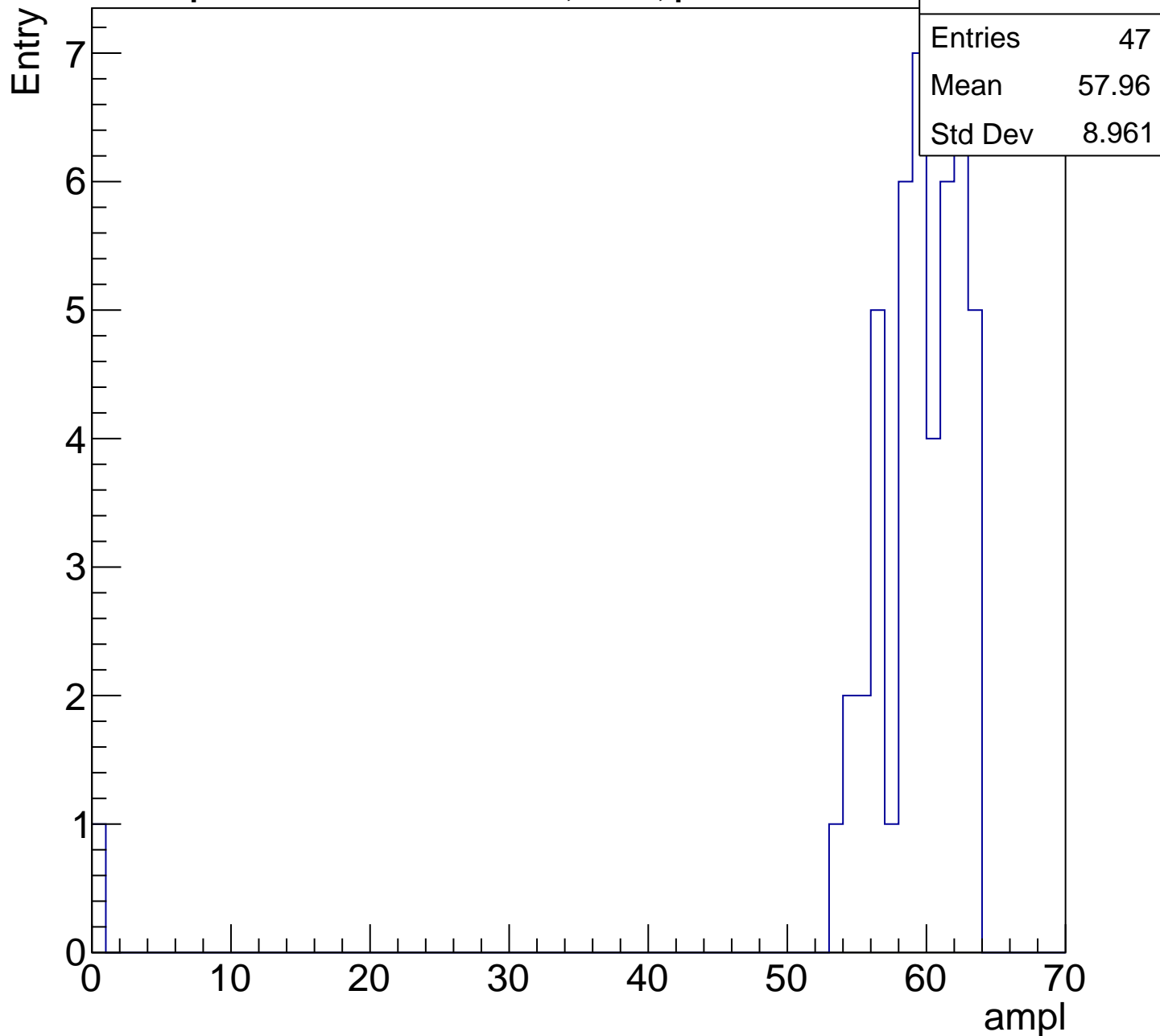
Entry

Entries	63
Mean	55
Std Dev	3.737



B1L103S, U5-ch95, adc5

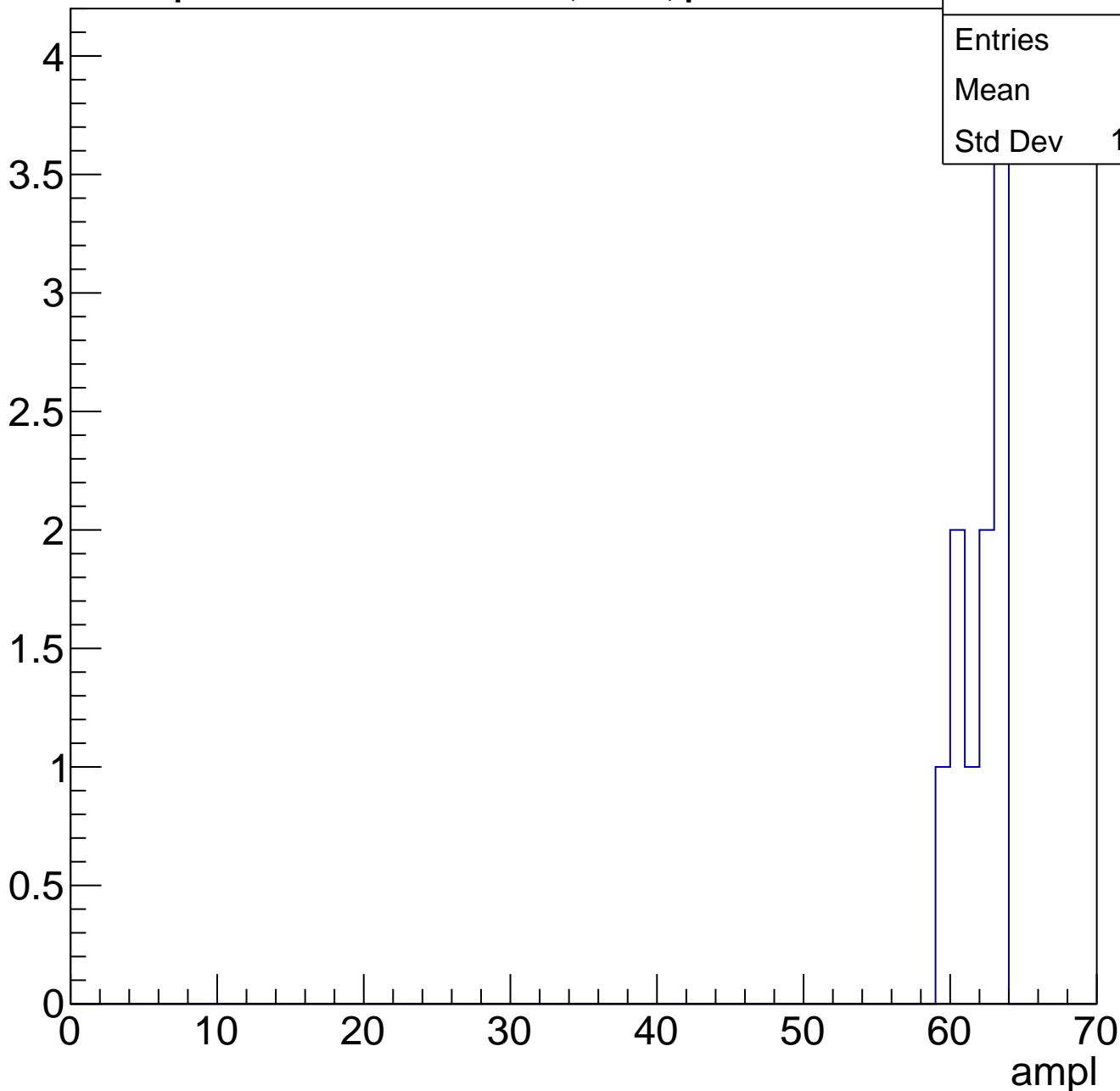
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	61.6
Std Dev	1.428

B1L103S, U5-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch96, adc0

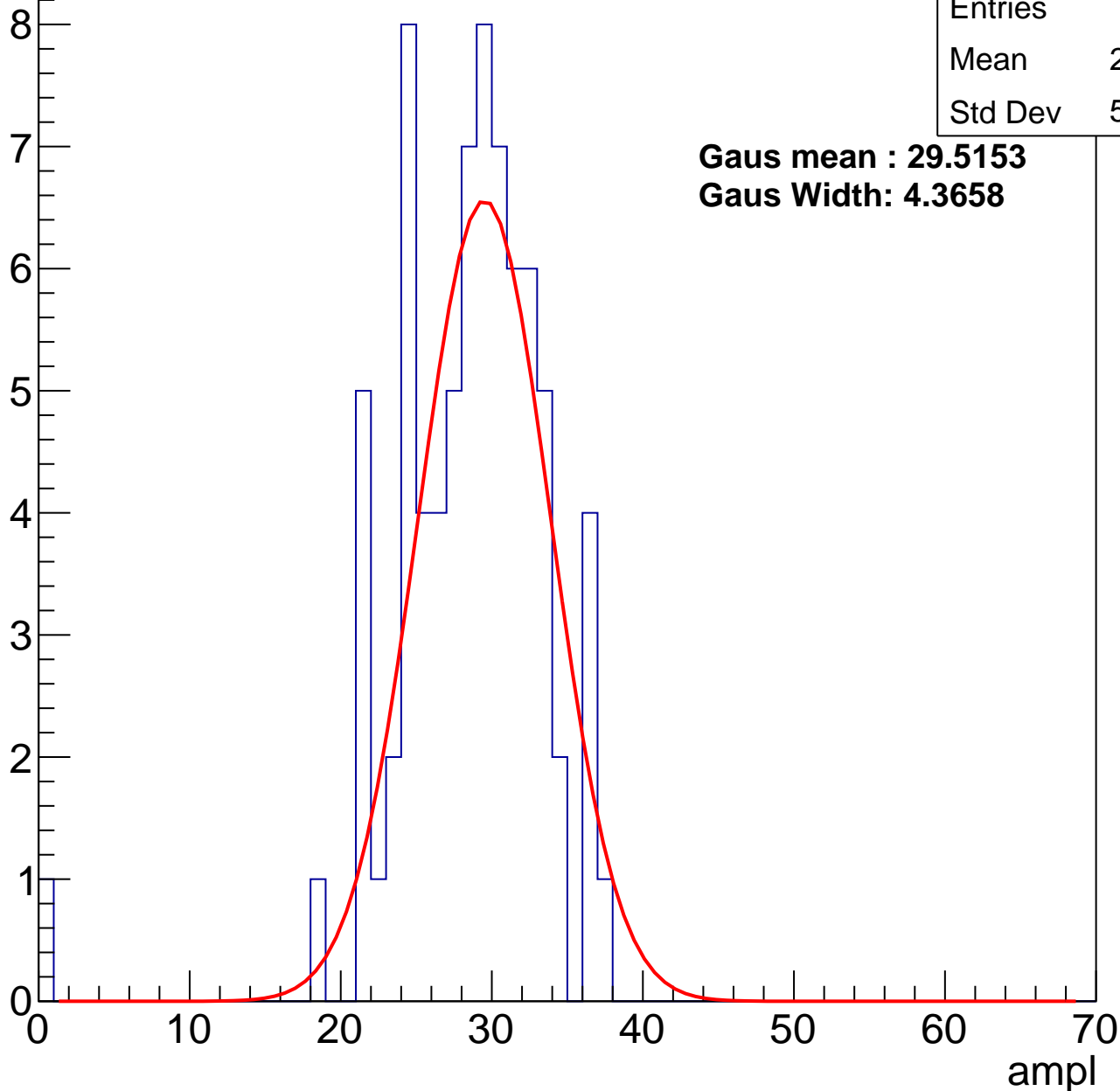
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	27.95
Std Dev	5.247

Gaus mean : 29.5153

Gaus Width: 4.3658



B1L103S, U5-ch96, adc1

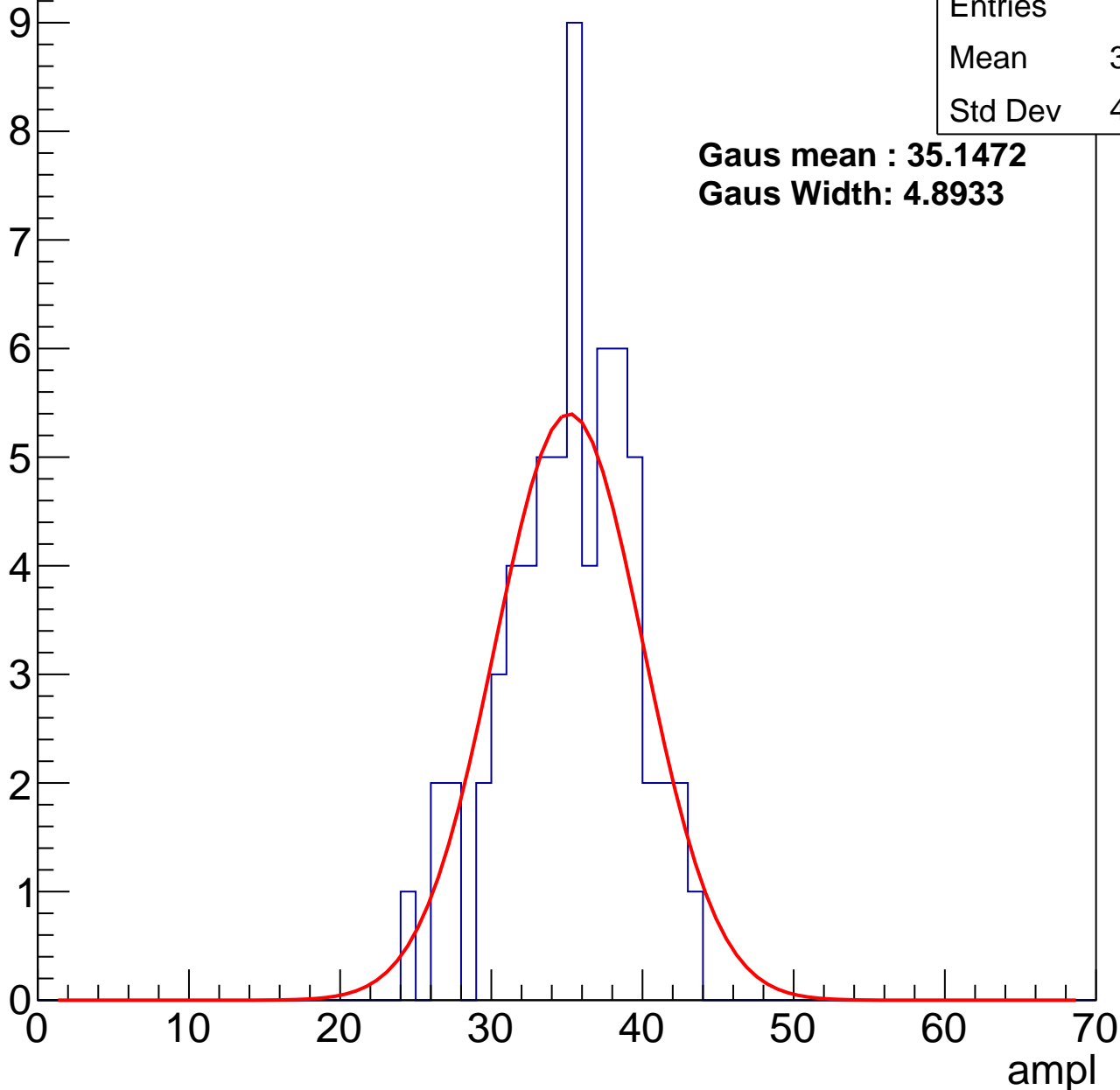
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	34.74
Std Dev	4.159

Gaus mean : 35.1472

Gaus Width: 4.8933



B1L103S, U5-ch96, adc2

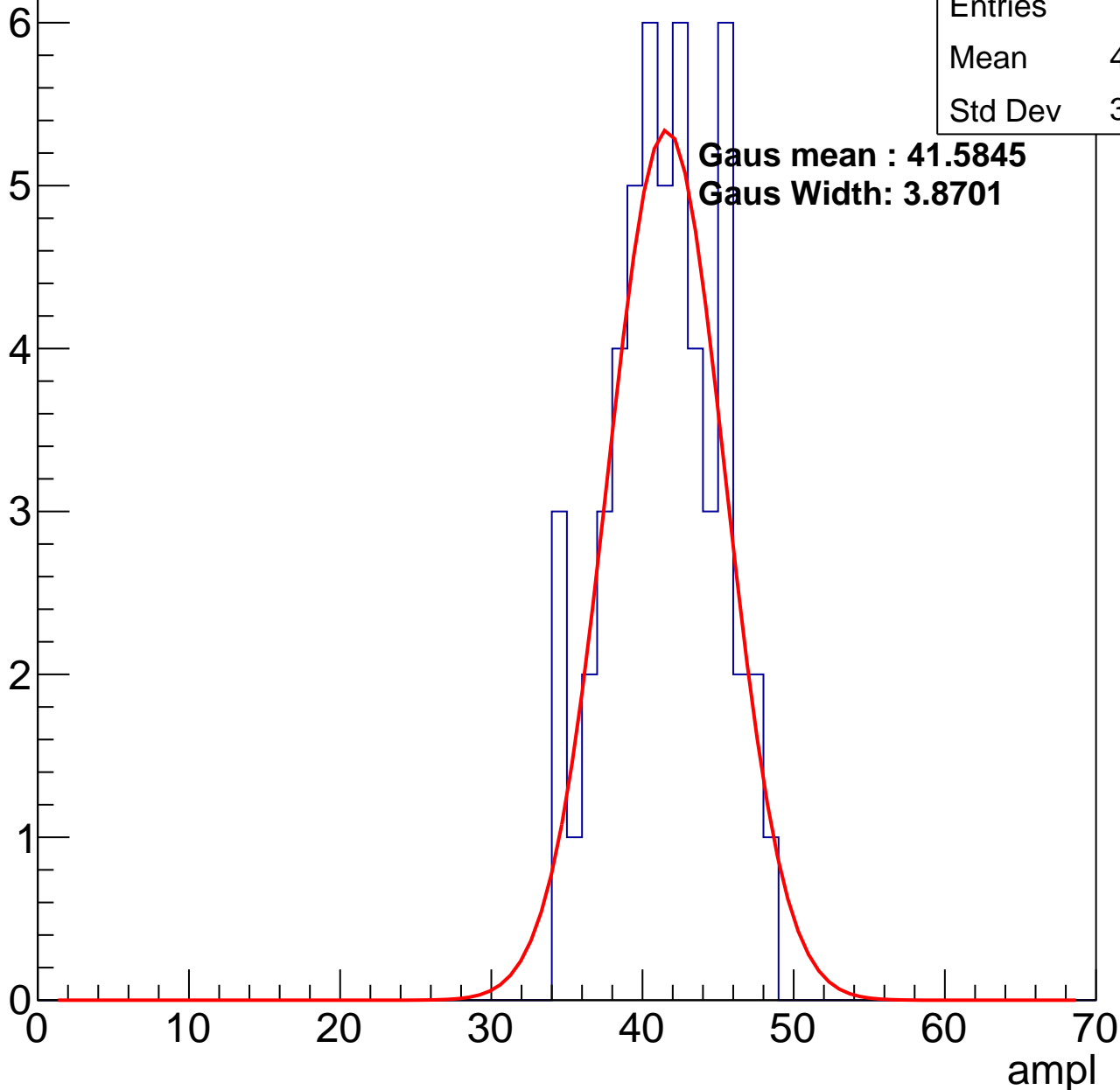
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	40.98
Std Dev	3.526

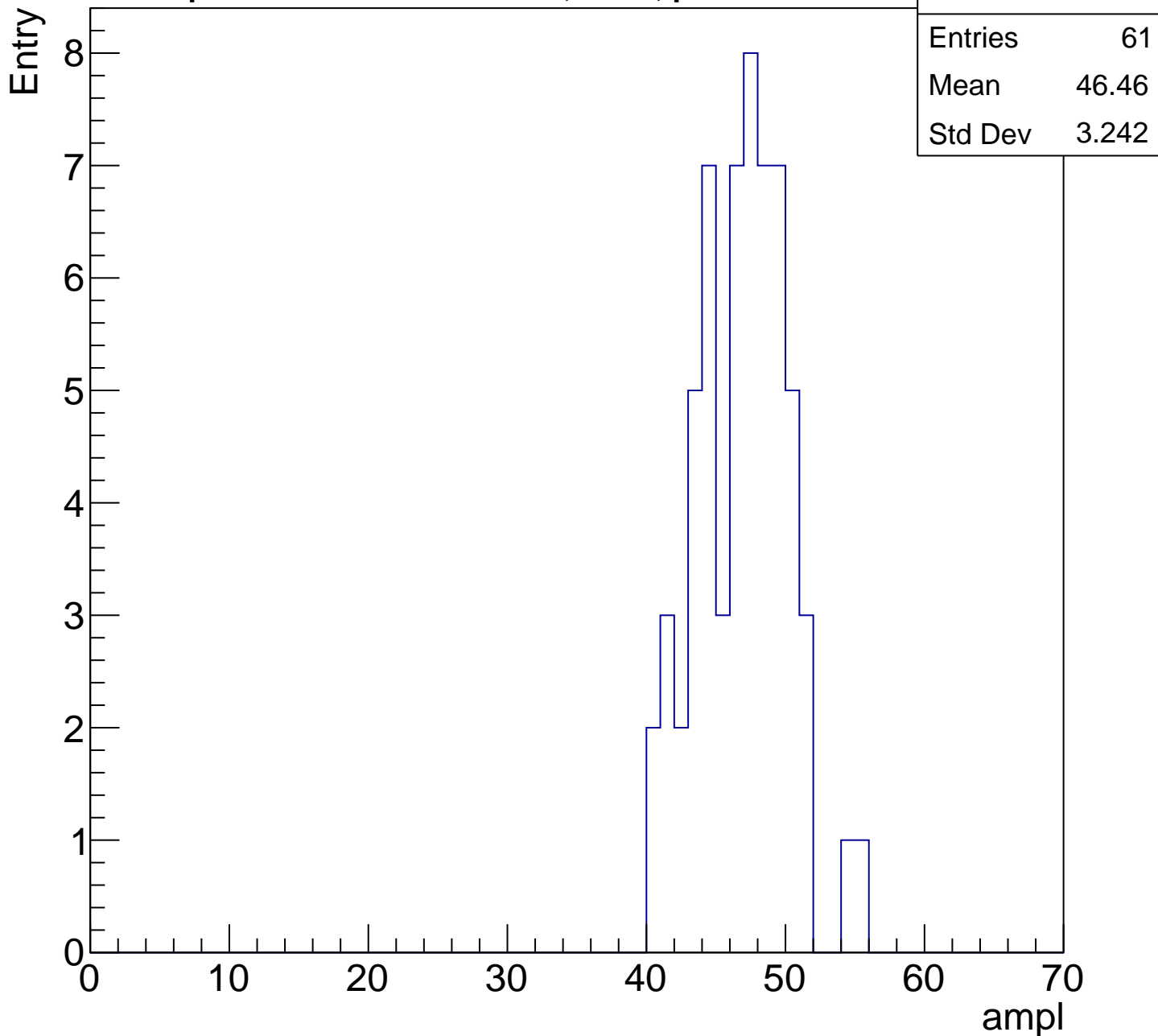
Gaus mean : 41.5845

Gaus Width: 3.8701



B1L103S, U5-ch96, adc3

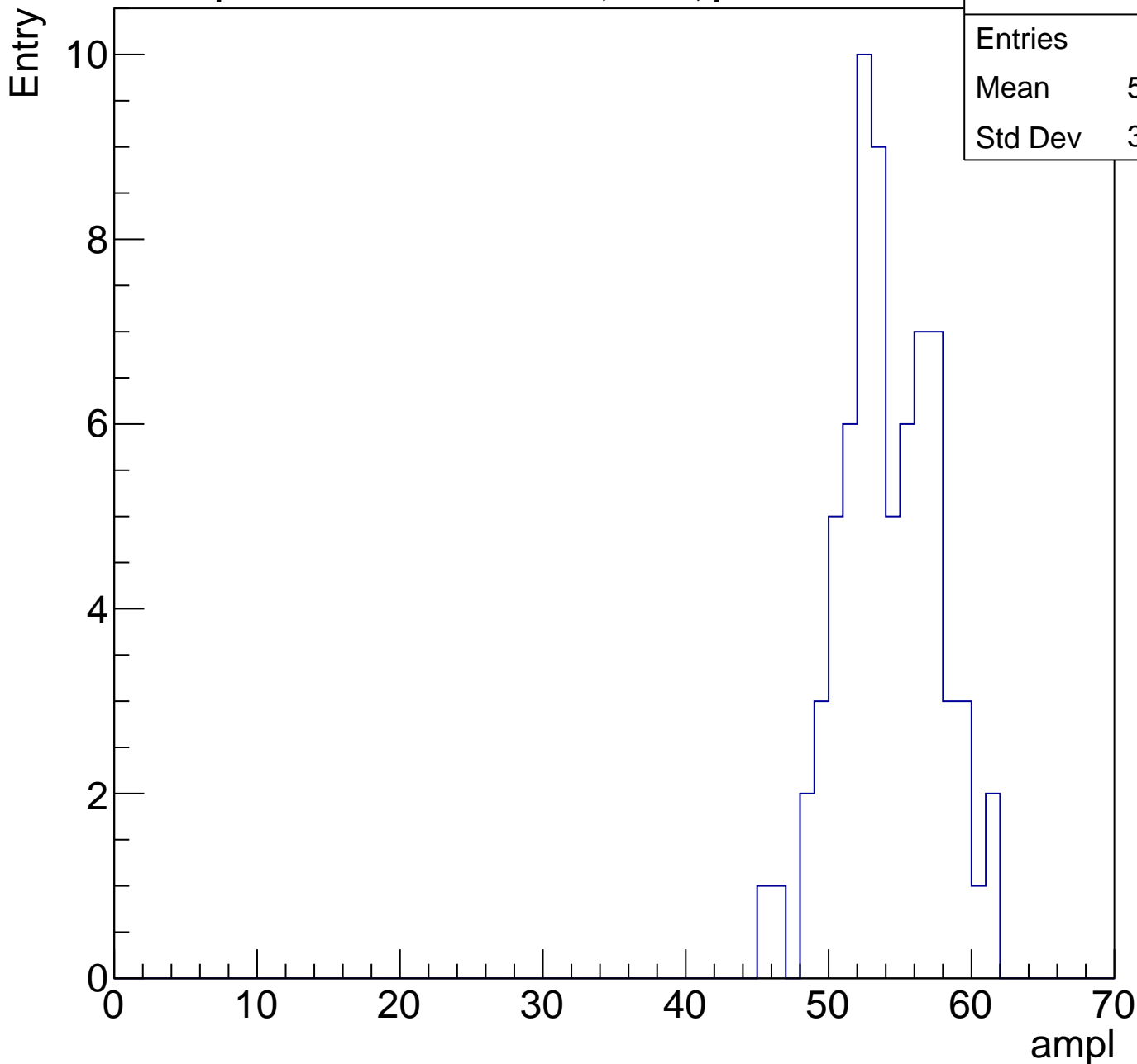
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	53.68
Std Dev	3.402

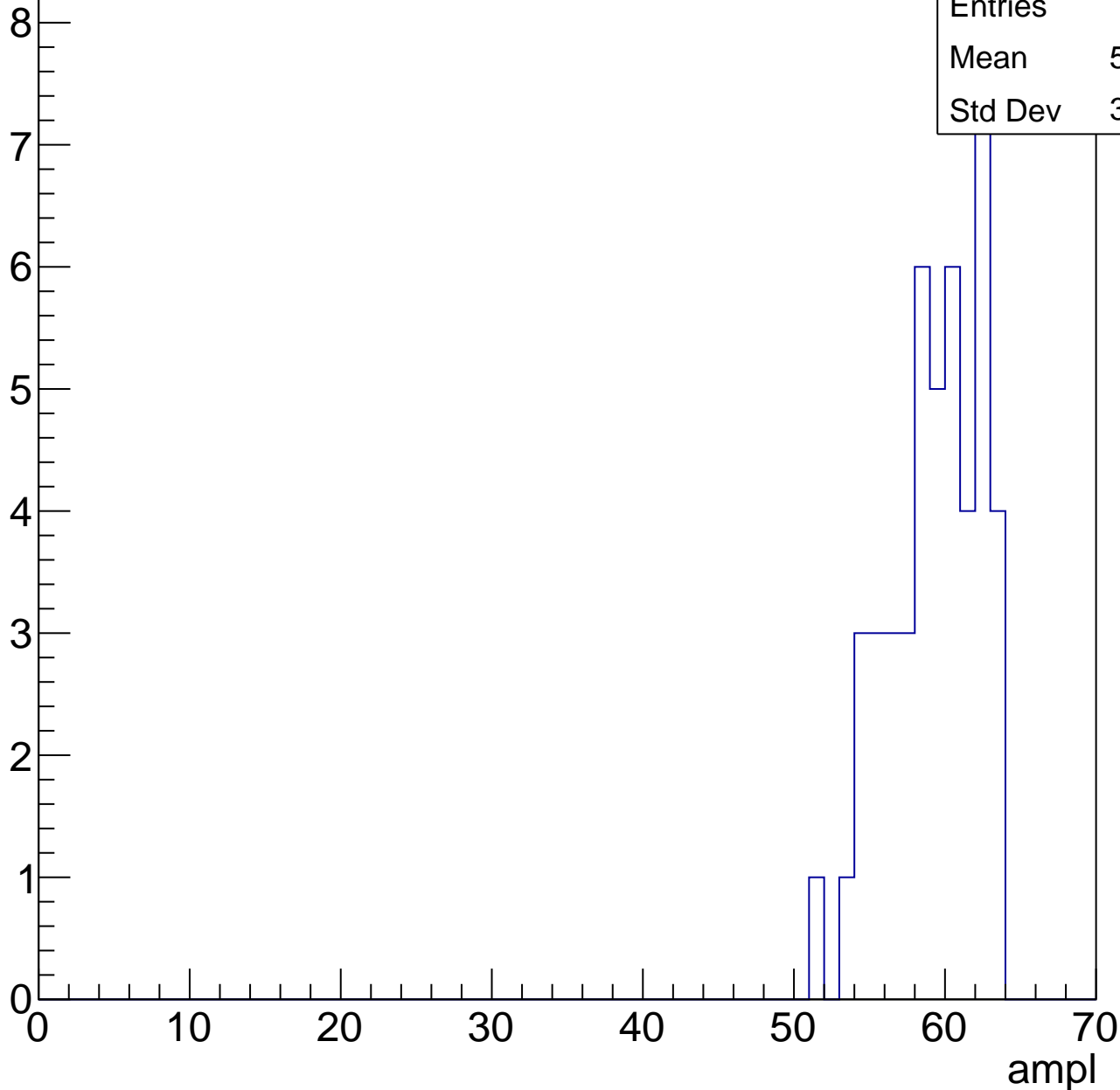


B1L103S, U5-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

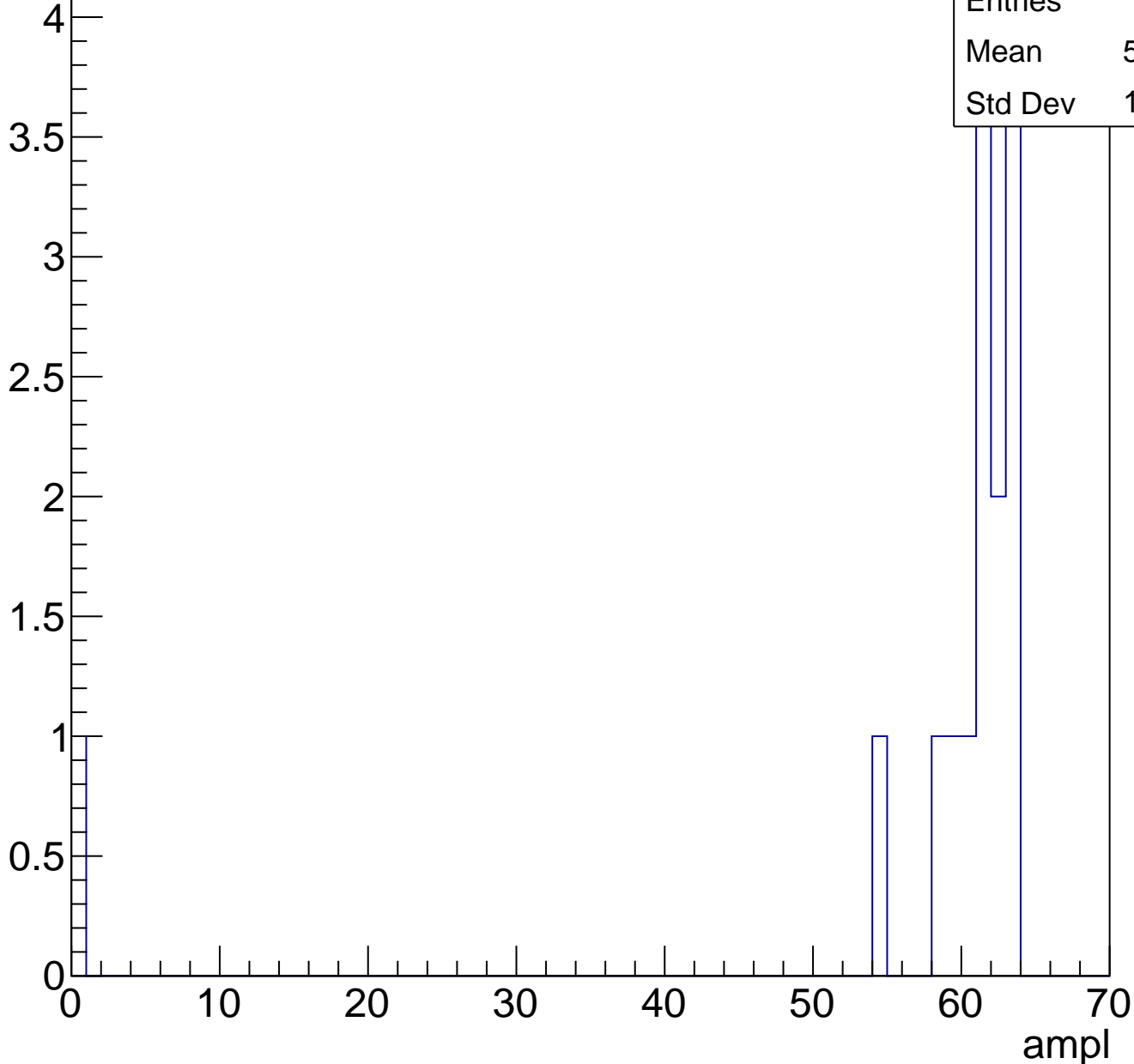
Entries	47
Mean	58.83
Std Dev	3.006



B1L103S, U5-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	15
Mean	56.73
Std Dev	15.34

B1L103S, U5-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

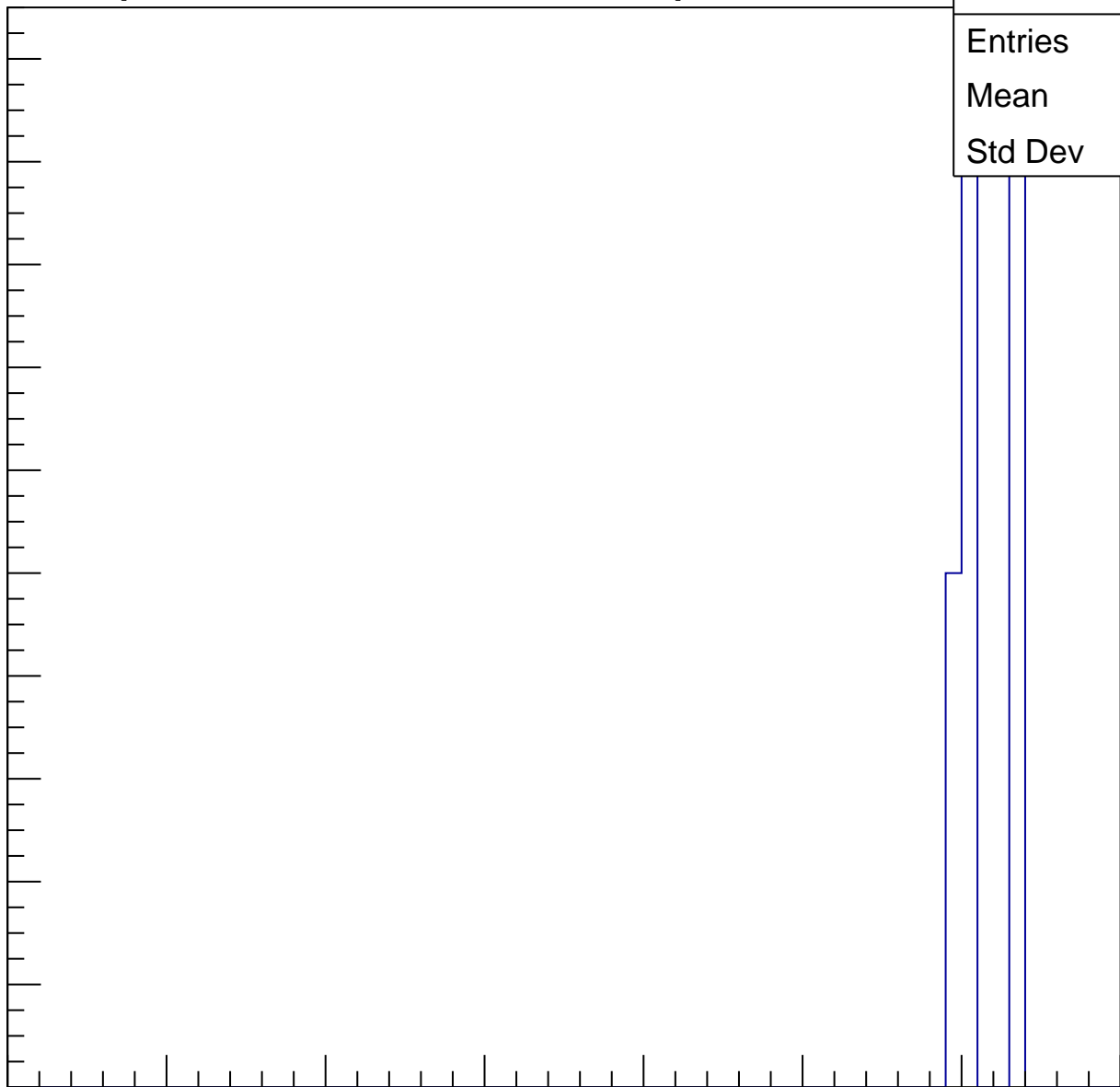
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61
Std Dev	1.673

0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch97, adc0

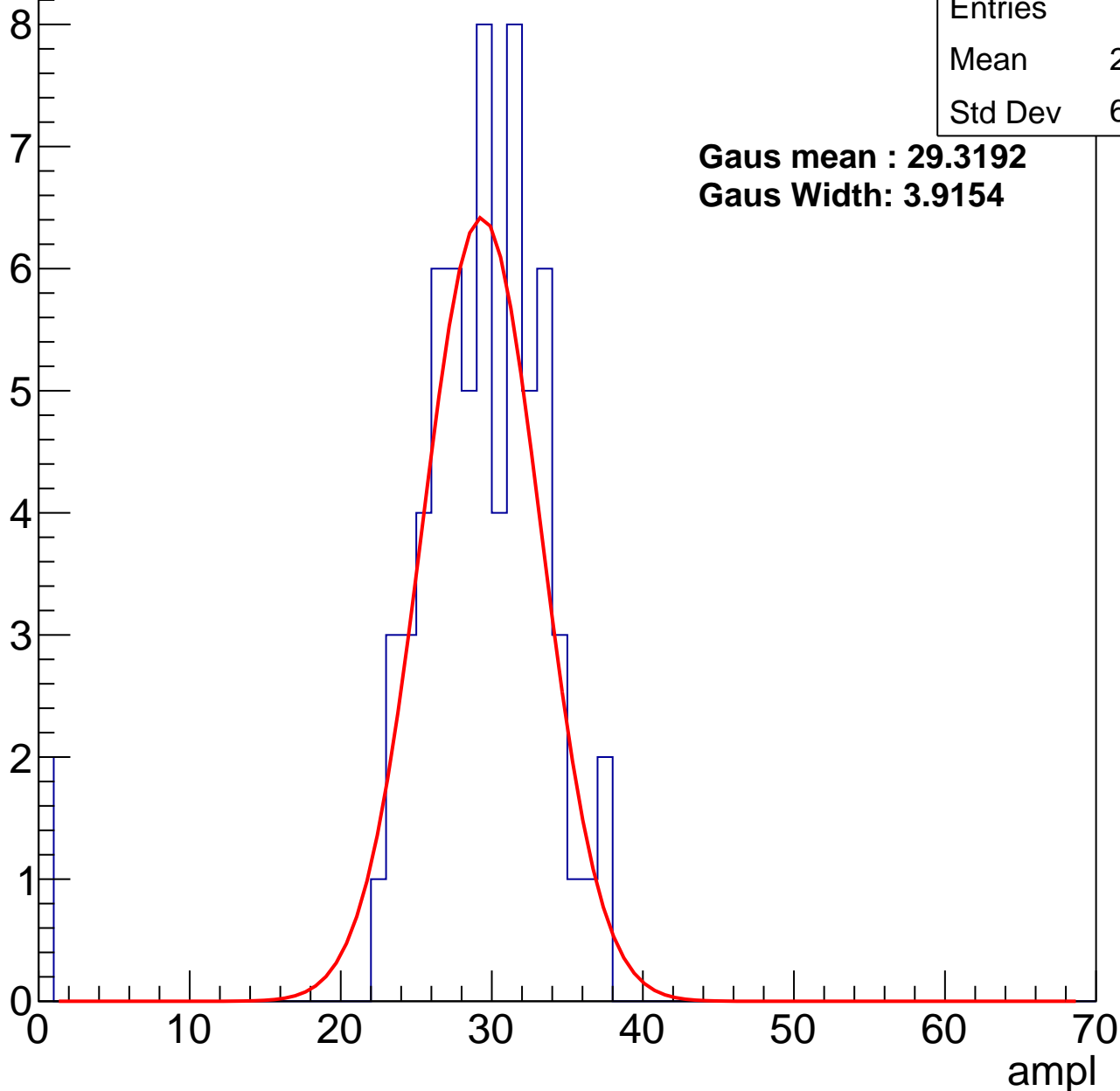
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.32
Std Dev	6.057

Gaus mean : 29.3192

Gaus Width: 3.9154



B1L103S, U5-ch97, adc1

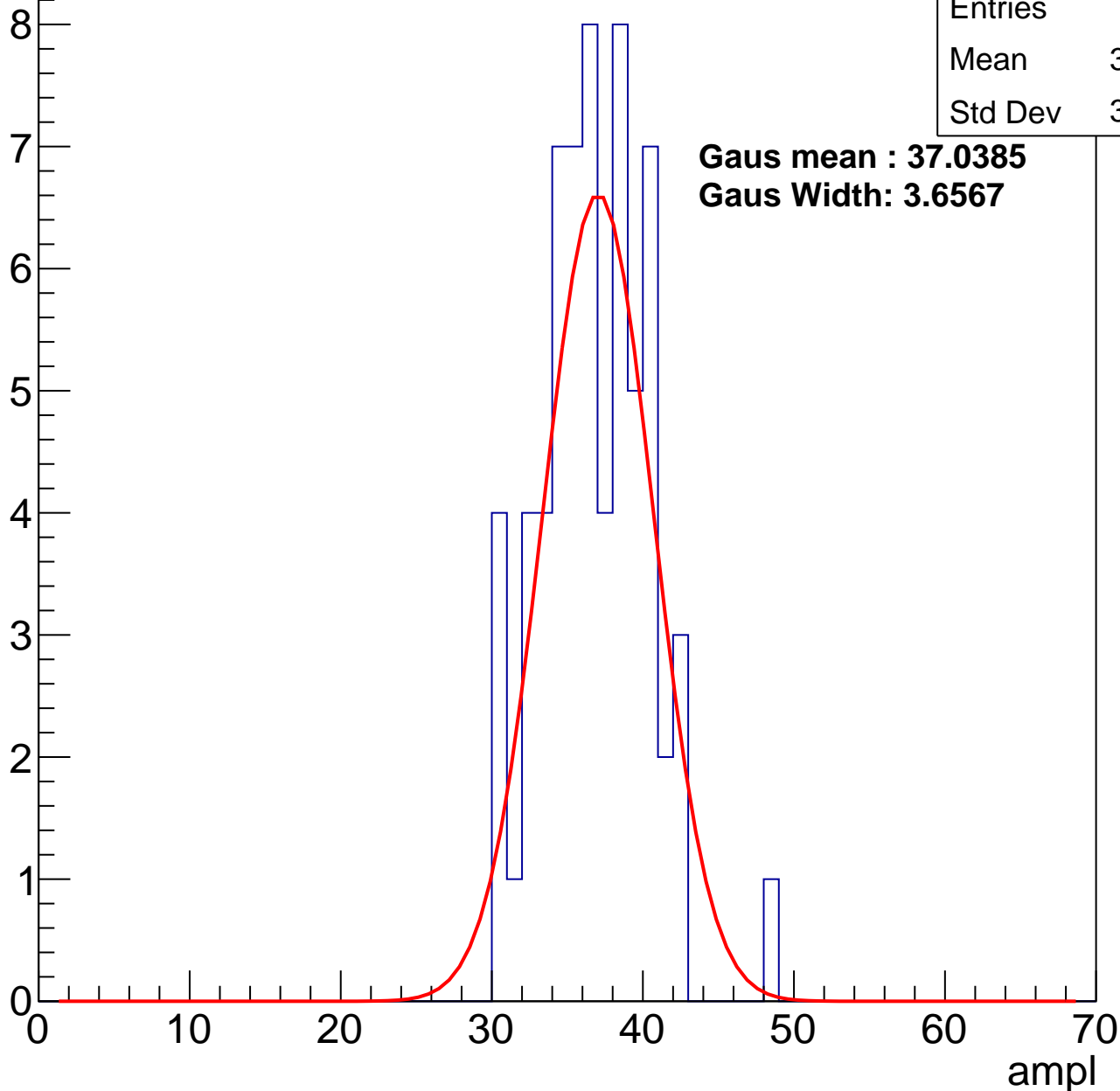
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.38
Std Dev	3.485

Gaus mean : 37.0385

Gaus Width: 3.6567



B1L103S, U5-ch97, adc2

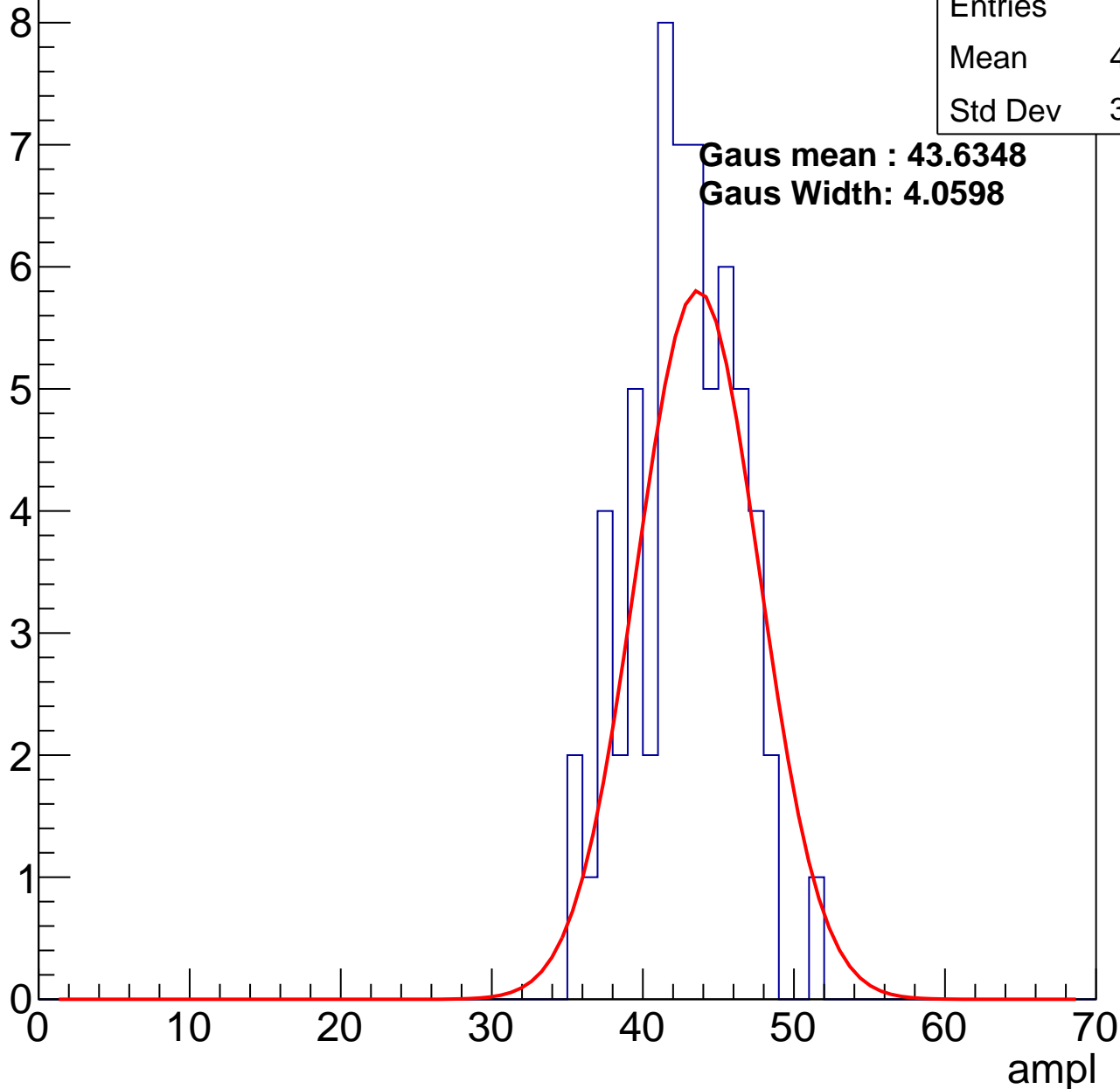
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.34
Std Dev	3.468

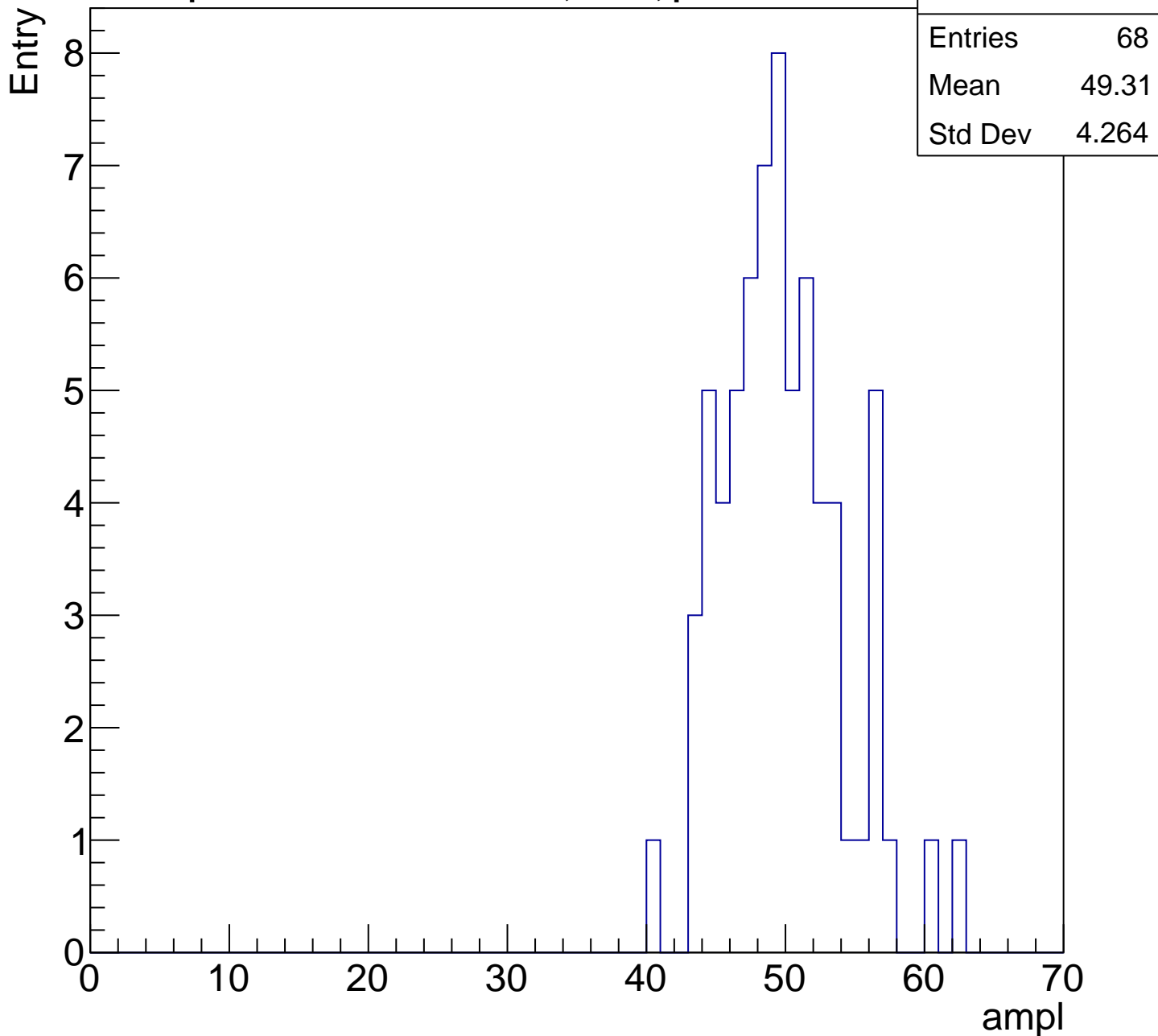
Gaus mean : 43.6348

Gaus Width: 4.0598



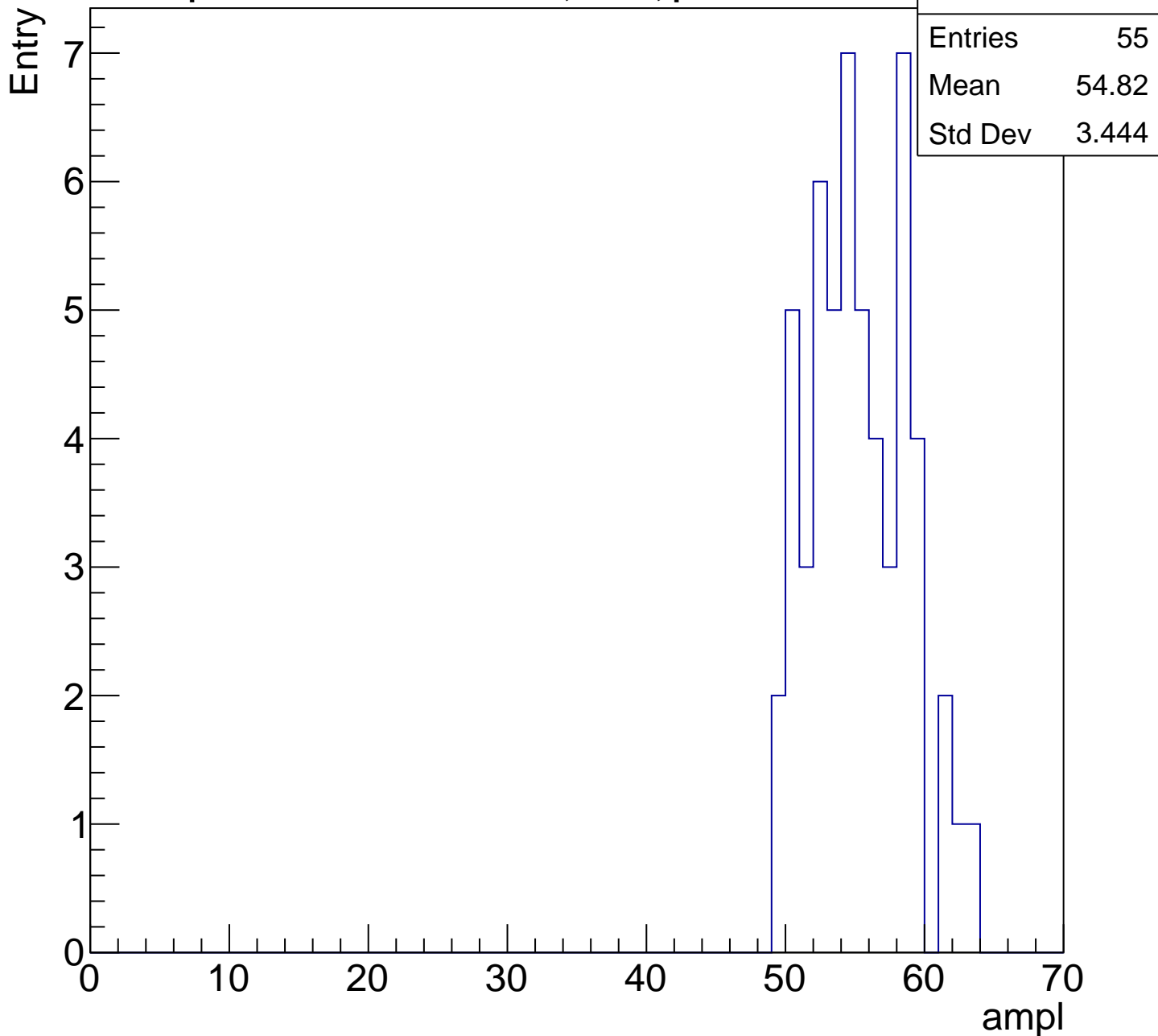
B1L103S, U5-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

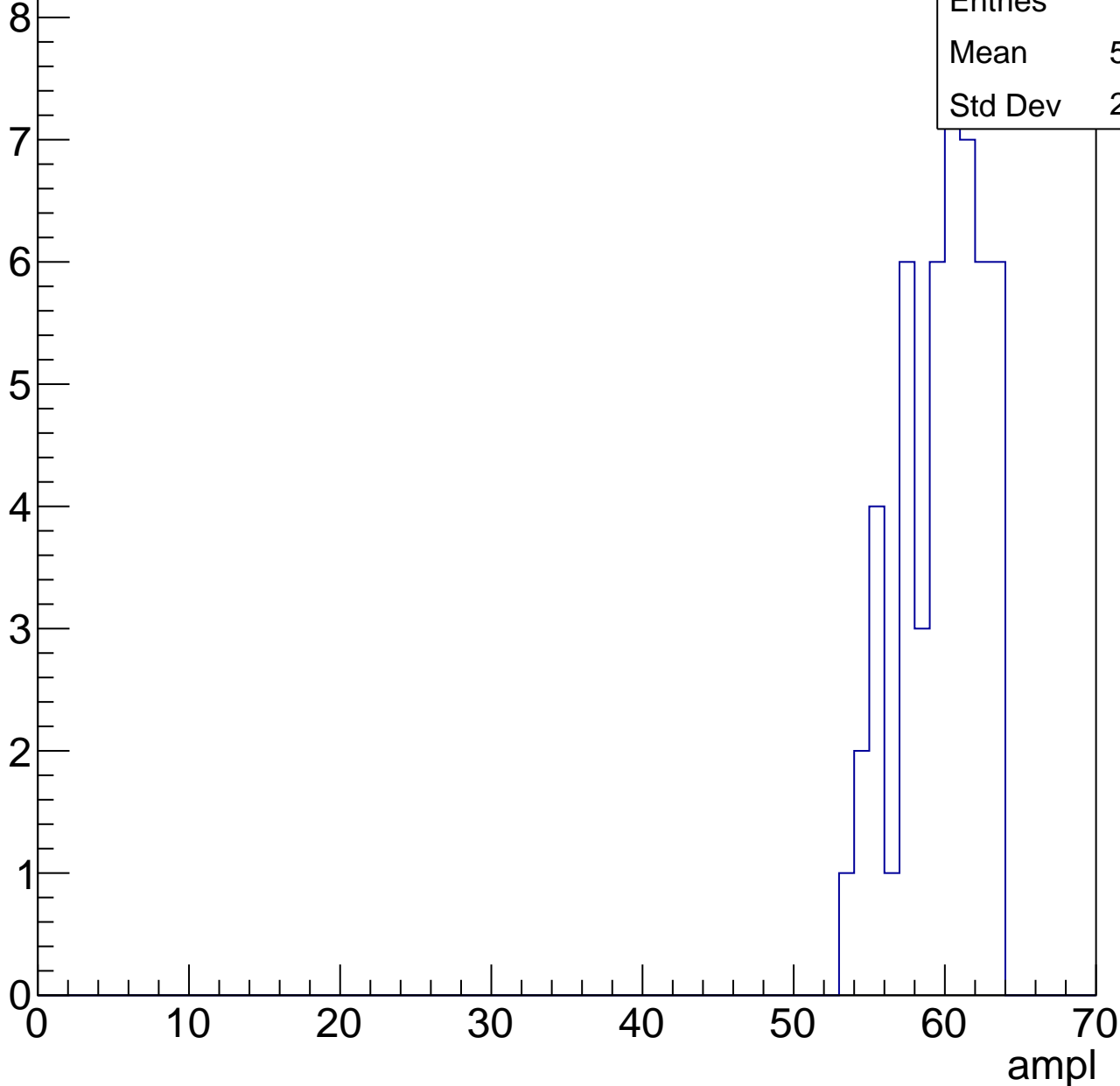


B1L103S, U5-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

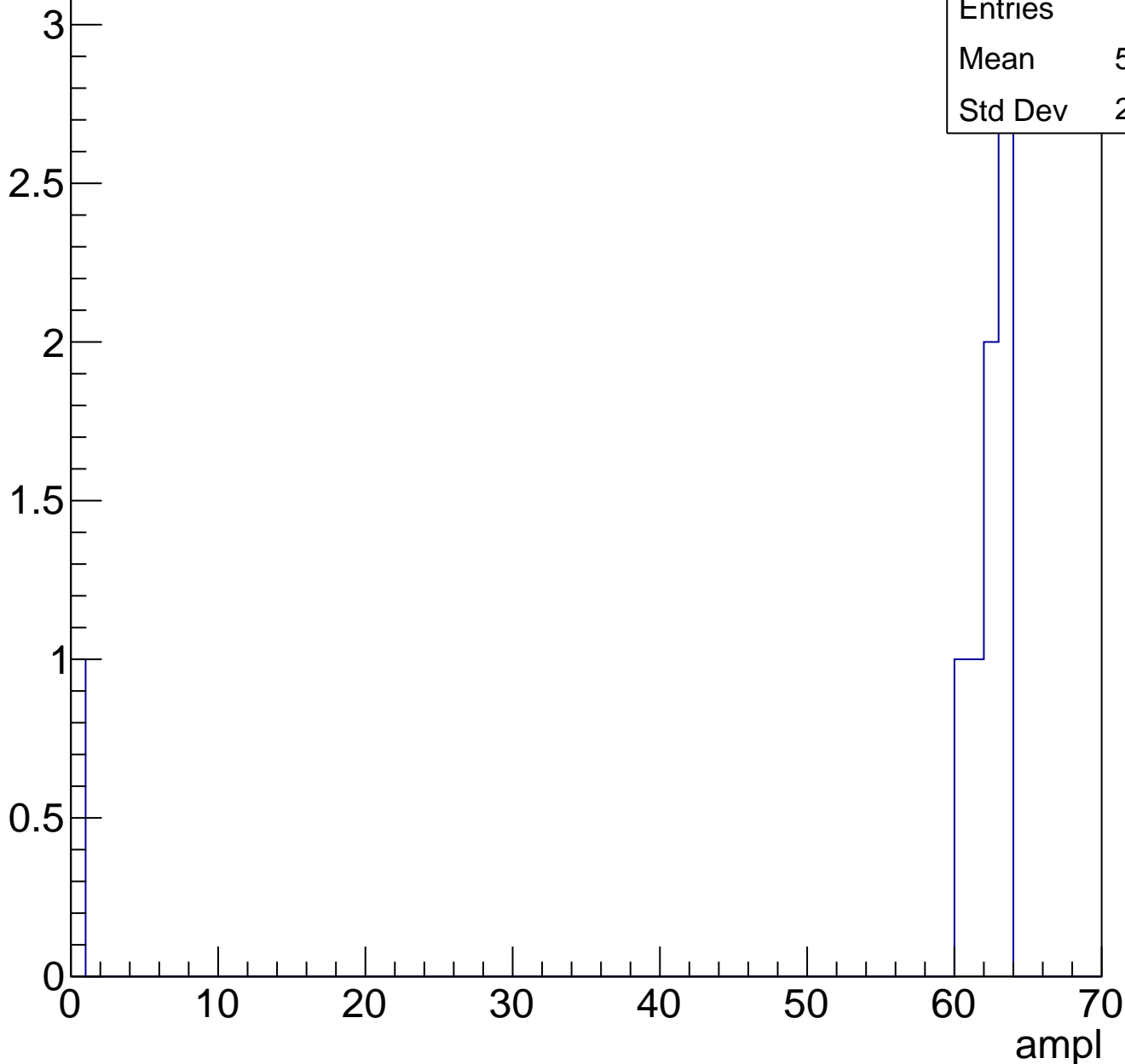
Entries	50
Mean	59.28
Std Dev	2.713



B1L103S, U5-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch98, adc0

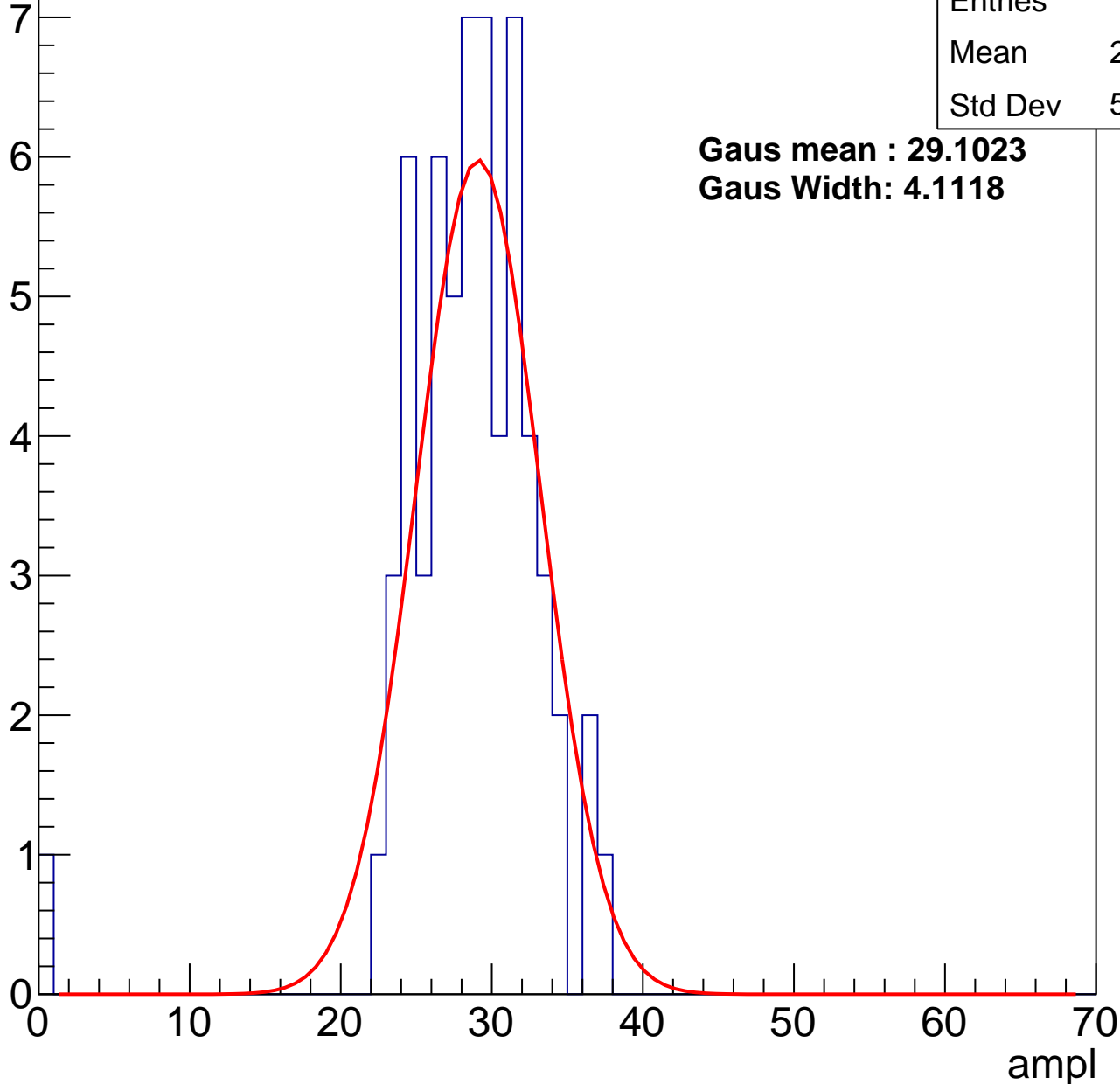
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.08
Std Dev	5.004

Gaus mean : 29.1023

Gaus Width: 4.1118



B1L103S, U5-ch98, adc1

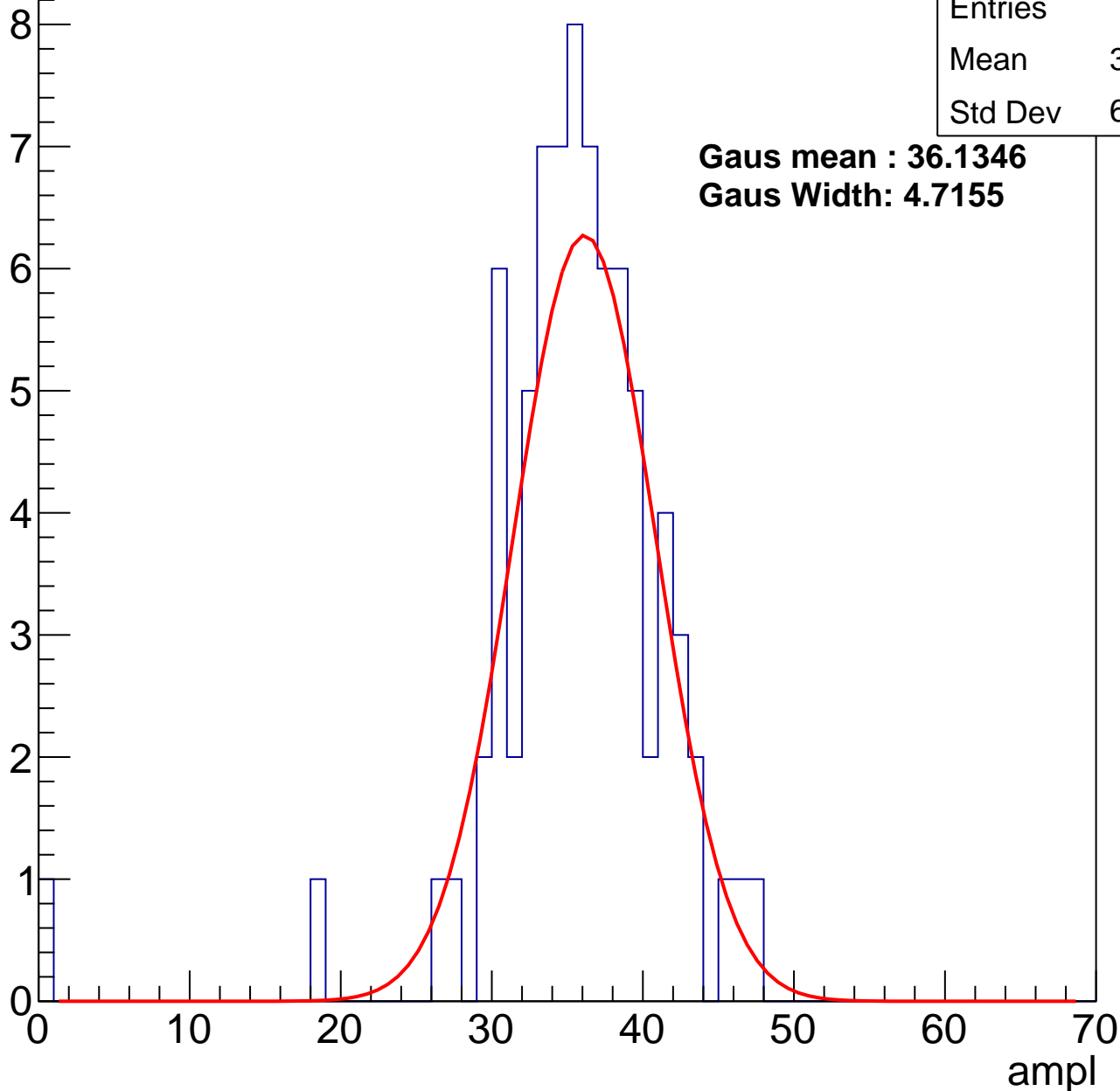
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	35.08
Std Dev	6.154

Gaus mean : 36.1346

Gaus Width: 4.7155



B1L103S, U5-ch98, adc2

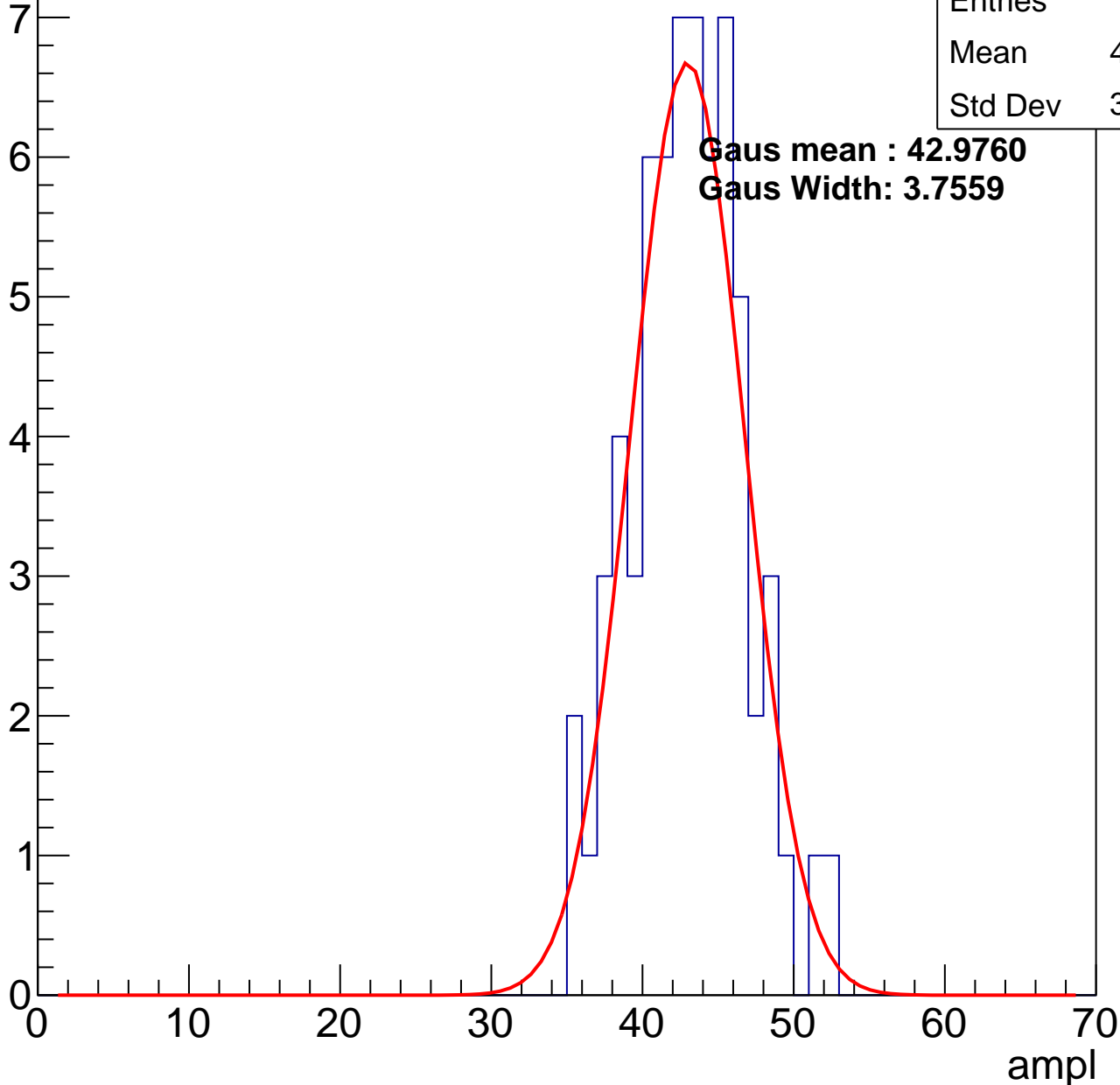
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.55
Std Dev	3.663

Gaus mean : 42.9760

Gaus Width: 3.7559

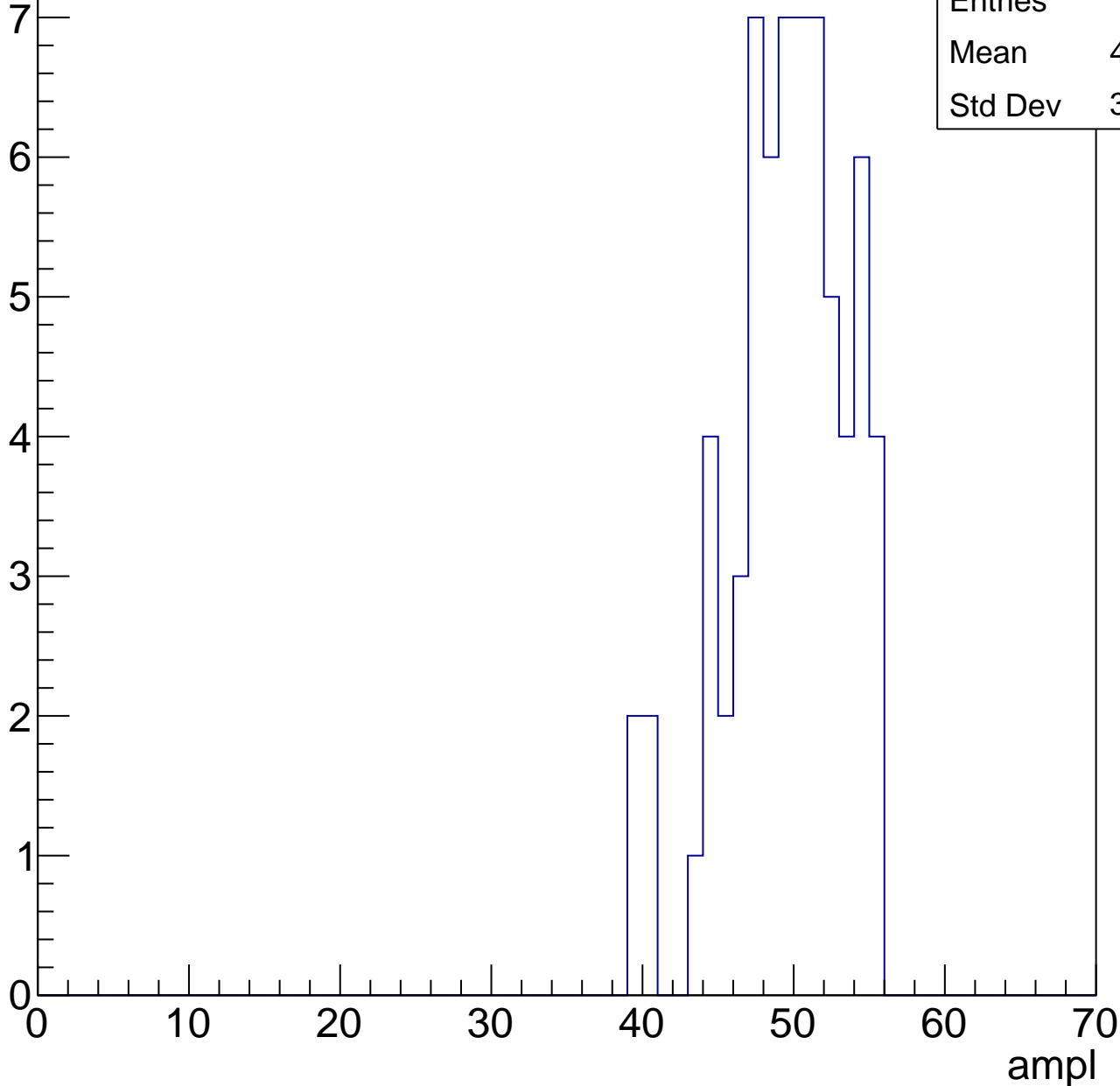


B1L103S, U5-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.07
Std Dev	3.918

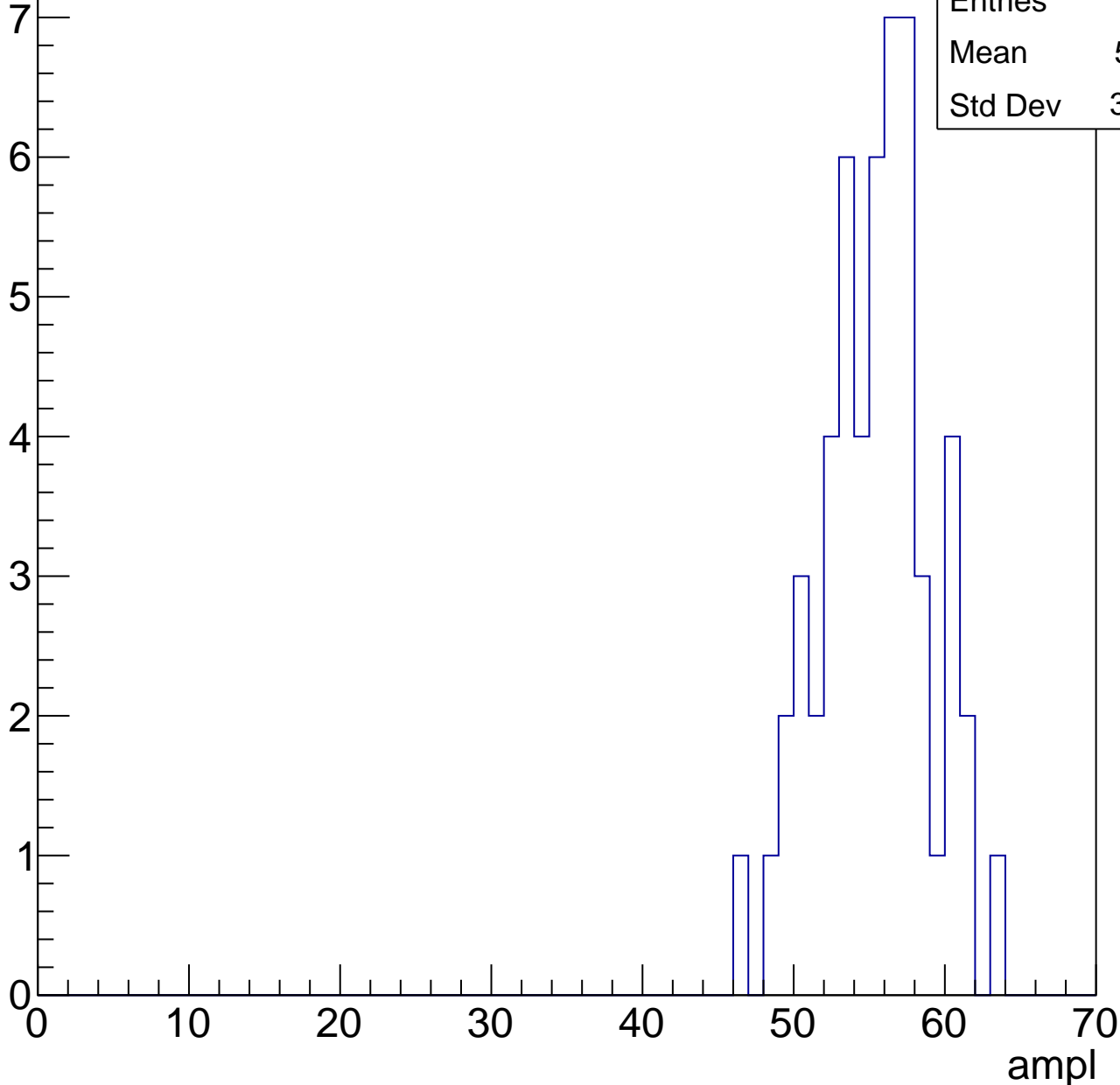


B1L103S, U5-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

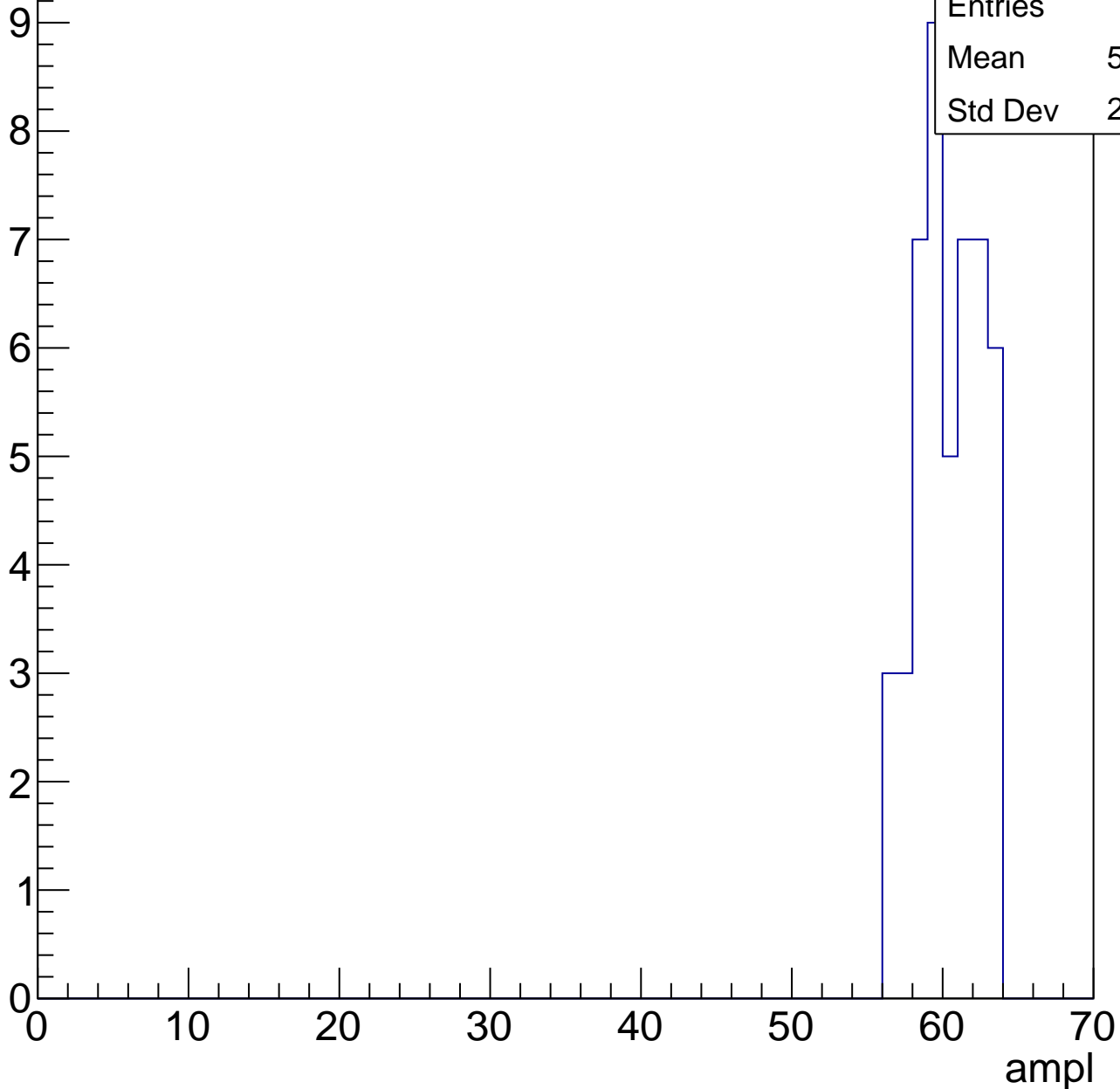
Entries	54
Mean	54.91
Std Dev	3.566



B1L103S, U5-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

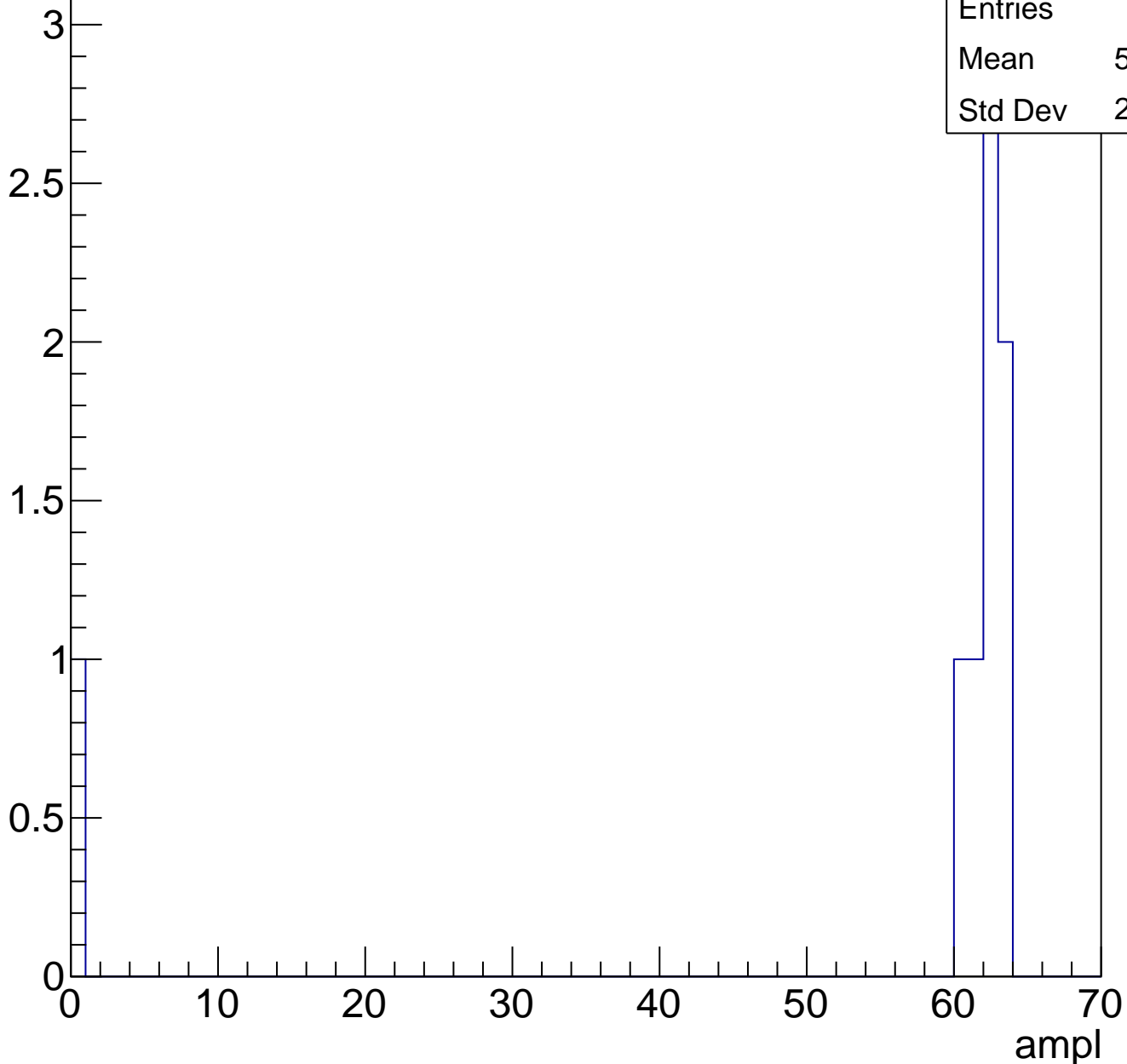


Entries	47
Mean	59.89
Std Dev	2.065

B1L103S, U5-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch99, adc0

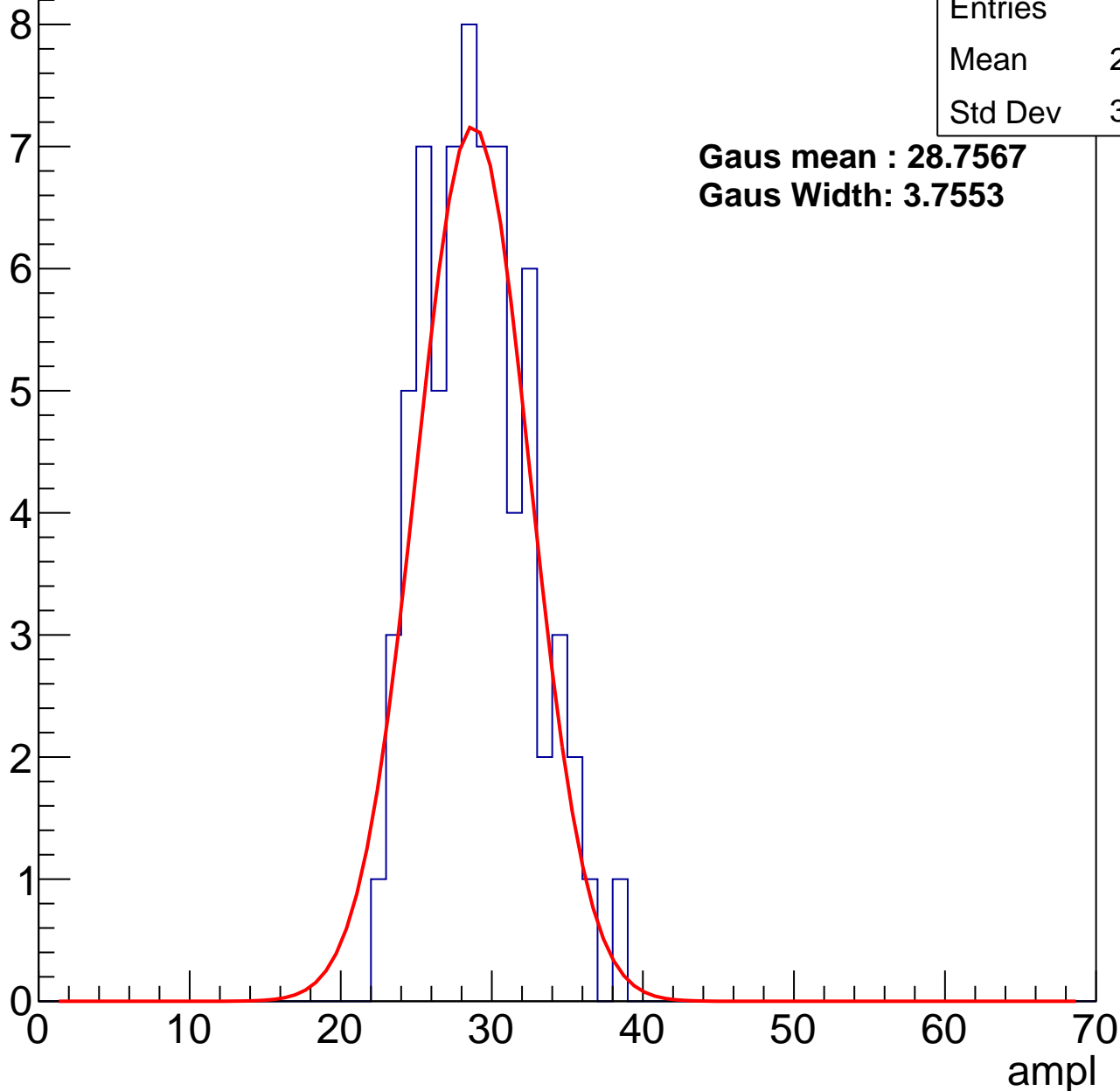
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	28.55
Std Dev	3.504

Gaus mean : 28.7567

Gaus Width: 3.7553



B1L103S, U5-ch99, adc1

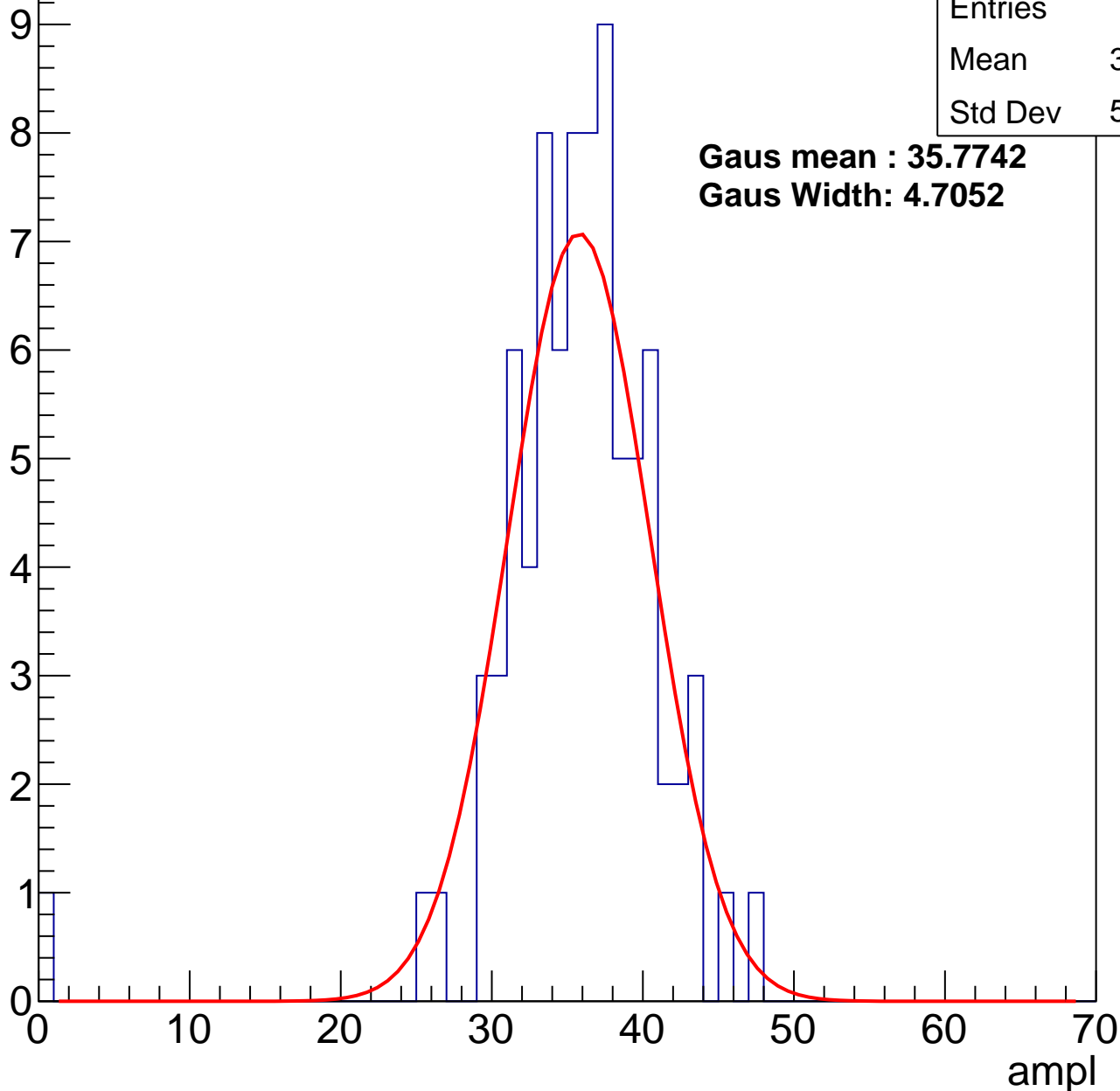
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	35.22
Std Dev	5.682

Gaus mean : 35.7742

Gaus Width: 4.7052

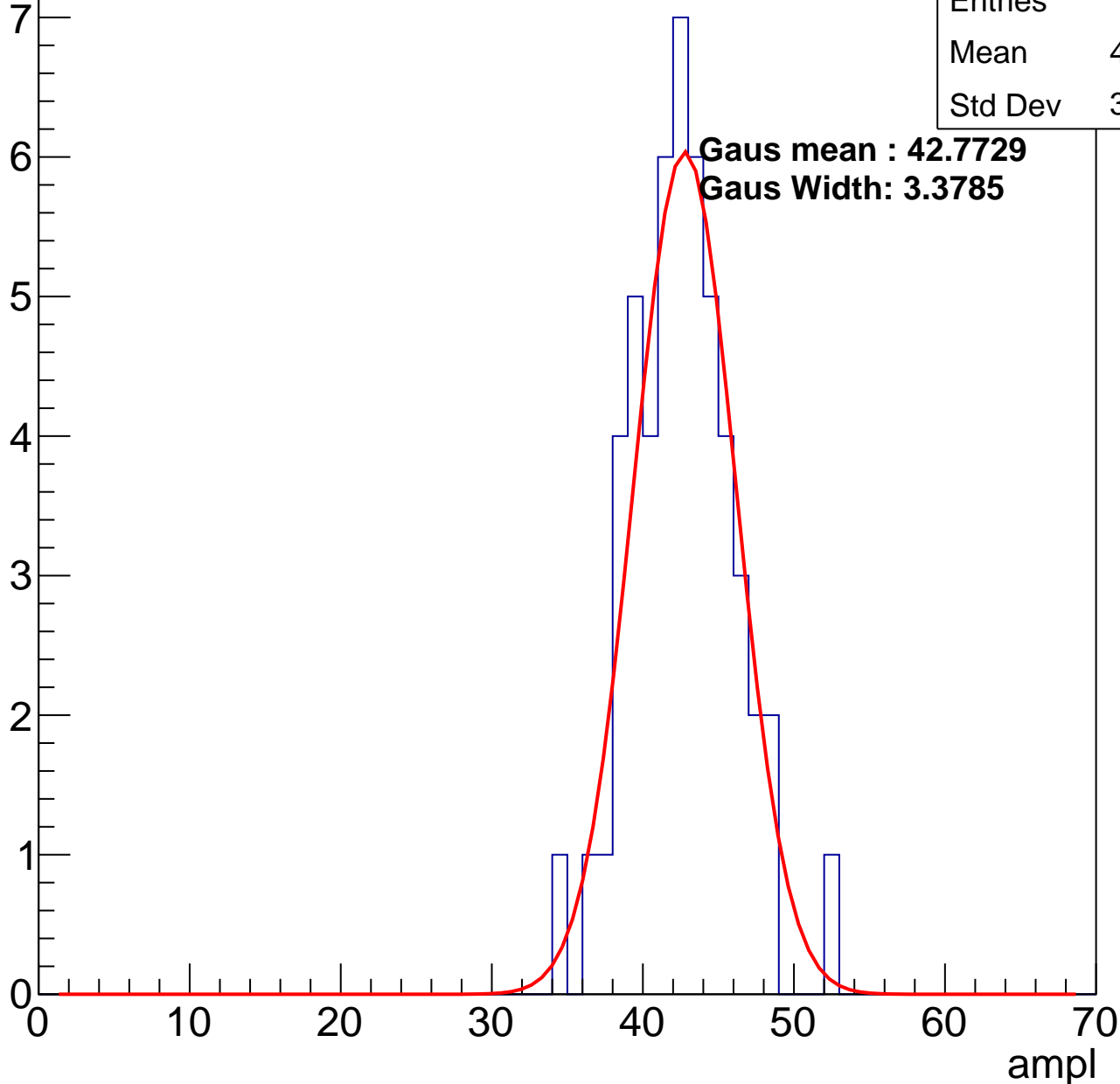


B1L103S, U5-ch99, adc2

calib_packv5_042523_0143.root, FC#7, port C2

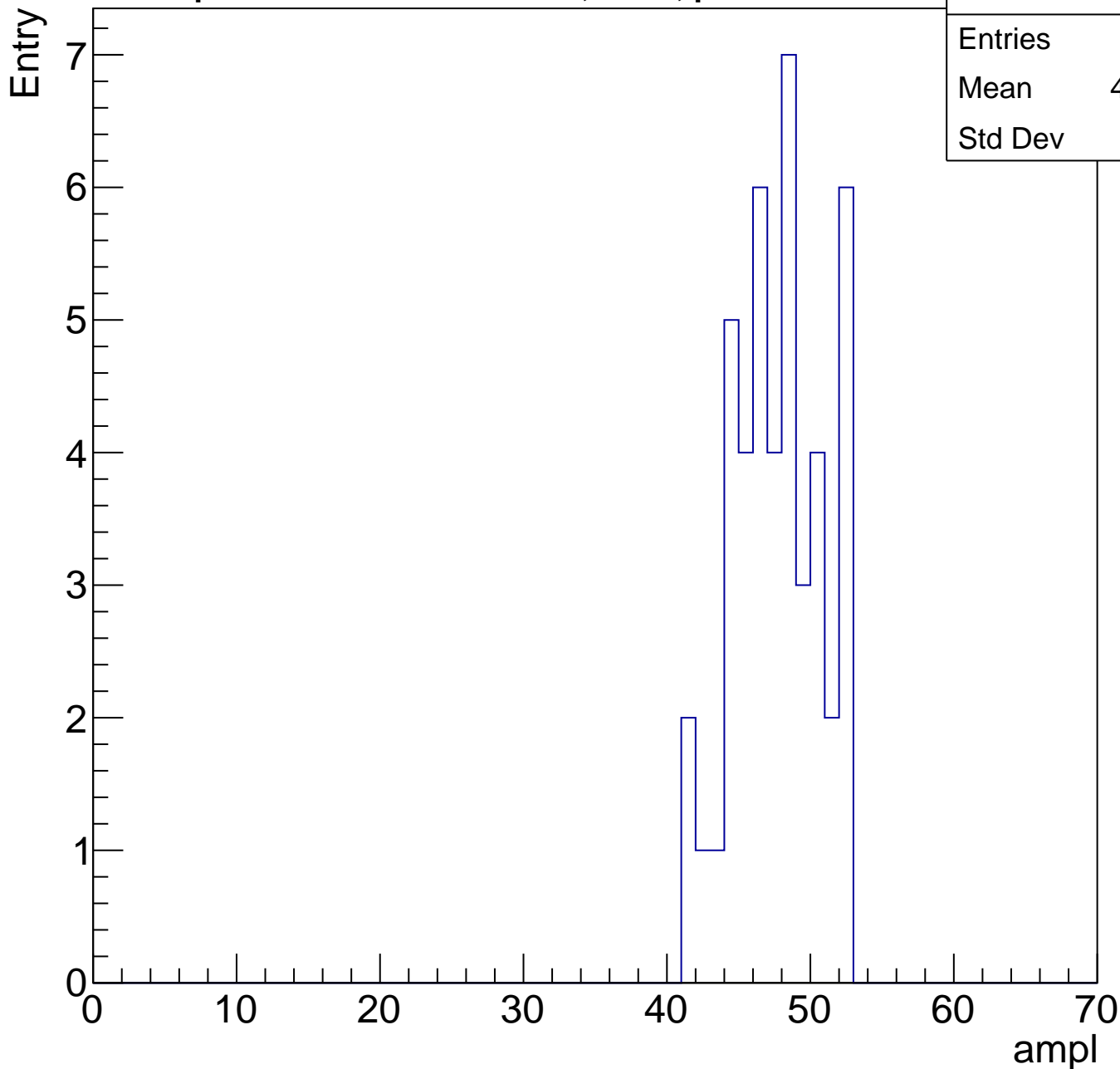
Entry

Entries	52
Mean	42.15
Std Dev	3.359



B1L103S, U5-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

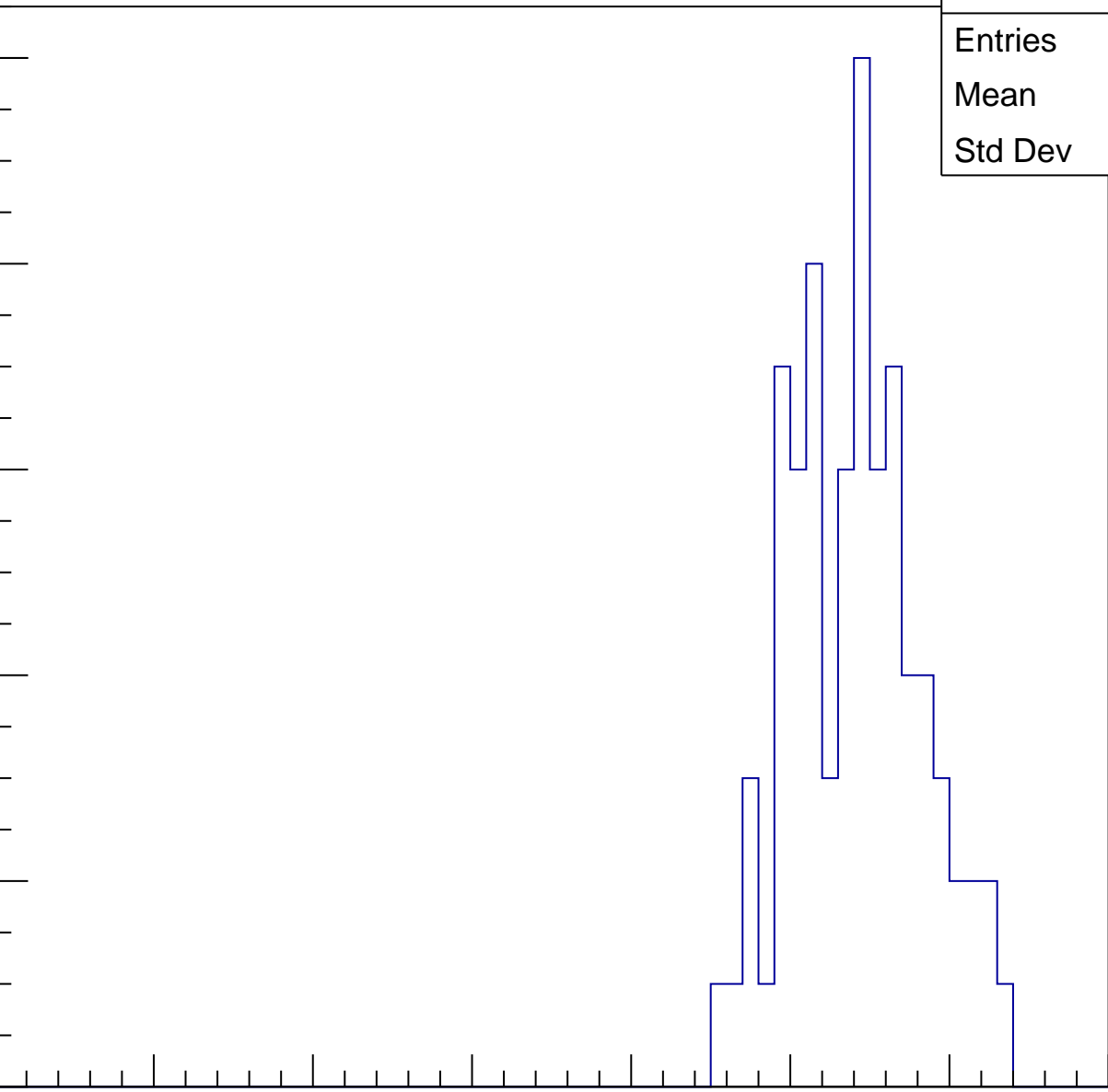
Entries	77
Mean	53.68
Std Dev	4.059

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

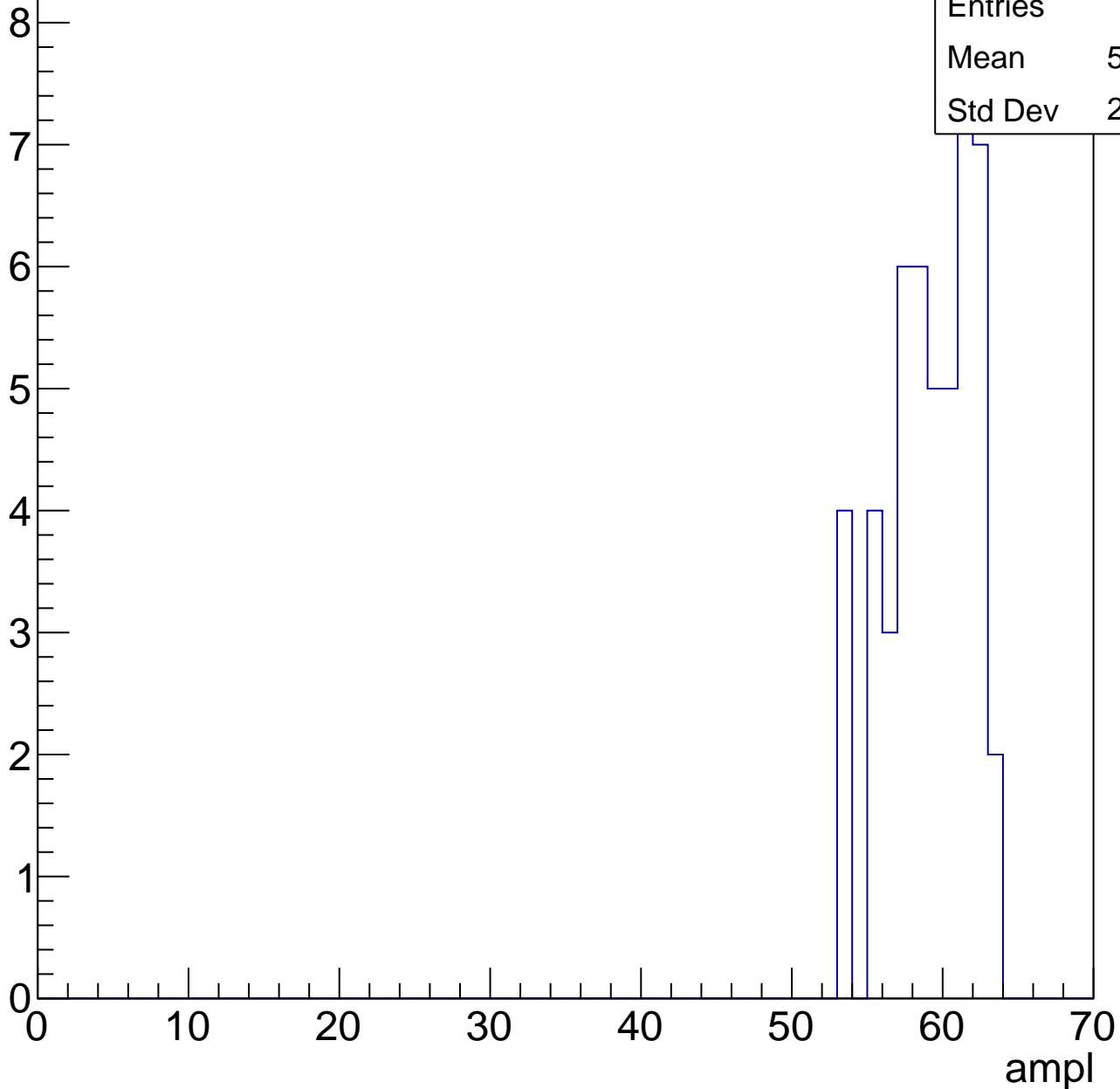


B1L103S, U5-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.66
Std Dev	2.797

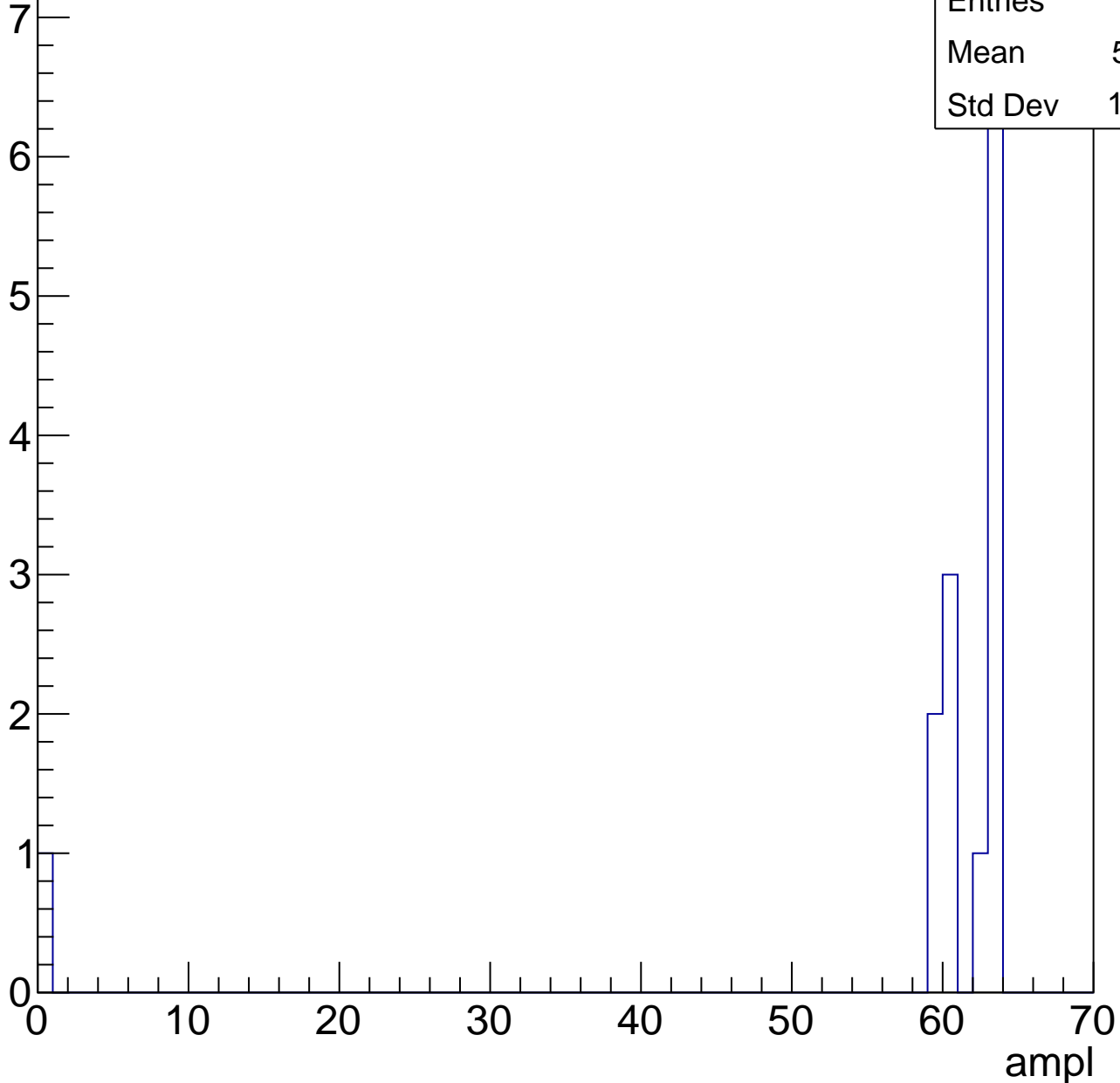


B1L103S, U5-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	57.21
Std Dev	15.95



B1L103S, U5-ch99, adc7

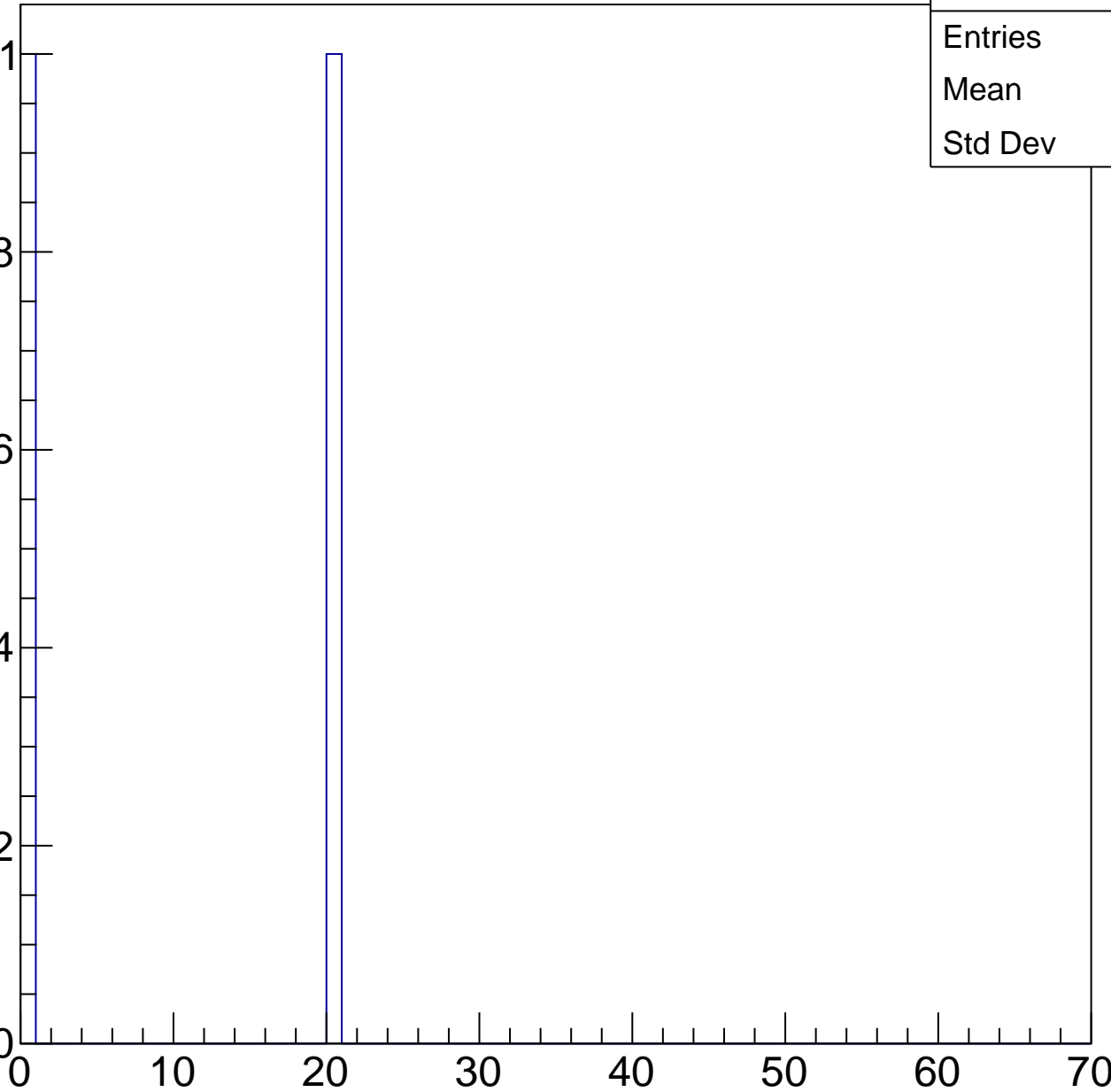
calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	10
Std Dev	10

ampl



B1L103S, U5-ch100, adc0

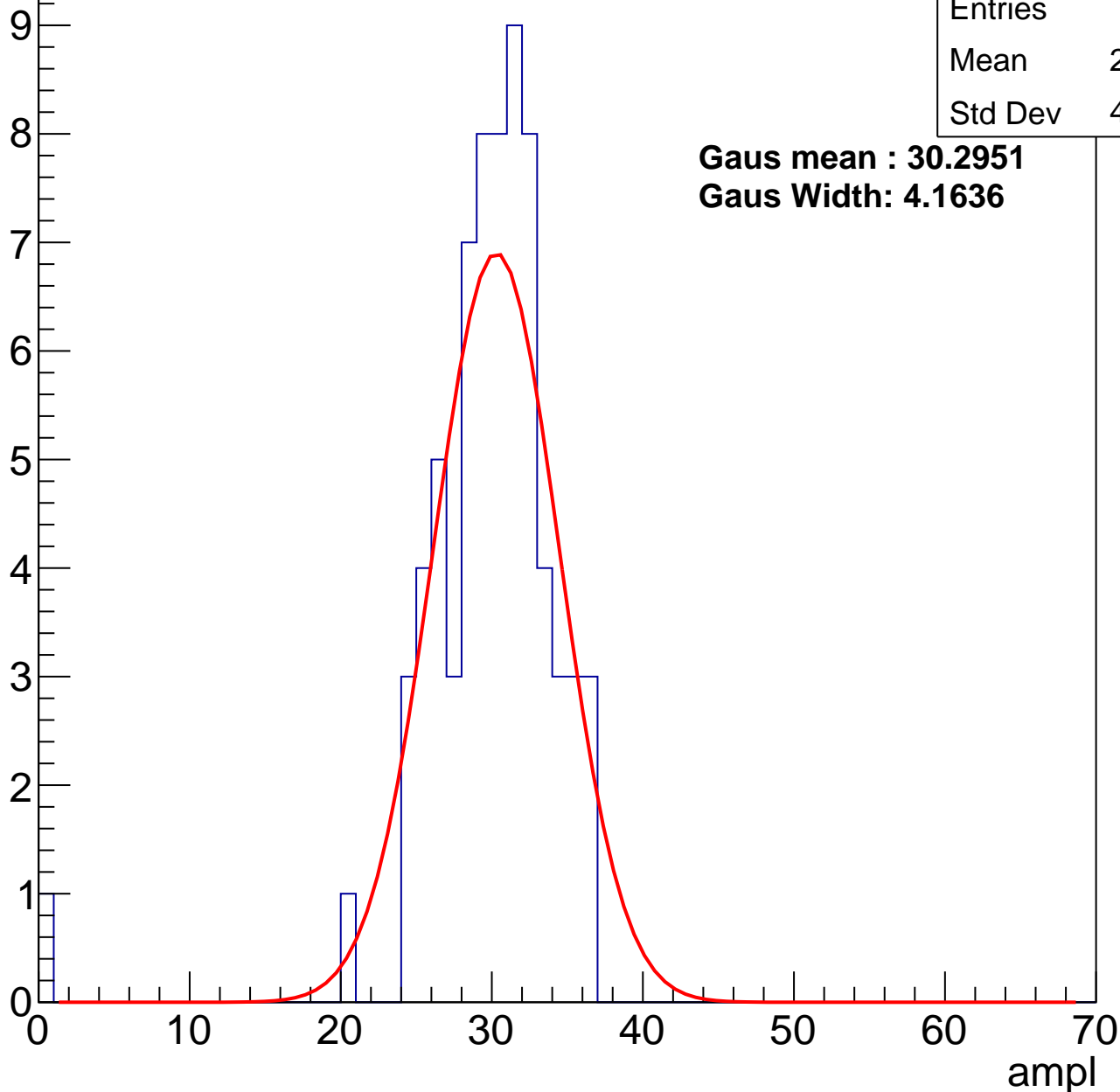
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.33
Std Dev	4.822

Gaus mean : 30.2951

Gaus Width: 4.1636



B1L103S, U5-ch100, adc1

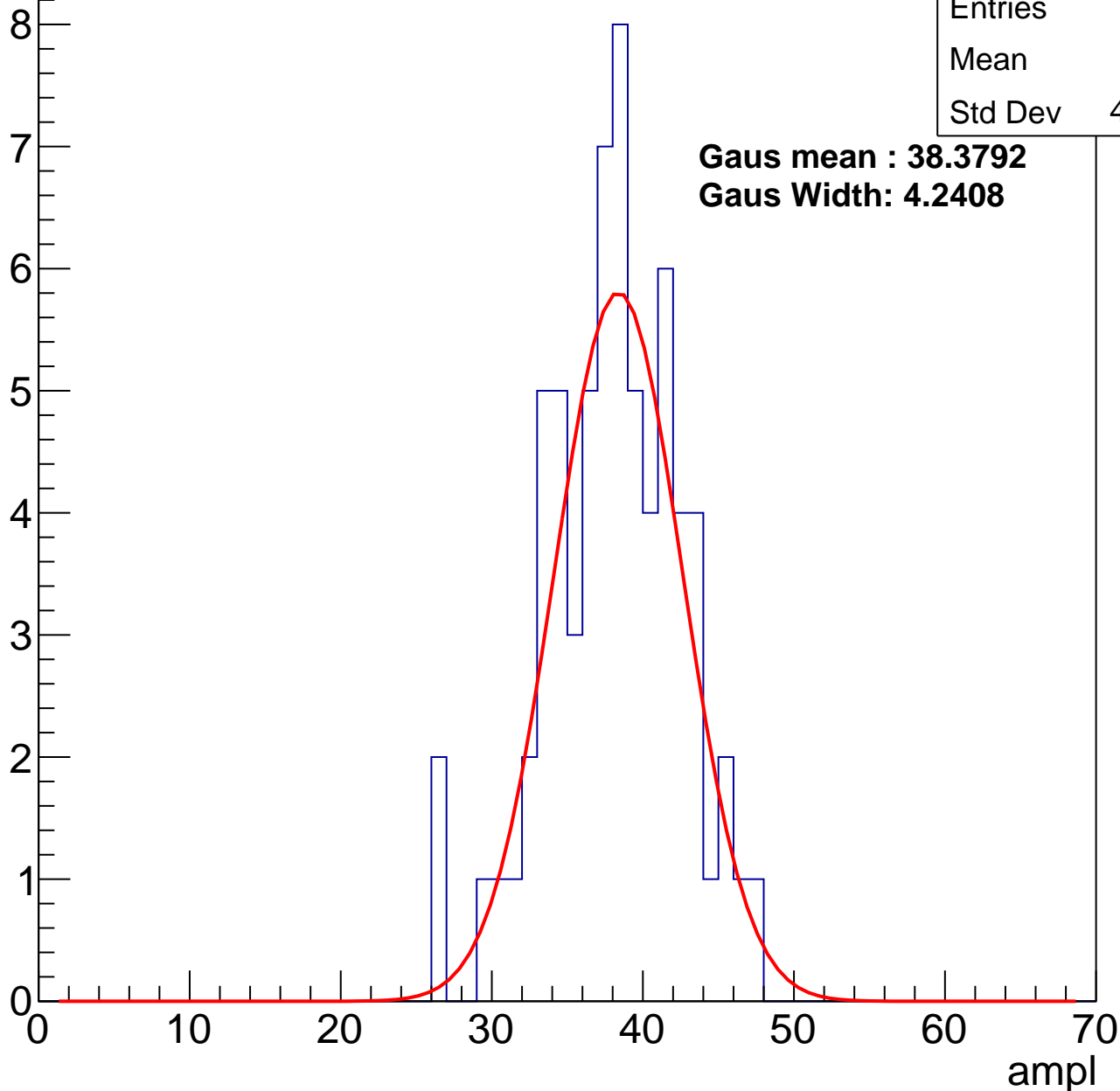
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.6
Std Dev	4.406

Gaus mean : 38.3792

Gaus Width: 4.2408



B1L103S, U5-ch100, adc2

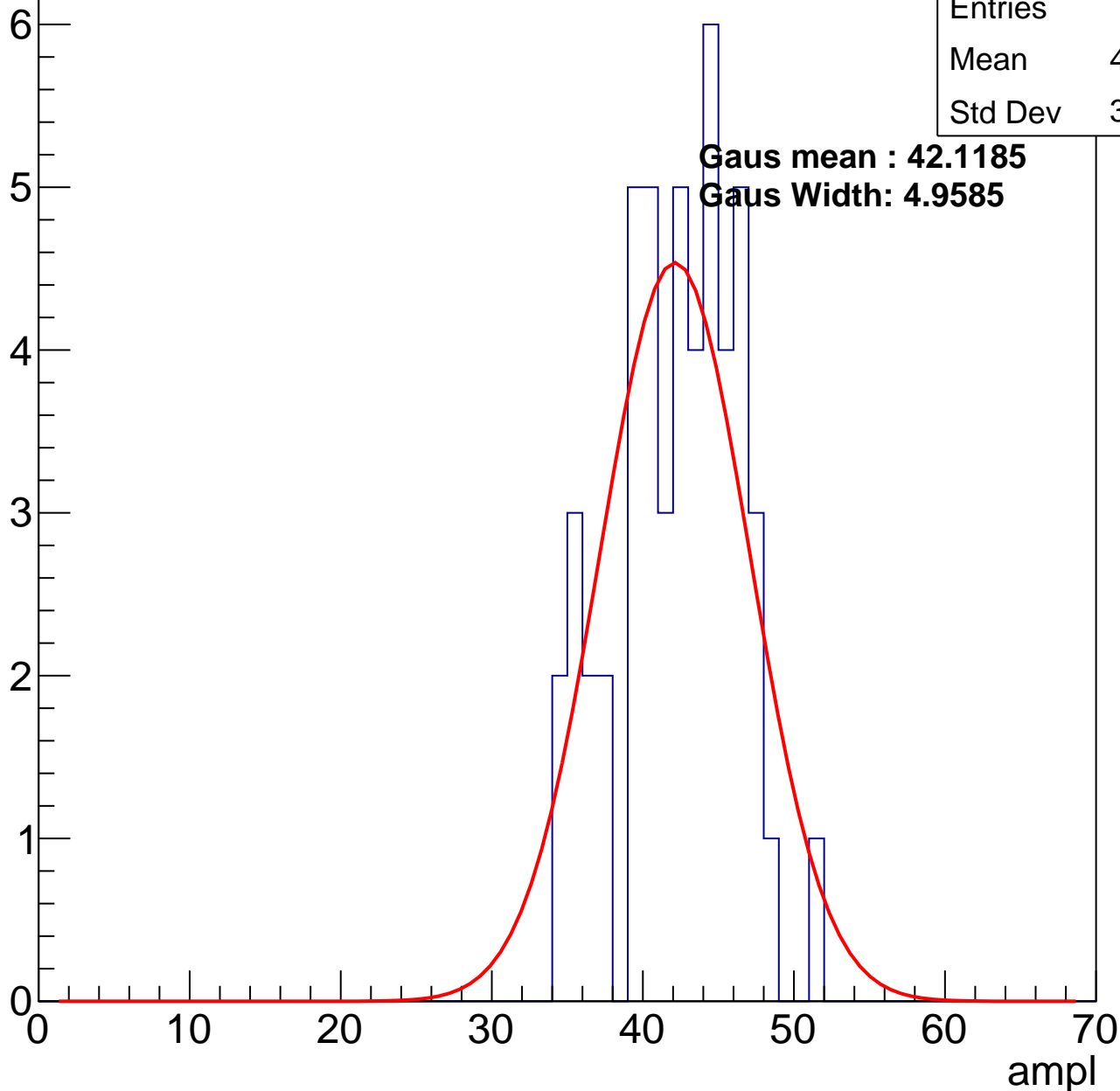
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.82
Std Dev	3.959

Gaus mean : 42.1185

Gaus Width: 4.9585

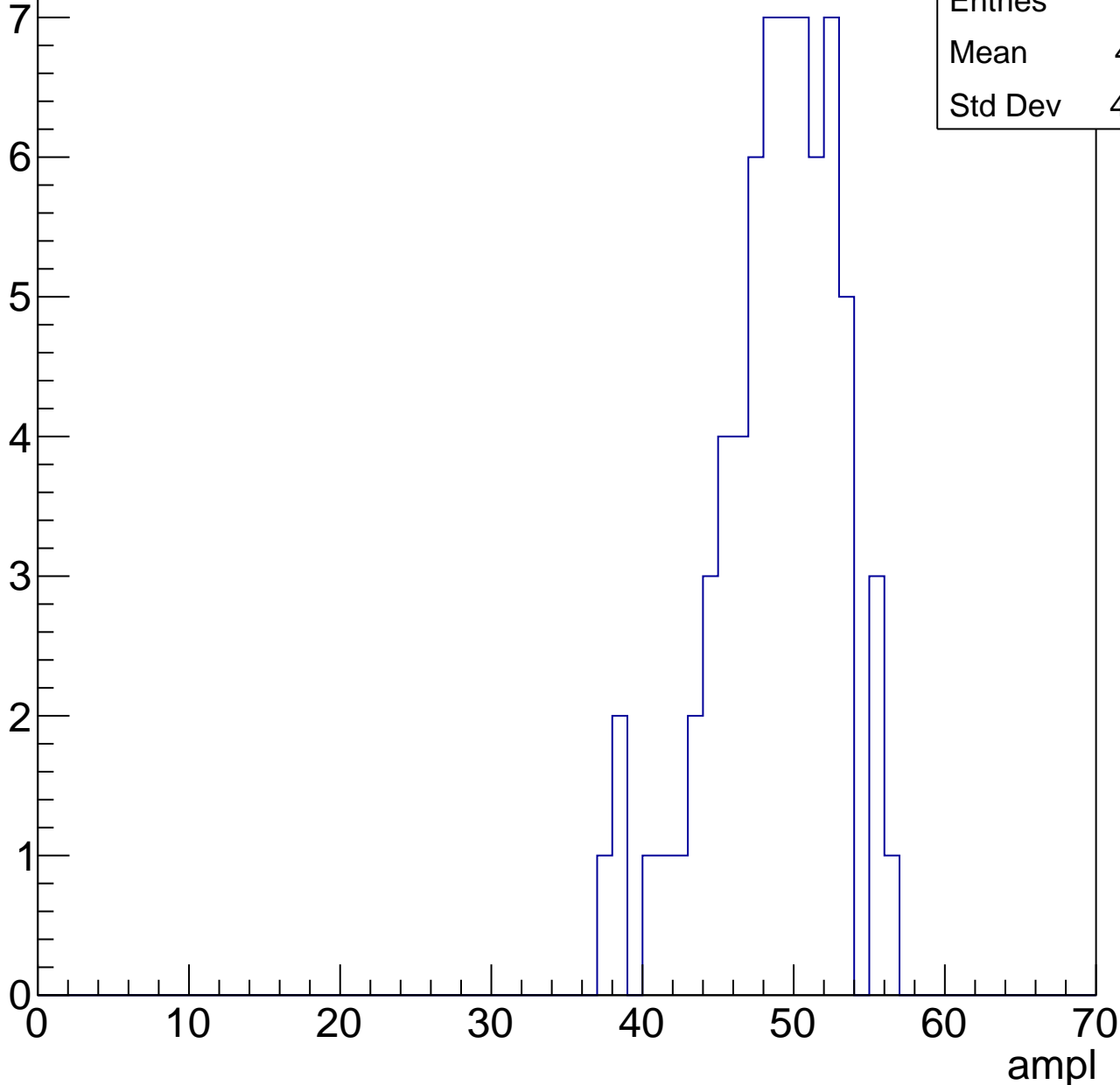


B1L103S, U5-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	48.31
Std Dev	4.124

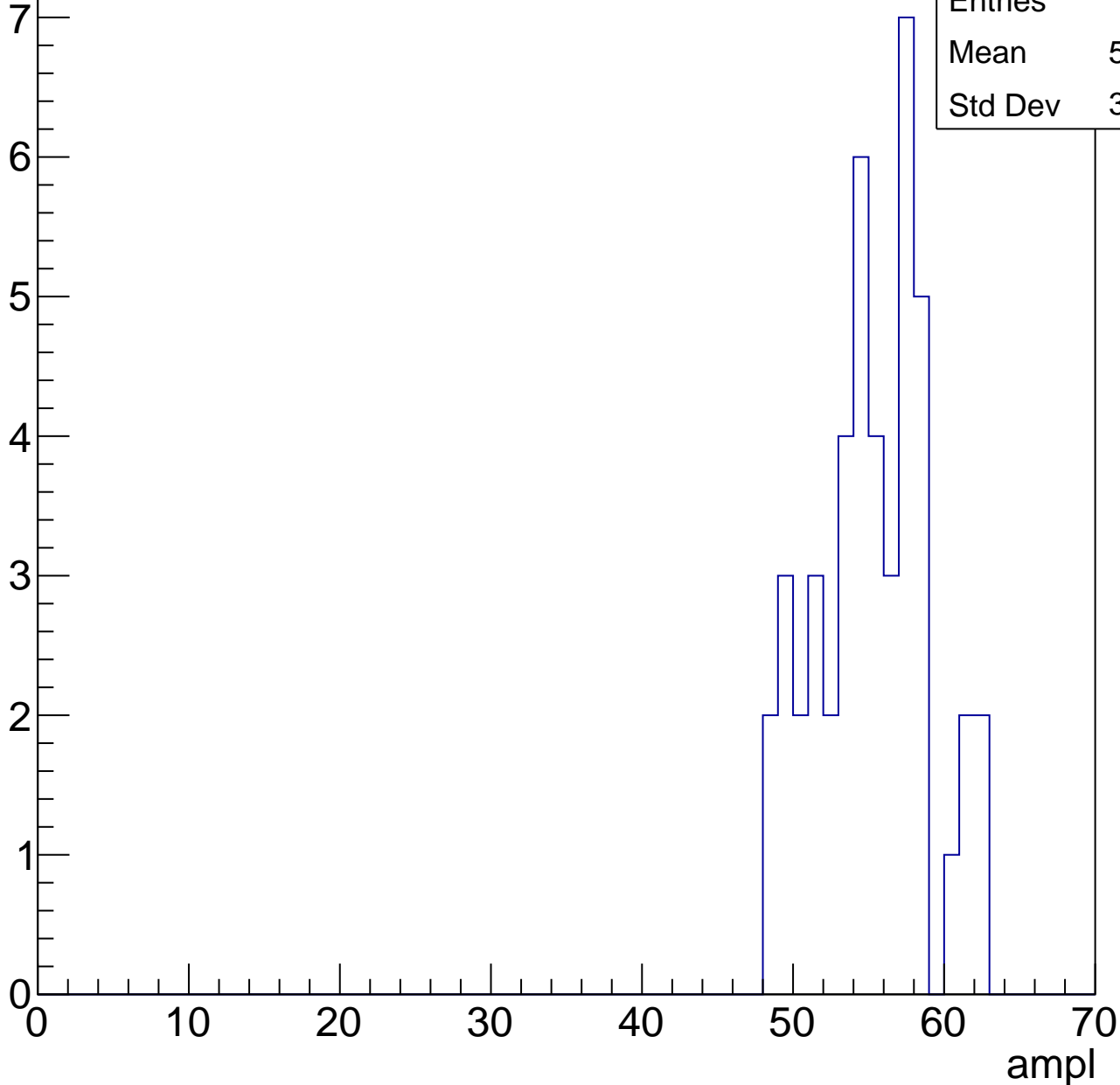


B1L103S, U5-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	54.76
Std Dev	3.649

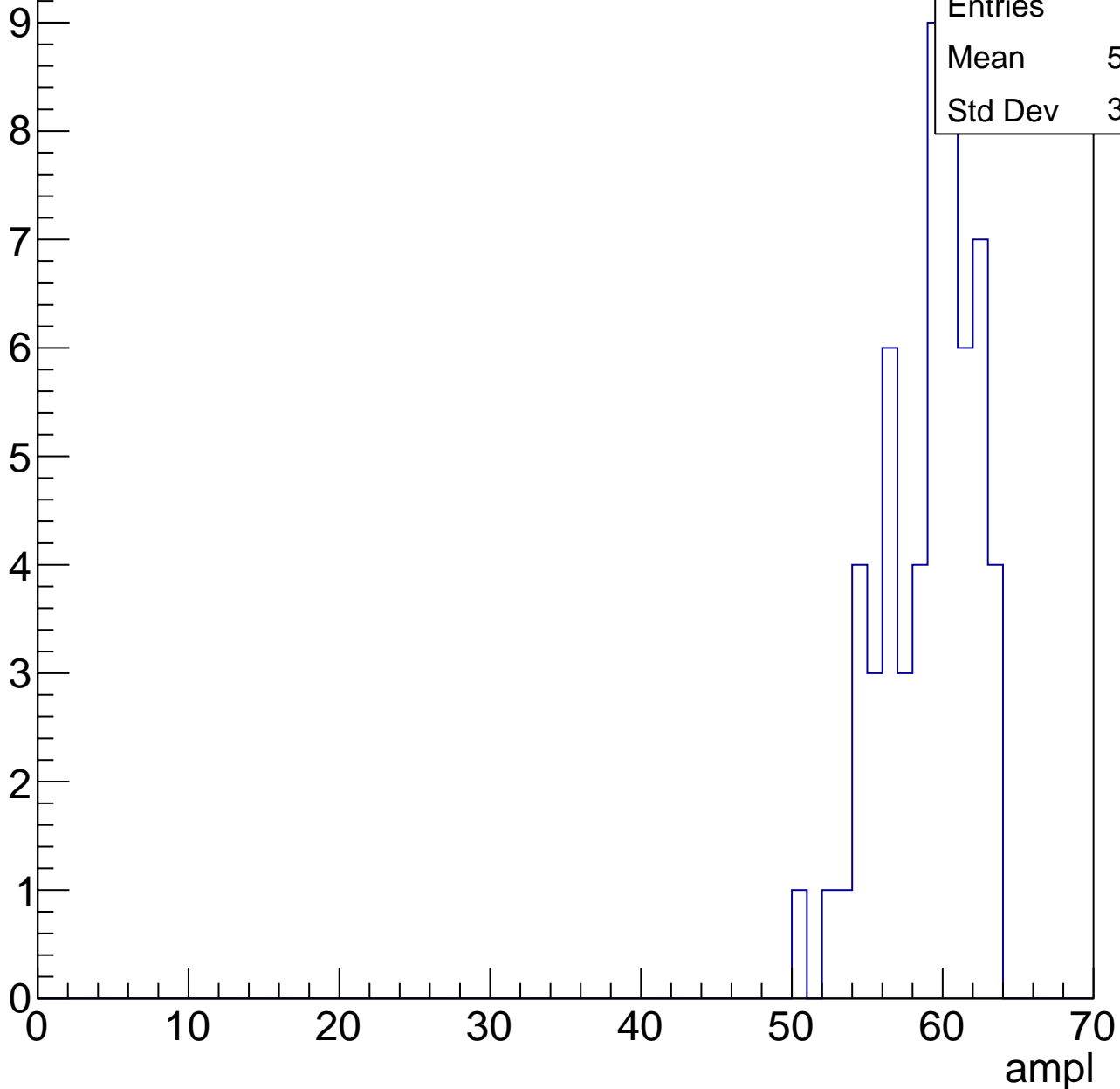


B1L103S, U5-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	58.56
Std Dev	3.055

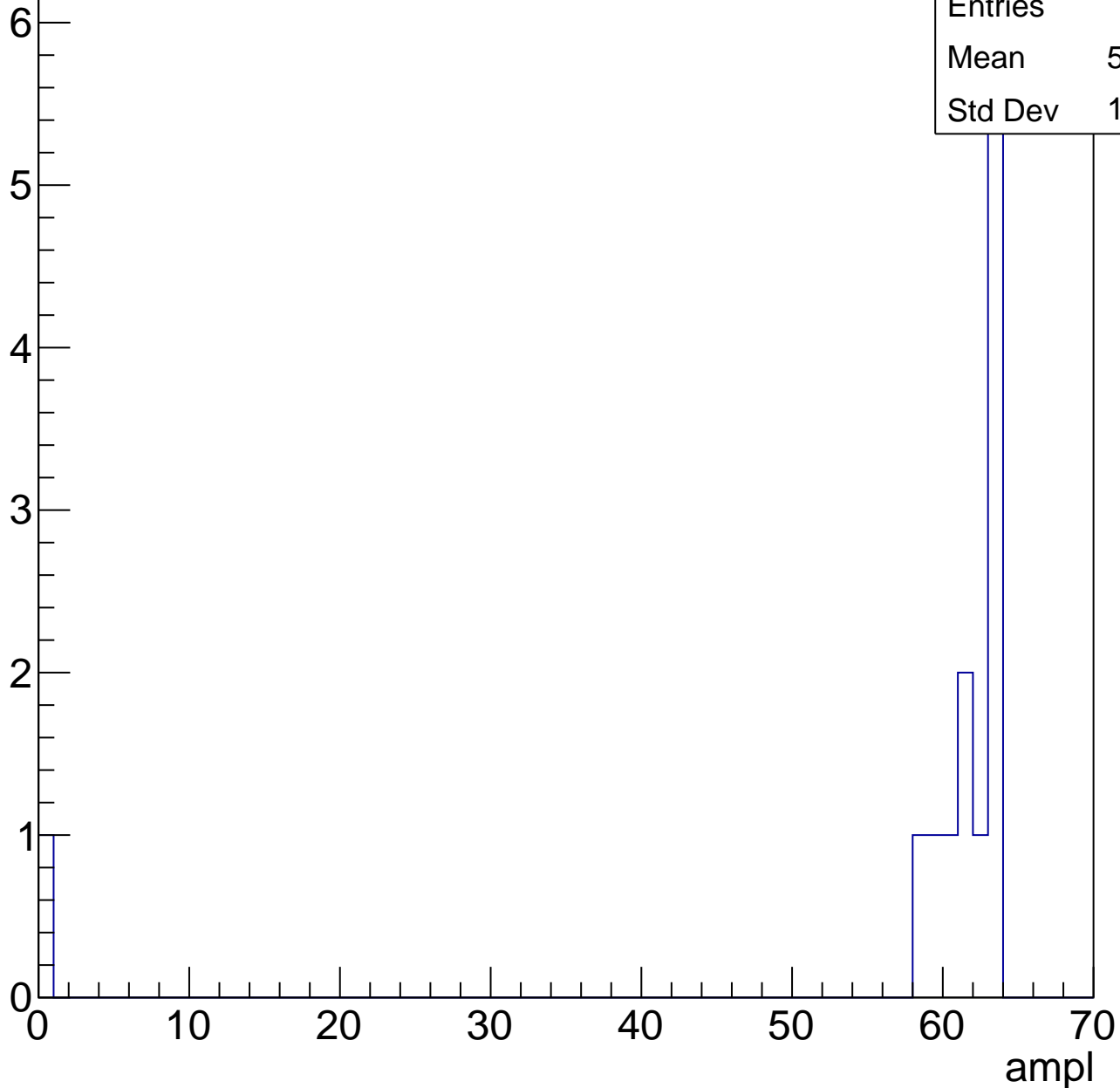


B1L103S, U5-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	56.85
Std Dev	16.49



B1L103S, U5-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch101, adc0

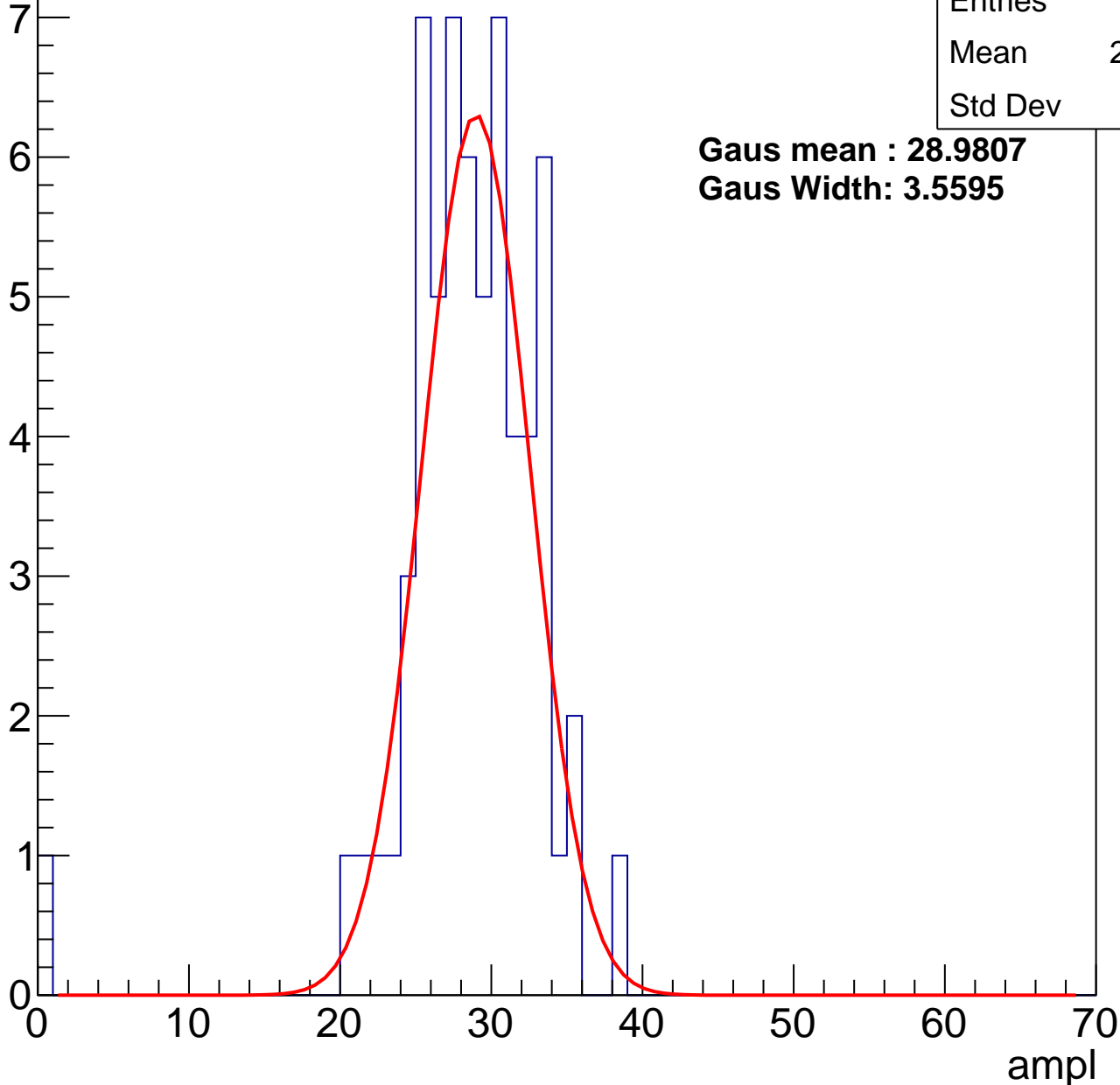
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.05
Std Dev	5.06

Gaus mean : 28.9807

Gaus Width: 3.5595



B1L103S, U5-ch101, adc1

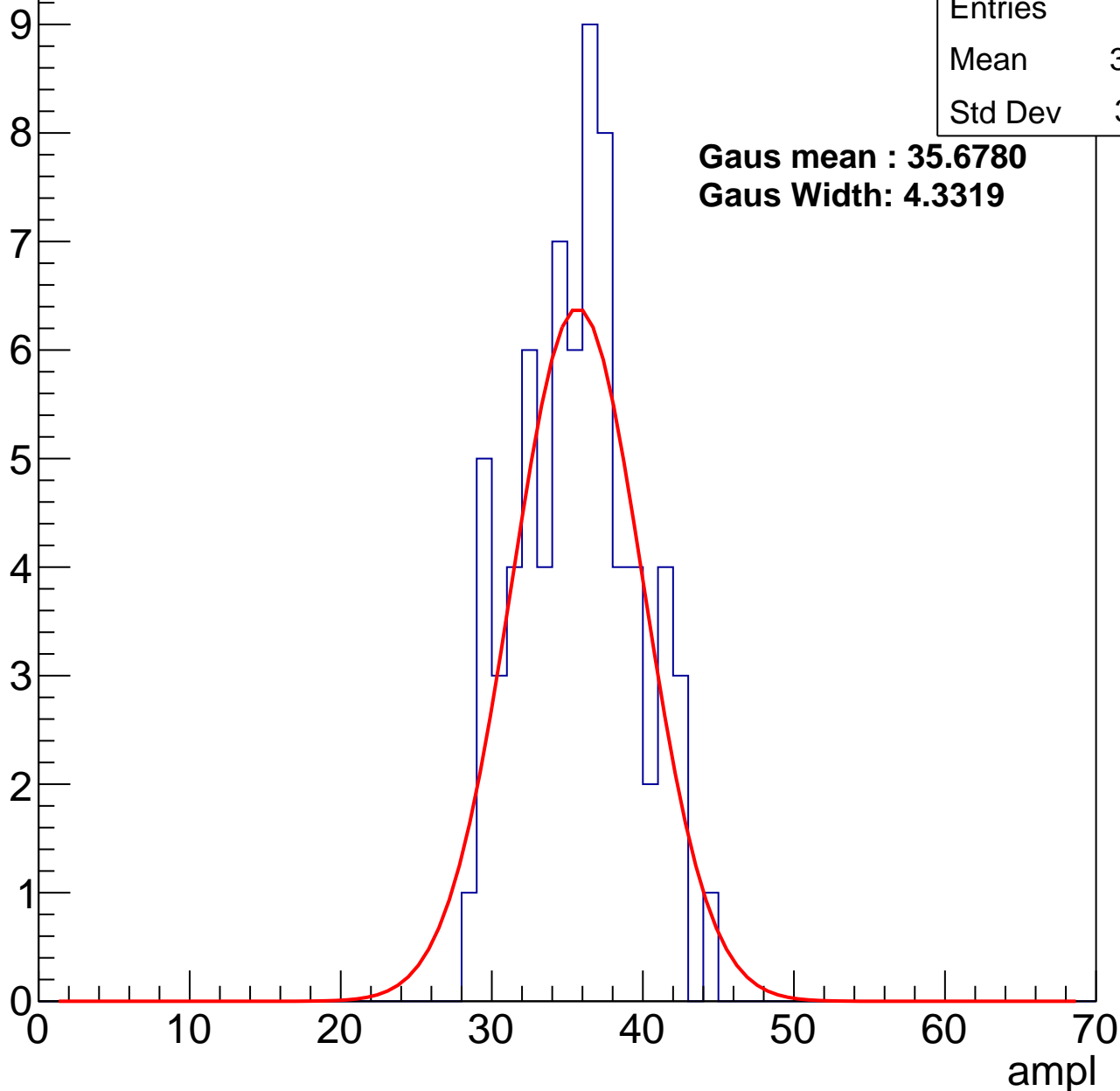
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.23
Std Dev	3.761

Gaus mean : 35.6780

Gaus Width: 4.3319



B1L103S, U5-ch101, adc2

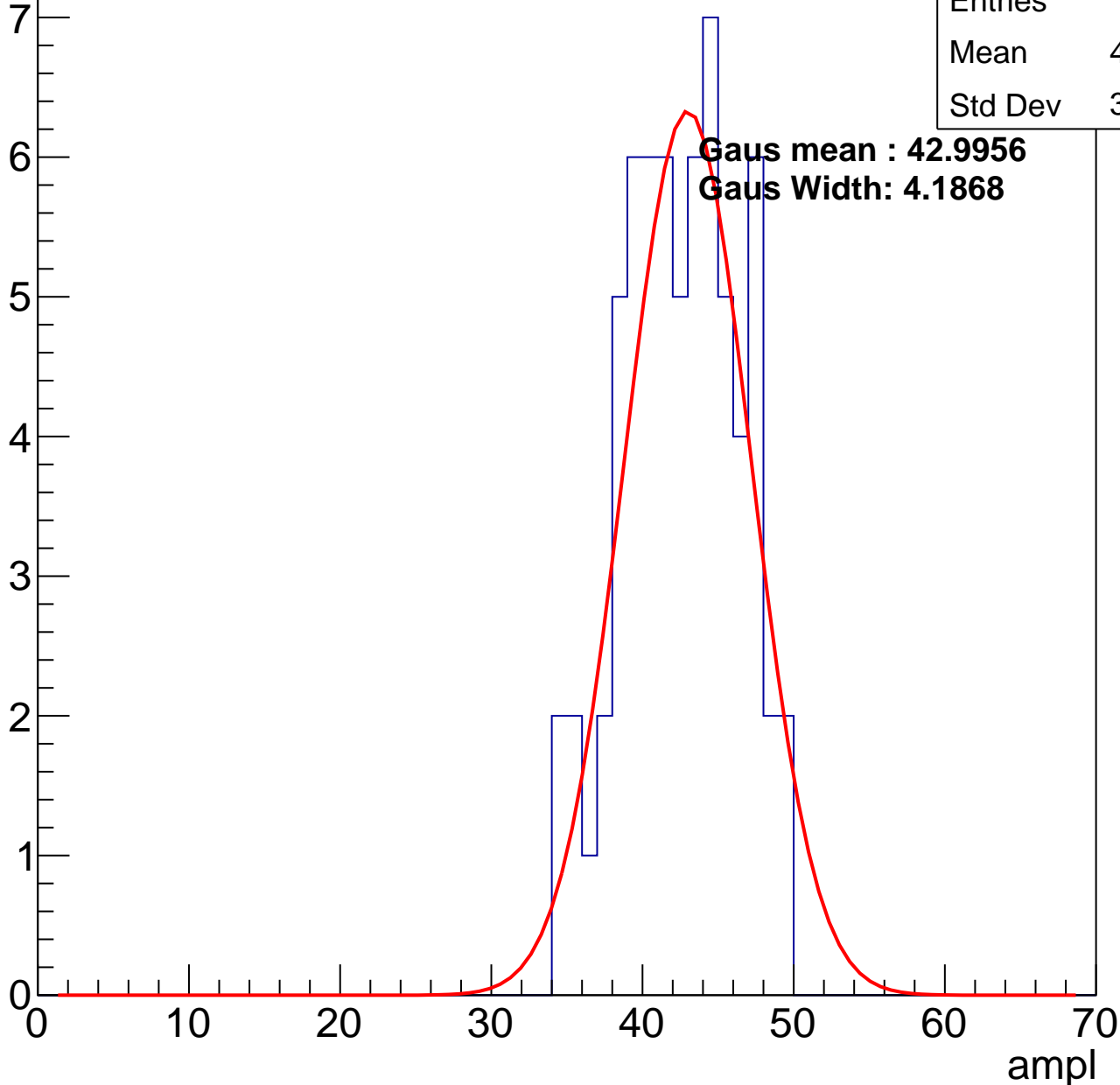
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.07
Std Dev	3.743

Gaus mean : 42.9956

Gaus Width: 4.1868

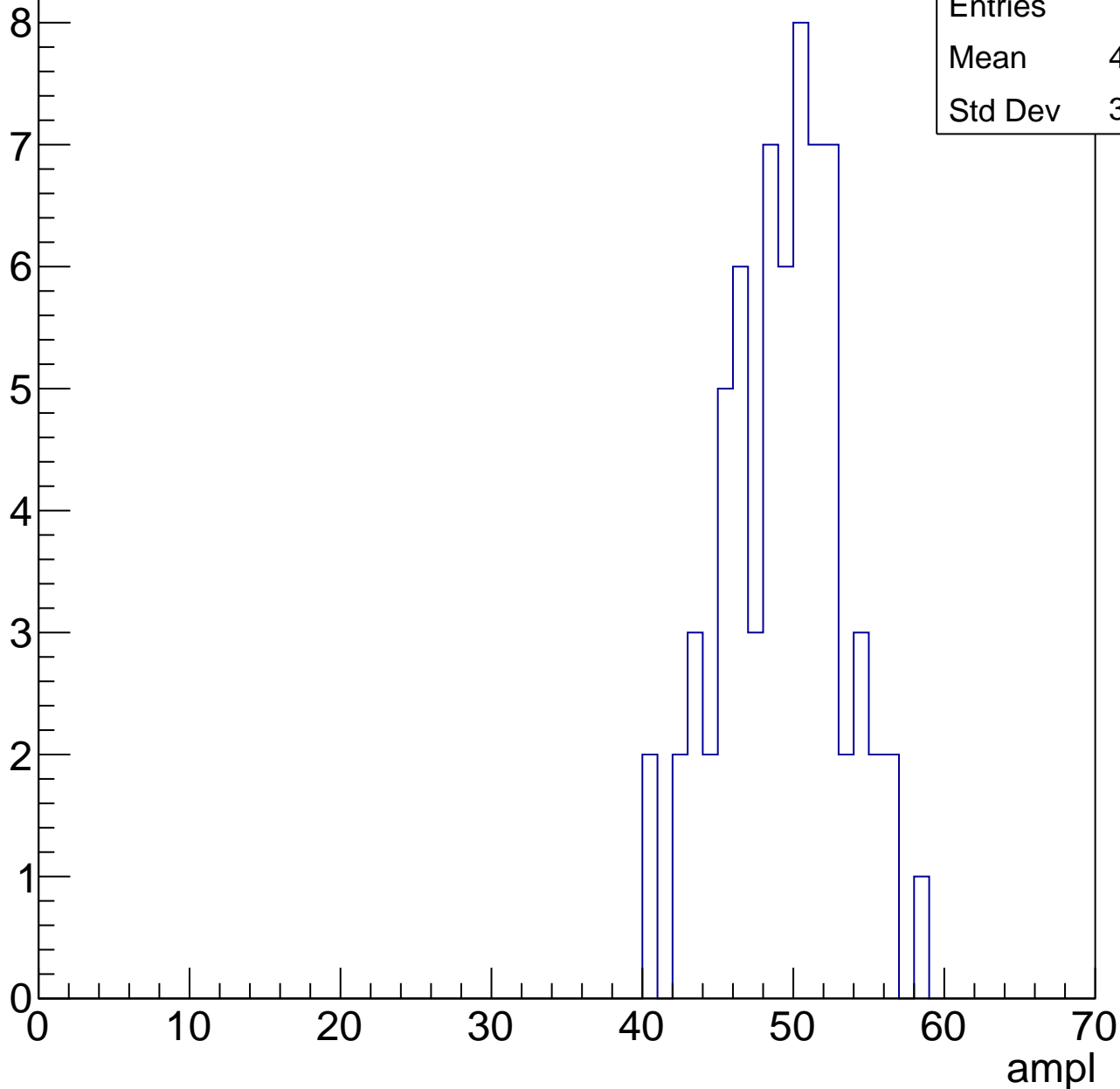


B1L103S, U5-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	48.85
Std Dev	3.885

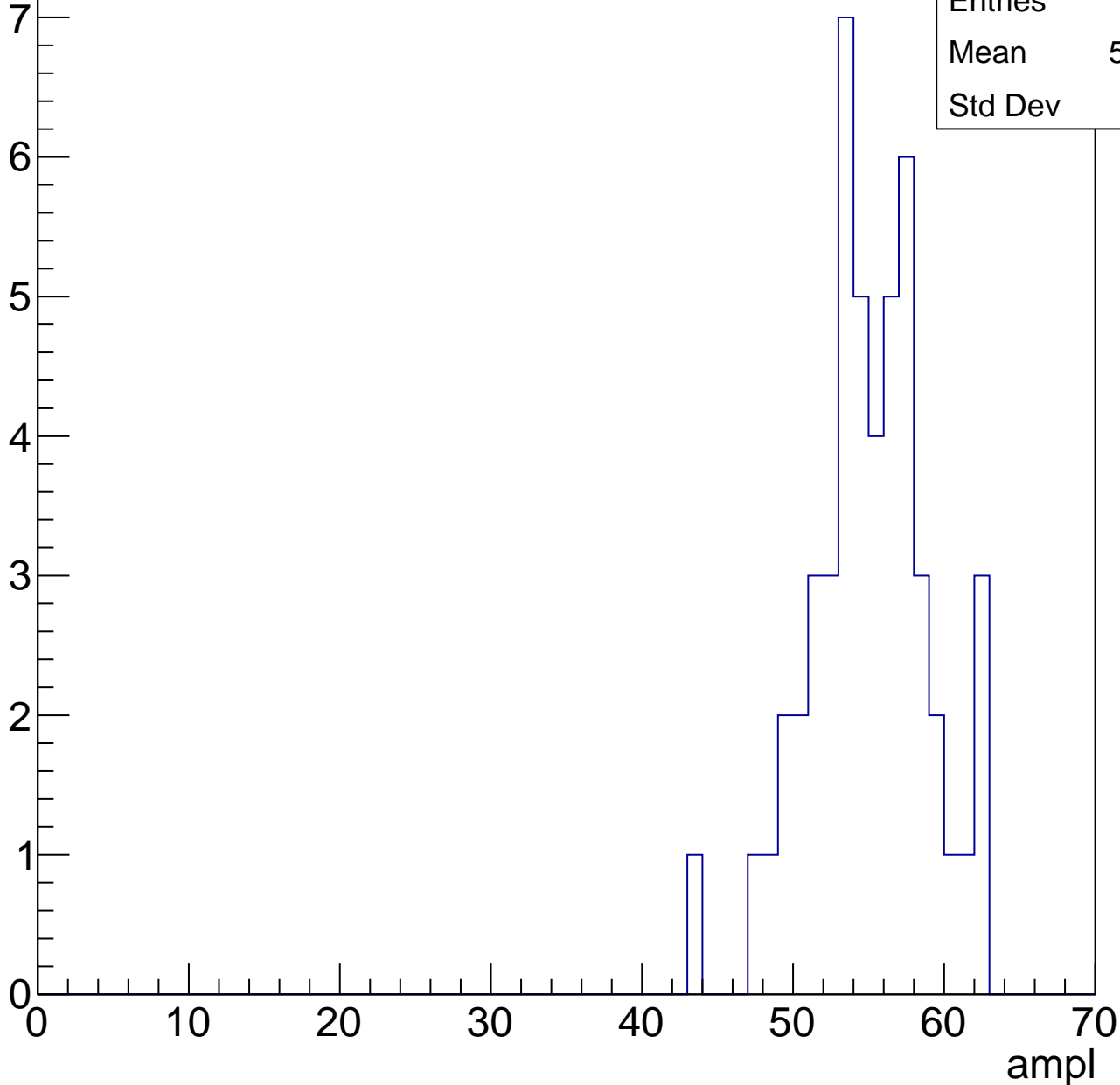


B1L103S, U5-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	54.54
Std Dev	3.93

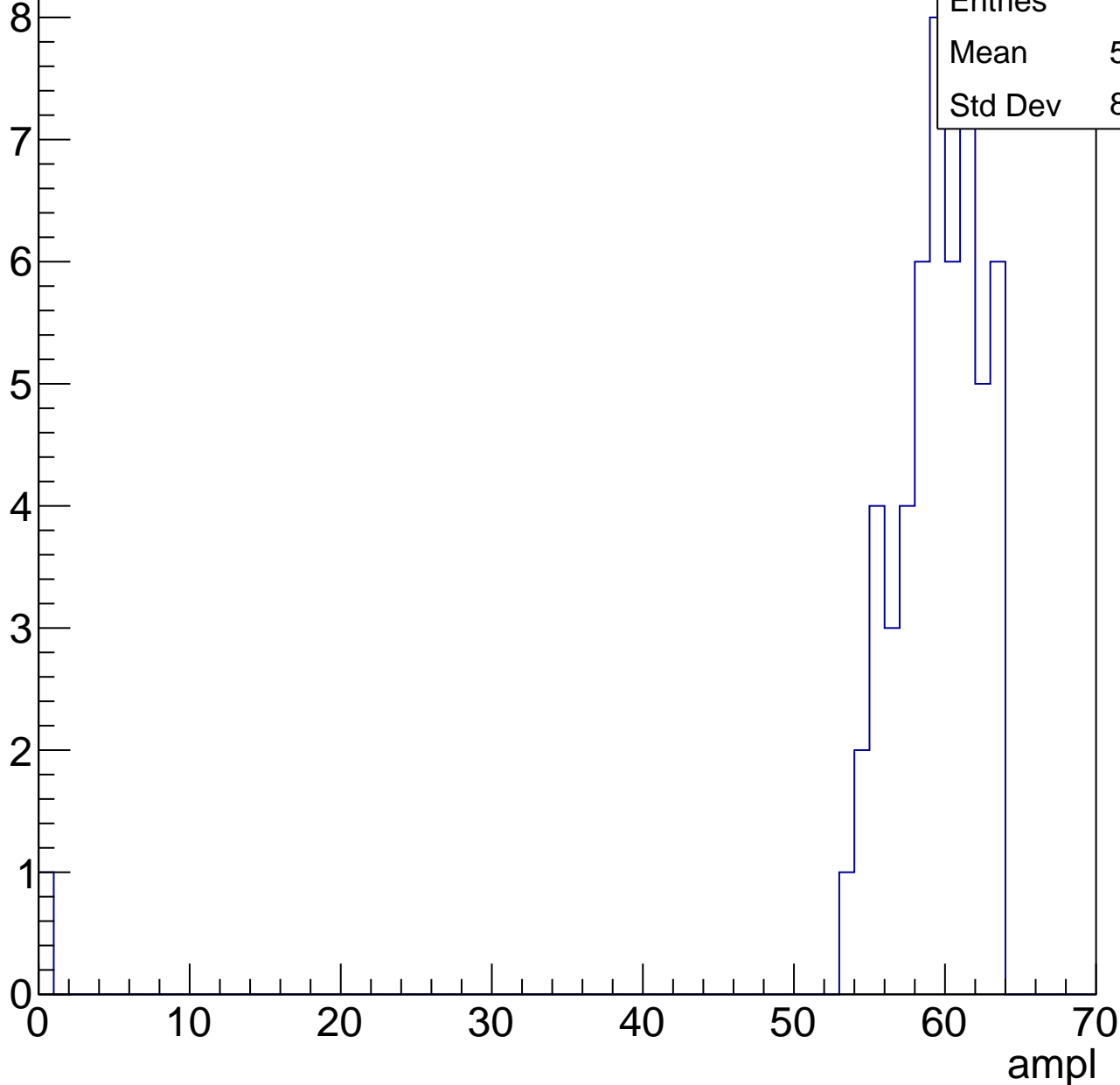


B1L103S, U5-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

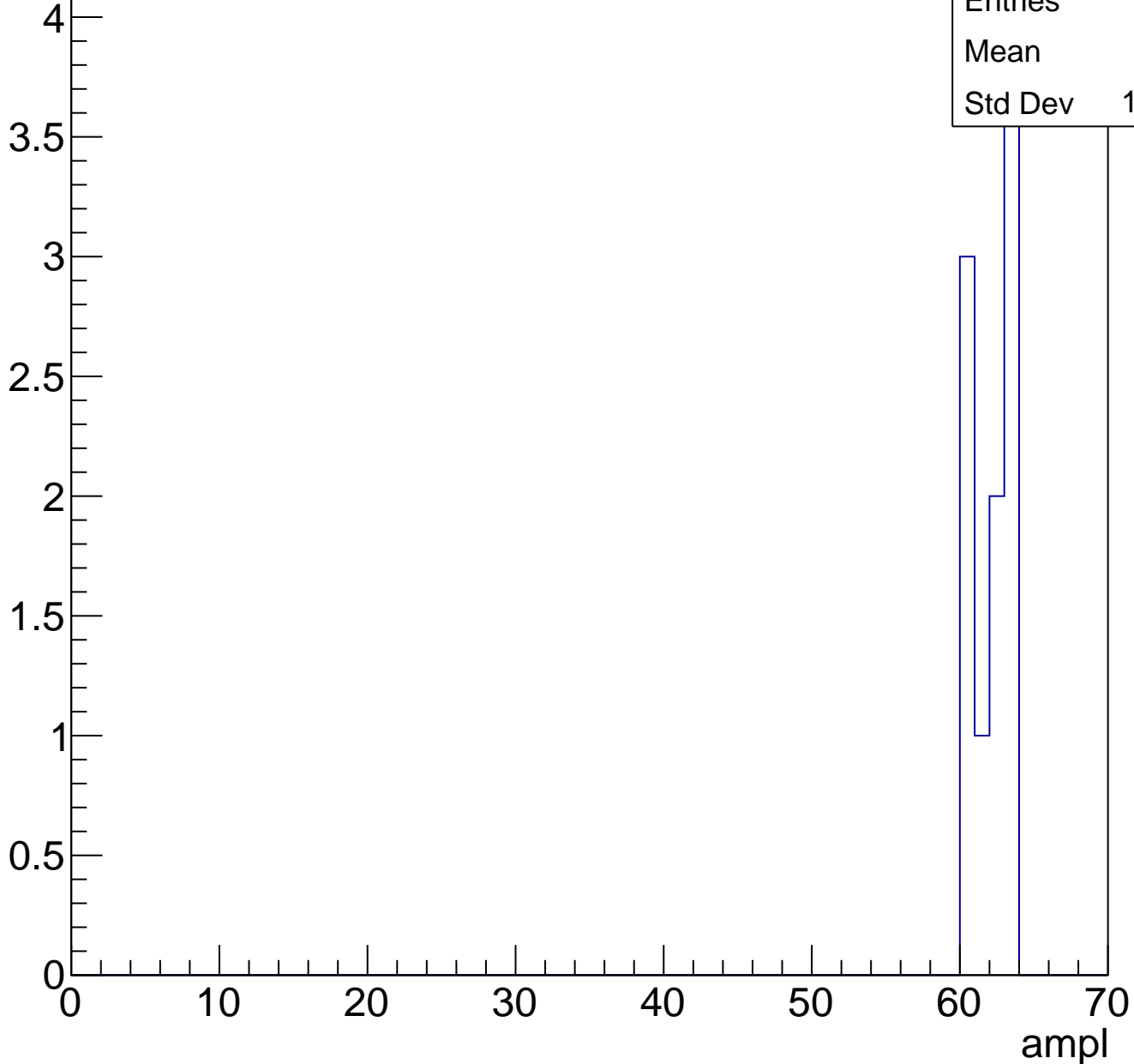
Entries	54
Mean	58.02
Std Dev	8.396



B1L103S, U5-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	61.7
Std Dev	1.269

B1L103S, U5-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch102, adc0

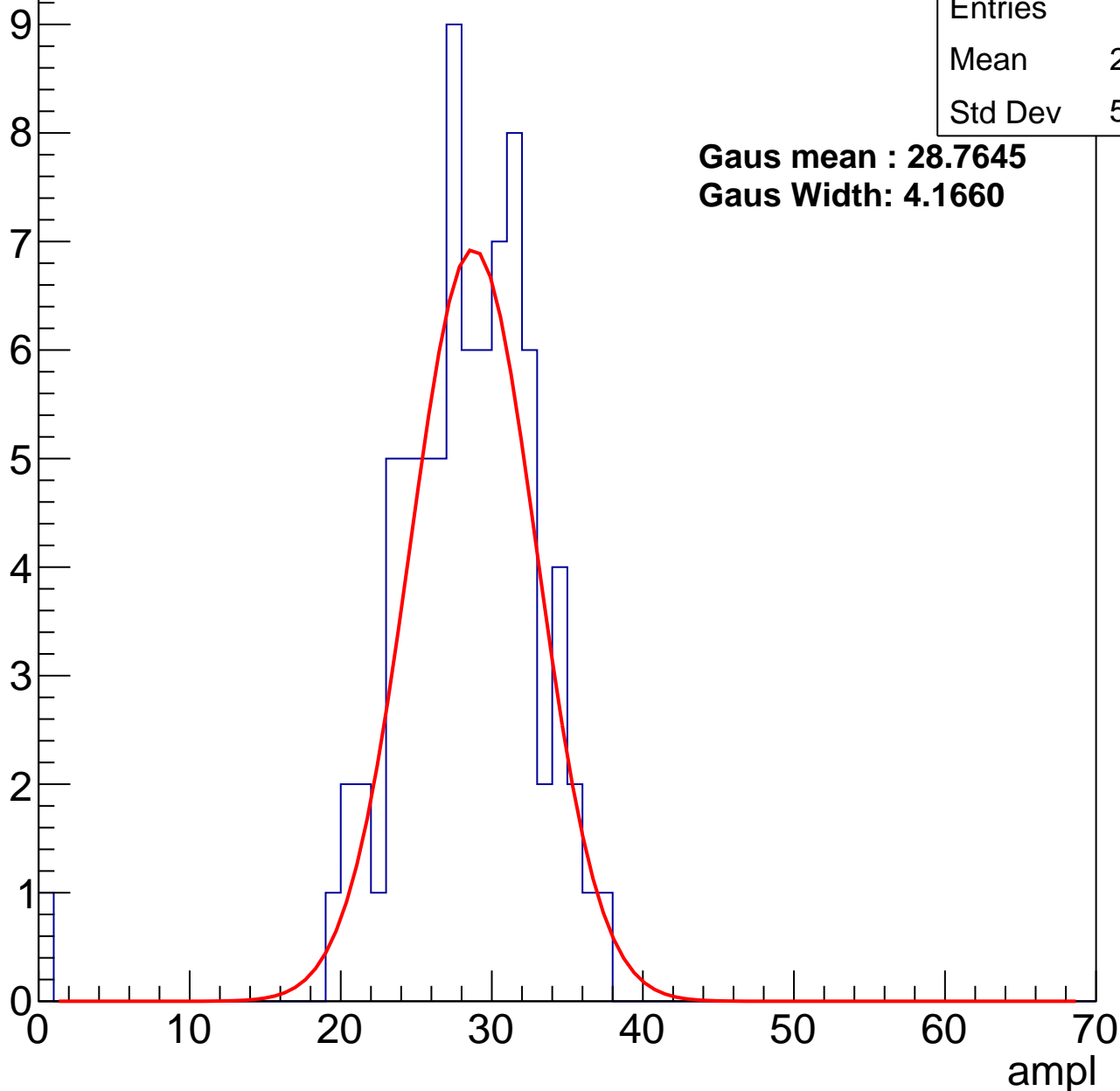
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	27.76
Std Dev	5.082

Gaus mean : 28.7645

Gaus Width: 4.1660



B1L103S, U5-ch102, adc1

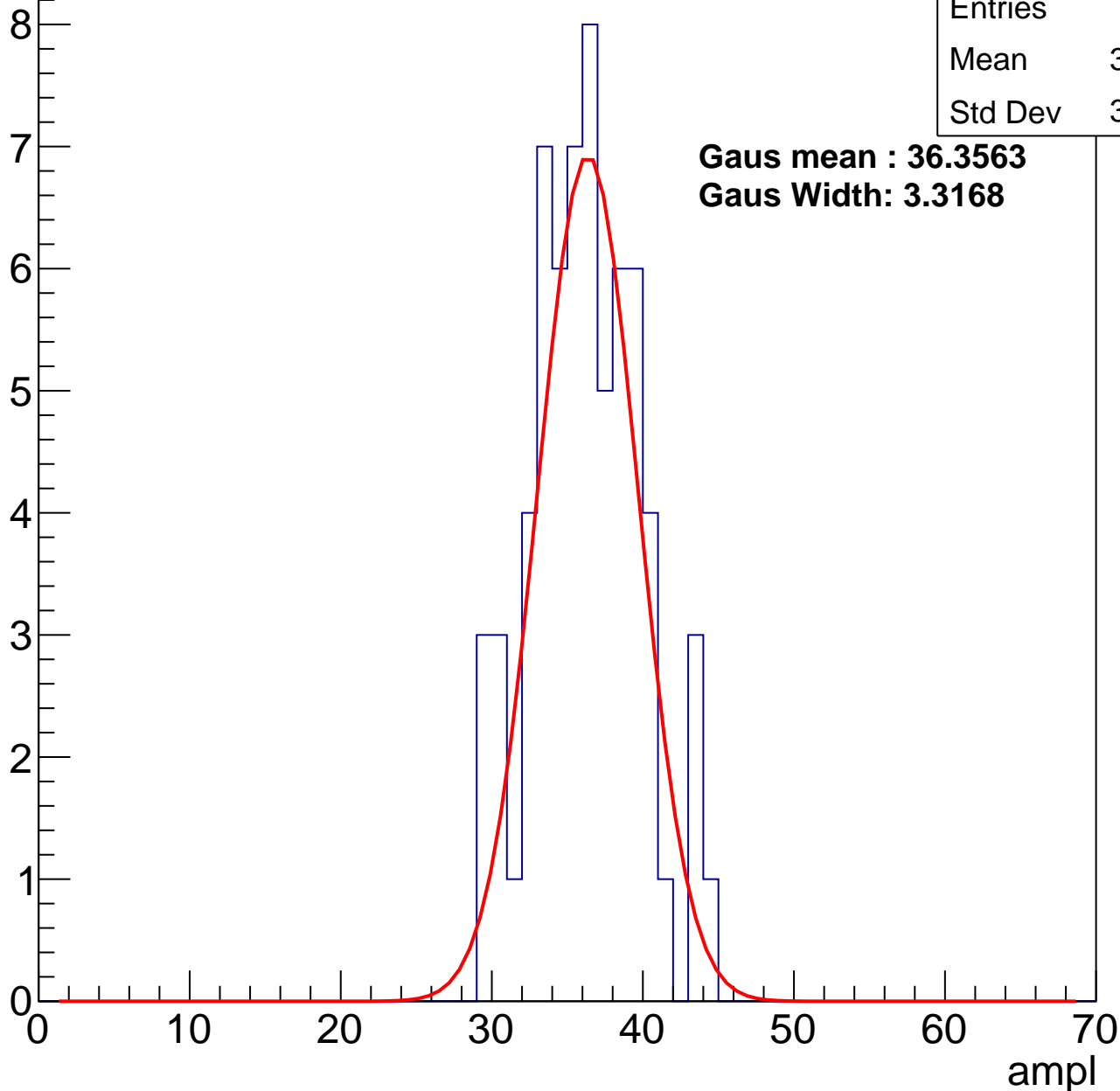
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.77
Std Dev	3.546

Gaus mean : 36.3563

Gaus Width: 3.3168



B1L103S, U5-ch102, adc2

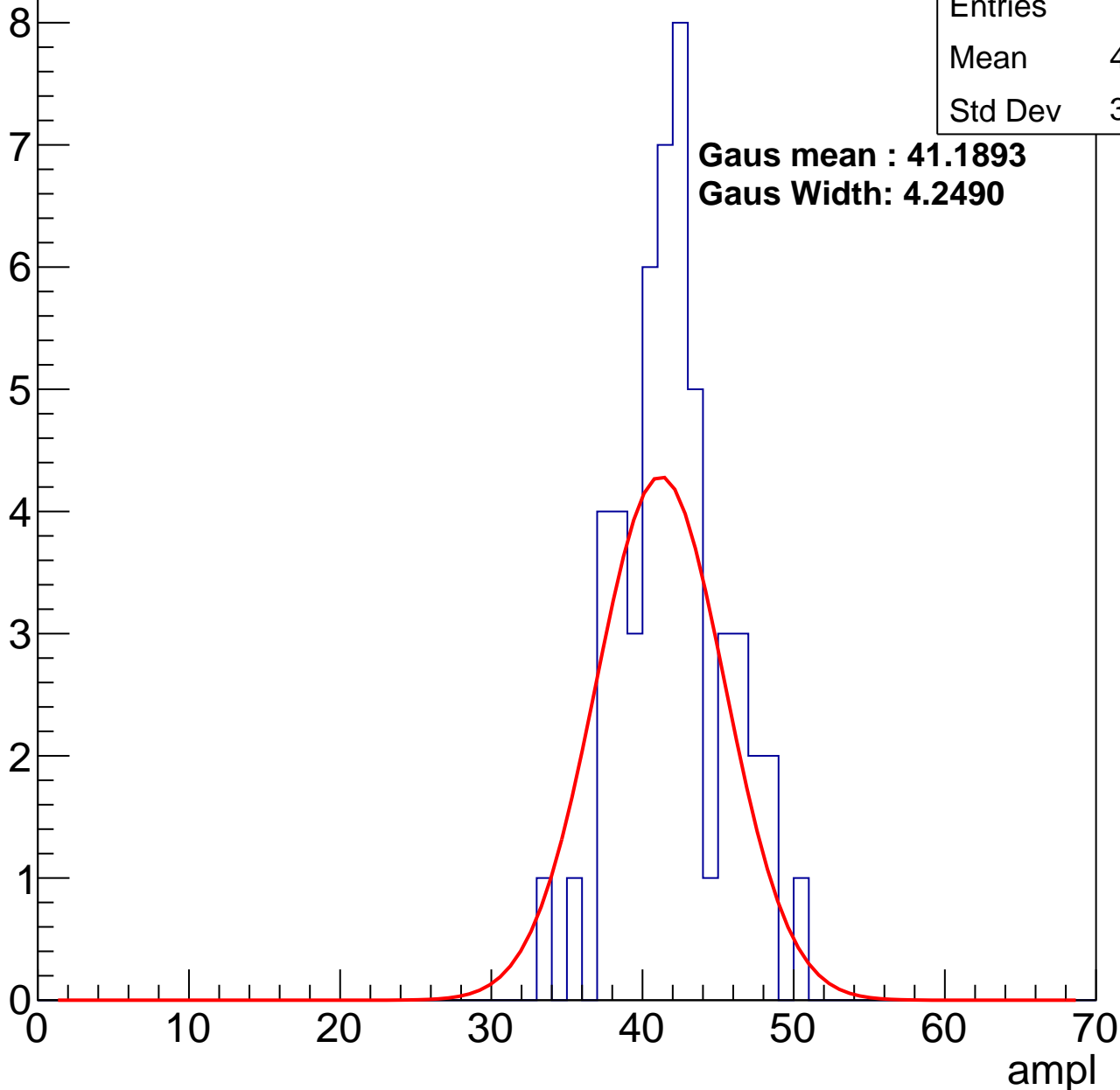
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.57
Std Dev	3.466

Gaus mean : 41.1893

Gaus Width: 4.2490

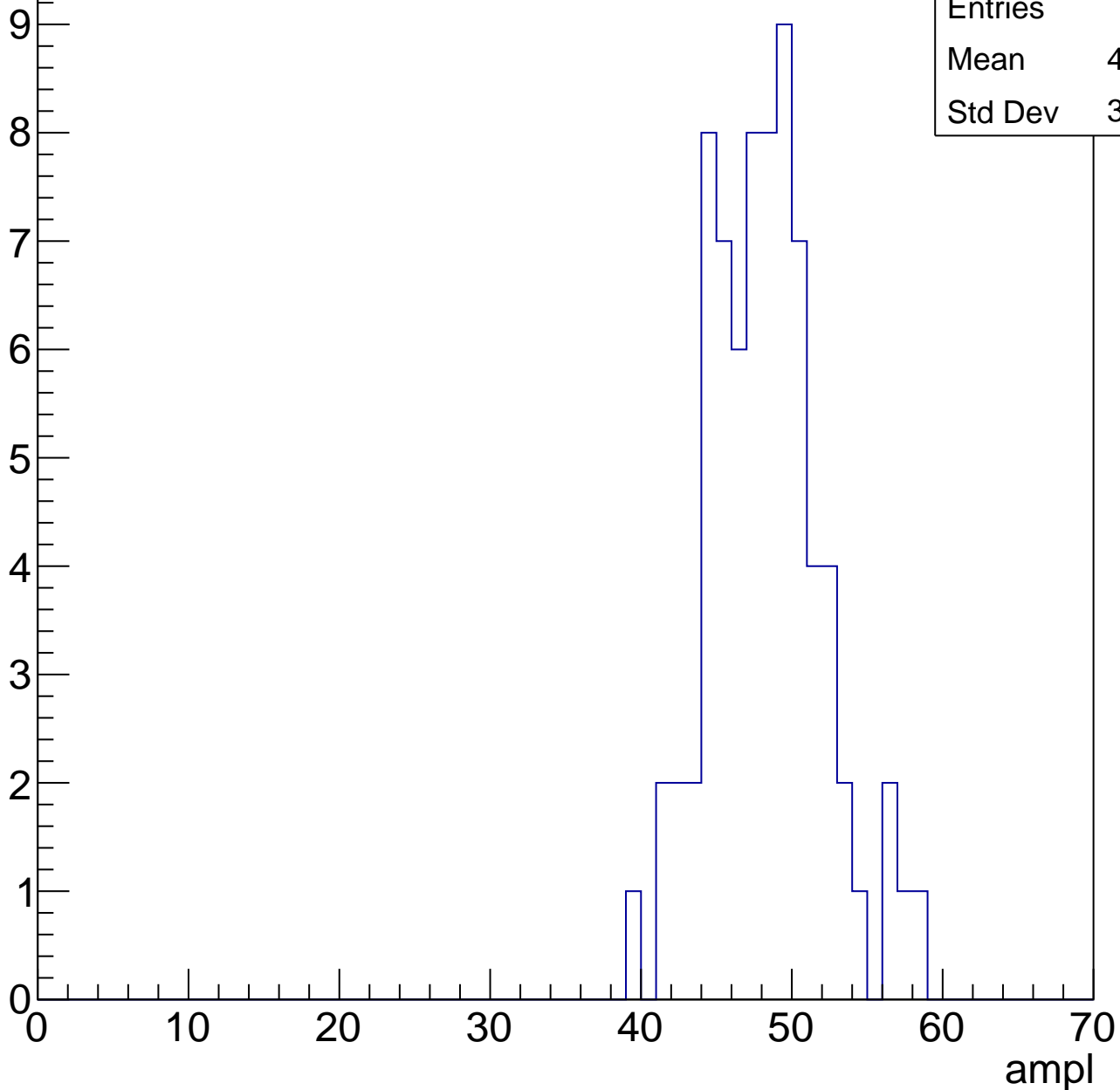


B1L103S, U5-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	47.79
Std Dev	3.746

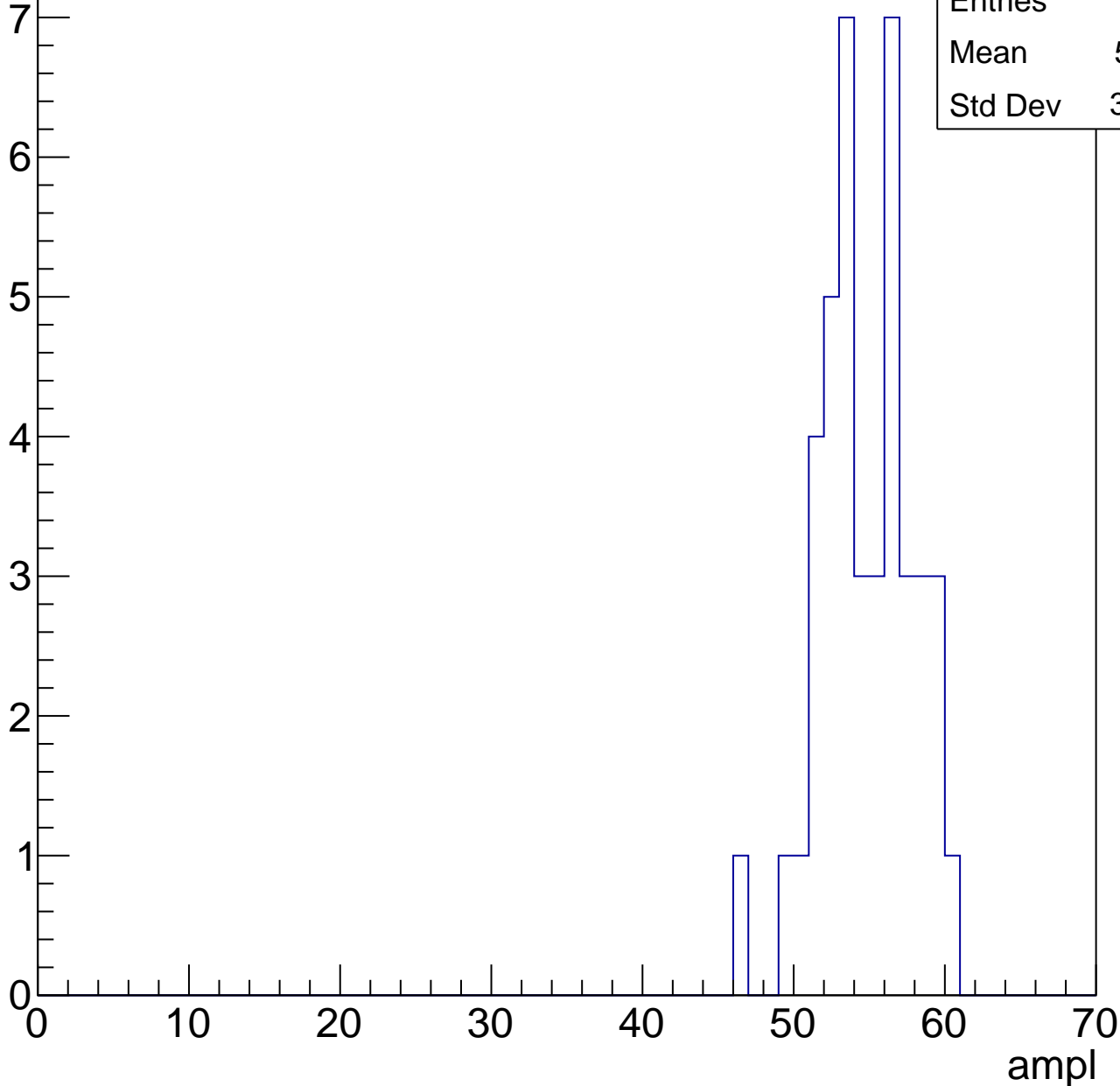


B1L103S, U5-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	54.31
Std Dev	3.012

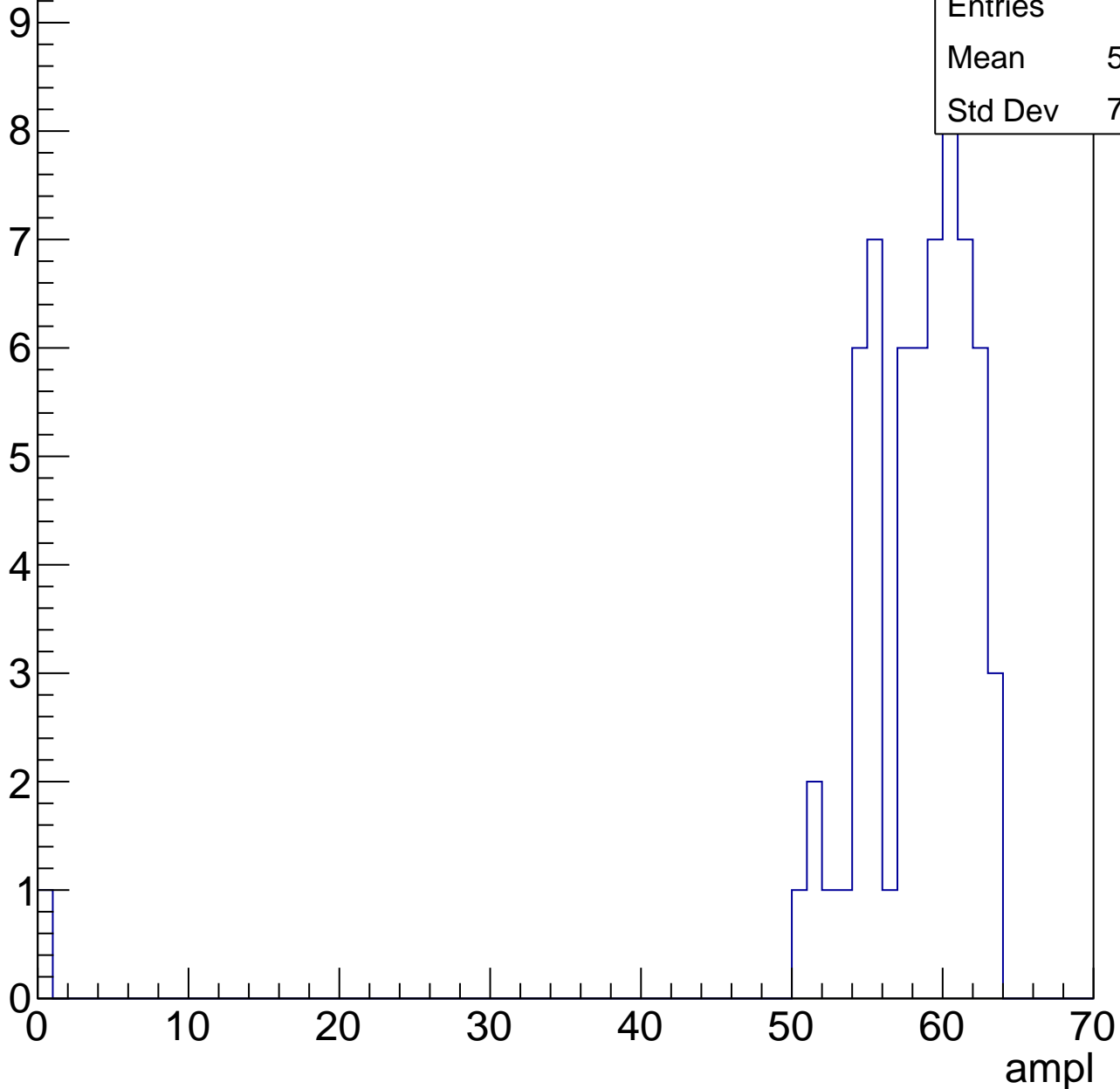


B1L103S, U5-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	57.08
Std Dev	7.885

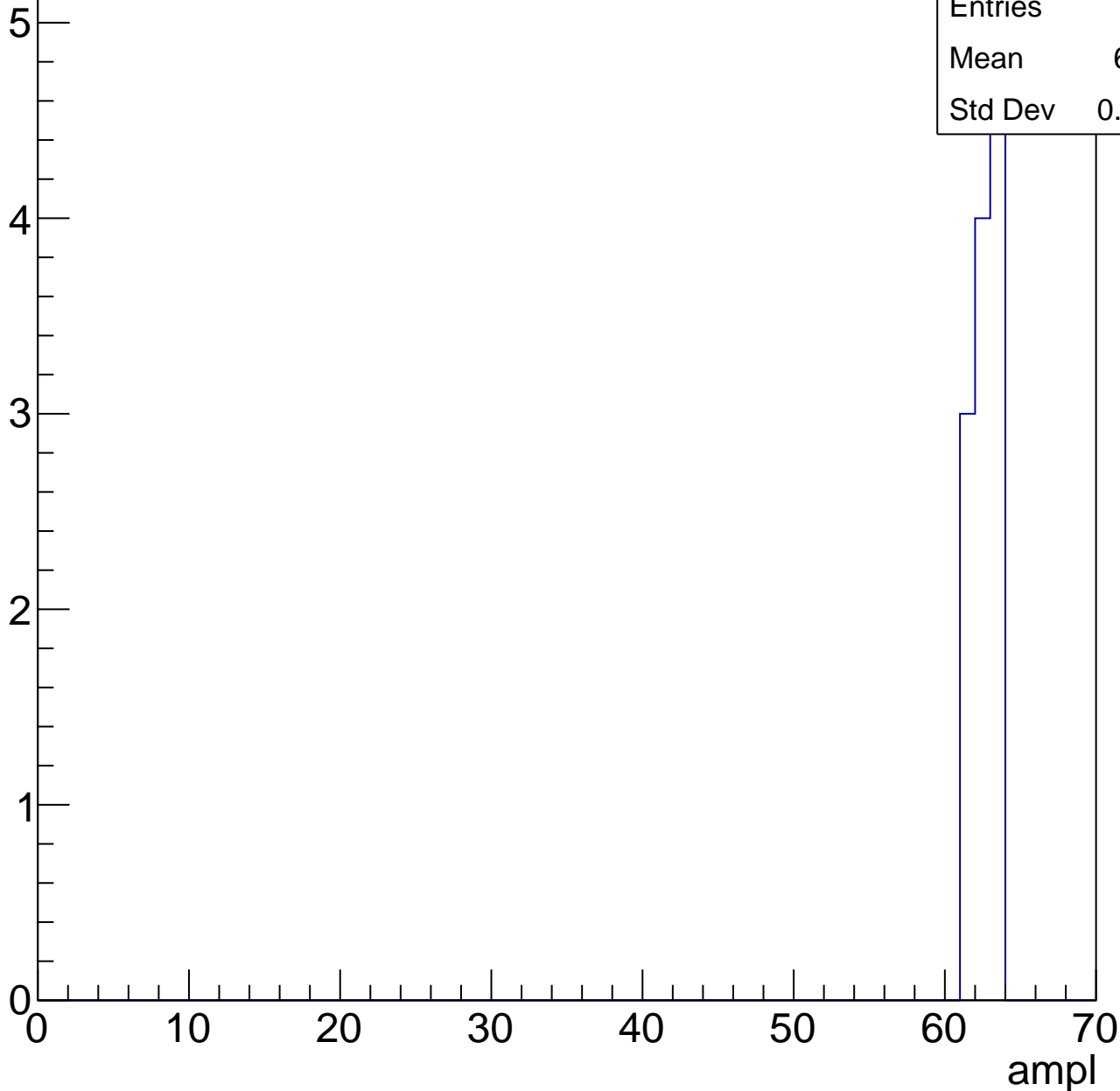


B1L103S, U5-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	62.17
Std Dev	0.7993



B1L103S, U5-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch103, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	29.45
Std Dev	3.586

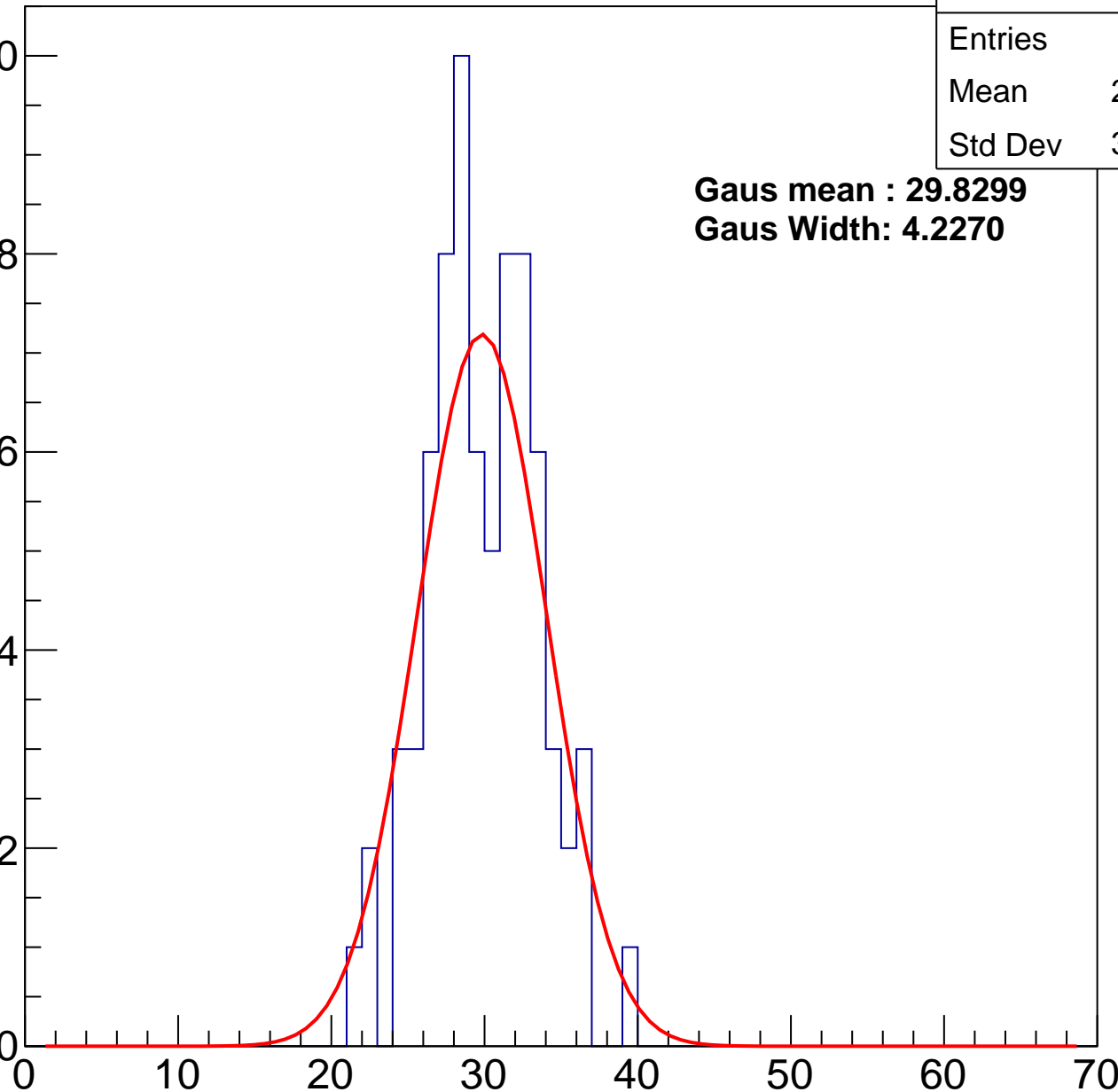
Gaus mean : 29.8299

Gaus Width: 4.2270

Entry

10
8
6
4
2
0

ampl



B1L103S, U5-ch103, adc1

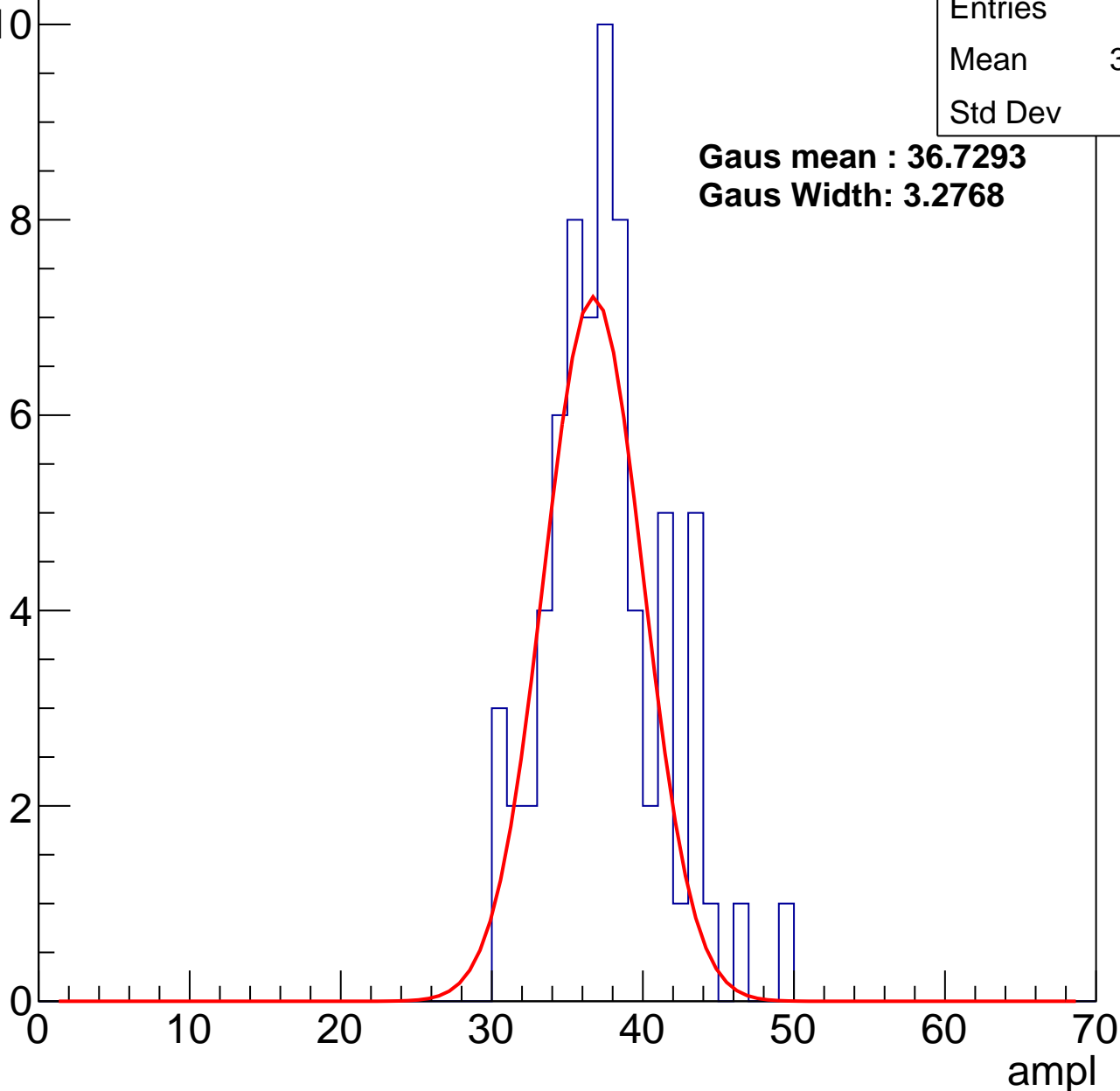
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	37.07
Std Dev	3.83

Gaus mean : 36.7293

Gaus Width: 3.2768



B1L103S, U5-ch103, adc2

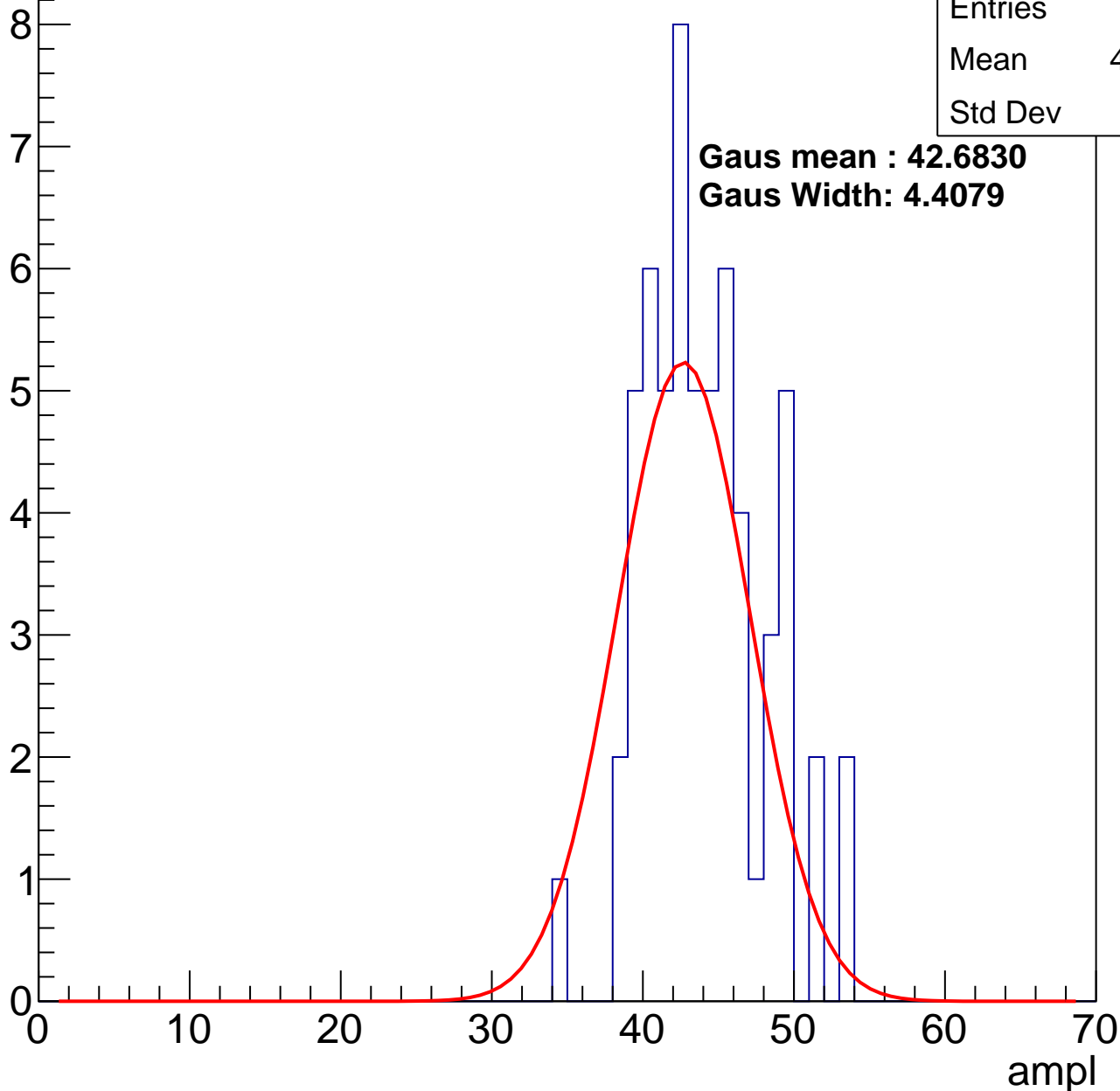
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.65
Std Dev	3.94

Gaus mean : 42.6830

Gaus Width: 4.4079

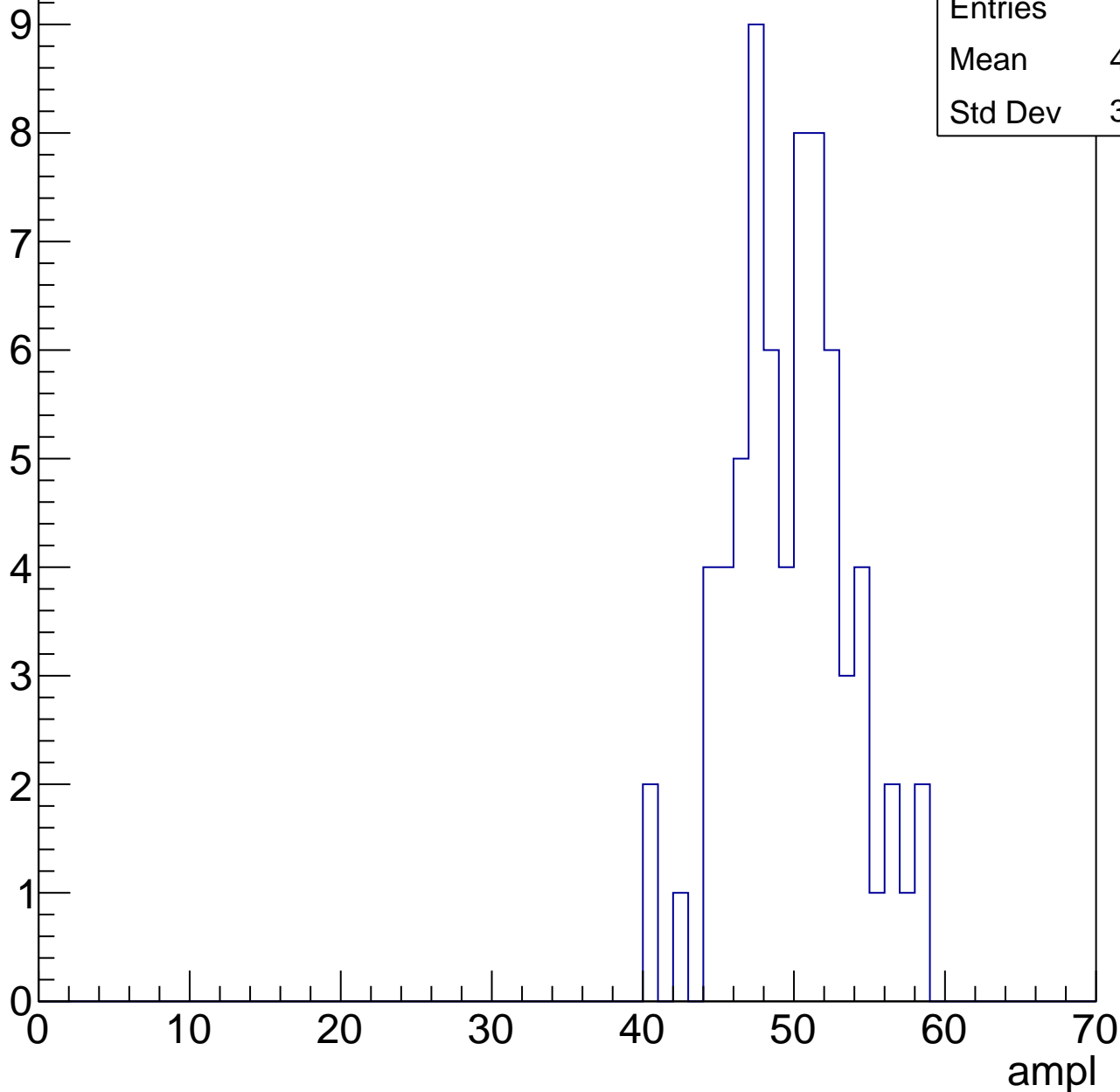


B1L103S, U5-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

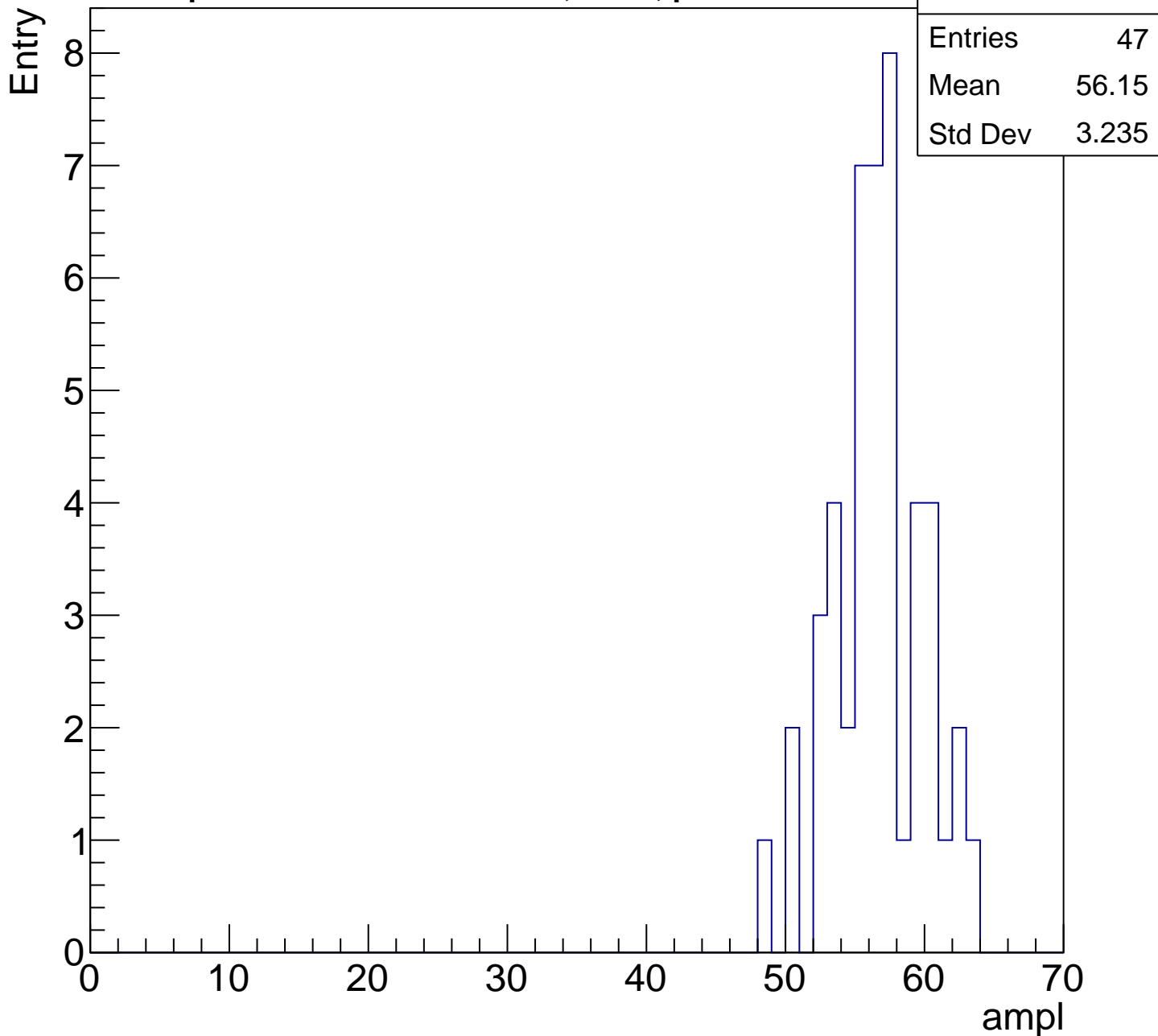
Entry

Entries	70
Mean	49.29
Std Dev	3.888



B1L103S, U5-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

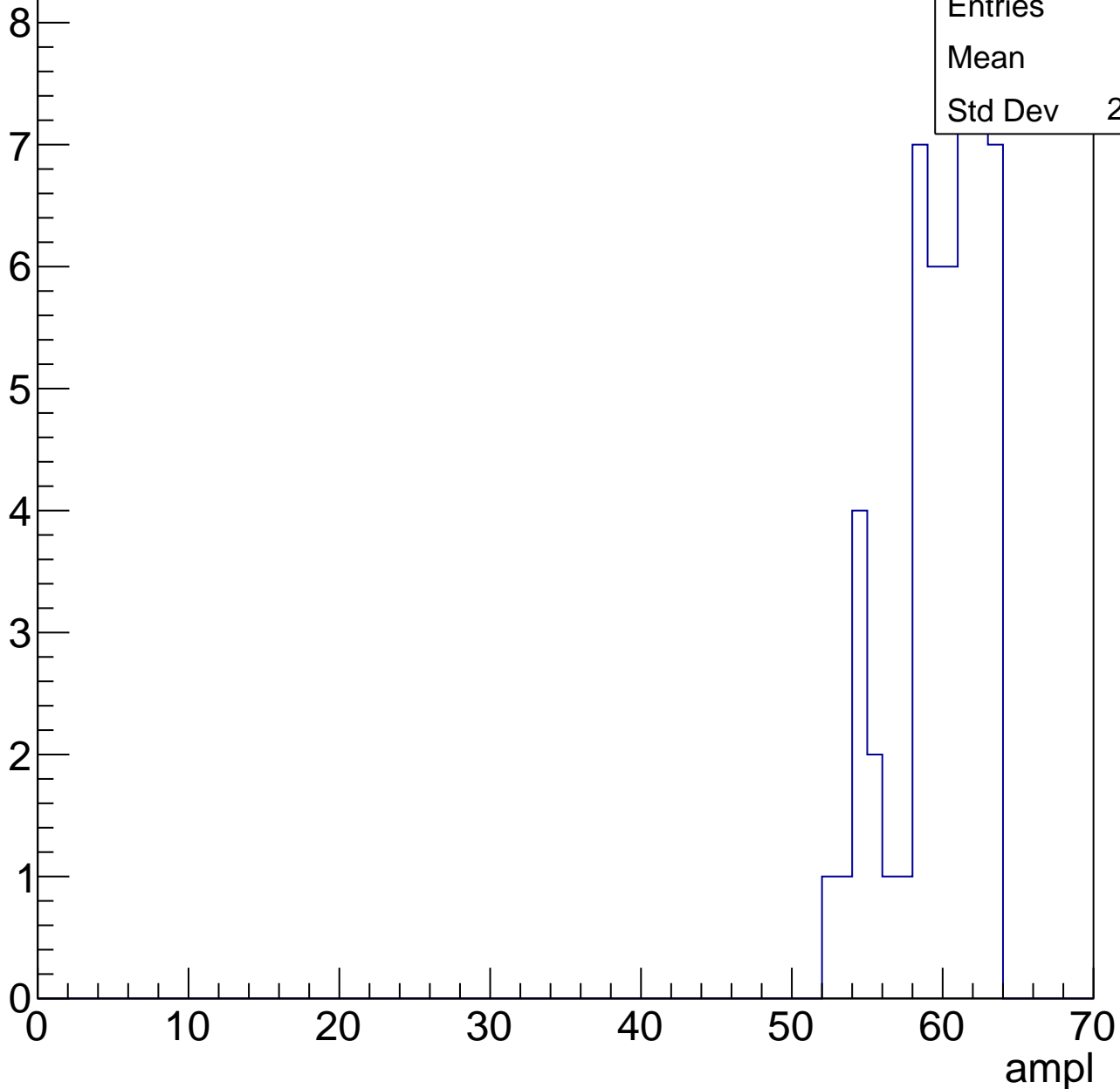


B1L103S, U5-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	59.4
Std Dev	2.943



B1L103S, U5-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

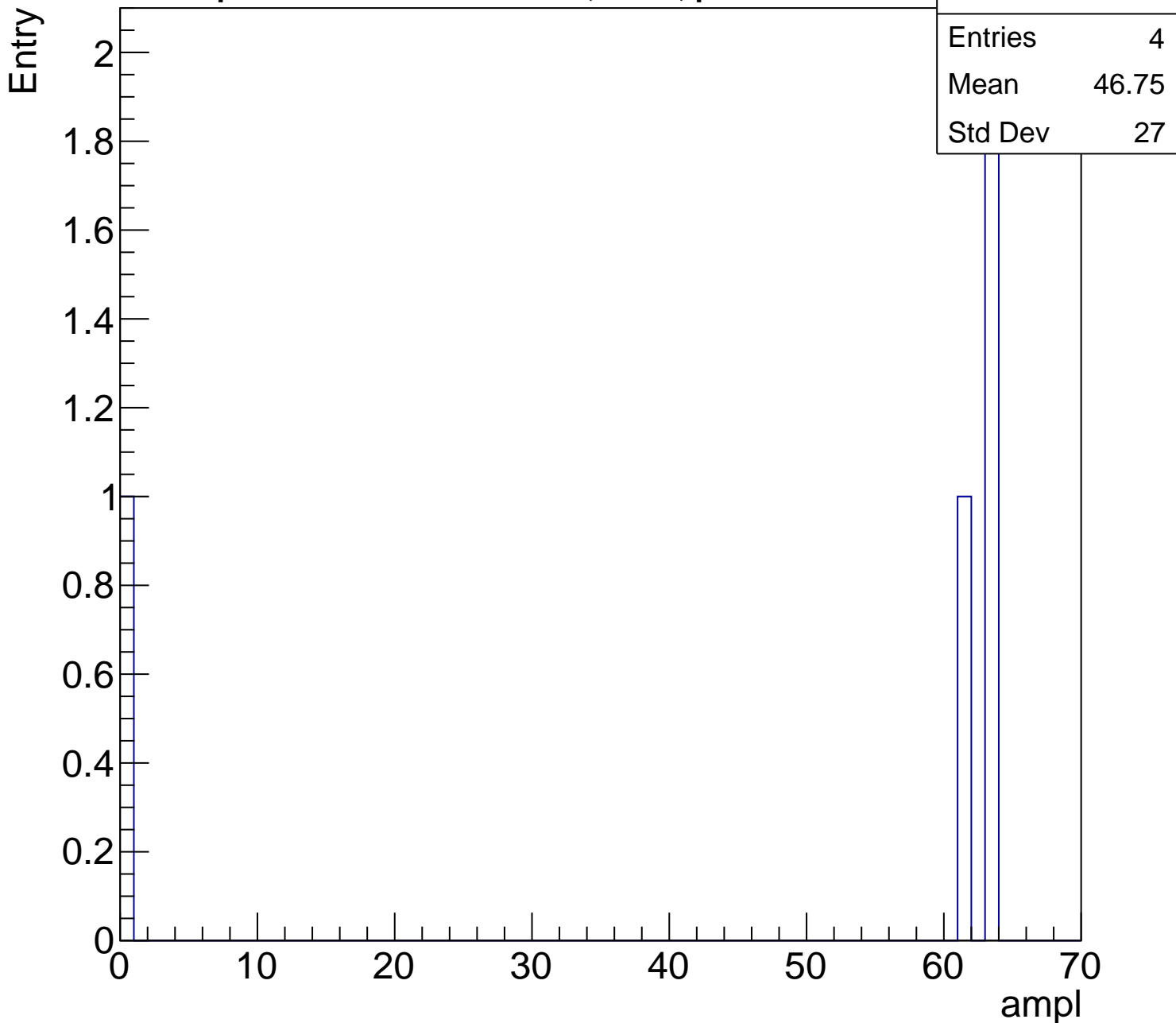
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	27

0 10 20 30 40 50 60 70

ampl



B1L103S, U5-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch104, adc0

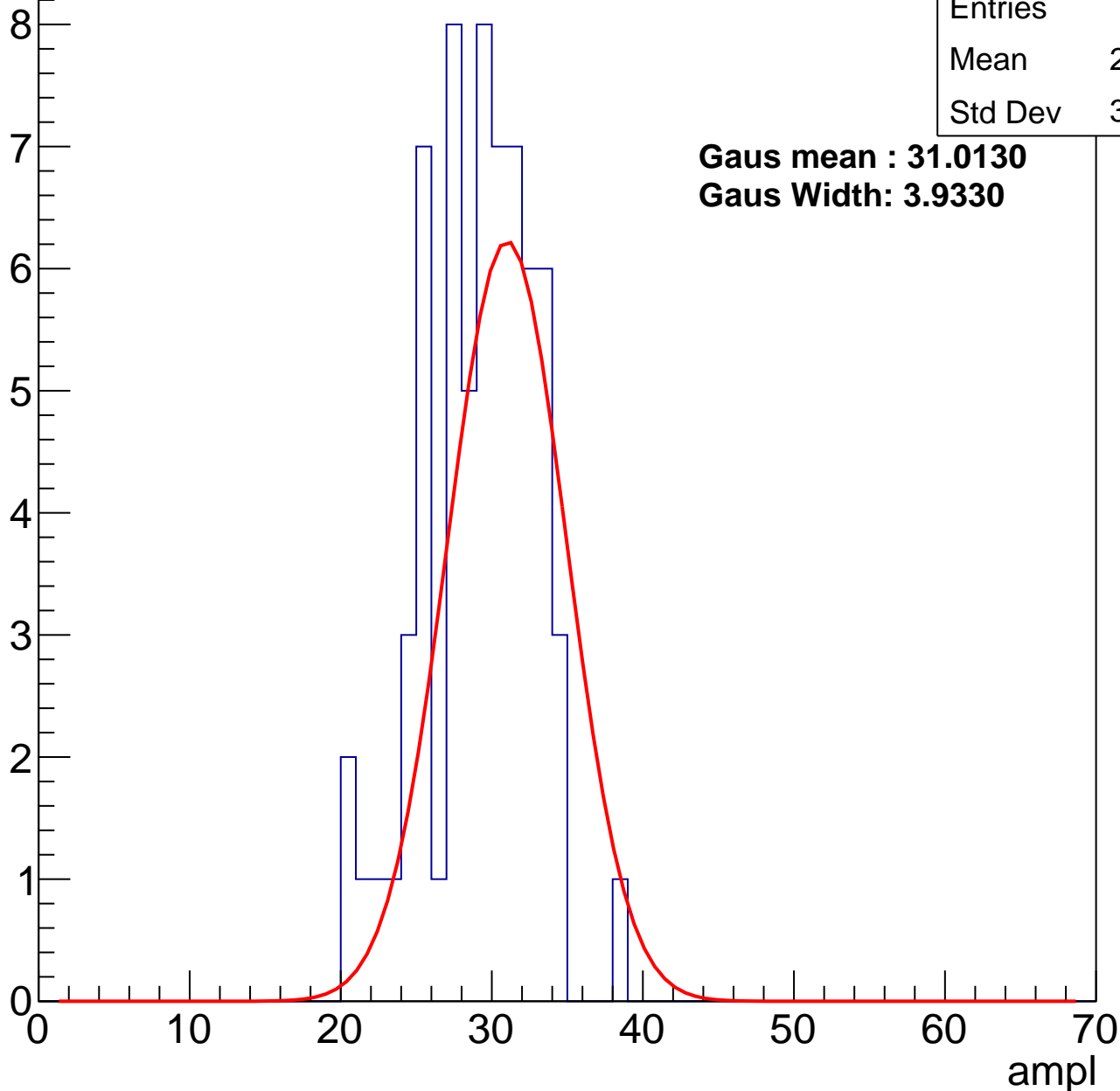
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.72
Std Dev	3.623

Gaus mean : 31.0130

Gaus Width: 3.9330



B1L103S, U5-ch104, adc1

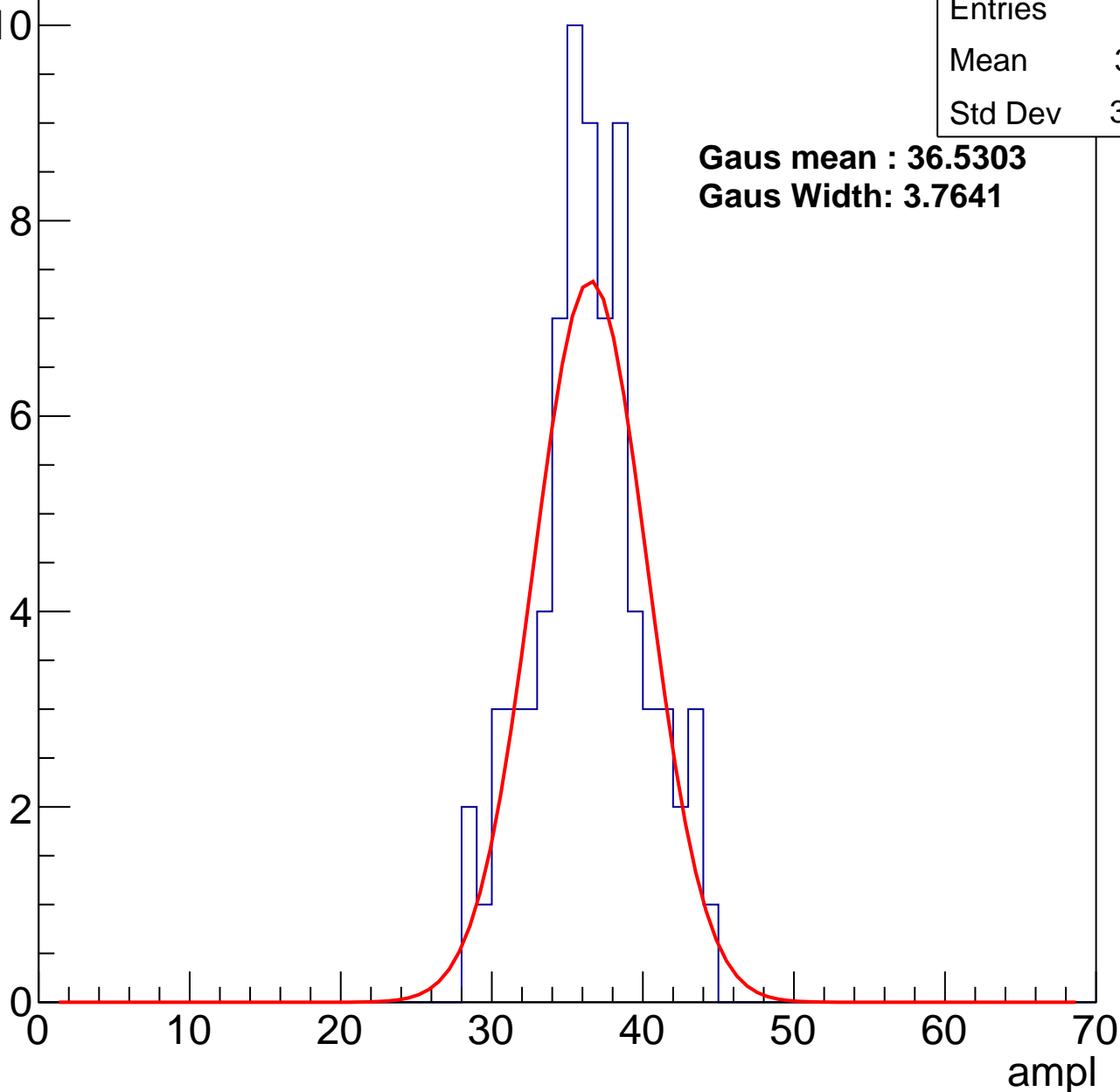
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.01
Std Dev	3.615

Gaus mean : 36.5303

Gaus Width: 3.7641



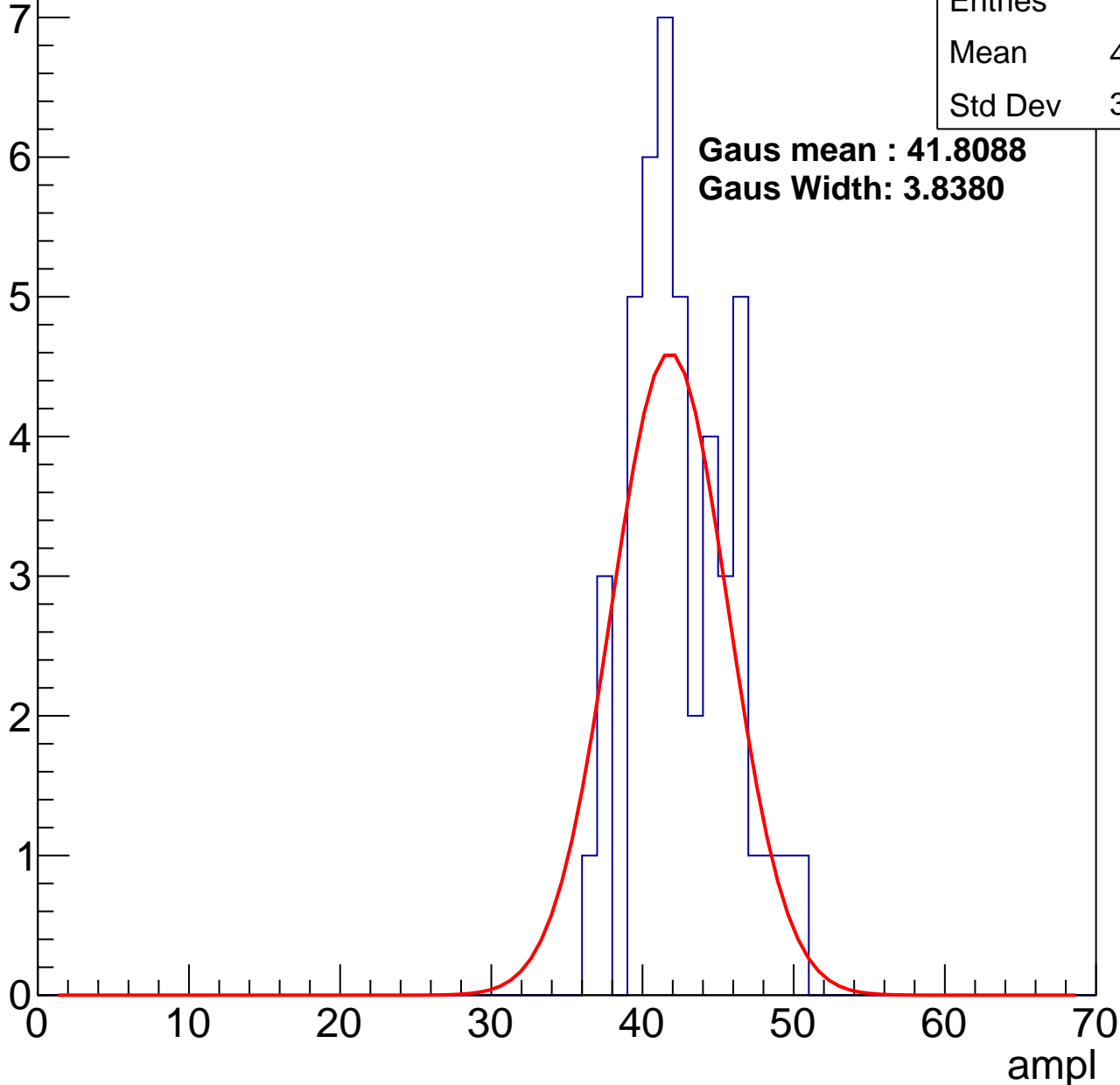
B1L103S, U5-ch104, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	42.22
Std Dev	3.279

Gaus mean : 41.8088
Gaus Width: 3.8380

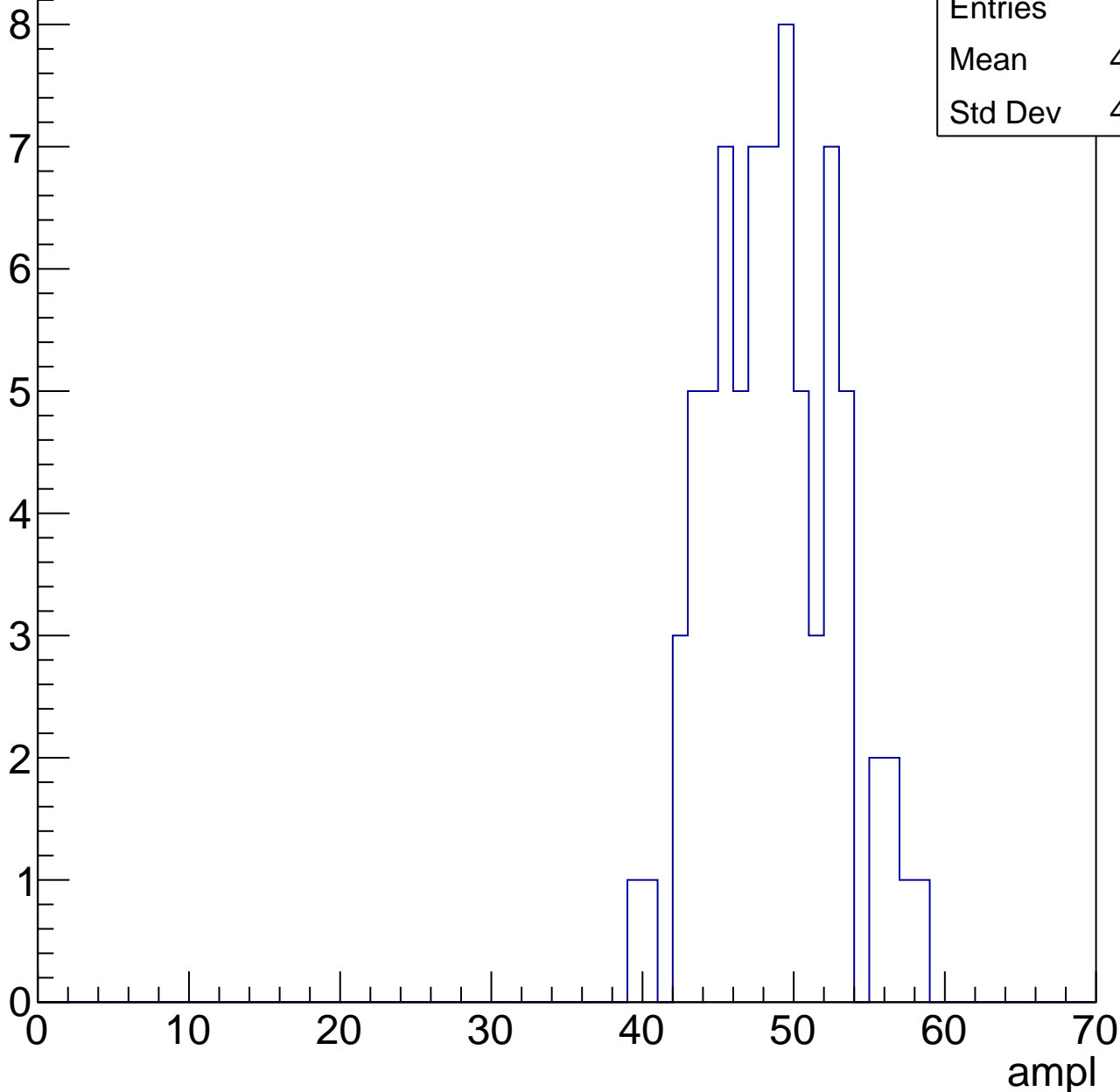


B1L103S, U5-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	48.15
Std Dev	4.075

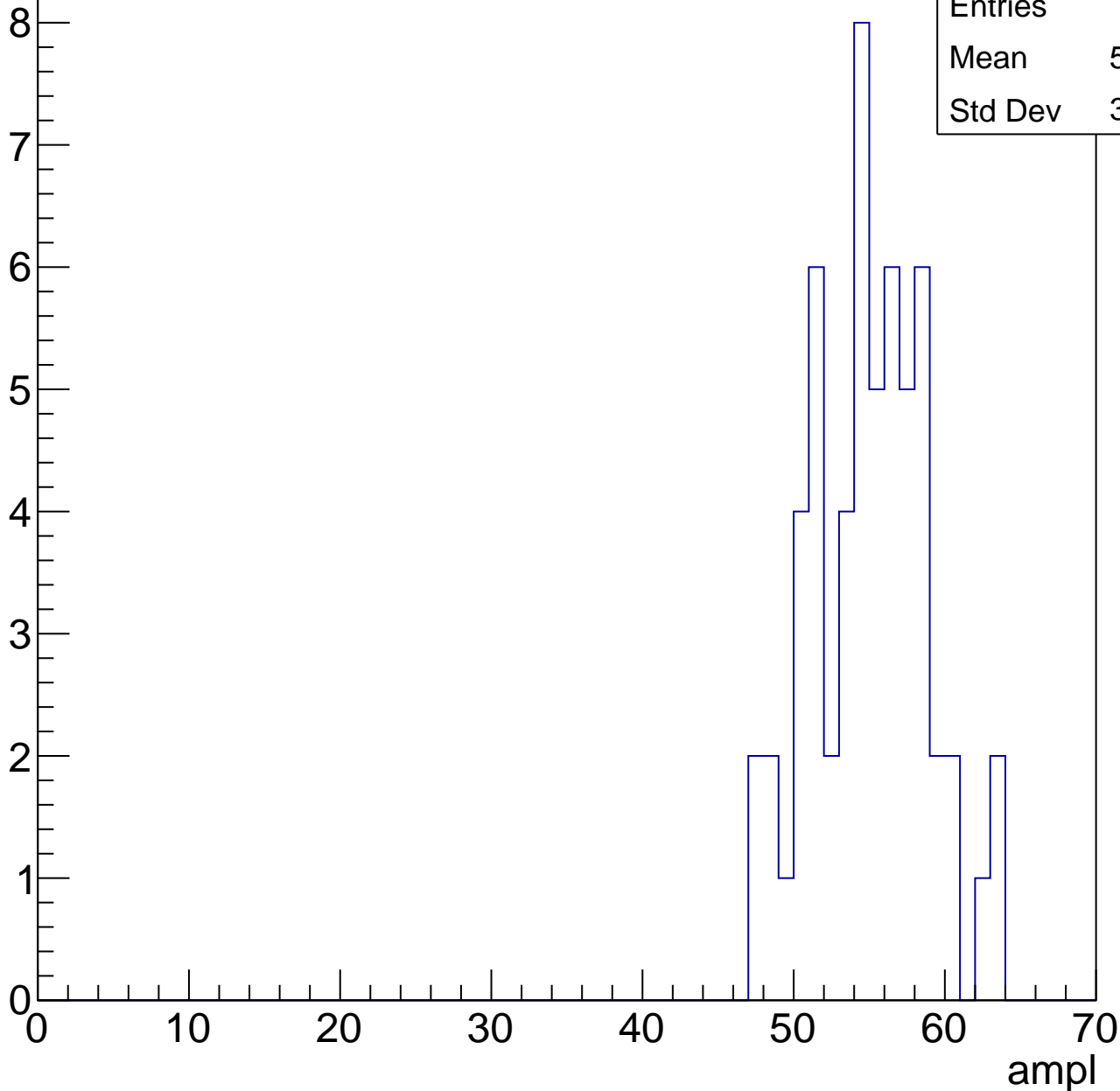


B1L103S, U5-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.53
Std Dev	3.775

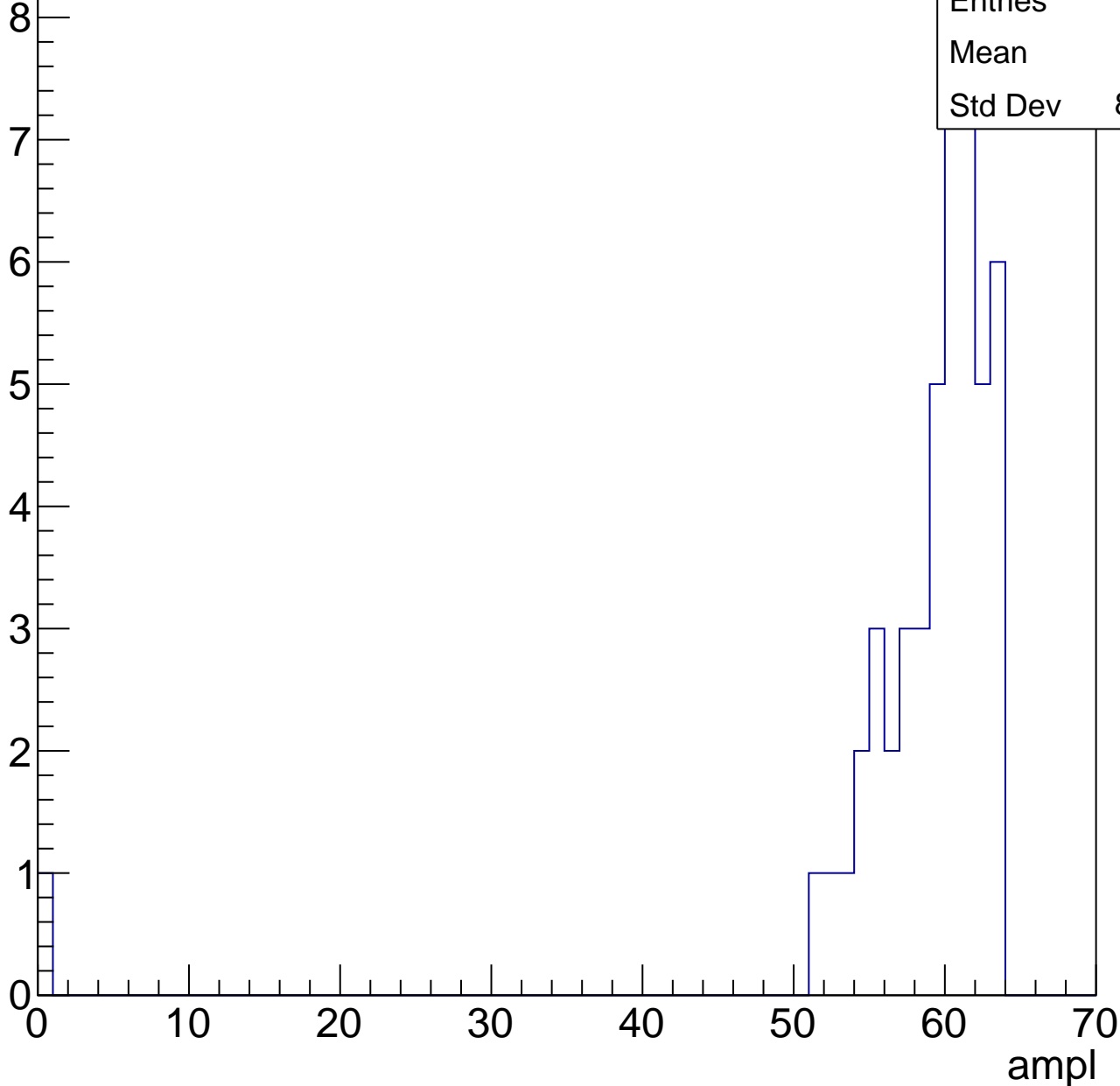


B1L103S, U5-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

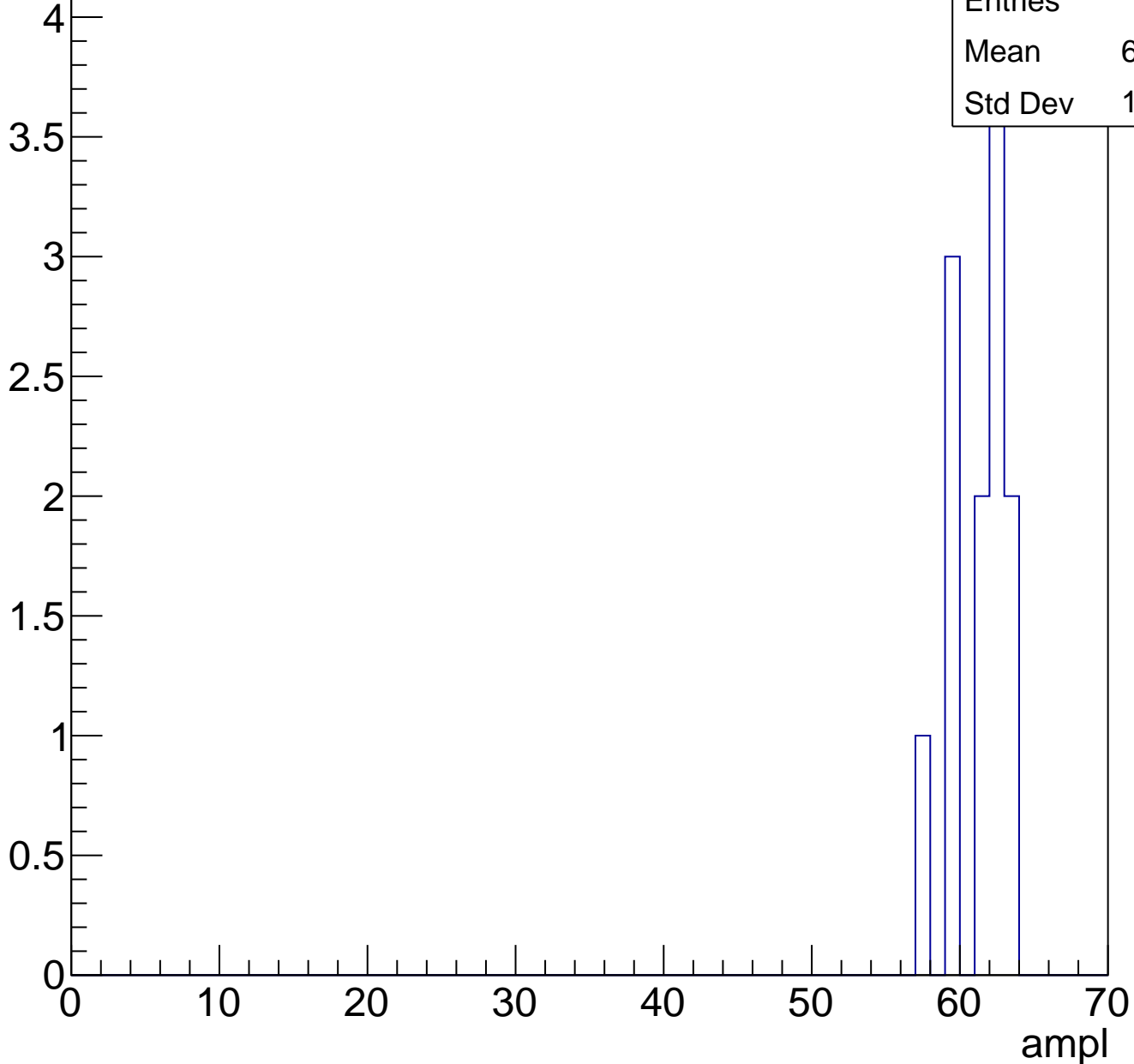
Entries	49
Mean	57.9
Std Dev	8.901



B1L103S, U5-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch105, adc0

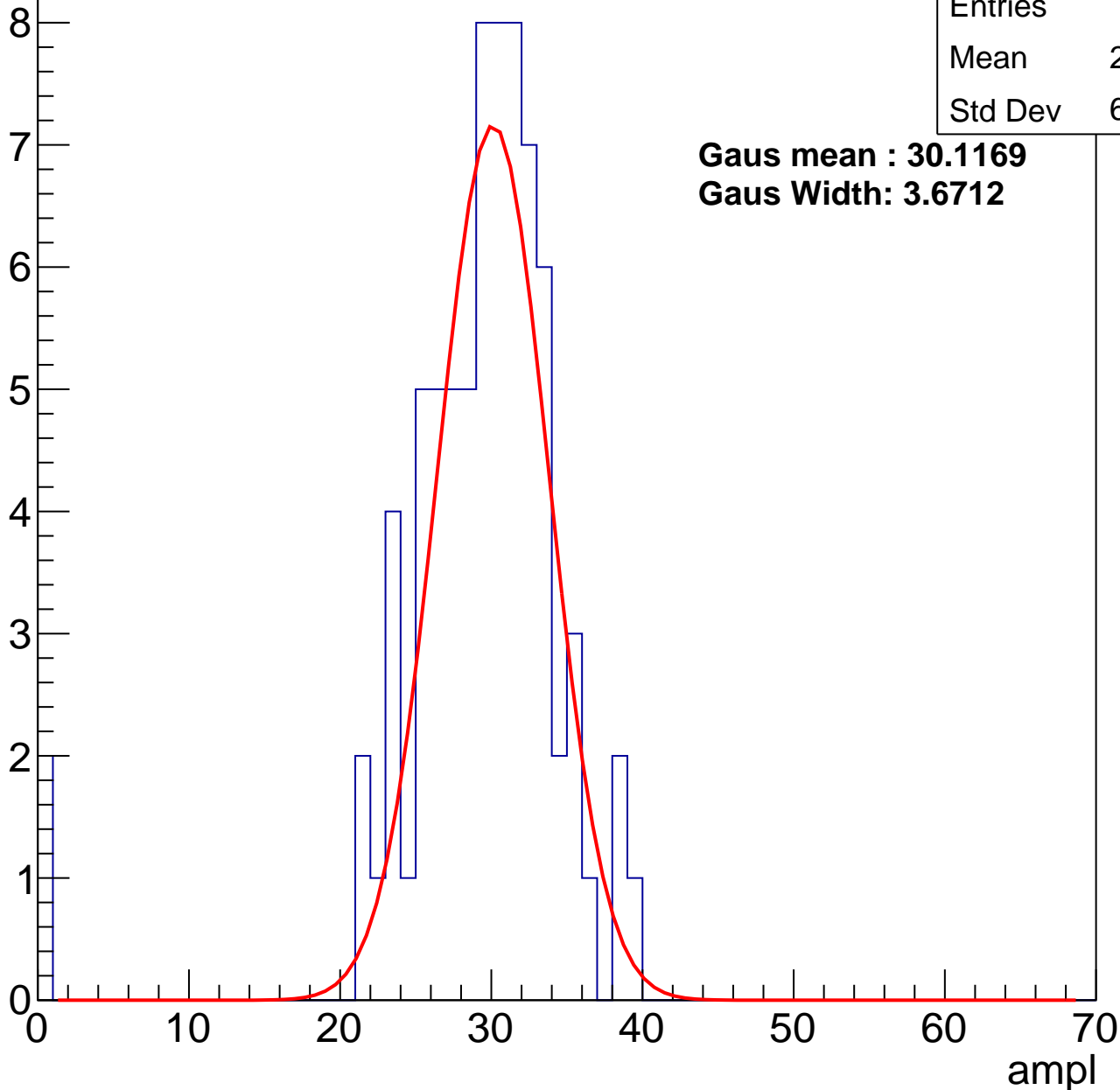
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.63
Std Dev	6.108

Gaus mean : 30.1169

Gaus Width: 3.6712



B1L103S, U5-ch105, adc1

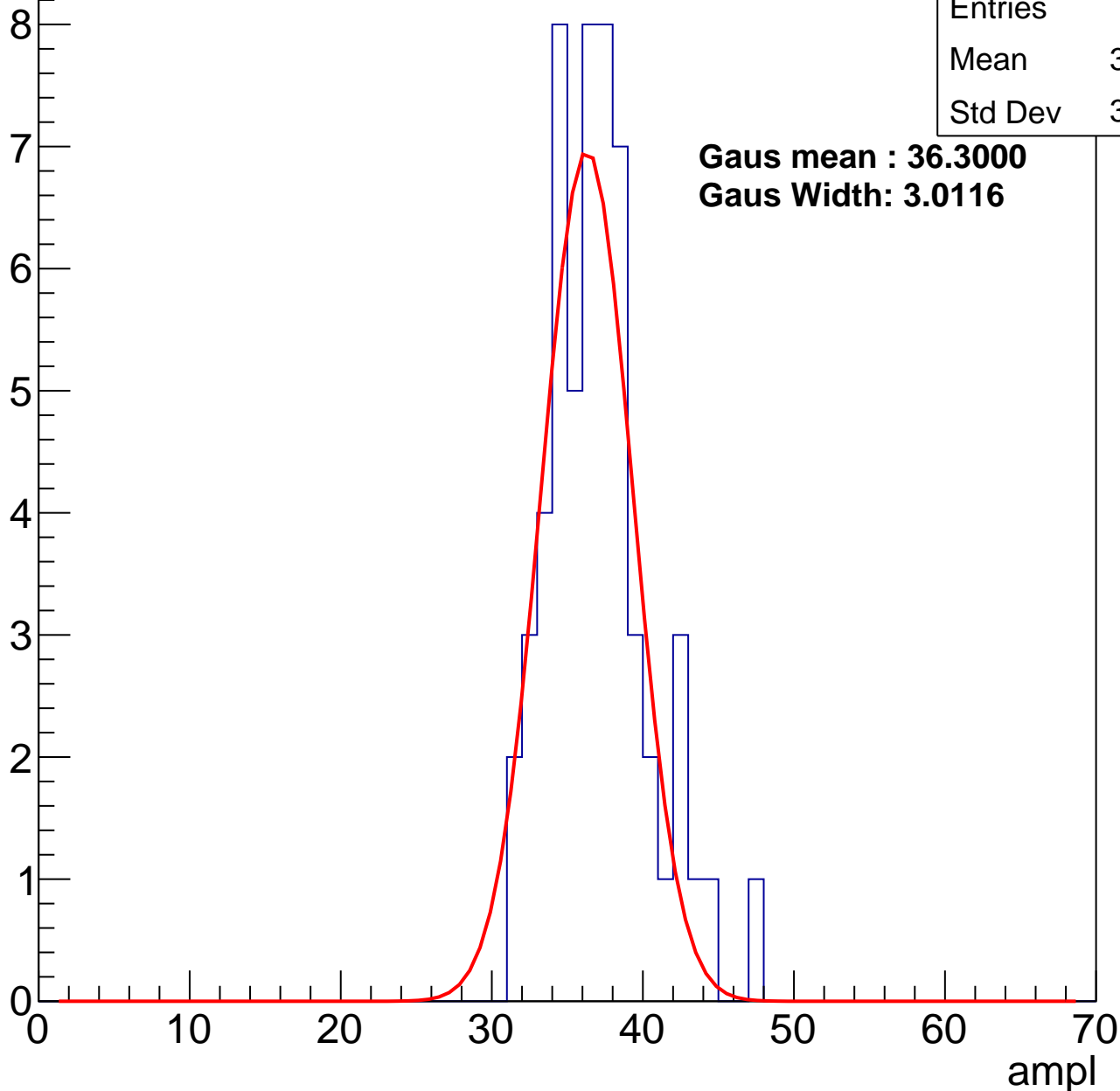
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.58
Std Dev	3.282

Gaus mean : 36.3000

Gaus Width: 3.0116



B1L103S, U5-ch105, adc2

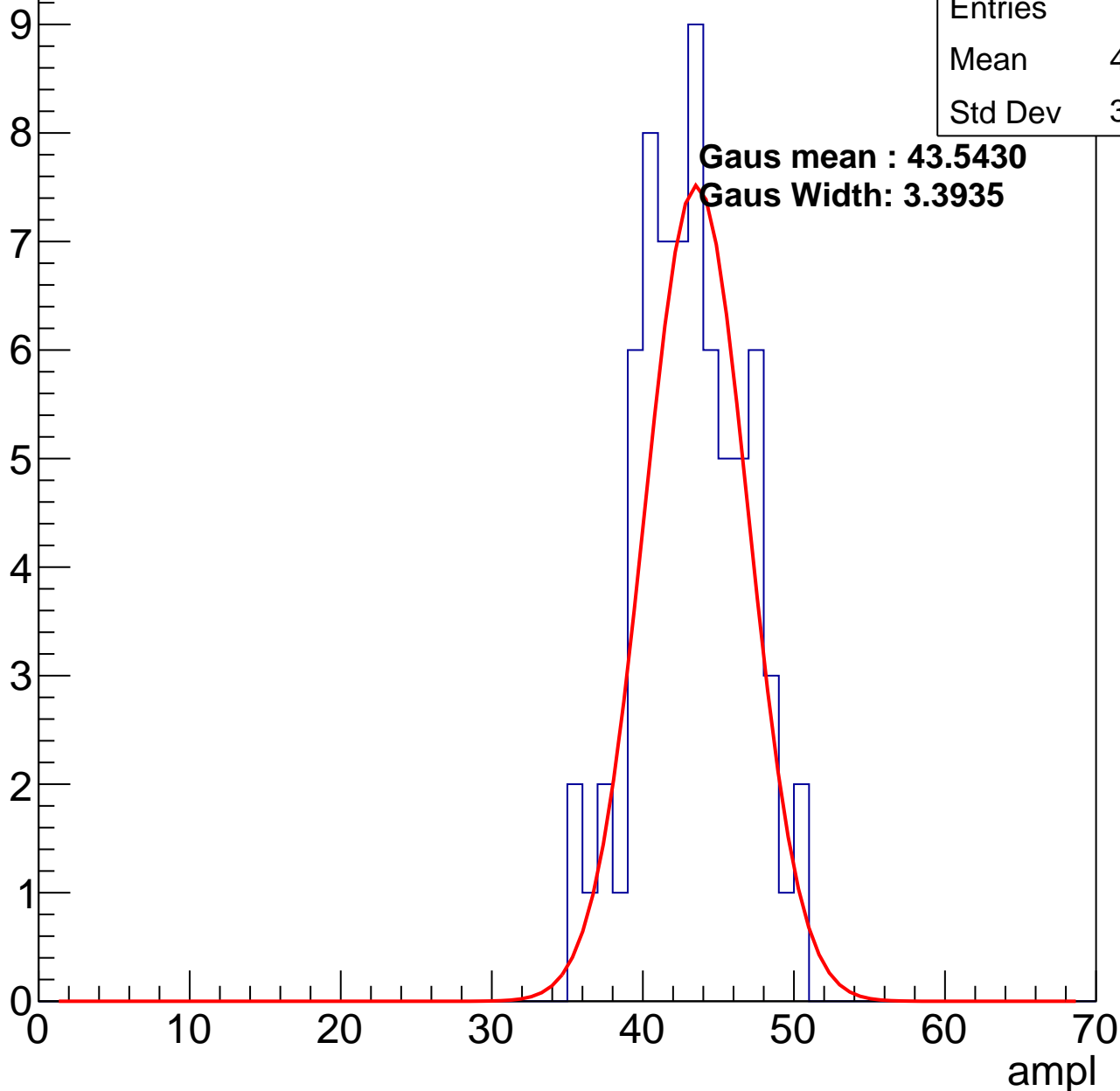
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.73
Std Dev	3.456

Gaus mean : 43.5430

Gaus Width: 3.3935

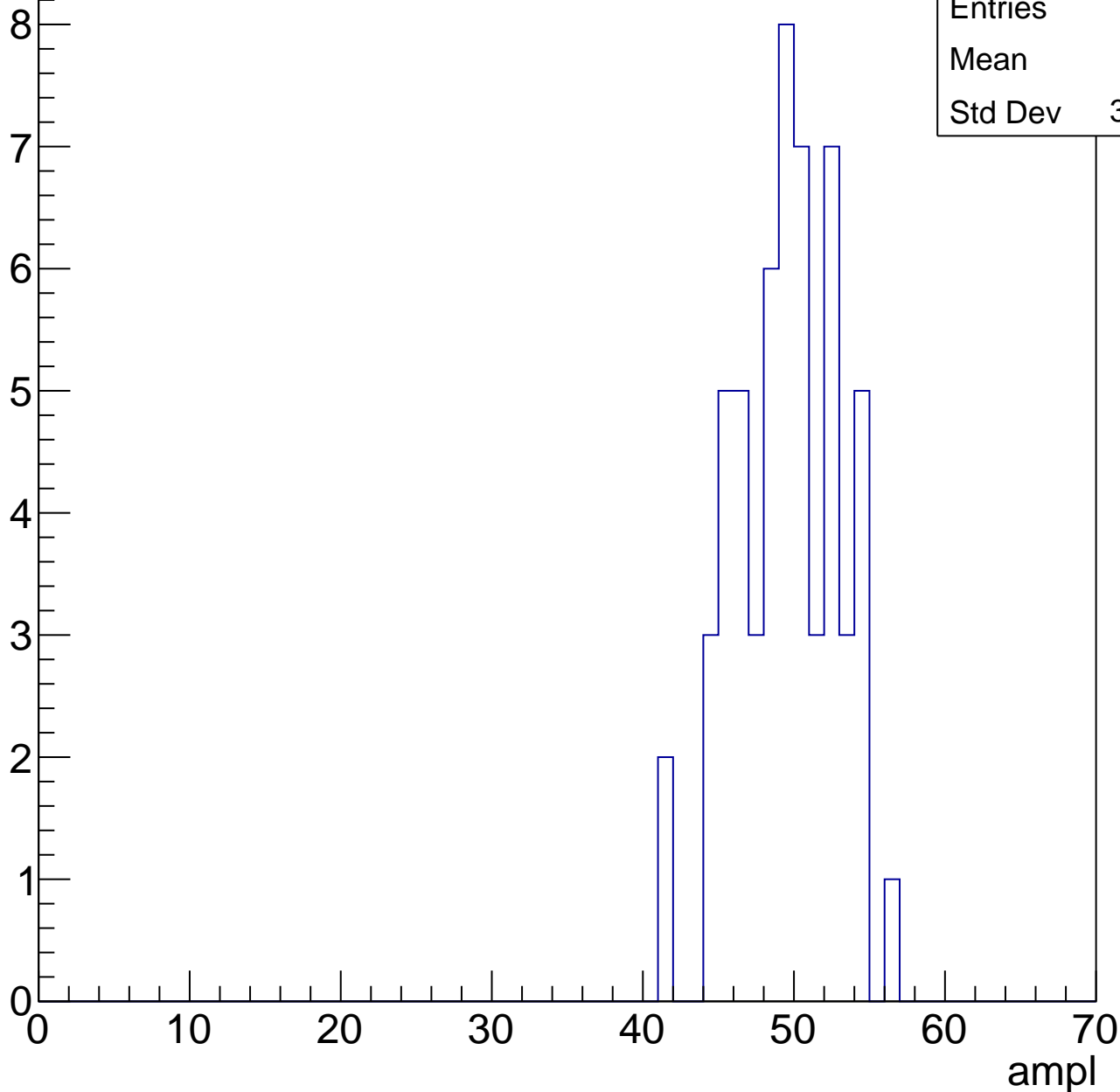


B1L103S, U5-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49
Std Dev	3.348



B1L103S, U5-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	63
Mean	55.48
Std Dev	3.121

Entry

10

8

6

4

2

0

0

10

20

30

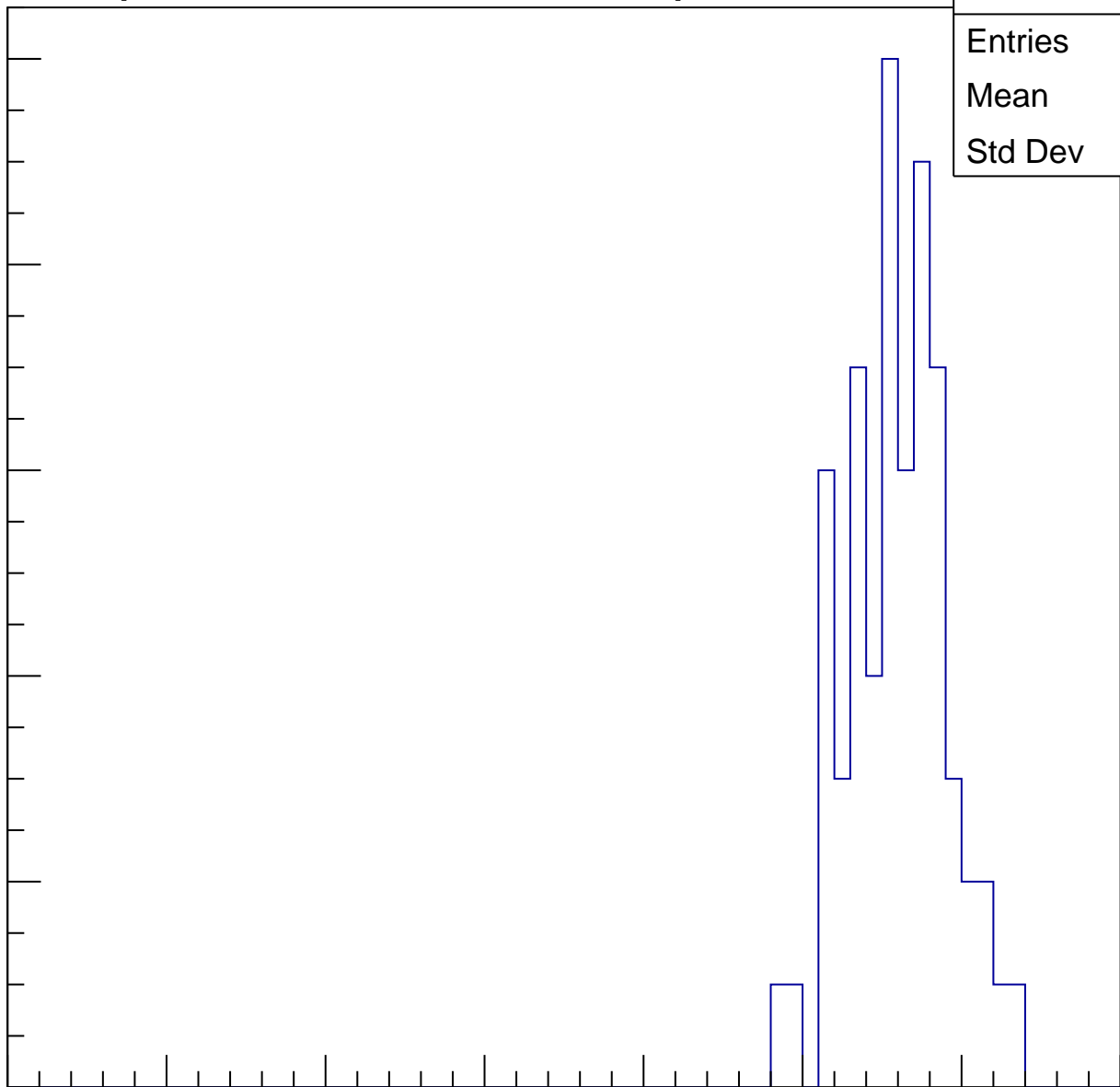
40

50

60

70

ampl

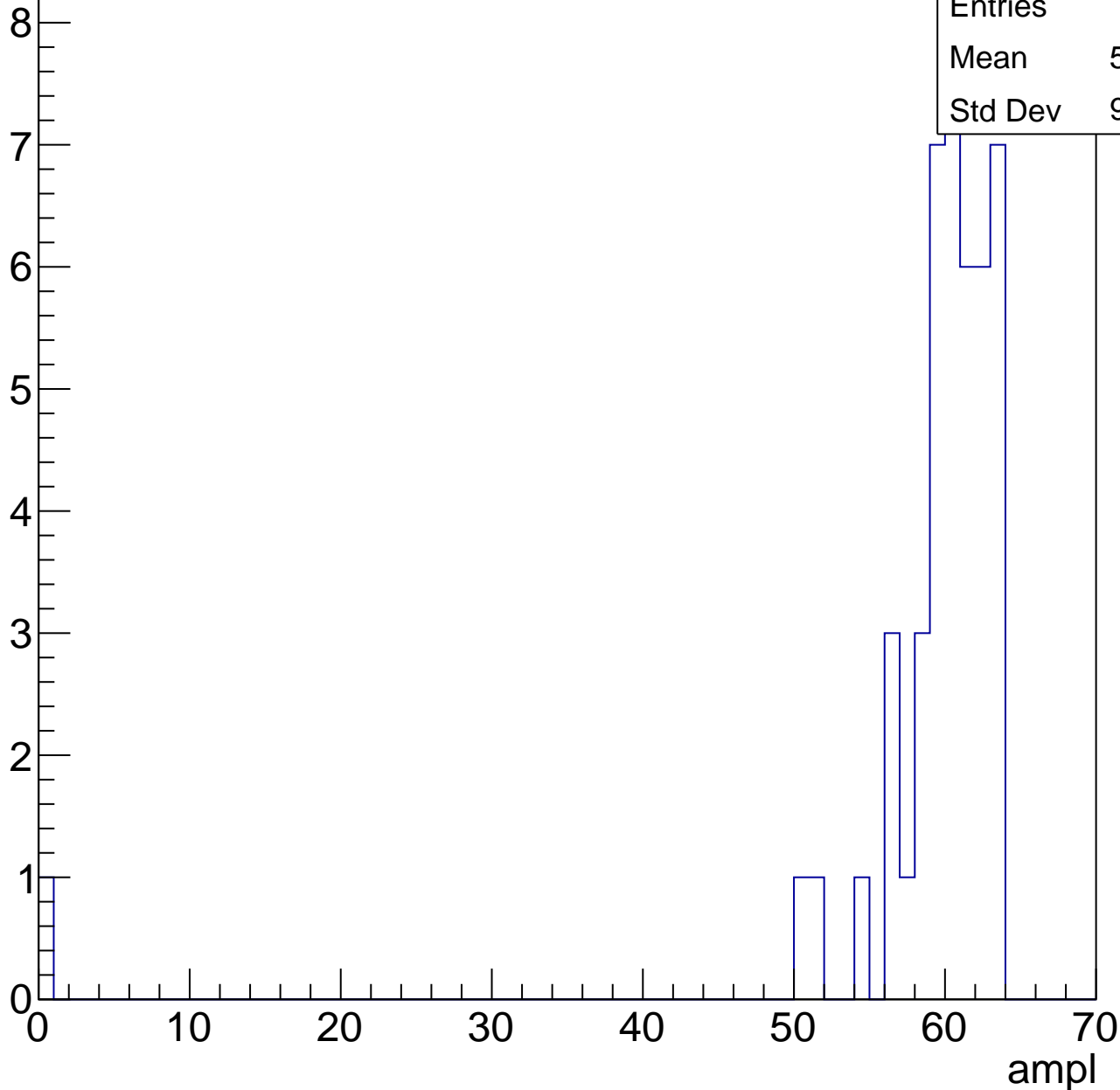


B1L103S, U5-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

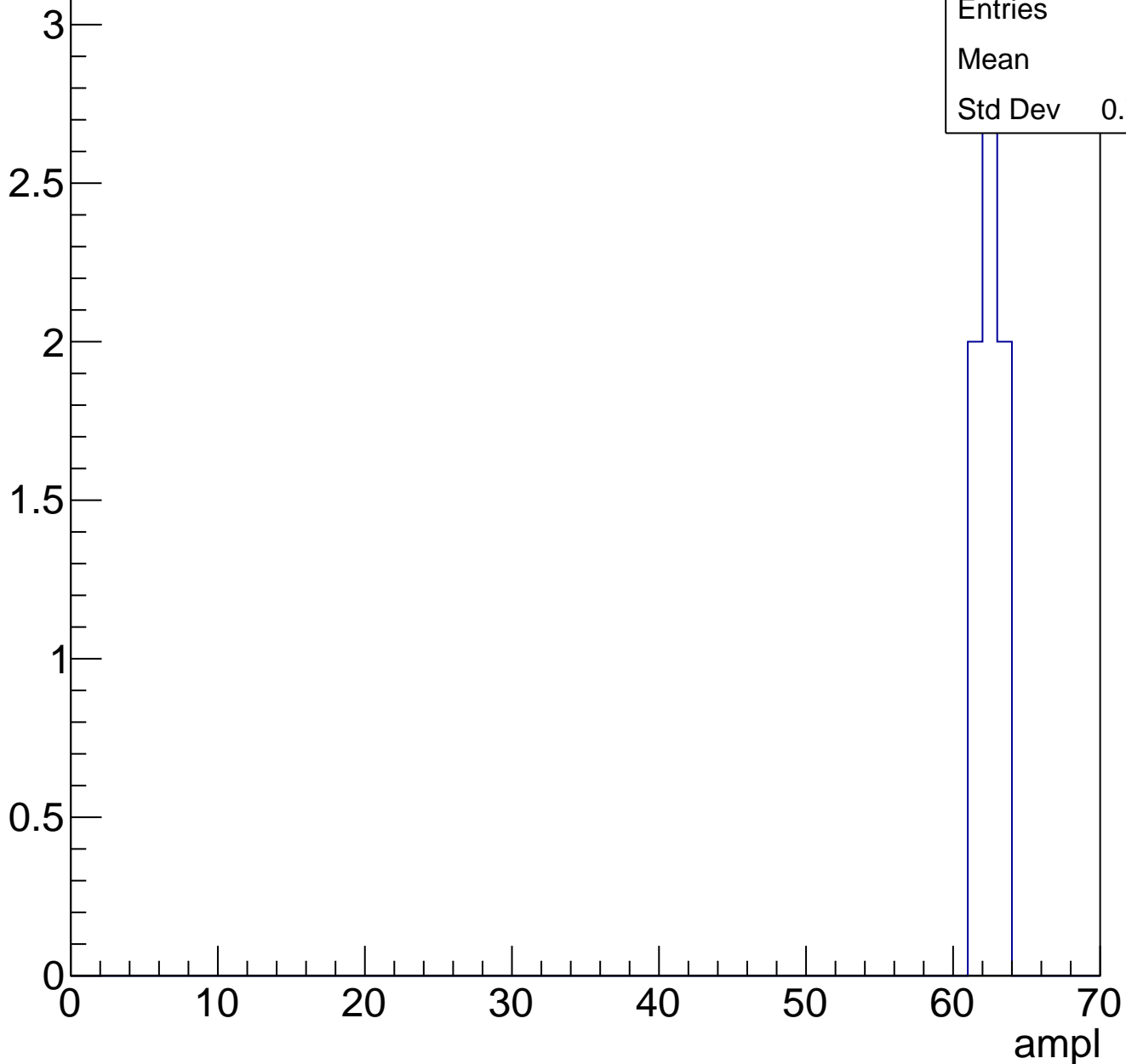
Entries	45
Mean	58.36
Std Dev	9.267



B1L103S, U5-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch106, adc0

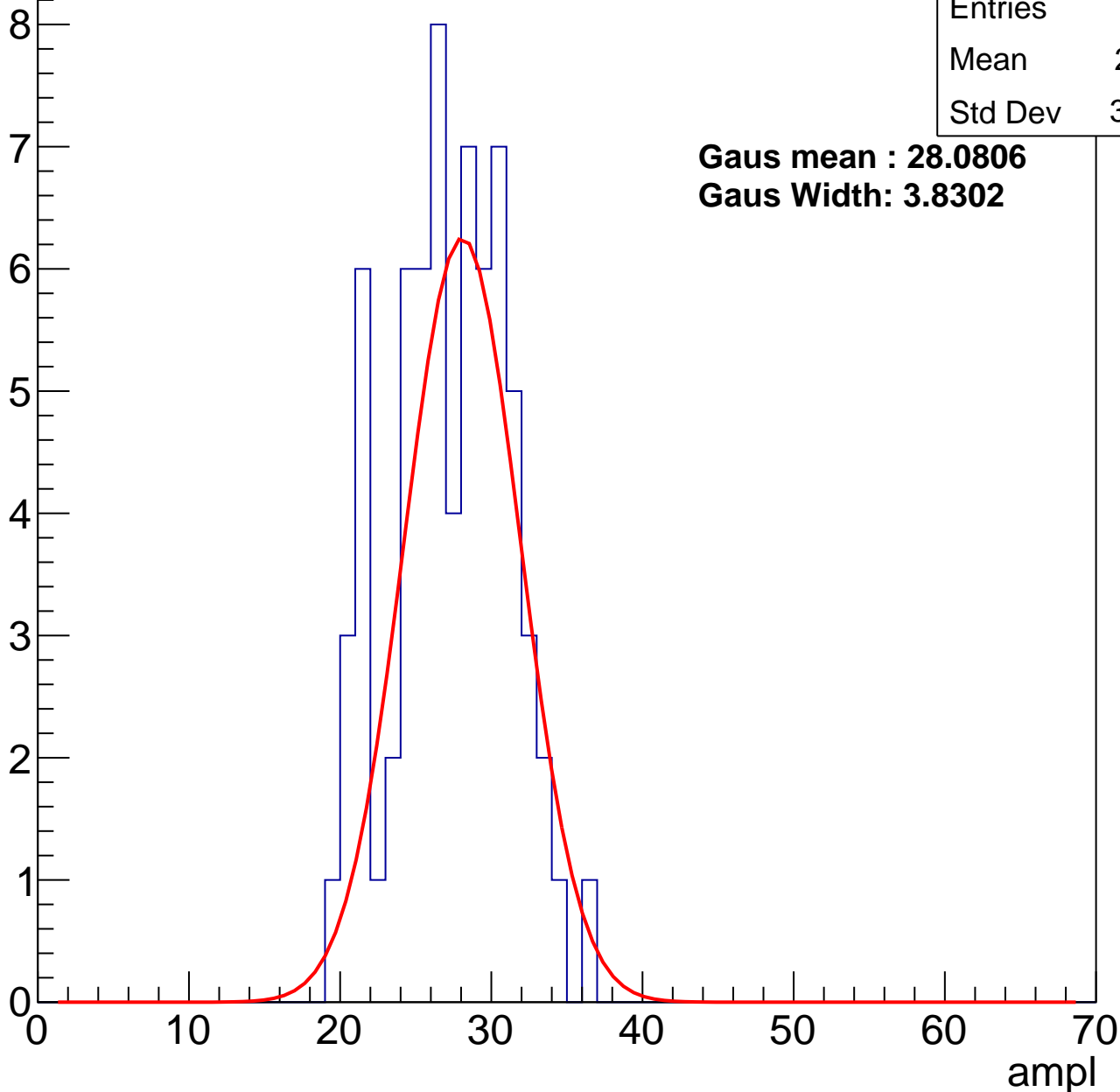
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	26.81
Std Dev	3.842

Gaus mean : 28.0806

Gaus Width: 3.8302



B1L103S, U5-ch106, adc1

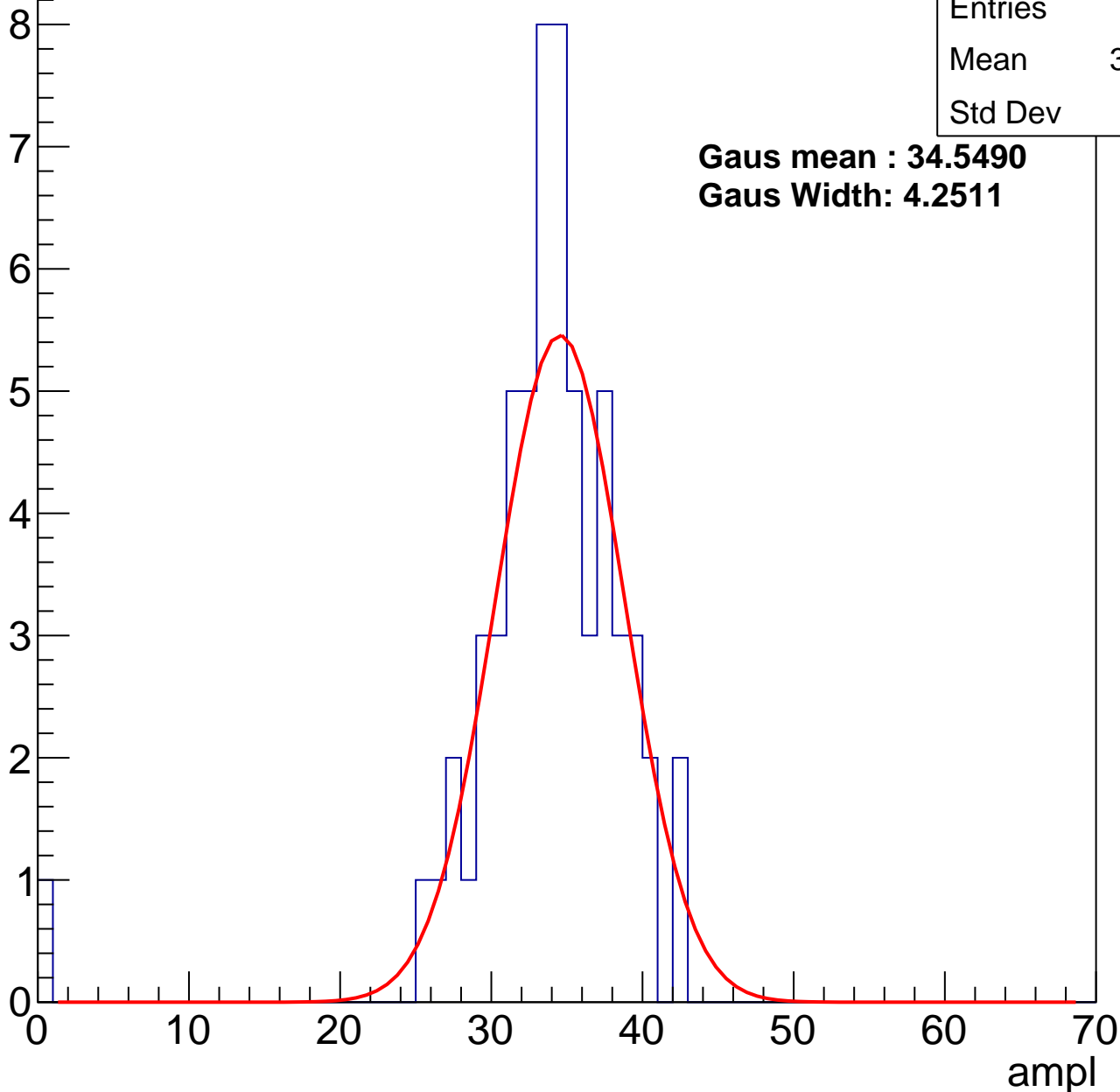
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	33.18
Std Dev	5.69

Gaus mean : 34.5490

Gaus Width: 4.2511



B1L103S, U5-ch106, adc2

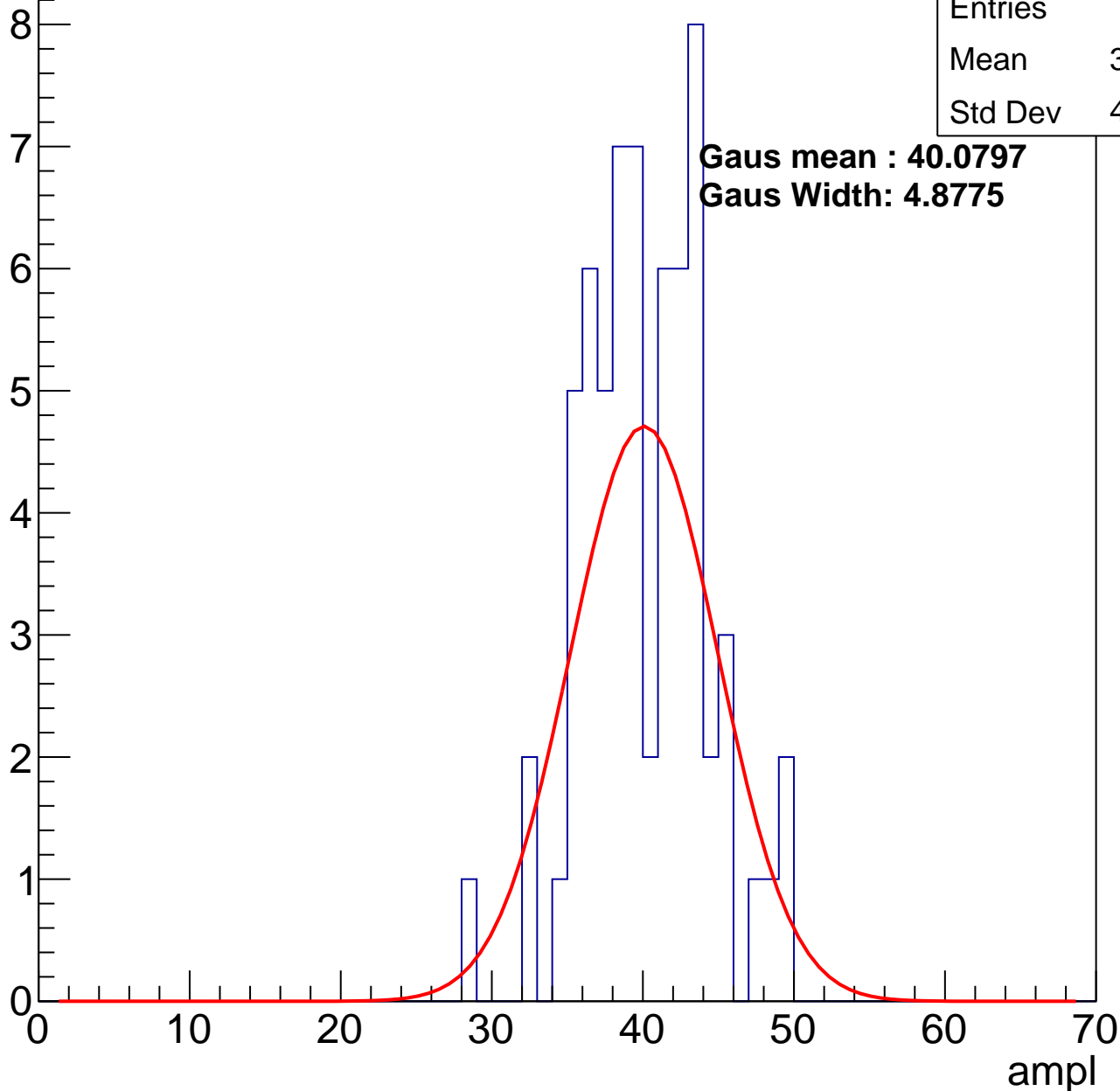
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	39.68
Std Dev	4.088

Gaus mean : 40.0797

Gaus Width: 4.8775

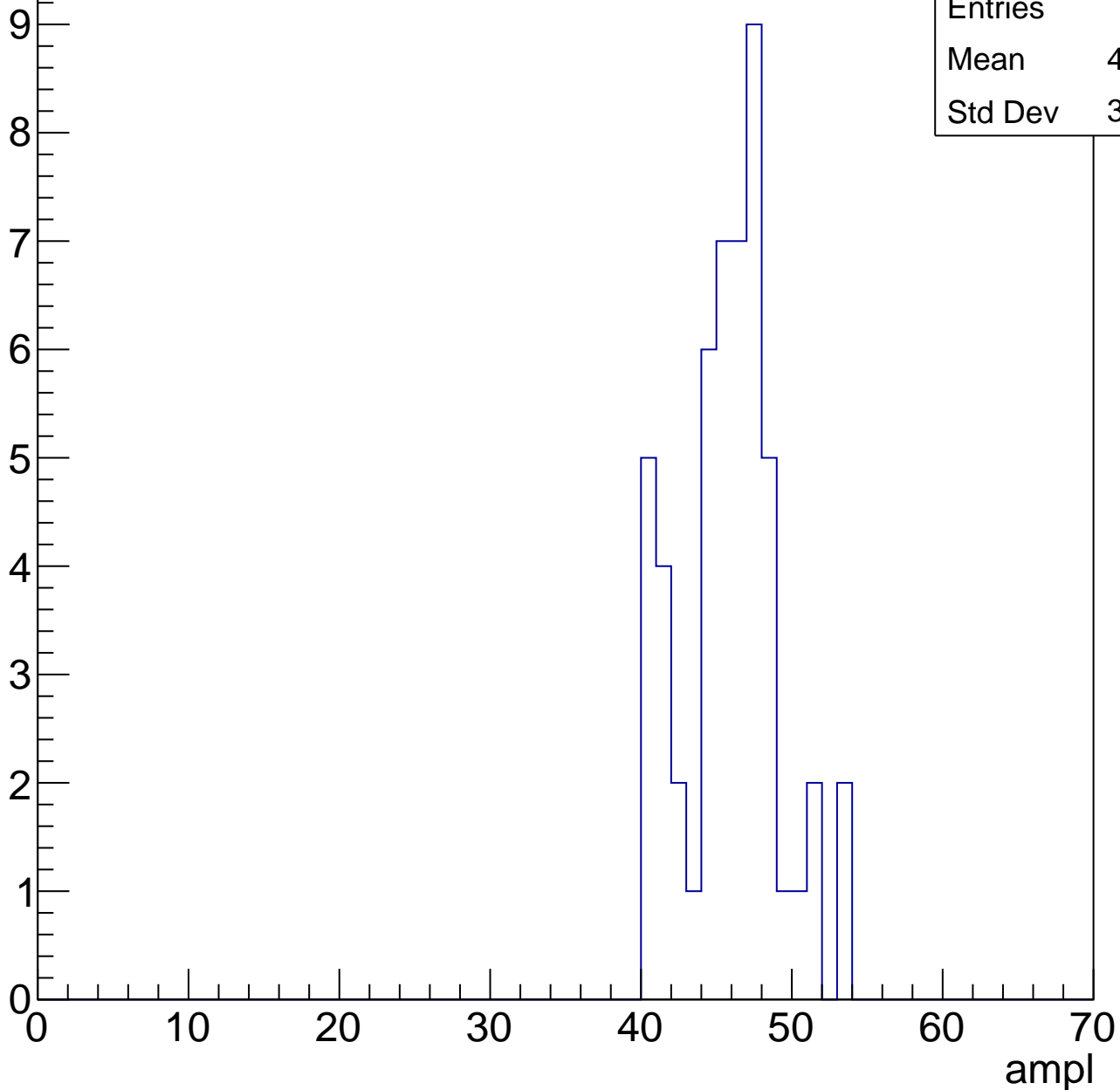


B1L103S, U5-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	45.42
Std Dev	3.213

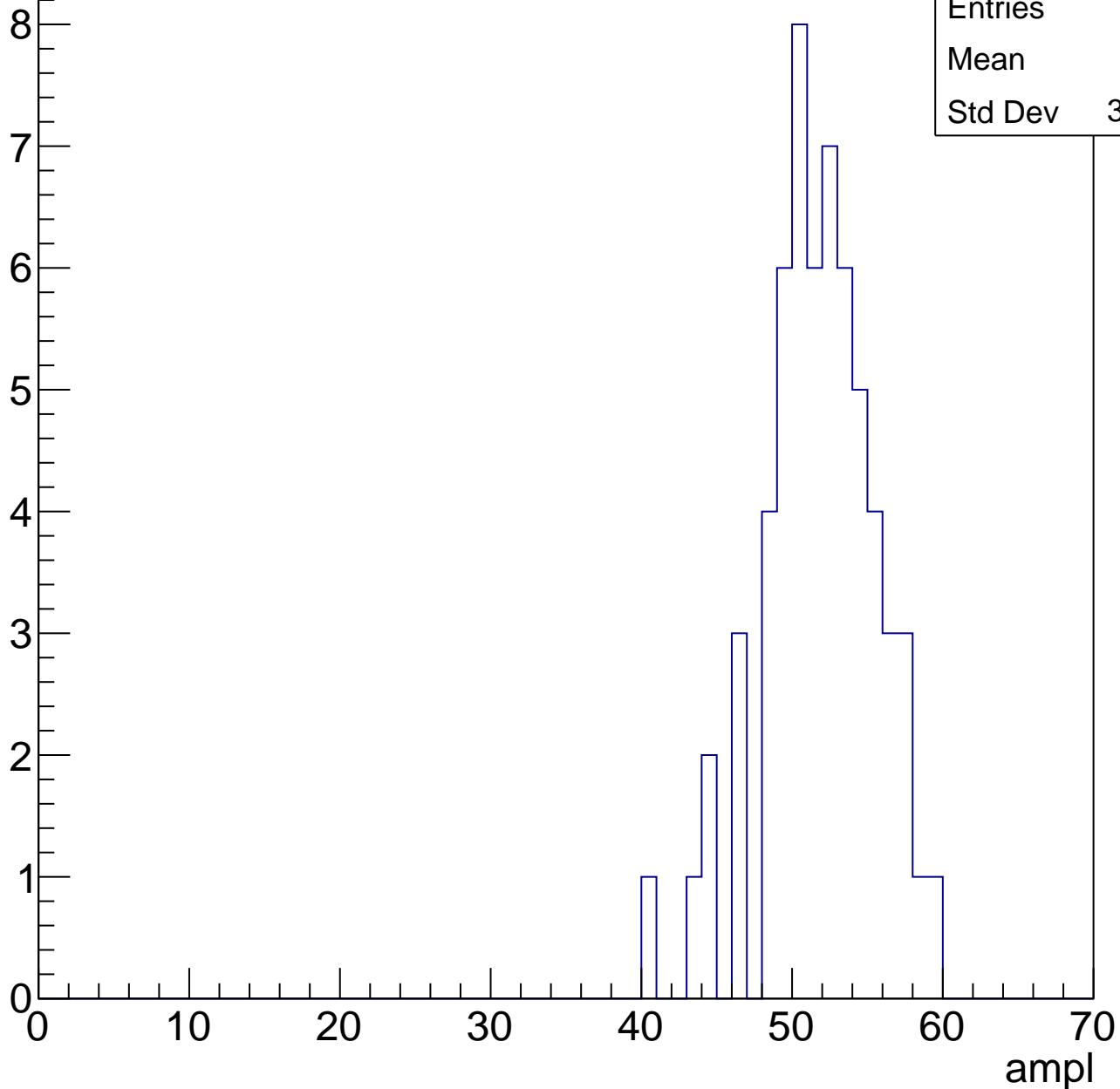


B1L103S, U5-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	51.3
Std Dev	3.748



B1L103S, U5-ch106, adc5

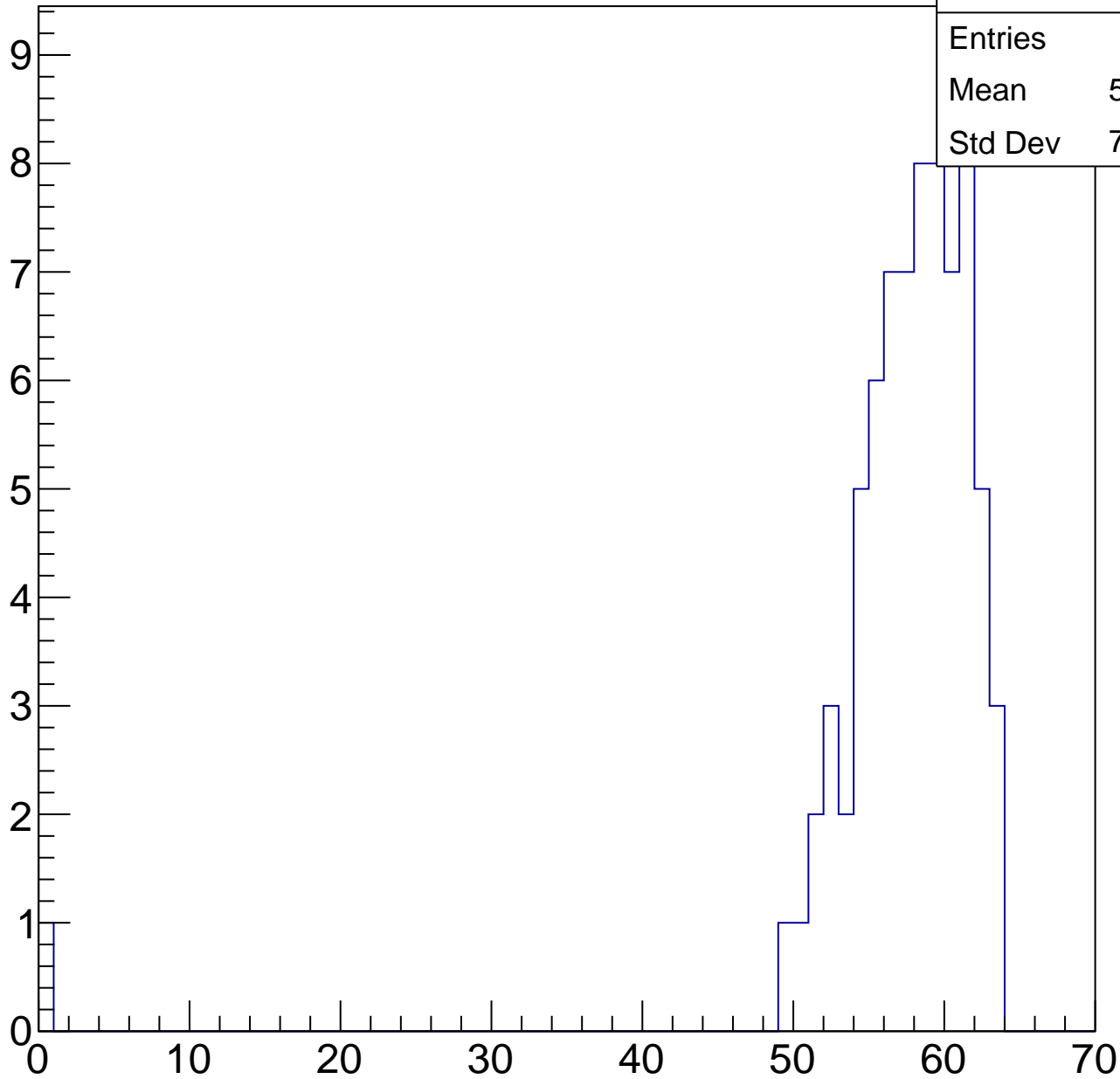
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	75
Mean	56.77
Std Dev	7.388

ampl

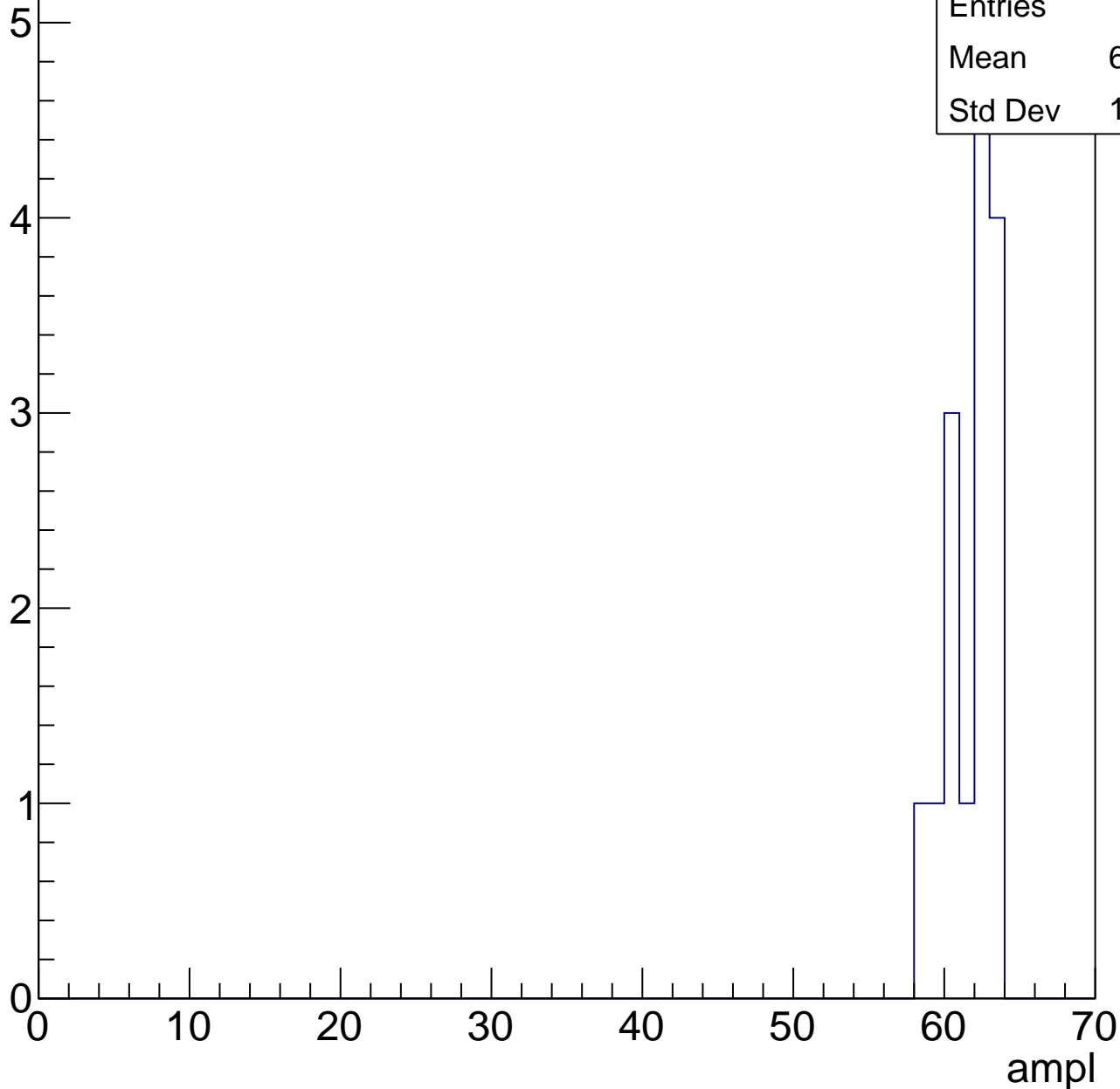


B1L103S, U5-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.33
Std Dev	1.535



B1L103S, U5-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



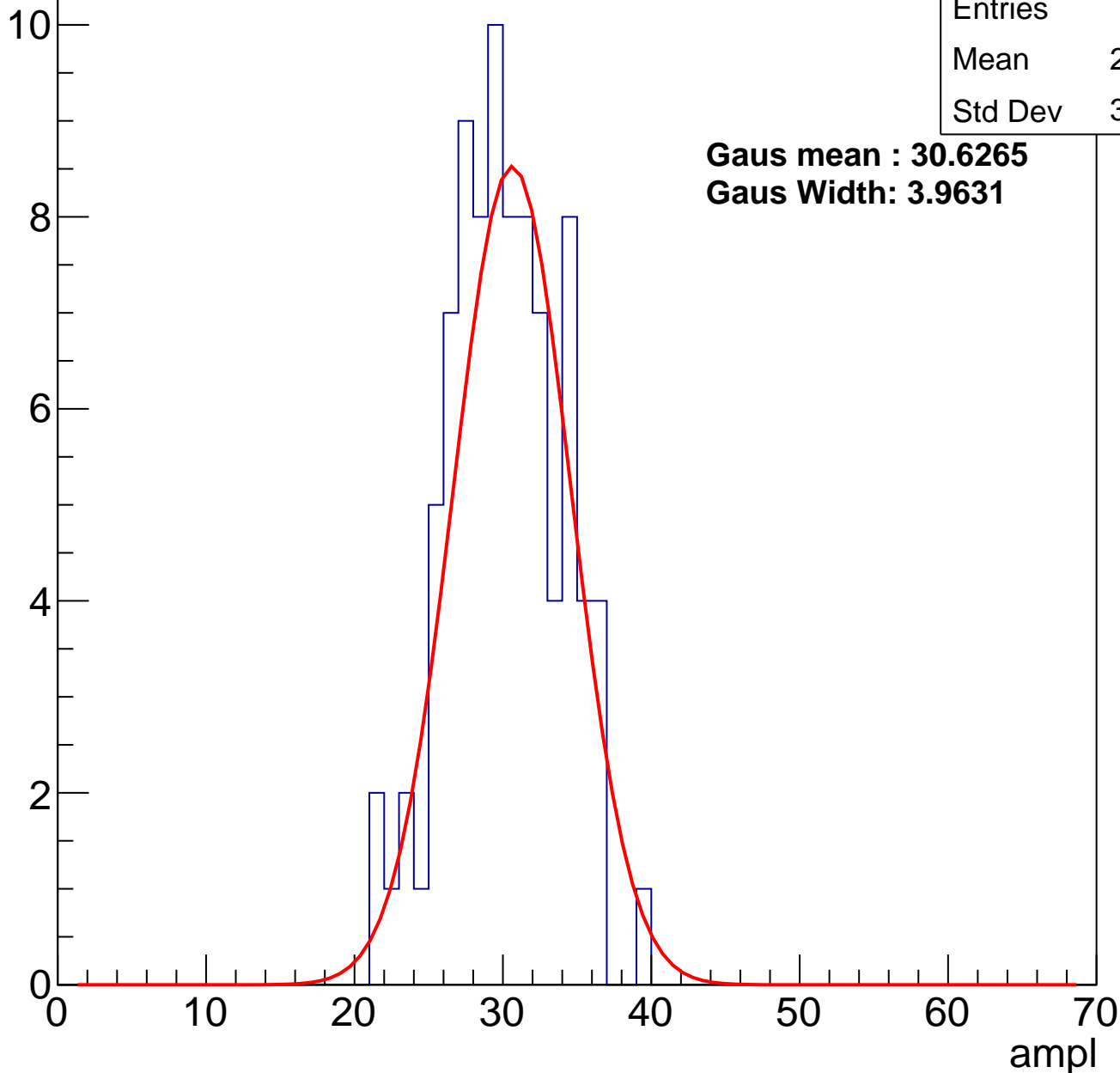
B1L103S, U5-ch107, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	89
Mean	29.63
Std Dev	3.722

Gaus mean : 30.6265
Gaus Width: 3.9631

Entry



B1L103S, U5-ch107, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	36.84
Std Dev	3.446

Gaus mean : 36.9202

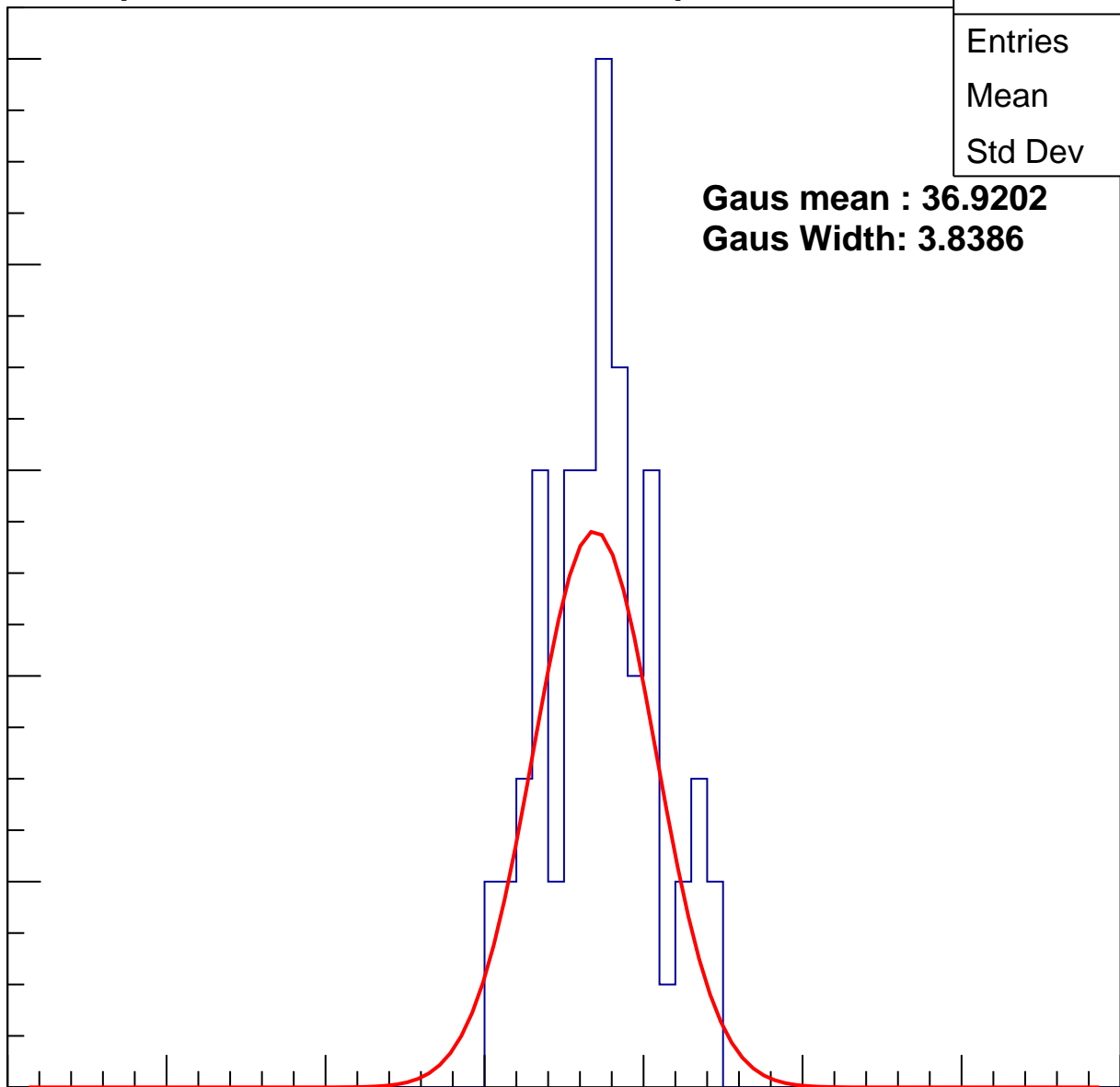
Gaus Width: 3.8386

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch107, adc2

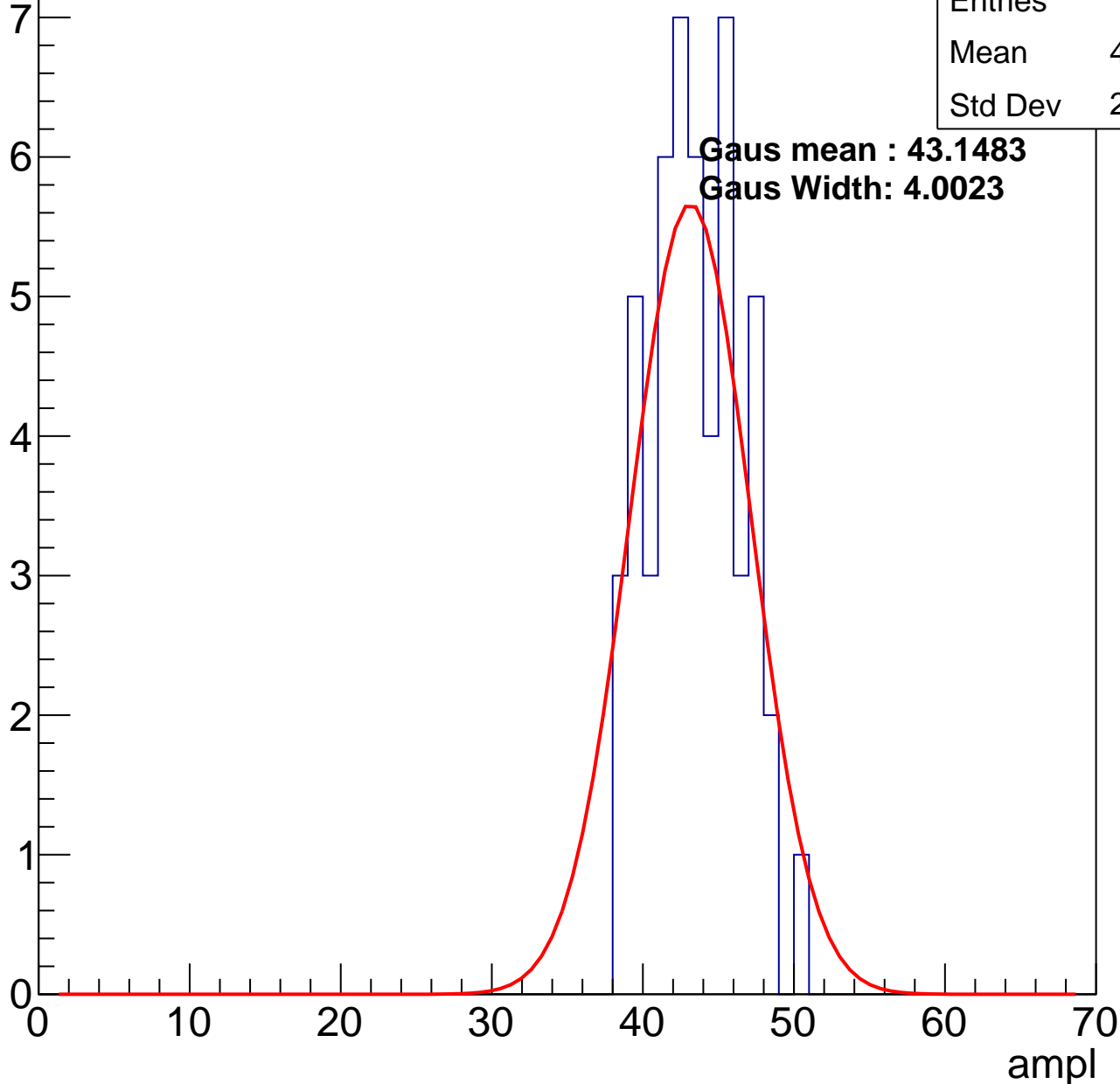
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	43.02
Std Dev	2.945

Gaus mean : 43.1483

Gaus Width: 4.0023

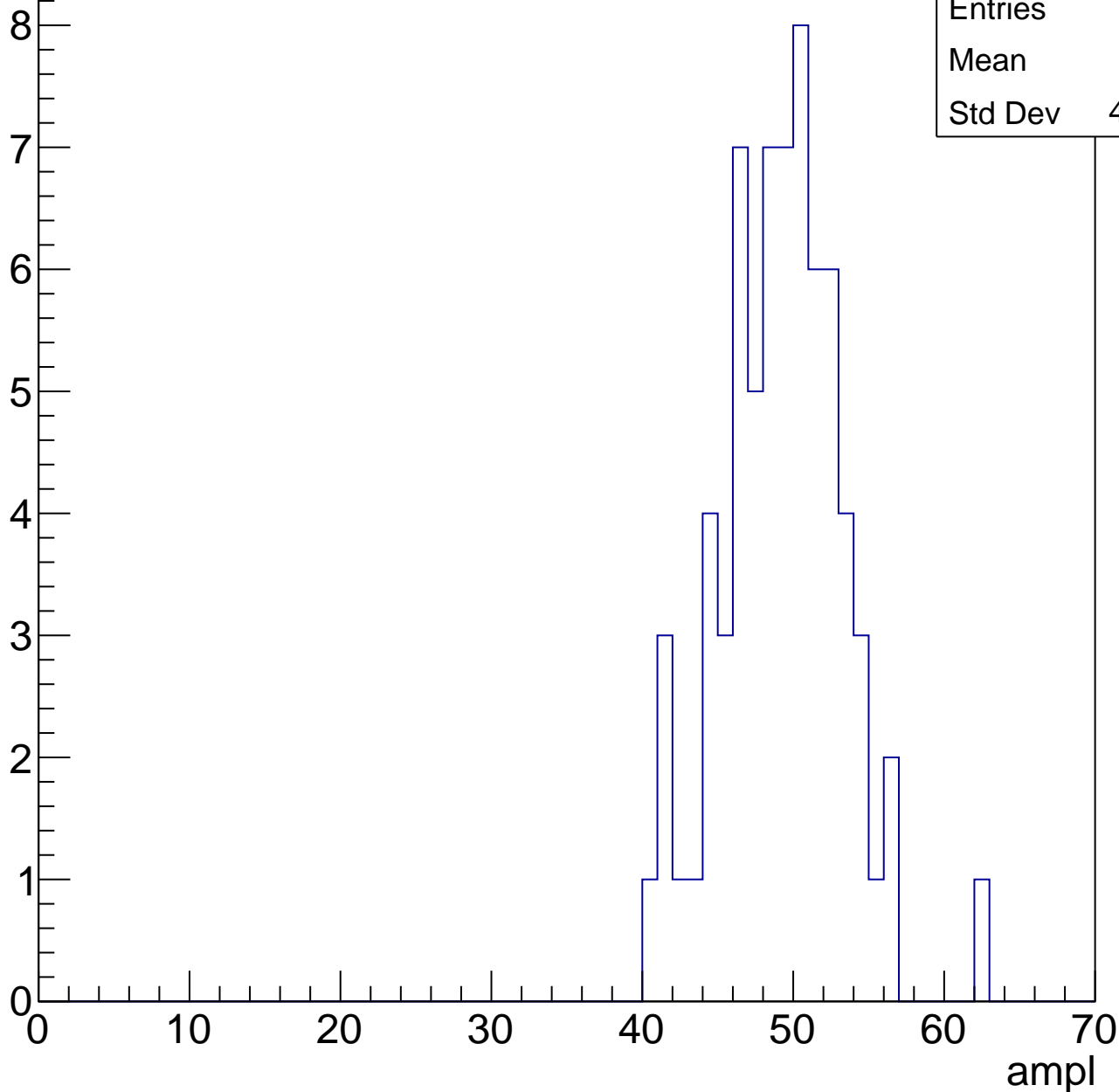


B1L103S, U5-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	48.8
Std Dev	4.002

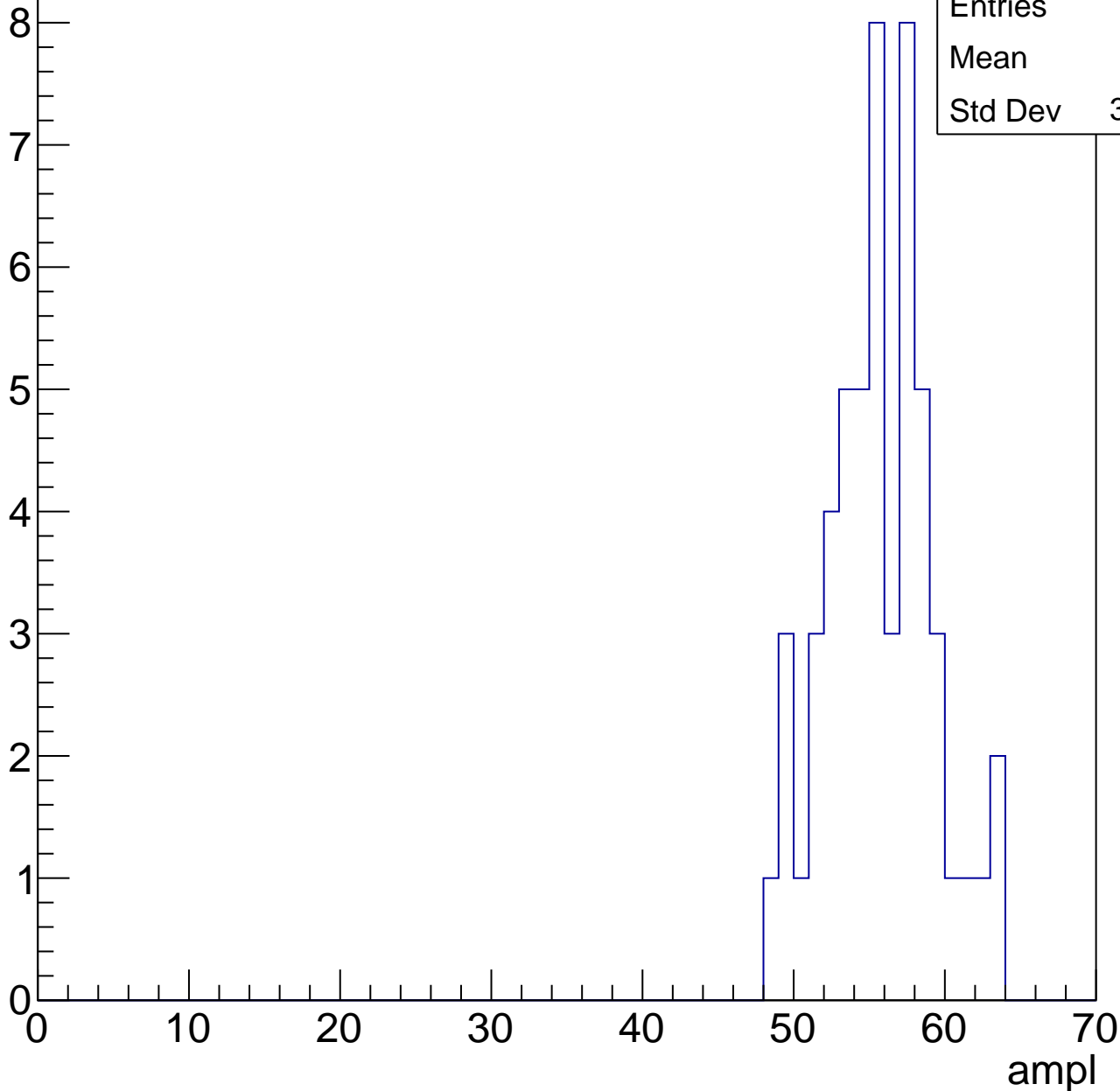


B1L103S, U5-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.2
Std Dev	3.482



B1L103S, U5-ch107, adc5

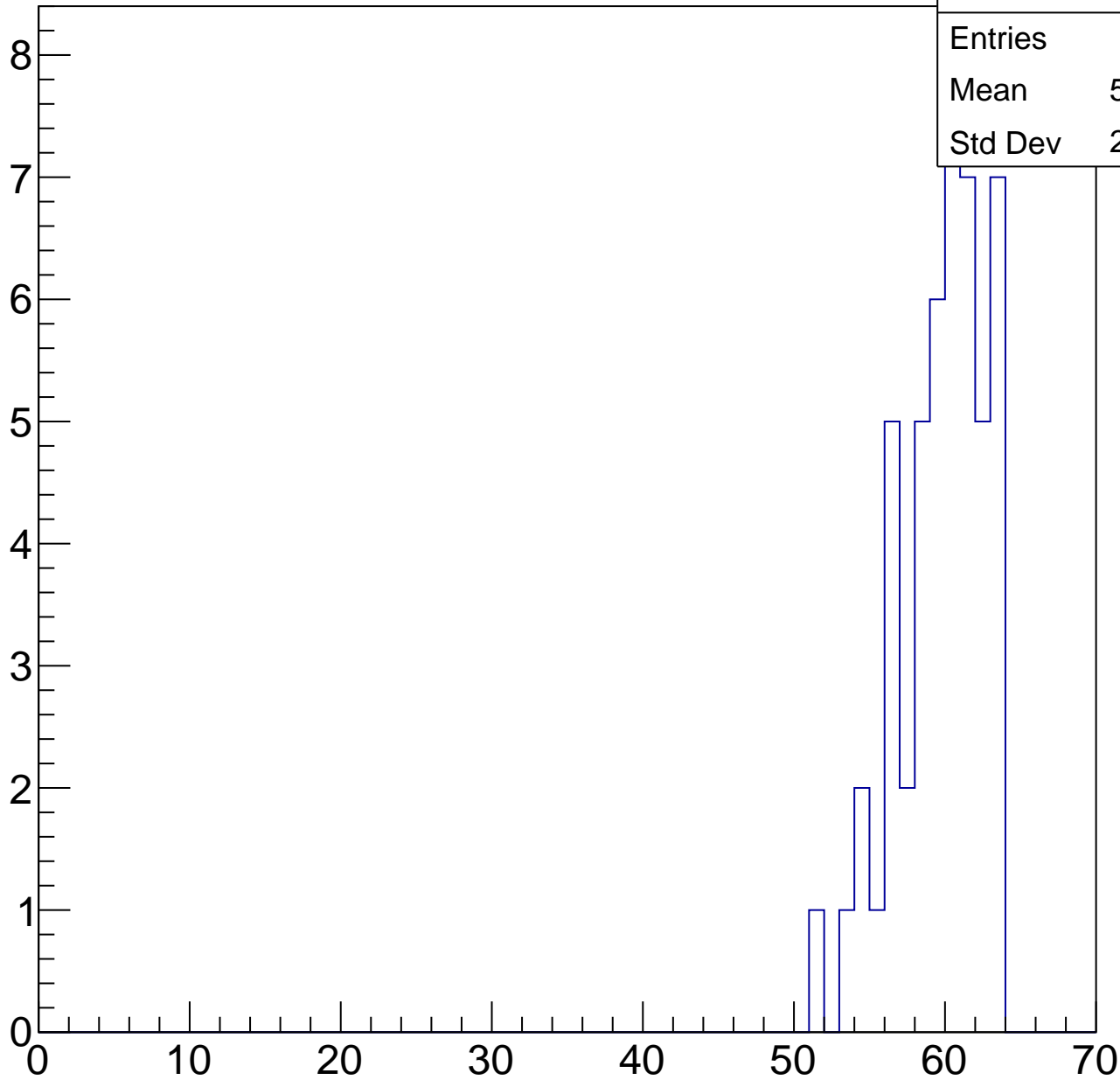
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.26
Std Dev	2.876

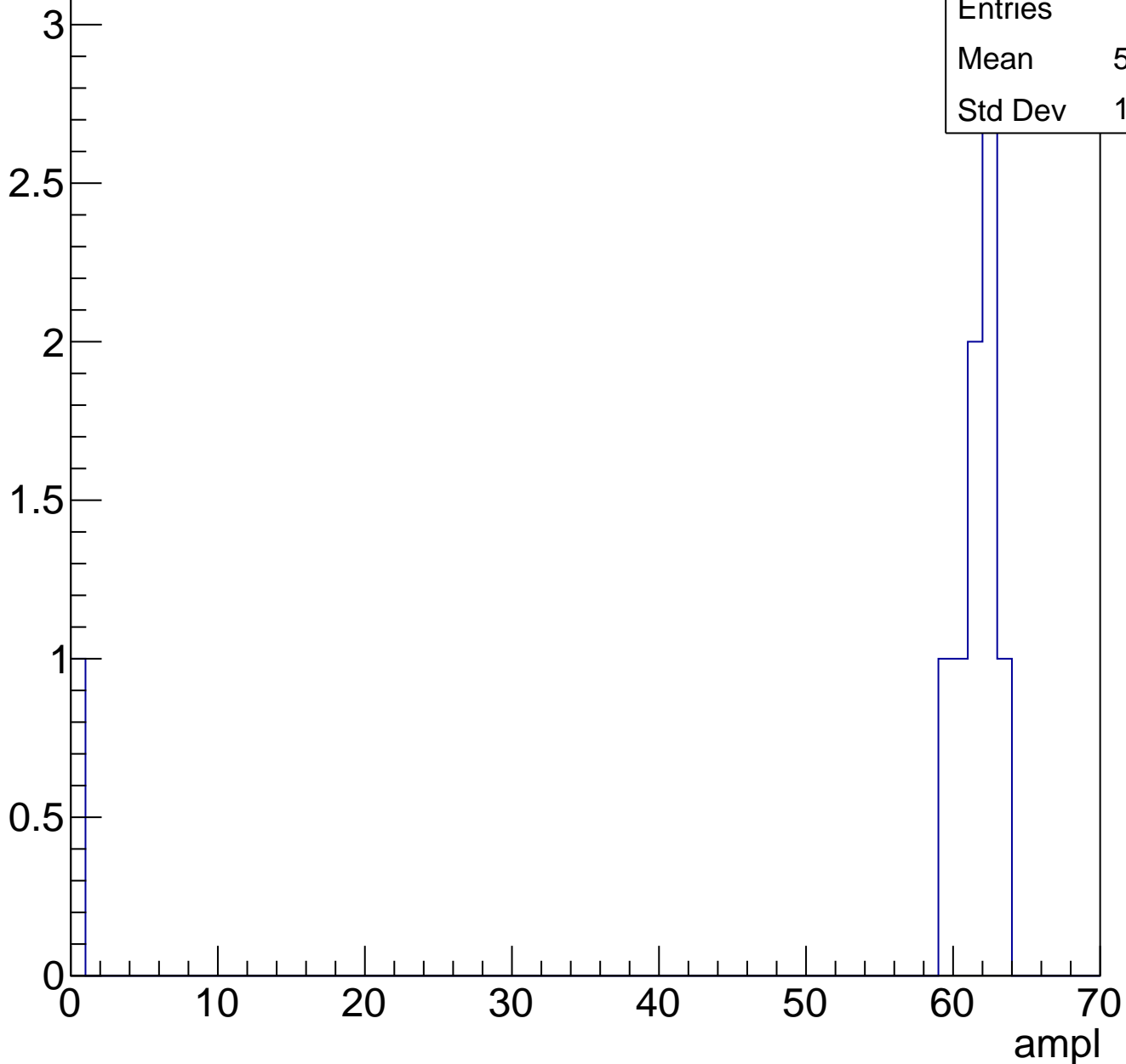
ampl



B1L103S, U5-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch108, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	26.75
Std Dev	5.274

Gaus mean : 28.7874

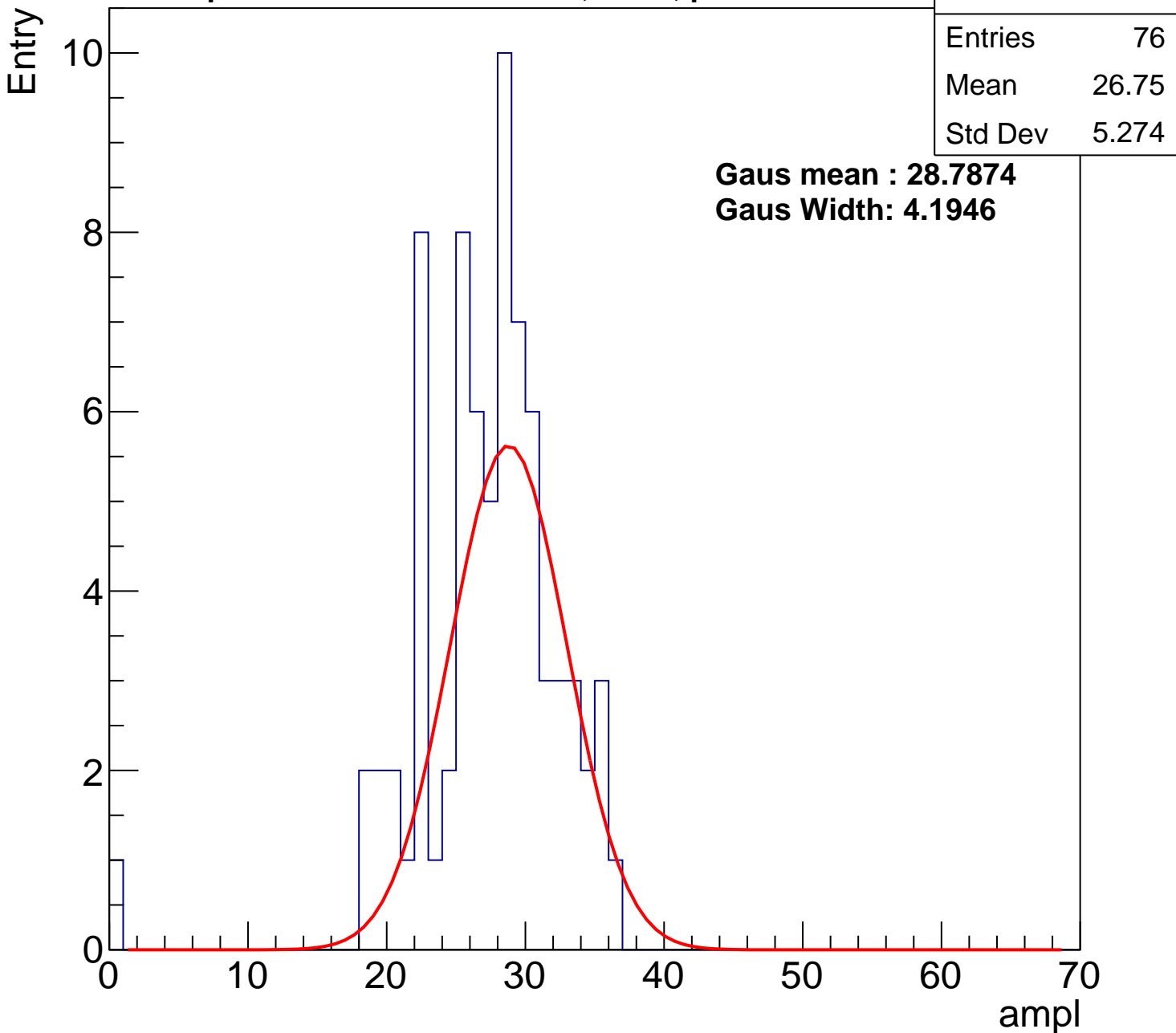
Gaus Width: 4.1946

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch108, adc1

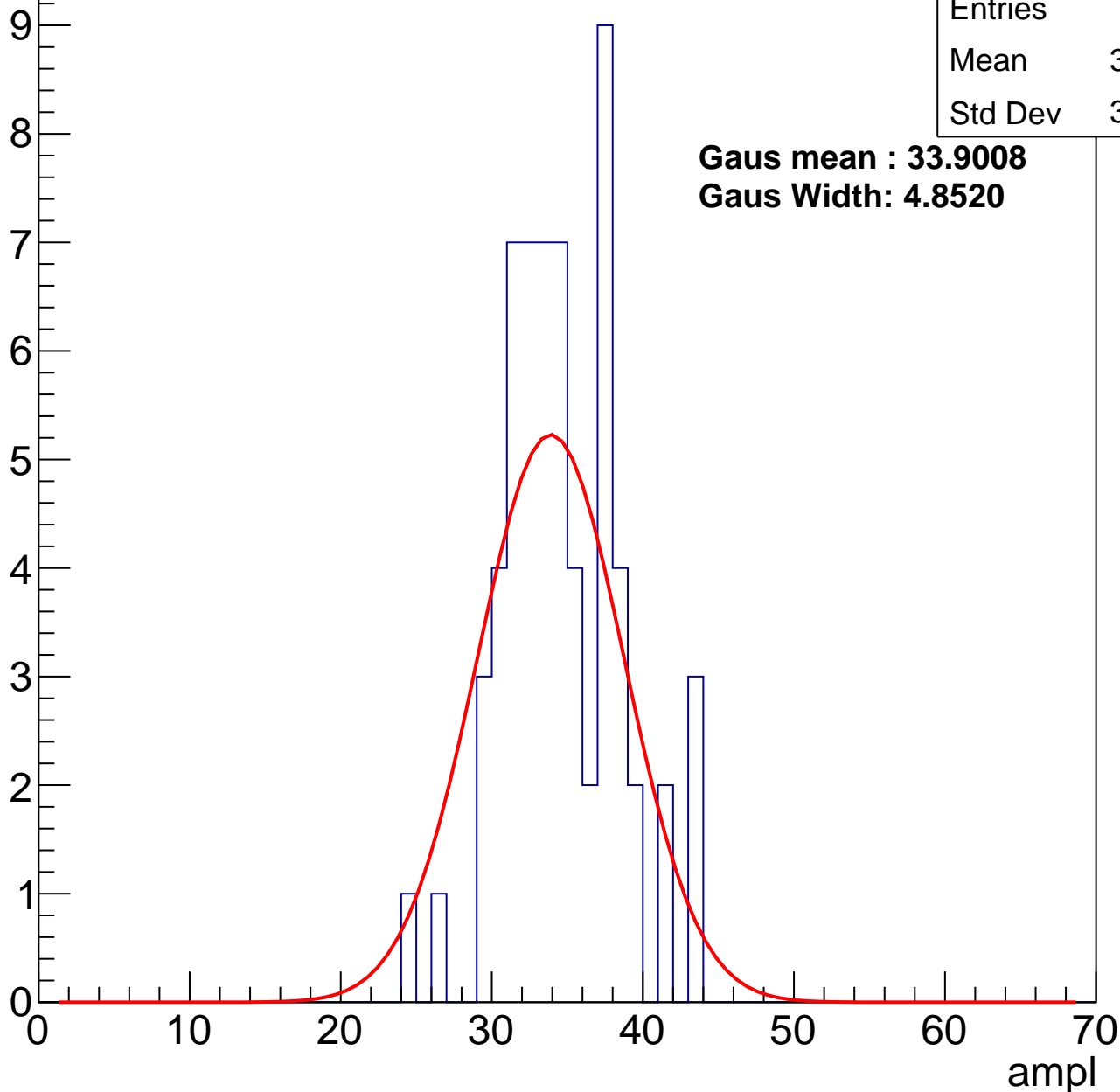
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	34.17
Std Dev	3.877

Gaus mean : 33.9008

Gaus Width: 4.8520



B1L103S, U5-ch108, adc2

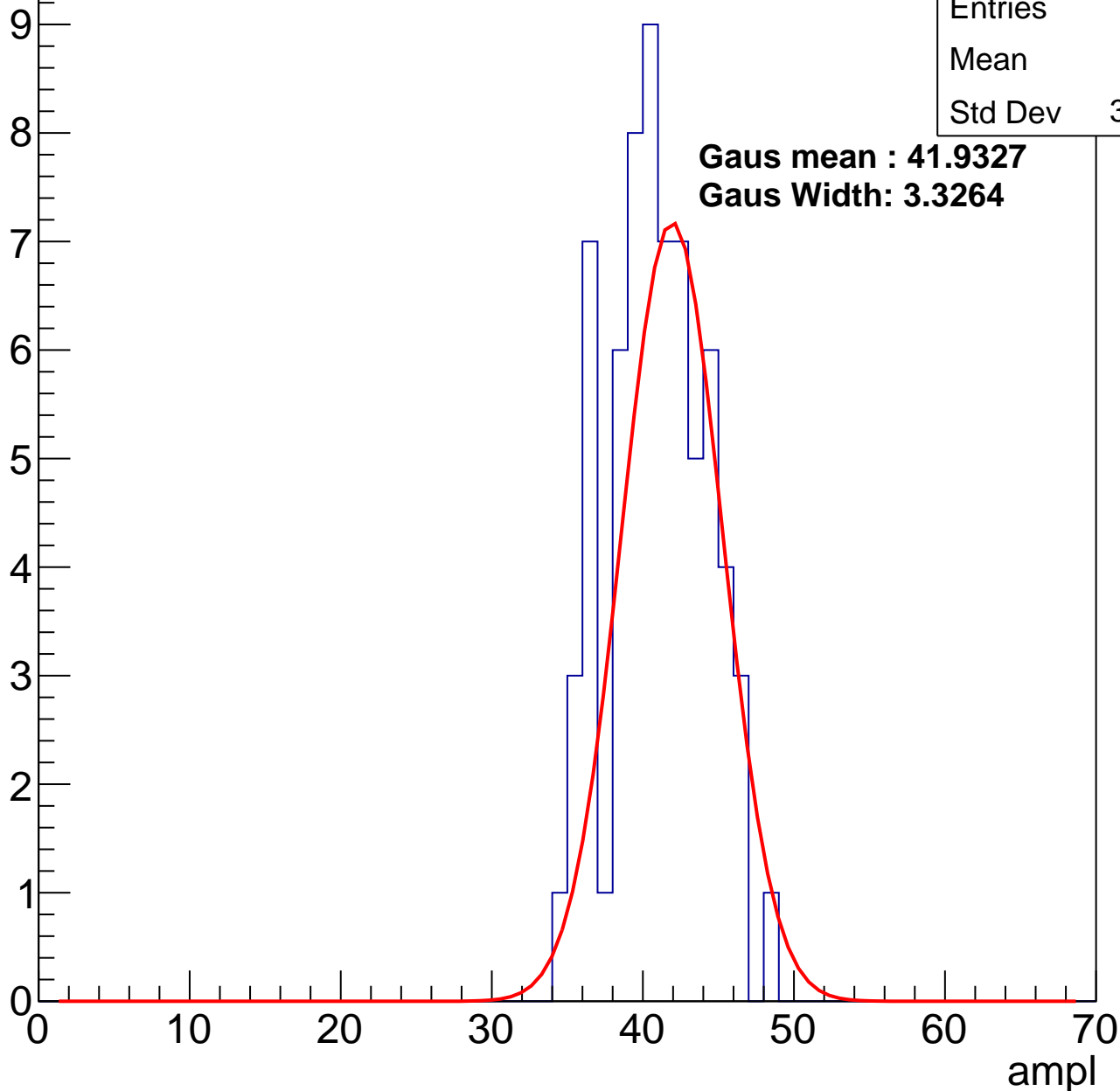
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	40.5
Std Dev	3.197

Gaus mean : 41.9327

Gaus Width: 3.3264

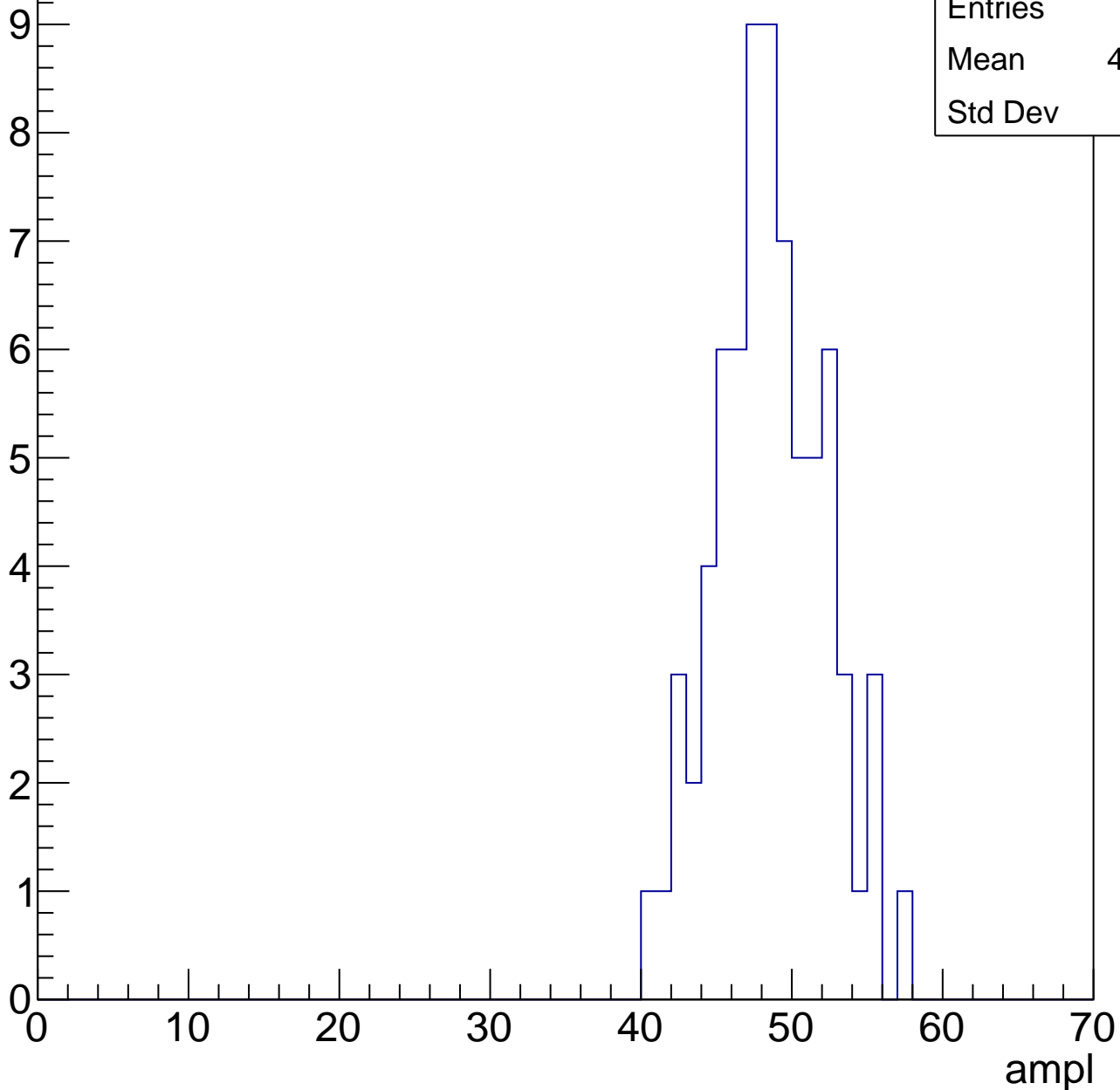


B1L103S, U5-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

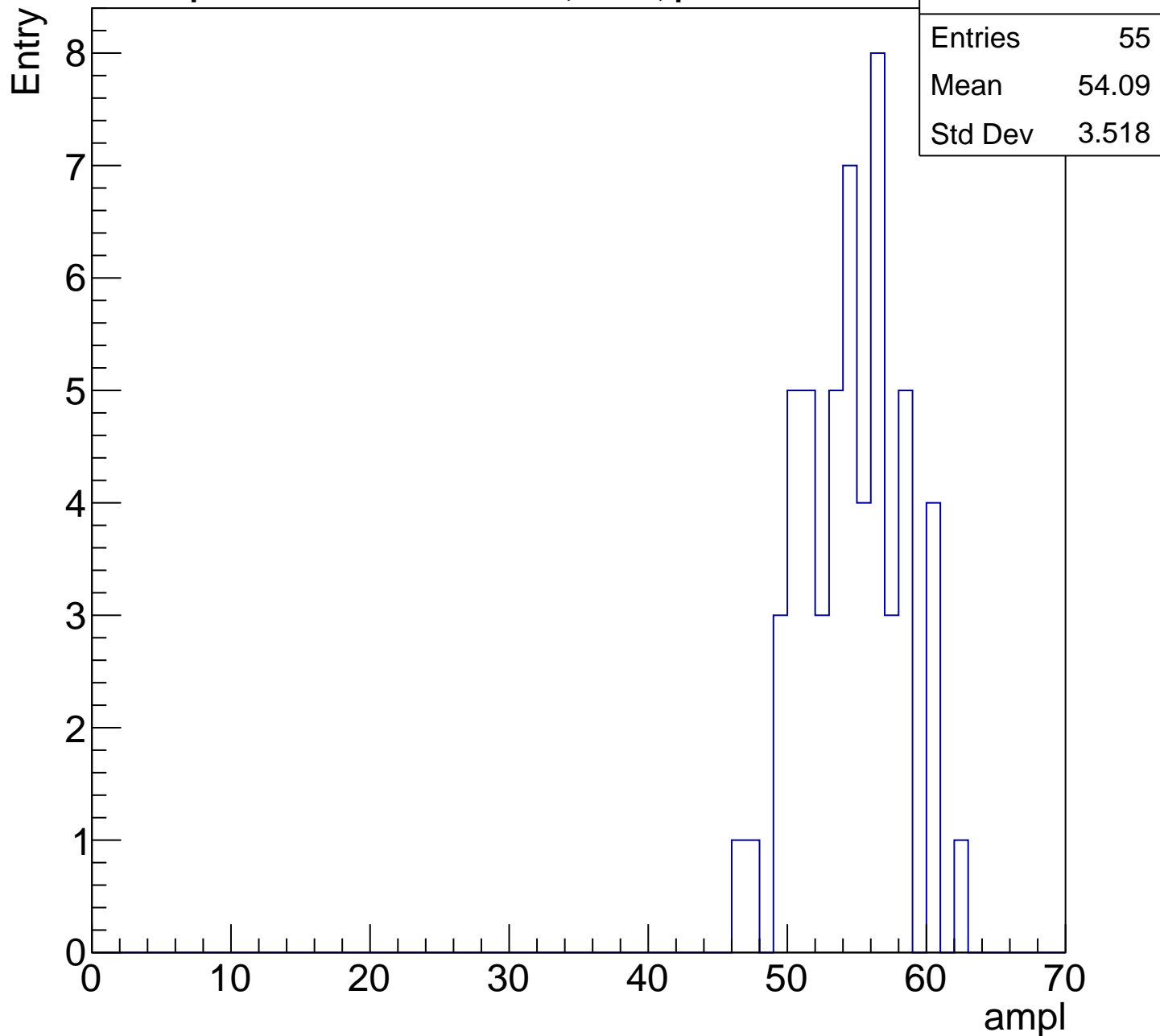
Entry

Entries	72
Mean	48.12
Std Dev	3.59



B1L103S, U5-ch108, adc4

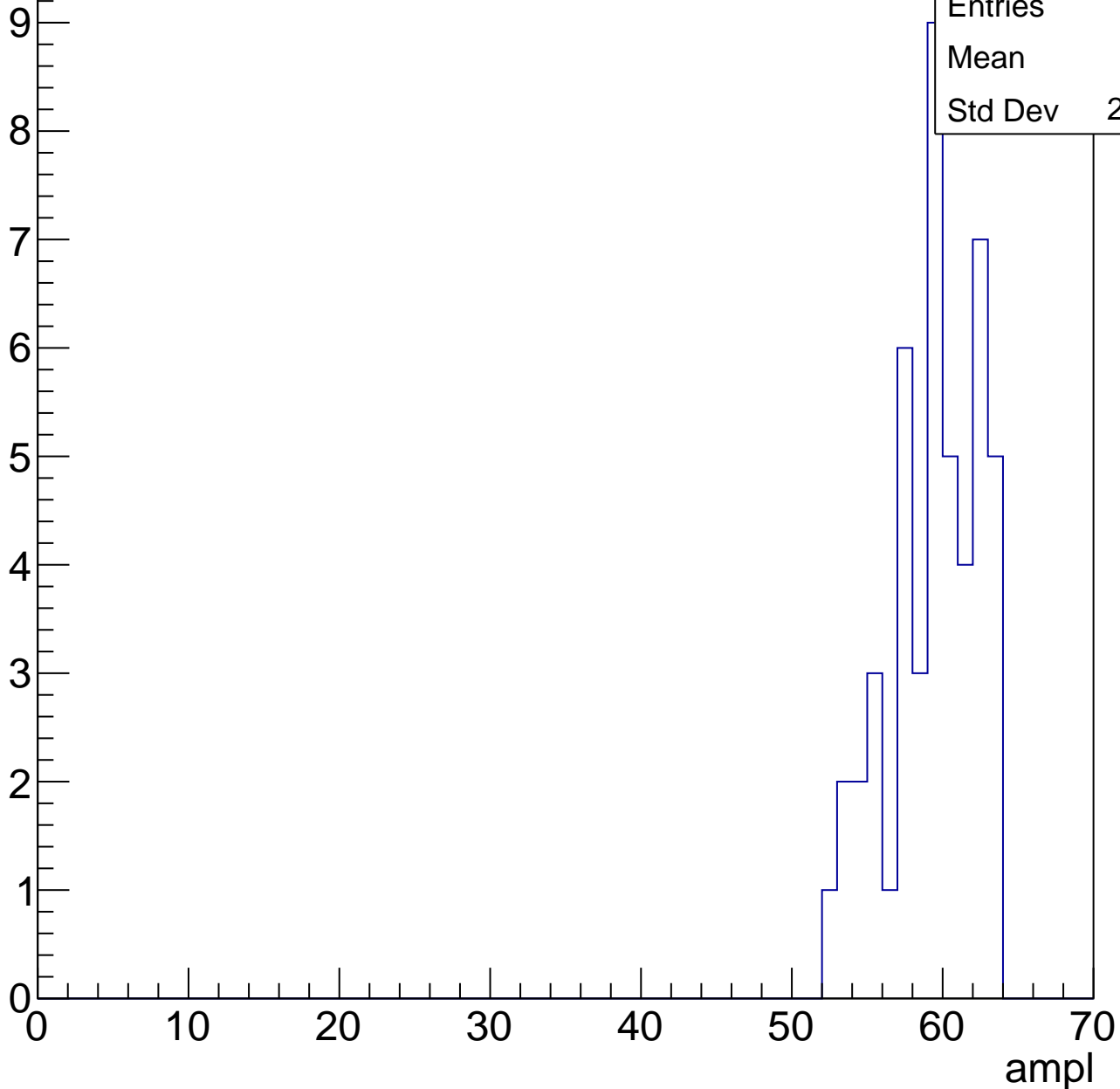
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U5-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

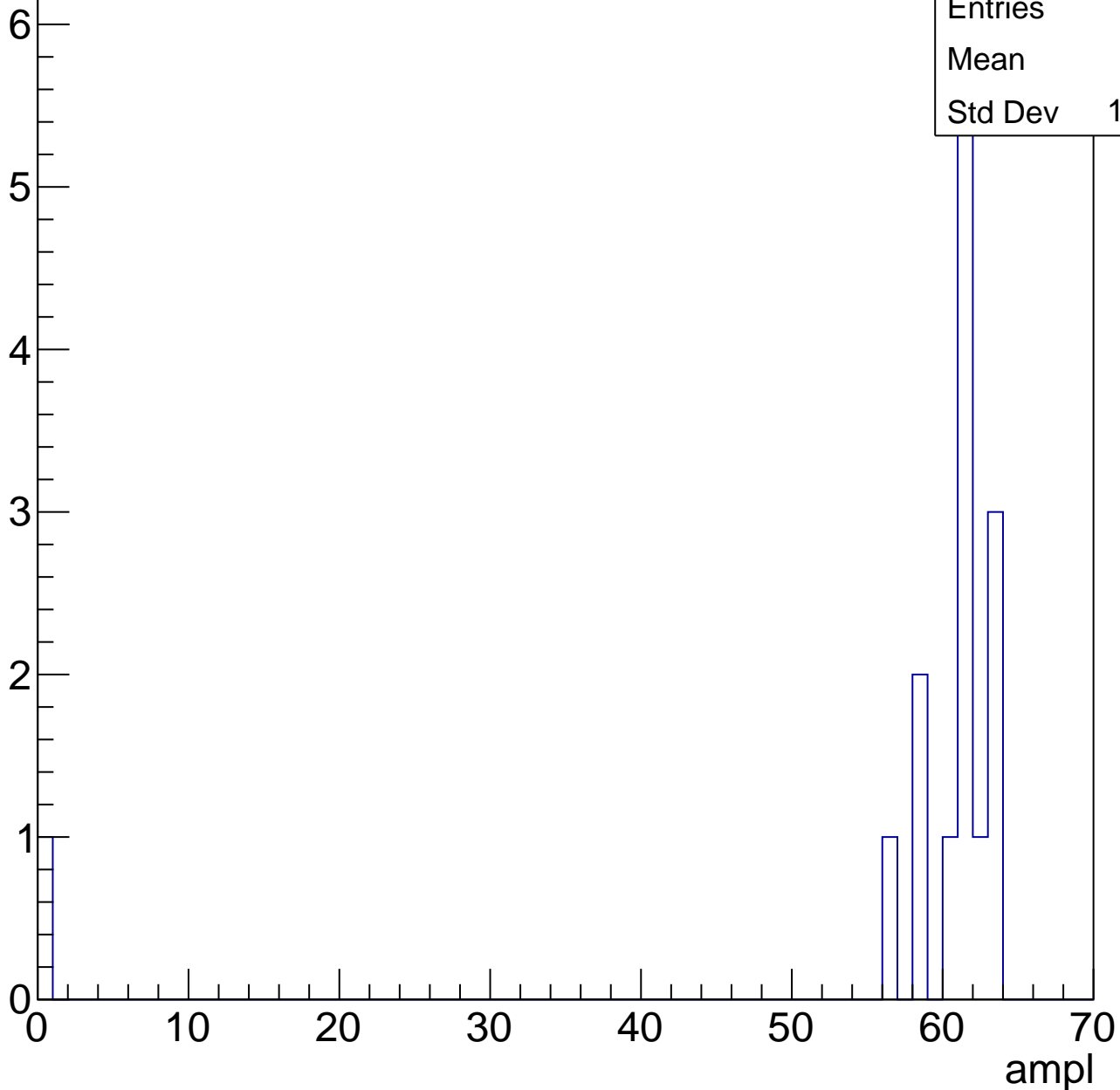


B1L103S, U5-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

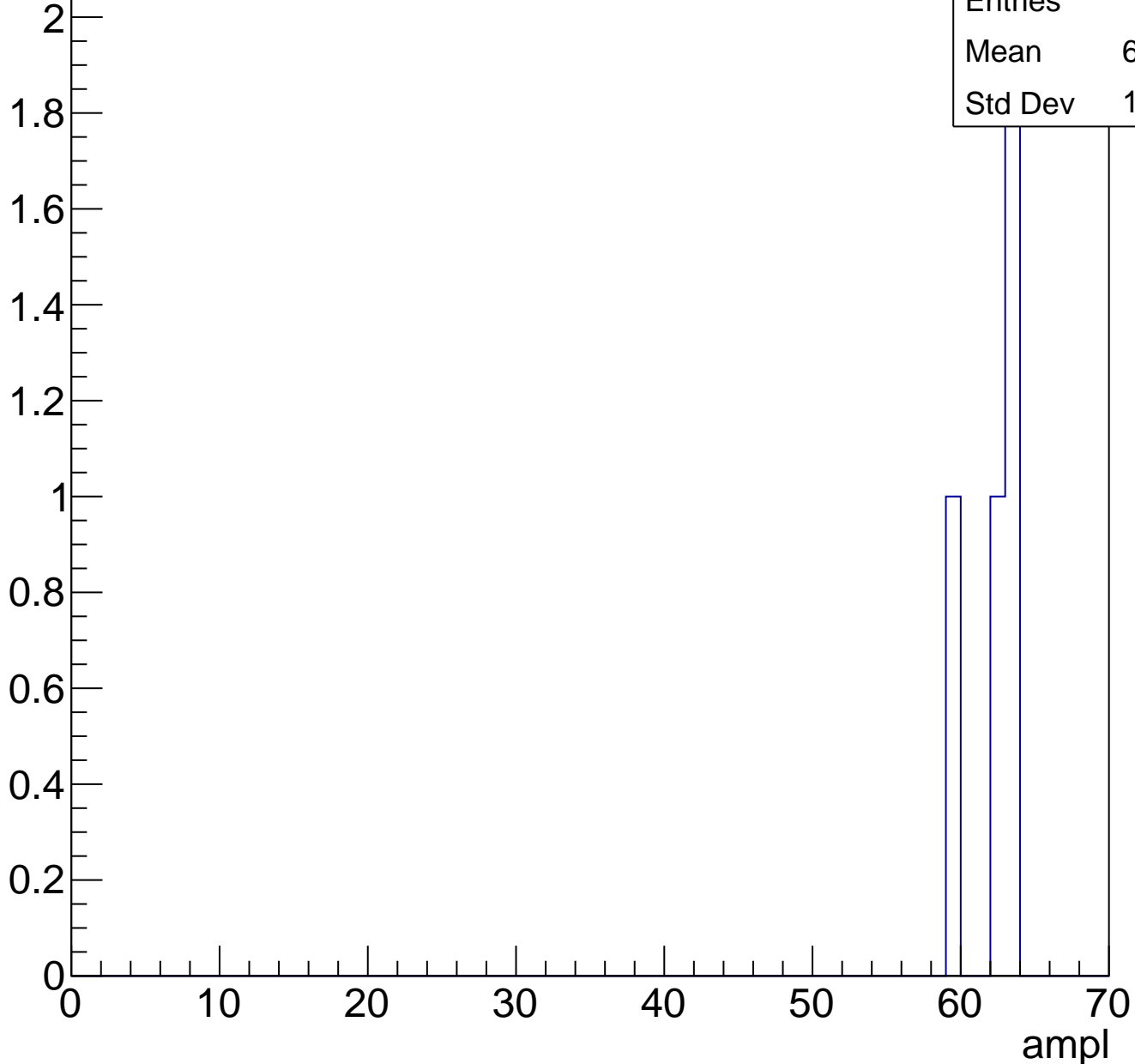
Entries	15
Mean	56.6
Std Dev	15.25



B1L103S, U5-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch109, adc0

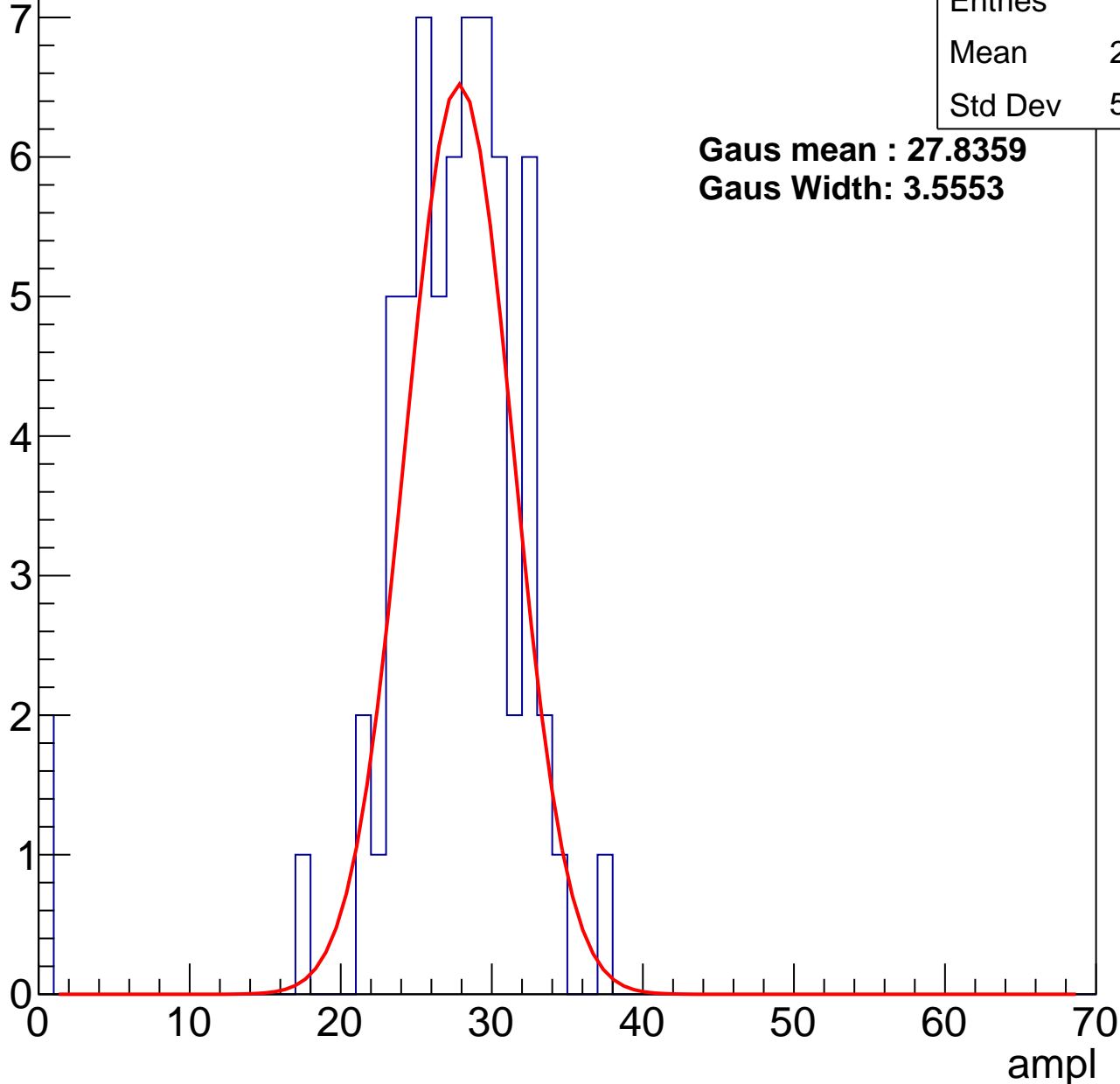
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	26.56
Std Dev	5.893

Gaus mean : 27.8359

Gaus Width: 3.5553



B1L103S, U5-ch109, adc1

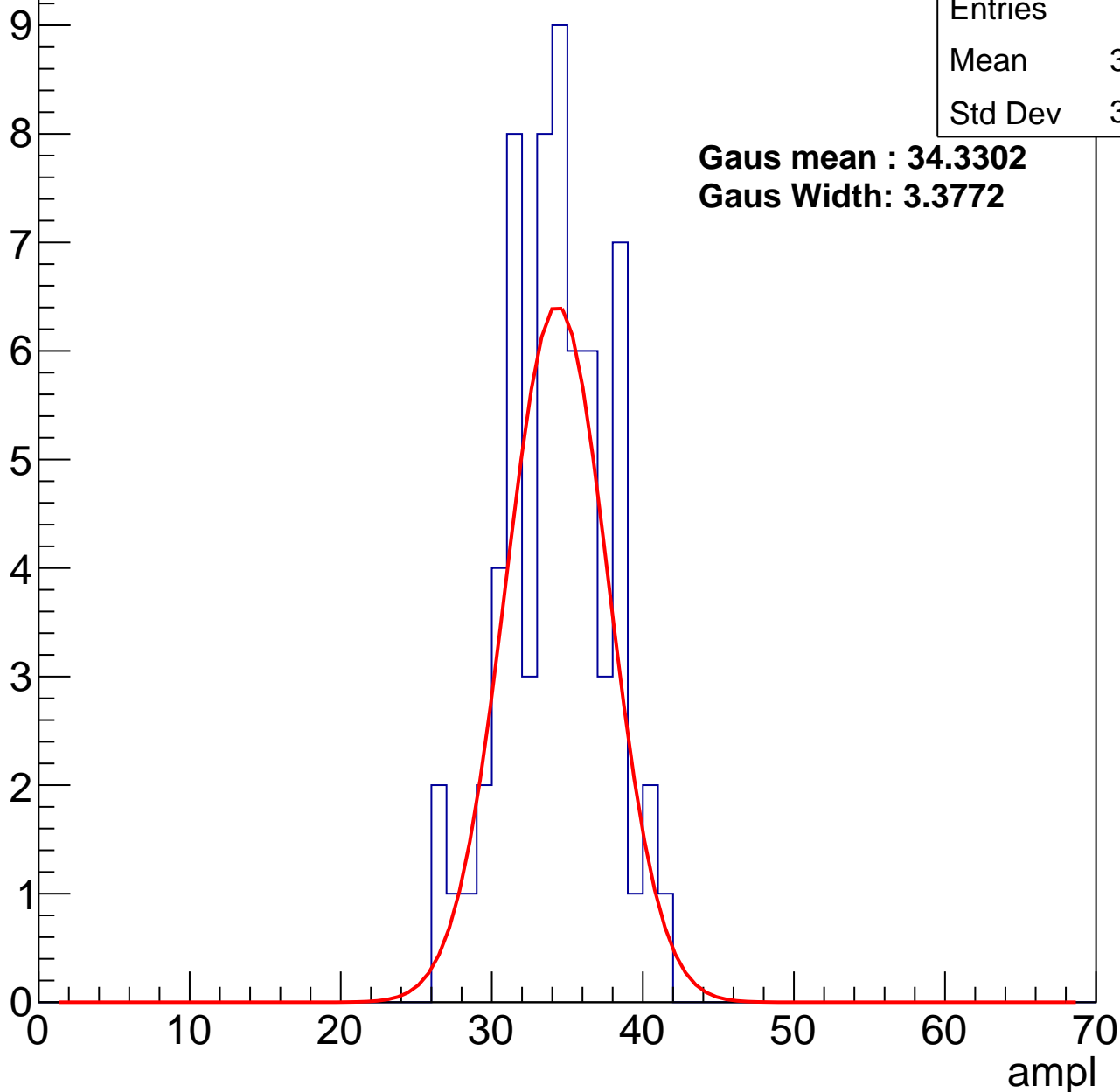
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	33.78
Std Dev	3.379

Gaus mean : 34.3302

Gaus Width: 3.3772



B1L103S, U5-ch109, adc2

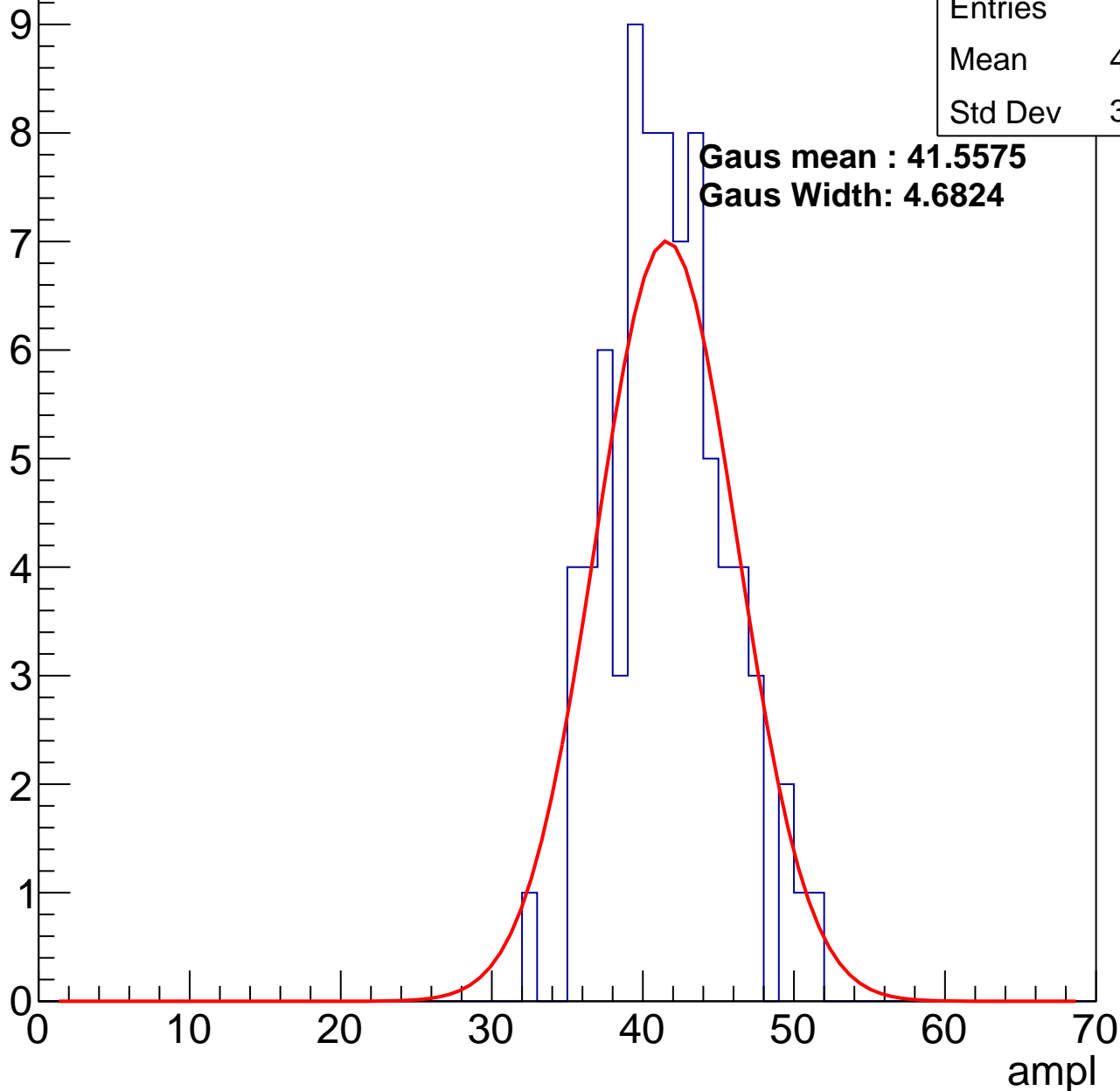
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	41.19
Std Dev	3.847

Gaus mean : 41.5575

Gaus Width: 4.6824

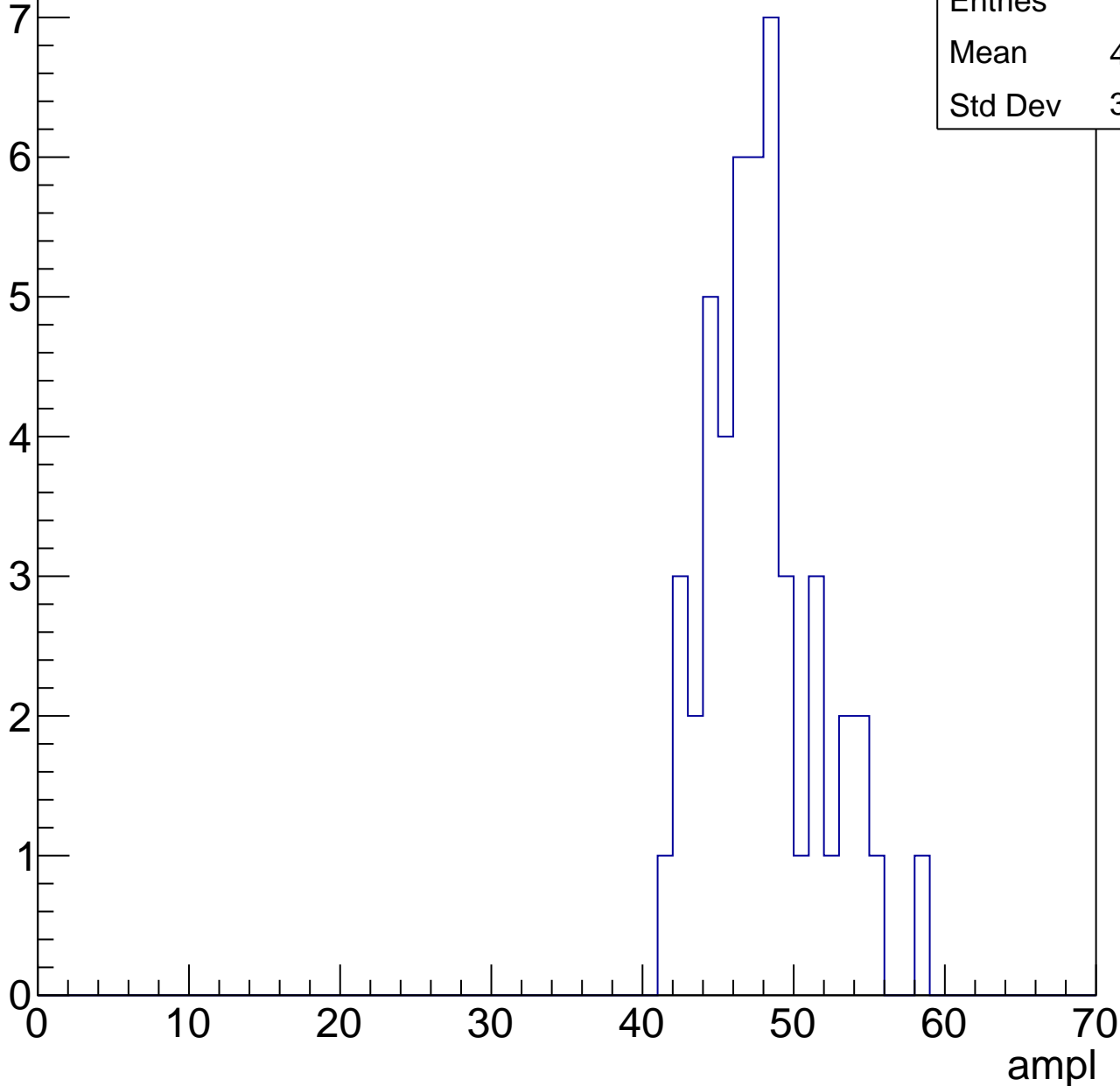


B1L103S, U5-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

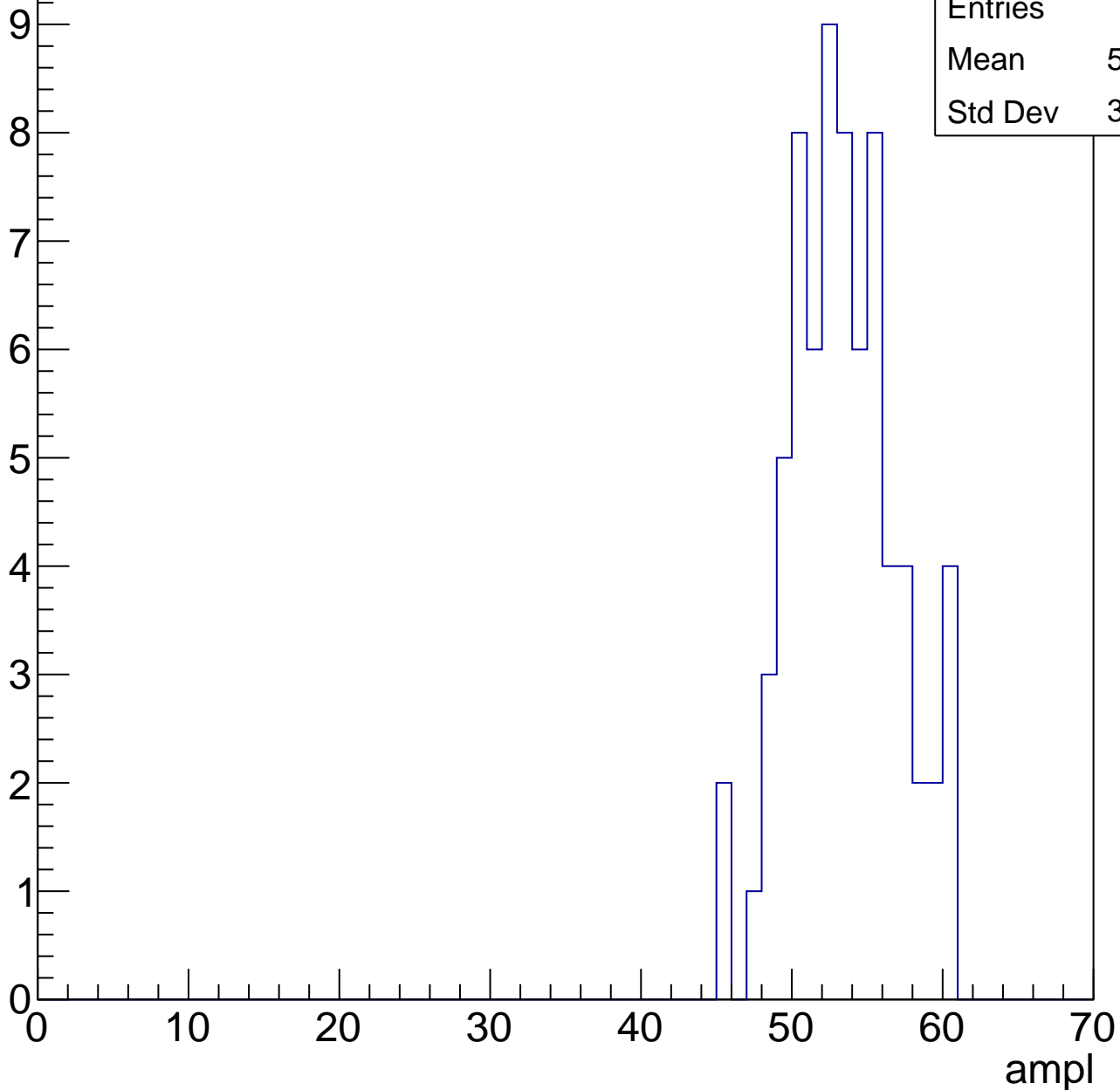
Entries	48
Mean	47.42
Std Dev	3.702



B1L103S, U5-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

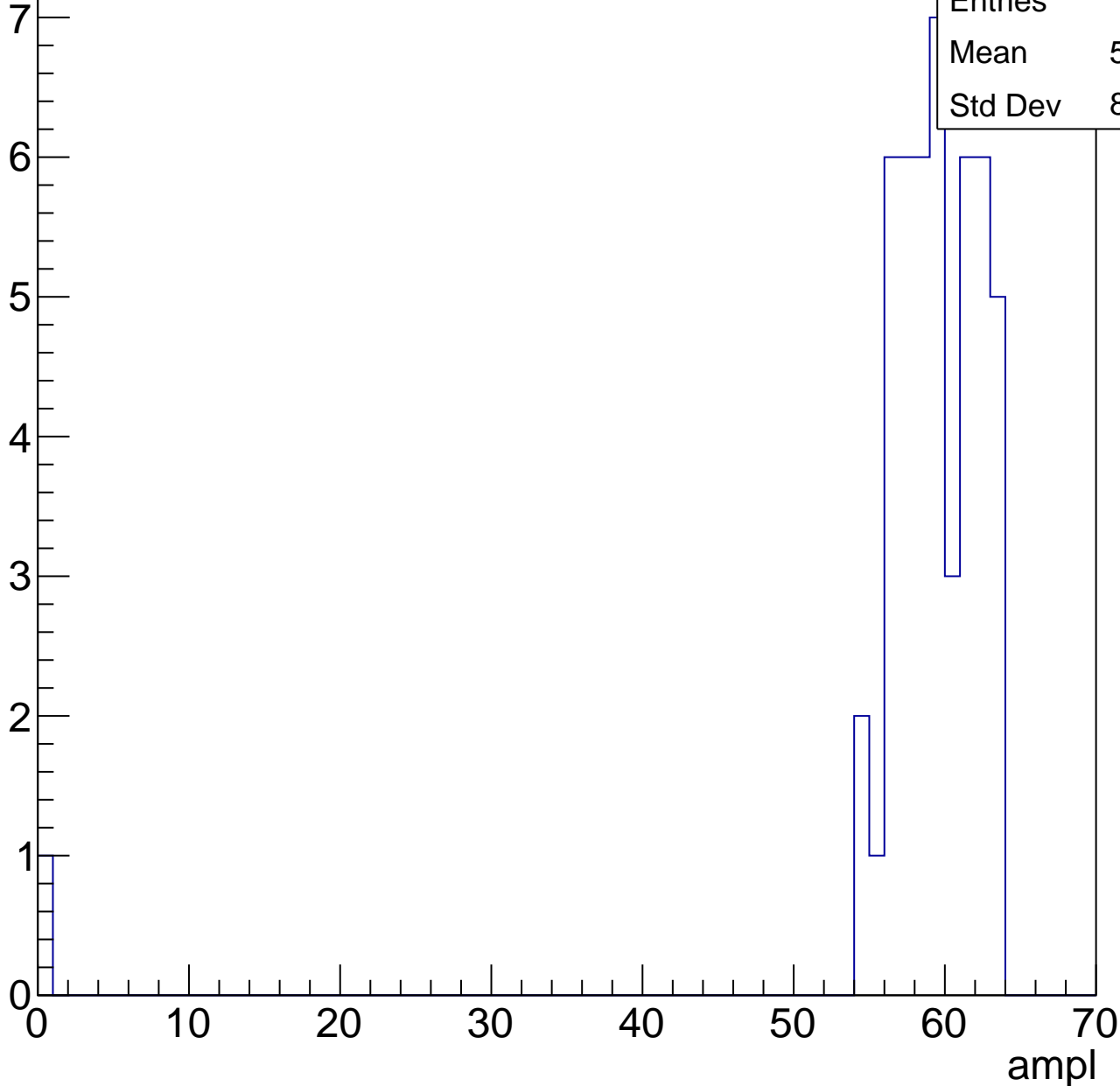


B1L103S, U5-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	57.86
Std Dev	8.722

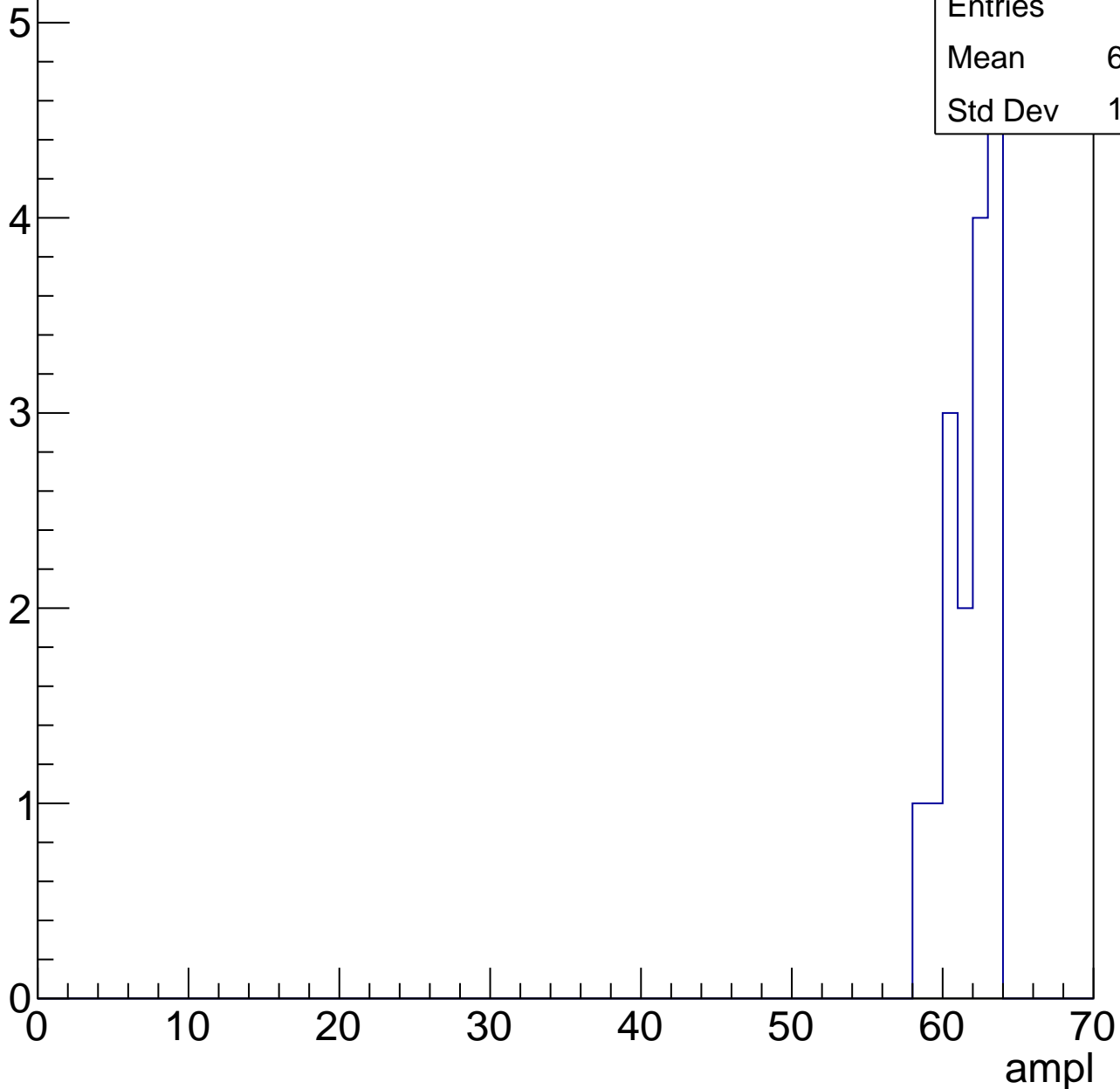


B1L103S, U5-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

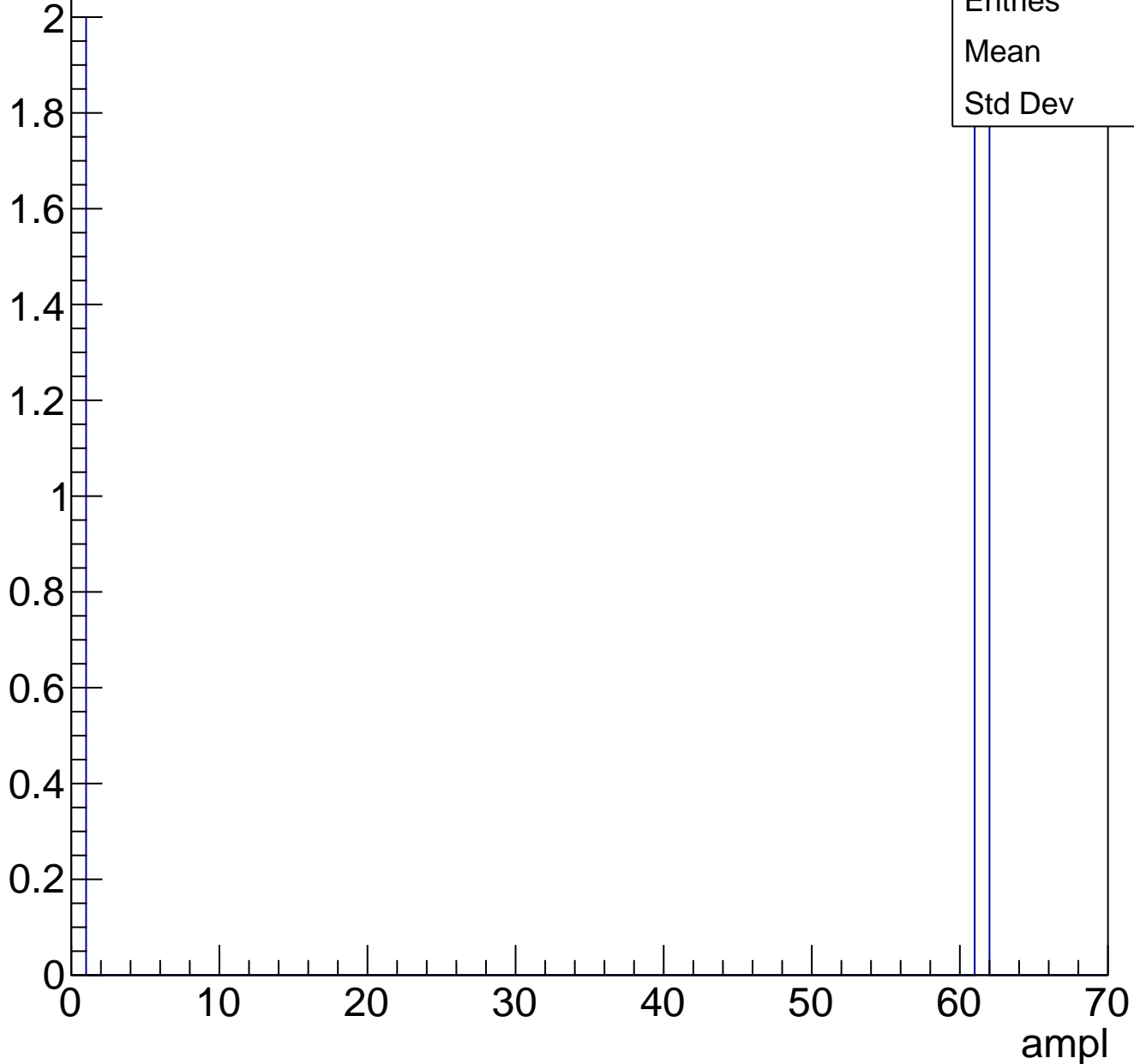
Entries	16
Mean	61.38
Std Dev	1.536



B1L103S, U5-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch110, adc0

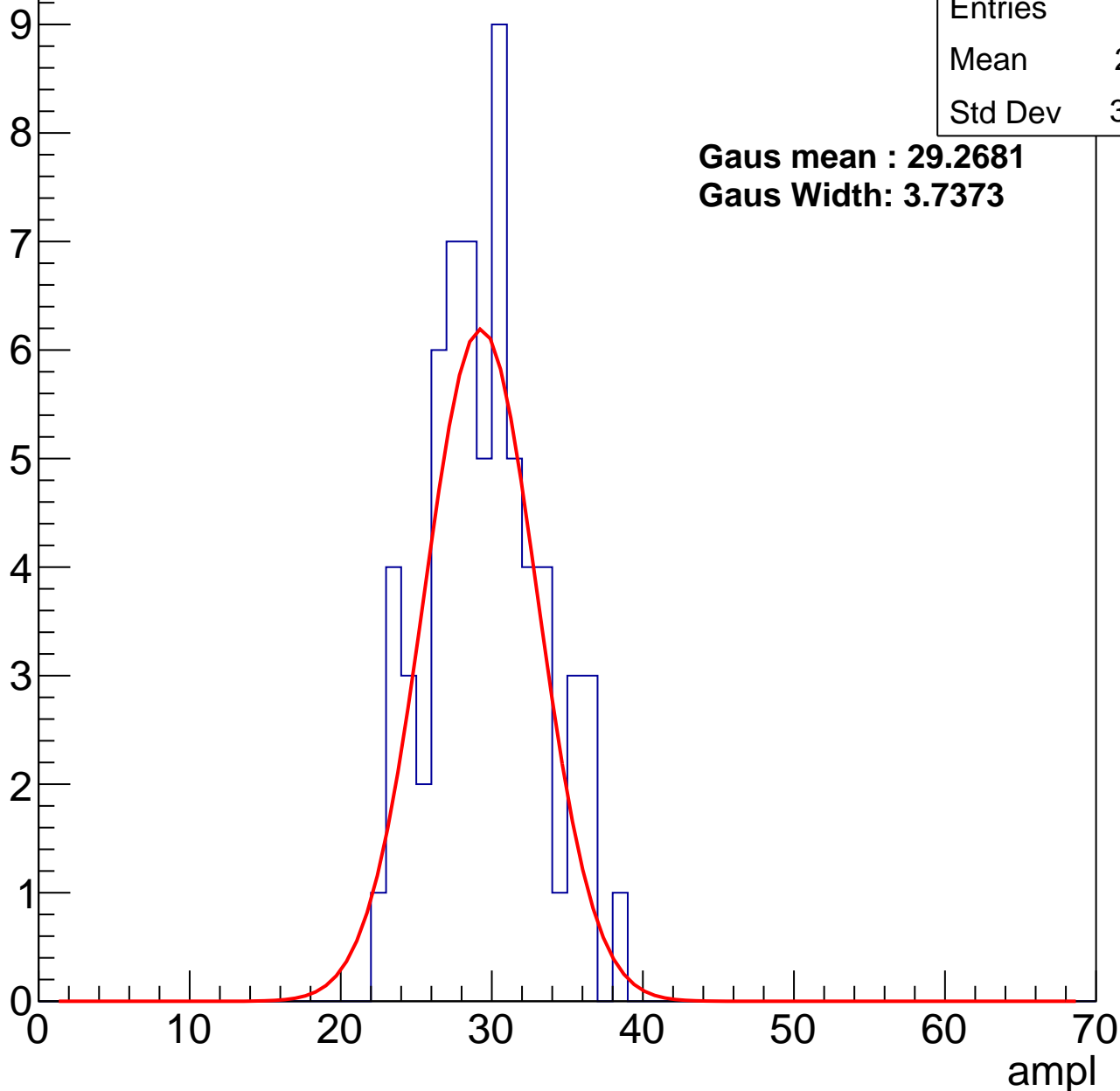
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	29.11
Std Dev	3.676

Gaus mean : 29.2681

Gaus Width: 3.7373



B1L103S, U5-ch110, adc1

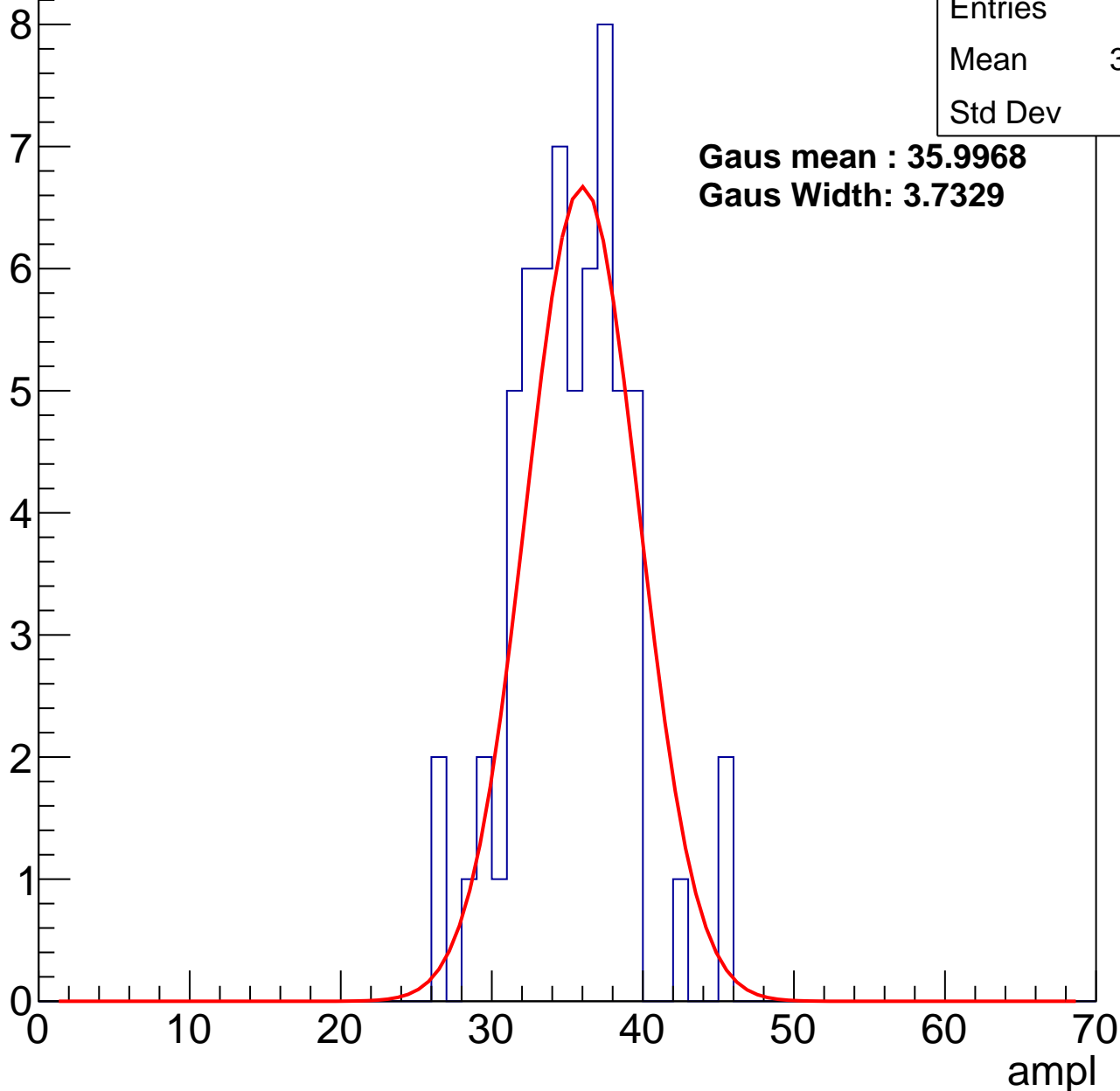
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	34.76
Std Dev	3.77

Gaus mean : 35.9968

Gaus Width: 3.7329



B1L103S, U5-ch110, adc2

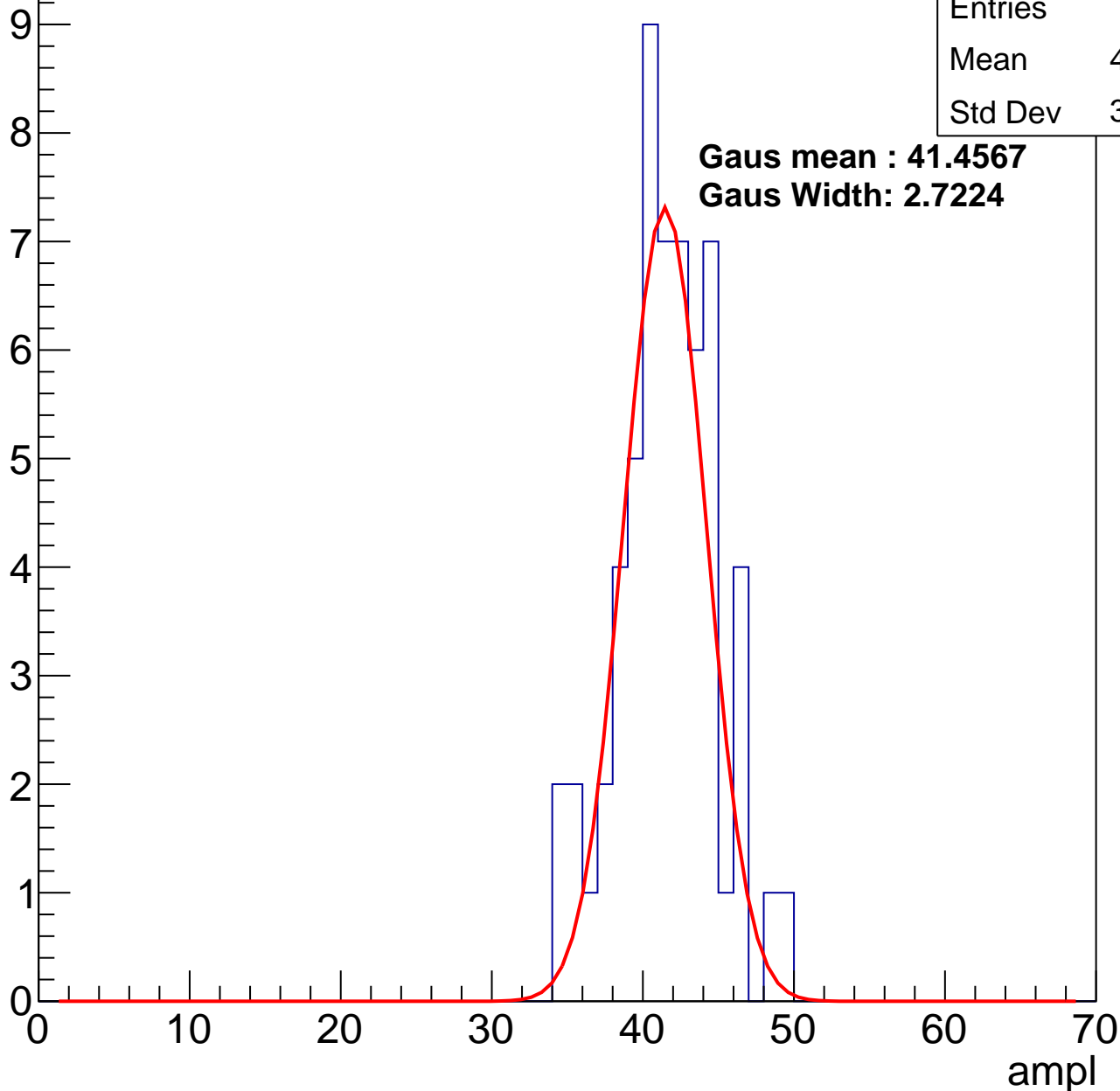
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	41.15
Std Dev	3.225

Gaus mean : 41.4567

Gaus Width: 2.7224

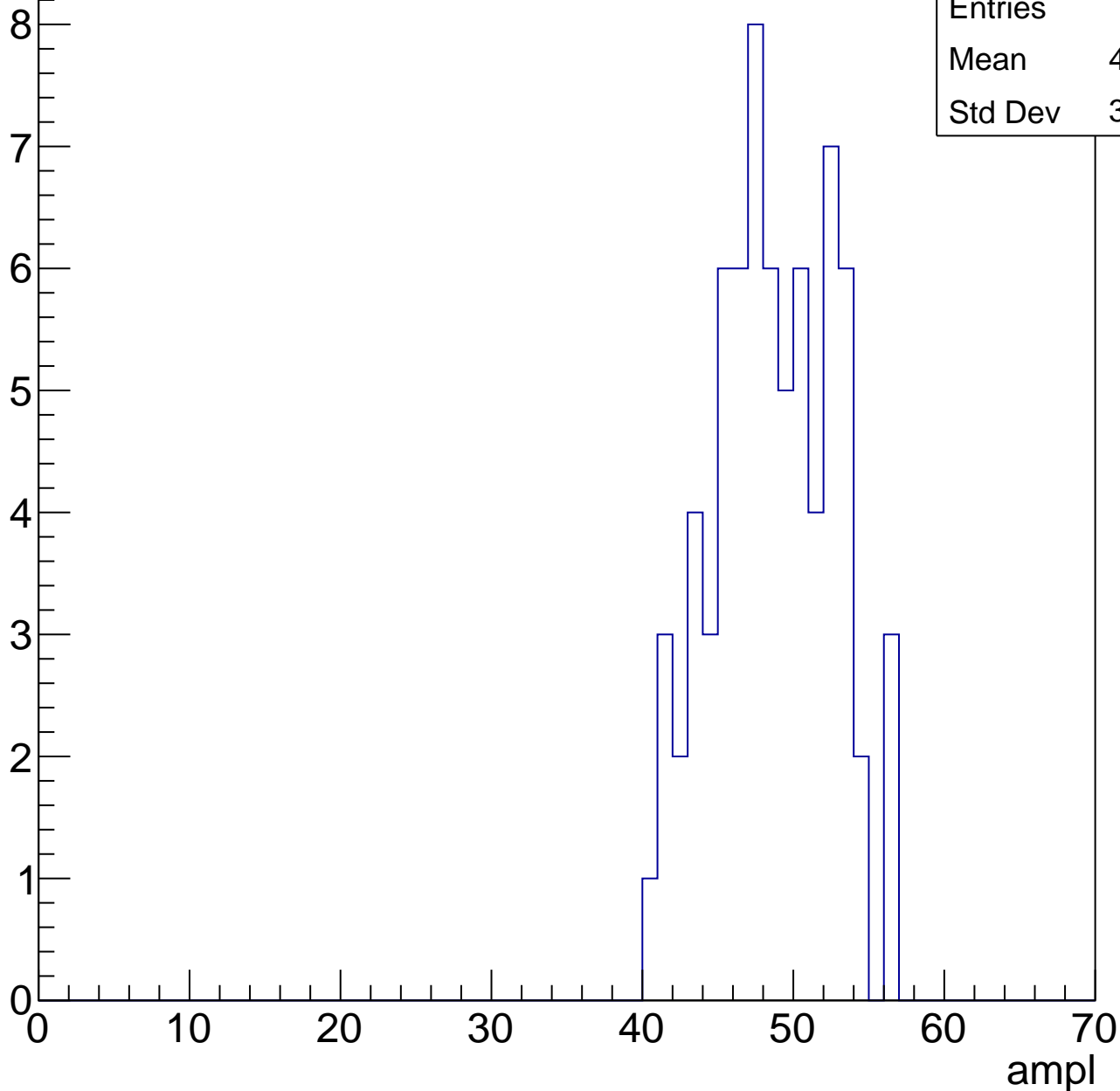


B1L103S, U5-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	48.17
Std Dev	3.919

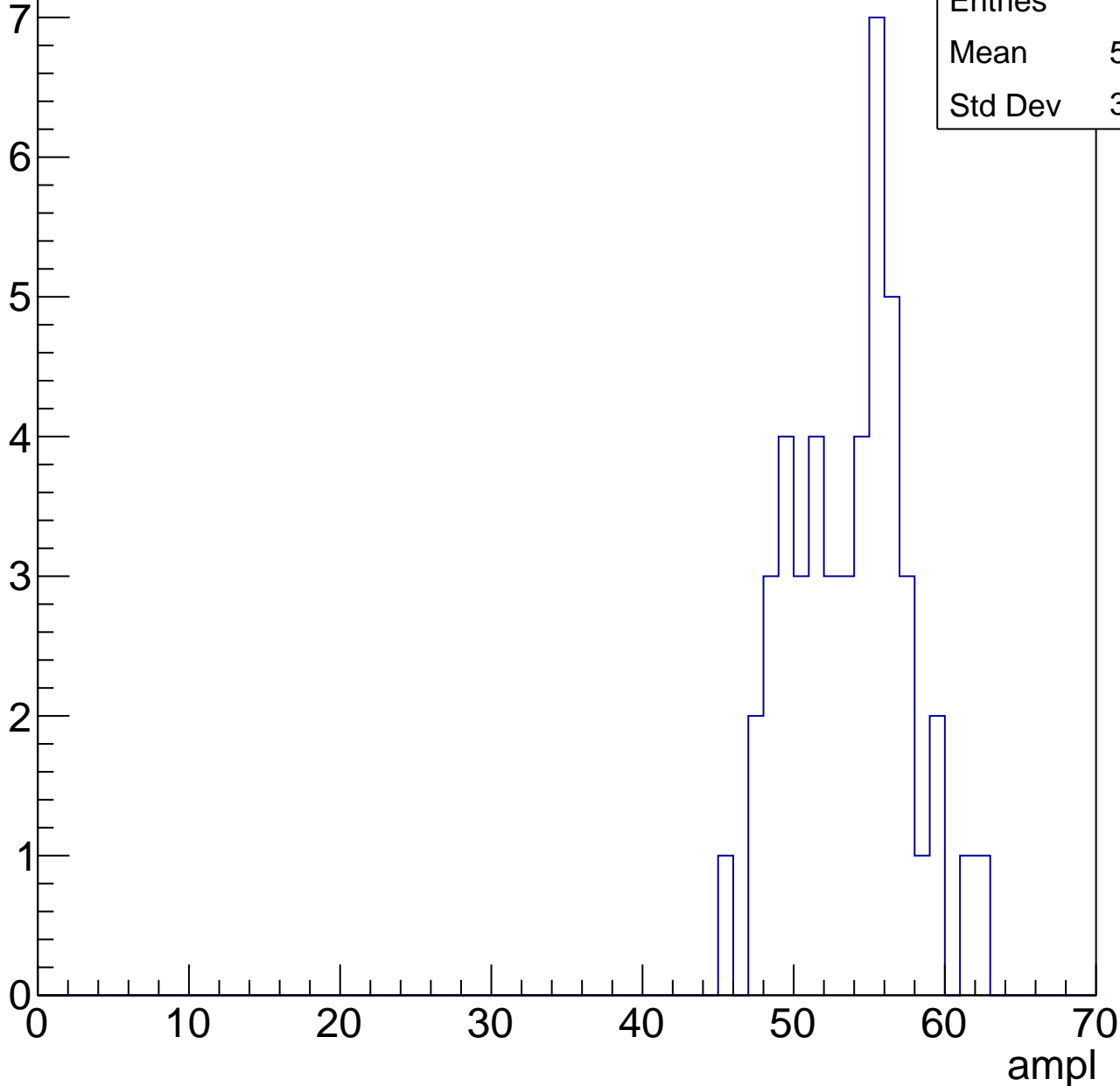


B1L103S, U5-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	53.17
Std Dev	3.822

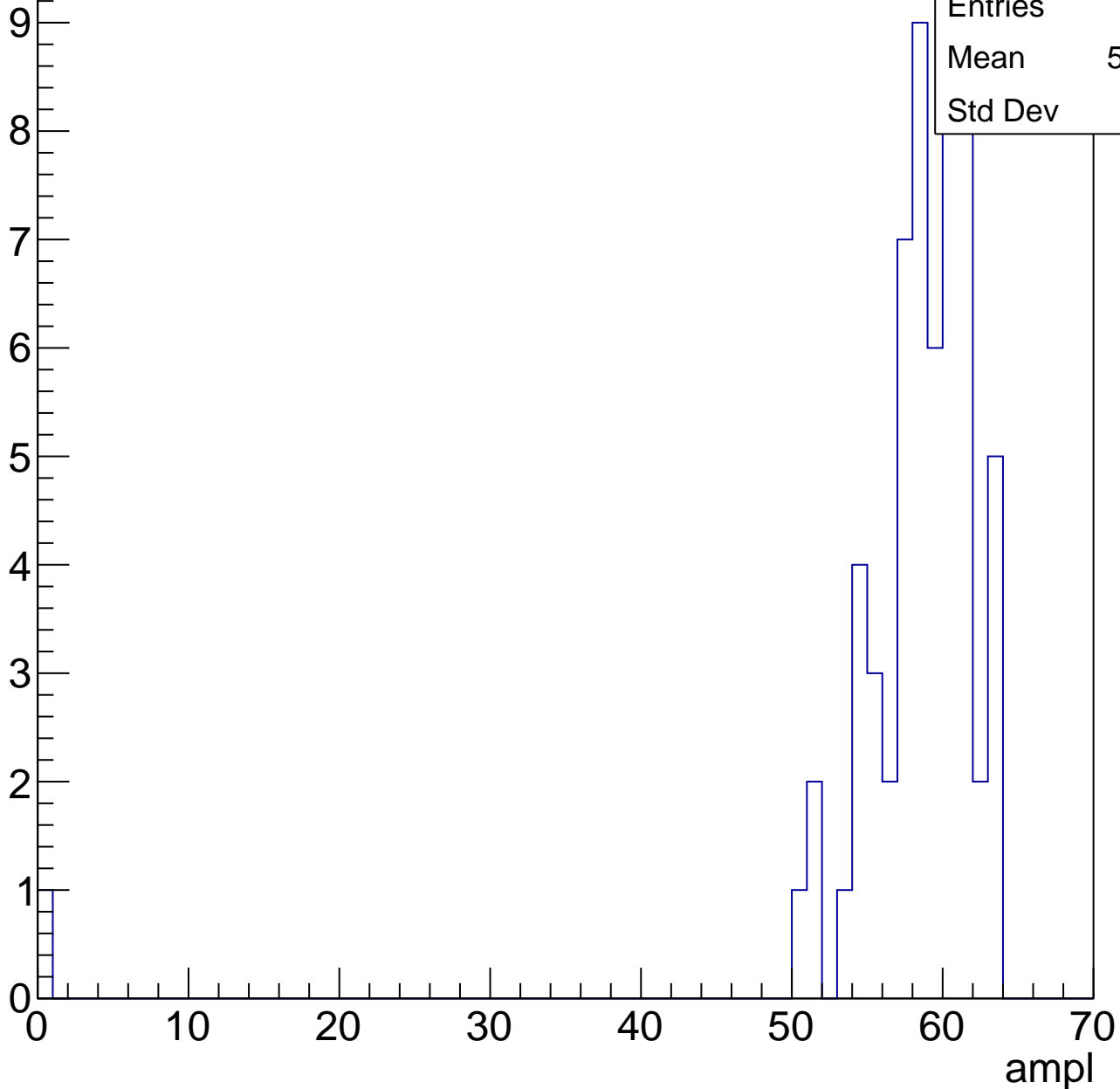


B1L103S, U5-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	57.35
Std Dev	8.07

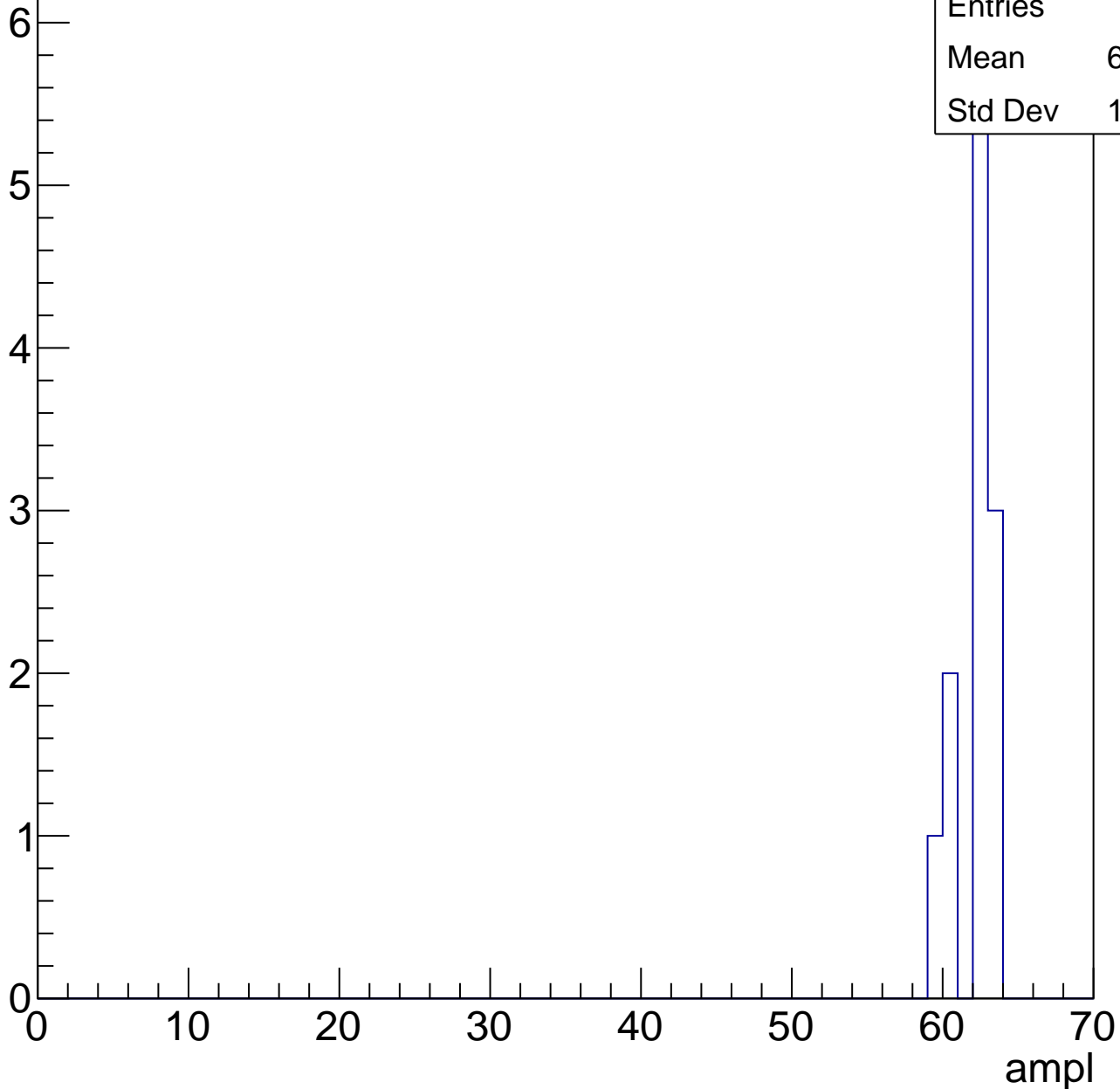


B1L103S, U5-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

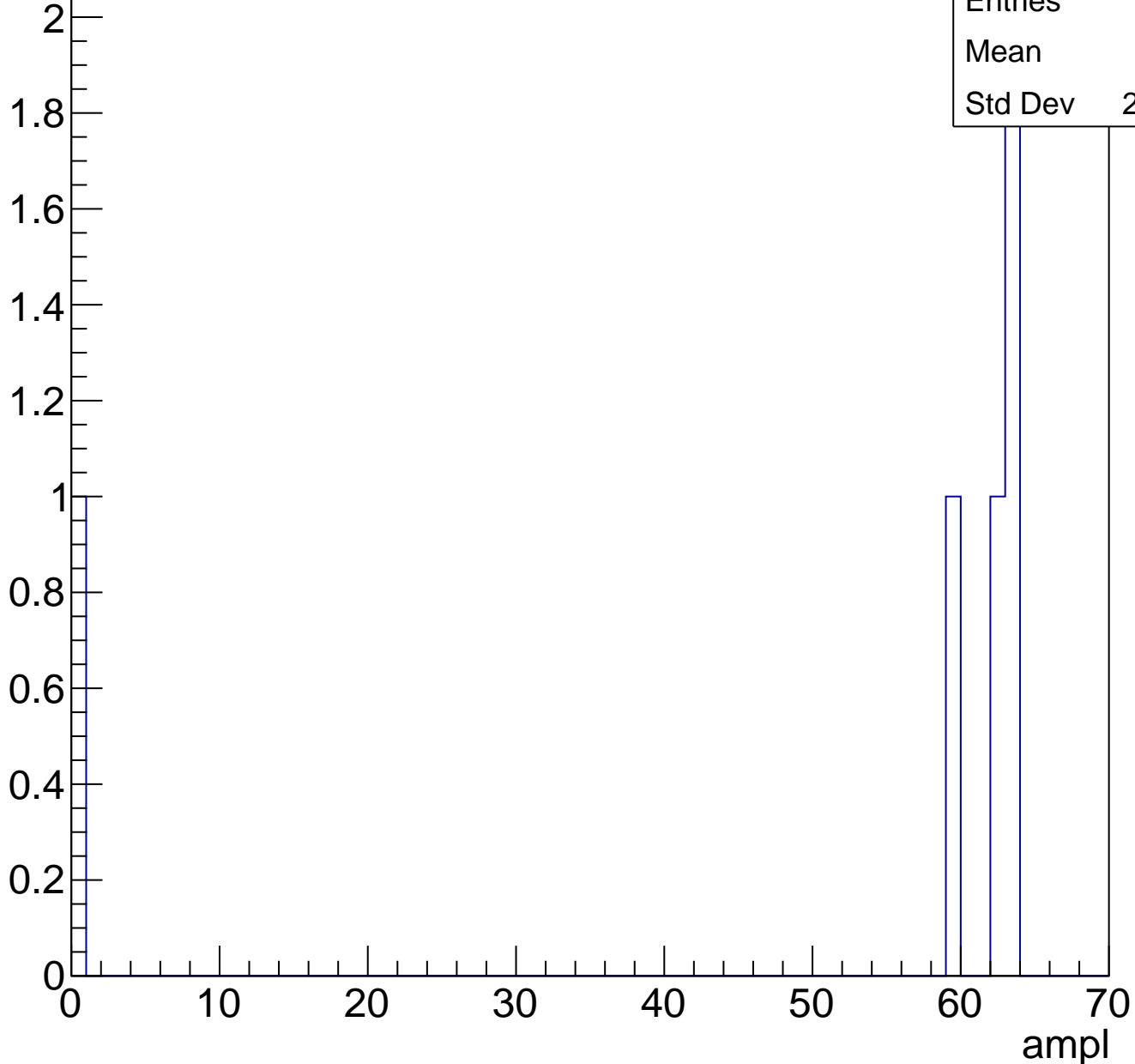
Entries	12
Mean	61.67
Std Dev	1.247



B1L103S, U5-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	49.4
Std Dev	24.74

B1L103S, U5-ch111, adc0

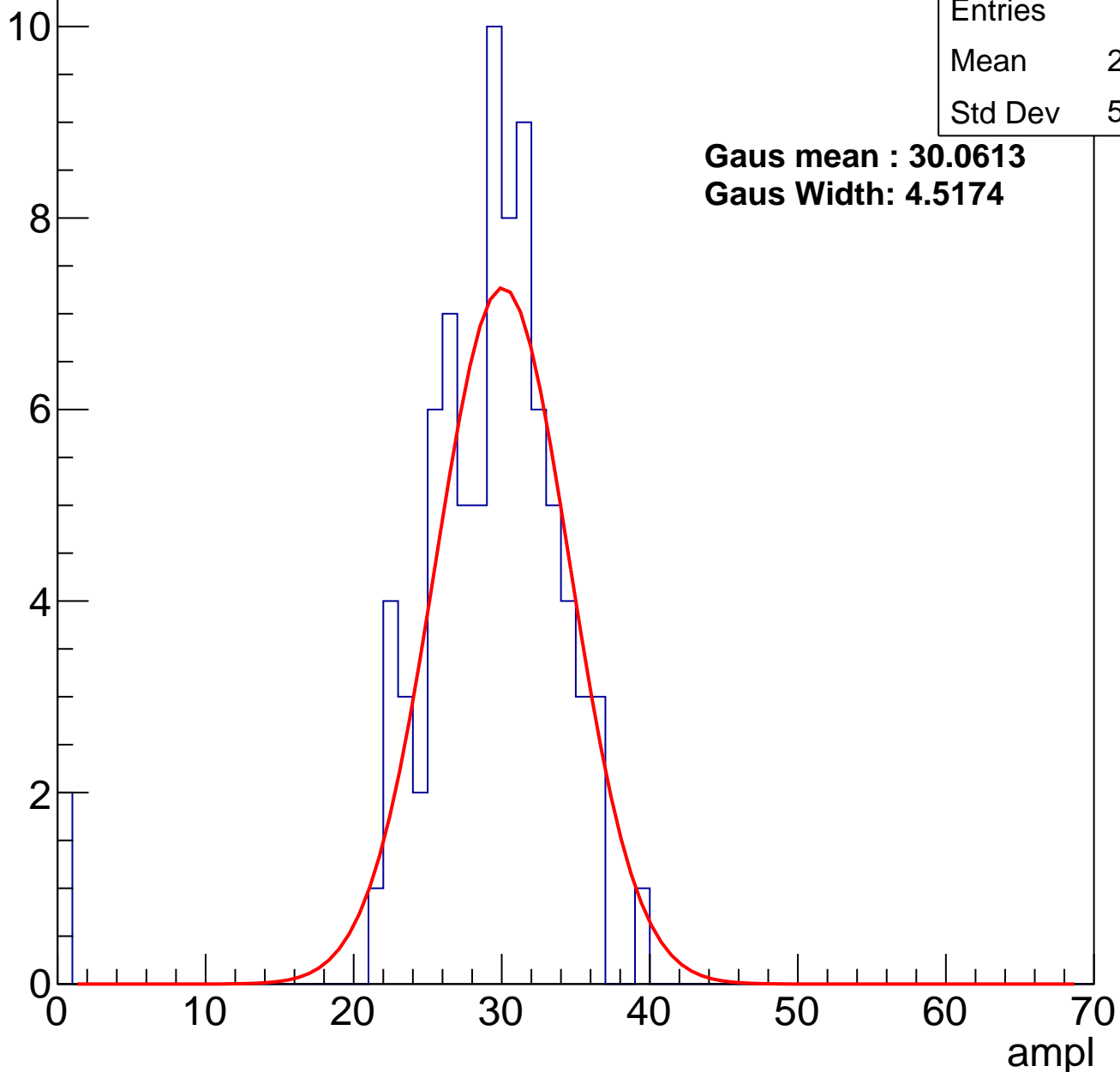
calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	28.42
Std Dev	5.864

Gaus mean : 30.0613

Gaus Width: 4.5174

Entry



B1L103S, U5-ch111, adc1

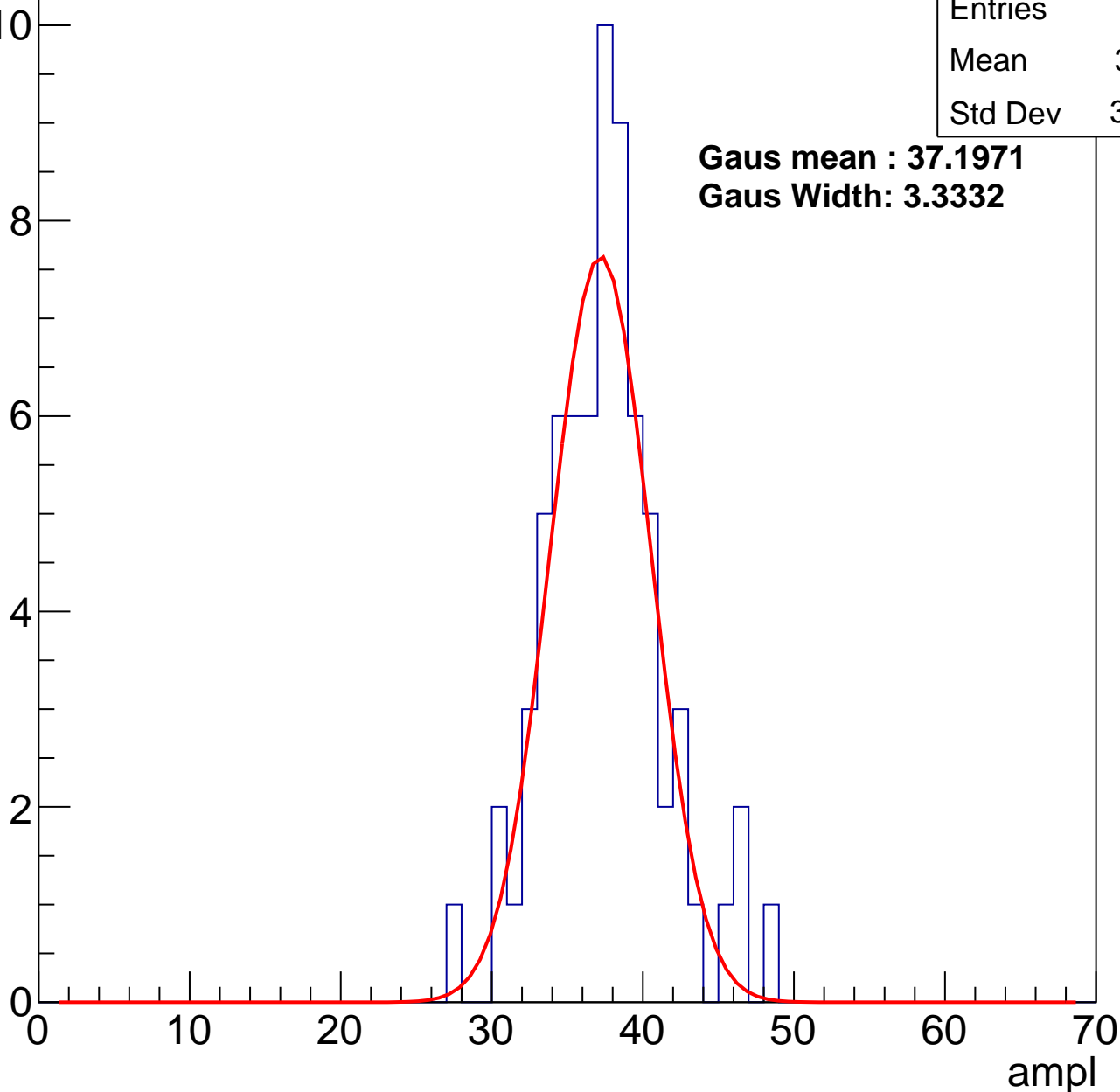
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	37.01
Std Dev	3.856

Gaus mean : 37.1971

Gaus Width: 3.3332



B1L103S, U5-ch111, adc2

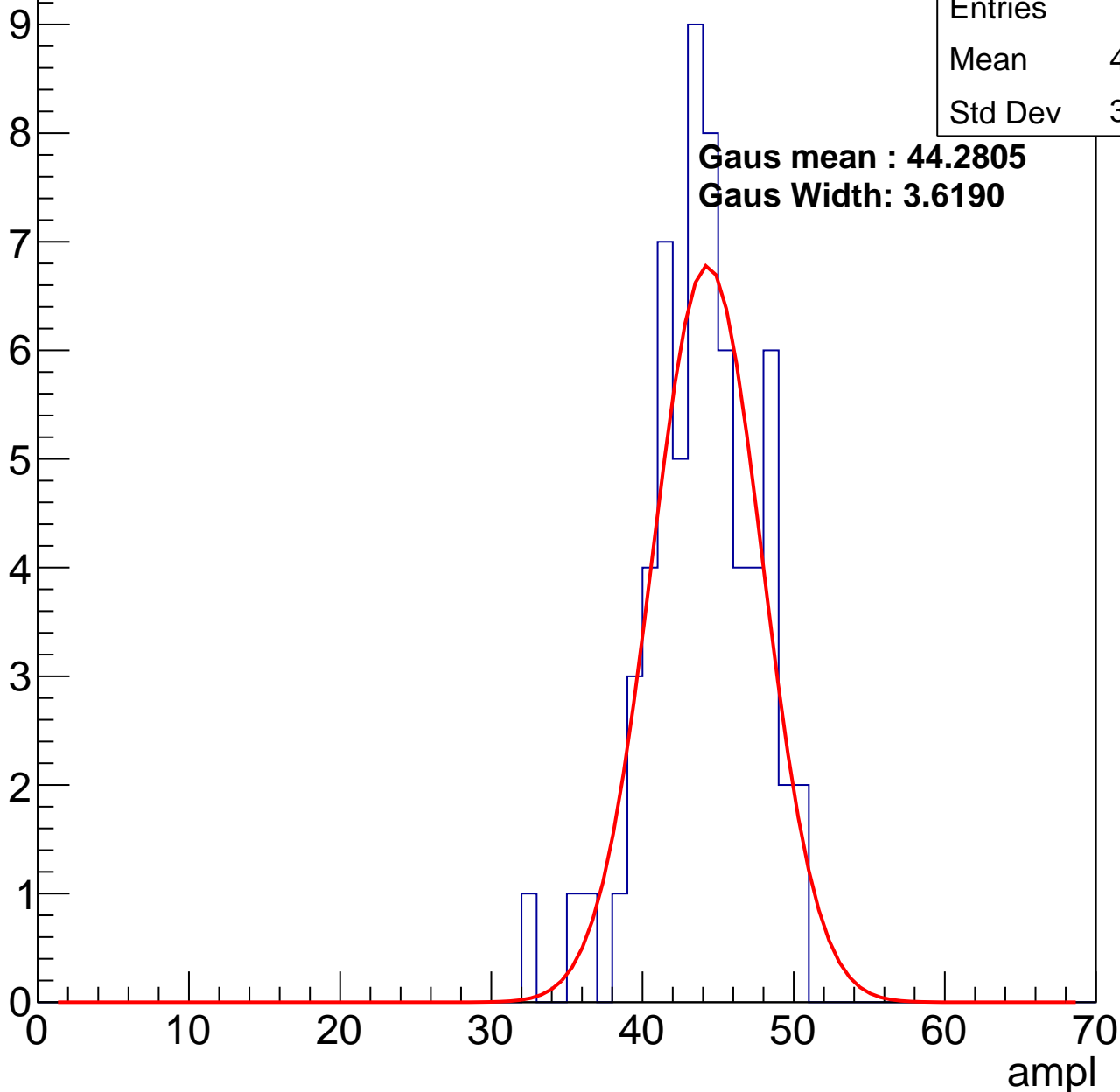
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.47
Std Dev	3.566

Gaus mean : 44.2805

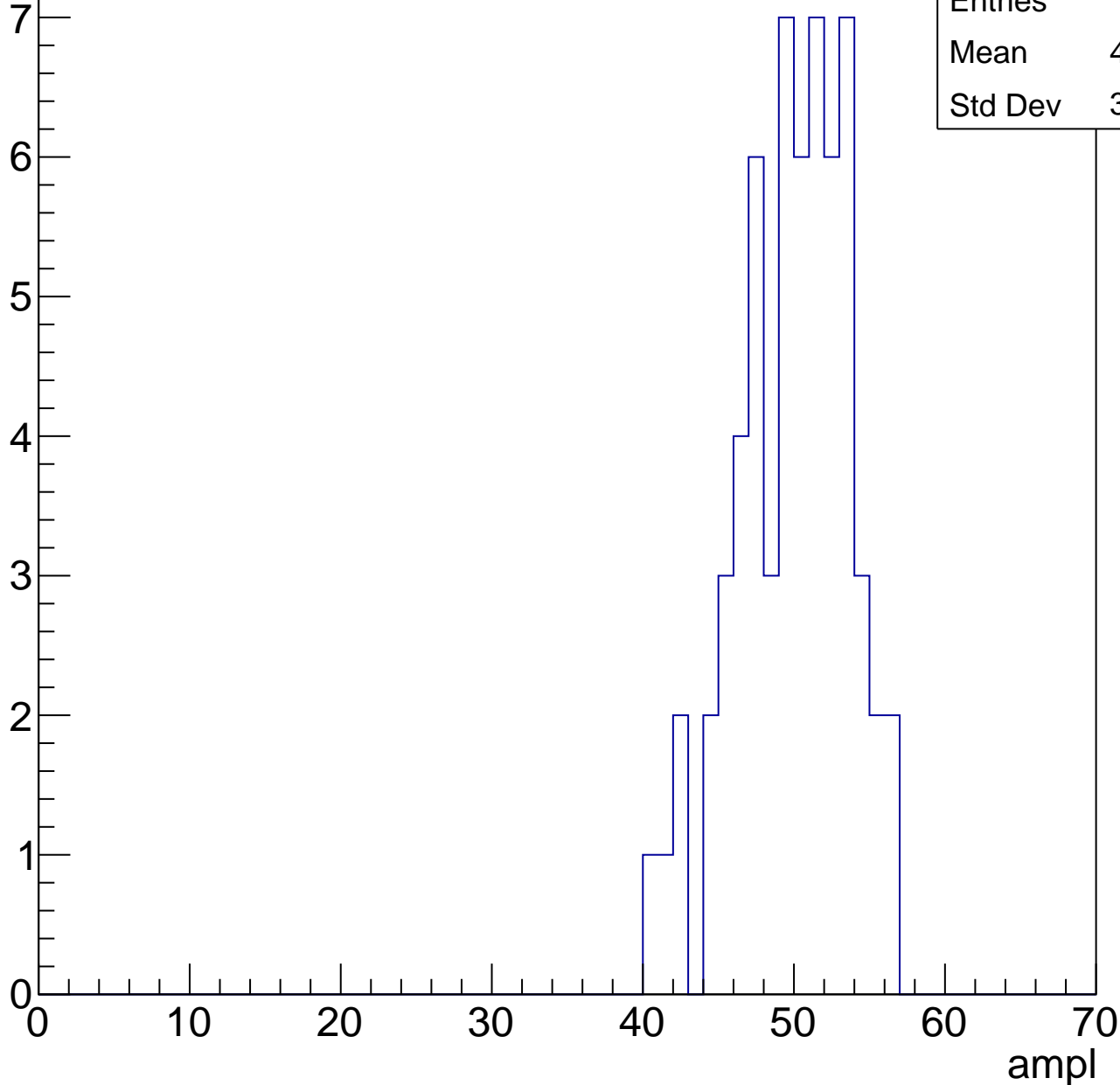
Gaus Width: 3.6190



B1L103S, U5-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



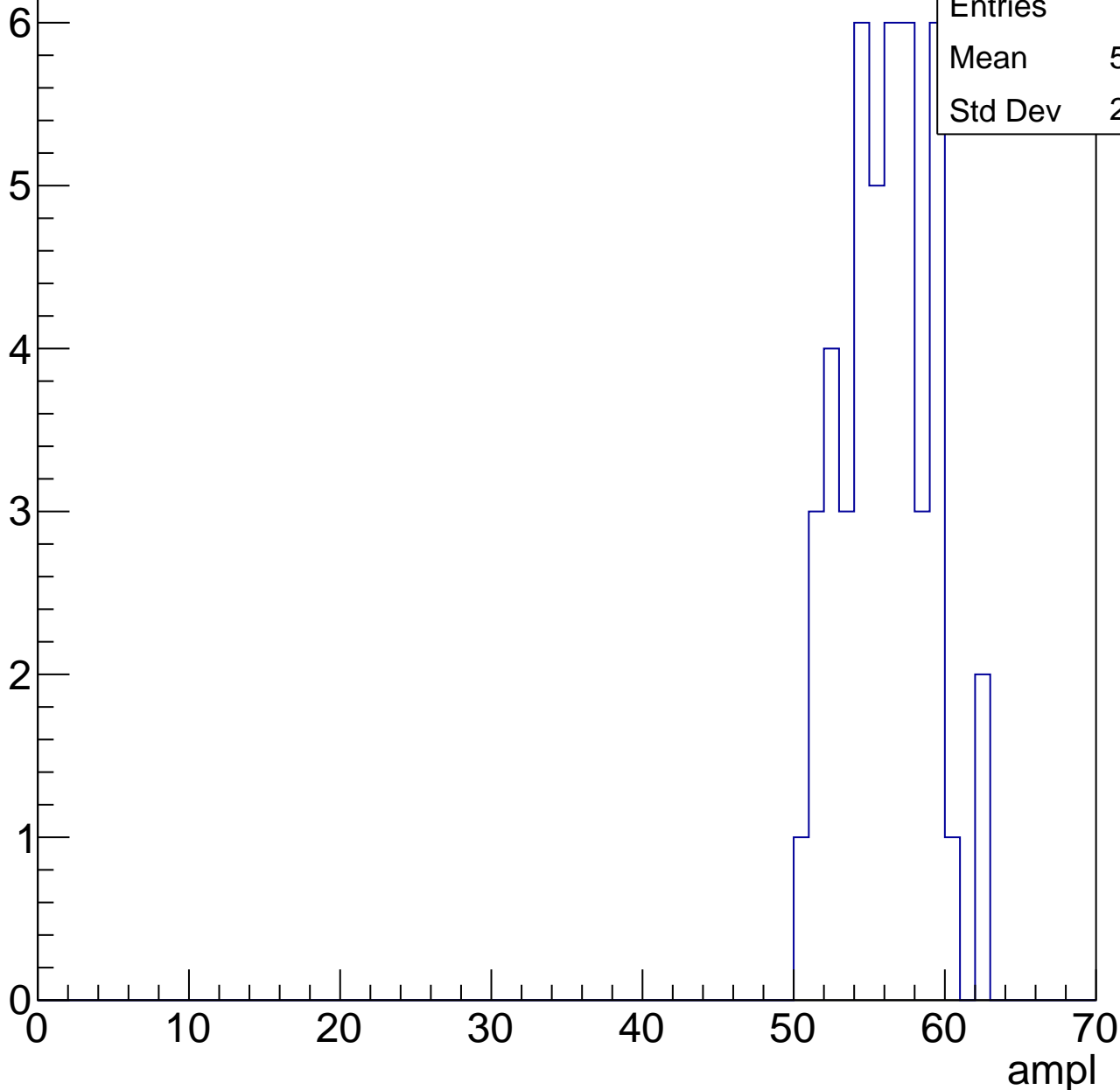
Entries	62
Mean	49.44
Std Dev	3.675

B1L103S, U5-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	55.63
Std Dev	2.892

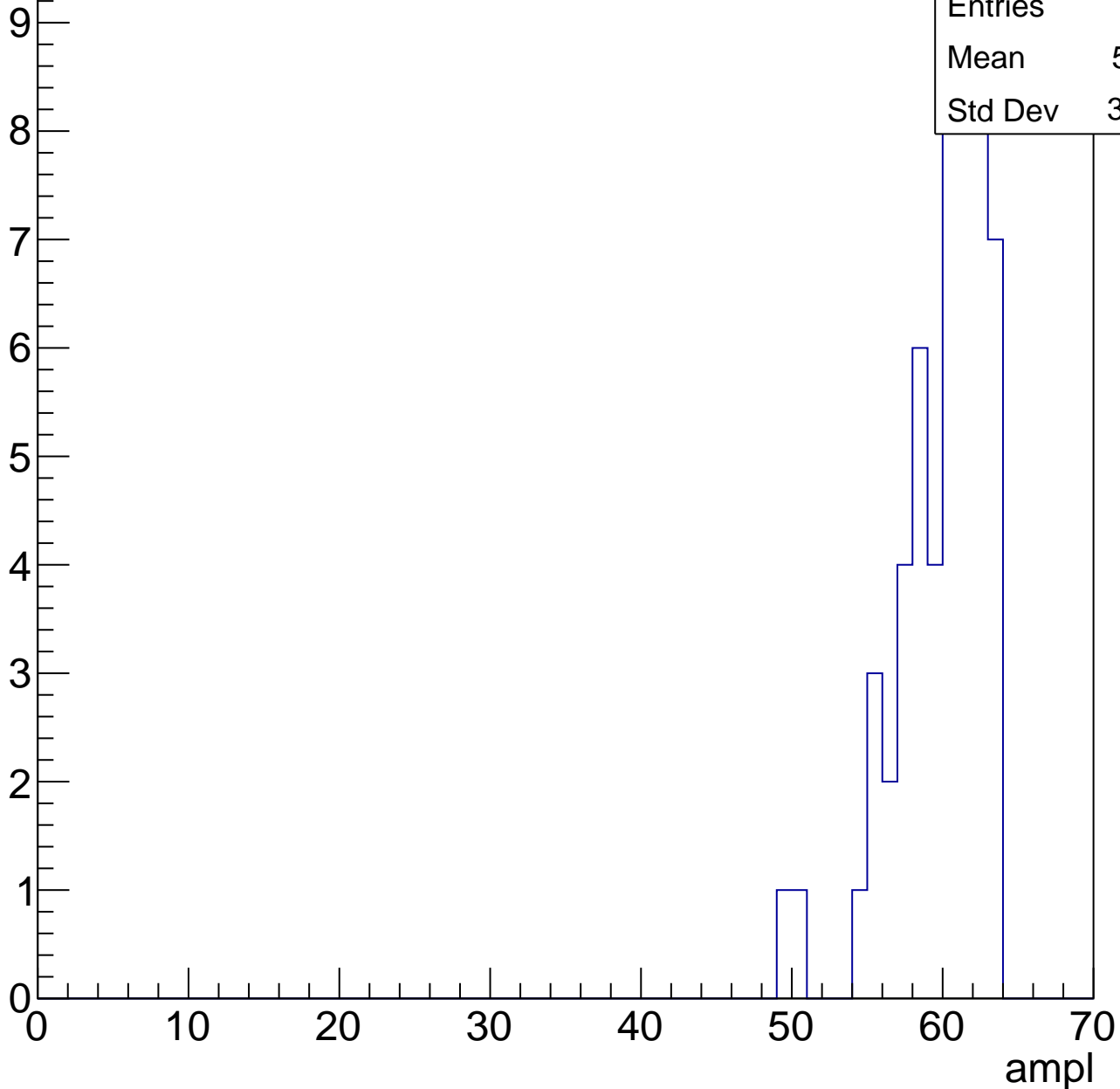


B1L103S, U5-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

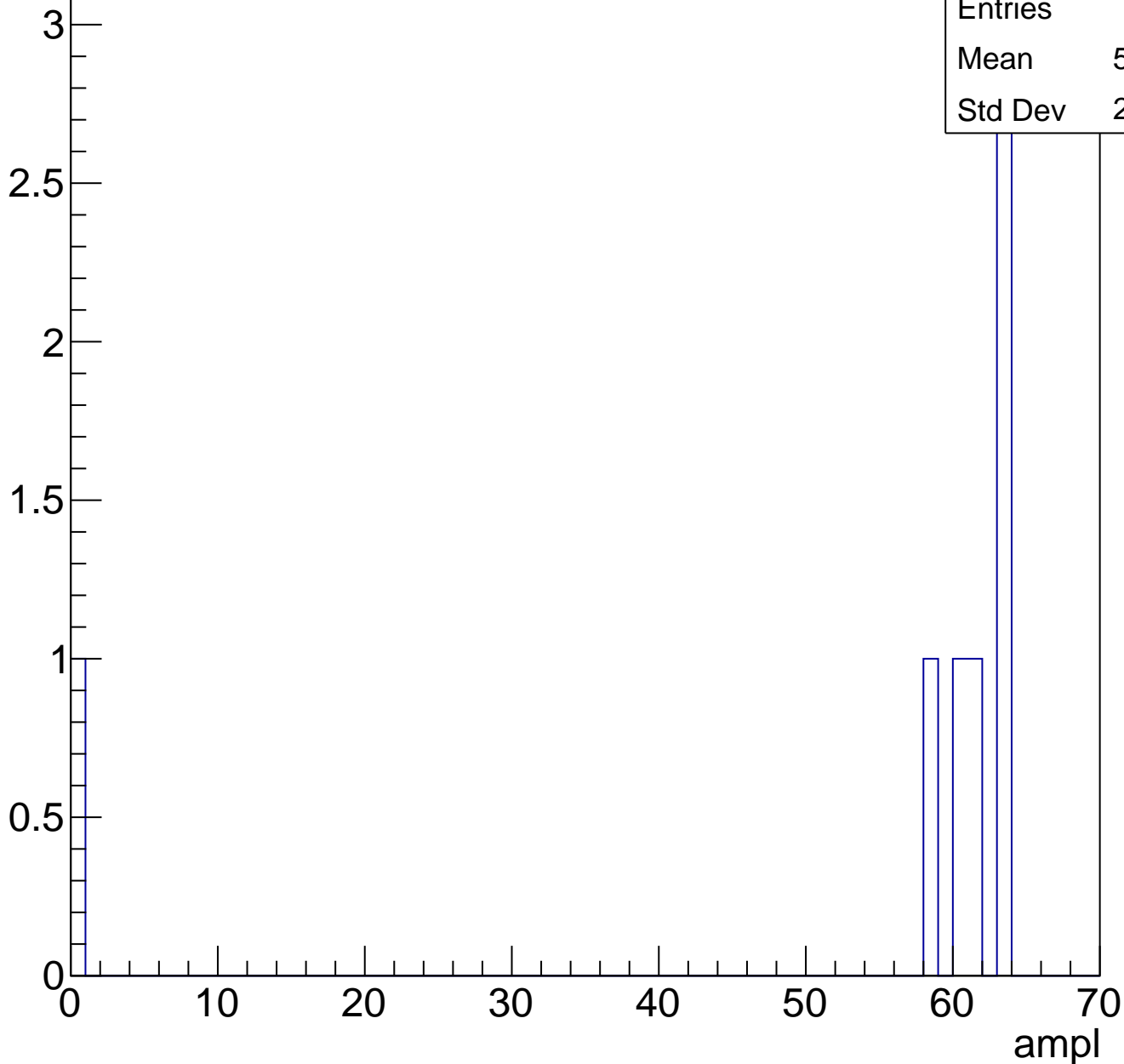
Entries	54
Mean	59.41
Std Dev	3.082



B1L103S, U5-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch112, adc0

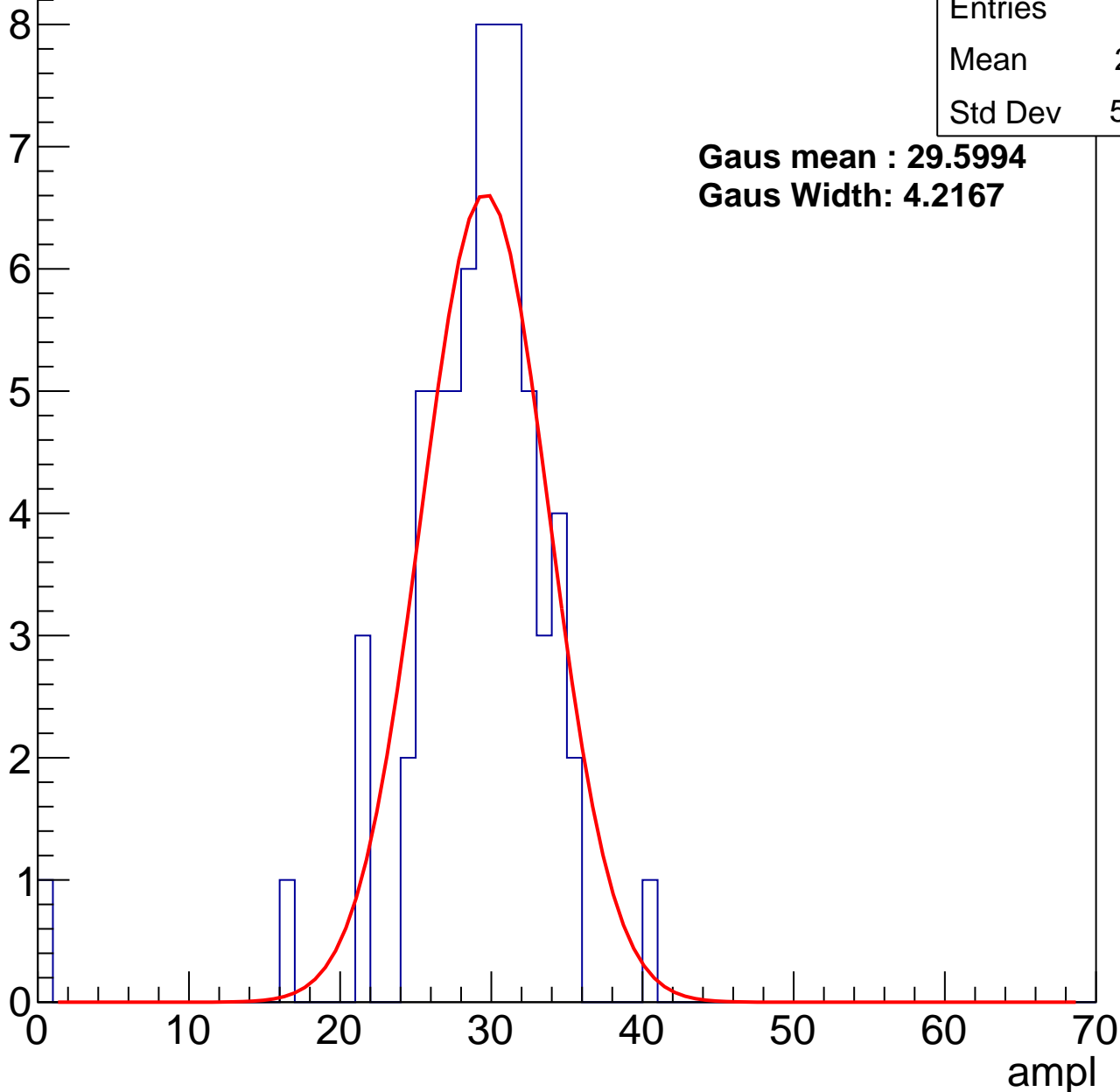
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.51
Std Dev	5.202

Gaus mean : 29.5994

Gaus Width: 4.2167



B1L103S, U5-ch112, adc1

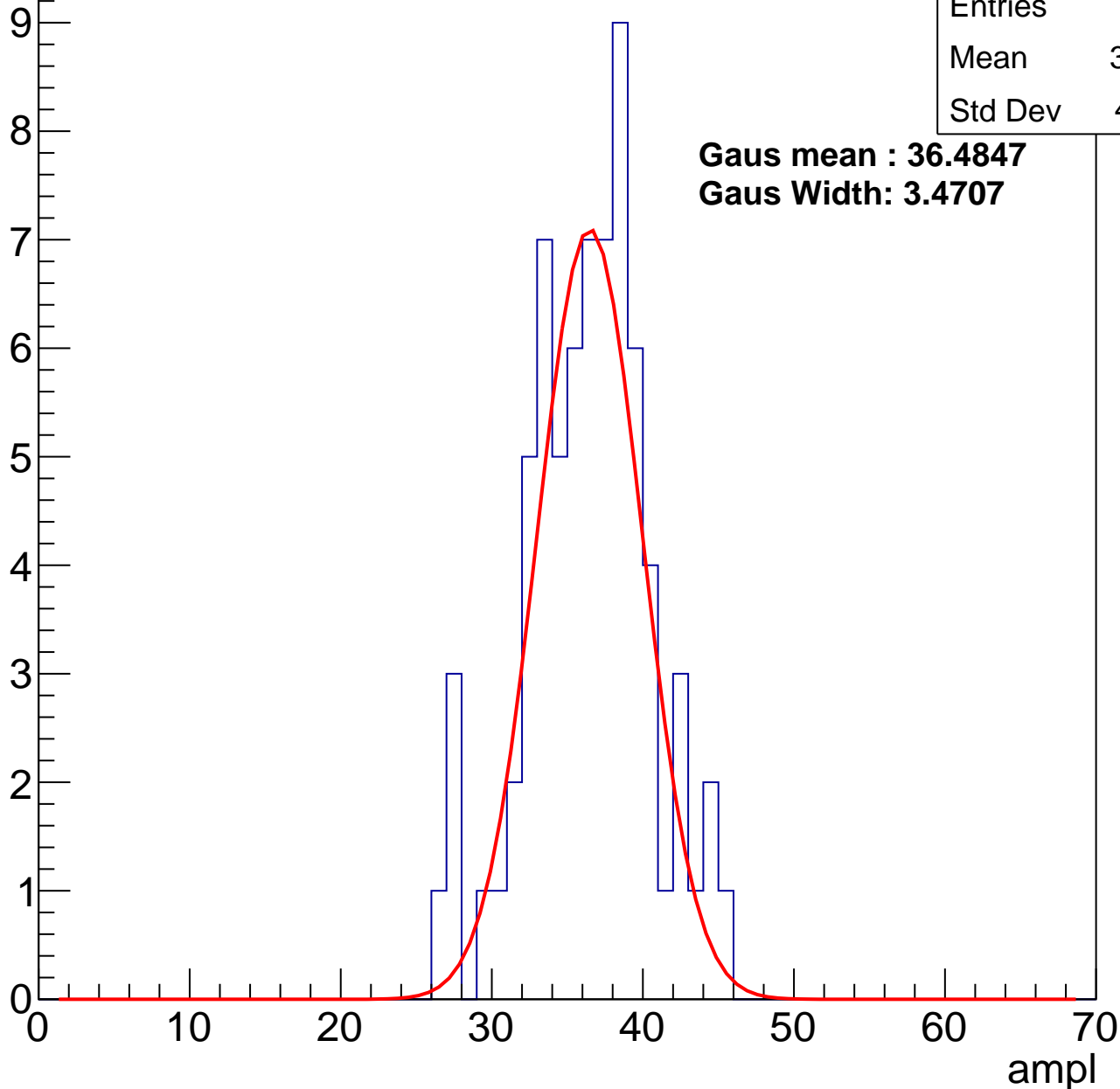
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	35.96
Std Dev	4.081

Gaus mean : 36.4847

Gaus Width: 3.4707



B1L103S, U5-ch112, adc2

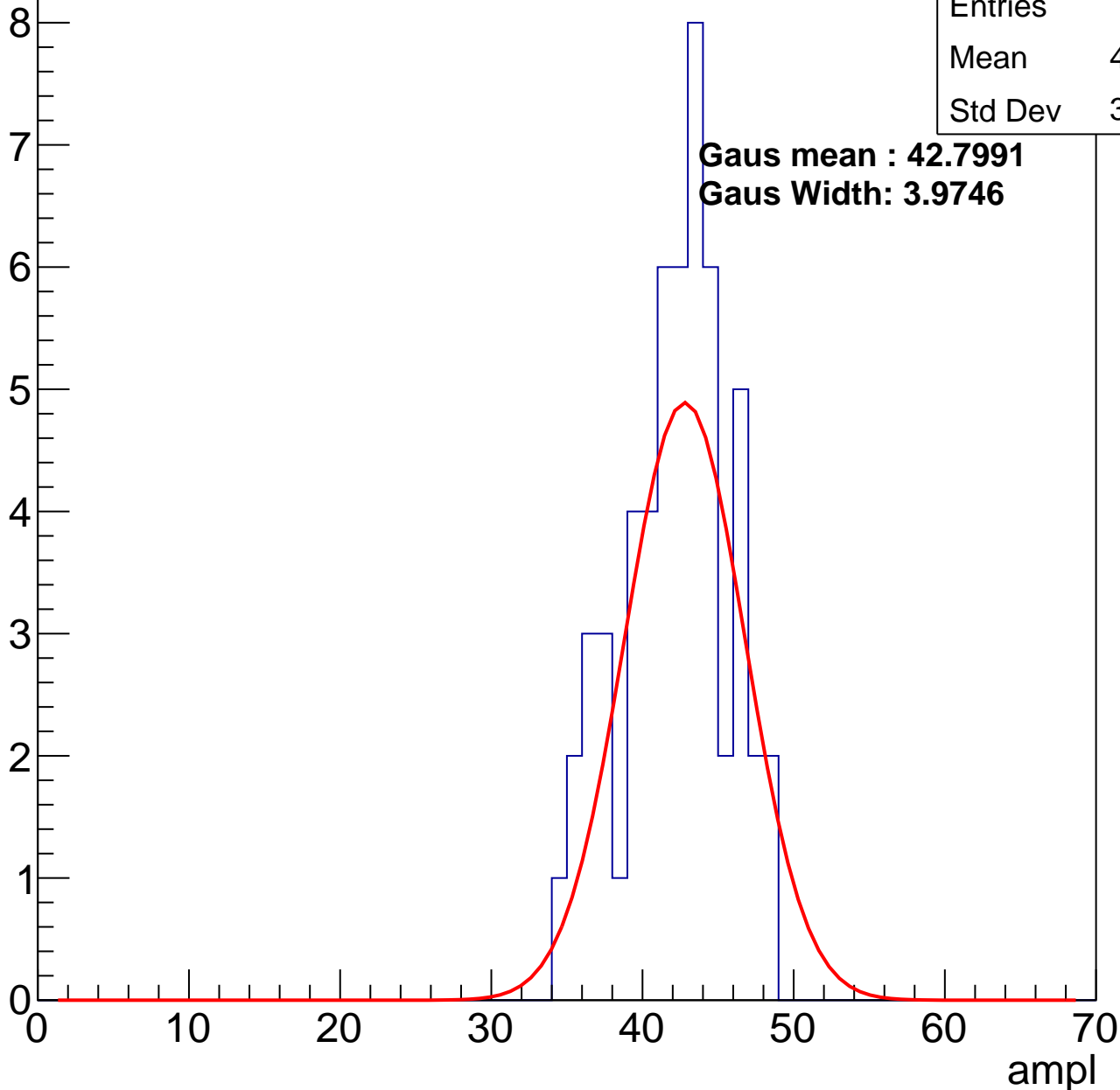
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	41.69
Std Dev	3.479

Gaus mean : 42.7991

Gaus Width: 3.9746

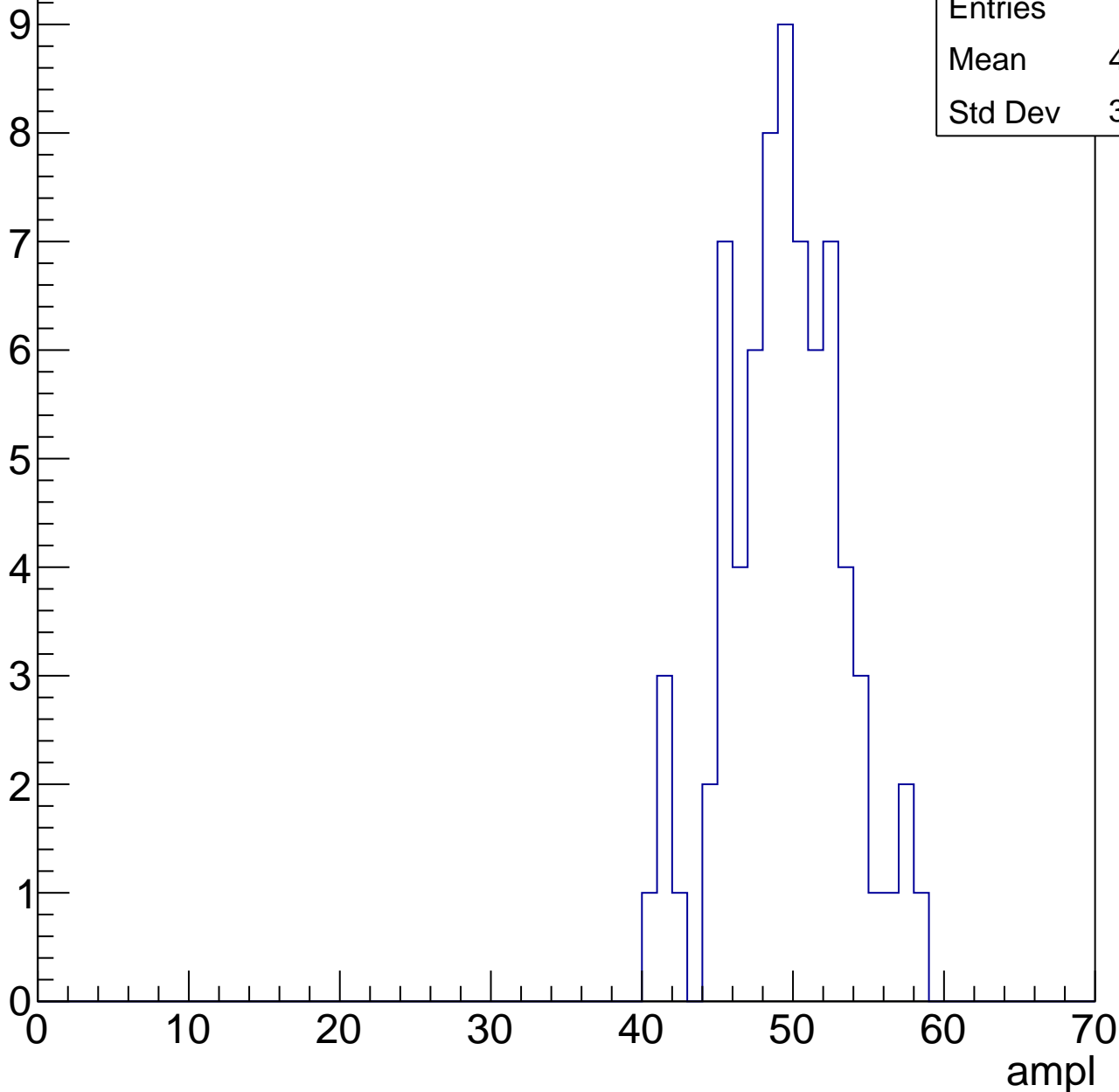


B1L103S, U5-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	48.99
Std Dev	3.845

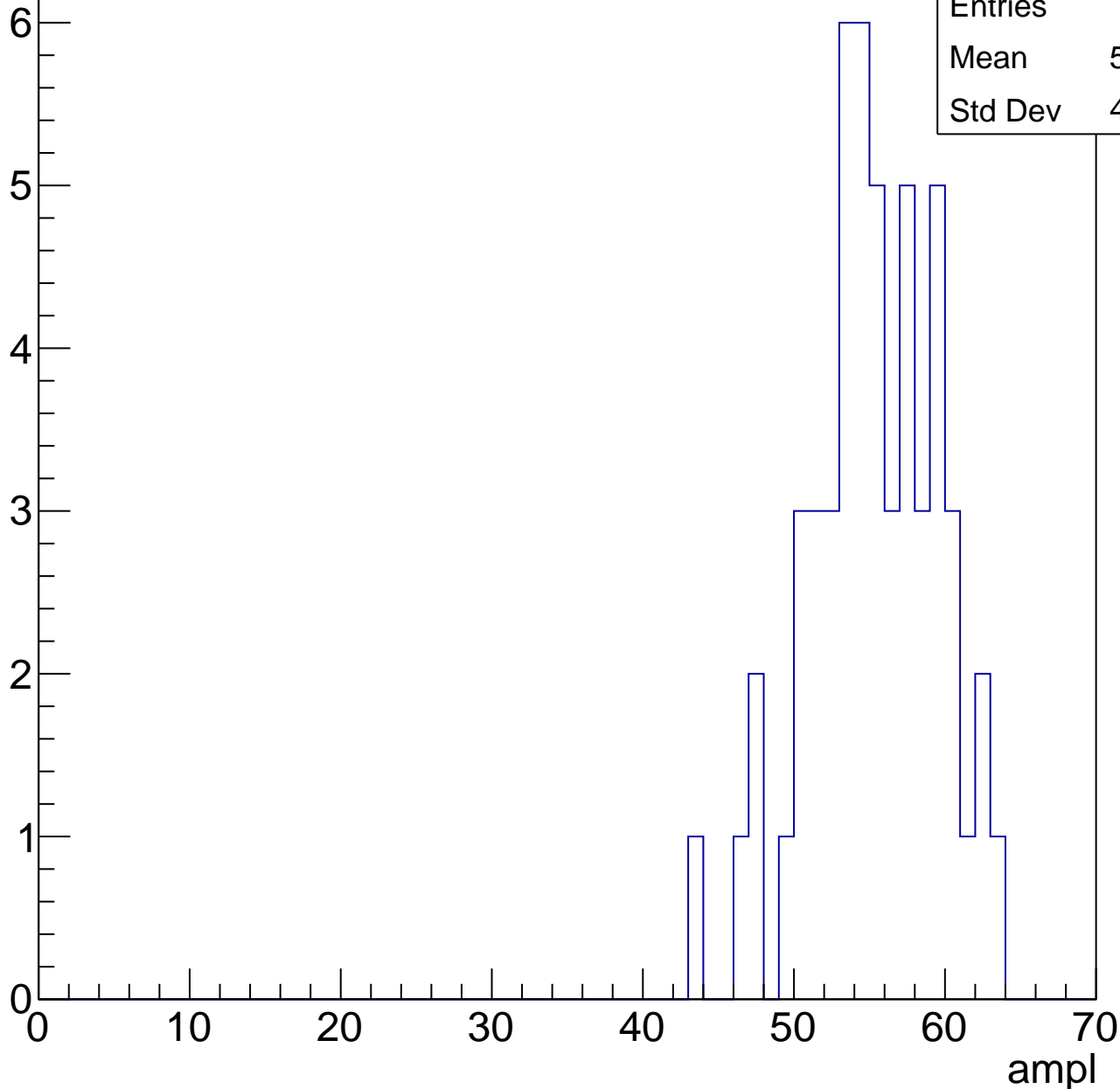


B1L103S, U5-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	54.78
Std Dev	4.237

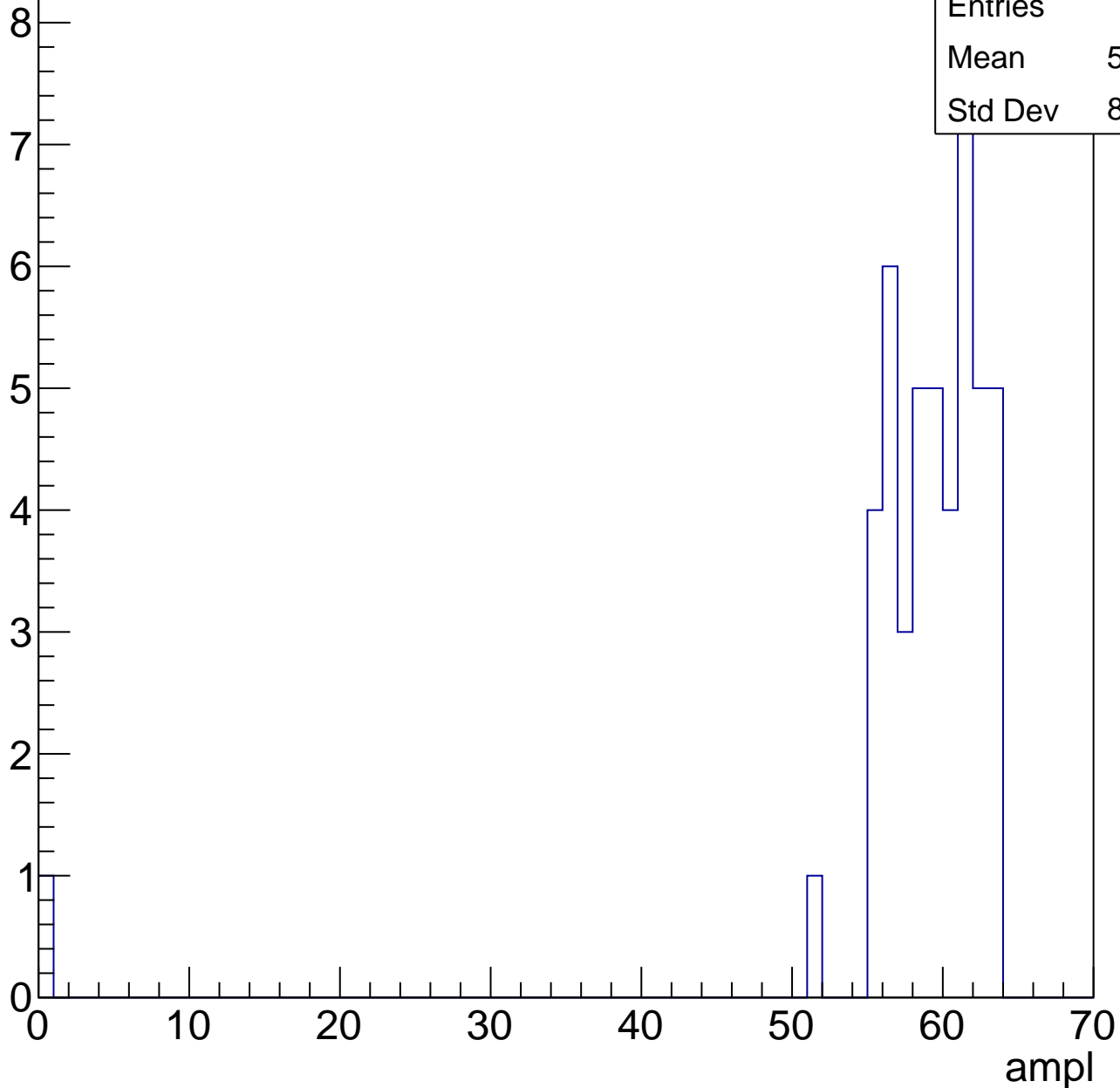


B1L103S, U5-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

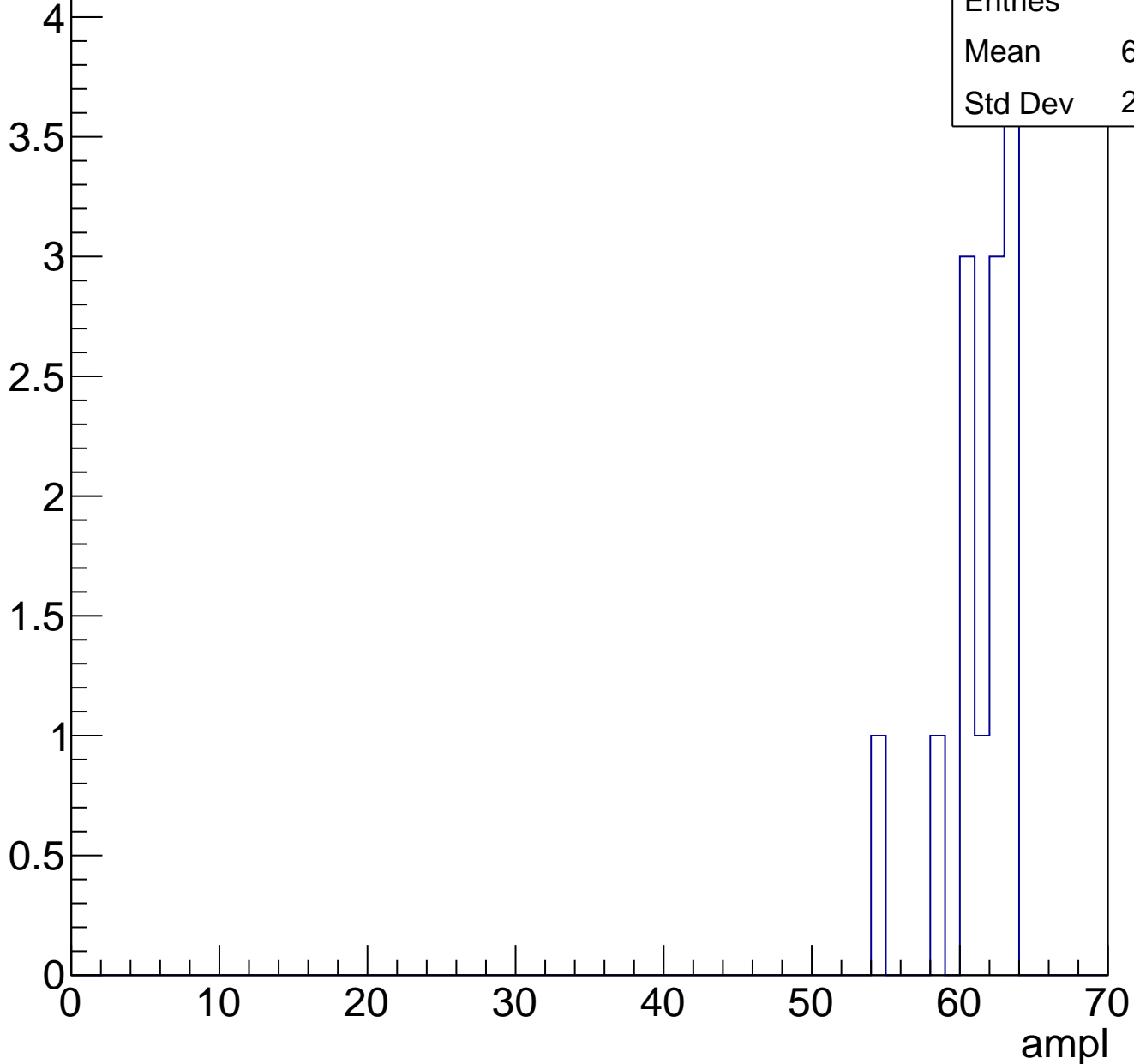
Entries	47
Mean	57.79
Std Dev	8.958



B1L103S, U5-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	13
Mean	60.85
Std Dev	2.476

B1L103S, U5-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U5-ch113, adc0

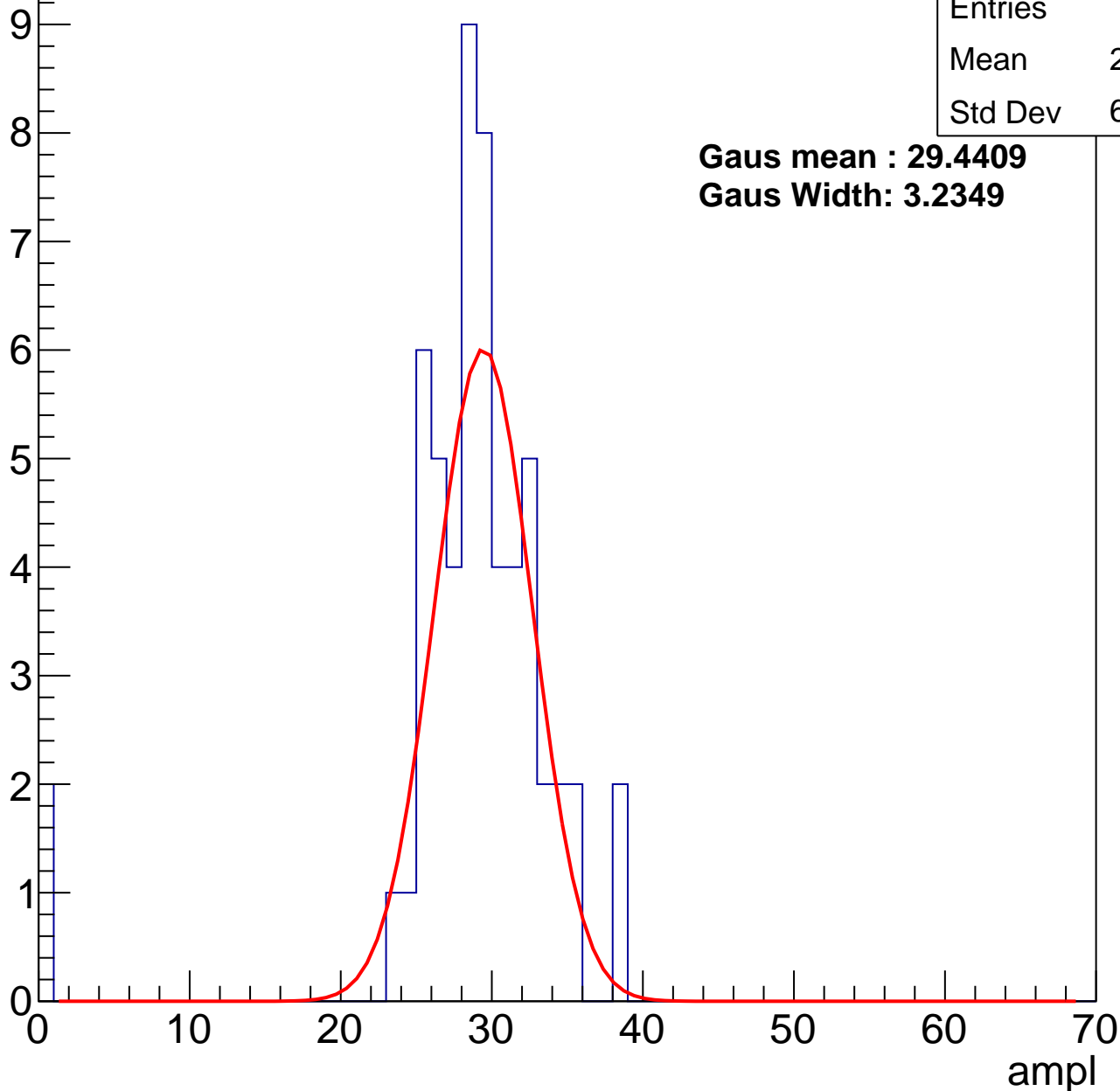
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	28.12
Std Dev	6.277

Gaus mean : 29.4409

Gaus Width: 3.2349



B1L103S, U5-ch113, adc1

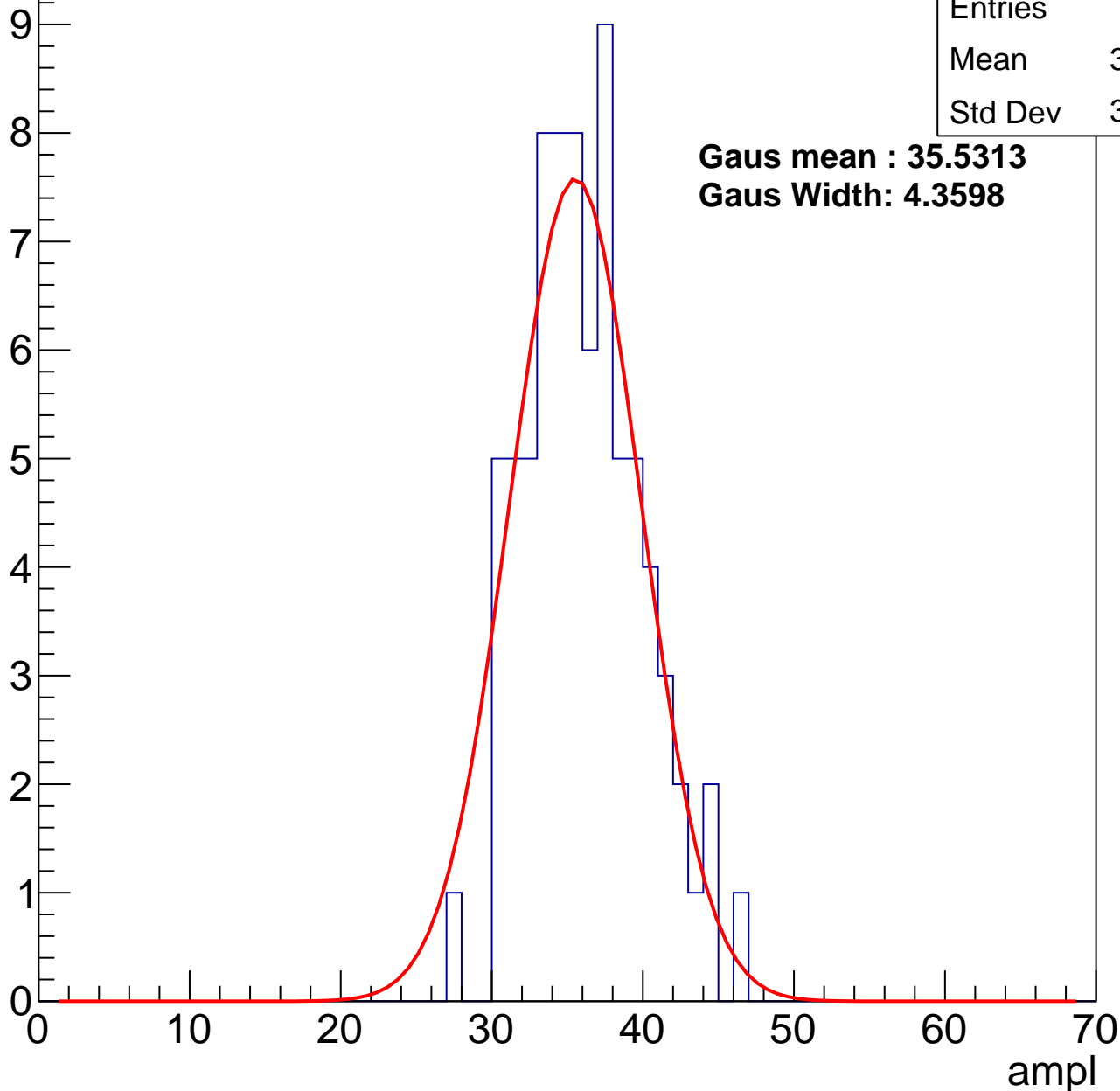
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	35.72
Std Dev	3.813

Gaus mean : 35.5313

Gaus Width: 4.3598

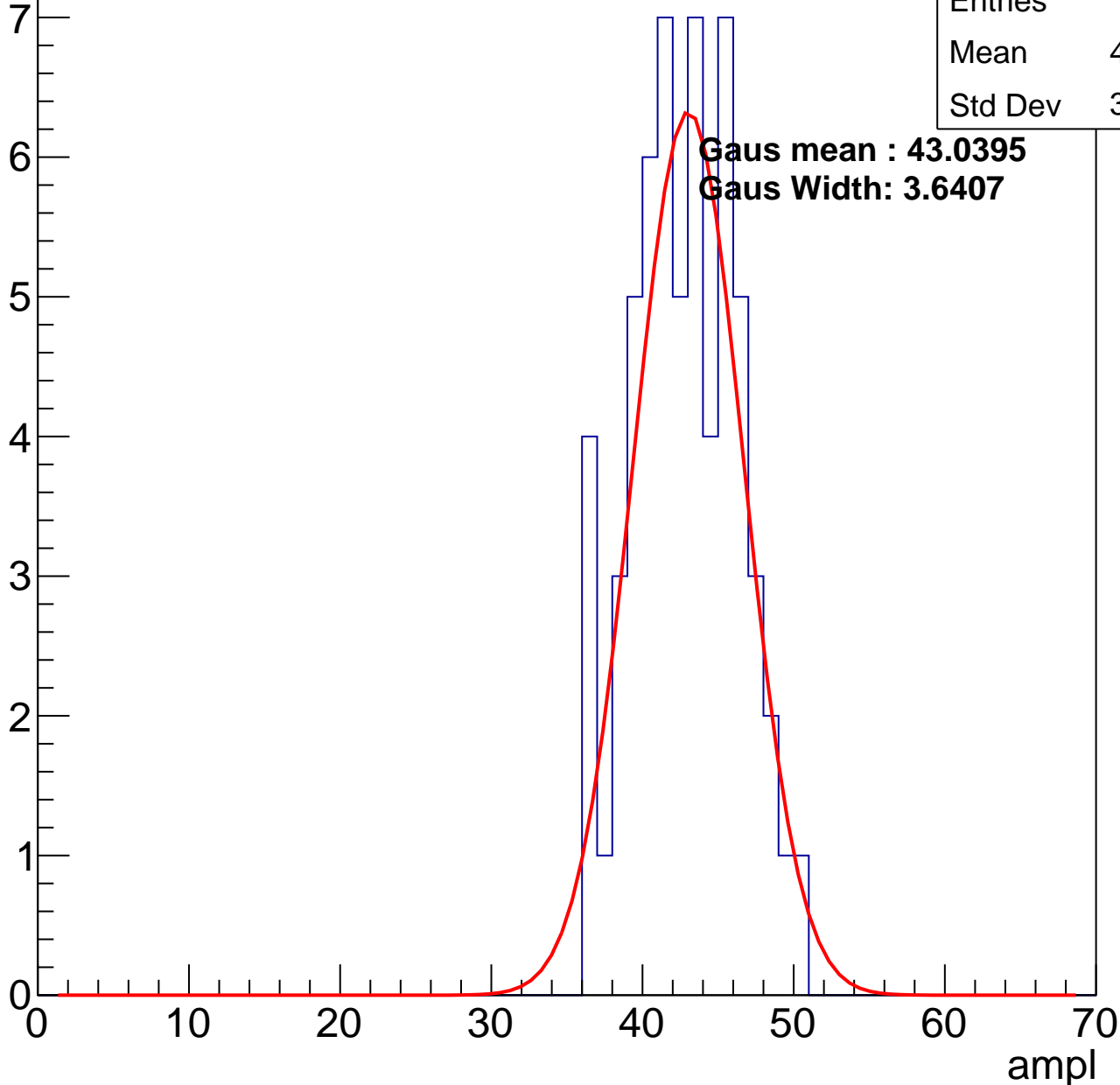


B1L103S, U5-ch113, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

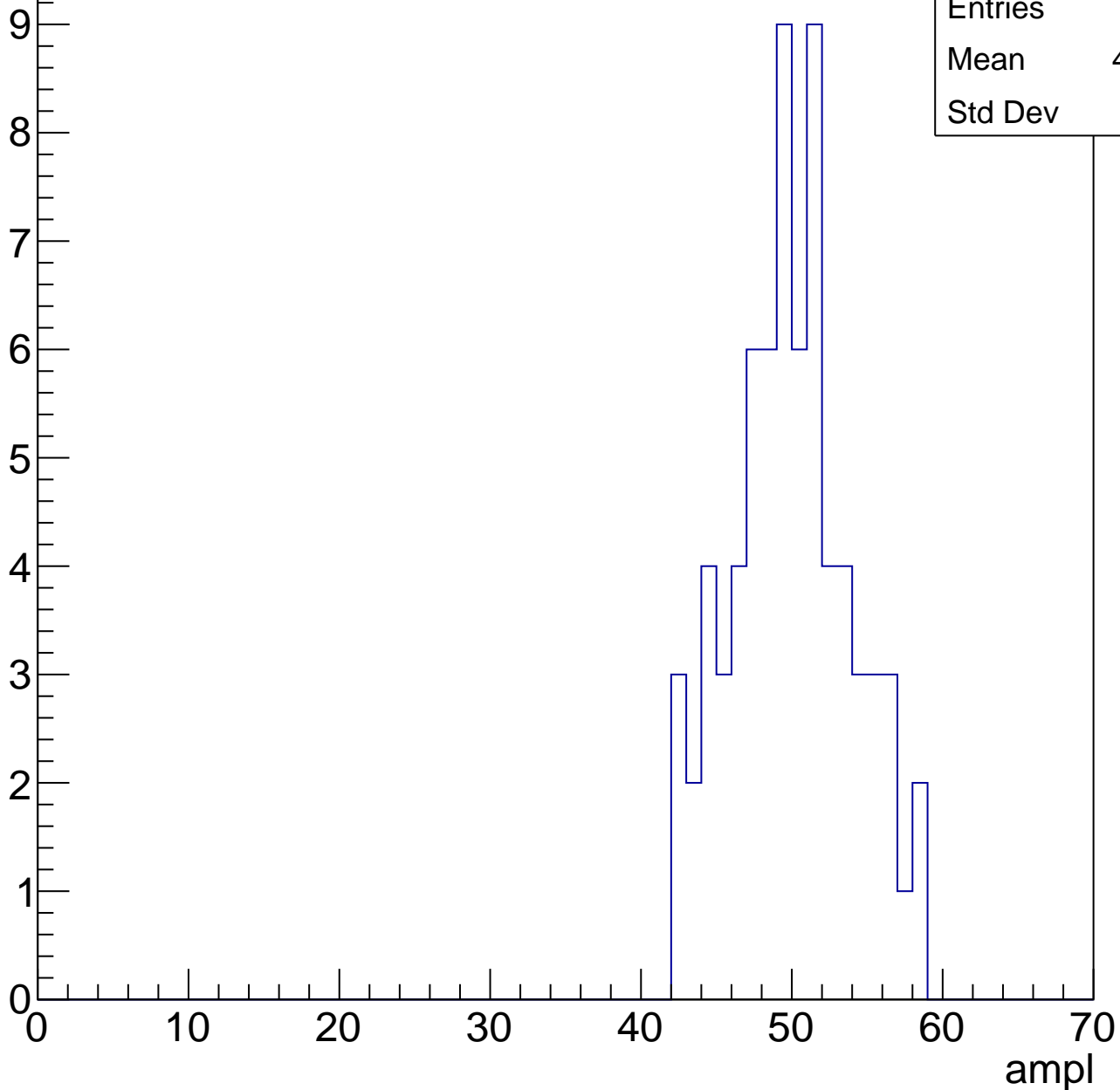
Entries	61
Mean	42.38
Std Dev	3.412



B1L103S, U5-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



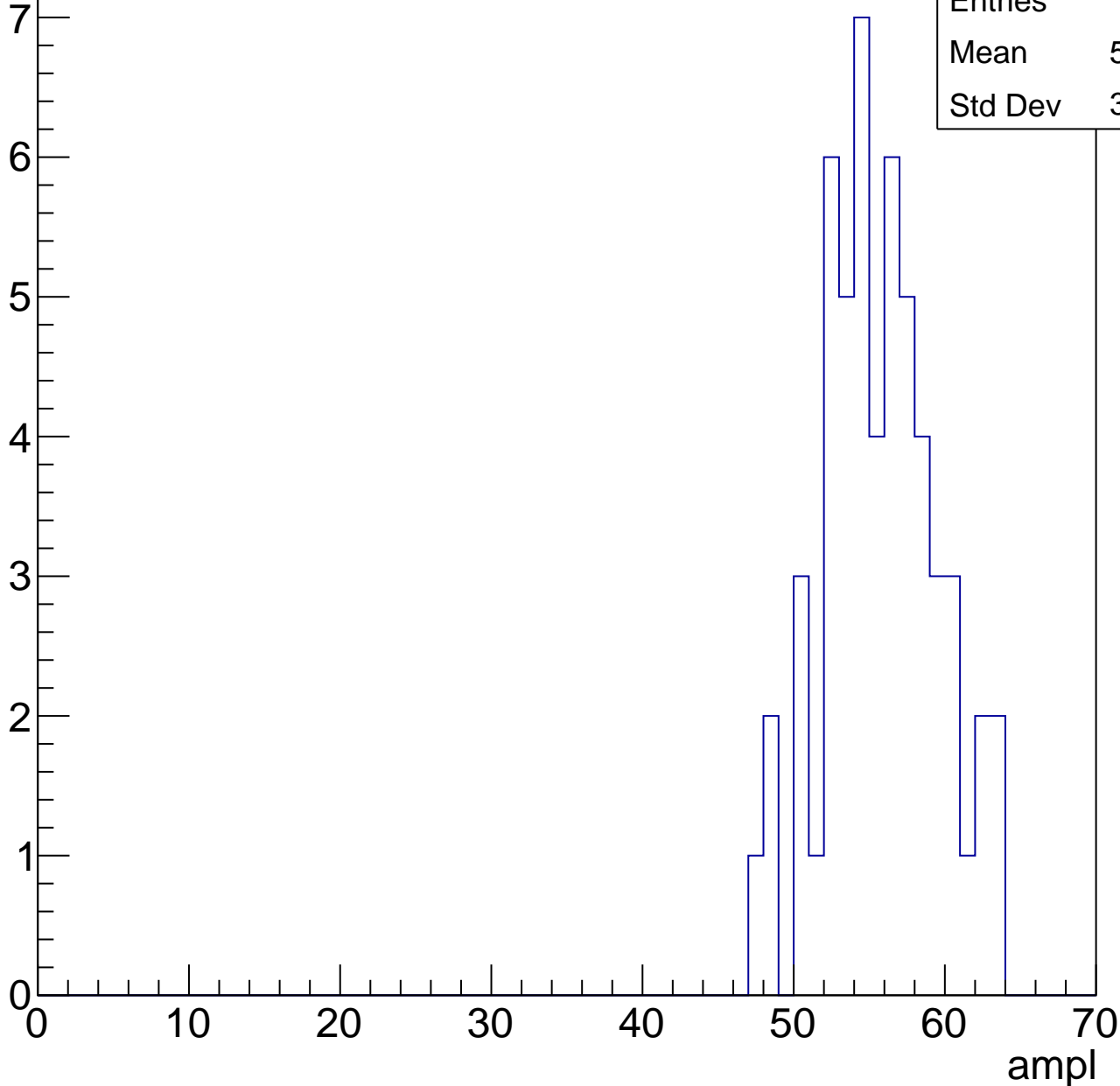
Entries	72
Mean	49.51
Std Dev	3.93

B1L103S, U5-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	55.27
Std Dev	3.758

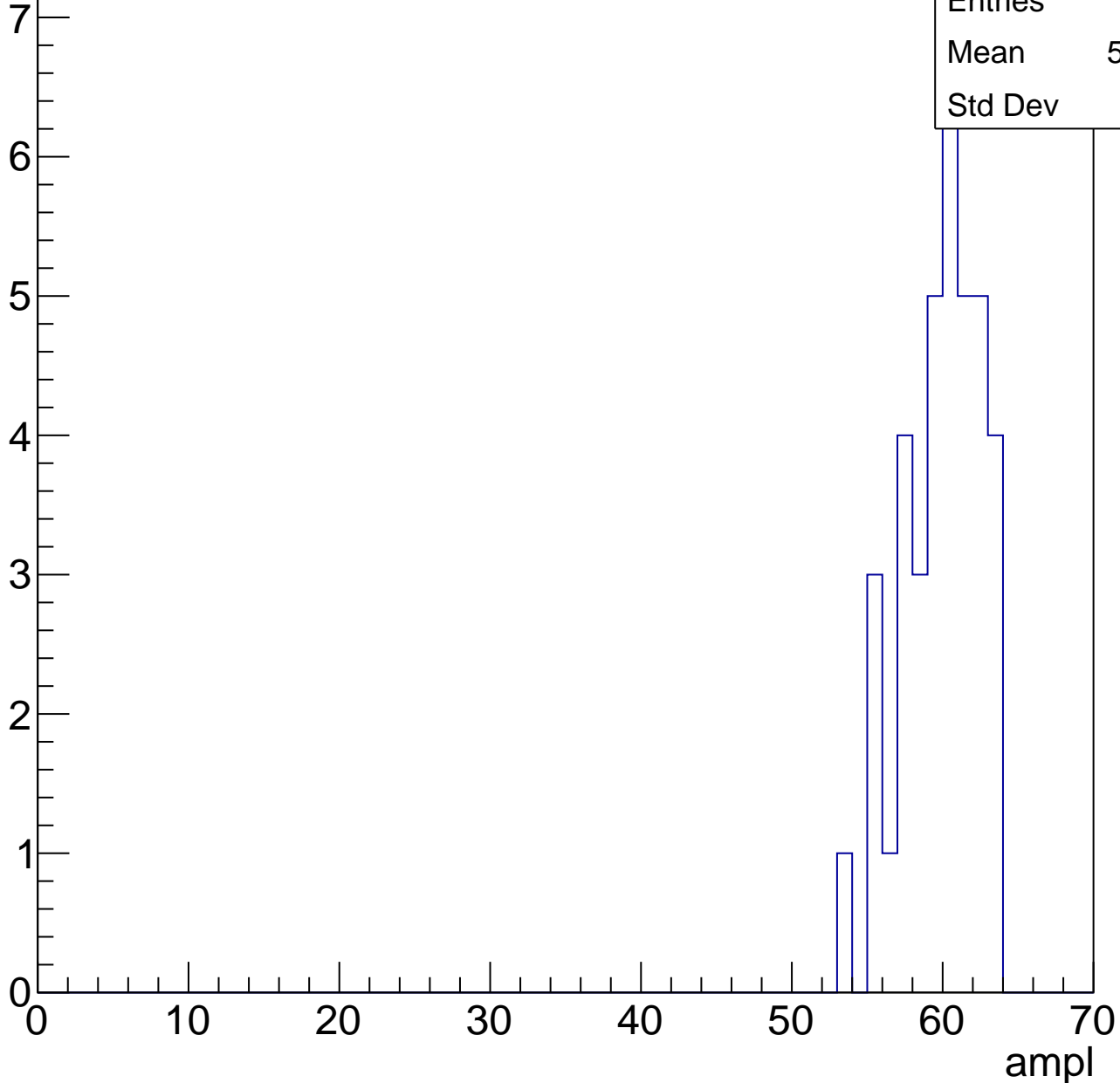


B1L103S, U5-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

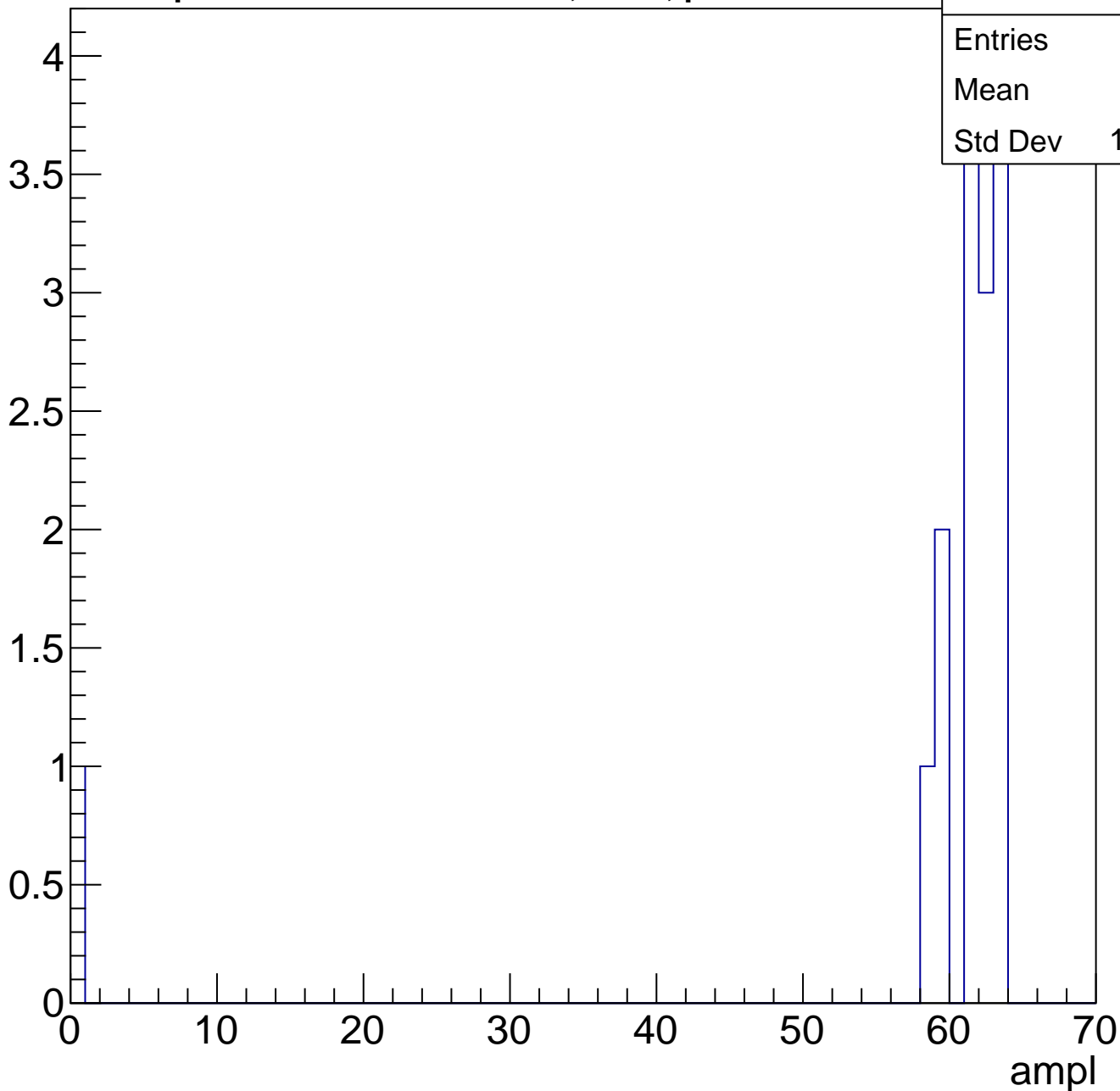
Entries	38
Mean	59.42
Std Dev	2.52



B1L103S, U5-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L103S, U5-ch114, adc0

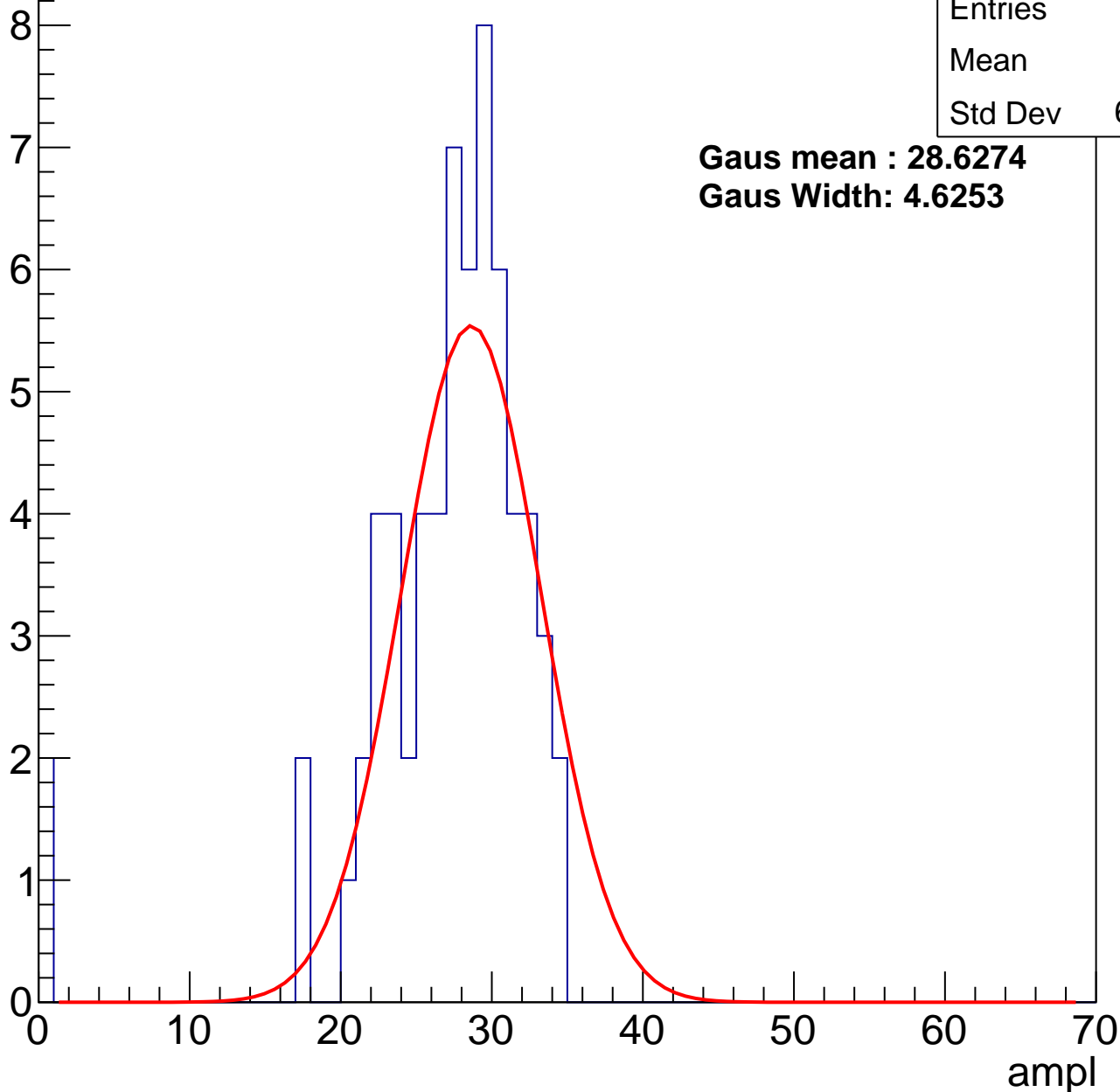
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	26.4
Std Dev	6.101

Gaus mean : 28.6274

Gaus Width: 4.6253



B1L103S, U5-ch114, adc1

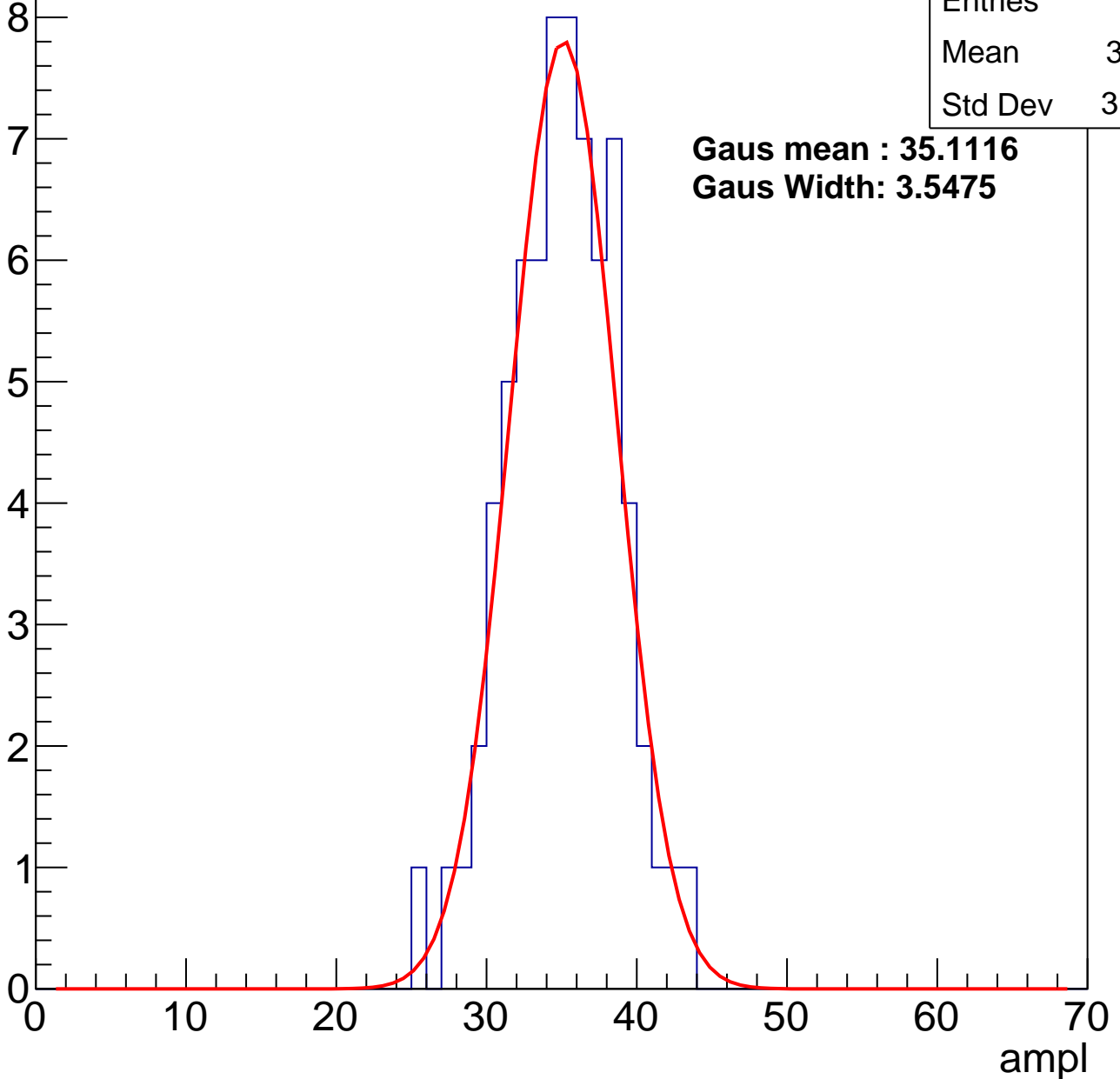
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	34.61
Std Dev	3.554

Gaus mean : 35.1116

Gaus Width: 3.5475



B1L103S, U5-ch114, adc2

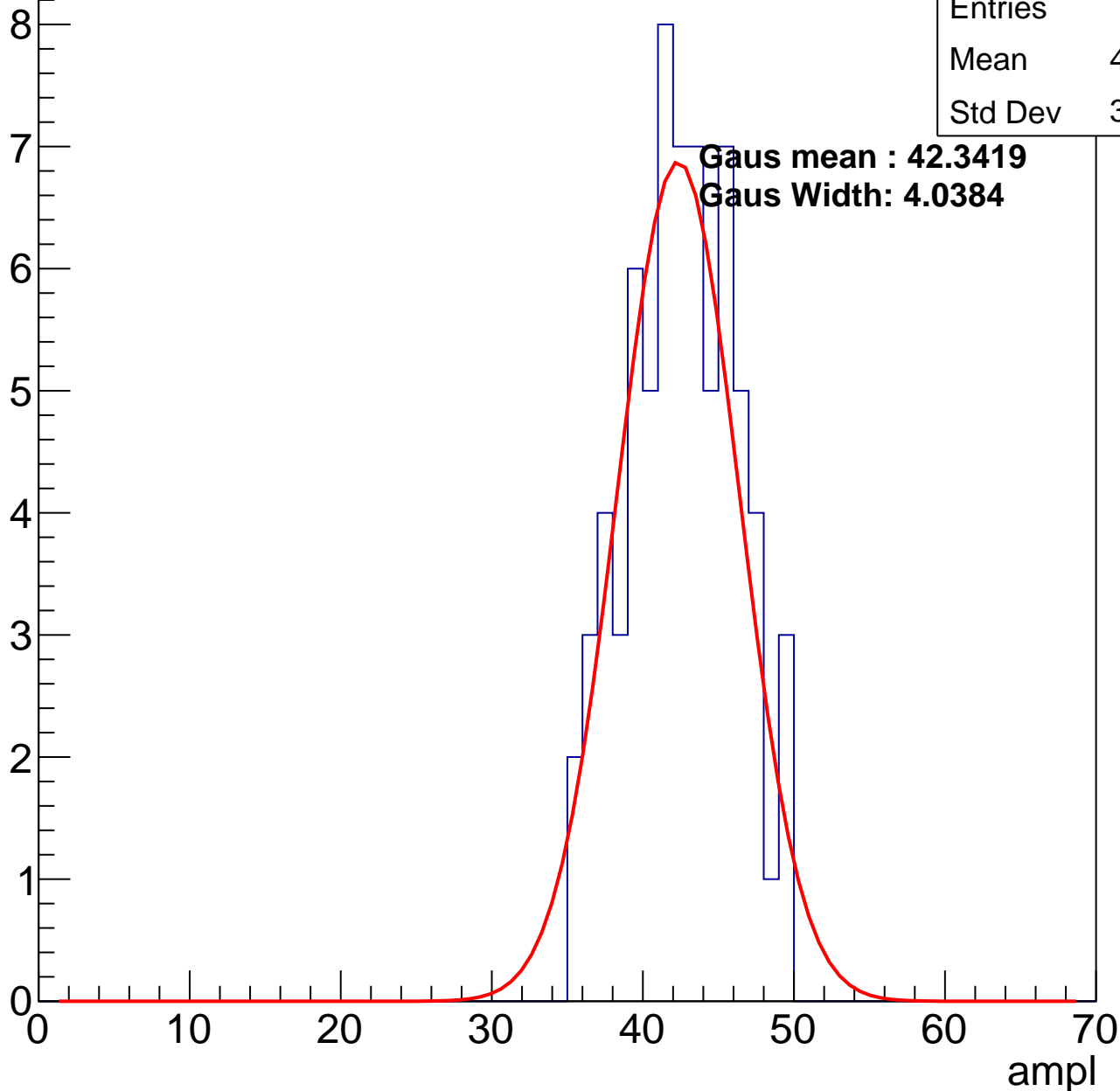
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.07
Std Dev	3.563

Gaus mean : 42.3419

Gaus Width: 4.0384

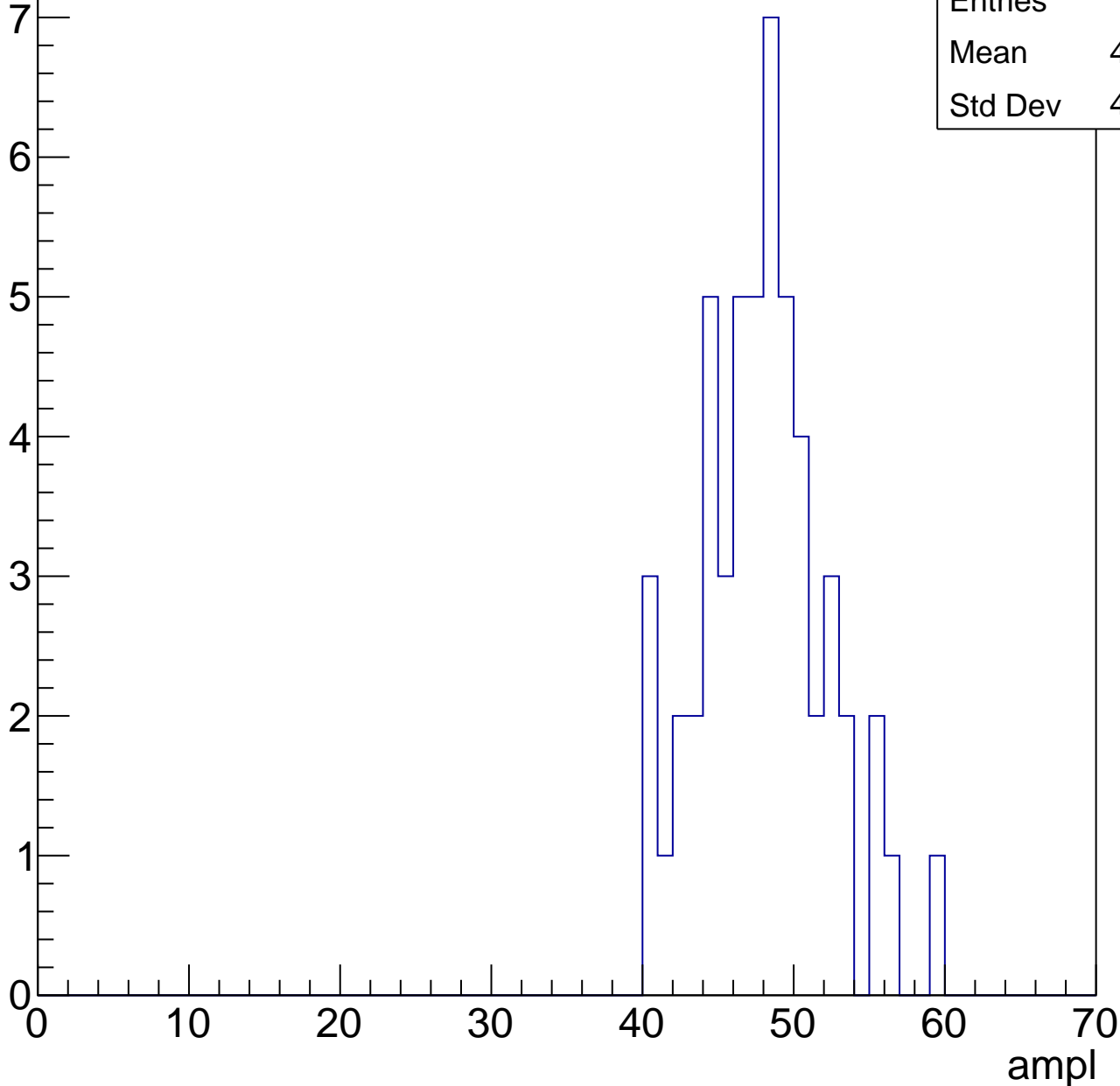


B1L103S, U5-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	47.57
Std Dev	4.132

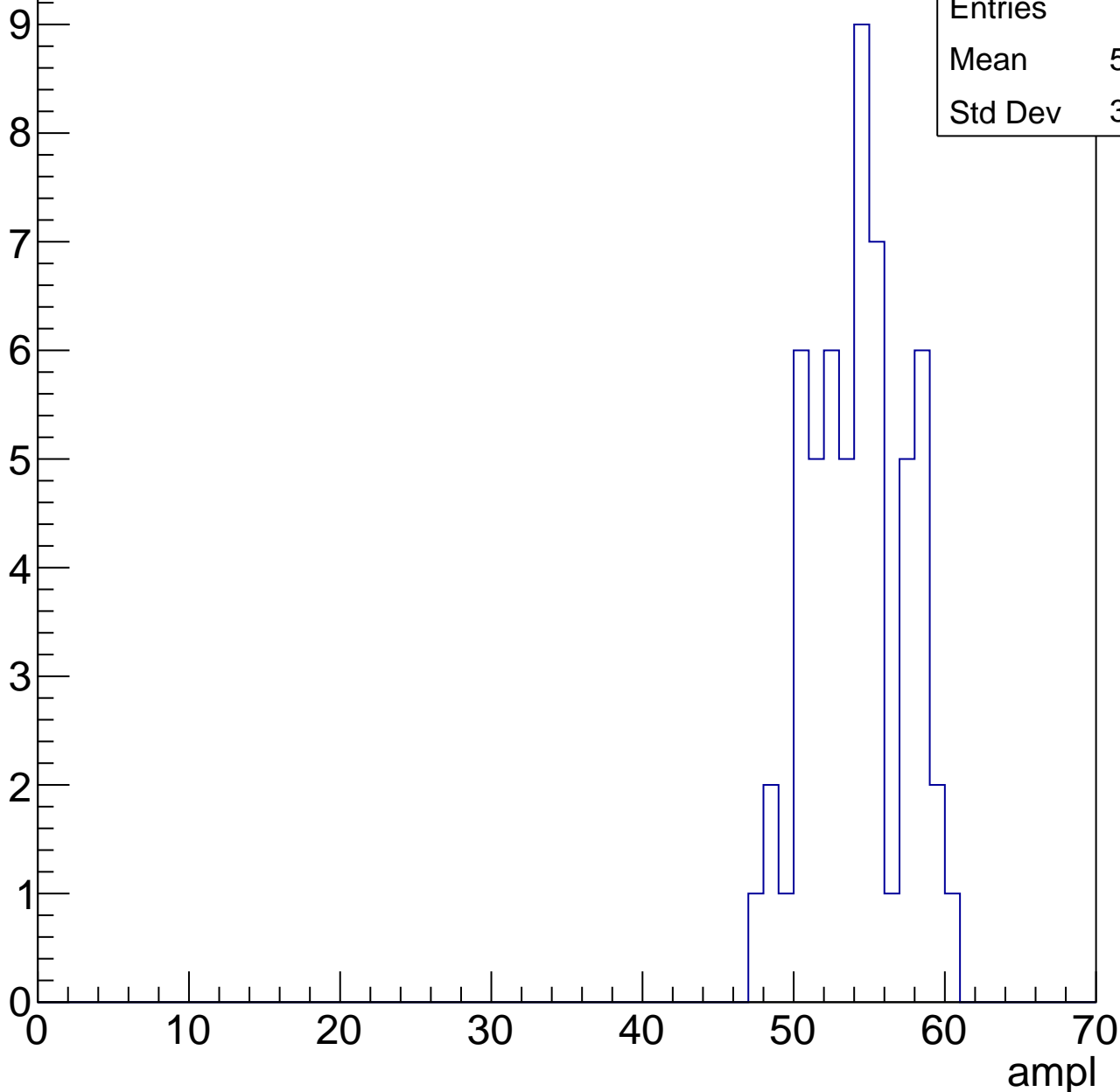


B1L103S, U5-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	53.72
Std Dev	3.105

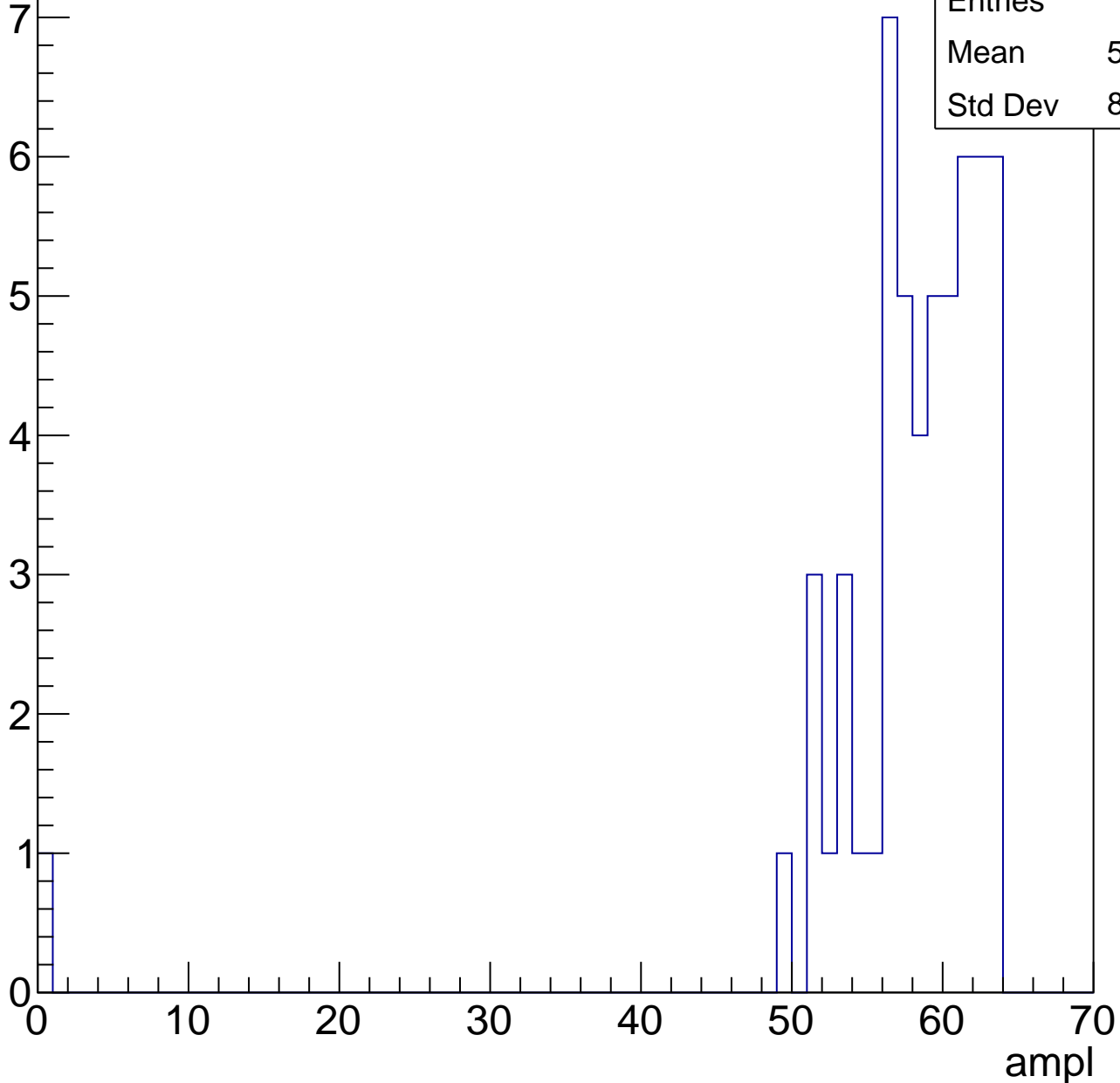


B1L103S, U5-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

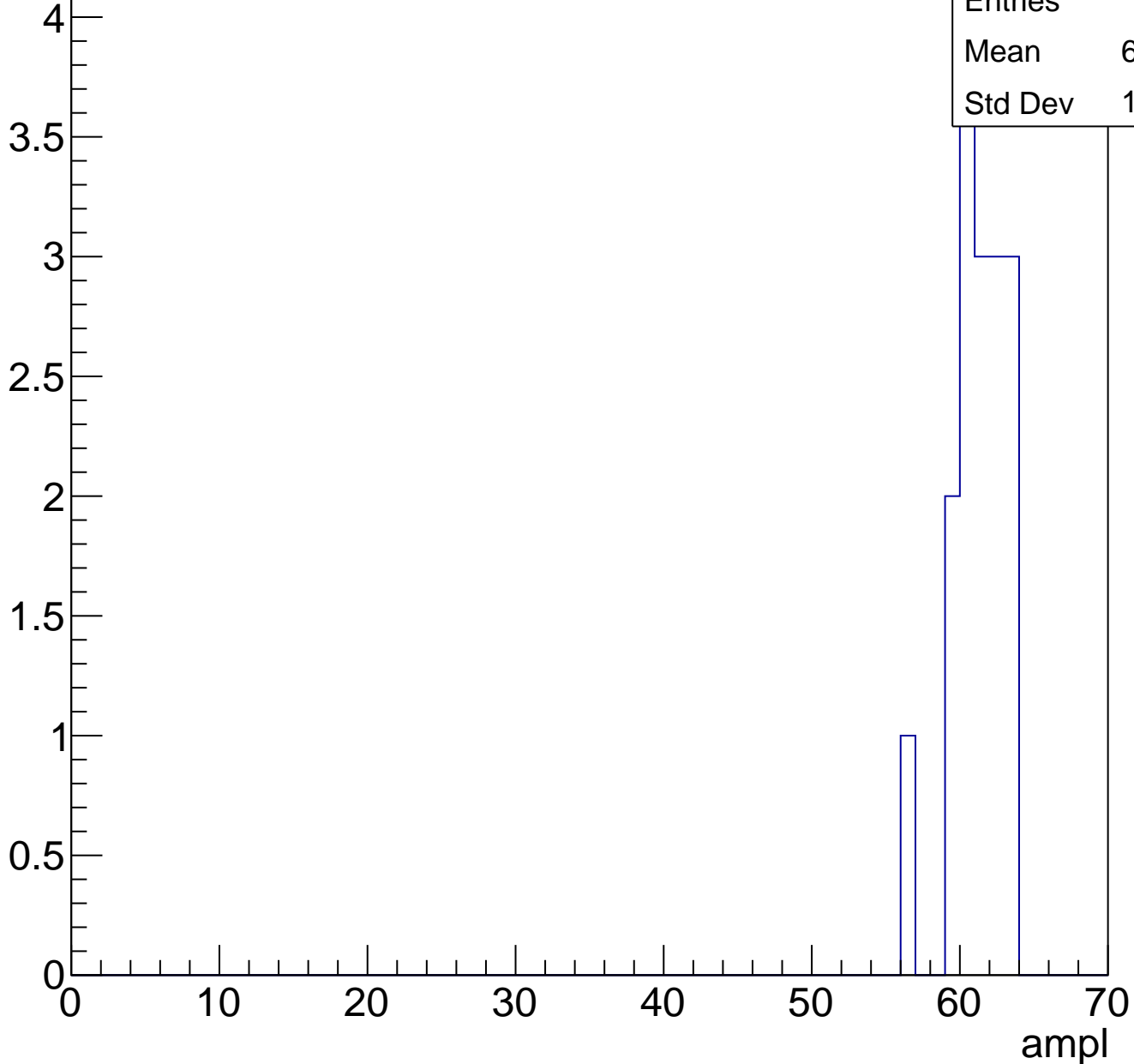
Entries	55
Mean	57.13
Std Dev	8.575



B1L103S, U5-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

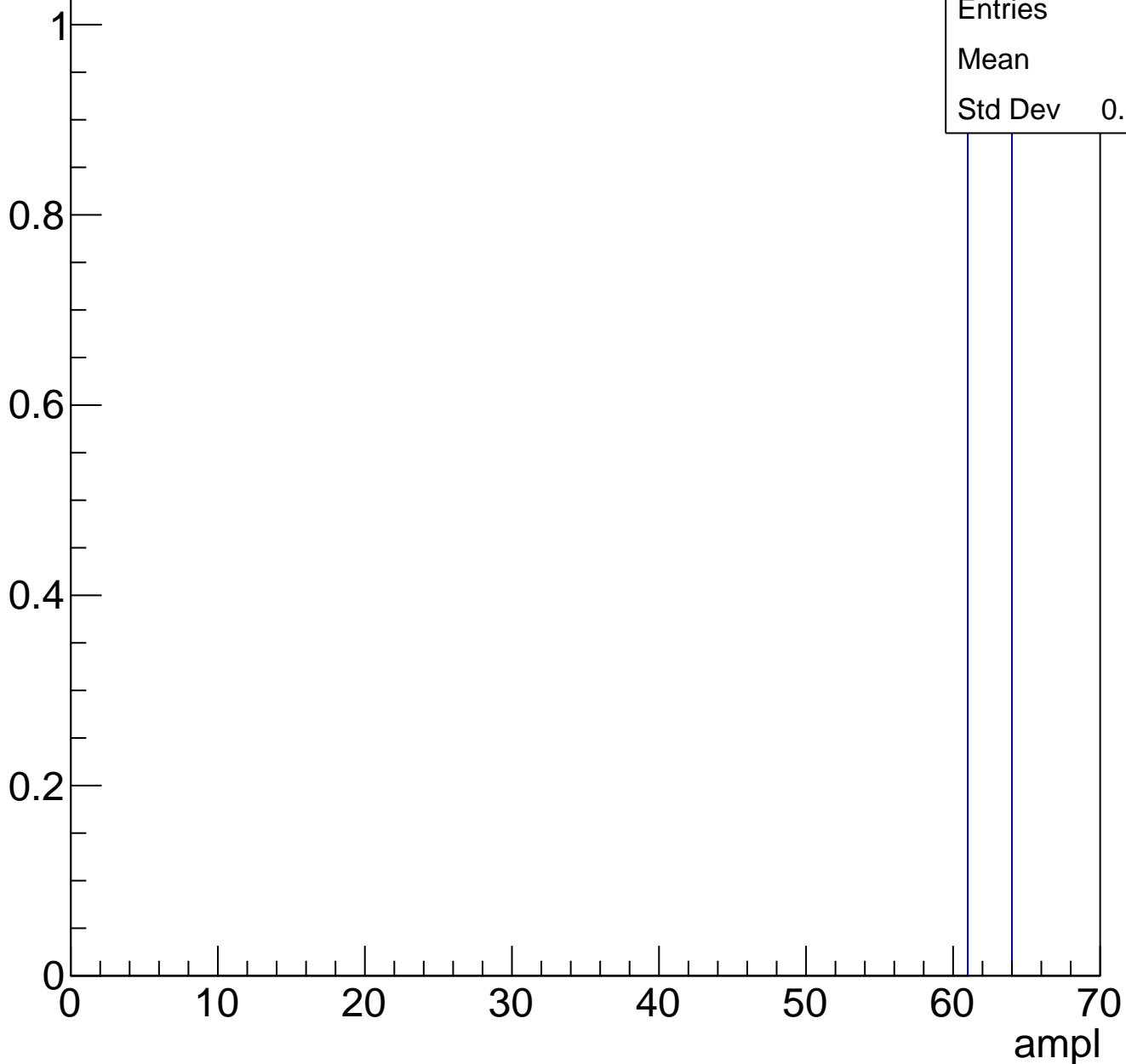
Entry



B1L103S, U5-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch115, adc0

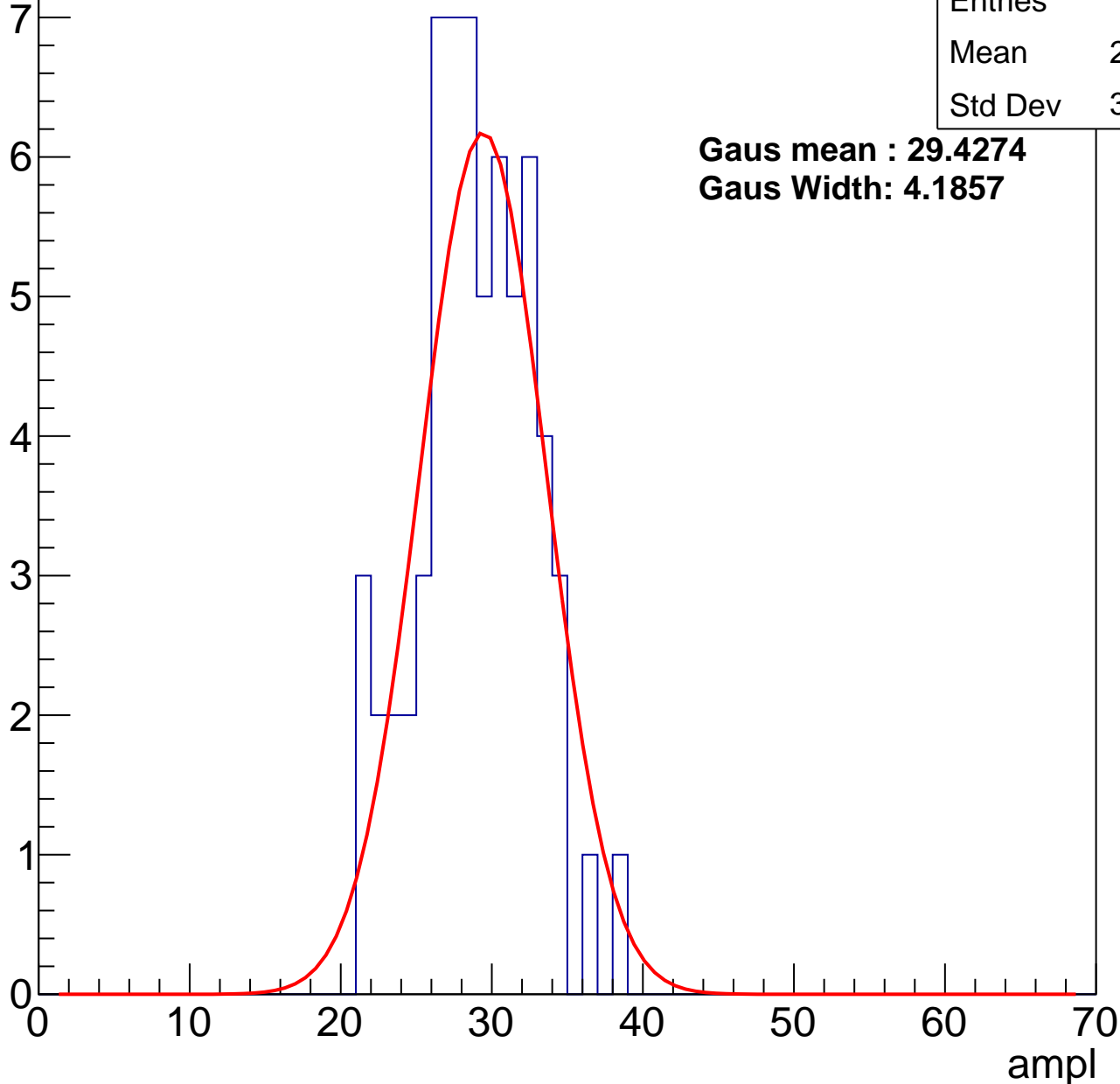
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.48
Std Dev	3.729

Gaus mean : 29.4274

Gaus Width: 4.1857



B1L103S, U5-ch115, adc1

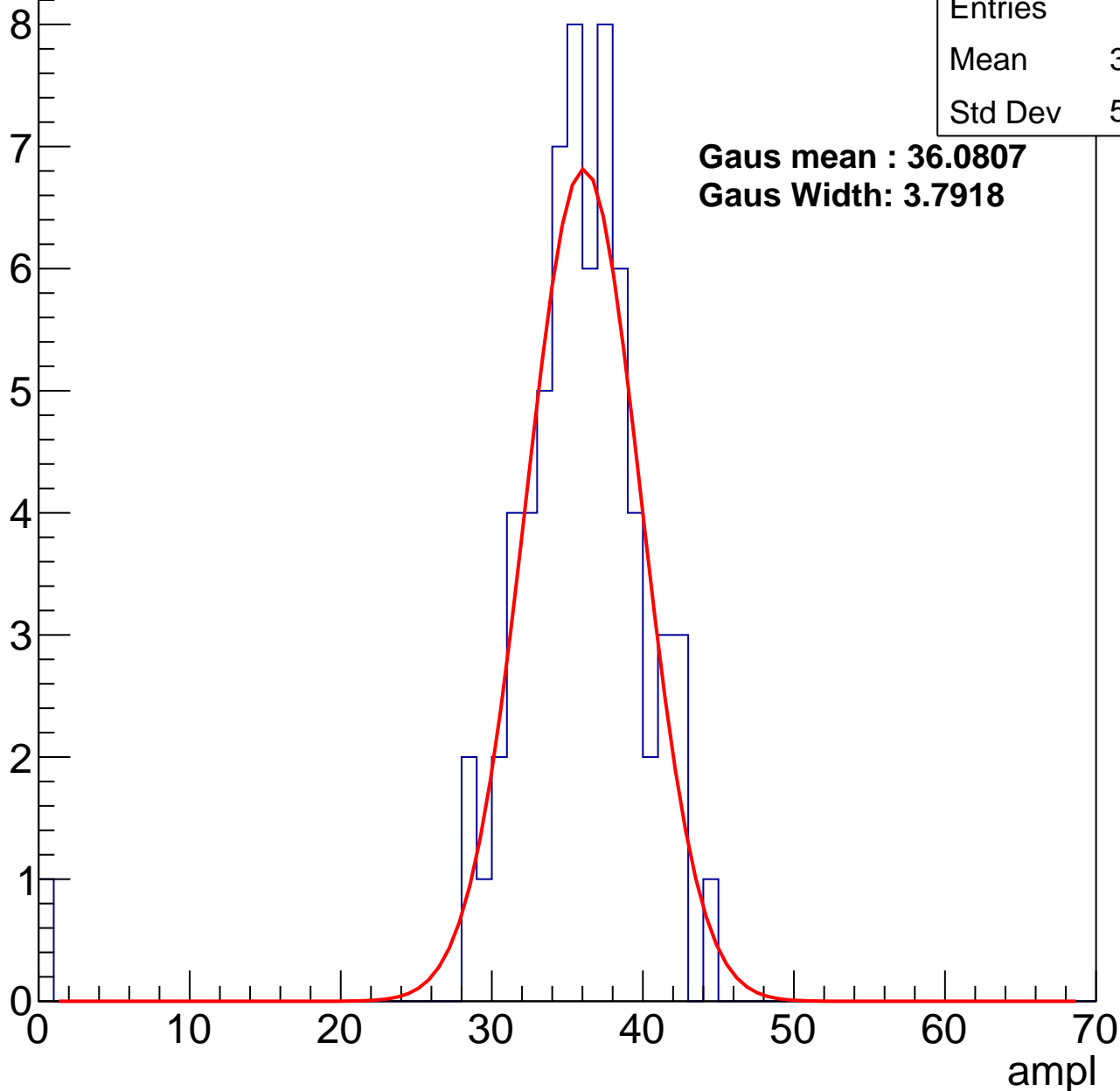
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.06
Std Dev	5.569

Gaus mean : 36.0807

Gaus Width: 3.7918



B1L103S, U5-ch115, adc2

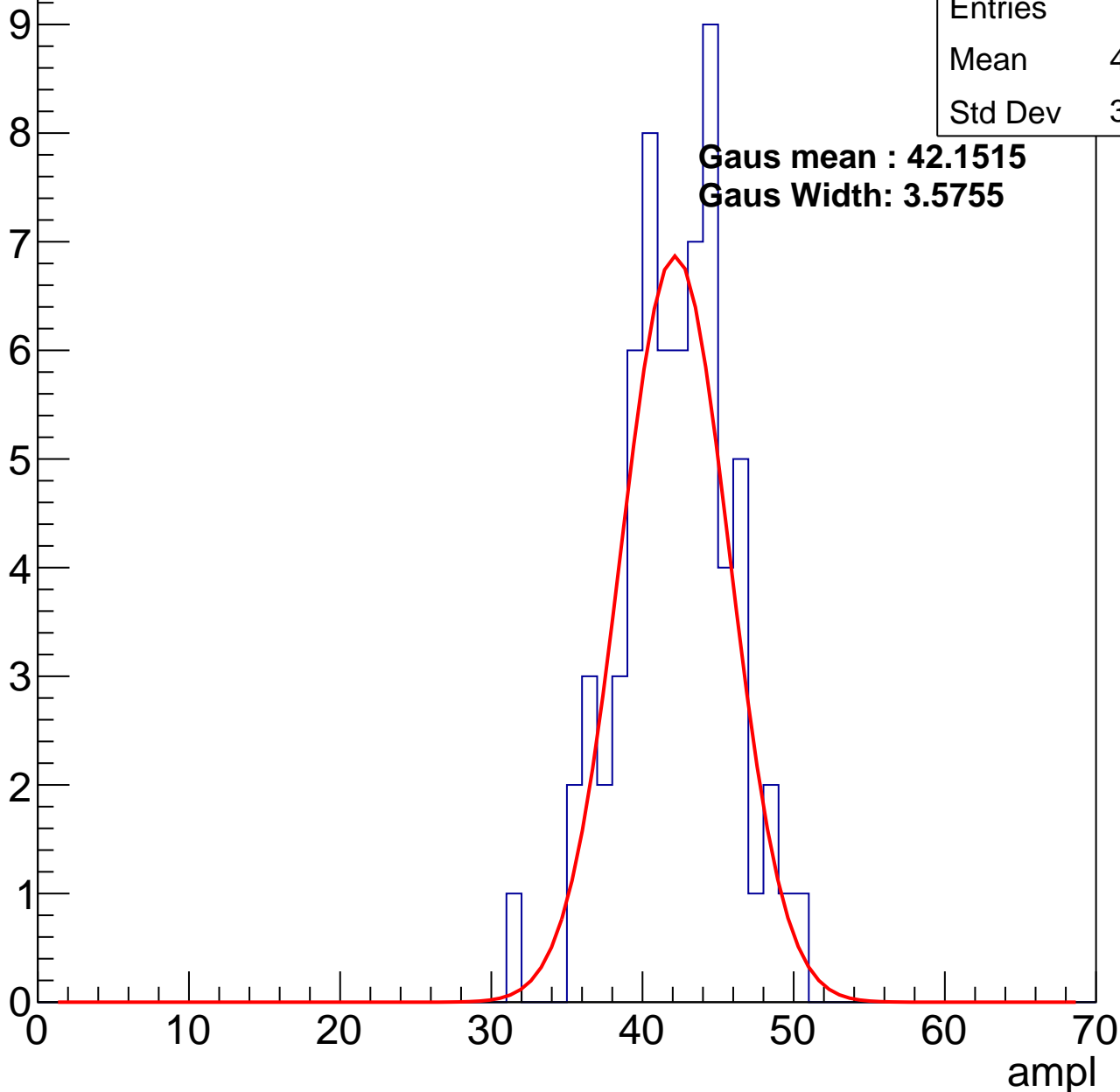
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	41.76
Std Dev	3.629

Gaus mean : 42.1515

Gaus Width: 3.5755

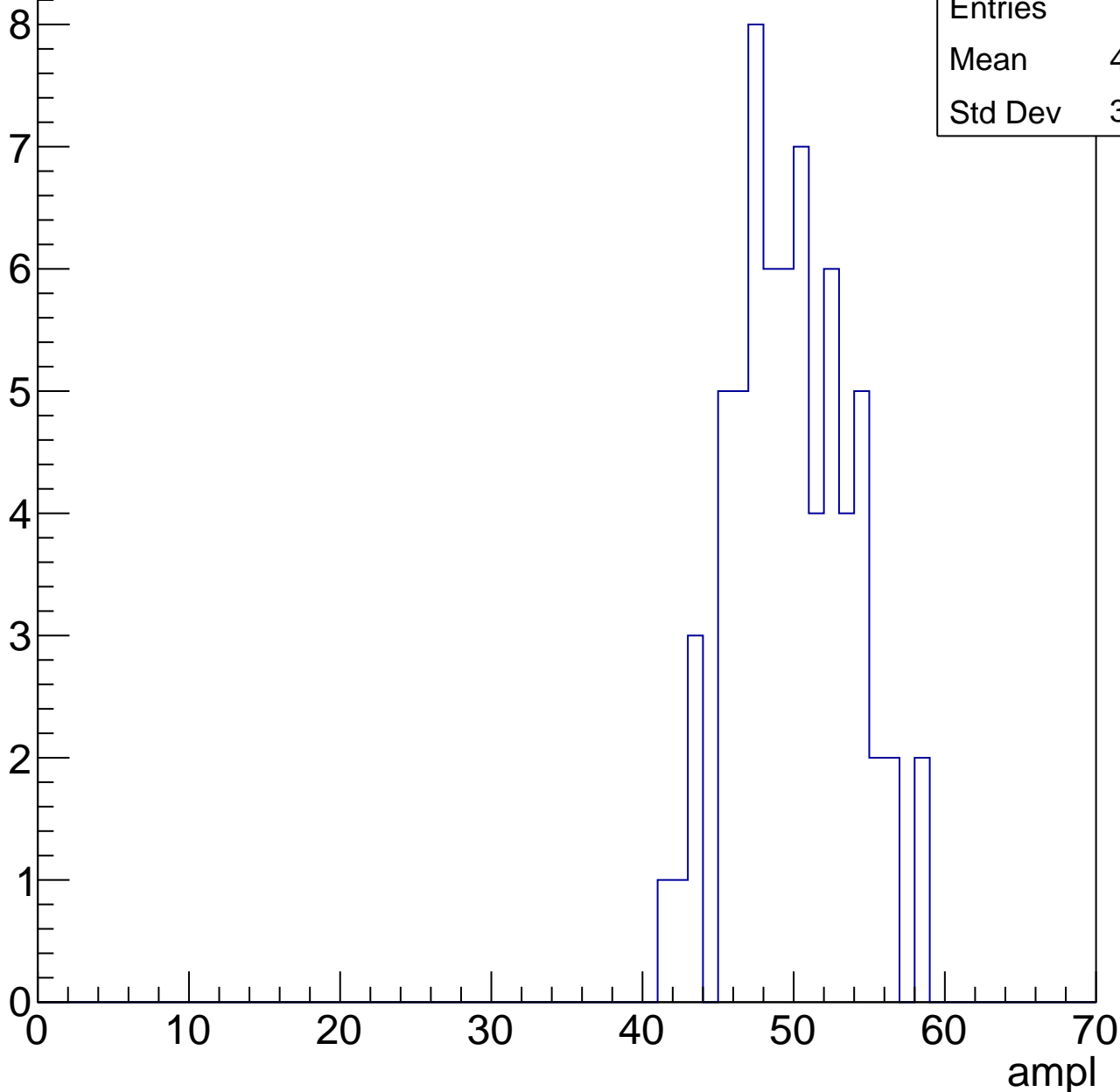


B1L103S, U5-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.42
Std Dev	3.806

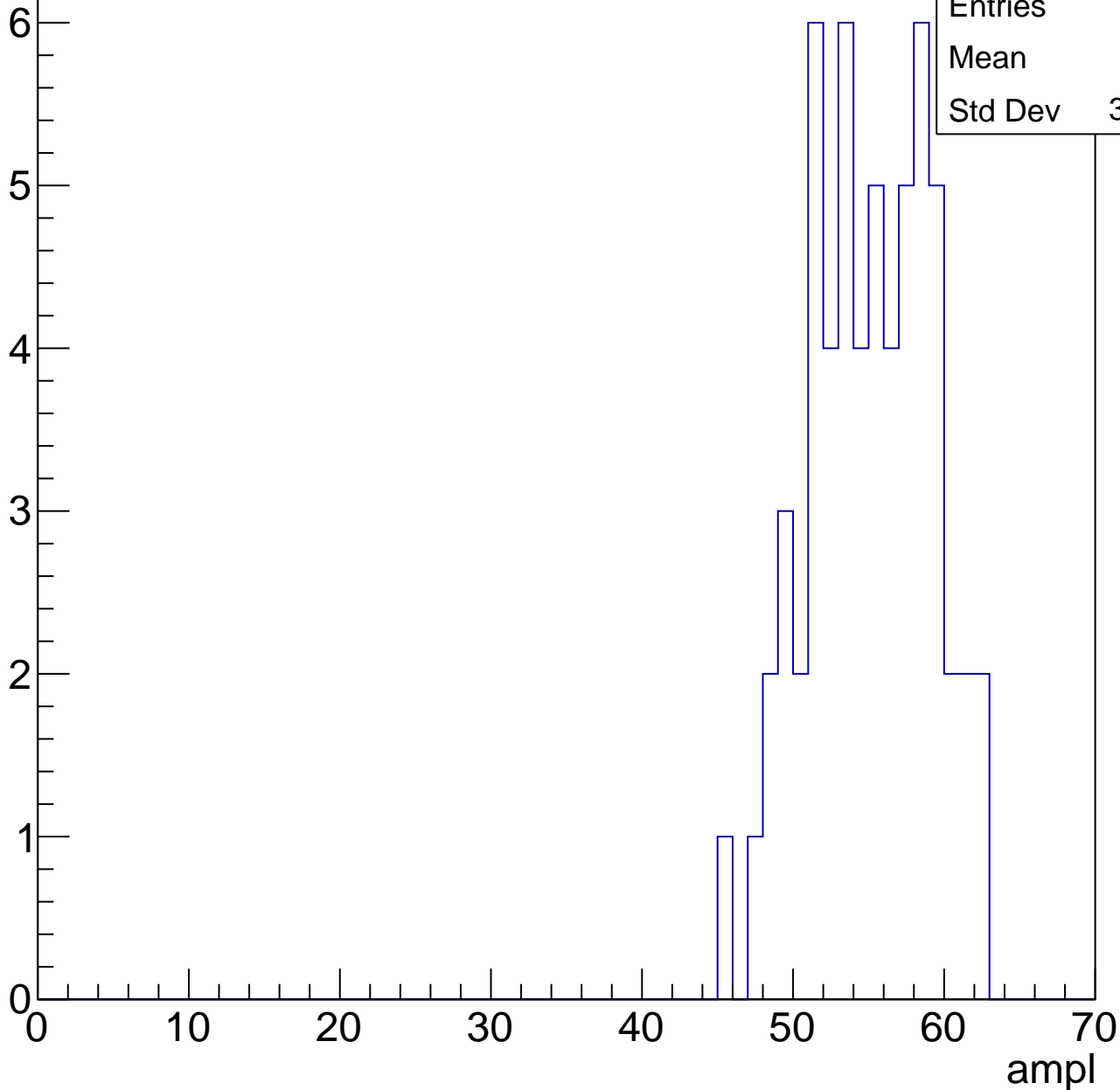


B1L103S, U5-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.6
Std Dev	3.976

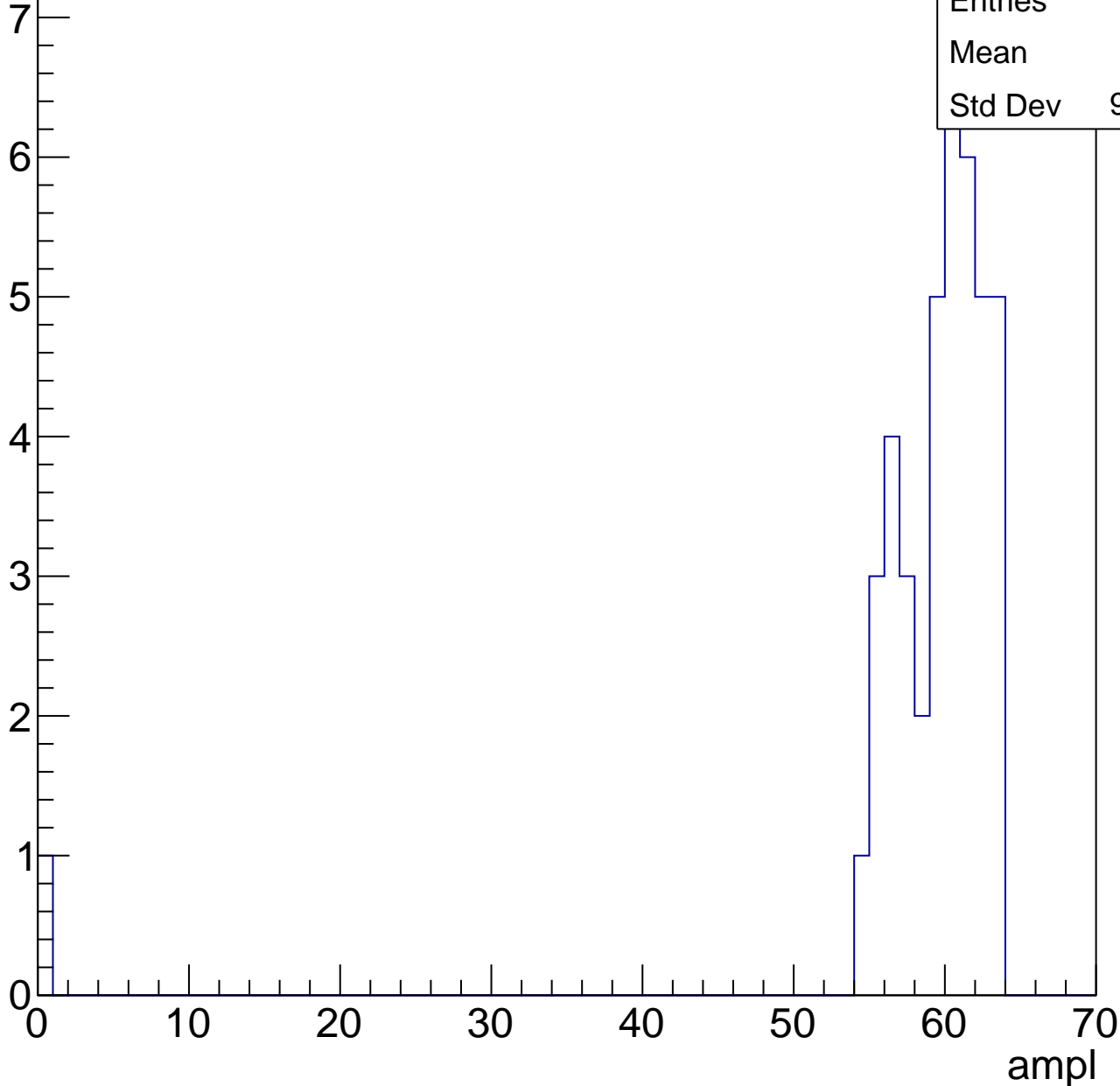


B1L103S, U5-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

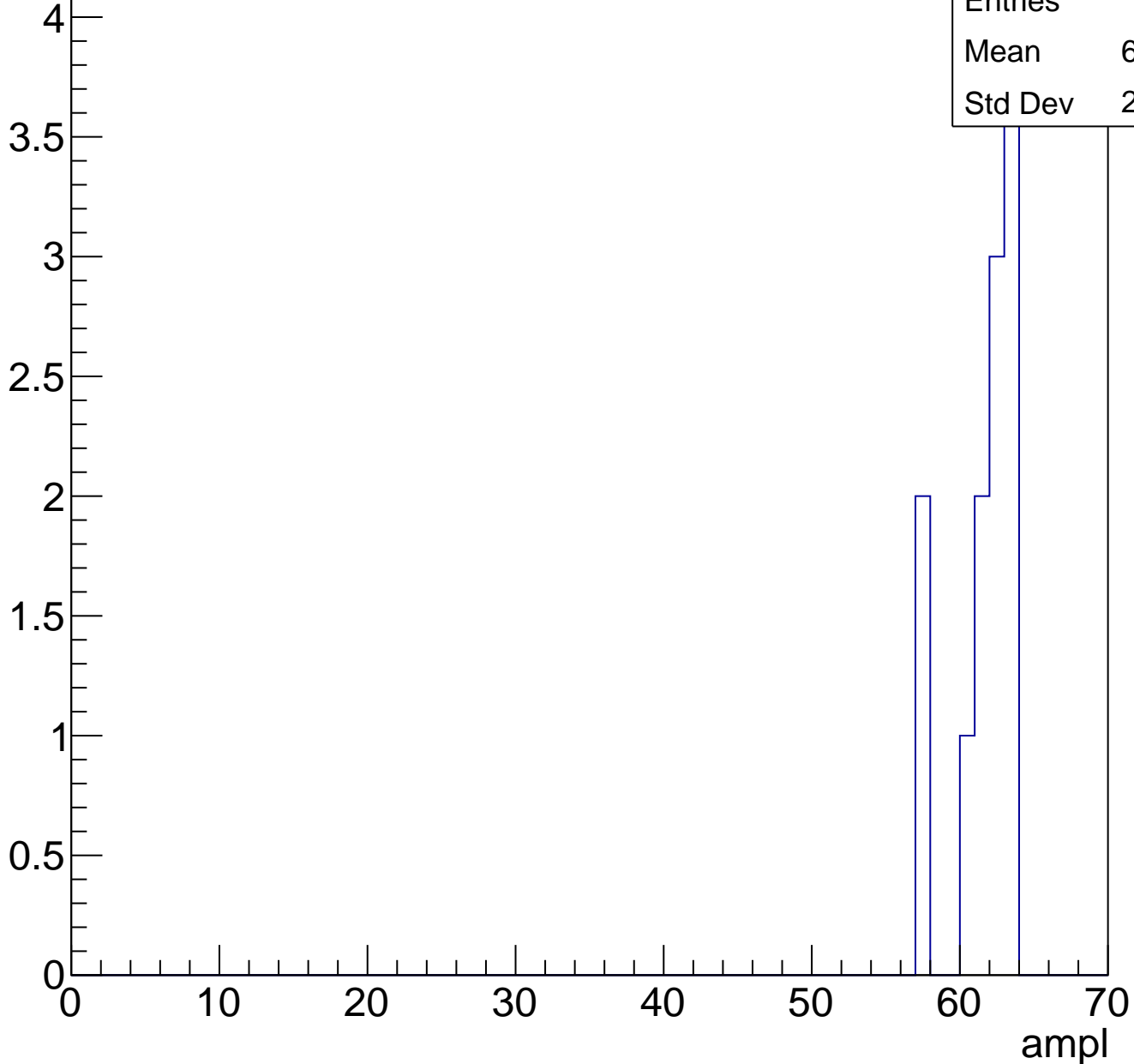
Entries	42
Mean	58
Std Dev	9.409



B1L103S, U5-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	12
Mean	61.17
Std Dev	2.075

B1L103S, U5-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L103S, U5-ch116, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	90
Mean	27.47
Std Dev	4.949

Gaus mean : 28.1182

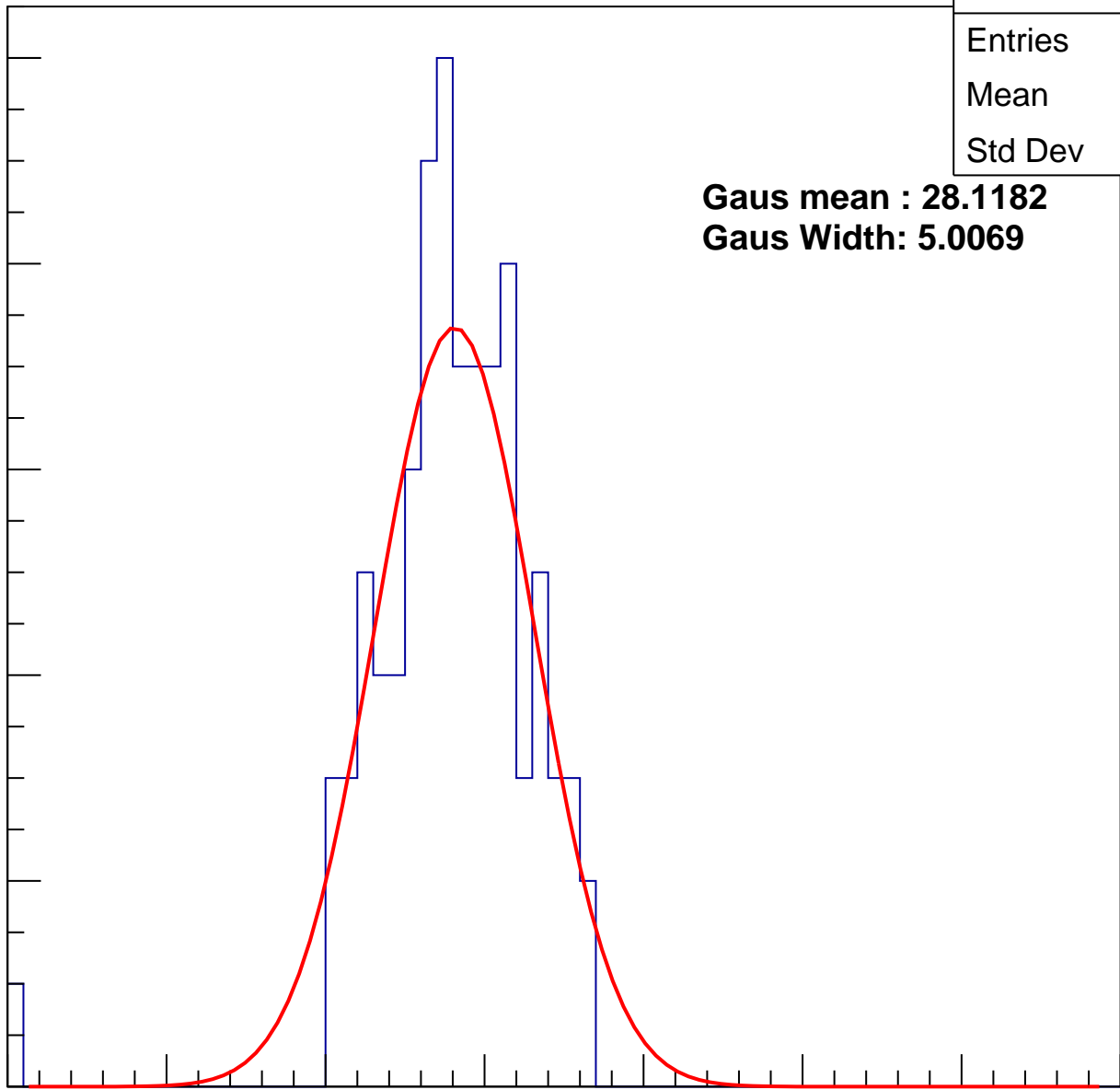
Gaus Width: 5.0069

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch116, adc1

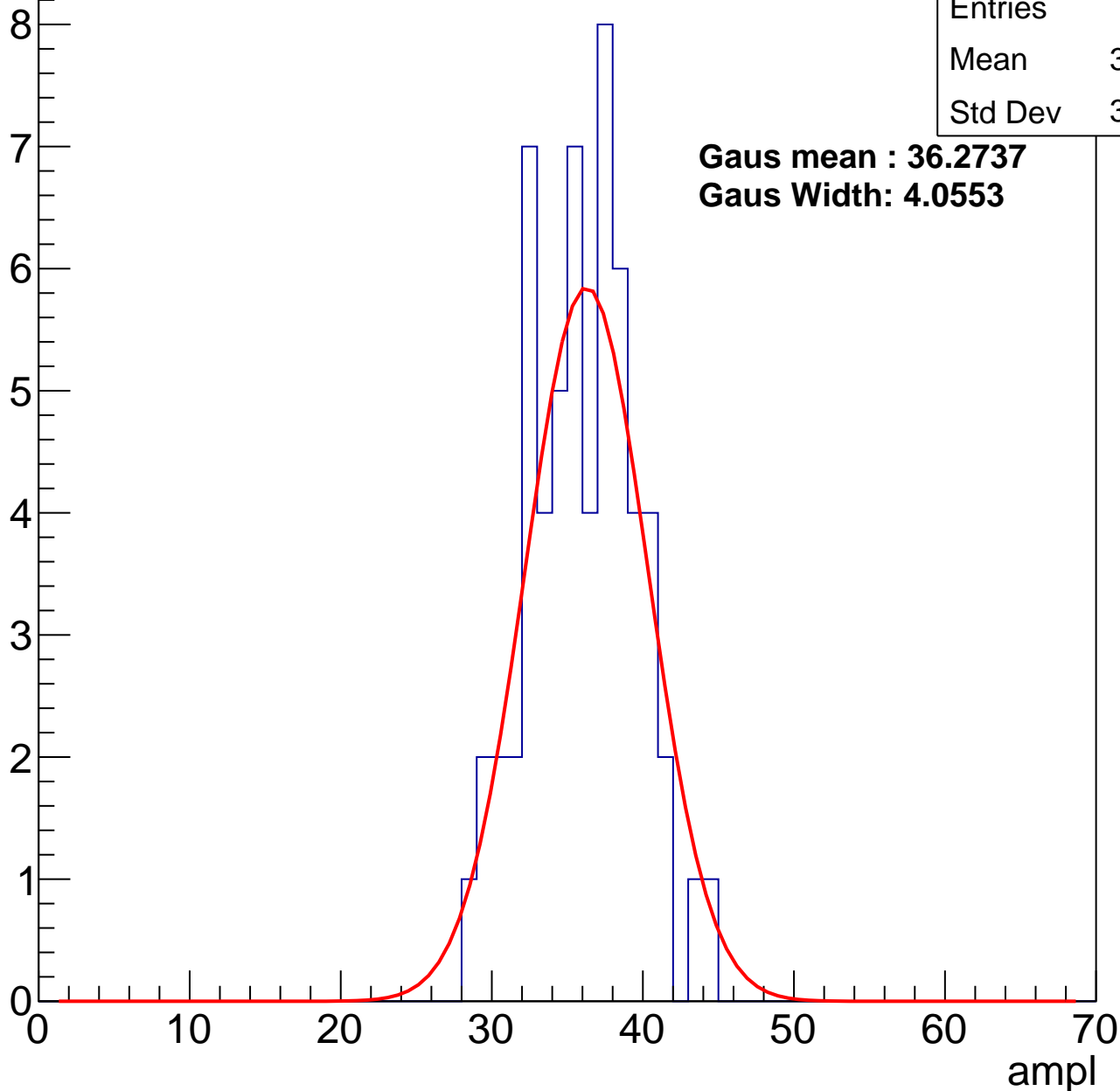
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.53
Std Dev	3.514

Gaus mean : 36.2737

Gaus Width: 4.0553



B1L103S, U5-ch116, adc2

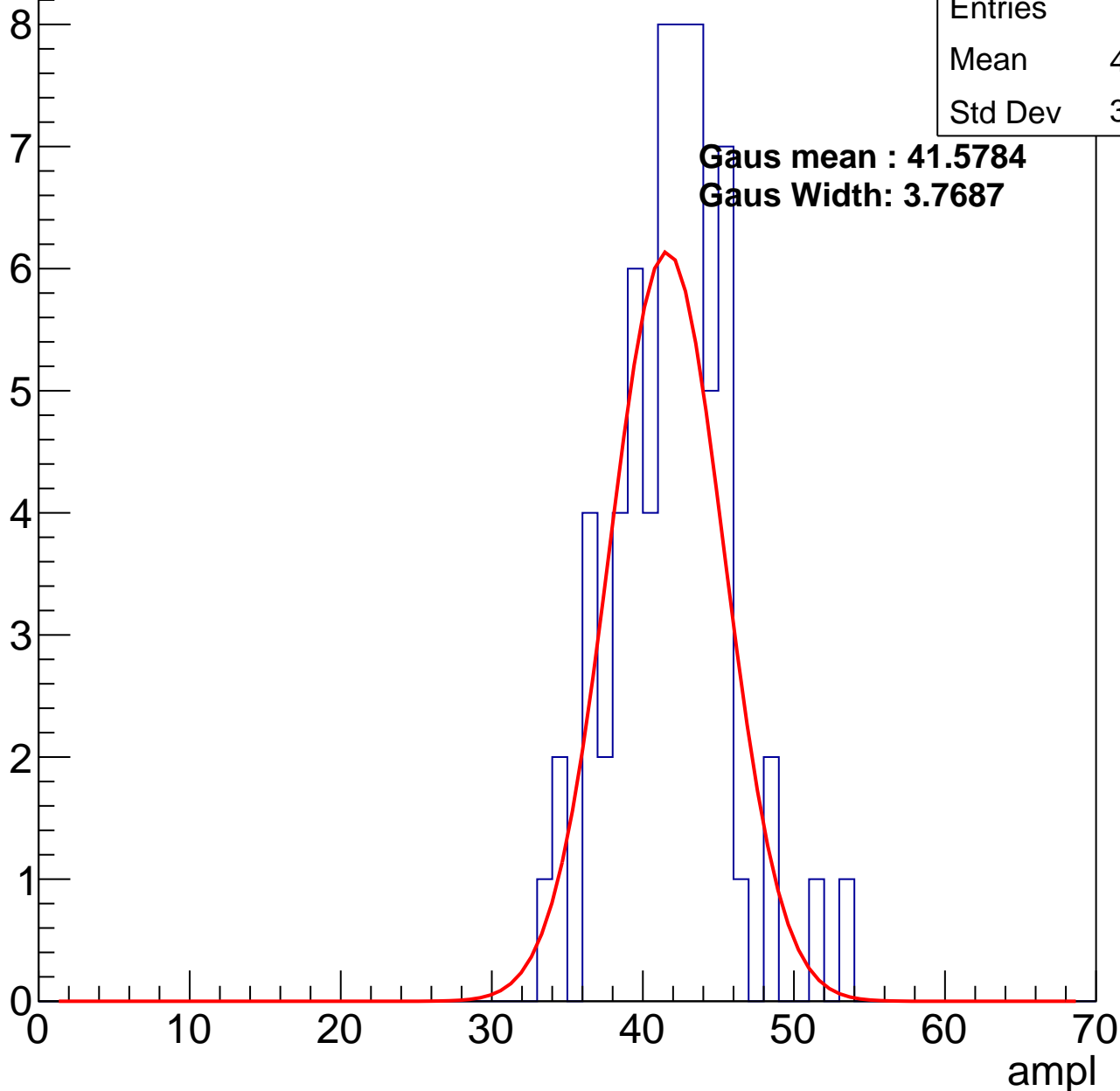
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	41.47
Std Dev	3.767

Gaus mean : 41.5784

Gaus Width: 3.7687

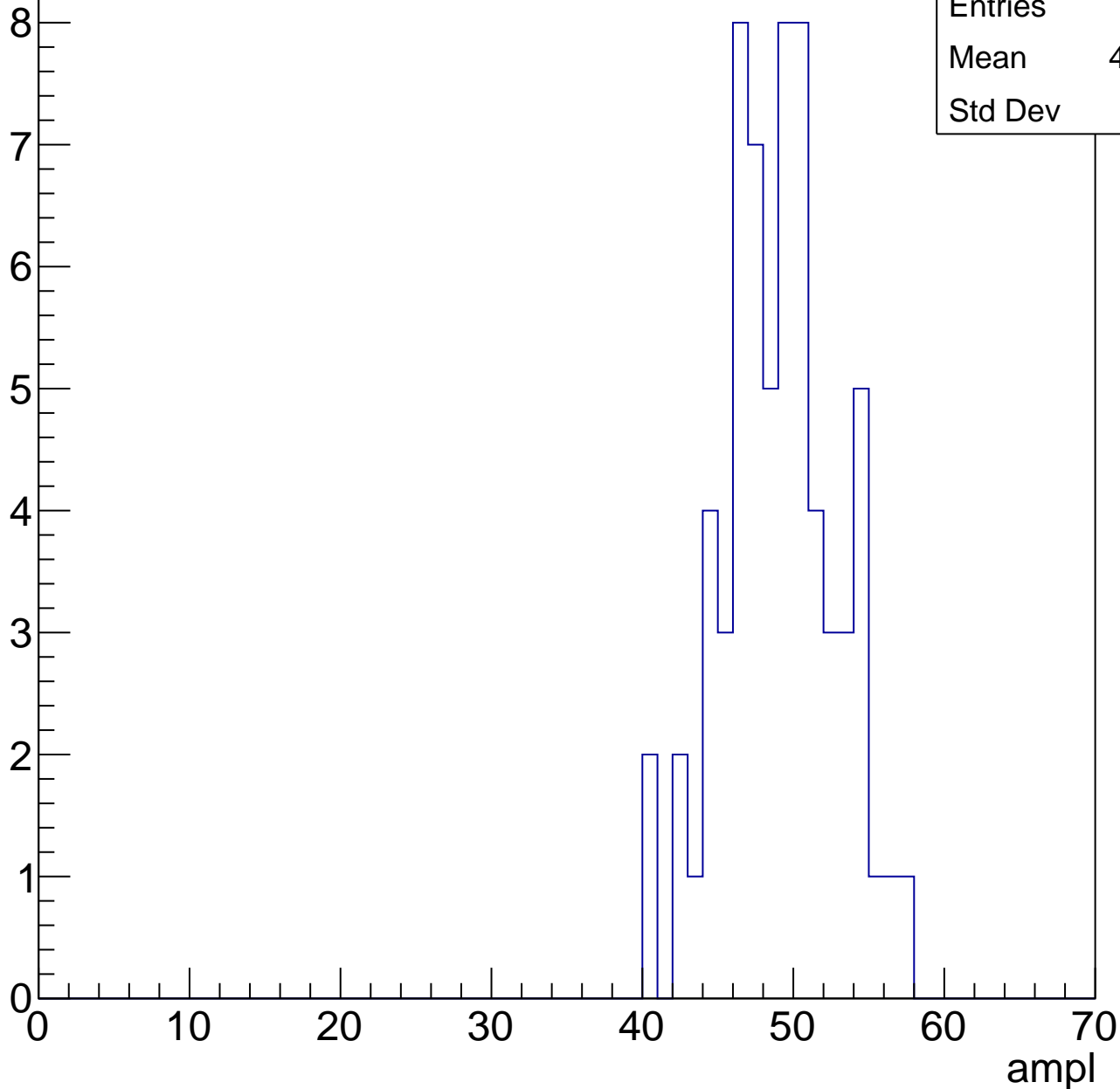


B1L103S, U5-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	48.55
Std Dev	3.71

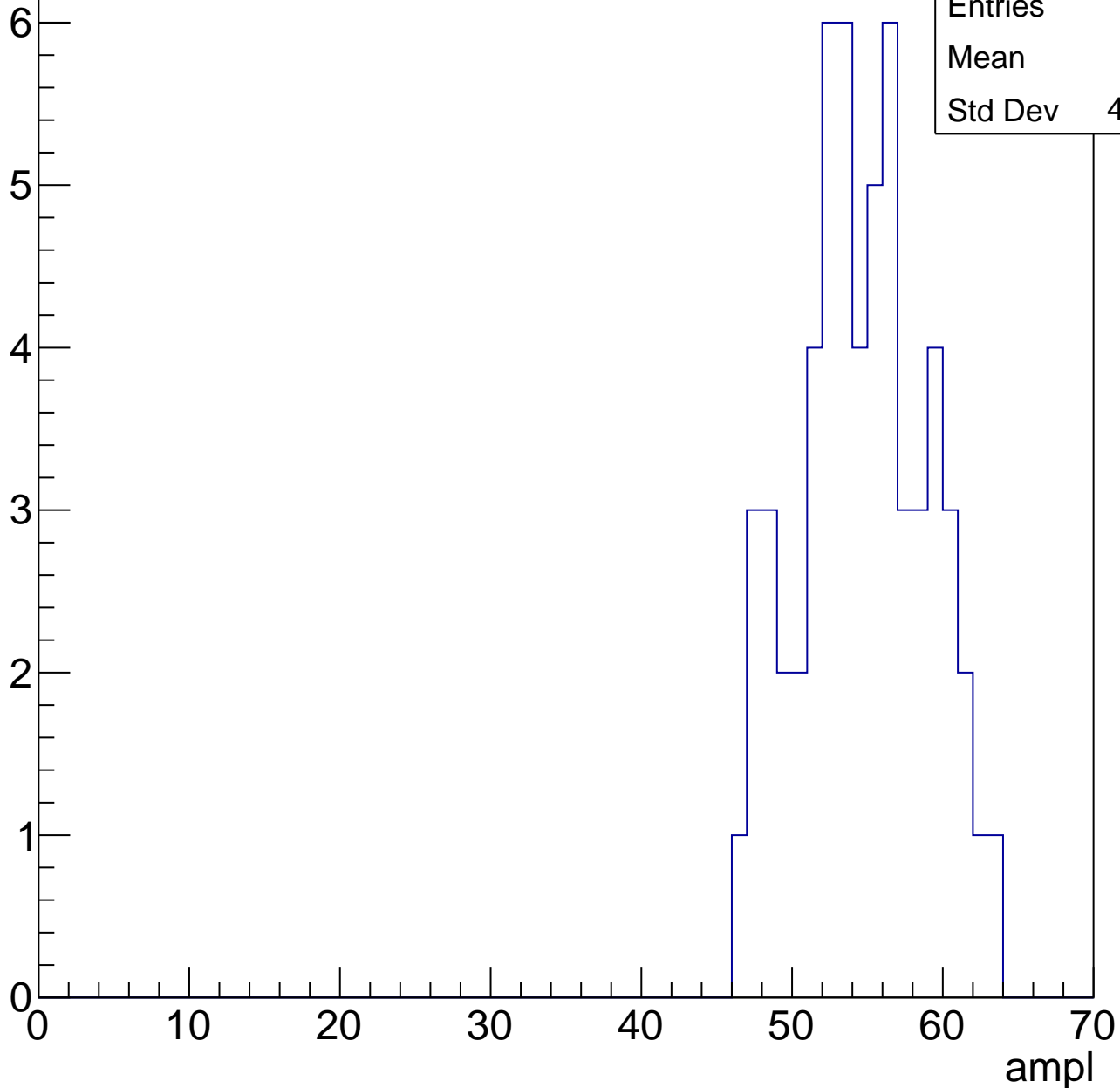


B1L103S, U5-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	54.2
Std Dev	4.157

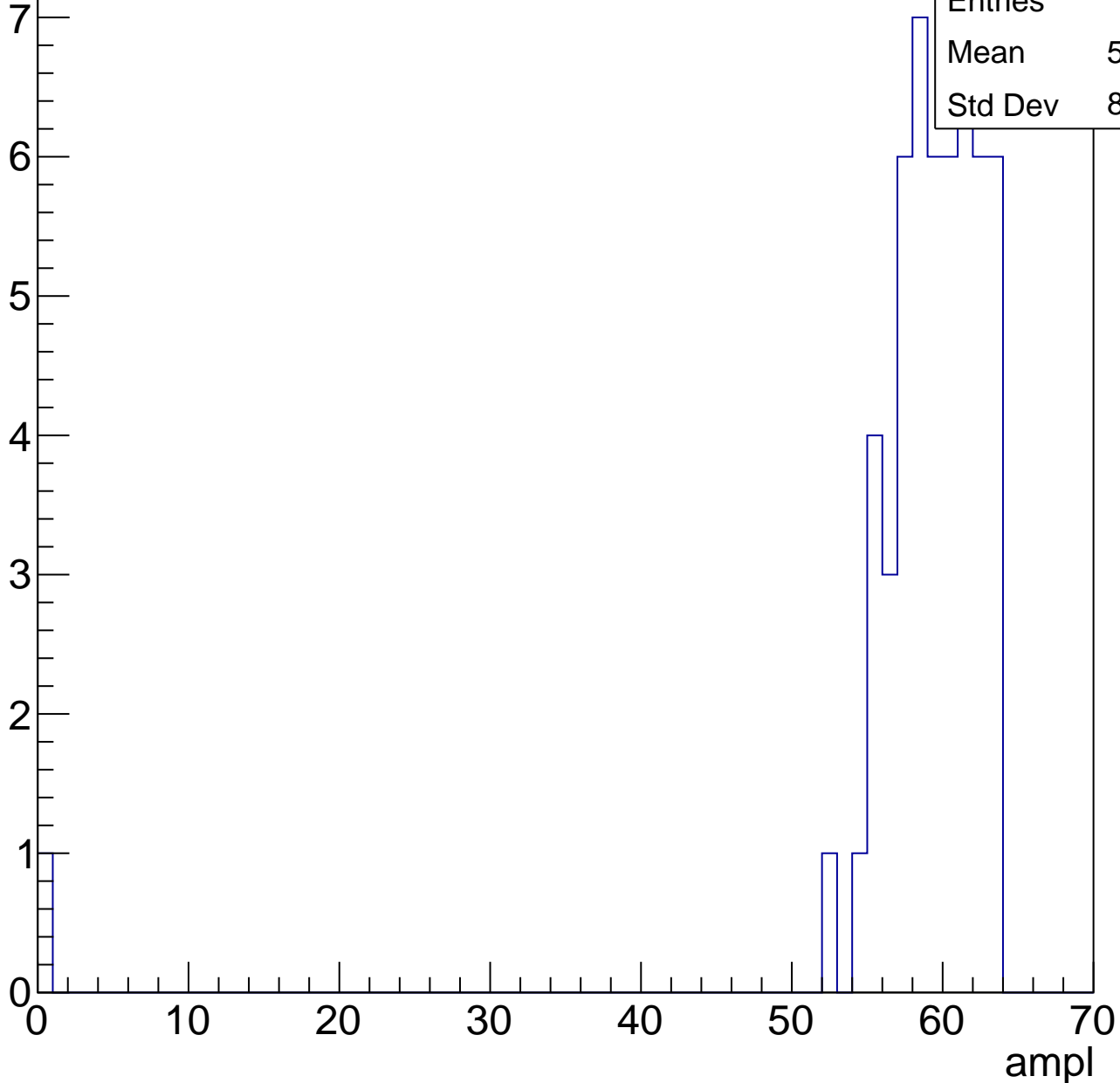


B1L103S, U5-ch116, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

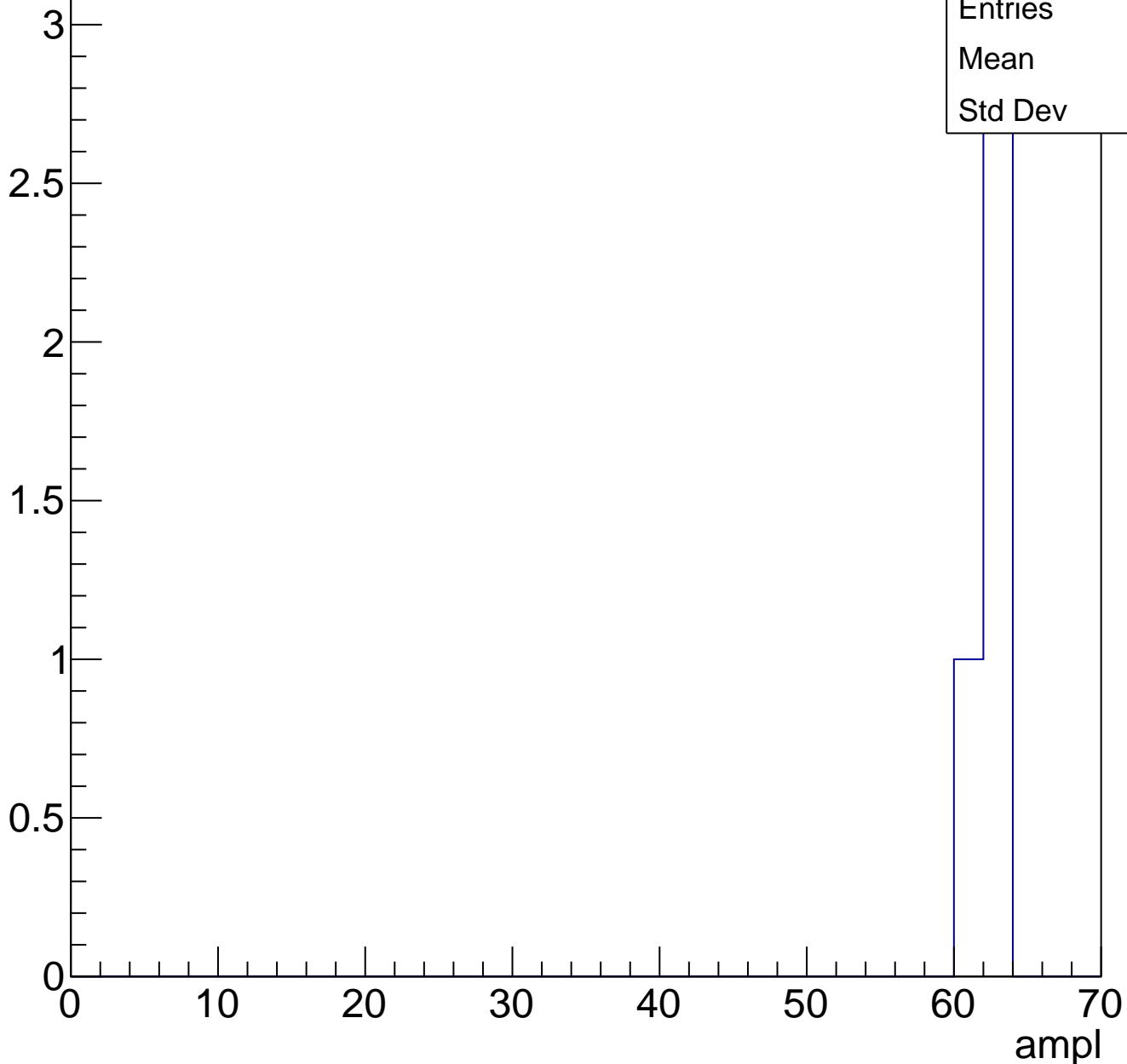
Entries	54
Mean	58.02
Std Dev	8.399



B1L103S, U5-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

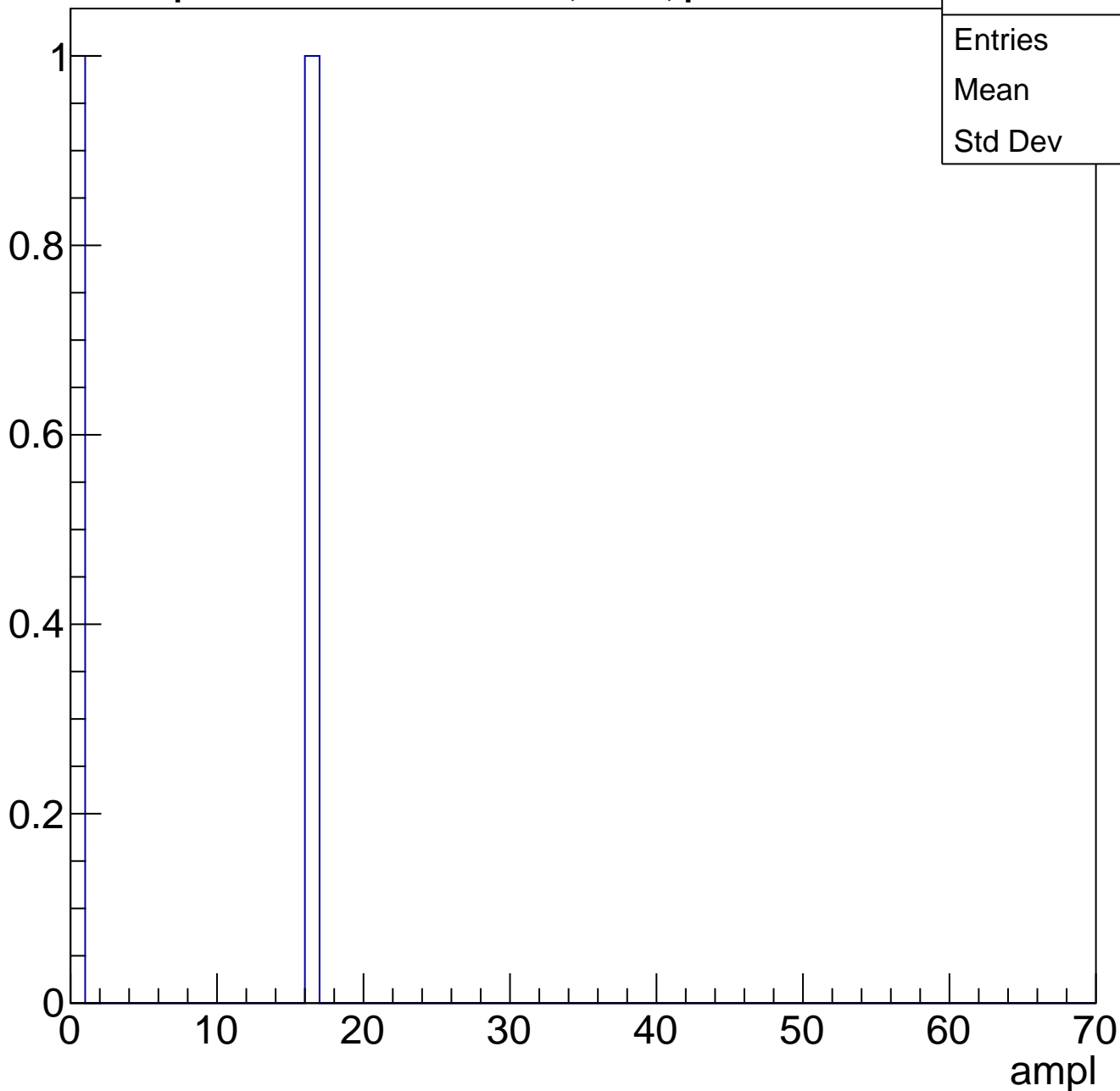
Entry



B1L103S, U5-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	8
Std Dev	8

B1L103S, U5-ch117, adc0

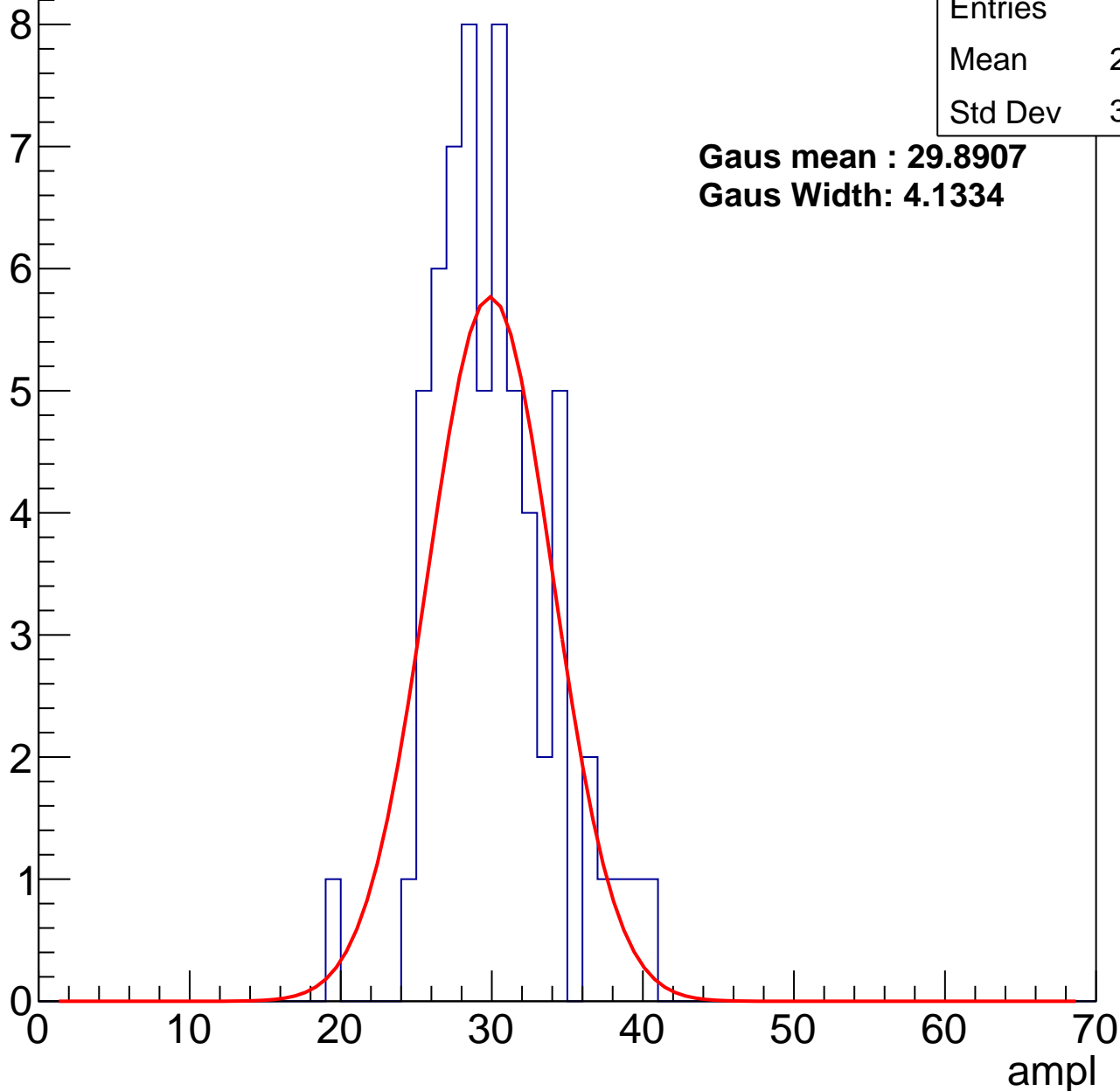
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.63
Std Dev	3.893

Gaus mean : 29.8907

Gaus Width: 4.1334



B1L103S, U5-ch117, adc1

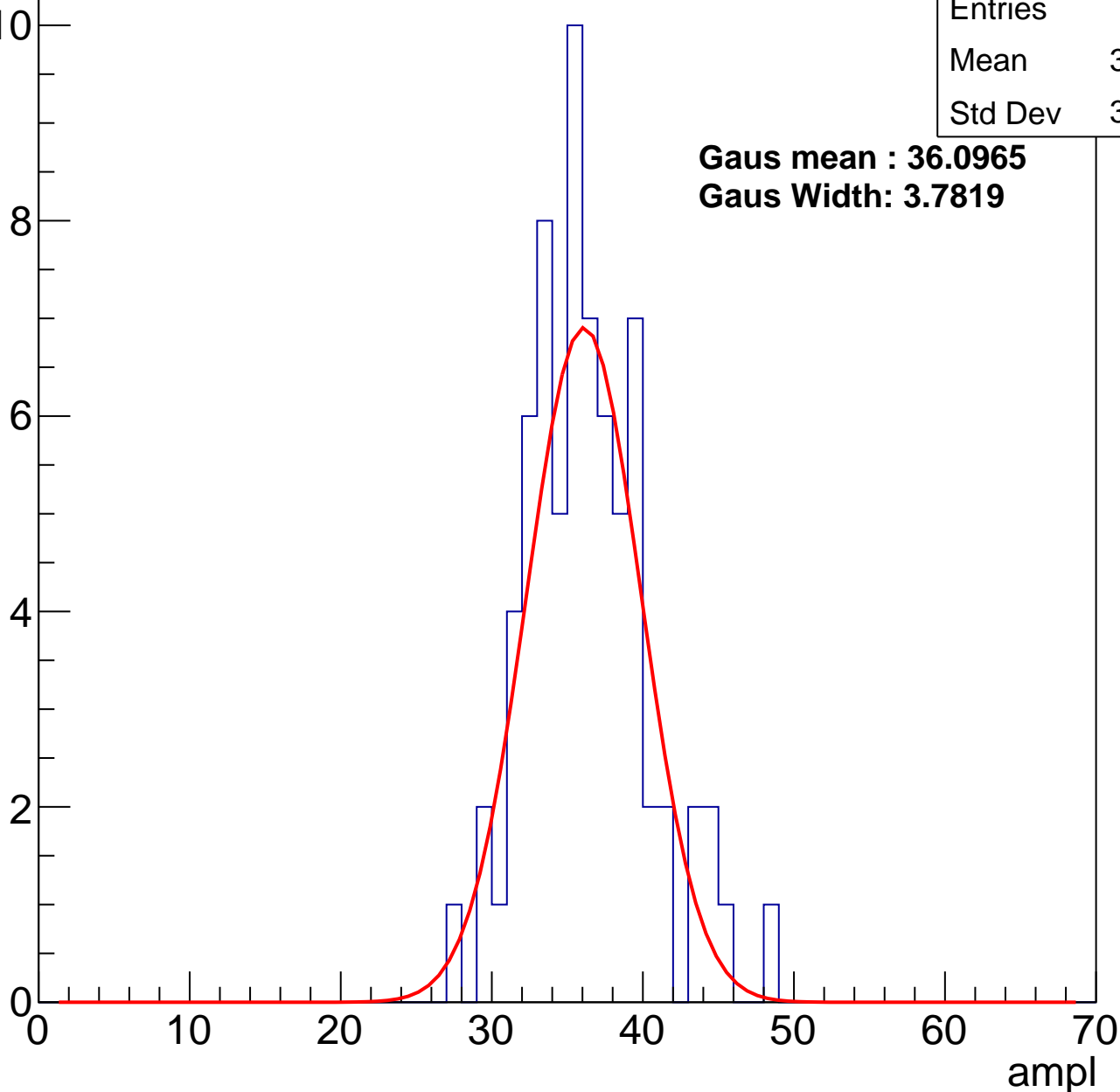
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	35.85
Std Dev	3.967

Gaus mean : 36.0965

Gaus Width: 3.7819



B1L103S, U5-ch117, adc2

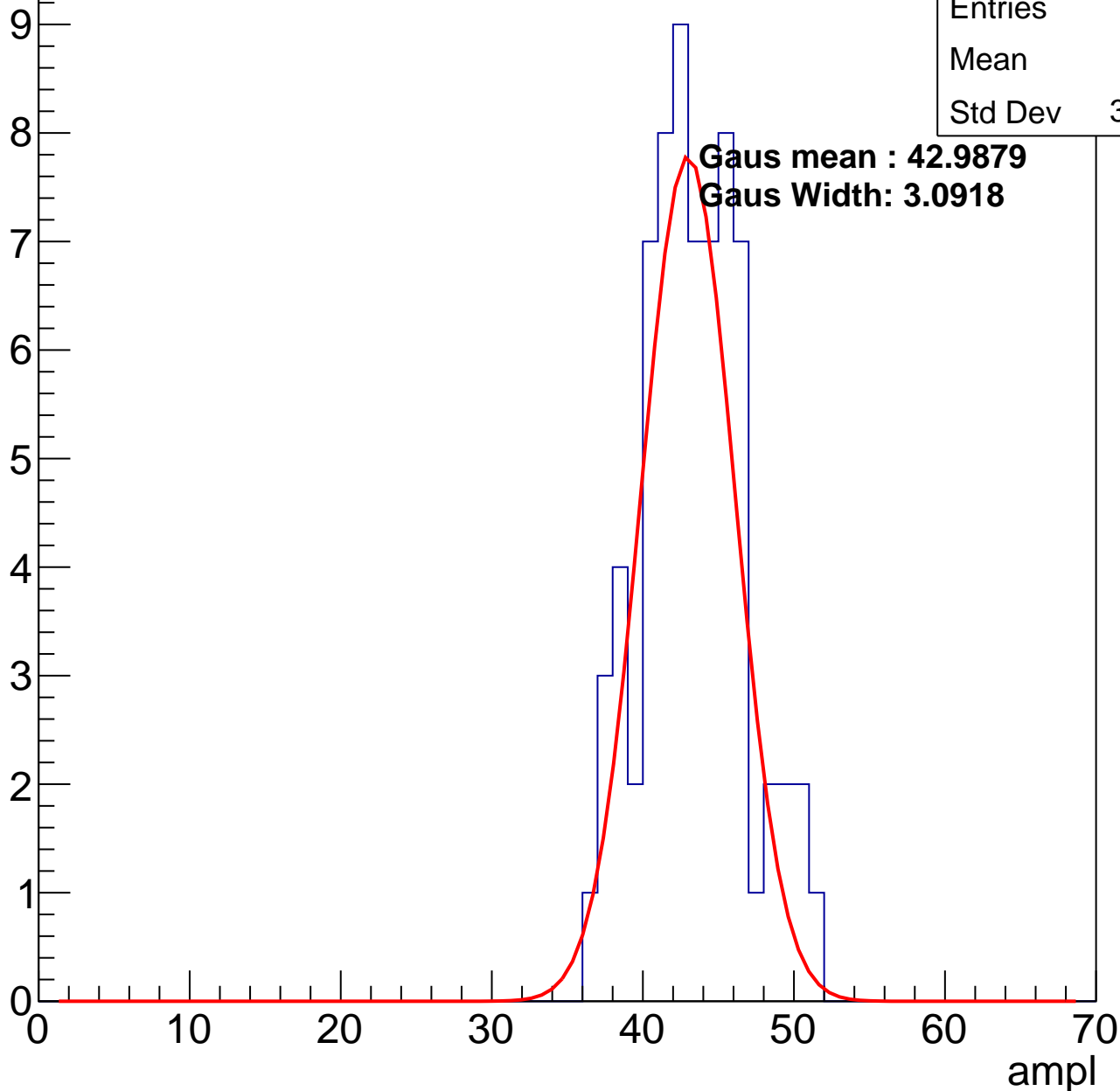
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.9
Std Dev	3.345

Gaus mean : 42.9879

Gaus Width: 3.0918

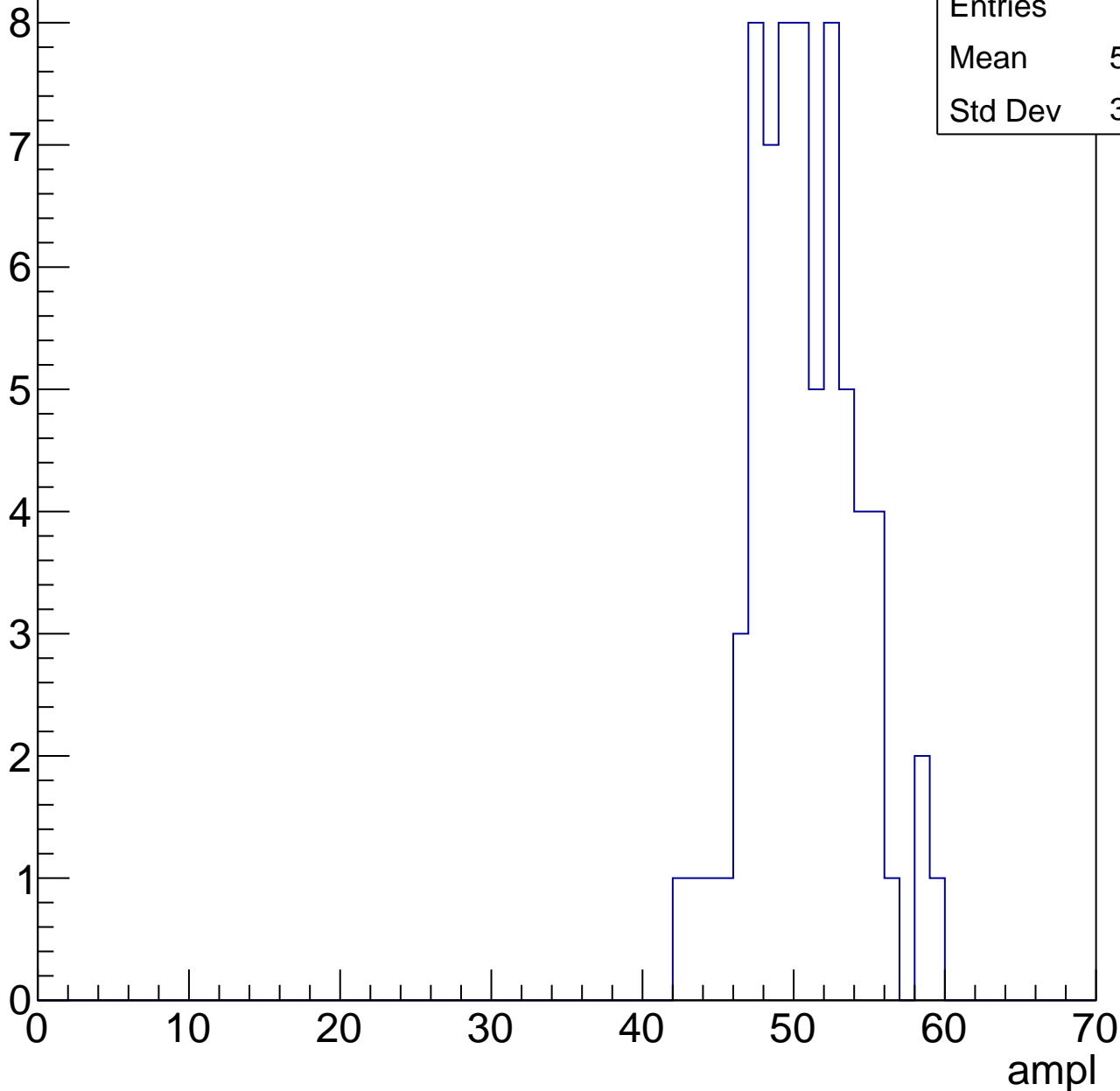


B1L103S, U5-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	50.28
Std Dev	3.463

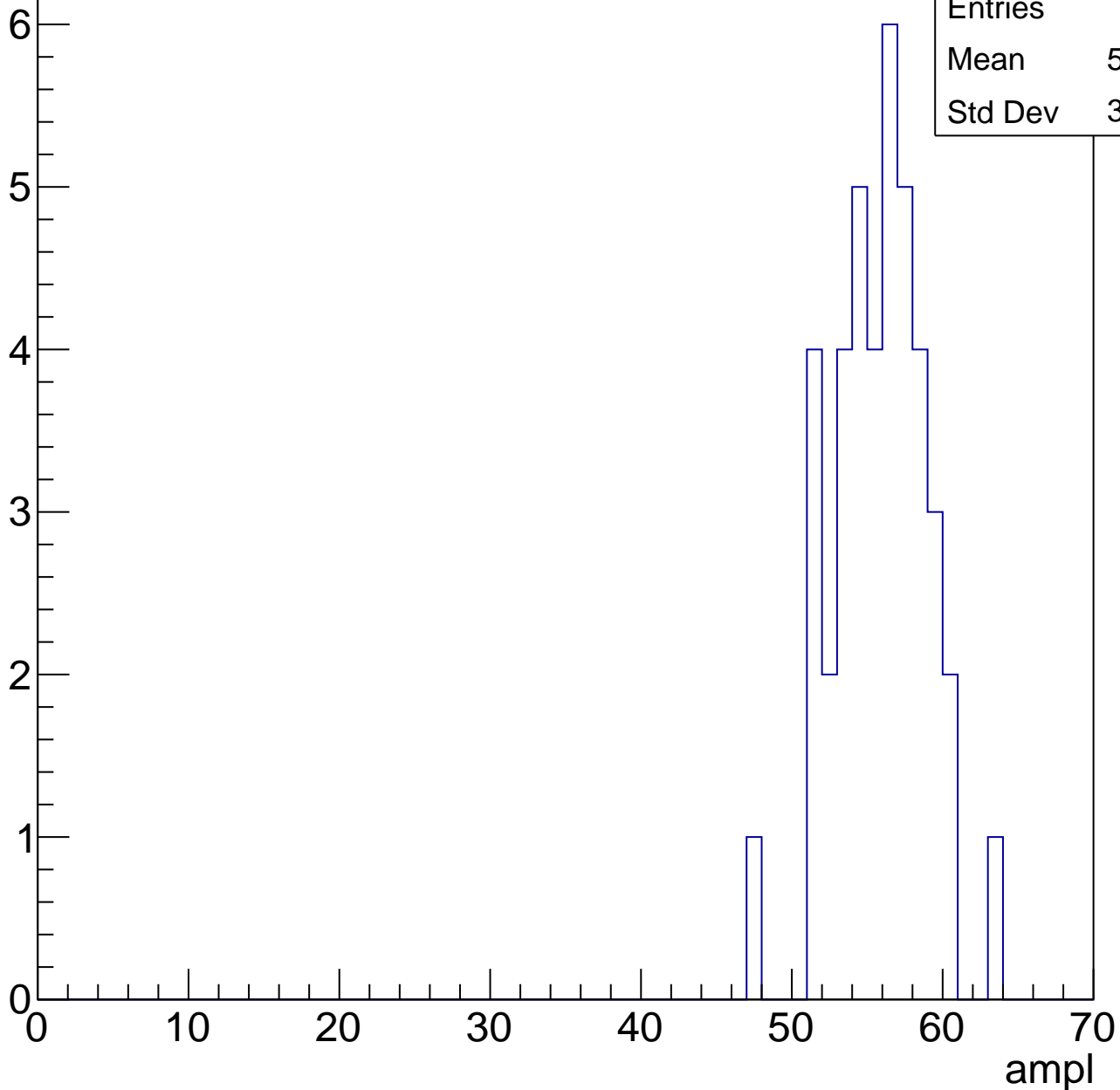


B1L103S, U5-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	55.37
Std Dev	3.066



B1L103S, U5-ch117, adc5

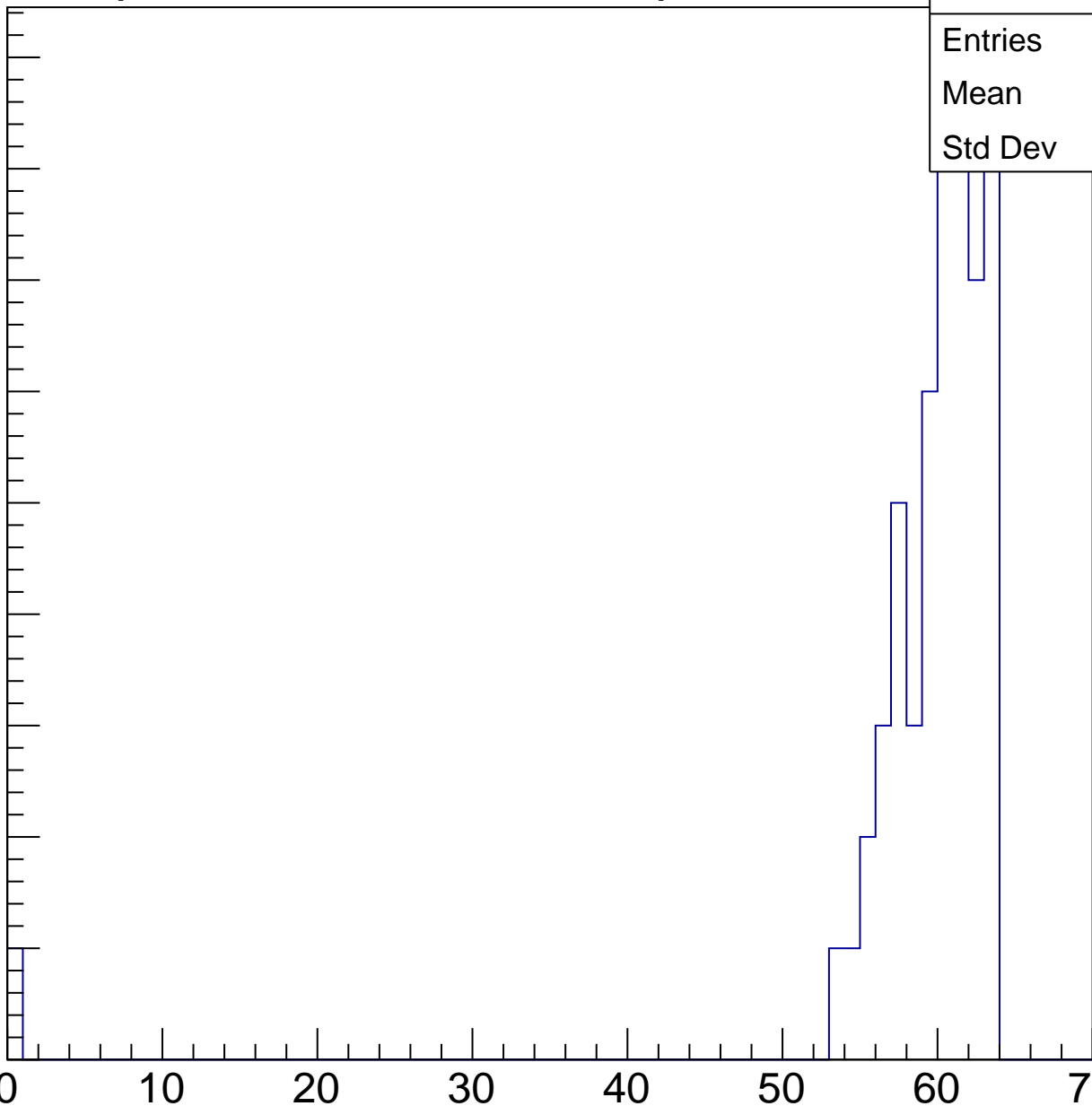
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	58.69
Std Dev	8.388

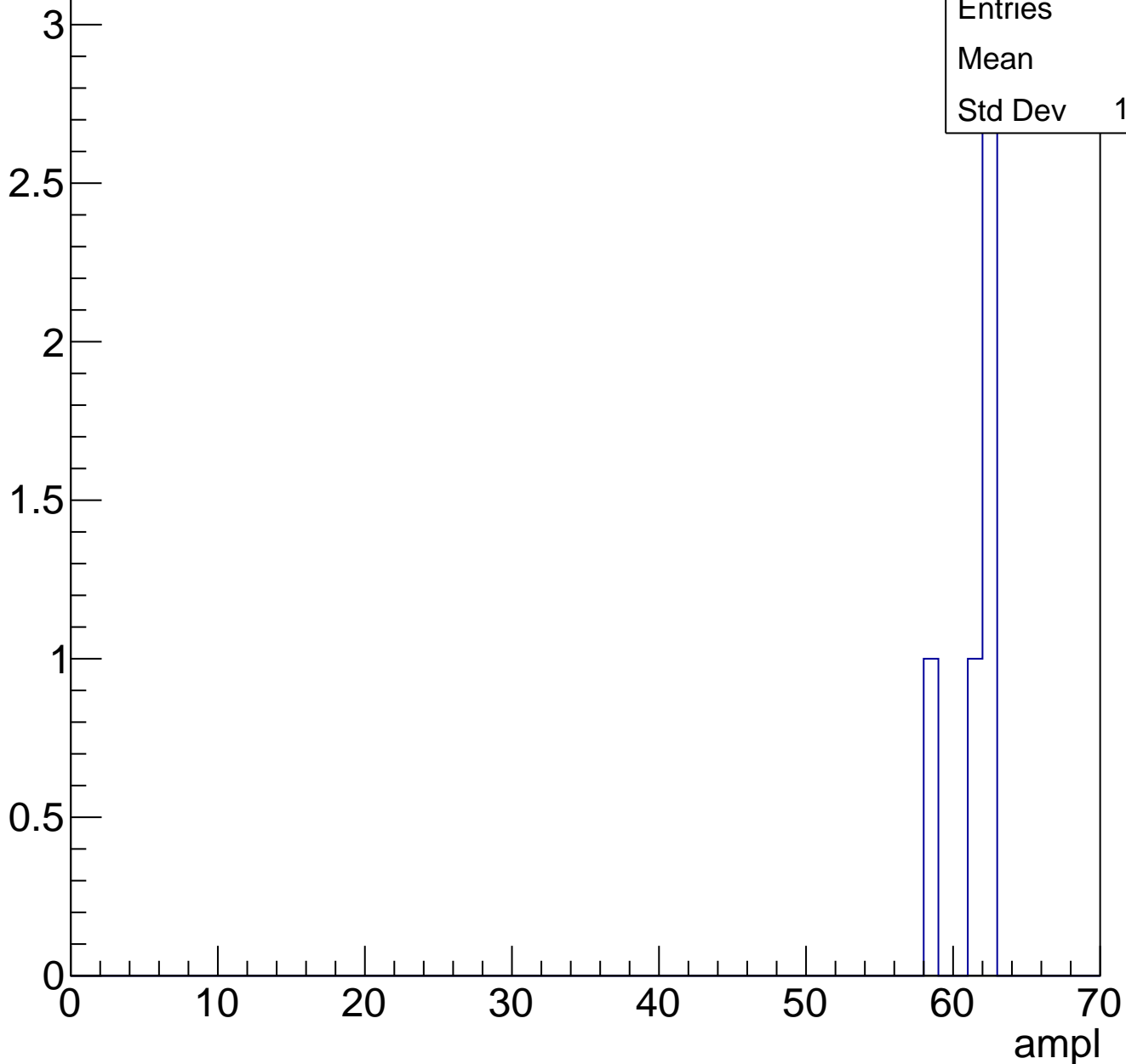
ampl



B1L103S, U5-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch118, adc0

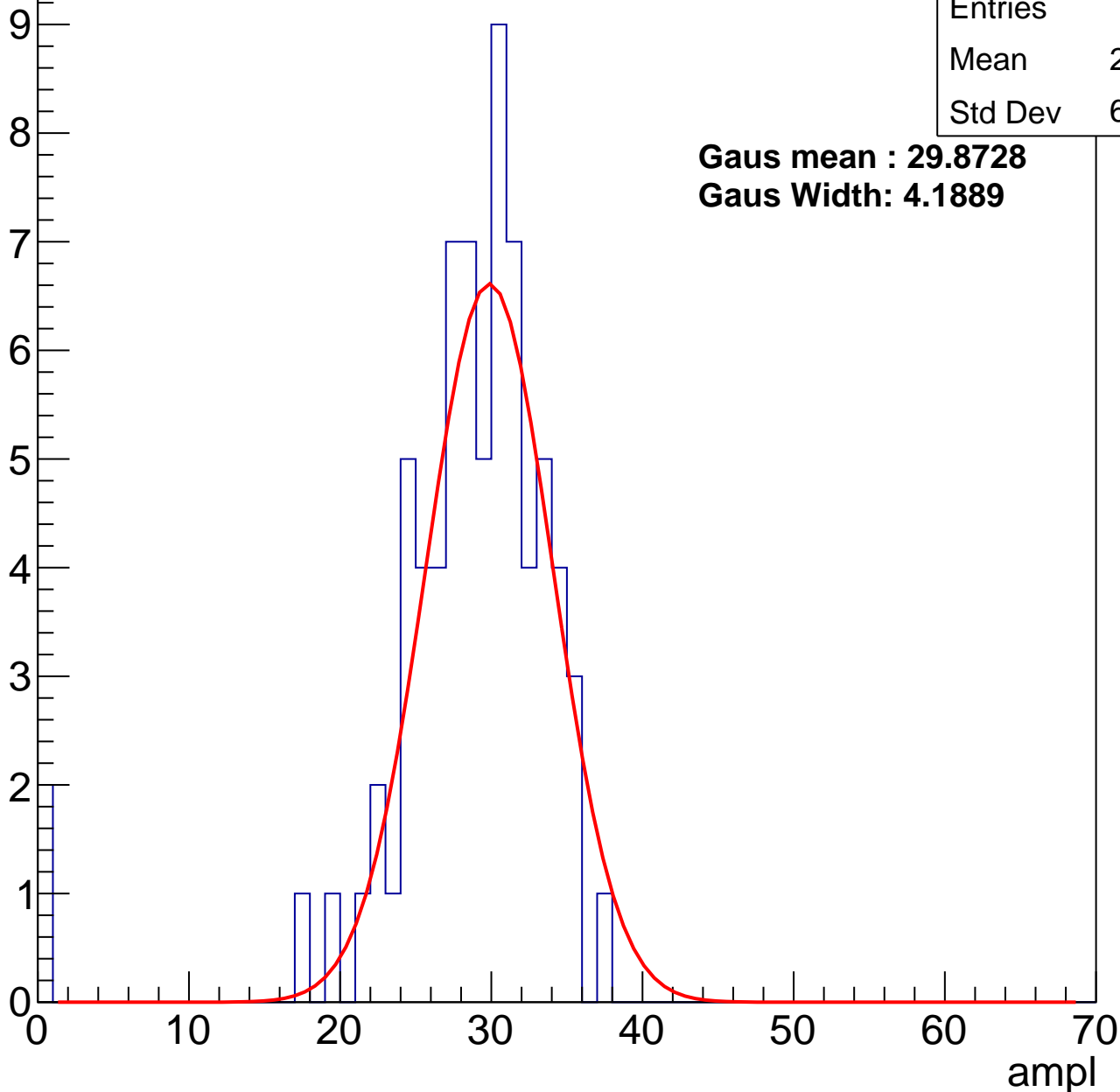
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	27.89
Std Dev	6.103

Gaus mean : 29.8728

Gaus Width: 4.1889



B1L103S, U5-ch118, adc1

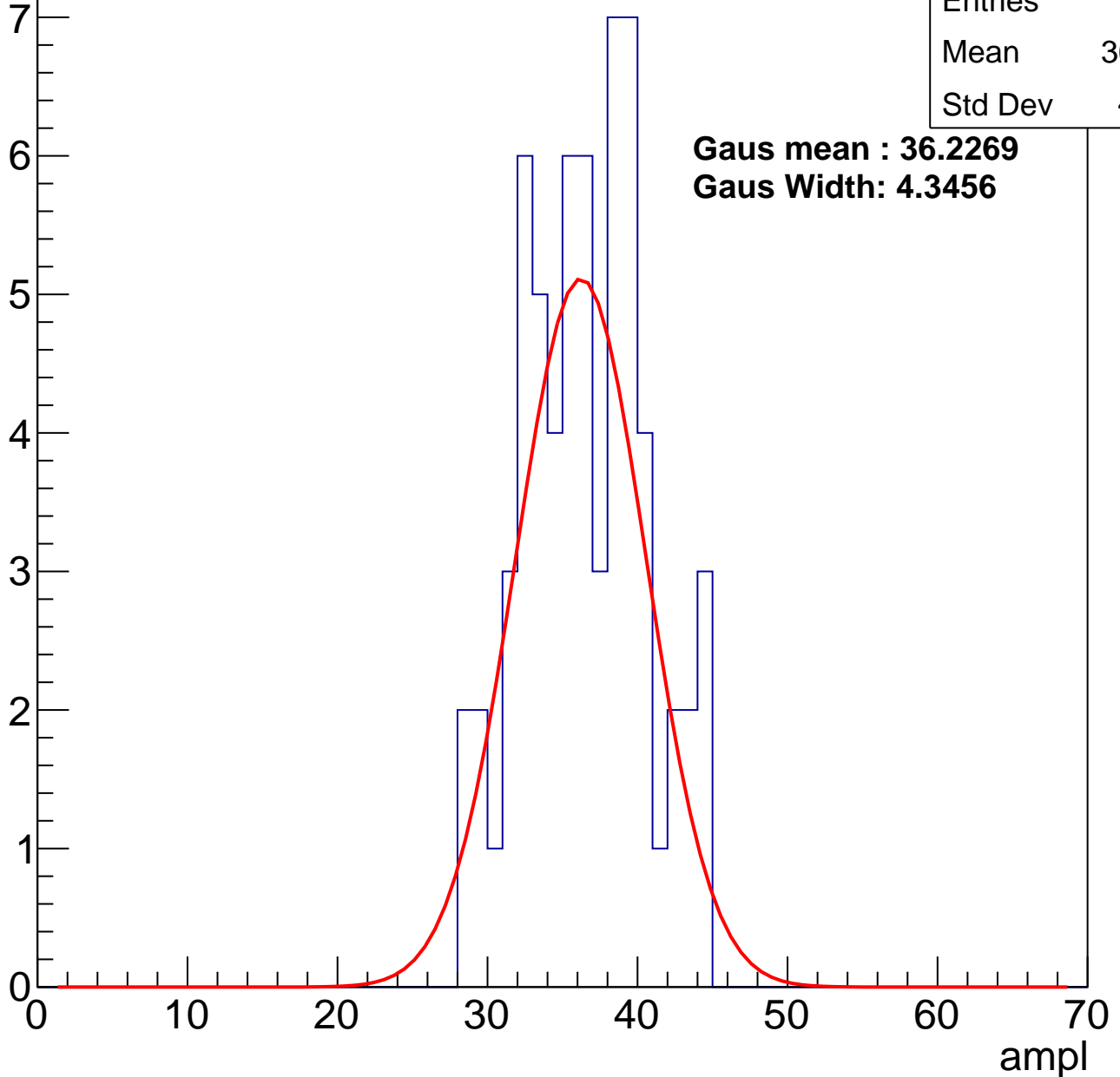
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.08
Std Dev	4.04

Gaus mean : 36.2269

Gaus Width: 4.3456



B1L103S, U5-ch118, adc2

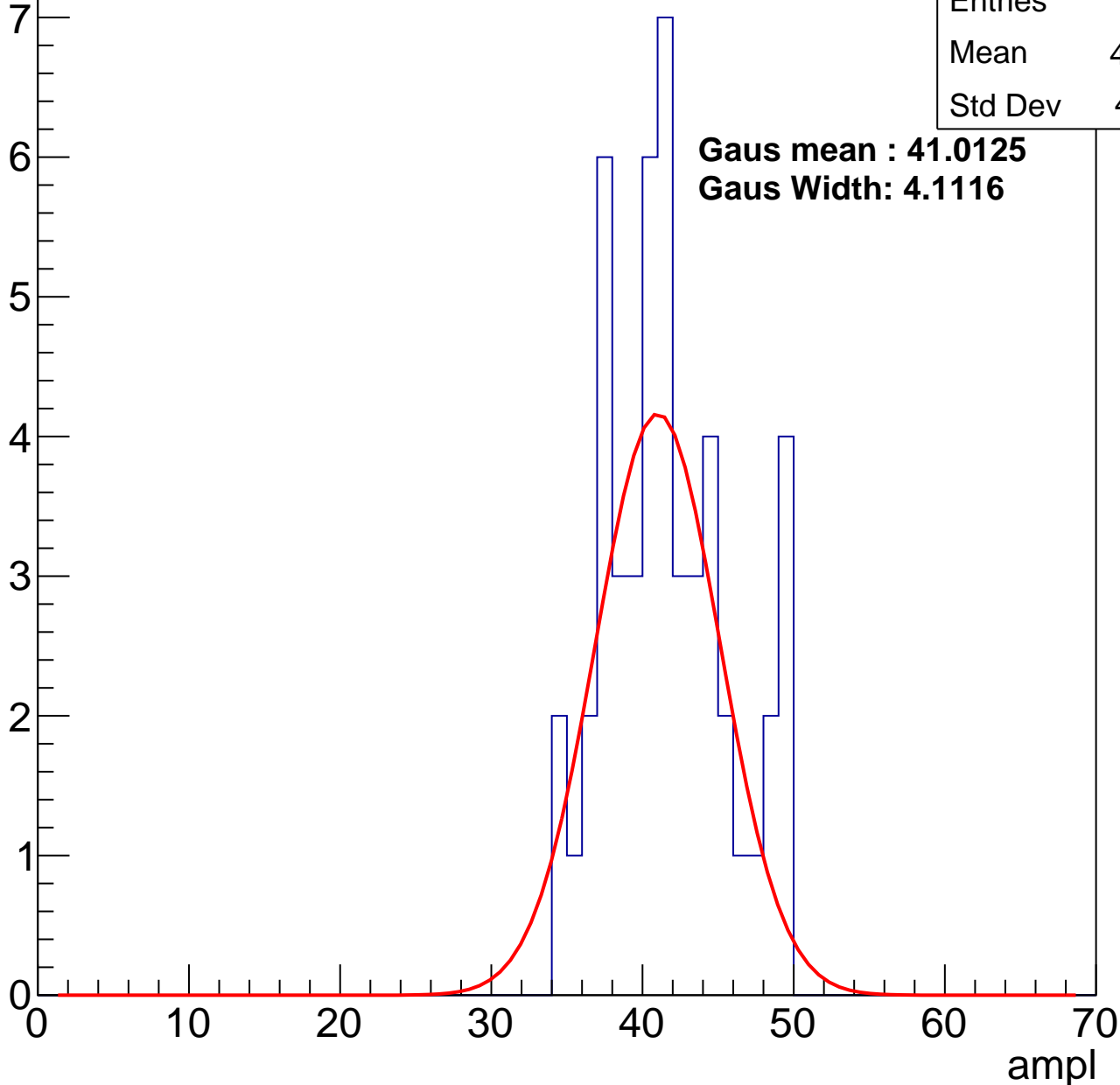
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	41.22
Std Dev	4.051

Gaus mean : 41.0125

Gaus Width: 4.1116

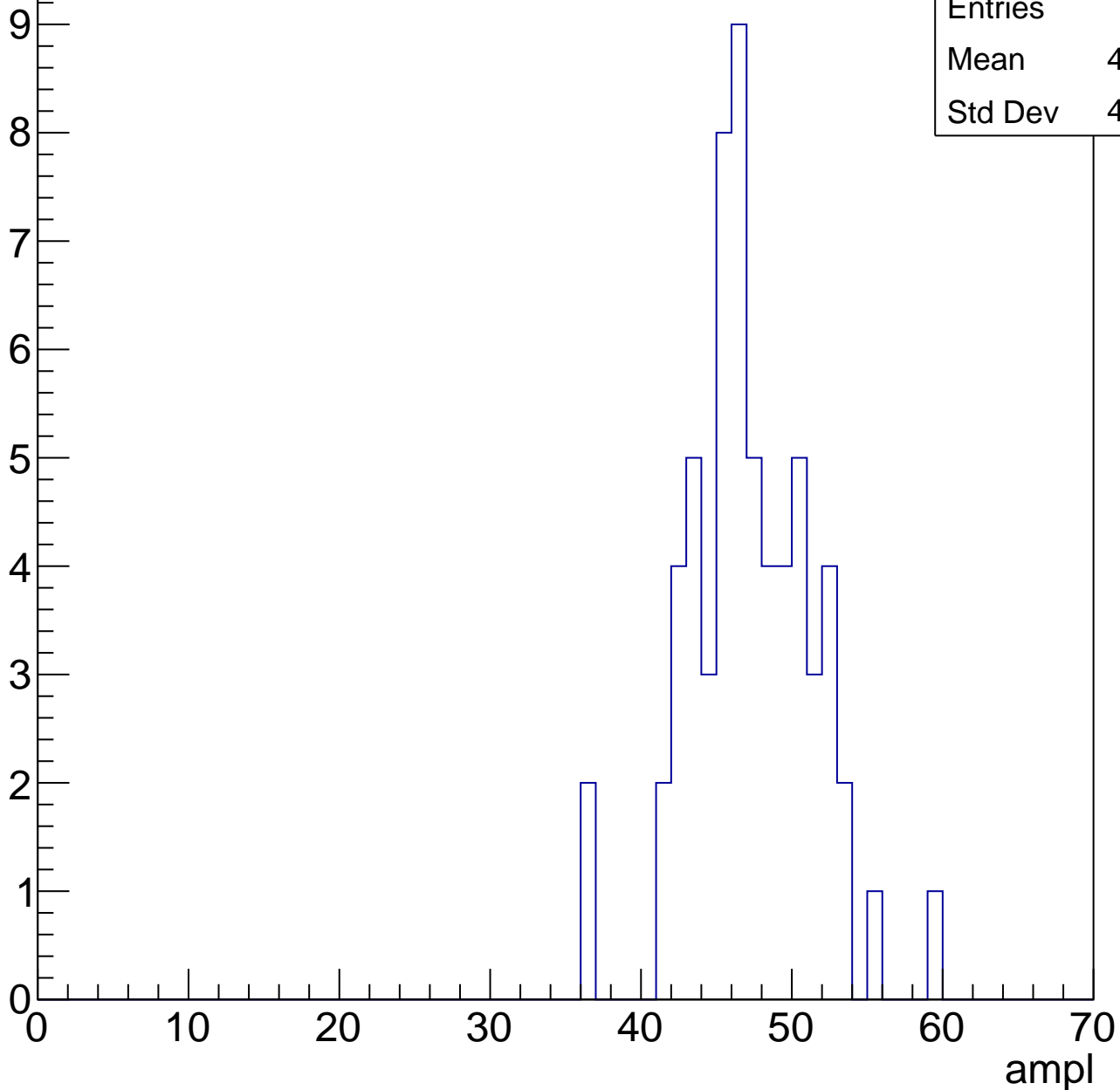


B1L103S, U5-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	46.73
Std Dev	4.116

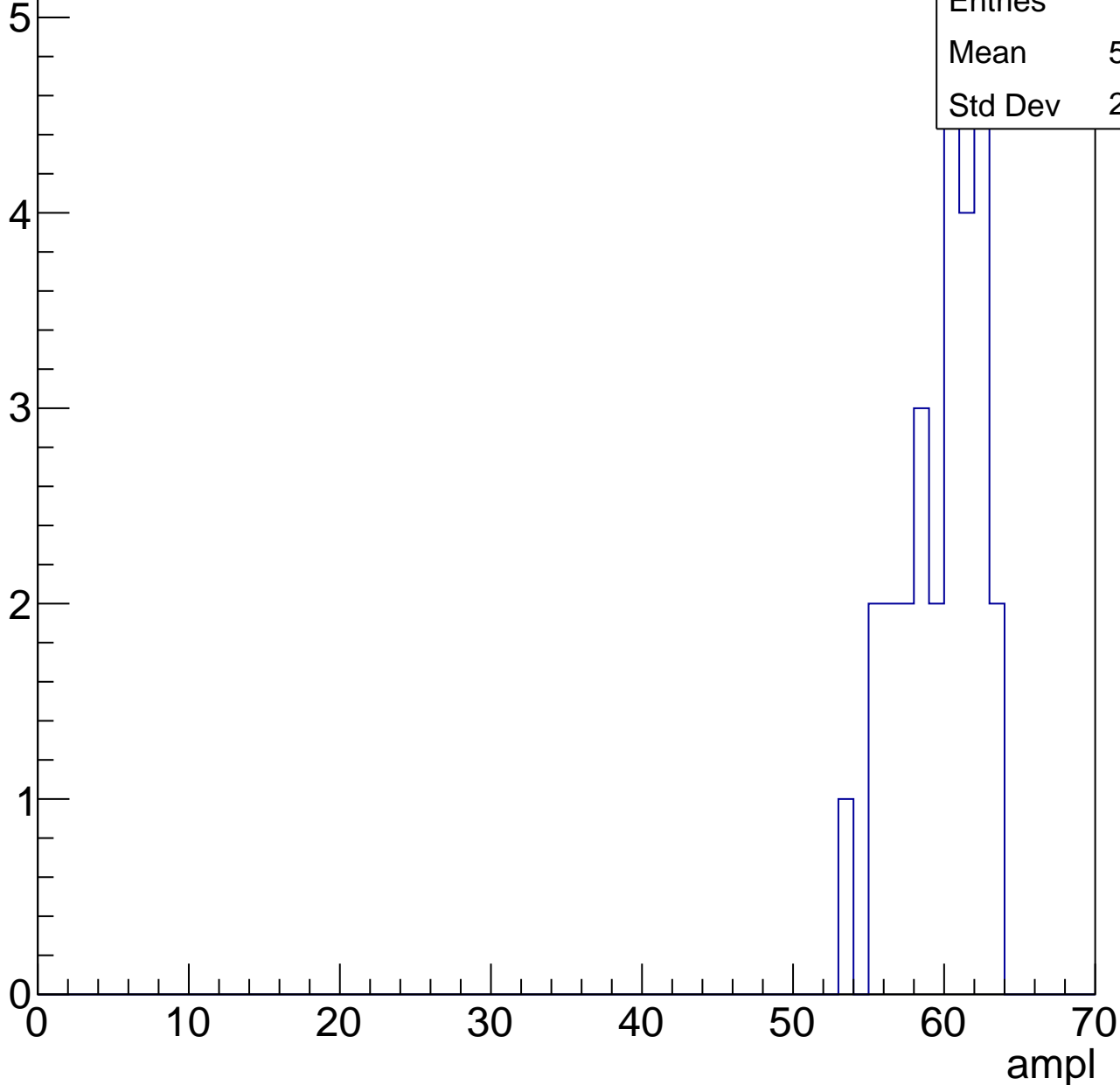


B1L103S, U5-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	59.32
Std Dev	2.619

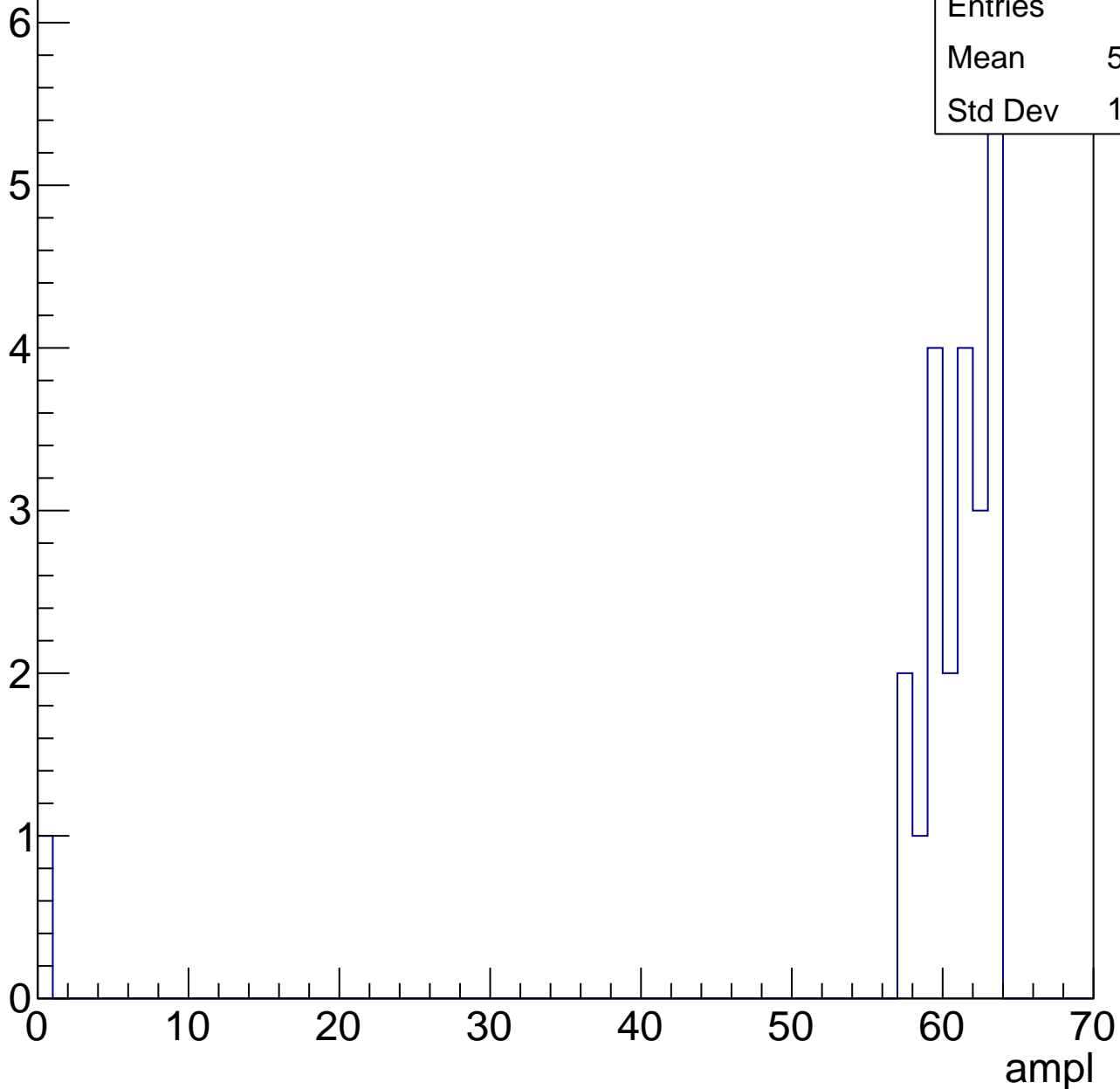


B1L103S, U5-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	23
Mean	58.09
Std Dev	12.53



B1L103S, U5-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch119, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	90
Mean	28.16
Std Dev	4.008

Gaus mean : 28.3140

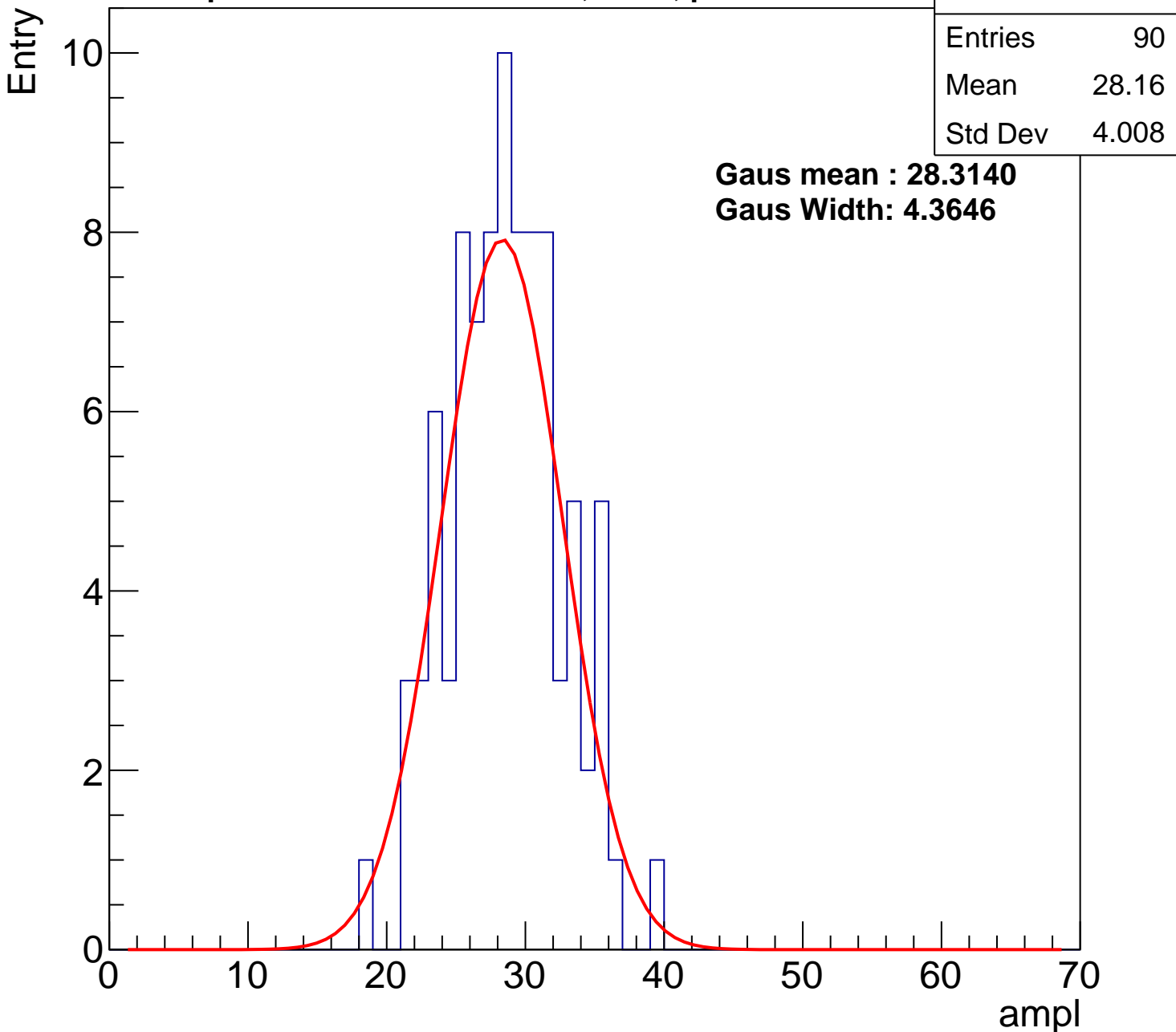
Gaus Width: 4.3646

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U5-ch119, adc1

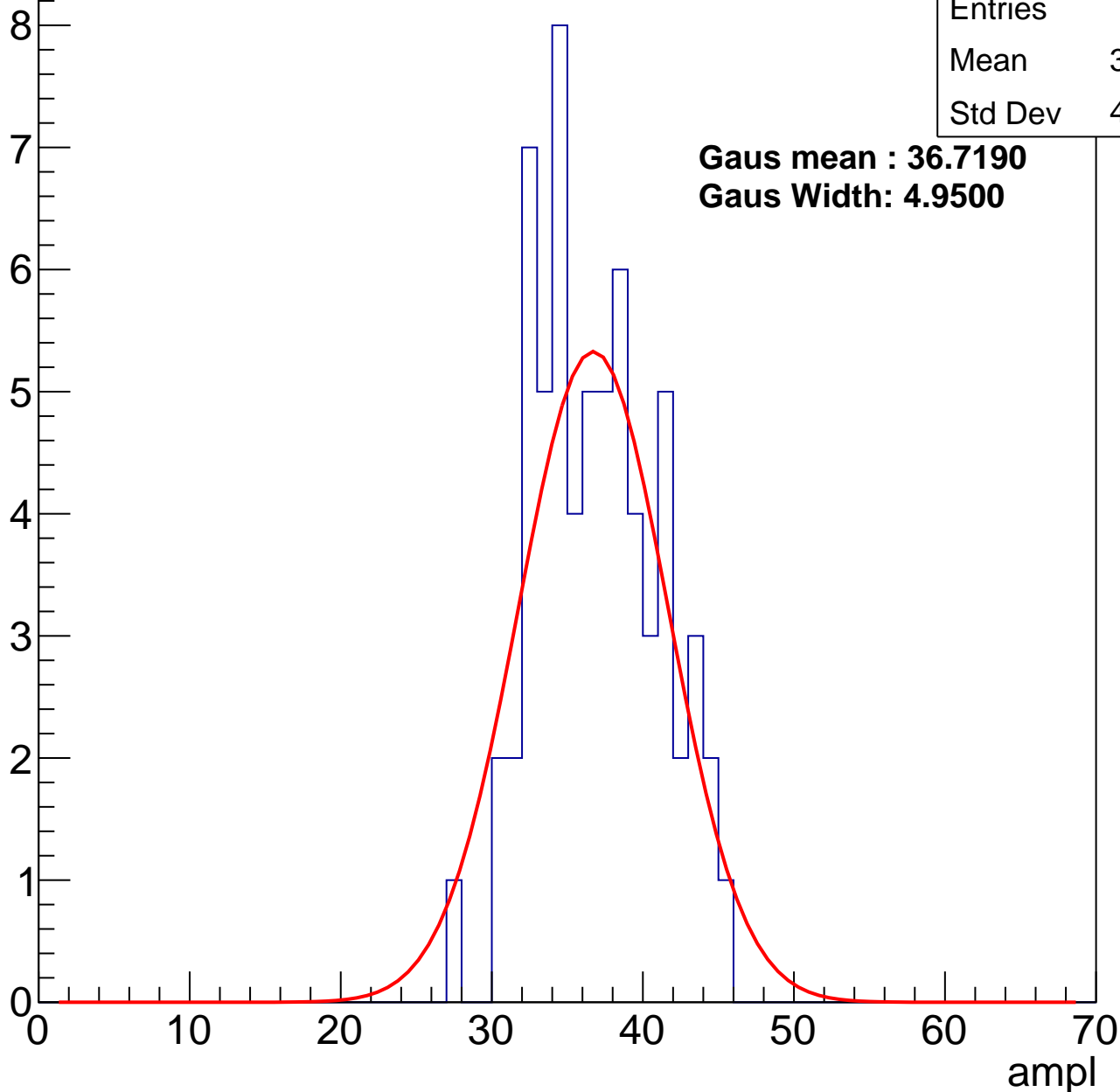
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.46
Std Dev	4.016

Gaus mean : 36.7190

Gaus Width: 4.9500

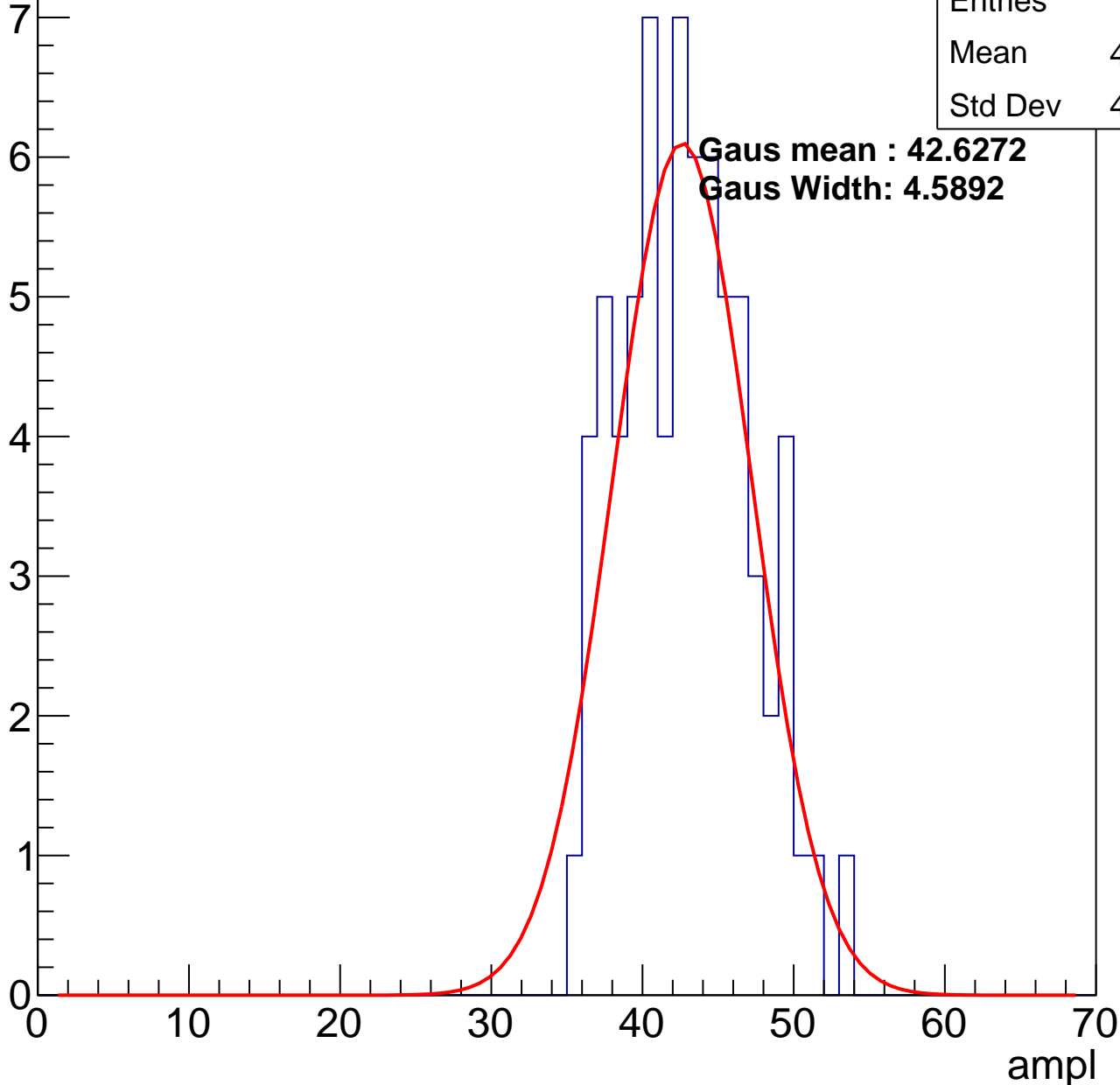


B1L103S, U5-ch119, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.44
Std Dev	4.127

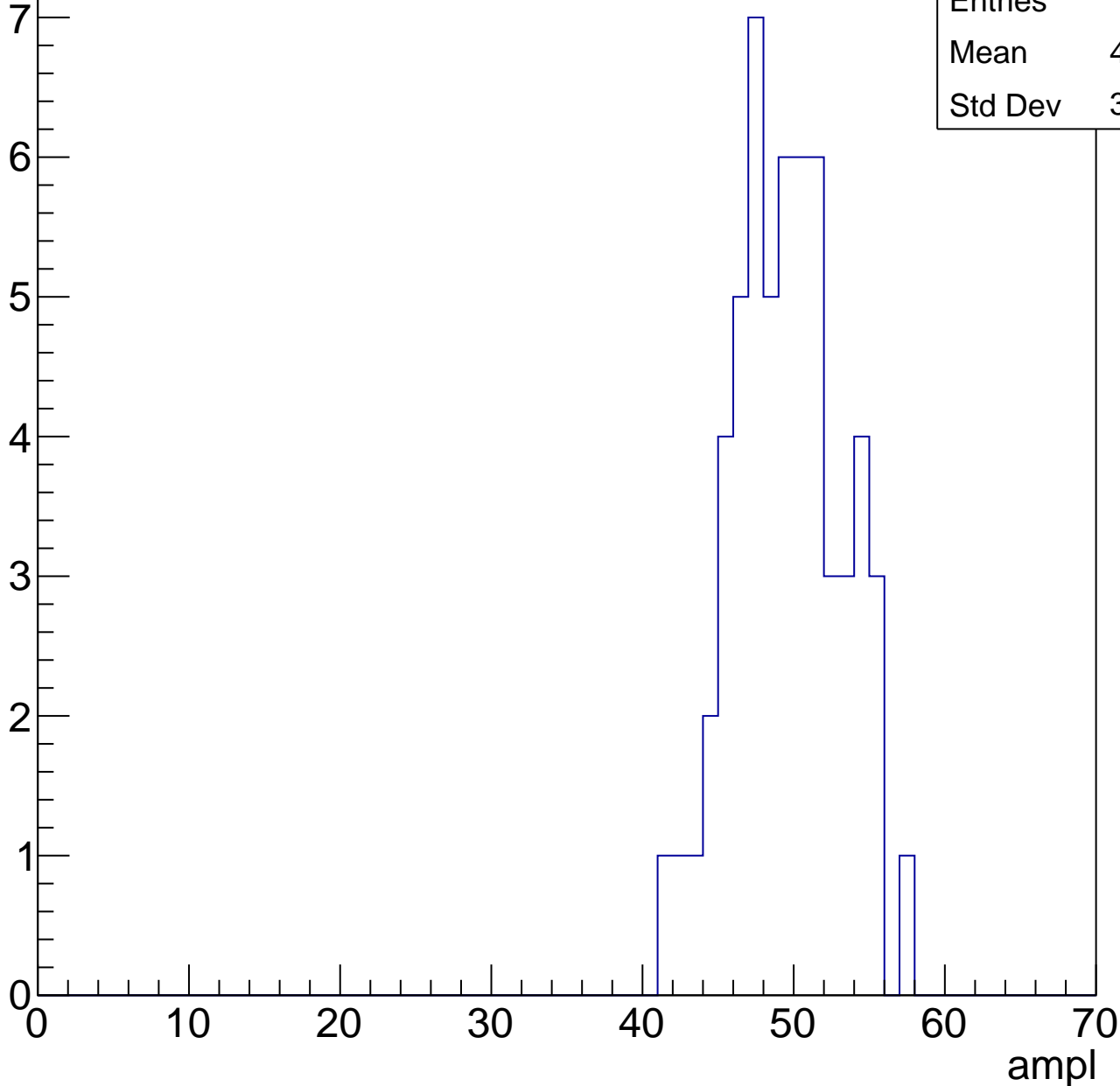


B1L103S, U5-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.07
Std Dev	3.518

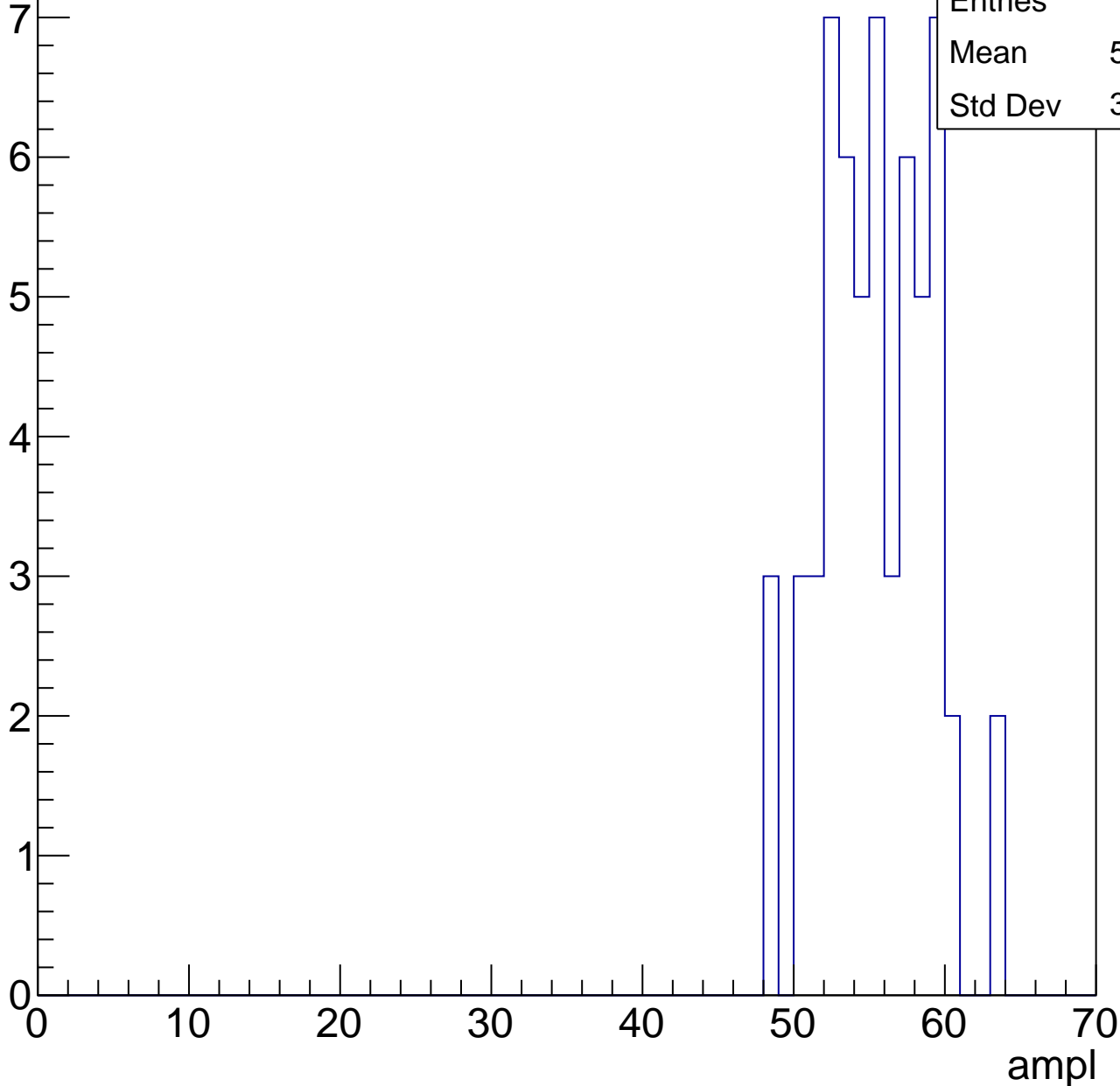


B1L103S, U5-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	54.97
Std Dev	3.503

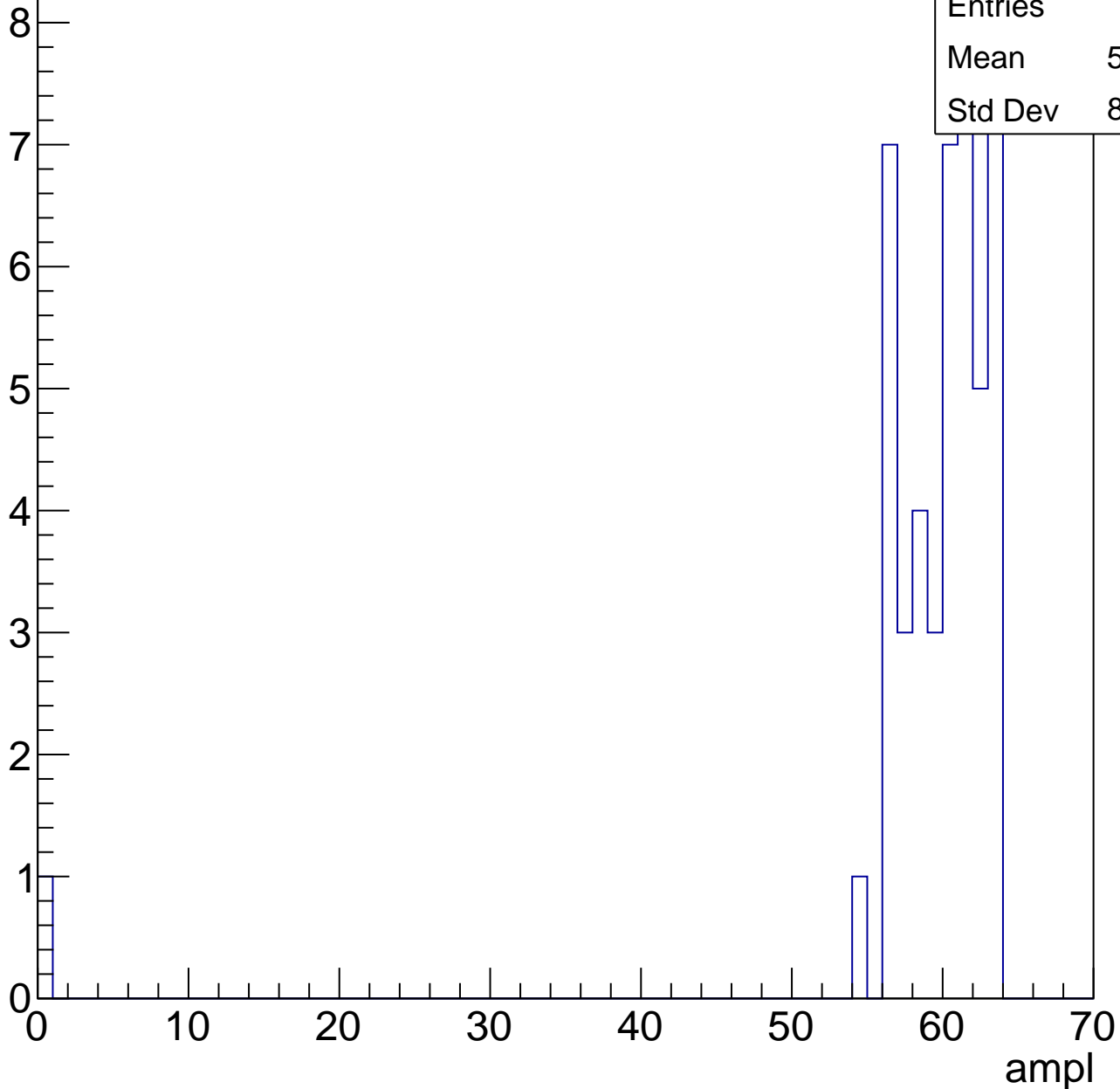


B1L103S, U5-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	58.47
Std Dev	8.972

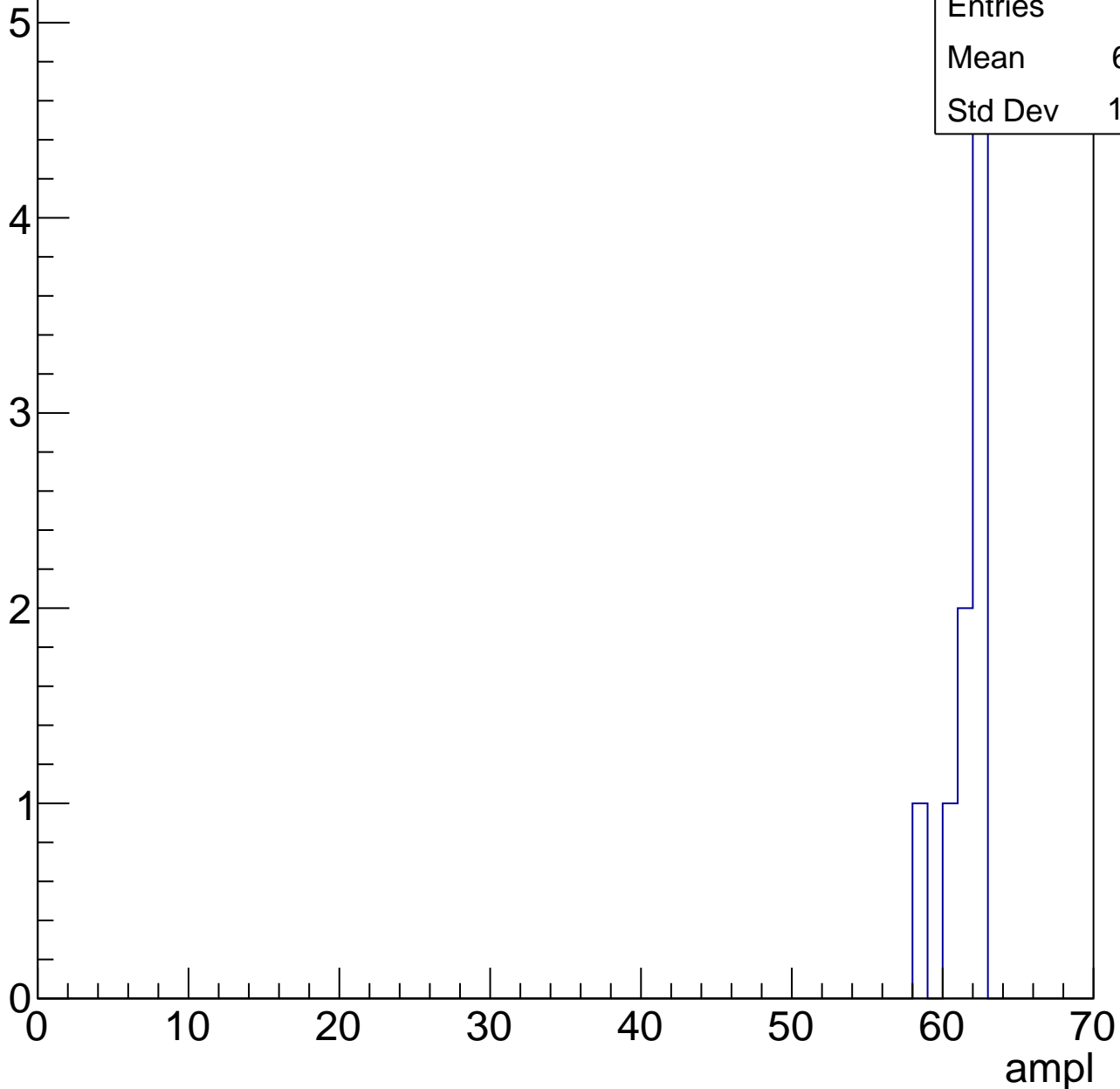


B1L103S, U5-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	61.11
Std Dev	1.286



B1L103S, U5-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch120, adc0

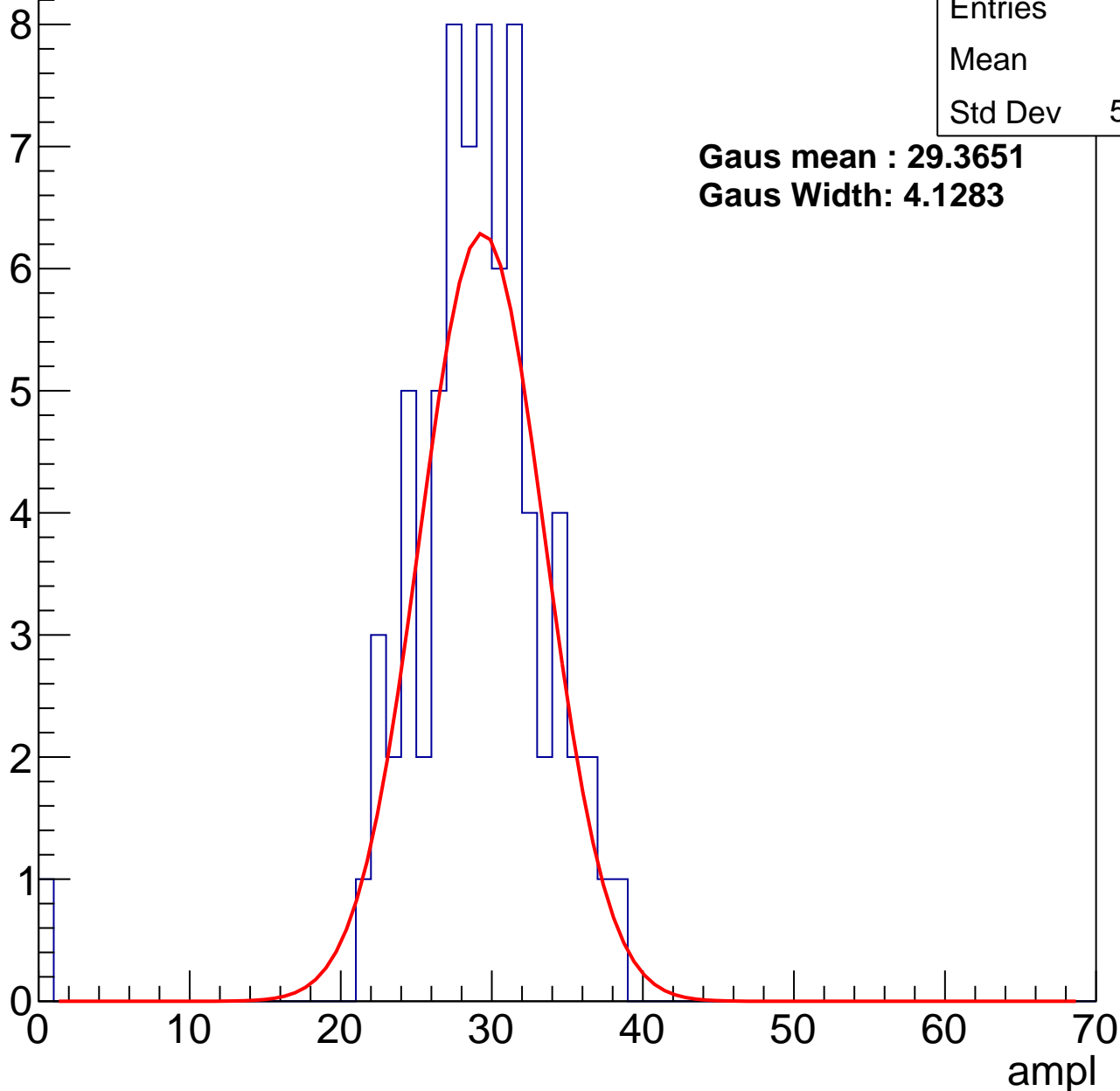
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.5
Std Dev	5.094

Gaus mean : 29.3651

Gaus Width: 4.1283



B1L103S, U5-ch120, adc1

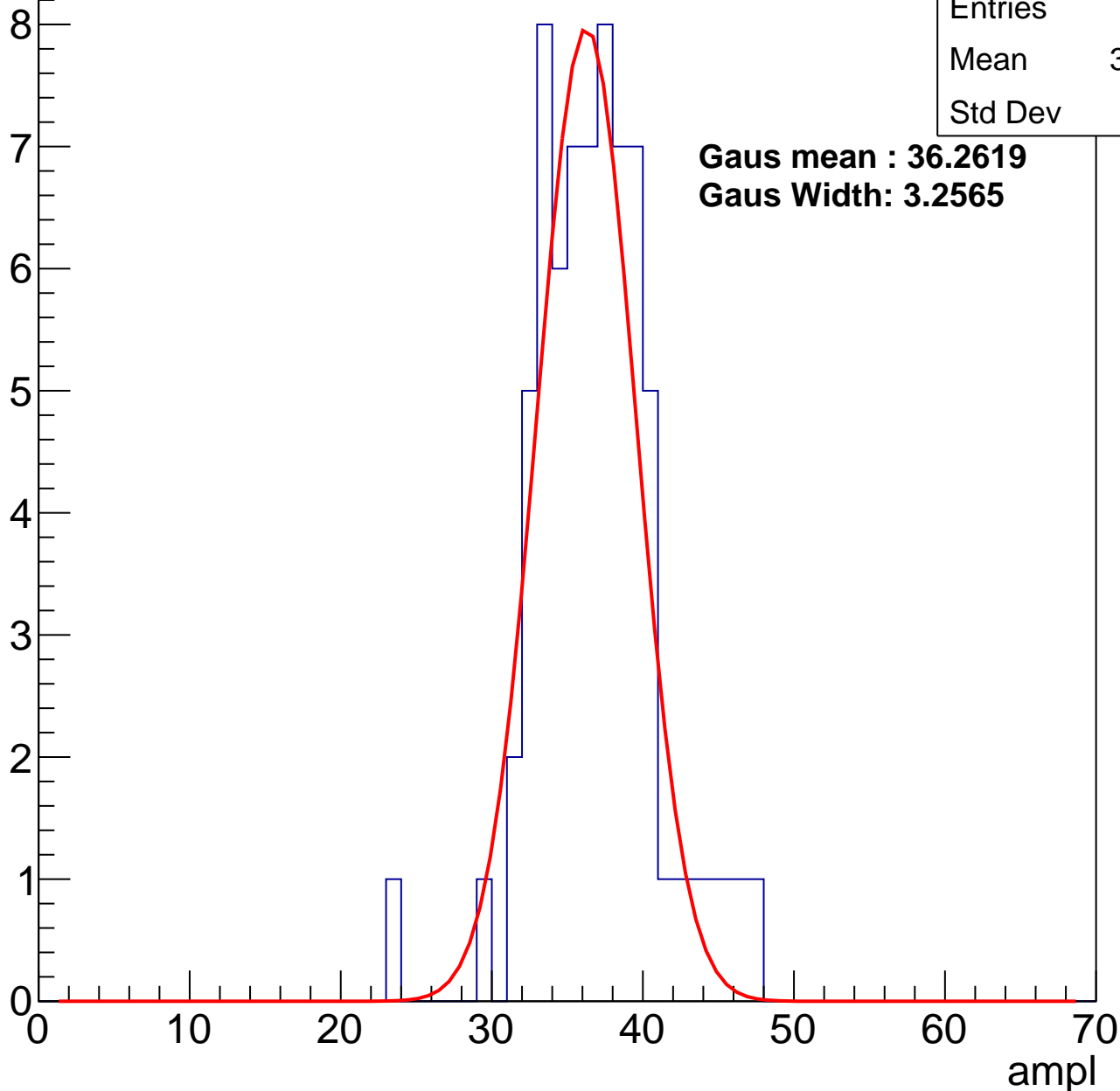
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.37
Std Dev	3.93

Gaus mean : 36.2619

Gaus Width: 3.2565



B1L103S, U5-ch120, adc2

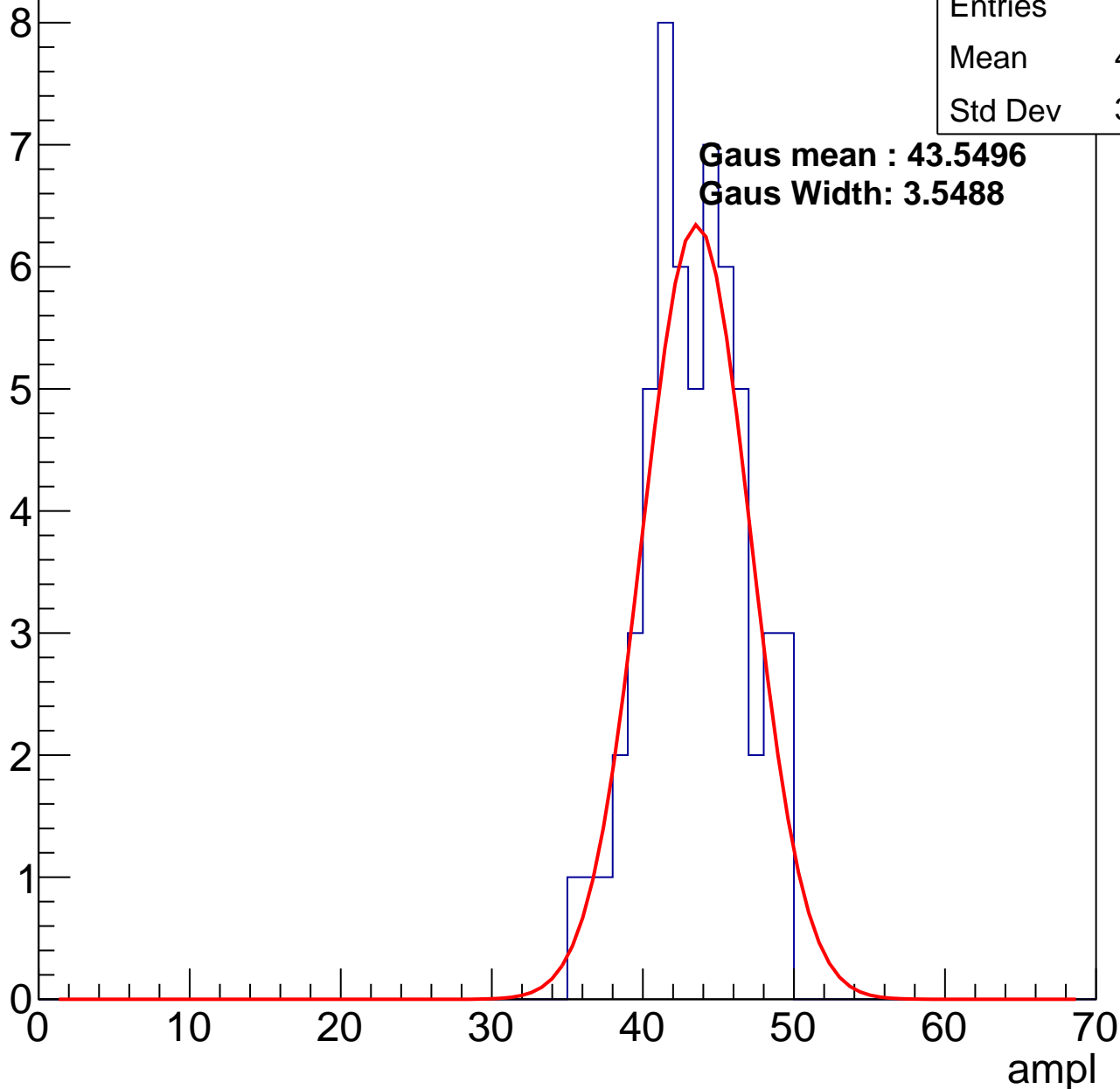
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.91
Std Dev	3.271

Gaus mean : 43.5496

Gaus Width: 3.5488

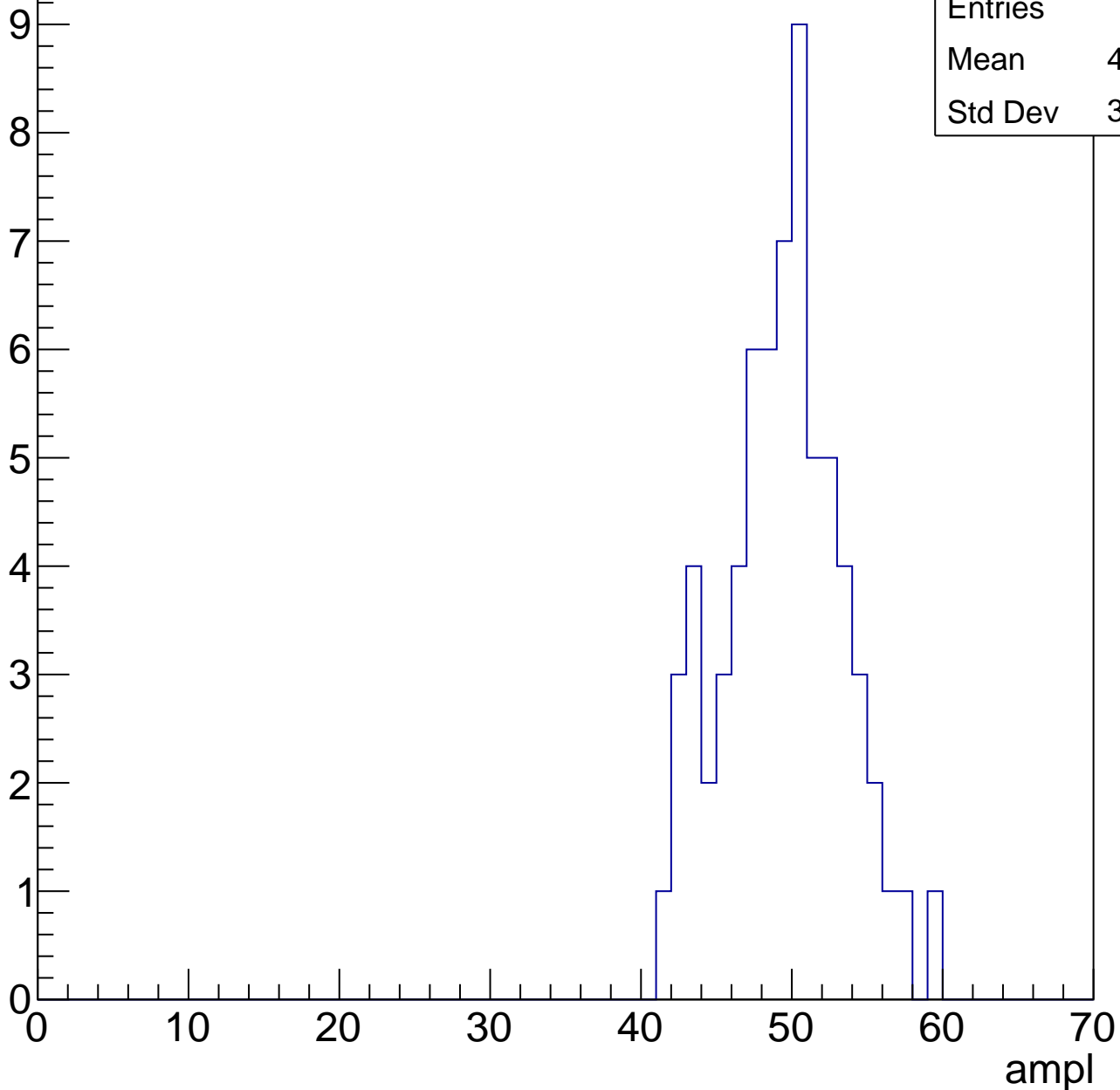


B1L103S, U5-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.96
Std Dev	3.896

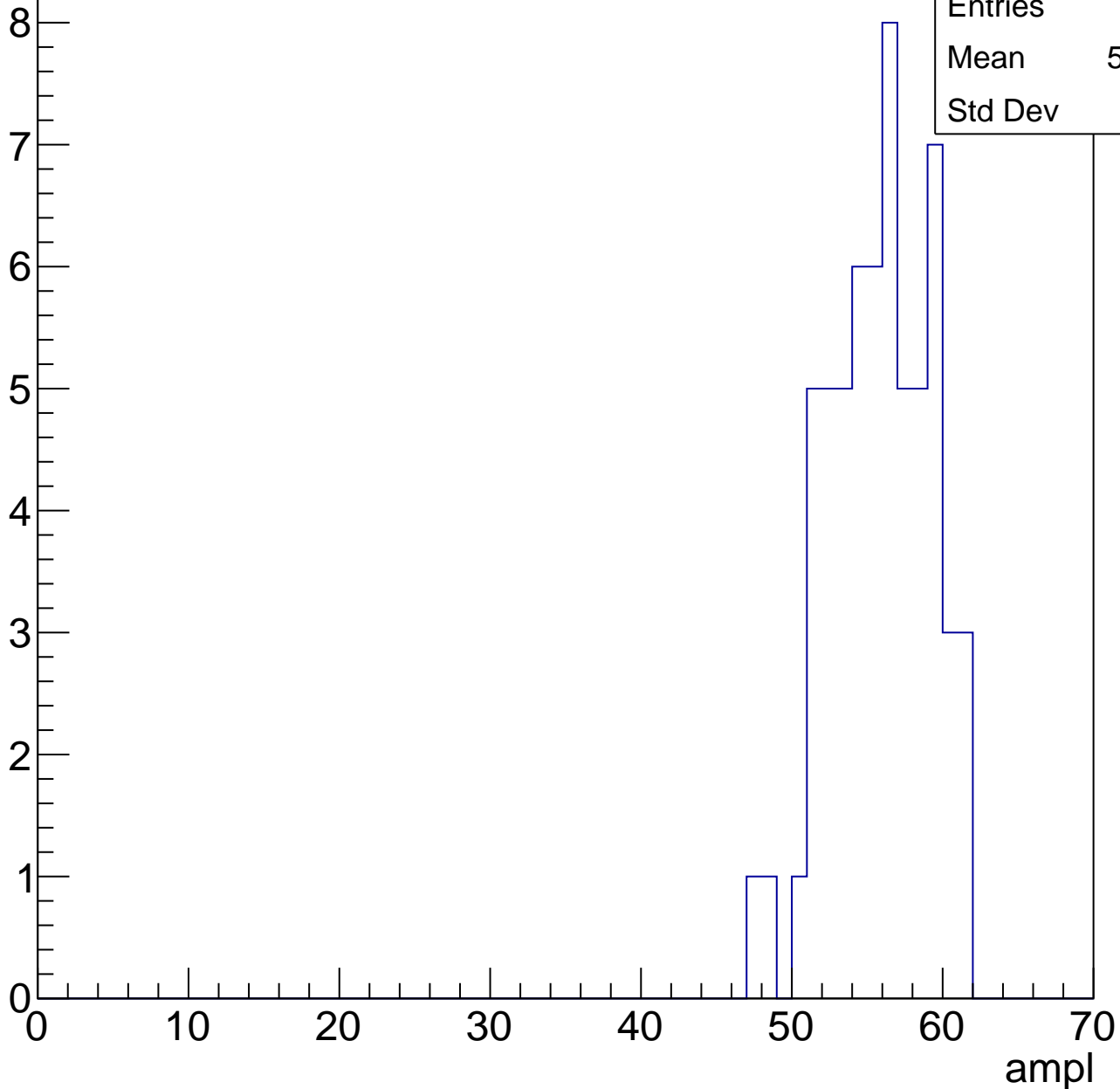


B1L103S, U5-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.38
Std Dev	3.26

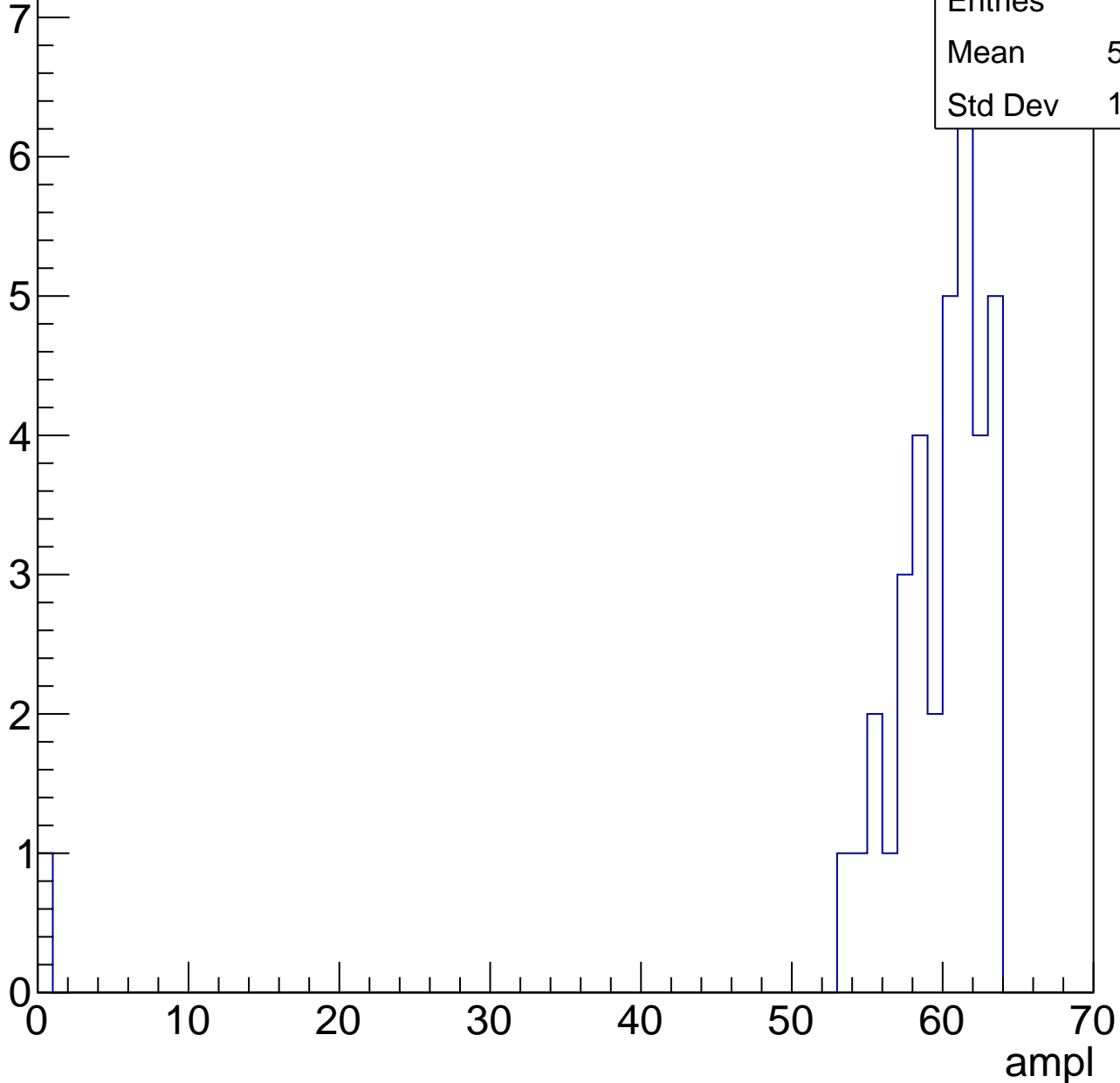


B1L103S, U5-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	57.89
Std Dev	10.14

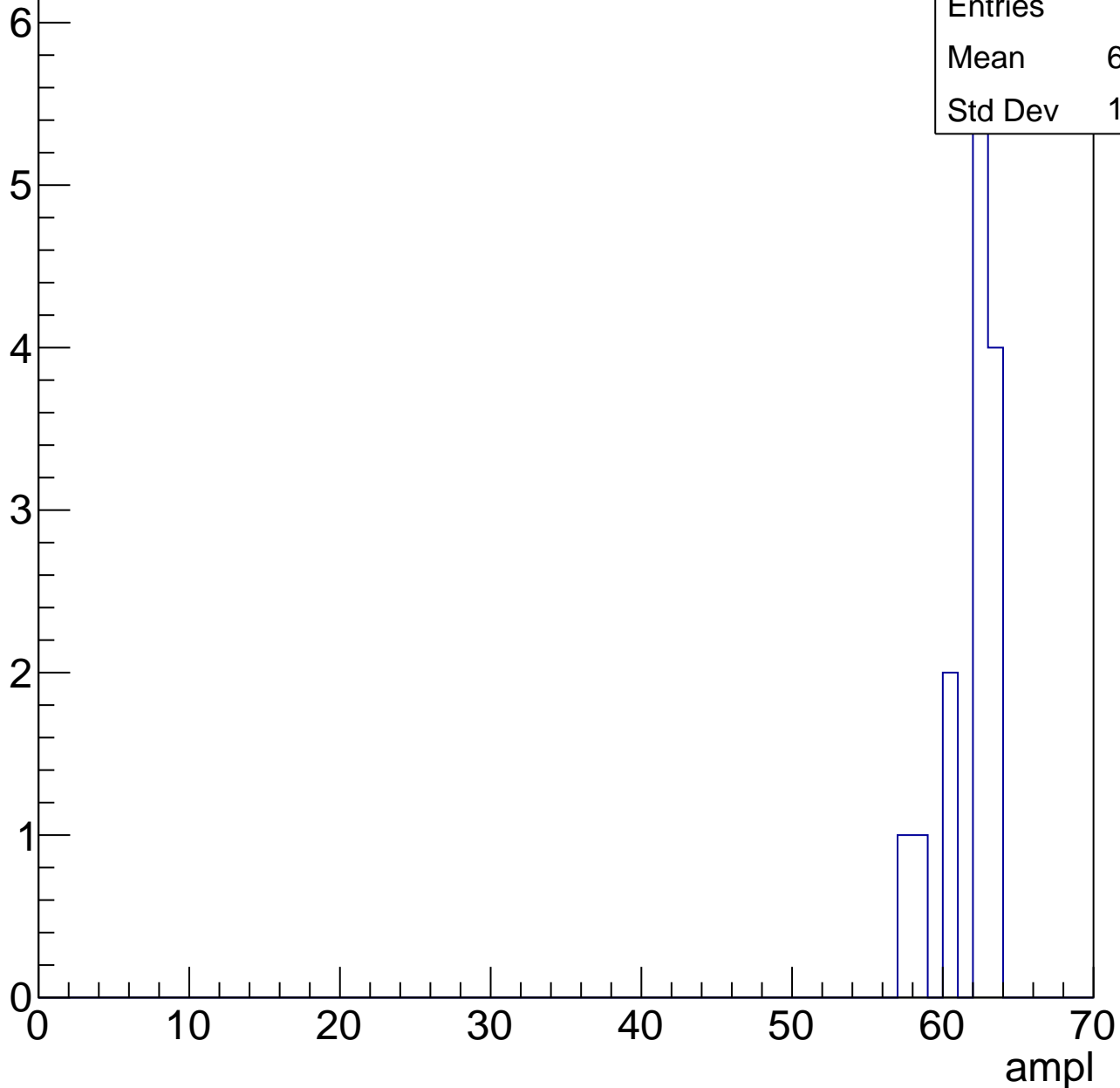


B1L103S, U5-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

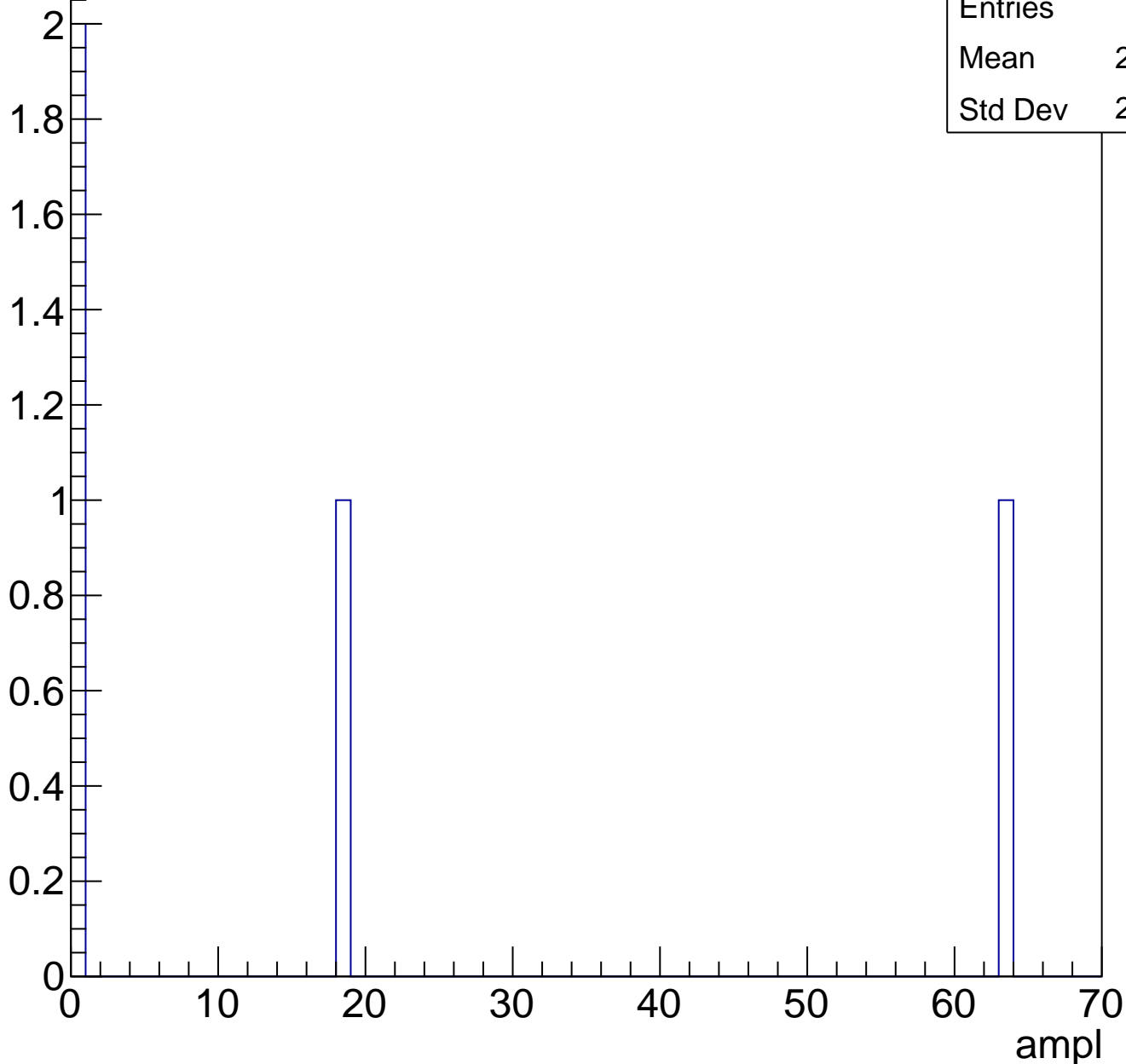
Entries	14
Mean	61.36
Std Dev	1.836



B1L103S, U5-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	20.25
Std Dev	25.75

B1L103S, U5-ch121, adc0

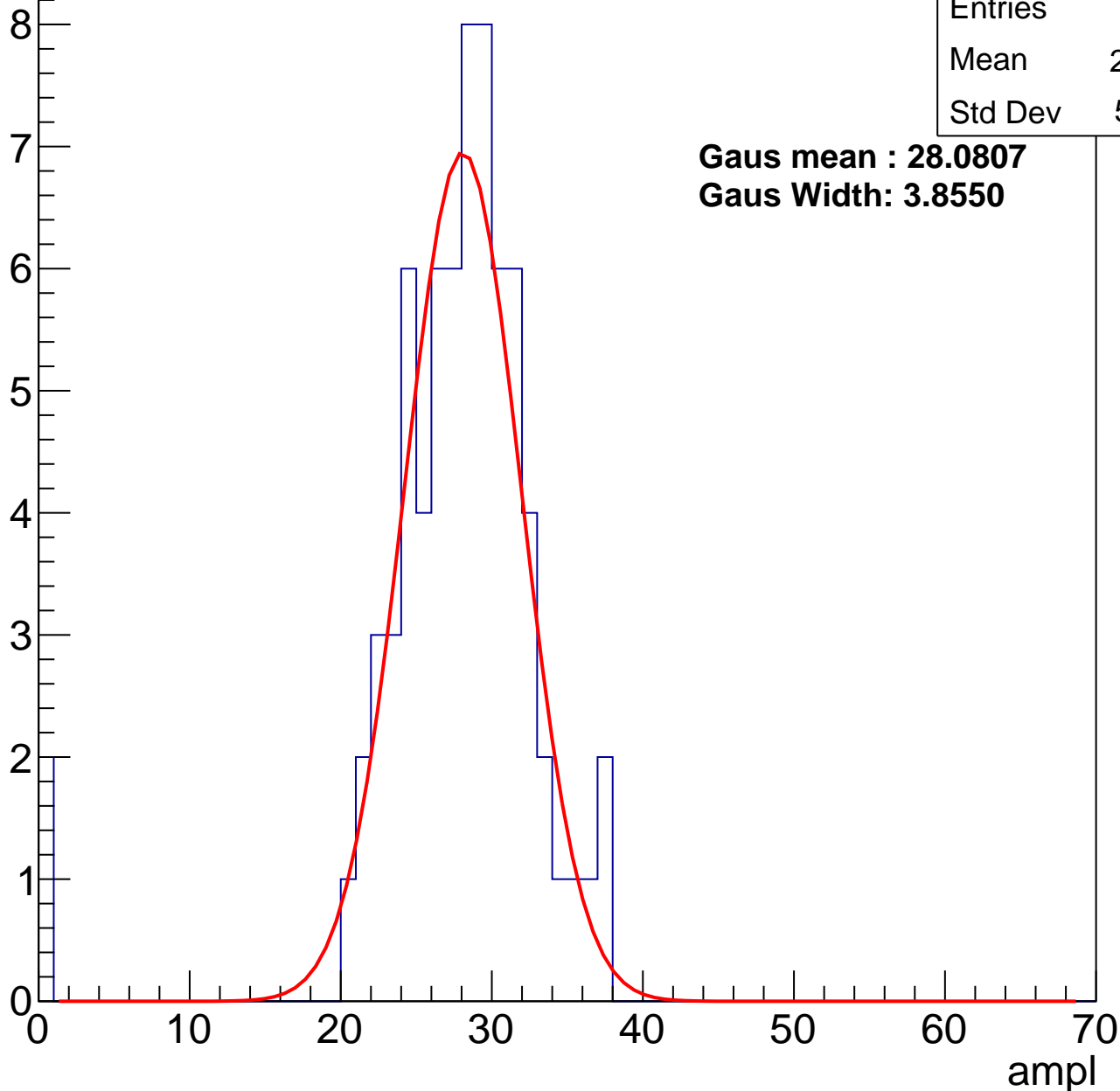
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.14
Std Dev	5.931

Gaus mean : 28.0807

Gaus Width: 3.8550



B1L103S, U5-ch121, adc1

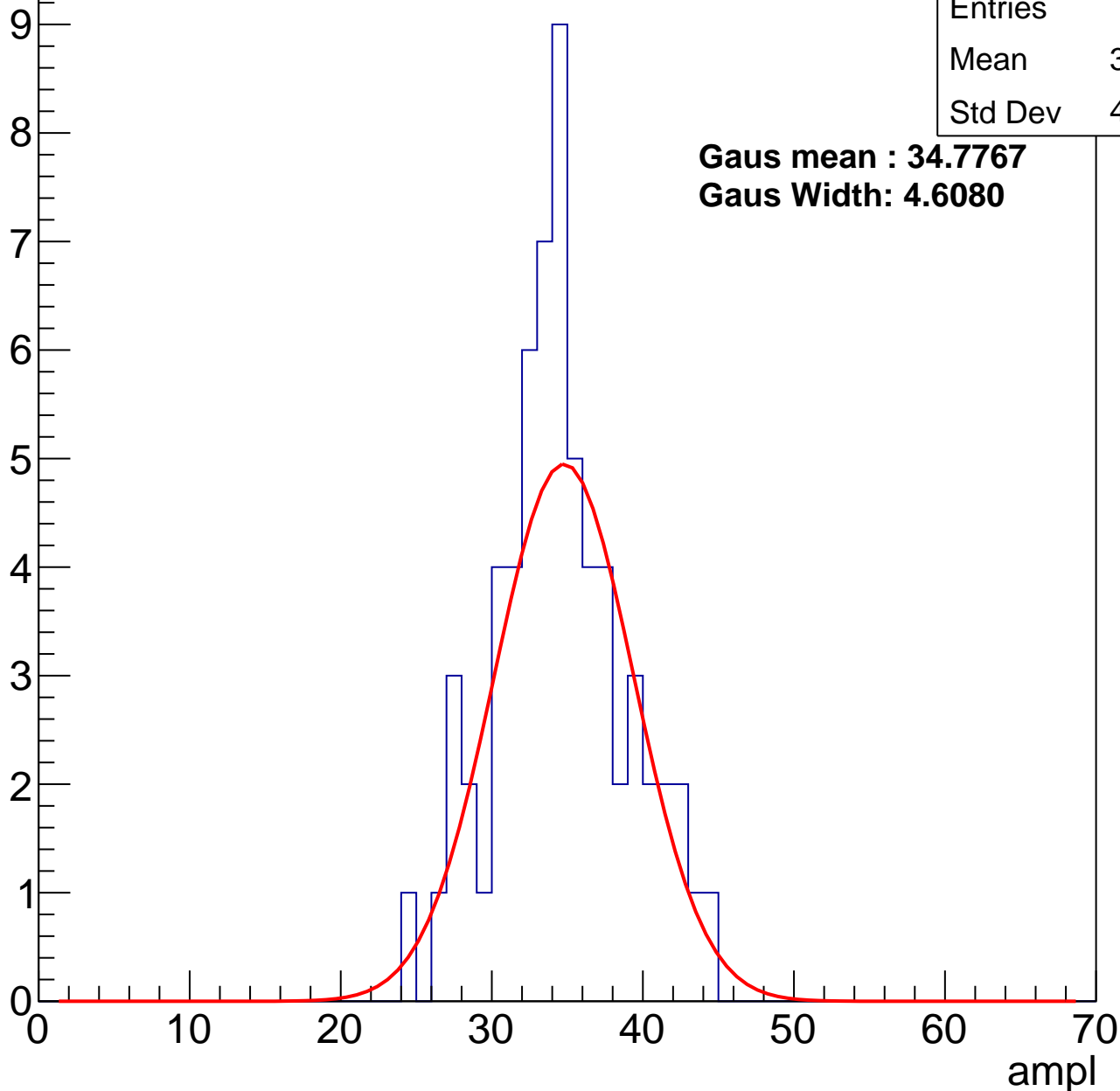
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	34.09
Std Dev	4.297

Gaus mean : 34.7767

Gaus Width: 4.6080



B1L103S, U5-ch121, adc2

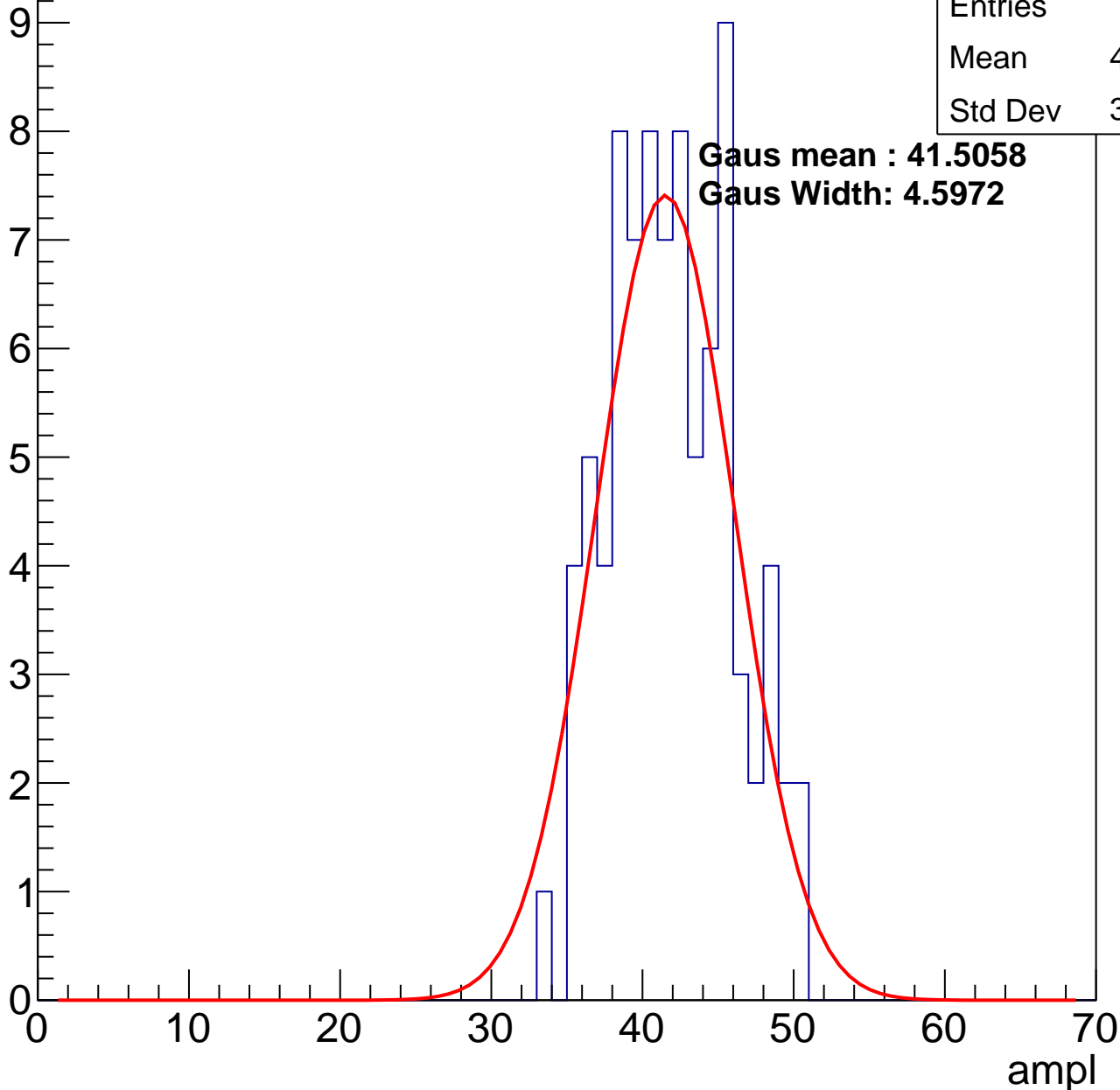
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	41.49
Std Dev	3.975

Gaus mean : 41.5058

Gaus Width: 4.5972

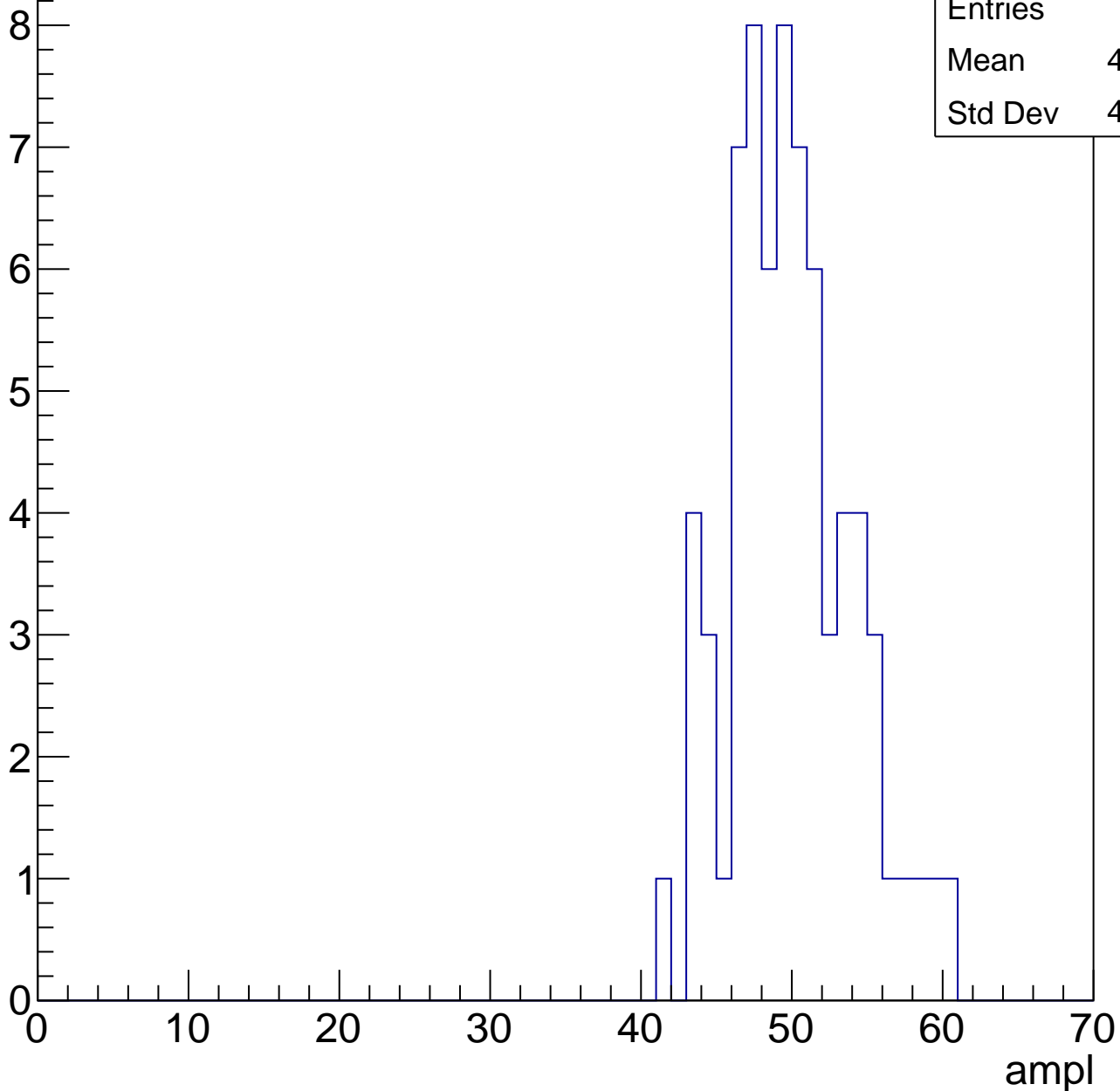


B1L103S, U5-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.47
Std Dev	4.017

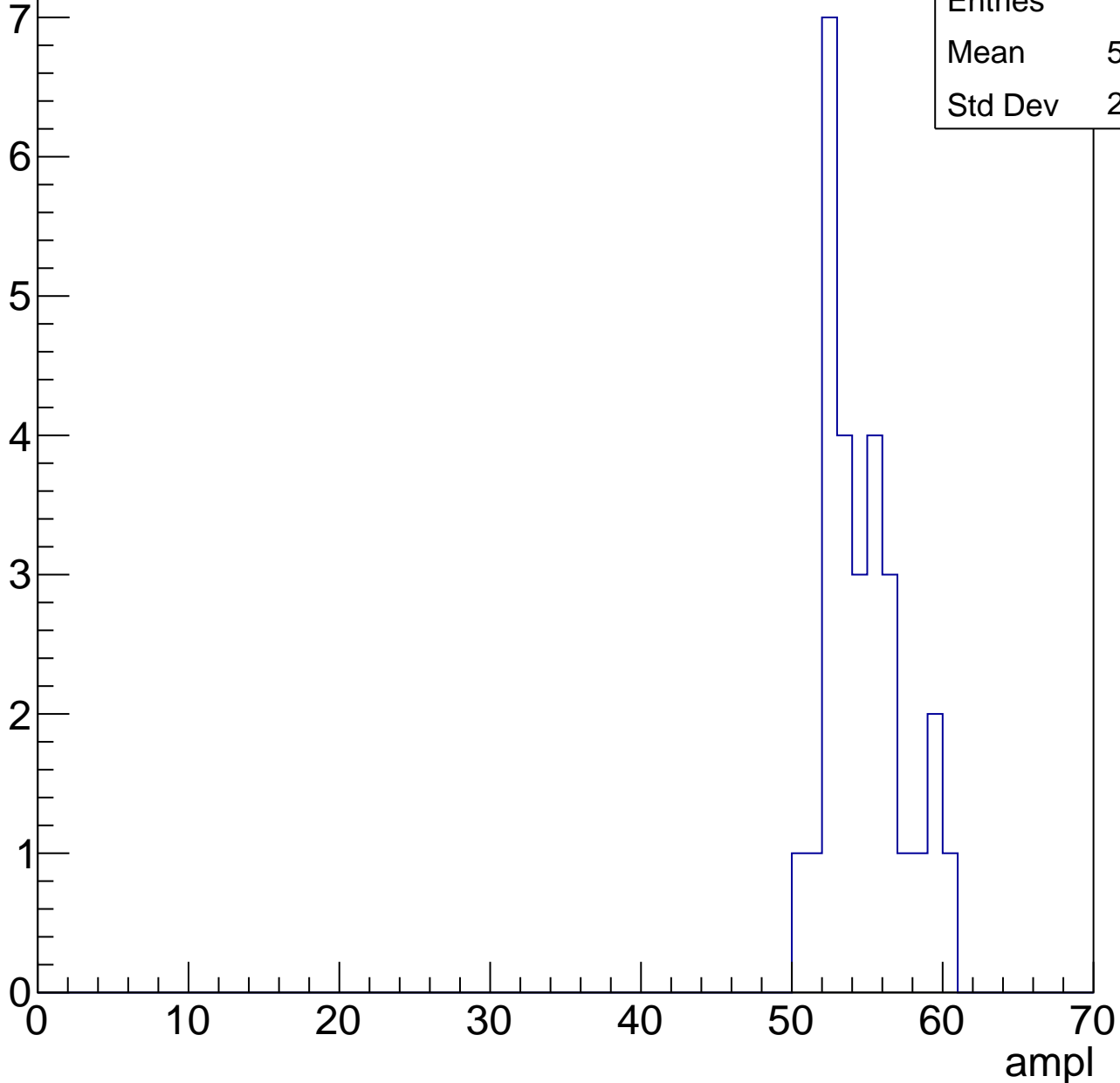


B1L103S, U5-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	54.29
Std Dev	2.548

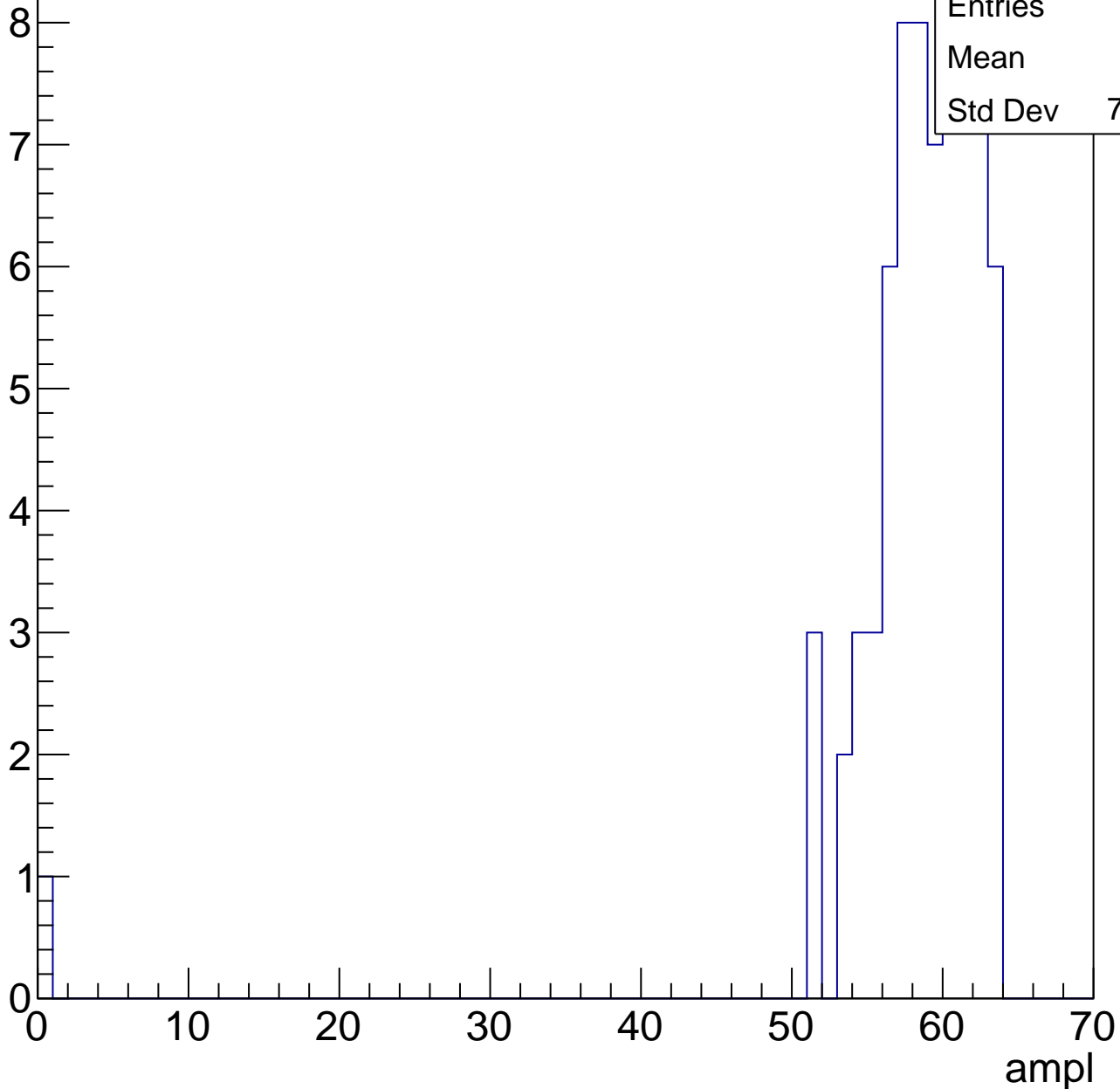


B1L103S, U5-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

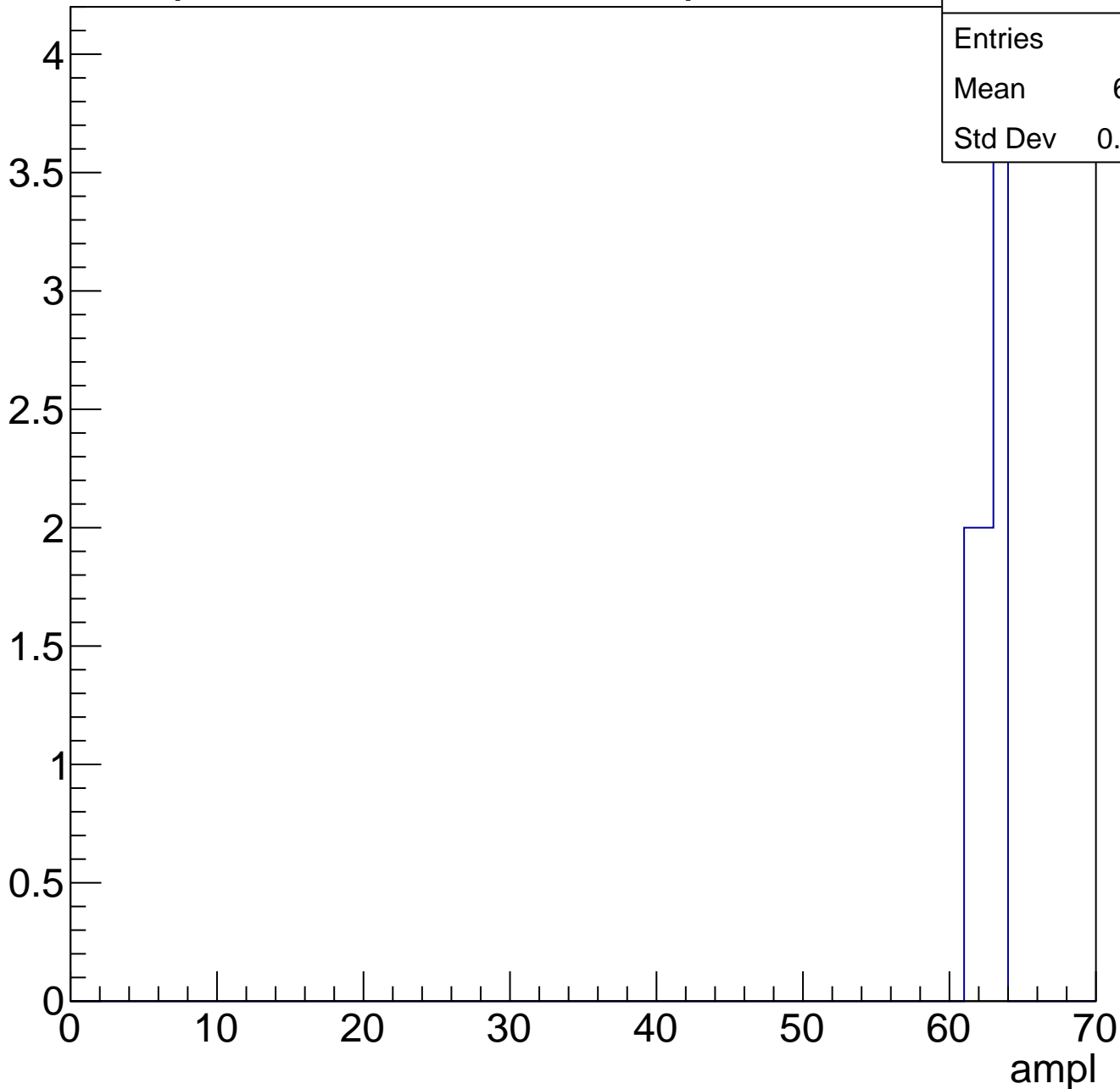
Entries	71
Mean	57.7
Std Dev	7.553



B1L103S, U5-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch122, adc0

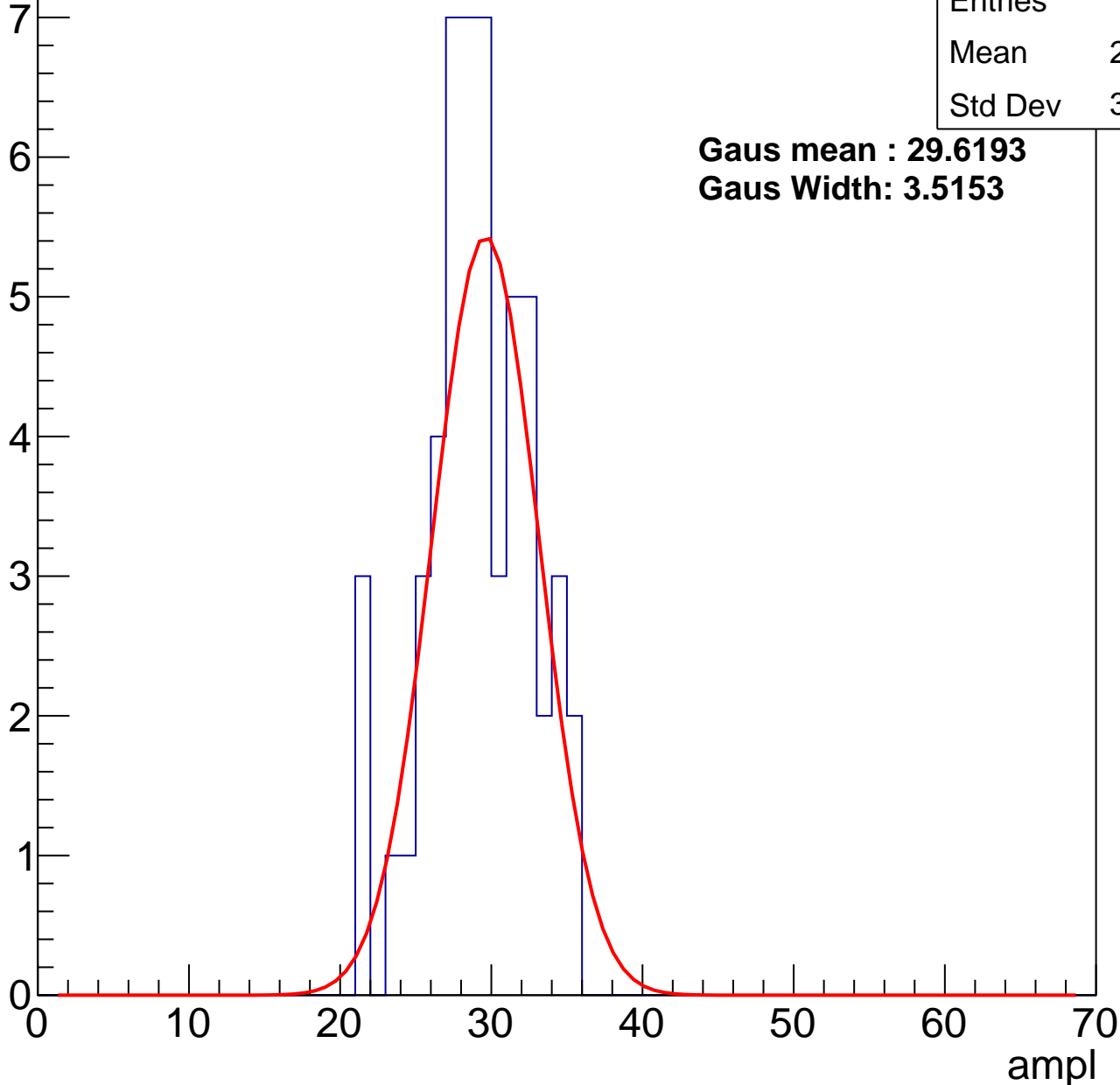
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	28.68
Std Dev	3.402

Gaus mean : 29.6193

Gaus Width: 3.5153



B1L103S, U5-ch122, adc1

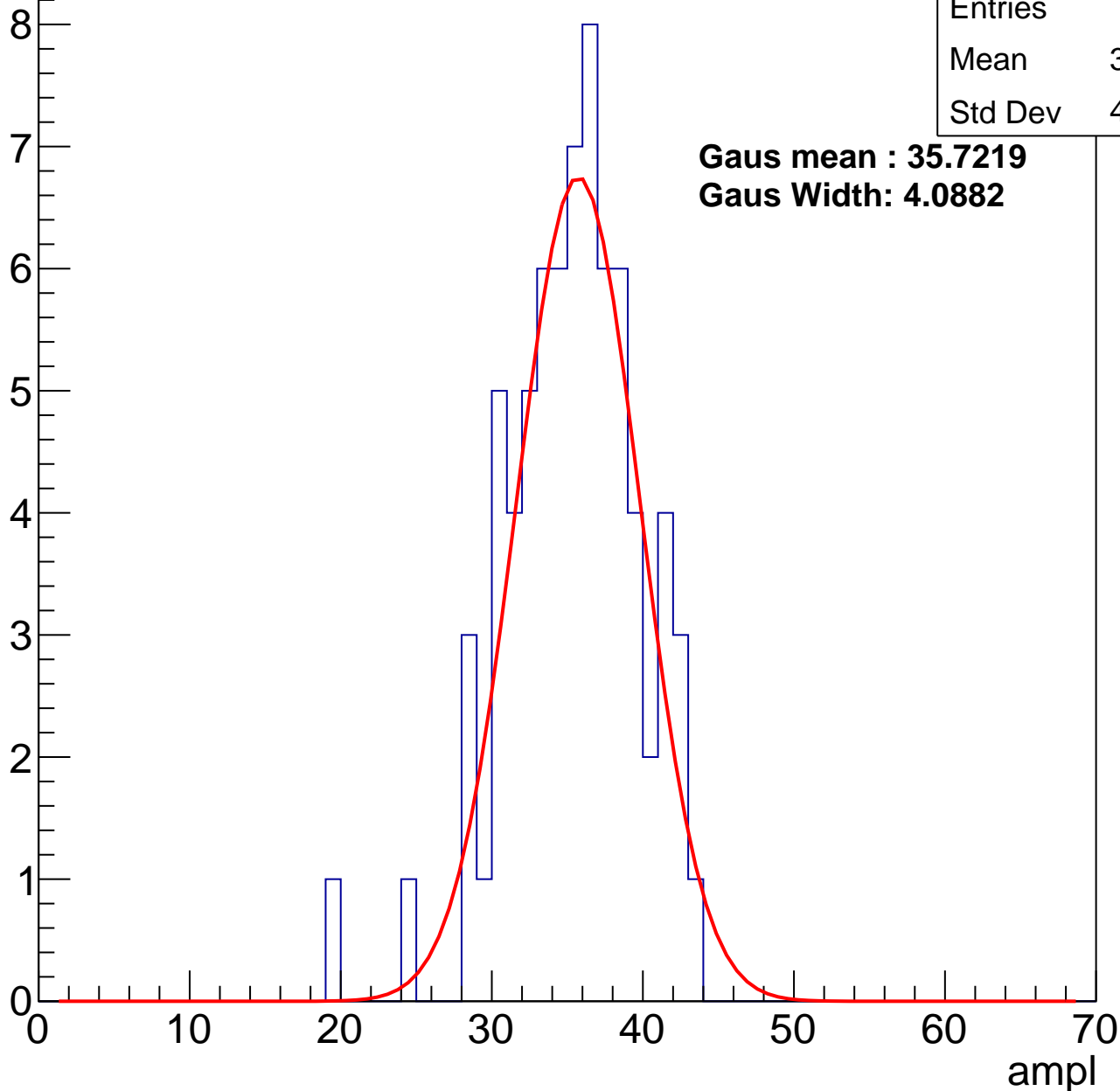
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	34.85
Std Dev	4.344

Gaus mean : 35.7219

Gaus Width: 4.0882



B1L103S, U5-ch122, adc2

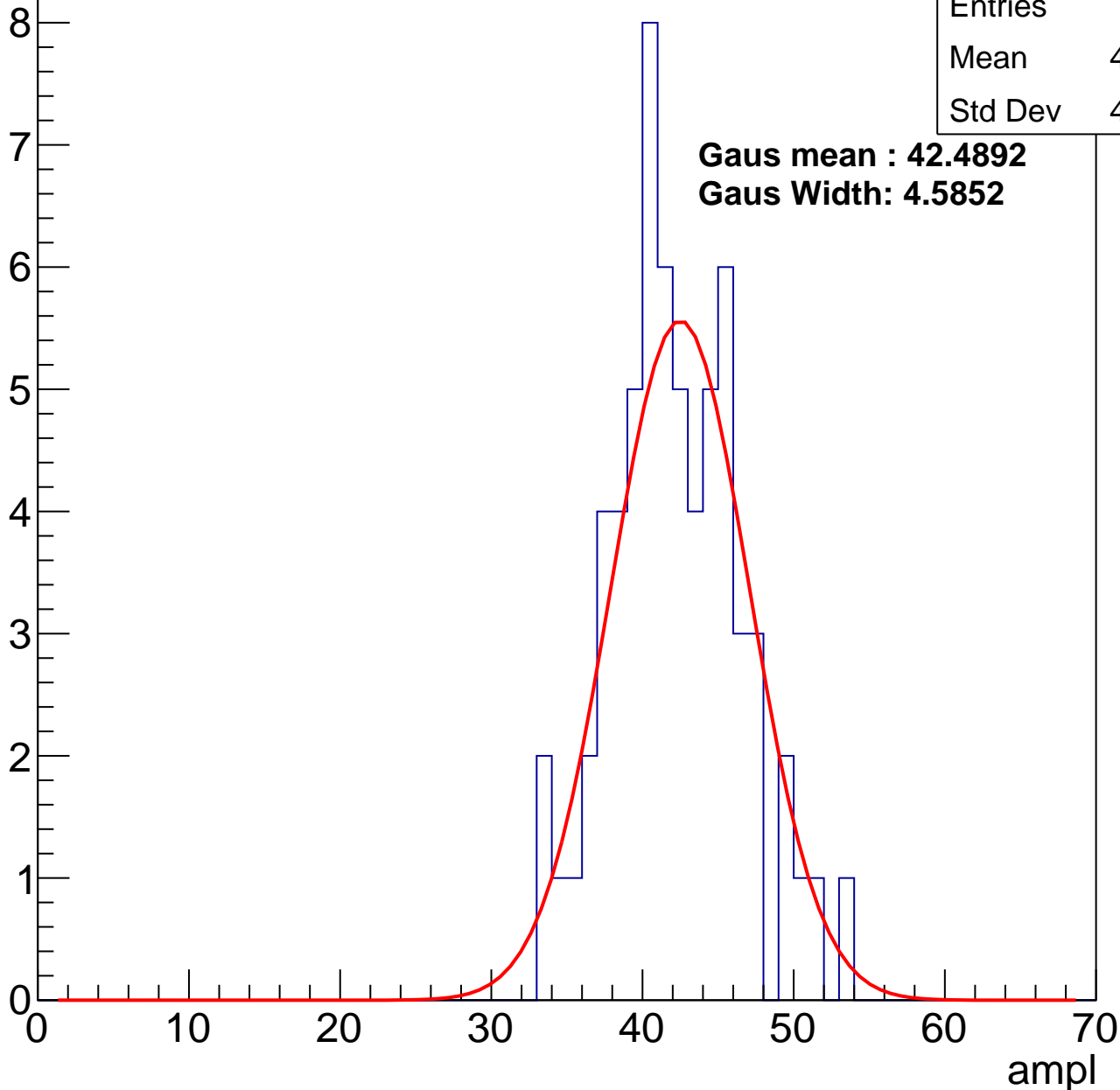
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	41.73
Std Dev	4.247

Gaus mean : 42.4892

Gaus Width: 4.5852

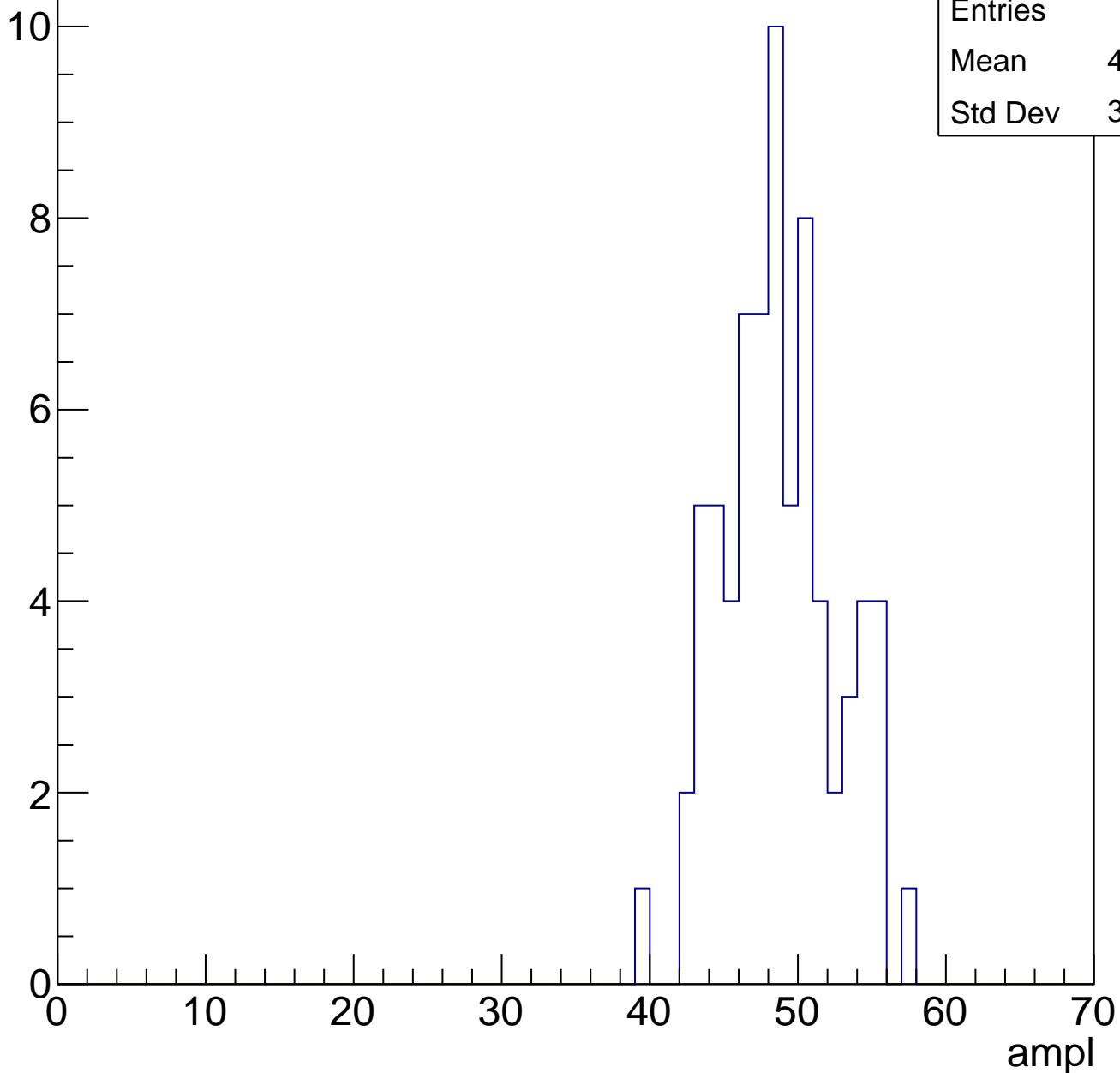


B1L103S, U5-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	48.25
Std Dev	3.774

Entry

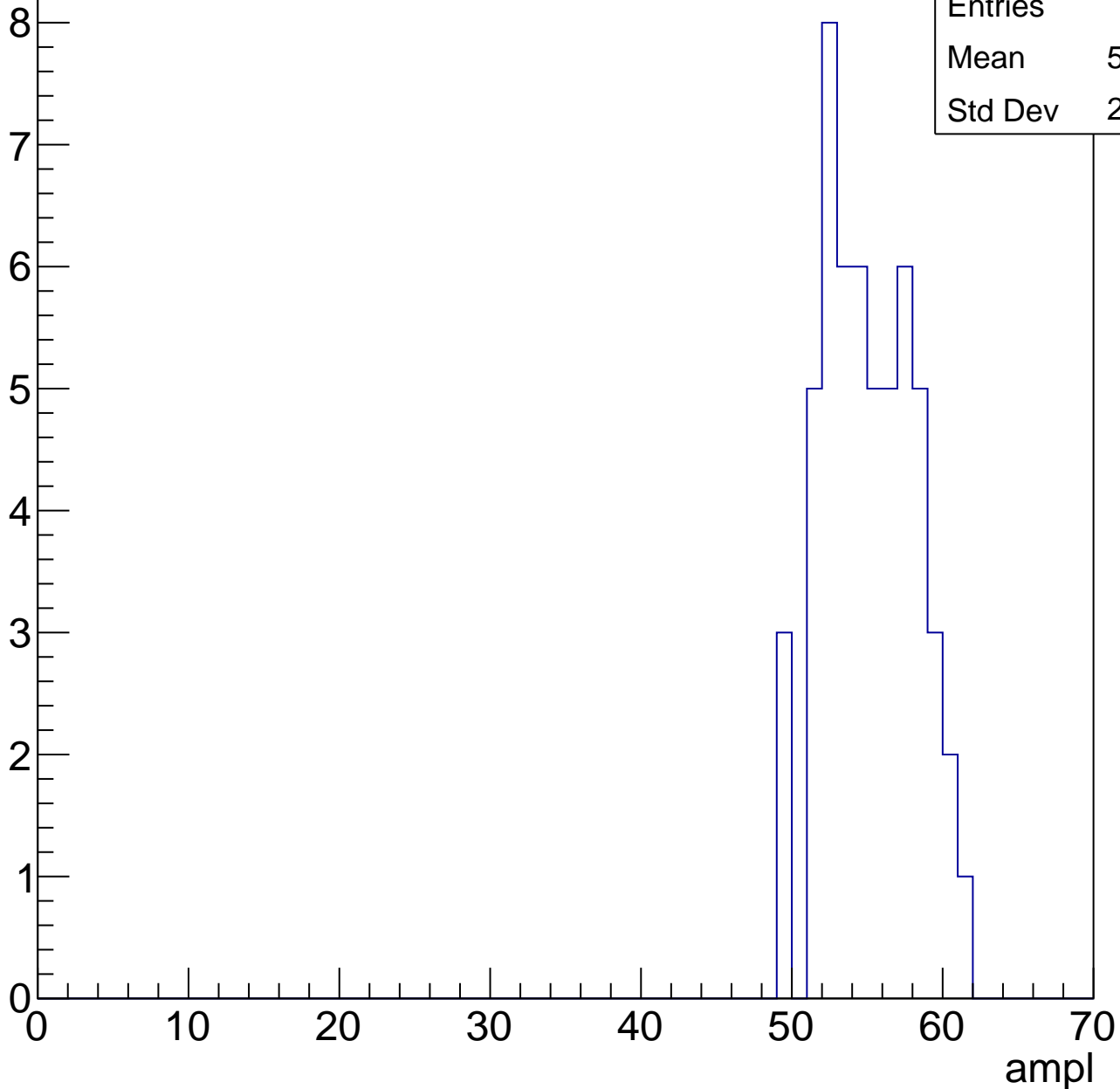


B1L103S, U5-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.64
Std Dev	2.987



B1L103S, U5-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 45

Mean 59.51

Std Dev 2.705

ampl

0

10

20

30

40

50

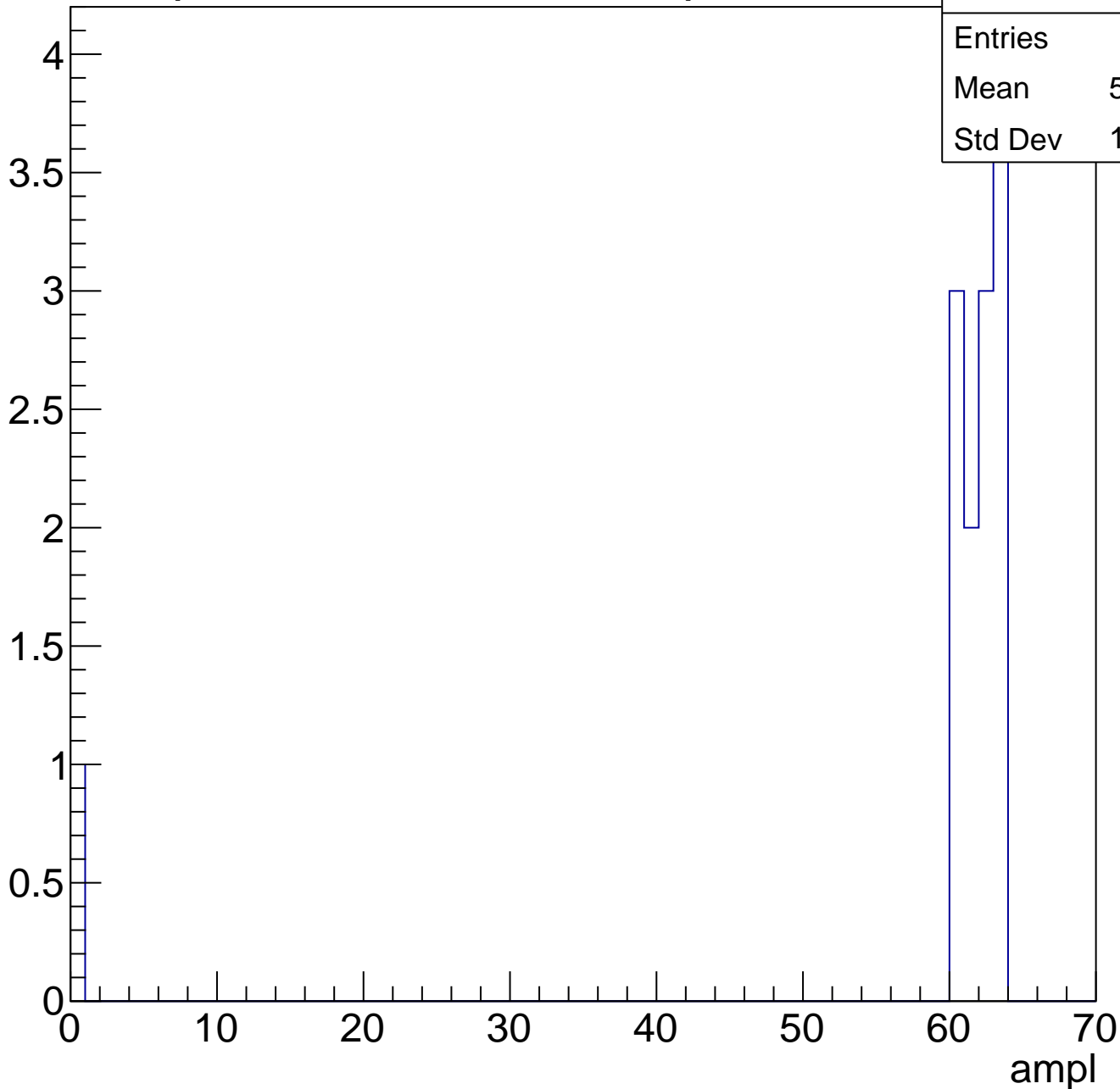
60

70

B1L103S, U5-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

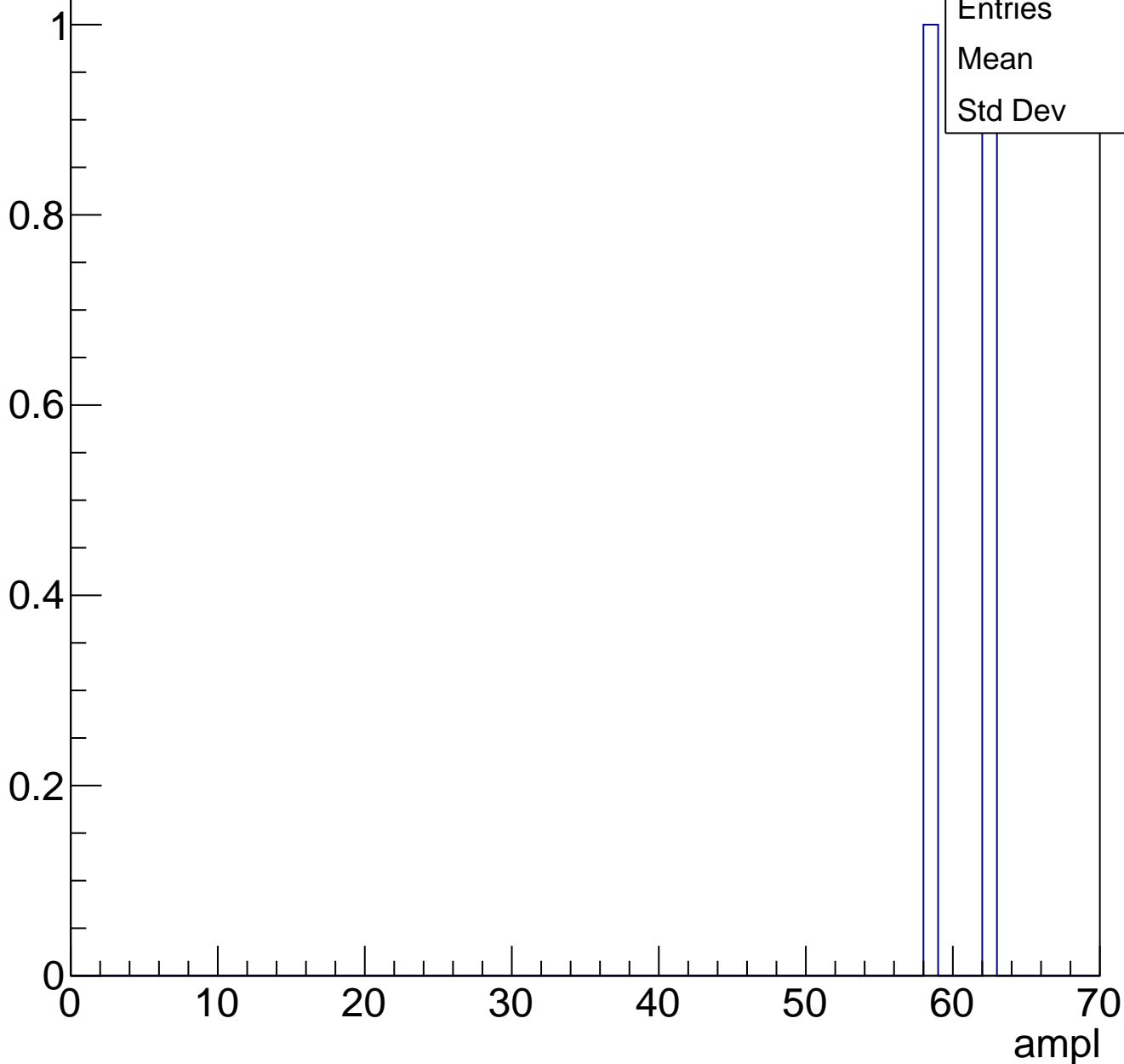


Entries	13
Mean	56.92
Std Dev	16.47

B1L103S, U5-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch123, adc0

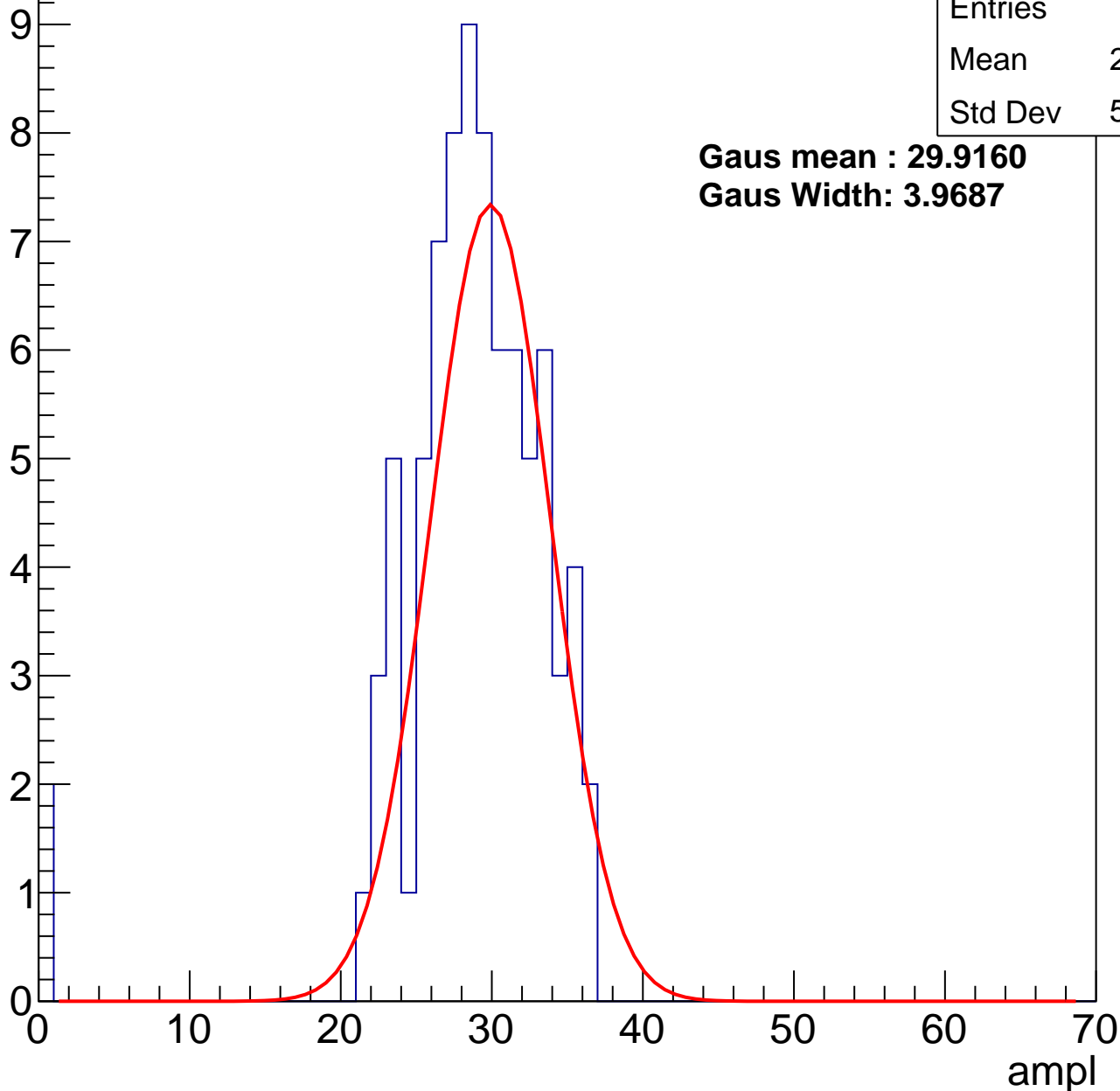
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	28.04
Std Dev	5.768

Gaus mean : 29.9160

Gaus Width: 3.9687



B1L103S, U5-ch123, adc1

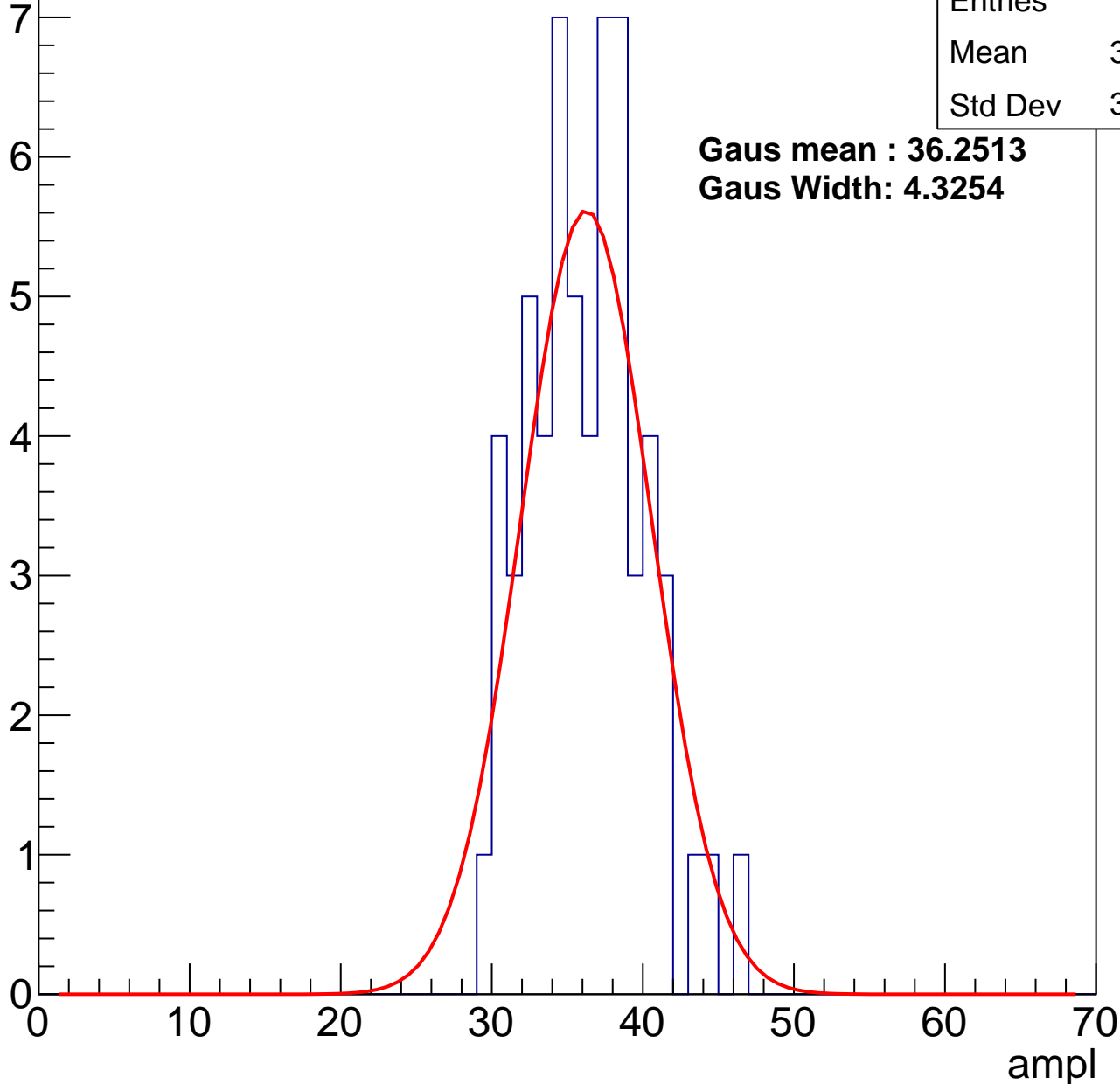
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.82
Std Dev	3.717

Gaus mean : 36.2513

Gaus Width: 4.3254



B1L103S, U5-ch123, adc2

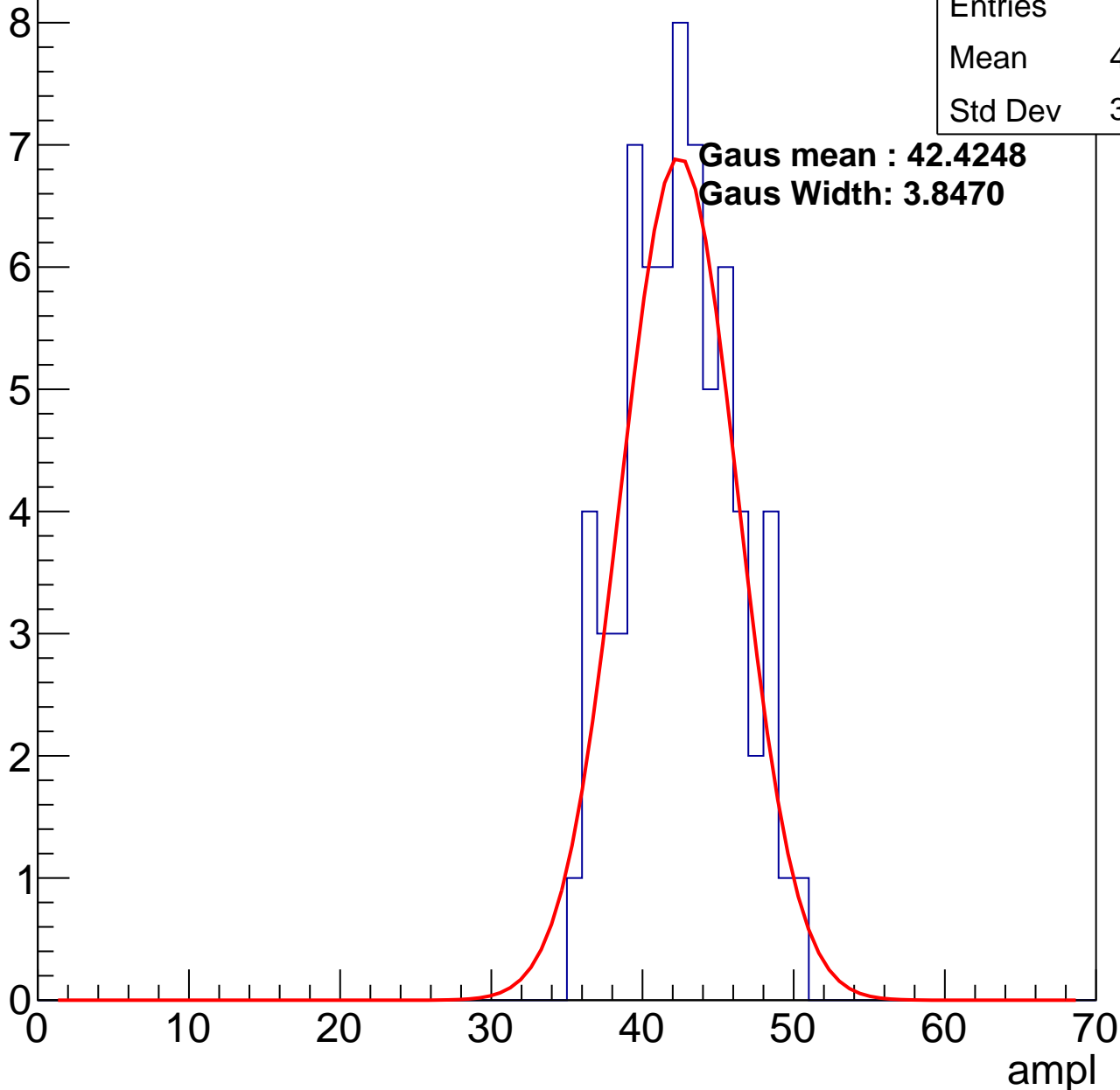
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.04
Std Dev	3.558

Gaus mean : 42.4248

Gaus Width: 3.8470

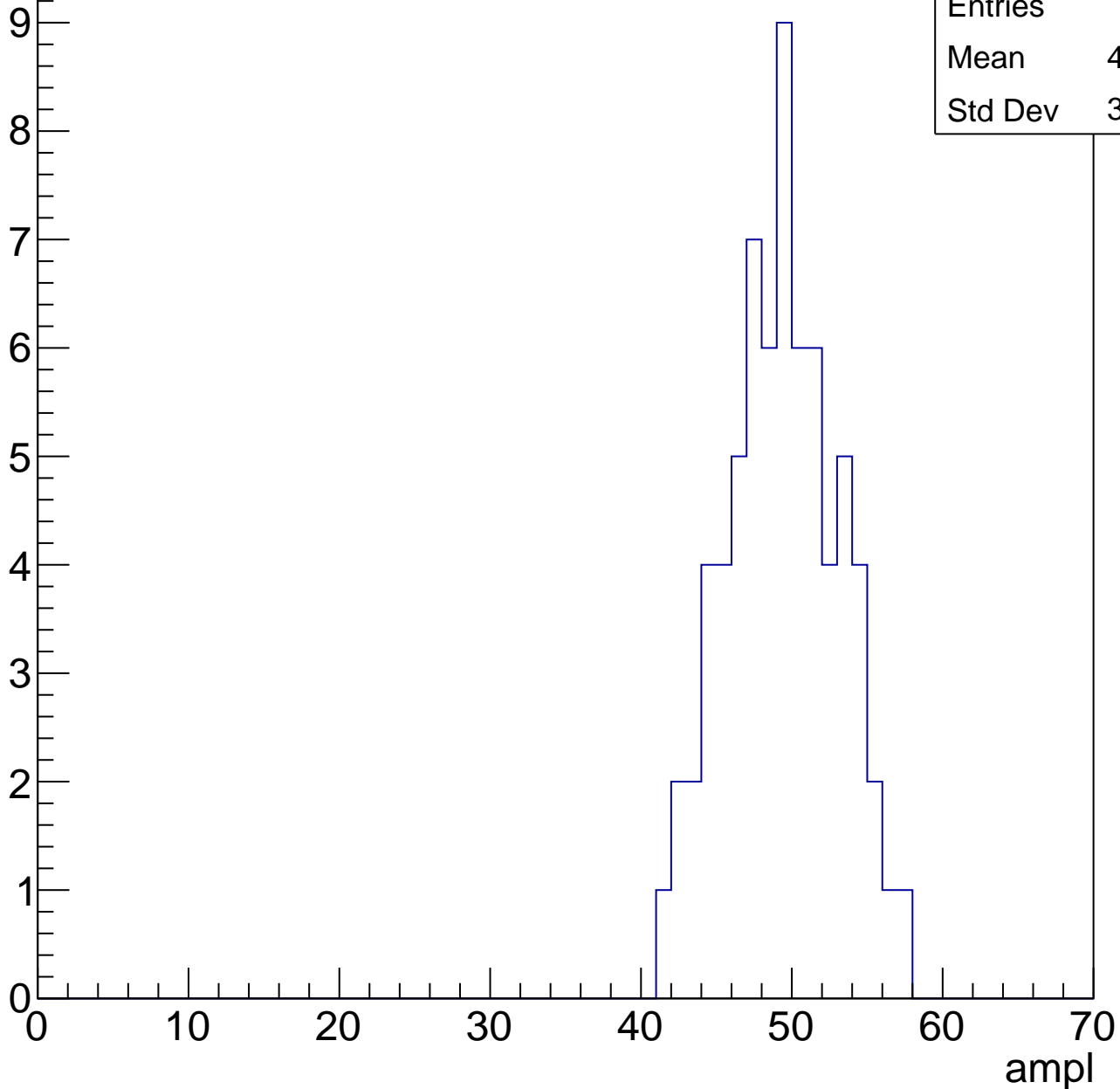


B1L103S, U5-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.88
Std Dev	3.626

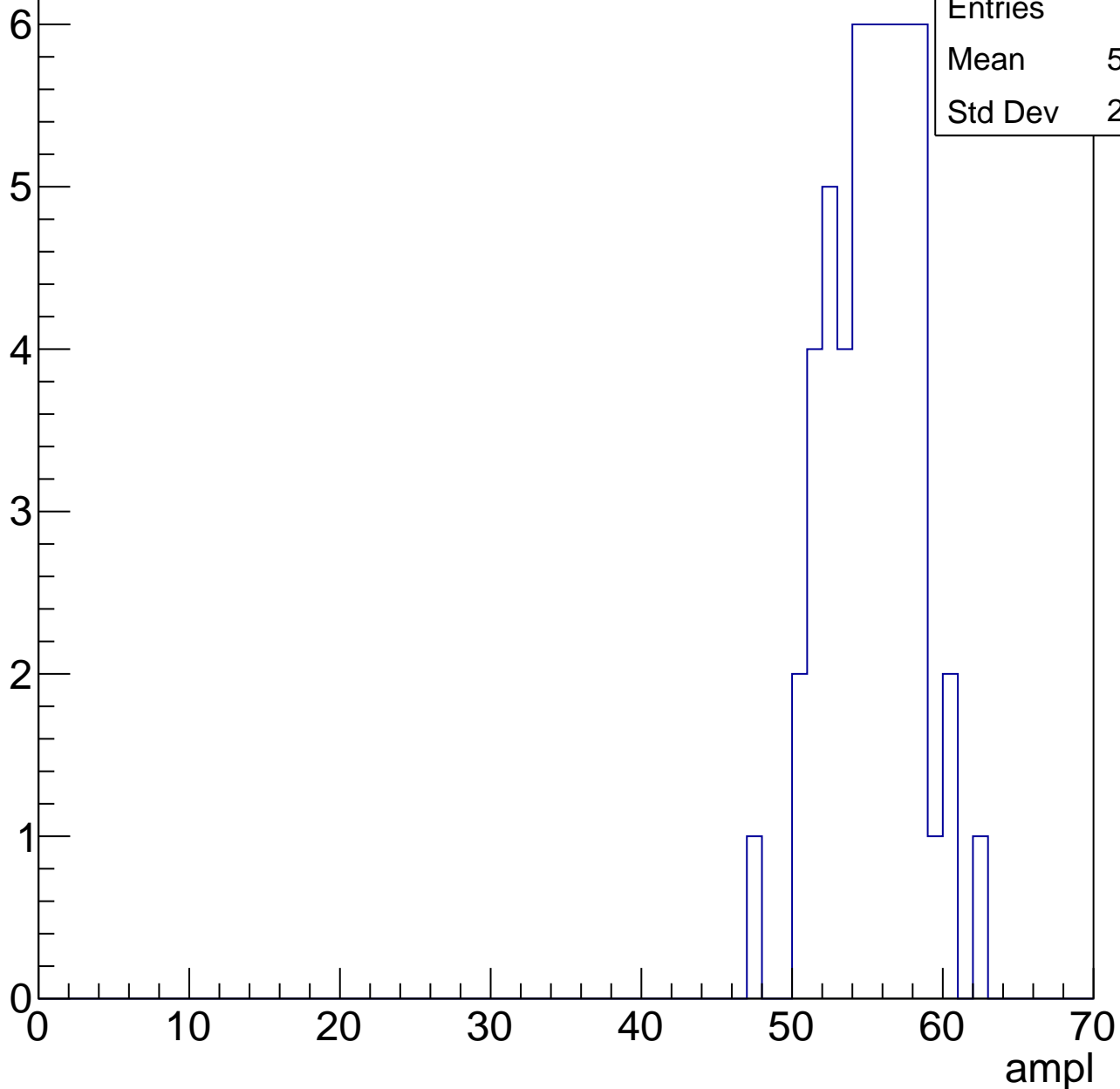


B1L103S, U5-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	54.88
Std Dev	2.978



B1L103S, U5-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

8

6

4

2

0

0

10

20

30

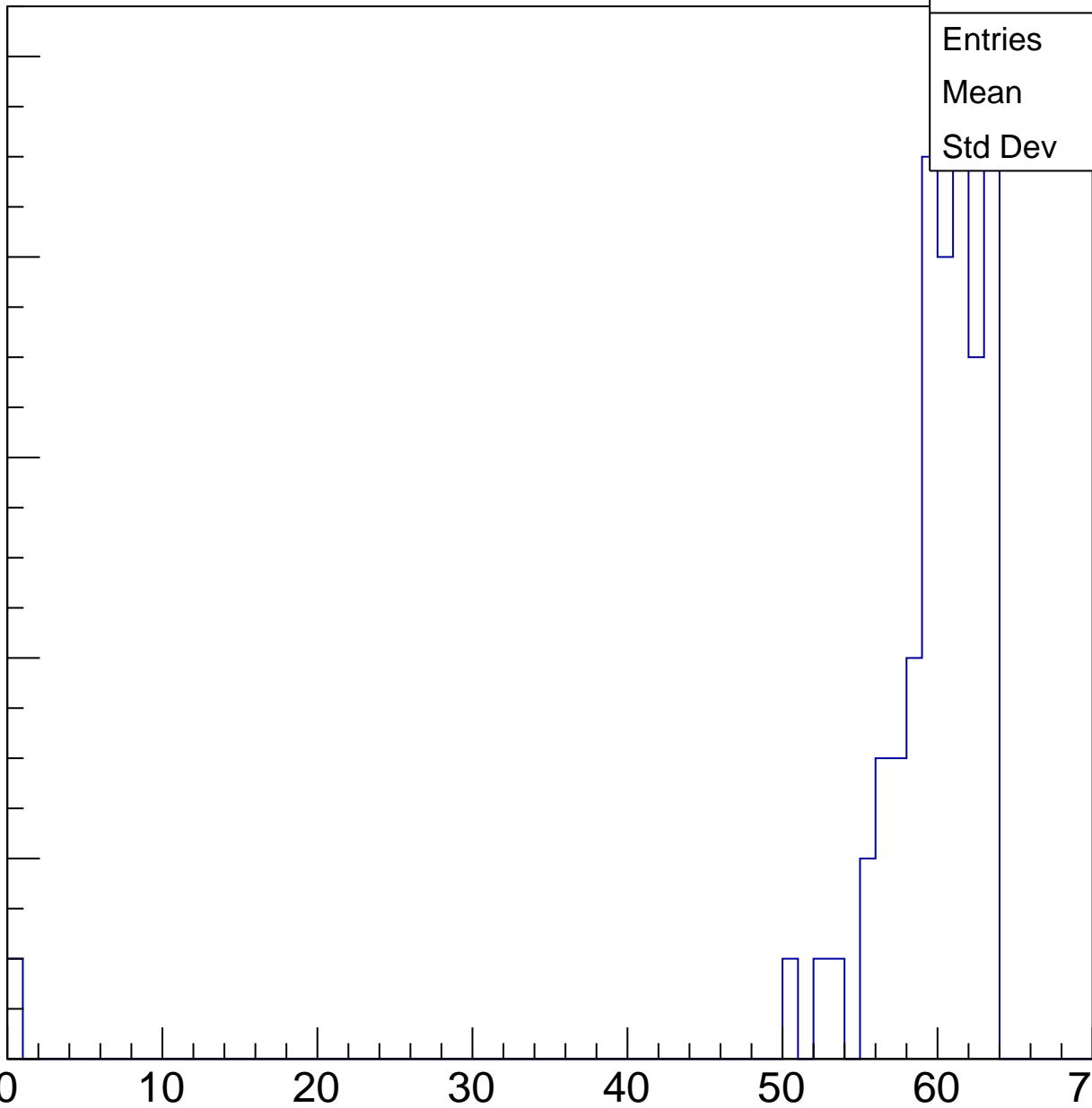
40

50

60

ampl

Entries	59
Mean	58.61
Std Dev	8.201



B1L103S, U5-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

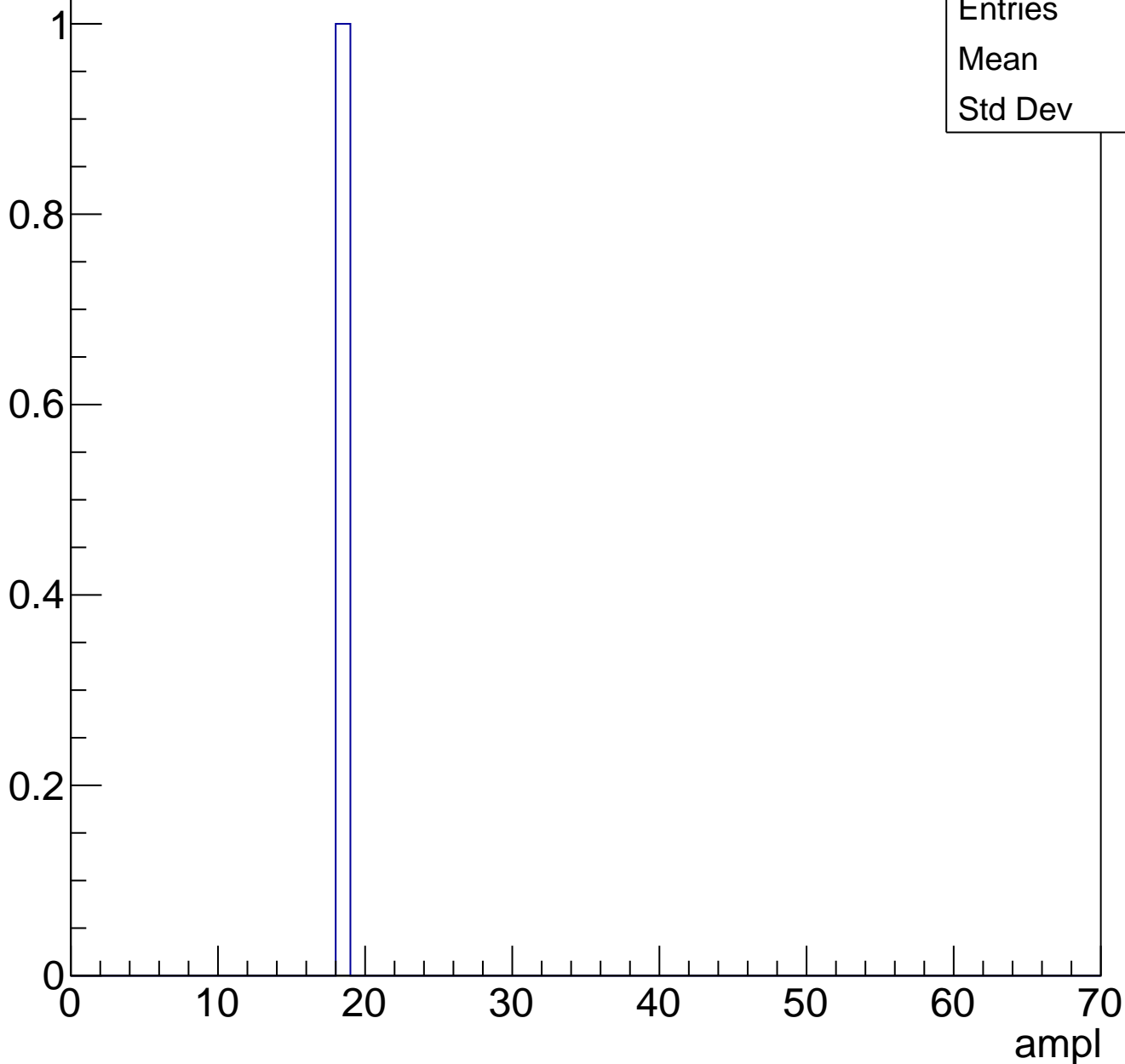
Entry



B1L103S, U5-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch124, adc0

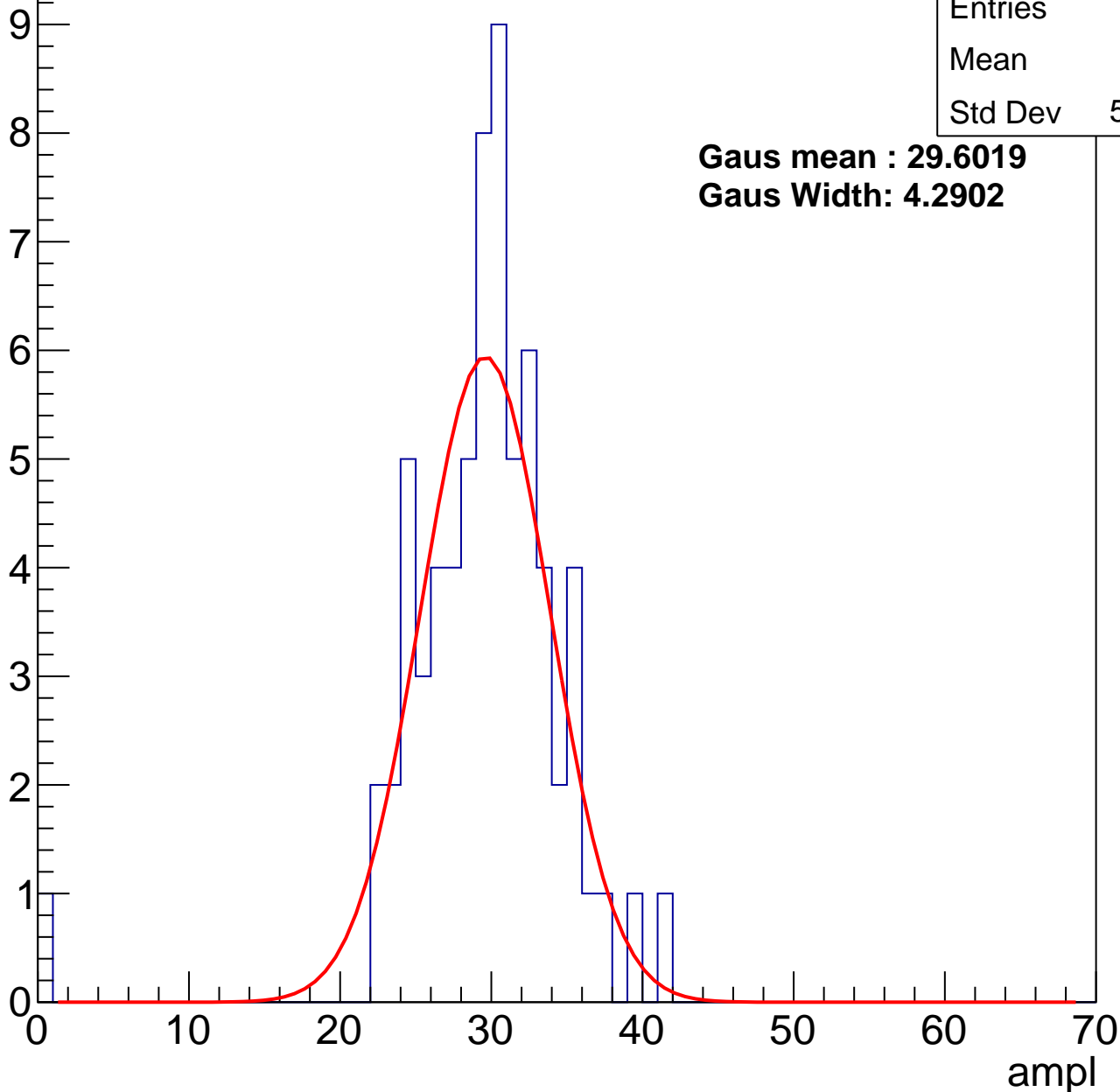
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	29.1
Std Dev	5.339

Gaus mean : 29.6019

Gaus Width: 4.2902



B1L103S, U5-ch124, adc1

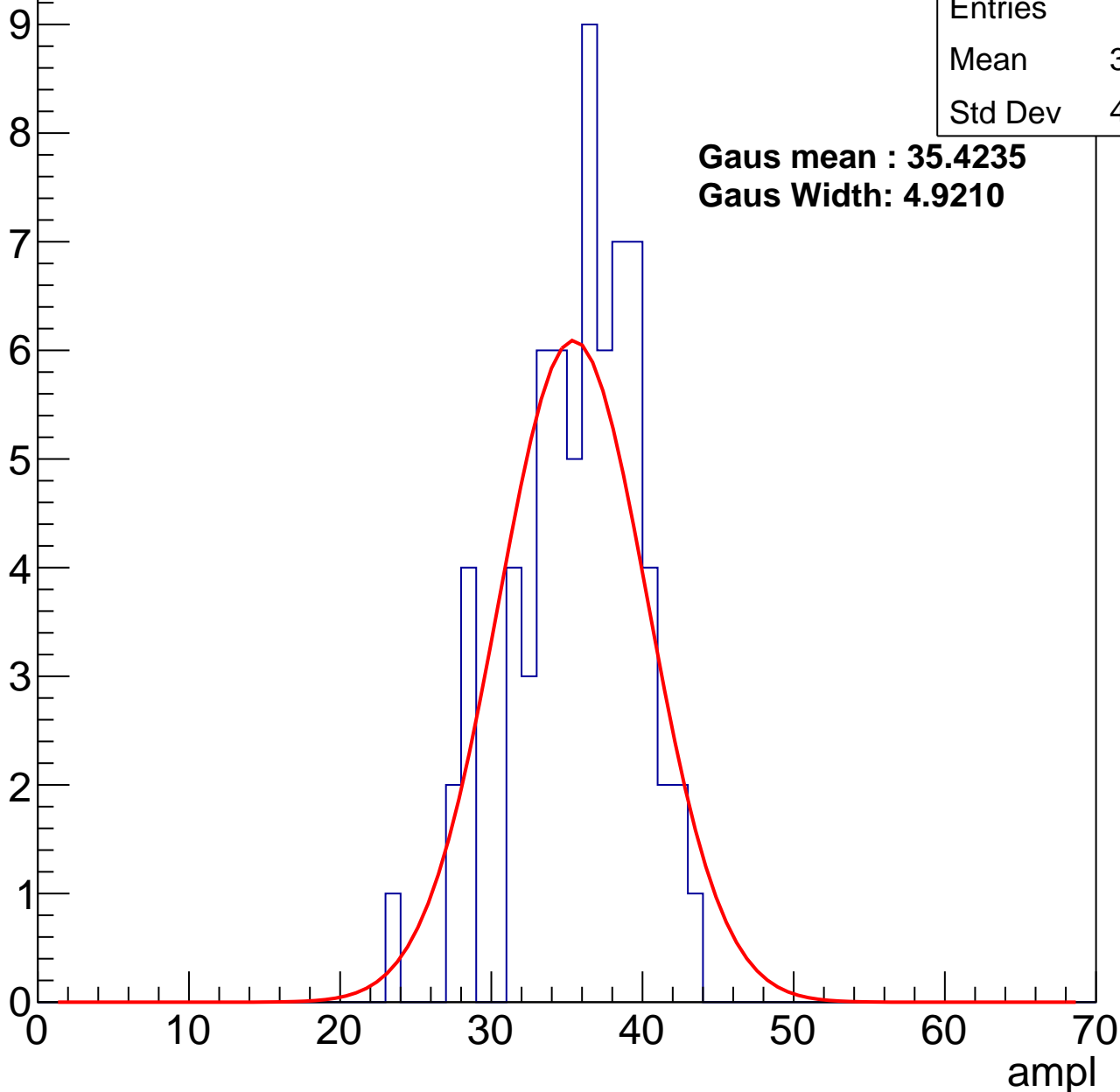
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.36
Std Dev	4.032

Gaus mean : 35.4235

Gaus Width: 4.9210



B1L103S, U5-ch124, adc2

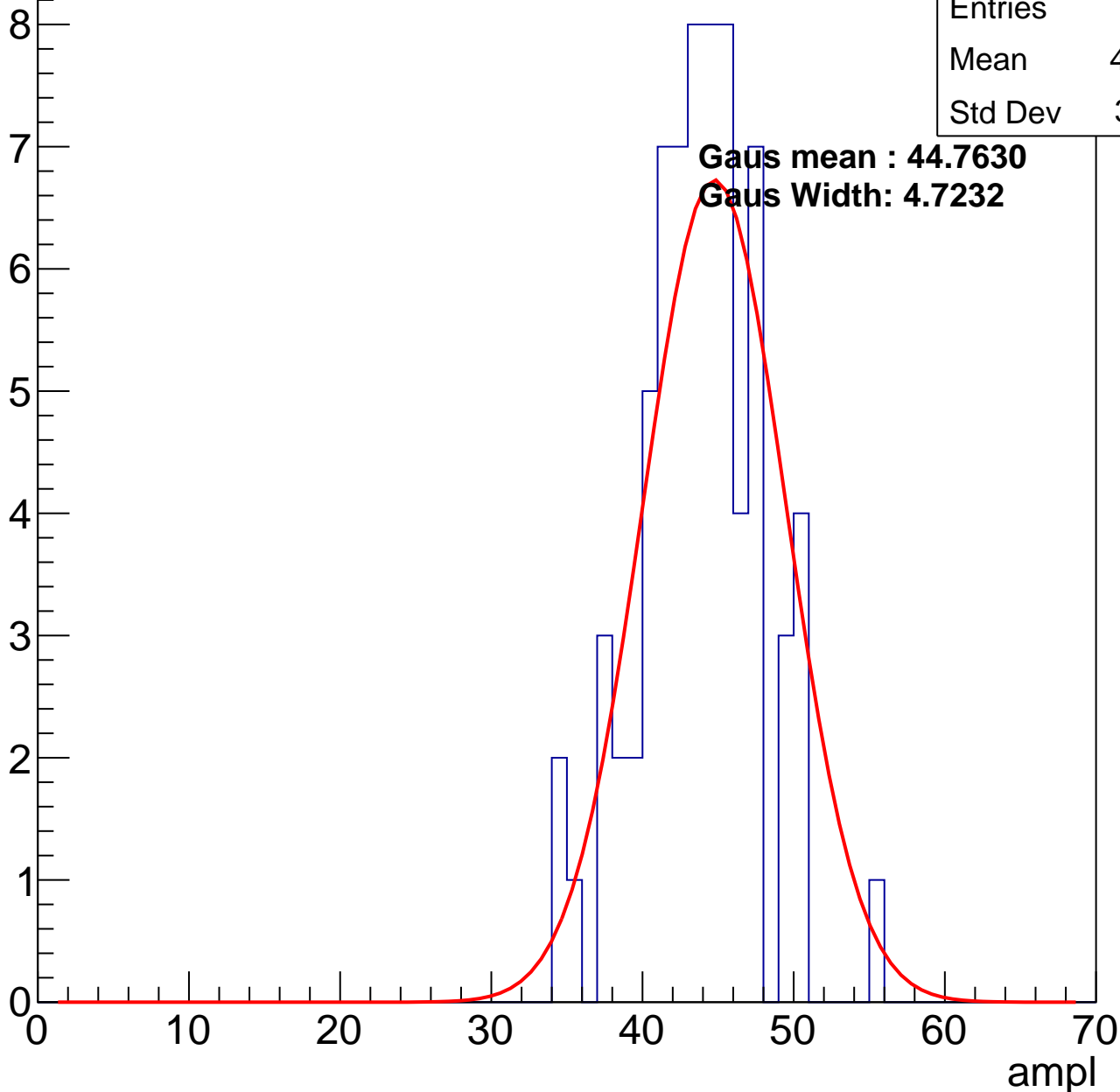
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.33
Std Dev	3.951

Gaus mean : 44.7630

Gaus Width: 4.7232

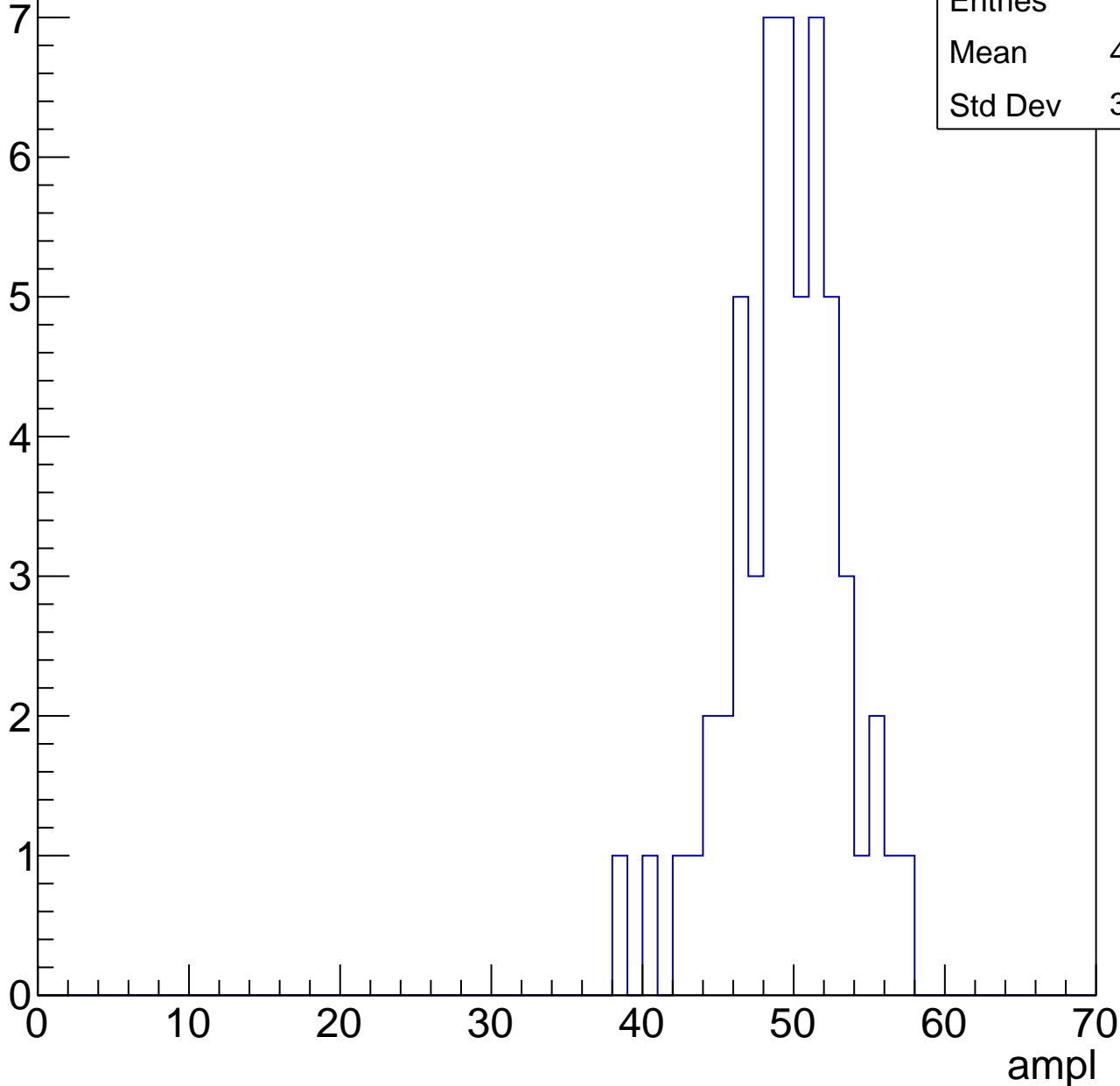


B1L103S, U5-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	48.98
Std Dev	3.739

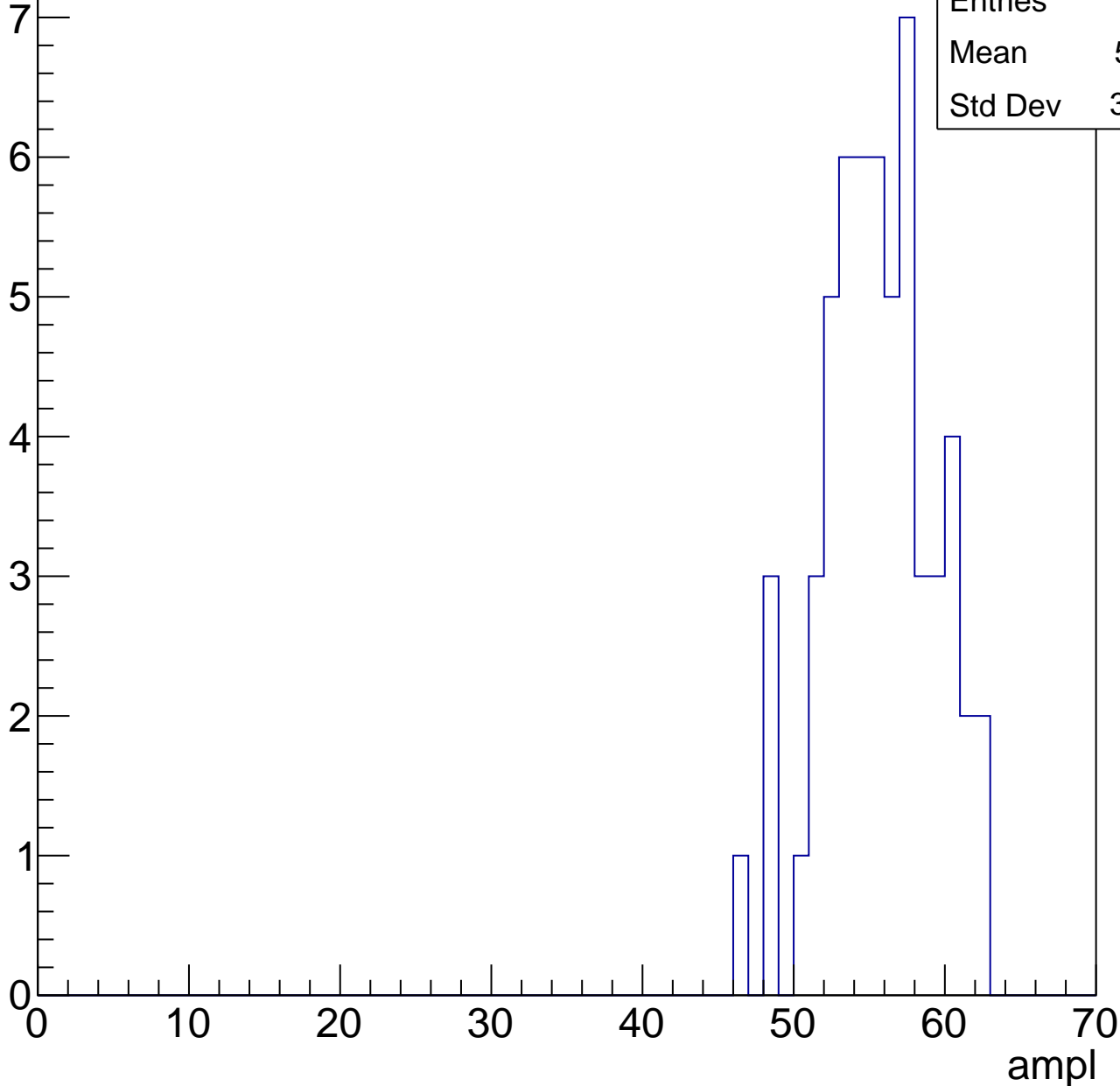


B1L103S, U5-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.11
Std Dev	3.636

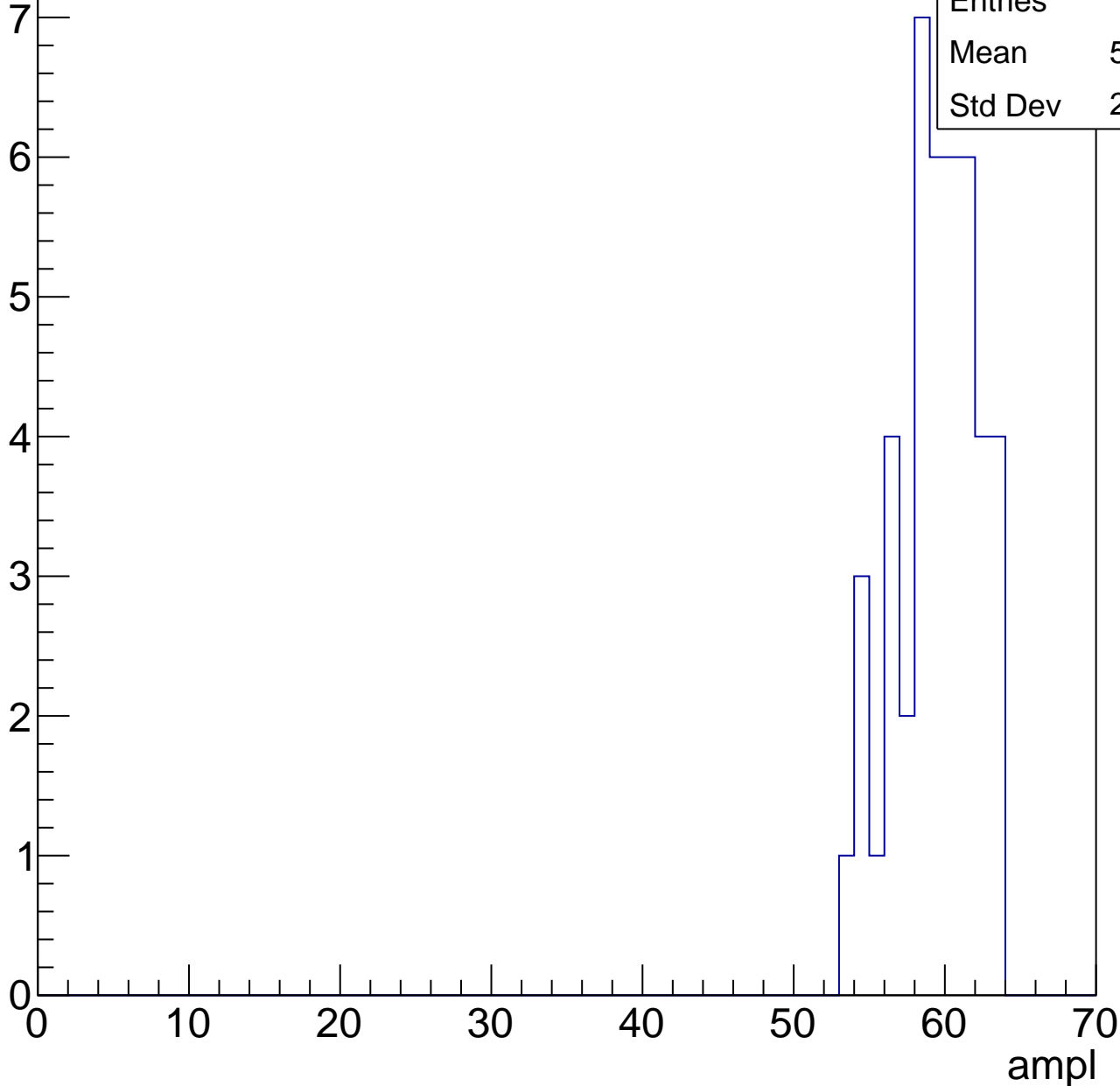


B1L103S, U5-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.95
Std Dev	2.645

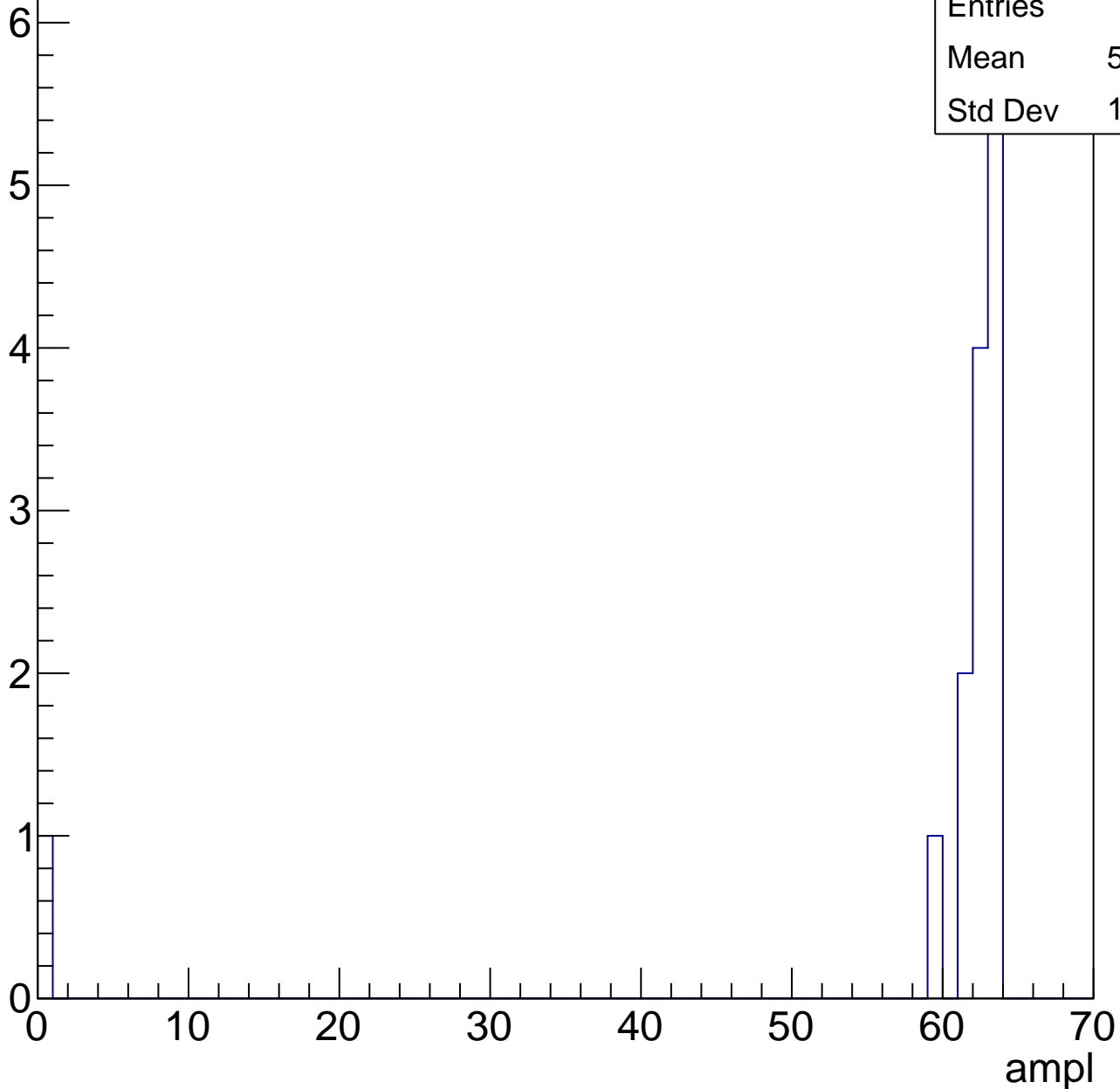


B1L103S, U5-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	57.64
Std Dev	16.03



B1L103S, U5-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U5-ch125, adc0

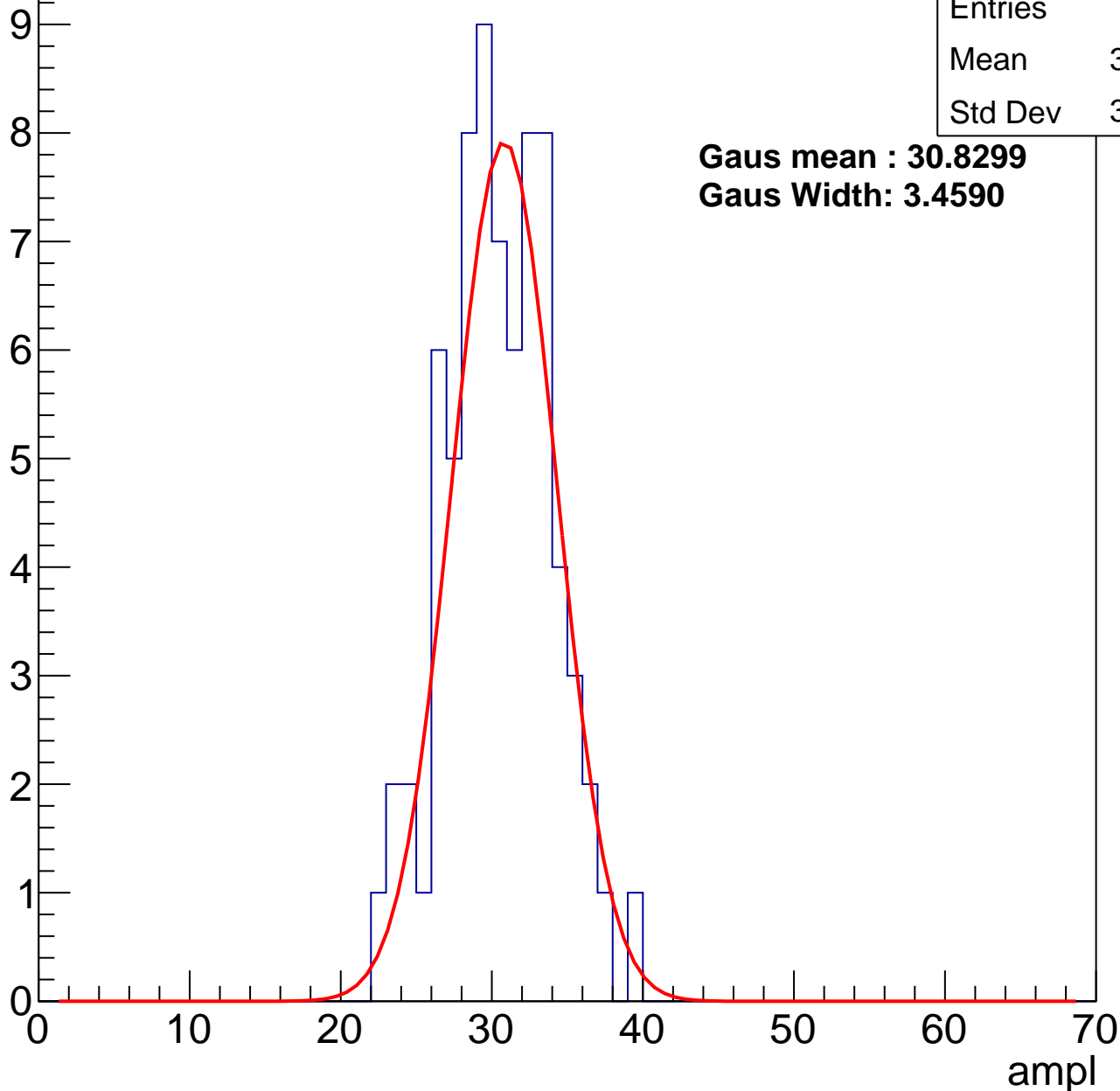
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.03
Std Dev	3.472

Gaus mean : 30.8299

Gaus Width: 3.4590



B1L103S, U5-ch125, adc1

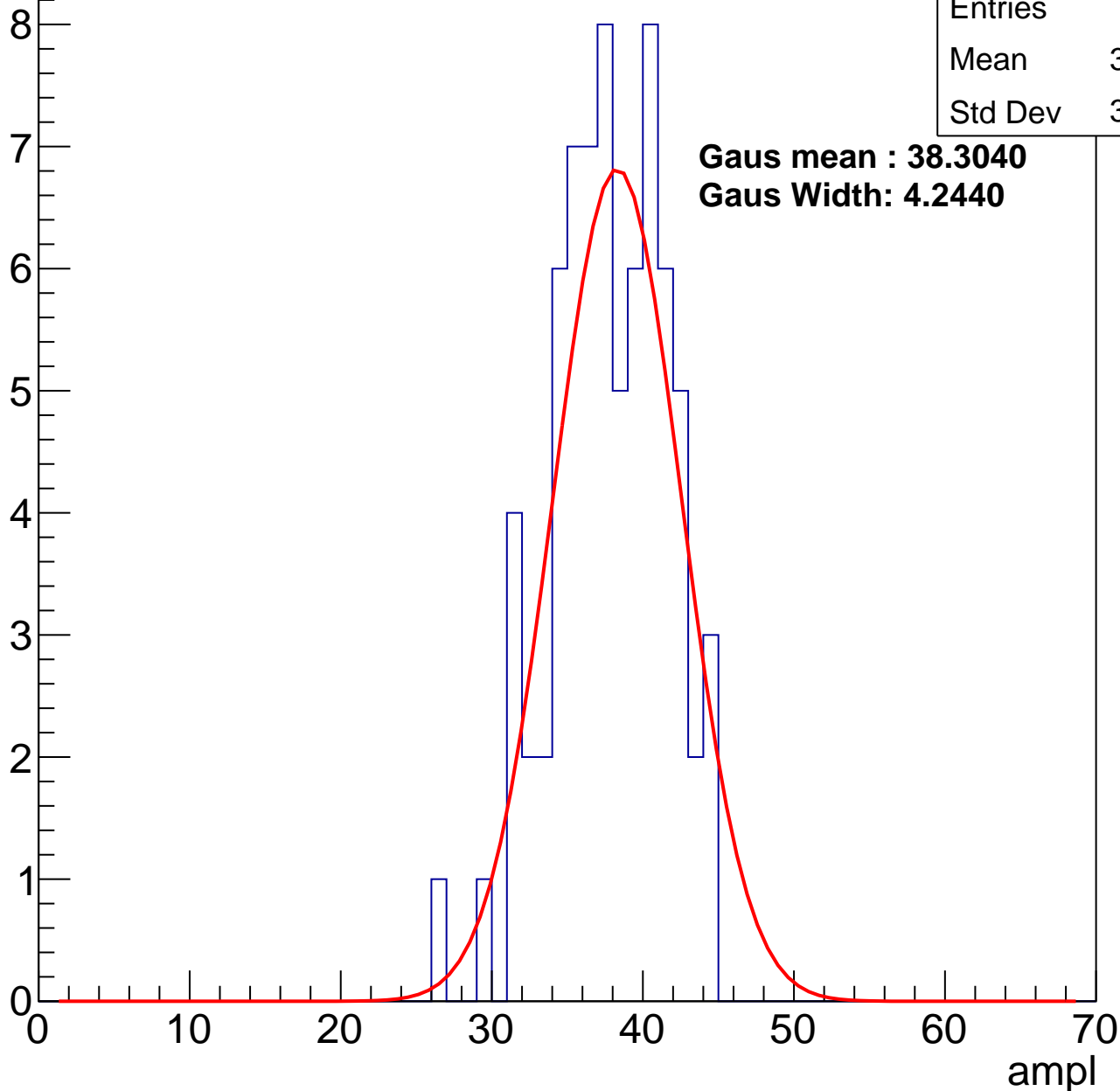
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	37.32
Std Dev	3.763

Gaus mean : 38.3040

Gaus Width: 4.2440



B1L103S, U5-ch125, adc2

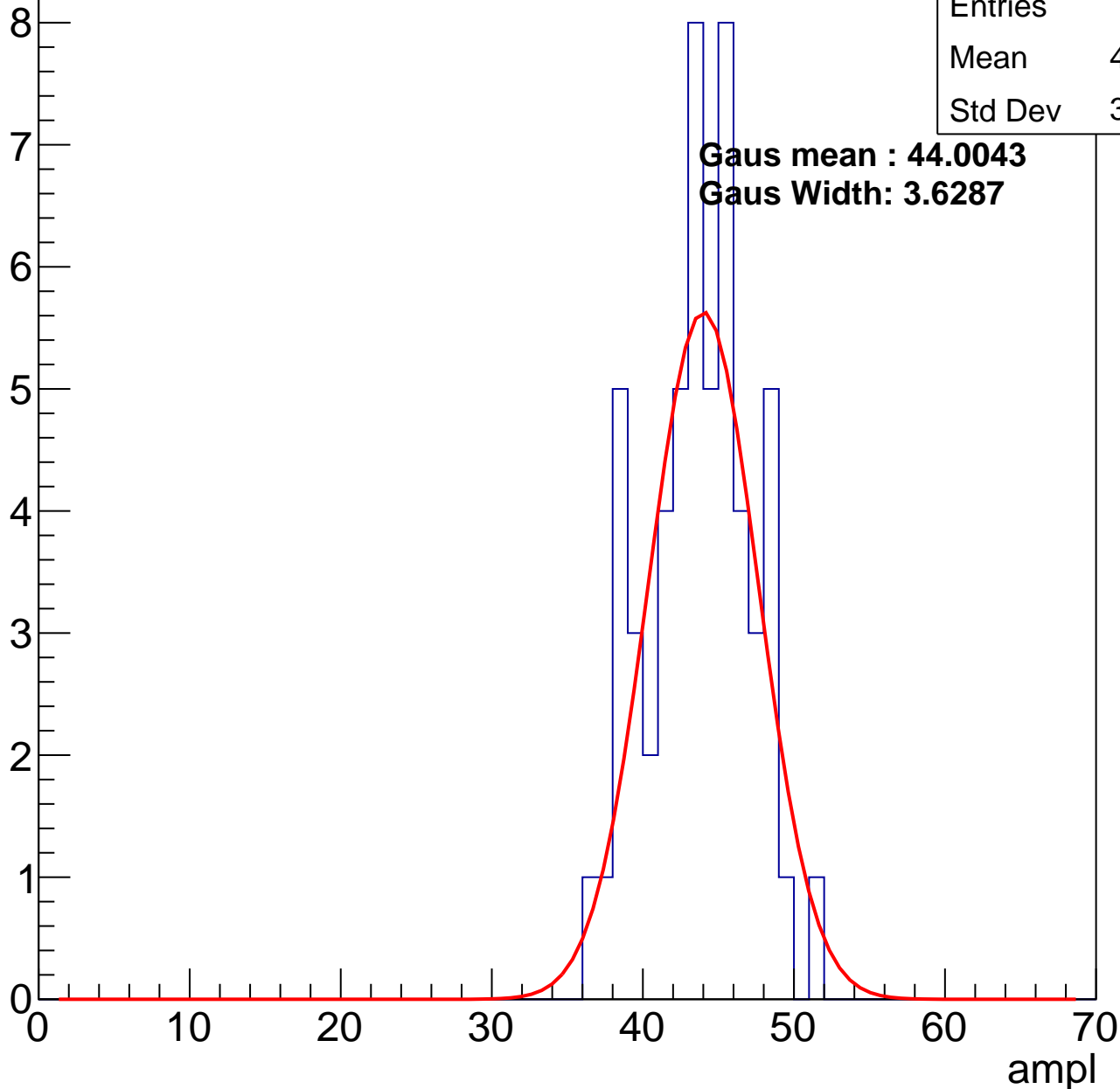
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.27
Std Dev	3.378

Gaus mean : 44.0043

Gaus Width: 3.6287



B1L103S, U5-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	50.03
Std Dev	2.957

Entry

10

8

6

4

2

0

0

10

20

30

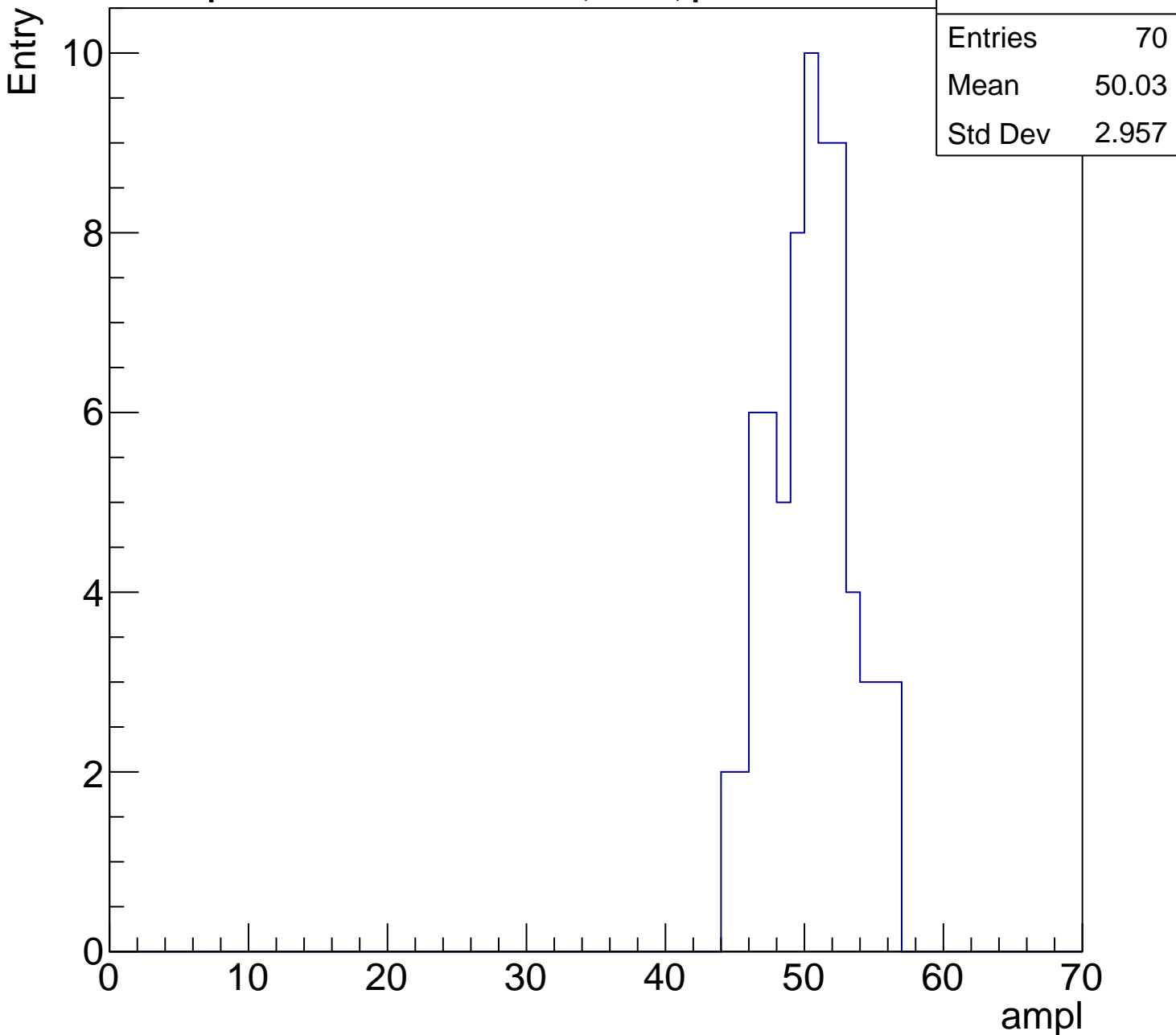
40

50

60

70

ampl

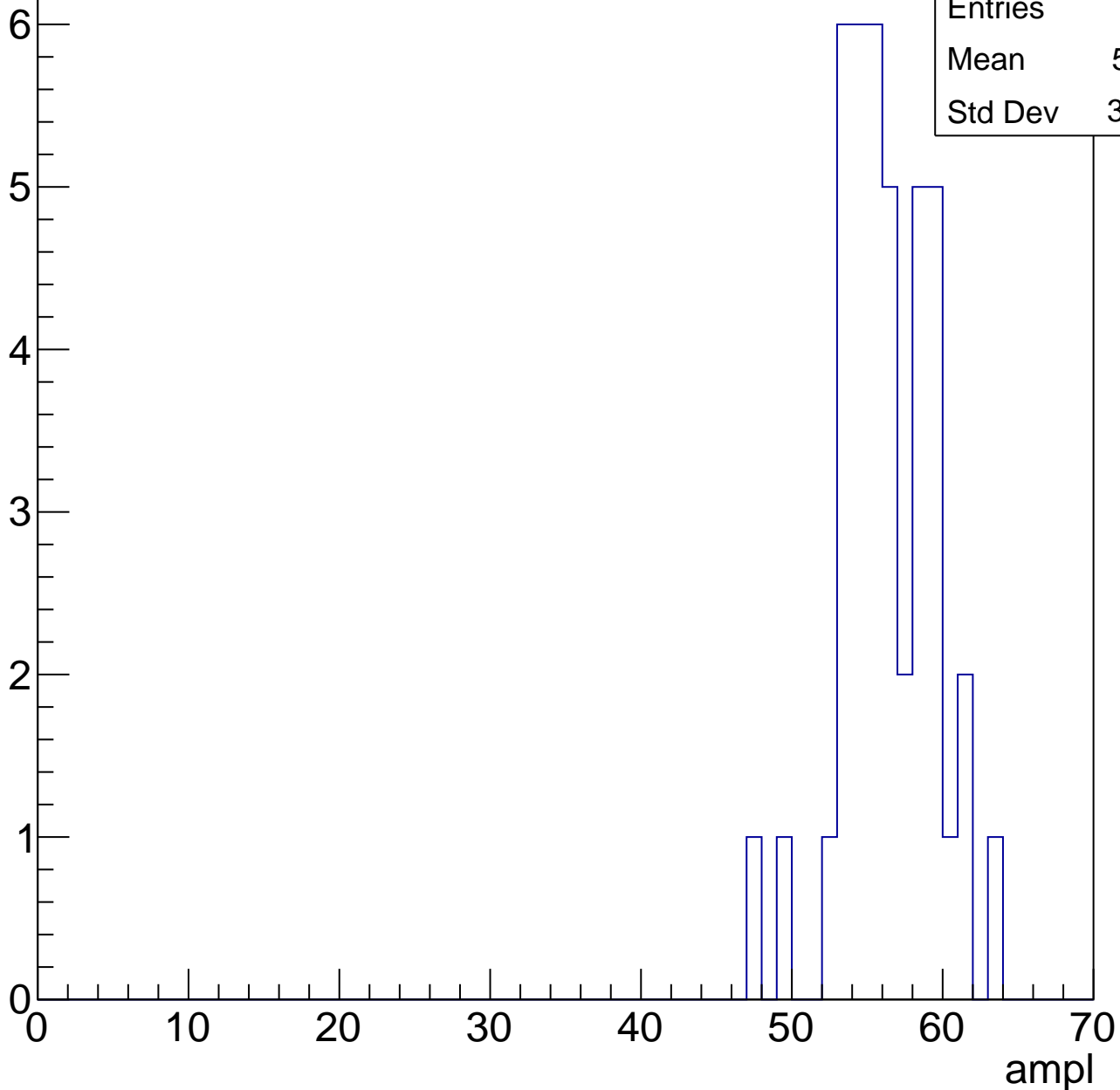


B1L103S, U5-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	55.81
Std Dev	3.126



B1L103S, U5-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	43
Mean	59.47
Std Dev	2.326

ampl

0

10

20

30

40

50

60

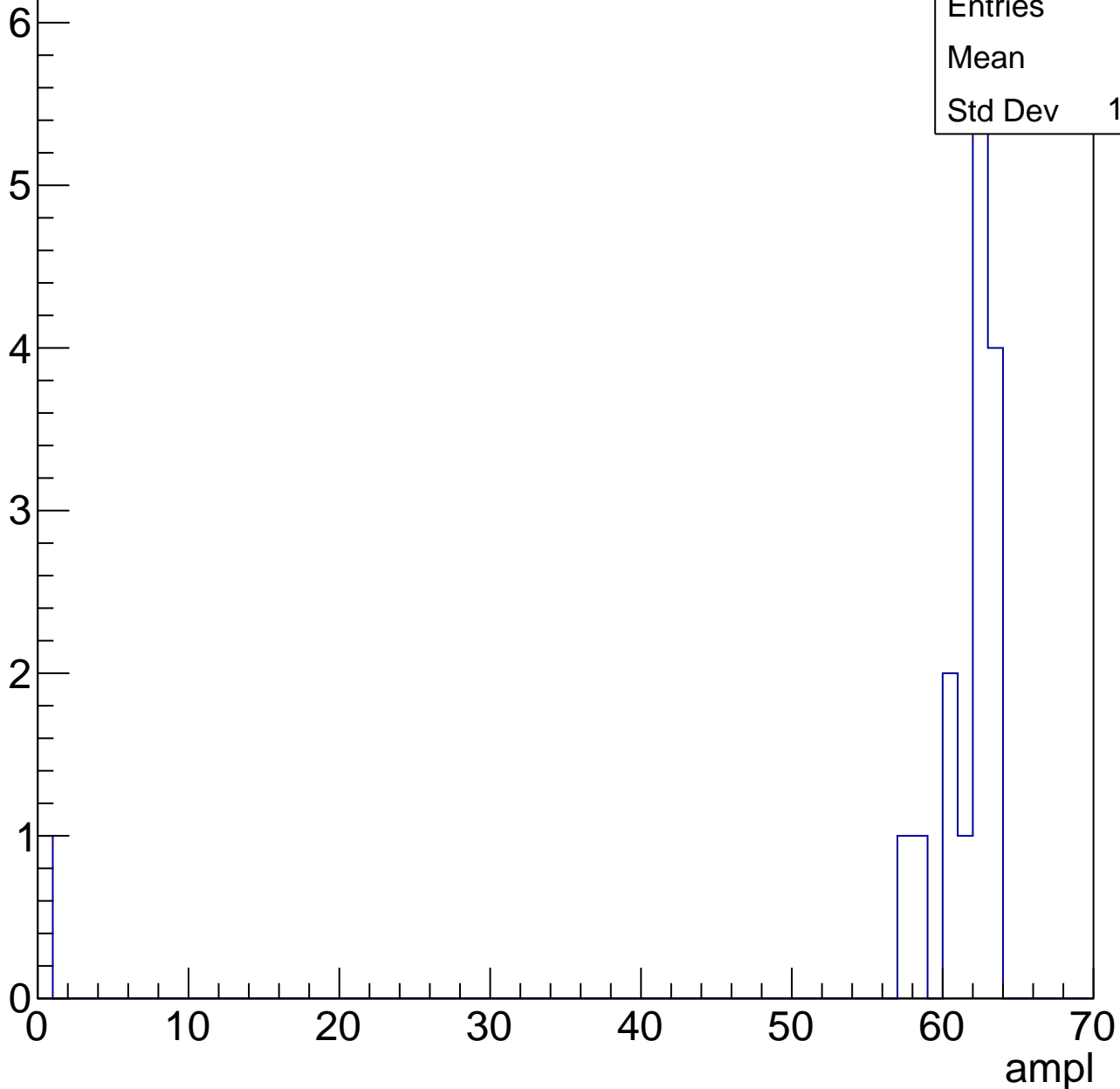
70

B1L103S, U5-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	57.5
Std Dev	14.95



B1L103S, U5-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U5-ch126, adc0

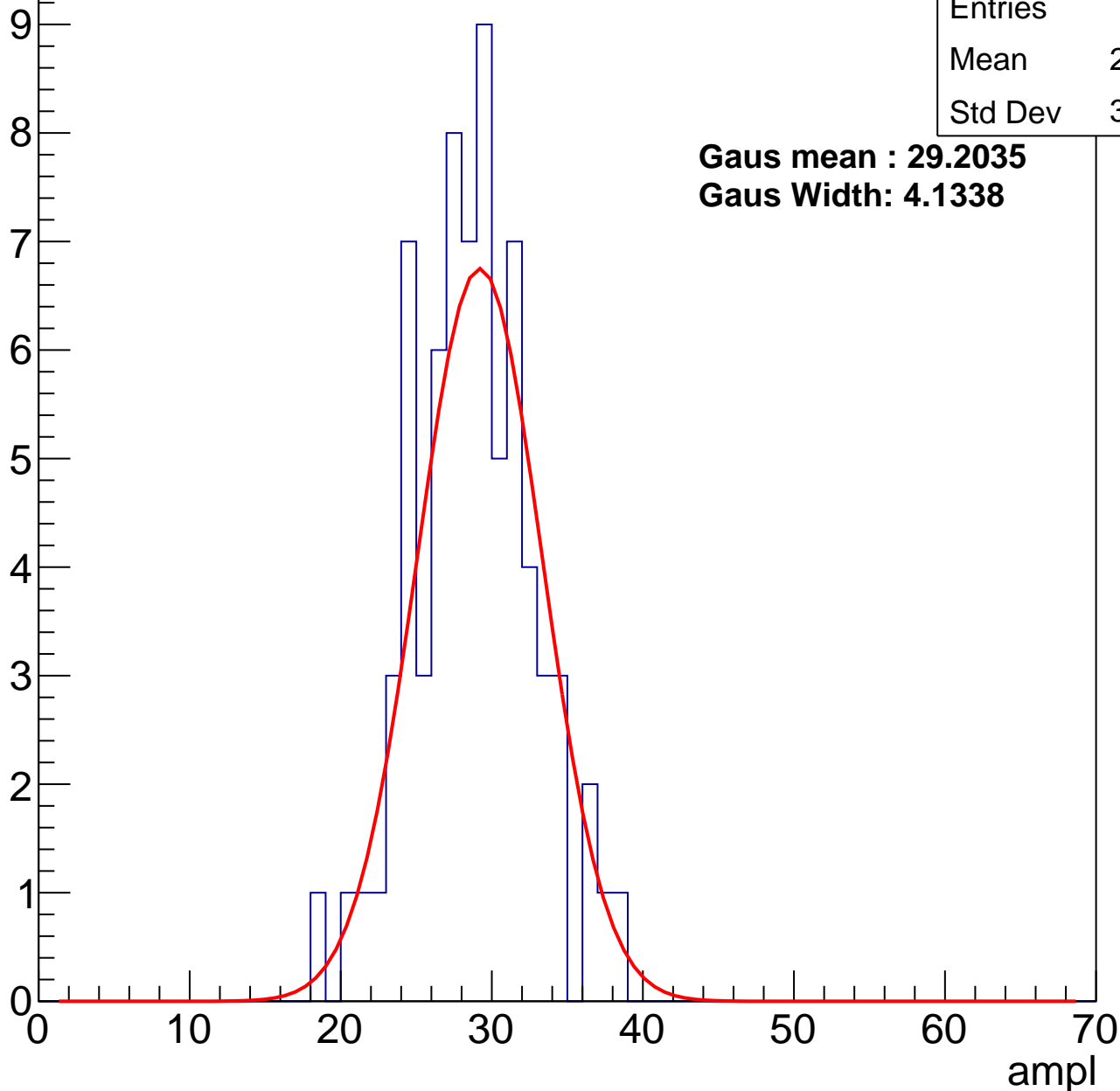
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.29
Std Dev	3.946

Gaus mean : 29.2035

Gaus Width: 4.1338



B1L103S, U5-ch126, adc1

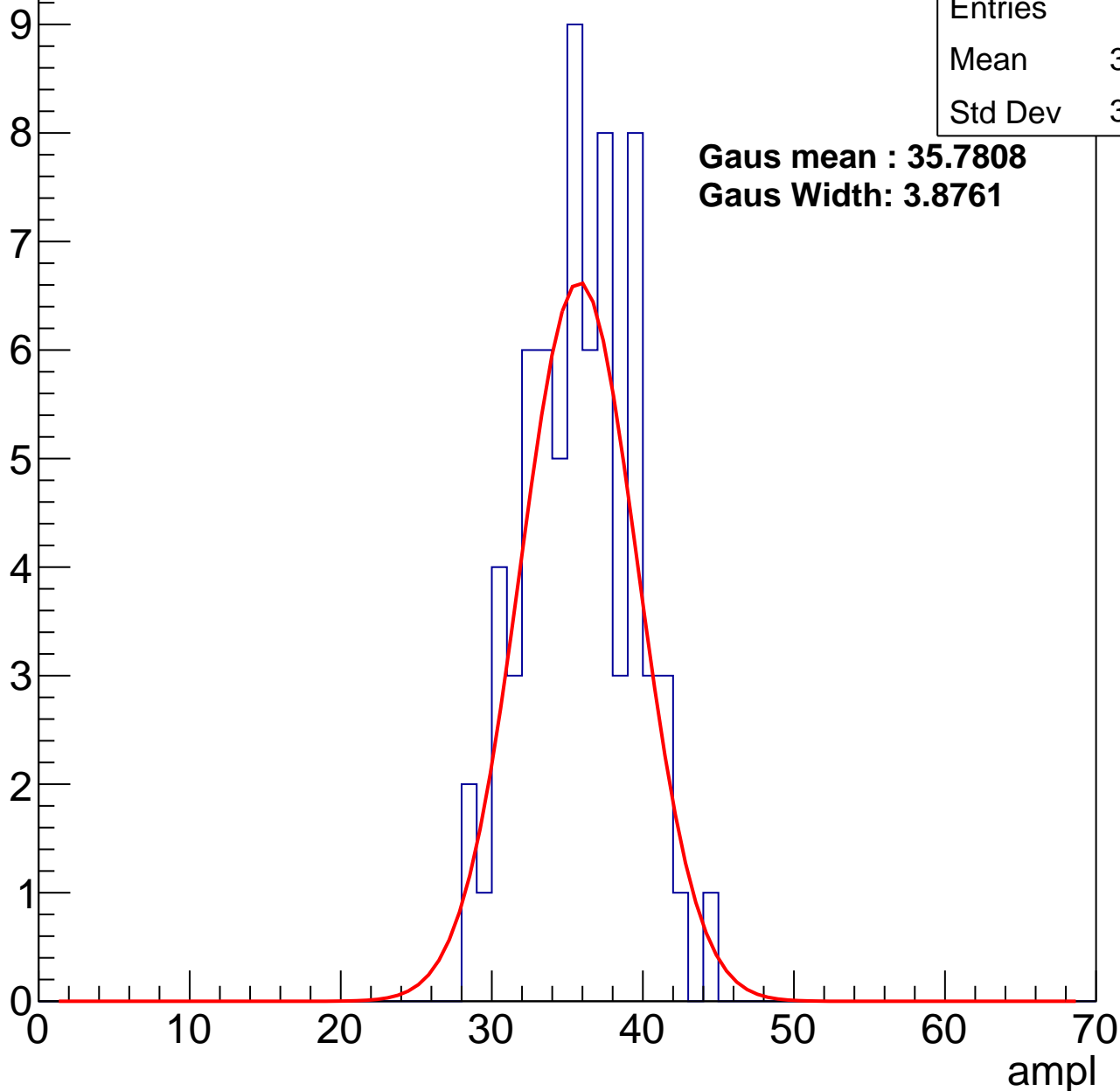
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.36
Std Dev	3.534

Gaus mean : 35.7808

Gaus Width: 3.8761



B1L103S, U5-ch126, adc2

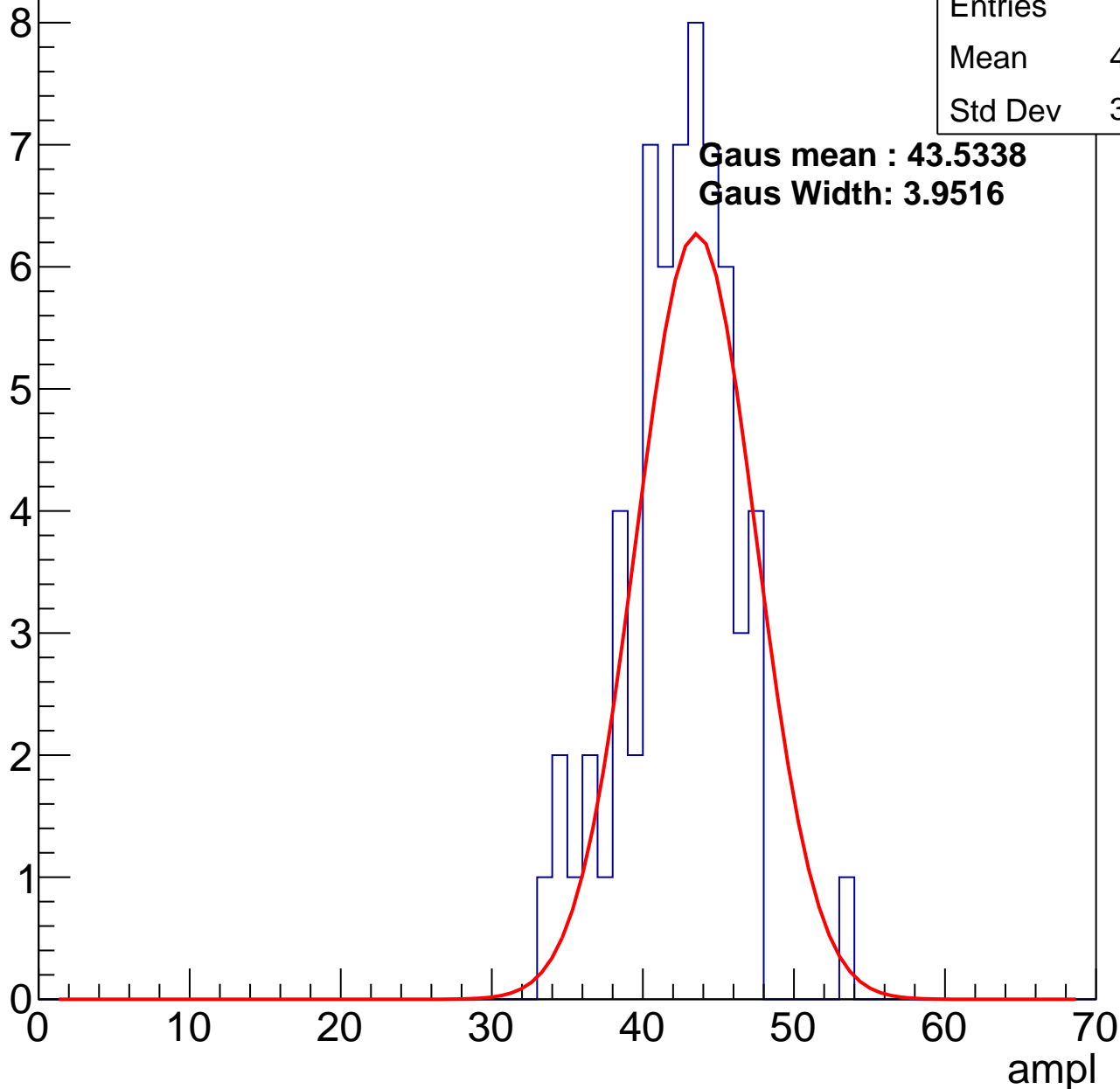
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.87
Std Dev	3.657

Gaus mean : 43.5338

Gaus Width: 3.9516

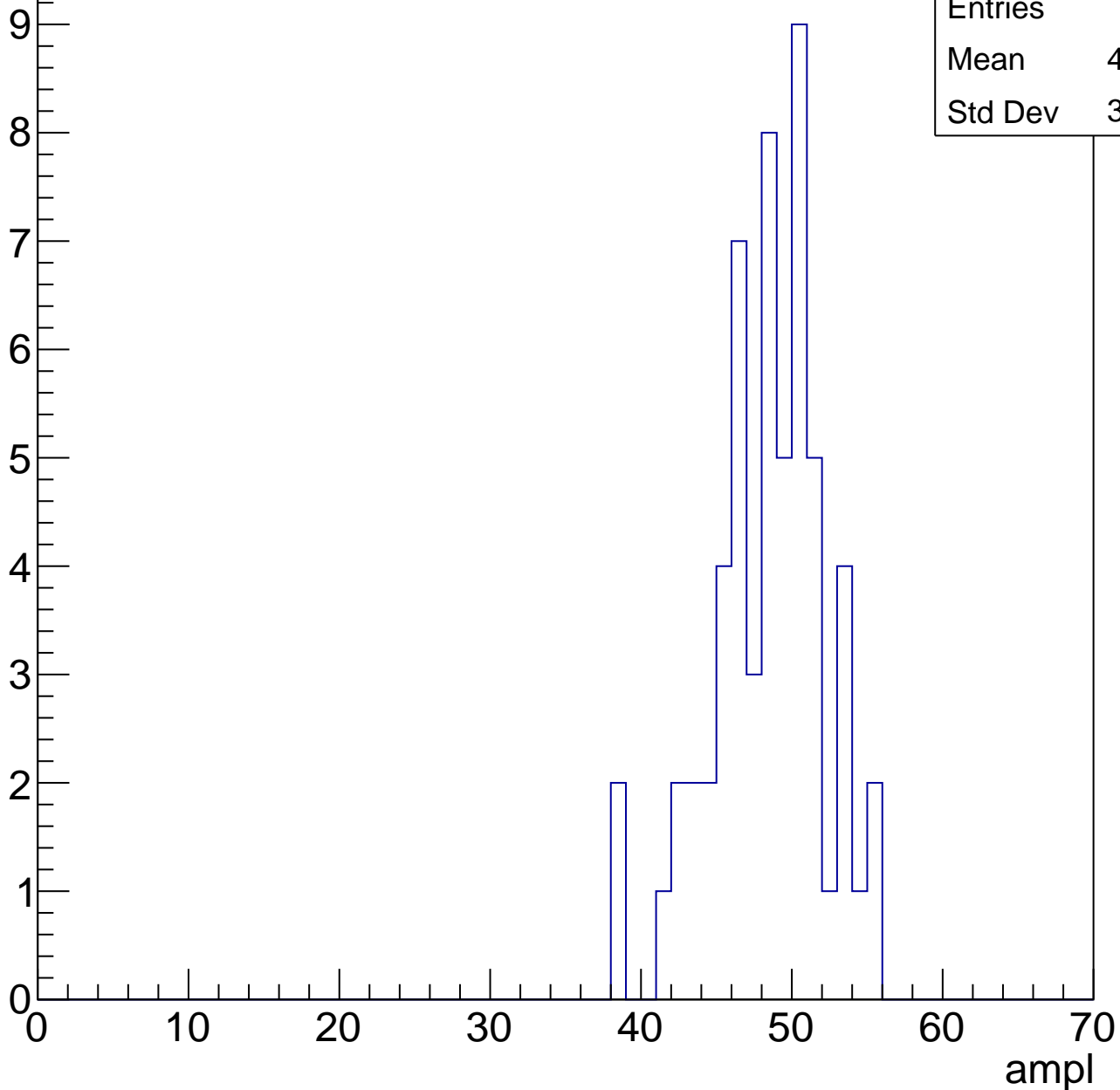


B1L103S, U5-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	47.93
Std Dev	3.727

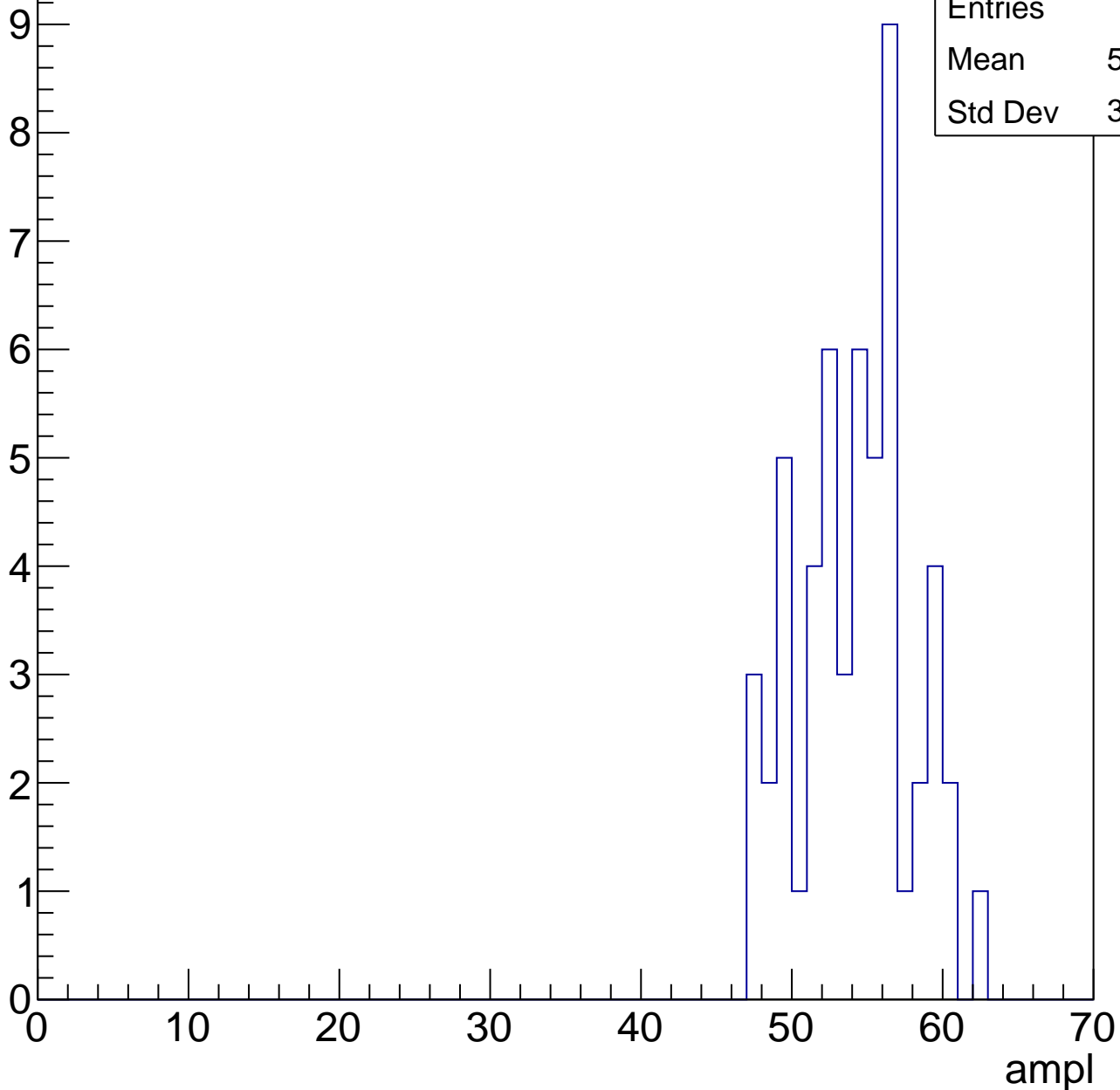


B1L103S, U5-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	53.72
Std Dev	3.694

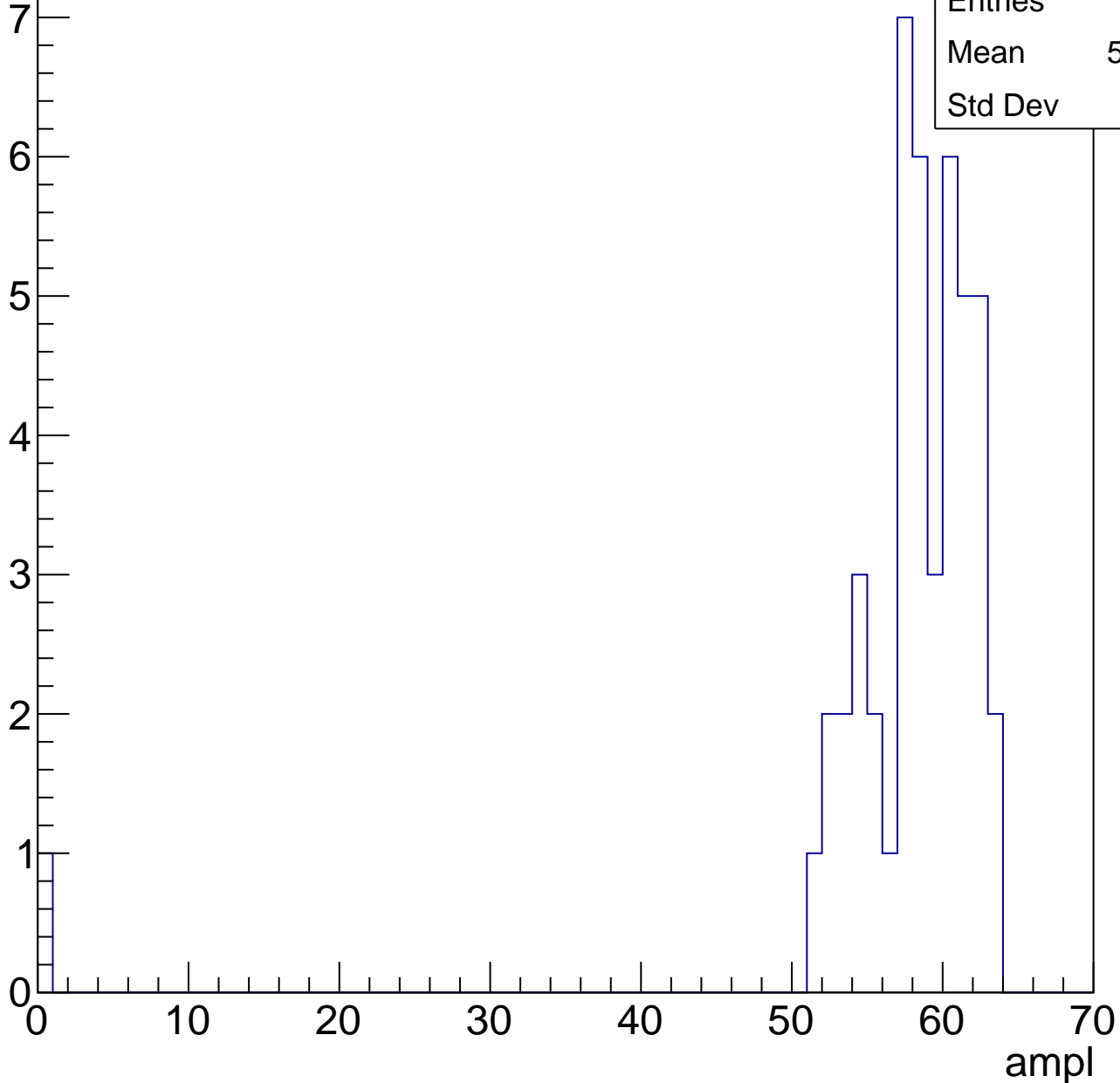


B1L103S, U5-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	56.83
Std Dev	9.03

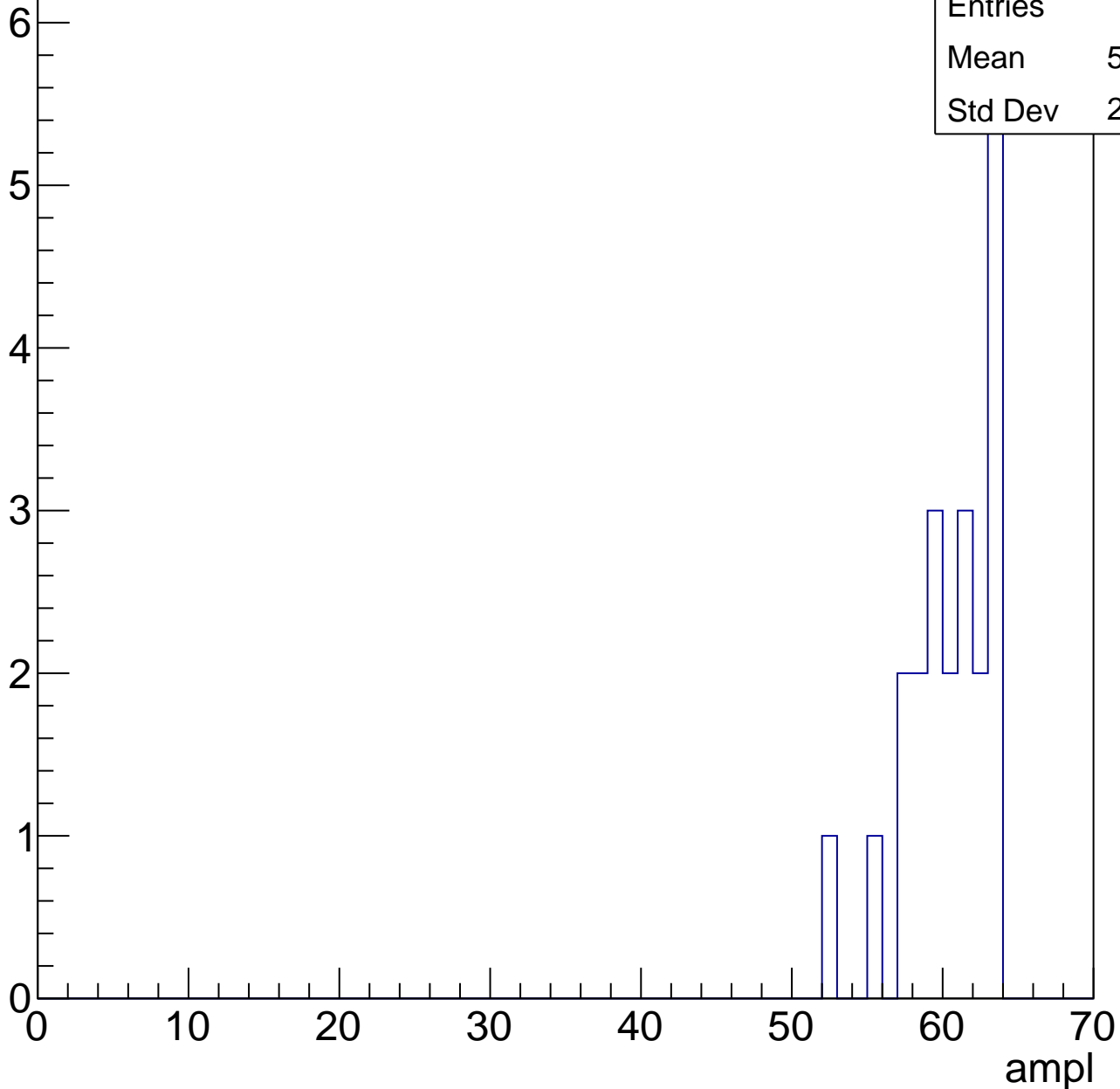


B1L103S, U5-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	22
Mean	59.95
Std Dev	2.884



B1L103S, U5-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	6
Mean	62
Std Dev	0.8165

B1L103S, U5-ch127, adc0

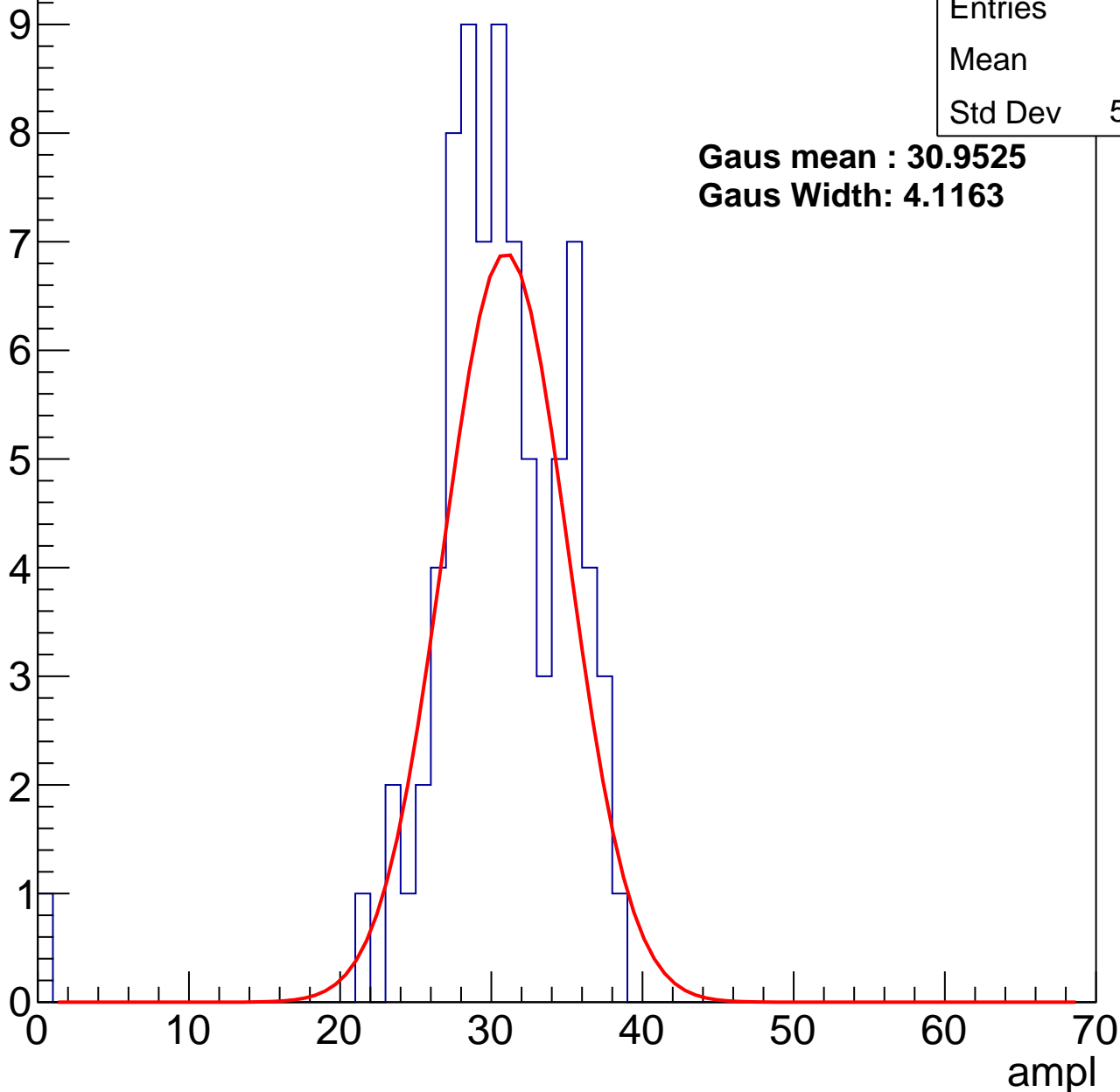
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	30
Std Dev	5.042

Gaus mean : 30.9525

Gaus Width: 4.1163



B1L103S, U5-ch127, adc1

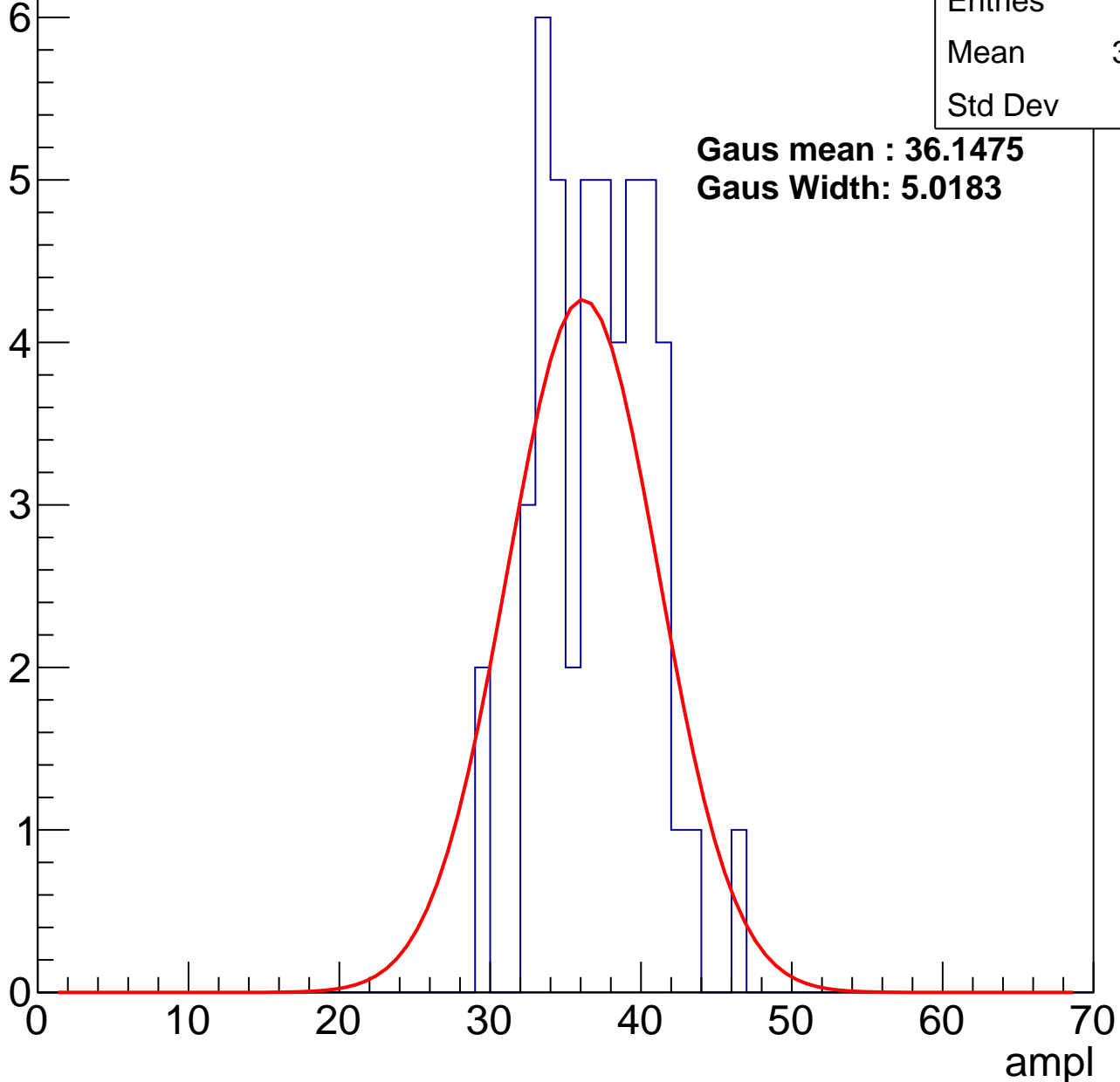
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	36.71
Std Dev	3.58

Gaus mean : 36.1475

Gaus Width: 5.0183



B1L103S, U5-ch127, adc2

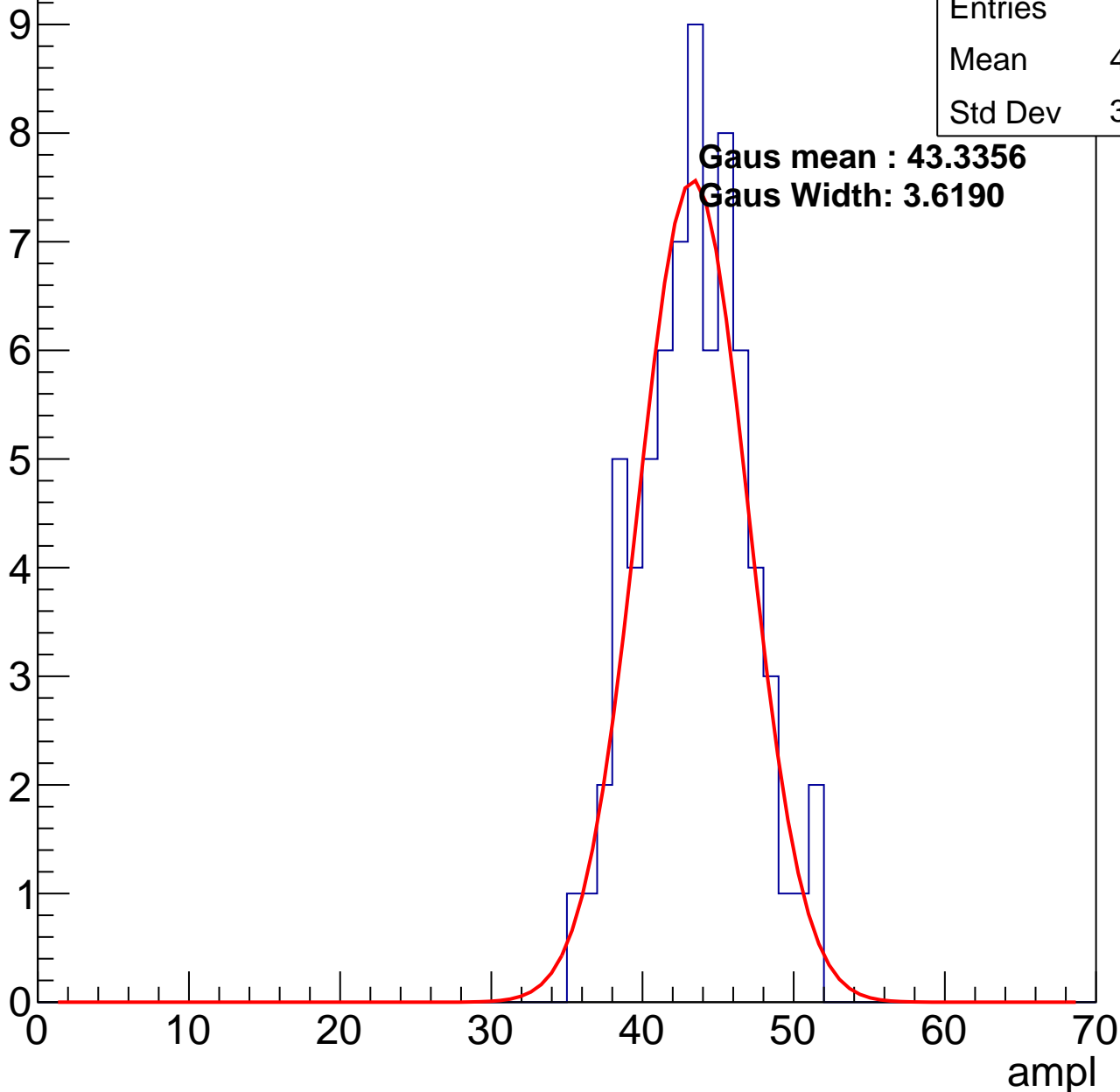
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.97
Std Dev	3.548

Gaus mean : 43.3356

Gaus Width: 3.6190

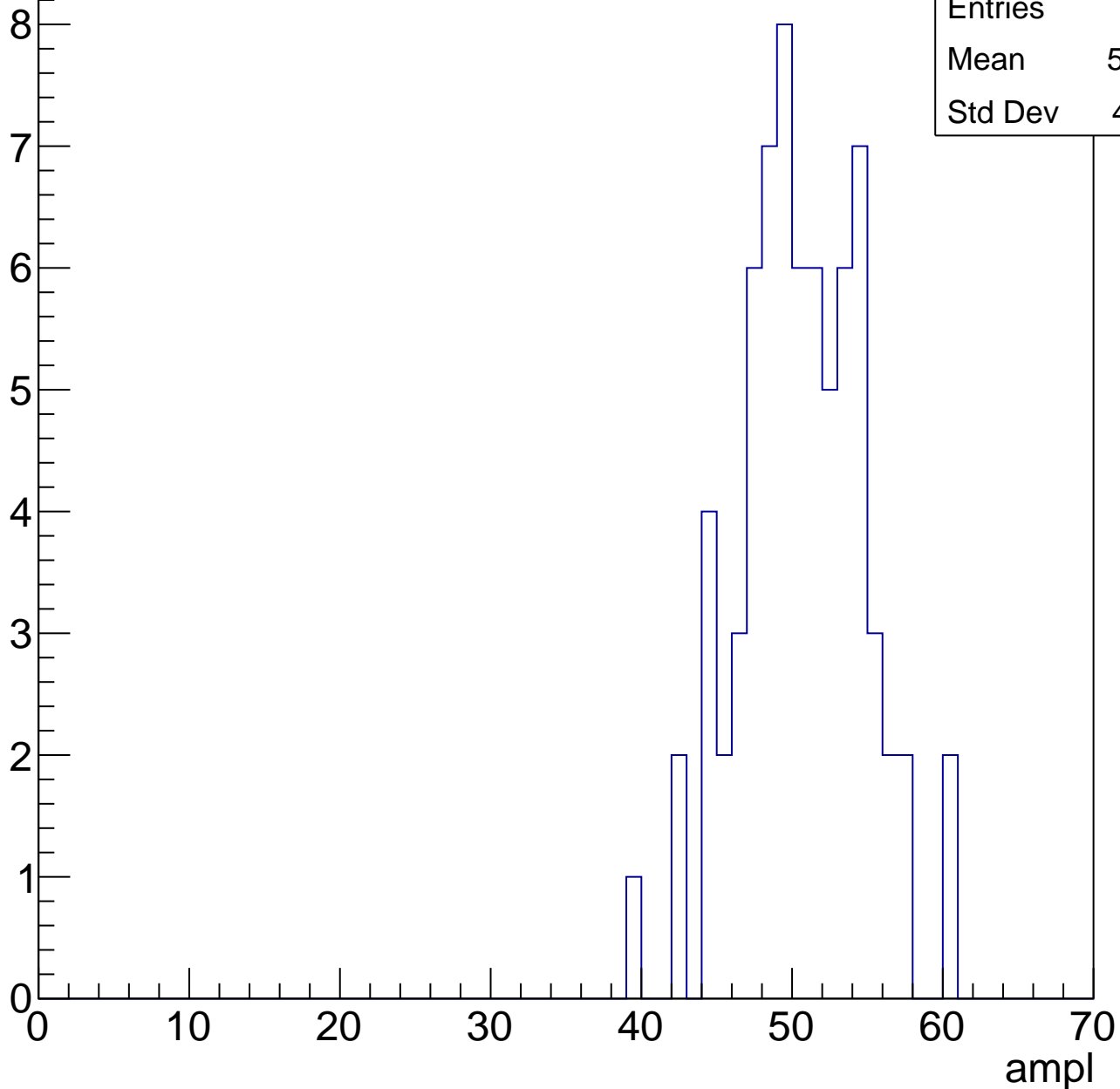


B1L103S, U5-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	50.14
Std Dev	4.111

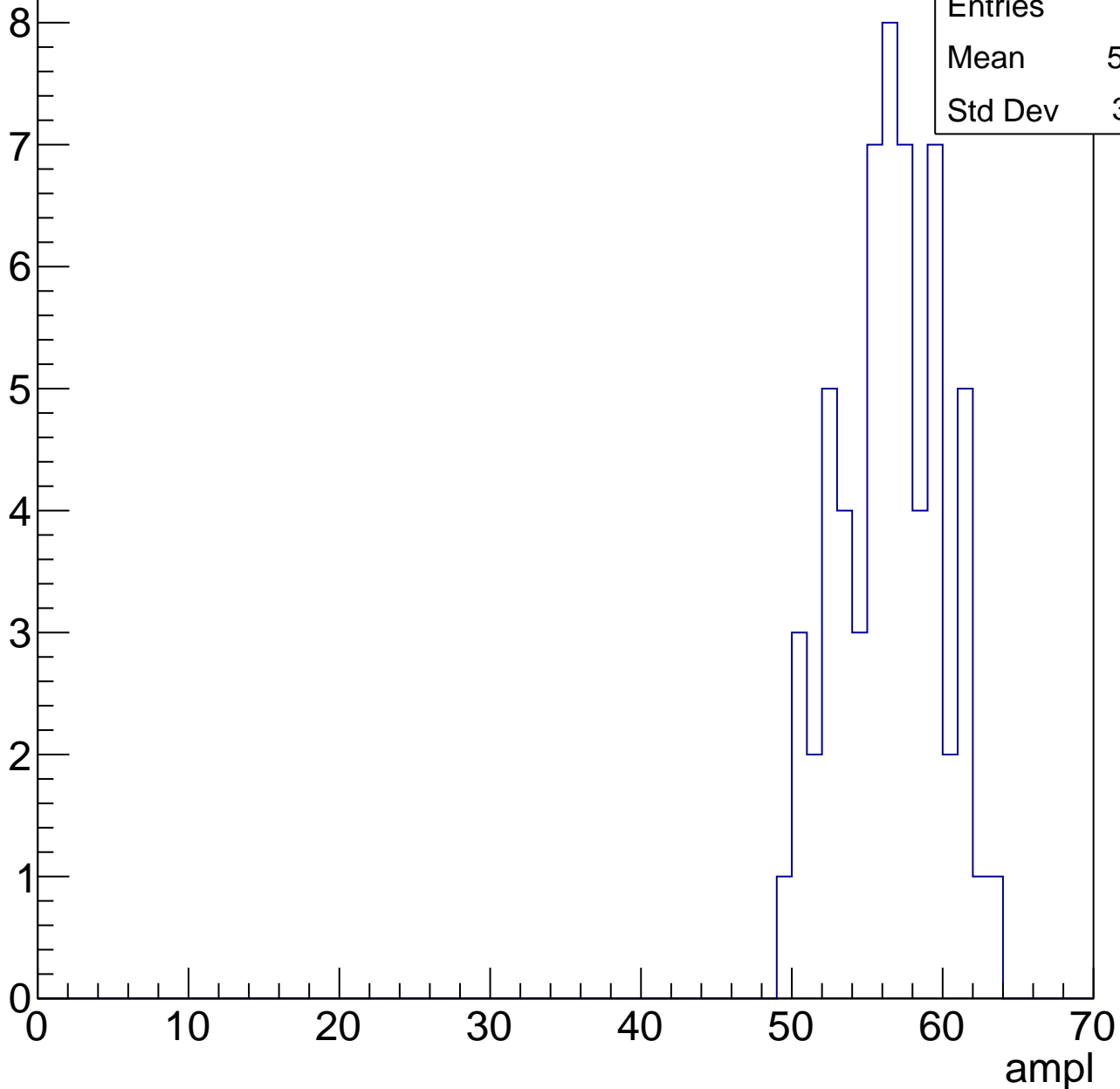


B1L103S, U5-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

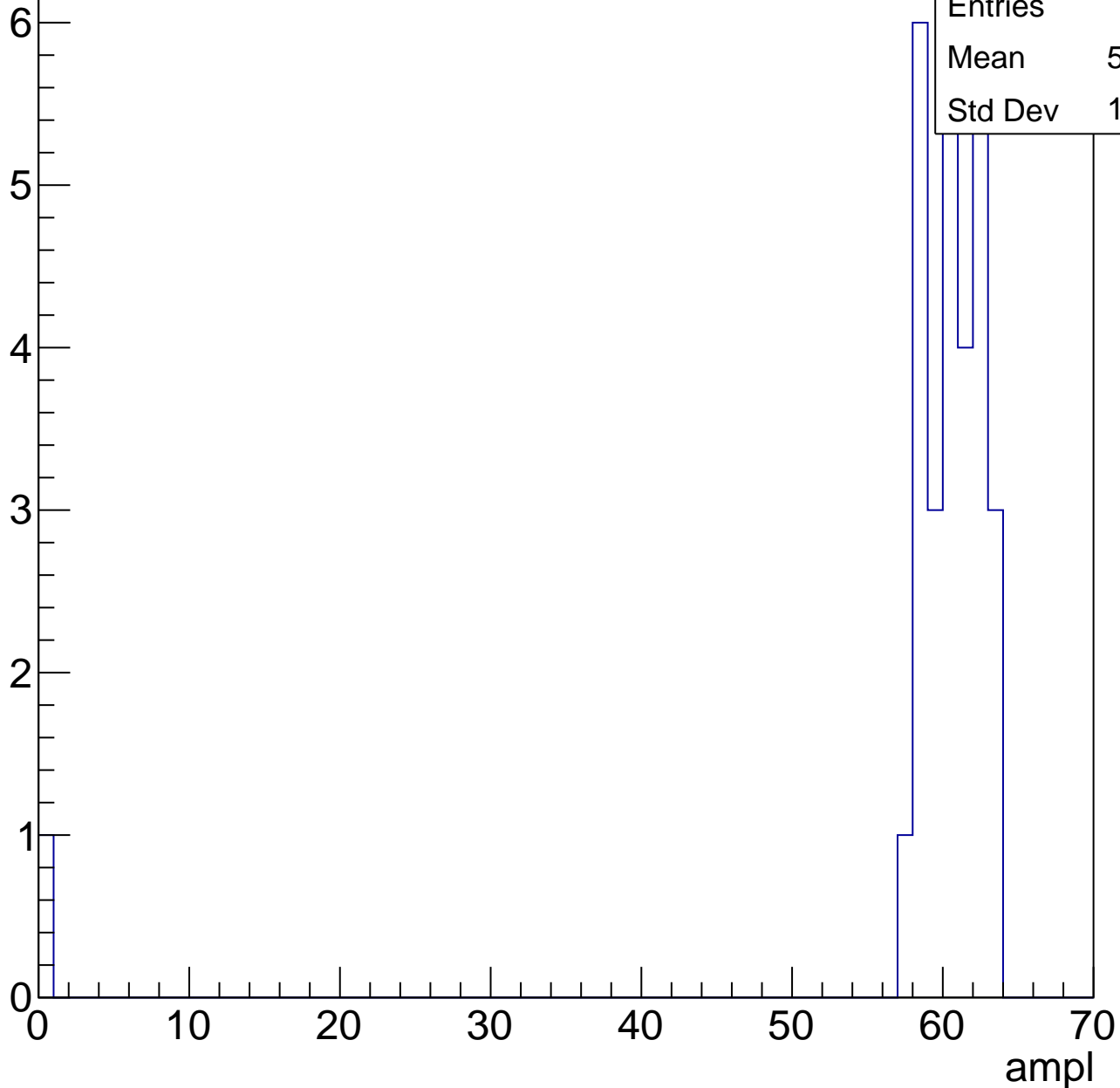
Entries	60
Mean	56.03
Std Dev	3.341



B1L103S, U5-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



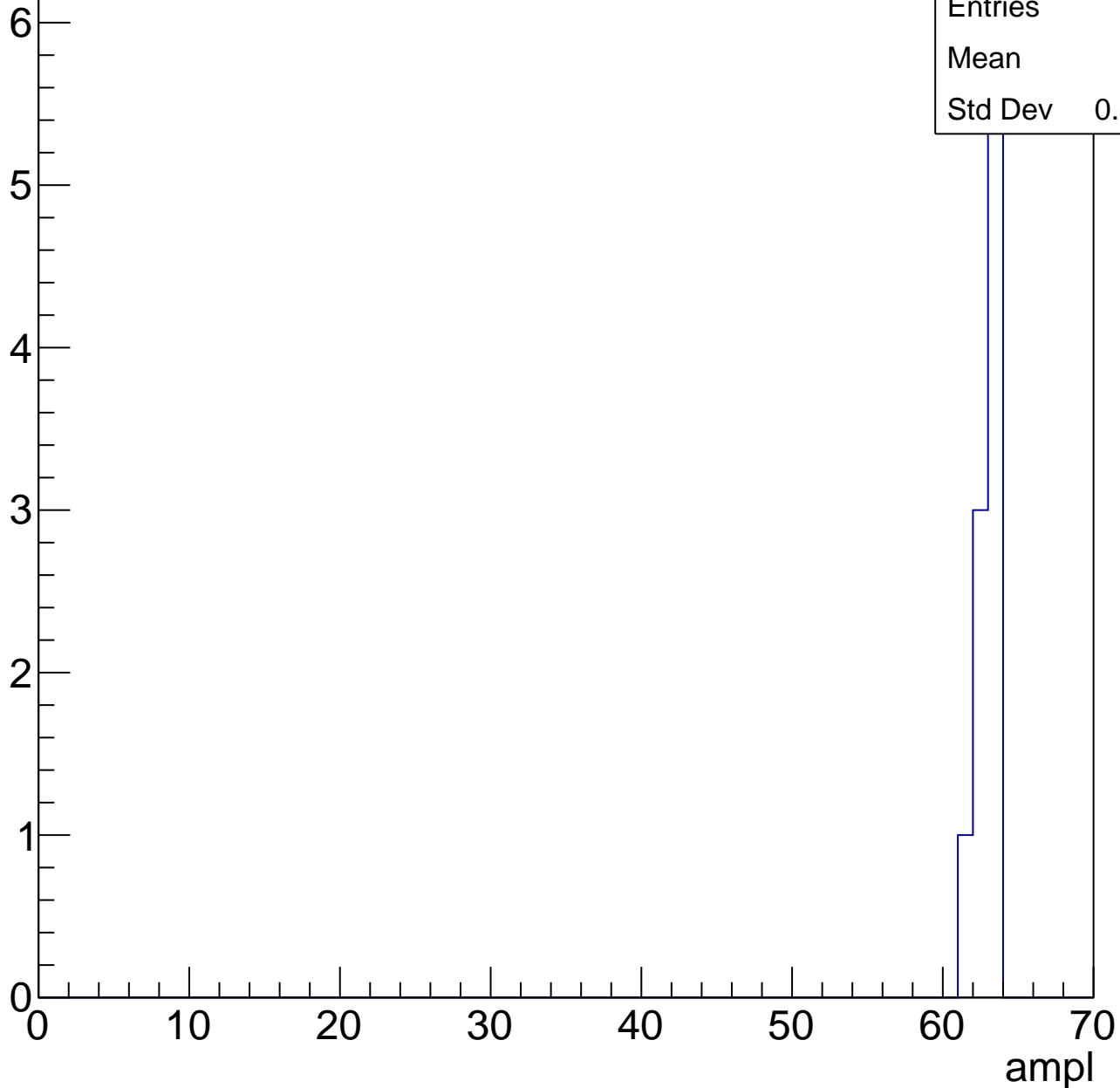
Entries	30
Mean	58.23
Std Dev	10.95

B1L103S, U5-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	10
Mean	62.5
Std Dev	0.6708



B1L103S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

