

B0L002S, U5-ch0, adc0

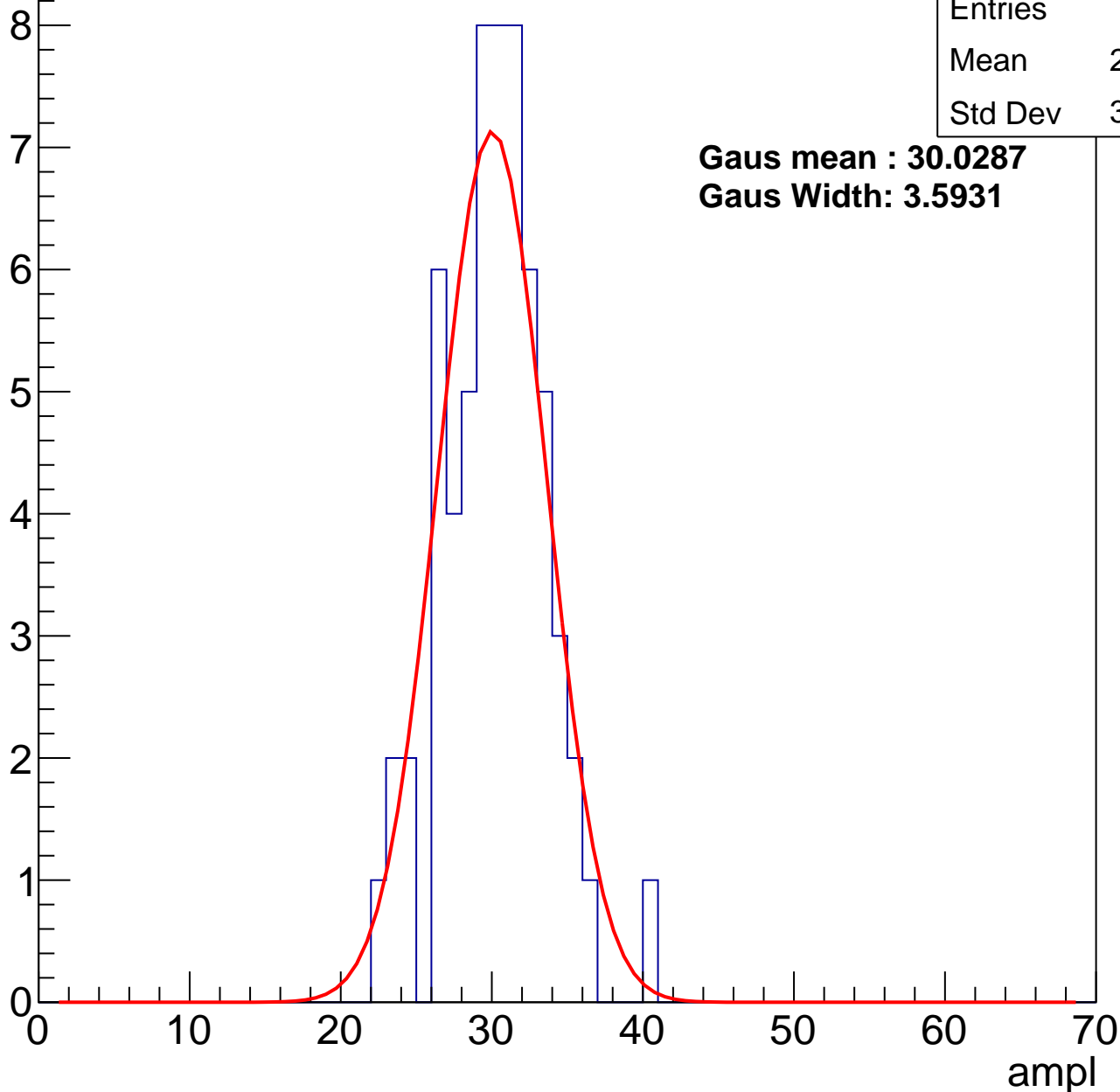
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.76
Std Dev	3.363

Gaus mean : 30.0287

Gaus Width: 3.5931



B0L002S, U5-ch0, adc1

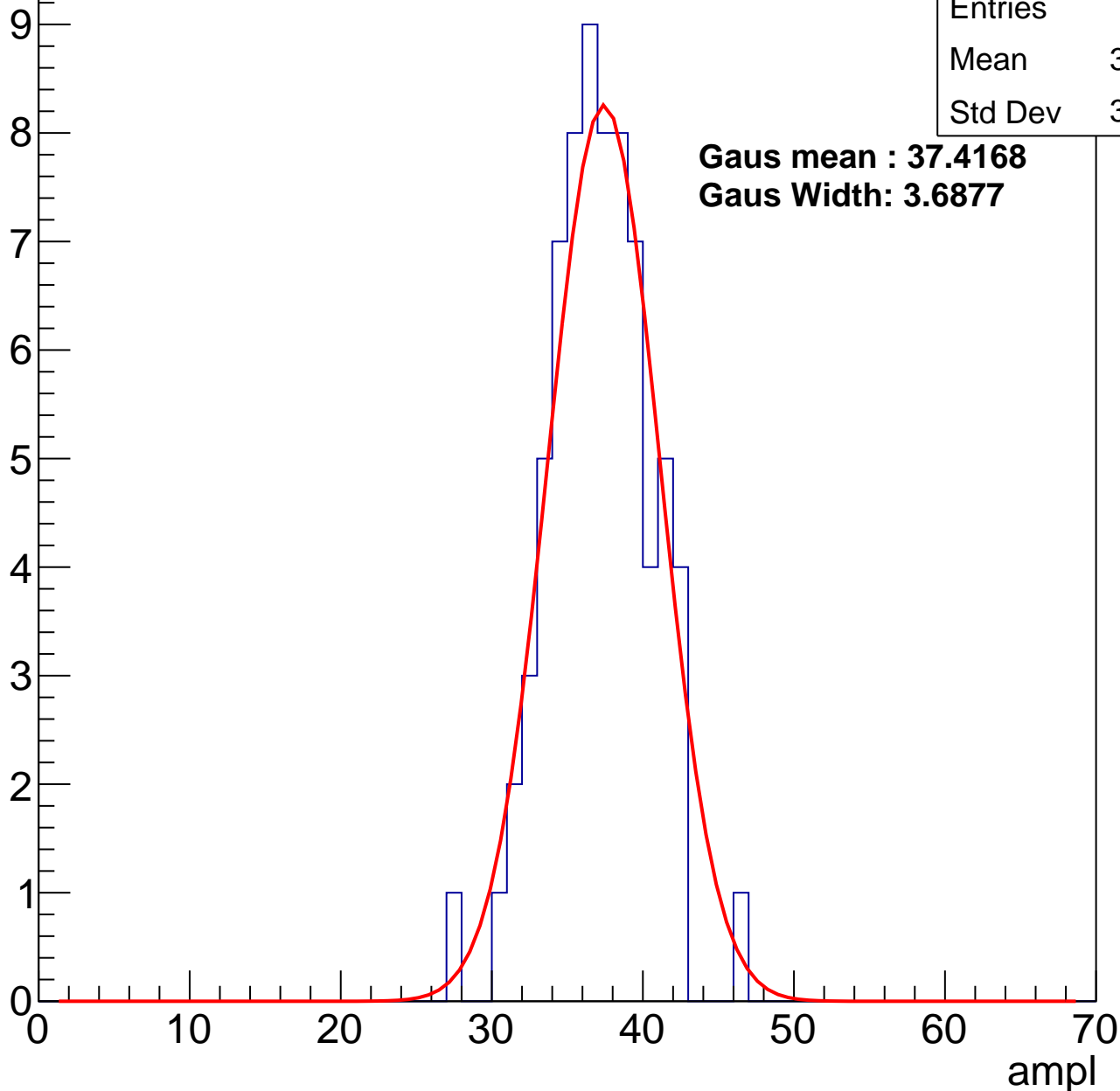
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	36.63
Std Dev	3.317

Gaus mean : 37.4168

Gaus Width: 3.6877



B0L002S, U5-ch0, adc2

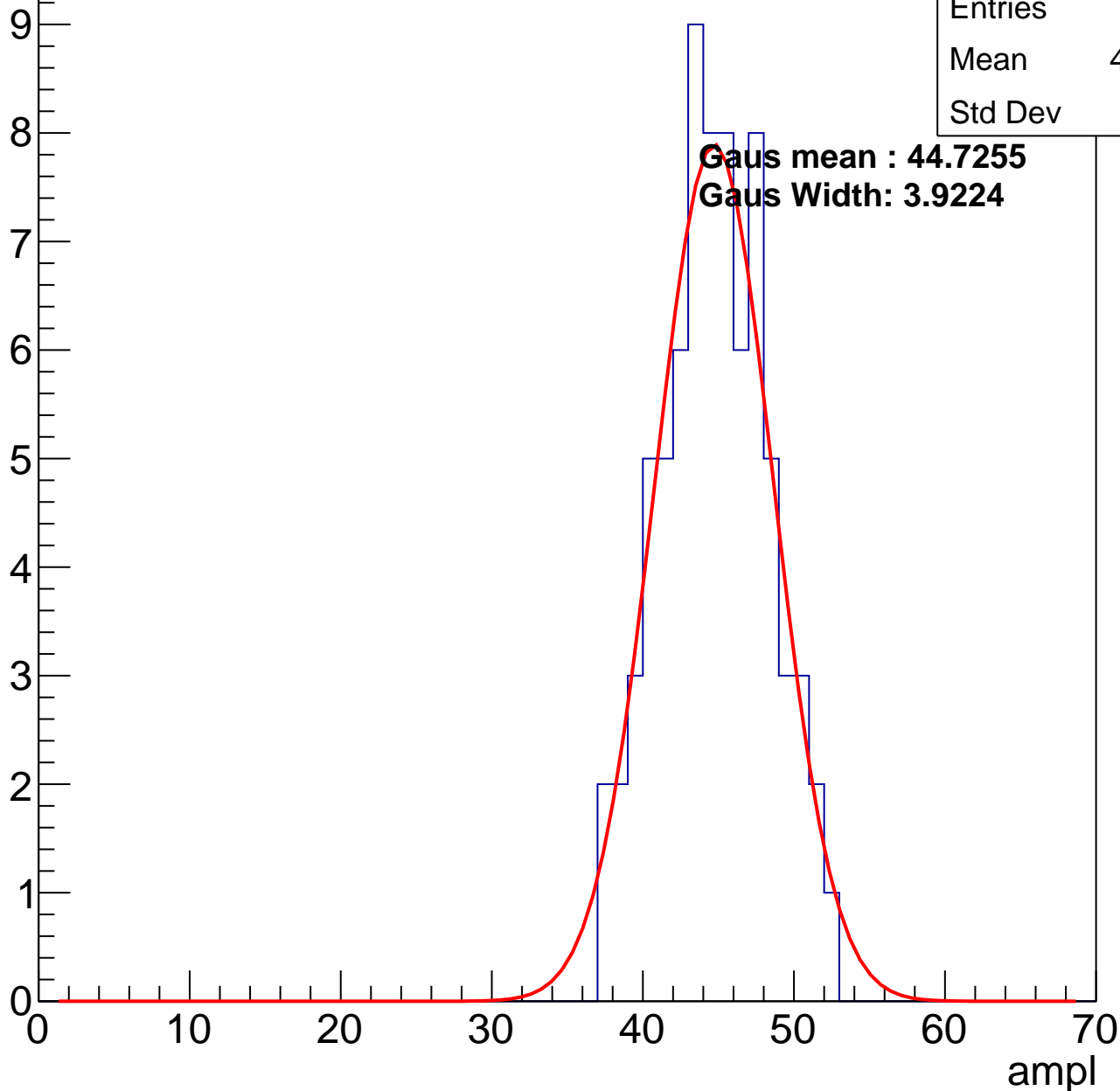
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	44.29
Std Dev	3.49

Gaus mean : 44.7255

Gaus Width: 3.9224

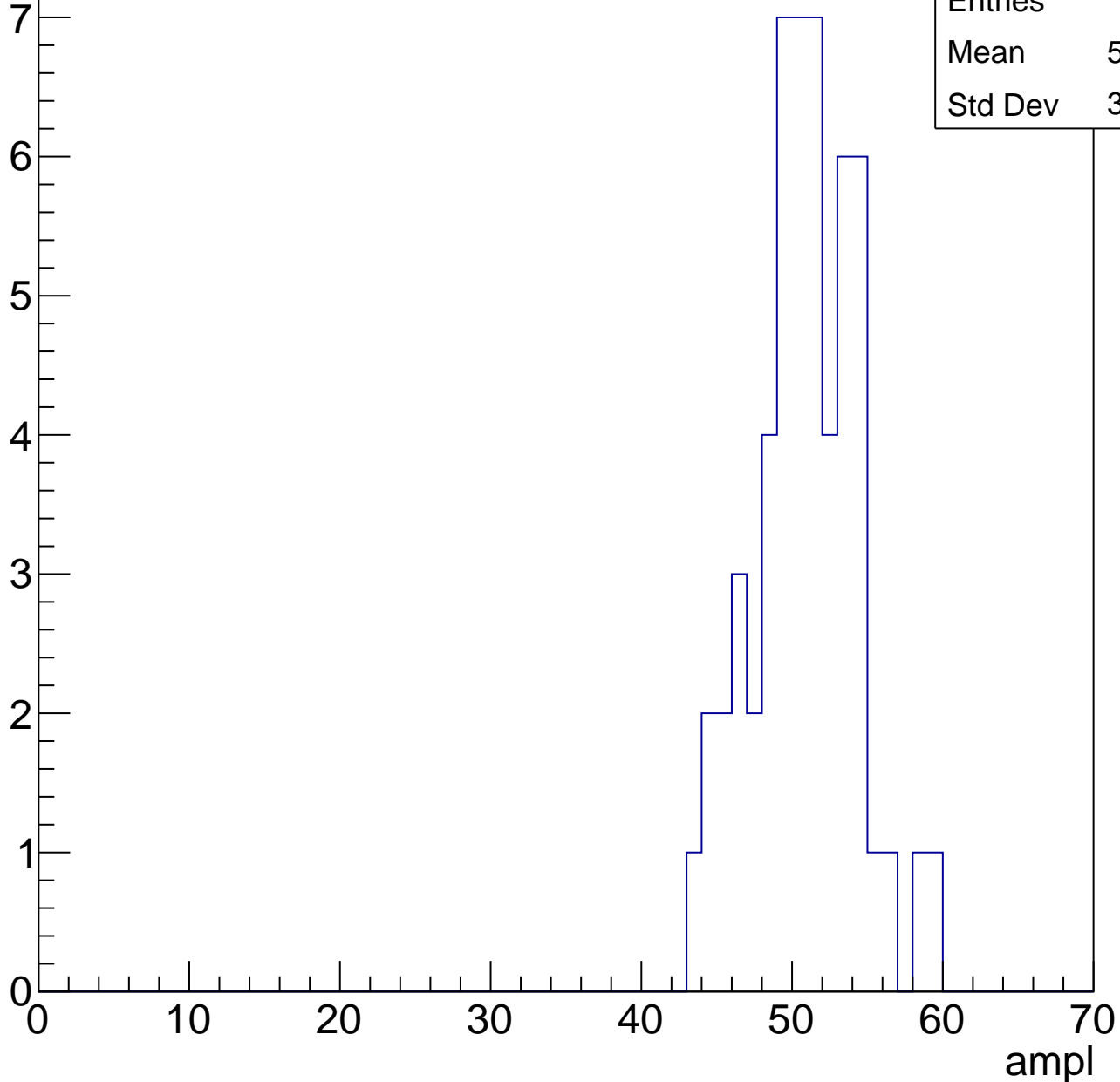


B0L002S, U5-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	50.42
Std Dev	3.383

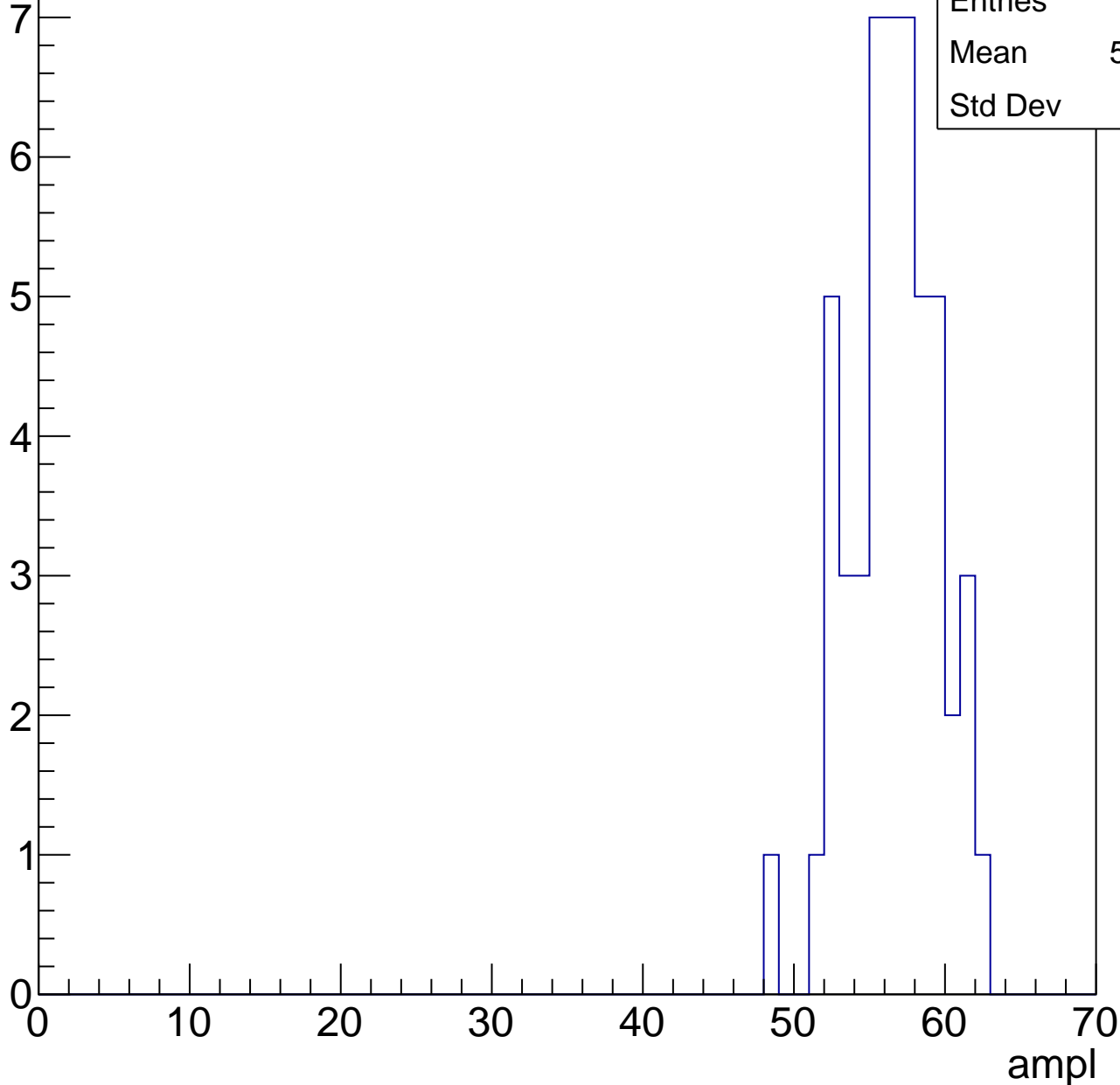


B0L002S, U5-ch0, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.12
Std Dev	2.93



B0L002S, U5-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.74
Std Dev	8.775

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B0L002S, U5-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L002S, U5-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch1, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	29.43
Std Dev	4.684

Gaus mean : 30.5431

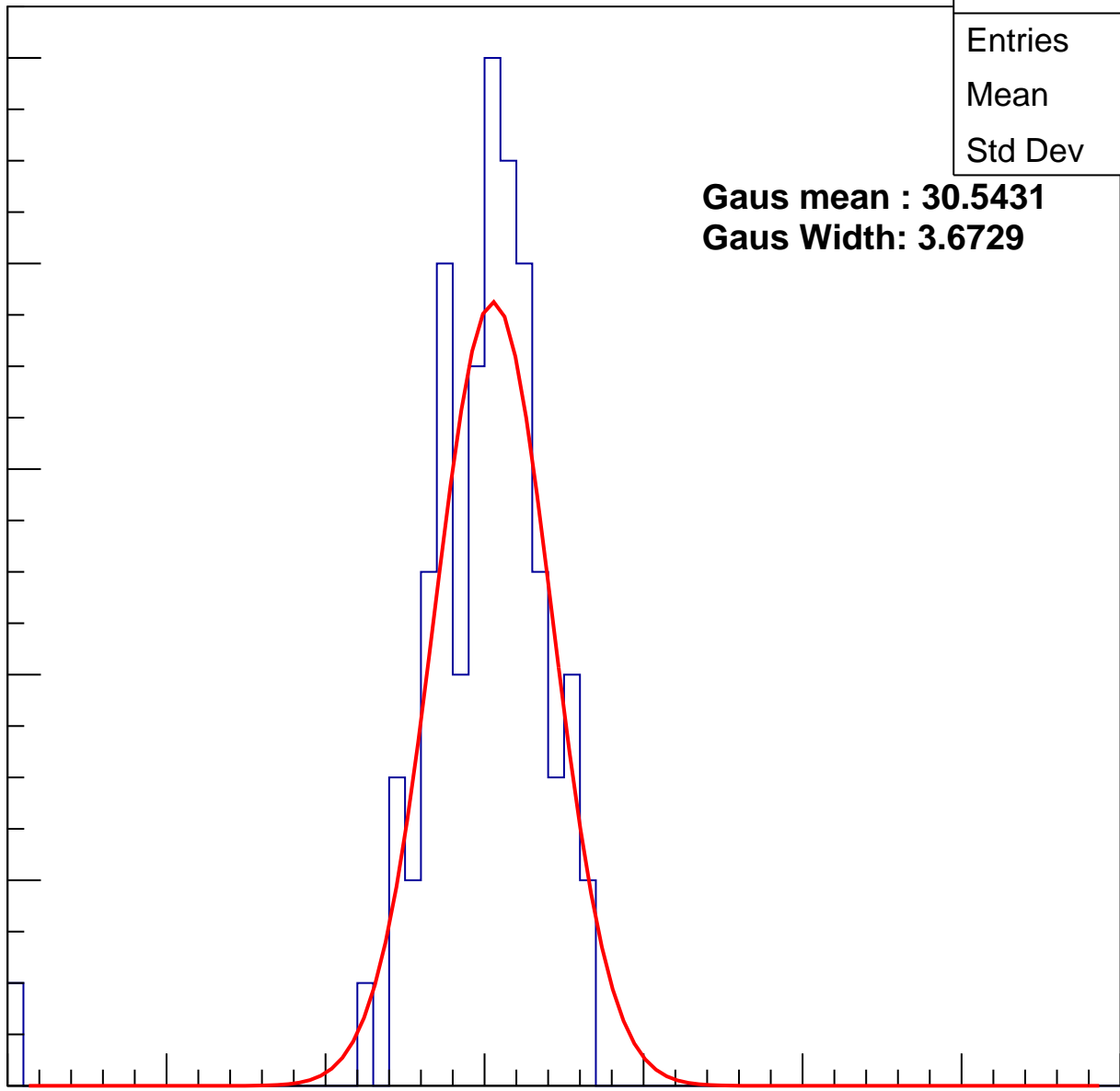
Gaus Width: 3.6729

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



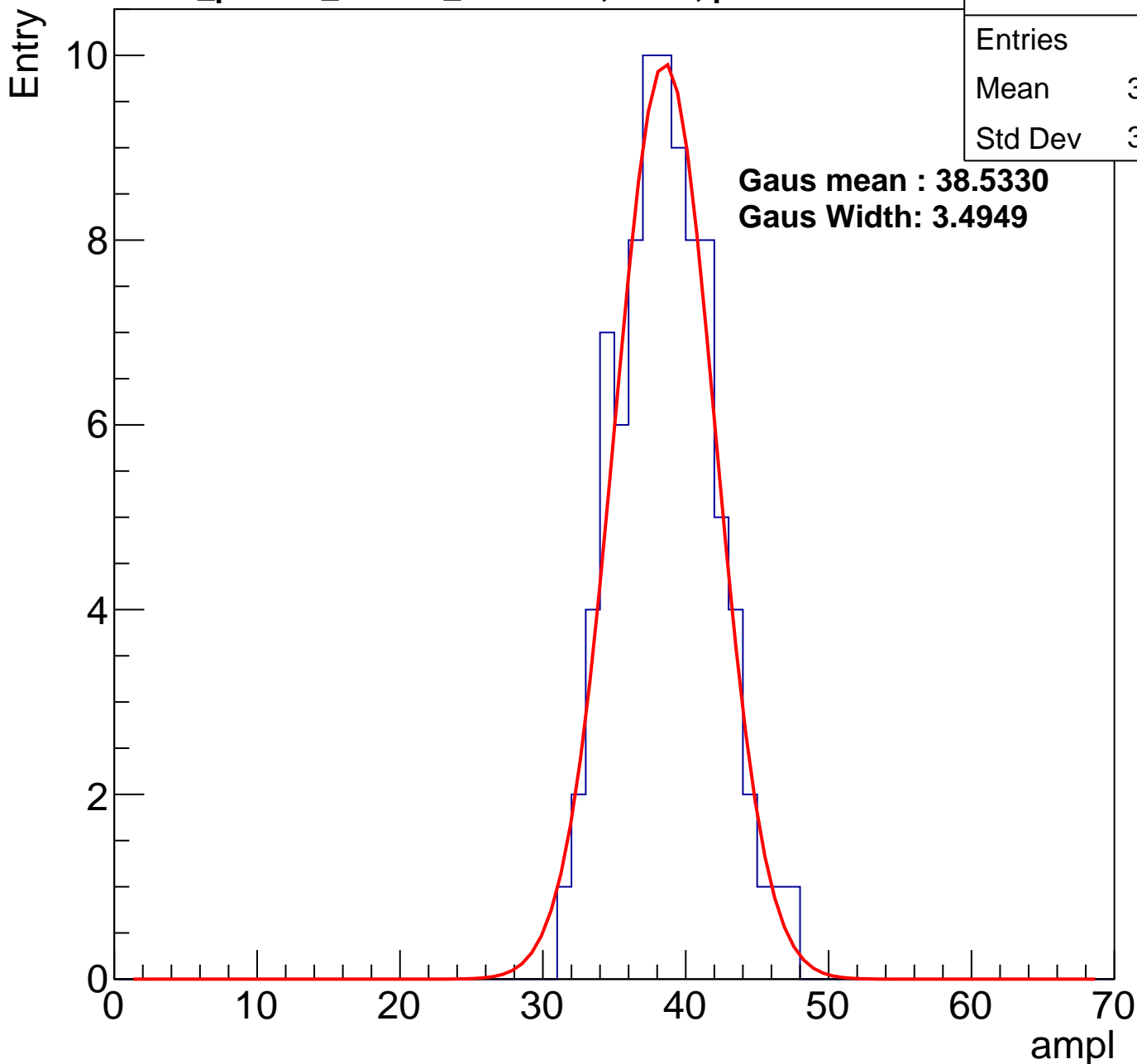
B0L002S, U5-ch1, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	38.16
Std Dev	3.366

Gaus mean : 38.5330

Gaus Width: 3.4949



B0L002S, U5-ch1, adc2

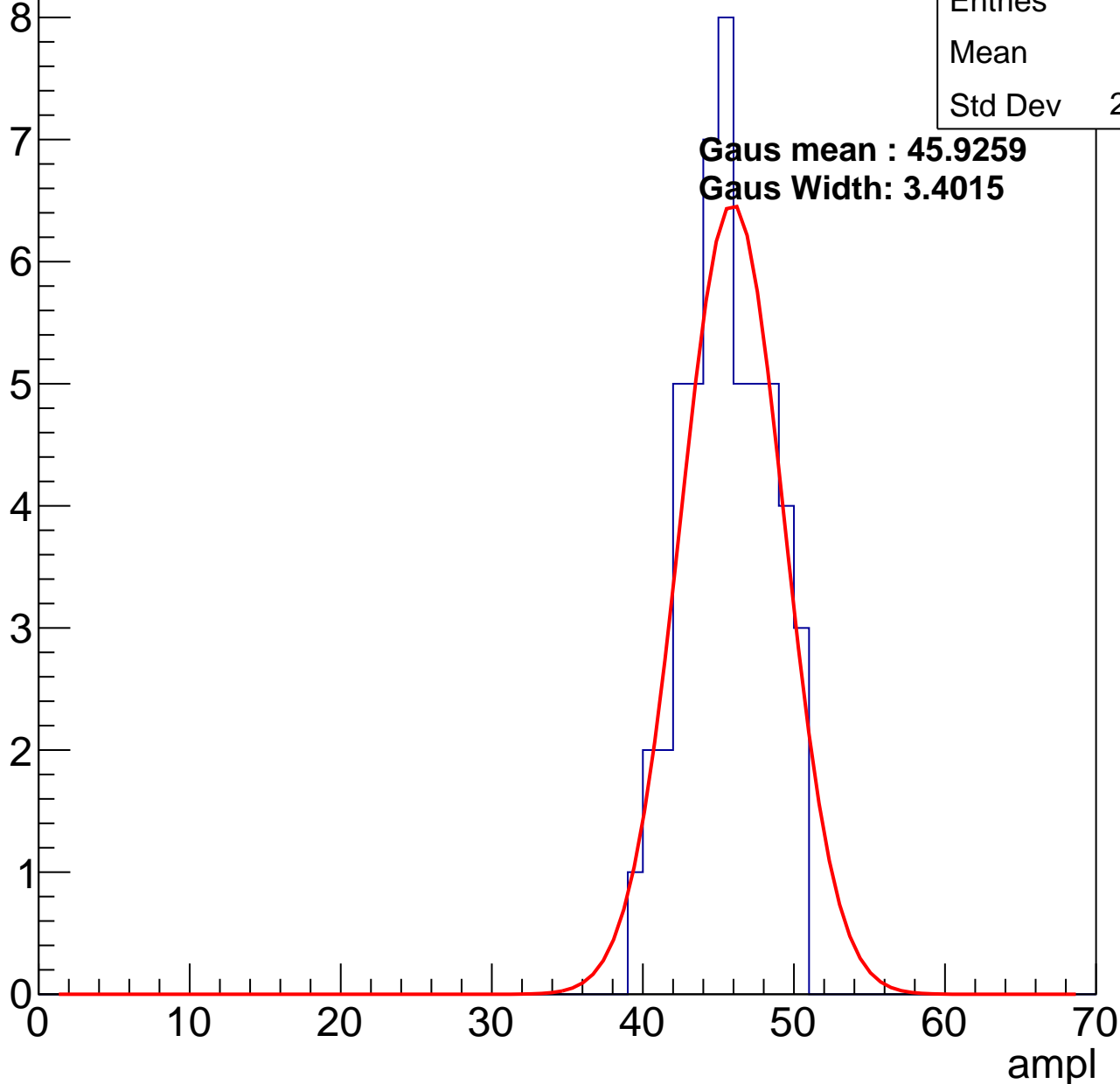
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	45.1
Std Dev	2.768

Gaus mean : 45.9259

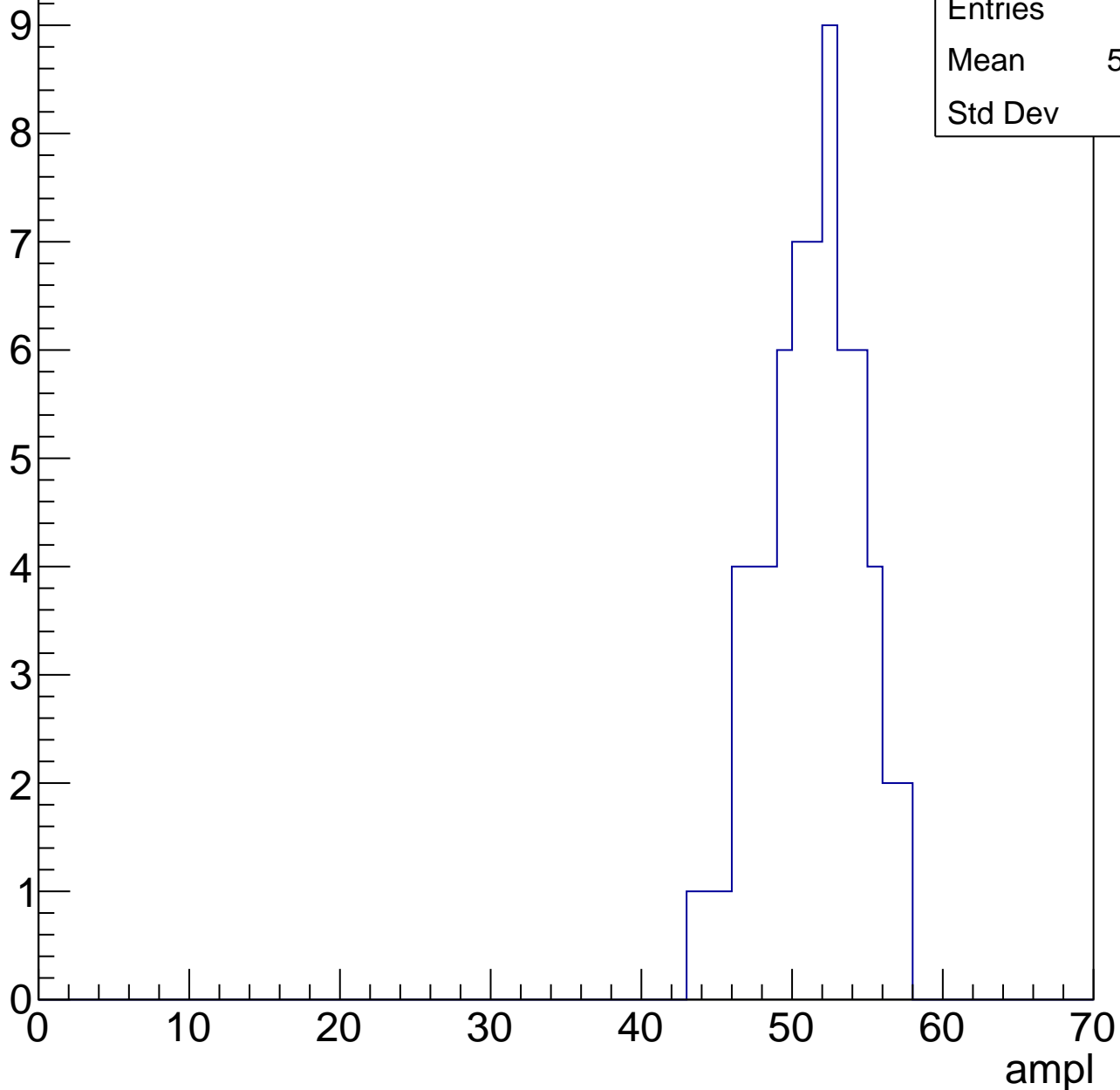
Gaus Width: 3.4015



B0L002S, U5-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

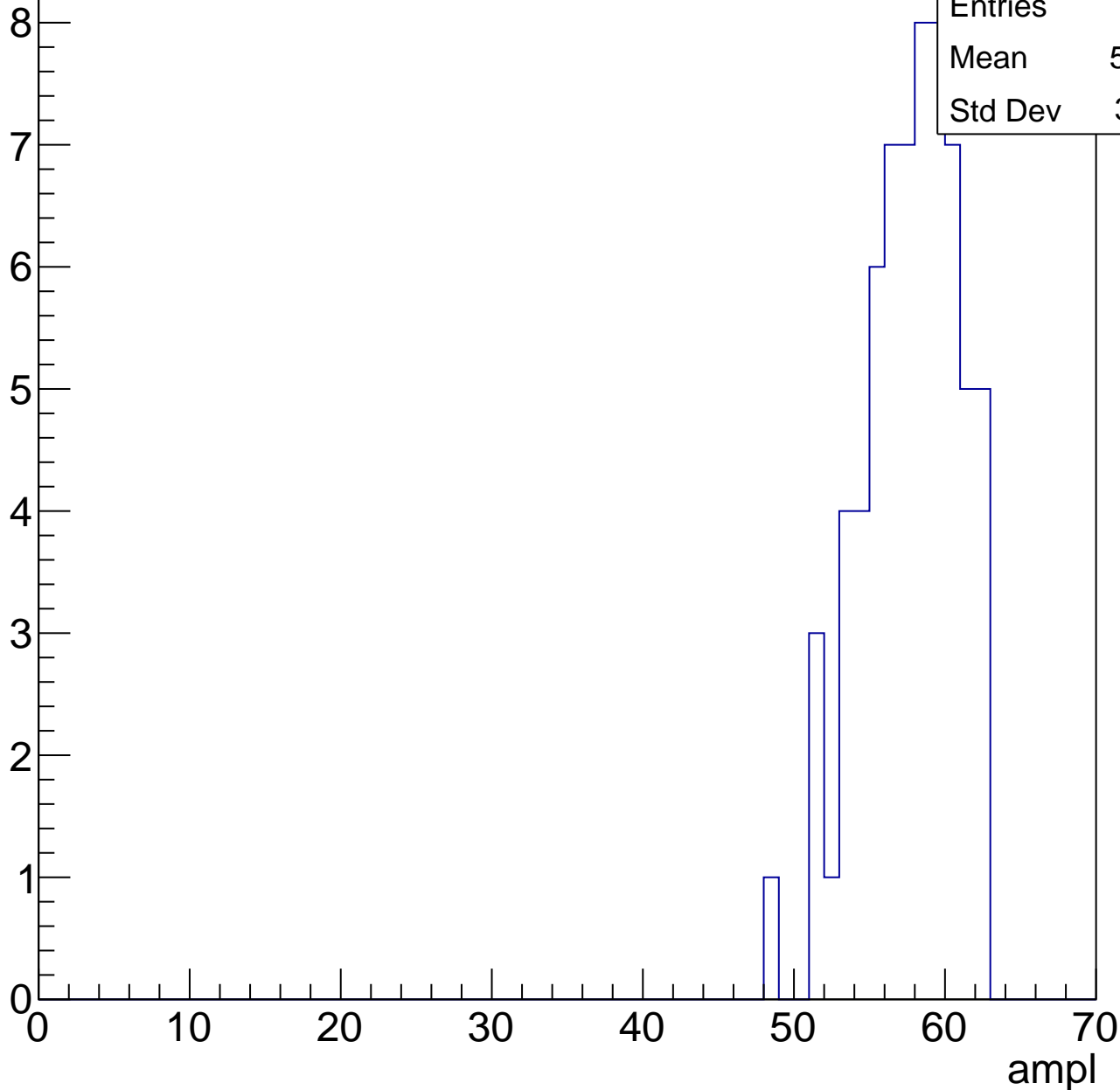


Entries	64
Mean	50.83
Std Dev	3.19

B0L002S, U5-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	30
Mean	59.07
Std Dev	11.14

Entry

10

8

6

4

2

0

0

10

20

30

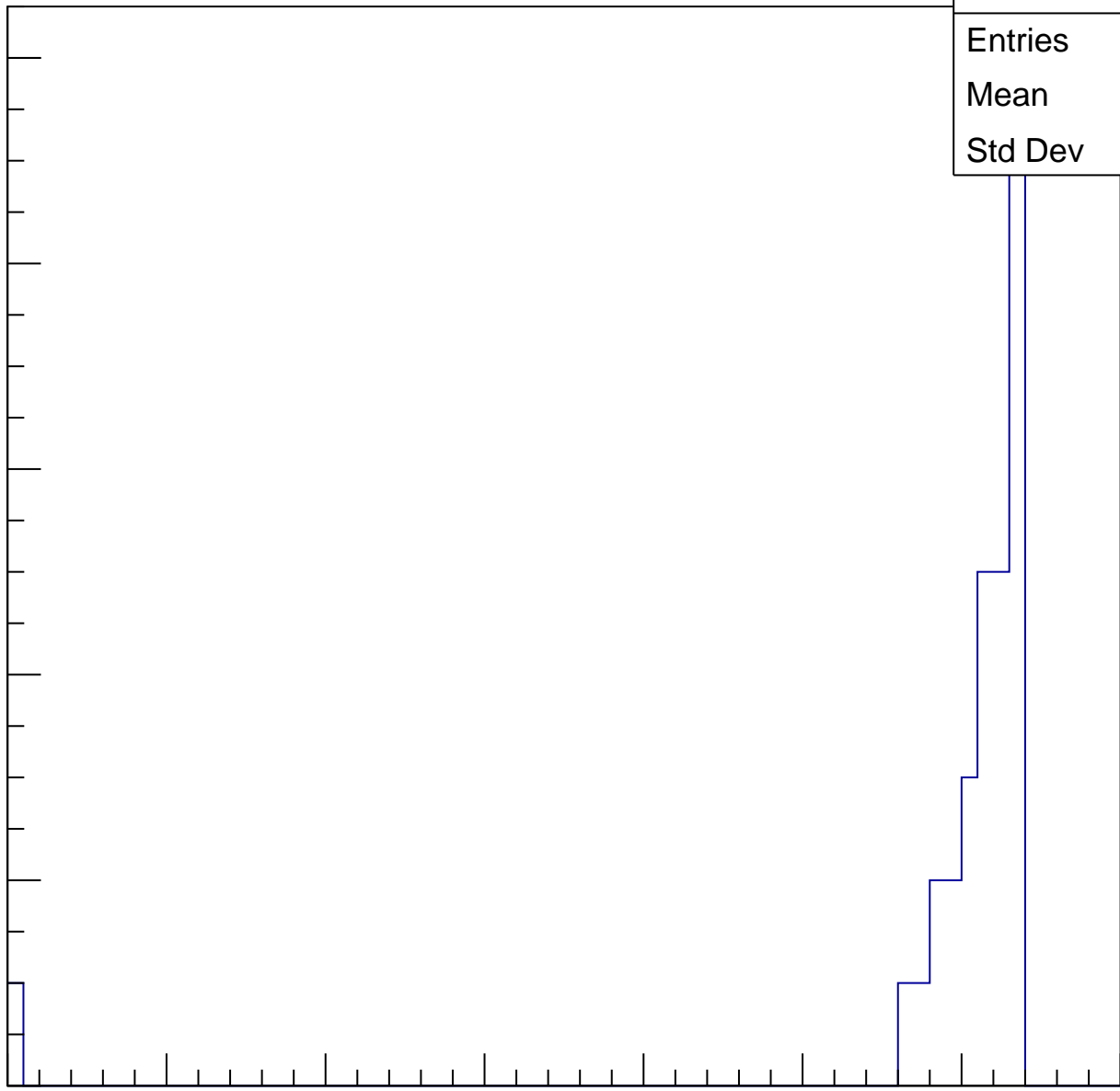
40

50

60

70

ampl



B0L002S, U5-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch2, adc0

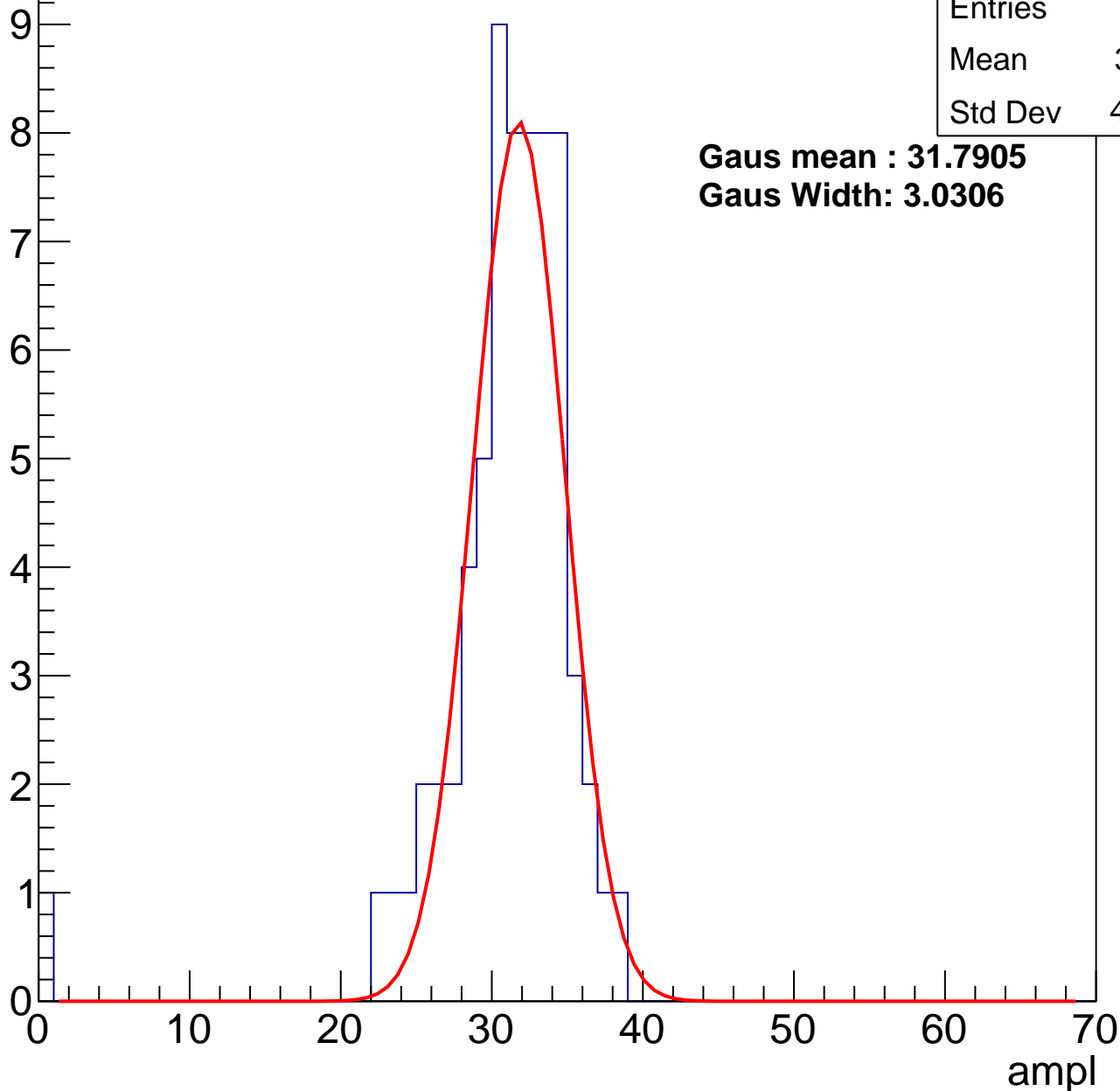
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.51
Std Dev	4.967

Gaus mean : 31.7905

Gaus Width: 3.0306



B0L002S, U5-ch2, adc1

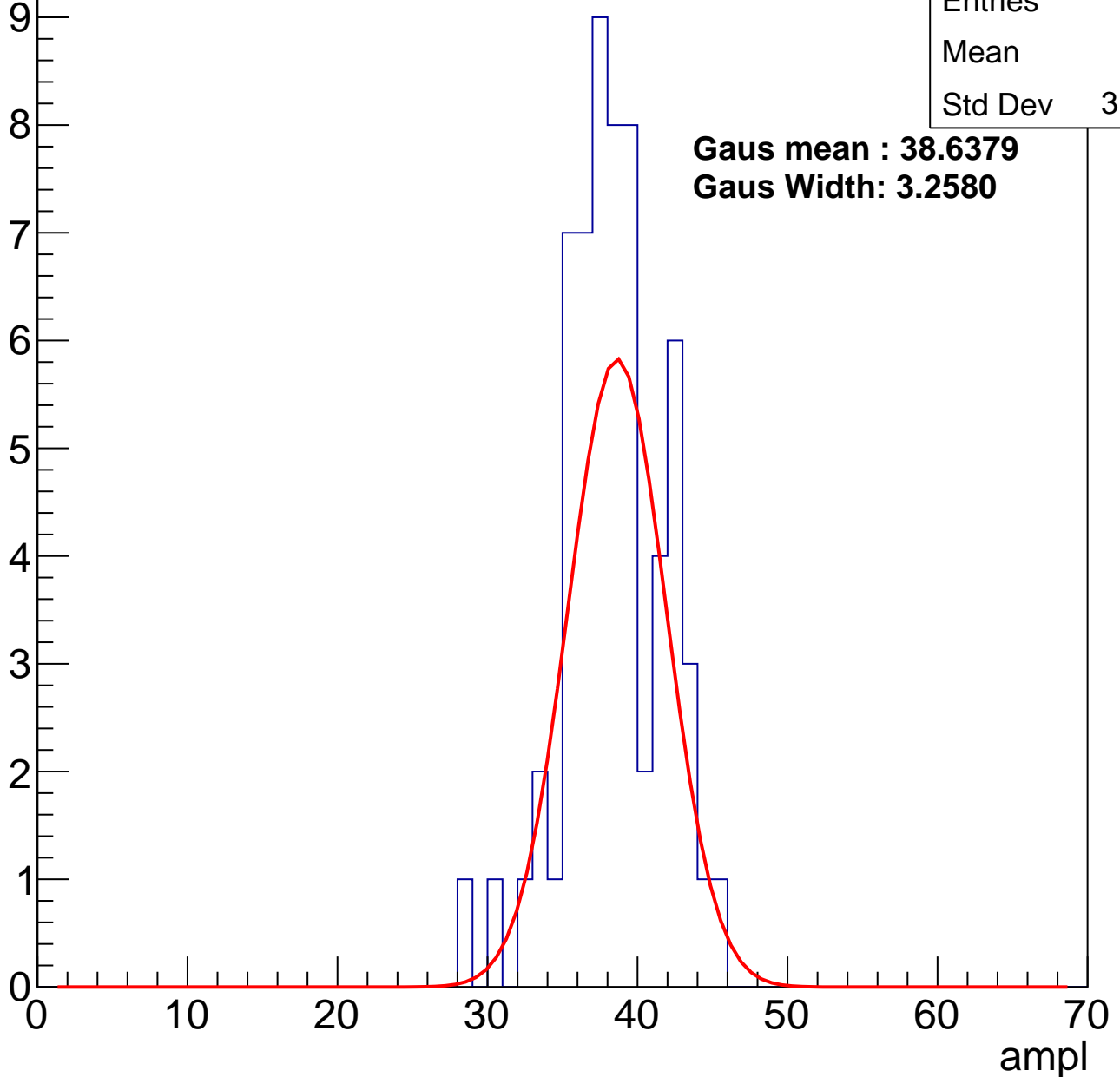
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.9
Std Dev	3.296

Gaus mean : 38.6379

Gaus Width: 3.2580



B0L002S, U5-ch2, adc2

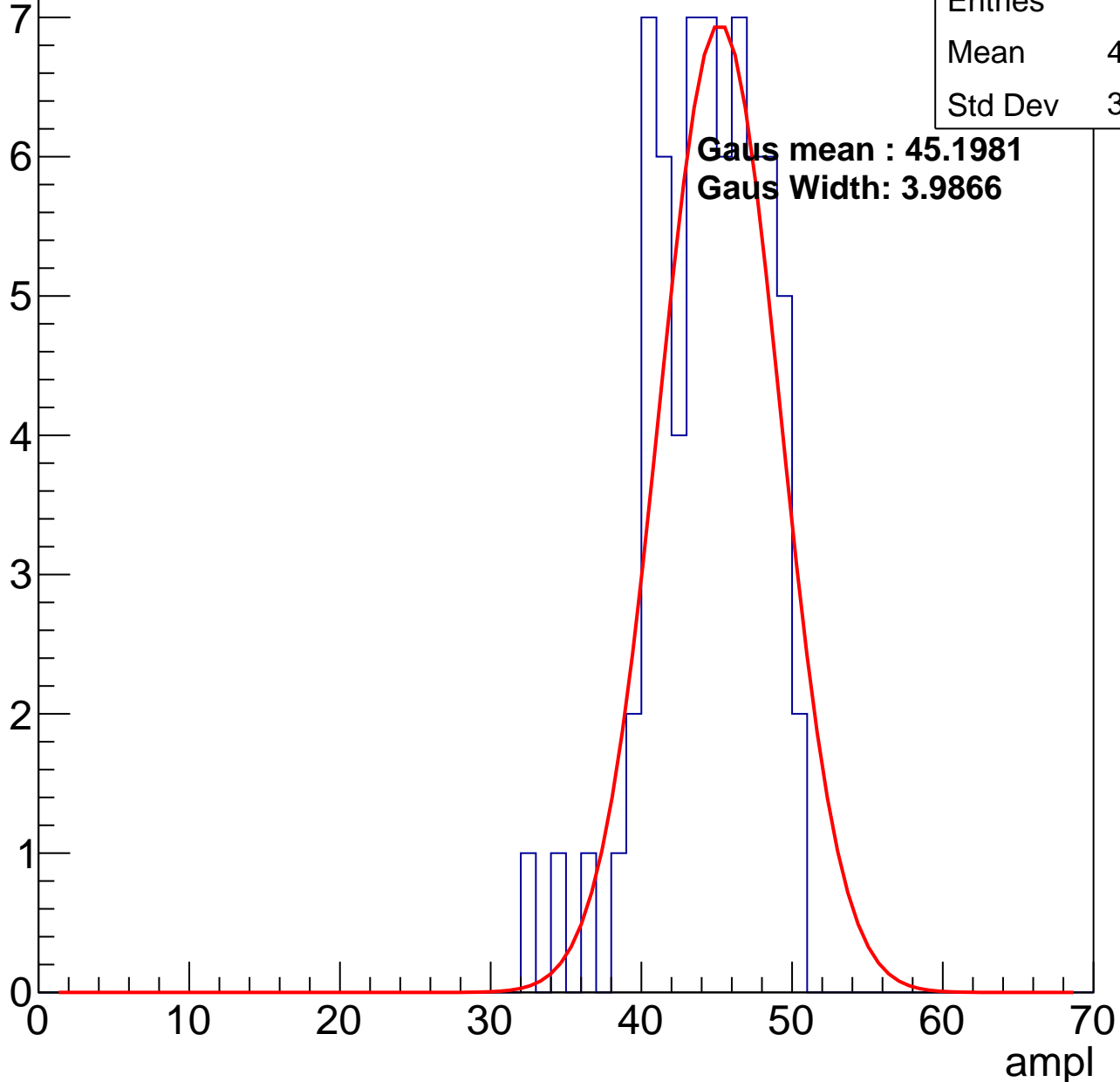
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.88
Std Dev	3.736

Gaus mean : 45.1981

Gaus Width: 3.9866

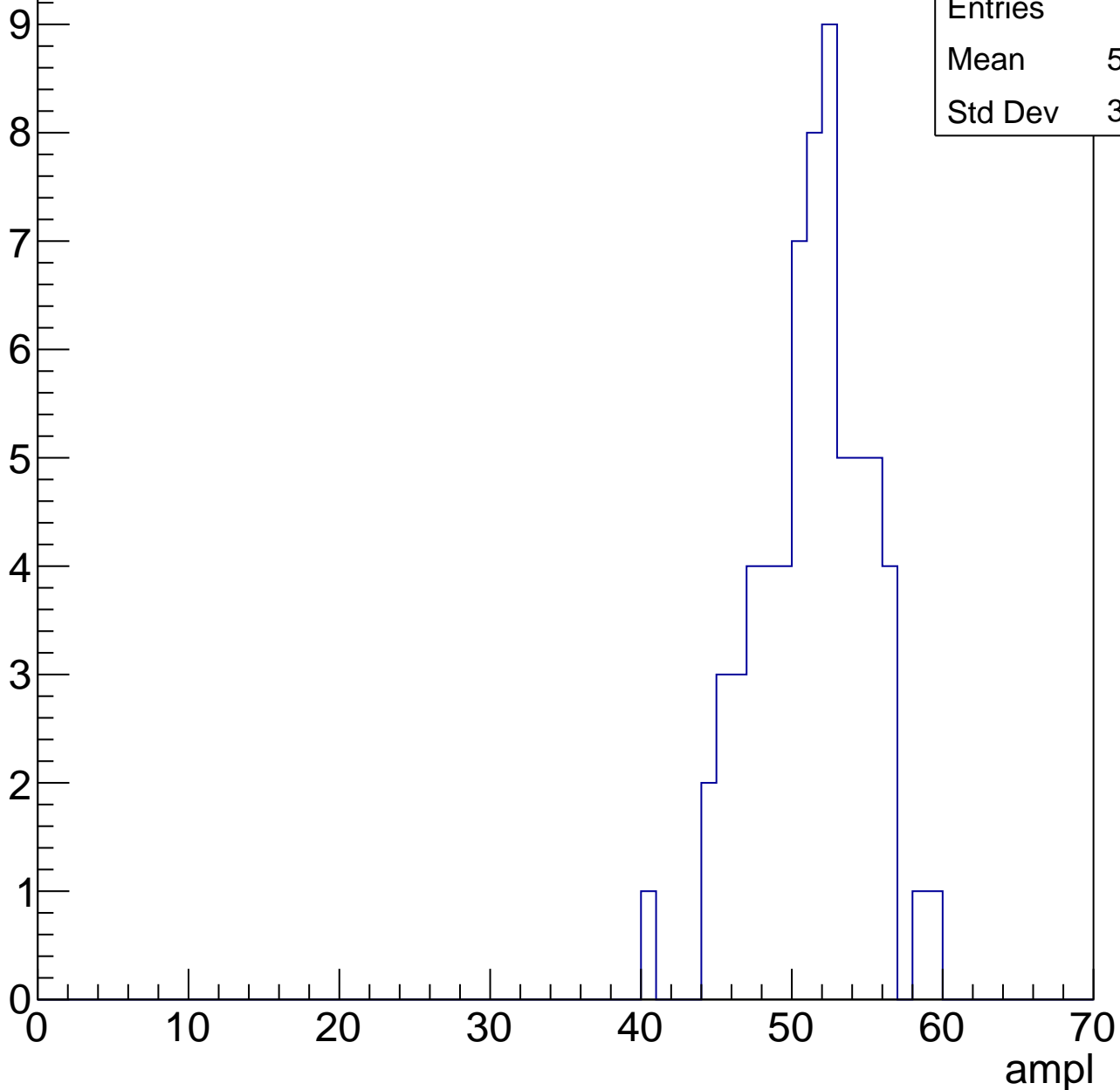


B0L002S, U5-ch2, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.82
Std Dev	3.672

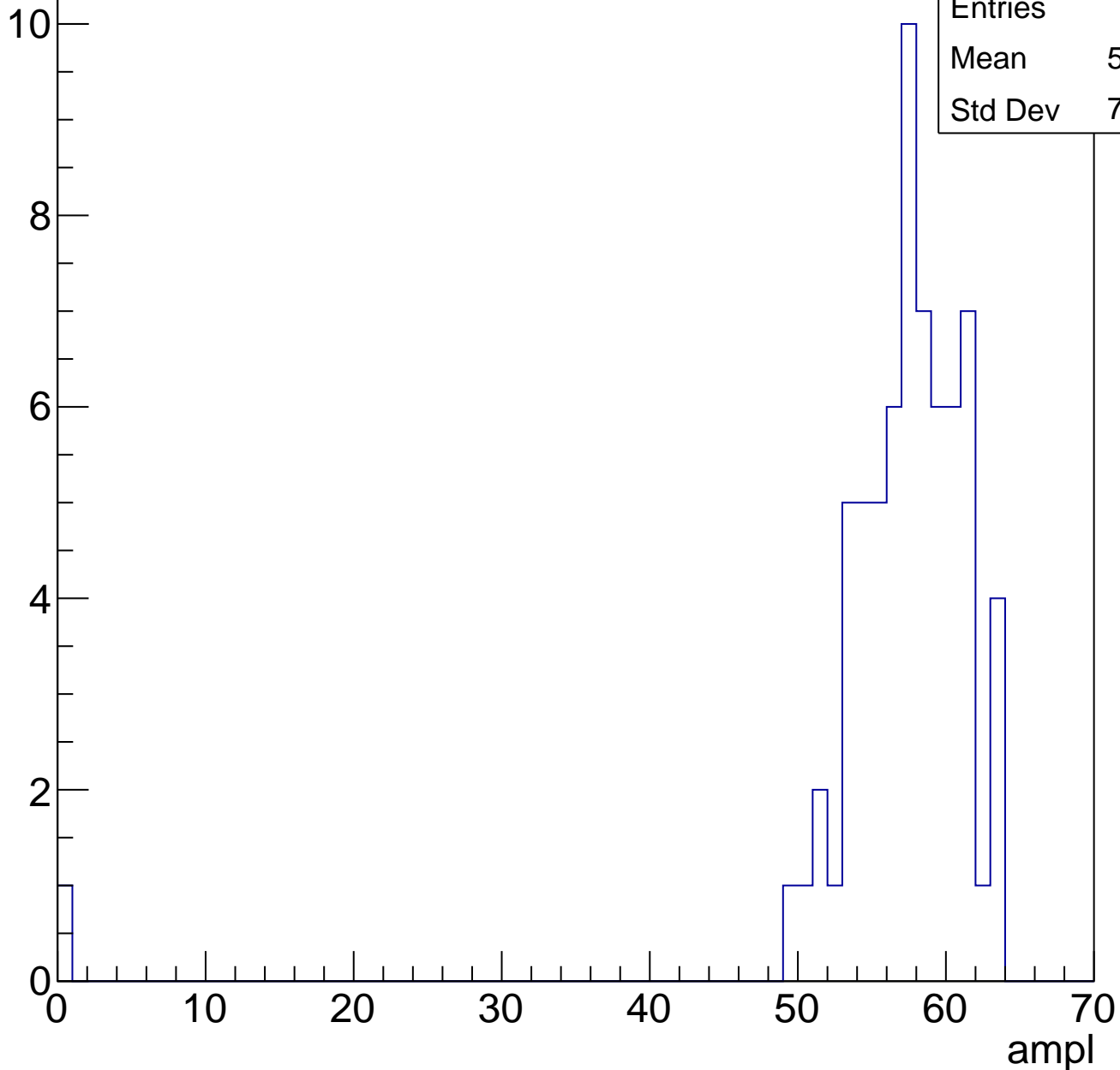


B0L002S, U5-ch2, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	56.32
Std Dev	7.617

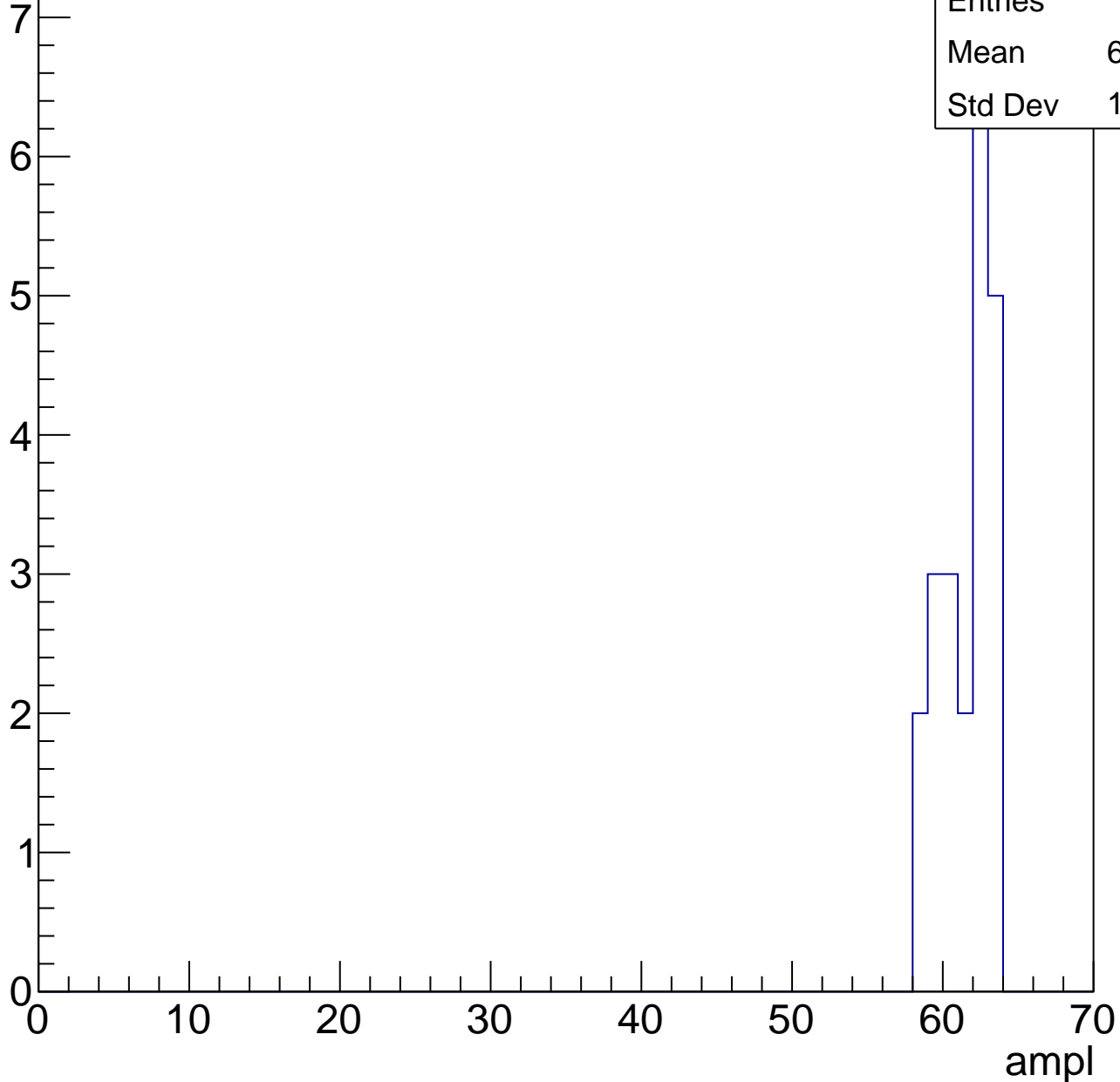
Entry



B0L002S, U5-ch2, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch3, adc0

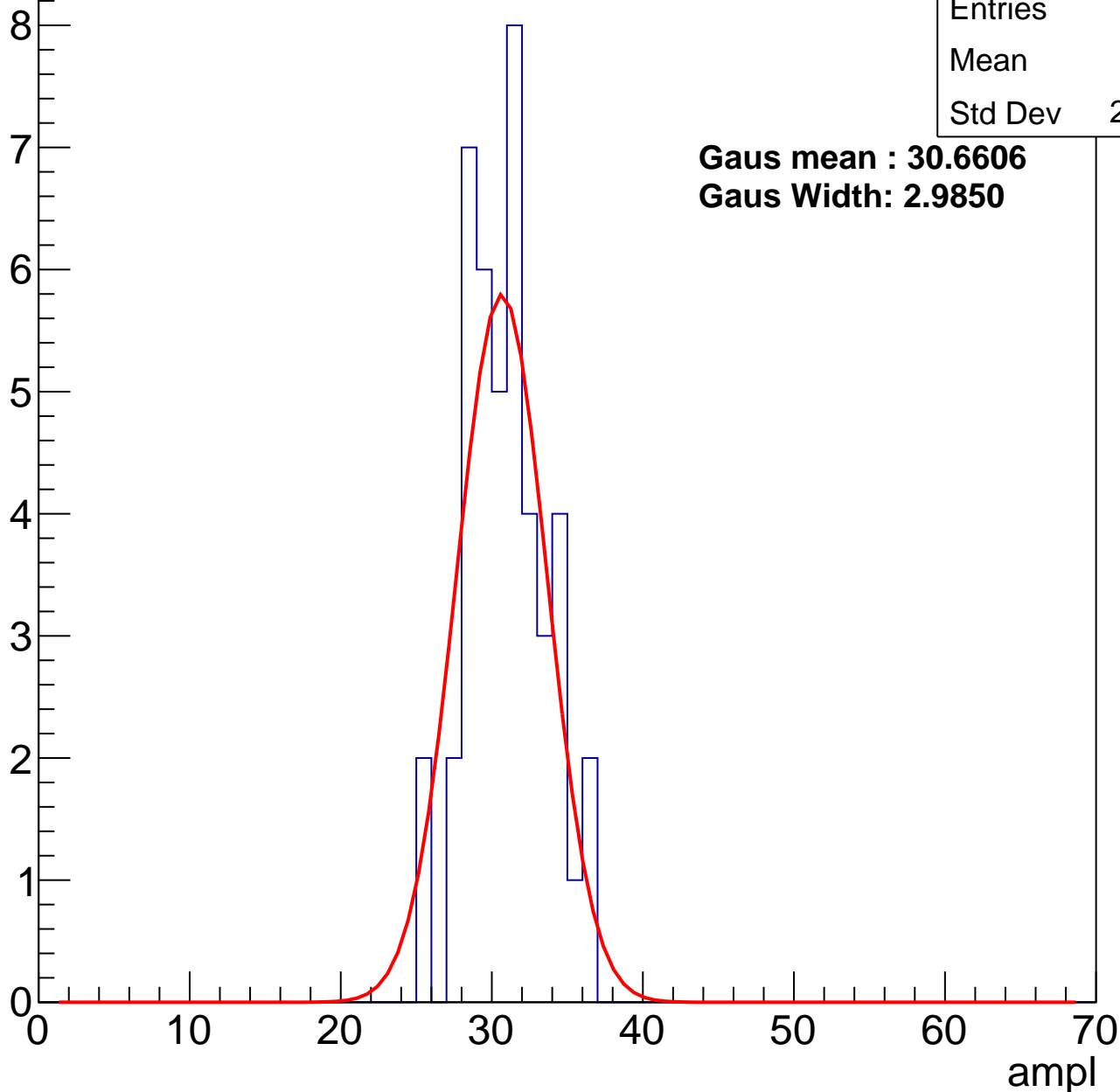
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	30.5
Std Dev	2.624

Gaus mean : 30.6606

Gaus Width: 2.9850

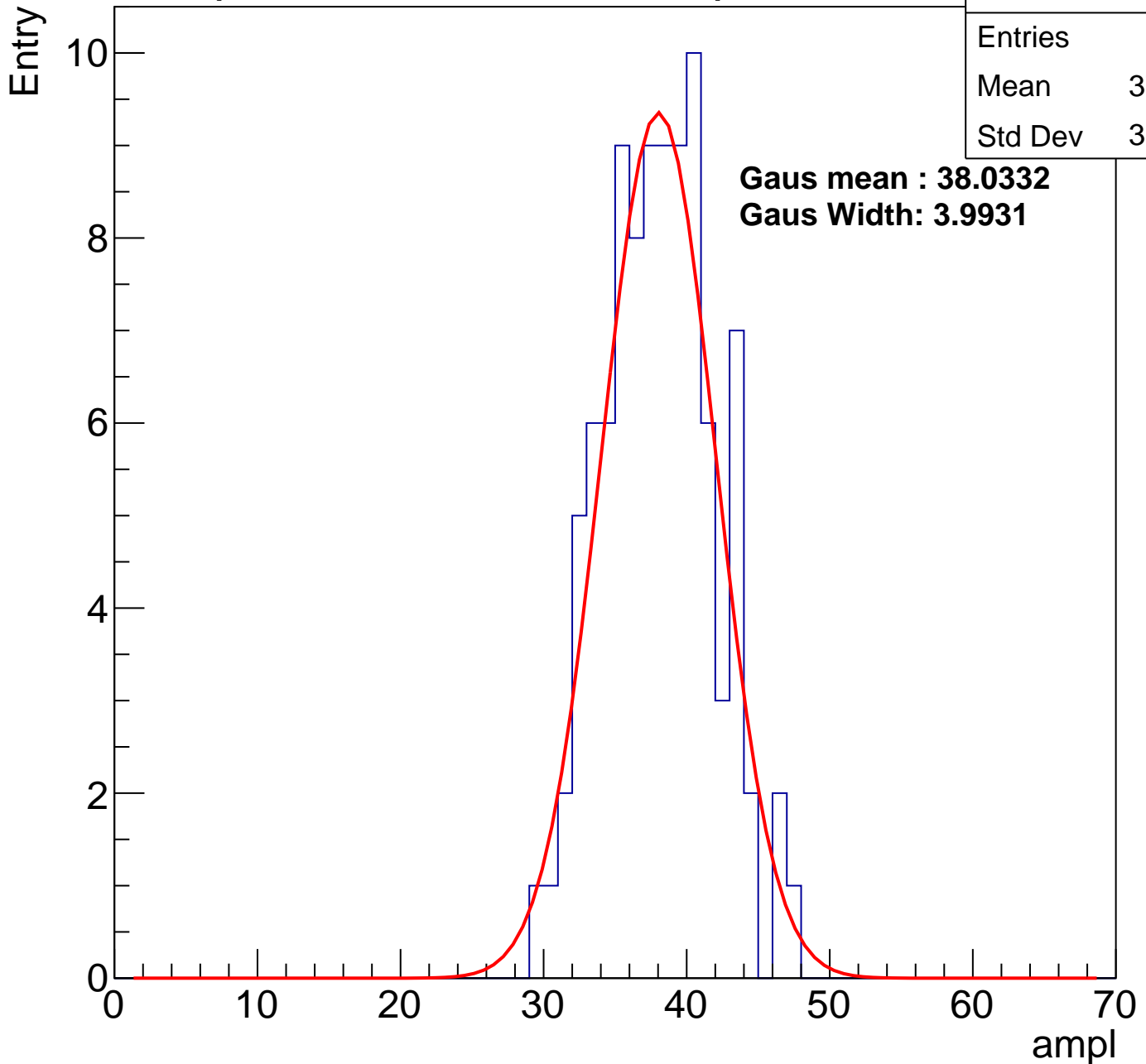


B0L002S, U5-ch3, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	96
Mean	37.62
Std Dev	3.798

Gaus mean : 38.0332
Gaus Width: 3.9931



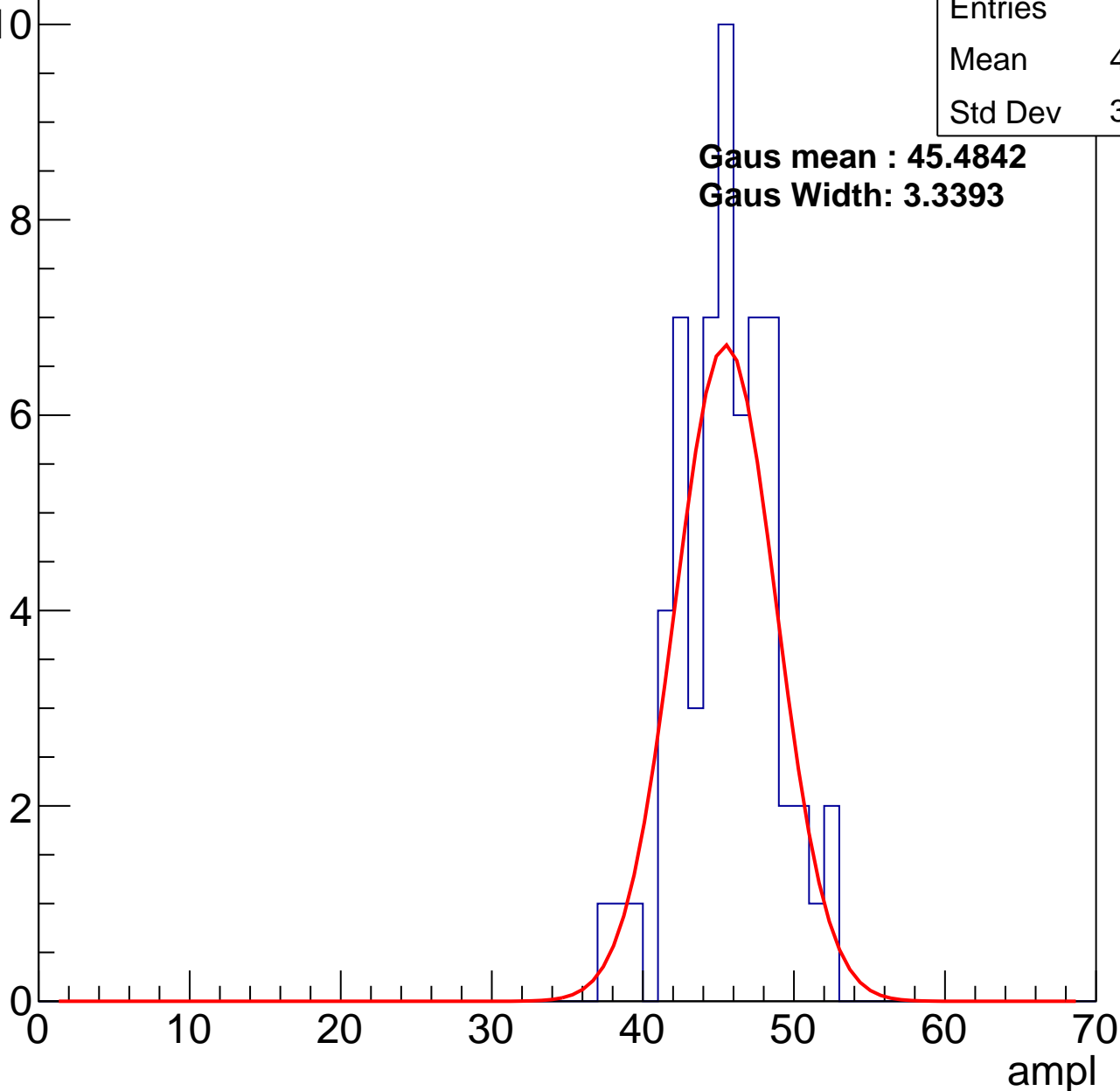
B0L002S, U5-ch3, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	45.13
Std Dev	3.154

Gaus mean : 45.4842
Gaus Width: 3.3393

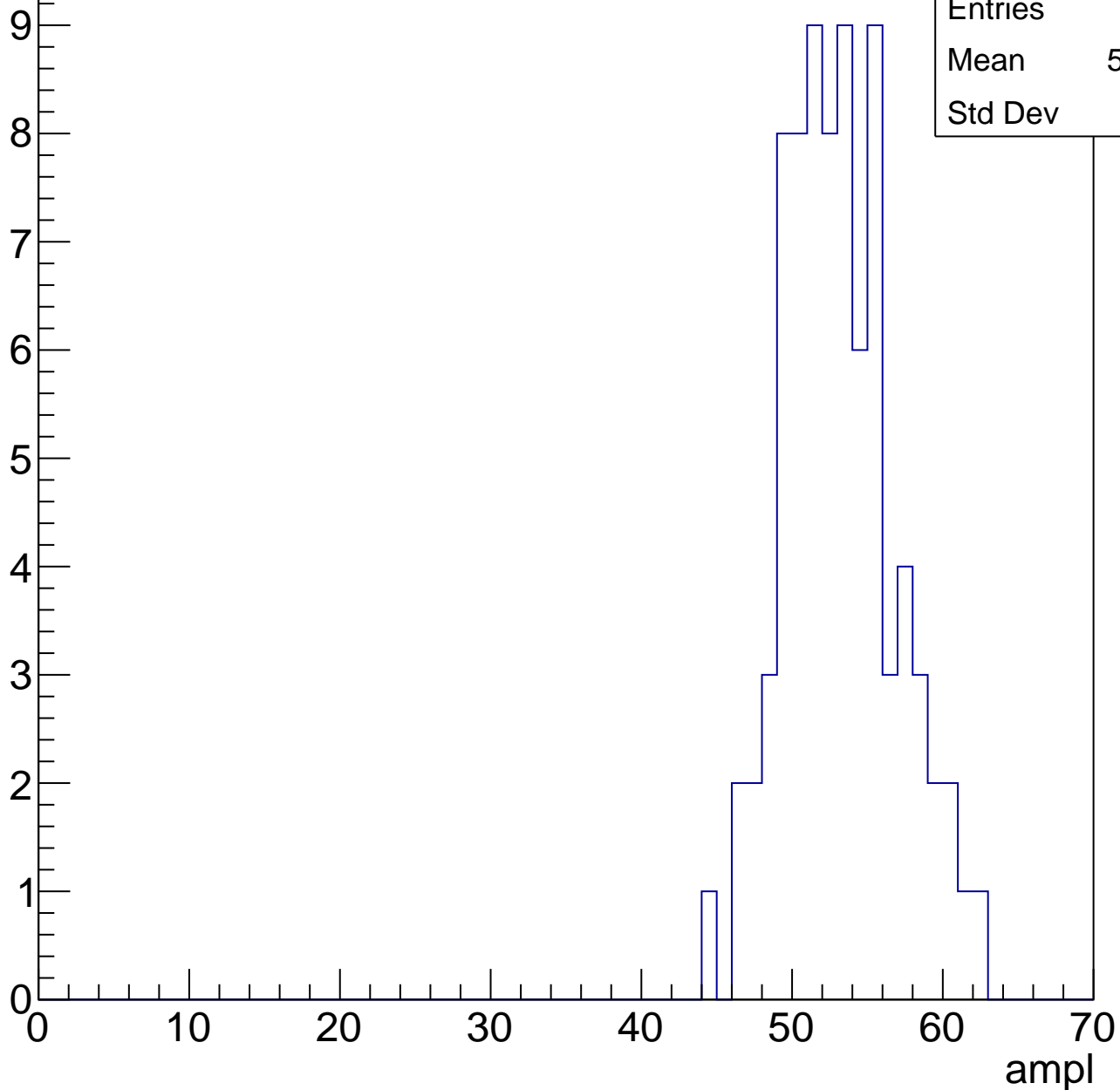
Entry



B0L002S, U5-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

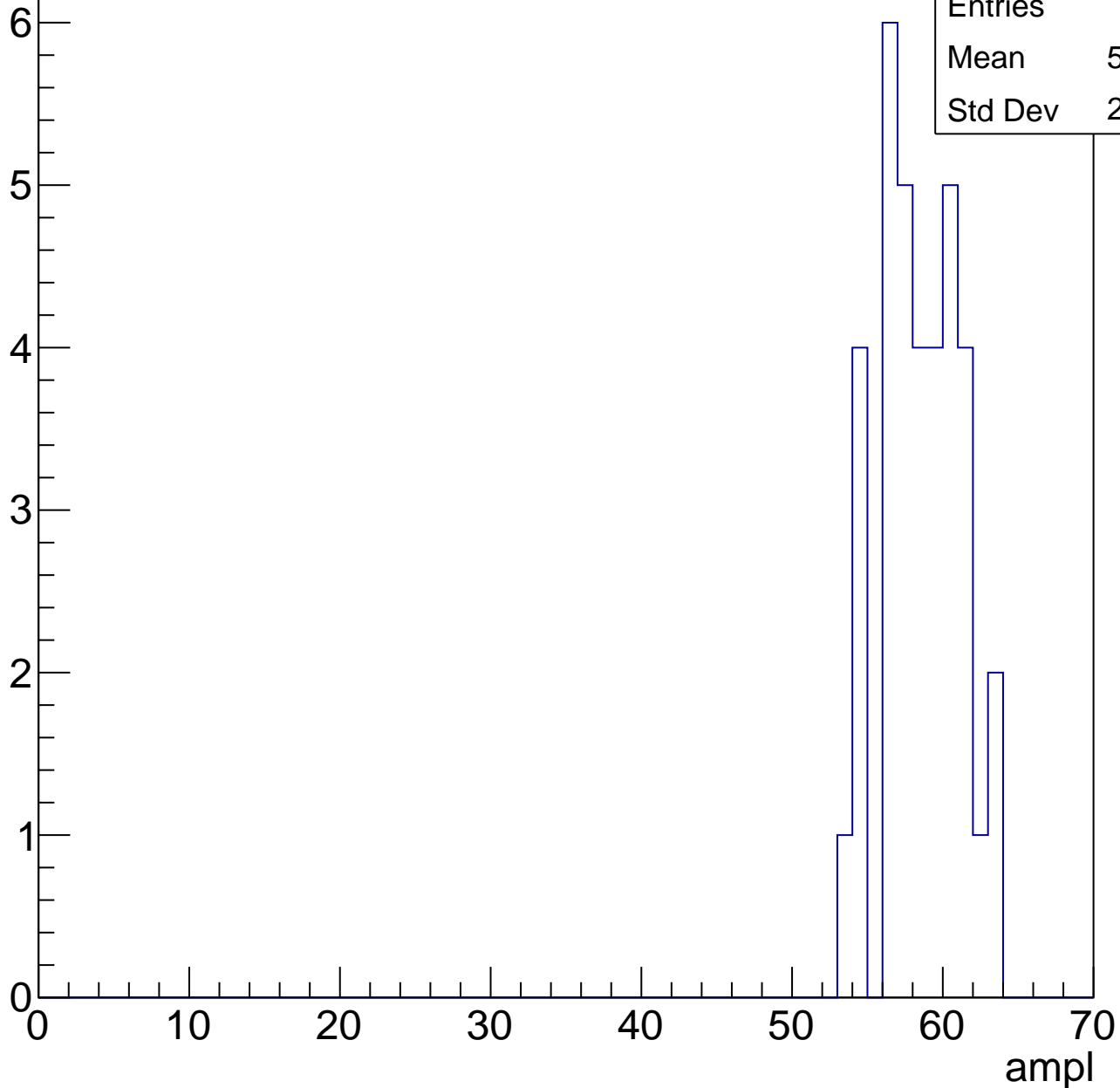


Entries	81
Mean	52.69
Std Dev	3.66

B0L002S, U5-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

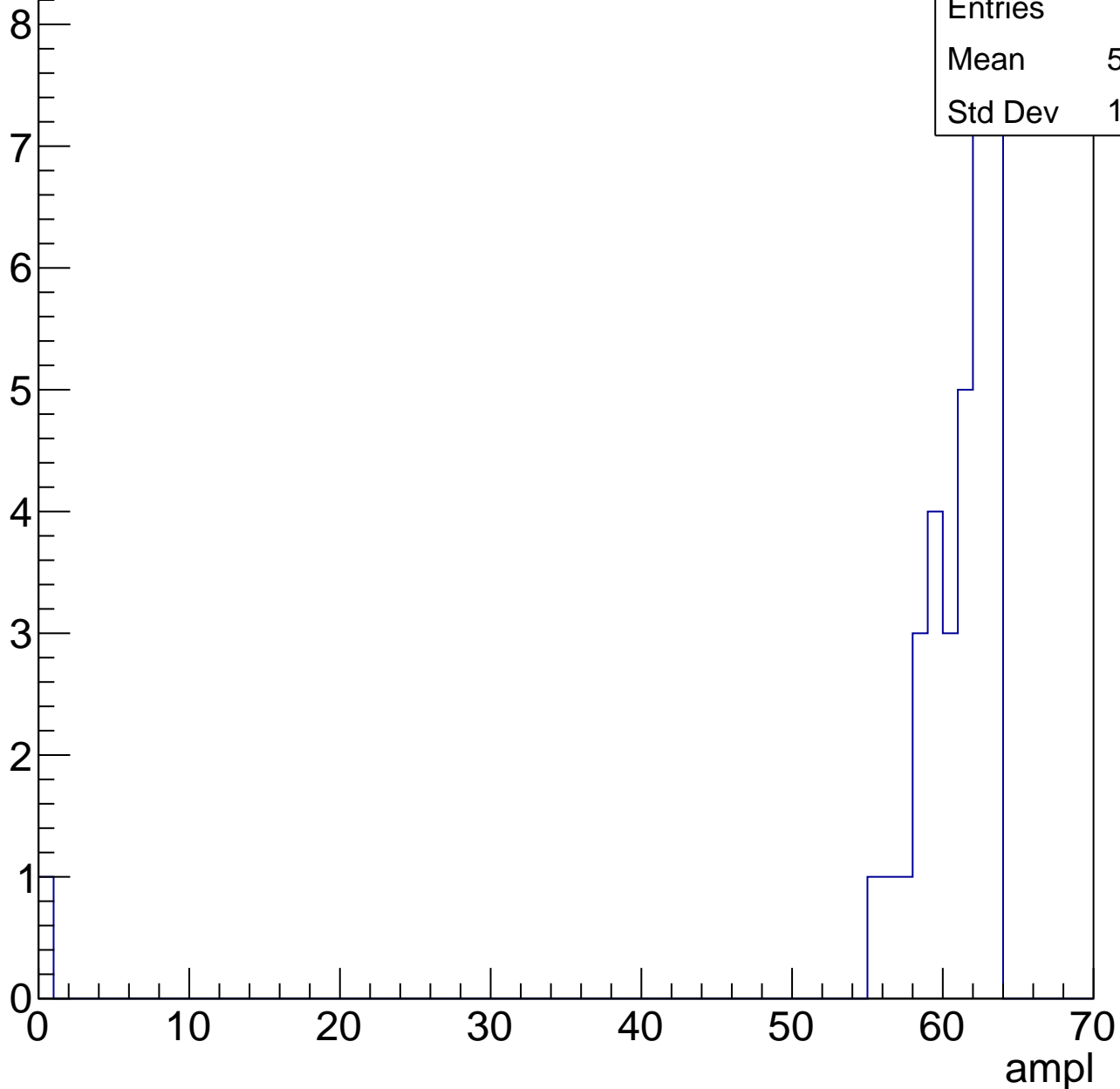


B0L002S, U5-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.94
Std Dev	10.33



B0L002S, U5-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

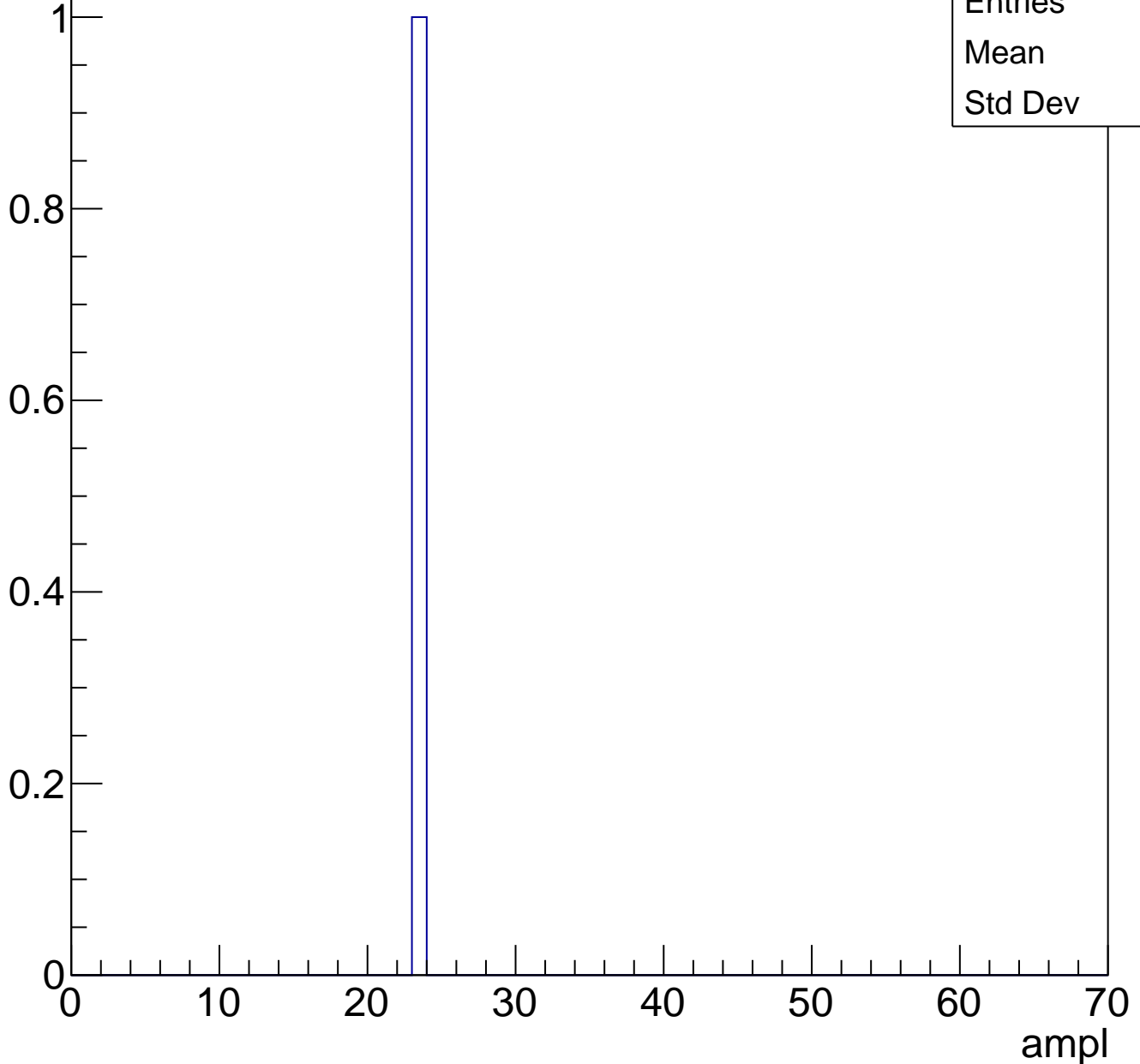


Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U5-ch4, adc0

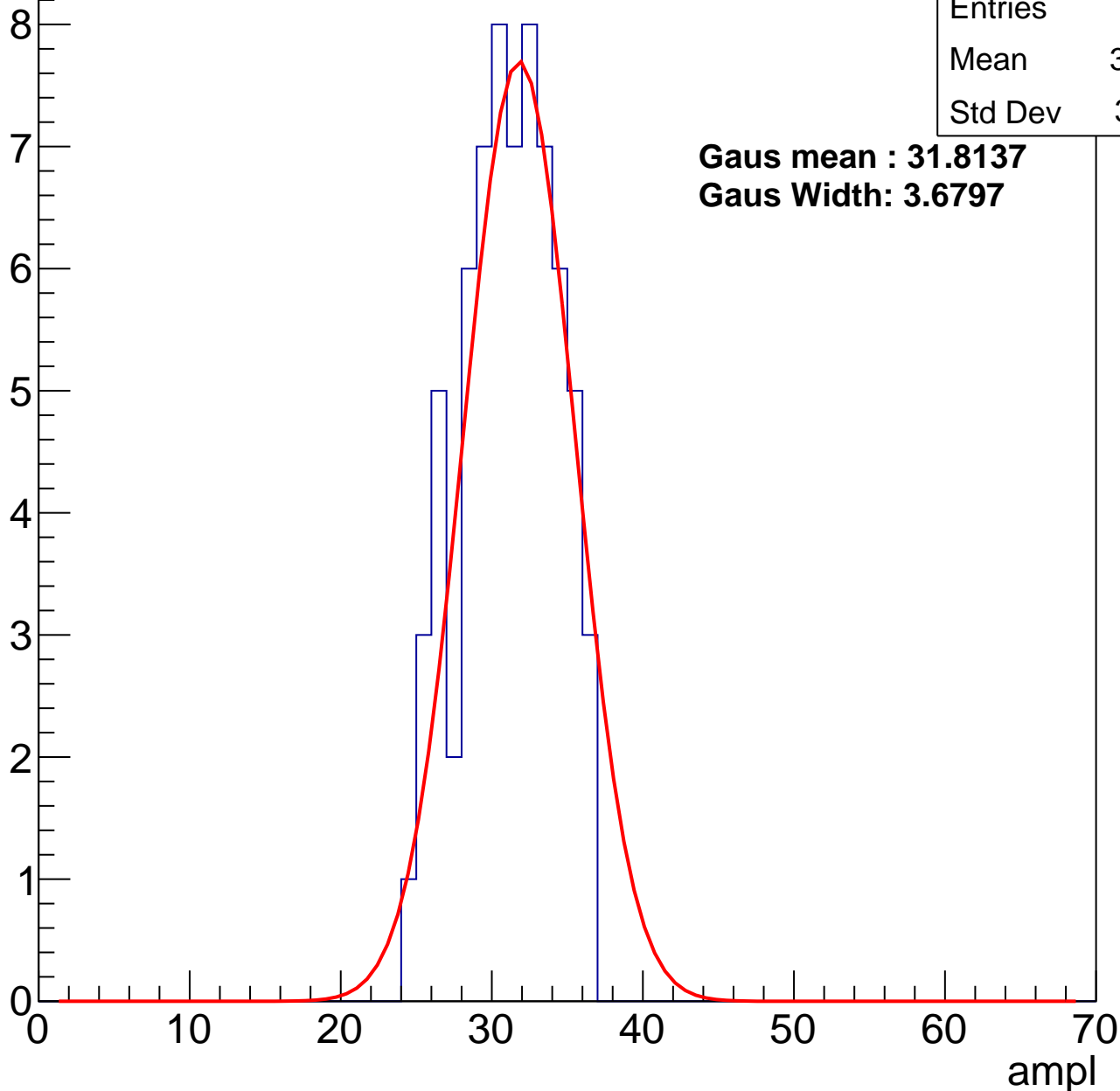
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.66
Std Dev	3.071

Gaus mean : 31.8137

Gaus Width: 3.6797



B0L002S, U5-ch4, adc1

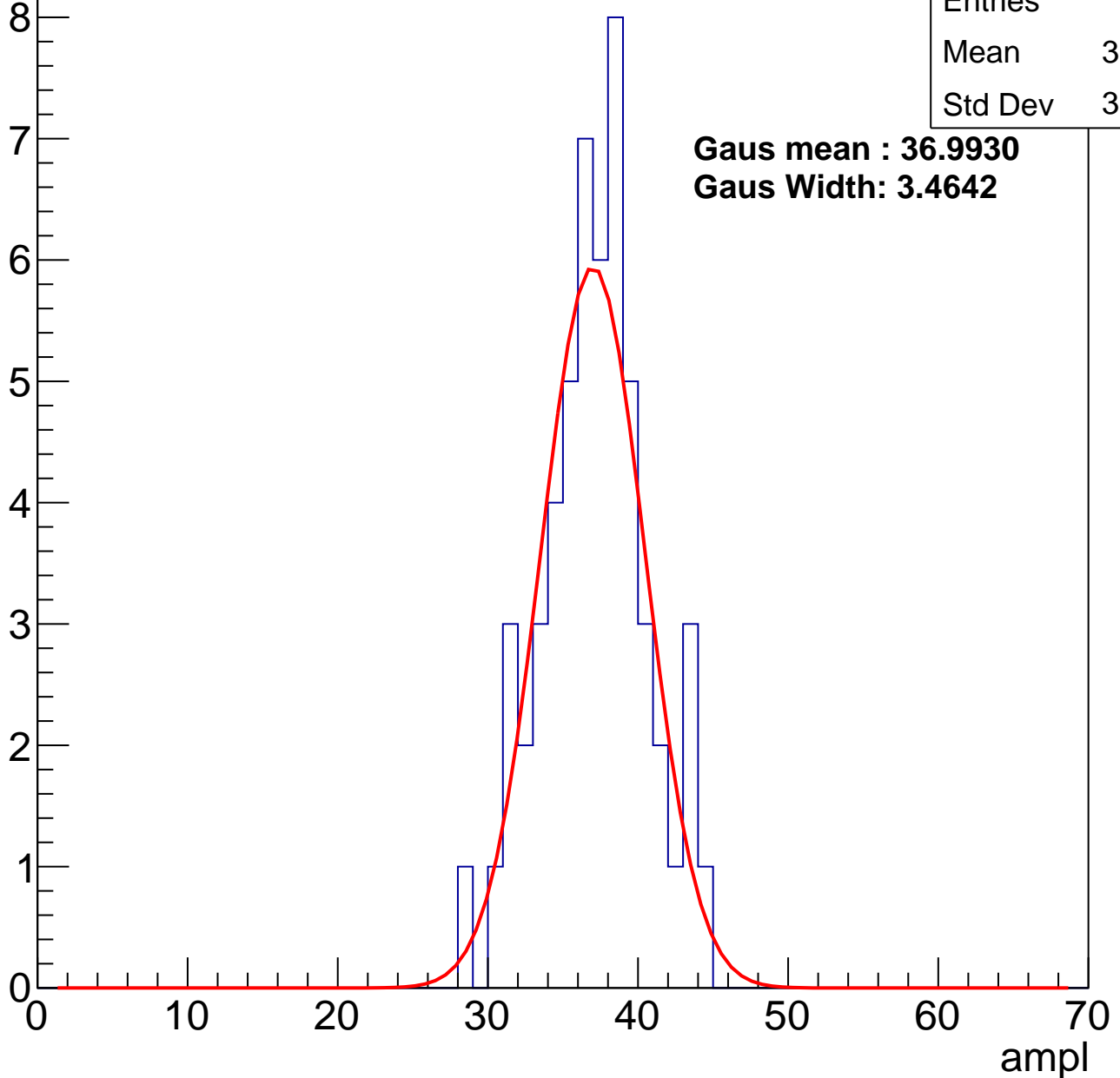
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.64
Std Dev	3.466

Gaus mean : 36.9930

Gaus Width: 3.4642



B0L002S, U5-ch4, adc2

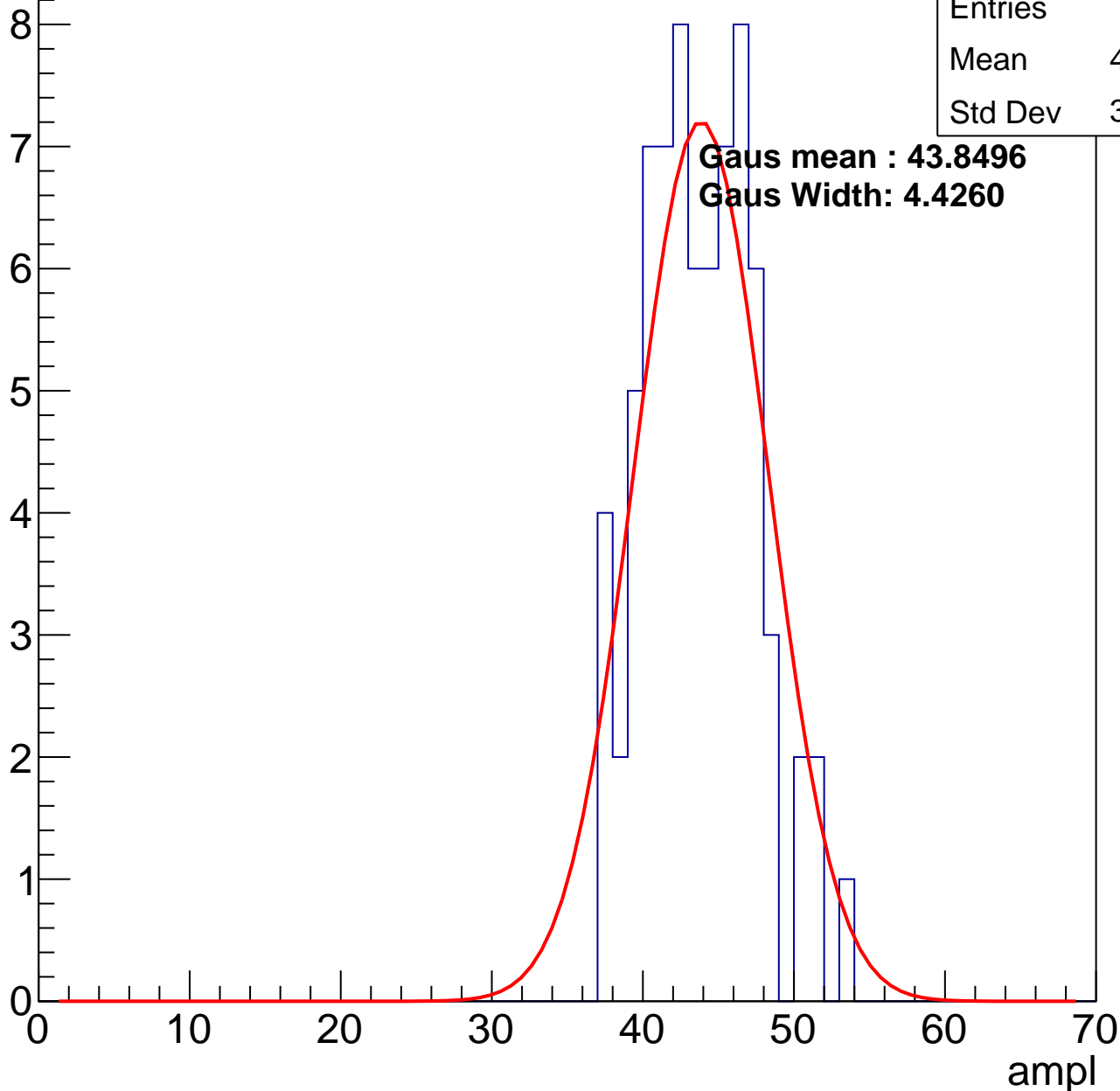
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.35
Std Dev	3.615

Gaus mean : 43.8496

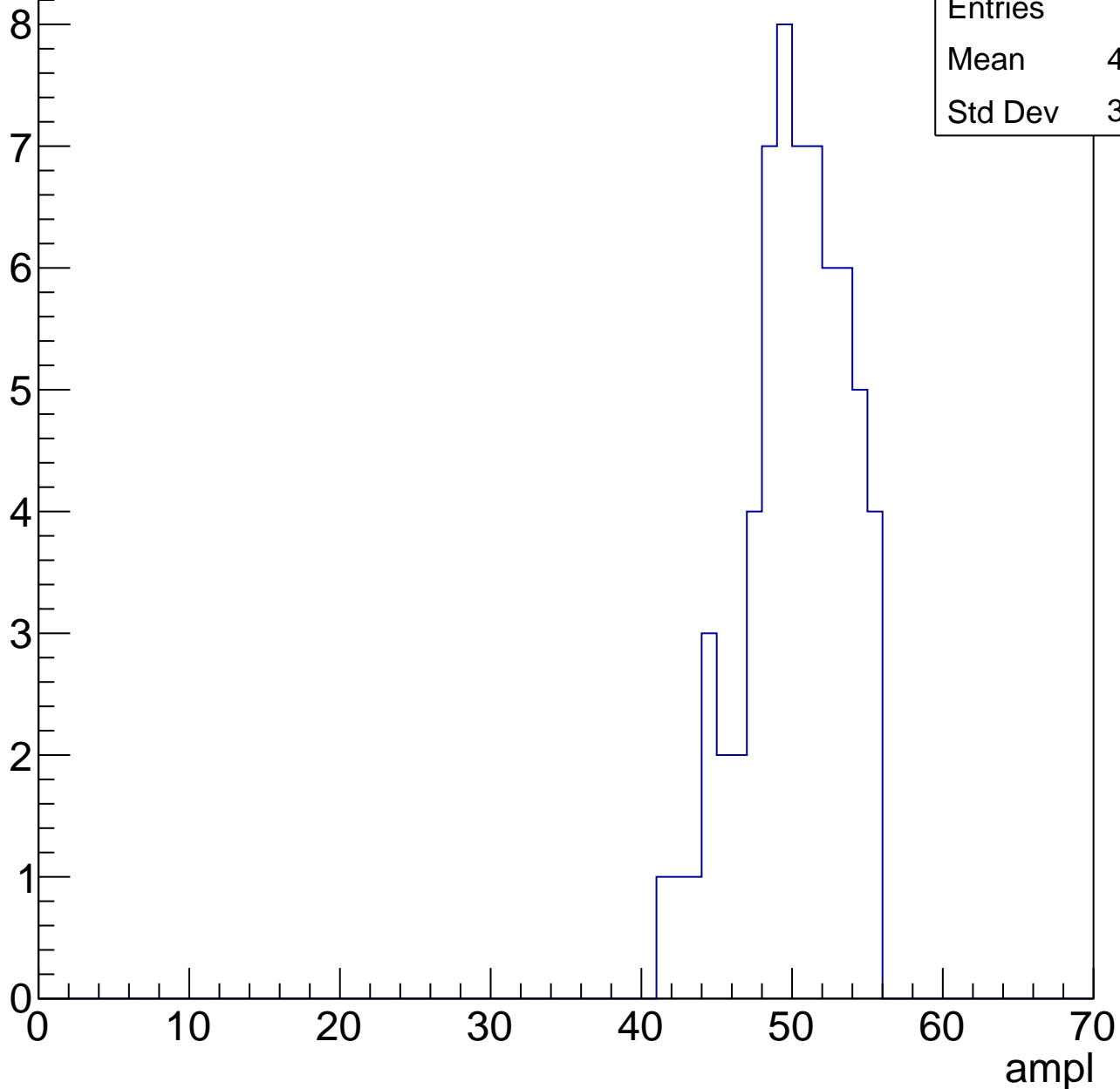
Gaus Width: 4.4260



B0L002S, U5-ch4, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

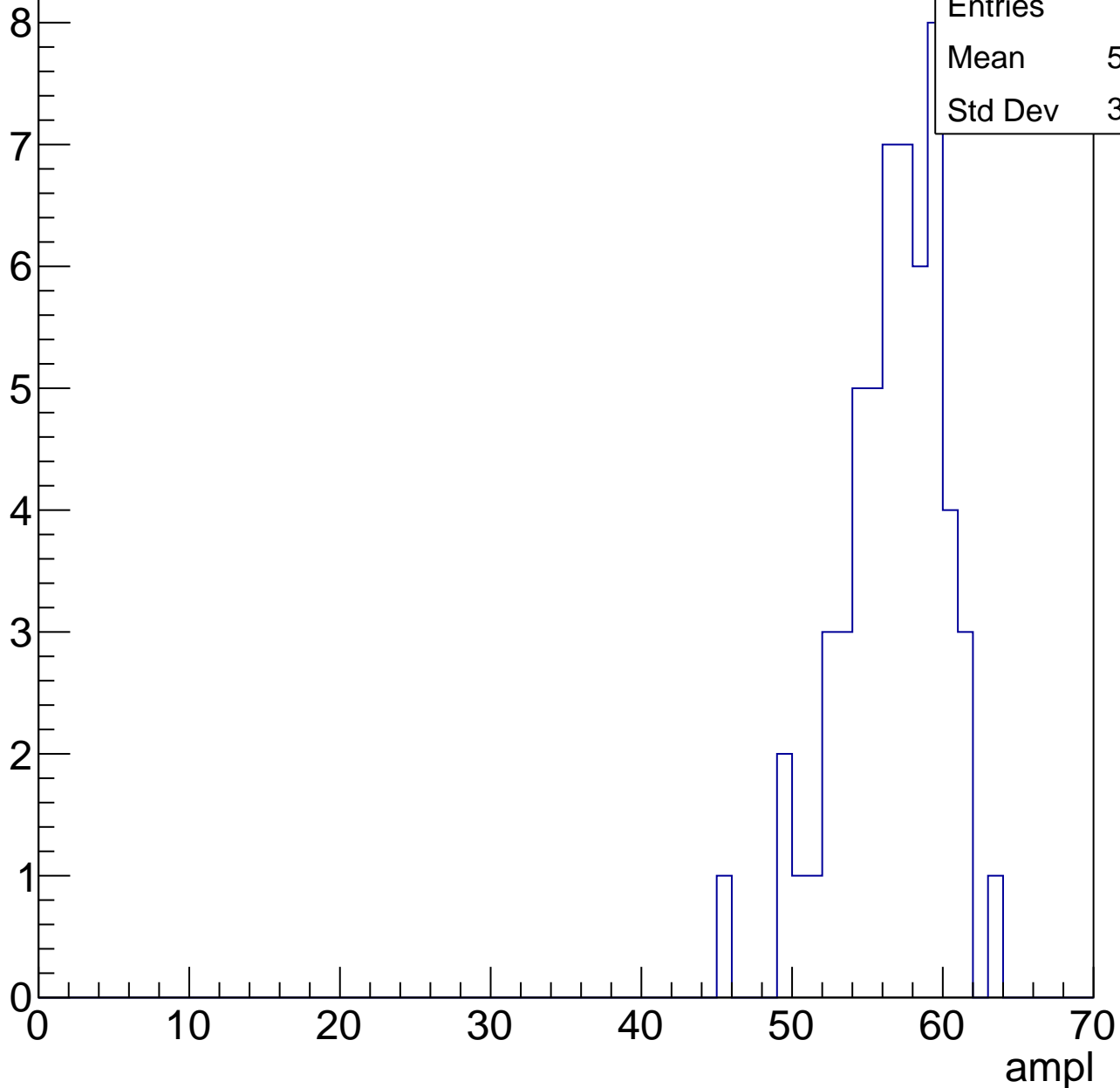


Entries	64
Mean	49.73
Std Dev	3.346

B0L002S, U5-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

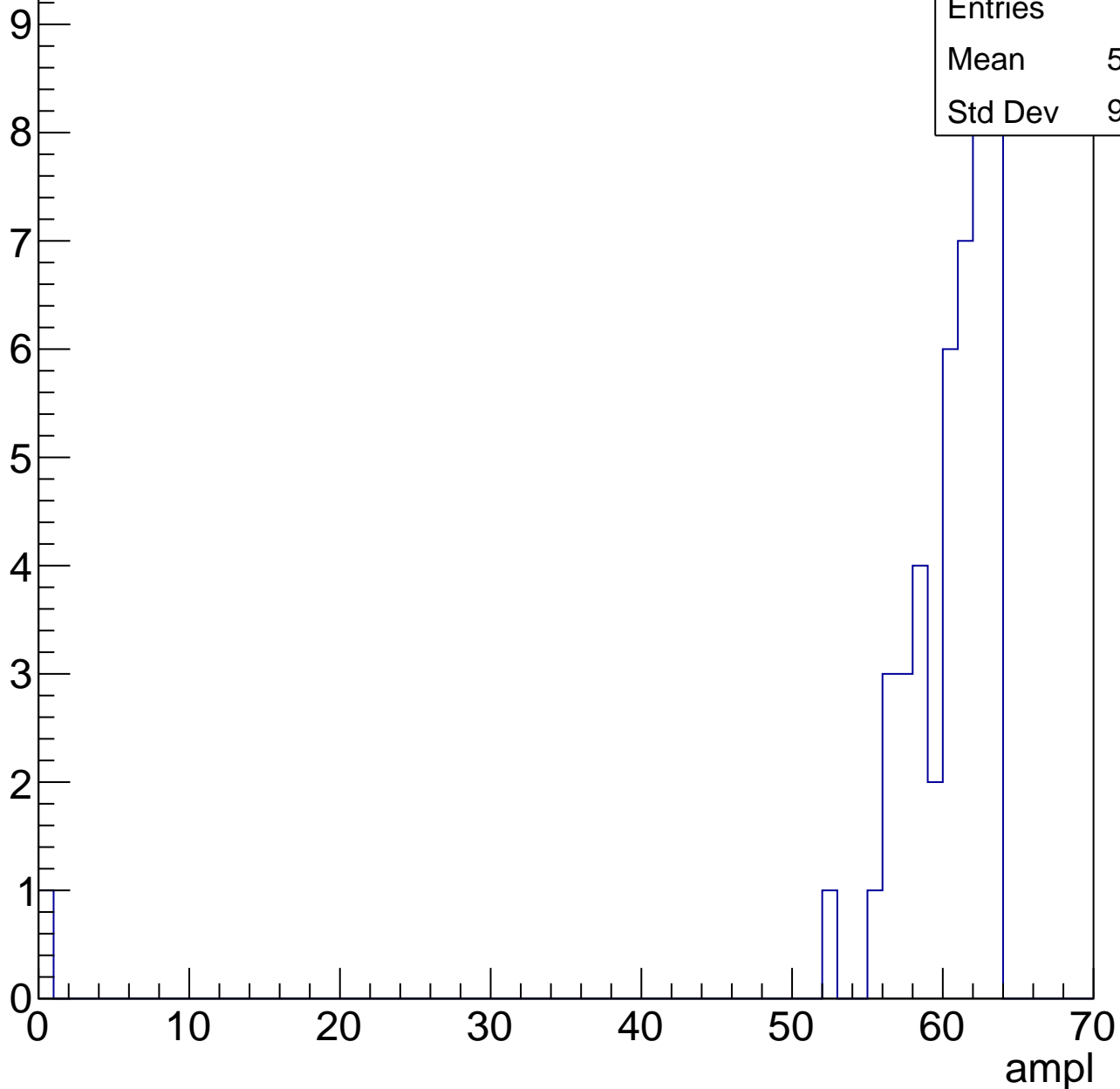


Entries	57
Mean	56.16
Std Dev	3.427

B0L002S, U5-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U5-ch5, adc0

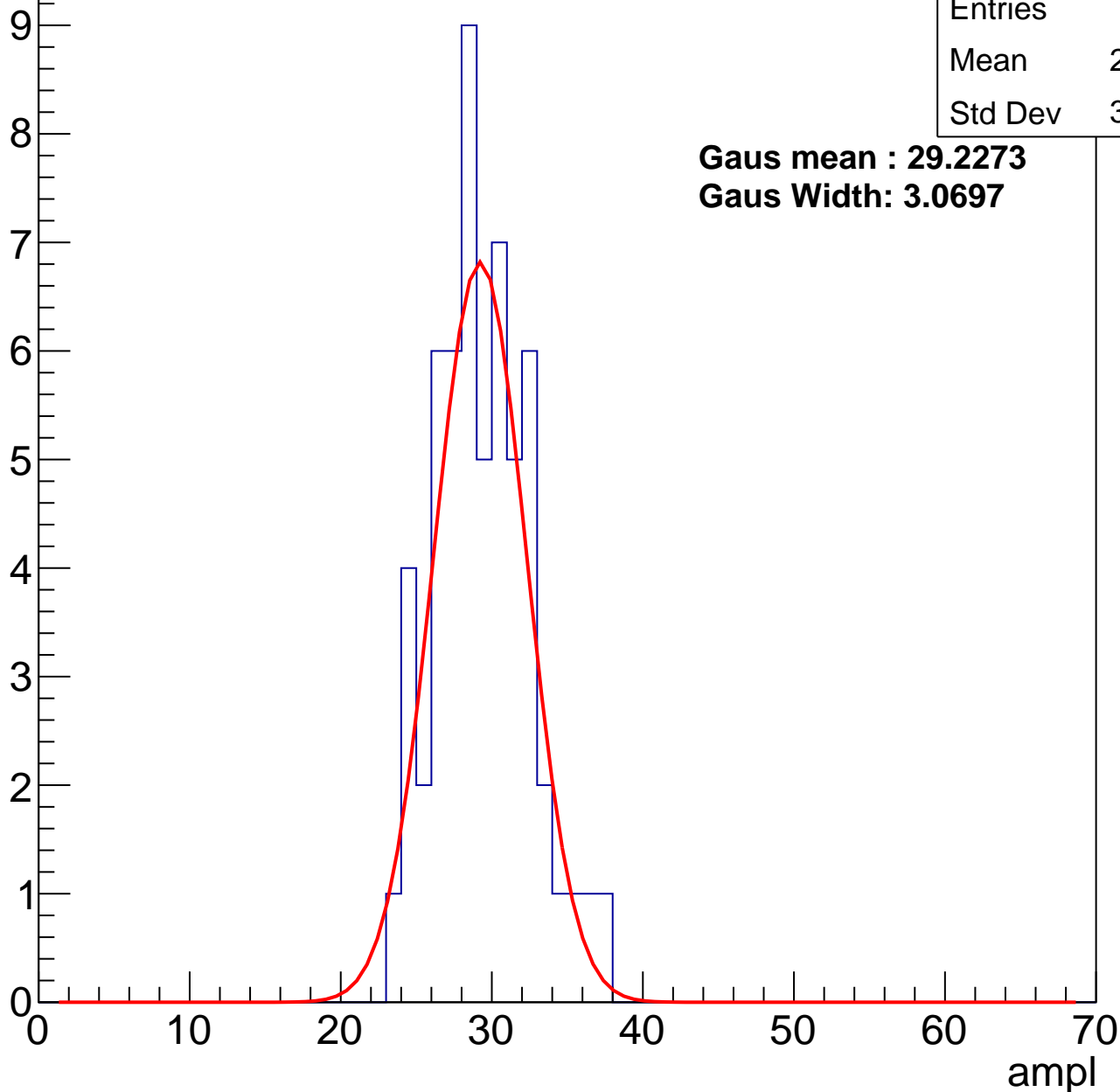
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	28.93
Std Dev	3.083

Gaus mean : 29.2273

Gaus Width: 3.0697



B0L002S, U5-ch5, adc1

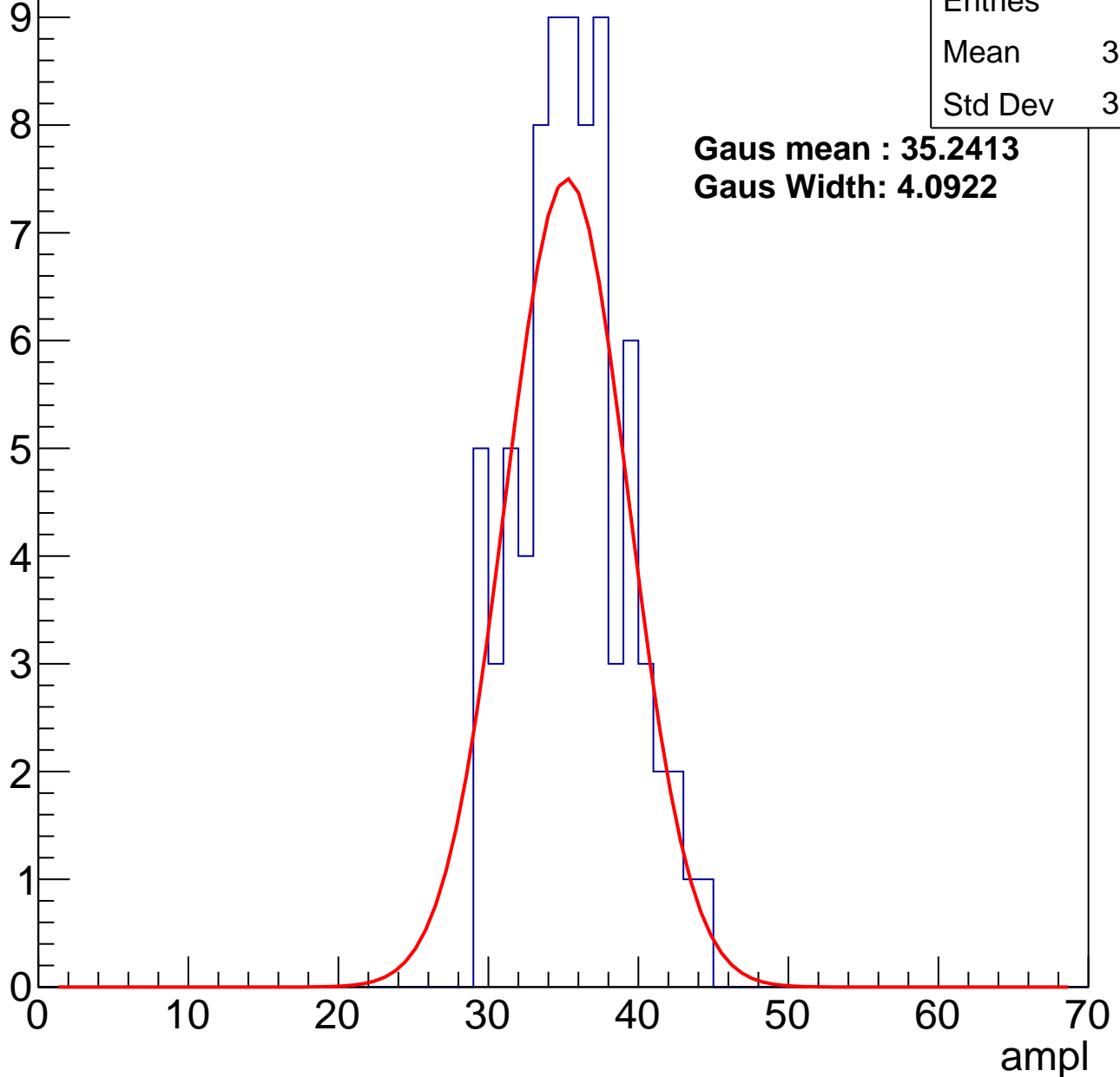
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	35.19
Std Dev	3.519

Gaus mean : 35.2413

Gaus Width: 4.0922



B0L002S, U5-ch5, adc2

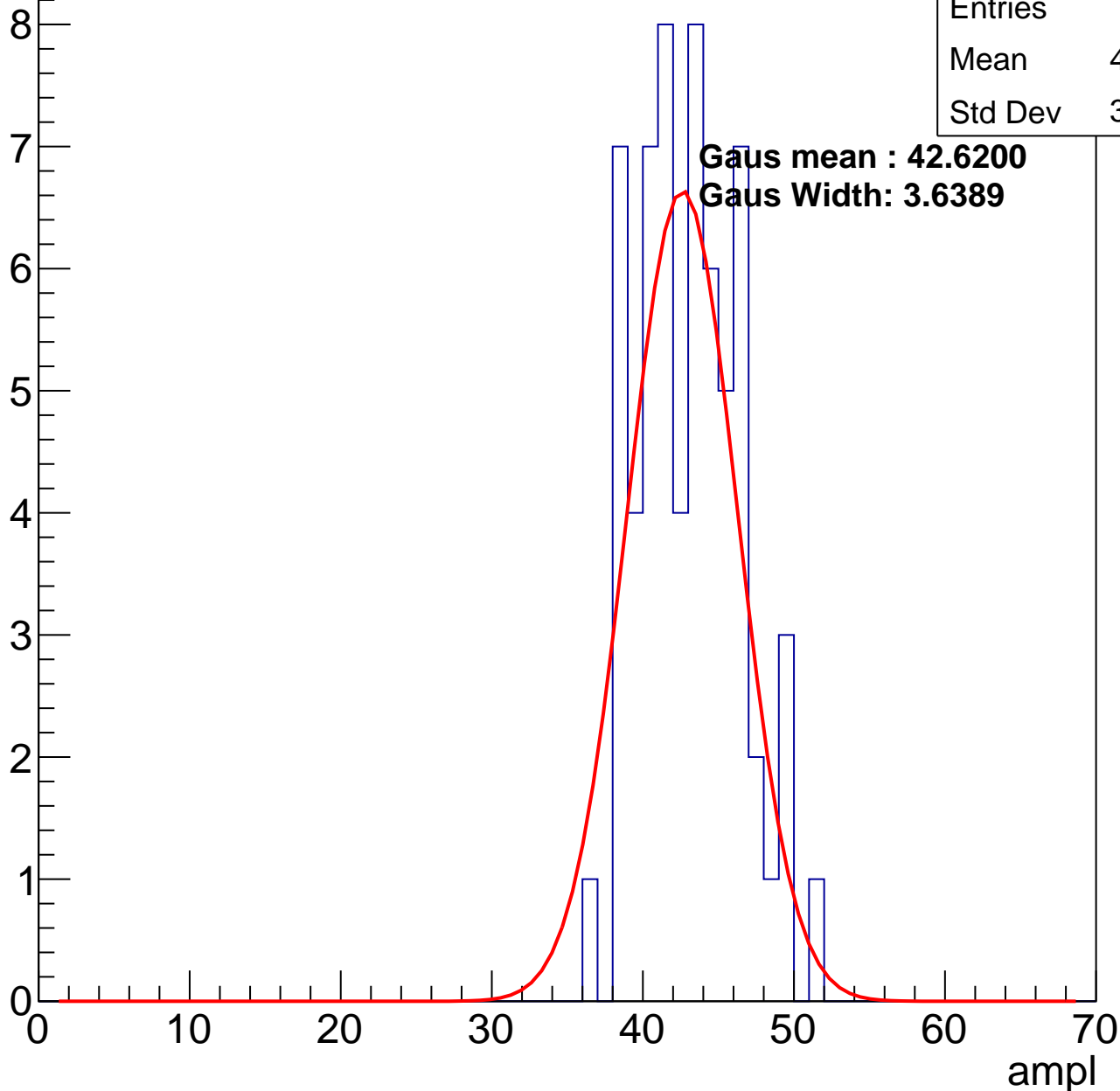
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.64
Std Dev	3.299

Gaus mean : 42.6200

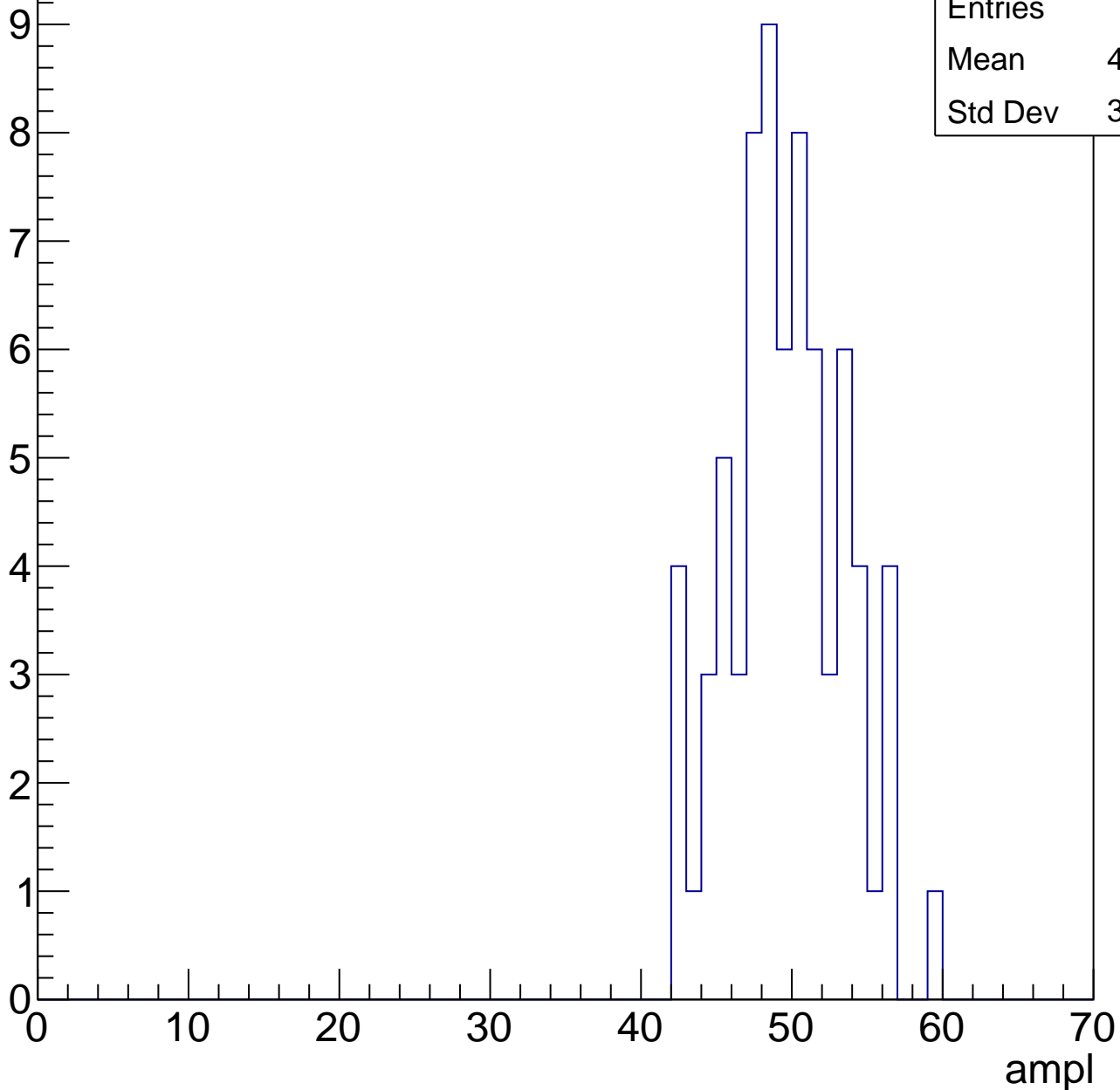
Gaus Width: 3.6389



B0L002S, U5-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

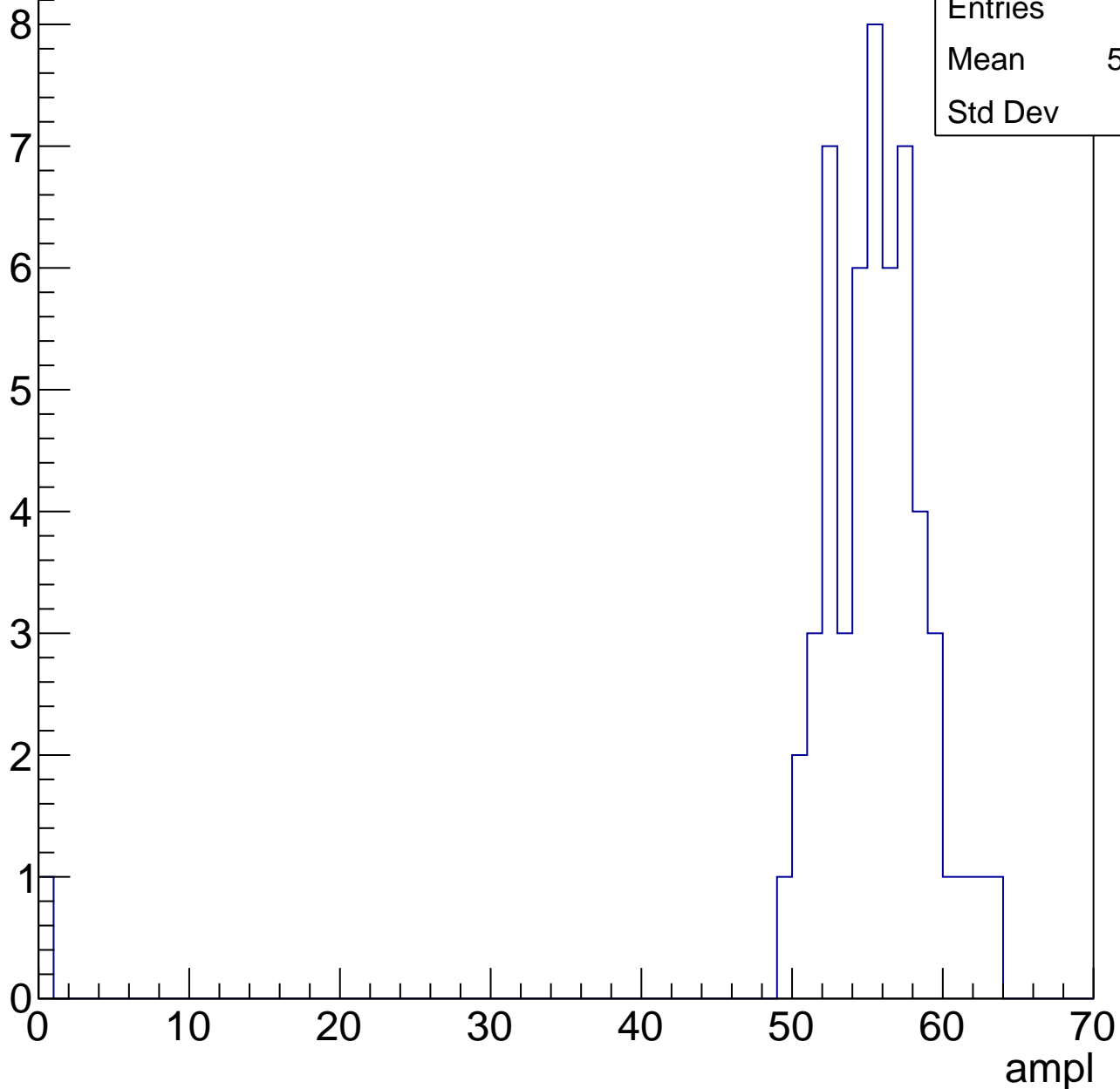
Entry



B0L002S, U5-ch5, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

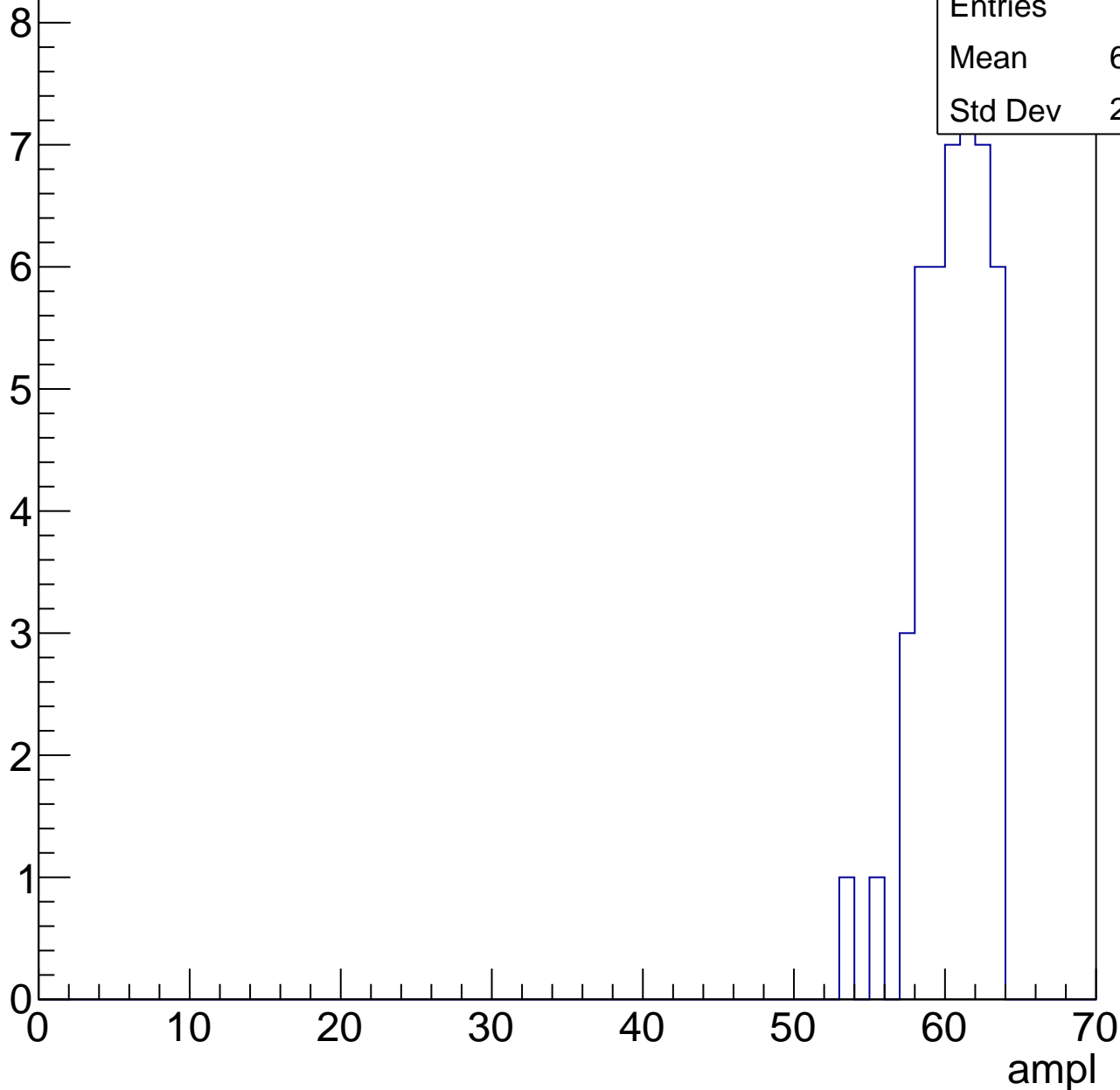


B0L002S, U5-ch5, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

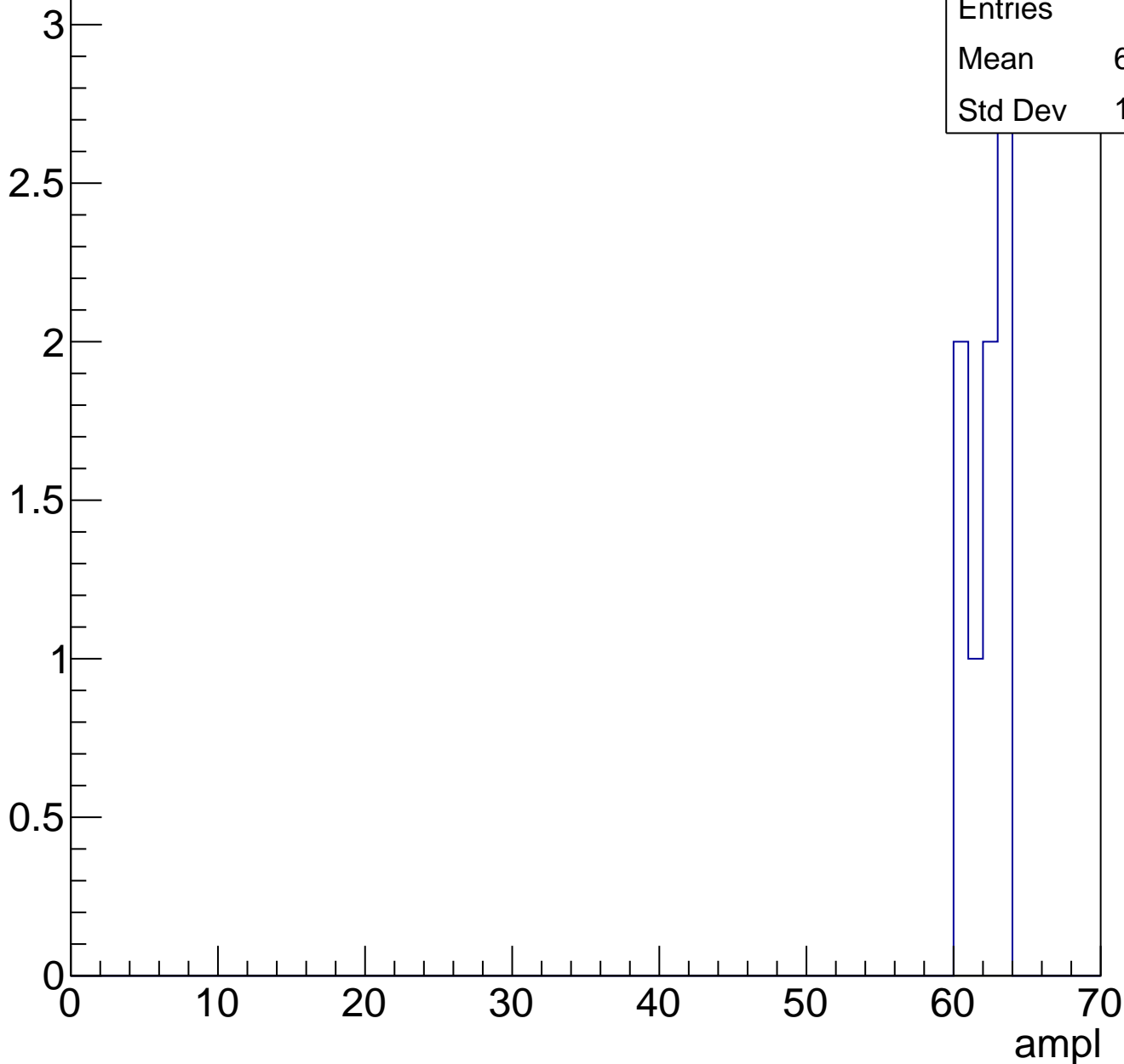
Entries	45
Mean	60.02
Std Dev	2.216



B0L002S, U5-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch6, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	96
Mean	29.53
Std Dev	5.671

Gaus mean : 30.7445

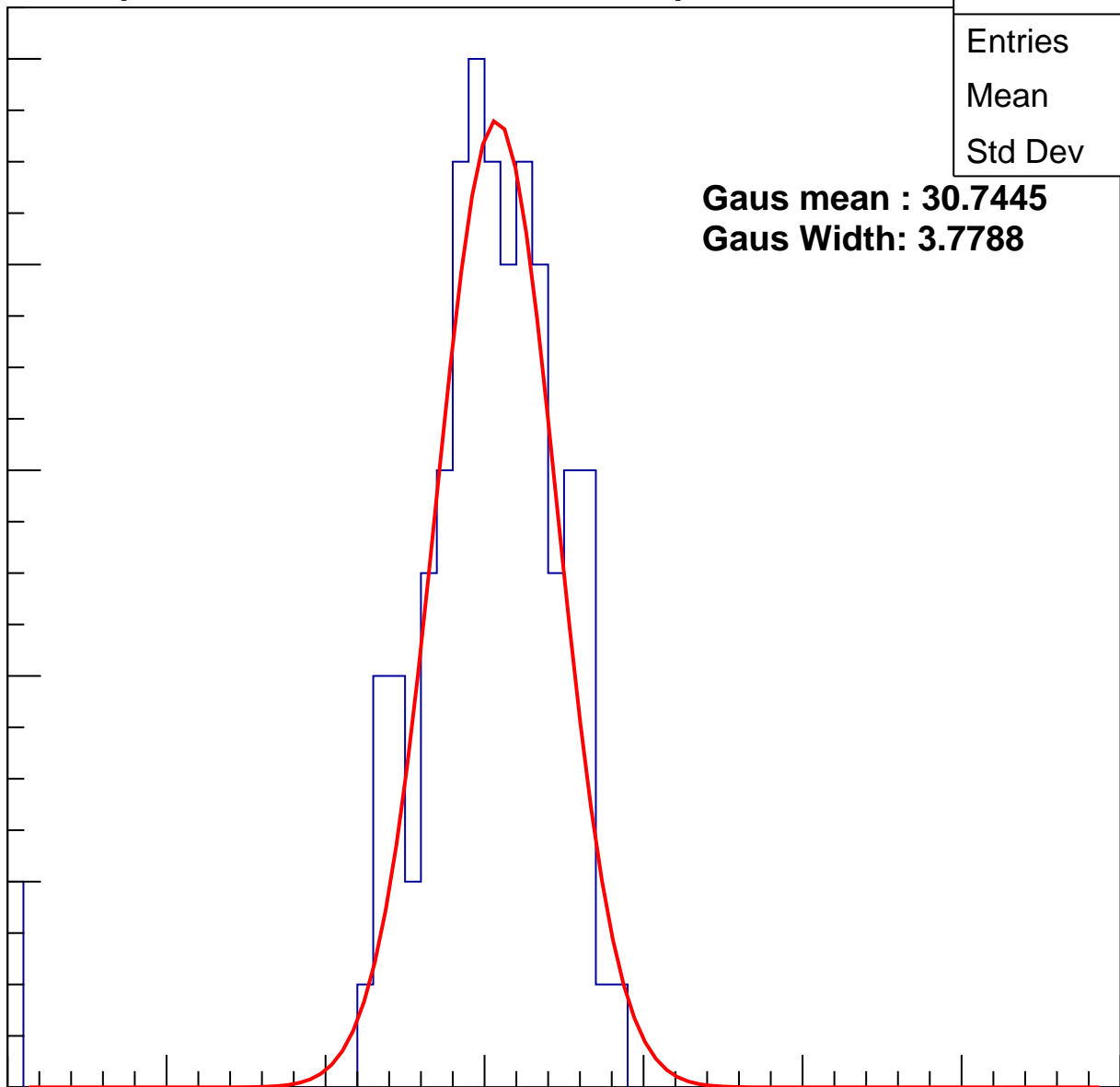
Gaus Width: 3.7788

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



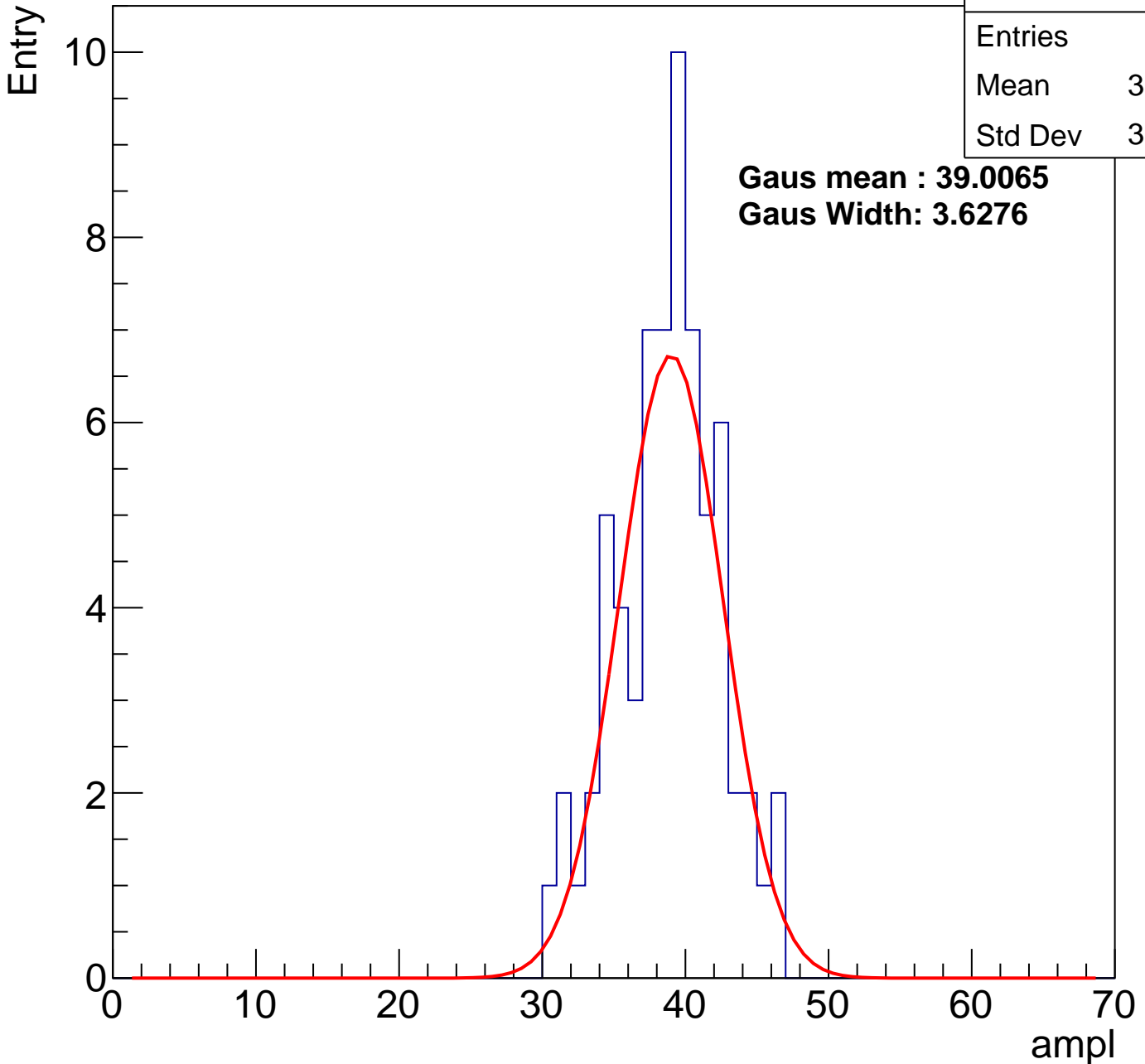
B0L002S, U5-ch6, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	38.37
Std Dev	3.565

Gaus mean : 39.0065

Gaus Width: 3.6276



B0L002S, U5-ch6, adc2

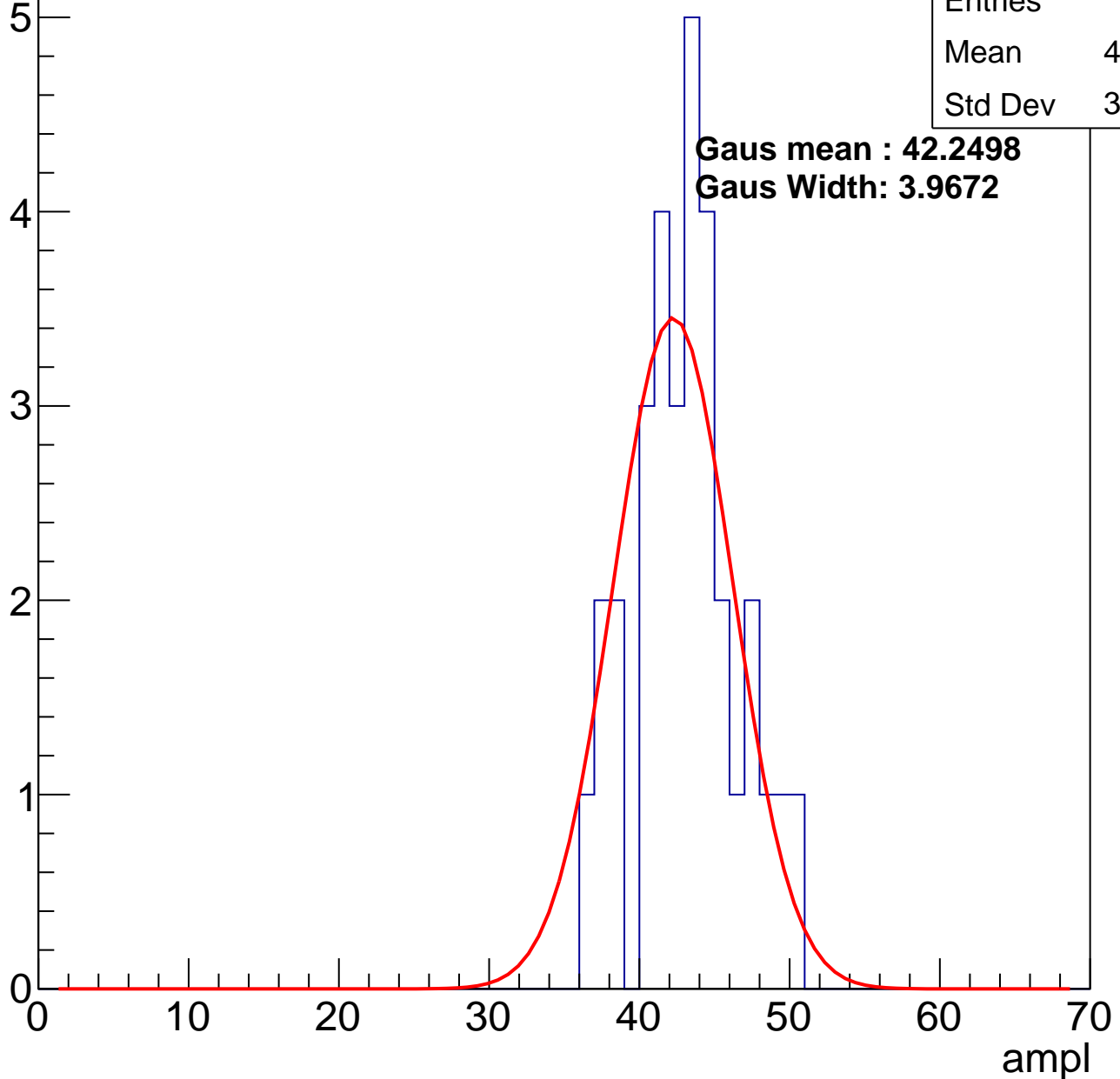
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	42.62
Std Dev	3.426

Gaus mean : 42.2498

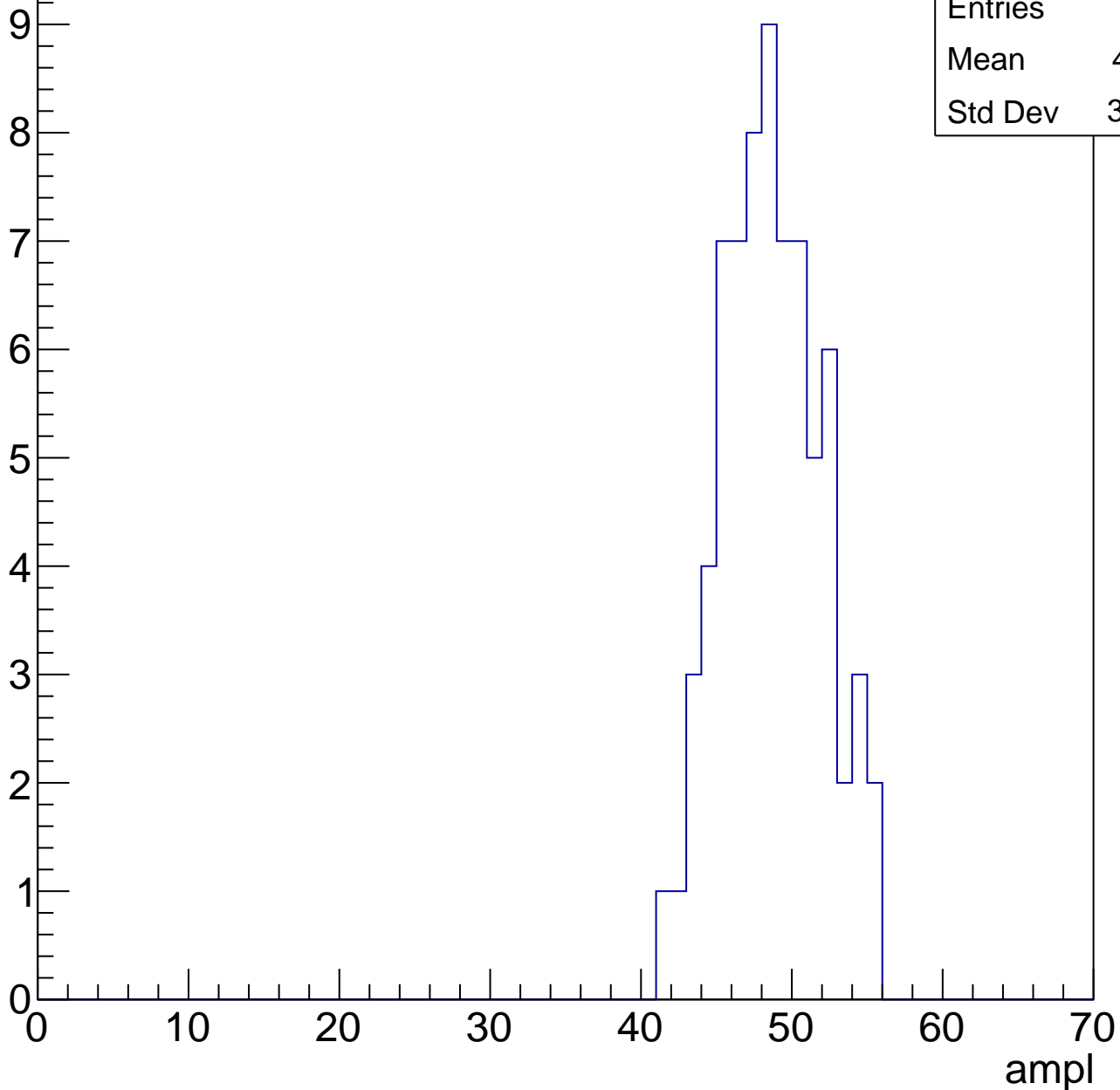
Gaus Width: 3.9672



B0L002S, U5-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

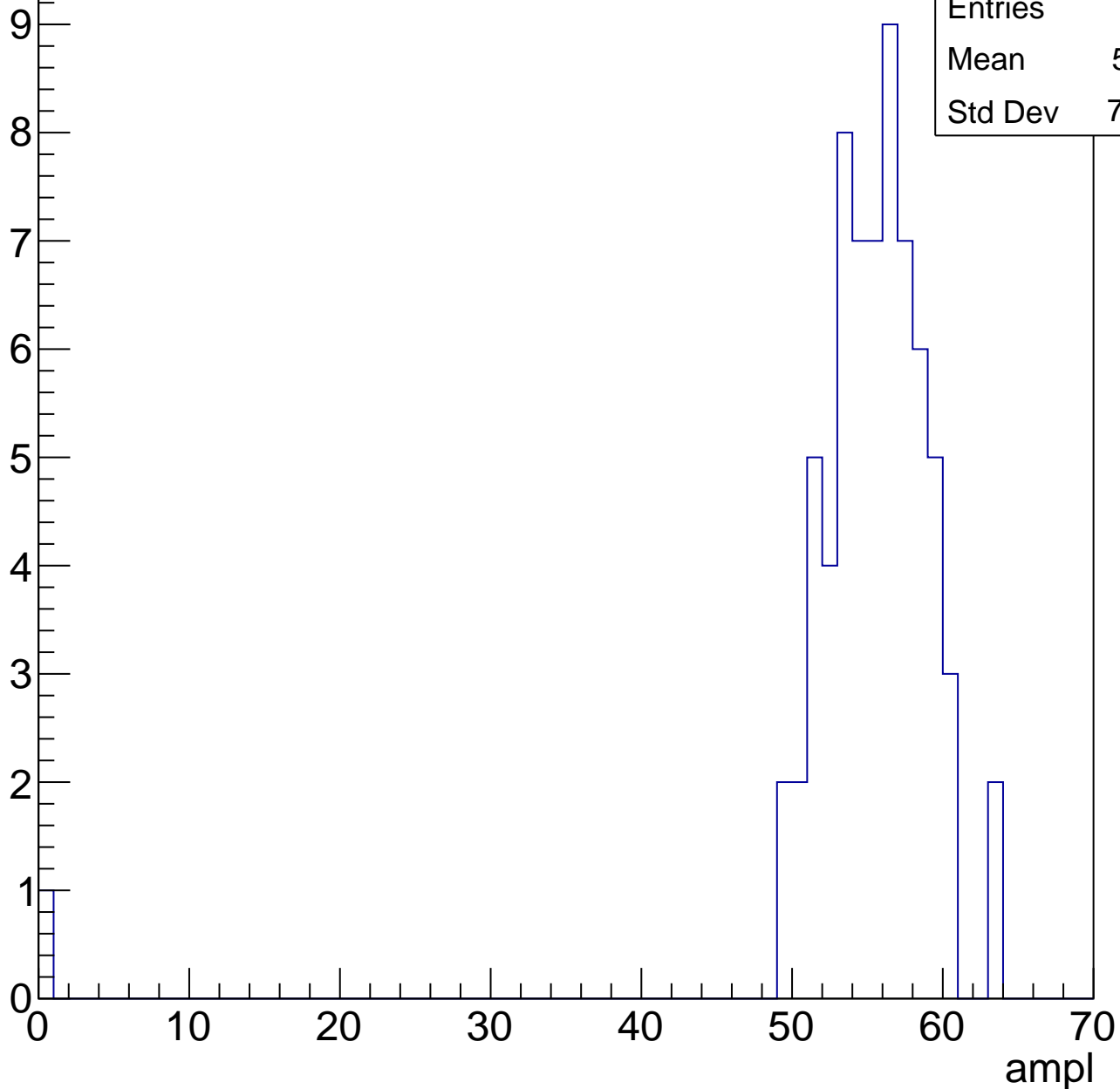


Entries	72
Mean	48.21
Std Dev	3.232

B0L002S, U5-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

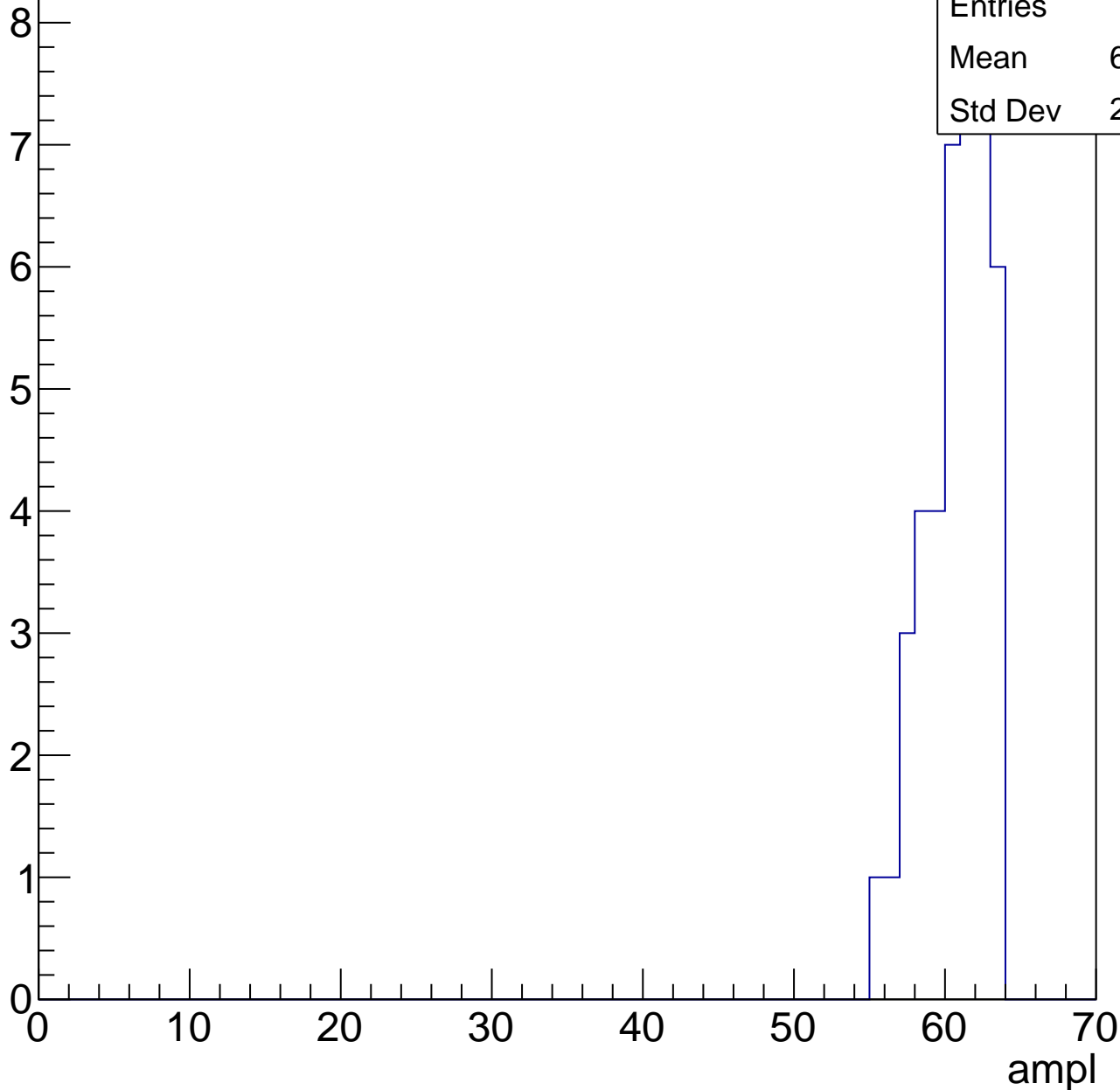


B0L002S, U5-ch6, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	60.29
Std Dev	2.062



B0L002S, U5-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

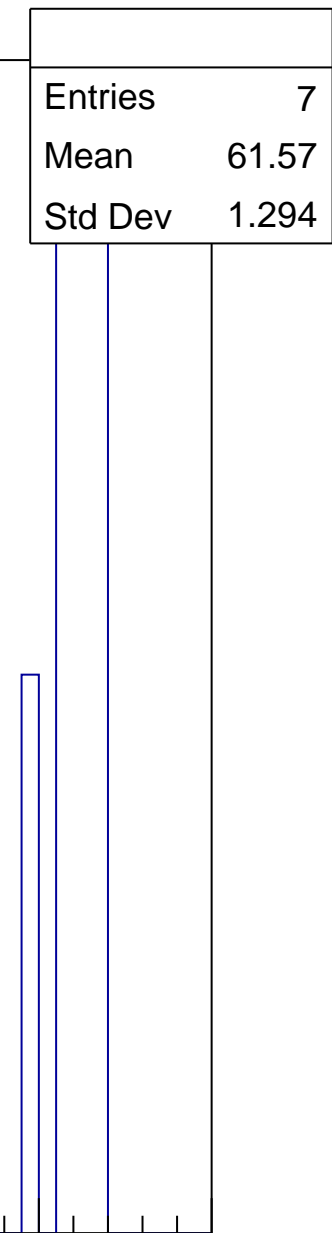
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.57
Std Dev	1.294

0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch7, adc0

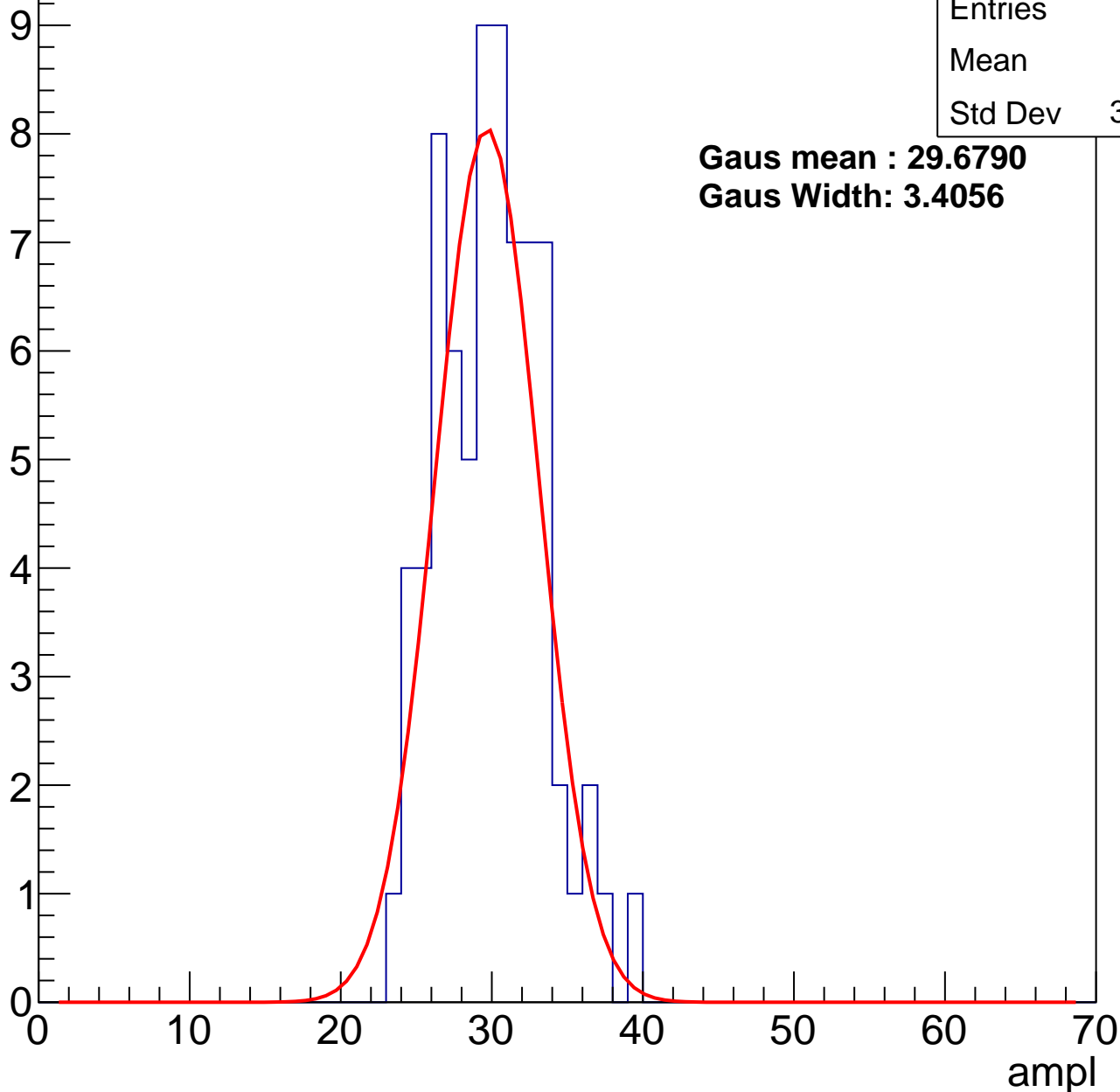
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	29.5
Std Dev	3.374

Gaus mean : 29.6790

Gaus Width: 3.4056



B0L002S, U5-ch7, adc1

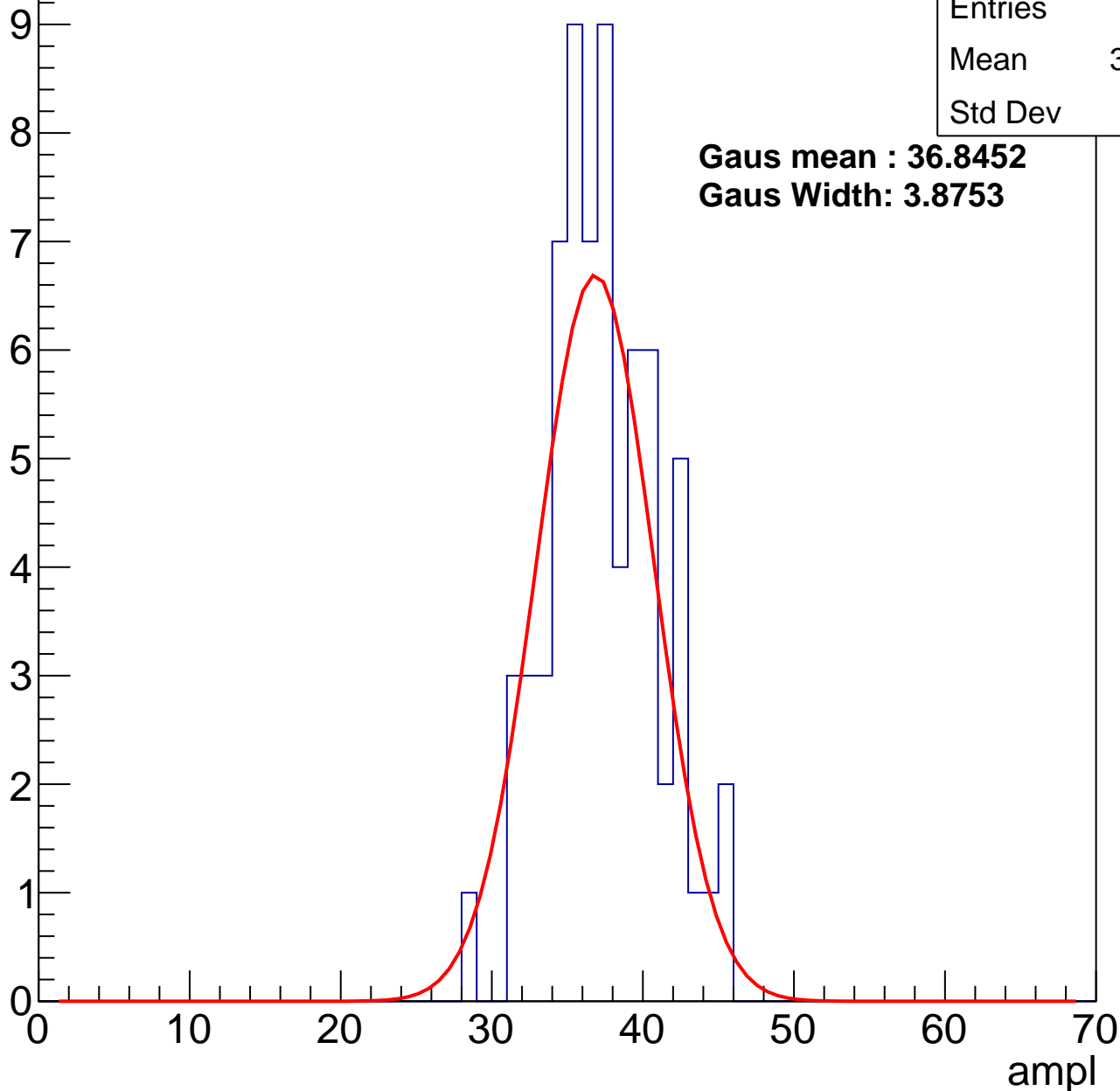
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.94
Std Dev	3.55

Gaus mean : 36.8452

Gaus Width: 3.8753



B0L002S, U5-ch7, adc2

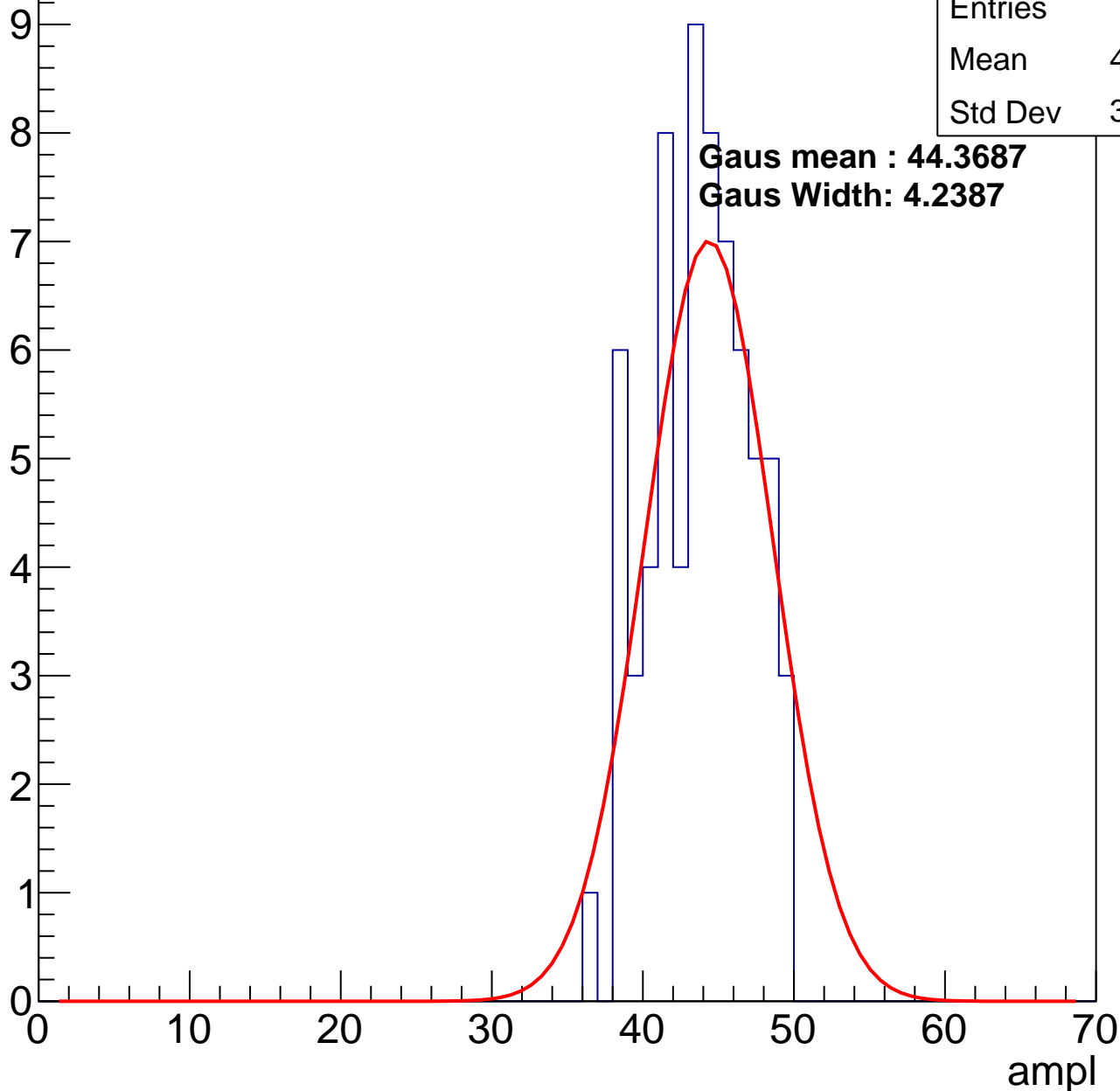
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.32
Std Dev	3.219

Gaus mean : 44.3687

Gaus Width: 4.2387

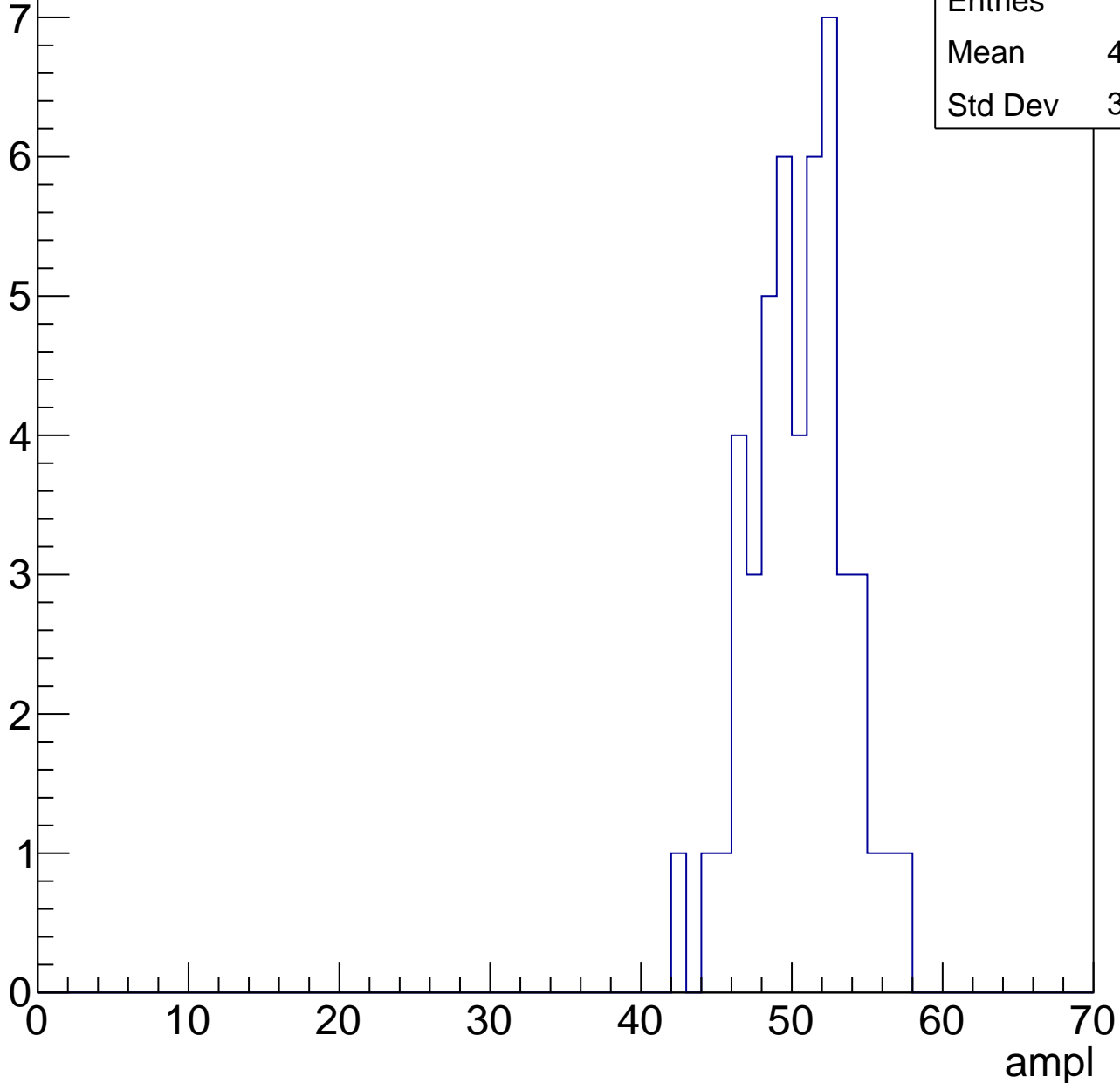


B0L002S, U5-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	49.98
Std Dev	3.132

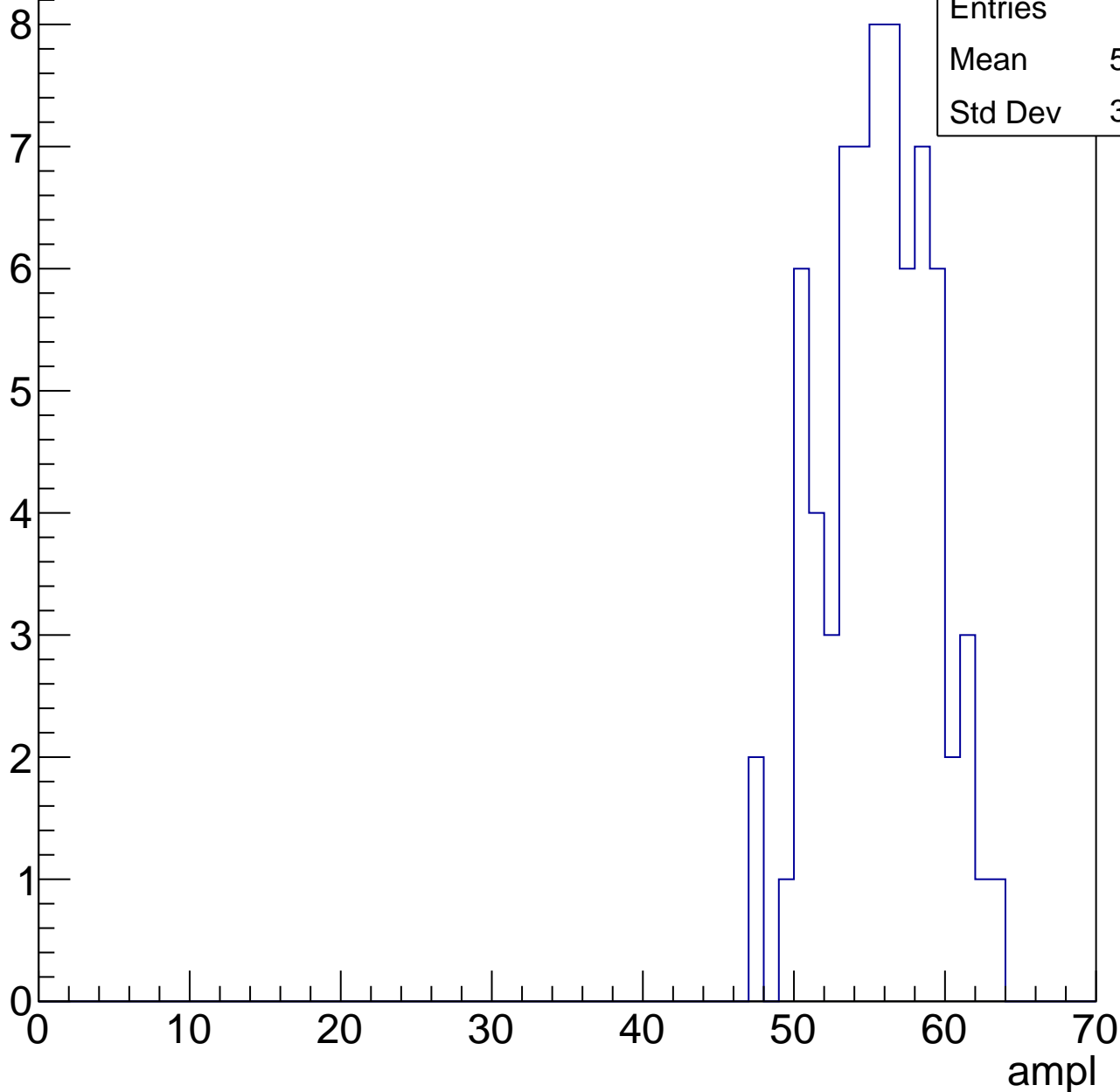


B0L002S, U5-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	55.14
Std Dev	3.537



B0L002S, U5-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

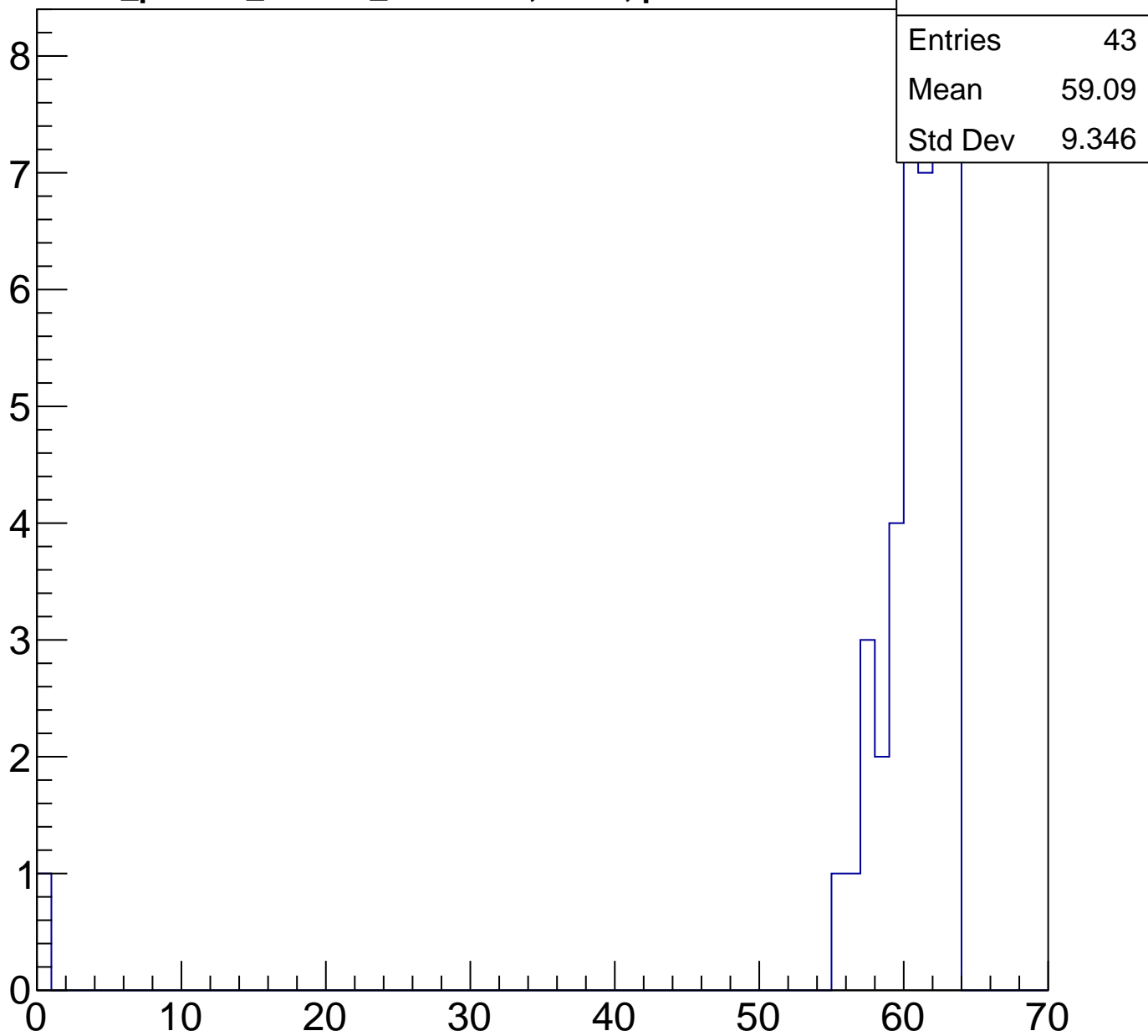
Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.09
Std Dev	9.346

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61
Std Dev	2.16

ampl

B0L002S, U5-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch8, adc0

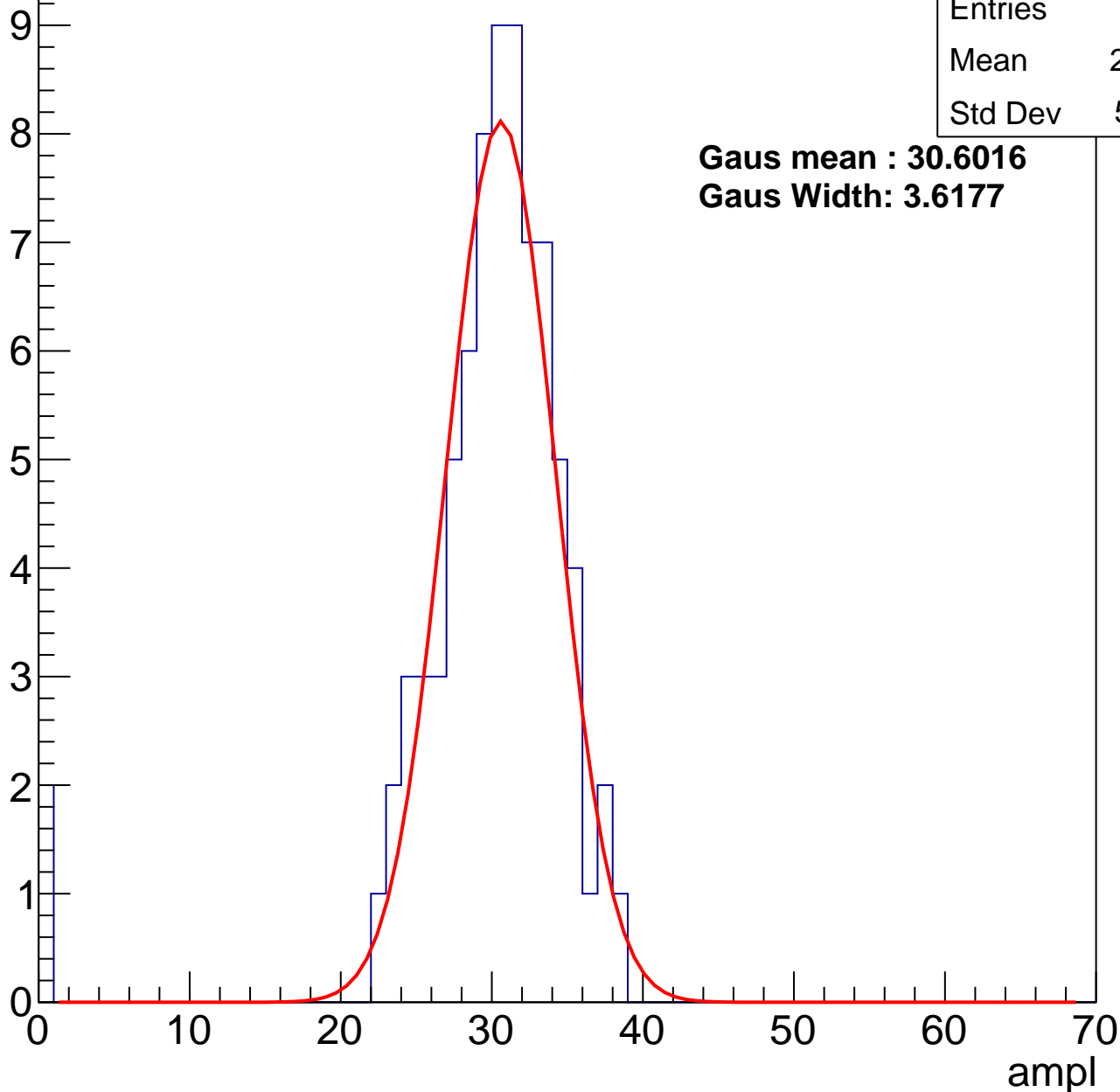
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.36
Std Dev	5.901

Gaus mean : 30.6016

Gaus Width: 3.6177



B0L002S, U5-ch8, adc1

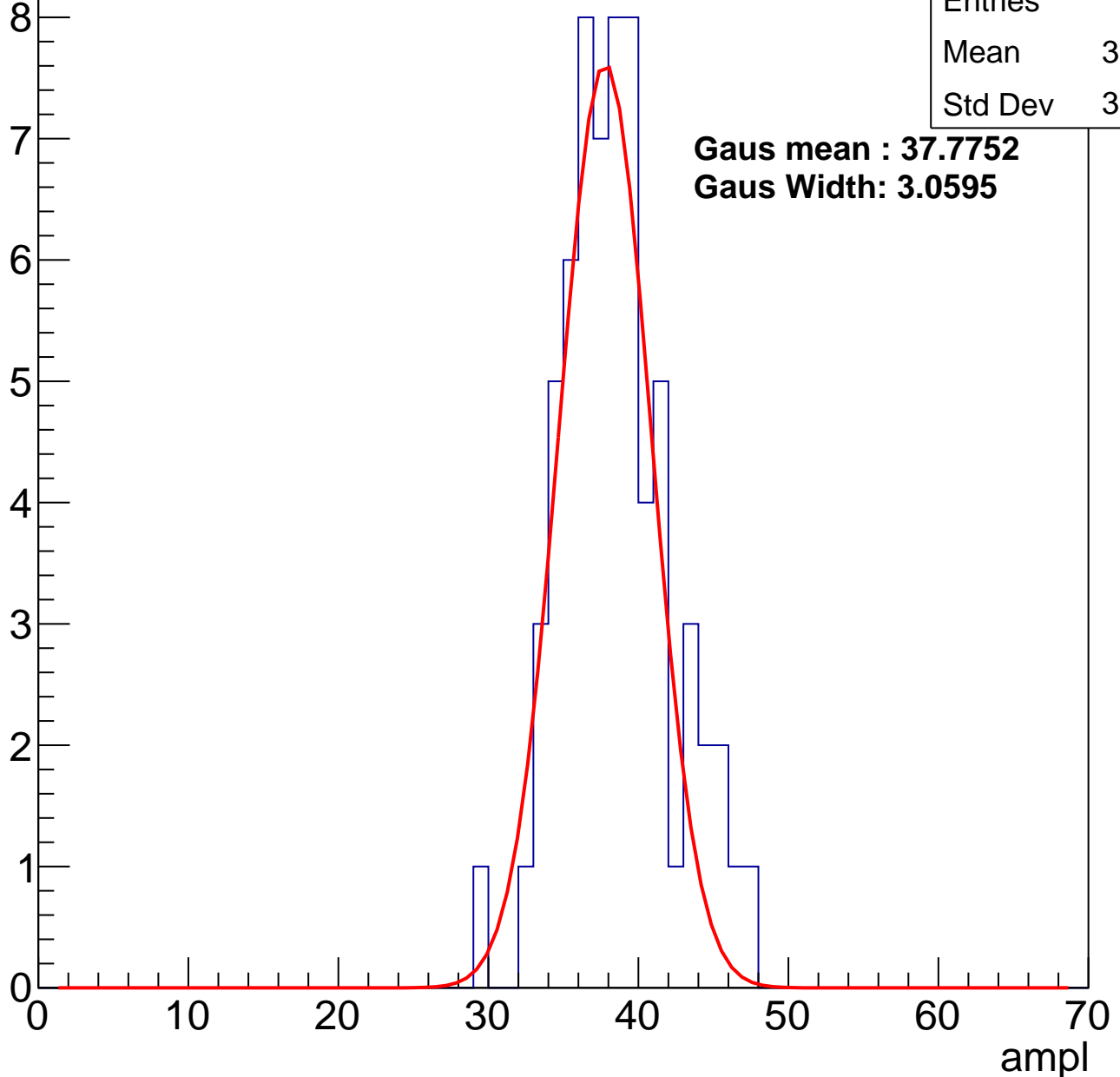
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.03
Std Dev	3.567

Gaus mean : 37.7752

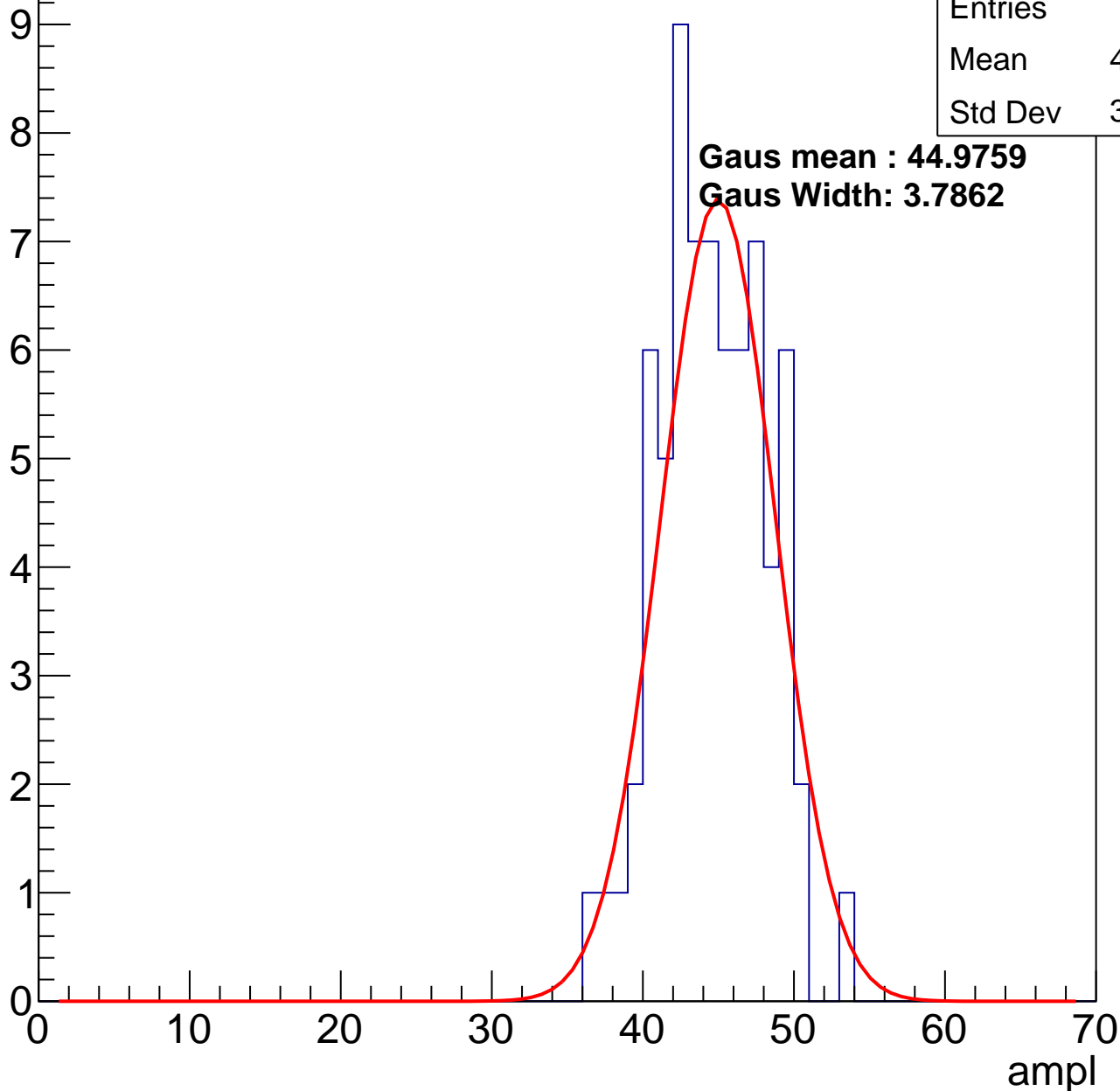
Gaus Width: 3.0595



B0L002S, U5-ch8, adc2

calib_packv5_042523_0143.root, FC#8, port C1

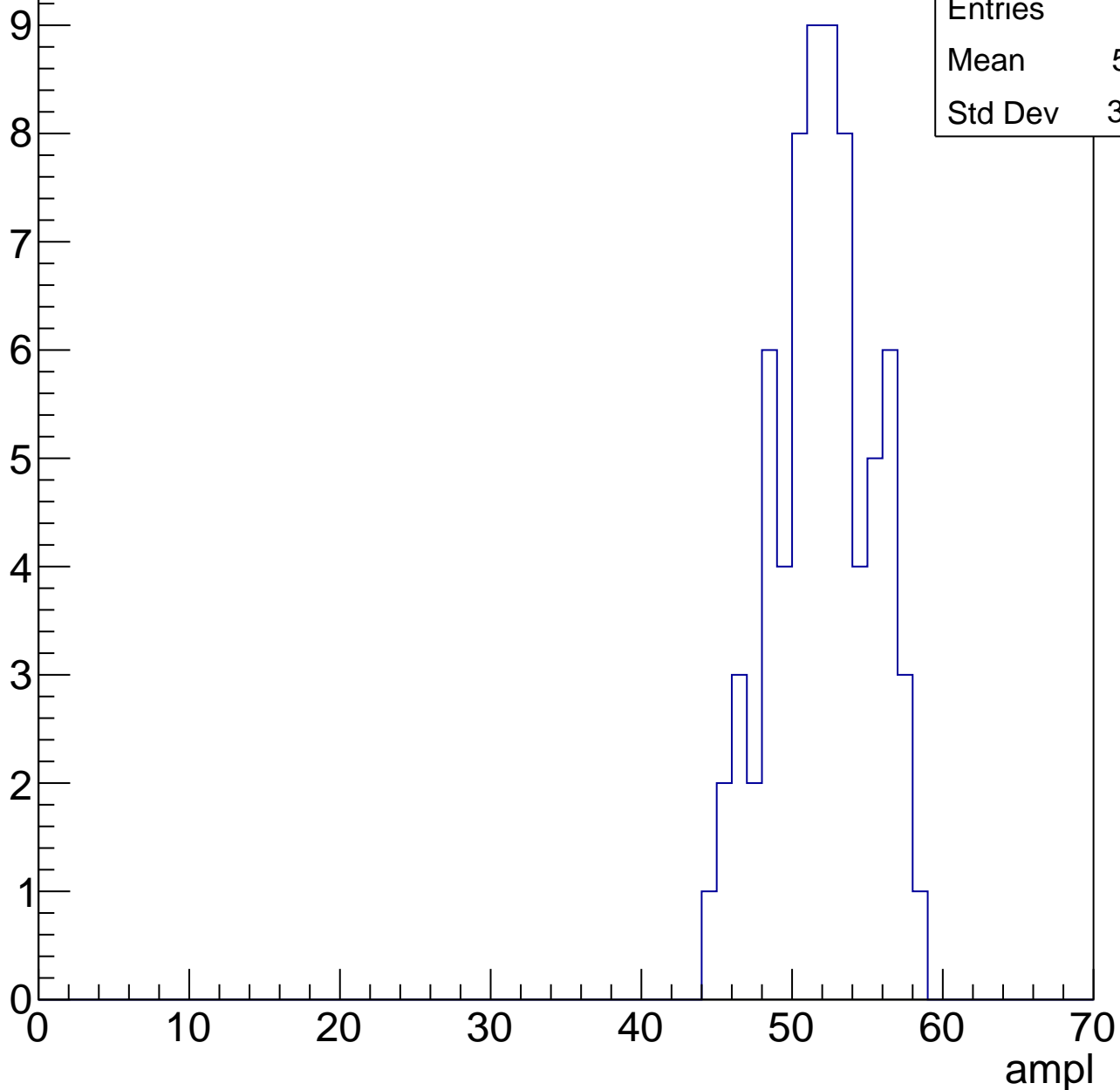
Entry



B0L002S, U5-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

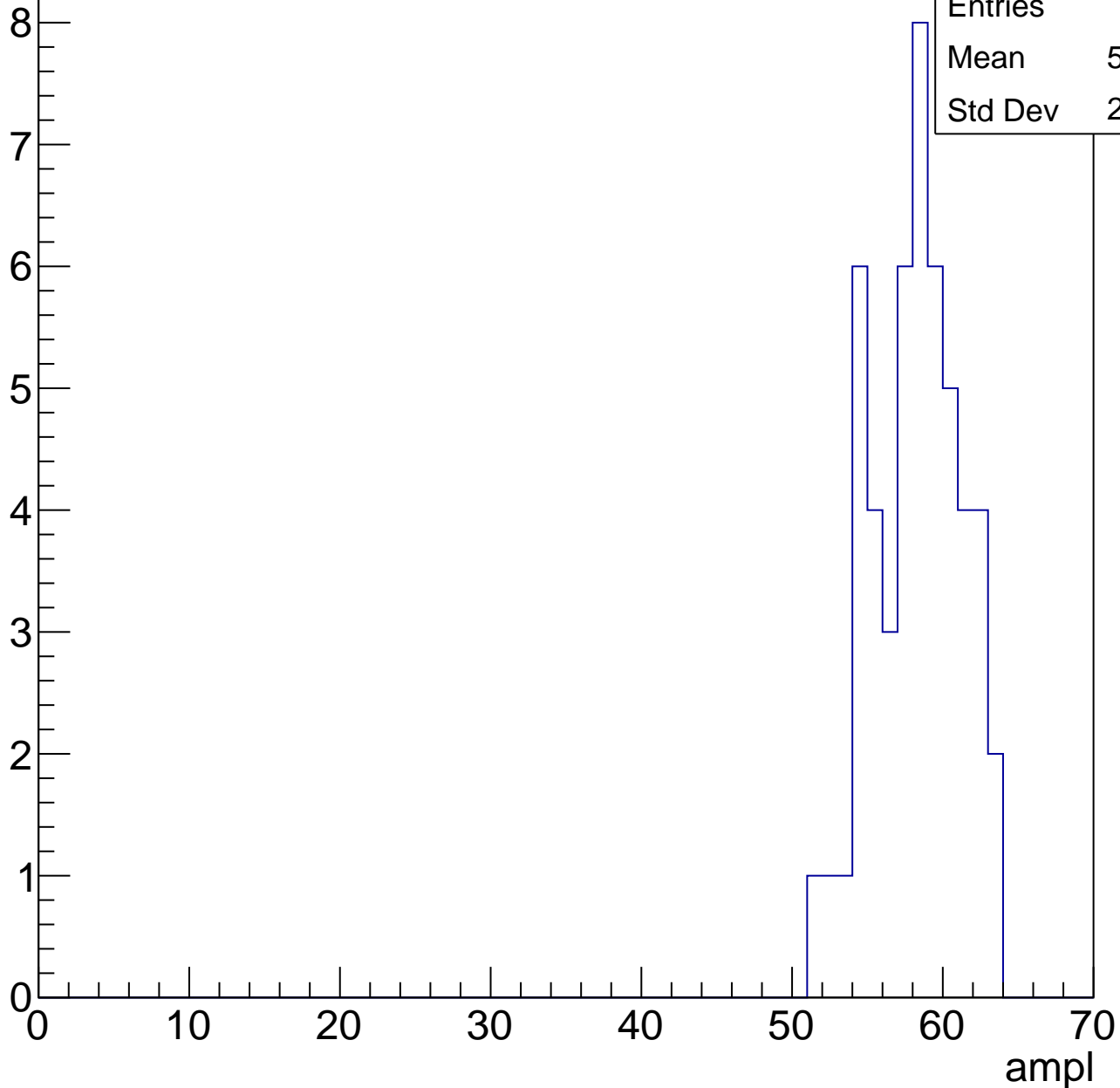


Entries	71
Mean	51.51
Std Dev	3.254

B0L002S, U5-ch8, adc4

calib_packv5_042523_0143.root, FC#8, port C1

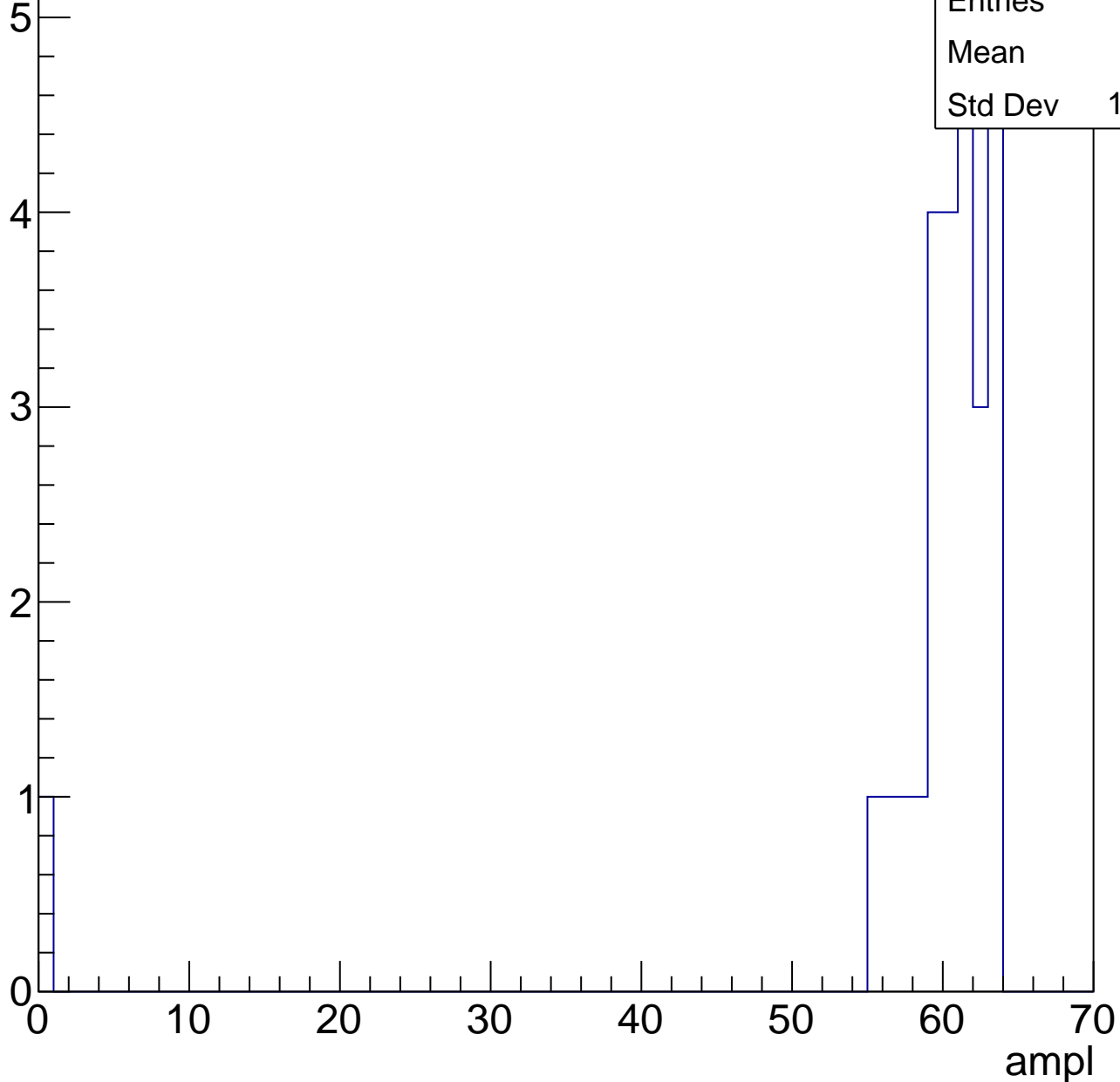
Entry



B0L002S, U5-ch8, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U5-ch9, adc0

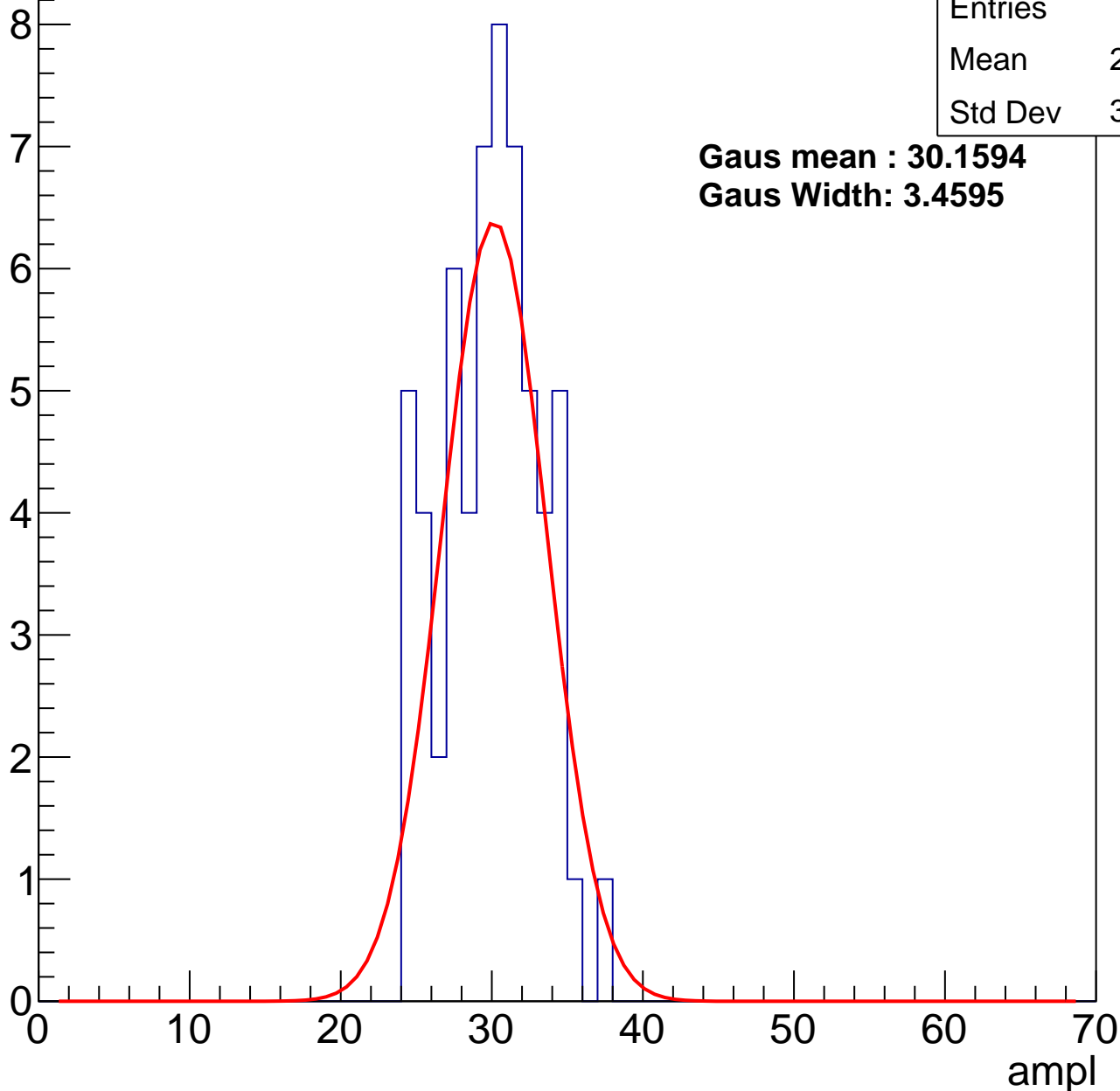
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.49
Std Dev	3.164

Gaus mean : 30.1594

Gaus Width: 3.4595

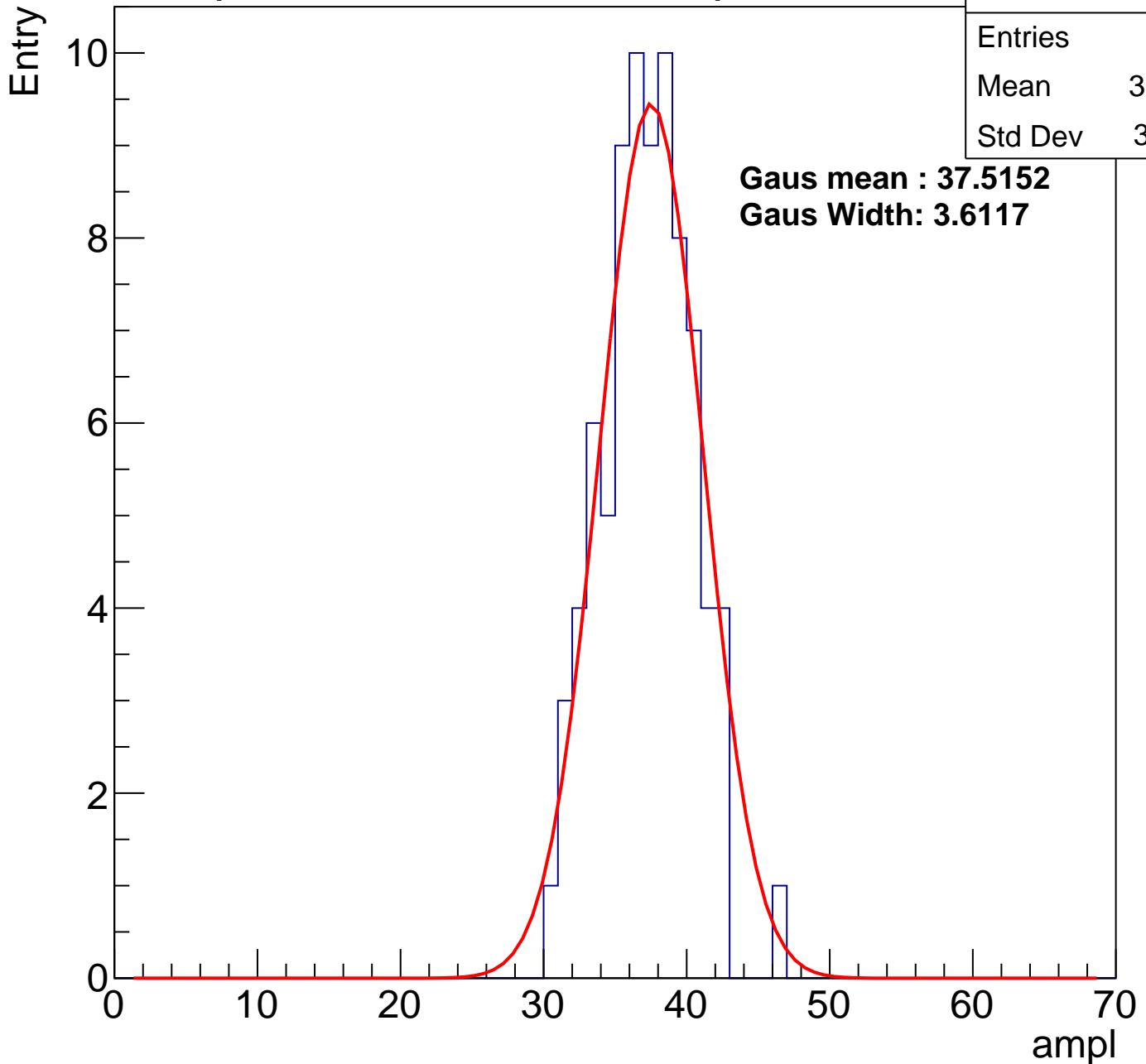


B0L002S, U5-ch9, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	36.75
Std Dev	3.121

Gaus mean : 37.5152
Gaus Width: 3.6117



B0L002S, U5-ch9, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	59
Mean	44.2
Std Dev	2.667

Gaus mean : 44.4288

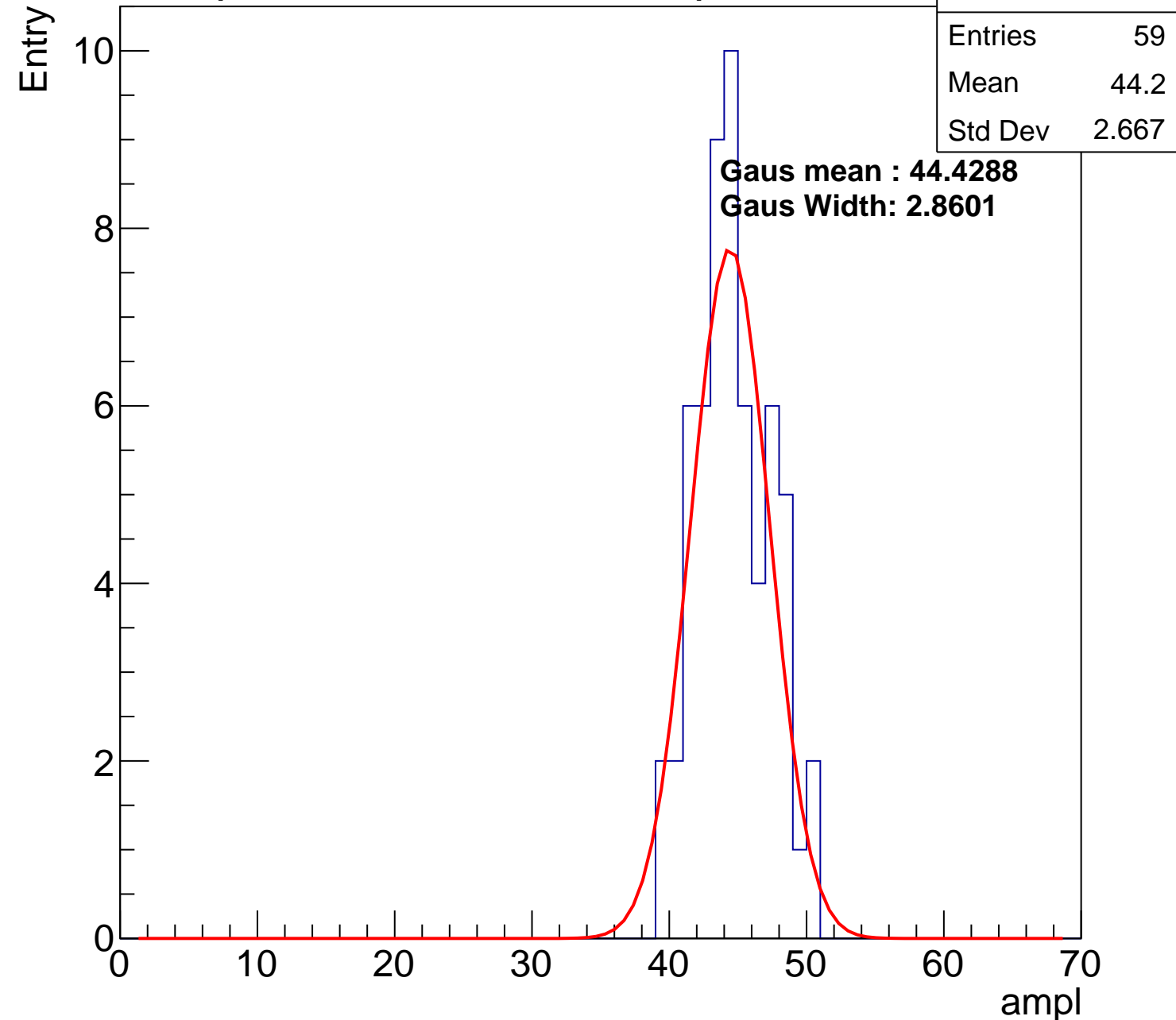
Gaus Width: 2.8601

Entry

10
8
6
4
2
0

ampl

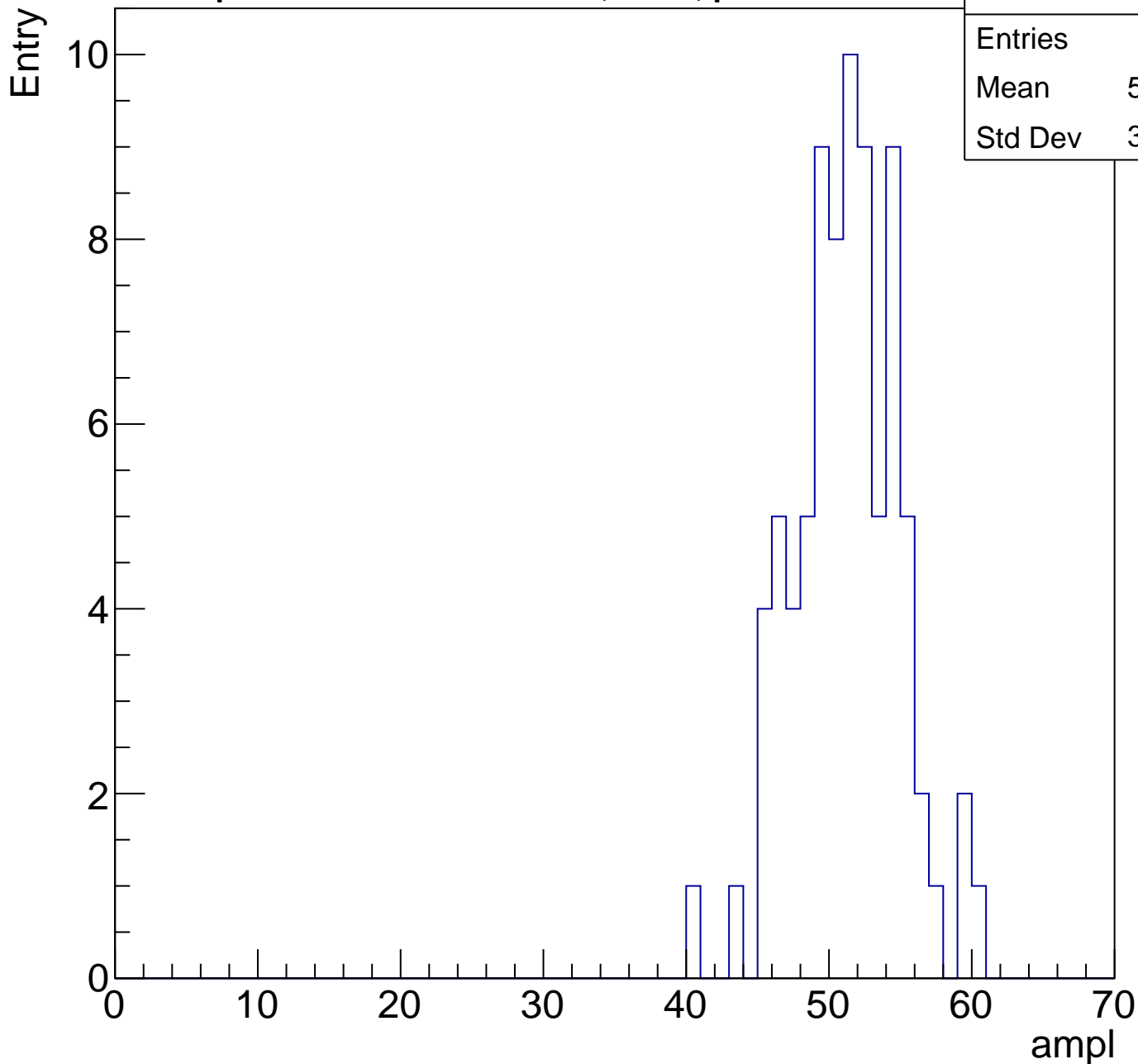
0 10 20 30 40 50 60 70



B0L002S, U5-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

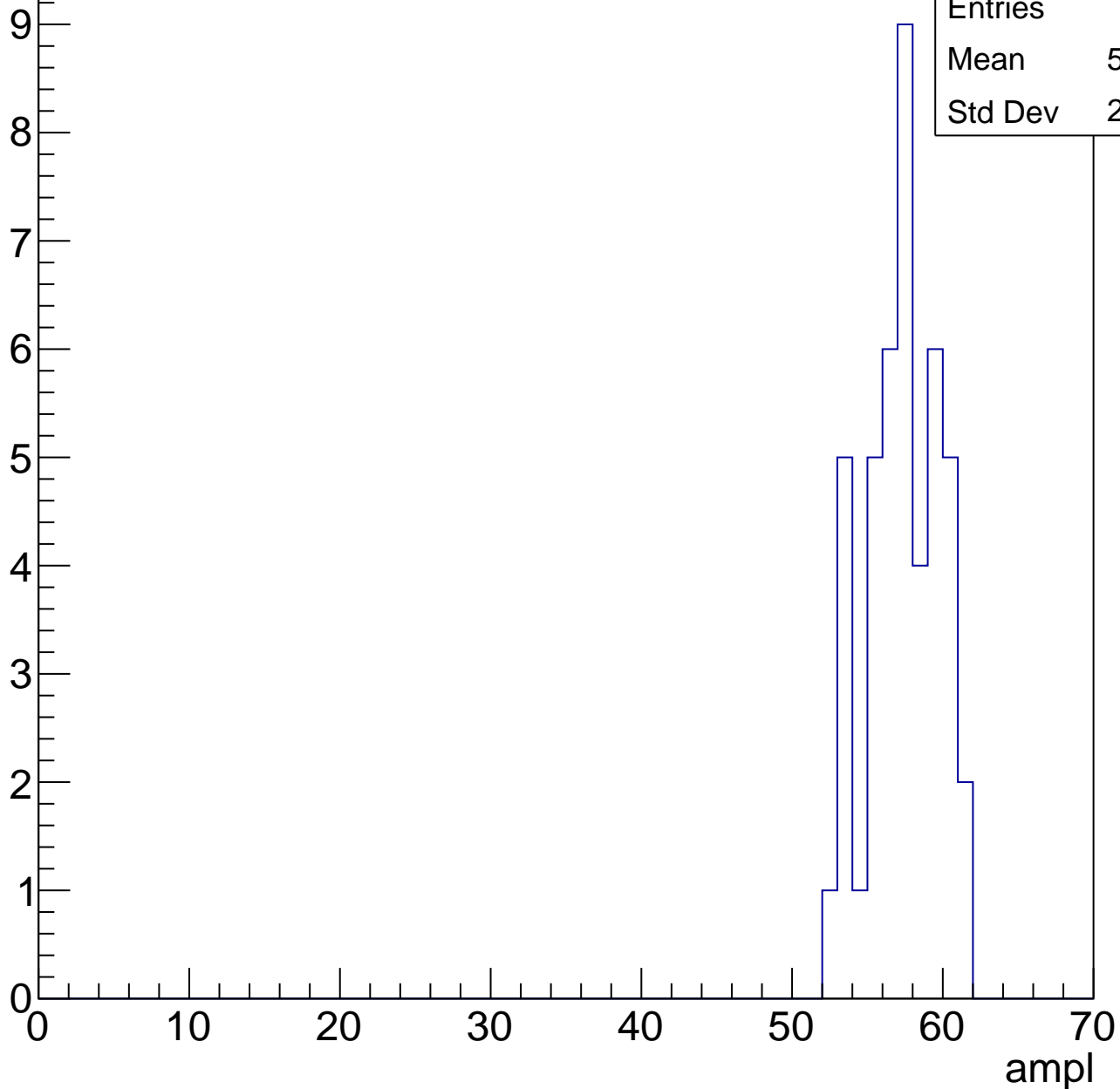
Entries	81
Mean	50.78
Std Dev	3.665



B0L002S, U5-ch9, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch9, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	43
Mean	59.44
Std Dev	9.379

Entry

10

8

6

4

2

0

0

10

20

30

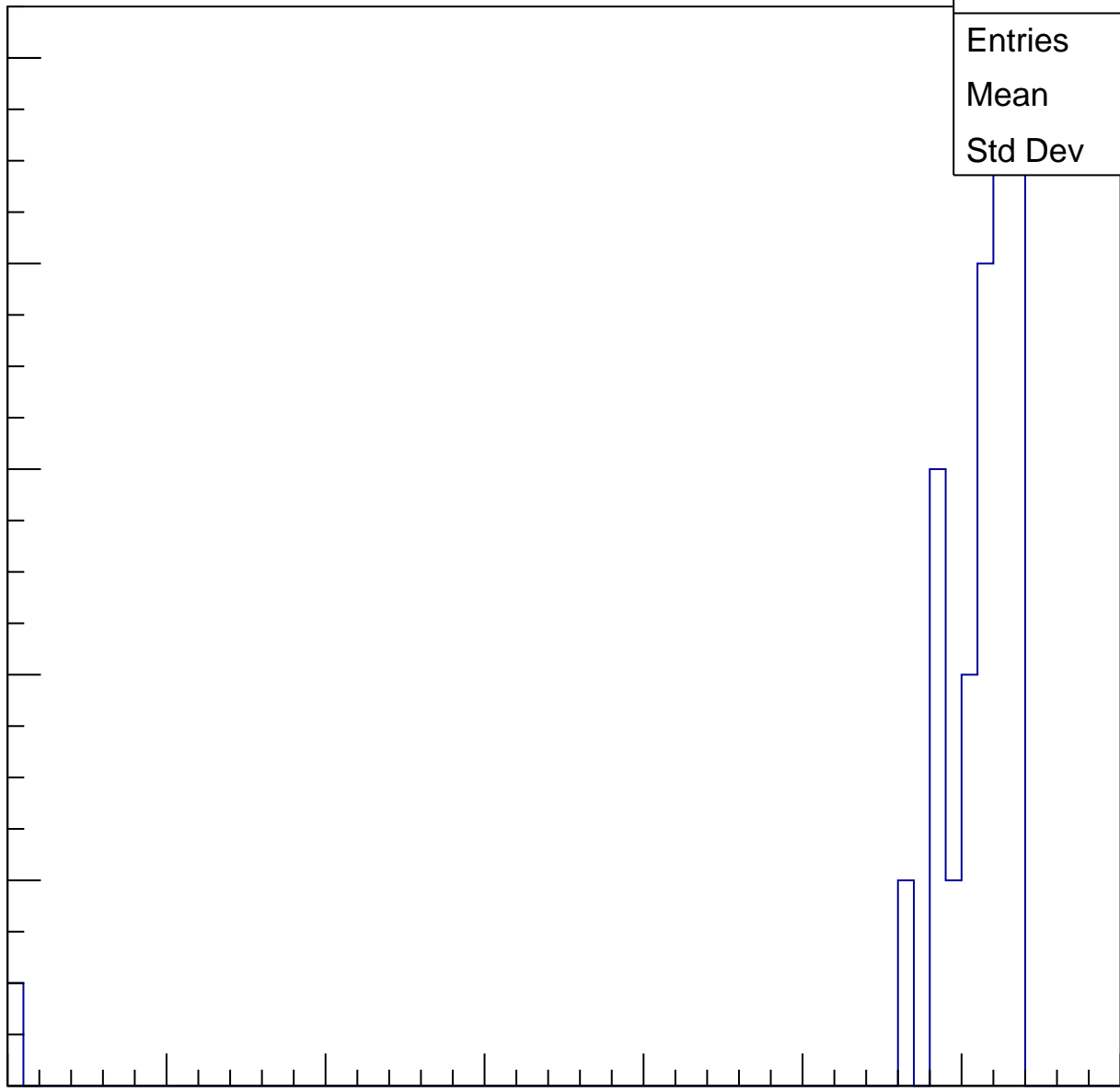
40

50

60

70

ampl



B0L002S, U5-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B0L002S, U5-ch10, adc0

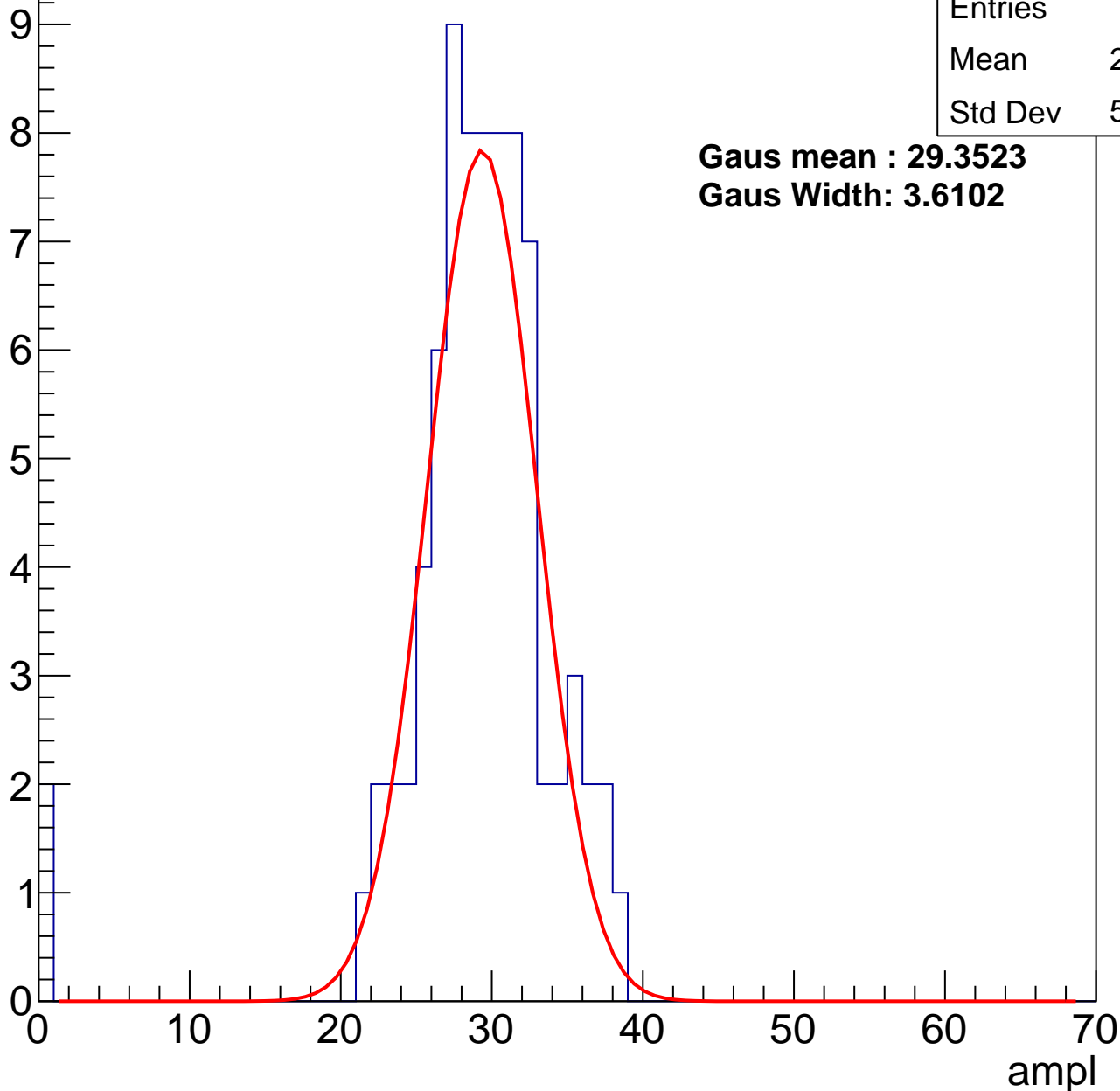
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	28.47
Std Dev	5.852

Gaus mean : 29.3523

Gaus Width: 3.6102



B0L002S, U5-ch10, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	36.96
Std Dev	3.513

Gaus mean : 37.3496

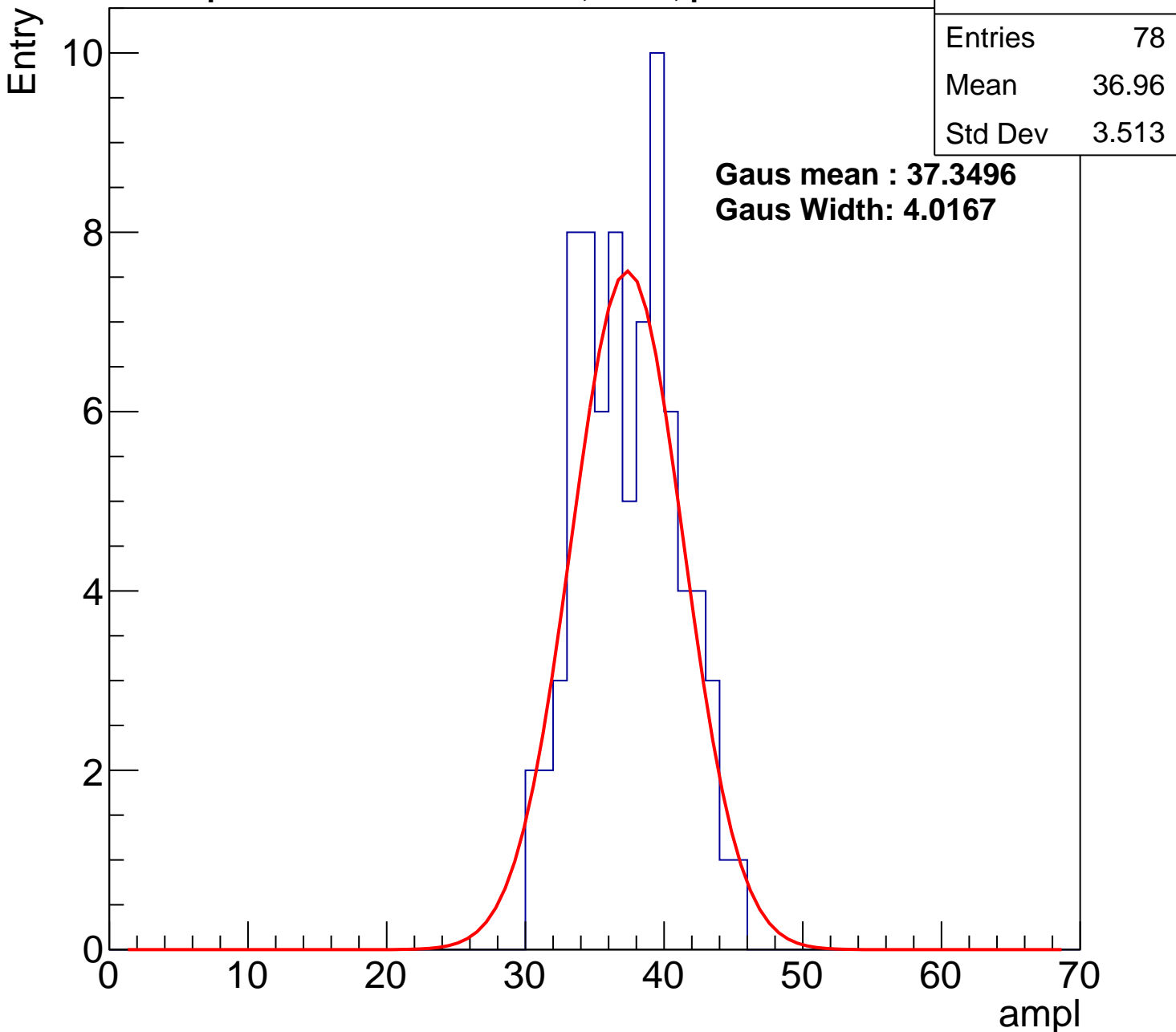
Gaus Width: 4.0167

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch10, adc2

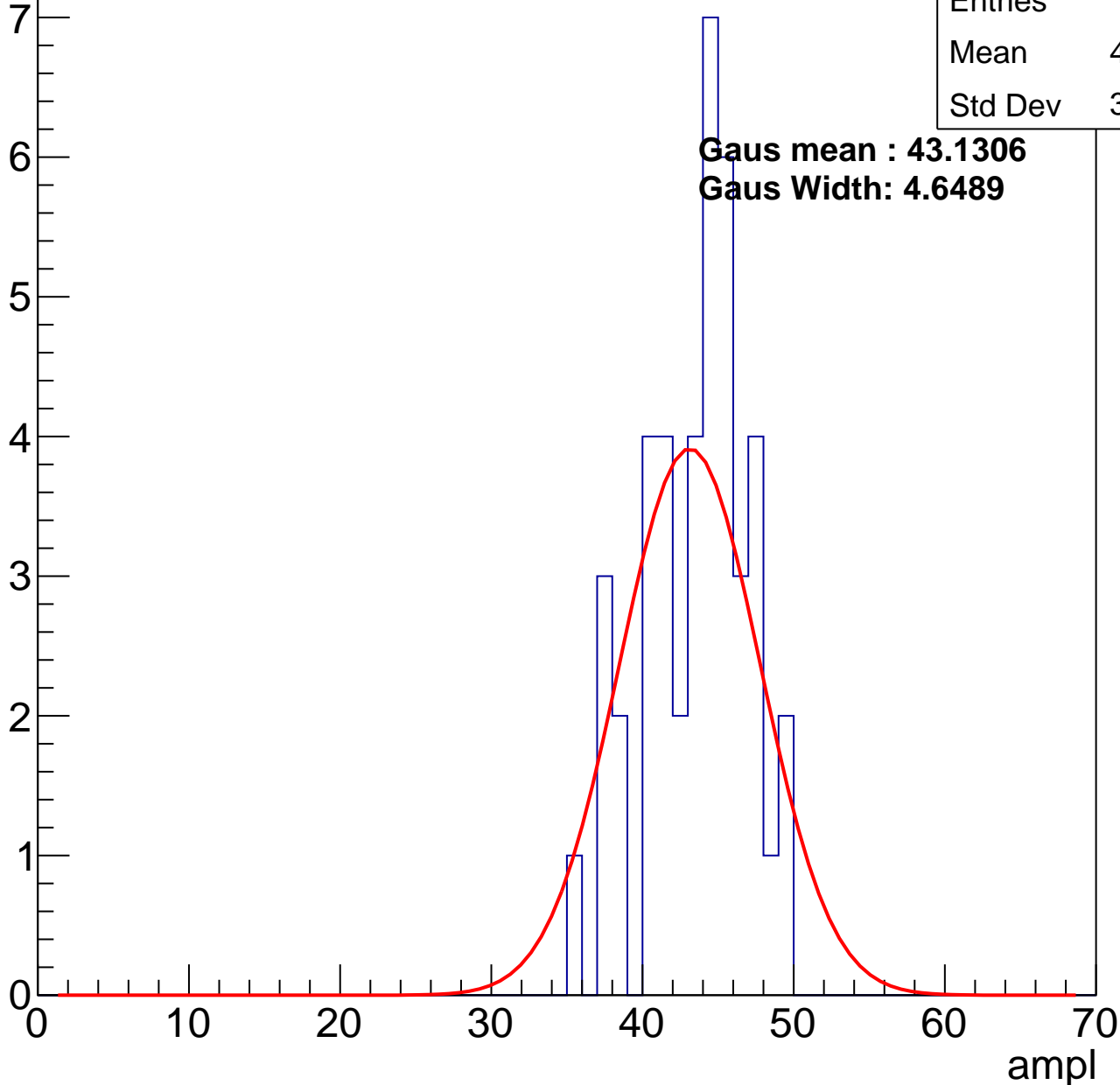
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	43.07
Std Dev	3.392

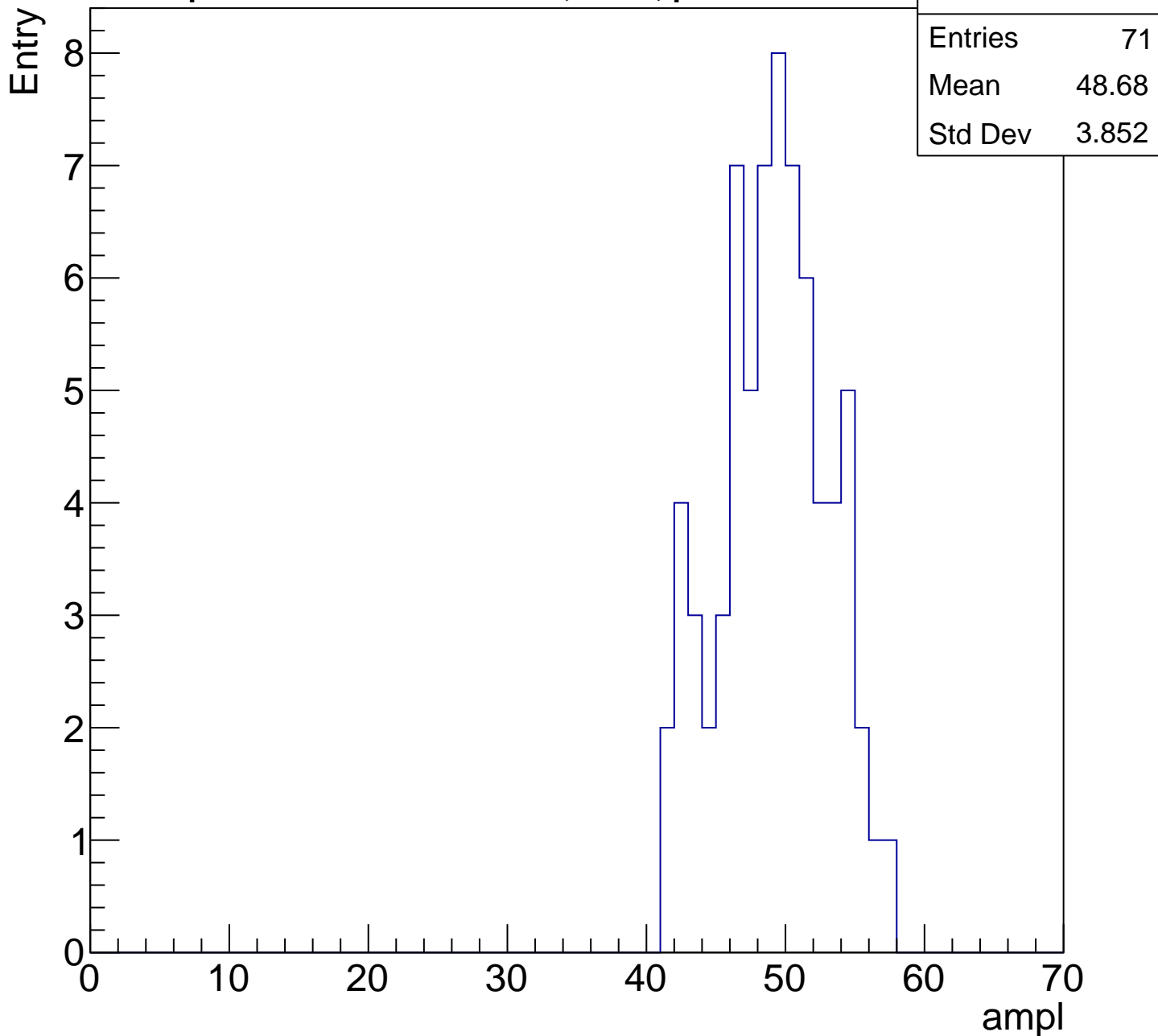
Gaus mean : 43.1306

Gaus Width: 4.6489



B0L002S, U5-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

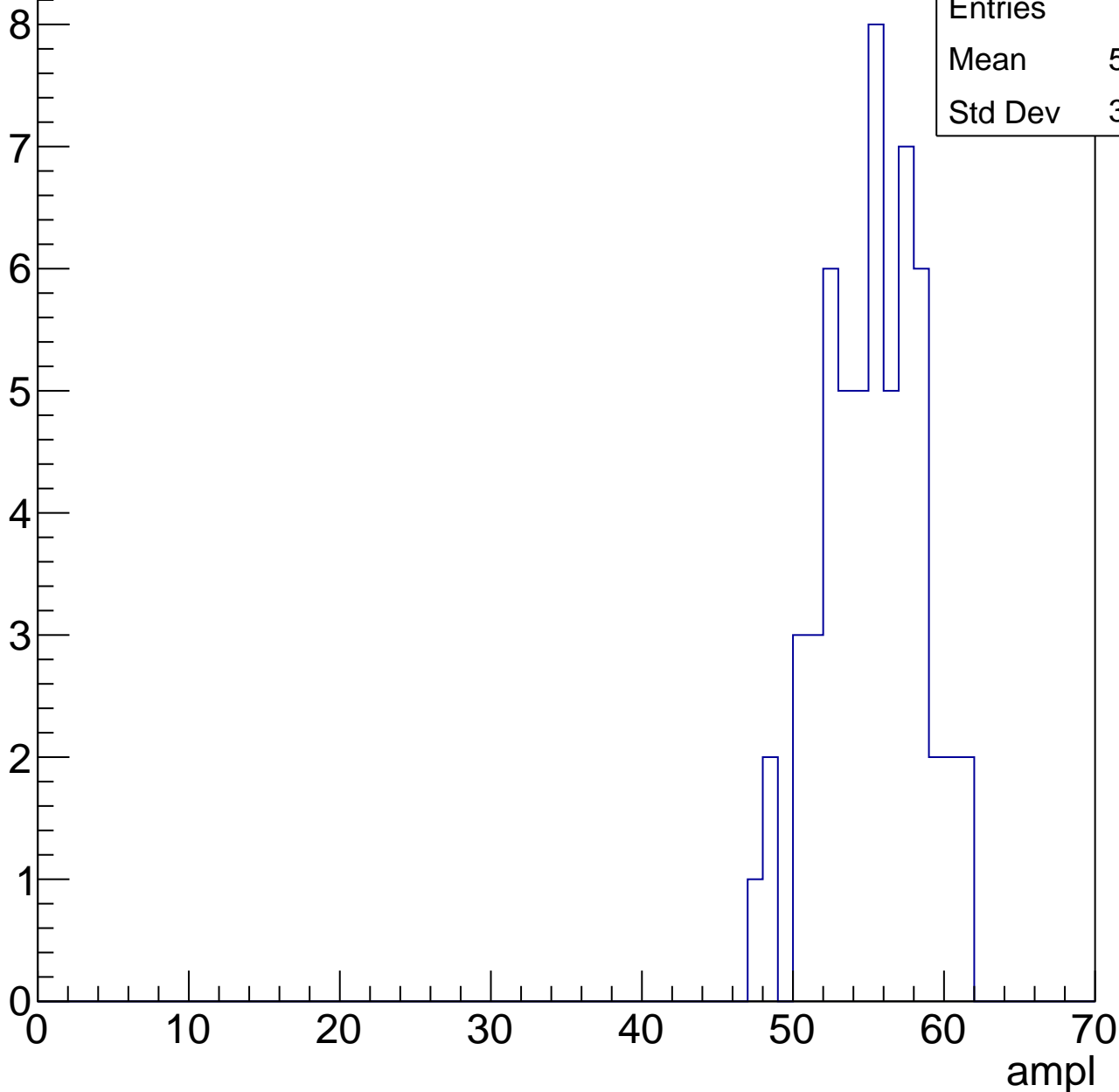


B0L002S, U5-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	54.74
Std Dev	3.247

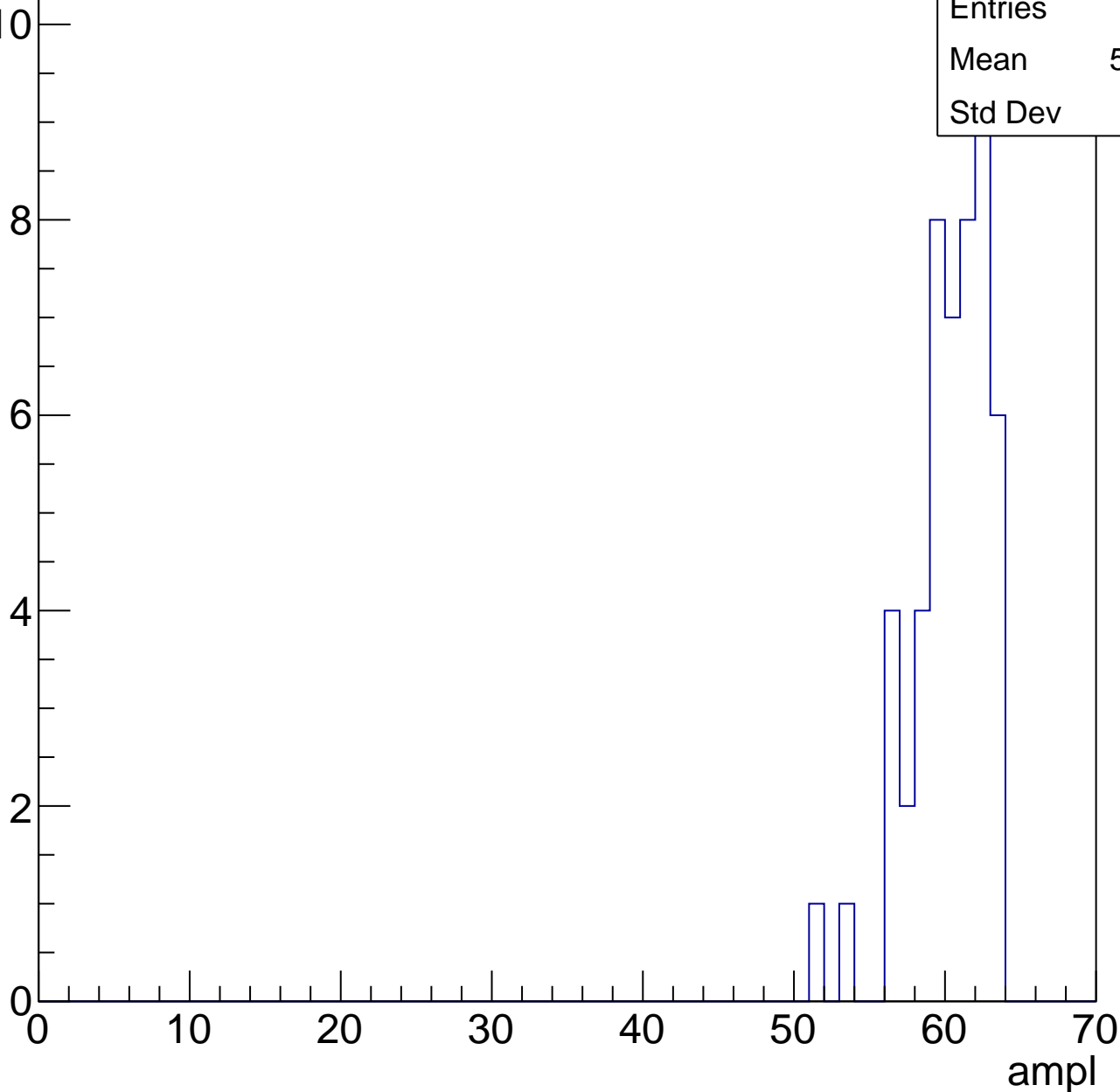


B0L002S, U5-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

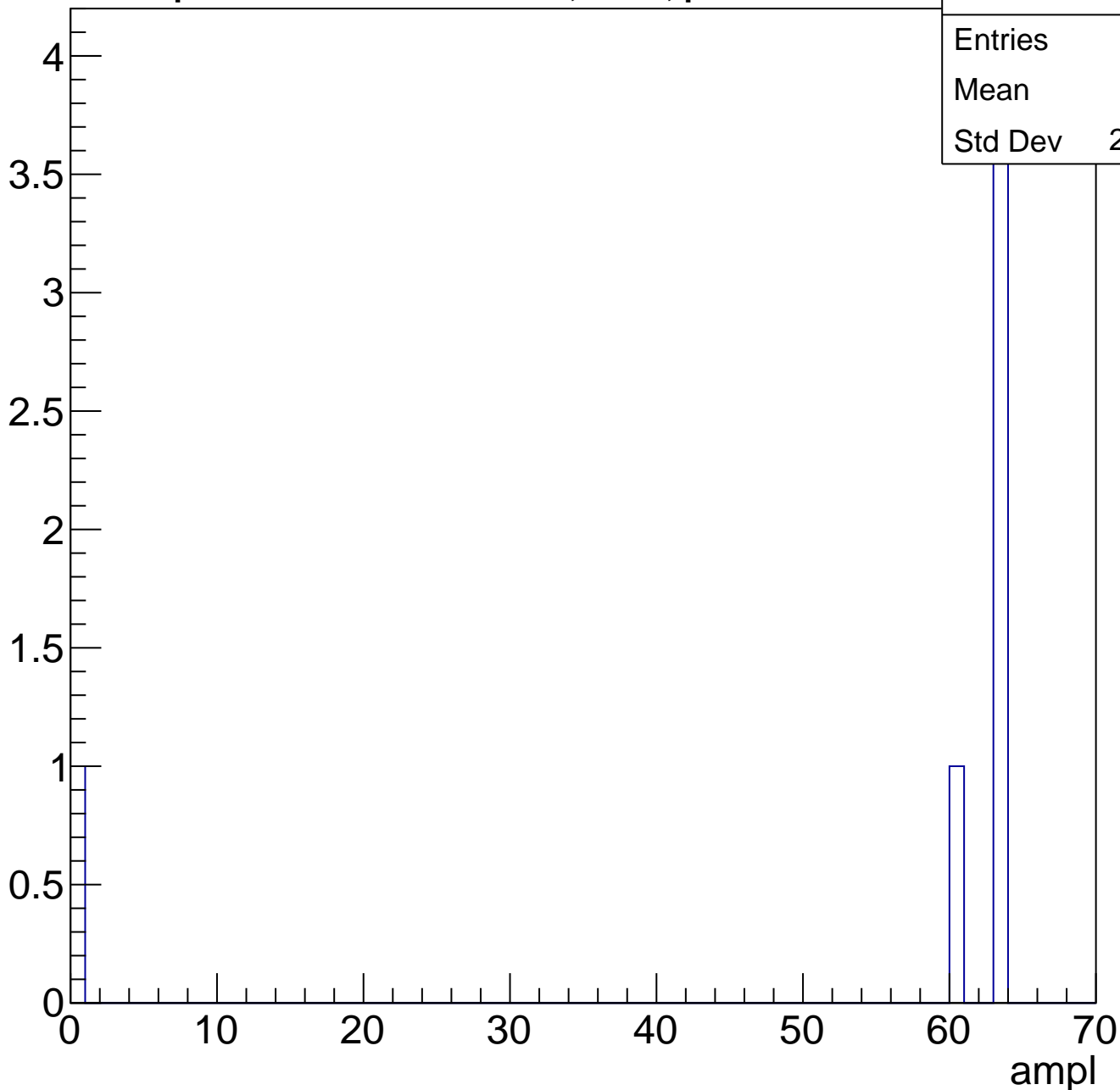
Entries	51
Mean	59.84
Std Dev	2.57



B0L002S, U5-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	6
Mean	52
Std Dev	23.28

B0L002S, U5-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch11, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	29.09
Std Dev	4.83

Gaus mean : 30.4687

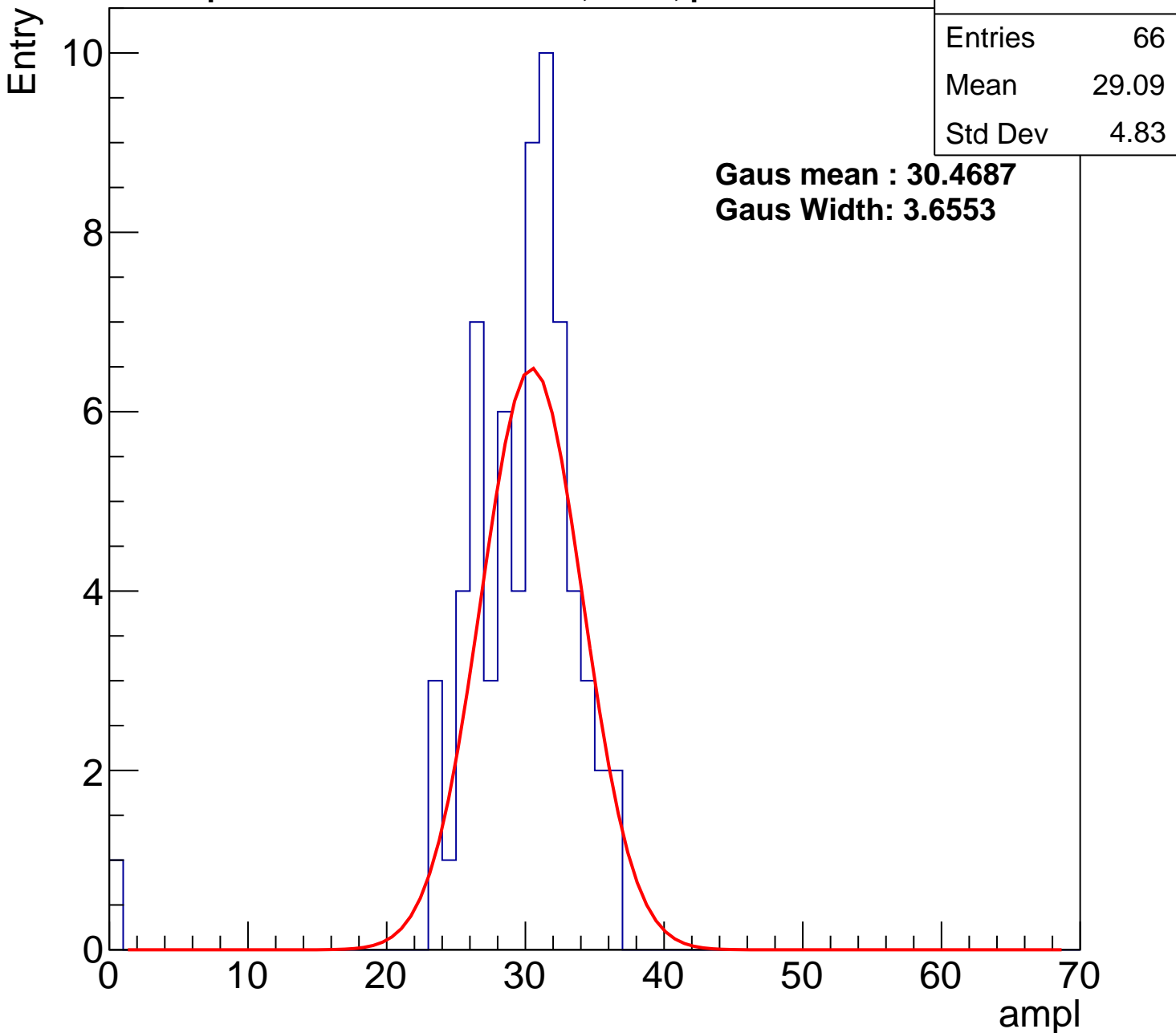
Gaus Width: 3.6553

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch11, adc1

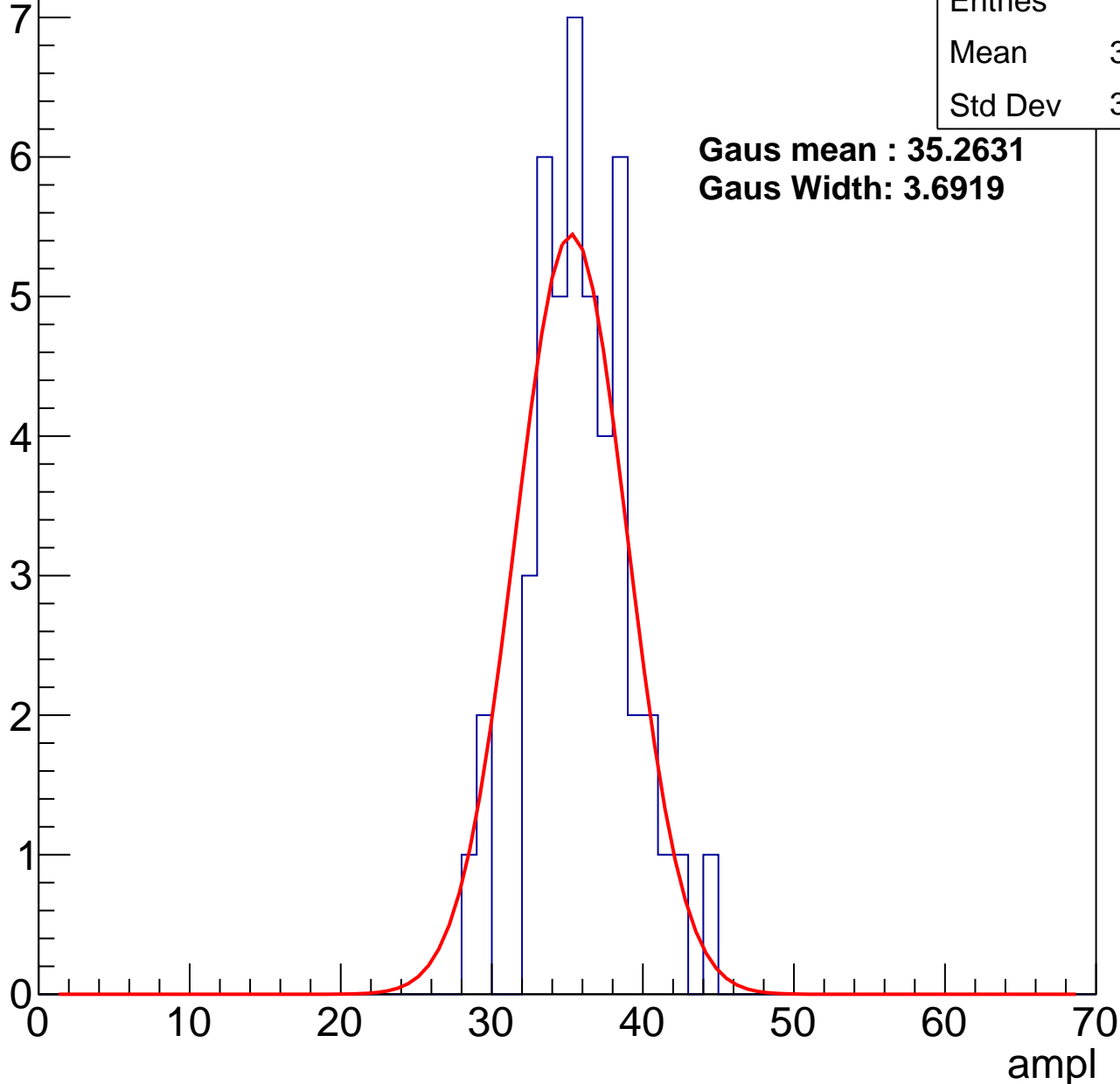
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	35.57
Std Dev	3.248

Gaus mean : 35.2631

Gaus Width: 3.6919



B0L002S, U5-ch11, adc2

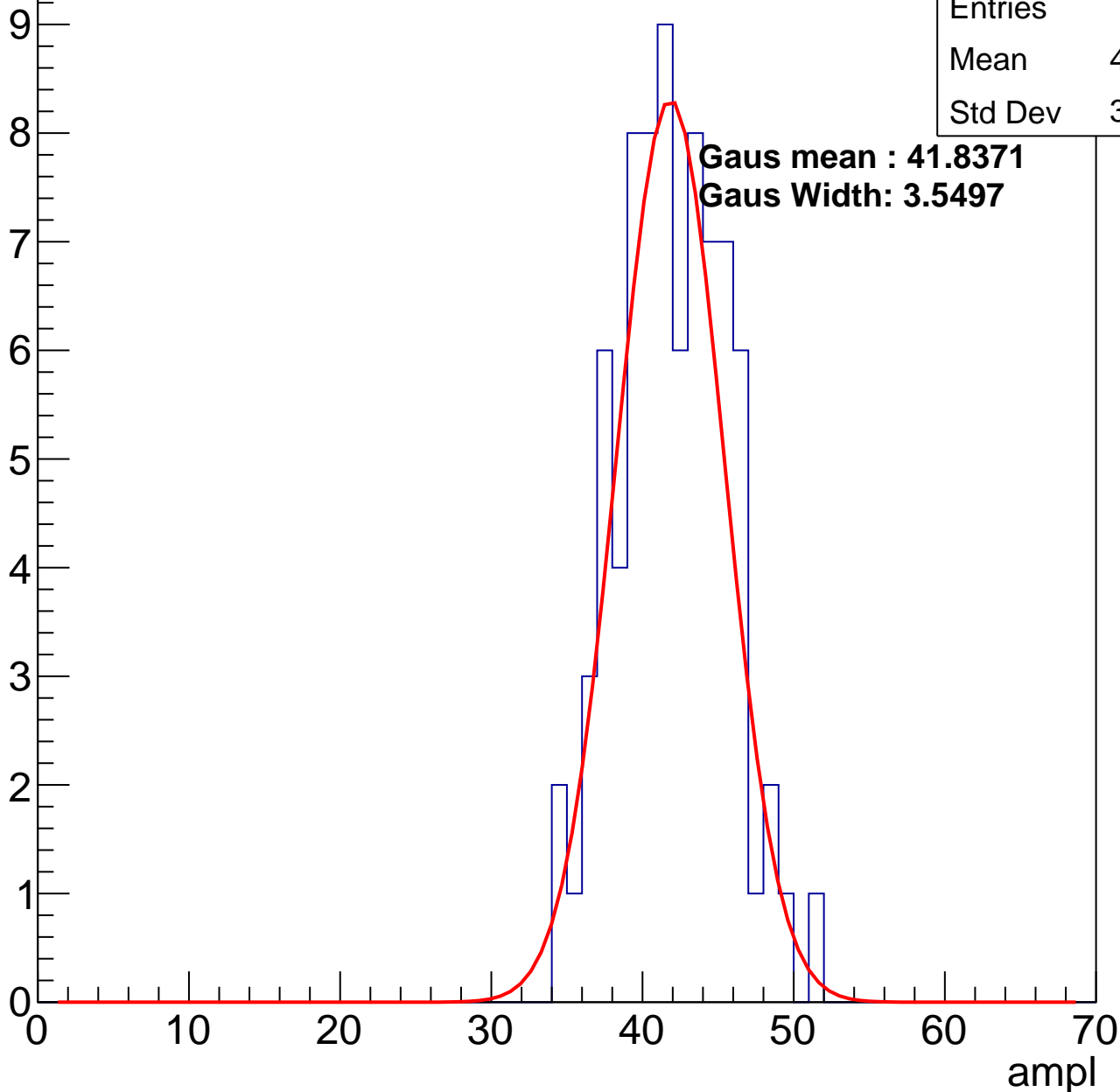
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	41.55
Std Dev	3.567

Gaus mean : 41.8371

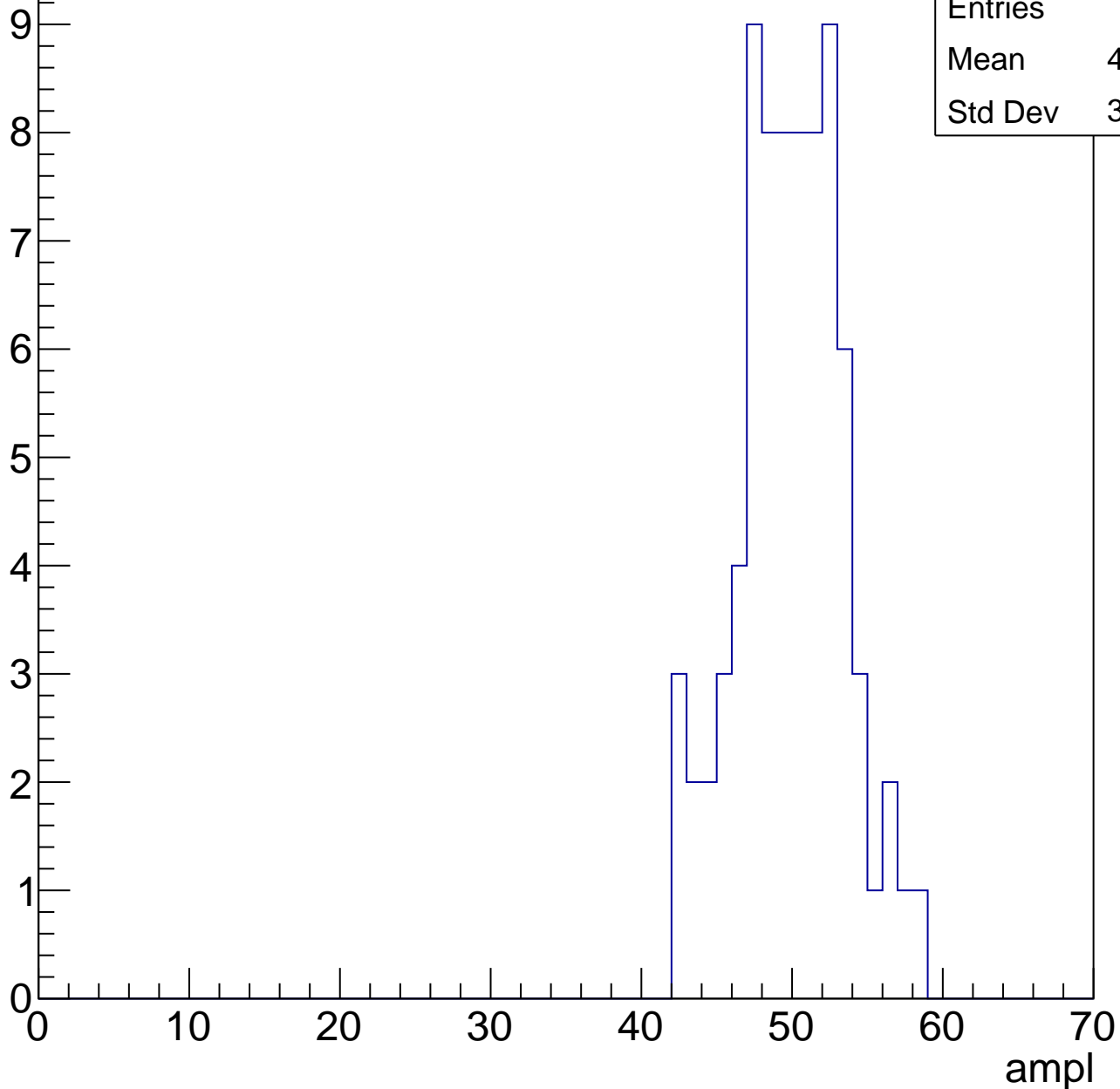
Gaus Width: 3.5497



B0L002S, U5-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



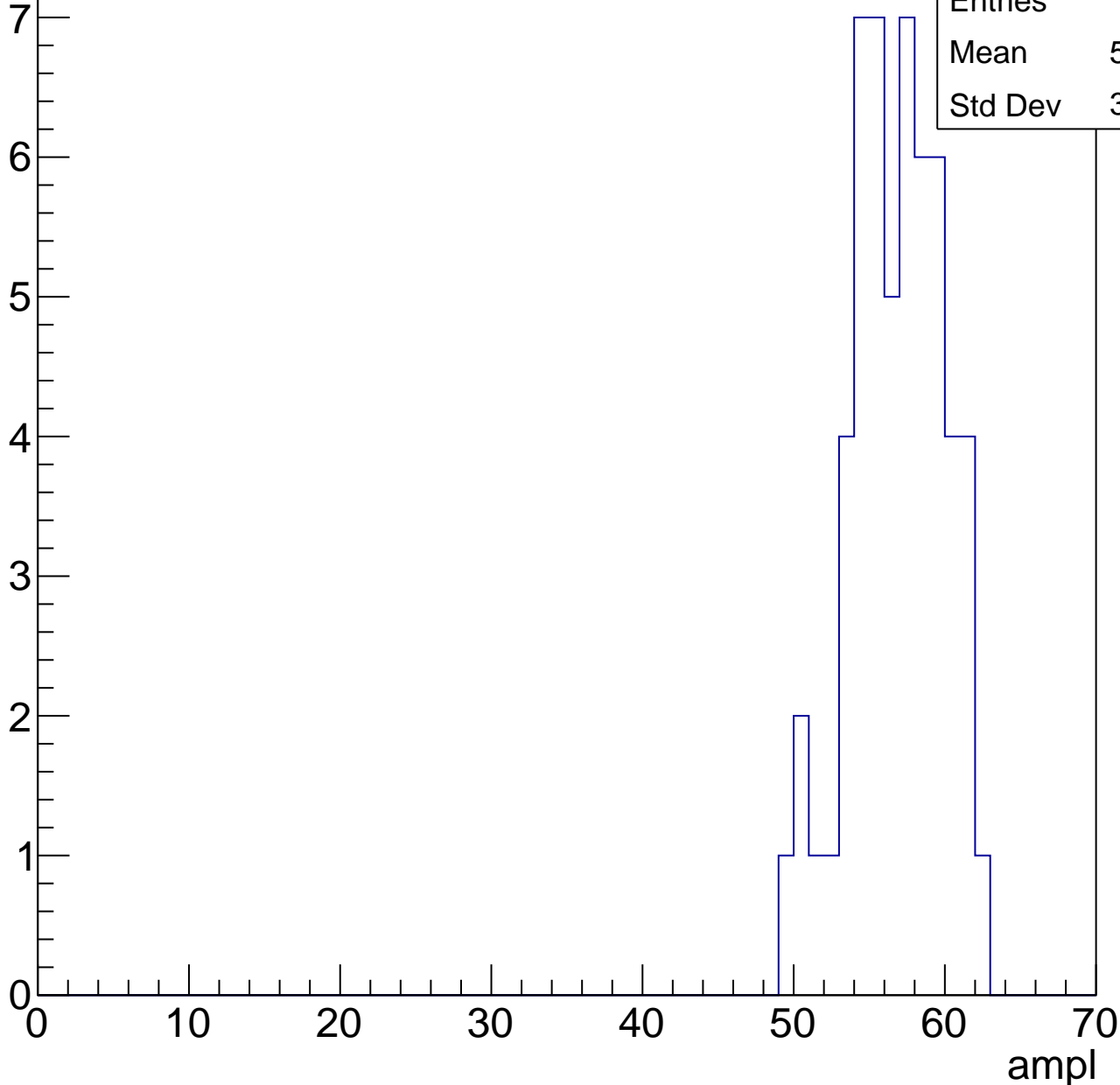
Entries	78
Mean	49.44
Std Dev	3.496

B0L002S, U5-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.32
Std Dev	3.024

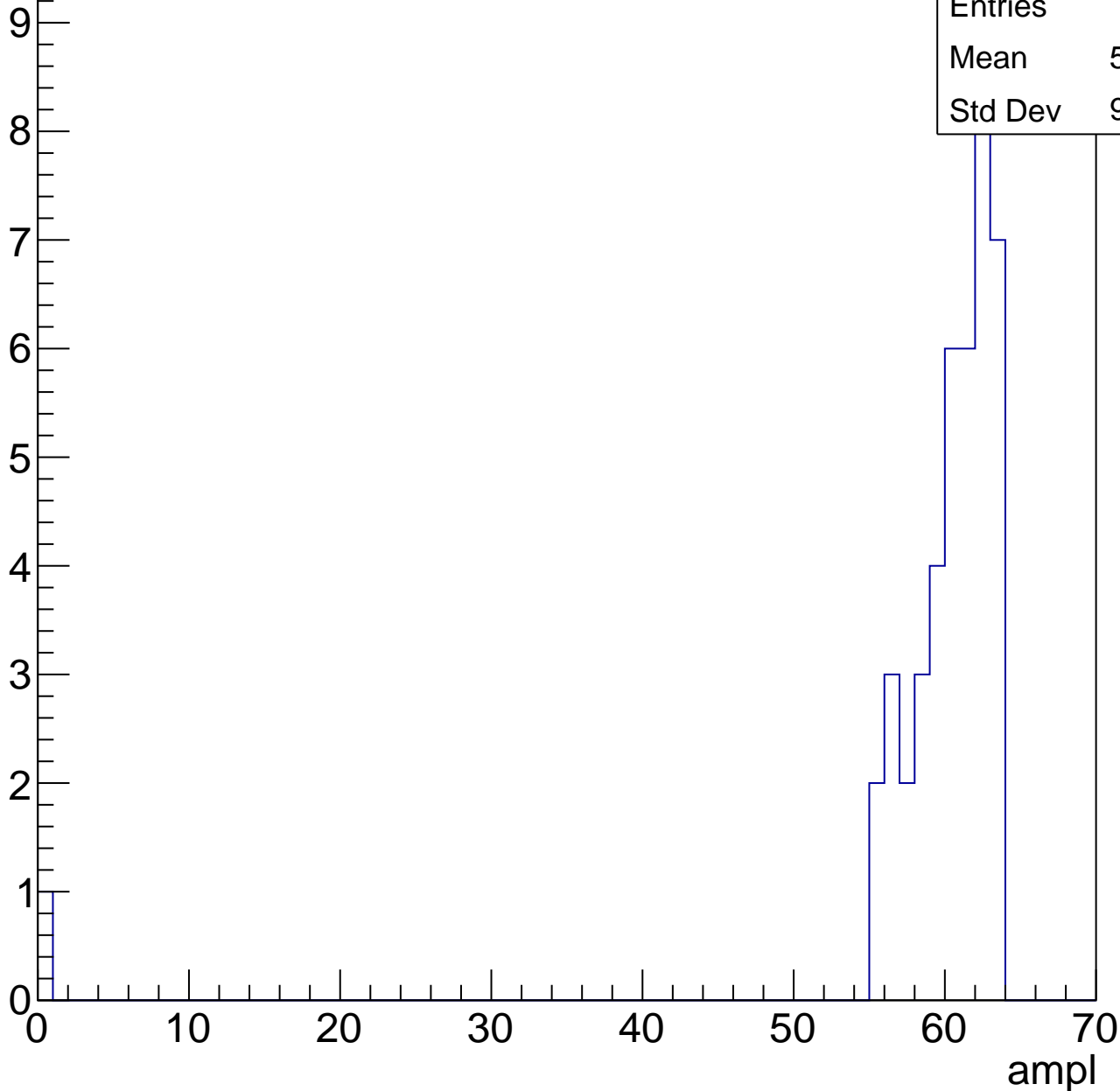


B0L002S, U5-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	58.77
Std Dev	9.366



B0L002S, U5-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L002S, U5-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch12, adc0

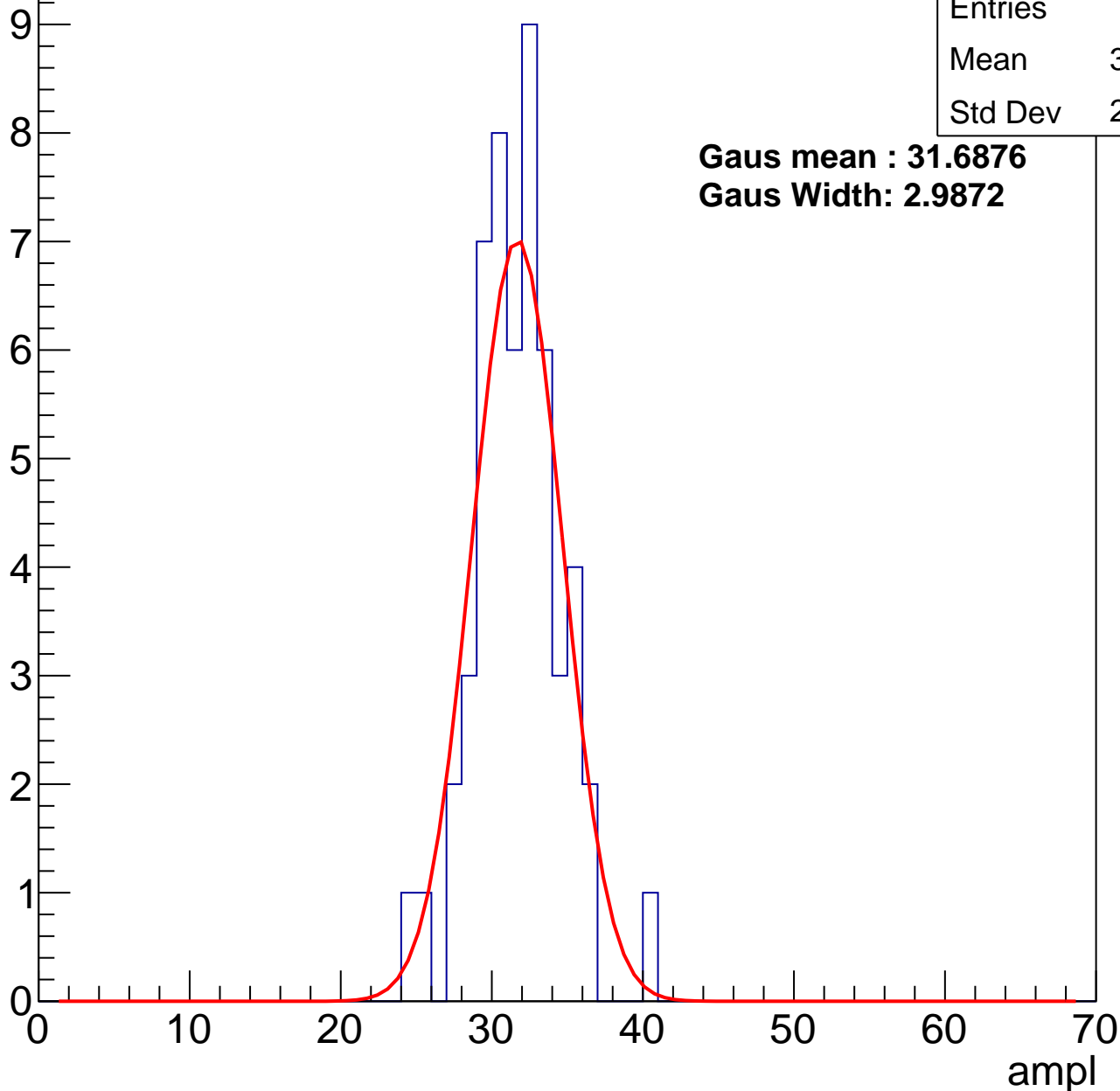
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	31.25
Std Dev	2.854

Gaus mean : 31.6876

Gaus Width: 2.9872



B0L002S, U5-ch12, adc1

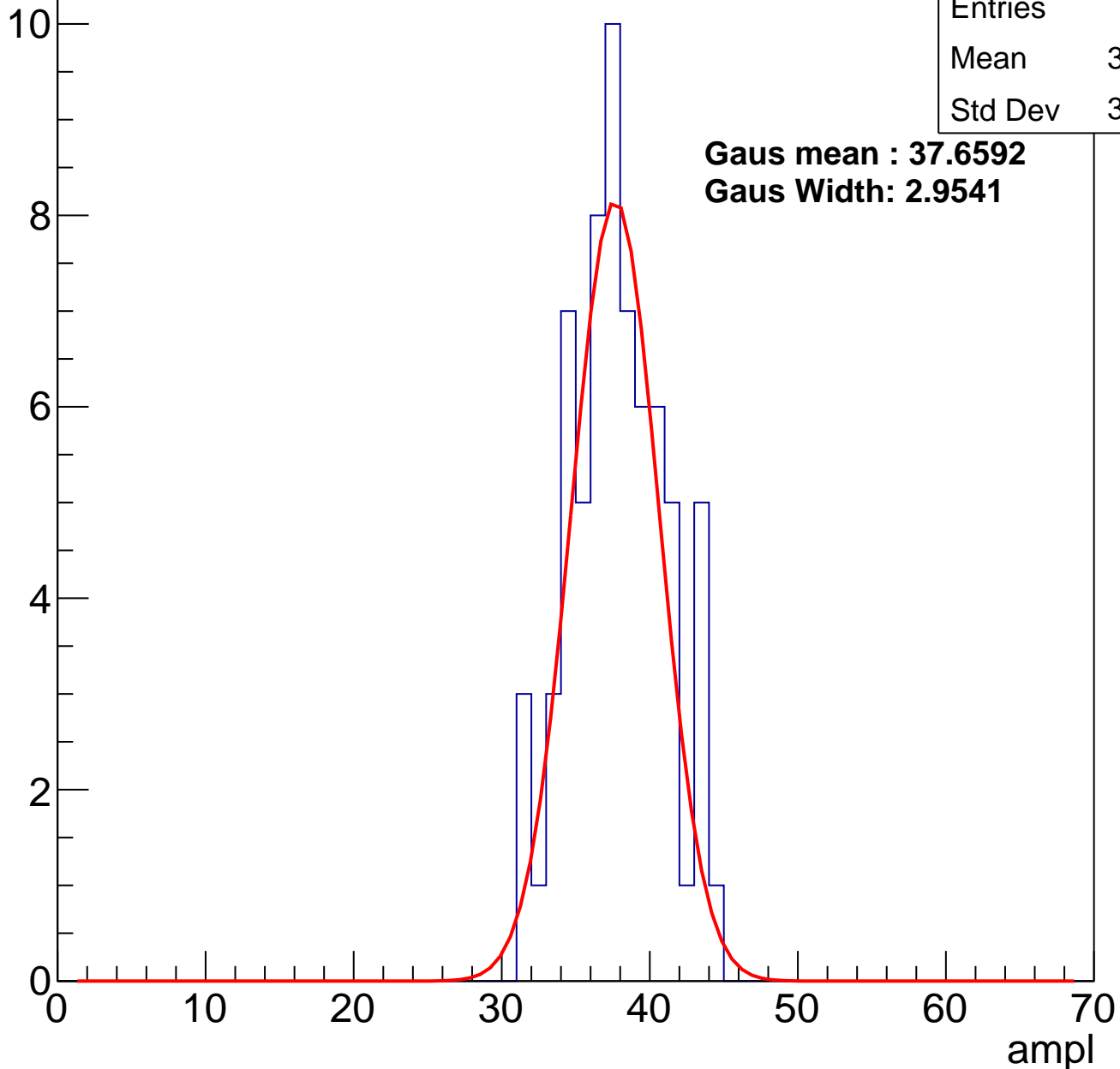
calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	37.37
Std Dev	3.166

Gaus mean : 37.6592

Gaus Width: 2.9541

Entry



B0L002S, U5-ch12, adc2

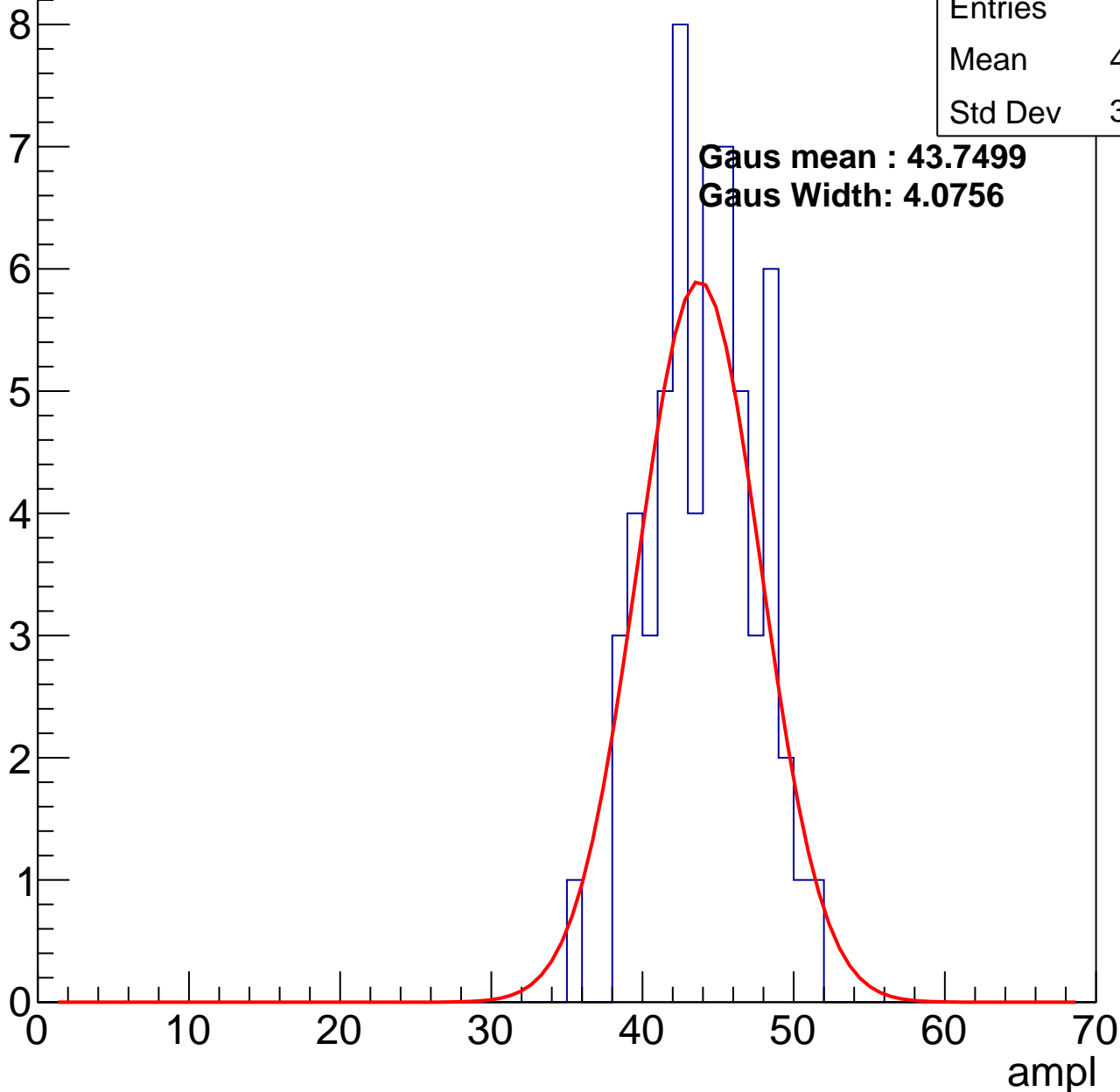
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.65
Std Dev	3.405

Gaus mean : 43.7499

Gaus Width: 4.0756

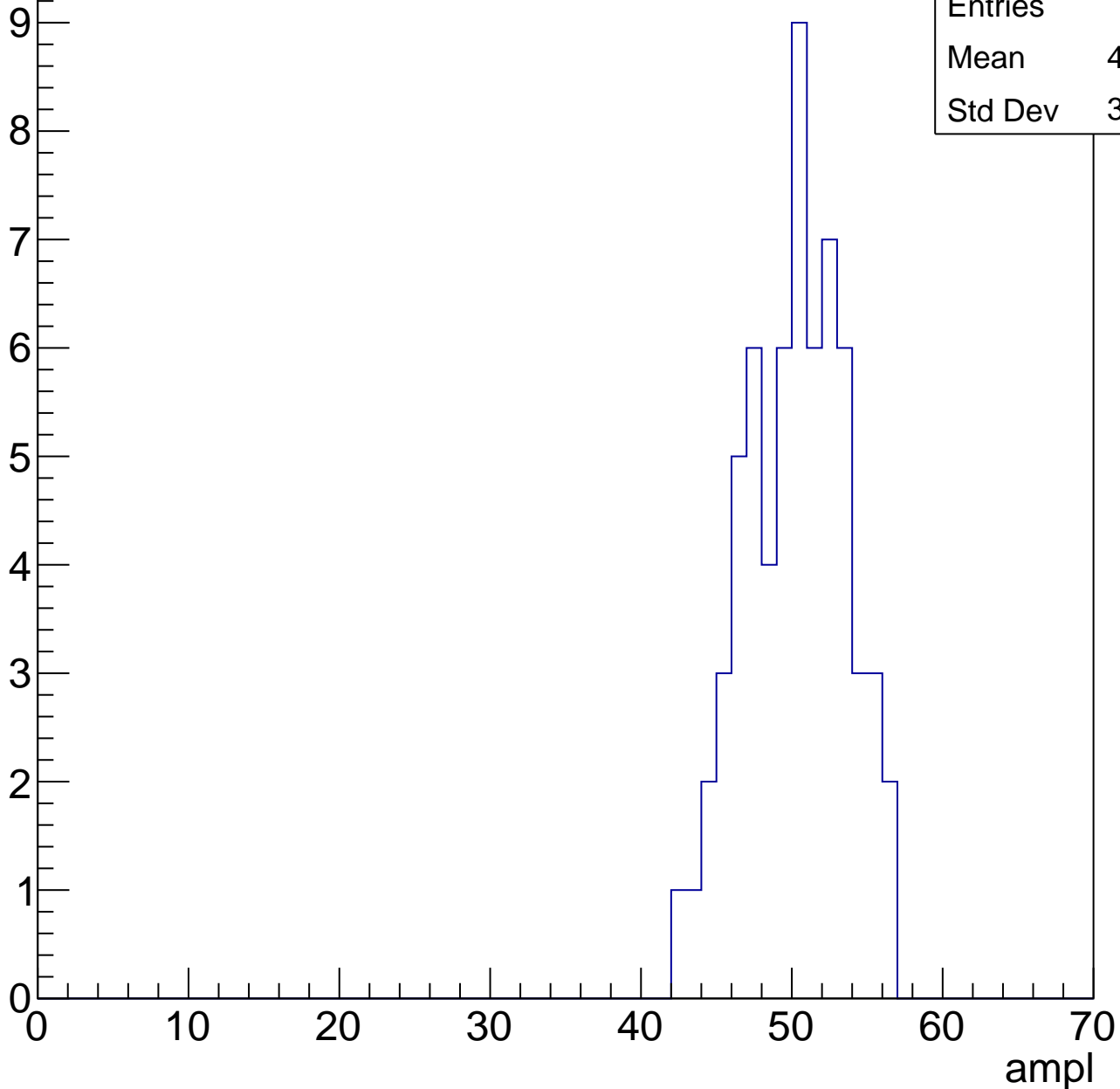


B0L002S, U5-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	49.73
Std Dev	3.294

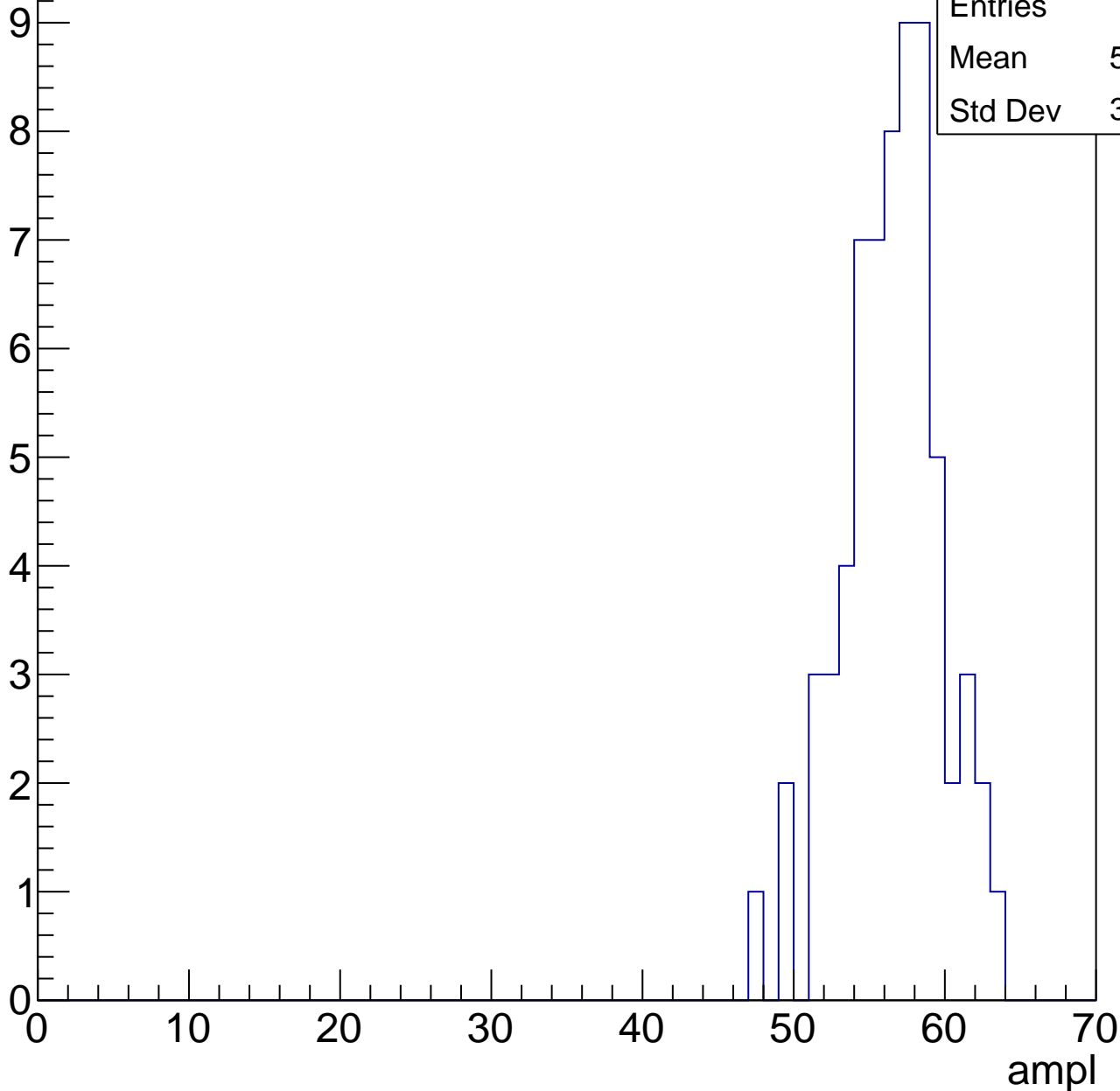


B0L002S, U5-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	56.02
Std Dev	3.236



B0L002S, U5-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

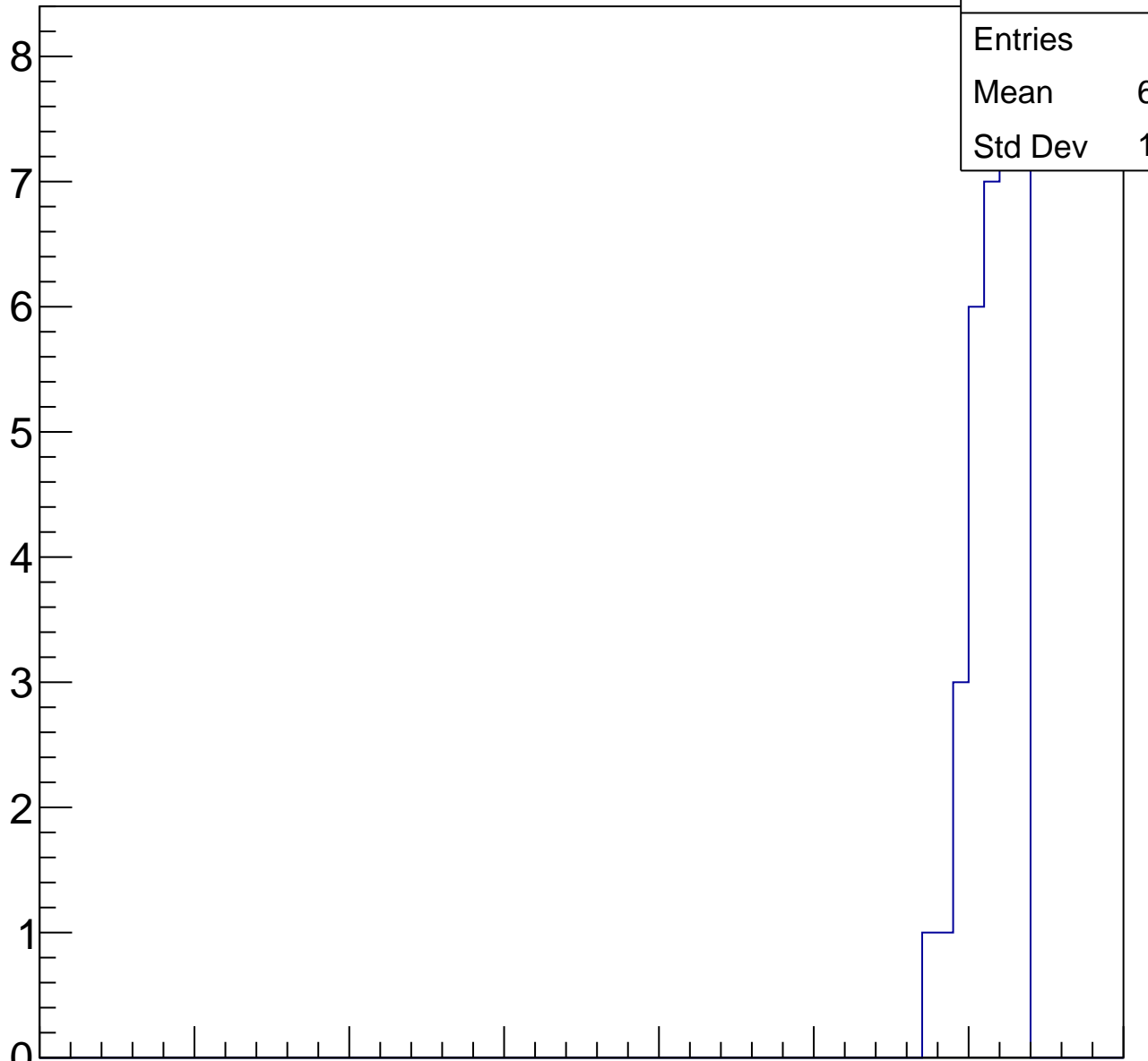
Entry

8
7
6
5
4
3
2
1
0

Entries	34
Mean	61.15
Std Dev	1.555

ampl

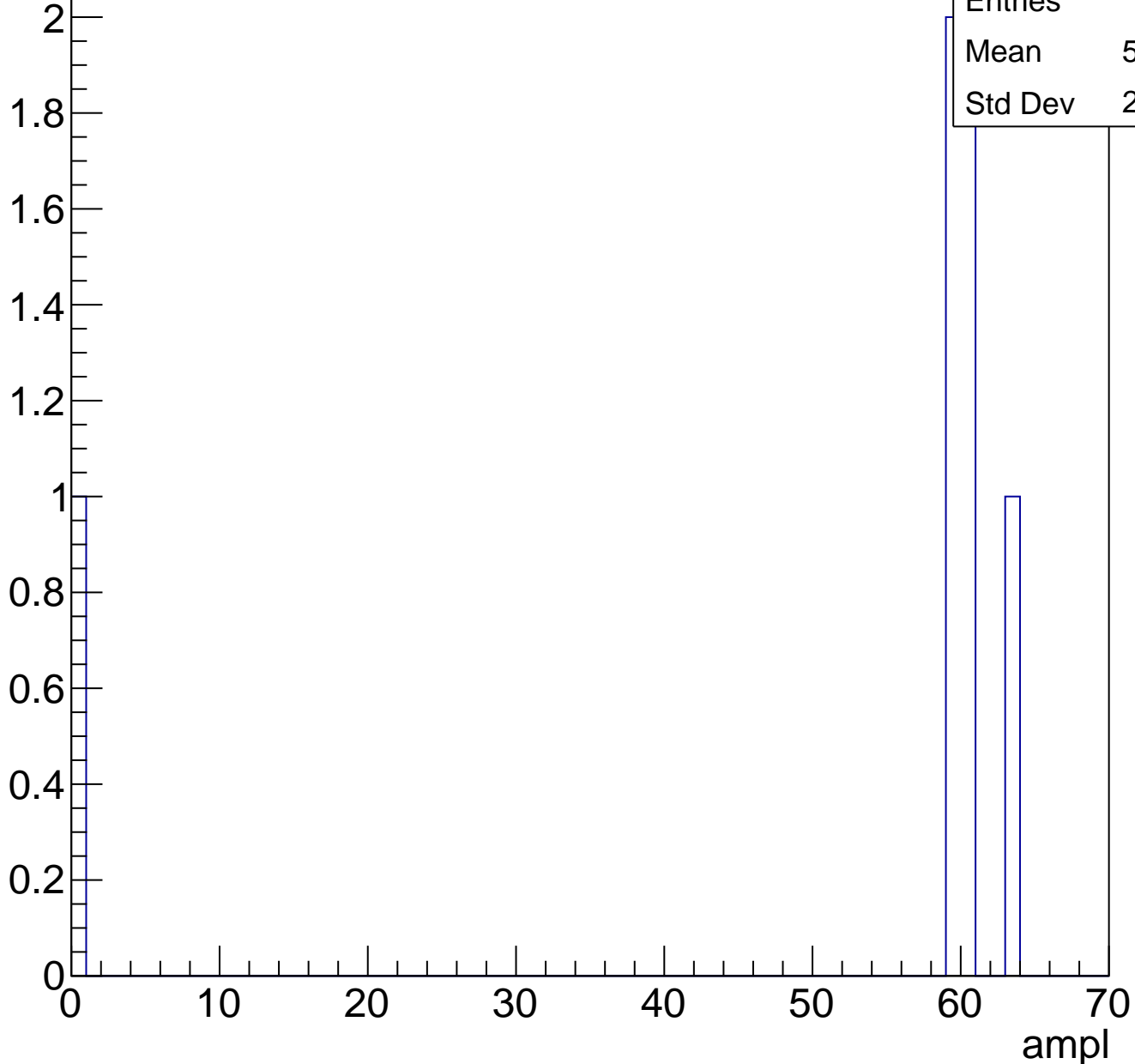
0 10 20 30 40 50 60 70



B0L002S, U5-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch13, adc0

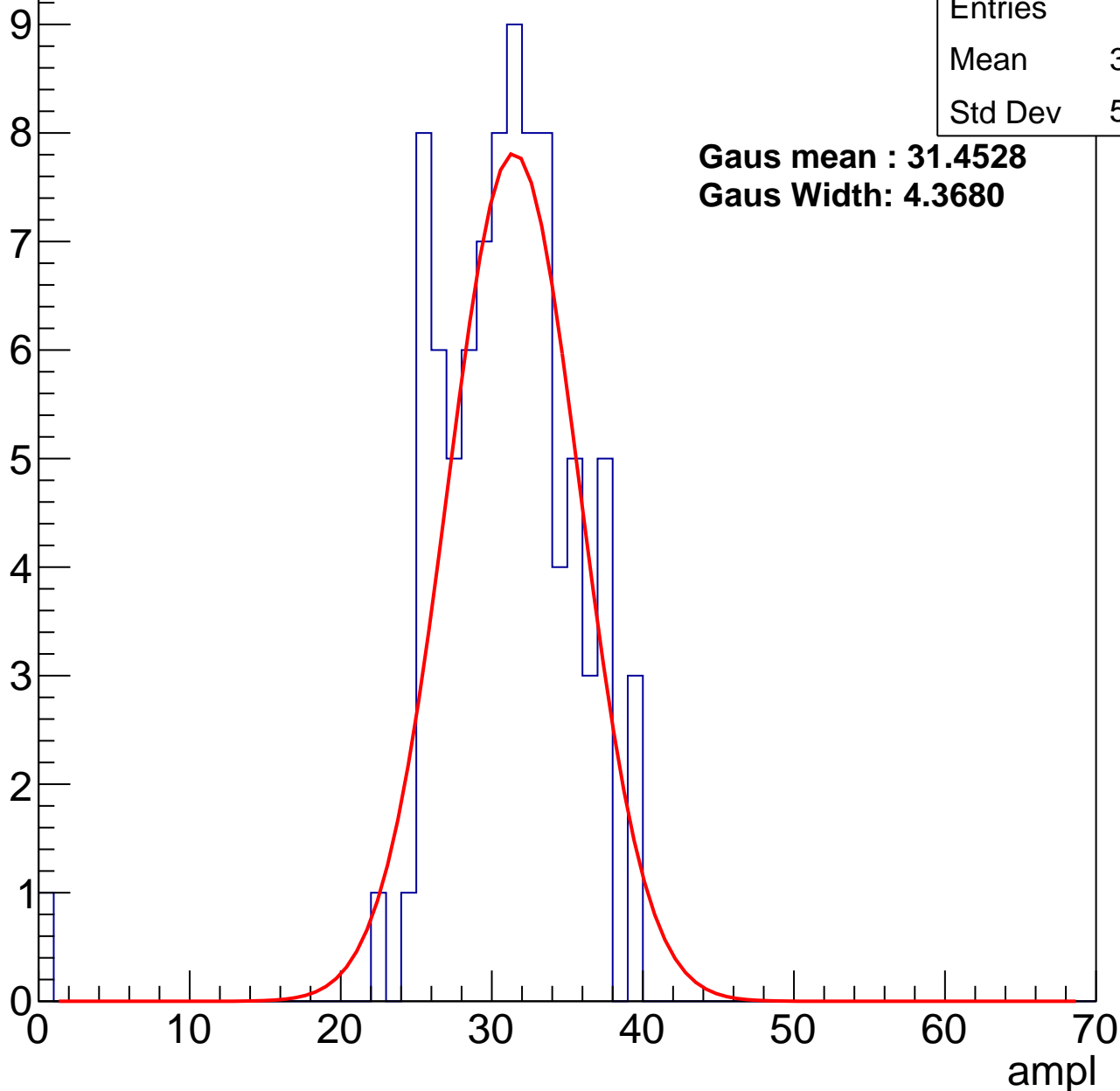
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	88
Mean	30.32
Std Dev	5.069

Gaus mean : 31.4528

Gaus Width: 4.3680



B0L002S, U5-ch13, adc1

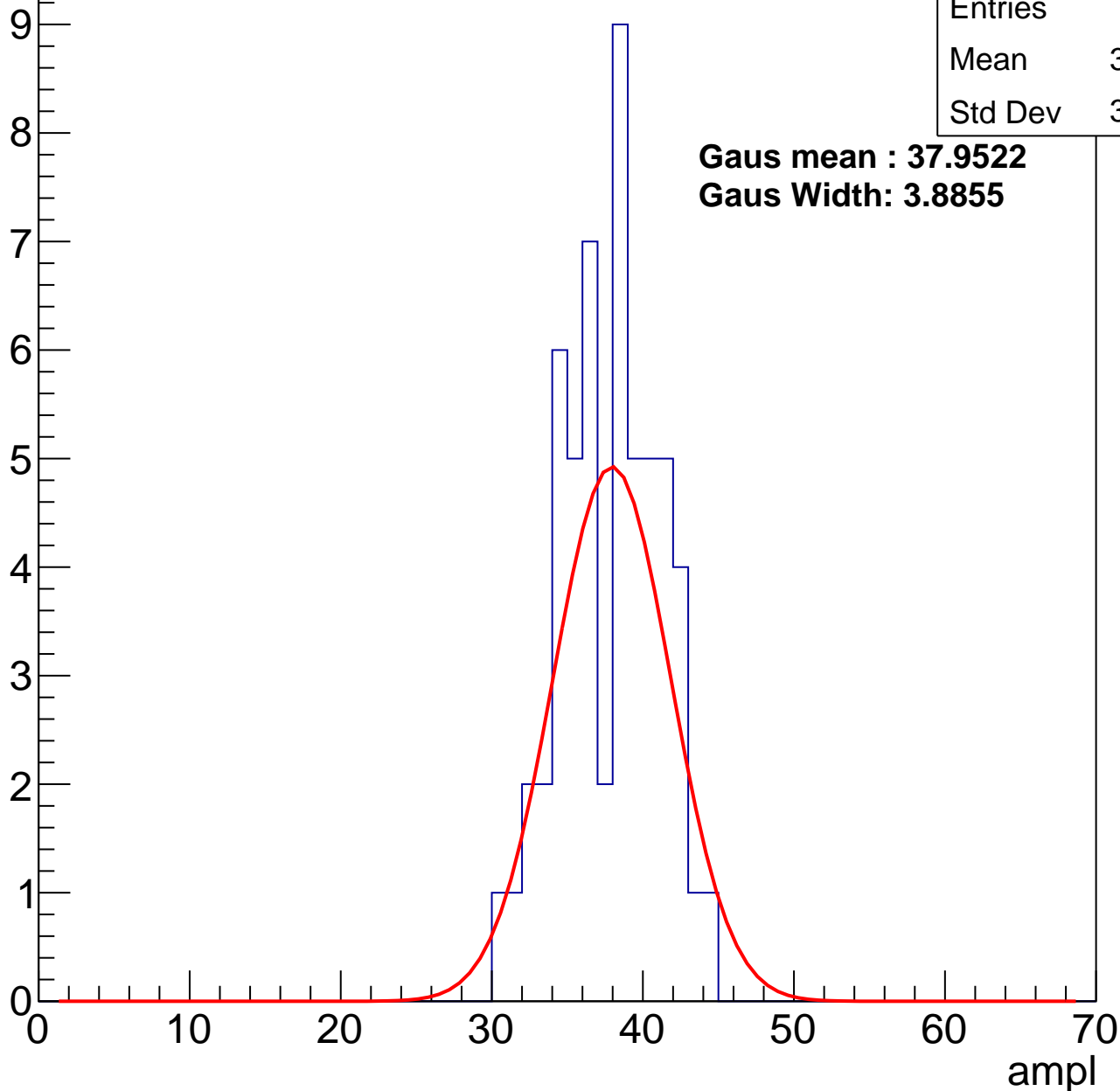
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	37.38
Std Dev	3.216

Gaus mean : 37.9522

Gaus Width: 3.8855



B0L002S, U5-ch13, adc2

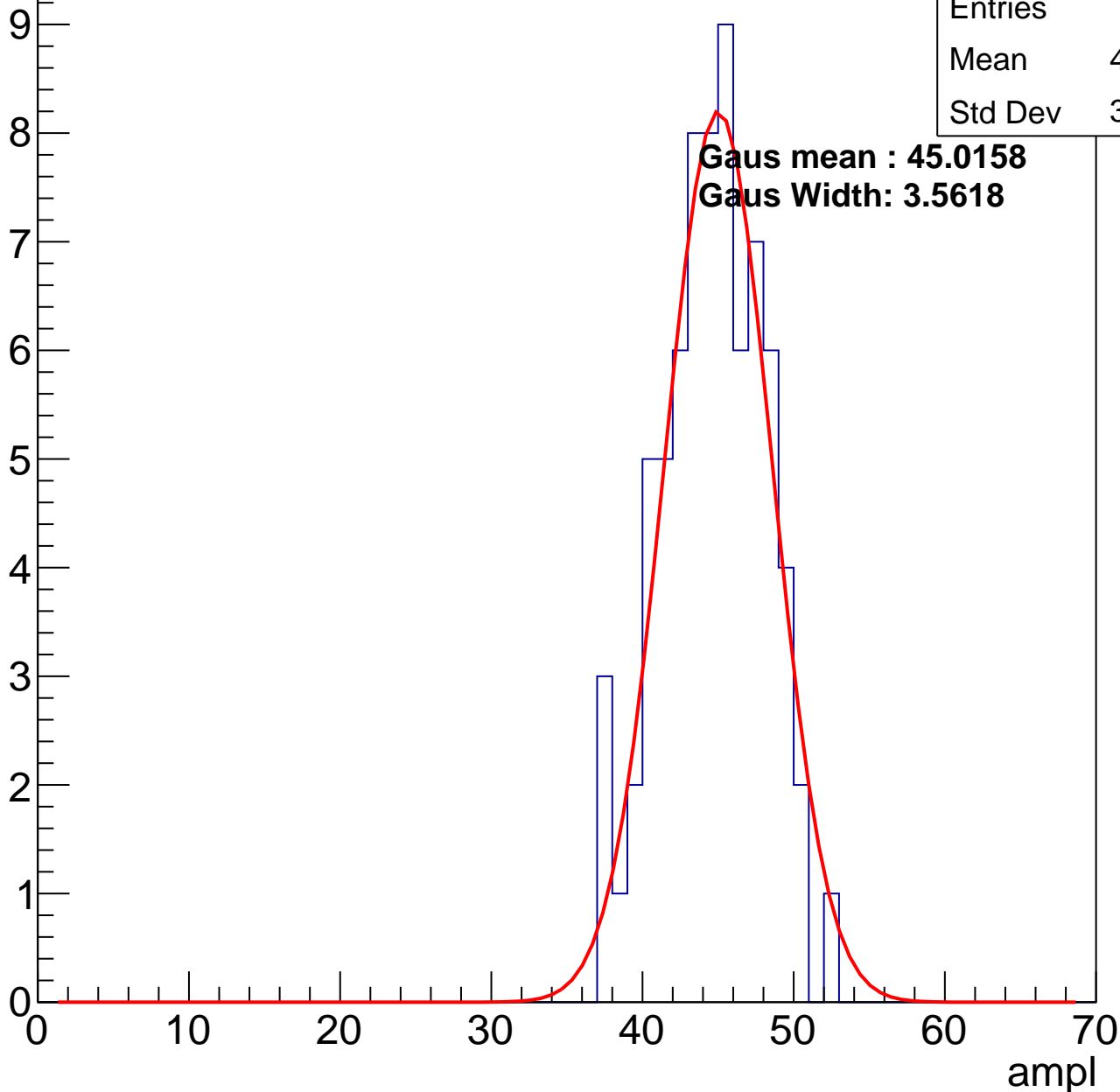
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.19
Std Dev	3.342

Gaus mean : 45.0158

Gaus Width: 3.5618

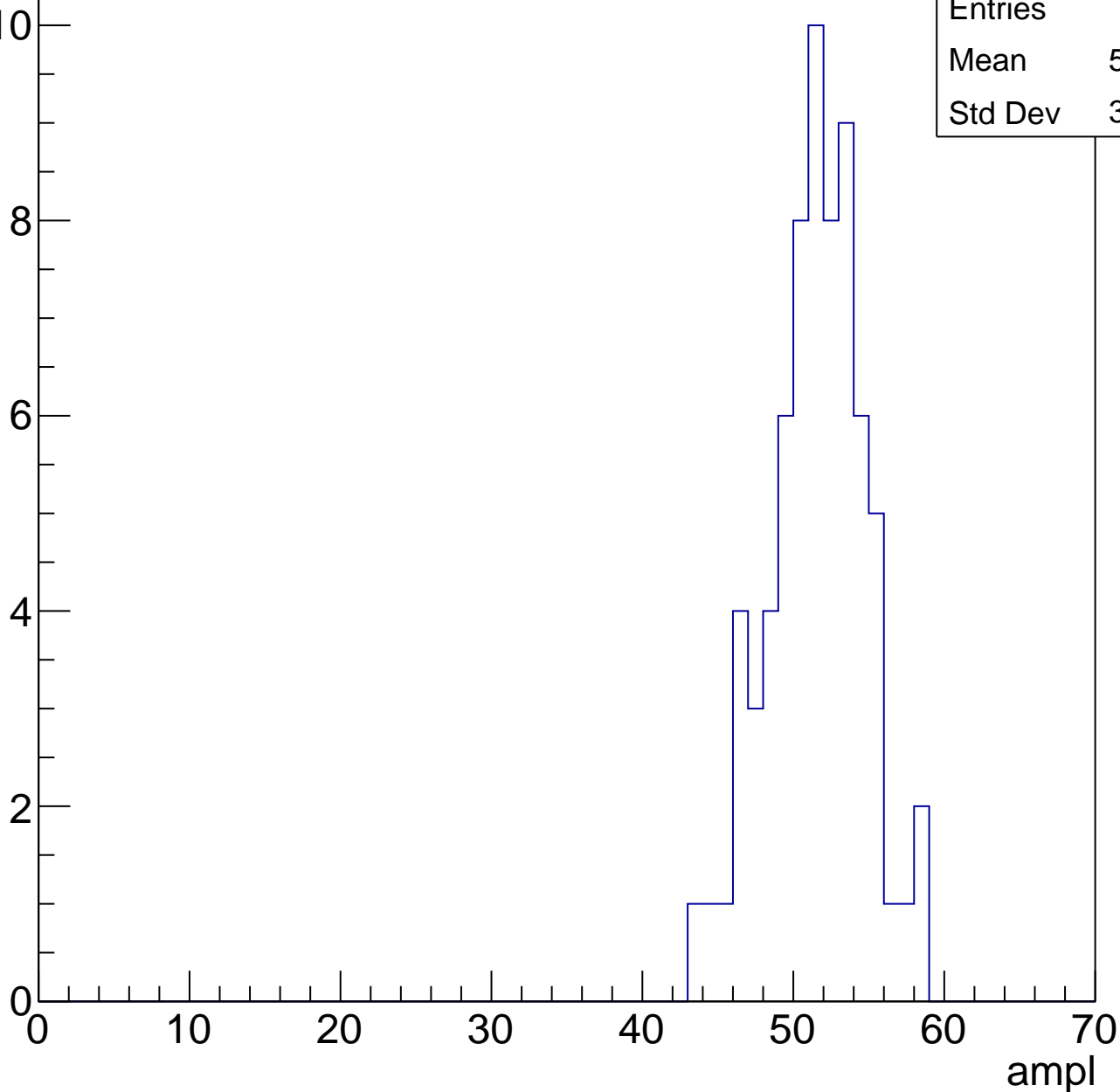


B0L002S, U5-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	51.06
Std Dev	3.166

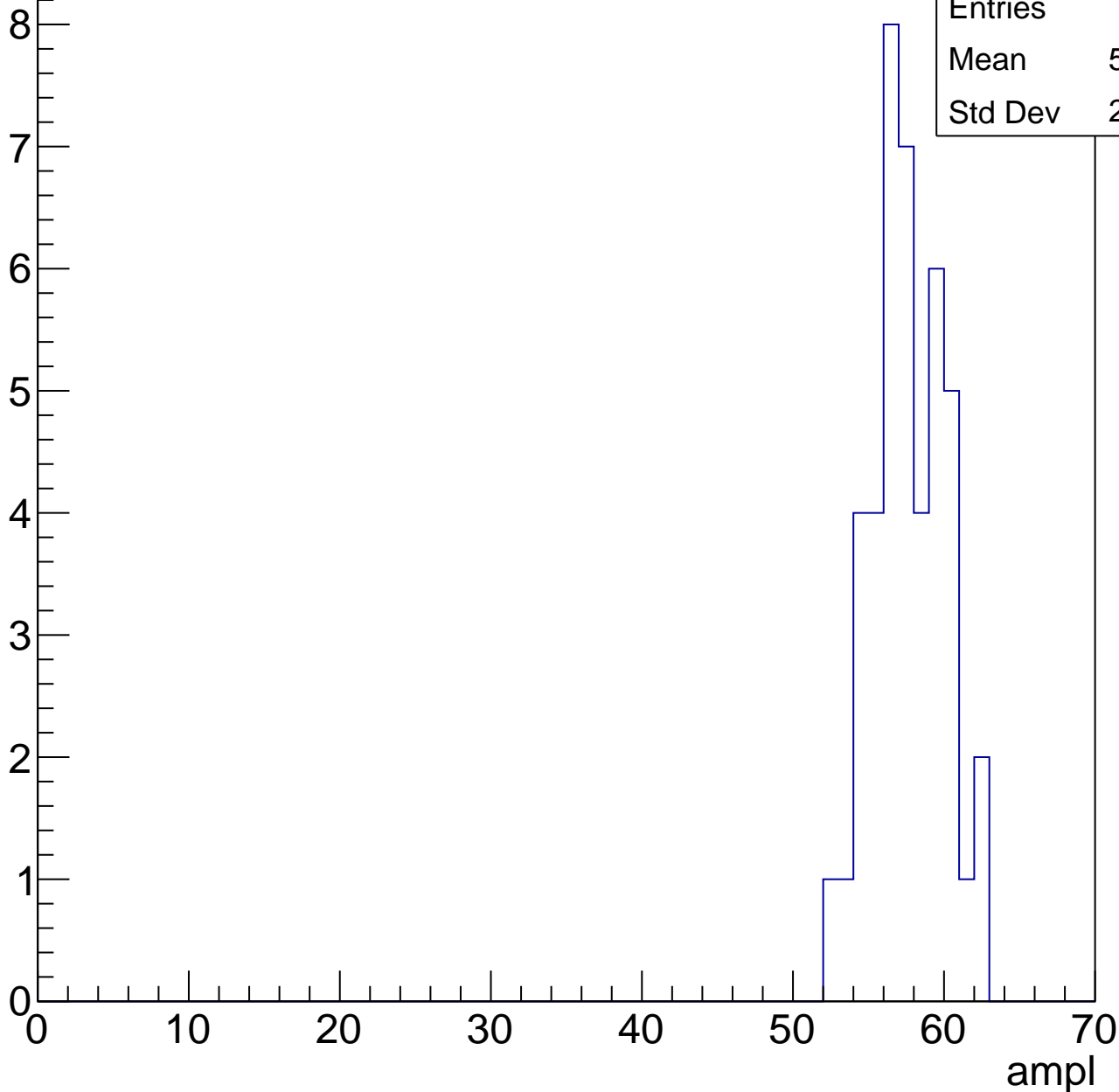


B0L002S, U5-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	57.19
Std Dev	2.355

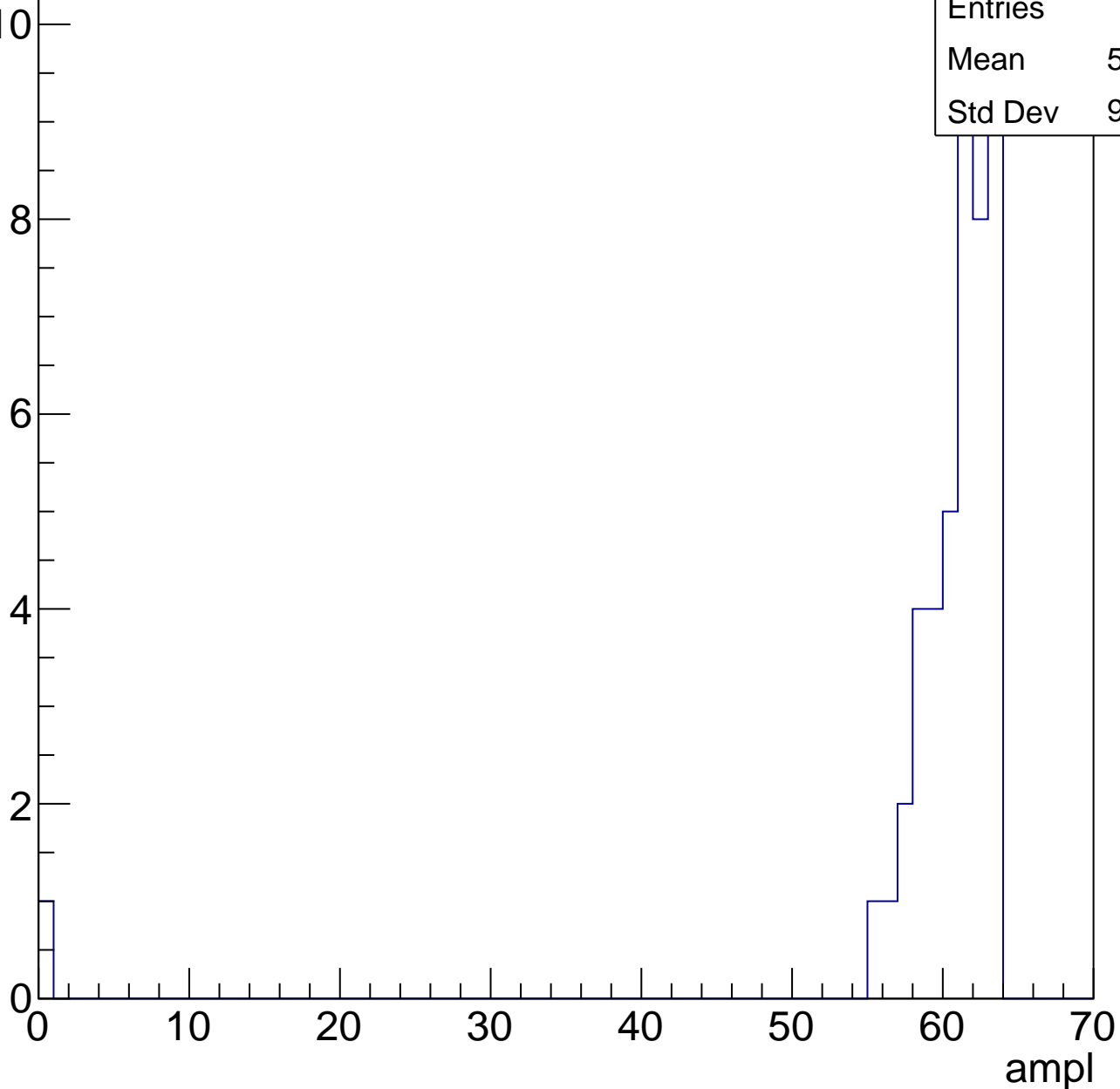


B0L002S, U5-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.29
Std Dev	9.174



B0L002S, U5-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch14, adc0

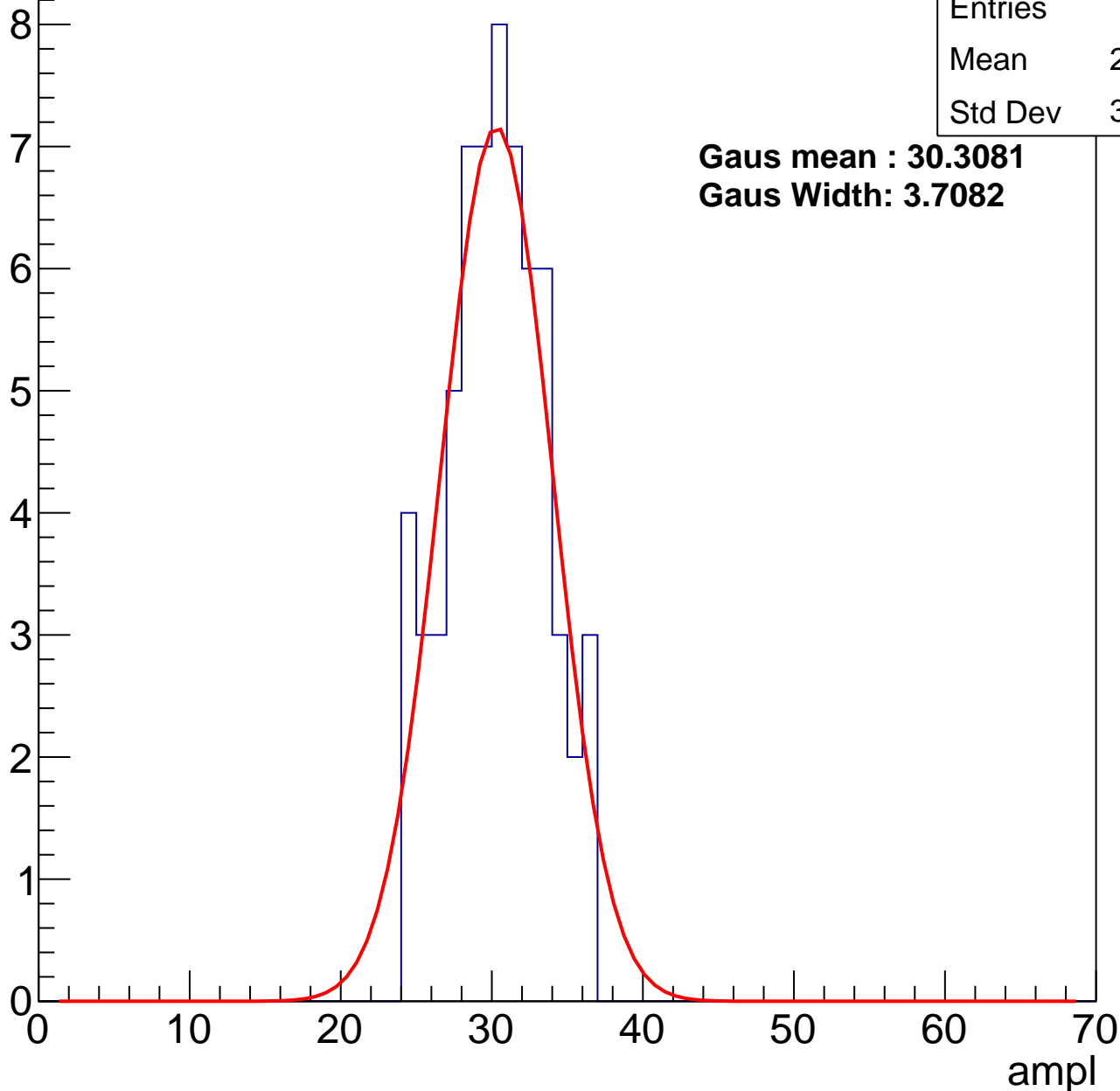
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.84
Std Dev	3.153

Gaus mean : 30.3081

Gaus Width: 3.7082



B0L002S, U5-ch14, adc1

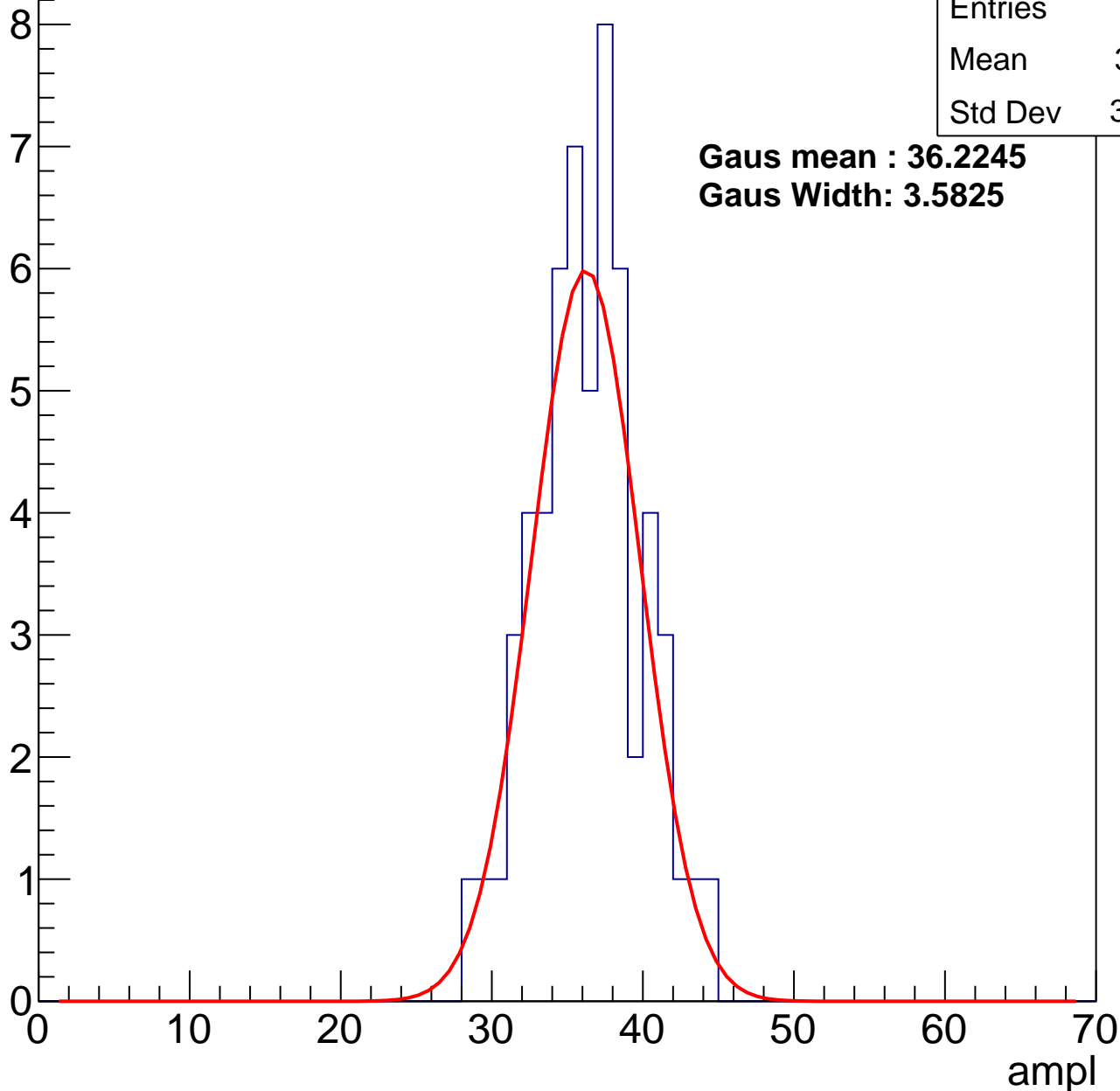
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	35.91
Std Dev	3.456

Gaus mean : 36.2245

Gaus Width: 3.5825



B0L002S, U5-ch14, adc2

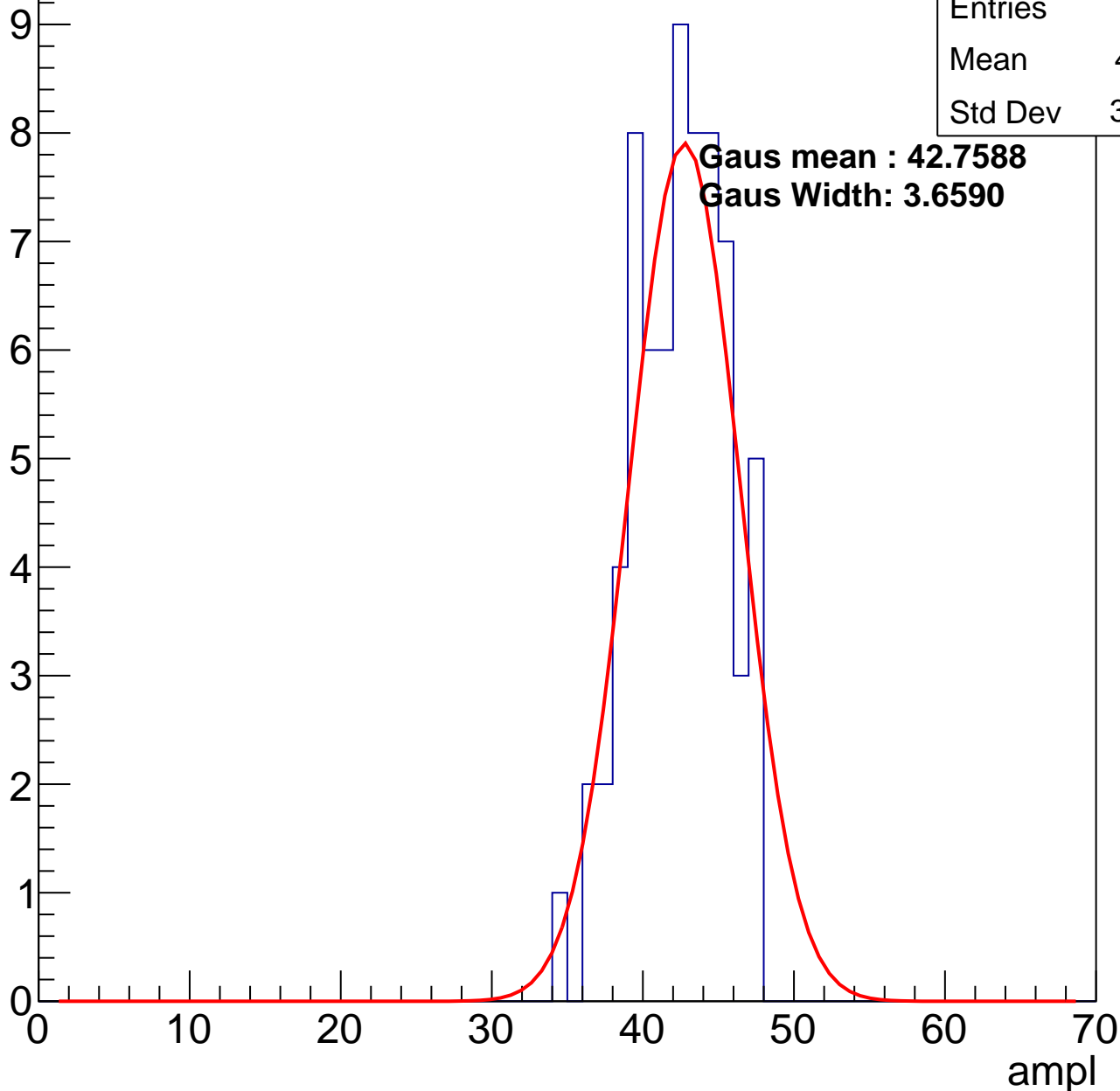
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	41.91
Std Dev	3.016

Gaus mean : 42.7588

Gaus Width: 3.6590

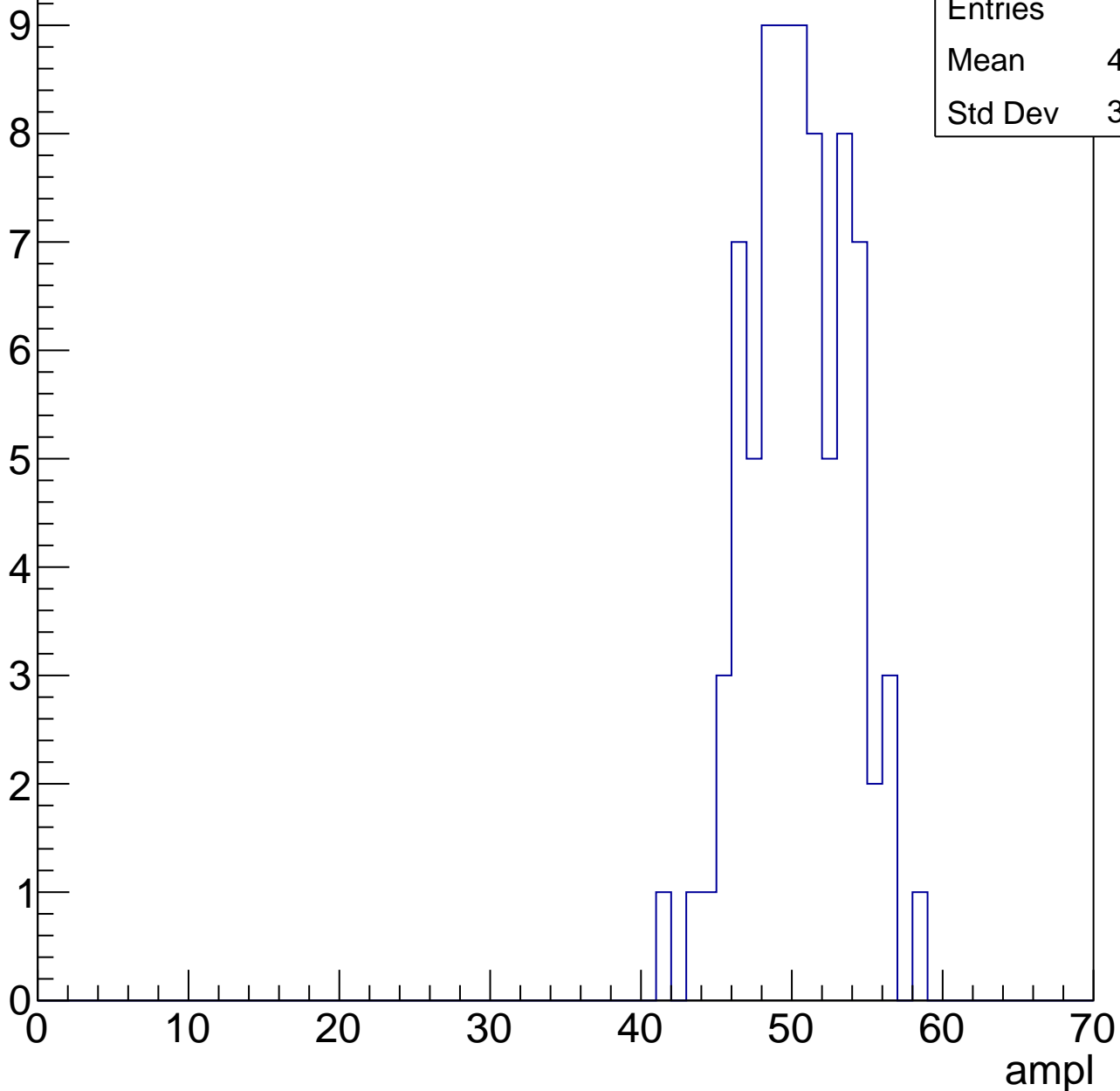


B0L002S, U5-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	49.99
Std Dev	3.332

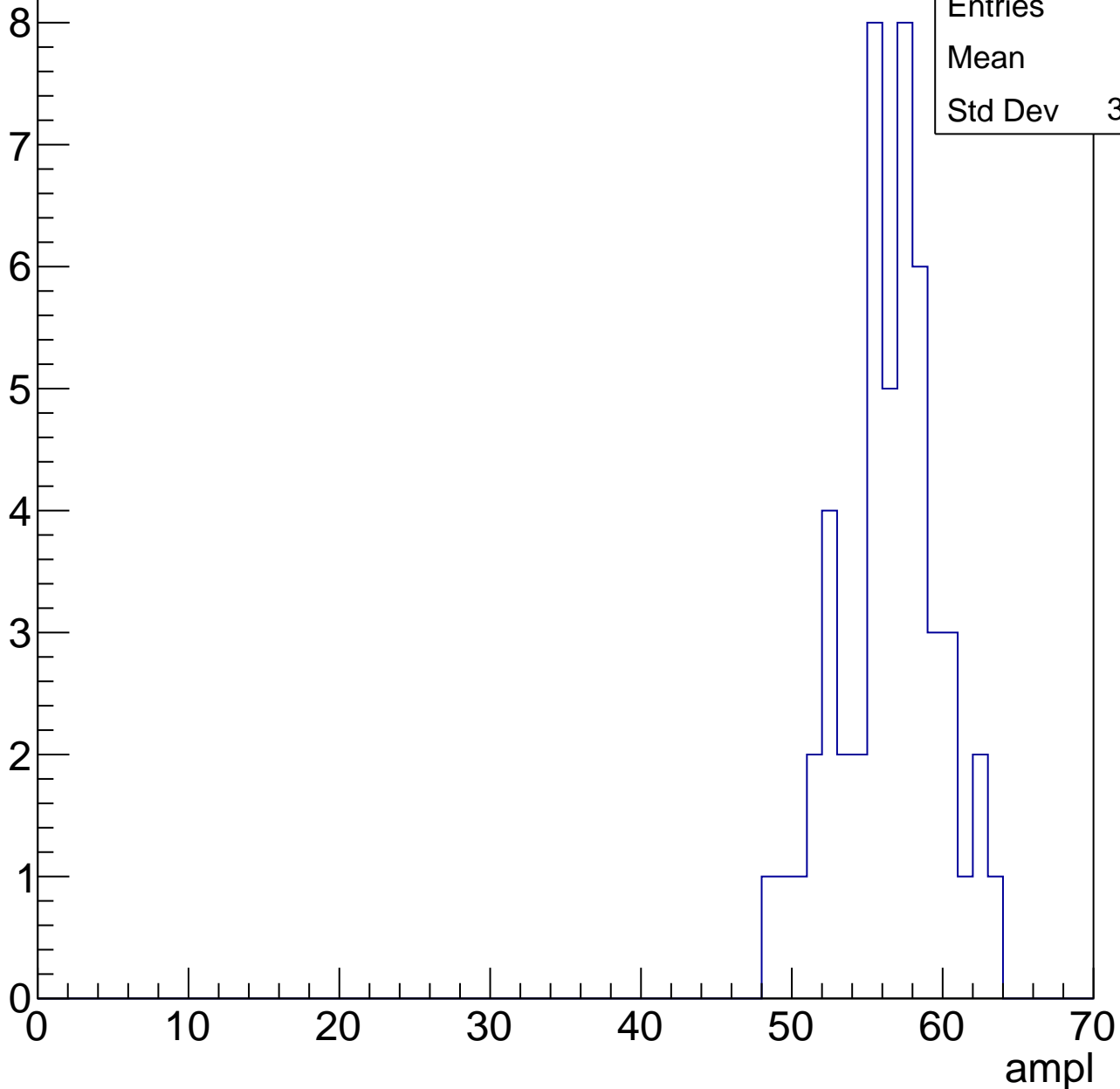


B0L002S, U5-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56
Std Dev	3.317



B0L002S, U5-ch14, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 58.6

Std Dev 9.374

ampl

0

10

20

30

40

50

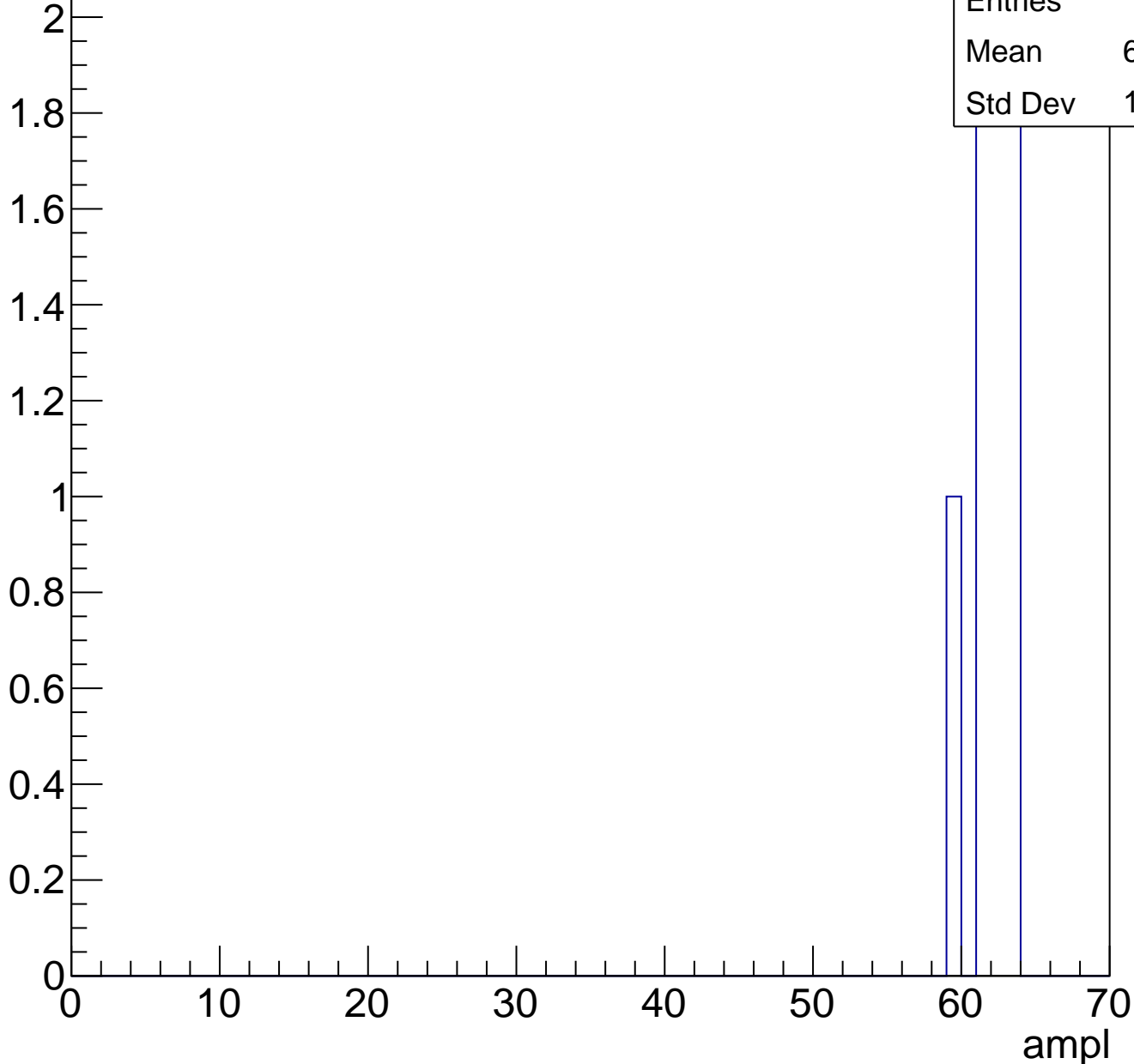
60

70

B0L002S, U5-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch15, adc0

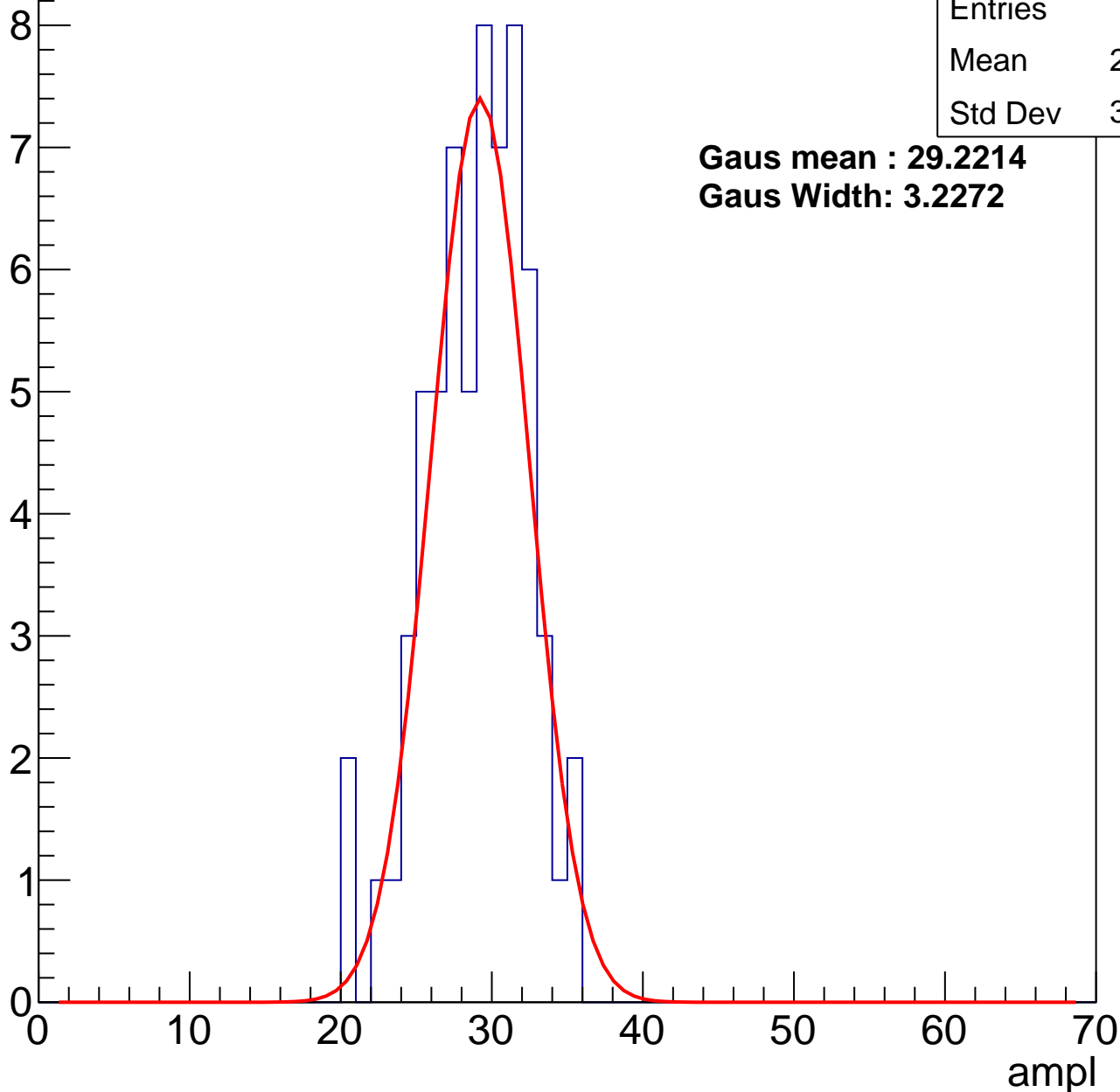
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	28.53
Std Dev	3.316

Gaus mean : 29.2214

Gaus Width: 3.2272



B0L002S, U5-ch15, adc1

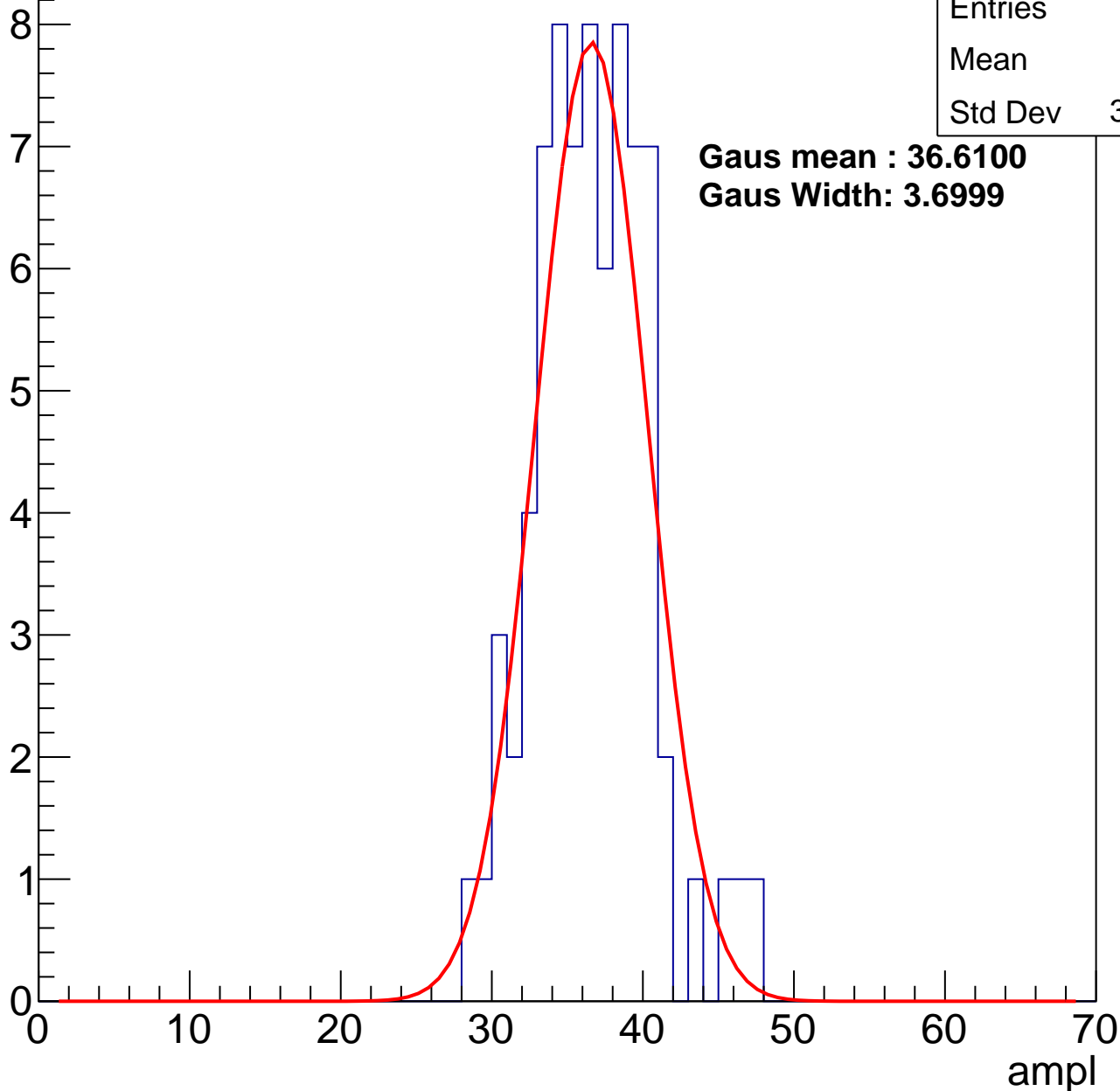
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	36.2
Std Dev	3.738

Gaus mean : 36.6100

Gaus Width: 3.6999



B0L002S, U5-ch15, adc2

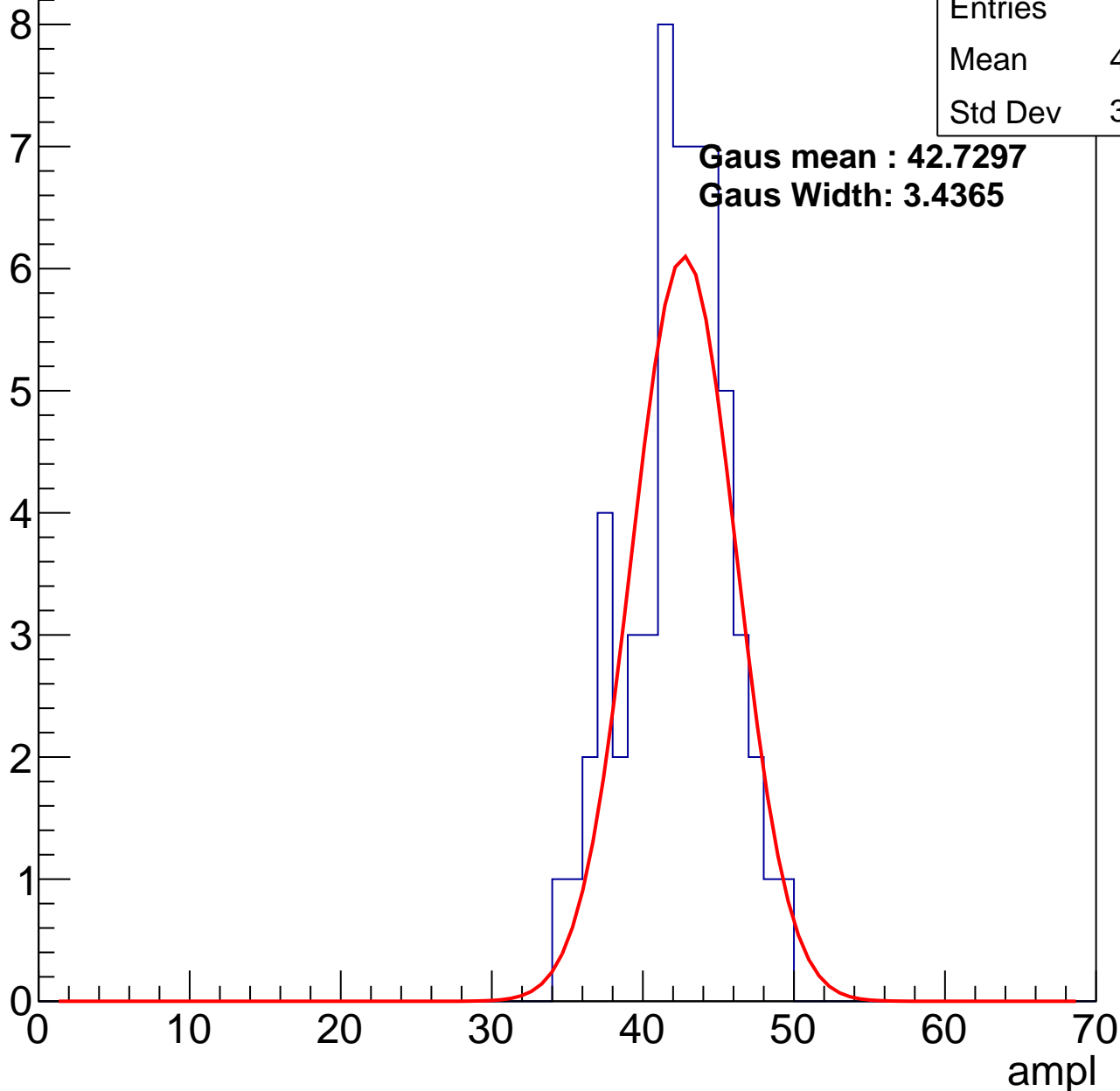
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	41.88
Std Dev	3.314

Gaus mean : 42.7297

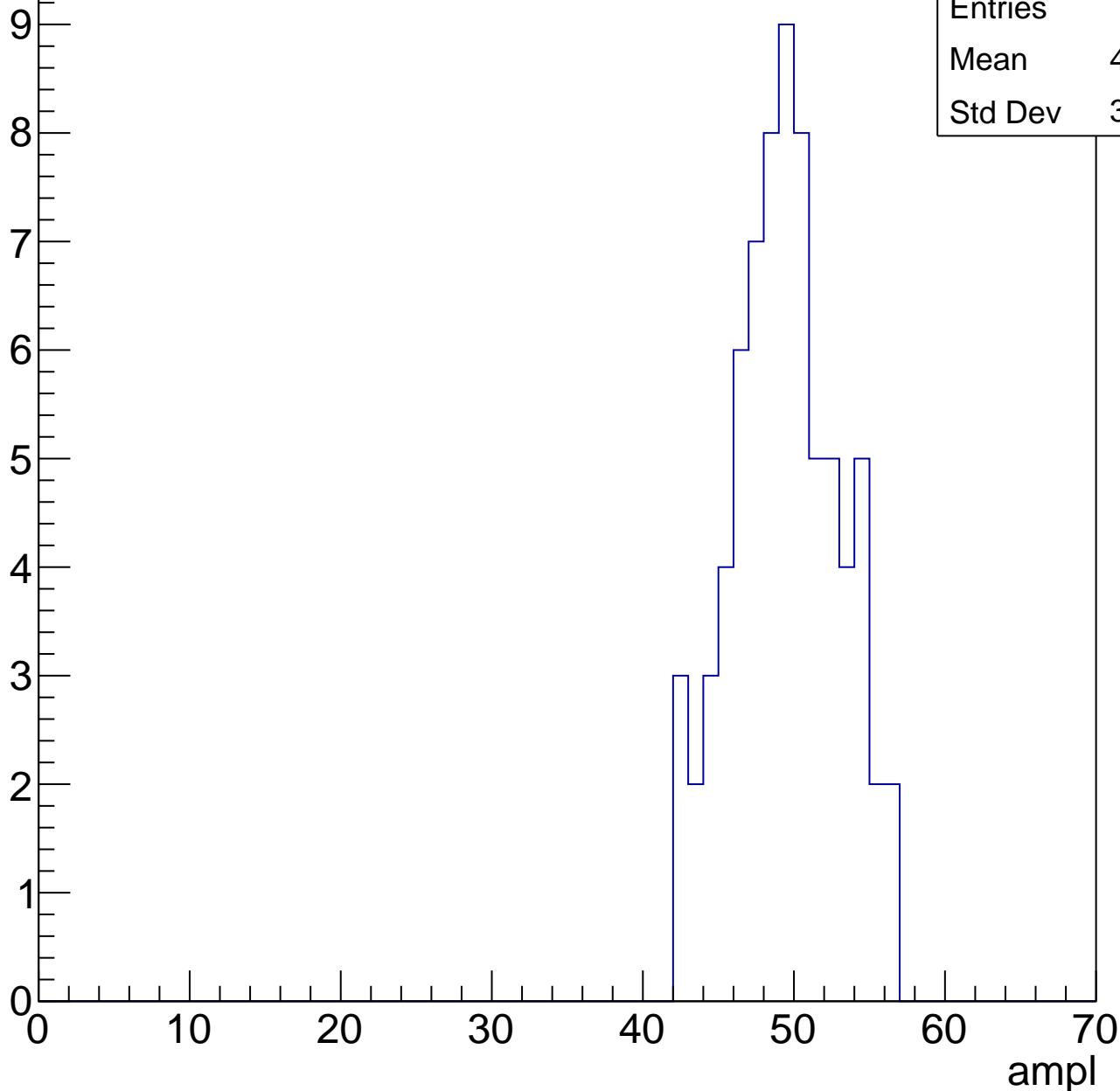
Gaus Width: 3.4365



B0L002S, U5-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



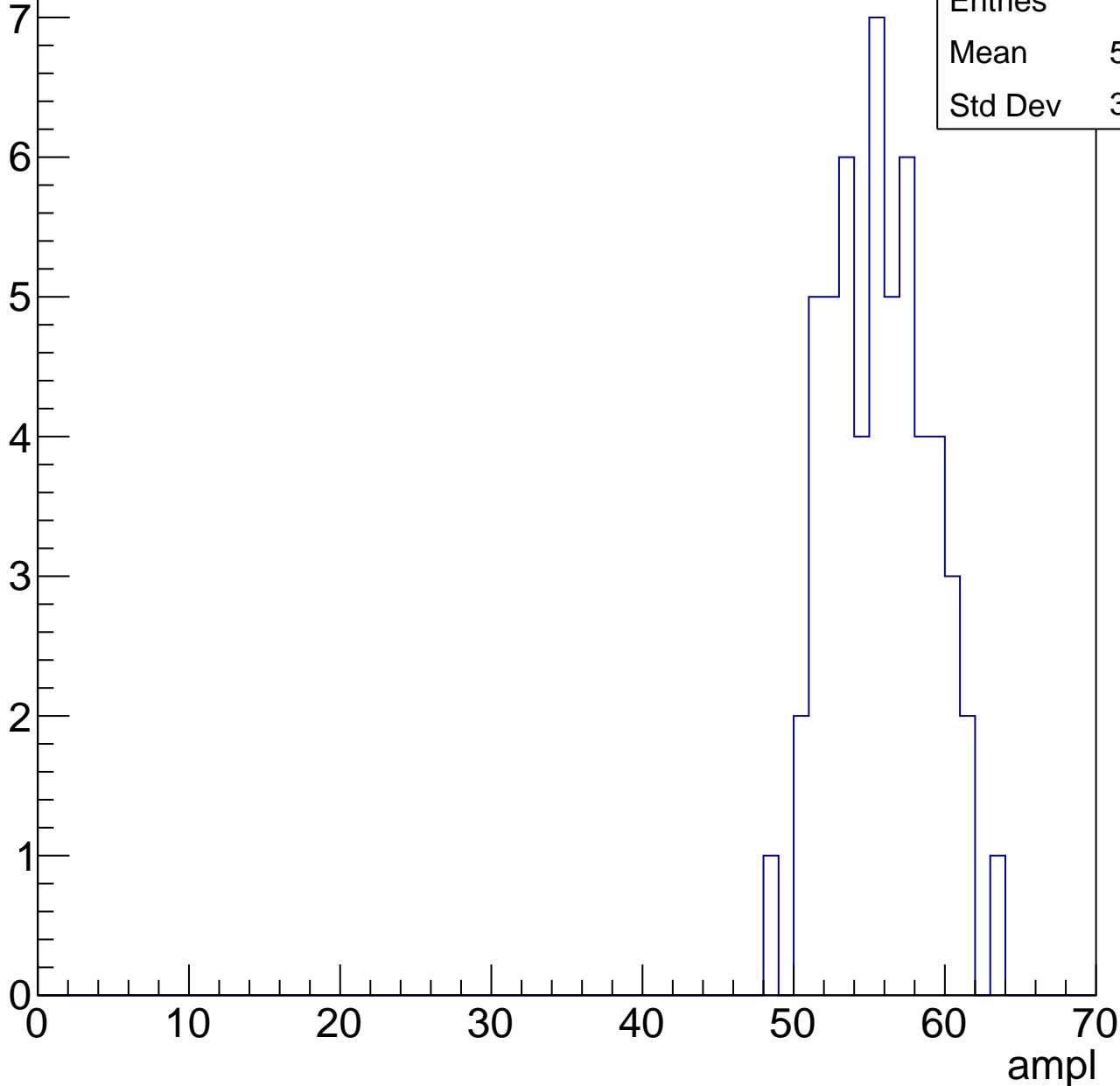
Entries	73
Mean	48.95
Std Dev	3.472

B0L002S, U5-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	55.22
Std Dev	3.274

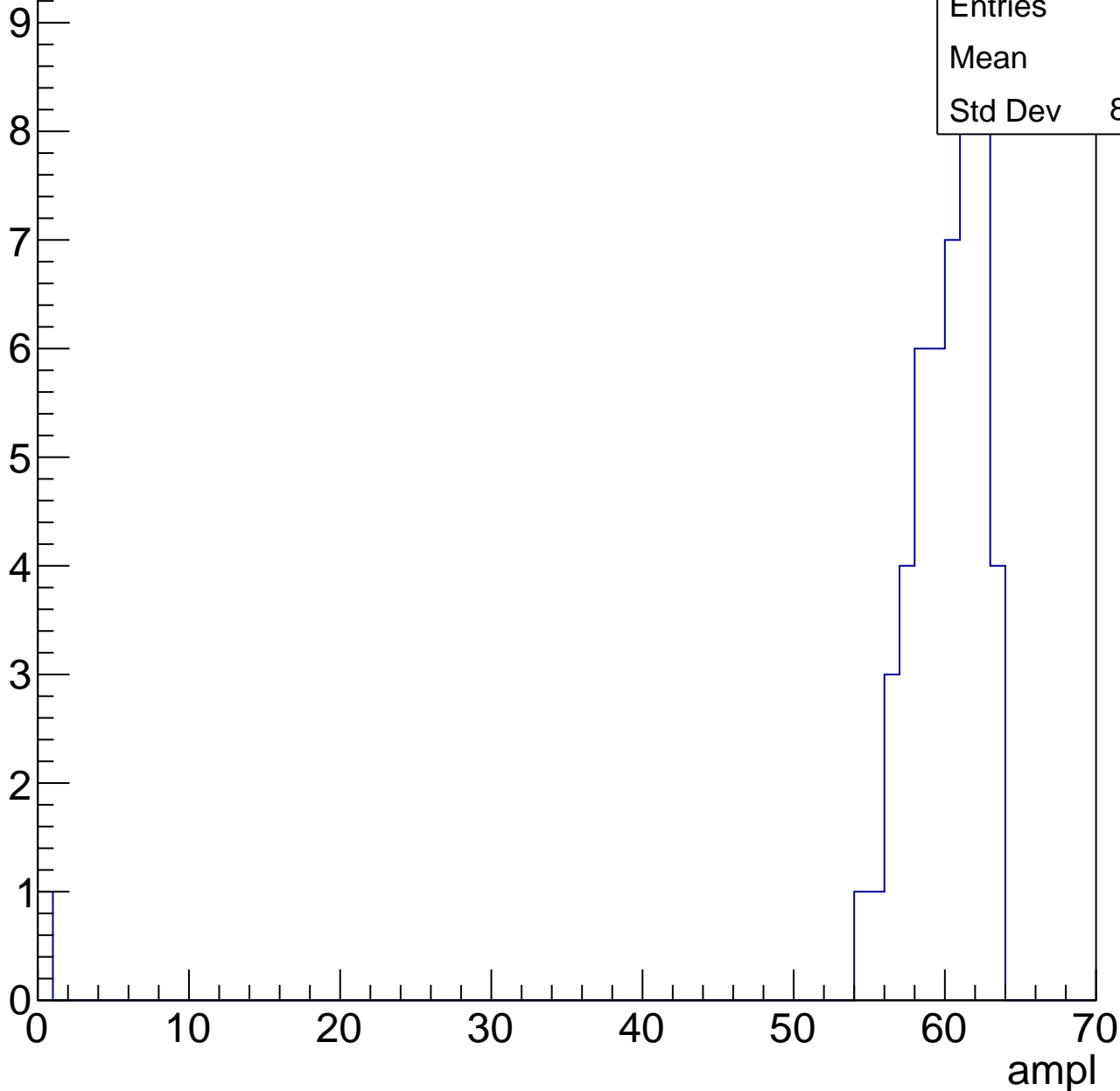


B0L002S, U5-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	58.5
Std Dev	8.652

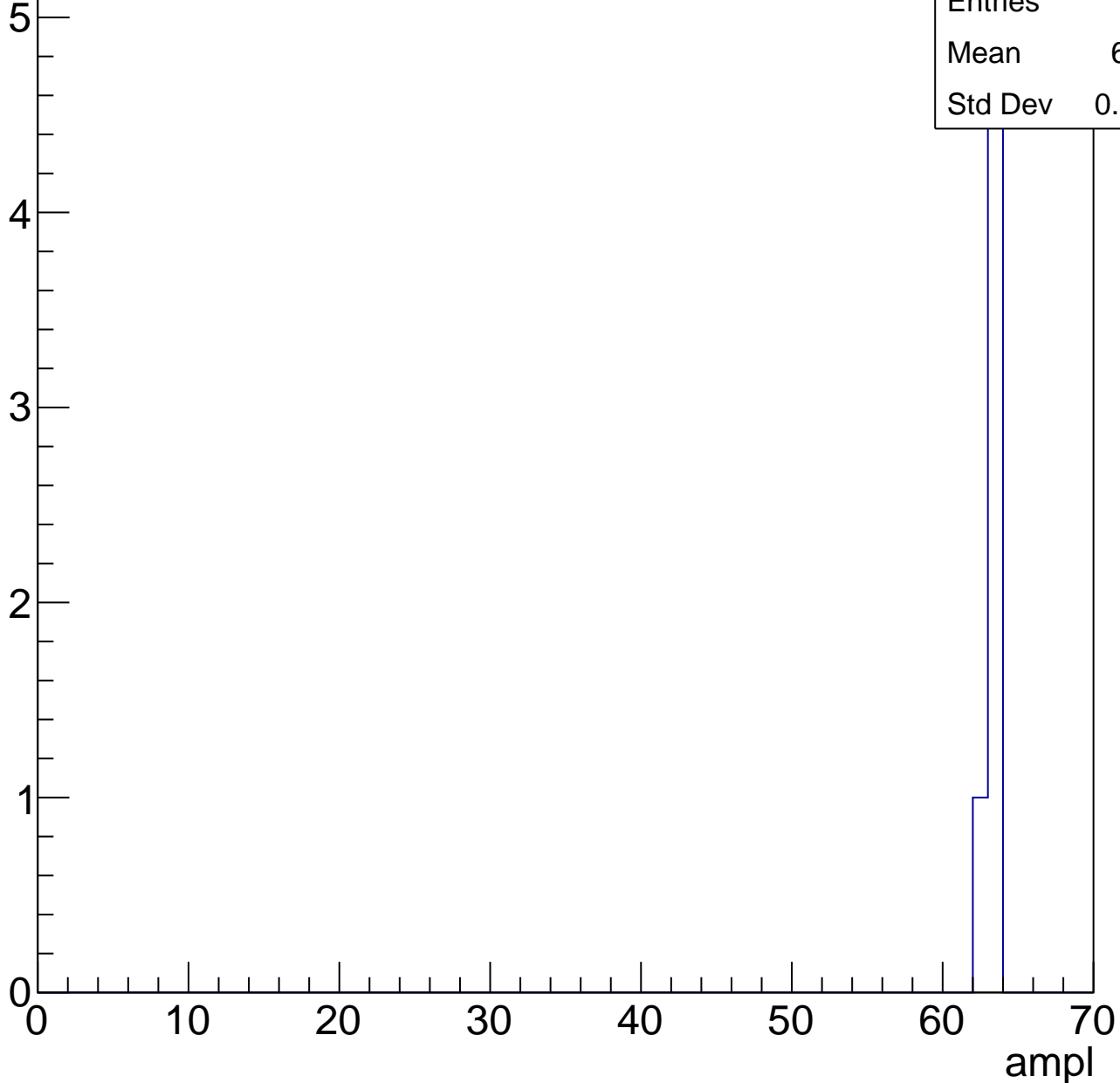


B0L002S, U5-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B0L002S, U5-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch16, adc0

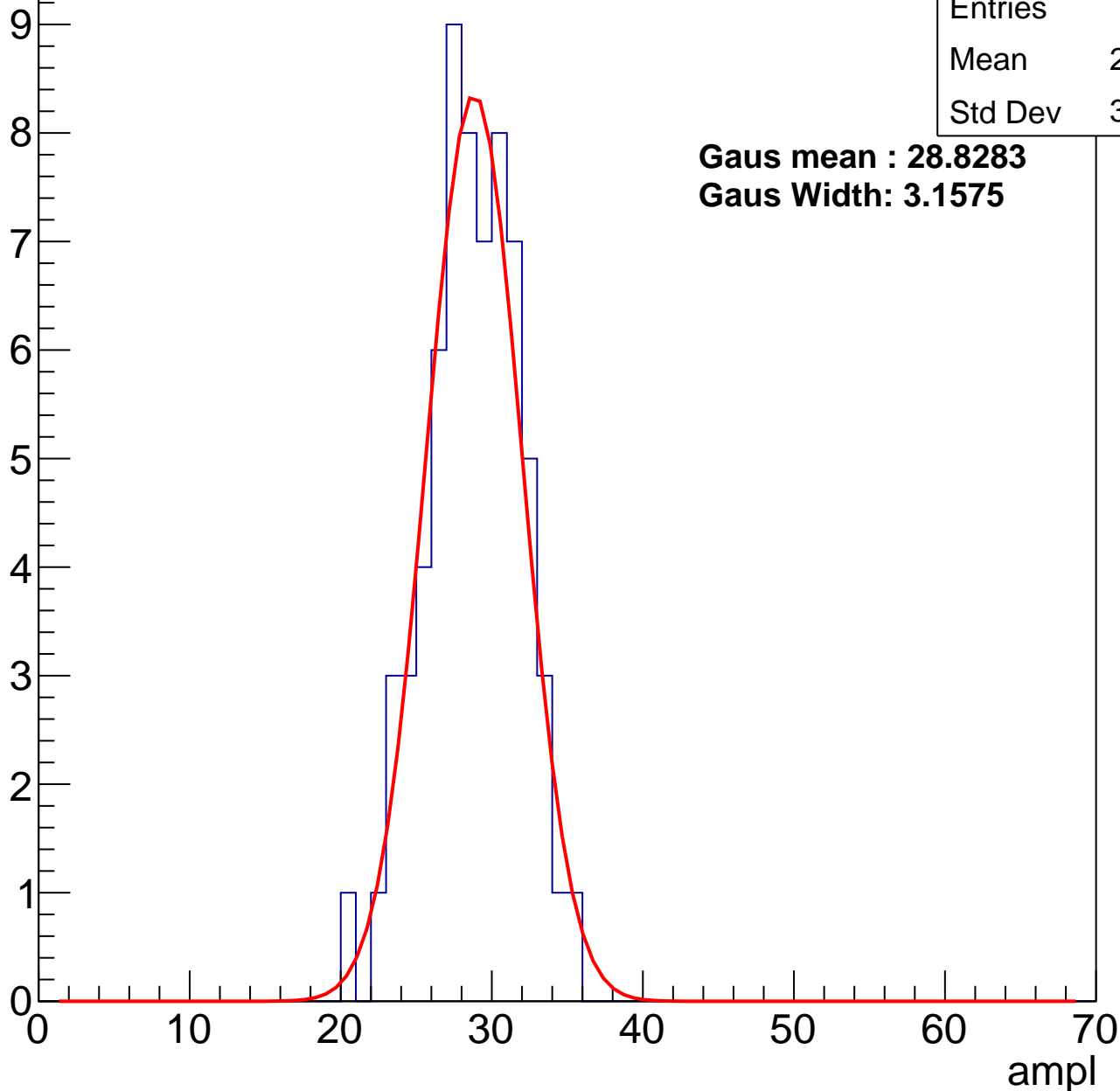
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	28.27
Std Dev	3.064

Gaus mean : 28.8283

Gaus Width: 3.1575



B0L002S, U5-ch16, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	35.47
Std Dev	3.206

Gaus mean : 35.9157

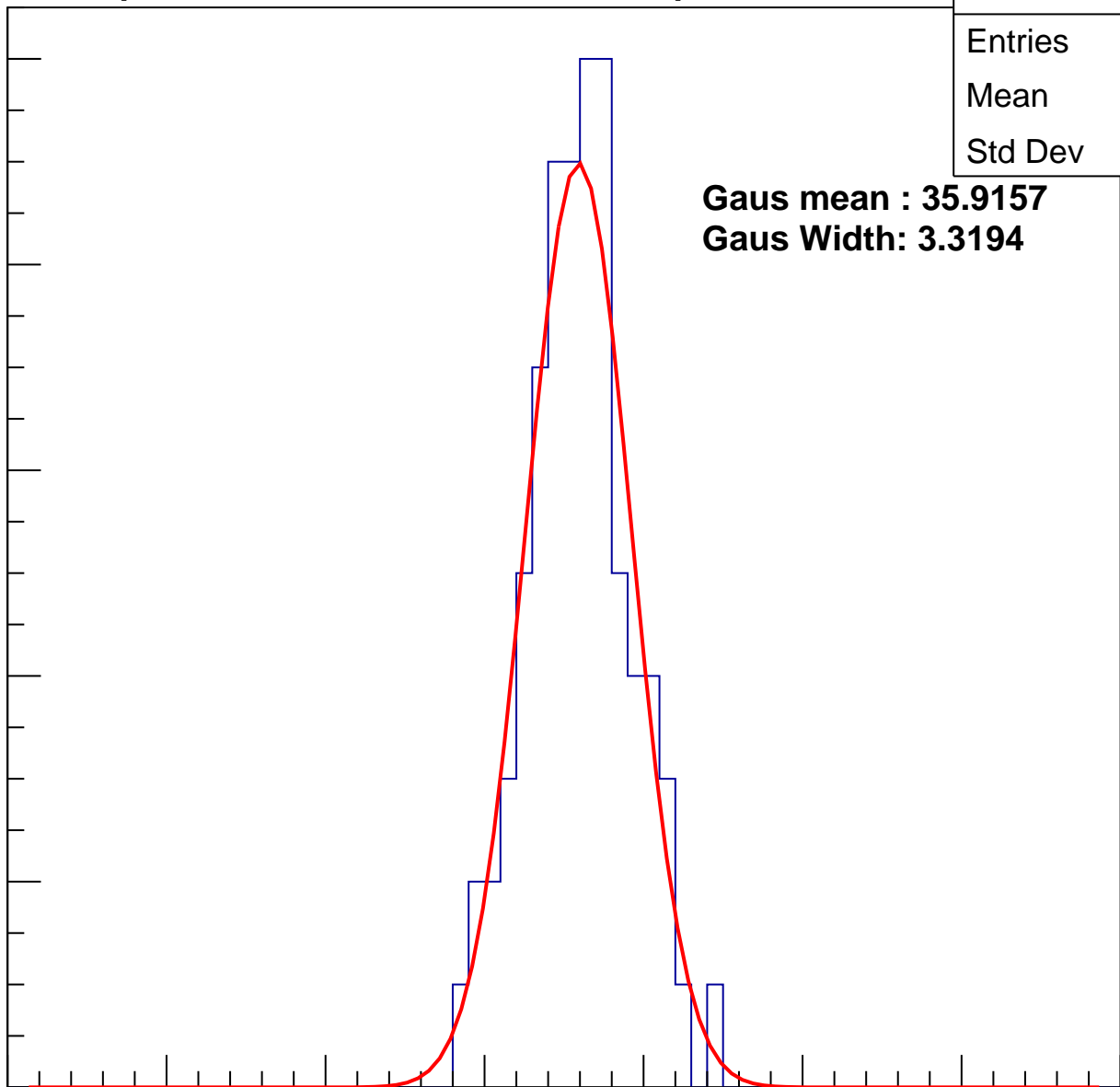
Gaus Width: 3.3194

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch16, adc2

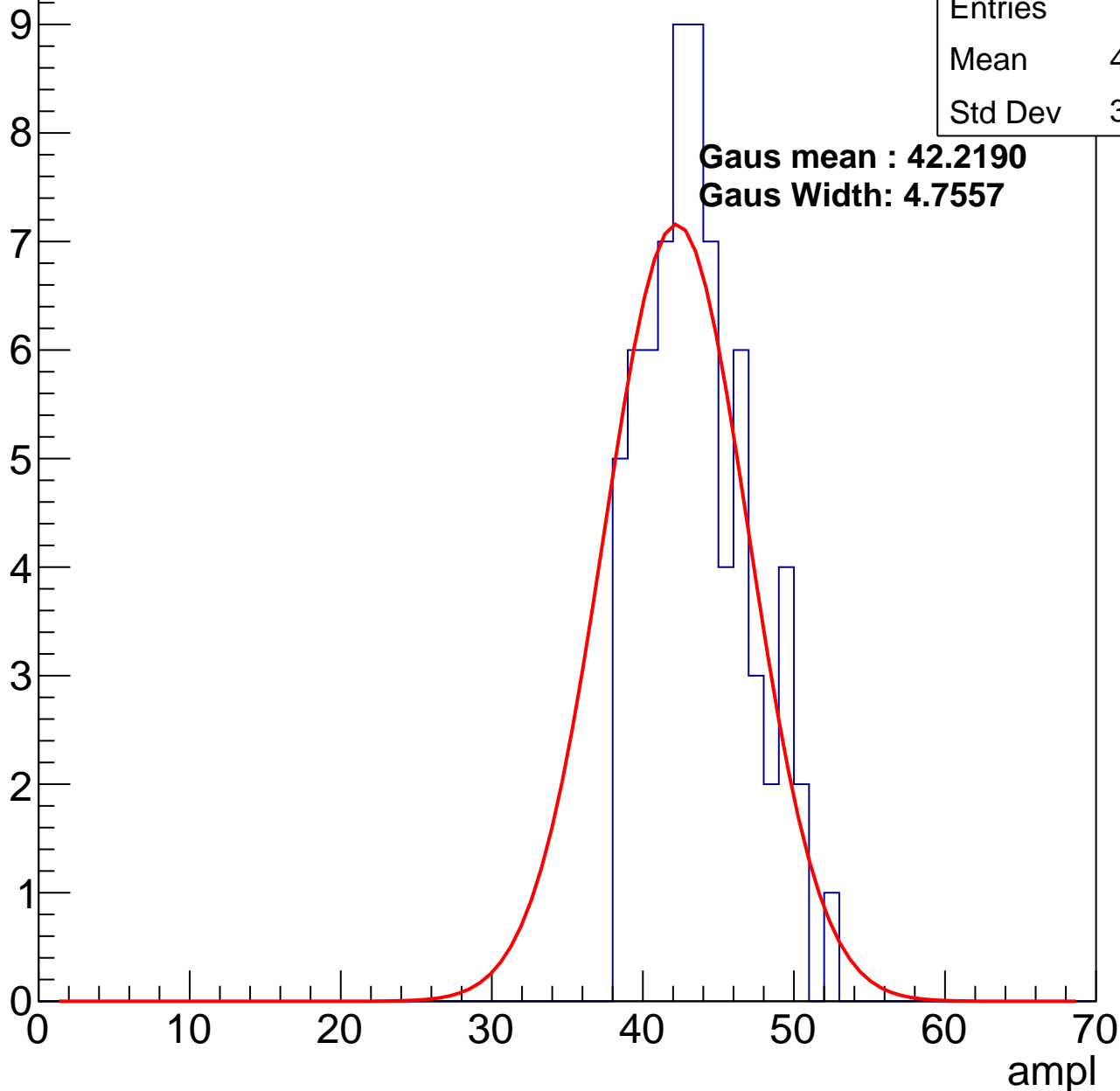
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.17
Std Dev	3.373

Gaus mean : 42.2190

Gaus Width: 4.7557

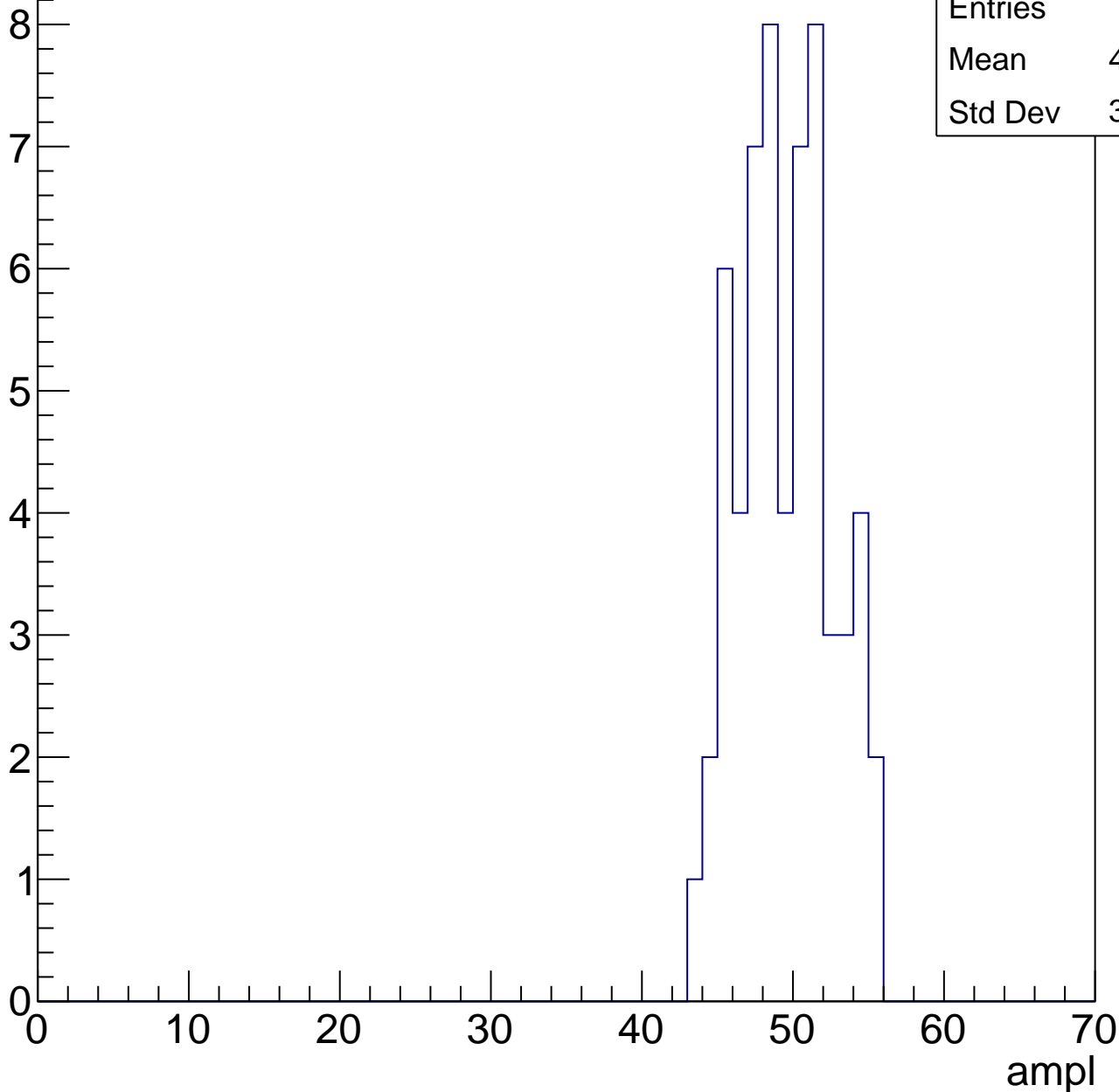


B0L002S, U5-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	49.03
Std Dev	3.025

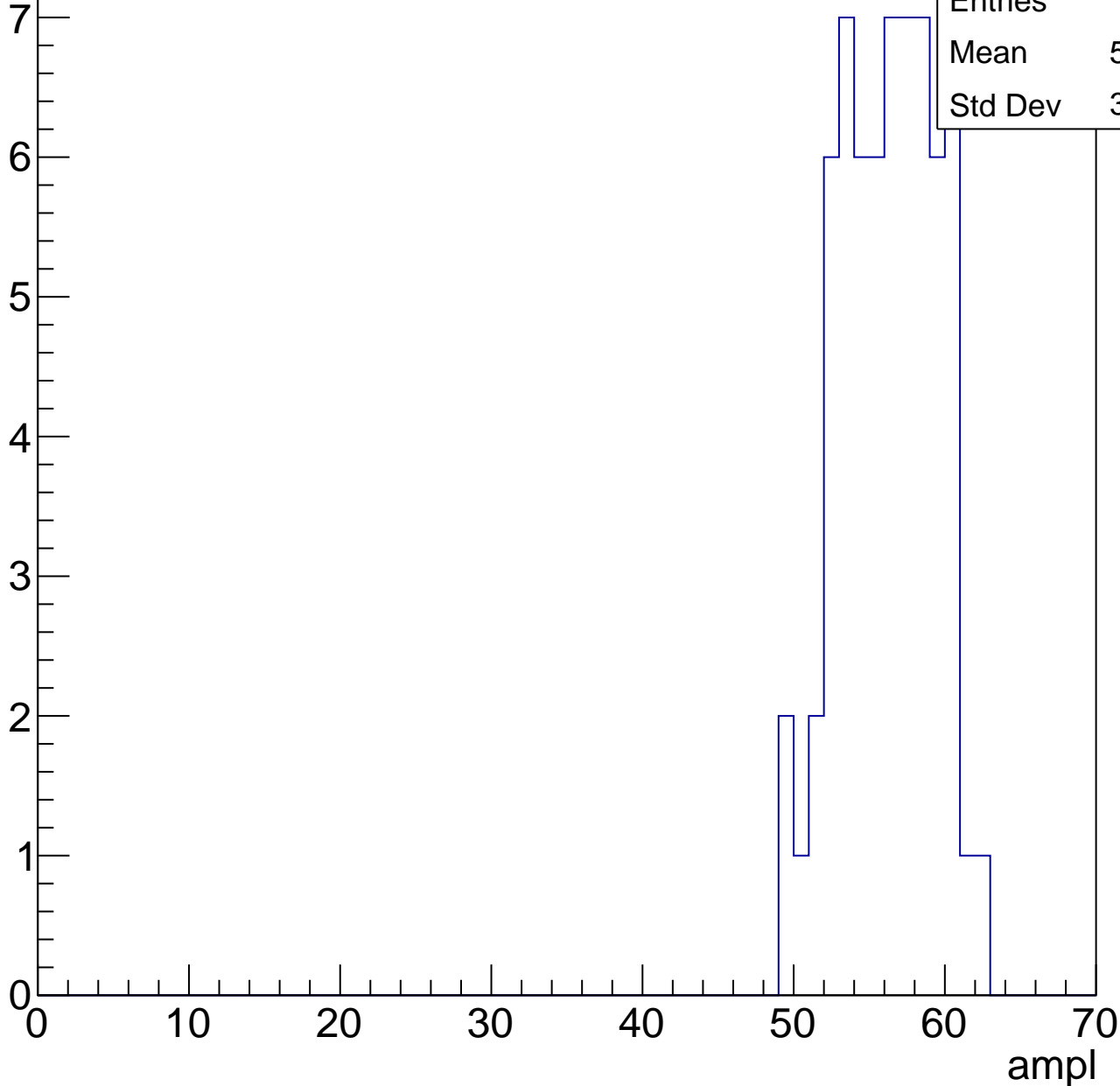


B0L002S, U5-ch16, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	55.77
Std Dev	3.093



B0L002S, U5-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.6
Std Dev	9.447

ampl

0

10

20

30

40

50

60

70

B0L002S, U5-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch17, adc0

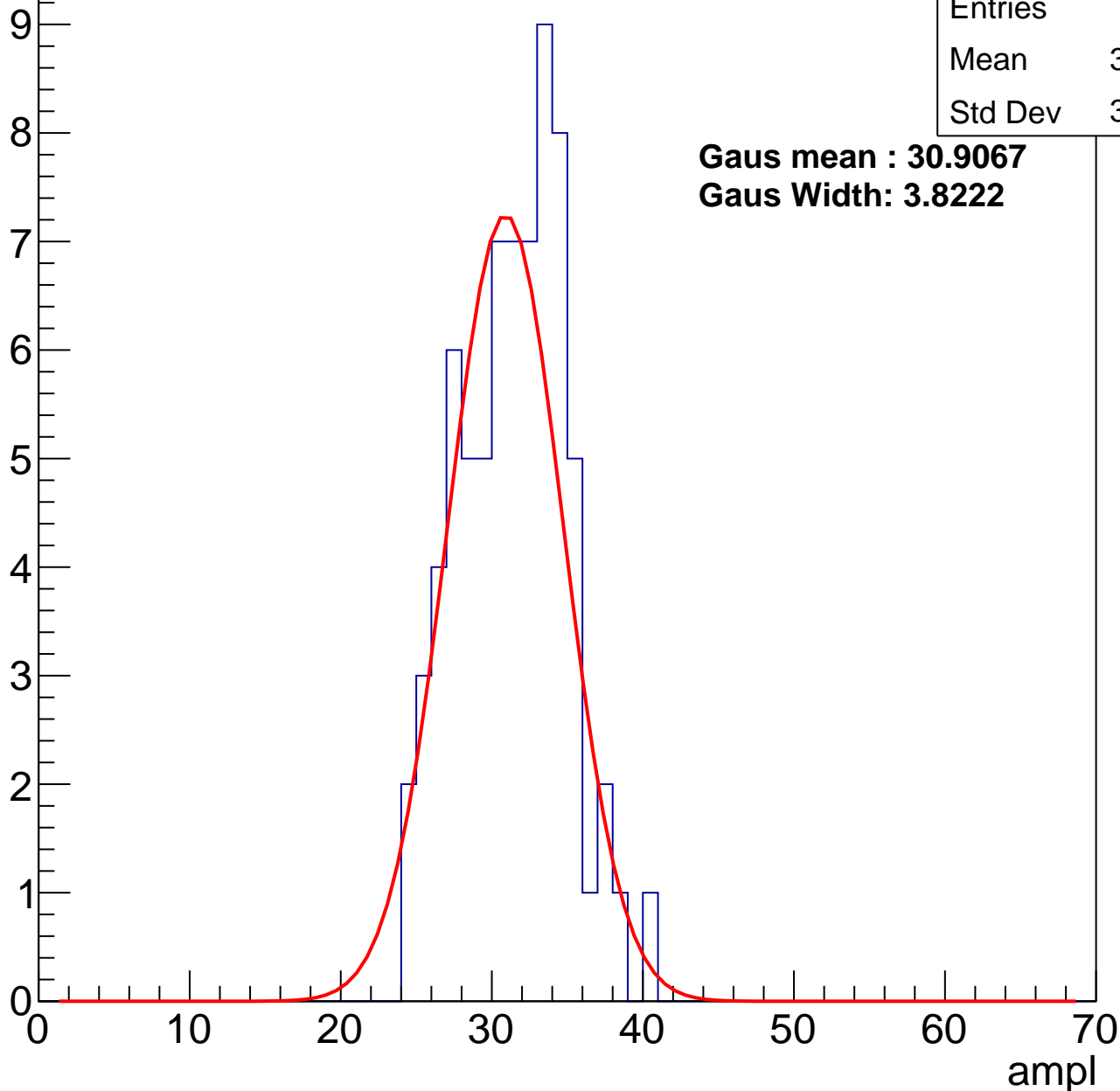
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.92
Std Dev	3.502

Gaus mean : 30.9067

Gaus Width: 3.8222



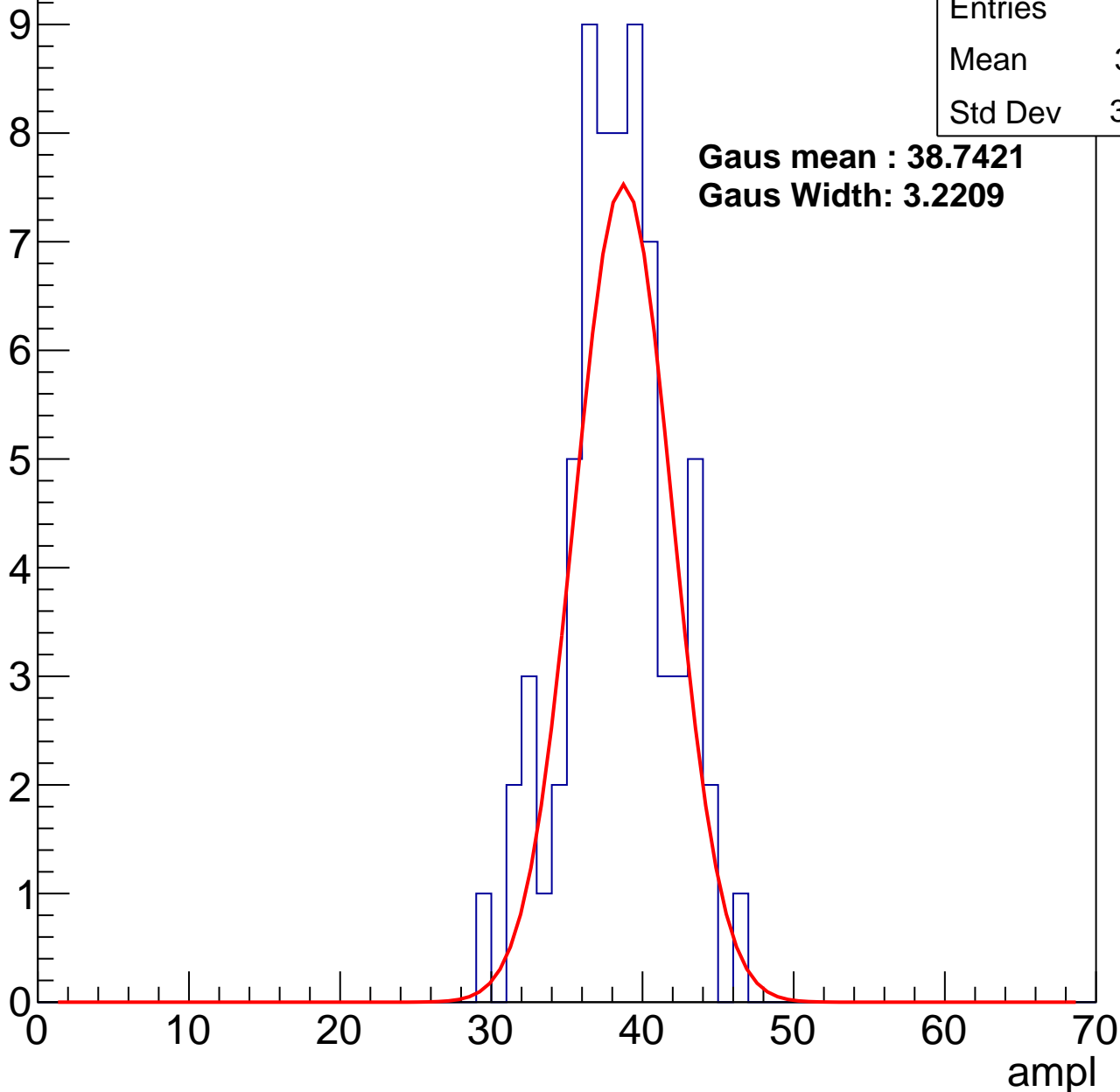
B0L002S, U5-ch17, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.91
Std Dev	3.417

Gaus mean : 38.7421
Gaus Width: 3.2209



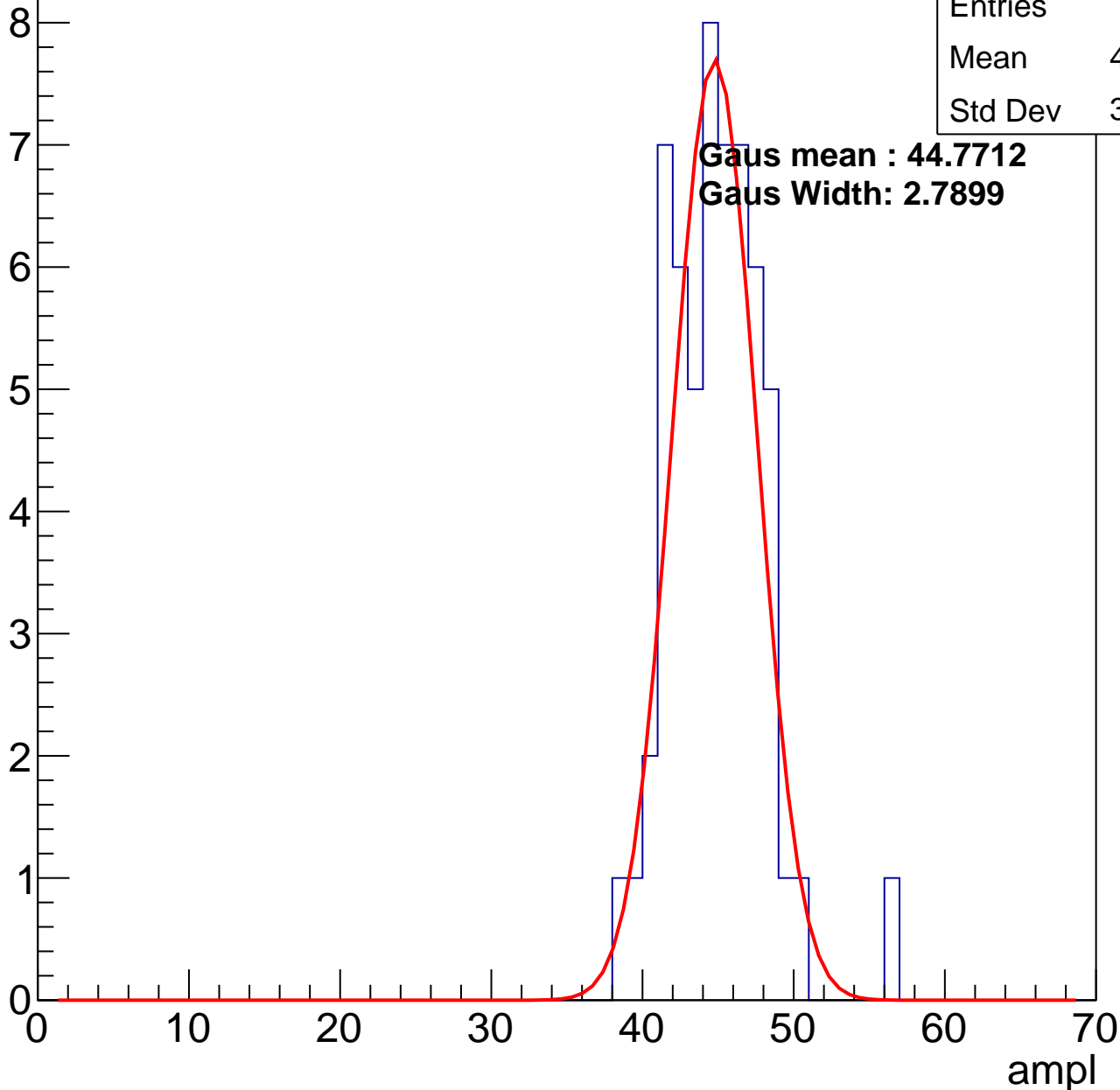
B0L002S, U5-ch17, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.43
Std Dev	3.074

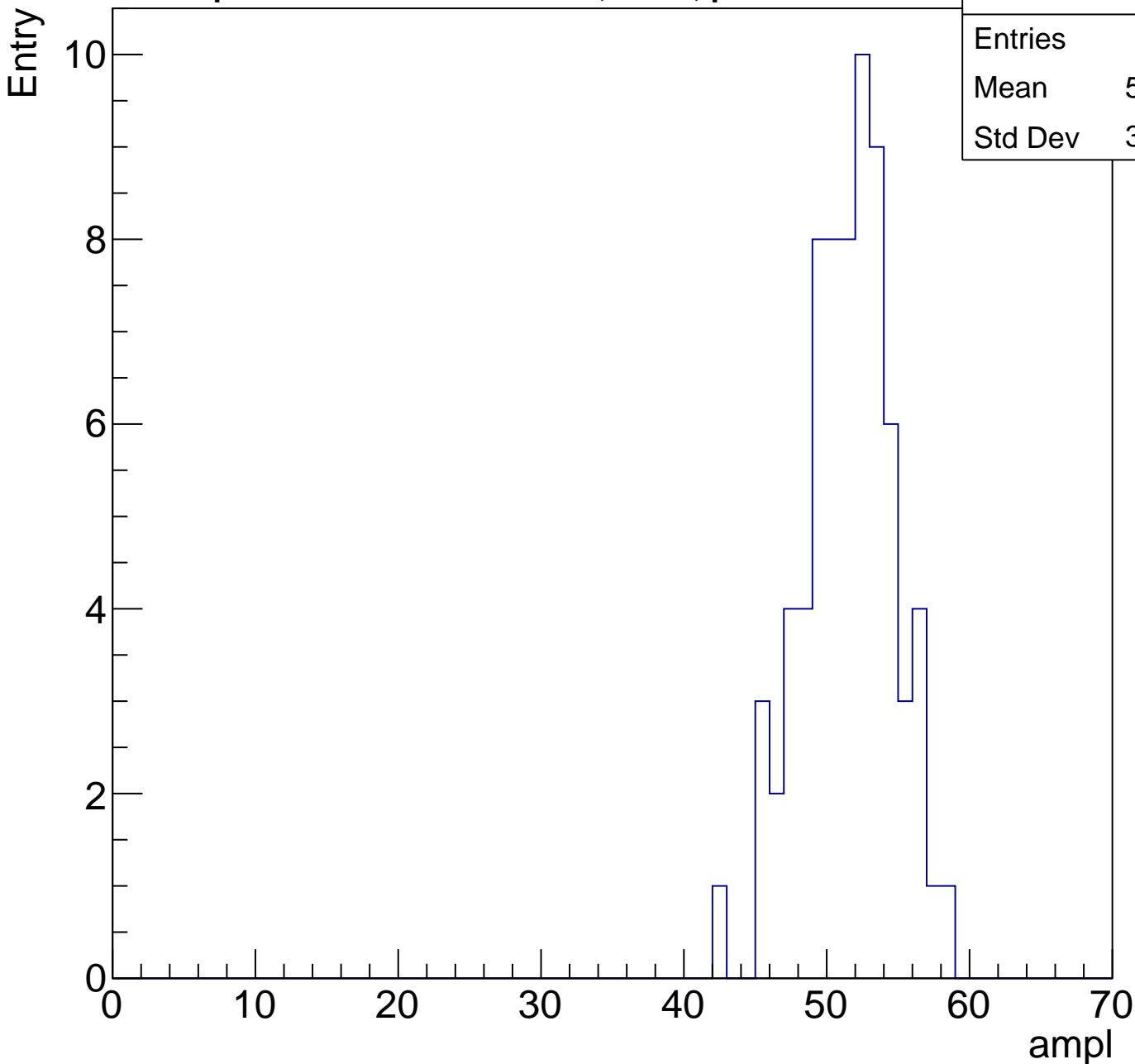
Gaus mean : 44.7712
Gaus Width: 2.7899



B0L002S, U5-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

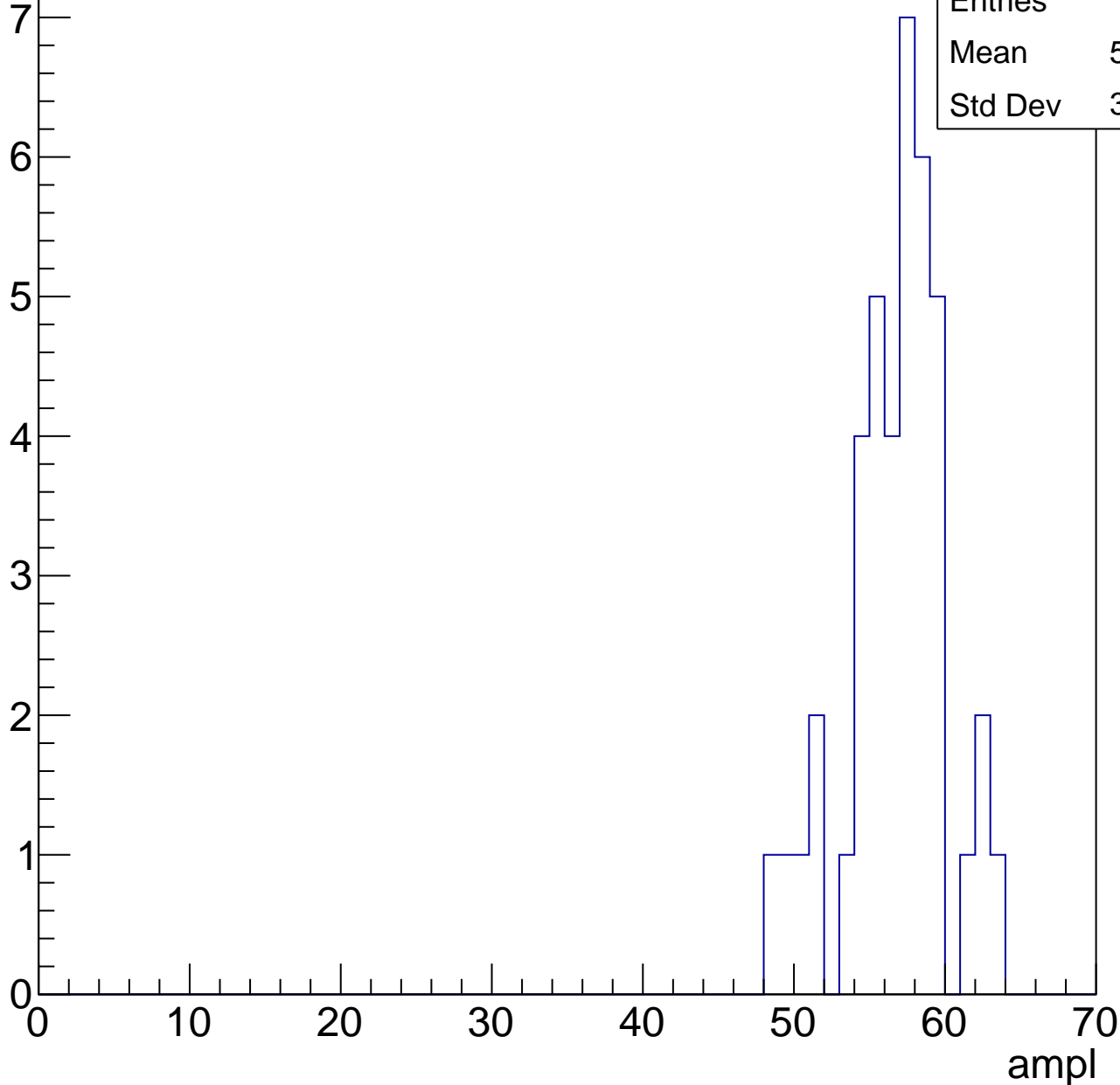
Entries	72
Mean	51.03
Std Dev	3.145



B0L002S, U5-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

48

Mean

59.1

Std Dev

8.87

Entries	48
Mean	59.1
Std Dev	8.87

B0L002S, U5-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch18, adc0

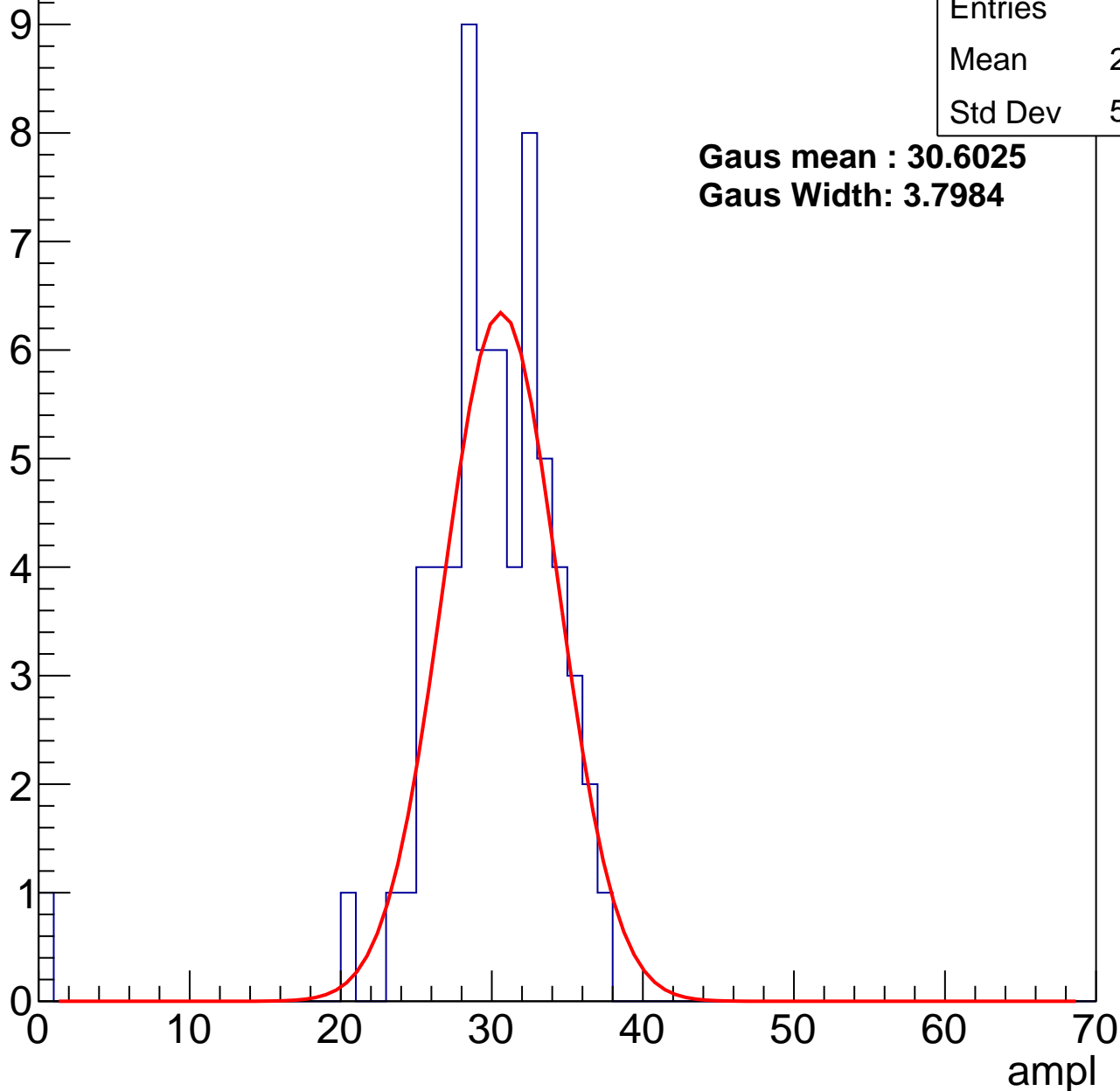
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.38
Std Dev	5.067

Gaus mean : 30.6025

Gaus Width: 3.7984



B0L002S, U5-ch18, adc1

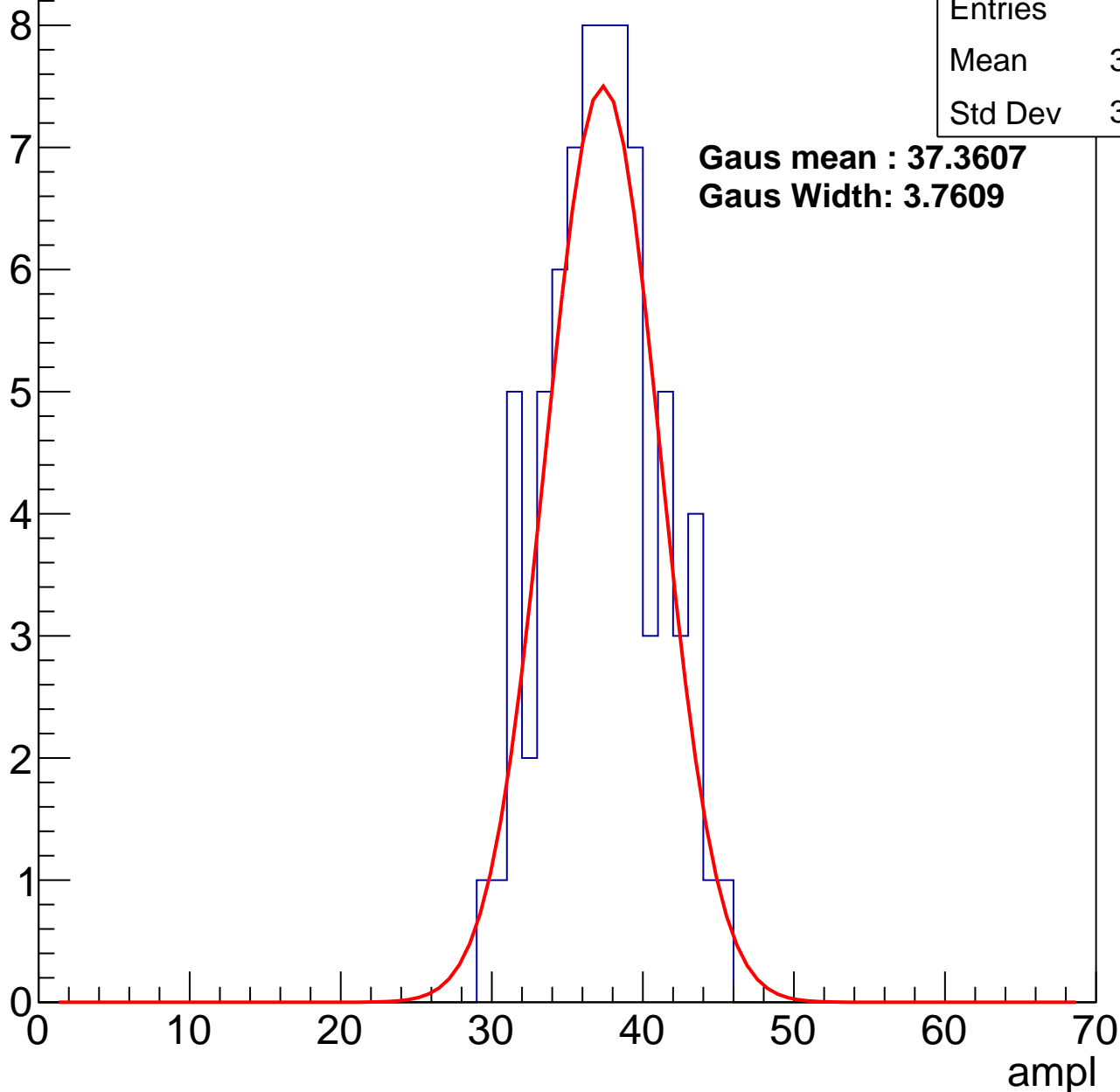
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	36.87
Std Dev	3.627

Gaus mean : 37.3607

Gaus Width: 3.7609

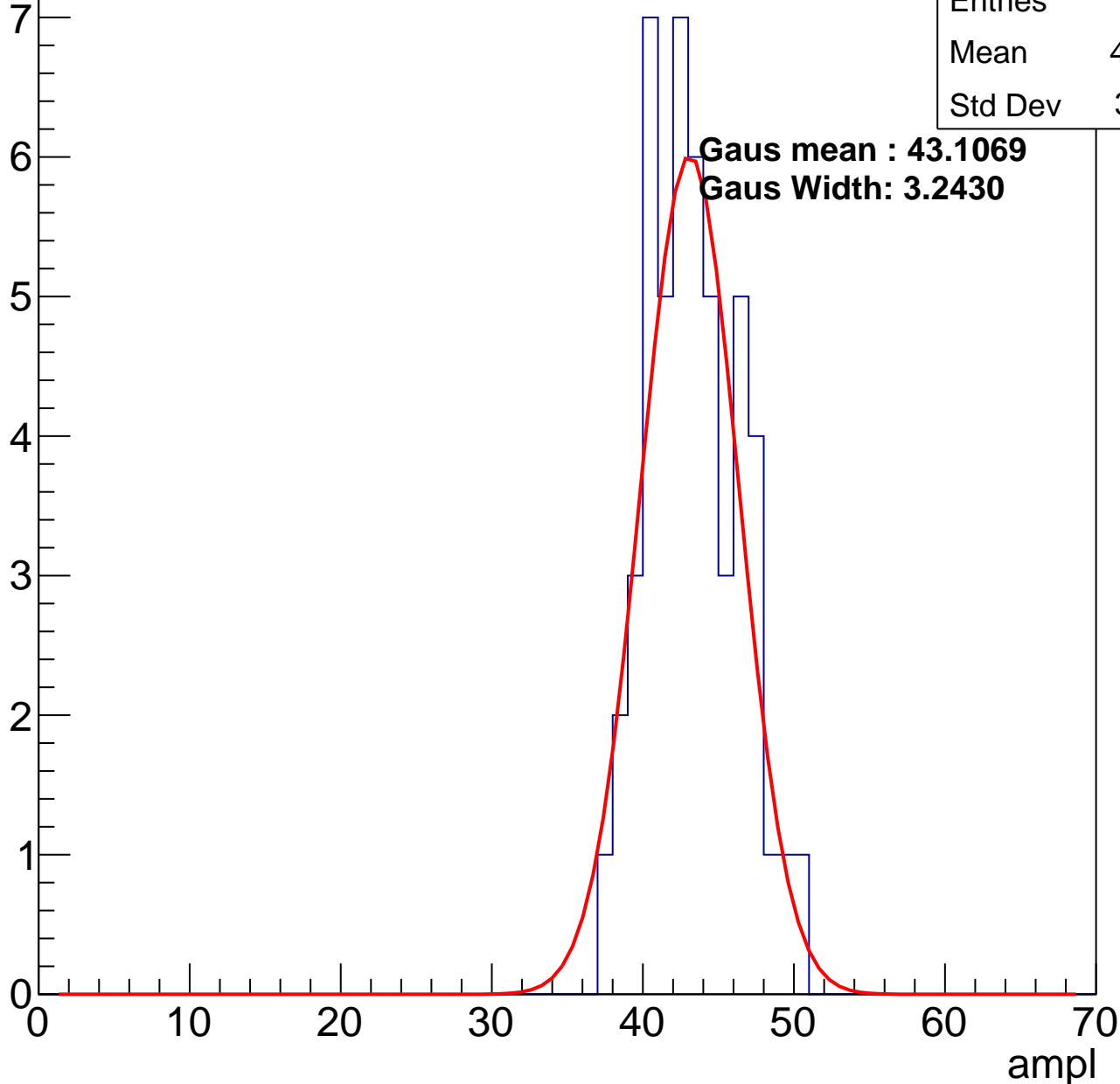


B0L002S, U5-ch18, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	42.88
Std Dev	3.001

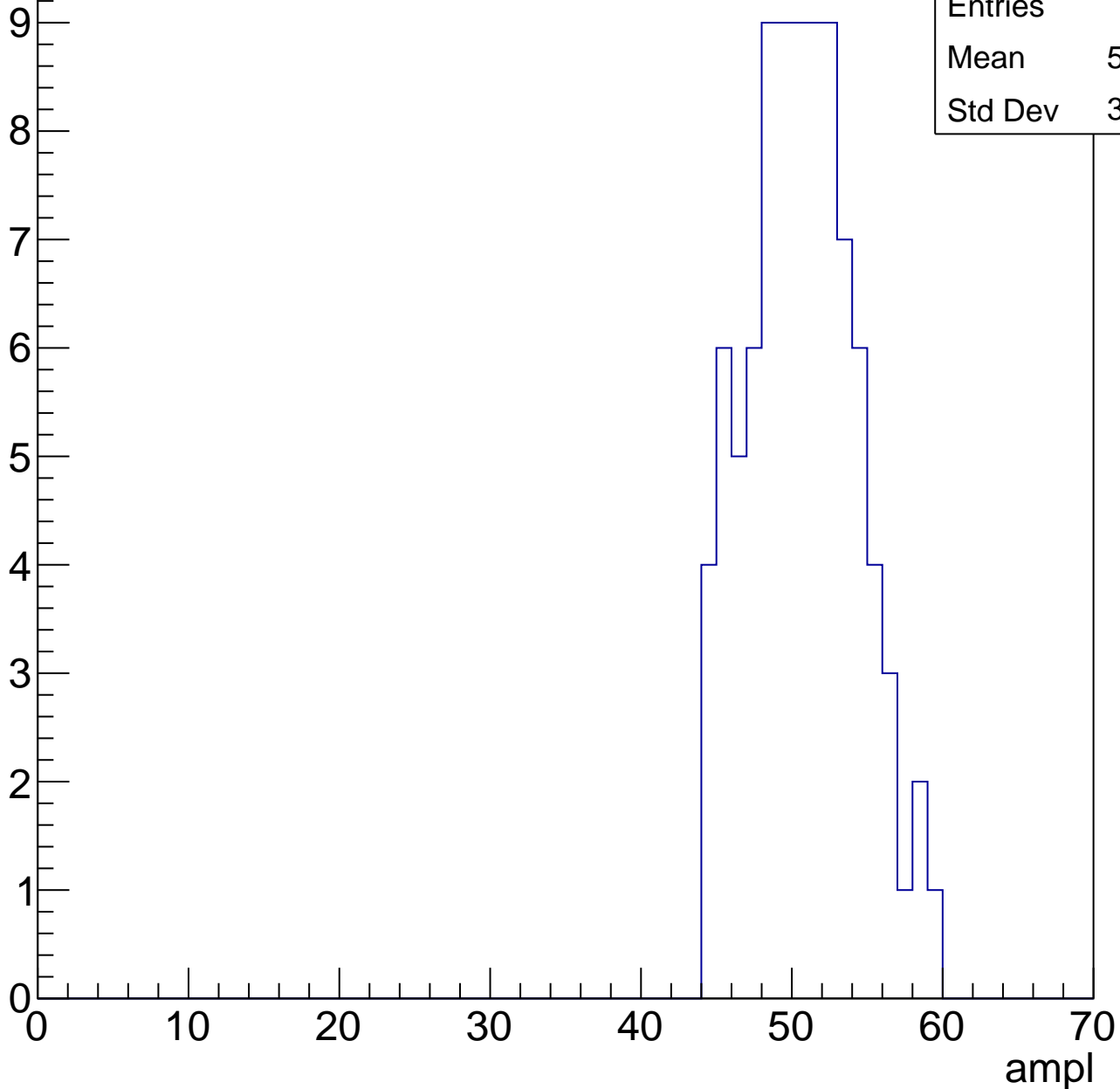


B0L002S, U5-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

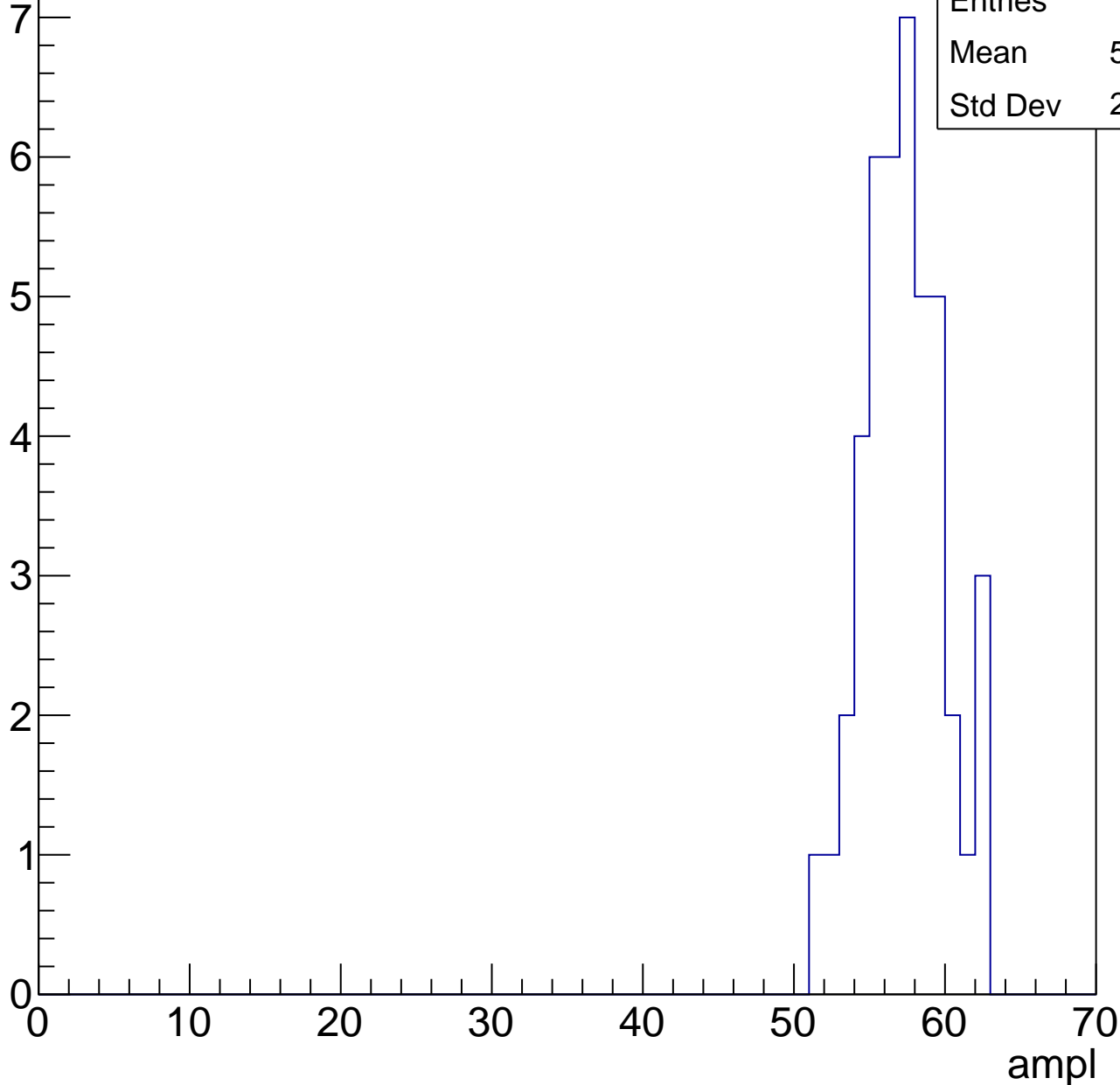
Entries	90
Mean	50.26
Std Dev	3.555



B0L002S, U5-ch18, adc4

calib_packv5_042523_0143.root, FC#8, port C1

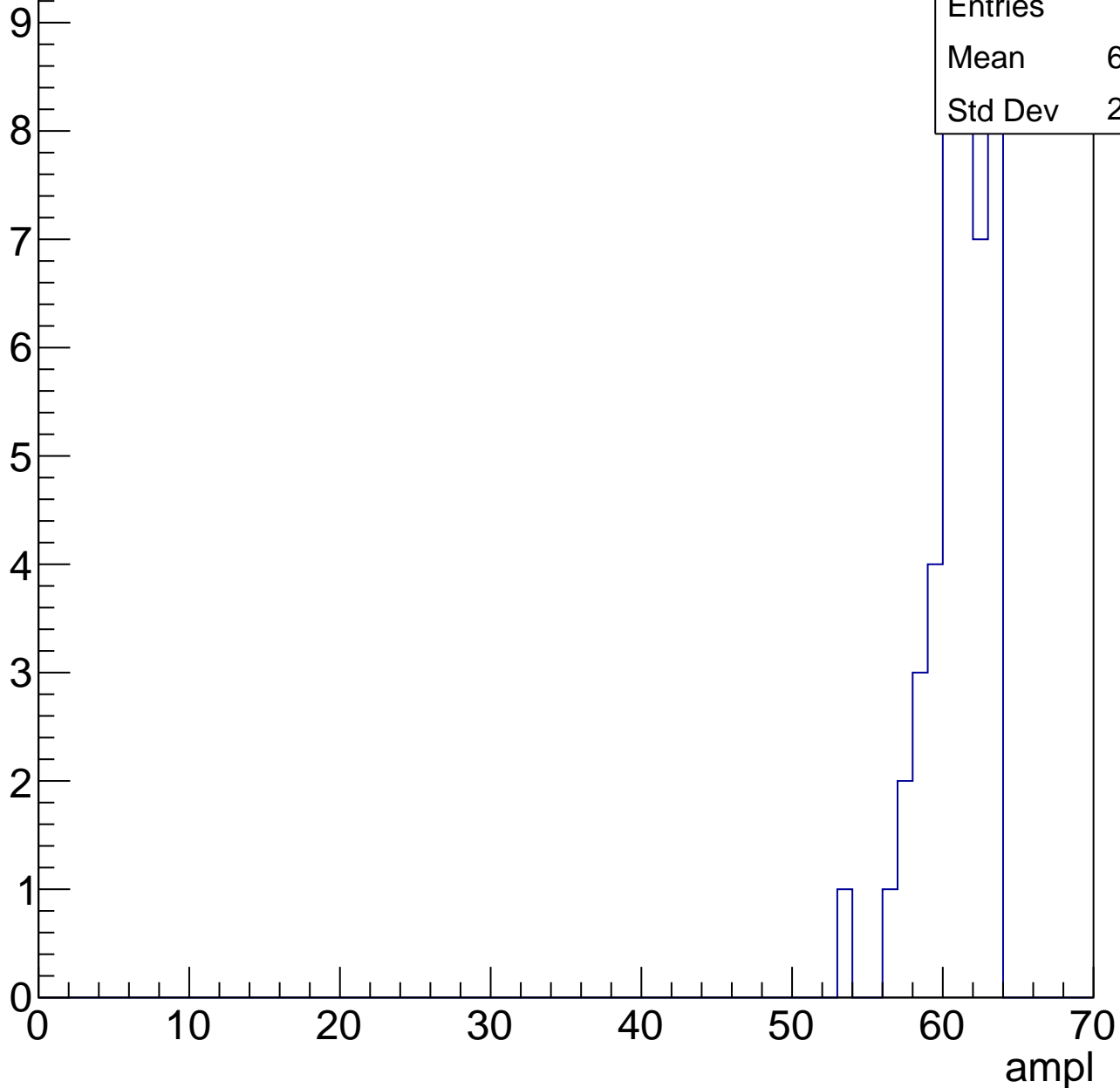
Entry



B0L002S, U5-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	44
Mean	60.52
Std Dev	2.158

B0L002S, U5-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch19, adc0

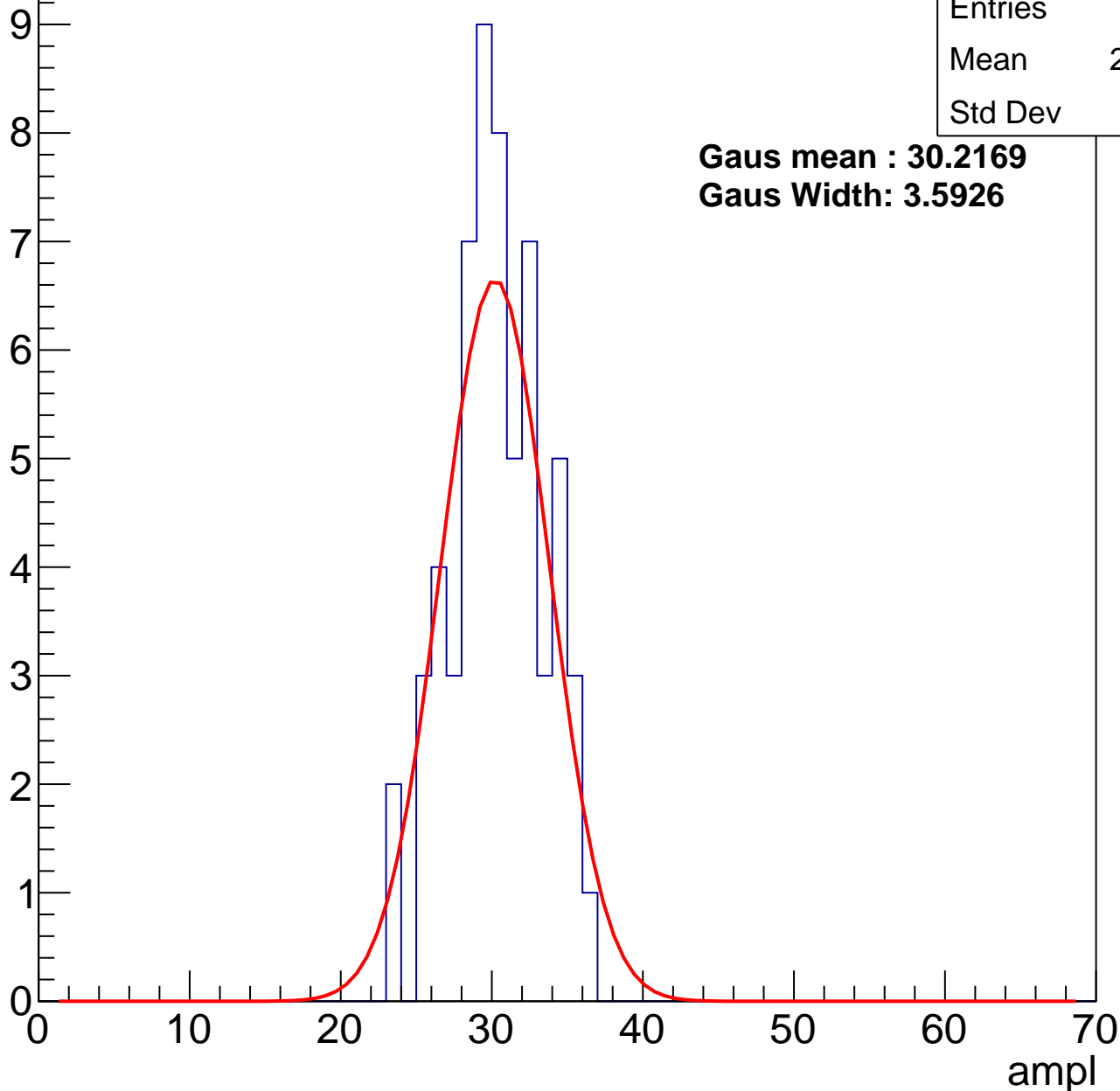
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.87
Std Dev	3.03

Gaus mean : 30.2169

Gaus Width: 3.5926



B0L002S, U5-ch19, adc1

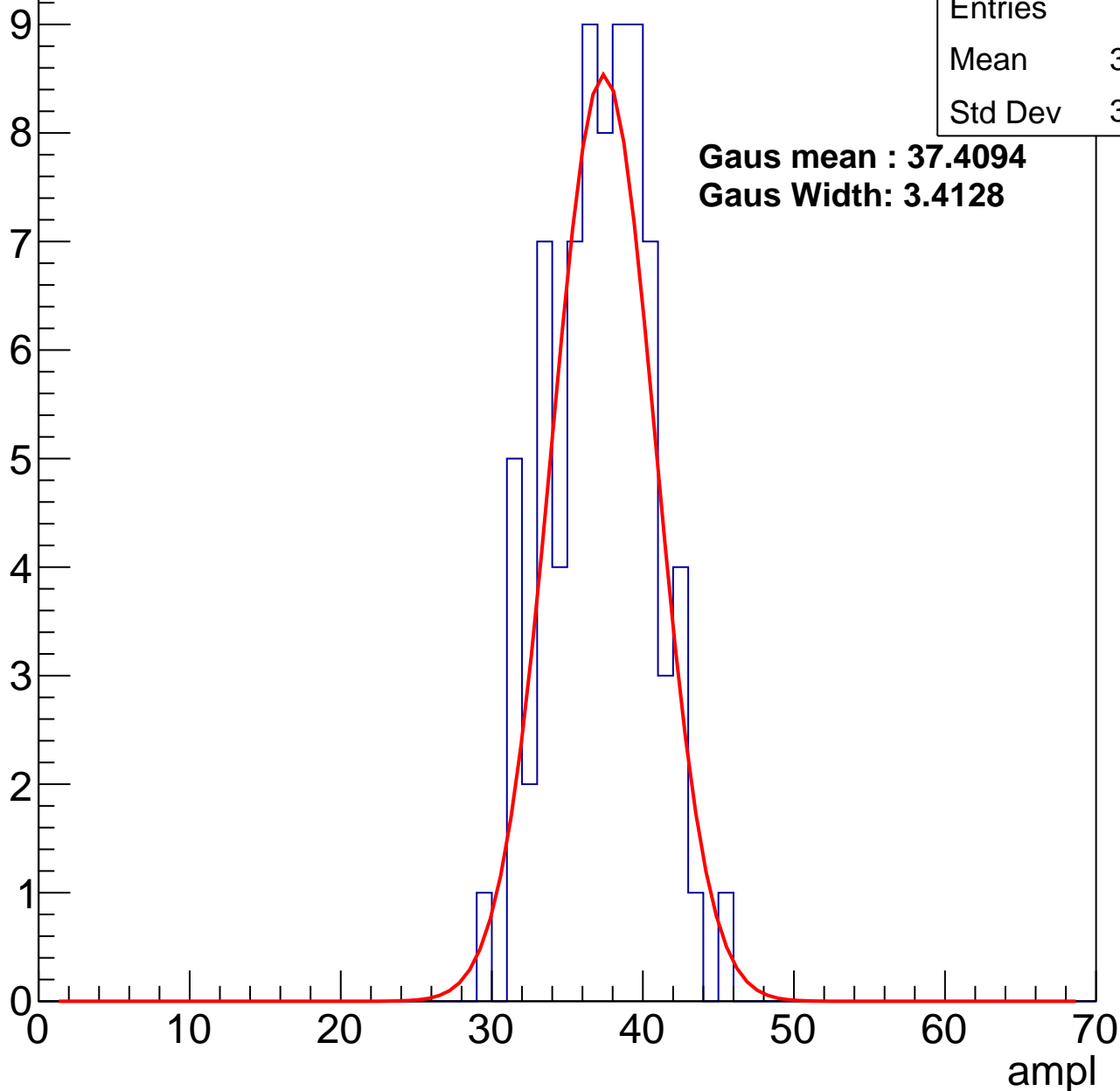
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.78
Std Dev	3.286

Gaus mean : 37.4094

Gaus Width: 3.4128



B0L002S, U5-ch19, adc2

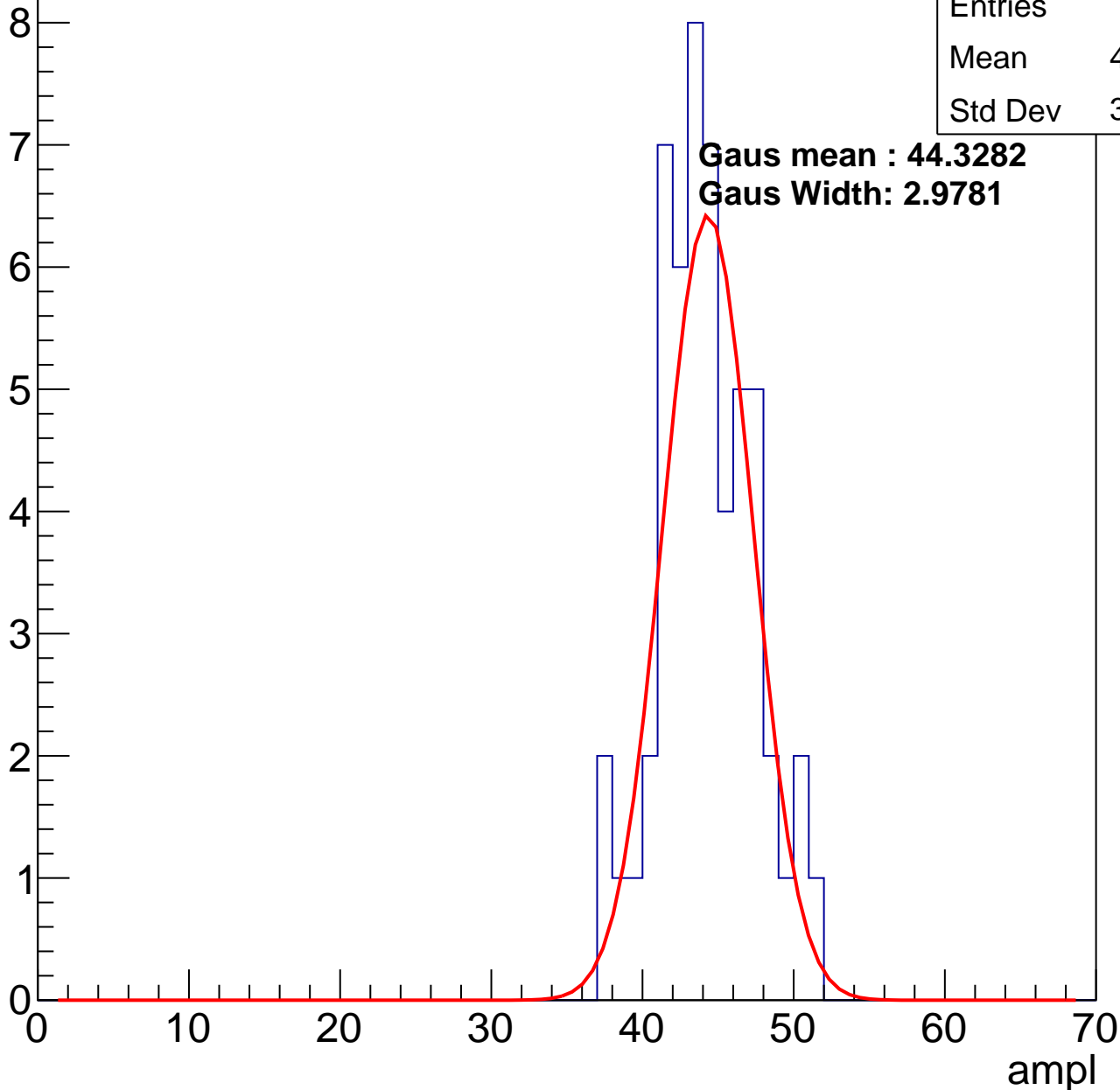
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.76
Std Dev	3.132

Gaus mean : 44.3282

Gaus Width: 2.9781

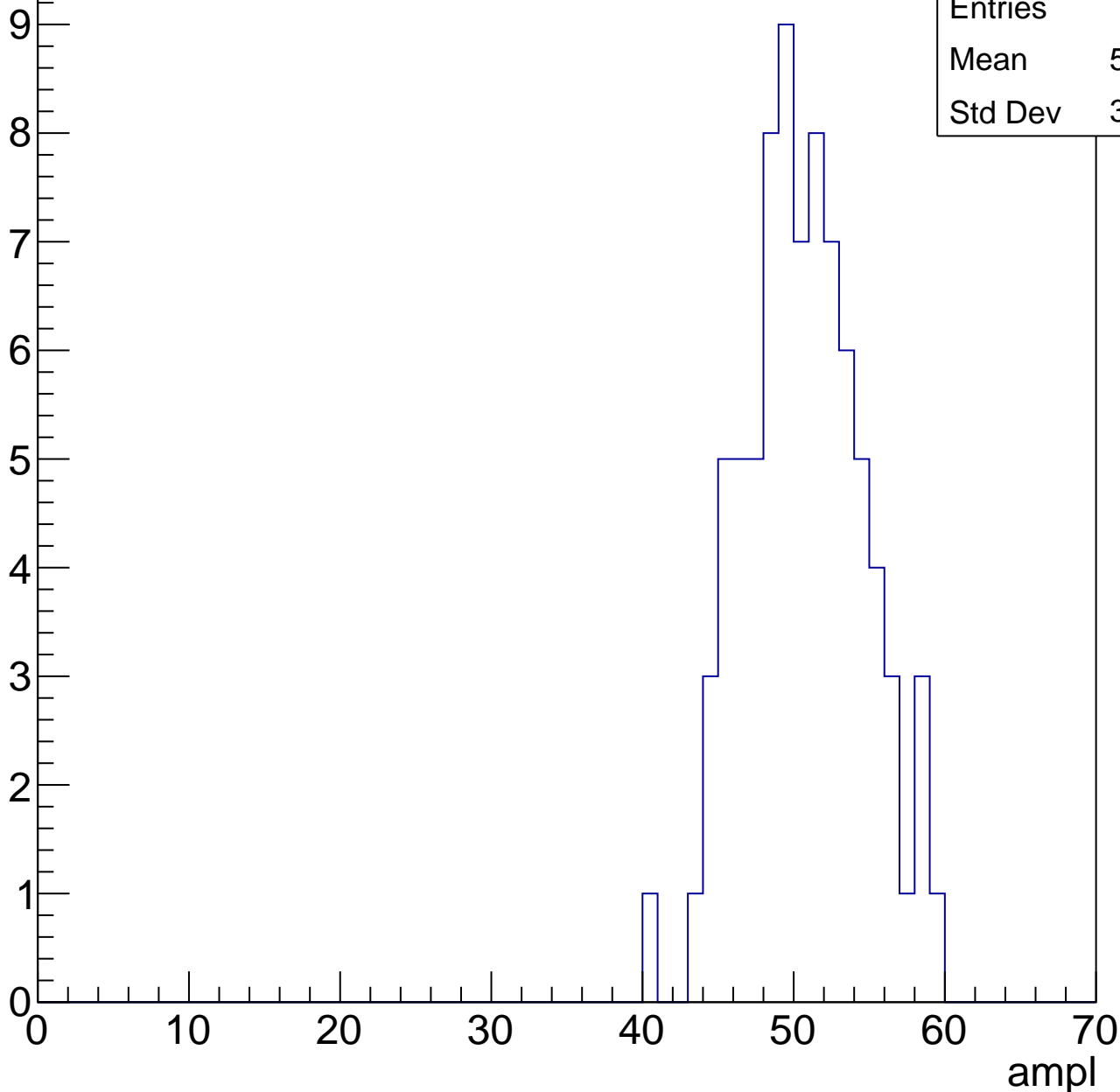


B0L002S, U5-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	50.22
Std Dev	3.895

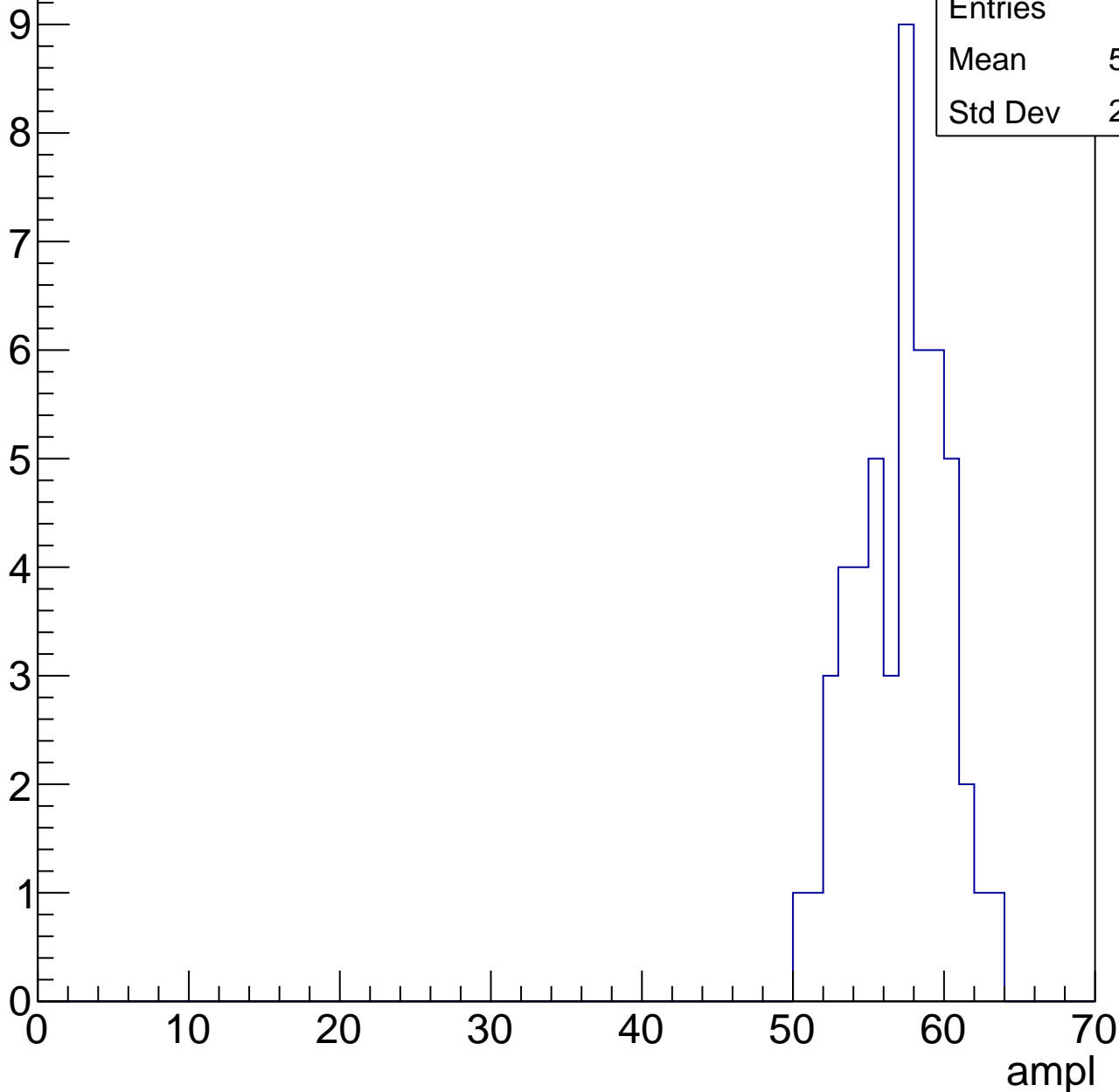


B0L002S, U5-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.67
Std Dev	2.955

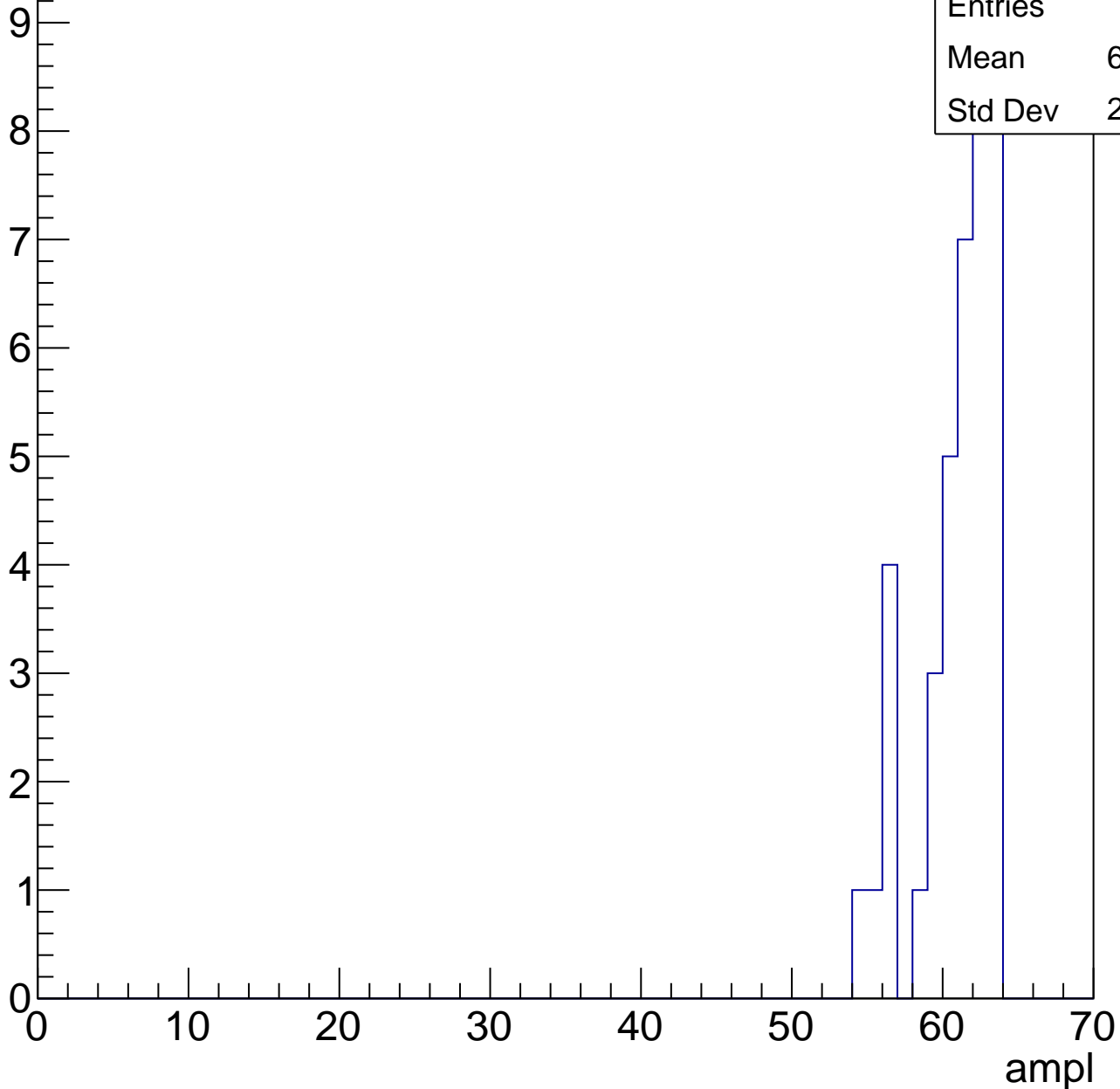


B0L002S, U5-ch19, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	60.44
Std Dev	2.479



B0L002S, U5-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch20, adc0

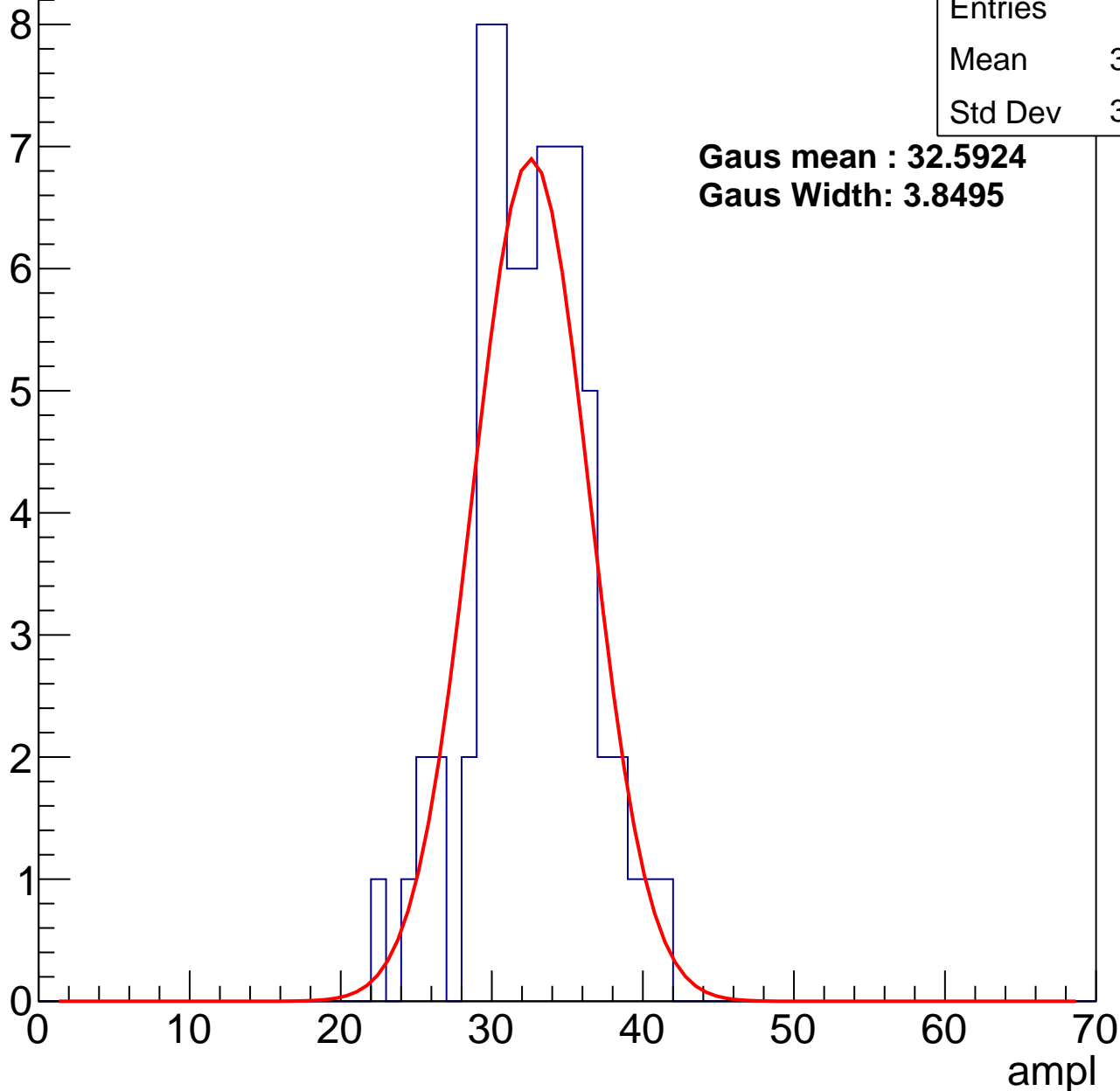
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	32.14
Std Dev	3.747

Gaus mean : 32.5924

Gaus Width: 3.8495



B0L002S, U5-ch20, adc1

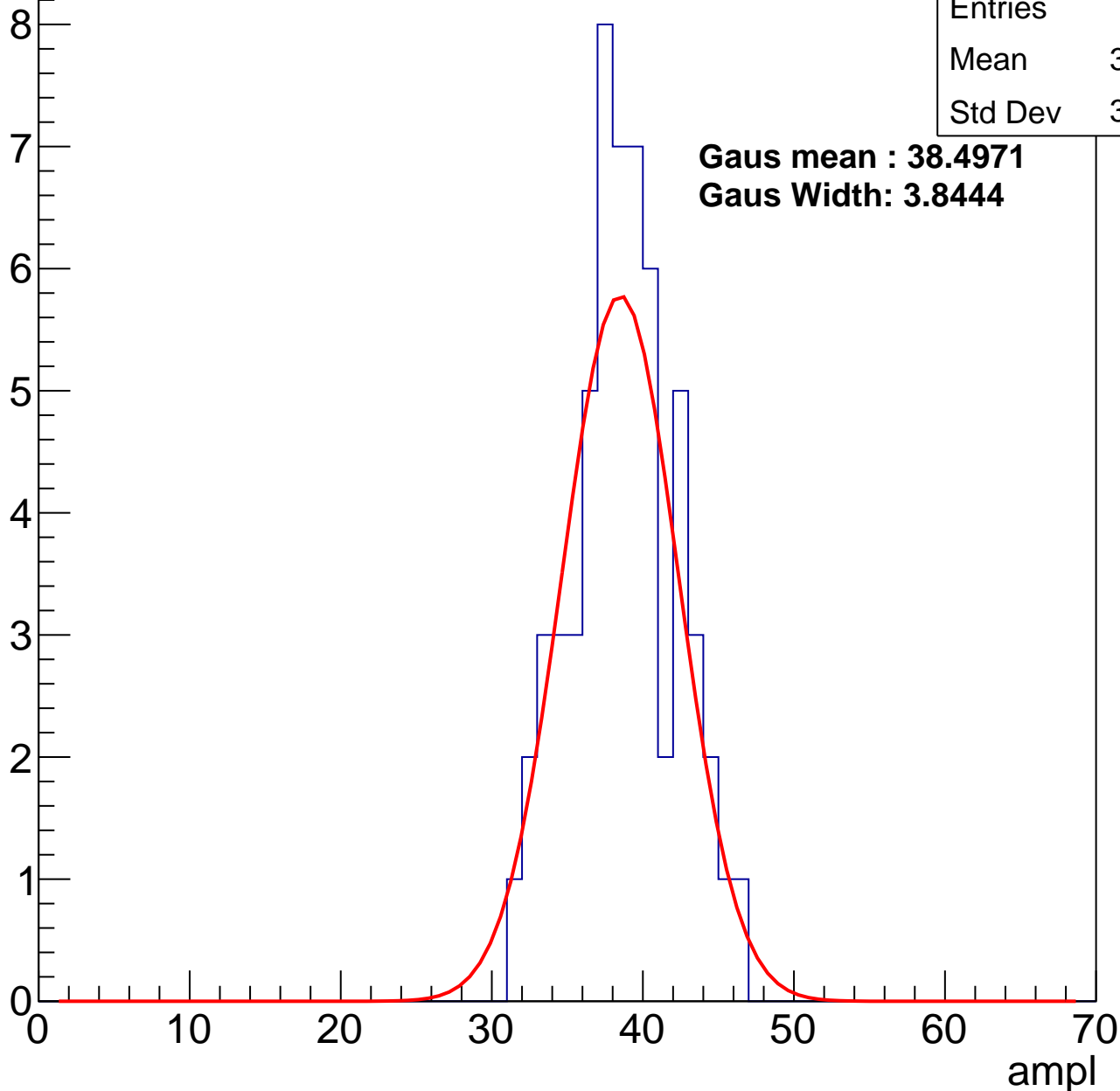
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38.24
Std Dev	3.407

Gaus mean : 38.4971

Gaus Width: 3.8444



B0L002S, U5-ch20, adc2

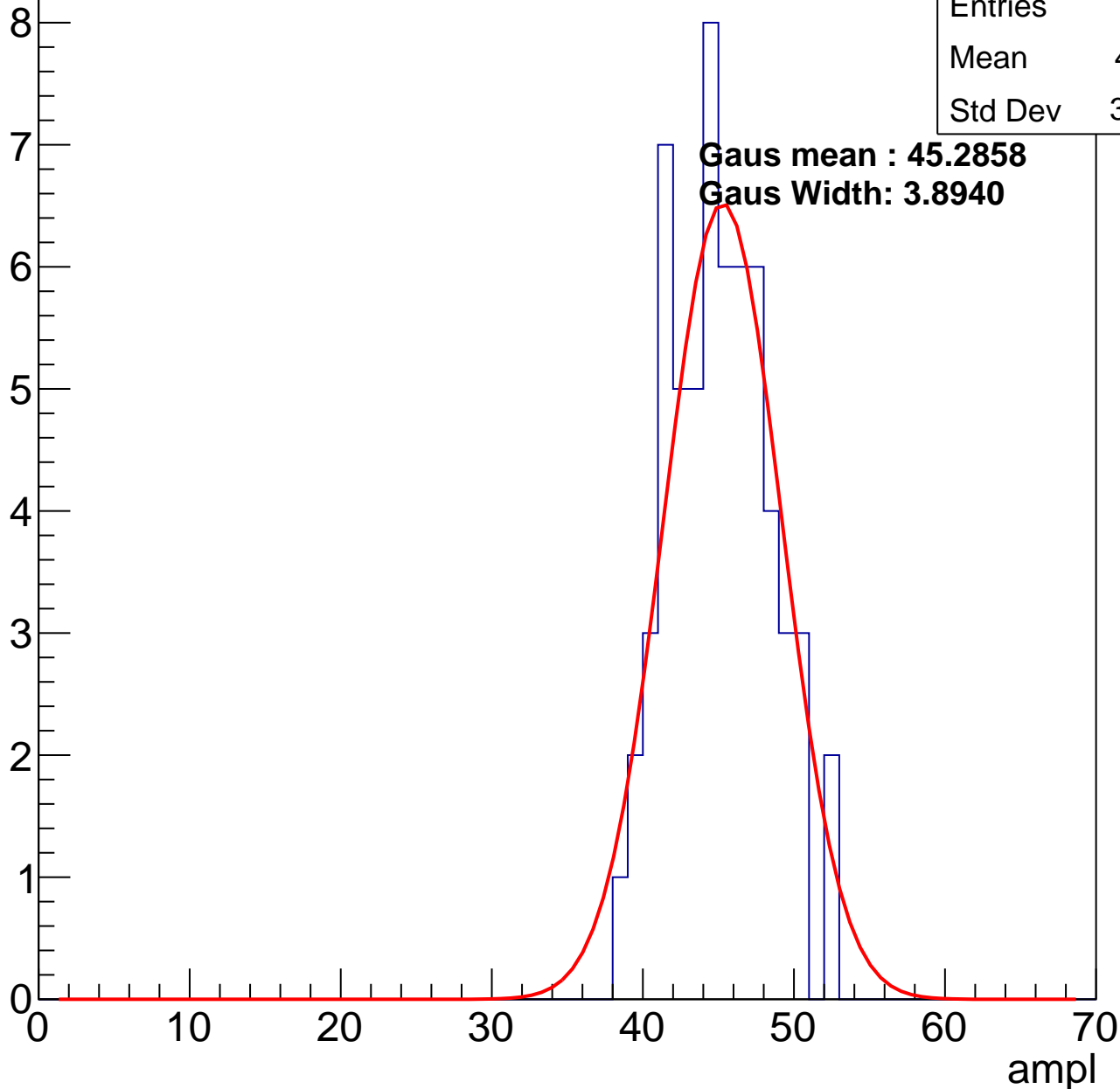
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.61
Std Dev	3.286

Gaus mean : 45.2858

Gaus Width: 3.8940

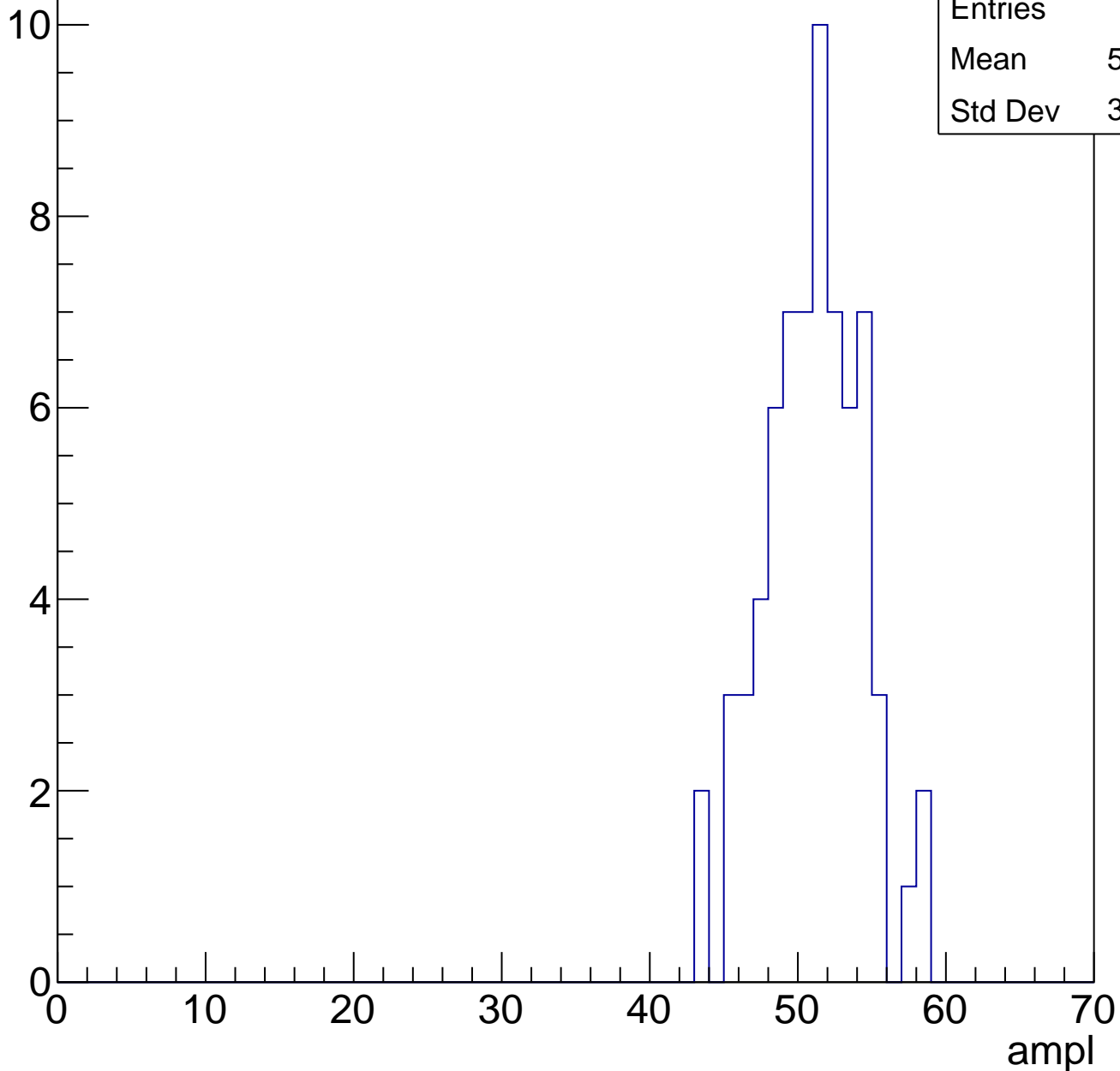


B0L002S, U5-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	50.53
Std Dev	3.265

Entry



B0L002S, U5-ch20, adc4

calib_packv5_042523_0143.root, FC#8, port C1

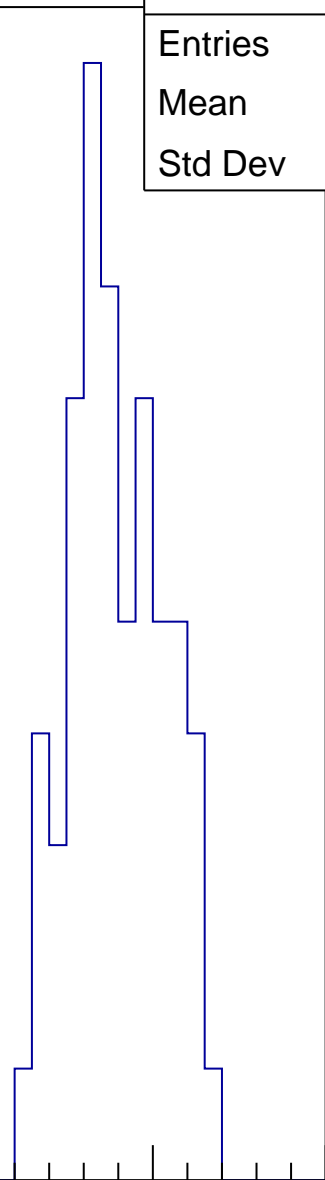
Entries	60
Mean	57.43
Std Dev	2.698

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

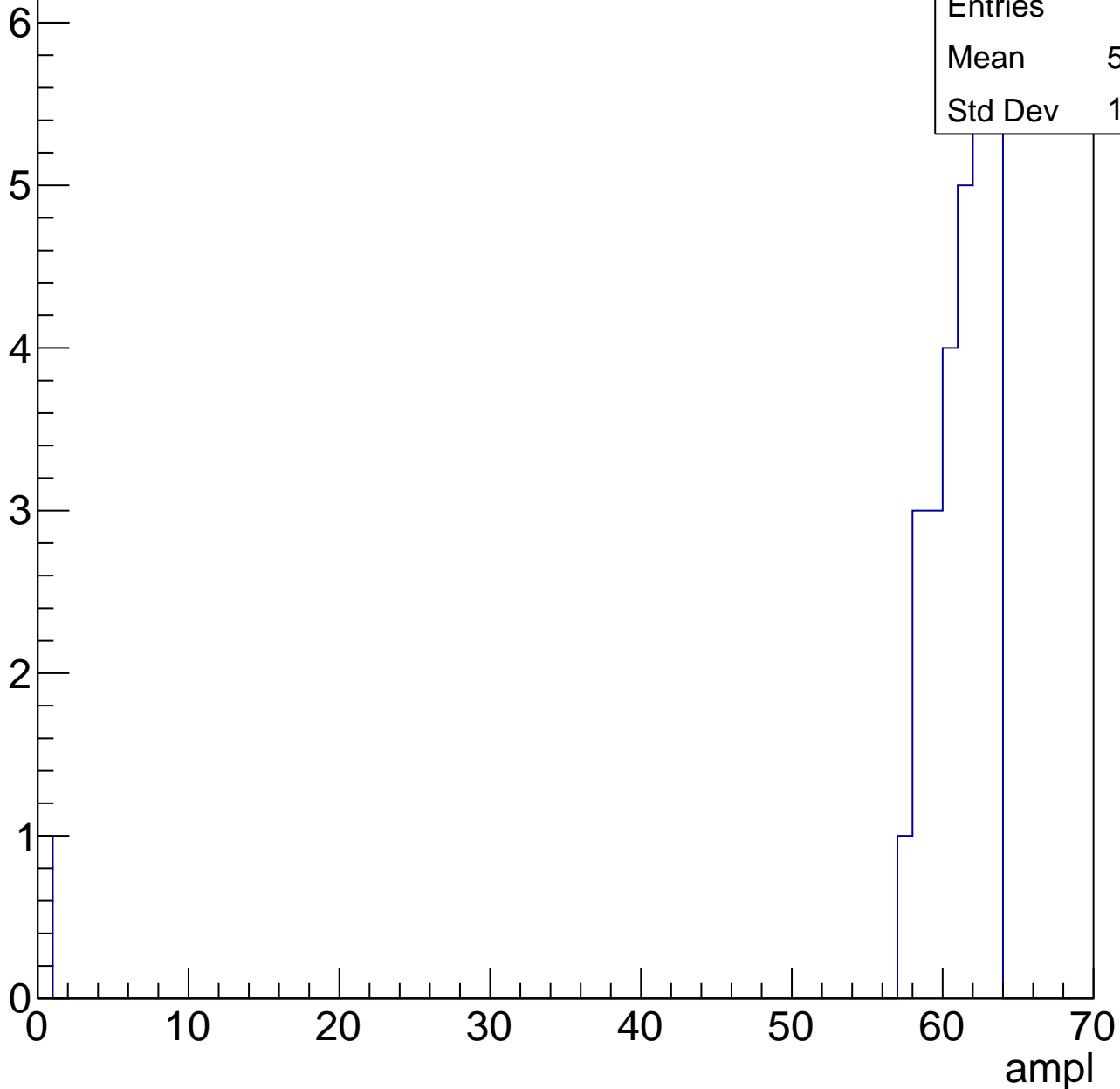


B0L002S, U5-ch20, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.72
Std Dev	11.23



B0L002S, U5-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch21, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	30.1
Std Dev	3.655

Gaus mean : 30.6166

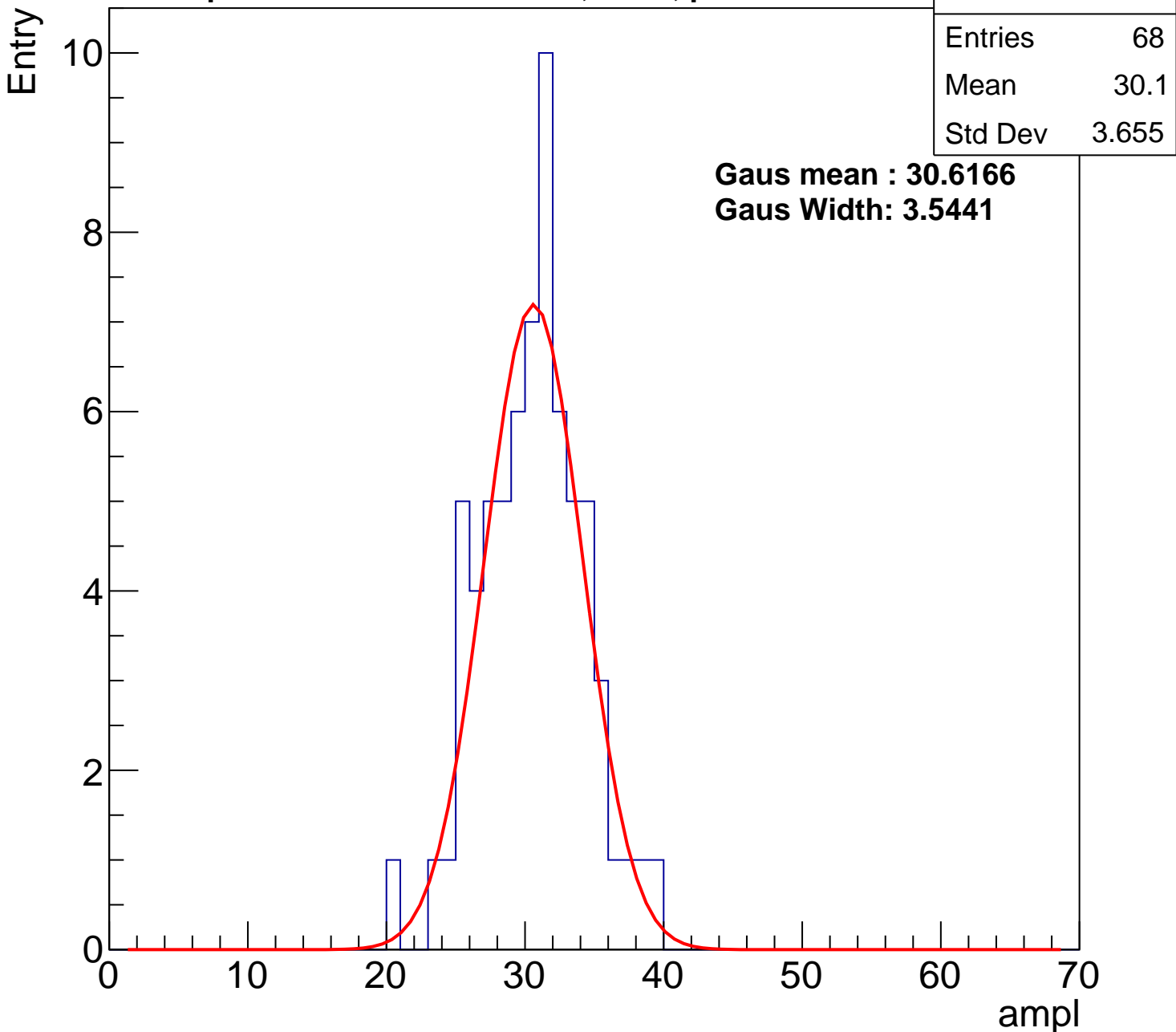
Gaus Width: 3.5441

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch21, adc1

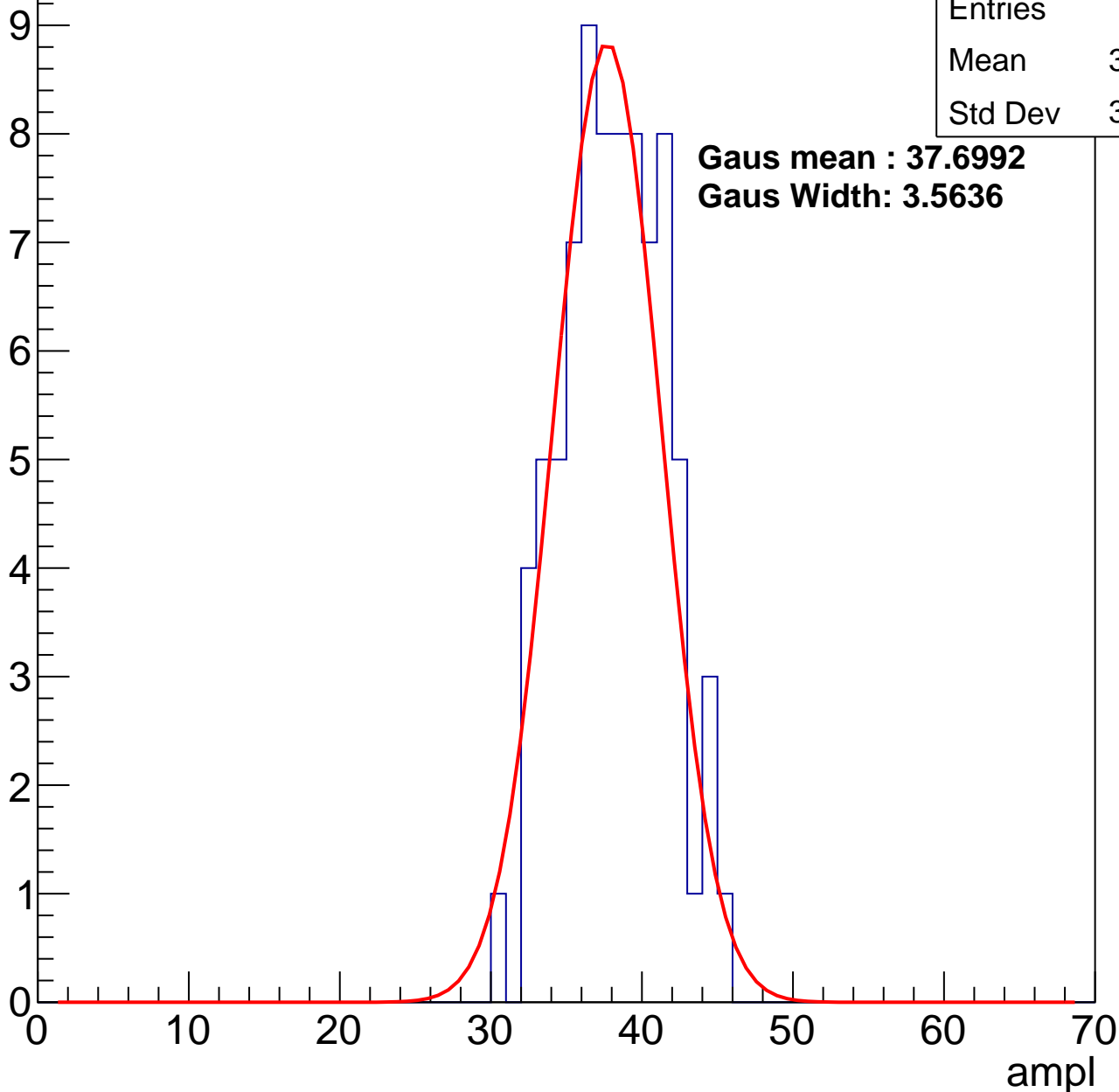
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	37.65
Std Dev	3.317

Gaus mean : 37.6992

Gaus Width: 3.5636



B0L002S, U5-ch21, adc2

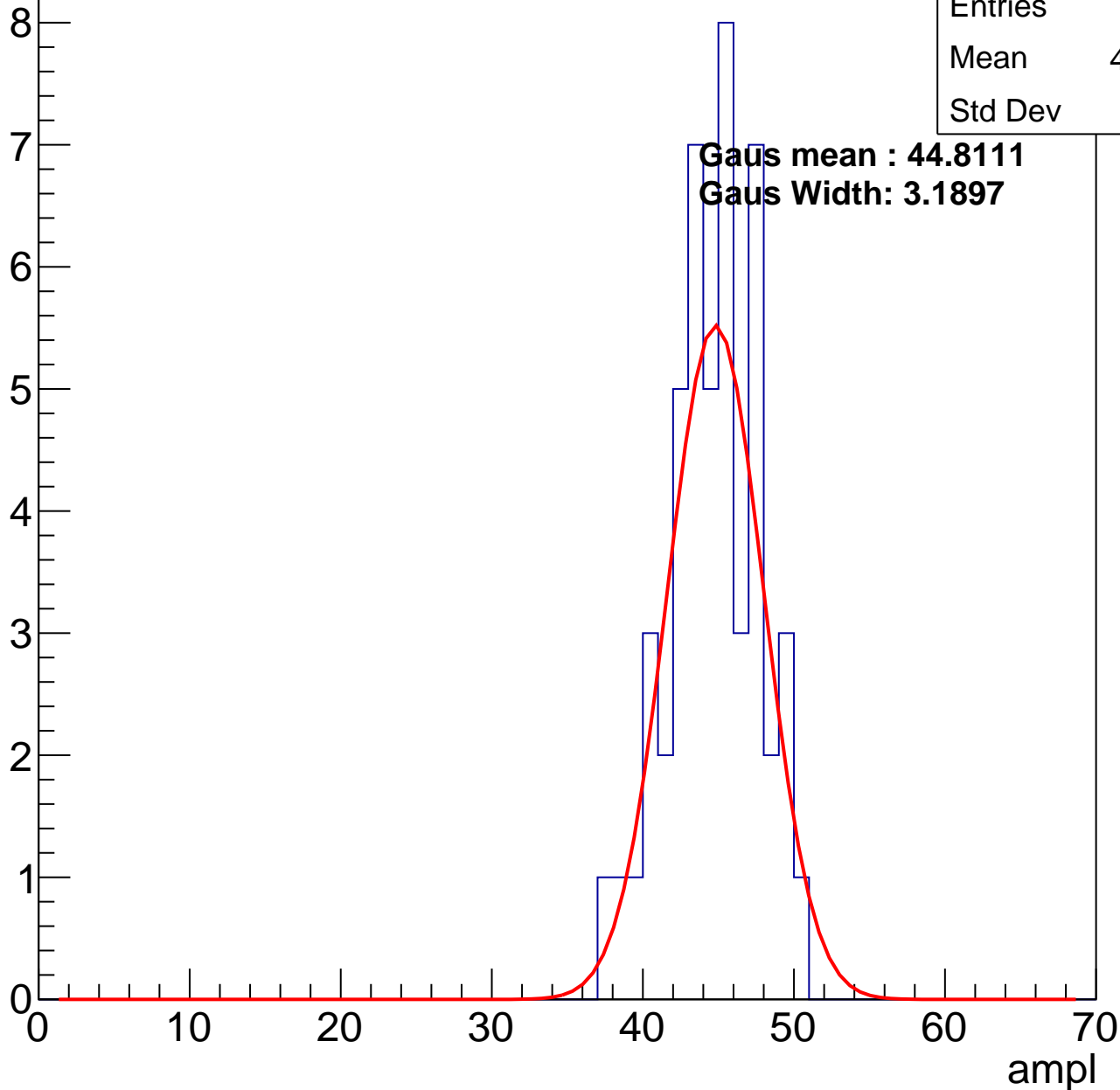
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	44.22
Std Dev	2.95

Gaus mean : 44.8111

Gaus Width: 3.1897

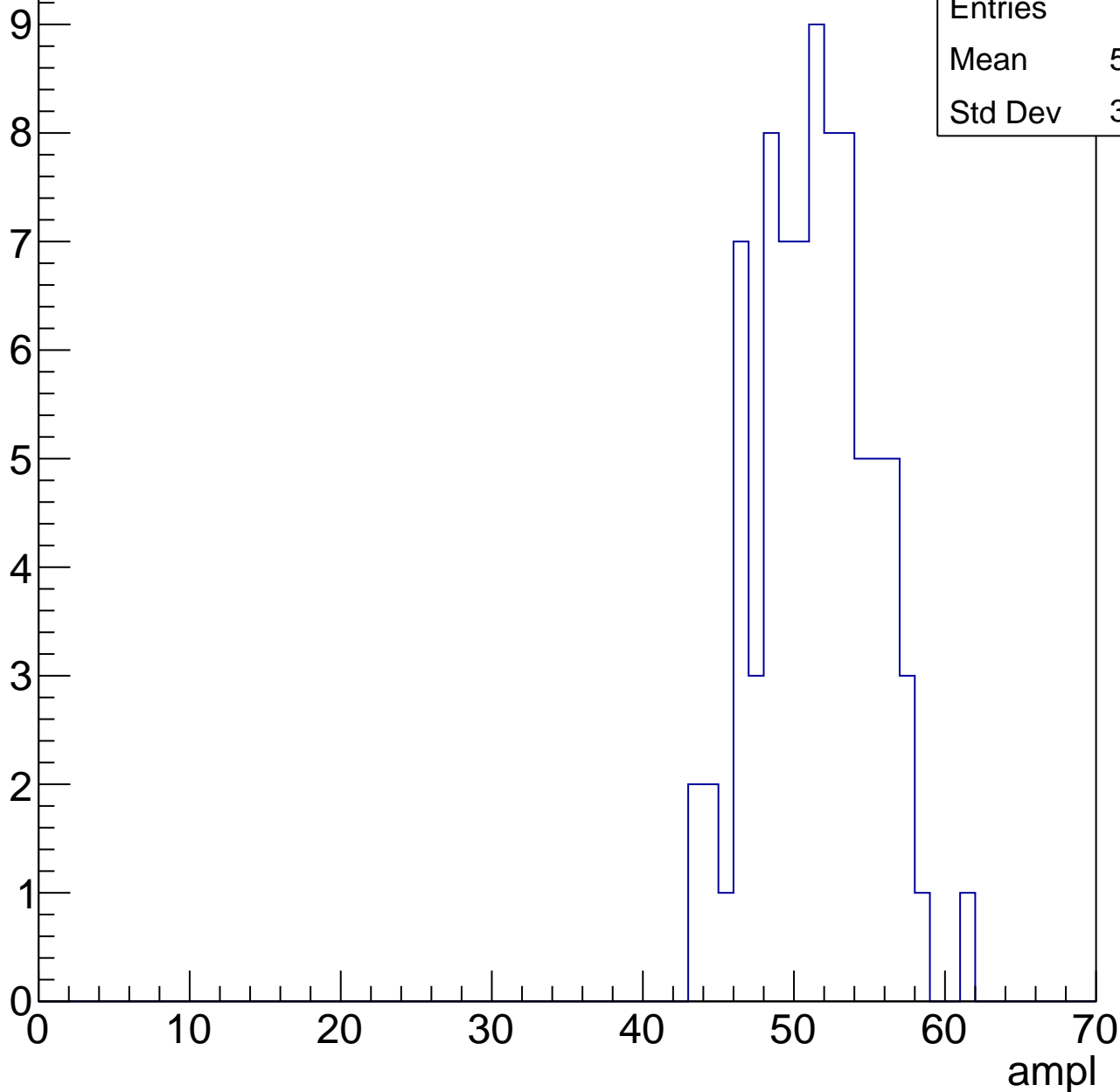


B0L002S, U5-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	50.89
Std Dev	3.725



B0L002S, U5-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	53
Mean	57.11
Std Dev	3.225

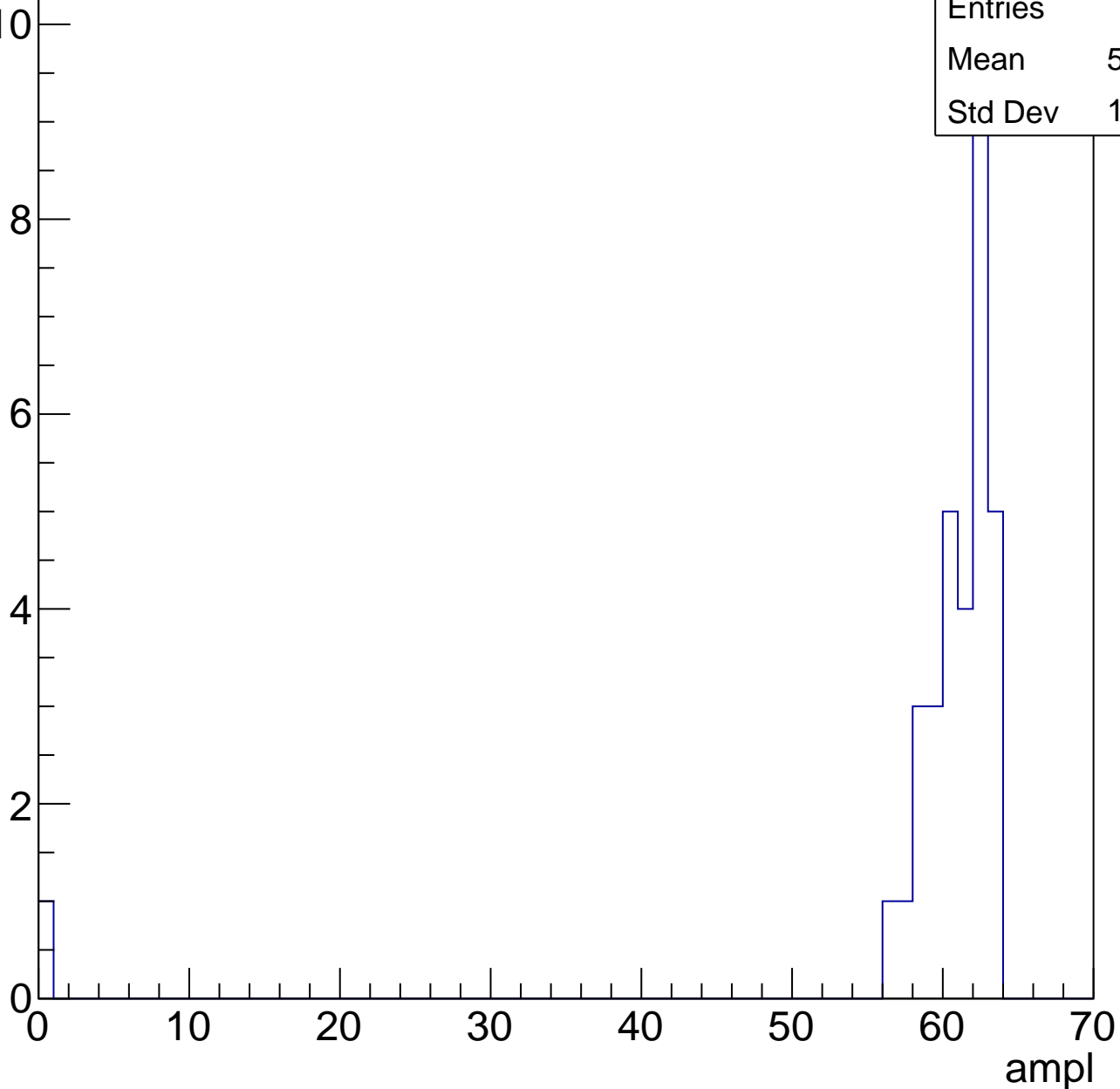
ampl

B0L002S, U5-ch21, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

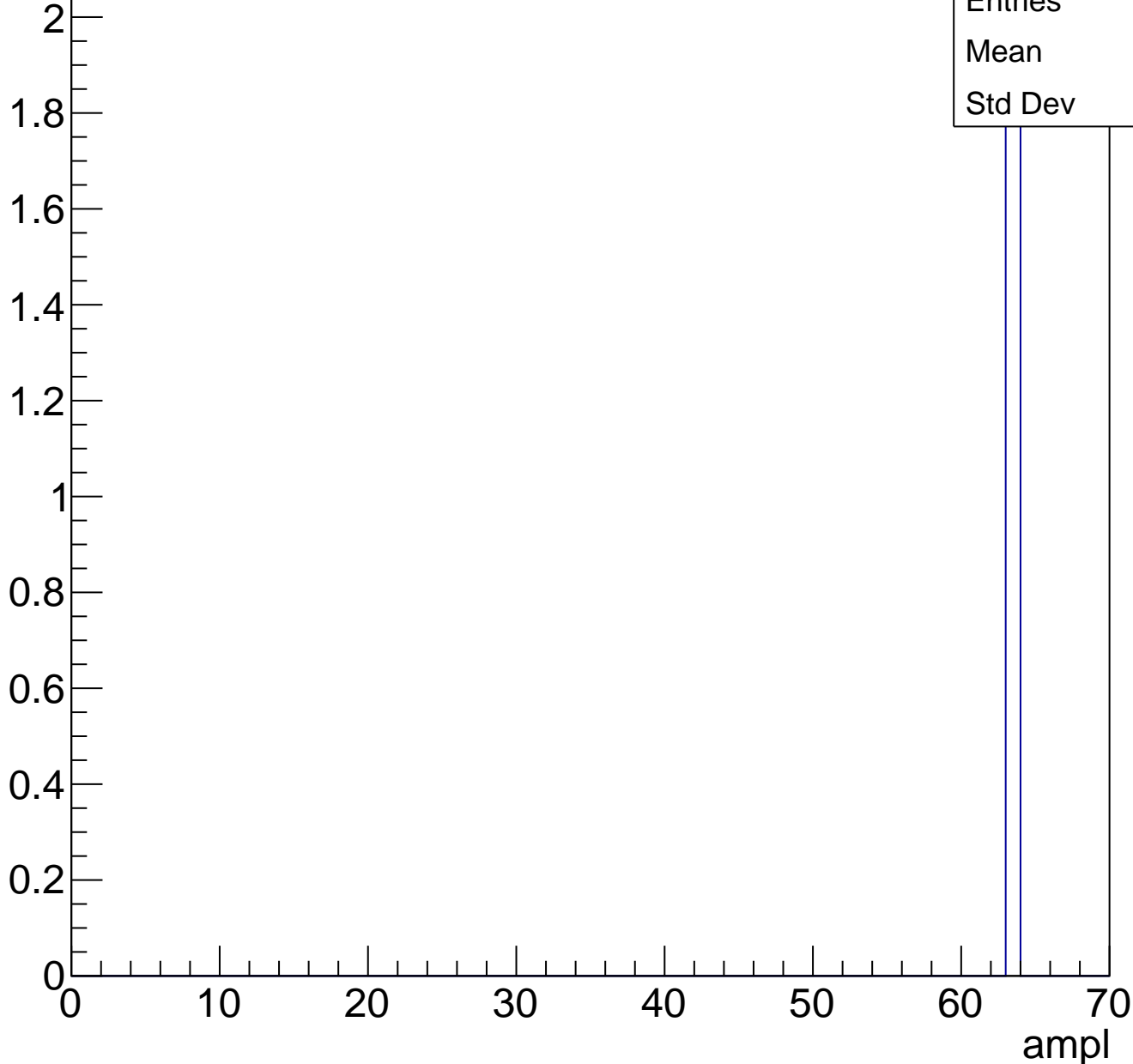
Entries	33
Mean	58.88
Std Dev	10.57



B0L002S, U5-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch22, adc0

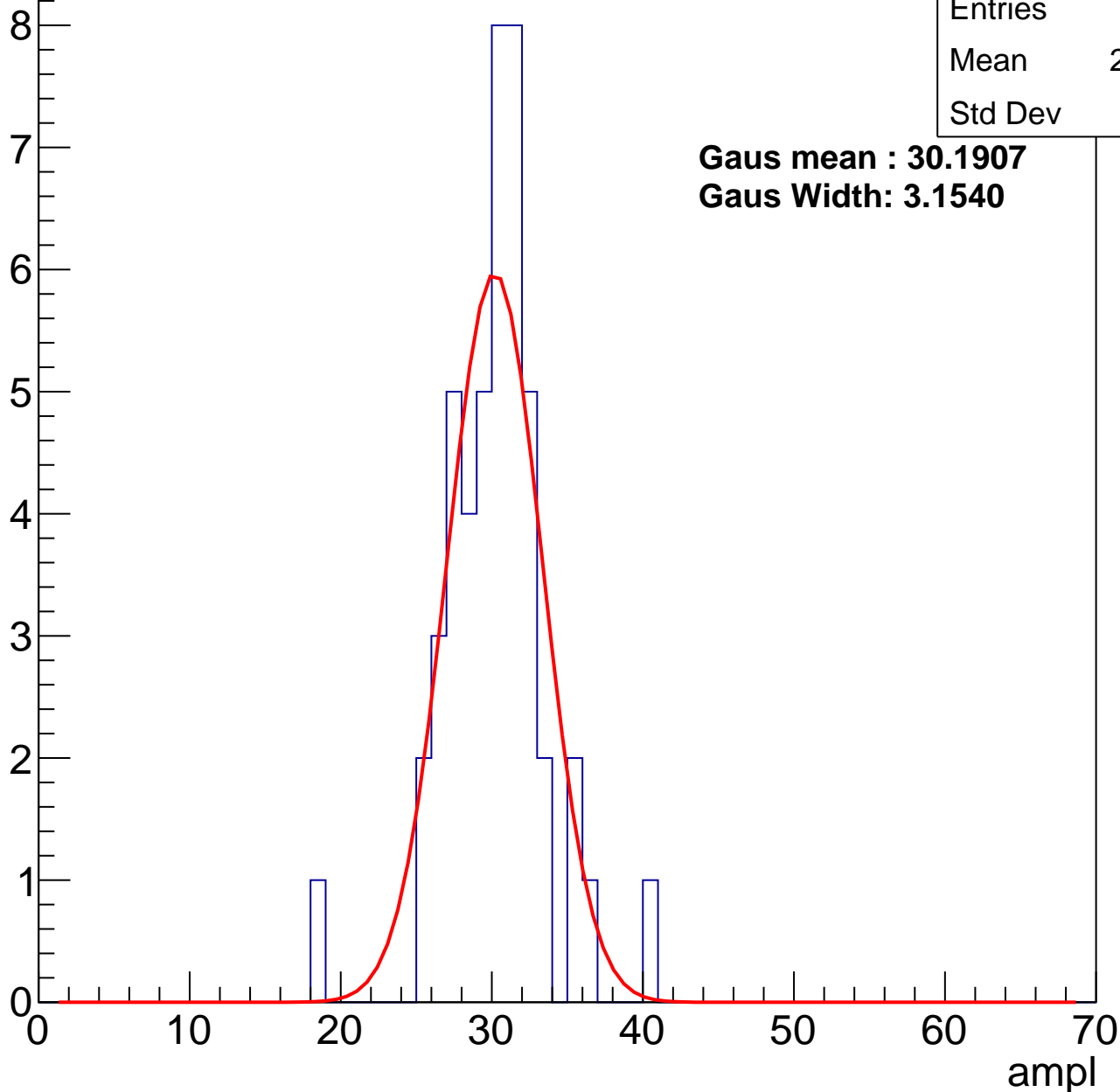
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	29.74
Std Dev	3.38

Gaus mean : 30.1907

Gaus Width: 3.1540



B0L002S, U5-ch22, adc1

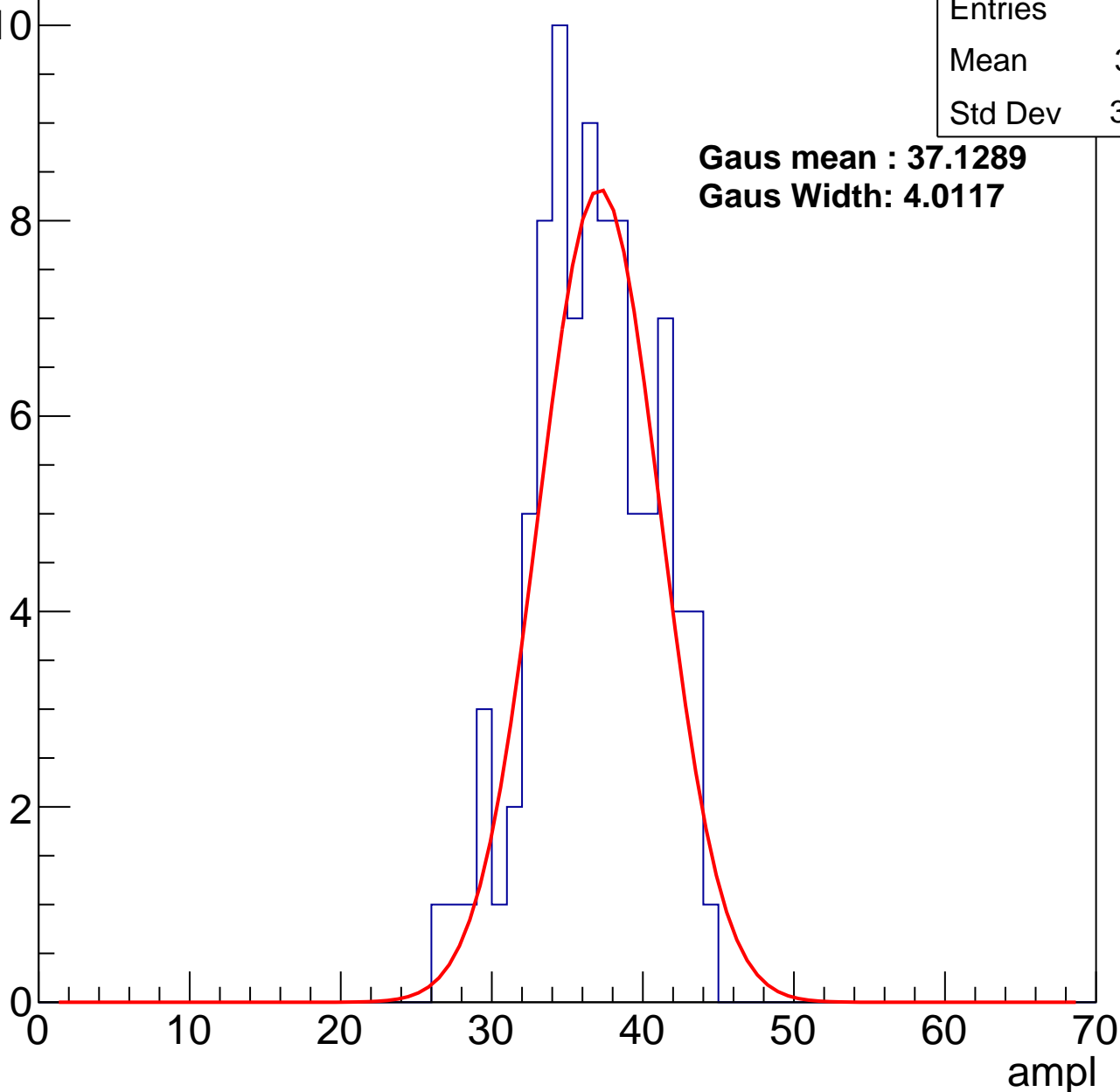
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	90
Mean	36.21
Std Dev	3.968

Gaus mean : 37.1289

Gaus Width: 4.0117



B0L002S, U5-ch22, adc2

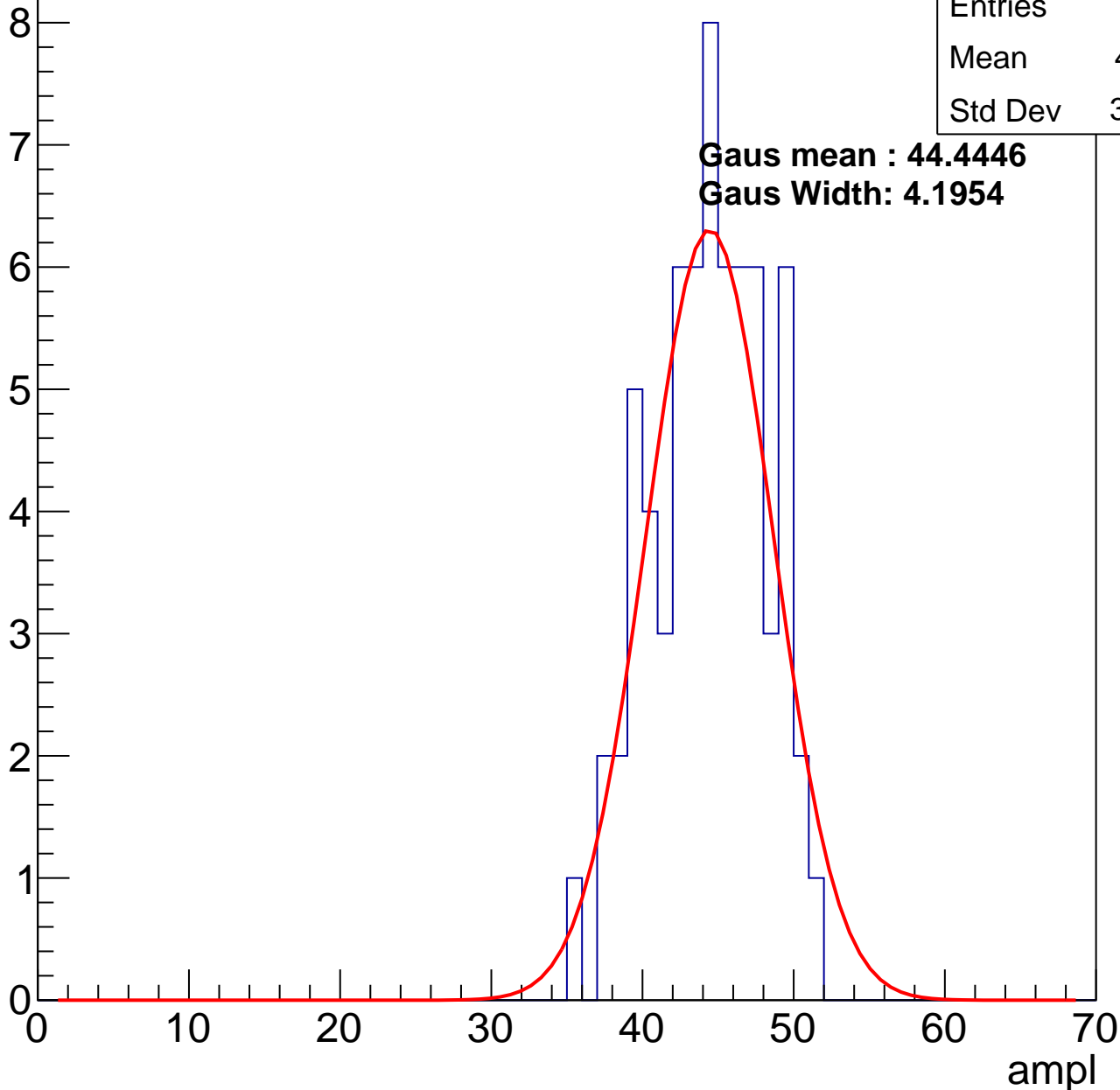
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.91
Std Dev	3.664

Gaus mean : 44.4446

Gaus Width: 4.1954

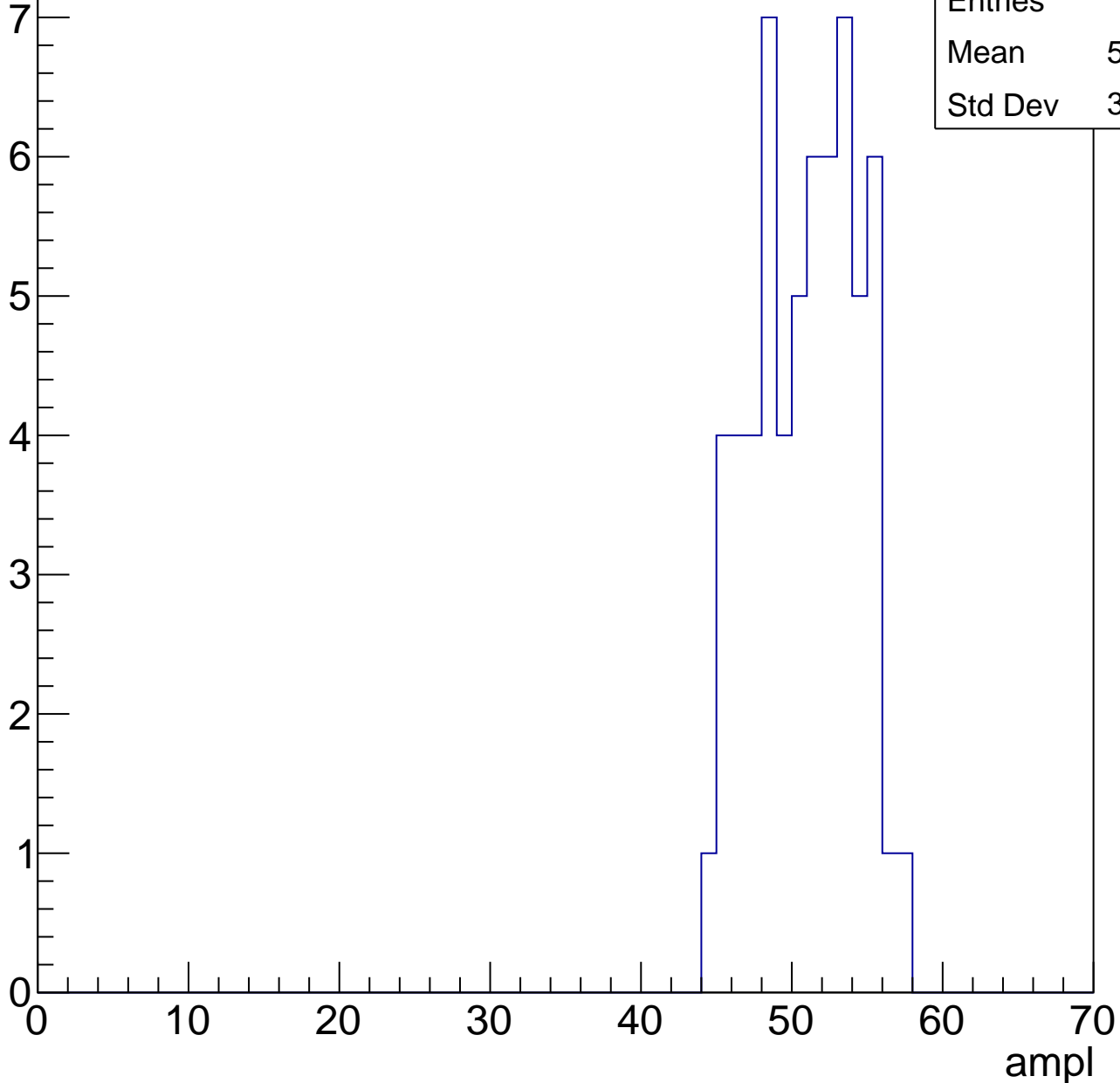


B0L002S, U5-ch22, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	50.49
Std Dev	3.292

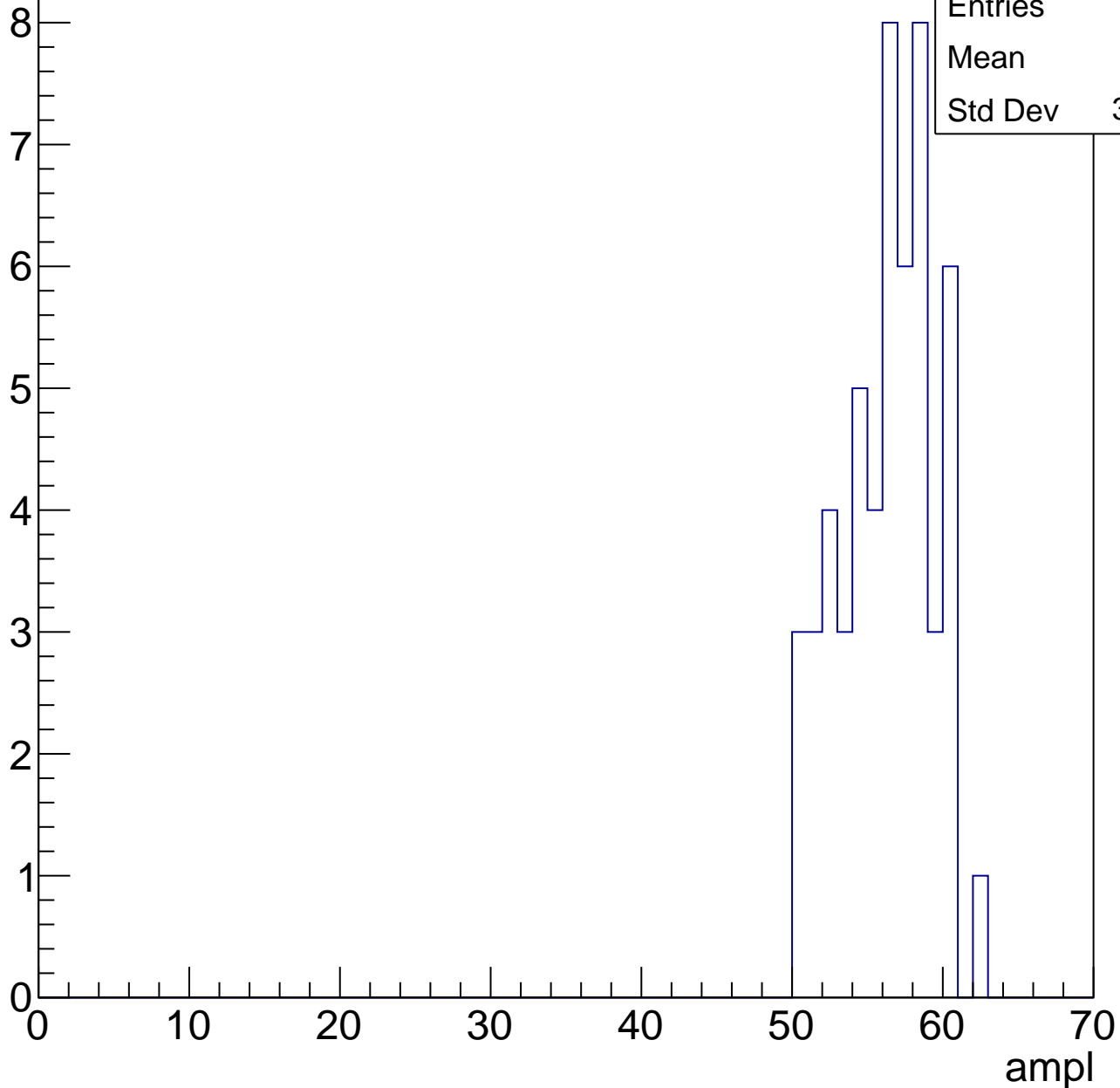


B0L002S, U5-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

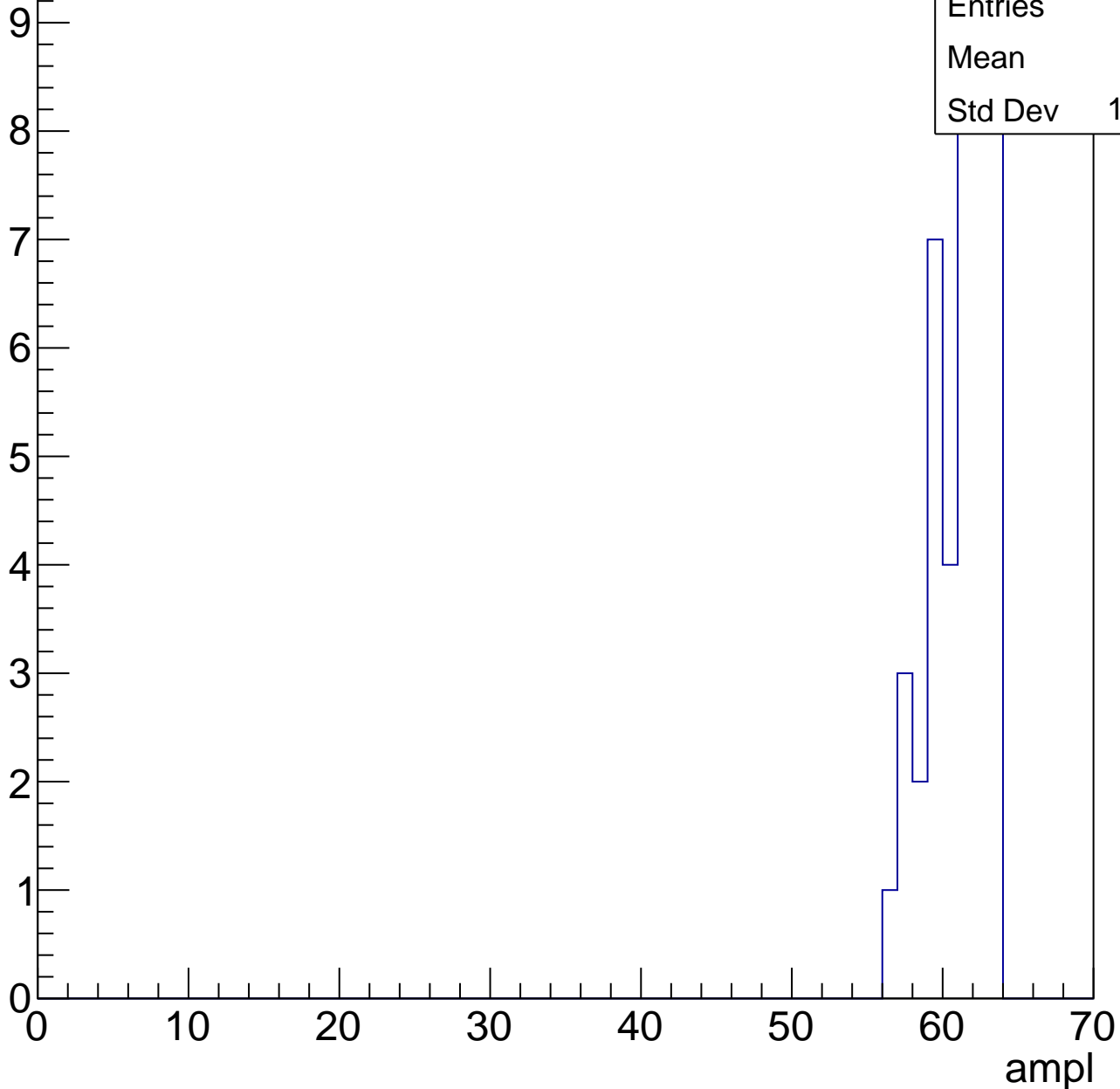
Entries	54
Mean	55.8
Std Dev	3.021



B0L002S, U5-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch23, adc0

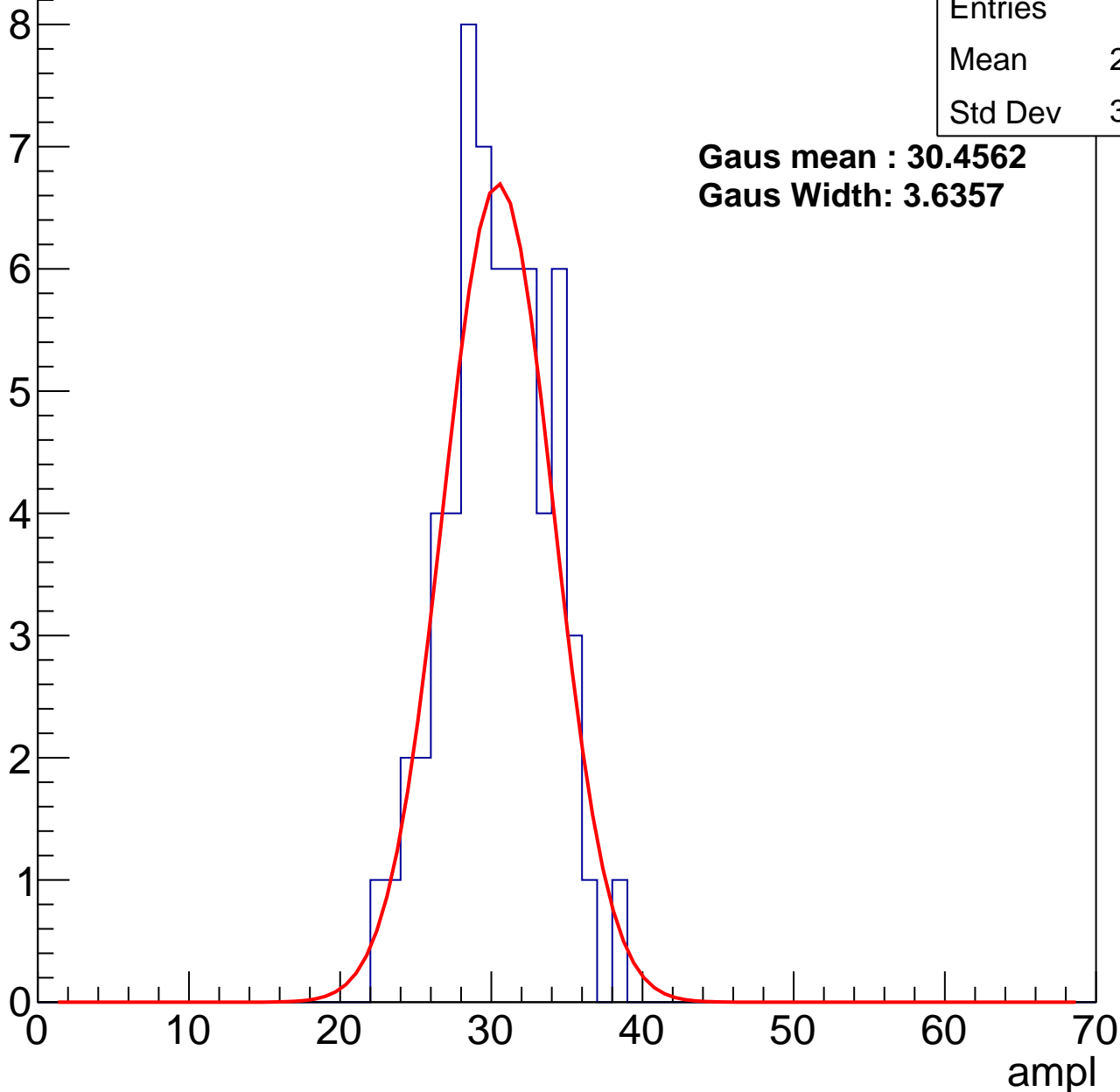
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.92
Std Dev	3.385

Gaus mean : 30.4562

Gaus Width: 3.6357



B0L002S, U5-ch23, adc1

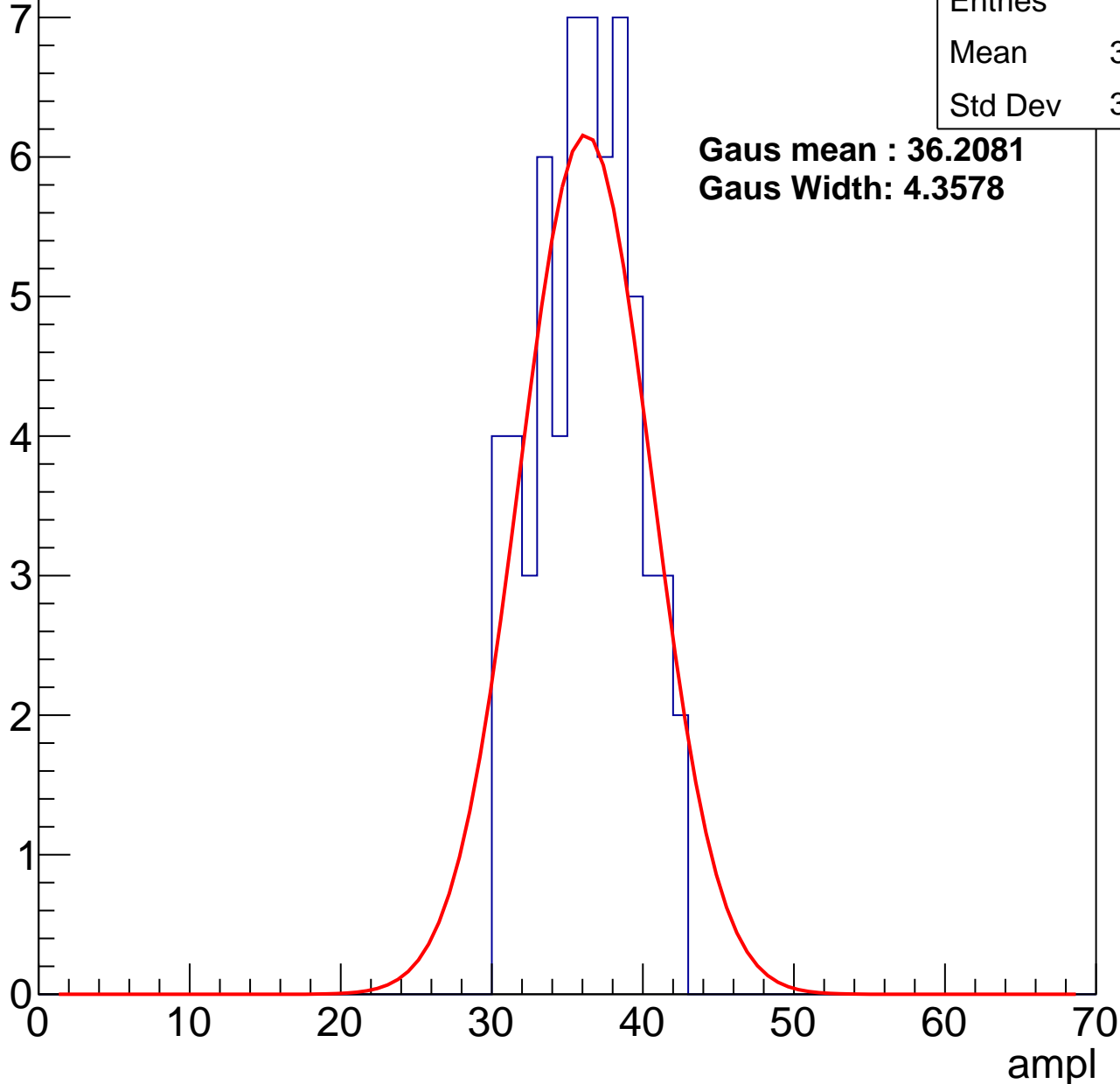
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	35.75
Std Dev	3.237

Gaus mean : 36.2081

Gaus Width: 4.3578



B0L002S, U5-ch23, adc2

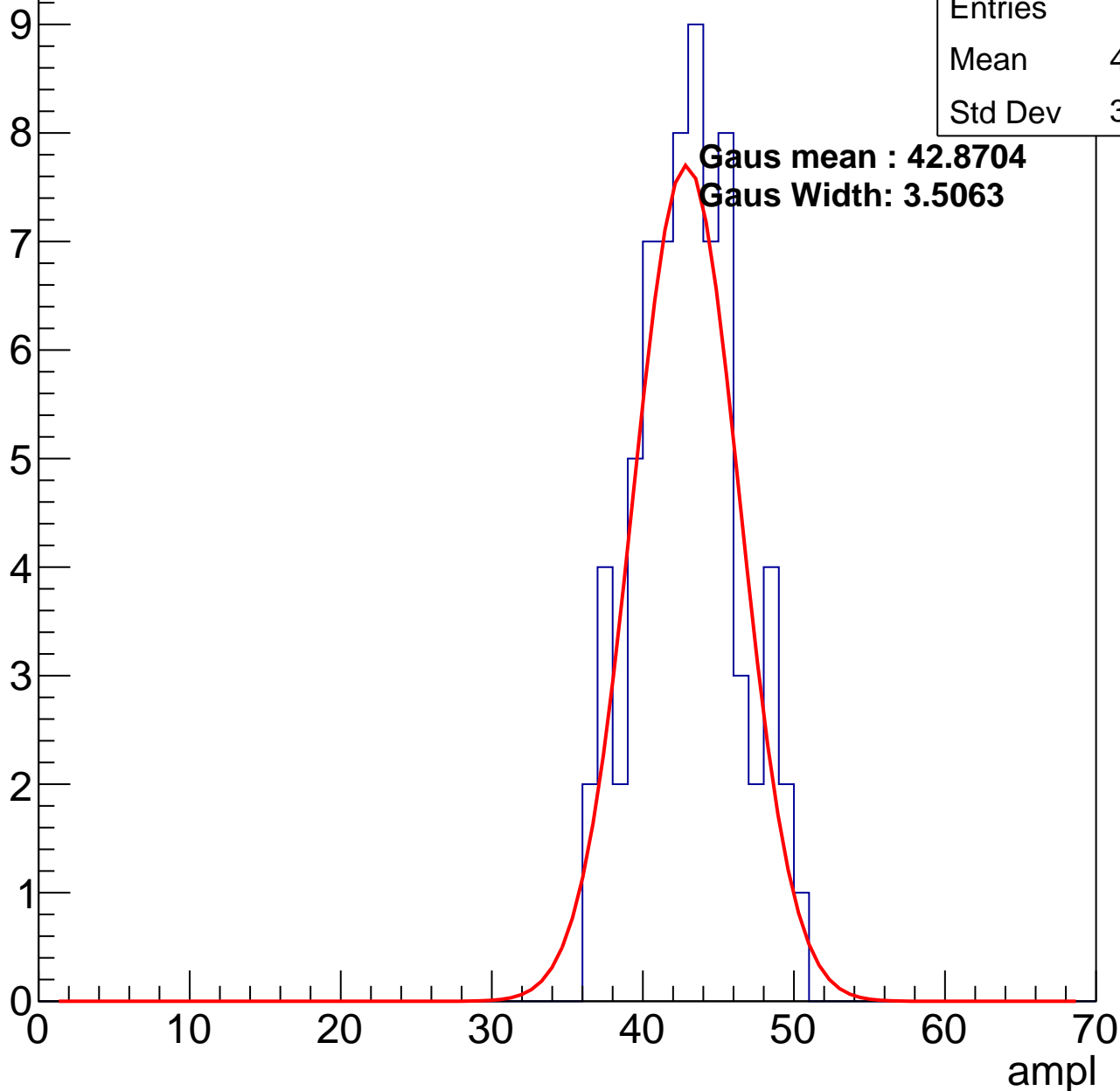
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	42.55
Std Dev	3.305

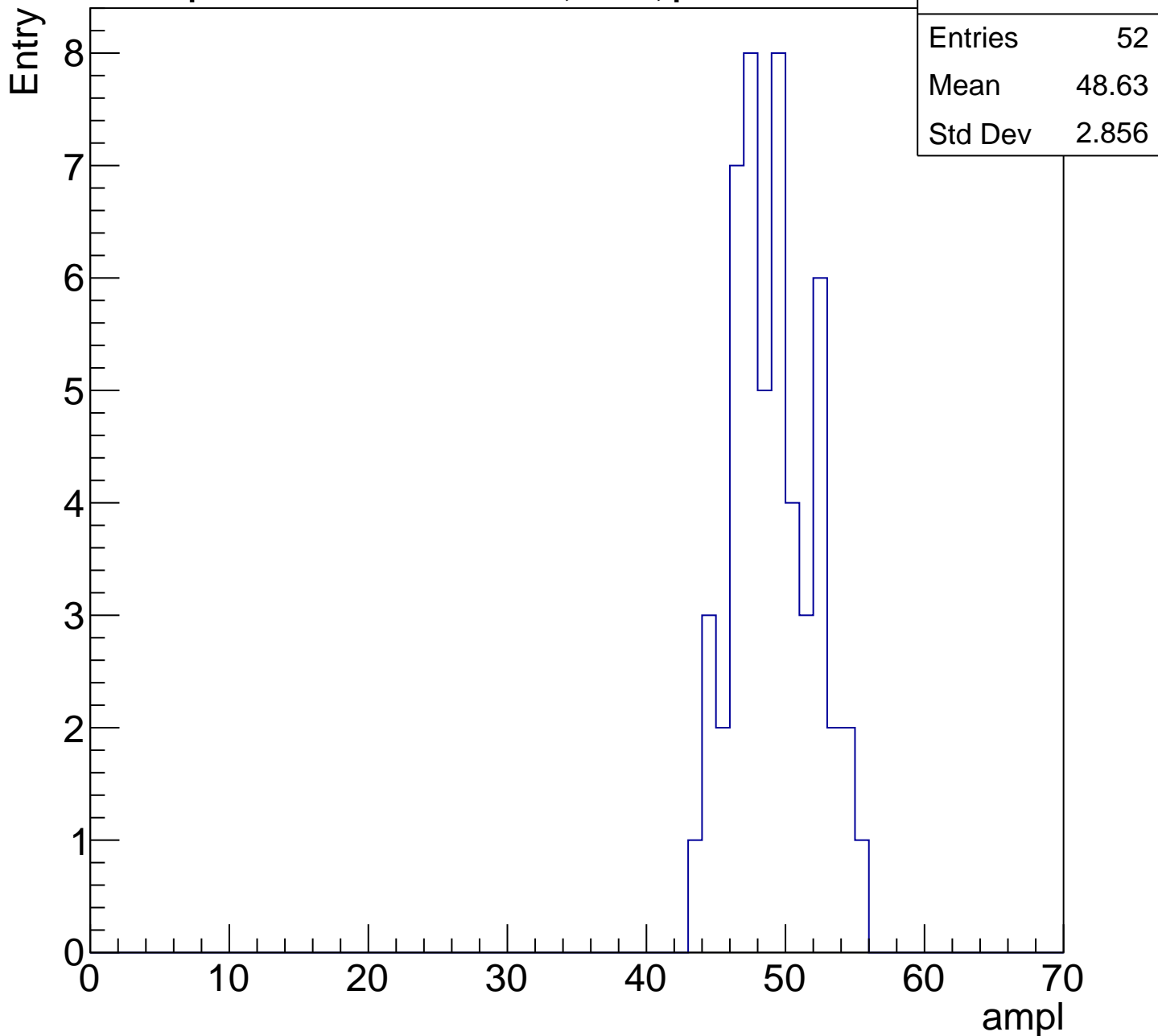
Gaus mean : 42.8704

Gaus Width: 3.5063



B0L002S, U5-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

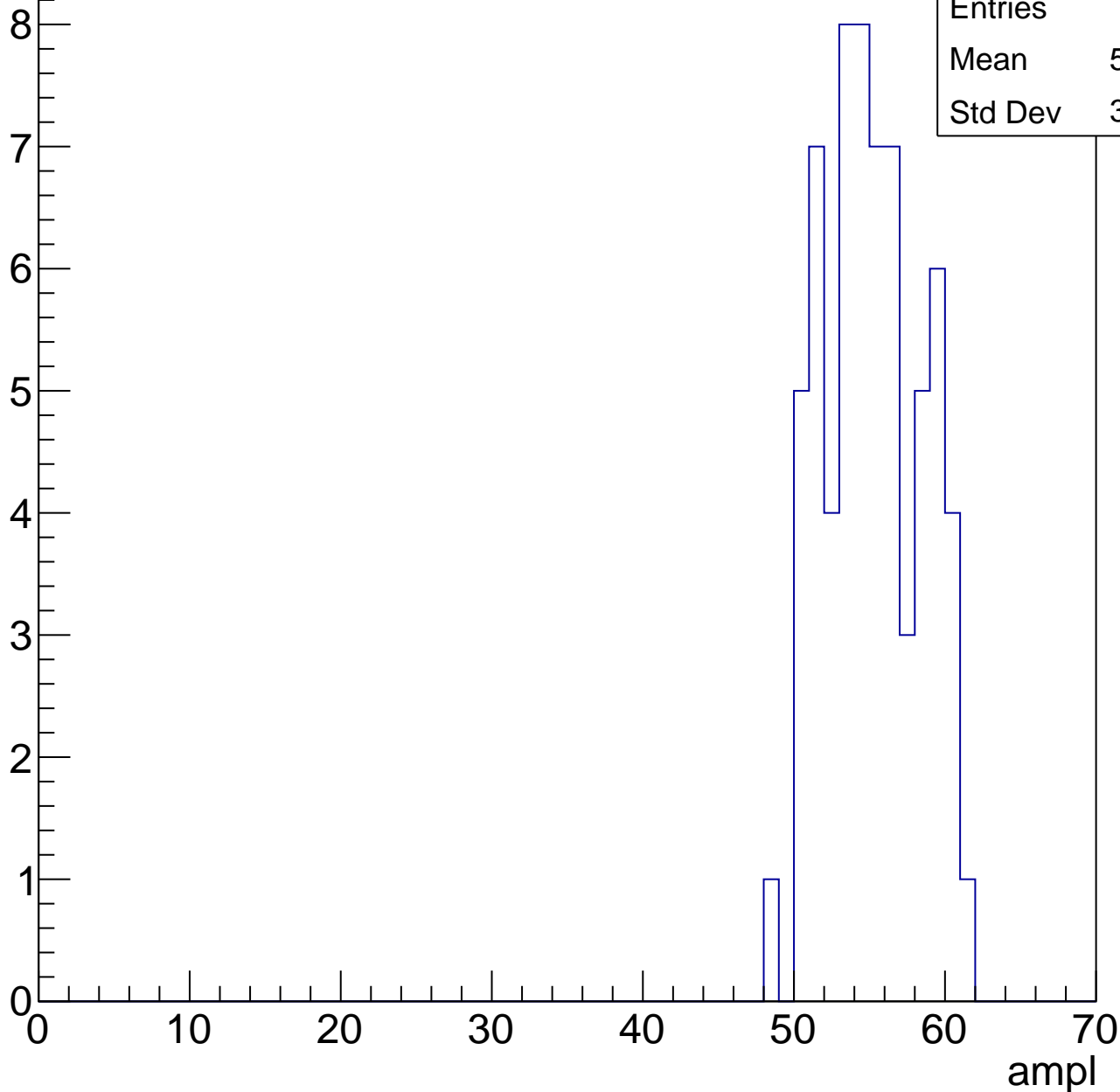


B0L002S, U5-ch23, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	54.73
Std Dev	3.146

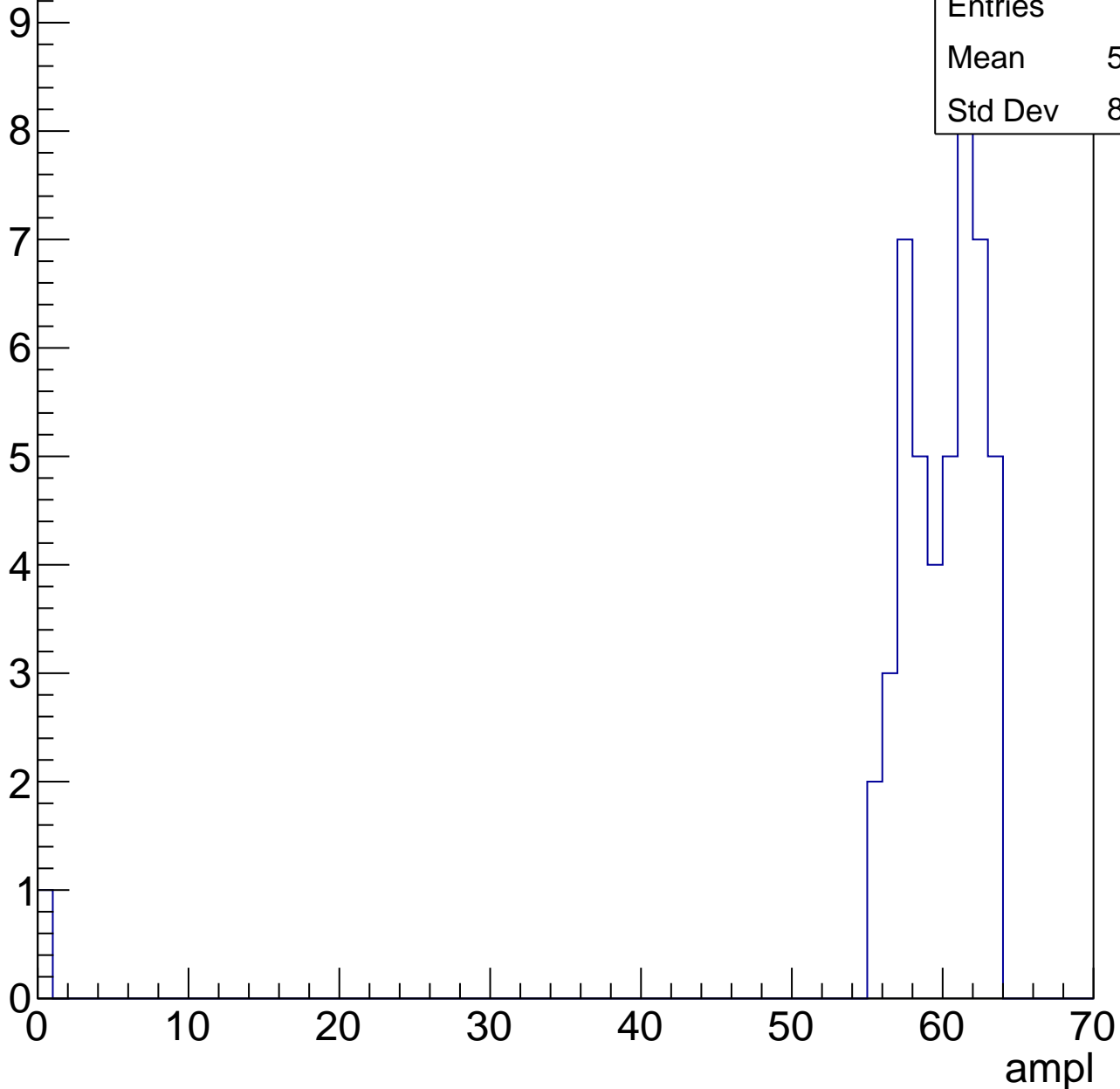


B0L002S, U5-ch23, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	58.35
Std Dev	8.823

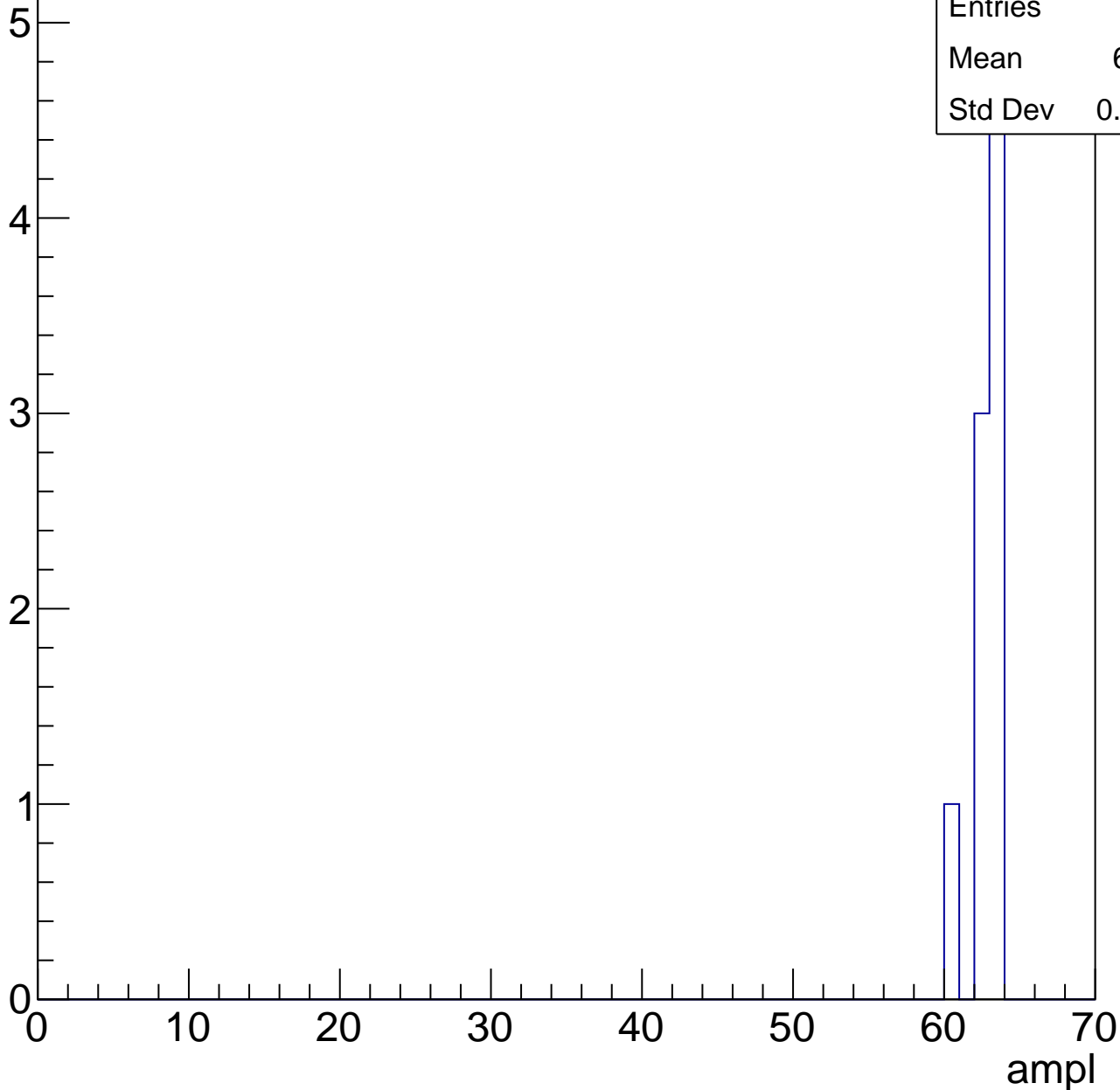


B0L002S, U5-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	62.33
Std Dev	0.9428



B0L002S, U5-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U5-ch24, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	31.02
Std Dev	3.096

Gaus mean : 31.4540

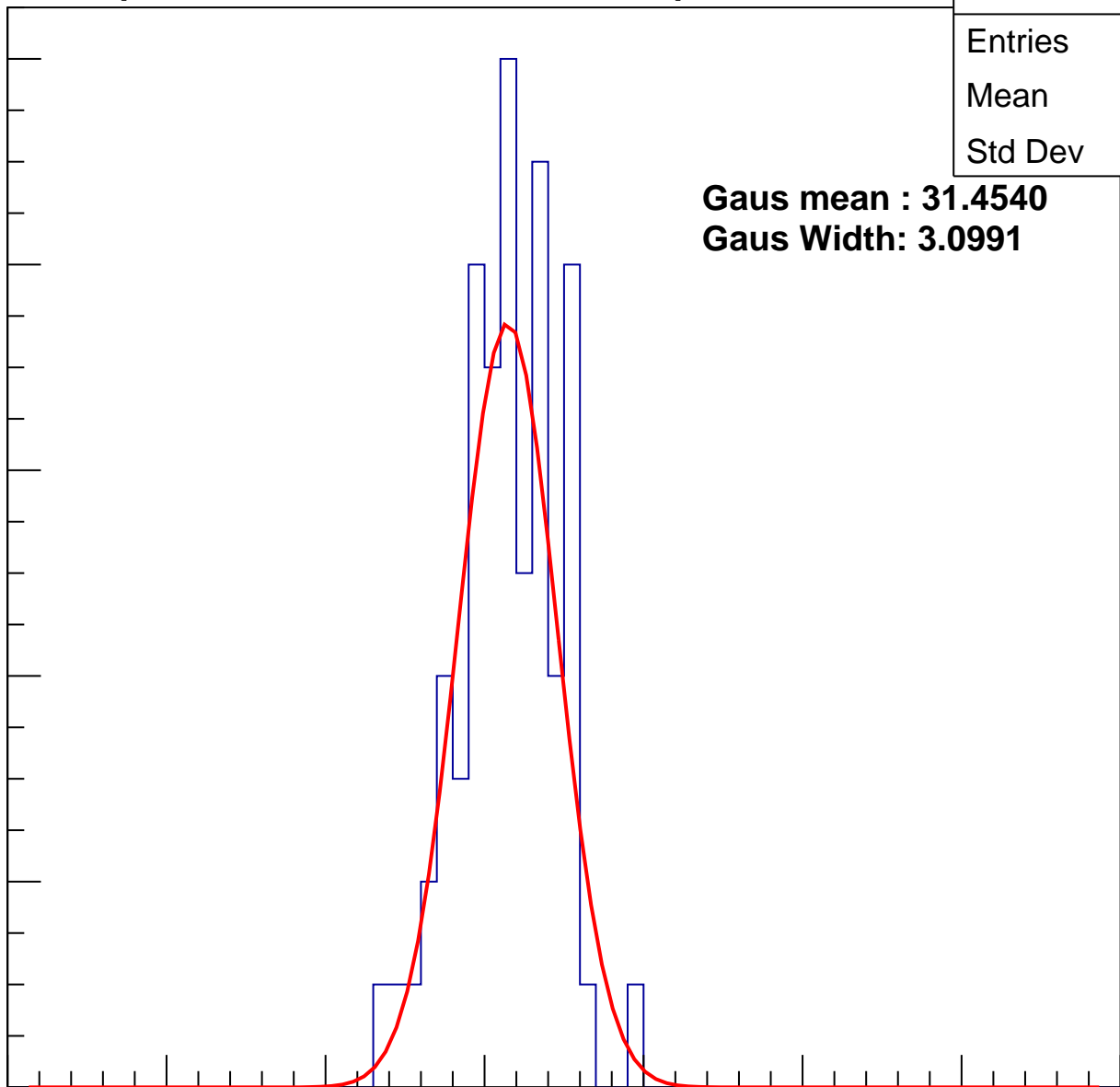
Gaus Width: 3.0991

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



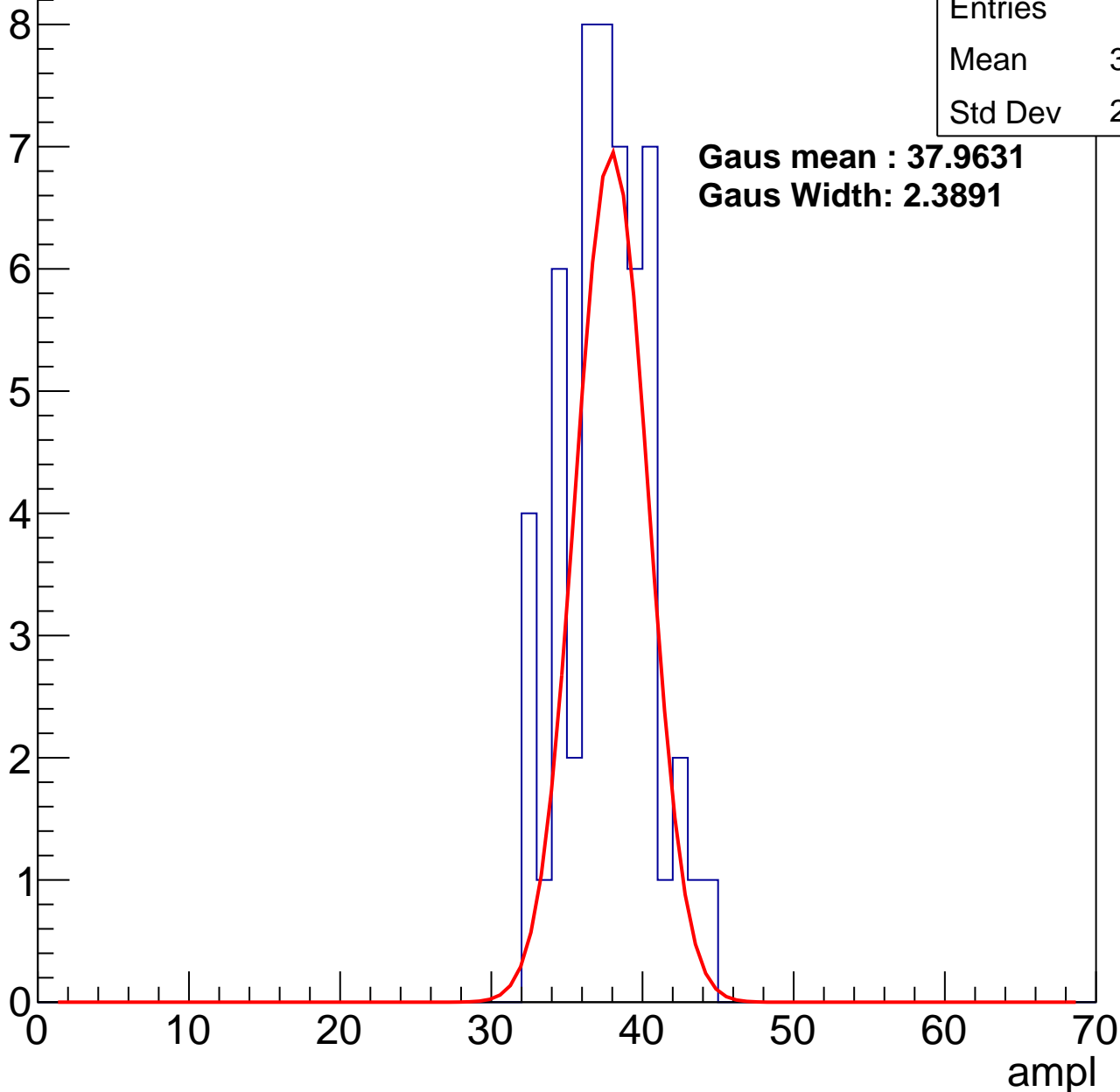
B0L002S, U5-ch24, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	37.24
Std Dev	2.815

Gaus mean : 37.9631
Gaus Width: 2.3891



B0L002S, U5-ch24, adc2

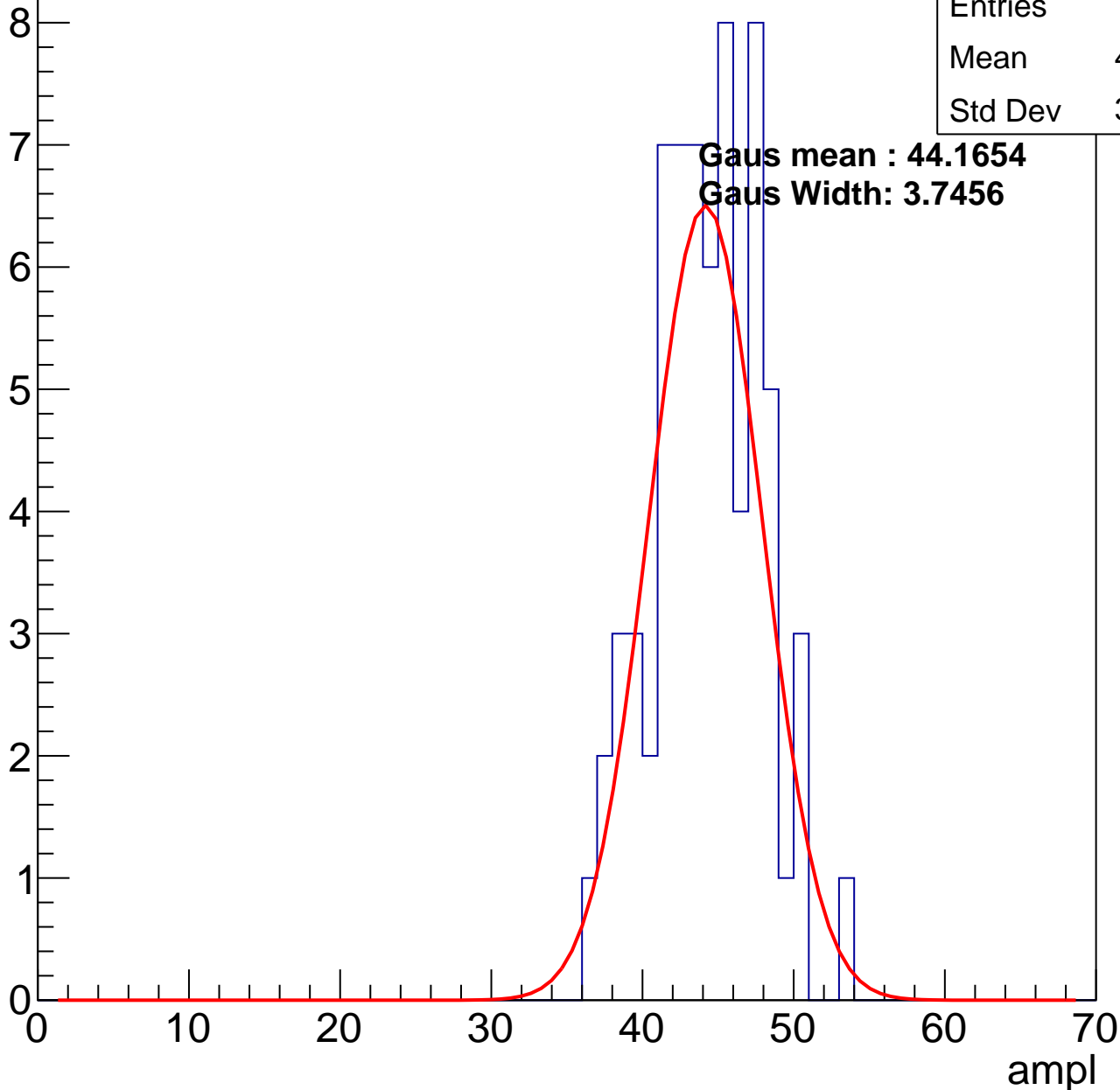
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.81
Std Dev	3.561

Gaus mean : 44.1654

Gaus Width: 3.7456

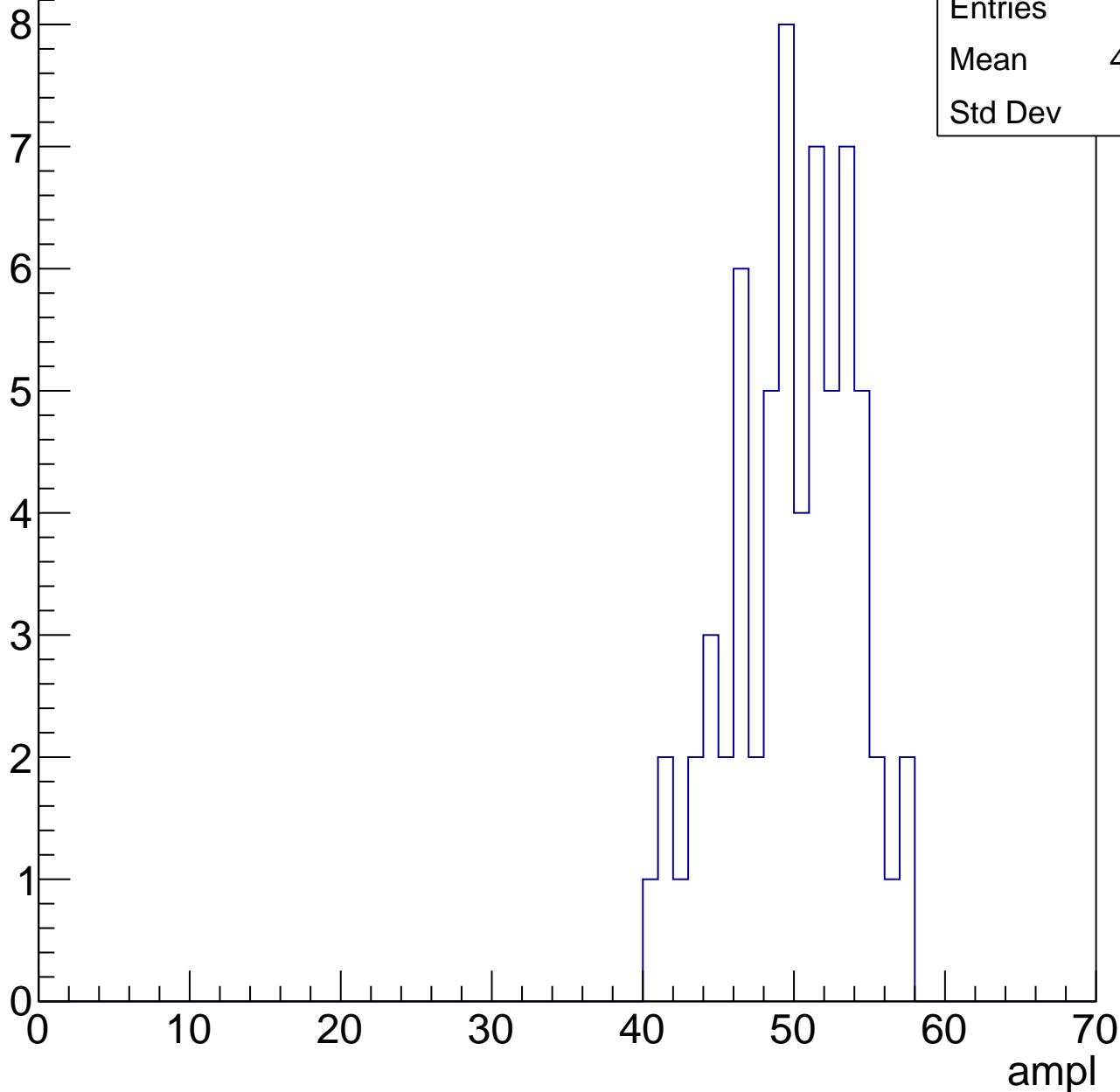


B0L002S, U5-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.42
Std Dev	4.03

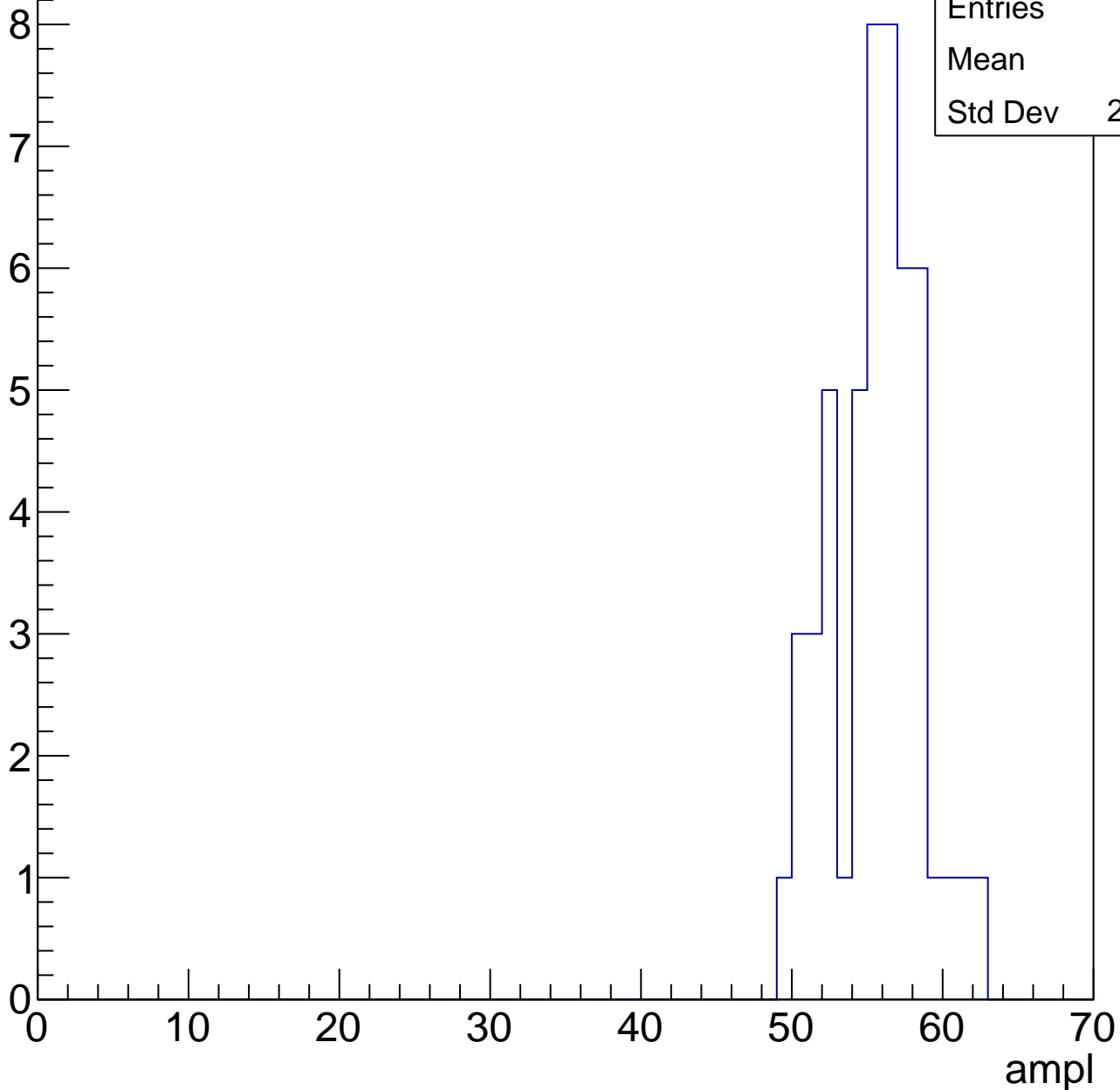


B0L002S, U5-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

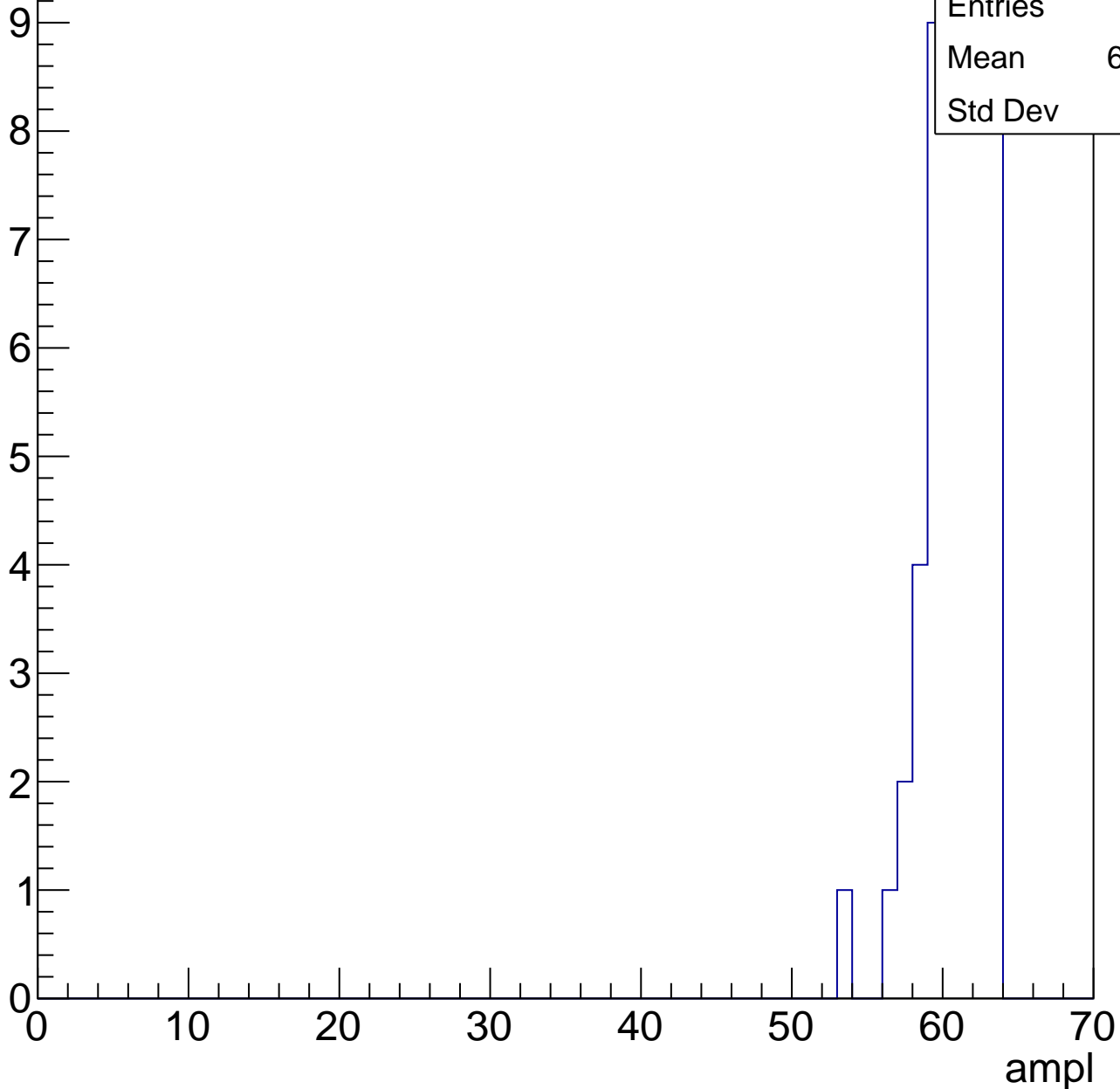
Entries	50
Mean	55.1
Std Dev	2.914



B0L002S, U5-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

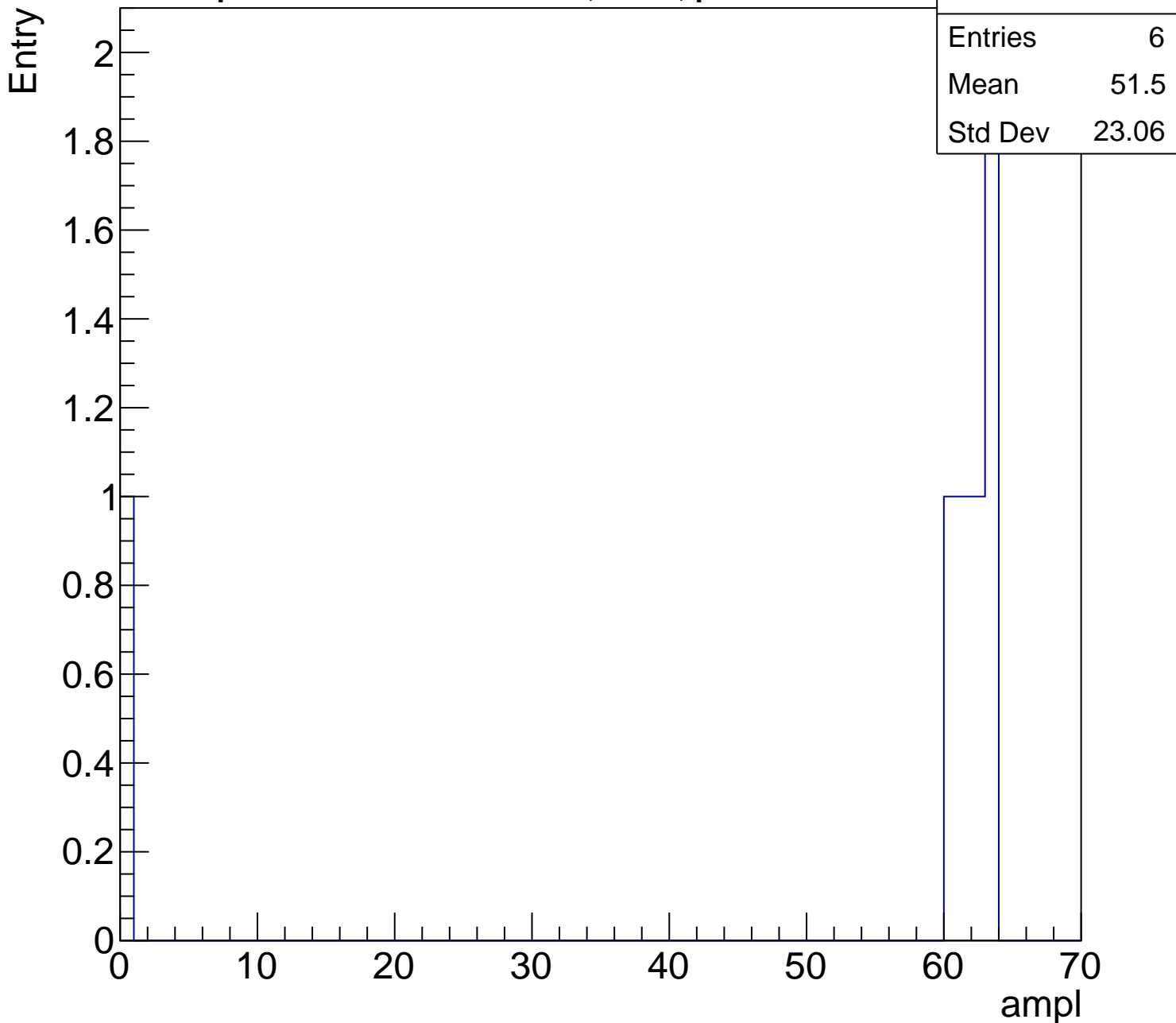
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.5
Std Dev	23.06

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch25, adc0

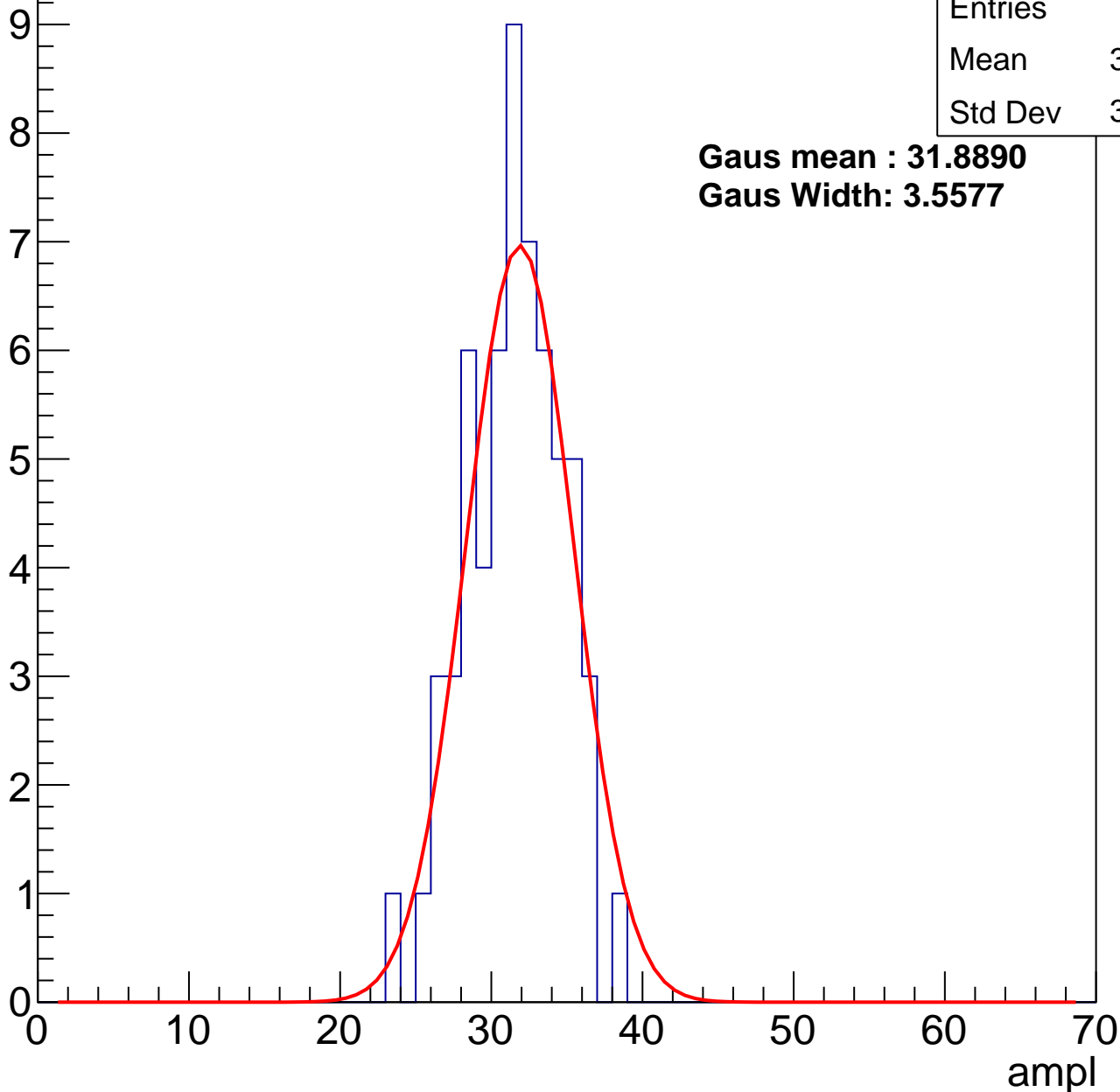
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	31.05
Std Dev	3.106

Gaus mean : 31.8890

Gaus Width: 3.5577



B0L002S, U5-ch25, adc1

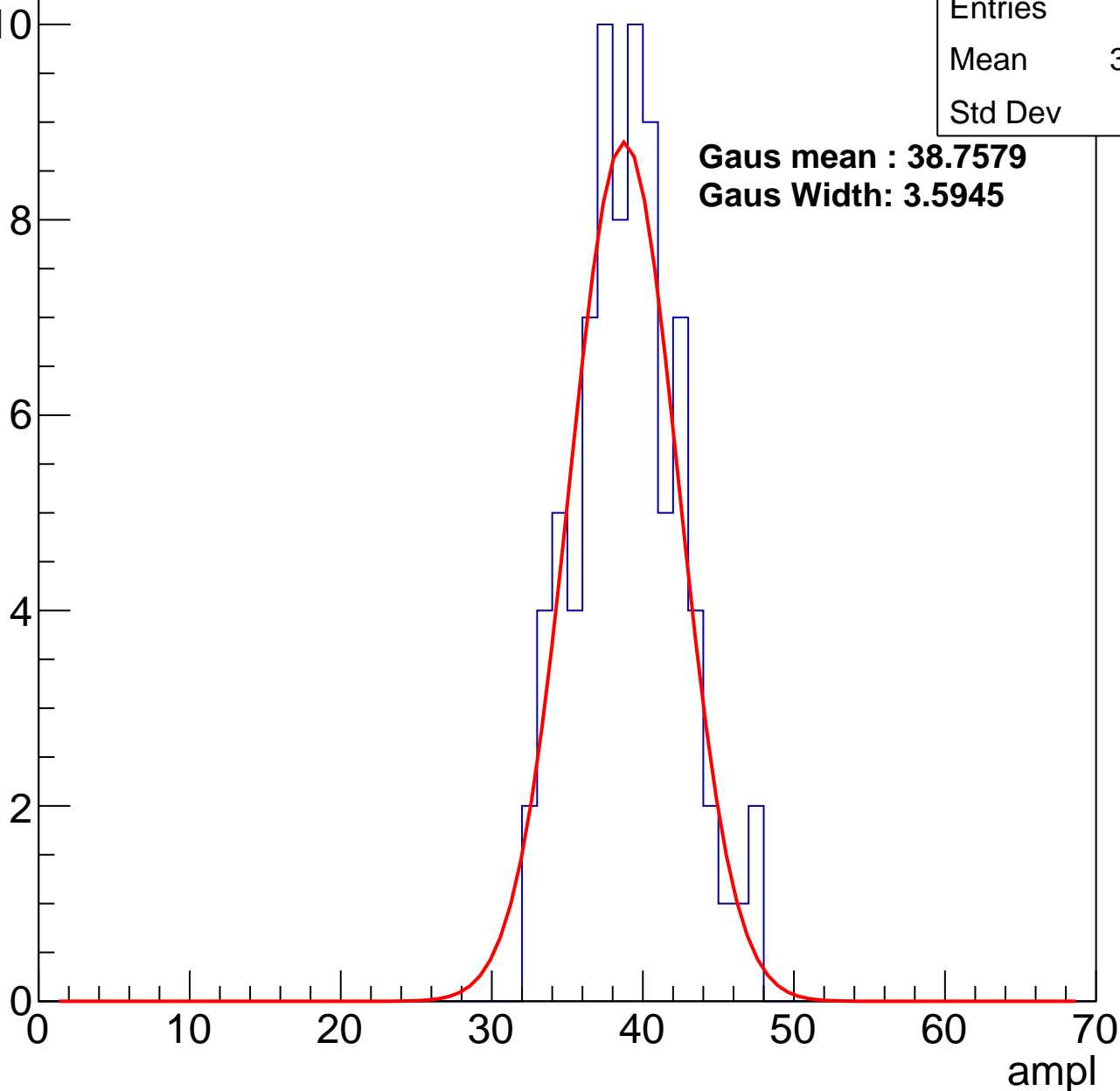
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	38.59
Std Dev	3.42

Gaus mean : 38.7579

Gaus Width: 3.5945



B0L002S, U5-ch25, adc2

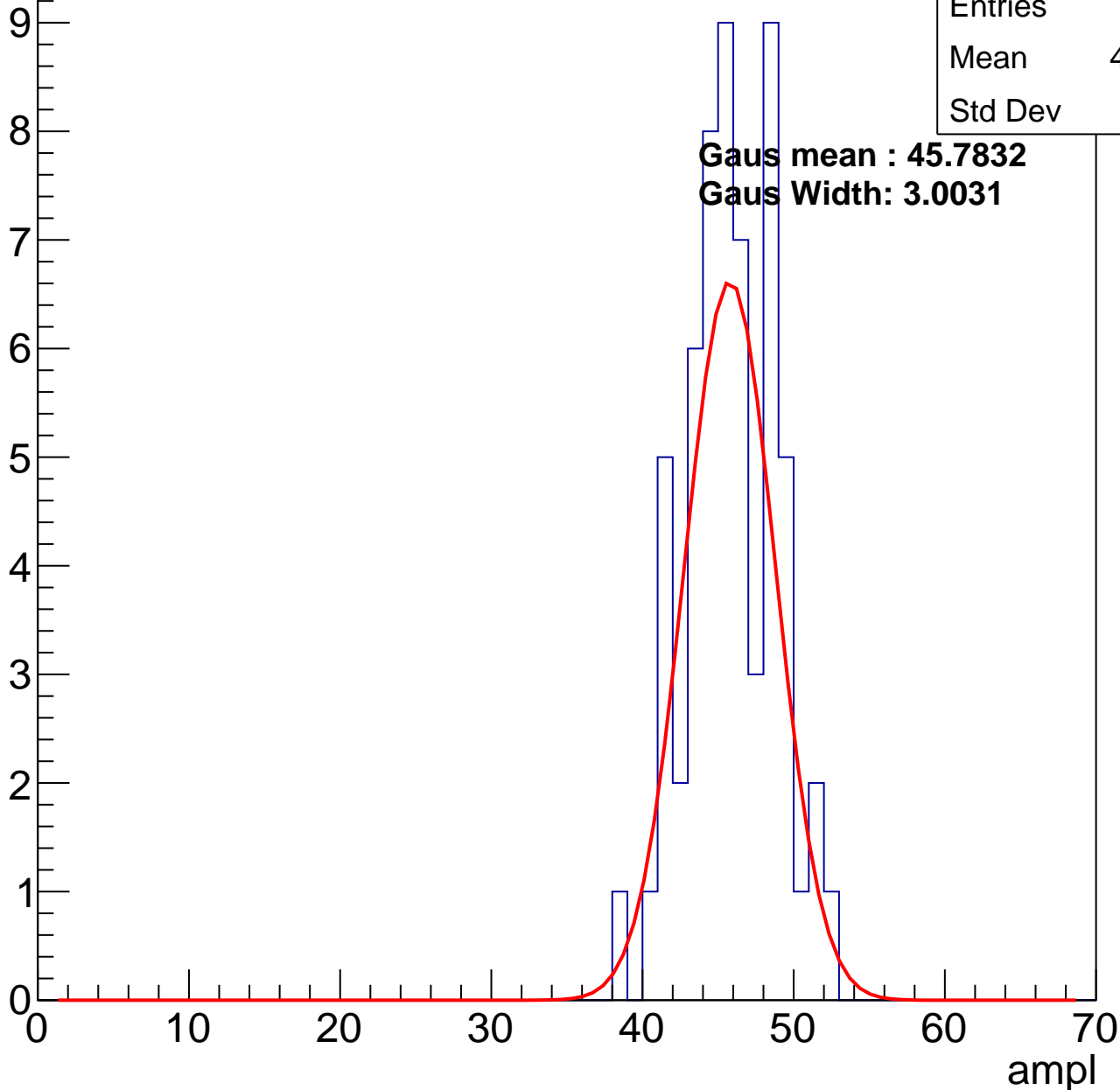
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	45.43
Std Dev	2.94

Gaus mean : 45.7832

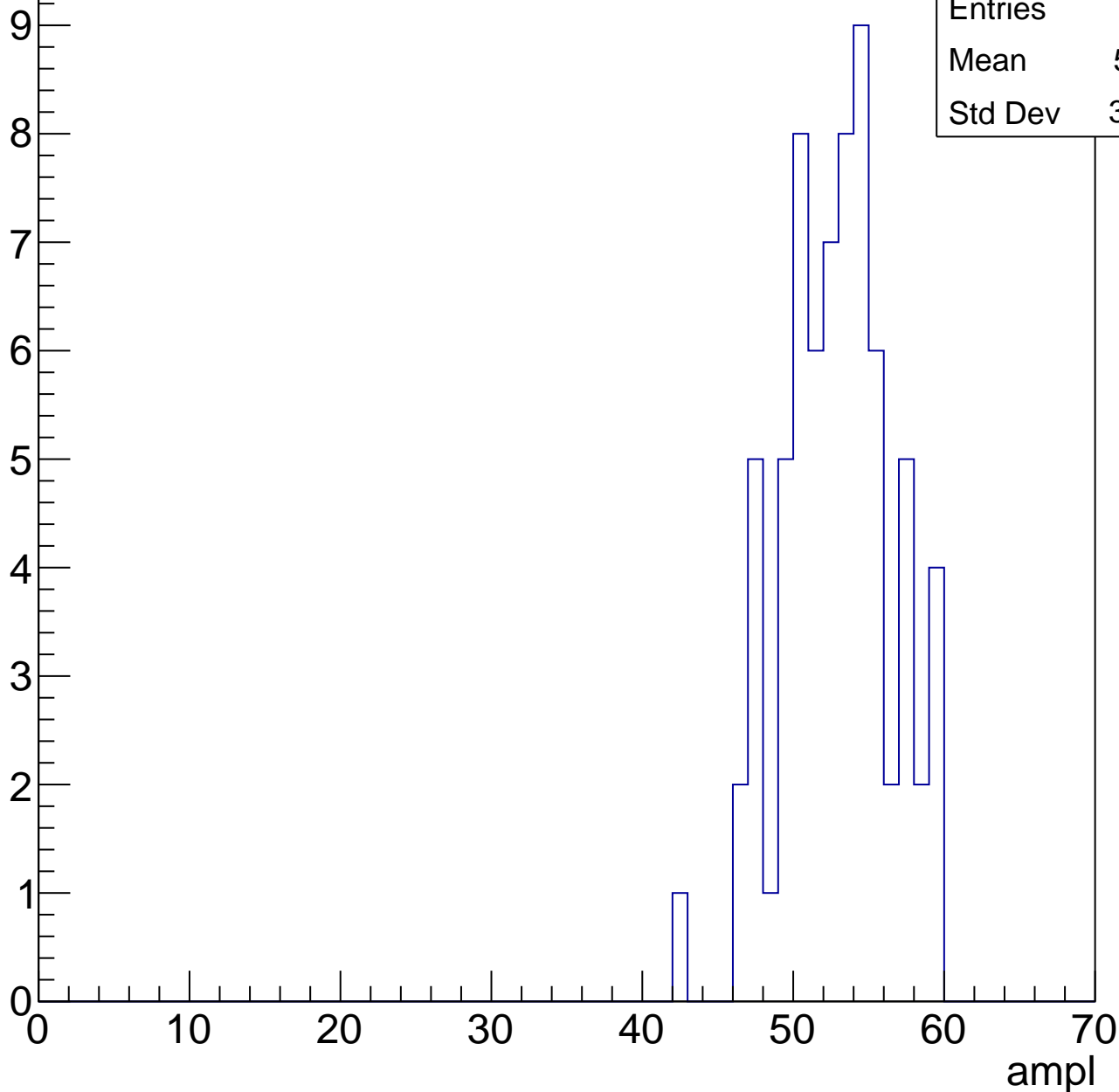
Gaus Width: 3.0031



B0L002S, U5-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

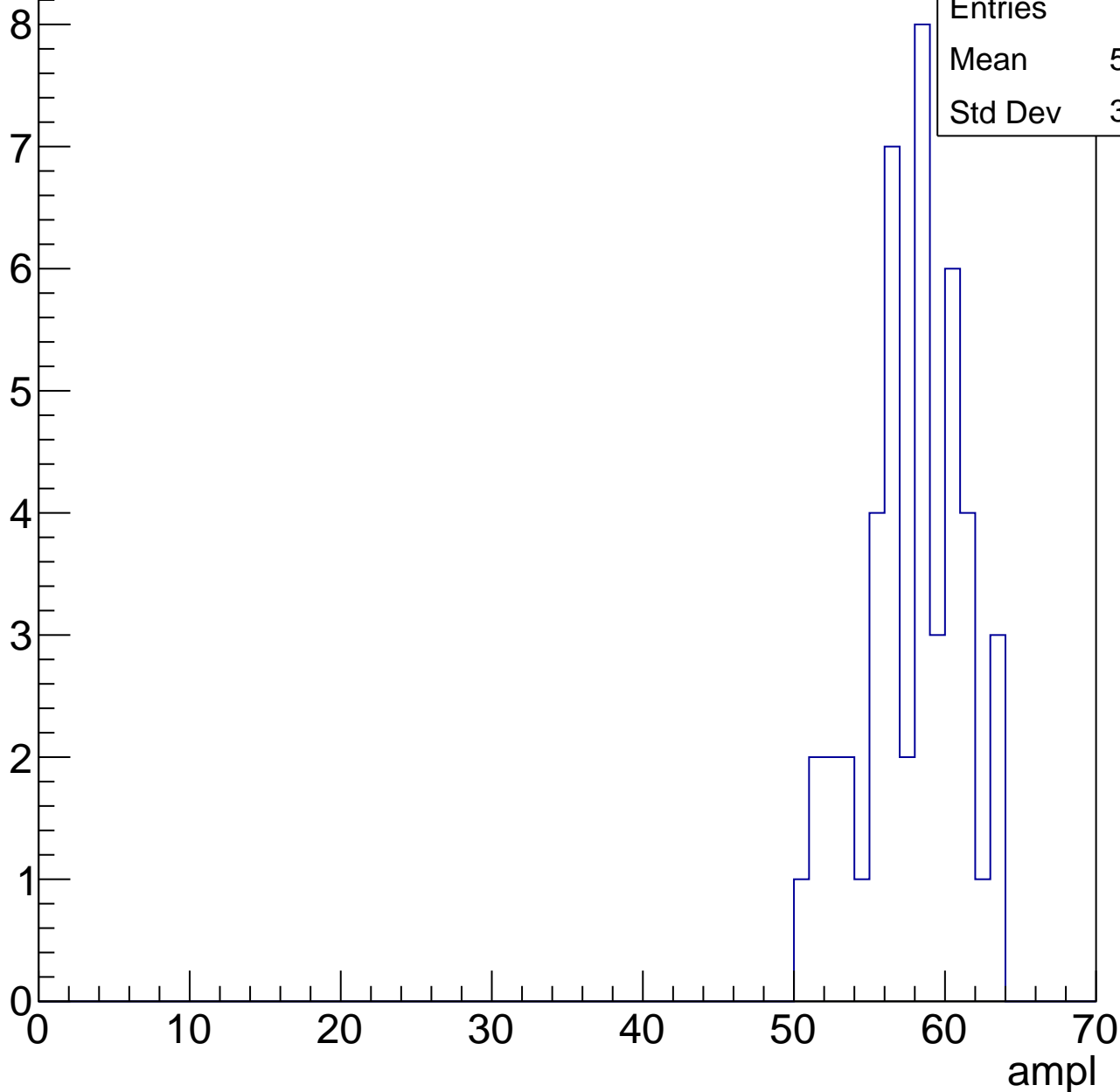


B0L002S, U5-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	57.35
Std Dev	3.298

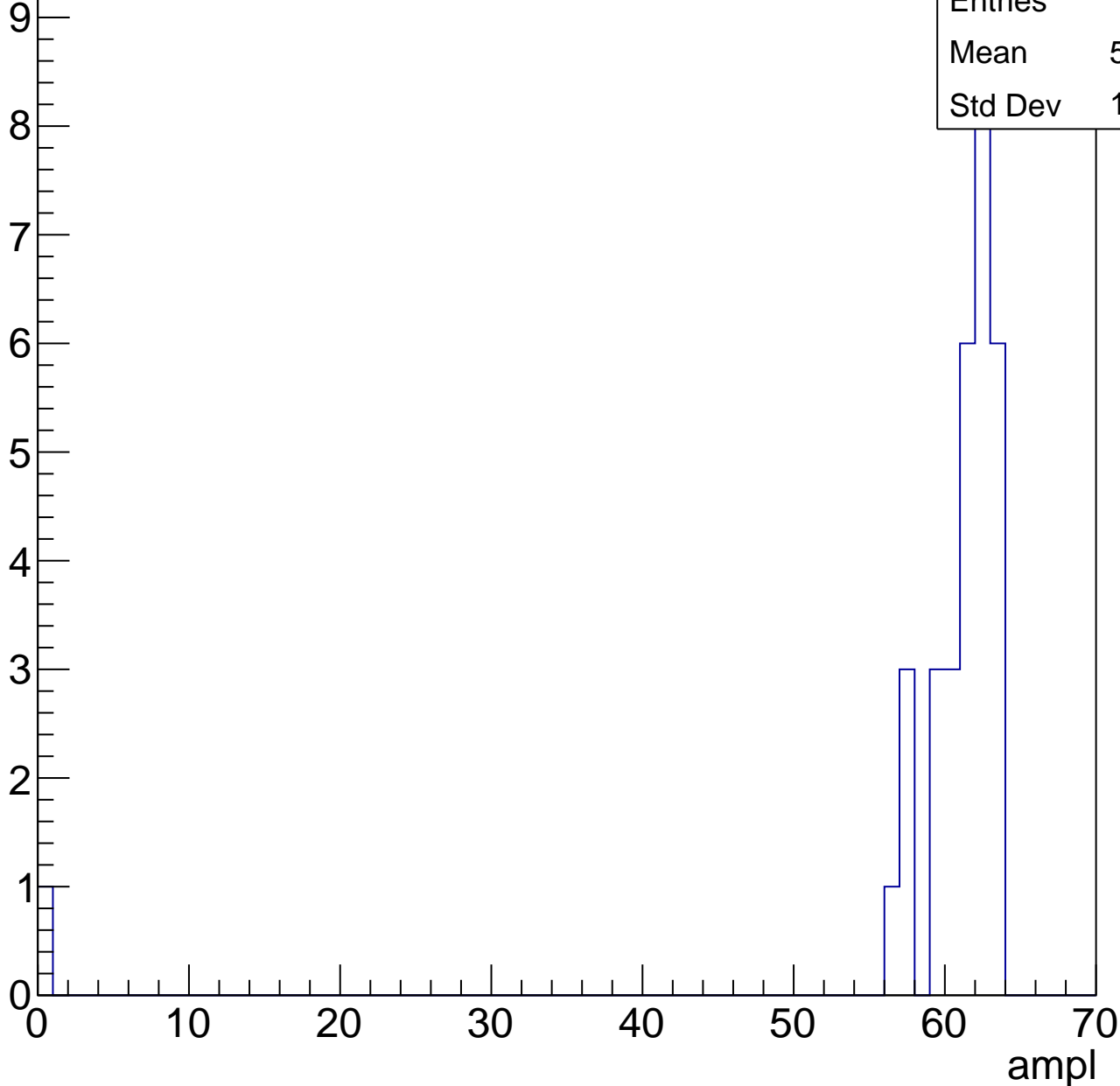


B0L002S, U5-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

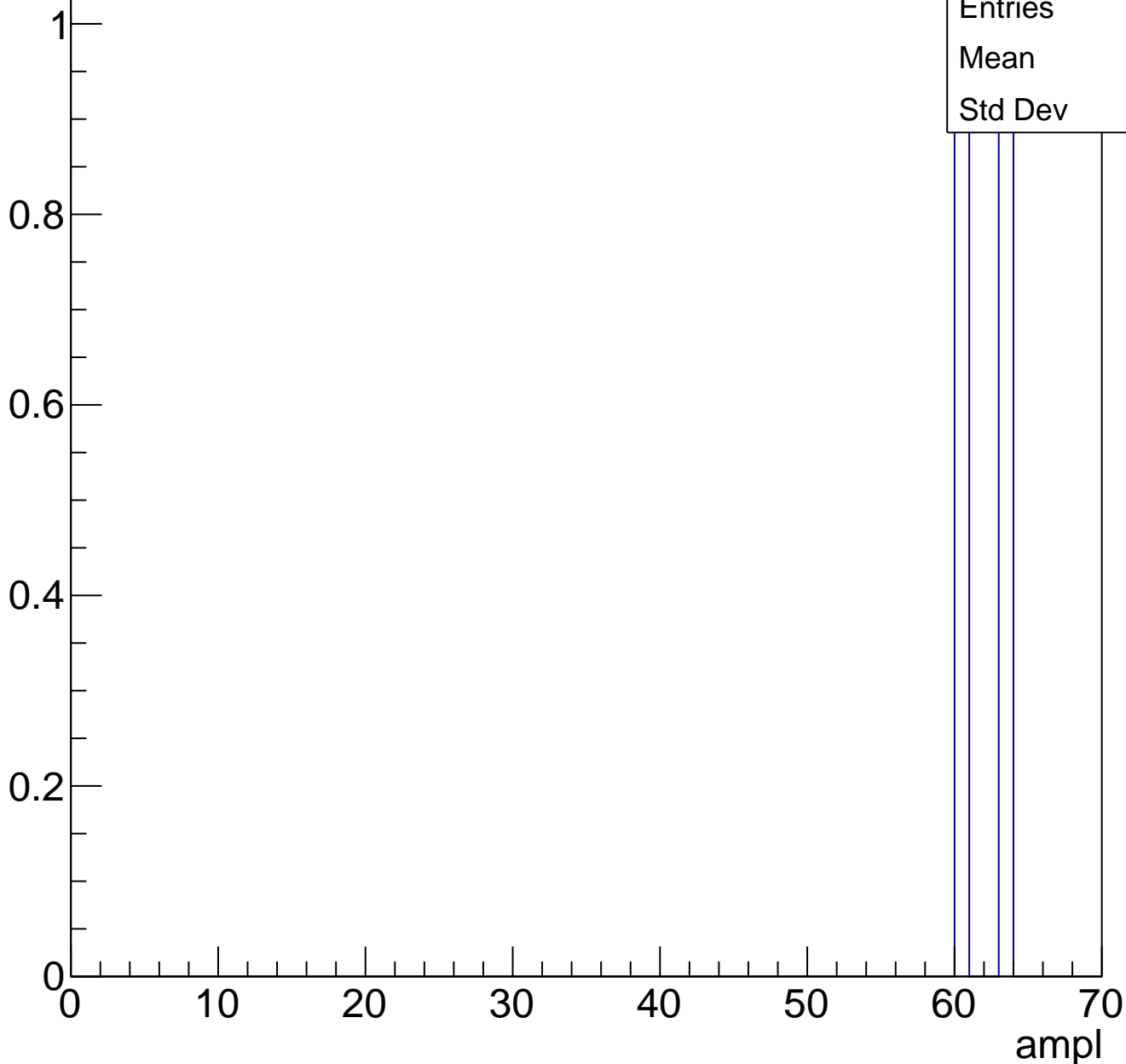
Entries	32
Mean	58.94
Std Dev	10.76



B0L002S, U5-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

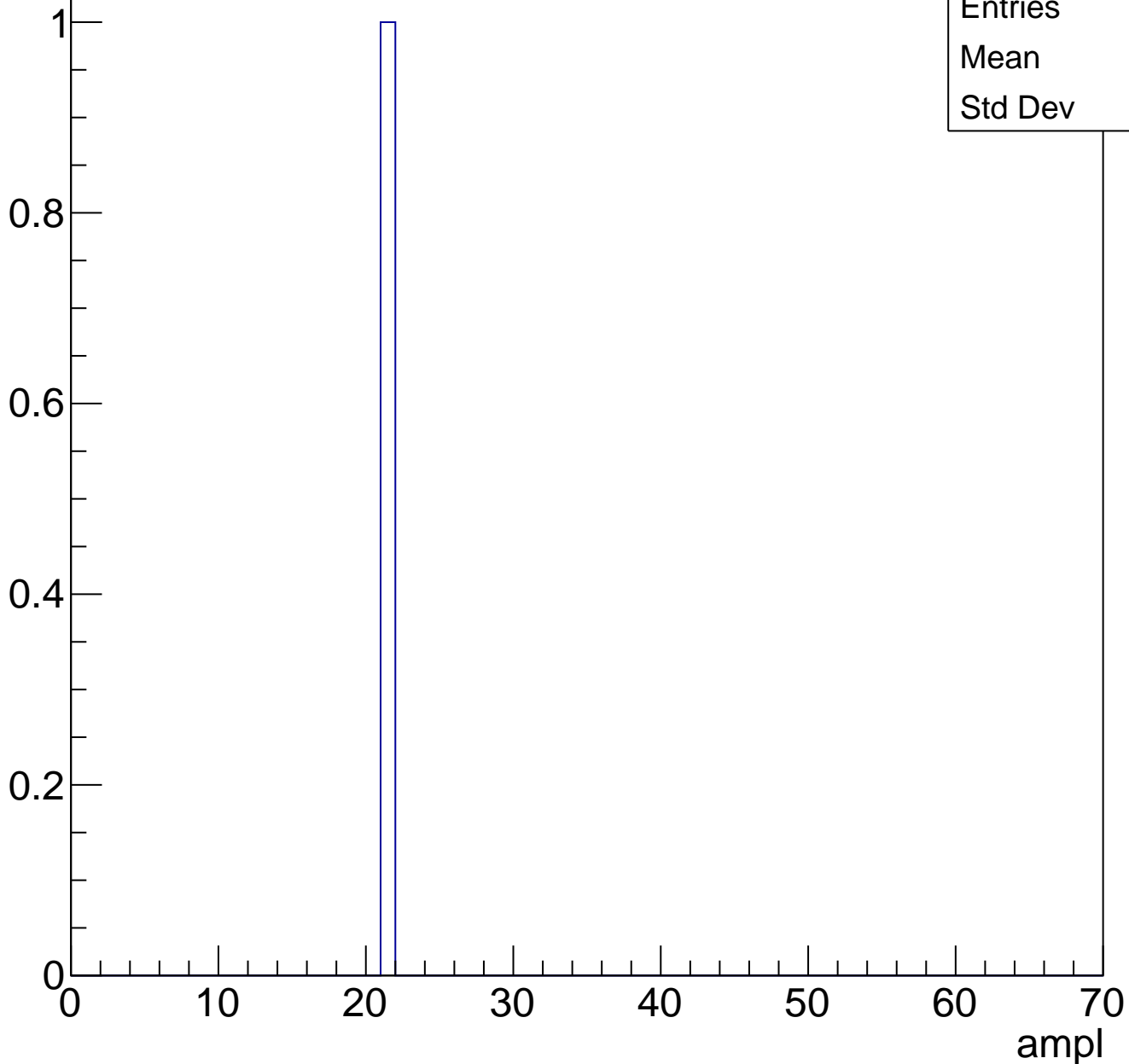
Entry



B0L002S, U5-ch25, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch26, adc0

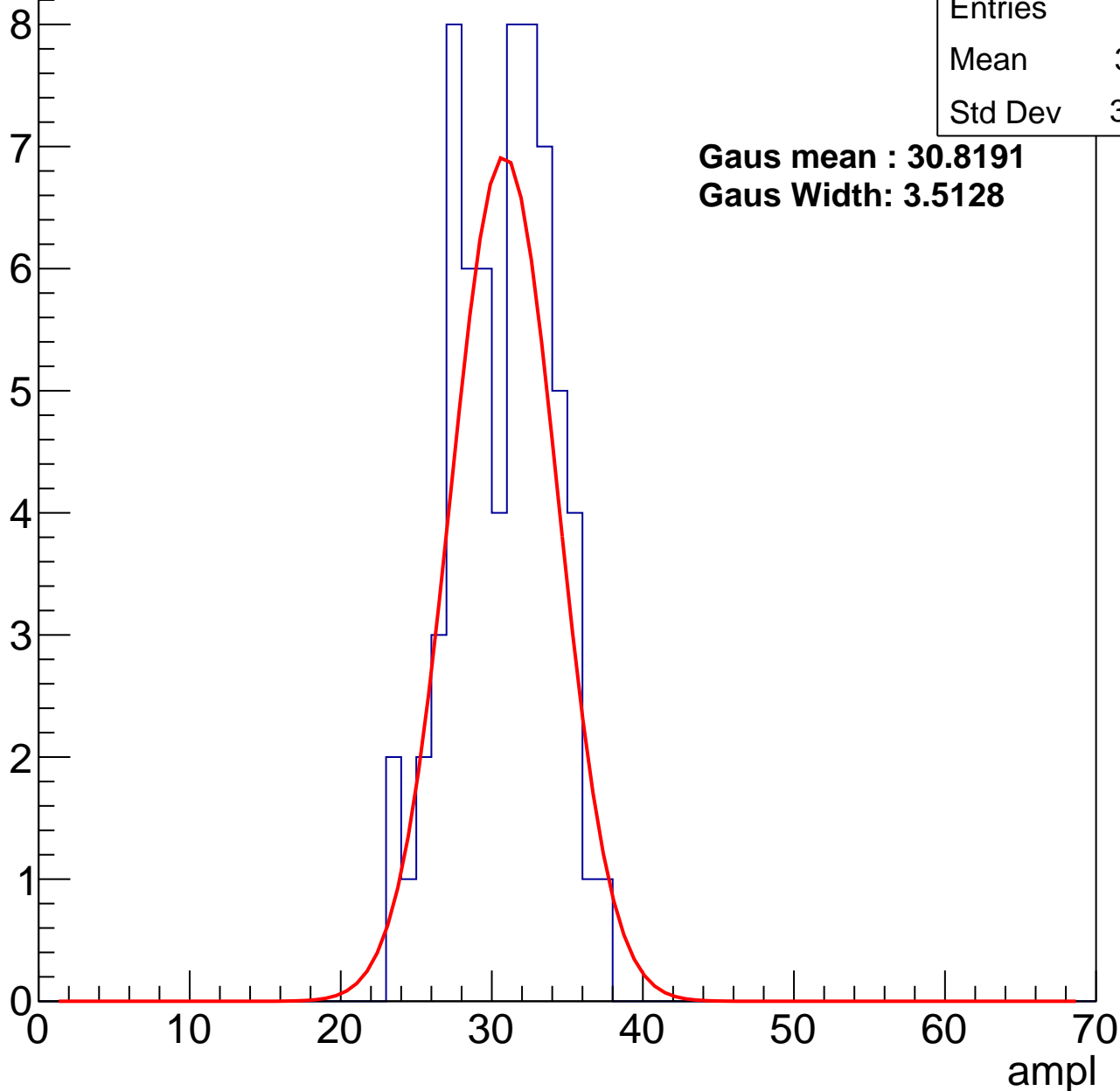
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.21
Std Dev	3.254

Gaus mean : 30.8191

Gaus Width: 3.5128



B0L002S, U5-ch26, adc1

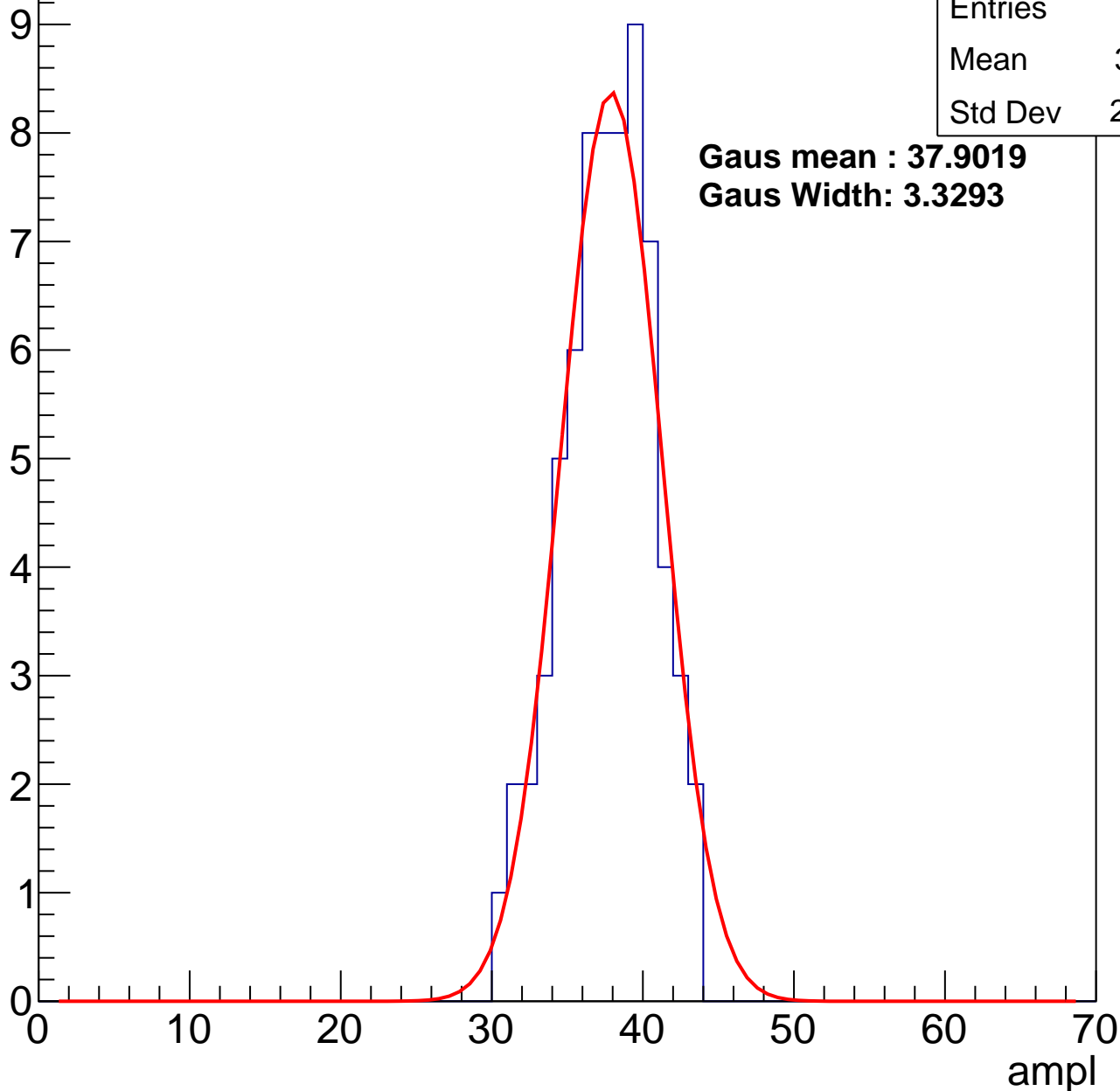
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.21
Std Dev	2.998

Gaus mean : 37.9019

Gaus Width: 3.3293



B0L002S, U5-ch26, adc2

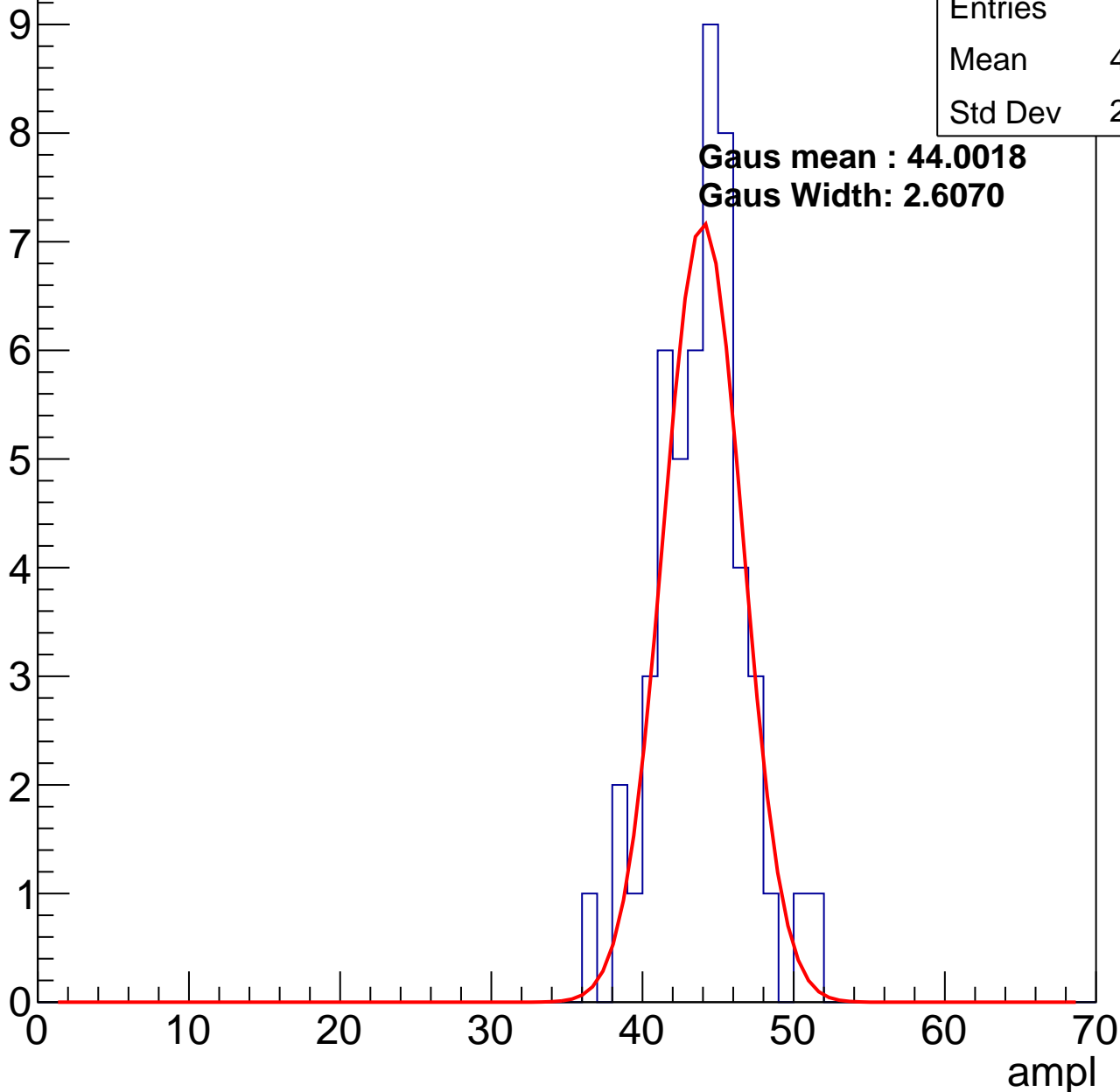
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	43.43
Std Dev	2.885

Gaus mean : 44.0018

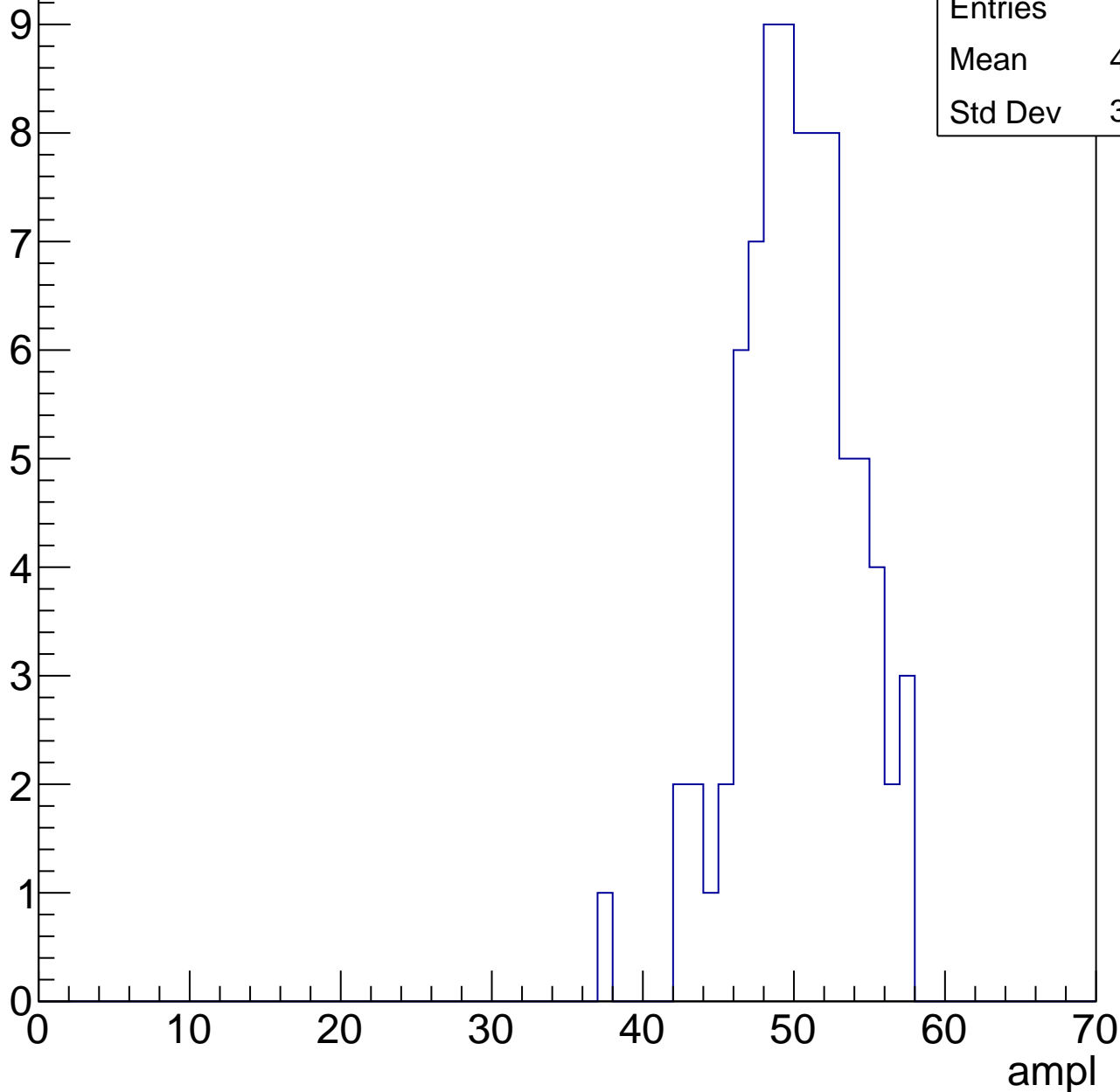
Gaus Width: 2.6070



B0L002S, U5-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1

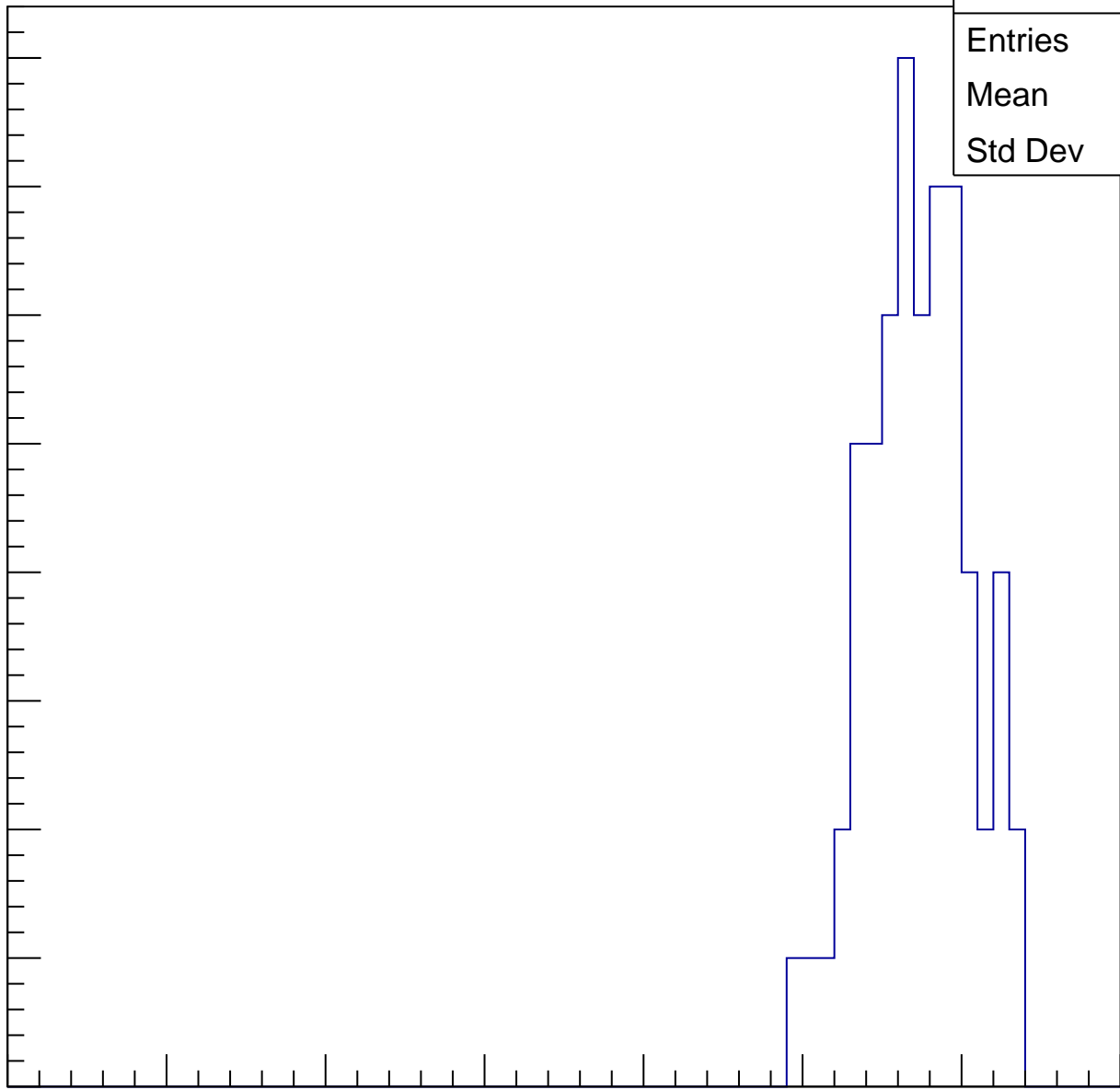
Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	56.79
Std Dev	3.209

ampl

0 10 20 30 40 50 60 70

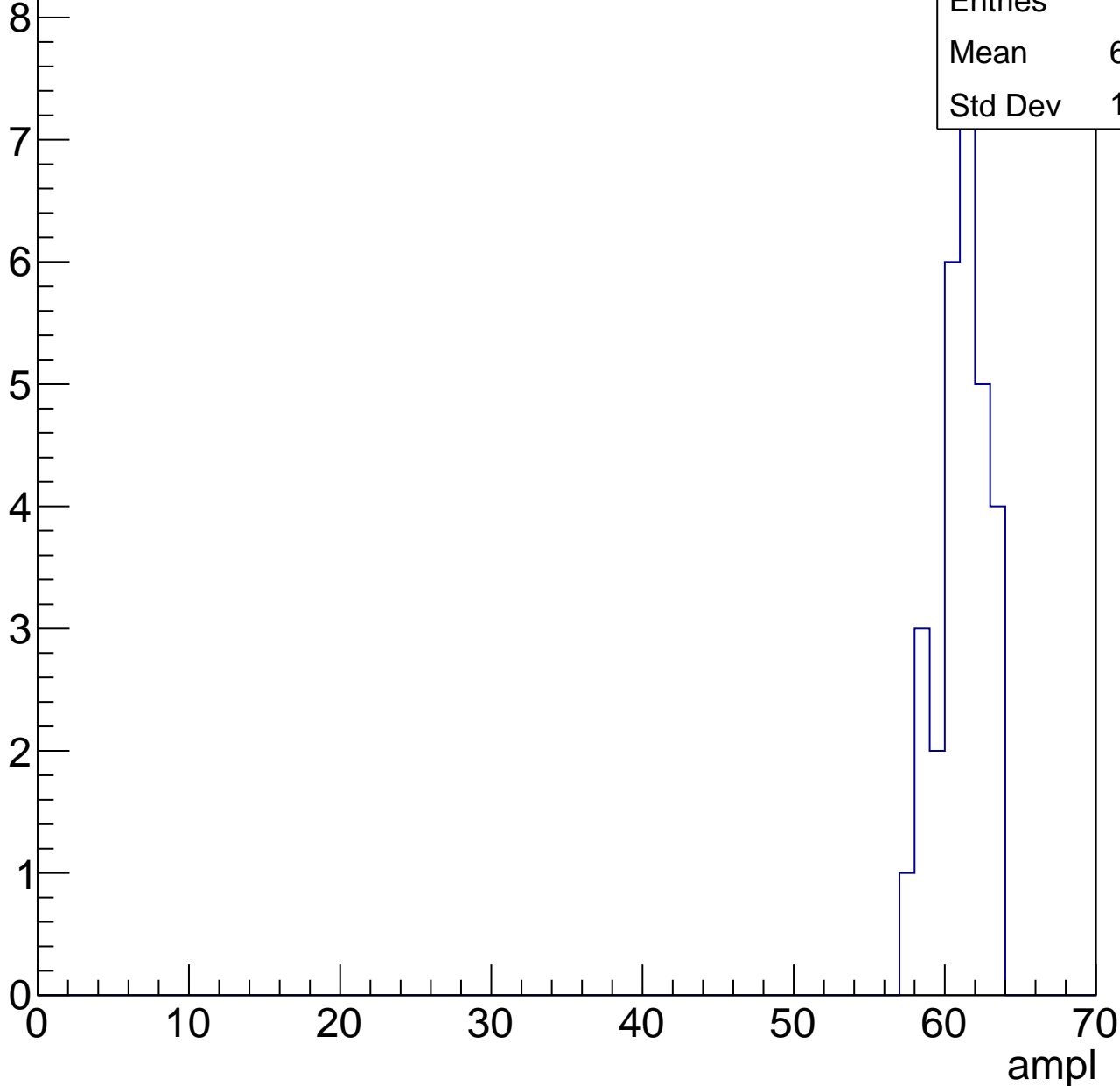


B0L002S, U5-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

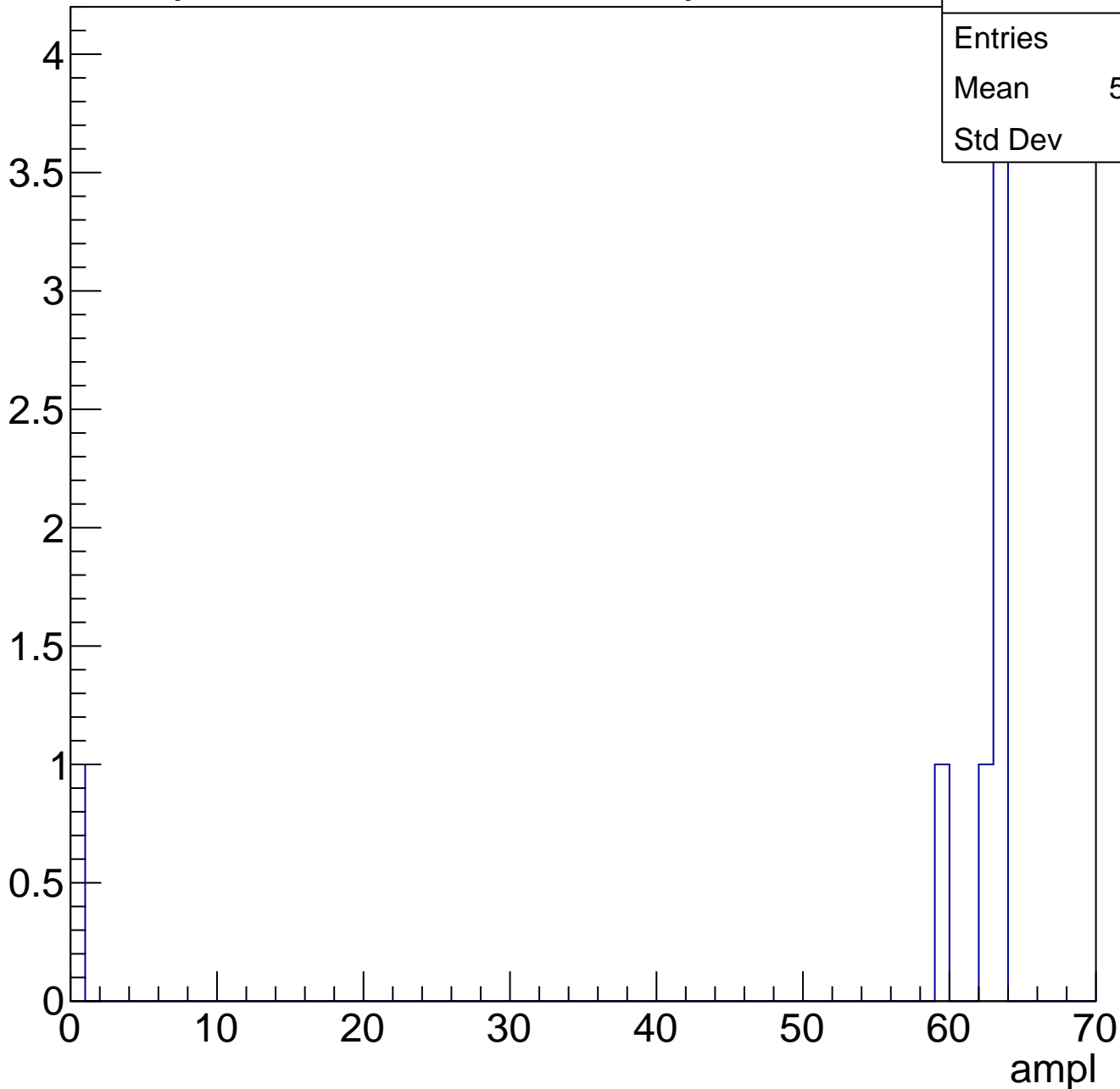
Entries	29
Mean	60.66
Std Dev	1.603



B0L002S, U5-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch27, adc0

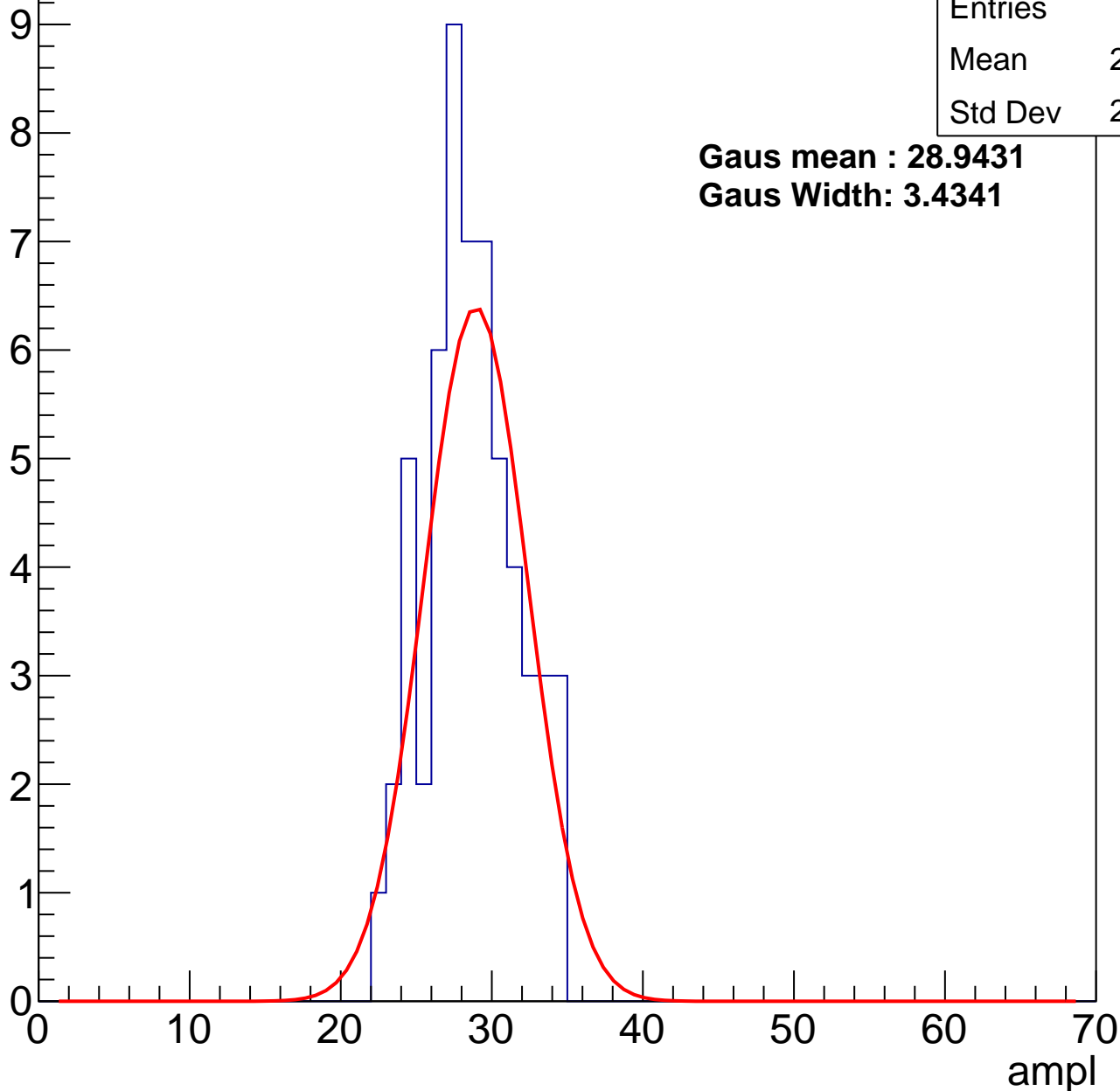
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	28.19
Std Dev	2.988

Gaus mean : 28.9431

Gaus Width: 3.4341



B0L002S, U5-ch27, adc1

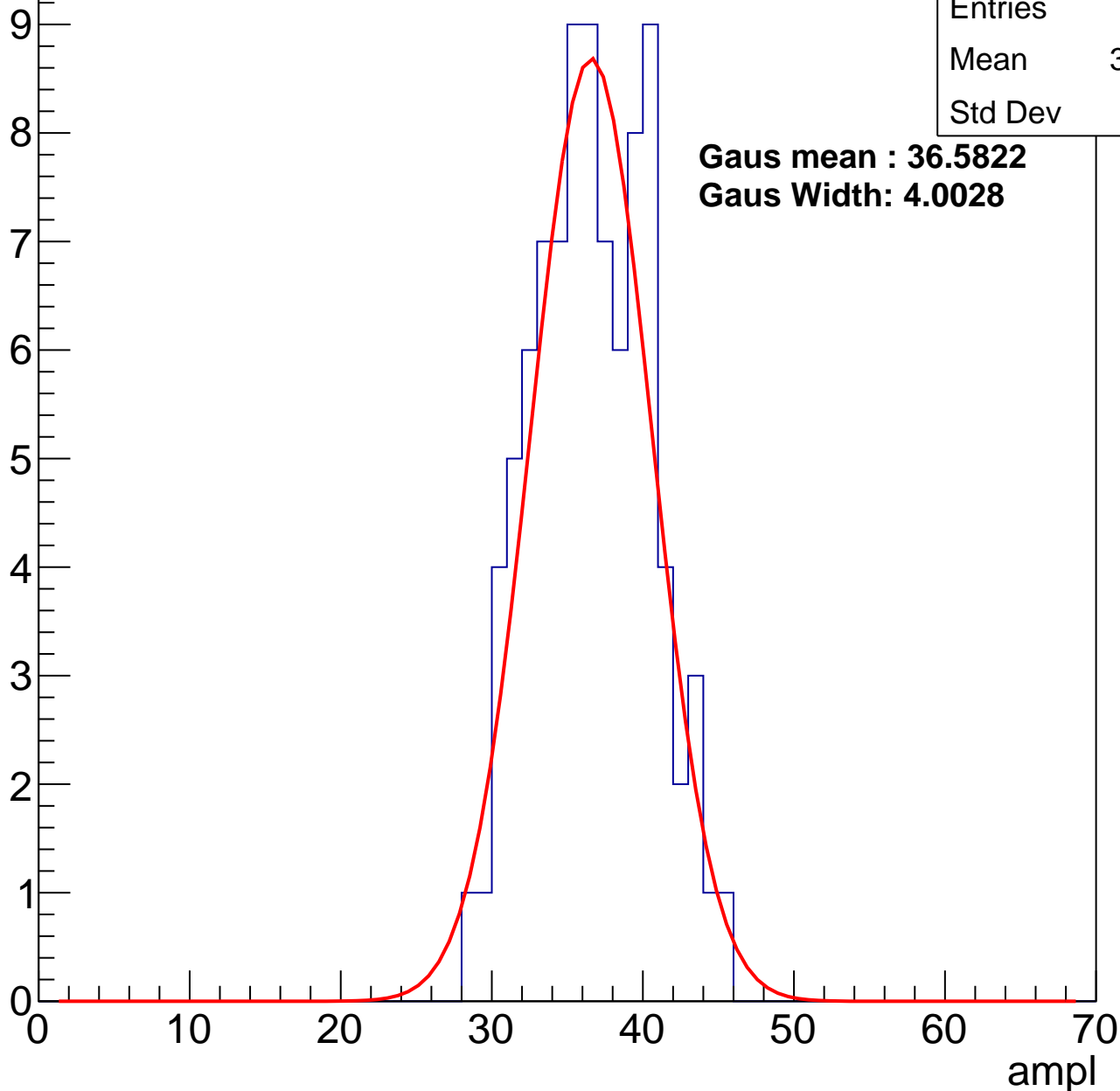
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	90
Mean	36.19
Std Dev	3.78

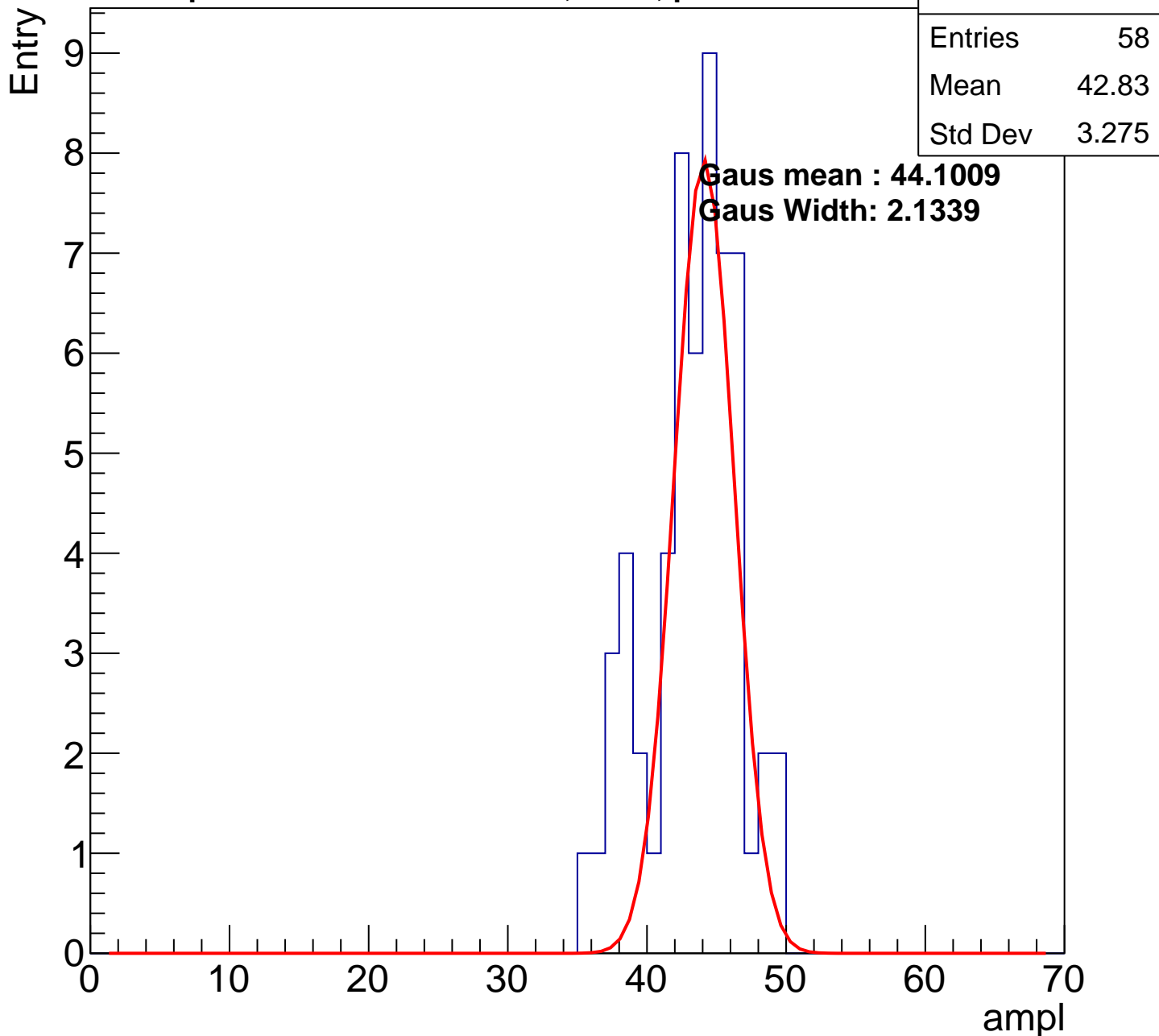
Gaus mean : 36.5822

Gaus Width: 4.0028



B0L002S, U5-ch27, adc2

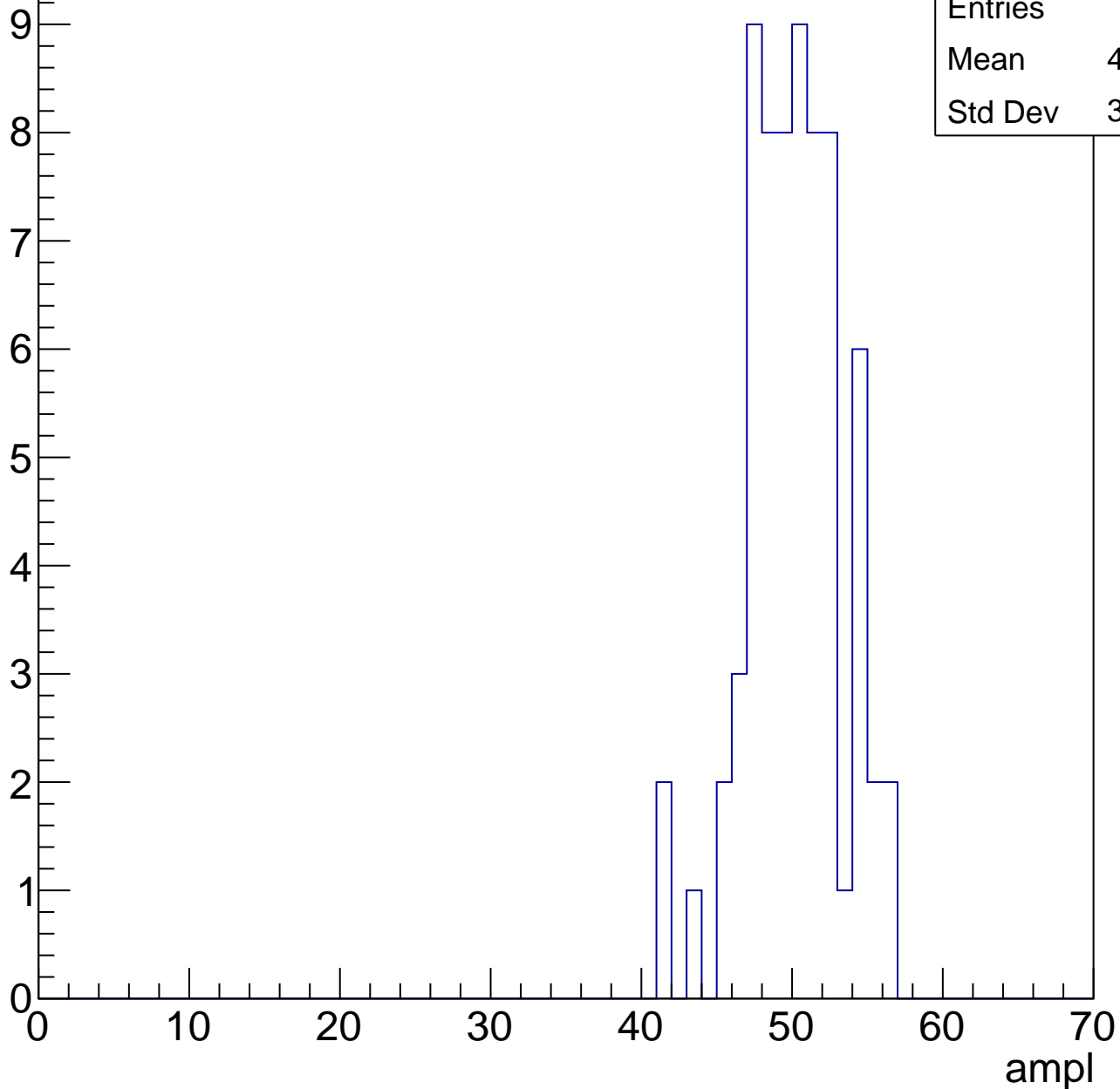
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

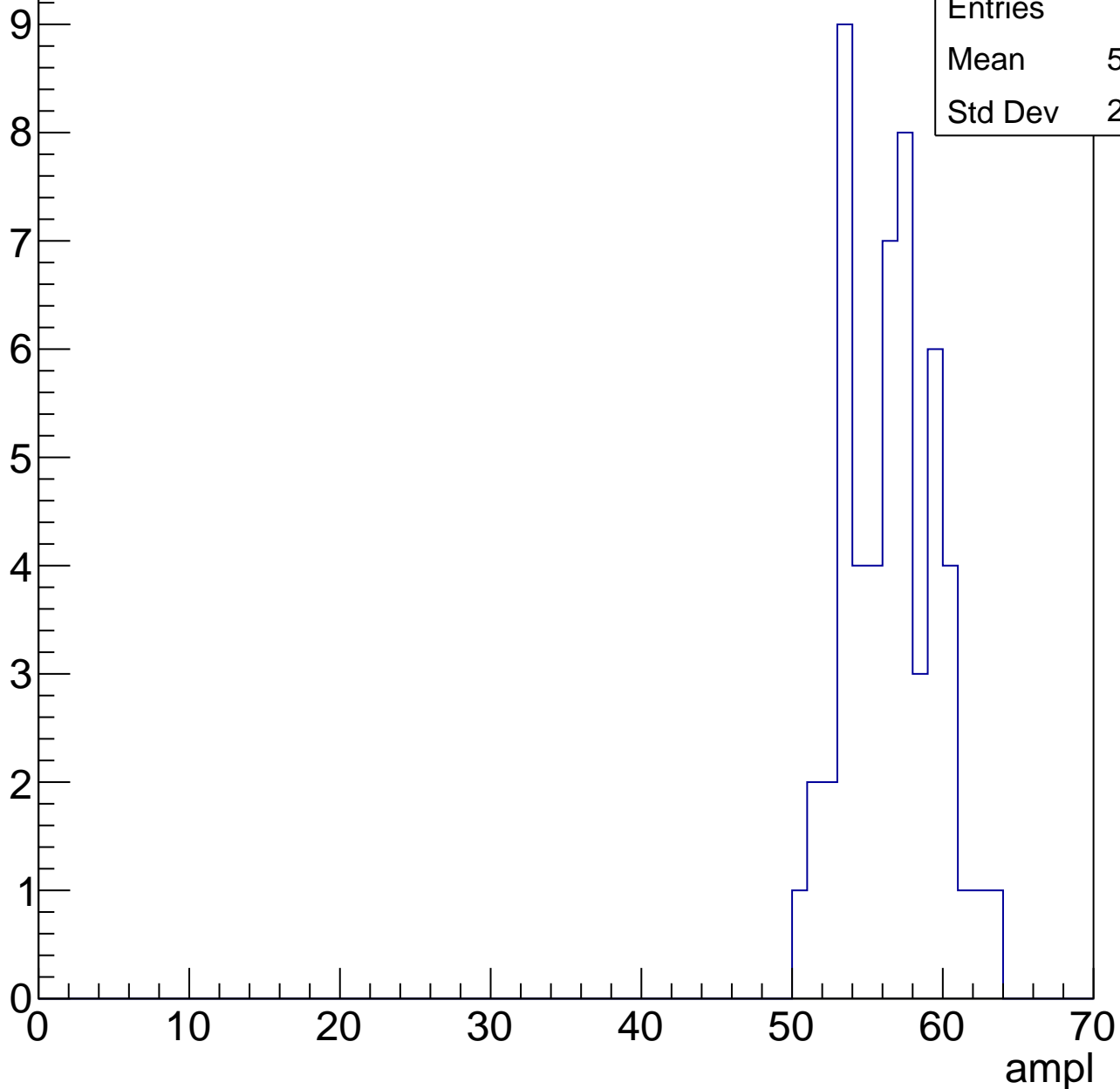


Entries	69
Mean	49.64
Std Dev	3.158

B0L002S, U5-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

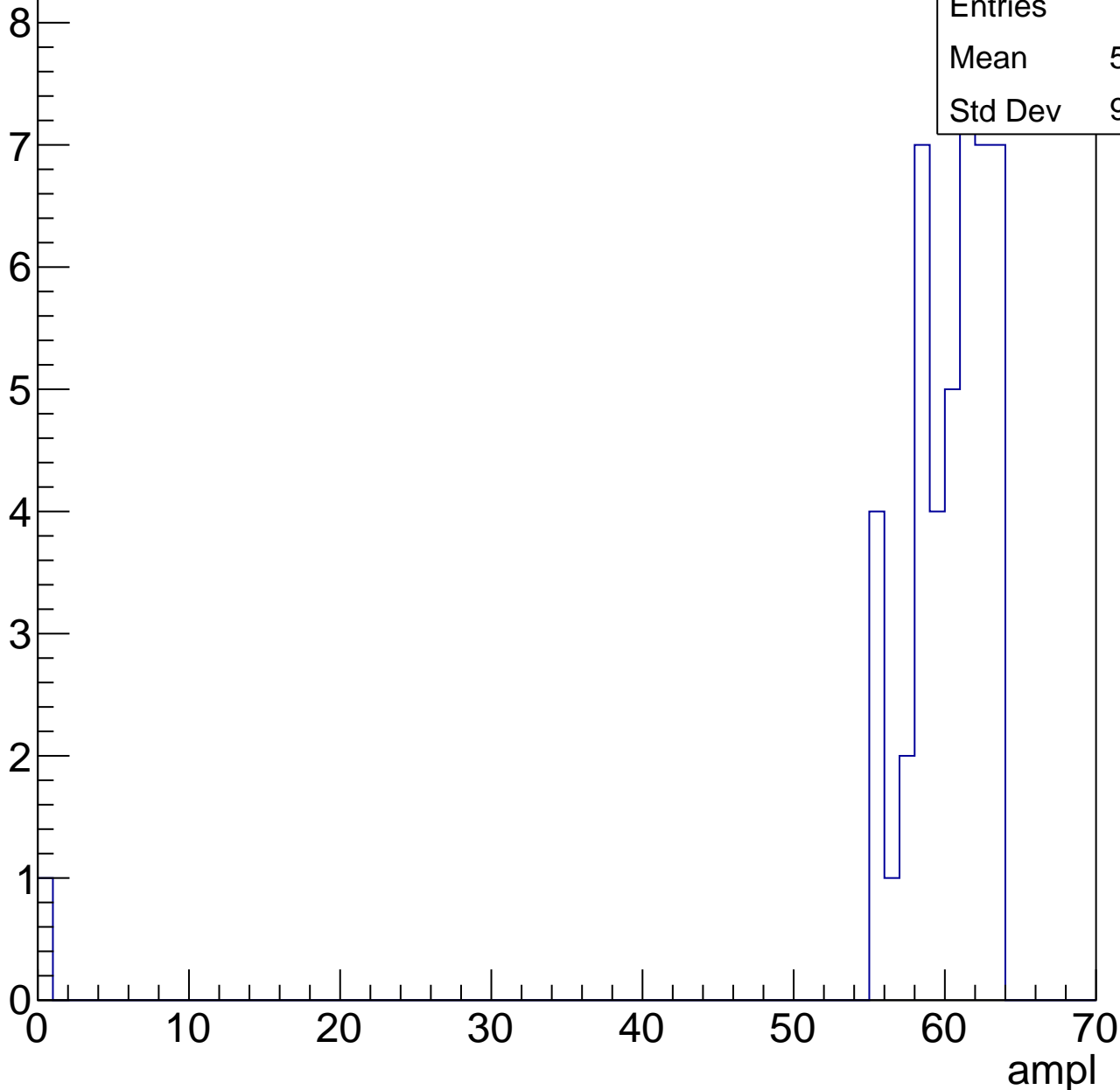


B0L002S, U5-ch27, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

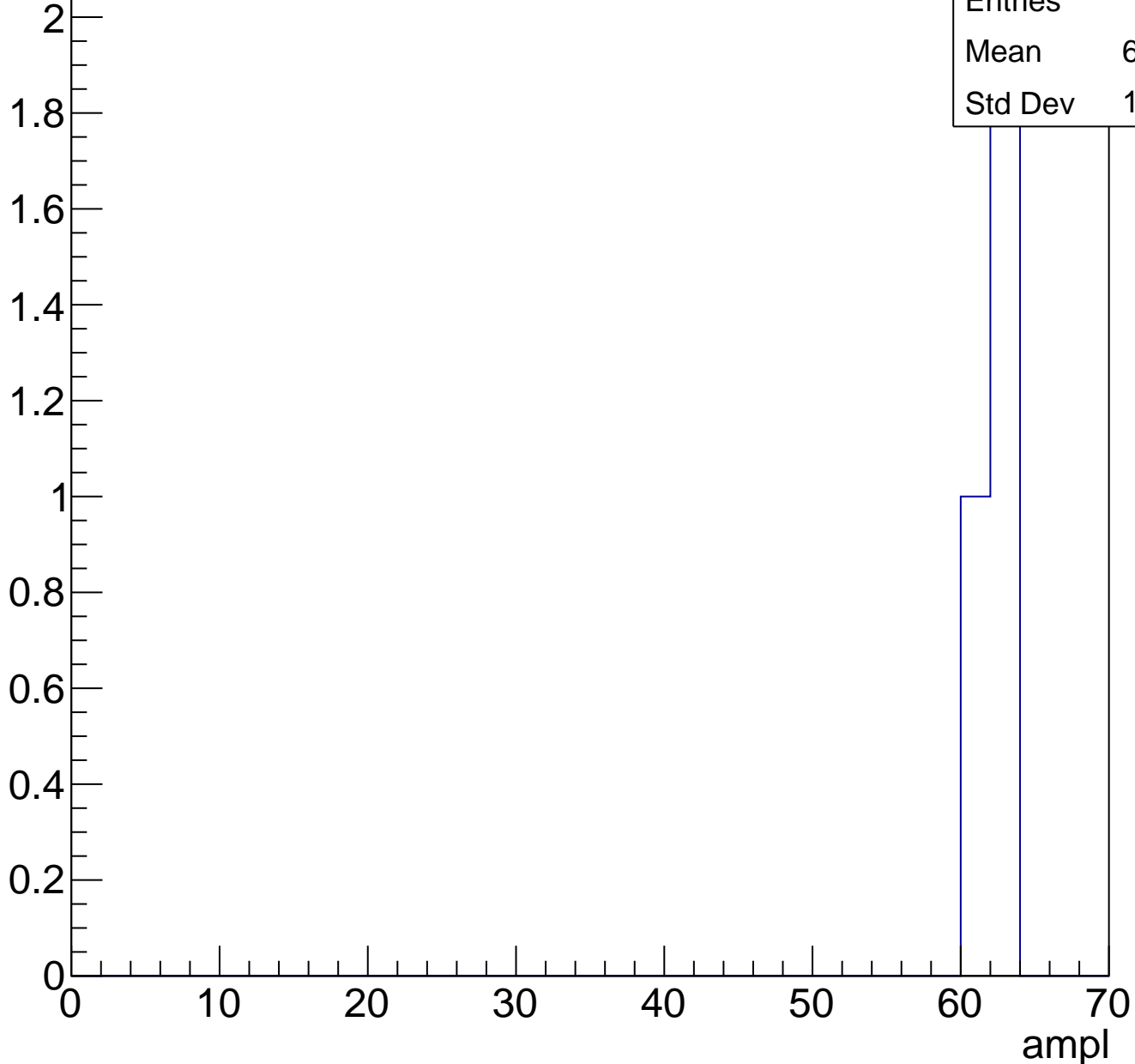
Entries	46
Mean	58.59
Std Dev	9.057



B0L002S, U5-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch28, adc0

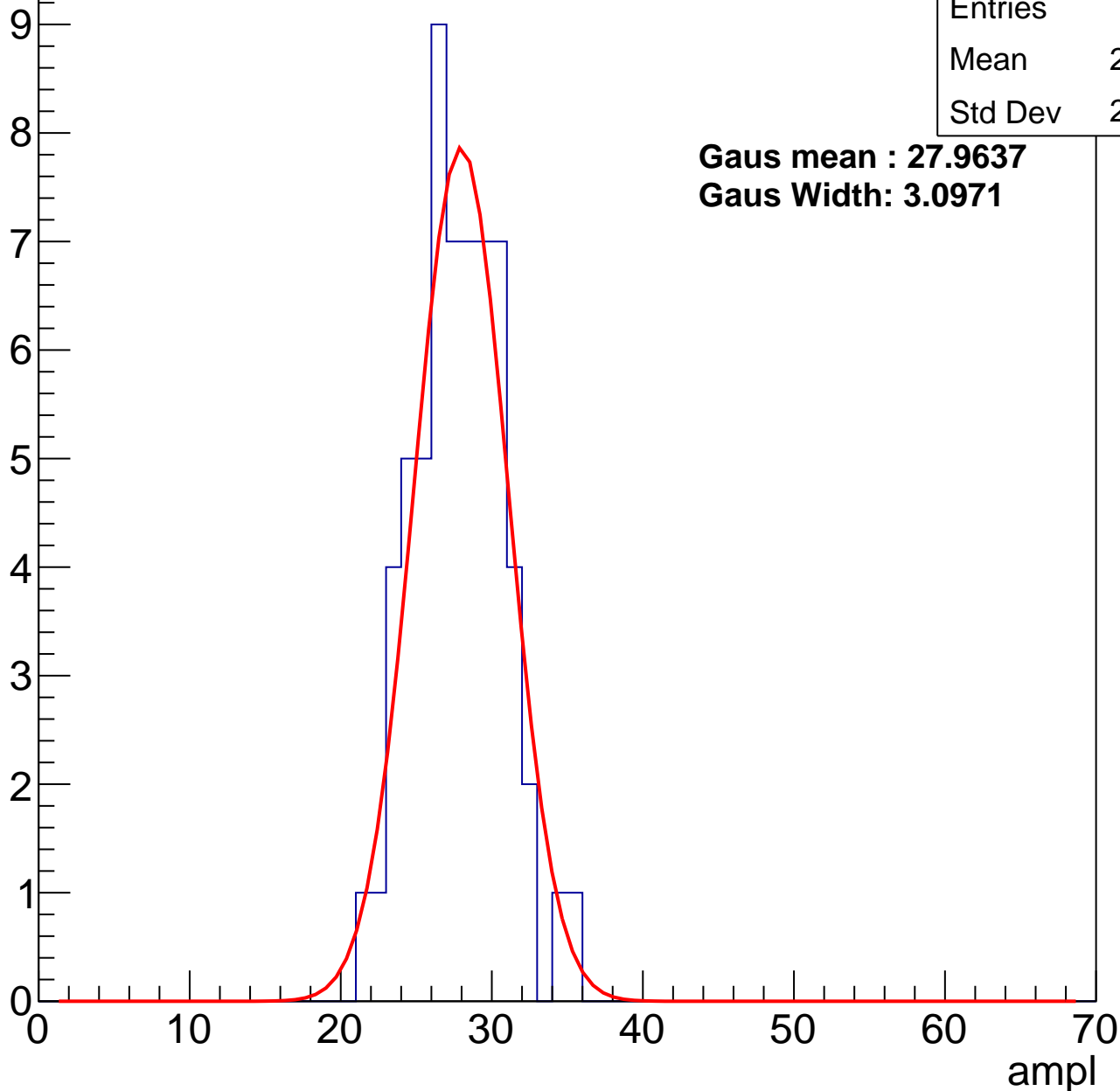
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	27.36
Std Dev	2.909

Gaus mean : 27.9637

Gaus Width: 3.0971



B0L002S, U5-ch28, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	97
Mean	35.39
Std Dev	3.668

Gaus mean : 35.7497

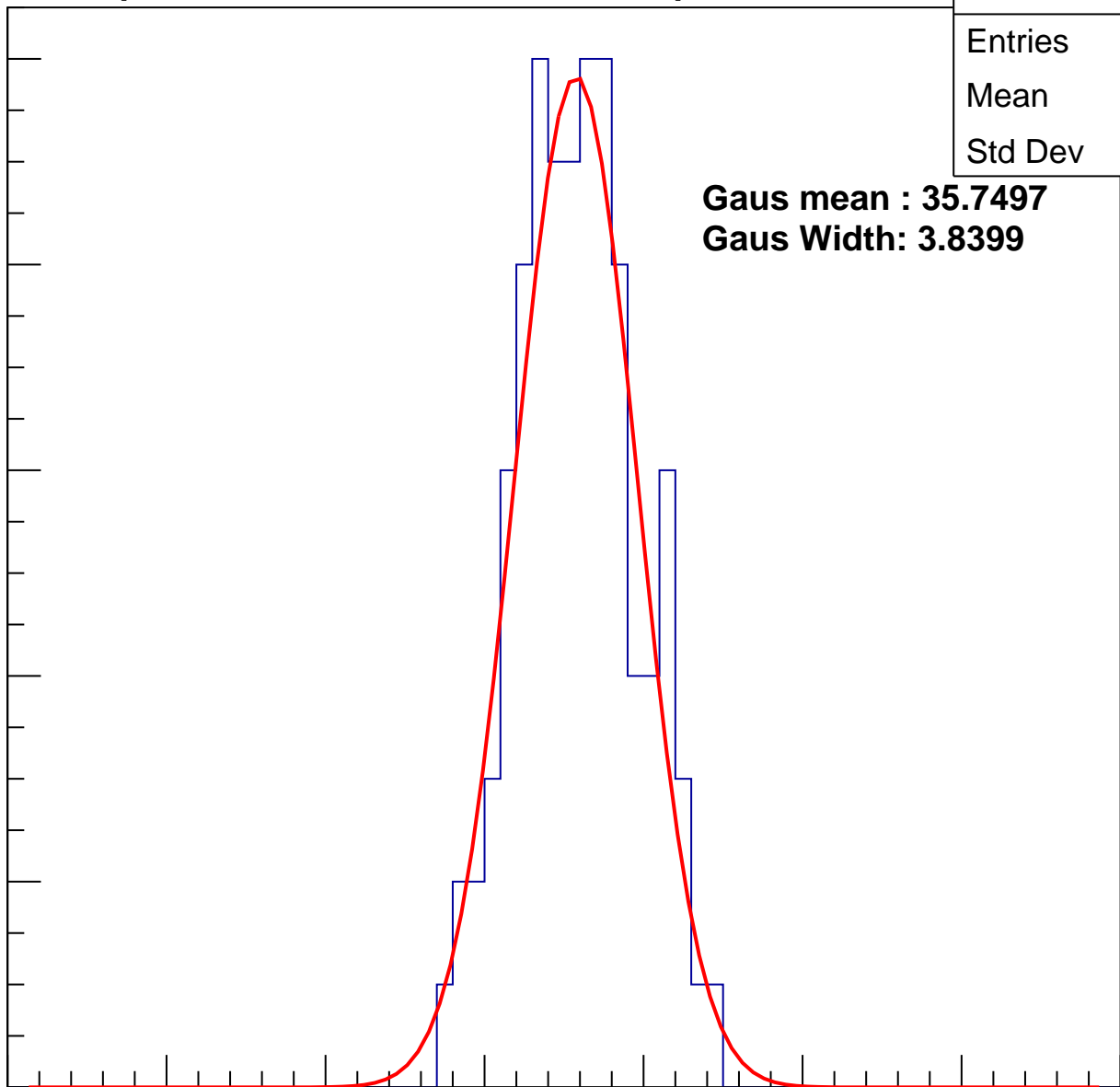
Gaus Width: 3.8399

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch28, adc2

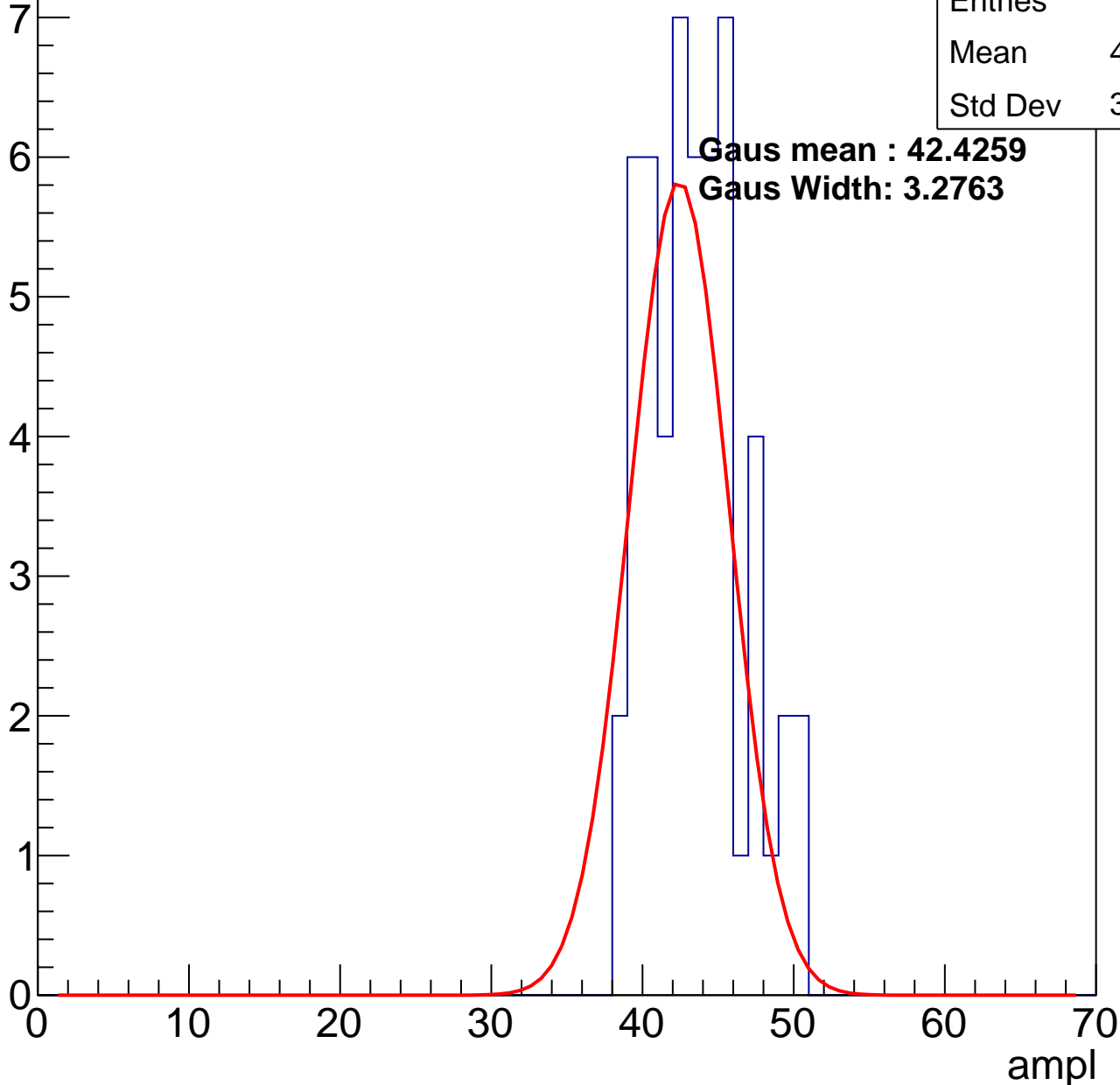
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.06
Std Dev	3.118

Gaus mean : 42.4259

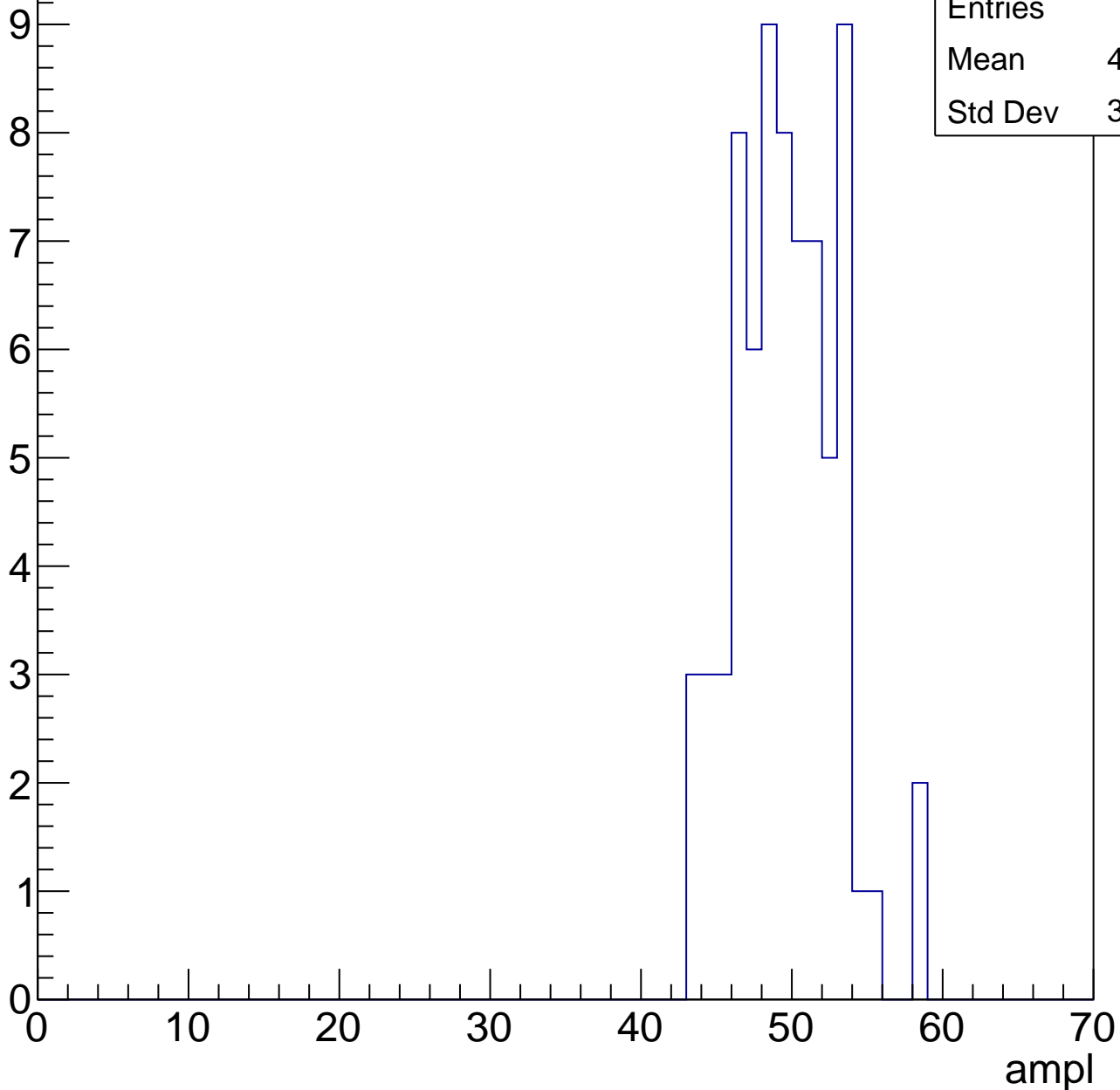
Gaus Width: 3.2763



B0L002S, U5-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

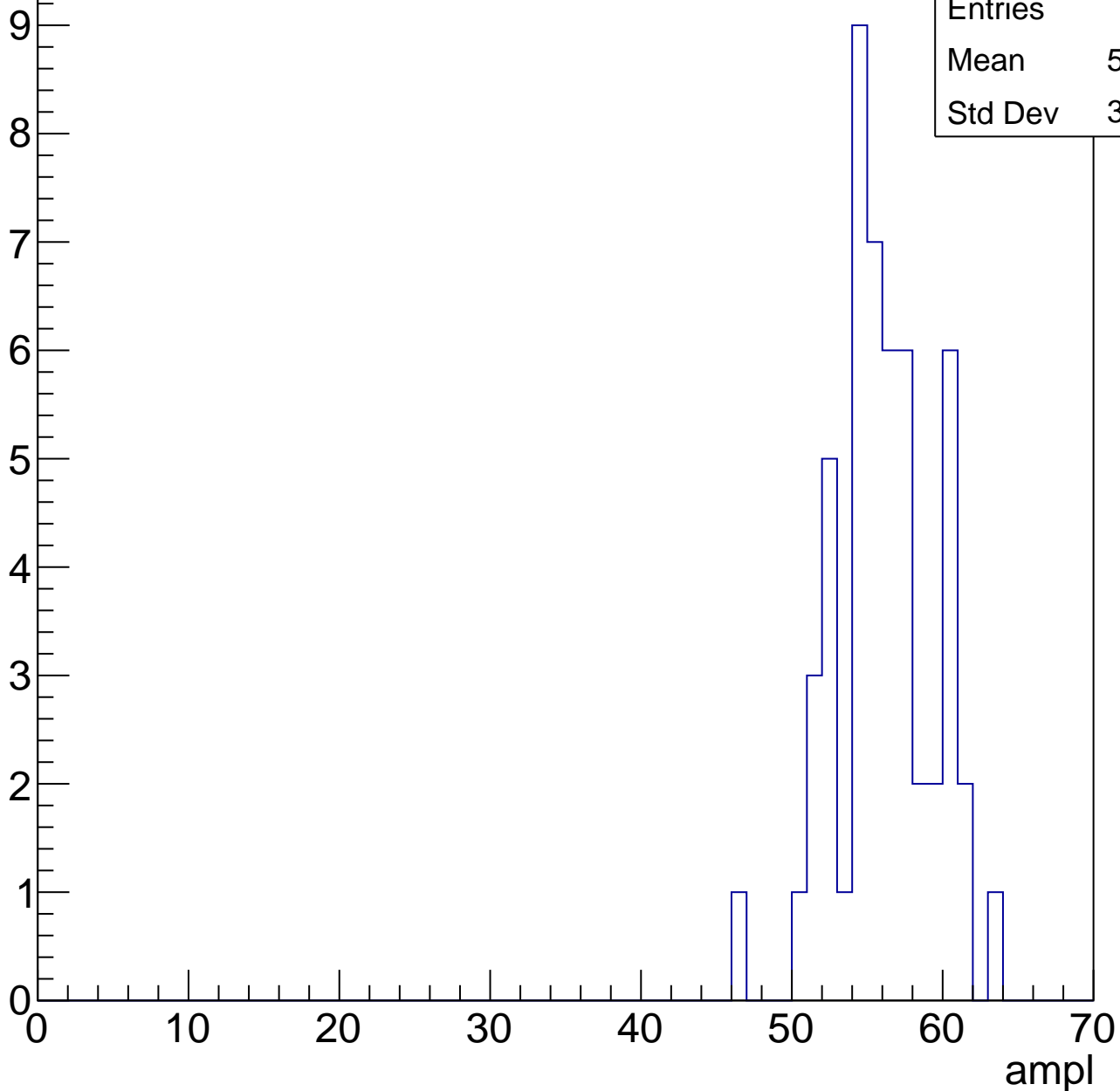


B0L002S, U5-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

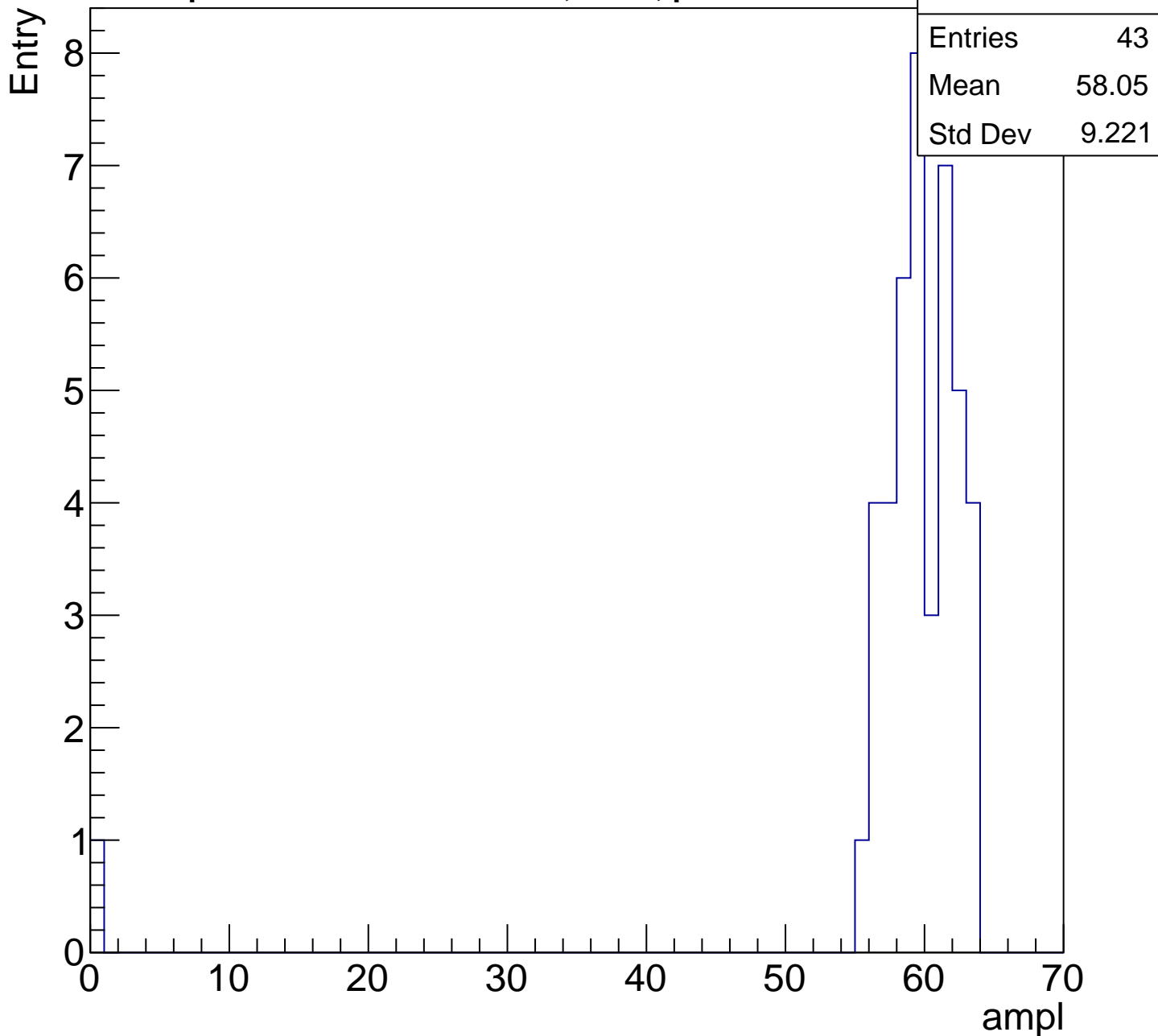
Entry

Entries	52
Mean	55.58
Std Dev	3.284



B0L002S, U5-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

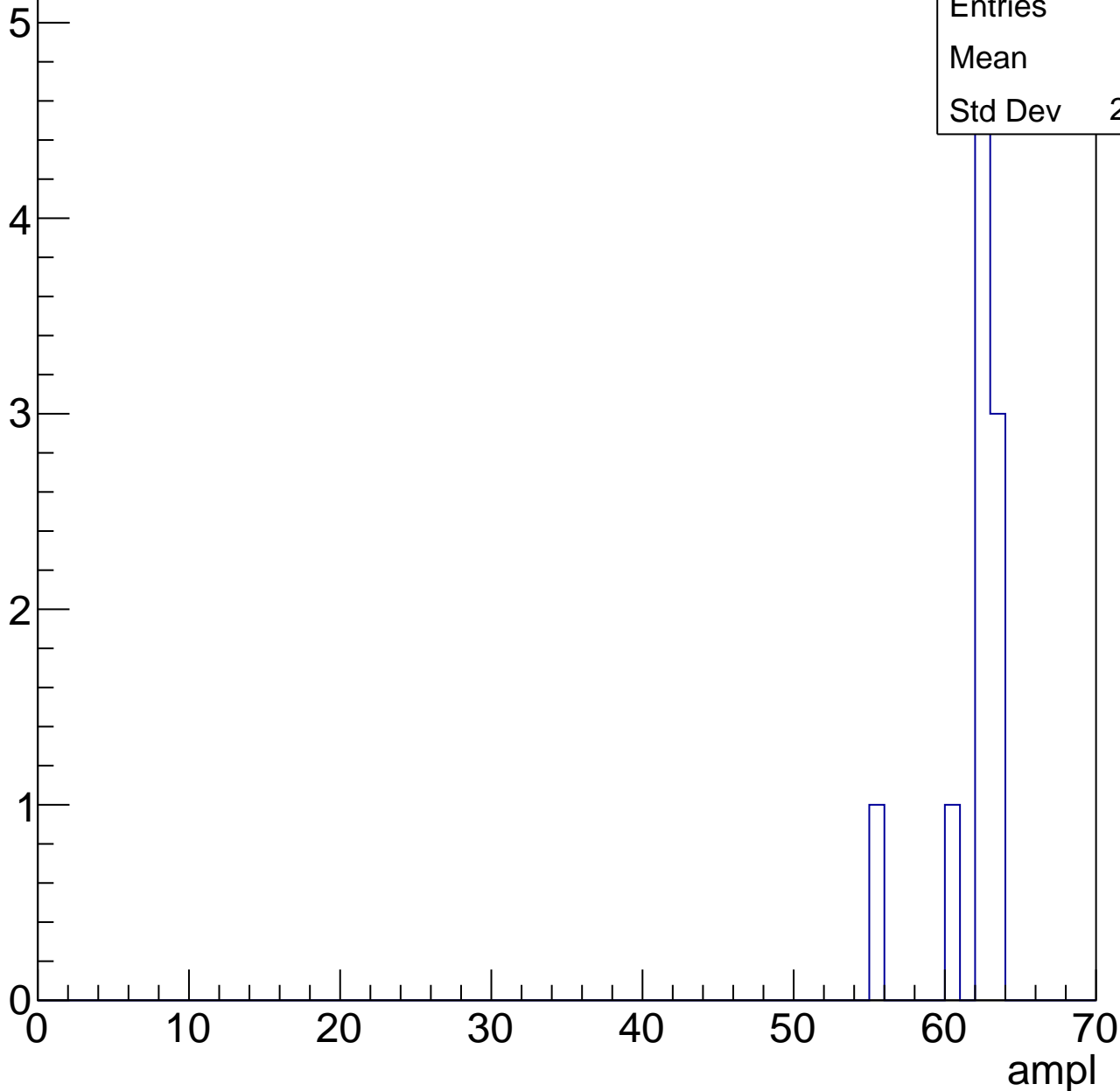


B0L002S, U5-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

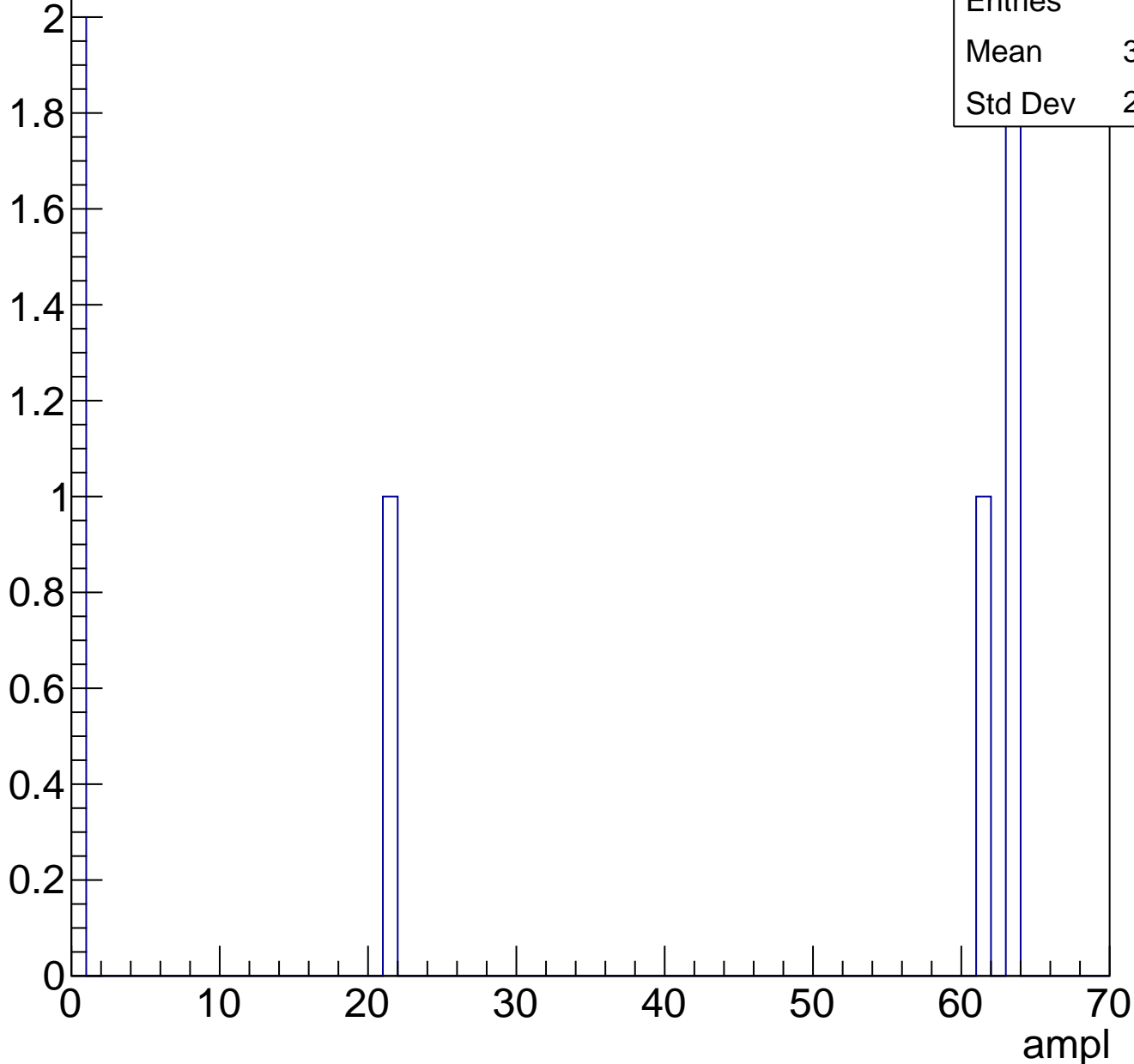
Entries	10
Mean	61.4
Std Dev	2.289



B0L002S, U5-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	6
Mean	34.67
Std Dev	28.55

B0L002S, U5-ch29, adc0

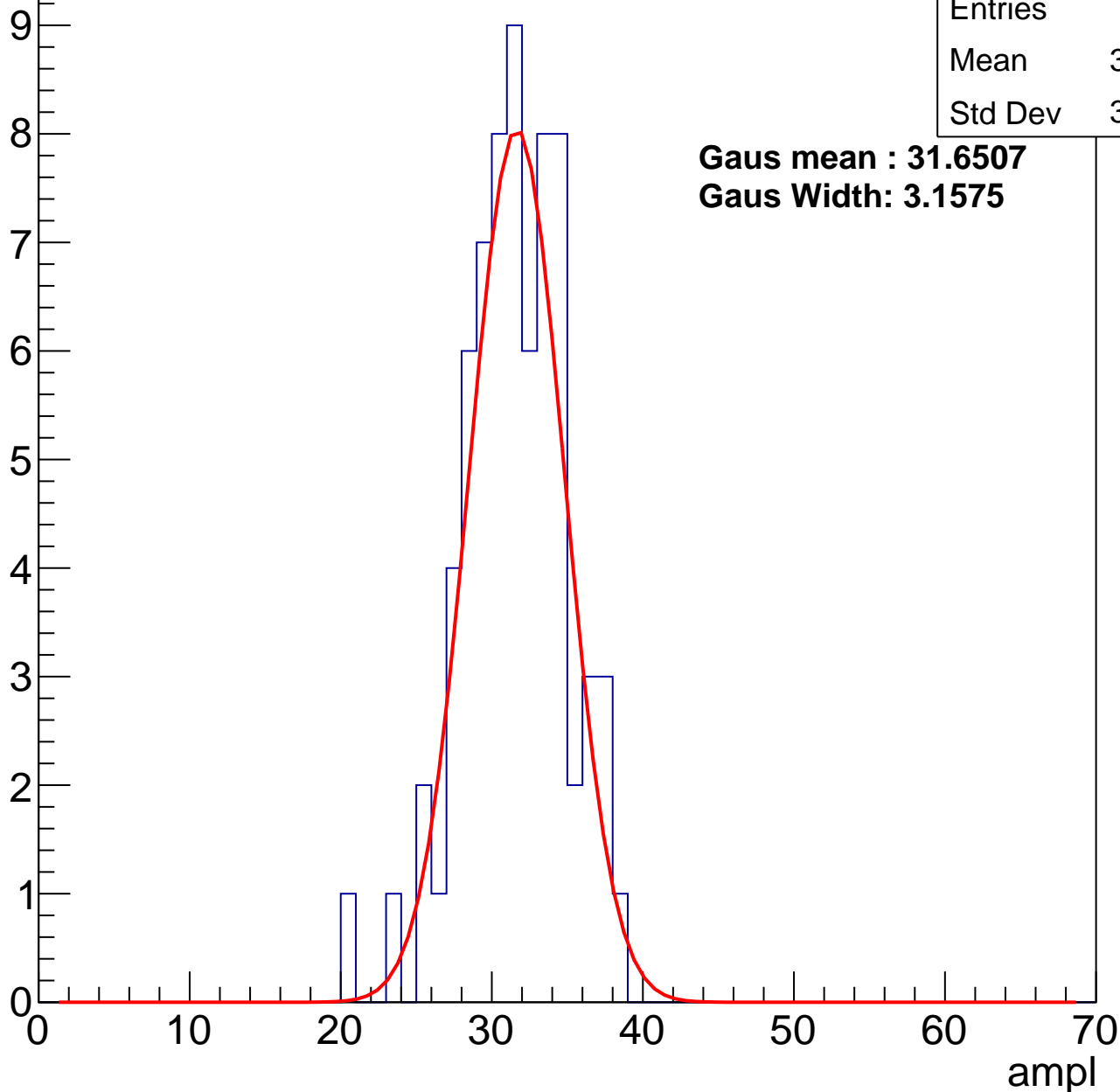
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	31.03
Std Dev	3.402

Gaus mean : 31.6507

Gaus Width: 3.1575



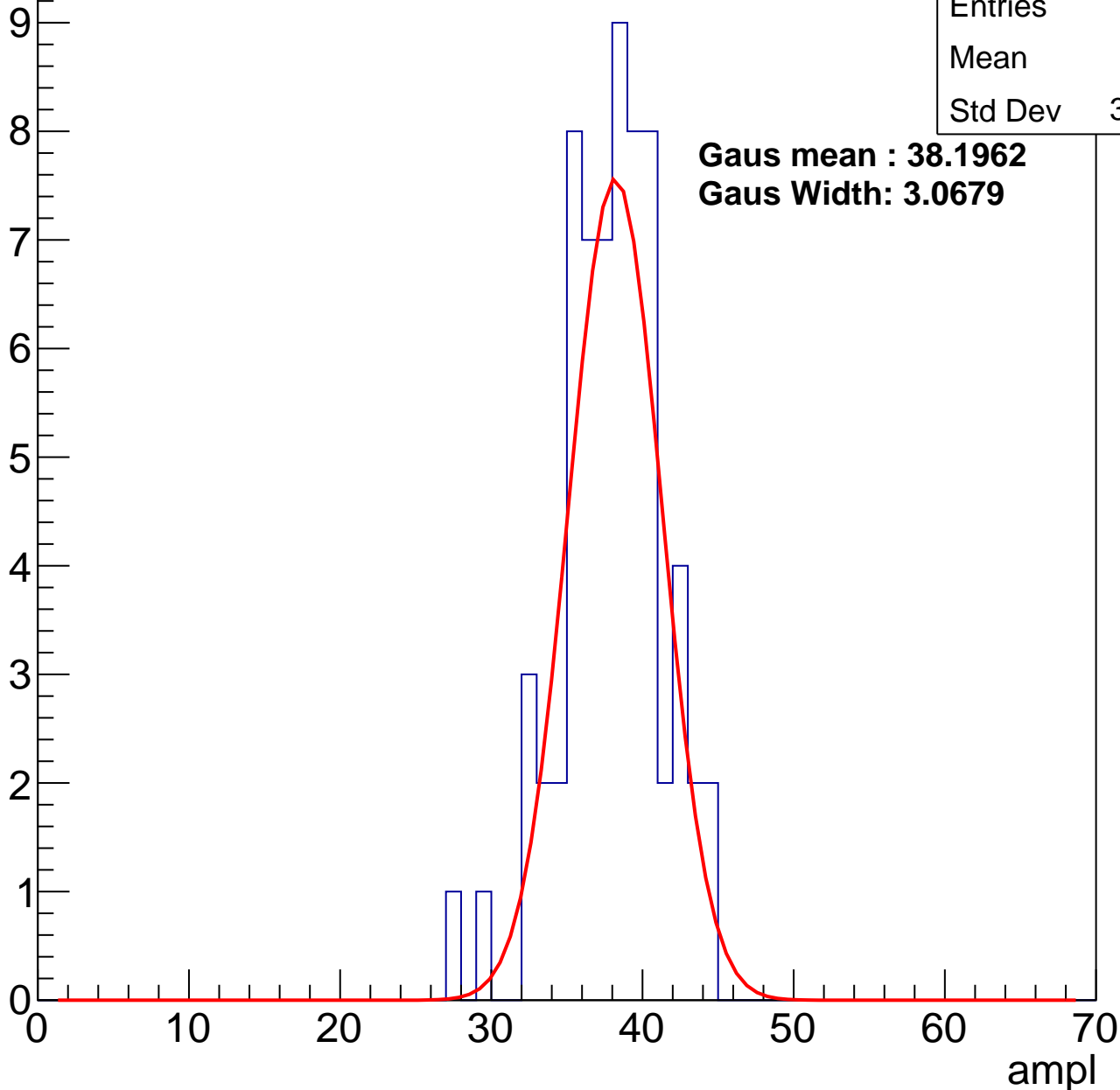
B0L002S, U5-ch29, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

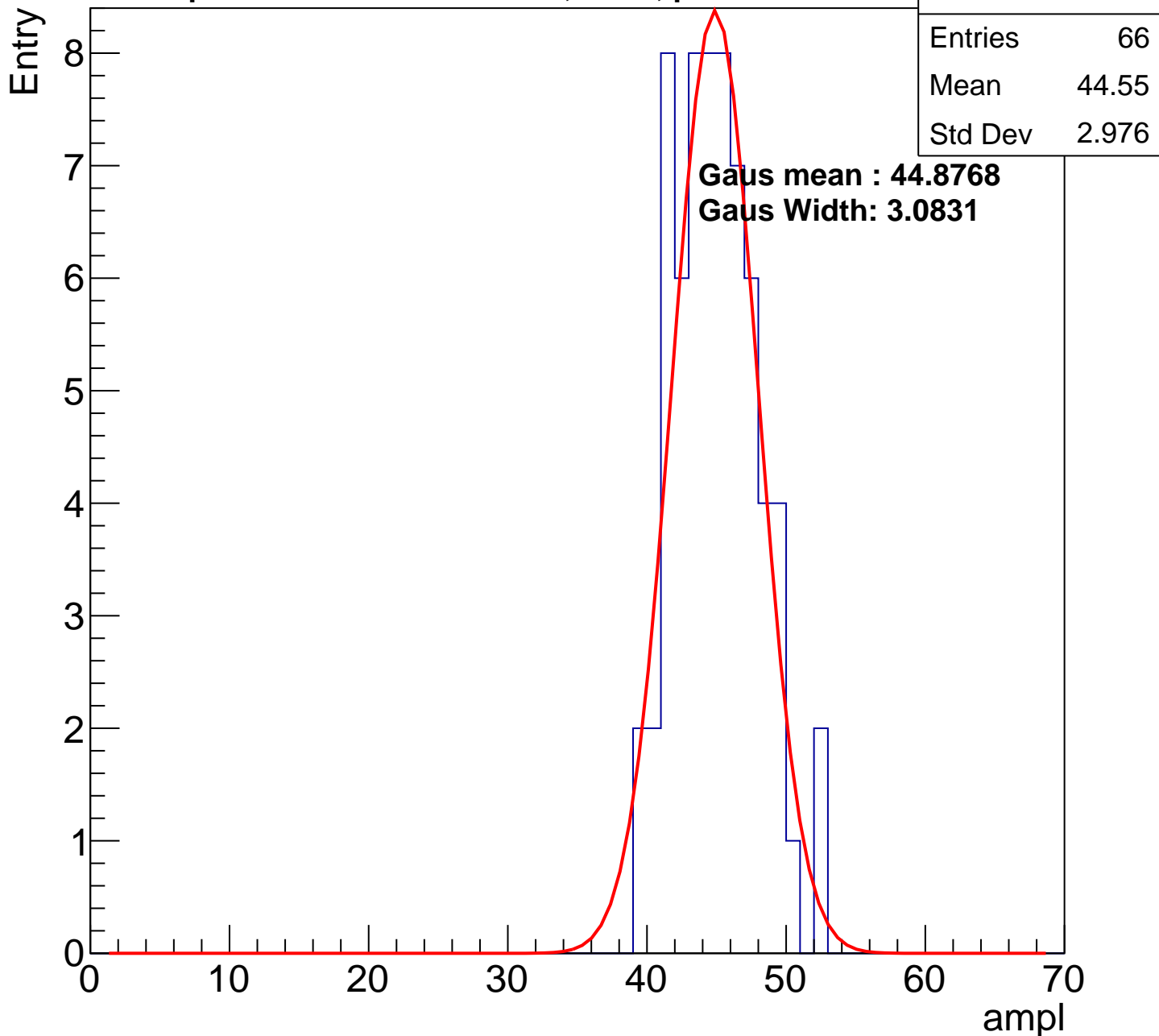
Entries	66
Mean	37.5
Std Dev	3.318

Gaus mean : 38.1962
Gaus Width: 3.0679



B0L002S, U5-ch29, adc2

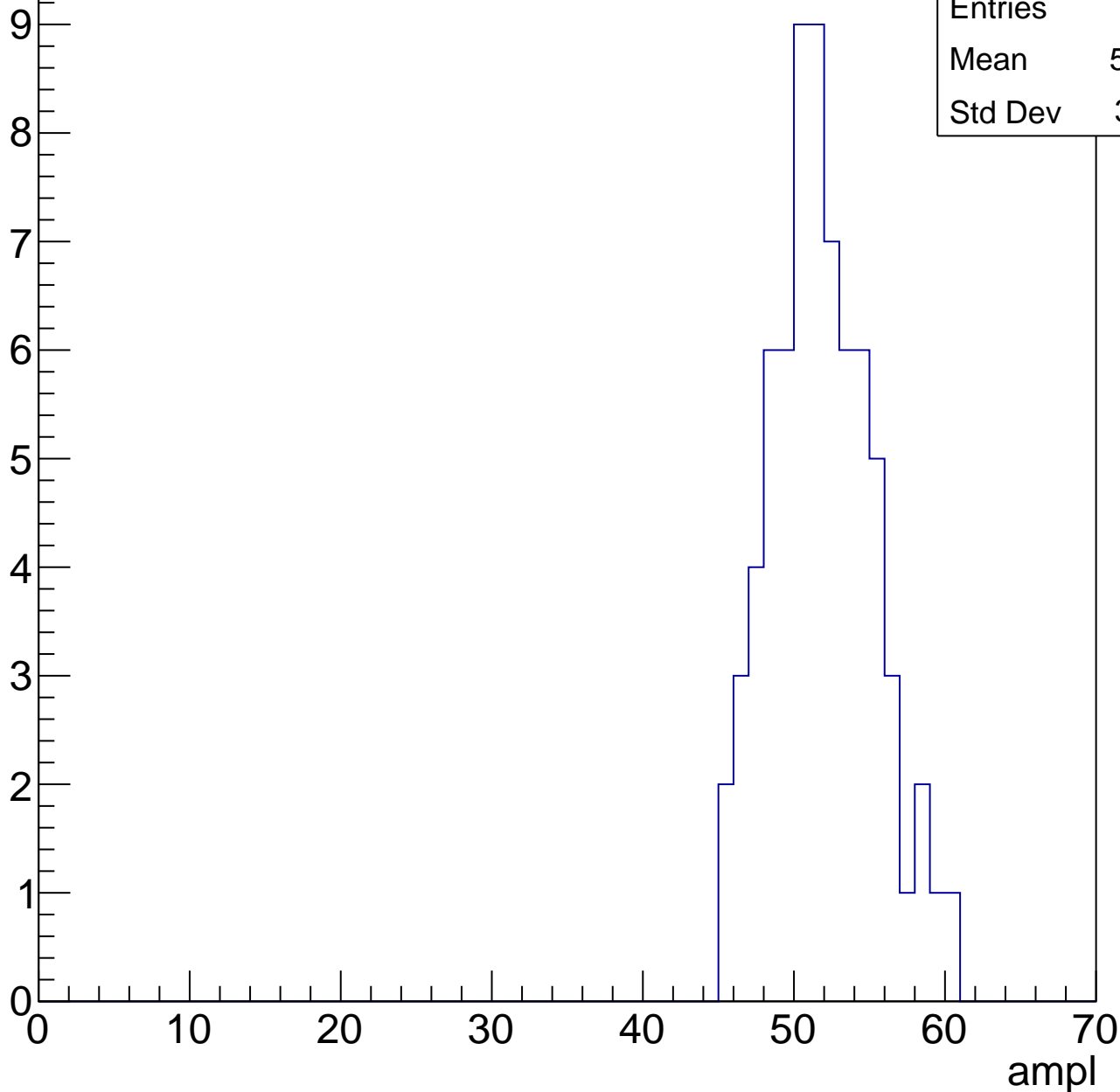
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

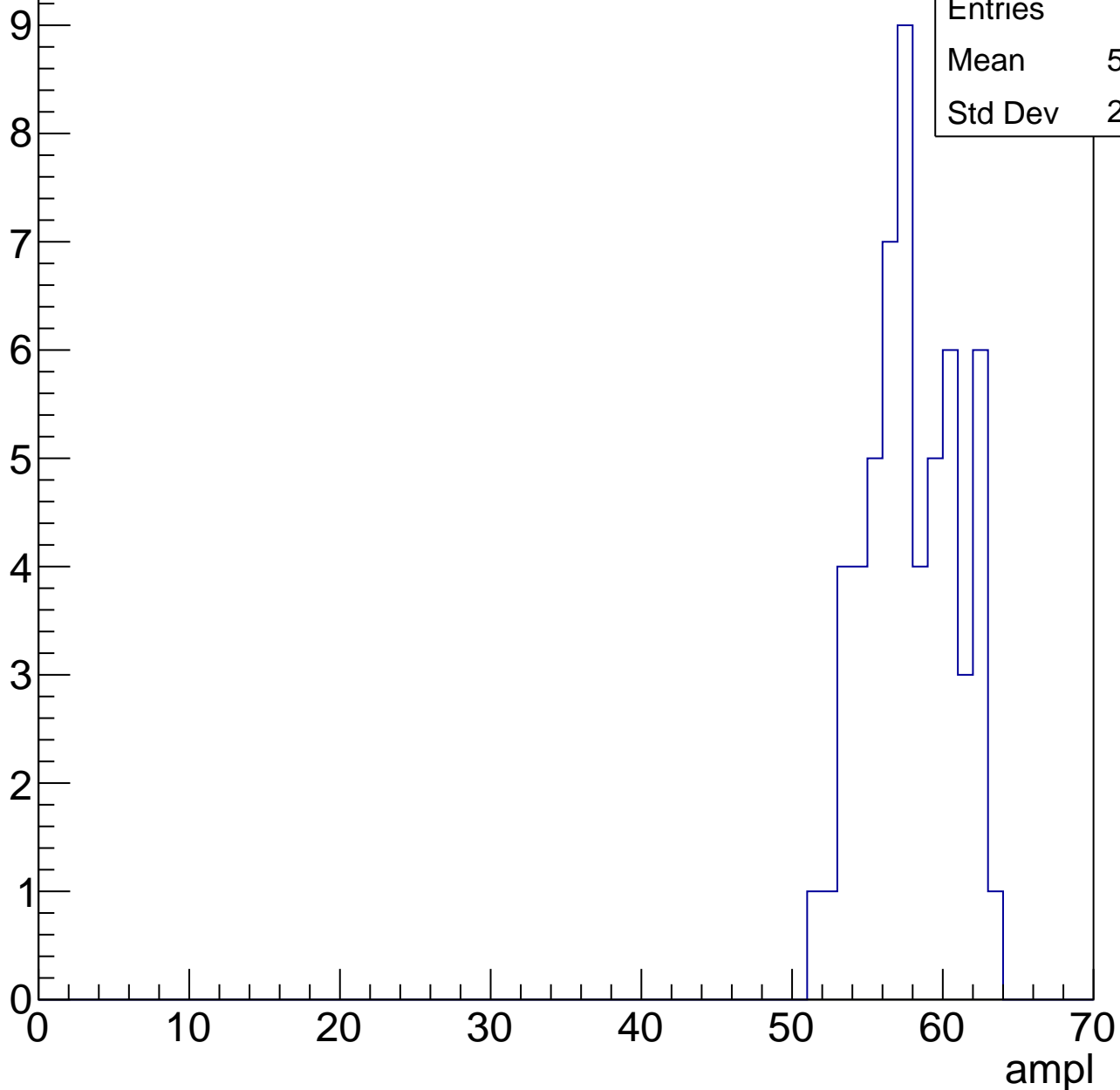


Entries	71
Mean	51.38
Std Dev	3.371

B0L002S, U5-ch29, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

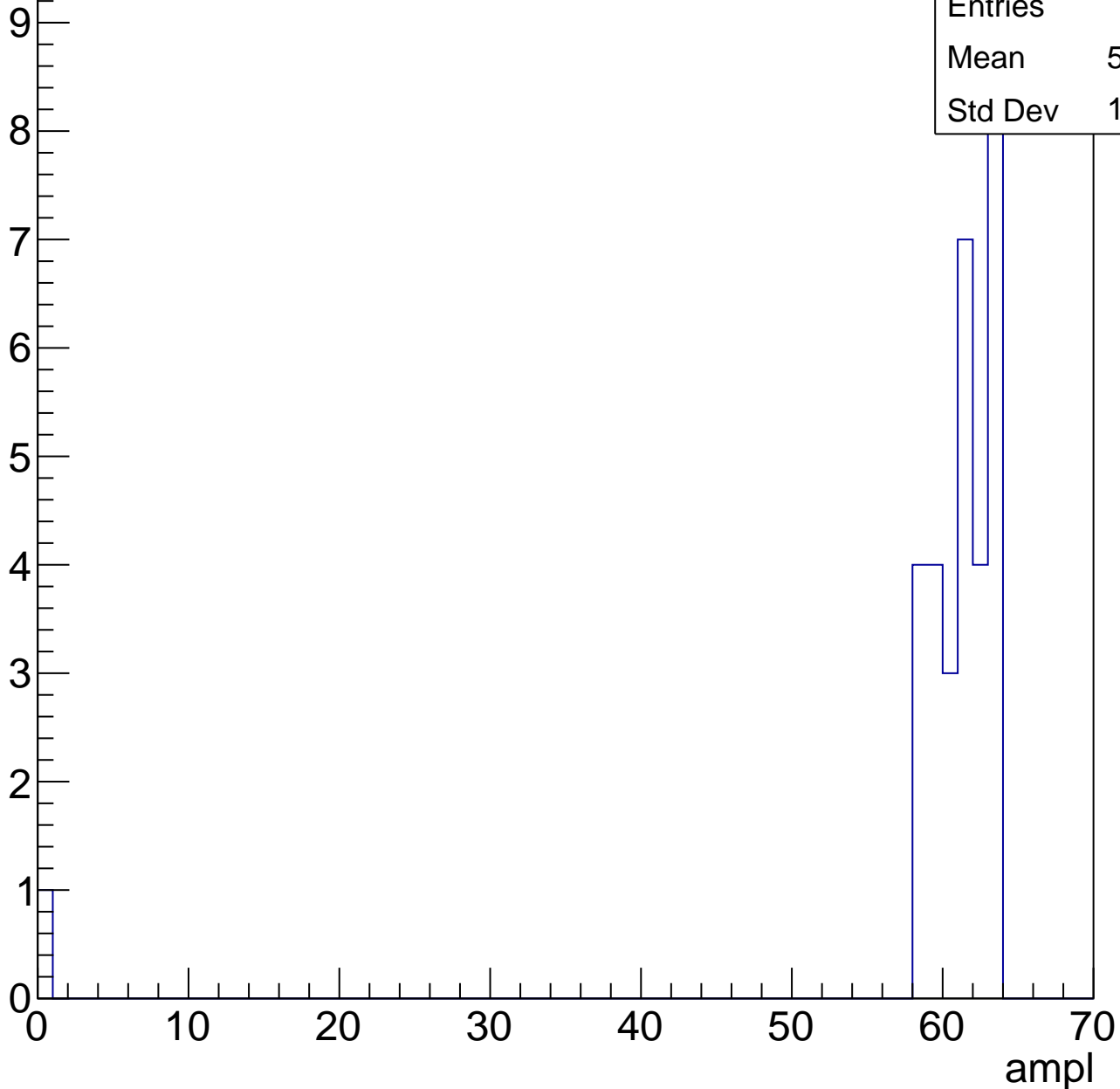


B0L002S, U5-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59.06
Std Dev	10.75



B0L002S, U5-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch30, adc0

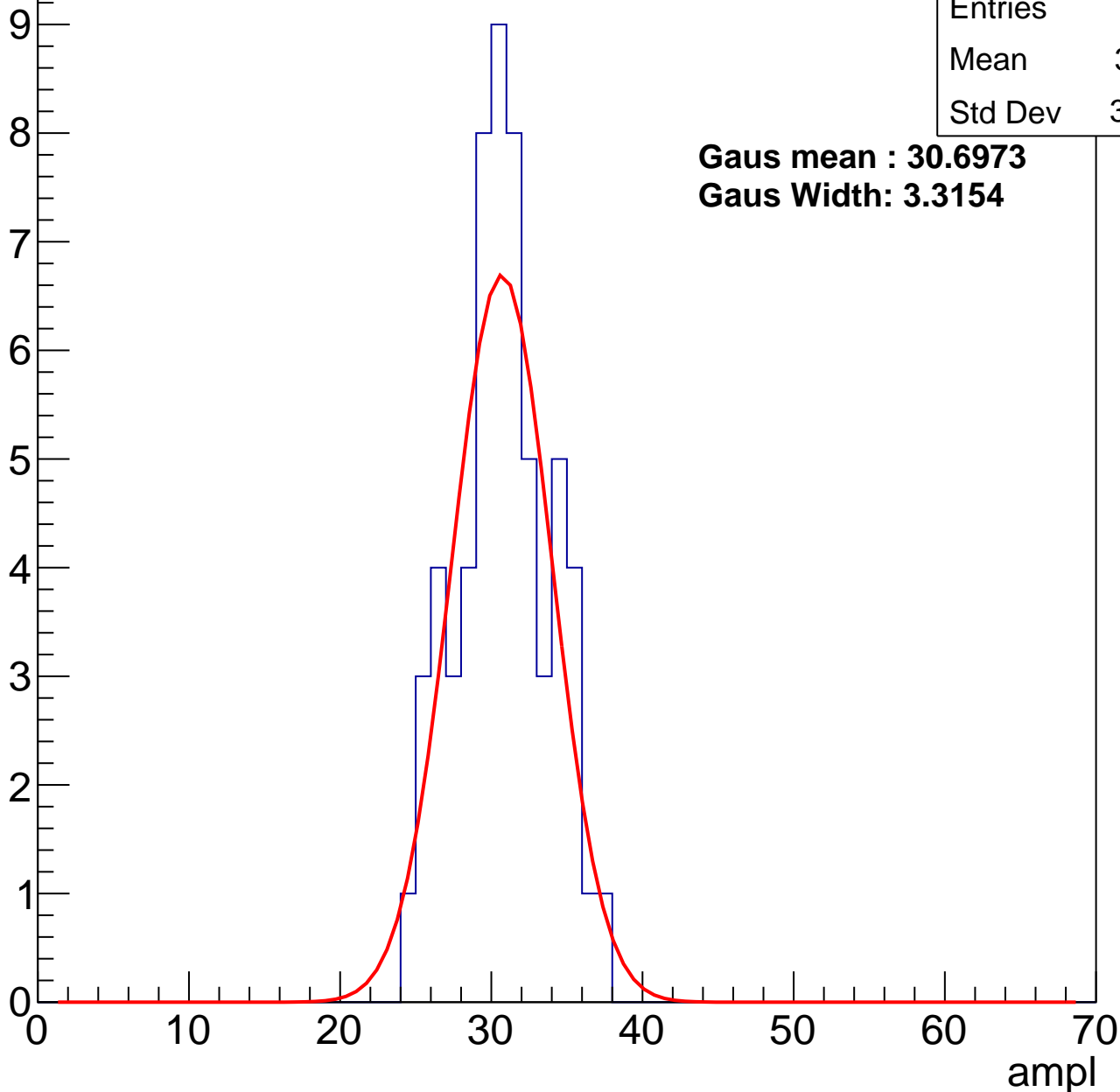
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.31
Std Dev	3.027

Gaus mean : 30.6973

Gaus Width: 3.3154



B0L002S, U5-ch30, adc1

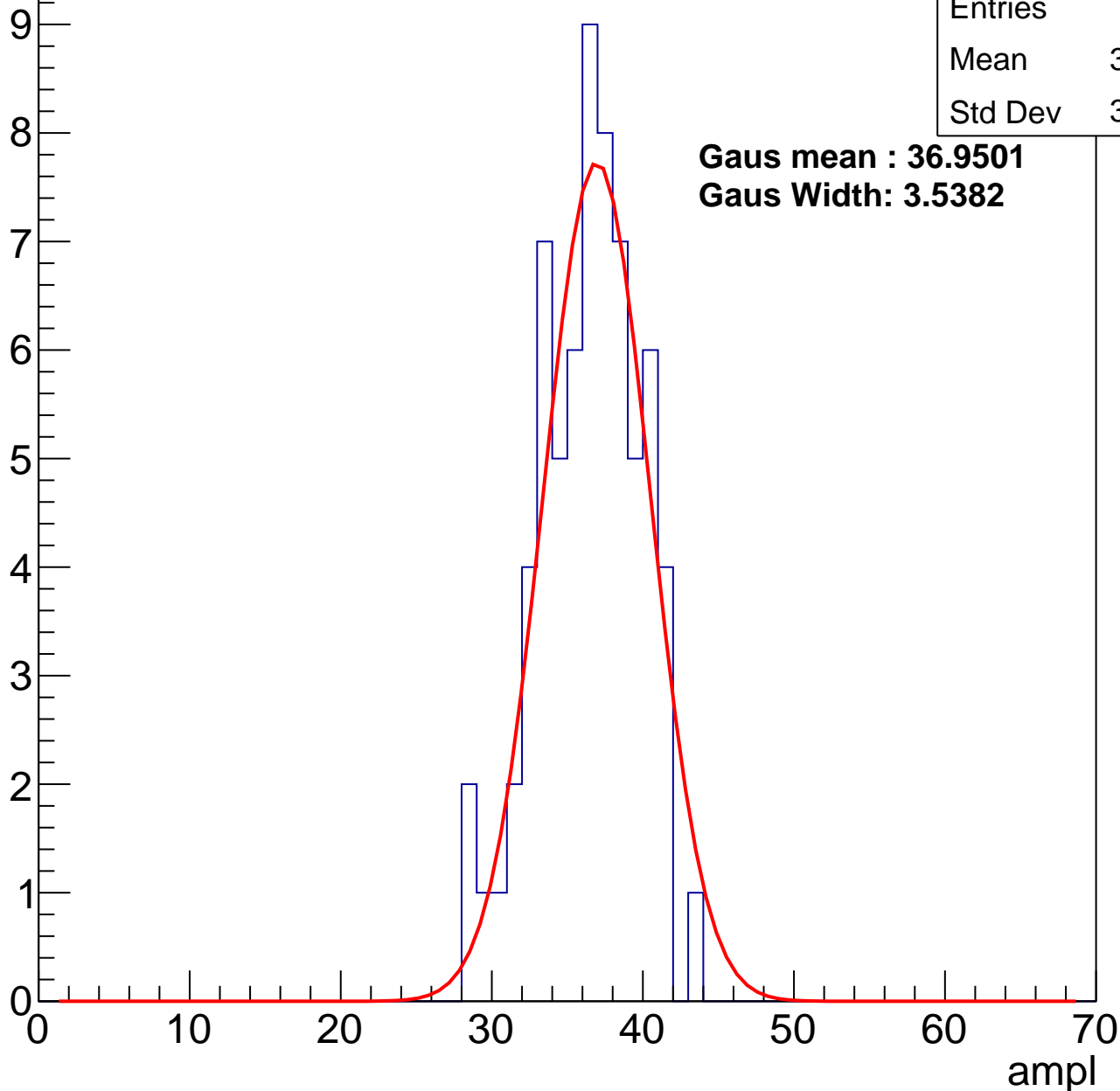
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.94
Std Dev	3.303

Gaus mean : 36.9501

Gaus Width: 3.5382



B0L002S, U5-ch30, adc2

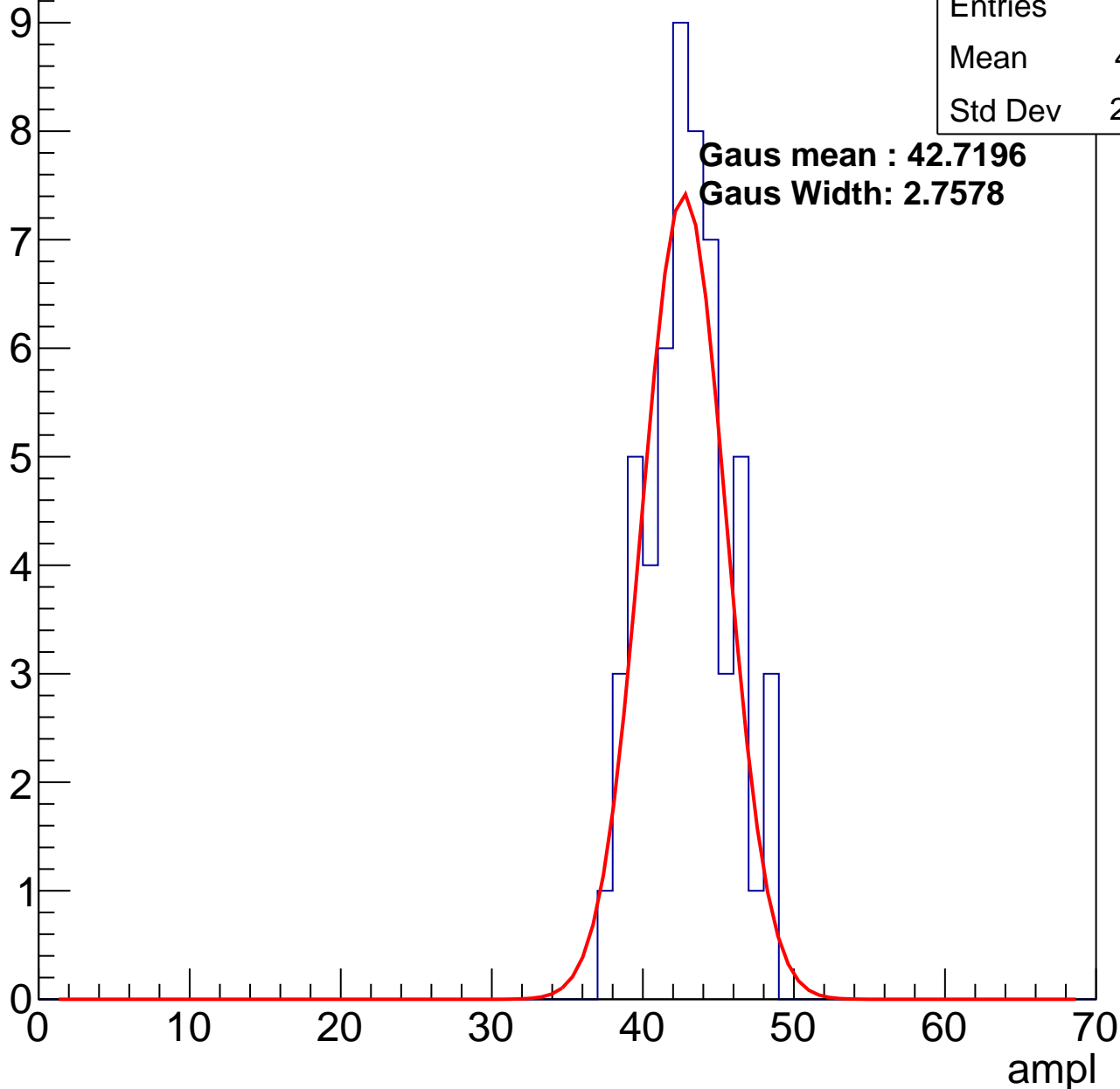
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	42.51
Std Dev	2.703

Gaus mean : 42.7196

Gaus Width: 2.7578



B0L002S, U5-ch30, adc3

calib_packv5_042523_0143.root, FC#8, port C1

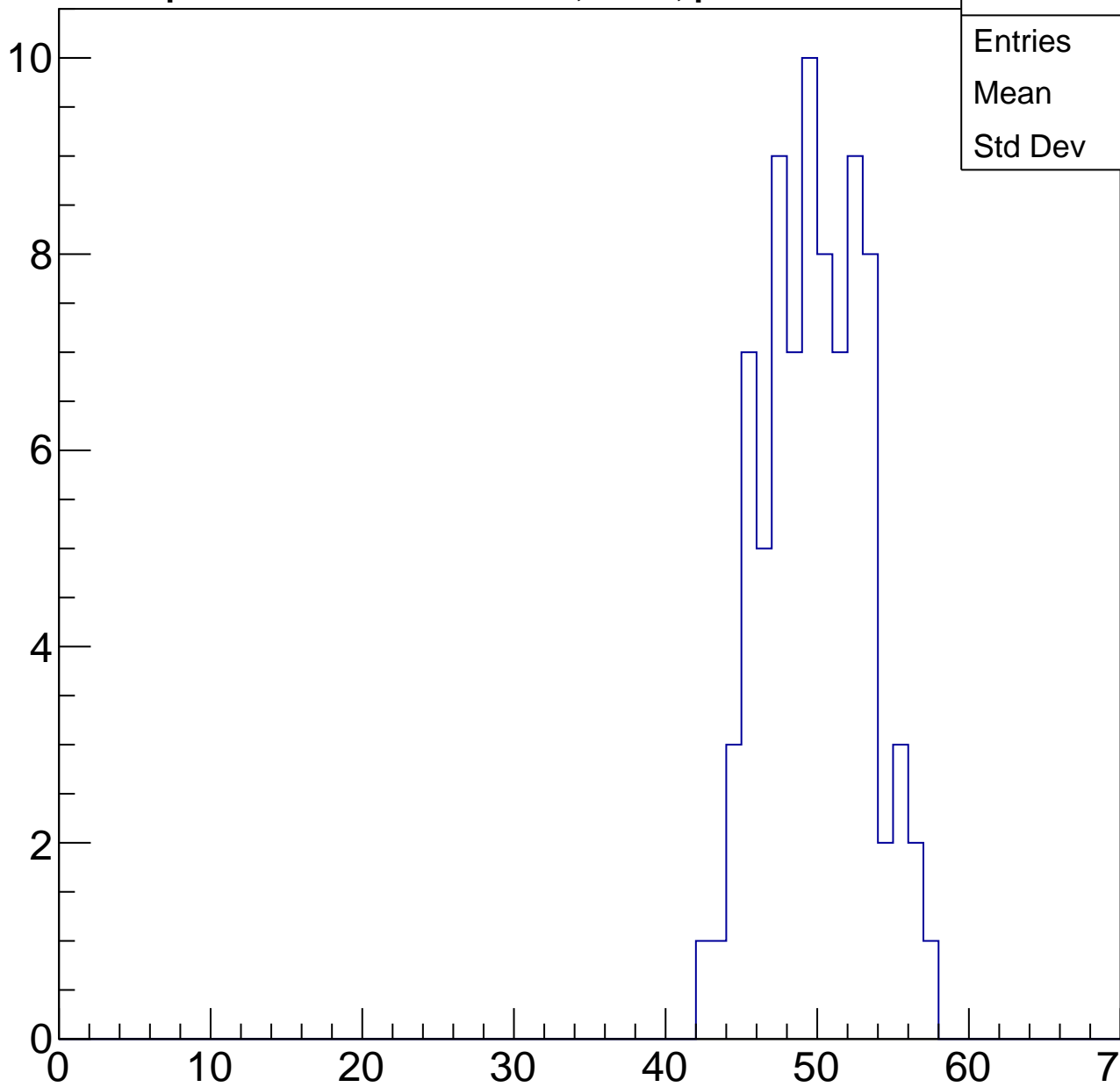
Entries	83
Mean	49.42
Std Dev	3.312

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

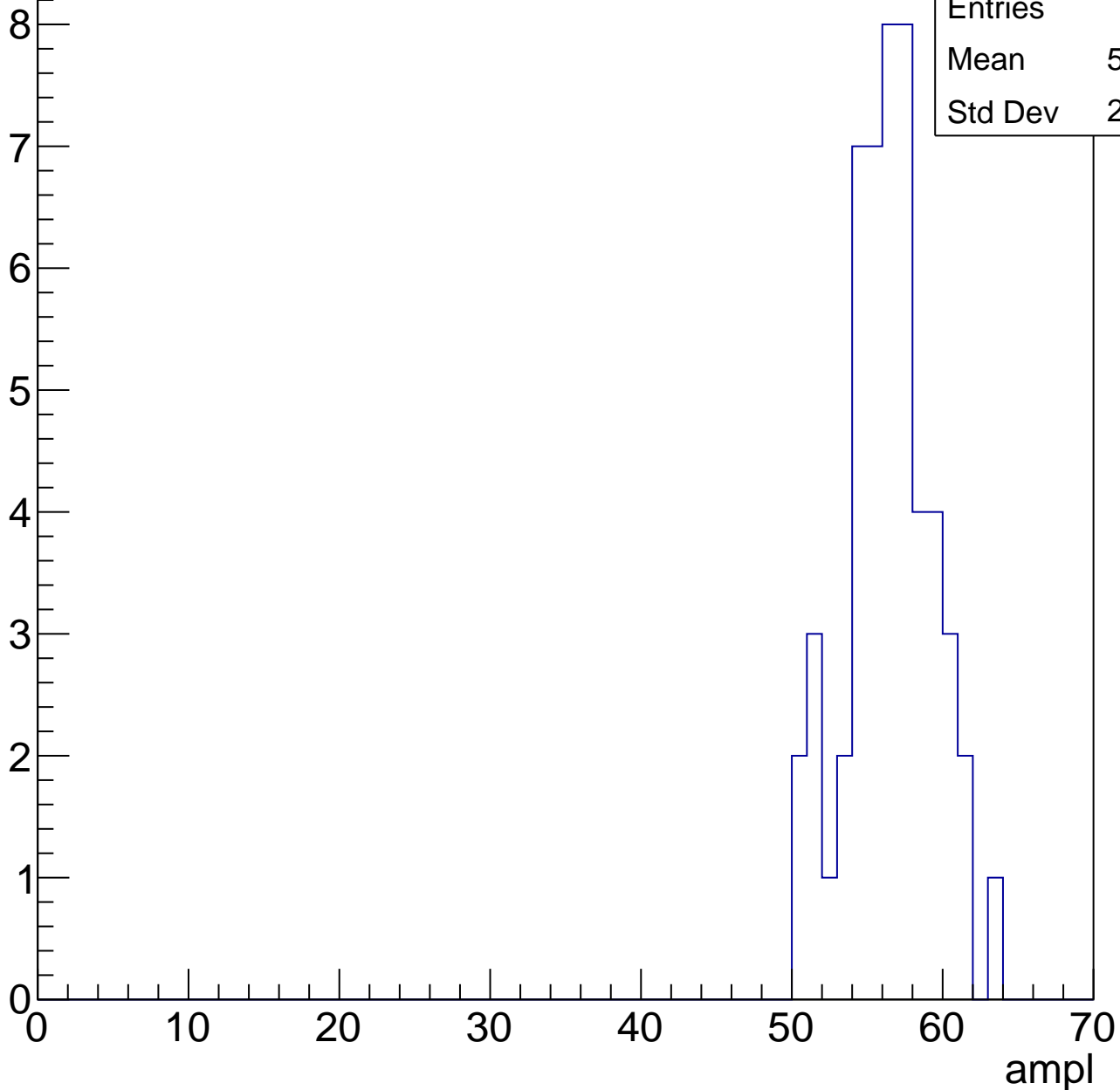


B0L002S, U5-ch30, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

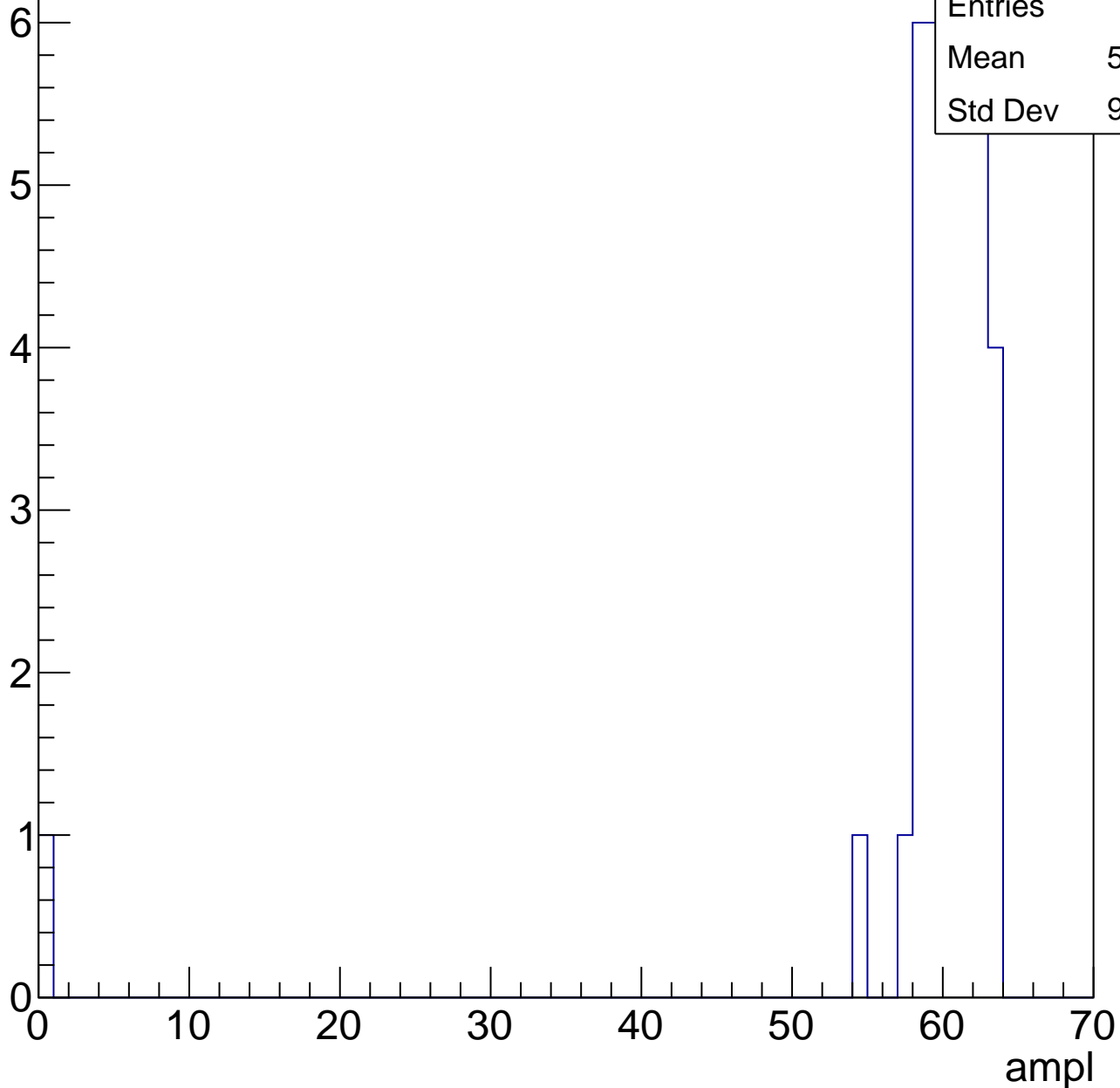
Entries	52
Mean	55.98
Std Dev	2.852



B0L002S, U5-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

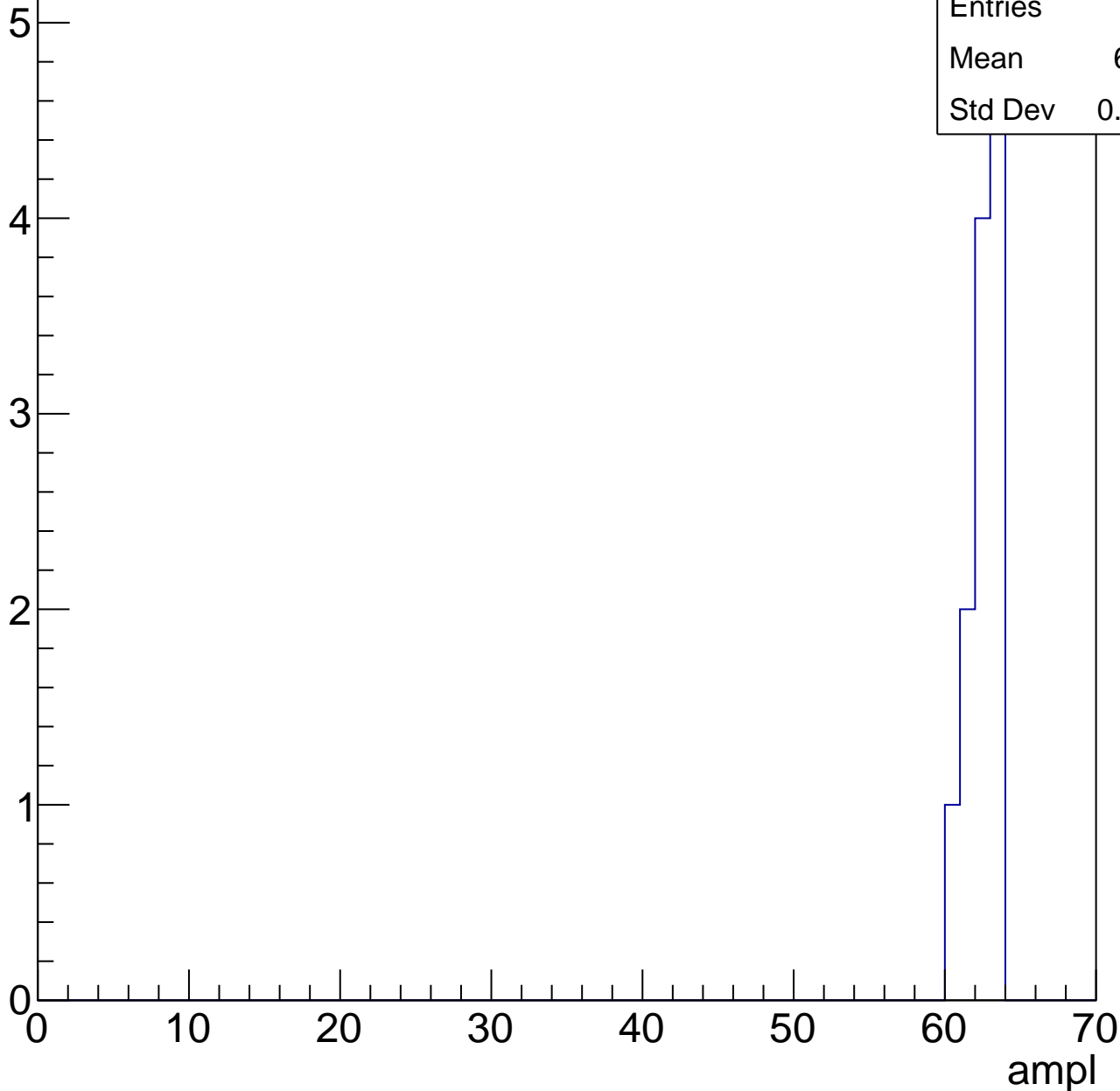


B0L002S, U5-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	62.08
Std Dev	0.9538



B0L002S, U5-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch31, adc0

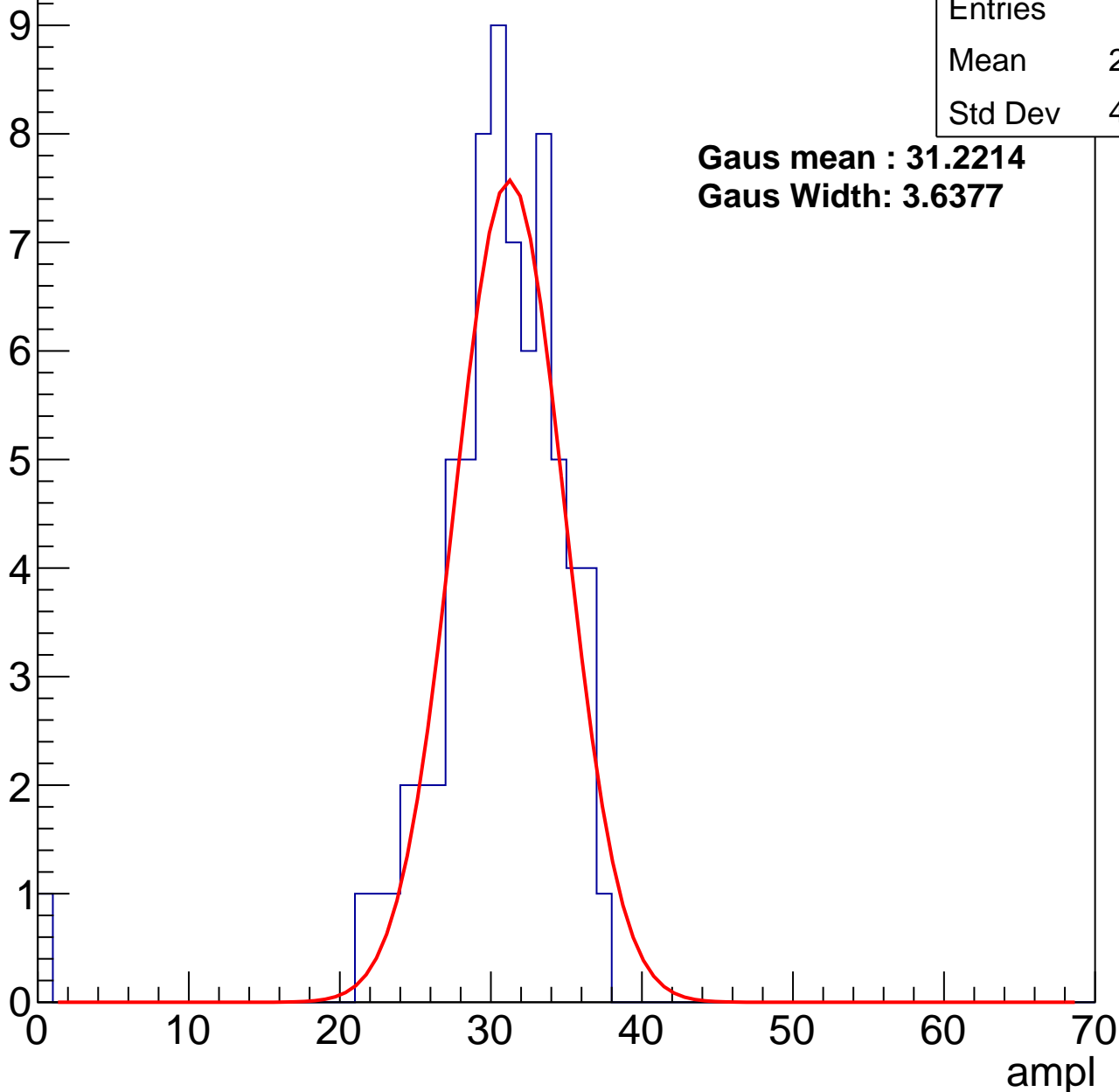
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	29.96
Std Dev	4.996

Gaus mean : 31.2214

Gaus Width: 3.6377



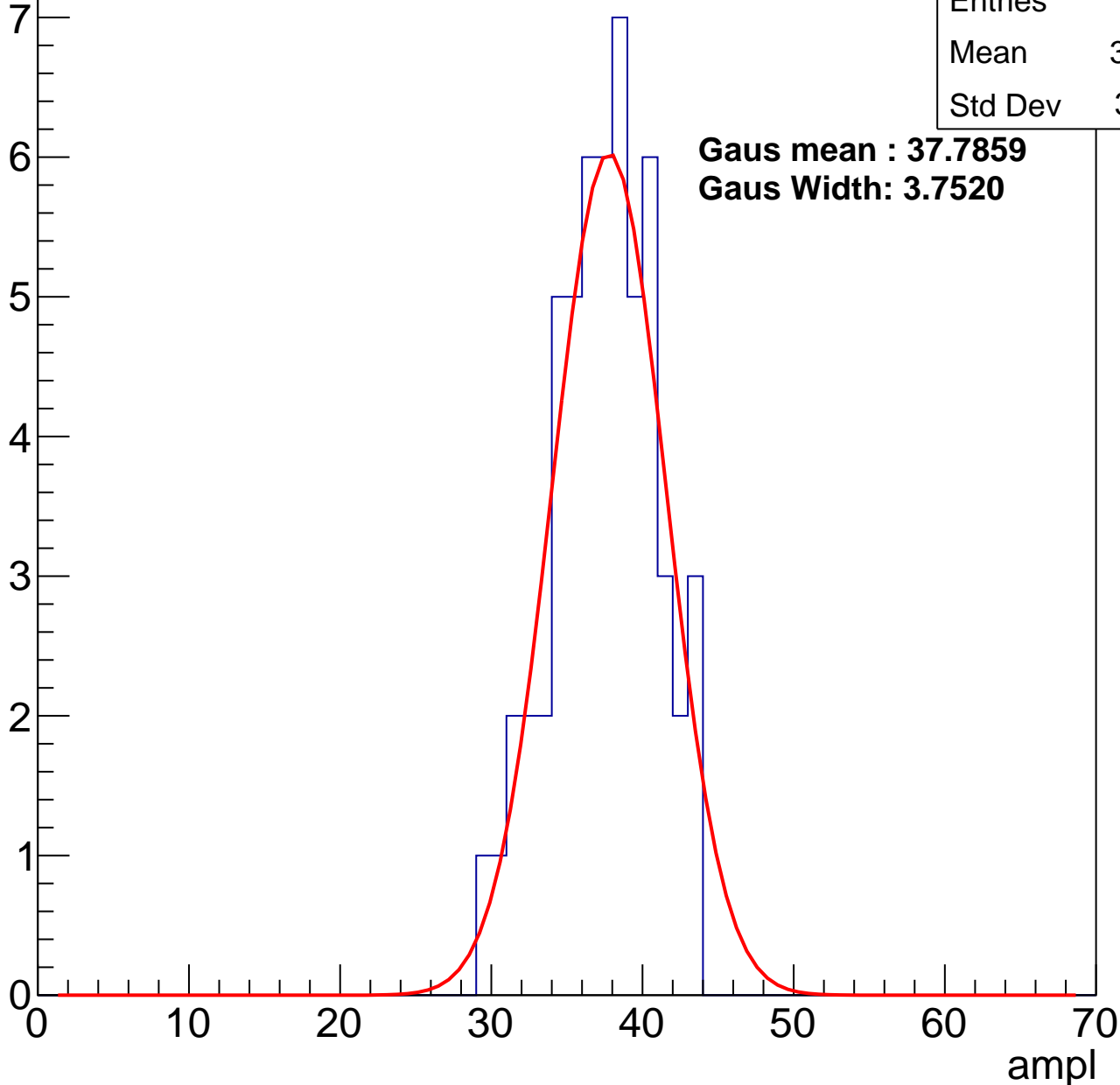
B0L002S, U5-ch31, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.98
Std Dev	3.341

Gaus mean : 37.7859
Gaus Width: 3.7520



B0L002S, U5-ch31, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

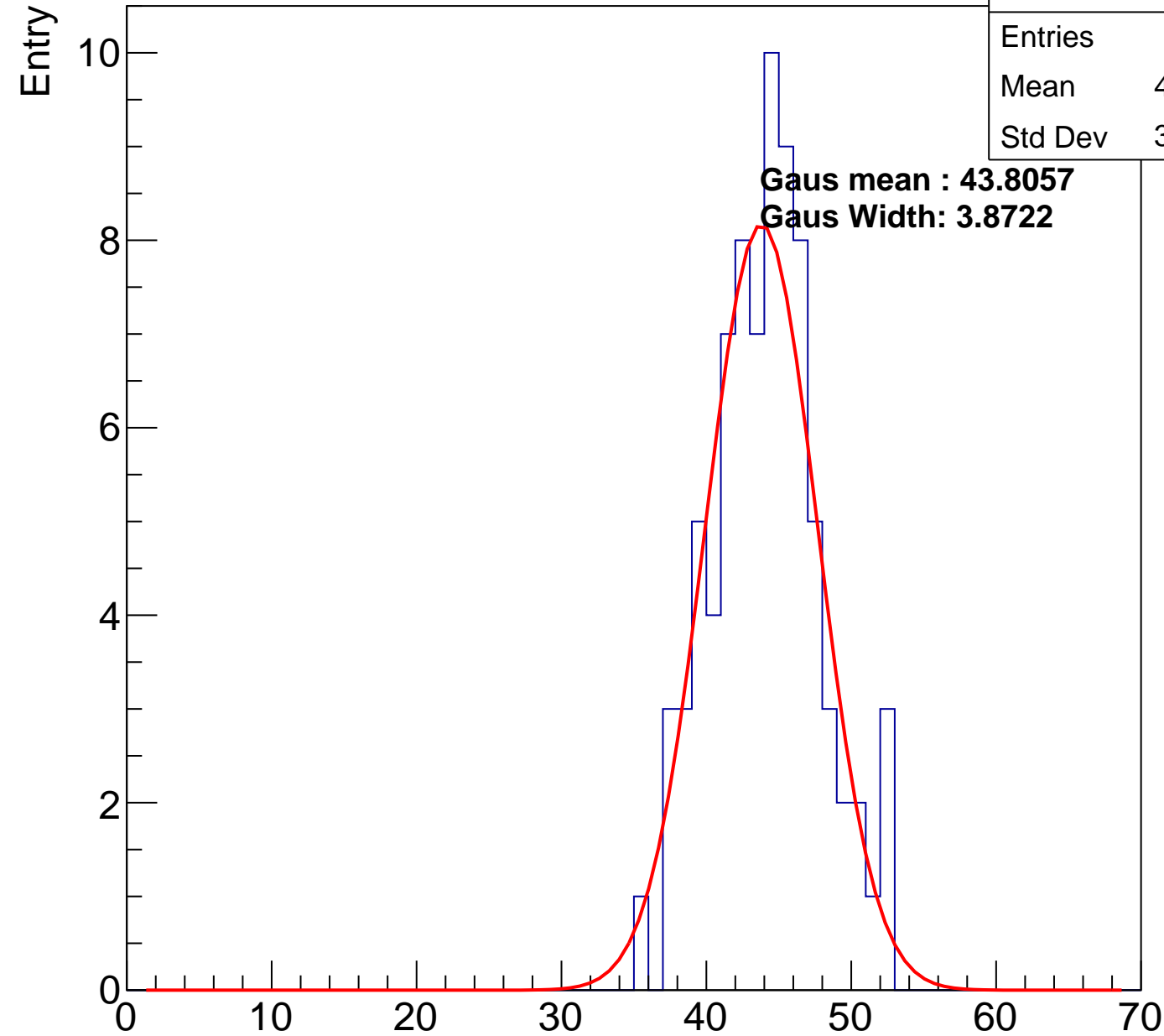
Entries	81
Mean	43.65
Std Dev	3.709

Gaus mean : 43.8057

Gaus Width: 3.8722

10
8
6
4
2
0

ampl

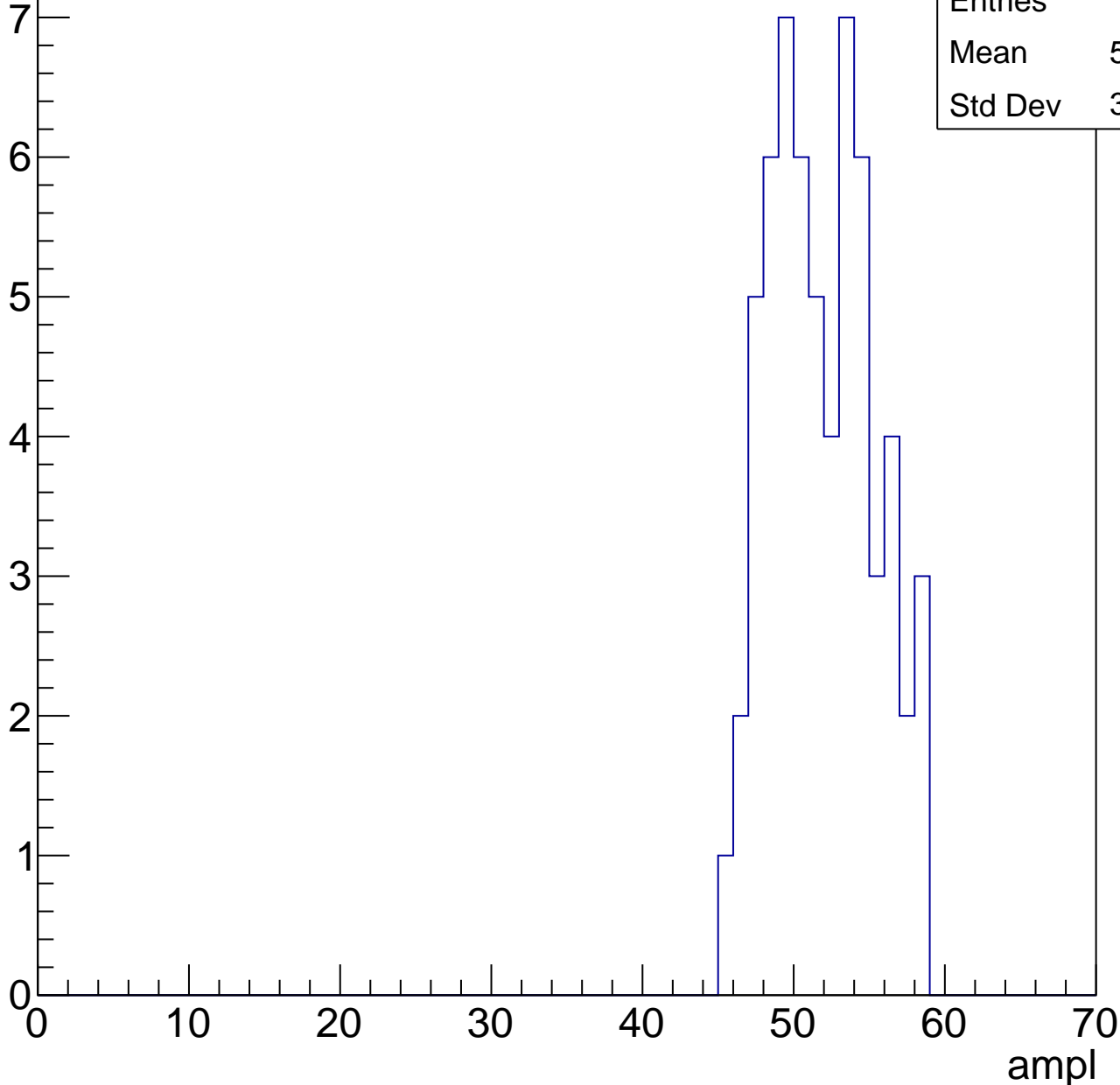


B0L002S, U5-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	51.44
Std Dev	3.376

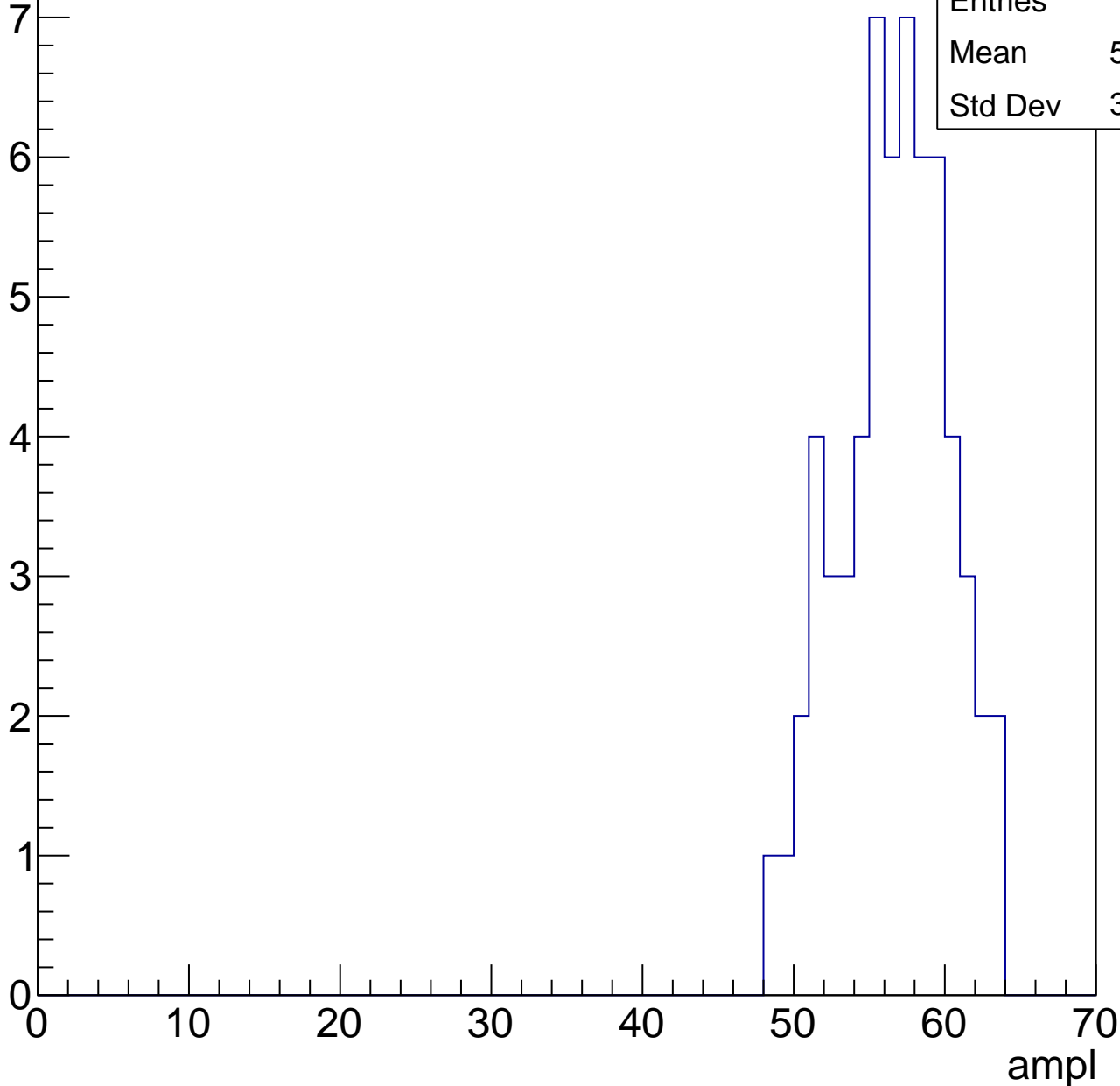


B0L002S, U5-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

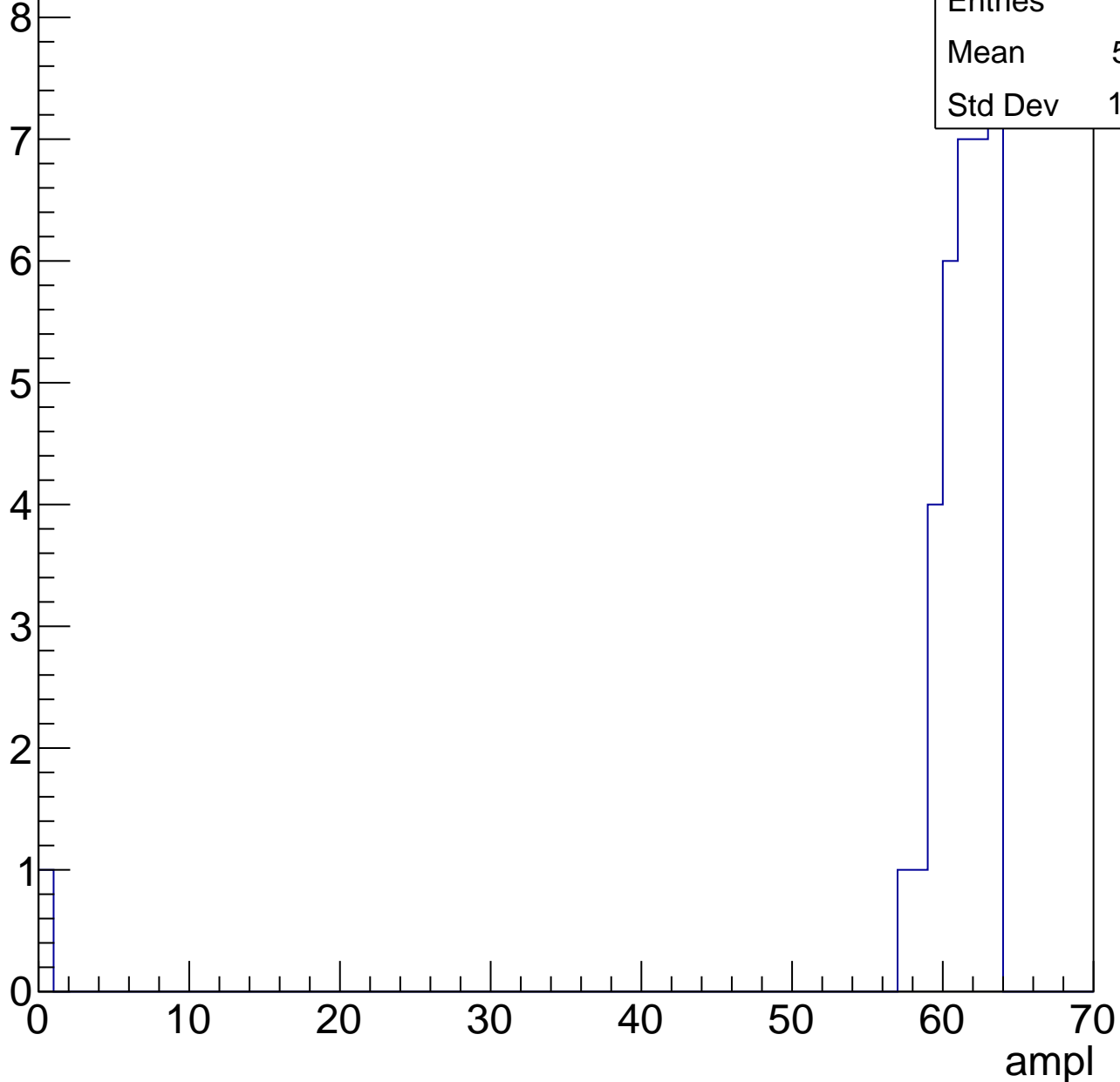
Entries	61
Mean	56.18
Std Dev	3.564



B0L002S, U5-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

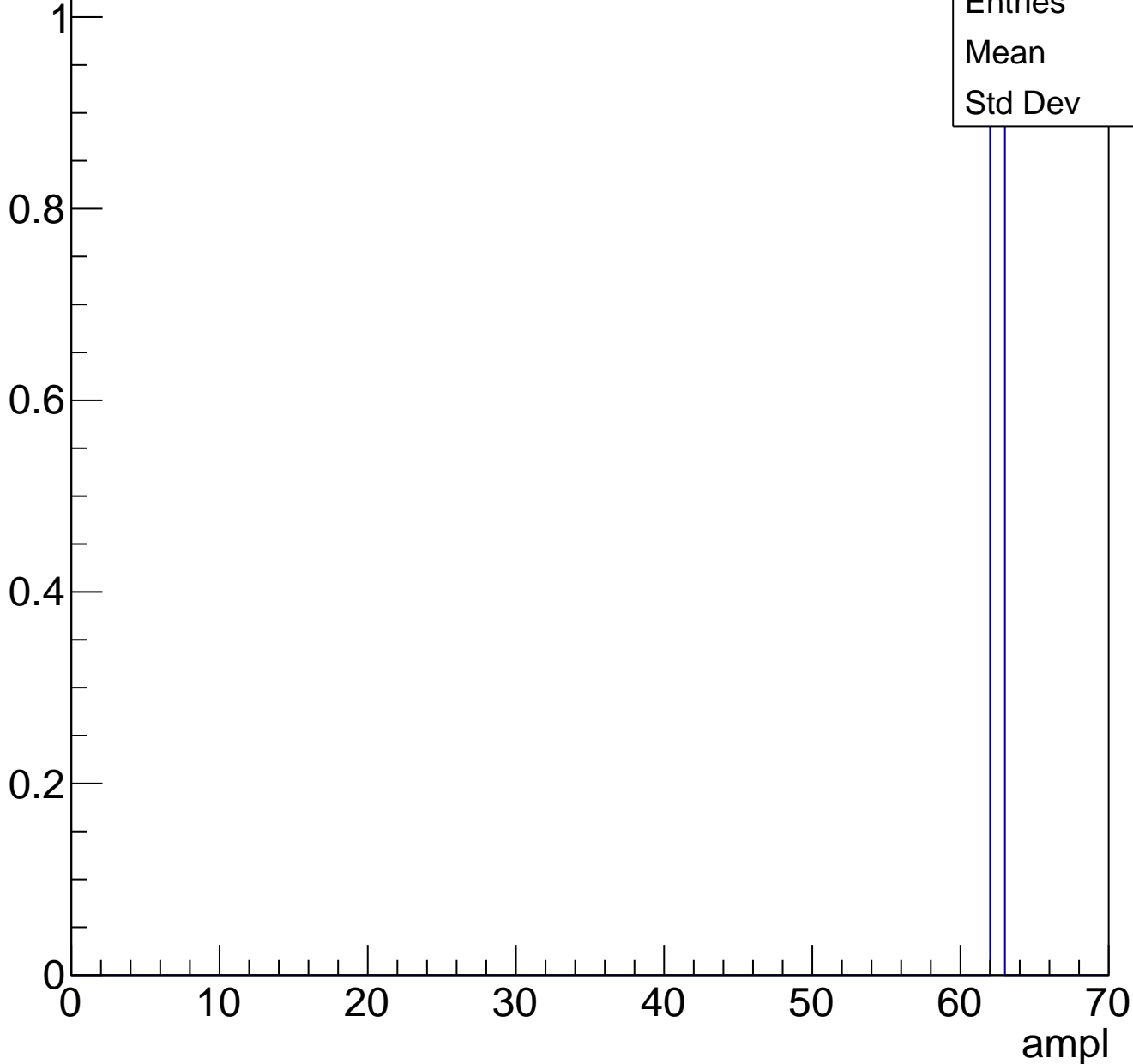
Entry



B0L002S, U5-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch32, adc0

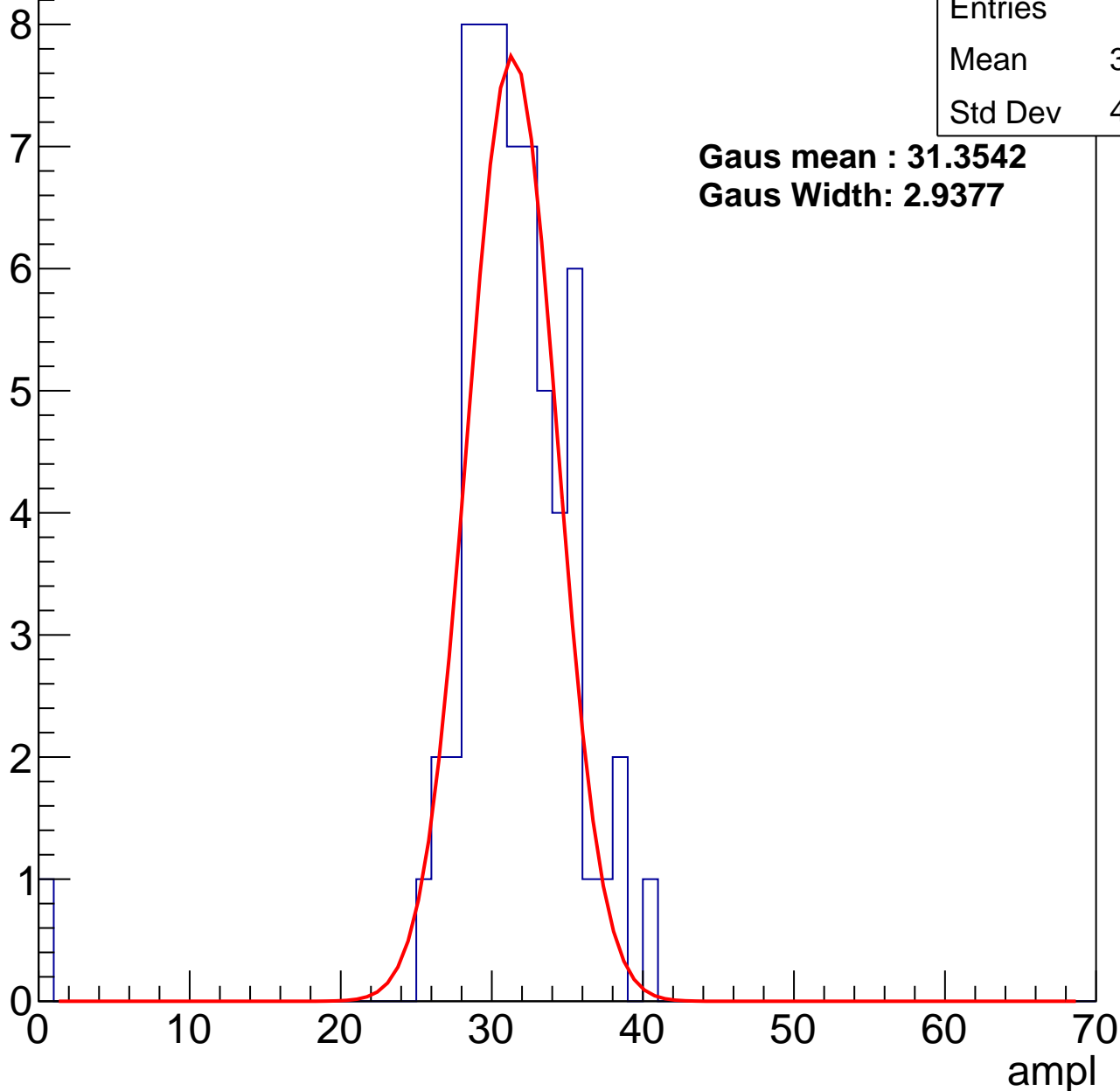
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.75
Std Dev	4.978

Gaus mean : 31.3542

Gaus Width: 2.9377



B0L002S, U5-ch32, adc1

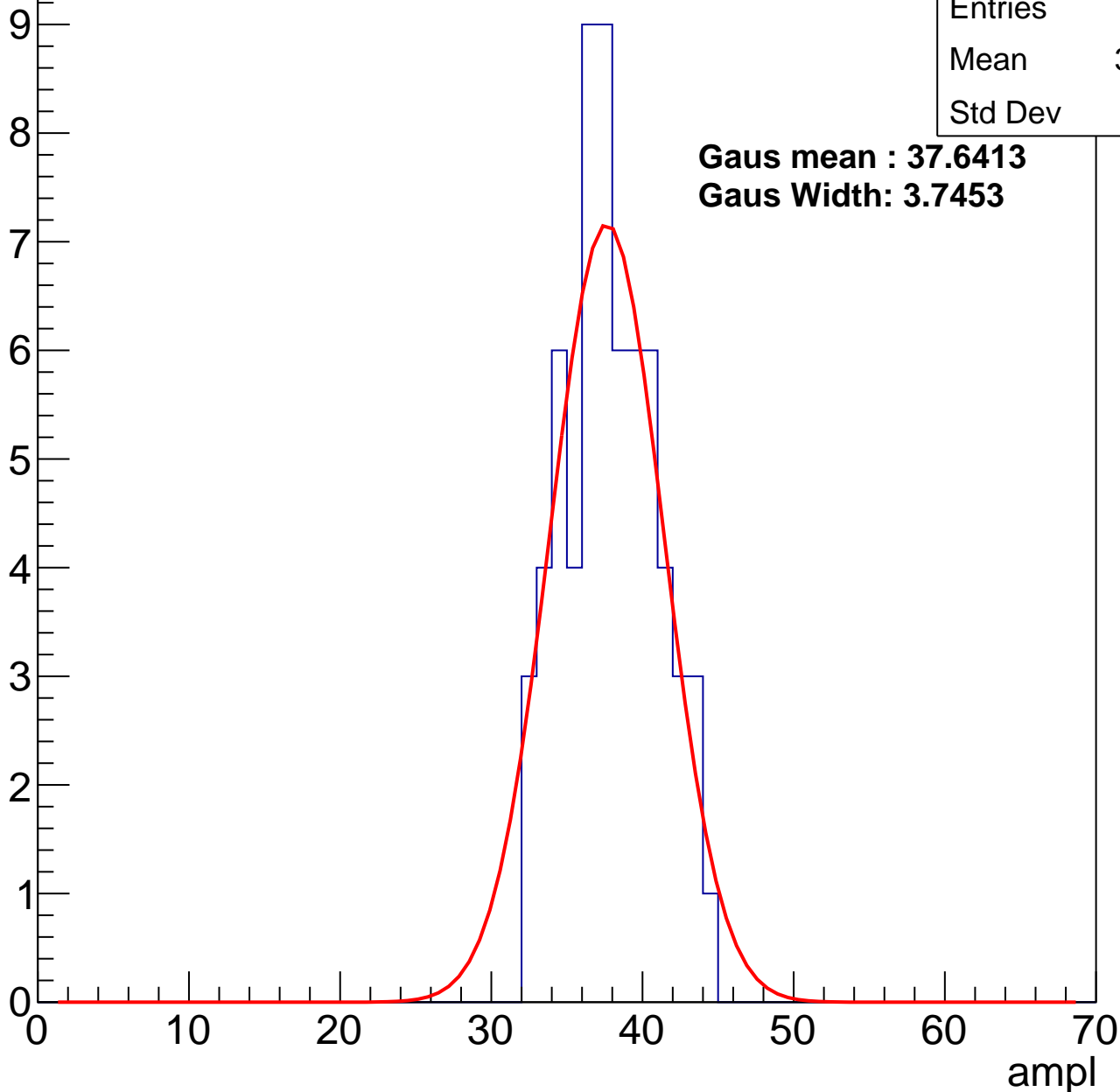
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.41
Std Dev	3.03

Gaus mean : 37.6413

Gaus Width: 3.7453



B0L002S, U5-ch32, adc2

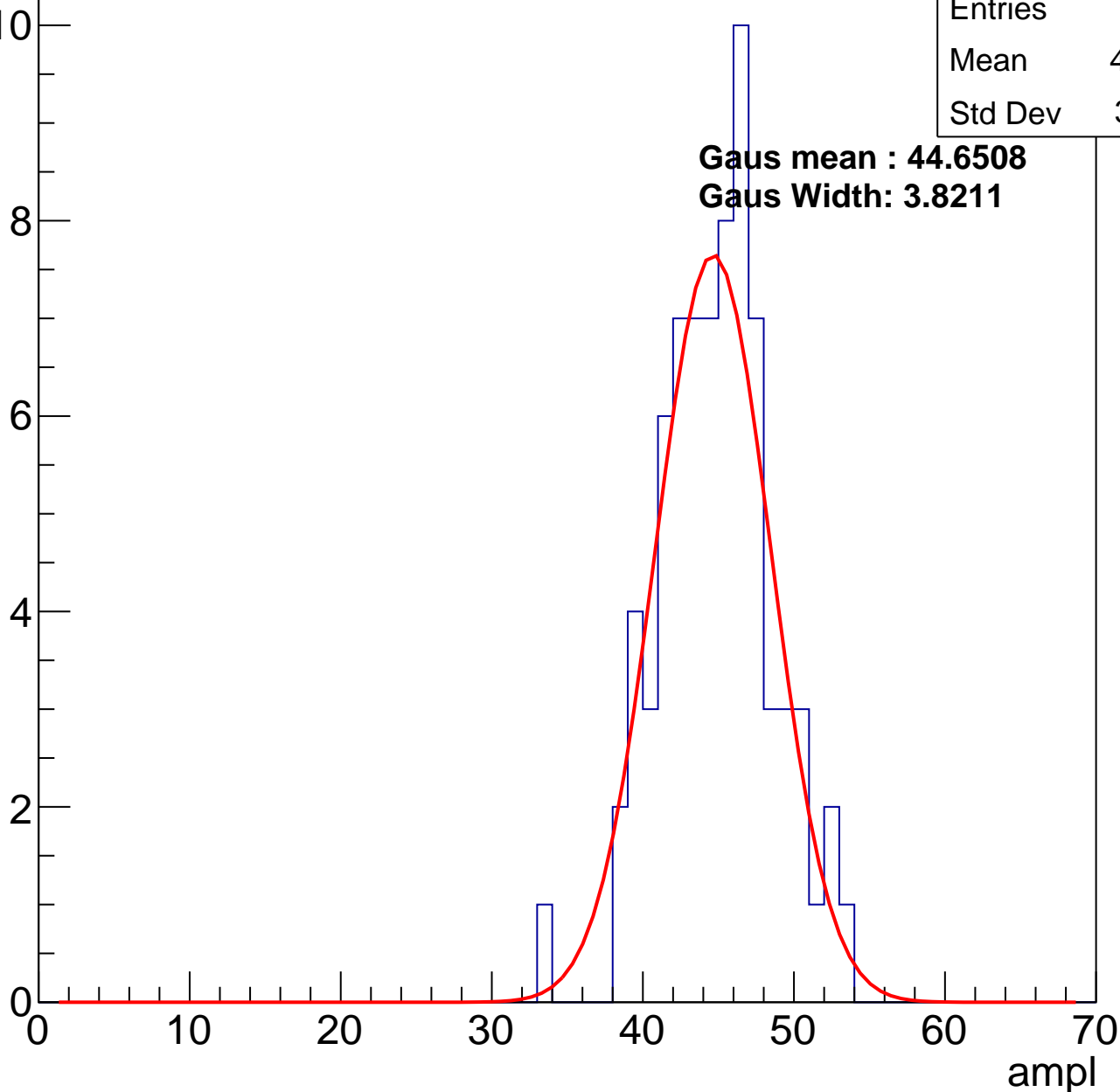
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.43
Std Dev	3.681

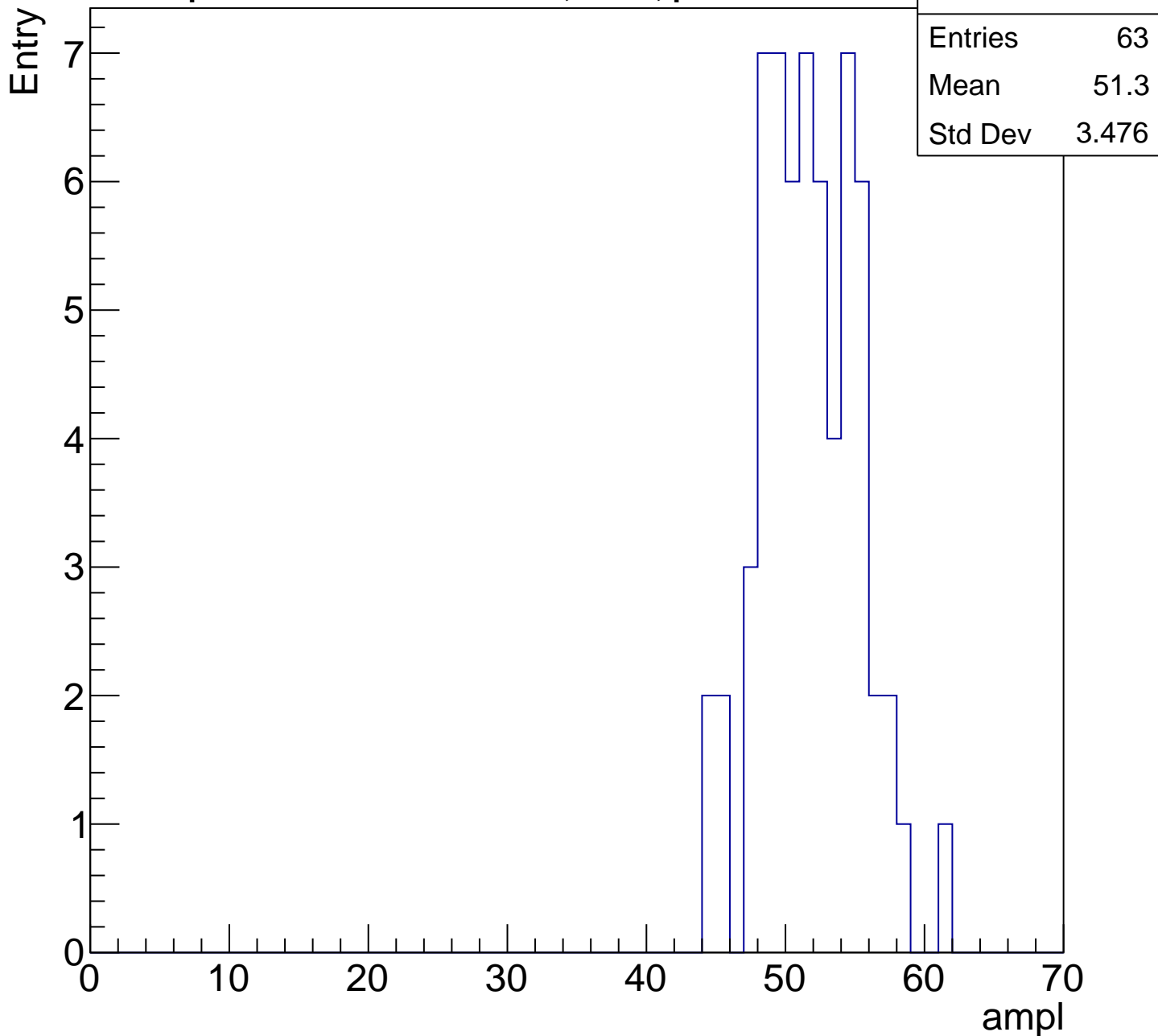
Gaus mean : 44.6508

Gaus Width: 3.8211



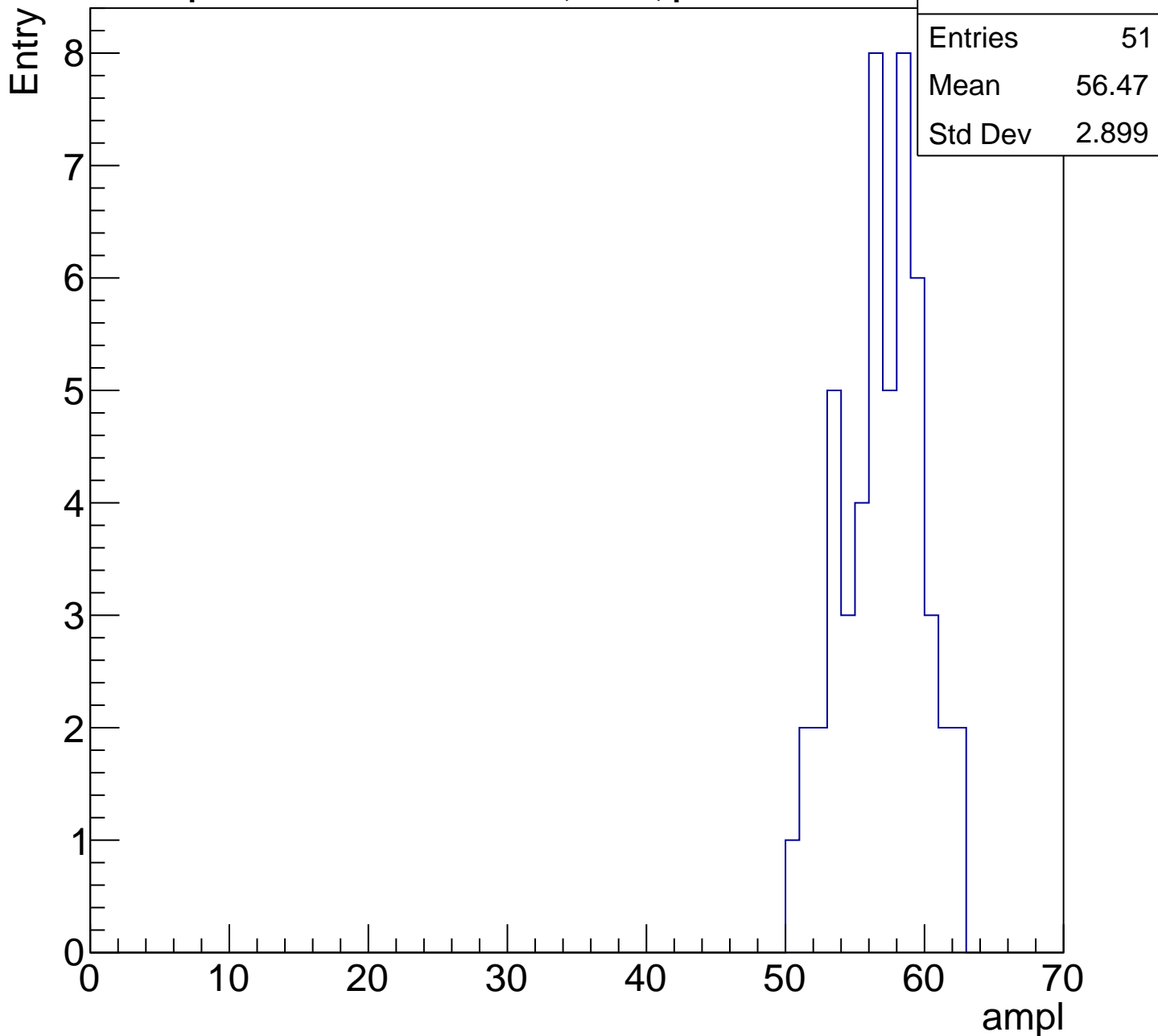
B0L002S, U5-ch32, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch32, adc4

calib_packv5_042523_0143.root, FC#8, port C1

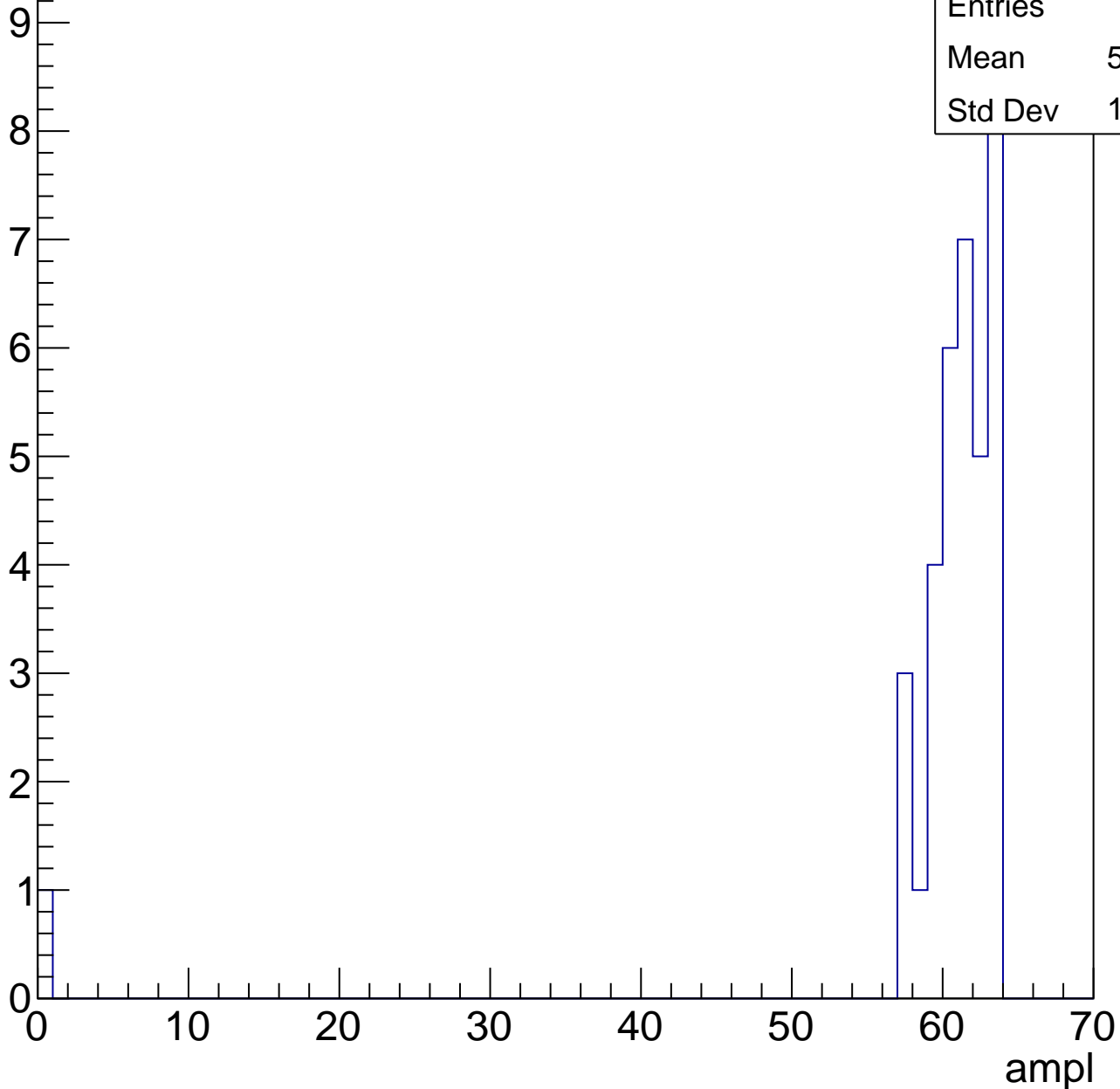


B0L002S, U5-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

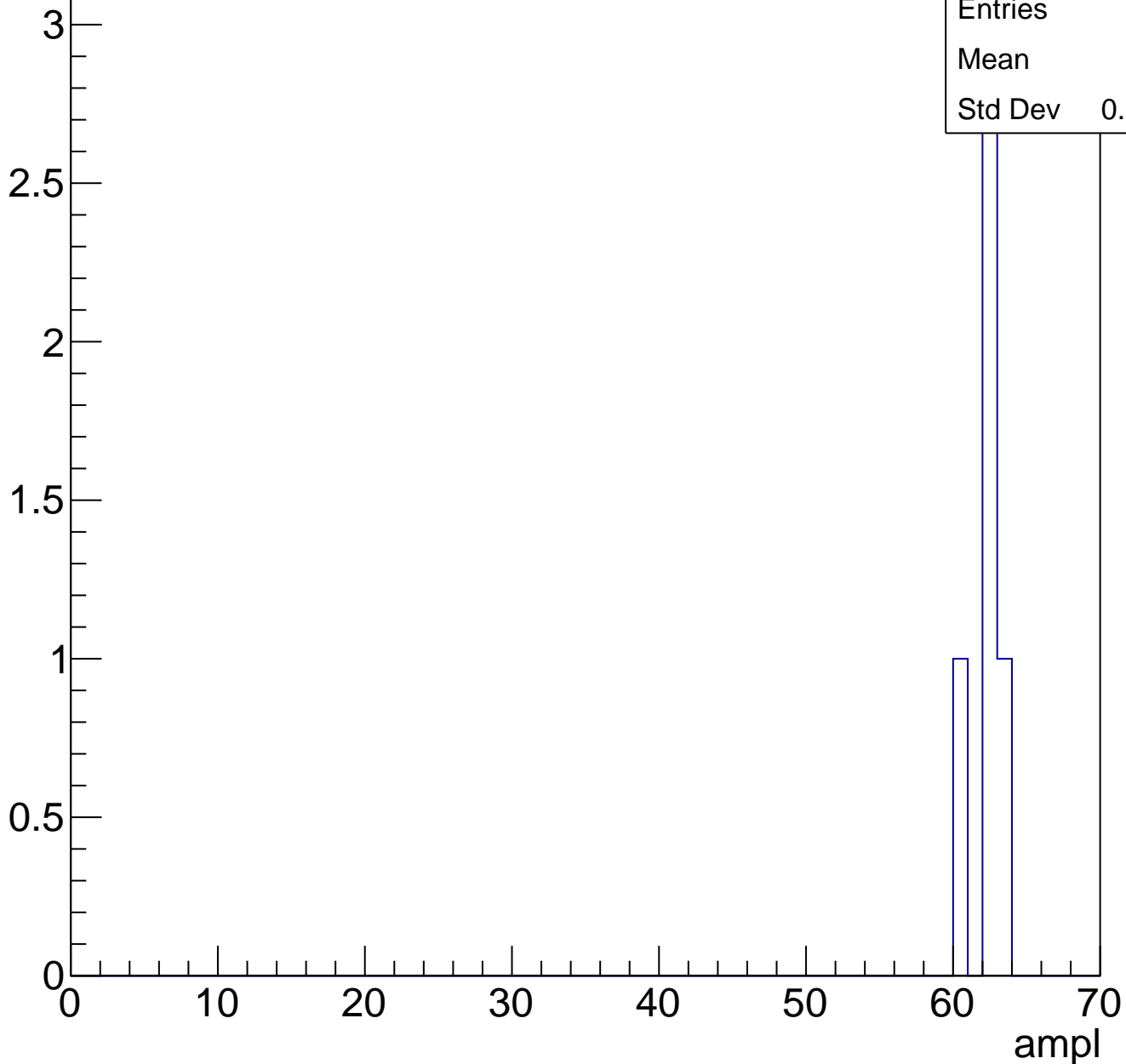
Entries	36
Mean	59.14
Std Dev	10.16



B0L002S, U5-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch33, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	30.58
Std Dev	3.879

Gaus mean : 31.7103

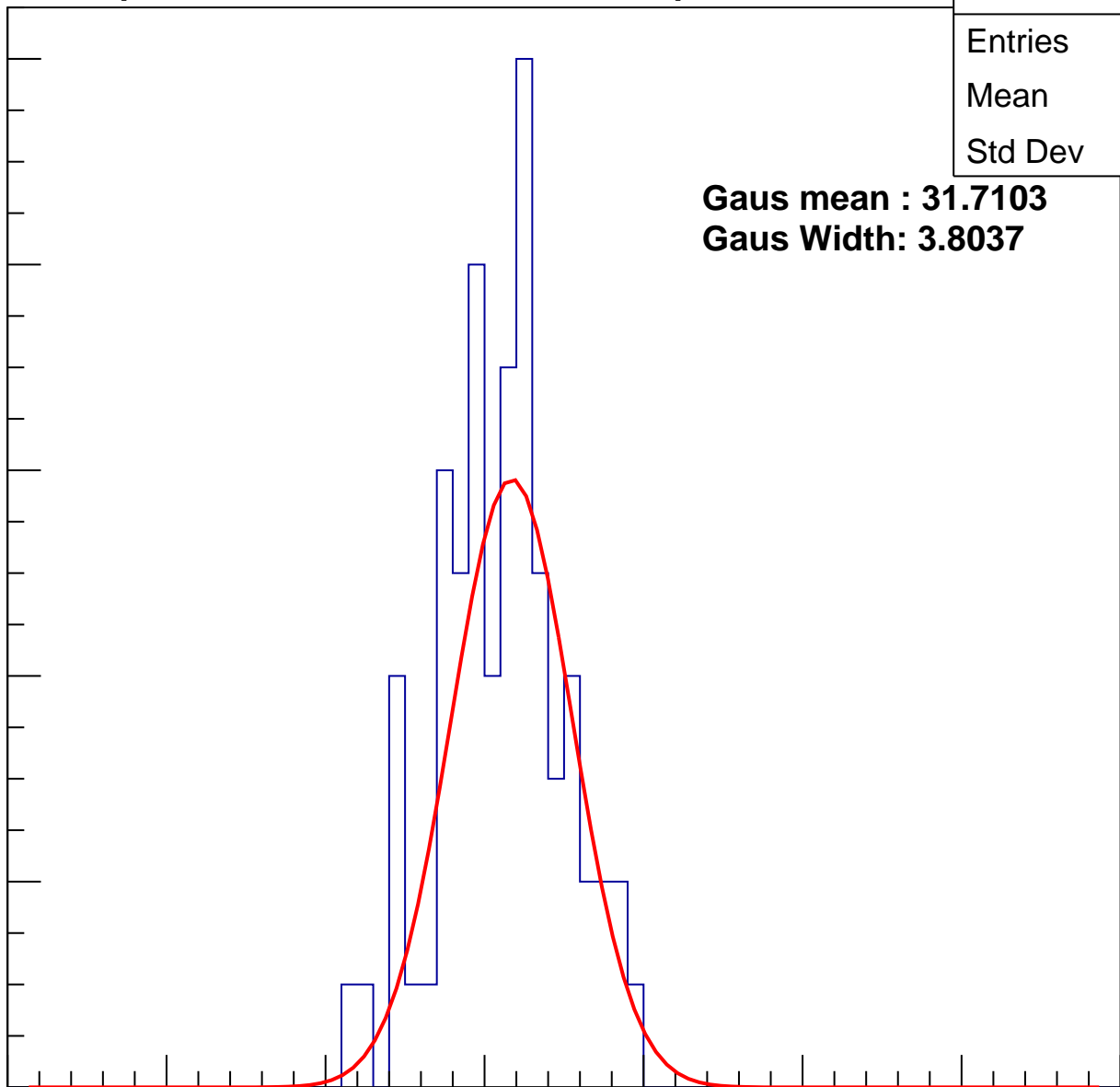
Gaus Width: 3.8037

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch33, adc1

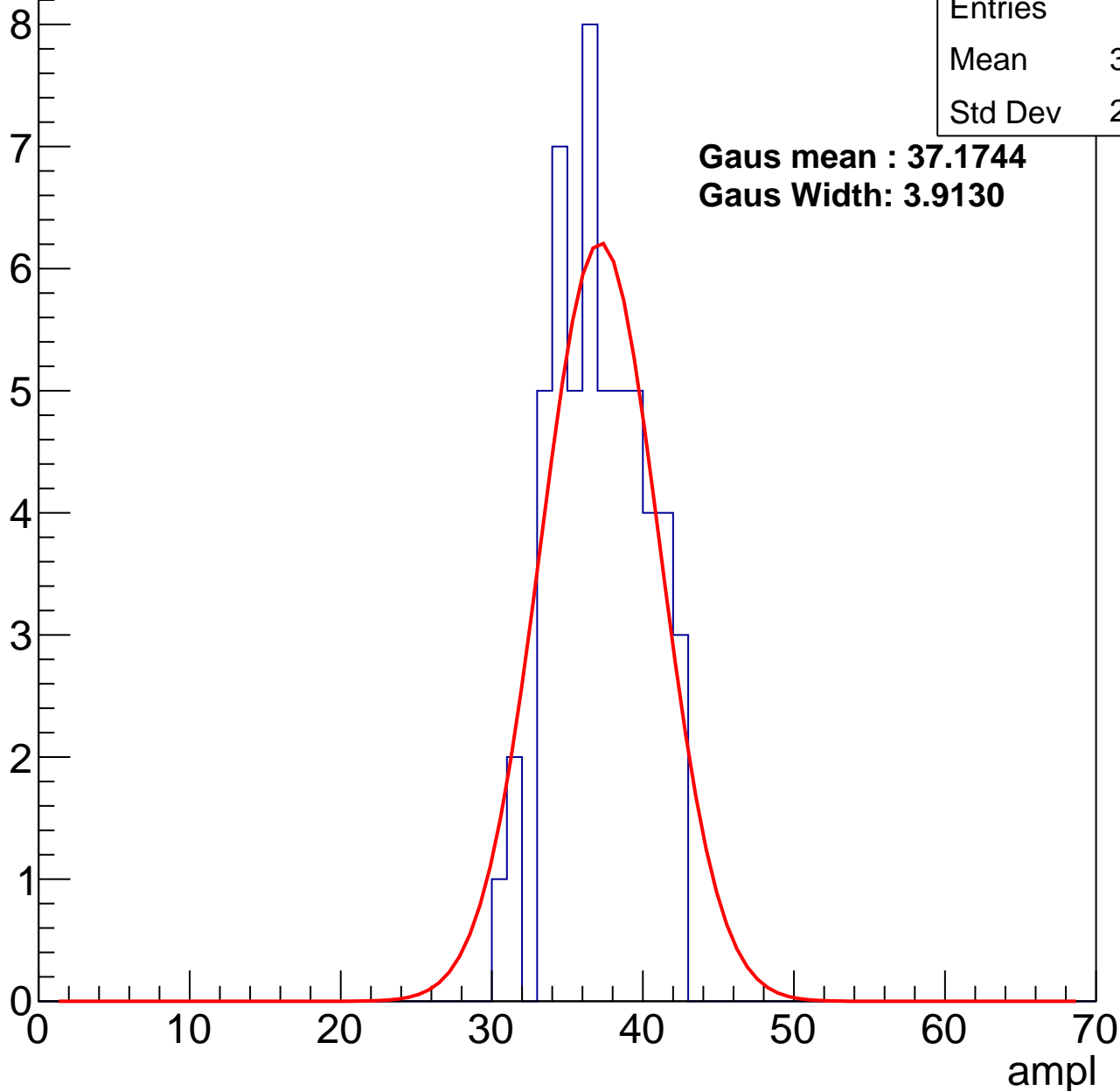
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	36.63
Std Dev	2.996

Gaus mean : 37.1744

Gaus Width: 3.9130



B0L002S, U5-ch33, adc2

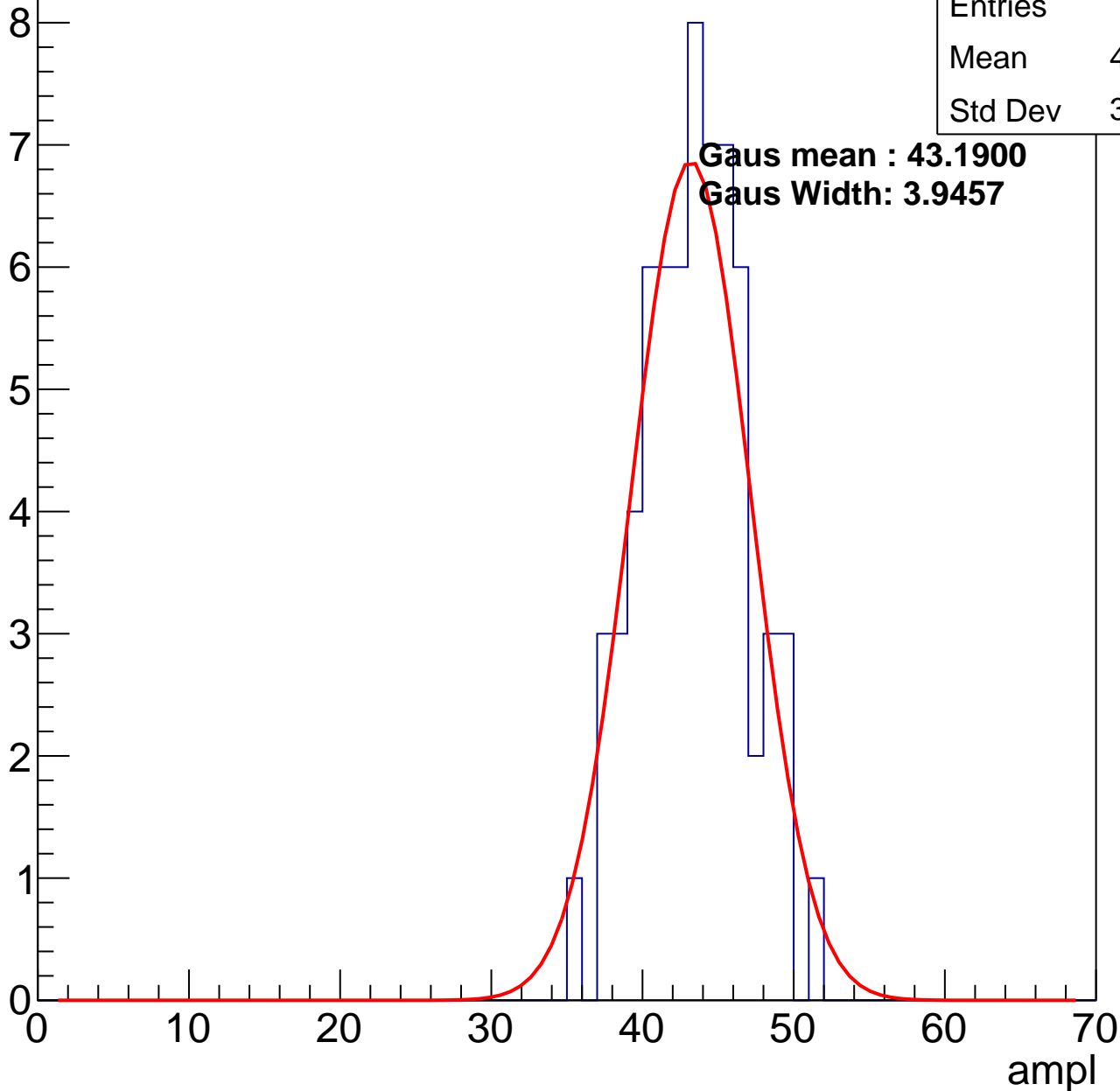
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.92
Std Dev	3.399

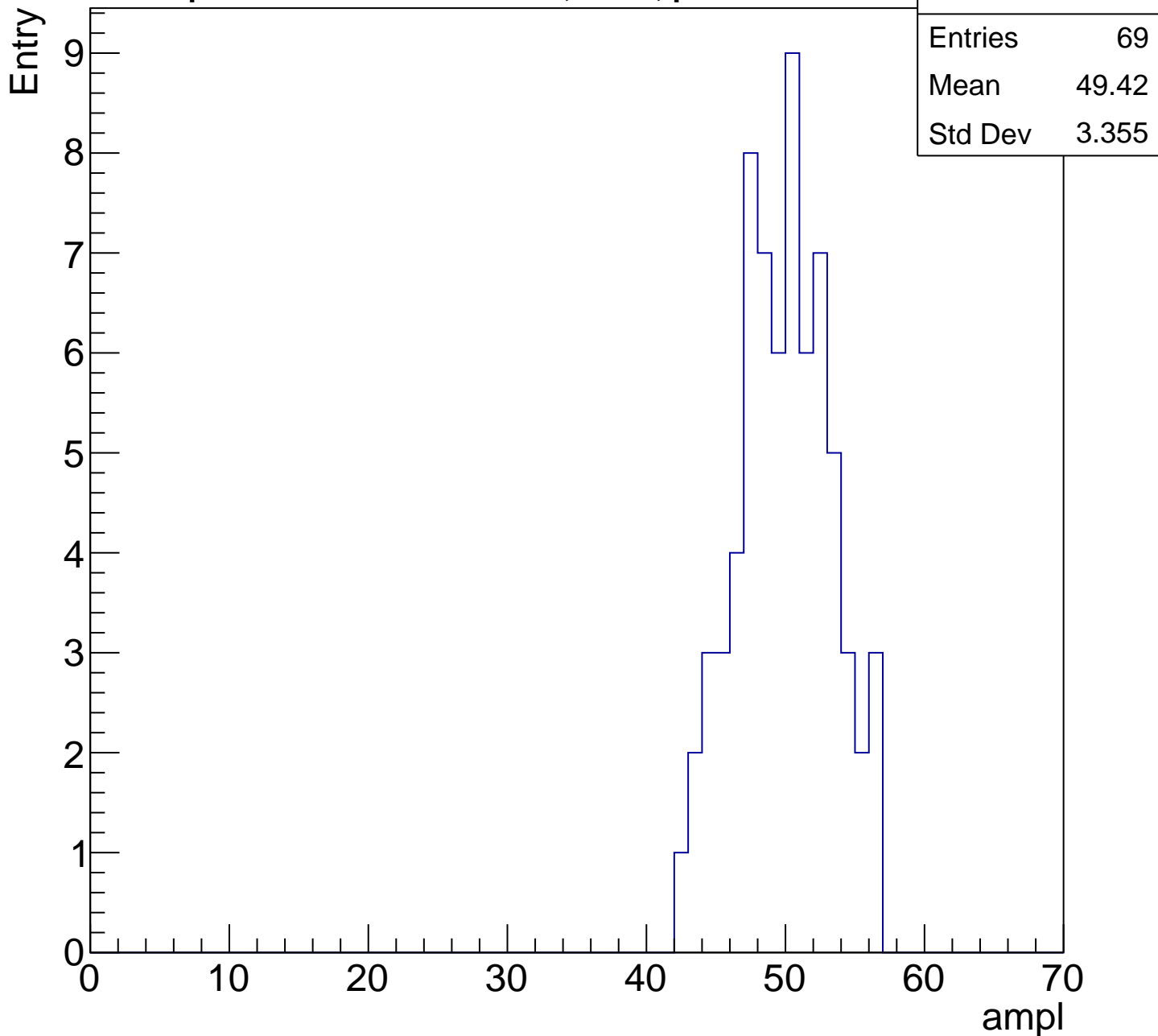
Gaus mean : 43.1900

Gaus Width: 3.9457



B0L002S, U5-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

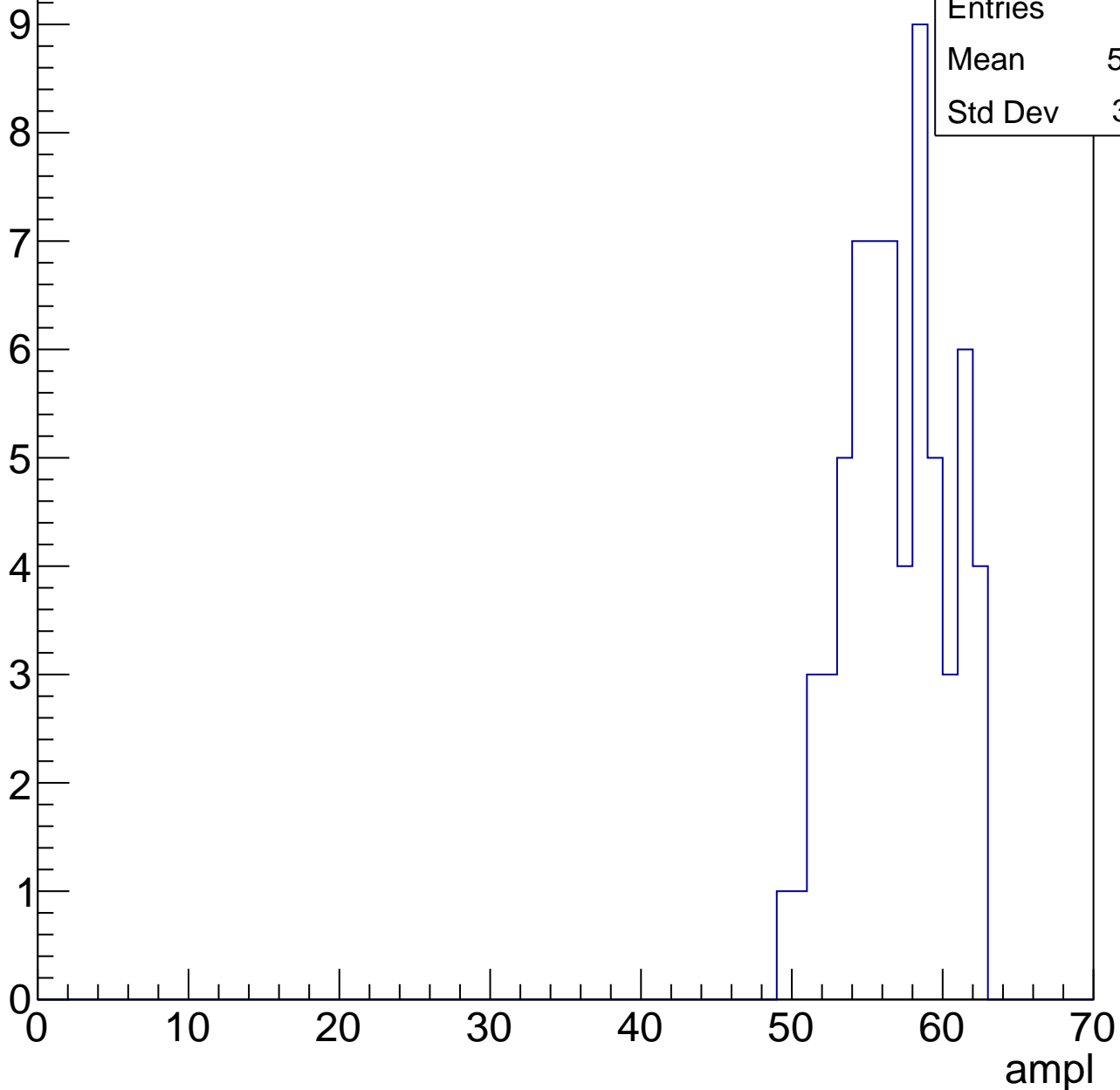


B0L002S, U5-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	56.42
Std Dev	3.291

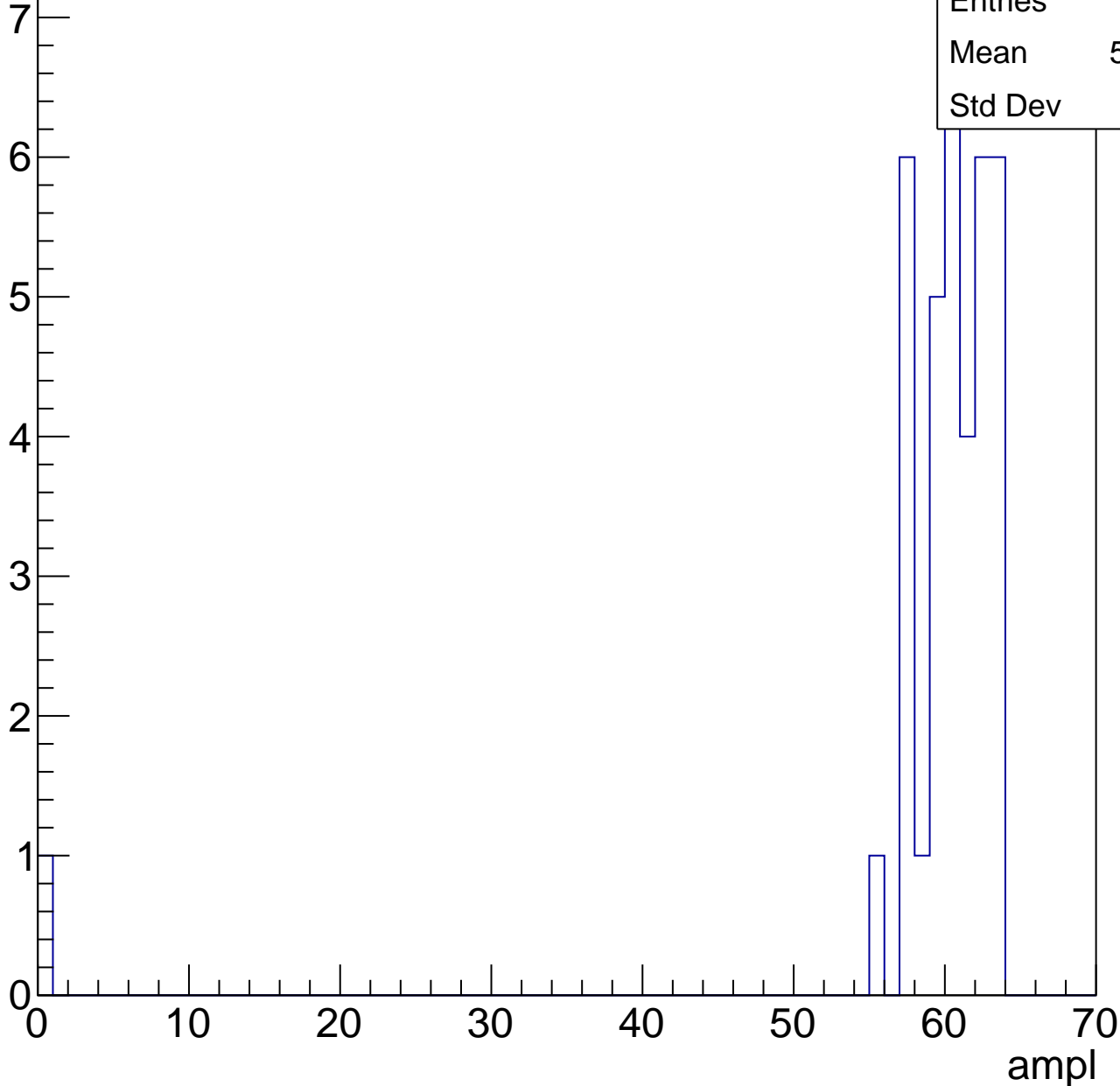


B0L002S, U5-ch33, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

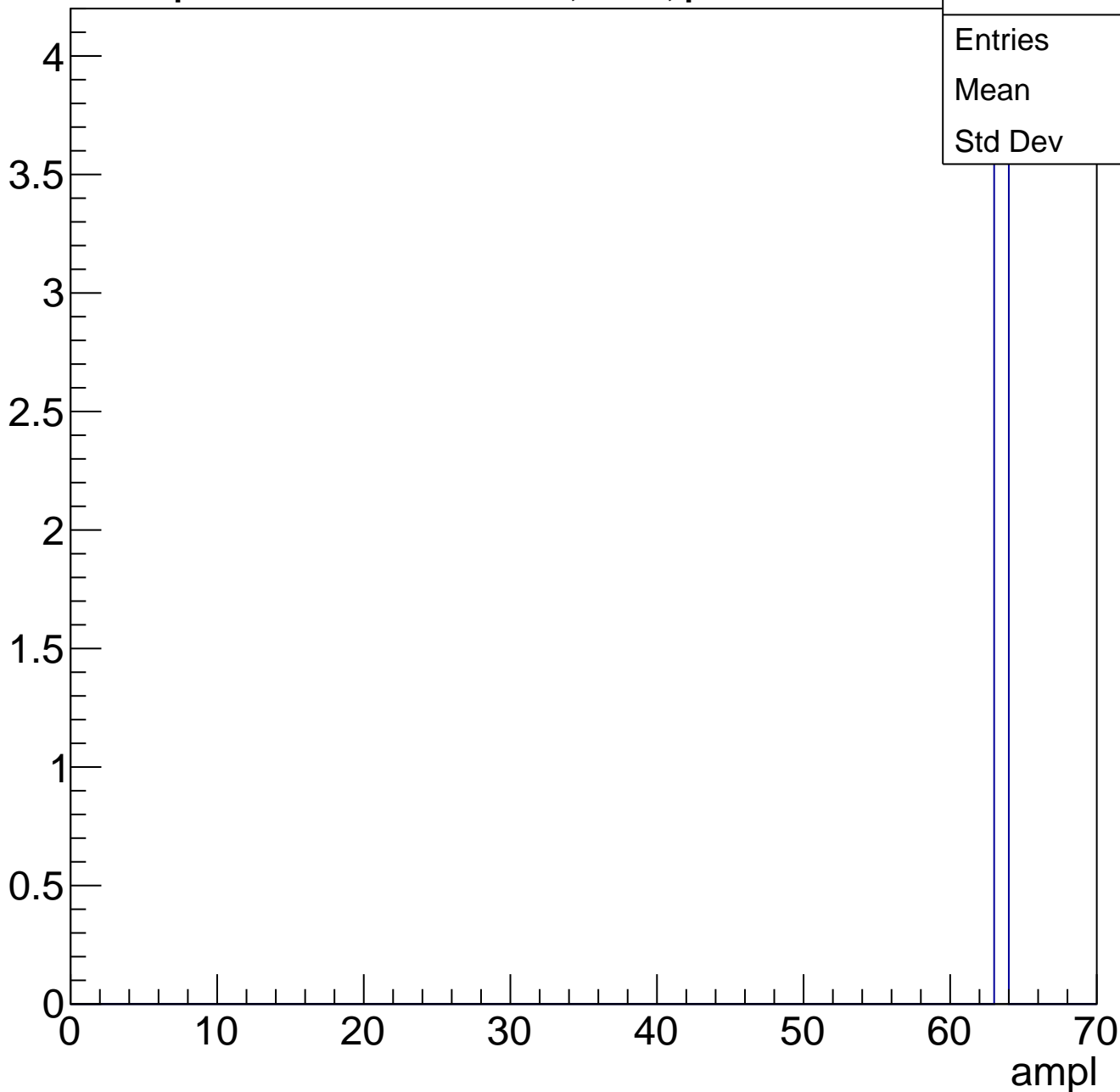
Entries	37
Mean	58.49
Std Dev	9.98



B0L002S, U5-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch34, adc0

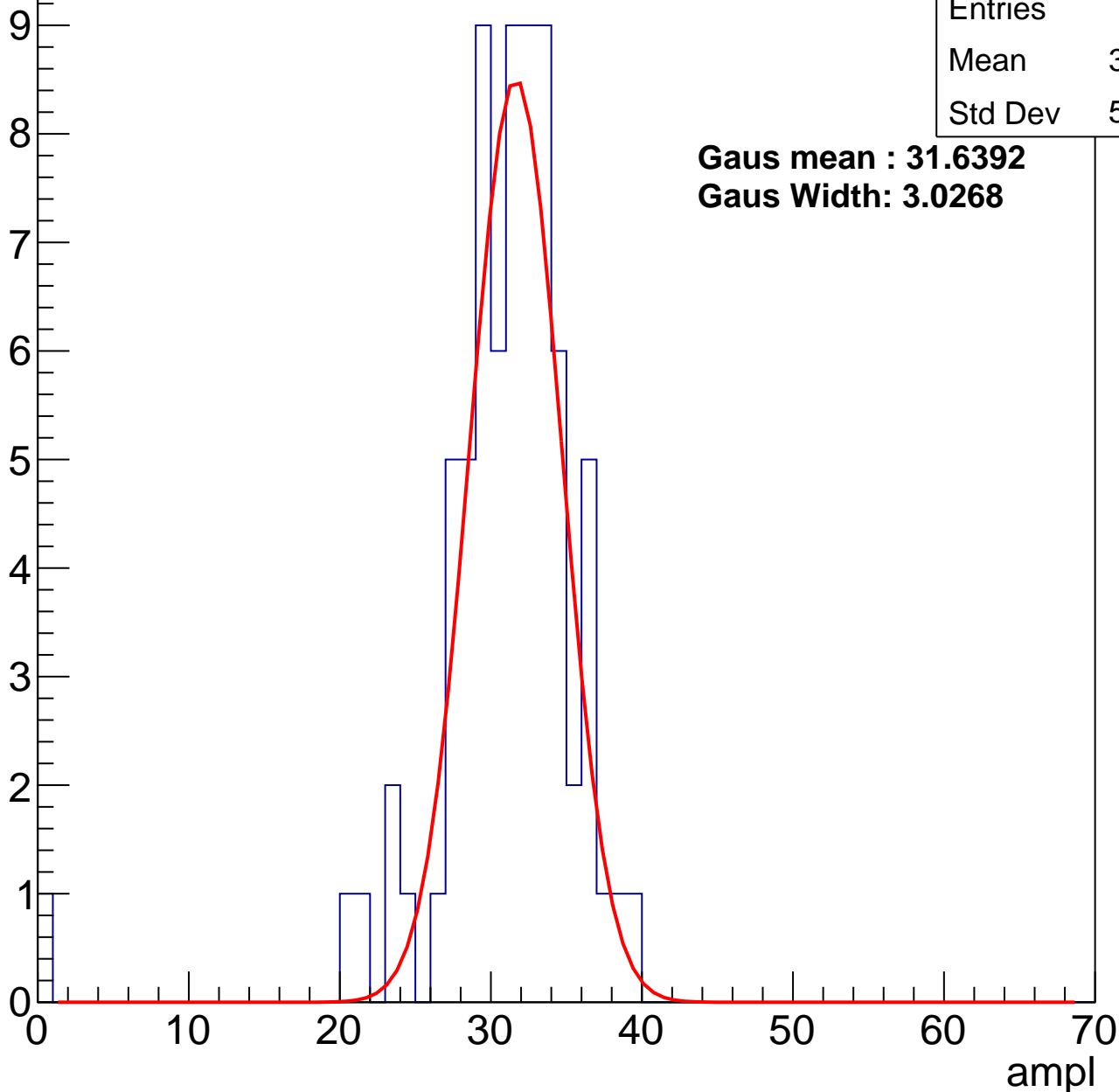
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.47
Std Dev	5.079

Gaus mean : 31.6392

Gaus Width: 3.0268



B0L002S, U5-ch34, adc1

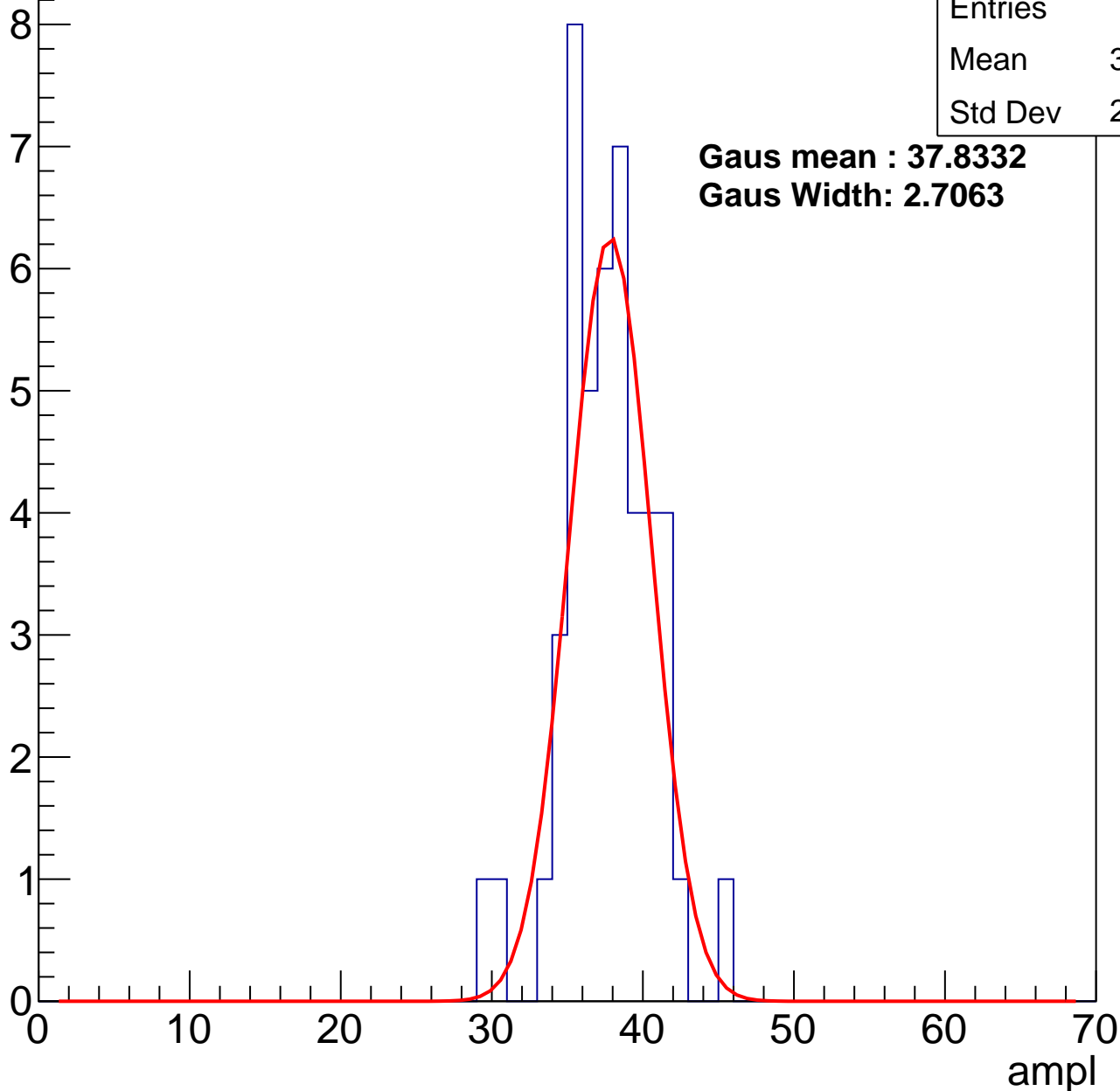
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	37.15
Std Dev	2.963

Gaus mean : 37.8332

Gaus Width: 2.7063



B0L002S, U5-ch34, adc2

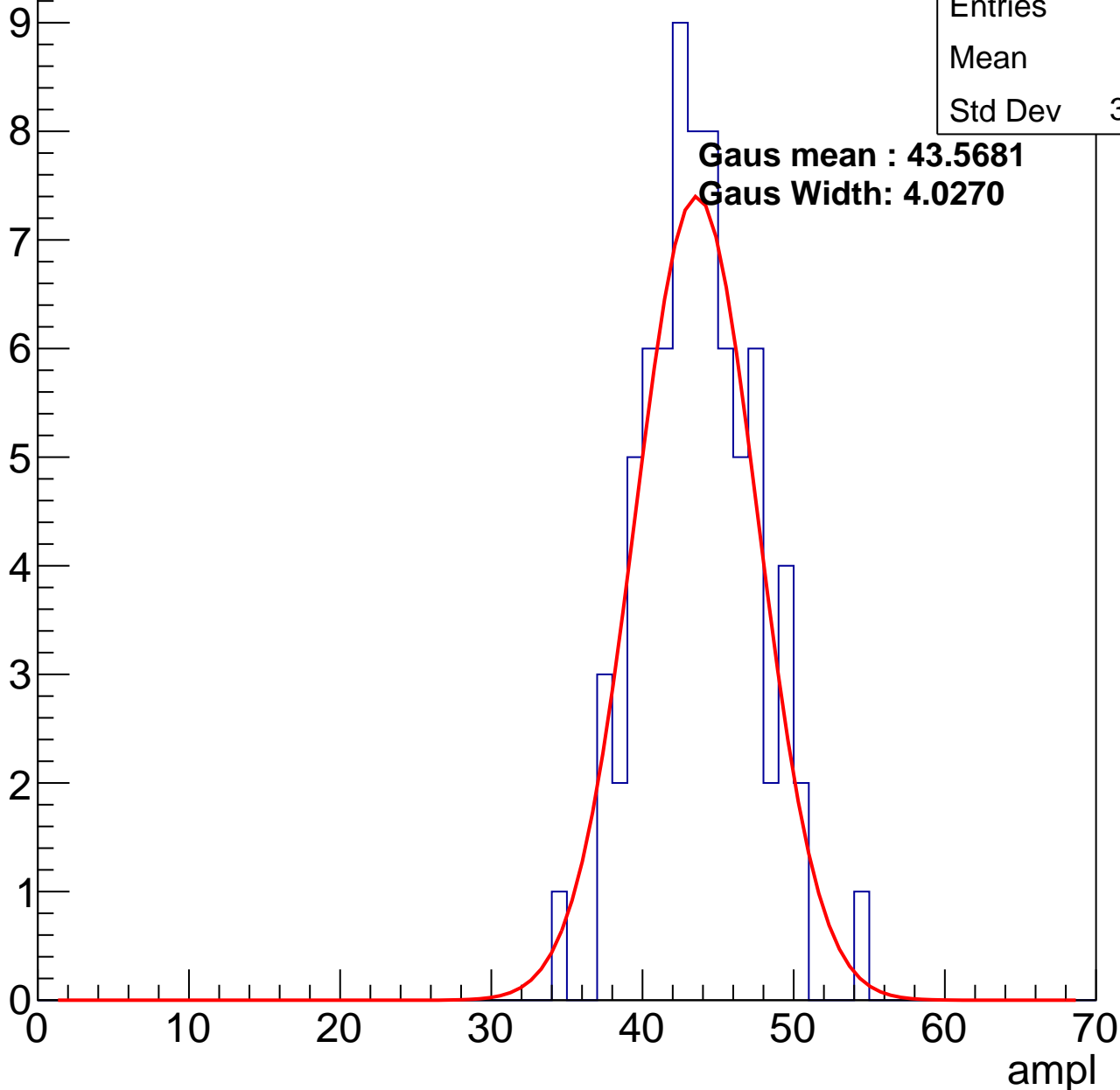
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.3
Std Dev	3.649

Gaus mean : 43.5681

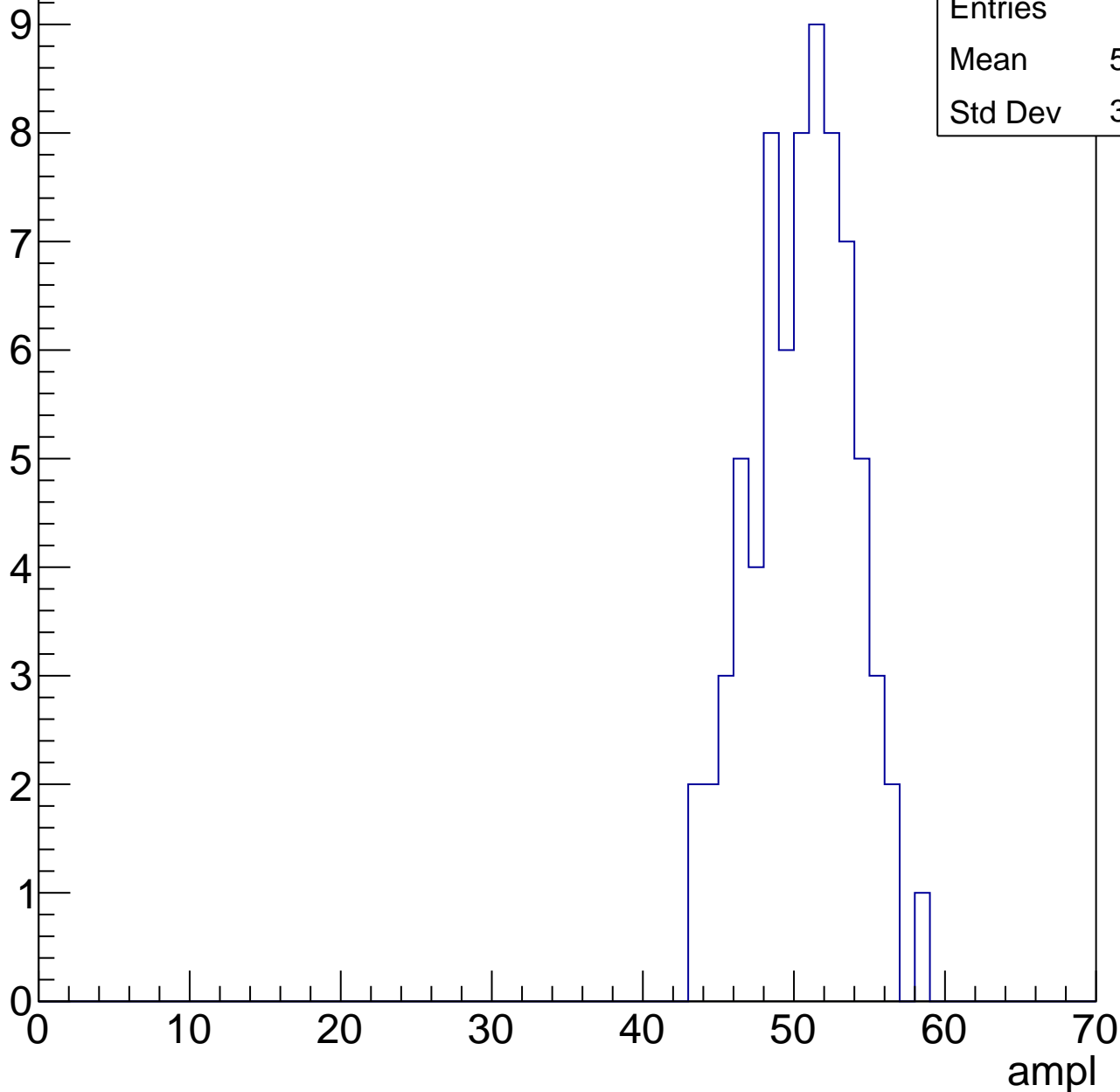
Gaus Width: 4.0270



B0L002S, U5-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch34, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	57.13
Std Dev	2.606

Entry

10

8

6

4

2

0

0

10

20

30

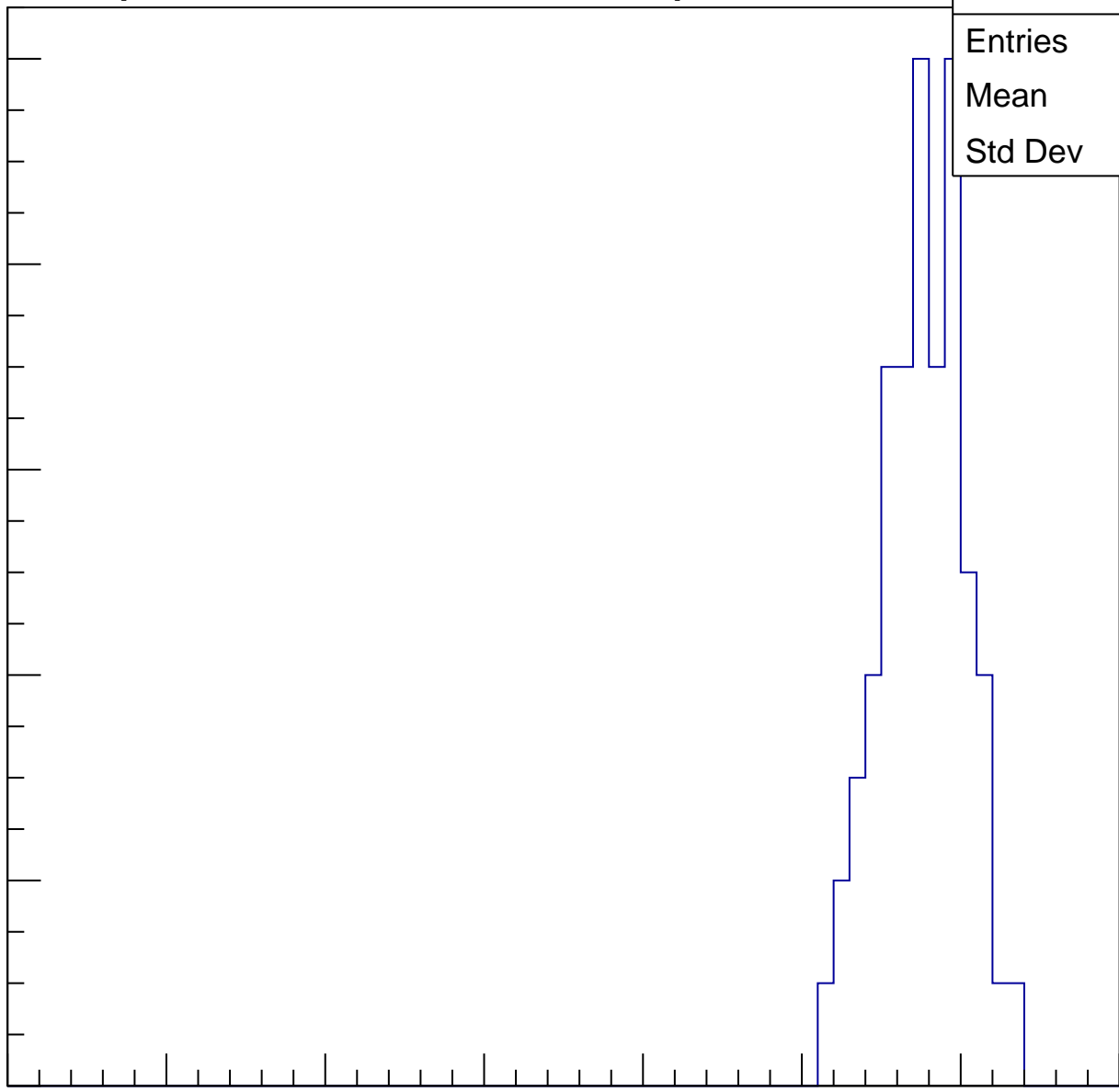
40

50

60

70

ampl

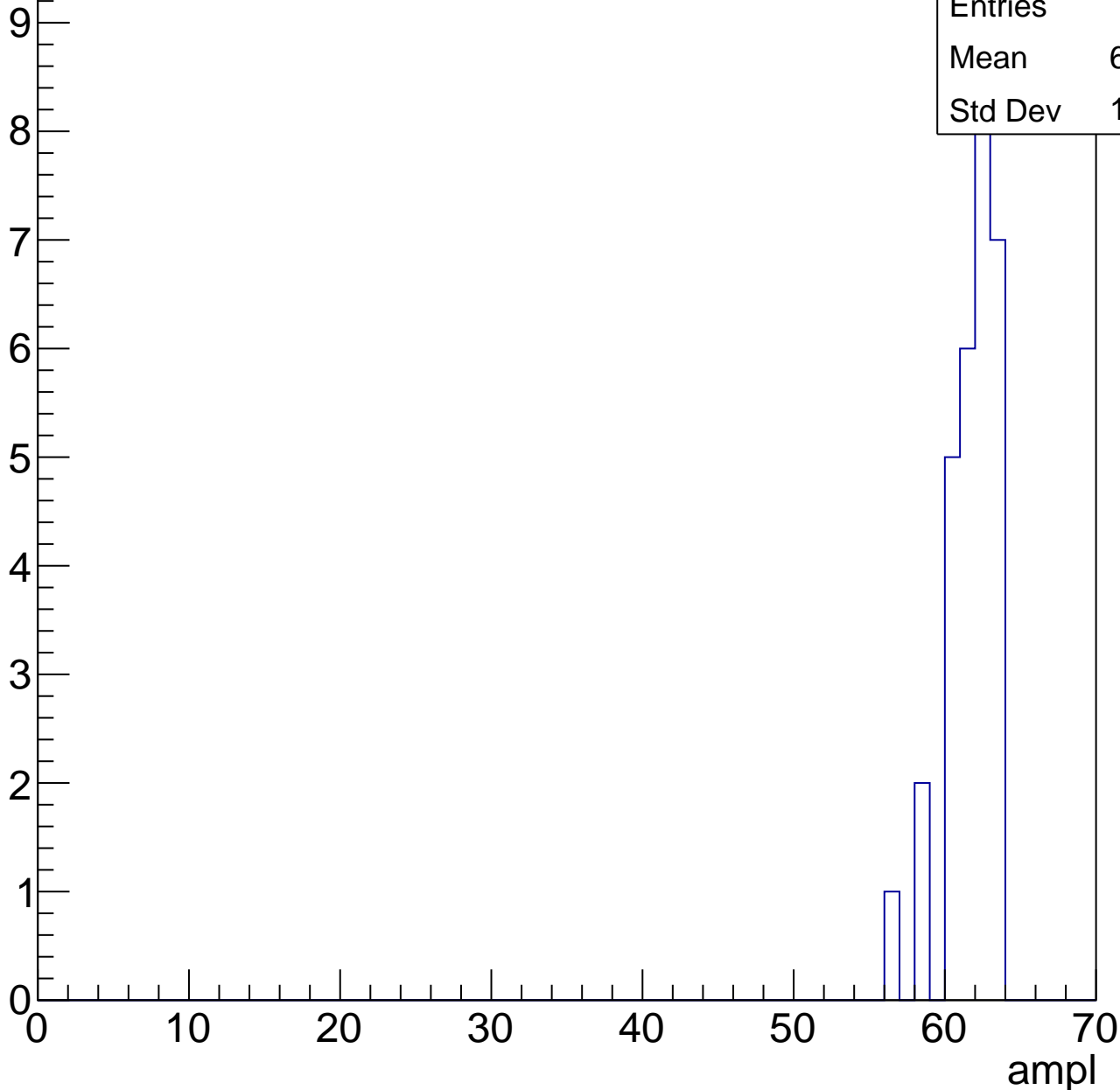


B0L002S, U5-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	61.23
Std Dev	1.667



B0L002S, U5-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch35, adc0

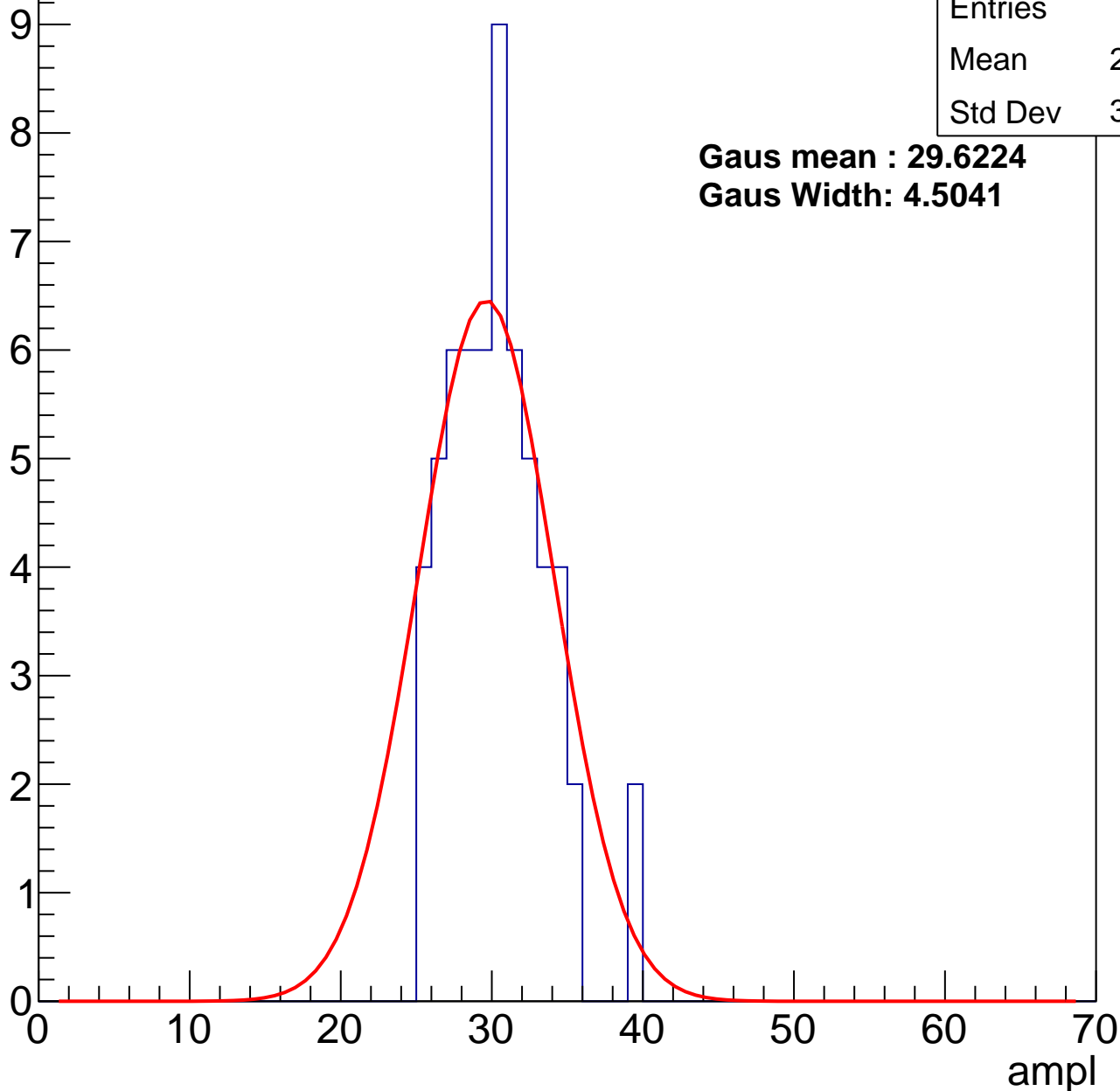
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.93
Std Dev	3.194

Gaus mean : 29.6224

Gaus Width: 4.5041



B0L002S, U5-ch35, adc1

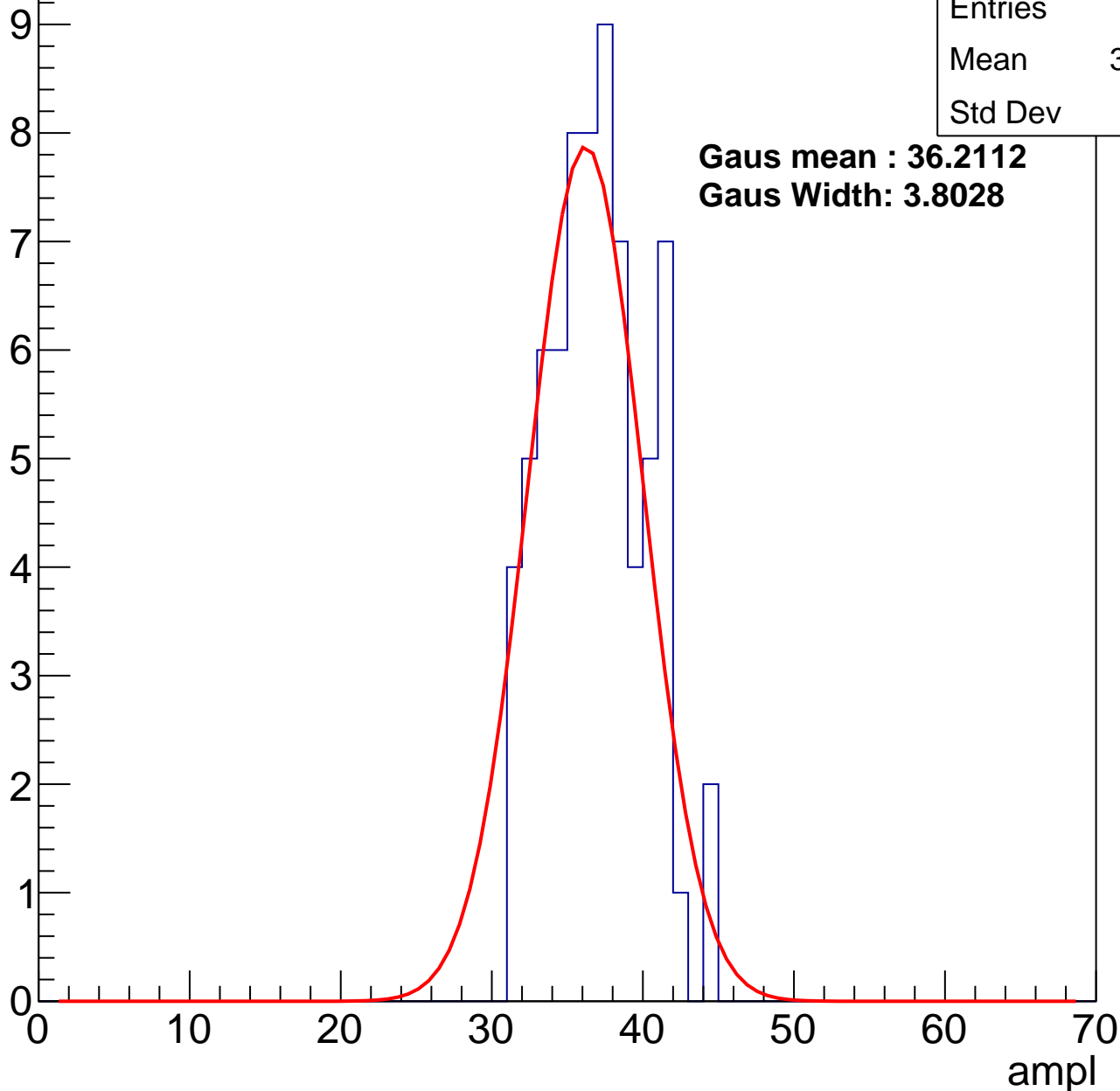
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.47
Std Dev	3.21

Gaus mean : 36.2112

Gaus Width: 3.8028



B0L002S, U5-ch35, adc2

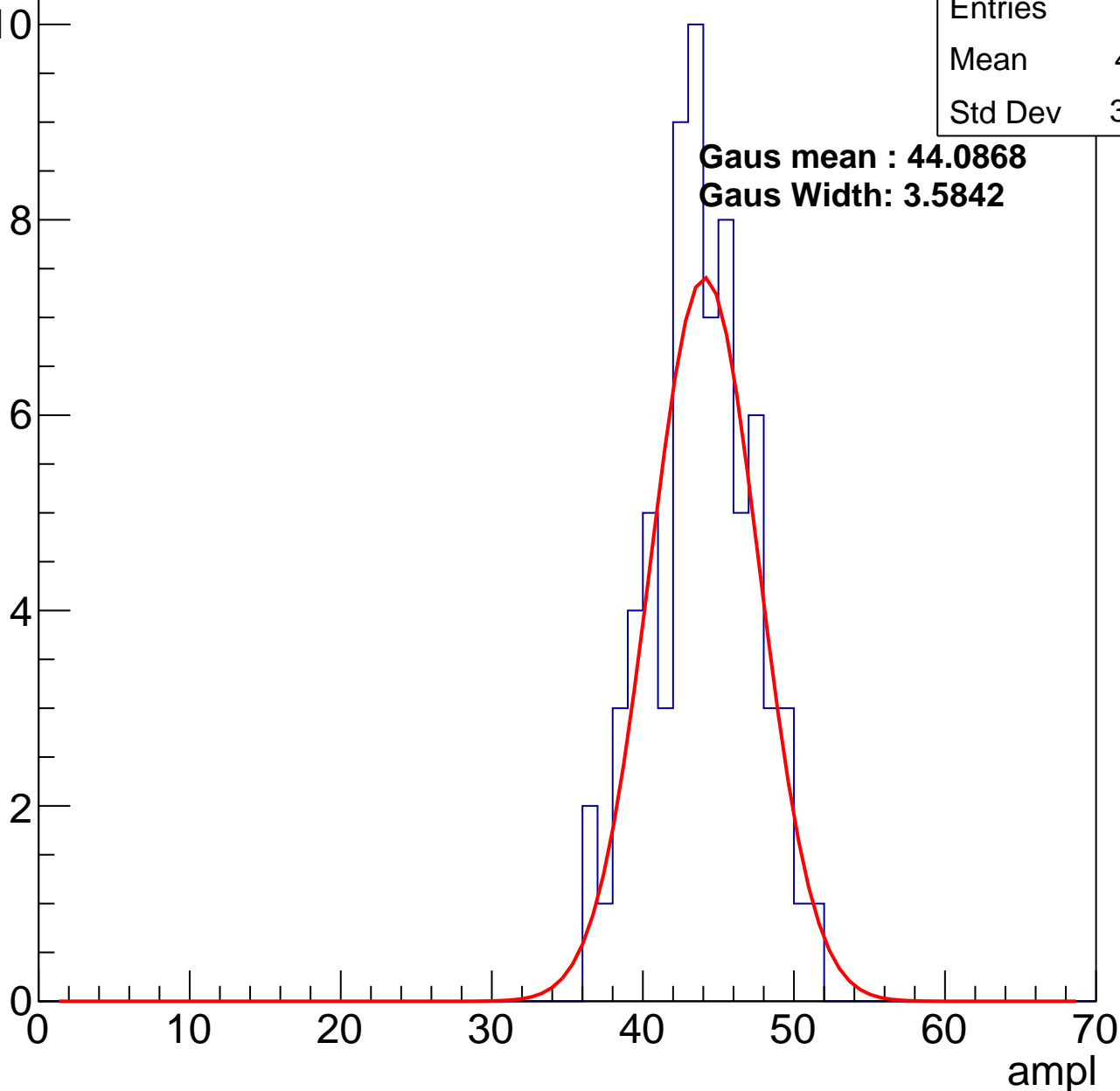
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.41
Std Dev	3.363

Gaus mean : 44.0868

Gaus Width: 3.5842

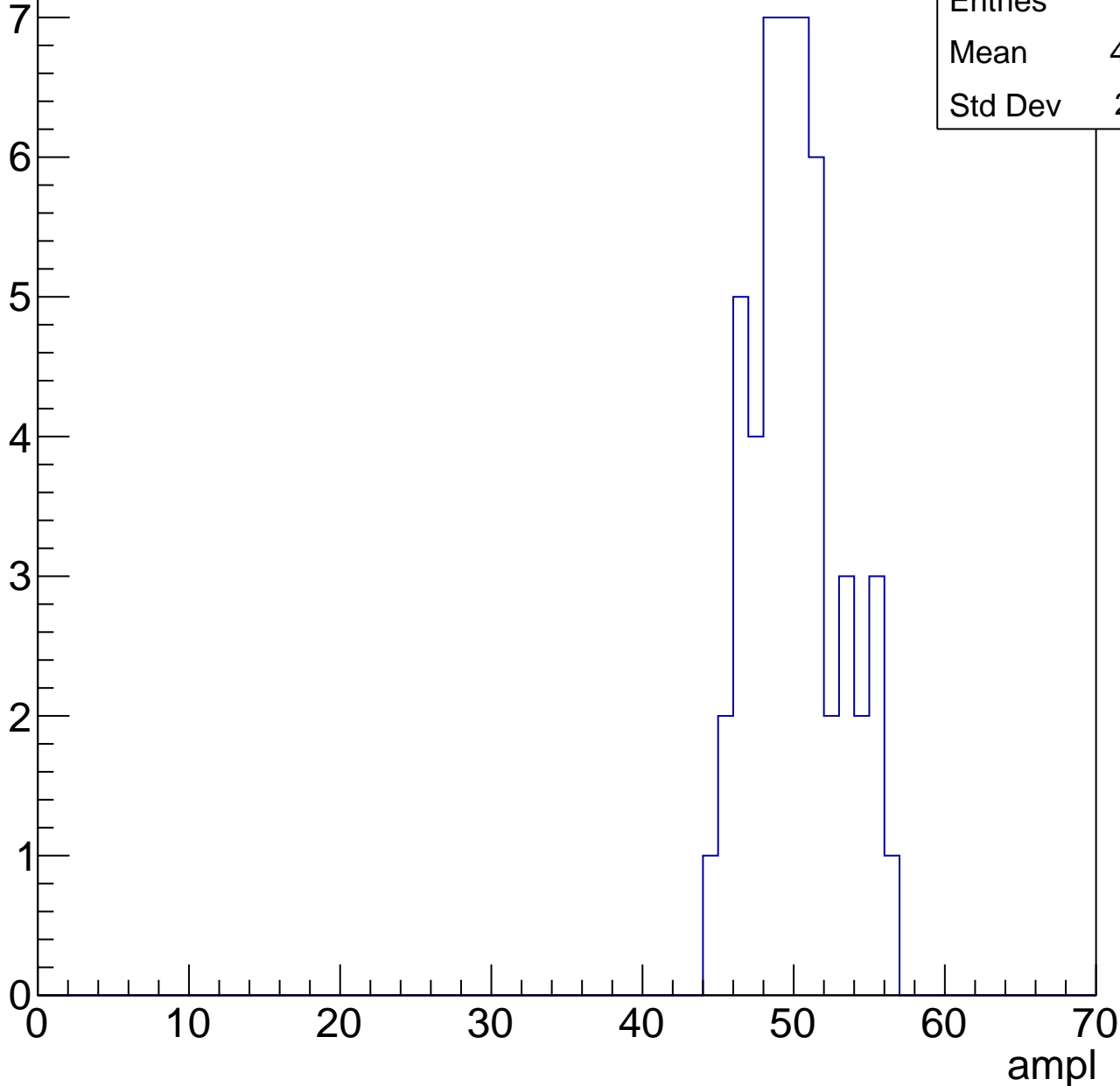


B0L002S, U5-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	49.58
Std Dev	2.871

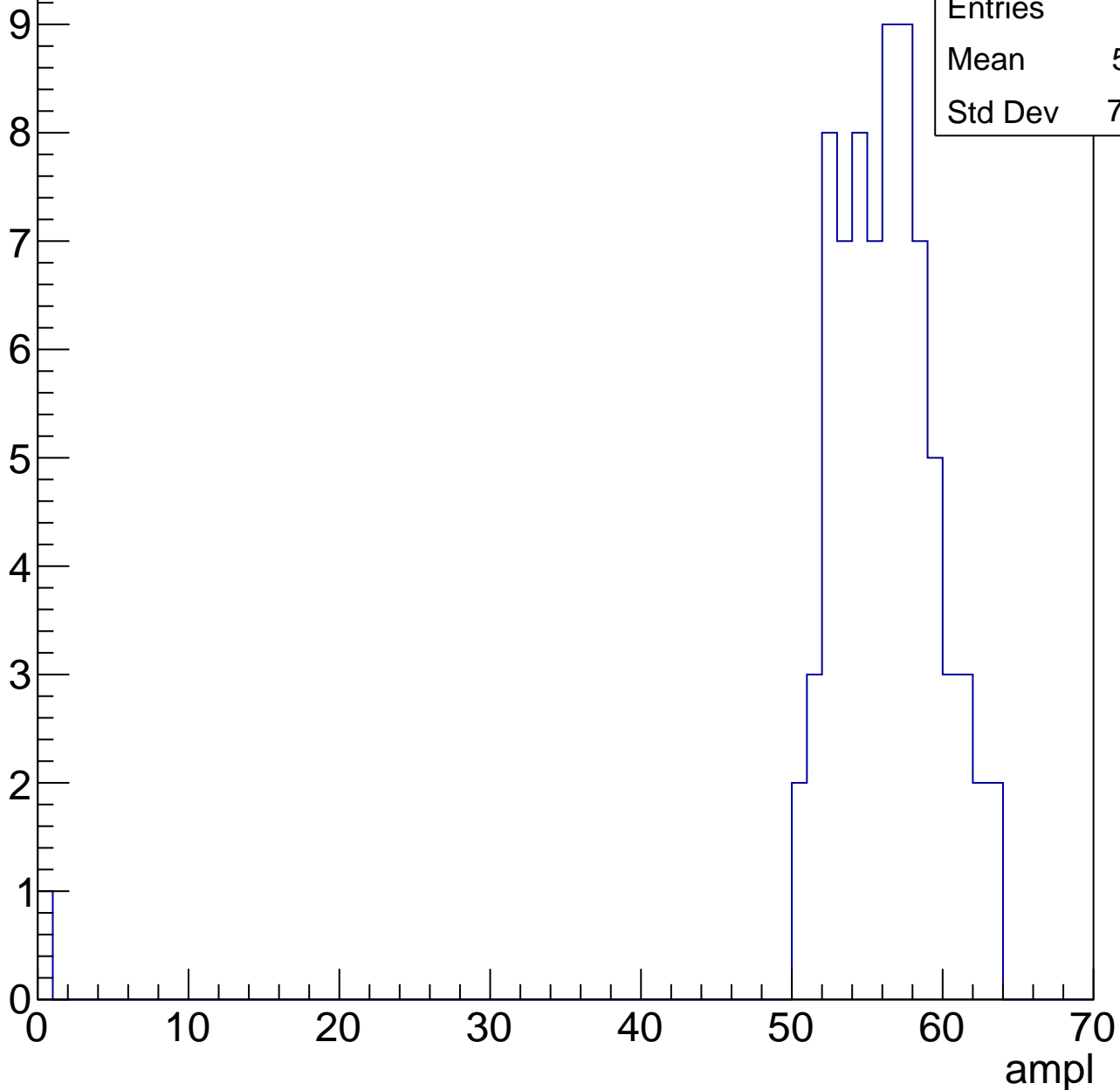


B0L002S, U5-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	55.11
Std Dev	7.096

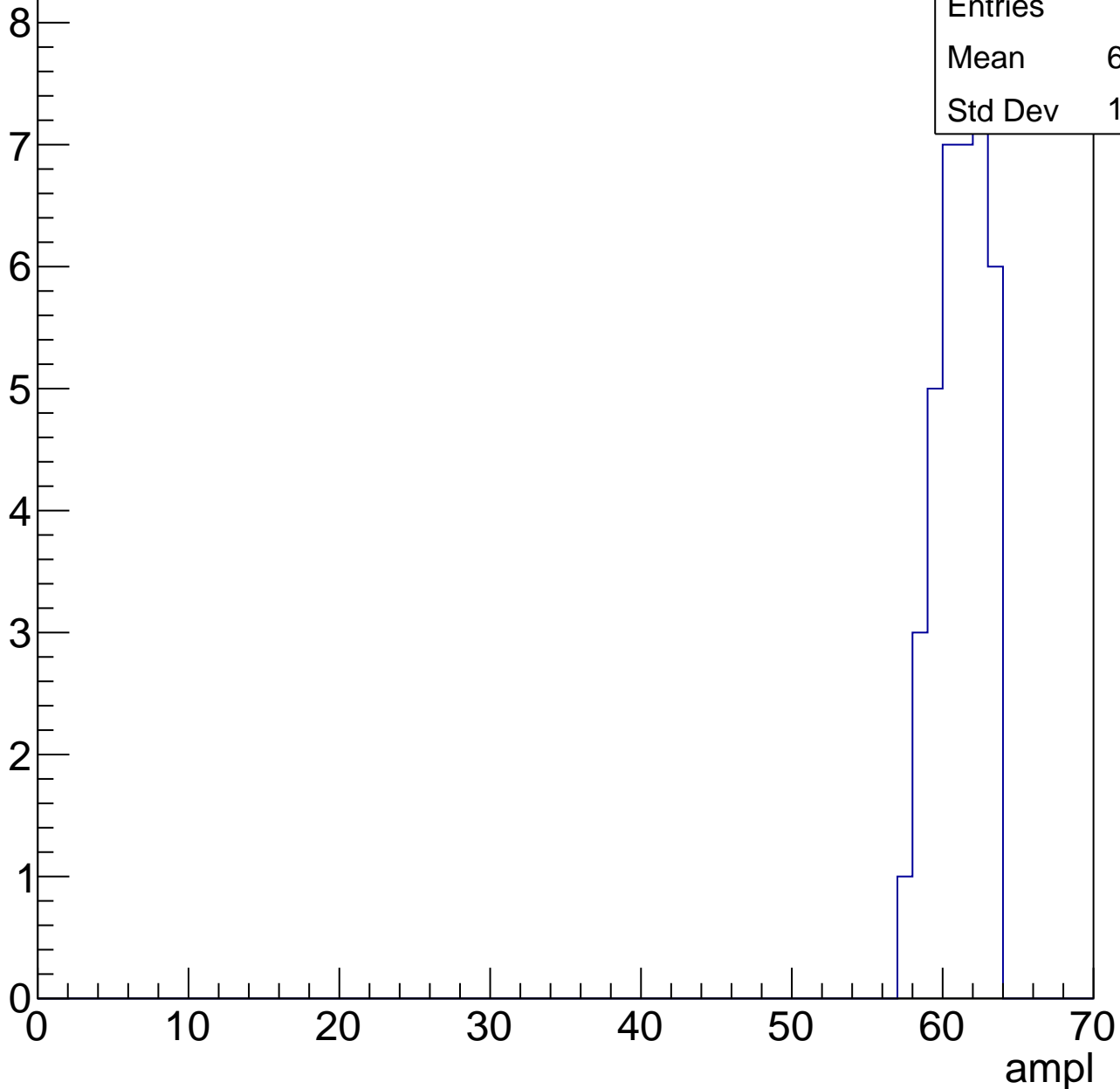


B0L002S, U5-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	60.73
Std Dev	1.638



B0L002S, U5-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch36, adc0

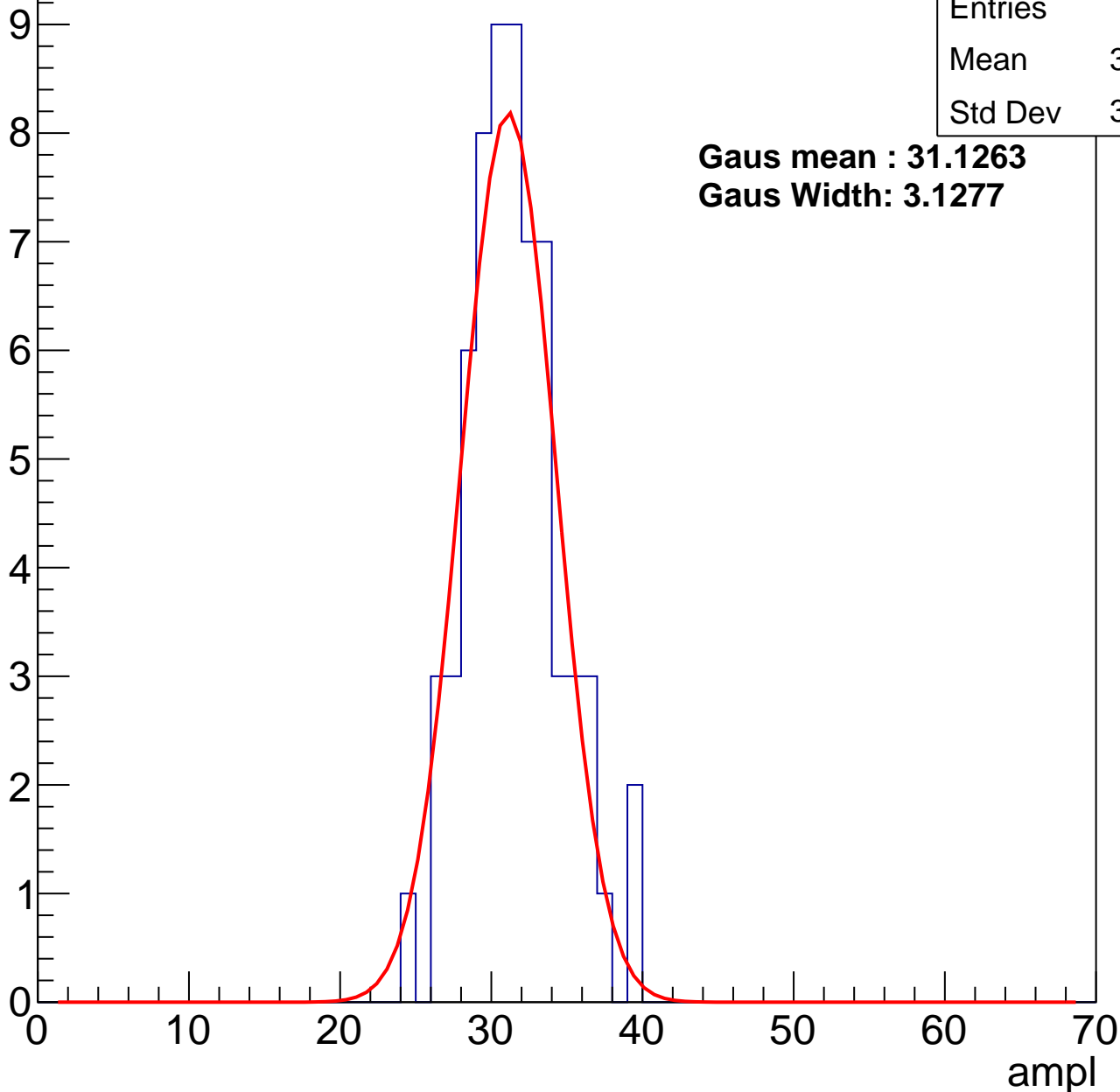
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	31.03
Std Dev	3.078

Gaus mean : 31.1263

Gaus Width: 3.1277



B0L002S, U5-ch36, adc1

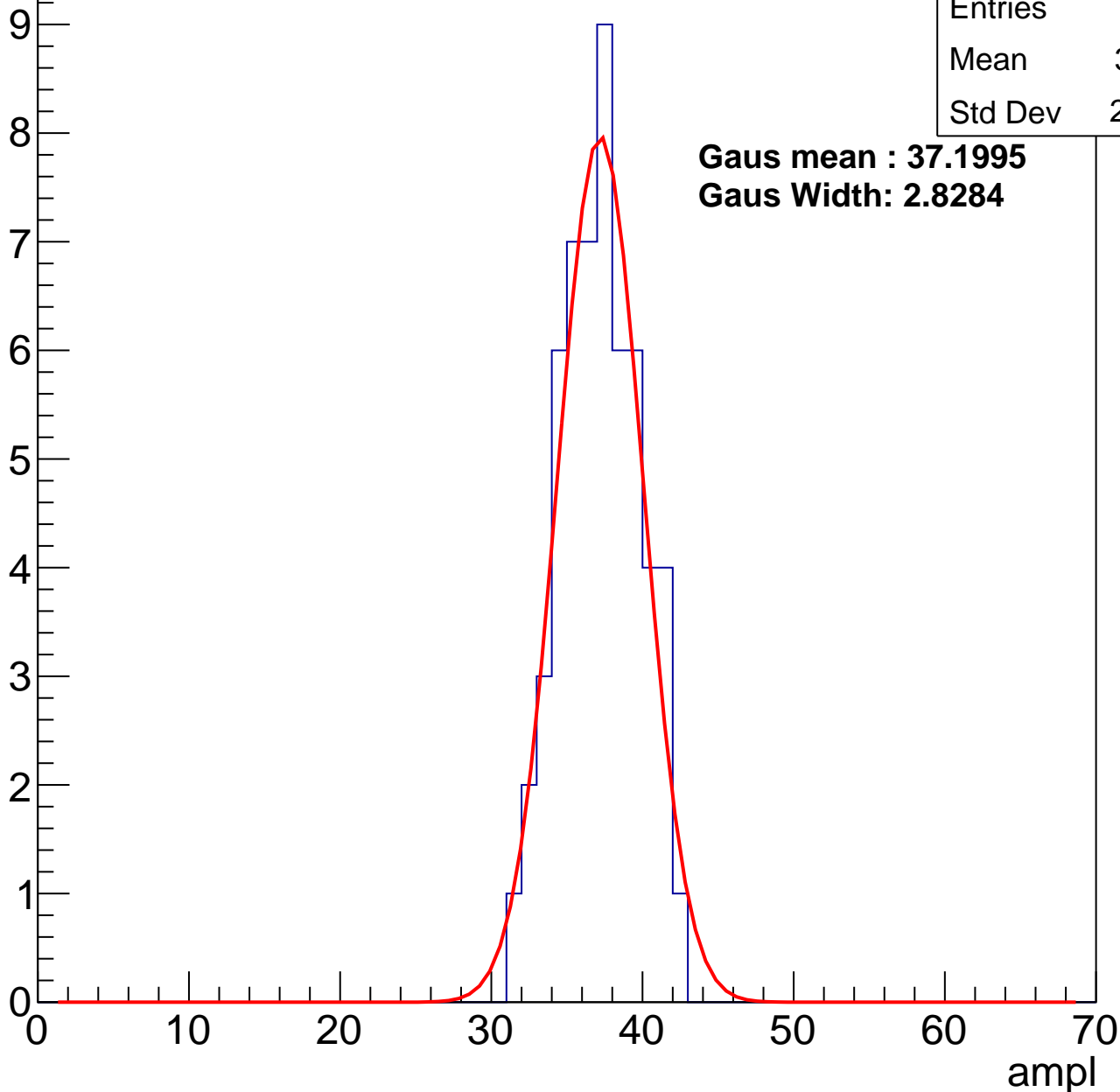
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.71
Std Dev	2.582

Gaus mean : 37.1995

Gaus Width: 2.8284



B0L002S, U5-ch36, adc2

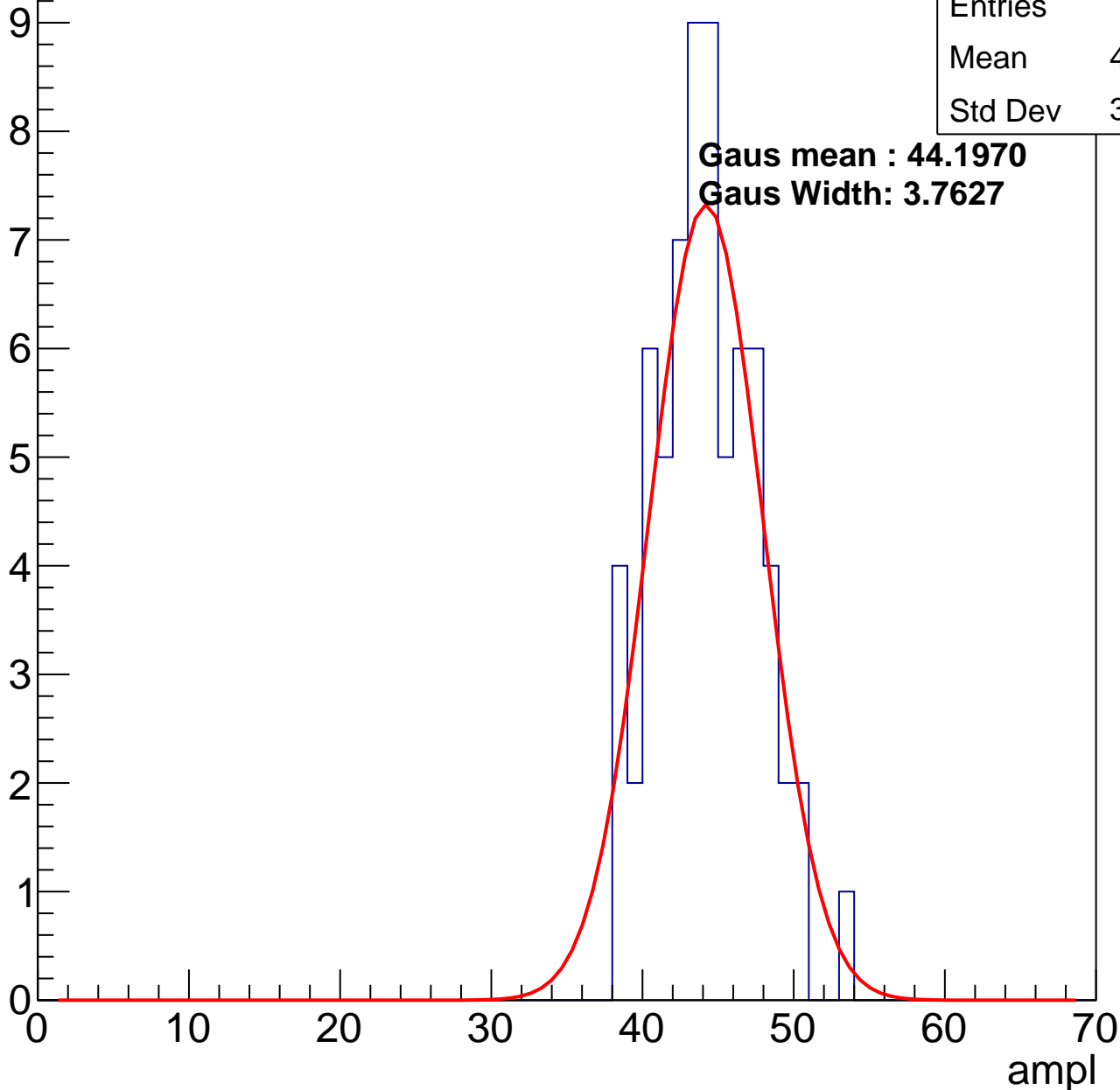
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.79
Std Dev	3.252

Gaus mean : 44.1970

Gaus Width: 3.7627

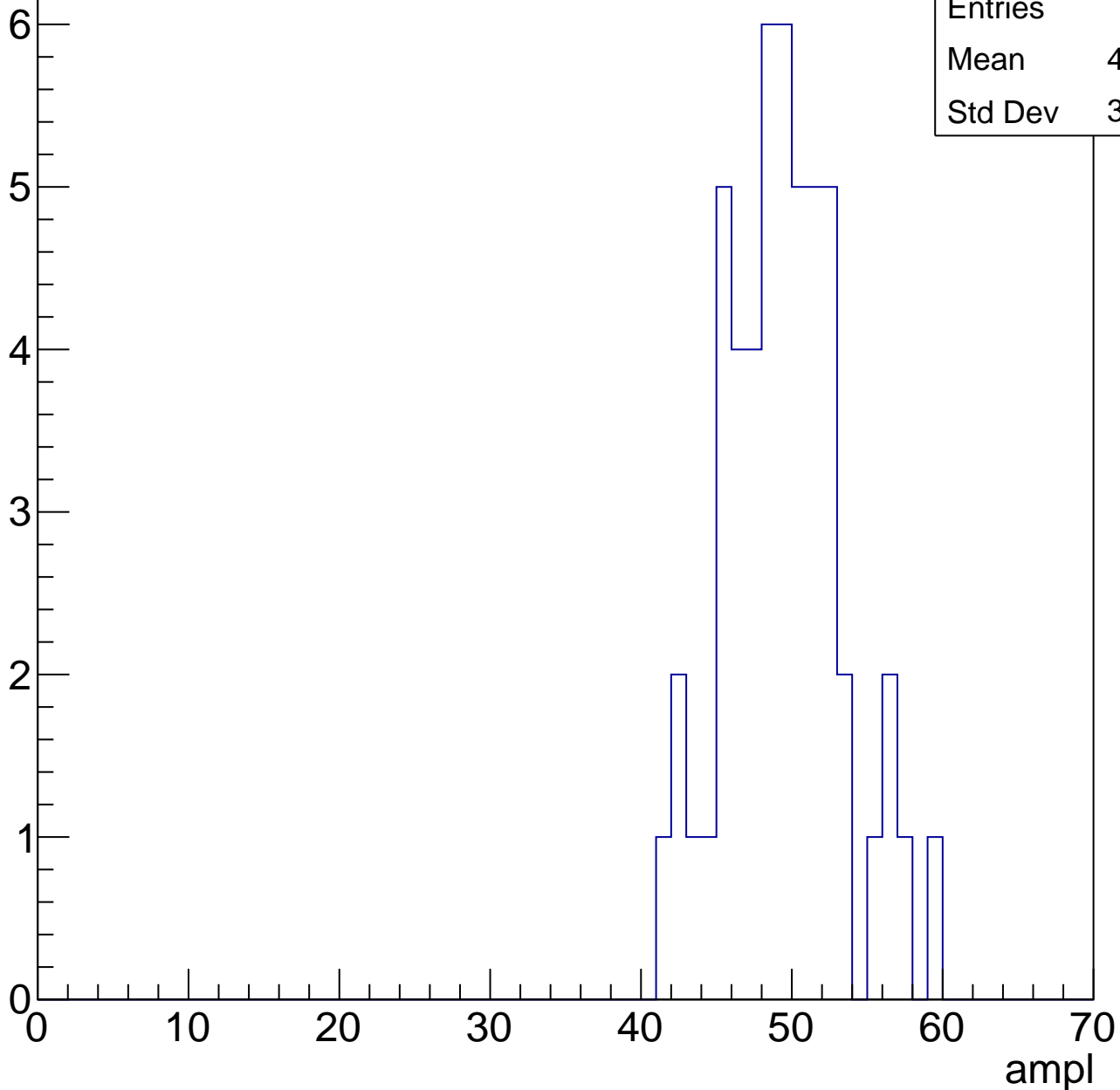


B0L002S, U5-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	48.94
Std Dev	3.825



B0L002S, U5-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

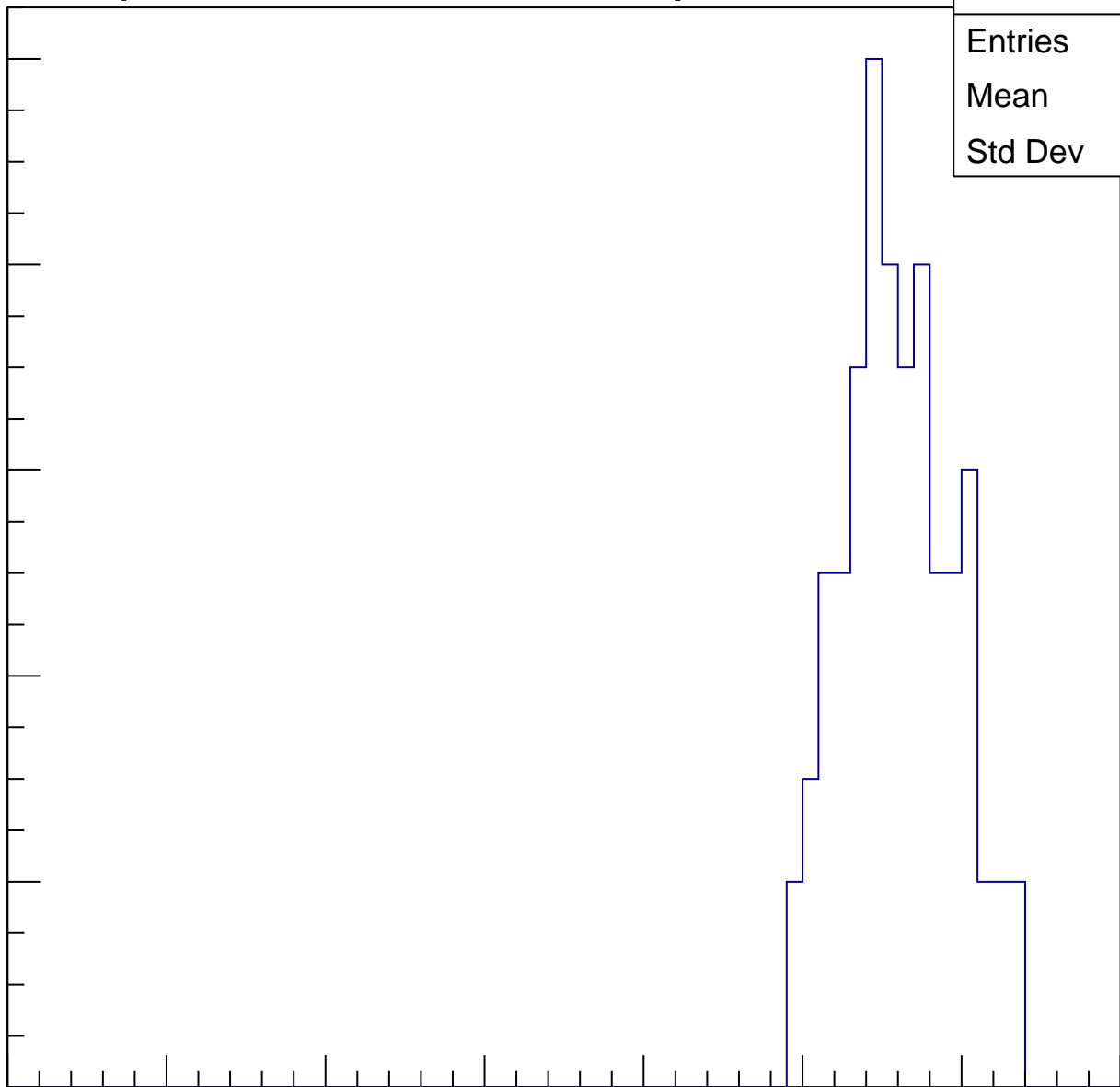
Entries	77
Mean	55.57
Std Dev	3.413

Entry

10
8
6
4
2
0

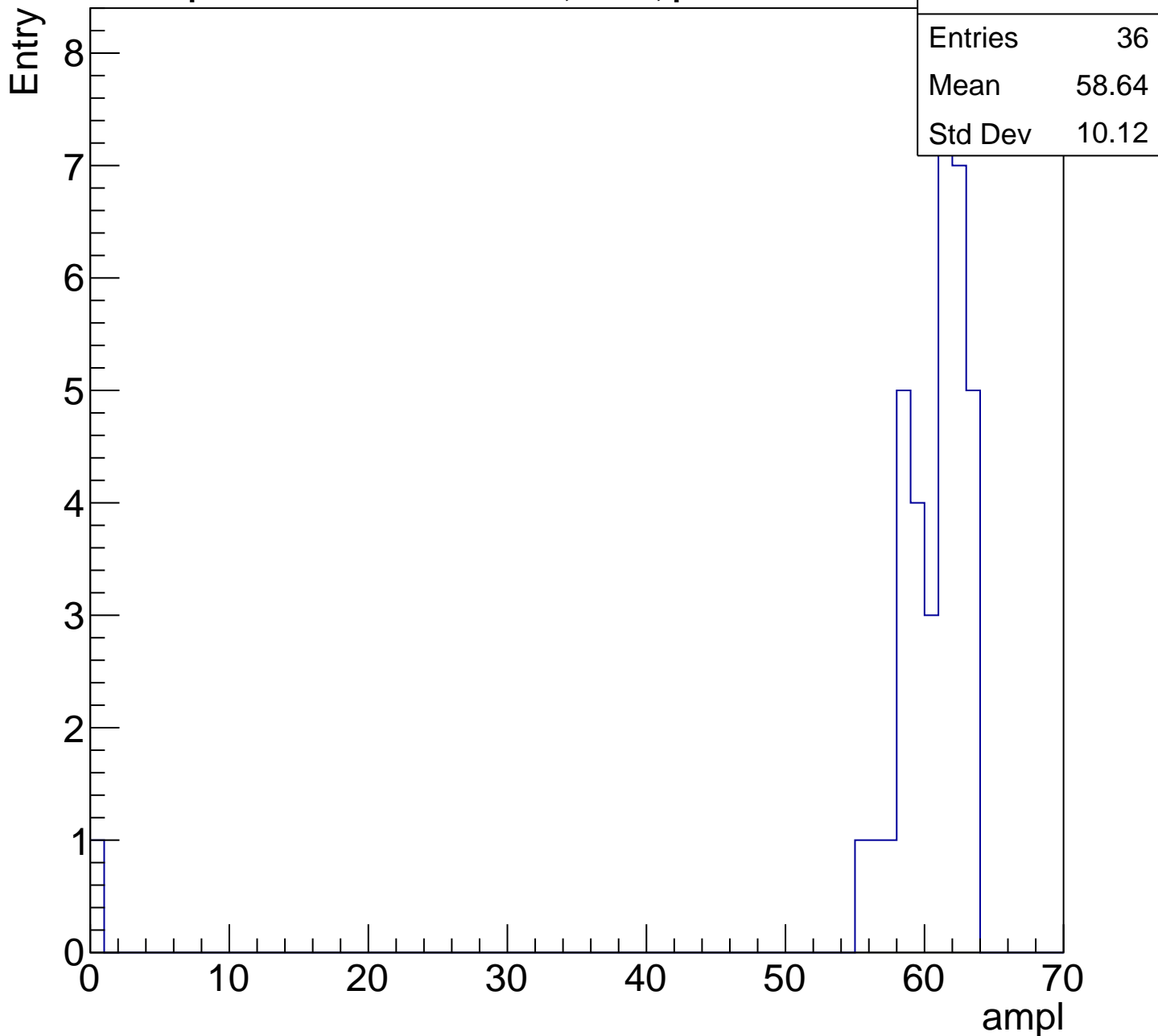
0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch36, adc5

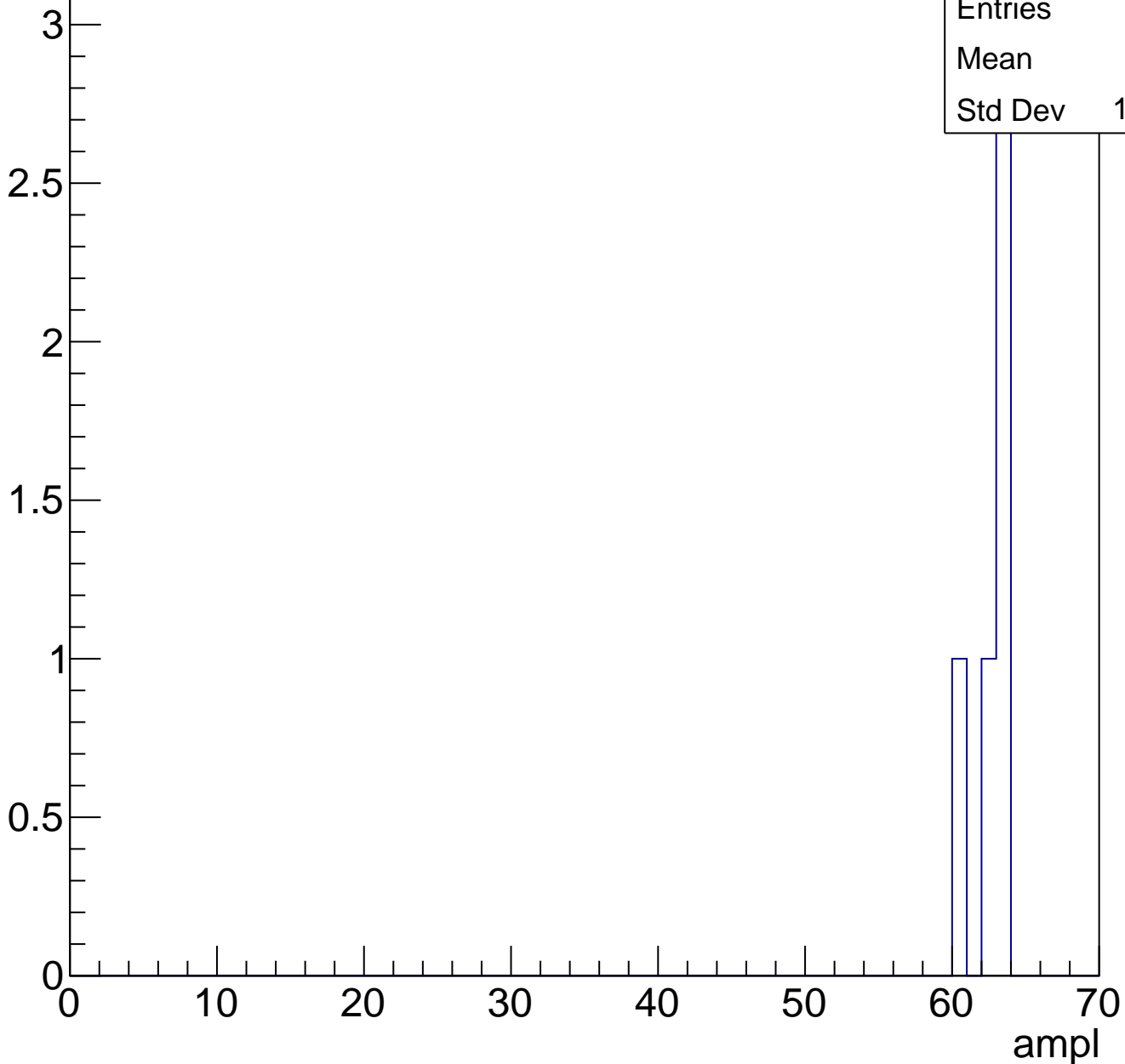
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U5-ch37, adc0

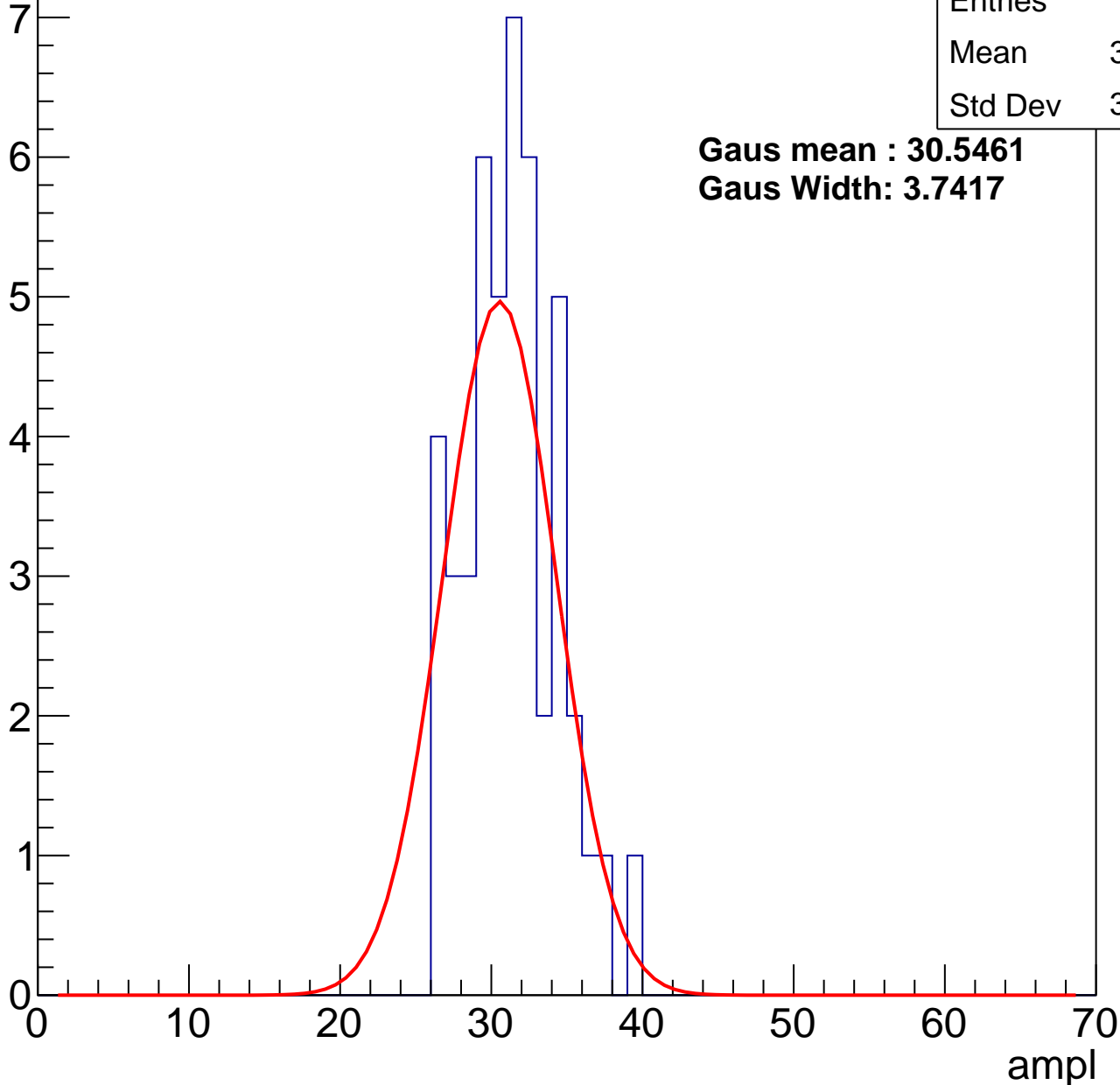
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	30.87
Std Dev	3.019

Gaus mean : 30.5461

Gaus Width: 3.7417



B0L002S, U5-ch37, adc1

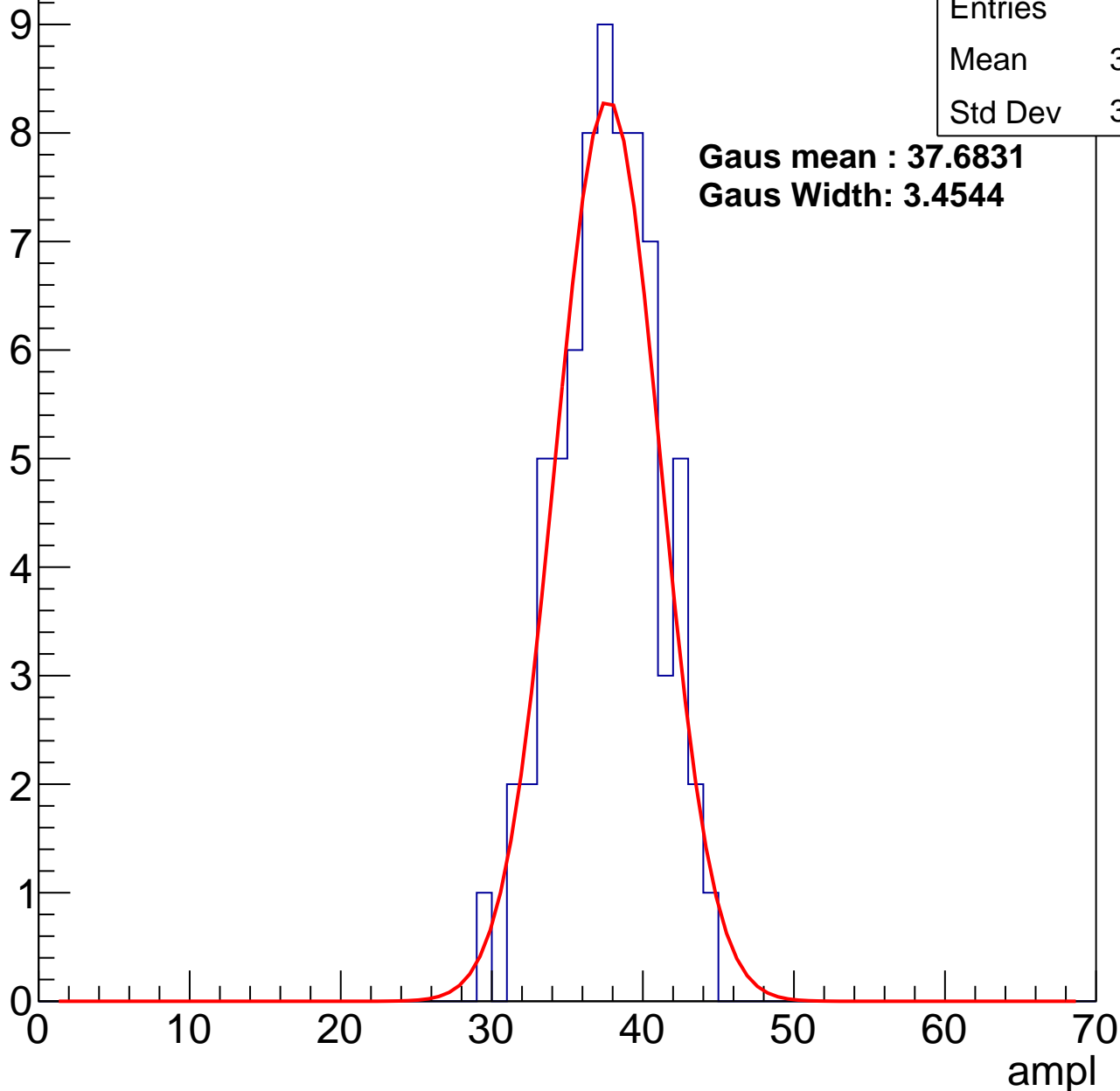
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	37.22
Std Dev	3.198

Gaus mean : 37.6831

Gaus Width: 3.4544



B0L002S, U5-ch37, adc2

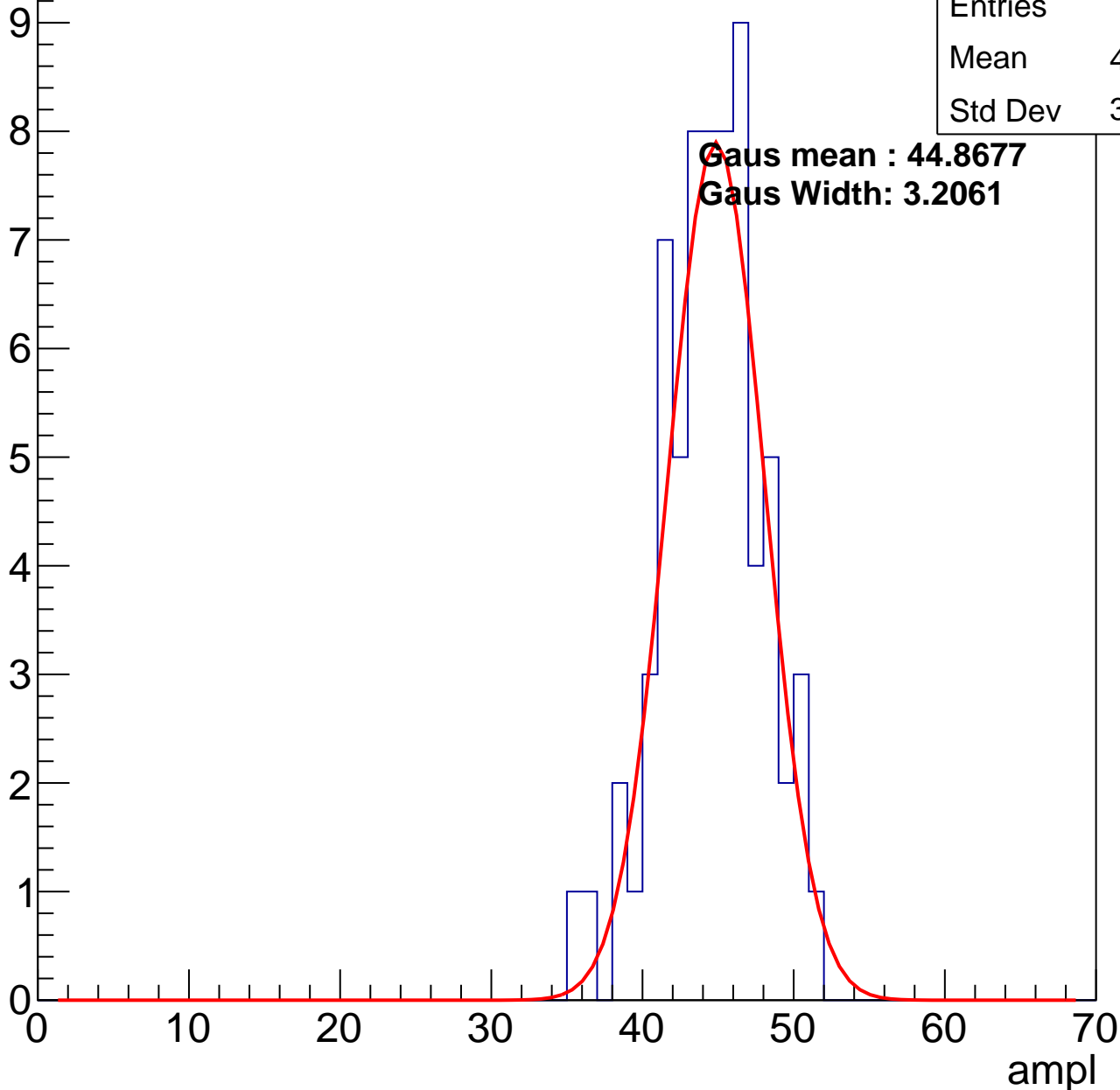
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	44.12
Std Dev	3.315

Gaus mean : 44.8677

Gaus Width: 3.2061

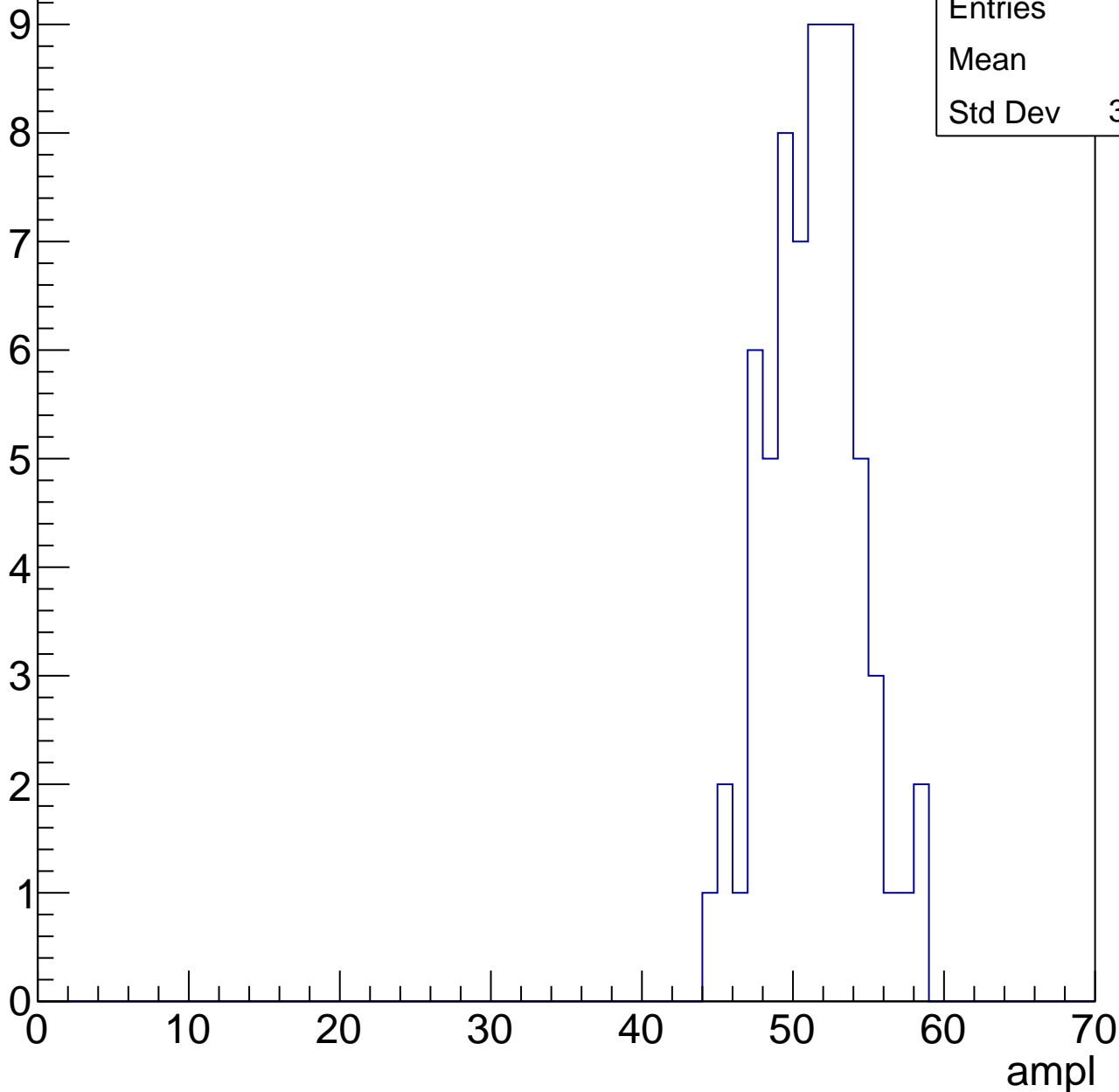


B0L002S, U5-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	50.9
Std Dev	3.003

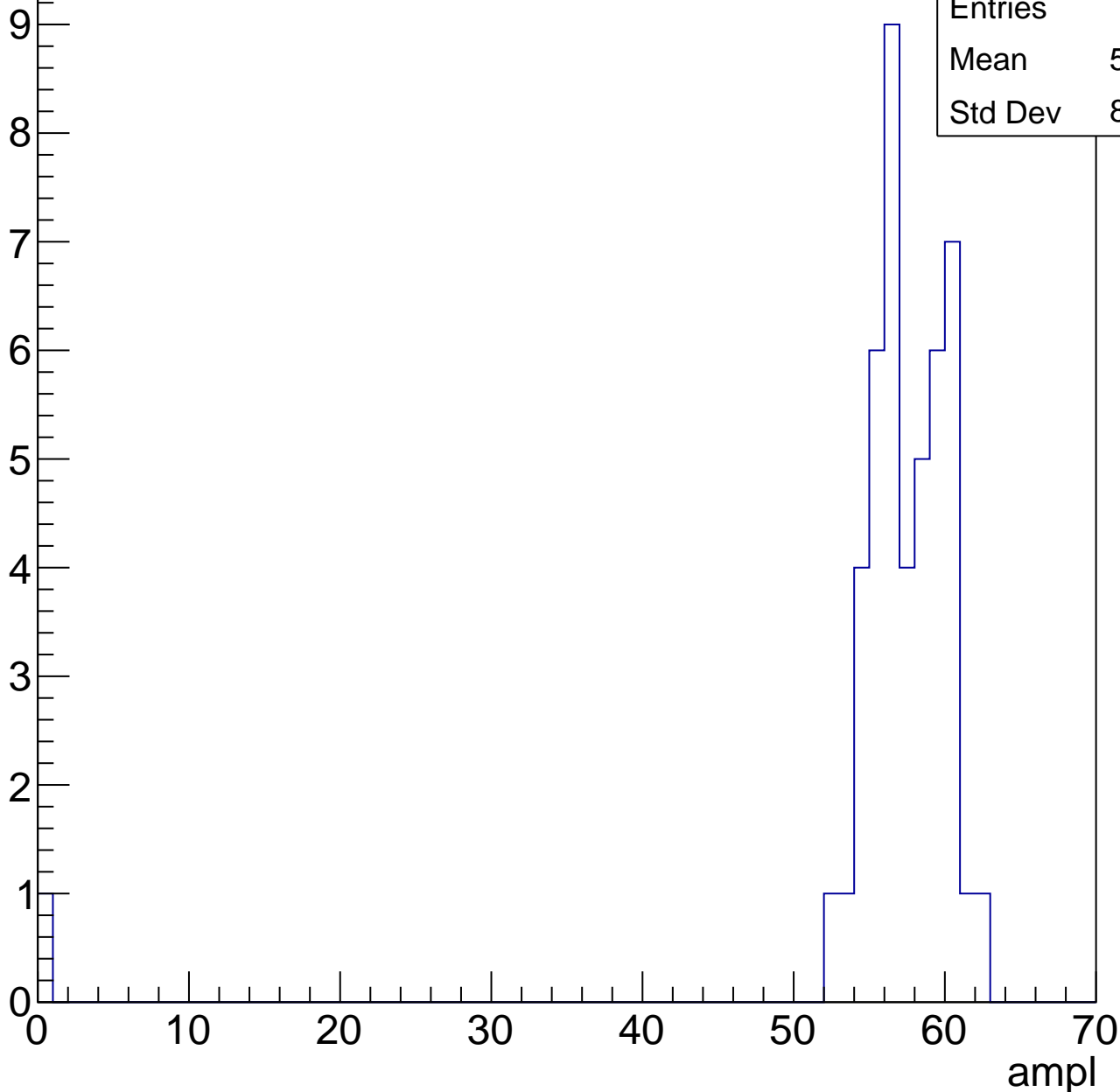


B0L002S, U5-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	55.87
Std Dev	8.639



B0L002S, U5-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries 45

Mean 60.47

Std Dev 2.344

8

6

4

2

0

0

10

20

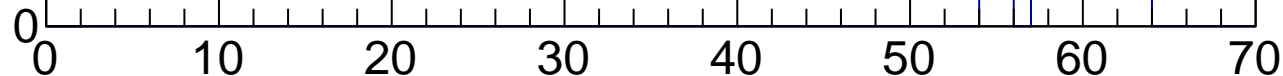
30

40

50

60

ampl



B0L002S, U5-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch38, adc0

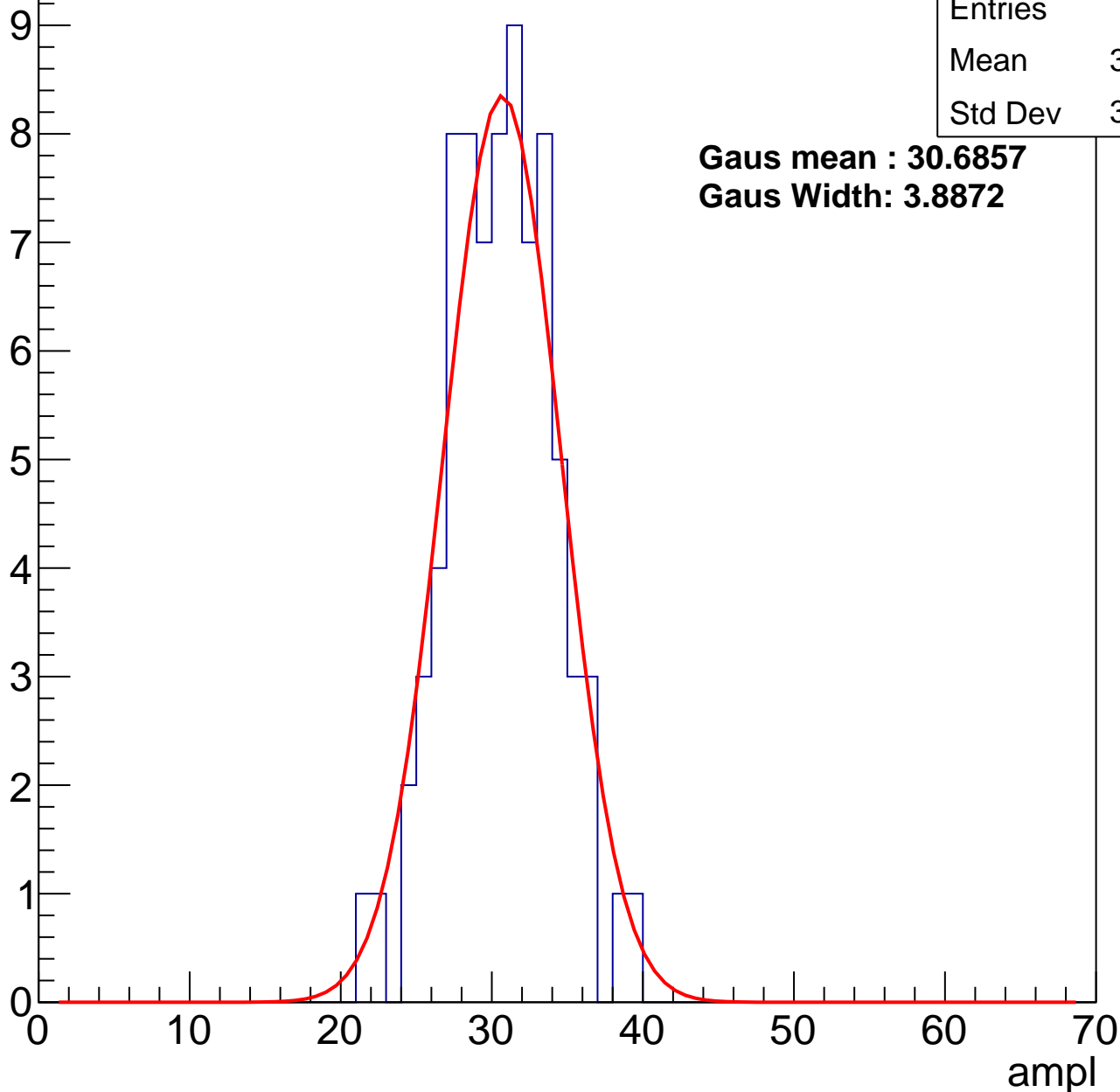
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.13
Std Dev	3.527

Gaus mean : 30.6857

Gaus Width: 3.8872



B0L002S, U5-ch38, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	36.93
Std Dev	3.53

Gaus mean : 37.5167

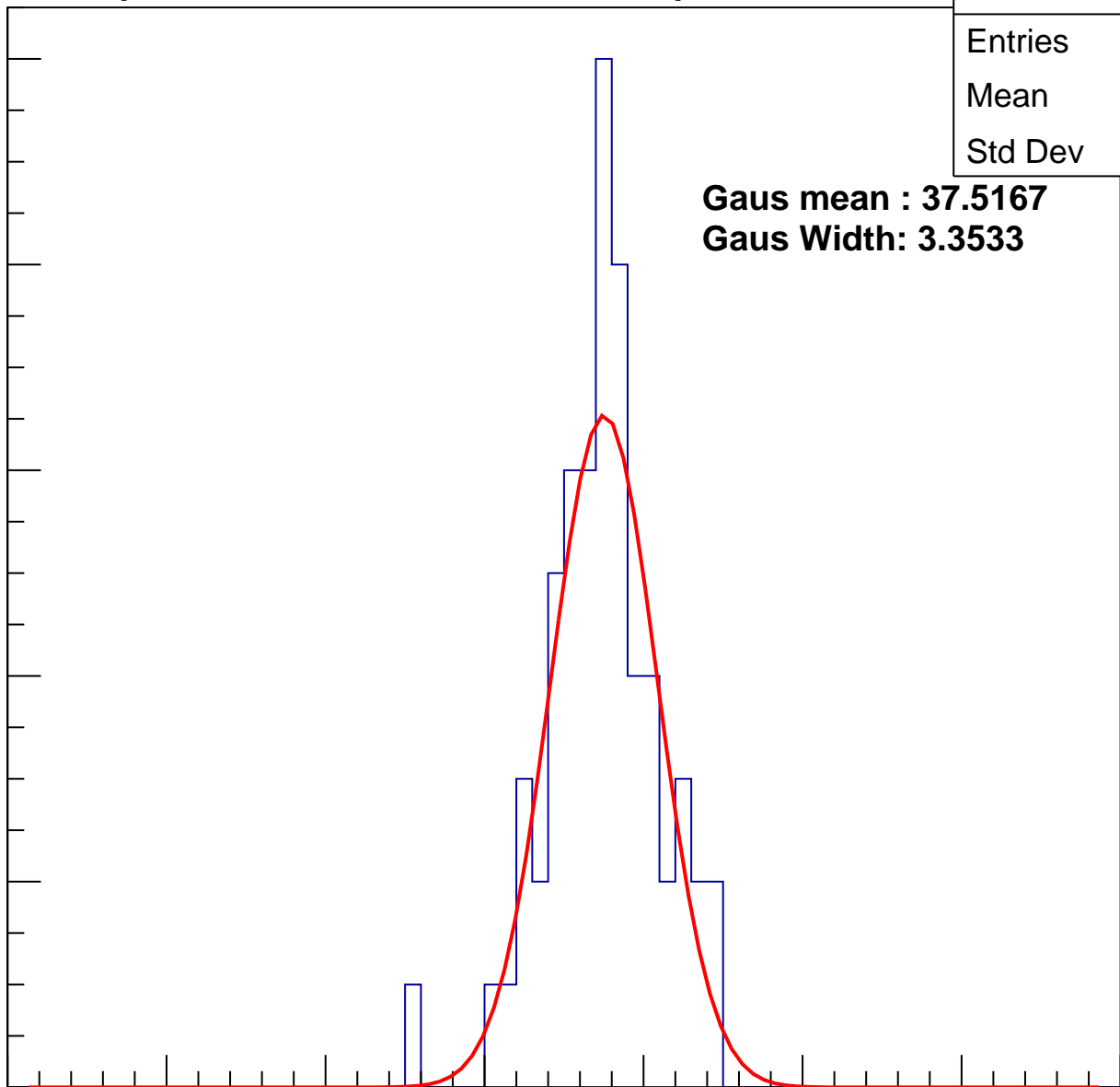
Gaus Width: 3.3533

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch38, adc2

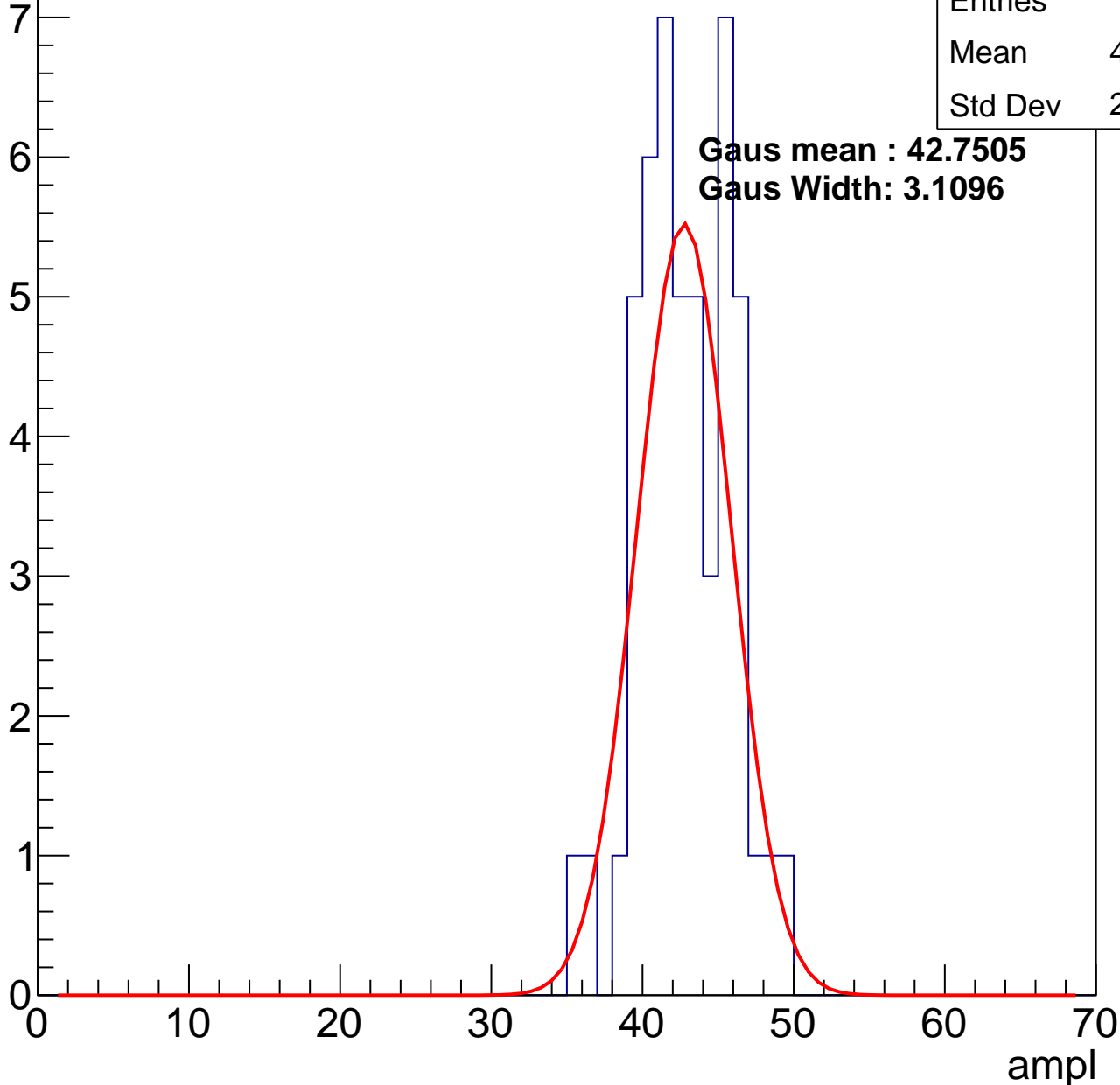
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	42.39
Std Dev	2.995

Gaus mean : 42.7505

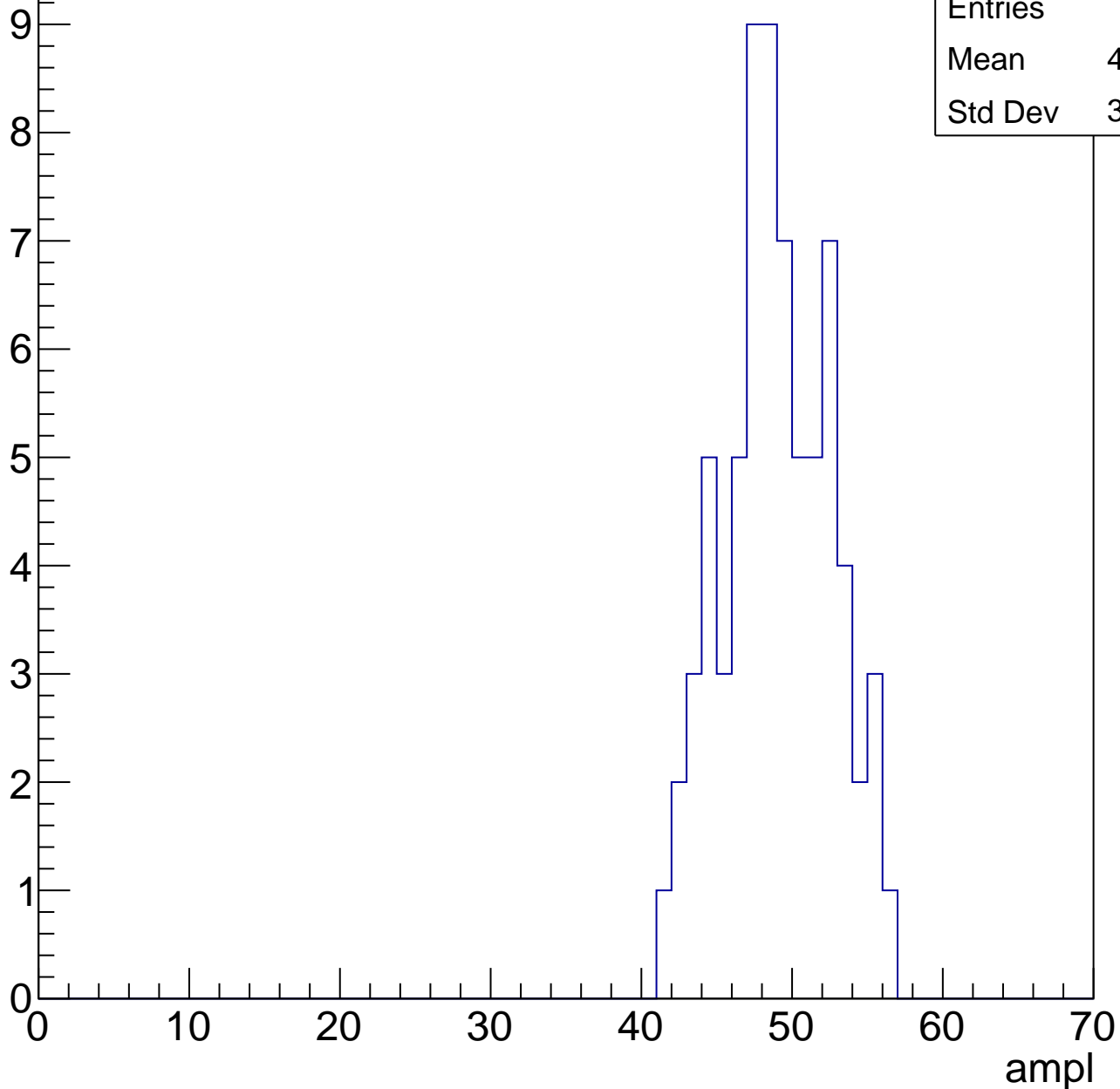
Gaus Width: 3.1096



B0L002S, U5-ch38, adc3

calib_packv5_042523_0143.root, FC#8, port C1

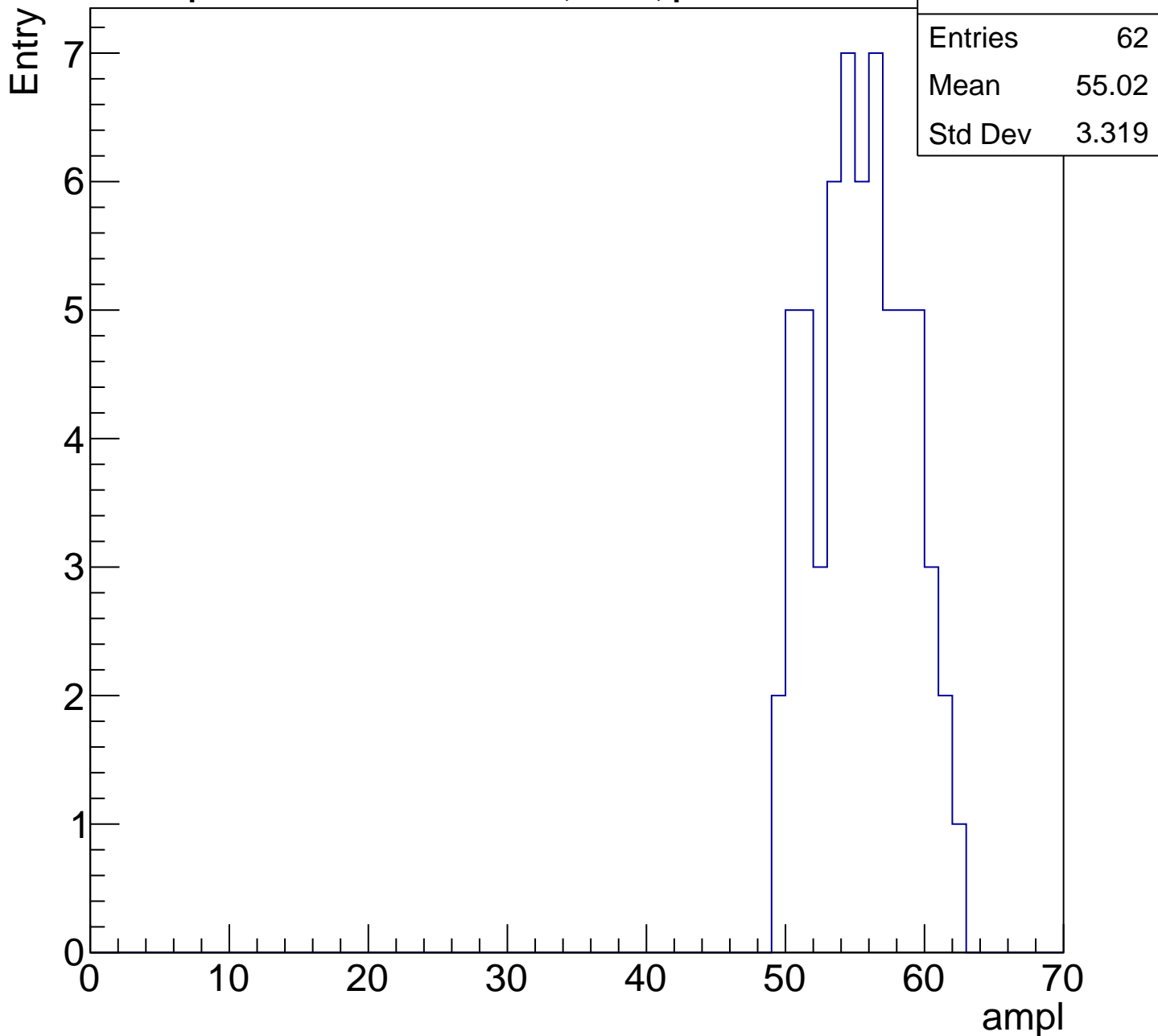
Entry



Entries	71
Mean	48.55
Std Dev	3.516

B0L002S, U5-ch38, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch38, adc5

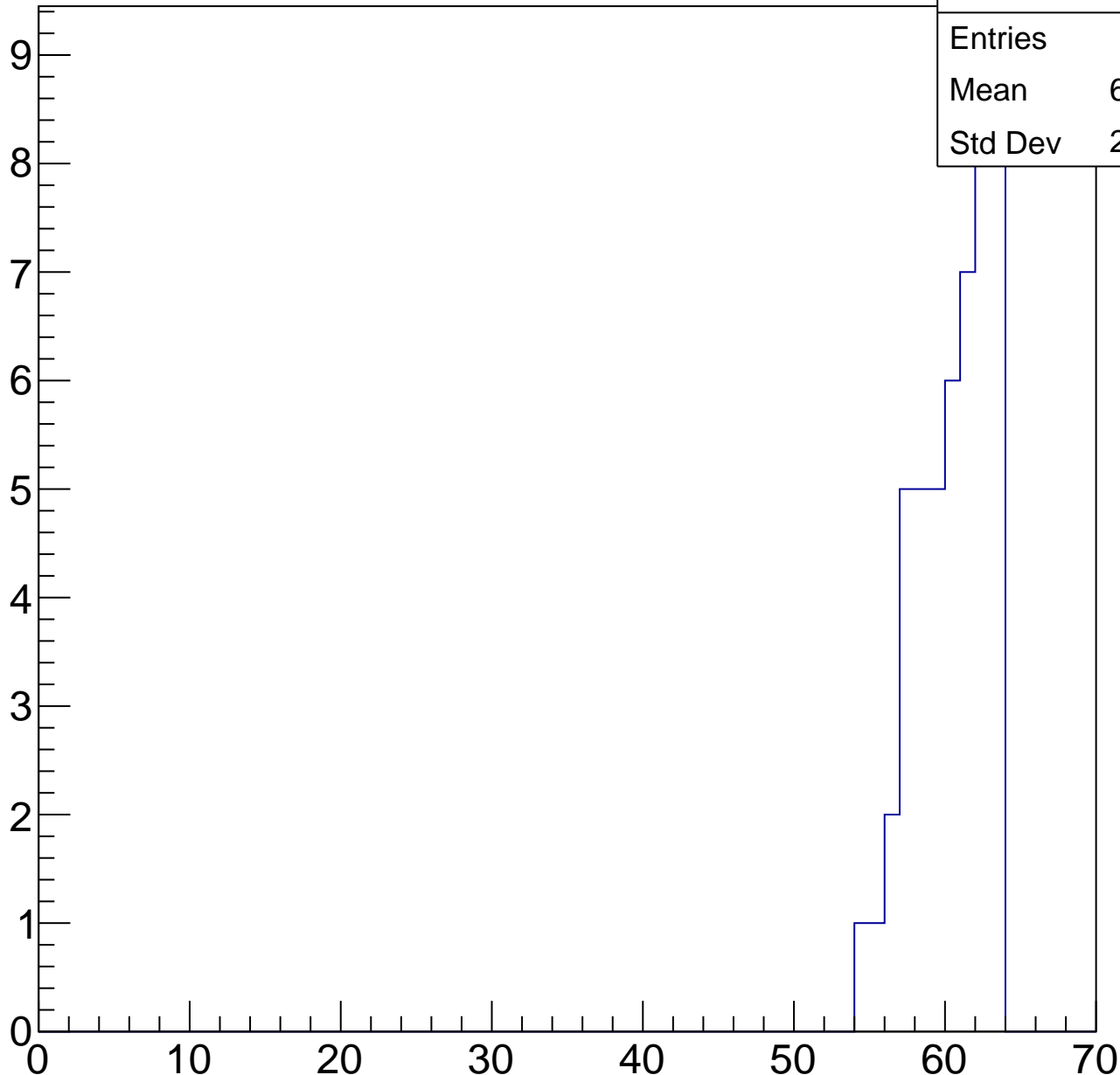
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	60.02
Std Dev	2.403

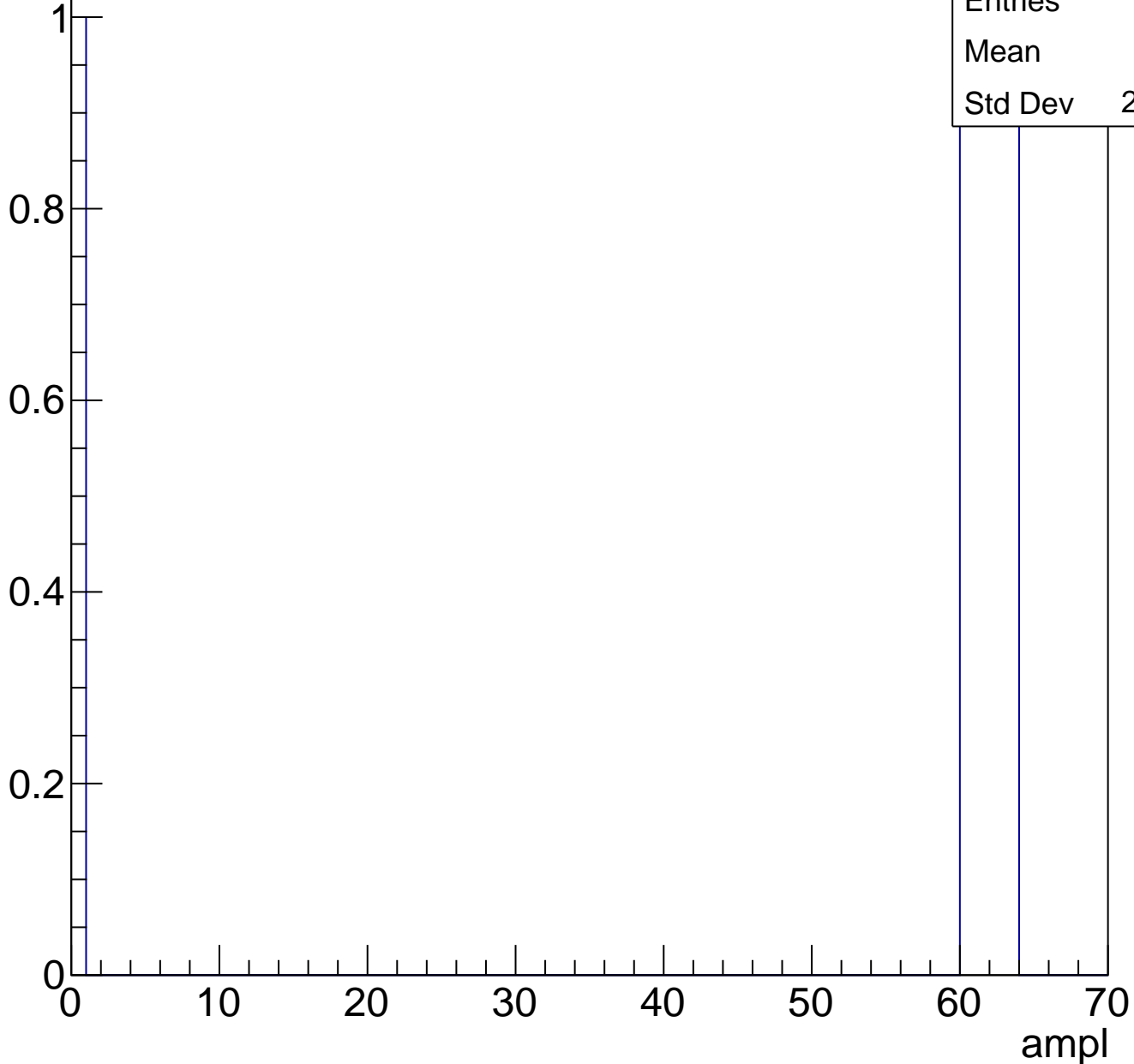
ampl



B0L002S, U5-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

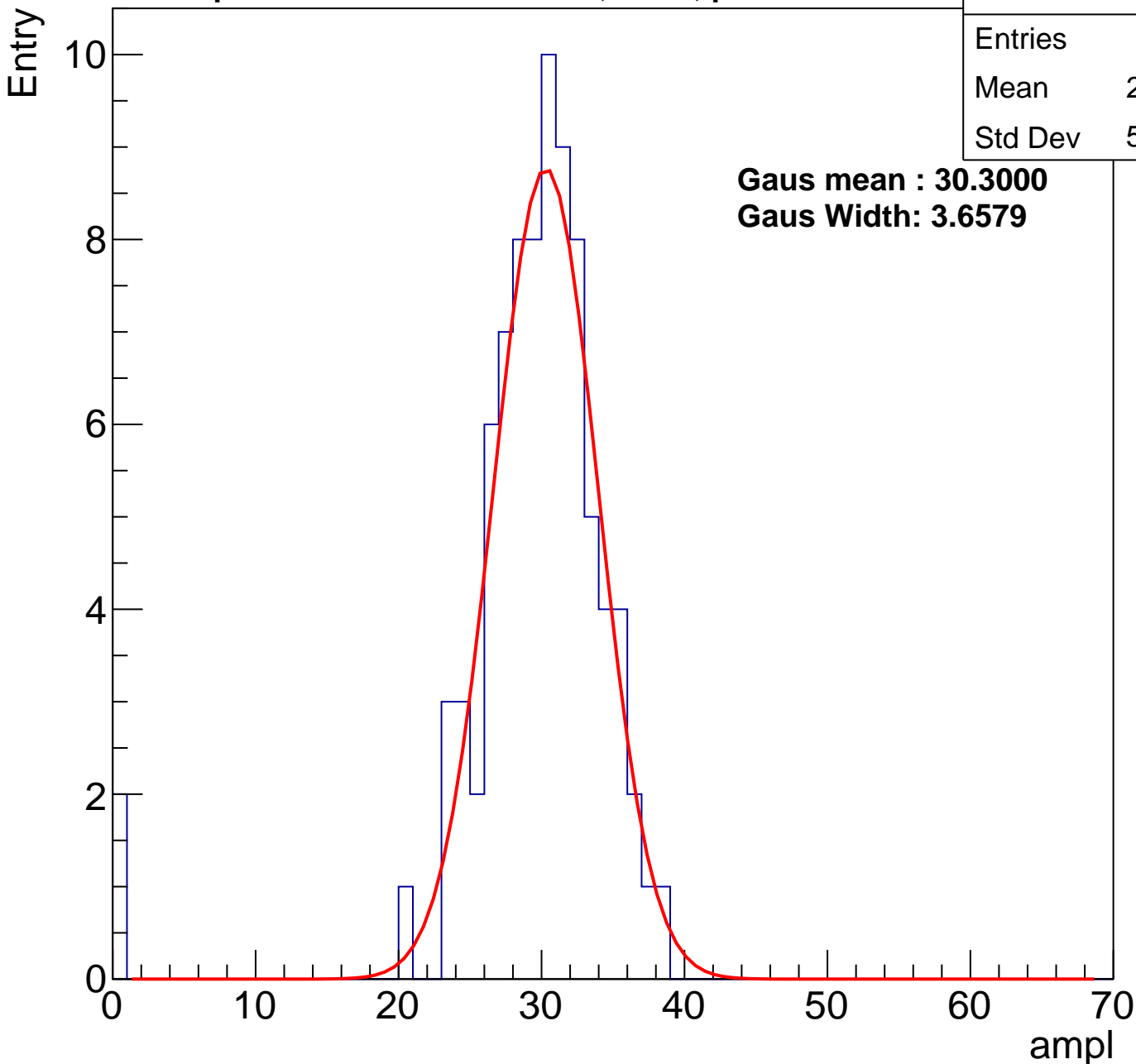
B0L002S, U5-ch39, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	28.99
Std Dev	5.725

Gaus mean : 30.3000

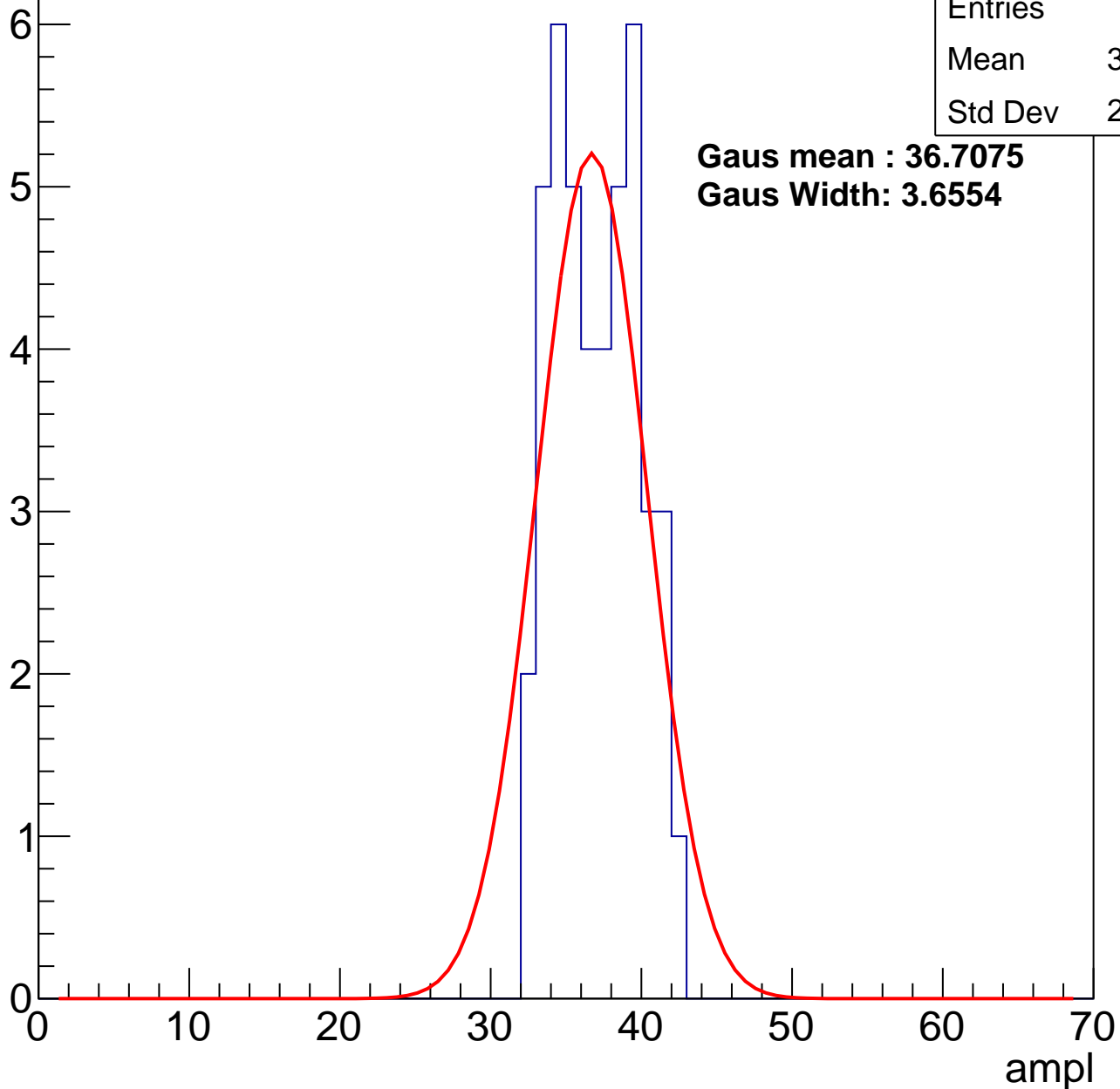
Gaus Width: 3.6579



B0L002S, U5-ch39, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch39, adc2

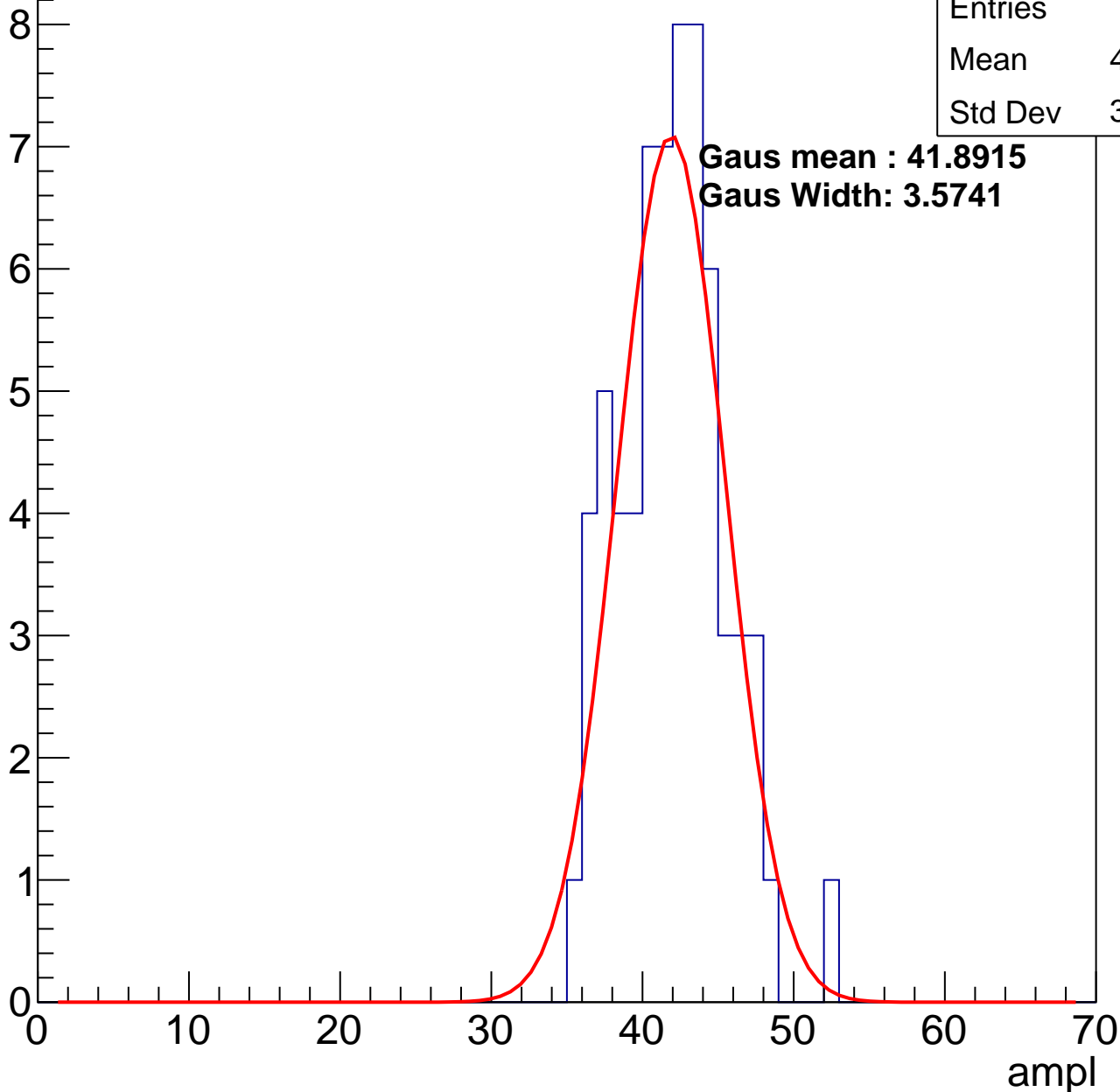
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	41.49
Std Dev	3.406

Gaus mean : 41.8915

Gaus Width: 3.5741

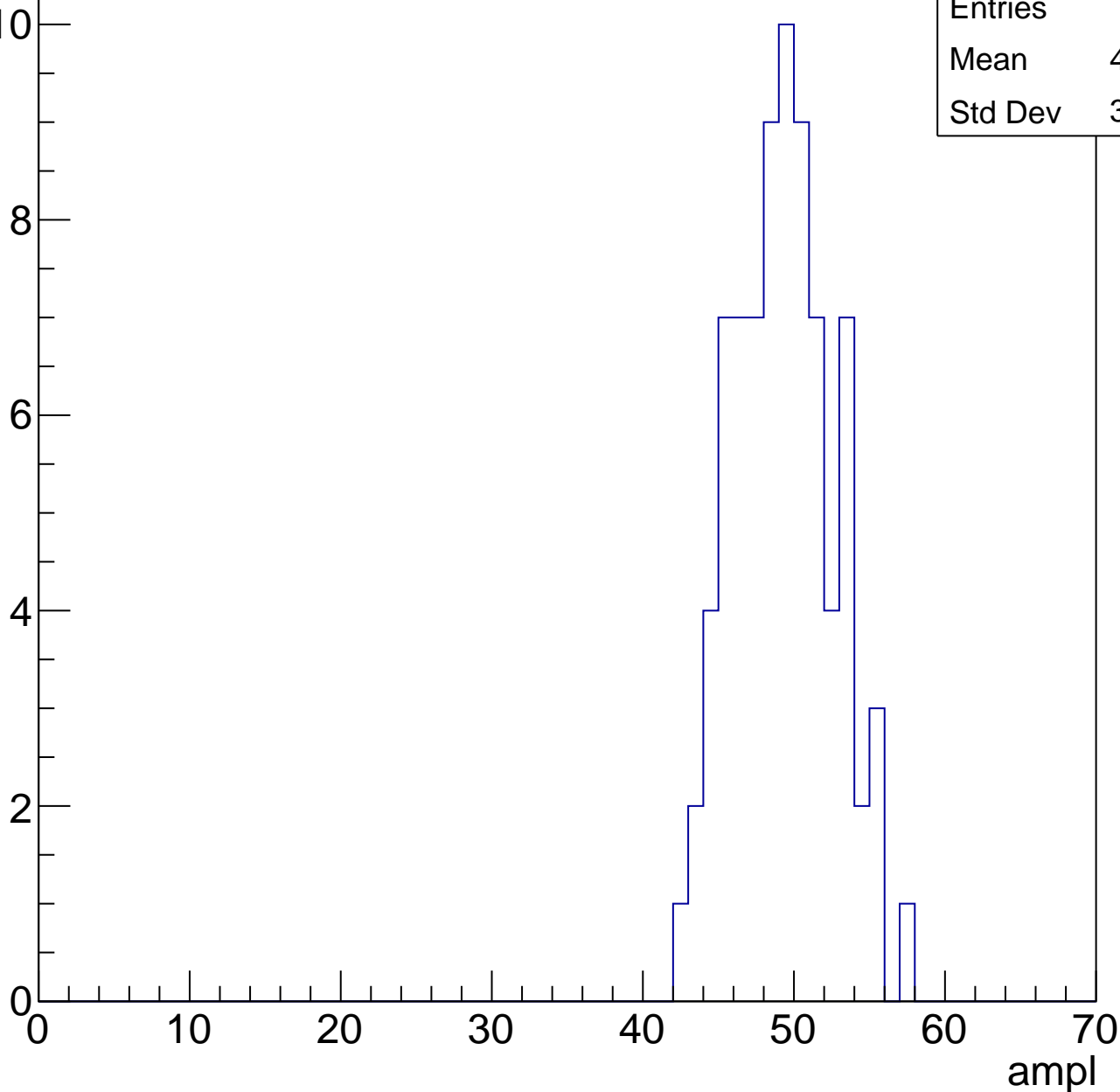


B0L002S, U5-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

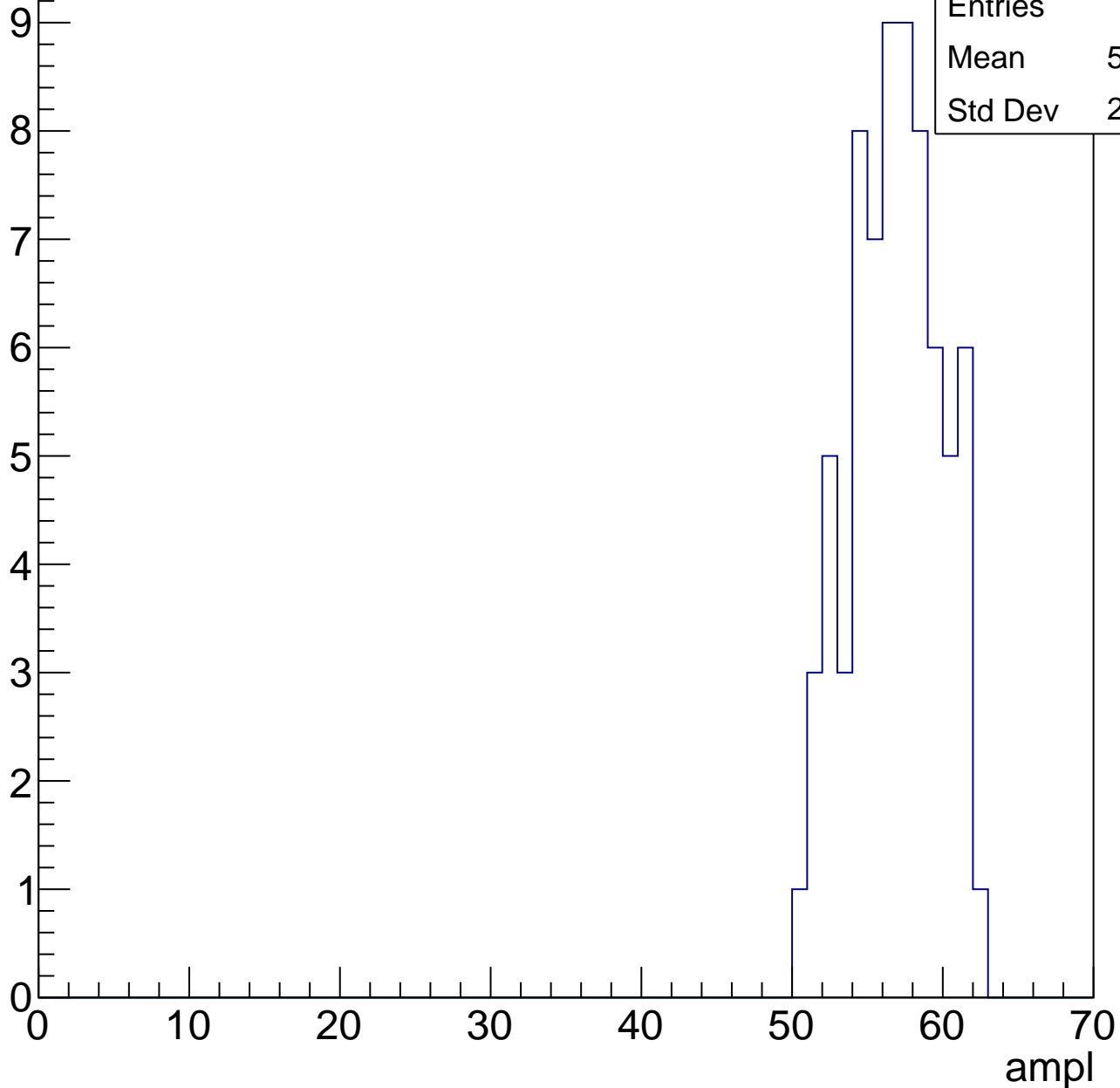
Entries	80
Mean	48.85
Std Dev	3.237



B0L002S, U5-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



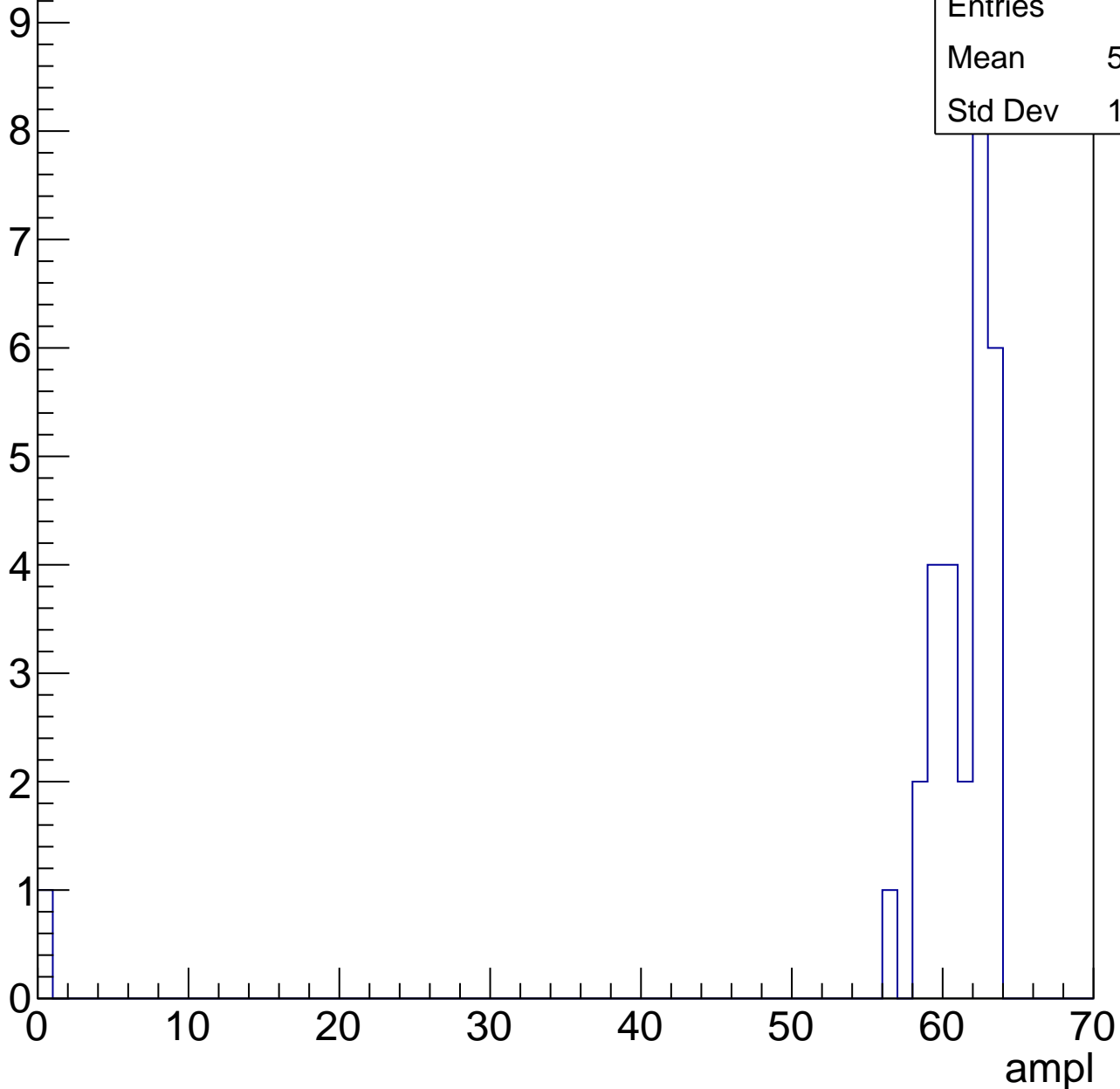
Entries	71
Mean	56.37
Std Dev	2.927

B0L002S, U5-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

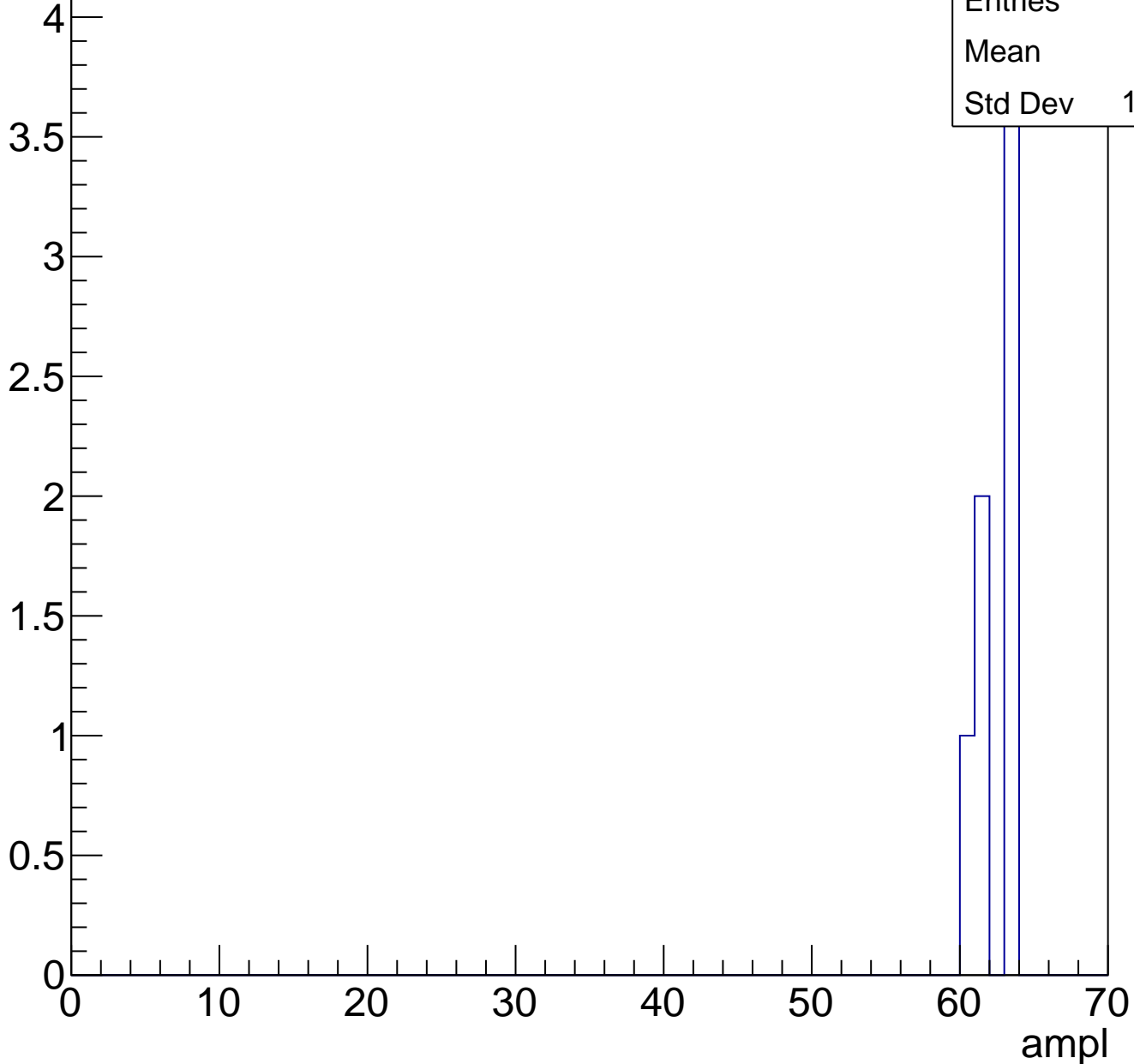
Entries	29
Mean	58.83
Std Dev	11.27



B0L002S, U5-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch40, adc0

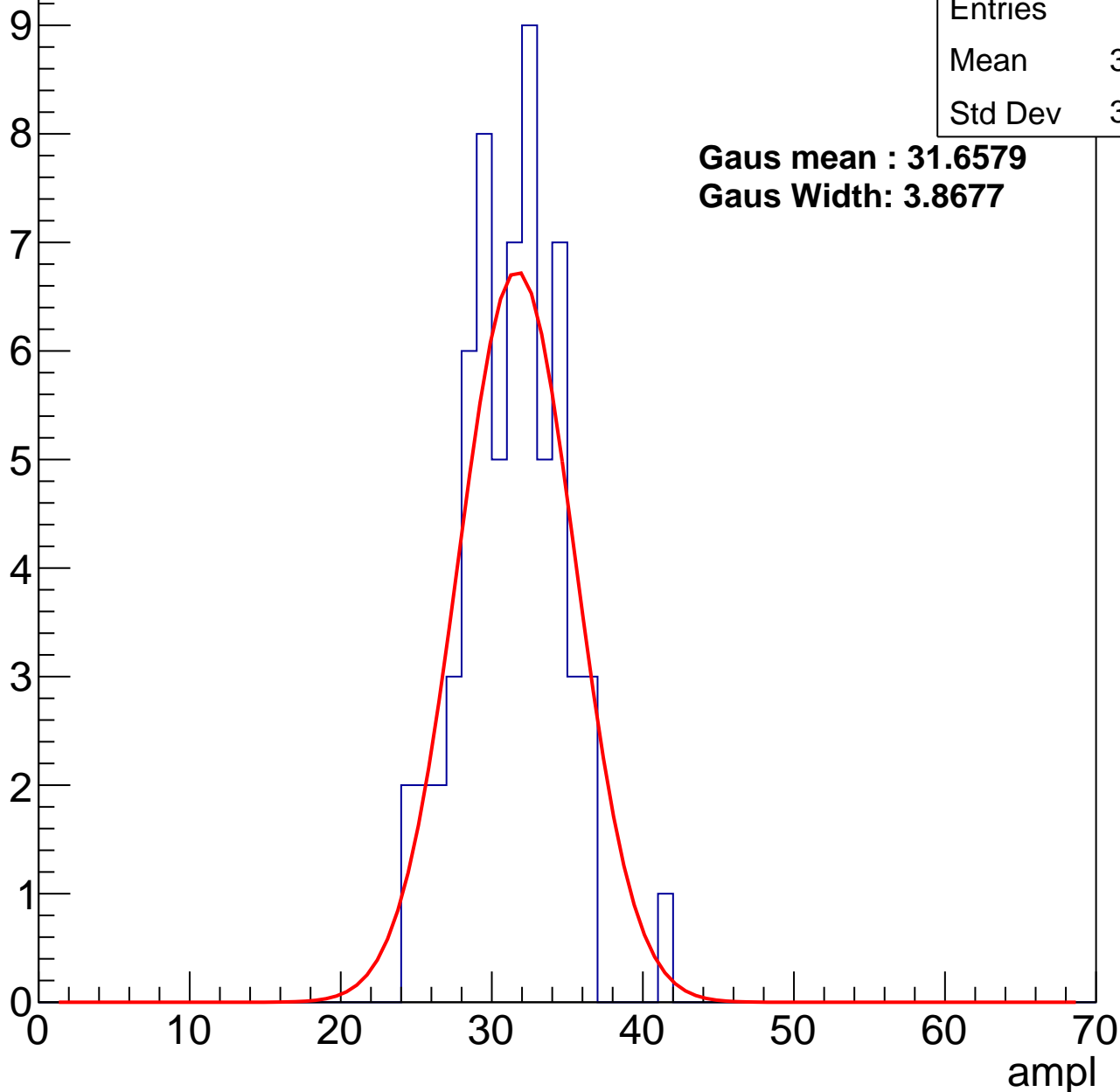
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.84
Std Dev	3.267

Gaus mean : 31.6579

Gaus Width: 3.8677



B0L002S, U5-ch40, adc1

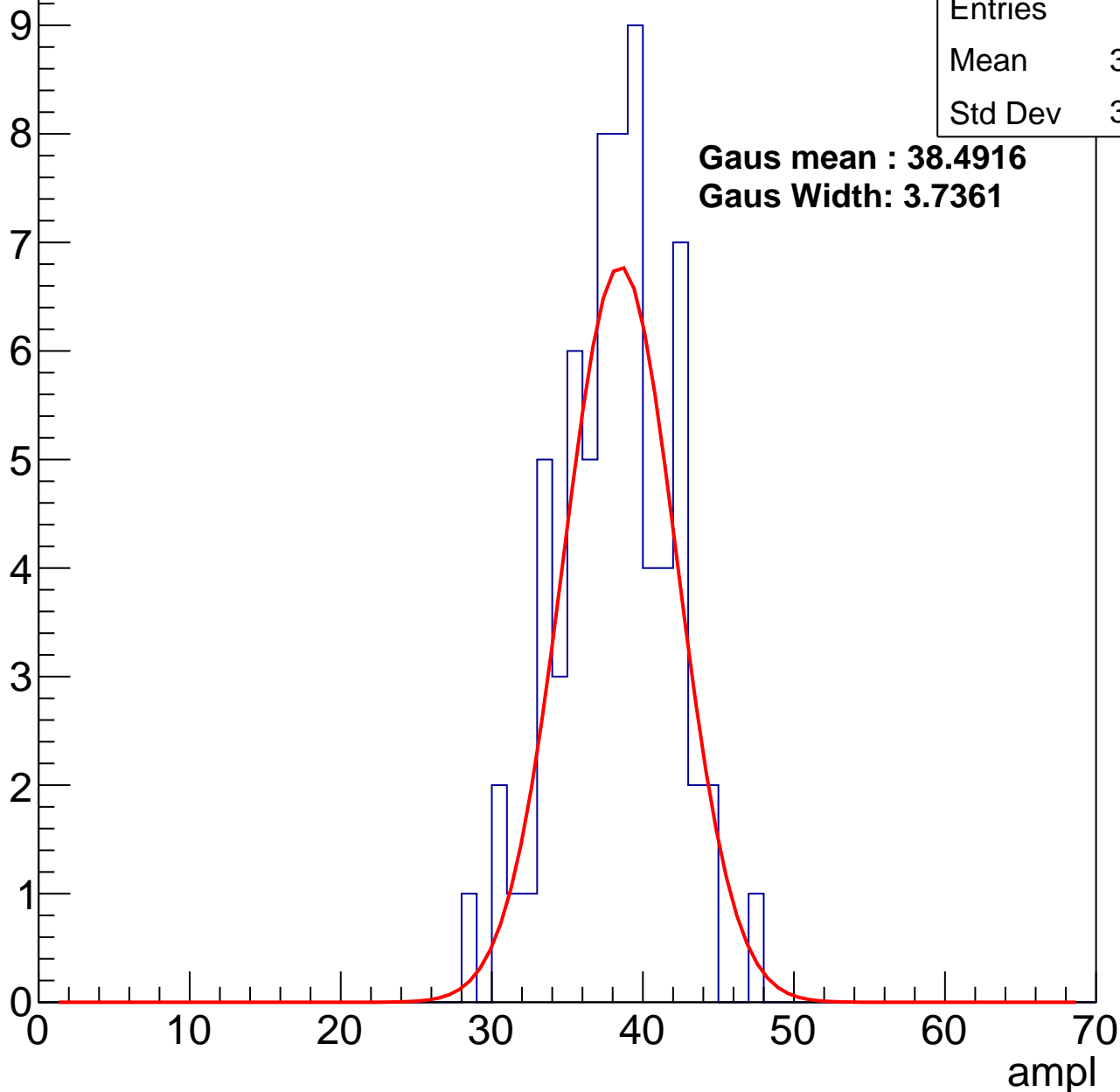
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.65
Std Dev	3.682

Gaus mean : 38.4916

Gaus Width: 3.7361



B0L002S, U5-ch40, adc2

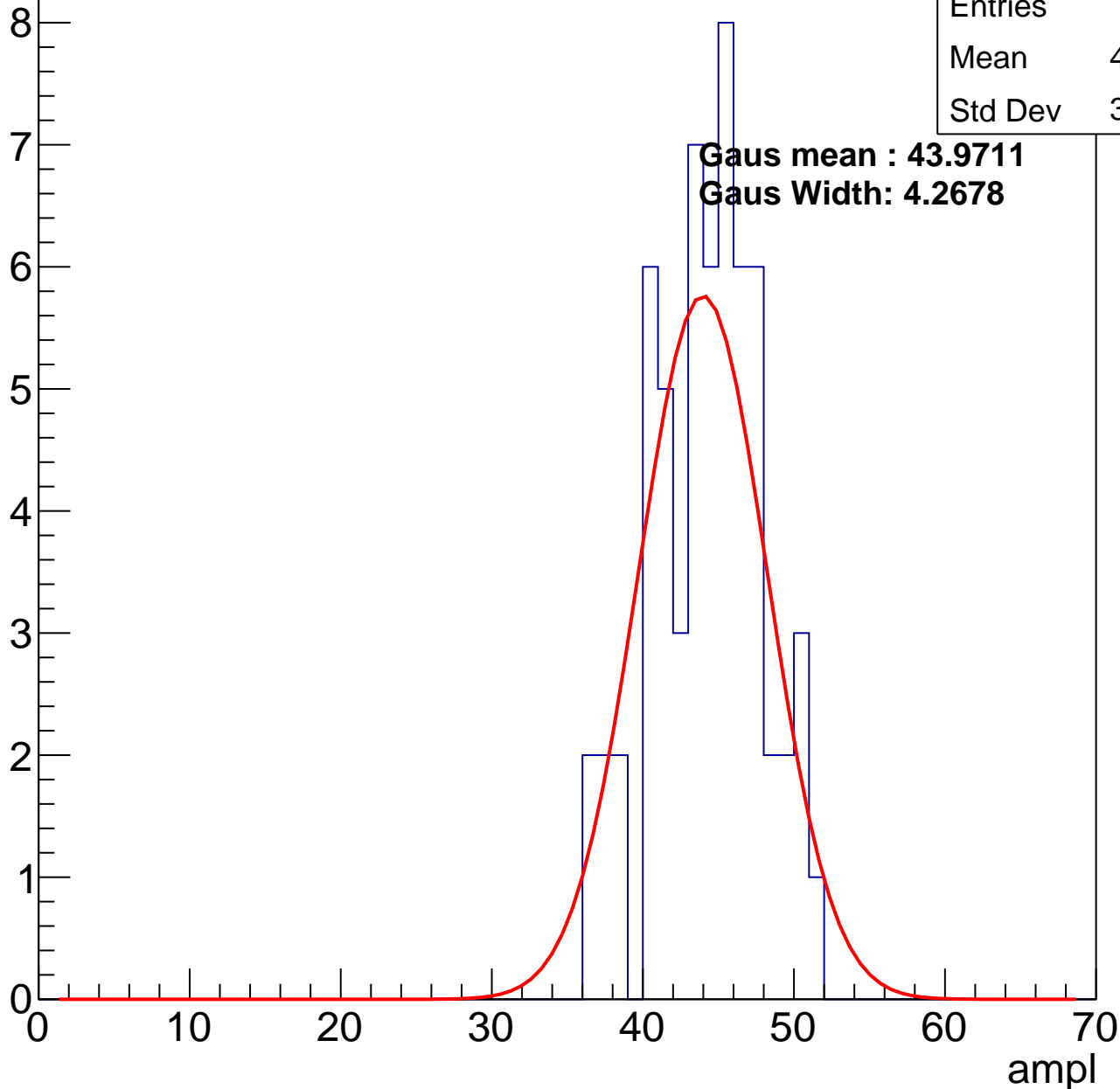
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.79
Std Dev	3.576

Gaus mean : 43.9711

Gaus Width: 4.2678

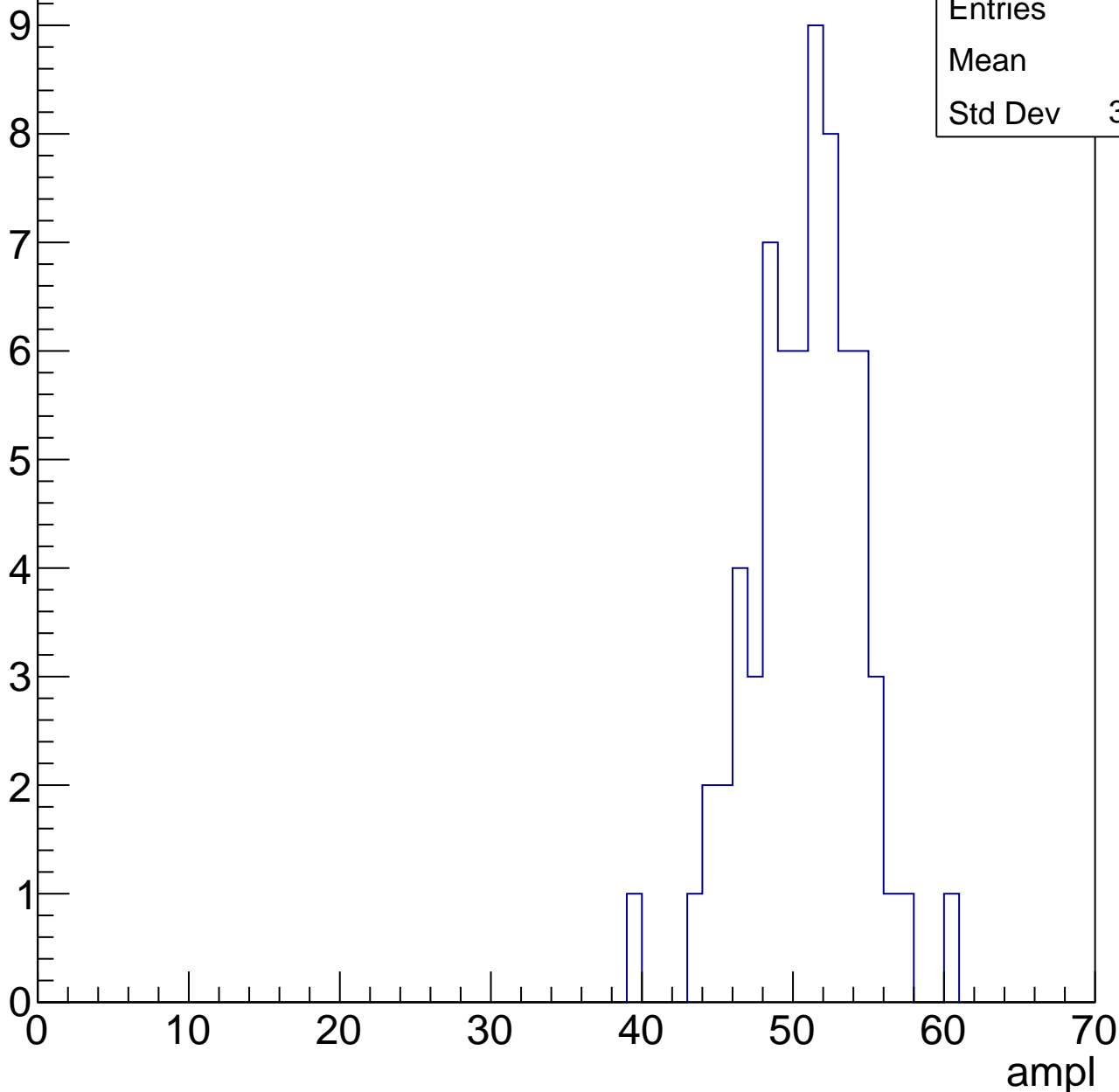


B0L002S, U5-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

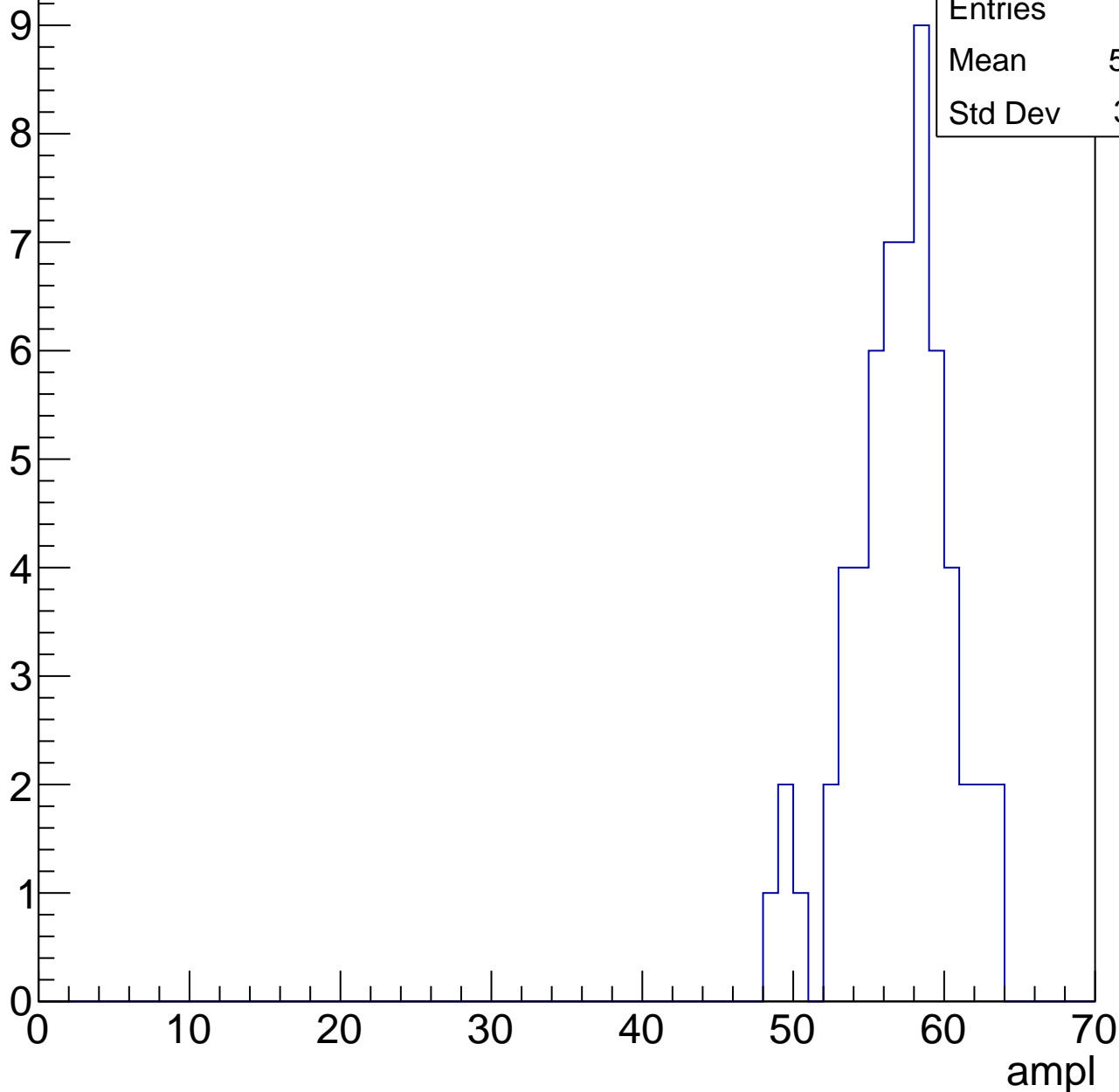
Entries	67
Mean	50.3
Std Dev	3.587



B0L002S, U5-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch40, adc5

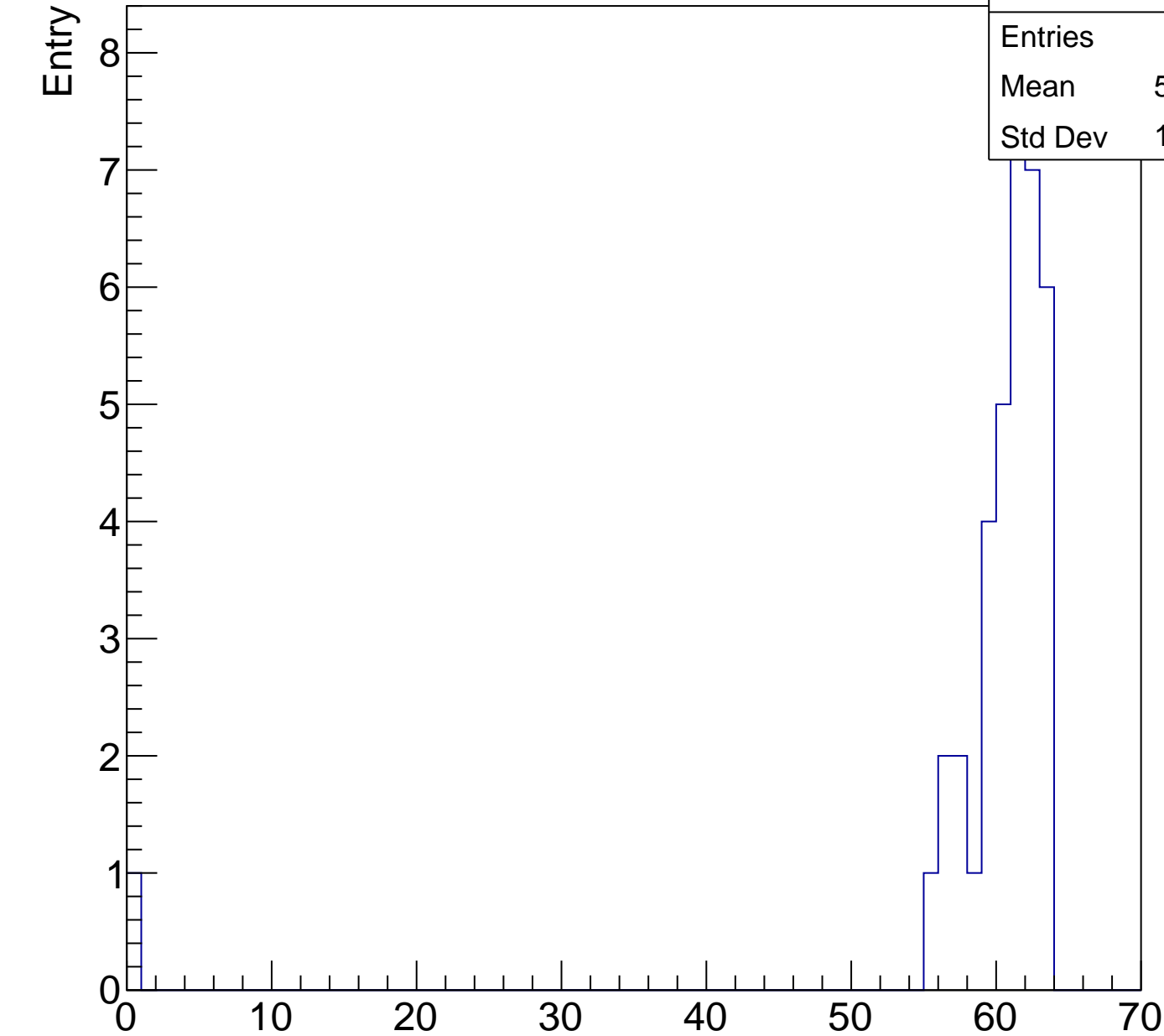
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	58.78
Std Dev	10.02

ampl



B0L002S, U5-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

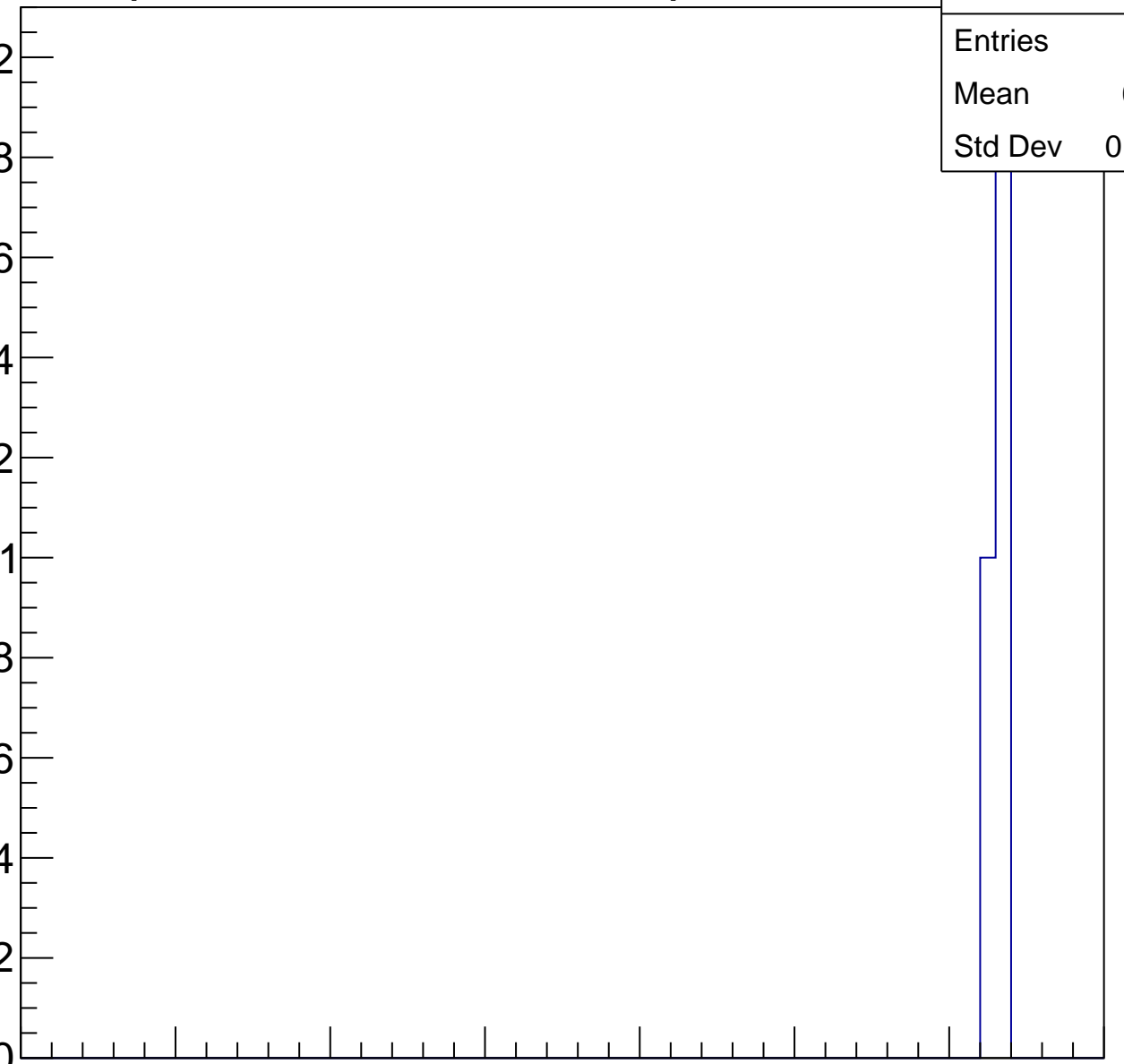
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch41, adc0

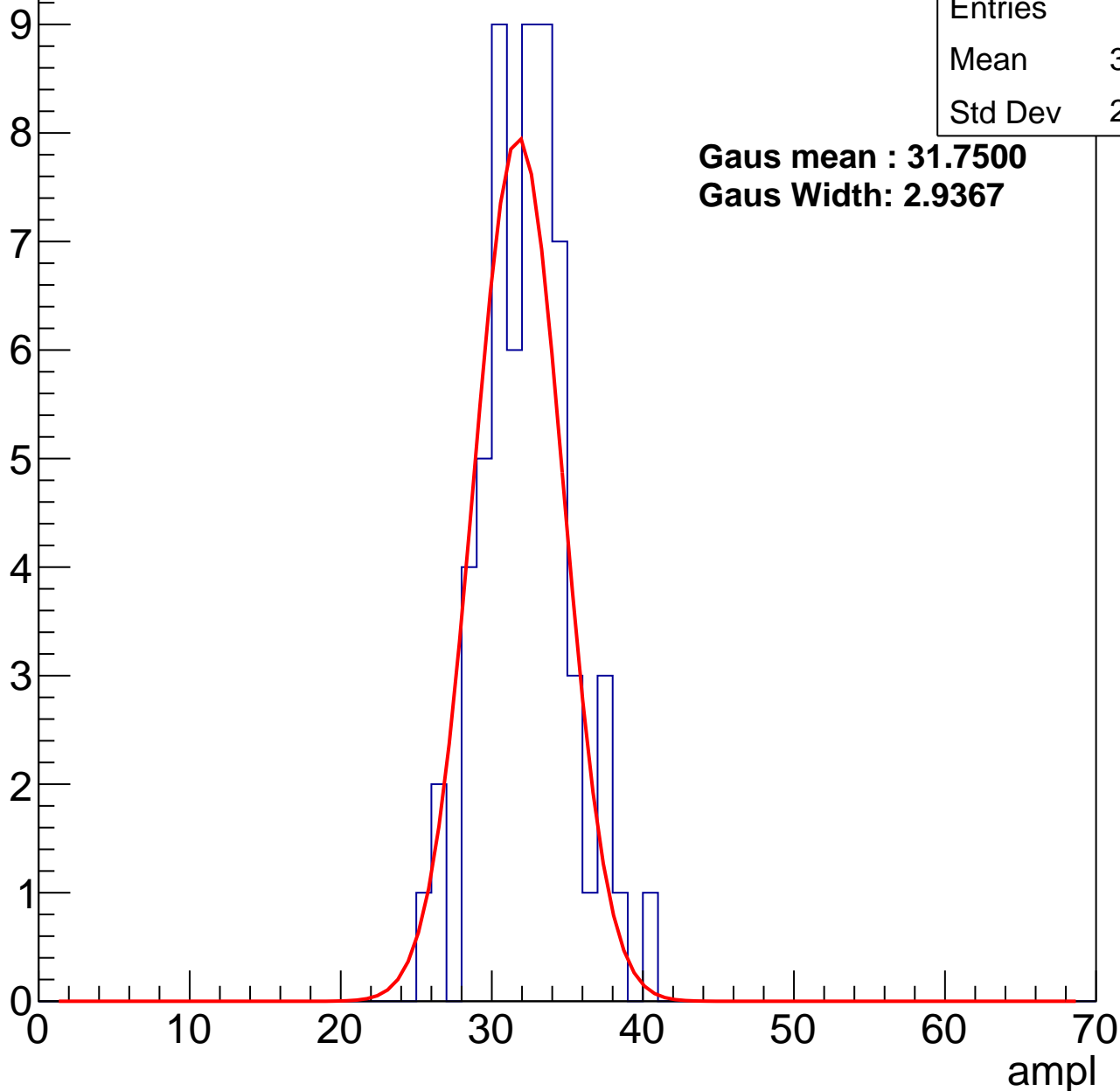
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	31.85
Std Dev	2.936

Gaus mean : 31.7500

Gaus Width: 2.9367



B0L002S, U5-ch41, adc1

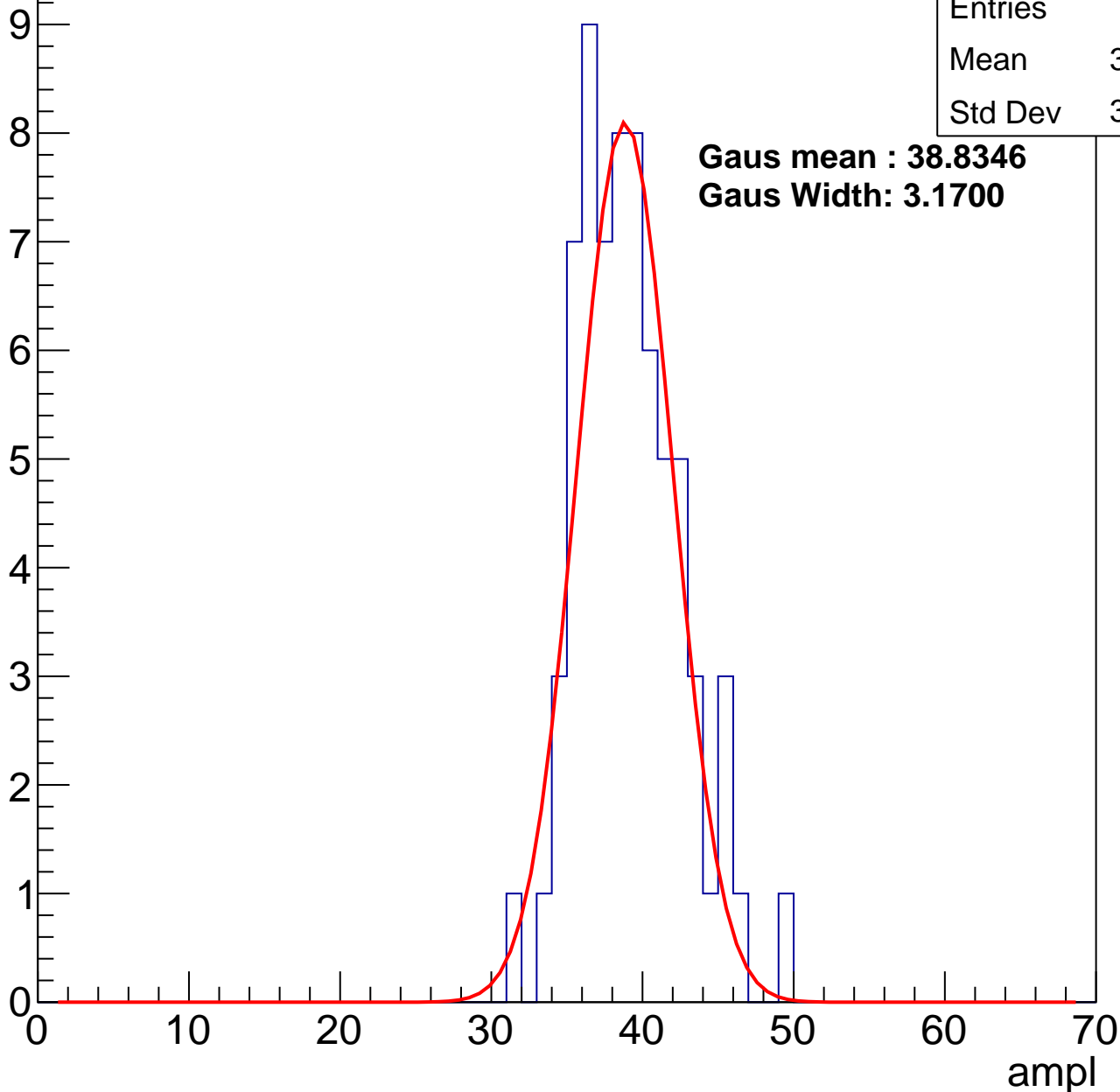
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	38.67
Std Dev	3.399

Gaus mean : 38.8346

Gaus Width: 3.1700



B0L002S, U5-ch41, adc2

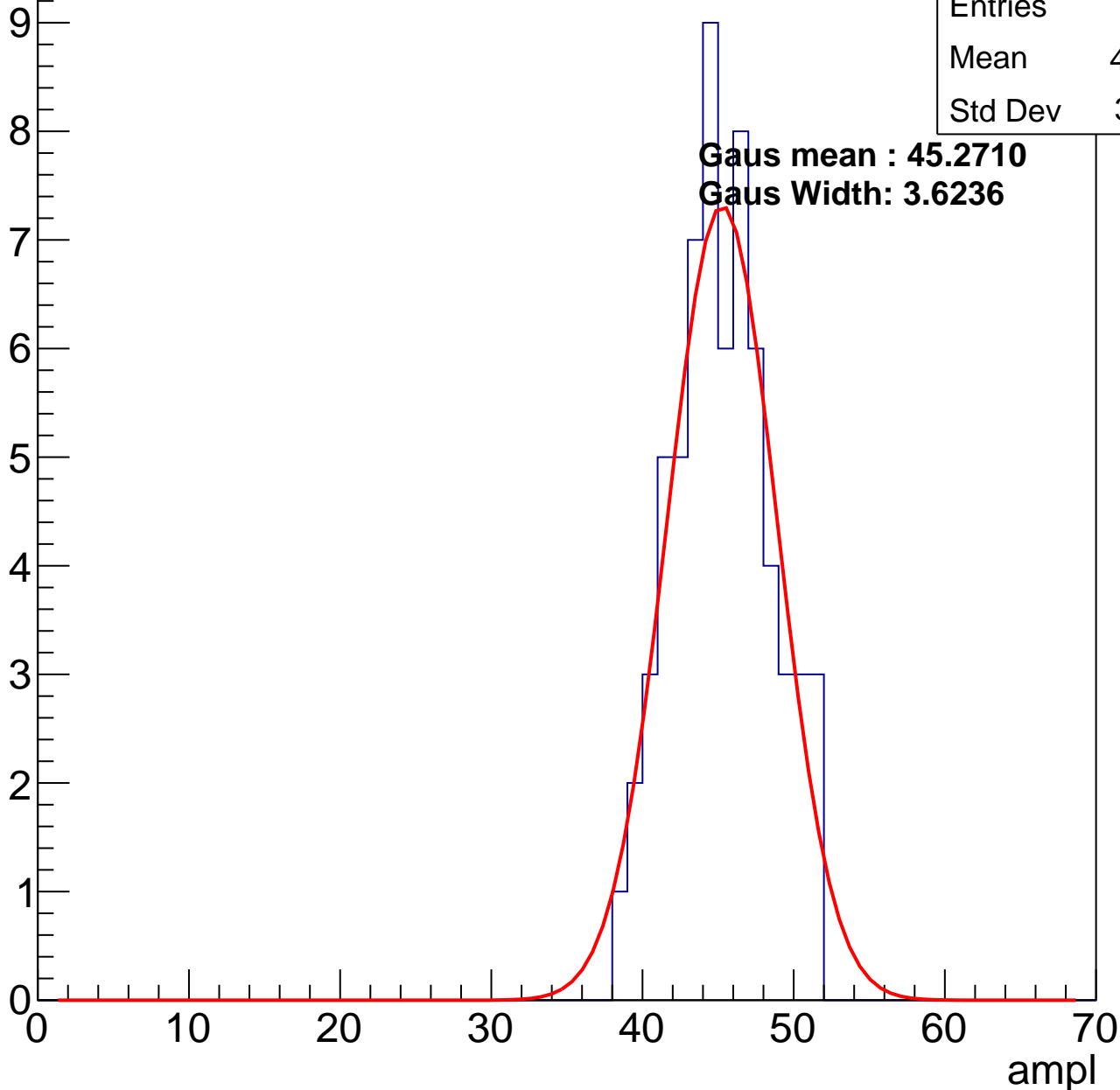
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.77
Std Dev	3.171

Gaus mean : 45.2710

Gaus Width: 3.6236

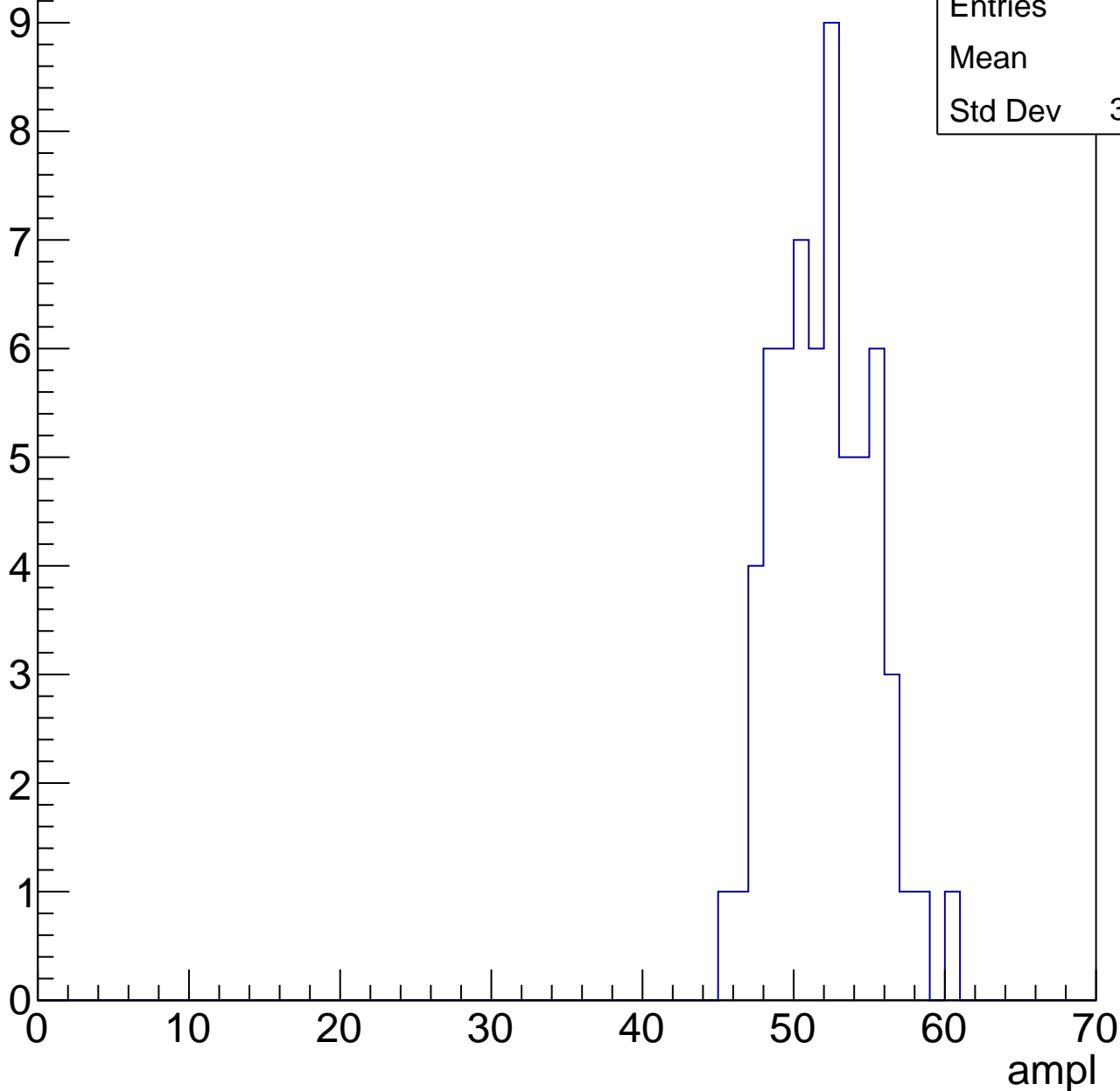


B0L002S, U5-ch41, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

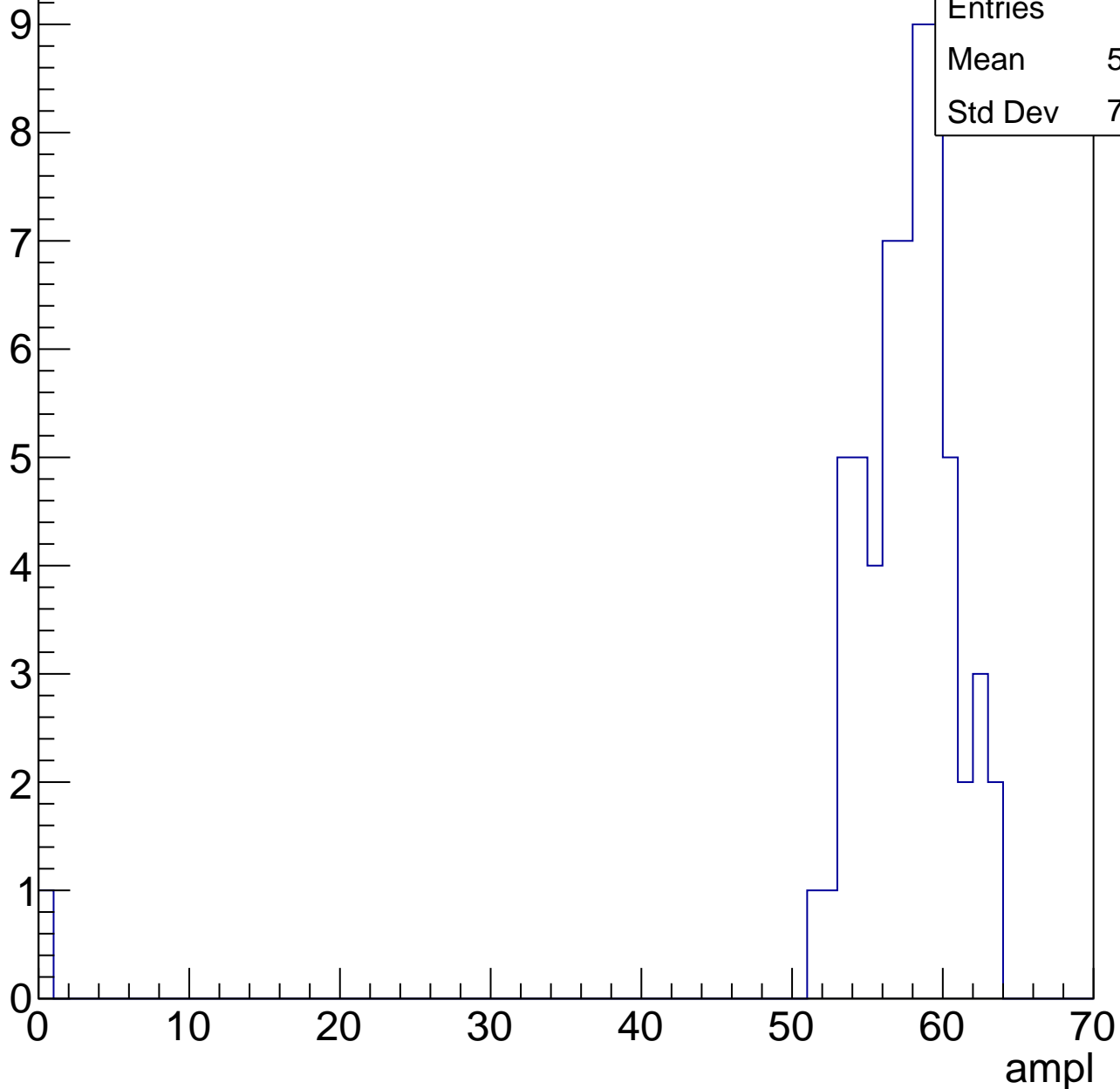
Entries	62
Mean	51.5
Std Dev	3.125



B0L002S, U5-ch41, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

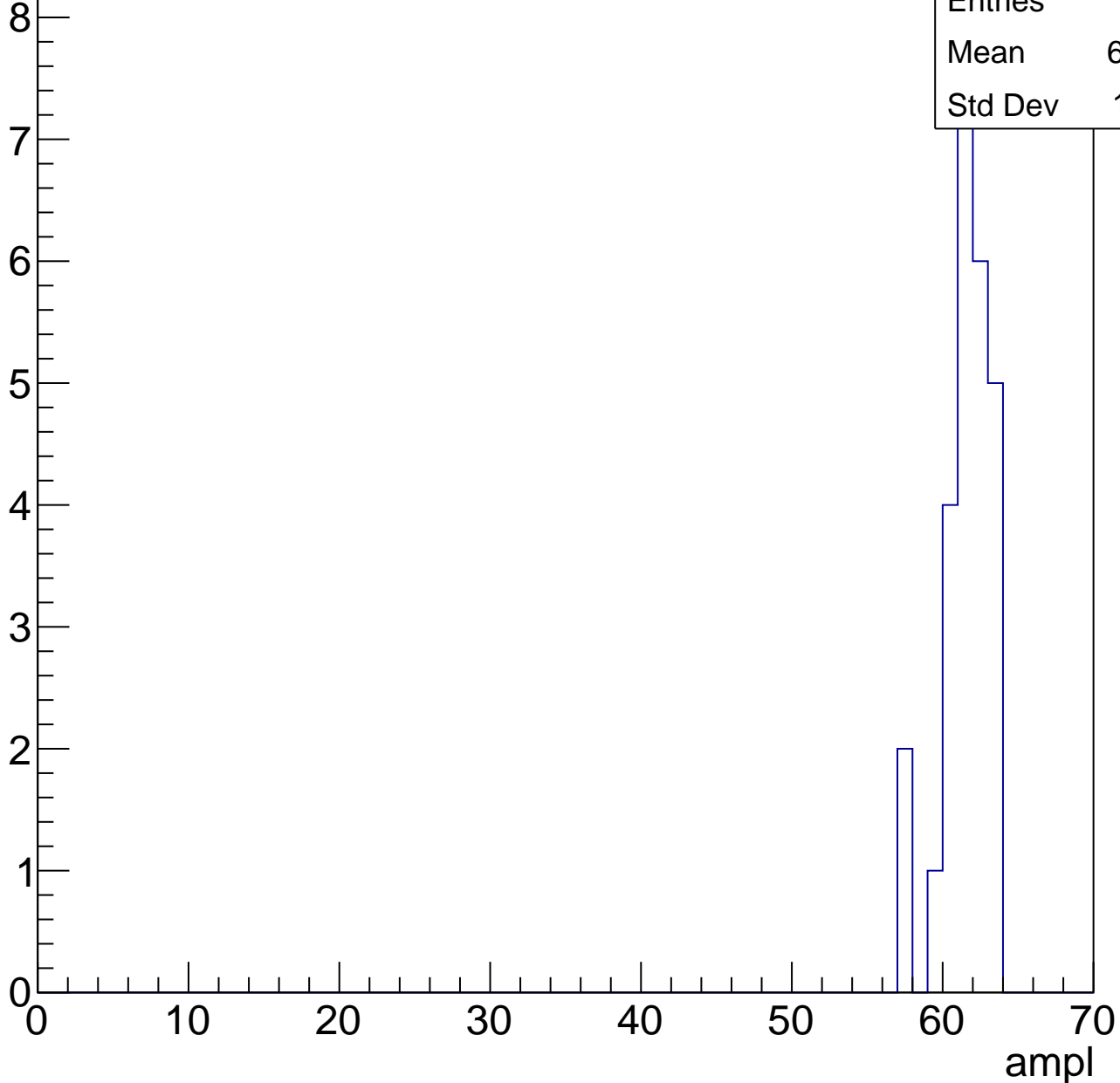


B0L002S, U5-ch41, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	61.08
Std Dev	1.591



B0L002S, U5-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch42, adc0

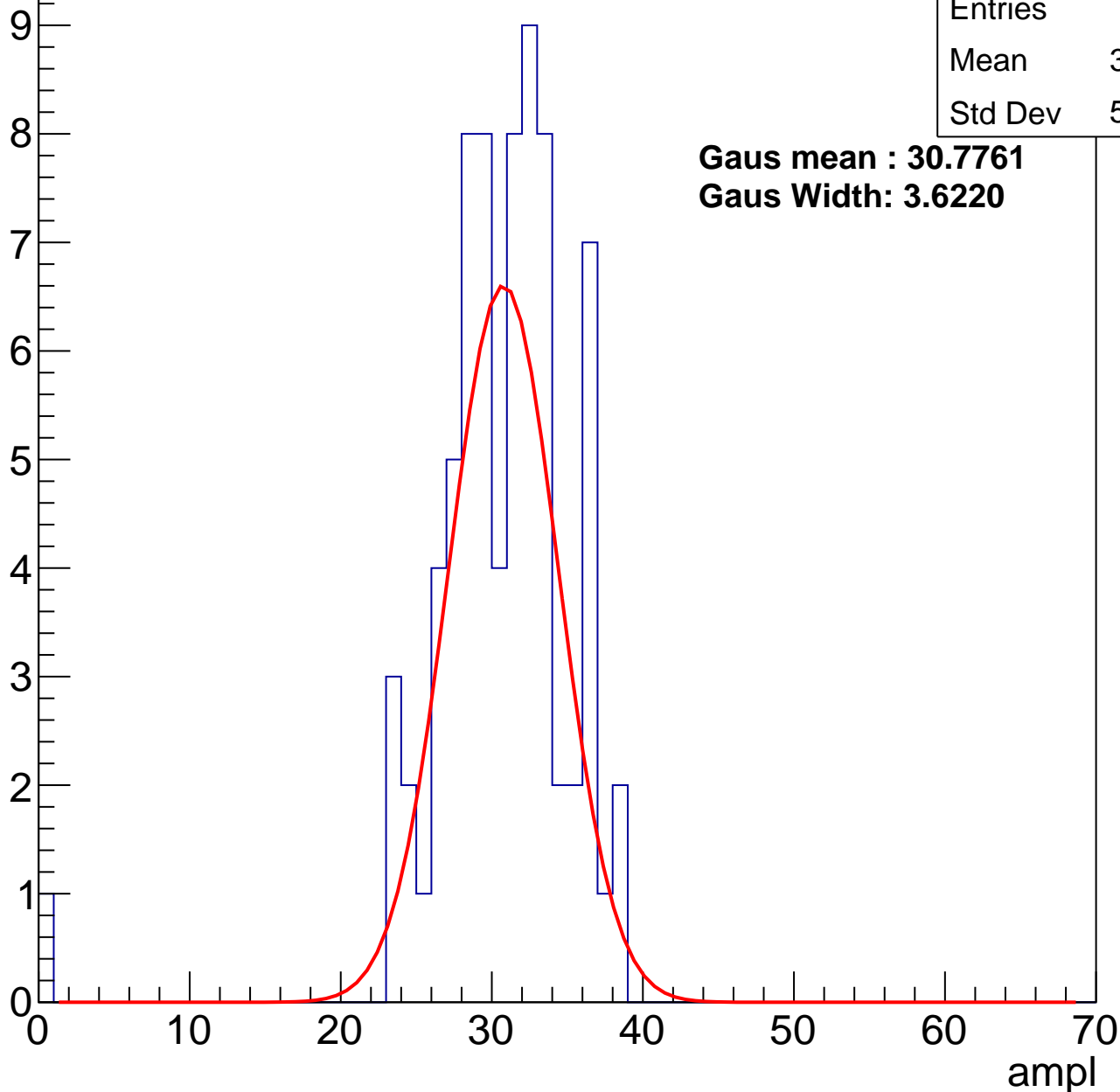
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.13
Std Dev	5.058

Gaus mean : 30.7761

Gaus Width: 3.6220



B0L002S, U5-ch42, adc1

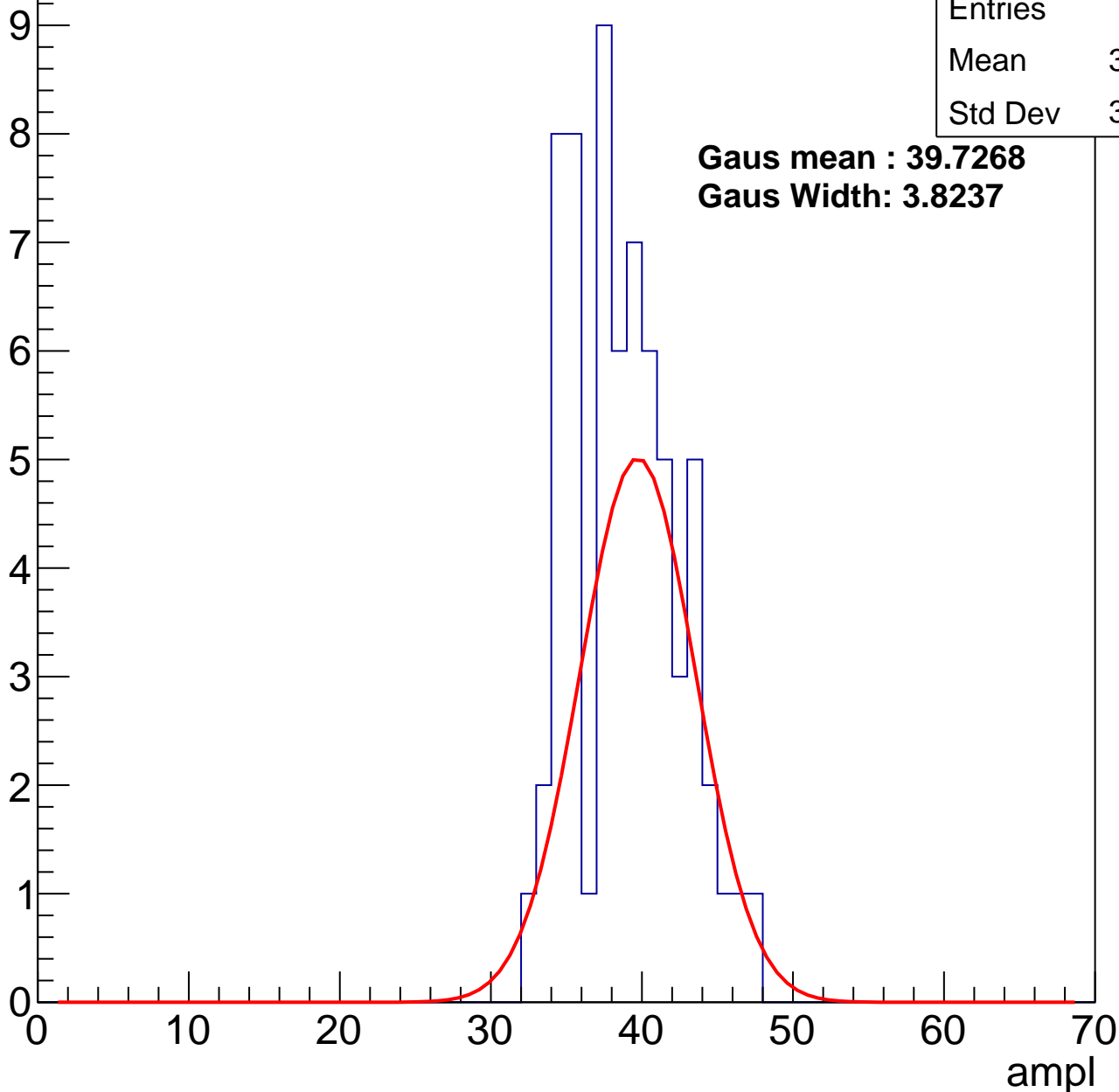
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.36
Std Dev	3.493

Gaus mean : 39.7268

Gaus Width: 3.8237



B0L002S, U5-ch42, adc2

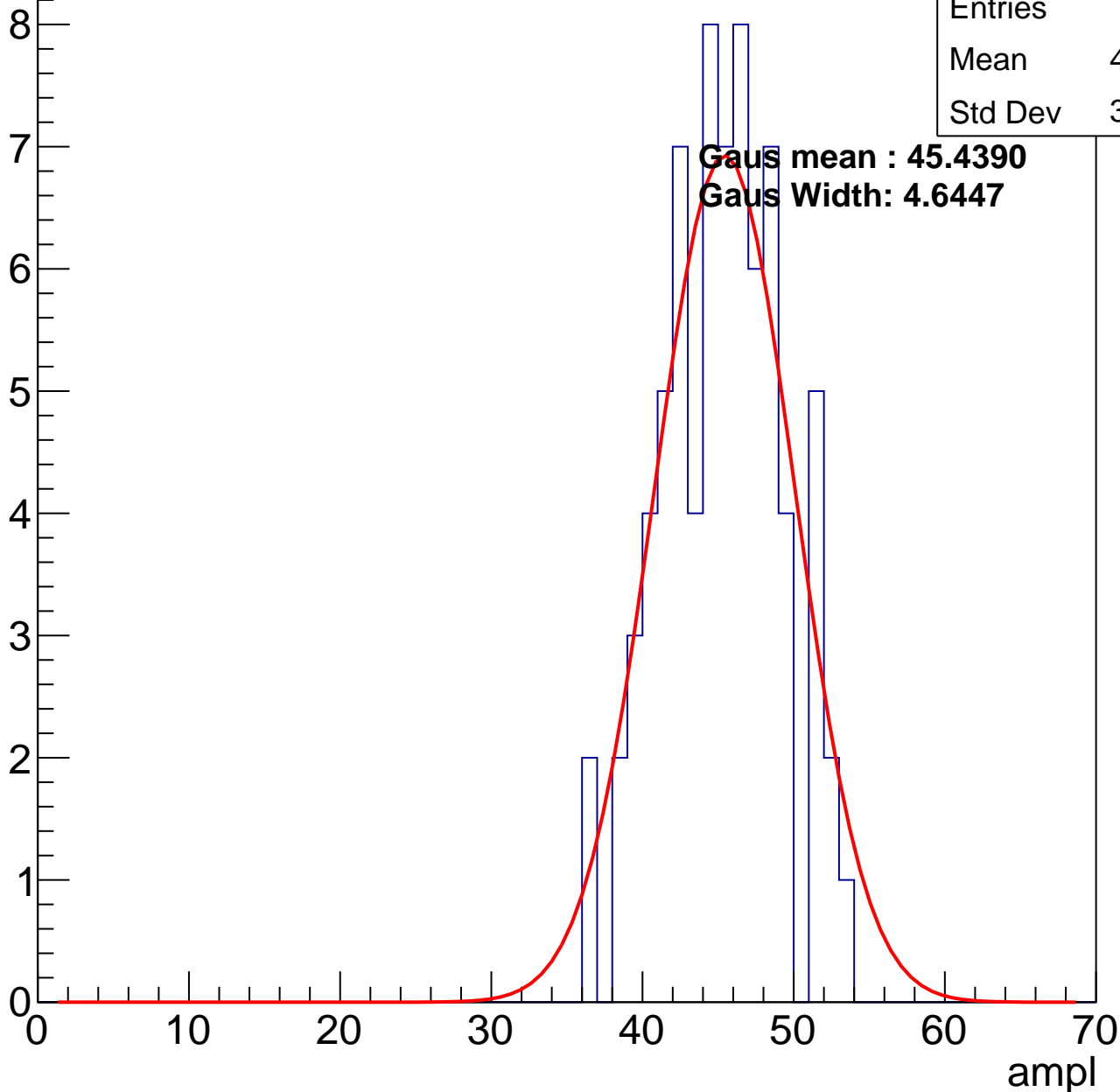
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.76
Std Dev	3.884

Gaus mean : 45.4390

Gaus Width: 4.6447

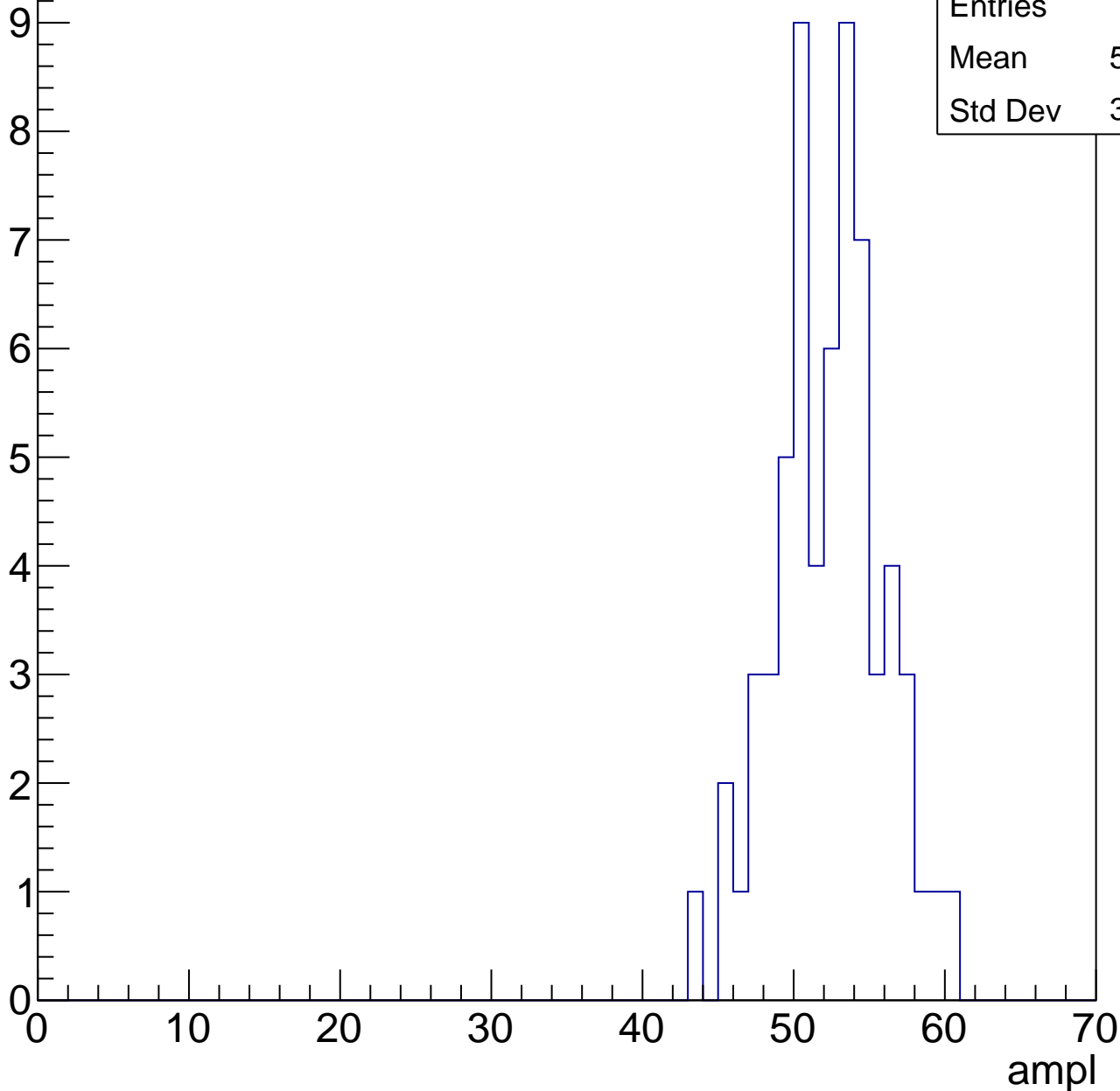


B0L002S, U5-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	51.86
Std Dev	3.504

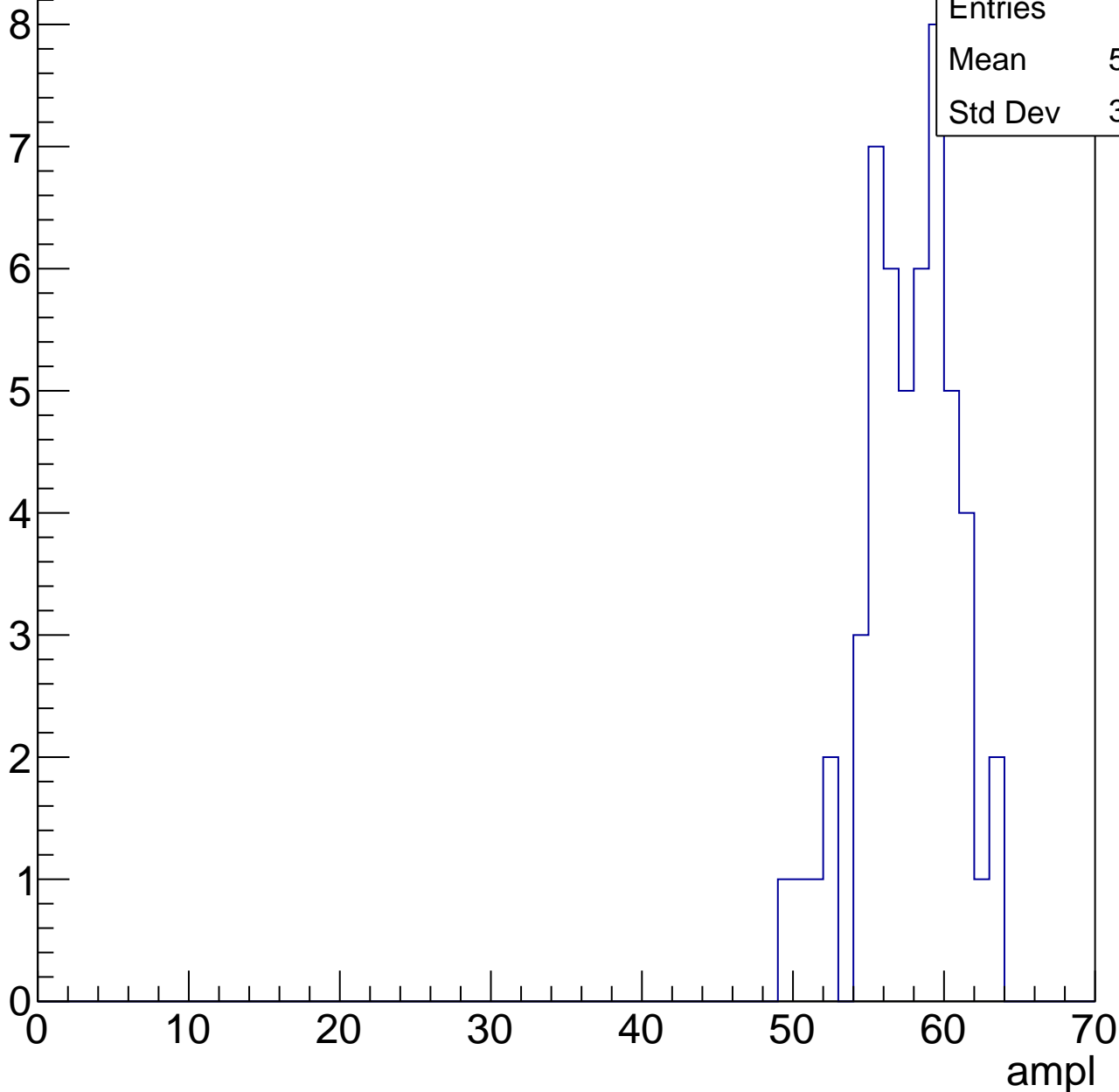


B0L002S, U5-ch42, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

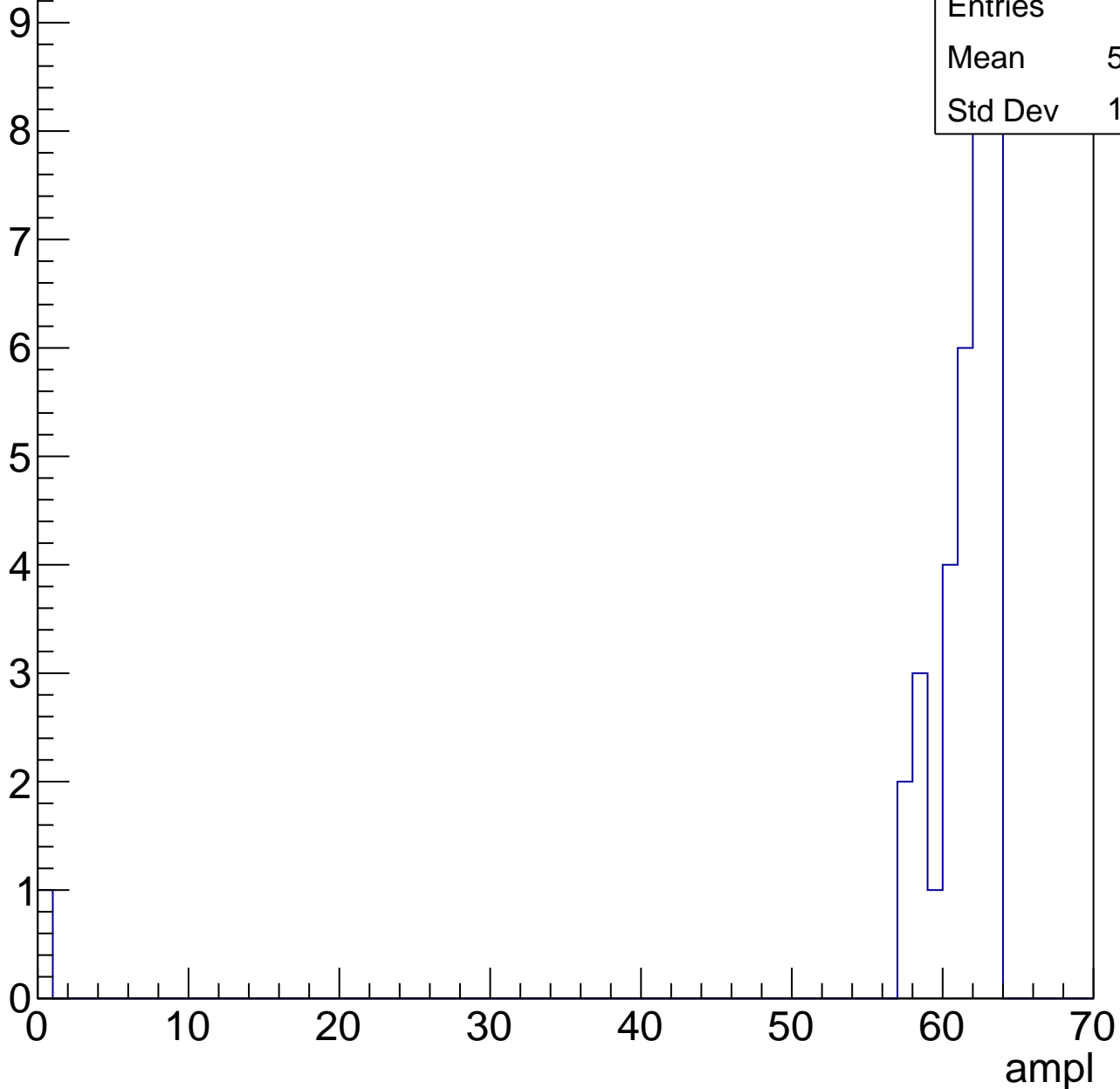
Entries	52
Mean	57.19
Std Dev	3.107



B0L002S, U5-ch42, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch43, adc0

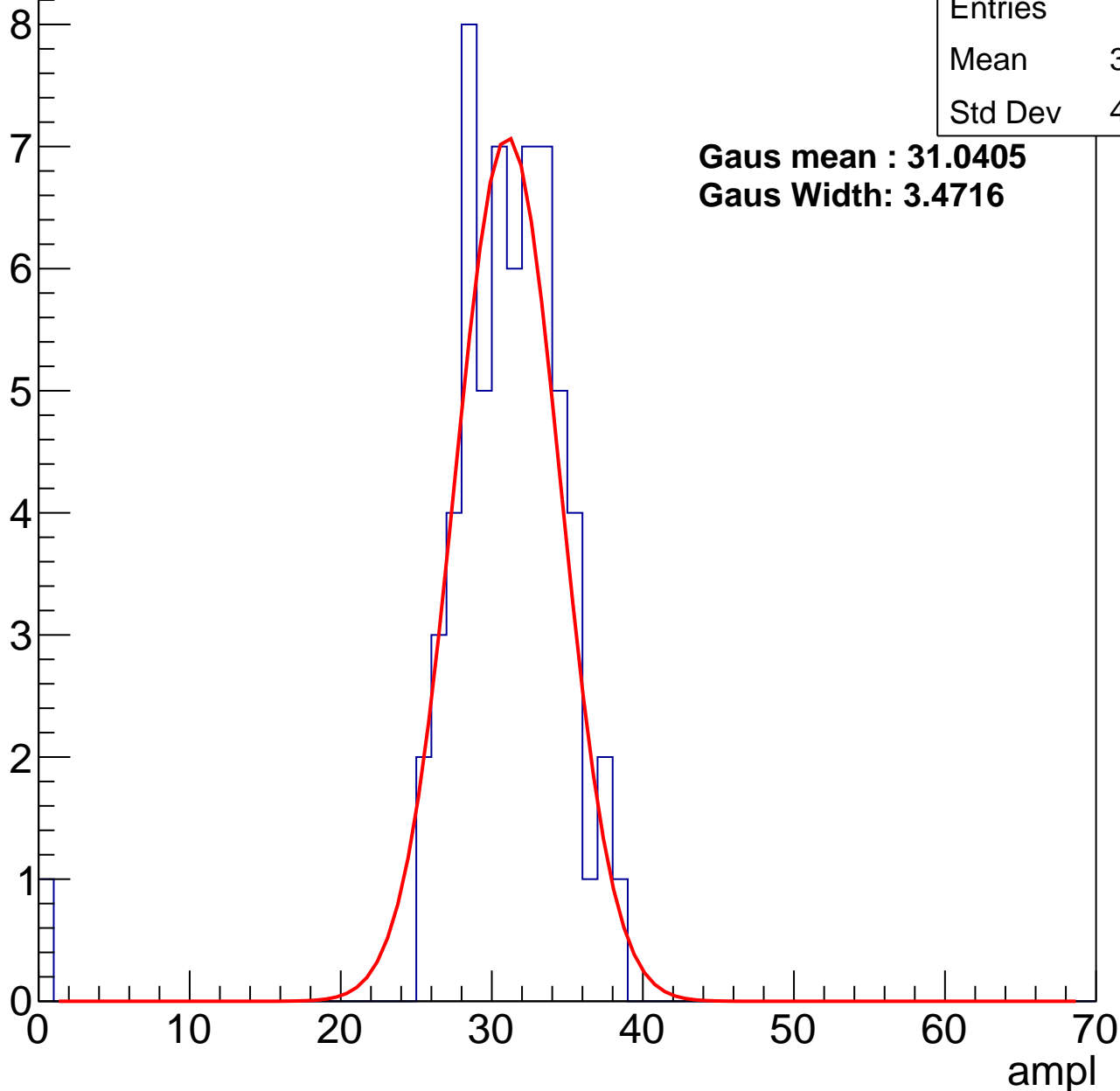
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.38
Std Dev	4.939

Gaus mean : 31.0405

Gaus Width: 3.4716



B0L002S, U5-ch43, adc1

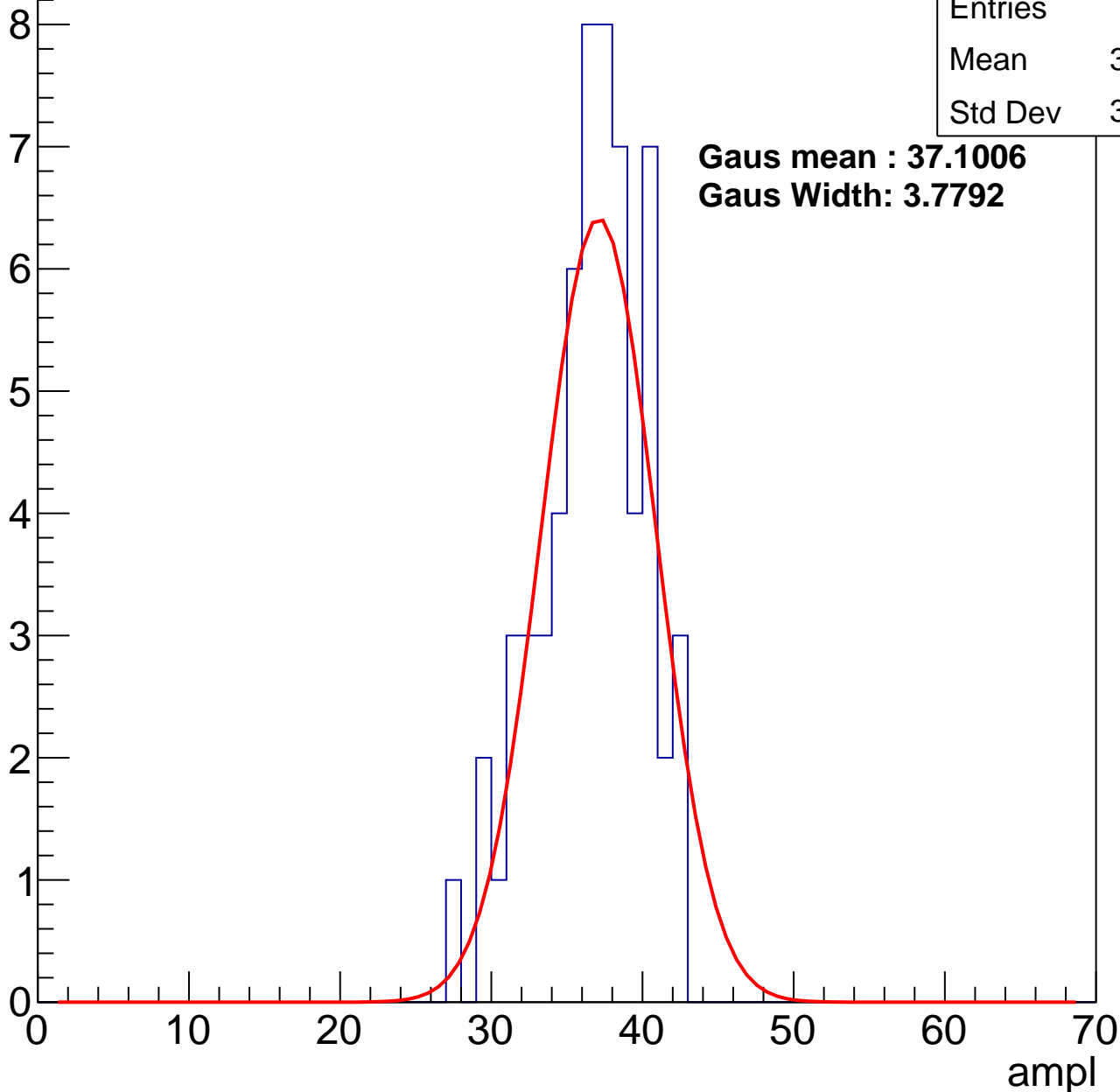
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.18
Std Dev	3.424

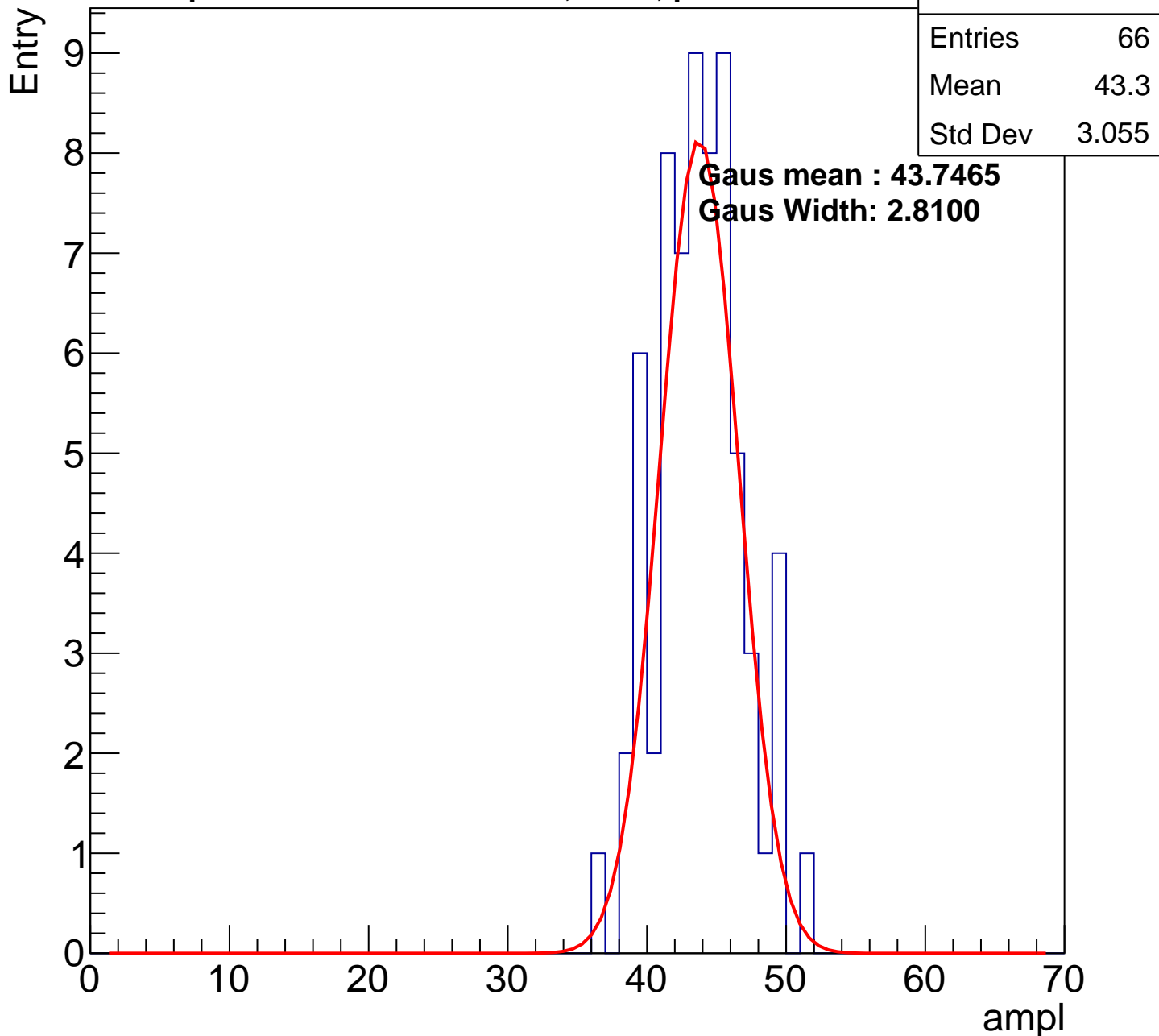
Gaus mean : 37.1006

Gaus Width: 3.7792



B0L002S, U5-ch43, adc2

calib_packv5_042523_0143.root, FC#8, port C1

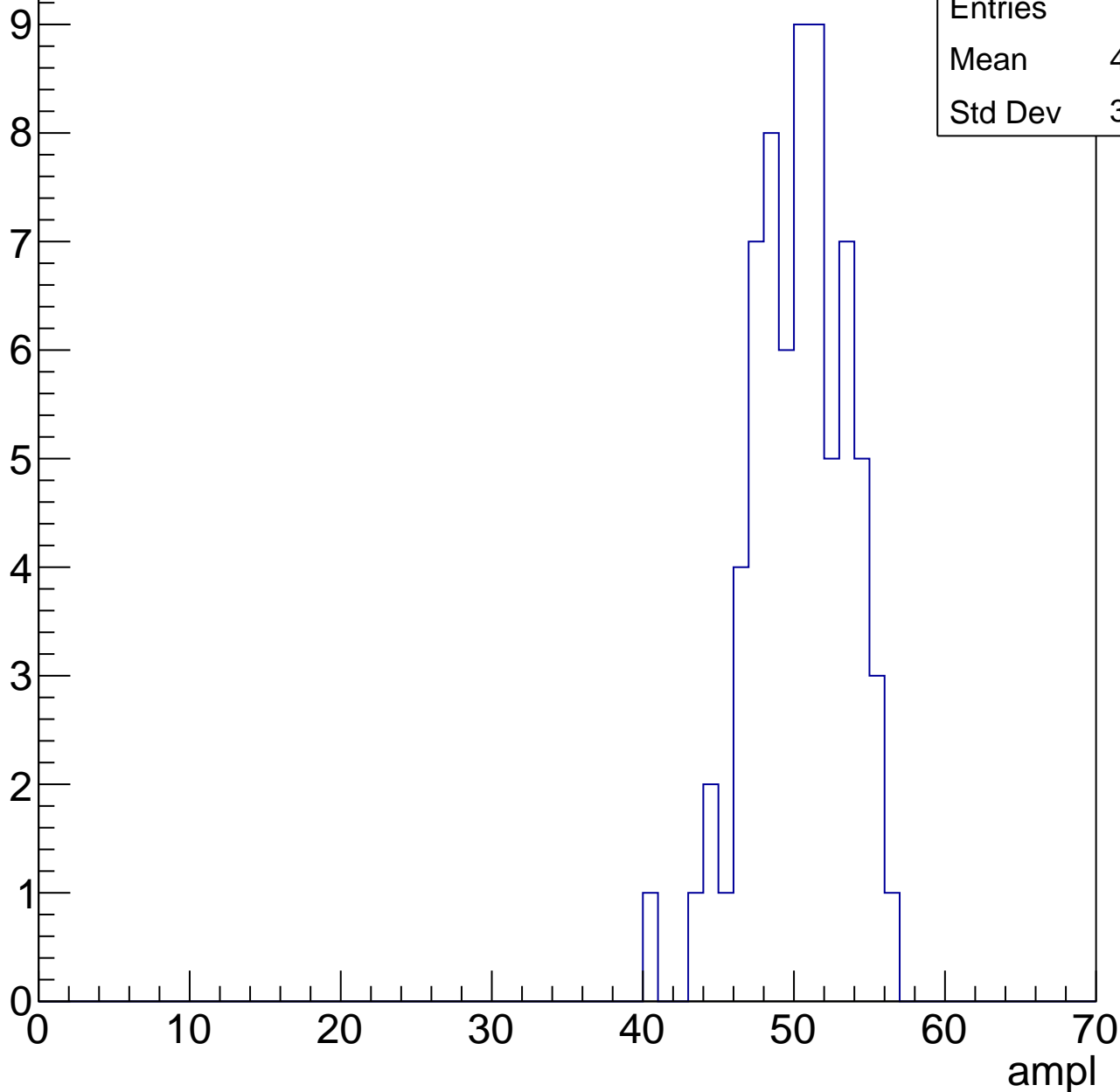


B0L002S, U5-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.83
Std Dev	3.167

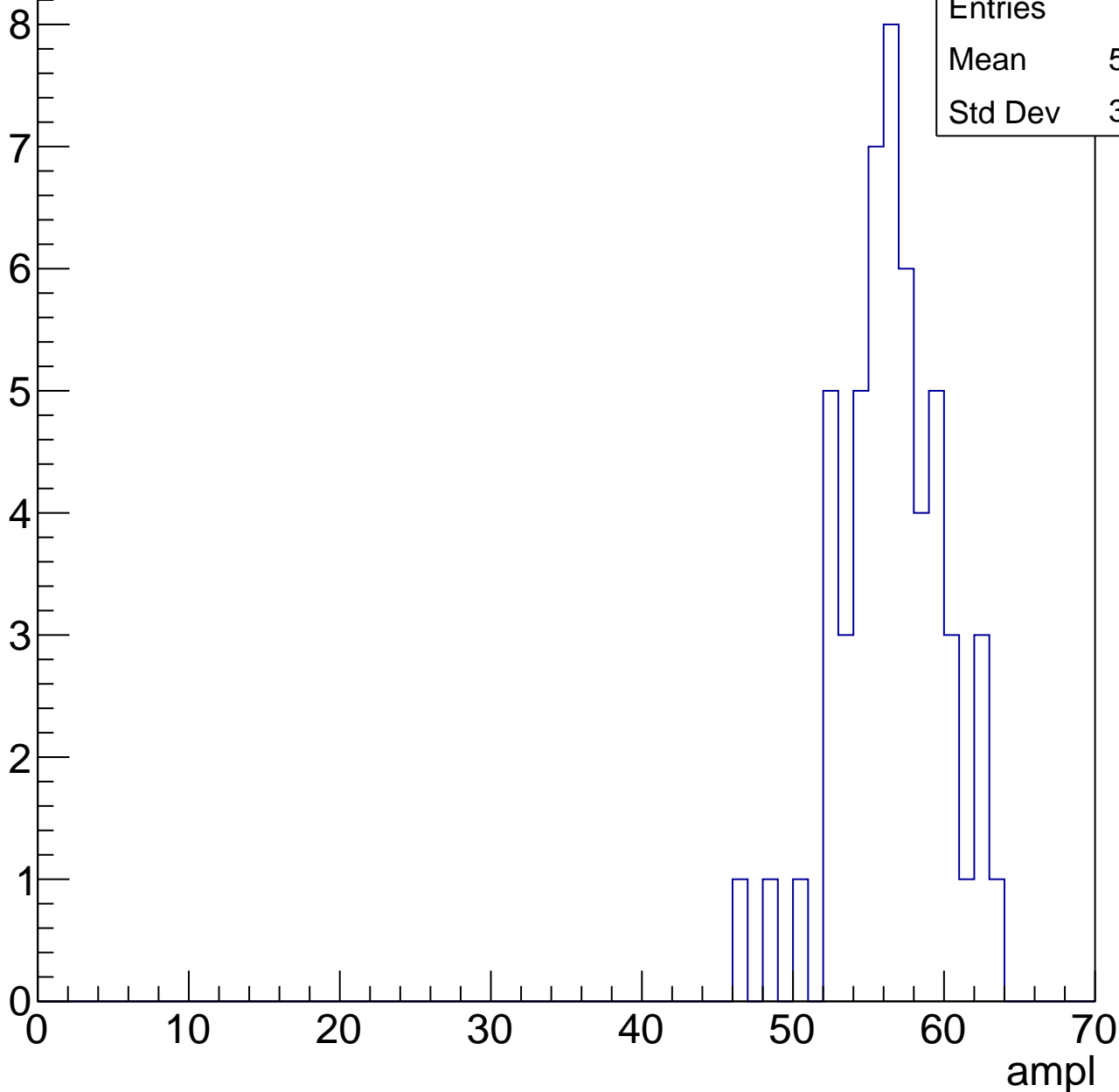


B0L002S, U5-ch43, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	56.02
Std Dev	3.418

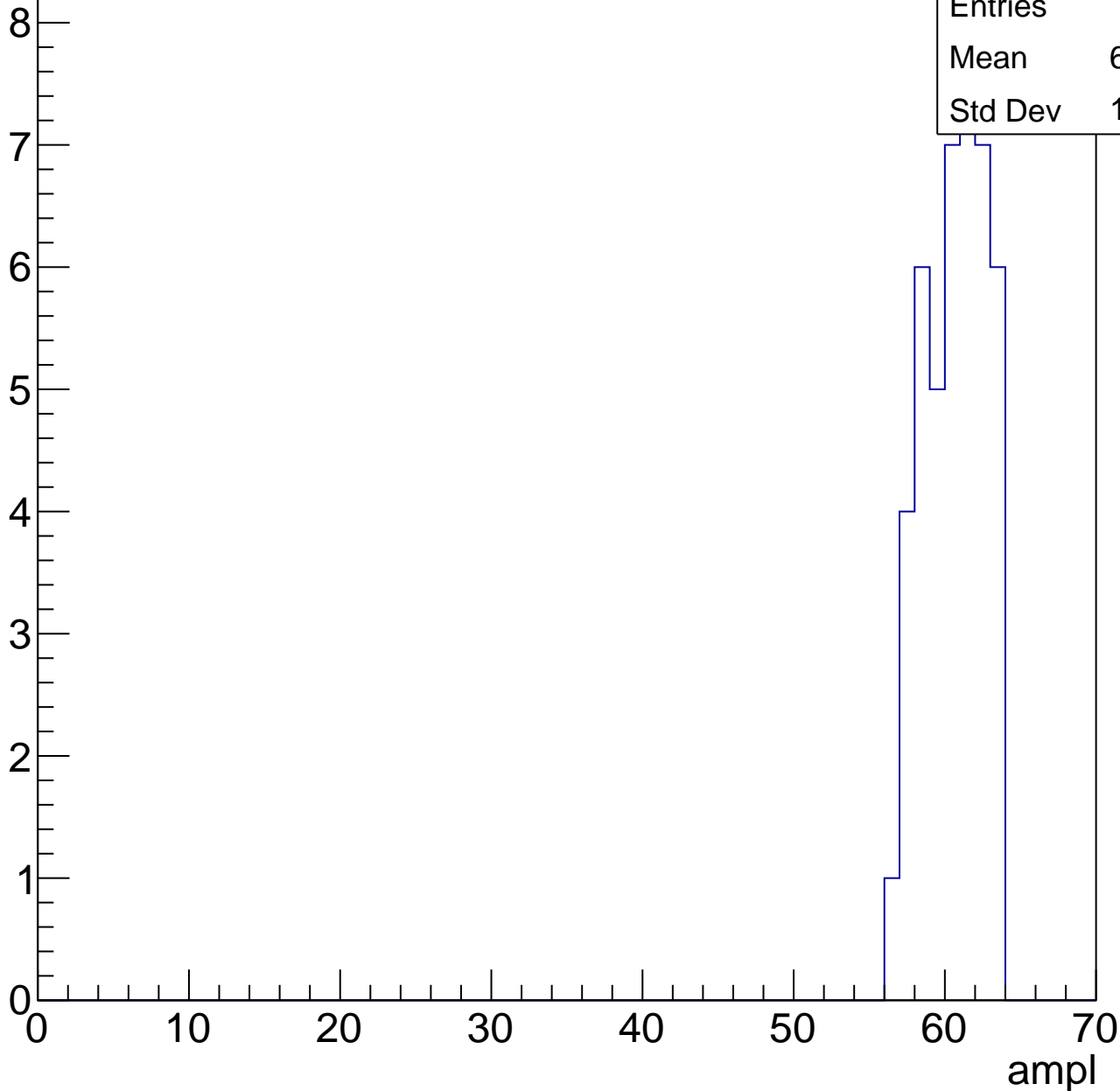


B0L002S, U5-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

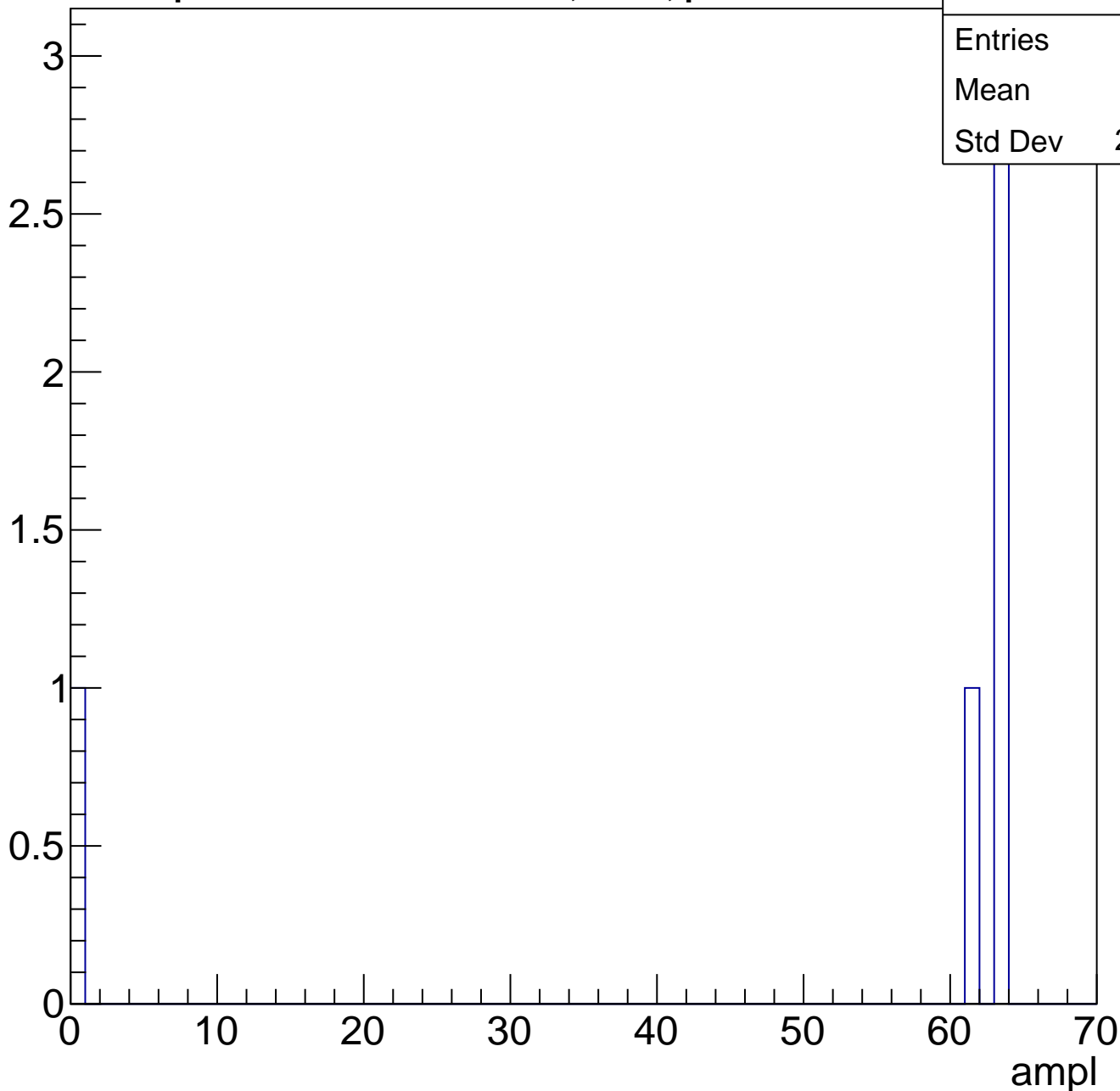
Entries	44
Mean	60.16
Std Dev	1.965



B0L002S, U5-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch44, adc0

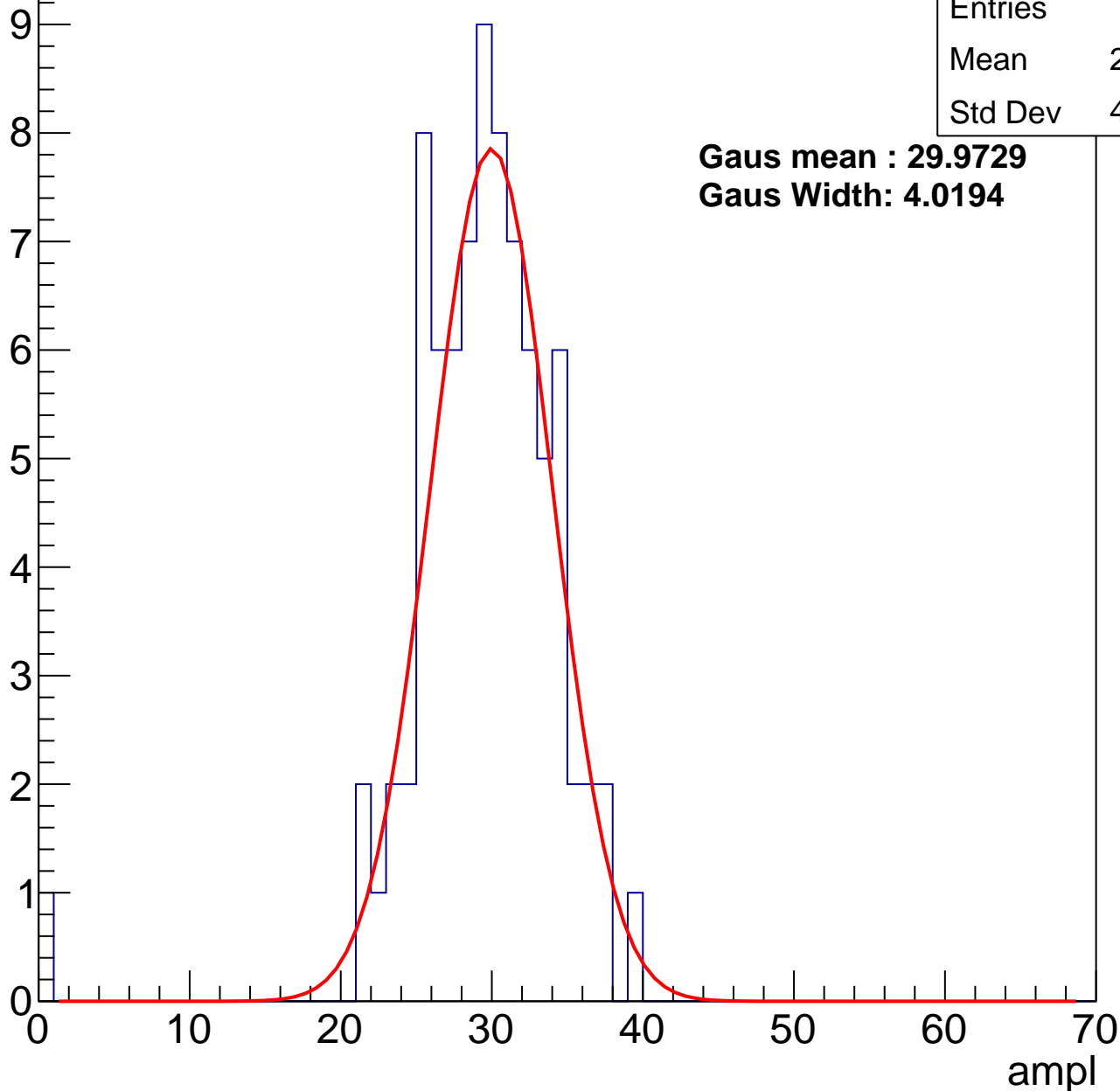
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	28.99
Std Dev	4.995

Gaus mean : 29.9729

Gaus Width: 4.0194



B0L002S, U5-ch44, adc1

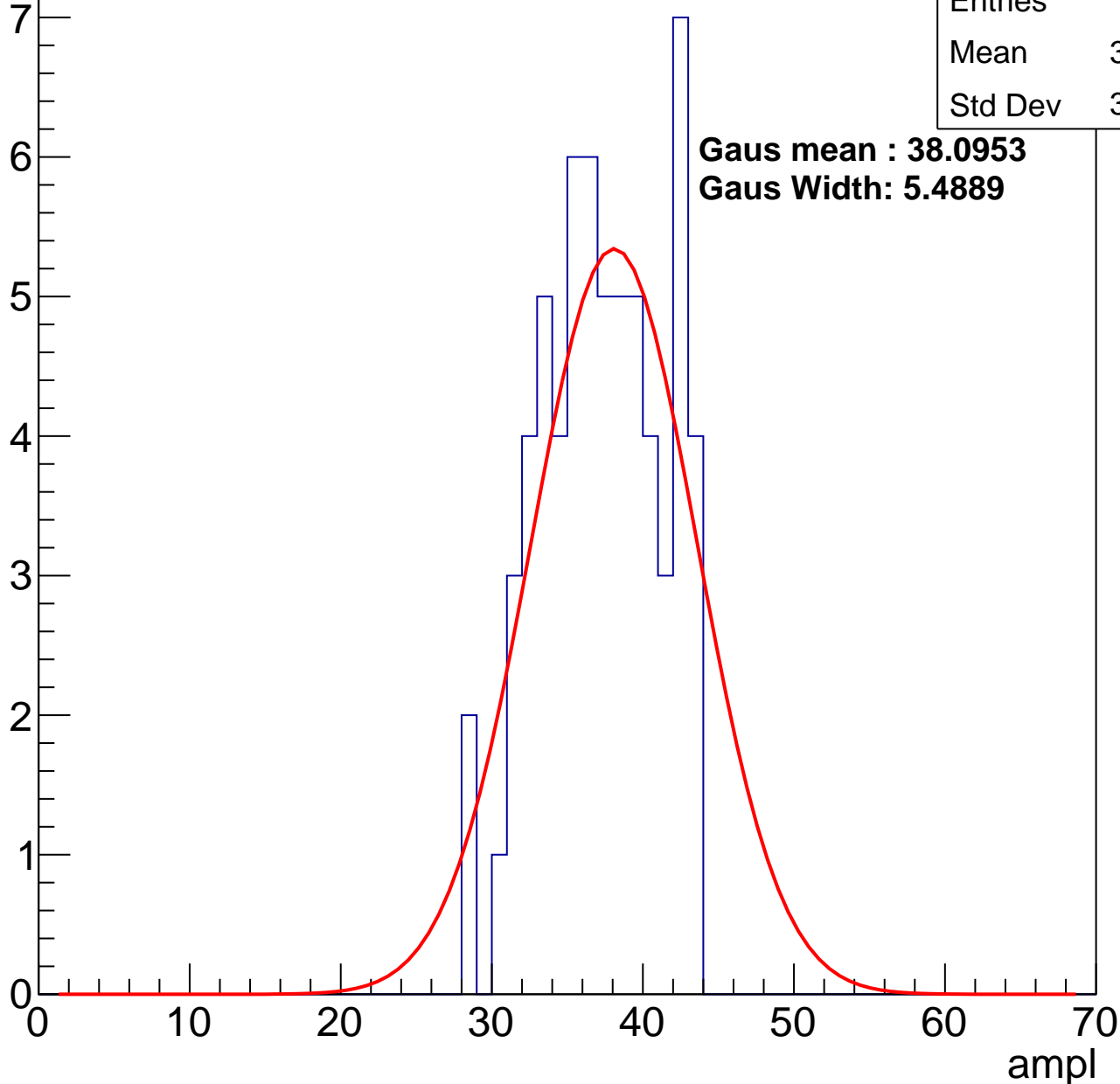
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.77
Std Dev	3.932

Gaus mean : 38.0953

Gaus Width: 5.4889



B0L002S, U5-ch44, adc2

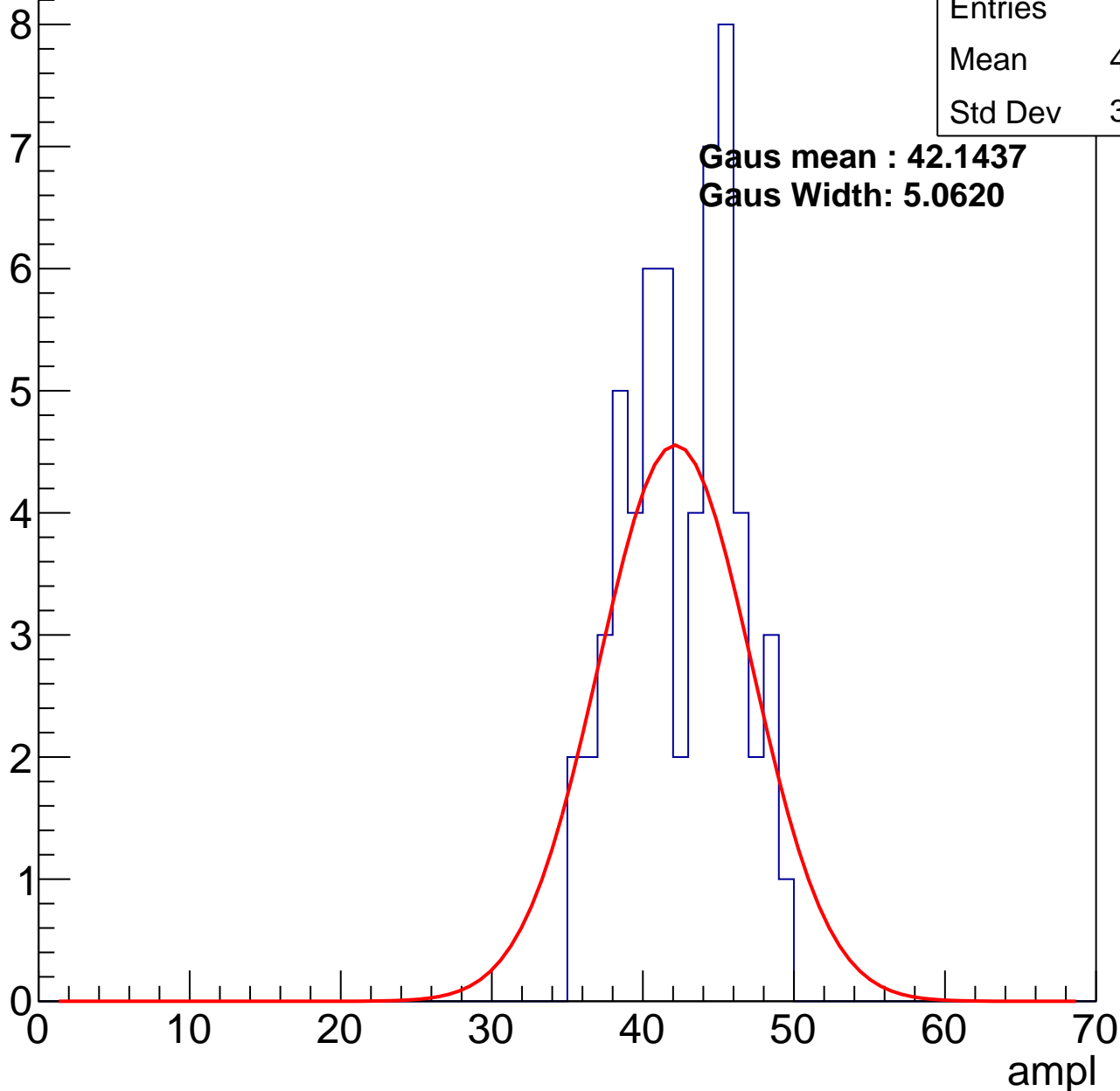
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	42.03
Std Dev	3.603

Gaus mean : 42.1437

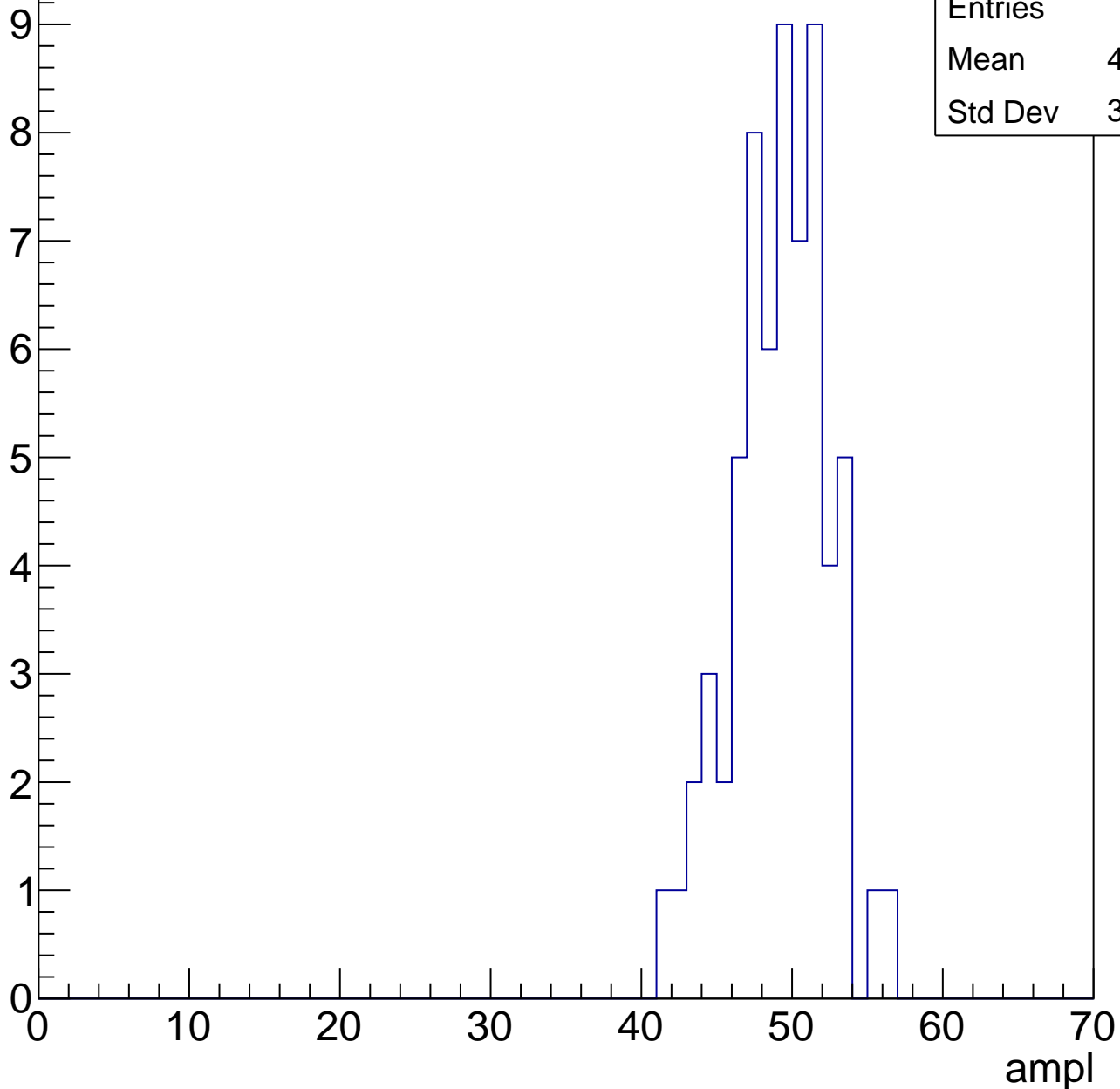
Gaus Width: 5.0620



B0L002S, U5-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

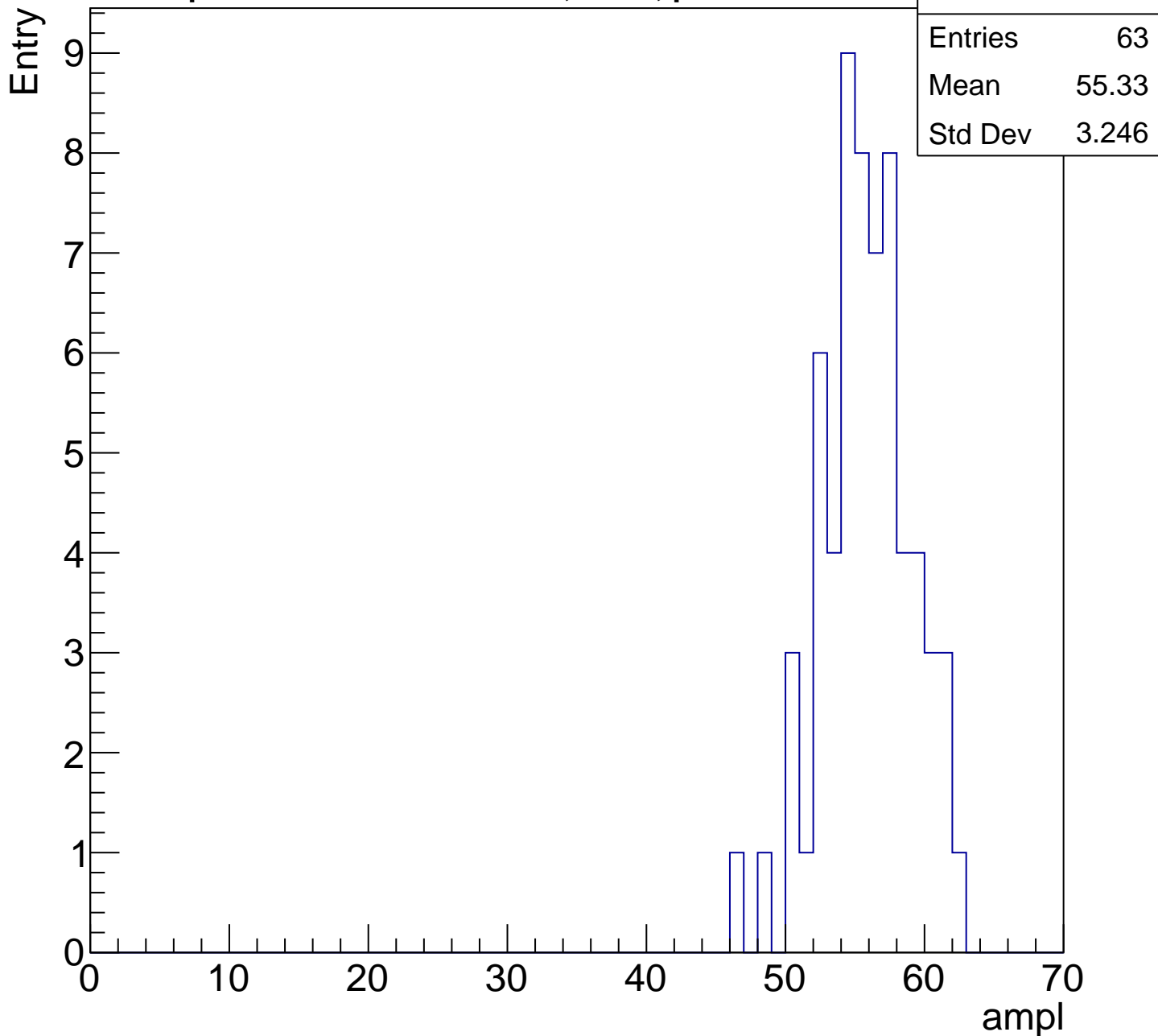
Entry



Entries	64
Mean	48.73
Std Dev	3.099

B0L002S, U5-ch44, adc4

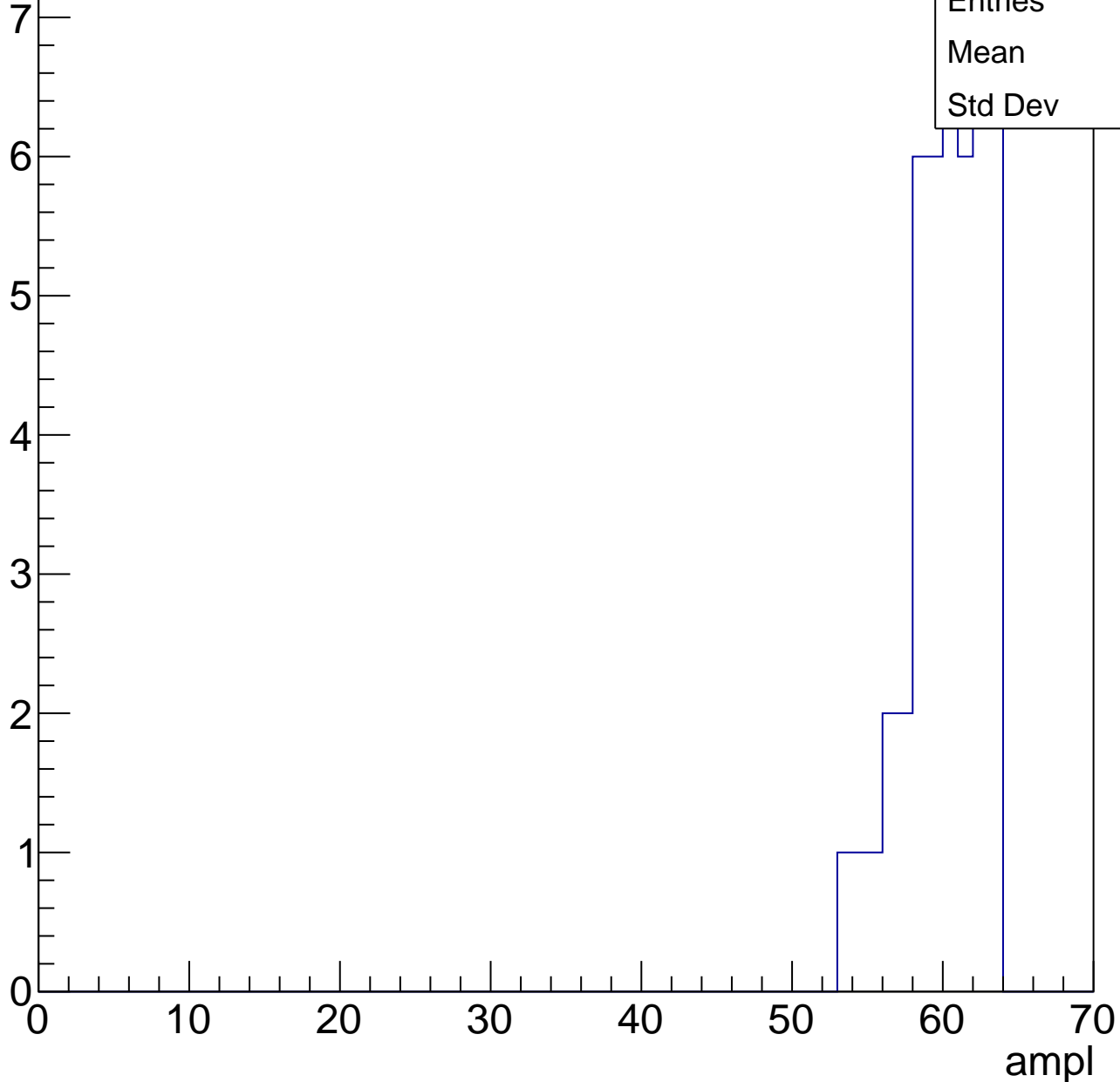
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	9
Std Dev	9

B0L002S, U5-ch45, adc0

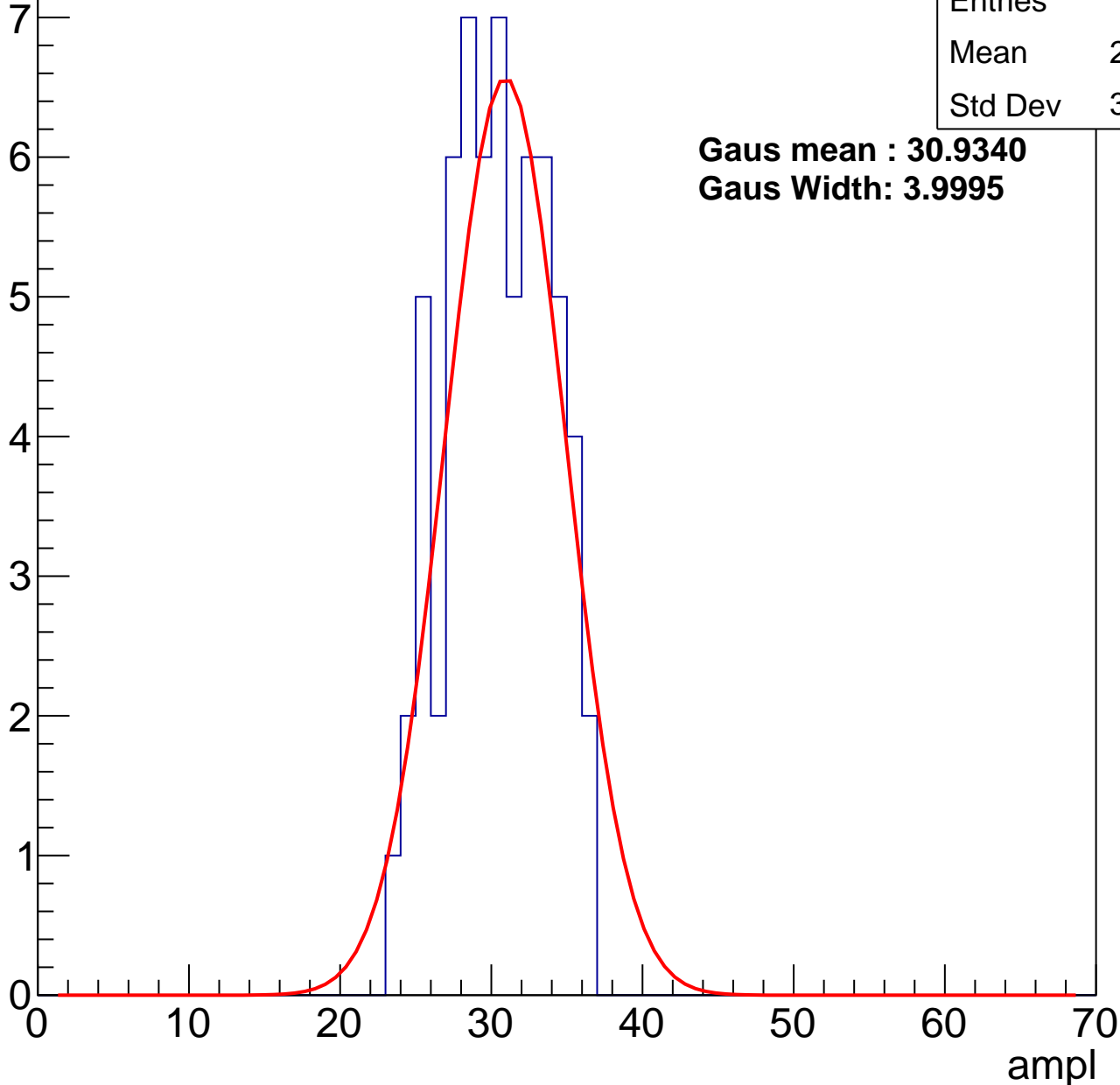
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.95
Std Dev	3.309

Gaus mean : 30.9340

Gaus Width: 3.9995



B0L002S, U5-ch45, adc1

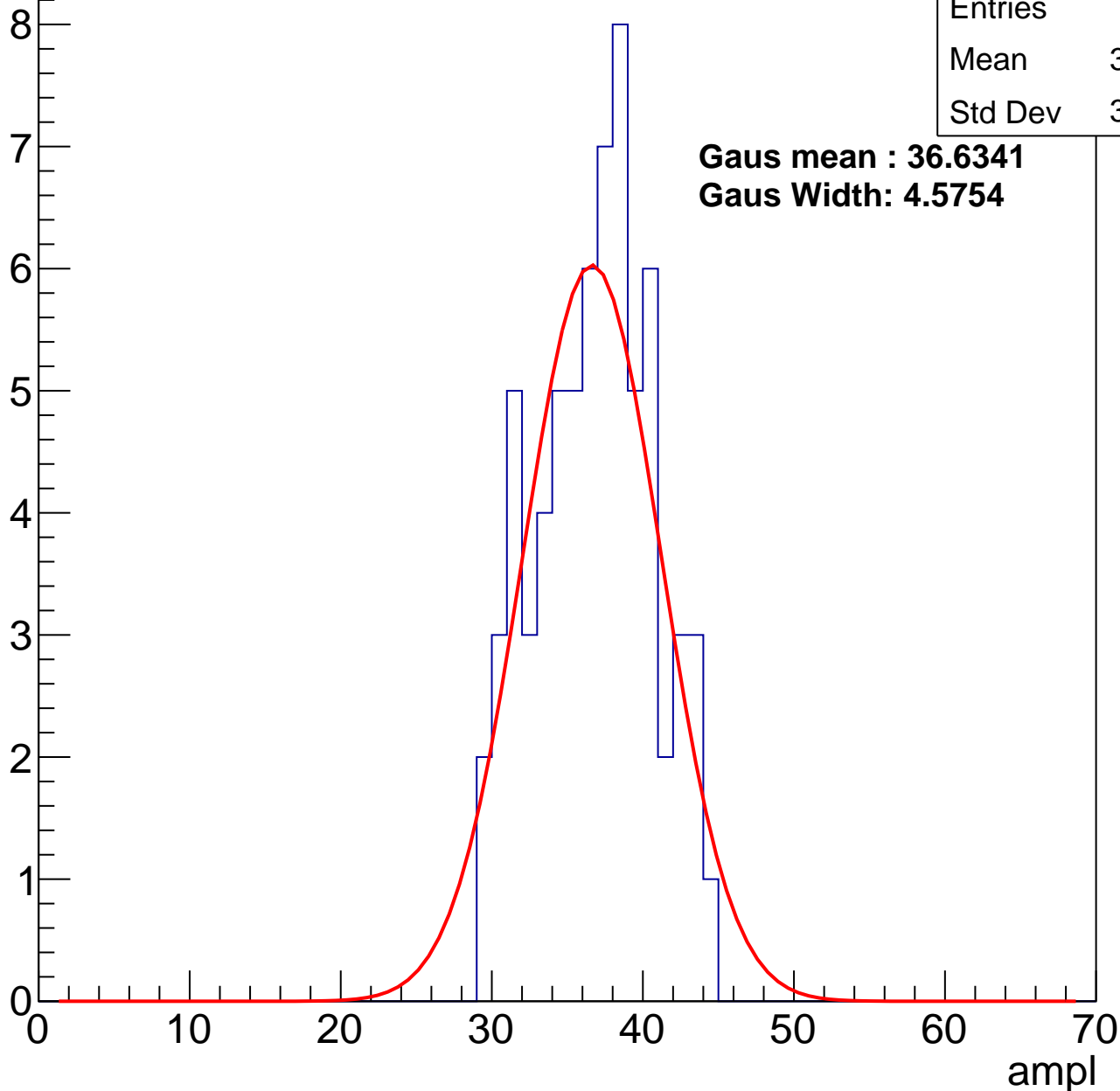
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	36.34
Std Dev	3.799

Gaus mean : 36.6341

Gaus Width: 4.5754



B0L002S, U5-ch45, adc2

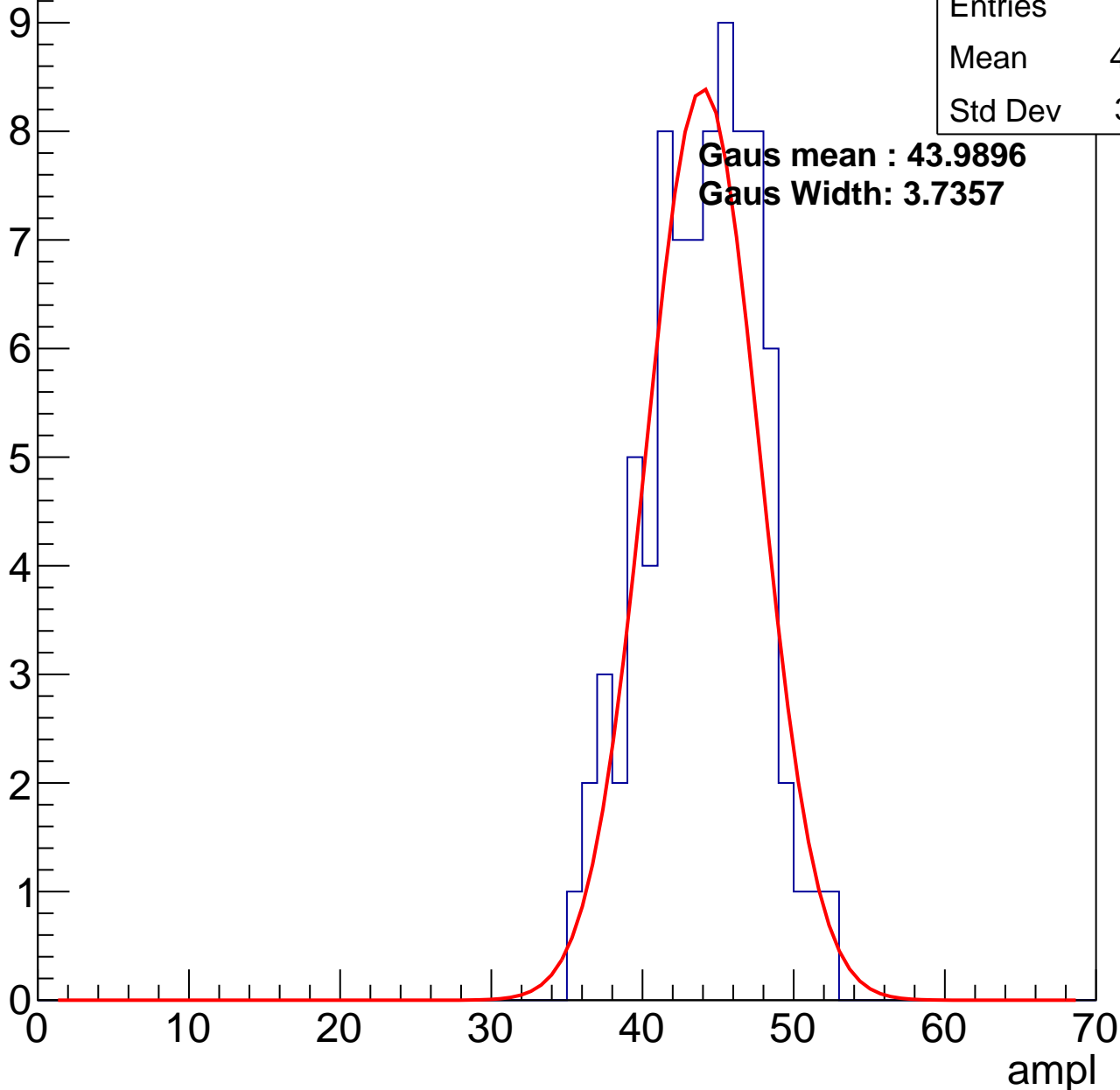
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	43.52
Std Dev	3.661

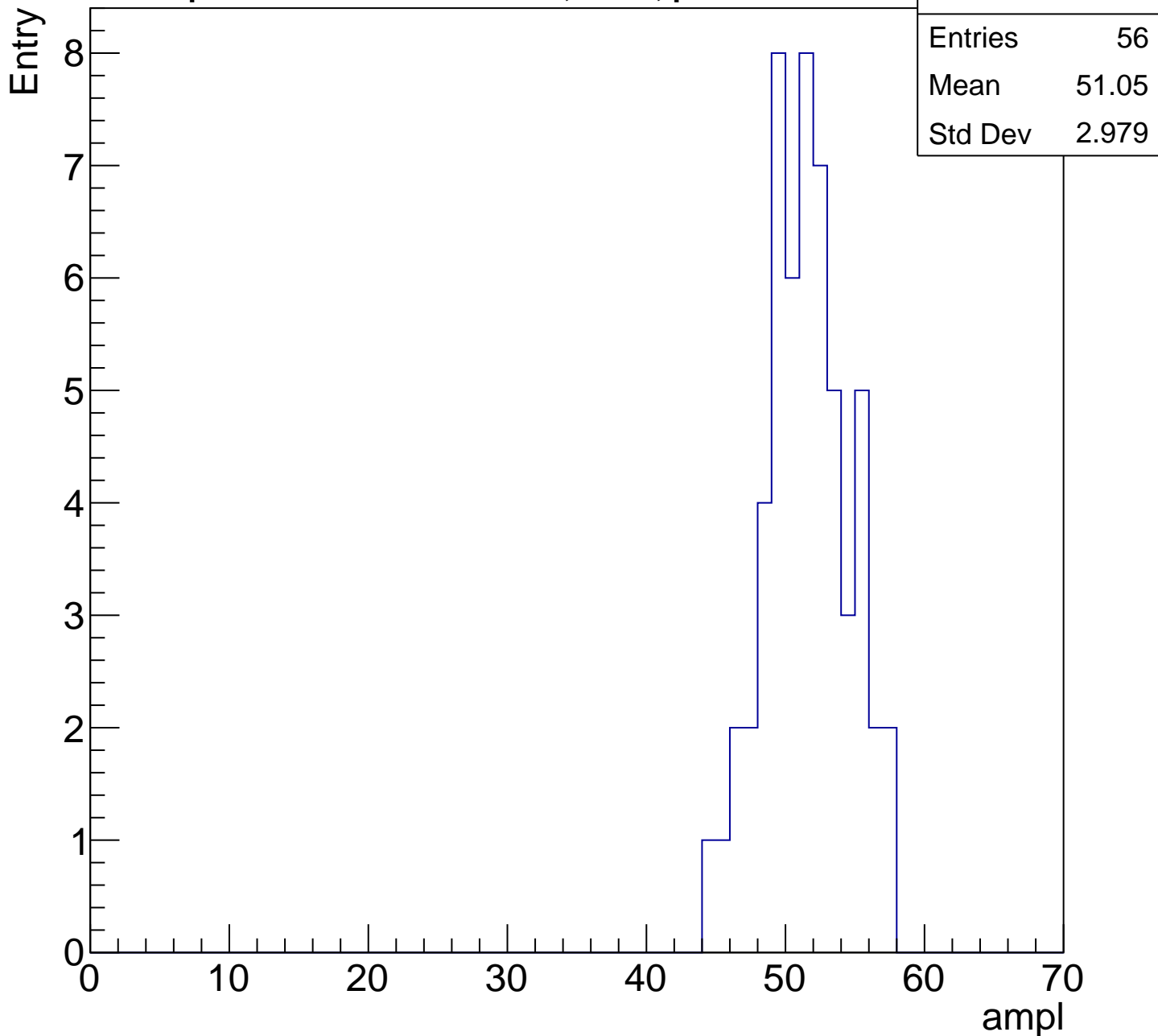
Gaus mean : 43.9896

Gaus Width: 3.7357



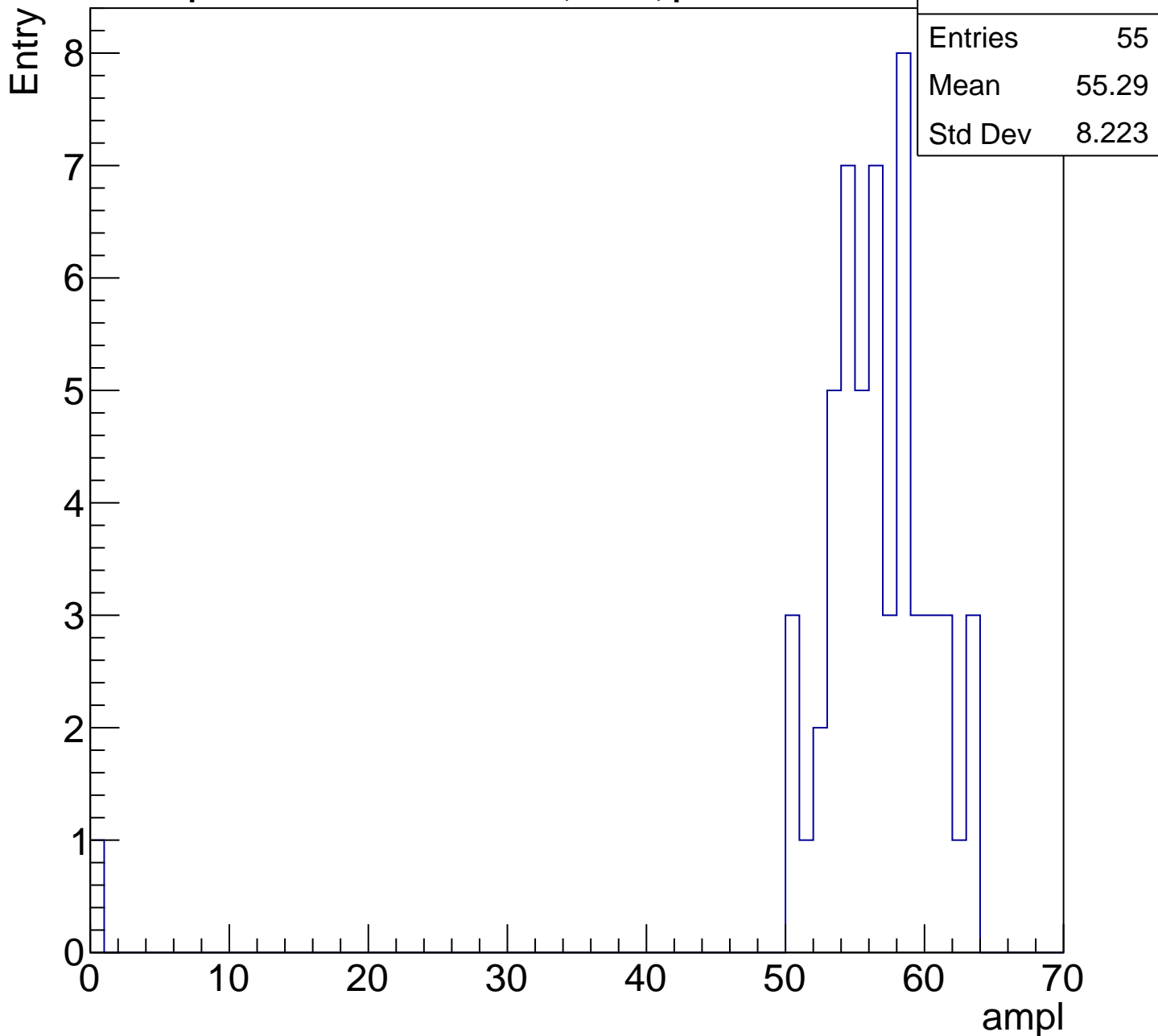
B0L002S, U5-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch45, adc4

calib_packv5_042523_0143.root, FC#8, port C1

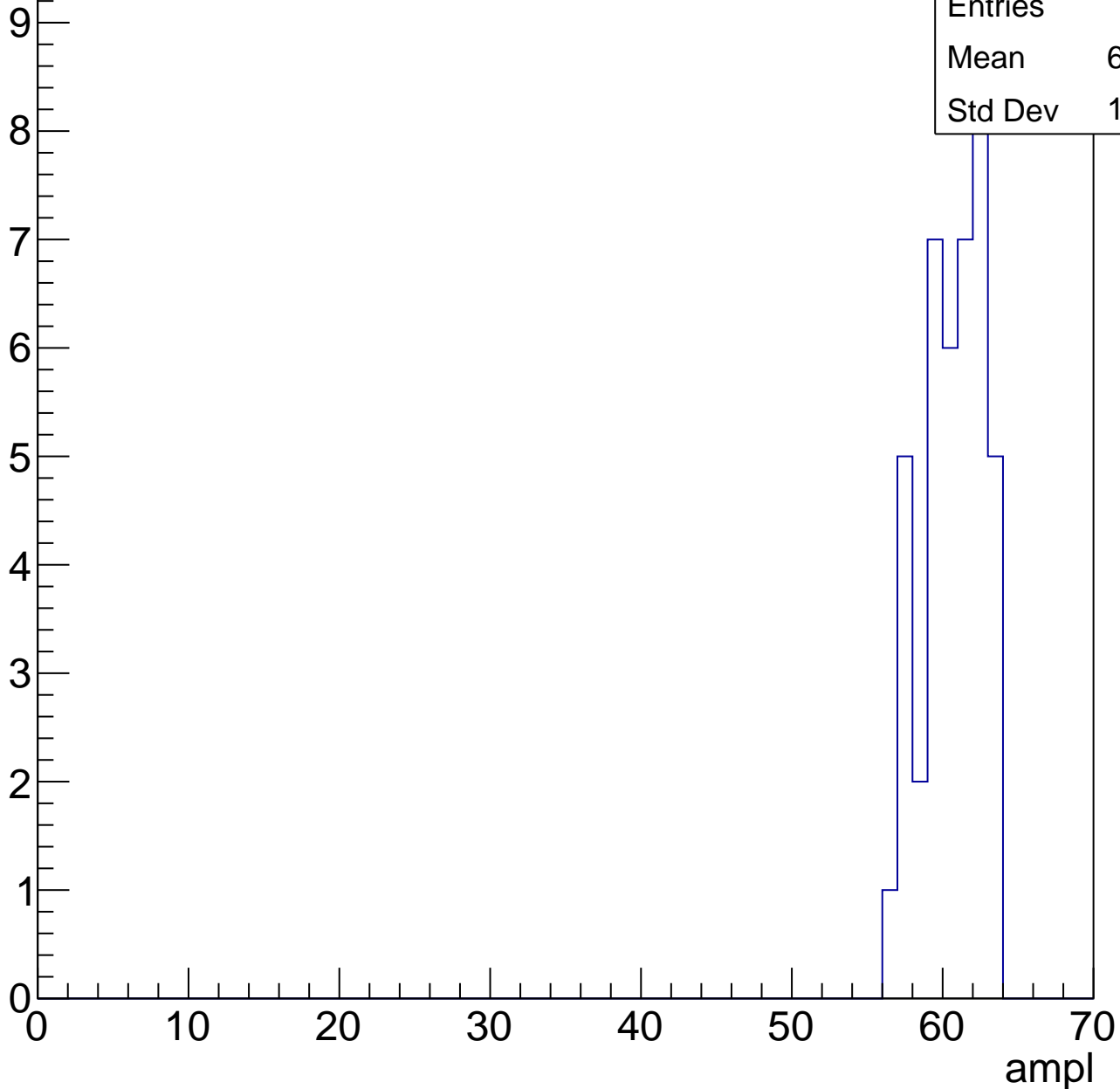


B0L002S, U5-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

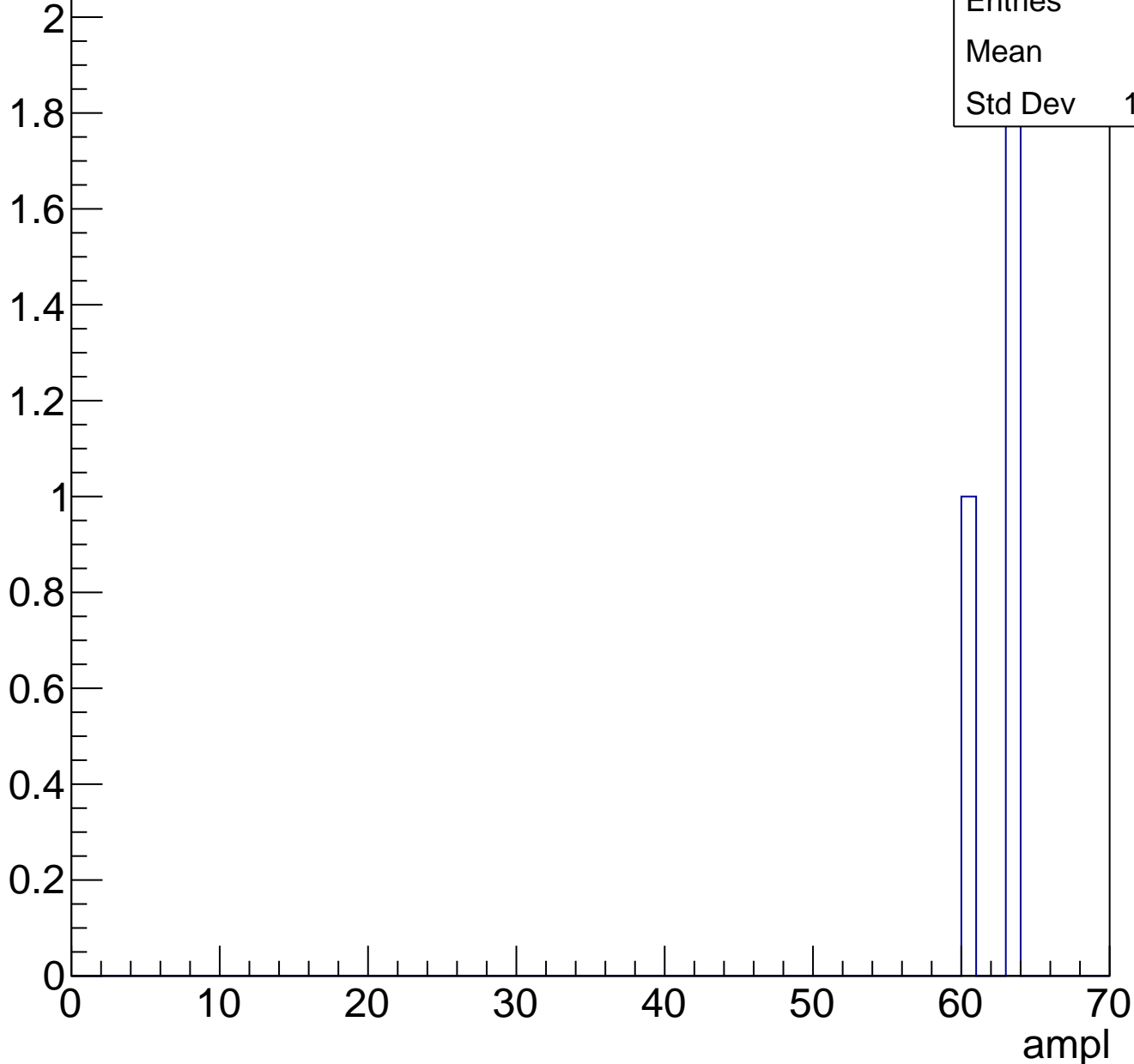
Entries	42
Mean	60.24
Std Dev	1.962



B0L002S, U5-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch46, adc0

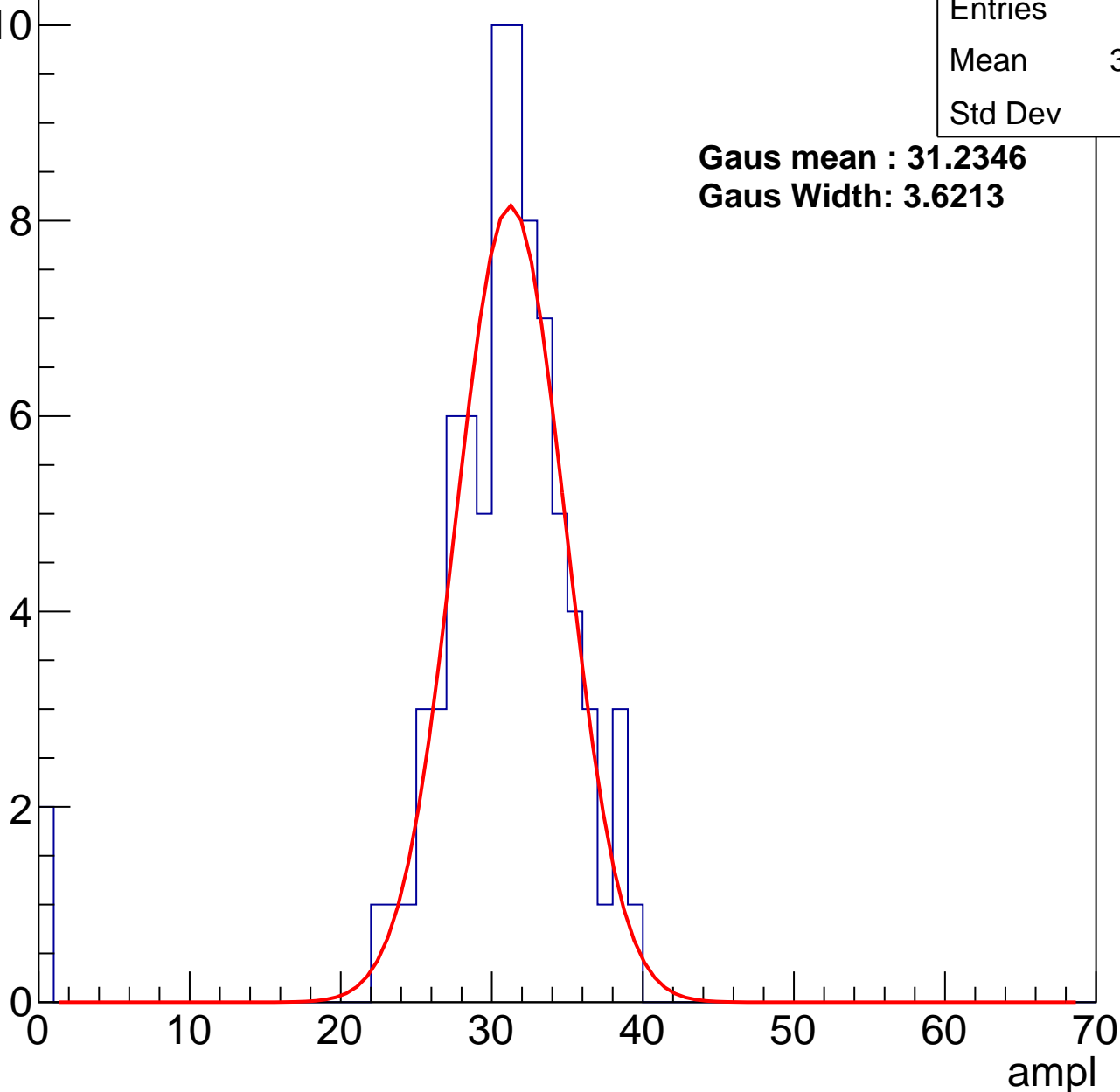
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	30.02
Std Dev	5.99

Gaus mean : 31.2346

Gaus Width: 3.6213



B0L002S, U5-ch46, adc1

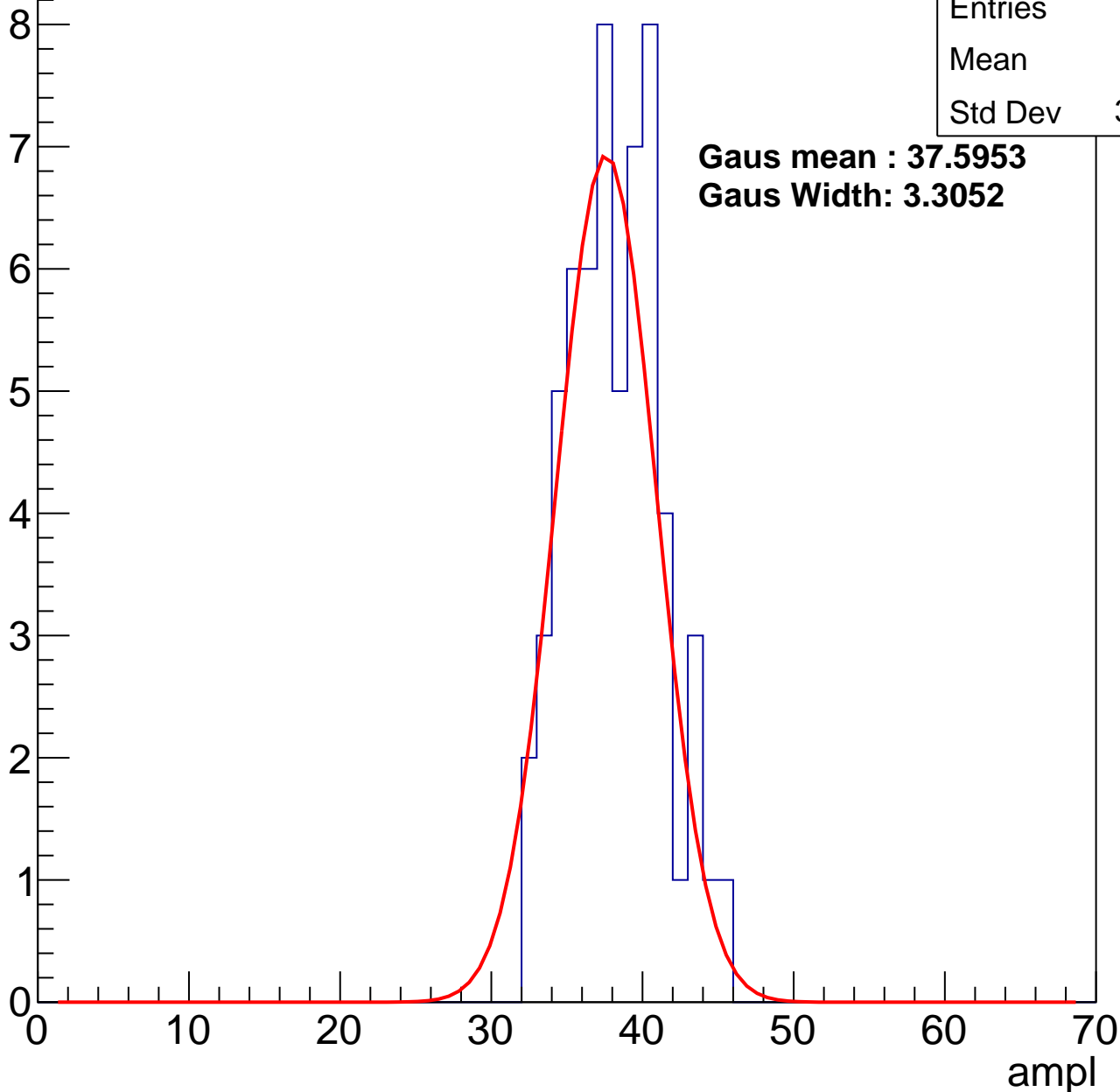
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.7
Std Dev	3.051

Gaus mean : 37.5953

Gaus Width: 3.3052



B0L002S, U5-ch46, adc2

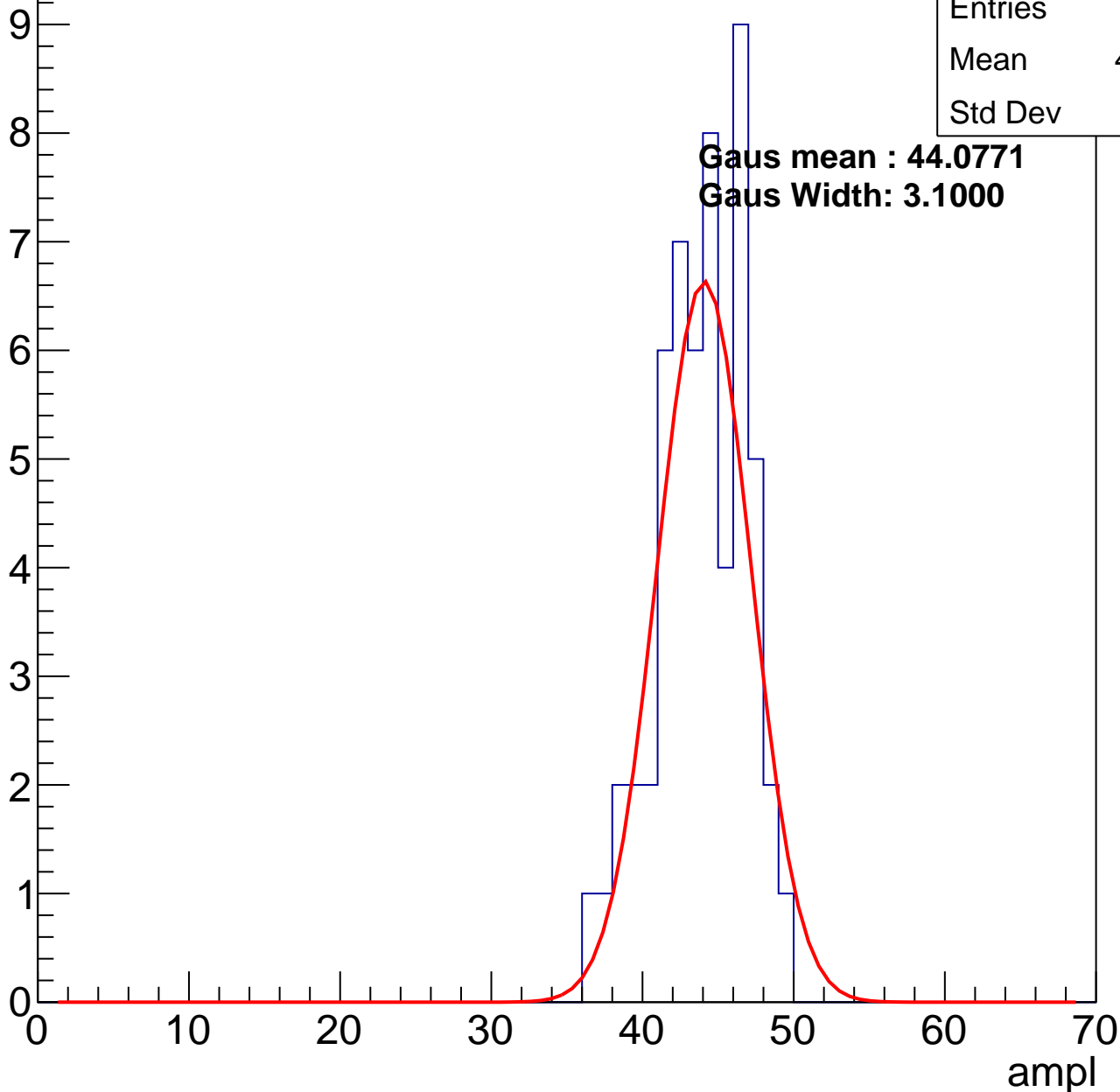
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.41
Std Dev	2.92

Gaus mean : 44.0771

Gaus Width: 3.1000

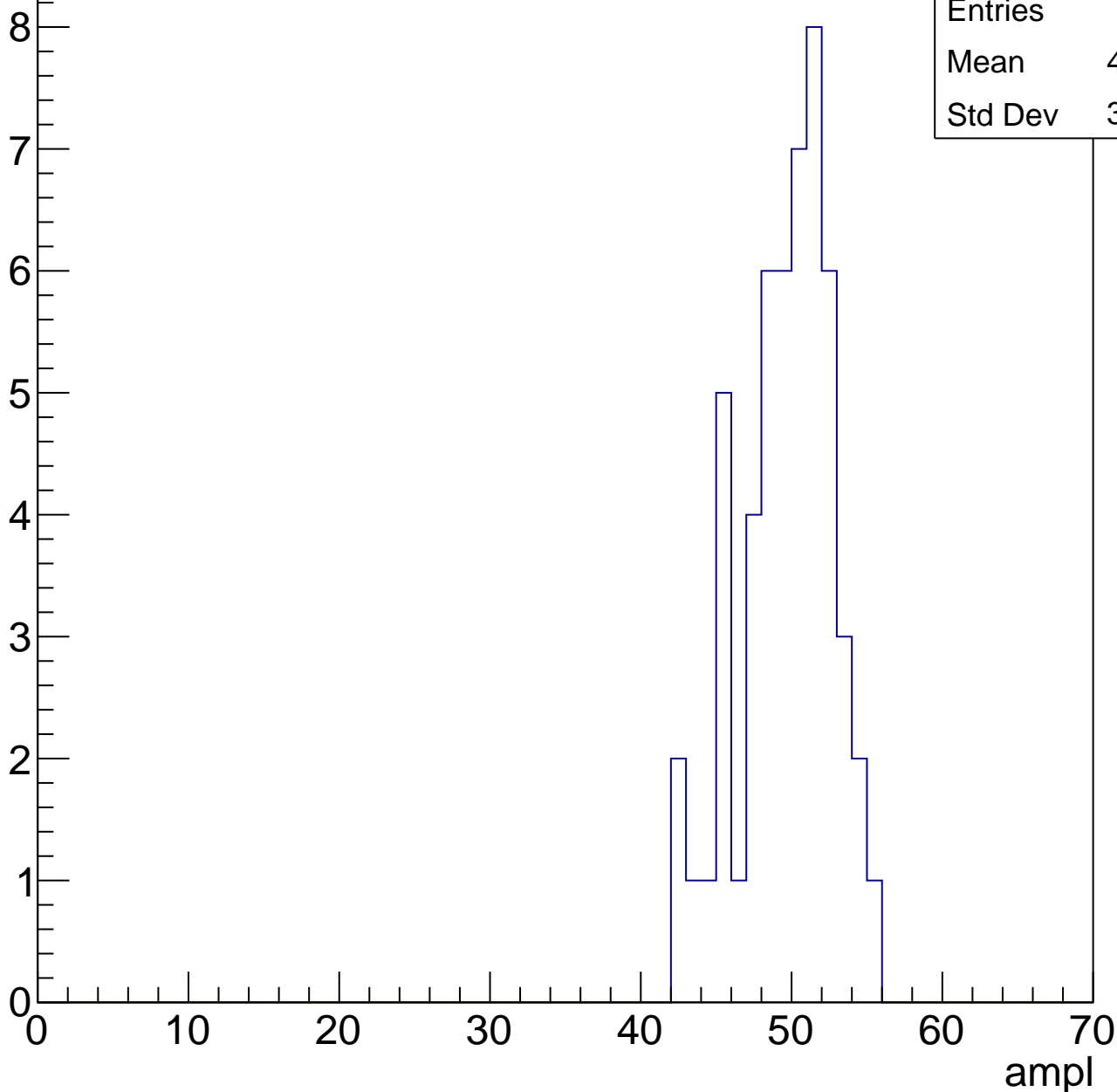


B0L002S, U5-ch46, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

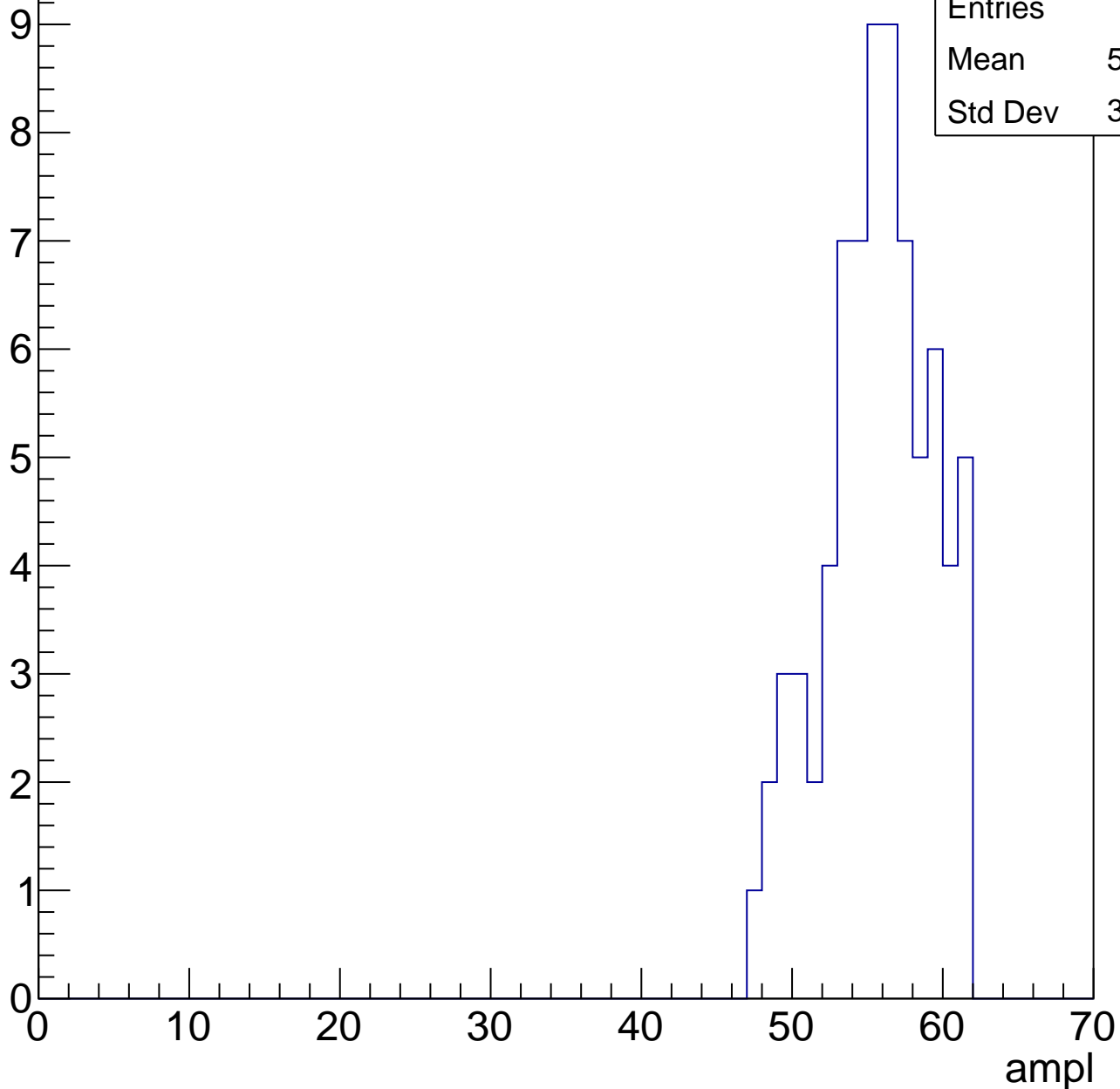
Entries	53
Mean	49.13
Std Dev	3.059



B0L002S, U5-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	74
Mean	55.22
Std Dev	3.492

B0L002S, U5-ch46, adc5

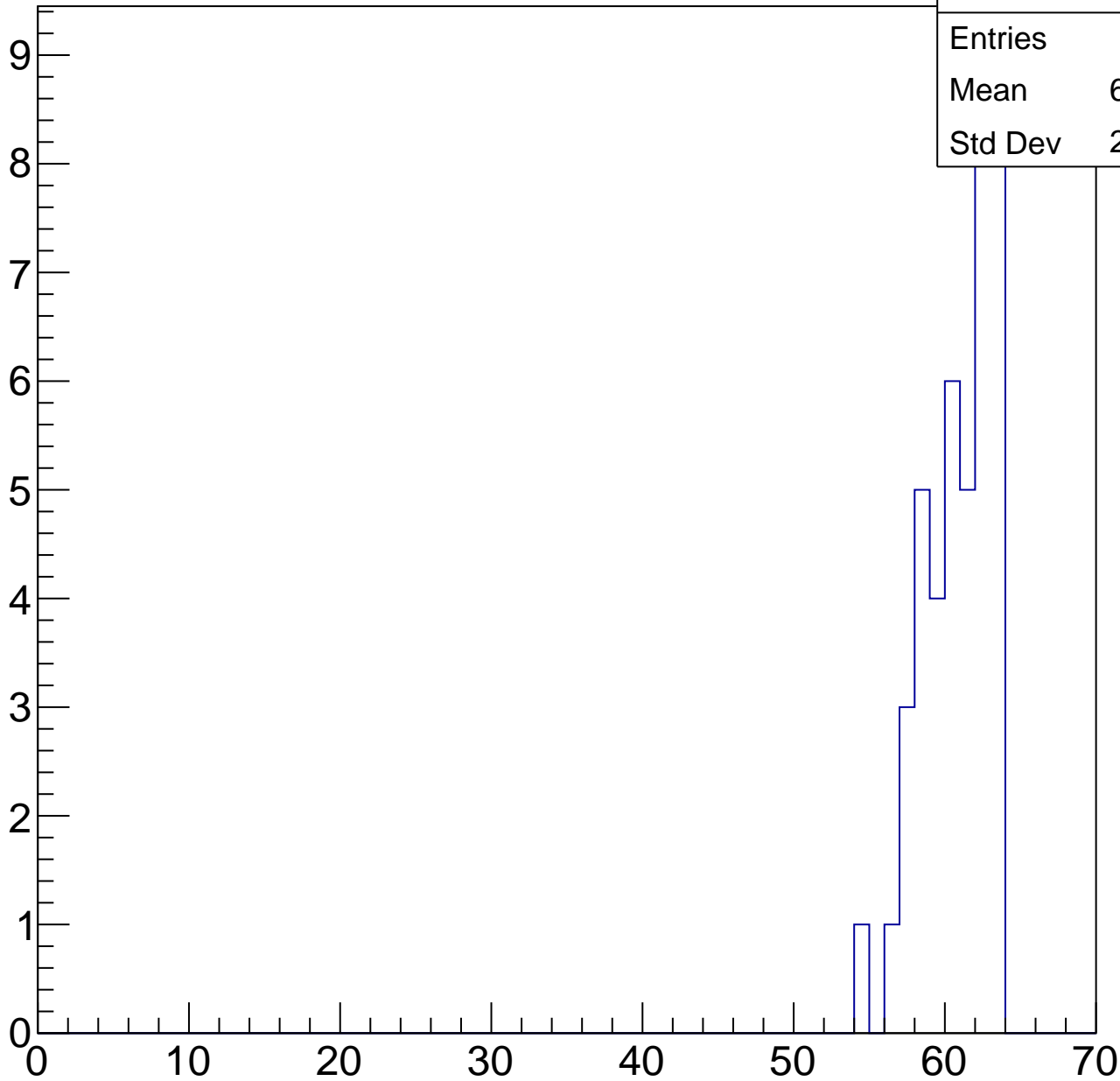
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.36
Std Dev	2.266

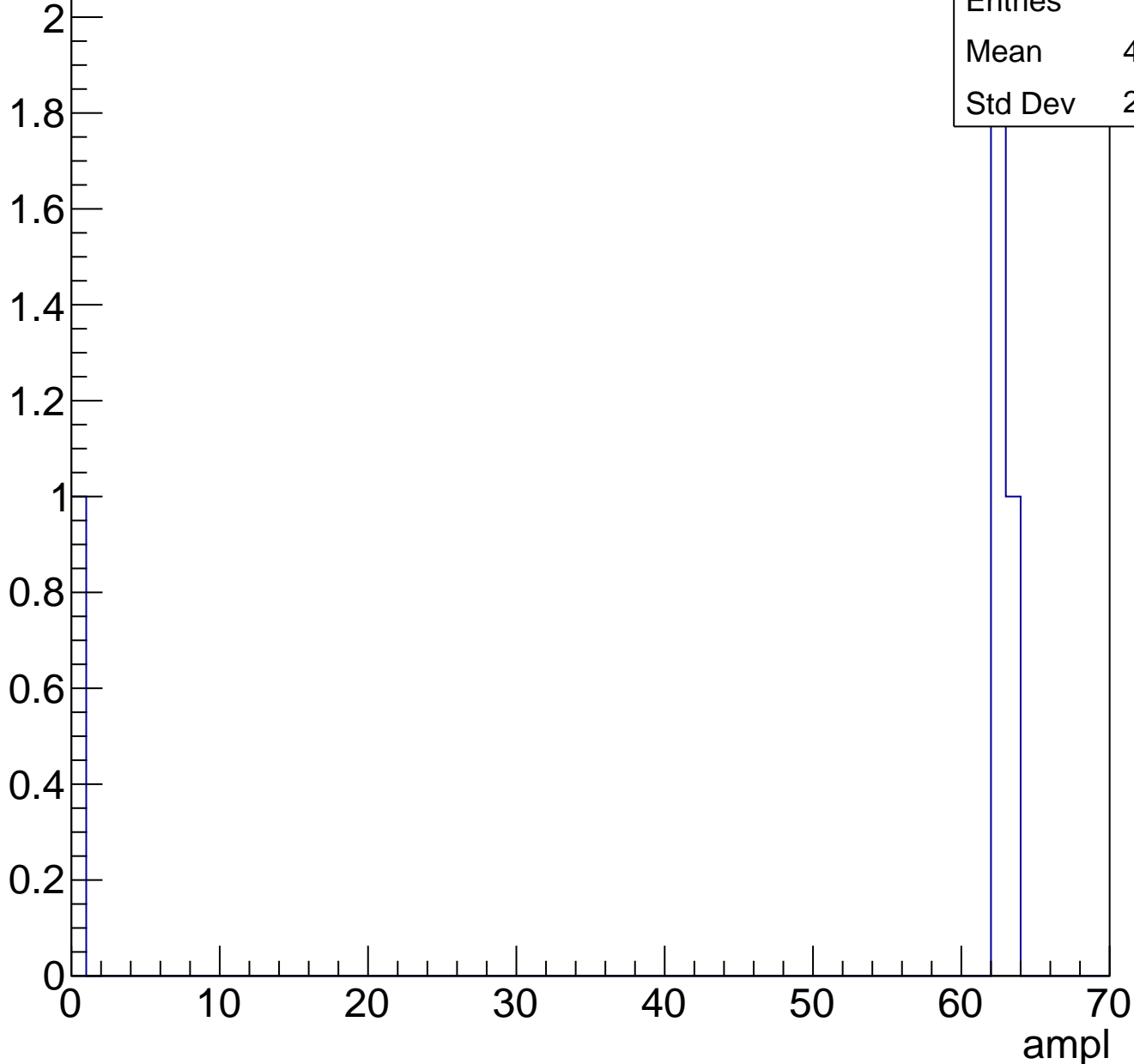
ampl



B0L002S, U5-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

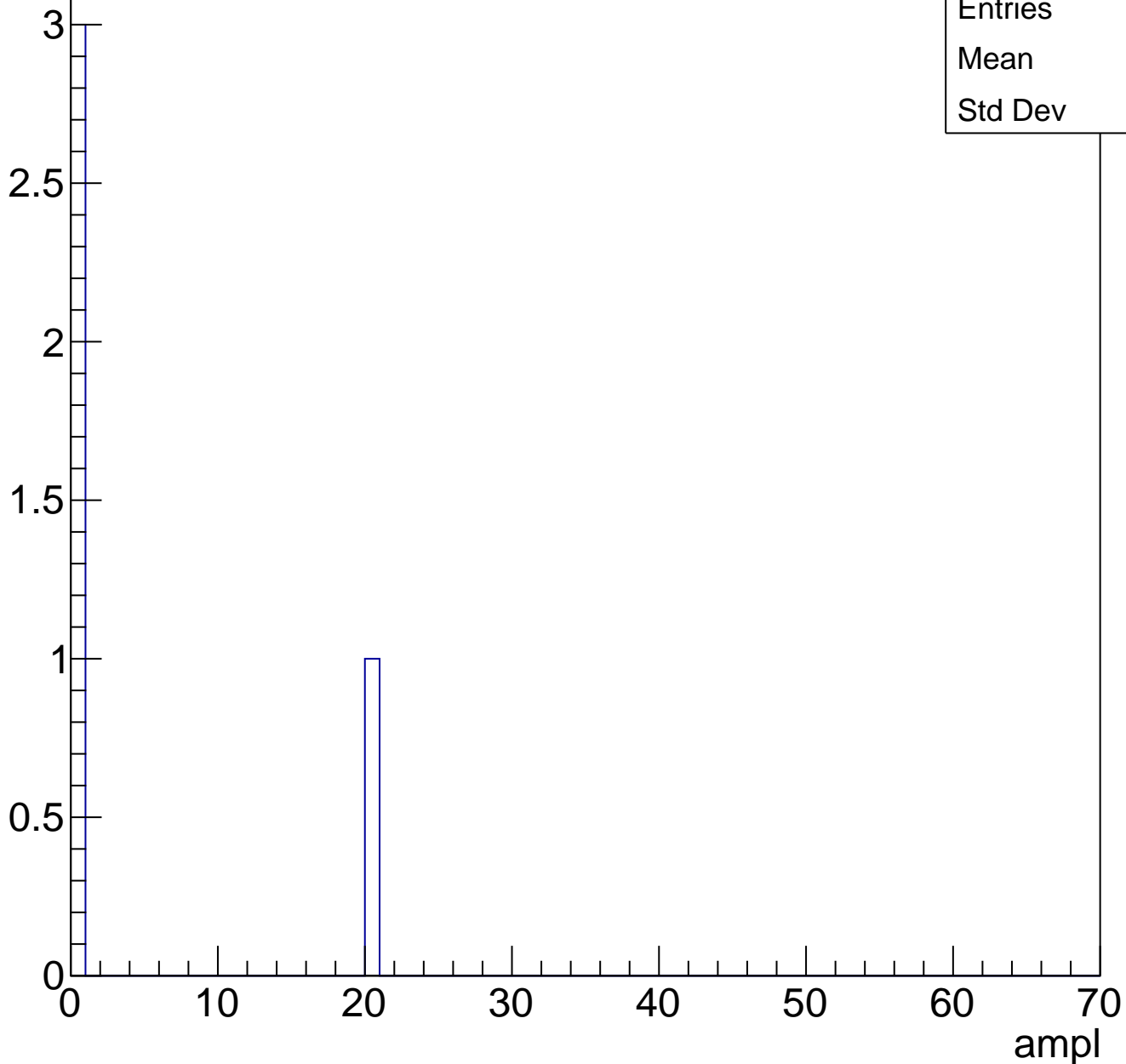
Entry



B0L002S, U5-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch47, adc0

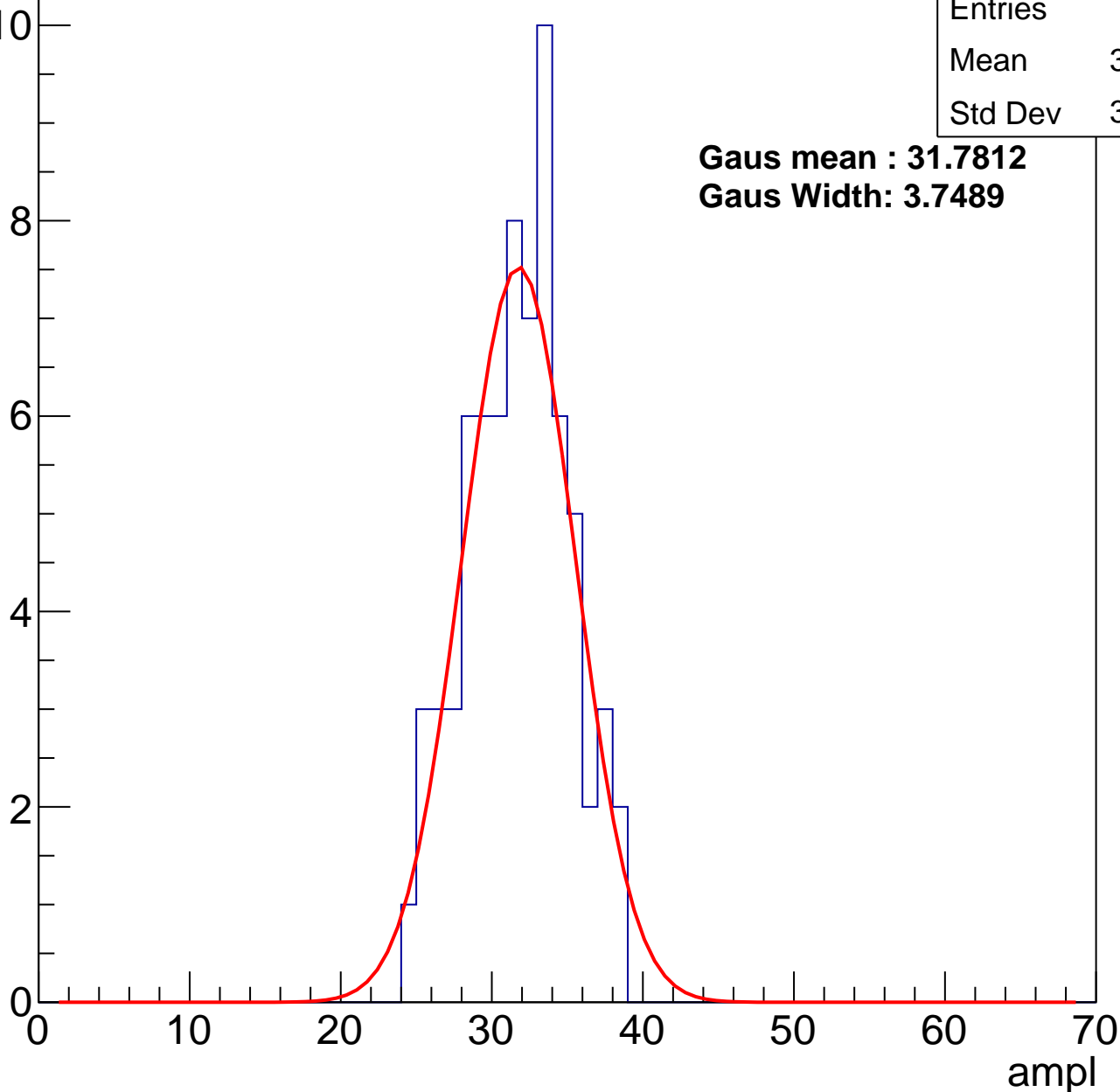
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.27
Std Dev	3.348

Gaus mean : 31.7812

Gaus Width: 3.7489



B0L002S, U5-ch47, adc1

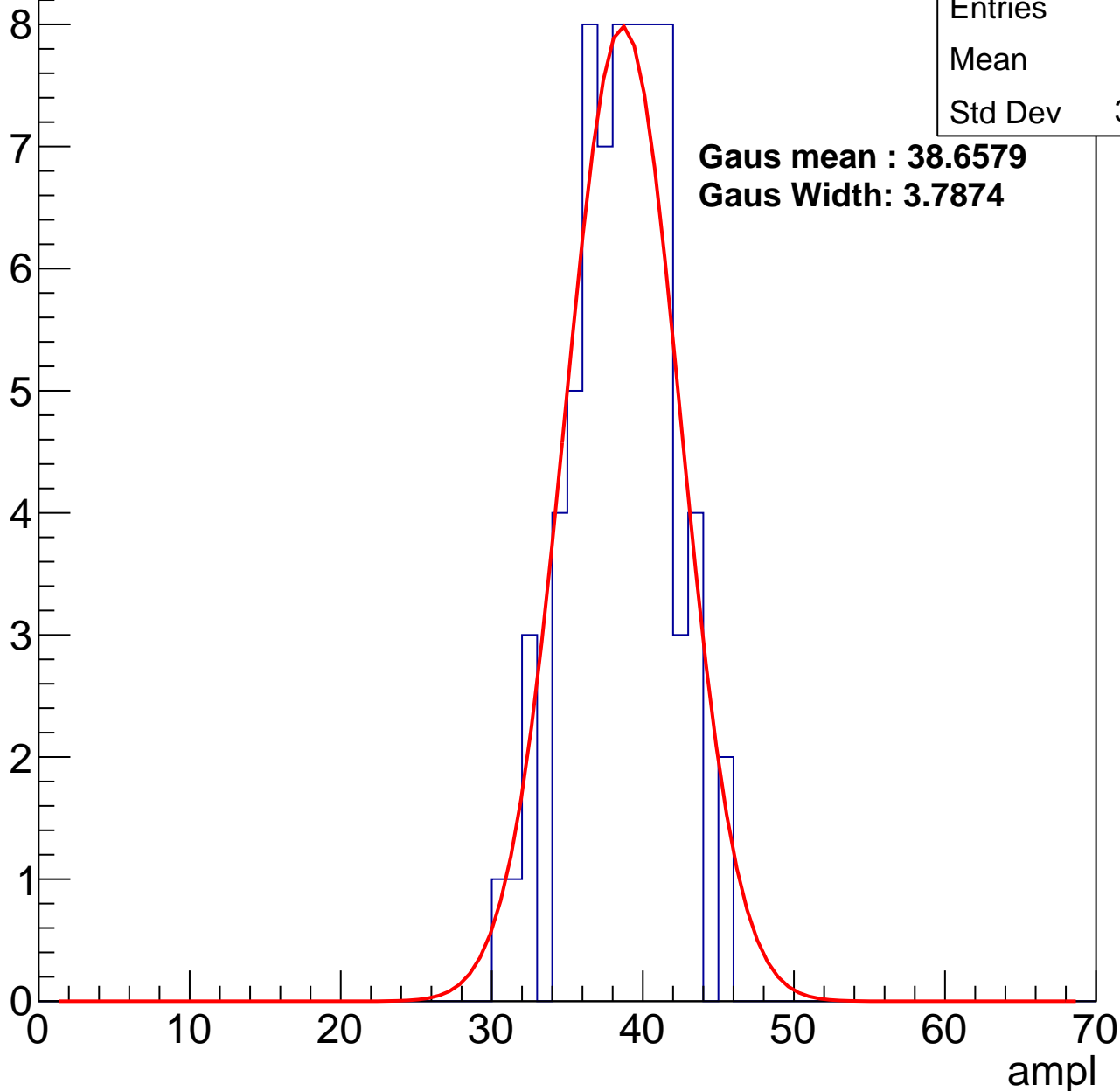
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	38.1
Std Dev	3.221

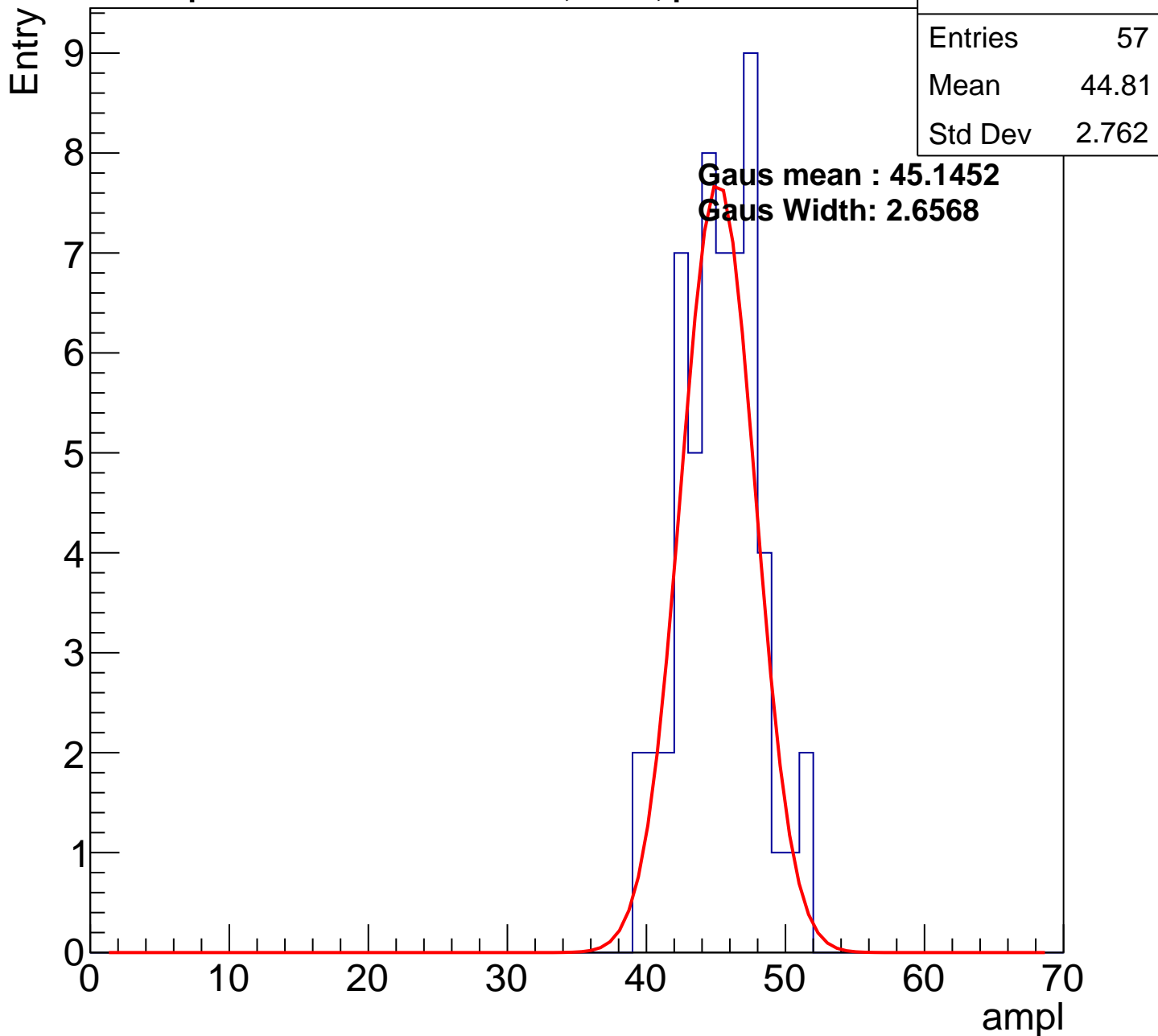
Gaus mean : 38.6579

Gaus Width: 3.7874



B0L002S, U5-ch47, adc2

calib_packv5_042523_0143.root, FC#8, port C1

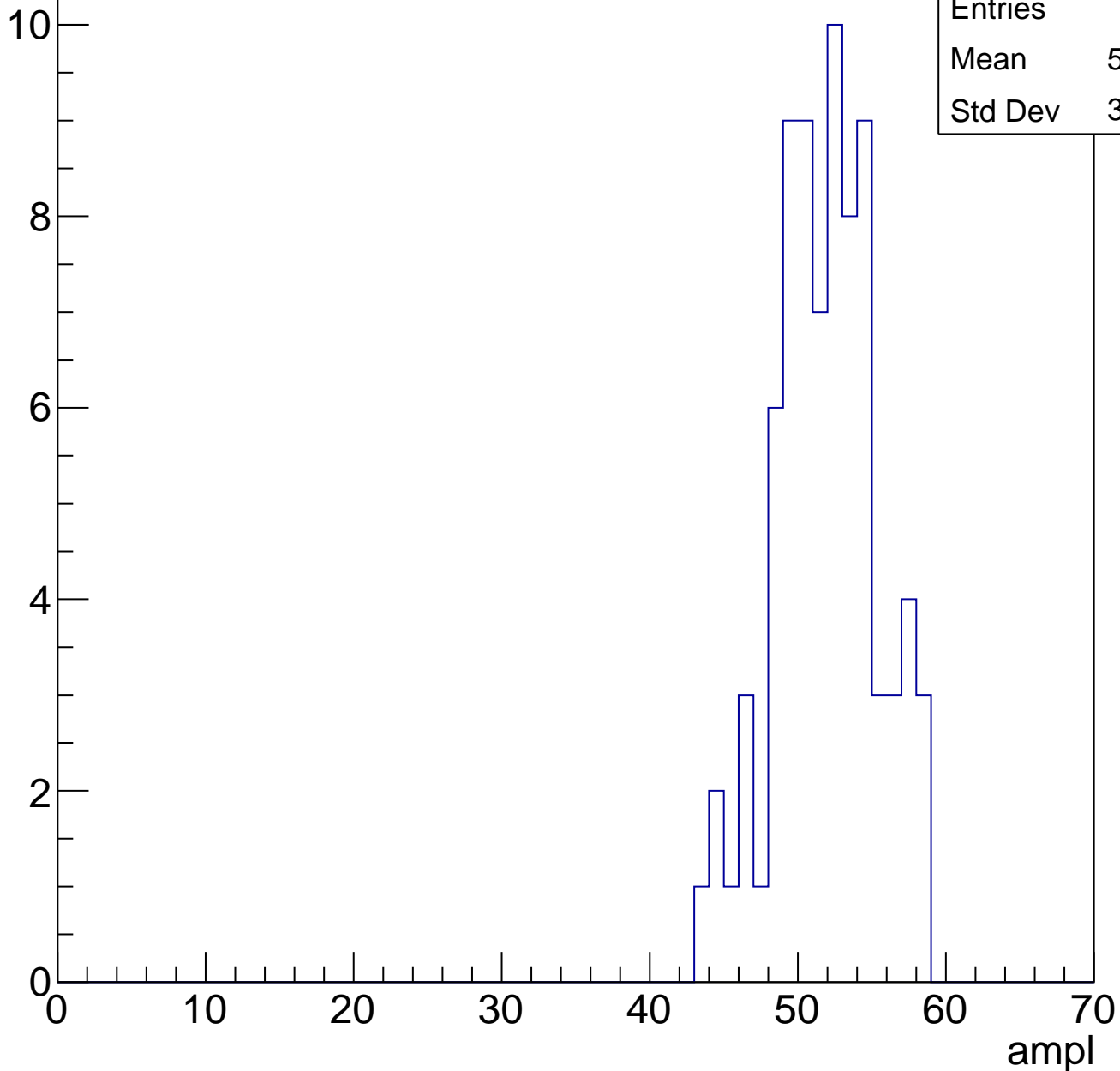


B0L002S, U5-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	51.42
Std Dev	3.396

Entry

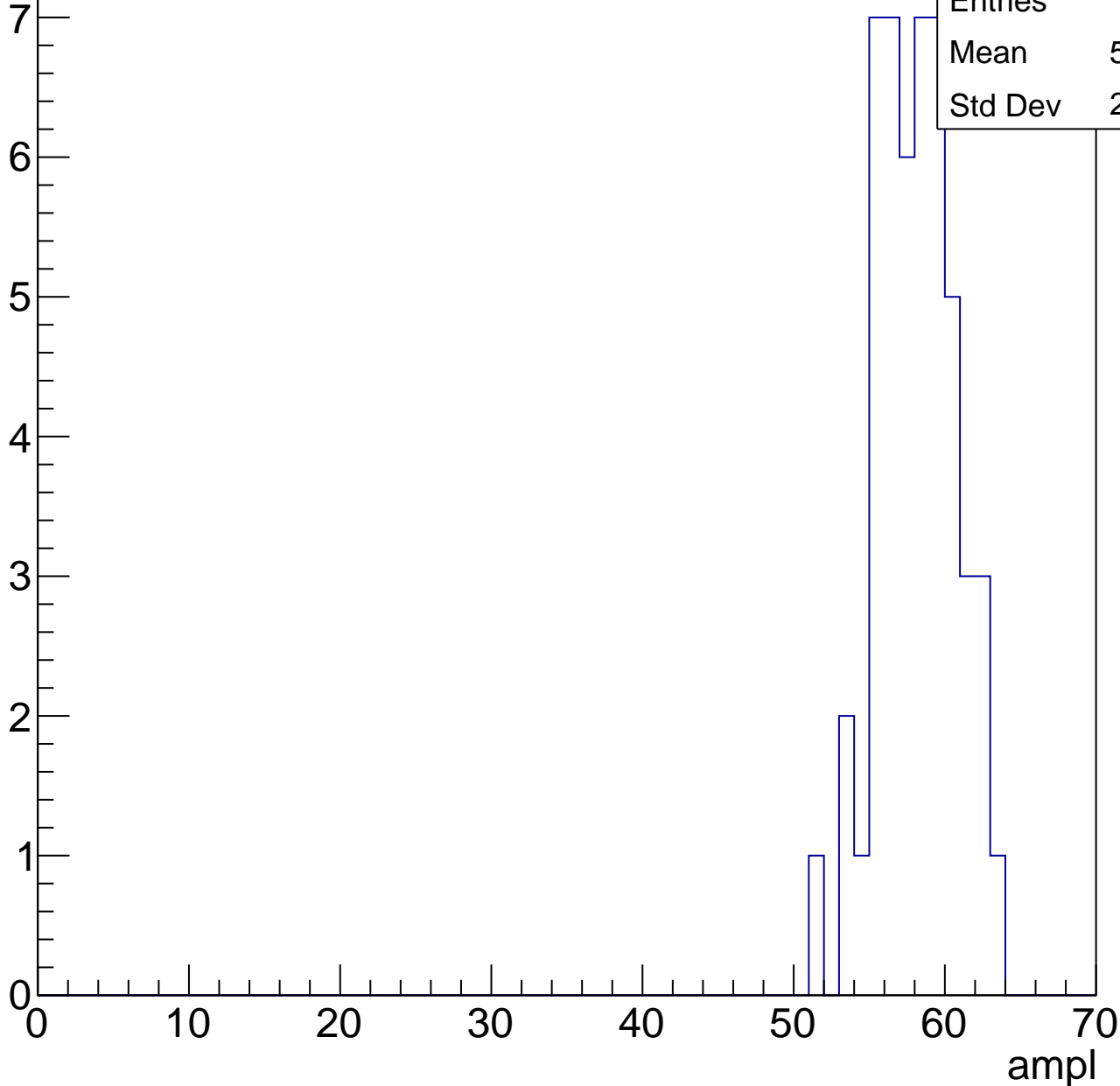


B0L002S, U5-ch47, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	57.62
Std Dev	2.576



B0L002S, U5-ch47, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	29
Mean	61.34
Std Dev	1.294

ampl

B0L002S, U5-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	42
Std Dev	29.7

B0L002S, U5-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch48, adc0

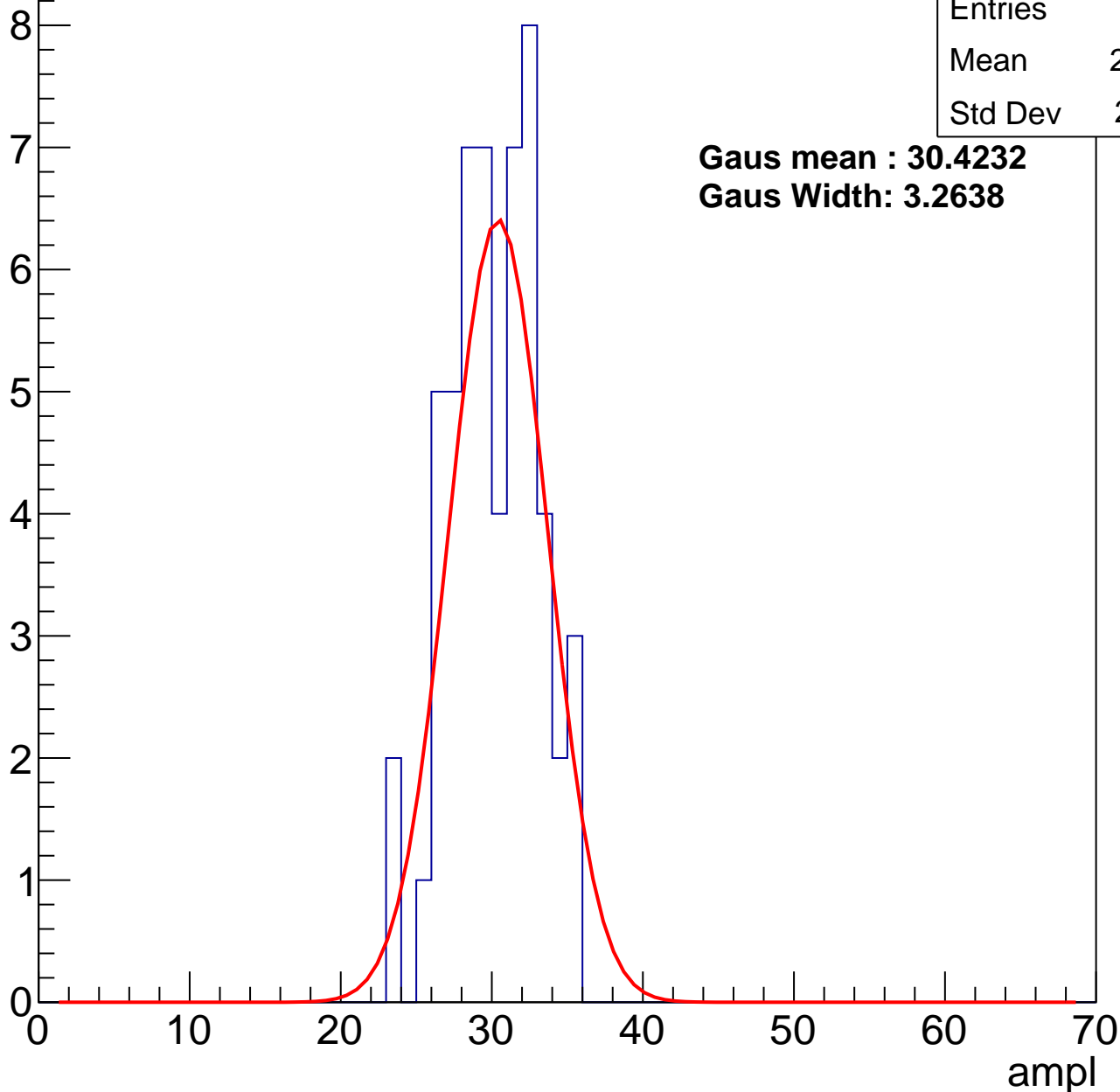
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	29.69
Std Dev	2.891

Gaus mean : 30.4232

Gaus Width: 3.2638



B0L002S, U5-ch48, adc1

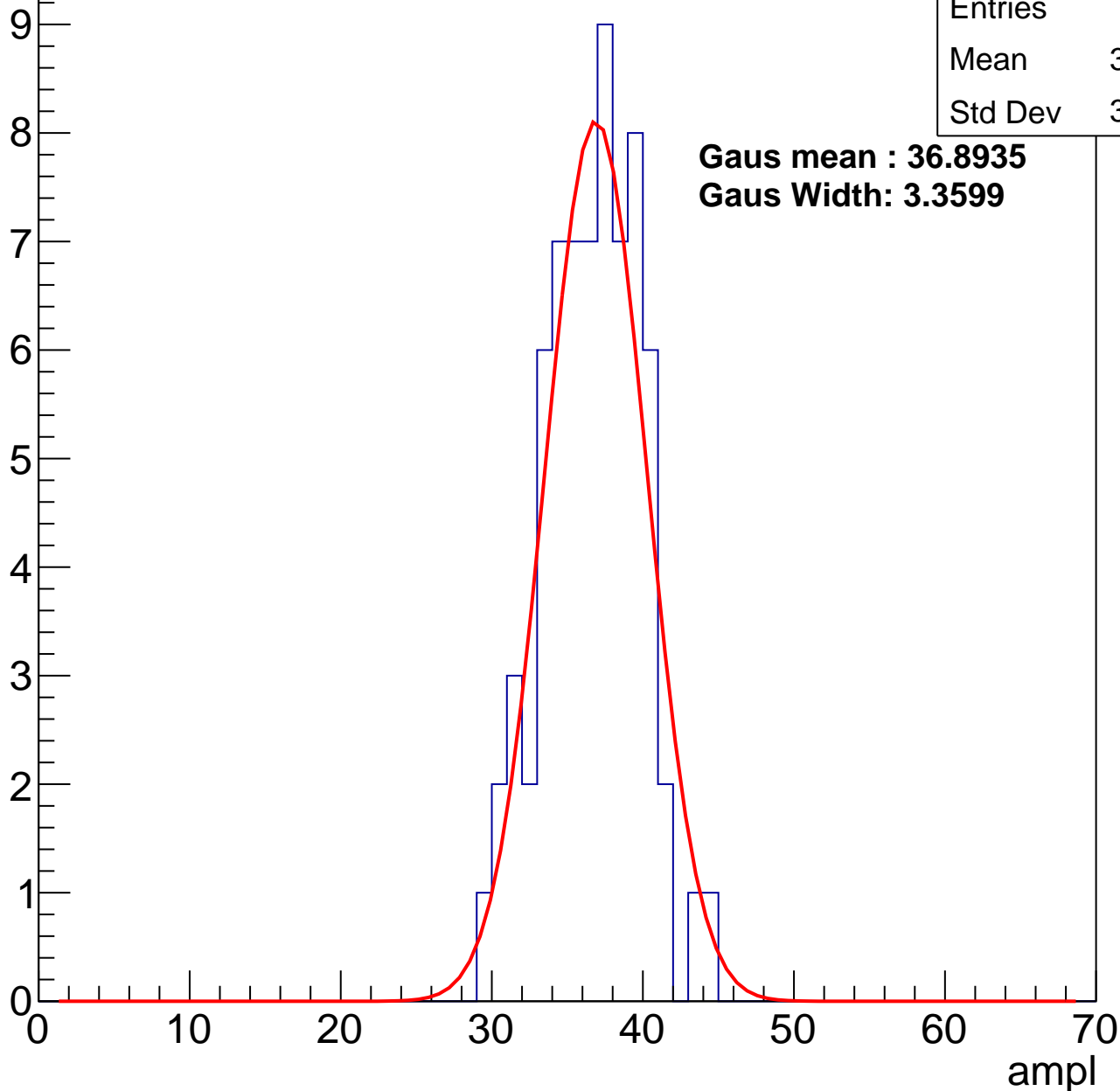
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.22
Std Dev	3.143

Gaus mean : 36.8935

Gaus Width: 3.3599



B0L002S, U5-ch48, adc2

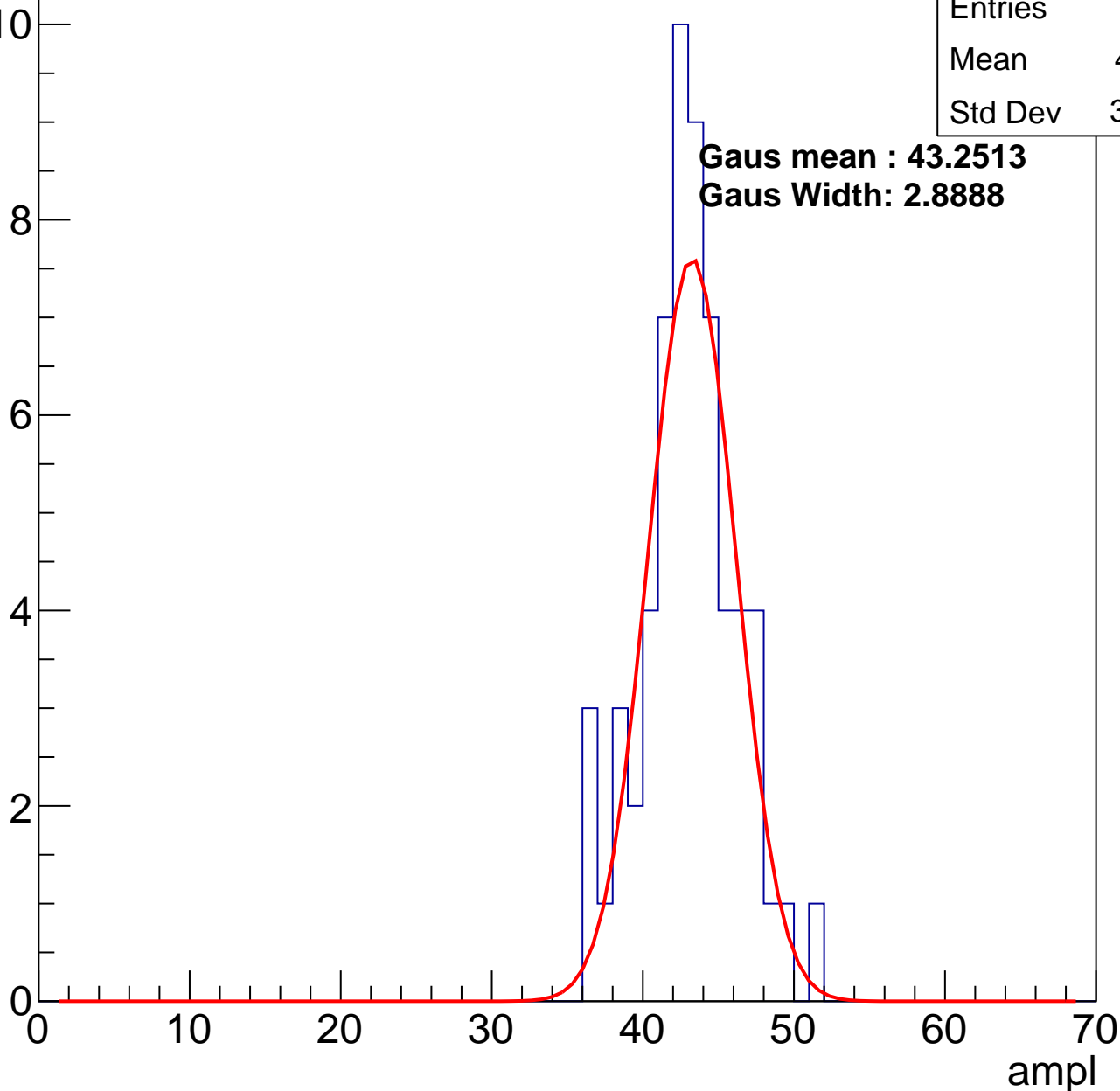
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.61
Std Dev	3.143

Gaus mean : 43.2513

Gaus Width: 2.8888

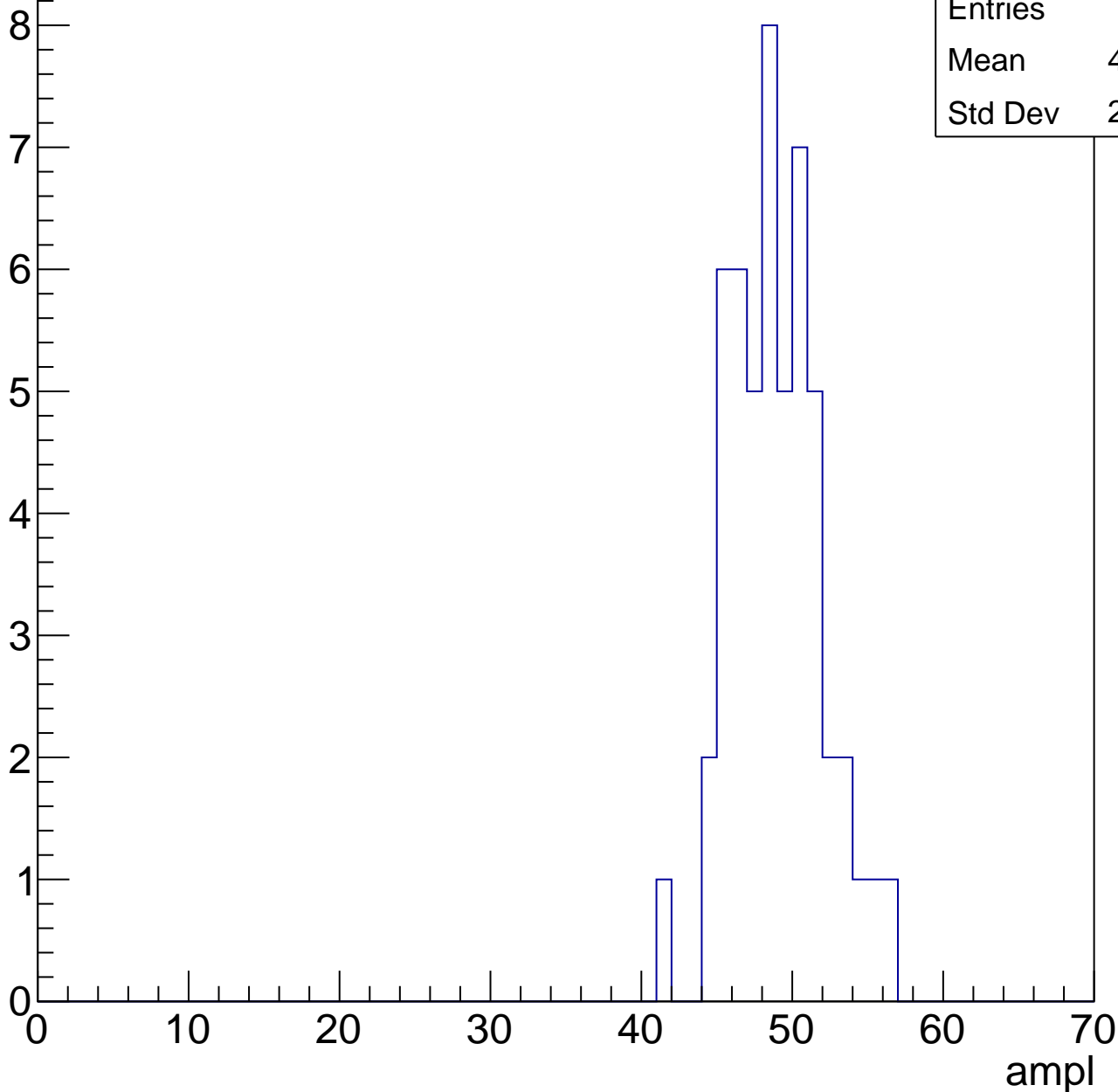


B0L002S, U5-ch48, adc3

calib_packv5_042523_0143.root, FC#8, port C1

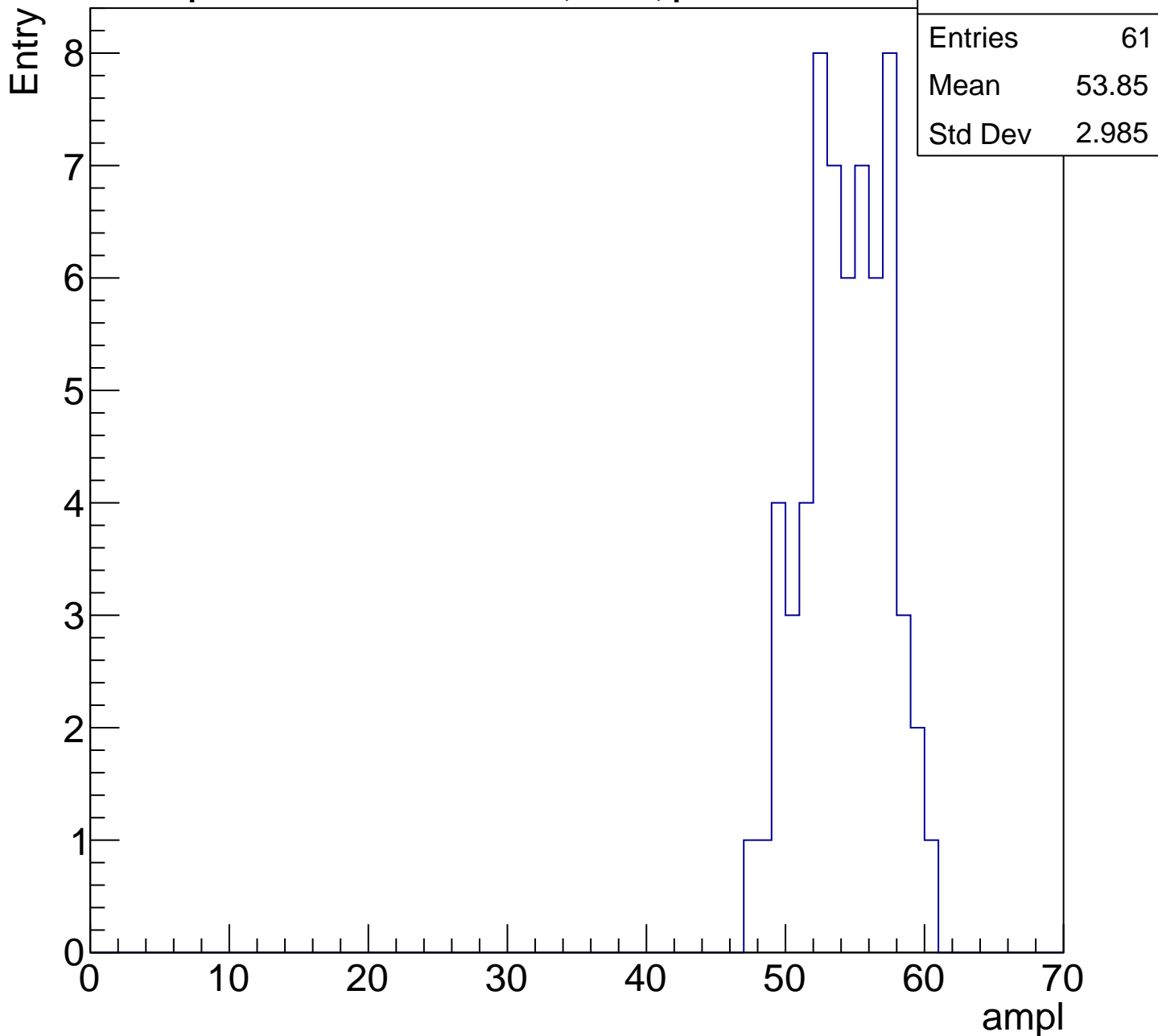
Entry

Entries	52
Mean	48.44
Std Dev	2.983



B0L002S, U5-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1

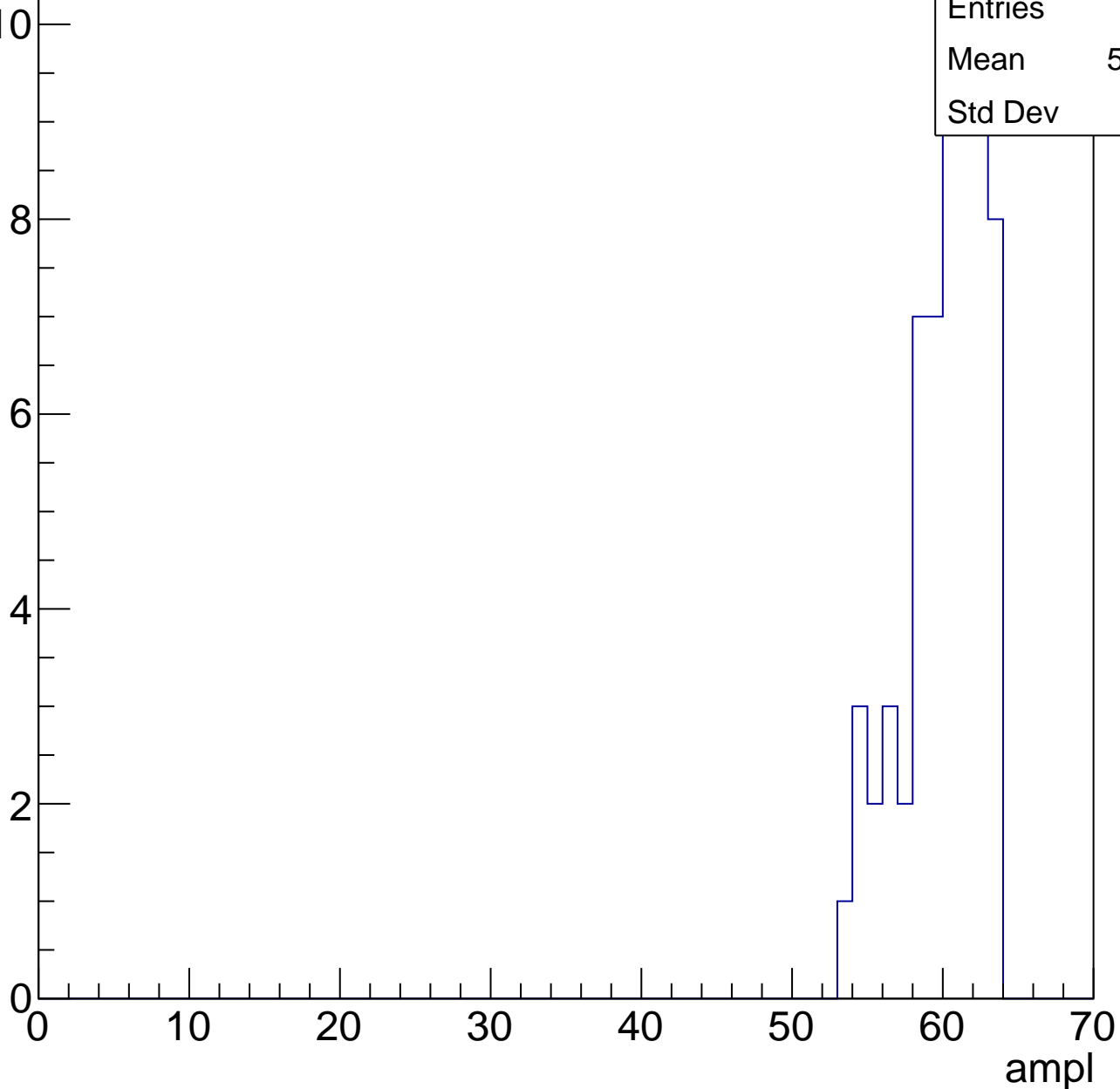


B0L002S, U5-ch48, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

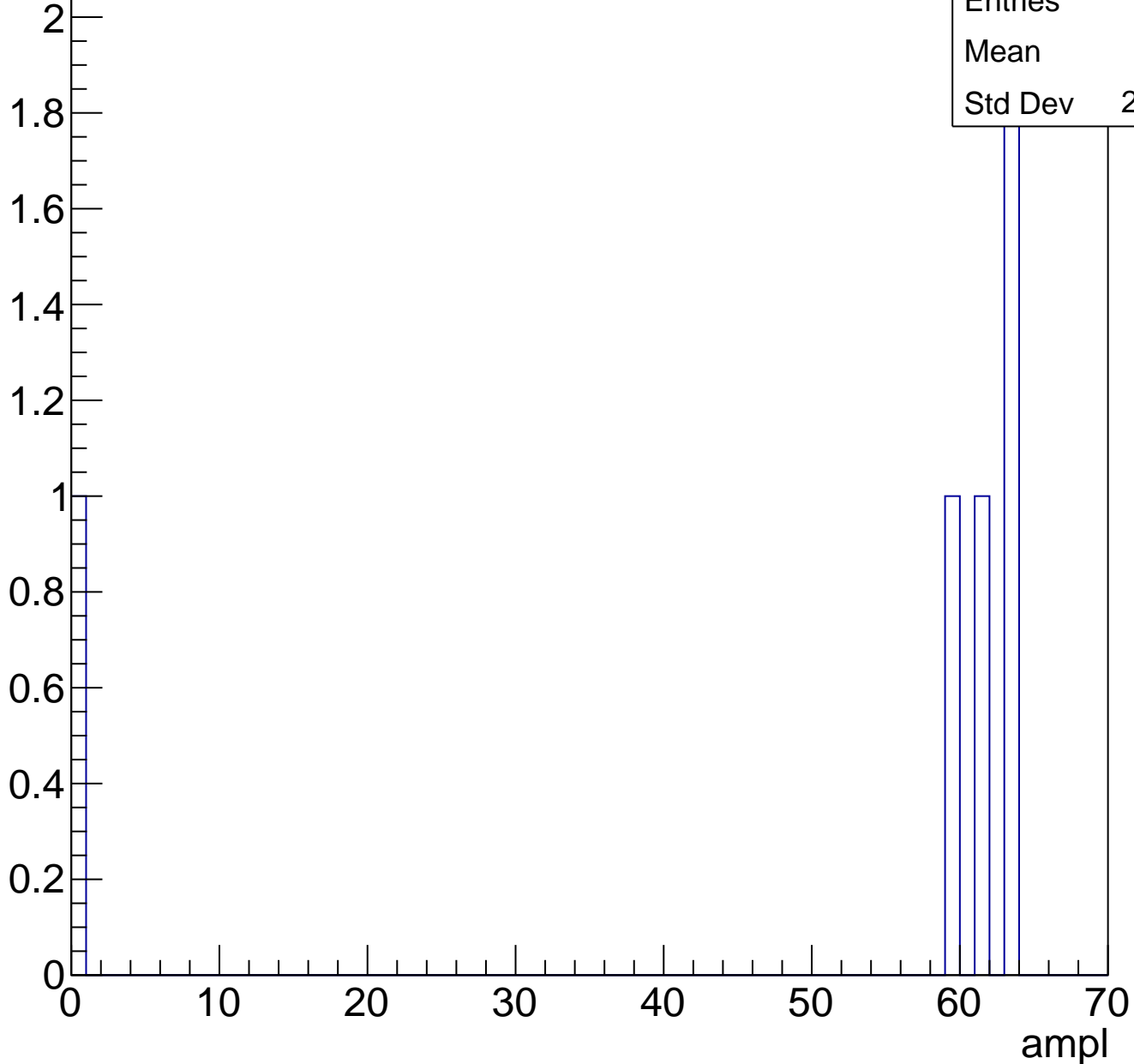
Entries	61
Mean	59.66
Std Dev	2.63



B0L002S, U5-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch49, adc0

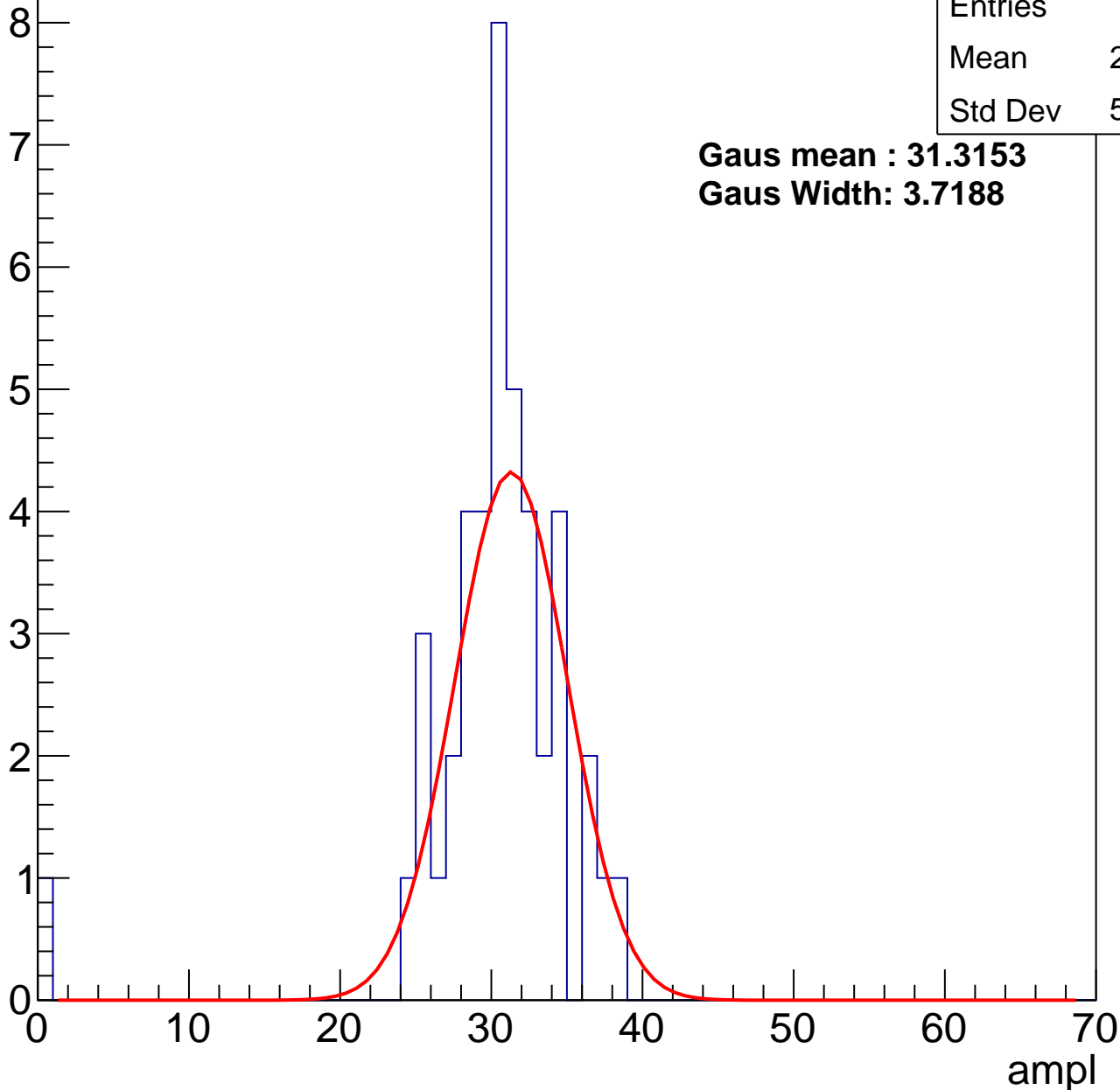
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	29.74
Std Dev	5.604

Gaus mean : 31.3153

Gaus Width: 3.7188



B0L002S, U5-ch49, adc1

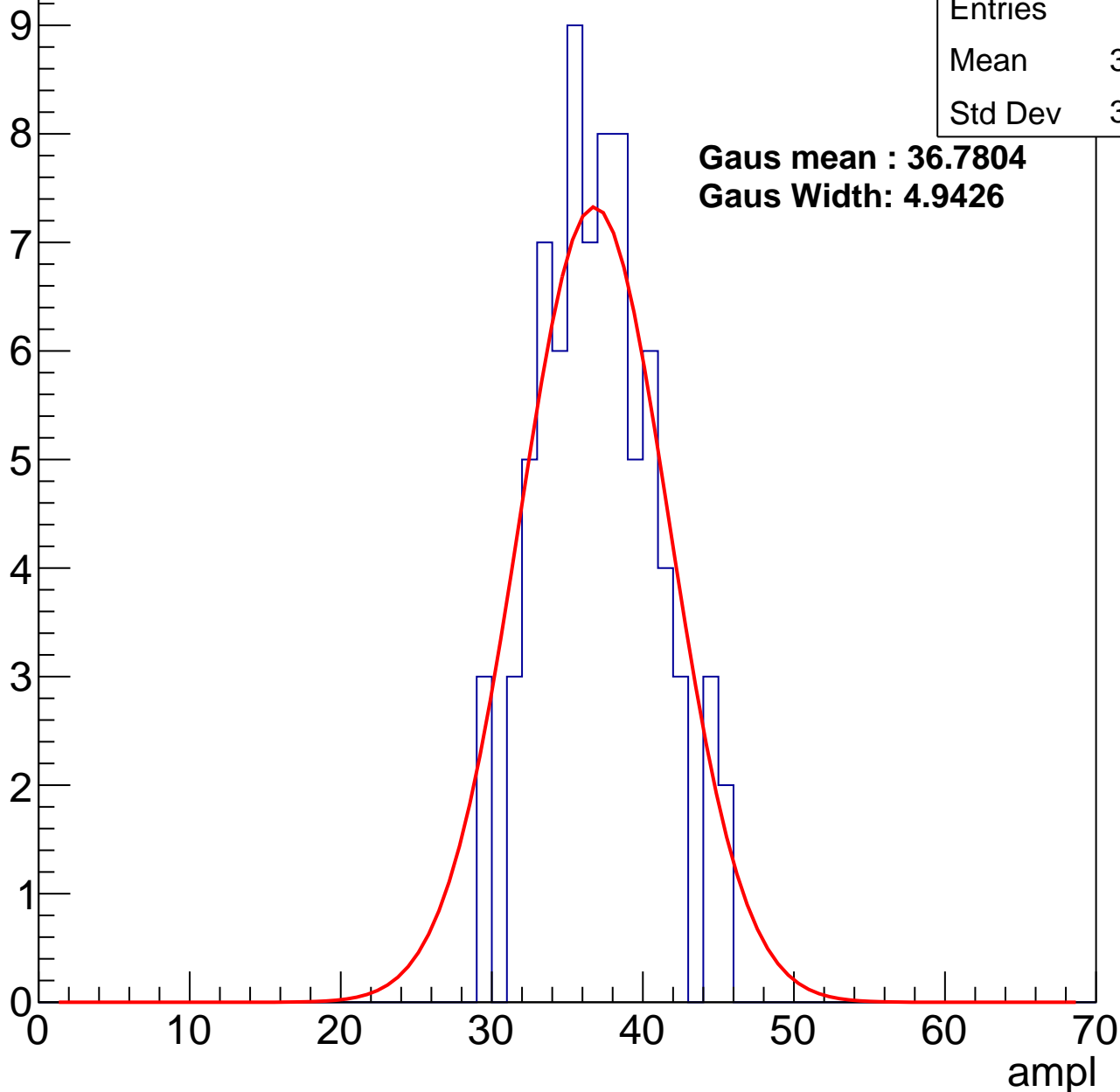
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	36.57
Std Dev	3.757

Gaus mean : 36.7804

Gaus Width: 4.9426



B0L002S, U5-ch49, adc2

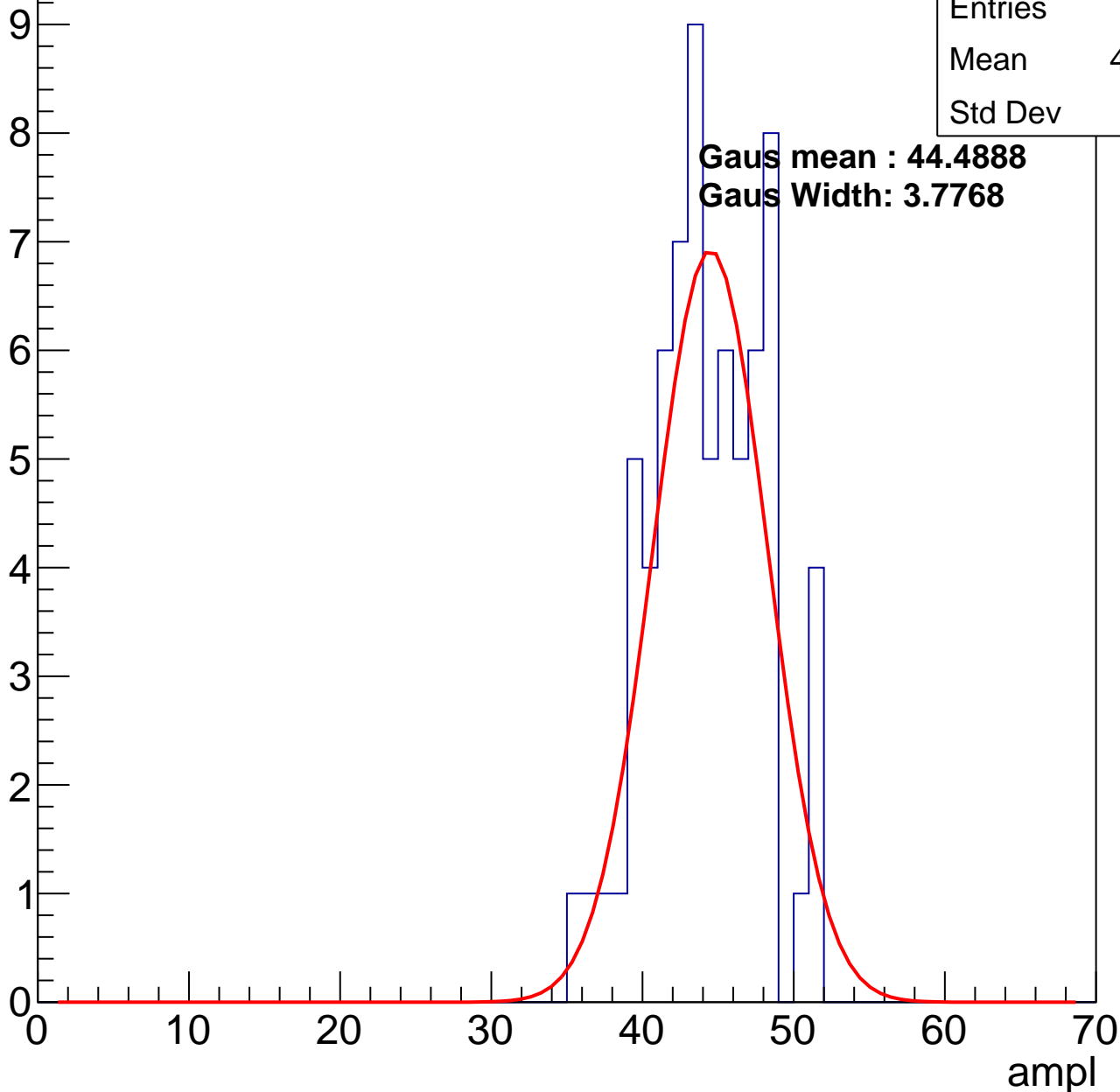
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.83
Std Dev	3.68

Gaus mean : 44.4888

Gaus Width: 3.7768



B0L002S, U5-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

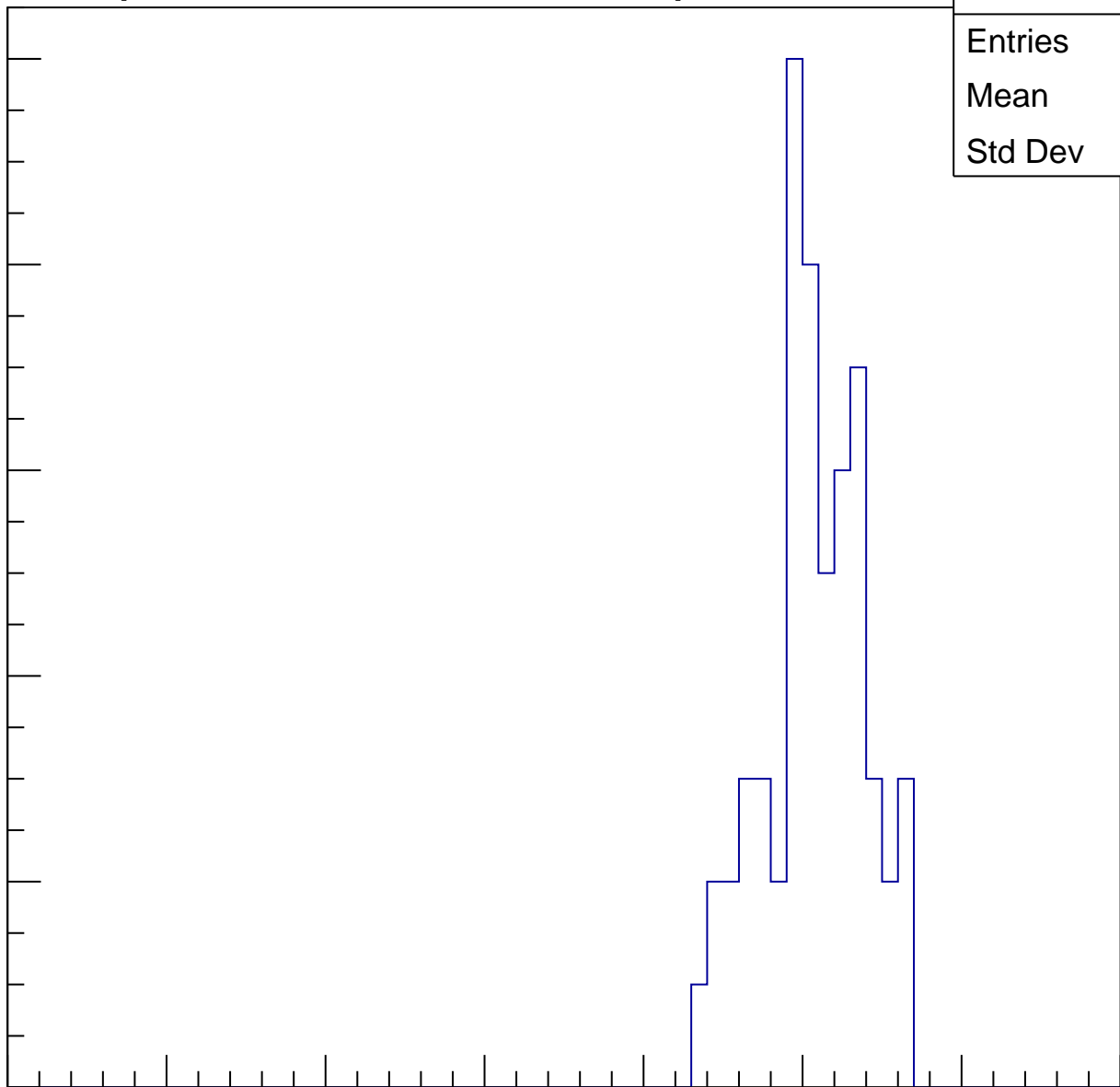
Entries	57
Mean	50.25
Std Dev	3.13

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

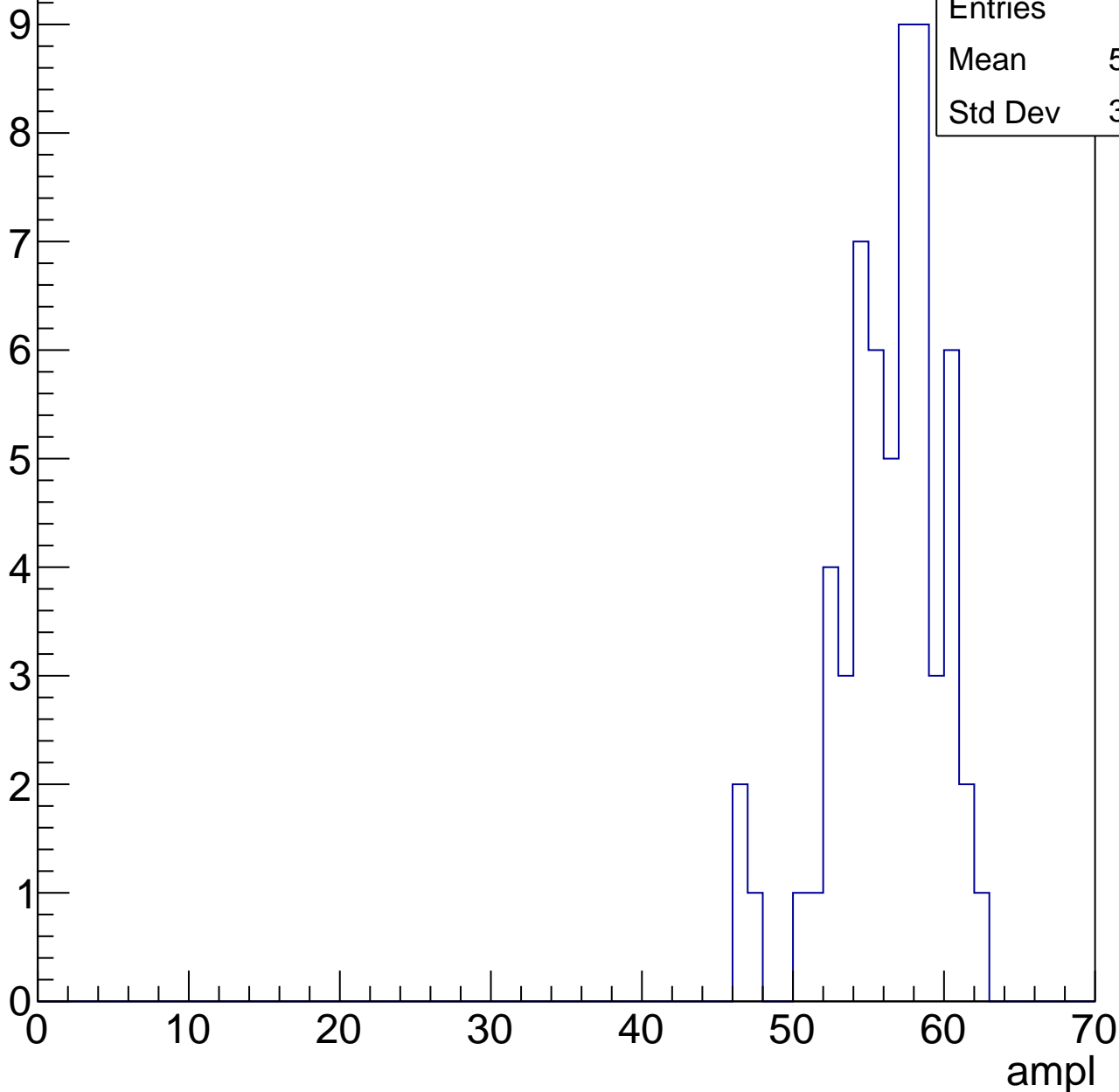


B0L002S, U5-ch49, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

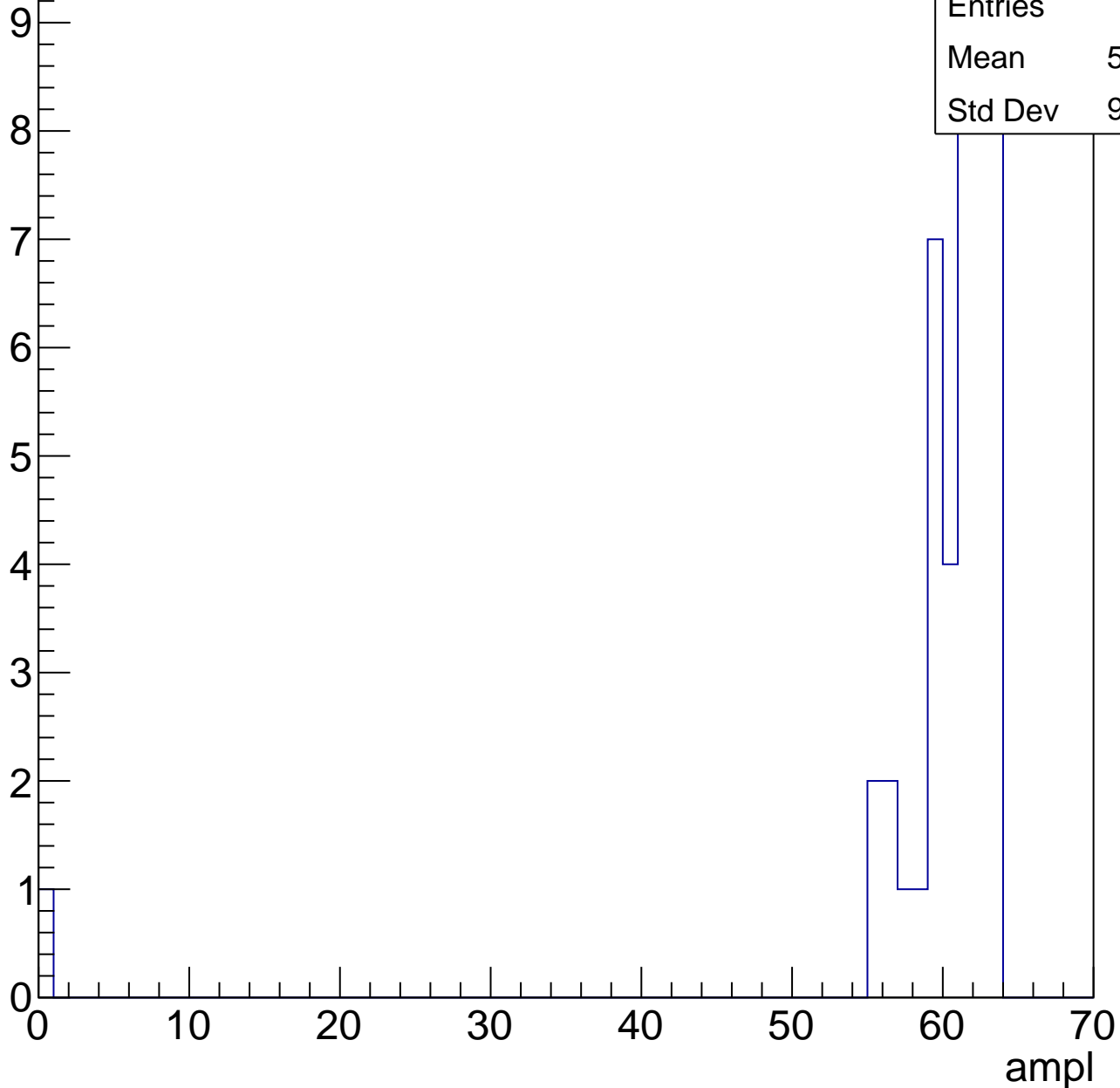
Entries	60
Mean	55.85
Std Dev	3.463



B0L002S, U5-ch49, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch50, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	30.67
Std Dev	4.73

Gaus mean : 31.1392

Gaus Width: 4.0206

Entry

10
8
6
4
2
0

ampl

0

10

20

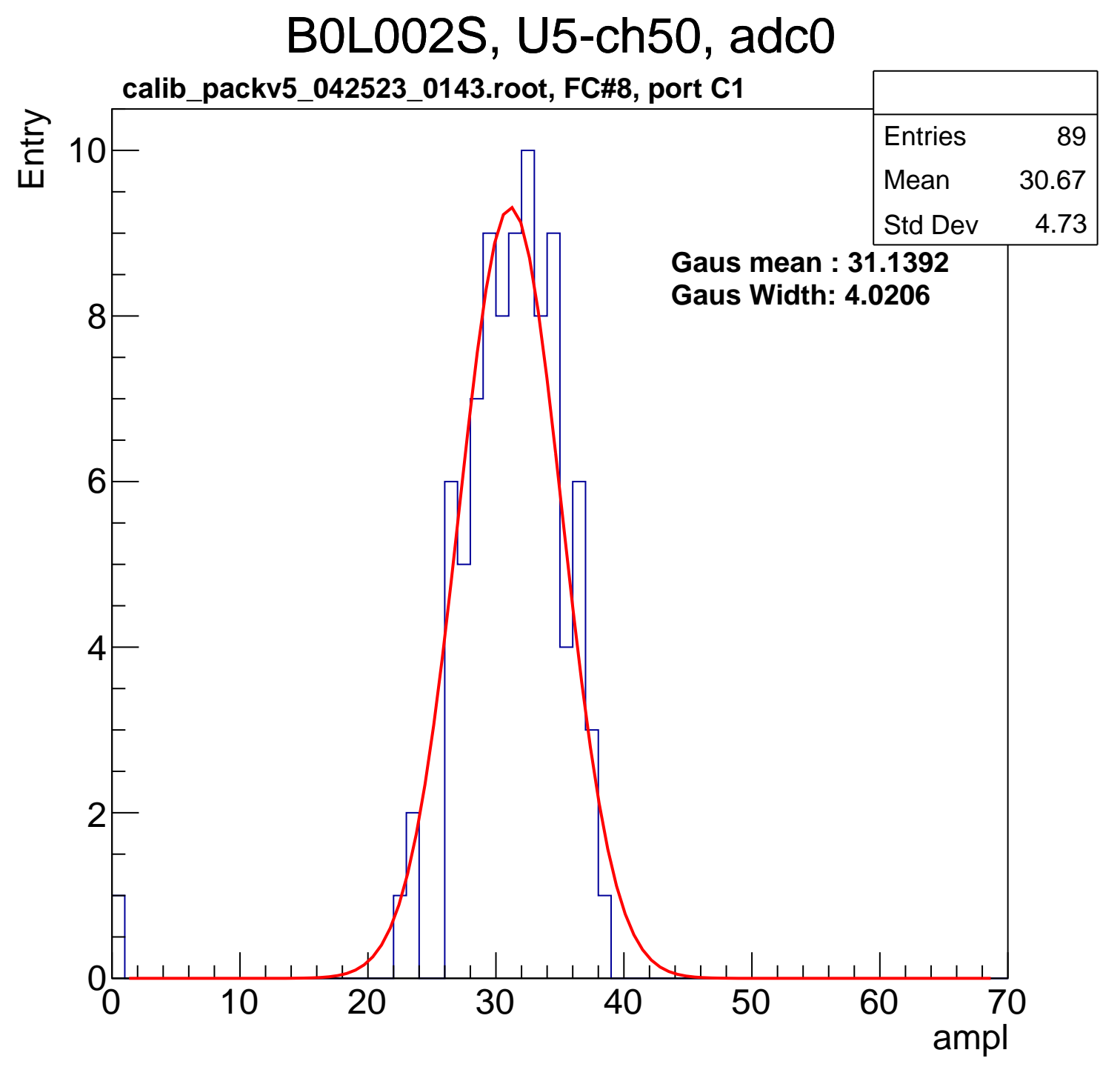
30

40

50

60

70



B0L002S, U5-ch50, adc1

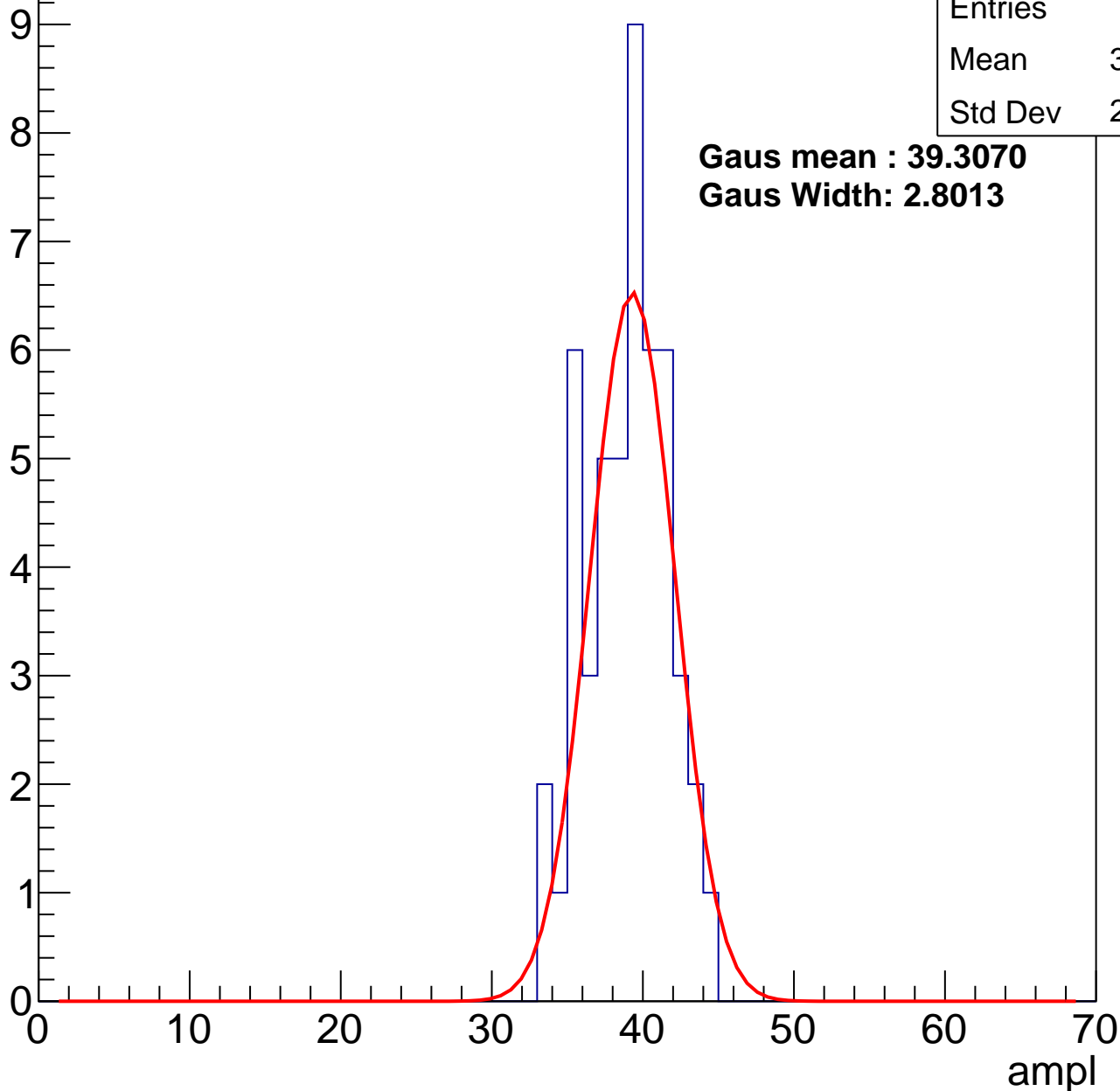
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	38.49
Std Dev	2.658

Gaus mean : 39.3070

Gaus Width: 2.8013



B0L002S, U5-ch50, adc2

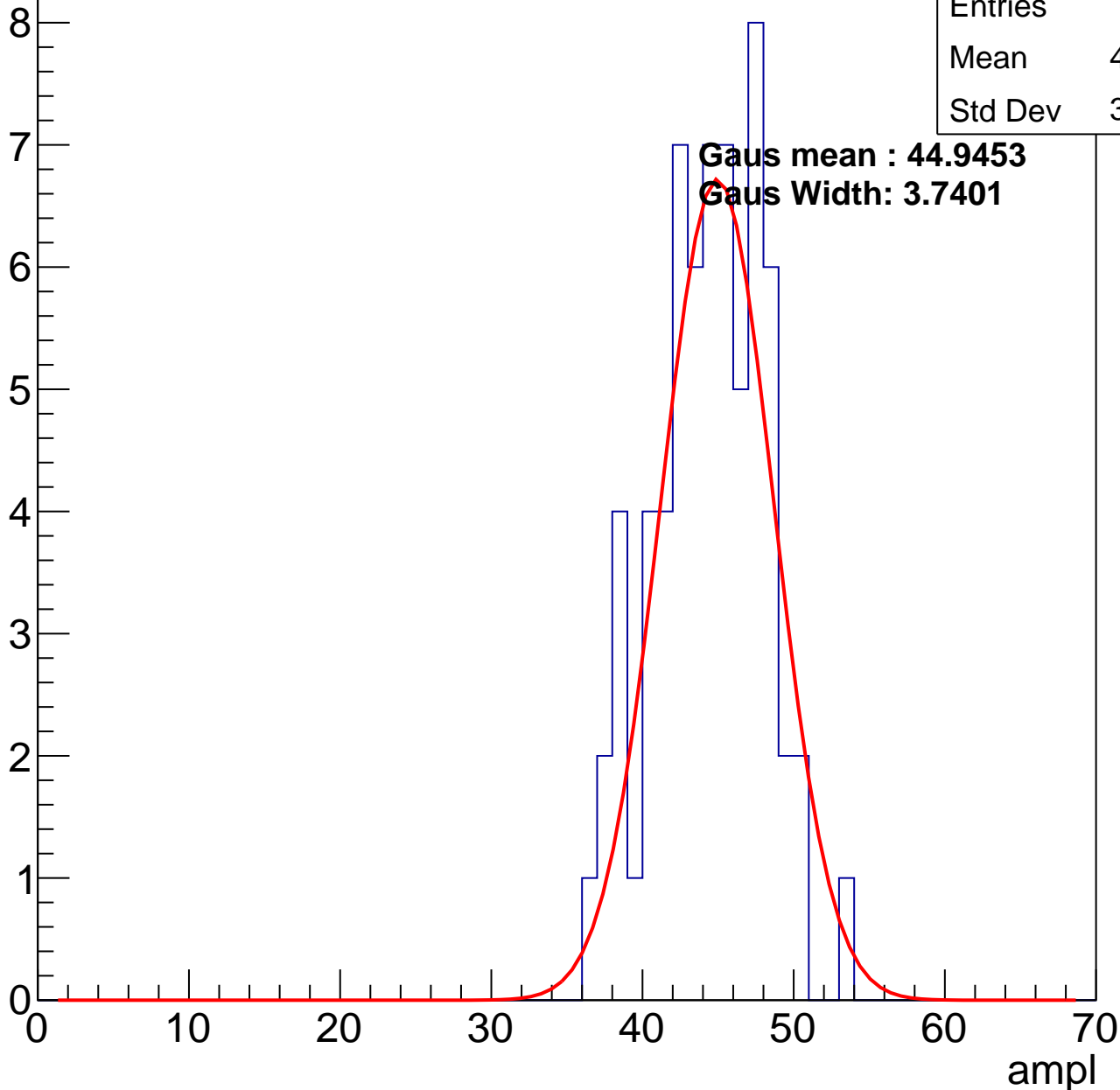
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.96
Std Dev	3.589

Gaus mean : 44.9453

Gaus Width: 3.7401



B0L002S, U5-ch50, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	50.56
Std Dev	3.551

Entry

10
8
6
4
2
0

ampl

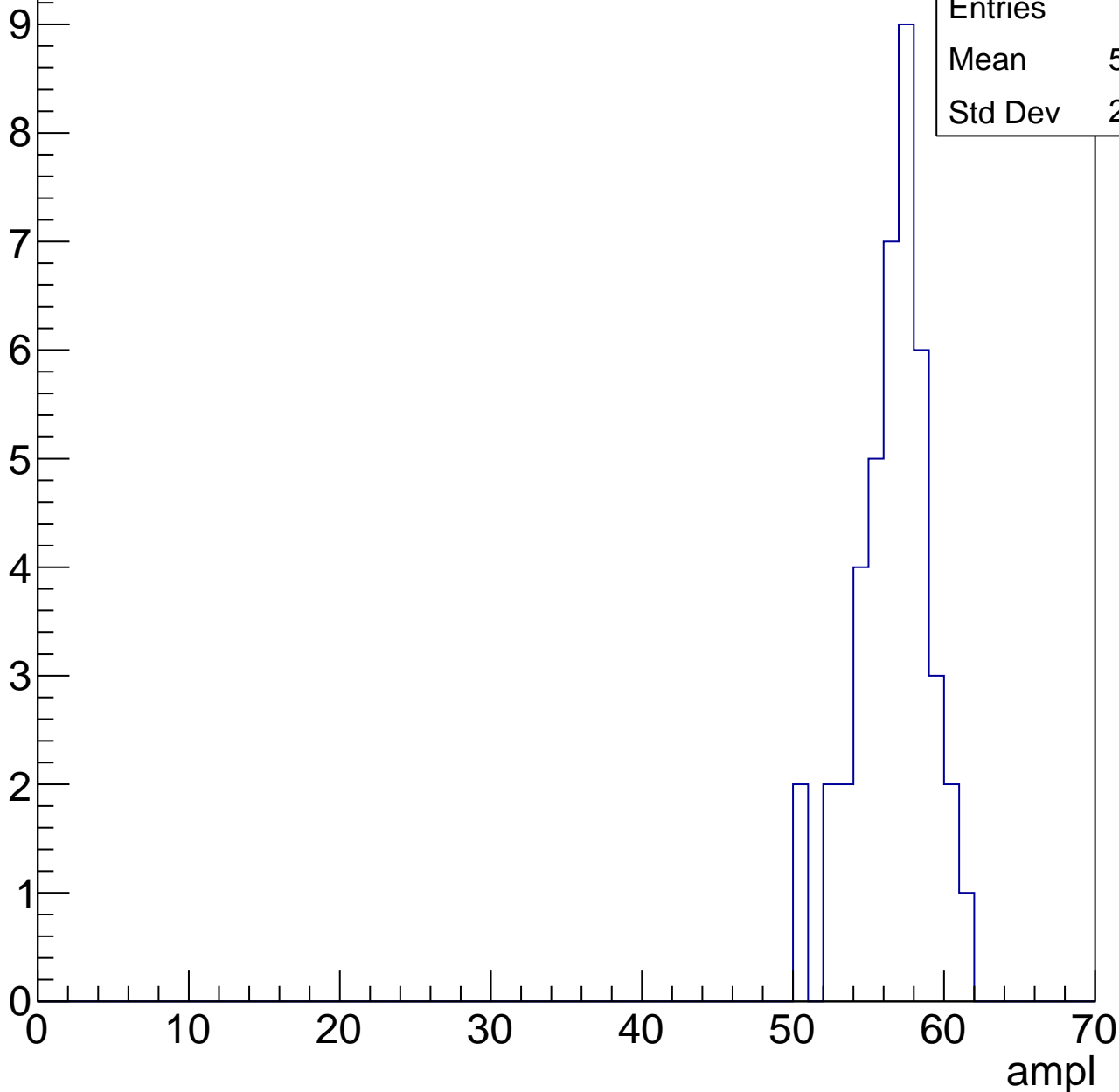
0 10 20 30 40 50 60 70

B0L002S, U5-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	56.09
Std Dev	2.457



B0L002S, U5-ch50, adc5

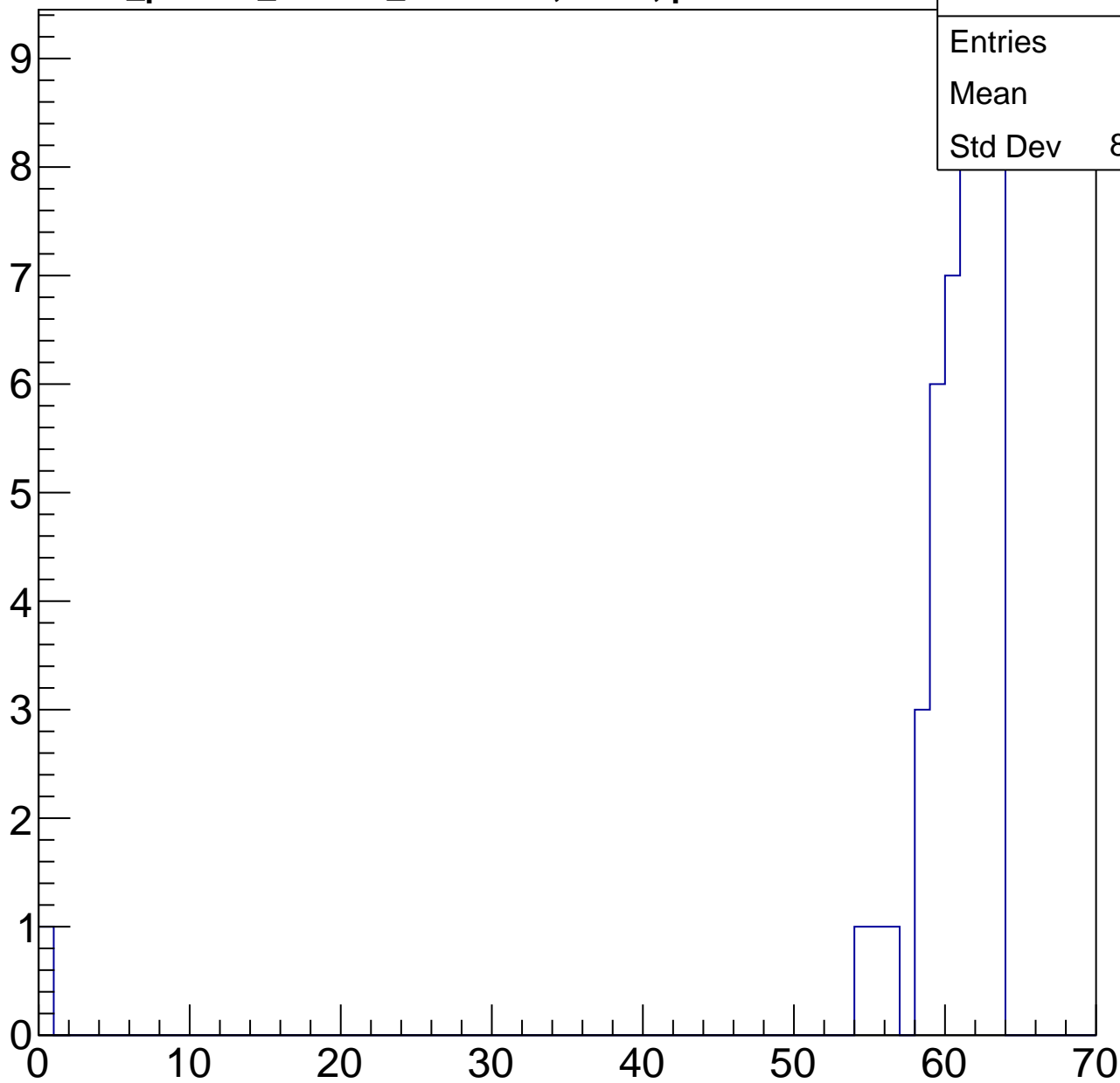
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.3
Std Dev	8.989

ampl



B0L002S, U5-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch51, adc0

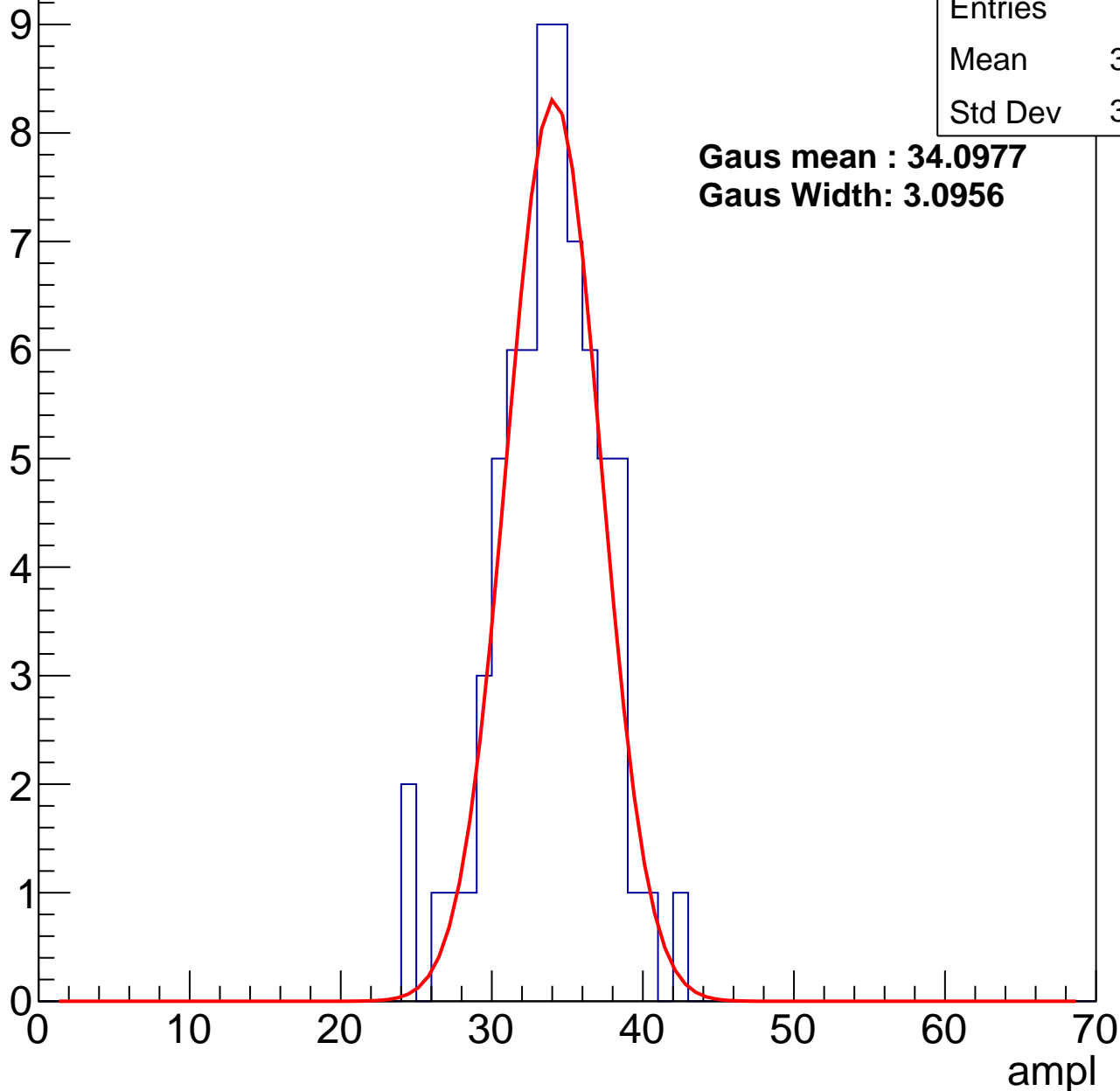
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	33.39
Std Dev	3.502

Gaus mean : 34.0977

Gaus Width: 3.0956



B0L002S, U5-ch51, adc1

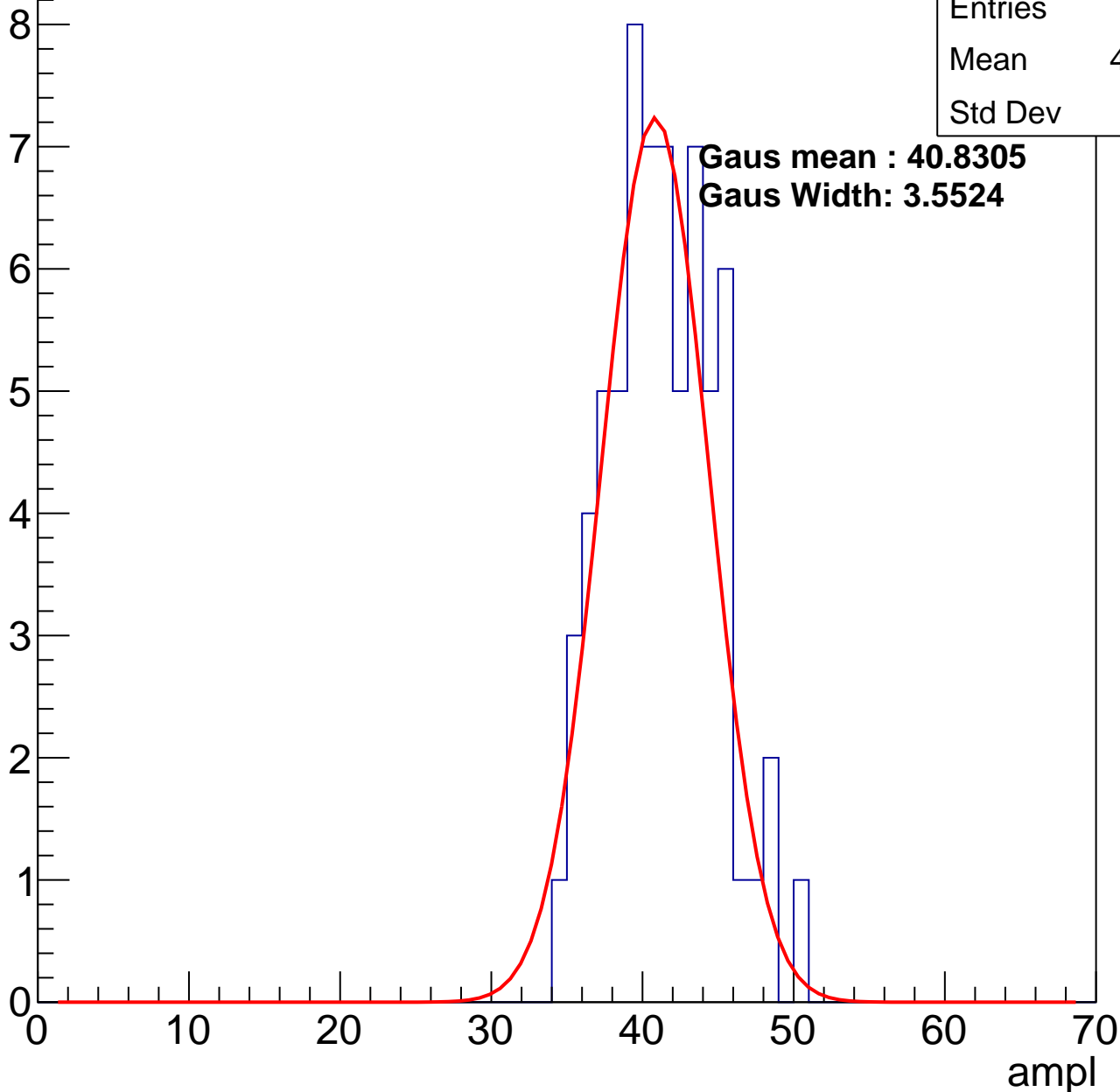
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	40.84
Std Dev	3.5

Gaus mean : 40.8305

Gaus Width: 3.5524



B0L002S, U5-ch51, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	47.2
Std Dev	3.742

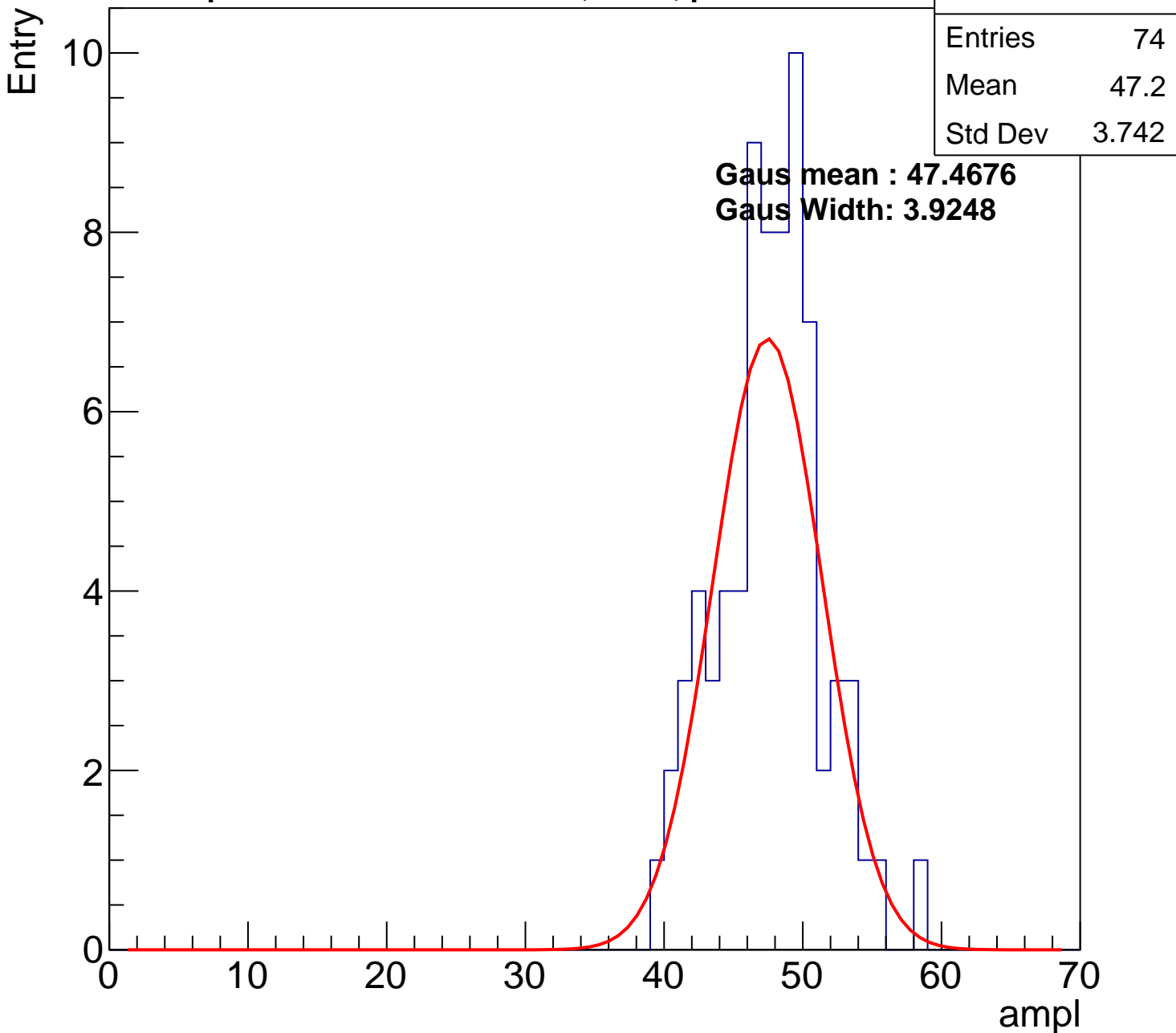
Gaus mean : 47.4676

Gaus Width: 3.9248

10
8
6
4
2
0

ampl

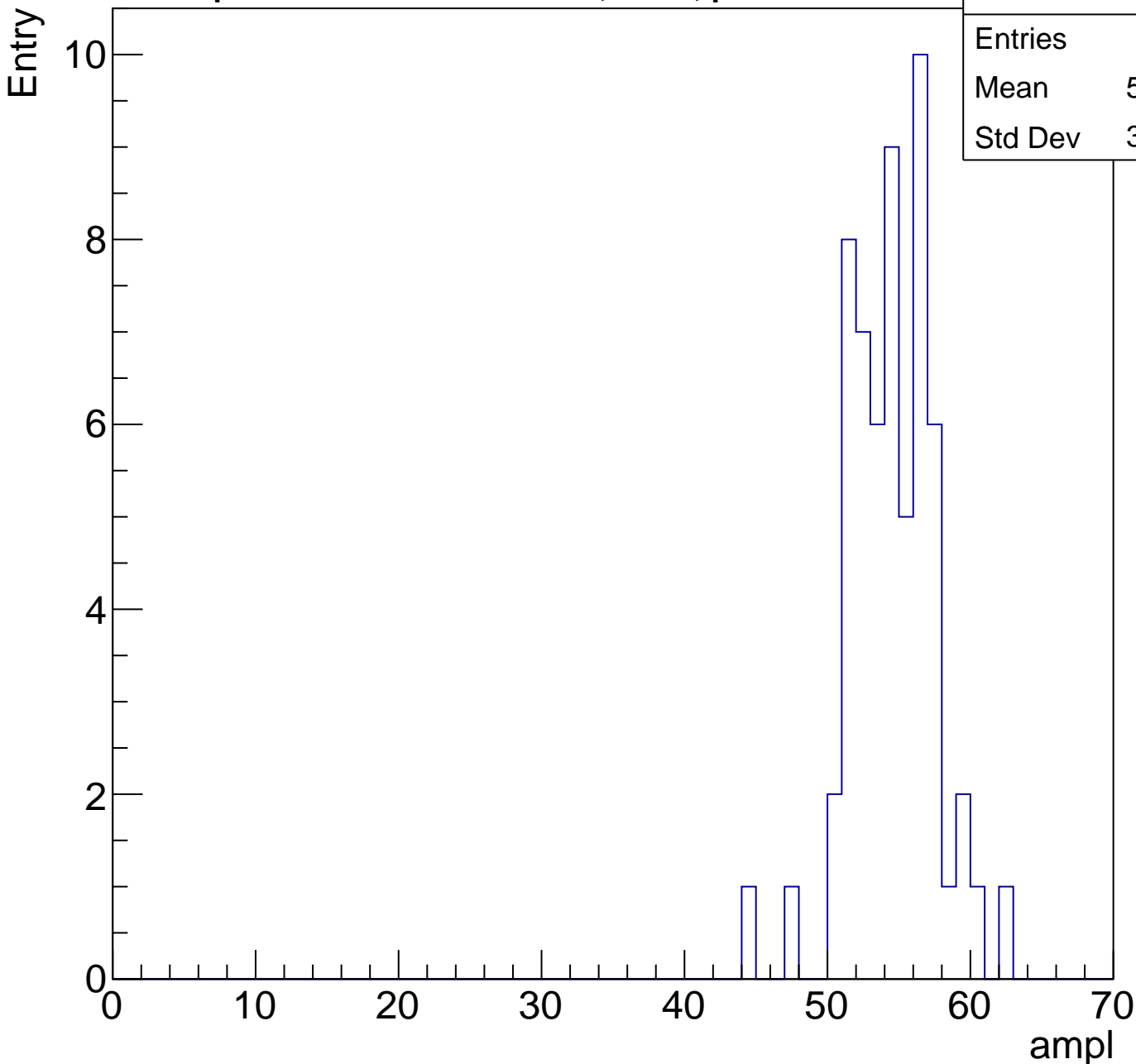
0 10 20 30 40 50 60 70



B0L002S, U5-ch51, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	54.03
Std Dev	3.033

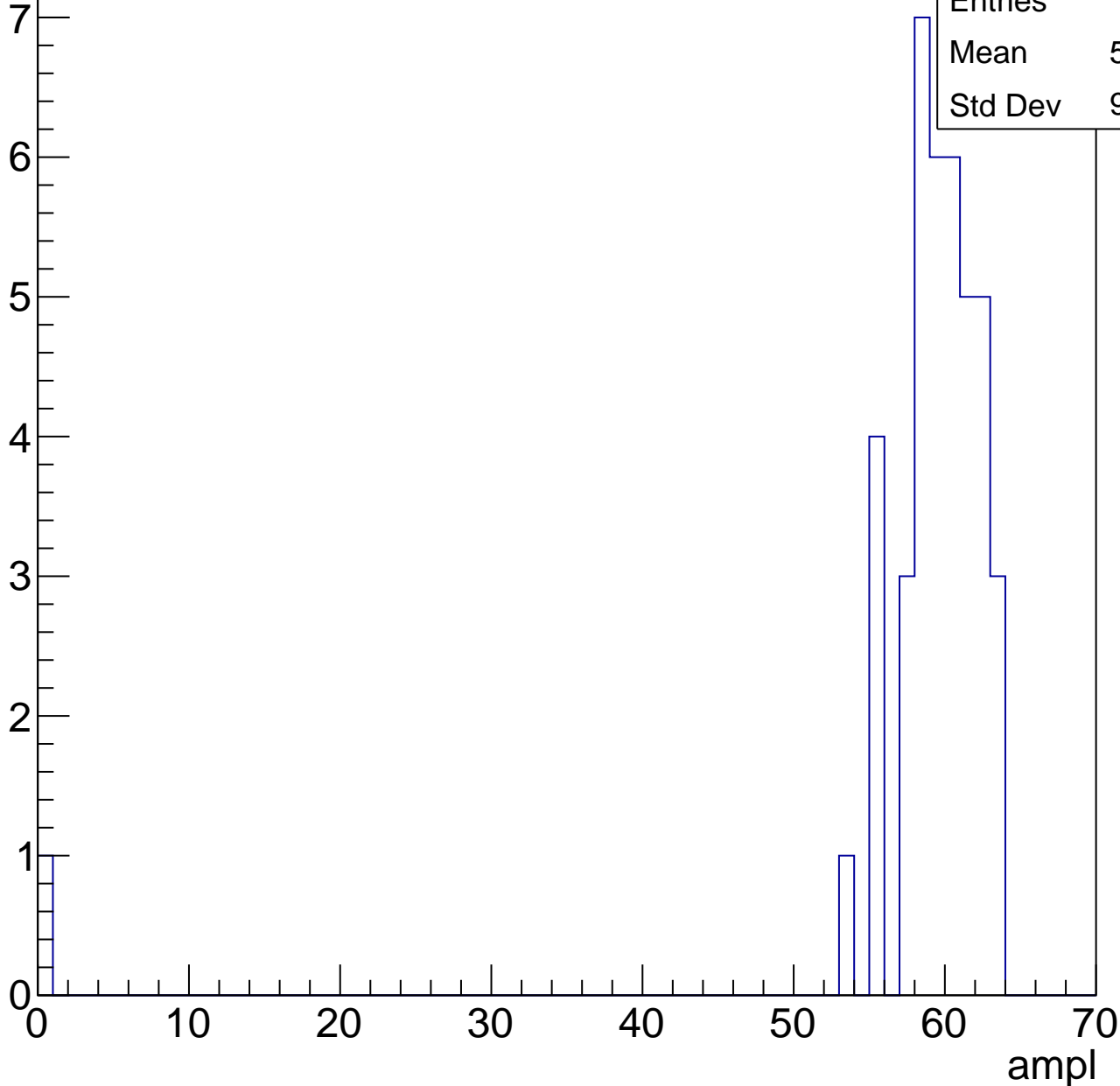


B0L002S, U5-ch51, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	57.76
Std Dev	9.442

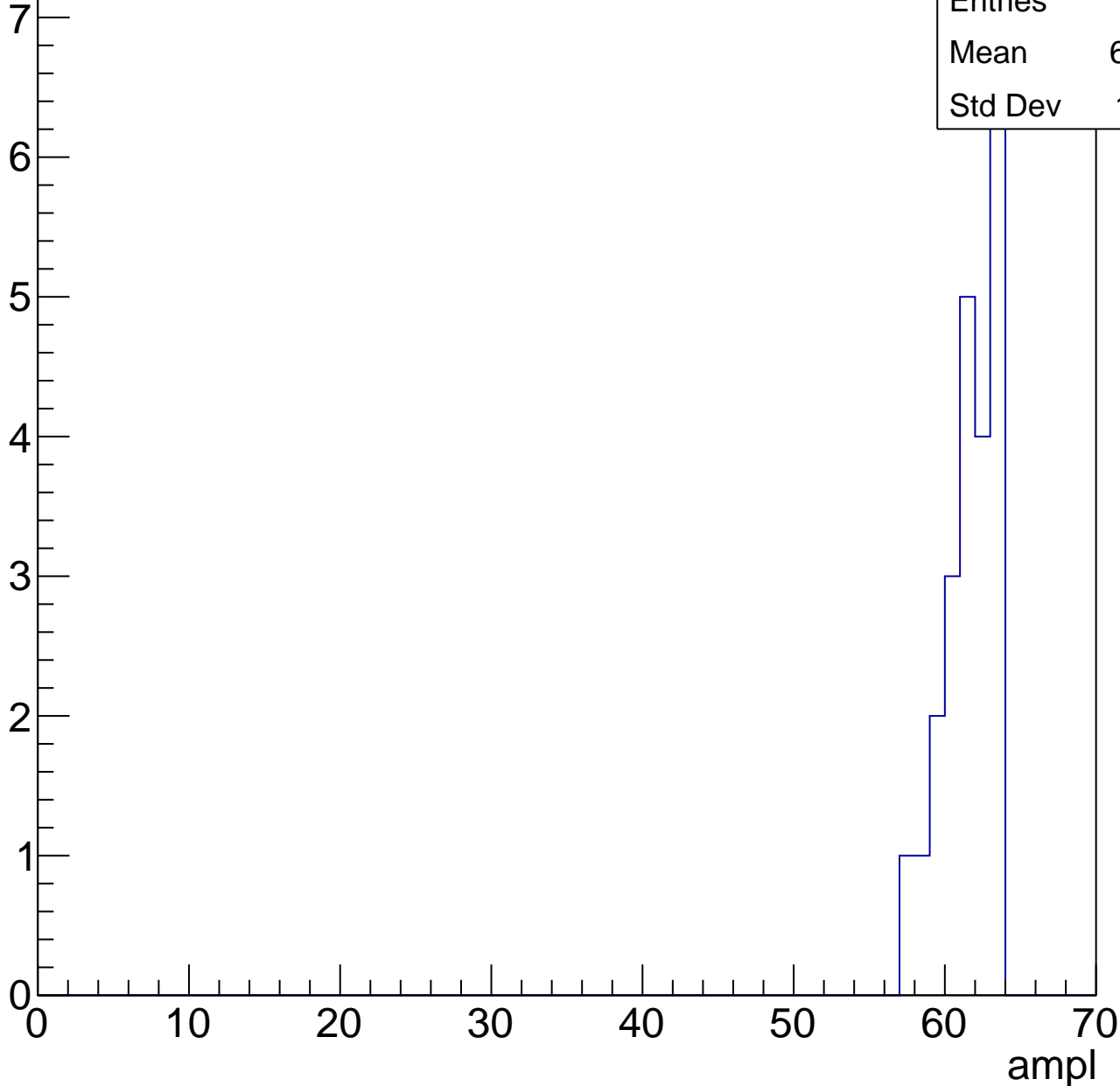


B0L002S, U5-ch51, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	61.17
Std Dev	1.711



B0L002S, U5-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch52, adc0

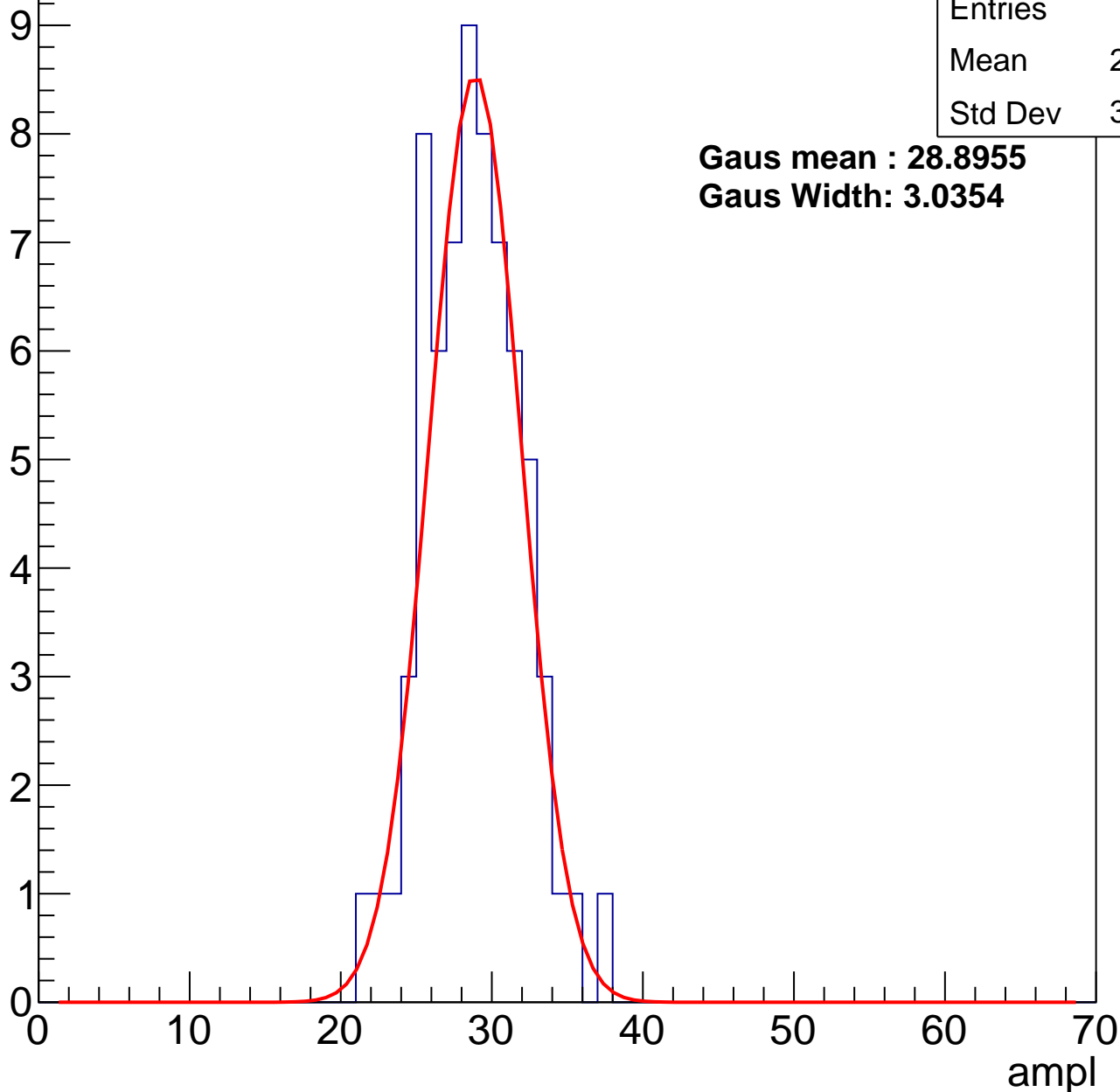
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.35
Std Dev	3.124

Gaus mean : 28.8955

Gaus Width: 3.0354



B0L002S, U5-ch52, adc1

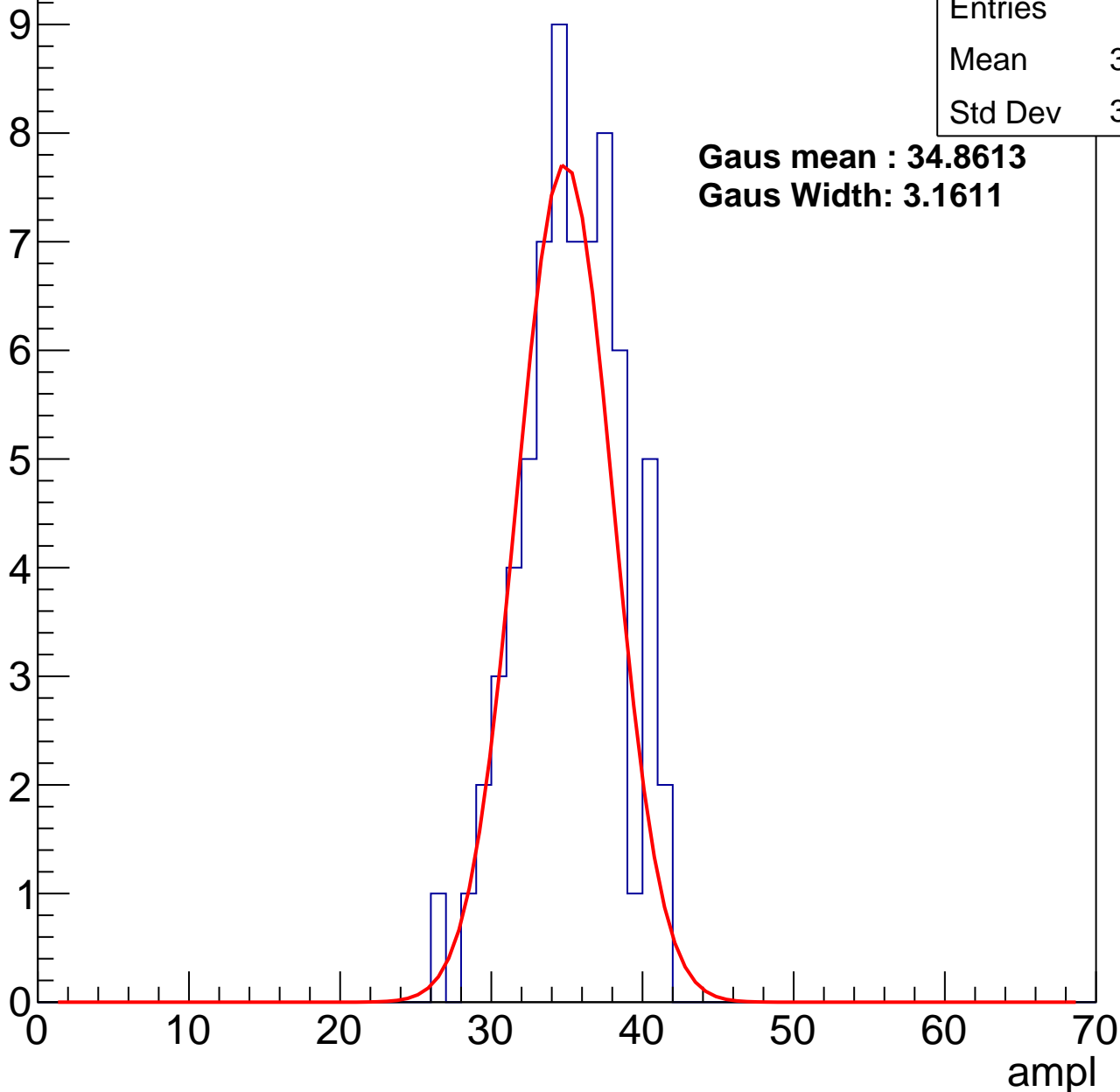
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	34.78
Std Dev	3.267

Gaus mean : 34.8613

Gaus Width: 3.1611



B0L002S, U5-ch52, adc2

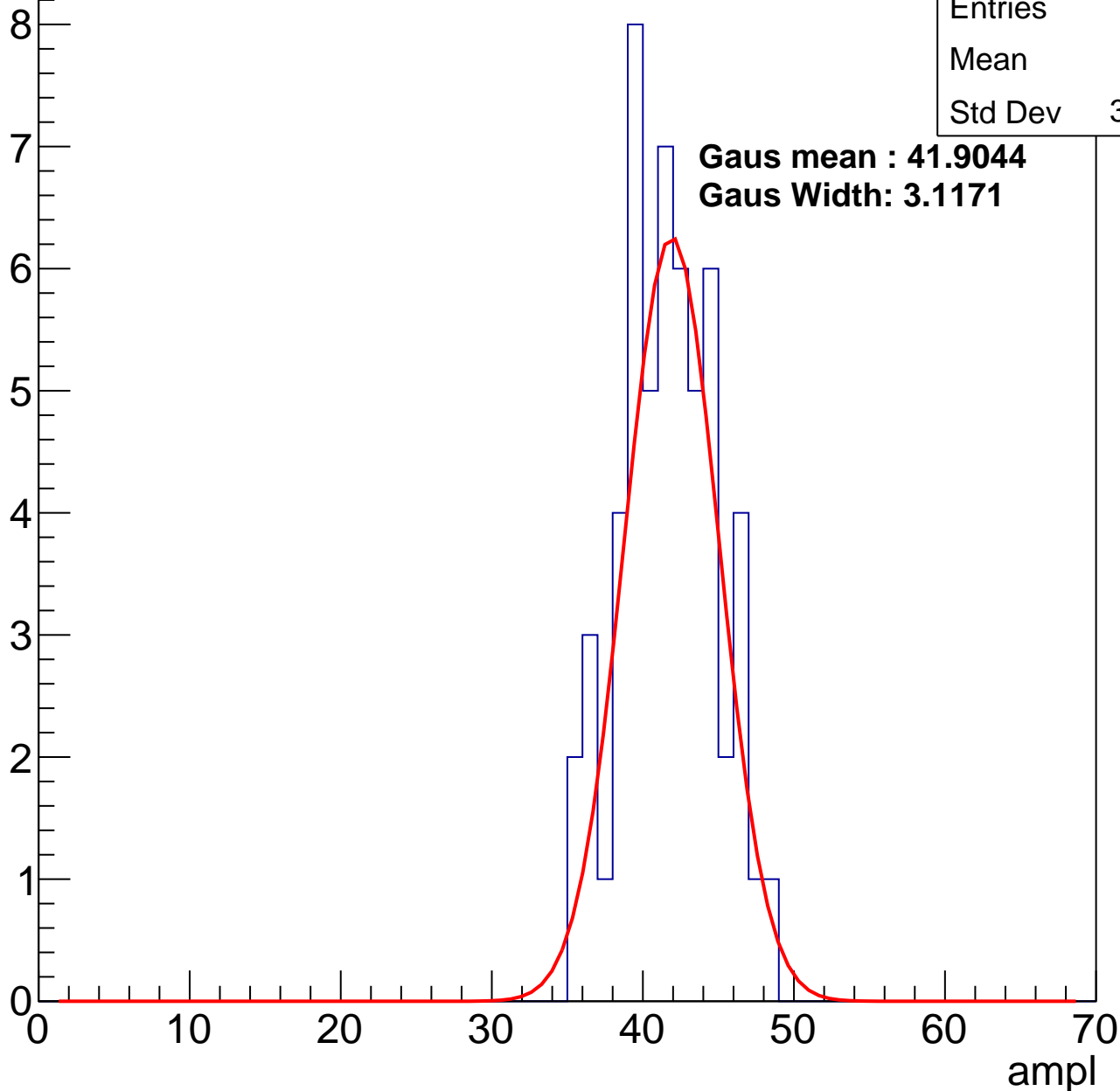
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	41.2
Std Dev	3.107

Gaus mean : 41.9044

Gaus Width: 3.1171

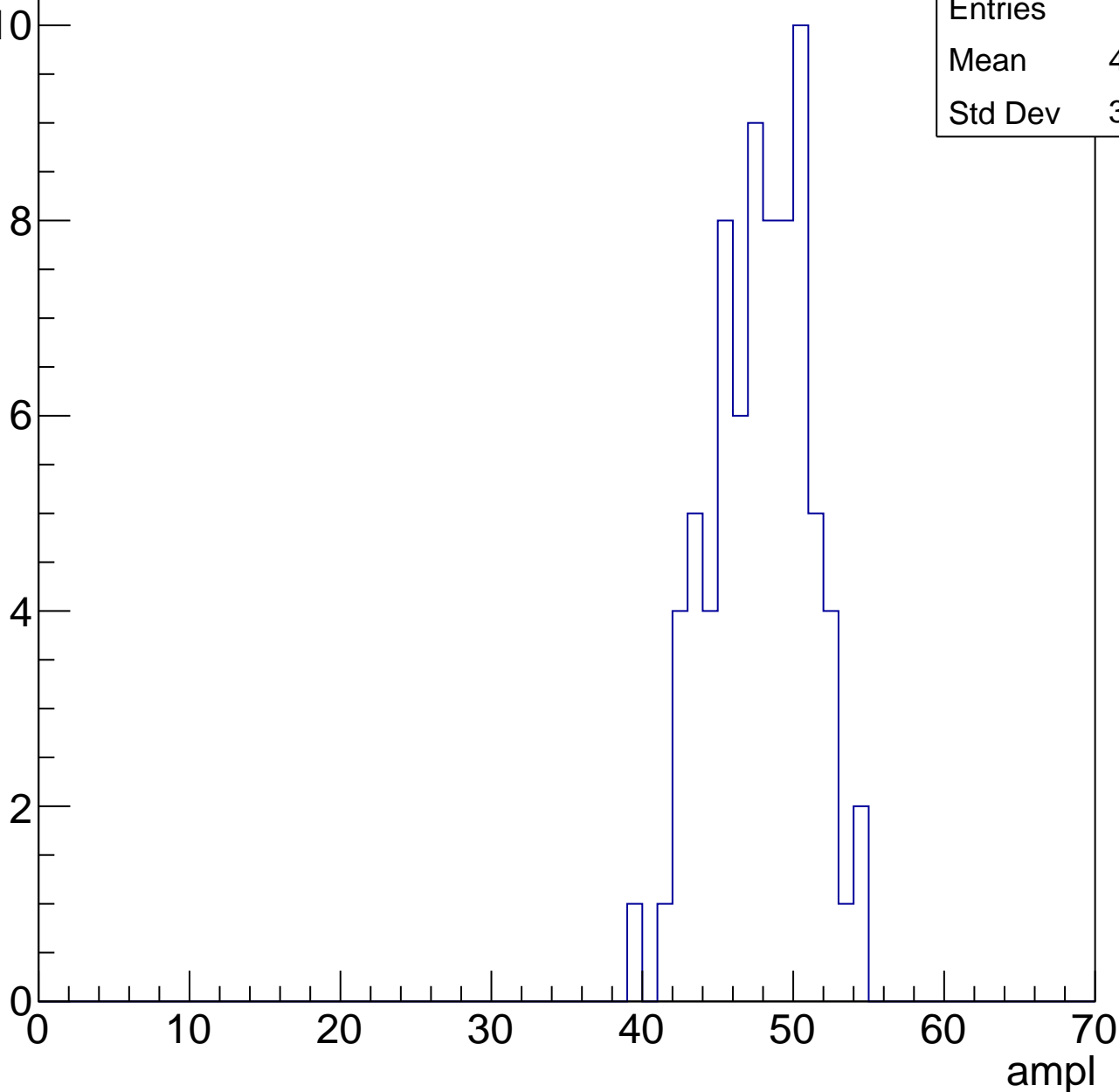


B0L002S, U5-ch52, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	47.34
Std Dev	3.222

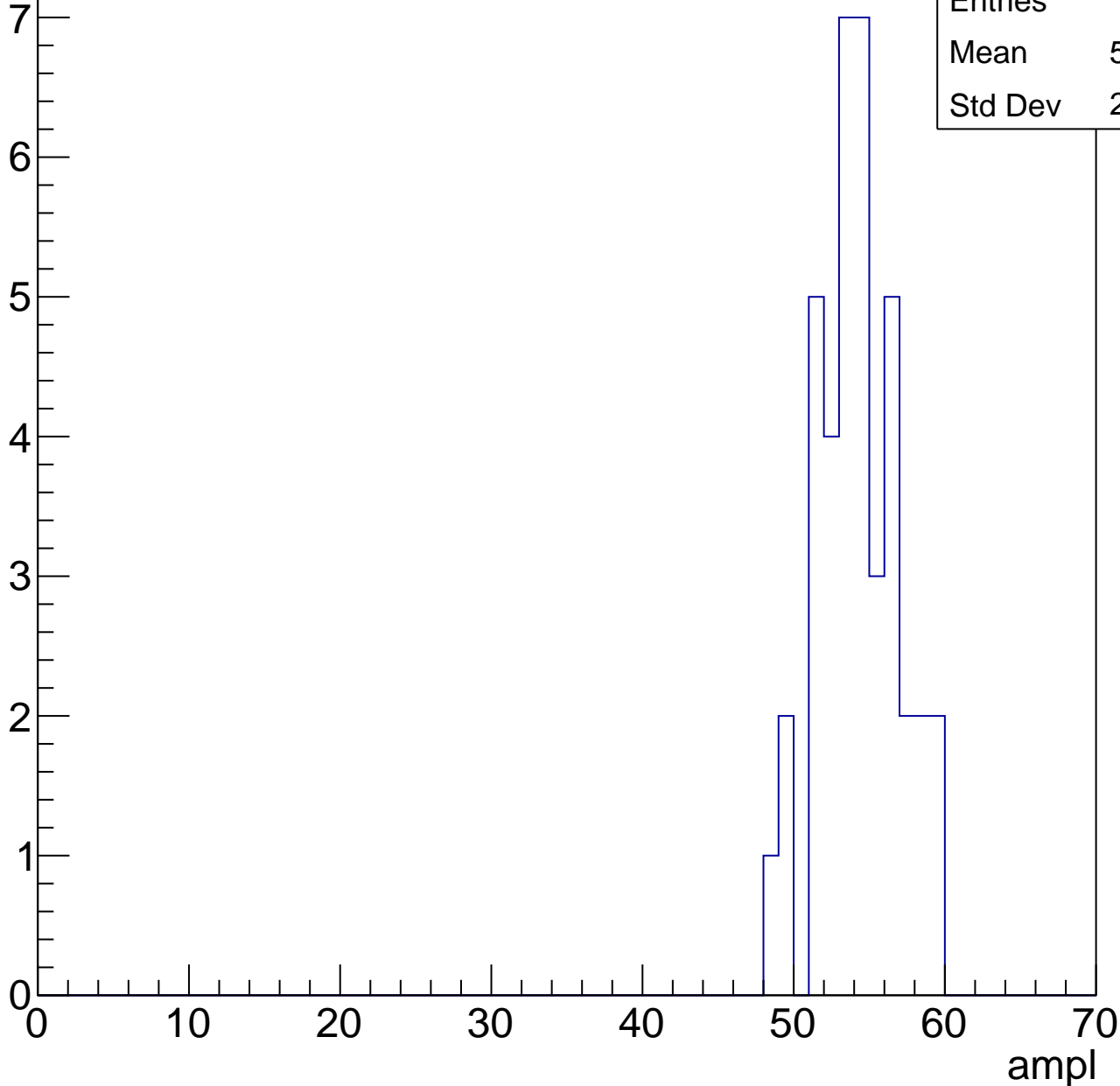


B0L002S, U5-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	53.77
Std Dev	2.622



B0L002S, U5-ch52, adc5

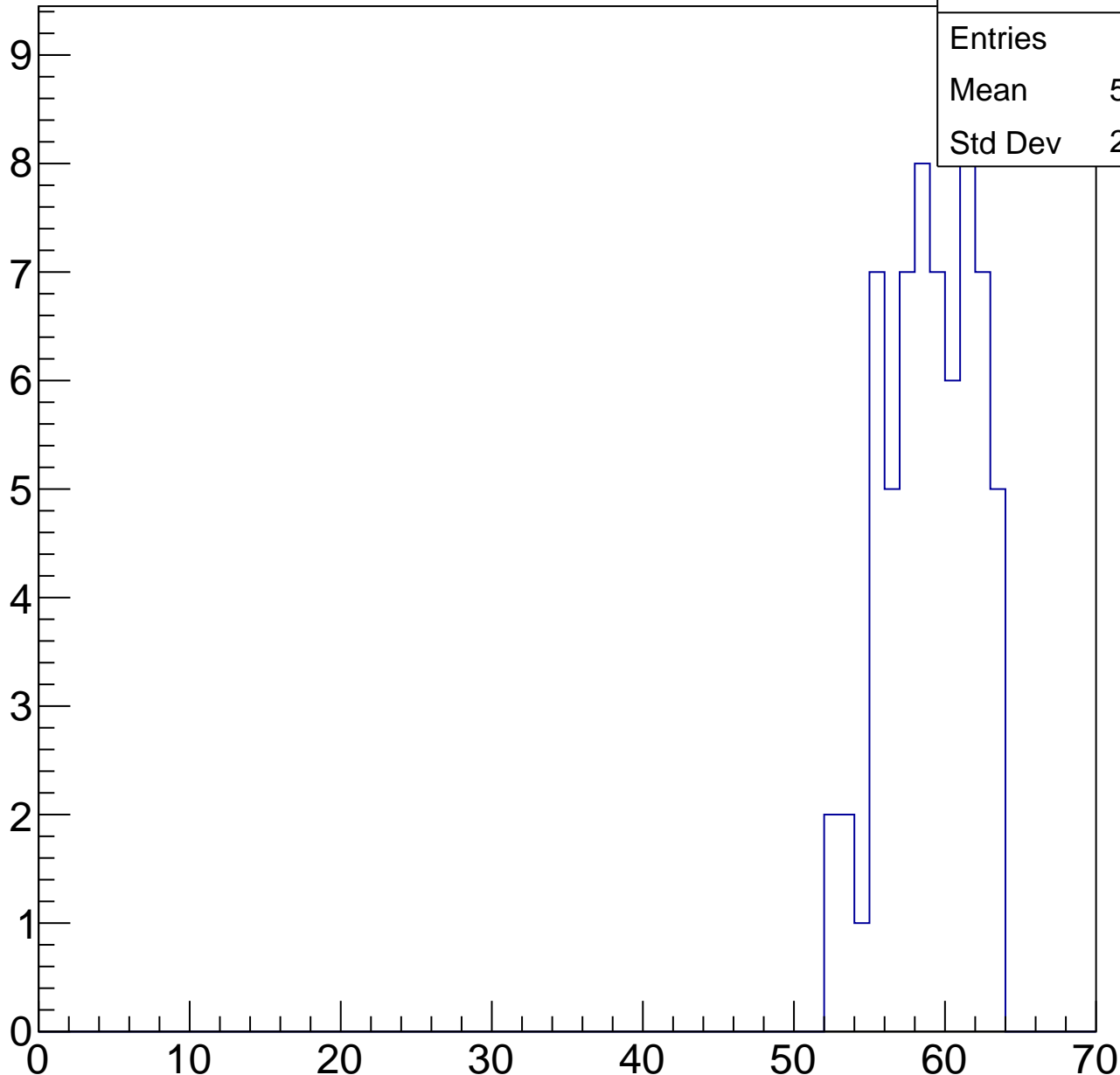
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	58.53
Std Dev	2.909

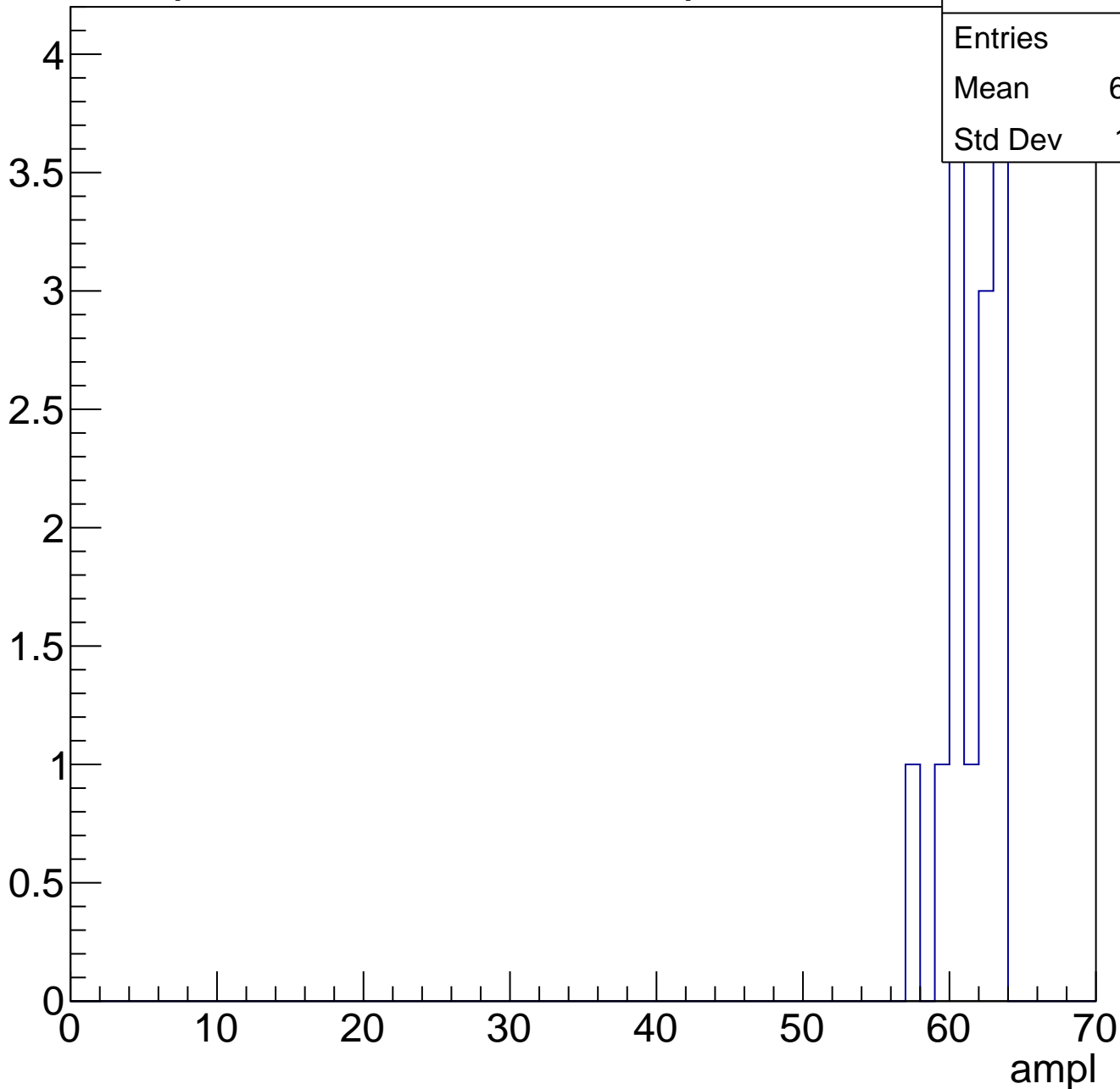
ampl



B0L002S, U5-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

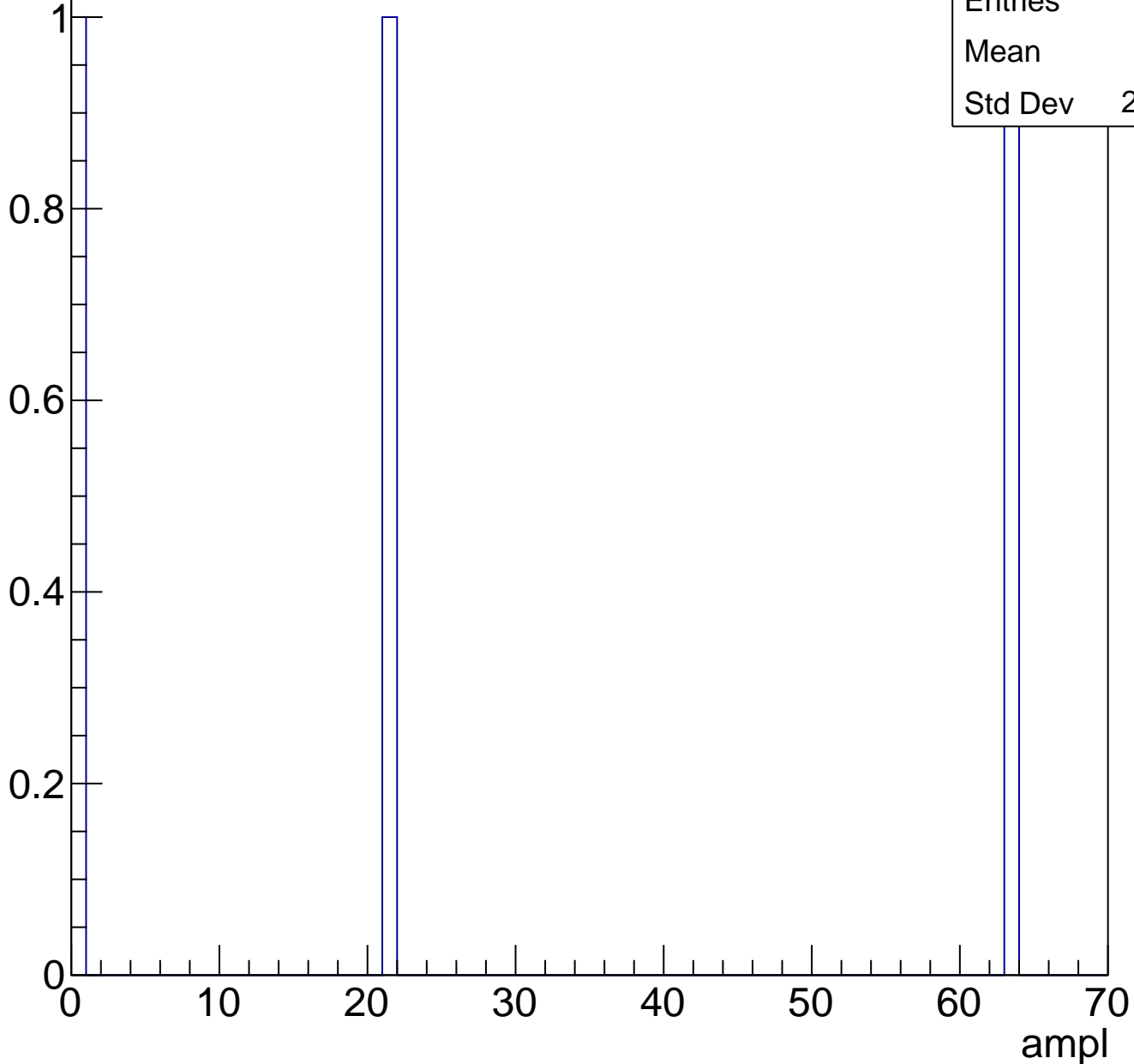
Entry



B0L002S, U5-ch52, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	28
Std Dev	26.19

B0L002S, U5-ch53, adc0

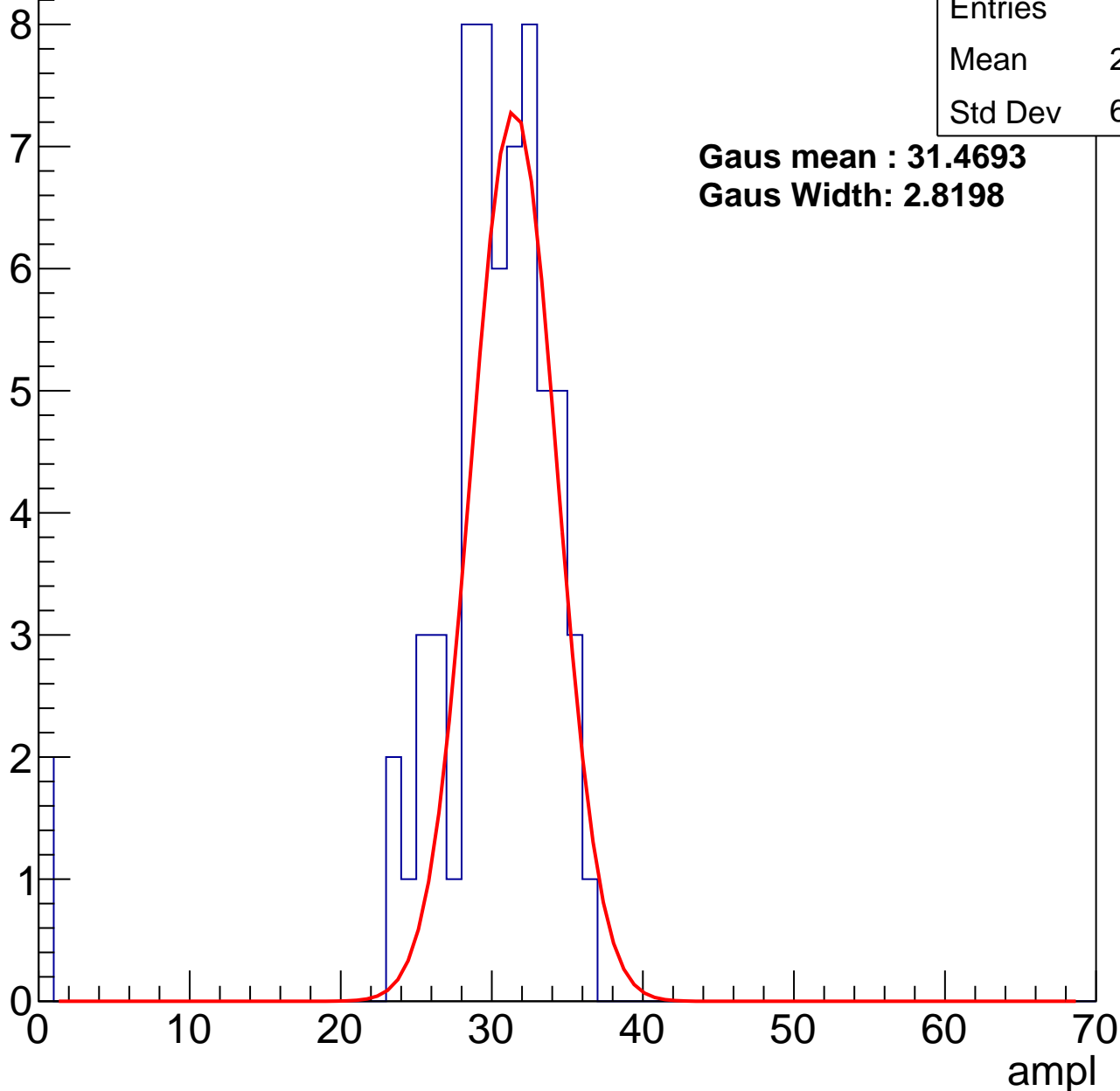
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.13
Std Dev	6.085

Gaus mean : 31.4693

Gaus Width: 2.8198



B0L002S, U5-ch53, adc1

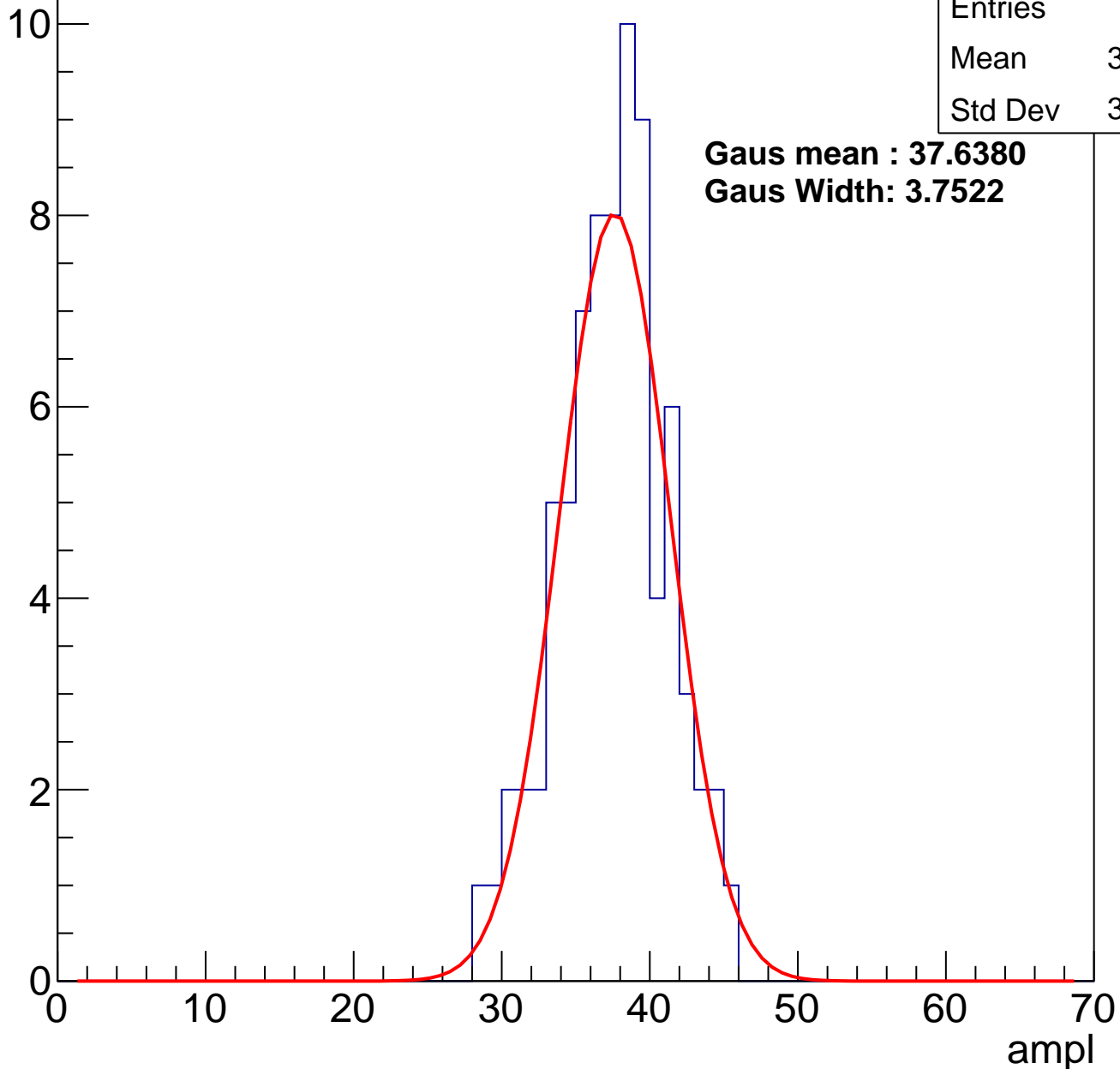
calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	37.04
Std Dev	3.604

Gaus mean : 37.6380

Gaus Width: 3.7522

Entry



B0L002S, U5-ch53, adc2

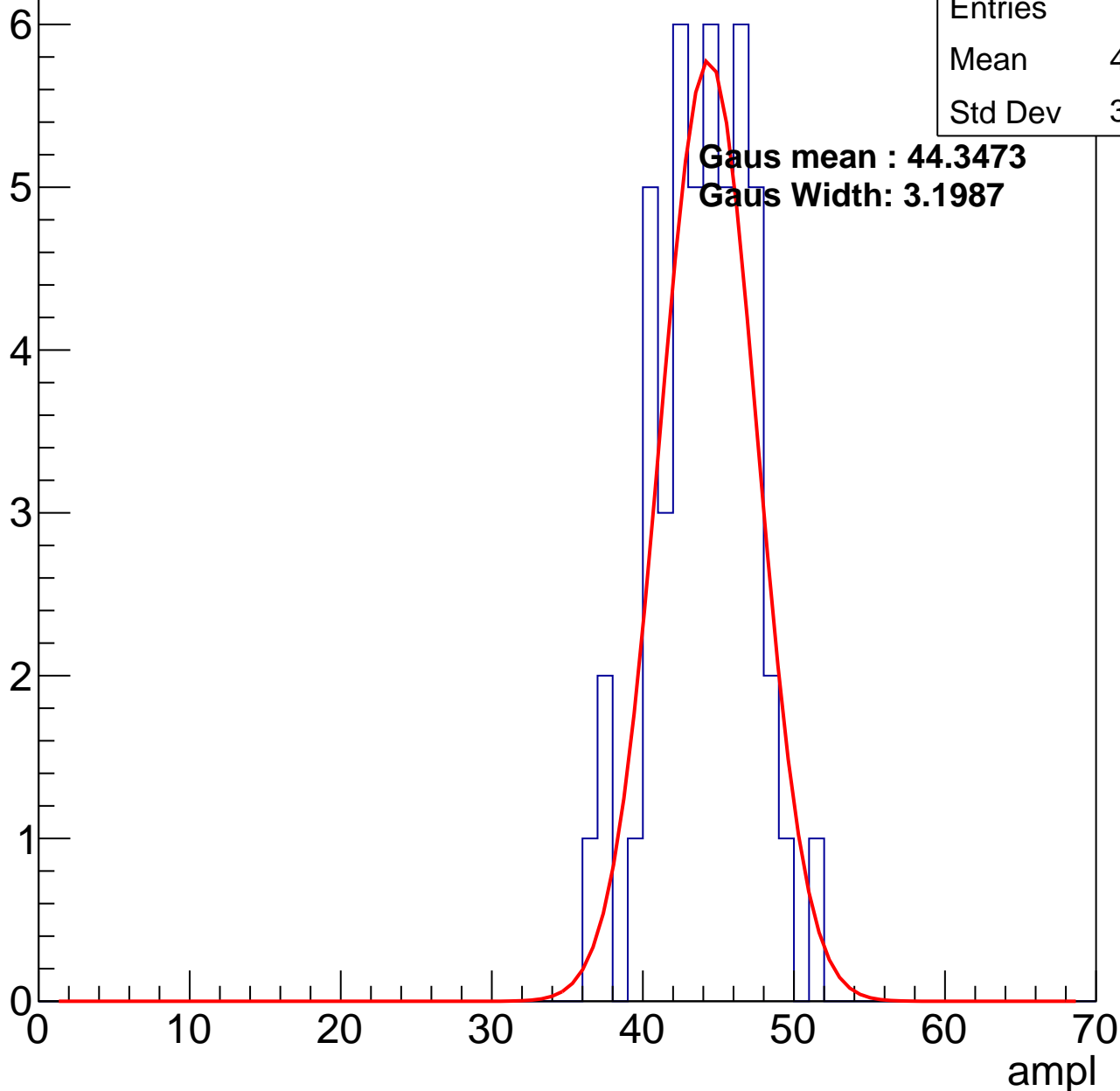
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	43.57
Std Dev	3.175

Gaus mean : 44.3473

Gaus Width: 3.1987

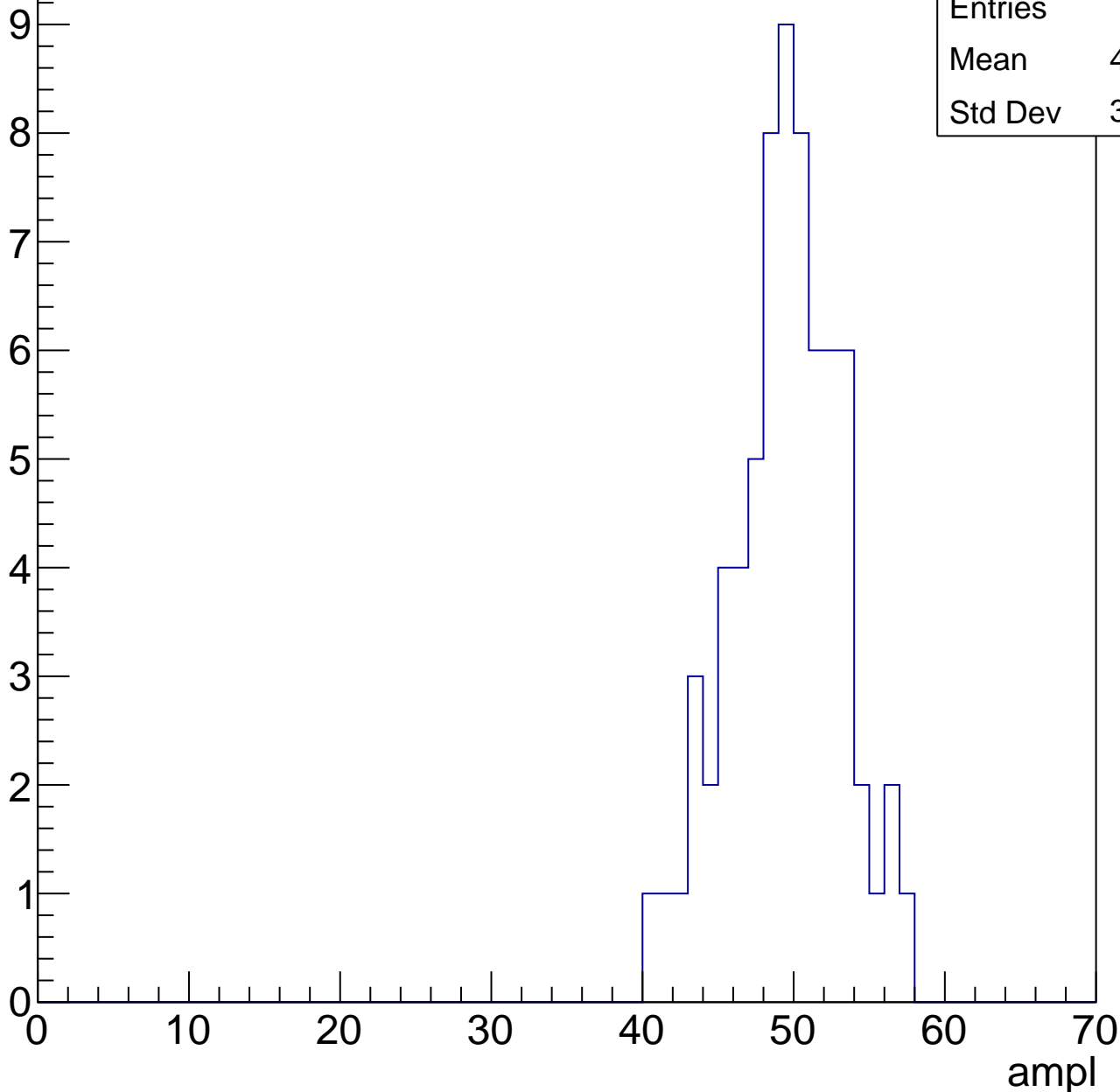


B0L002S, U5-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

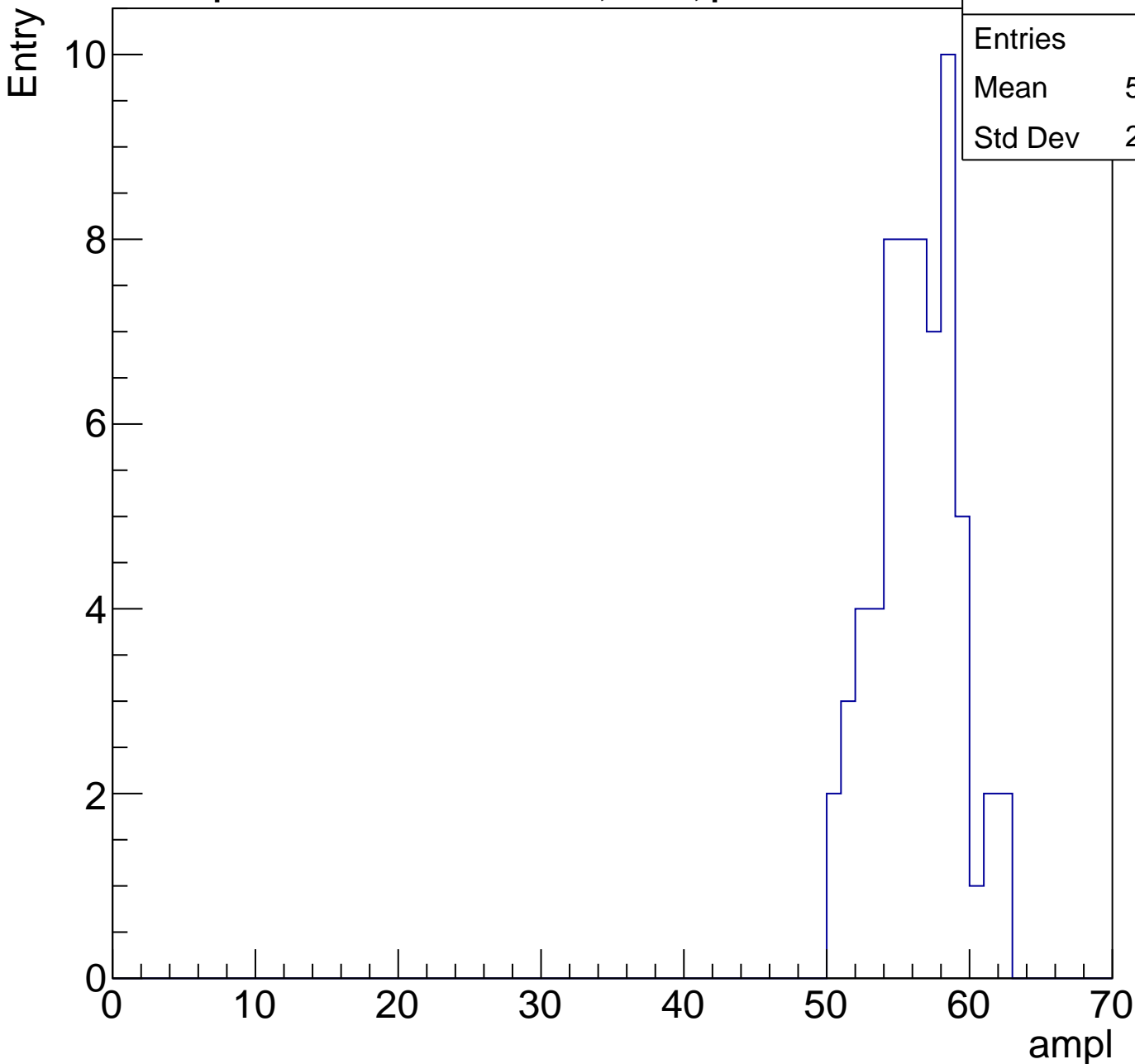
Entries	70
Mean	49.03
Std Dev	3.605



B0L002S, U5-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	55.83
Std Dev	2.837



B0L002S, U5-ch53, adc5

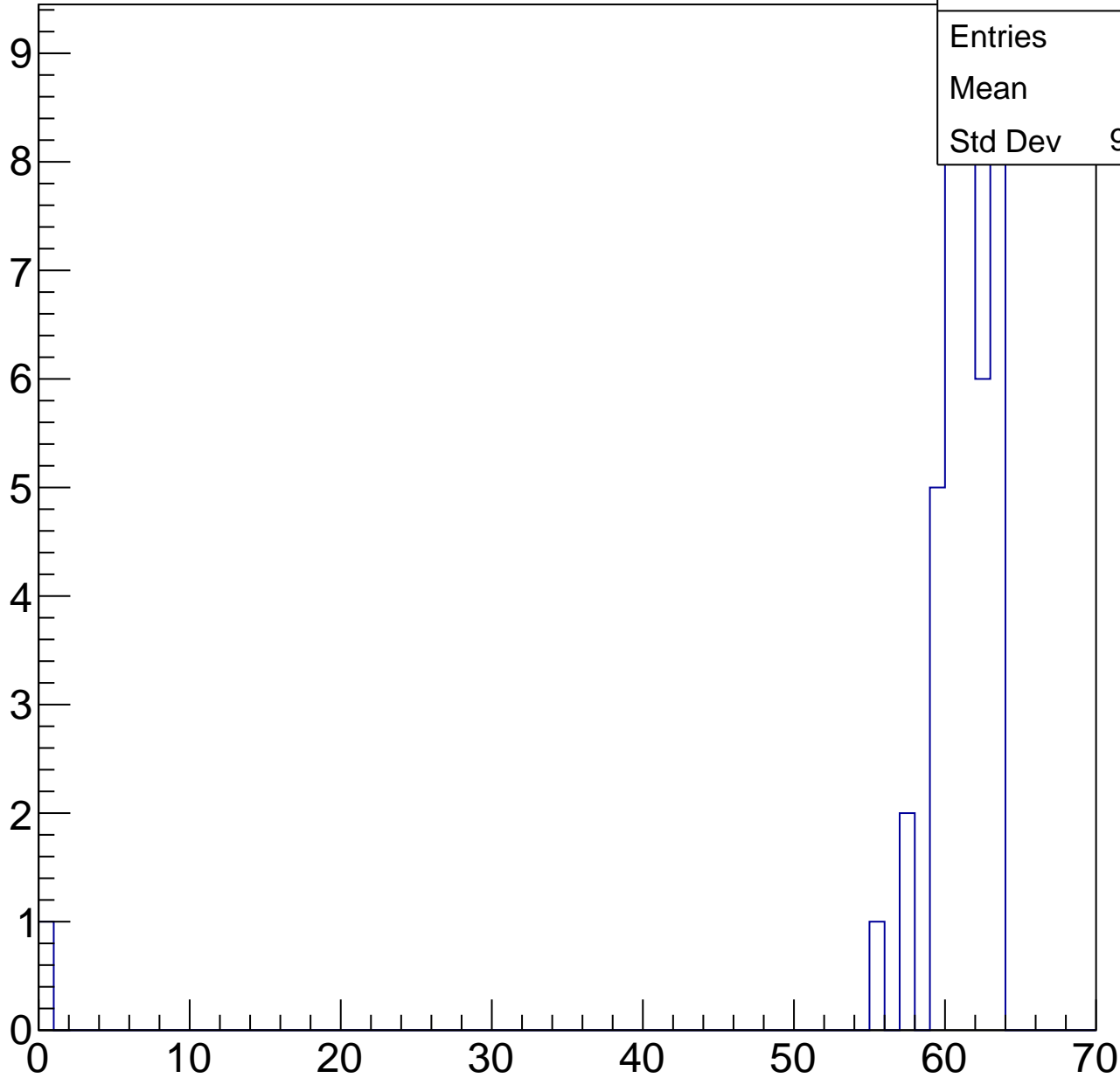
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.2
Std Dev	9.652

ampl



B0L002S, U5-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U5-ch54, adc0

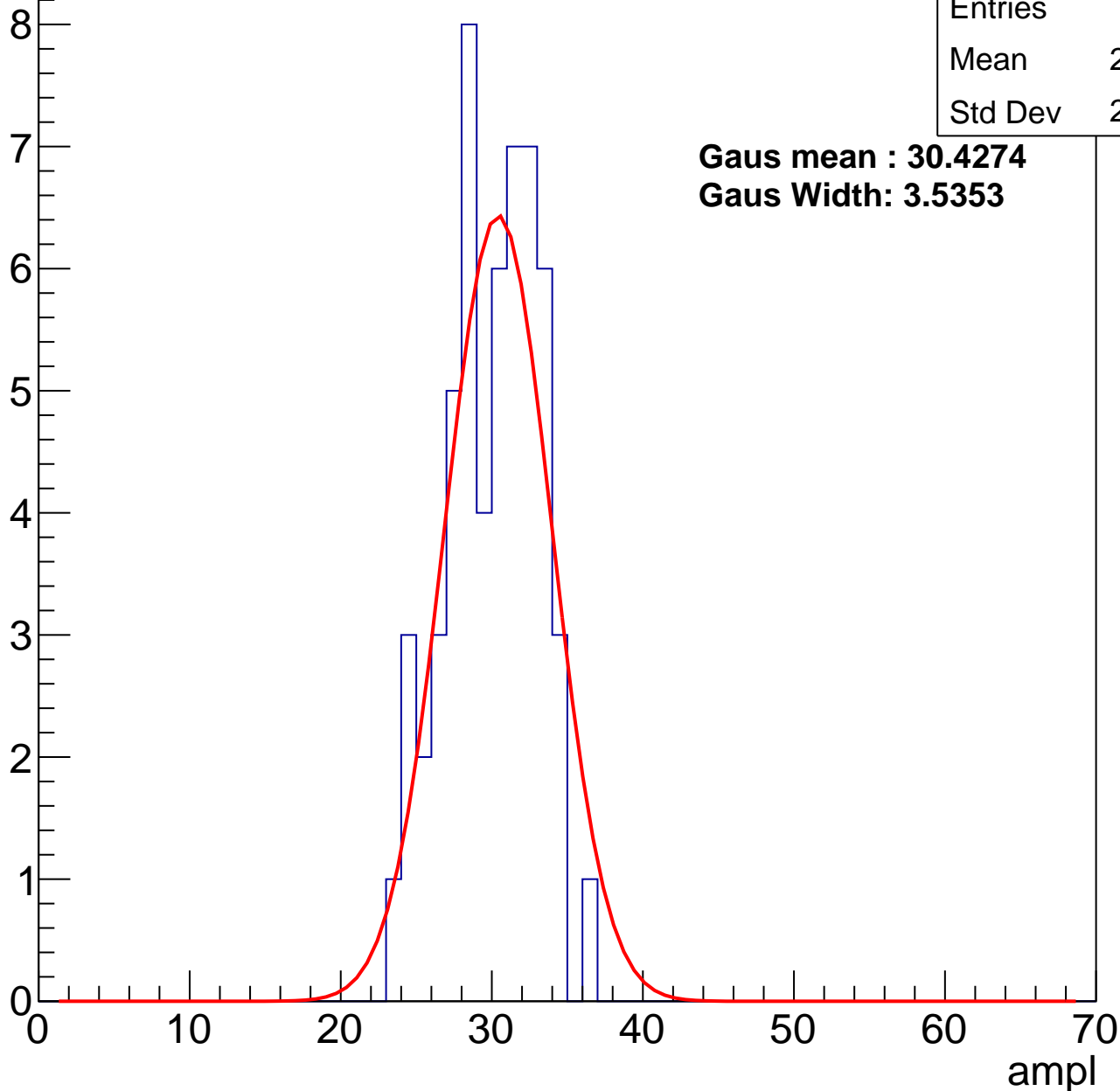
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	29.55
Std Dev	2.982

Gaus mean : 30.4274

Gaus Width: 3.5353



B0L002S, U5-ch54, adc1

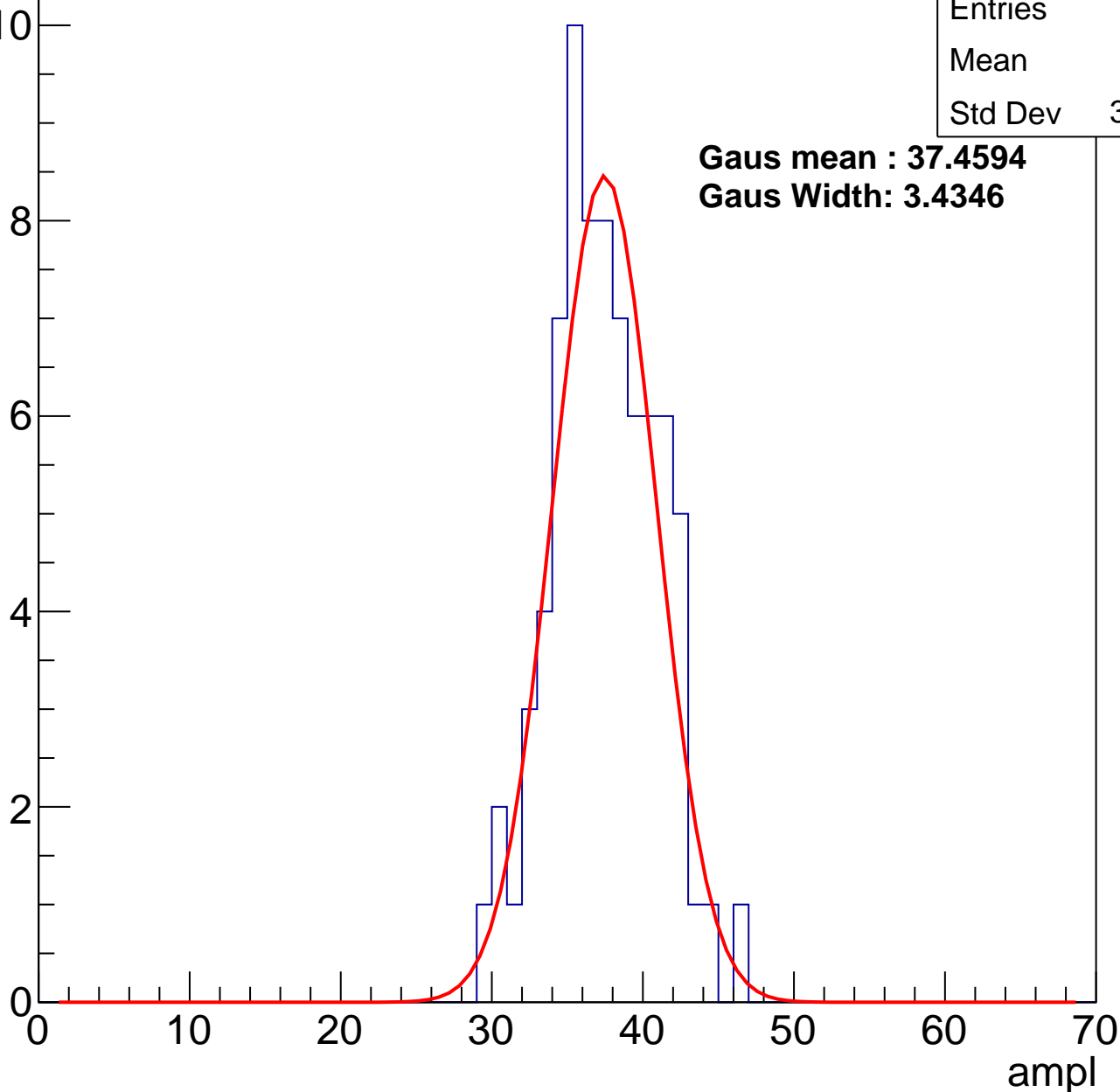
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	37
Std Dev	3.457

Gaus mean : 37.4594

Gaus Width: 3.4346



B0L002S, U5-ch54, adc2

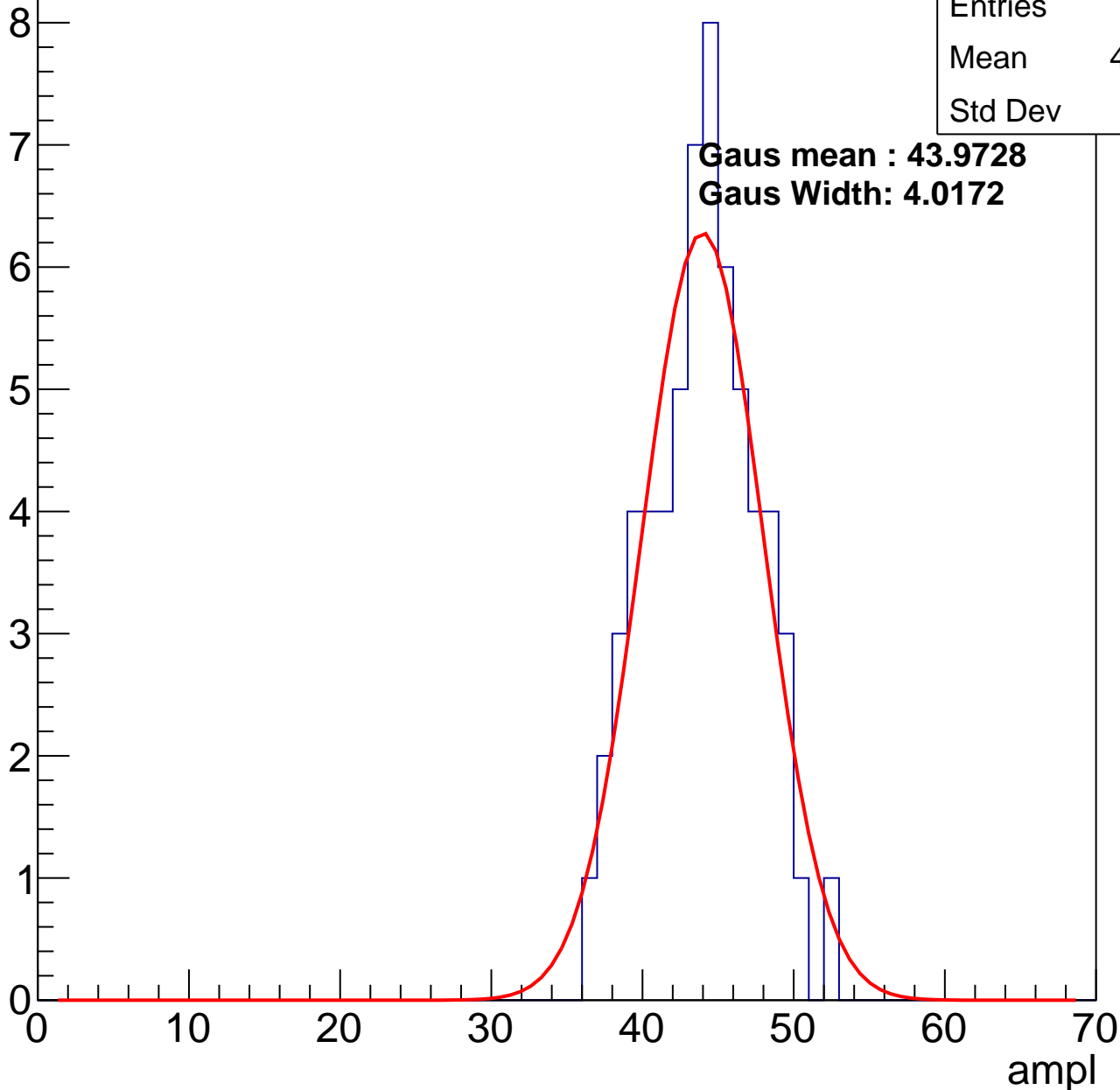
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.48
Std Dev	3.55

Gaus mean : 43.9728

Gaus Width: 4.0172

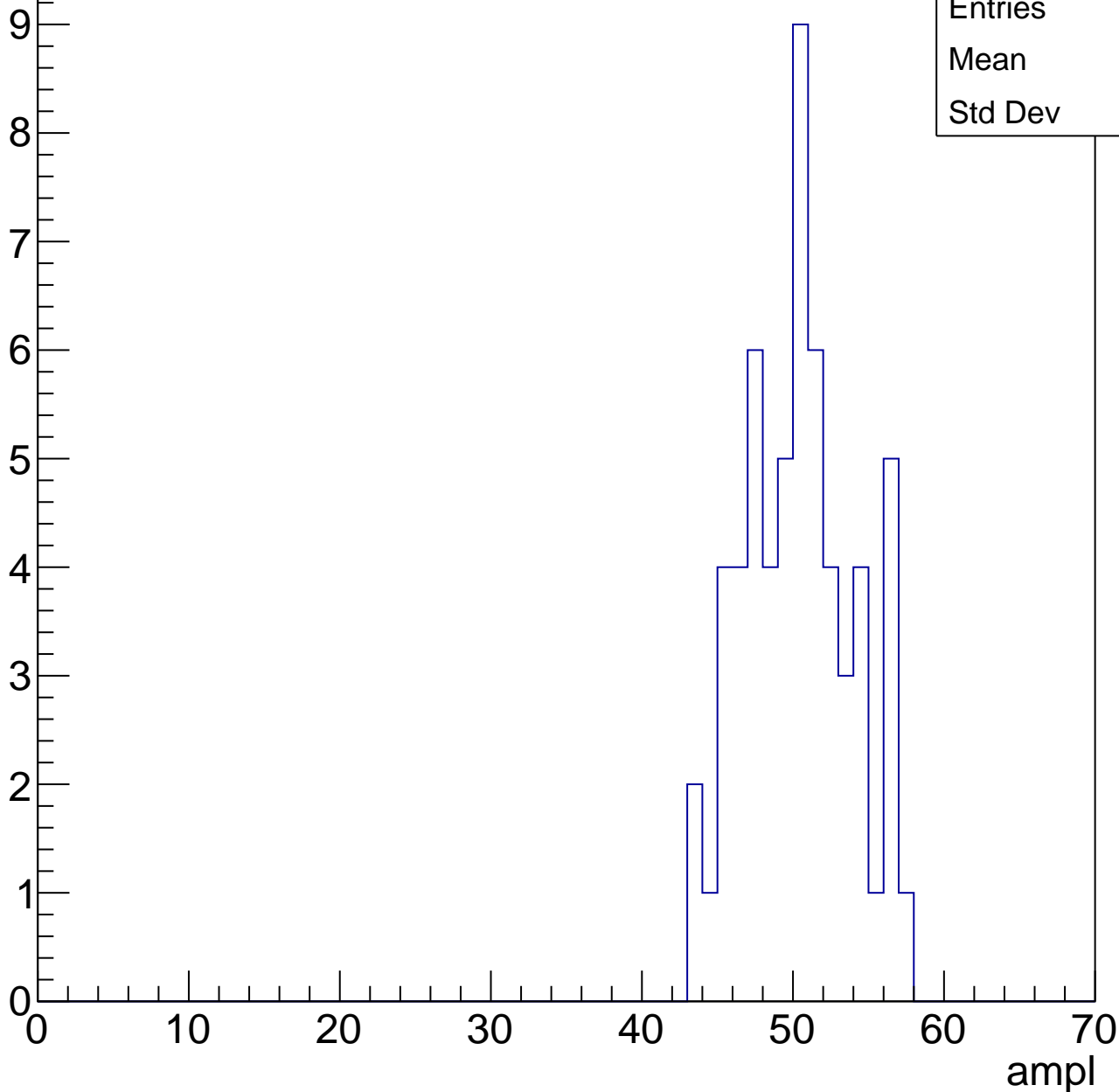


B0L002S, U5-ch54, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	49.9
Std Dev	3.54

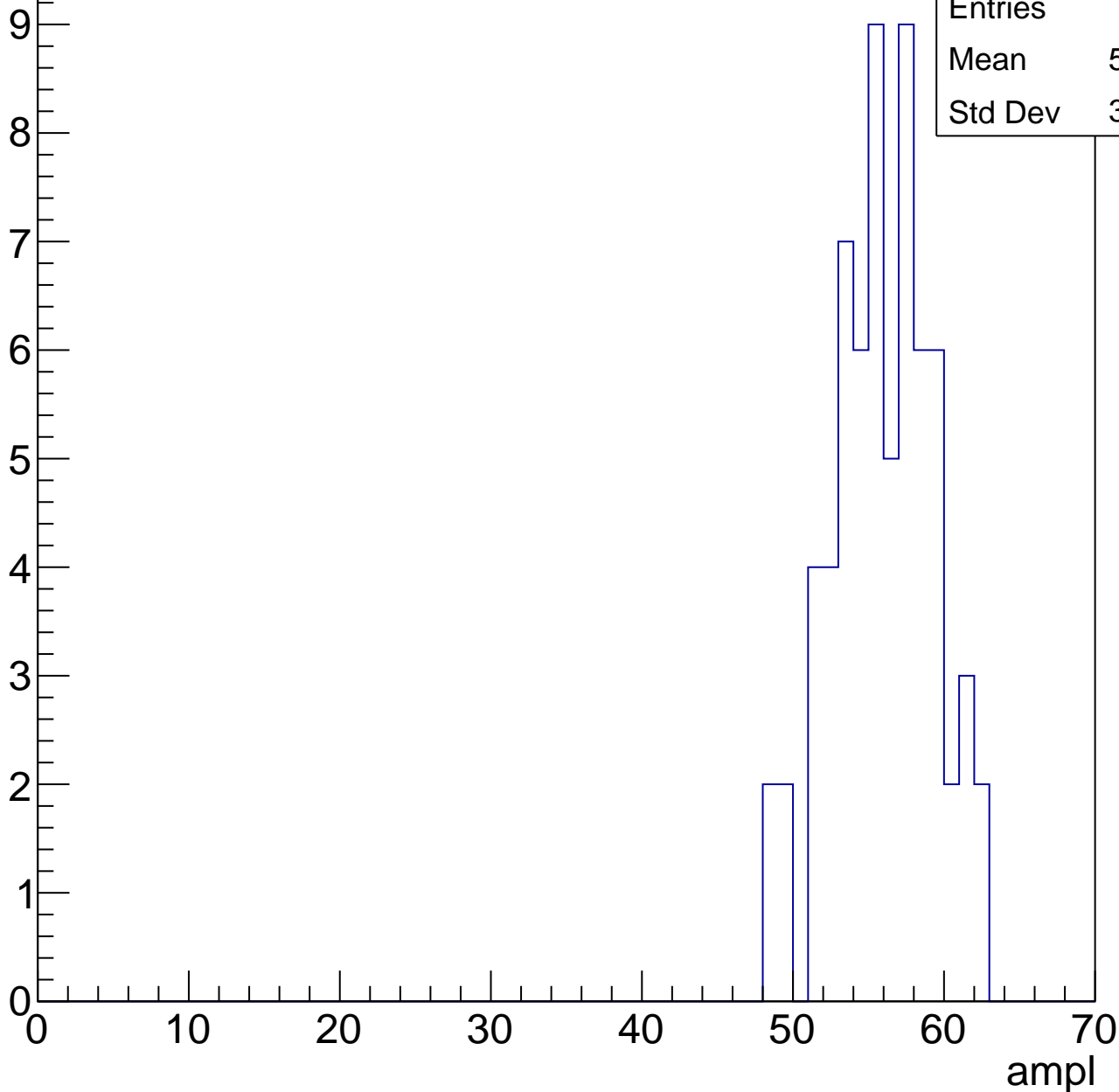


B0L002S, U5-ch54, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	55.49
Std Dev	3.312



B0L002S, U5-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.22
Std Dev	9.596

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B0L002S, U5-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch55, adc0

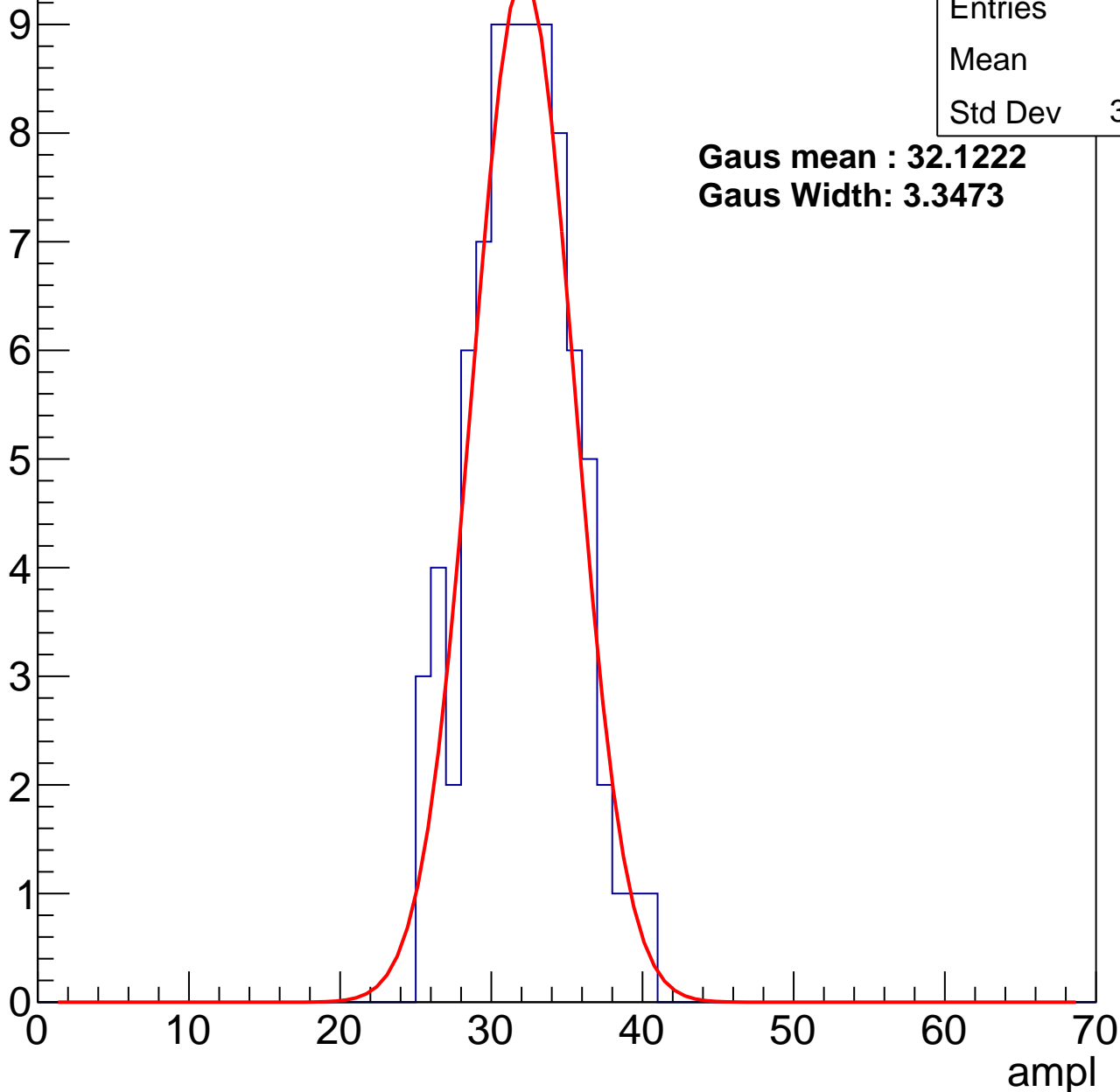
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	31.6
Std Dev	3.323

Gaus mean : 32.1222

Gaus Width: 3.3473



B0L002S, U5-ch55, adc1

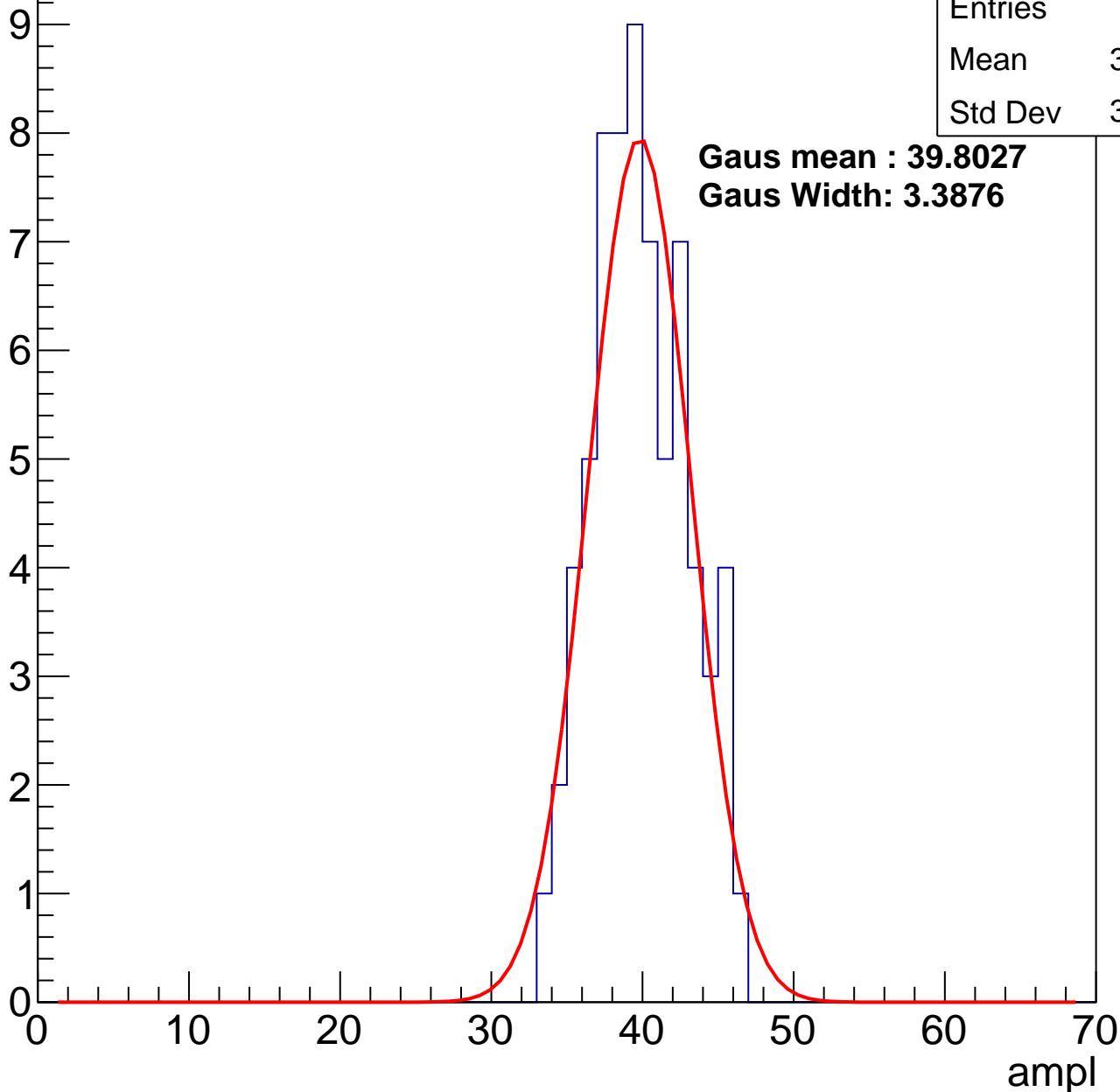
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	39.43
Std Dev	3.079

Gaus mean : 39.8027

Gaus Width: 3.3876



B0L002S, U5-ch55, adc2

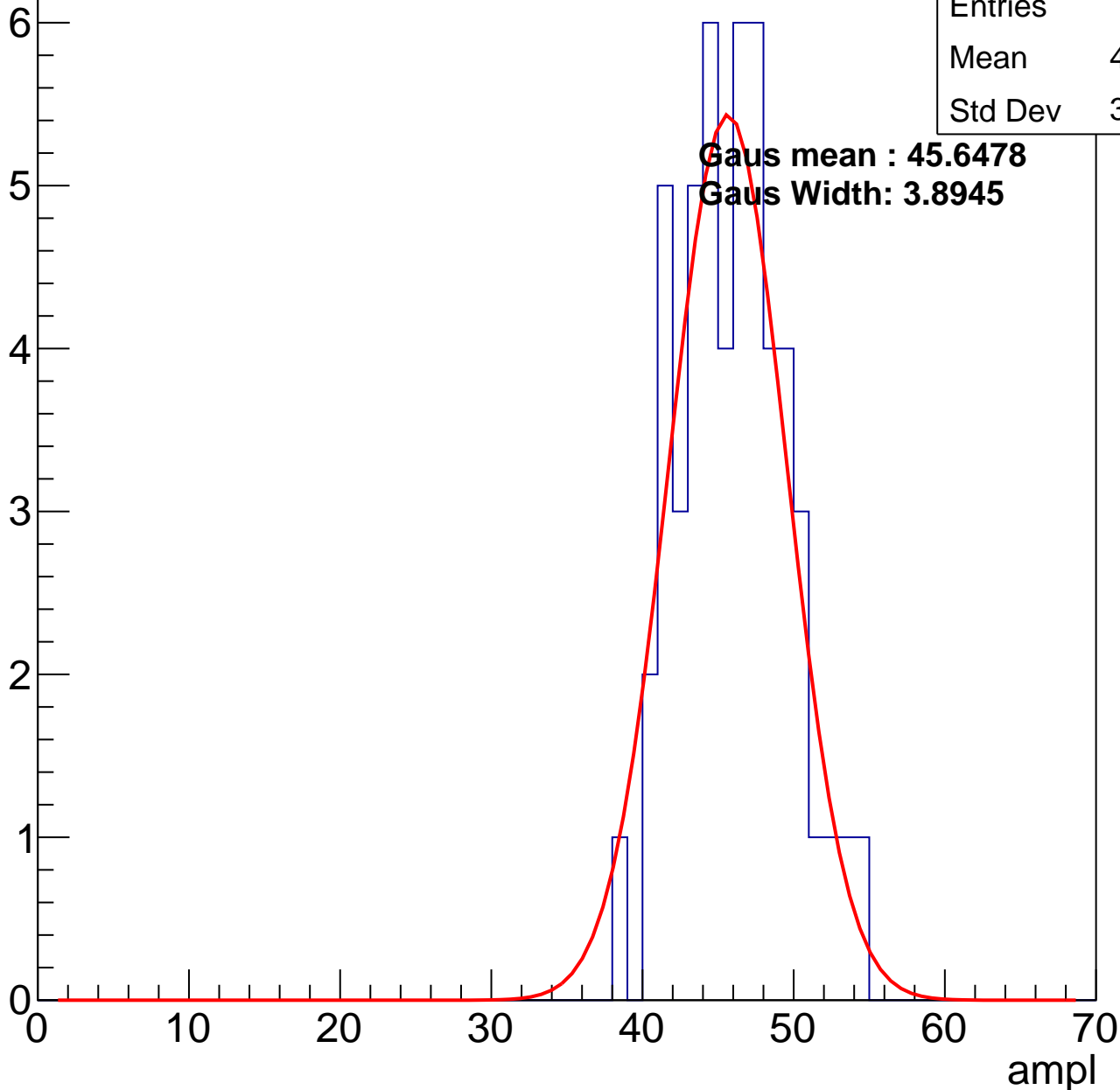
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	45.55
Std Dev	3.505

Gaus mean : 45.6478

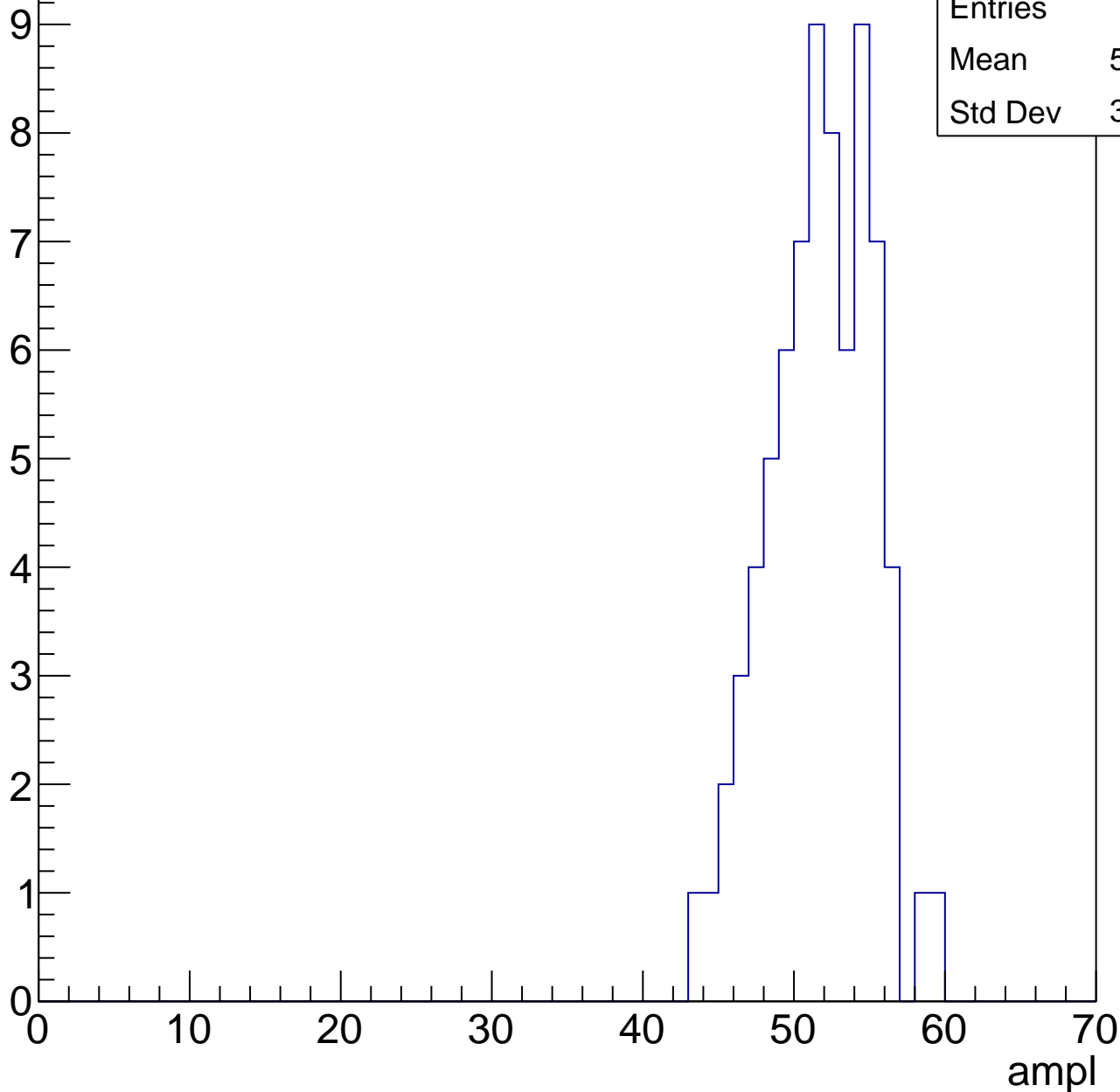
Gaus Width: 3.8945



B0L002S, U5-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

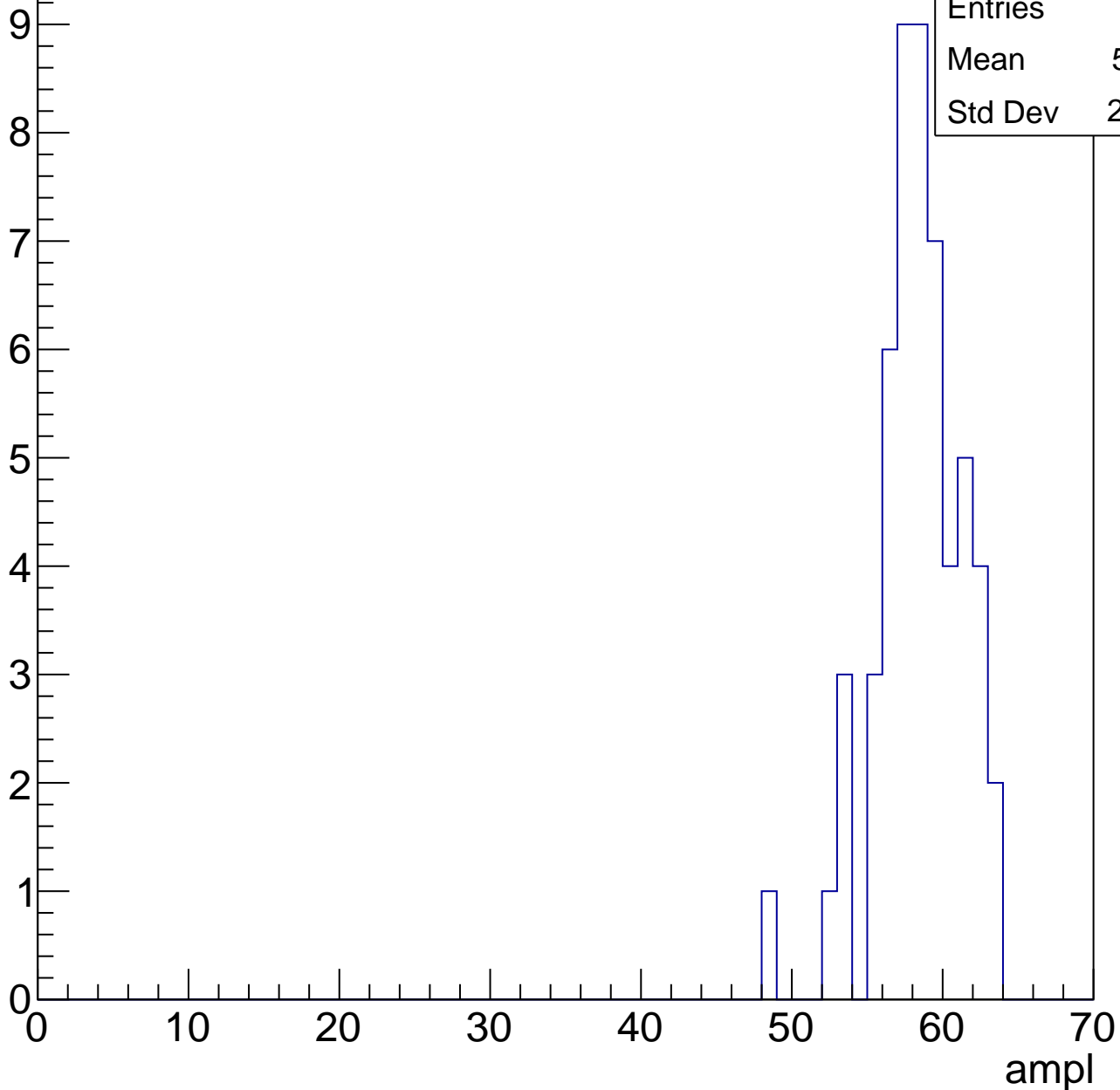


Entries	74
Mean	51.24
Std Dev	3.356

B0L002S, U5-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

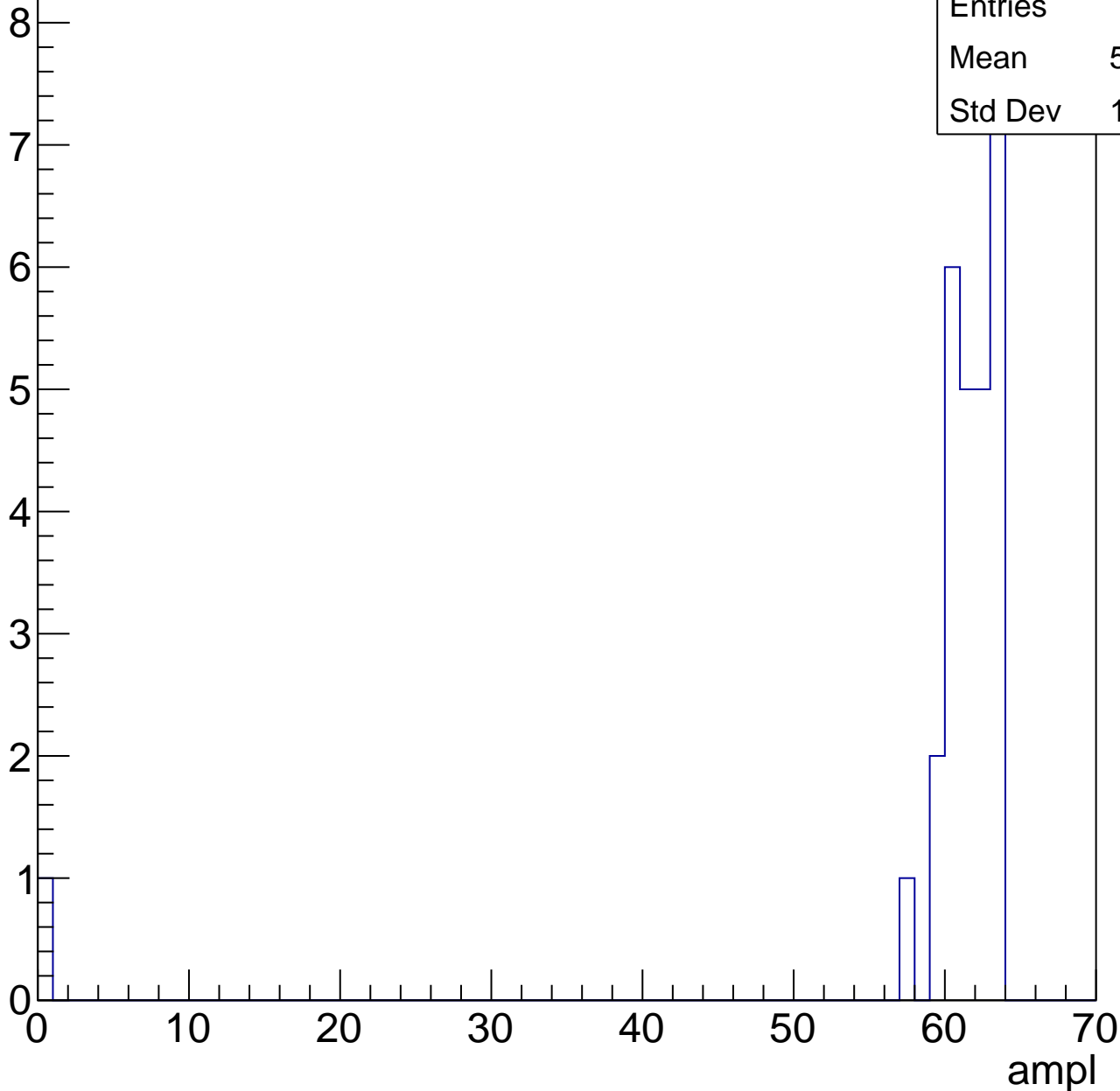


B0L002S, U5-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	59.07
Std Dev	11.47



B0L002S, U5-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch56, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	30.4
Std Dev	4.902

Gaus mean : 31.4057

Gaus Width: 2.6177

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L002S, U5-ch56, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	38.32
Std Dev	3.421

Gaus mean : 38.5585

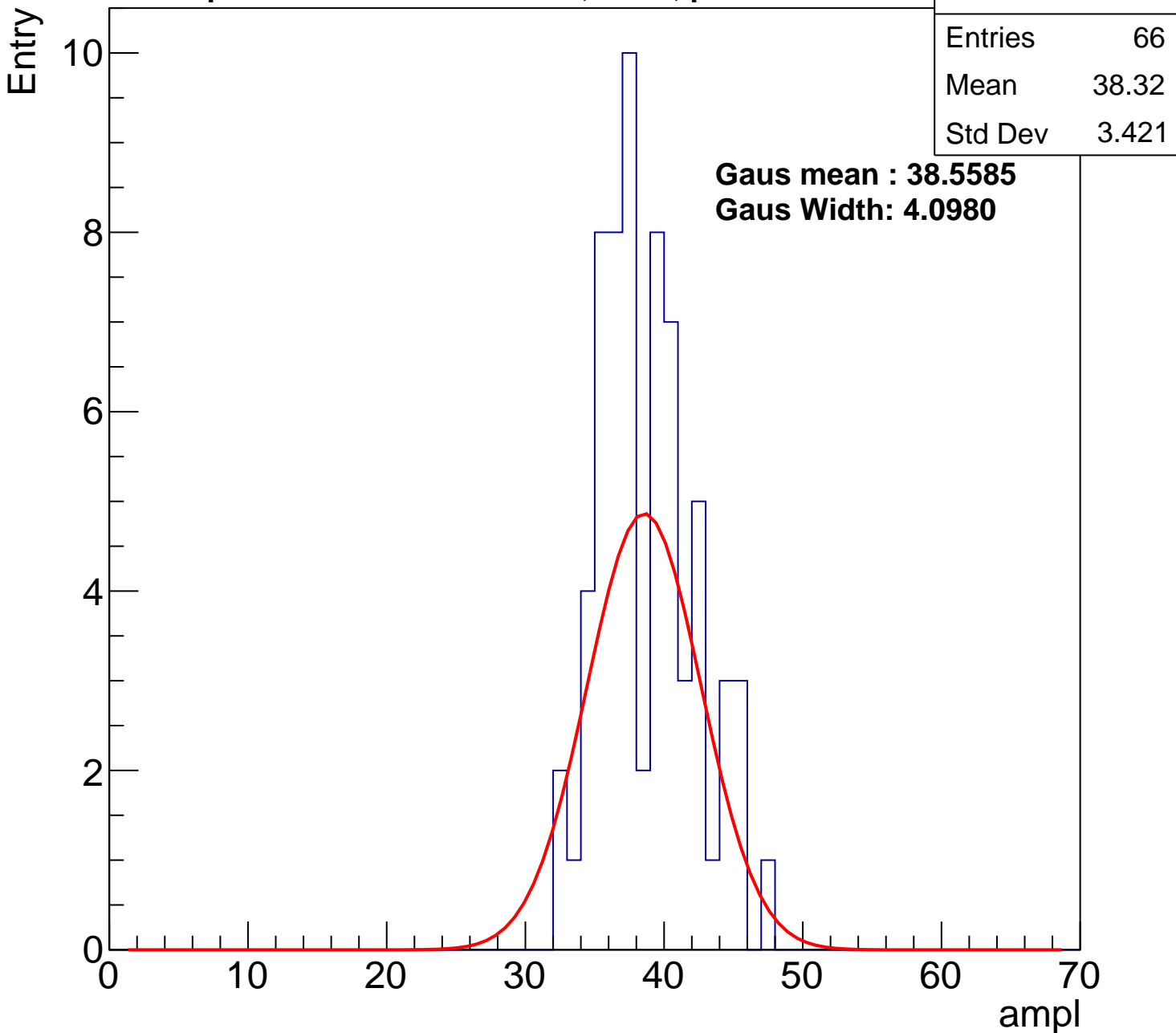
Gaus Width: 4.0980

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch56, adc2

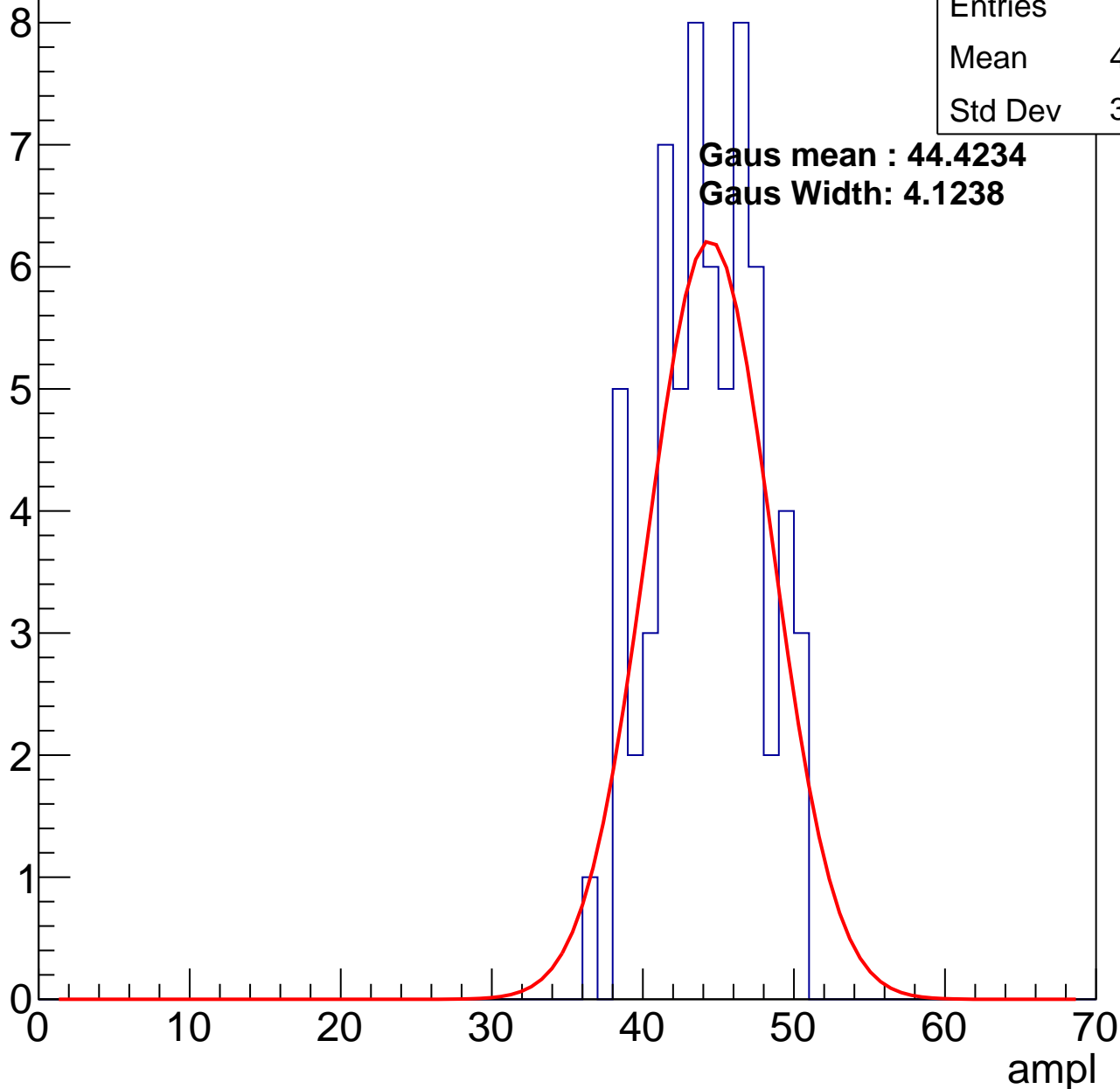
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.78
Std Dev	3.422

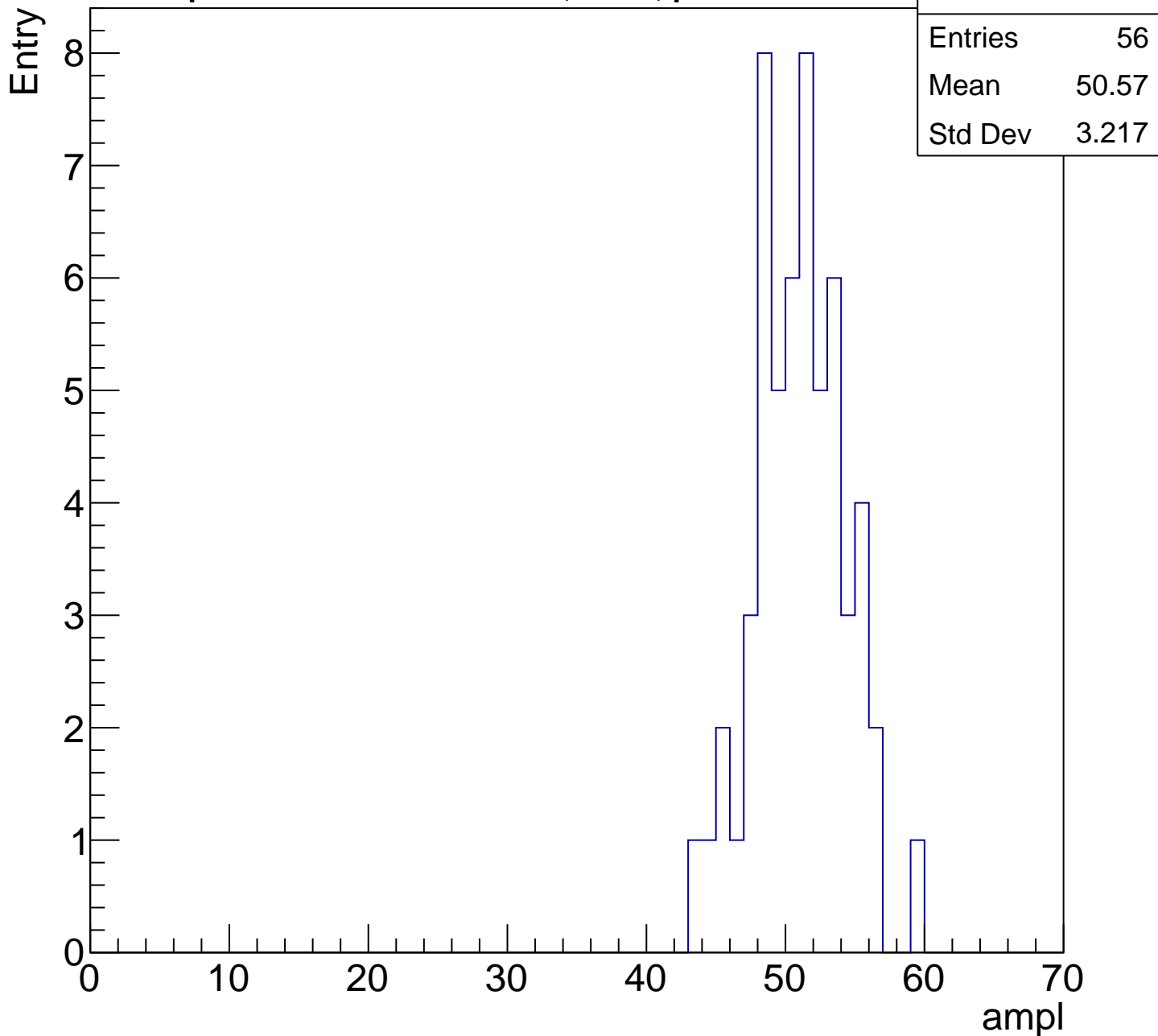
Gaus mean : 44.4234

Gaus Width: 4.1238



B0L002S, U5-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1

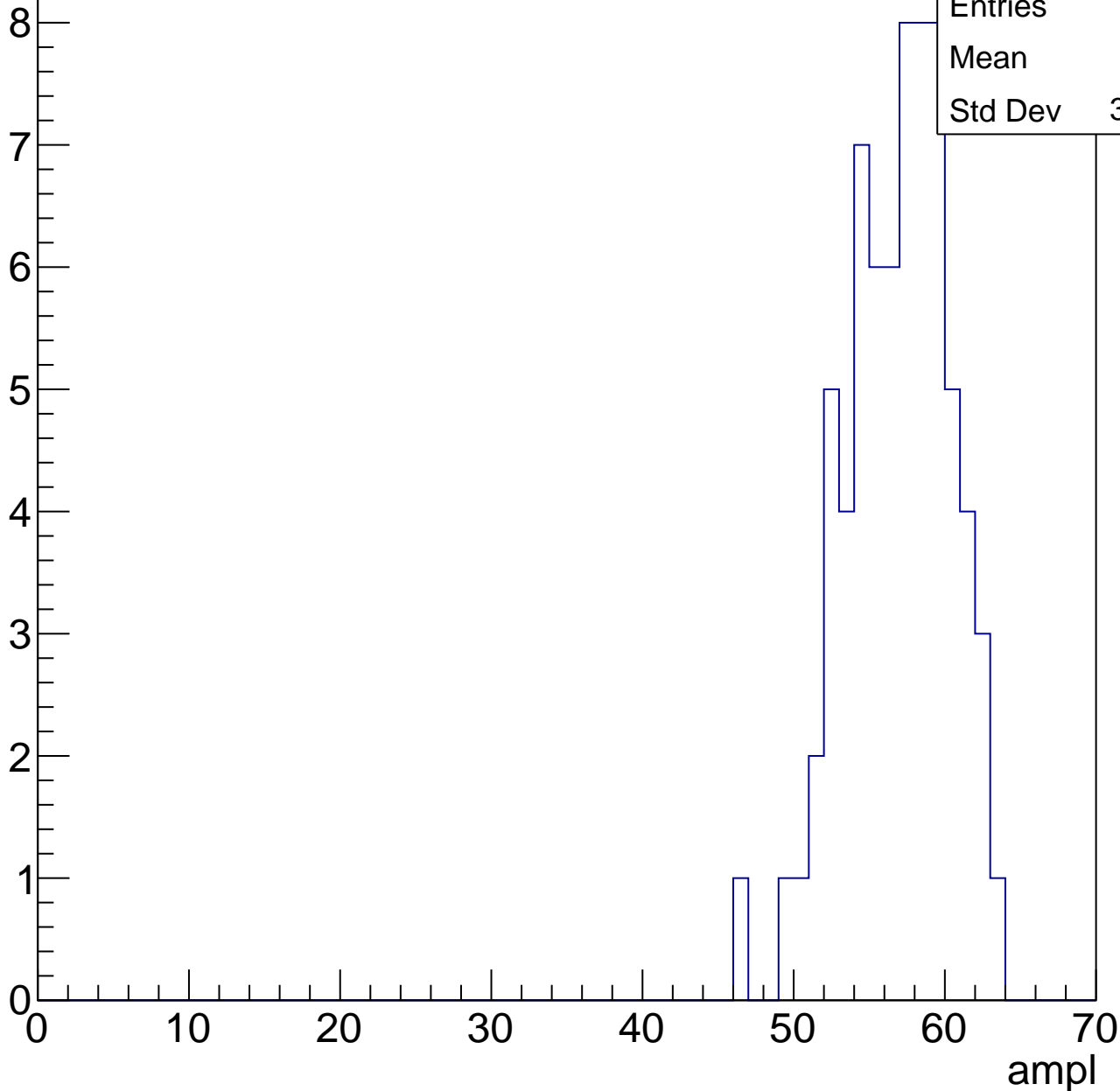


B0L002S, U5-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	56.4
Std Dev	3.428

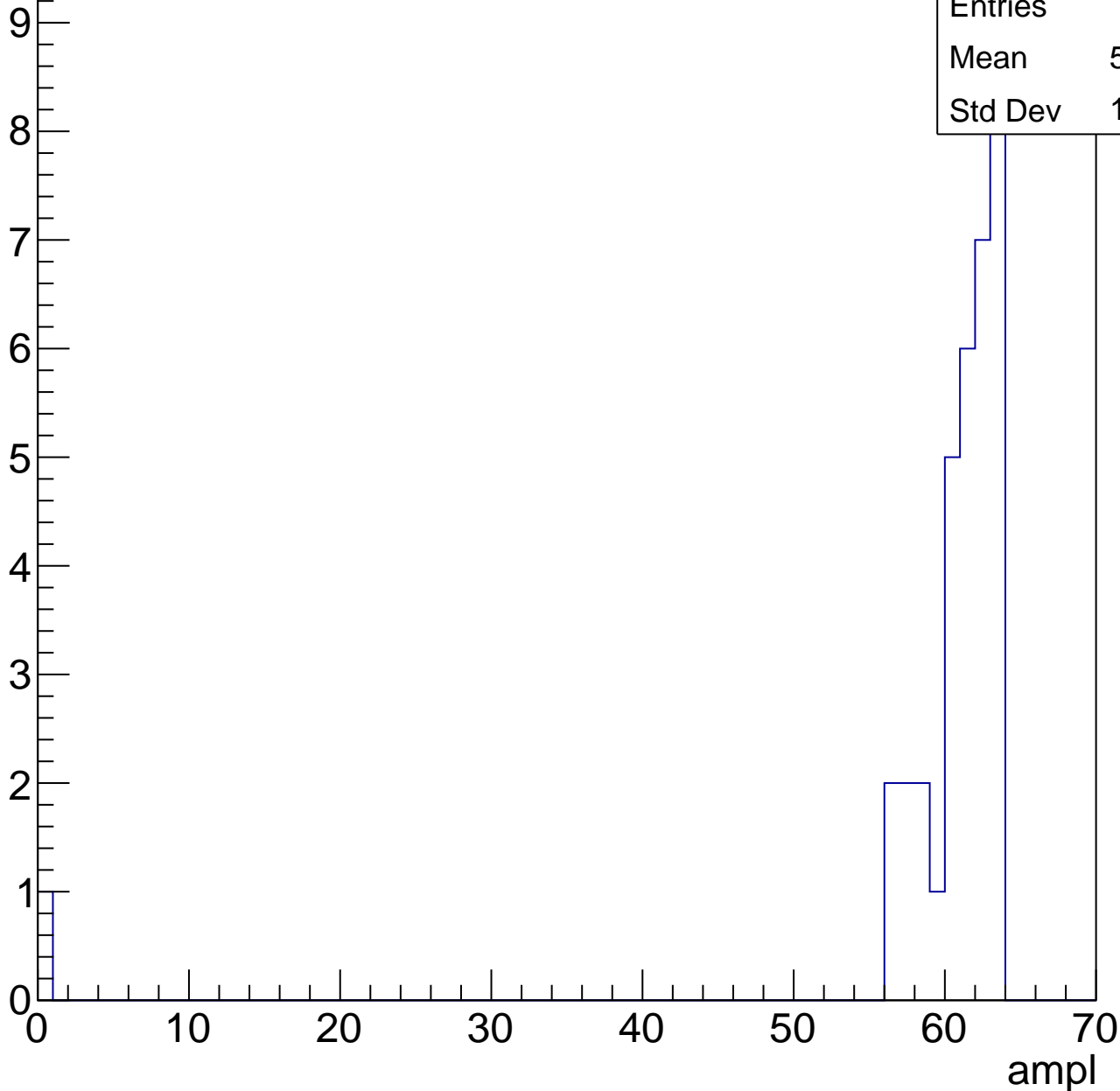


B0L002S, U5-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.09
Std Dev	10.34



B0L002S, U5-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch57, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	30.45
Std Dev	3.14

Gaus mean : 30.8239

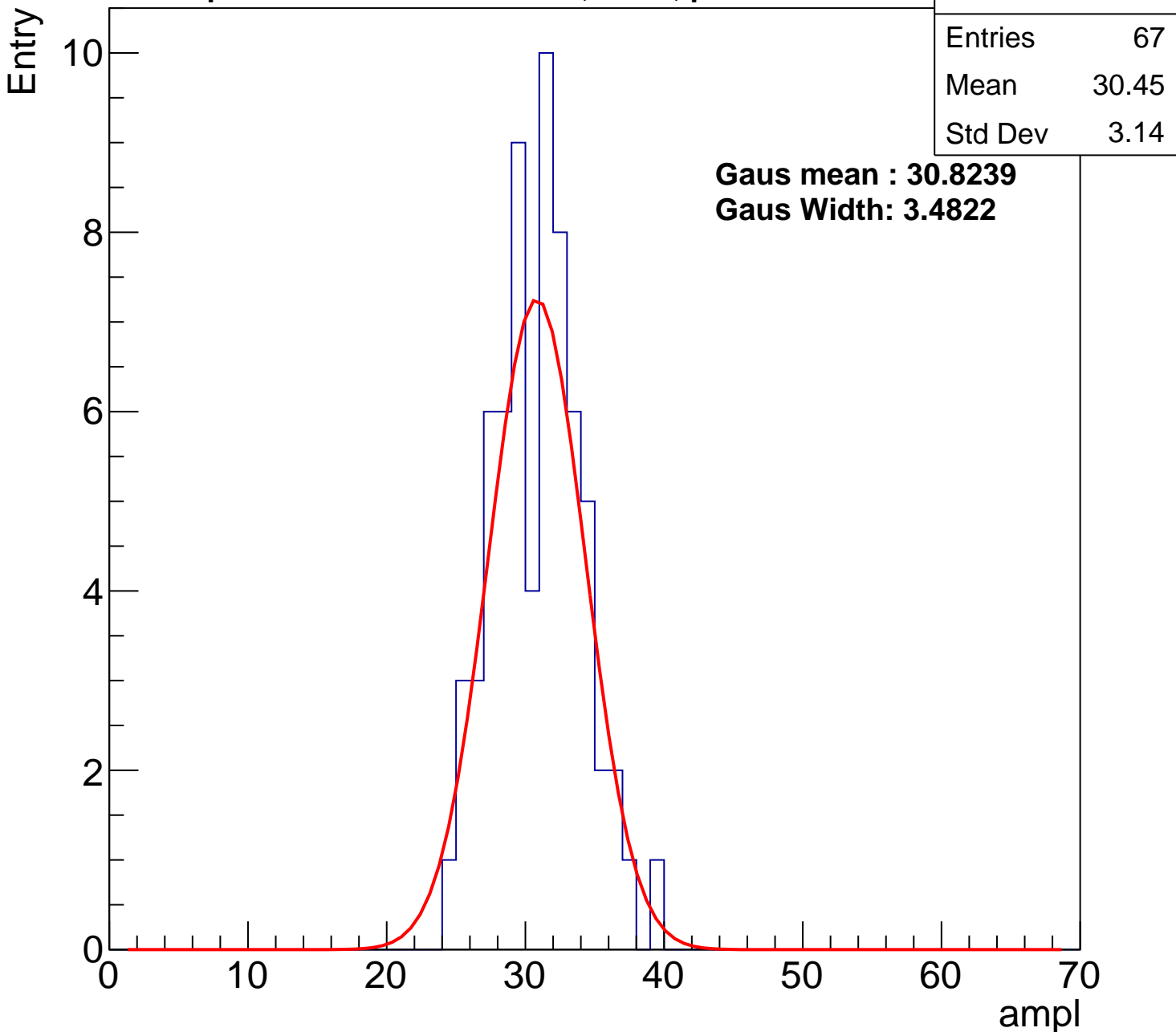
Gaus Width: 3.4822

Entry

10
8
6
4
2
0

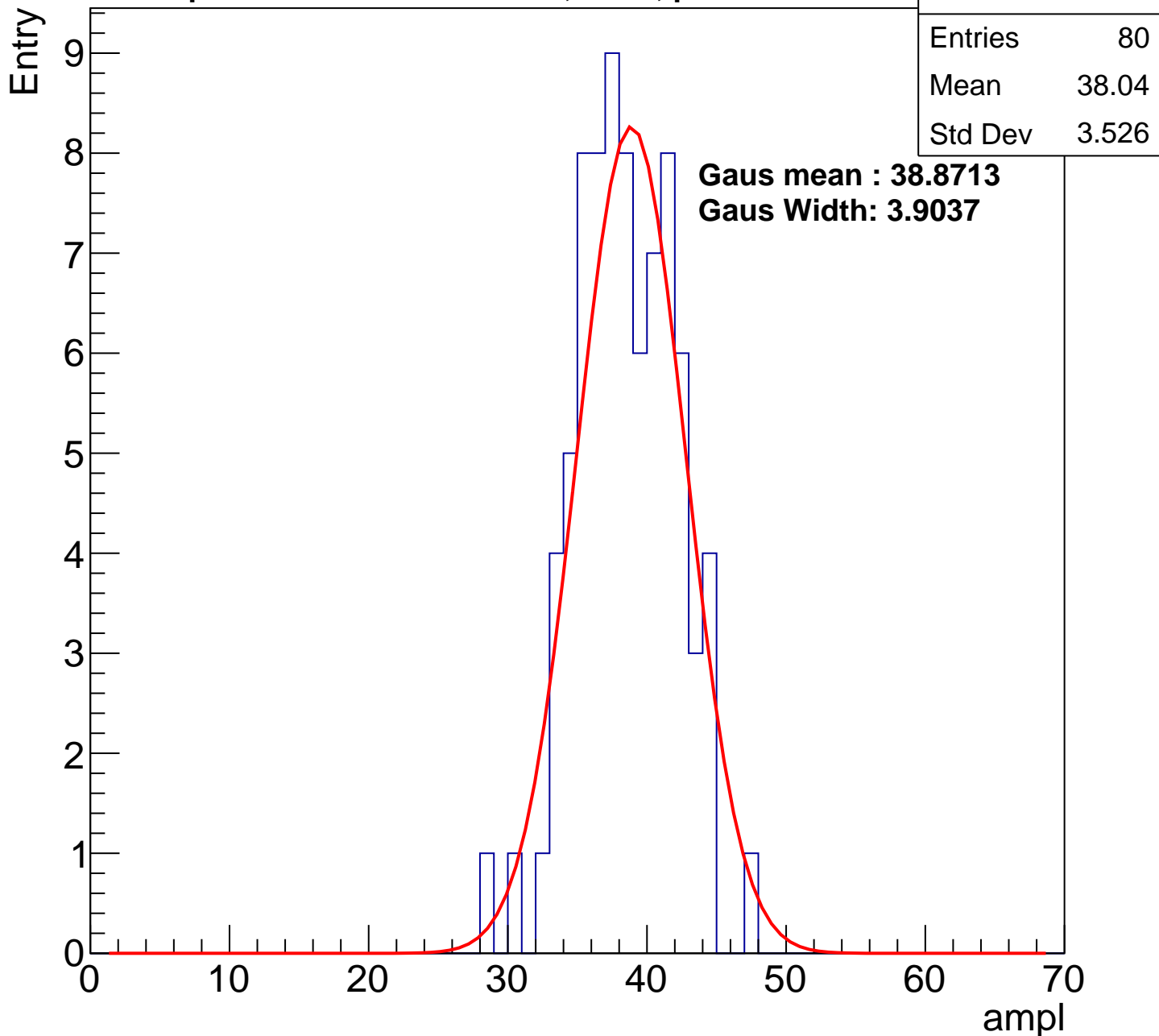
ampl

0 10 20 30 40 50 60 70



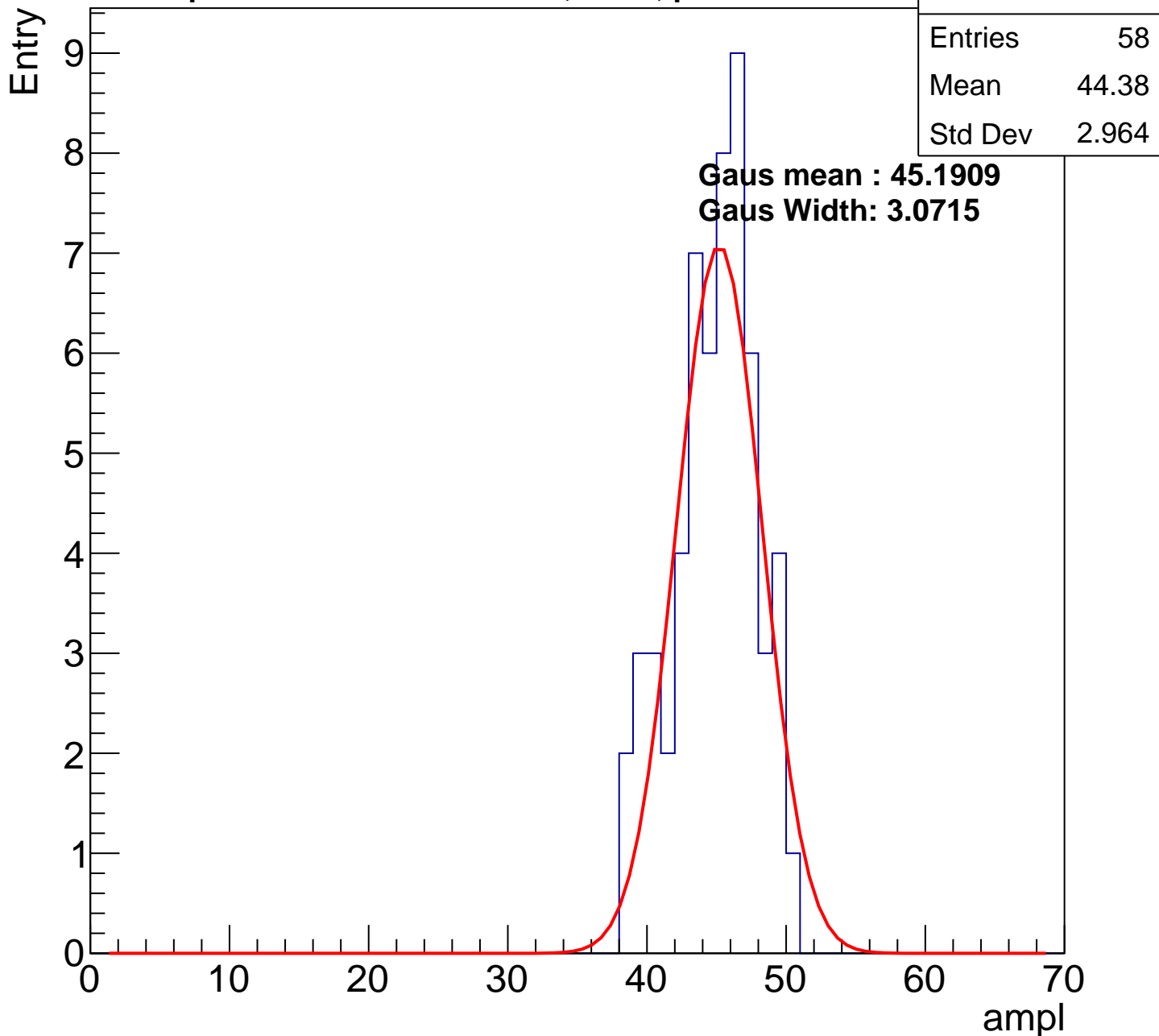
B0L002S, U5-ch57, adc1

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch57, adc2

calib_packv5_042523_0143.root, FC#8, port C1

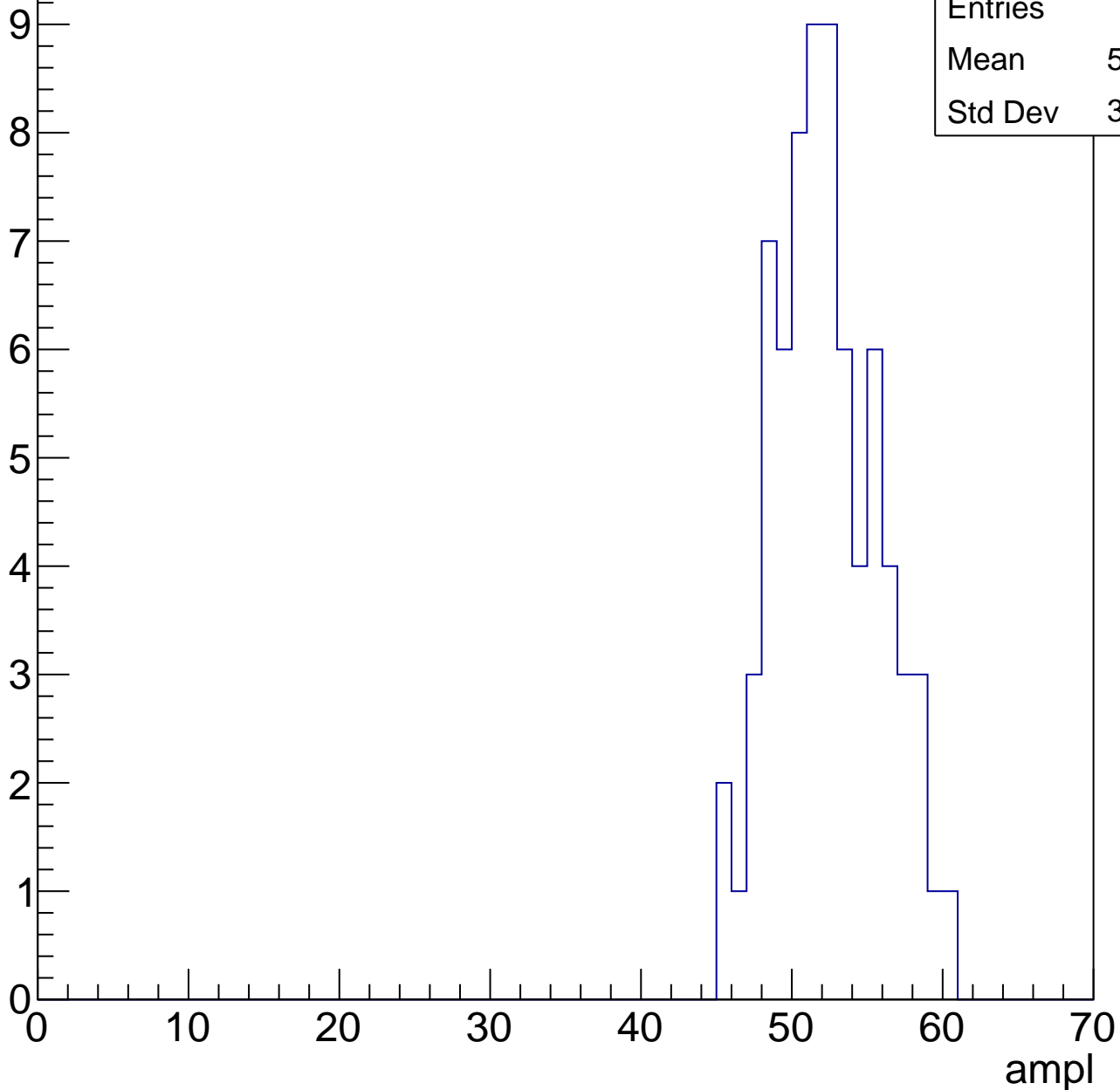


B0L002S, U5-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	51.86
Std Dev	3.414

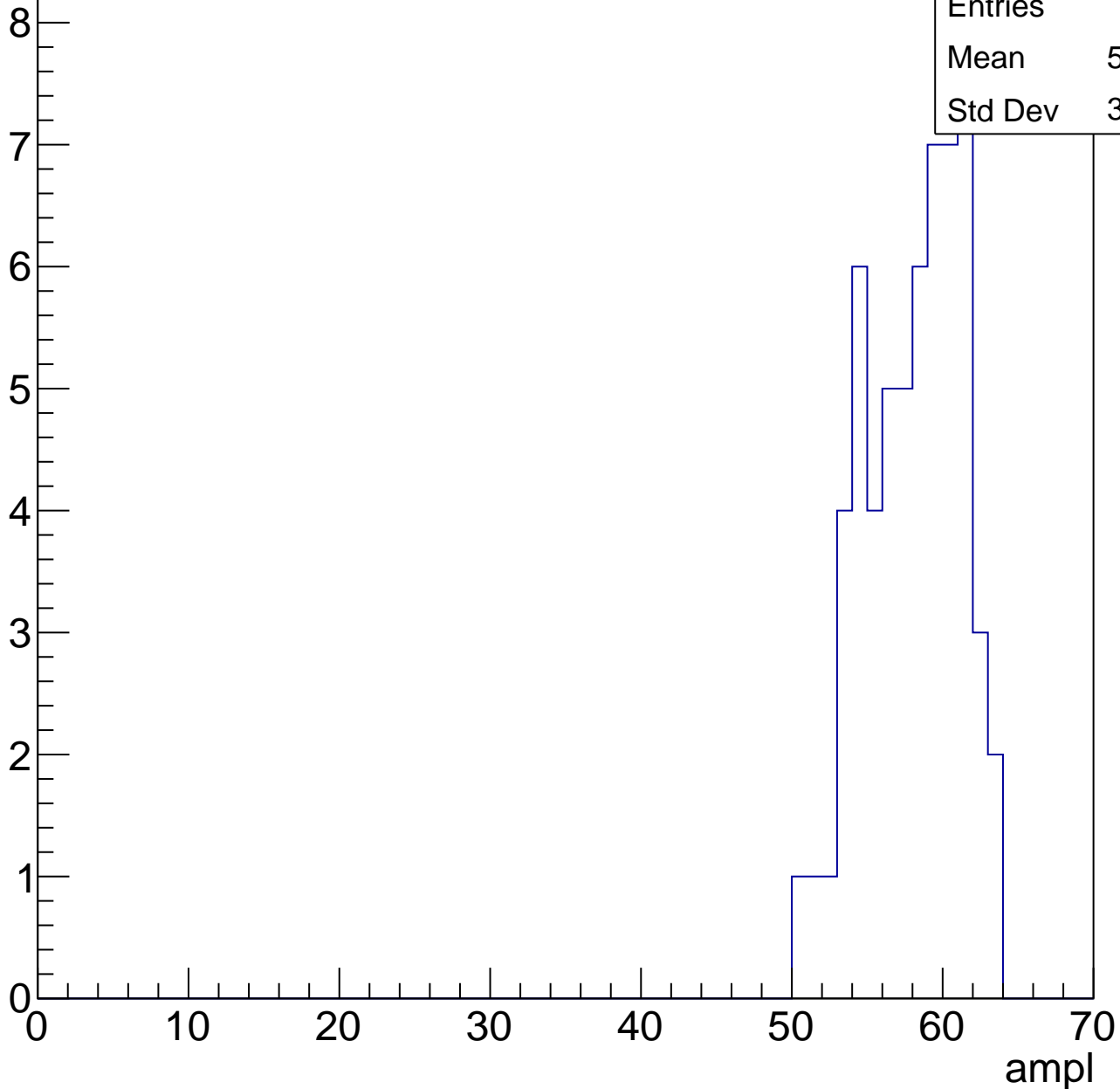


B0L002S, U5-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	57.58
Std Dev	3.164

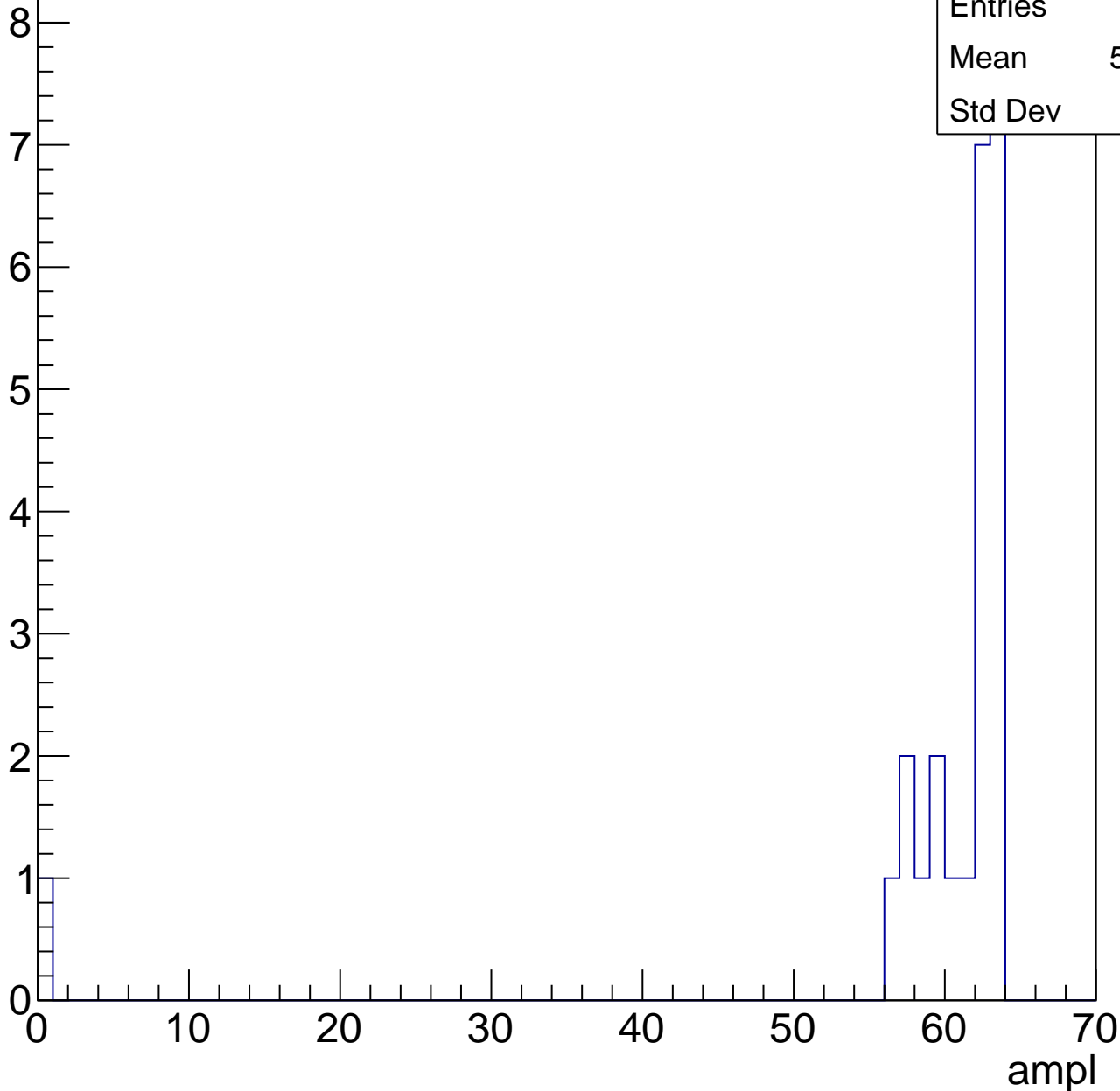


B0L002S, U5-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

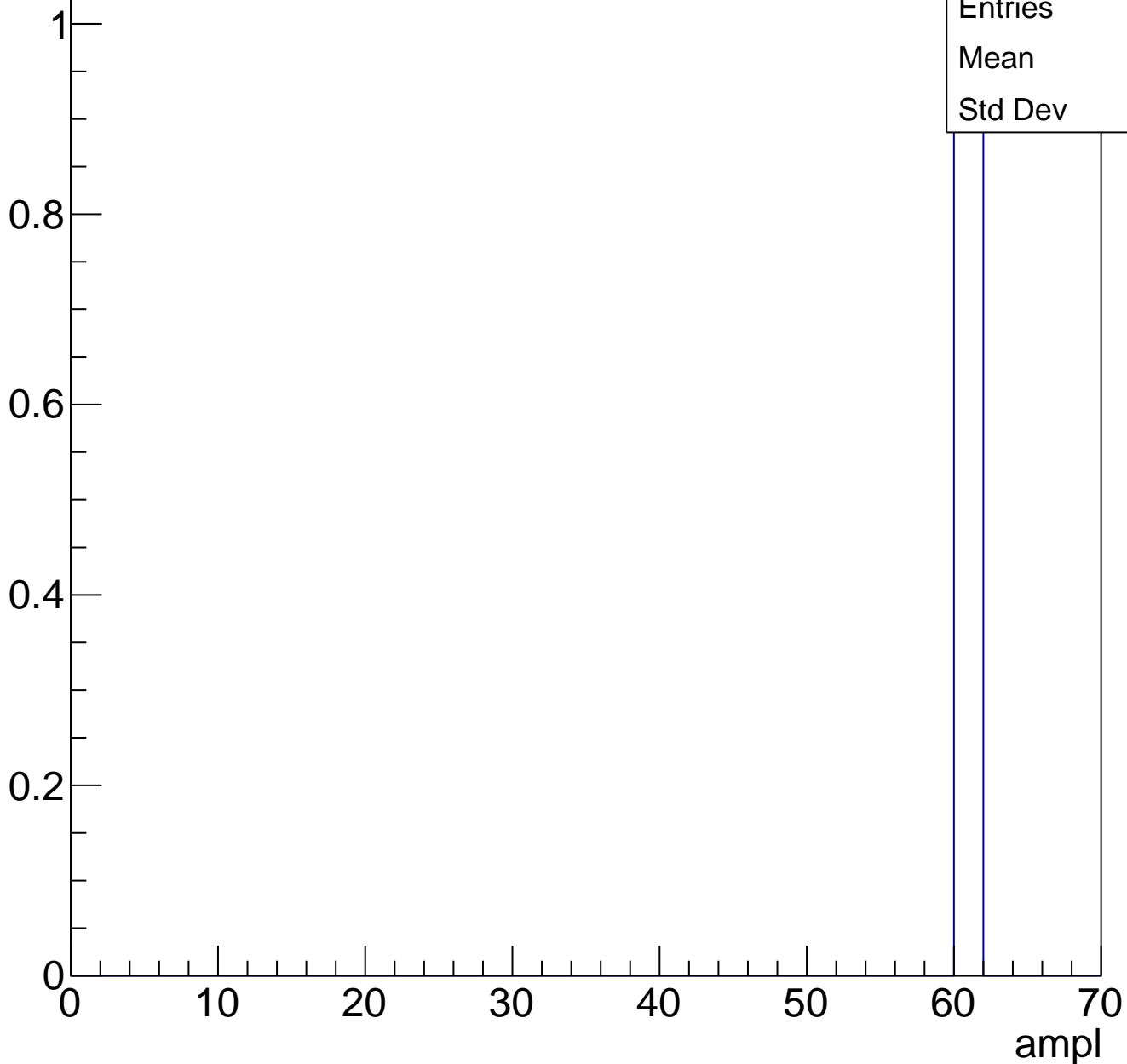
Entries	24
Mean	58.54
Std Dev	12.4



B0L002S, U5-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch58, adc0

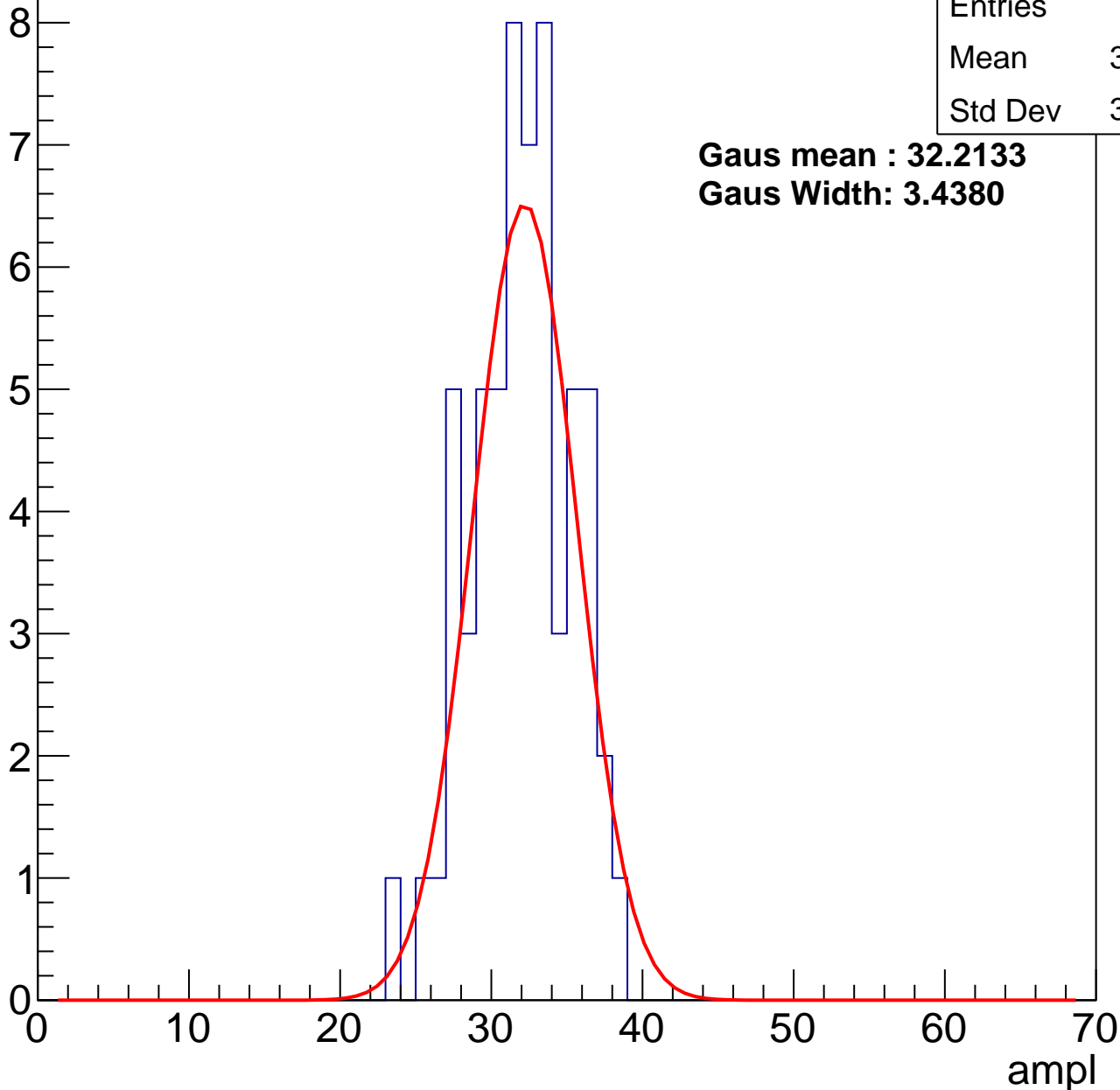
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	31.55
Std Dev	3.243

Gaus mean : 32.2133

Gaus Width: 3.4380



B0L002S, U5-ch58, adc1

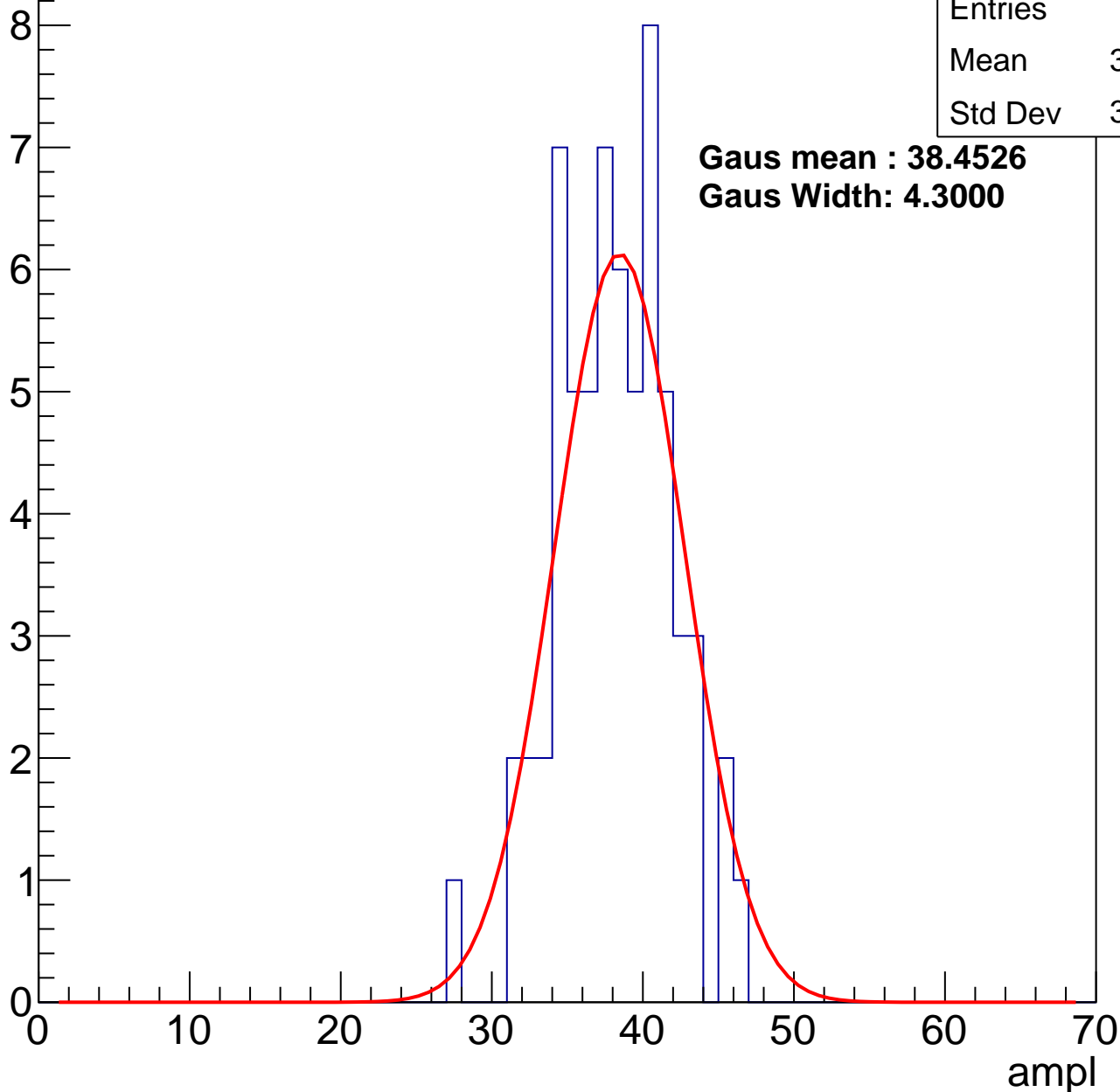
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.66
Std Dev	3.717

Gaus mean : 38.4526

Gaus Width: 4.3000



B0L002S, U5-ch58, adc2

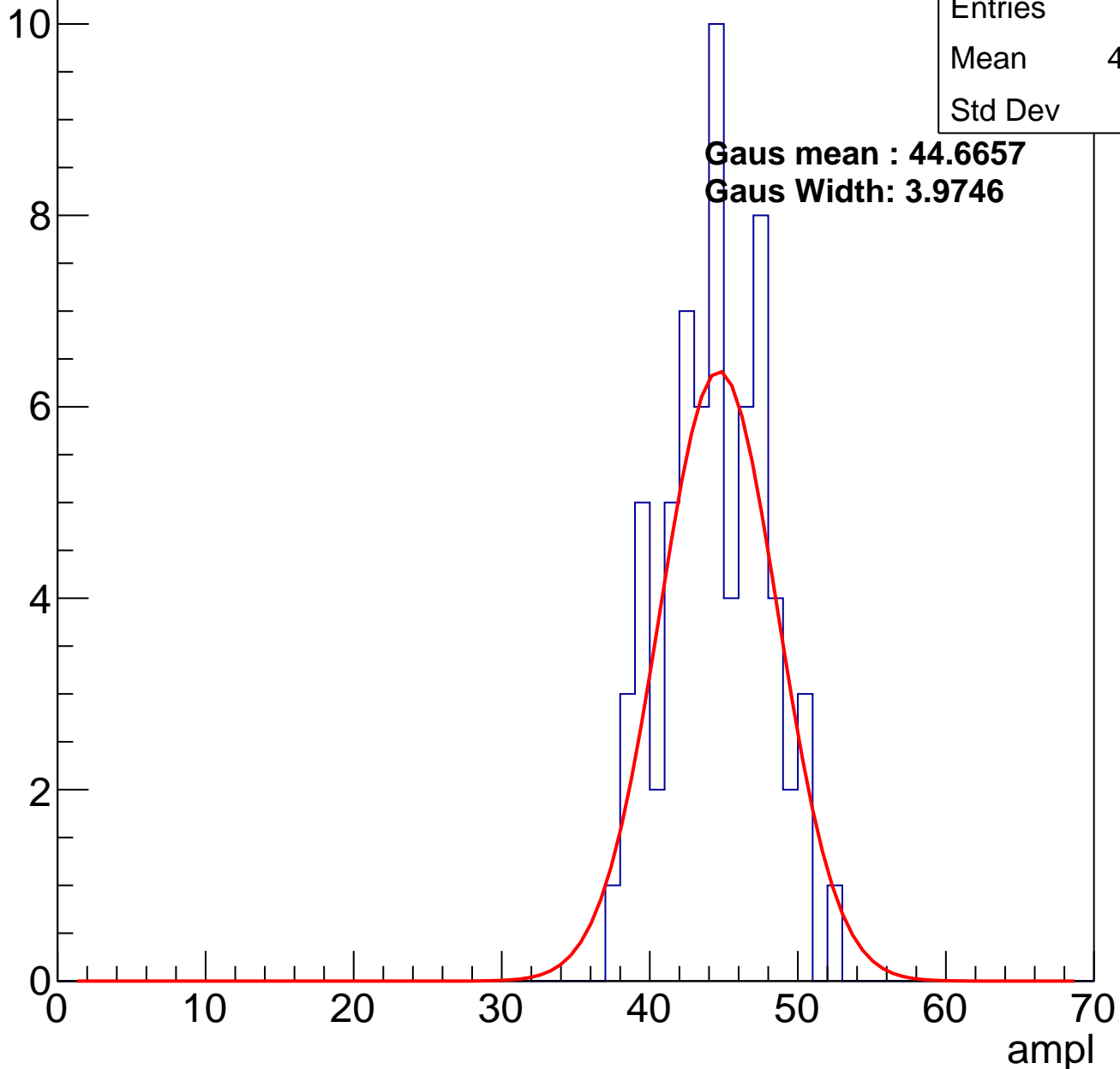
calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	43.99
Std Dev	3.41

Gaus mean : 44.6657

Gaus Width: 3.9746

Entry

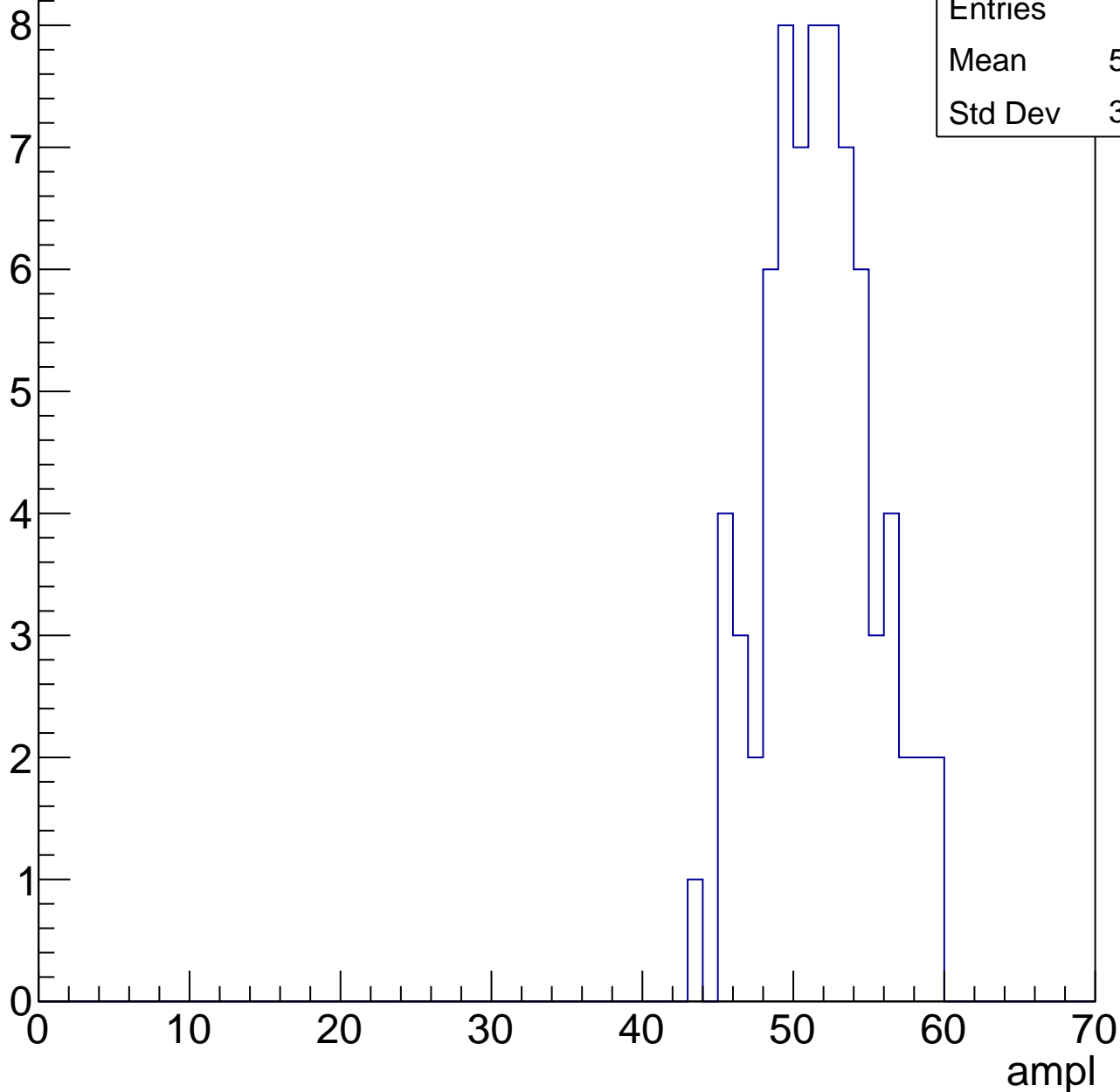


B0L002S, U5-ch58, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	51.25
Std Dev	3.588



B0L002S, U5-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 57.32

Std Dev 2.893

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

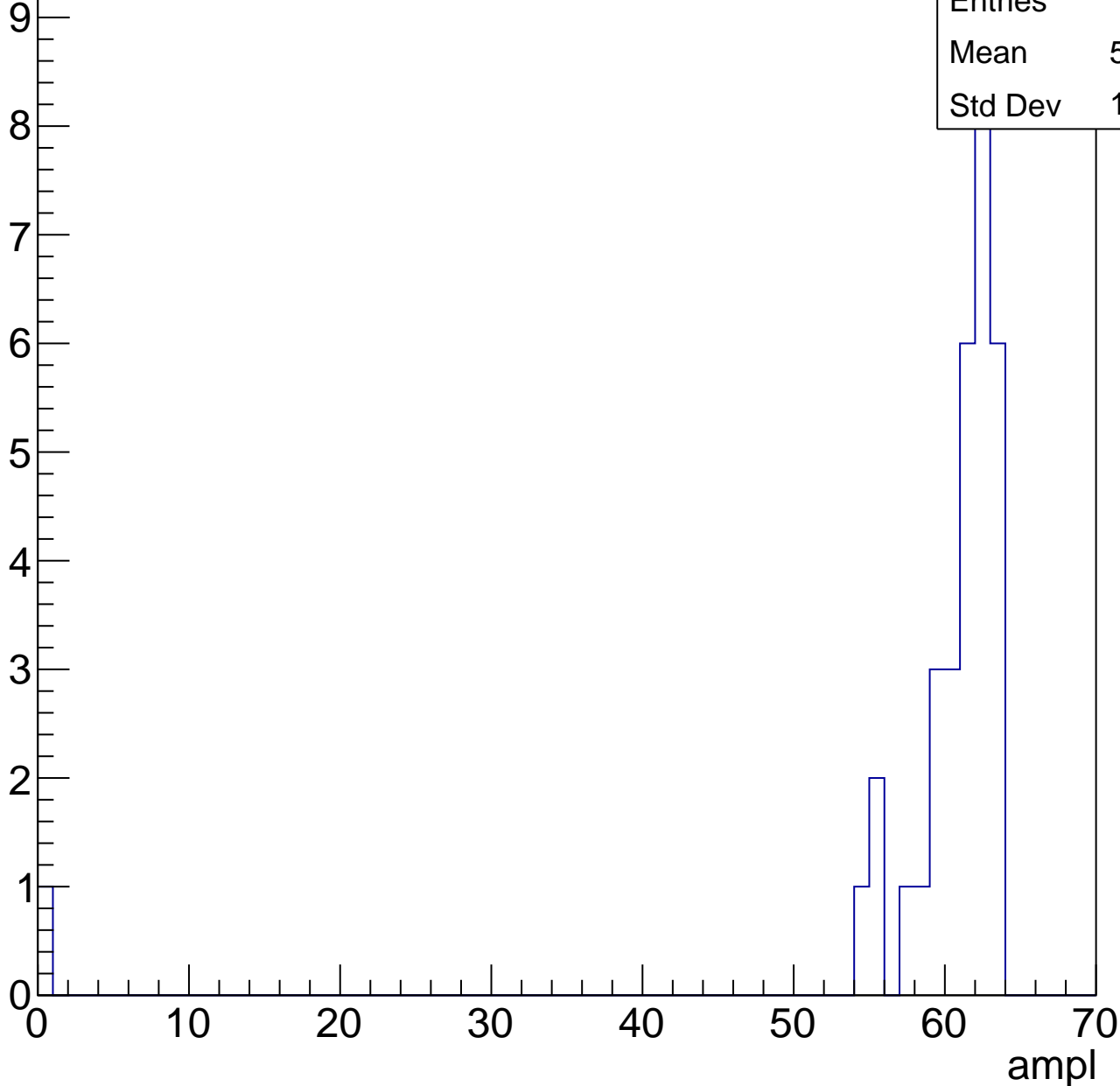
70

B0L002S, U5-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.73
Std Dev	10.65



B0L002S, U5-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch59, adc0

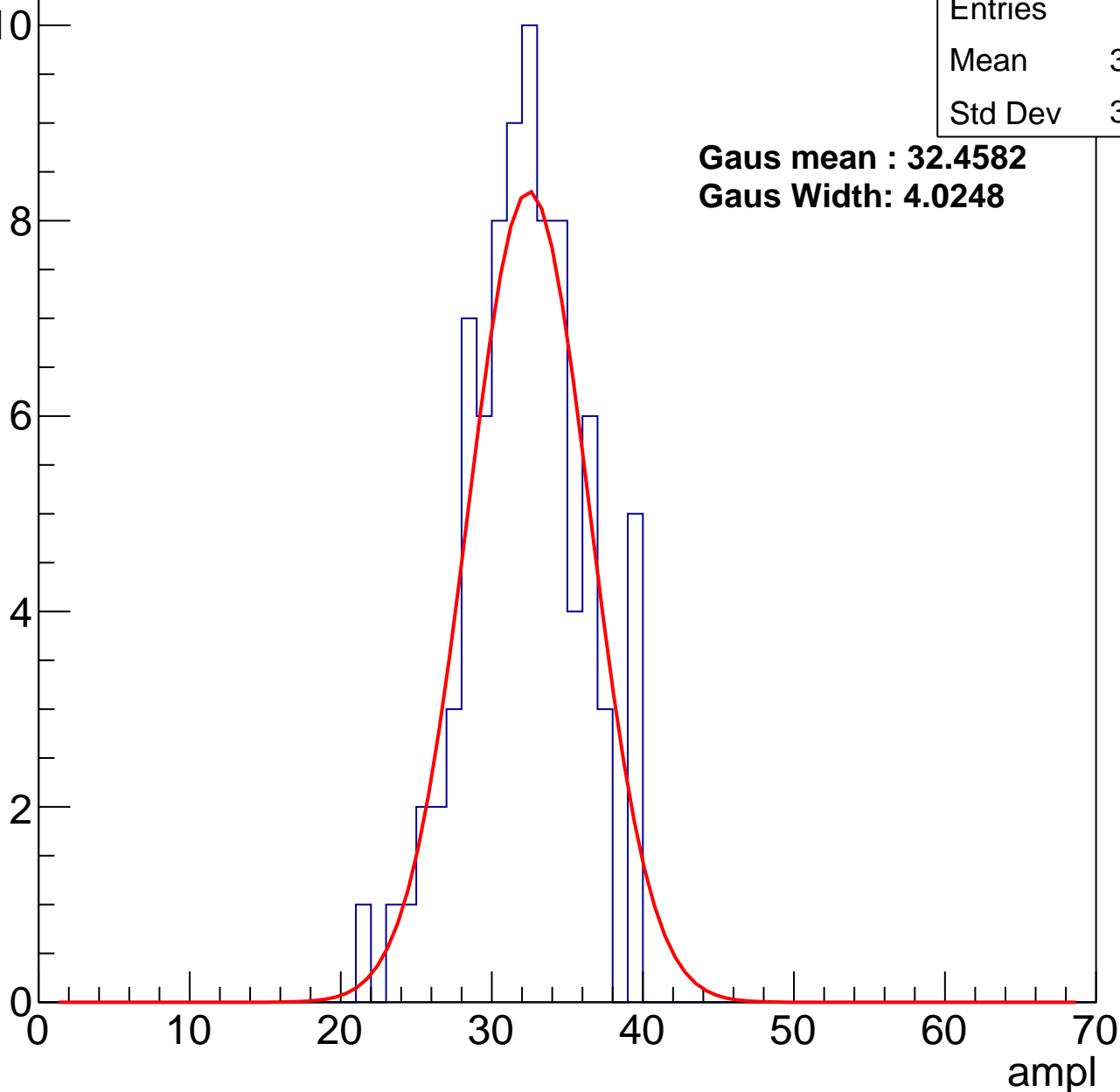
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	31.64
Std Dev	3.779

Gaus mean : 32.4582

Gaus Width: 4.0248



B0L002S, U5-ch59, adc1

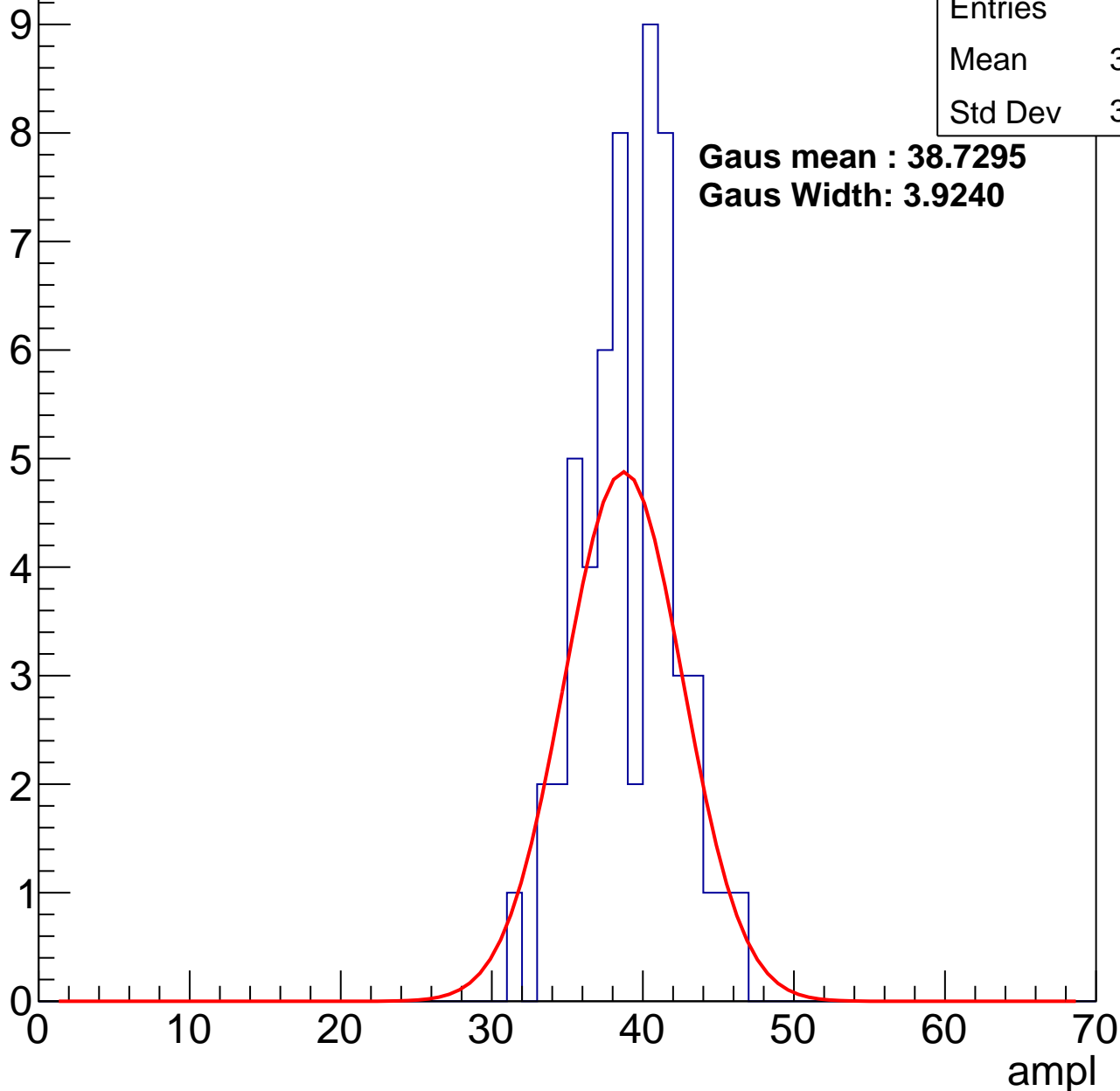
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	38.68
Std Dev	3.146

Gaus mean : 38.7295

Gaus Width: 3.9240



B0L002S, U5-ch59, adc2

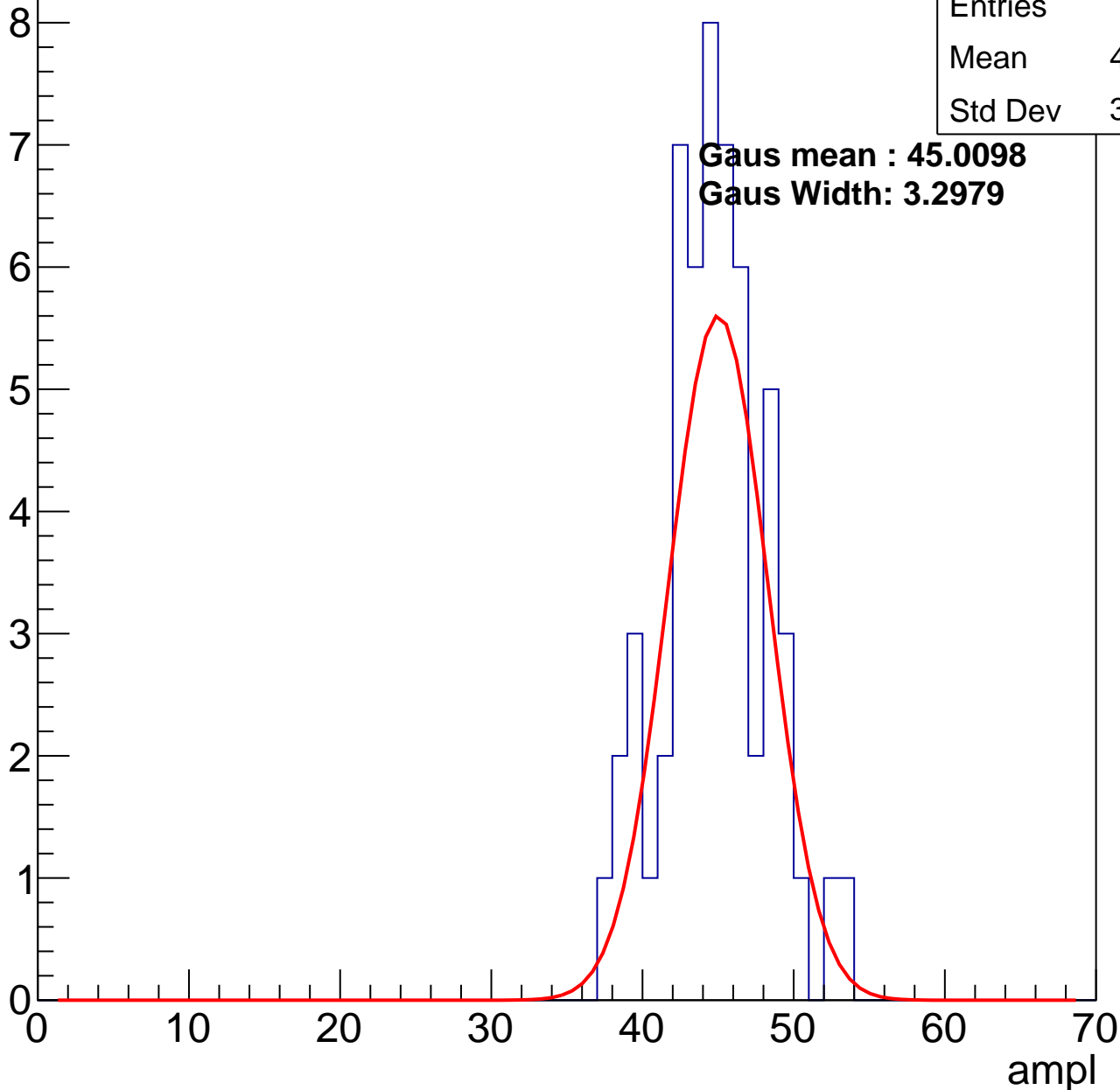
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	44.34
Std Dev	3.387

Gaus mean : 45.0098

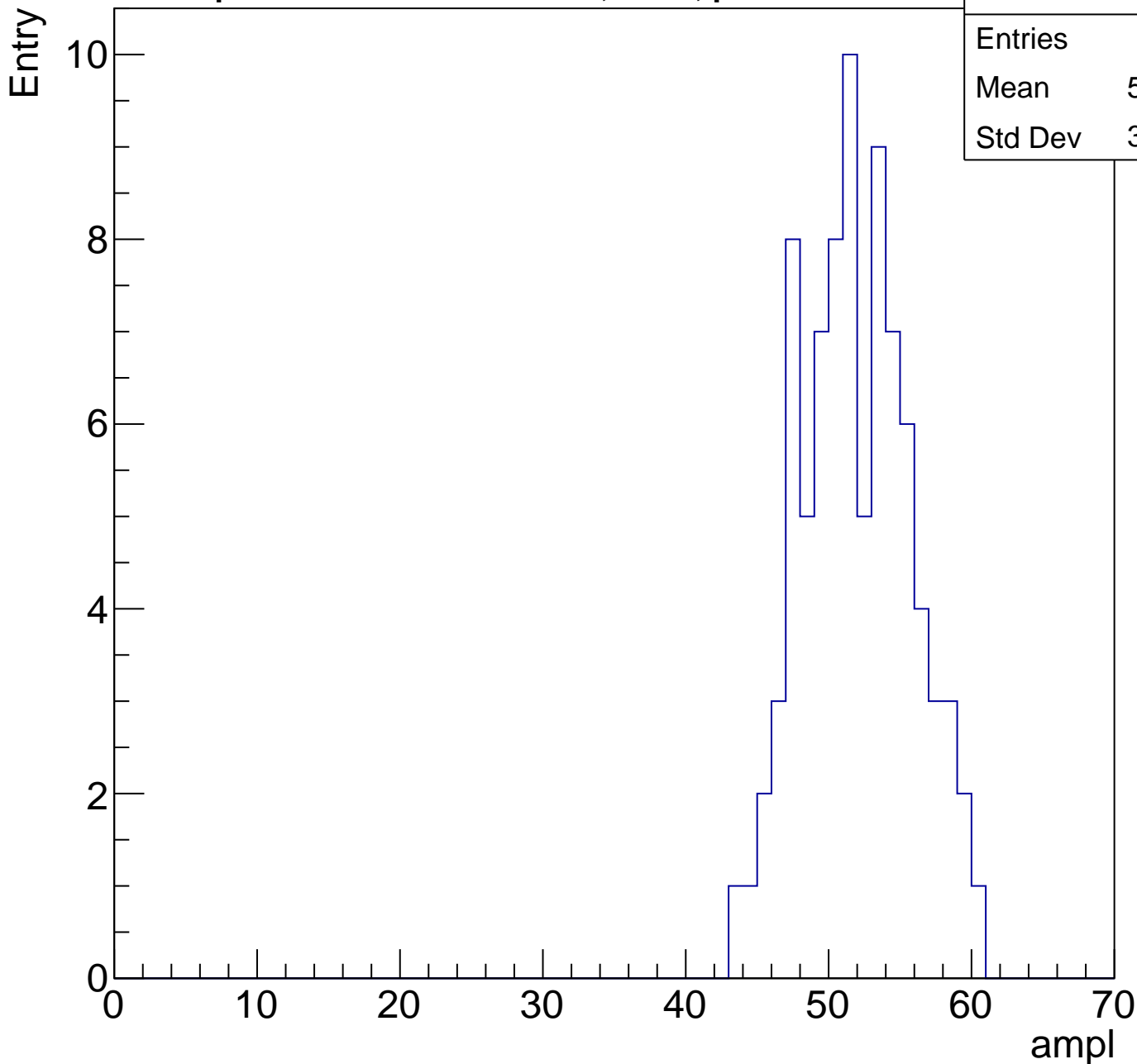
Gaus Width: 3.2979



B0L002S, U5-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	51.48
Std Dev	3.778

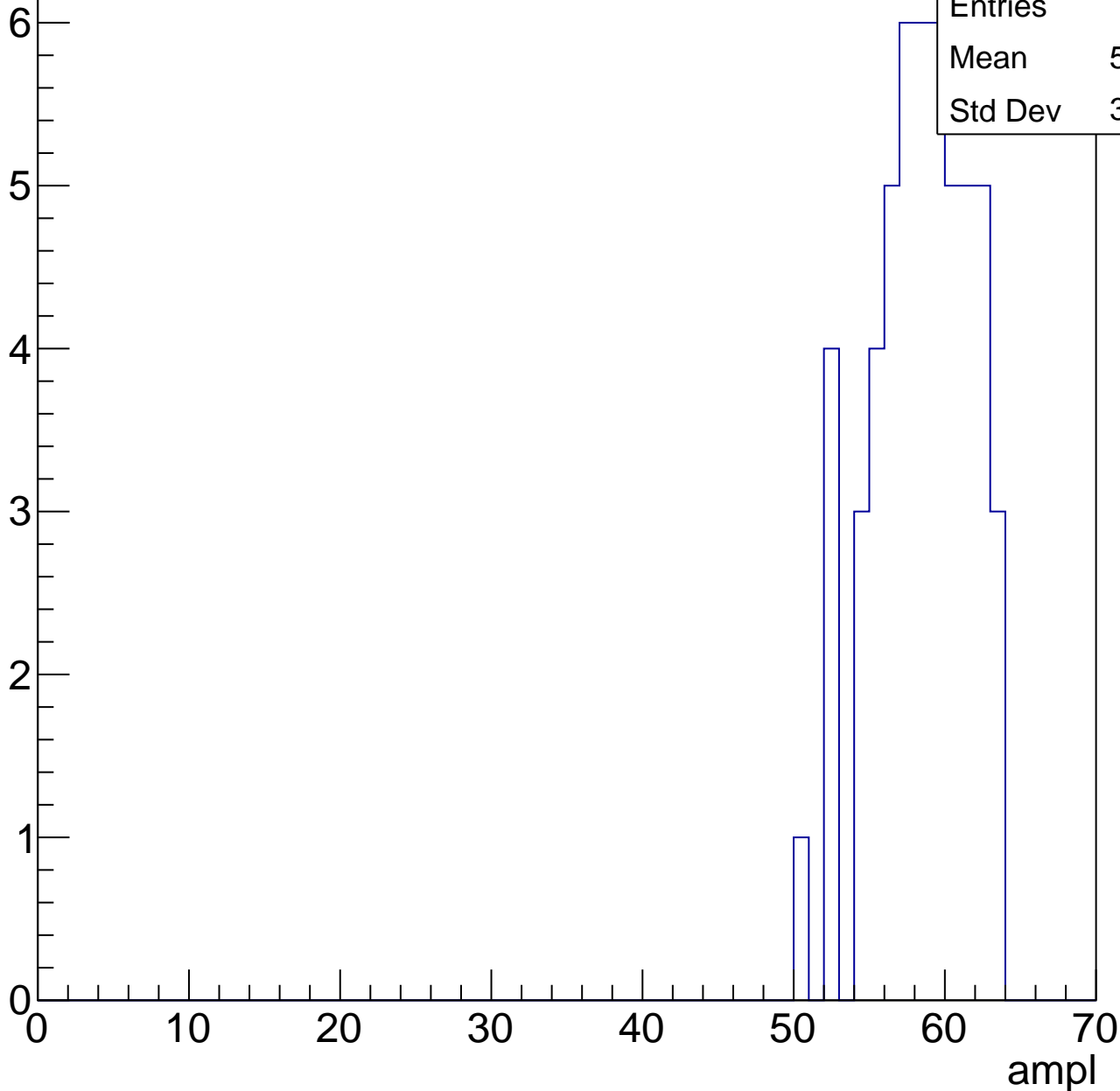


B0L002S, U5-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	57.89
Std Dev	3.202

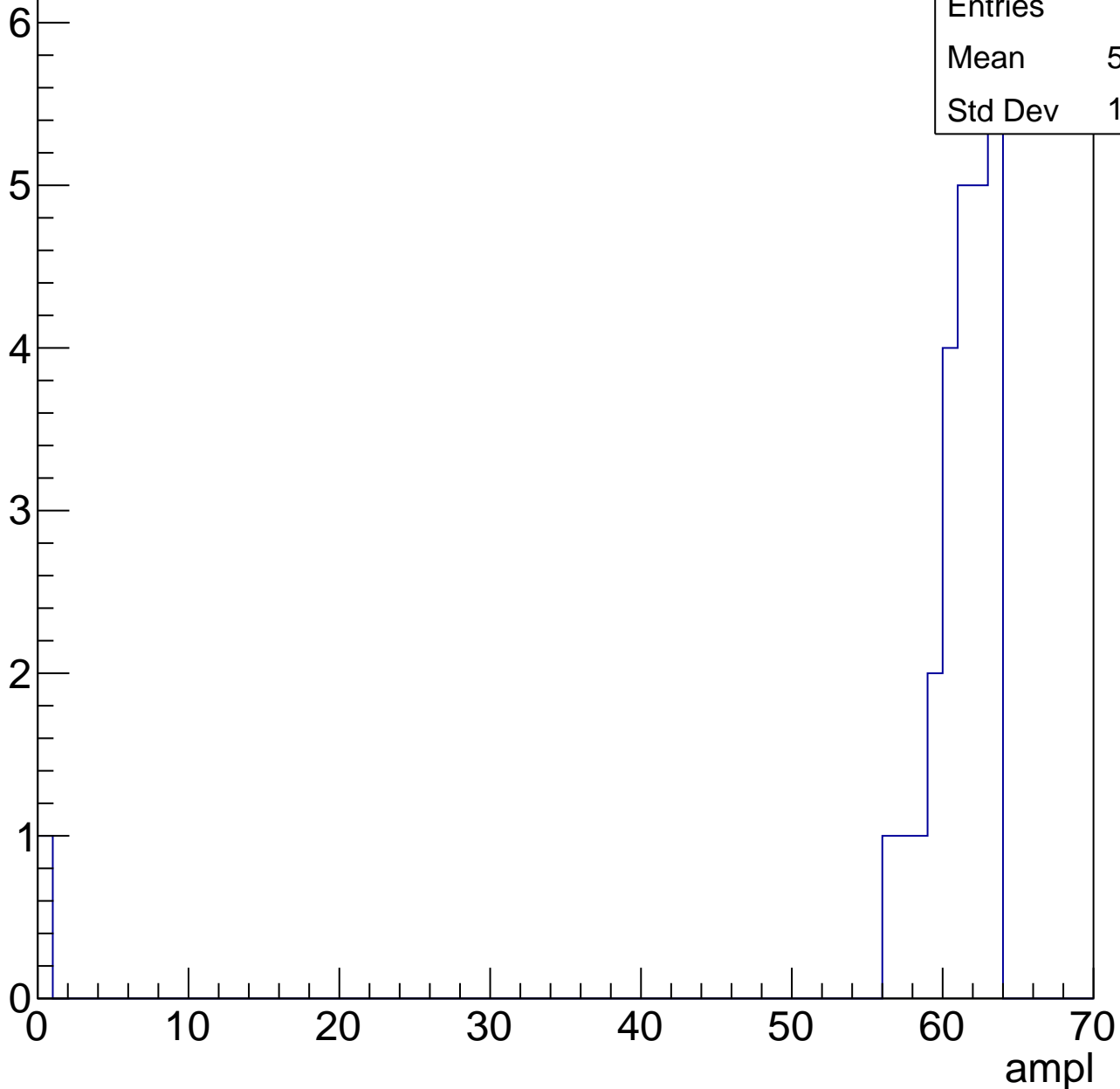


B0L002S, U5-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	58.54
Std Dev	11.86



B0L002S, U5-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L002S, U5-ch60, adc0

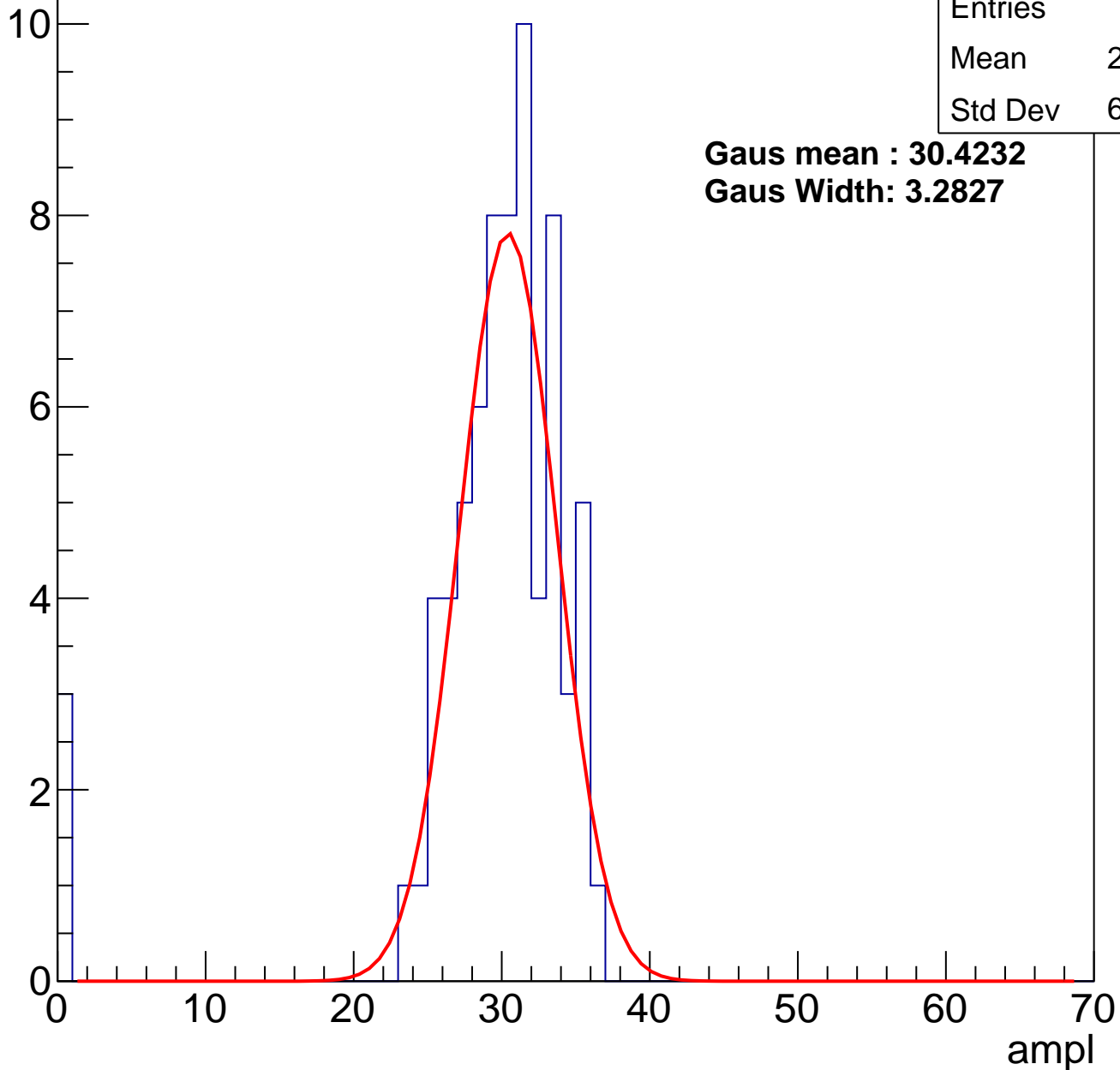
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	28.75
Std Dev	6.736

Gaus mean : 30.4232

Gaus Width: 3.2827

Entry



B0L002S, U5-ch60, adc1

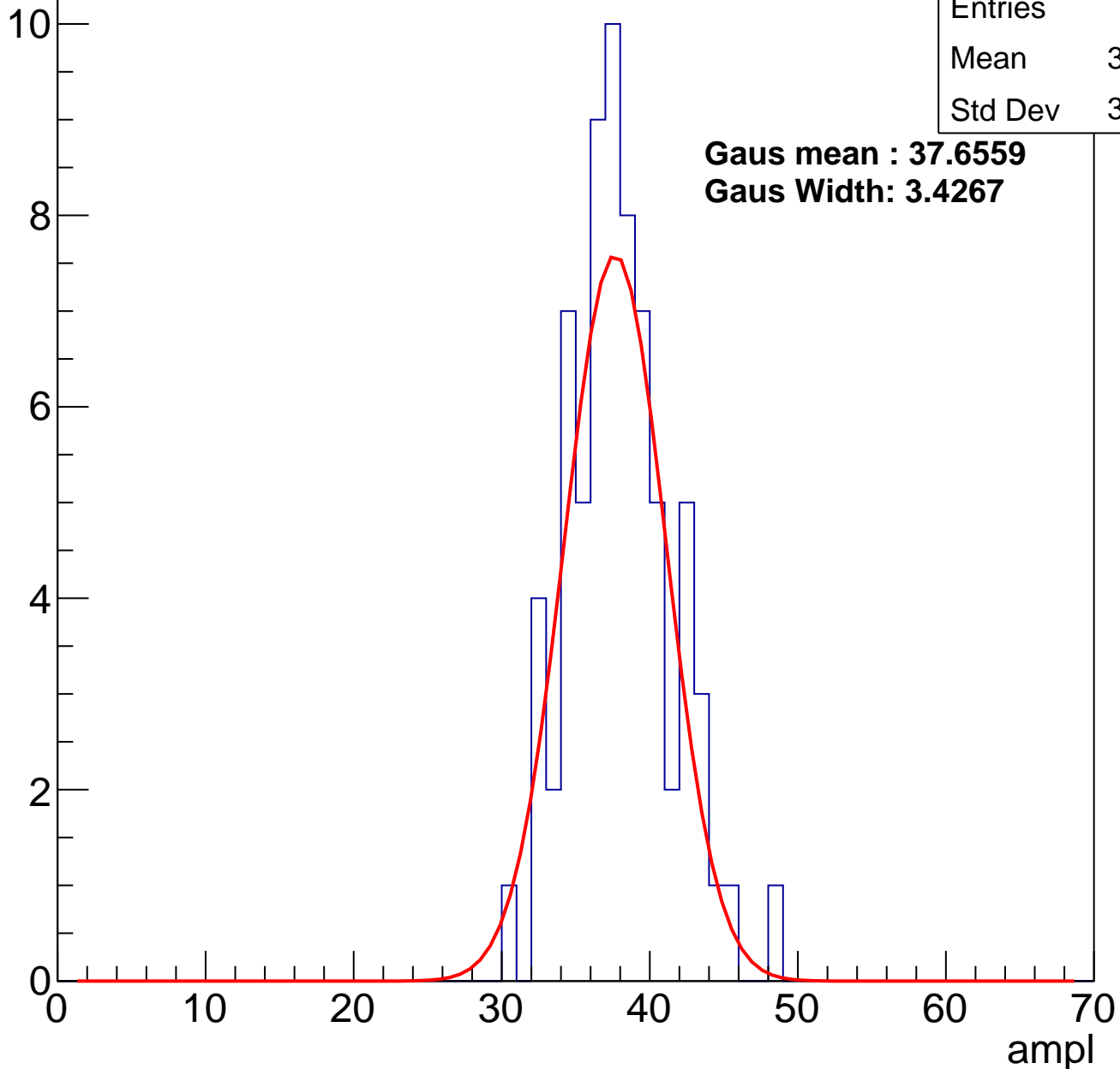
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	37.55
Std Dev	3.422

Gaus mean : 37.6559

Gaus Width: 3.4267

Entry



B0L002S, U5-ch60, adc2

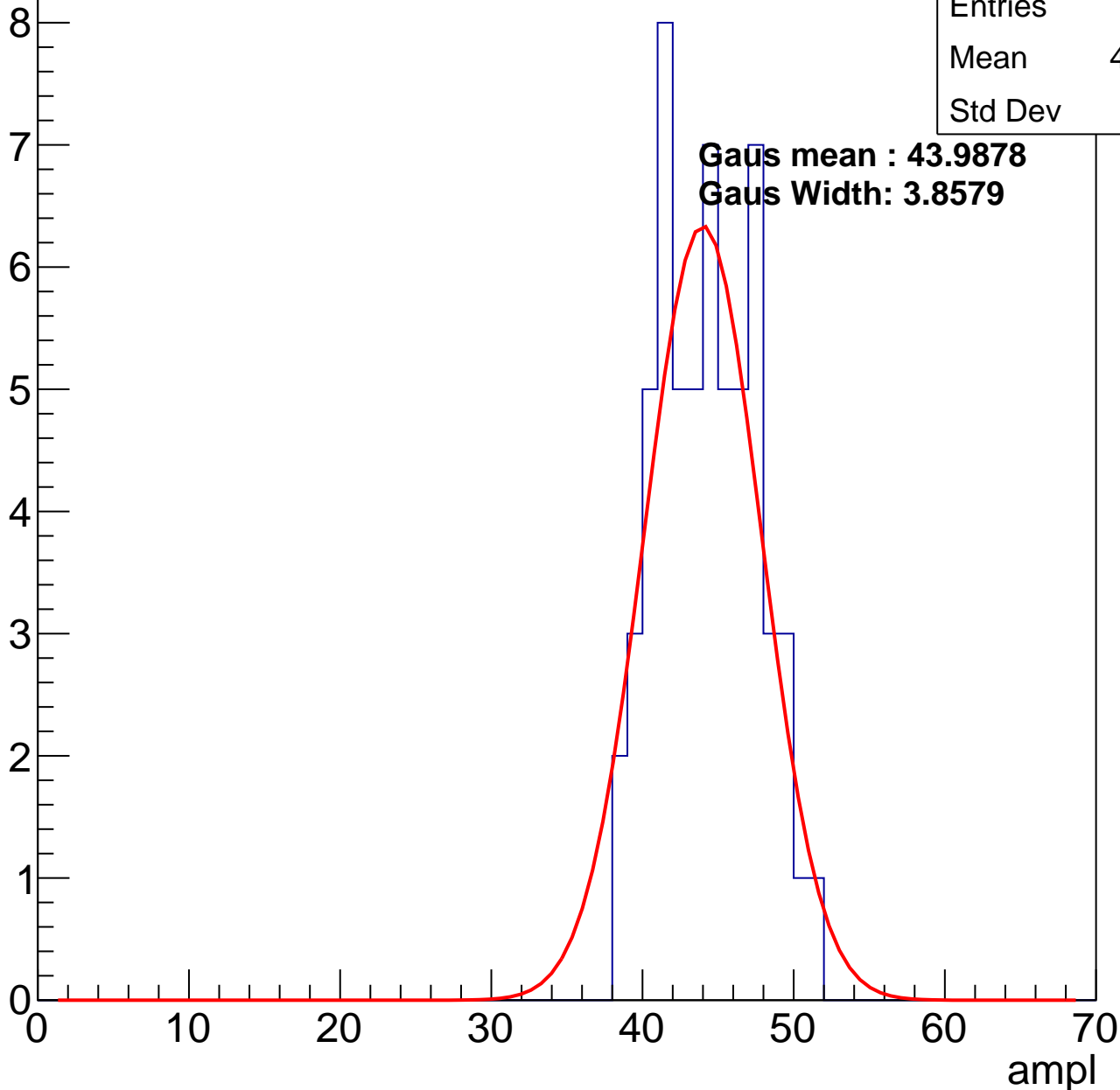
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.83
Std Dev	3.21

Gaus mean : 43.9878

Gaus Width: 3.8579

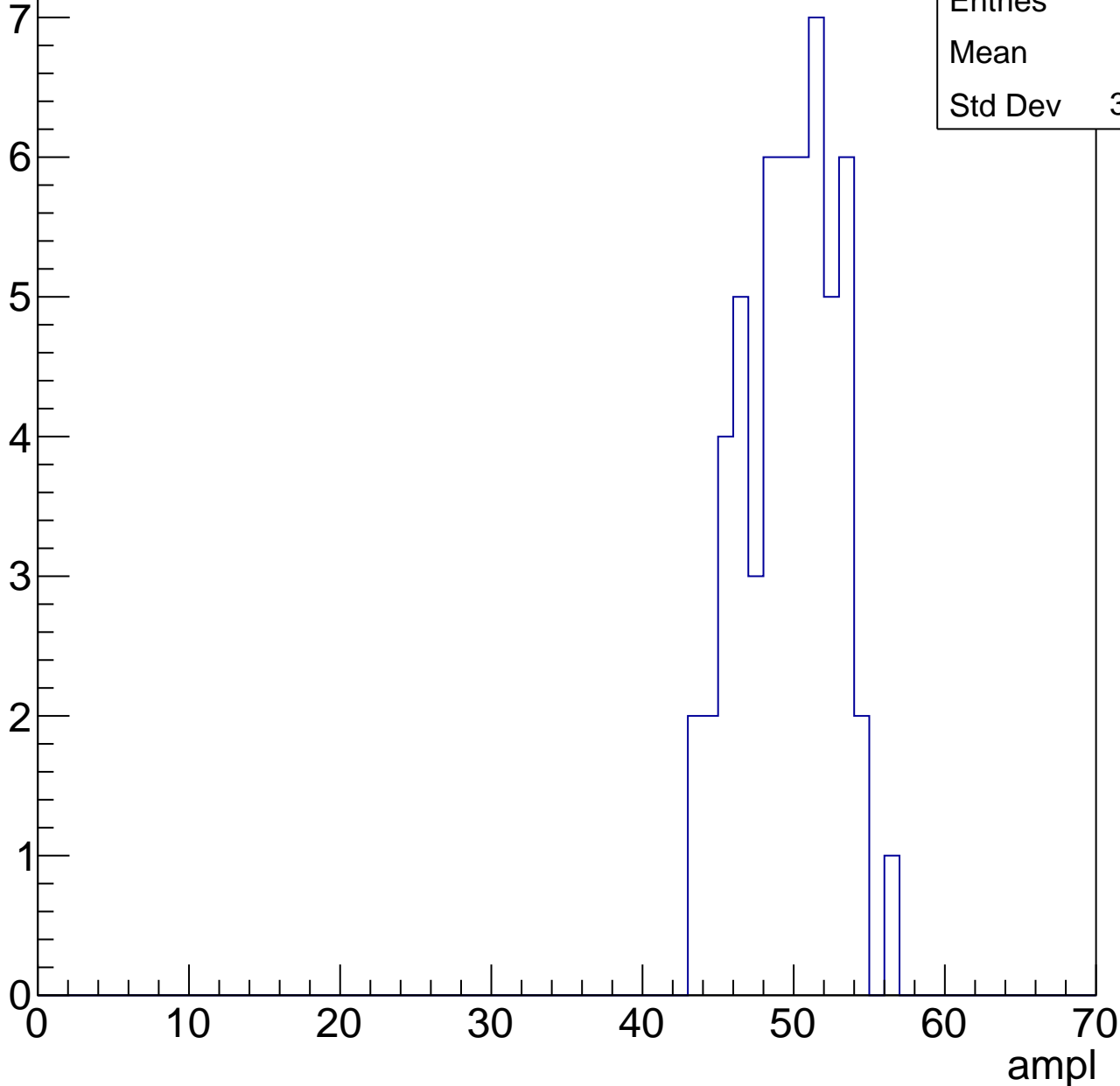


B0L002S, U5-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

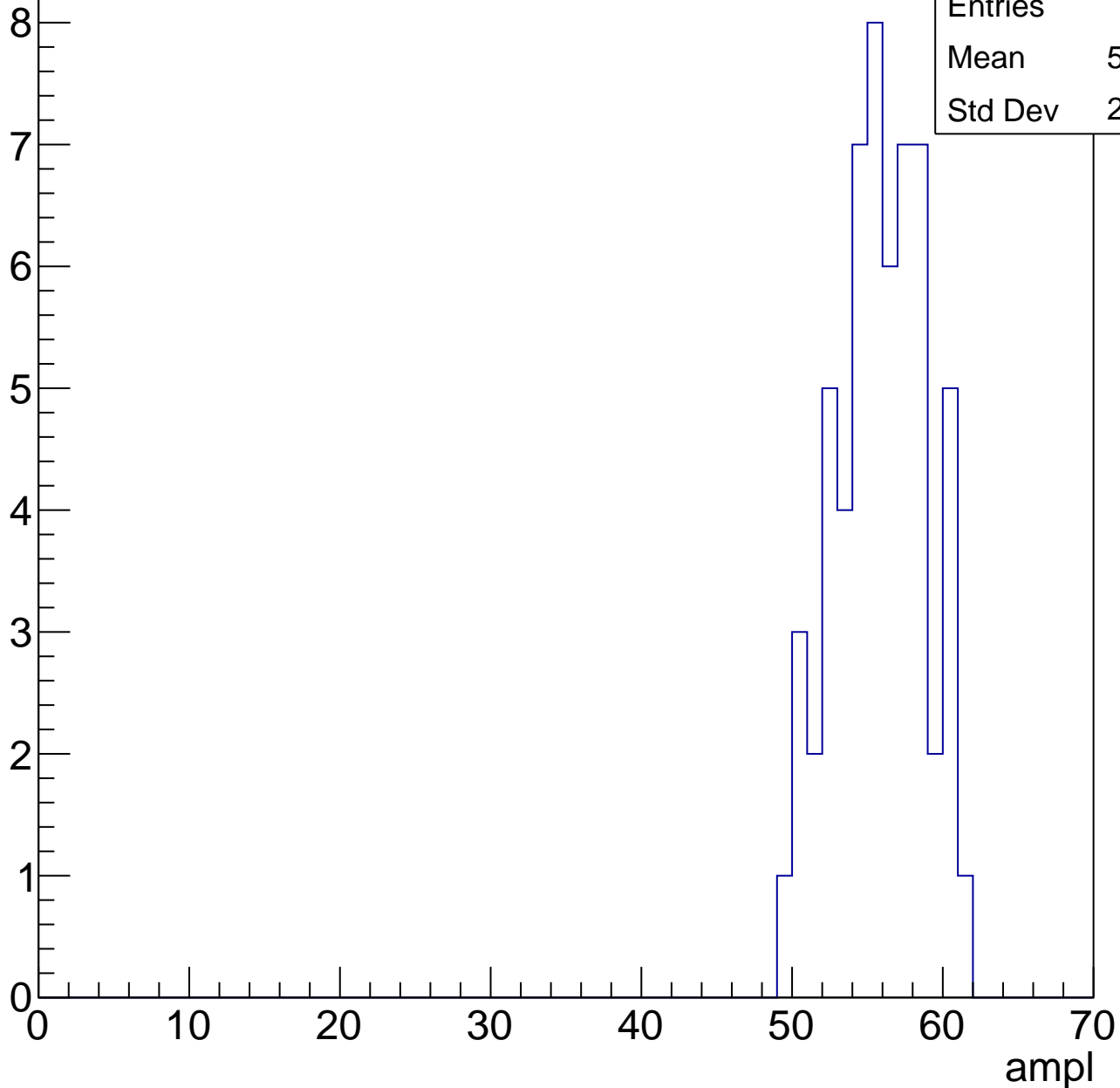
Entries	55
Mean	49.2
Std Dev	3.077



B0L002S, U5-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	58
Mean	55.36
Std Dev	2.917

B0L002S, U5-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

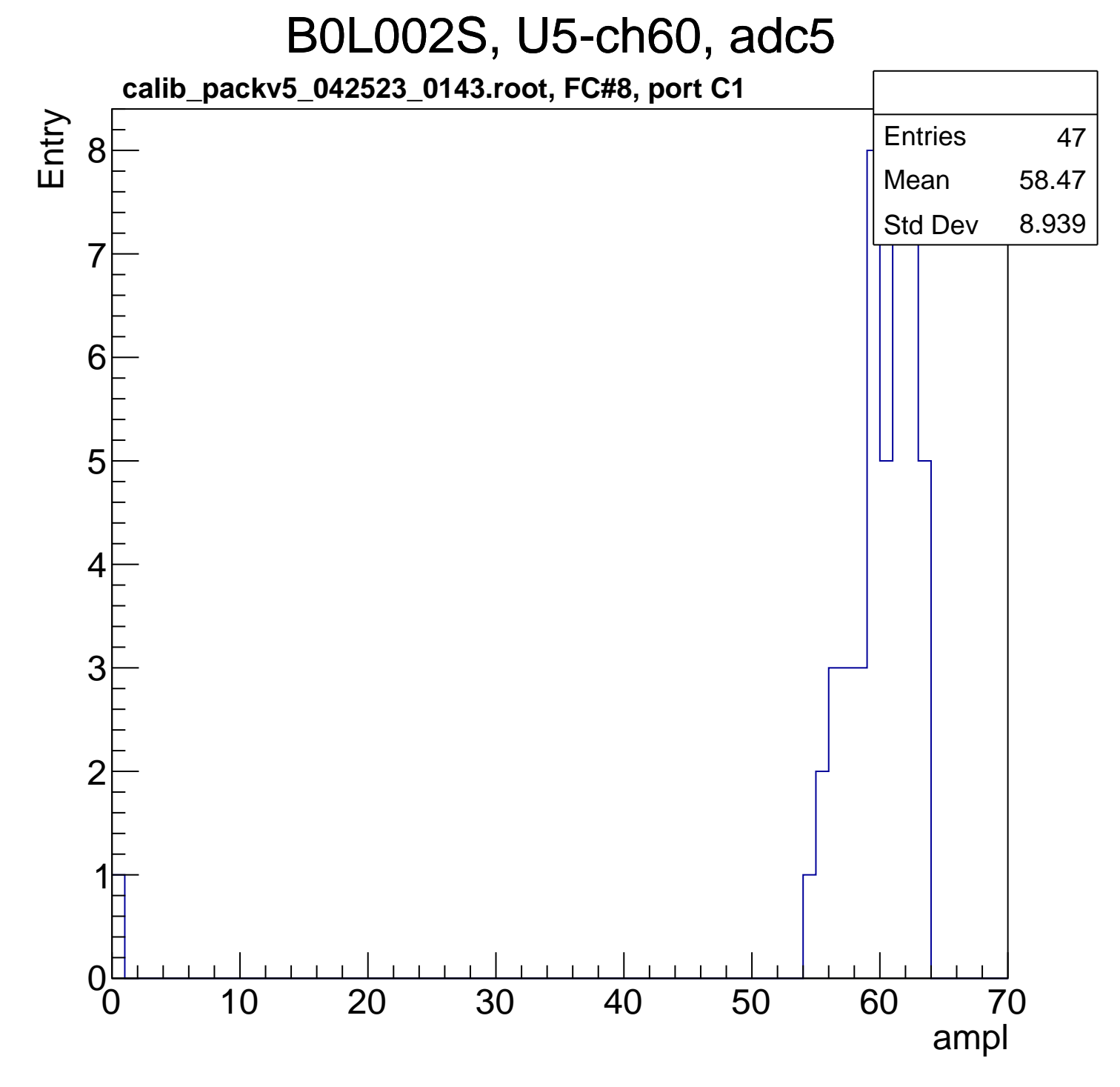
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.47
Std Dev	8.939

ampl

0 10 20 30 40 50 60 70

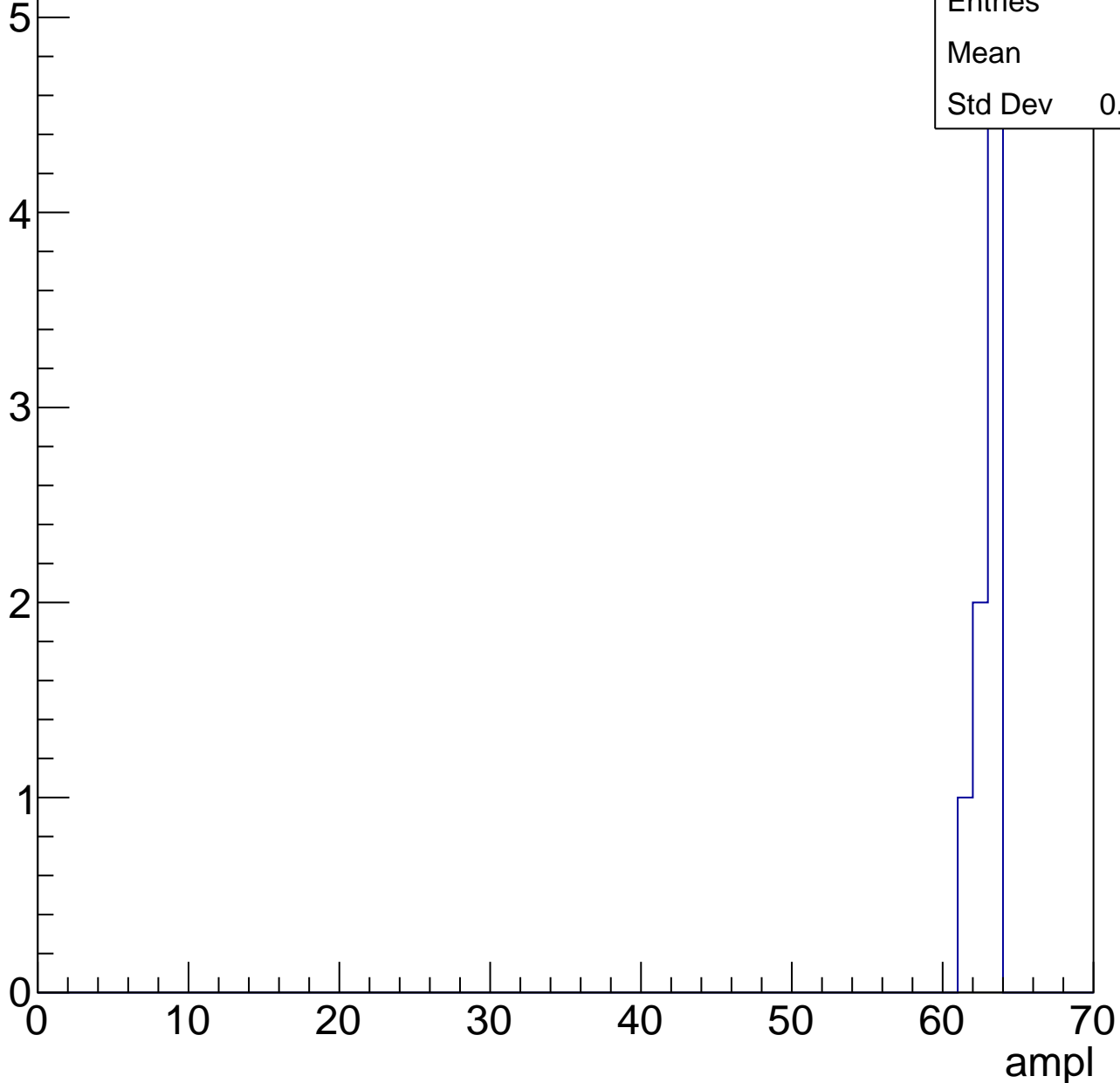


B0L002S, U5-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B0L002S, U5-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch61, adc0

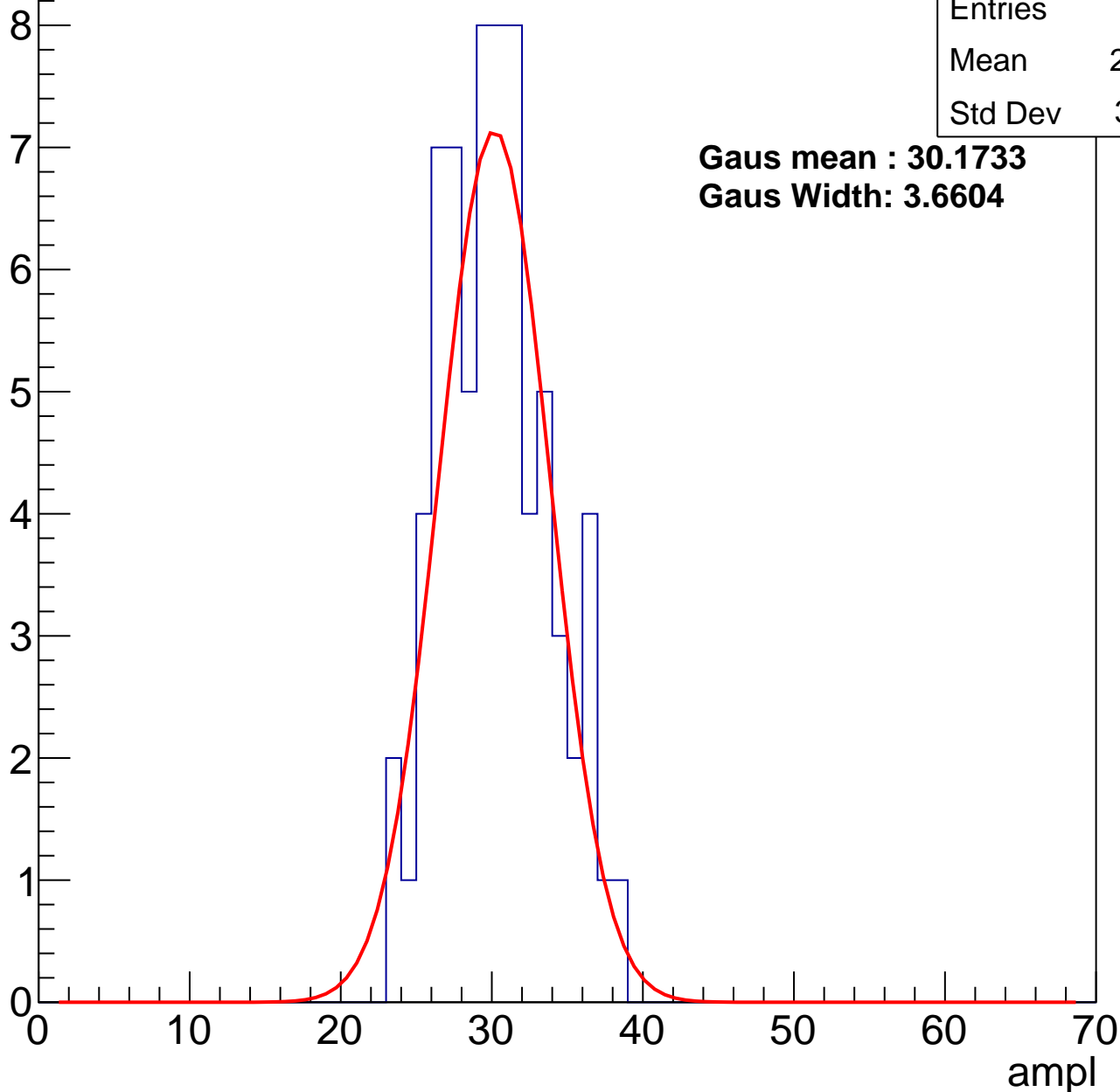
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.79
Std Dev	3.501

Gaus mean : 30.1733

Gaus Width: 3.6604



B0L002S, U5-ch61, adc1

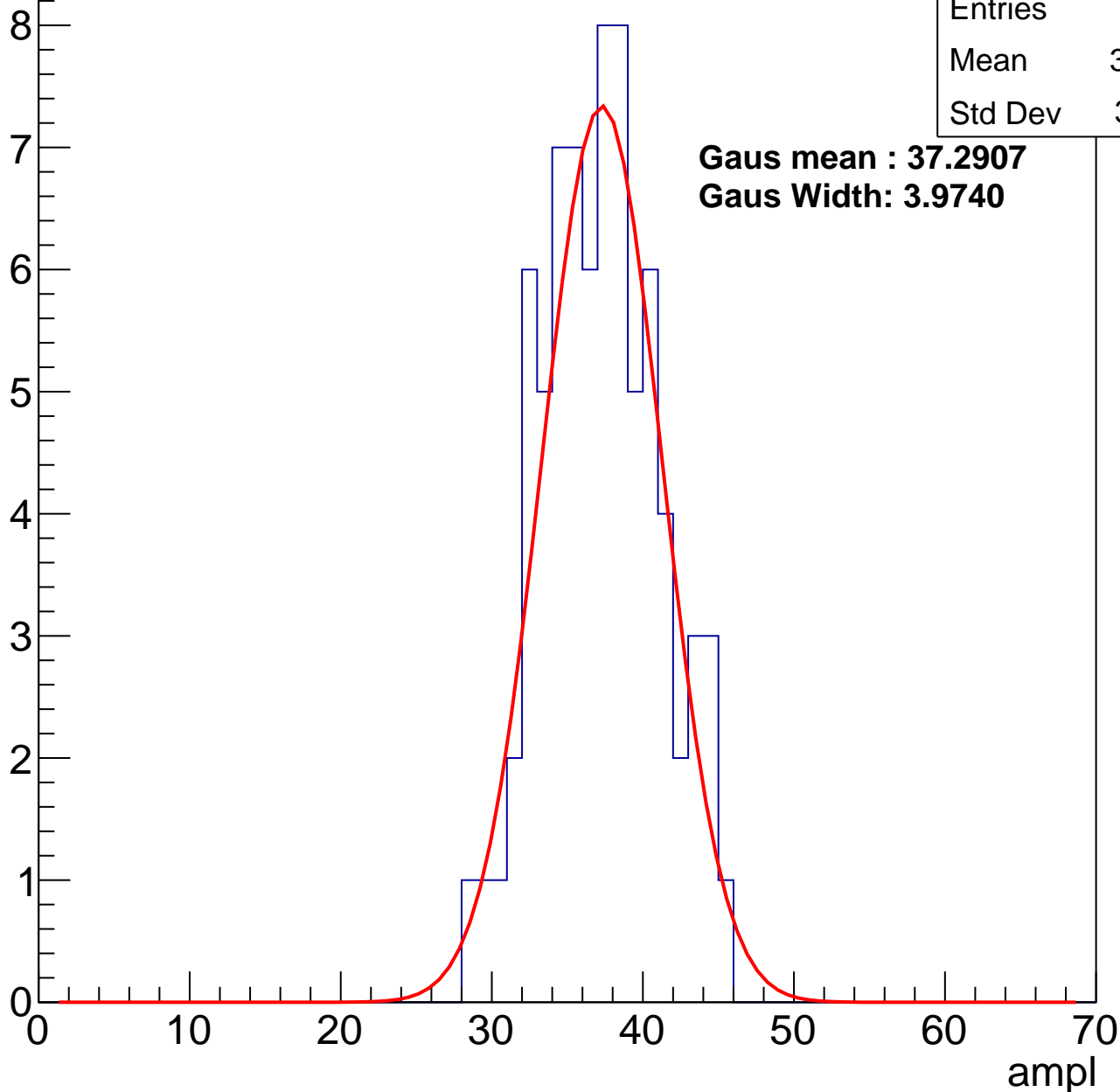
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.76
Std Dev	3.811

Gaus mean : 37.2907

Gaus Width: 3.9740



B0L002S, U5-ch61, adc2

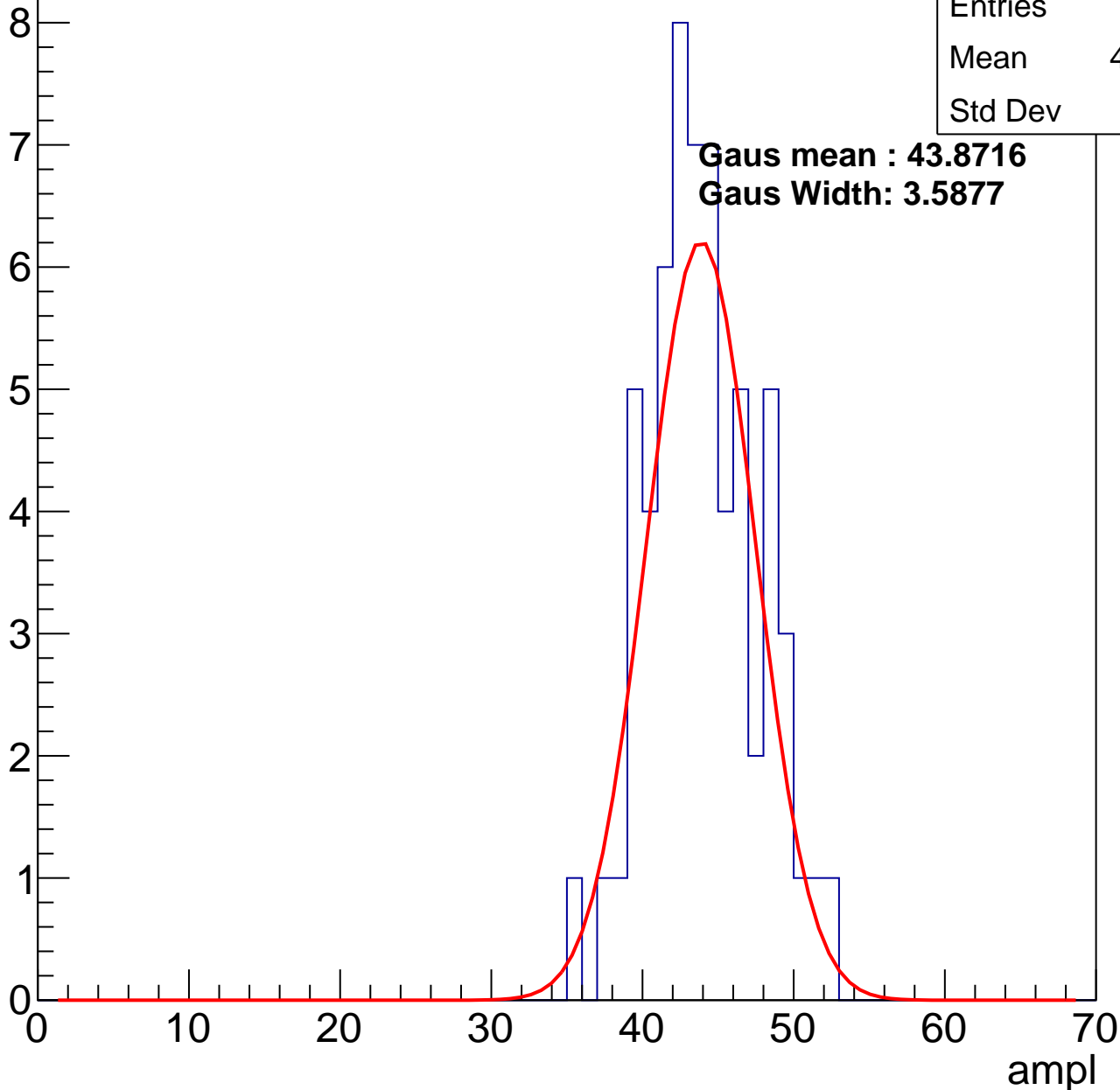
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.55
Std Dev	3.55

Gaus mean : 43.8716

Gaus Width: 3.5877

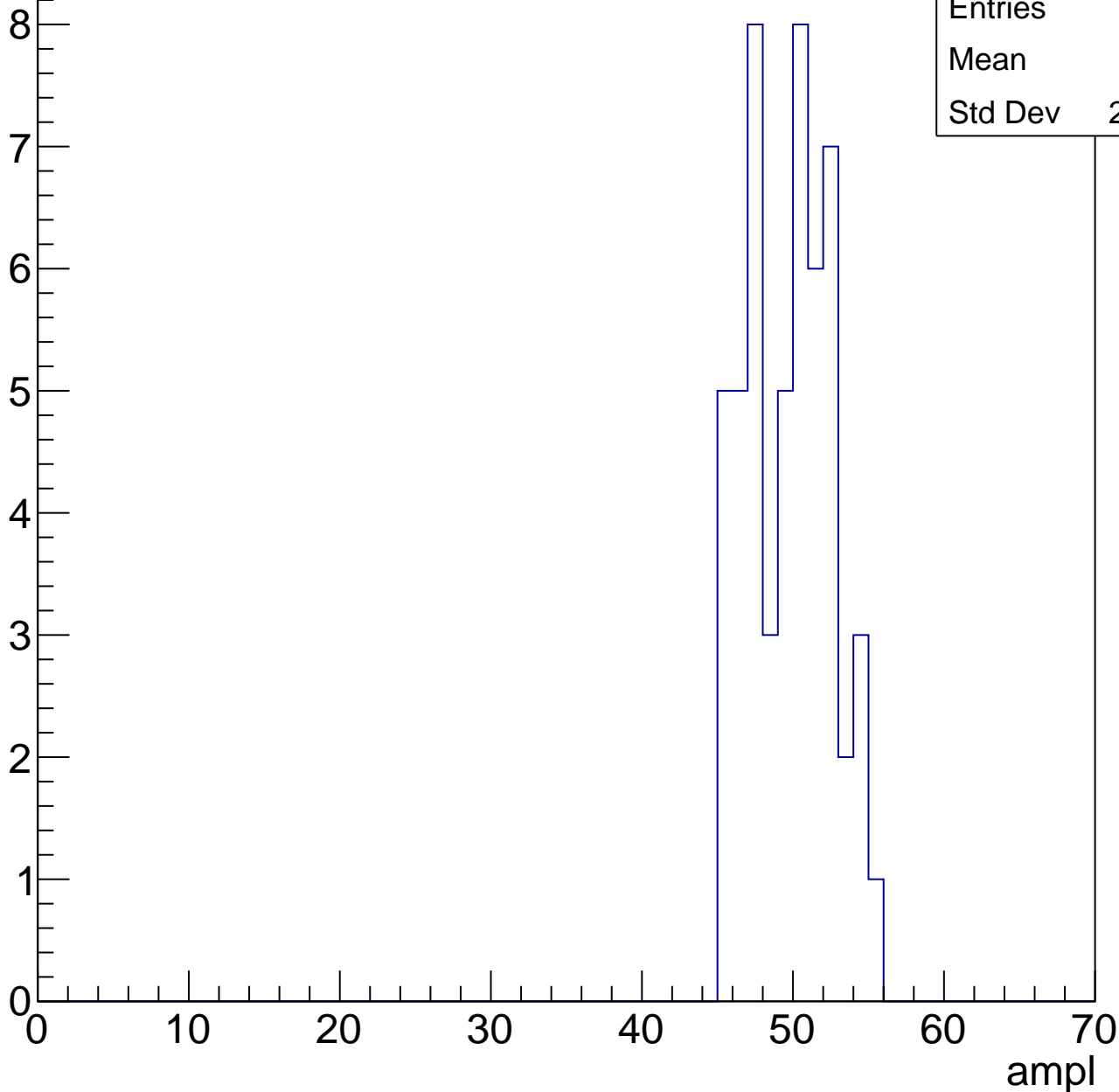


B0L002S, U5-ch61, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	49.3
Std Dev	2.724

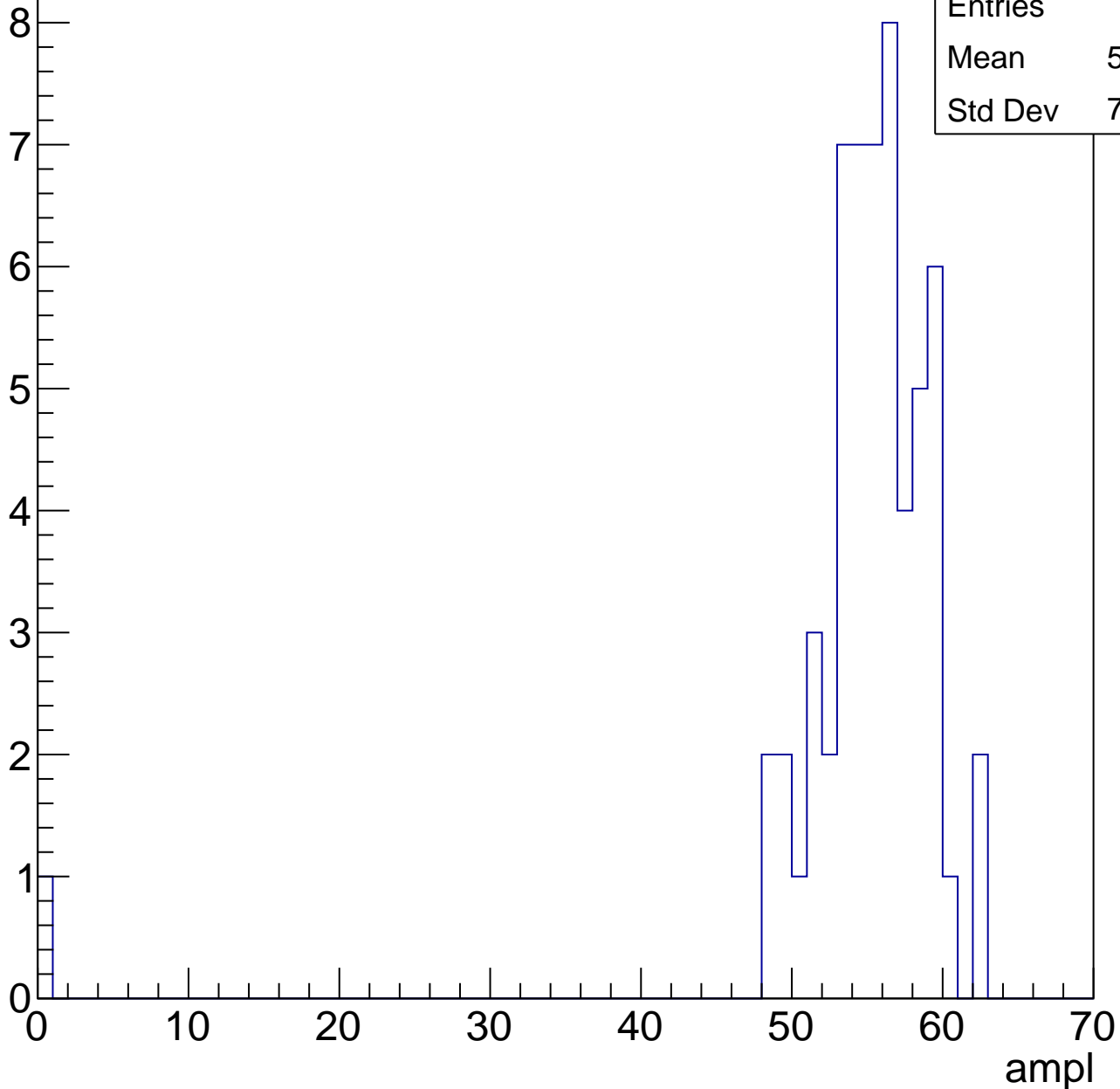


B0L002S, U5-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

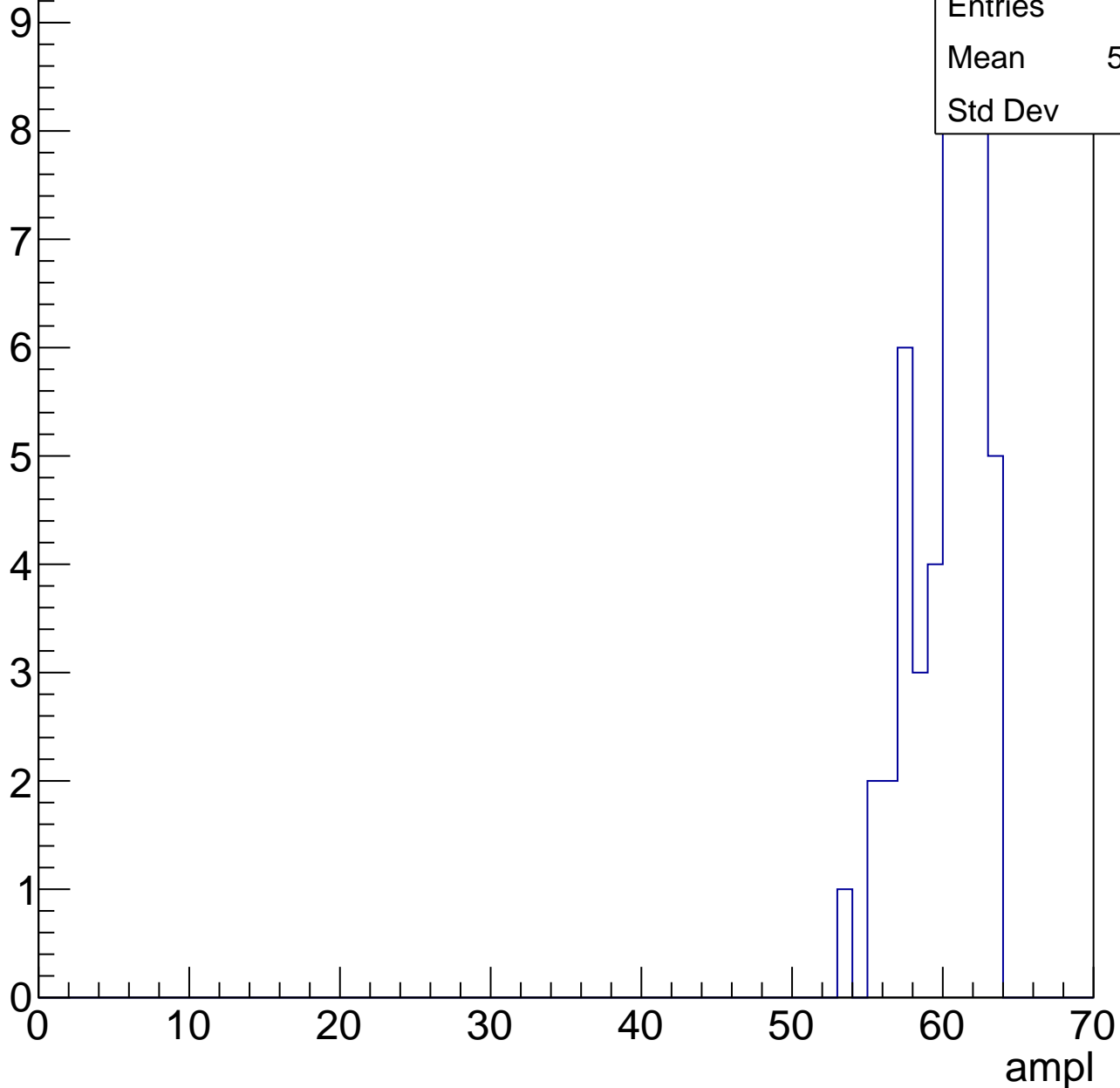
Entries	58
Mean	54.12
Std Dev	7.839



B0L002S, U5-ch61, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



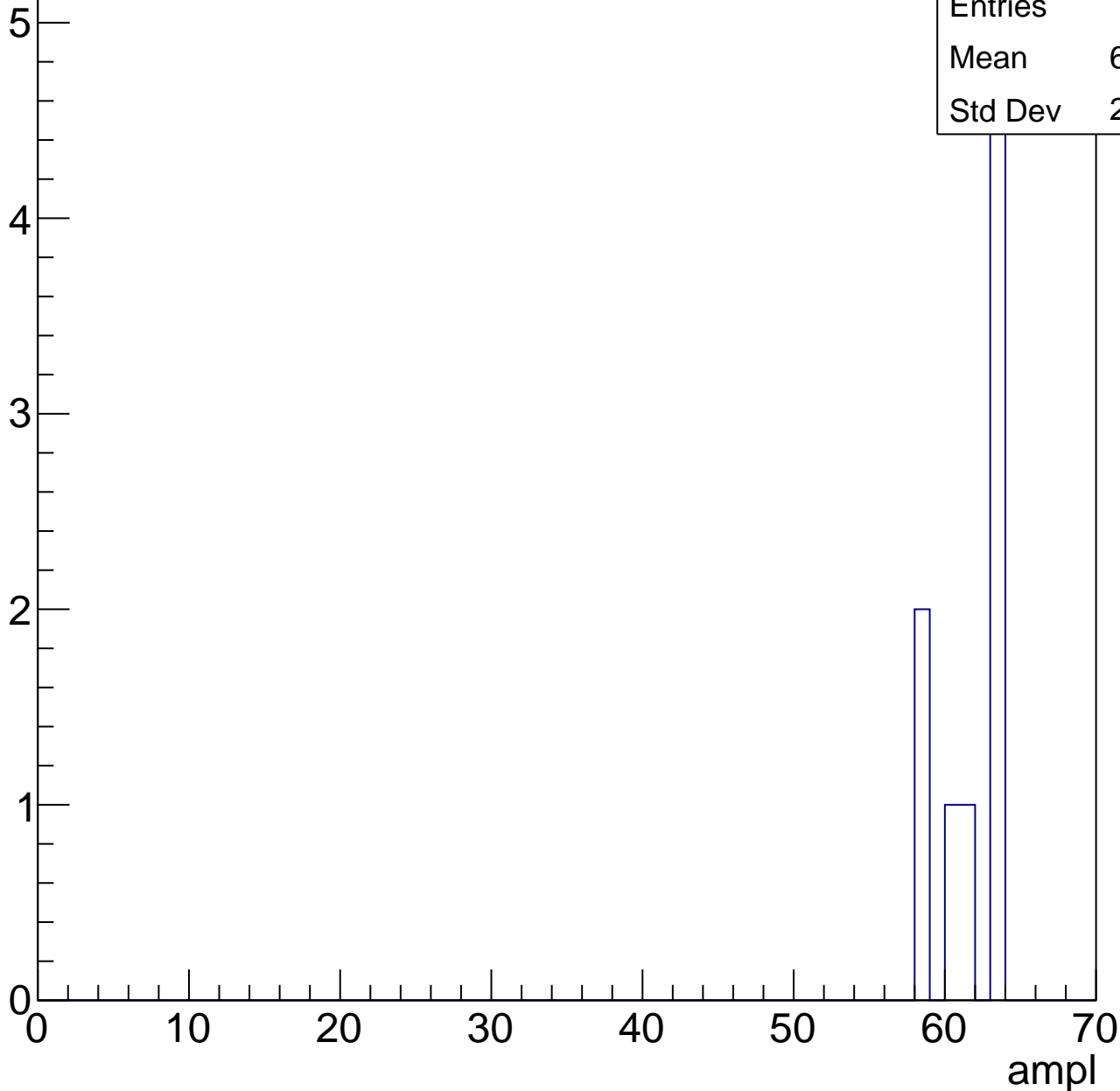
Entries	48
Mean	59.73
Std Dev	2.43

B0L002S, U5-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	61.33
Std Dev	2.055



B0L002S, U5-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L002S, U5-ch62, adc0

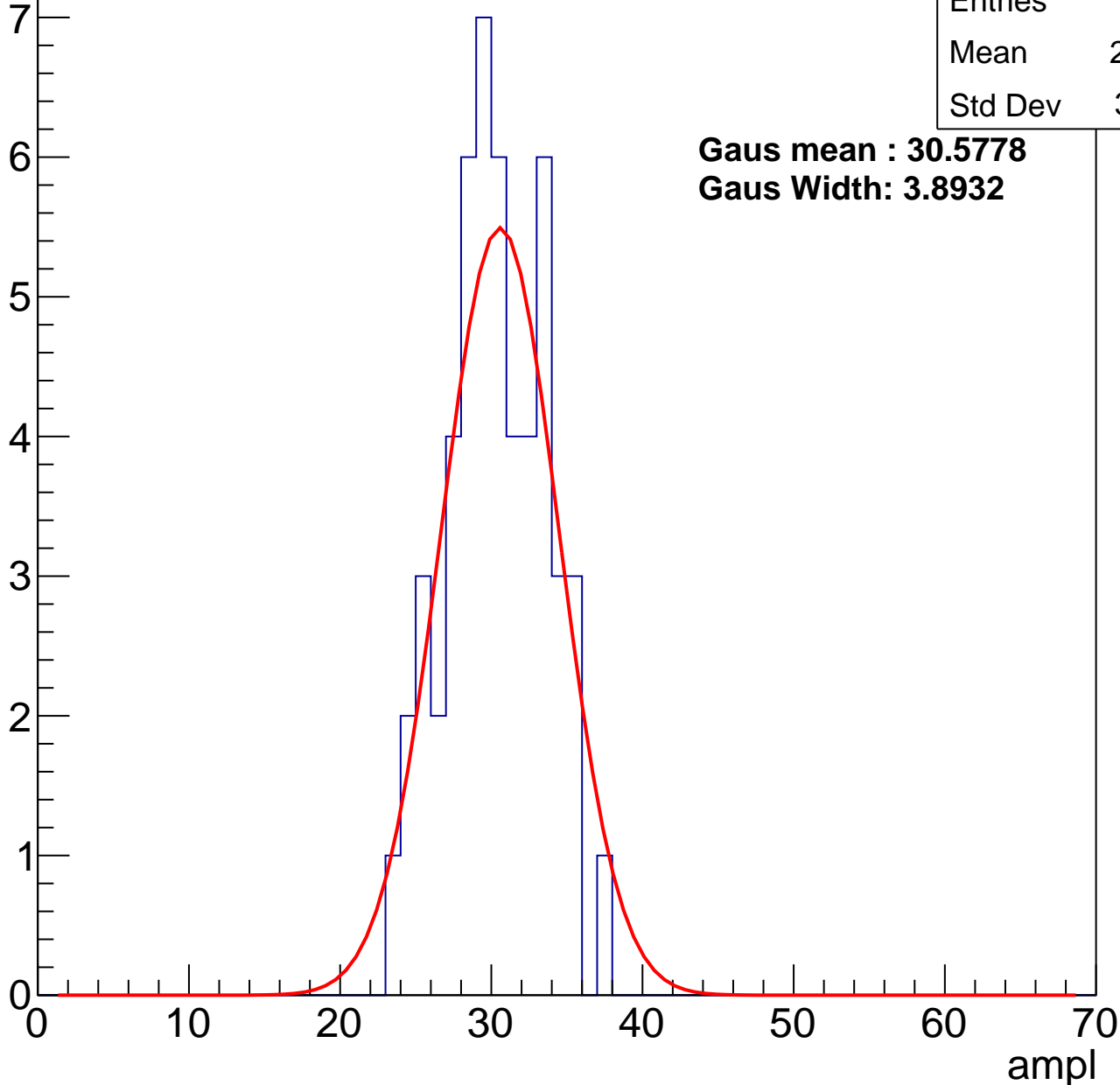
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	29.83
Std Dev	3.221

Gaus mean : 30.5778

Gaus Width: 3.8932



B0L002S, U5-ch62, adc1

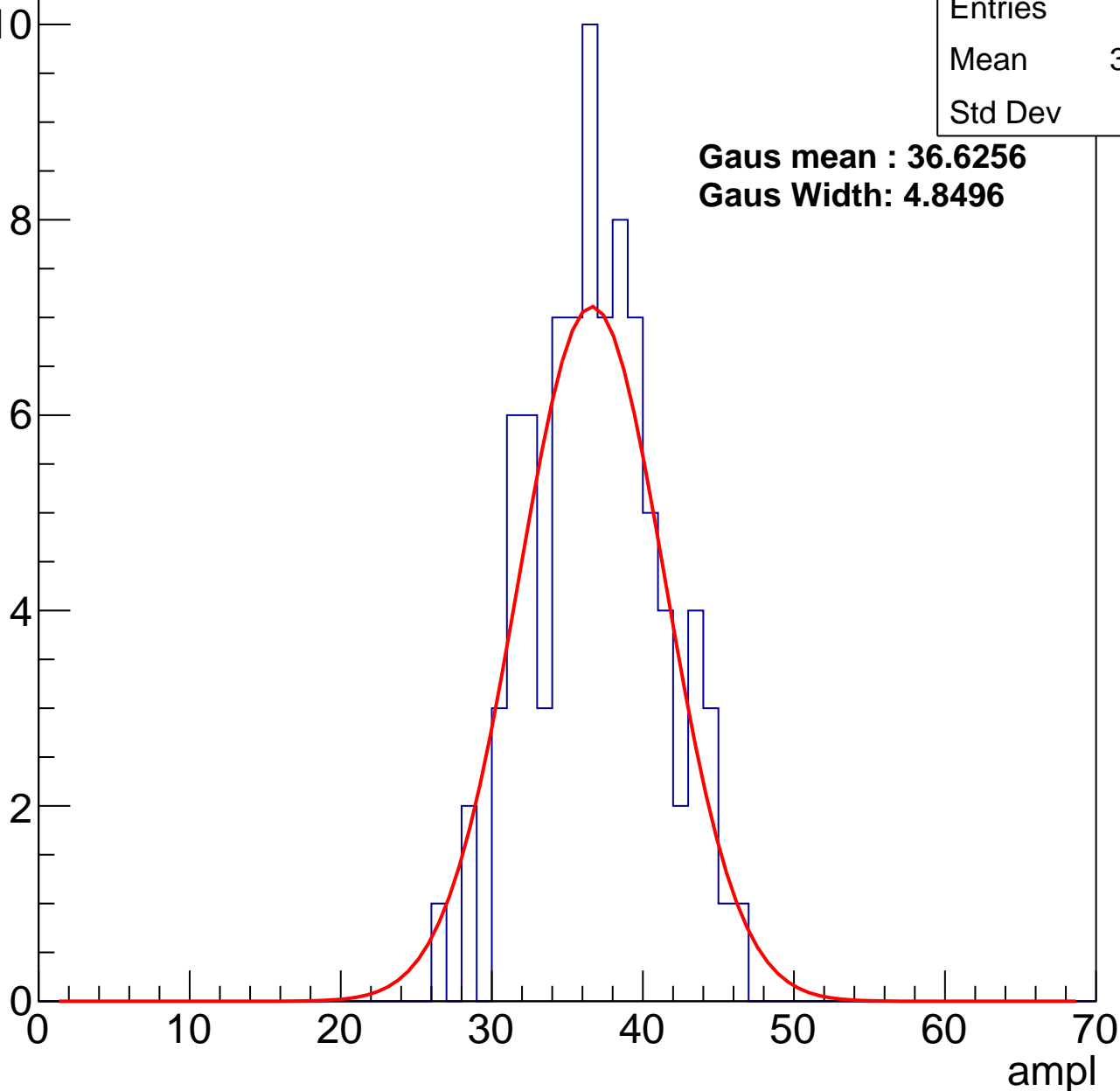
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	36.45
Std Dev	4.22

Gaus mean : 36.6256

Gaus Width: 4.8496

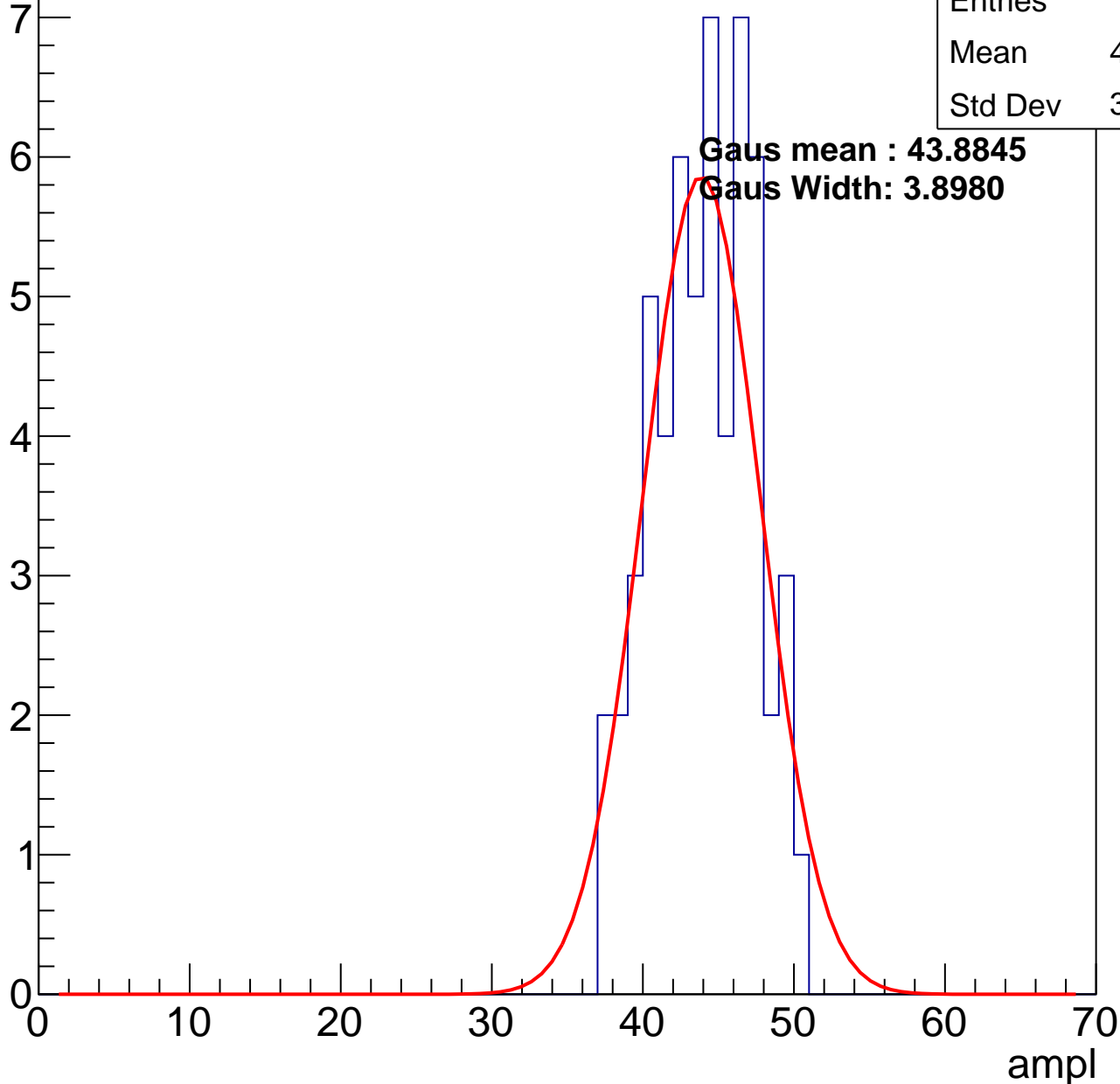


B0L002S, U5-ch62, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.56
Std Dev	3.266

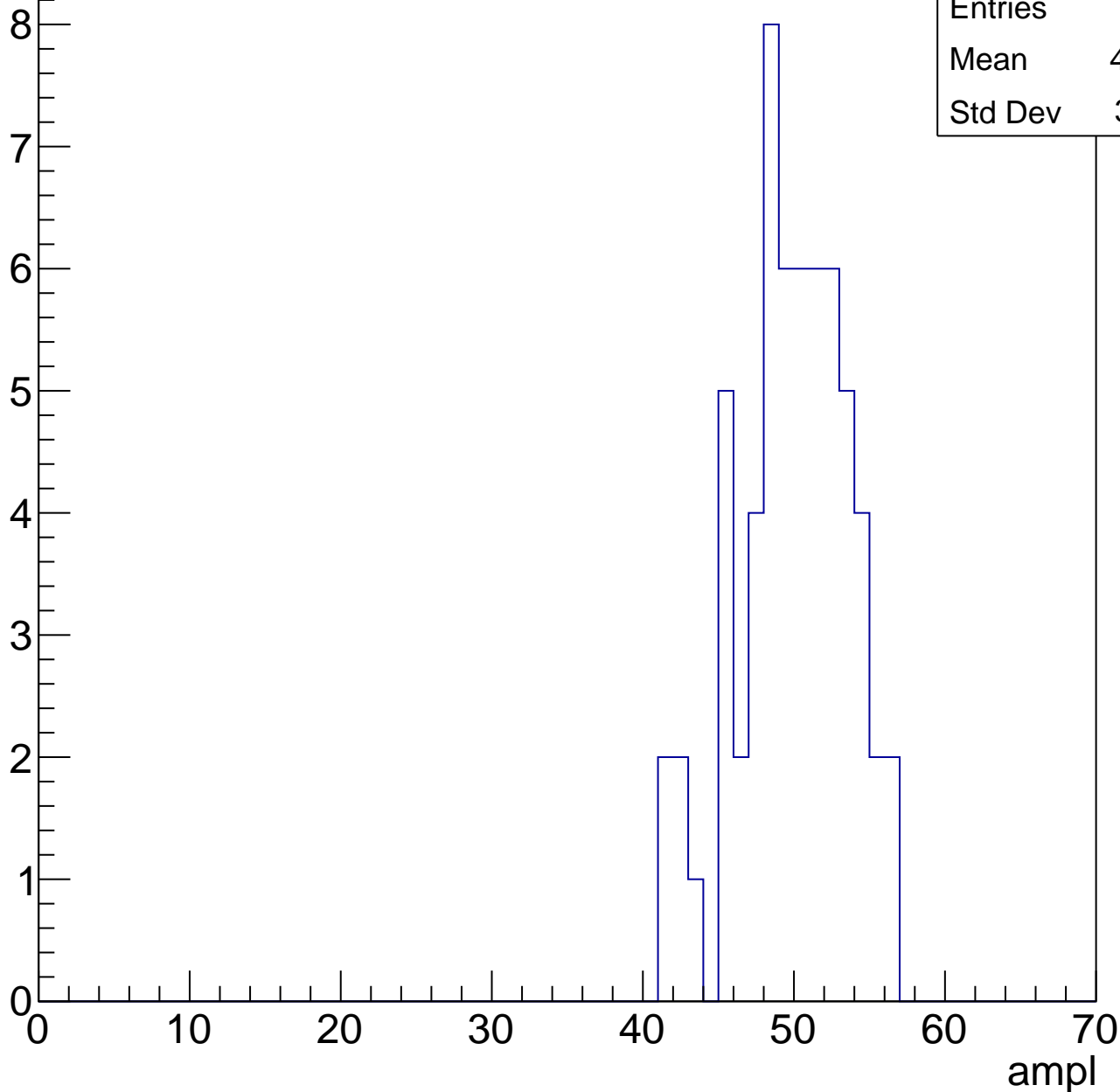


B0L002S, U5-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.39
Std Dev	3.641



B0L002S, U5-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	55.91
Std Dev	3.263

Entry

10

8

6

4

2

0

0

10

20

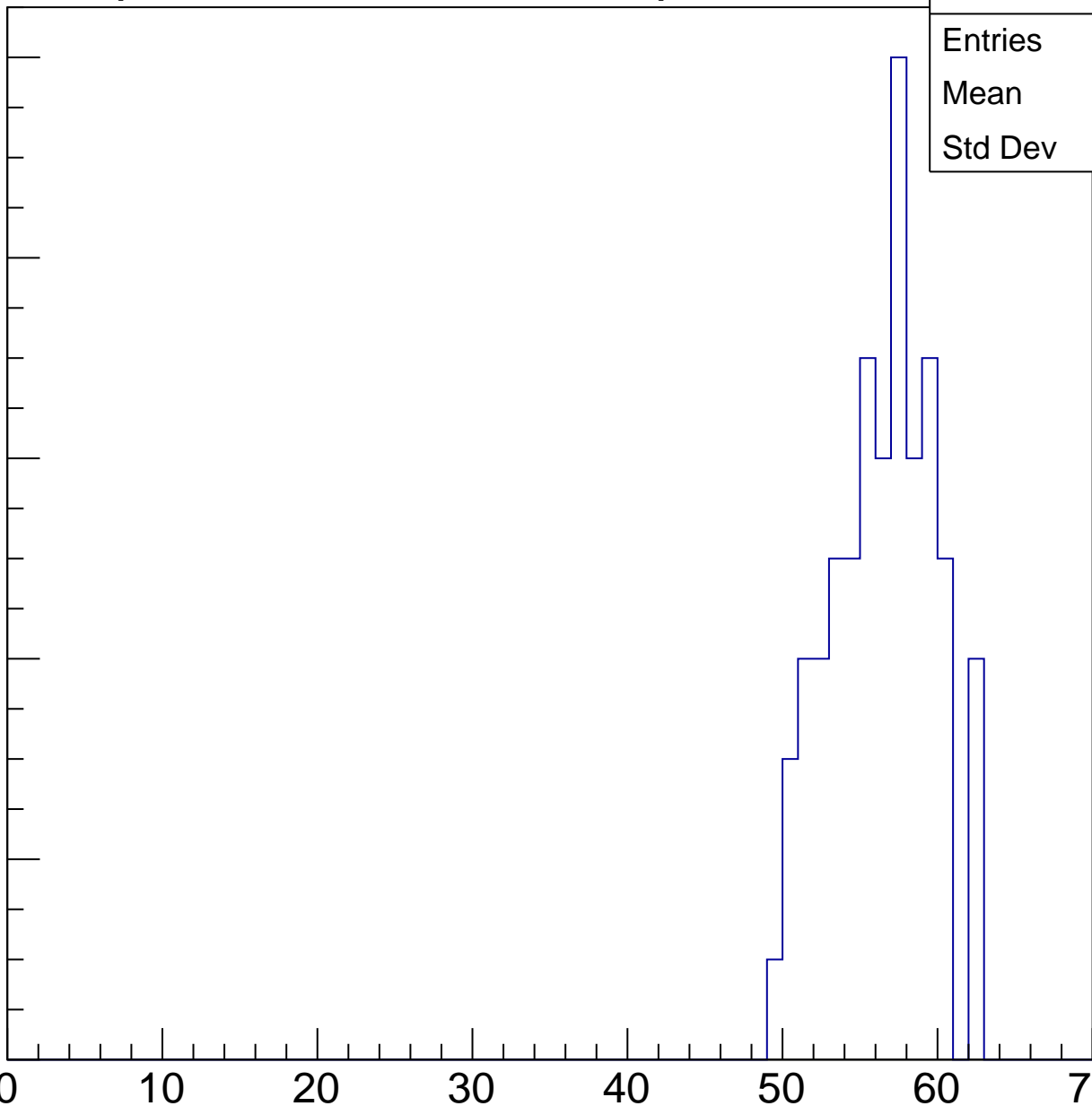
30

40

50

60

ampl

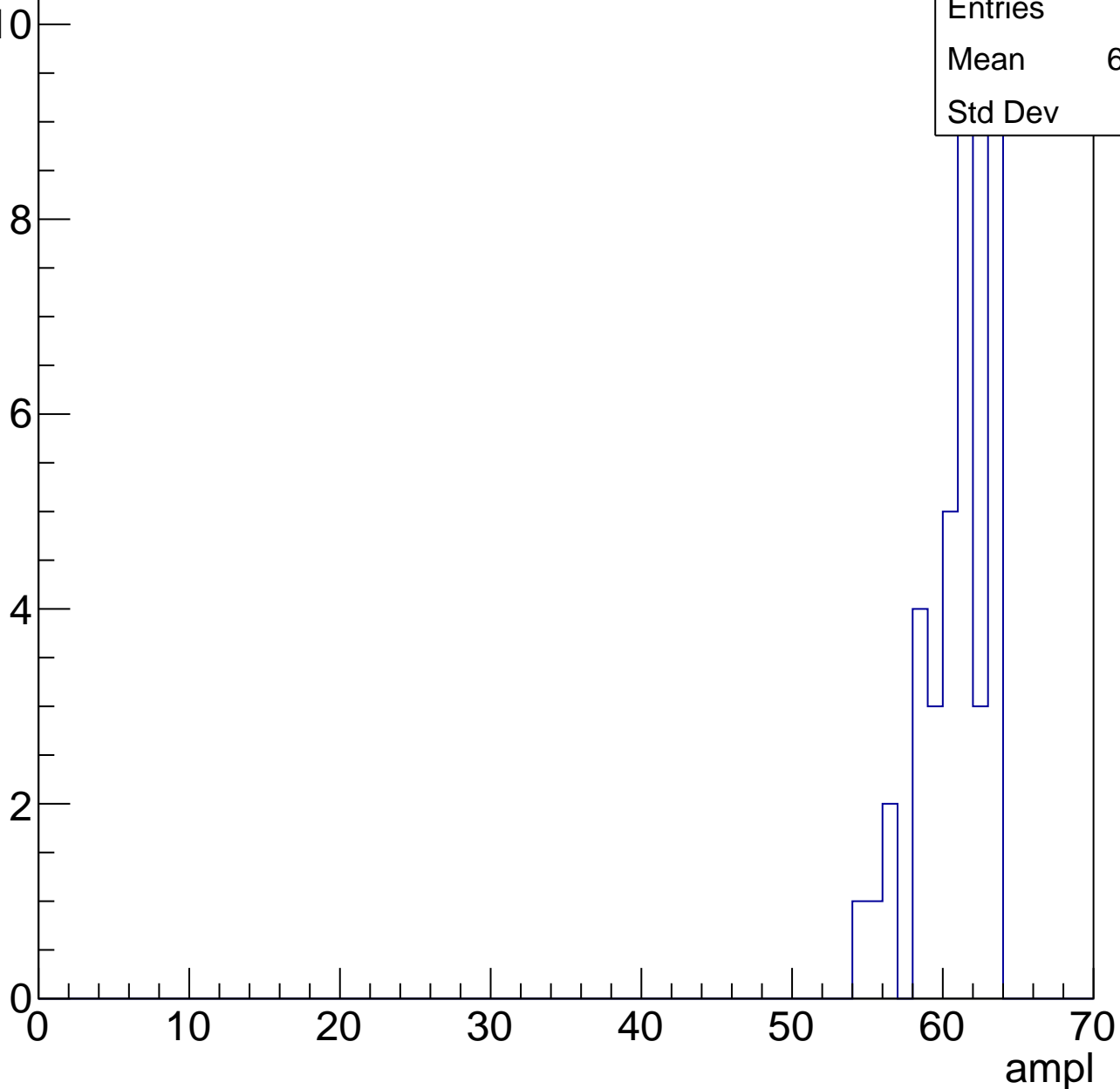


B0L002S, U5-ch62, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

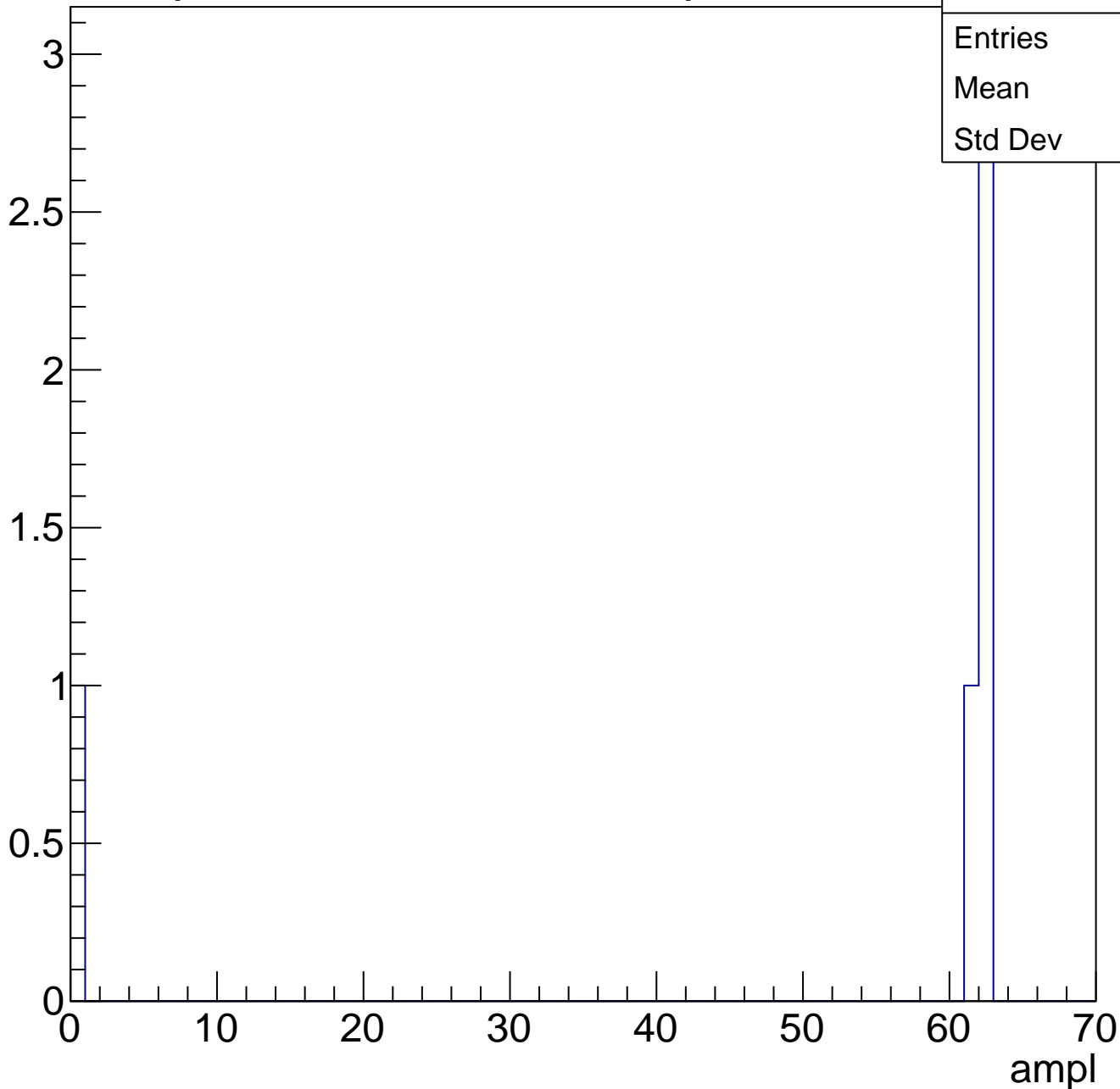
Entries	38
Mean	60.39
Std Dev	2.39



B0L002S, U5-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch62, adc7

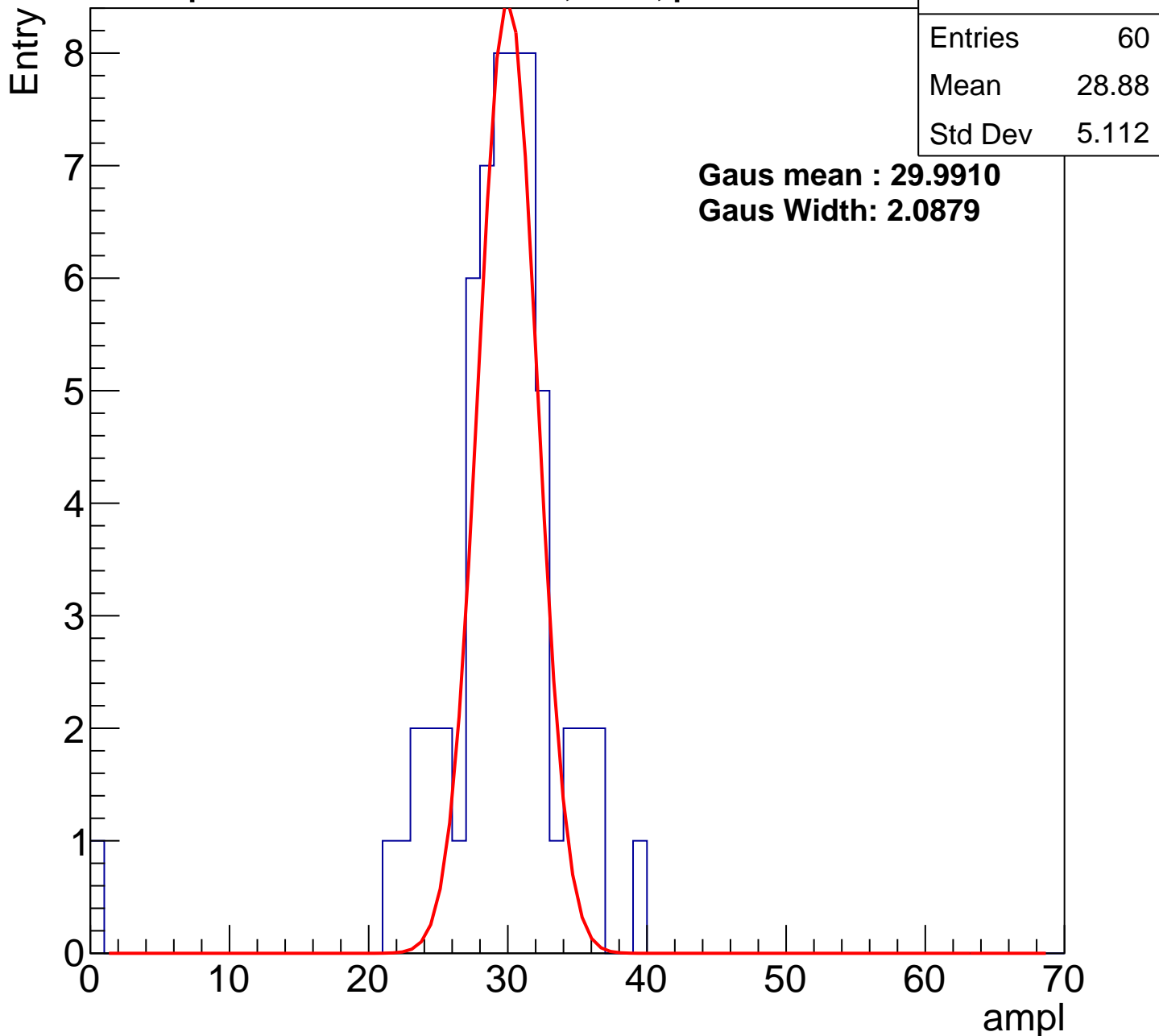
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch63, adc0

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch63, adc1

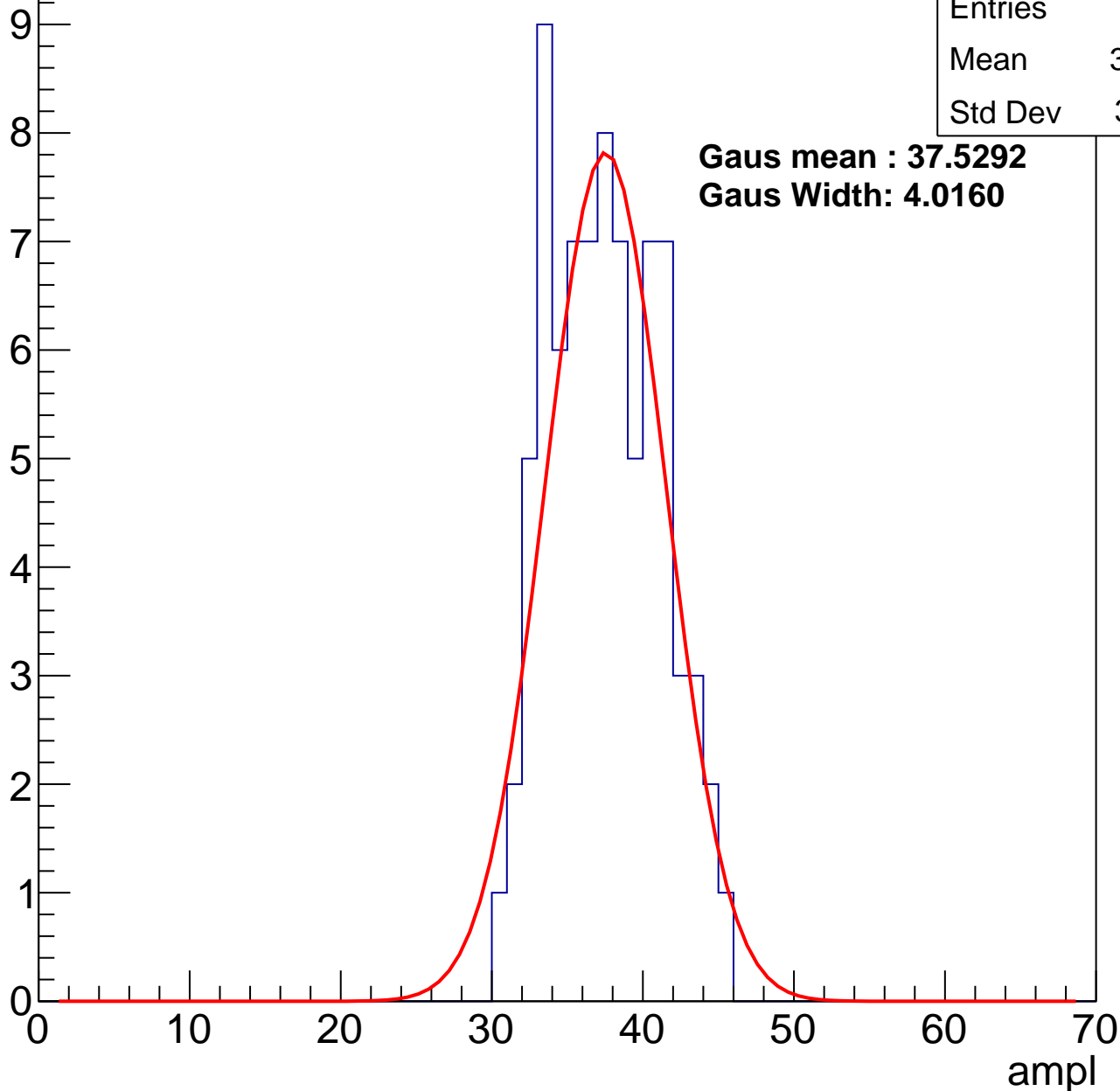
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	37.02
Std Dev	3.581

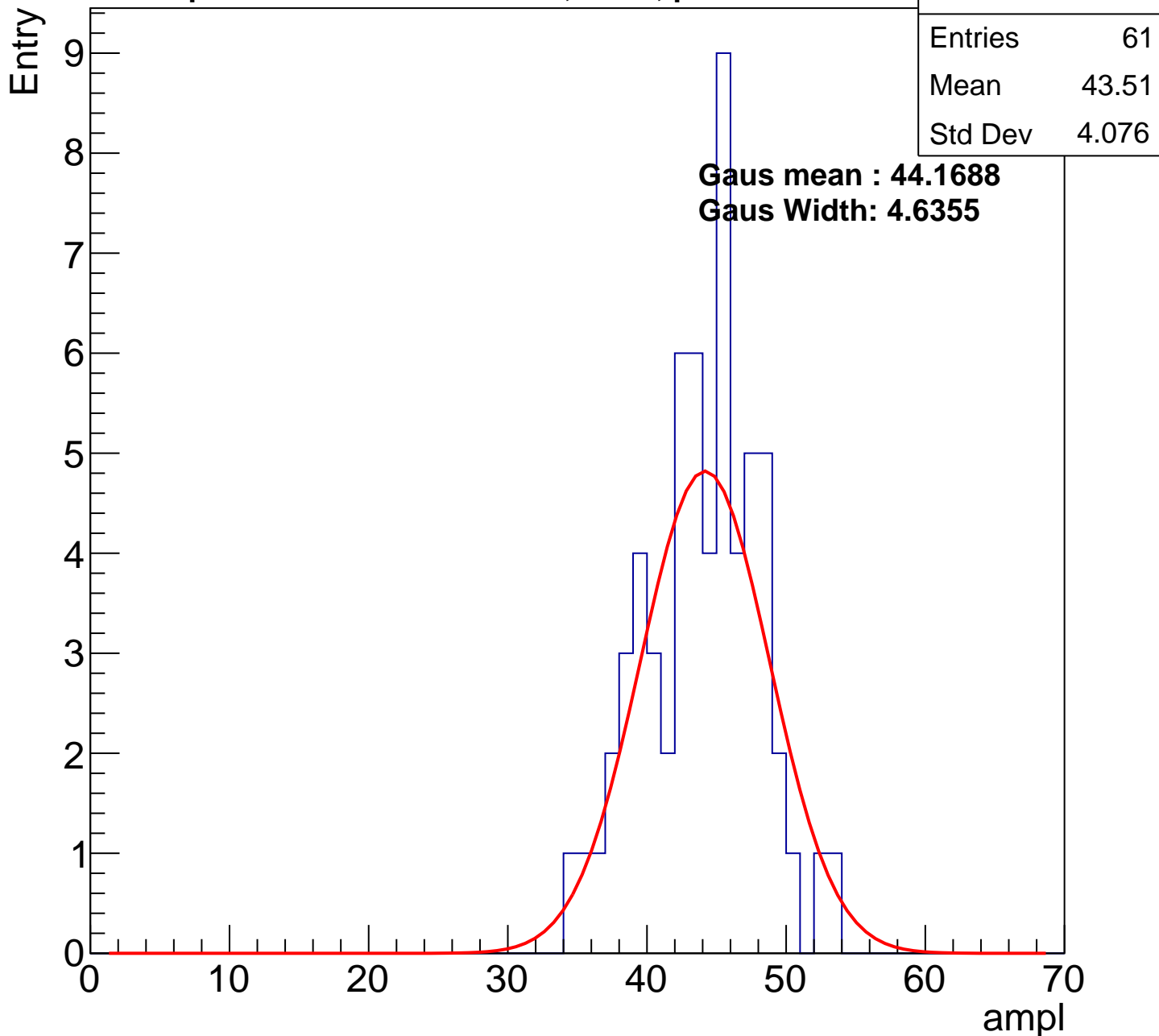
Gaus mean : 37.5292

Gaus Width: 4.0160



B0L002S, U5-ch63, adc2

calib_packv5_042523_0143.root, FC#8, port C1

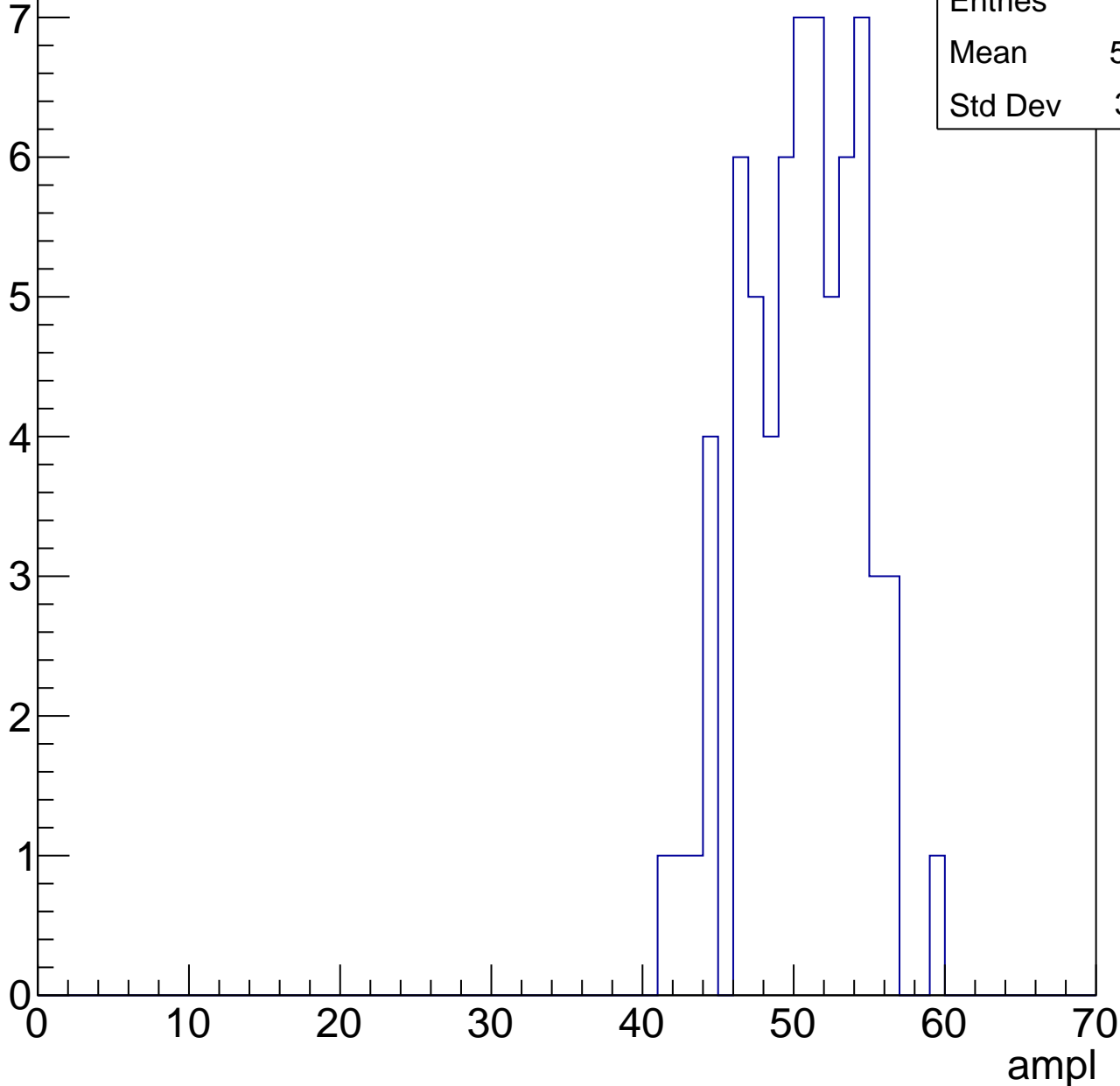


B0L002S, U5-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

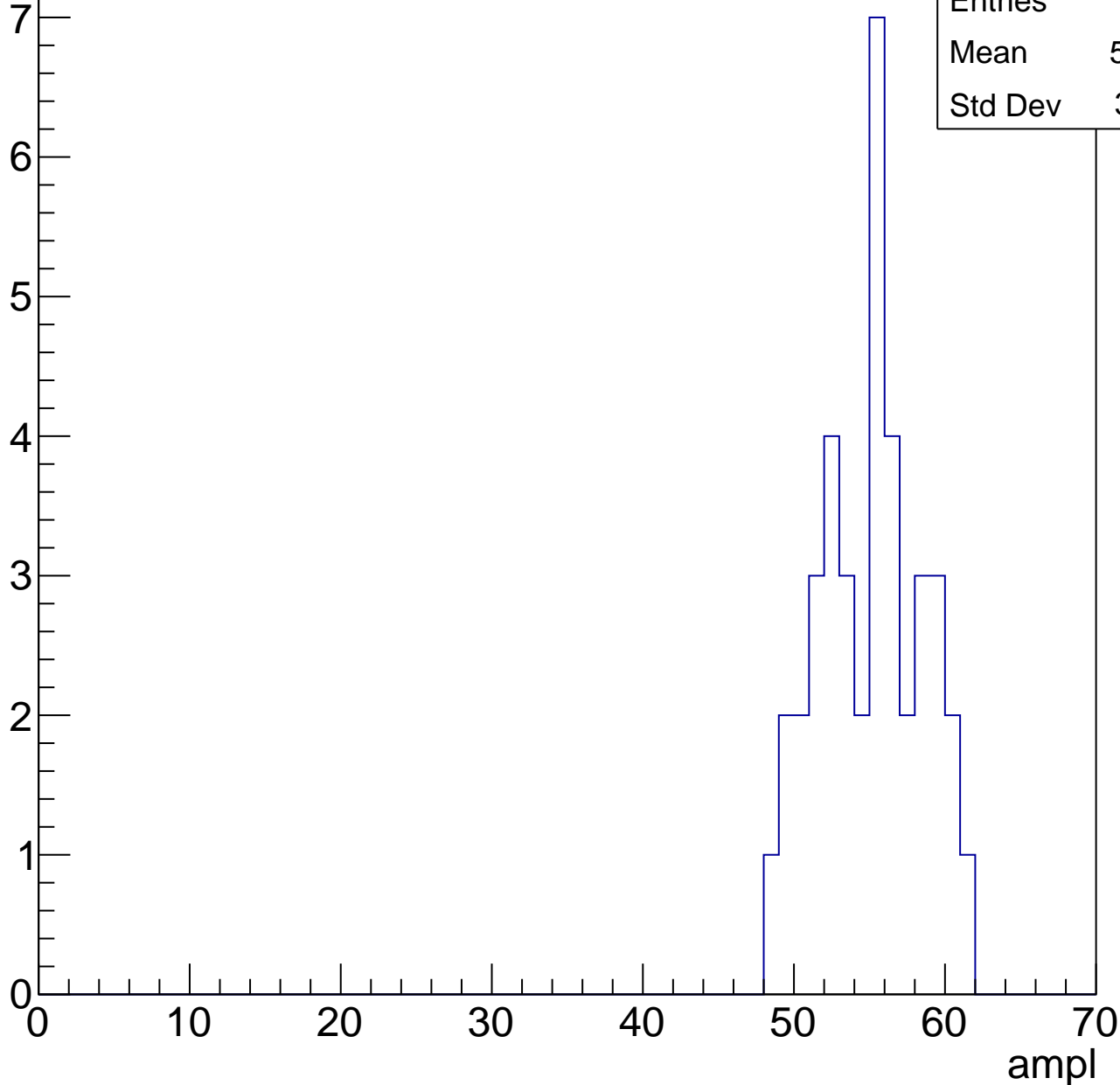
Entries	67
Mean	50.06
Std Dev	3.781



B0L002S, U5-ch63, adc4

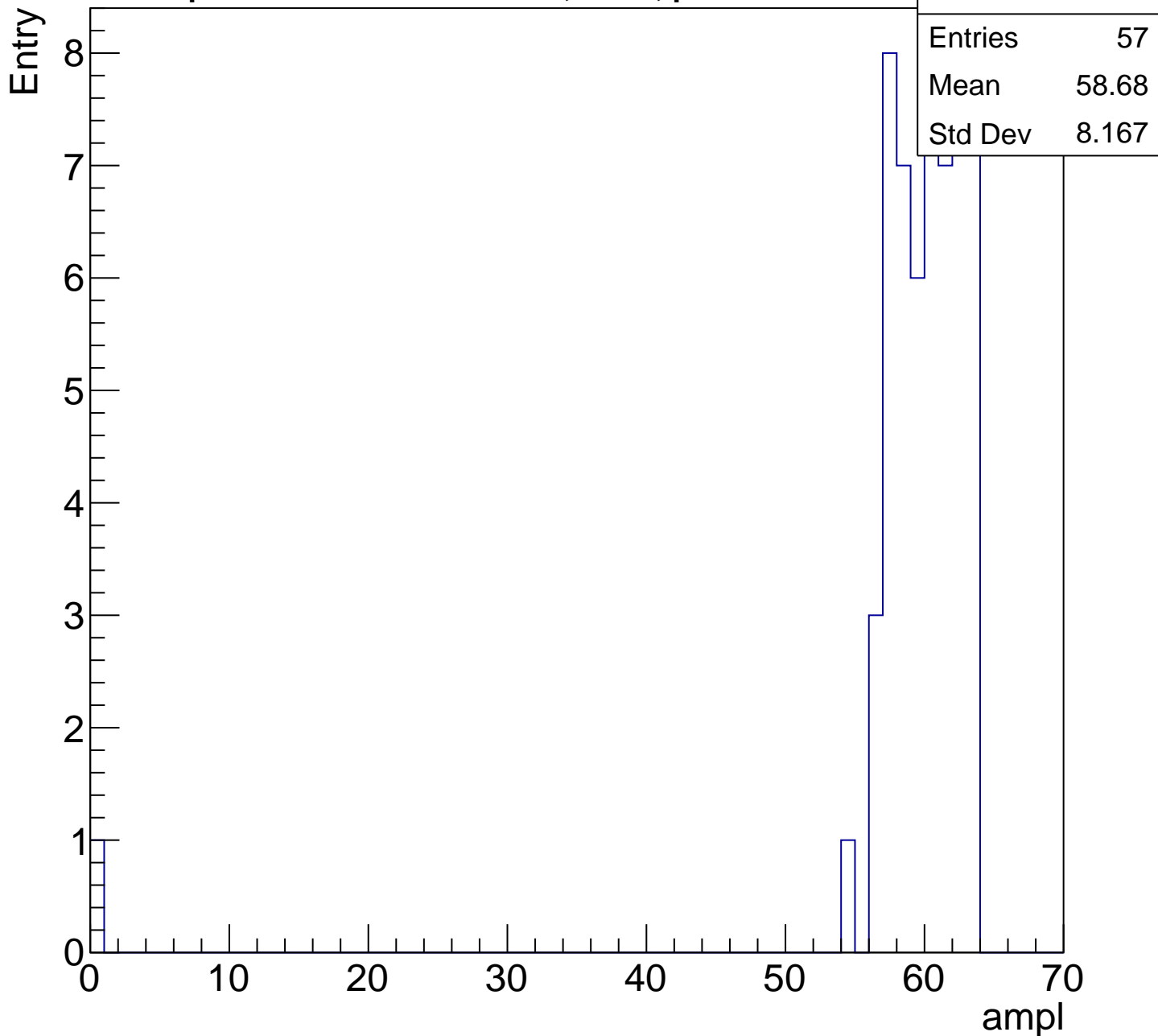
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

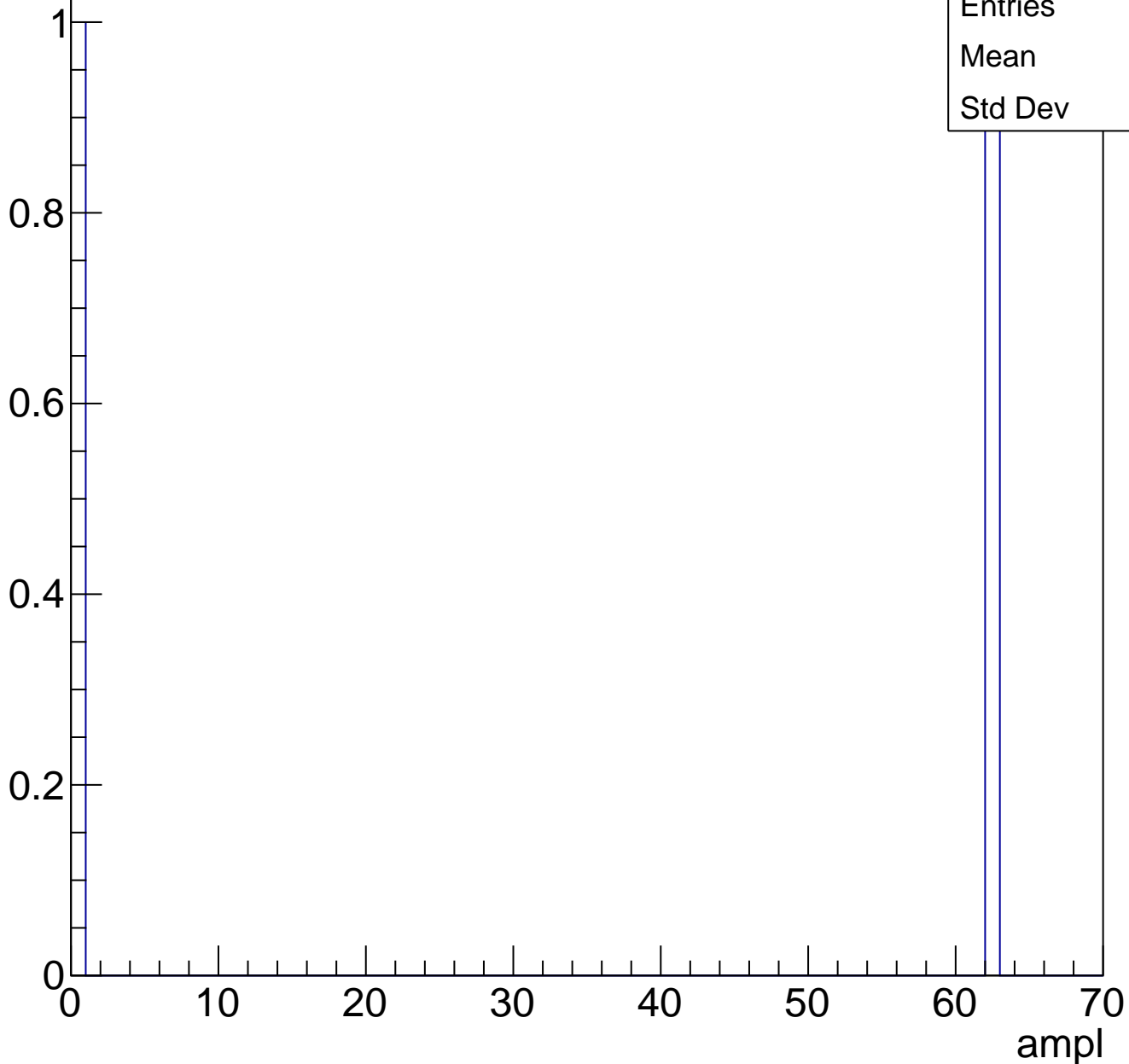
Entries	5
Mean	62
Std Dev	0.8944

ampl

B0L002S, U5-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch64, adc0

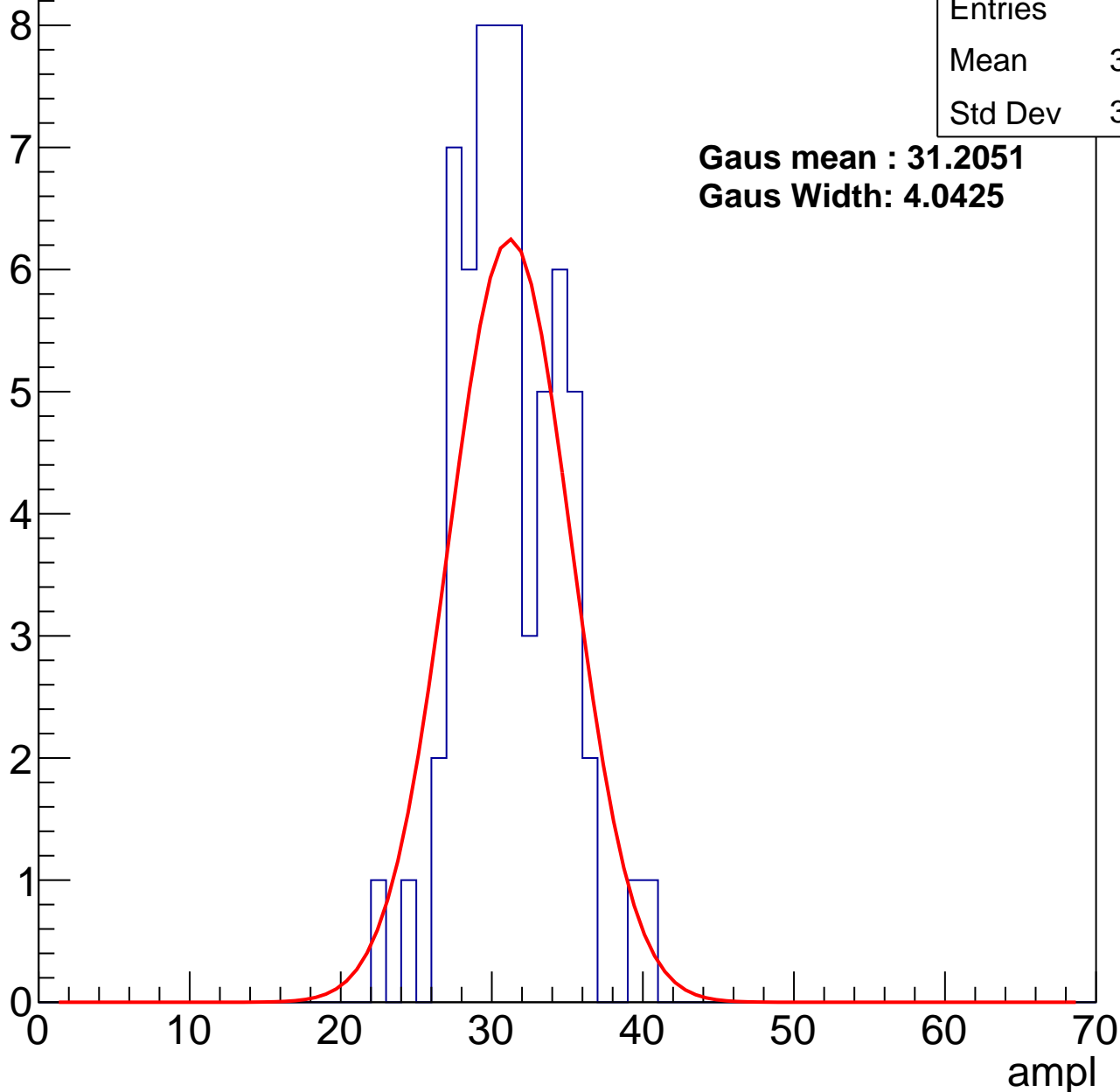
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.72
Std Dev	3.384

Gaus mean : 31.2051

Gaus Width: 4.0425



B0L002S, U5-ch64, adc1

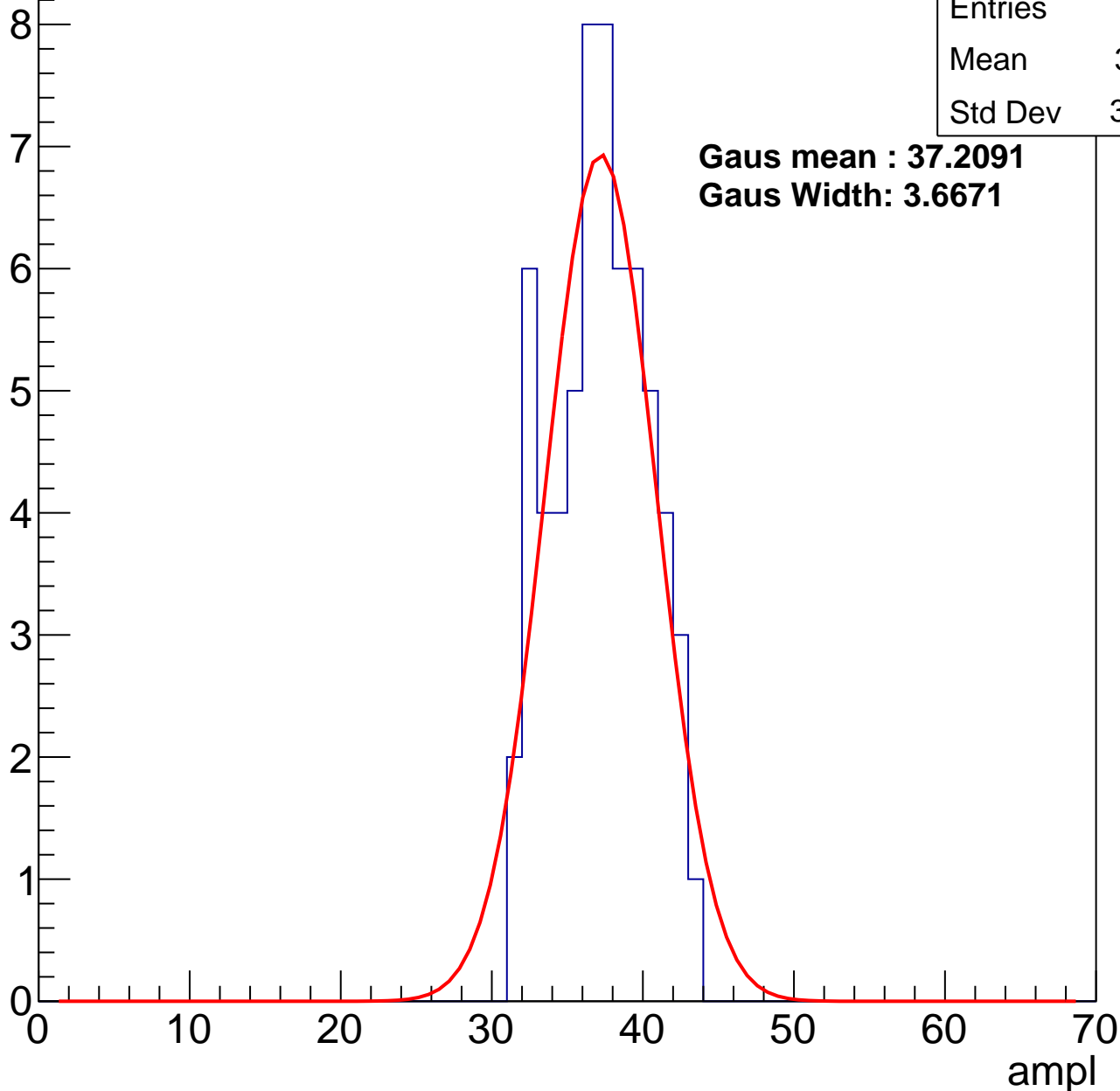
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.71
Std Dev	3.097

Gaus mean : 37.2091

Gaus Width: 3.6671



B0L002S, U5-ch64, adc2

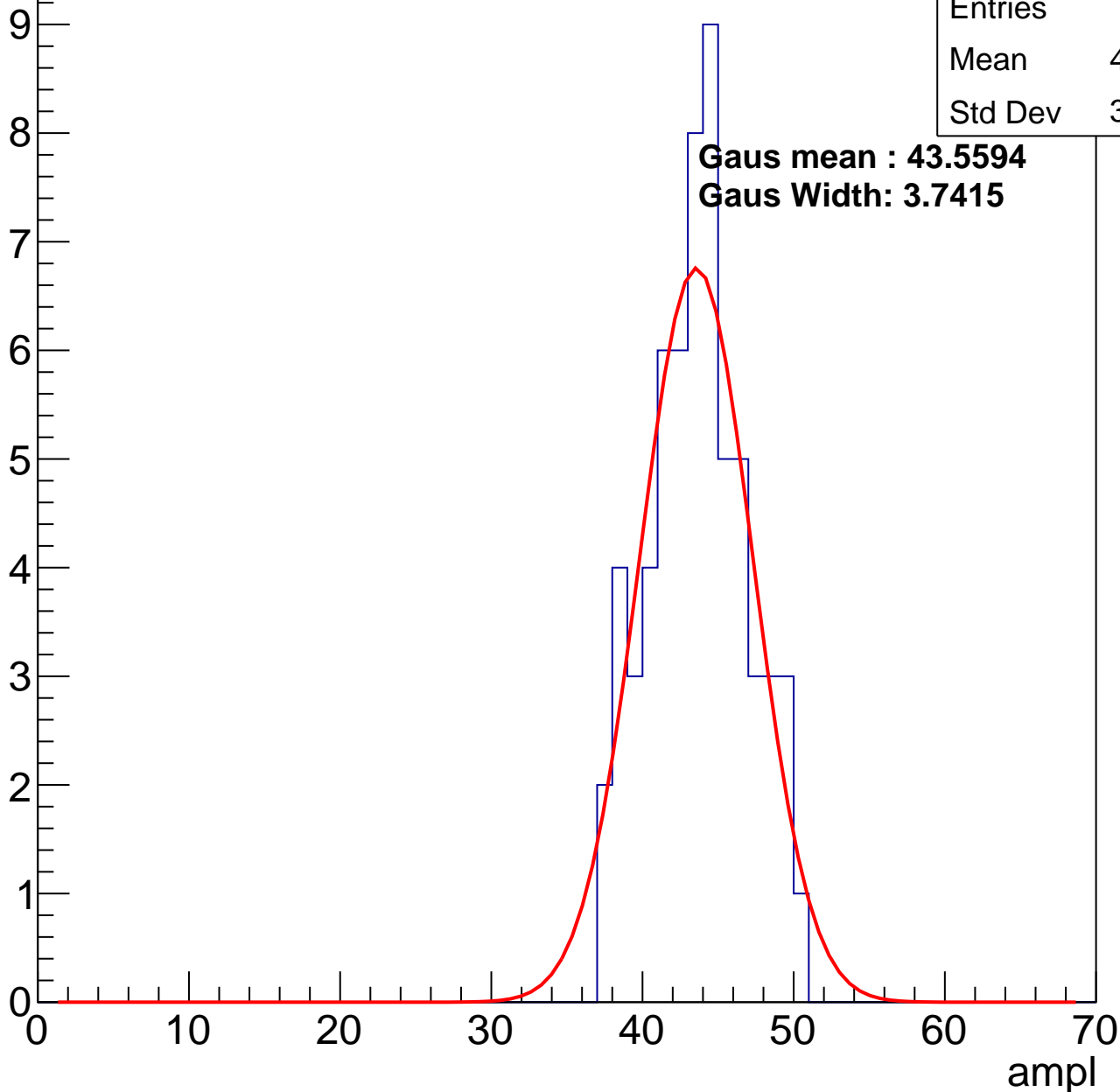
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.19
Std Dev	3.207

Gaus mean : 43.5594

Gaus Width: 3.7415

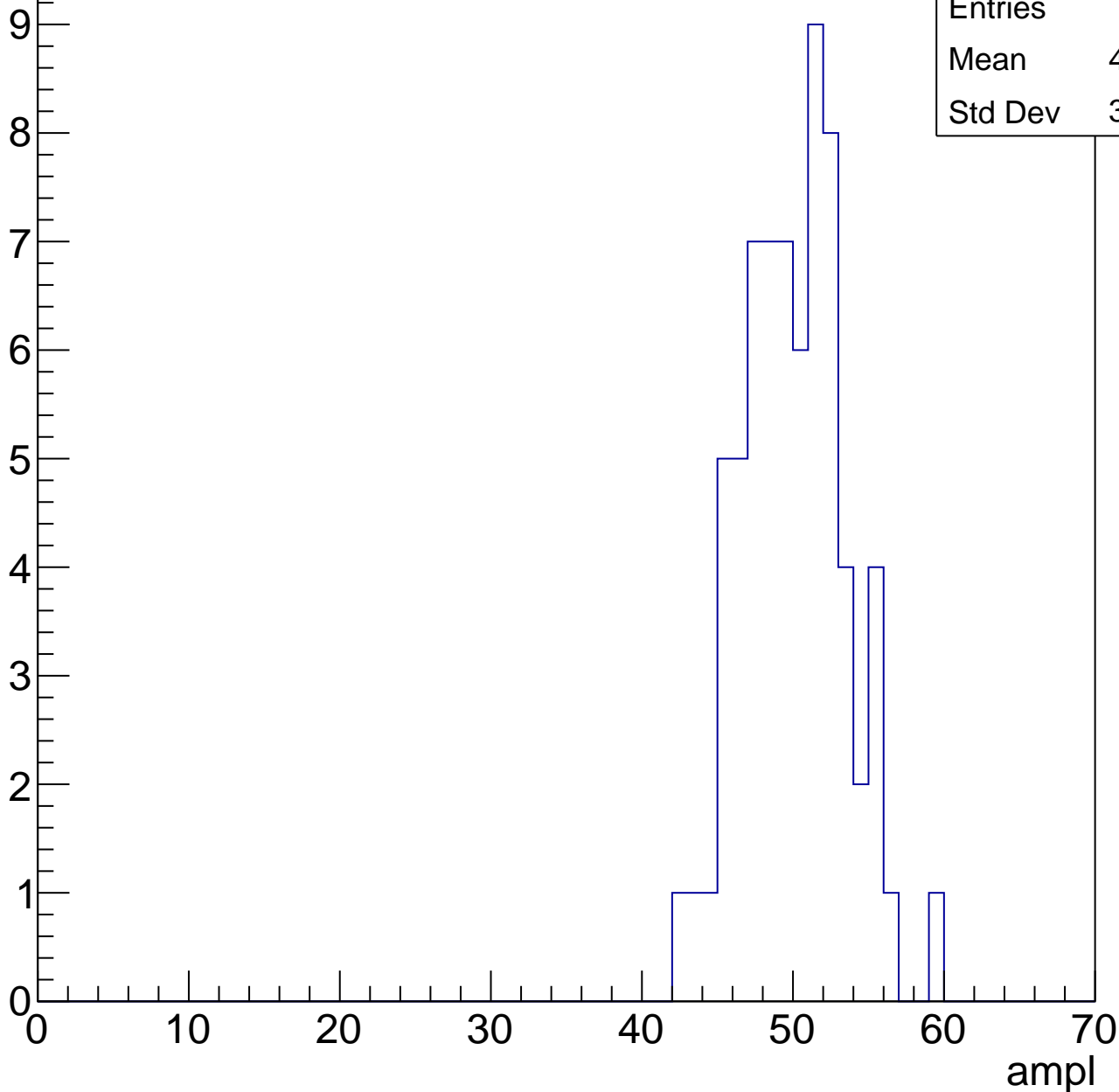


B0L002S, U5-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.59
Std Dev	3.333

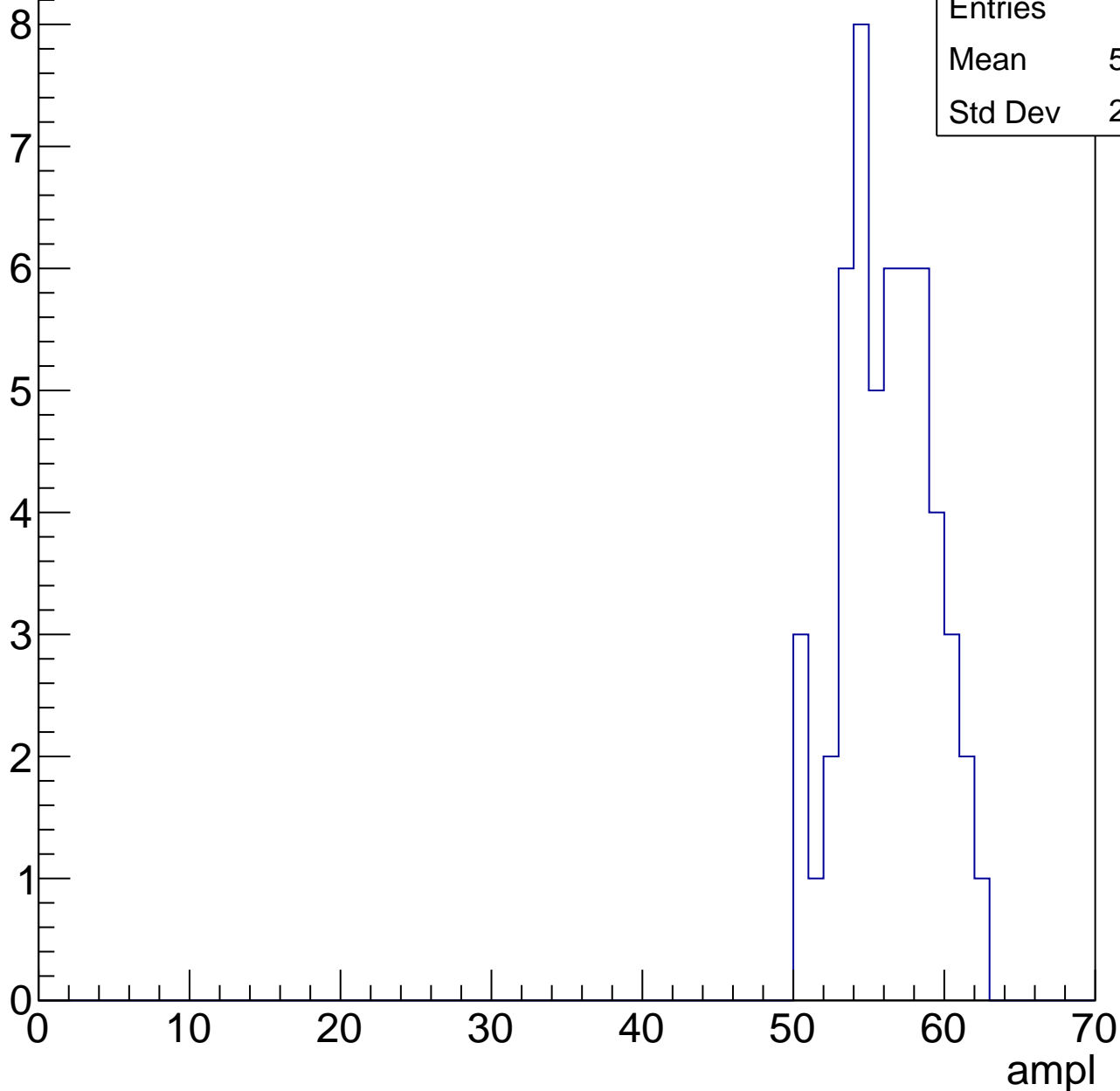


B0L002S, U5-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

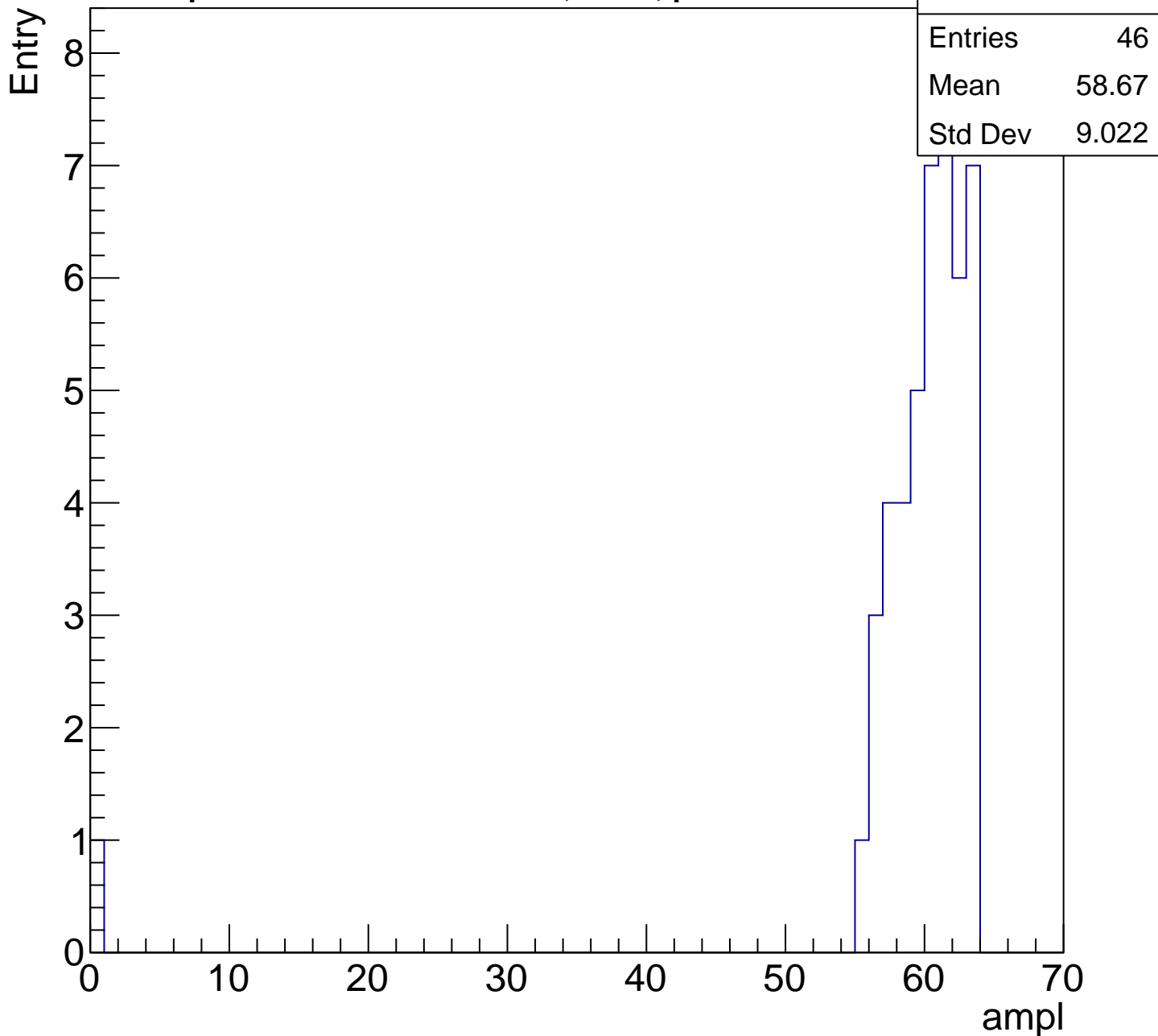
Entry

Entries	53
Mean	55.77
Std Dev	2.924



B0L002S, U5-ch64, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch64, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch65, adc0

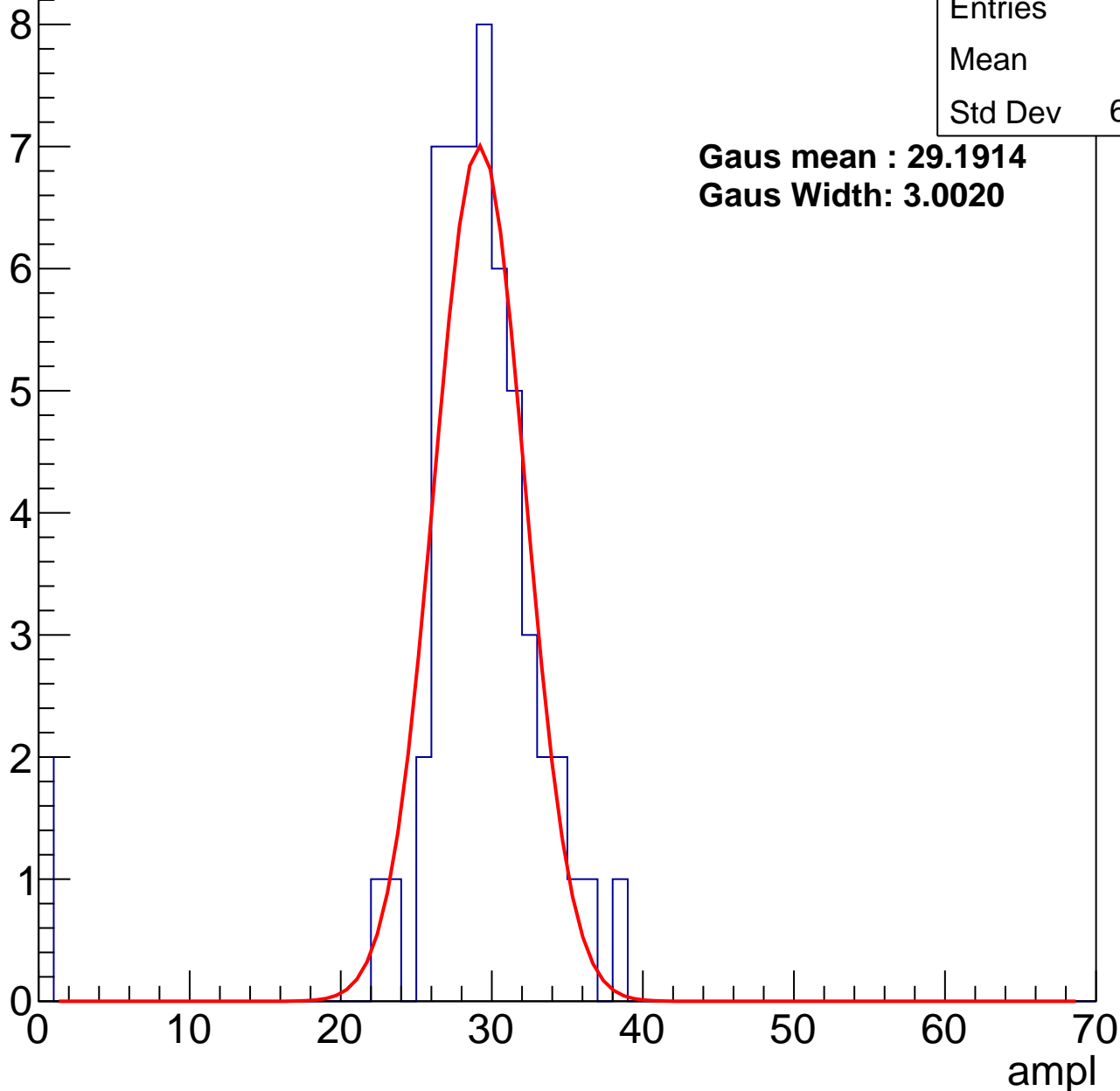
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	28
Std Dev	6.185

Gaus mean : 29.1914

Gaus Width: 3.0020



B0L002S, U5-ch65, adc1

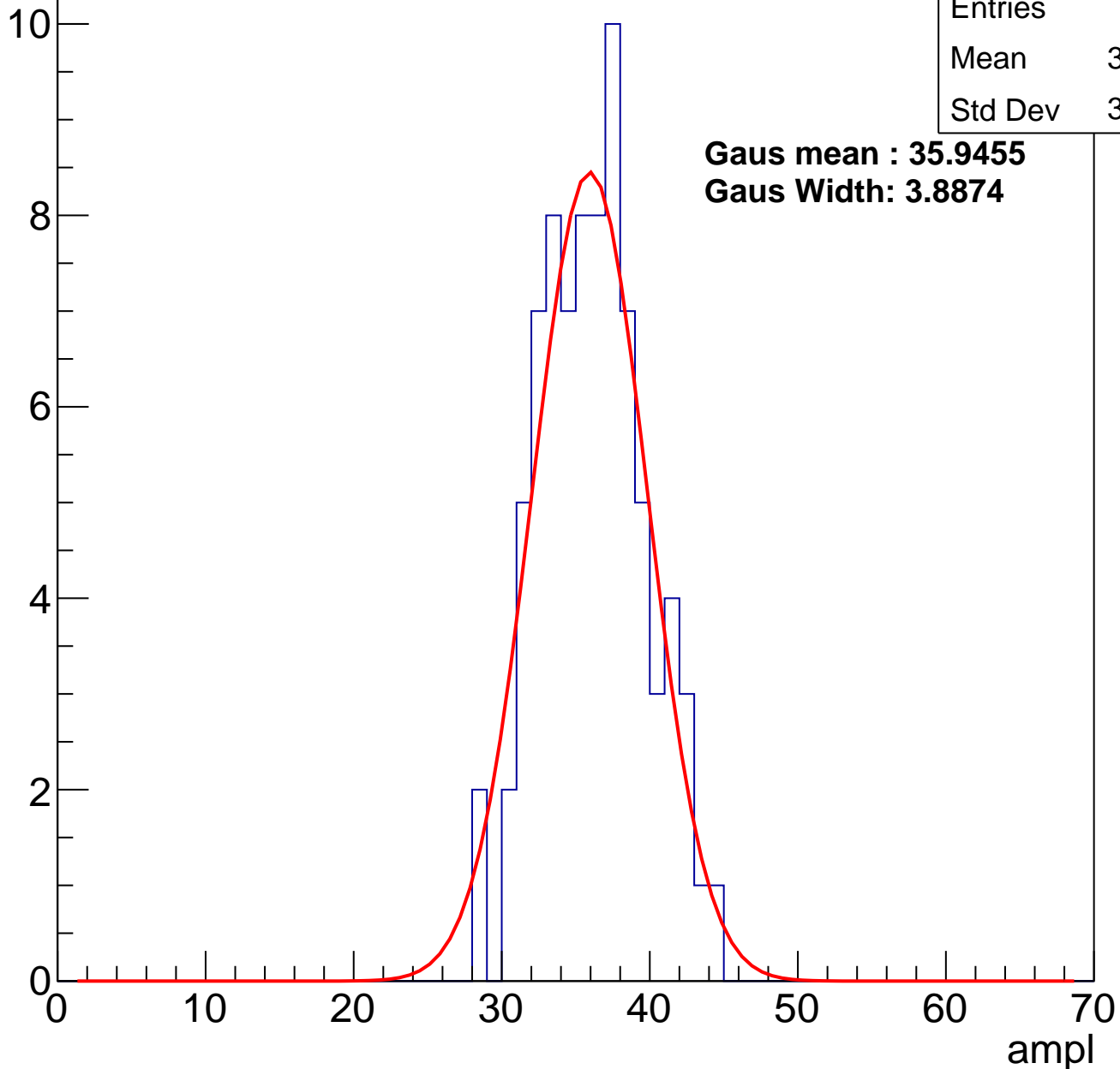
calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	35.72
Std Dev	3.497

Gaus mean : 35.9455

Gaus Width: 3.8874

Entry

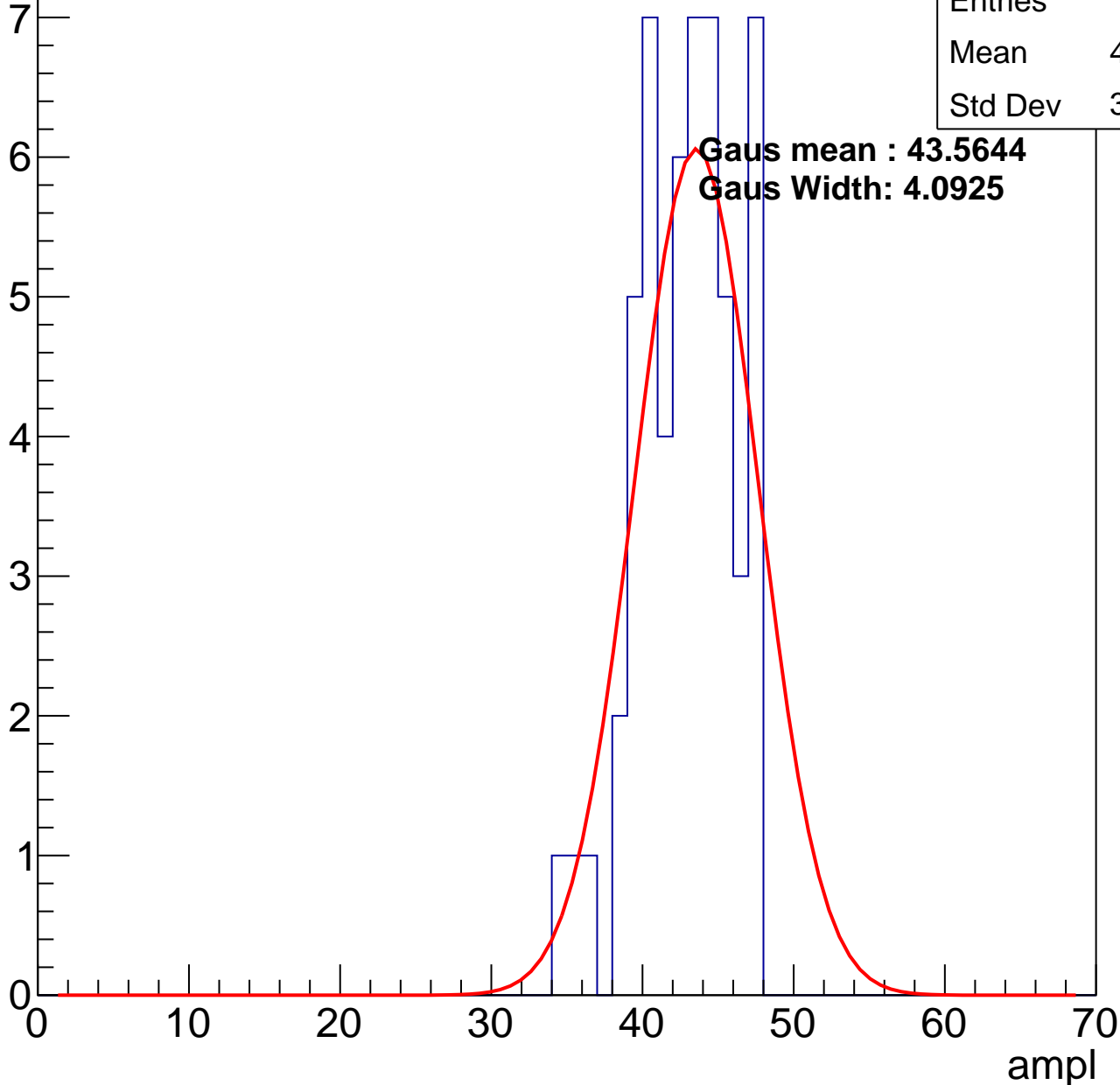


B0L002S, U5-ch65, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	42.38
Std Dev	3.143



B0L002S, U5-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

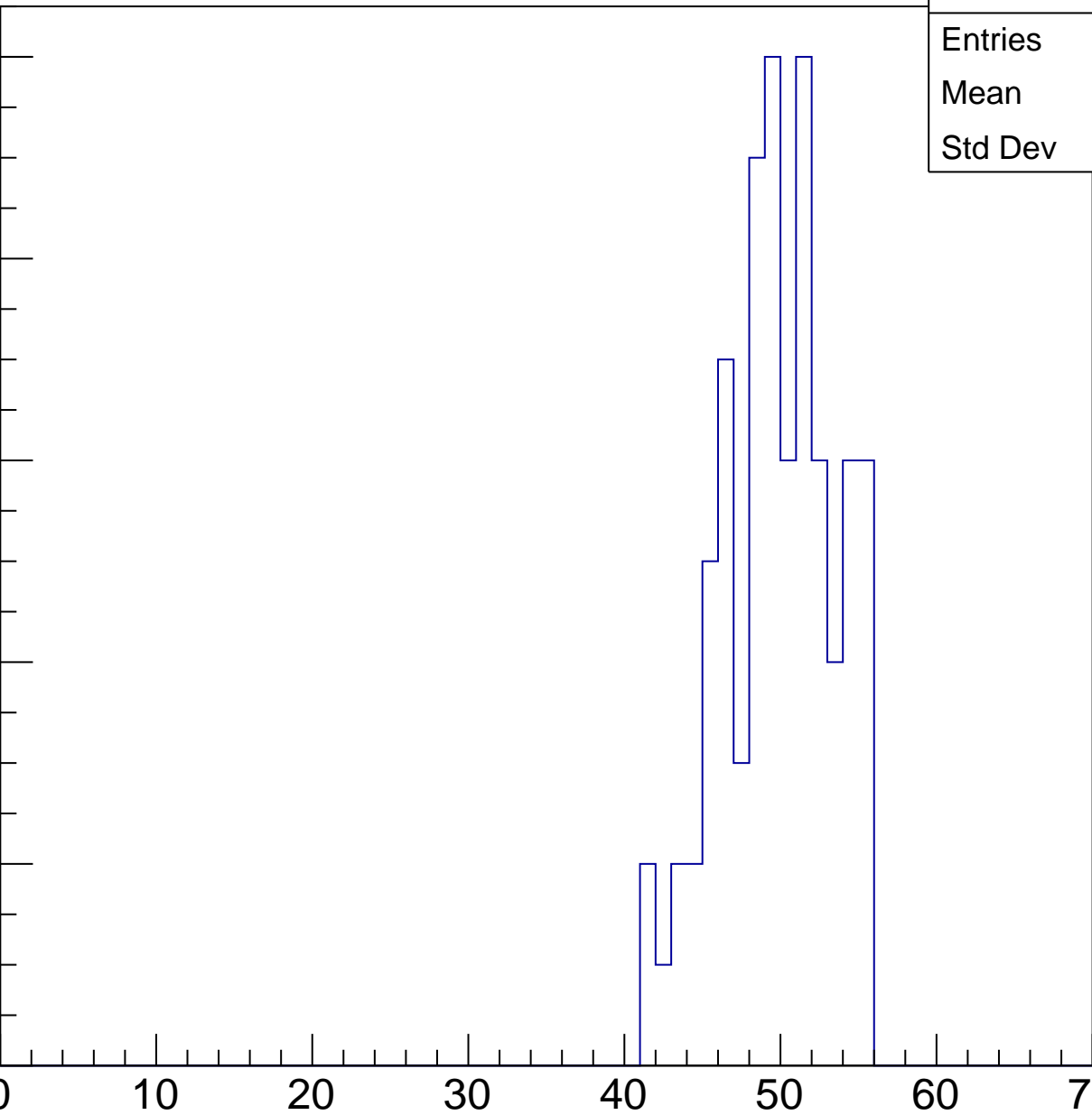
Entries	79
Mean	49.32
Std Dev	3.531

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

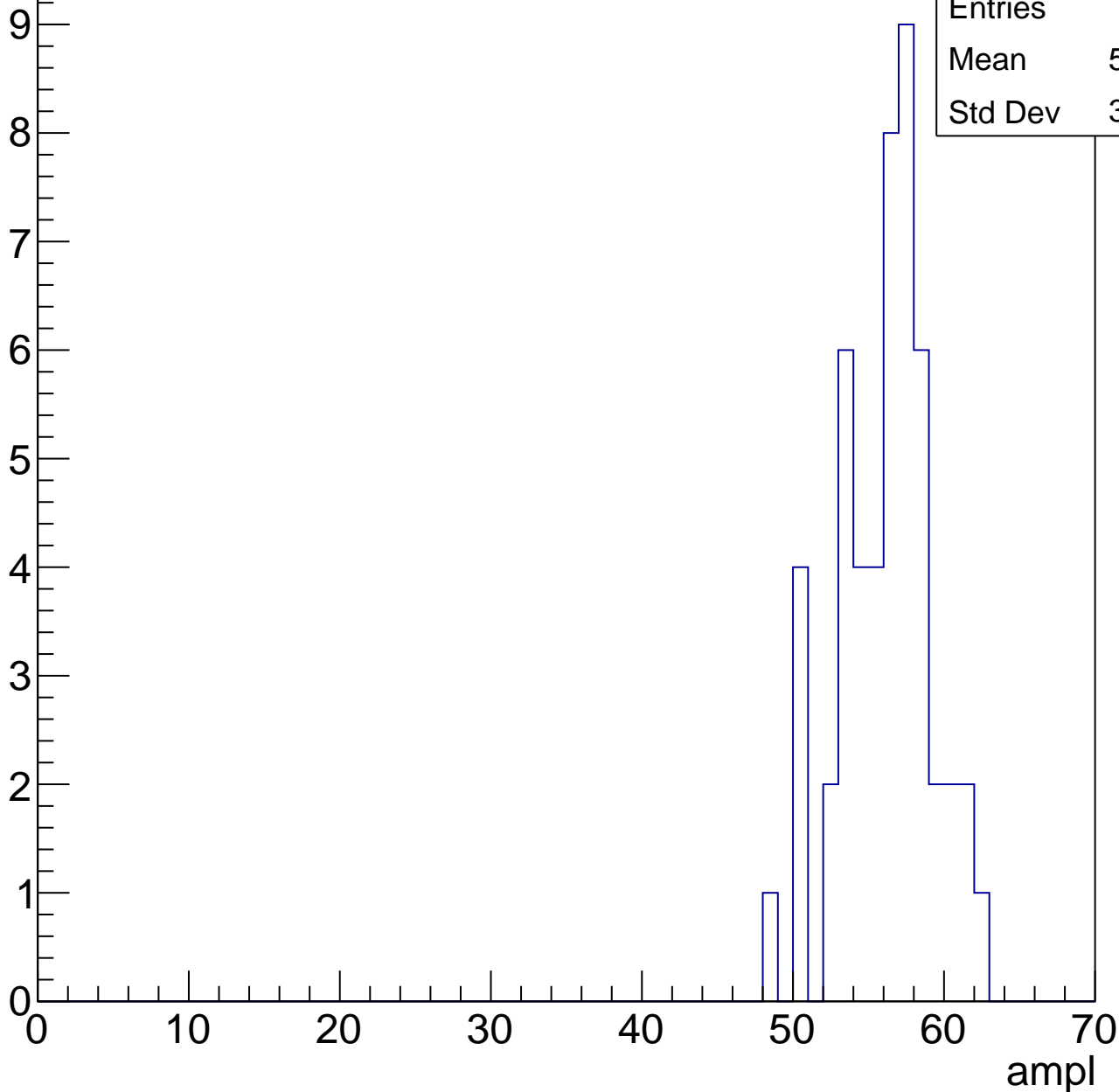


B0L002S, U5-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

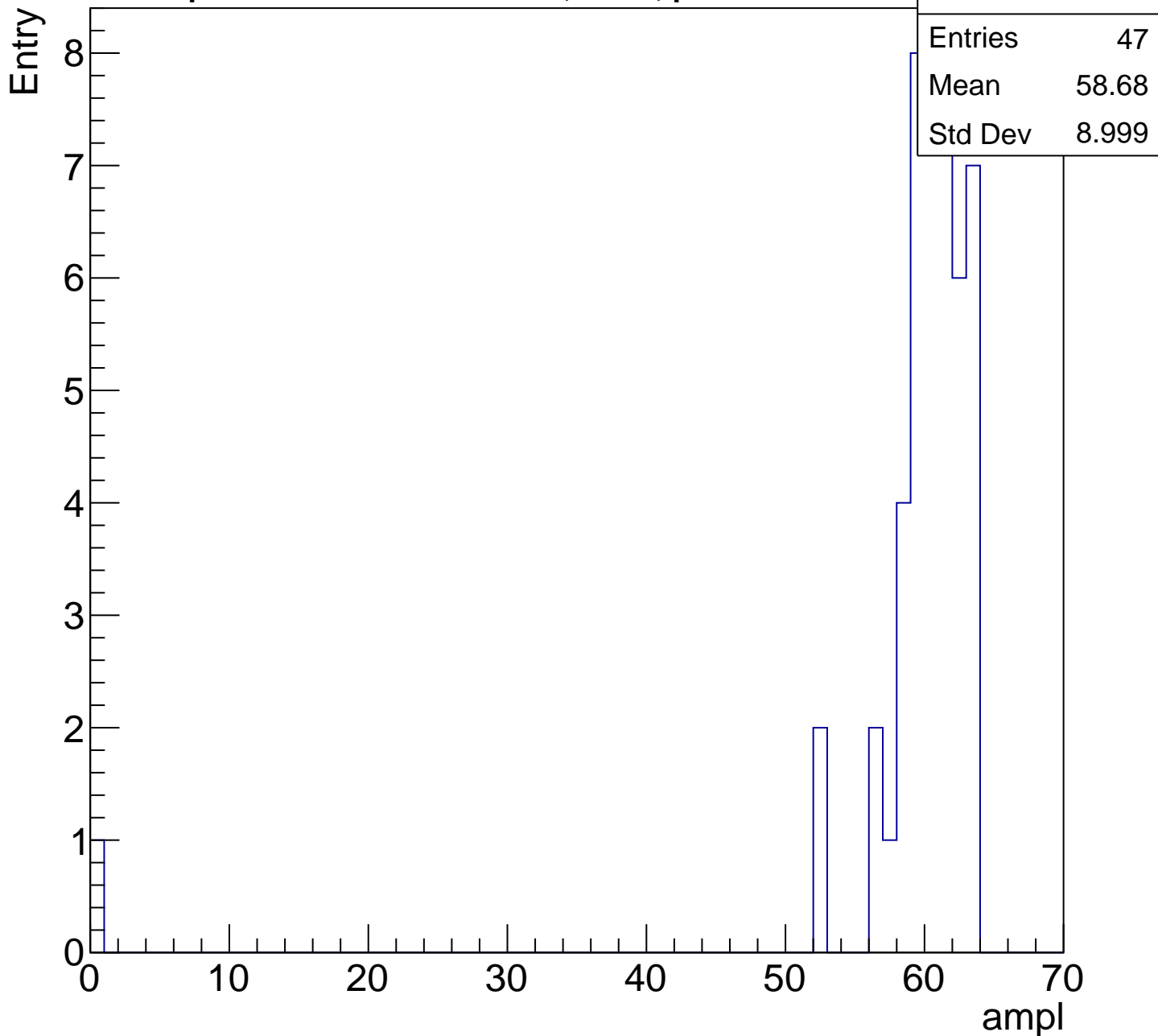
Entry

Entries	51
Mean	55.63
Std Dev	3.055



B0L002S, U5-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch66, adc0

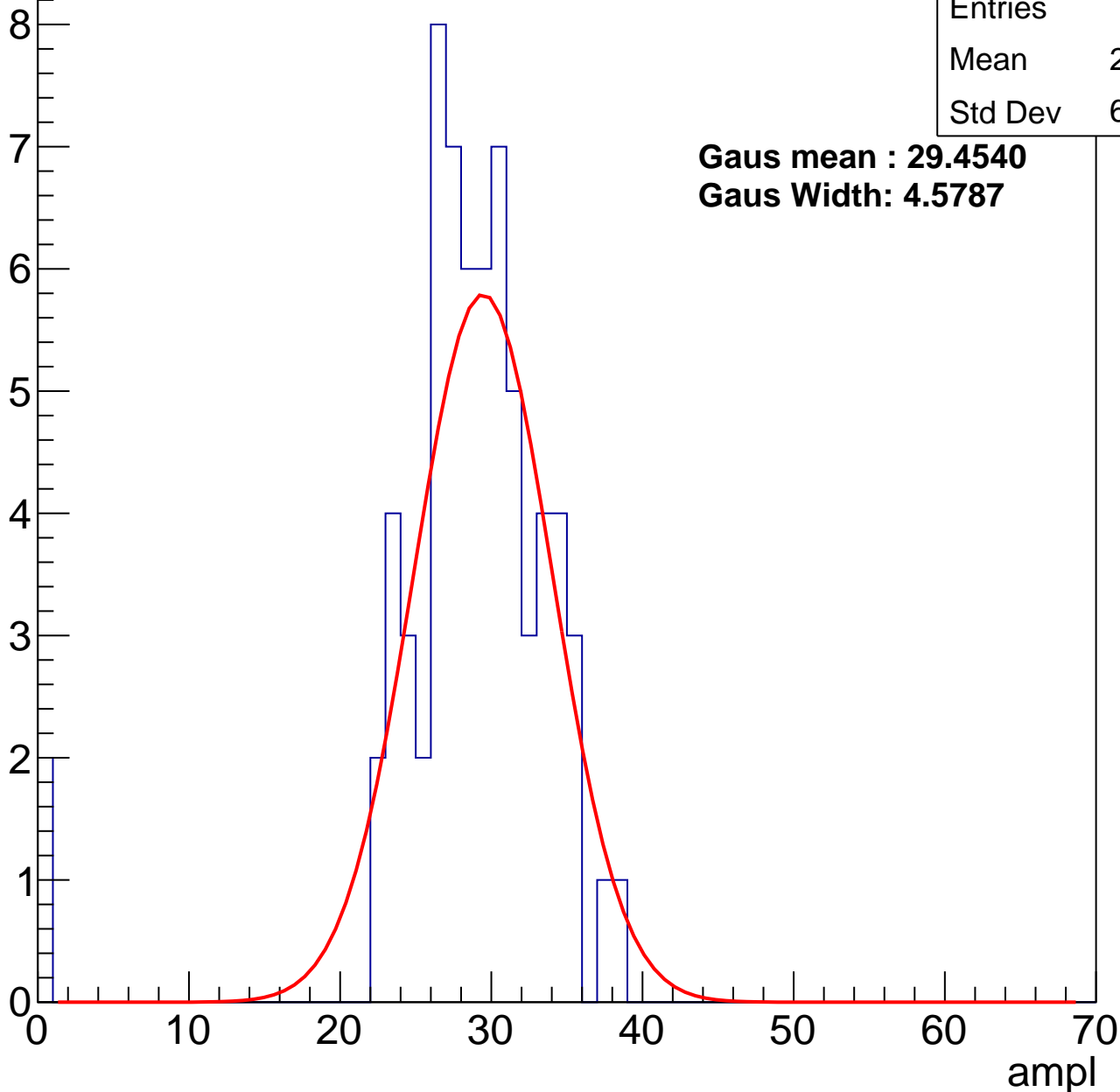
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.03
Std Dev	6.116

Gaus mean : 29.4540

Gaus Width: 4.5787



B0L002S, U5-ch66, adc1

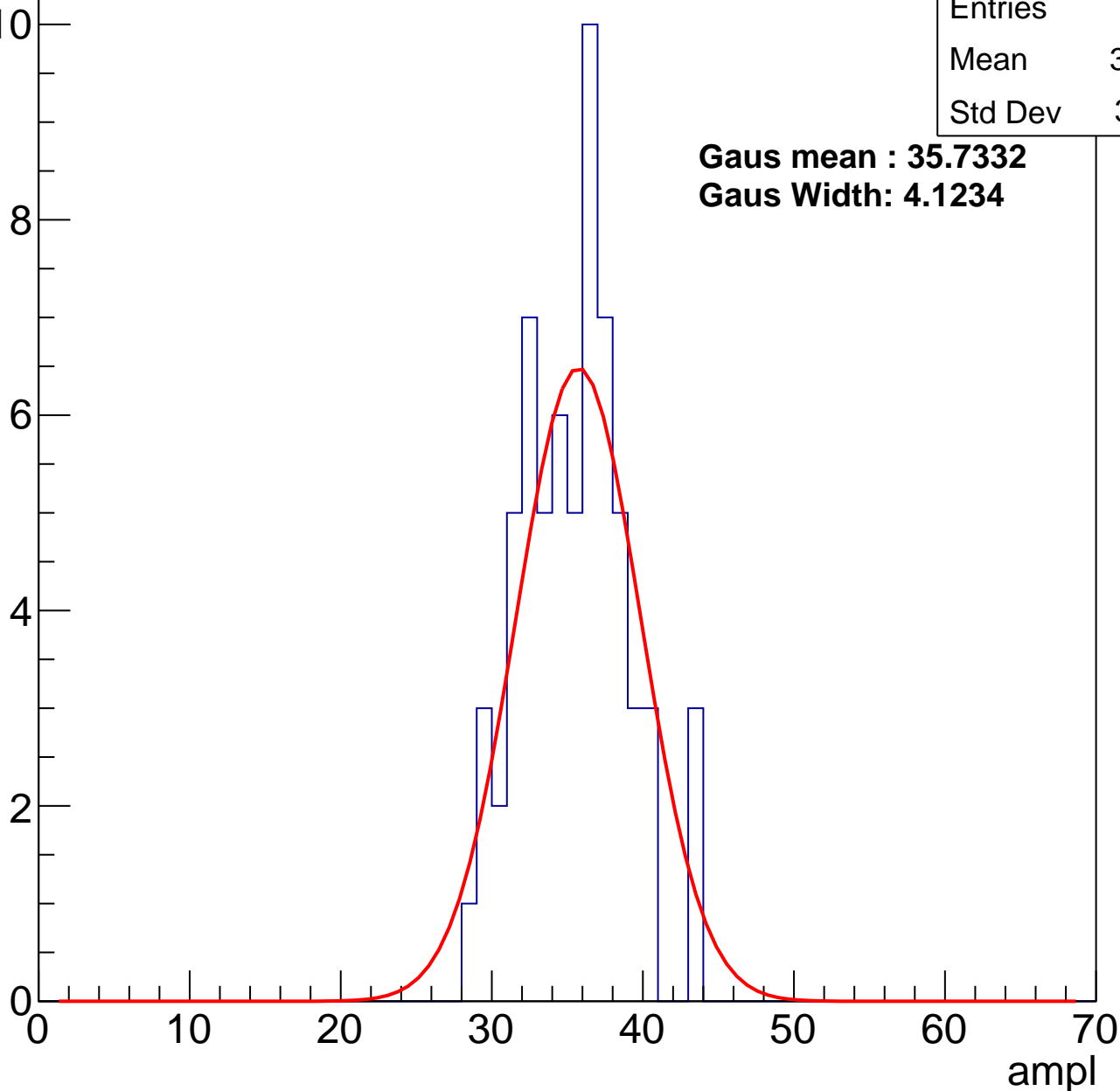
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	34.97
Std Dev	3.451

Gaus mean : 35.7332

Gaus Width: 4.1234



B0L002S, U5-ch66, adc2

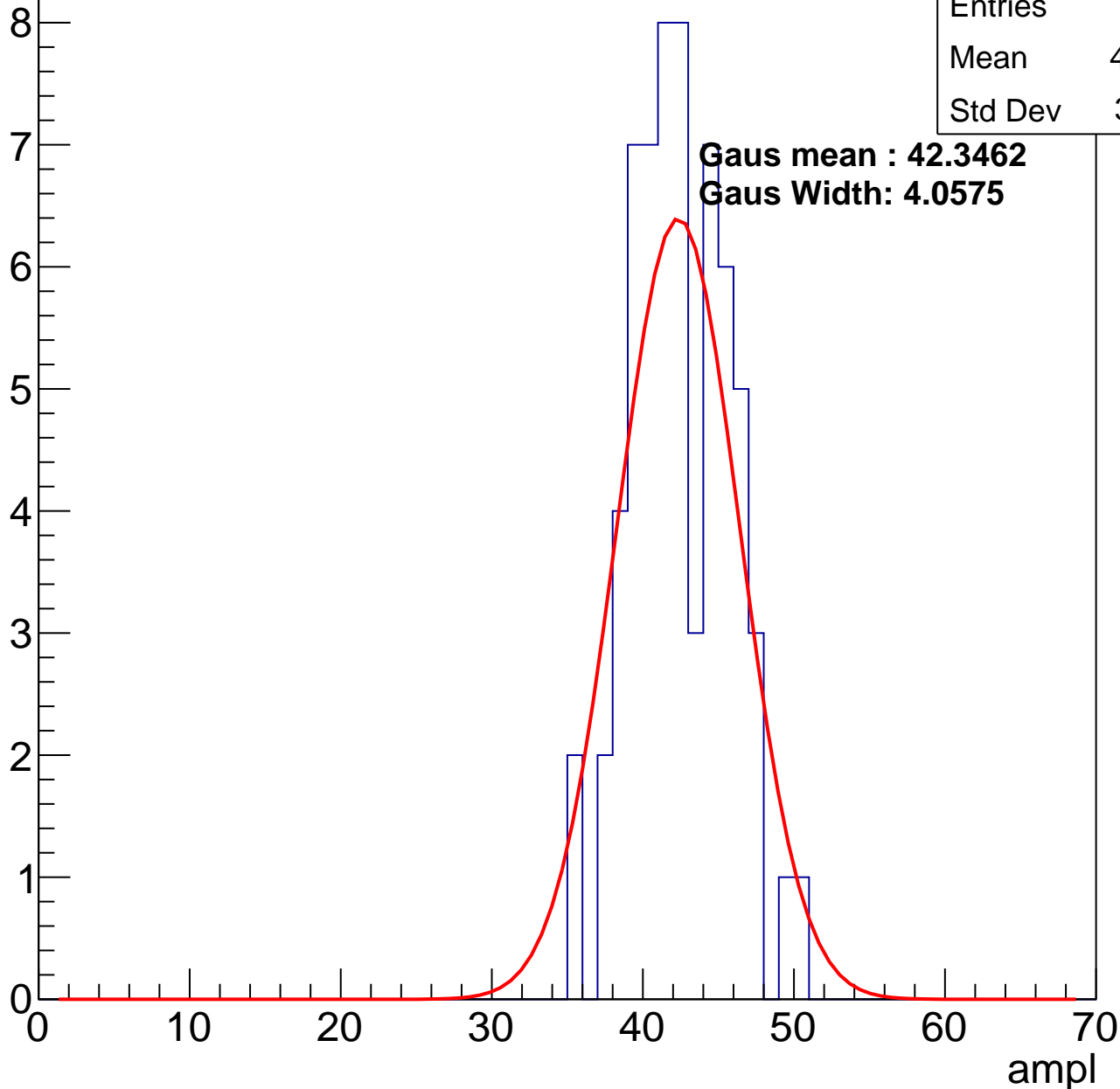
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.03
Std Dev	3.221

Gaus mean : 42.3462

Gaus Width: 4.0575

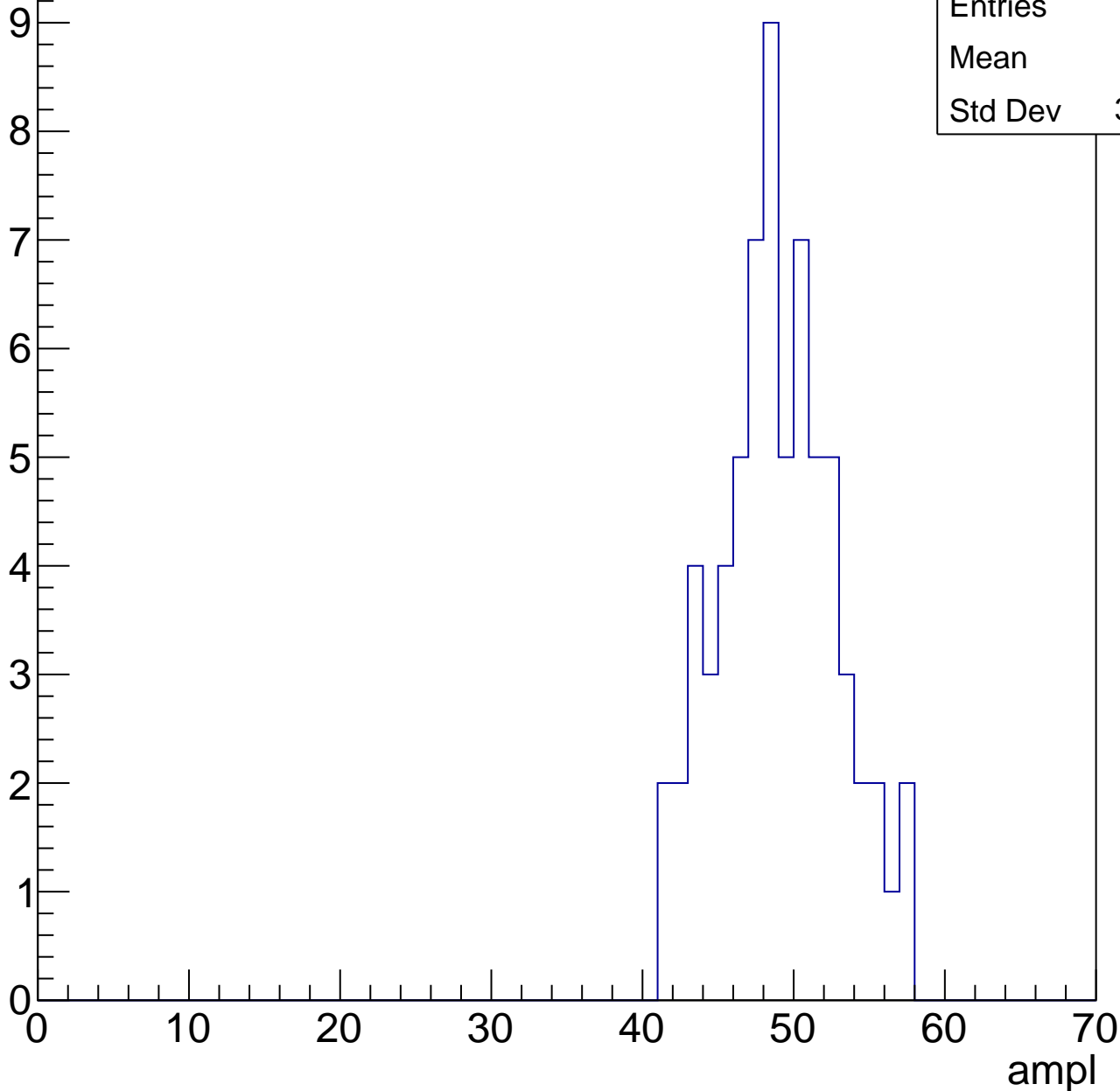


B0L002S, U5-ch66, adc3

calib_packv5_042523_0143.root, FC#8, port C1

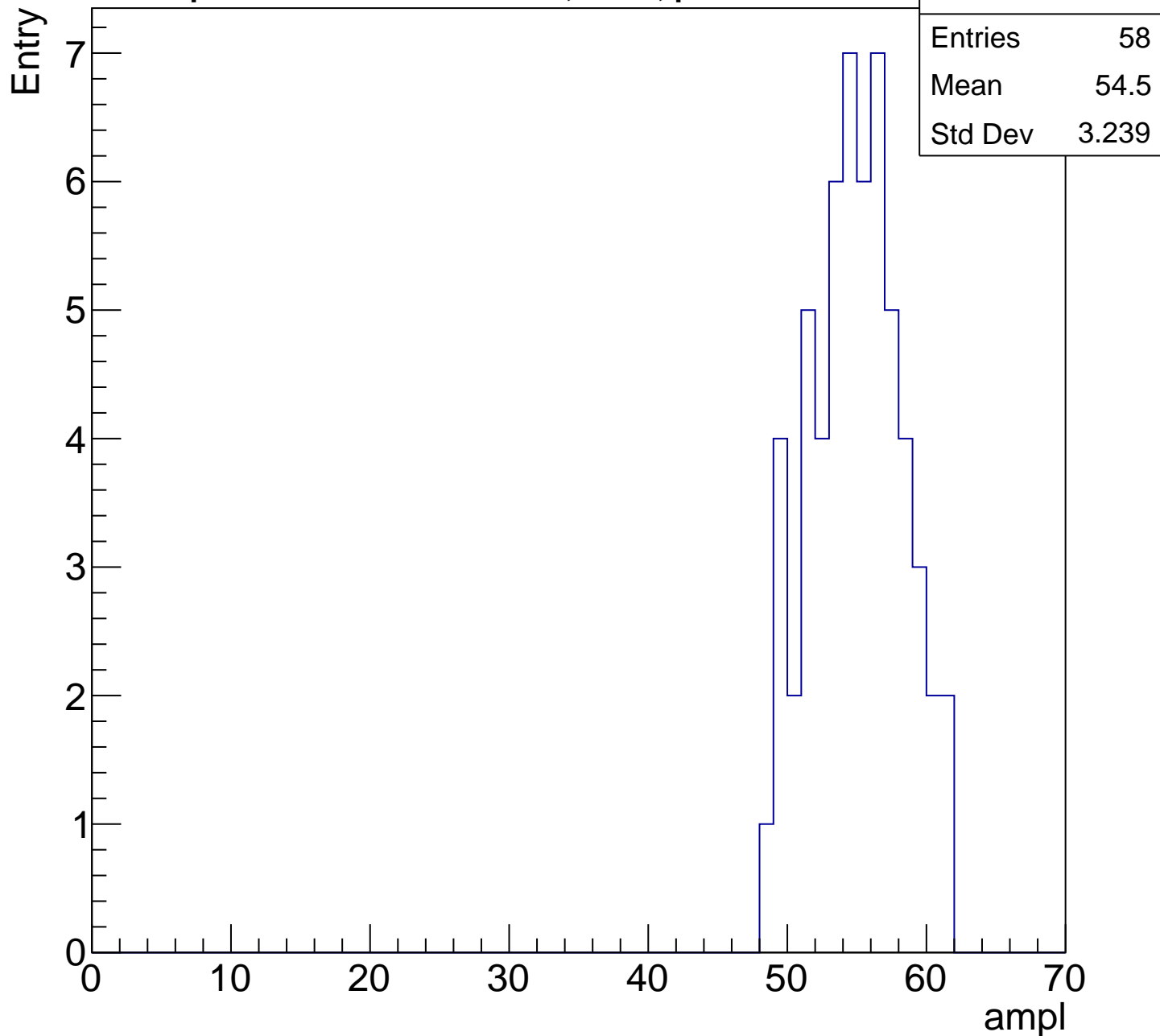
Entry

Entries	68
Mean	48.5
Std Dev	3.821



B0L002S, U5-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1

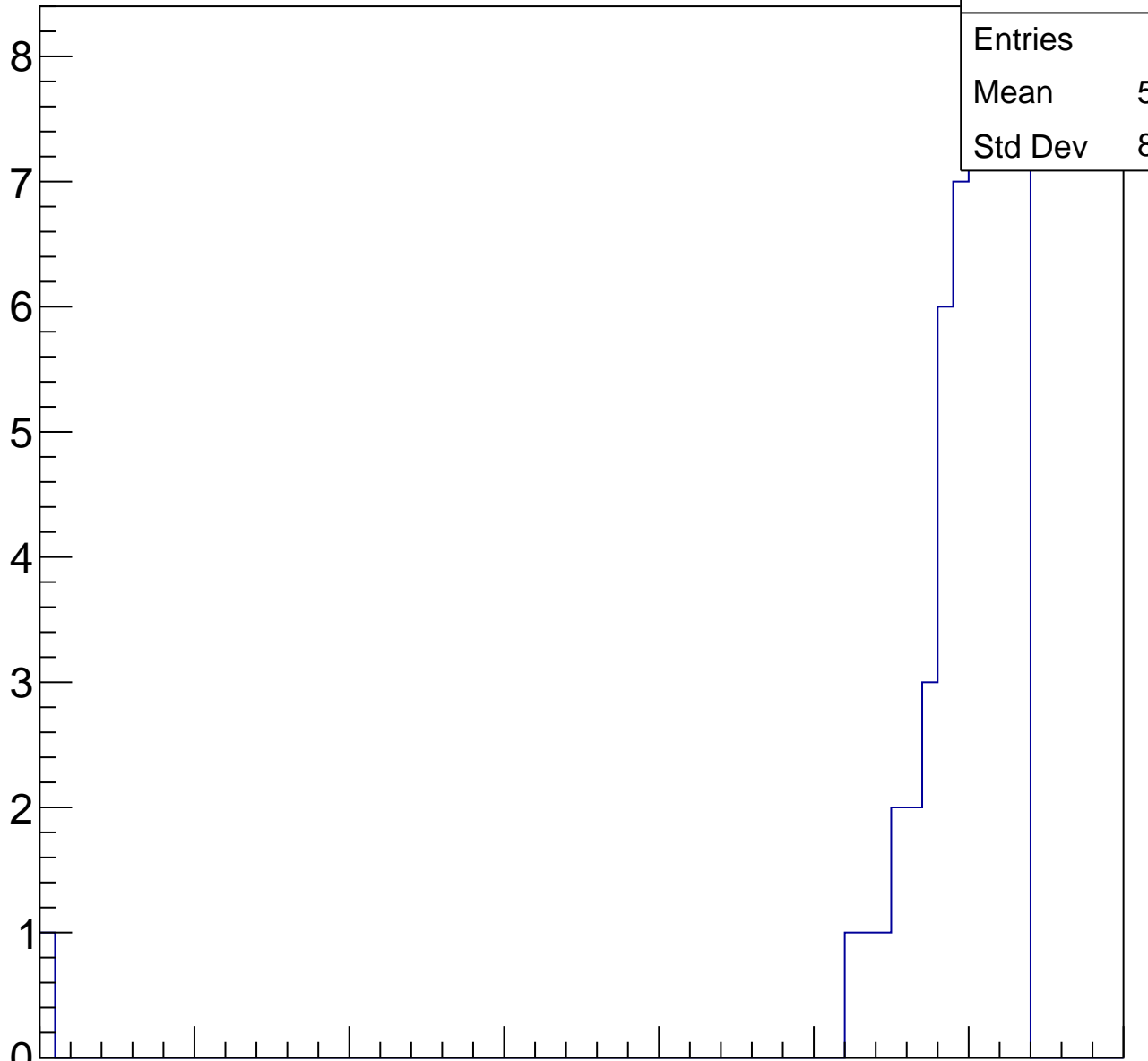
Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.59
Std Dev	8.334

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U5-ch67, adc0

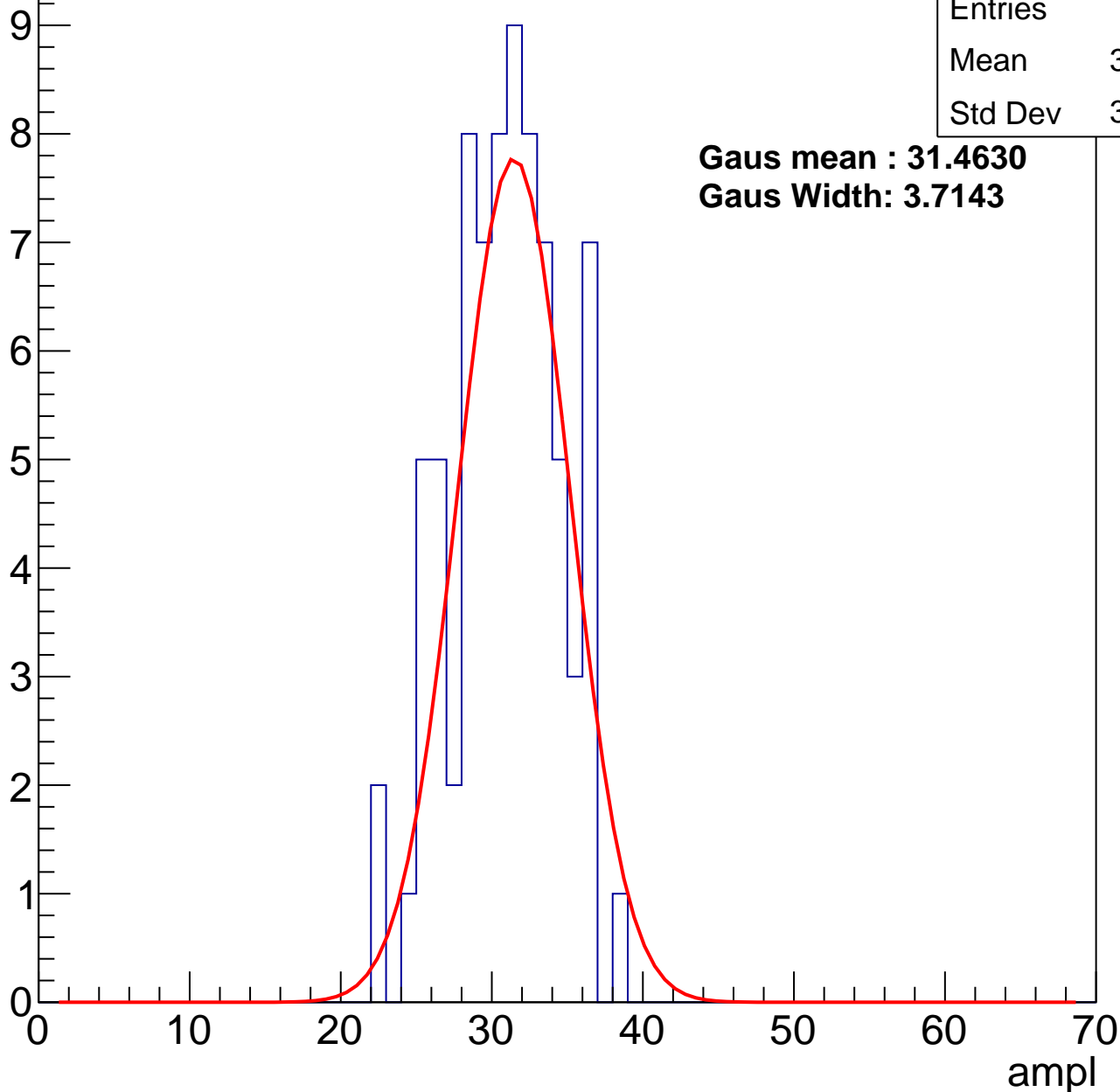
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	30.45
Std Dev	3.554

Gaus mean : 31.4630

Gaus Width: 3.7143



B0L002S, U5-ch67, adc1

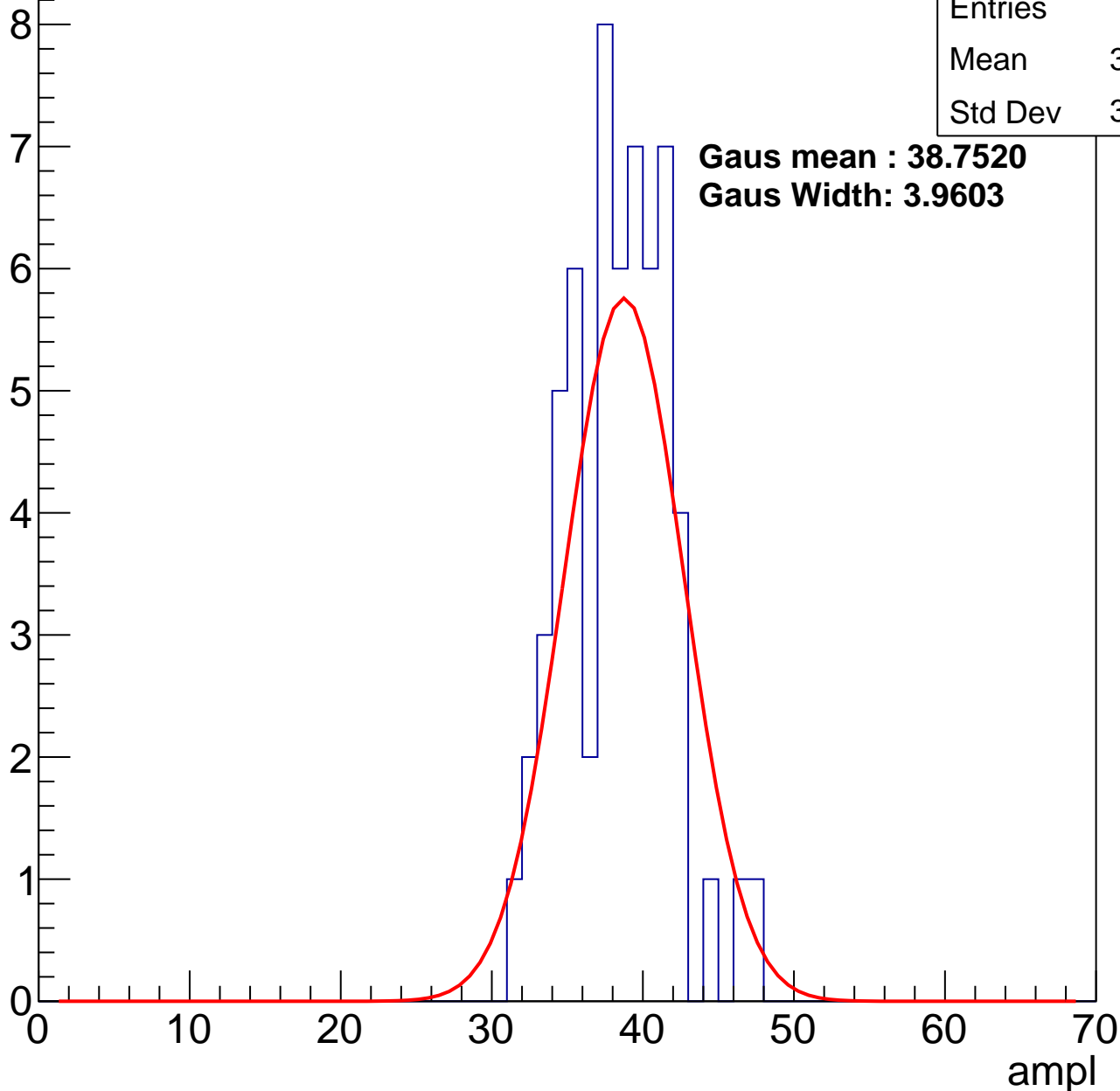
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.92
Std Dev	3.378

Gaus mean : 38.7520

Gaus Width: 3.9603



B0L002S, U5-ch67, adc2

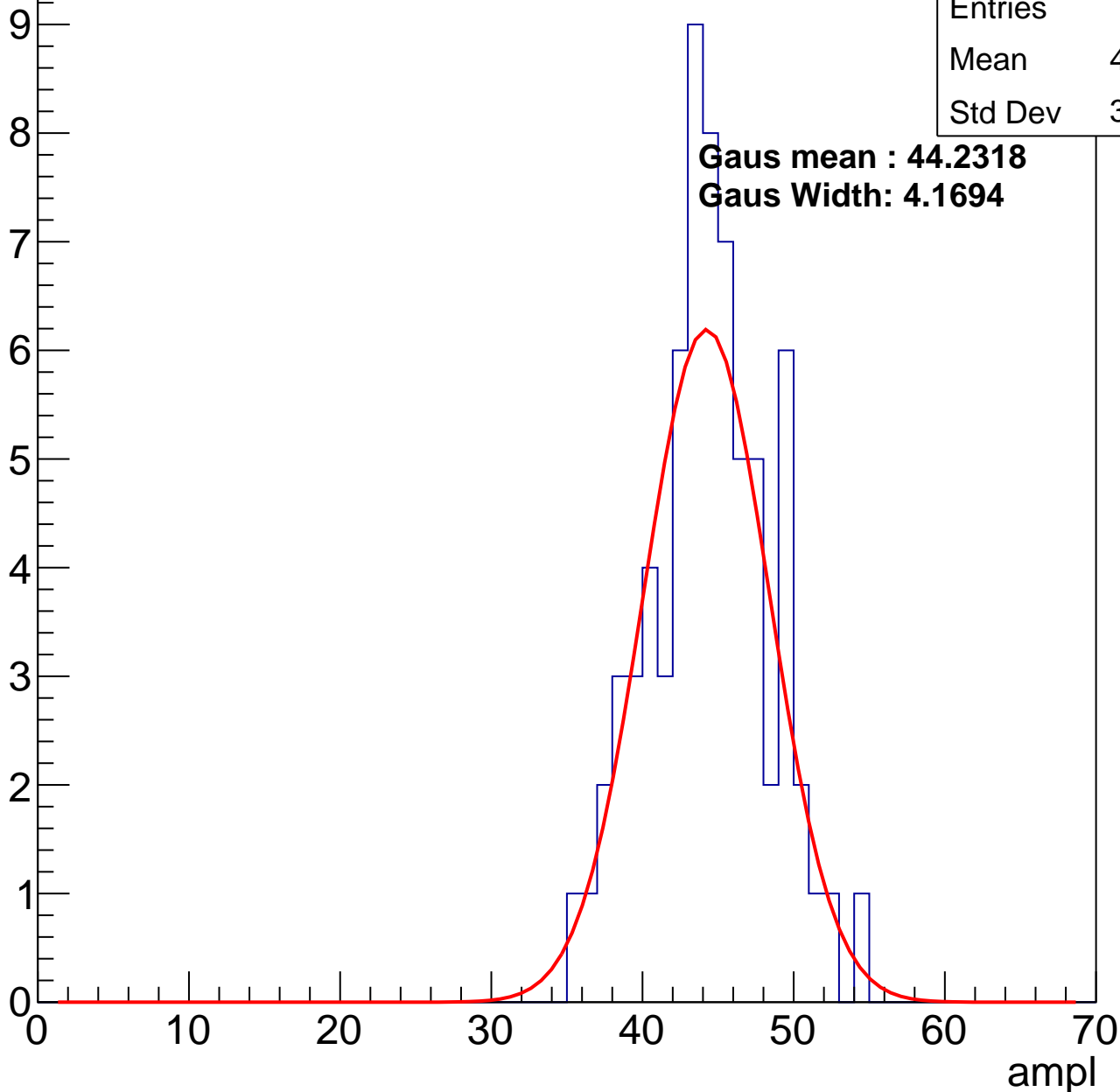
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.96
Std Dev	3.944

Gaus mean : 44.2318

Gaus Width: 4.1694

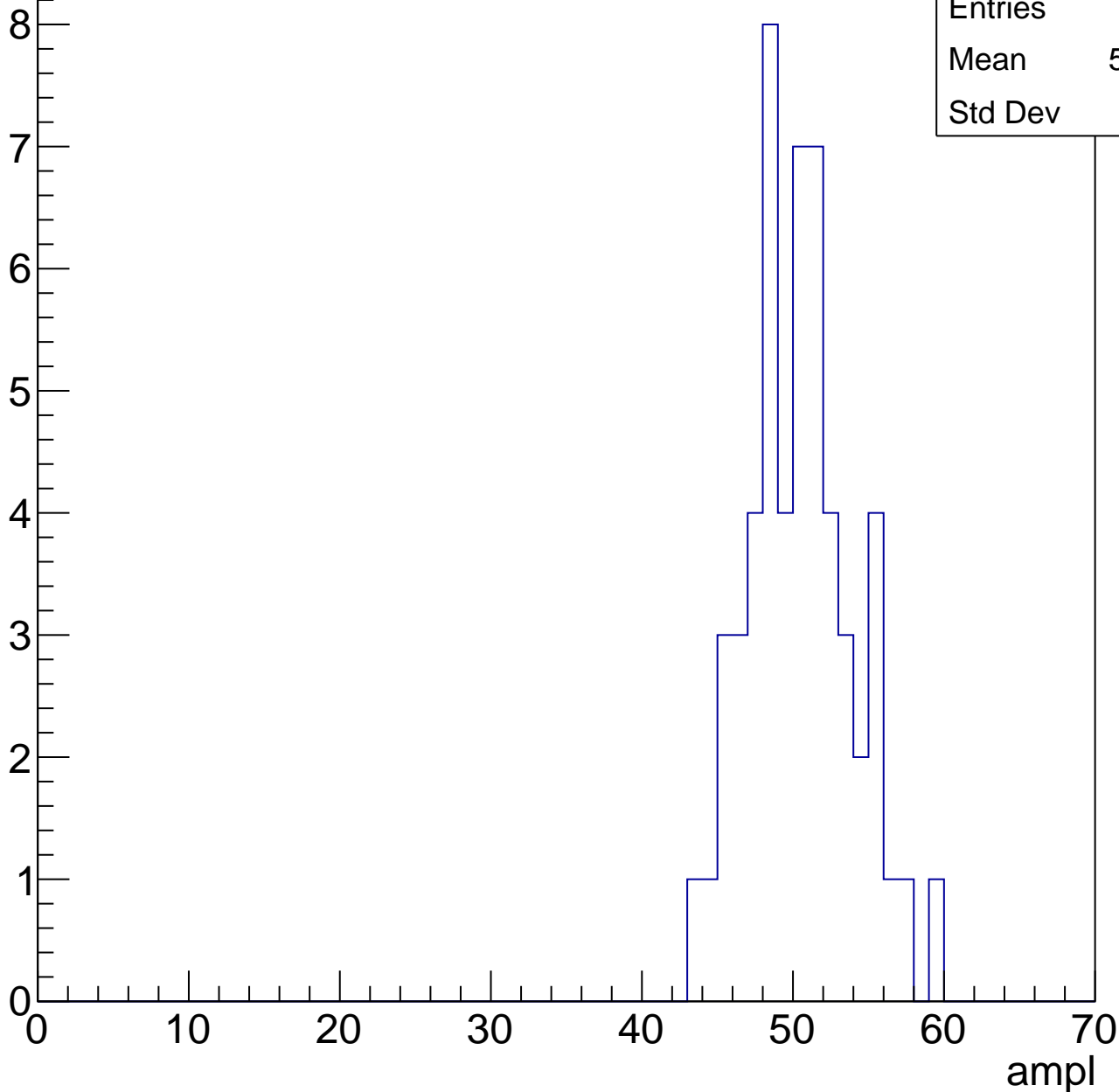


B0L002S, U5-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

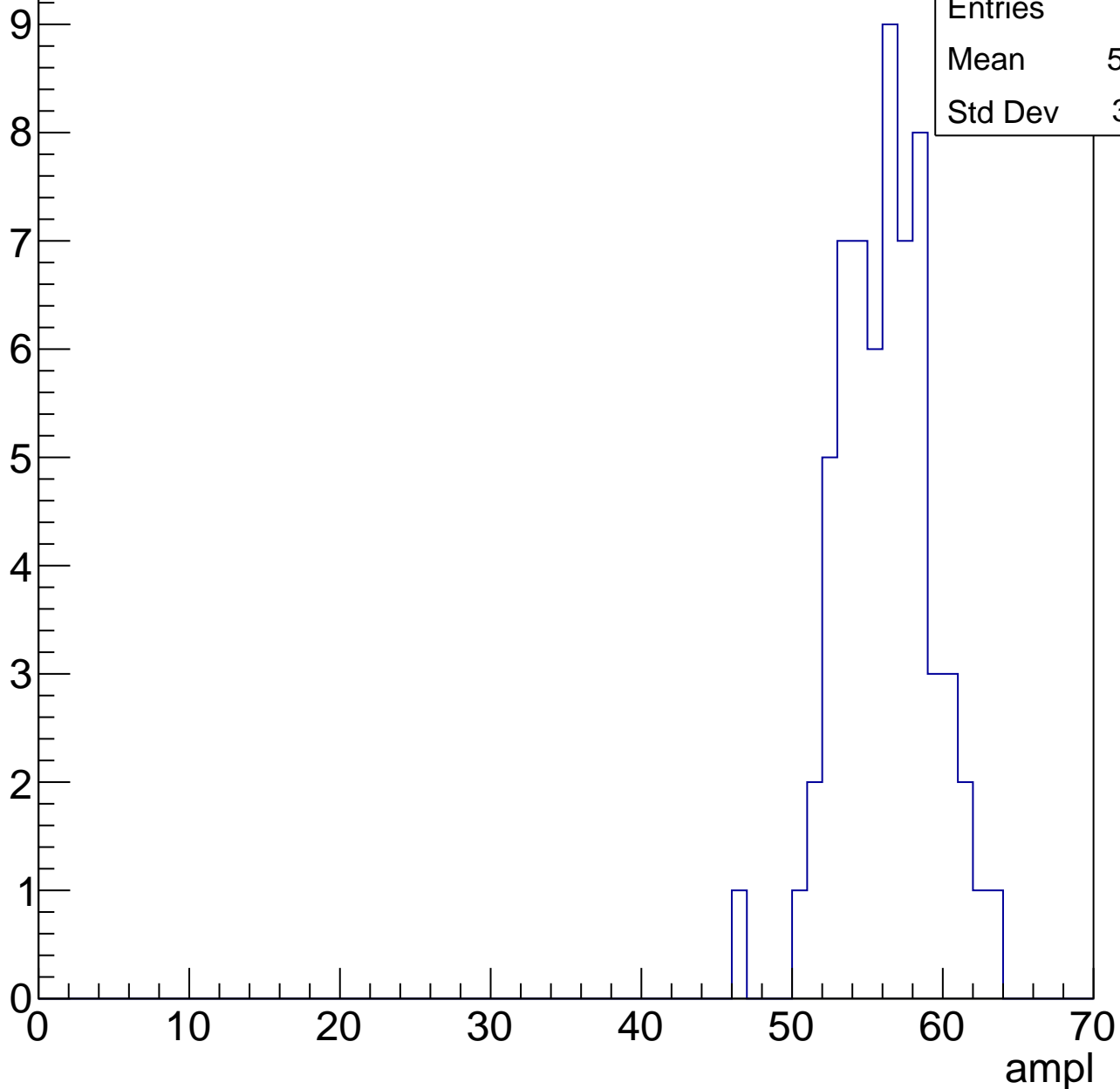
Entries	54
Mean	50.04
Std Dev	3.41



B0L002S, U5-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	38
Mean	60.68
Std Dev	1.718

ampl

10

20

30

40

50

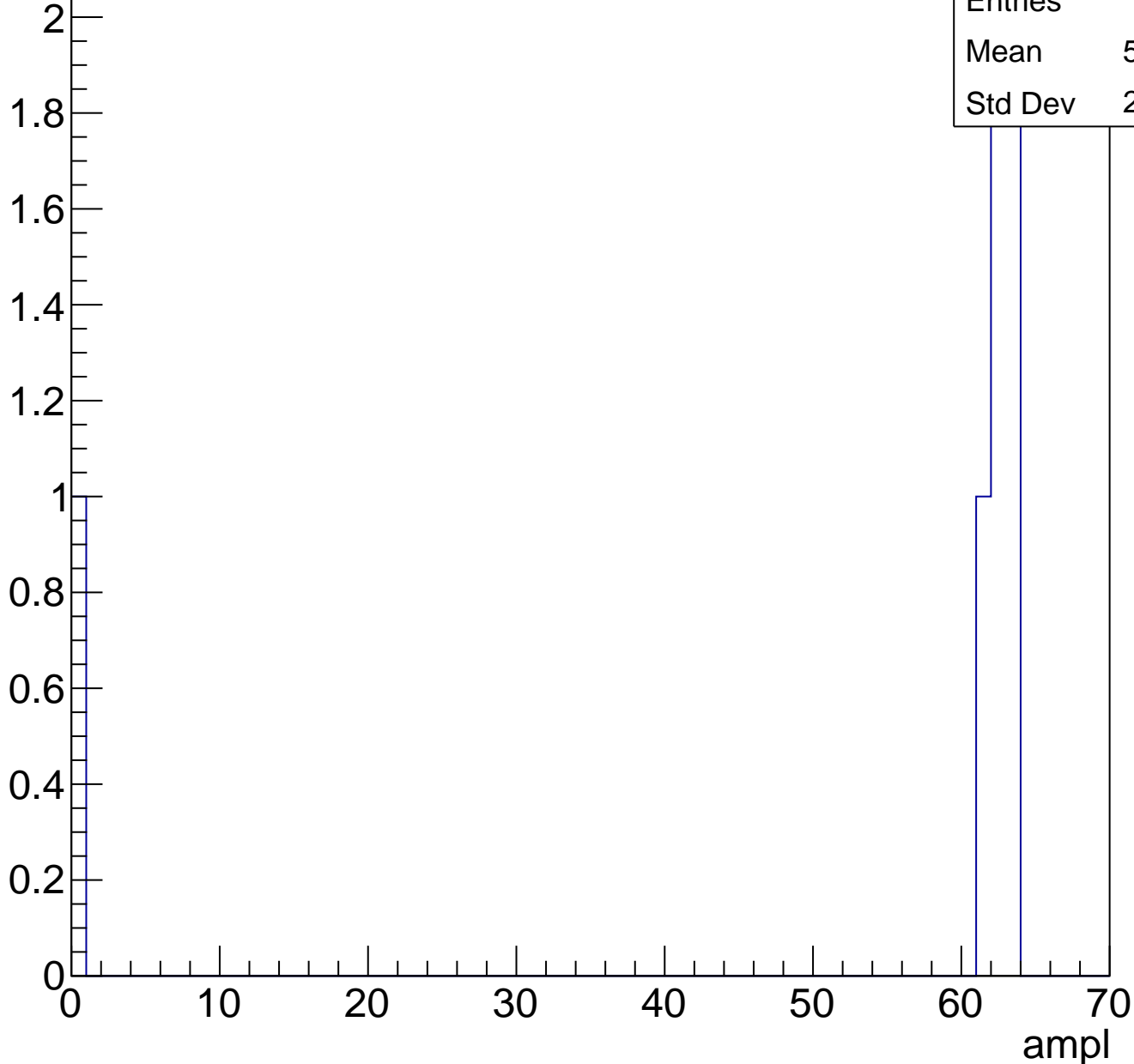
60

70

B0L002S, U5-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch68, adc0

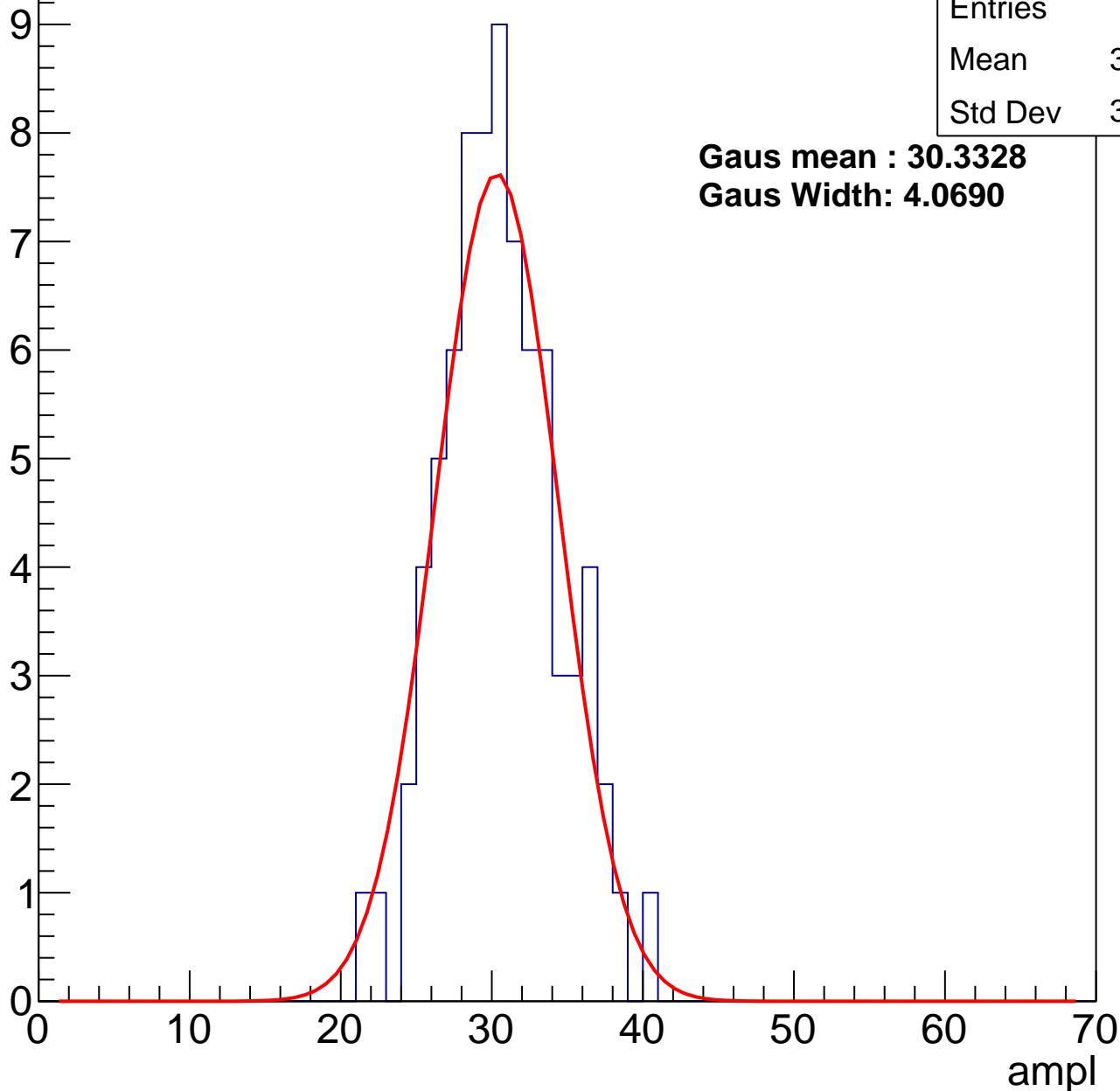
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.12
Std Dev	3.786

Gaus mean : 30.3328

Gaus Width: 4.0690



B0L002S, U5-ch68, adc1

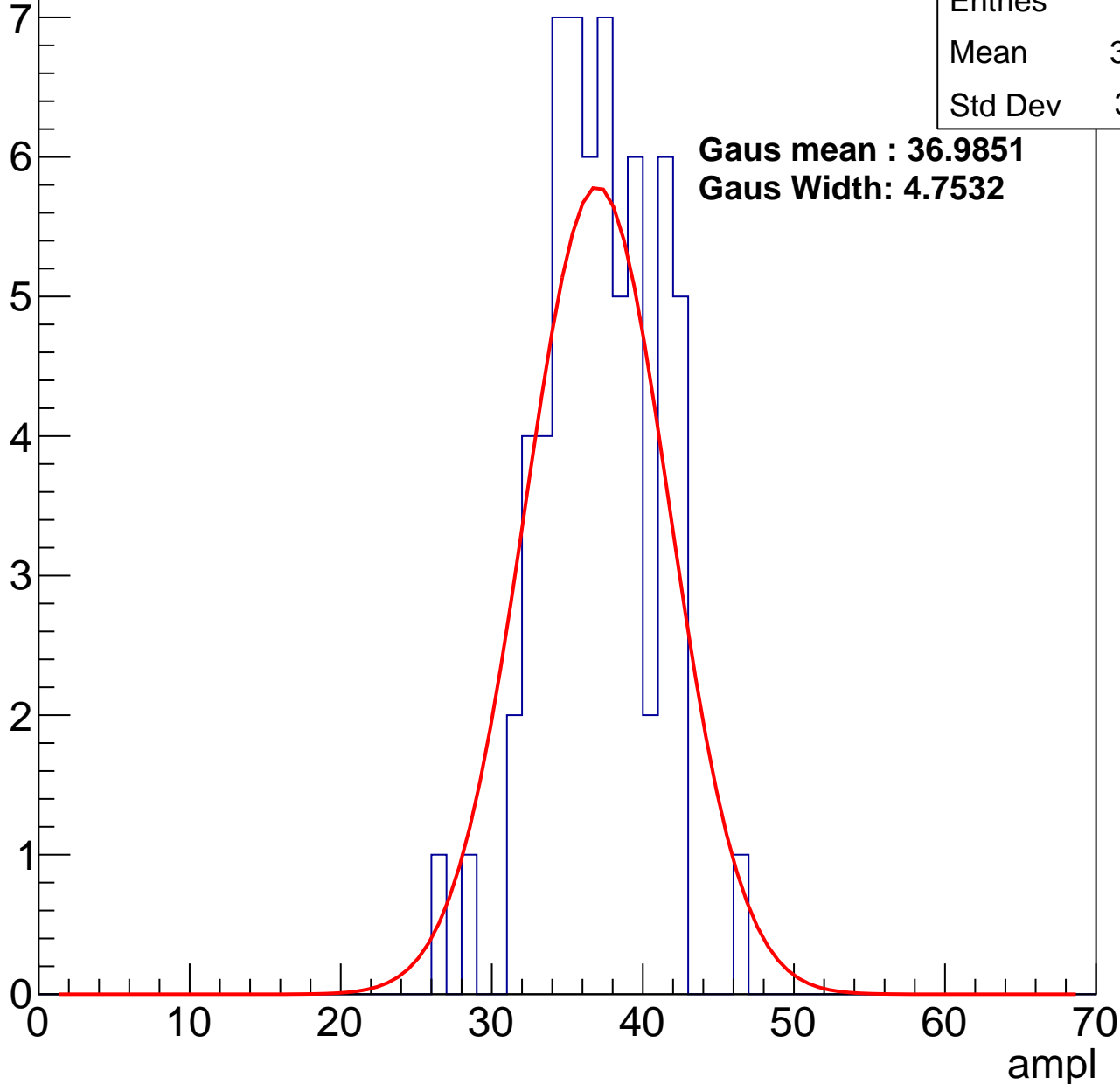
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.56
Std Dev	3.691

Gaus mean : 36.9851

Gaus Width: 4.7532



B0L002S, U5-ch68, adc2

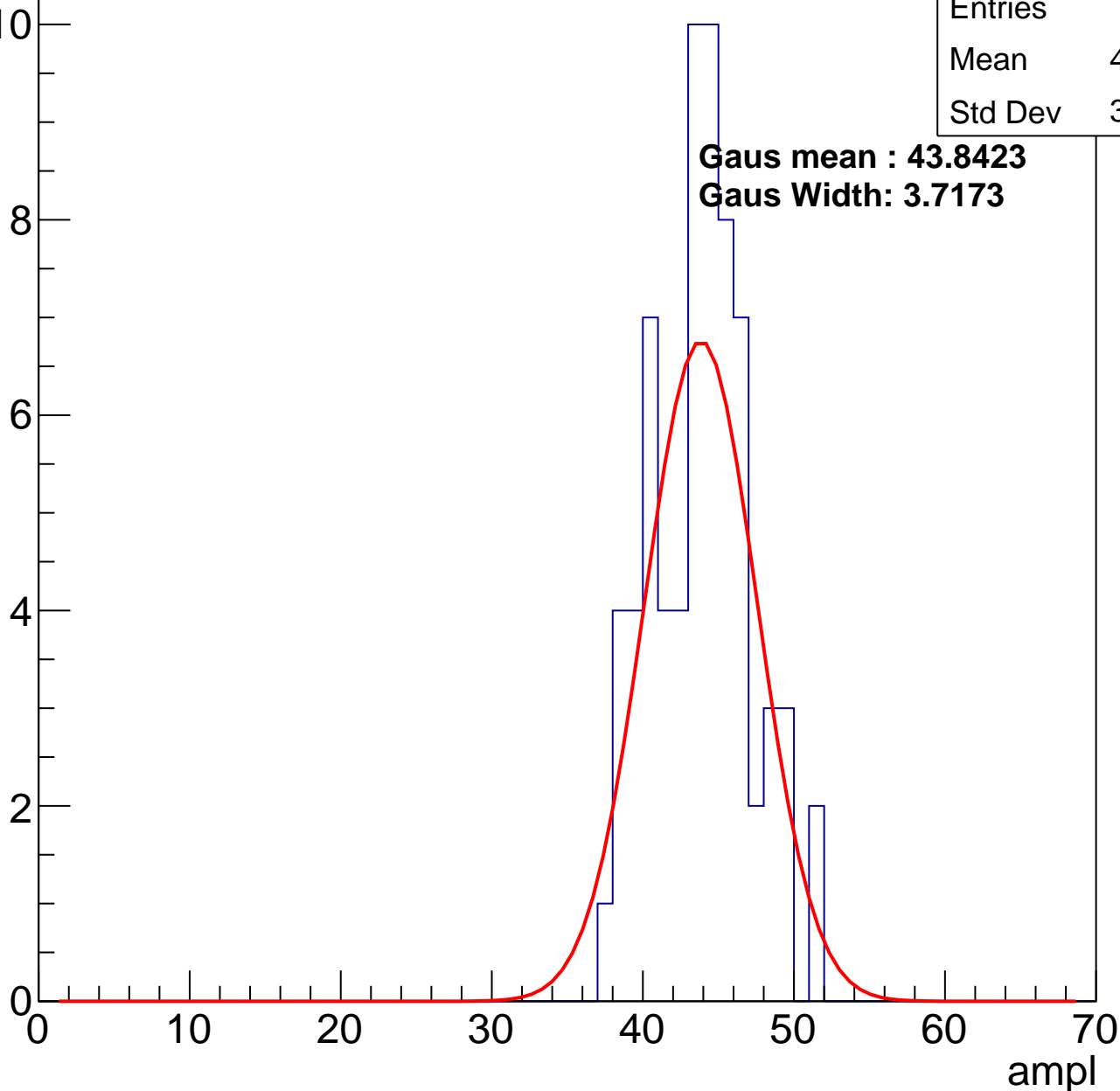
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.42
Std Dev	3.228

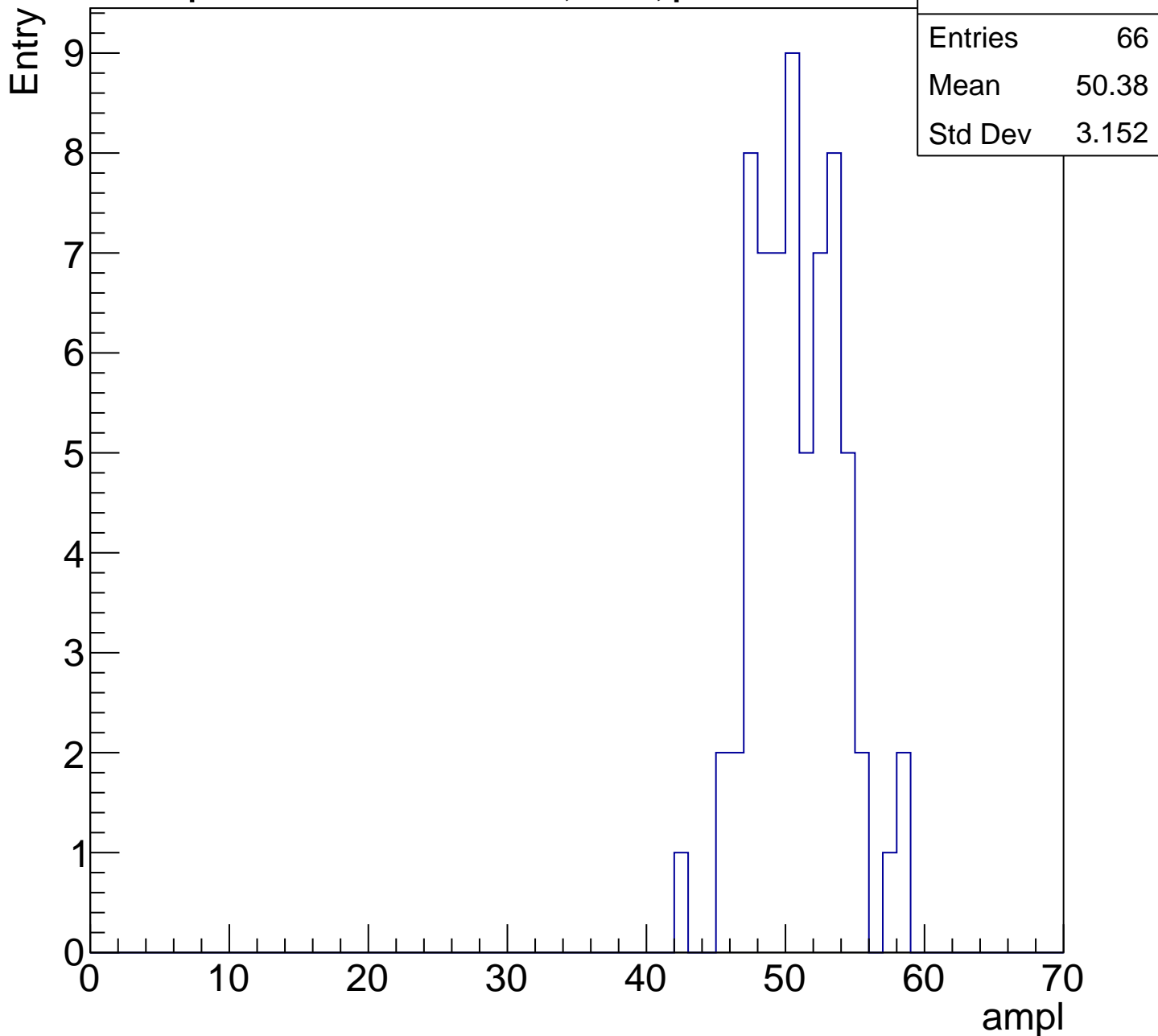
Gaus mean : 43.8423

Gaus Width: 3.7173



B0L002S, U5-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1

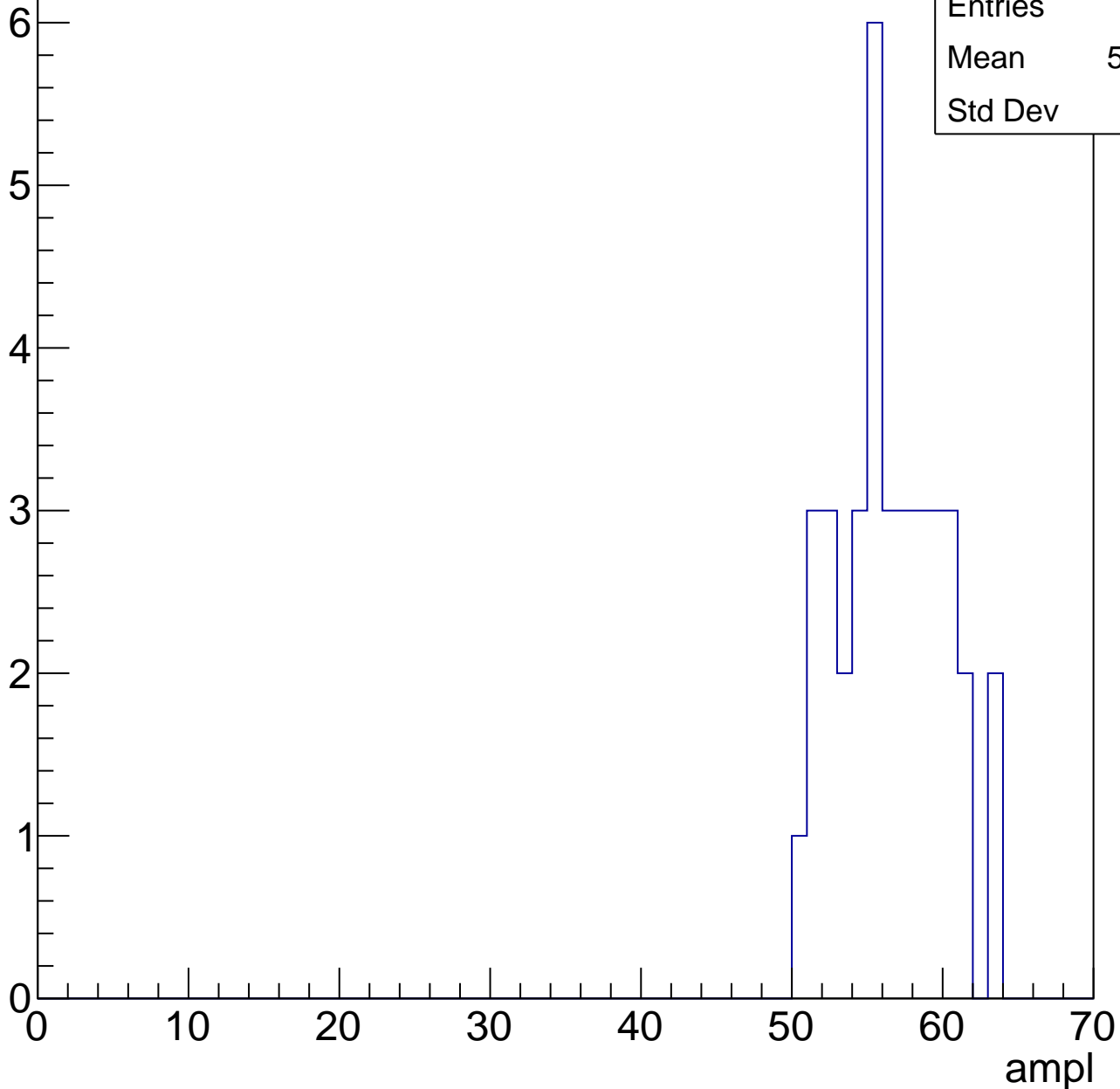


B0L002S, U5-ch68, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

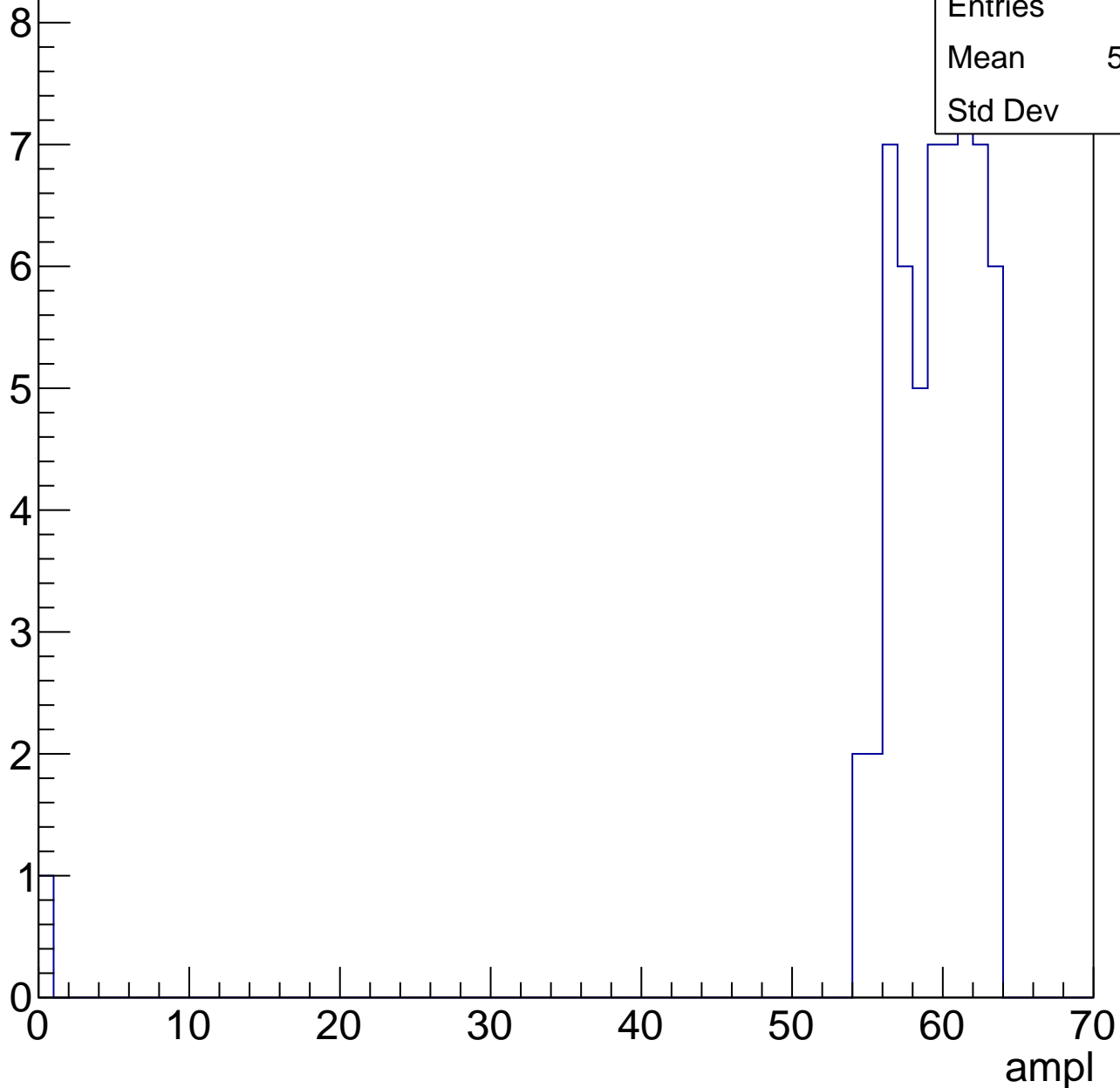
Entries	37
Mean	56.08
Std Dev	3.42



B0L002S, U5-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch69, adc0

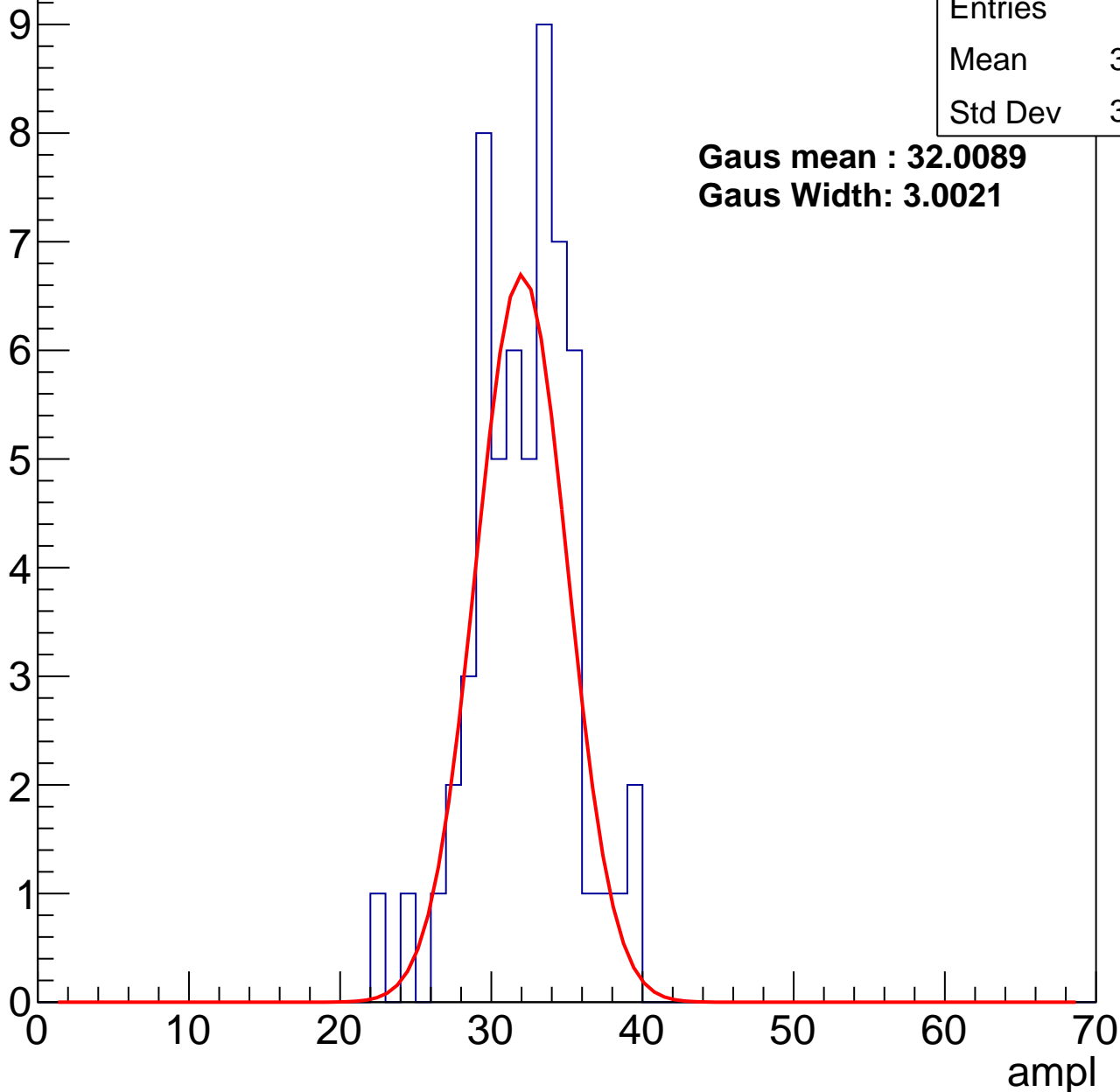
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	31.73
Std Dev	3.339

Gaus mean : 32.0089

Gaus Width: 3.0021



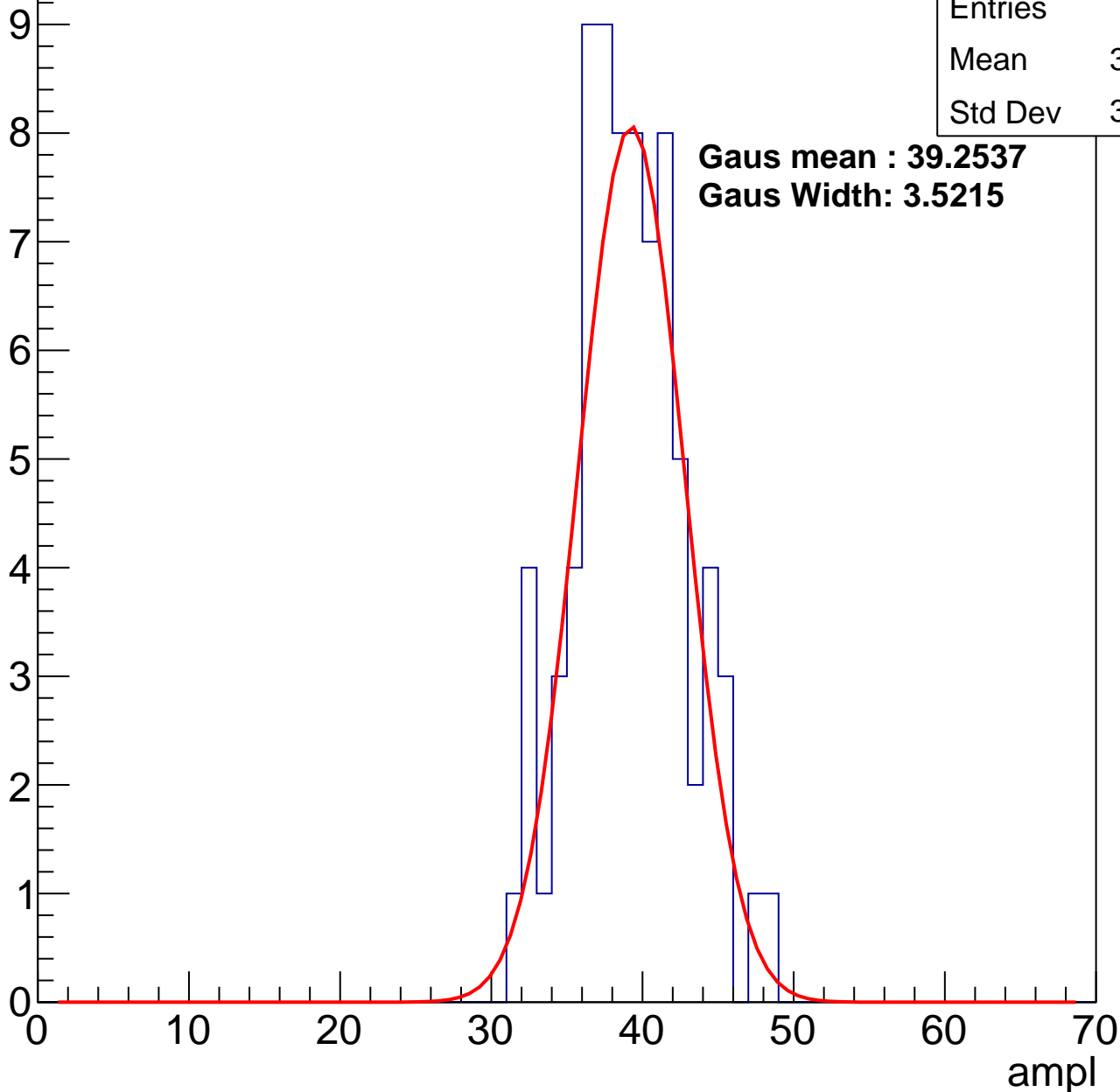
B0L002S, U5-ch69, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	38.68
Std Dev	3.618

Gaus mean : 39.2537
Gaus Width: 3.5215



B0L002S, U5-ch69, adc2

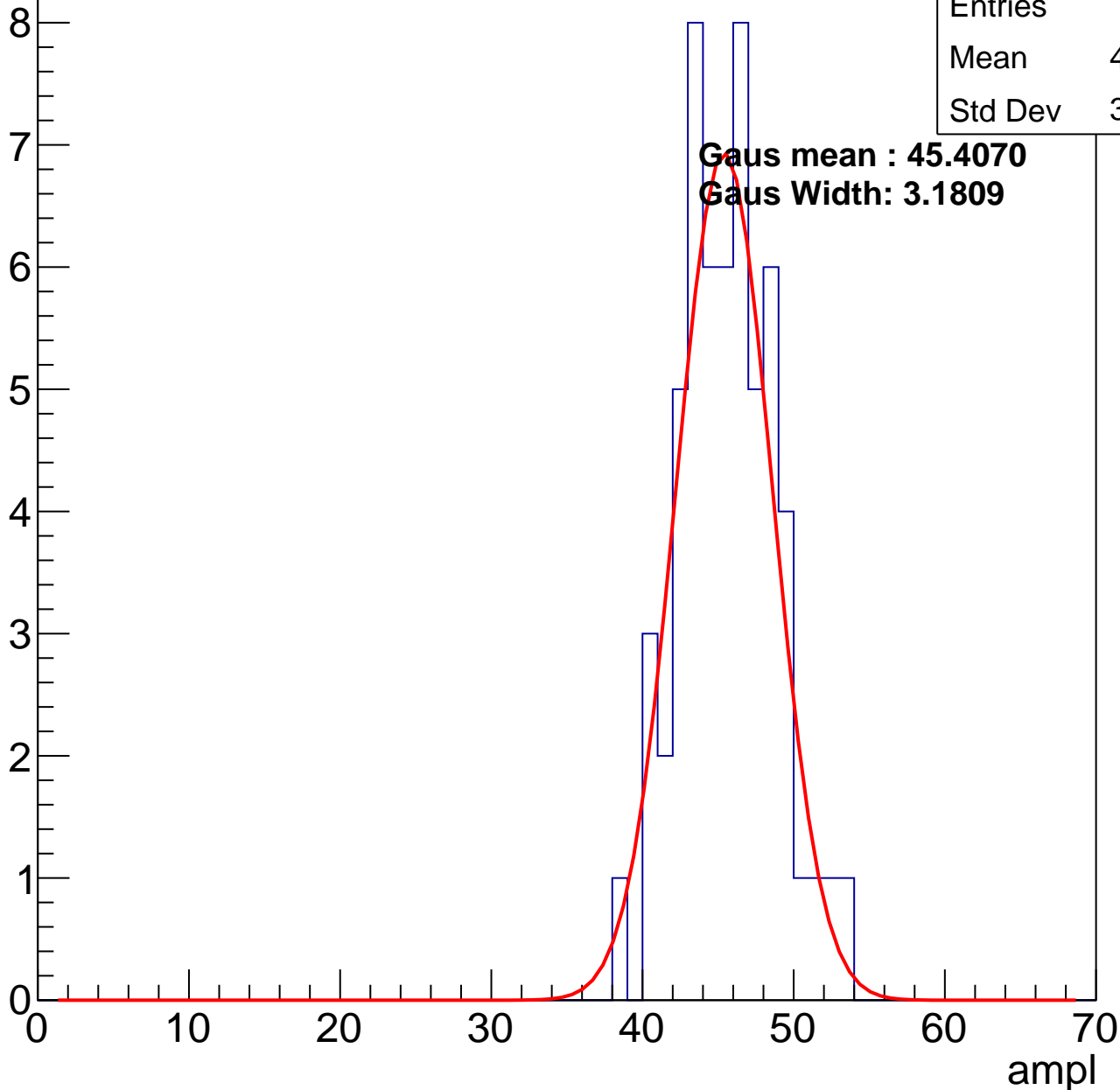
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	45.19
Std Dev	3.099

Gaus mean : 45.4070

Gaus Width: 3.1809

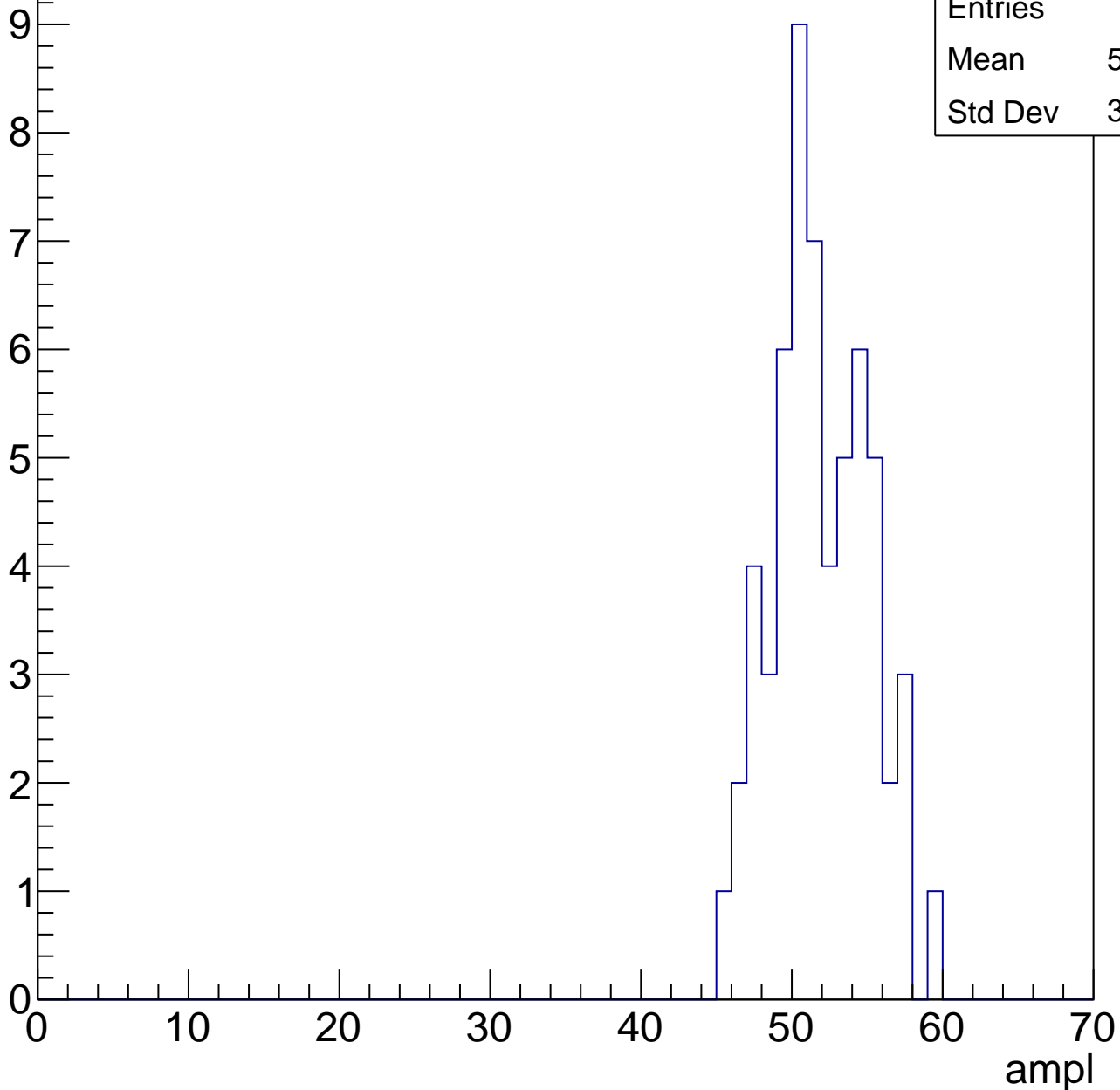


B0L002S, U5-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

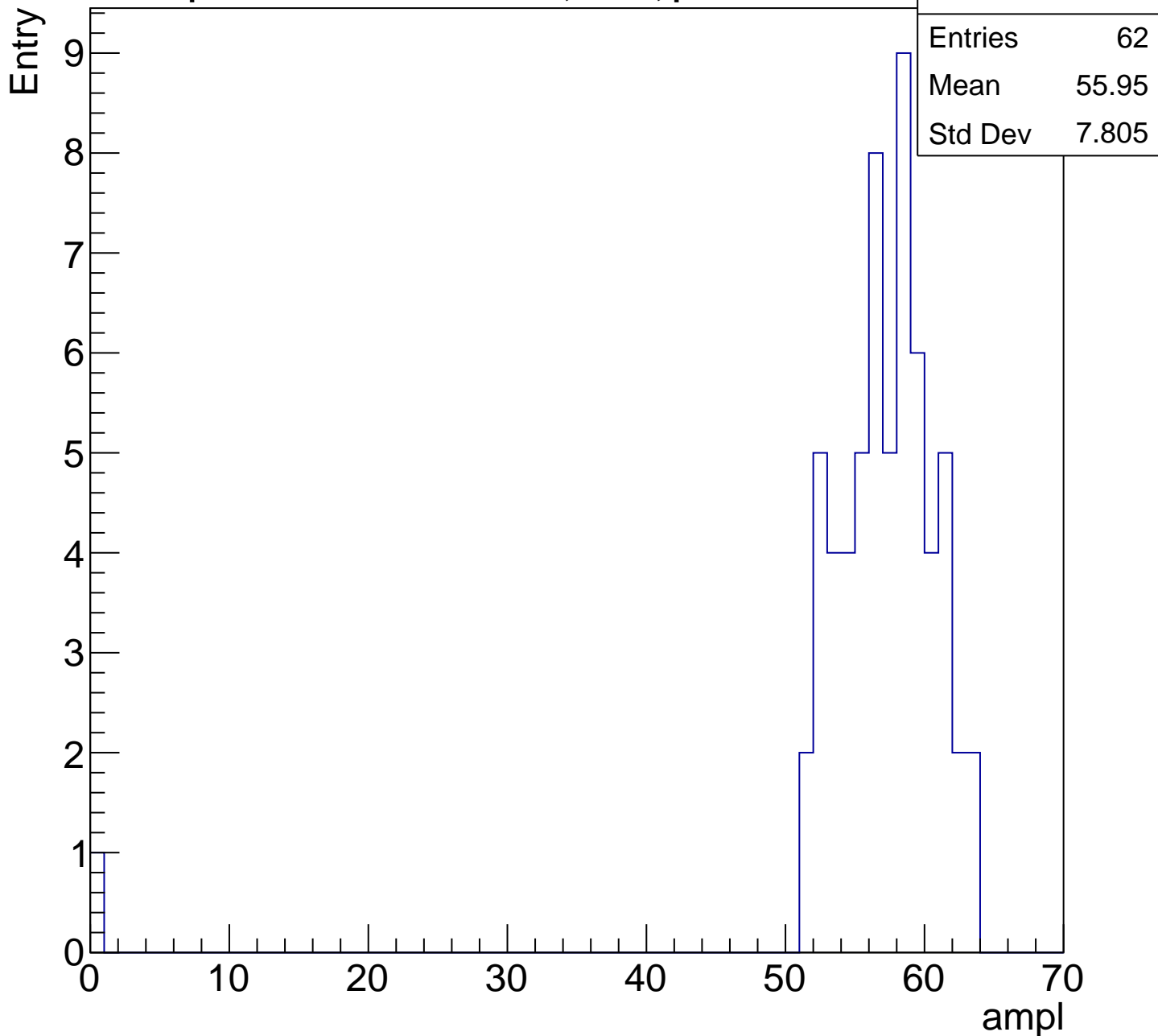
Entry

Entries	58
Mean	51.45
Std Dev	3.158



B0L002S, U5-ch69, adc4

calib_packv5_042523_0143.root, FC#8, port C1

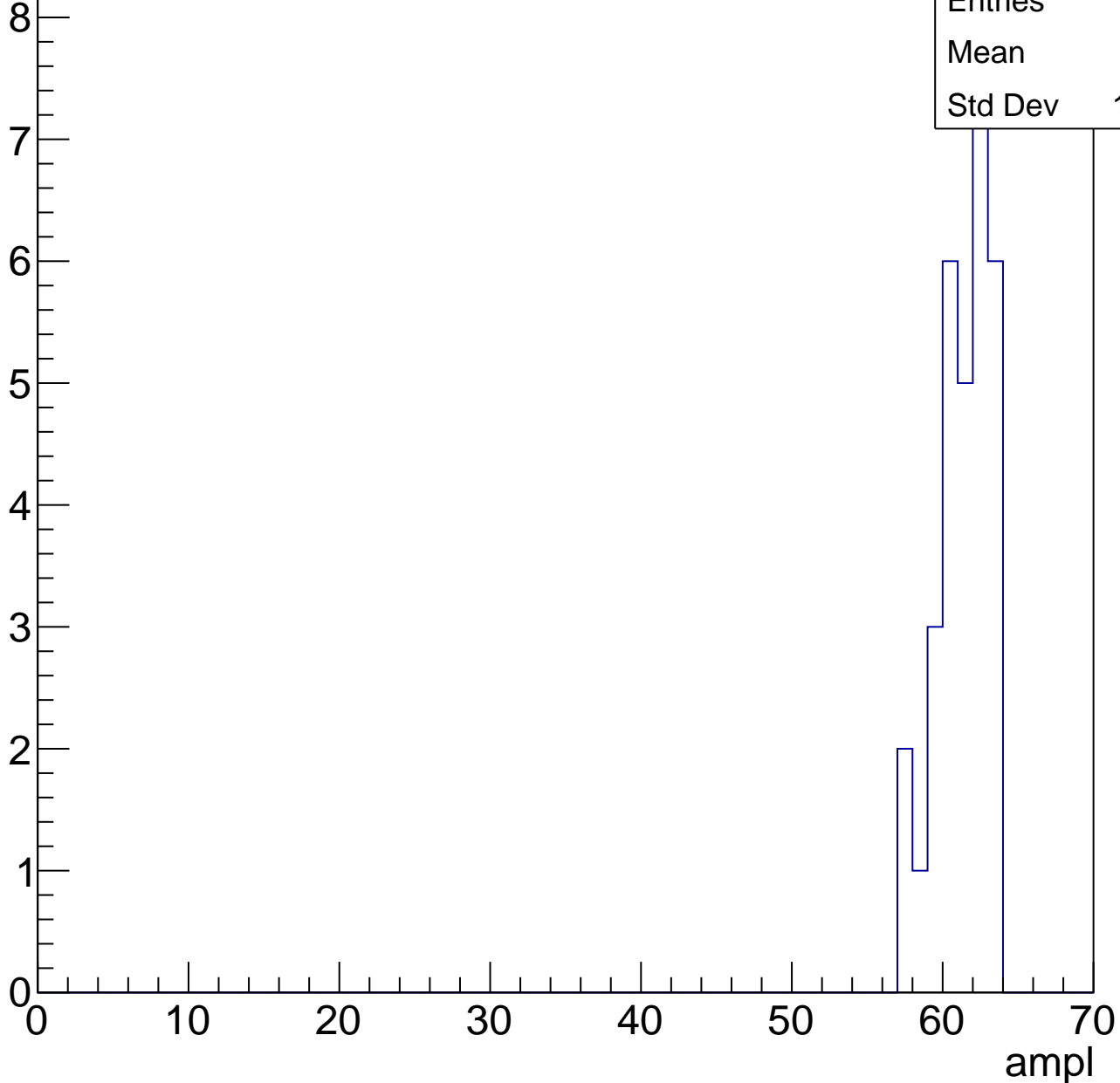


B0L002S, U5-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	60.9
Std Dev	1.711



B0L002S, U5-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch70, adc0

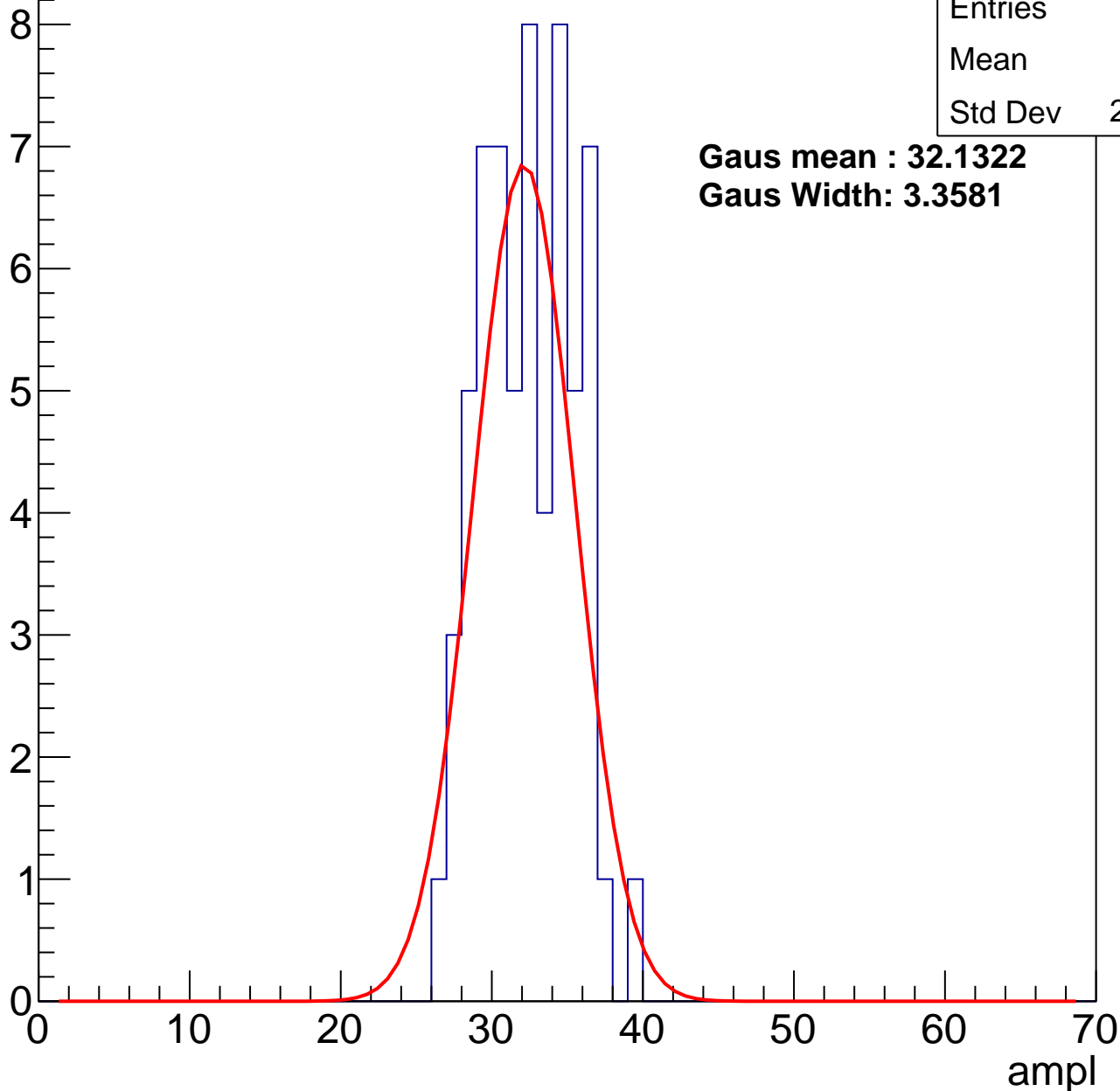
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.9
Std Dev	2.988

Gaus mean : 32.1322

Gaus Width: 3.3581



B0L002S, U5-ch70, adc1

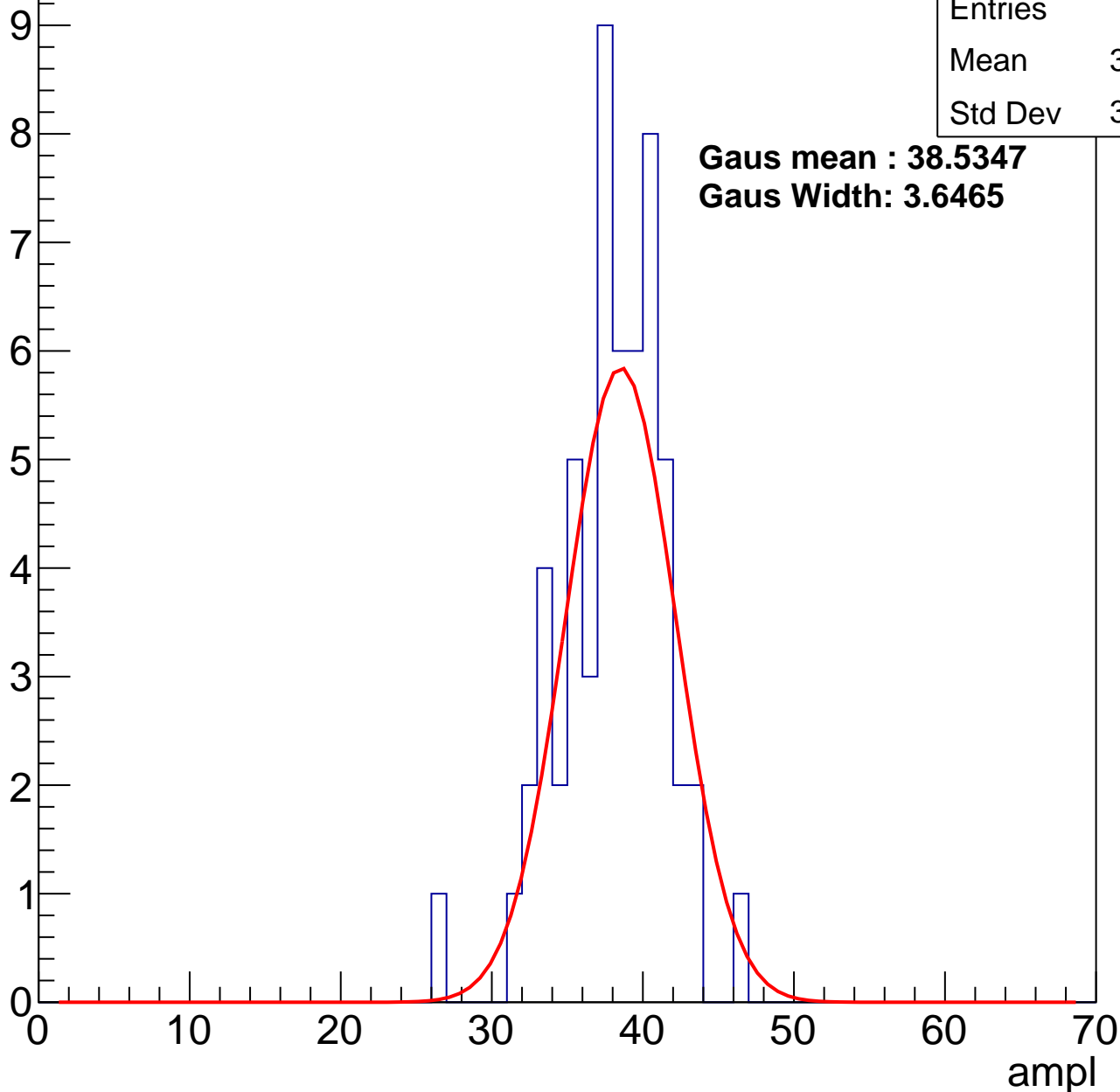
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	37.54
Std Dev	3.454

Gaus mean : 38.5347

Gaus Width: 3.6465



B0L002S, U5-ch70, adc2

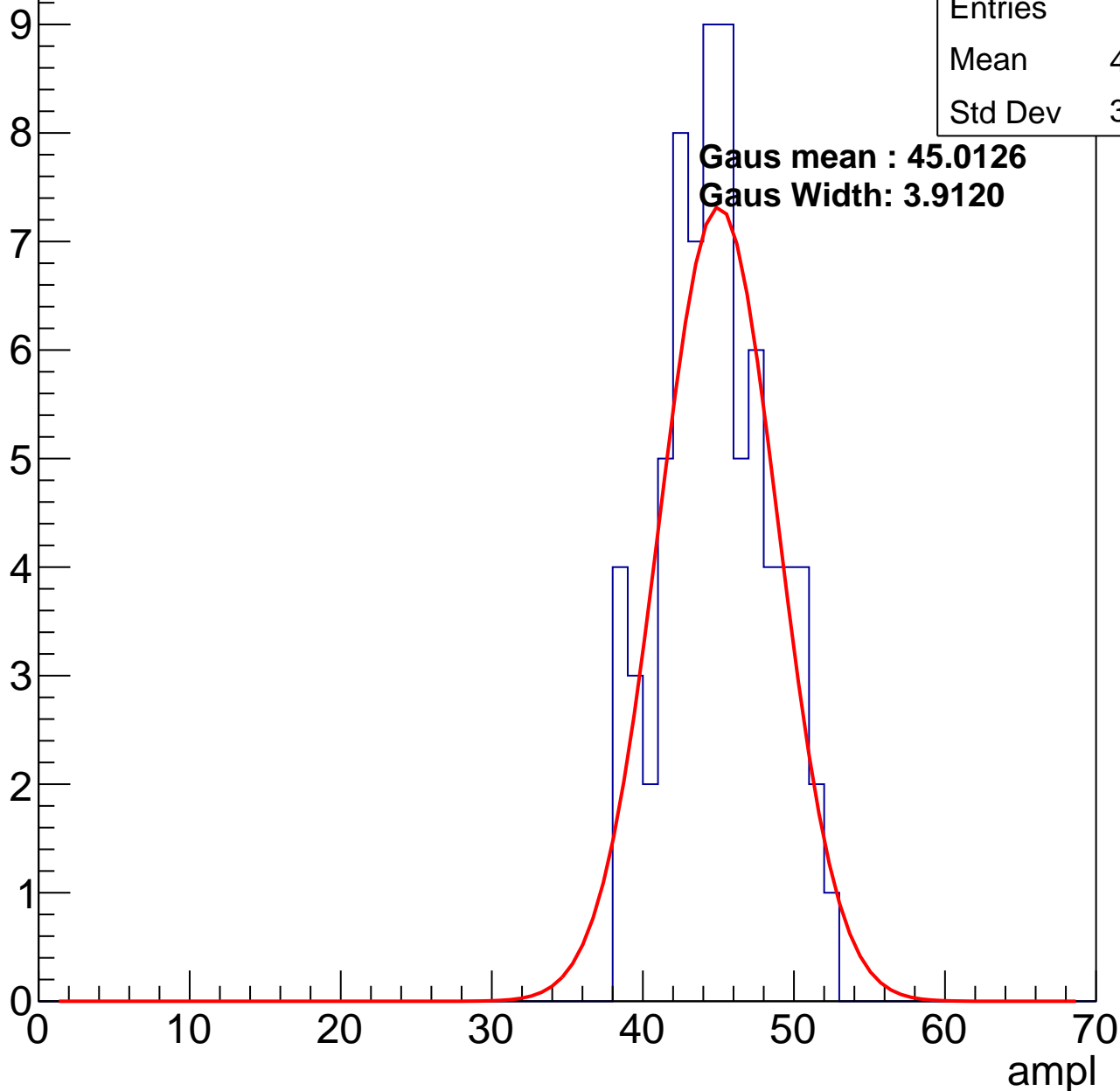
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.47
Std Dev	3.457

Gaus mean : 45.0126

Gaus Width: 3.9120

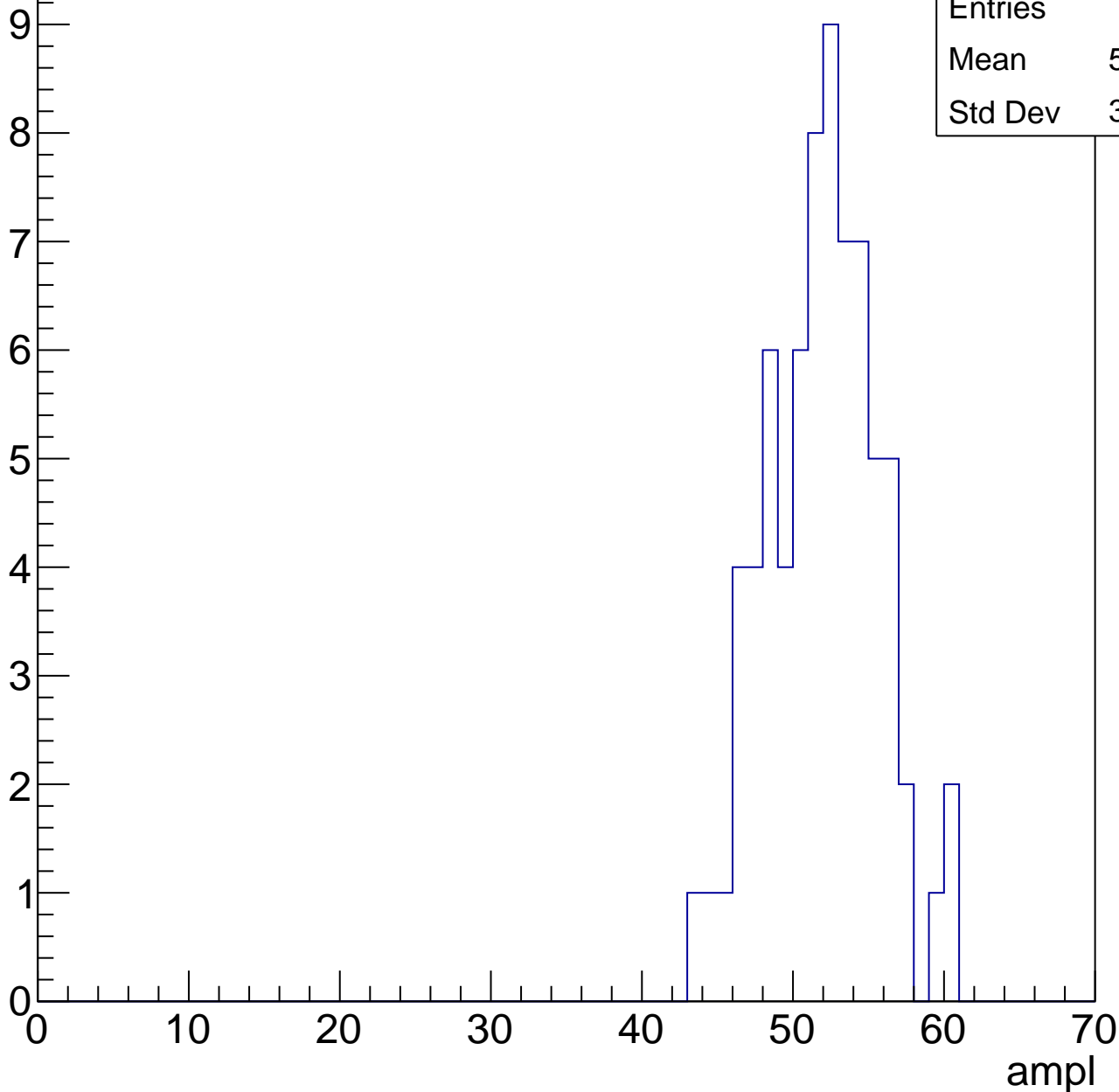


B0L002S, U5-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	51.52
Std Dev	3.649

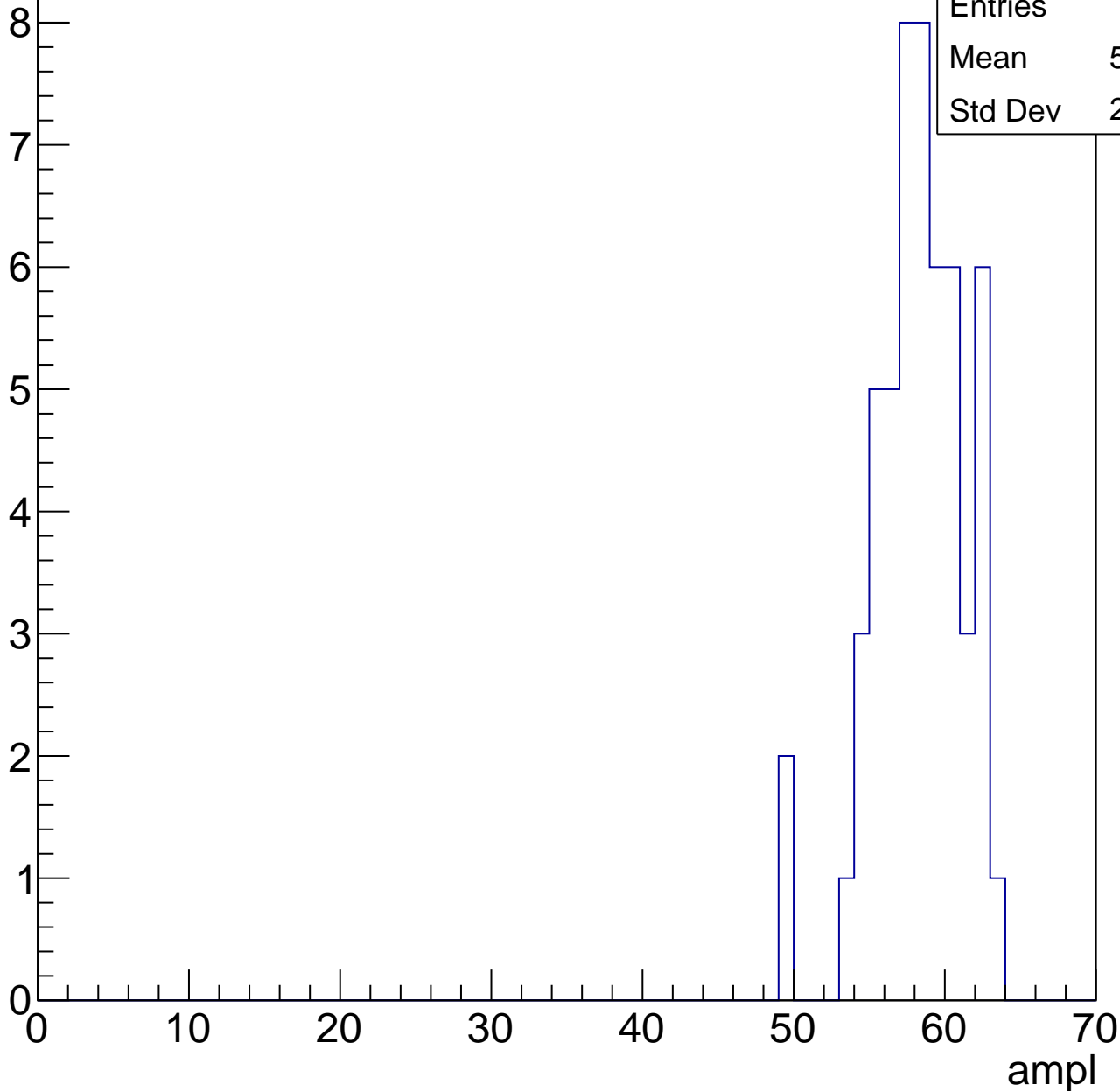


B0L002S, U5-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	57.78
Std Dev	2.992

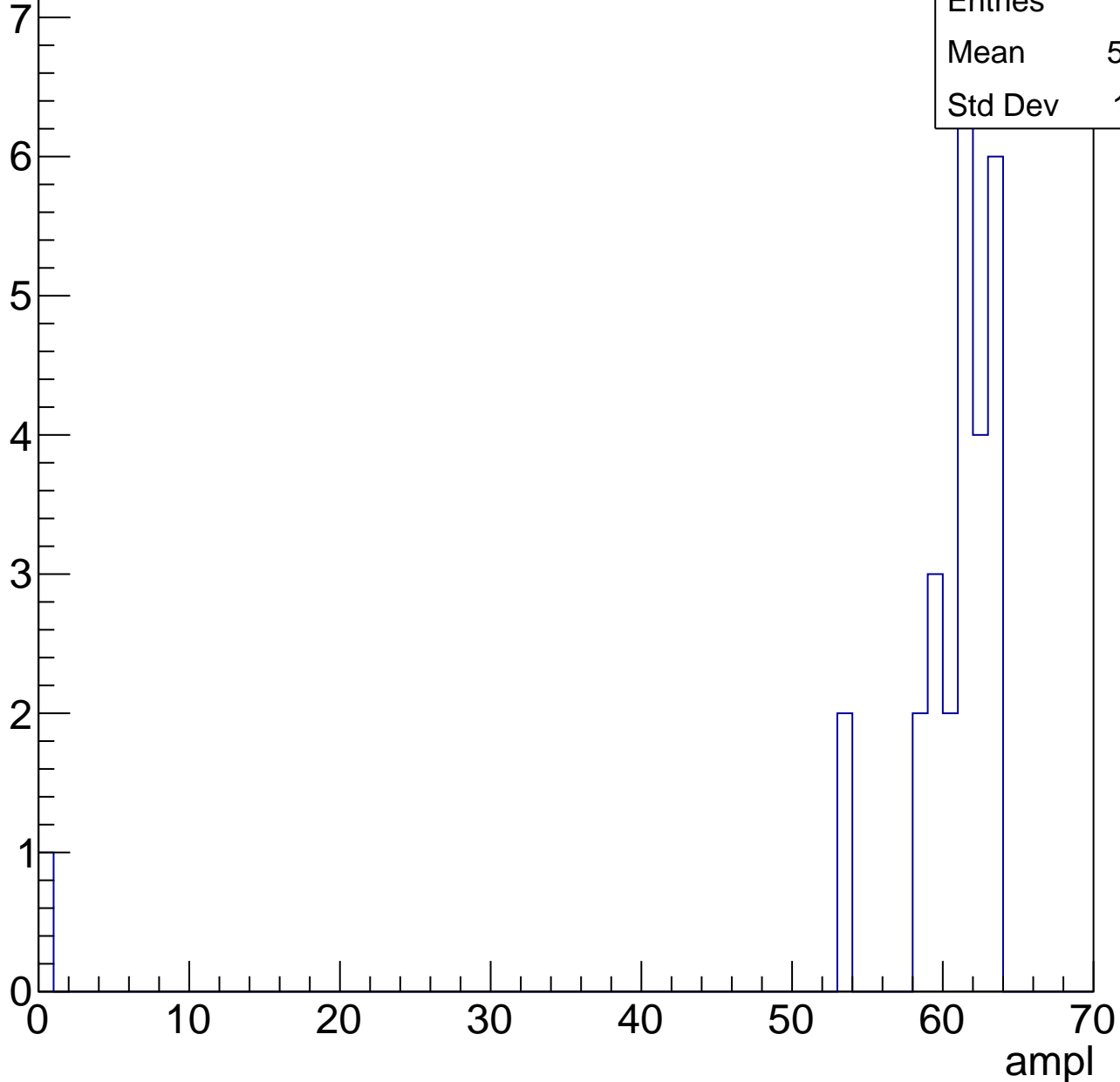


B0L002S, U5-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

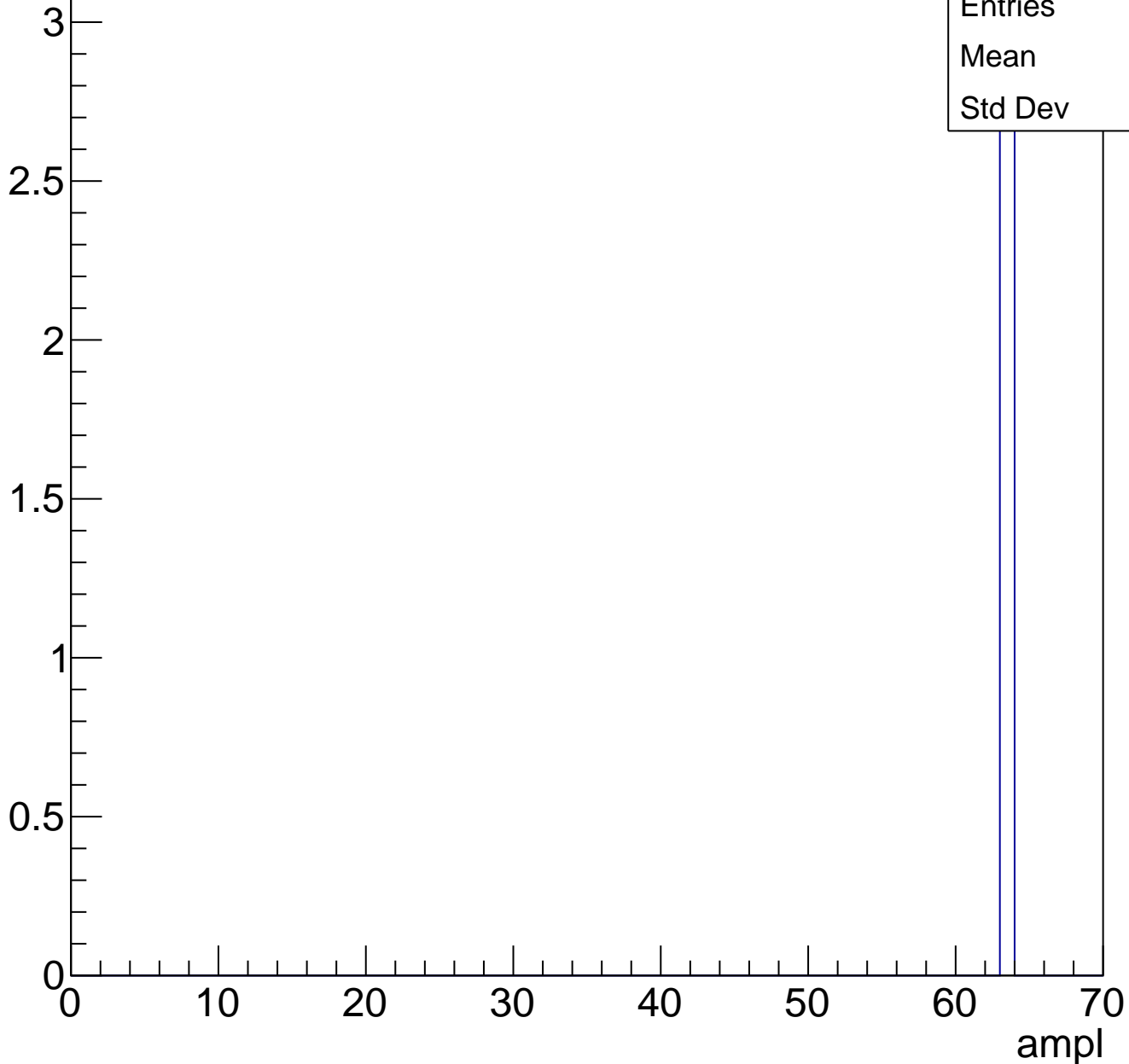
Entries	27
Mean	58.22
Std Dev	11.71



B0L002S, U5-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch71, adc0

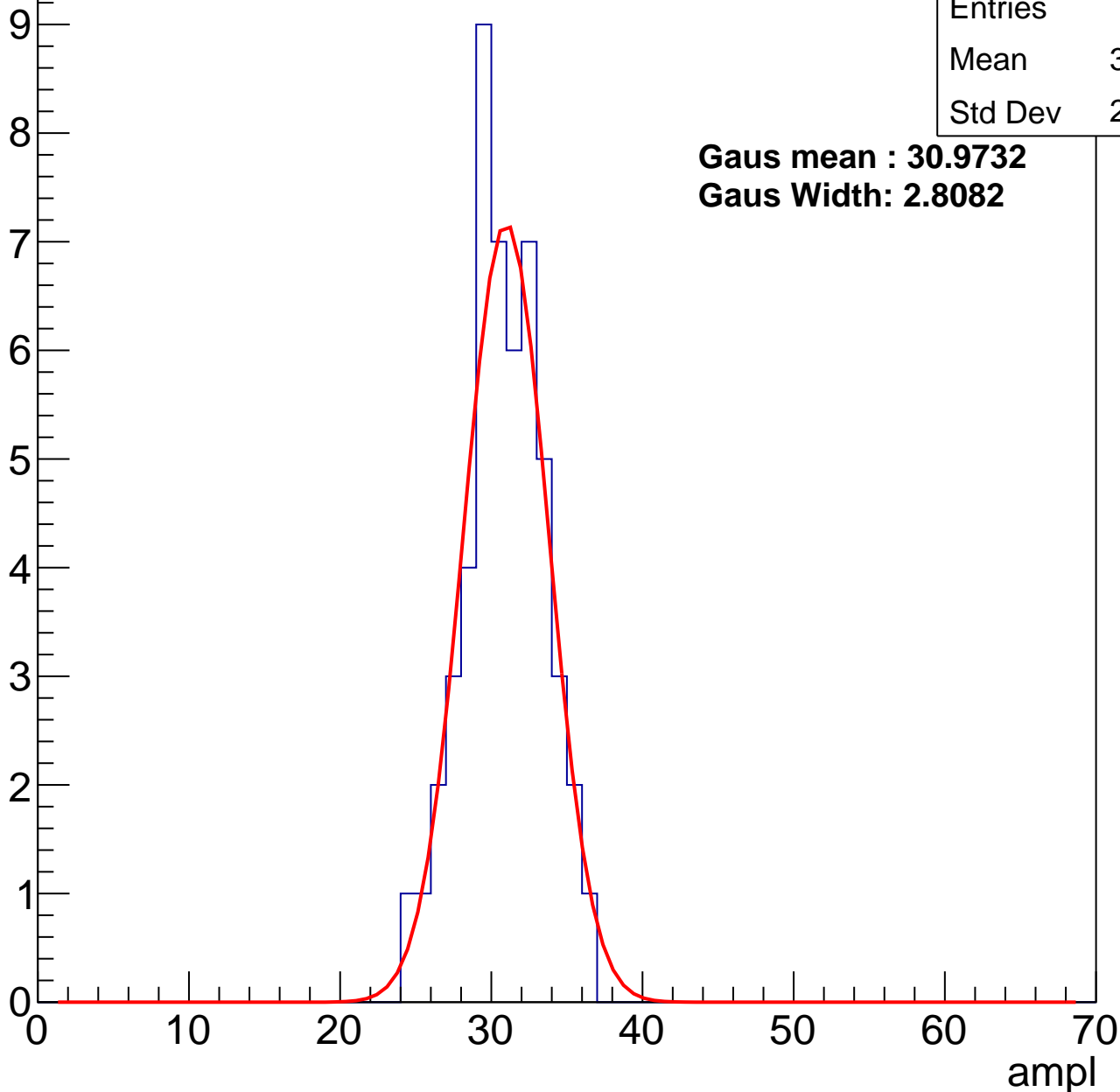
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	30.35
Std Dev	2.626

Gaus mean : 30.9732

Gaus Width: 2.8082



B0L002S, U5-ch71, adc1

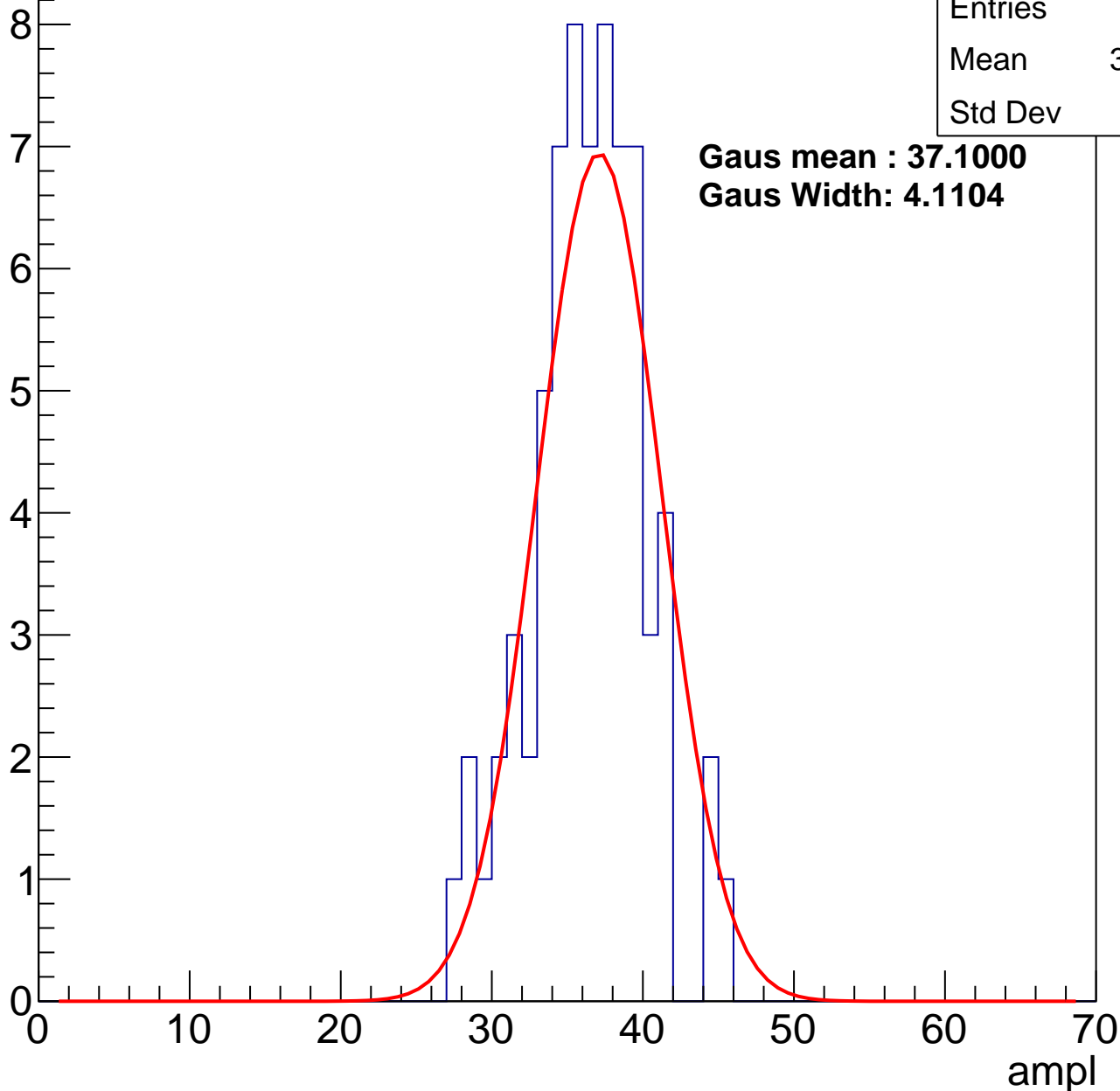
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	35.94
Std Dev	3.73

Gaus mean : 37.1000

Gaus Width: 4.1104



B0L002S, U5-ch71, adc2

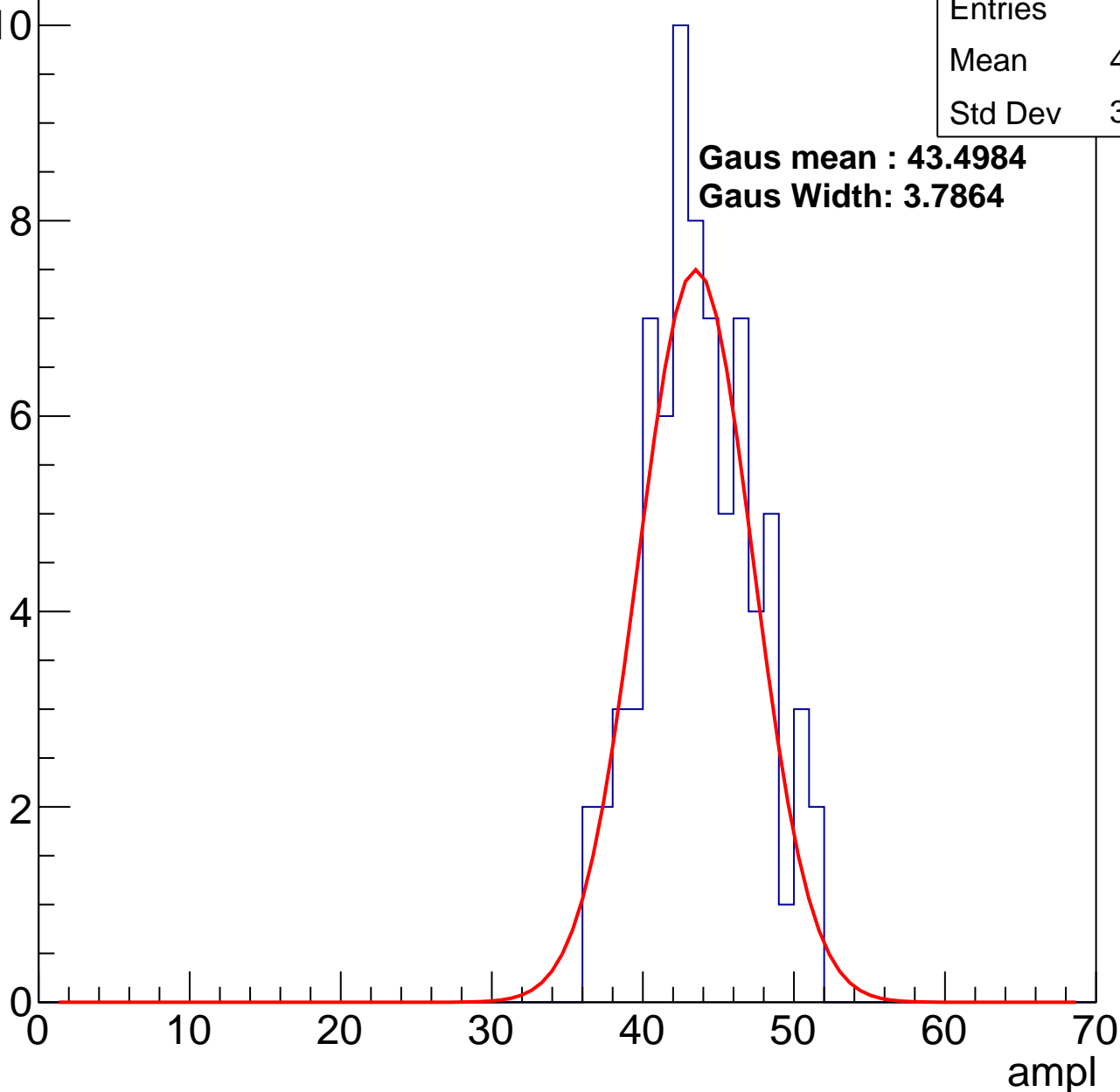
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	43.35
Std Dev	3.598

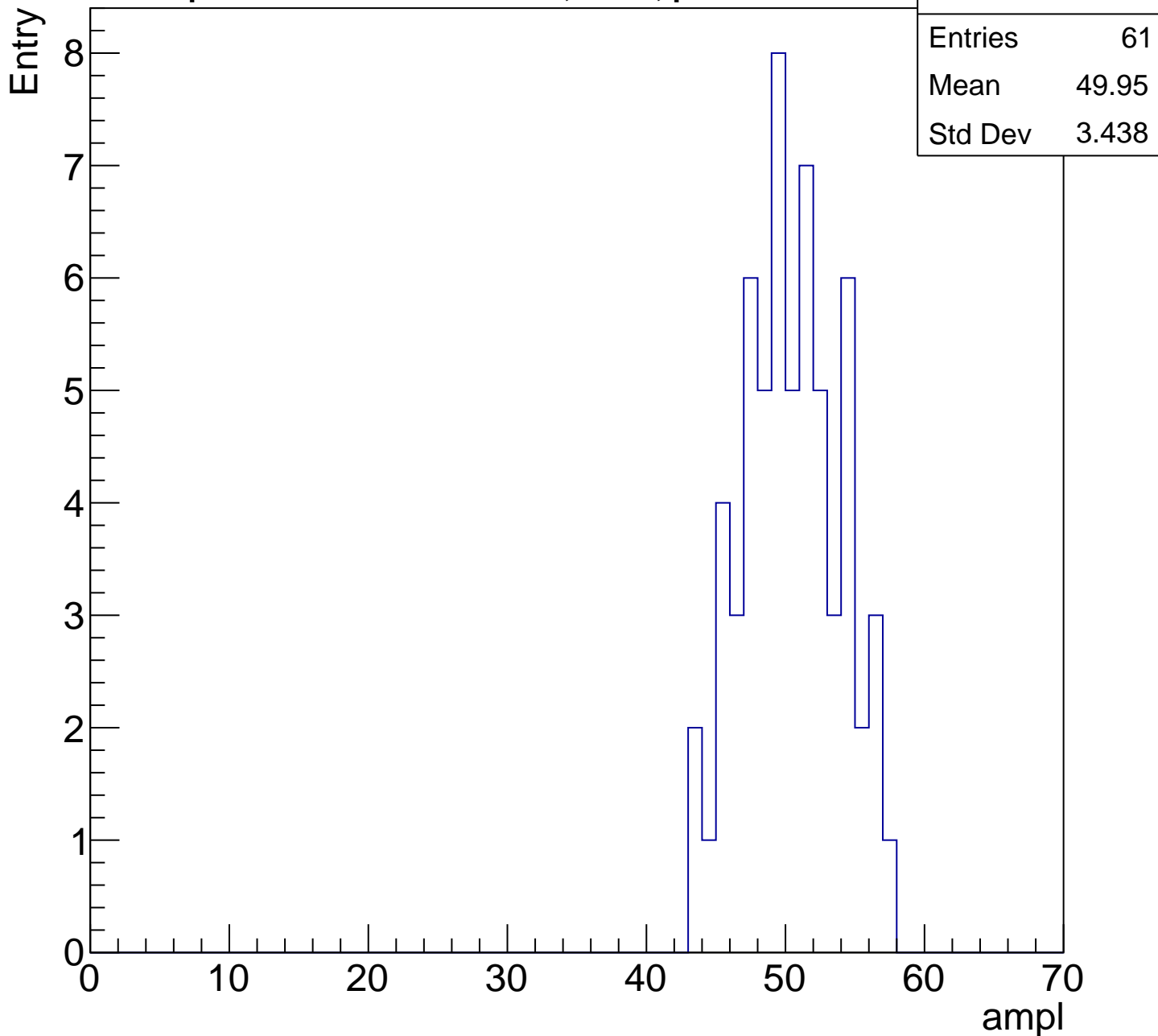
Gaus mean : 43.4984

Gaus Width: 3.7864



B0L002S, U5-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

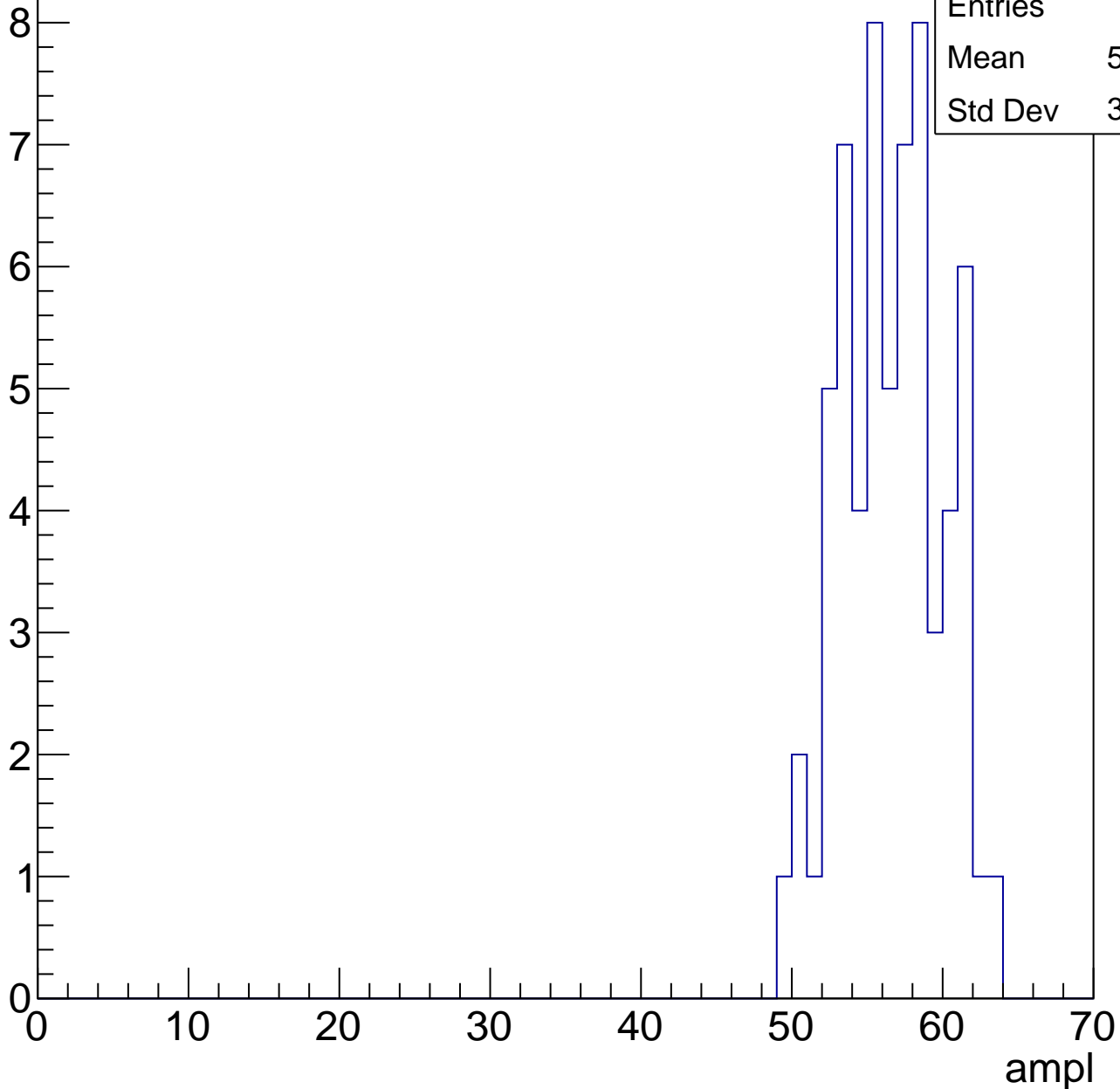


B0L002S, U5-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.16
Std Dev	3.277

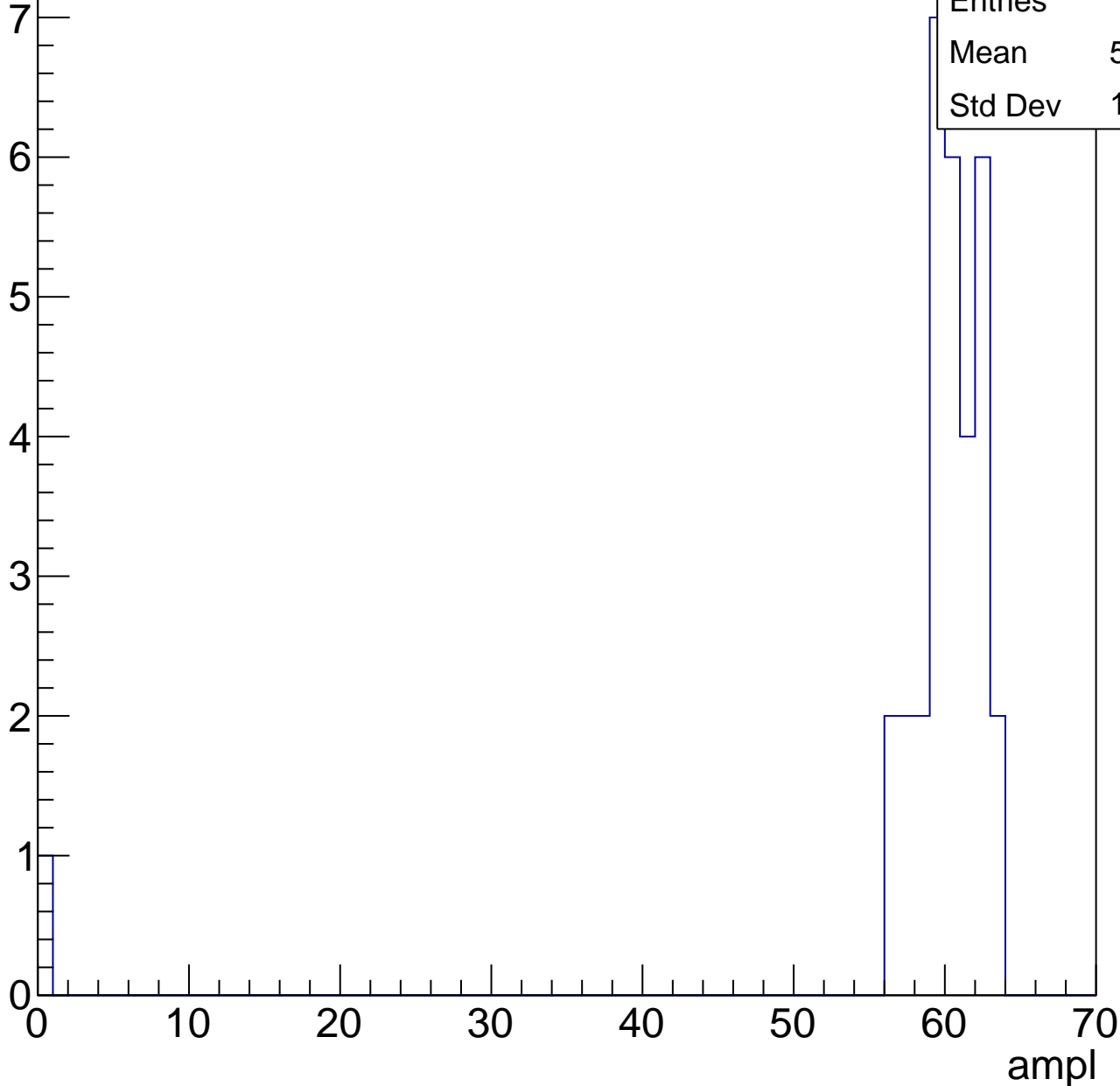


B0L002S, U5-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	58.03
Std Dev	10.59

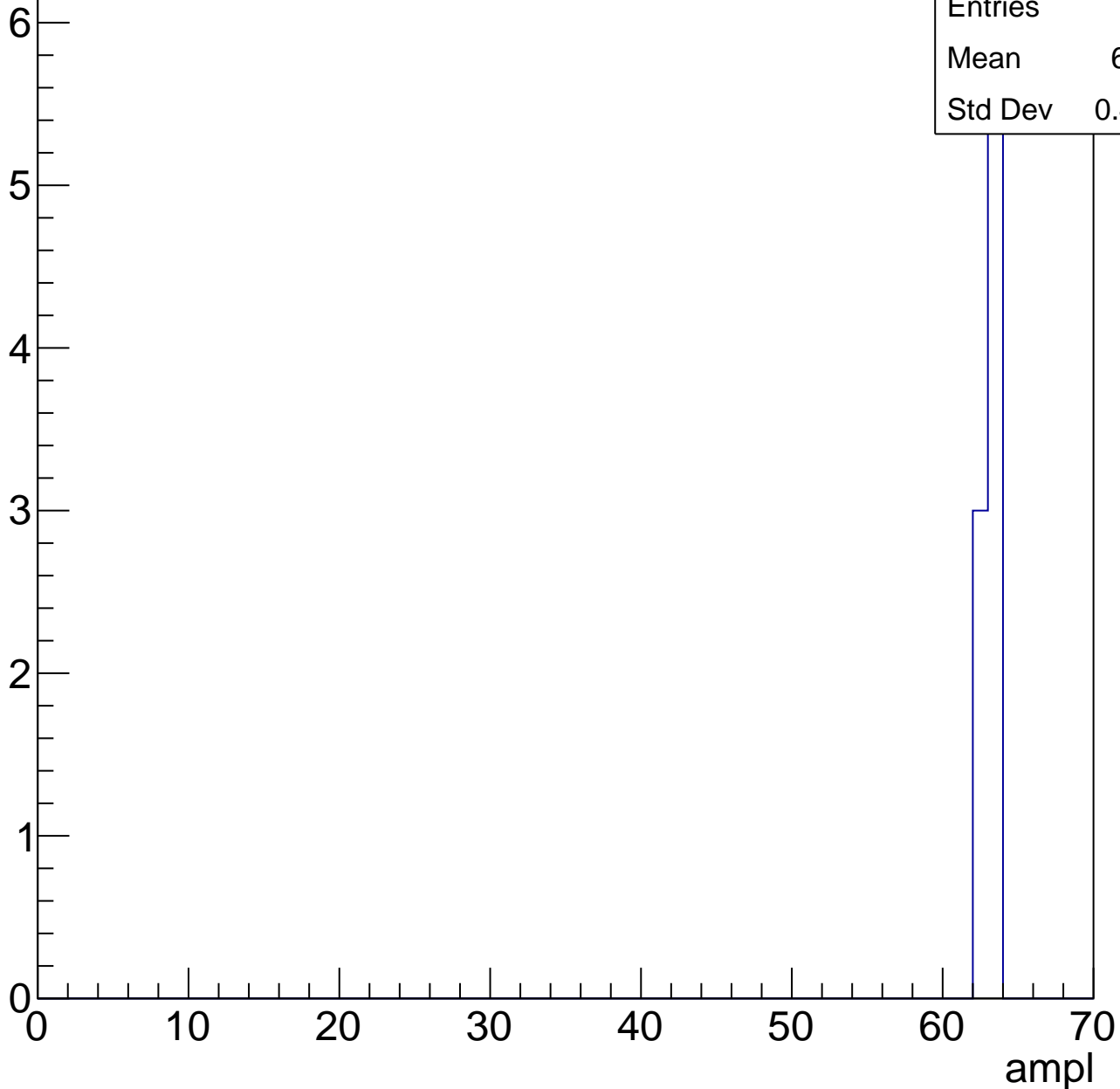


B0L002S, U5-ch71, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

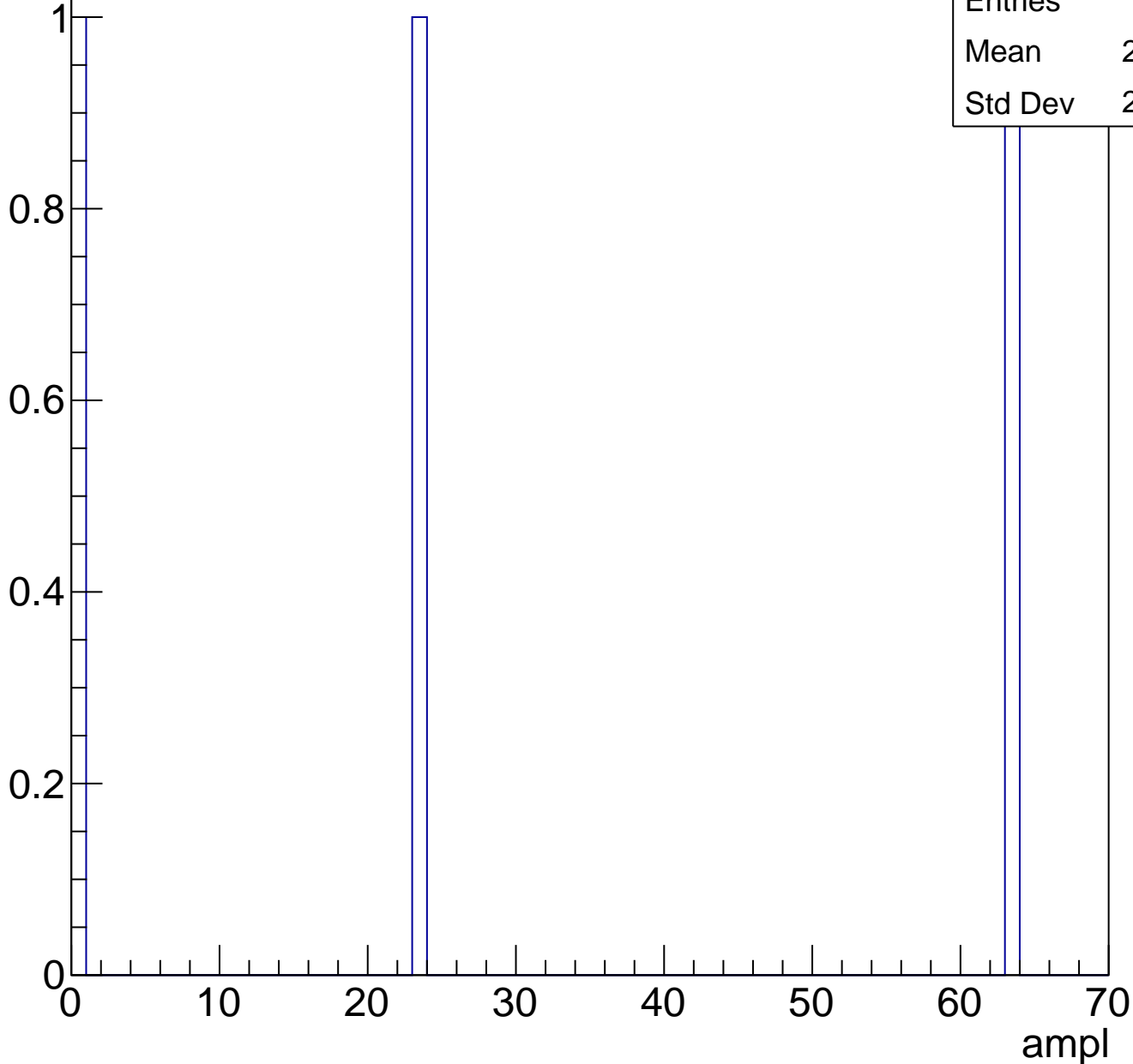
Entries	9
Mean	62.67
Std Dev	0.4714



B0L002S, U5-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	28.67
Std Dev	26.03

B0L002S, U5-ch72, adc0

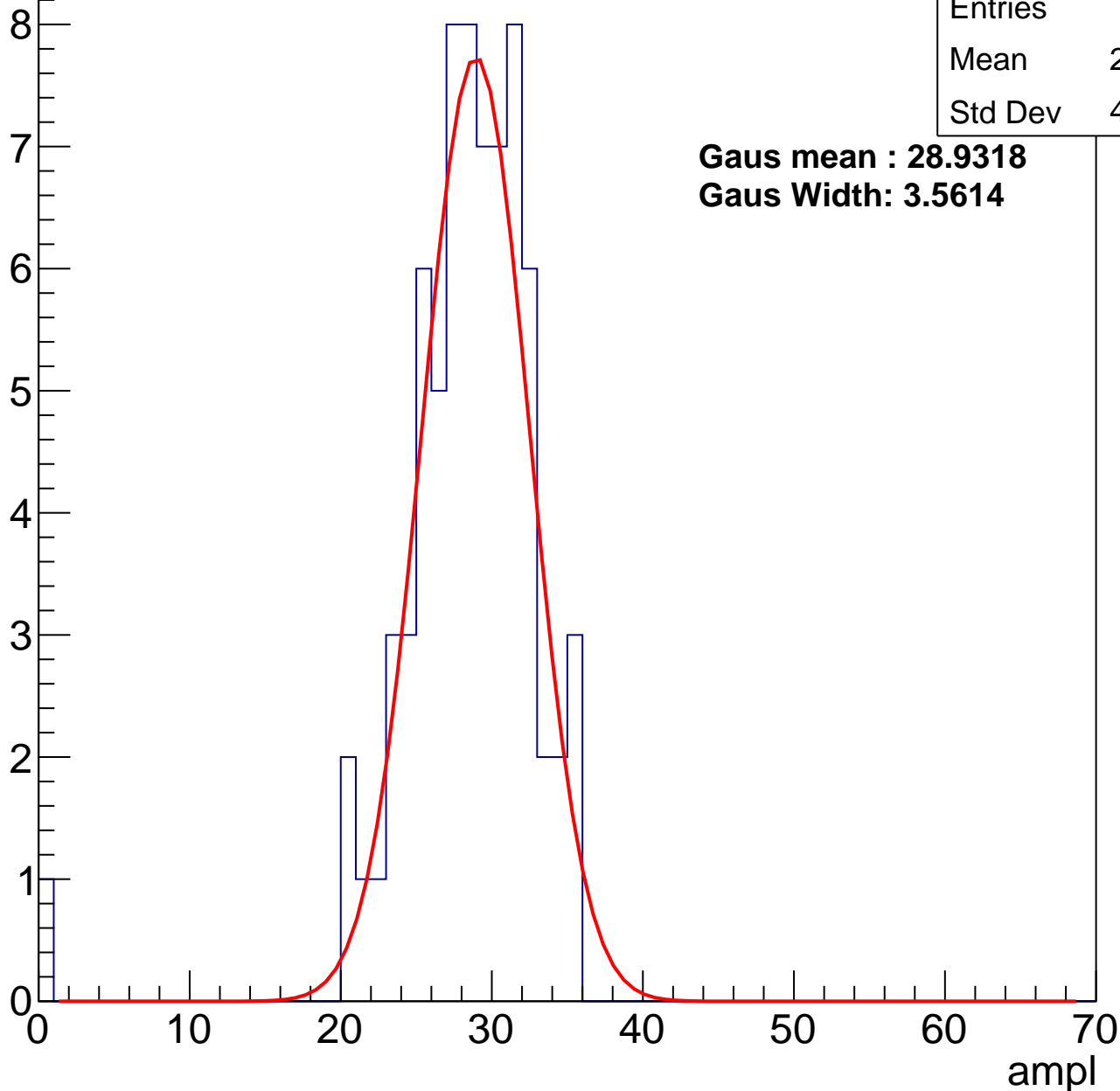
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	27.89
Std Dev	4.787

Gaus mean : 28.9318

Gaus Width: 3.5614



B0L002S, U5-ch72, adc1

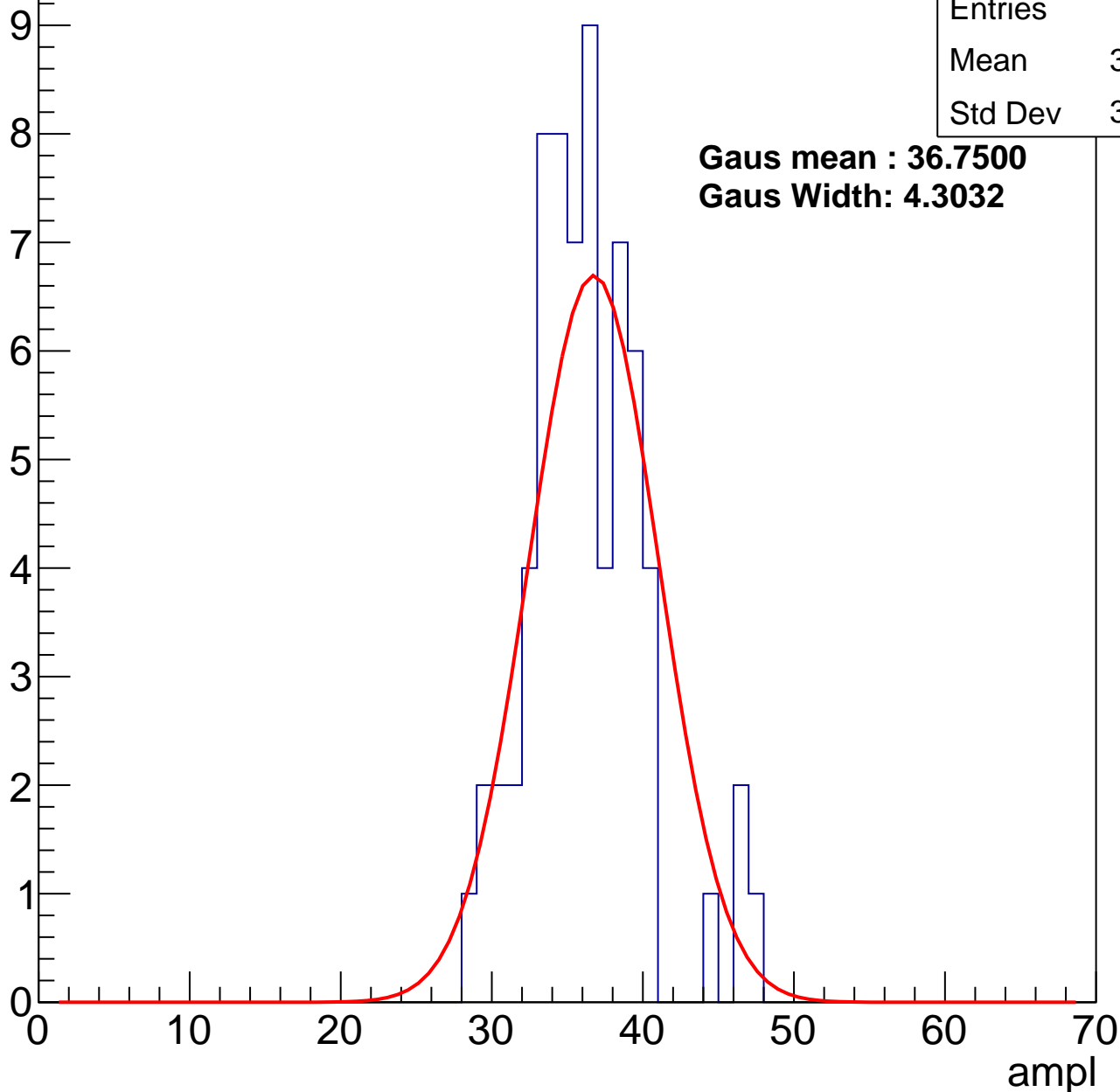
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.76
Std Dev	3.816

Gaus mean : 36.7500

Gaus Width: 4.3032



B0L002S, U5-ch72, adc2

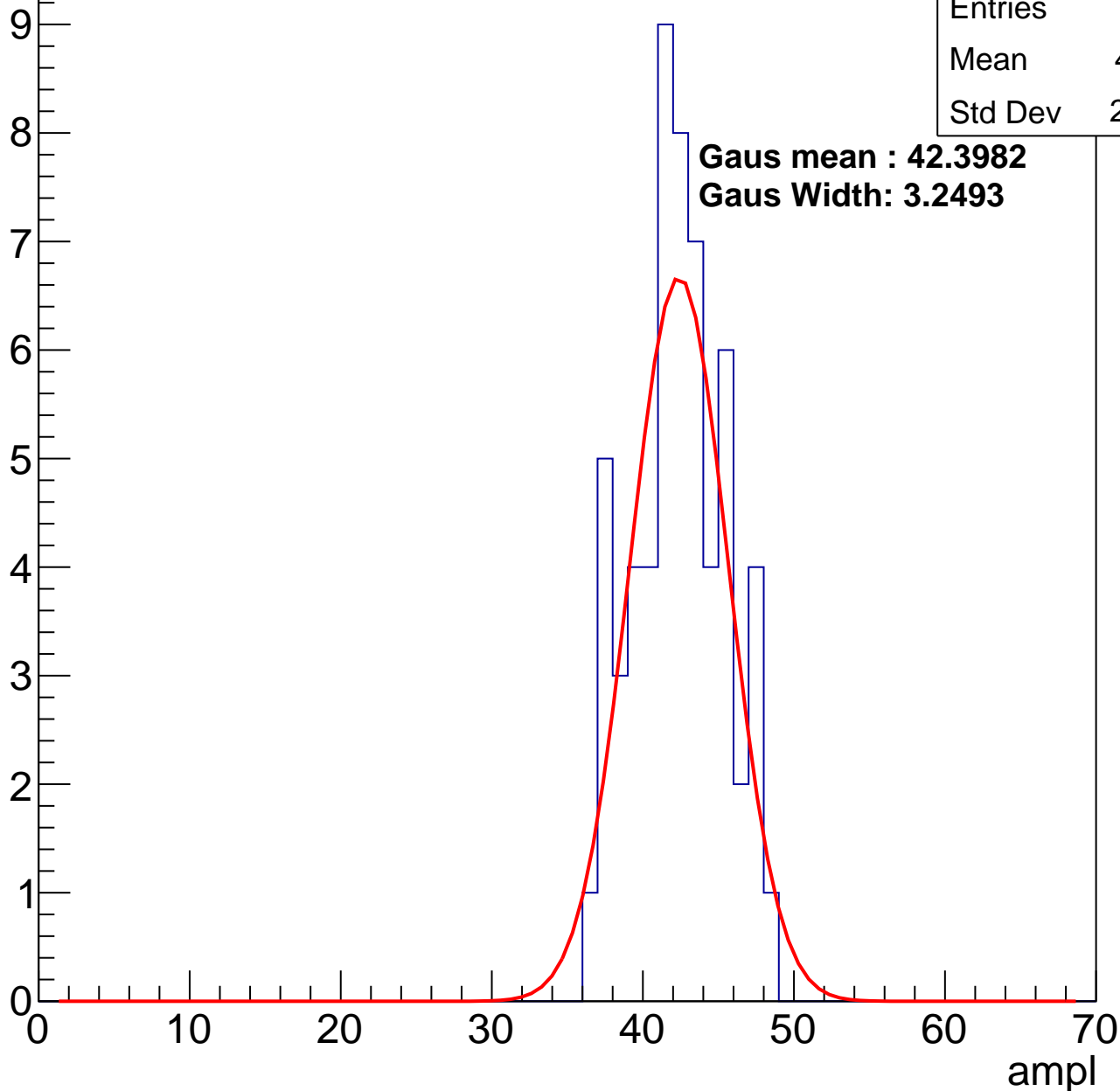
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	41.91
Std Dev	2.979

Gaus mean : 42.3982

Gaus Width: 3.2493

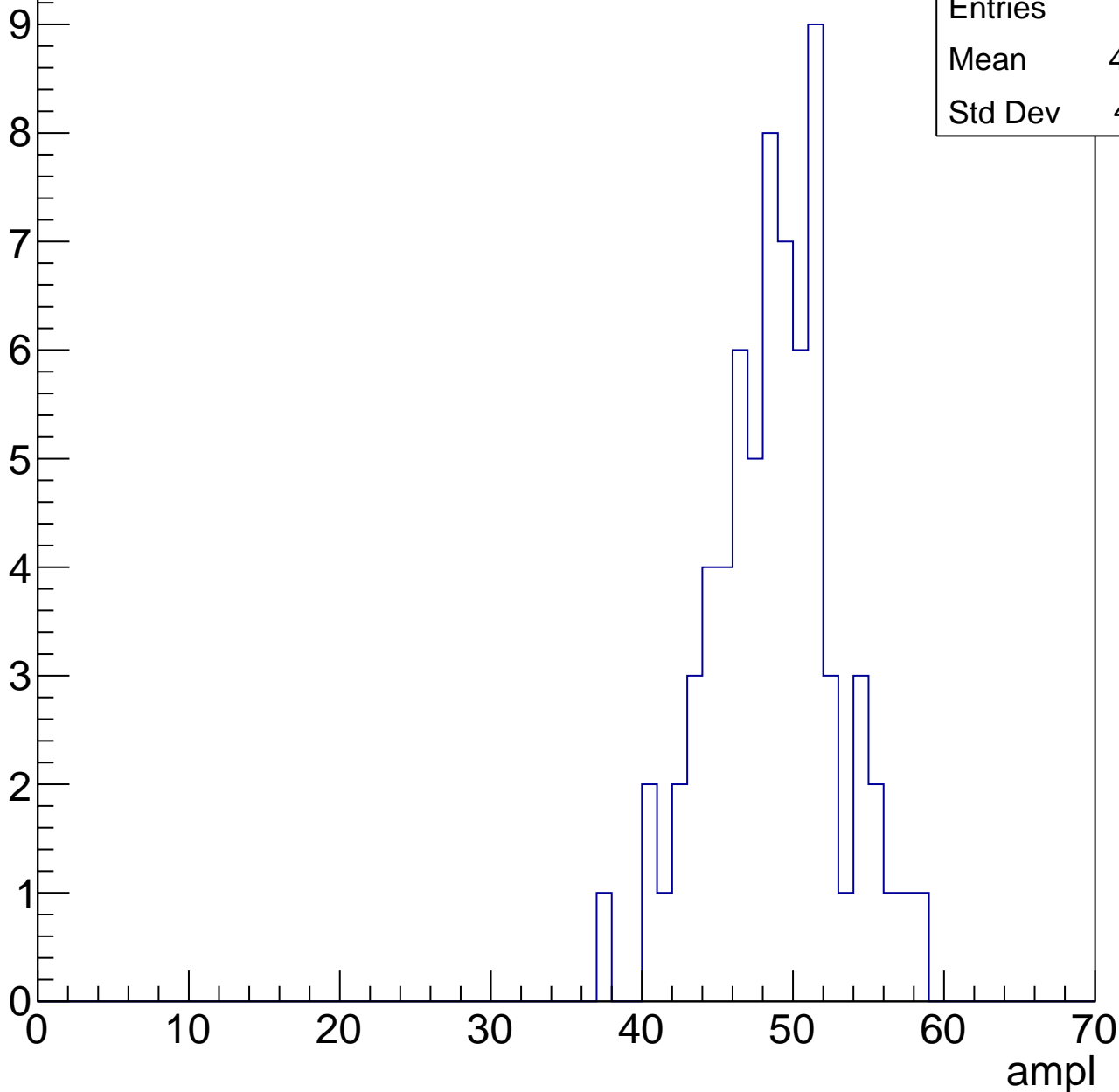


B0L002S, U5-ch72, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	48.23
Std Dev	4.151

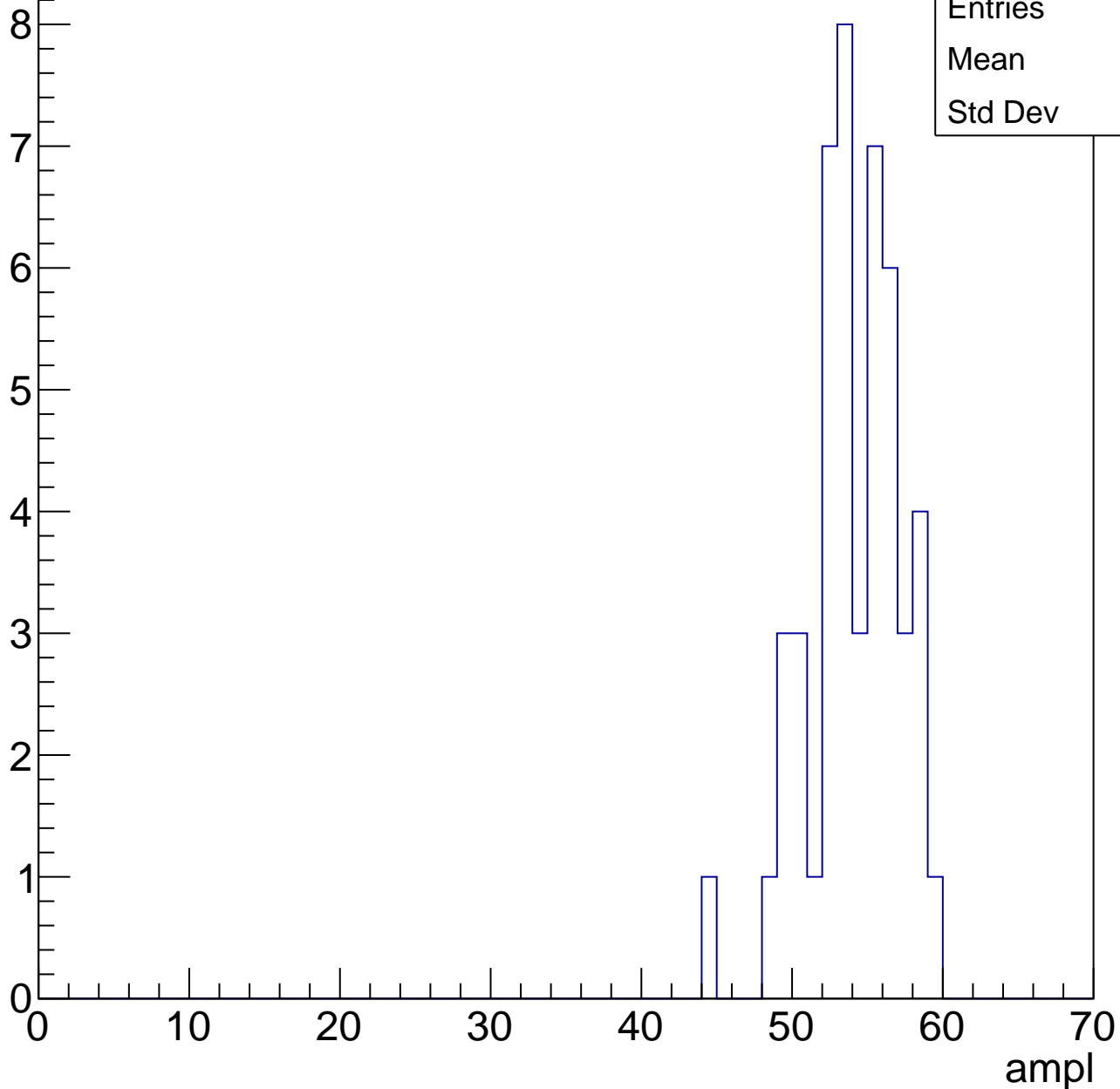


B0L002S, U5-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	53.6
Std Dev	3.04

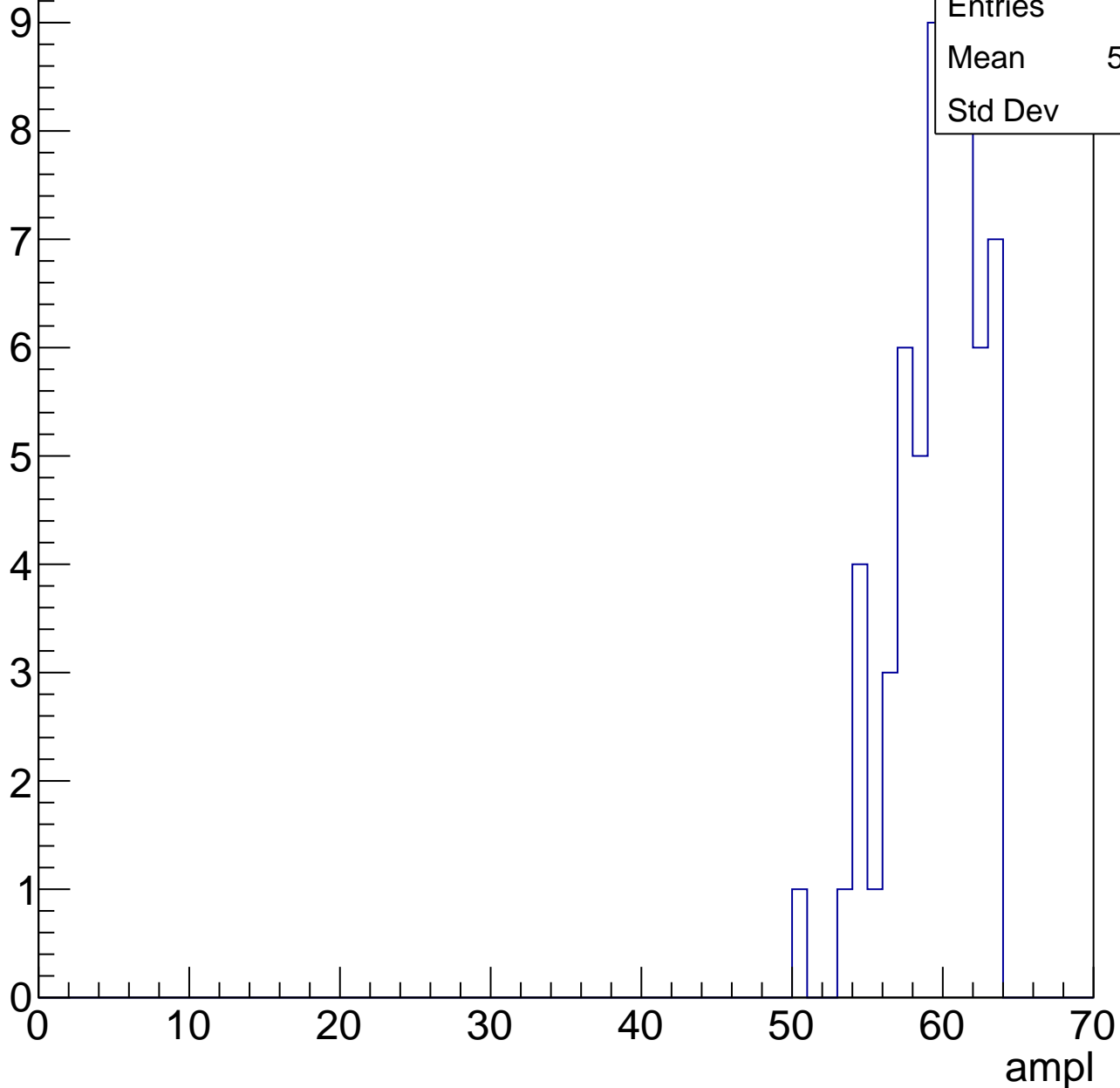


B0L002S, U5-ch72, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

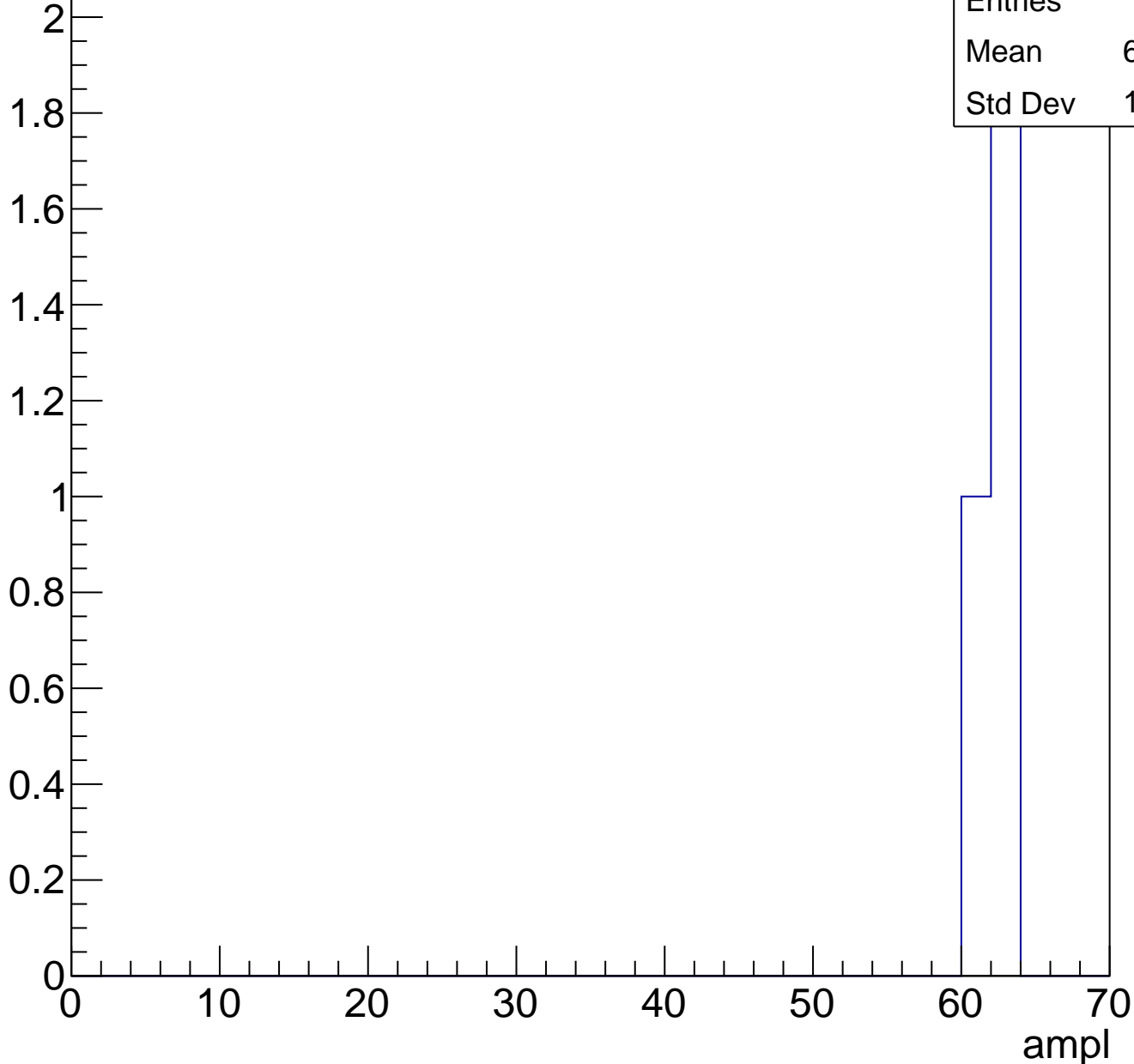
Entries	61
Mean	59.13
Std Dev	2.86



B0L002S, U5-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

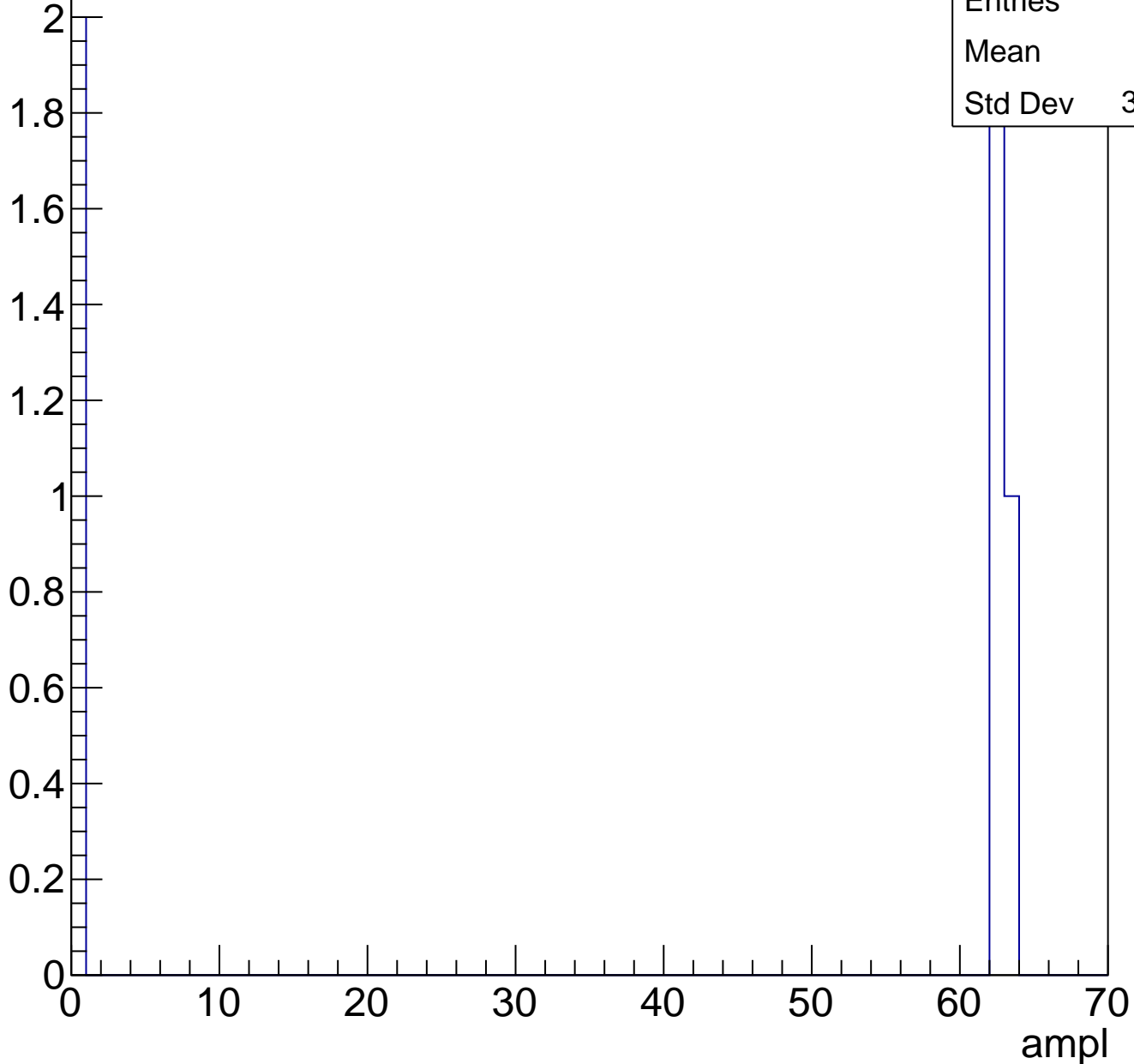
Entry



B0L002S, U5-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch73, adc0

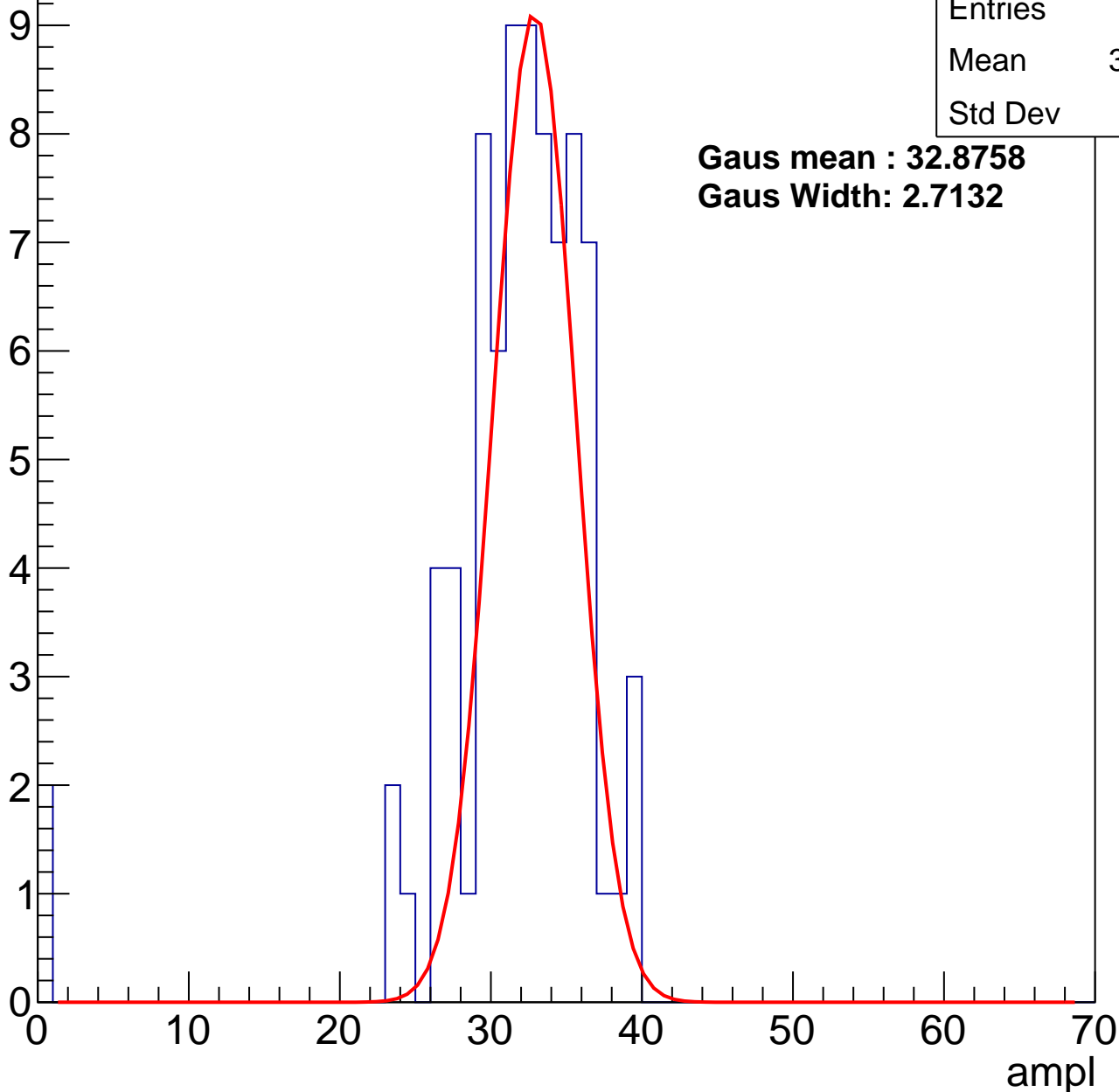
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	31.05
Std Dev	6.08

Gaus mean : 32.8758

Gaus Width: 2.7132



B0L002S, U5-ch73, adc1

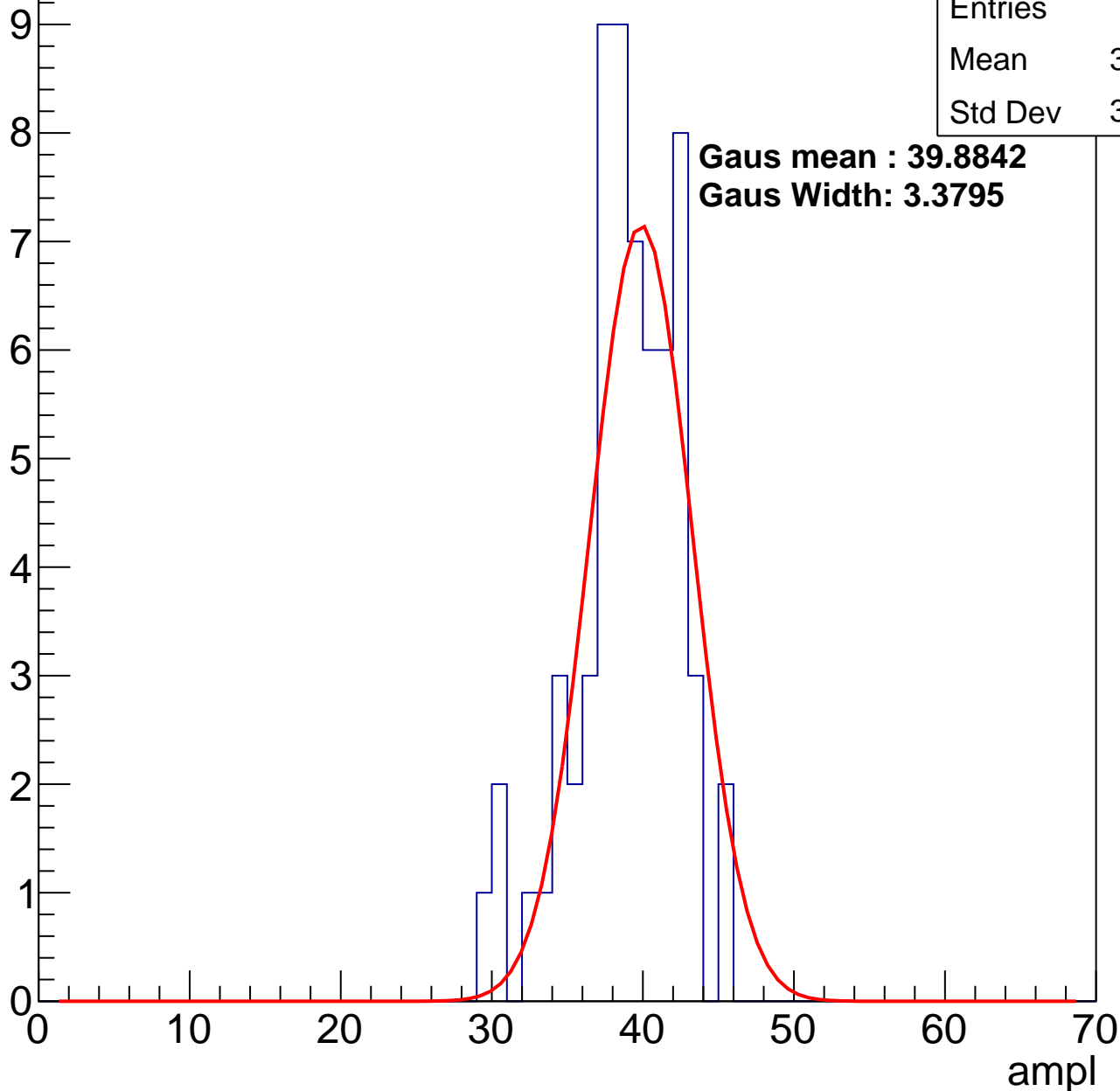
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.46
Std Dev	3.403

Gaus mean : 39.8842

Gaus Width: 3.3795



B0L002S, U5-ch73, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45.74
Std Dev	3.162

Gaus mean : 45.9027

Gaus Width: 3.9693

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

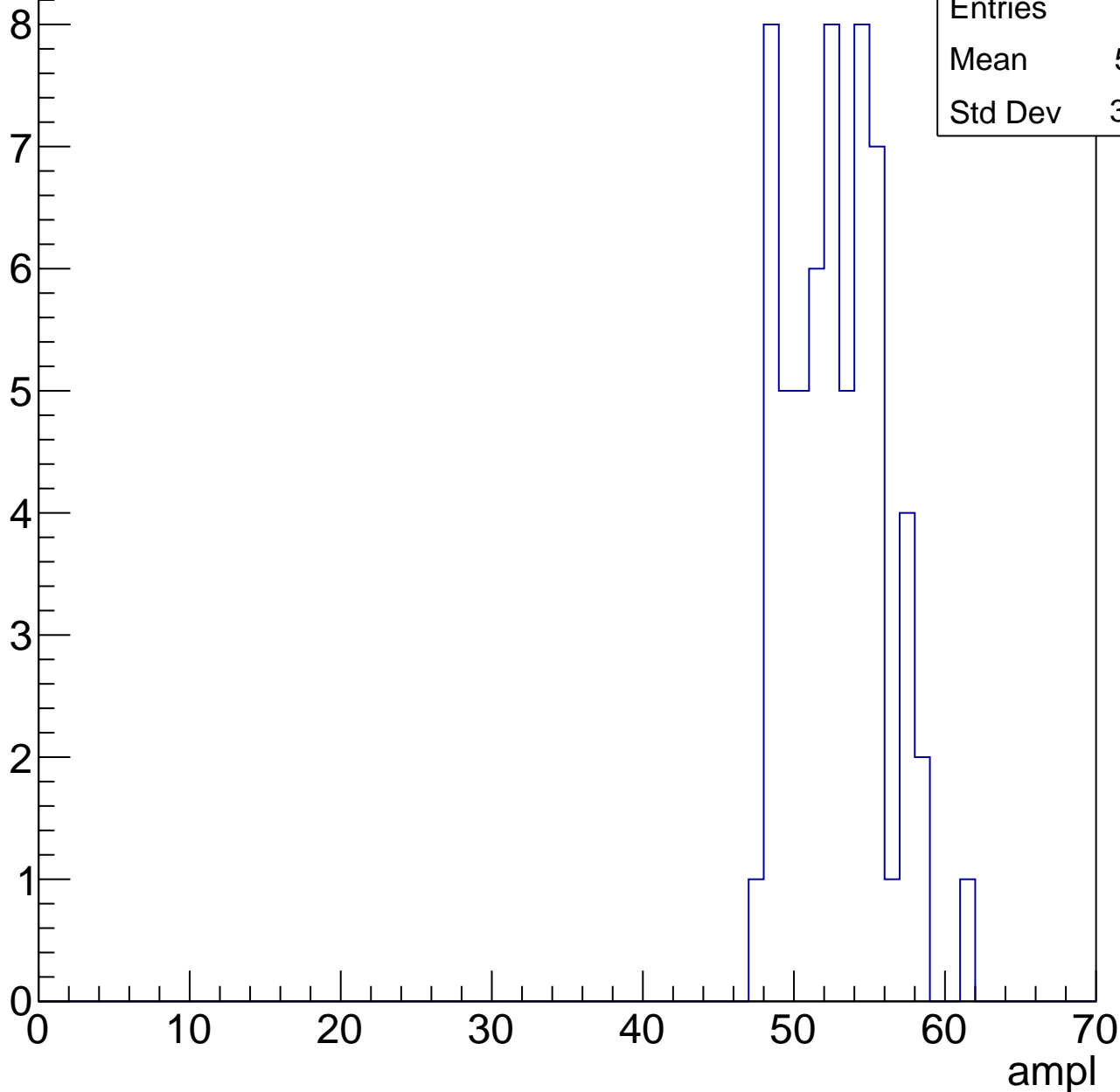
70

B0L002S, U5-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	52.31
Std Dev	3.102

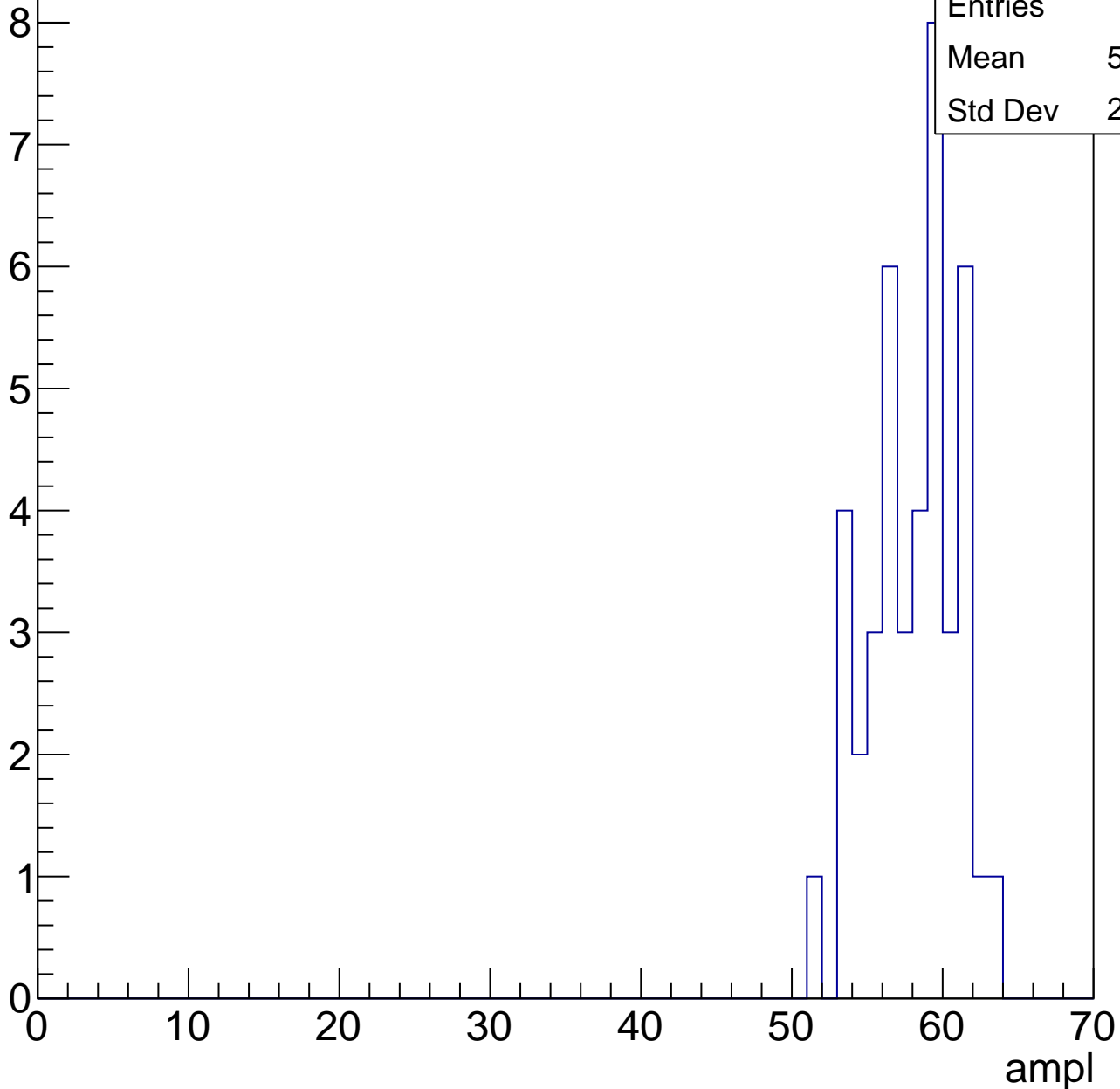


B0L002S, U5-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	57.57
Std Dev	2.855

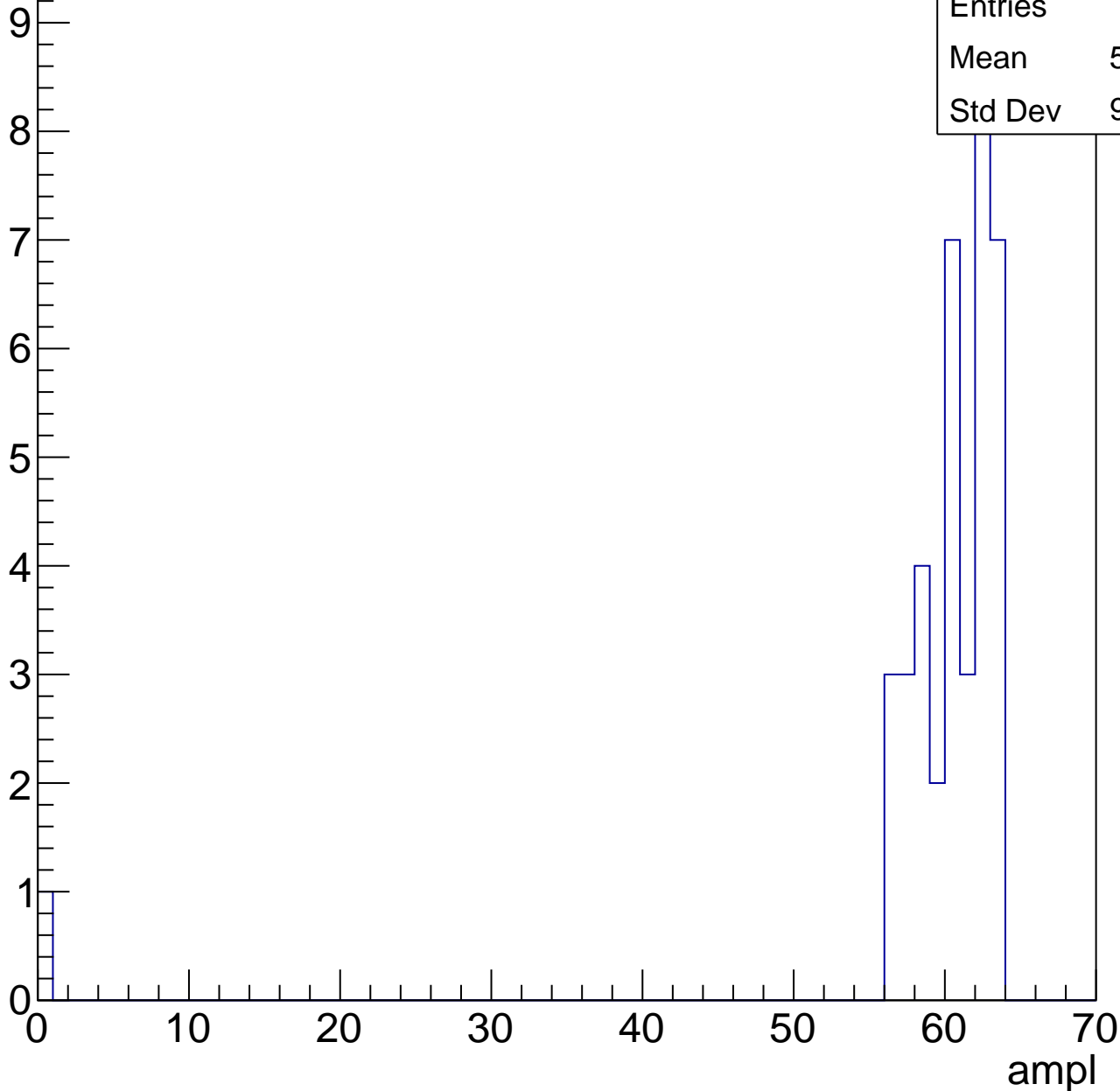


B0L002S, U5-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	58.74
Std Dev	9.784



B0L002S, U5-ch73, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch74, adc0

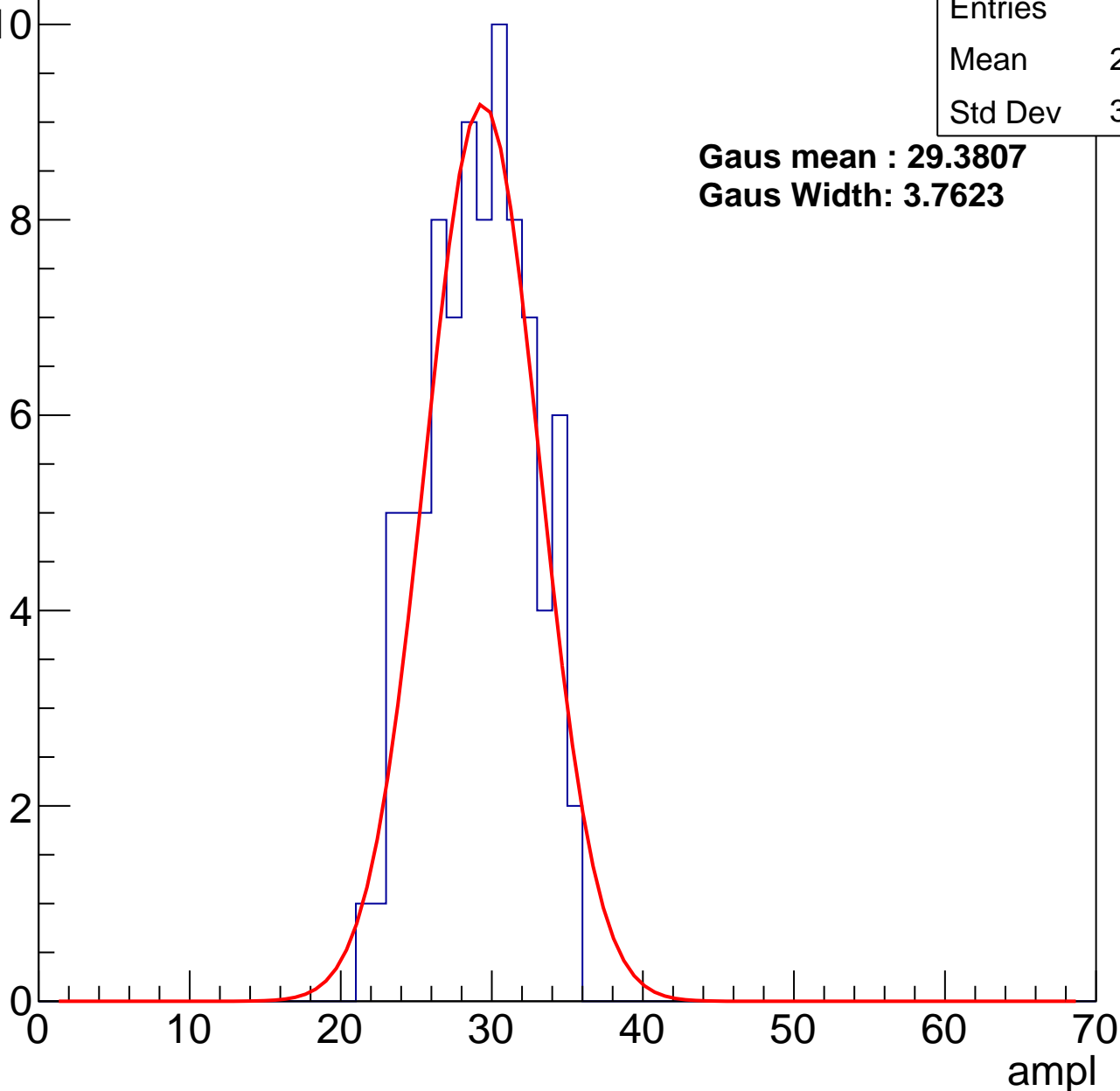
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	28.63
Std Dev	3.386

Gaus mean : 29.3807

Gaus Width: 3.7623



B0L002S, U5-ch74, adc1

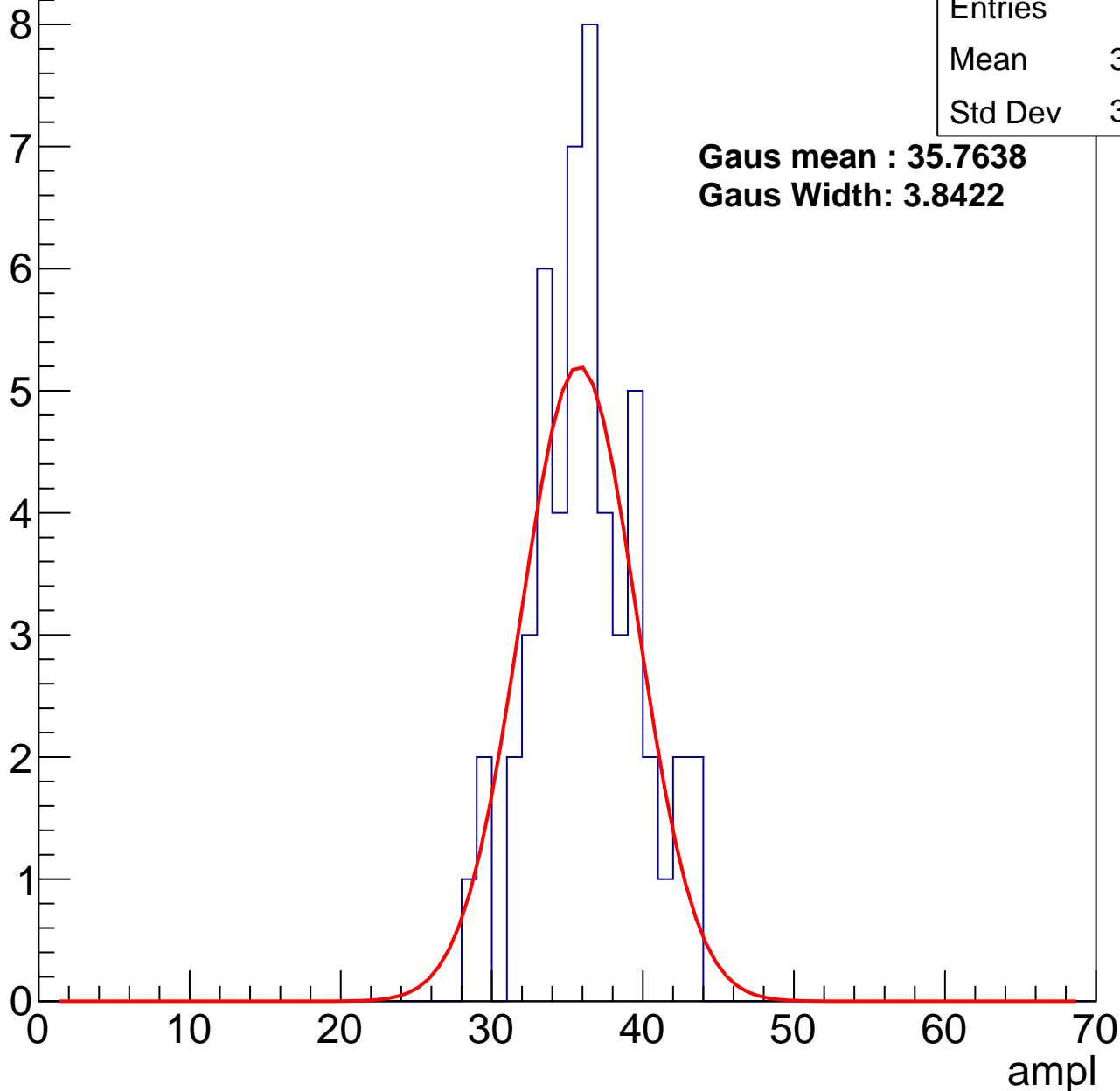
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	35.75
Std Dev	3.458

Gaus mean : 35.7638

Gaus Width: 3.8422

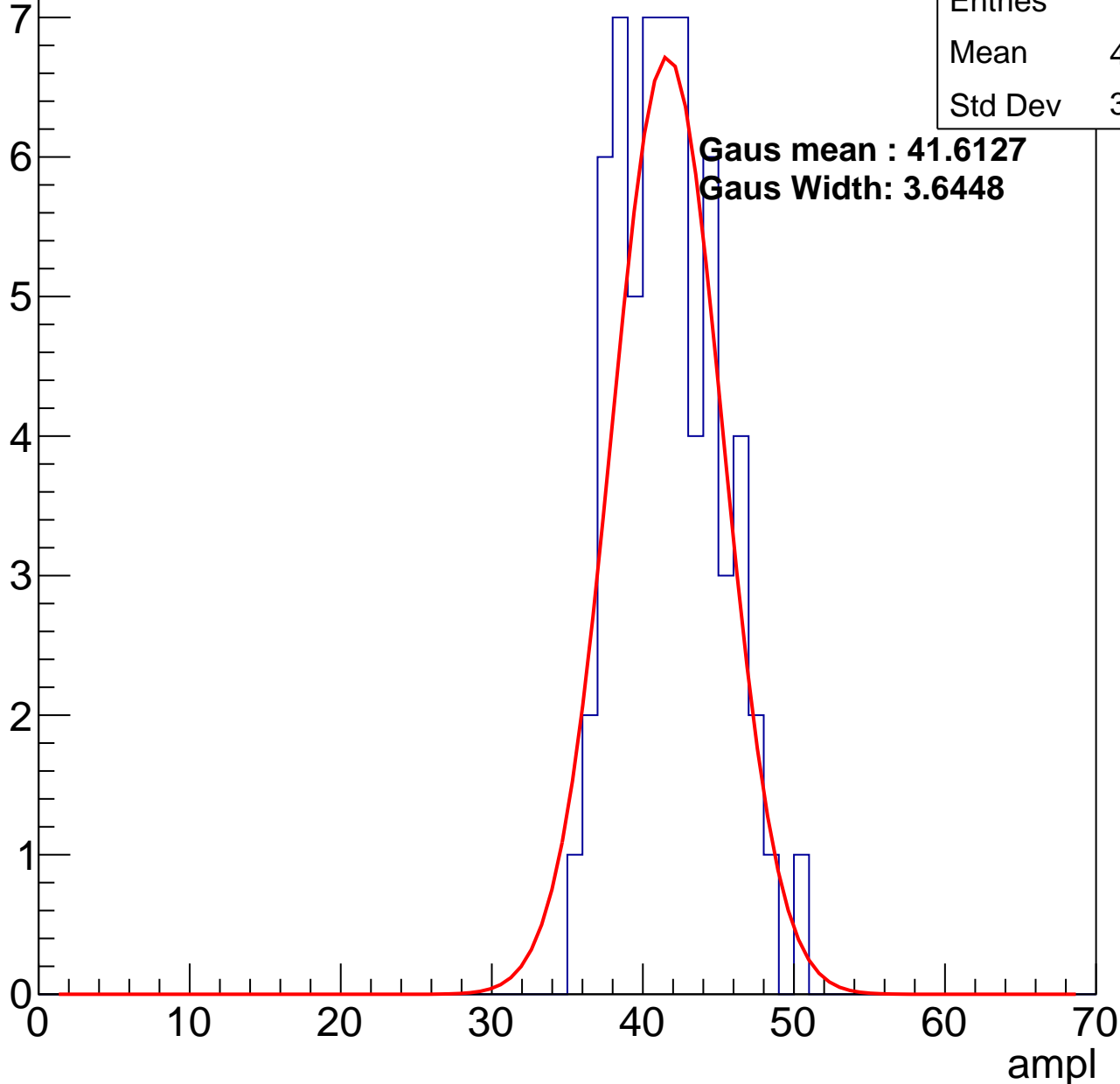


B0L002S, U5-ch74, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

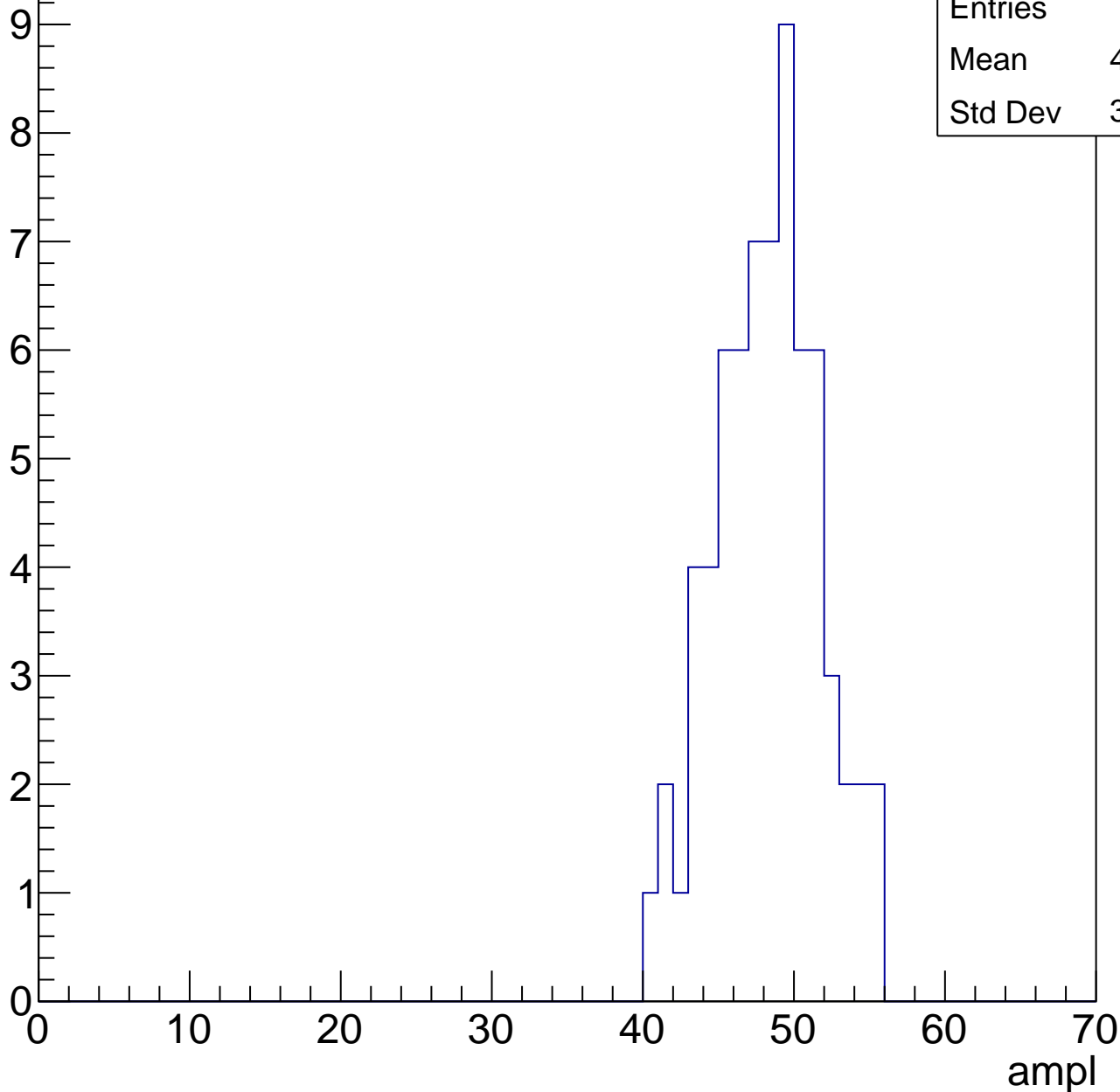
Entries	63
Mean	41.24
Std Dev	3.322



B0L002S, U5-ch74, adc3

calib_packv5_042523_0143.root, FC#8, port C1

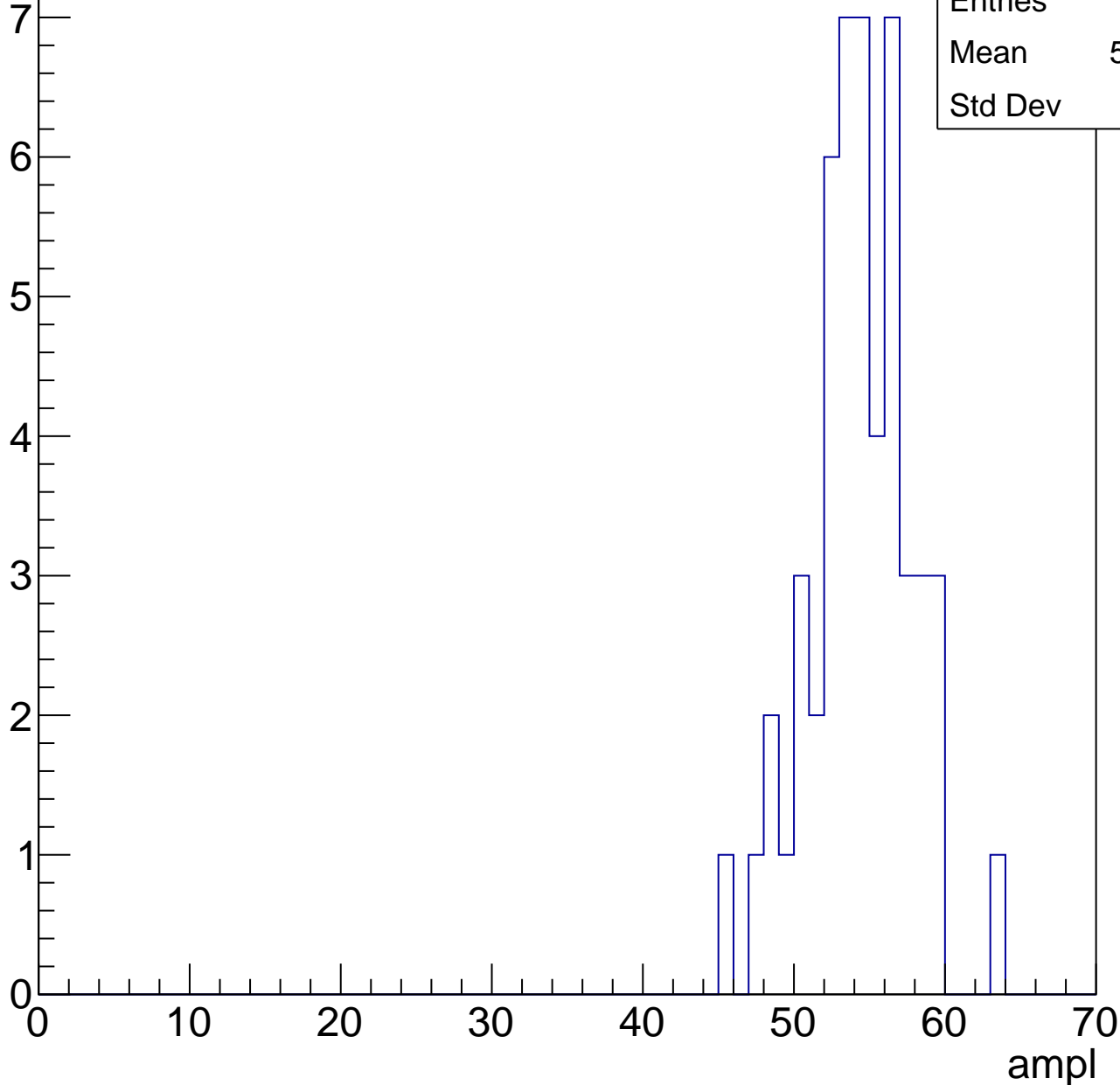
Entry



B0L002S, U5-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	54
Mean	58.57
Std Dev	3.071

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

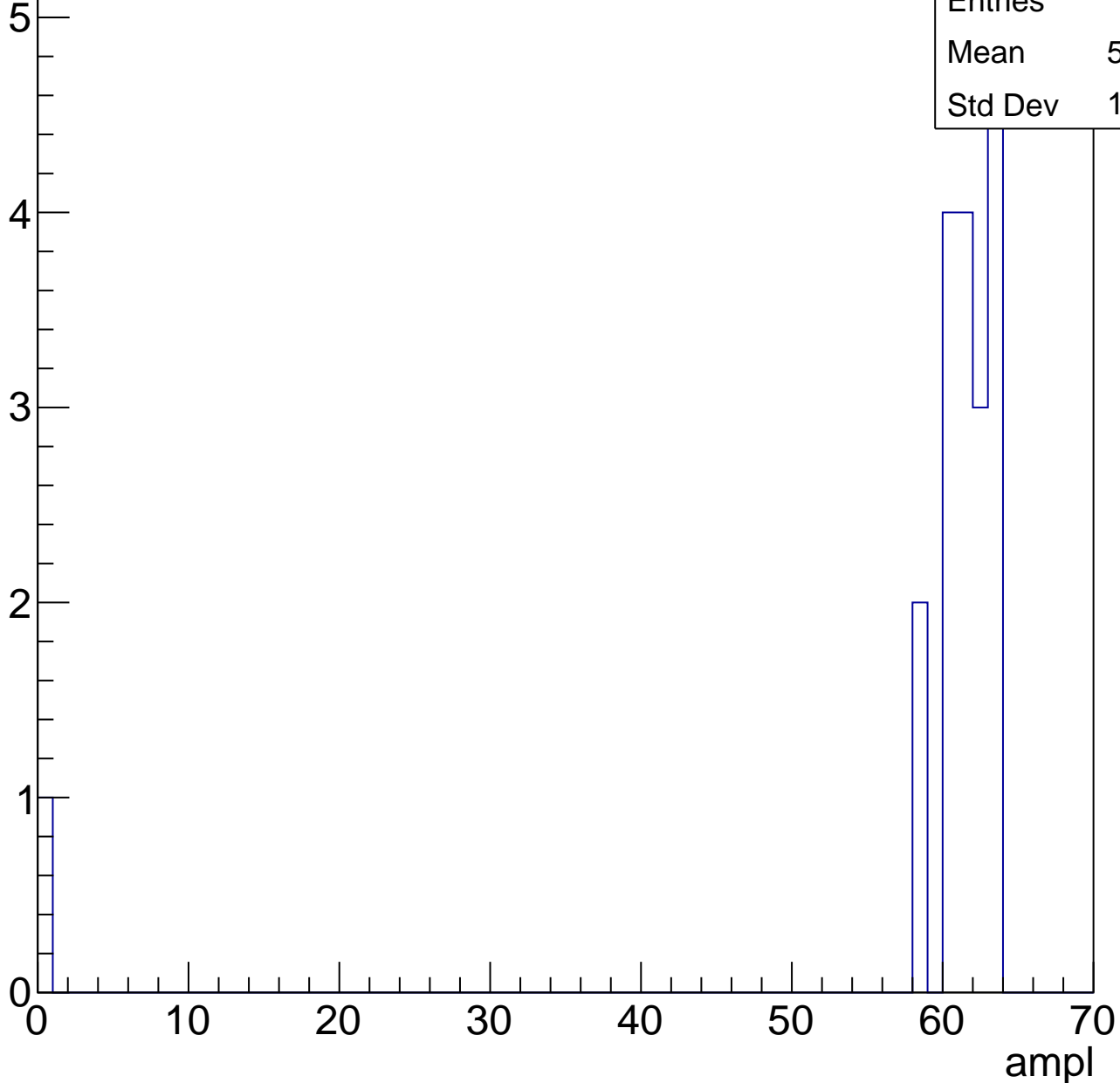
ampl

B0L002S, U5-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	57.95
Std Dev	13.74



B0L002S, U5-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch75, adc0

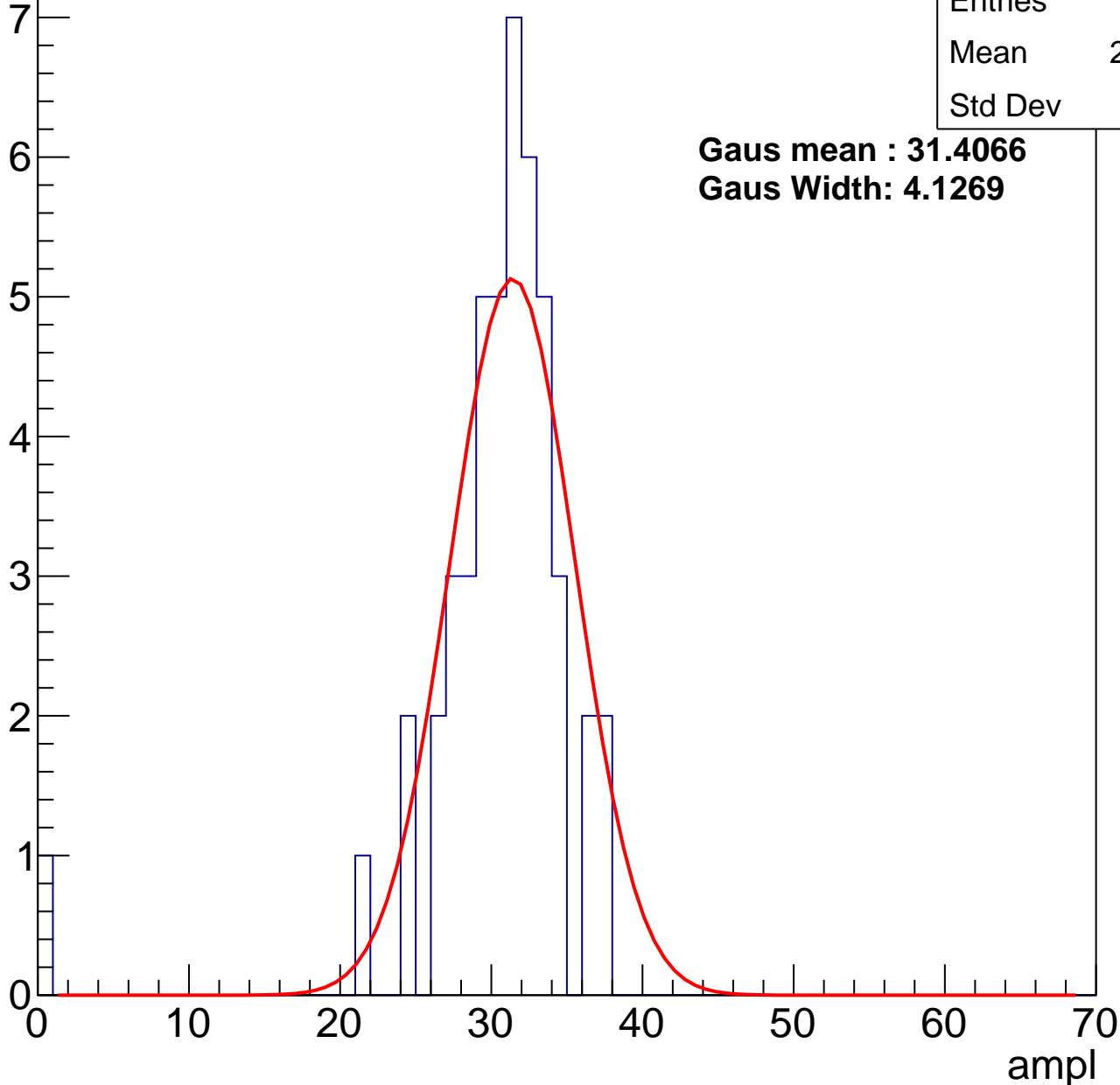
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	29.85
Std Dev	5.5

Gaus mean : 31.4066

Gaus Width: 4.1269



B0L002S, U5-ch75, adc1

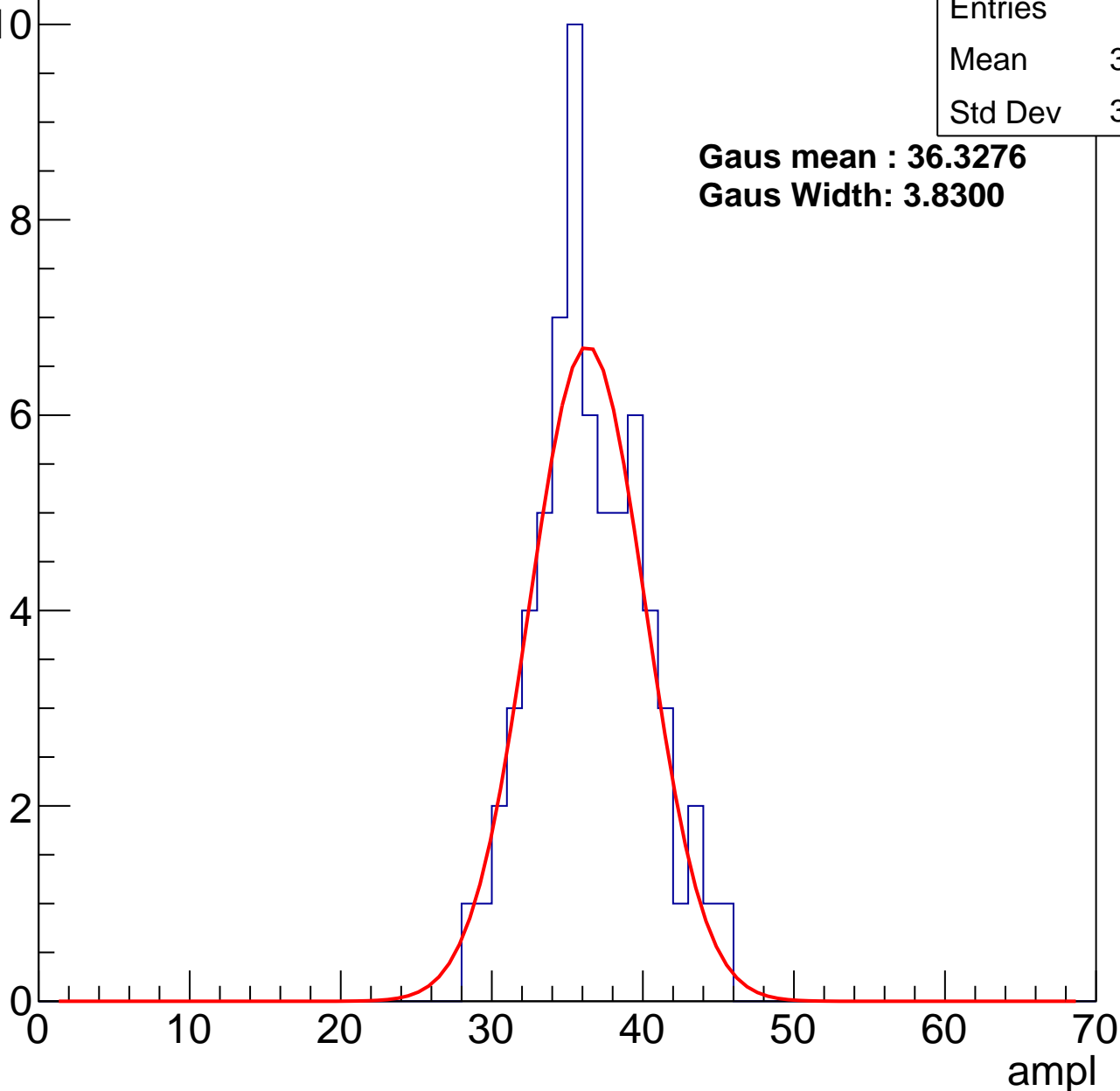
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.06
Std Dev	3.673

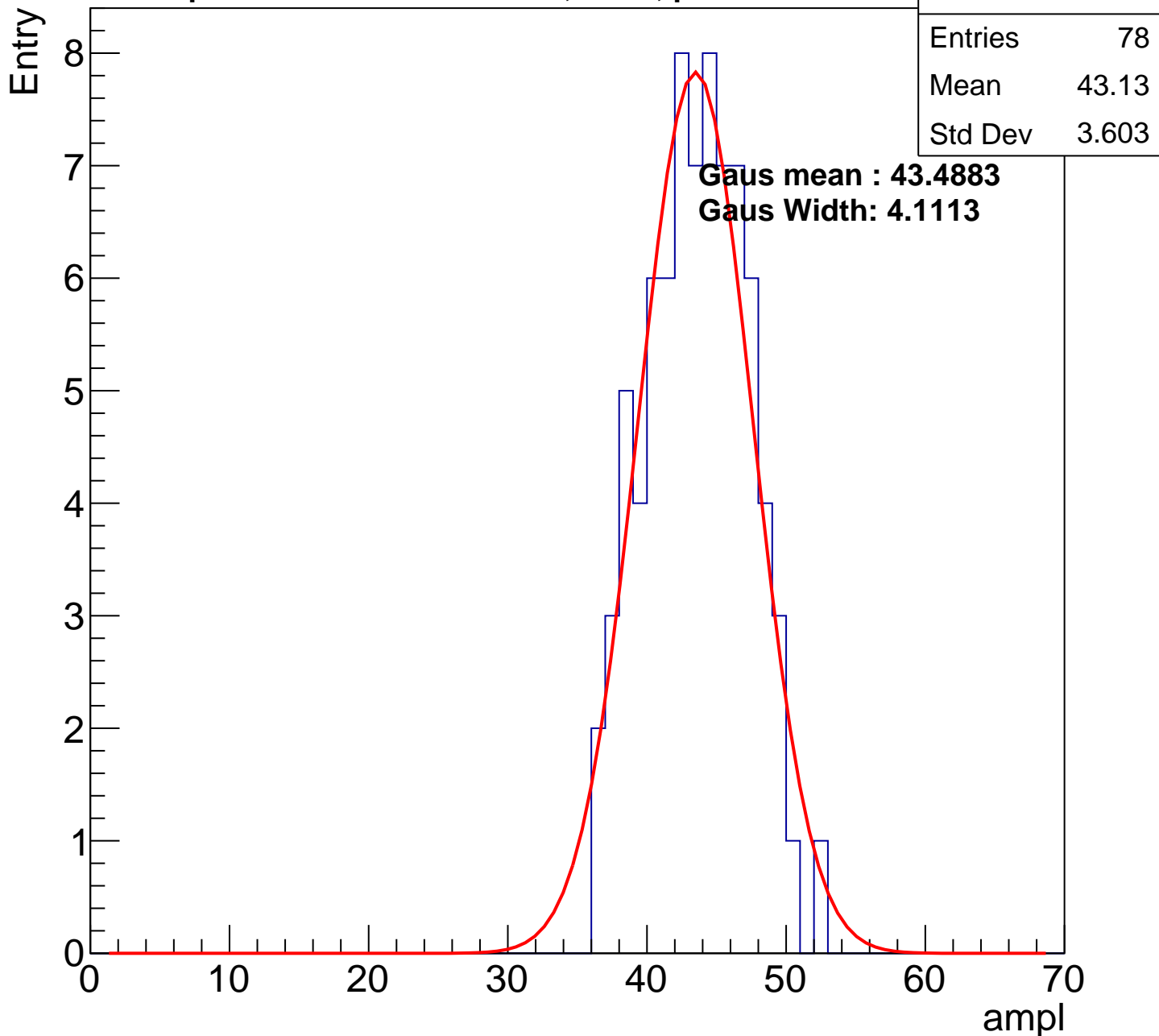
Gaus mean : 36.3276

Gaus Width: 3.8300



B0L002S, U5-ch75, adc2

calib_packv5_042523_0143.root, FC#8, port C1

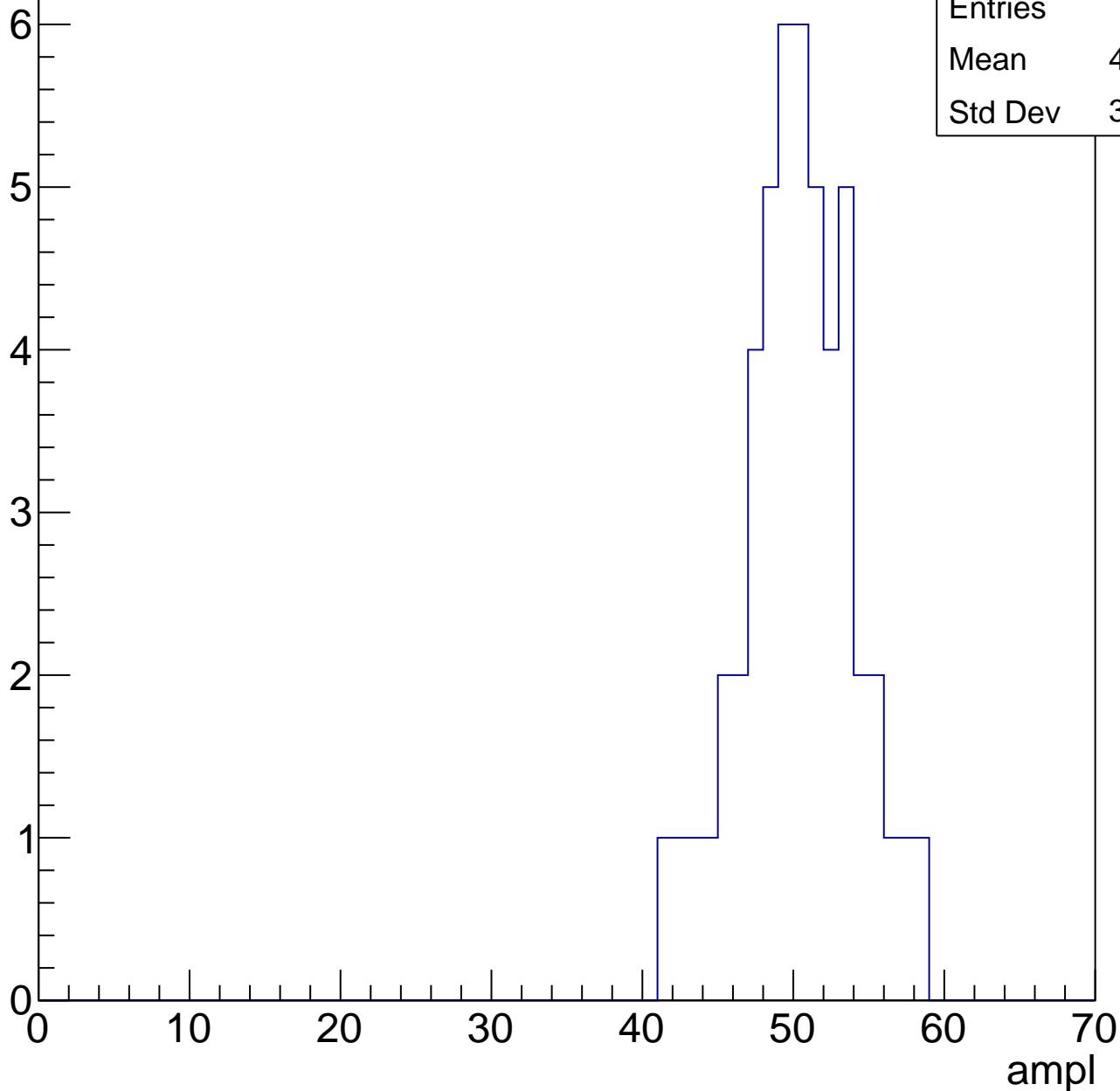


B0L002S, U5-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	49.82
Std Dev	3.659

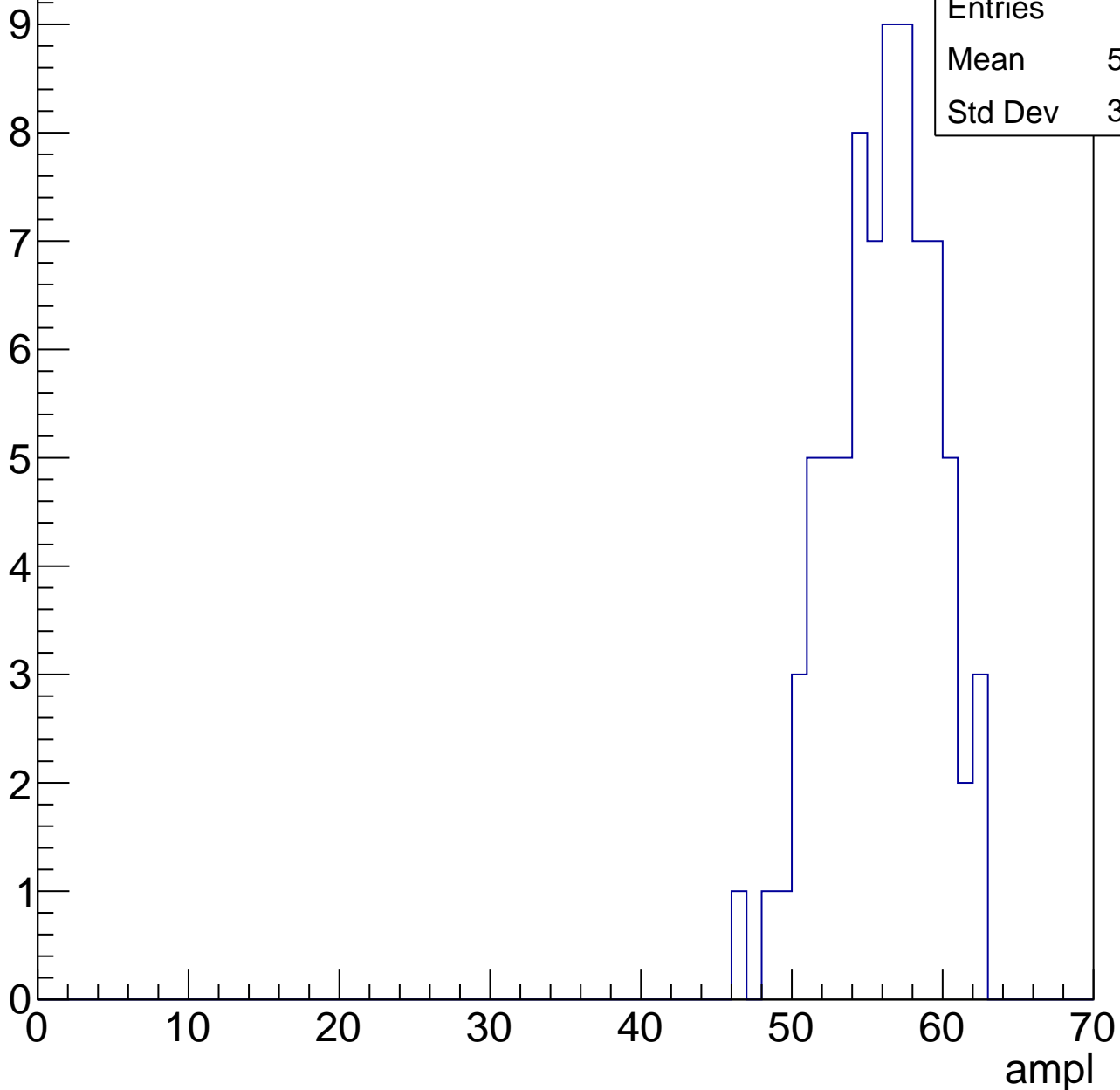


B0L002S, U5-ch75, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	55.56
Std Dev	3.459



B0L002S, U5-ch75, adc5

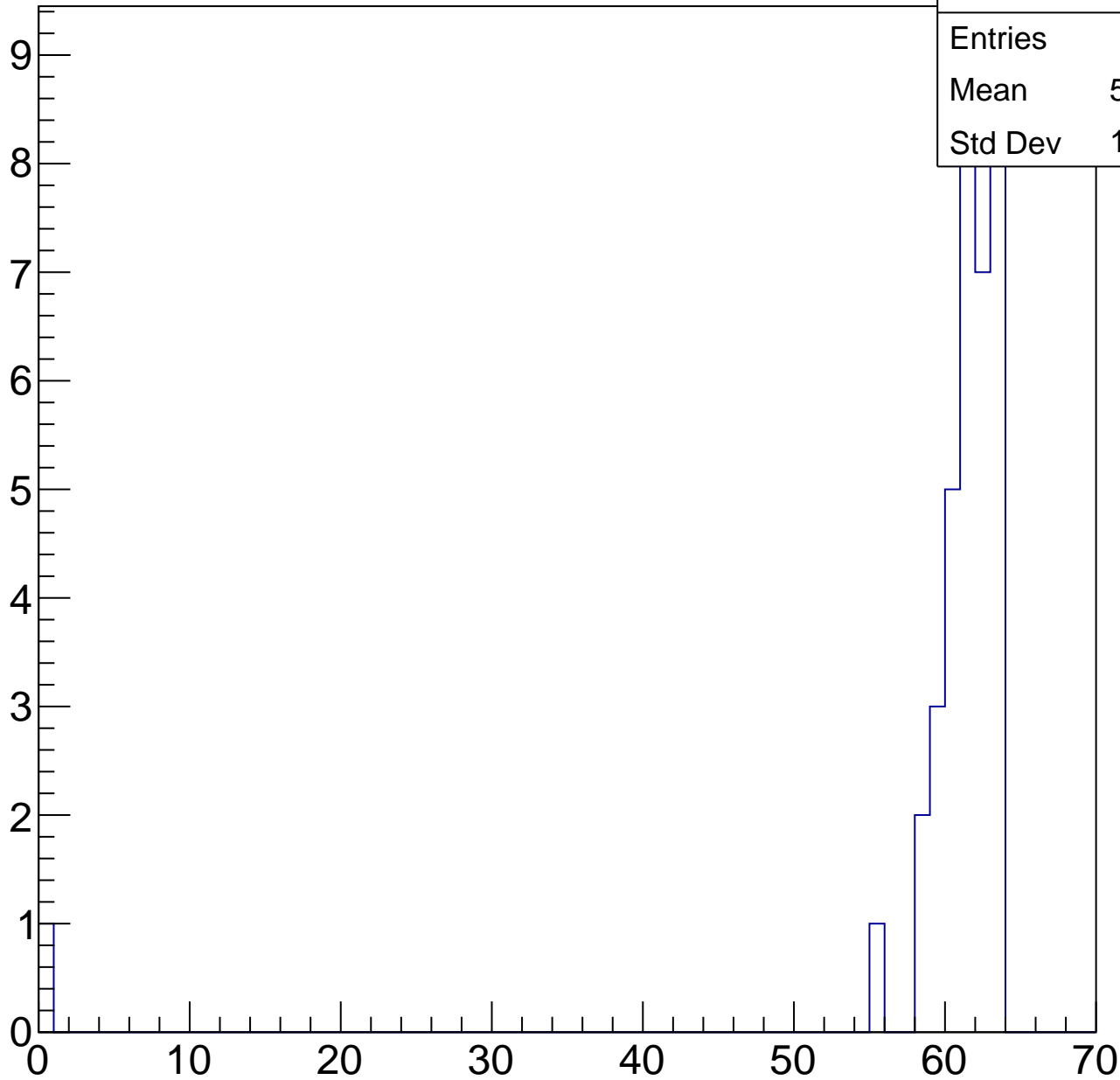
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	36
Mean	59.36
Std Dev	10.19

ampl



B0L002S, U5-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch76, adc0

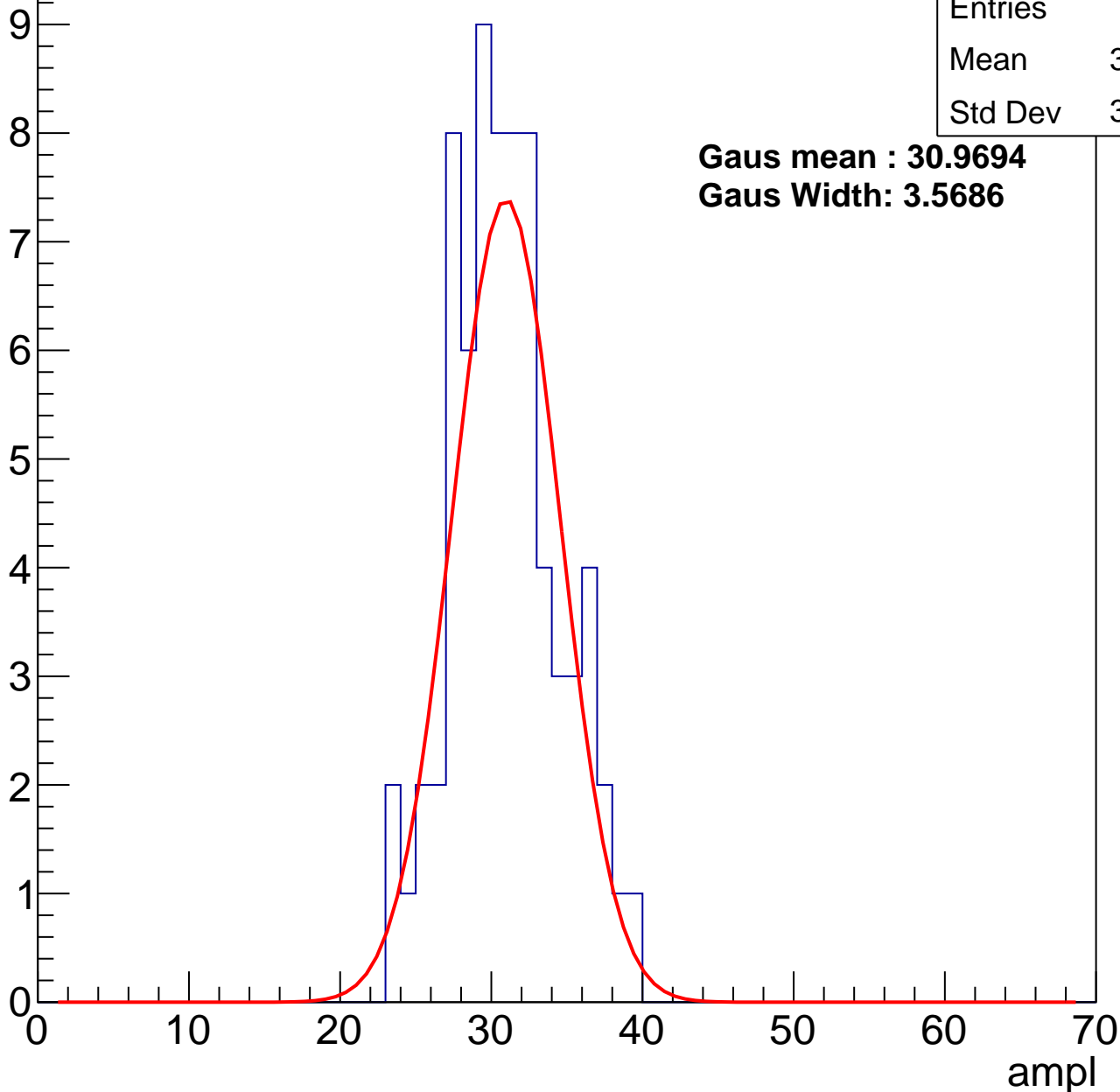
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.49
Std Dev	3.516

Gaus mean : 30.9694

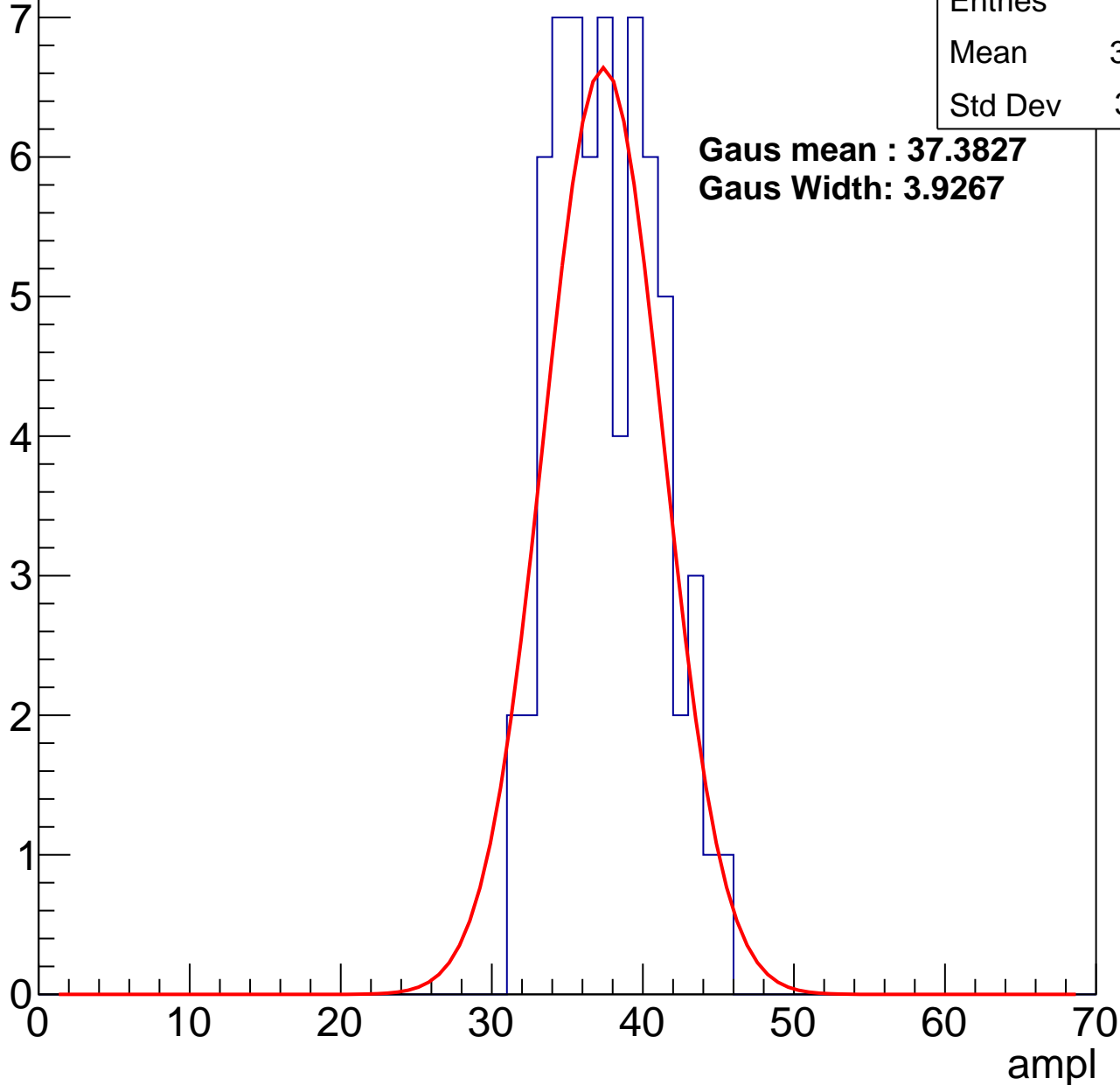
Gaus Width: 3.5686



B0L002S, U5-ch76, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch76, adc2

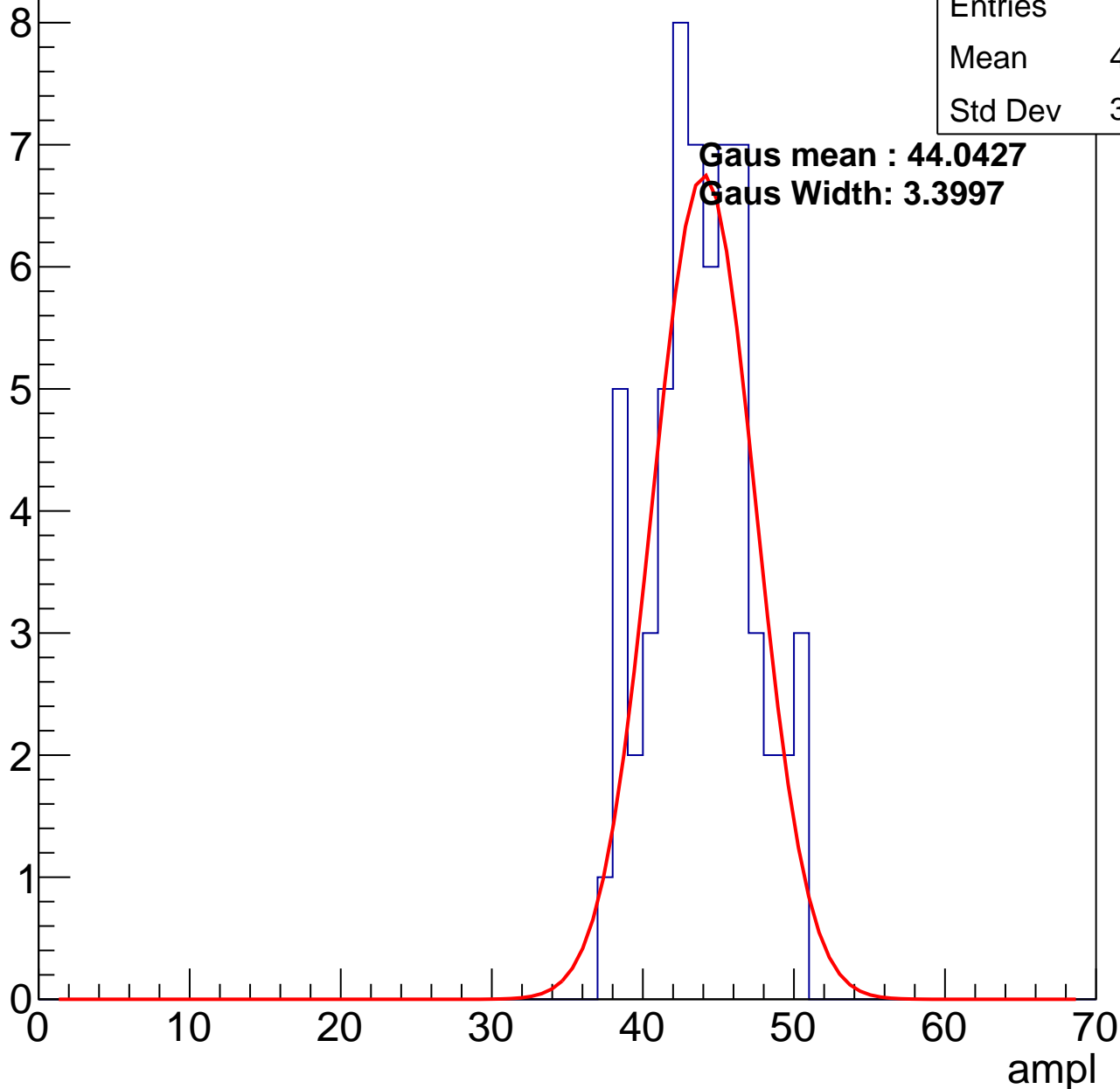
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	43.49
Std Dev	3.257

Gaus mean : 44.0427

Gaus Width: 3.3997

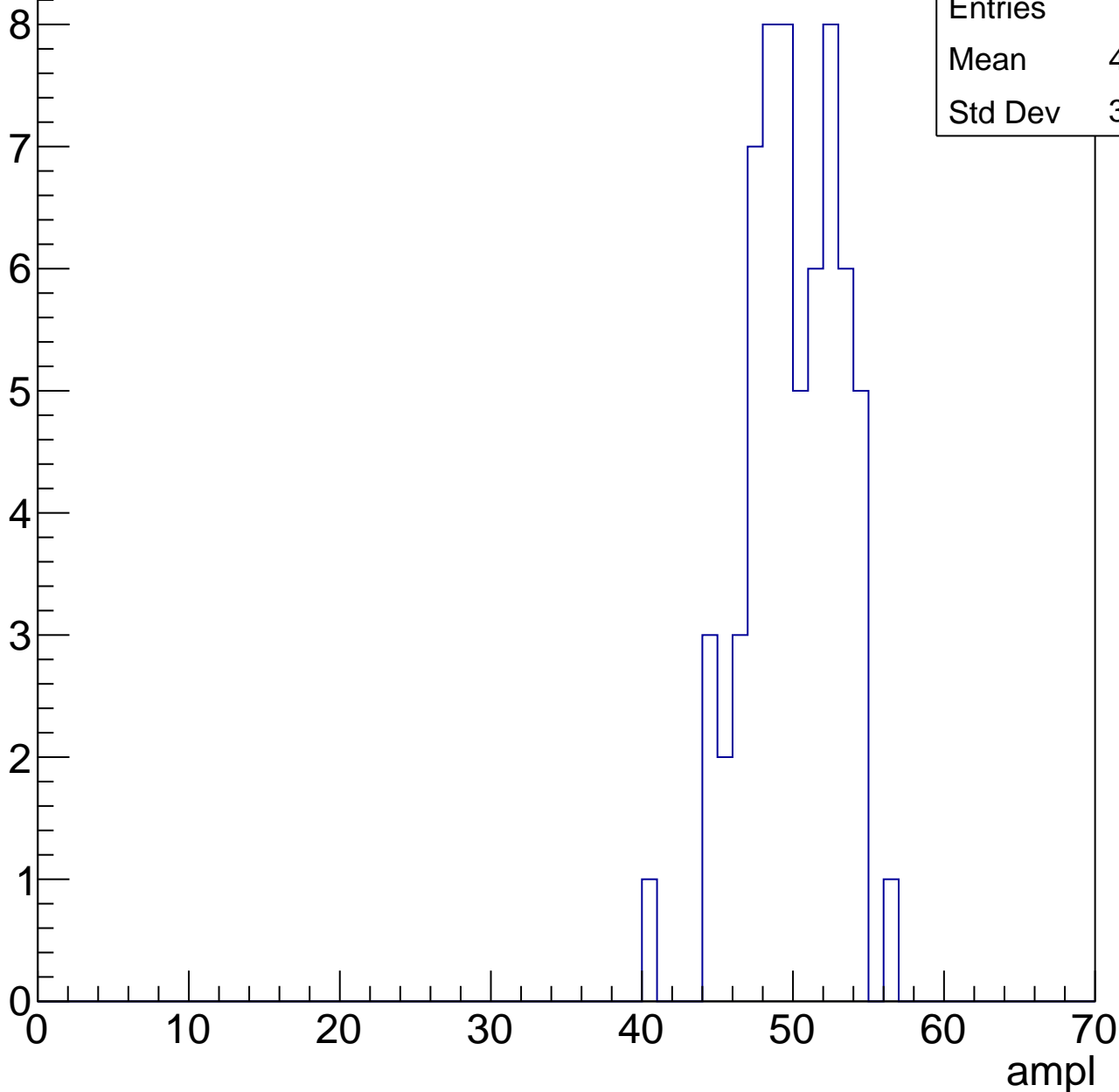


B0L002S, U5-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

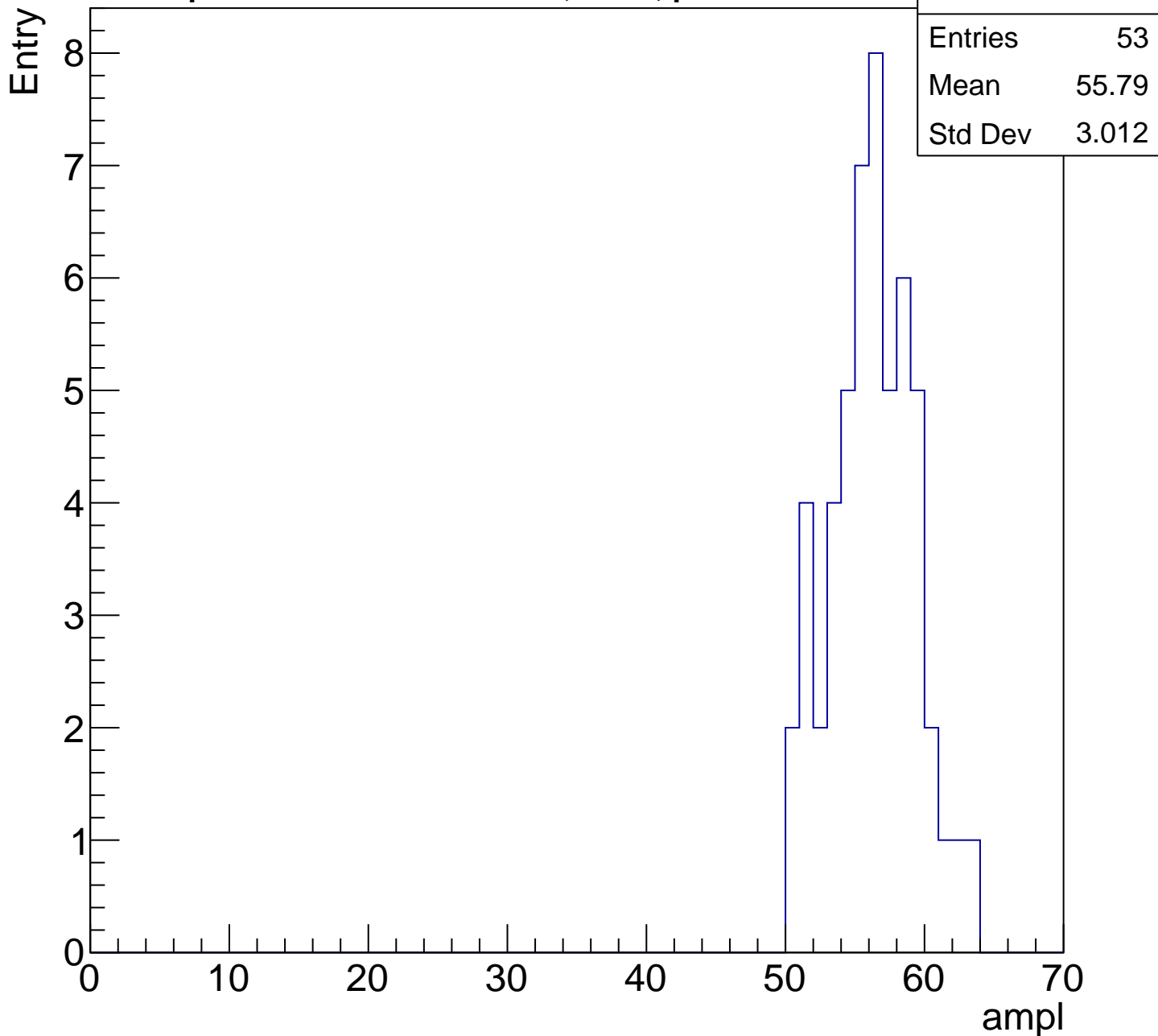
Entry

Entries	63
Mean	49.54
Std Dev	3.095



B0L002S, U5-ch76, adc4

calib_packv5_042523_0143.root, FC#8, port C1

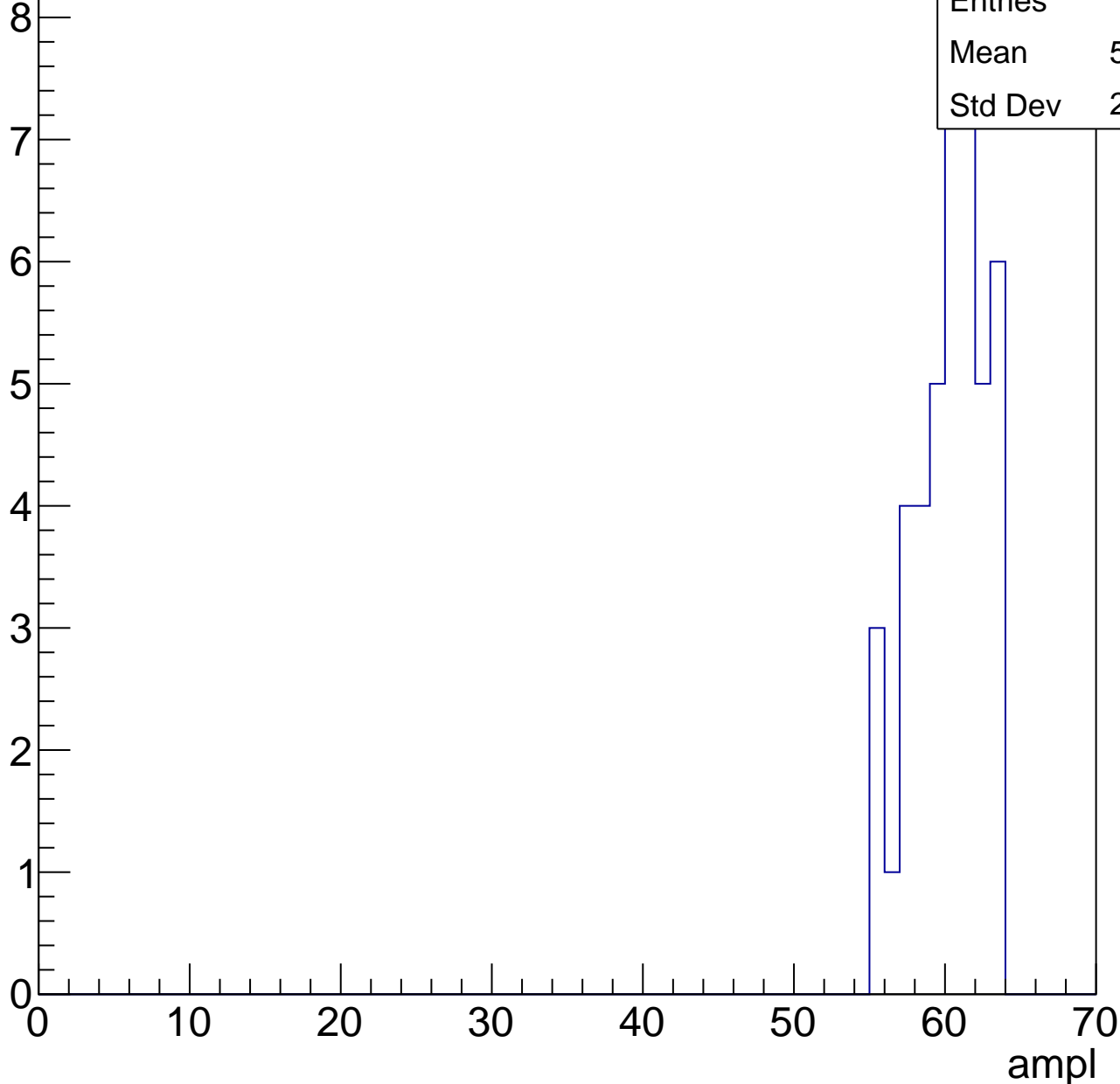


B0L002S, U5-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

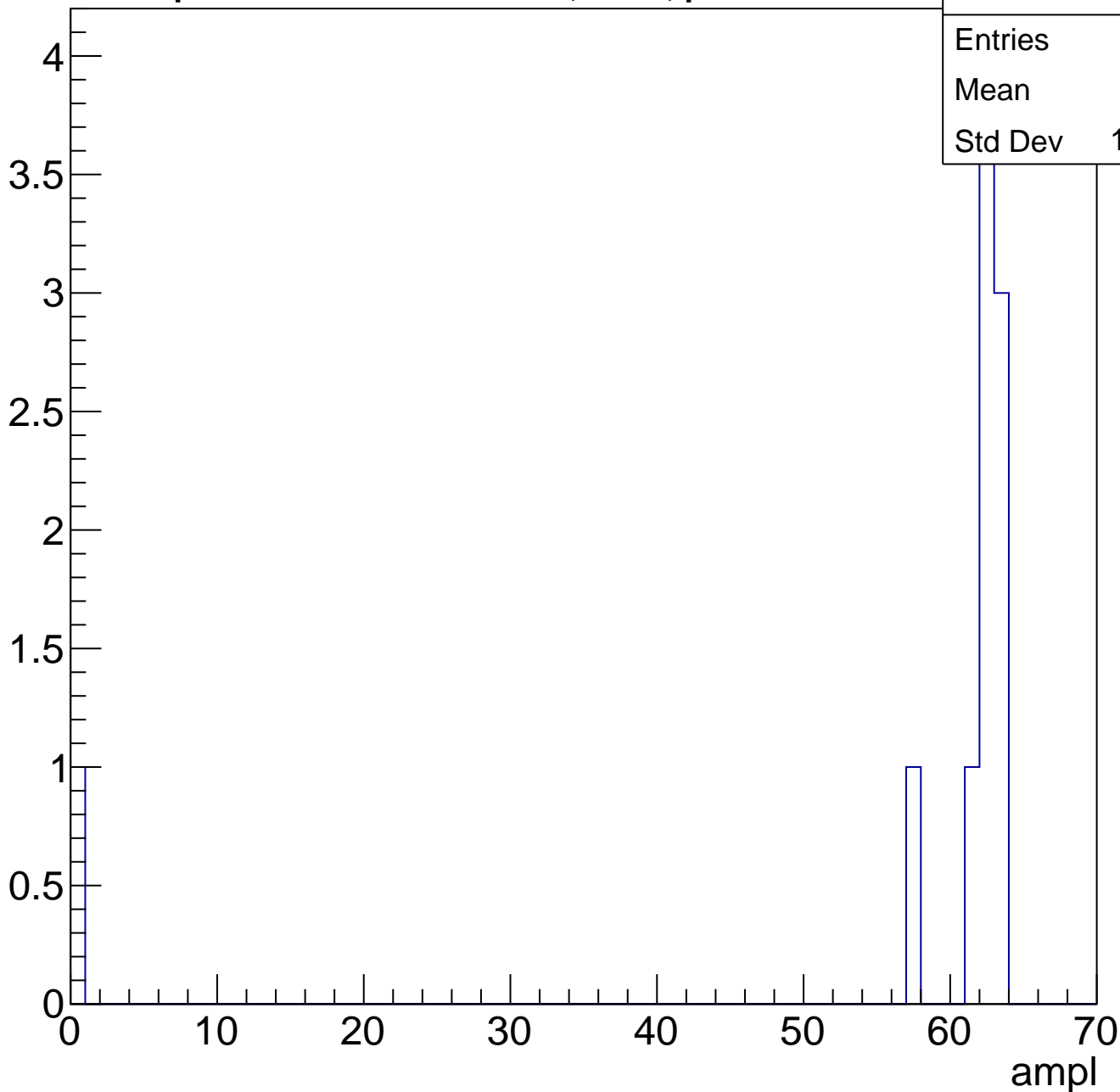
Entries	44
Mean	59.82
Std Dev	2.279



B0L002S, U5-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch77, adc0

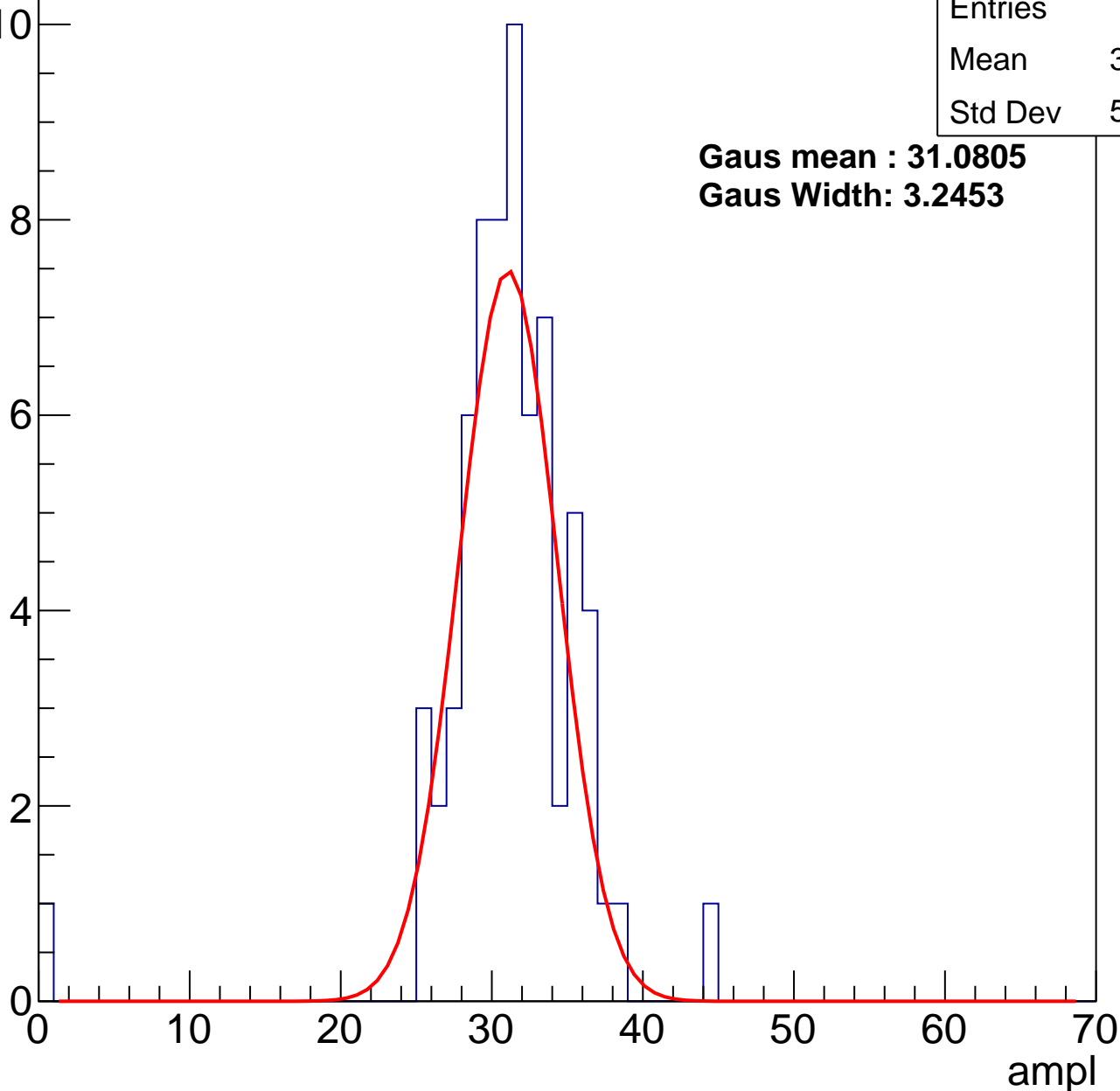
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.69
Std Dev	5.059

Gaus mean : 31.0805

Gaus Width: 3.2453



B0L002S, U5-ch77, adc1

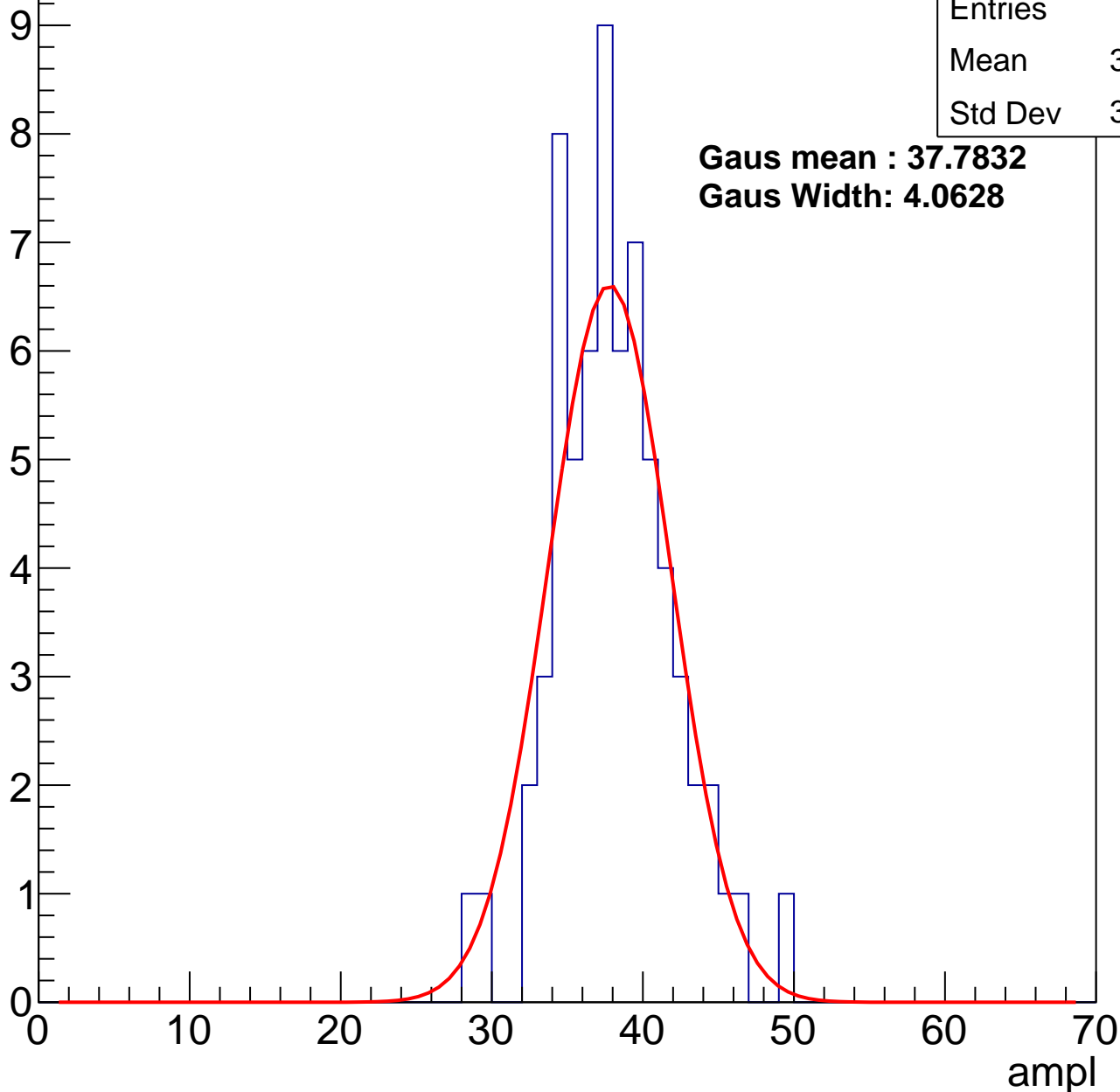
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.63
Std Dev	3.847

Gaus mean : 37.7832

Gaus Width: 4.0628



B0L002S, U5-ch77, adc2

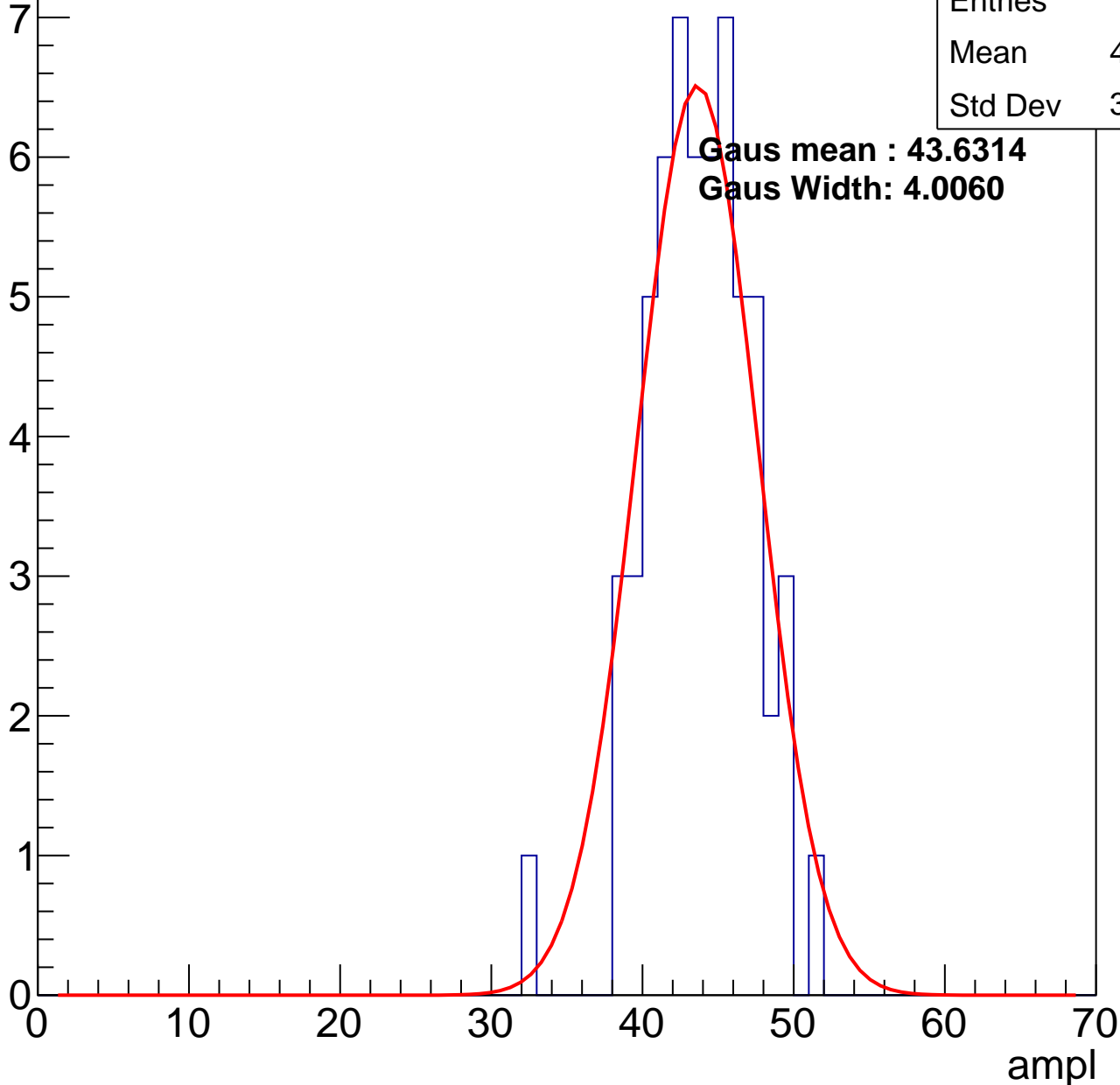
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.32
Std Dev	3.403

Gaus mean : 43.6314

Gaus Width: 4.0060

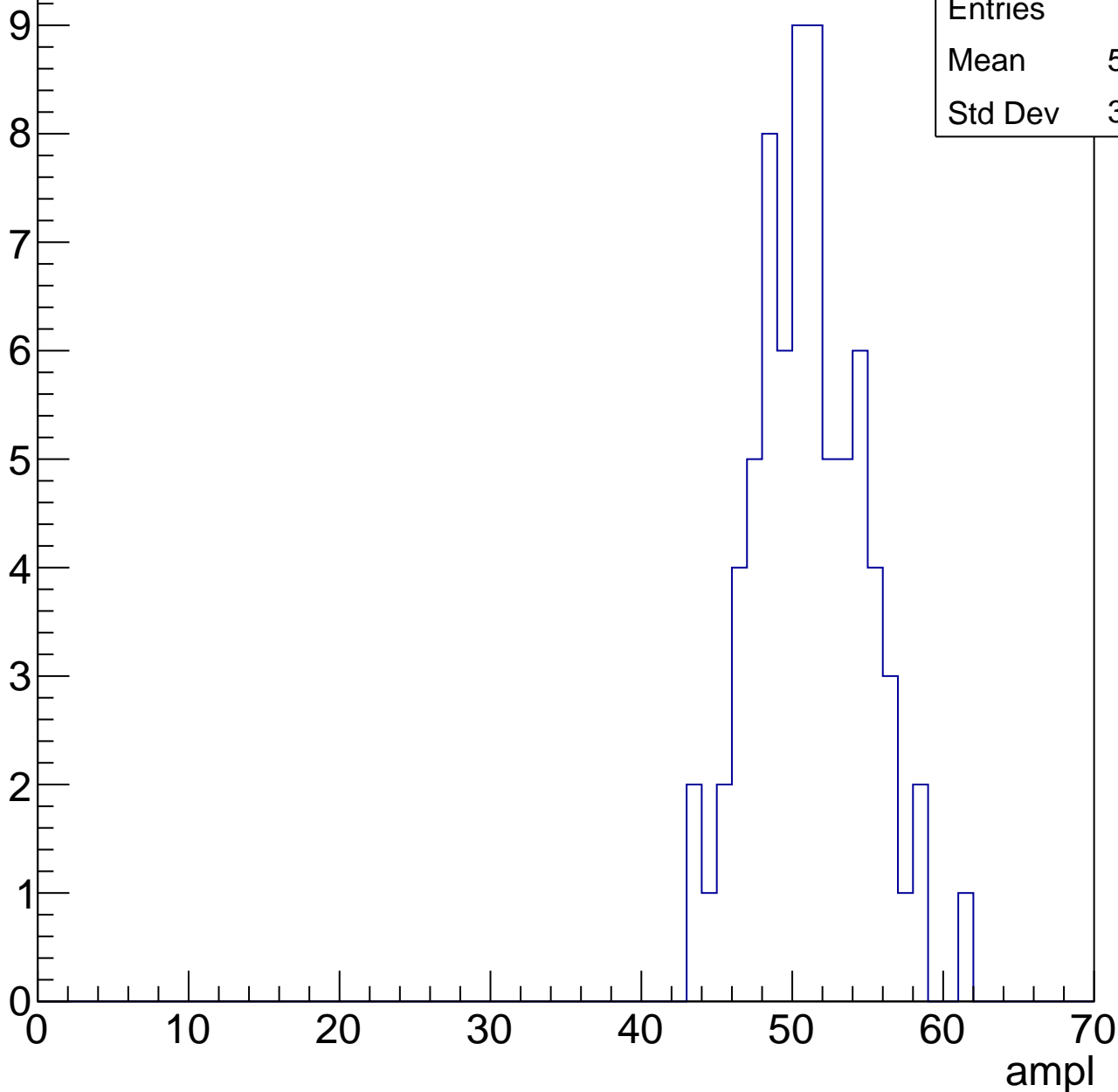


B0L002S, U5-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	50.64
Std Dev	3.658

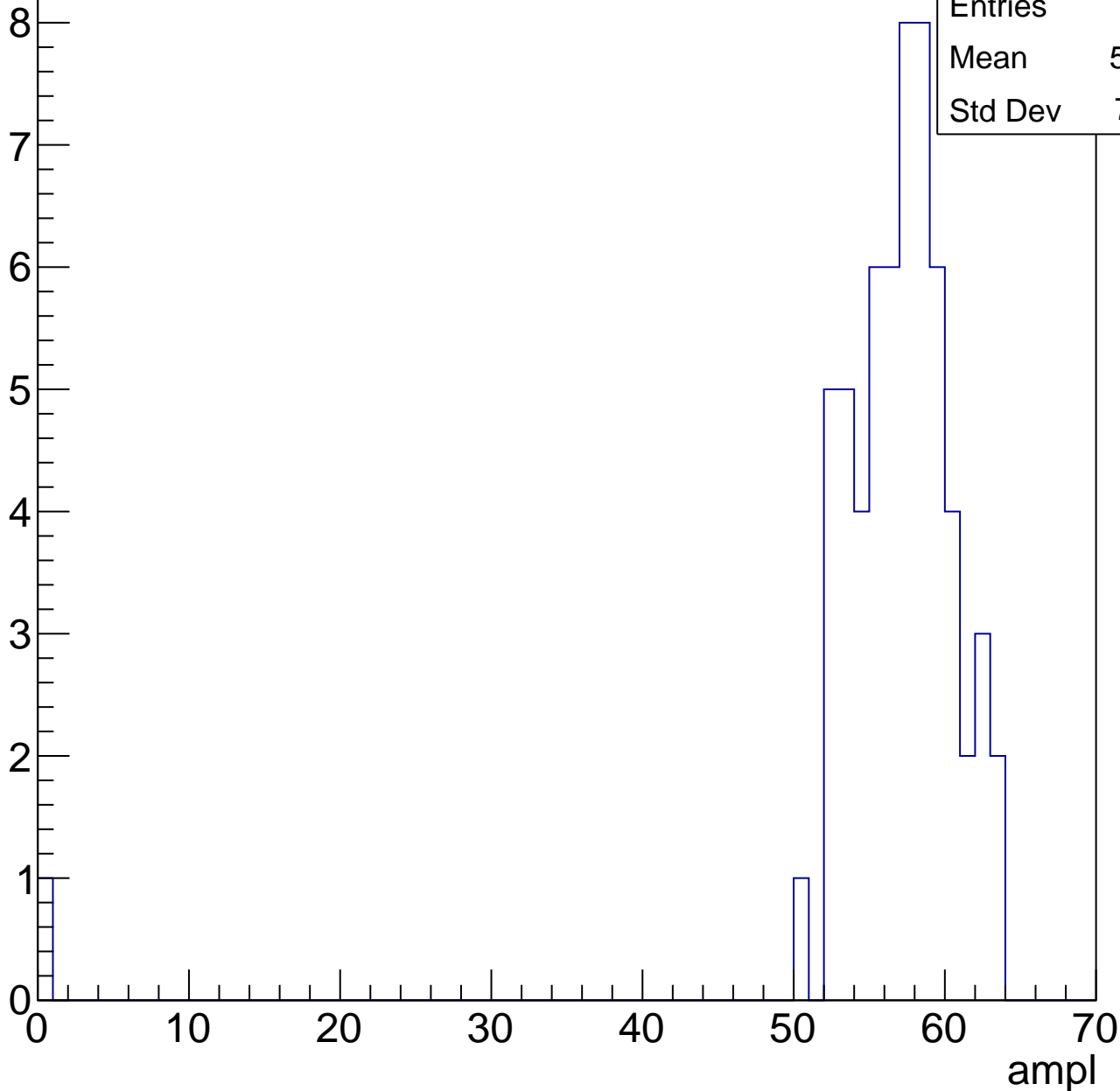


B0L002S, U5-ch77, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	55.82
Std Dev	7.821



B0L002S, U5-ch77, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries

29

Mean

60.76

Std Dev

1.755

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

7

B0L002S, U5-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch77, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch78, adc0

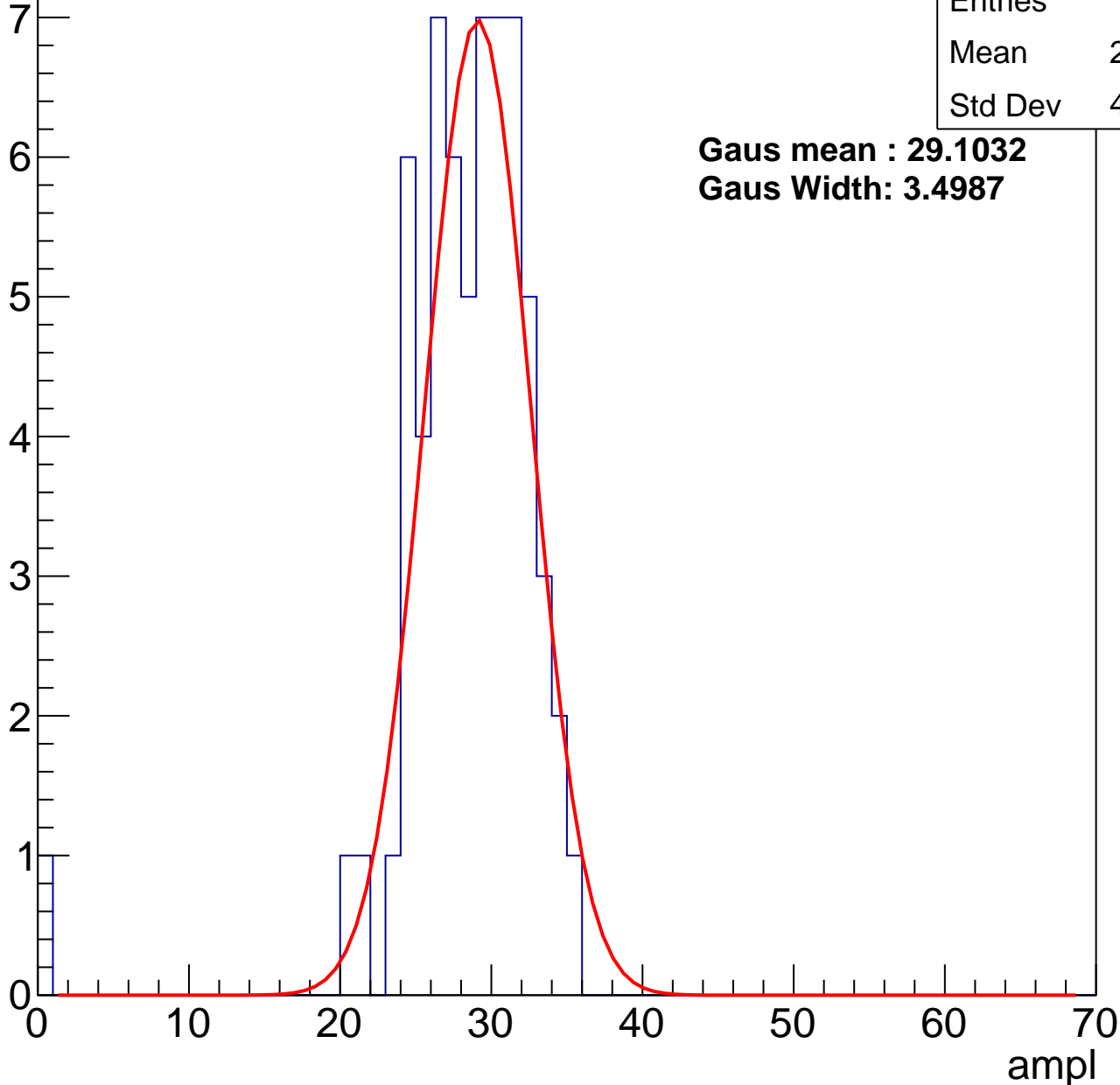
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	27.88
Std Dev	4.778

Gaus mean : 29.1032

Gaus Width: 3.4987



B0L002S, U5-ch78, adc1

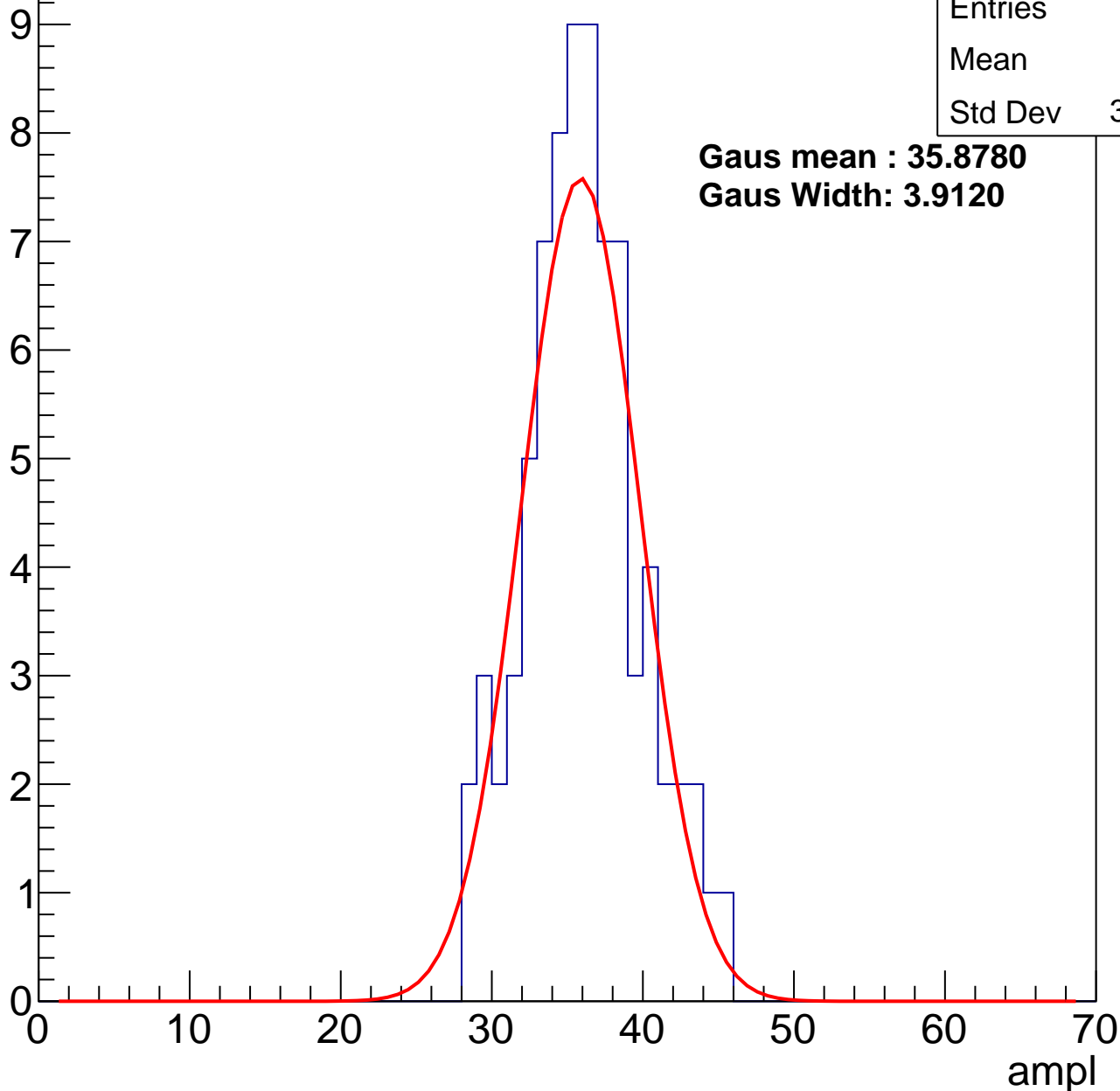
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	35.6
Std Dev	3.753

Gaus mean : 35.8780

Gaus Width: 3.9120



B0L002S, U5-ch78, adc2

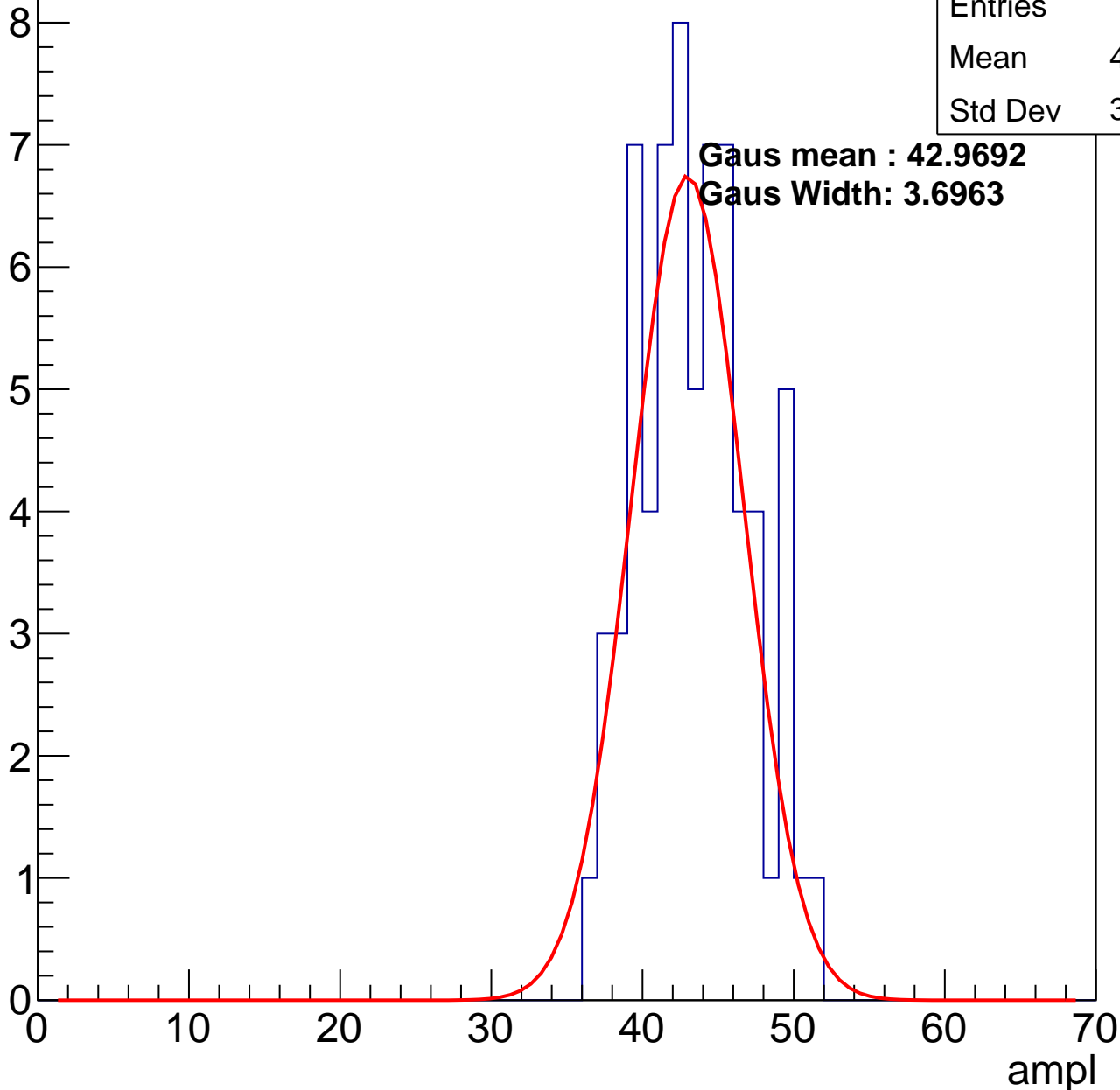
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.96
Std Dev	3.575

Gaus mean : 42.9692

Gaus Width: 3.6963

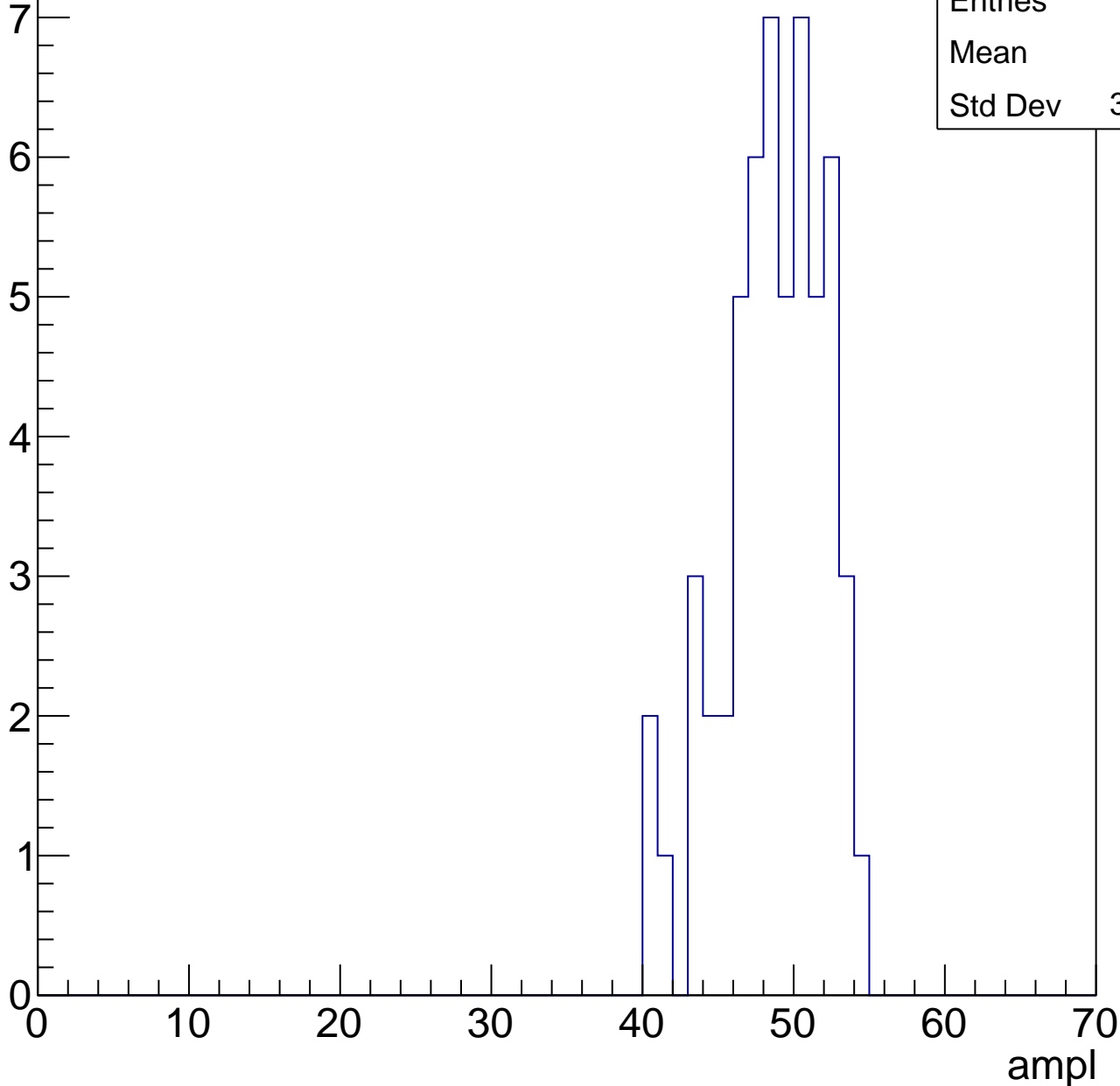


B0L002S, U5-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	48.2
Std Dev	3.332

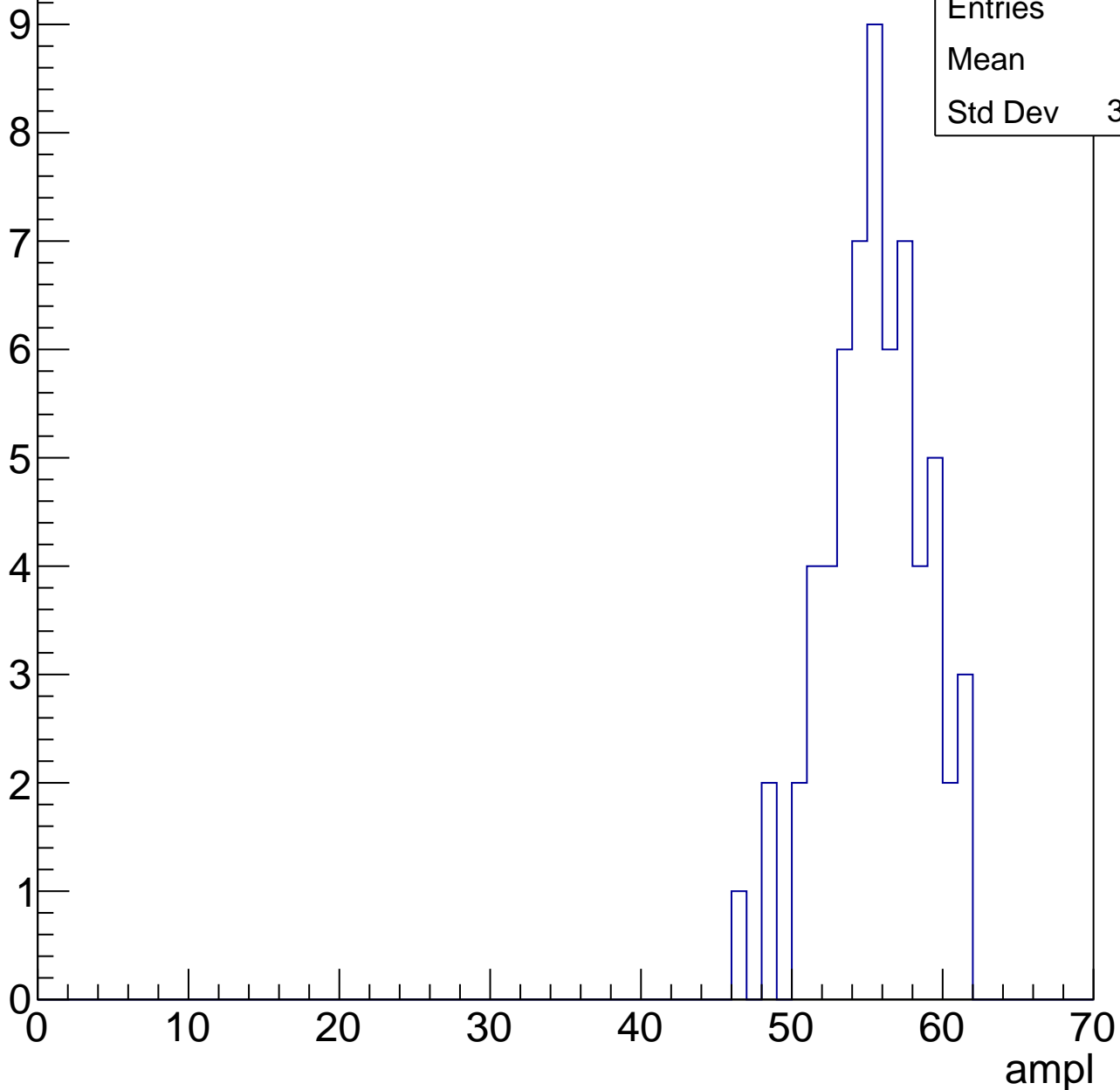


B0L002S, U5-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55
Std Dev	3.282

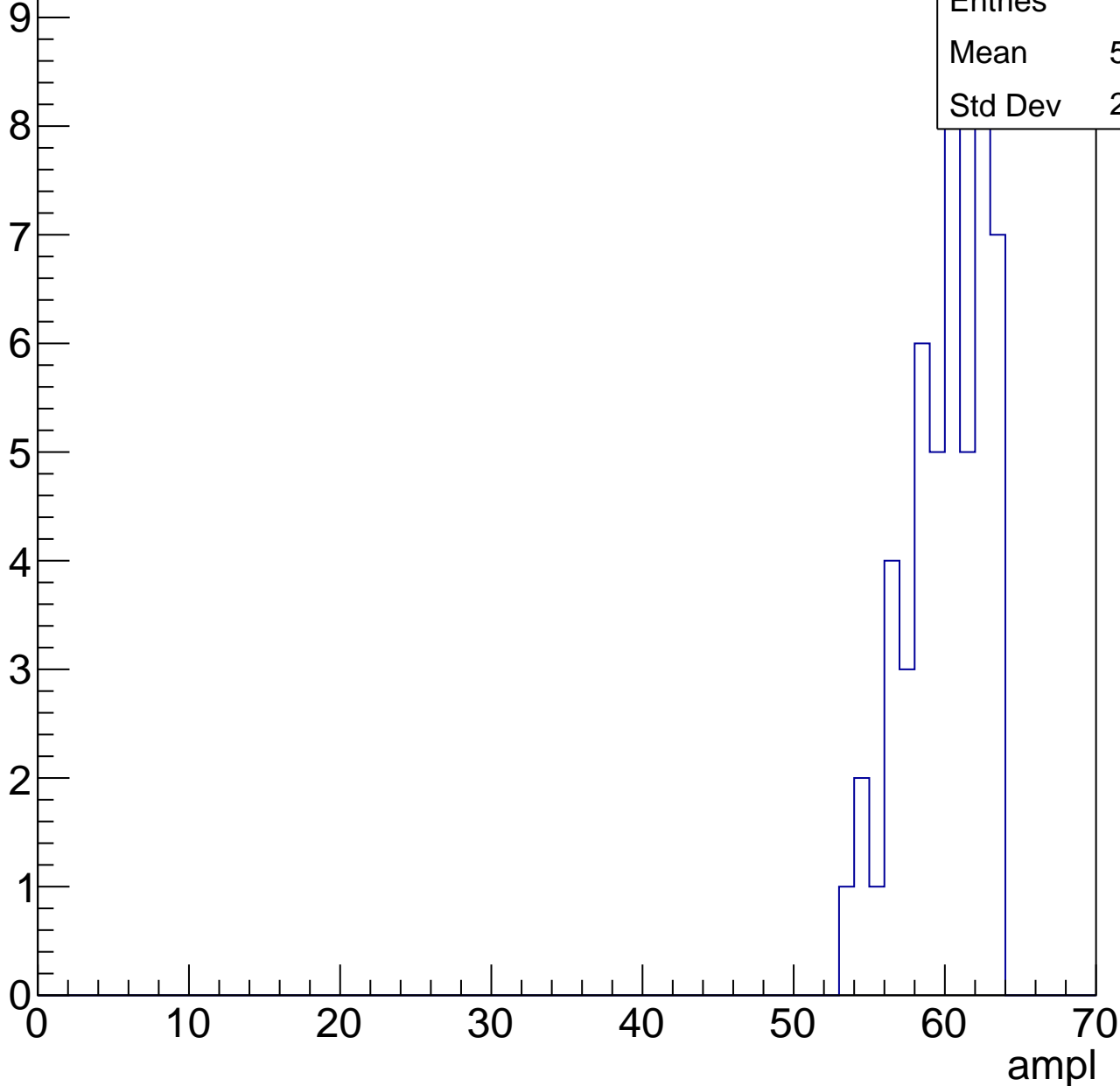


B0L002S, U5-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

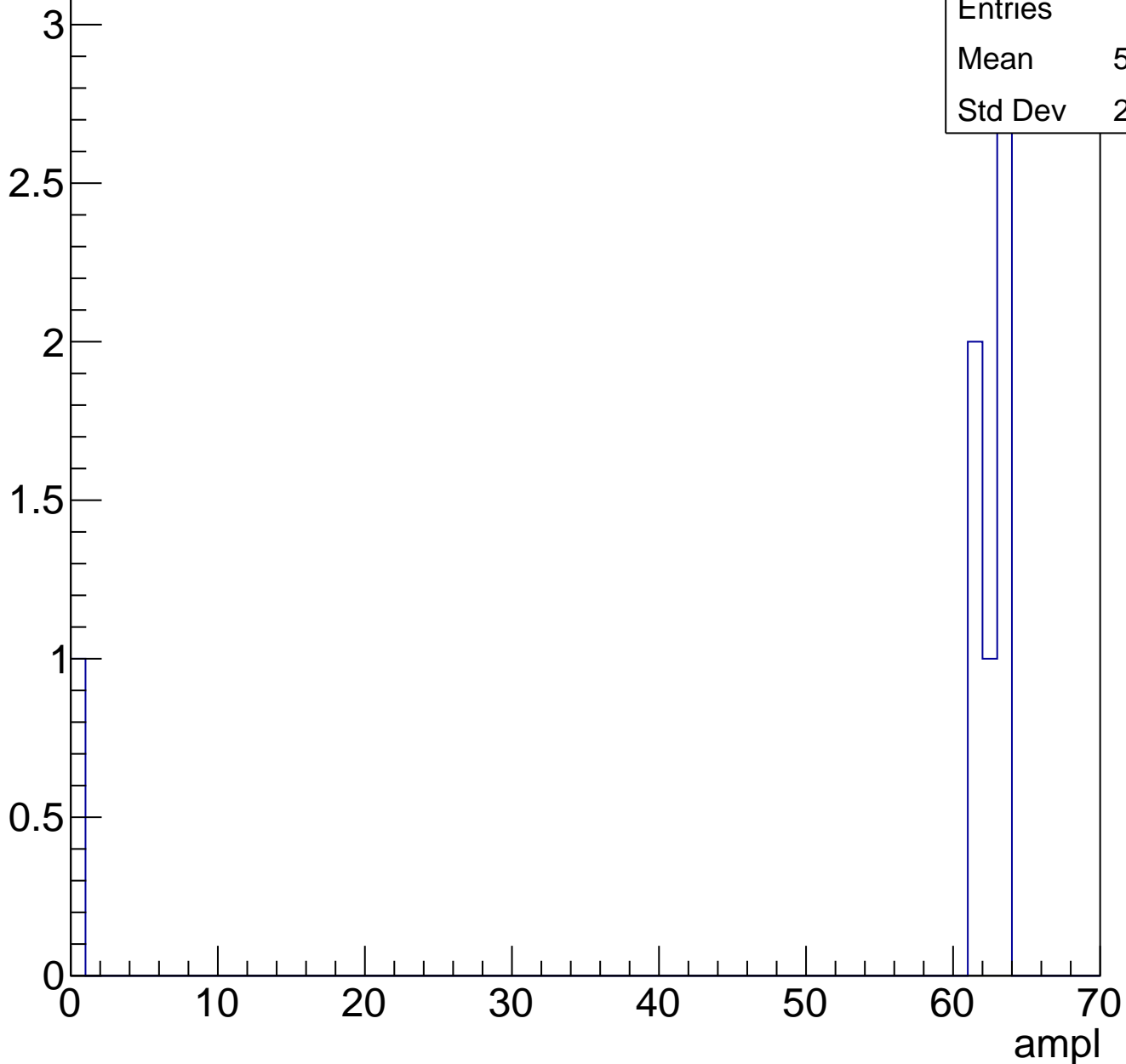
Entries	51
Mean	59.57
Std Dev	2.659



B0L002S, U5-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch79, adc0

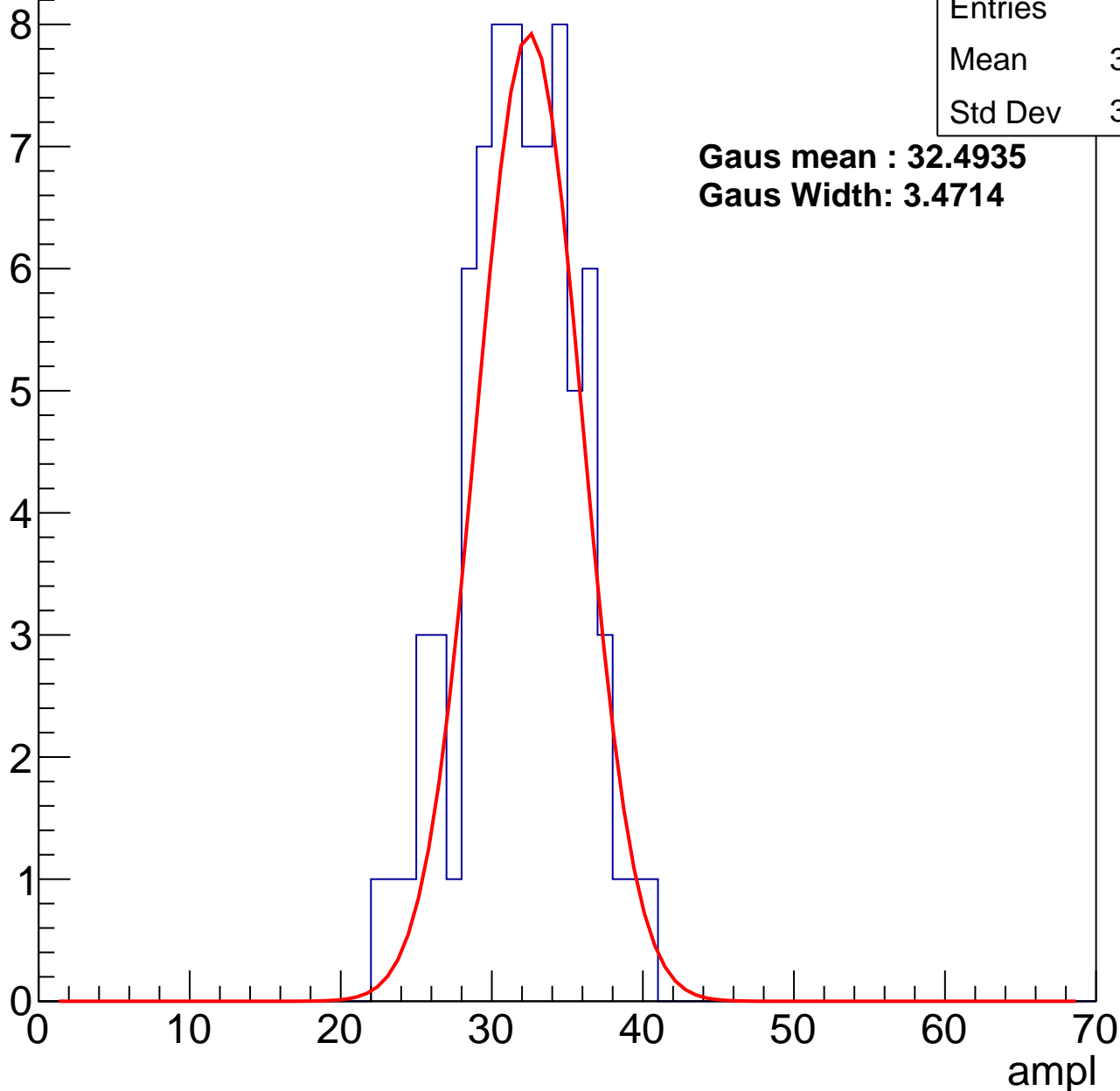
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	31.46
Std Dev	3.765

Gaus mean : 32.4935

Gaus Width: 3.4714



B0L002S, U5-ch79, adc1

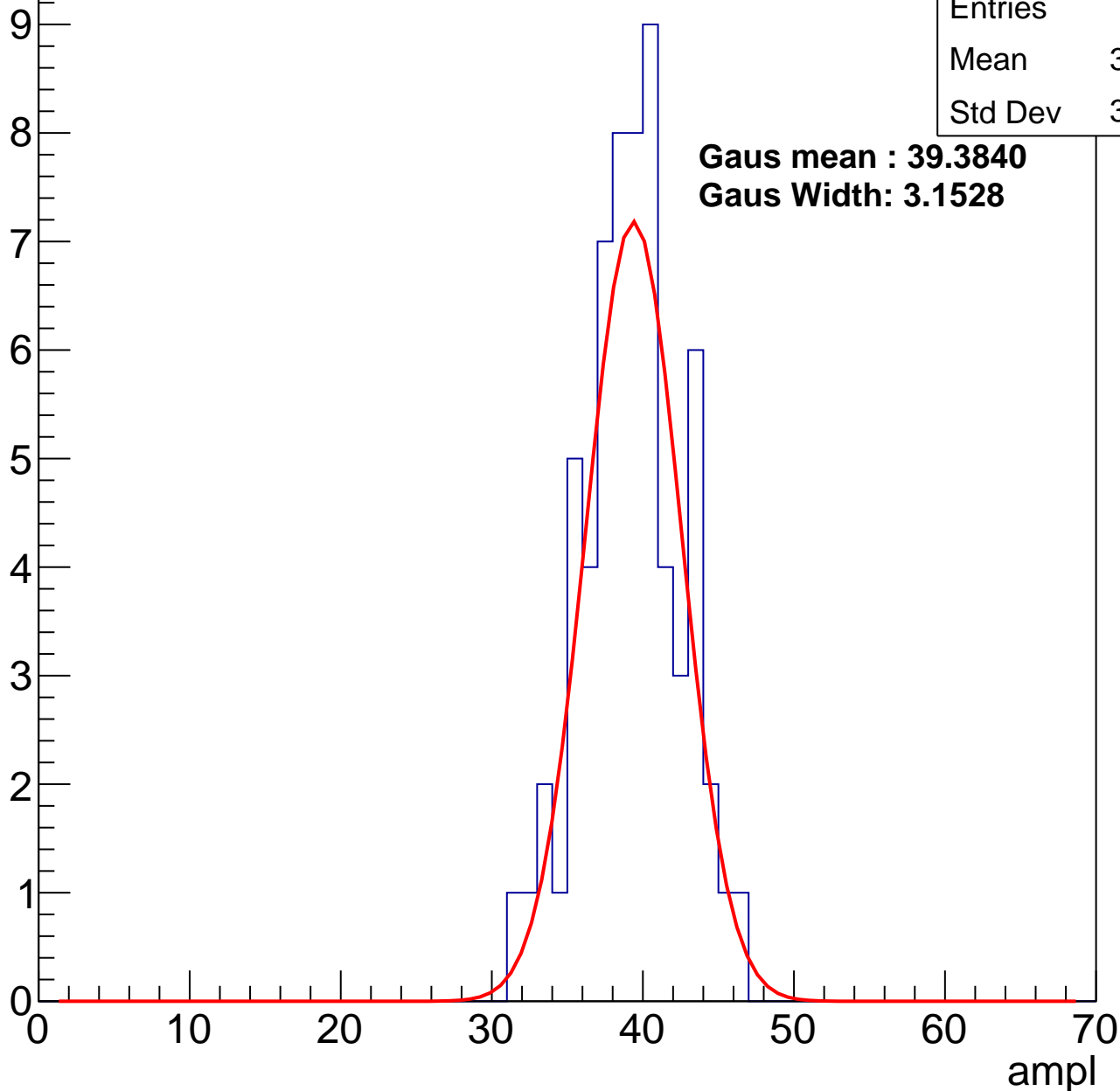
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.79
Std Dev	3.193

Gaus mean : 39.3840

Gaus Width: 3.1528



B0L002S, U5-ch79, adc2

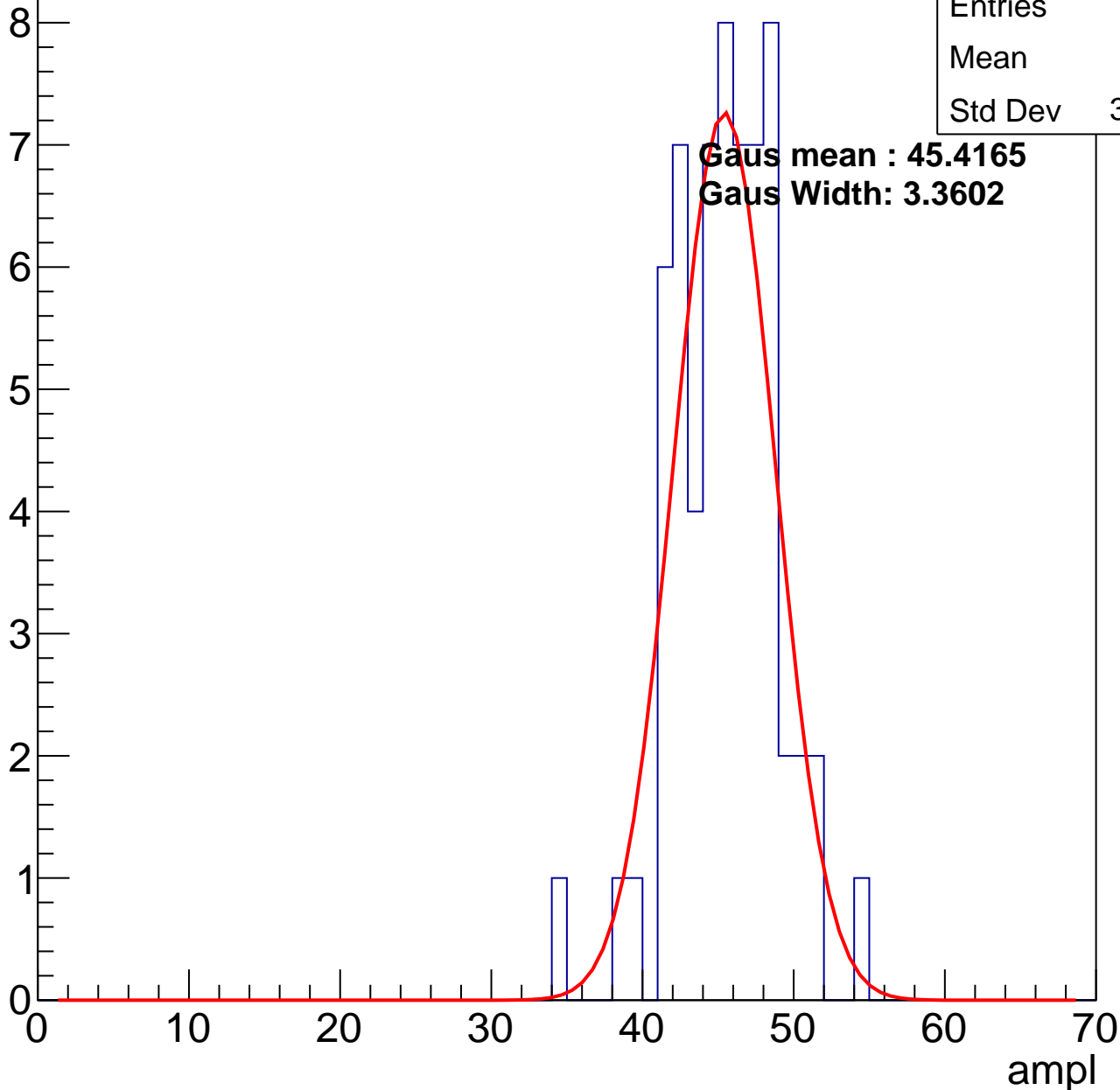
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	45
Std Dev	3.377

Gaus mean : 45.4165

Gaus Width: 3.3602

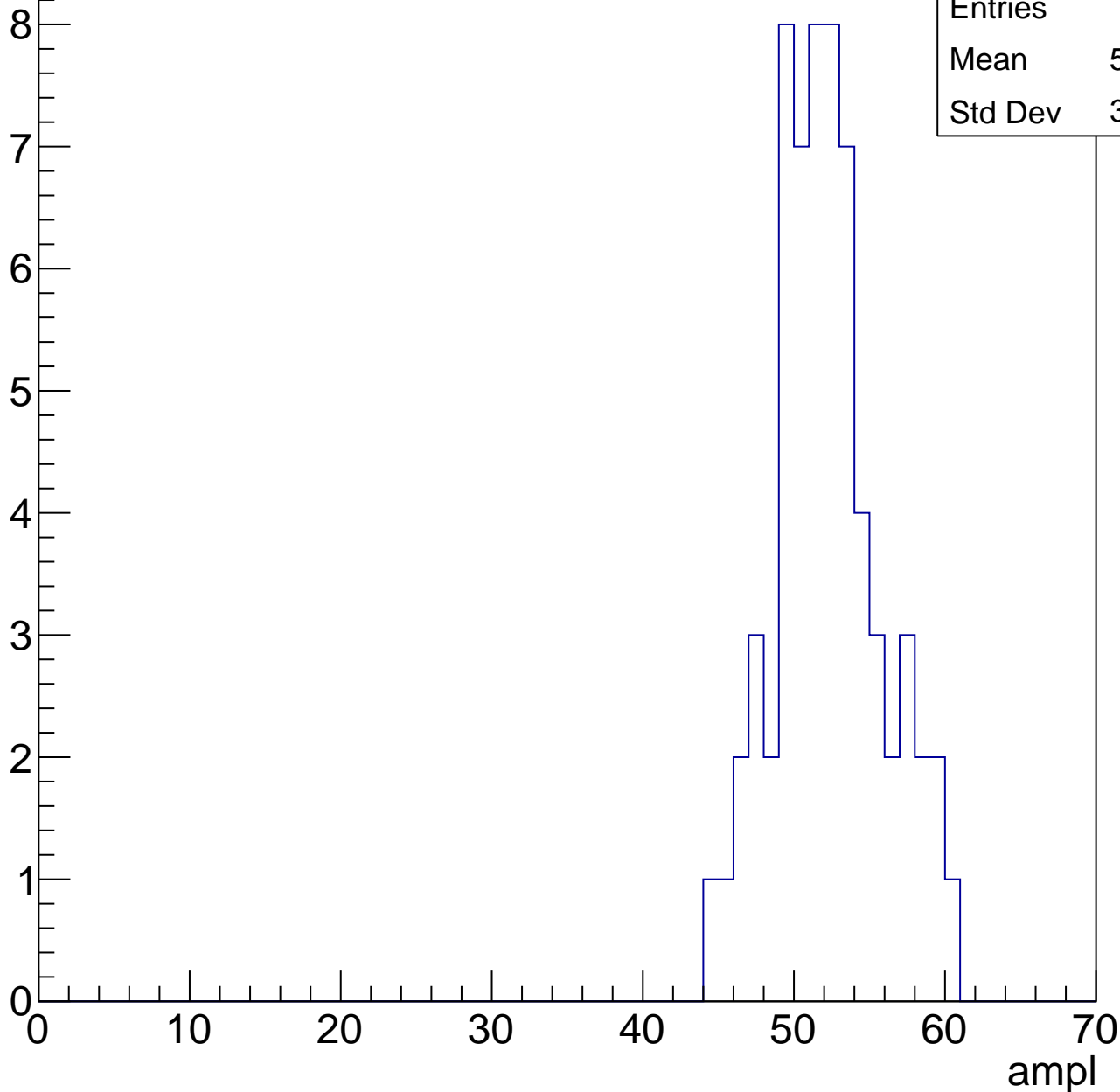


B0L002S, U5-ch79, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.77
Std Dev	3.508

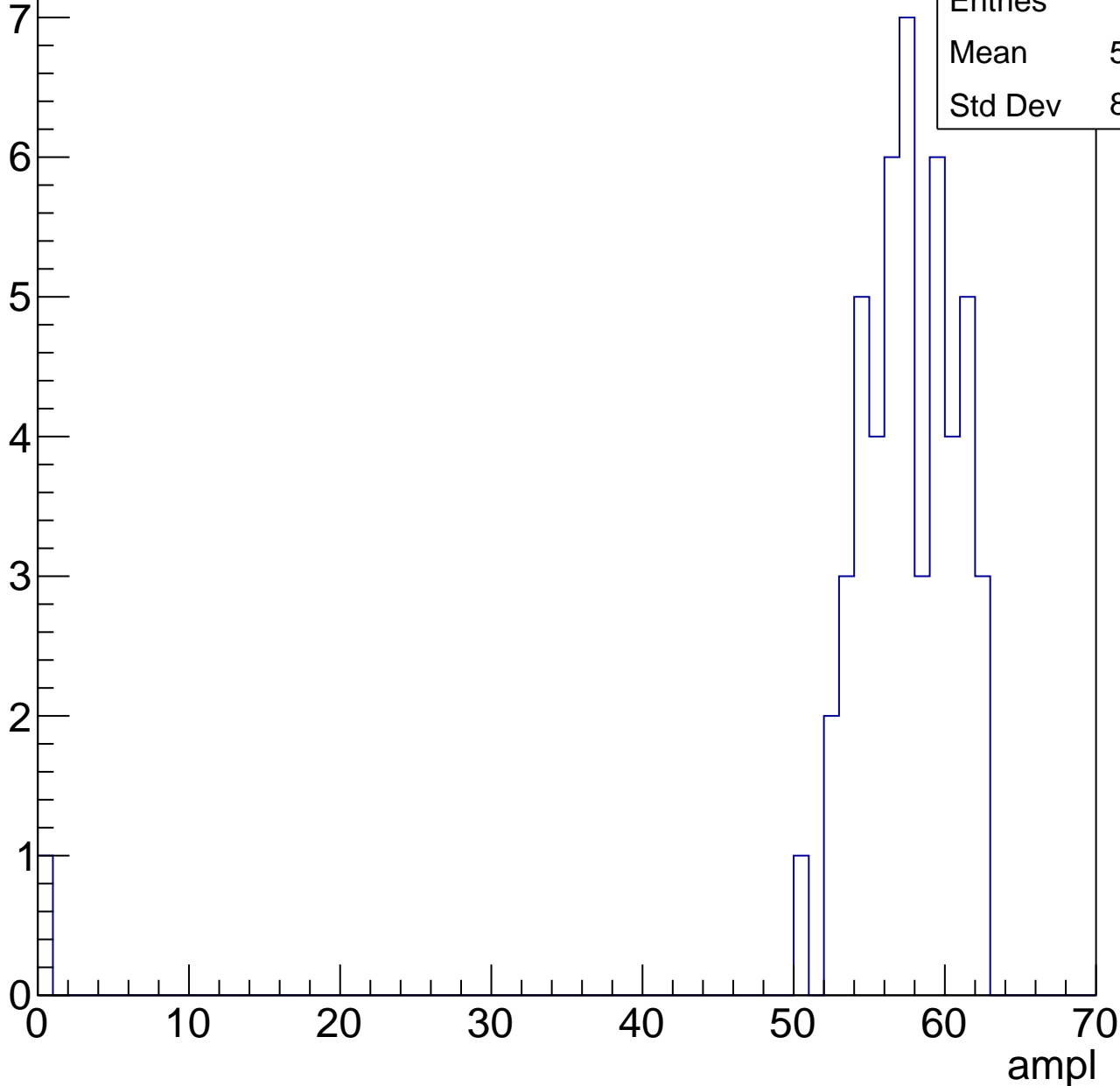


B0L002S, U5-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	55.94
Std Dev	8.514

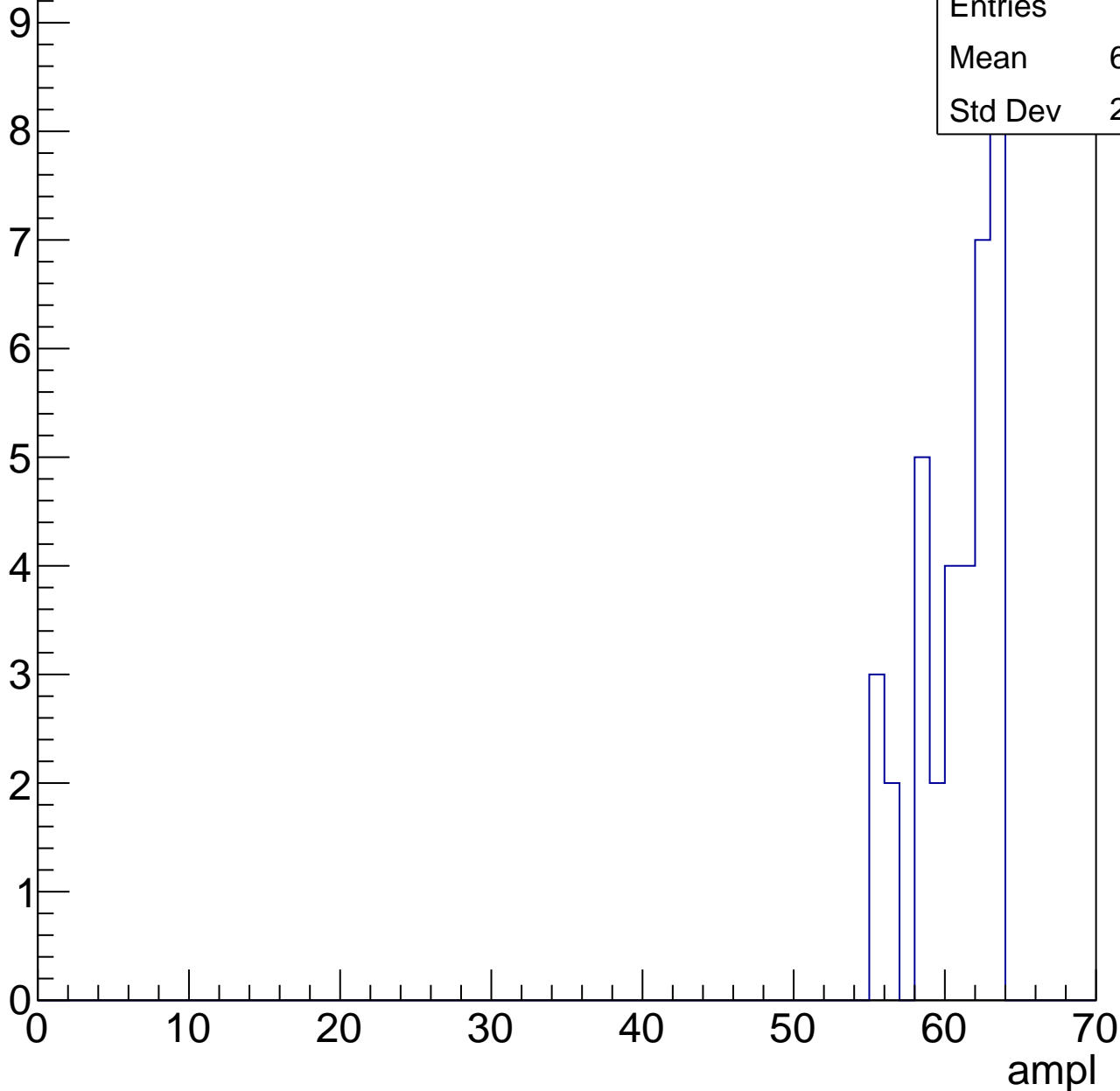


B0L002S, U5-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	60.28
Std Dev	2.578



B0L002S, U5-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch80, adc0

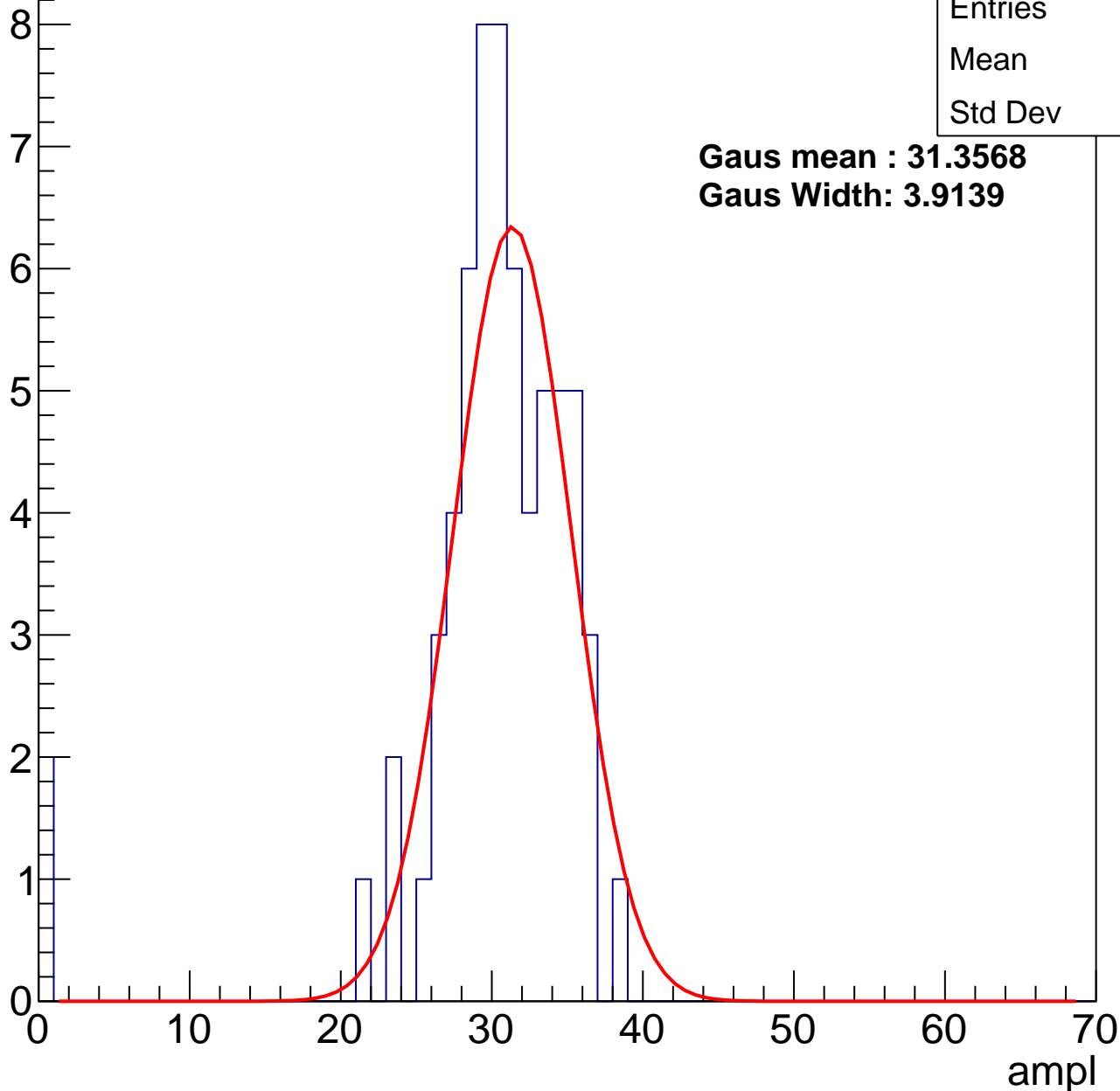
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.5
Std Dev	6.31

Gaus mean : 31.3568

Gaus Width: 3.9139



B0L002S, U5-ch80, adc1

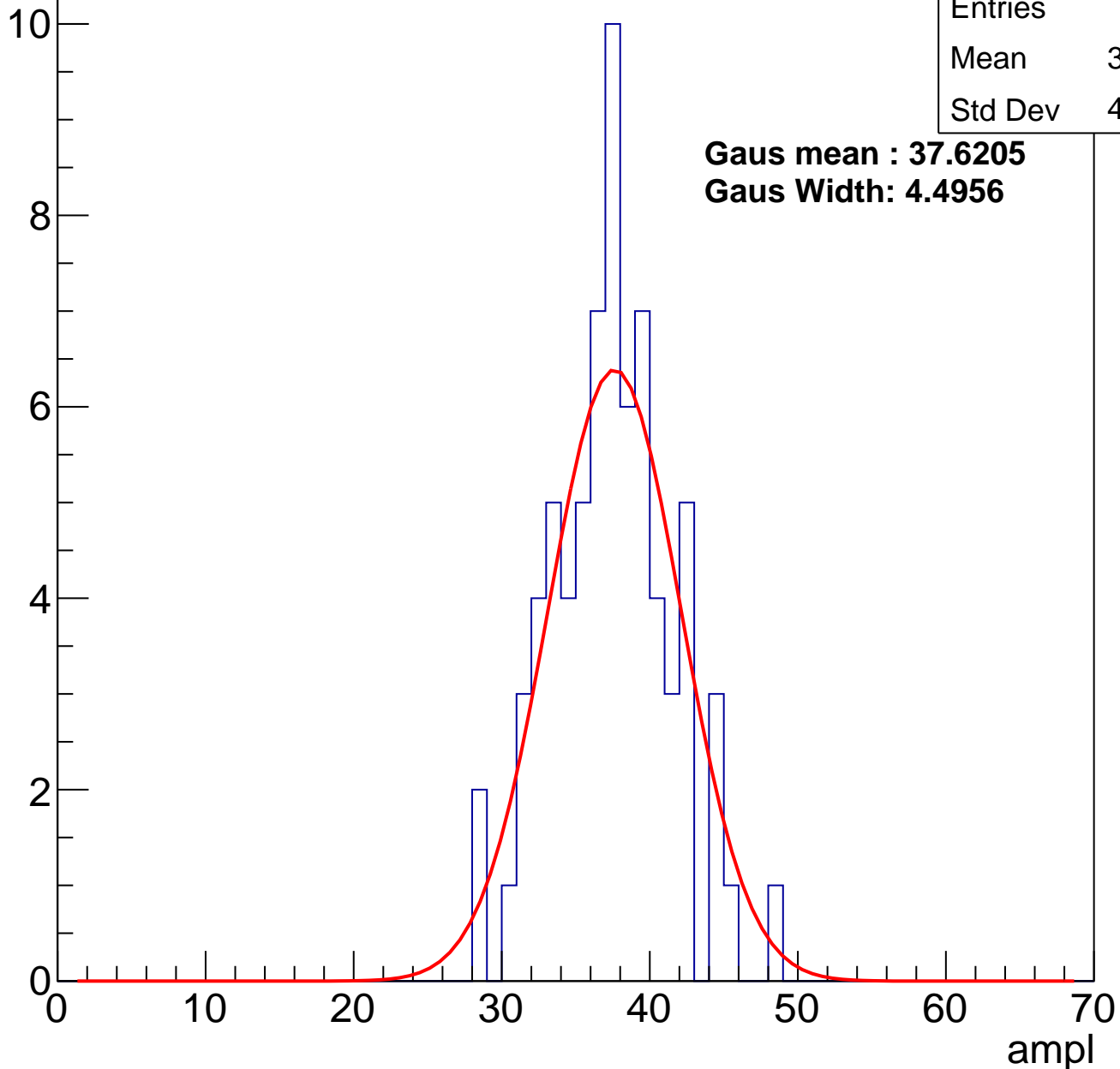
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	36.96
Std Dev	4.002

Gaus mean : 37.6205

Gaus Width: 4.4956

Entry



B0L002S, U5-ch80, adc2

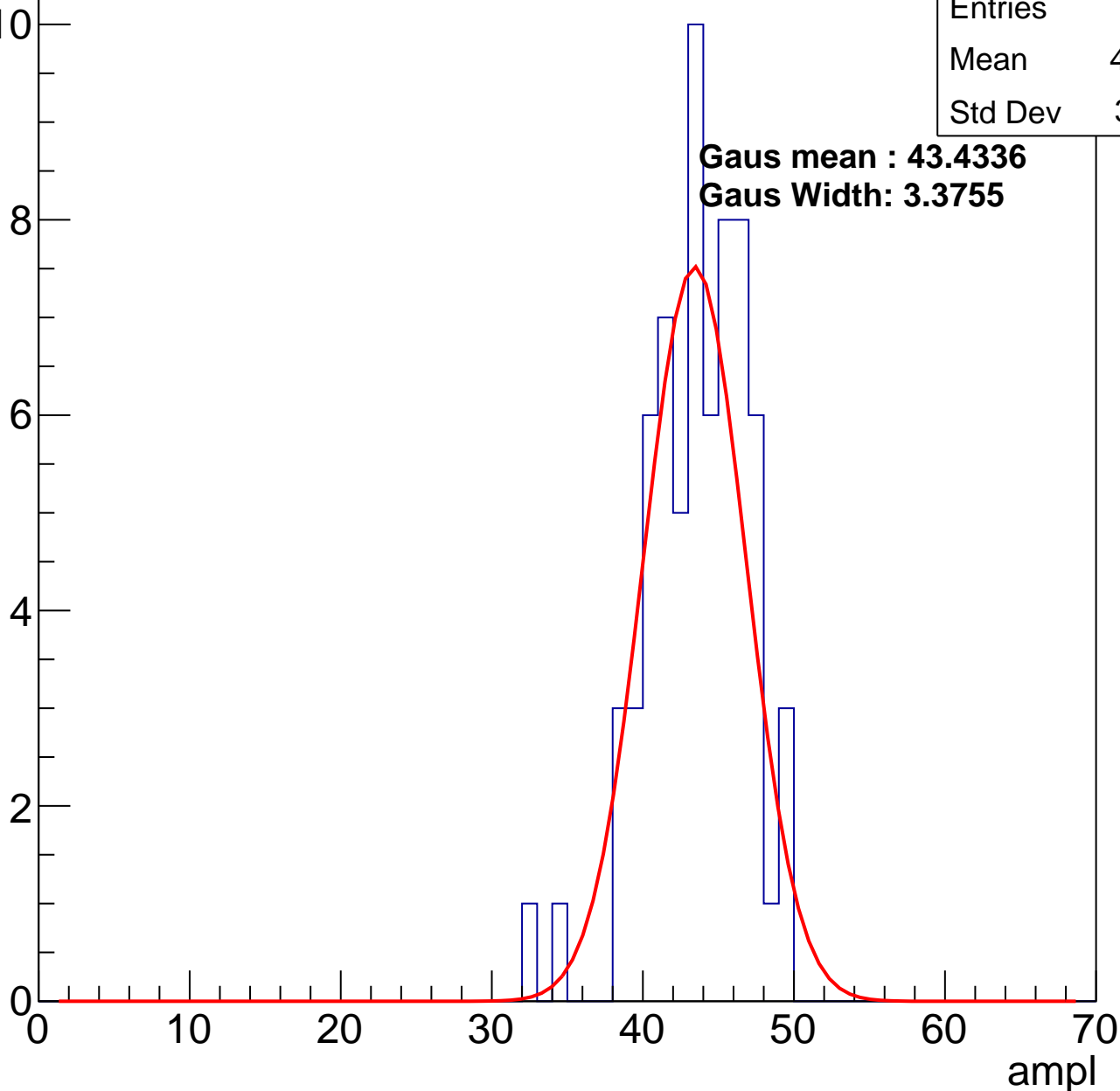
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.13
Std Dev	3.321

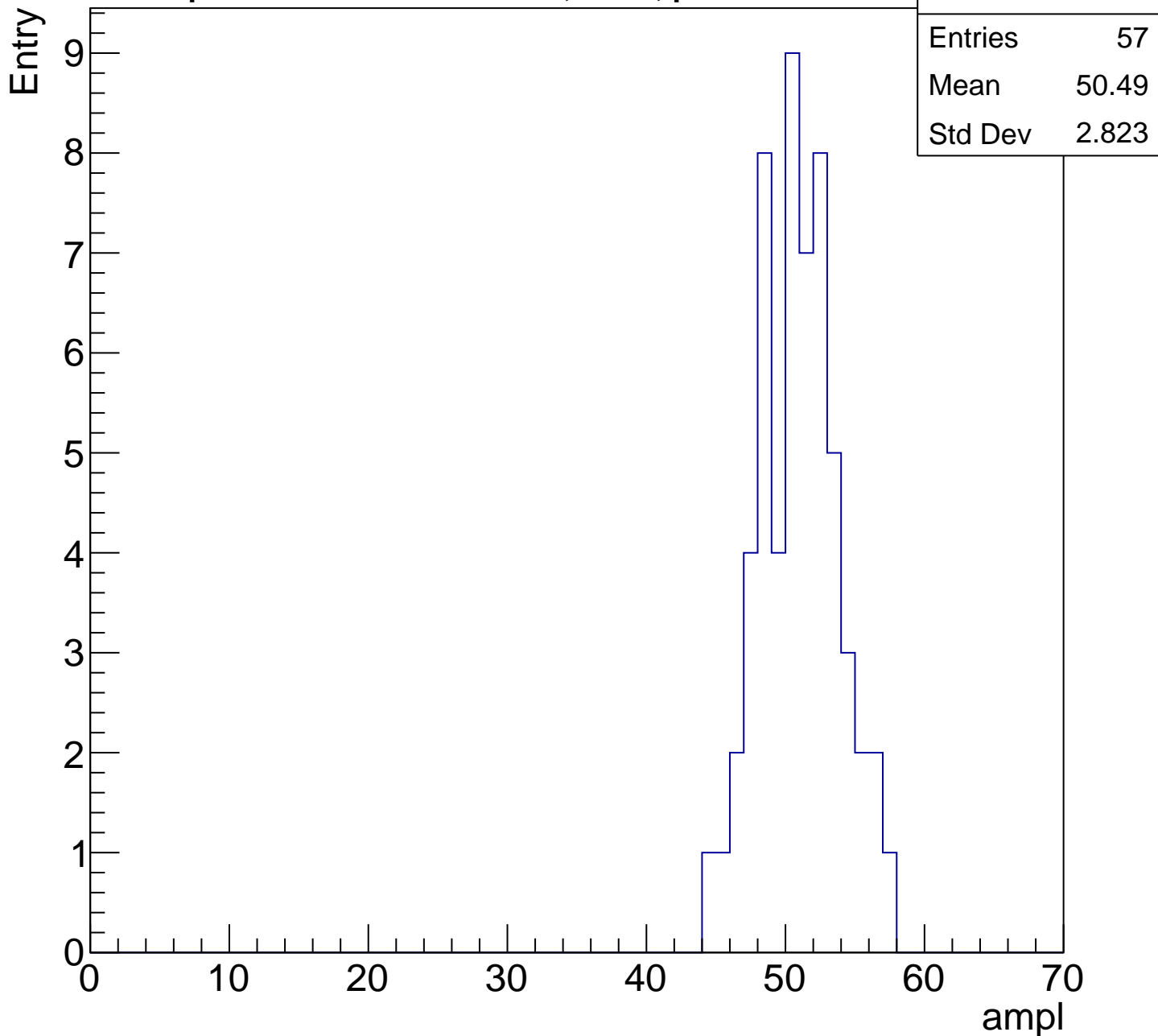
Gaus mean : 43.4336

Gaus Width: 3.3755



B0L002S, U5-ch80, adc3

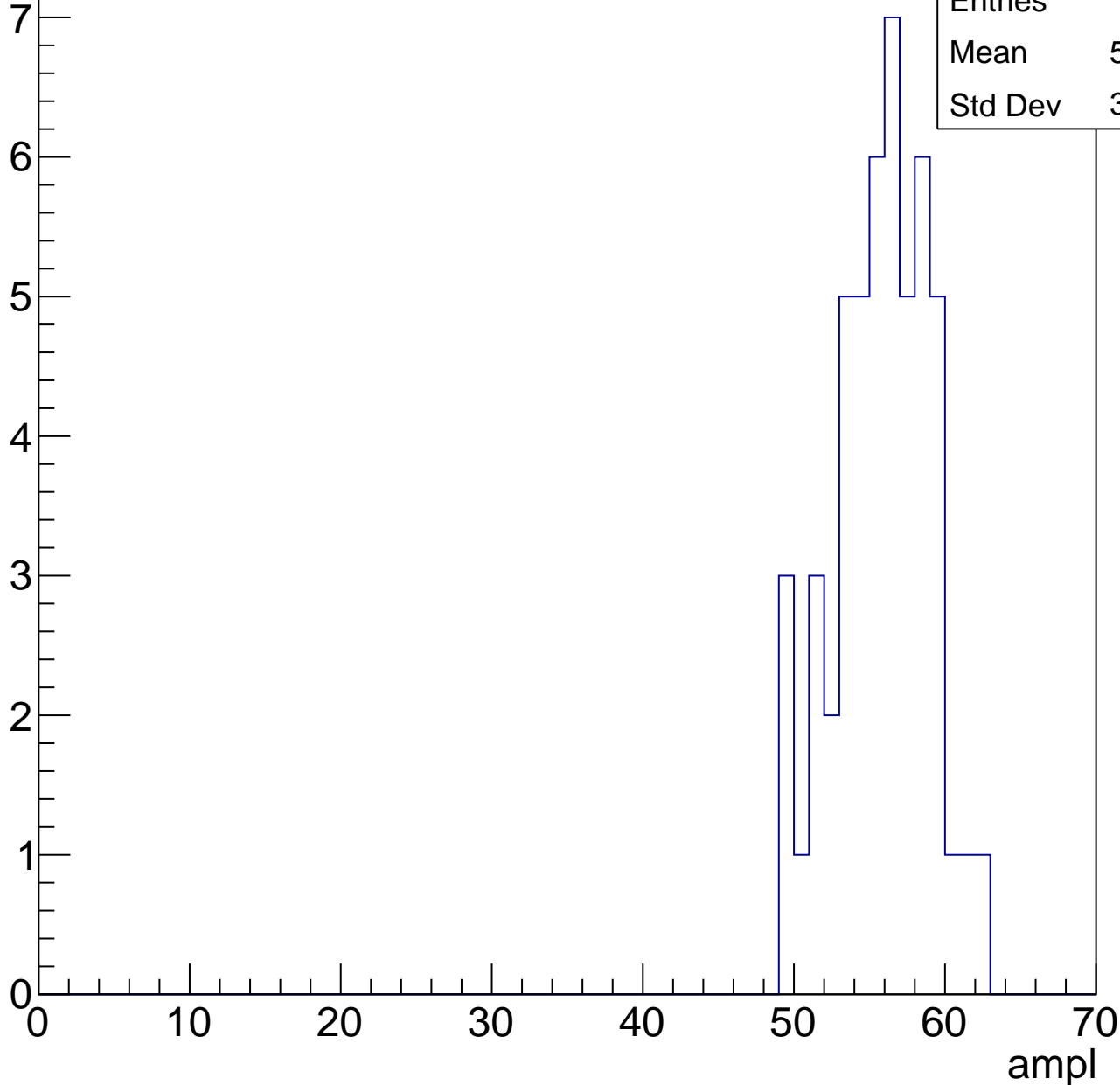
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch80, adc5

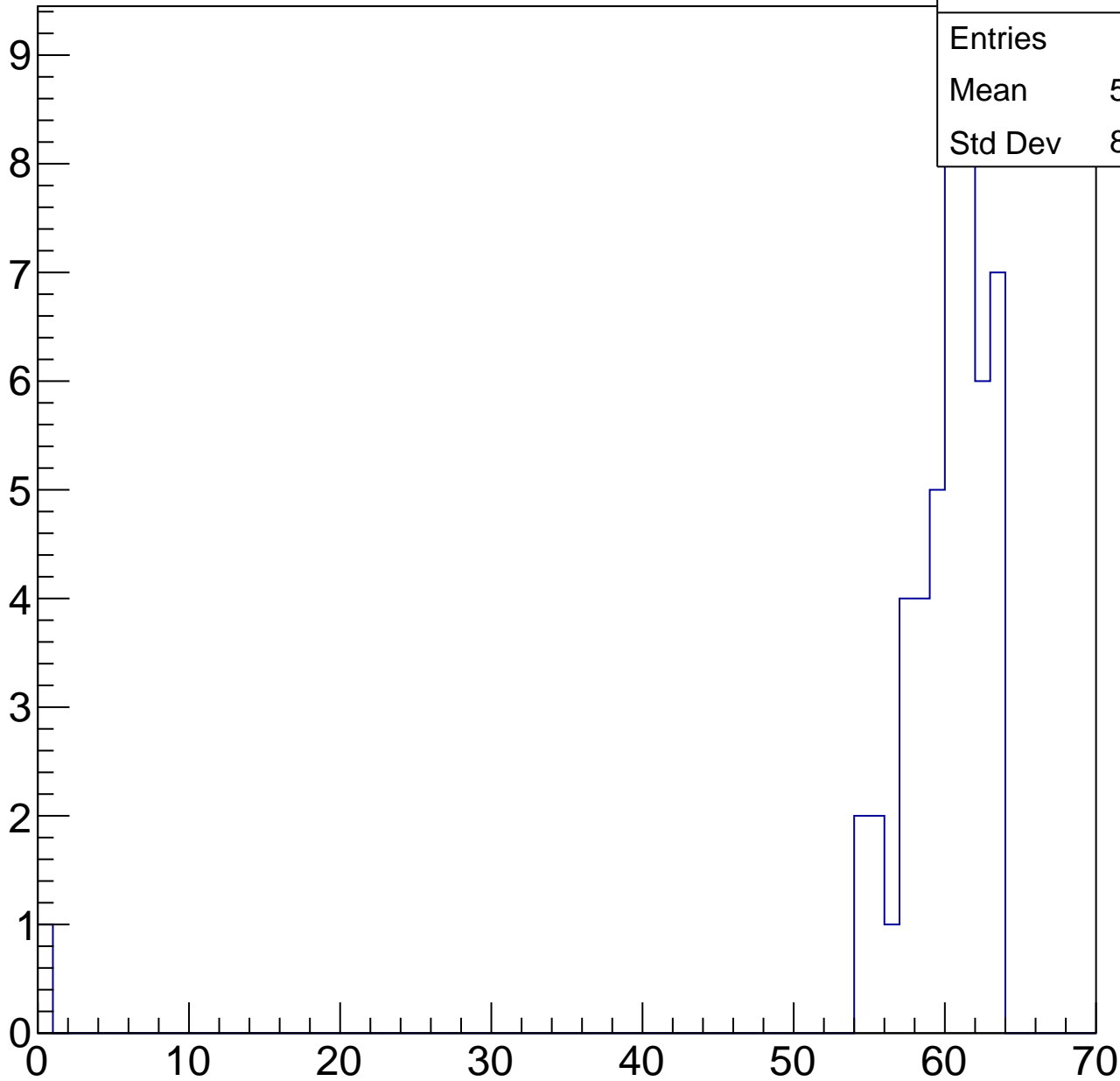
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.57
Std Dev	8.795

ampl



B0L002S, U5-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch81, adc0

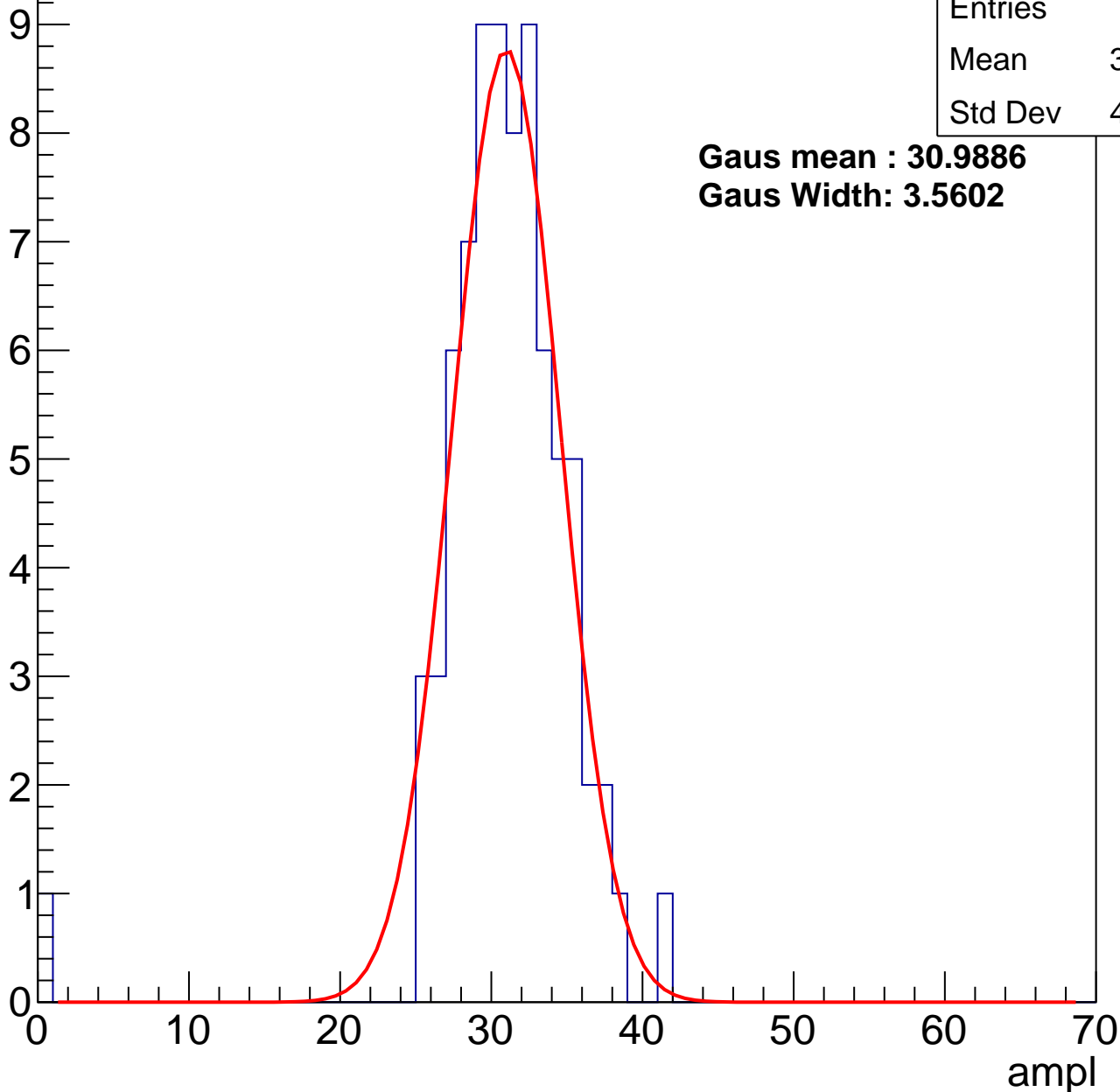
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.47
Std Dev	4.774

Gaus mean : 30.9886

Gaus Width: 3.5602



B0L002S, U5-ch81, adc1

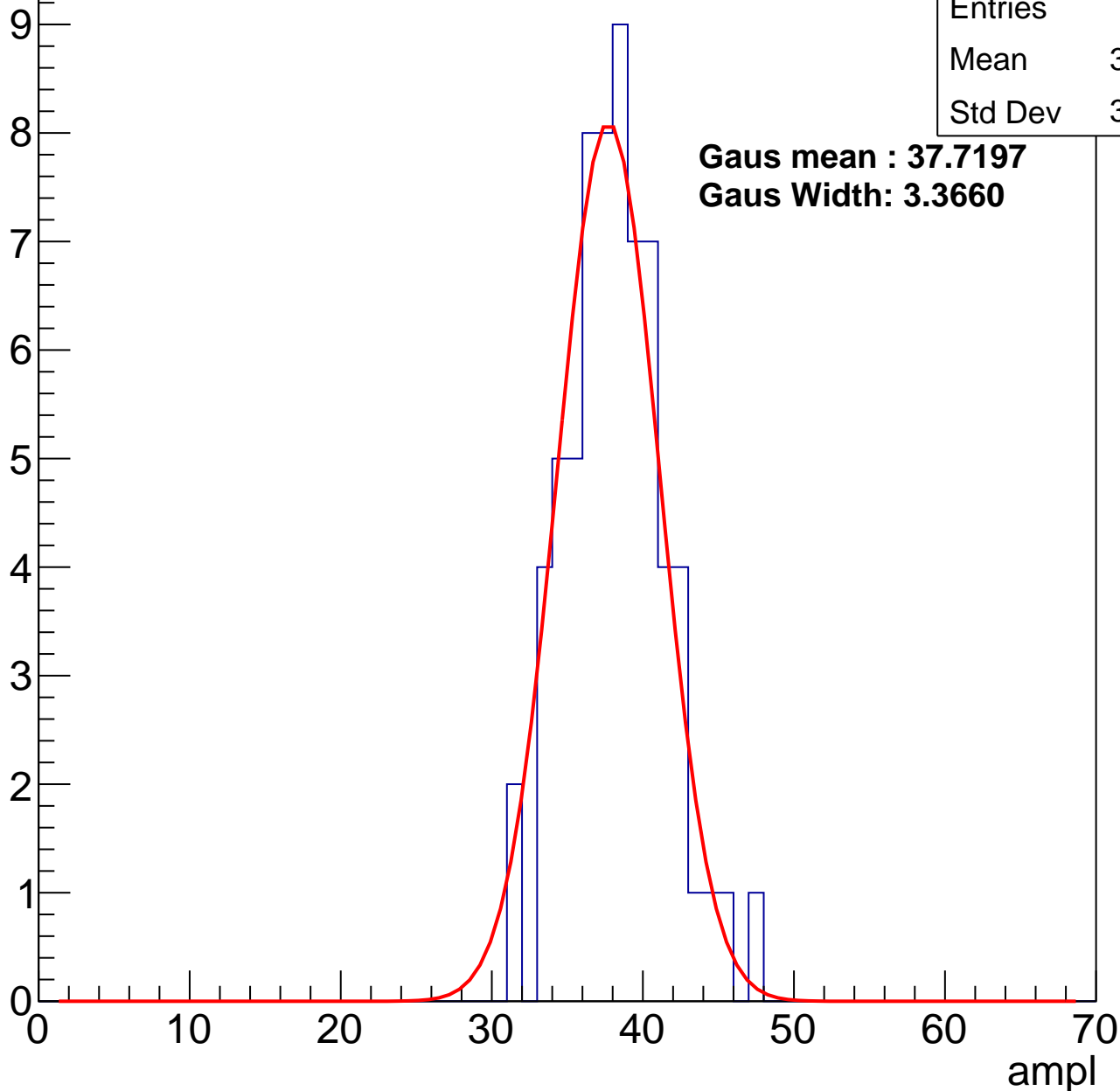
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.75
Std Dev	3.197

Gaus mean : 37.7197

Gaus Width: 3.3660



B0L002S, U5-ch81, adc2

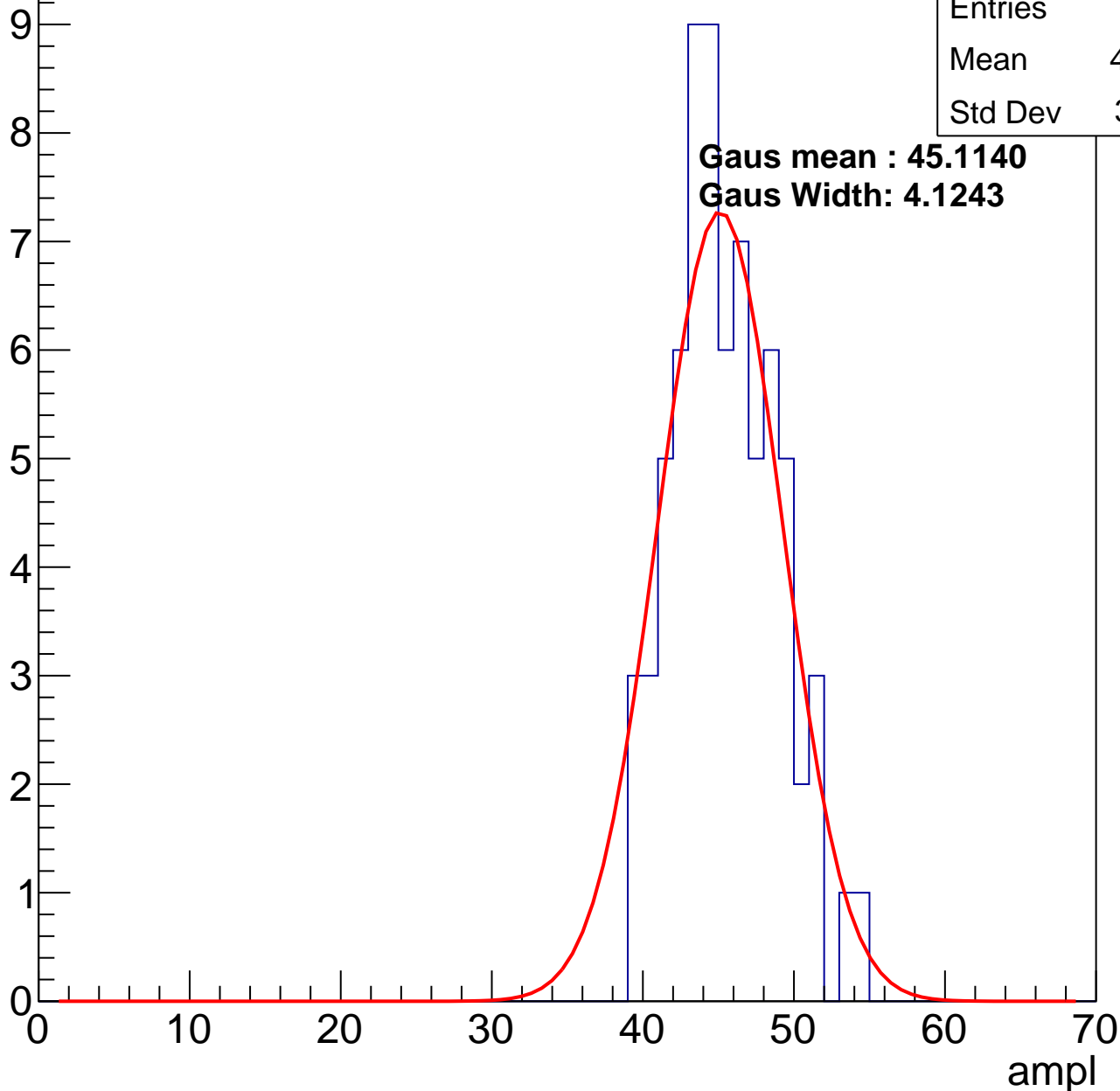
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	45.03
Std Dev	3.411

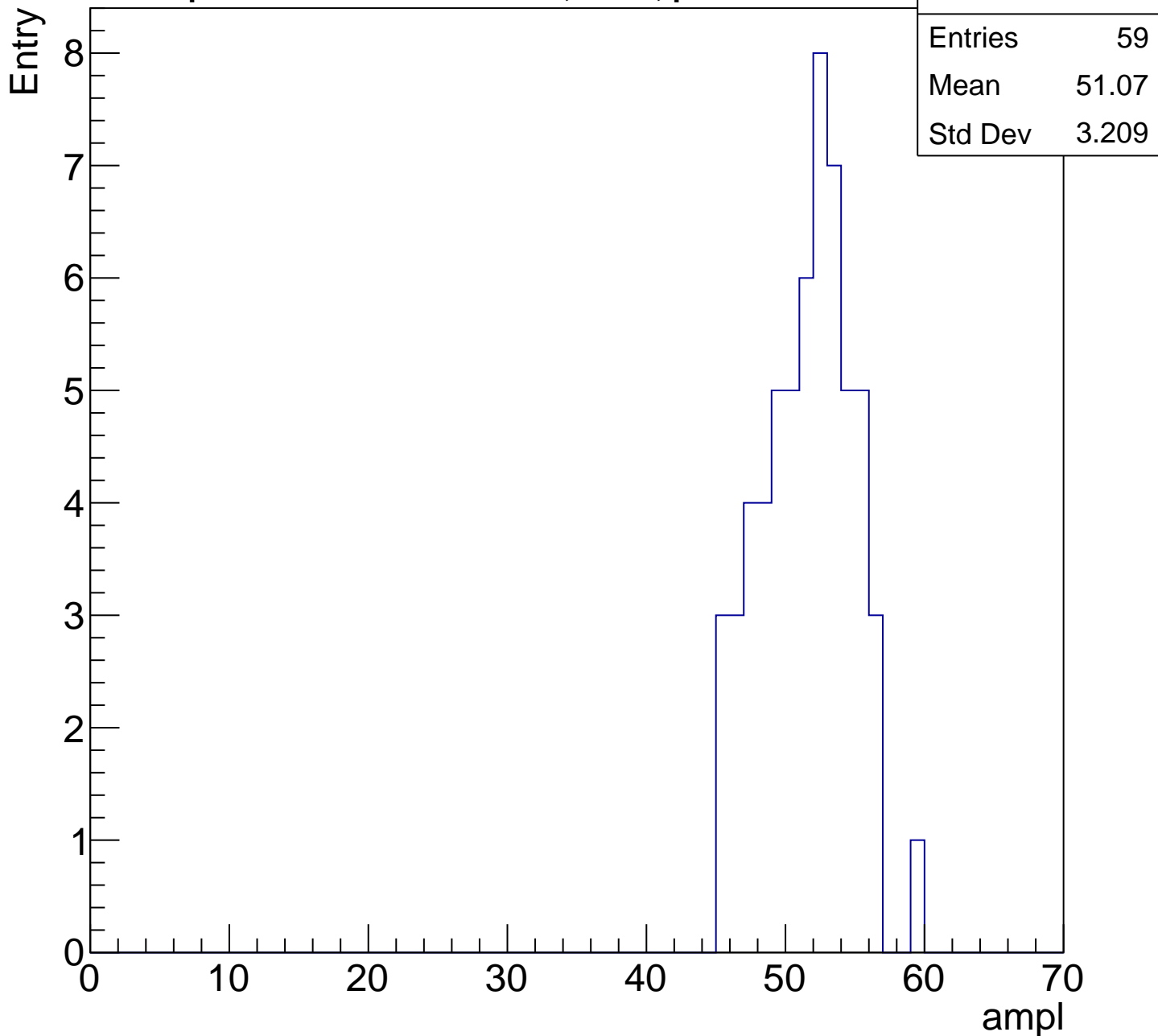
Gaus mean : 45.1140

Gaus Width: 4.1243



B0L002S, U5-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

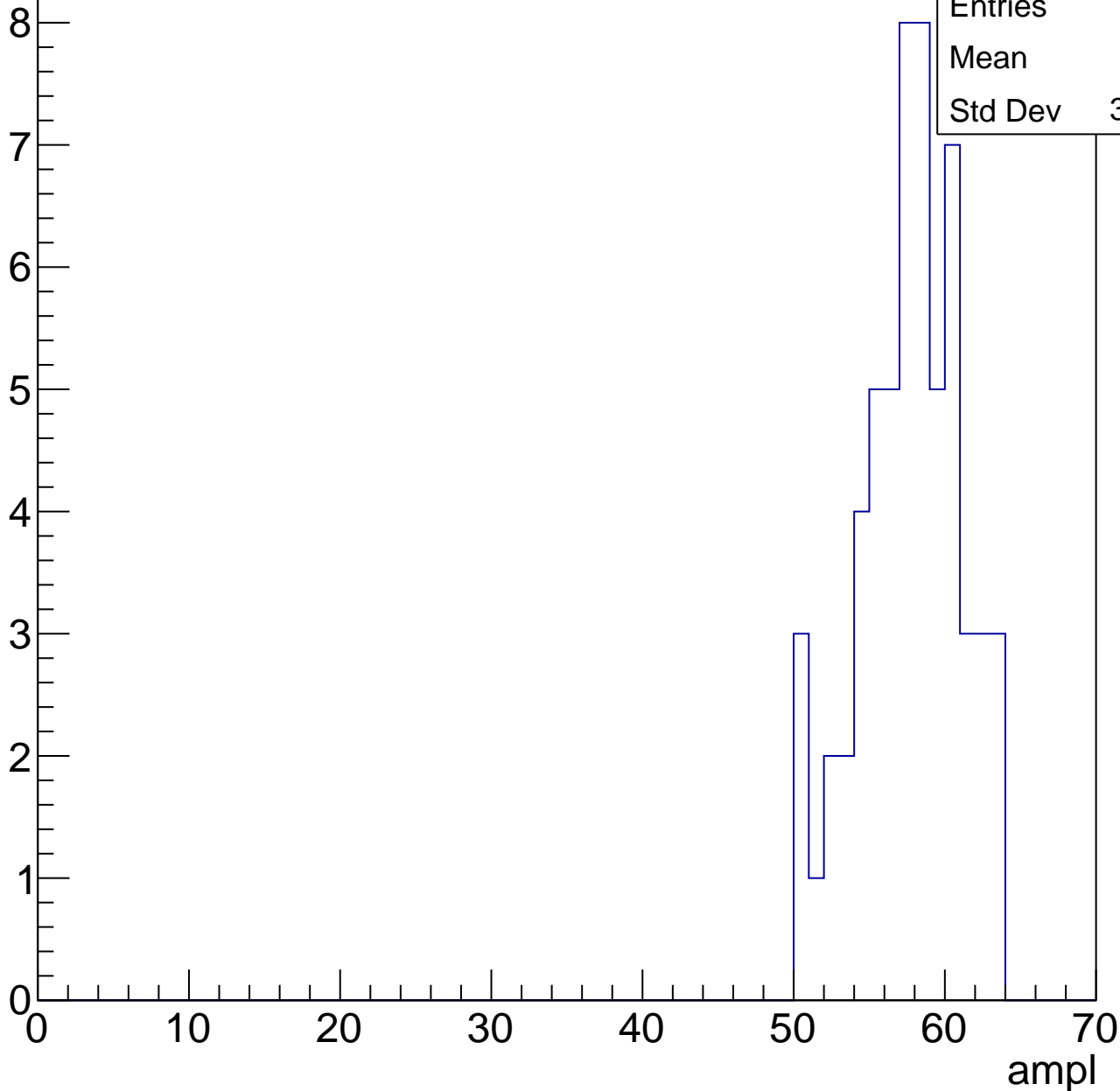


B0L002S, U5-ch81, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	57.2
Std Dev	3.308

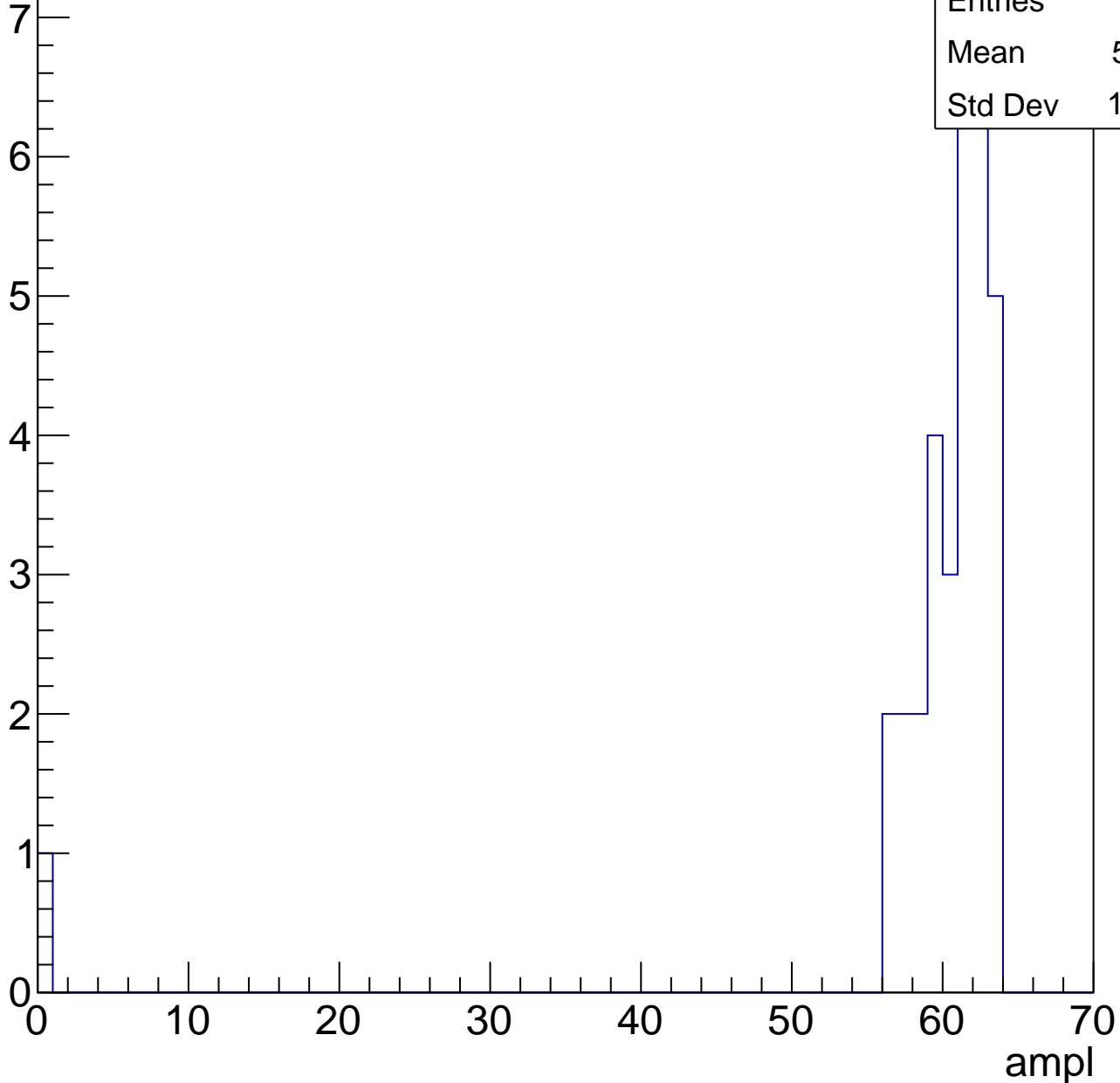


B0L002S, U5-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.61
Std Dev	10.56



B0L002S, U5-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U5-ch82, adc0

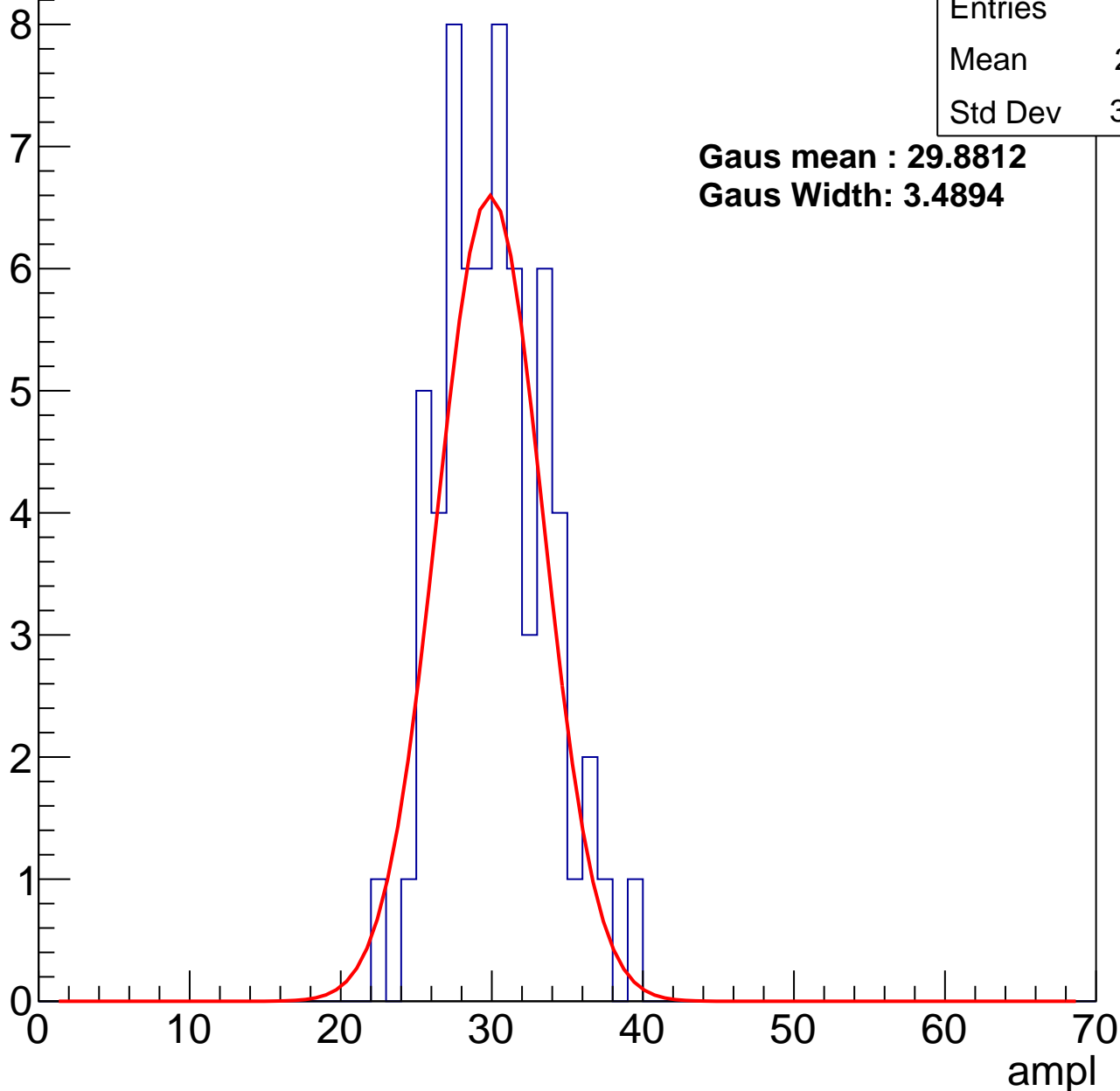
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.71
Std Dev	3.448

Gaus mean : 29.8812

Gaus Width: 3.4894



B0L002S, U5-ch82, adc1

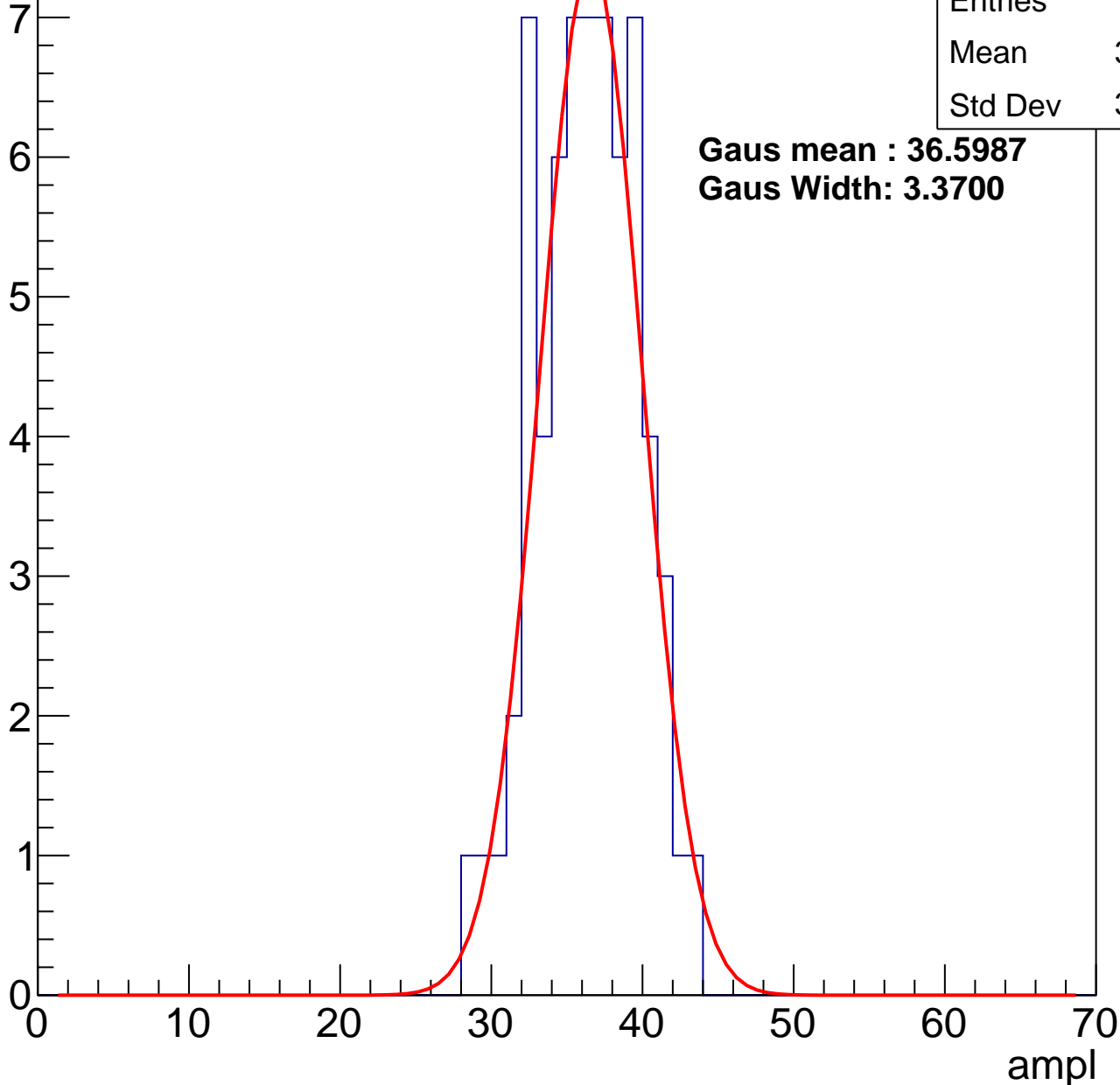
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.91
Std Dev	3.271

Gaus mean : 36.5987

Gaus Width: 3.3700



B0L002S, U5-ch82, adc2

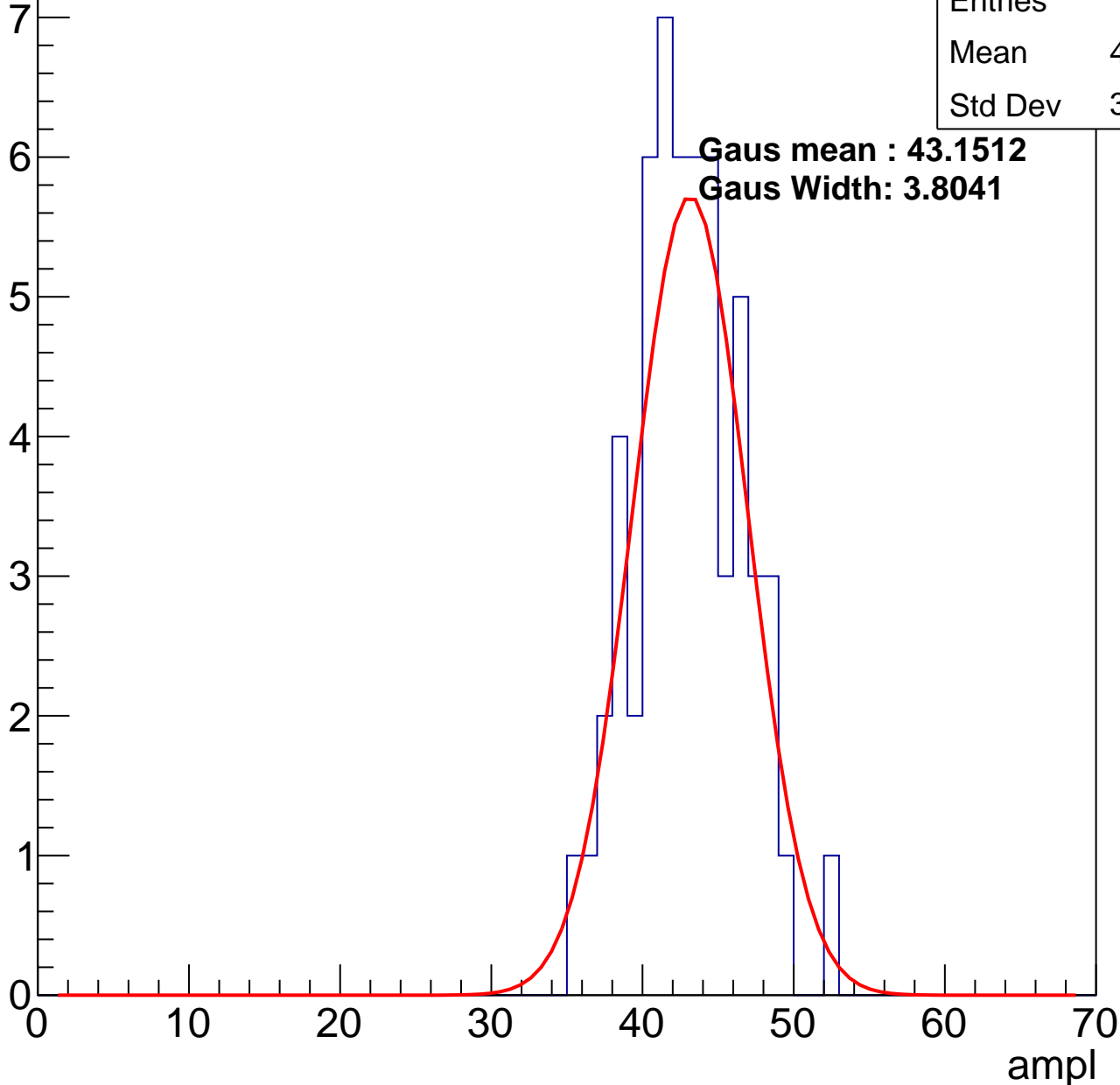
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.58
Std Dev	3.499

Gaus mean : 43.1512

Gaus Width: 3.8041



B0L002S, U5-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	49.43
Std Dev	3.842

Entry

10
8
6
4
2
0

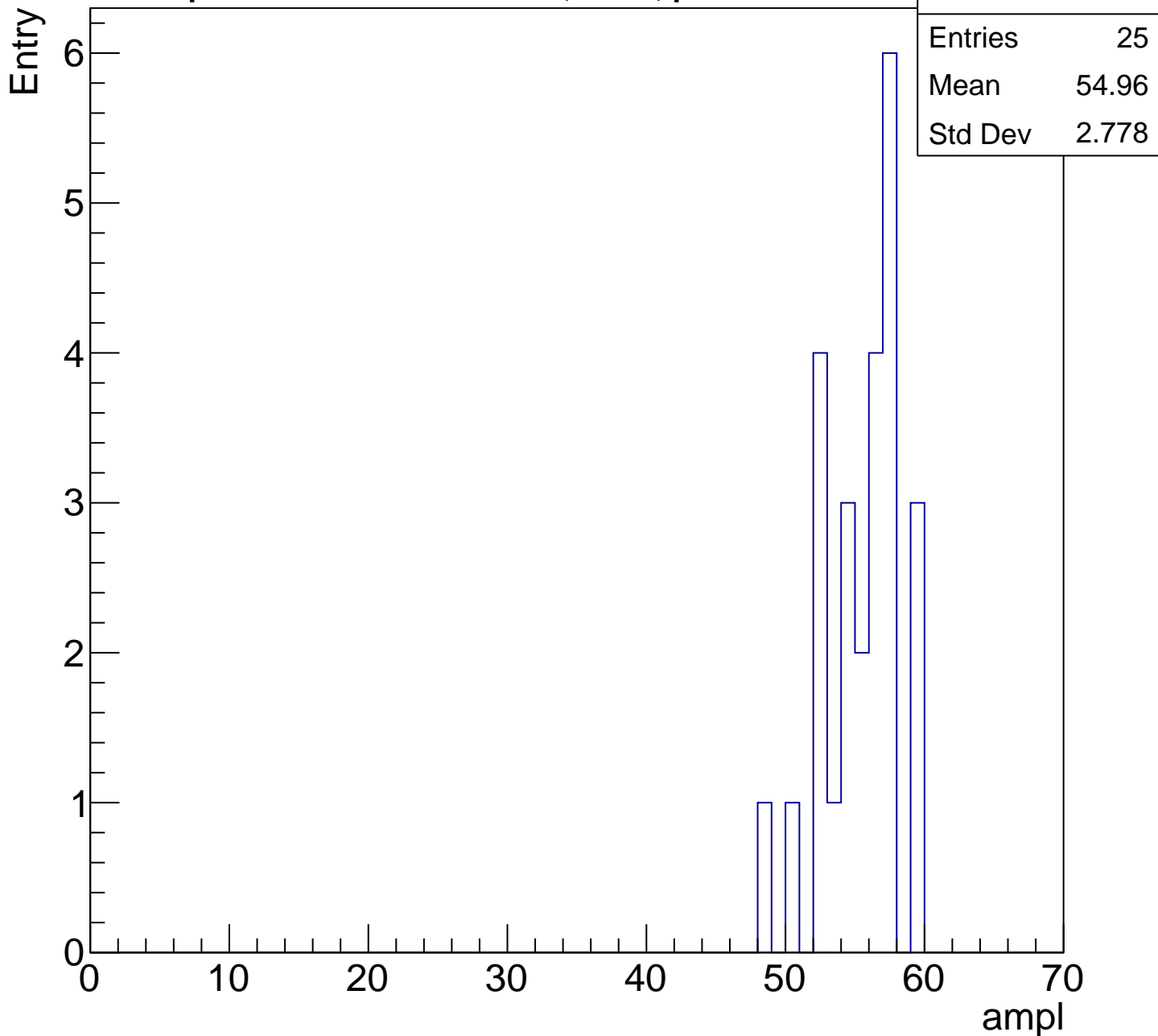
0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch82, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	58.33
Std Dev	7.971

Entry

10

8

6

4

2

0

0

10

20

30

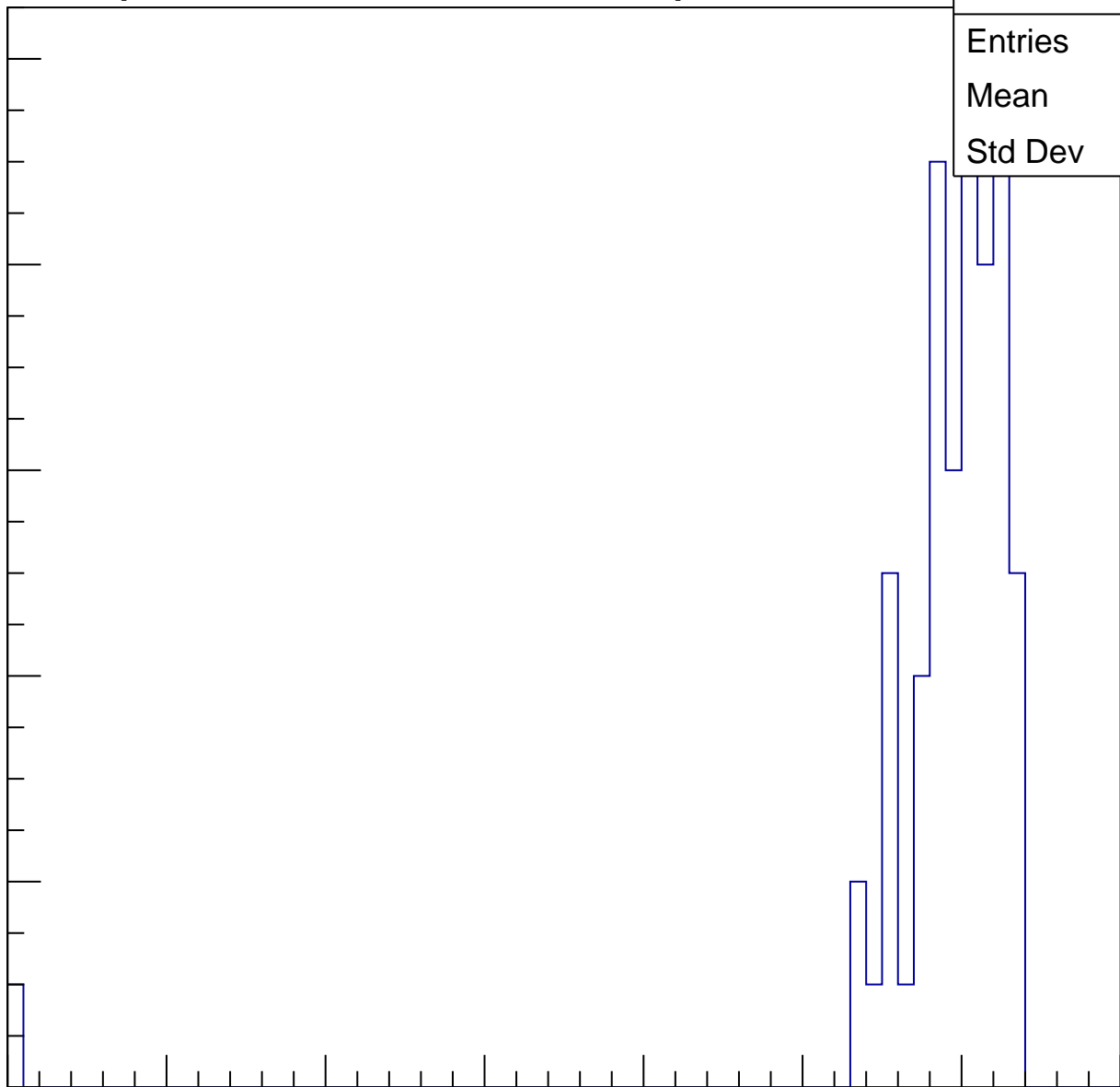
40

50

60

70

ampl

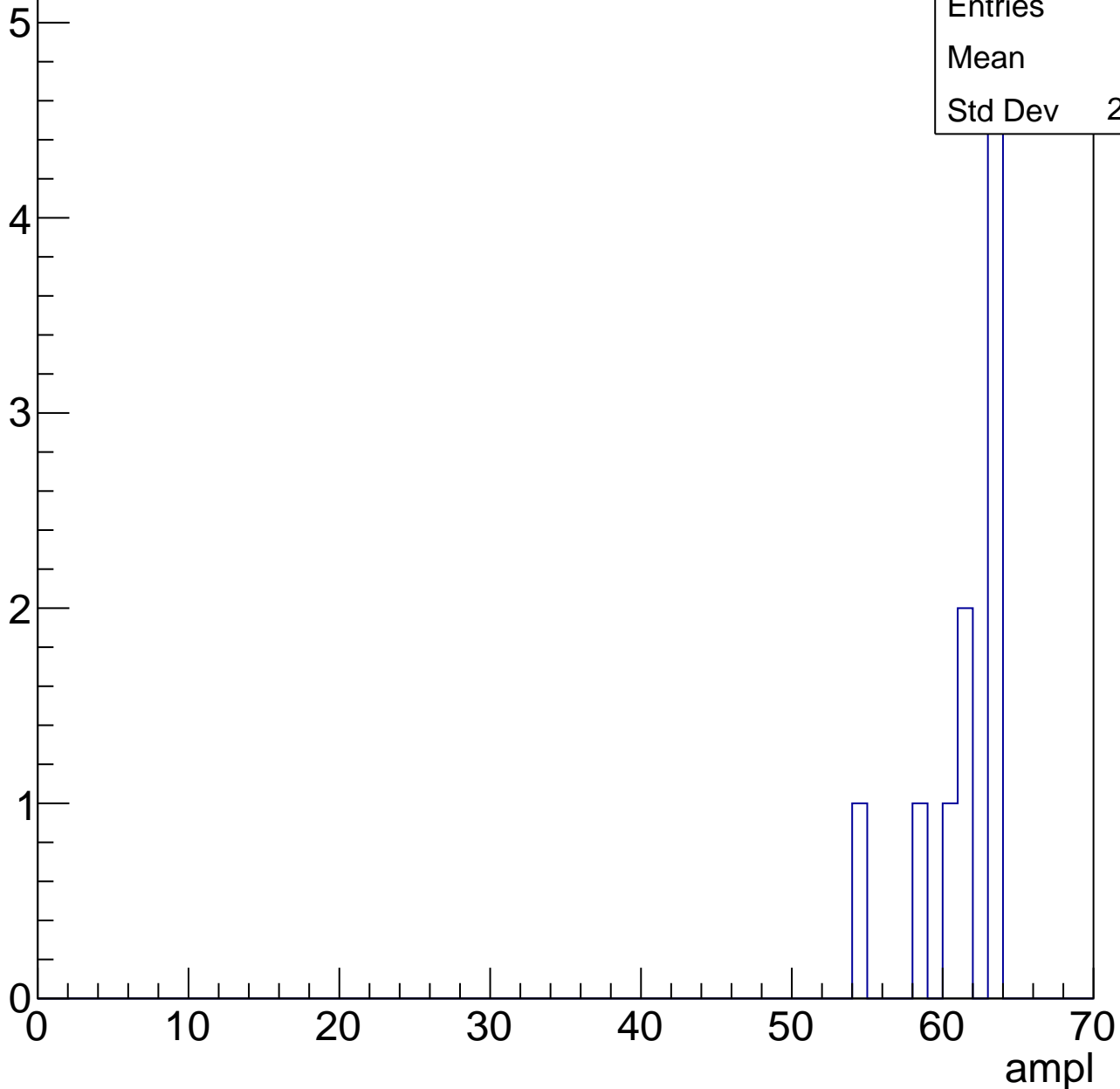


B0L002S, U5-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	60.9
Std Dev	2.809



B0L002S, U5-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L002S, U5-ch83, adc0

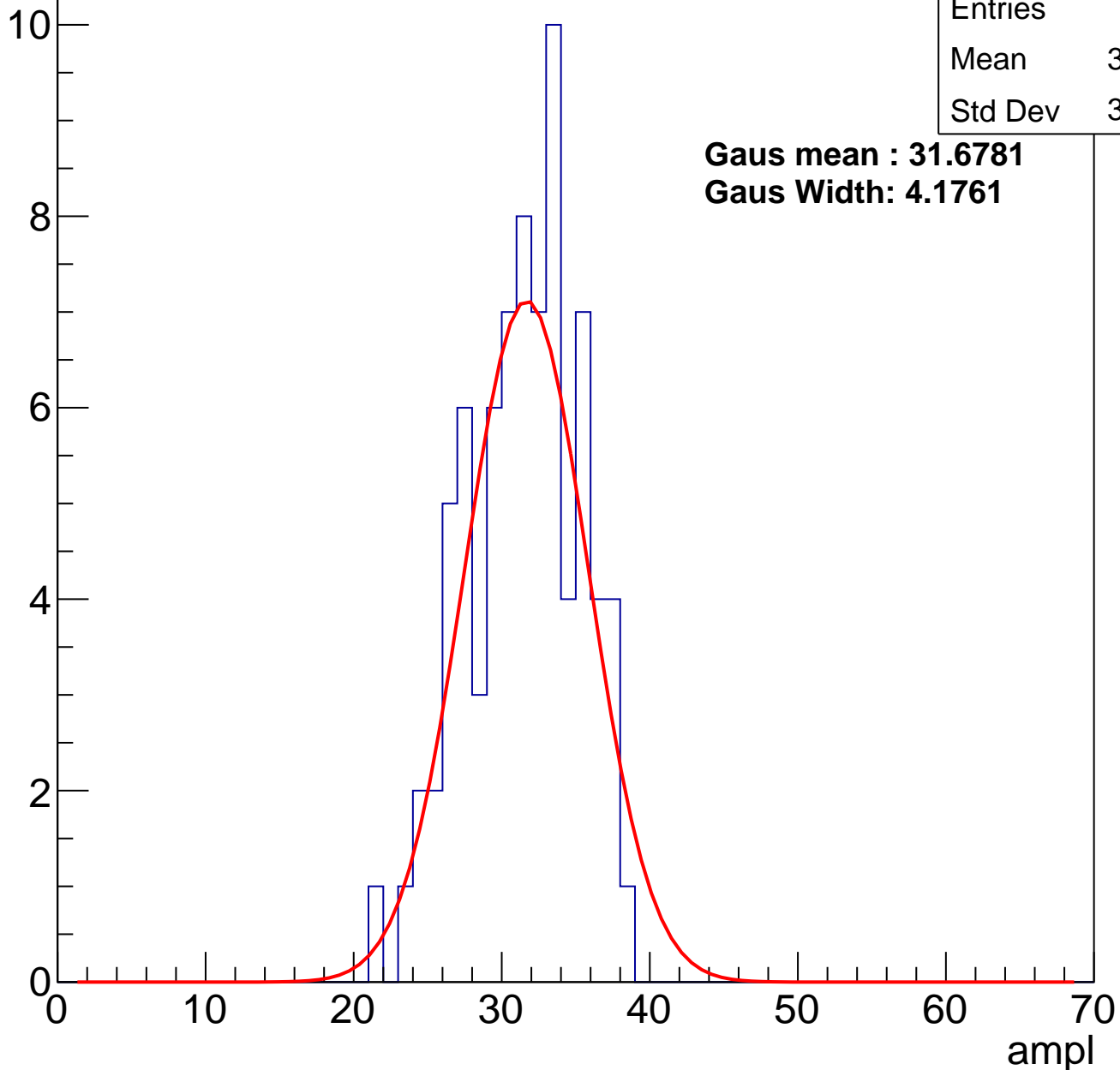
calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	30.96
Std Dev	3.757

Gaus mean : 31.6781

Gaus Width: 4.1761

Entry



B0L002S, U5-ch83, adc1

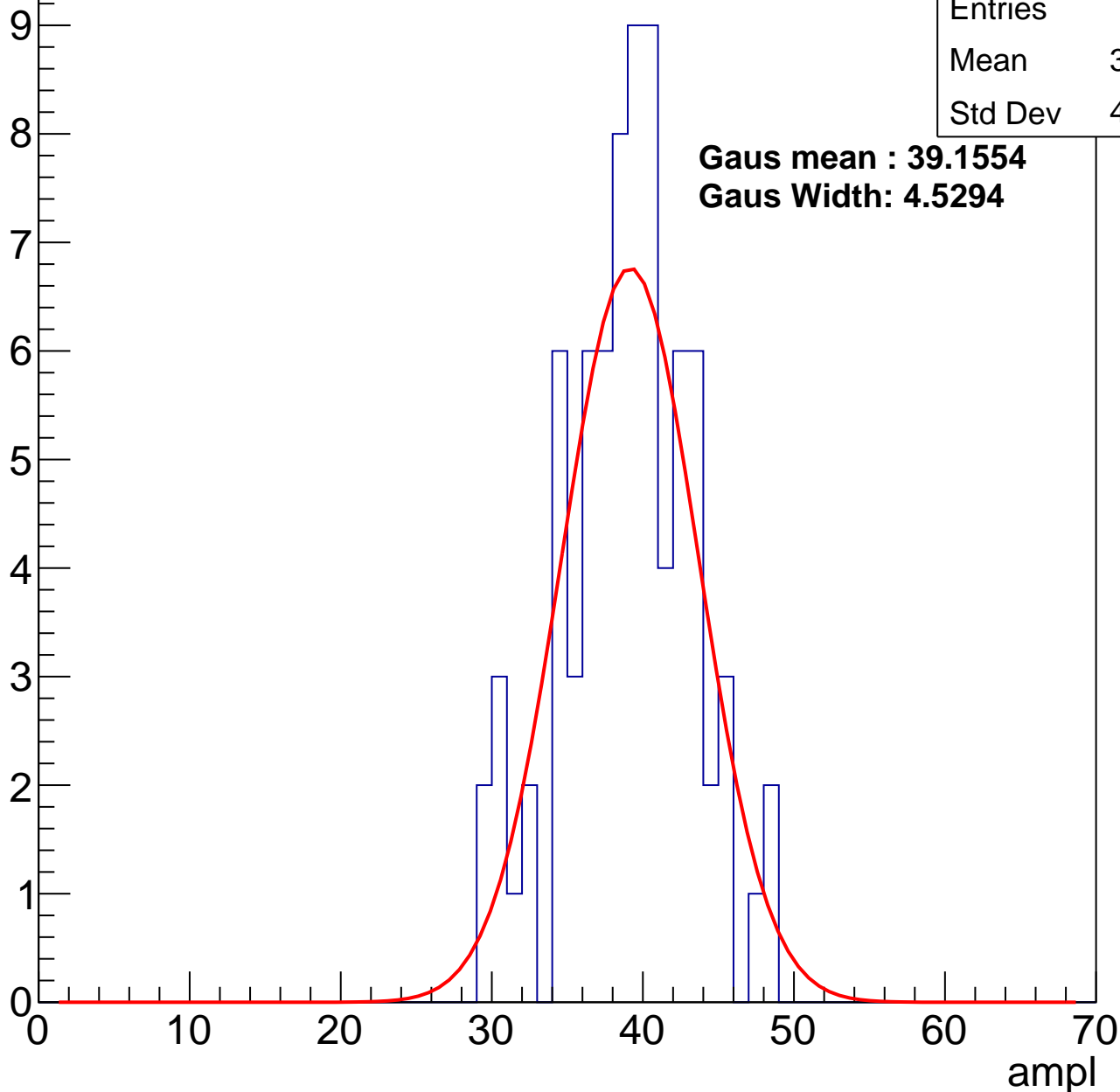
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	38.54
Std Dev	4.257

Gaus mean : 39.1554

Gaus Width: 4.5294



B0L002S, U5-ch83, adc2

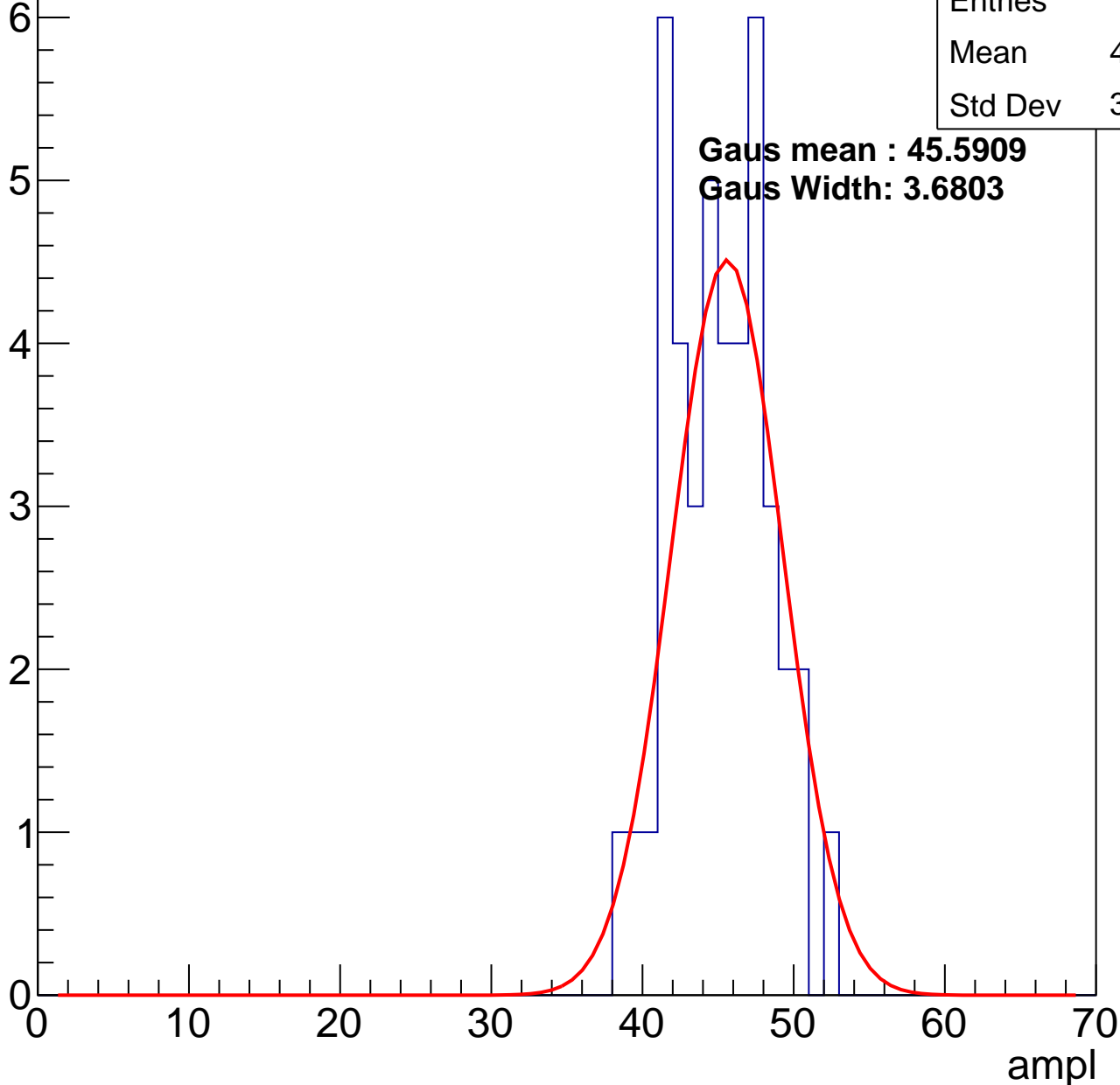
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	44.65
Std Dev	3.198

Gaus mean : 45.5909

Gaus Width: 3.6803

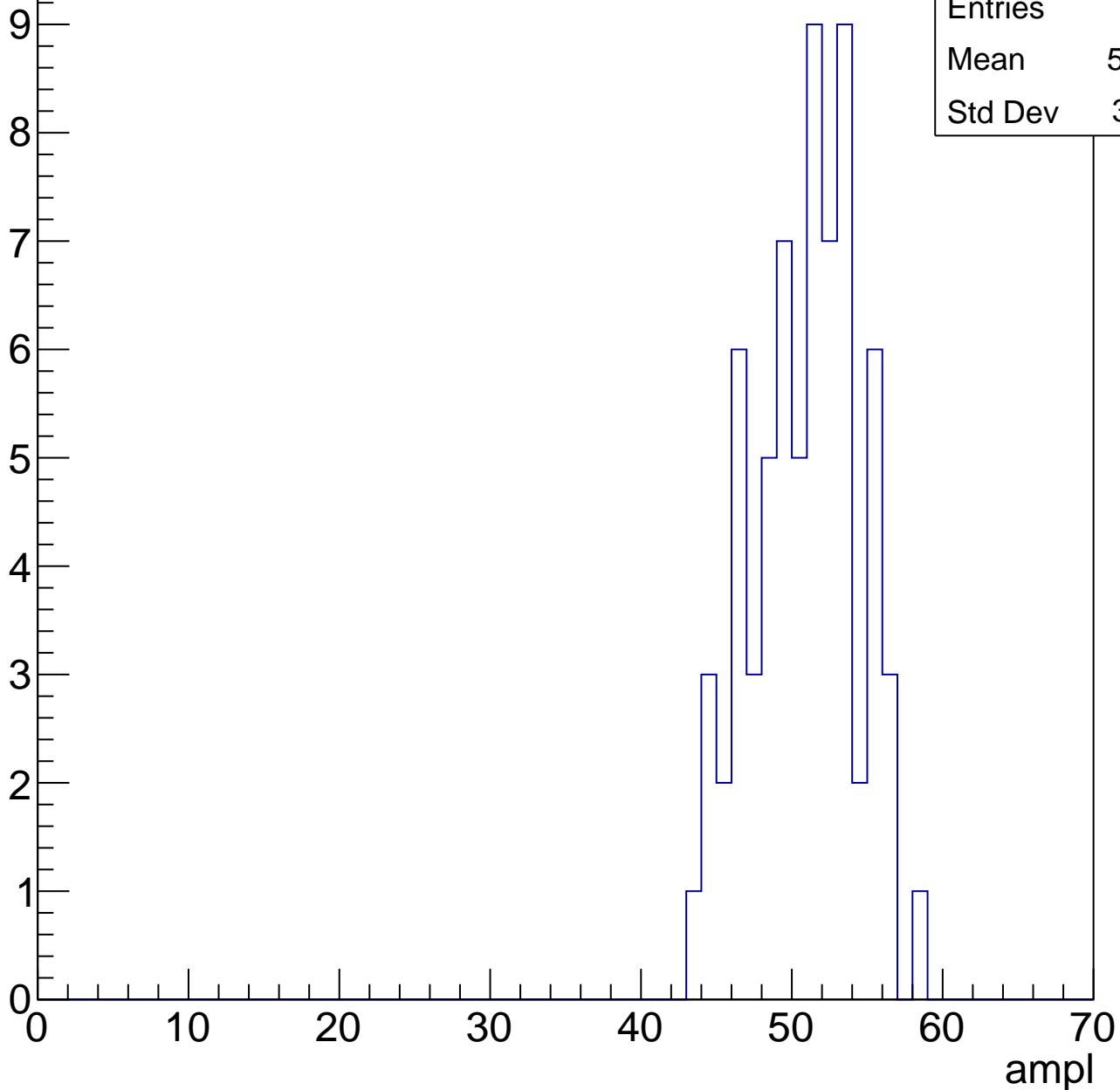


B0L002S, U5-ch83, adc3

calib_packv5_042523_0143.root, FC#8, port C1

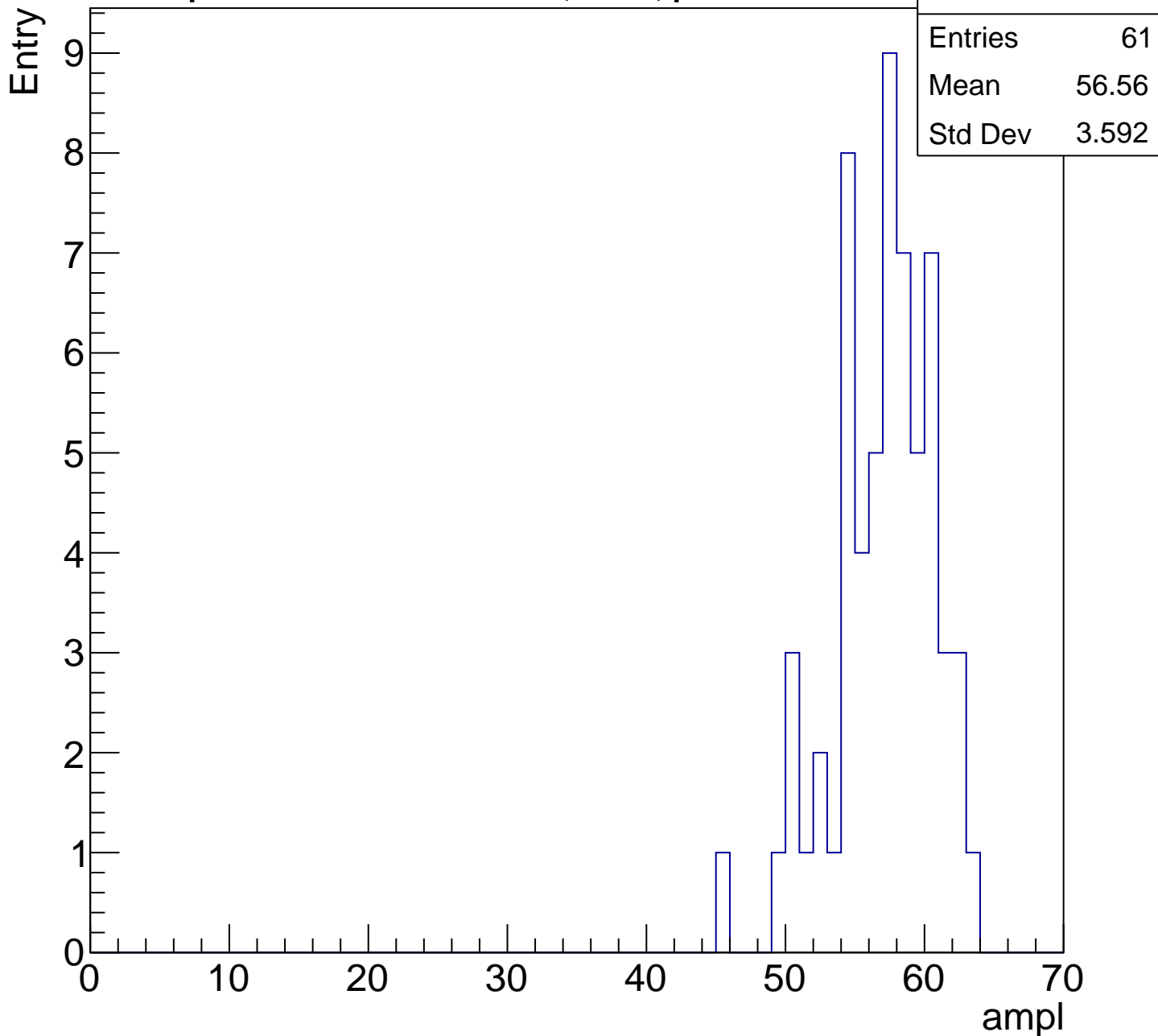
Entry

Entries	69
Mean	50.42
Std Dev	3.441



B0L002S, U5-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

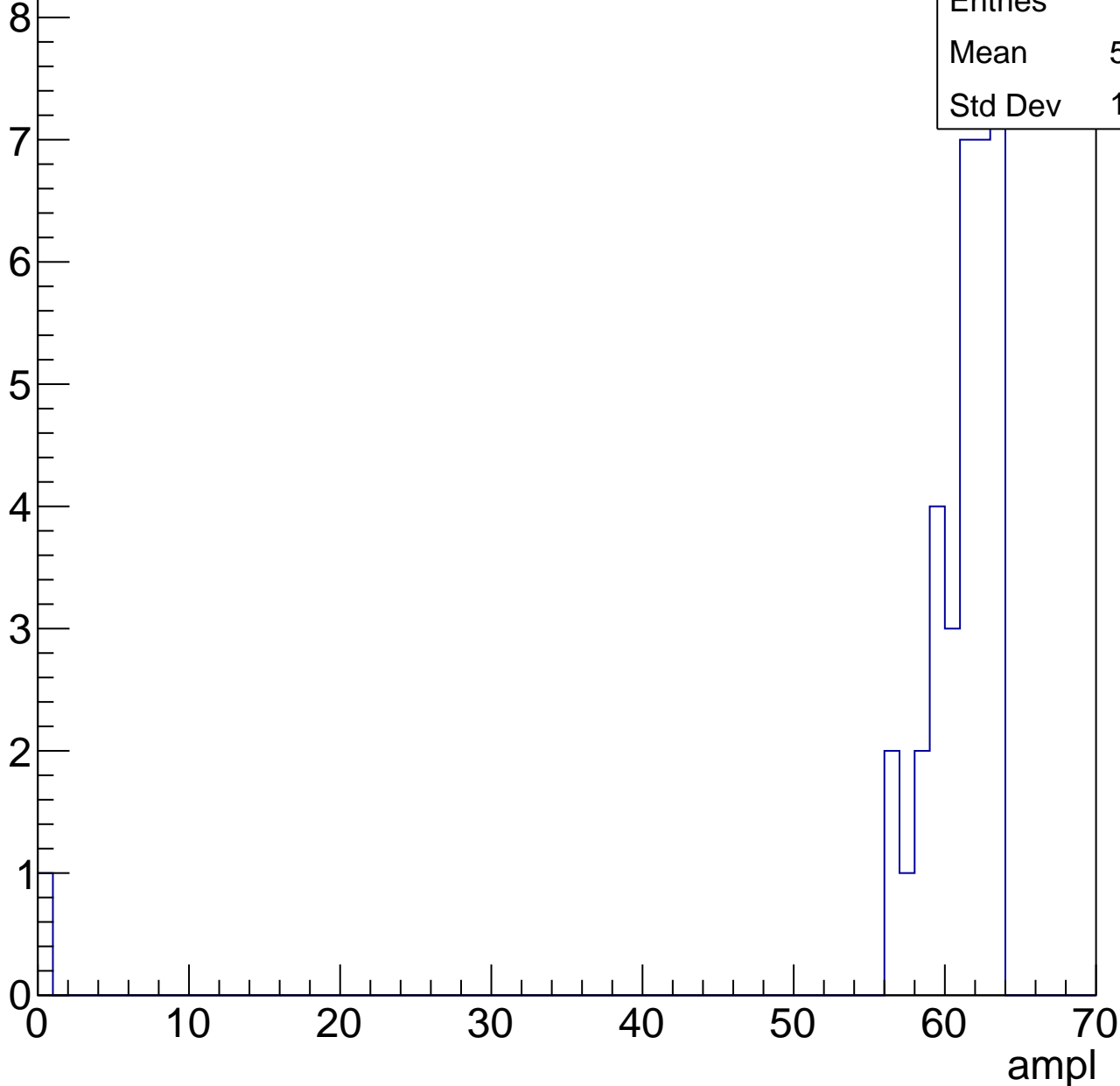


B0L002S, U5-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

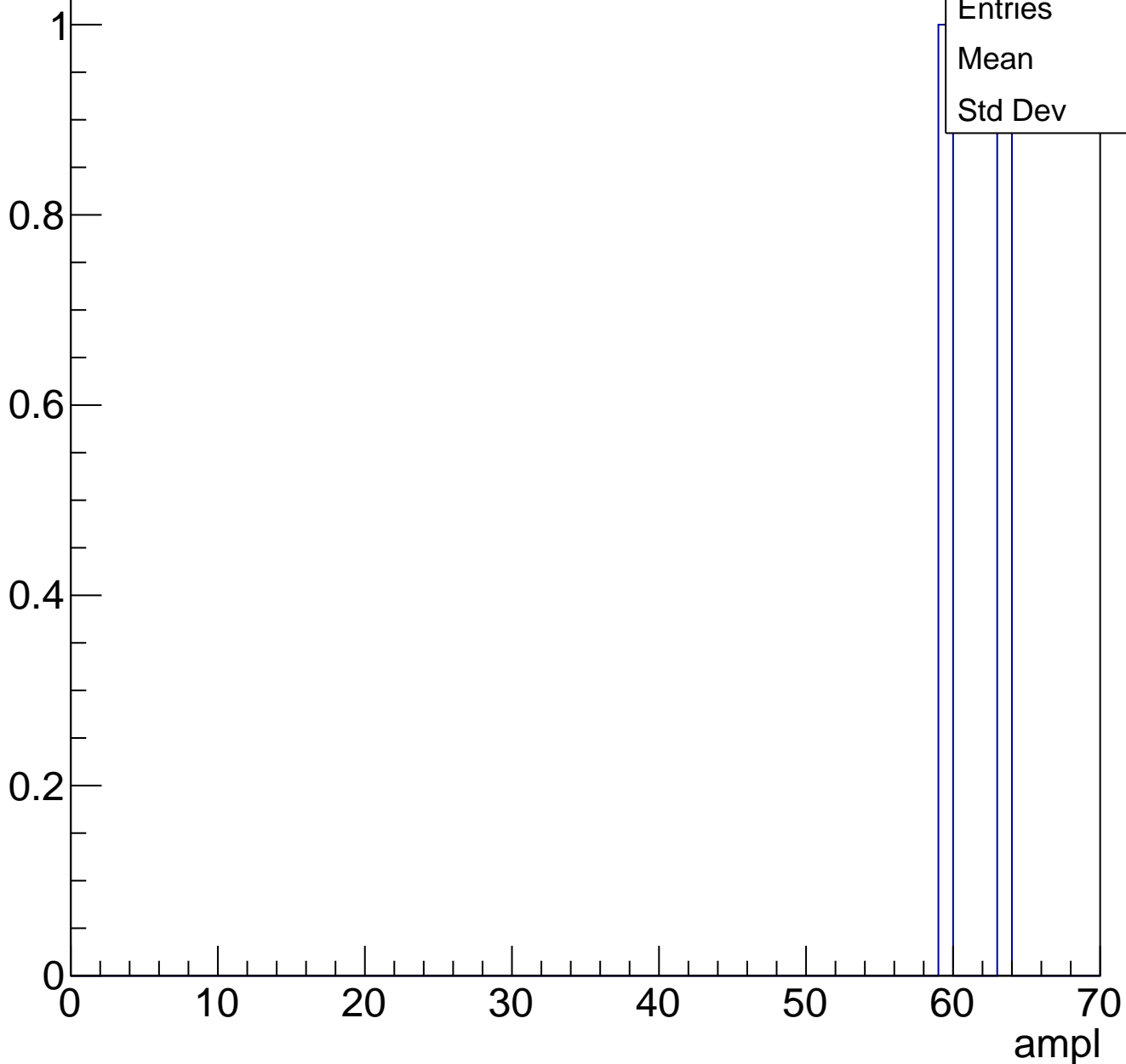
Entries	35
Mean	59.03
Std Dev	10.32



B0L002S, U5-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch84, adc0

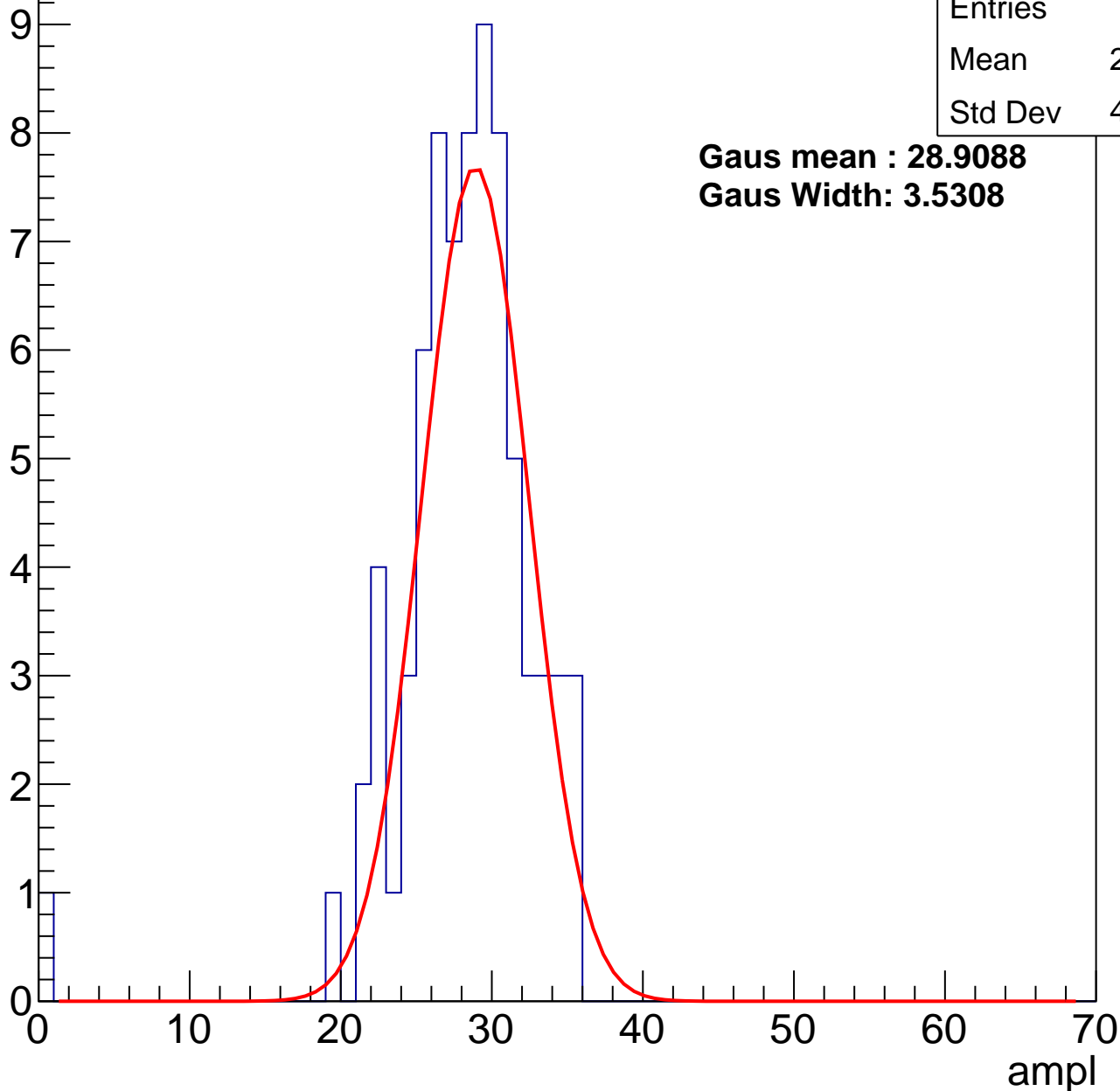
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	27.64
Std Dev	4.799

Gaus mean : 28.9088

Gaus Width: 3.5308



B0L002S, U5-ch84, adc1

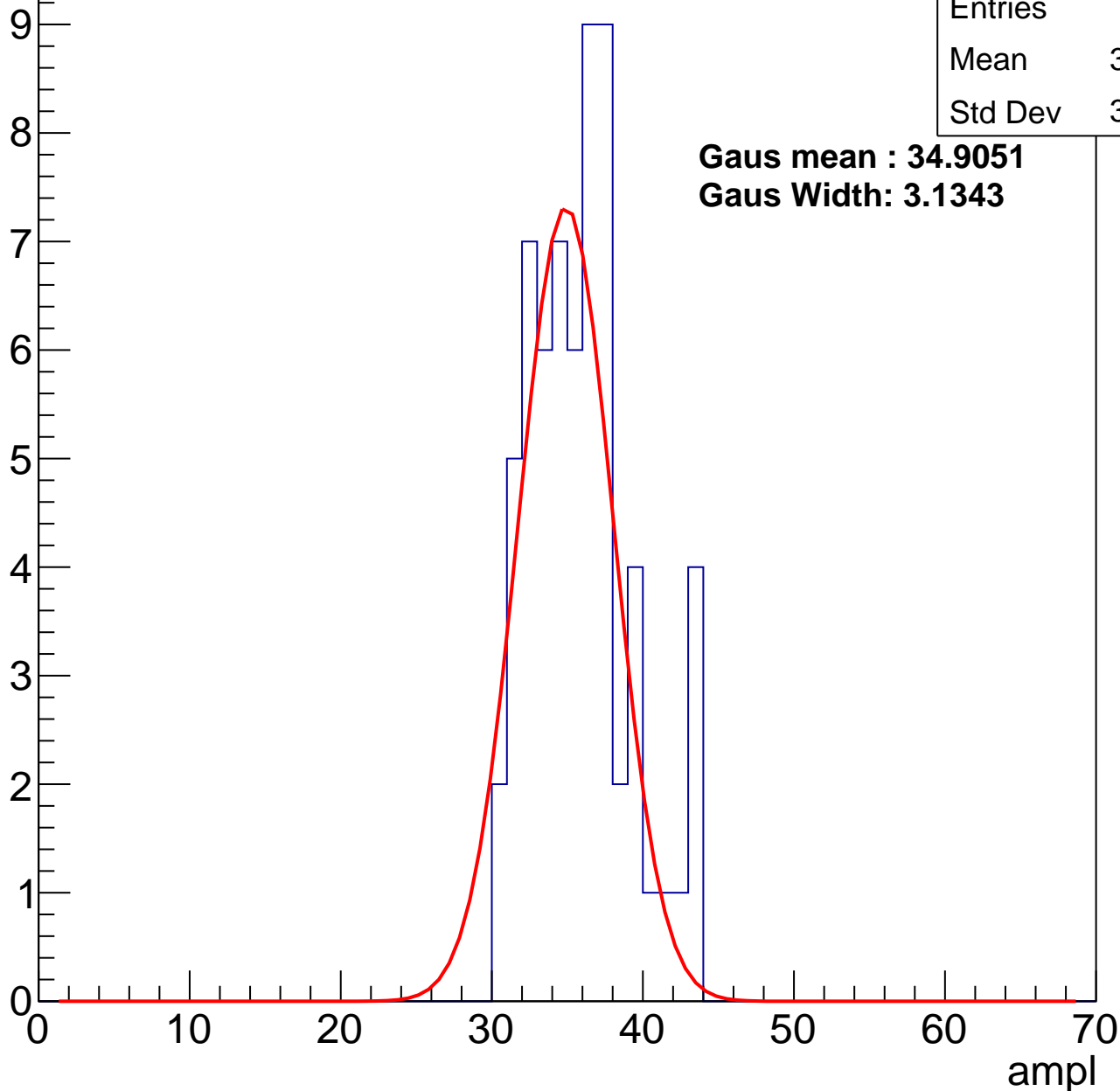
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	35.45
Std Dev	3.316

Gaus mean : 34.9051

Gaus Width: 3.1343



B0L002S, U5-ch84, adc2

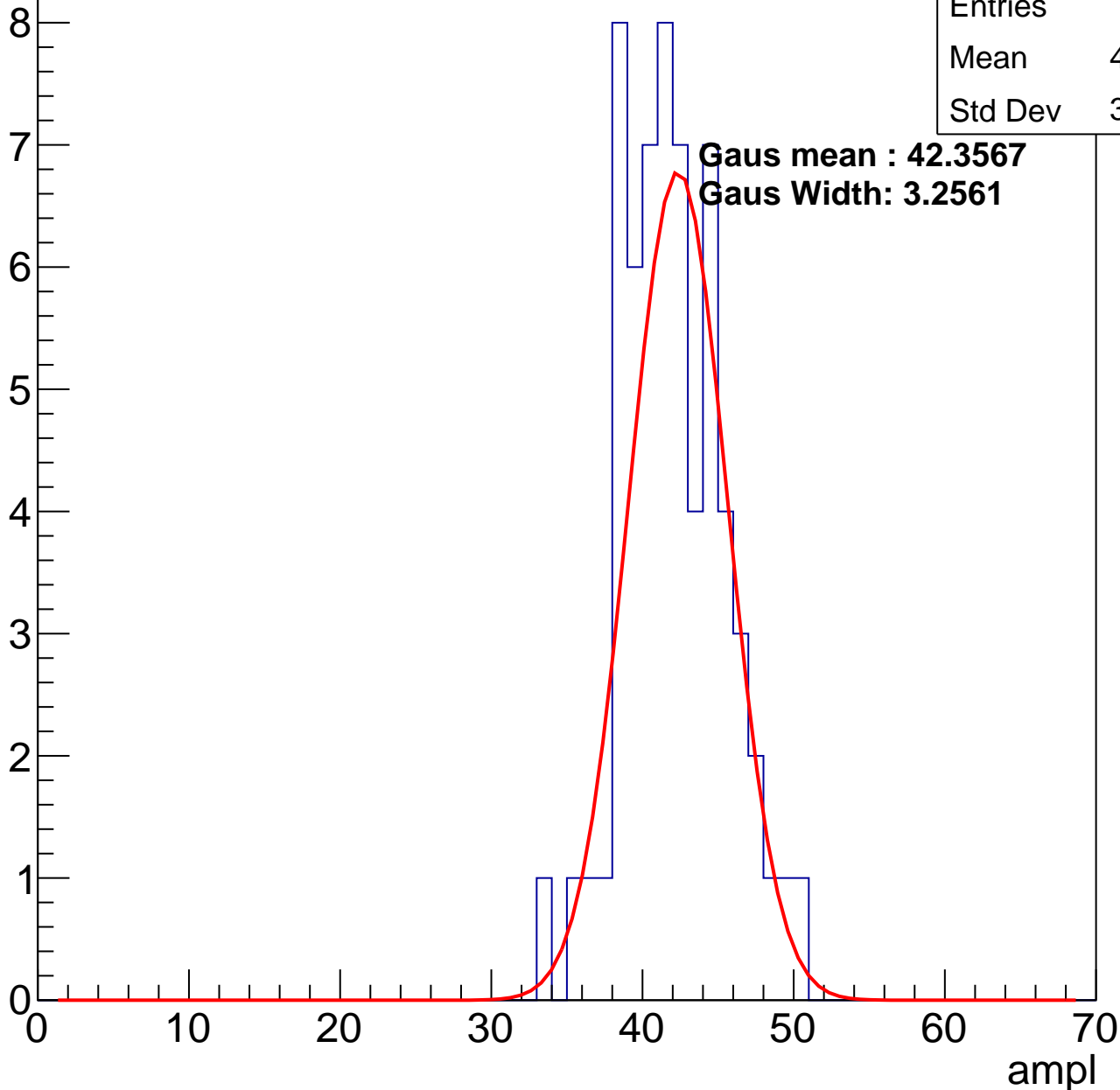
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	41.59
Std Dev	3.365

Gaus mean : 42.3567

Gaus Width: 3.2561

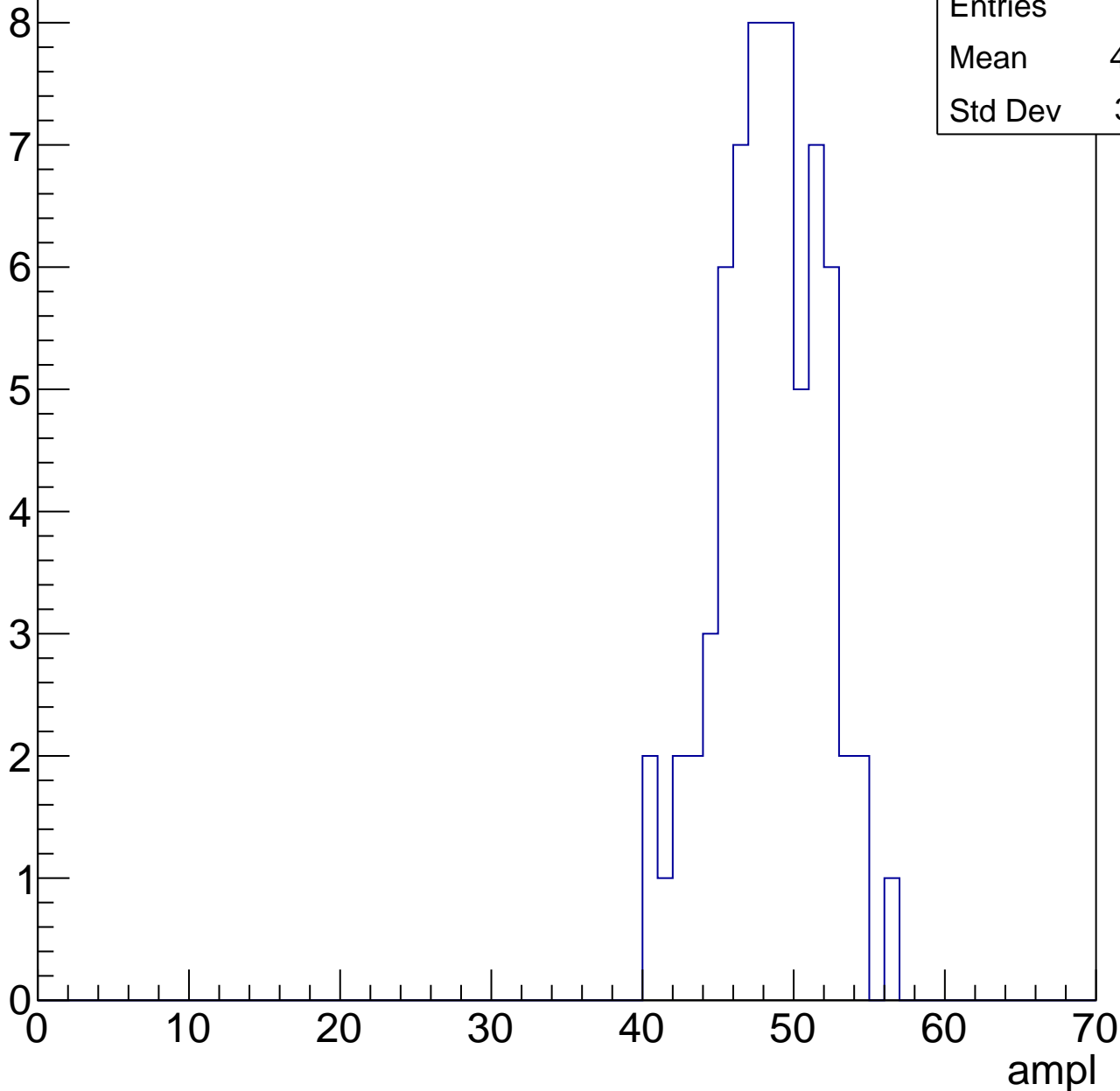


B0L002S, U5-ch84, adc3

calib_packv5_042523_0143.root, FC#8, port C1

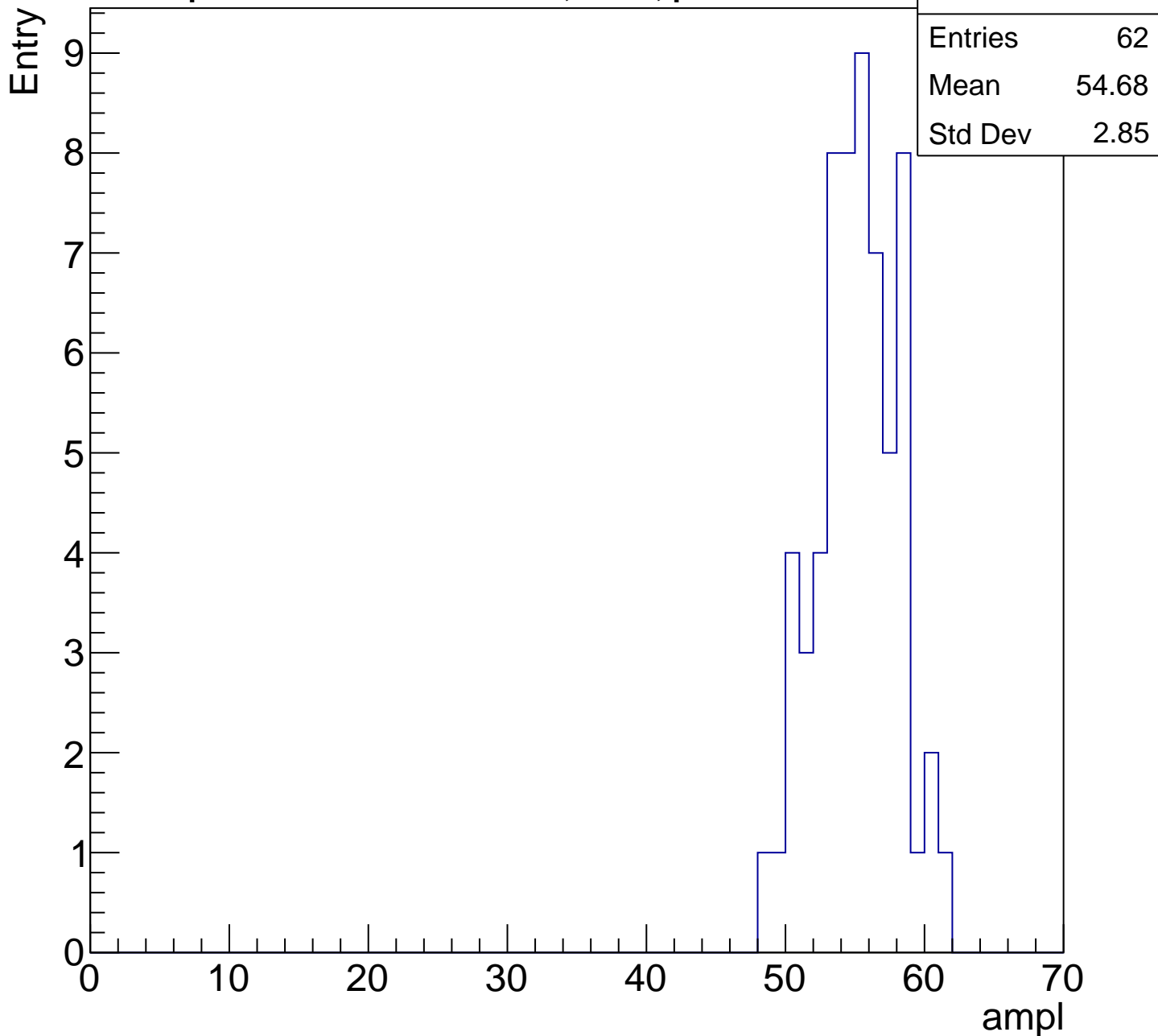
Entry

Entries	70
Mean	47.94
Std Dev	3.401



B0L002S, U5-ch84, adc4

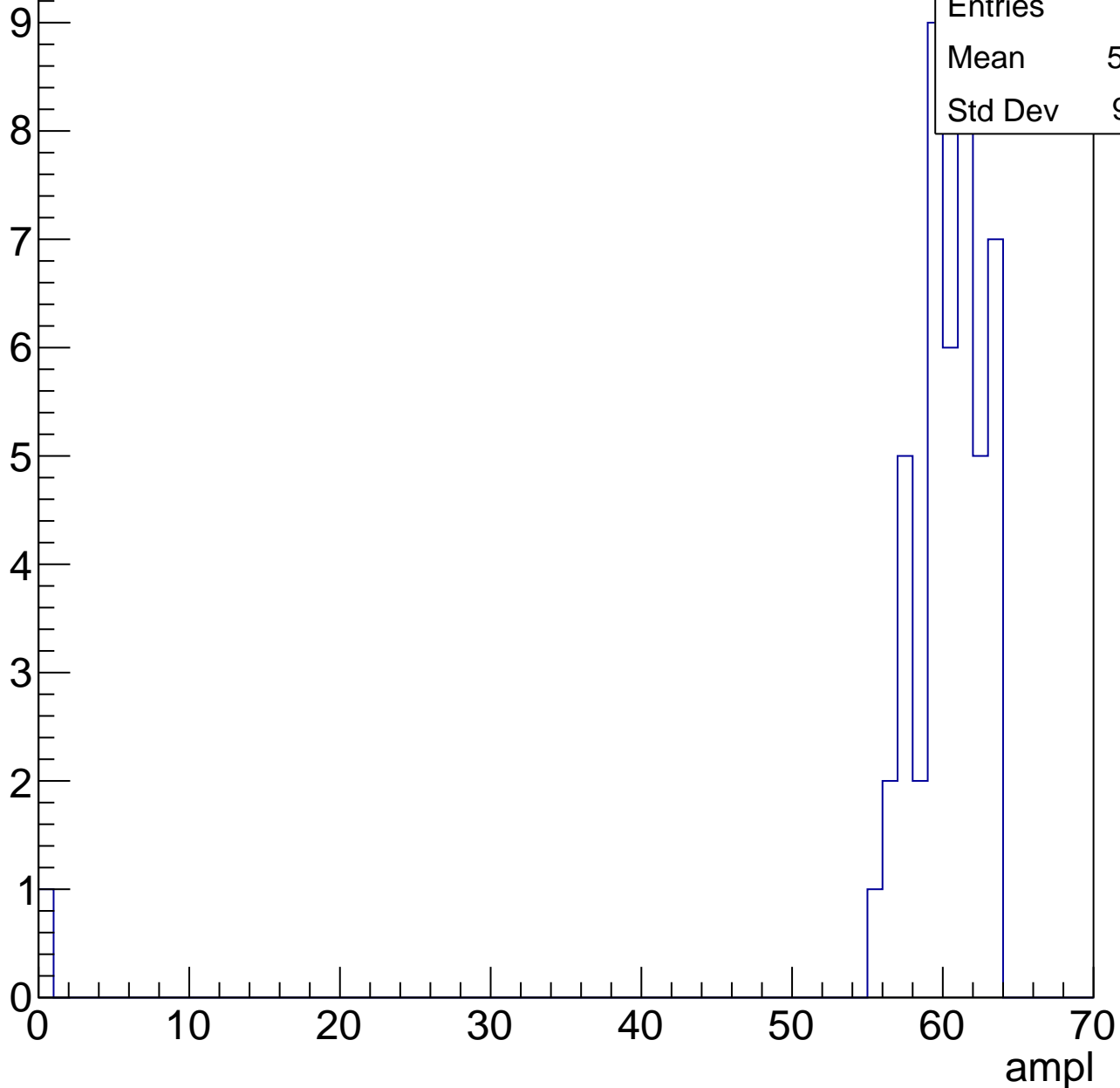
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

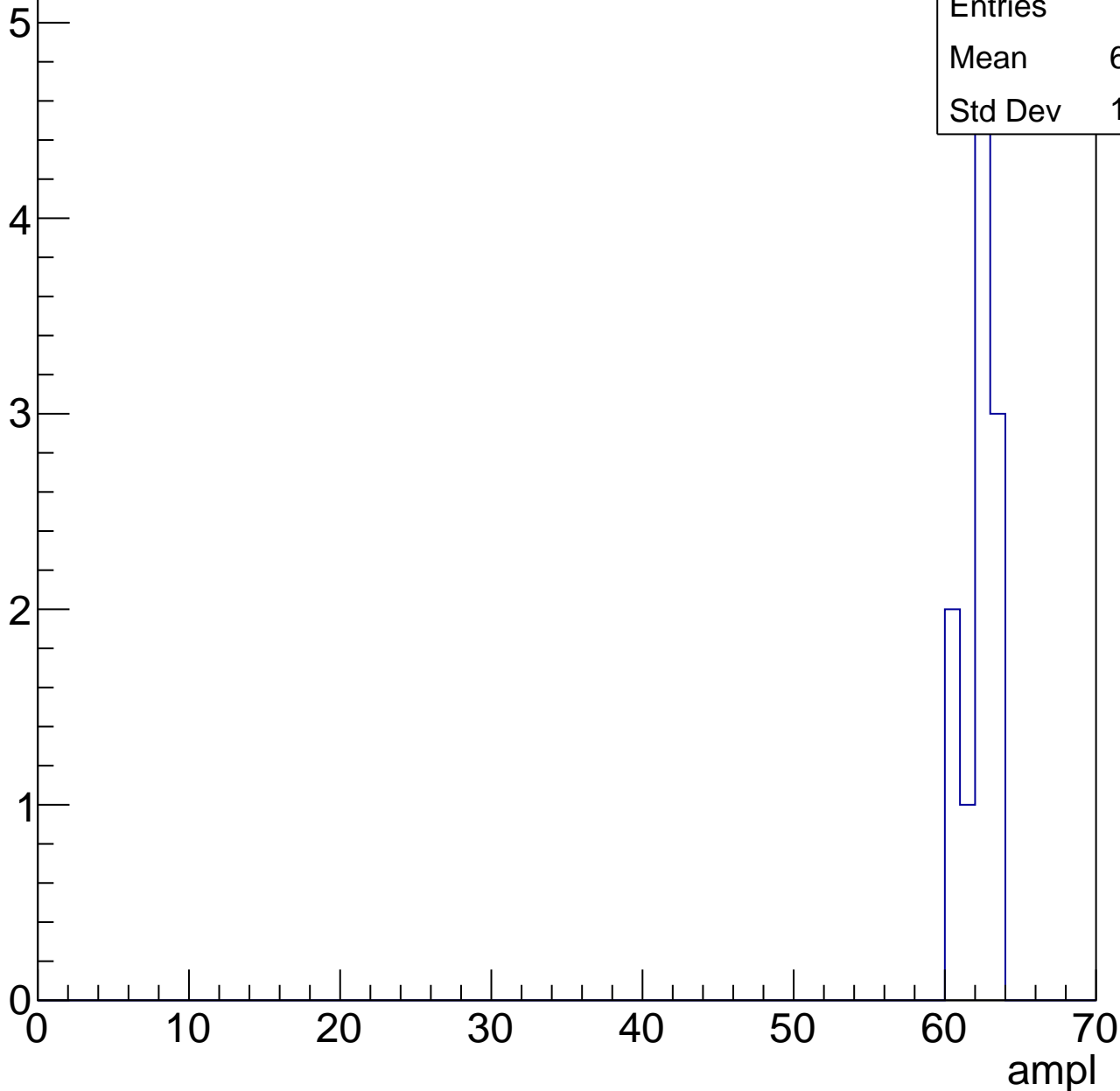


B0L002S, U5-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	11
Mean	61.82
Std Dev	1.029



B0L002S, U5-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch85, adc0

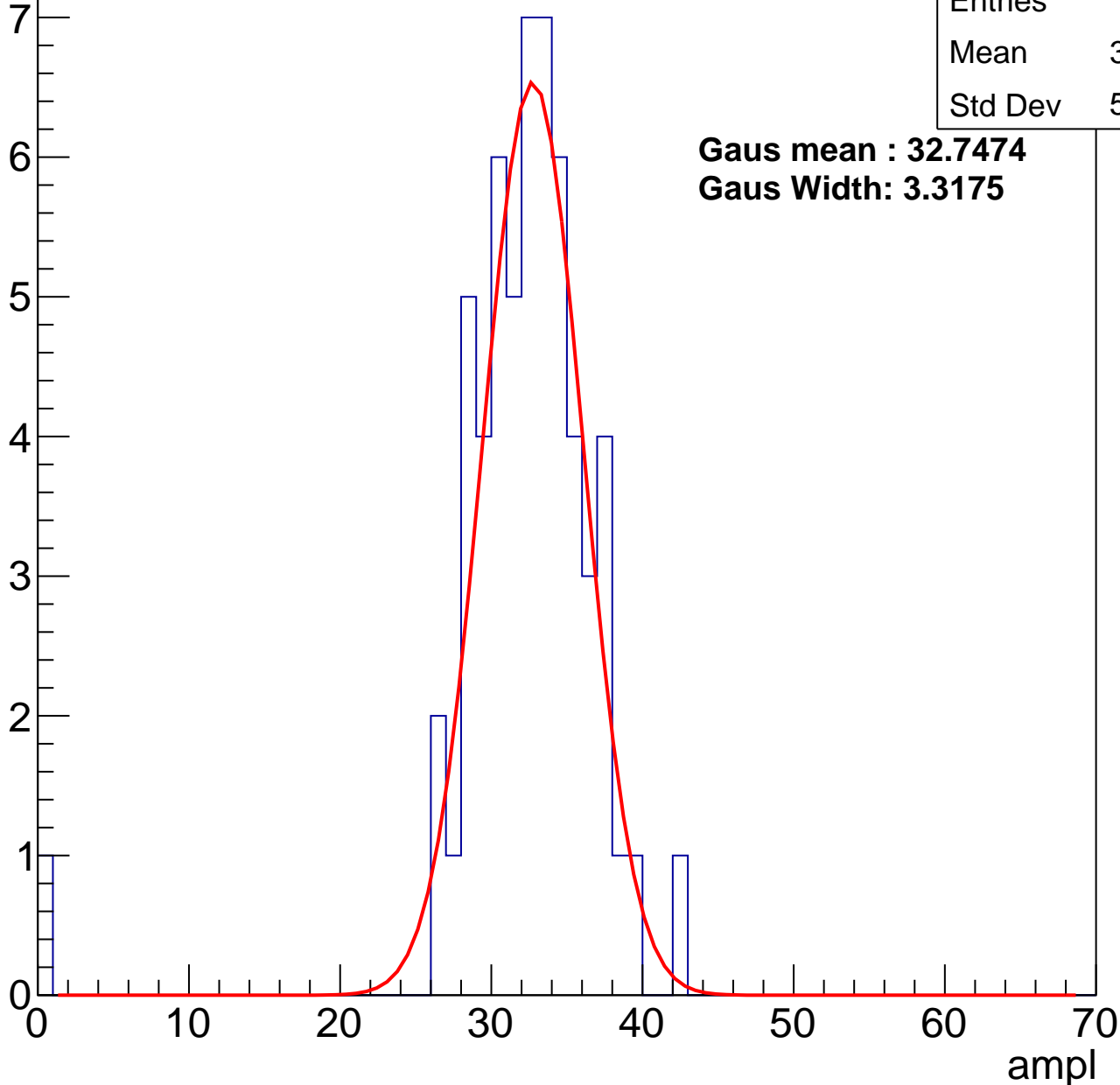
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.79
Std Dev	5.355

Gaus mean : 32.7474

Gaus Width: 3.3175



B0L002S, U5-ch85, adc1

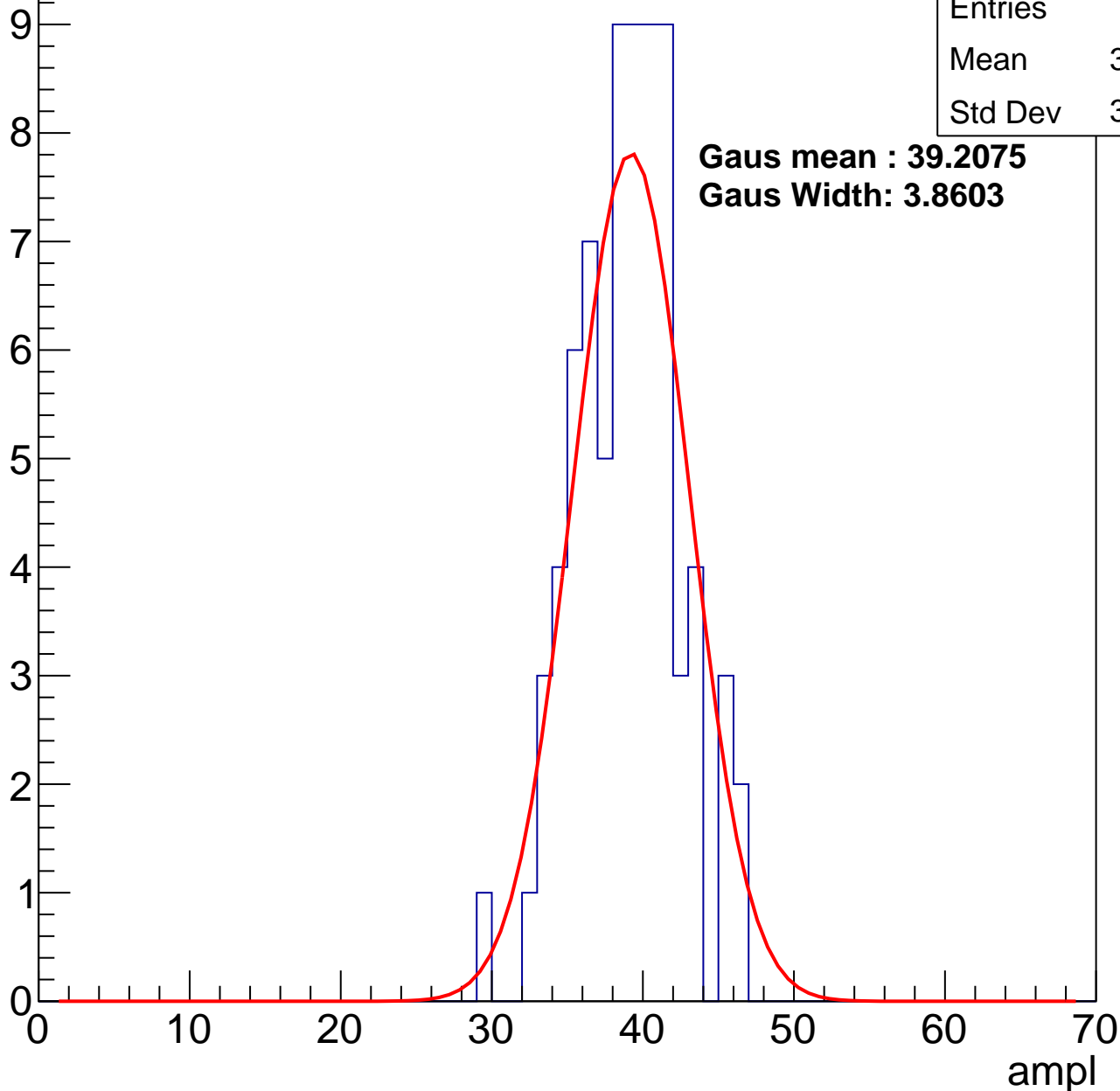
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.53
Std Dev	3.407

Gaus mean : 39.2075

Gaus Width: 3.8603



B0L002S, U5-ch85, adc2

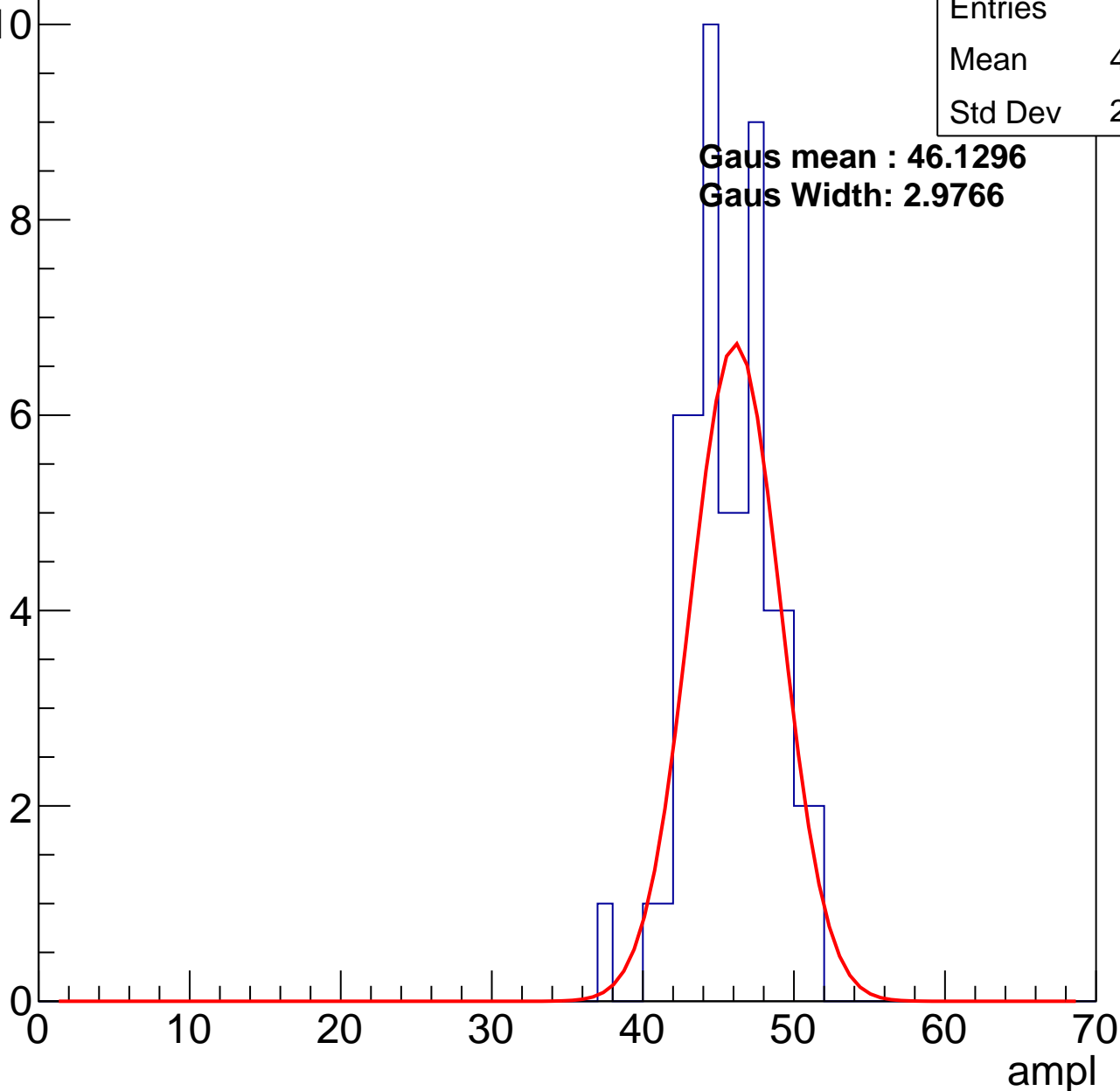
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	45.29
Std Dev	2.839

Gaus mean : 46.1296

Gaus Width: 2.9766

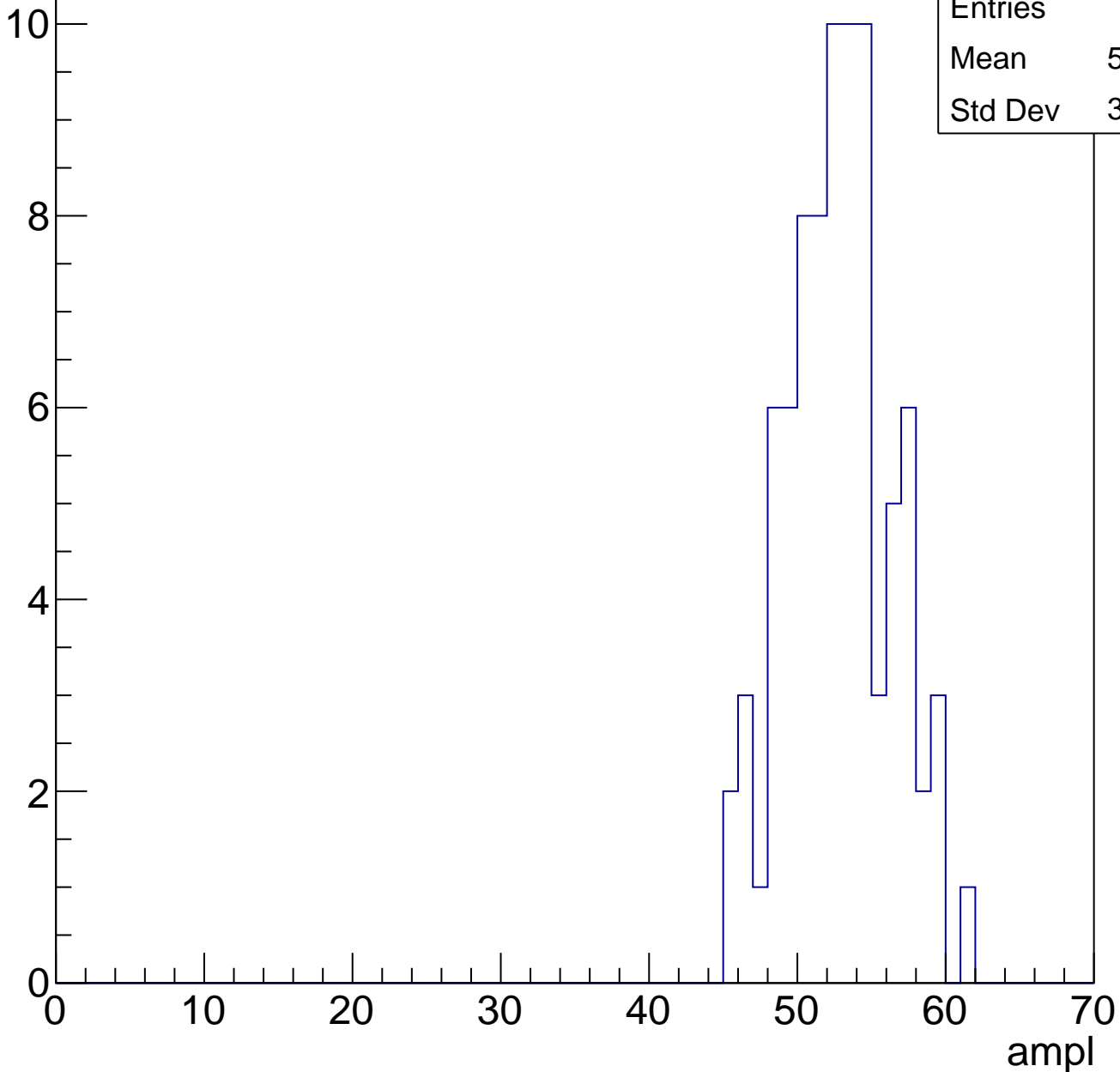


B0L002S, U5-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	52.33
Std Dev	3.482

Entry

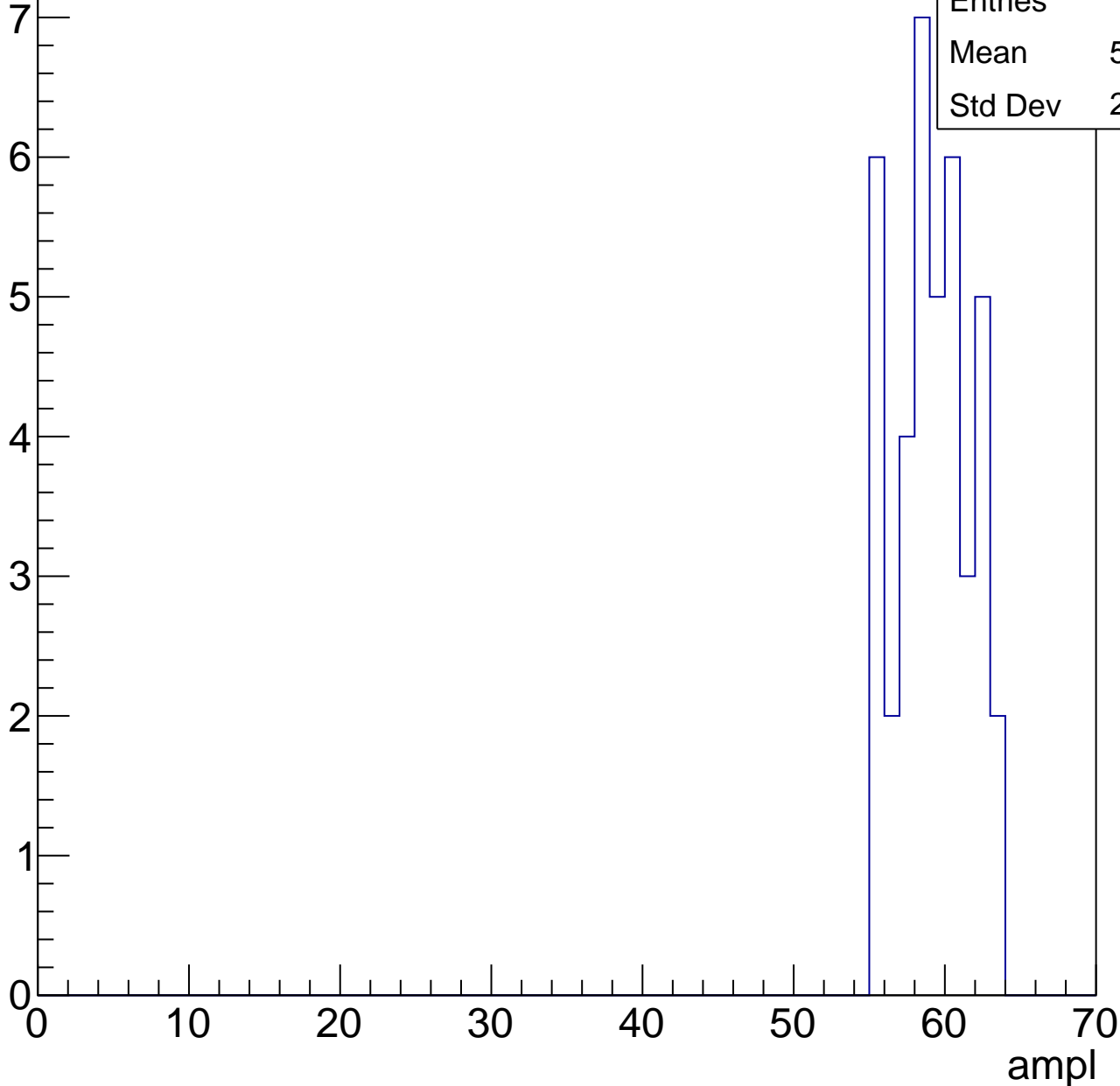


B0L002S, U5-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	58.75
Std Dev	2.395

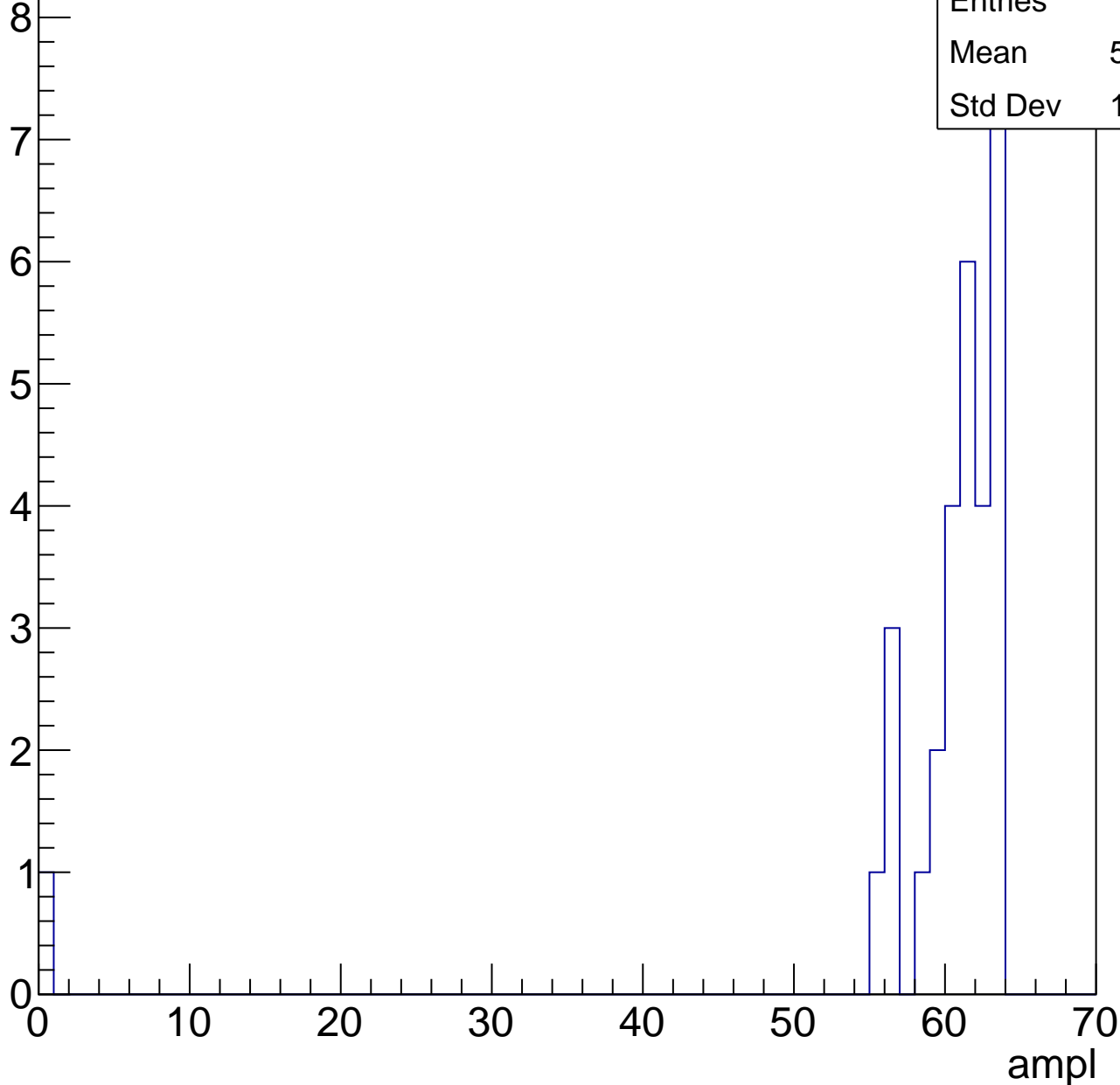


B0L002S, U5-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

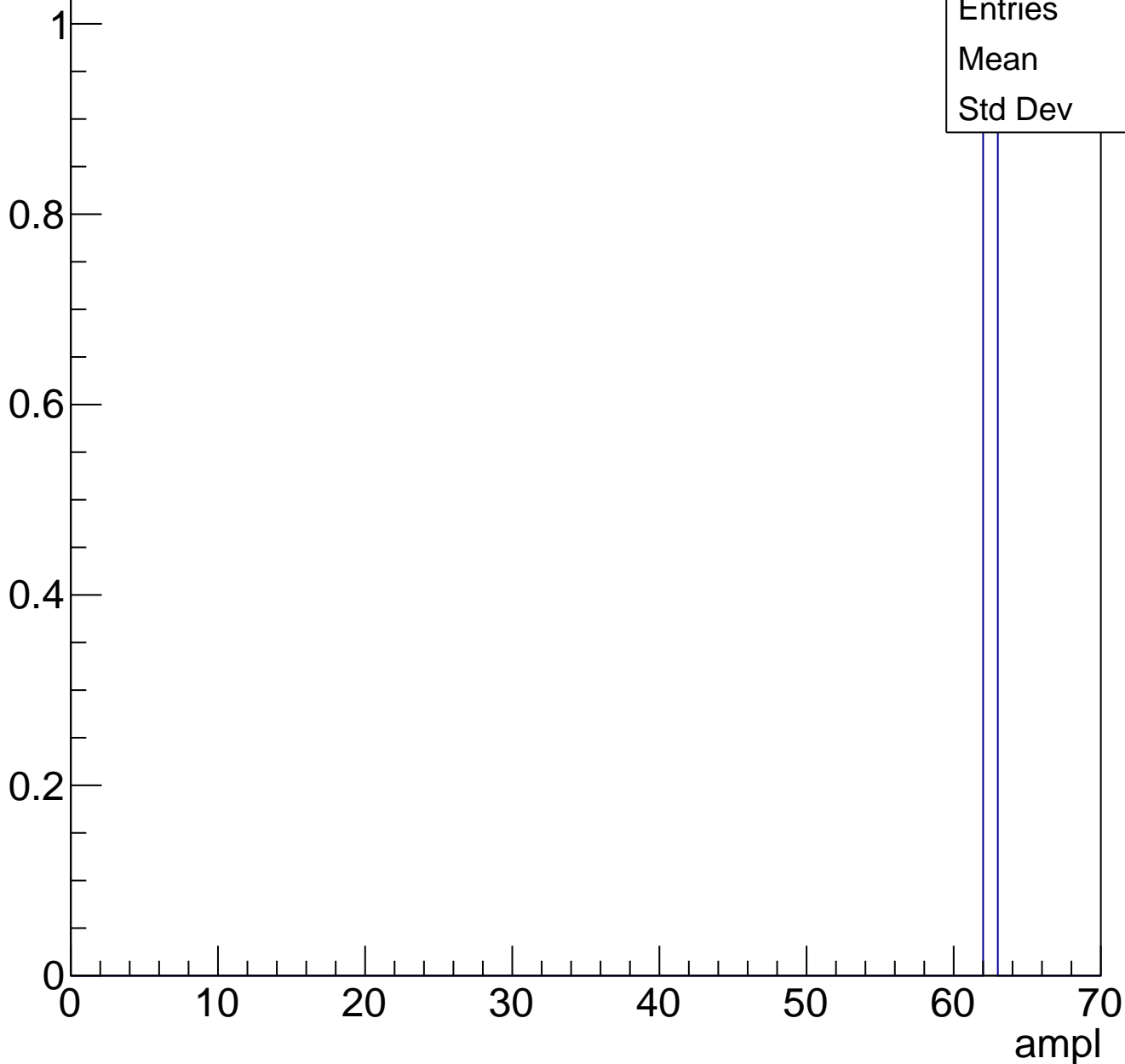
Entries	30
Mean	58.57
Std Dev	11.12



B0L002S, U5-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch86, adc0

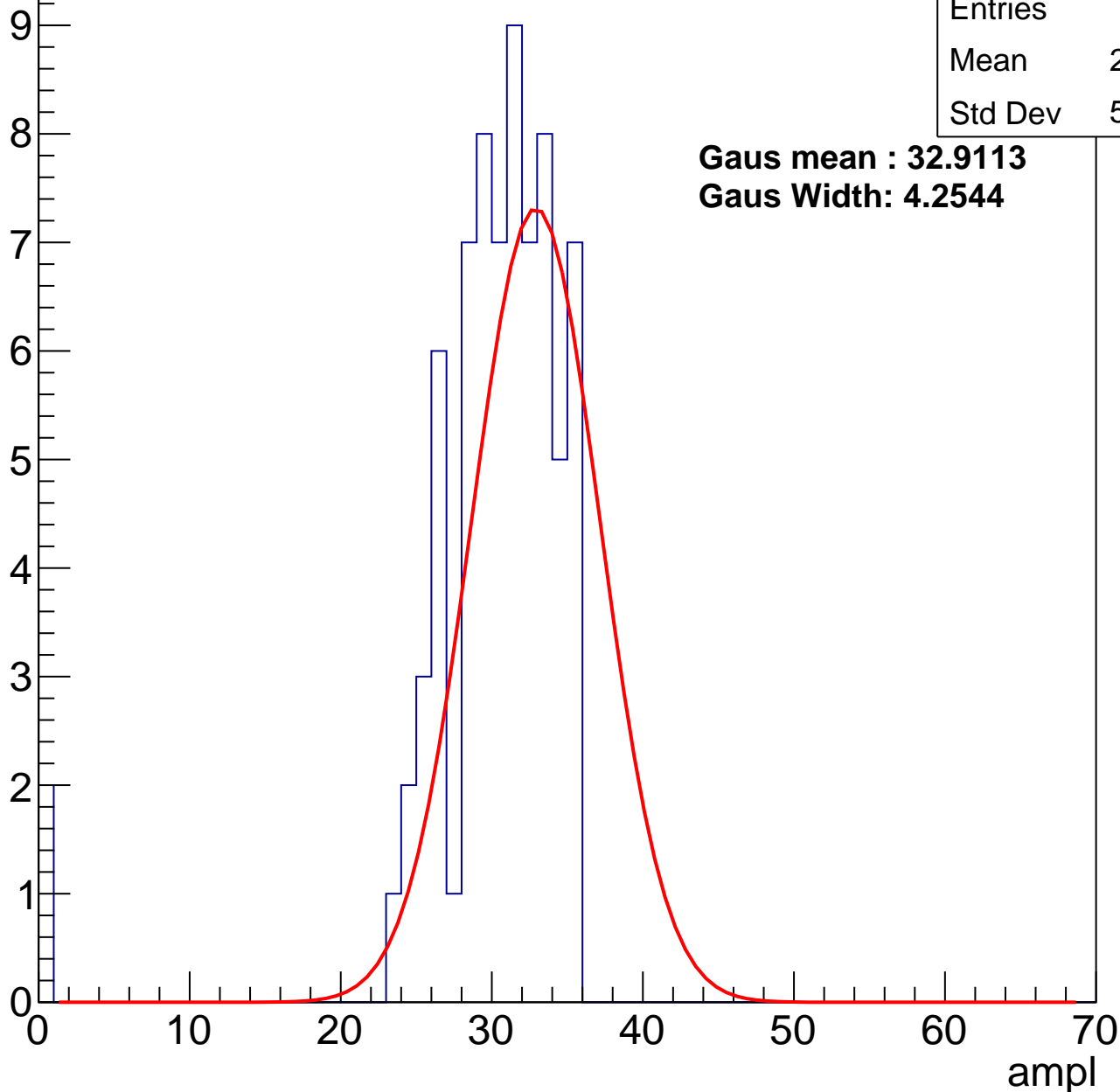
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.44
Std Dev	5.826

Gaus mean : 32.9113

Gaus Width: 4.2544



B0L002S, U5-ch86, adc1

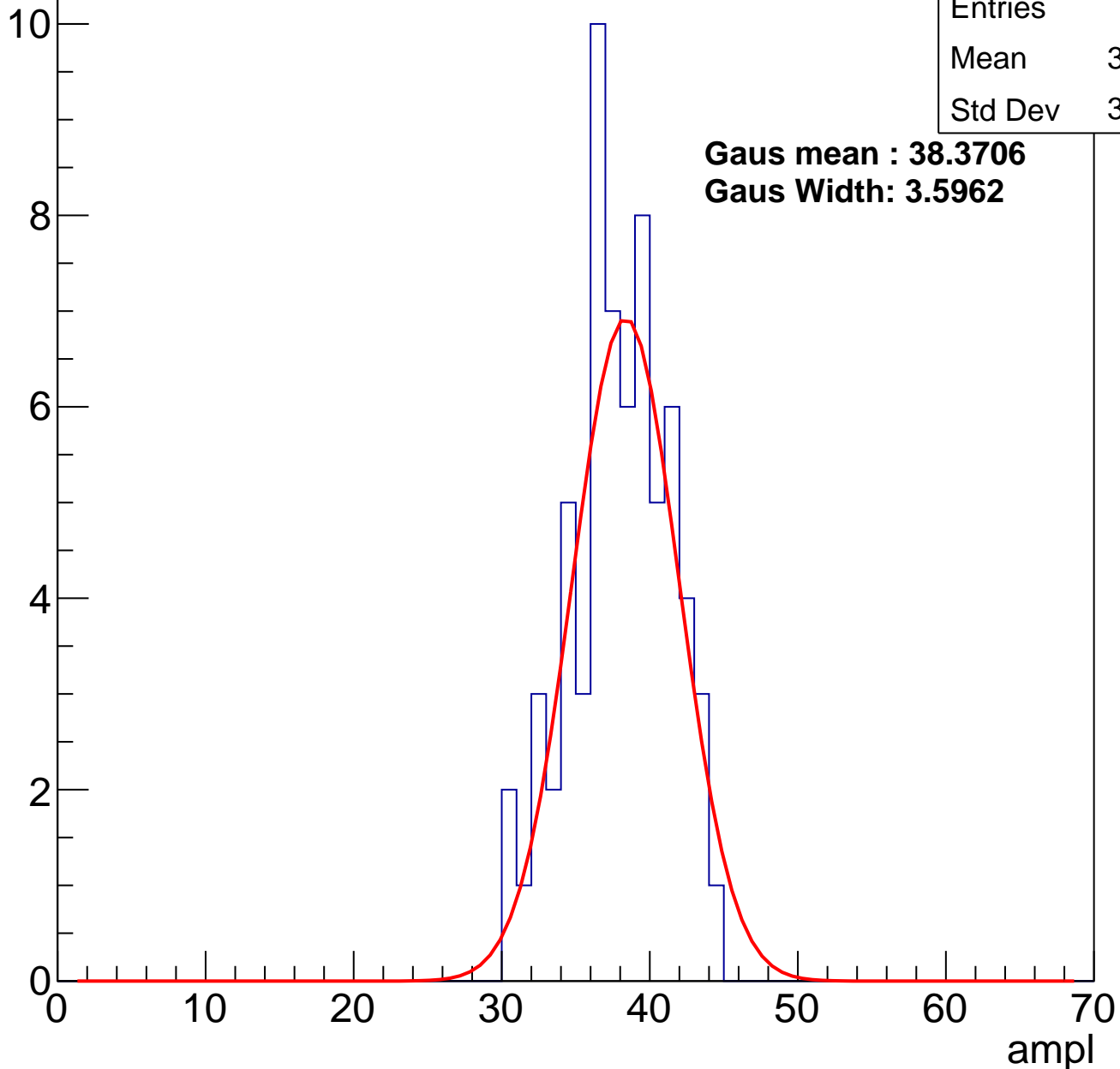
calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	37.48
Std Dev	3.322

Gaus mean : 38.3706

Gaus Width: 3.5962

Entry



B0L002S, U5-ch86, adc2

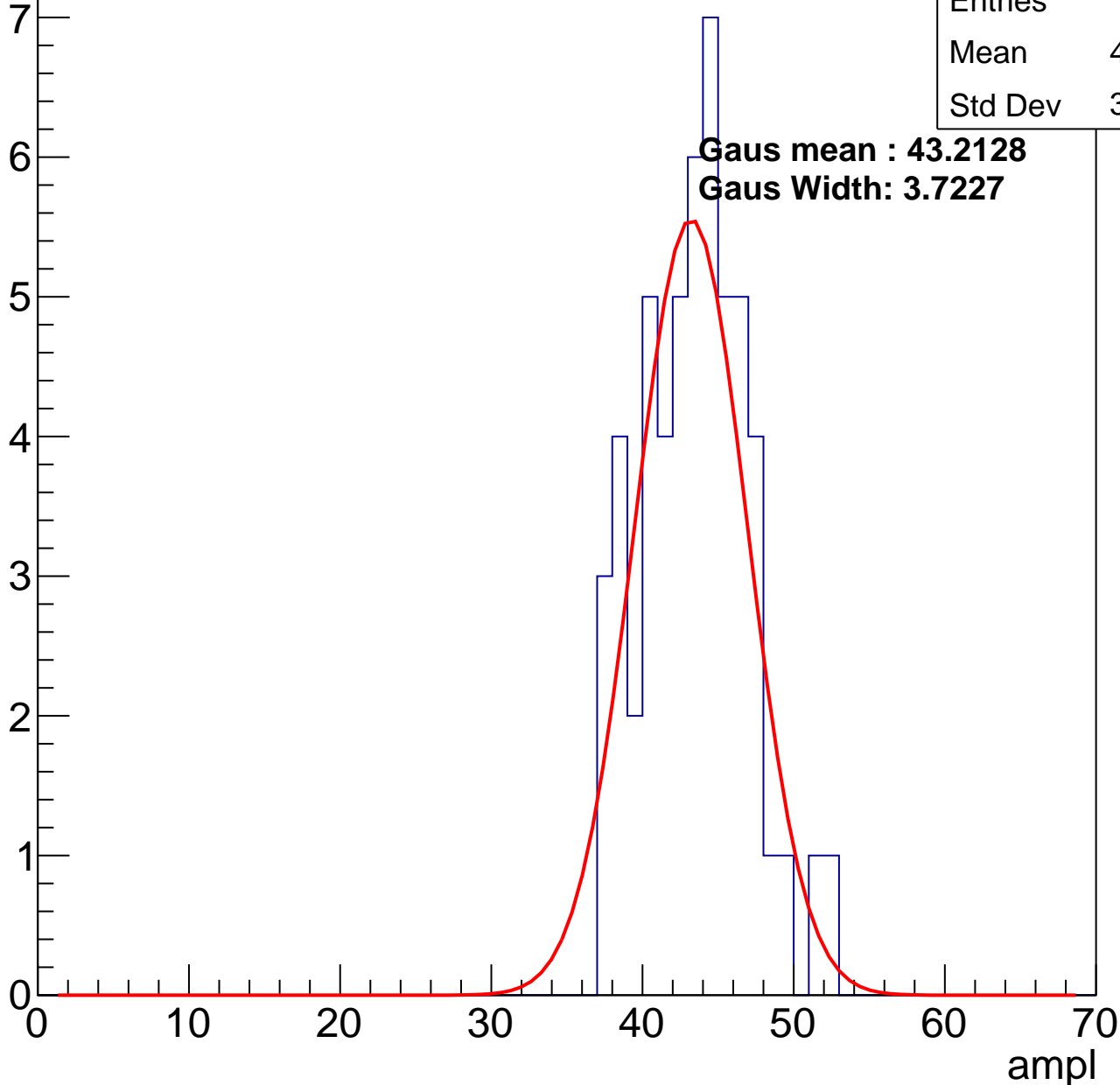
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.04
Std Dev	3.464

Gaus mean : 43.2128

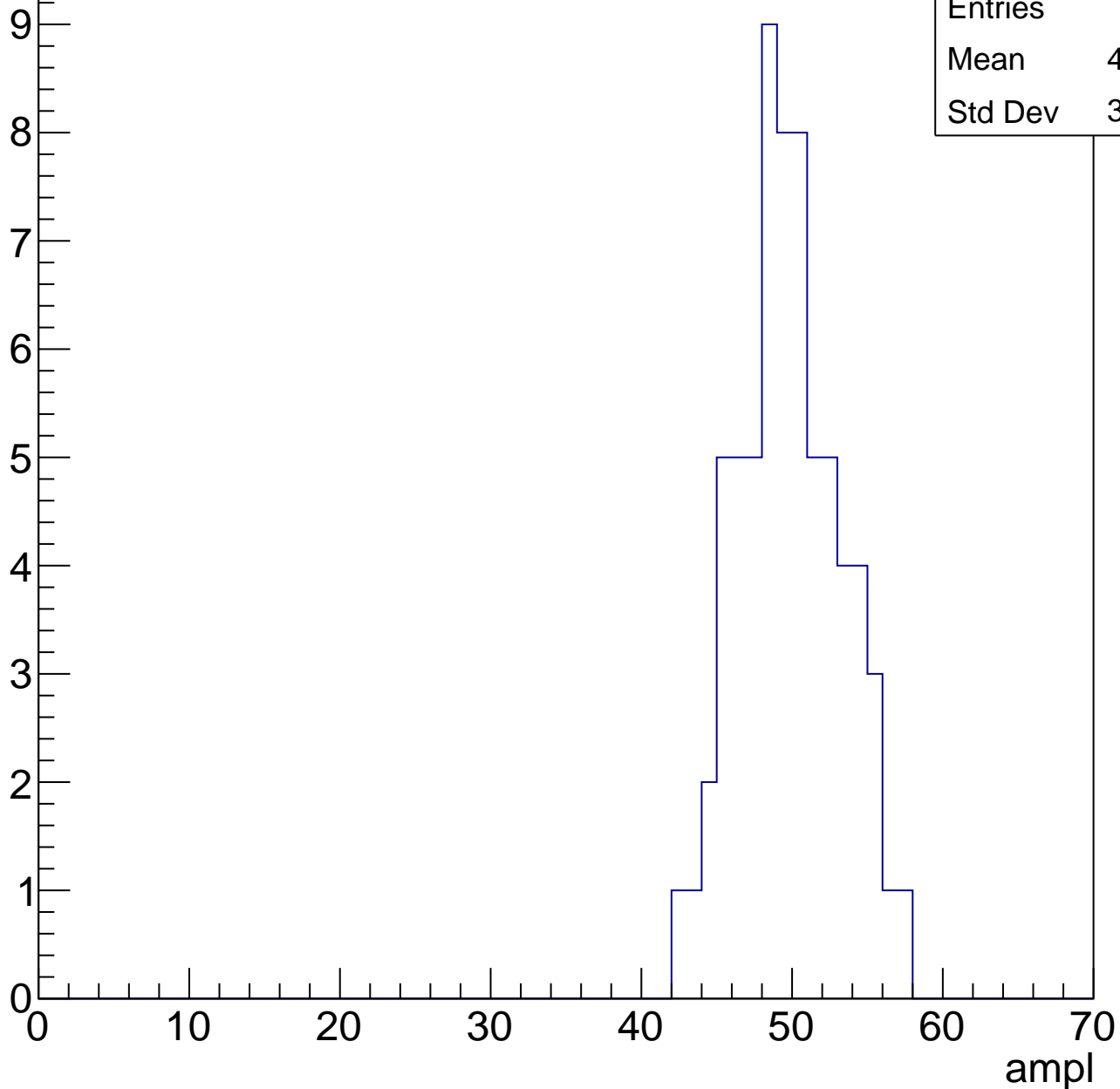
Gaus Width: 3.7227



B0L002S, U5-ch86, adc3

calib_packv5_042523_0143.root, FC#8, port C1

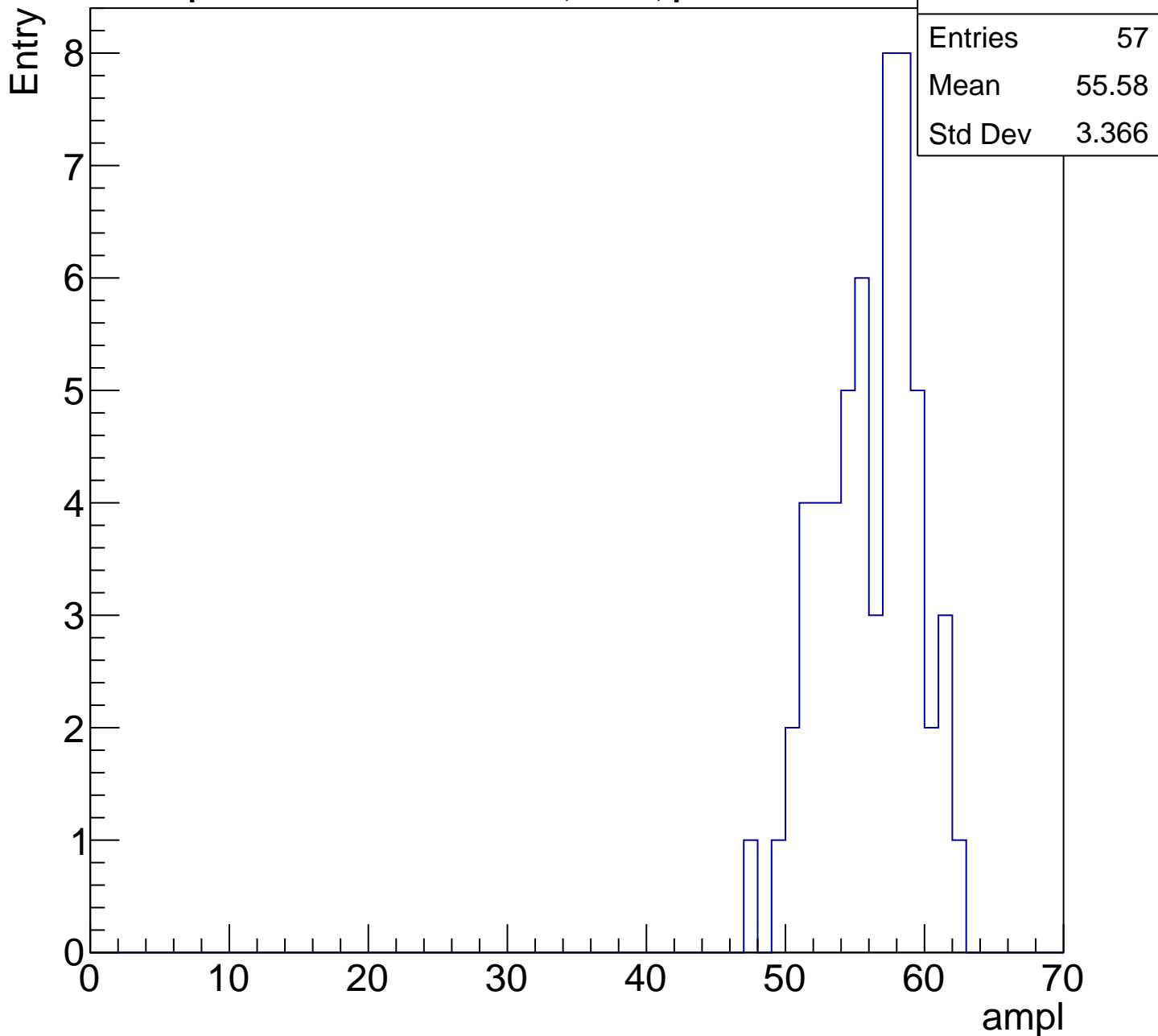
Entry



Entries	67
Mean	49.37
Std Dev	3.318

B0L002S, U5-ch86, adc4

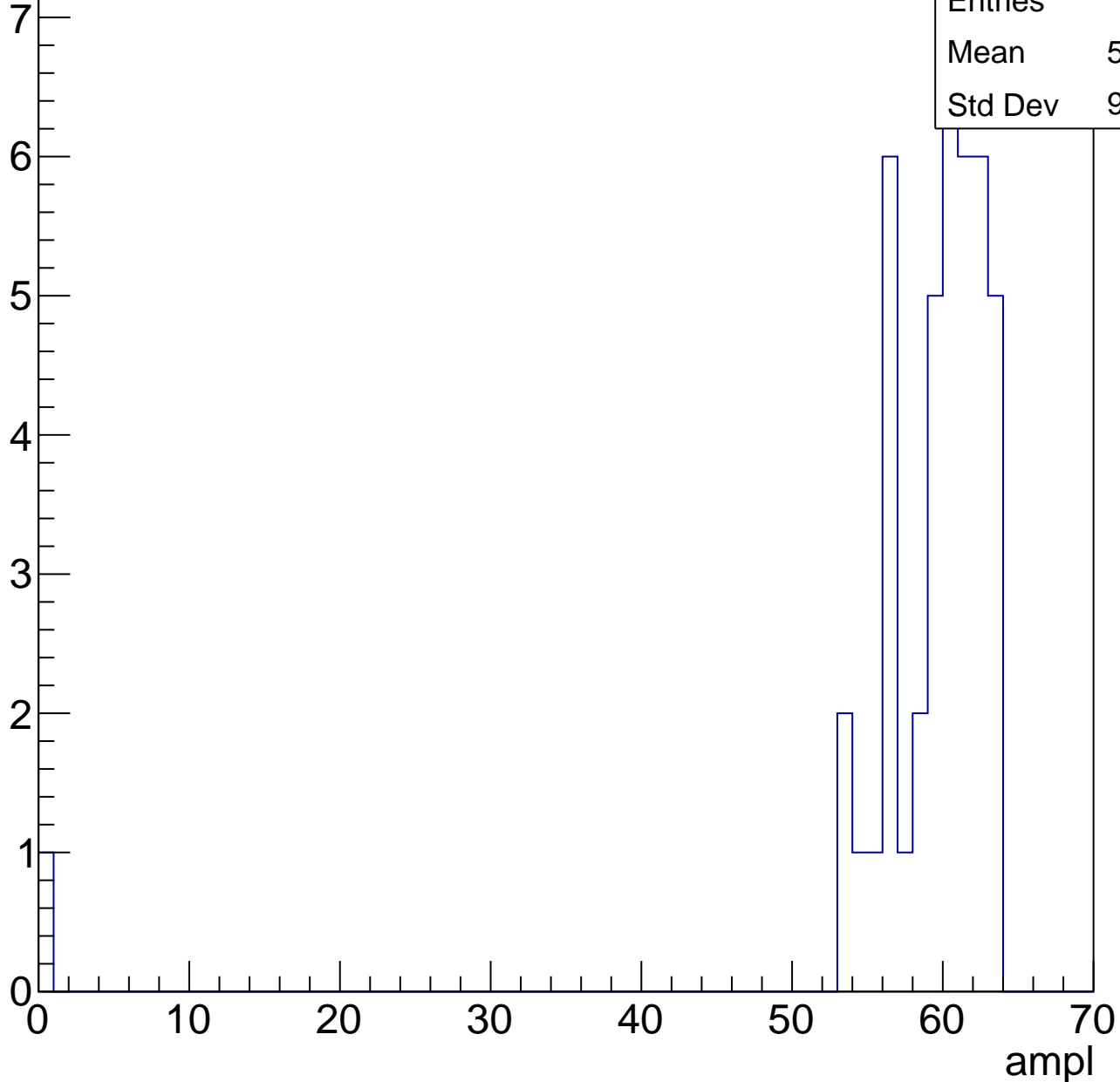
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch86, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

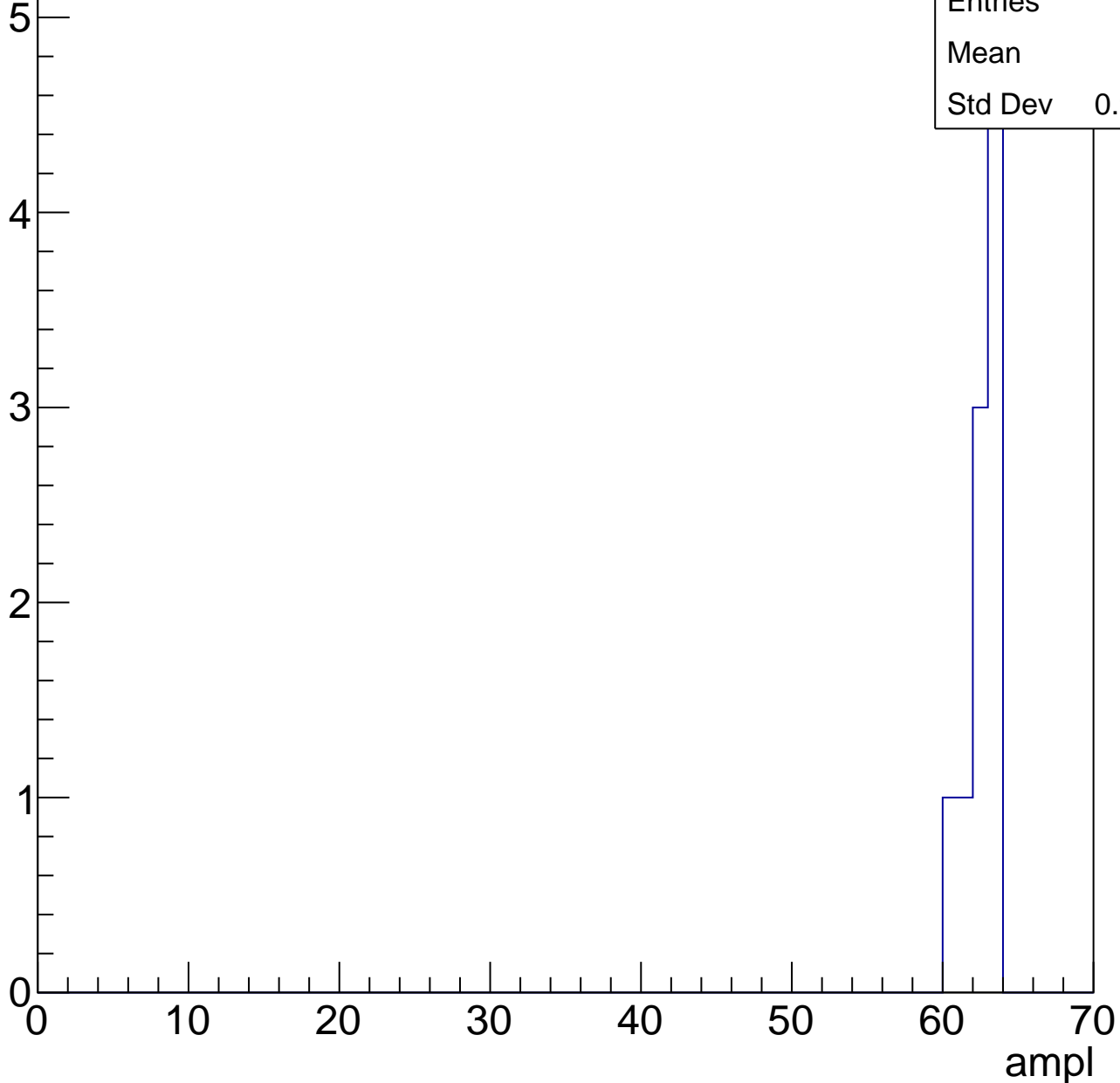


B0L002S, U5-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	62.2
Std Dev	0.9798



B0L002S, U5-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch87, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

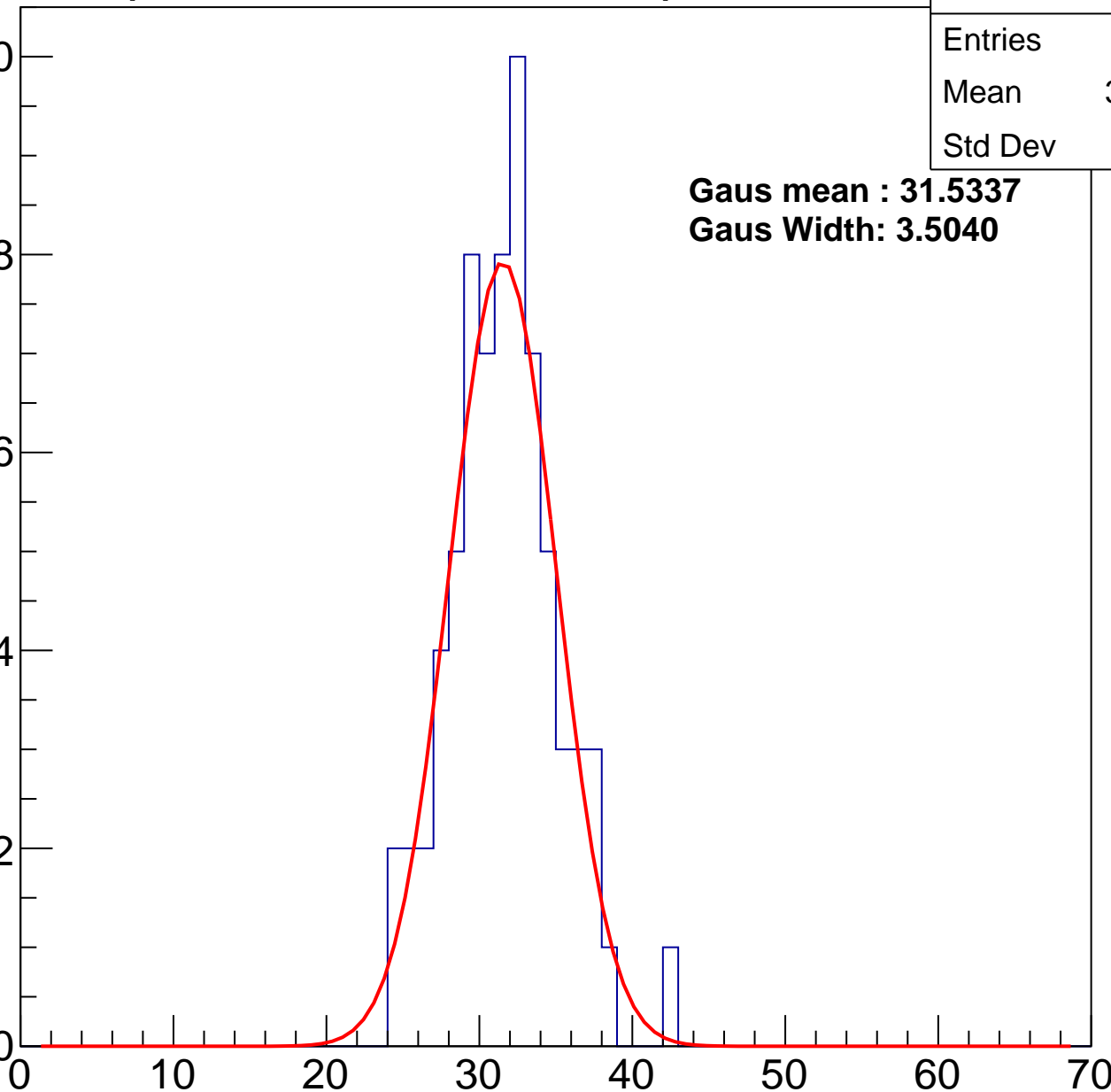
Entries	71
Mean	31.17
Std Dev	3.46

Gaus mean : 31.5337

Gaus Width: 3.5040

10
8
6
4
2
0

ampl



B0L002S, U5-ch87, adc1

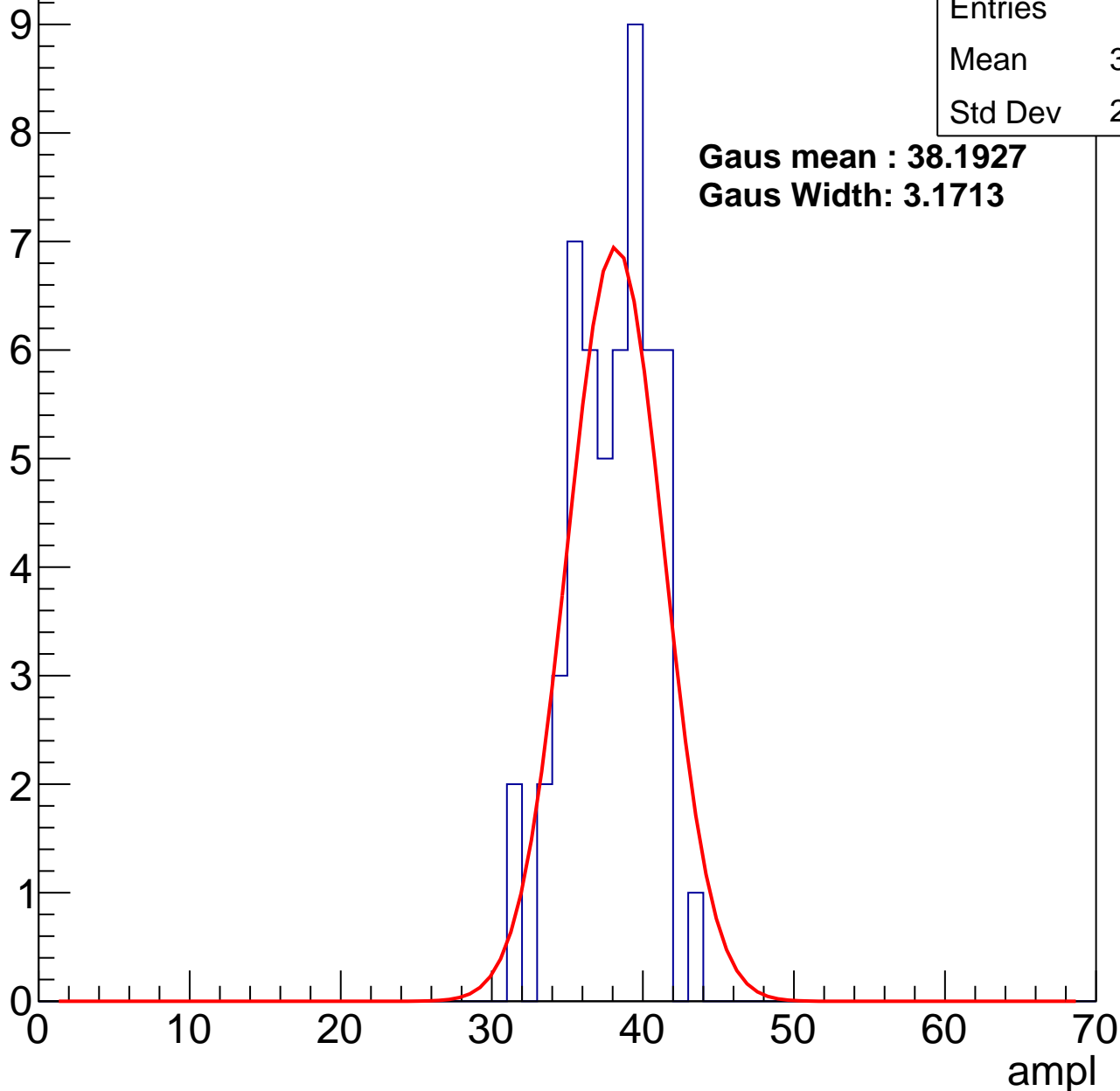
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	37.43
Std Dev	2.688

Gaus mean : 38.1927

Gaus Width: 3.1713



B0L002S, U5-ch87, adc2

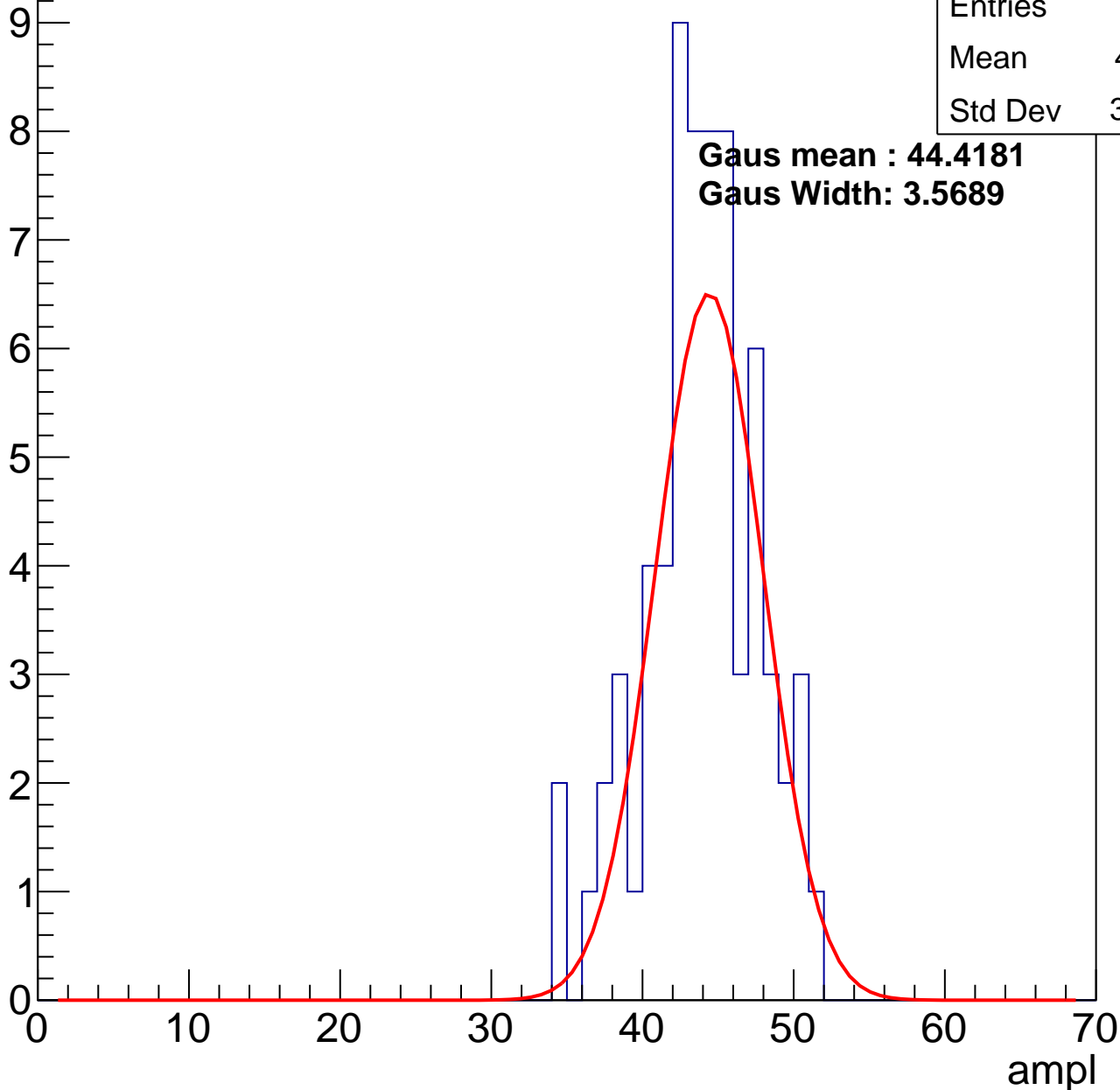
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.41
Std Dev	3.727

Gaus mean : 44.4181

Gaus Width: 3.5689

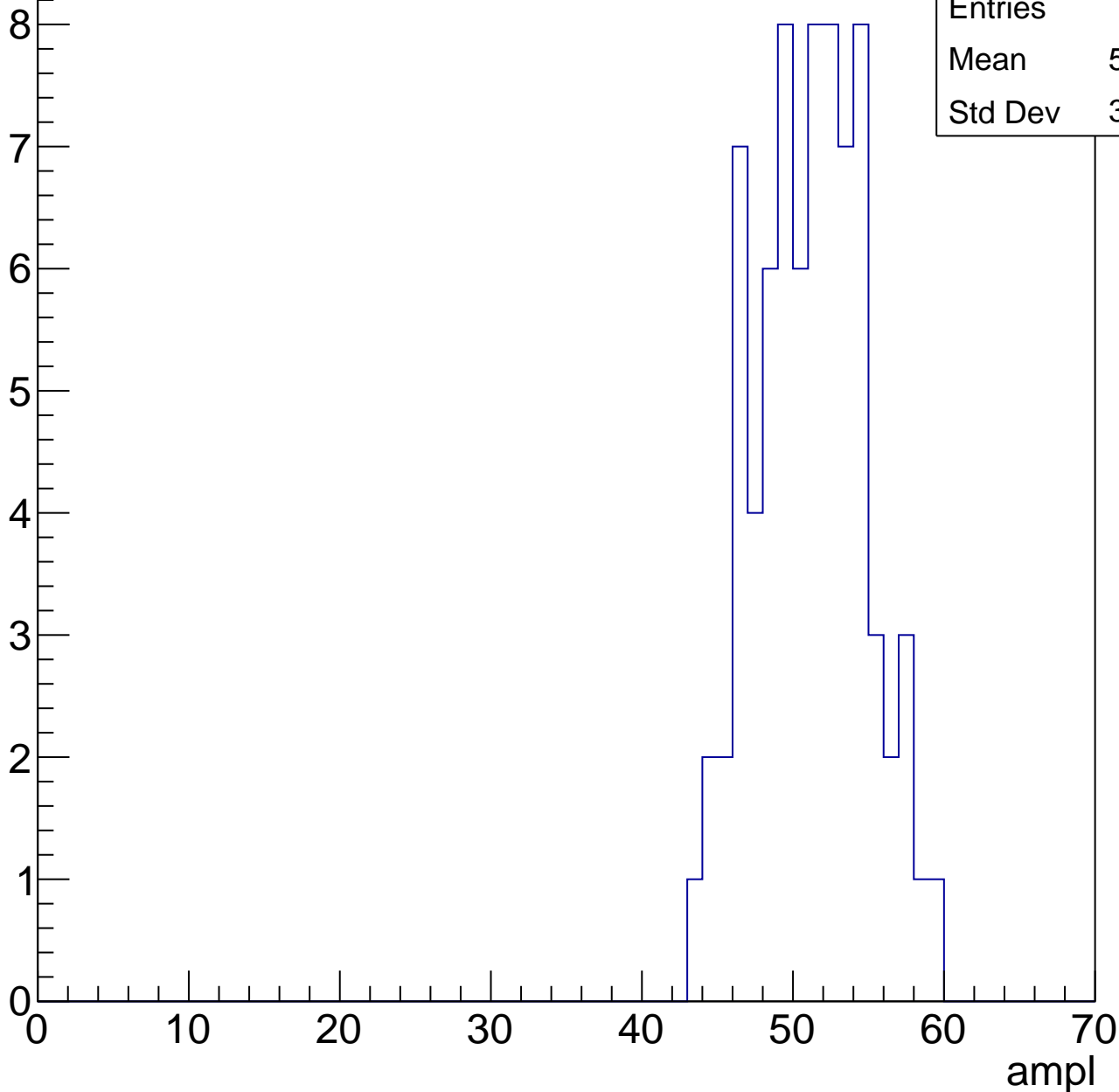


B0L002S, U5-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

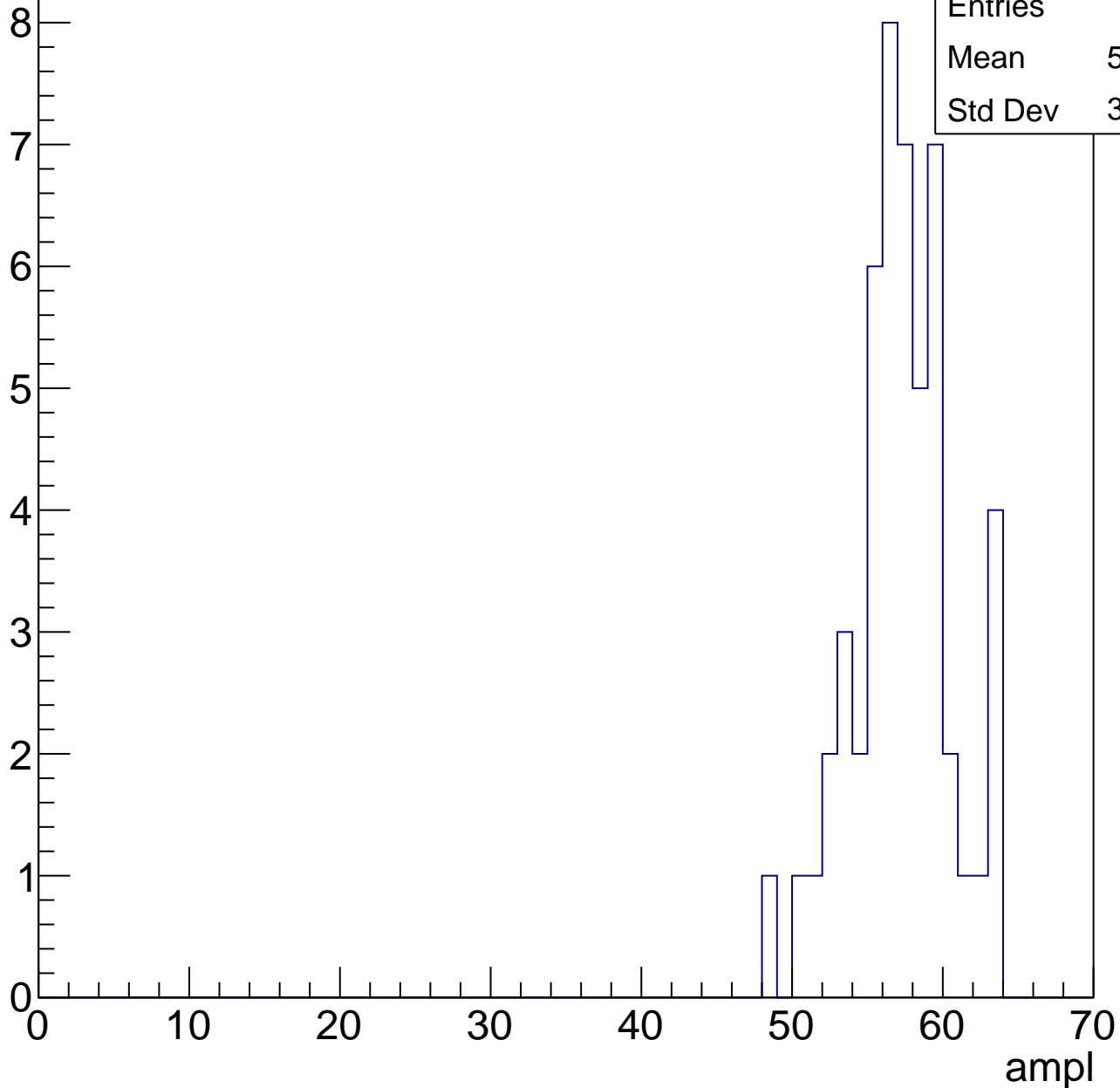
Entries	77
Mean	50.69
Std Dev	3.576



B0L002S, U5-ch87, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

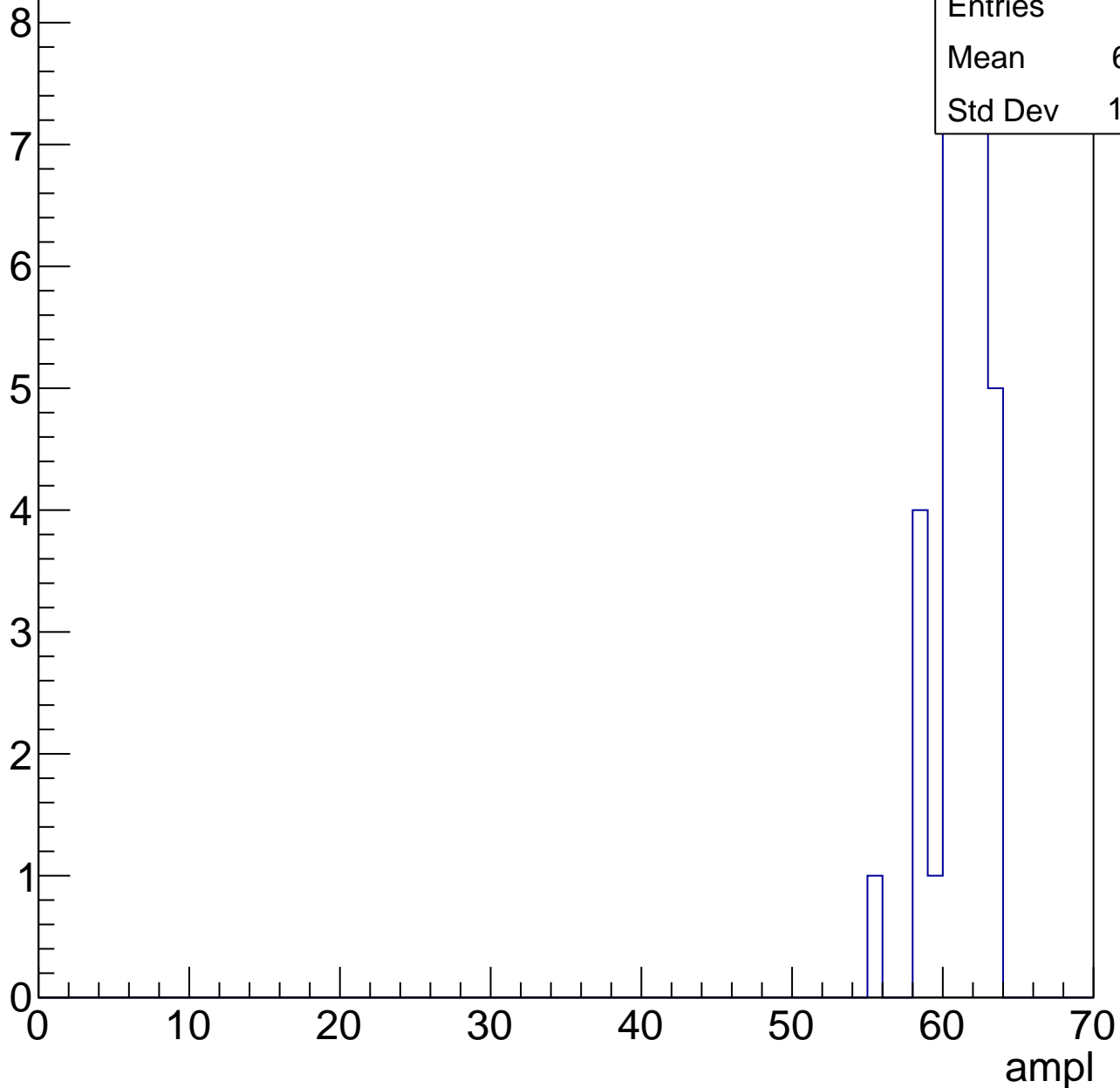


Entries	51
Mean	56.76
Std Dev	3.276

B0L002S, U5-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

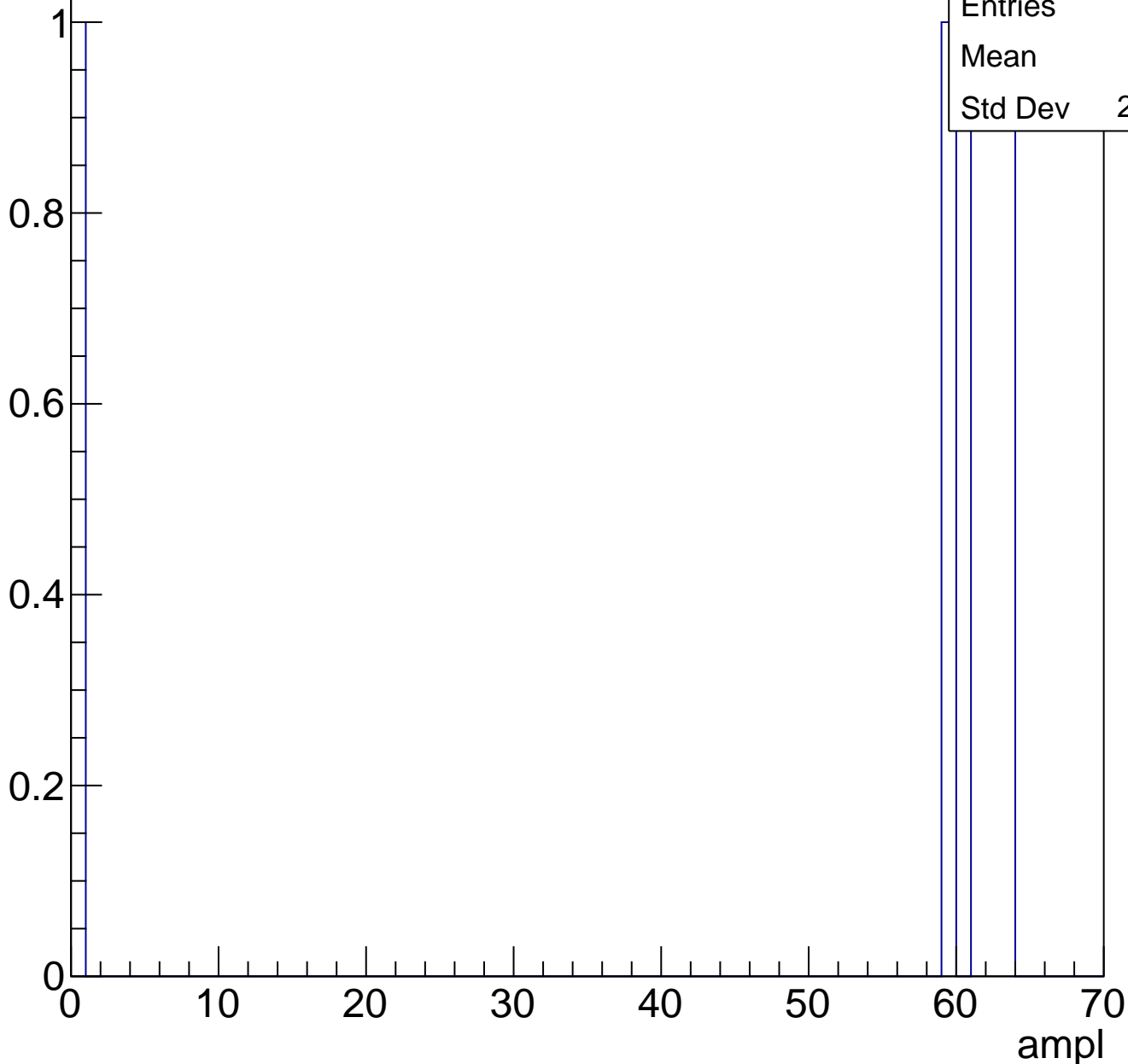
Entry



B0L002S, U5-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch88, adc0

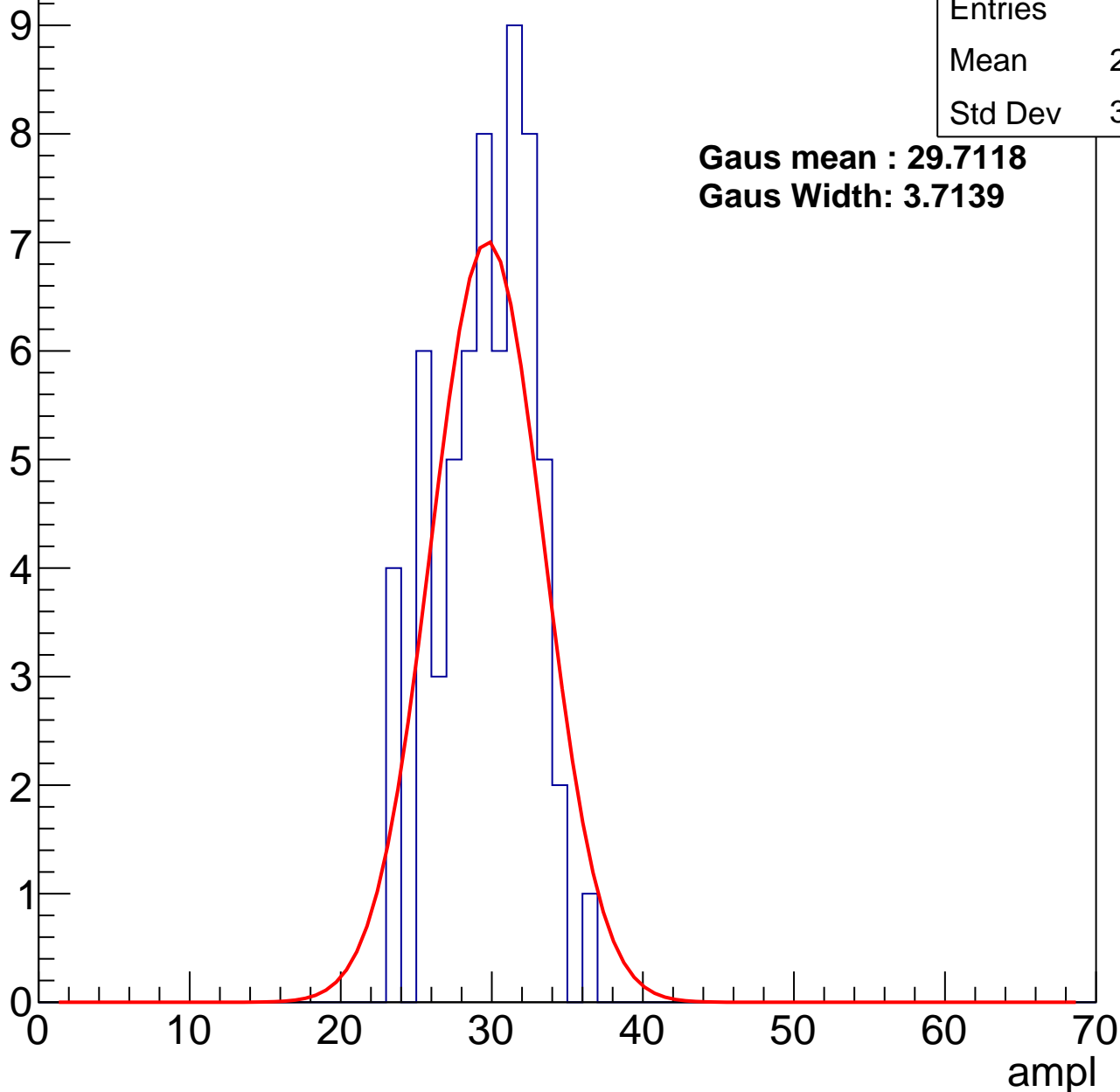
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.19
Std Dev	3.044

Gaus mean : 29.7118

Gaus Width: 3.7139



B0L002S, U5-ch88, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	36.86
Std Dev	3.305

Gaus mean : 37.2894

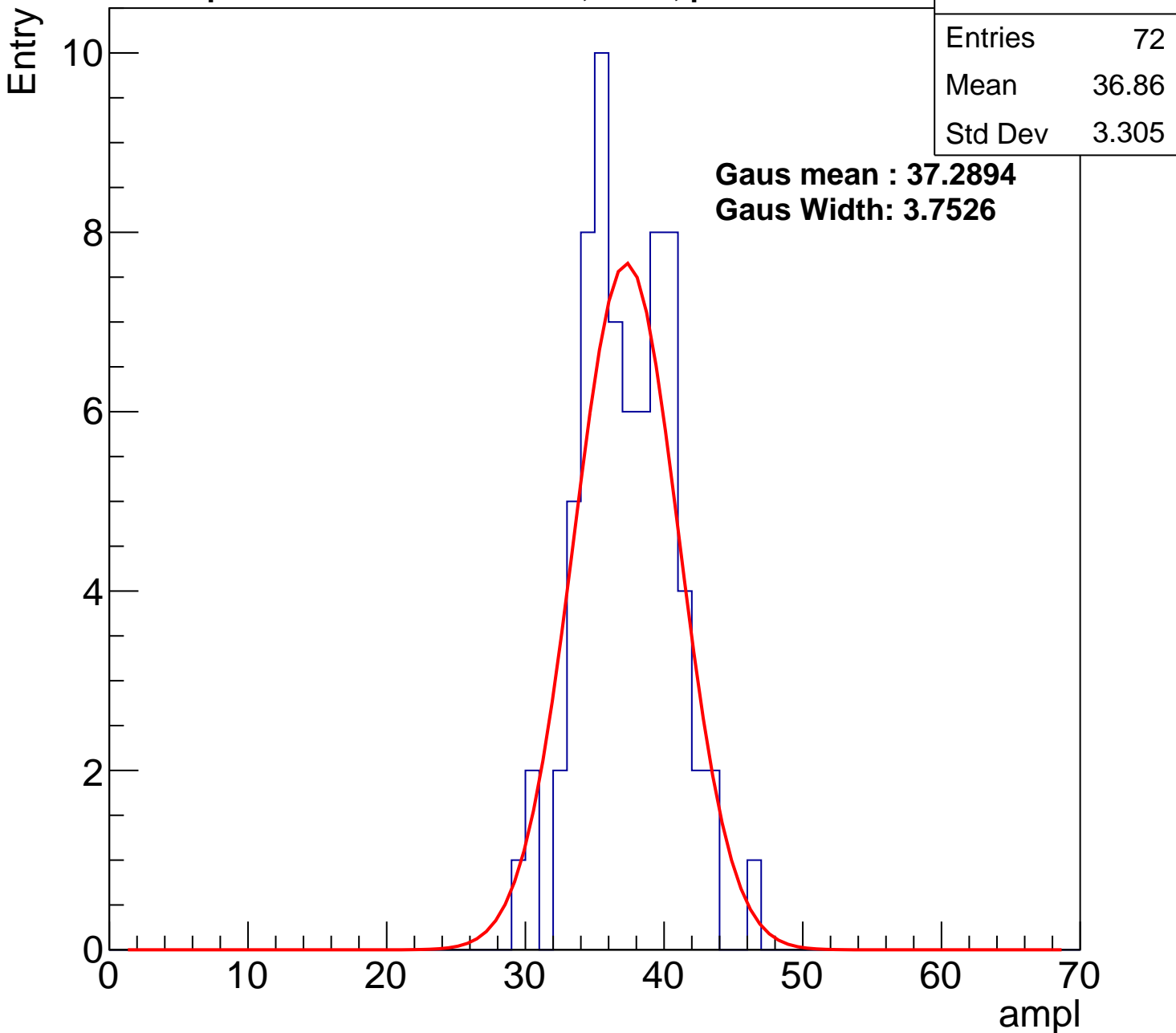
Gaus Width: 3.7526

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch88, adc2

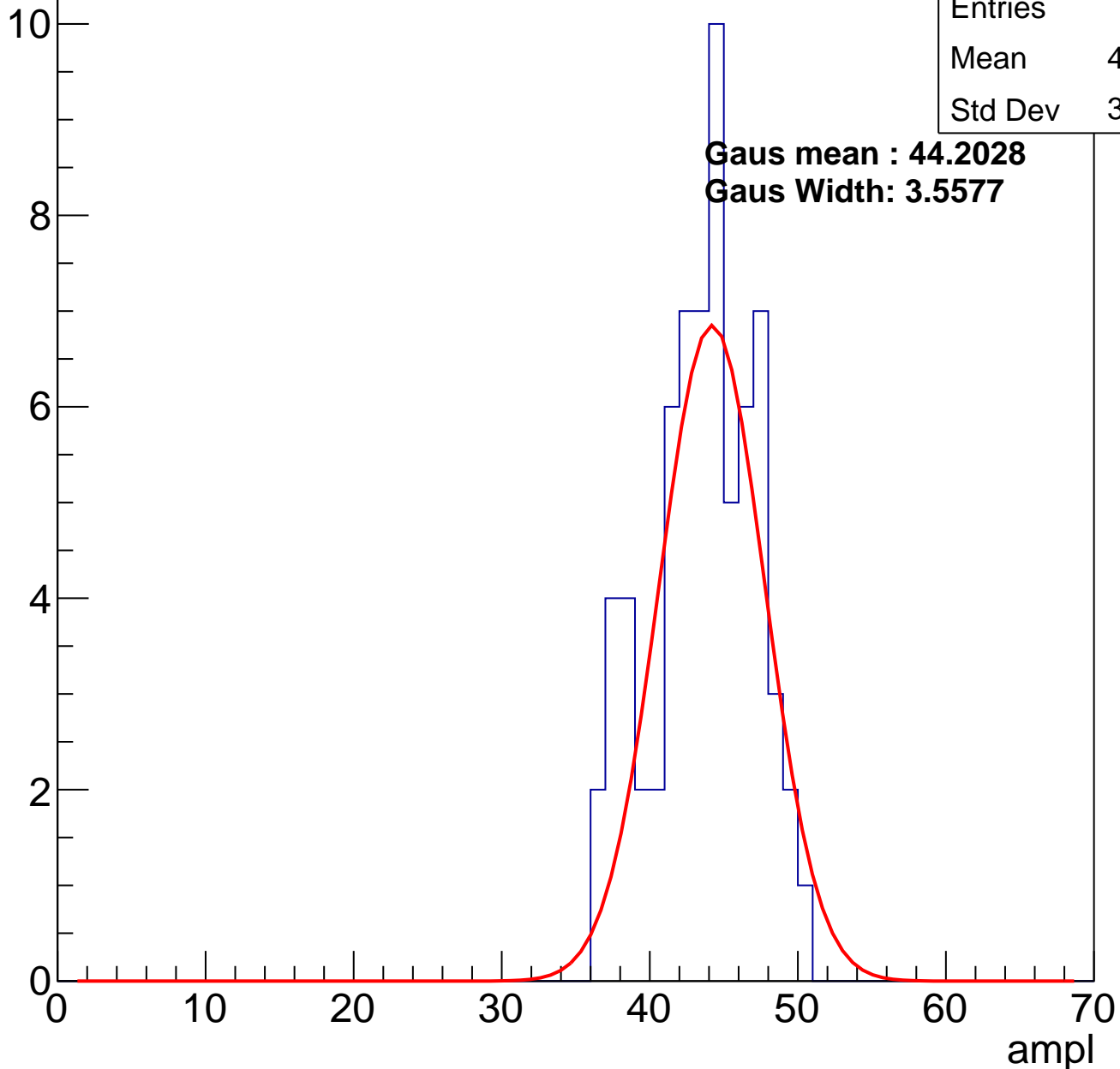
calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	43.13
Std Dev	3.459

Gaus mean : 44.2028

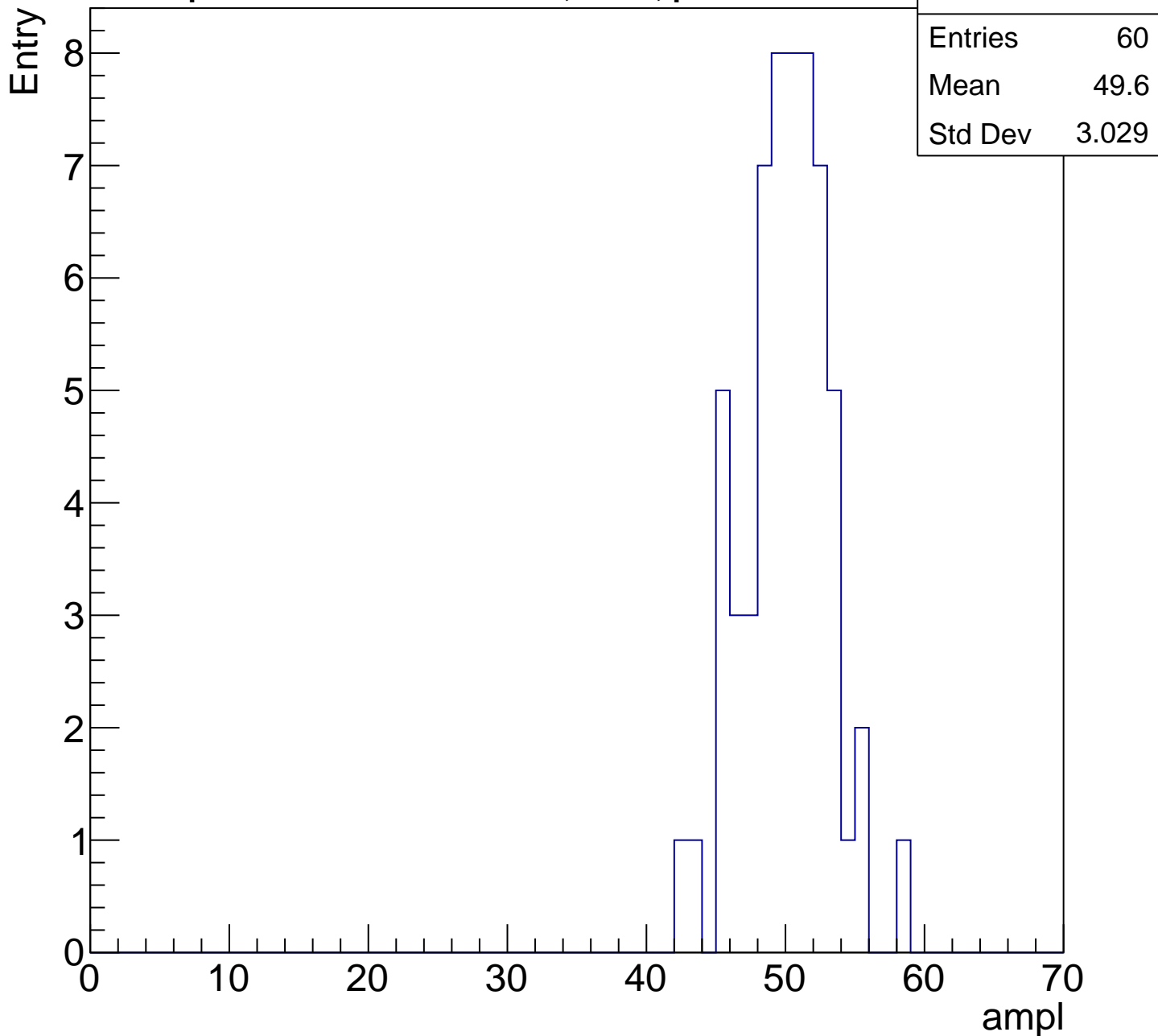
Gaus Width: 3.5577

Entry



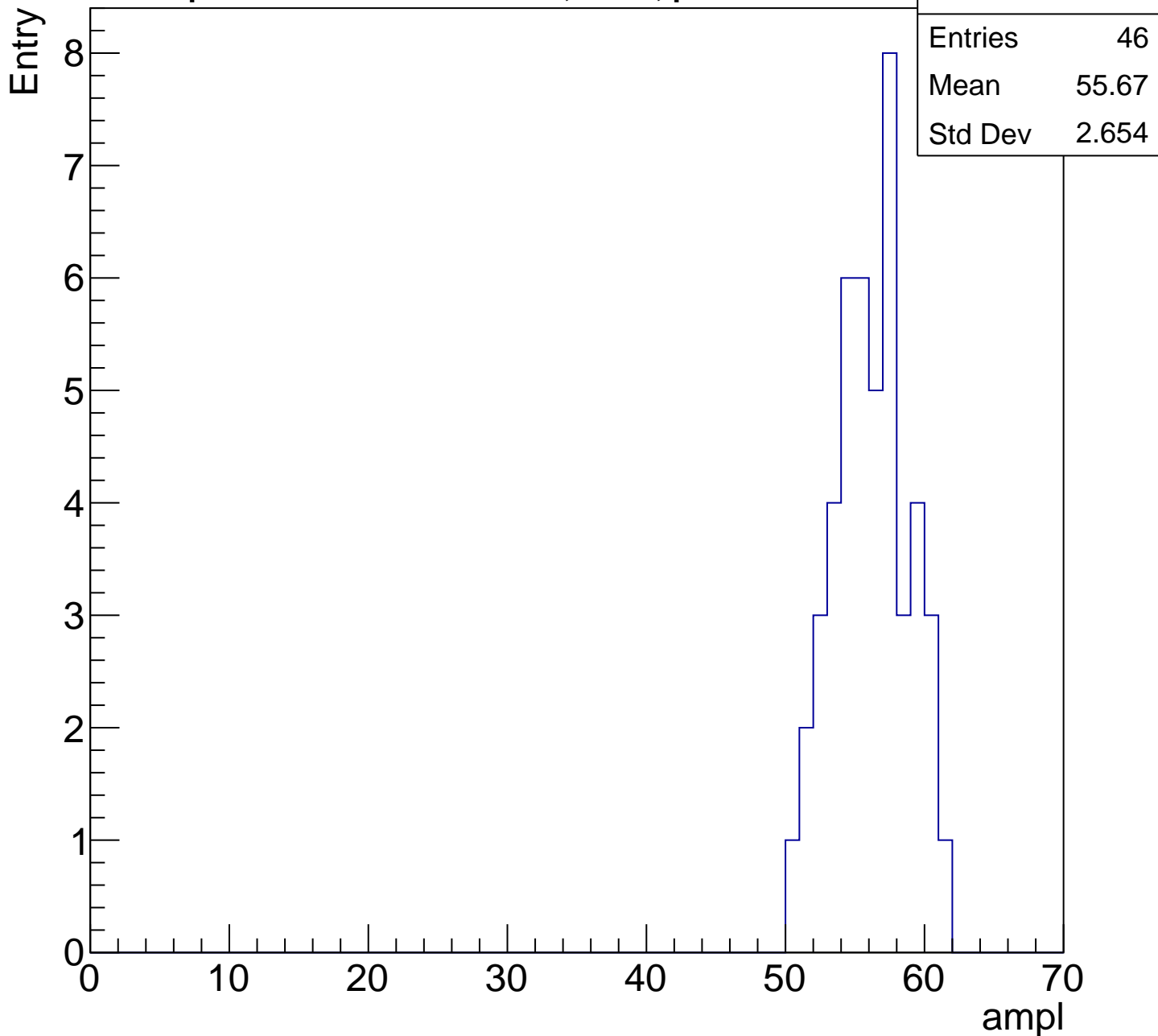
B0L002S, U5-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch88, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch88, adc5

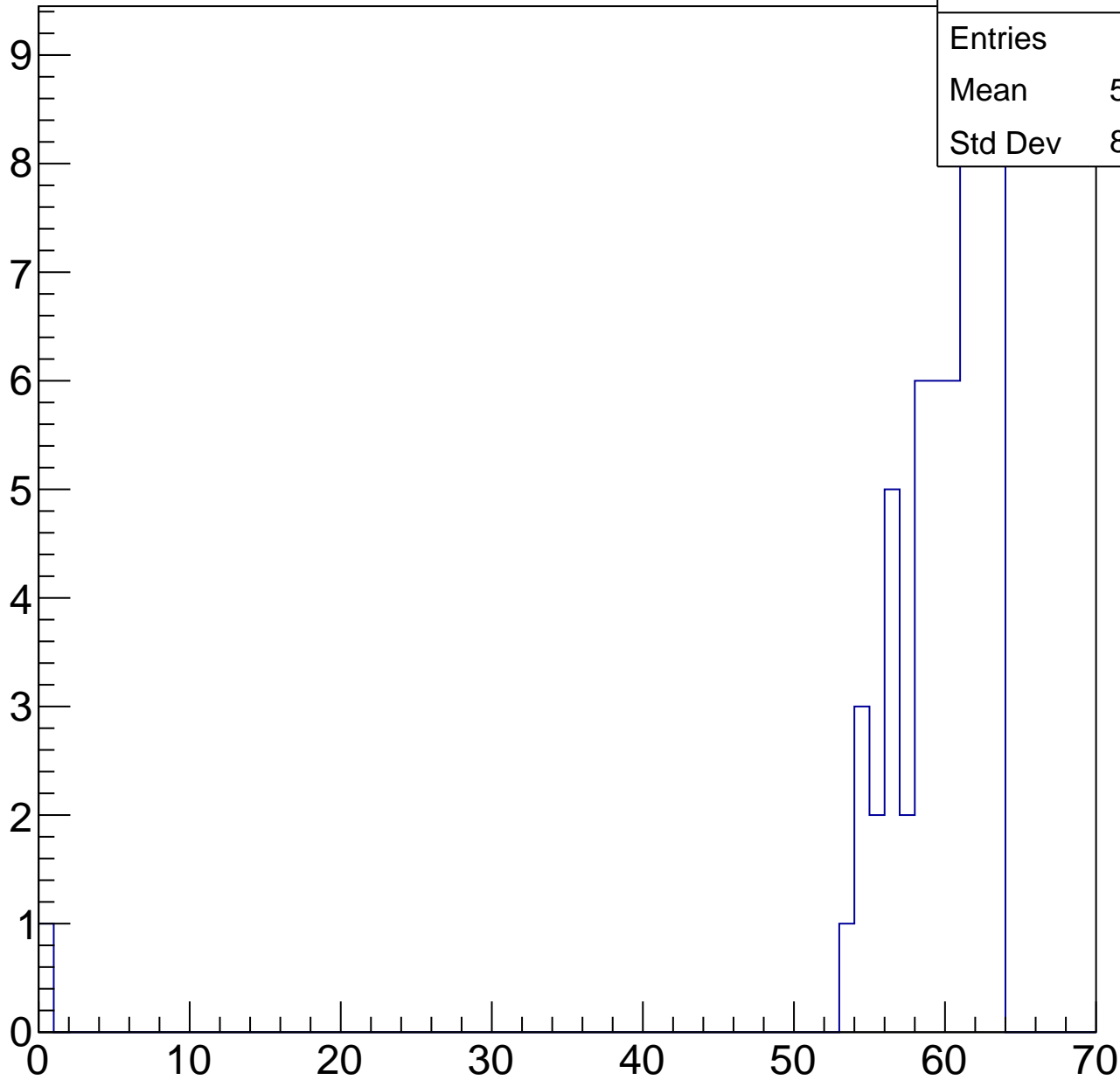
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.46
Std Dev	8.293

ampl



B0L002S, U5-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	61.6
Std Dev	1.02

B0L002S, U5-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch89, adc0

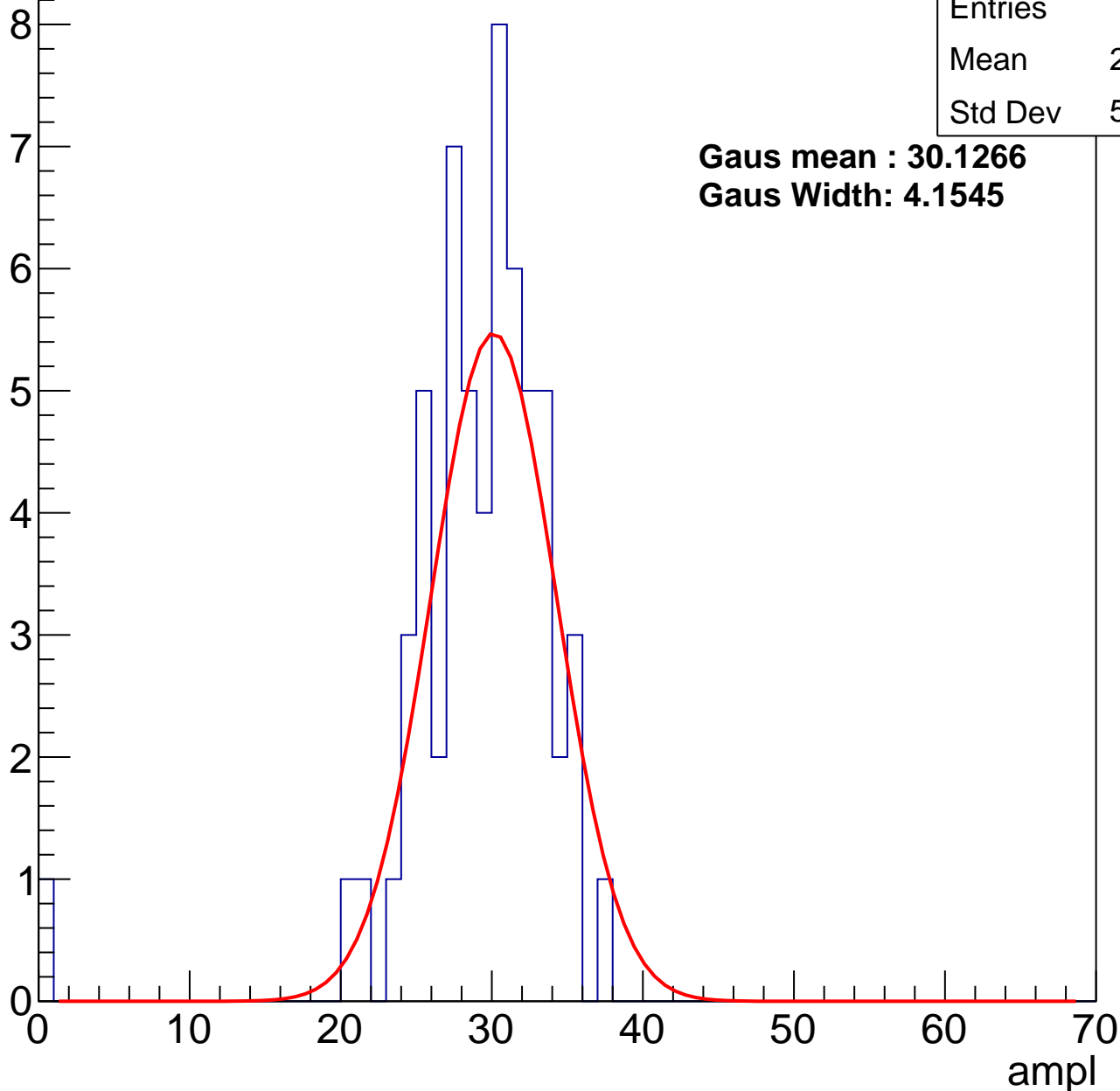
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	28.65
Std Dev	5.163

Gaus mean : 30.1266

Gaus Width: 4.1545



B0L002S, U5-ch89, adc1

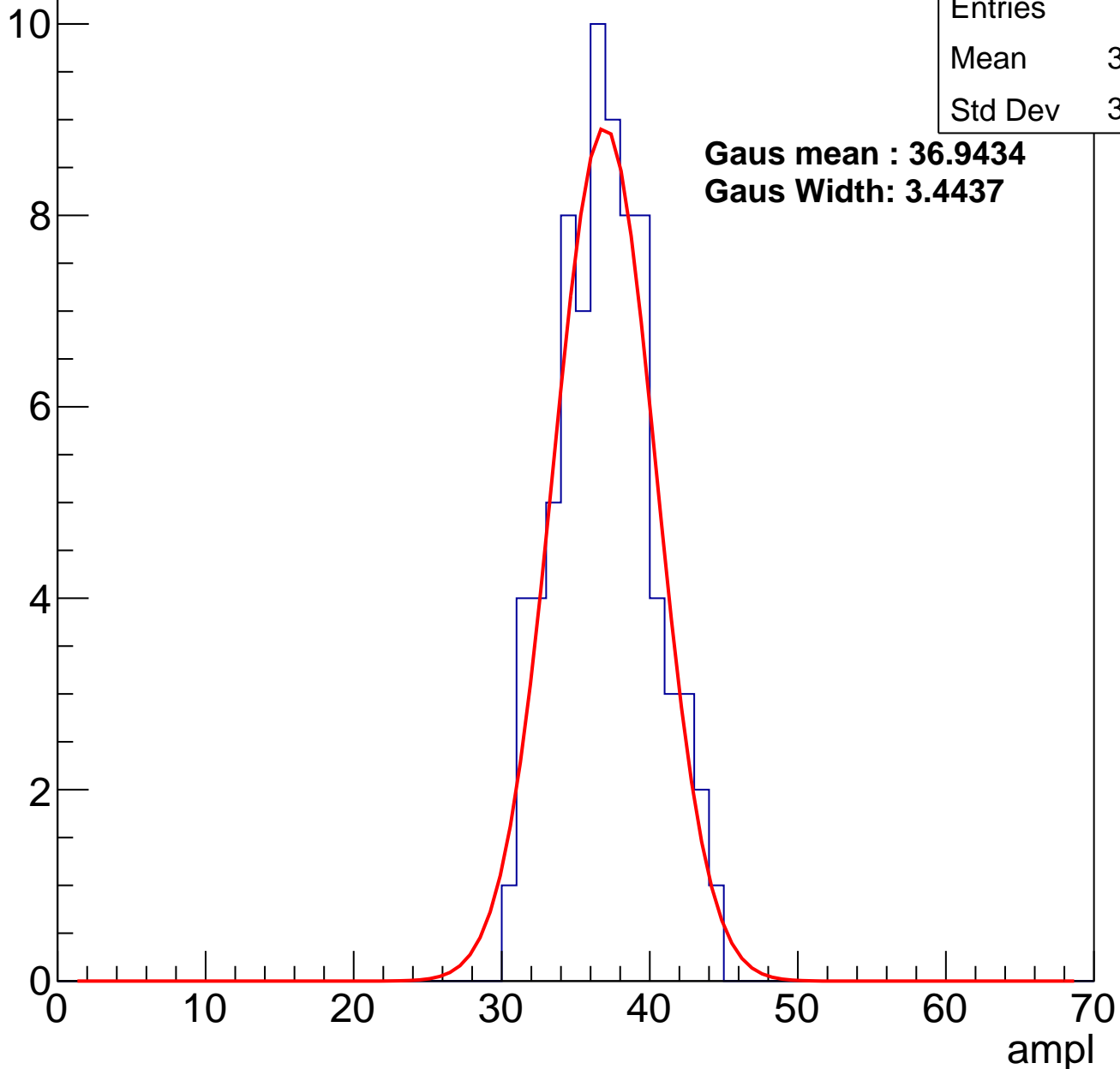
calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	36.52
Std Dev	3.193

Gaus mean : 36.9434

Gaus Width: 3.4437

Entry



B0L002S, U5-ch89, adc2

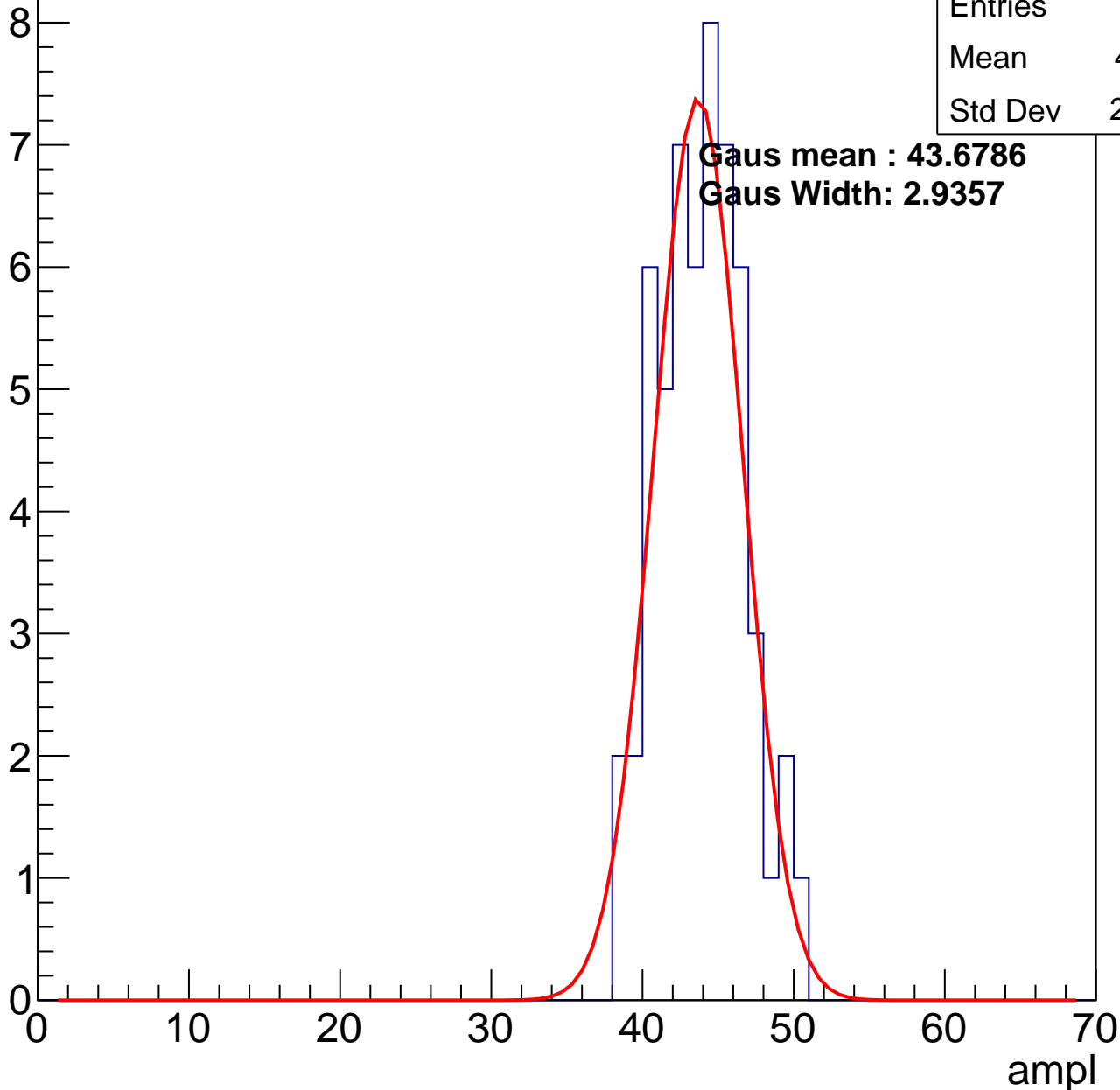
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.41
Std Dev	2.795

Gaus mean : 43.6786

Gaus Width: 2.9357

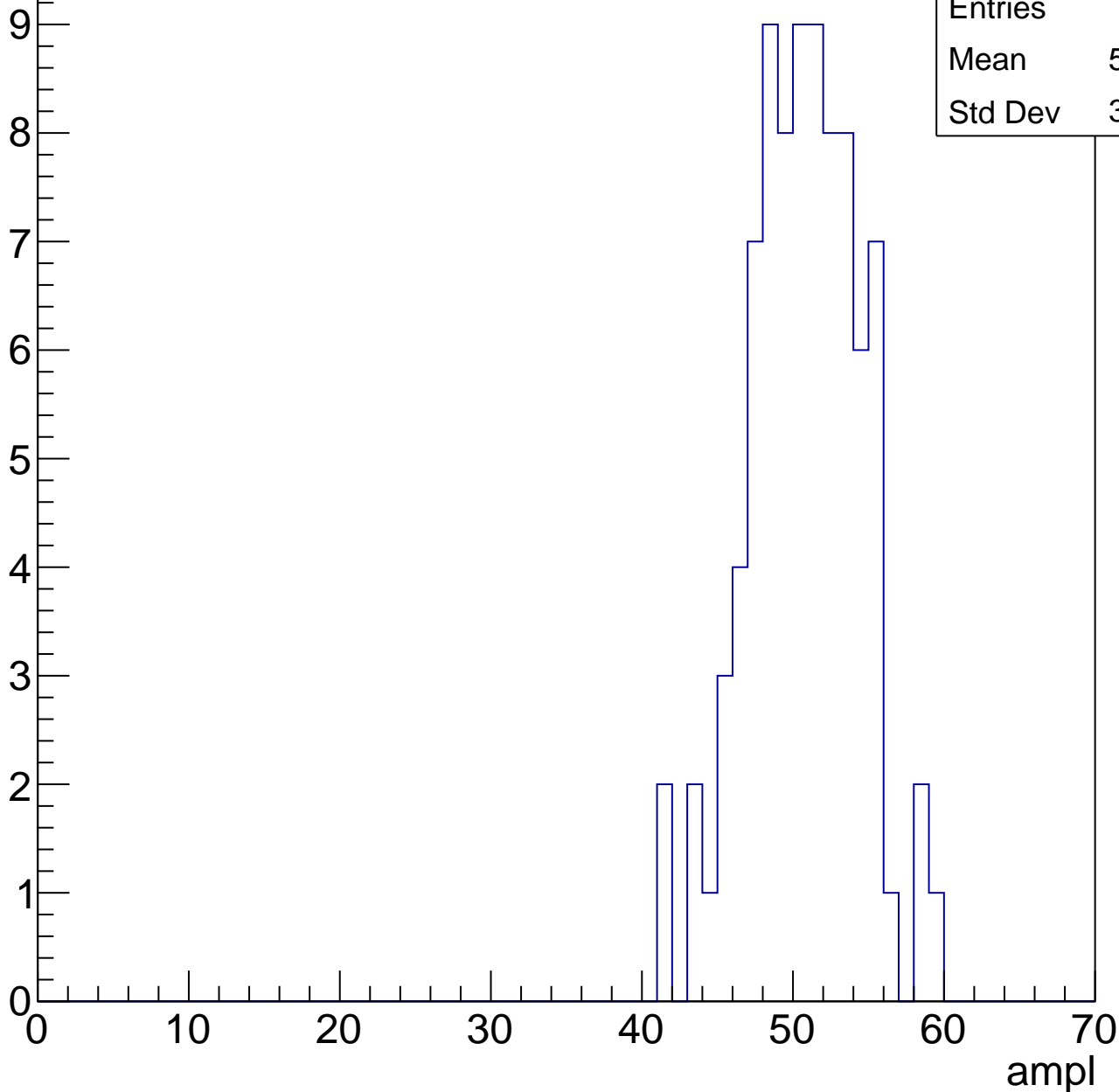


B0L002S, U5-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	50.26
Std Dev	3.659

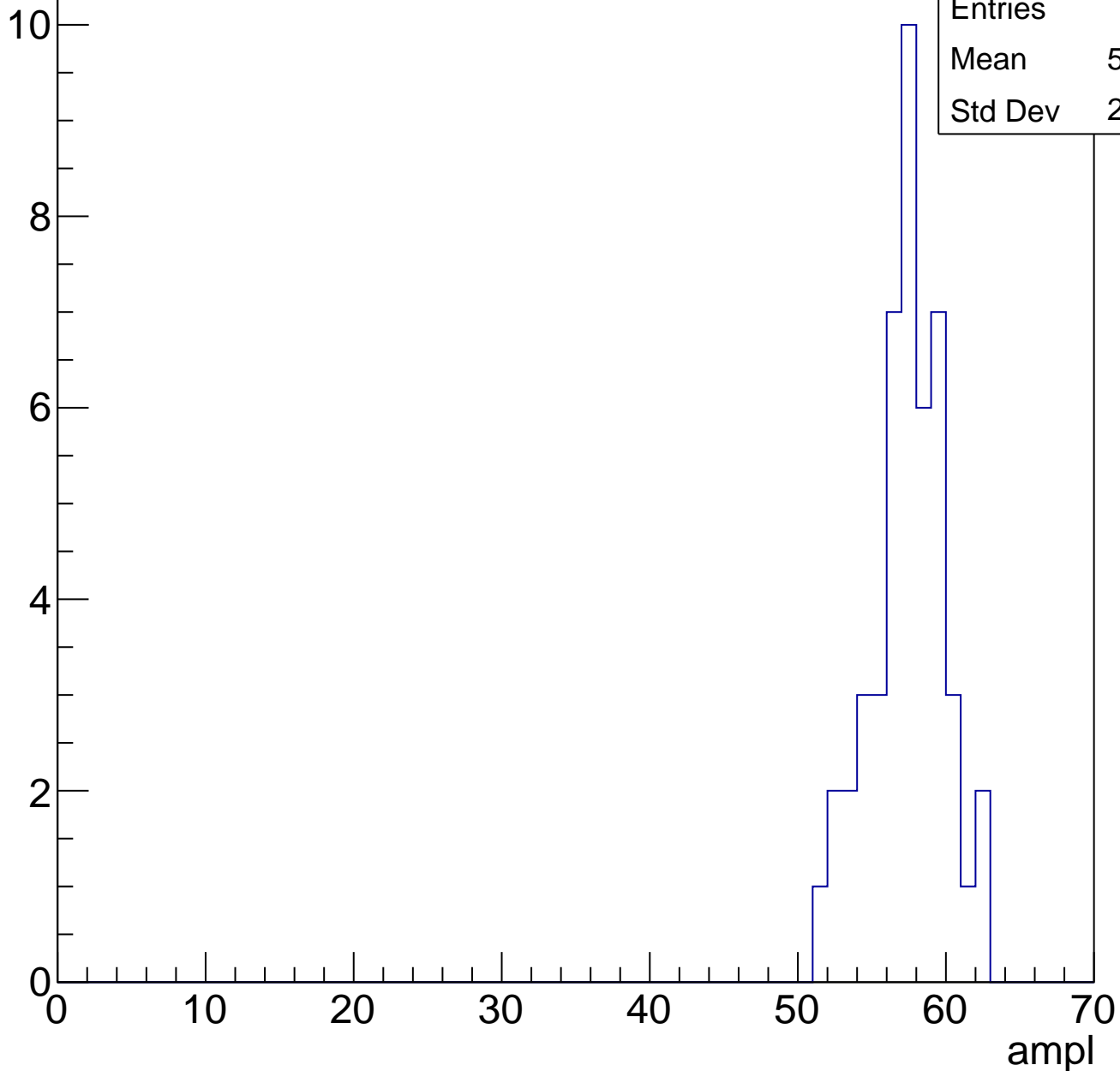


B0L002S, U5-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	47
Mean	56.94
Std Dev	2.487

Entry

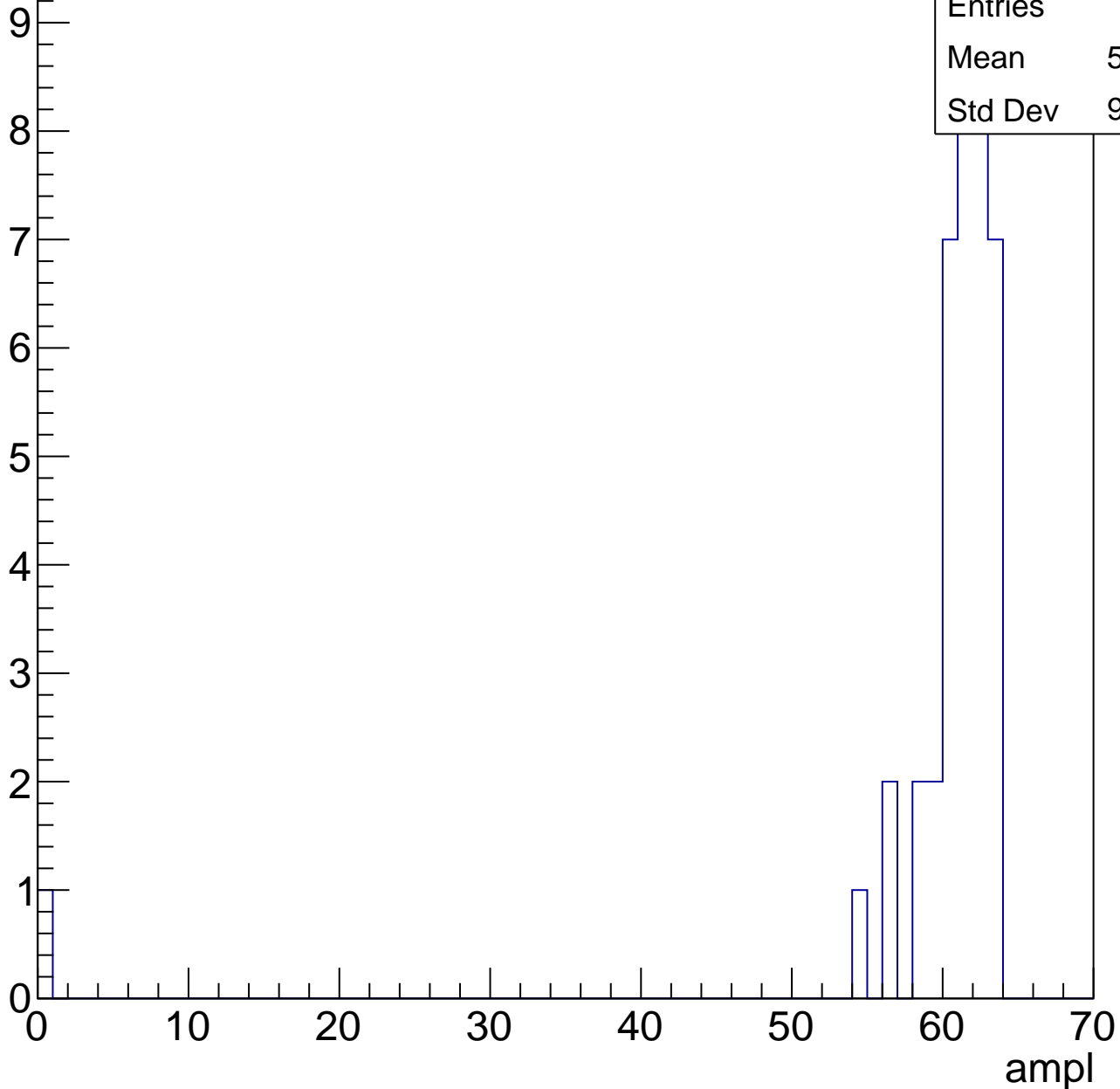


B0L002S, U5-ch89, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

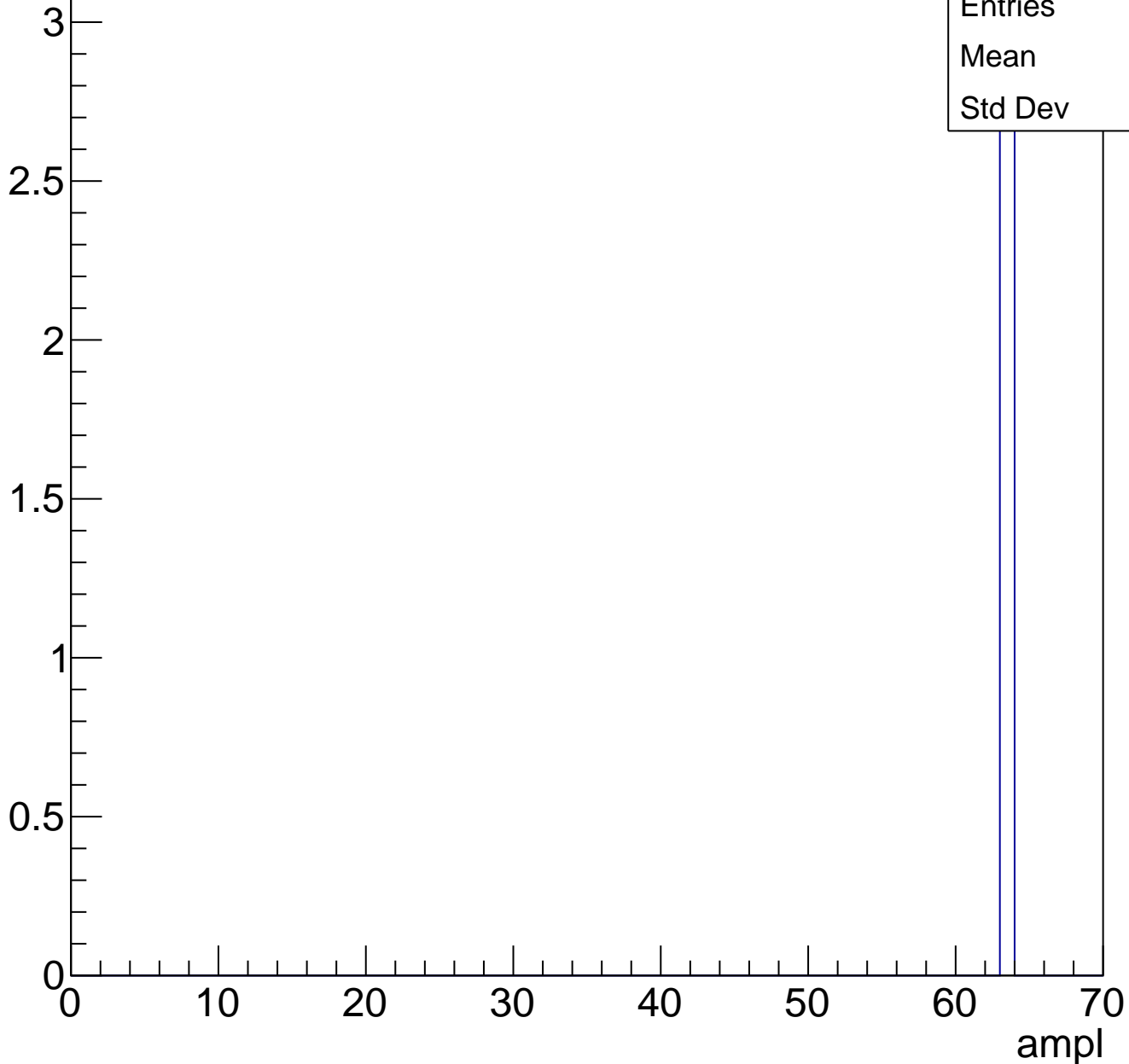
Entries	39
Mean	59.13
Std Dev	9.809



B0L002S, U5-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U5-ch90, adc0

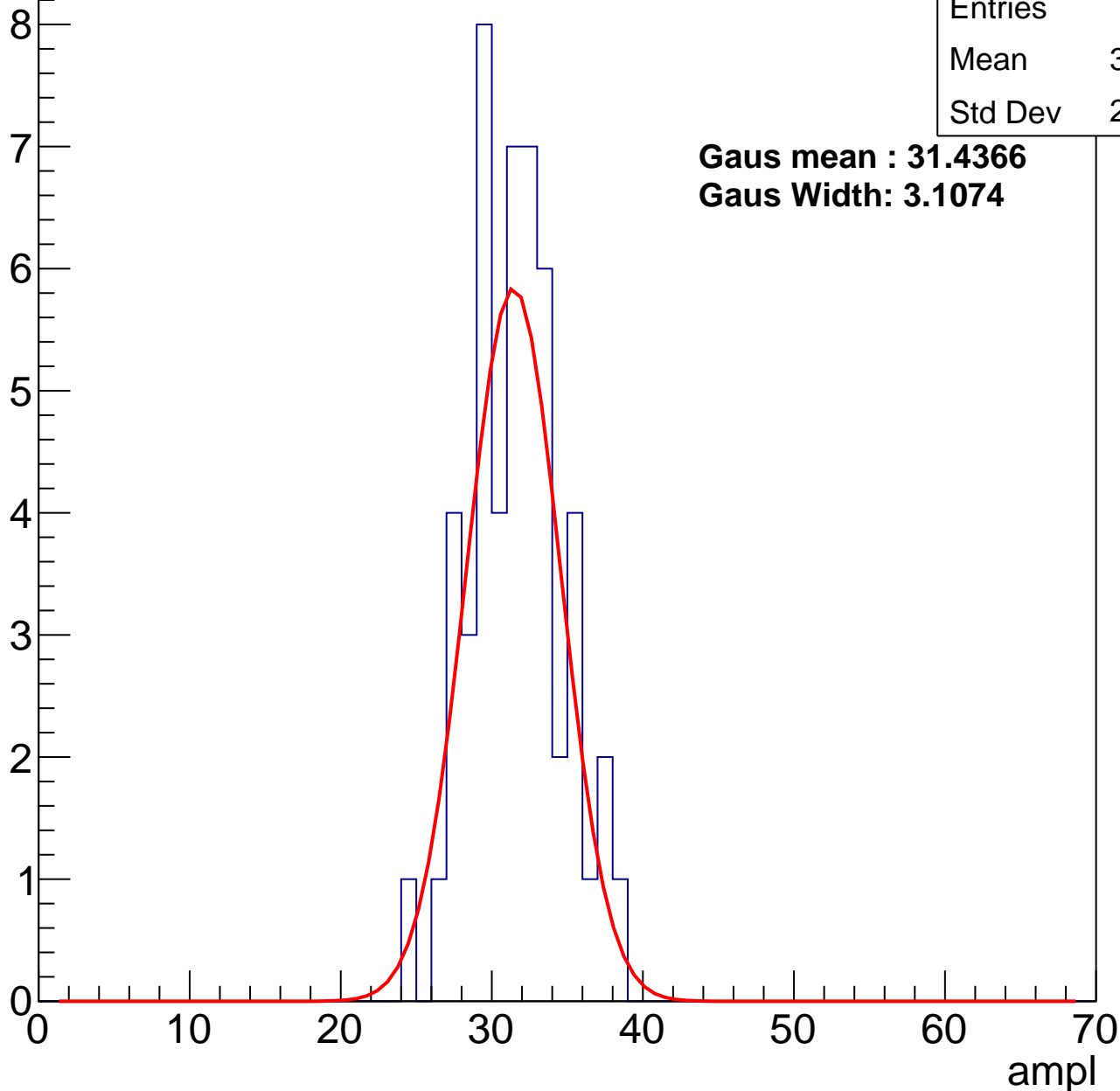
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	31.16
Std Dev	2.999

Gaus mean : 31.4366

Gaus Width: 3.1074



B0L002S, U5-ch90, adc1

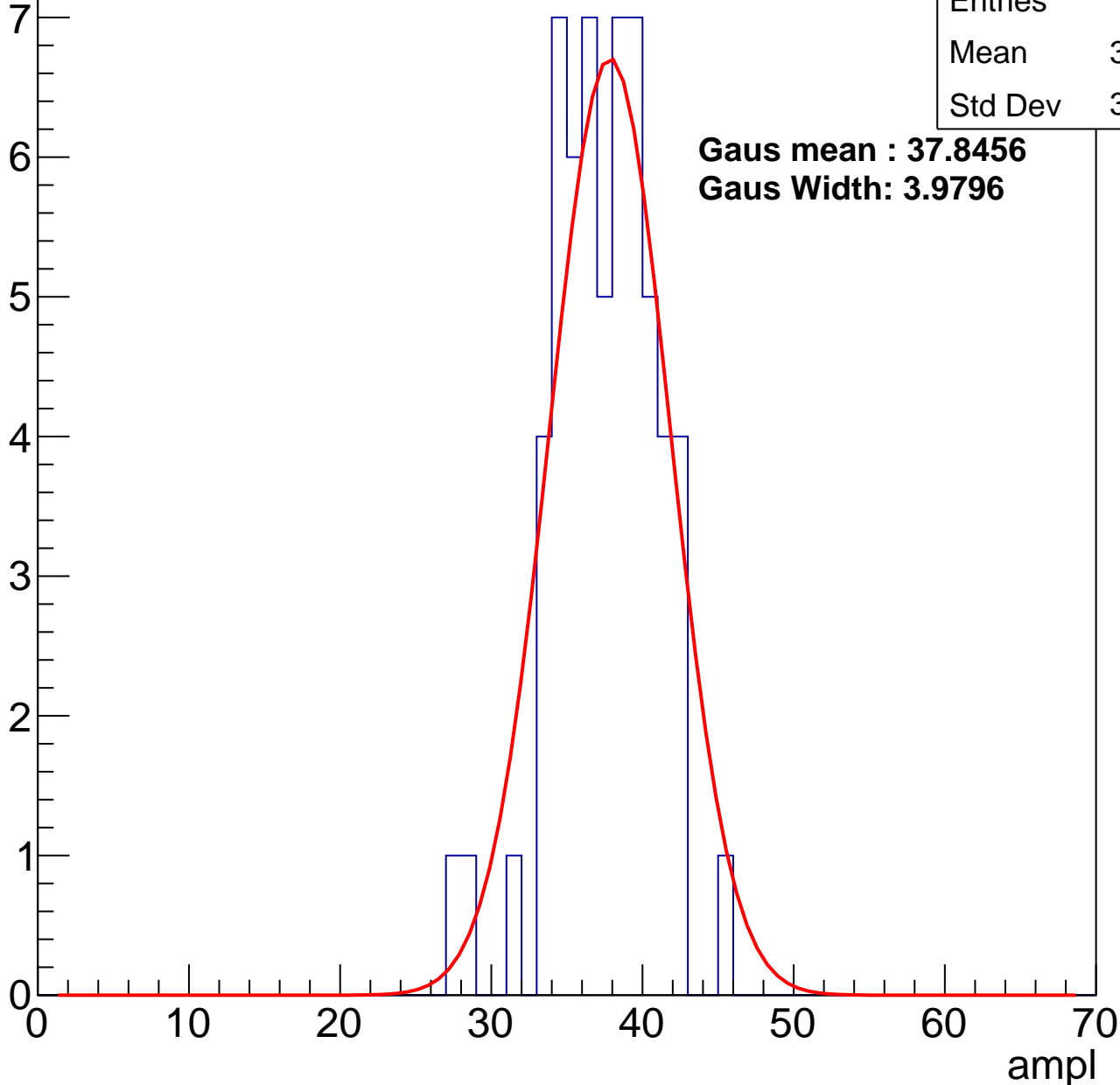
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.98
Std Dev	3.374

Gaus mean : 37.8456

Gaus Width: 3.9796



B0L002S, U5-ch90, adc2

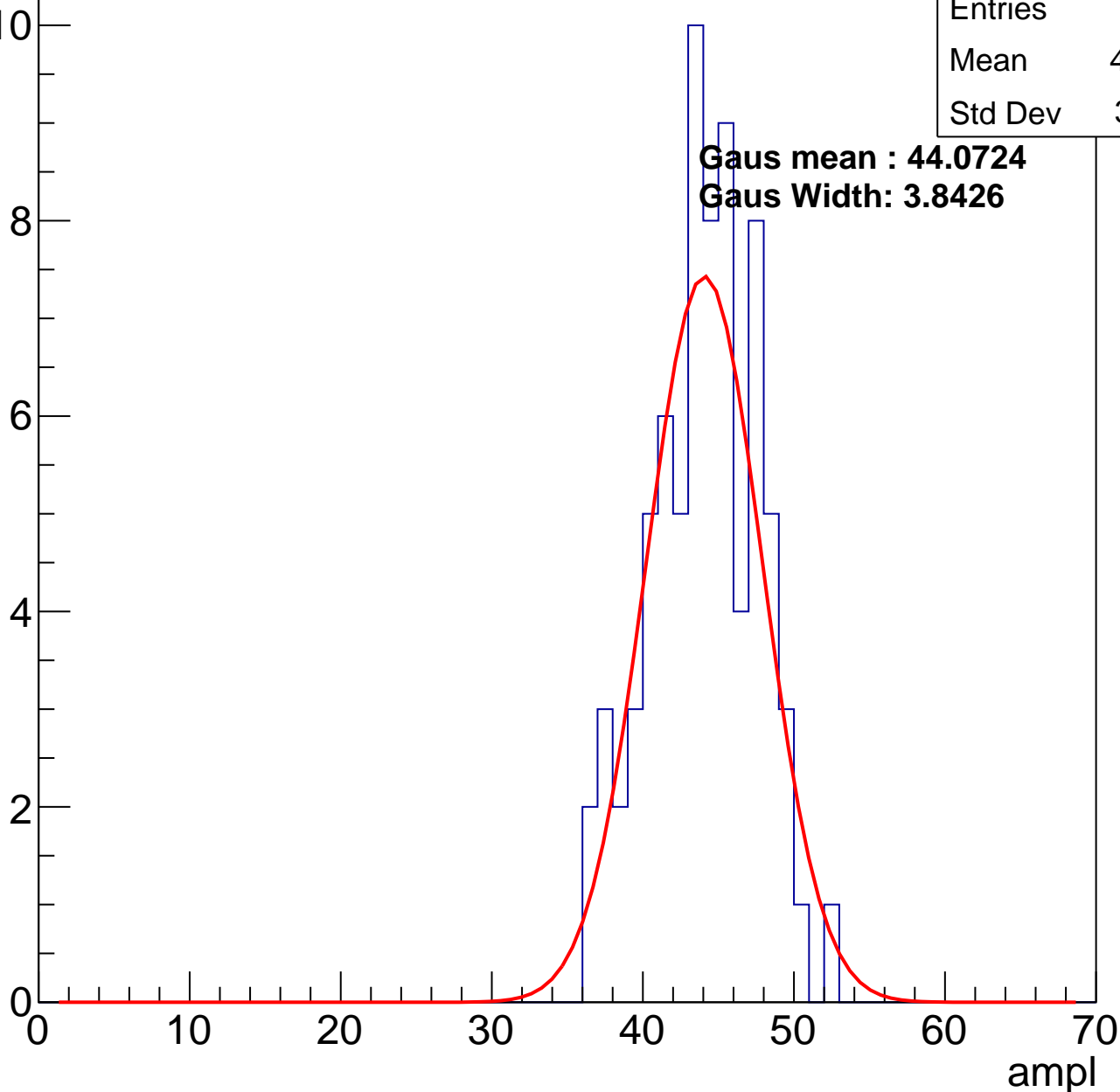
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	43.57
Std Dev	3.511

Gaus mean : 44.0724

Gaus Width: 3.8426

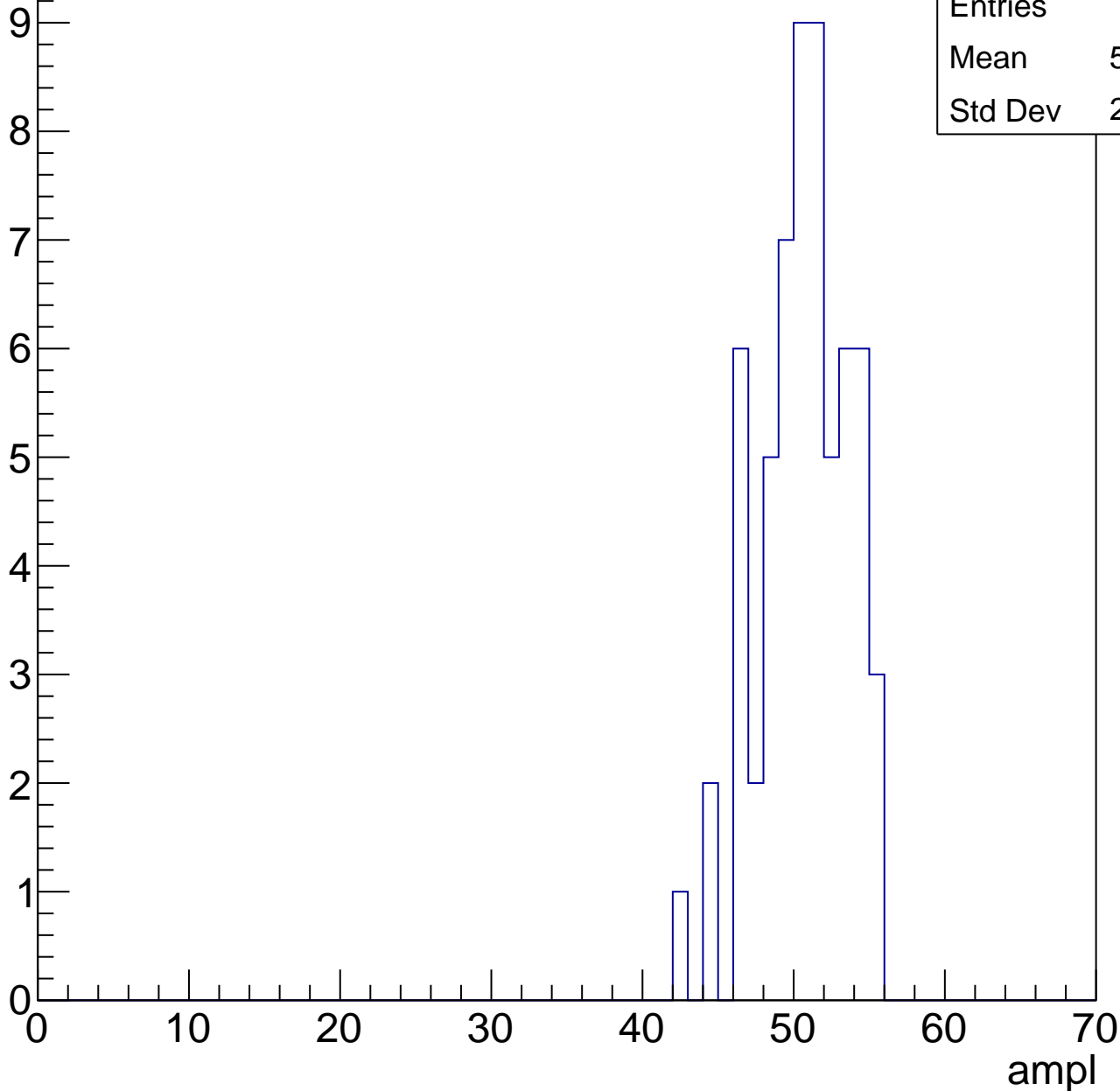


B0L002S, U5-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	50.15
Std Dev	2.947

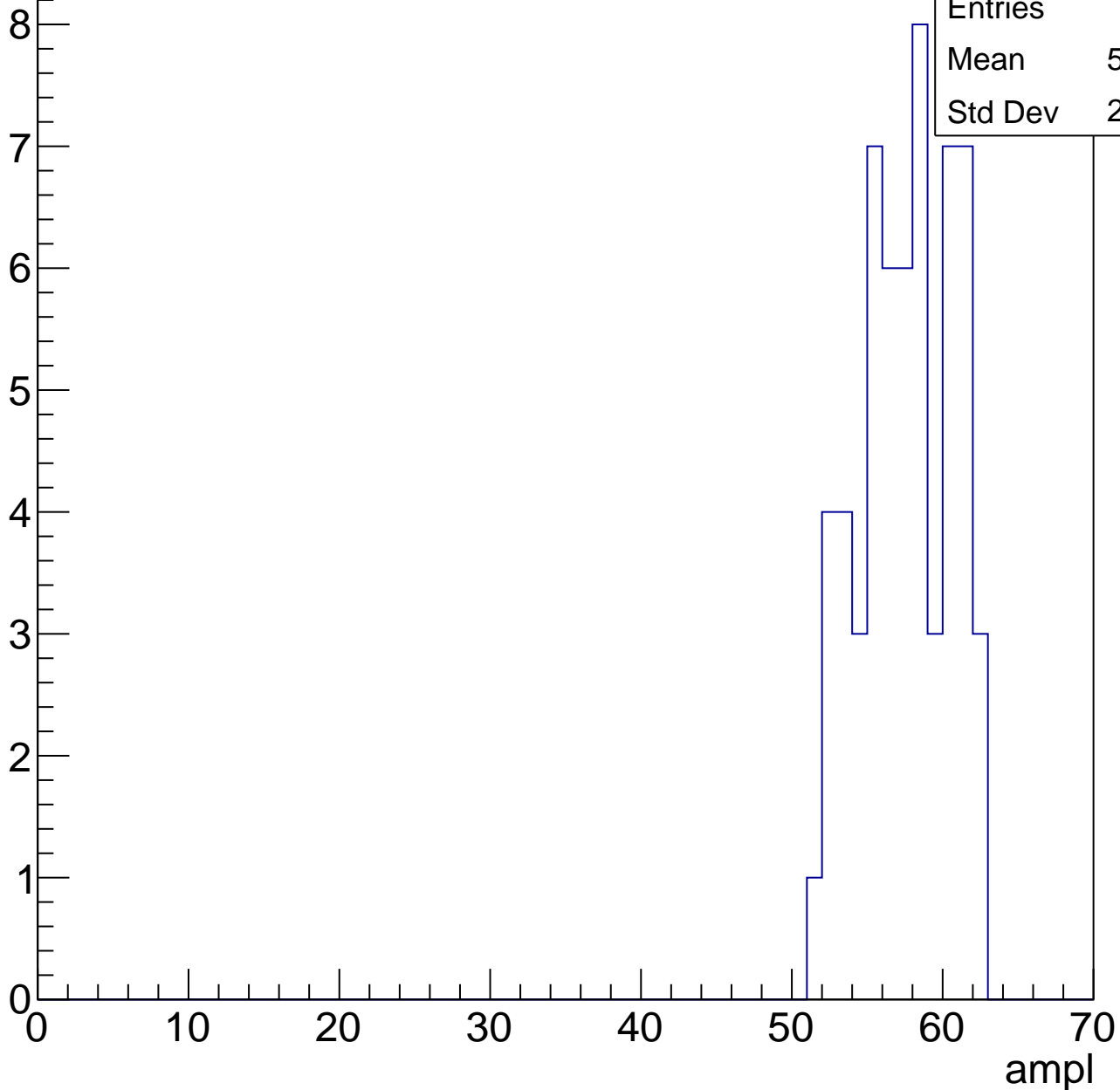


B0L002S, U5-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	57.12
Std Dev	2.998

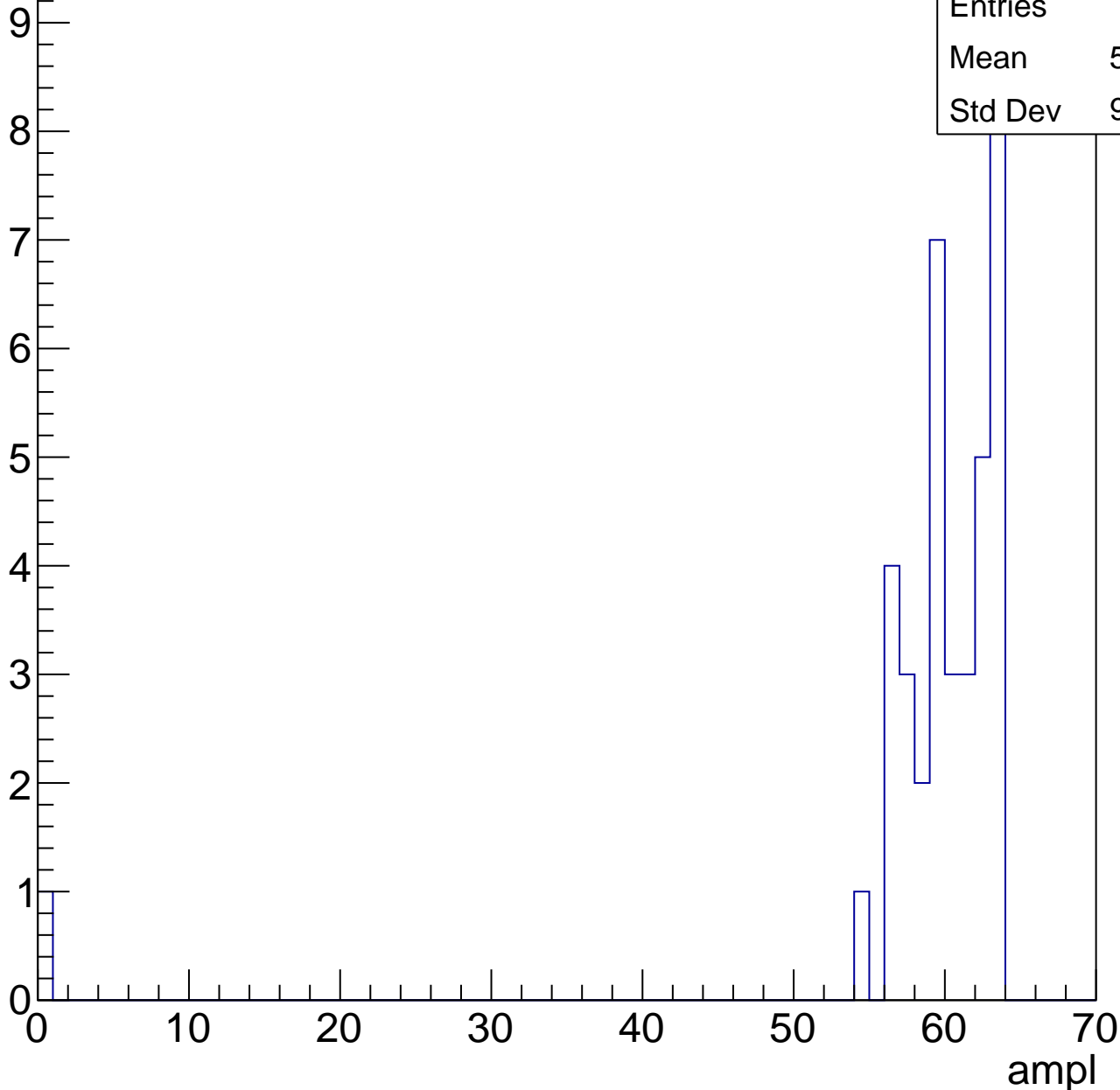


B0L002S, U5-ch90, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.37
Std Dev	9.927



B0L002S, U5-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

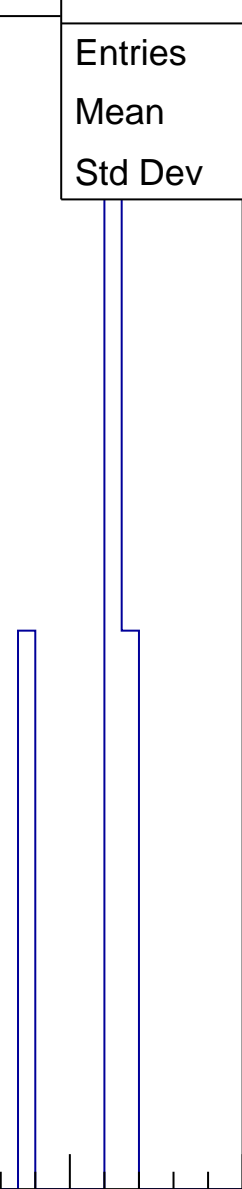
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61
Std Dev	2.345

0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U5-ch91, adc0

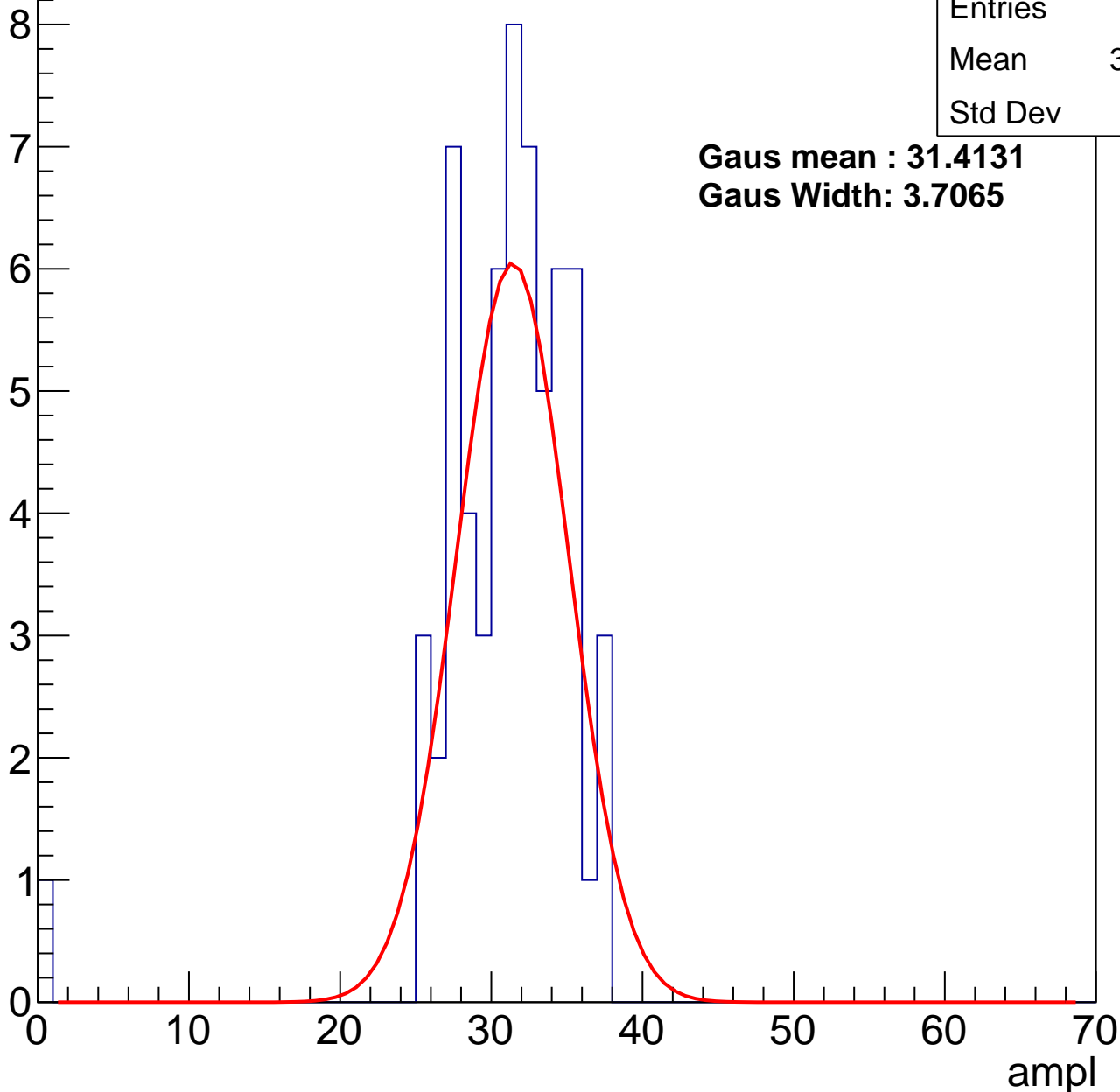
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.53
Std Dev	5.05

Gaus mean : 31.4131

Gaus Width: 3.7065



B0L002S, U5-ch91, adc1

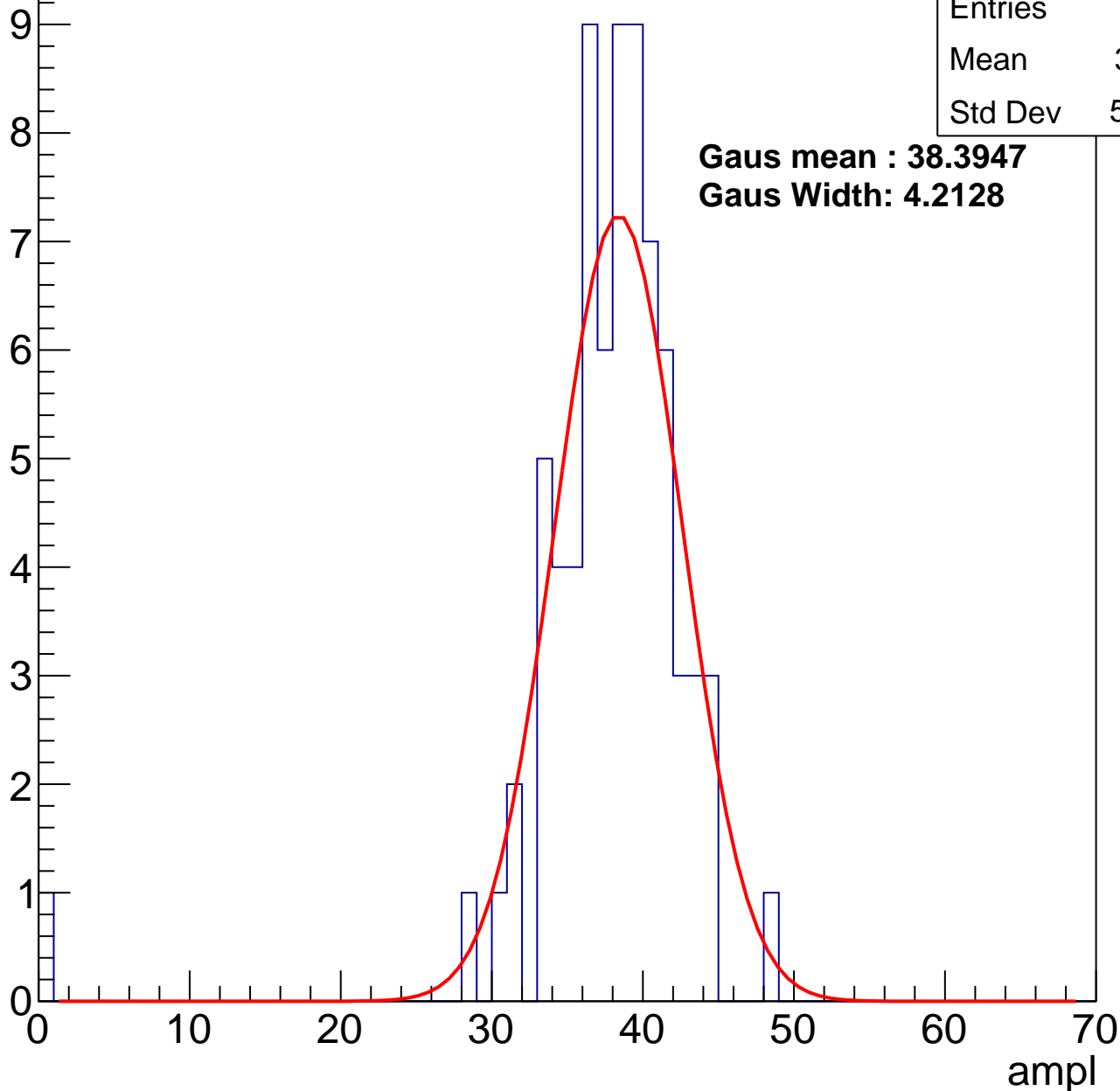
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.31
Std Dev	5.647

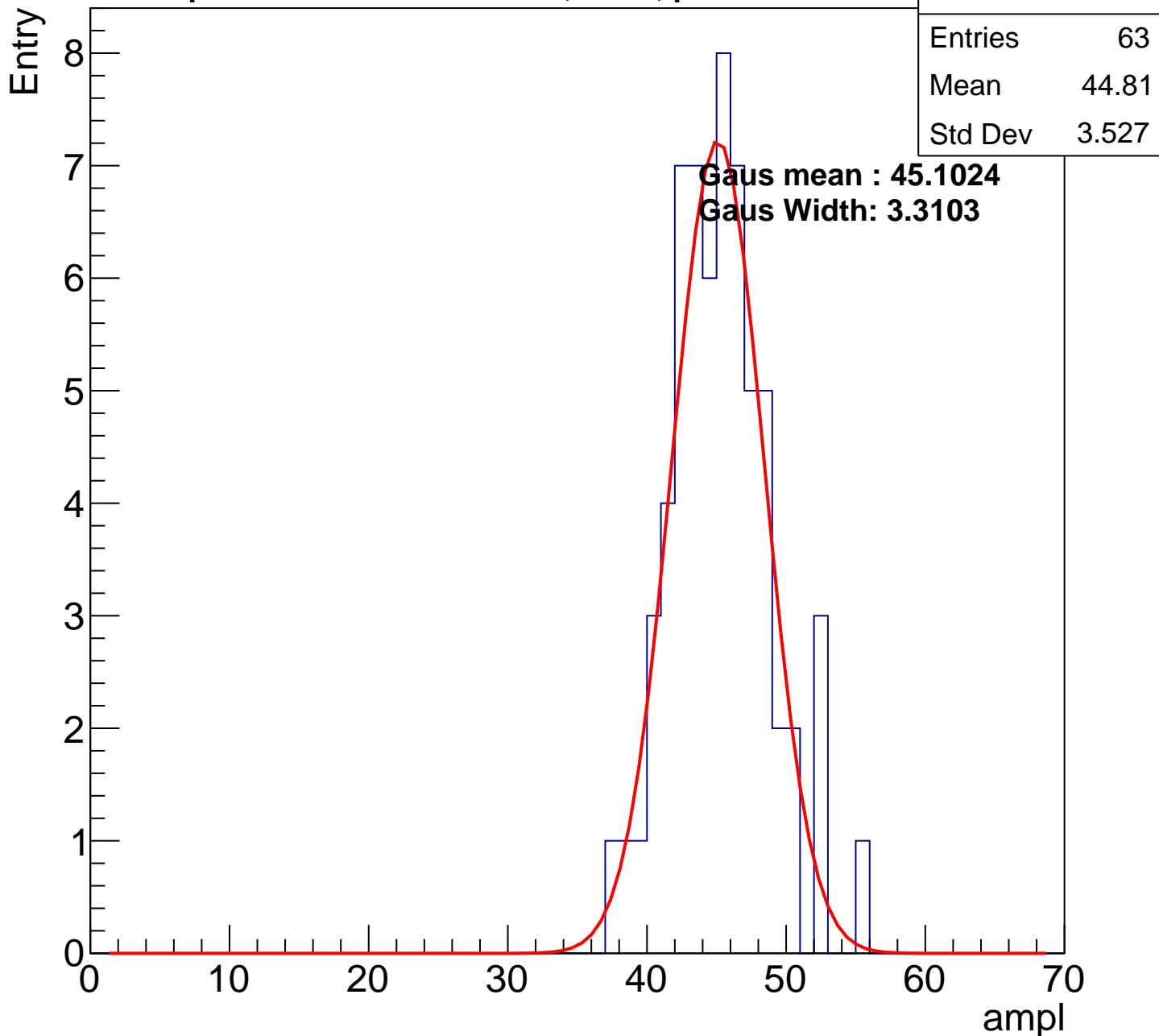
Gaus mean : 38.3947

Gaus Width: 4.2128



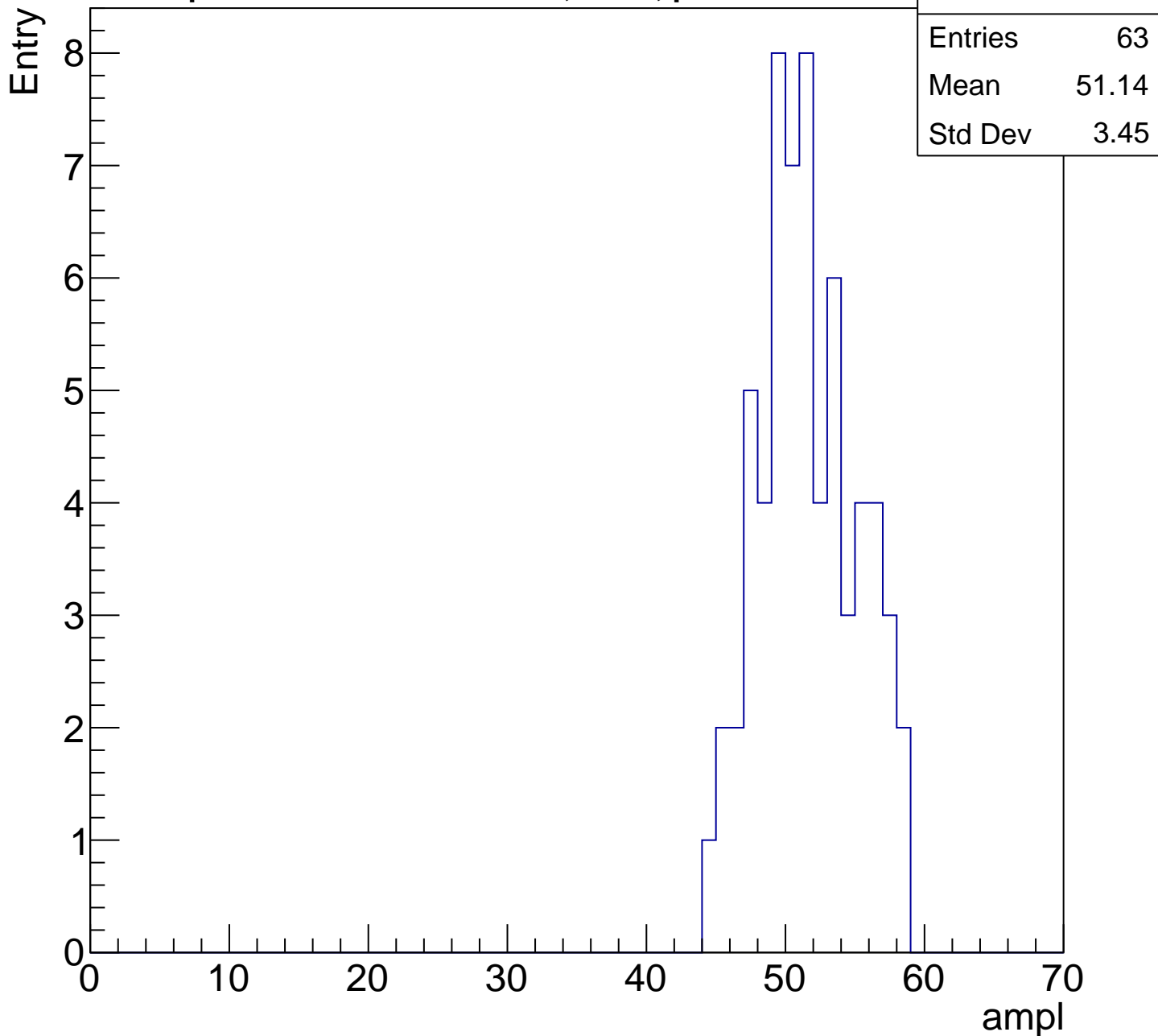
B0L002S, U5-ch91, adc2

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

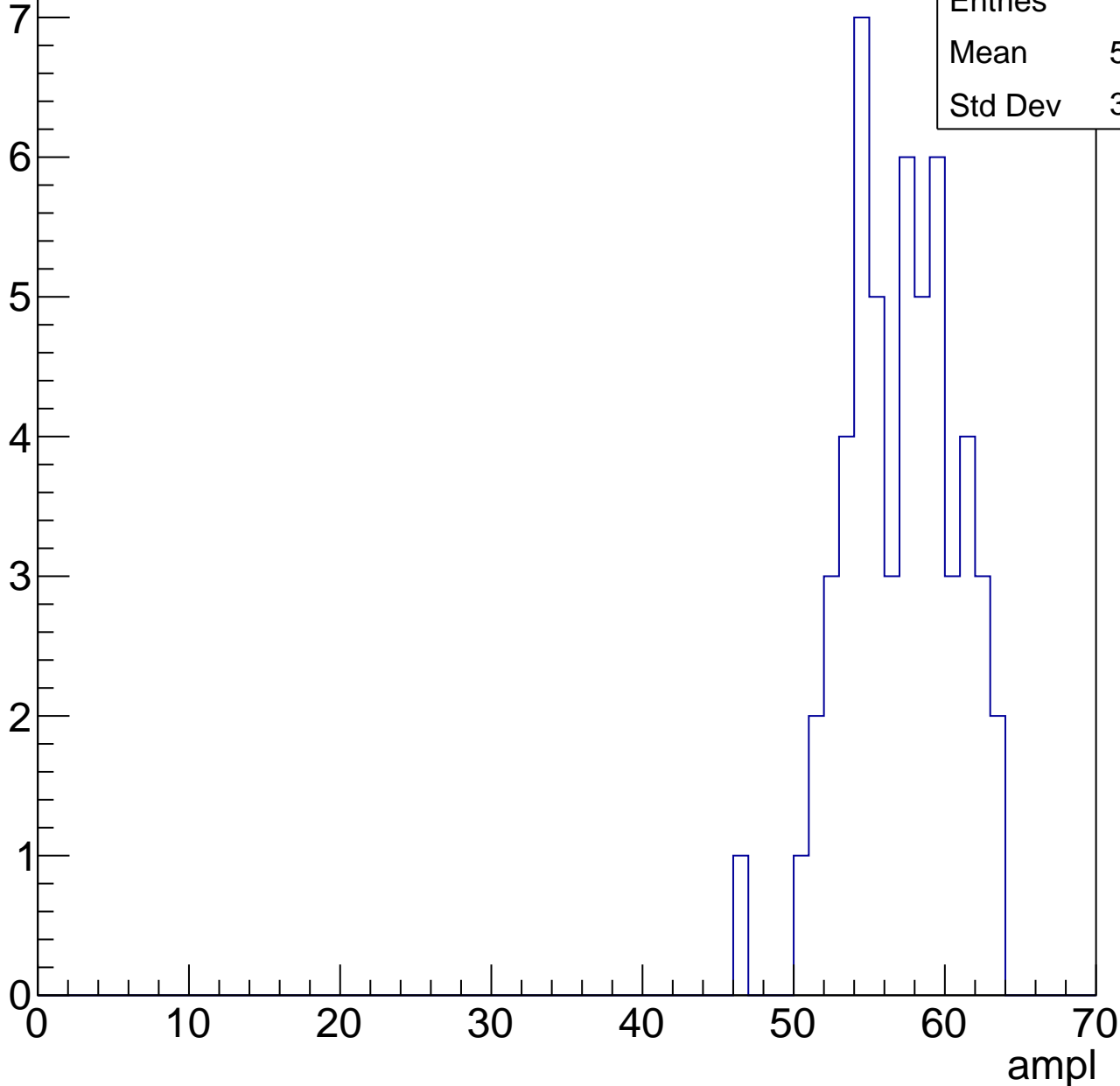


B0L002S, U5-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

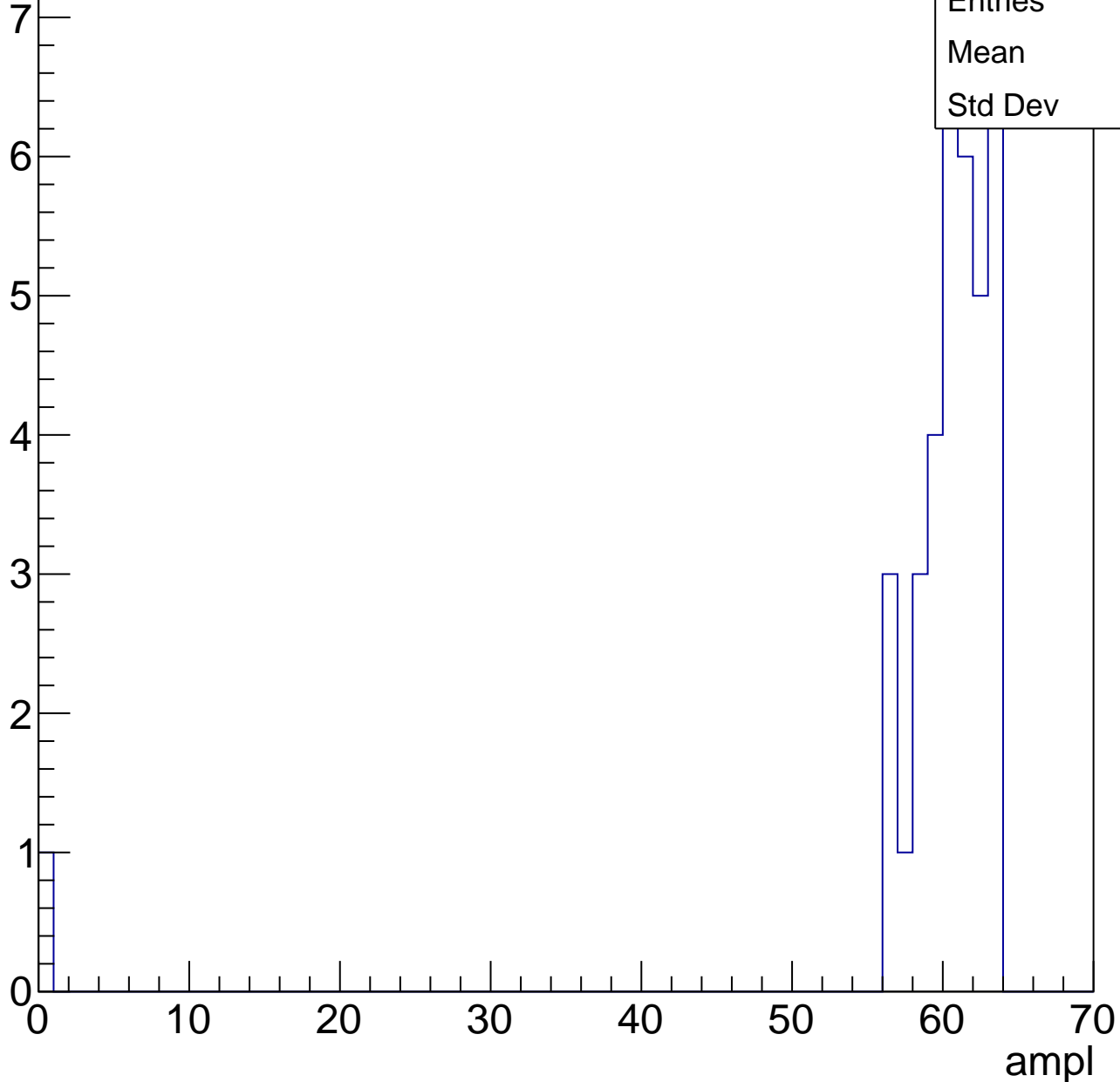
Entries	55
Mean	56.53
Std Dev	3.622



B0L002S, U5-ch91, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch92, adc0

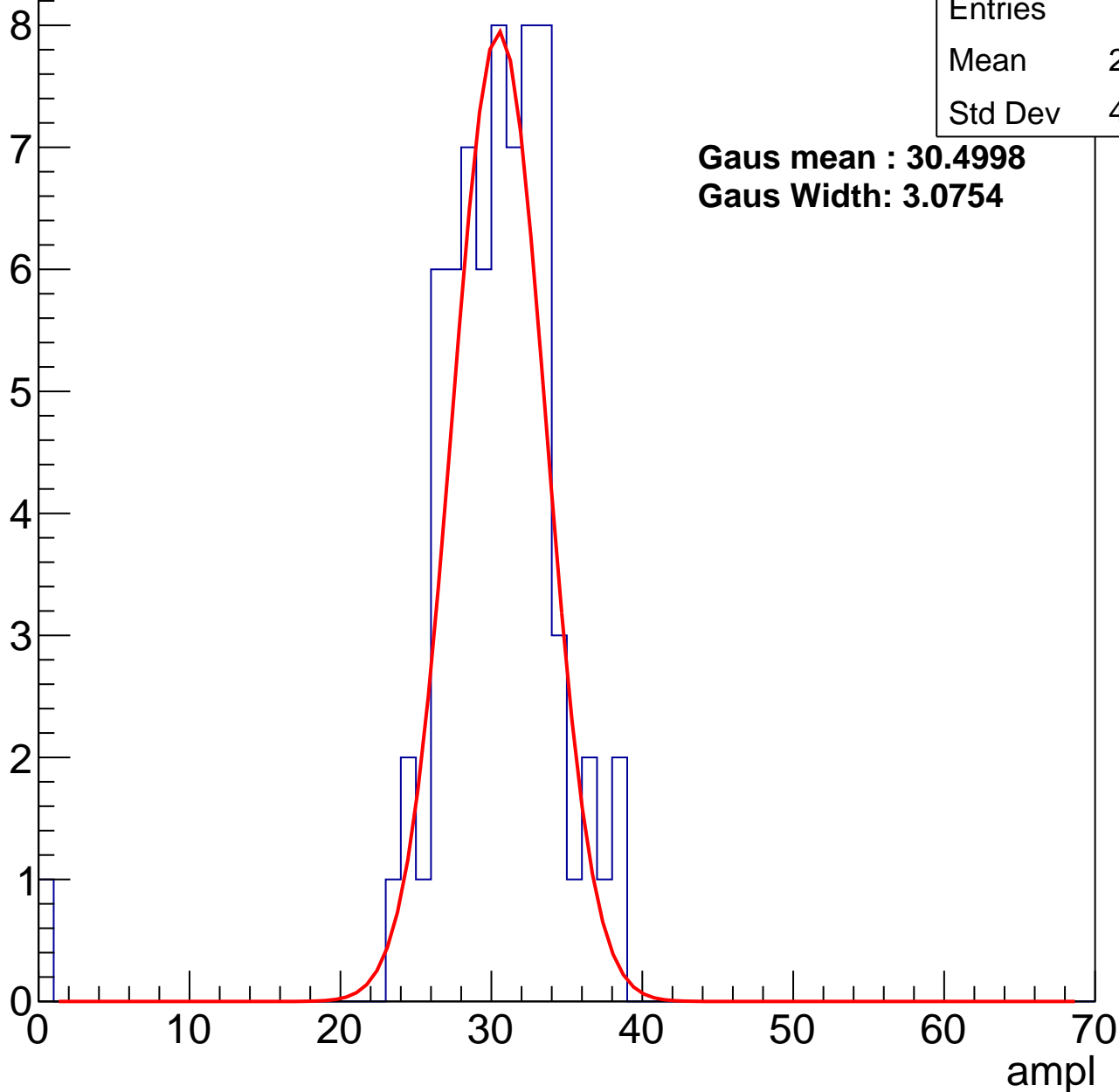
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.76
Std Dev	4.868

Gaus mean : 30.4998

Gaus Width: 3.0754



B0L002S, U5-ch92, adc1

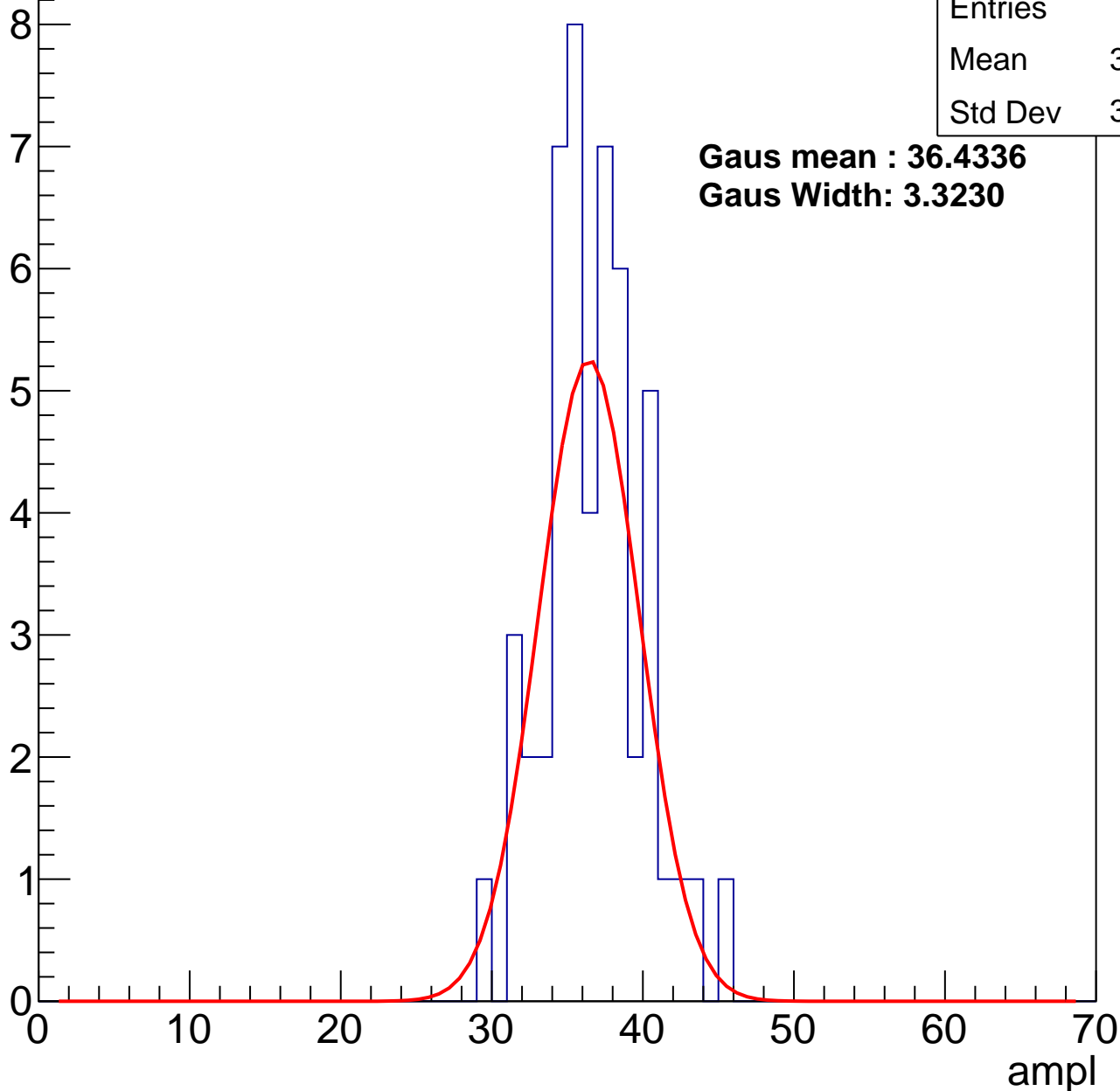
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	36.27
Std Dev	3.212

Gaus mean : 36.4336

Gaus Width: 3.3230



B0L002S, U5-ch92, adc2

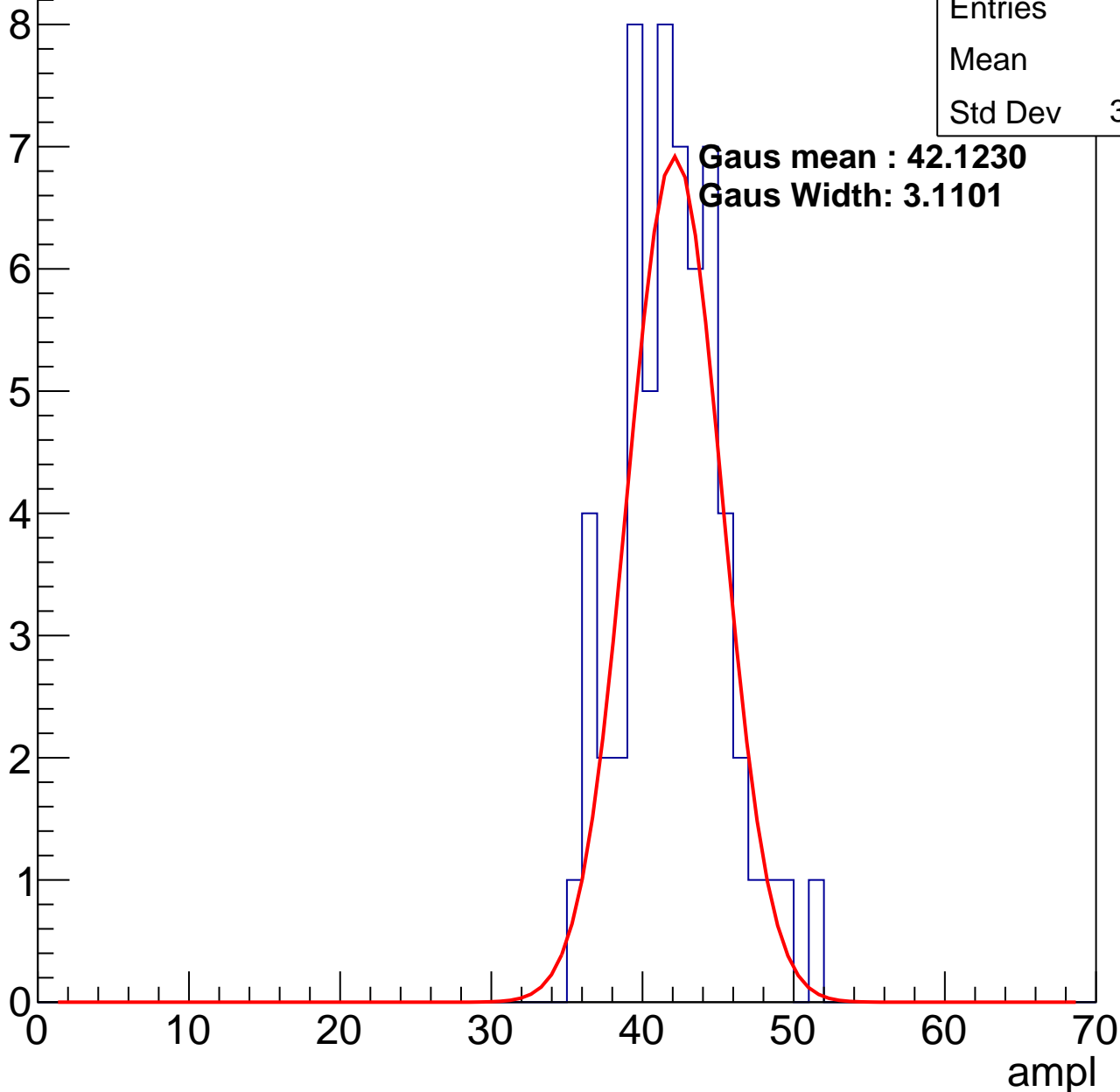
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	41.6
Std Dev	3.313

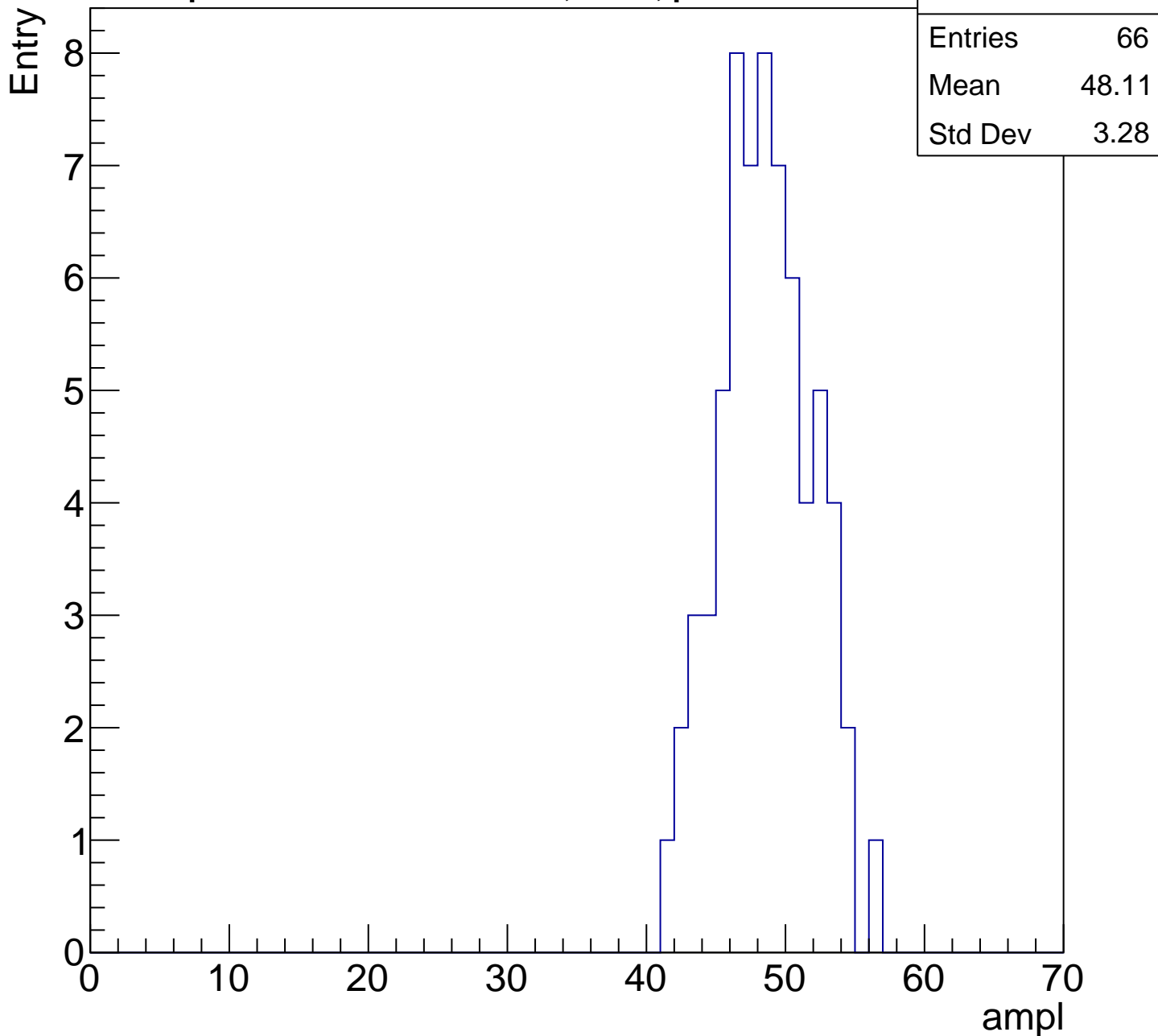
Gaus mean : 42.1230

Gaus Width: 3.1101



B0L002S, U5-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

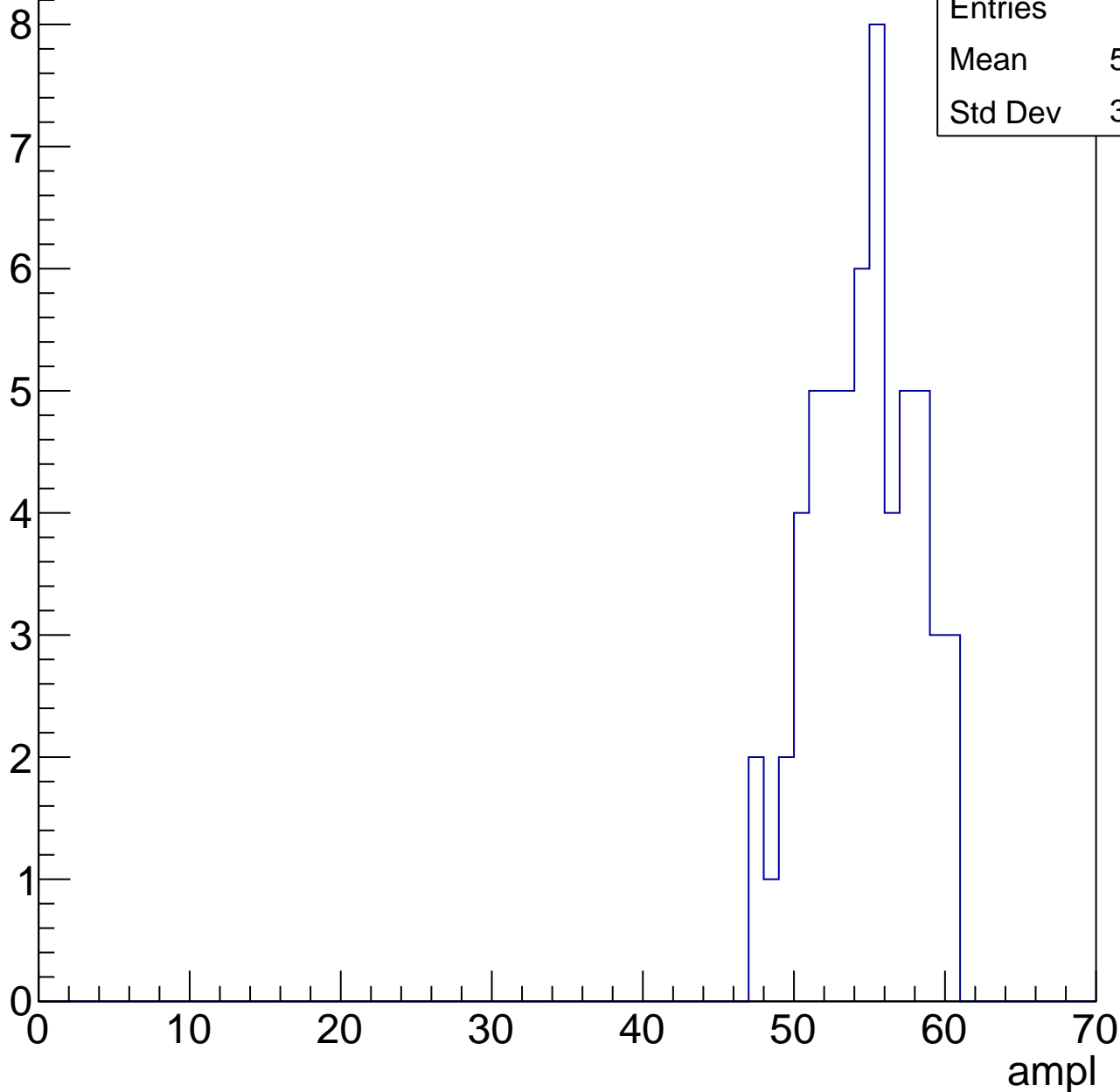


B0L002S, U5-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	54.14
Std Dev	3.345



B0L002S, U5-ch92, adc5

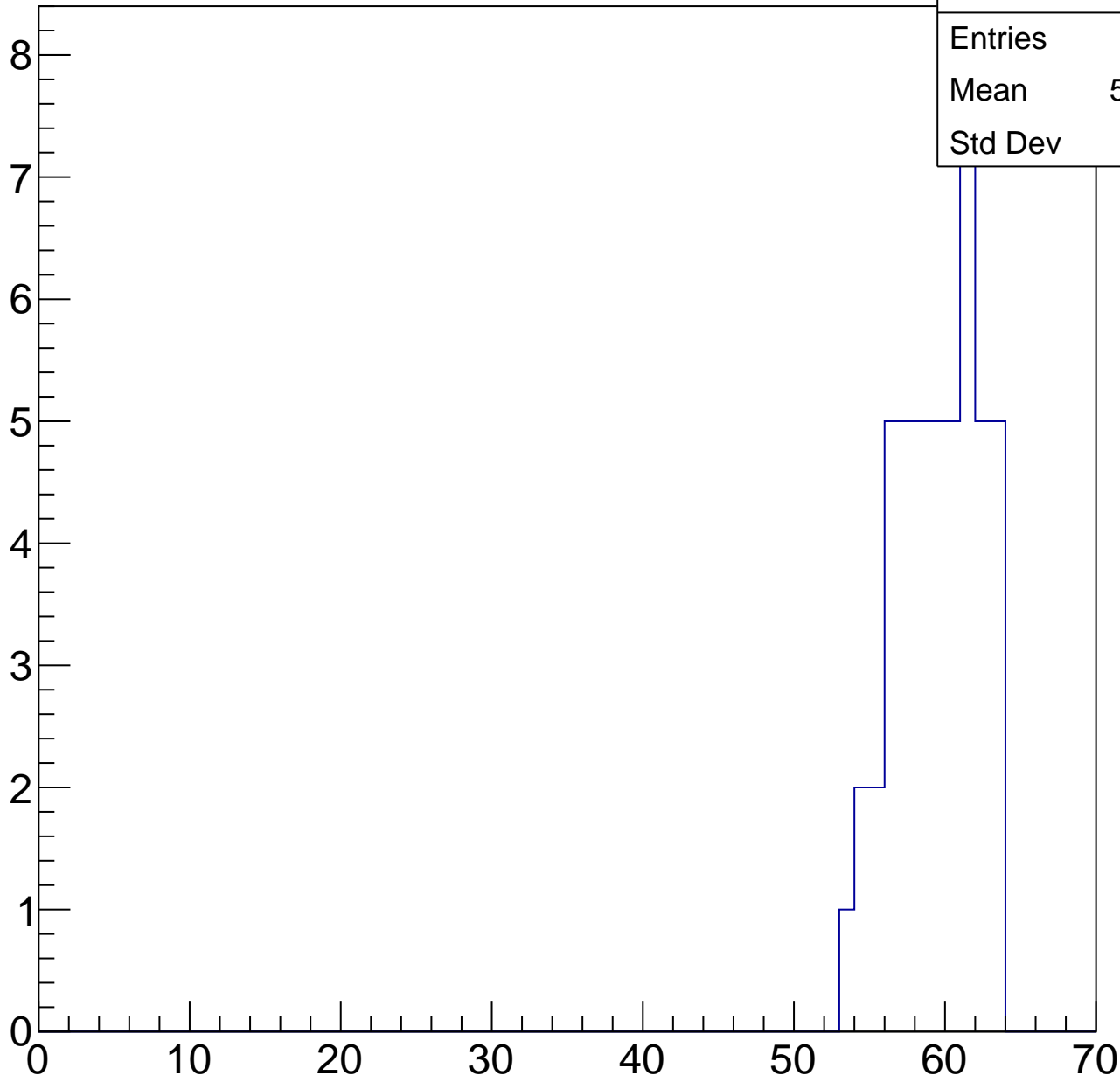
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.04
Std Dev	2.7

ampl

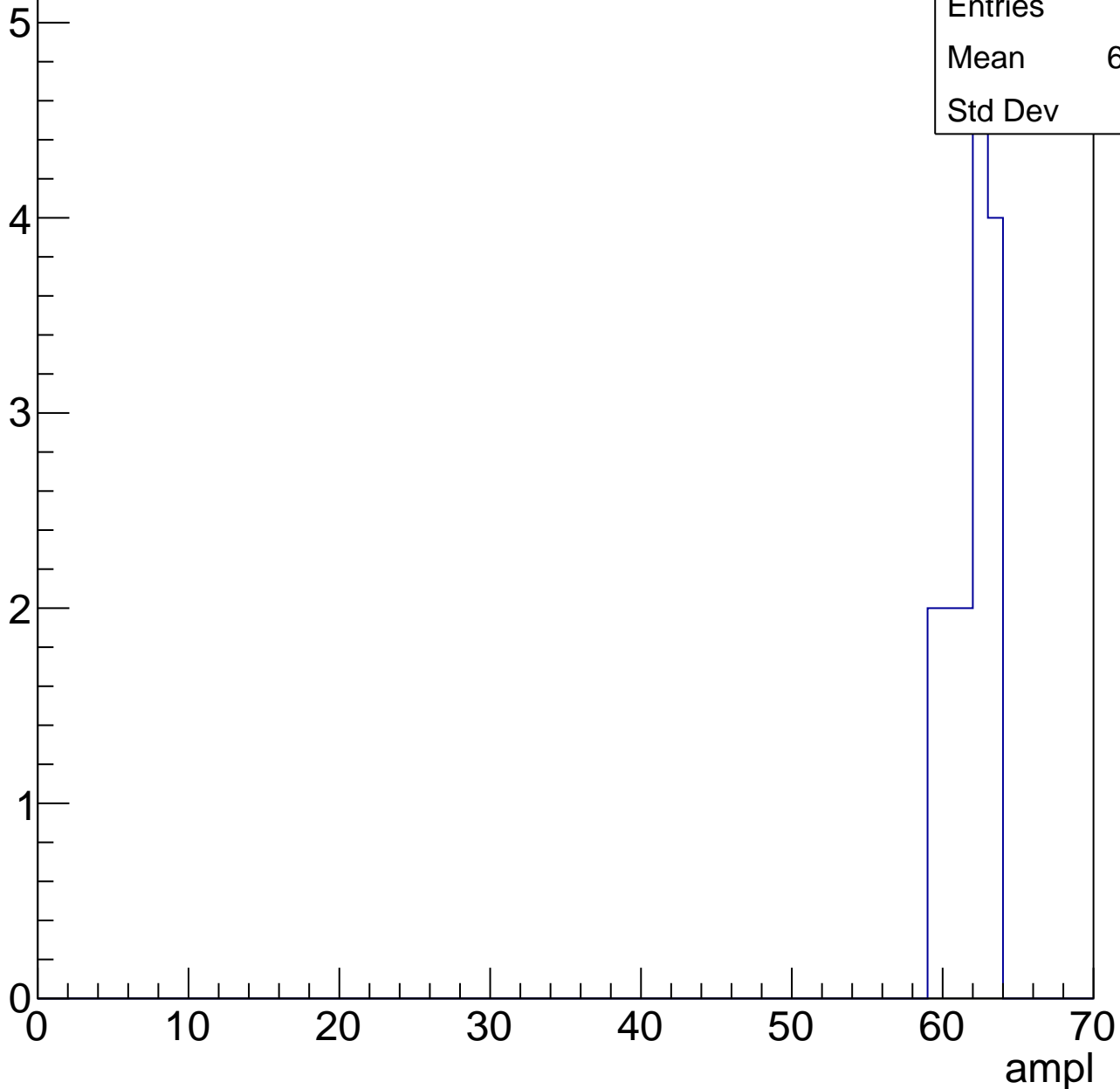


B0L002S, U5-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

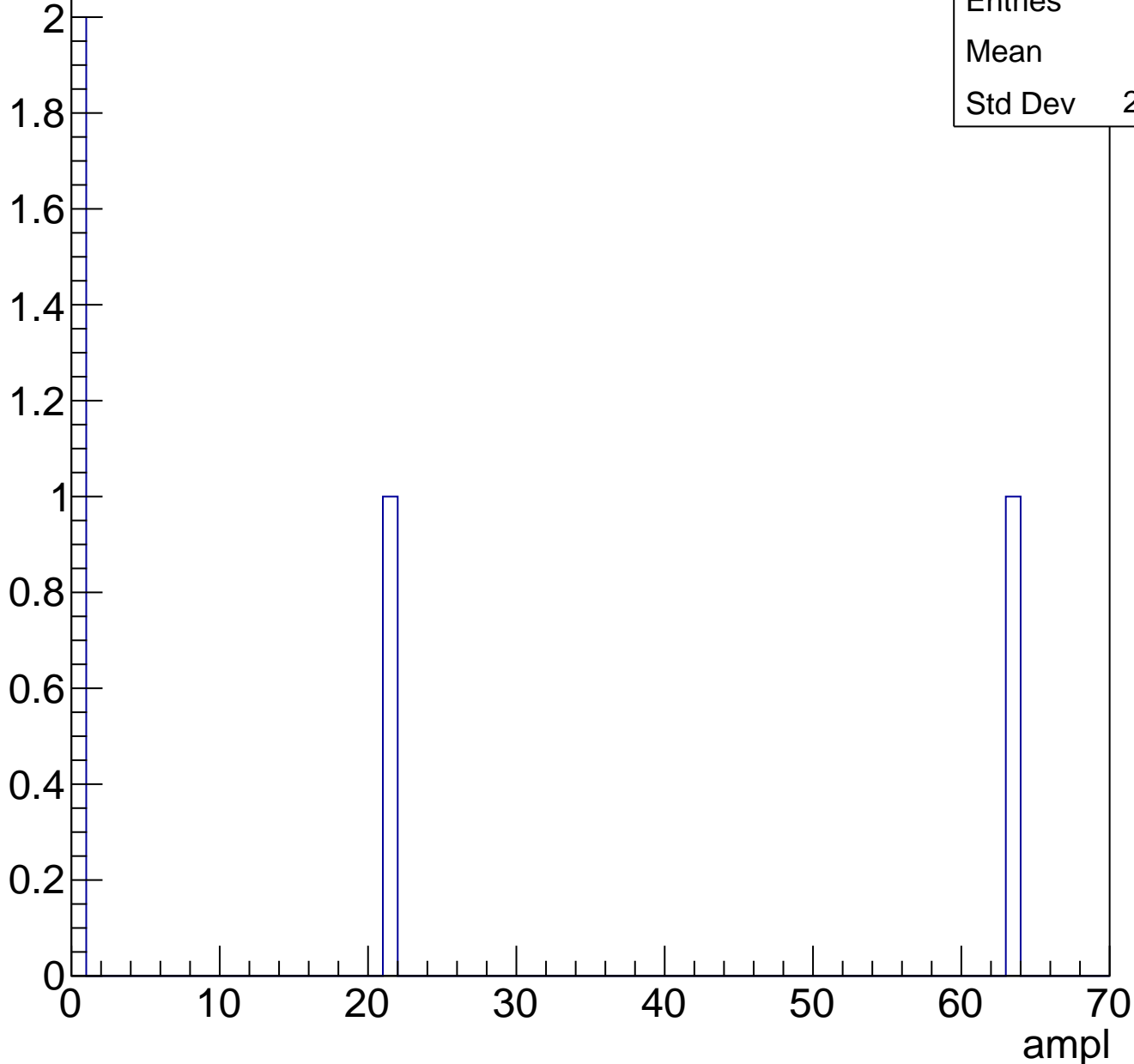
Entries	15
Mean	61.47
Std Dev	1.36



B0L002S, U5-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	21
Std Dev	25.72

B0L002S, U5-ch93, adc0

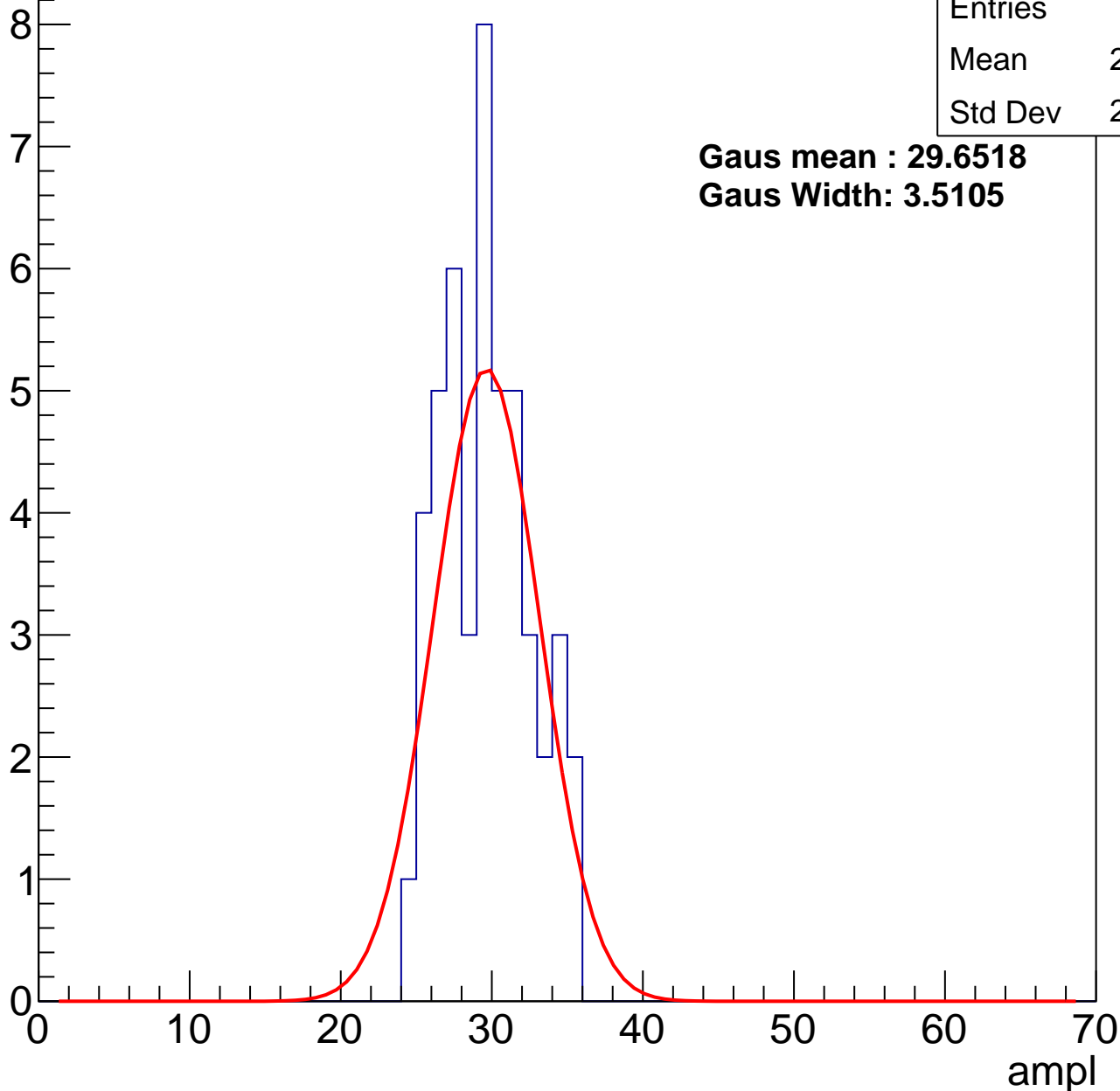
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	29.17
Std Dev	2.883

Gaus mean : 29.6518

Gaus Width: 3.5105



B0L002S, U5-ch93, adc1

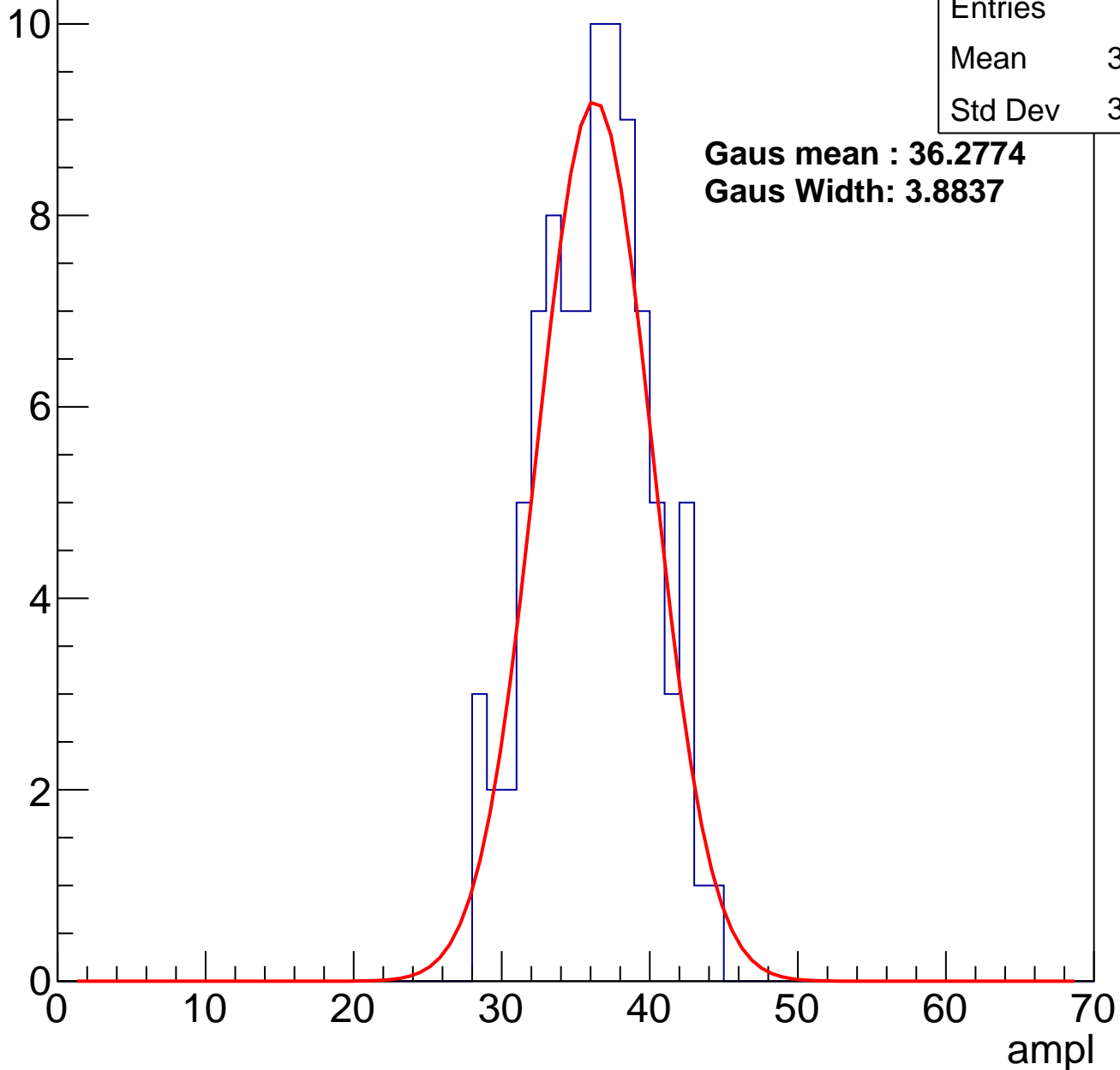
calib_packv5_042523_0143.root, FC#8, port C1

Entries	92
Mean	35.79
Std Dev	3.699

Gaus mean : 36.2774

Gaus Width: 3.8837

Entry



B0L002S, U5-ch93, adc2

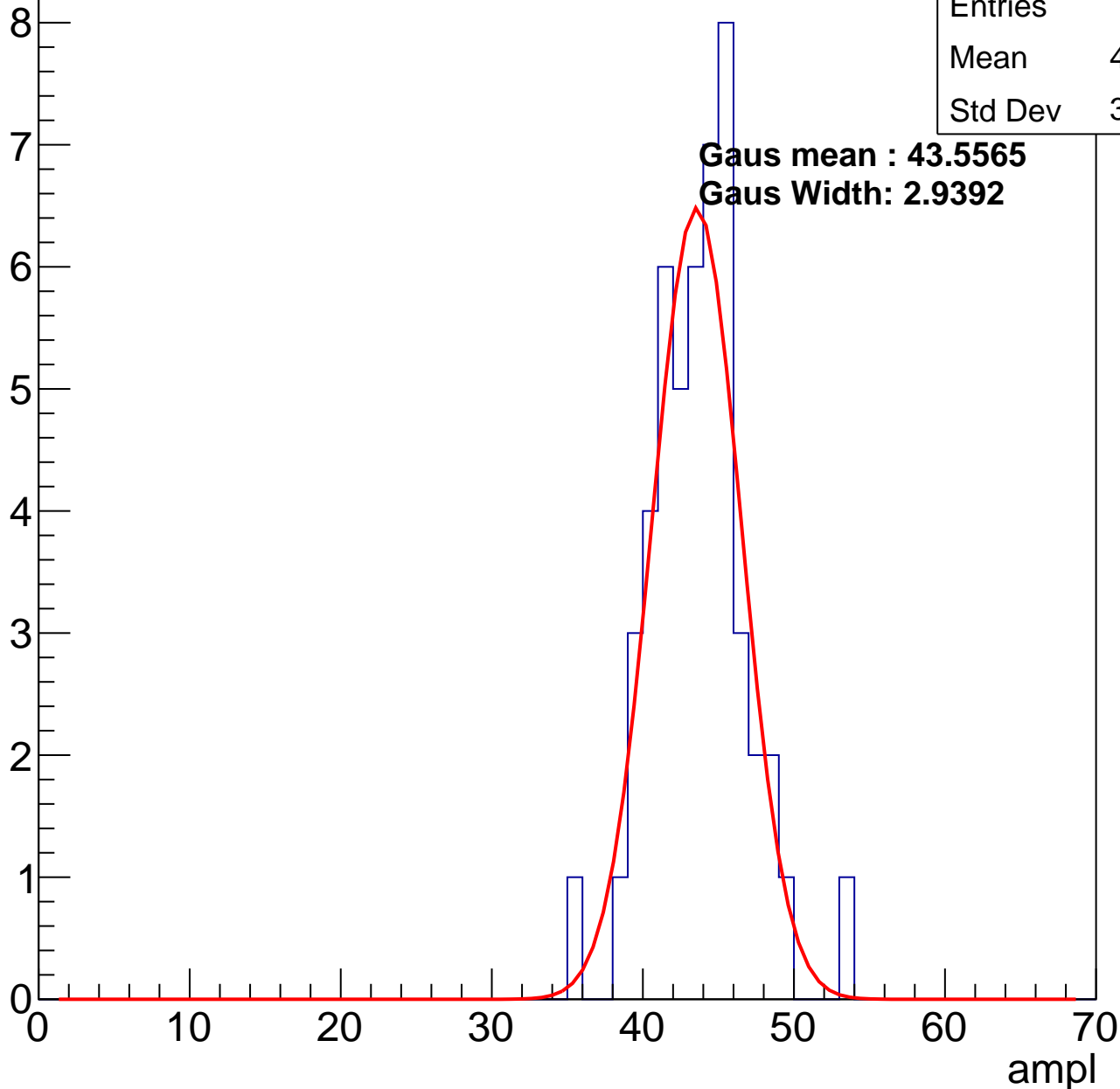
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	43.24
Std Dev	3.108

Gaus mean : 43.5565

Gaus Width: 2.9392

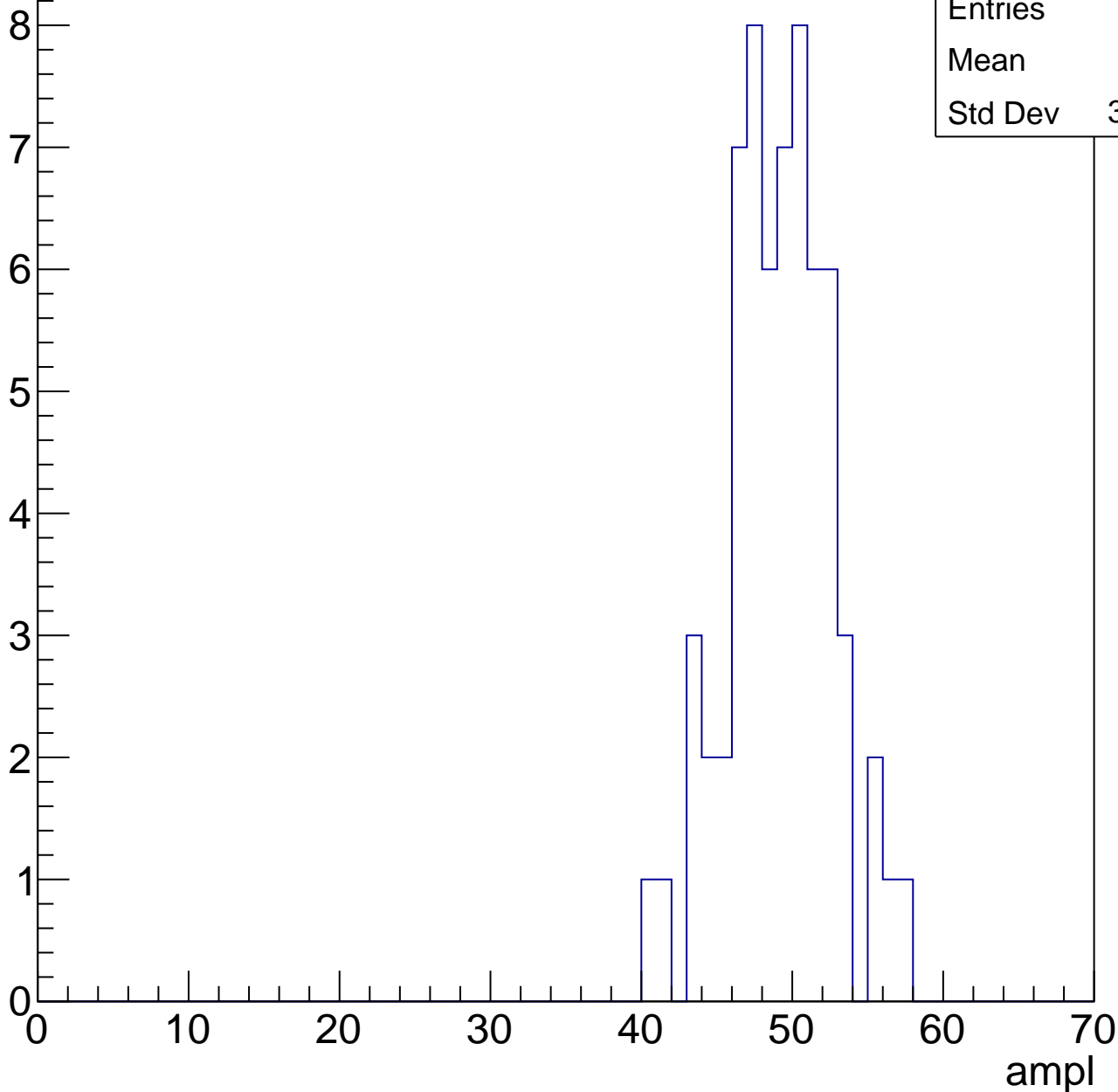


B0L002S, U5-ch93, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	48.7
Std Dev	3.422

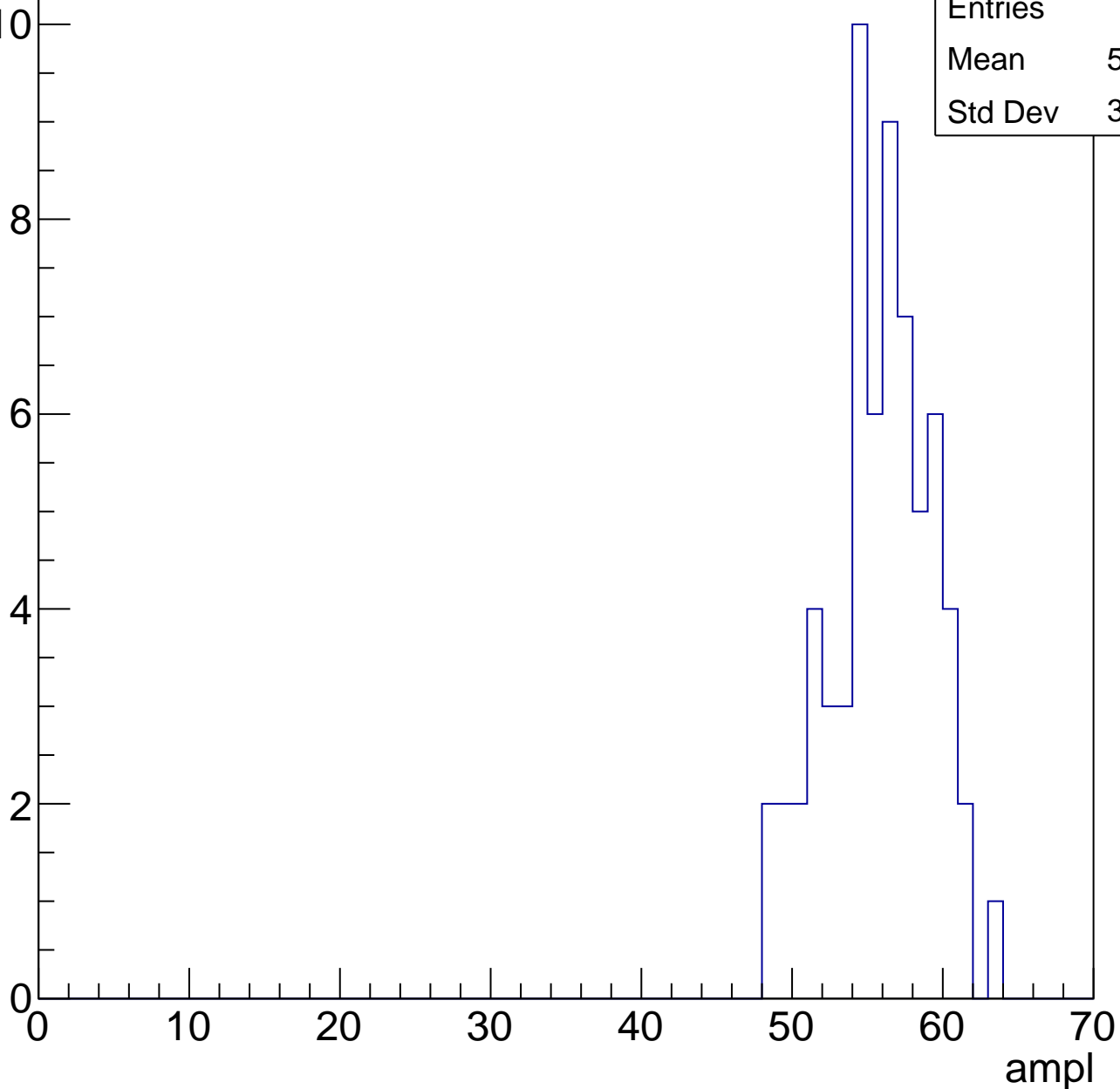


B0L002S, U5-ch93, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	55.38
Std Dev	3.343



B0L002S, U5-ch93, adc5

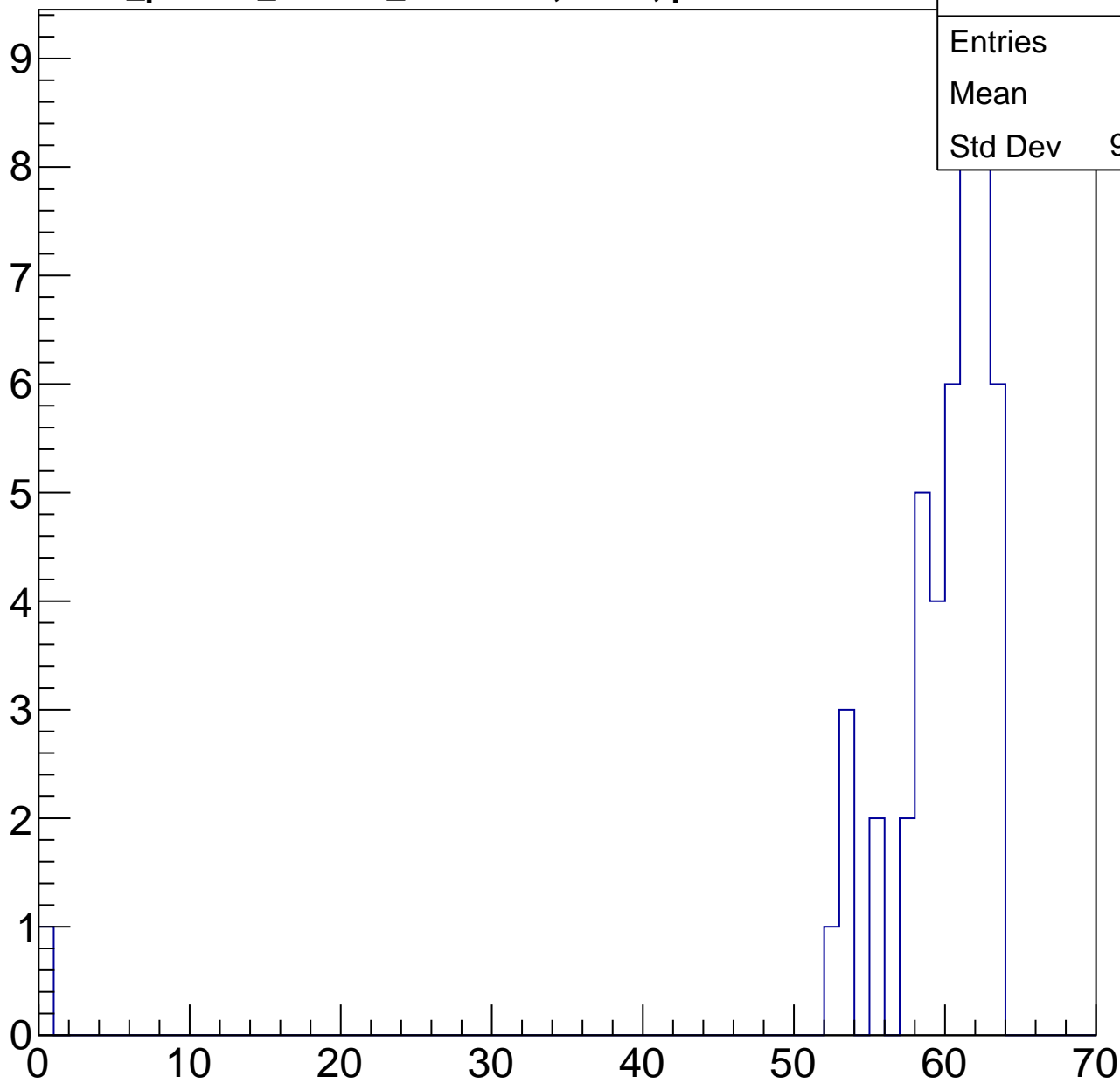
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.4
Std Dev	9.087

ampl



B0L002S, U5-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.75
Std Dev	0.433

B0L002S, U5-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U5-ch94, adc0

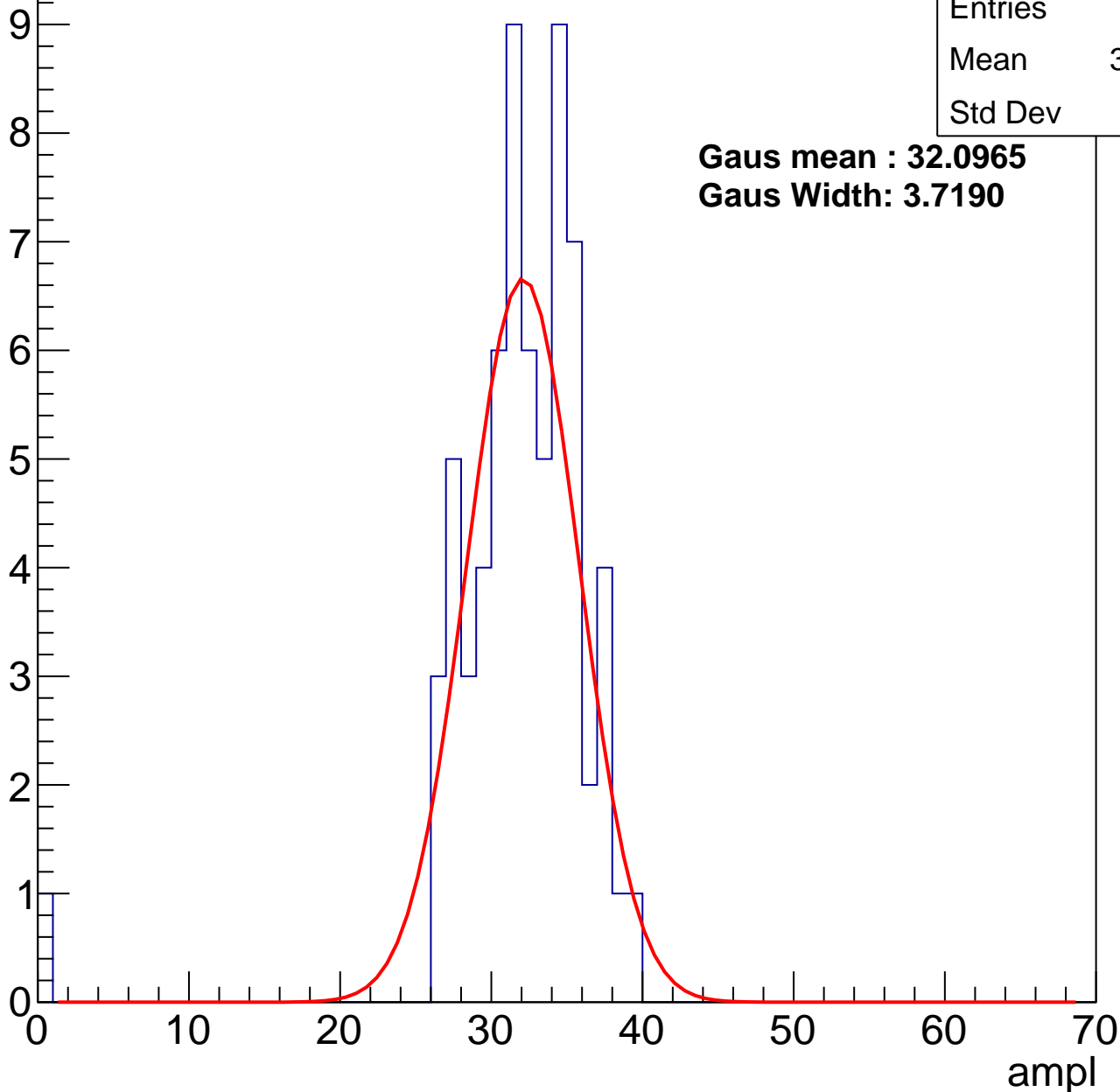
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	31.47
Std Dev	5.04

Gaus mean : 32.0965

Gaus Width: 3.7190



B0L002S, U5-ch94, adc1

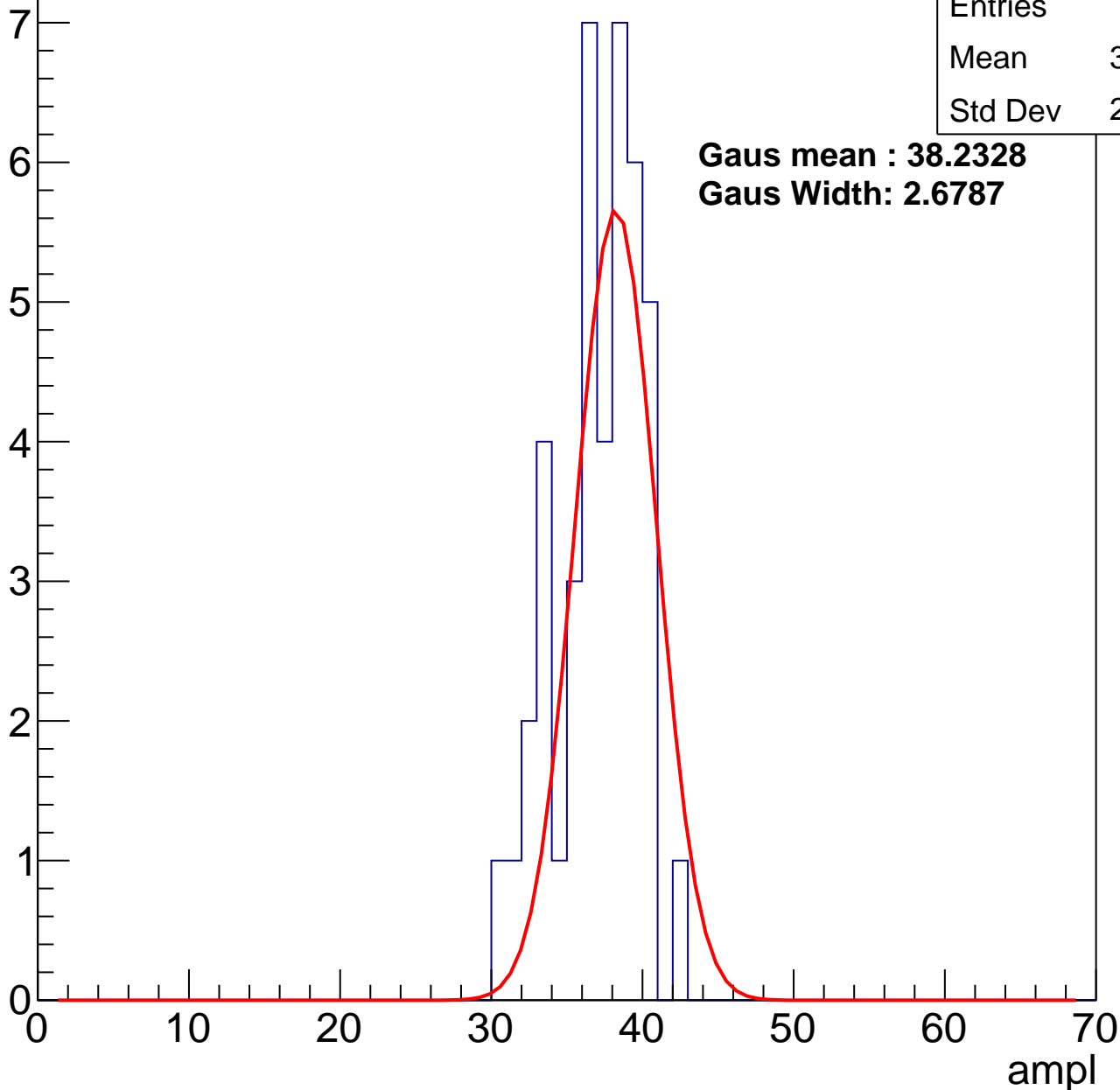
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	36.62
Std Dev	2.768

Gaus mean : 38.2328

Gaus Width: 2.6787



B0L002S, U5-ch94, adc2

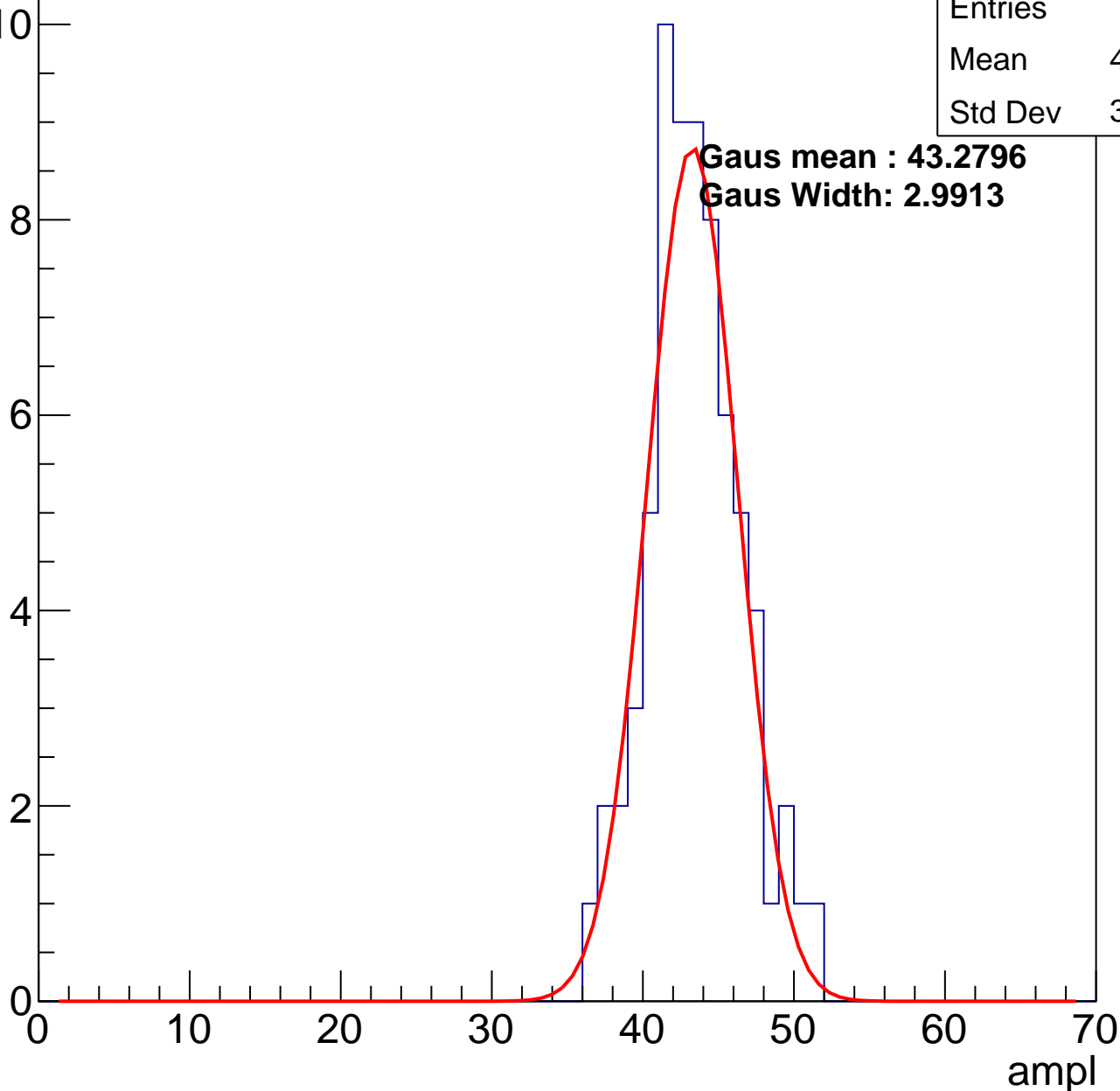
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	42.97
Std Dev	3.102

Gaus mean : 43.2796

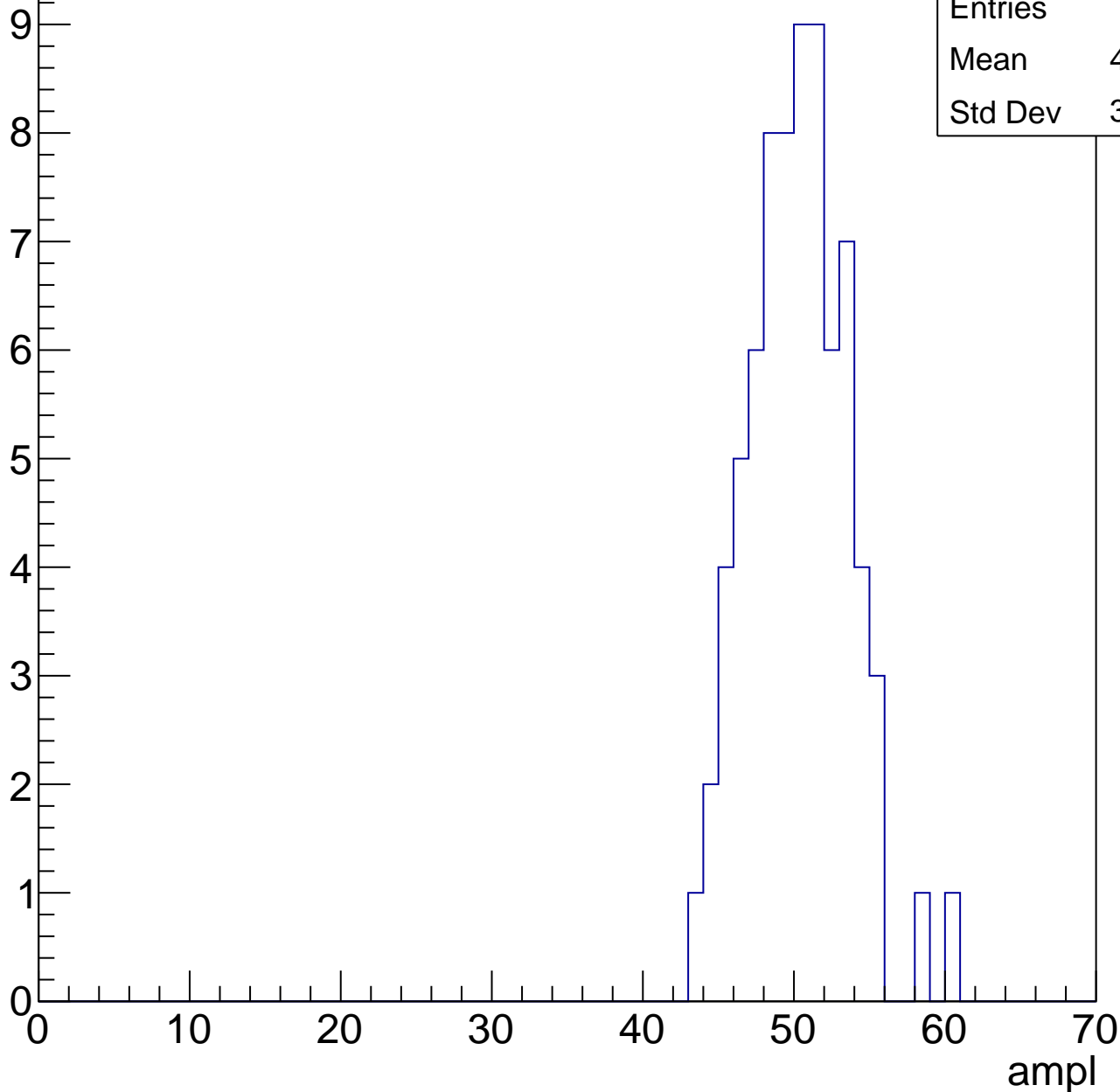
Gaus Width: 2.9913



B0L002S, U5-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

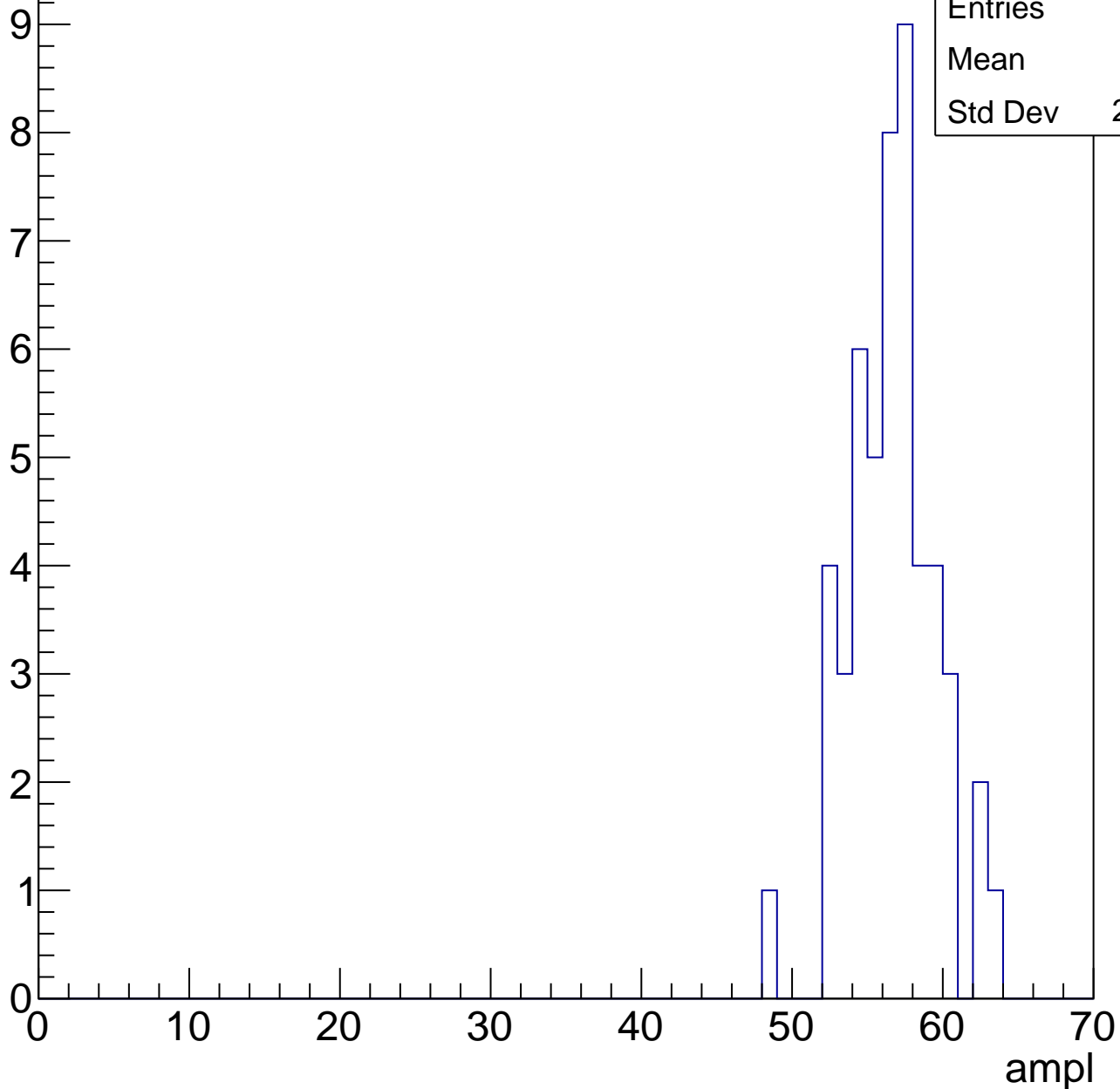


Entries	74
Mean	49.86
Std Dev	3.273

B0L002S, U5-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

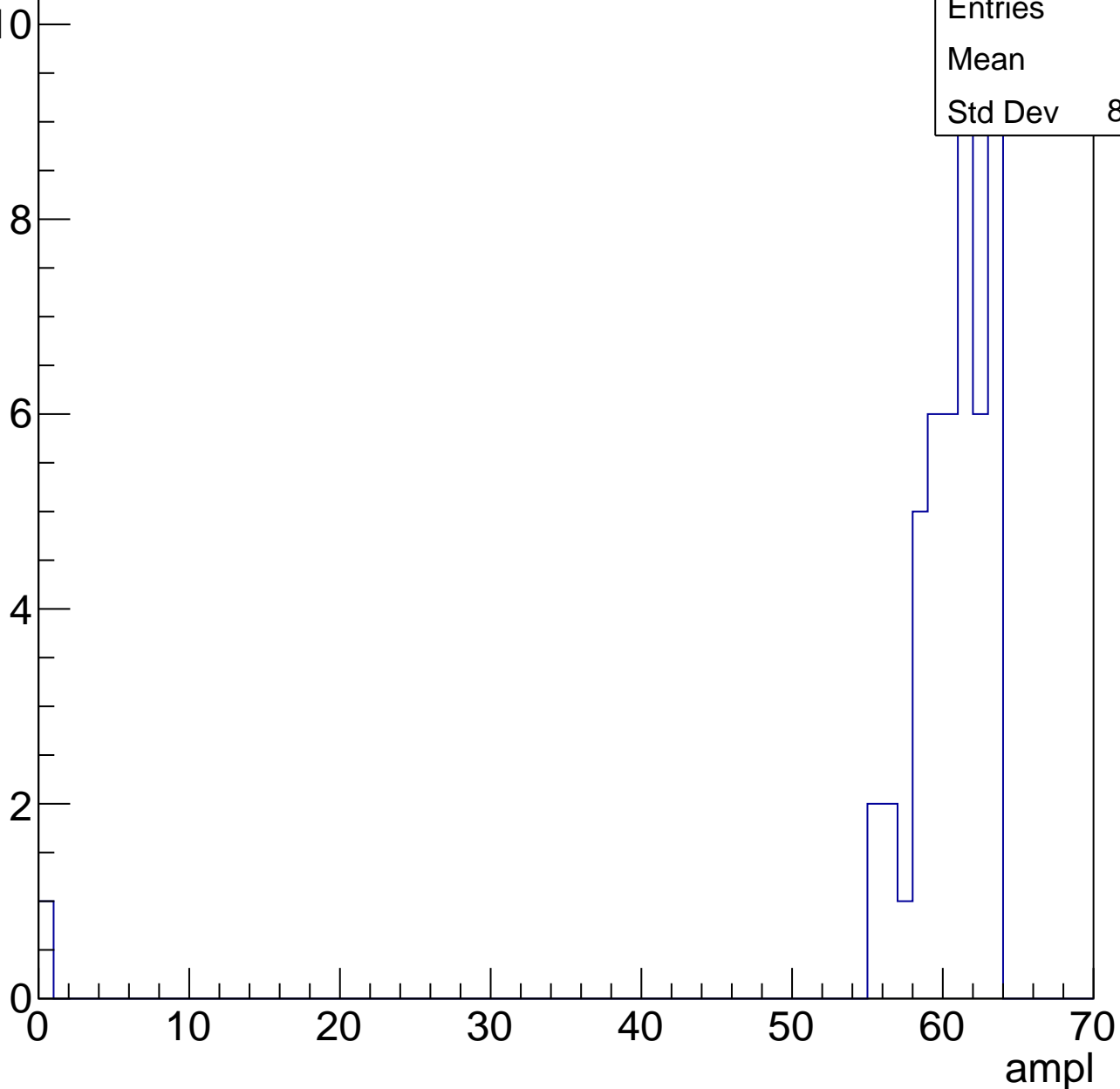


B0L002S, U5-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	59
Std Dev	8.879



B0L002S, U5-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch95, adc0

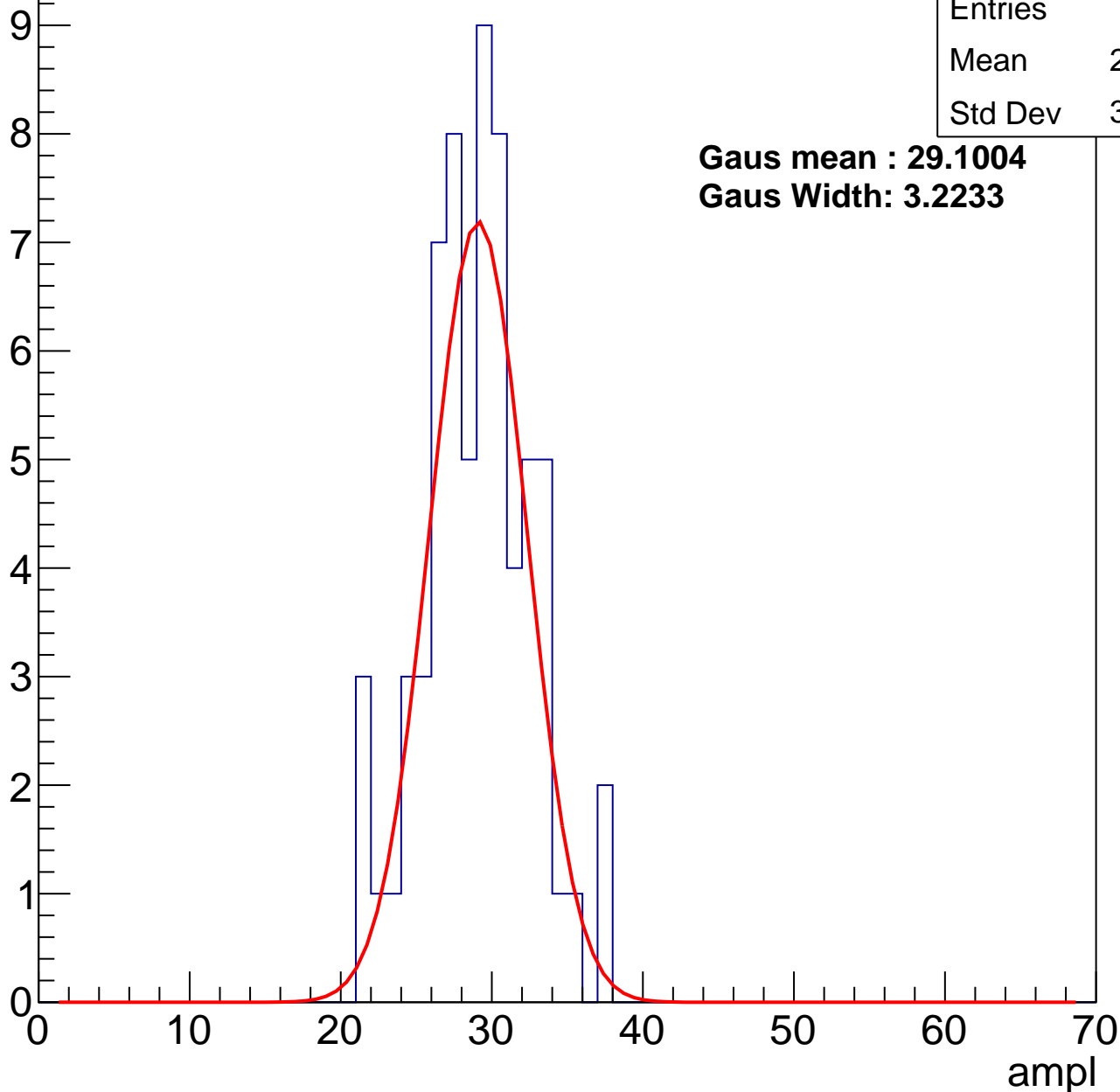
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	28.58
Std Dev	3.538

Gaus mean : 29.1004

Gaus Width: 3.2233



B0L002S, U5-ch95, adc1

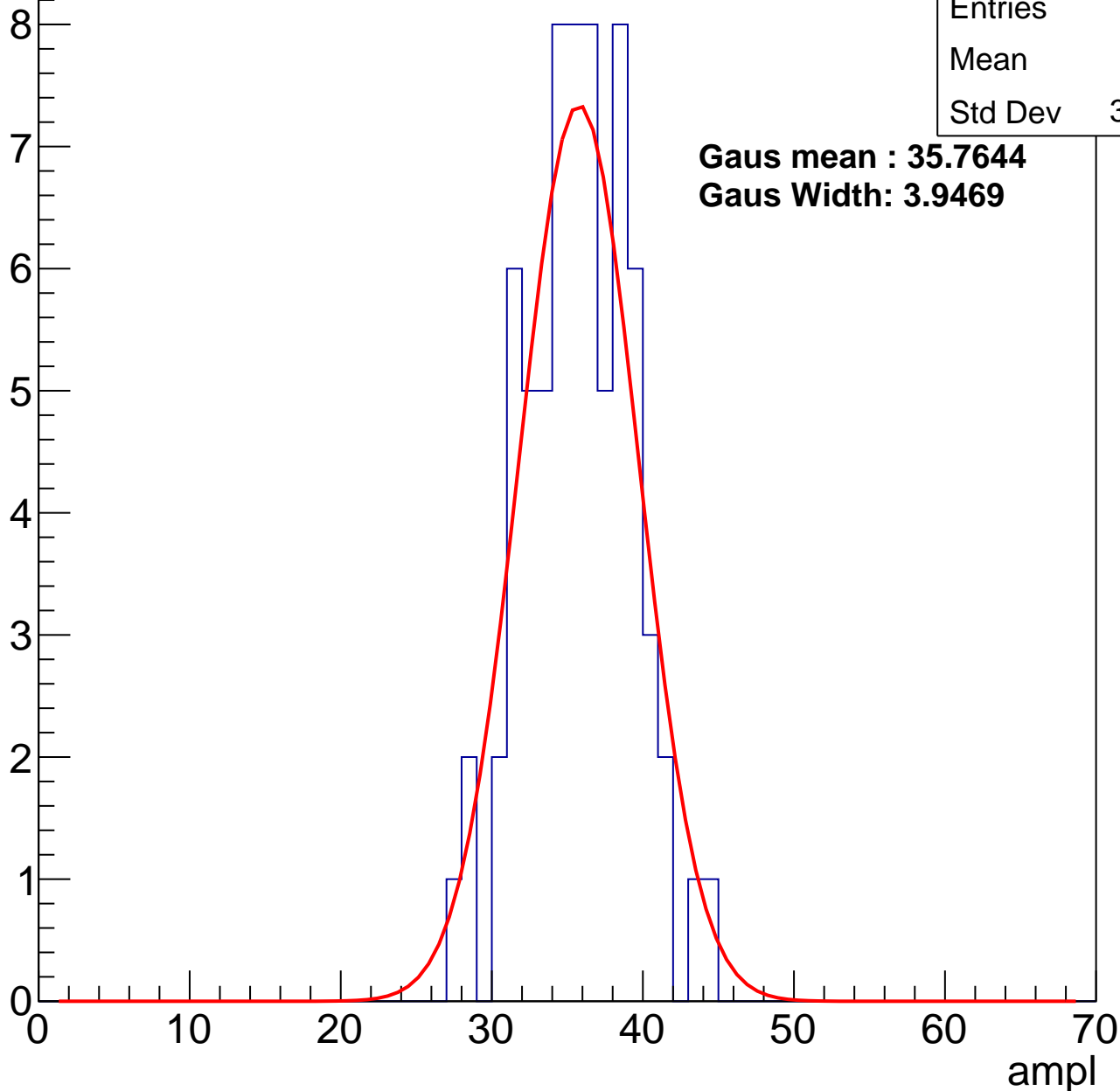
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	35.3
Std Dev	3.486

Gaus mean : 35.7644

Gaus Width: 3.9469



B0L002S, U5-ch95, adc2

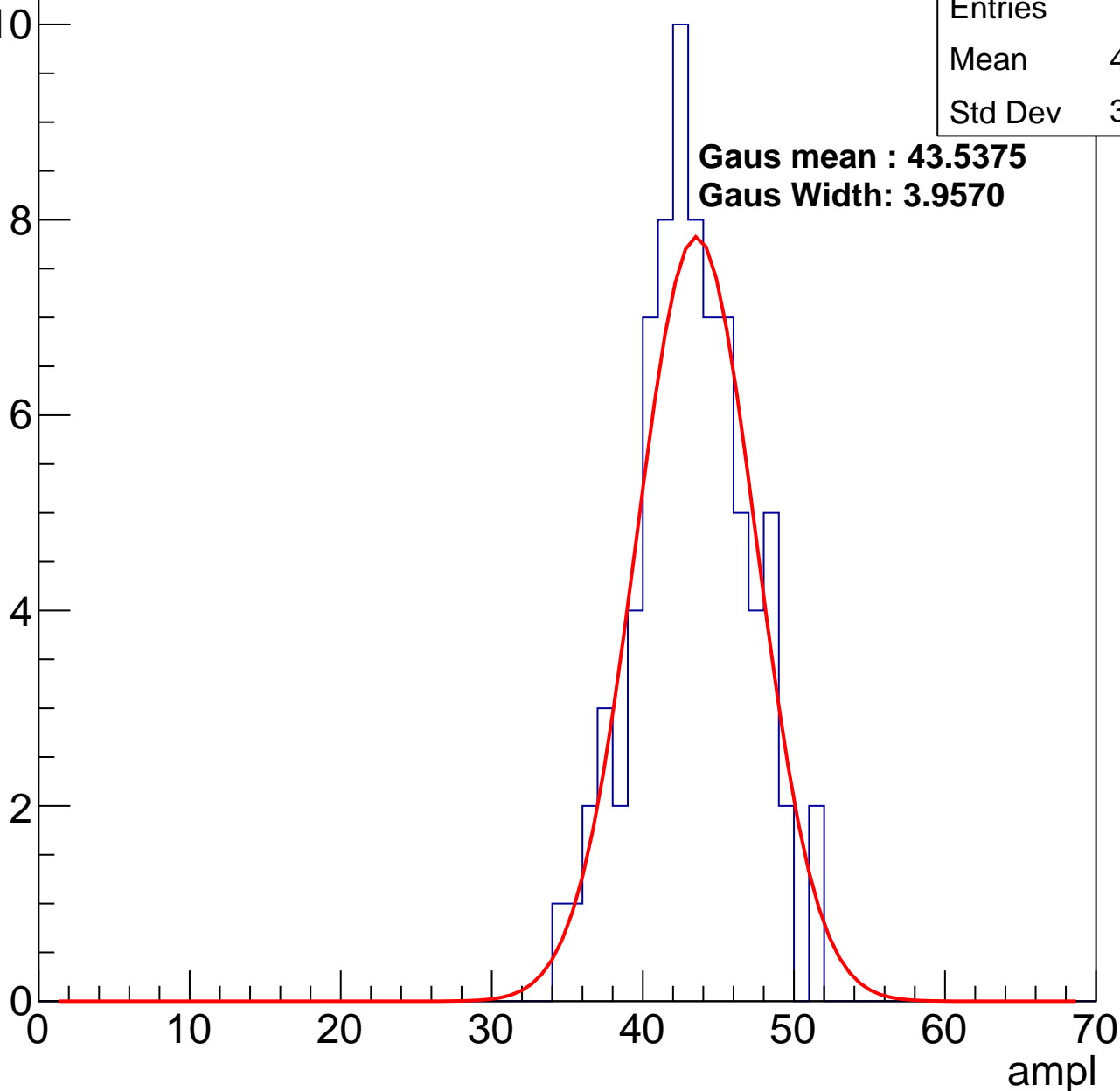
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	42.78
Std Dev	3.643

Gaus mean : 43.5375

Gaus Width: 3.9570

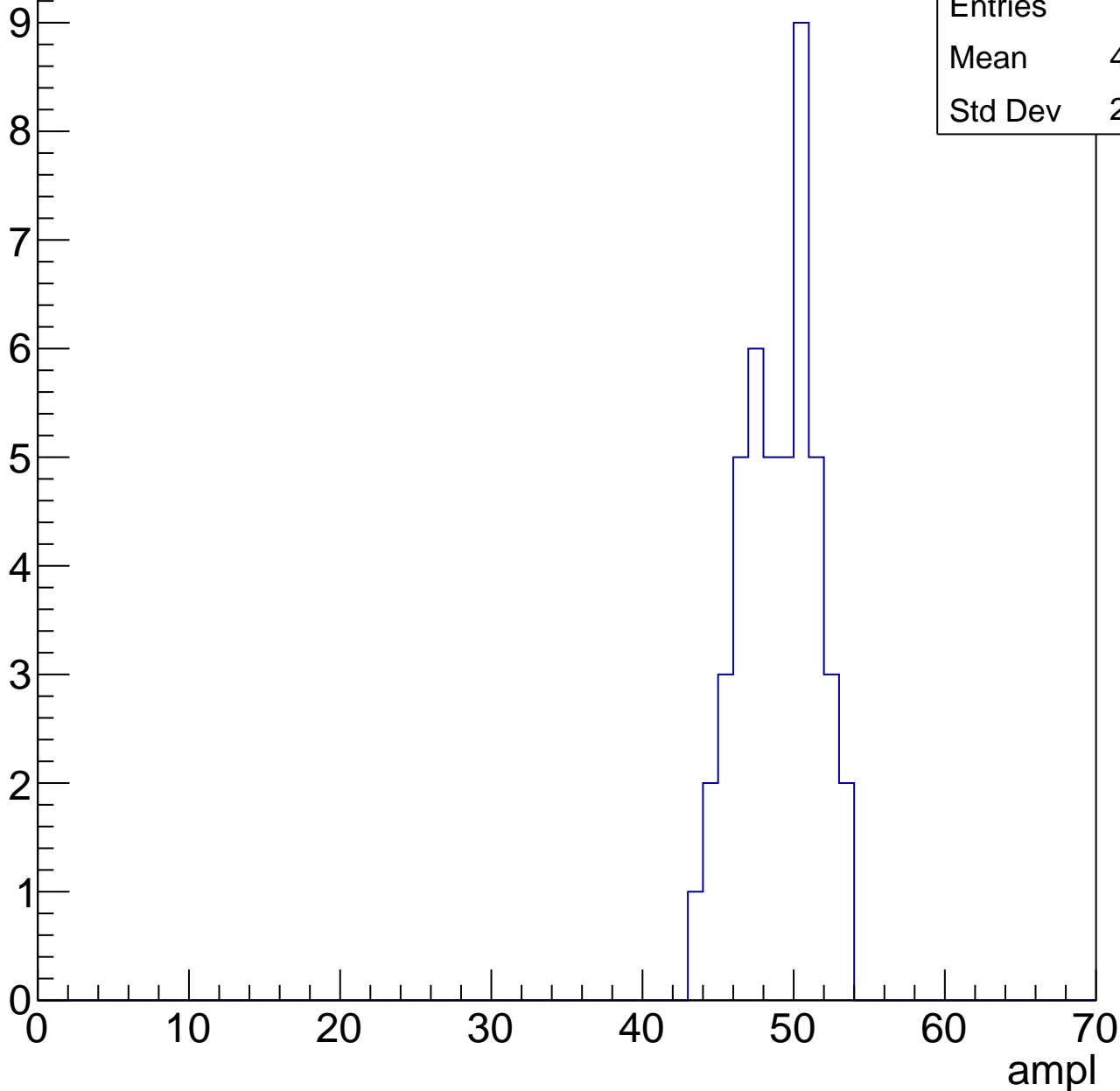


B0L002S, U5-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	48.48
Std Dev	2.482

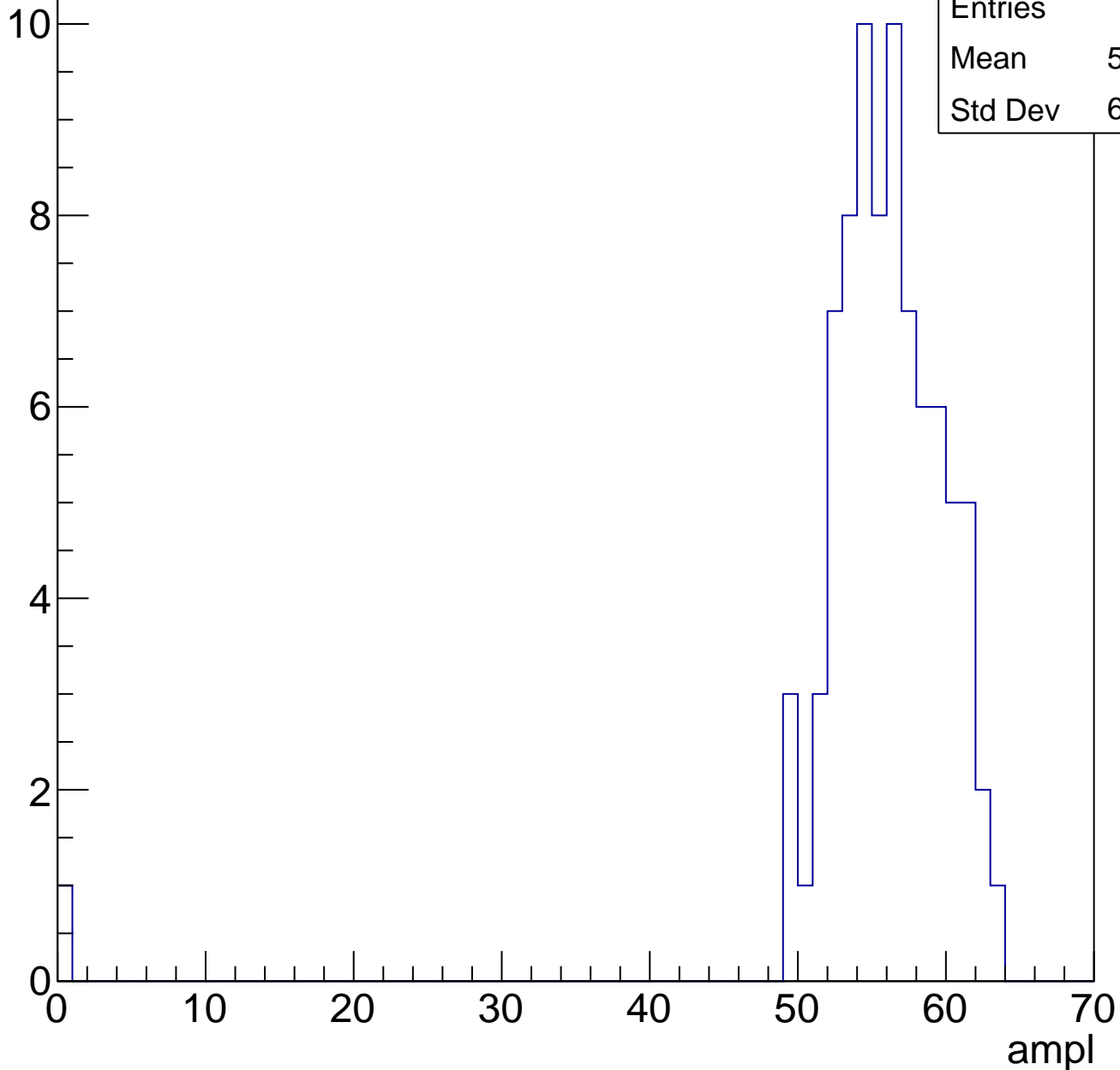


B0L002S, U5-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	55.07
Std Dev	6.912

Entry

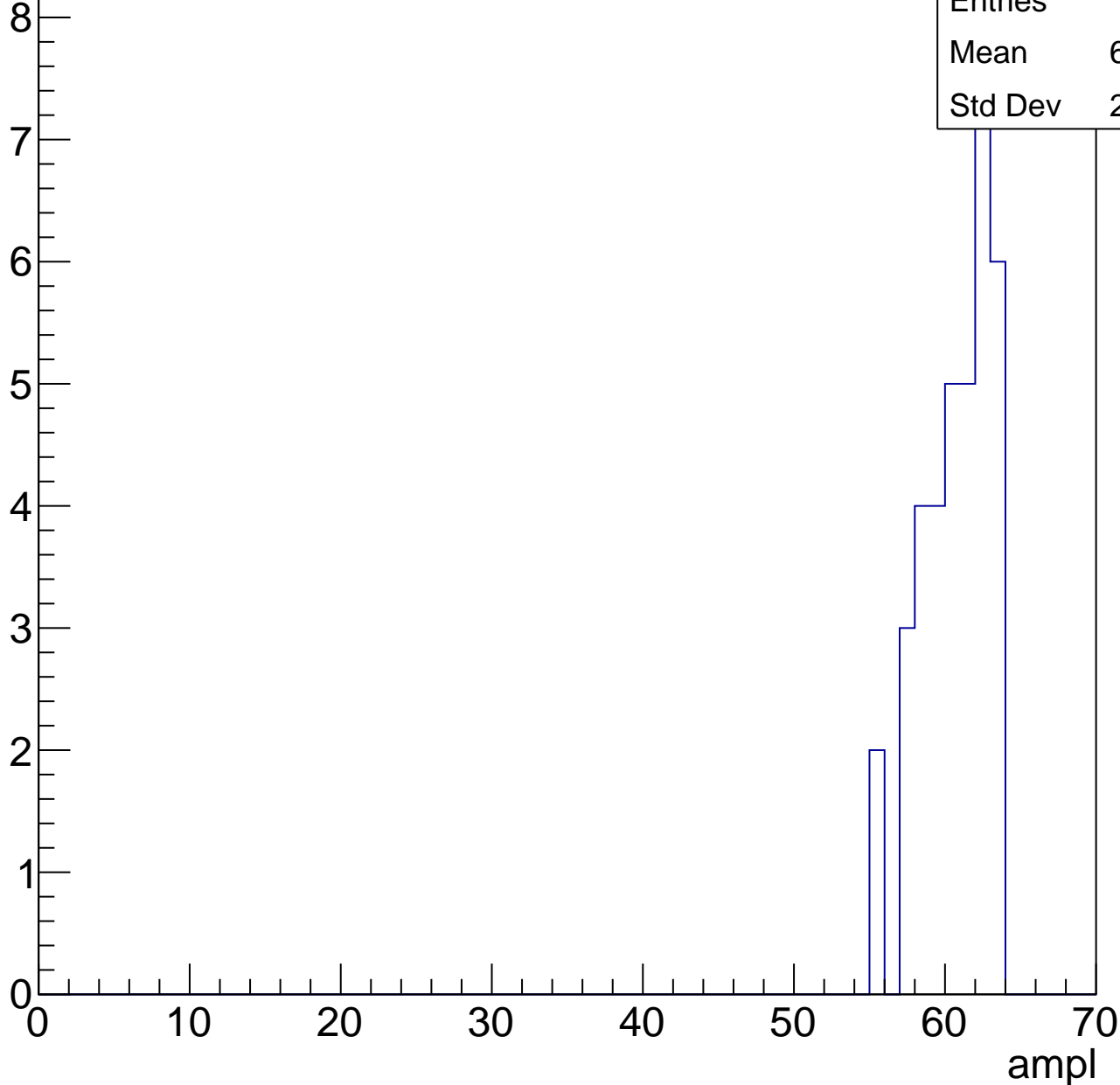


B0L002S, U5-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

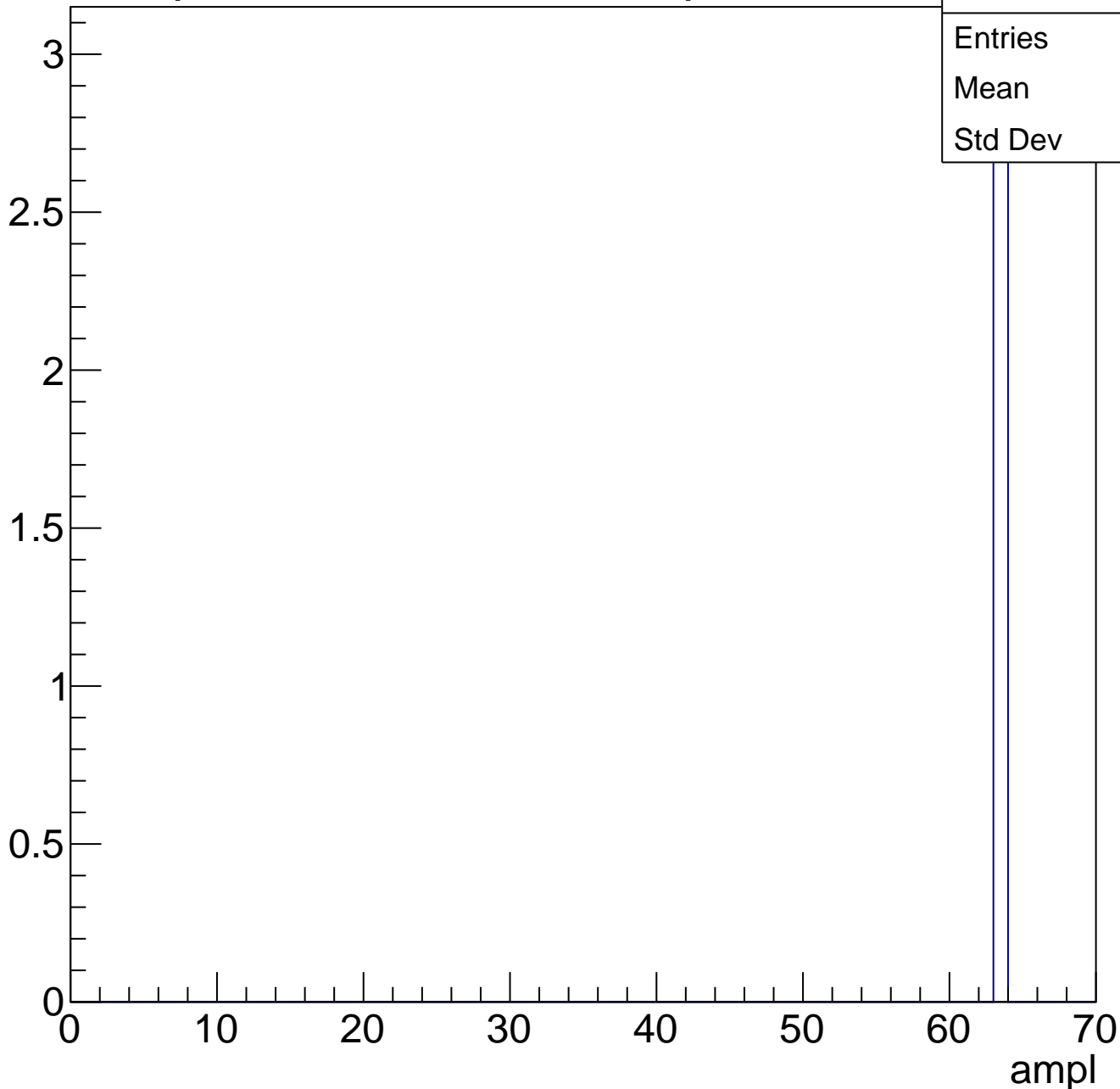
Entries	37
Mean	60.22
Std Dev	2.244



B0L002S, U5-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch96, adc0

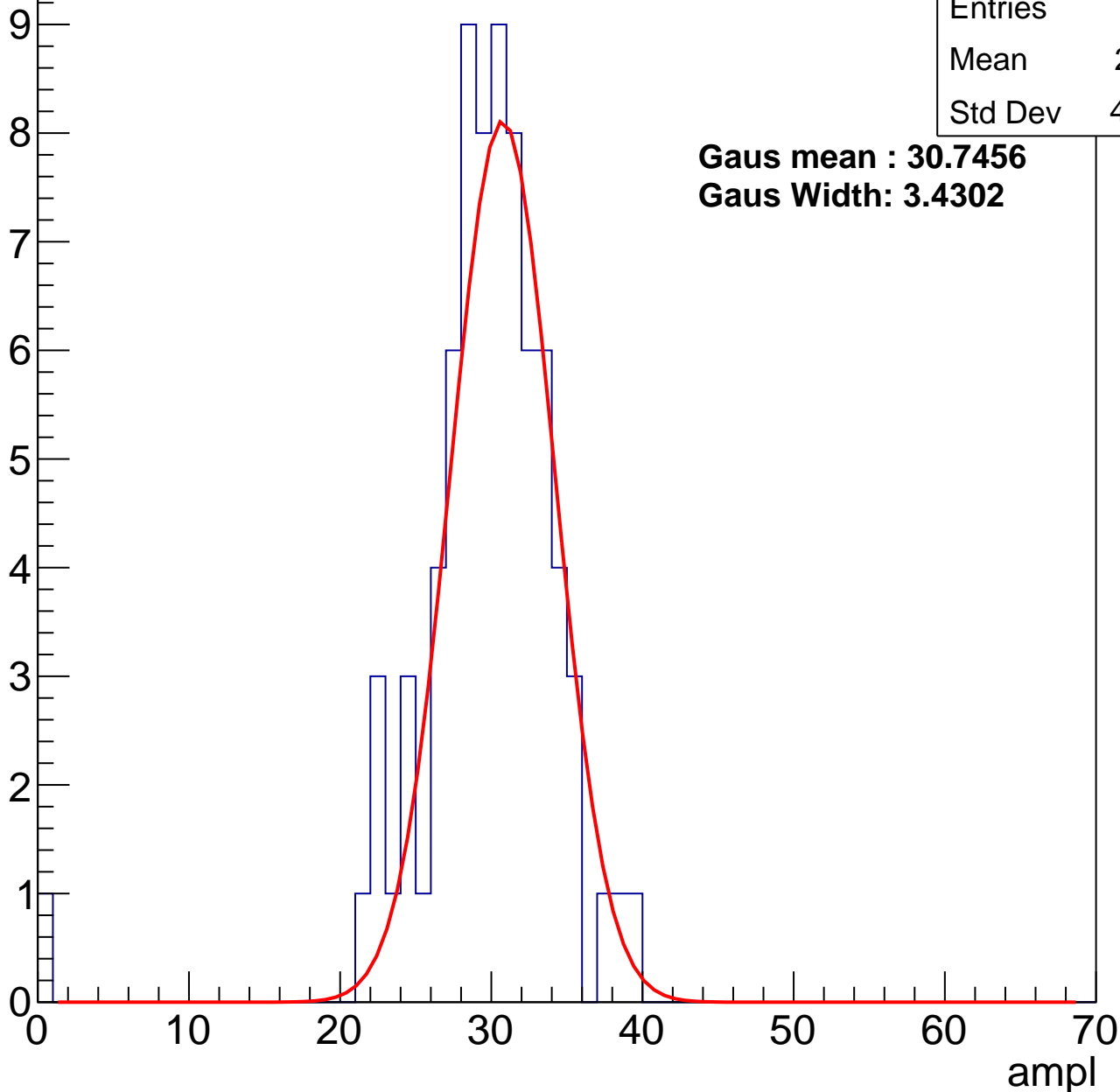
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	29.21
Std Dev	4.985

Gaus mean : 30.7456

Gaus Width: 3.4302



B0L002S, U5-ch96, adc1

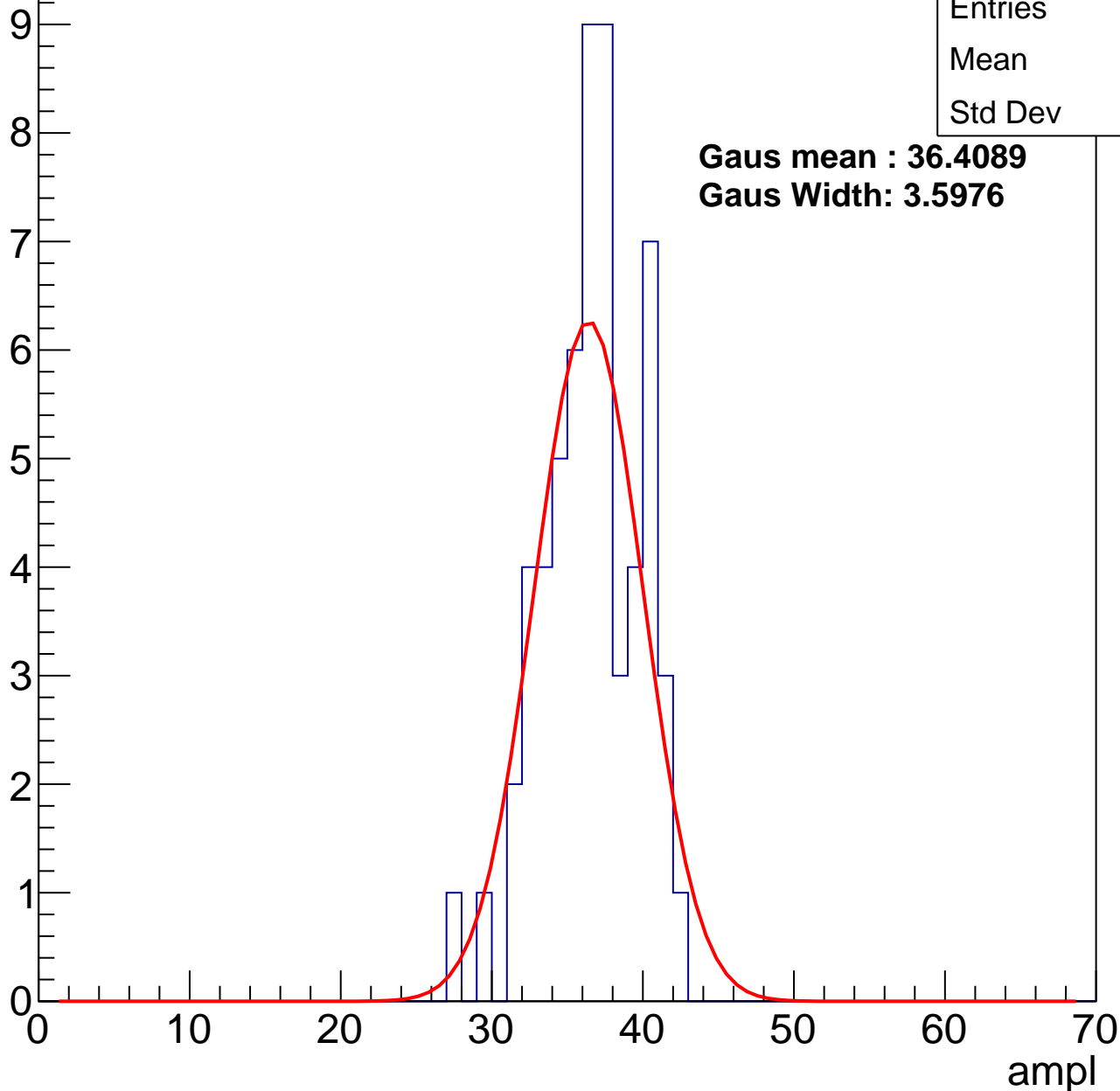
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.1
Std Dev	3.15

Gaus mean : 36.4089

Gaus Width: 3.5976



B0L002S, U5-ch96, adc2

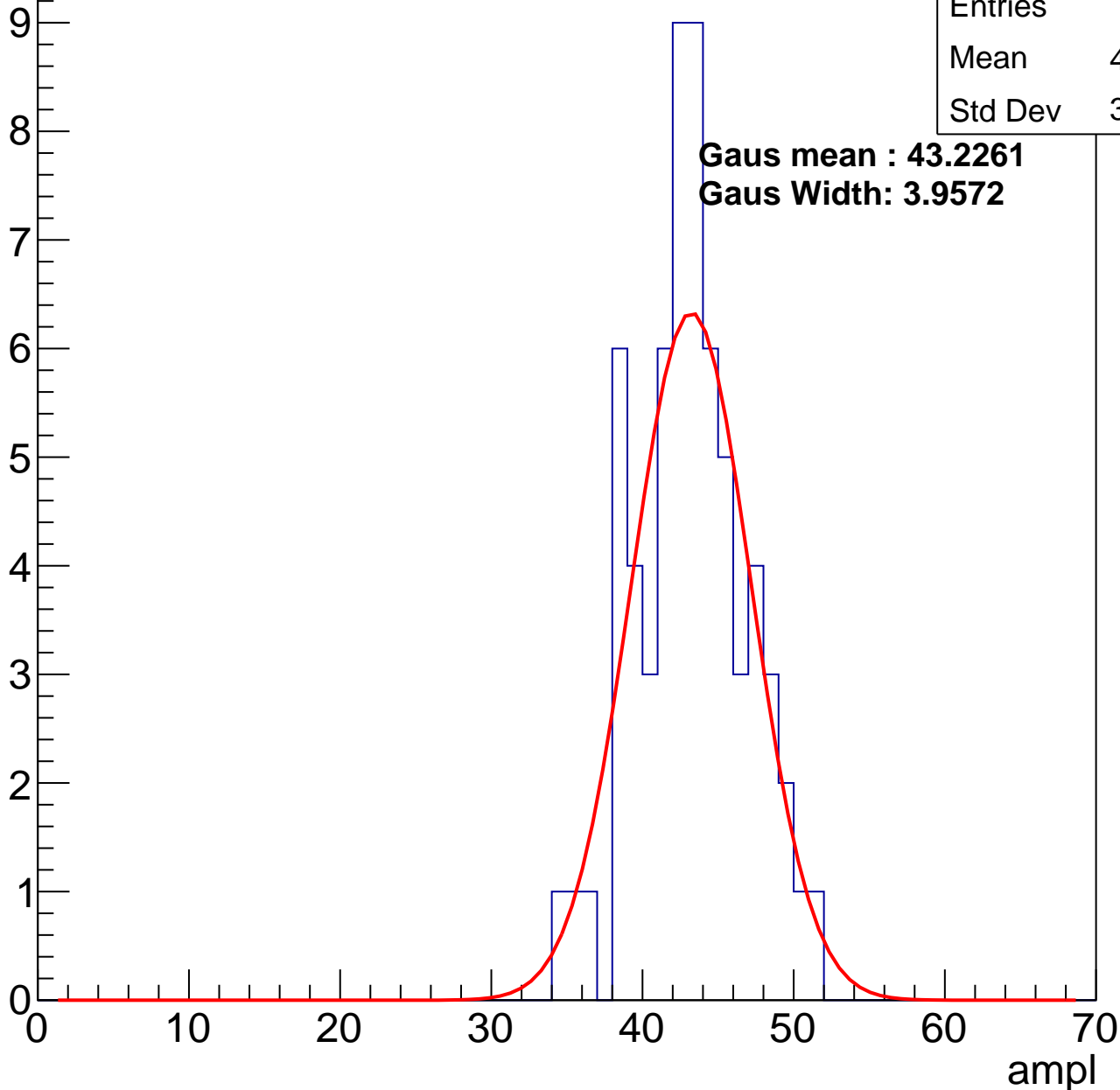
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	42.74
Std Dev	3.596

Gaus mean : 43.2261

Gaus Width: 3.9572

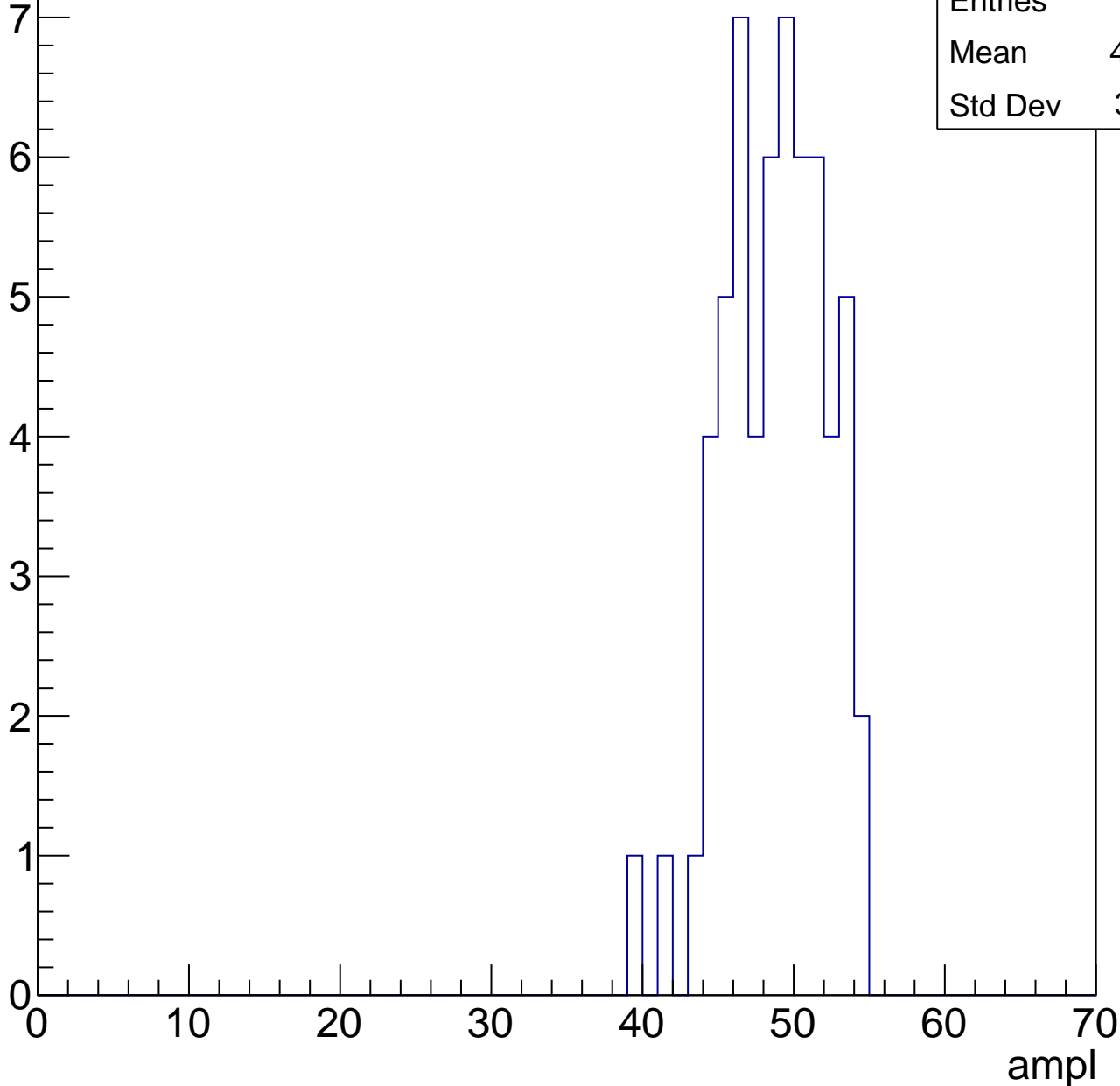


B0L002S, U5-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

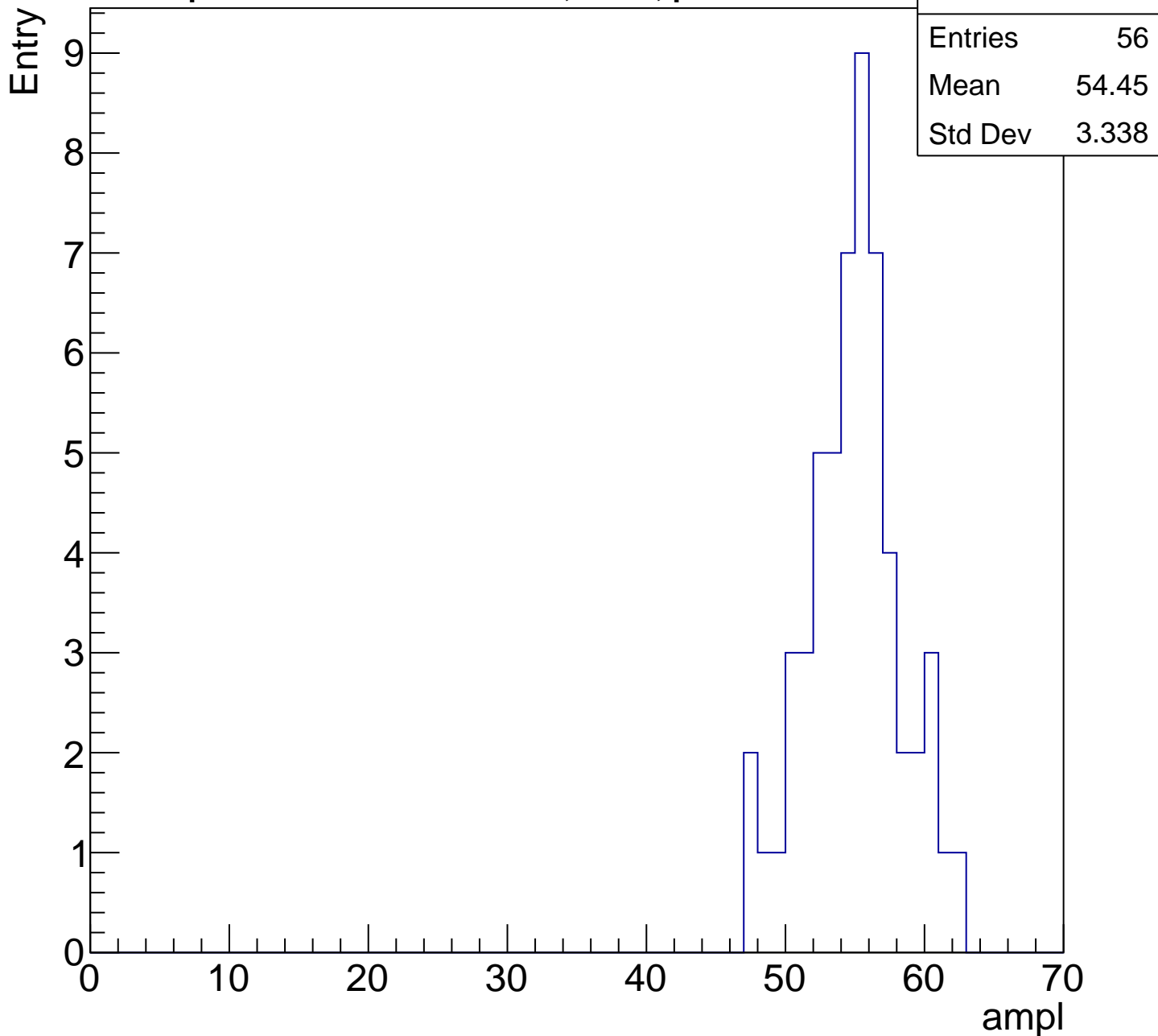
Entry

Entries	59
Mean	48.34
Std Dev	3.281



B0L002S, U5-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U5-ch96, adc5

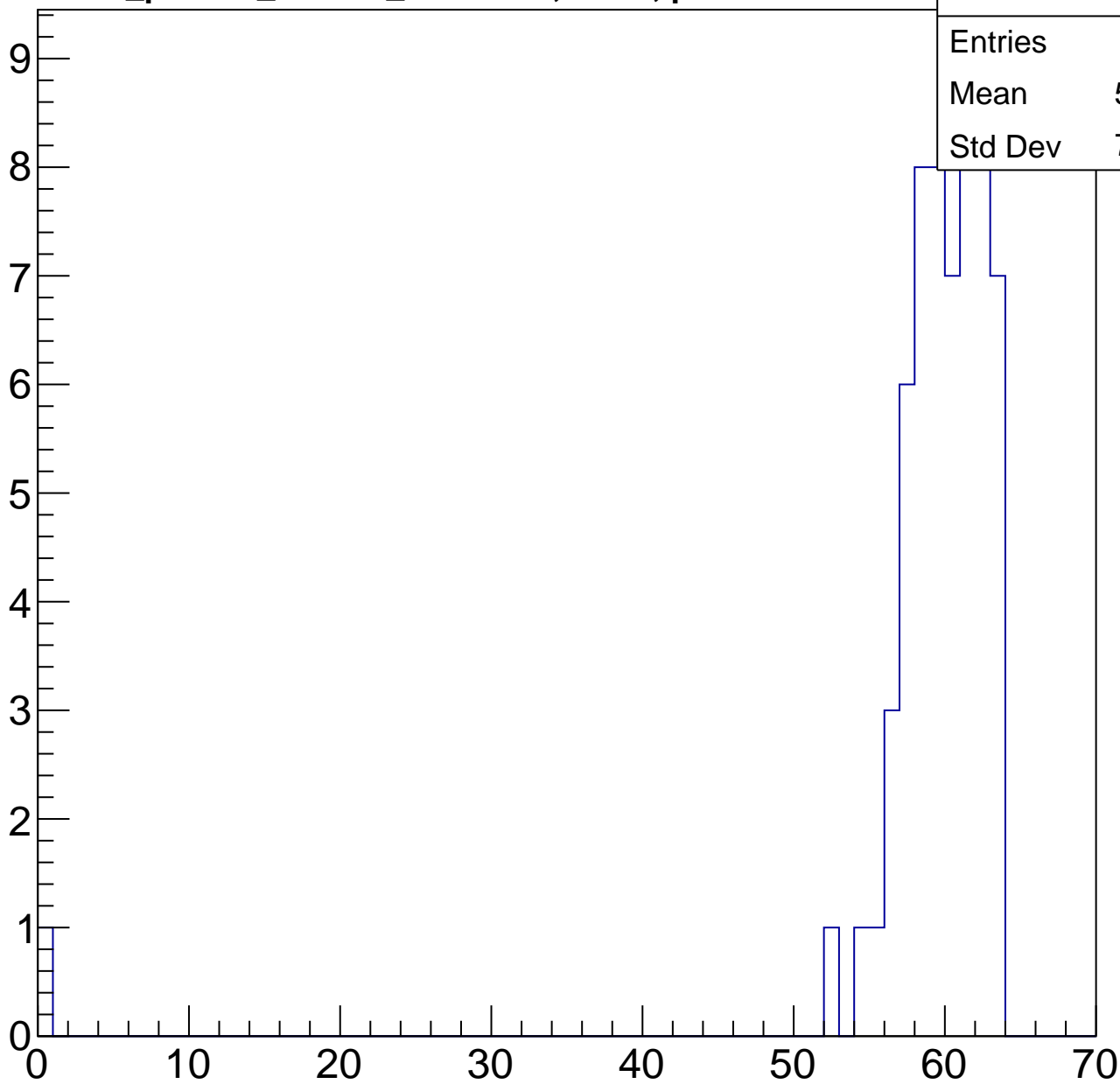
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	58.61
Std Dev	7.951

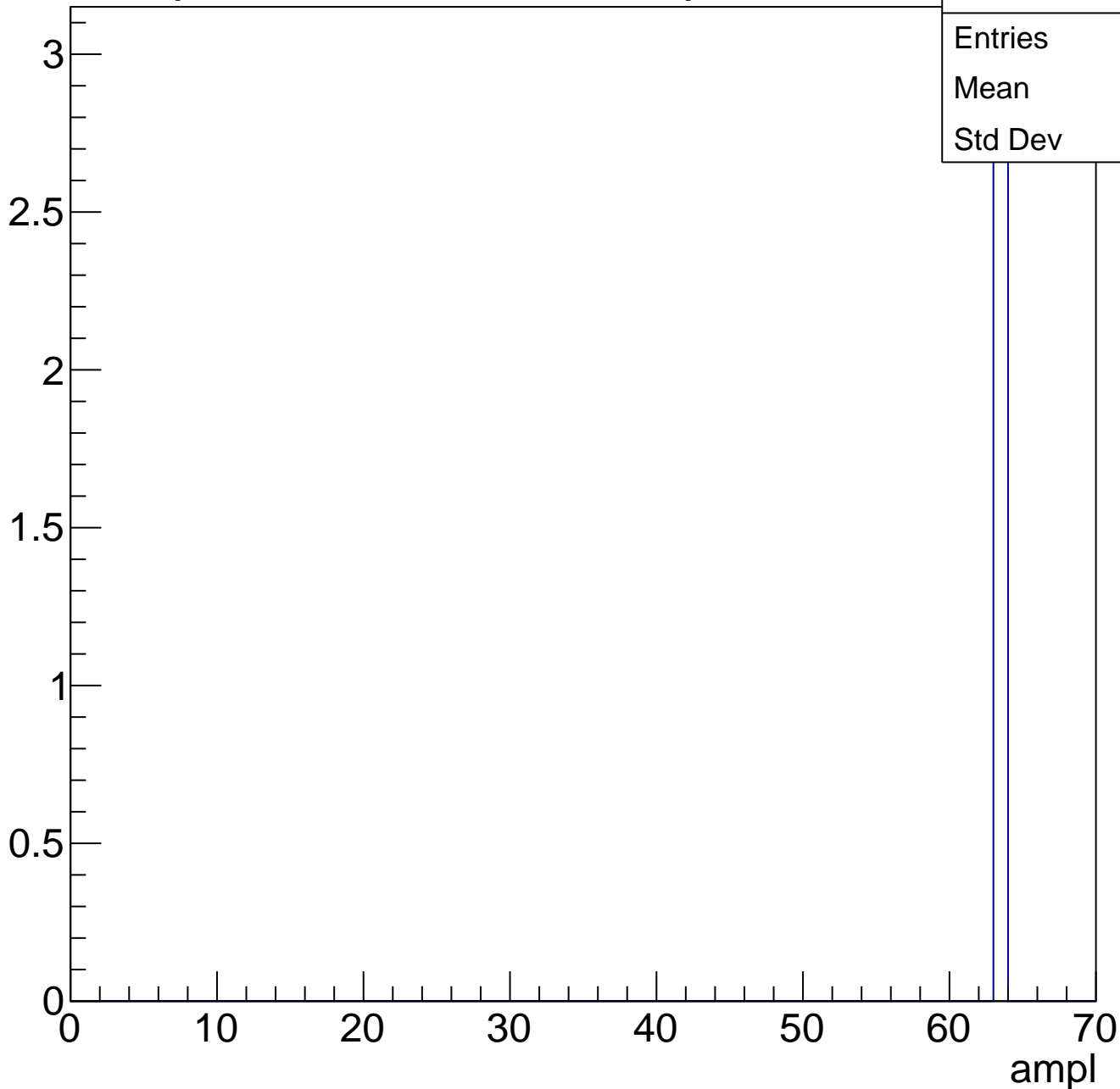
ampl



B0L002S, U5-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U5-ch97, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	30.65
Std Dev	2.858

Gaus mean : 31.4167

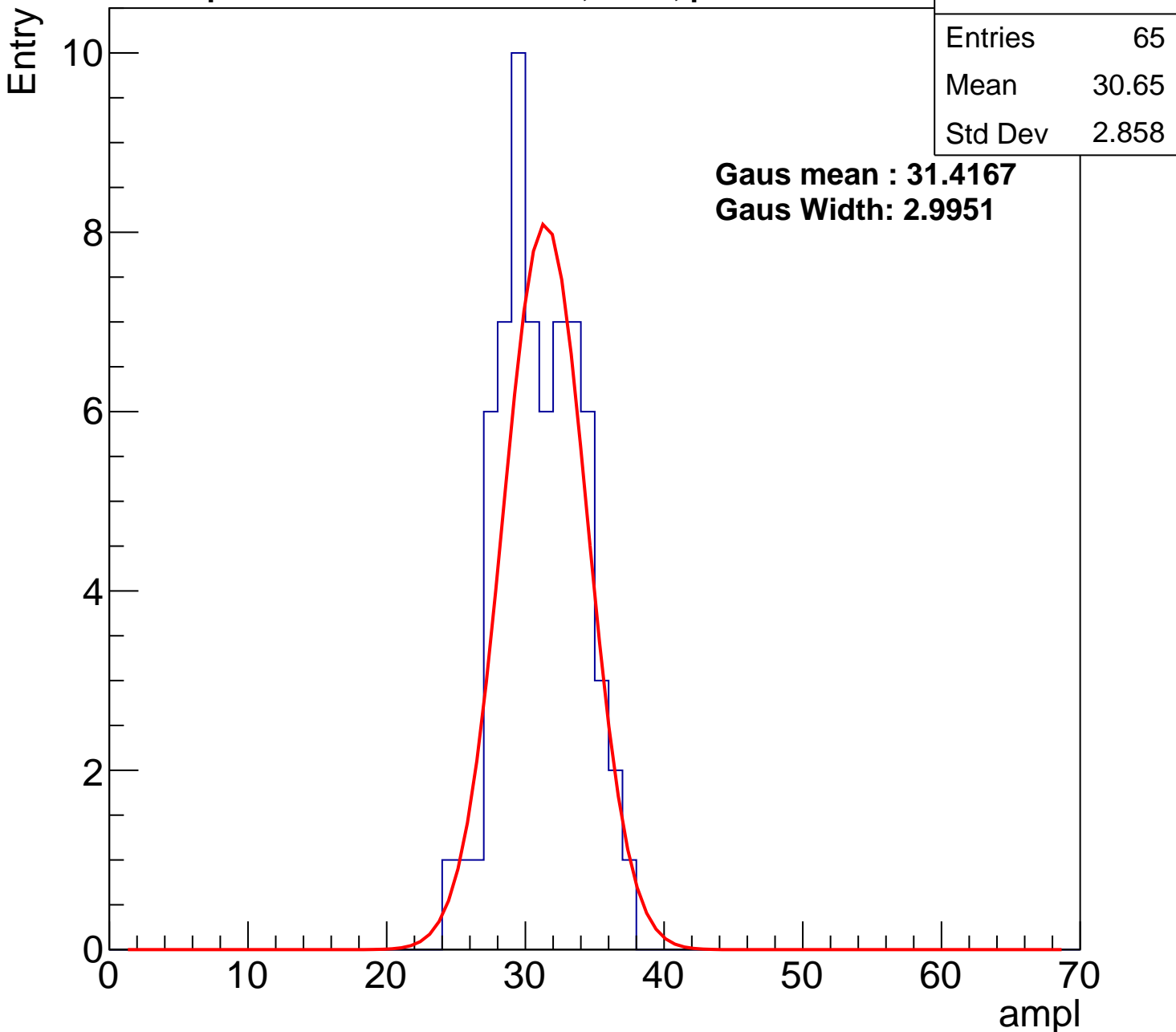
Gaus Width: 2.9951

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch97, adc1

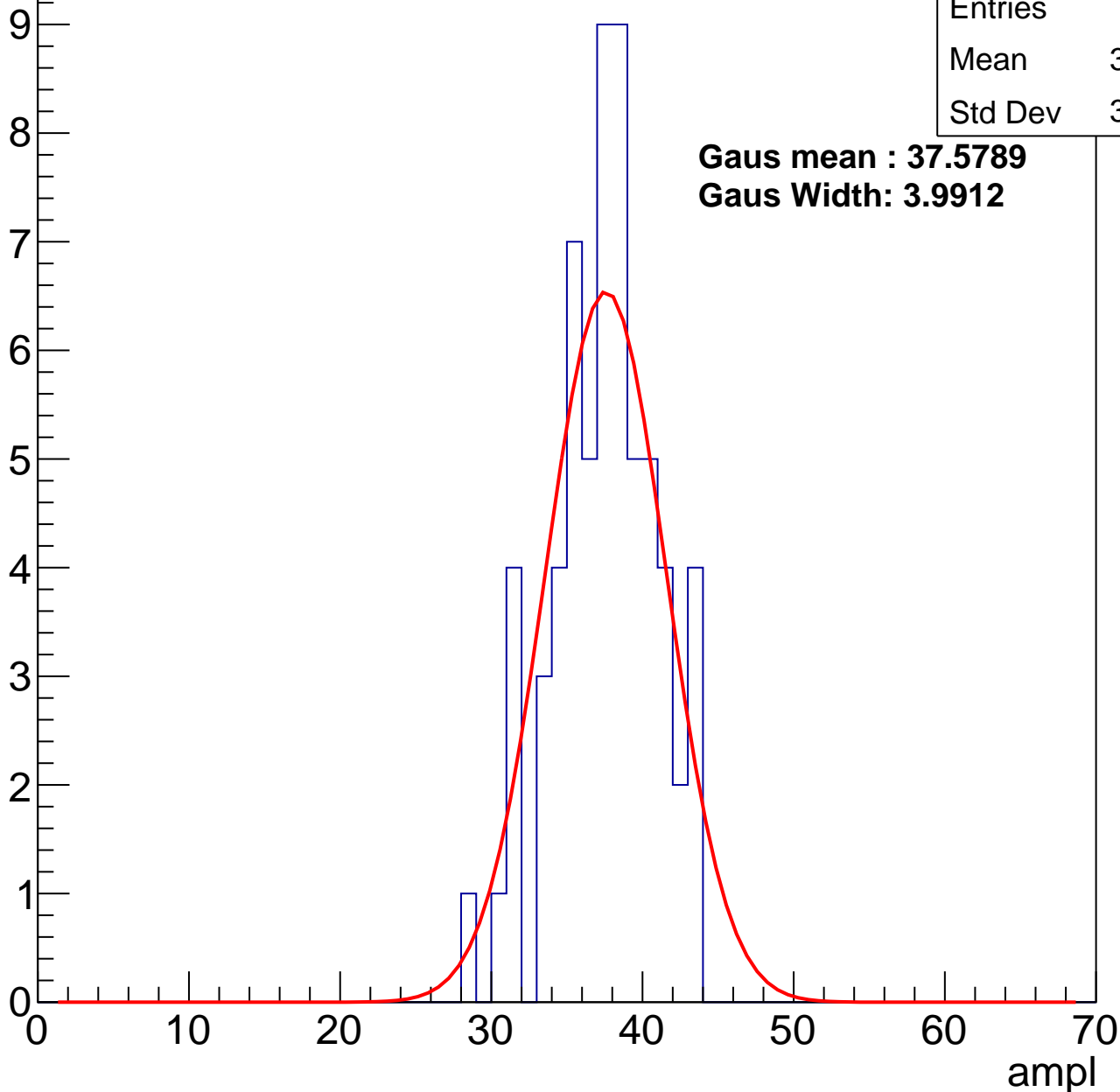
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.02
Std Dev	3.388

Gaus mean : 37.5789

Gaus Width: 3.9912



B0L002S, U5-ch97, adc2

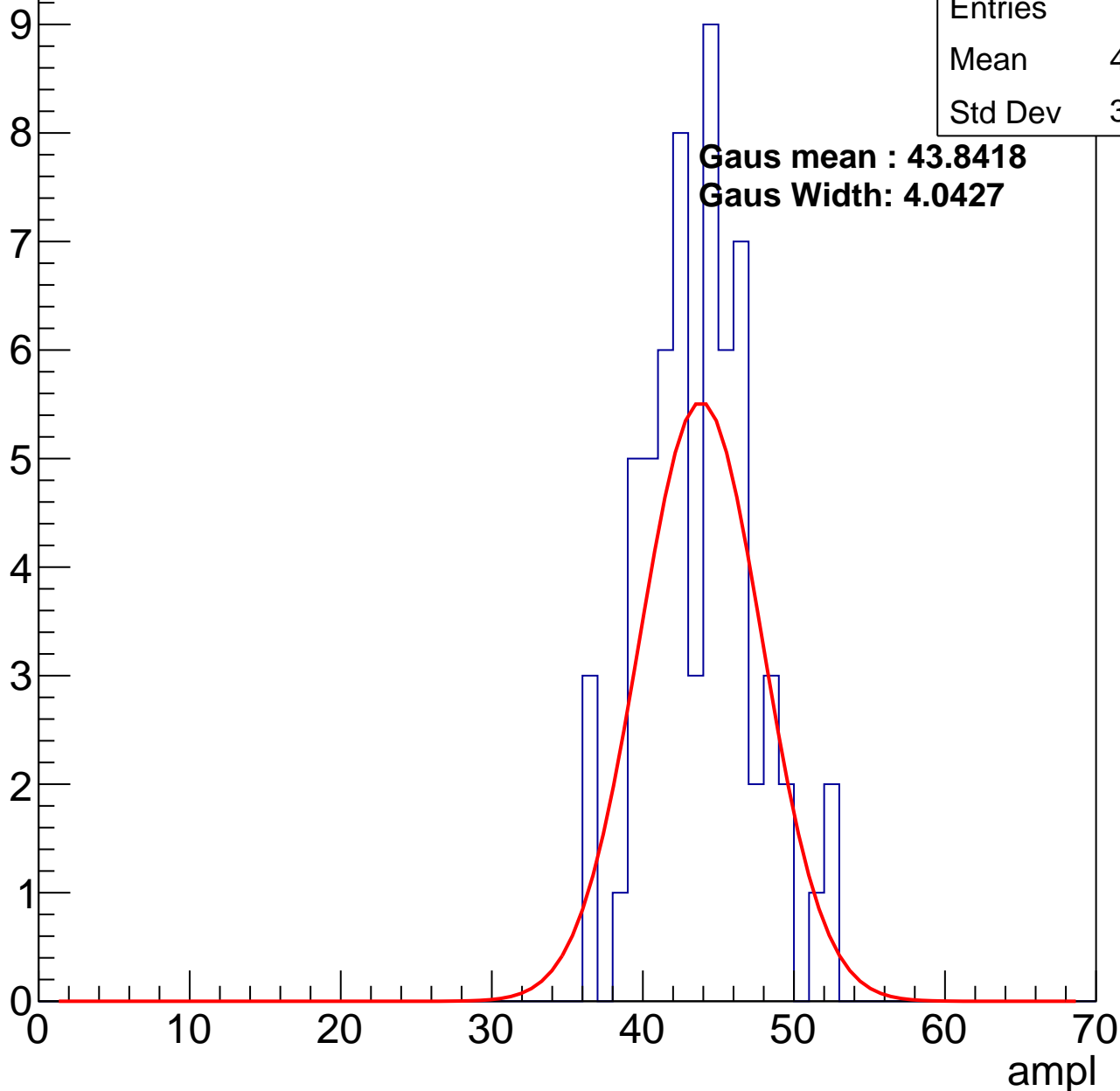
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.35
Std Dev	3.613

Gaus mean : 43.8418

Gaus Width: 4.0427

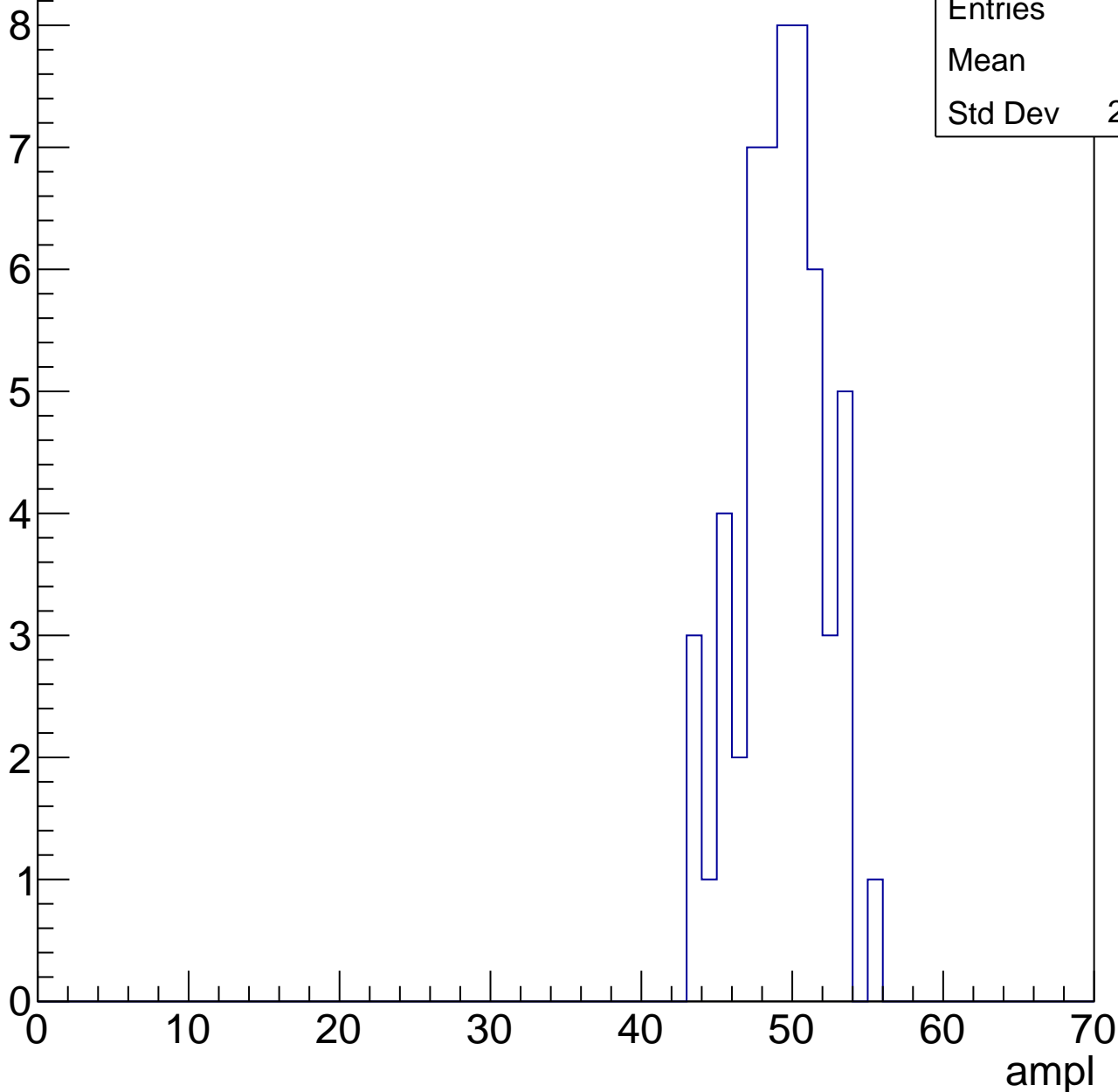


B0L002S, U5-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	48.8
Std Dev	2.773



B0L002S, U5-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	55.18
Std Dev	3.118

Entry

10

8

6

4

2

0

0

10

20

30

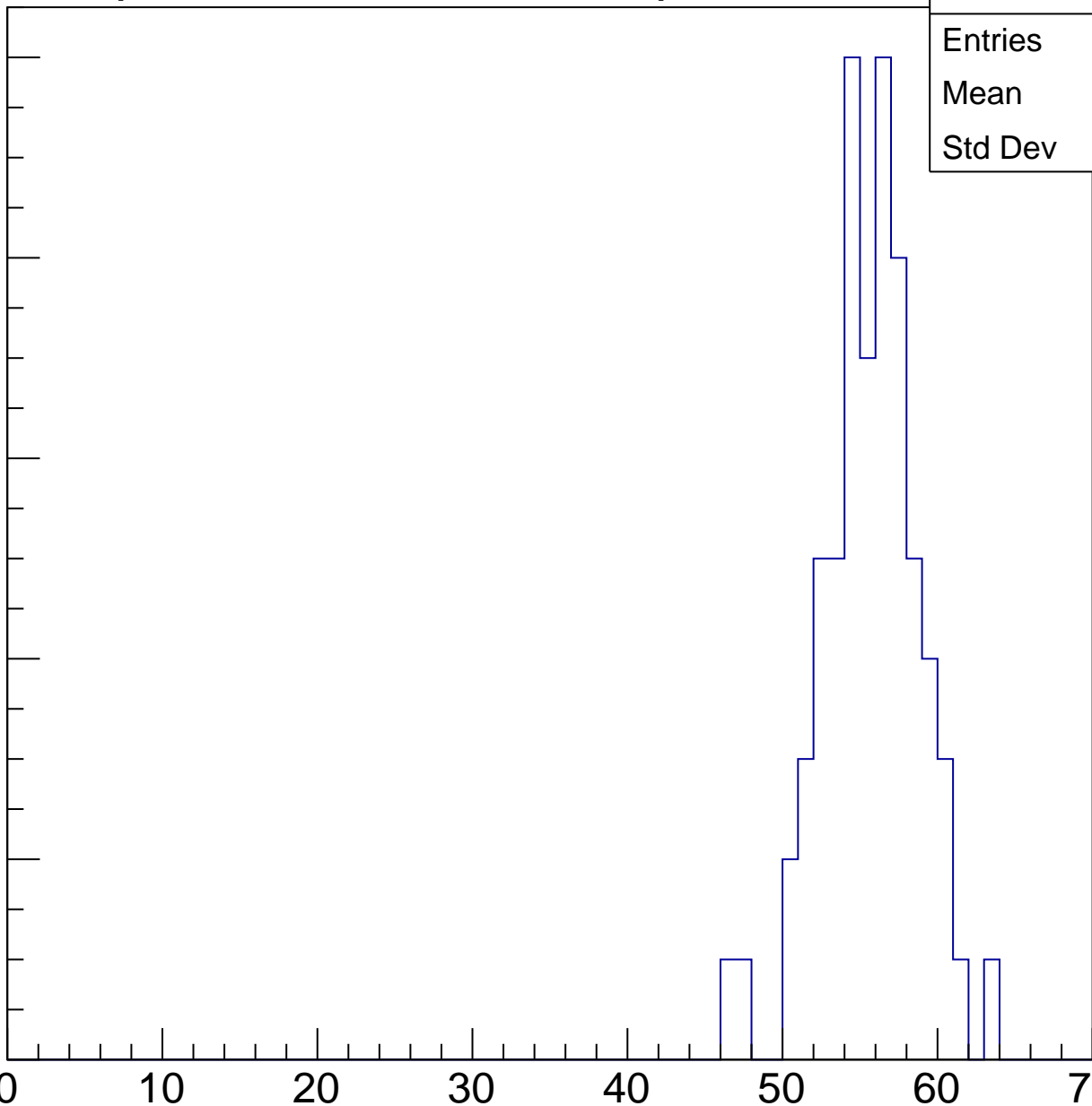
40

50

60

70

ampl

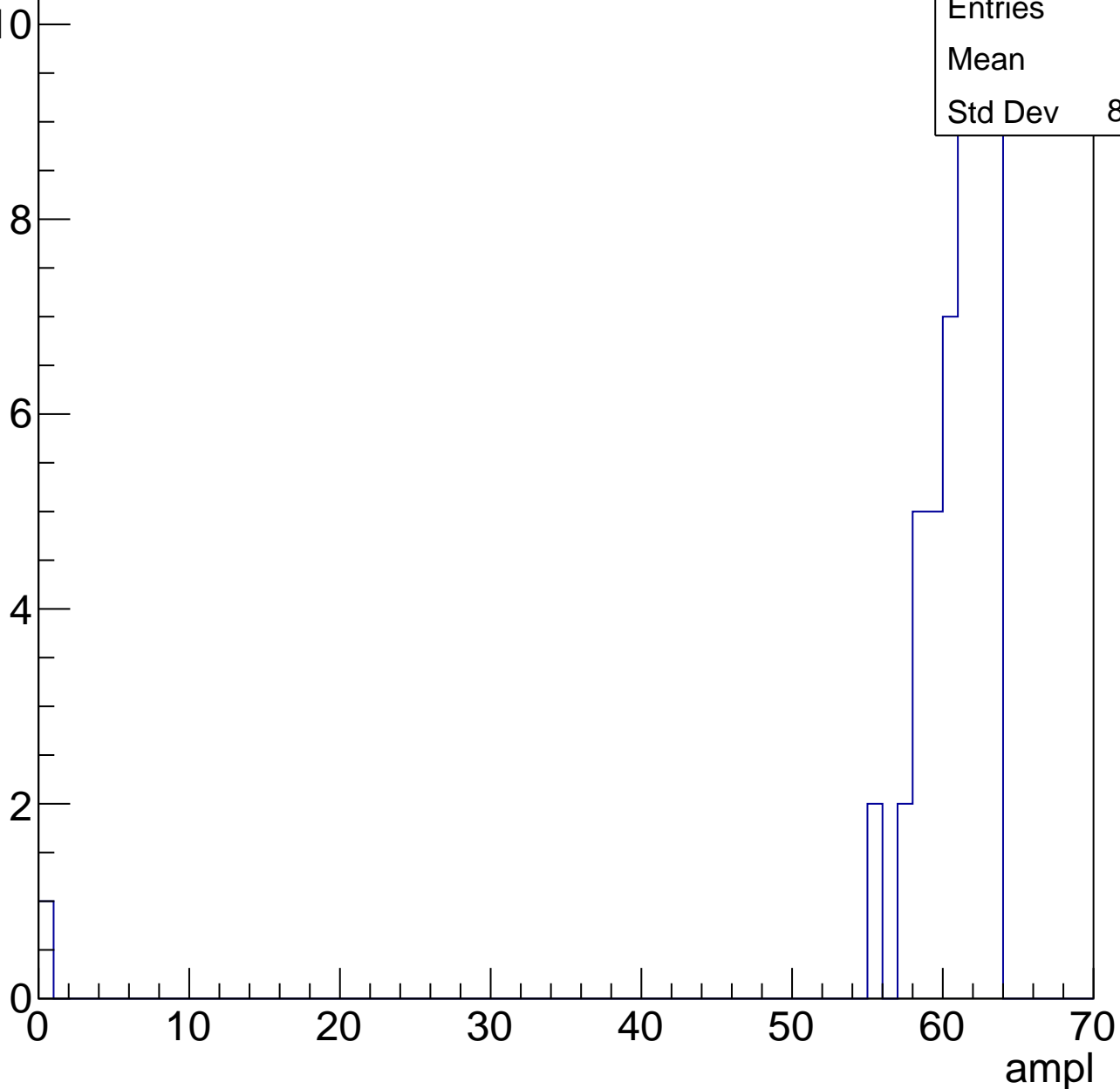


B0L002S, U5-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	59.3
Std Dev	8.716



B0L002S, U5-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch98, adc0

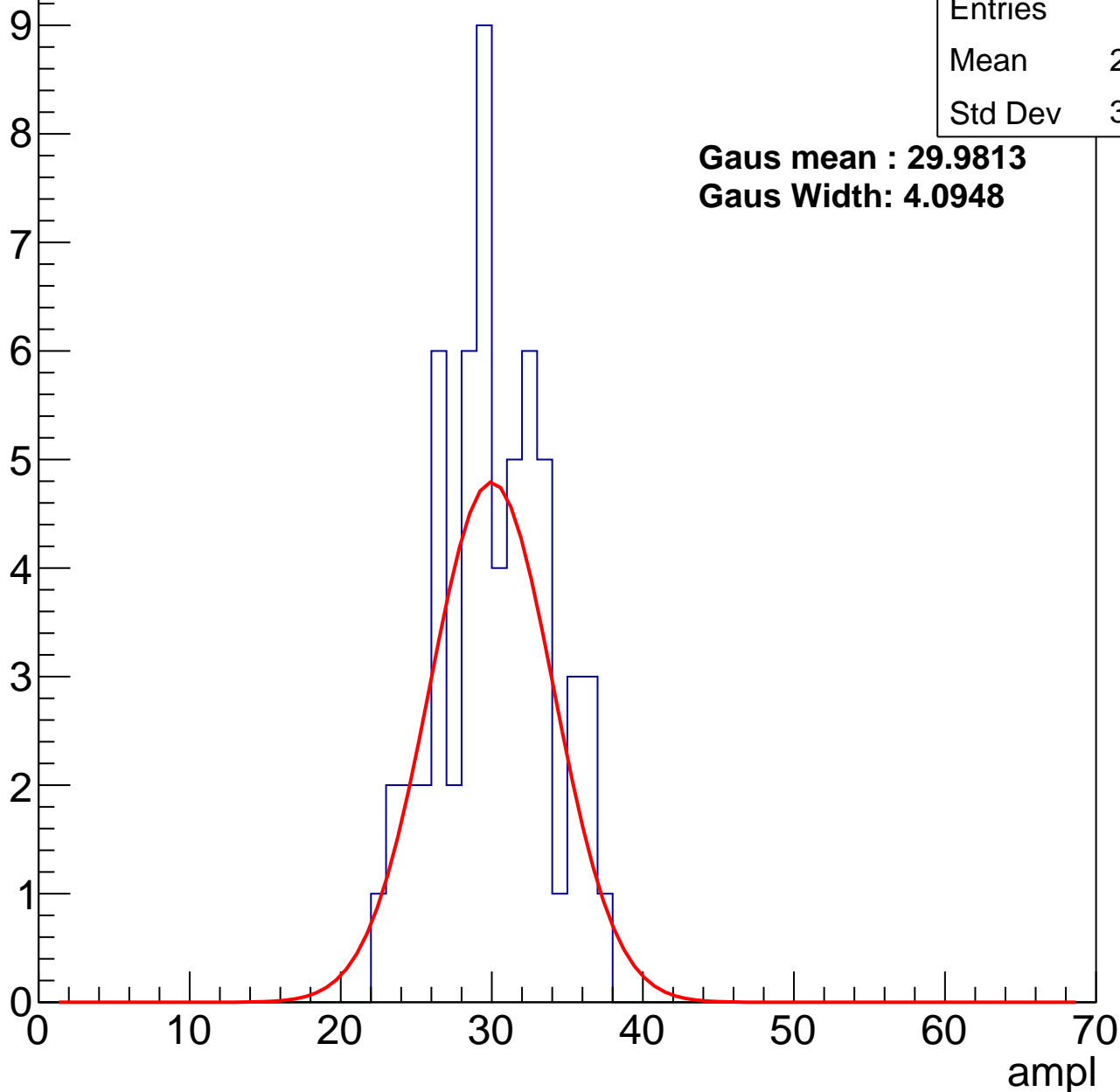
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.67
Std Dev	3.588

Gaus mean : 29.9813

Gaus Width: 4.0948



B0L002S, U5-ch98, adc1

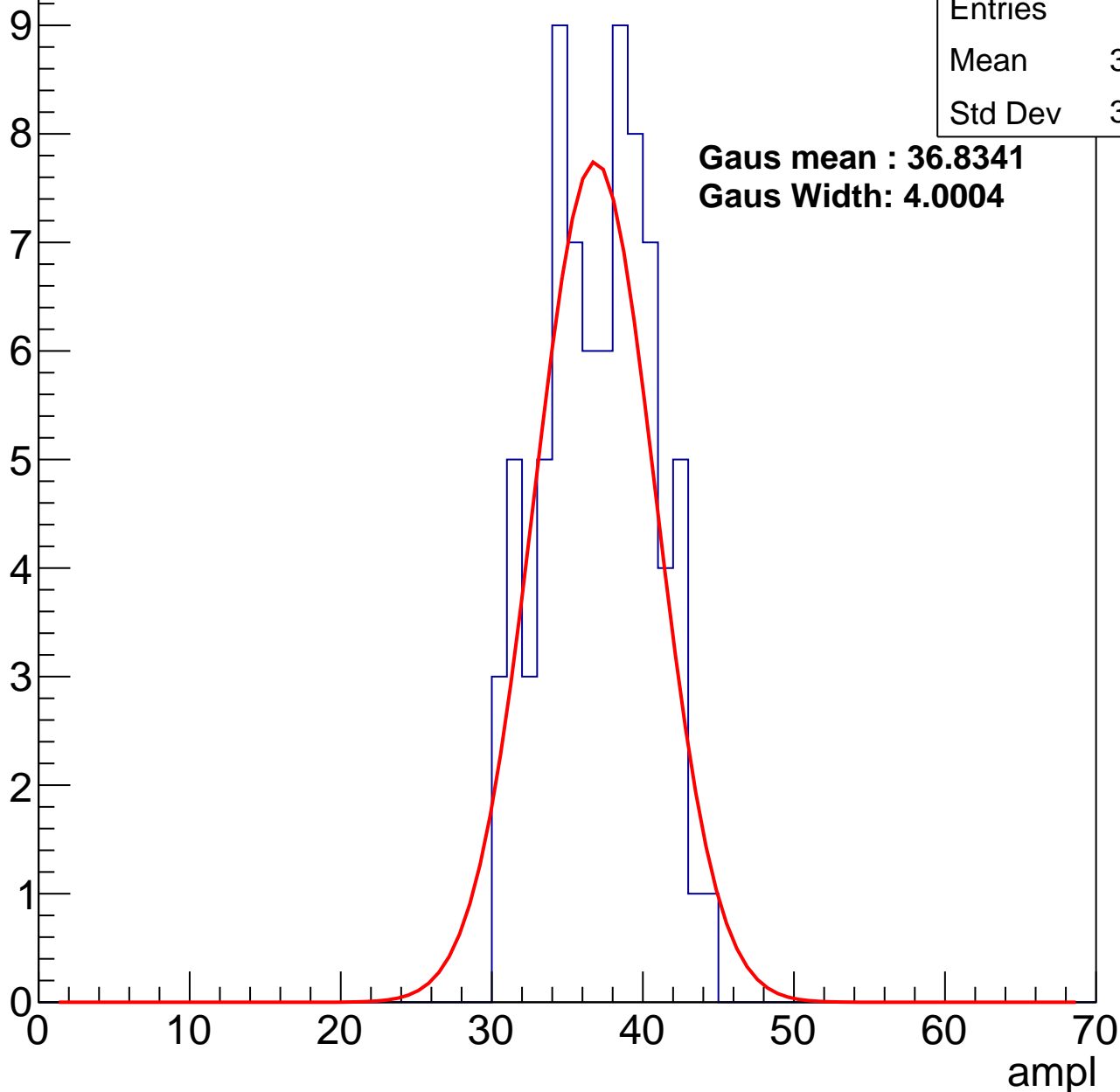
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	36.58
Std Dev	3.488

Gaus mean : 36.8341

Gaus Width: 4.0004



B0L002S, U5-ch98, adc2

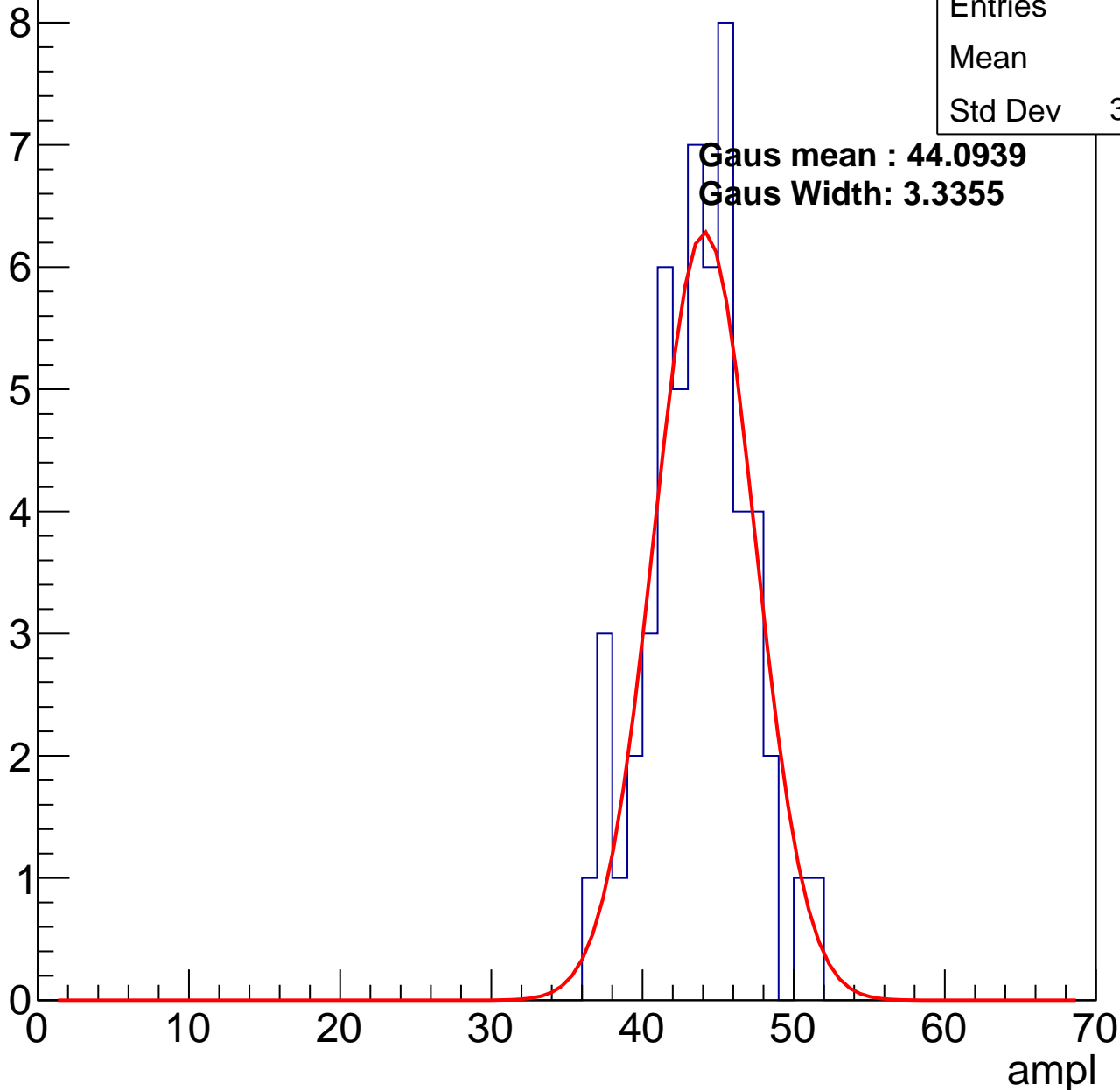
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.2
Std Dev	3.245

Gaus mean : 44.0939

Gaus Width: 3.3355

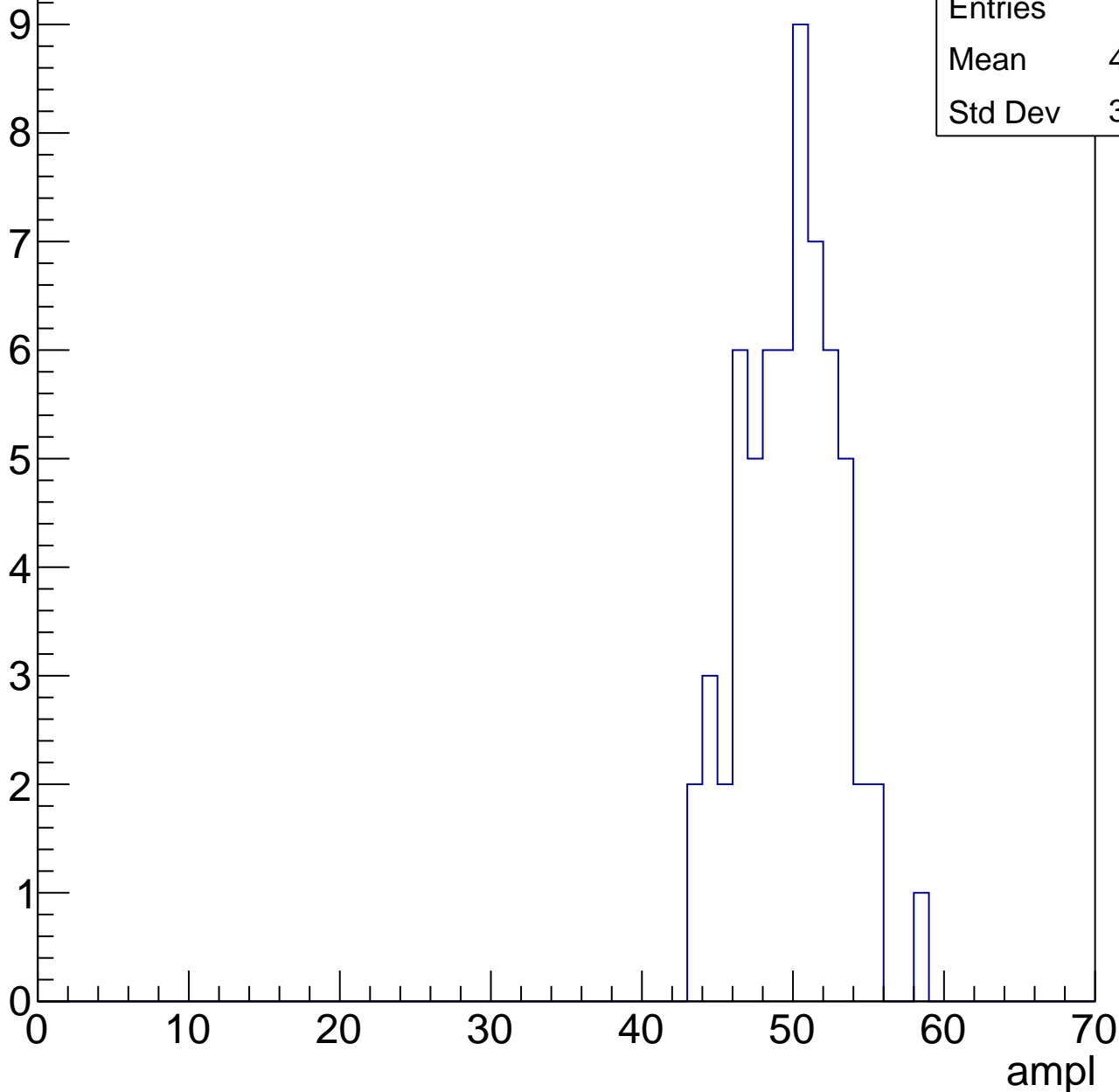


B0L002S, U5-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

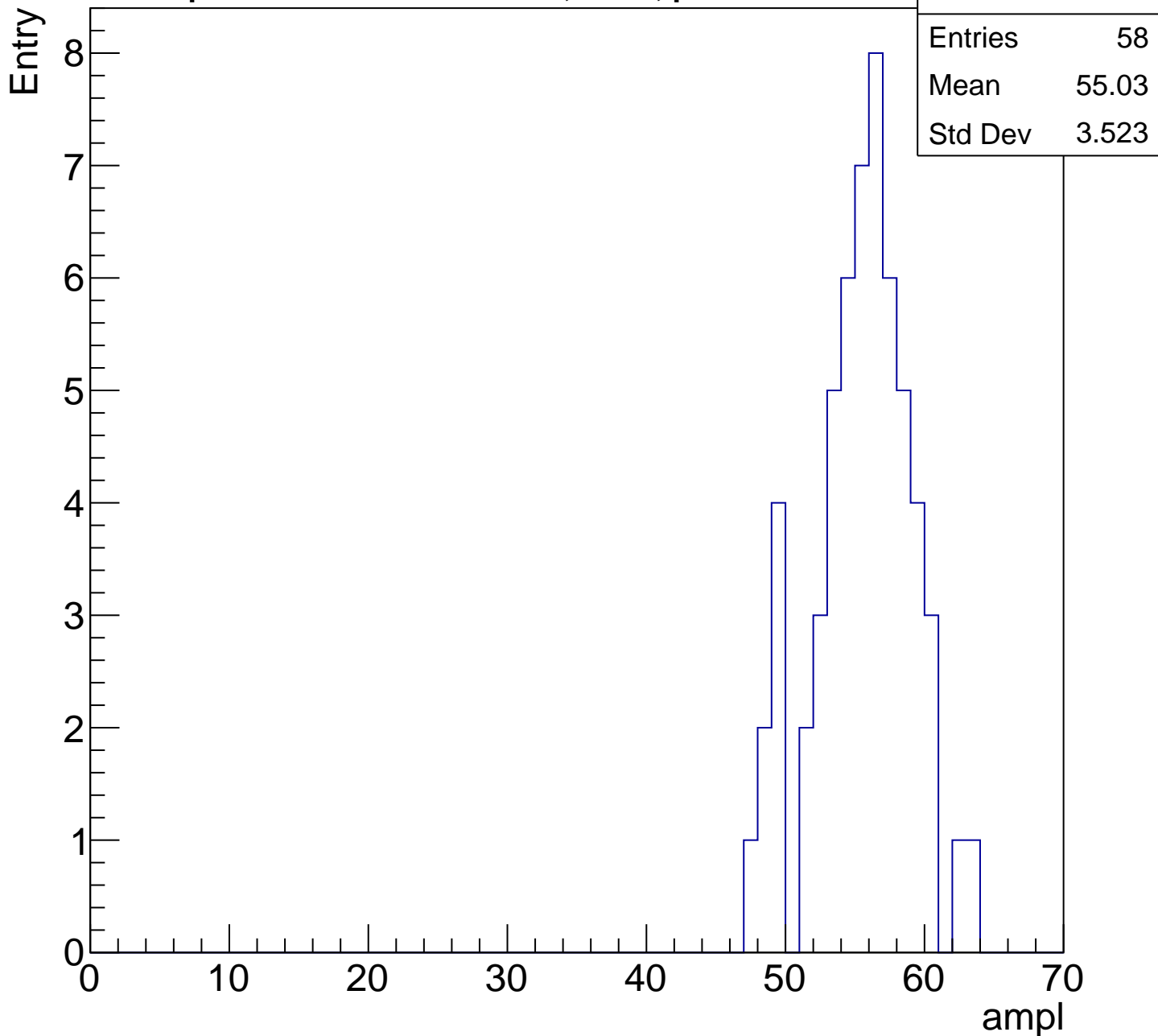
Entry

Entries	62
Mean	49.37
Std Dev	3.174



B0L002S, U5-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

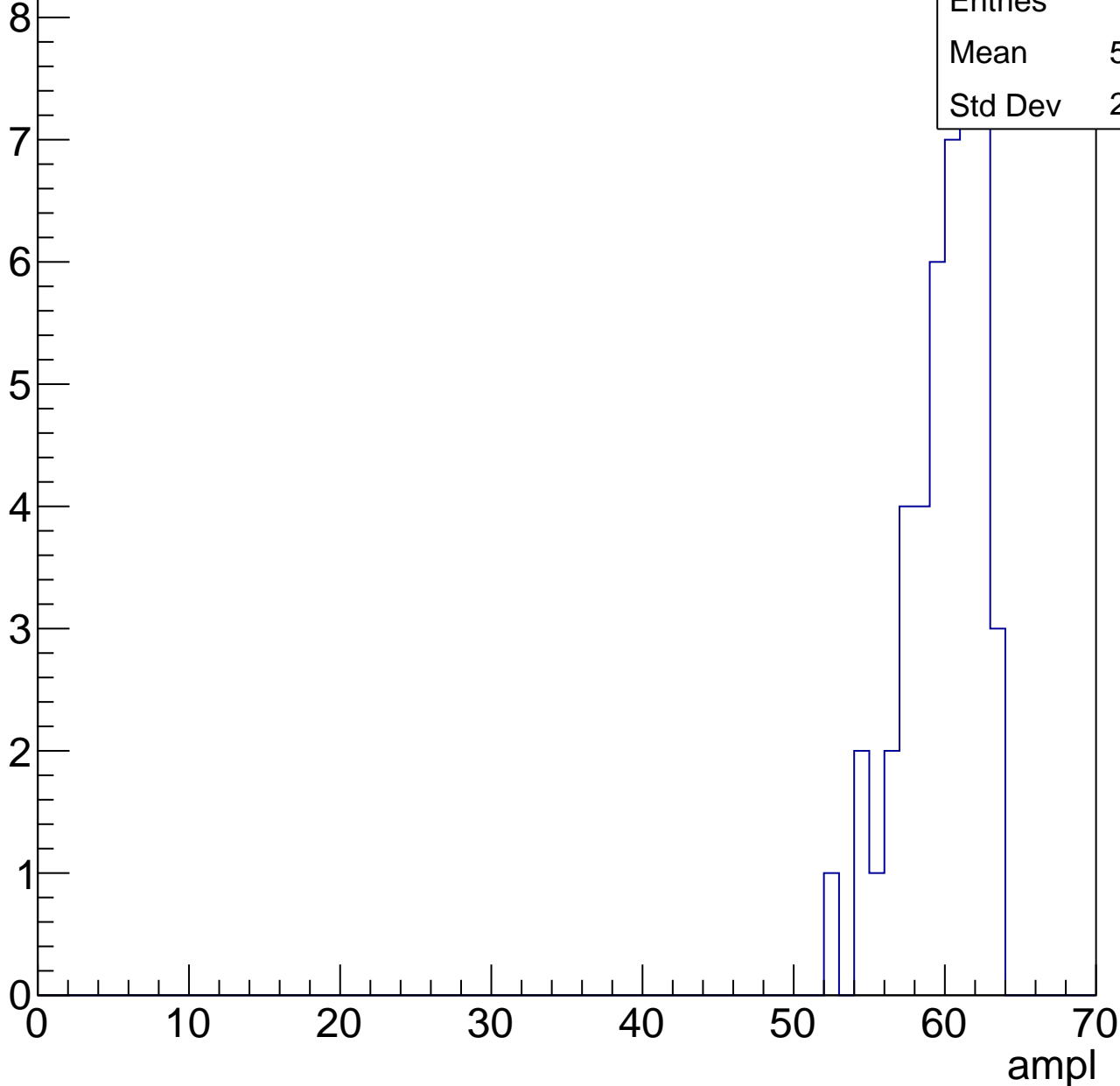


B0L002S, U5-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	59.43
Std Dev	2.568

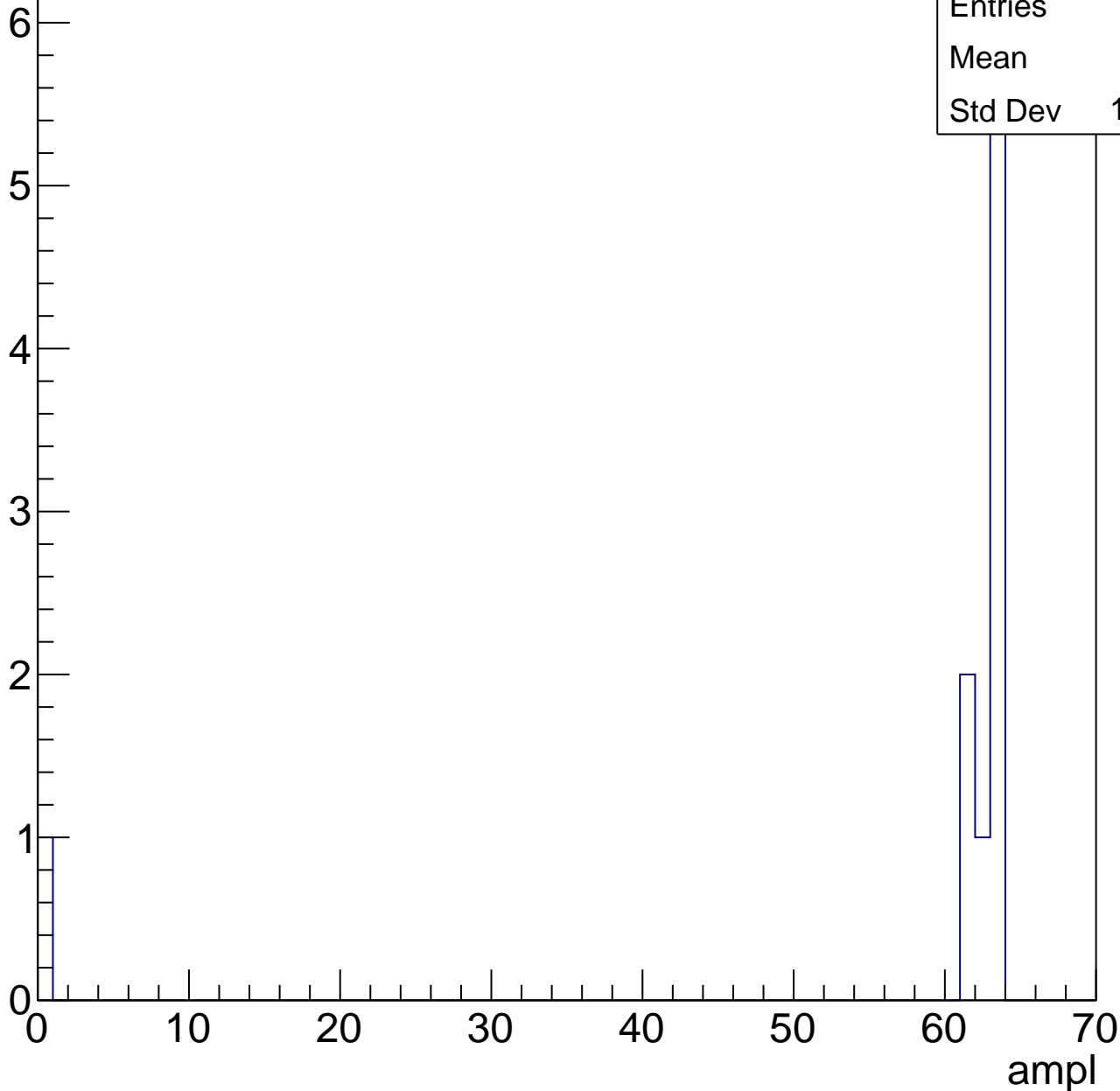


B0L002S, U5-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	56.2
Std Dev	18.75



B0L002S, U5-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch99, adc0

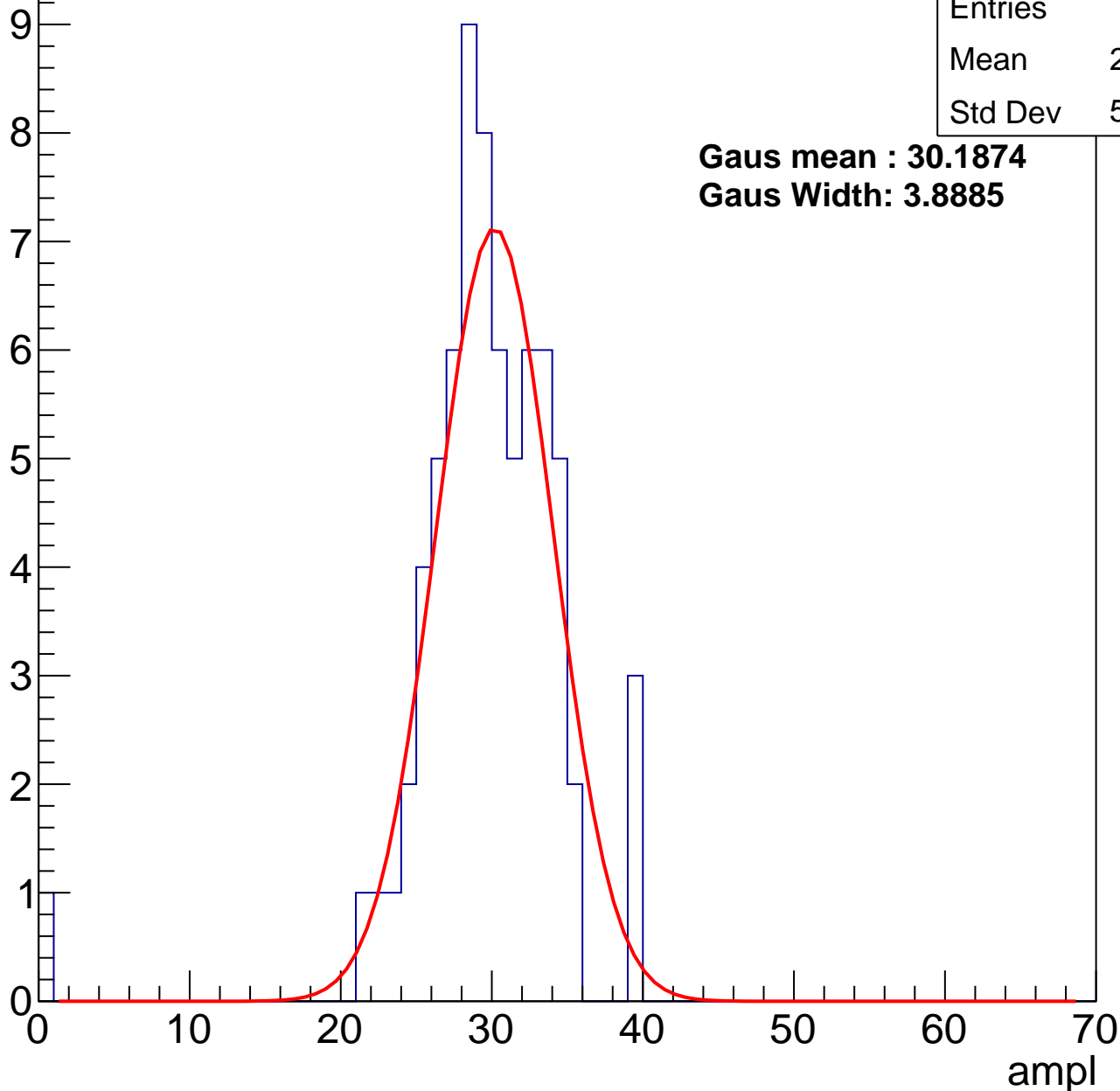
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.18
Std Dev	5.108

Gaus mean : 30.1874

Gaus Width: 3.8885



B0L002S, U5-ch99, adc1

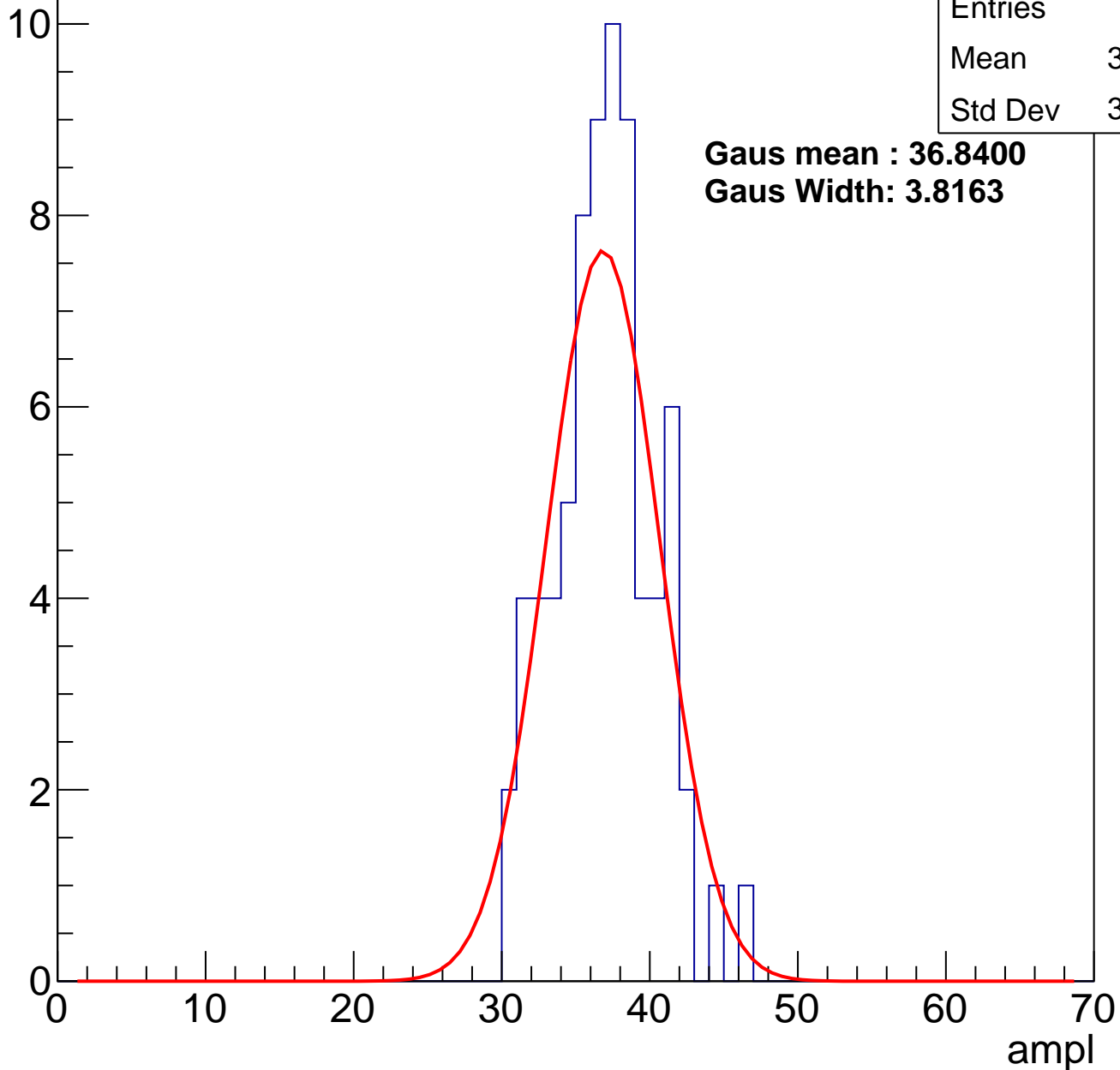
calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	36.52
Std Dev	3.336

Gaus mean : 36.8400

Gaus Width: 3.8163

Entry

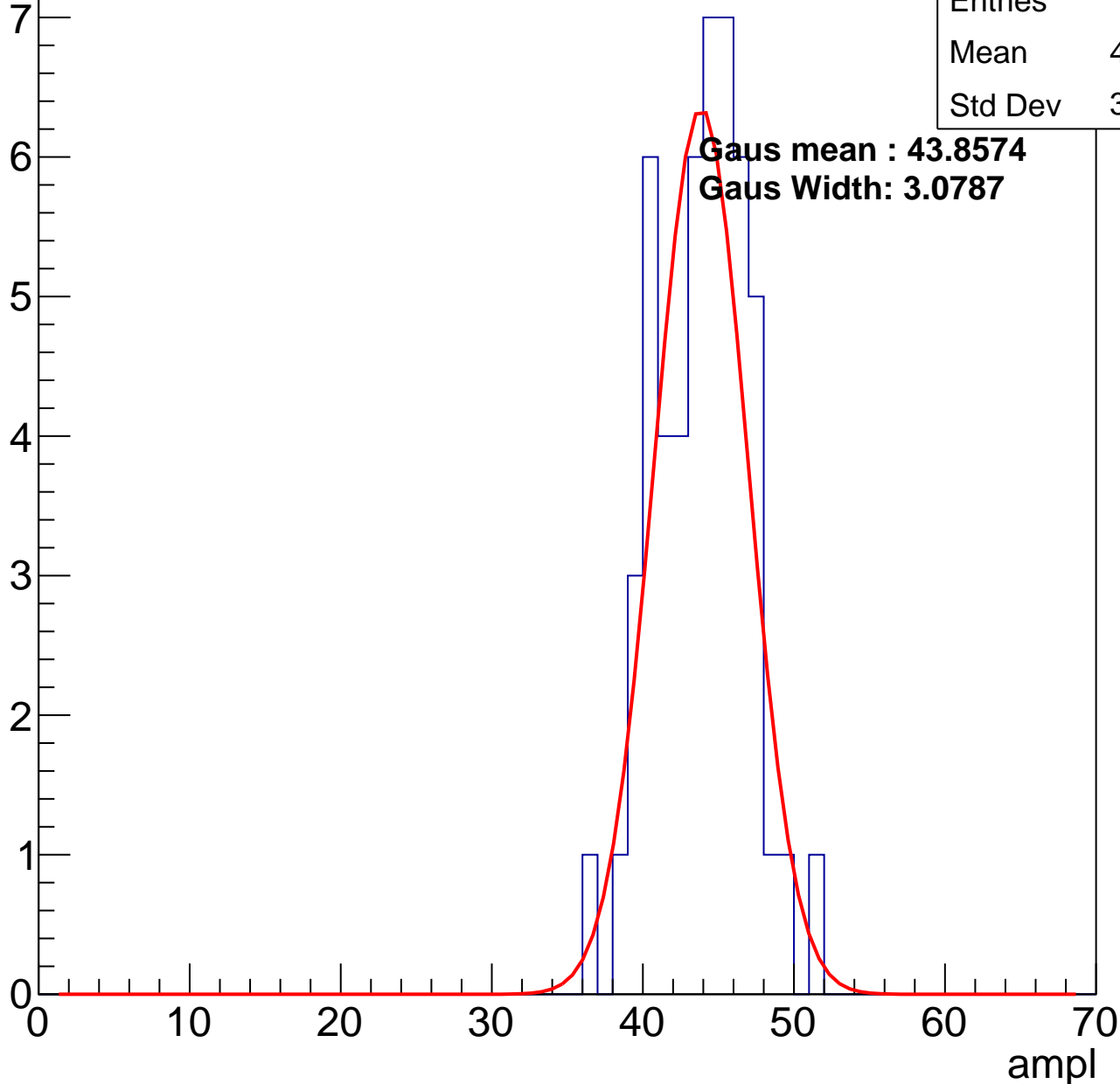


B0L002S, U5-ch99, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.45
Std Dev	3.007

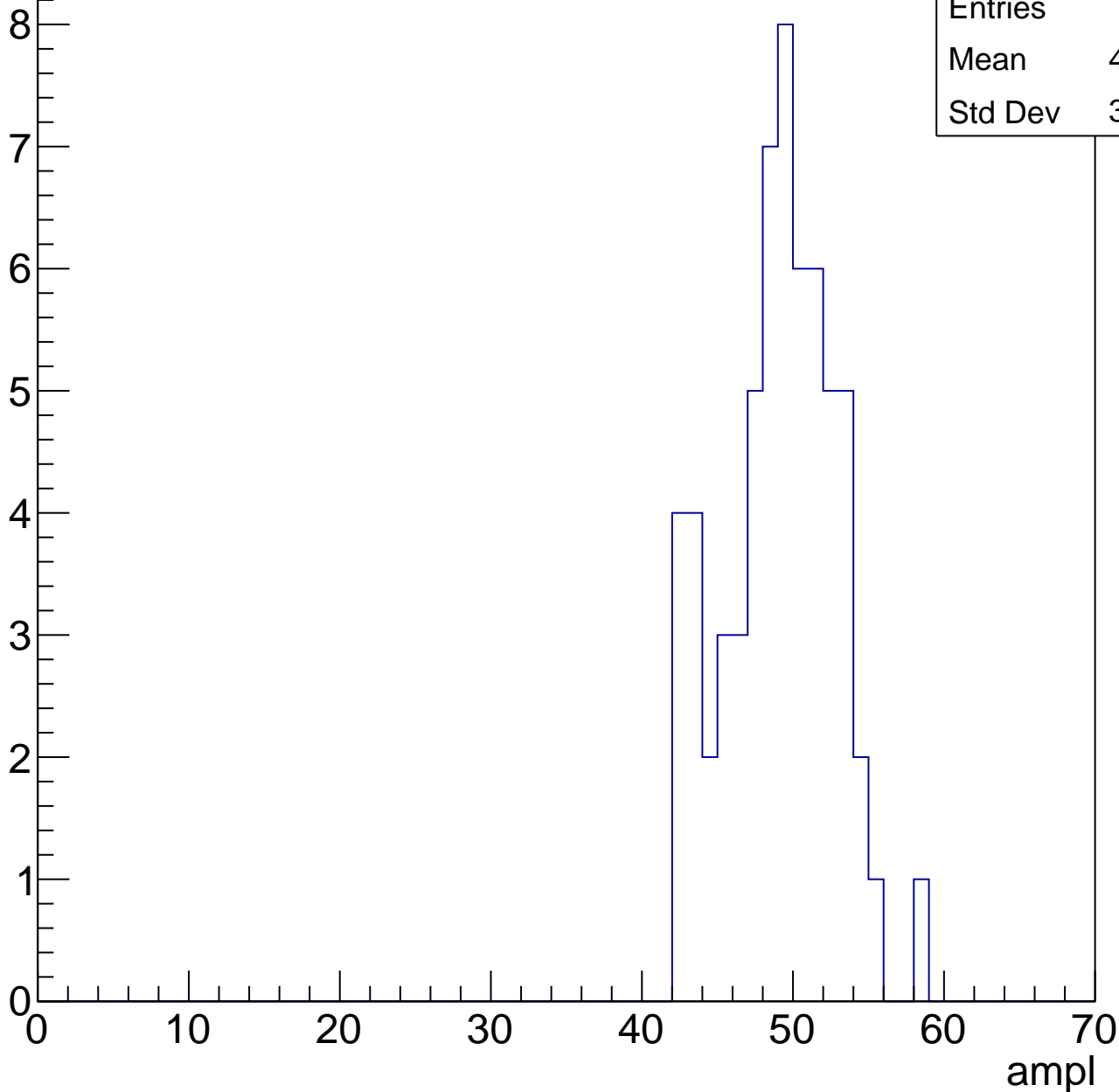


B0L002S, U5-ch99, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	48.65
Std Dev	3.606

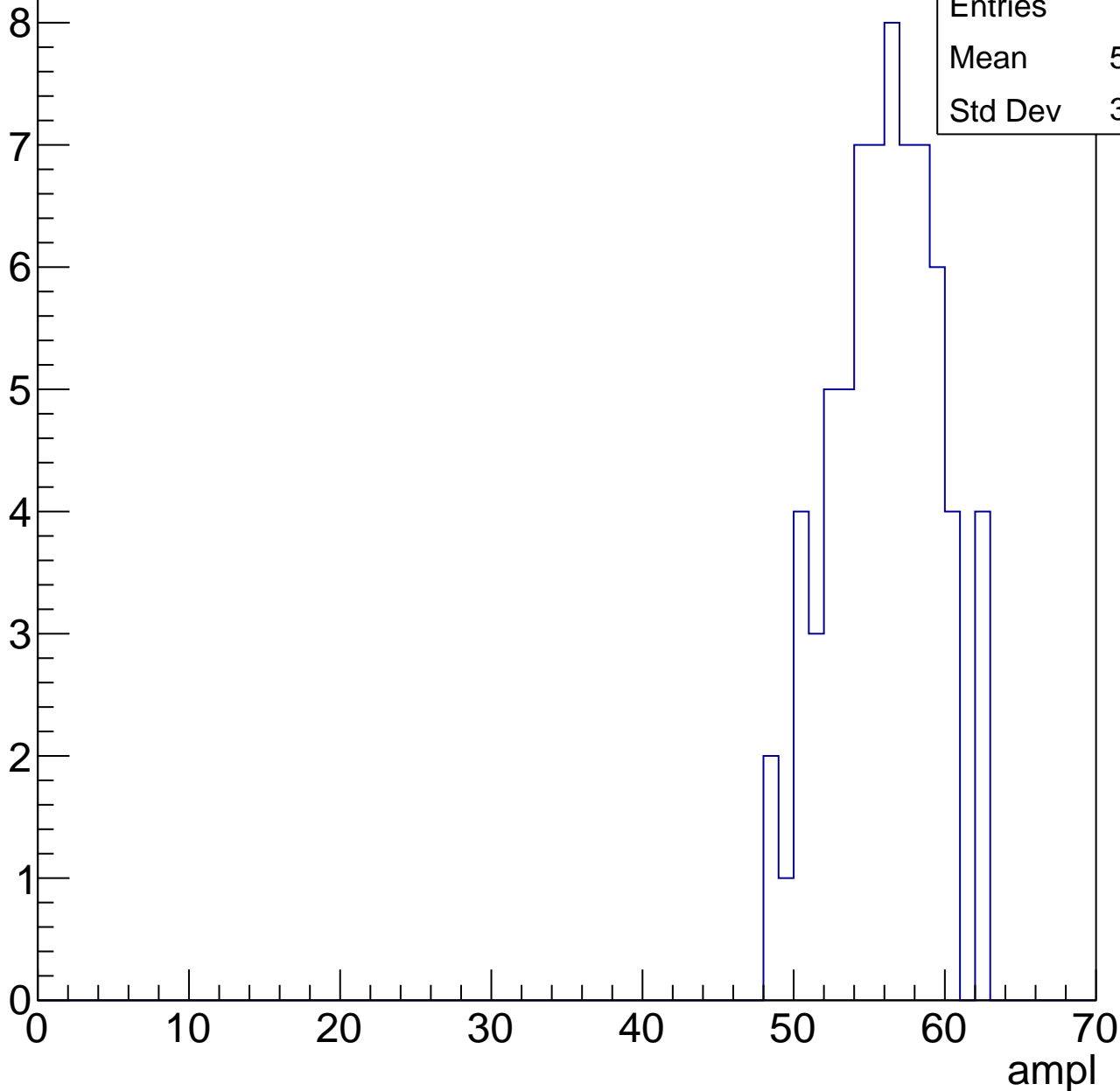


B0L002S, U5-ch99, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	55.44
Std Dev	3.446

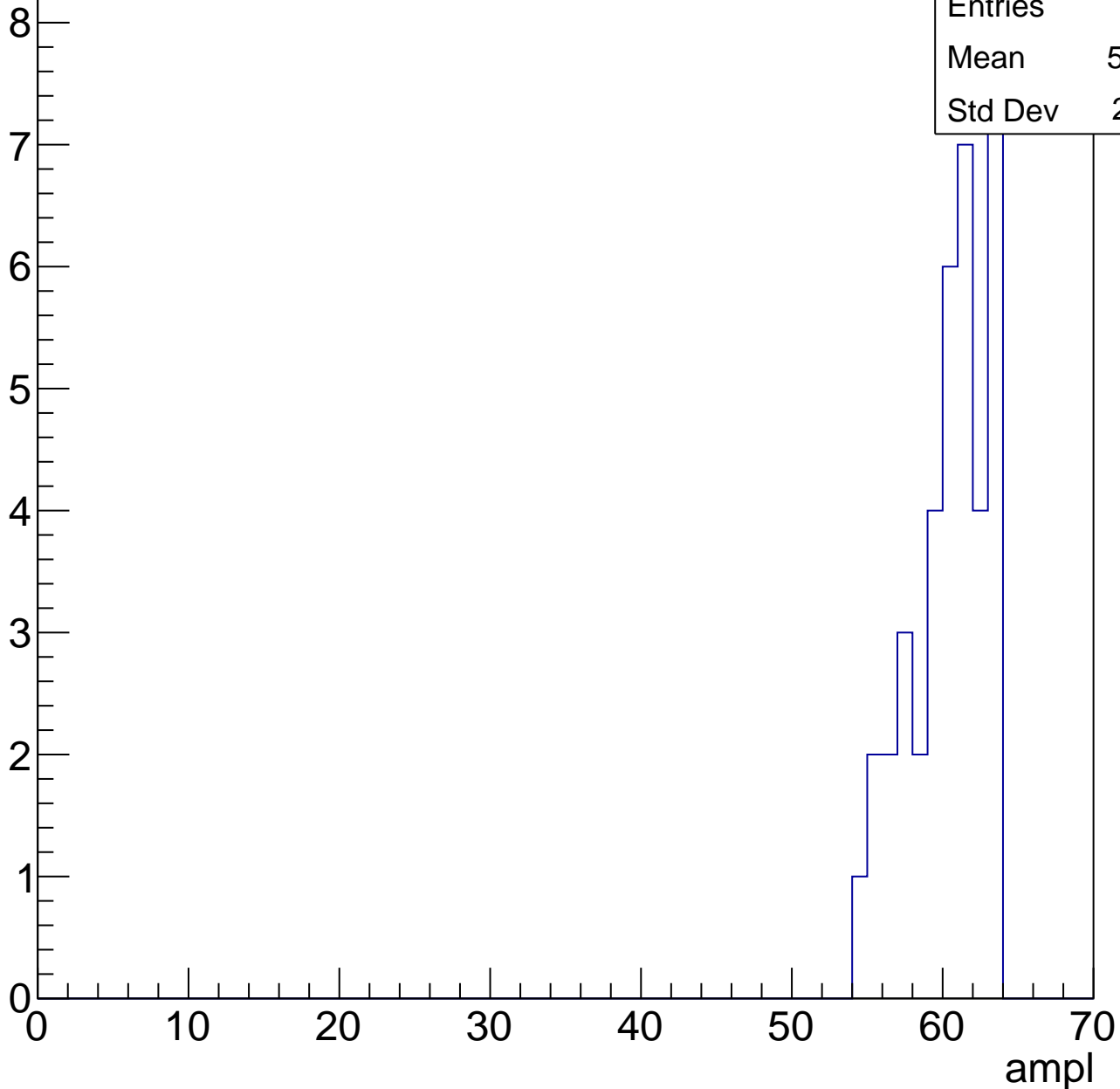


B0L002S, U5-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

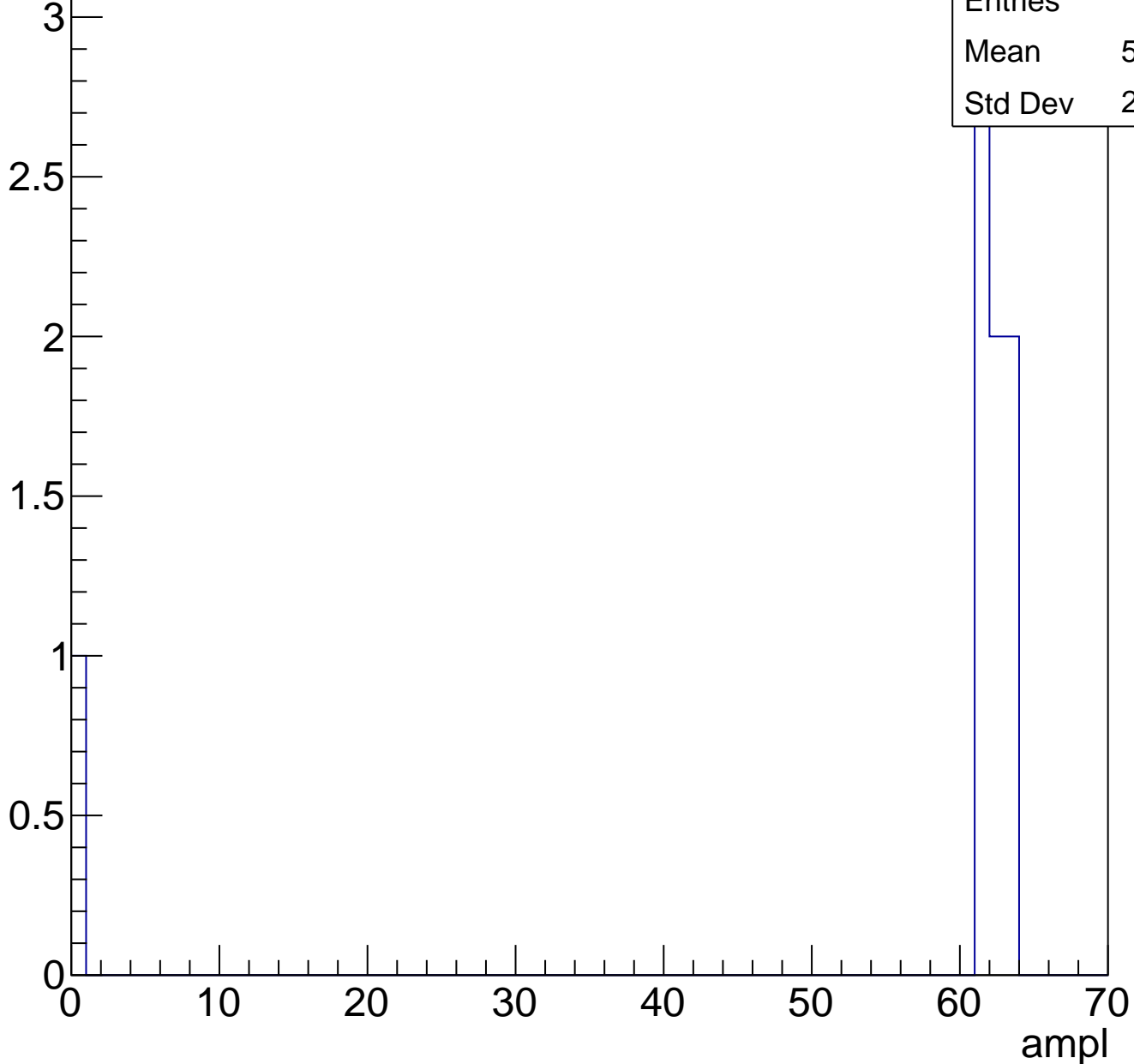
Entries	39
Mean	59.95
Std Dev	2.541



B0L002S, U5-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch100, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	27.22
Std Dev	4.673

Gaus mean : 27.7910

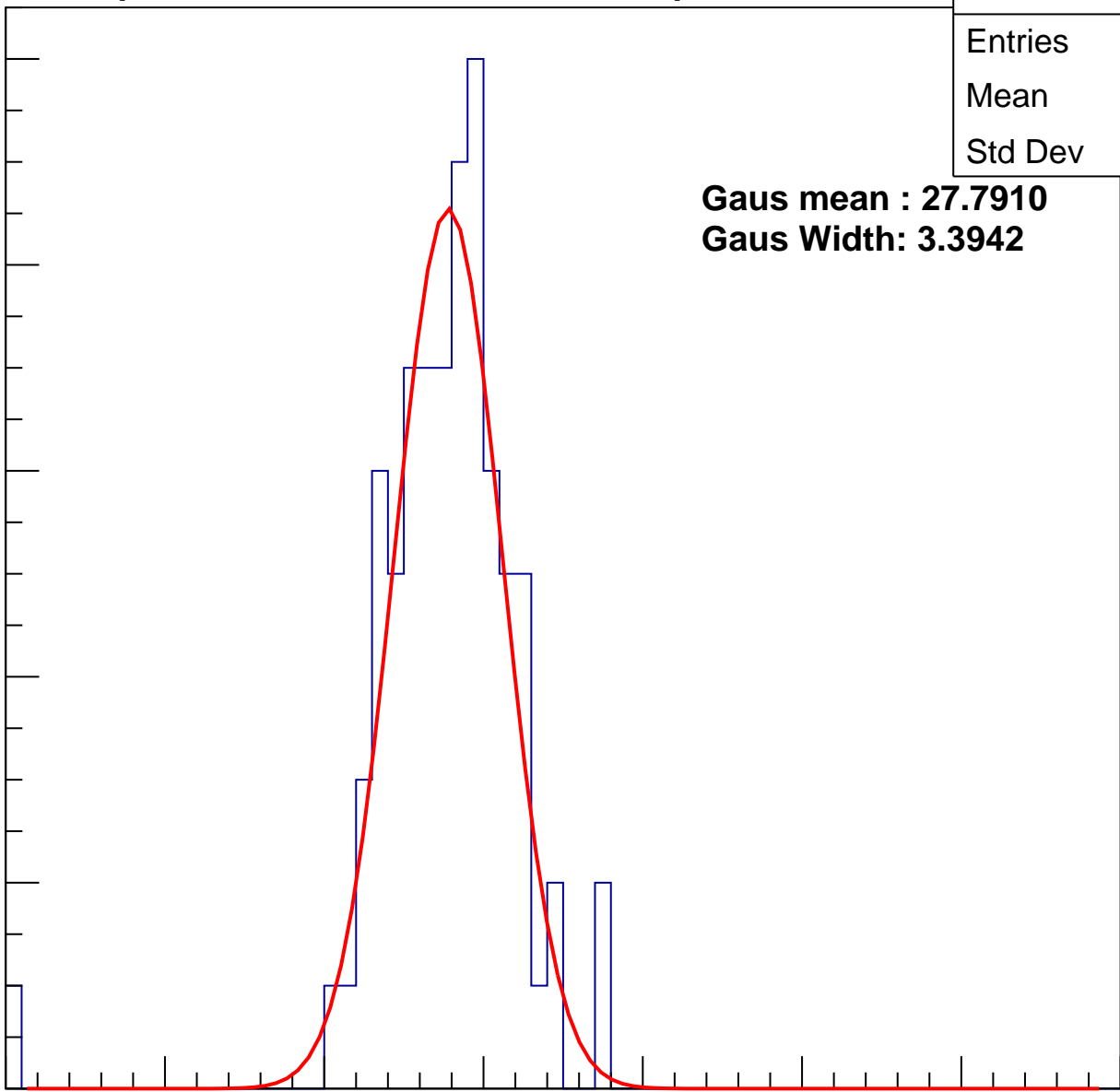
Gaus Width: 3.3942

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch100, adc1

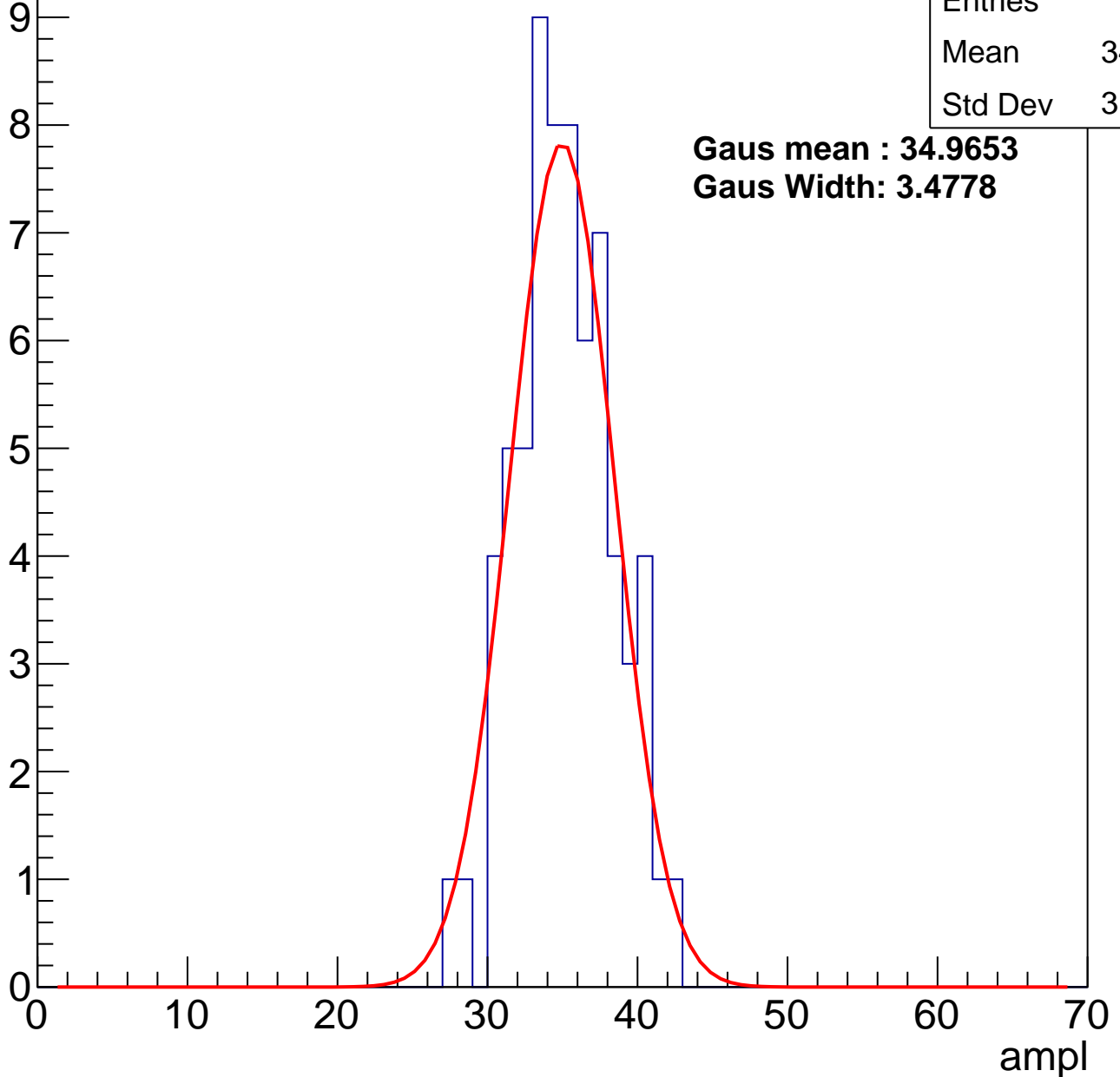
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	34.72
Std Dev	3.185

Gaus mean : 34.9653

Gaus Width: 3.4778



B0L002S, U5-ch100, adc2

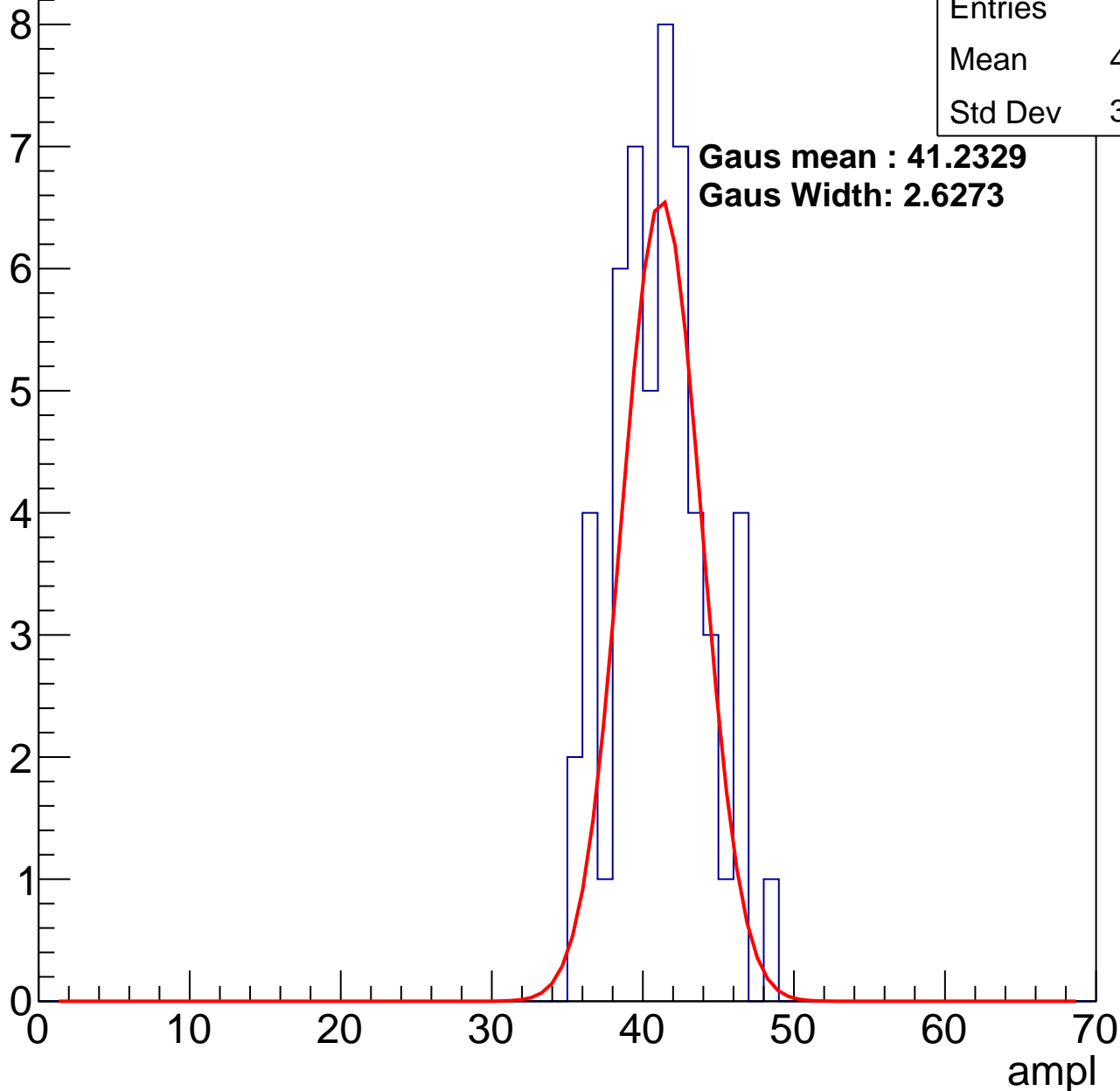
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	40.66
Std Dev	3.022

Gaus mean : 41.2329

Gaus Width: 2.6273

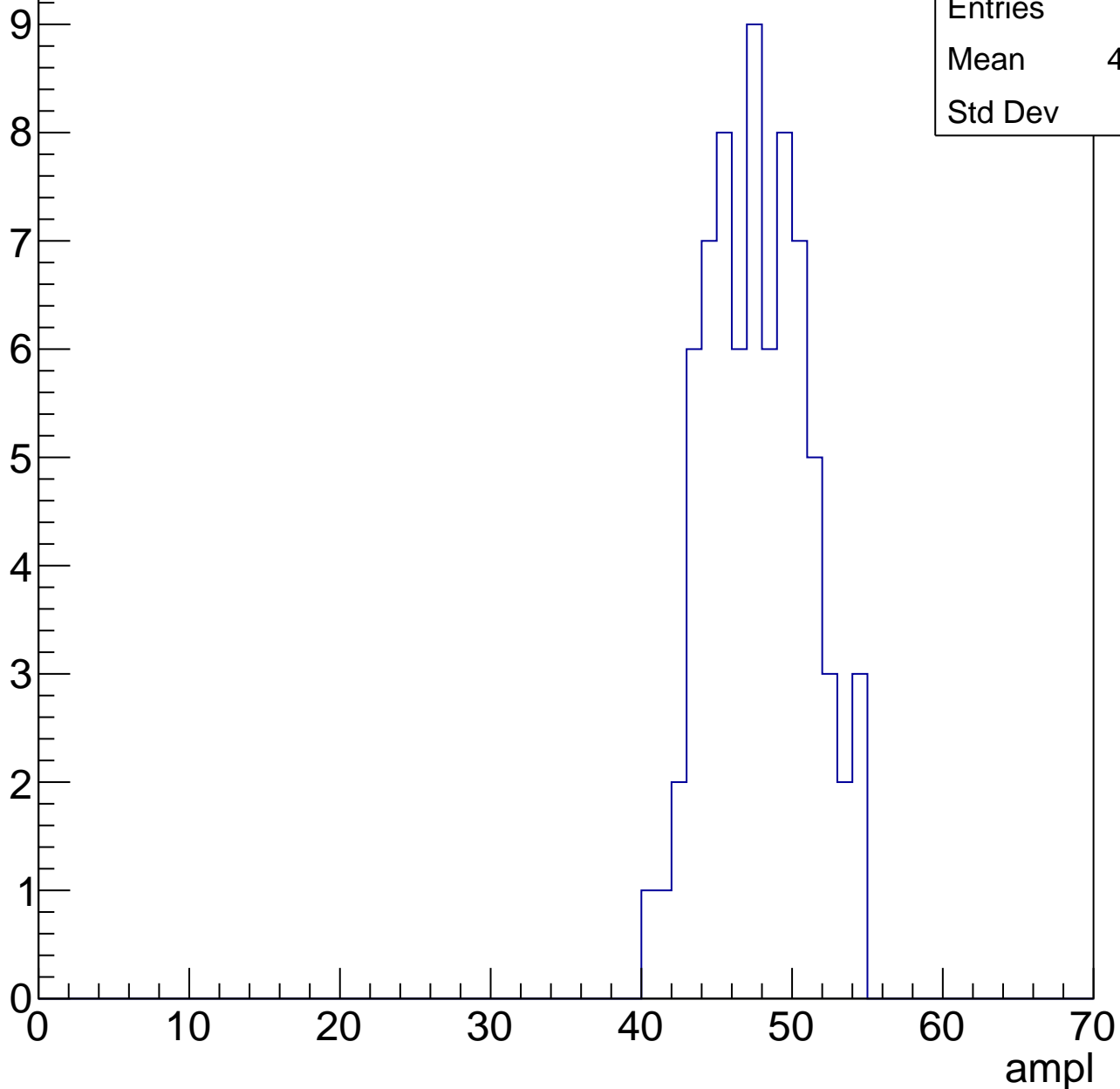


B0L002S, U5-ch100, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	47.28
Std Dev	3.29

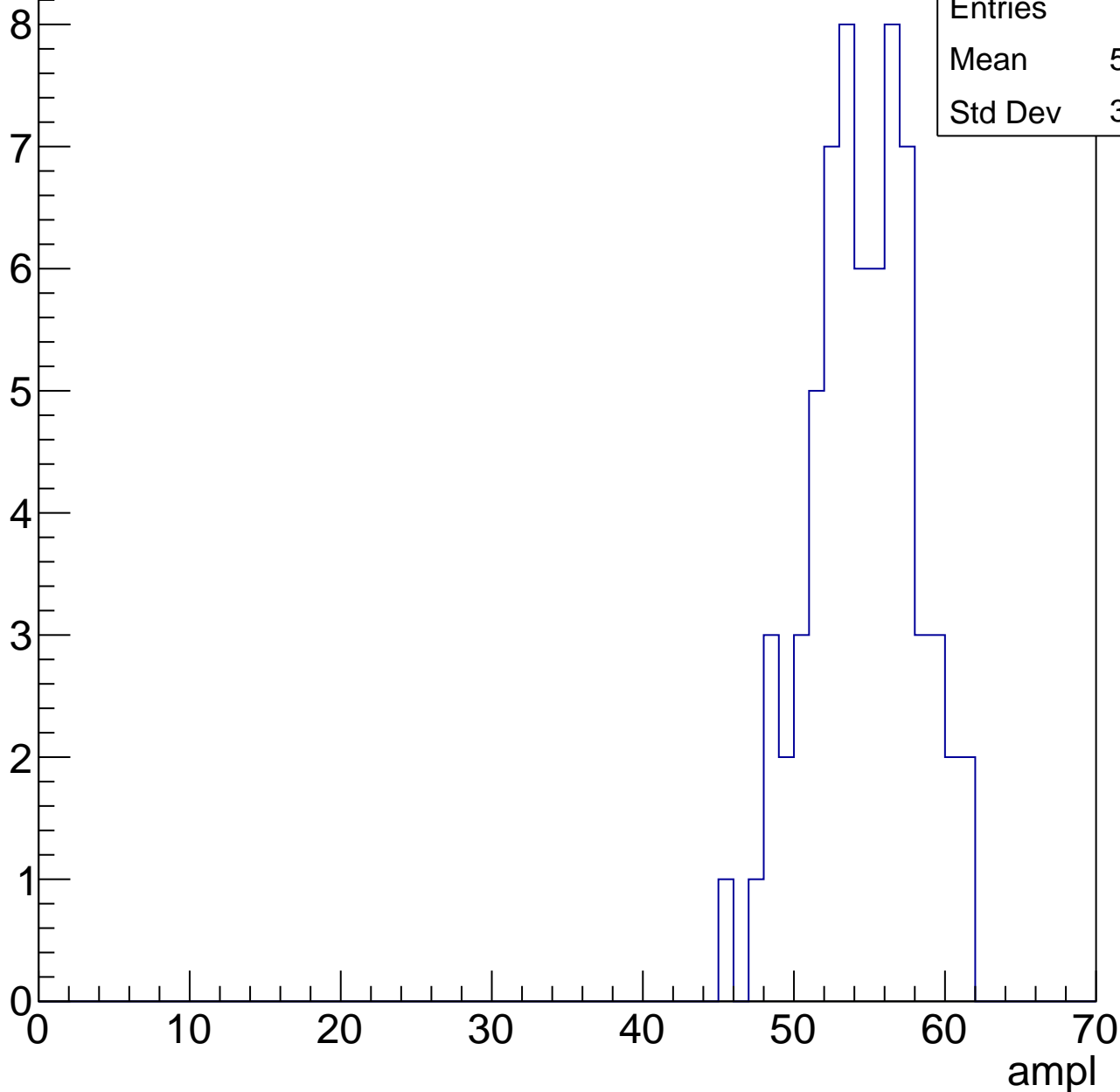


B0L002S, U5-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	54.04
Std Dev	3.479

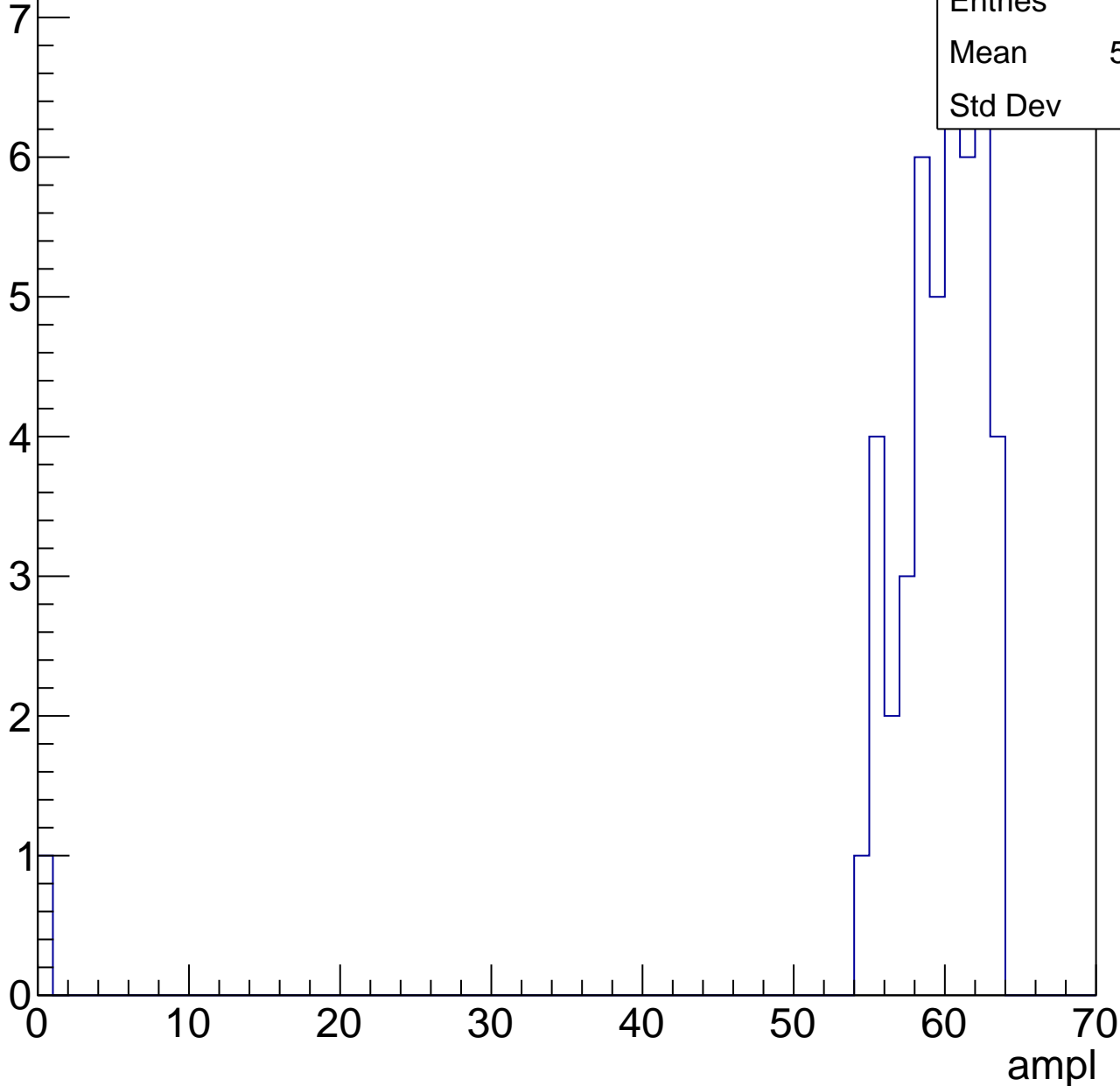


B0L002S, U5-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	58.09
Std Dev	9

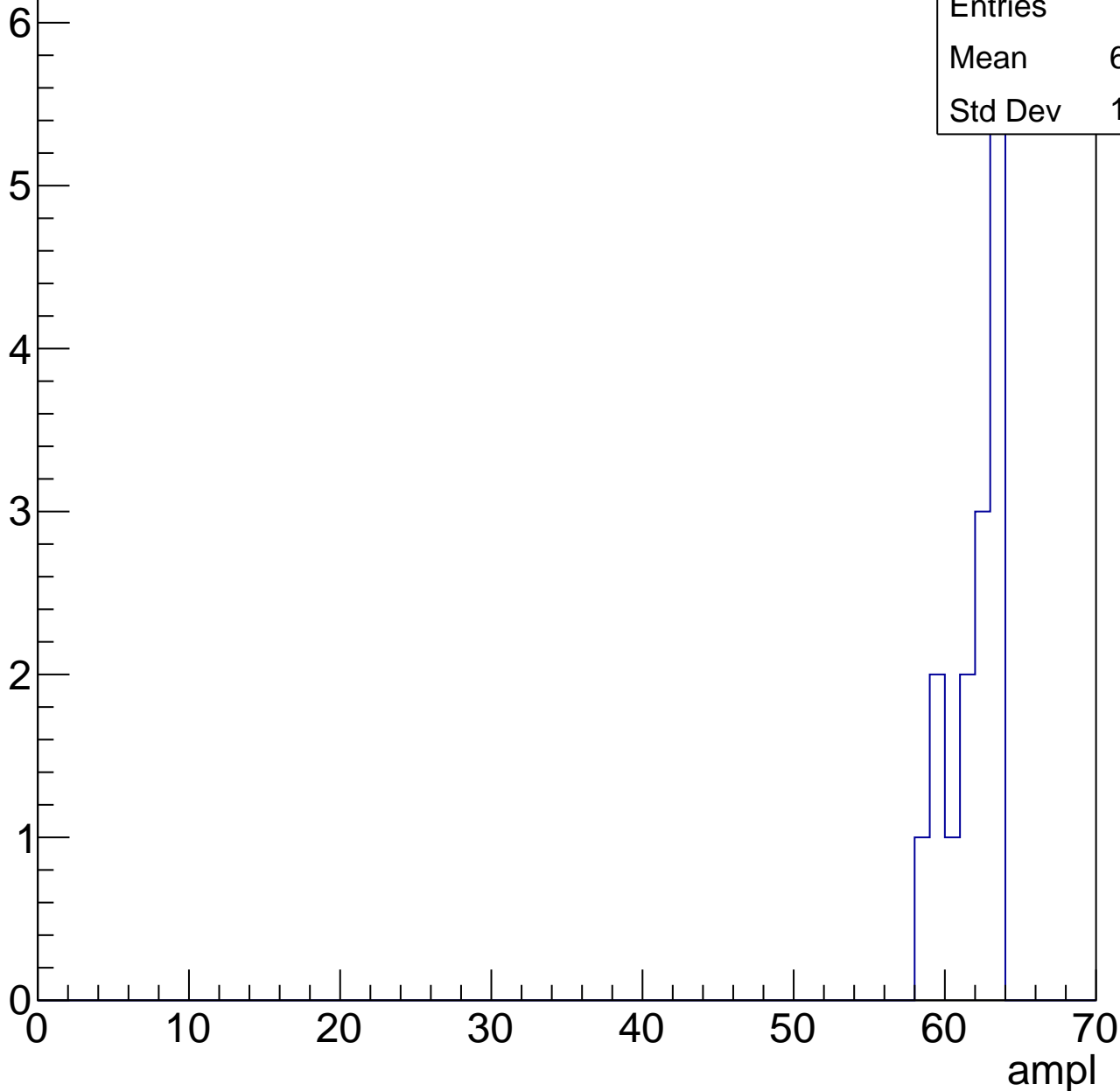


B0L002S, U5-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	15
Mean	61.47
Std Dev	1.668



B0L002S, U5-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L002S, U5-ch101, adc0

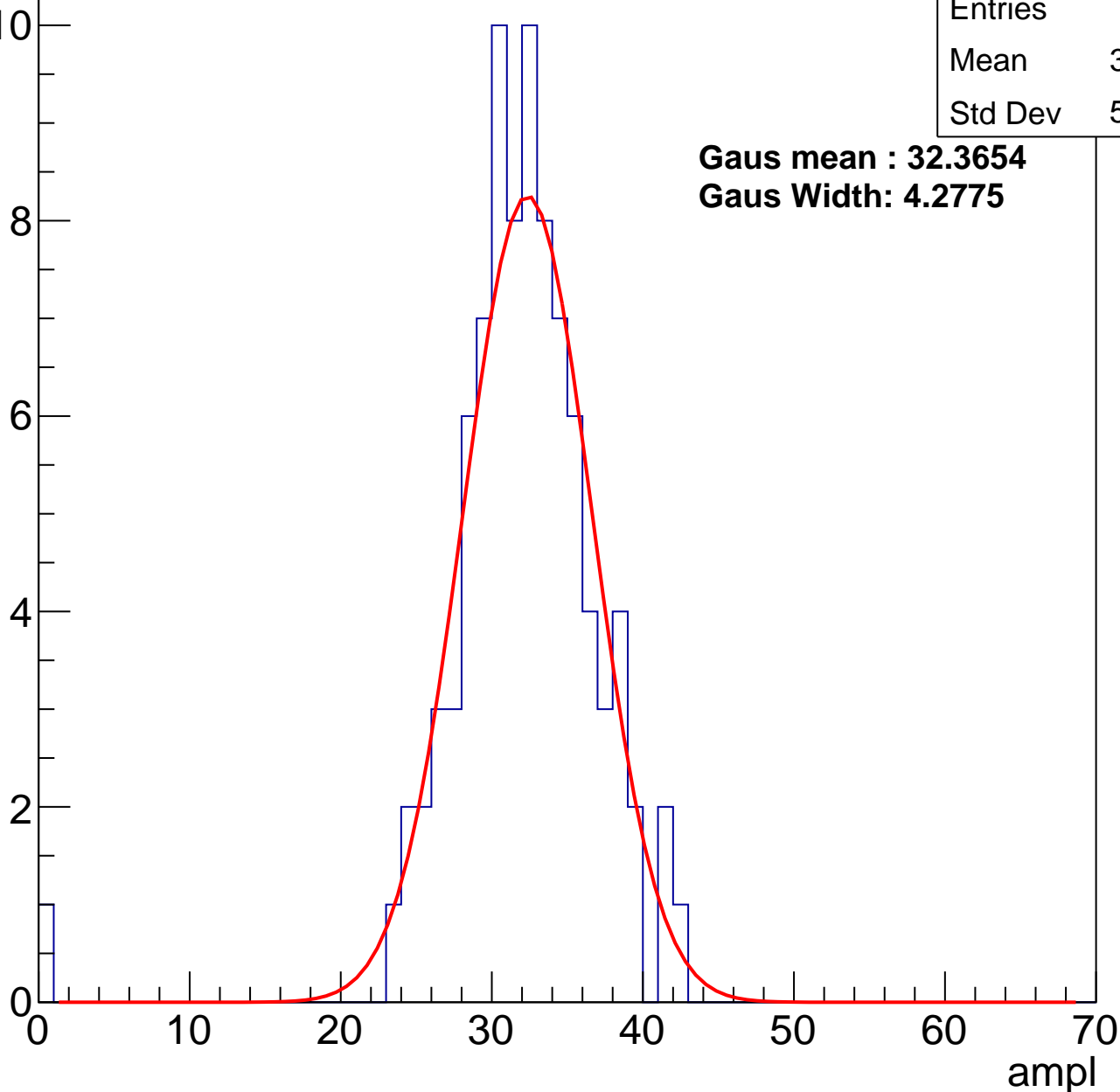
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	90
Mean	31.56
Std Dev	5.203

Gaus mean : 32.3654

Gaus Width: 4.2775



B0L002S, U5-ch101, adc1

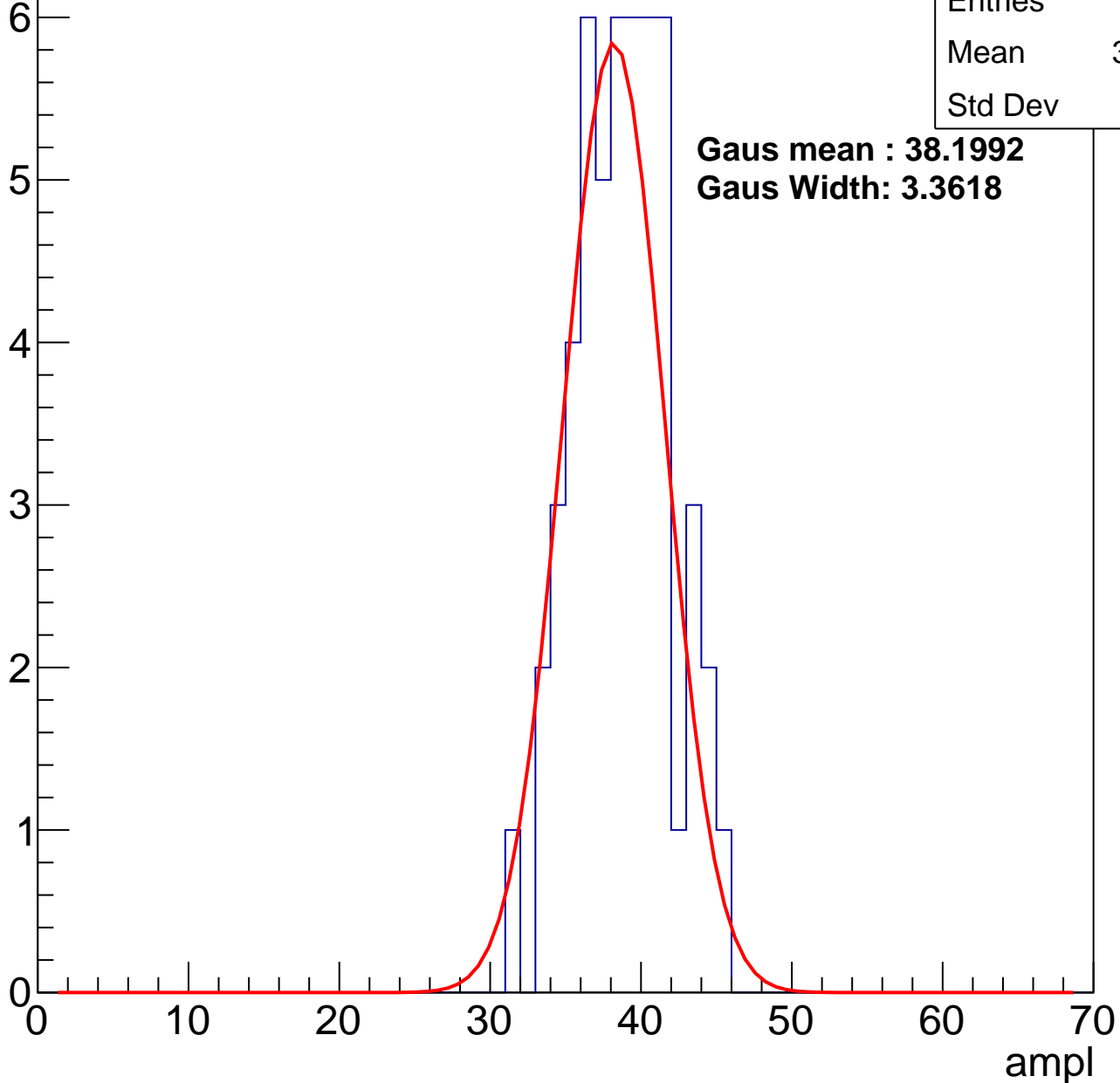
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	38.31
Std Dev	3.11

Gaus mean : 38.1992

Gaus Width: 3.3618



B0L002S, U5-ch101, adc2

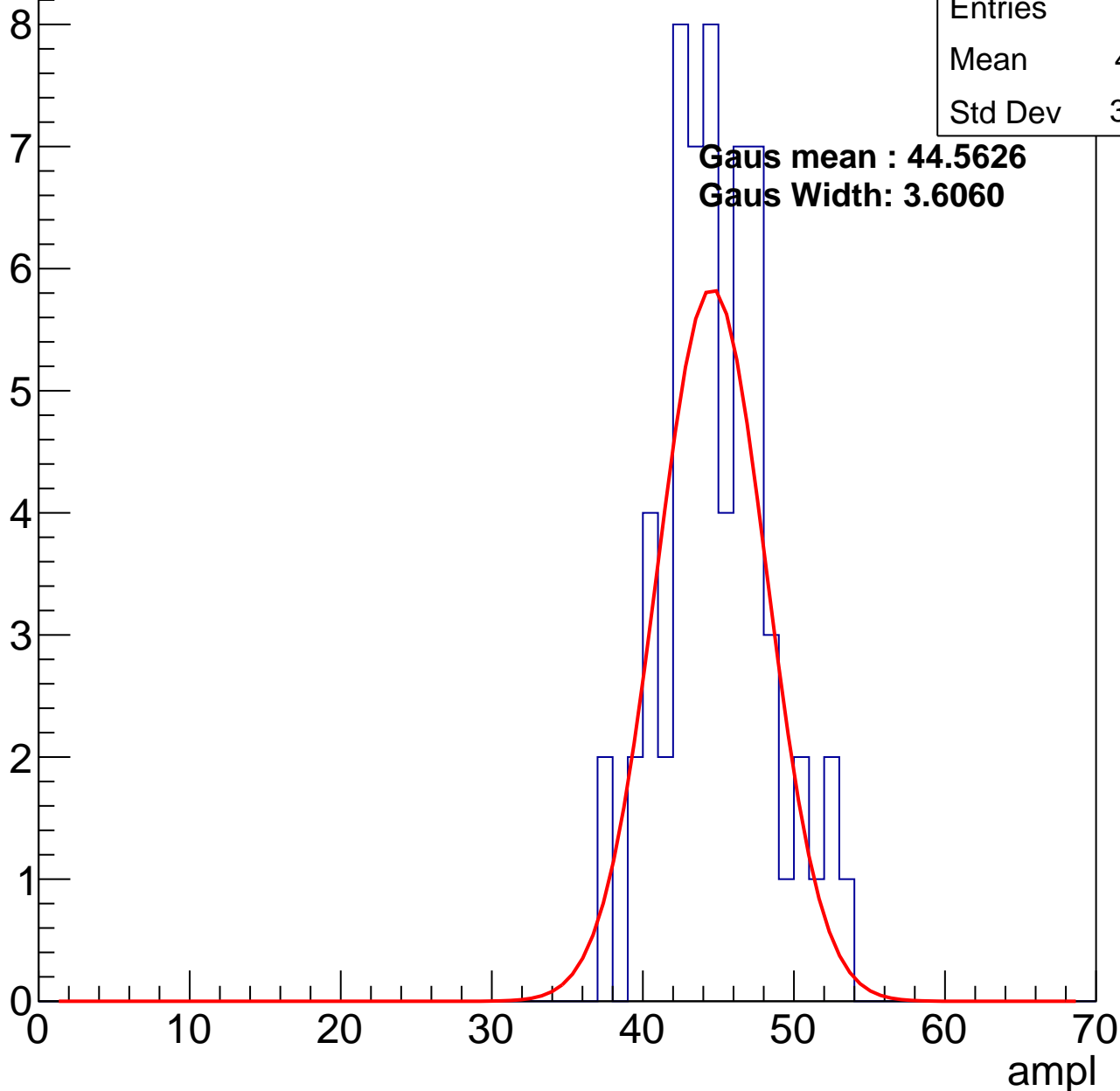
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.51
Std Dev	3.509

Gaus mean : 44.5626

Gaus Width: 3.6060

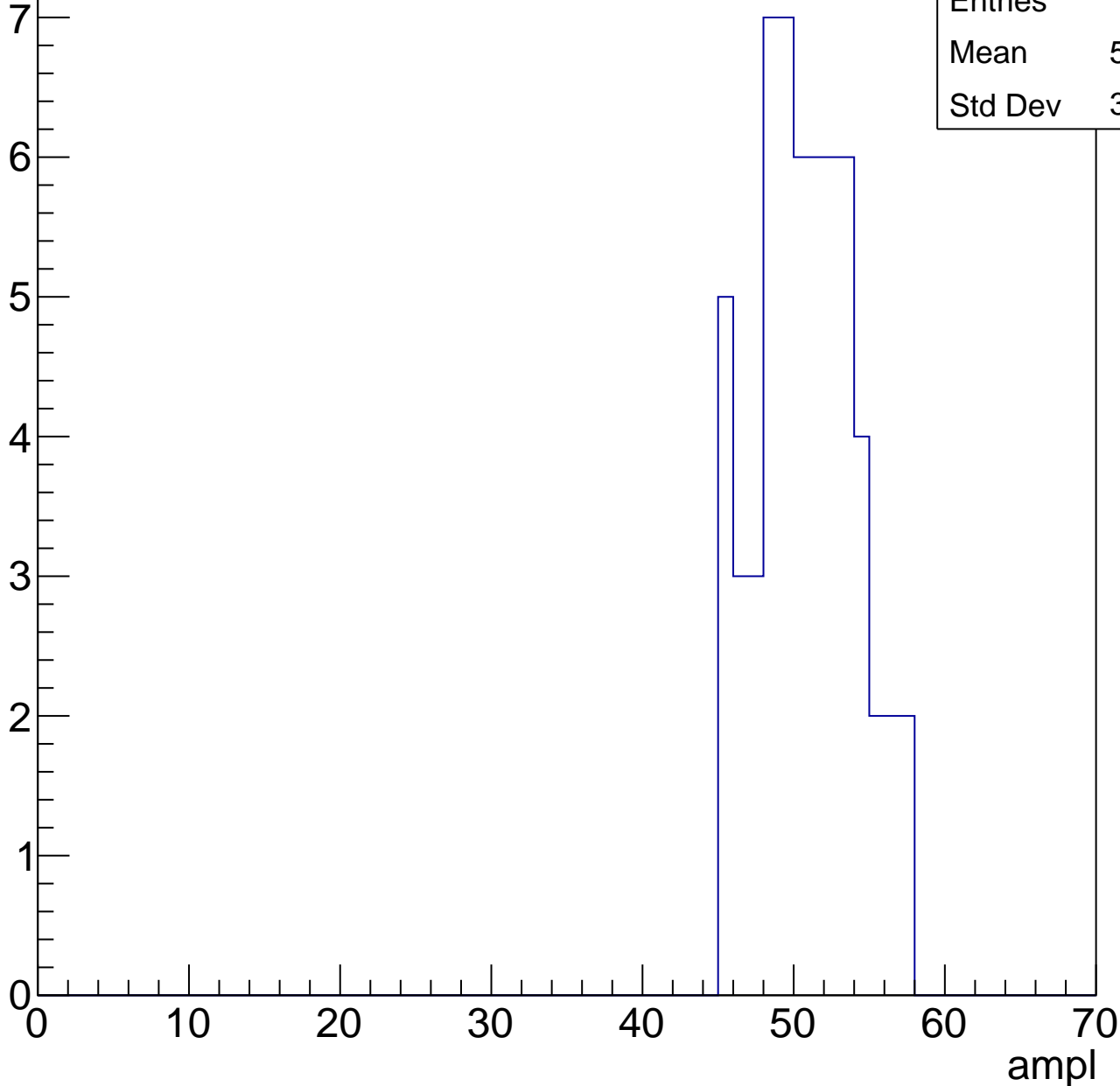


B0L002S, U5-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.36
Std Dev	3.177

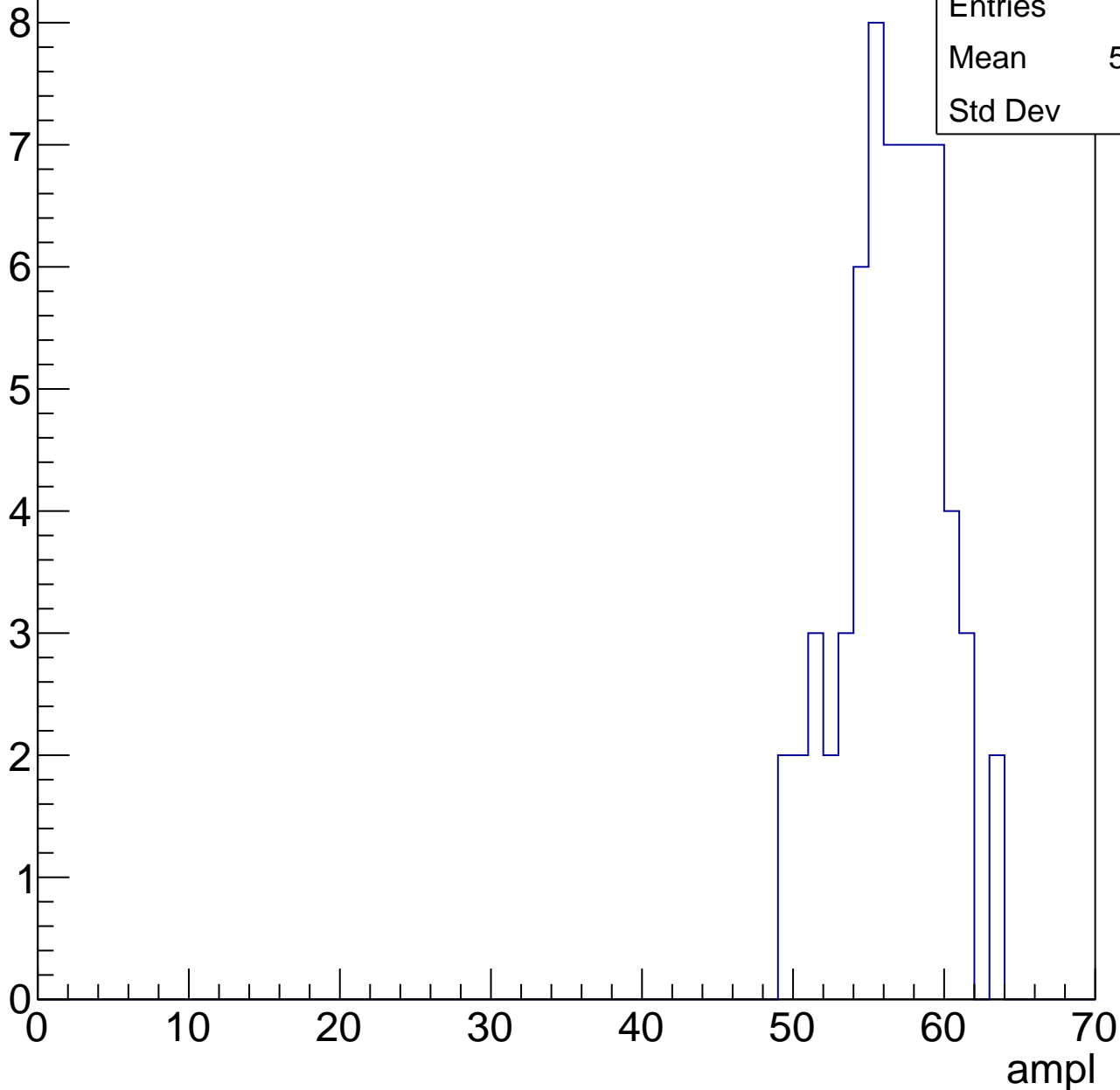


B0L002S, U5-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.14
Std Dev	3.26



B0L002S, U5-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	40
Mean	59.38
Std Dev	9.674

Entry

10

8

6

4

2

0

0

10

20

30

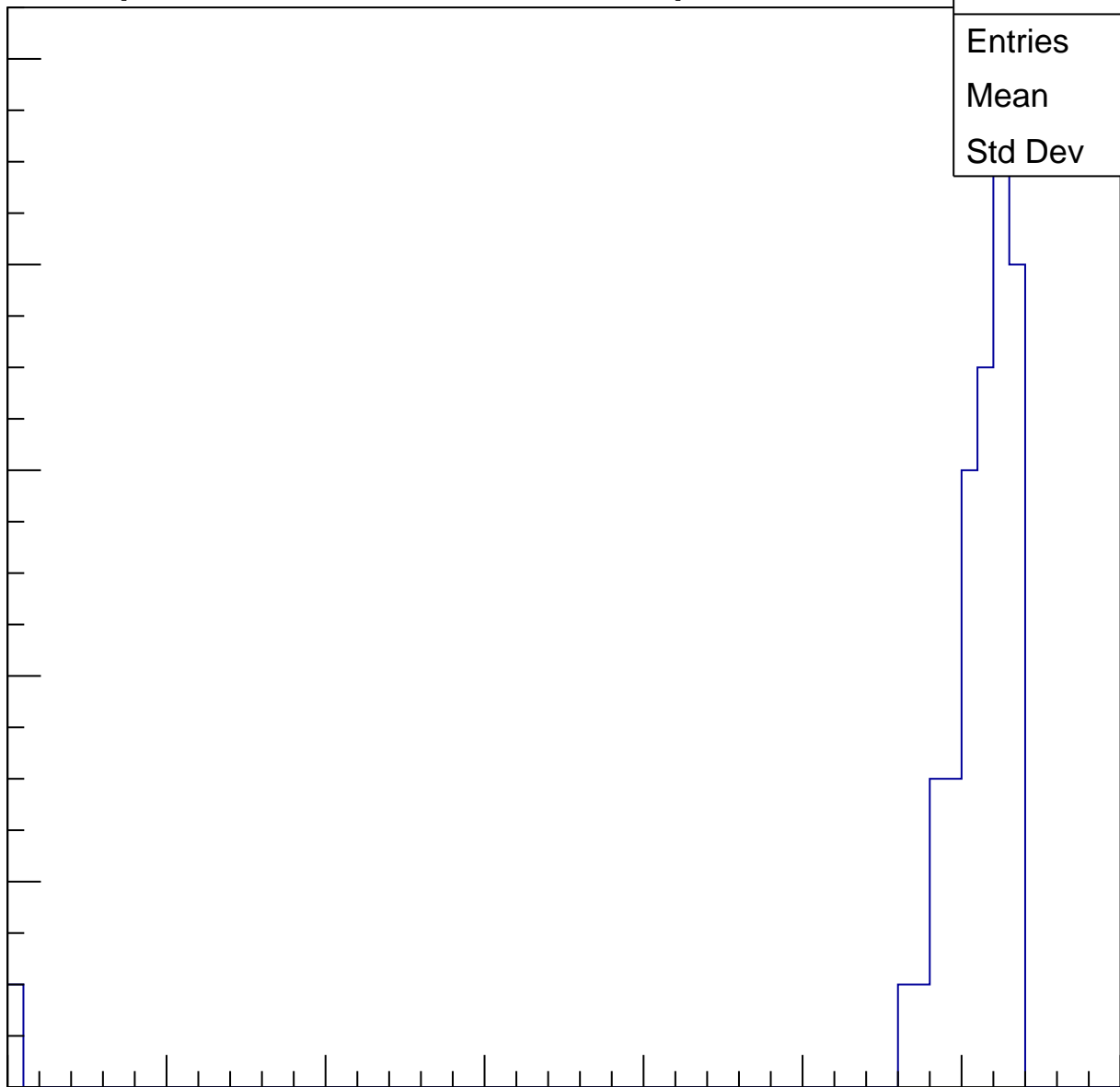
40

50

60

70

ampl



B0L002S, U5-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch102, adc0

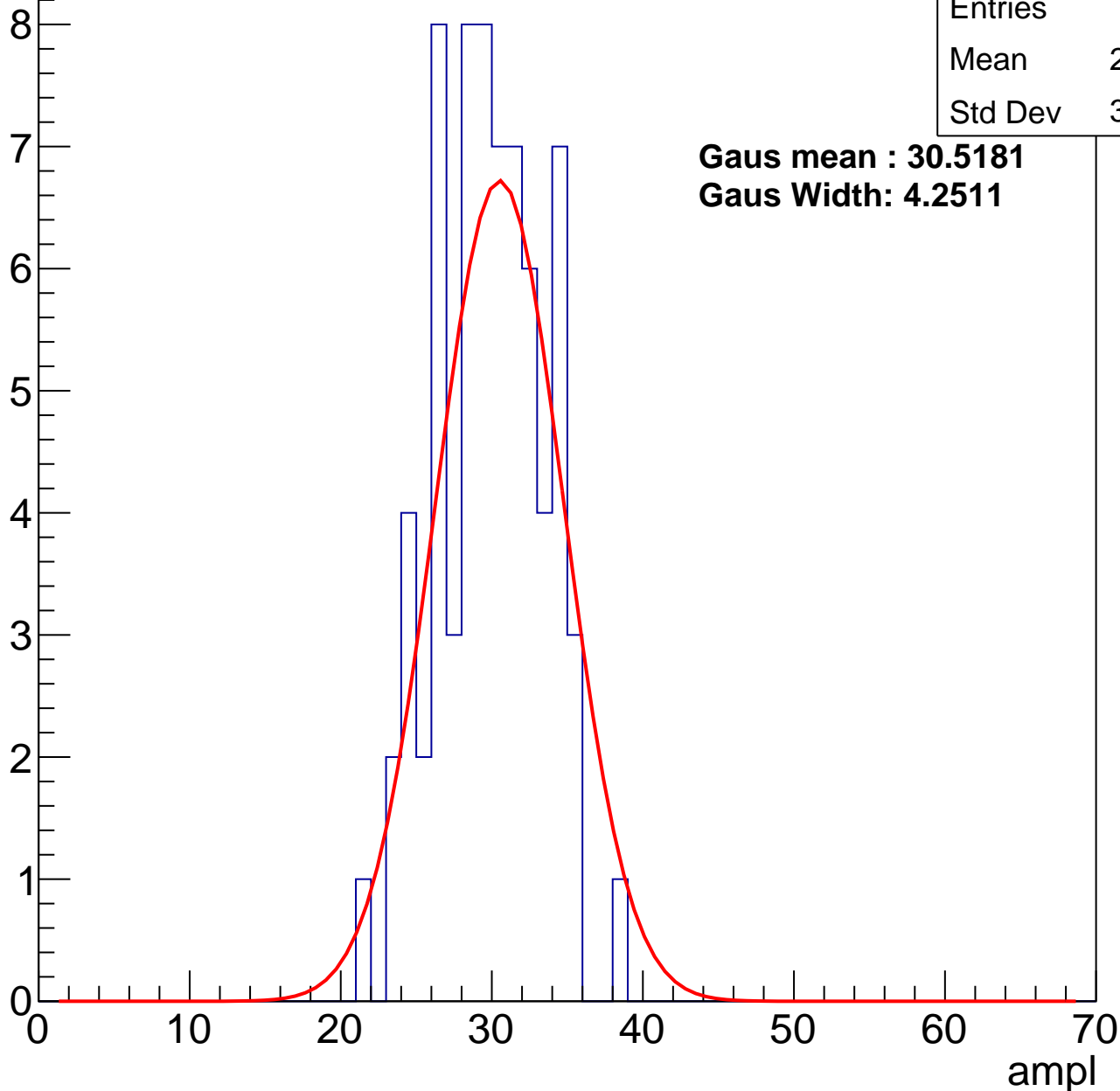
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.44
Std Dev	3.487

Gaus mean : 30.5181

Gaus Width: 4.2511



B0L002S, U5-ch102, adc1

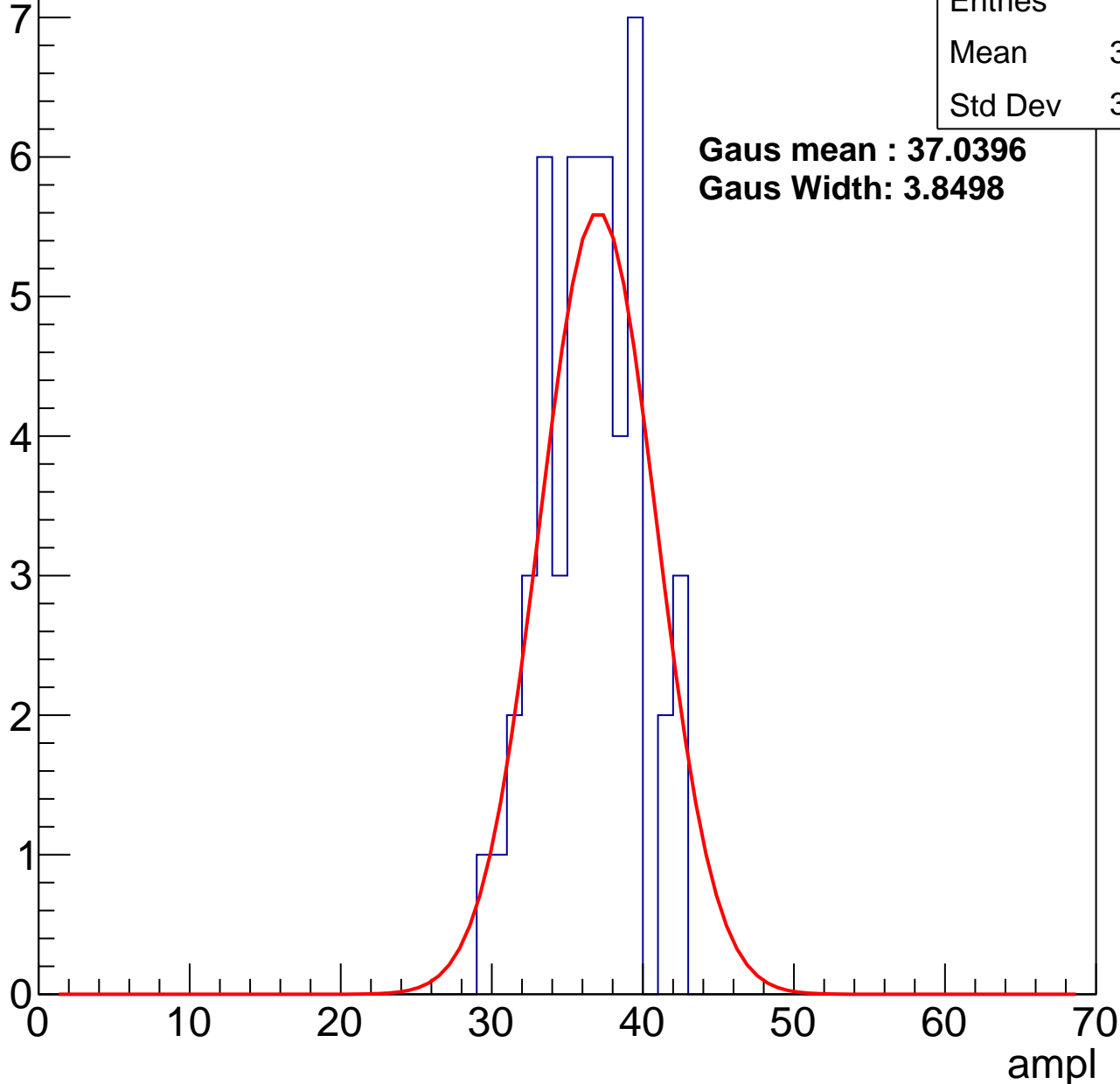
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	35.96
Std Dev	3.156

Gaus mean : 37.0396

Gaus Width: 3.8498



B0L002S, U5-ch102, adc2

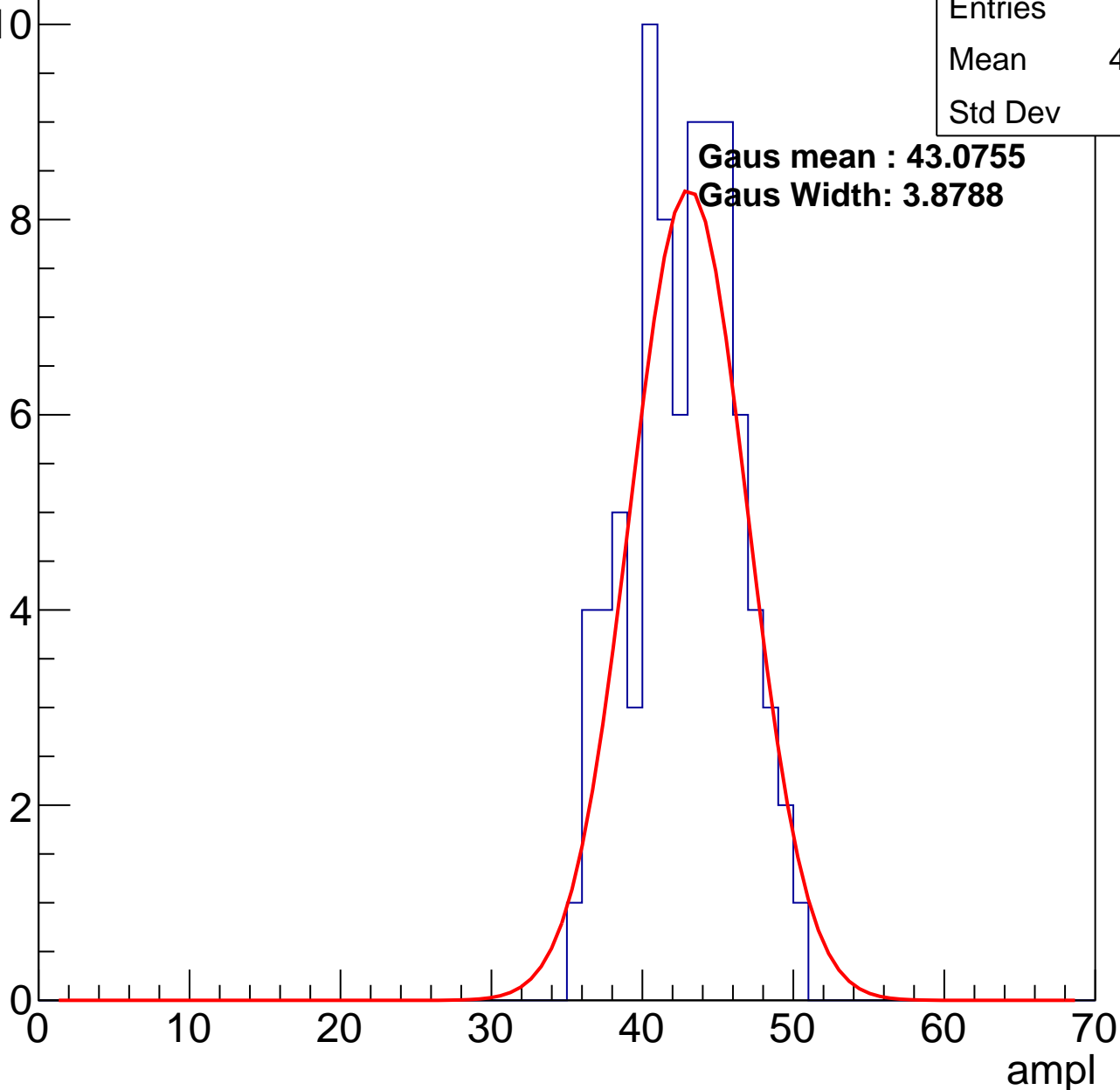
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	42.36
Std Dev	3.5

Gaus mean : 43.0755

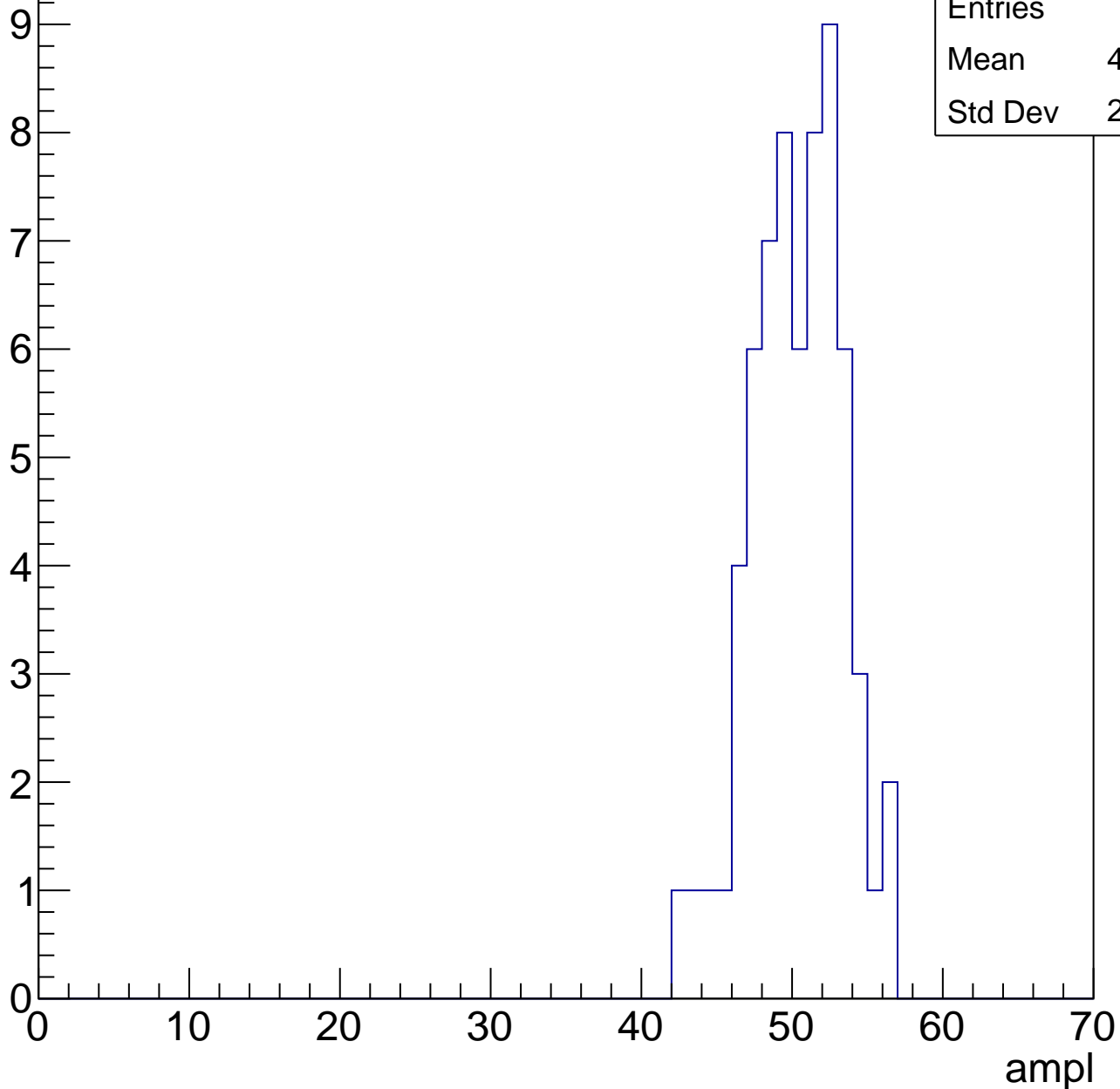
Gaus Width: 3.8788



B0L002S, U5-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



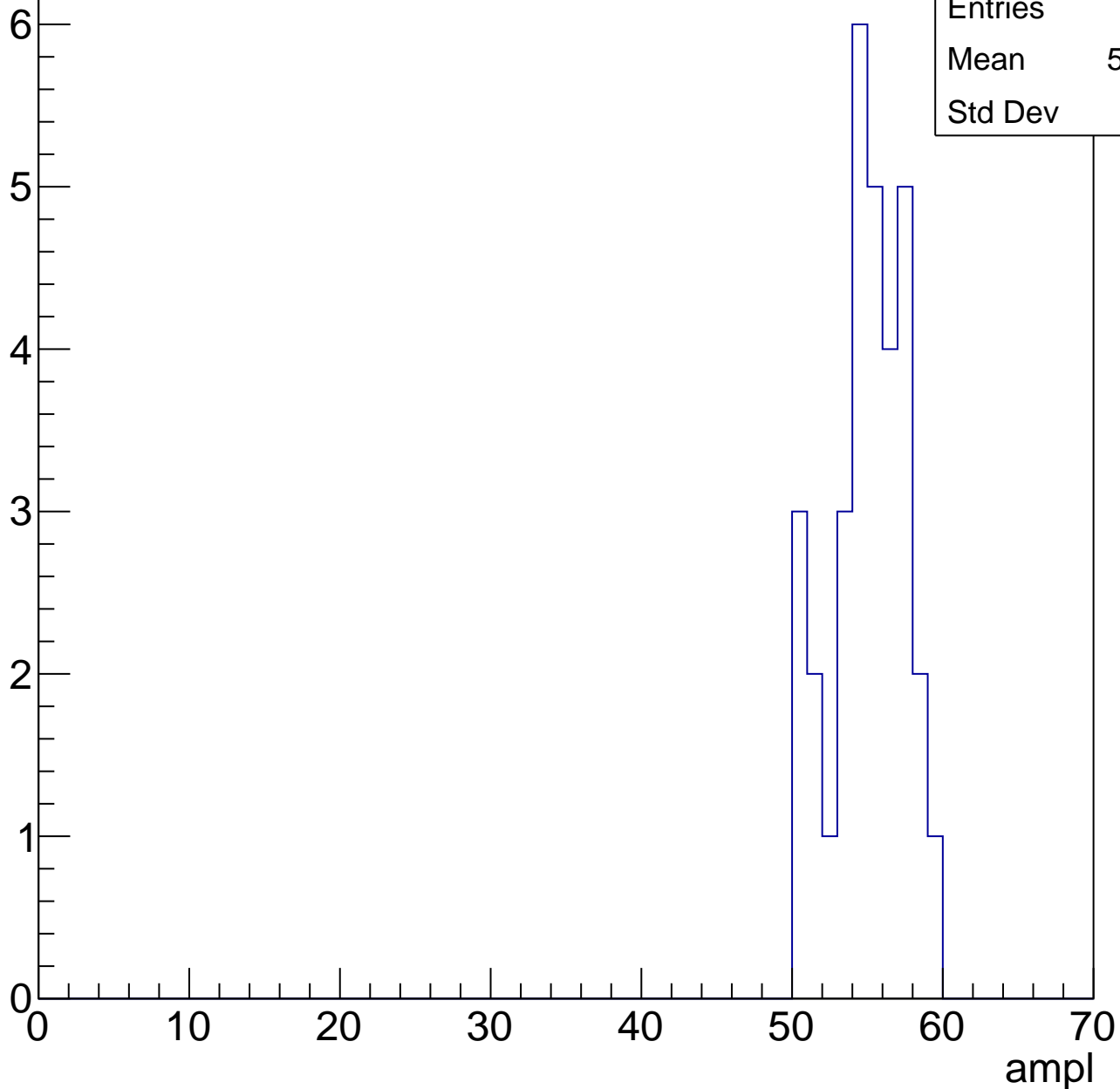
Entries	64
Mean	49.86
Std Dev	2.984

B0L002S, U5-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	54.56
Std Dev	2.41

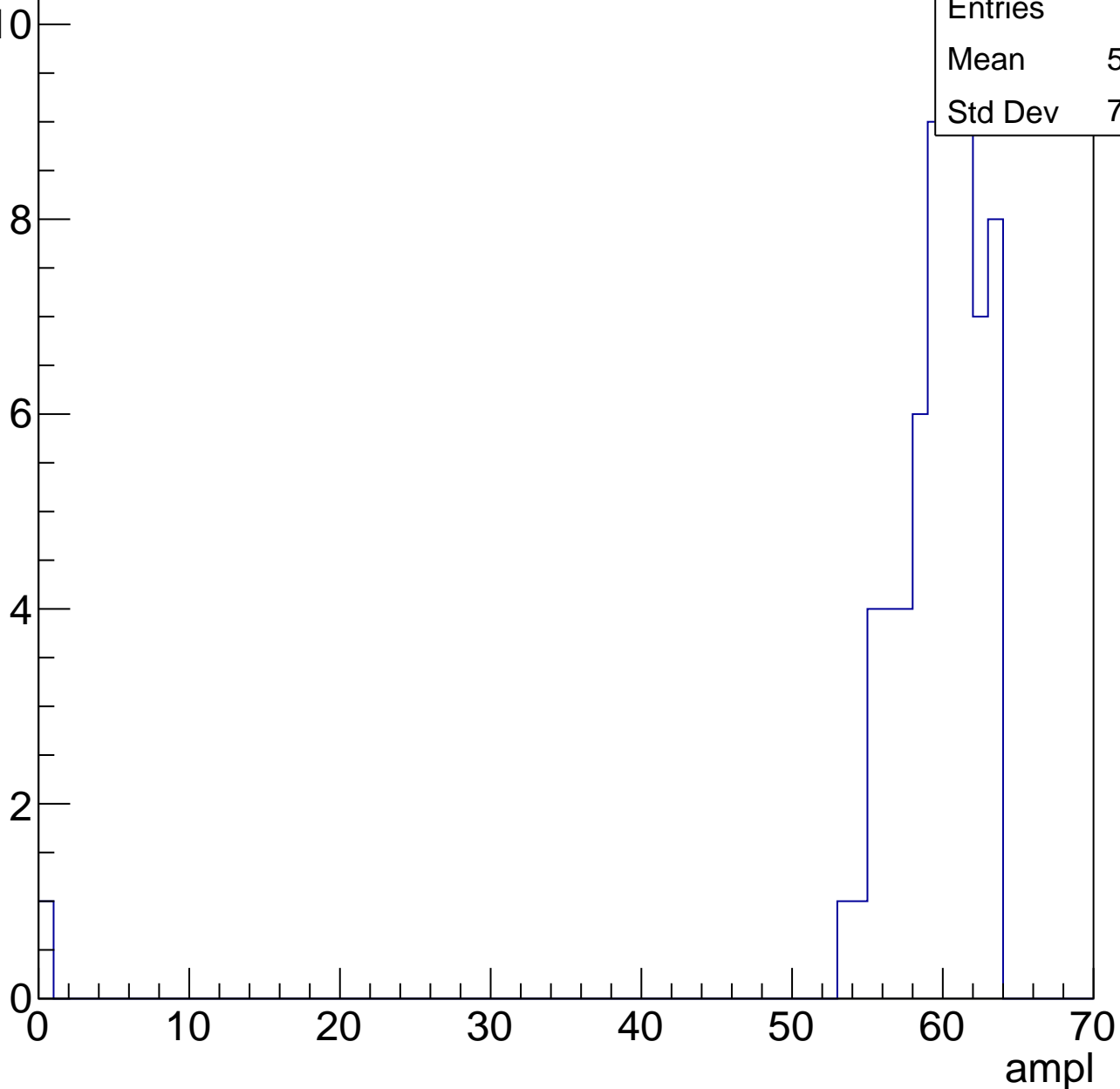


B0L002S, U5-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

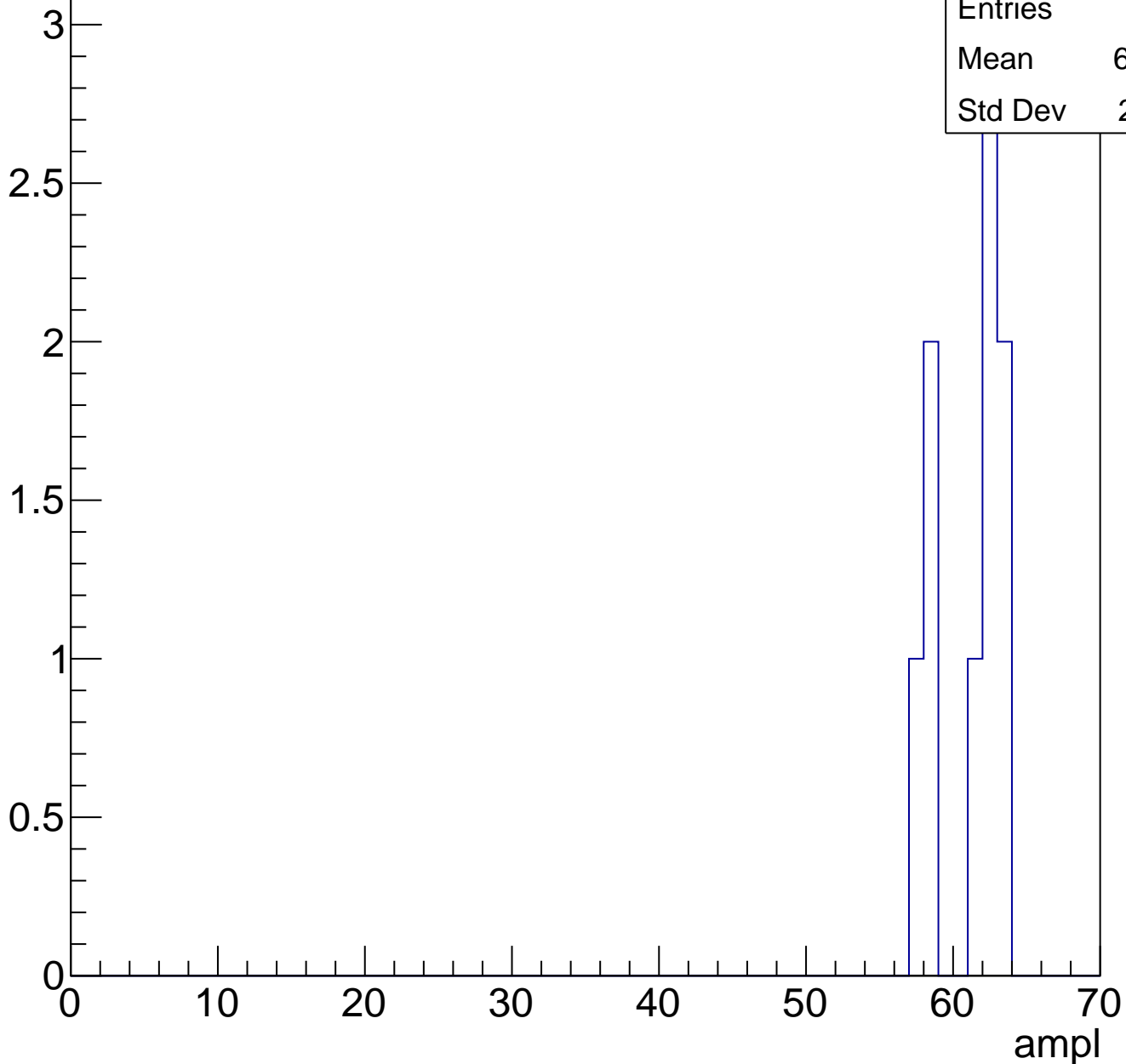
Entries	64
Mean	58.52
Std Dev	7.792



B0L002S, U5-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch103, adc0

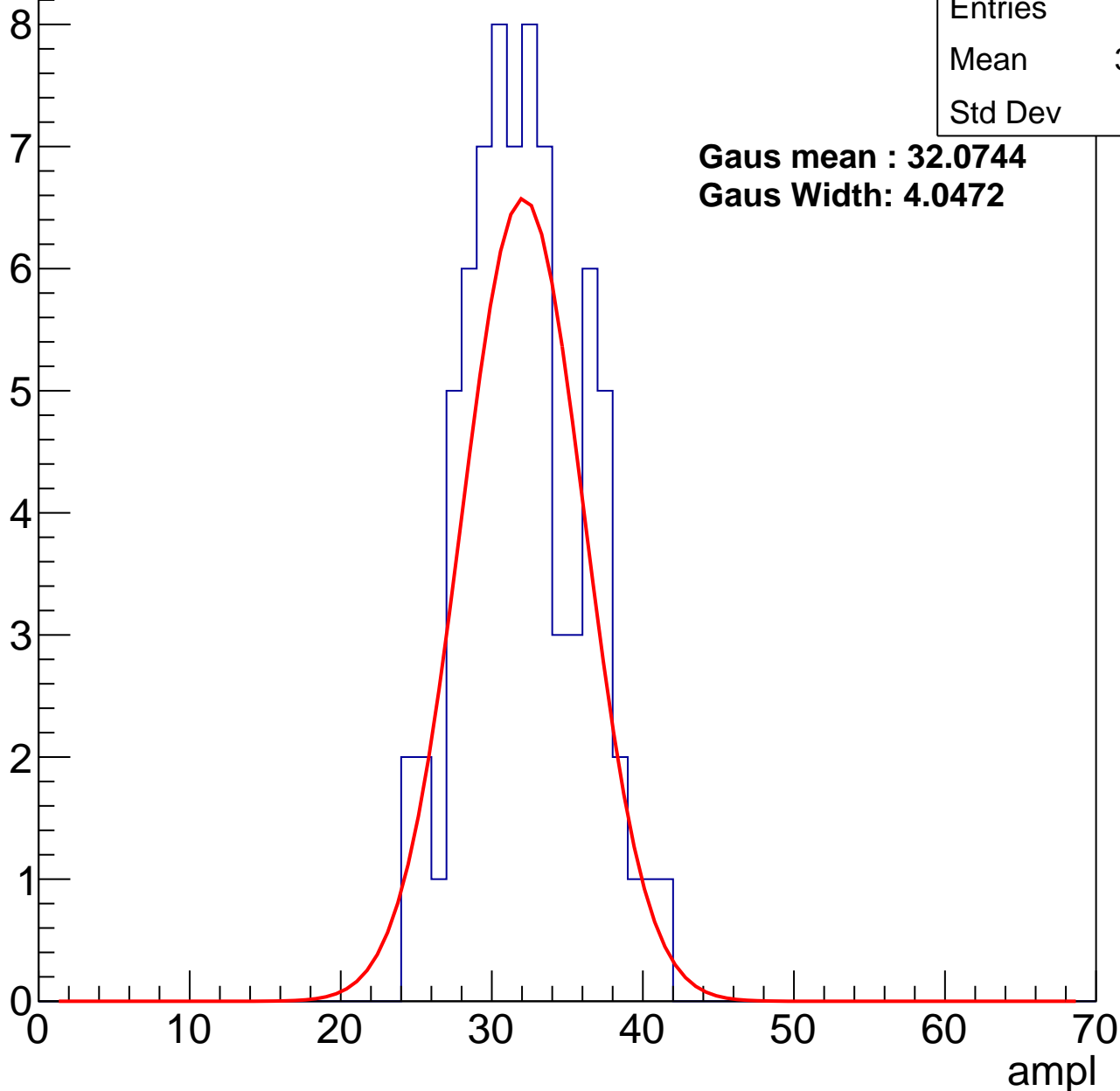
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.71
Std Dev	3.85

Gaus mean : 32.0744

Gaus Width: 4.0472



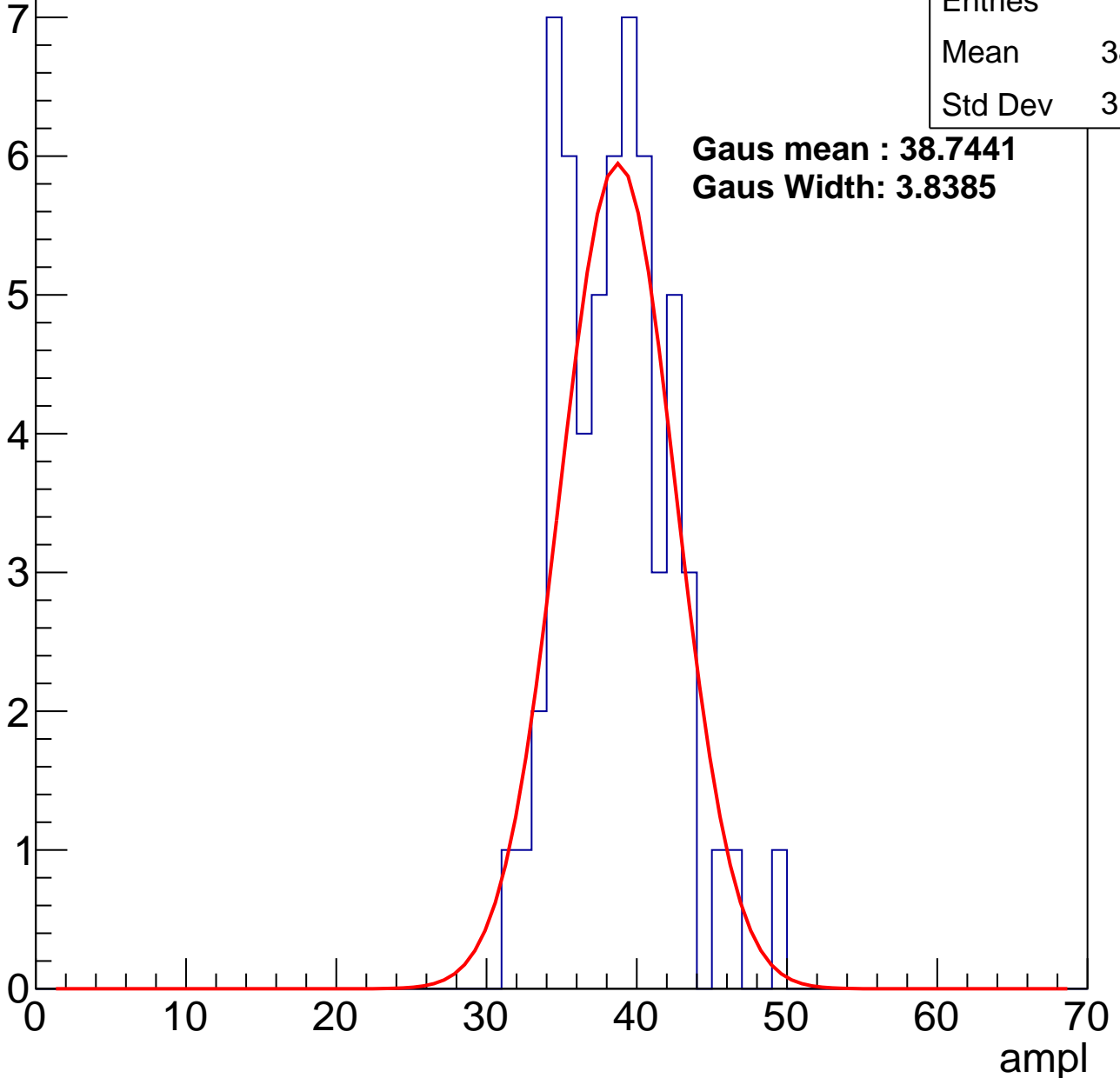
B0L002S, U5-ch103, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38.12
Std Dev	3.608

Gaus mean : 38.7441
Gaus Width: 3.8385



B0L002S, U5-ch103, adc2

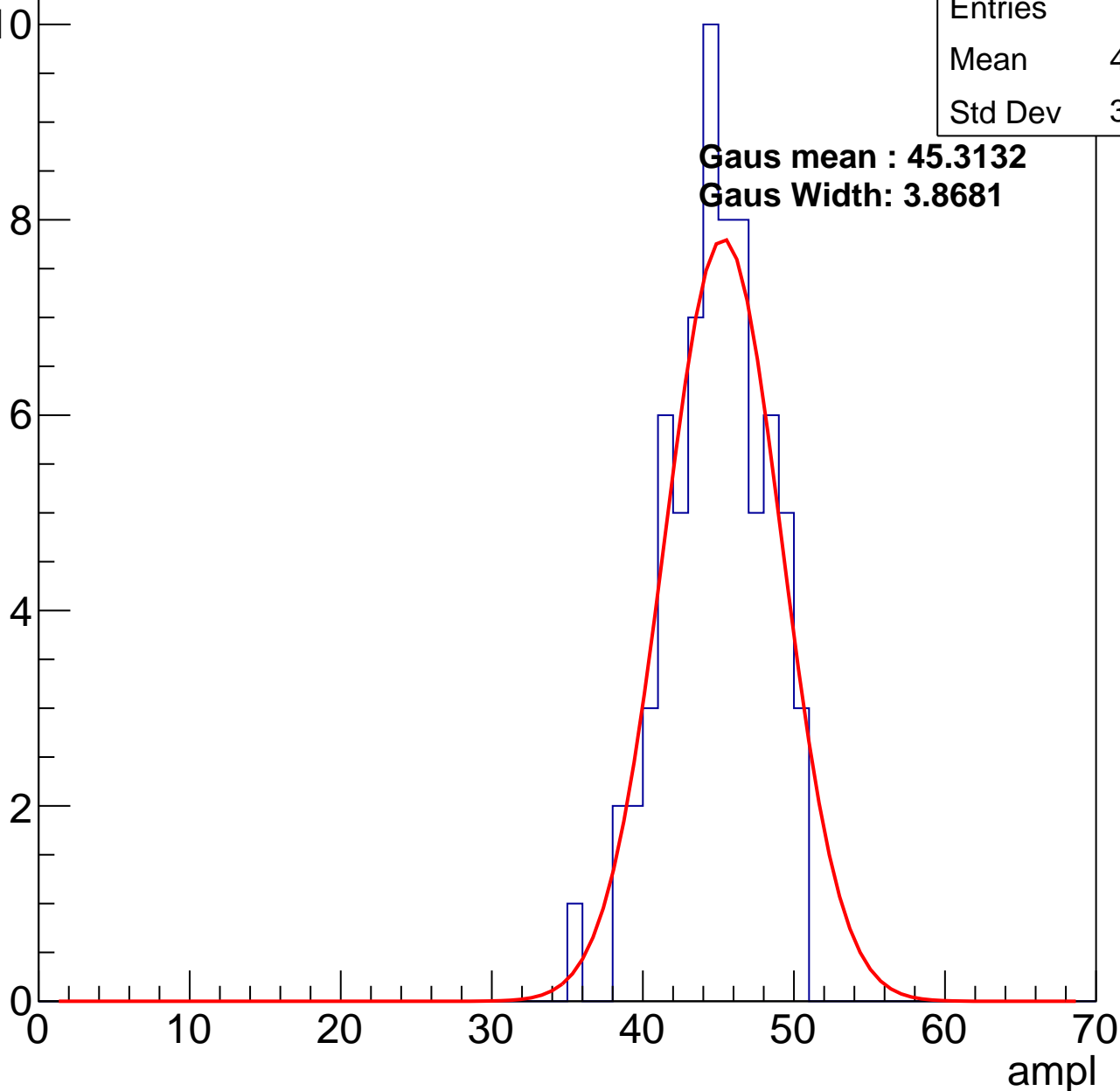
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.39
Std Dev	3.217

Gaus mean : 45.3132

Gaus Width: 3.8681

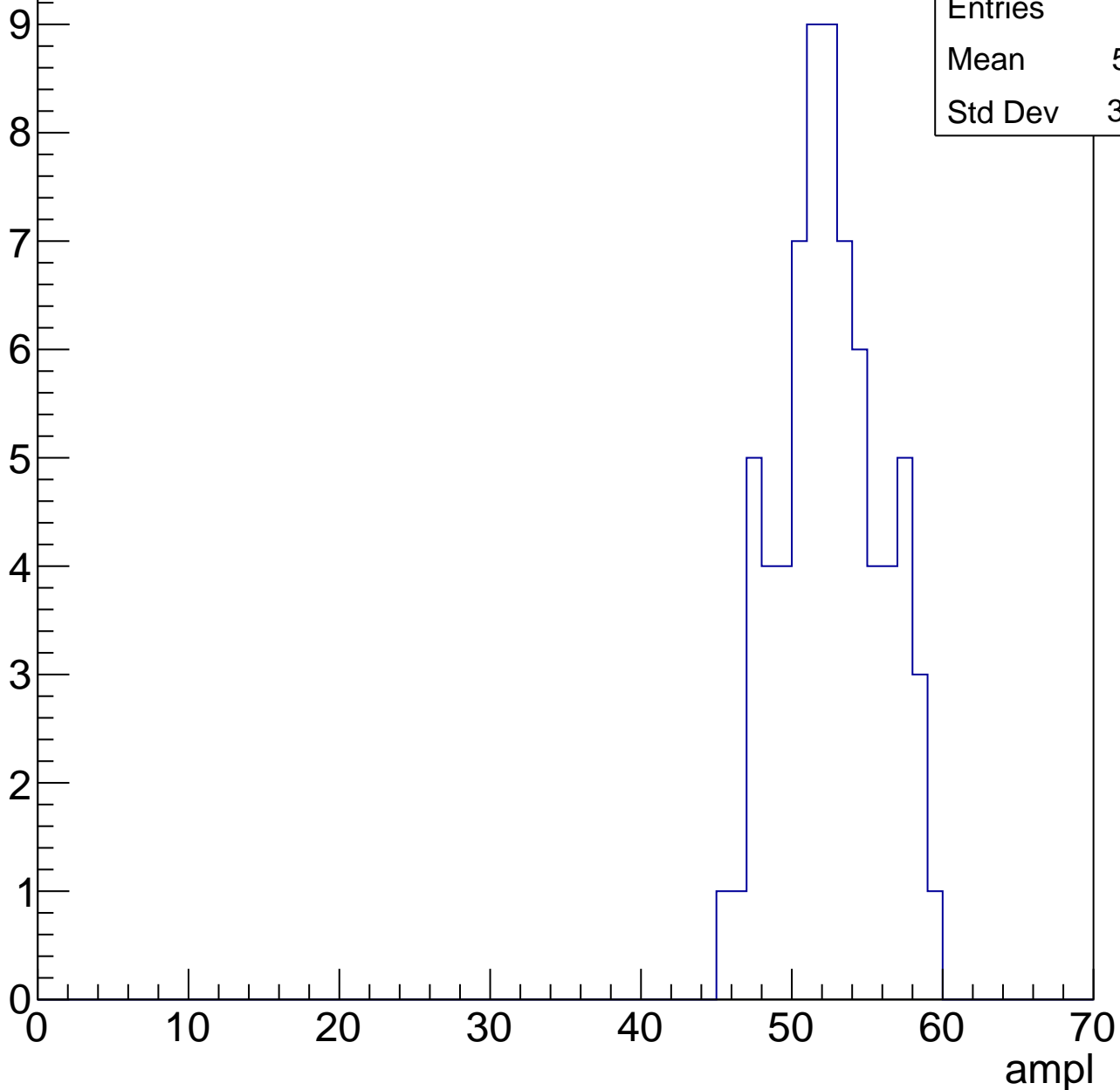


B0L002S, U5-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

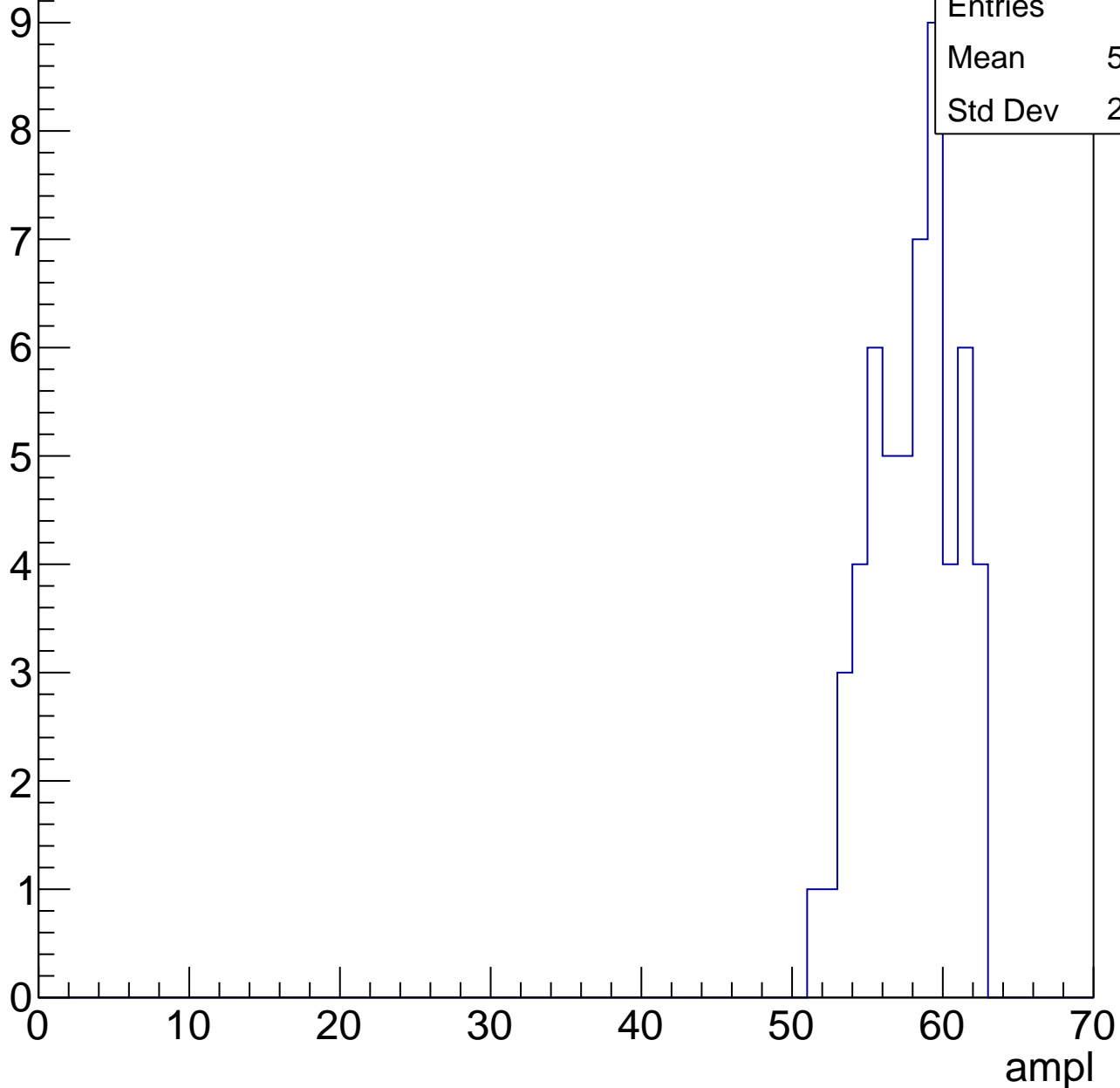
Entries	70
Mean	52.11
Std Dev	3.293



B0L002S, U5-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



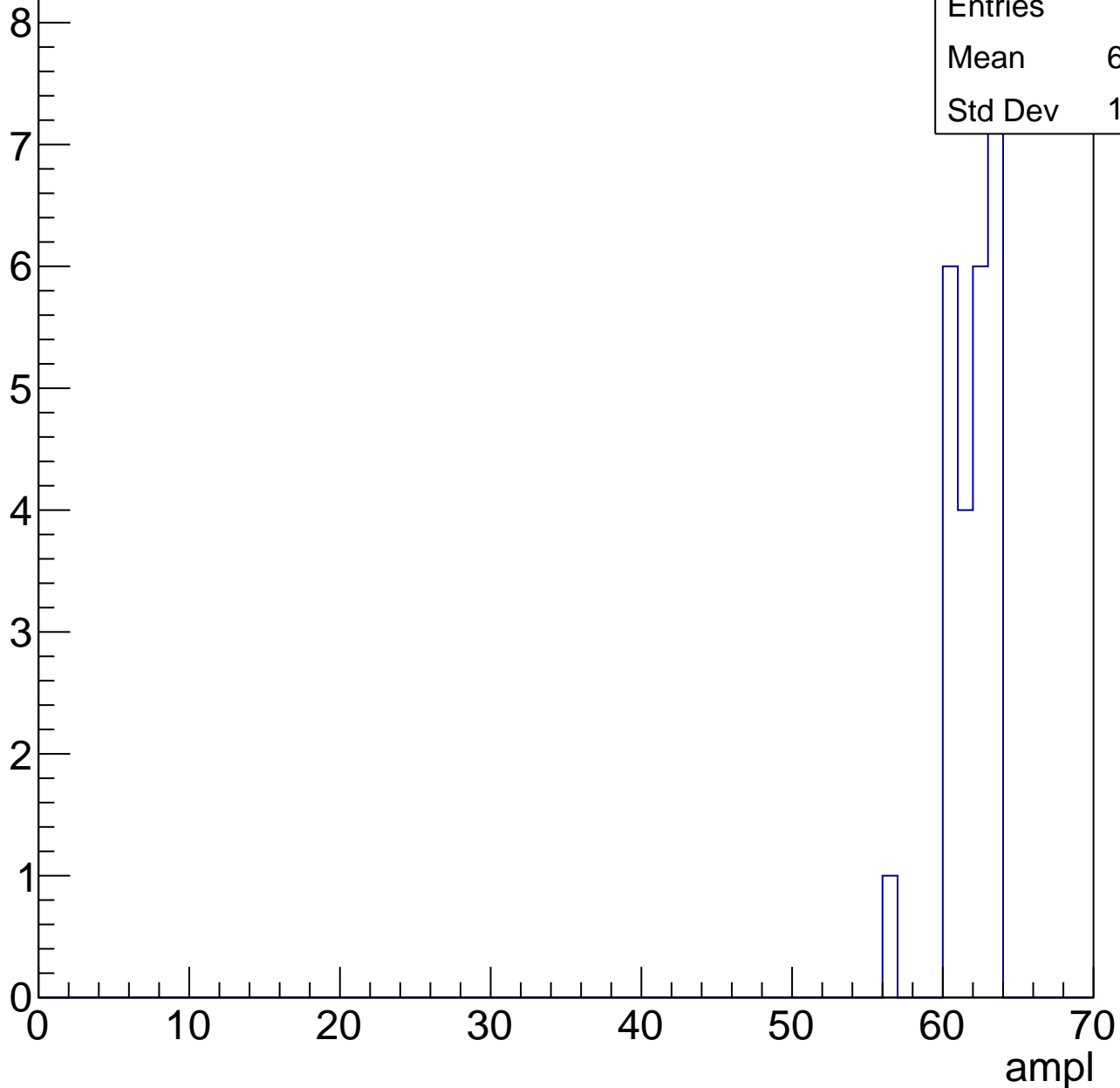
Entries	55
Mean	57.53
Std Dev	2.808

B0L002S, U5-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	61.44
Std Dev	1.602



B0L002S, U5-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch104, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	57
Mean	28.49
Std Dev	3.118

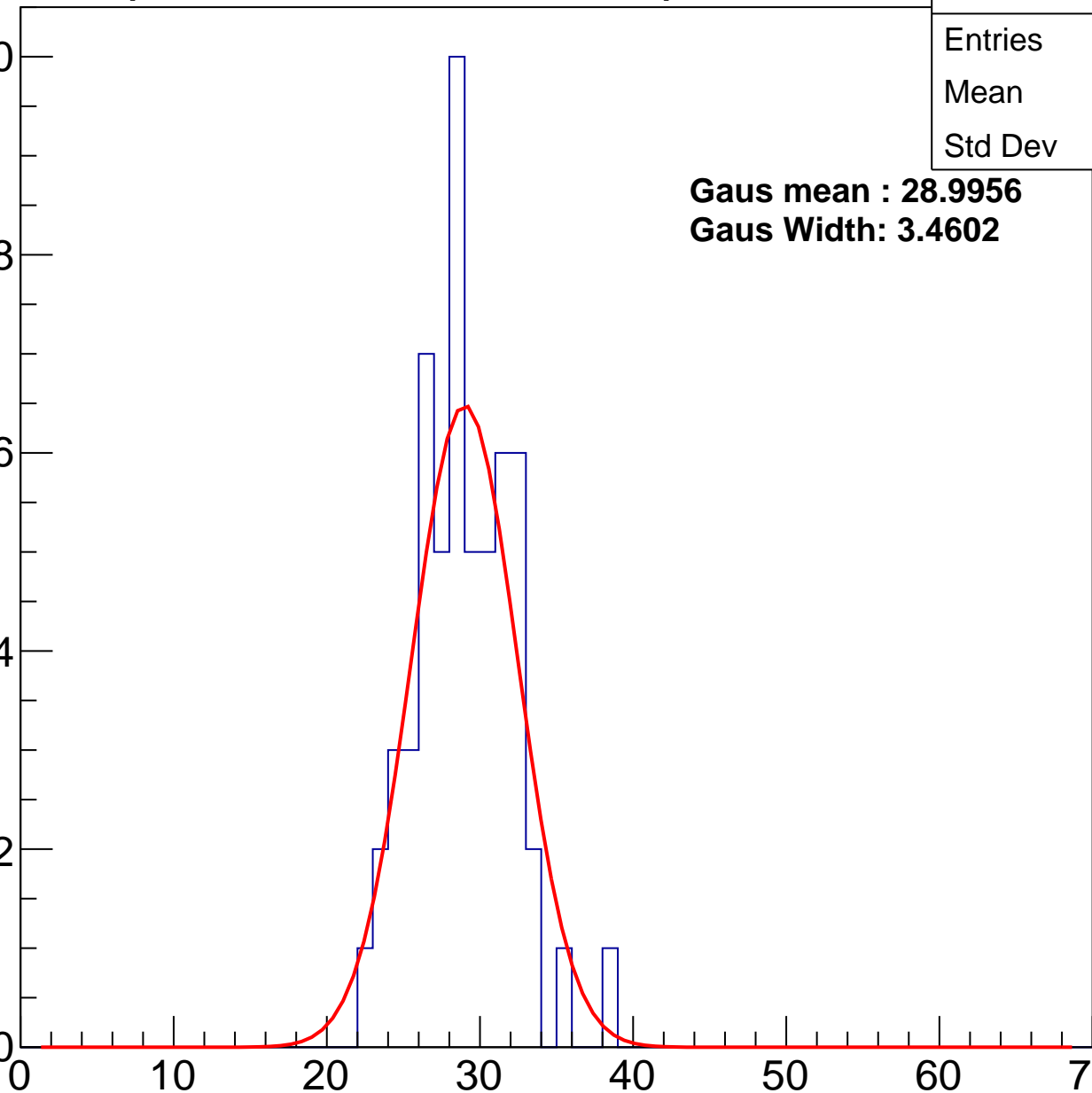
Gaus mean : 28.9956

Gaus Width: 3.4602

Entry

10
8
6
4
2
0

ampl



B0L002S, U5-ch104, adc1

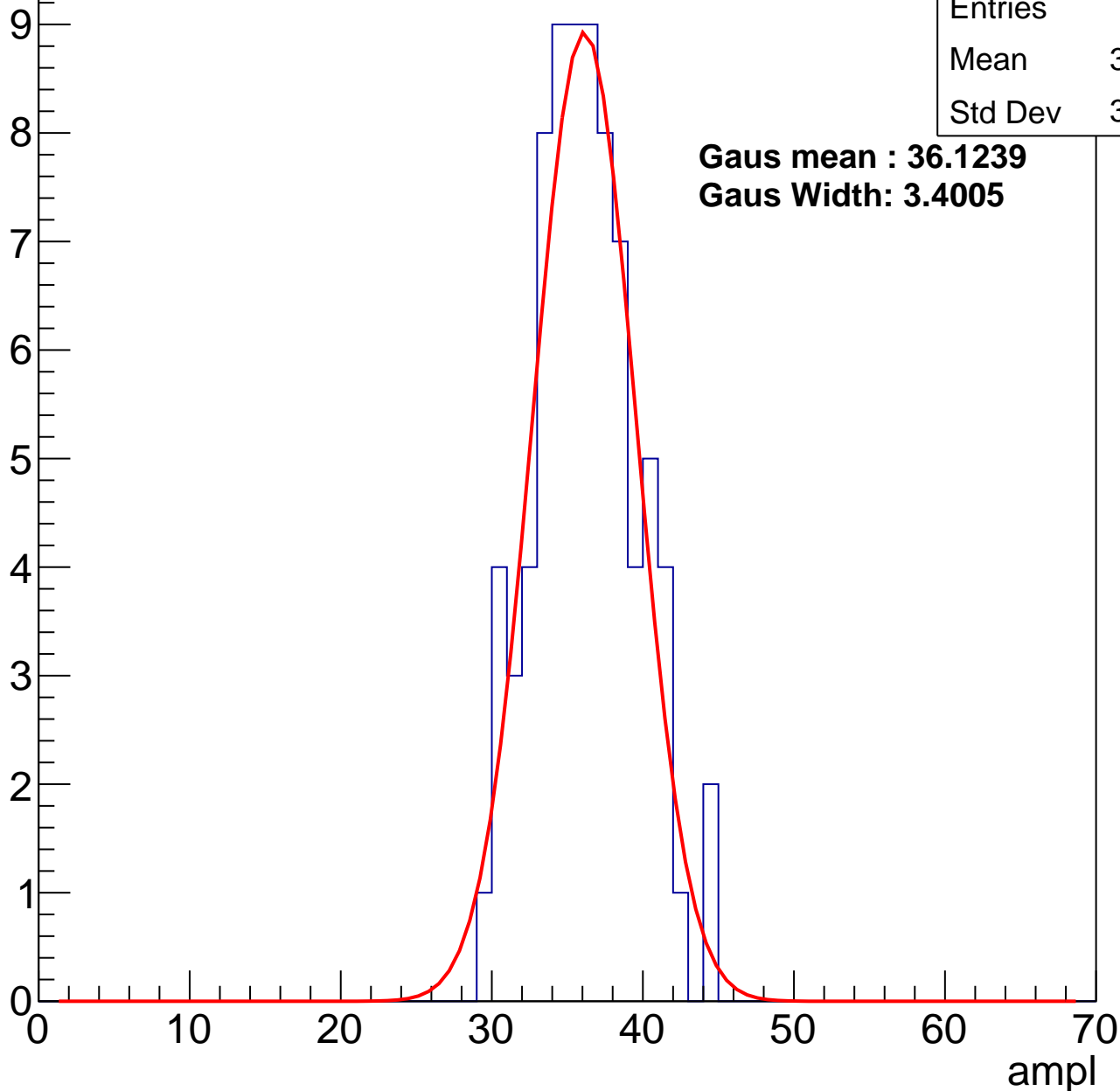
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	35.78
Std Dev	3.323

Gaus mean : 36.1239

Gaus Width: 3.4005

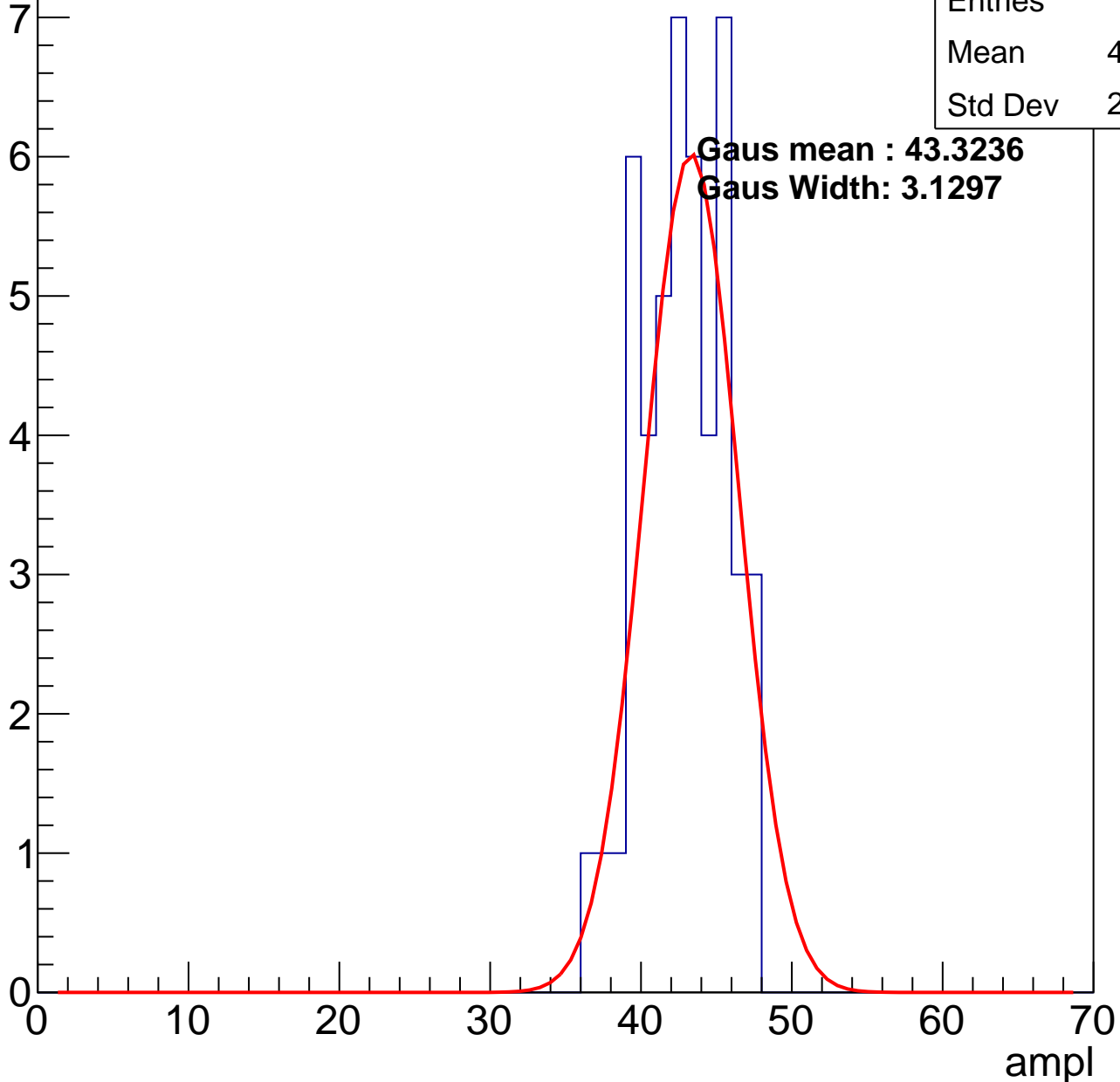


B0L002S, U5-ch104, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	42.33
Std Dev	2.718

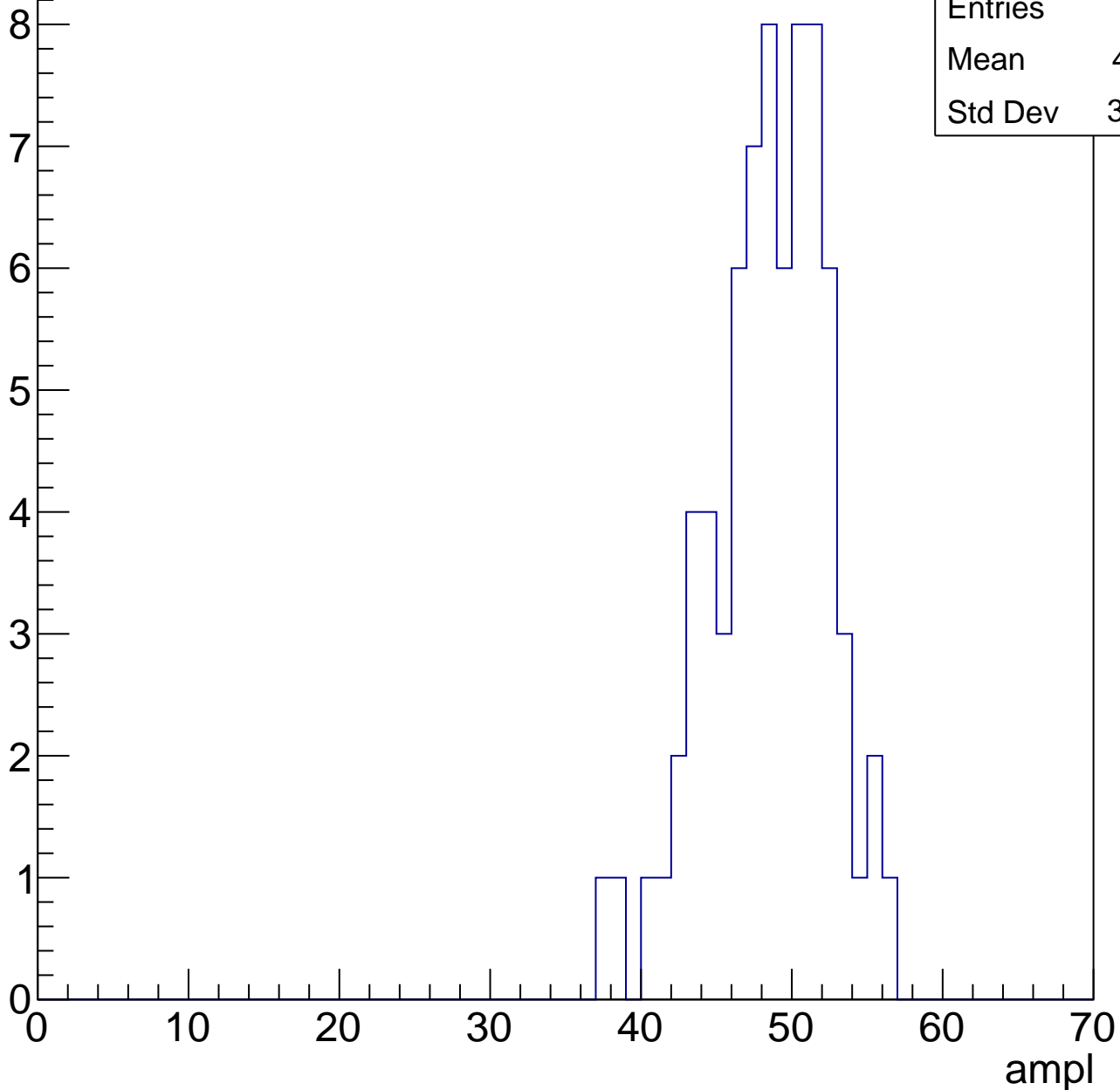


B0L002S, U5-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	48.01
Std Dev	3.894

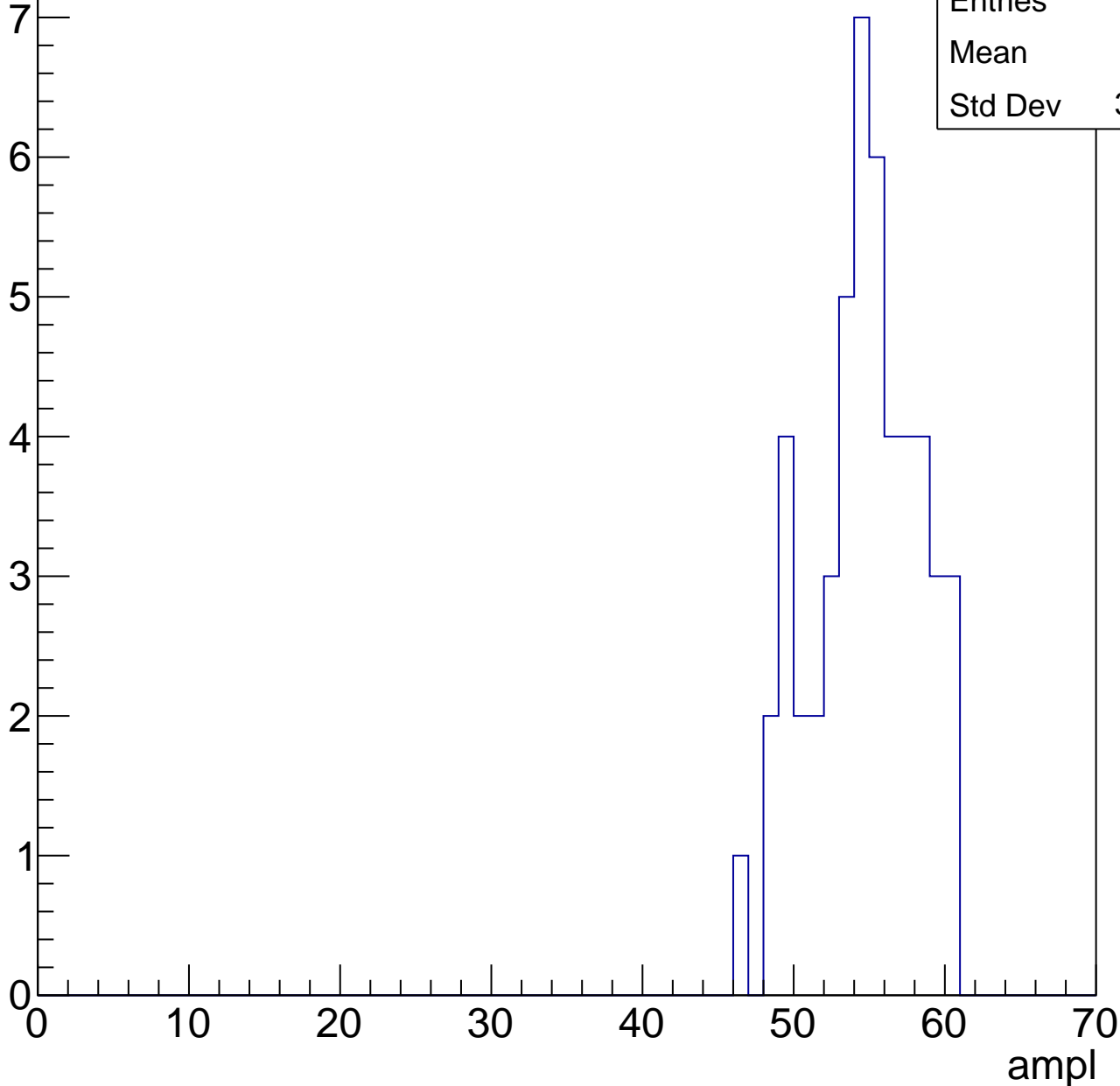


B0L002S, U5-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	54.2
Std Dev	3.481



B0L002S, U5-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 57.75

Std Dev 8.529

0

10

20

30

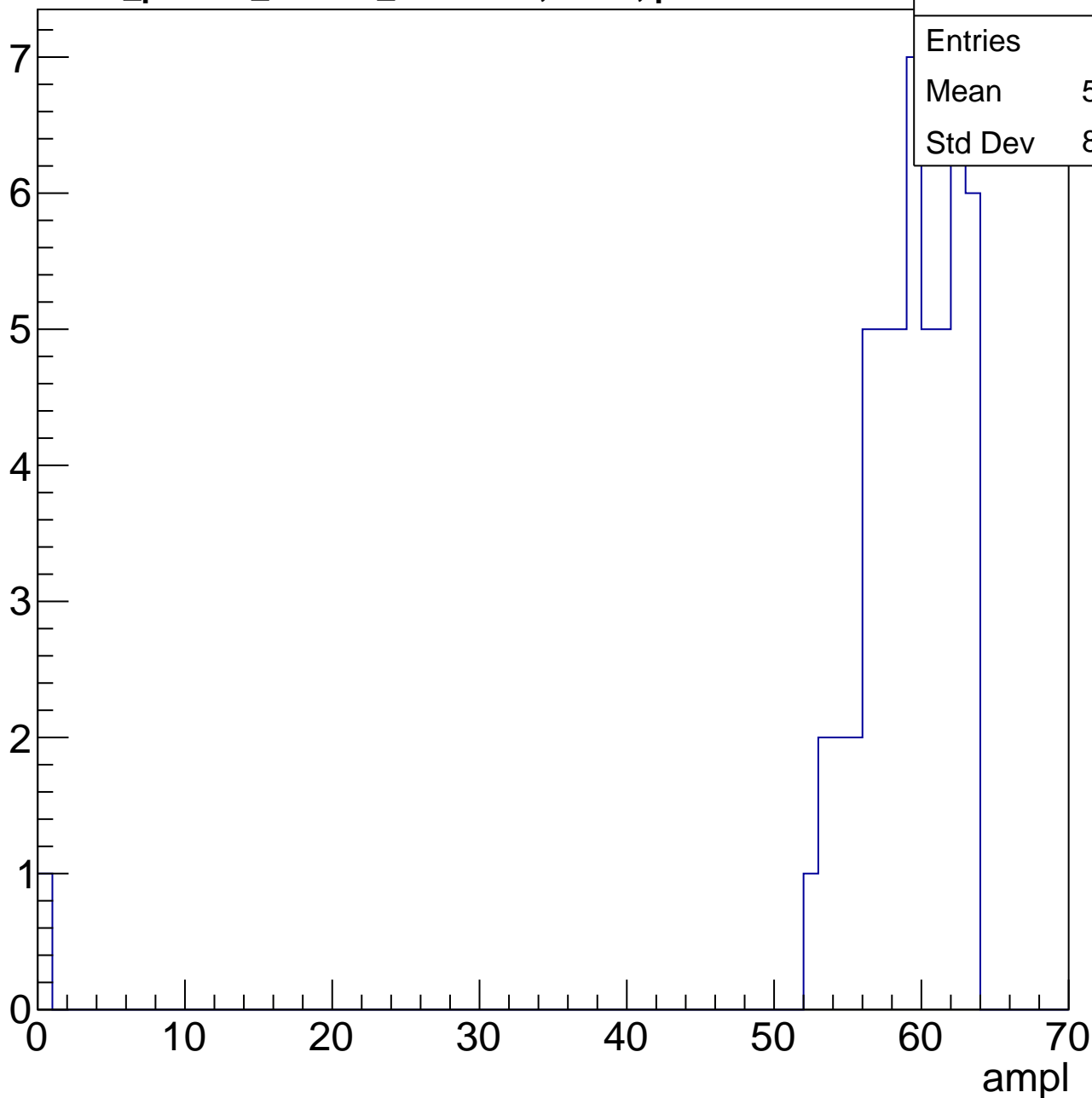
40

50

60

70

ampl

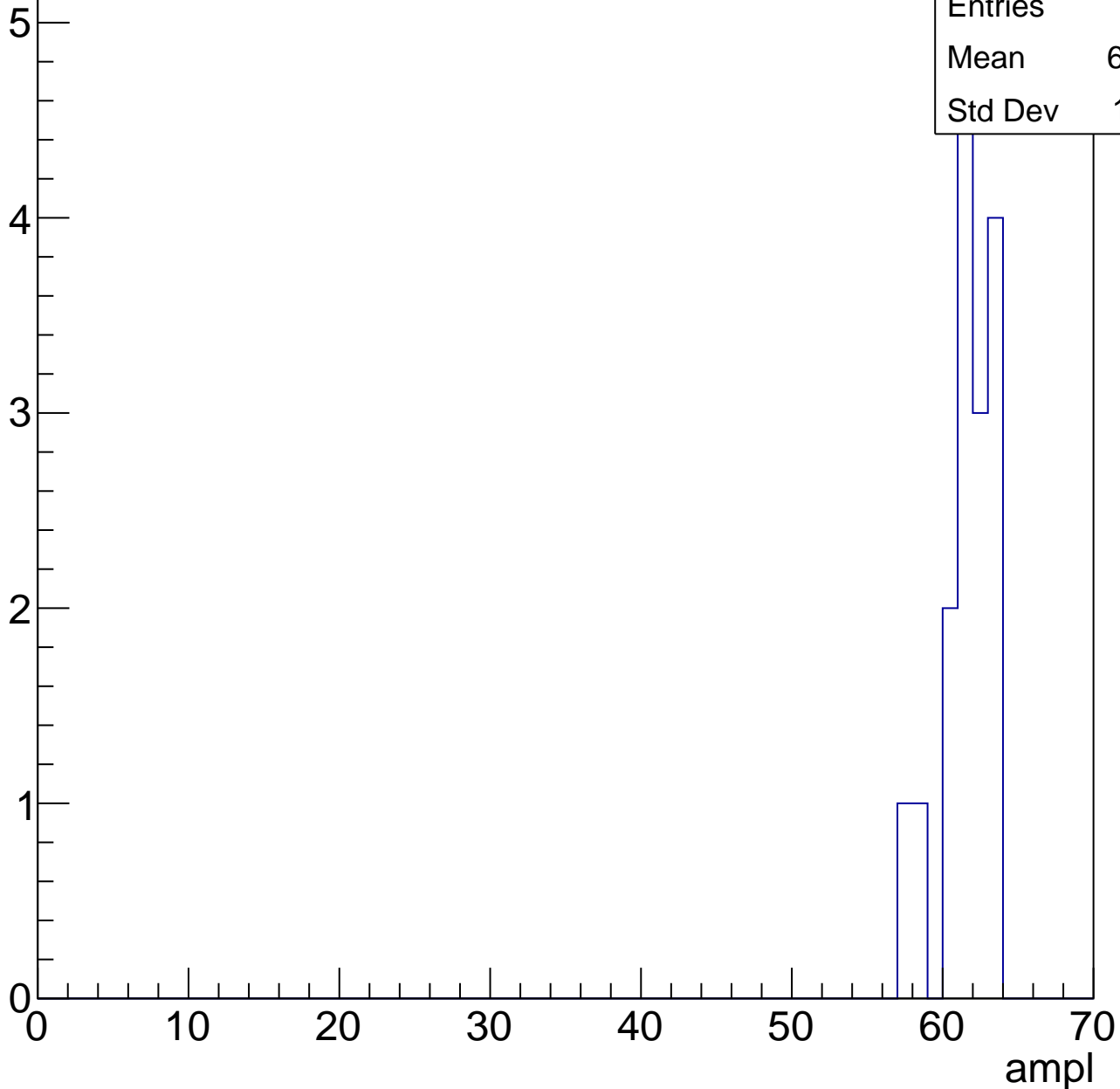


B0L002S, U5-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	16
Mean	61.12
Std Dev	1.691



B0L002S, U5-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch105, adc0

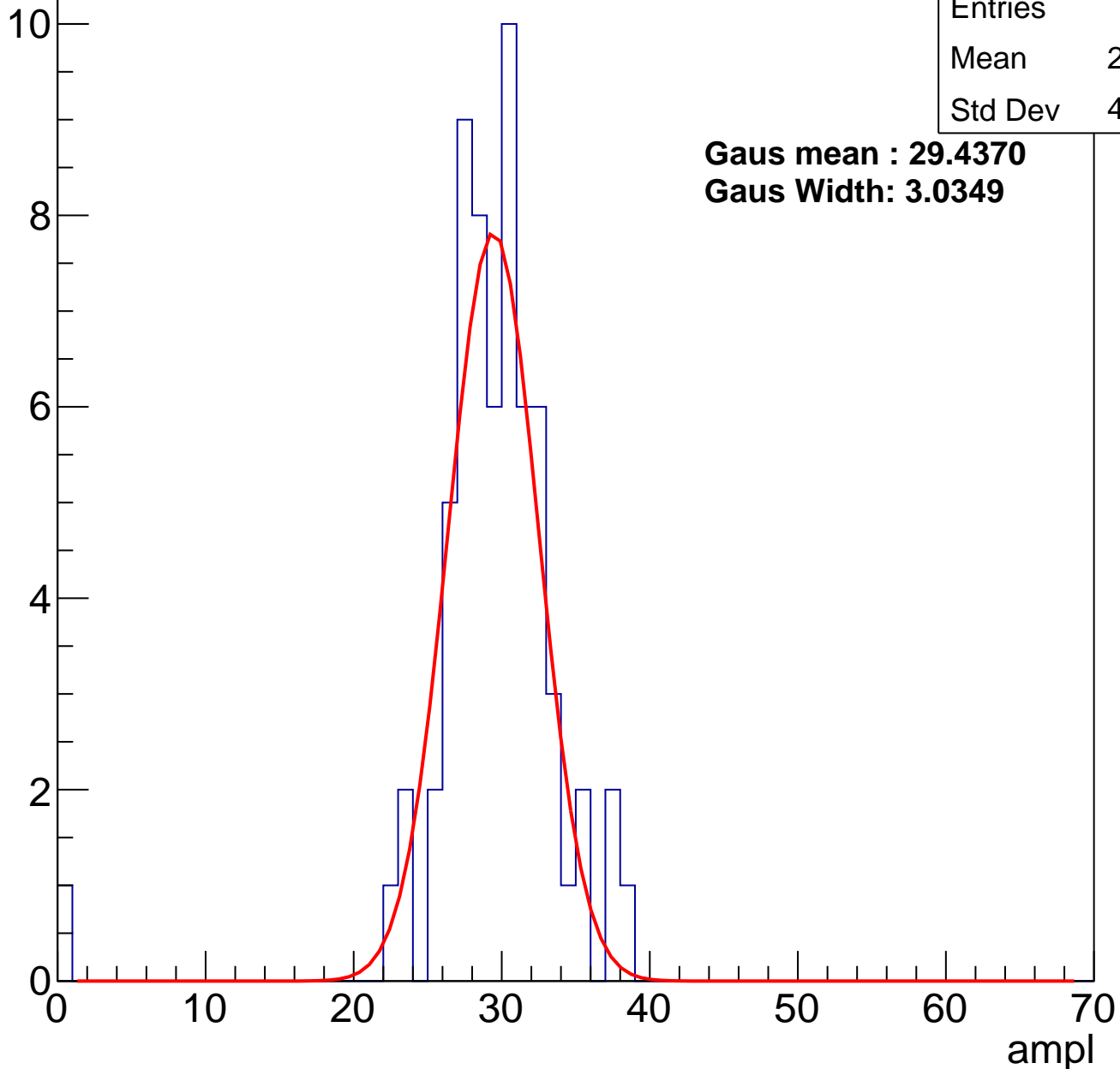
calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	28.95
Std Dev	4.843

Gaus mean : 29.4370

Gaus Width: 3.0349

Entry

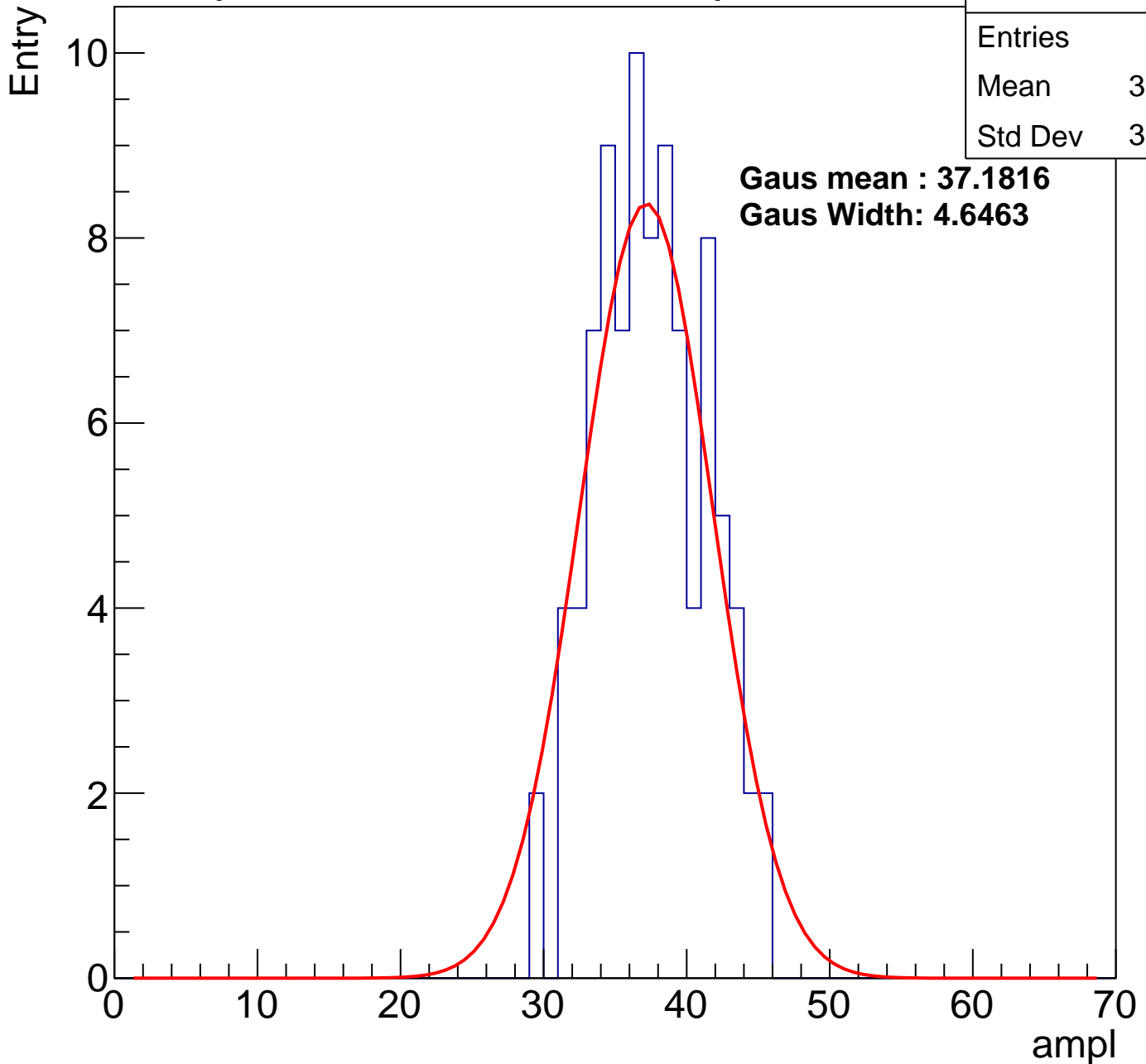


B0L002S, U5-ch105, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	92
Mean	37.08
Std Dev	3.757

Gaus mean : 37.1816
Gaus Width: 4.6463



B0L002S, U5-ch105, adc2

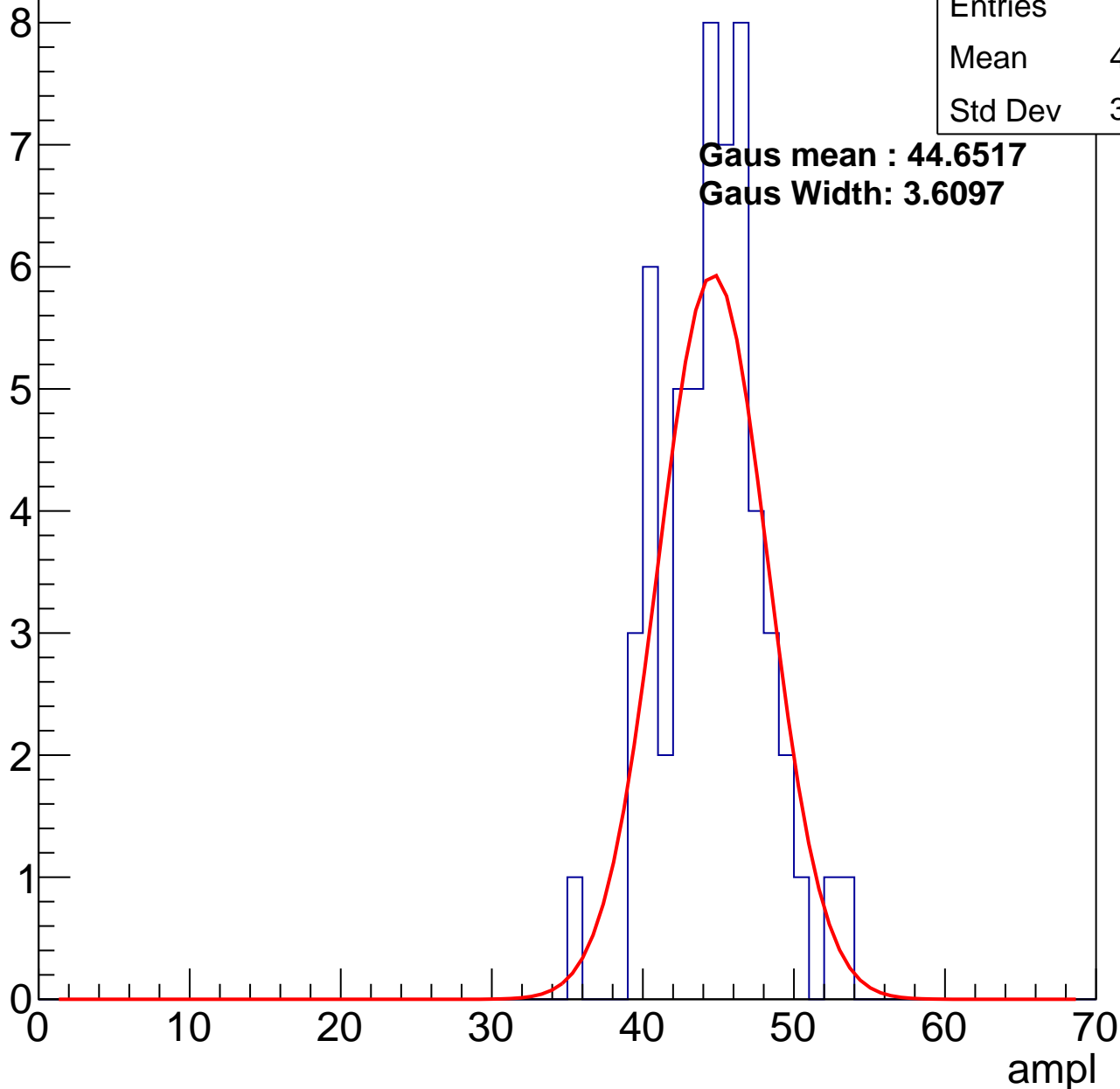
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.19
Std Dev	3.369

Gaus mean : 44.6517

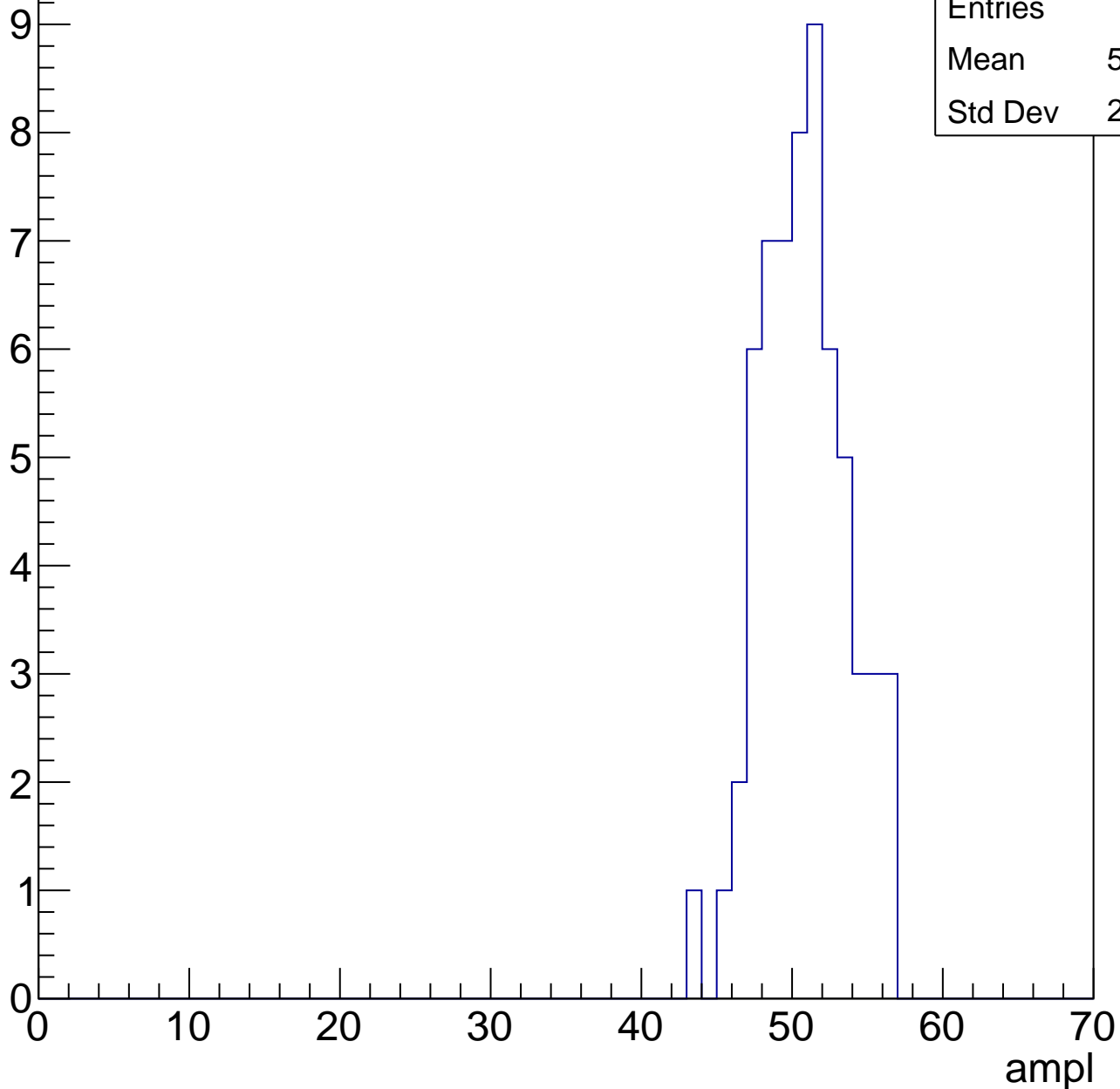
Gaus Width: 3.6097



B0L002S, U5-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



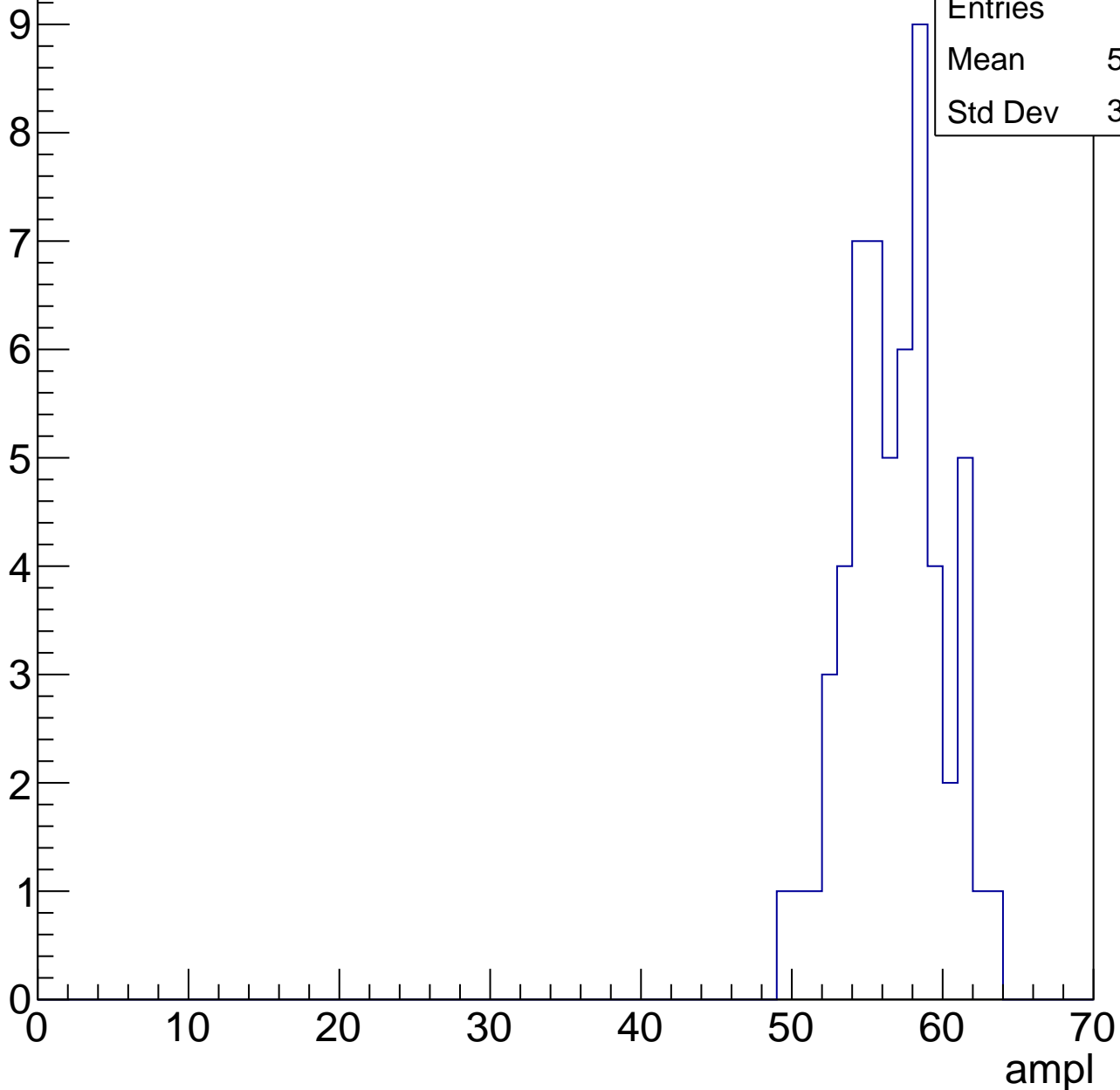
Entries	61
Mean	50.36
Std Dev	2.852

B0L002S, U5-ch105, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

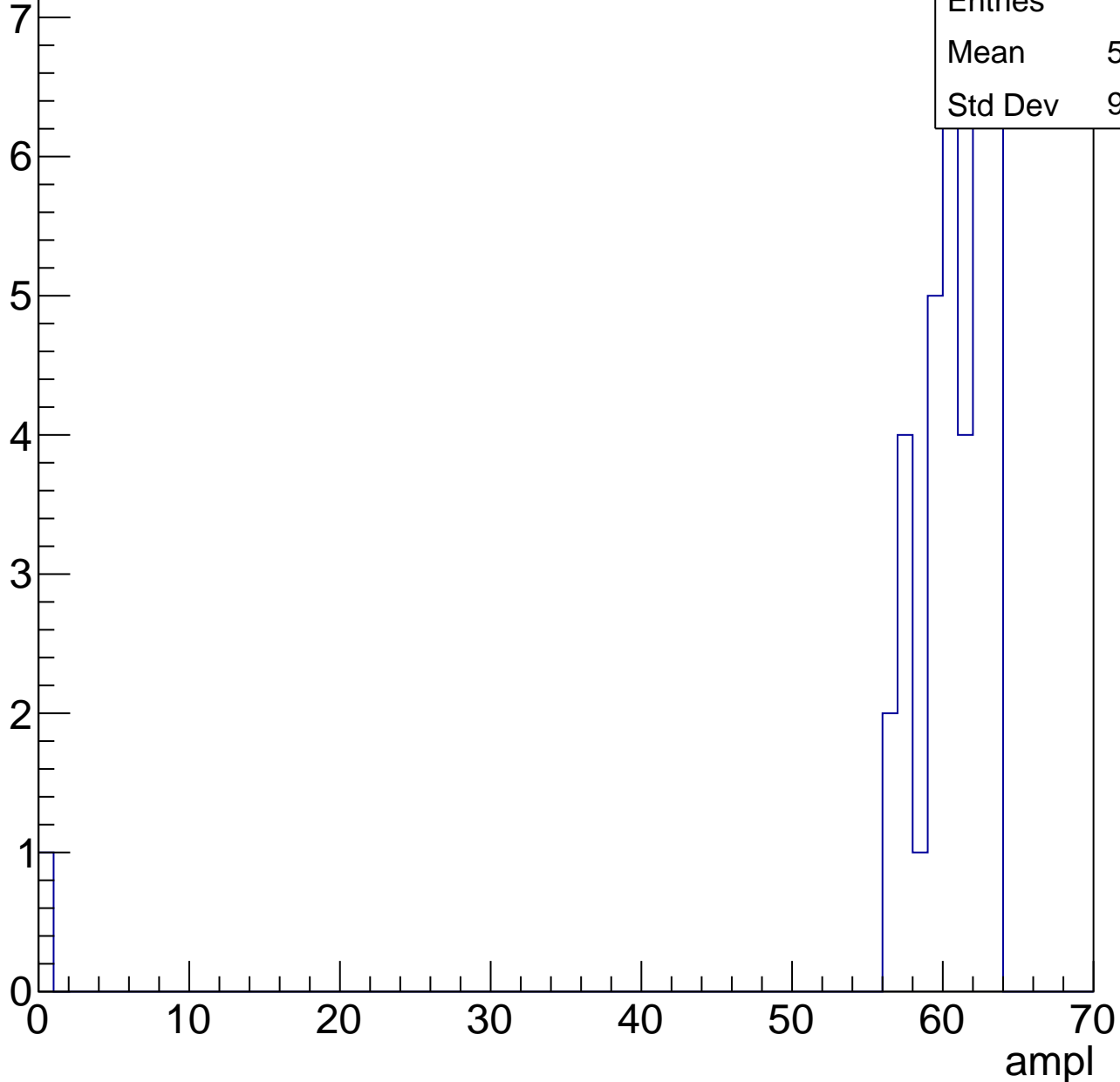
Entries	57
Mean	56.33
Std Dev	3.086



B0L002S, U5-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

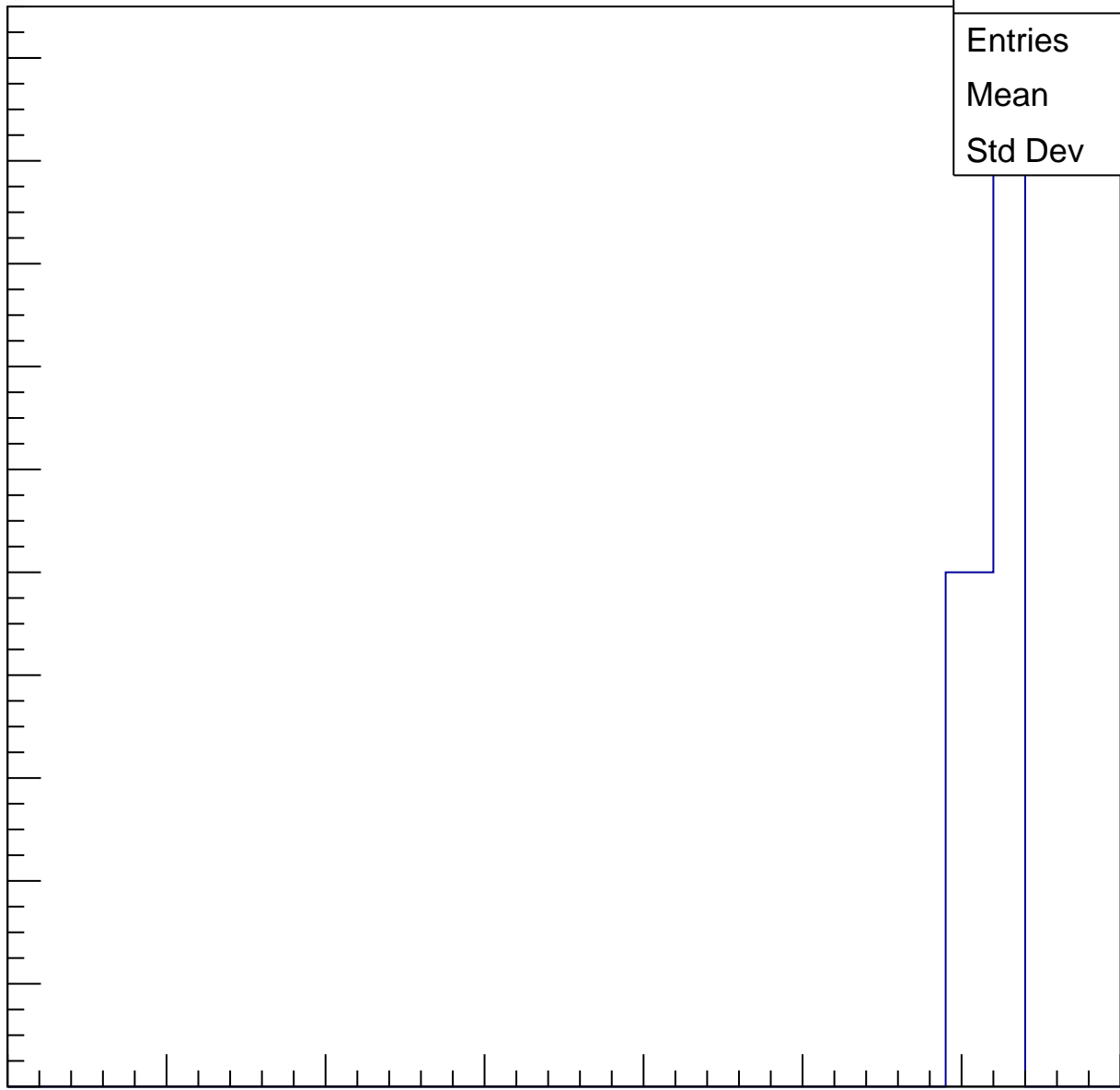
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.43
Std Dev	1.4

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch106, adc0

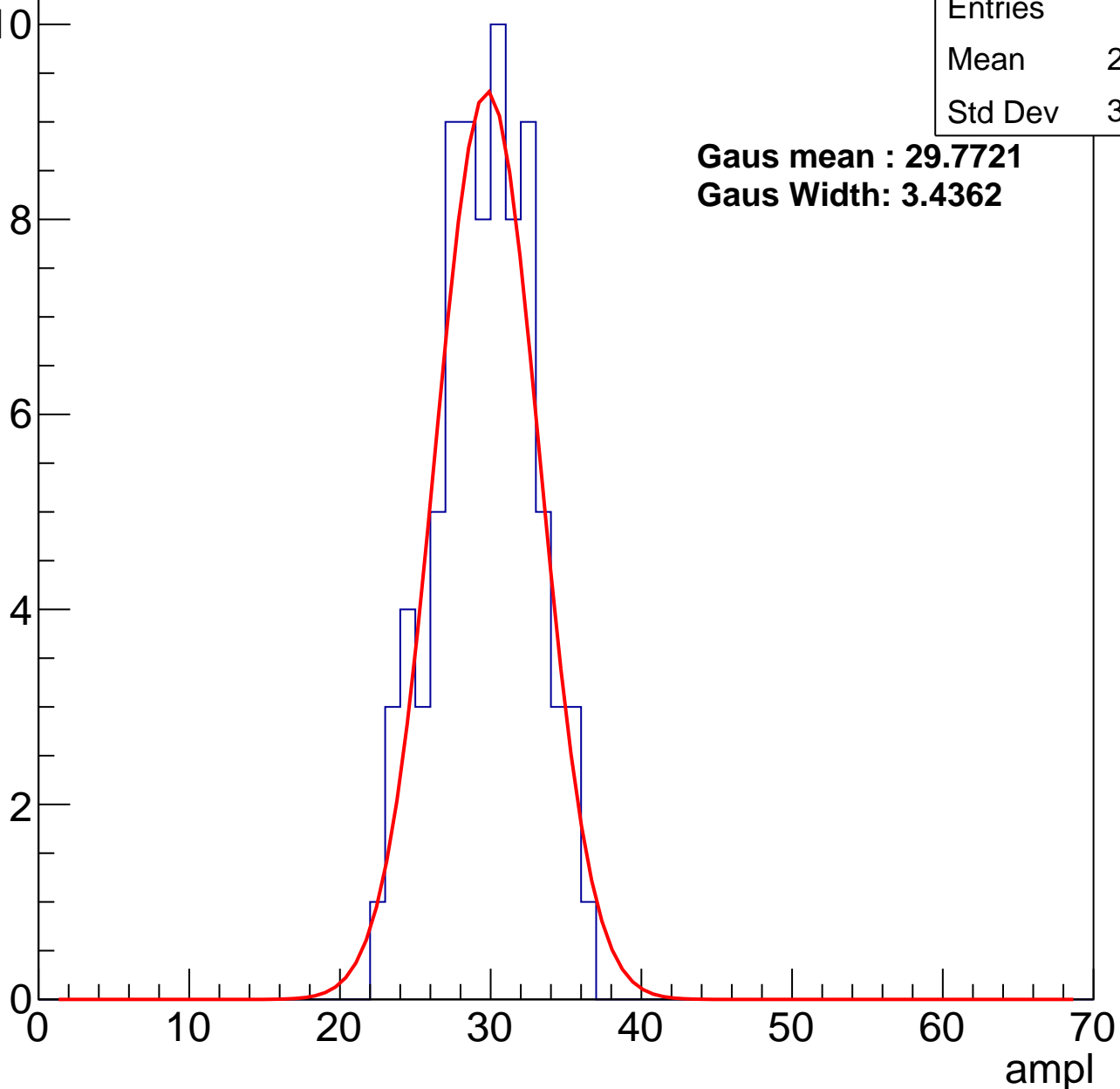
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	29.17
Std Dev	3.196

Gaus mean : 29.7721

Gaus Width: 3.4362



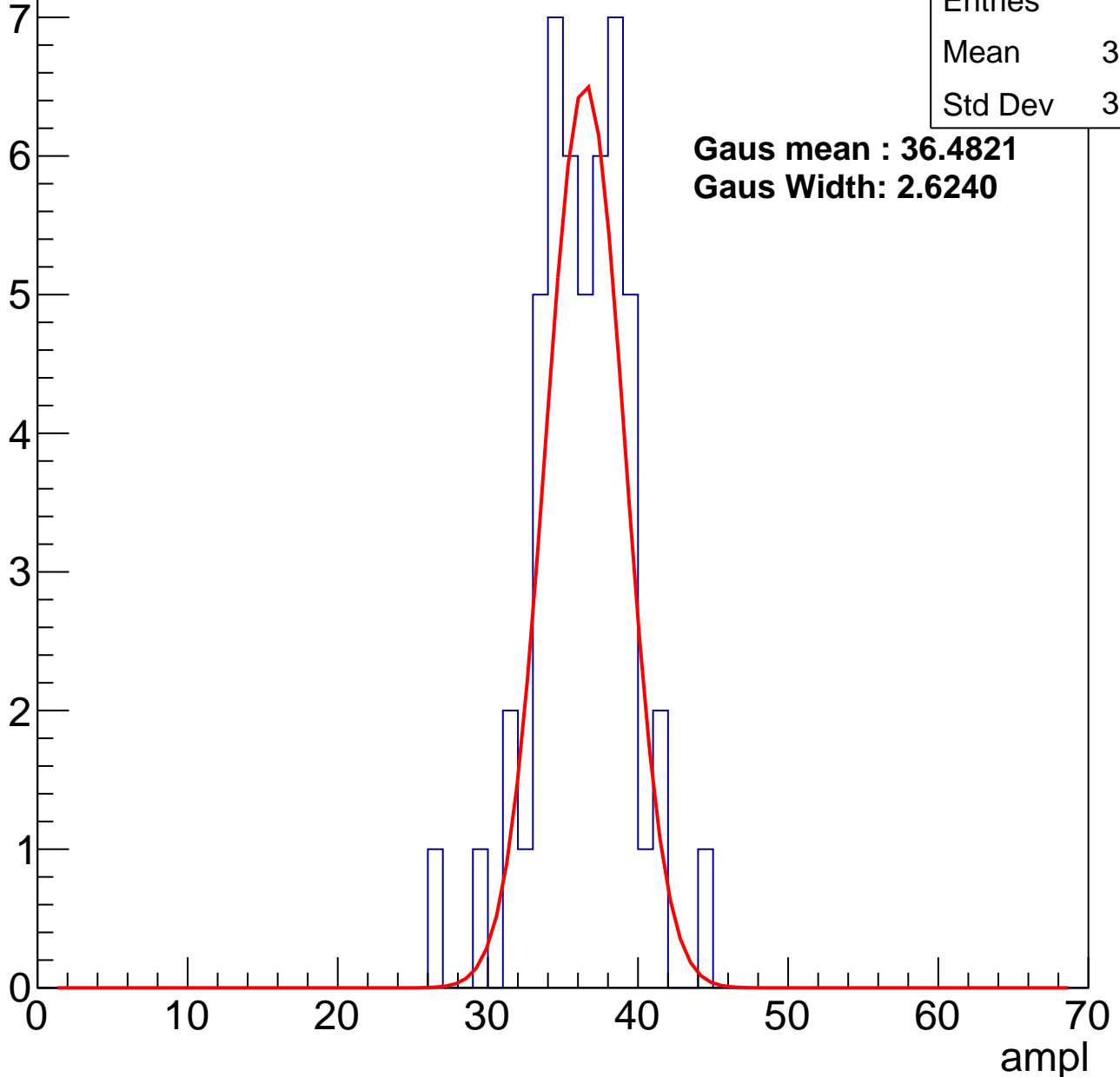
B0L002S, U5-ch106, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	35.82
Std Dev	3.167

Gaus mean : 36.4821
Gaus Width: 2.6240



B0L002S, U5-ch106, adc2

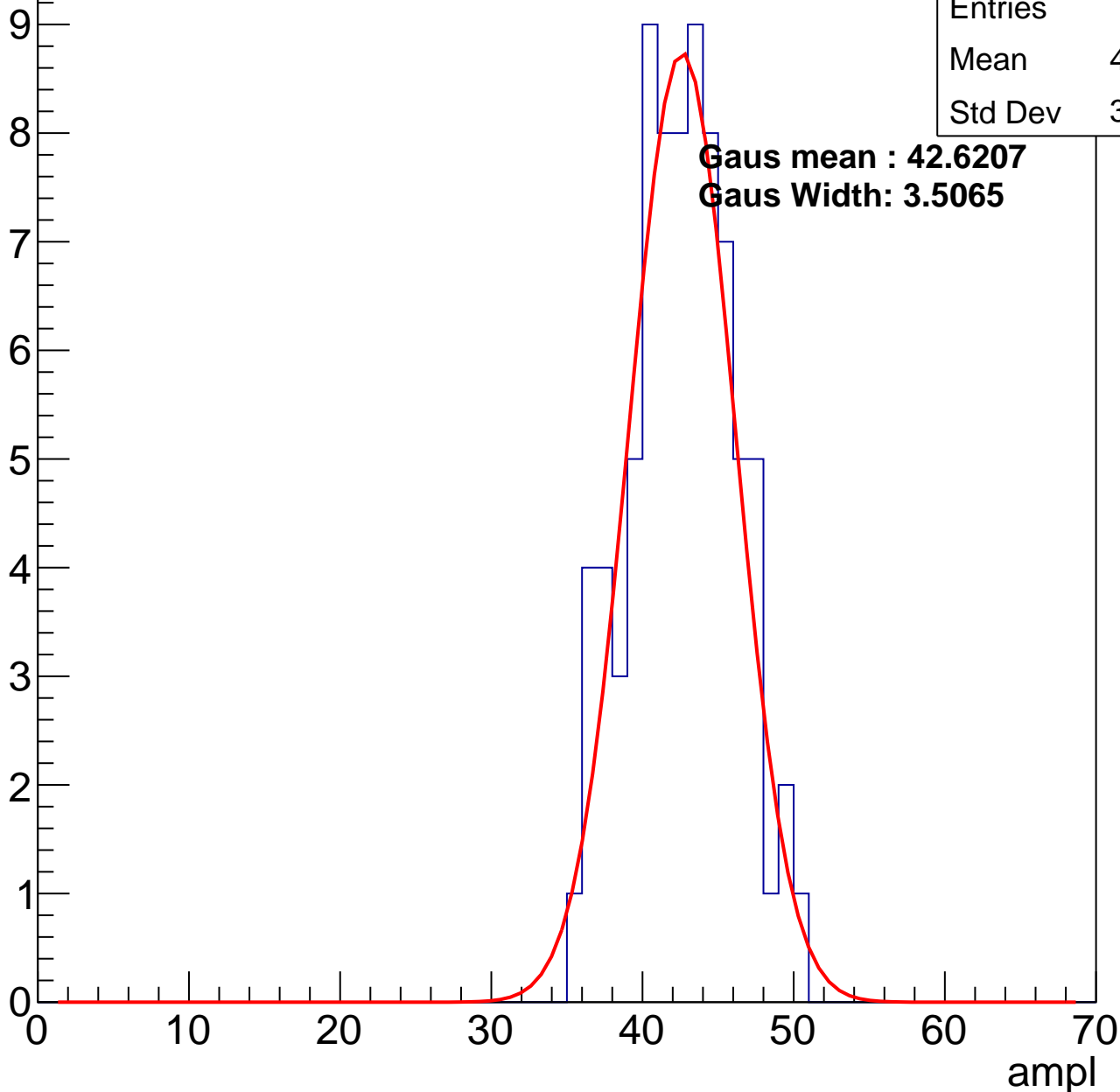
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	42.19
Std Dev	3.417

Gaus mean : 42.6207

Gaus Width: 3.5065

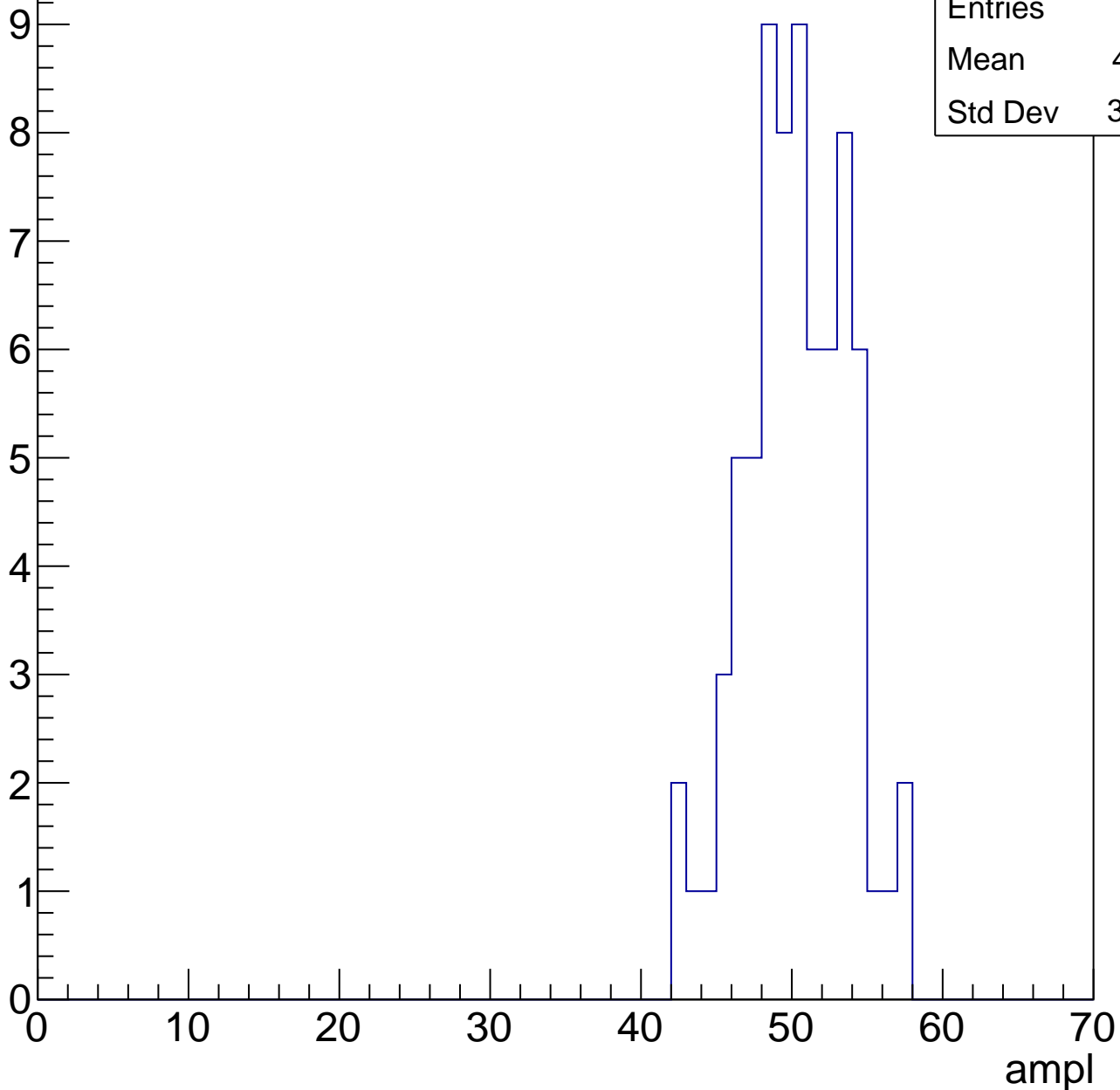


B0L002S, U5-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	49.81
Std Dev	3.334

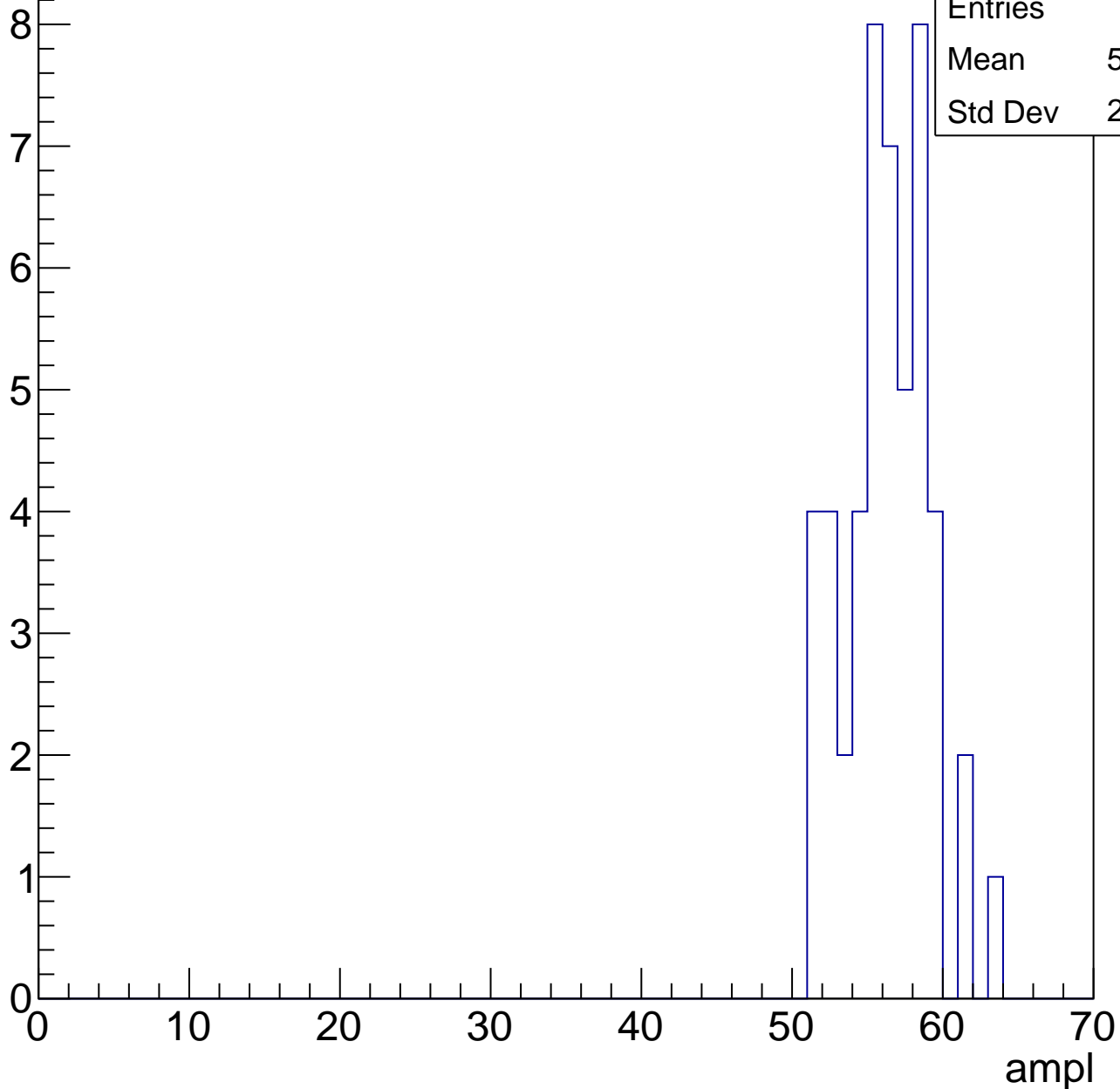


B0L002S, U5-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	55.84
Std Dev	2.773

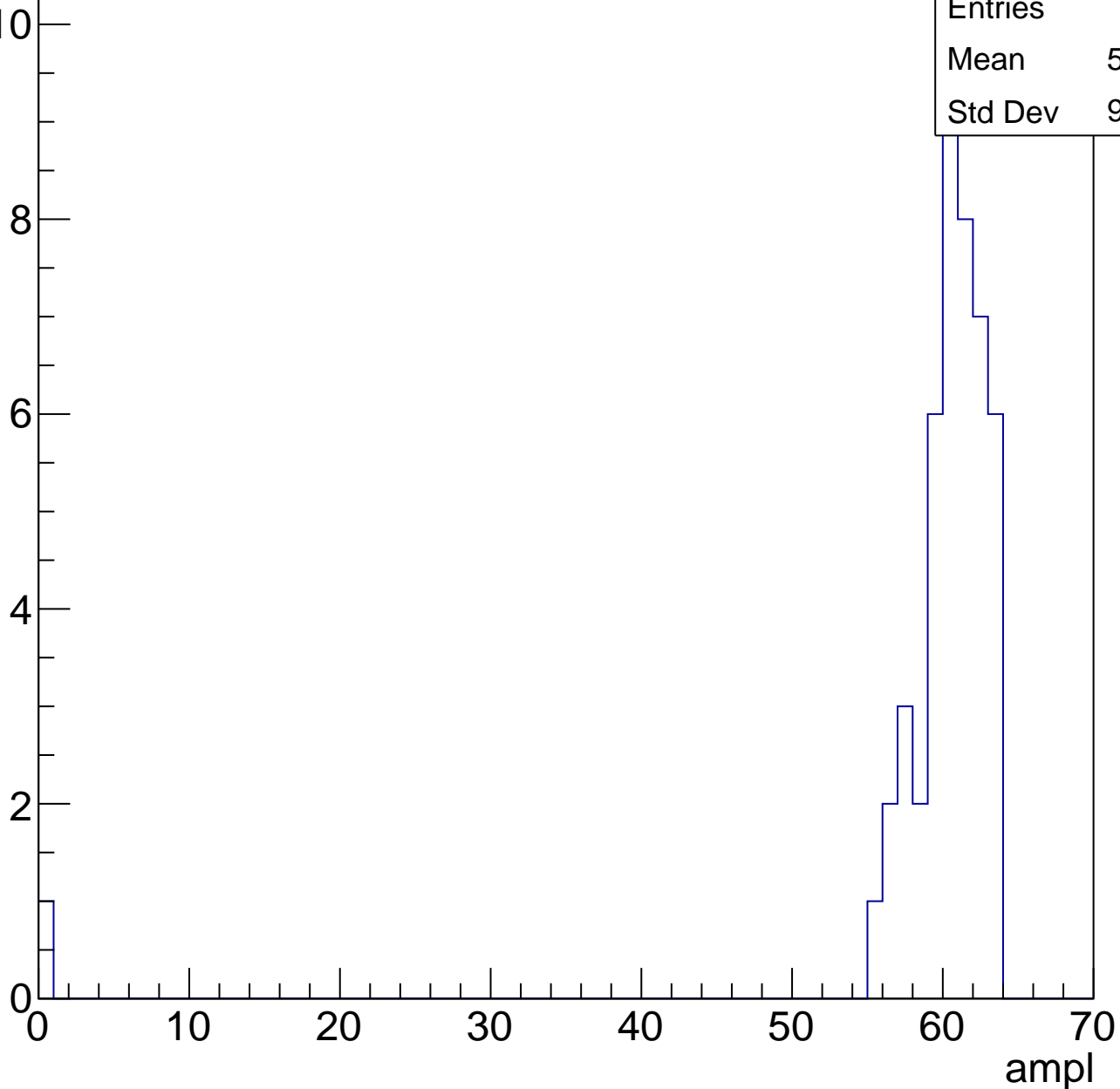


B0L002S, U5-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	58.87
Std Dev	9.004



B0L002S, U5-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch107, adc0

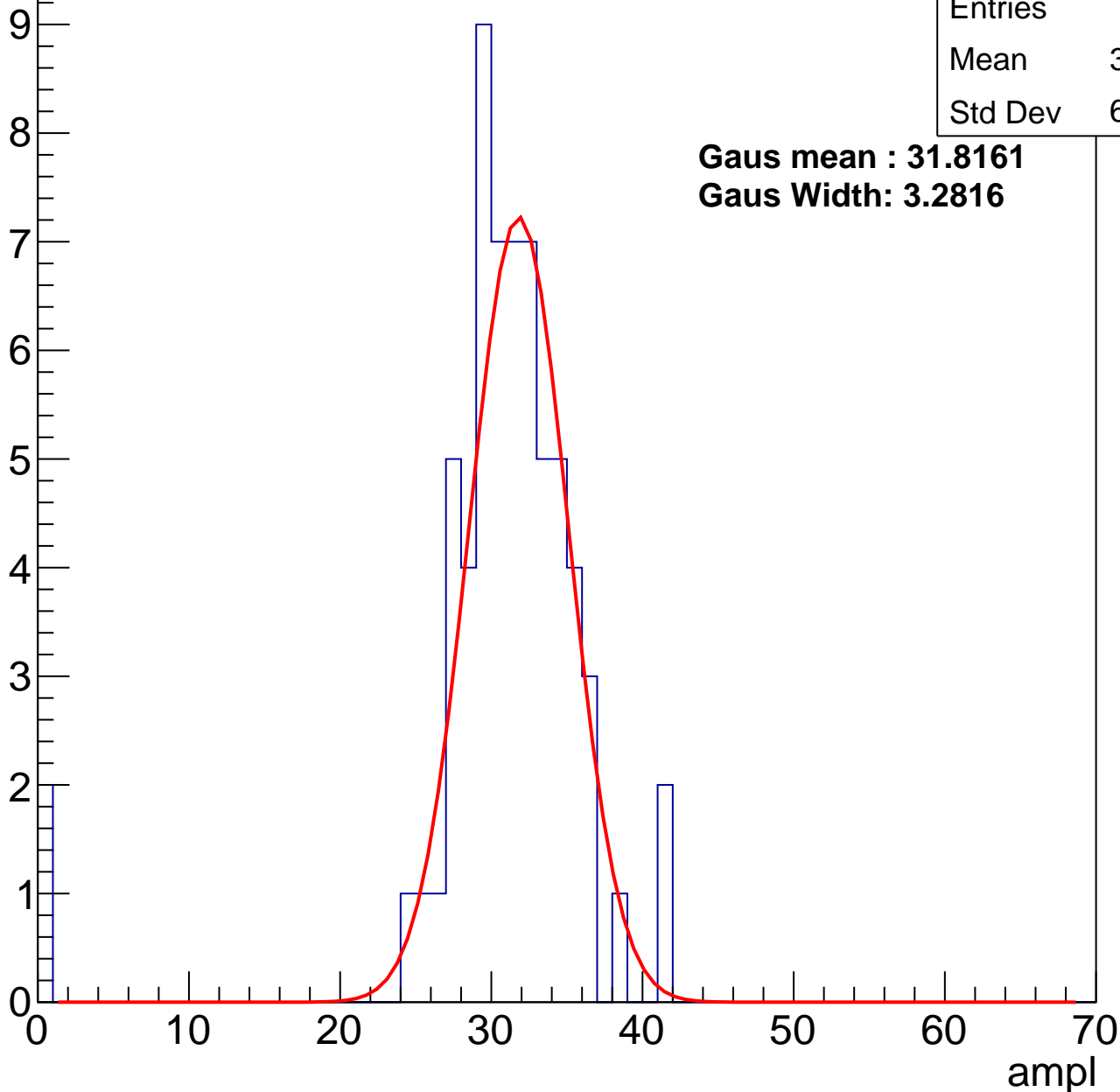
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.27
Std Dev	6.394

Gaus mean : 31.8161

Gaus Width: 3.2816



B0L002S, U5-ch107, adc1

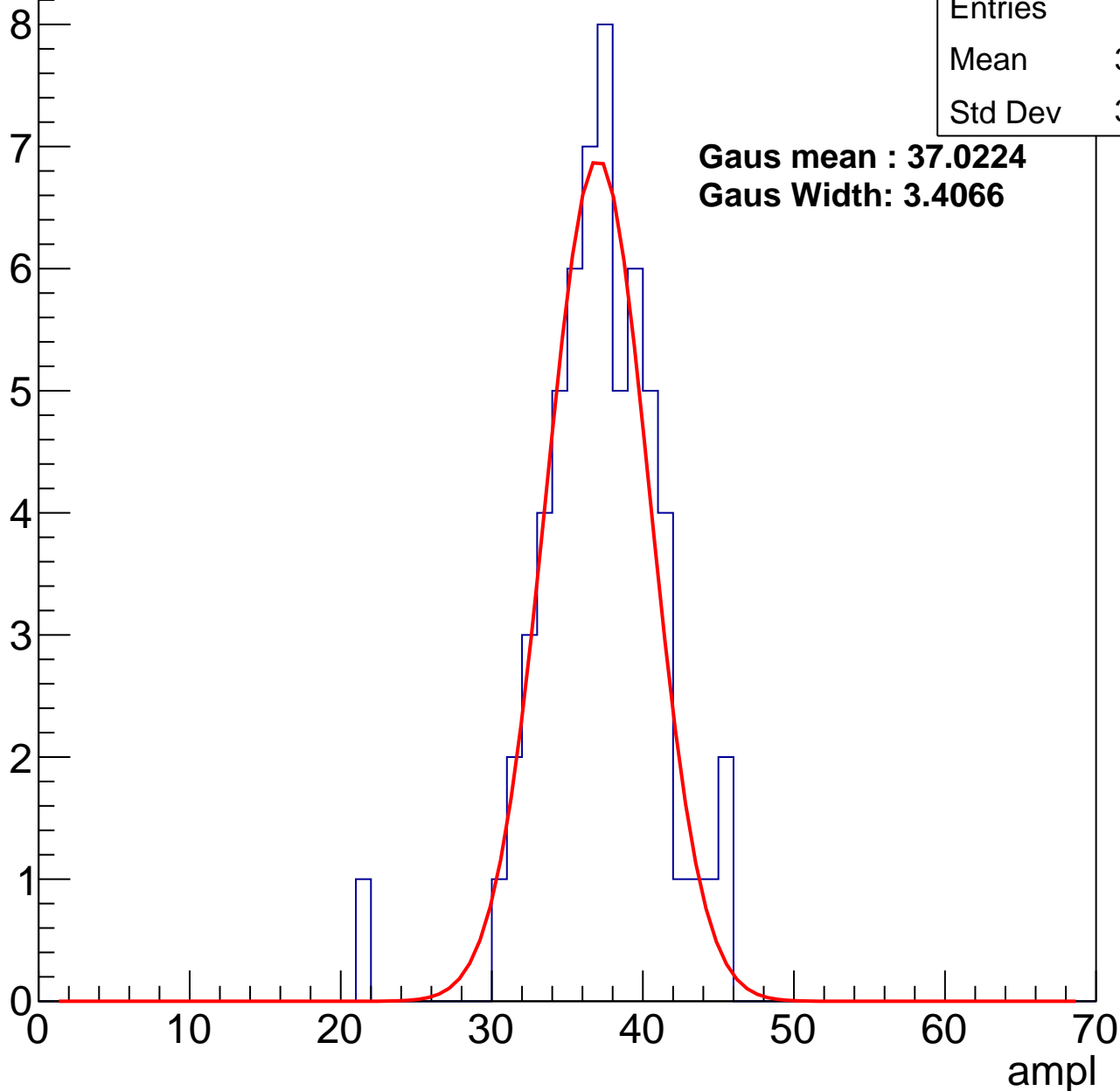
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.71
Std Dev	3.941

Gaus mean : 37.0224

Gaus Width: 3.4066



B0L002S, U5-ch107, adc2

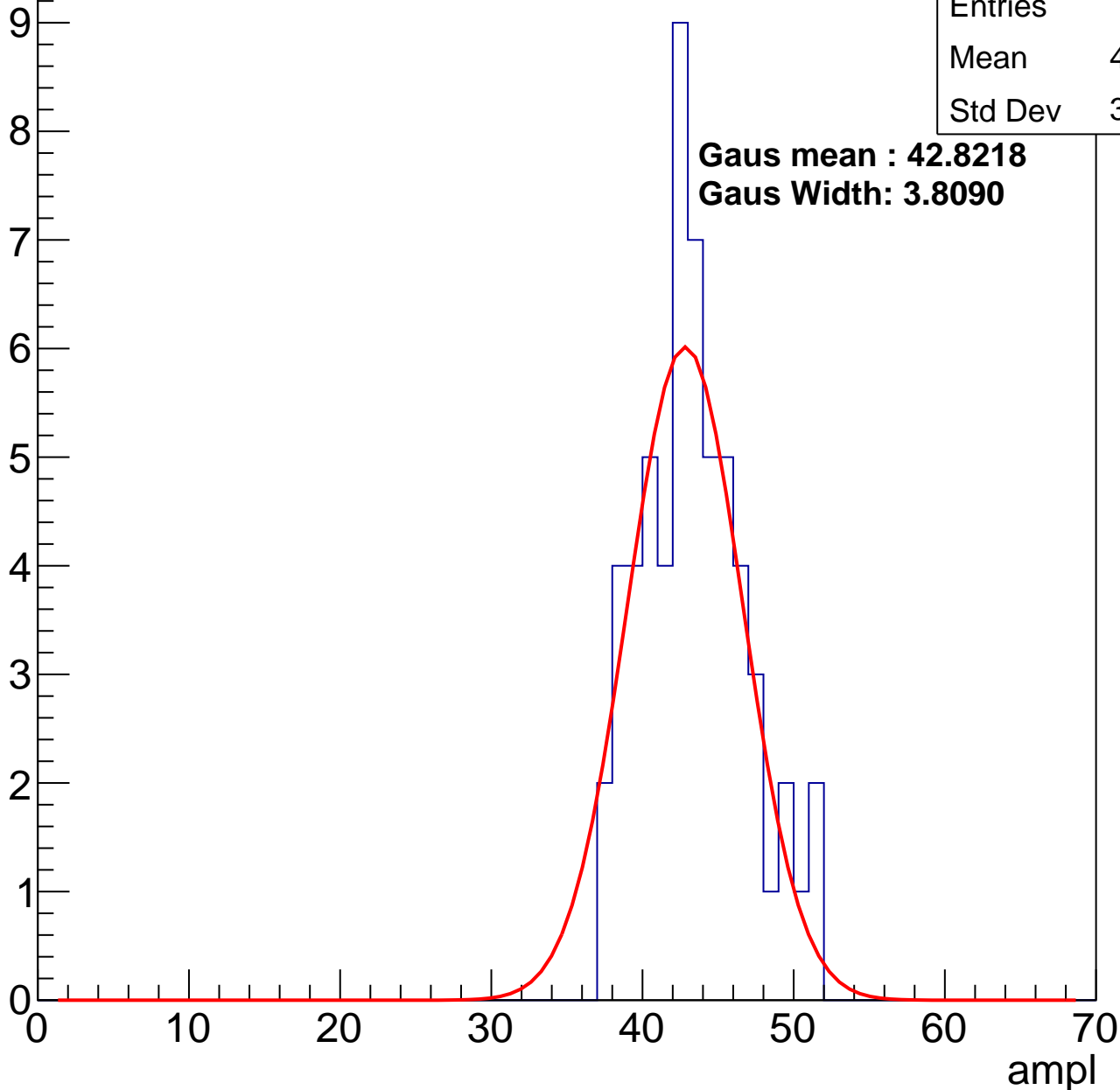
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.98
Std Dev	3.447

Gaus mean : 42.8218

Gaus Width: 3.8090

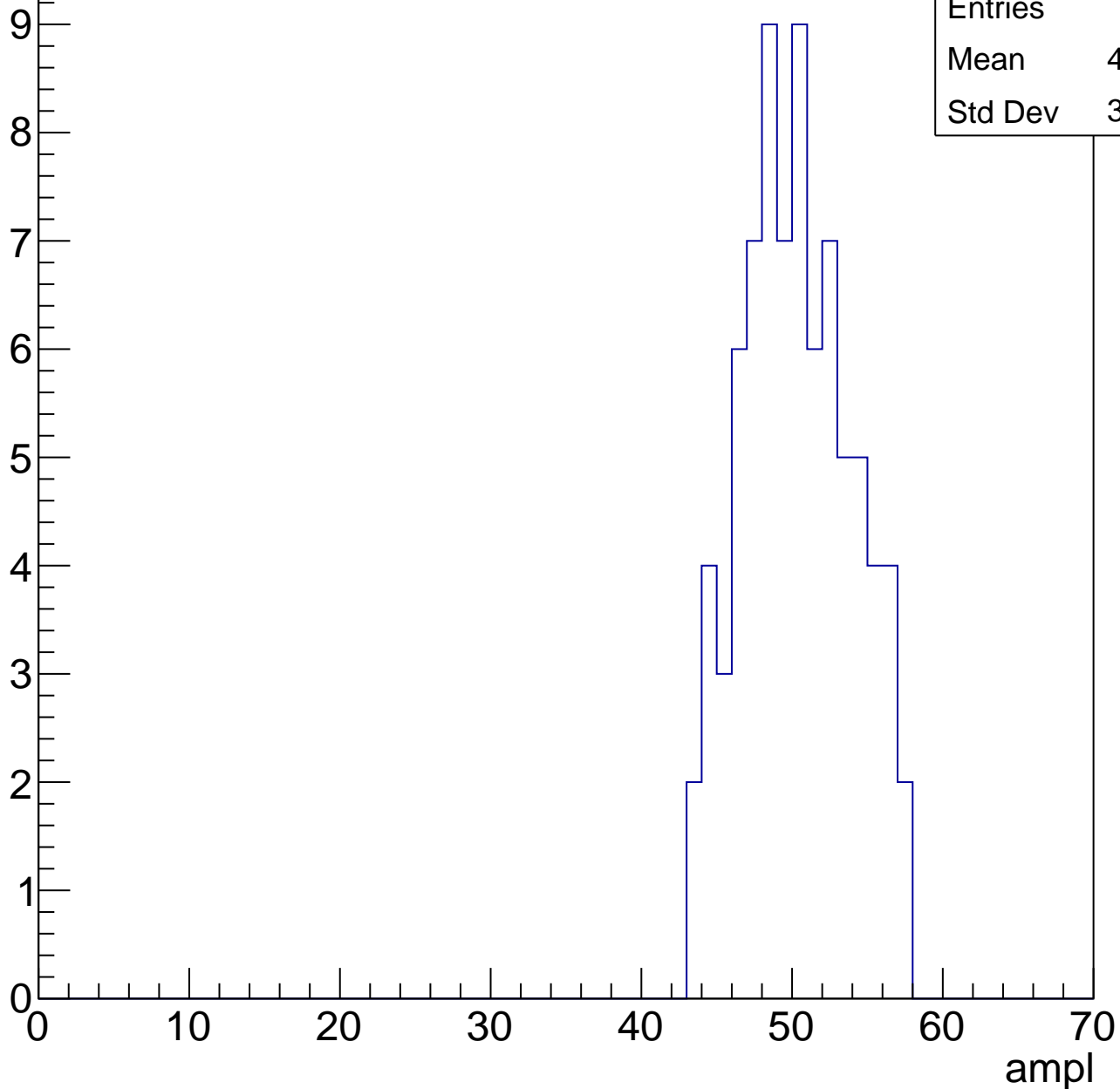


B0L002S, U5-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	49.88
Std Dev	3.569

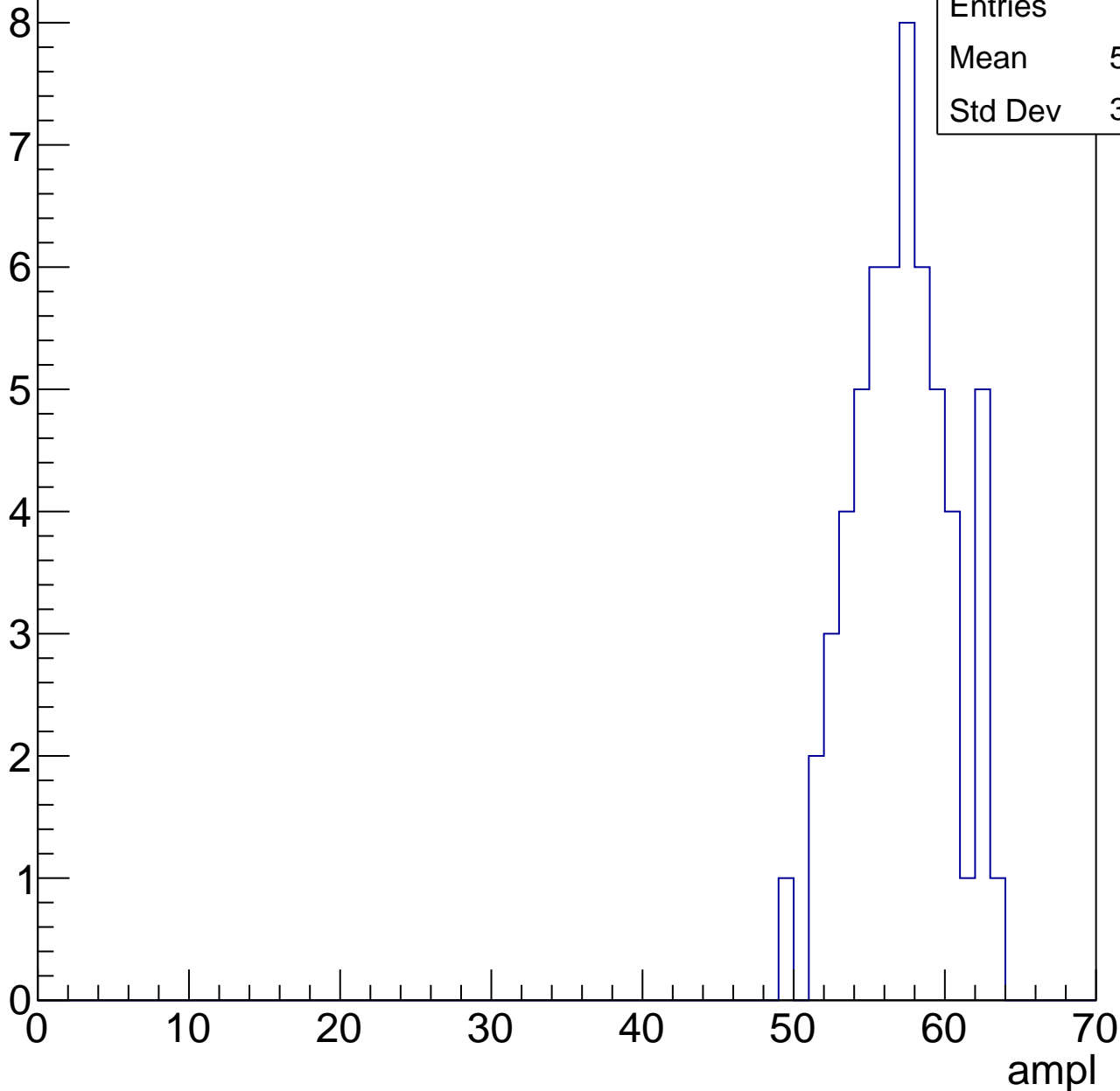


B0L002S, U5-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.63
Std Dev	3.193

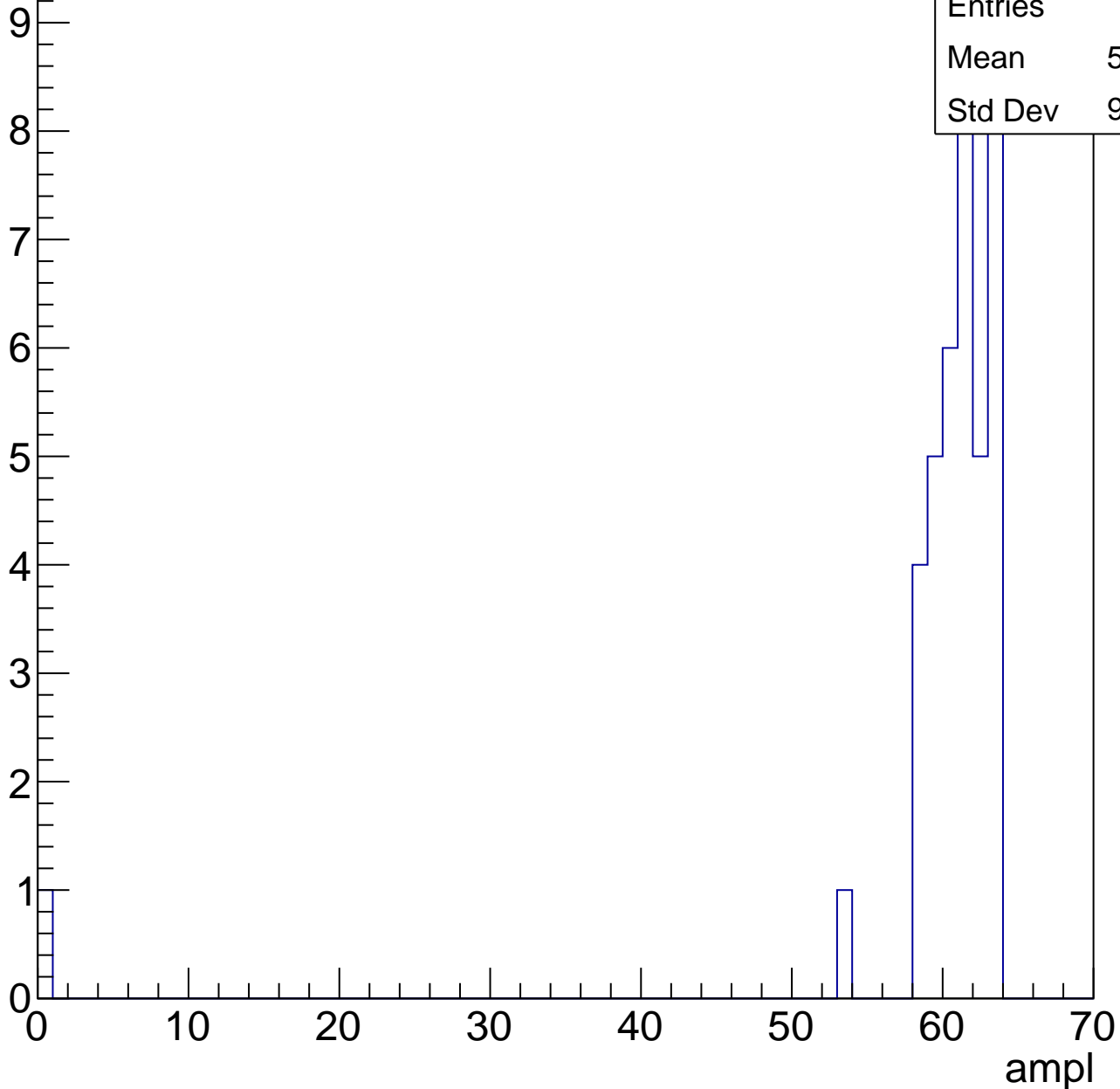


B0L002S, U5-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.15
Std Dev	9.684



B0L002S, U5-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch108, adc0

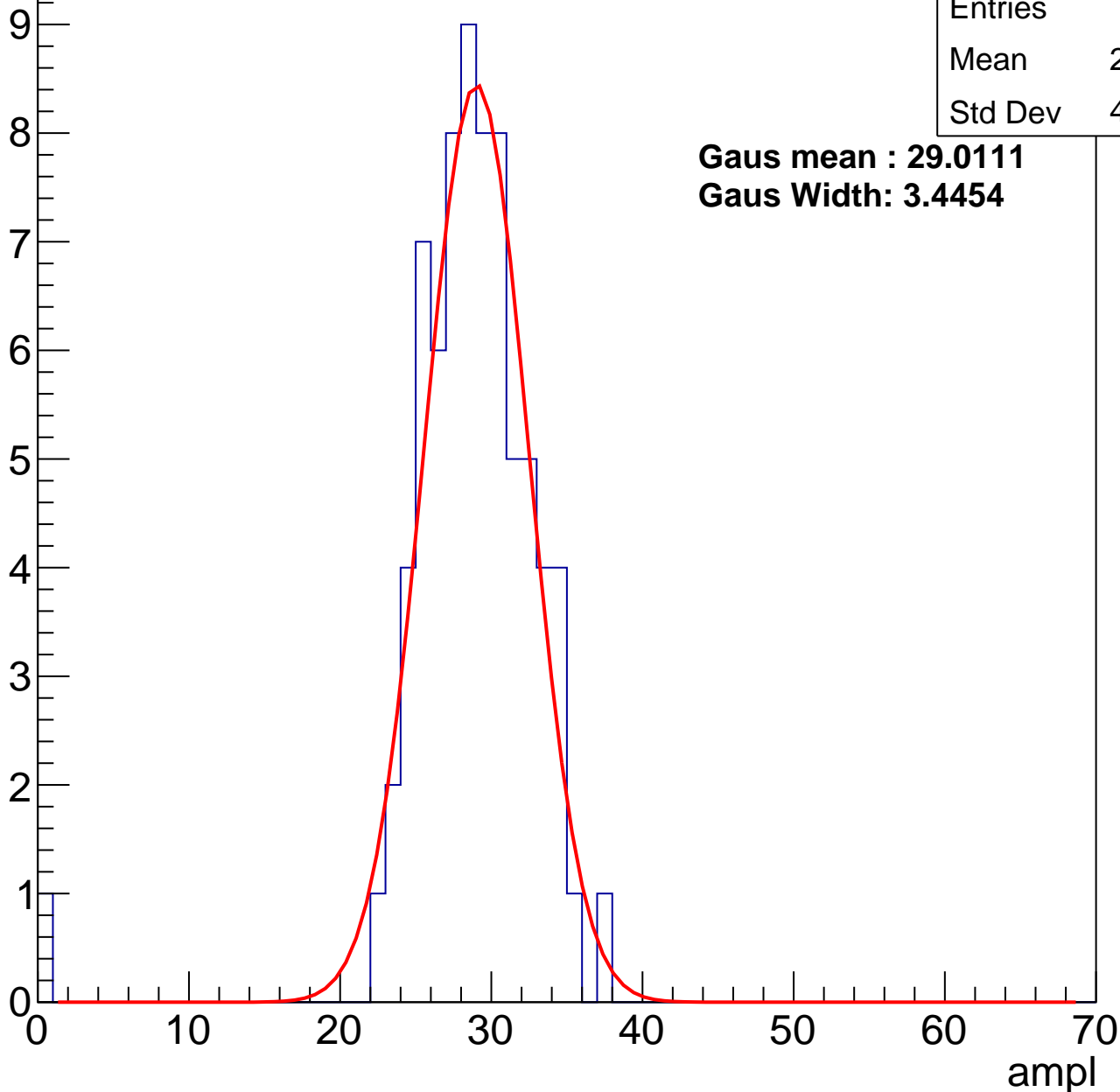
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	28.24
Std Dev	4.597

Gaus mean : 29.0111

Gaus Width: 3.4454



B0L002S, U5-ch108, adc1

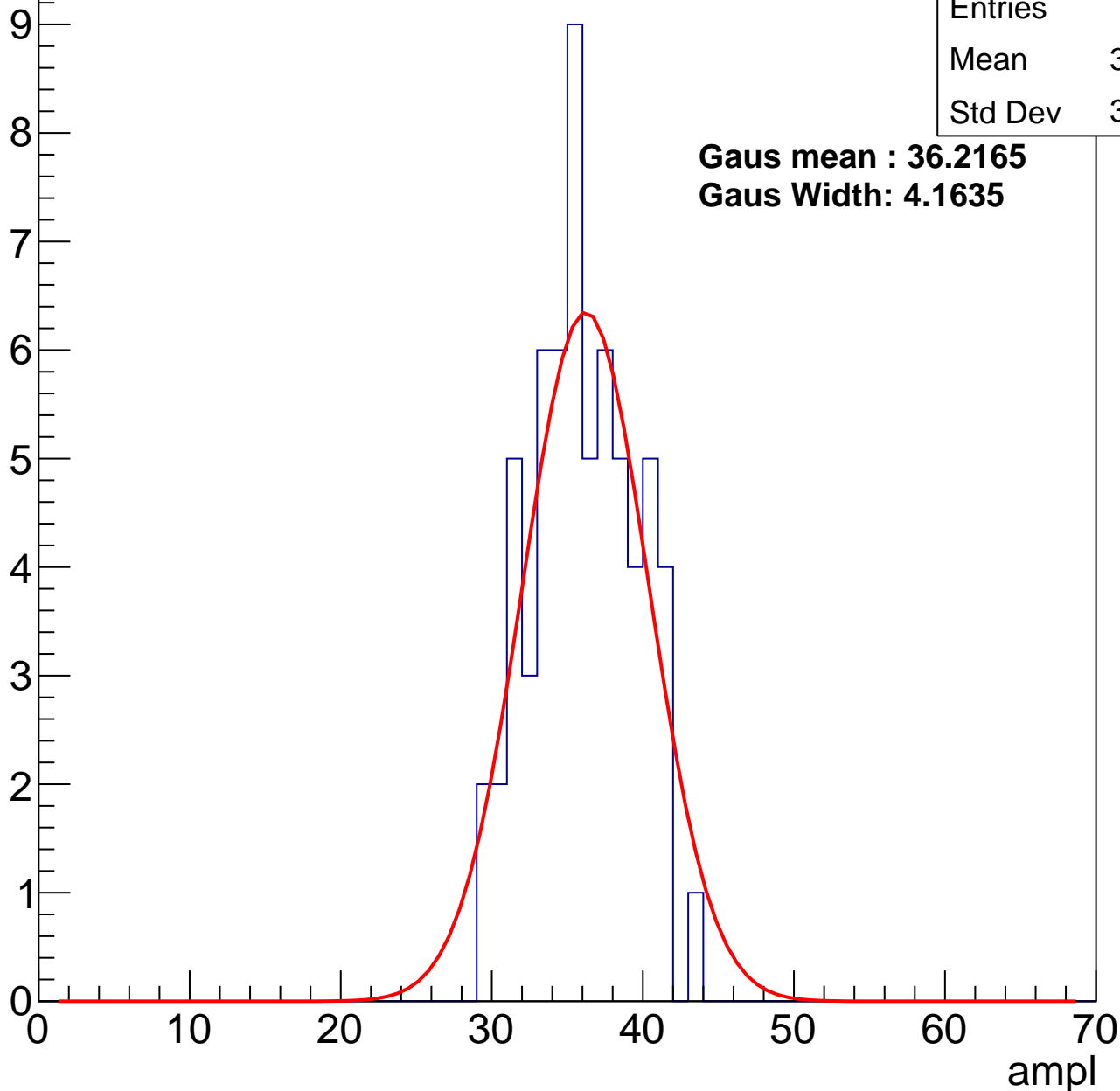
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	35.57
Std Dev	3.356

Gaus mean : 36.2165

Gaus Width: 4.1635



B0L002S, U5-ch108, adc2

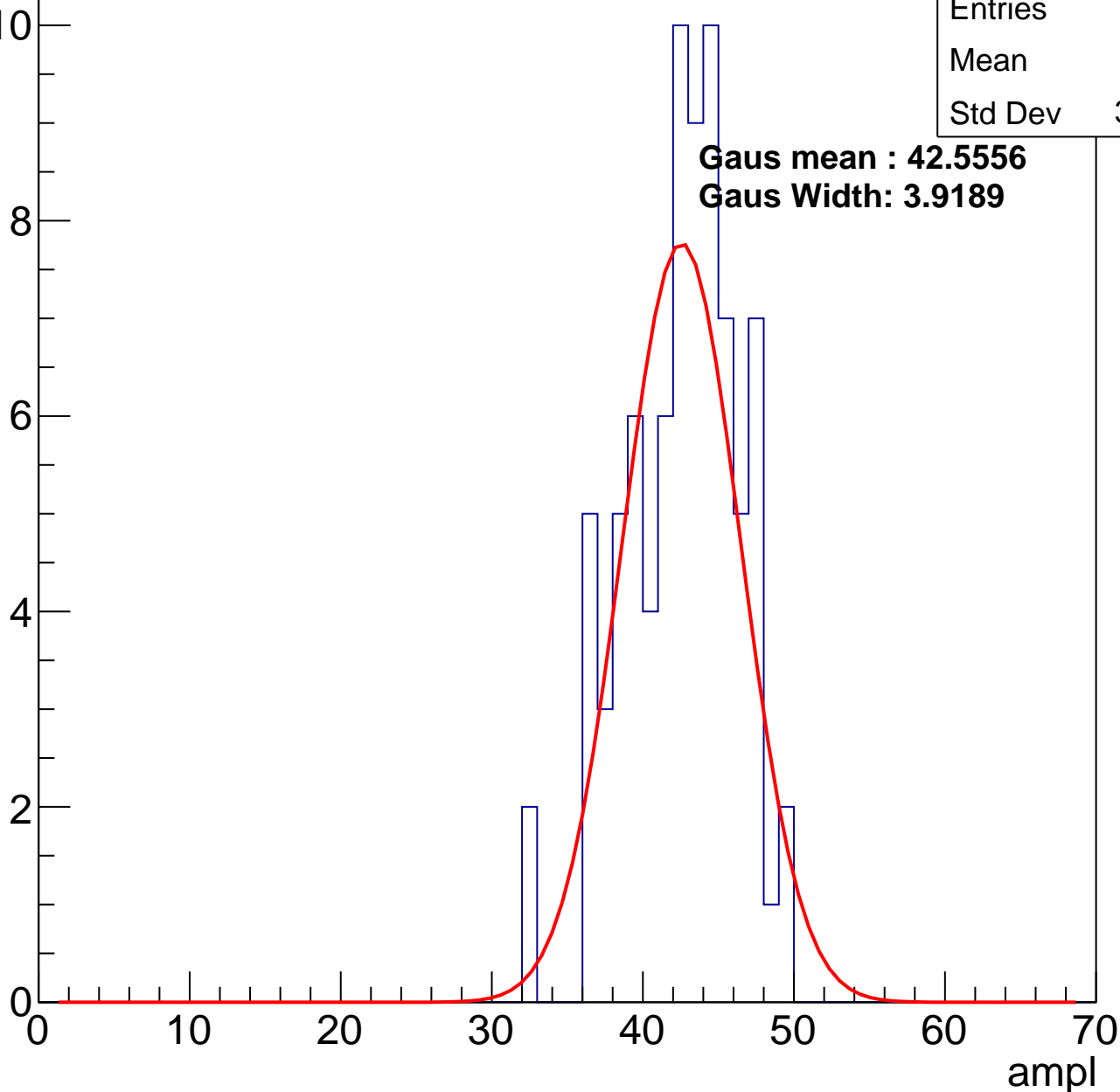
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	42.1
Std Dev	3.681

Gaus mean : 42.5556

Gaus Width: 3.9189

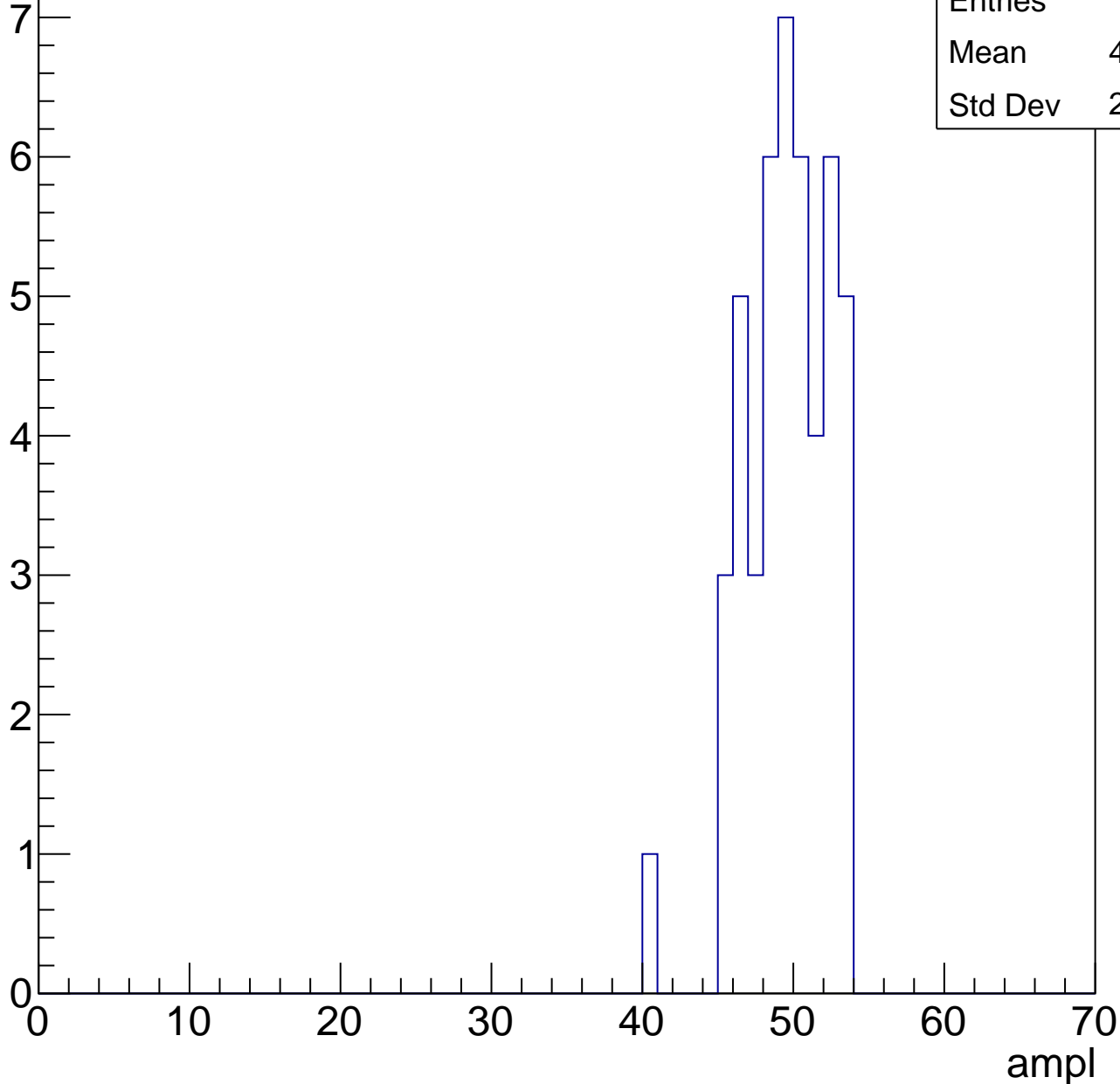


B0L002S, U5-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	49.09
Std Dev	2.749

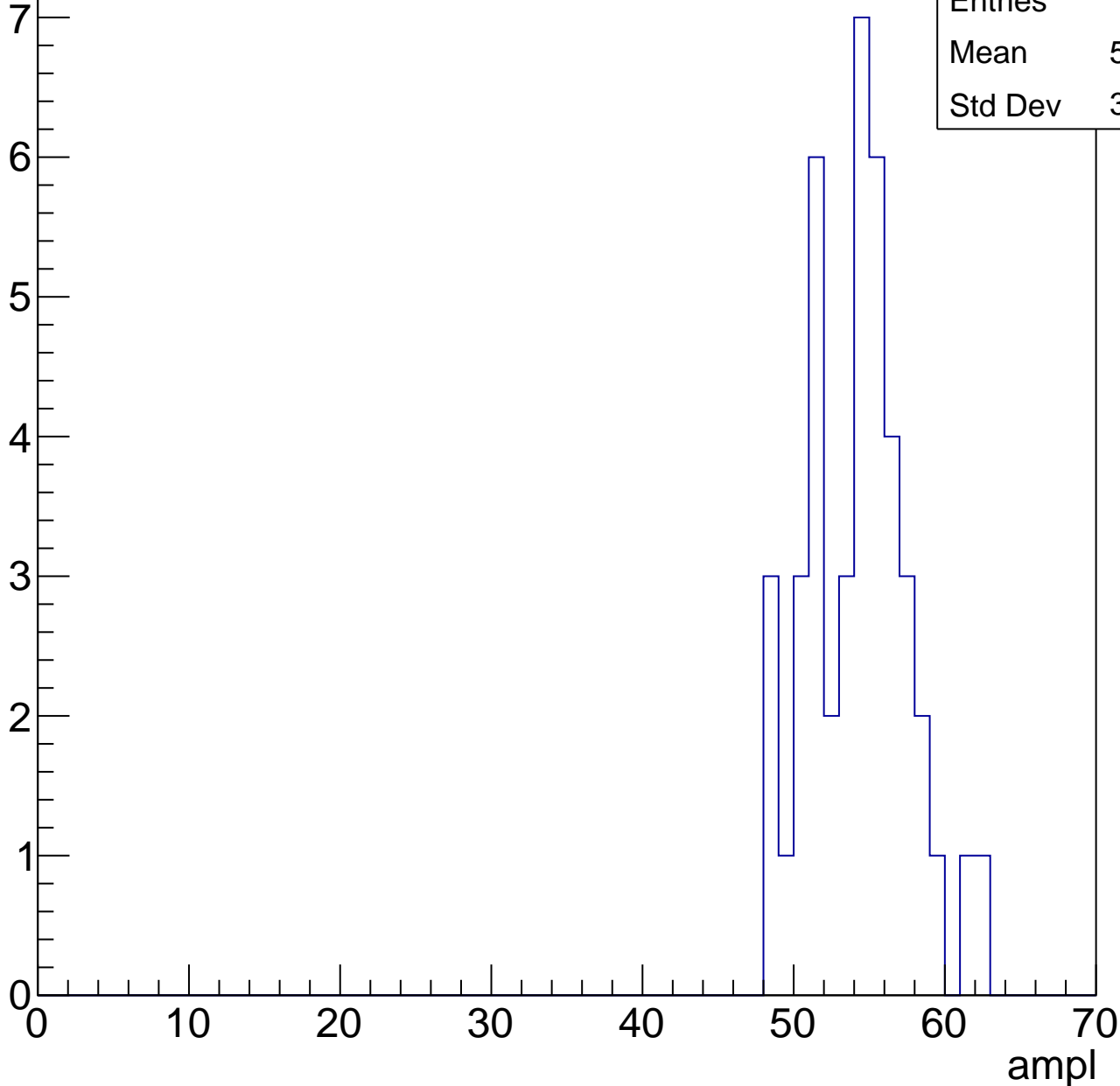


B0L002S, U5-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

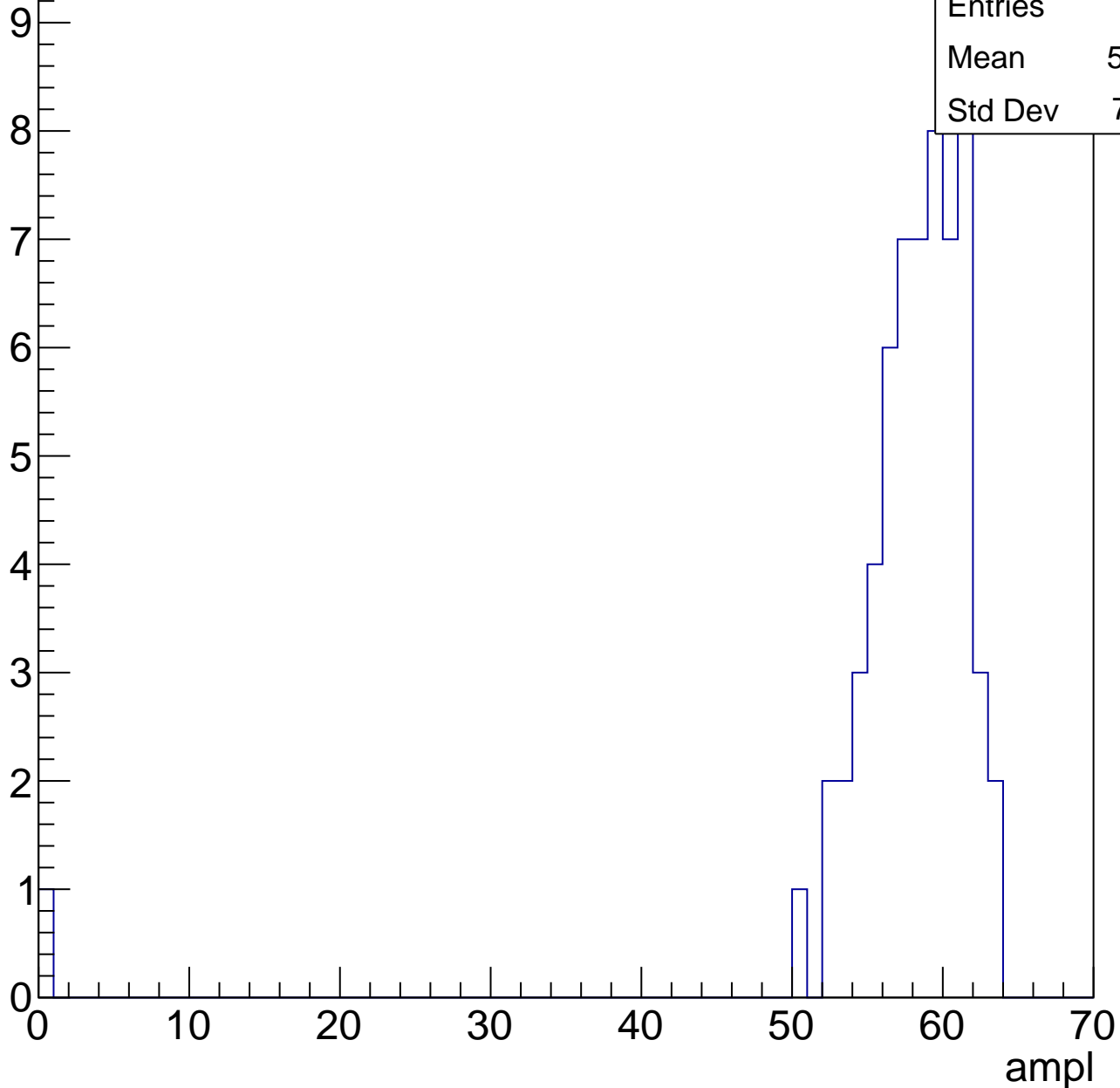
Entries	43
Mean	53.79
Std Dev	3.282



B0L002S, U5-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

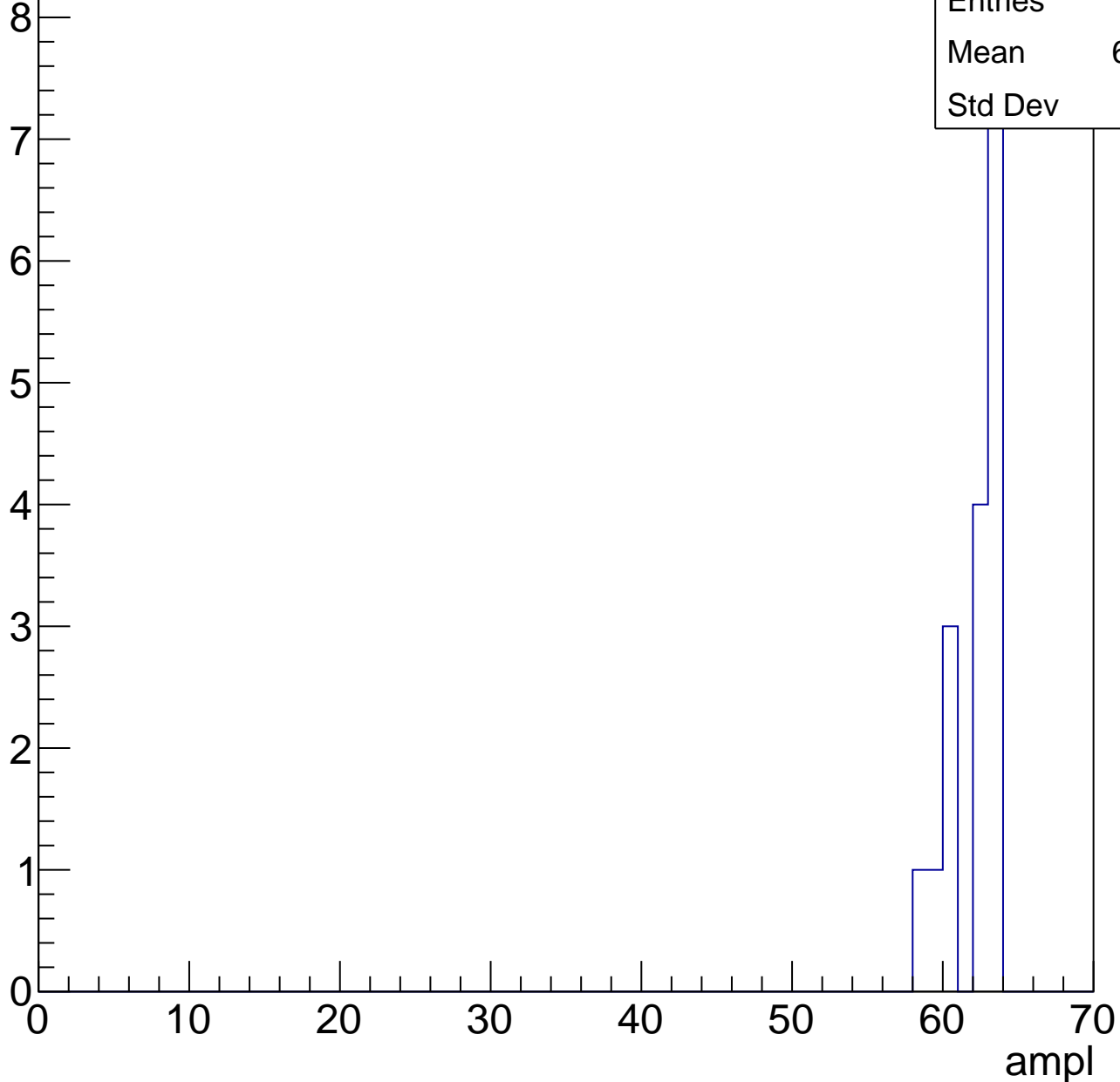


B0L002S, U5-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

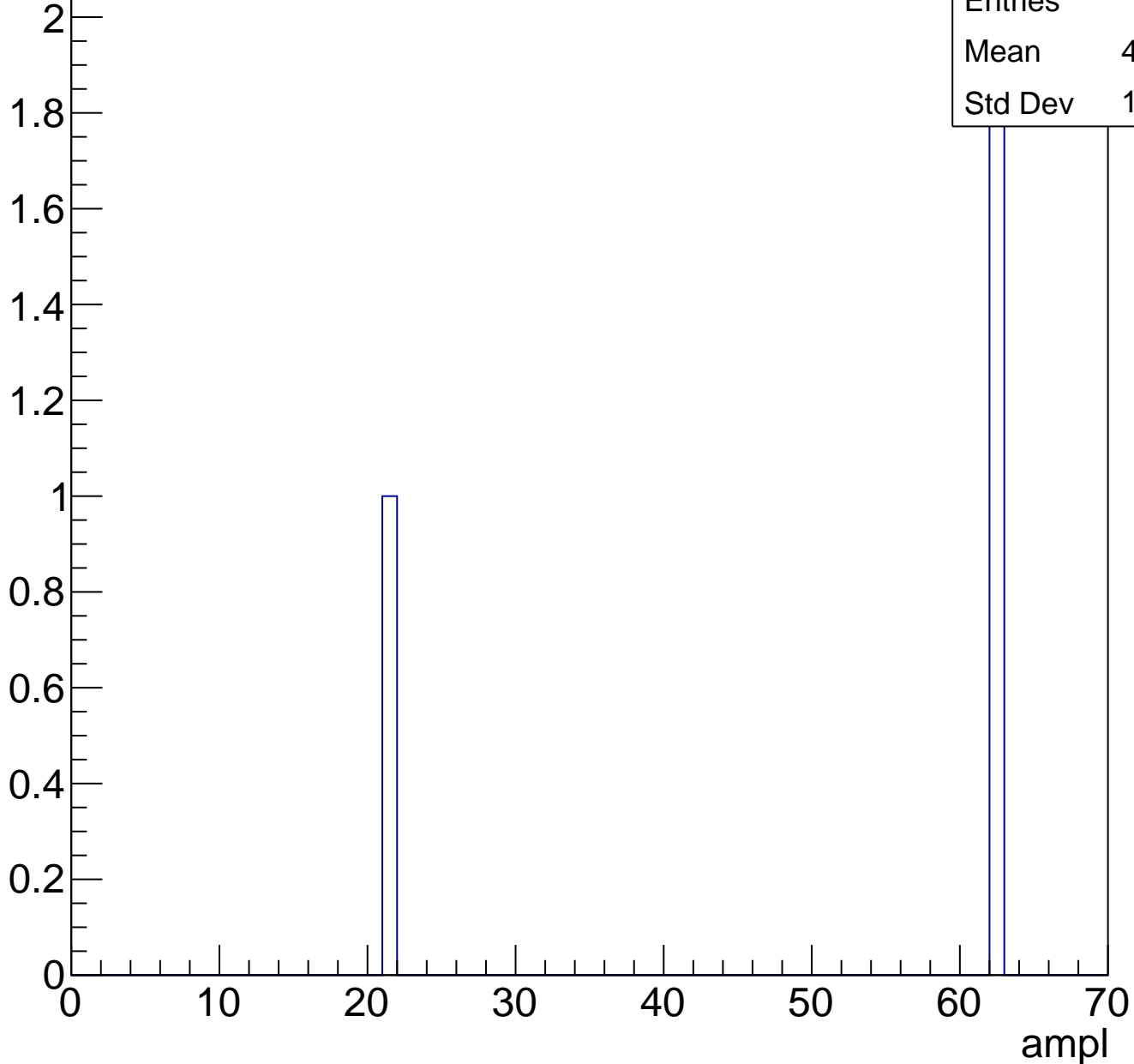
Entries	17
Mean	61.71
Std Dev	1.6



B0L002S, U5-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	48.33
Std Dev	19.33

B0L002S, U5-ch109, adc0

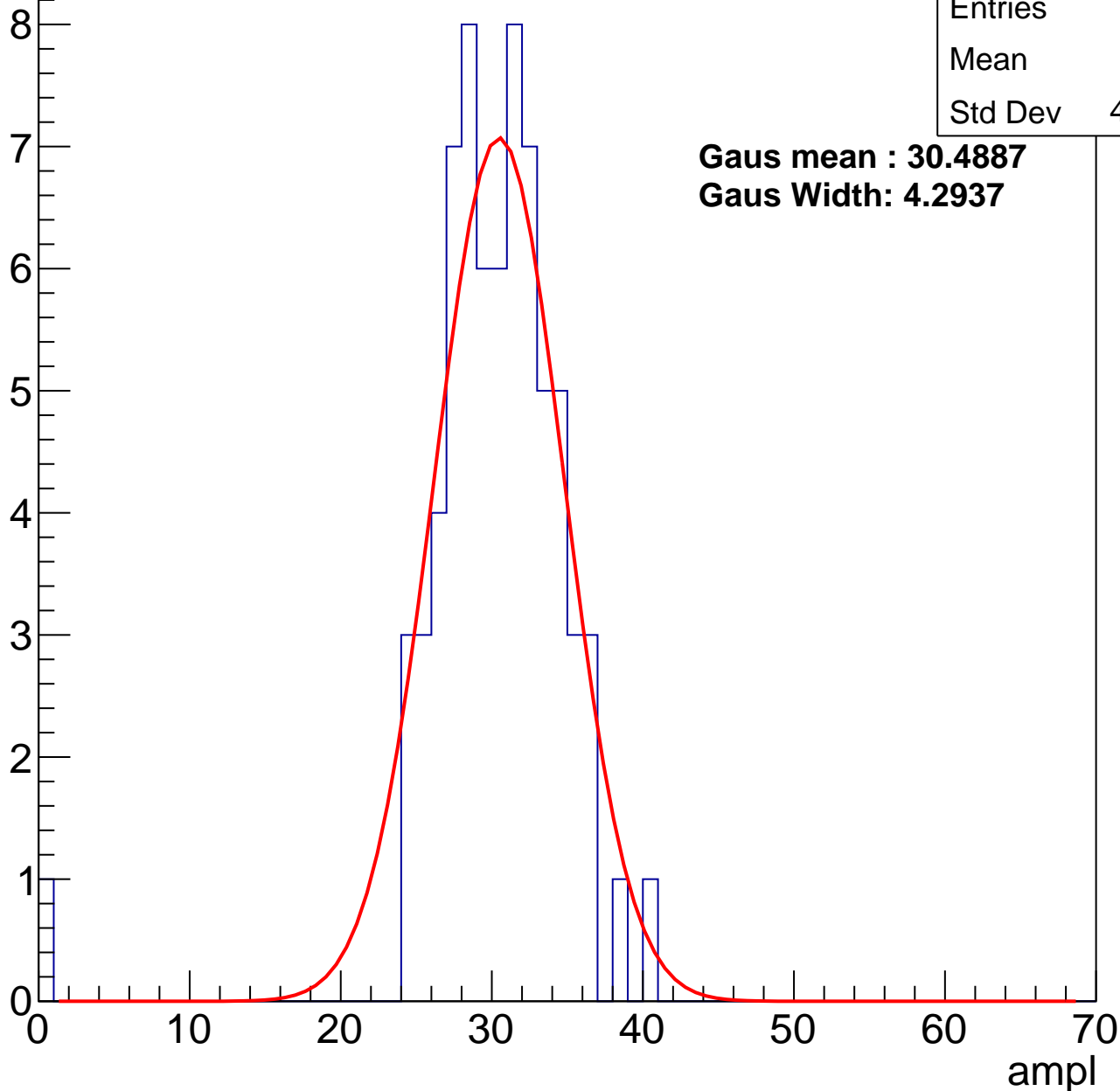
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.8
Std Dev	4.969

Gaus mean : 30.4887

Gaus Width: 4.2937



B0L002S, U5-ch109, adc1

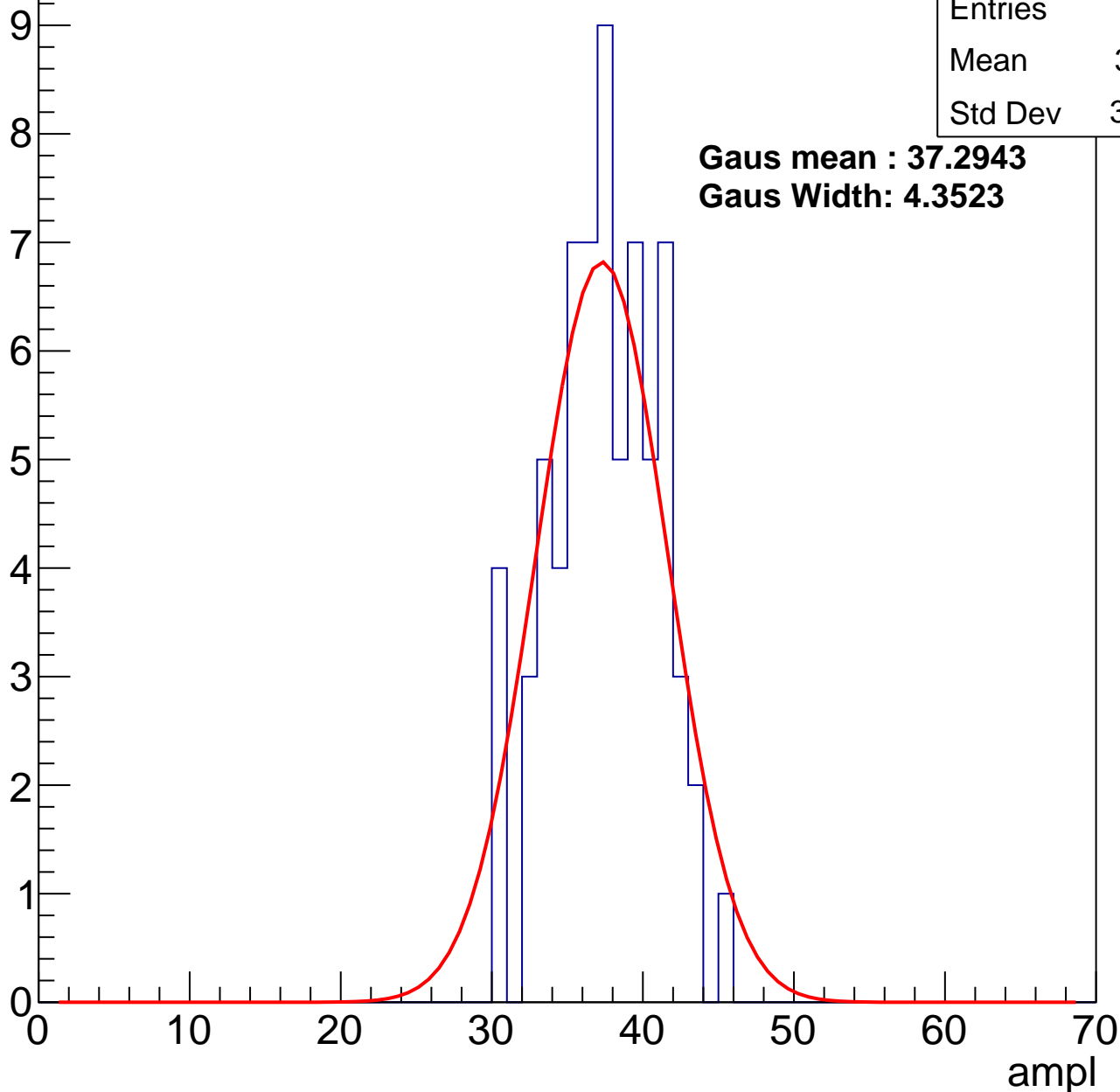
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.01
Std Dev	3.454

Gaus mean : 37.2943

Gaus Width: 4.3523



B0L002S, U5-ch109, adc2

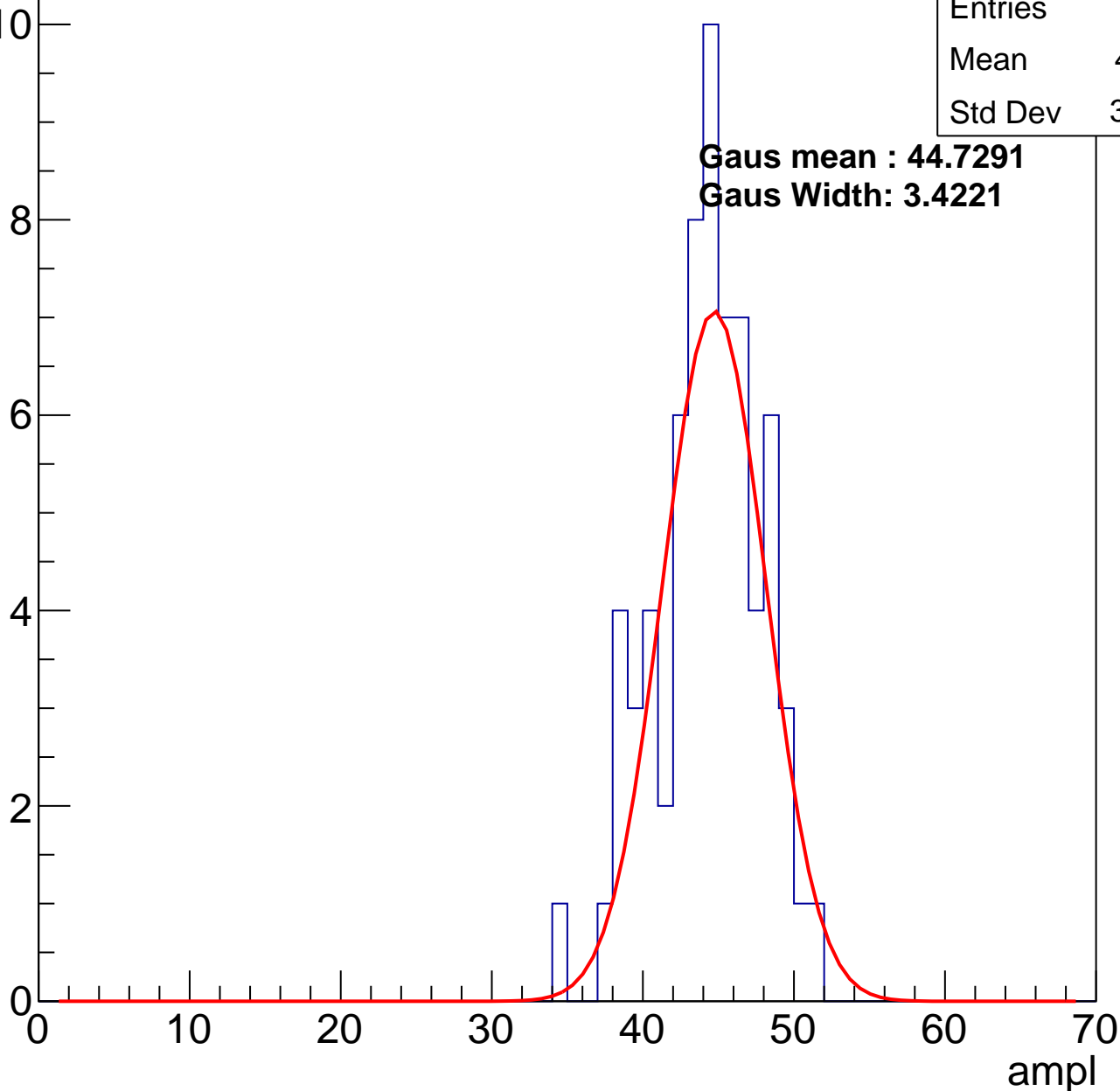
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.81
Std Dev	3.444

Gaus mean : 44.7291

Gaus Width: 3.4221

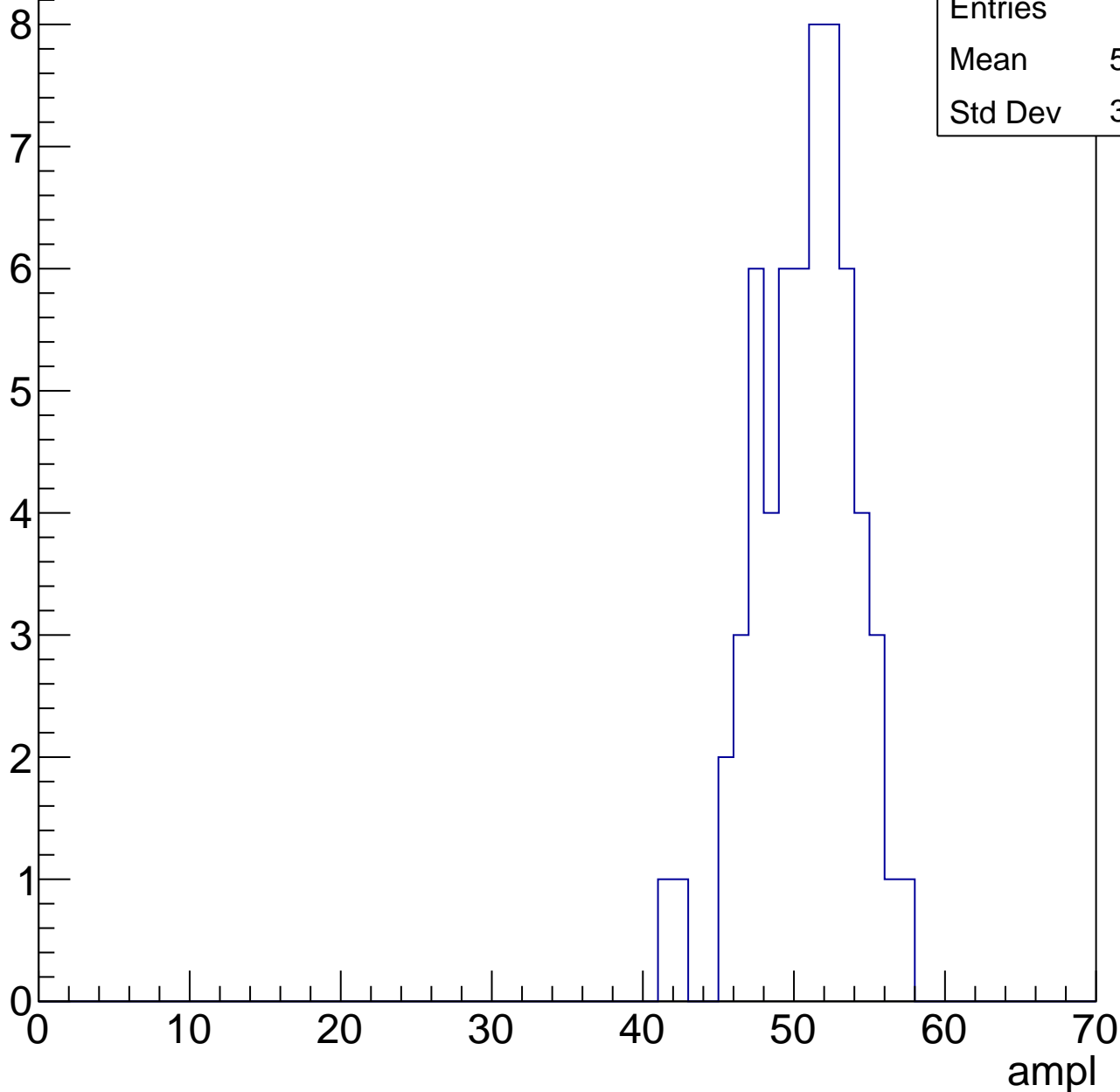


B0L002S, U5-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.25
Std Dev	3.249

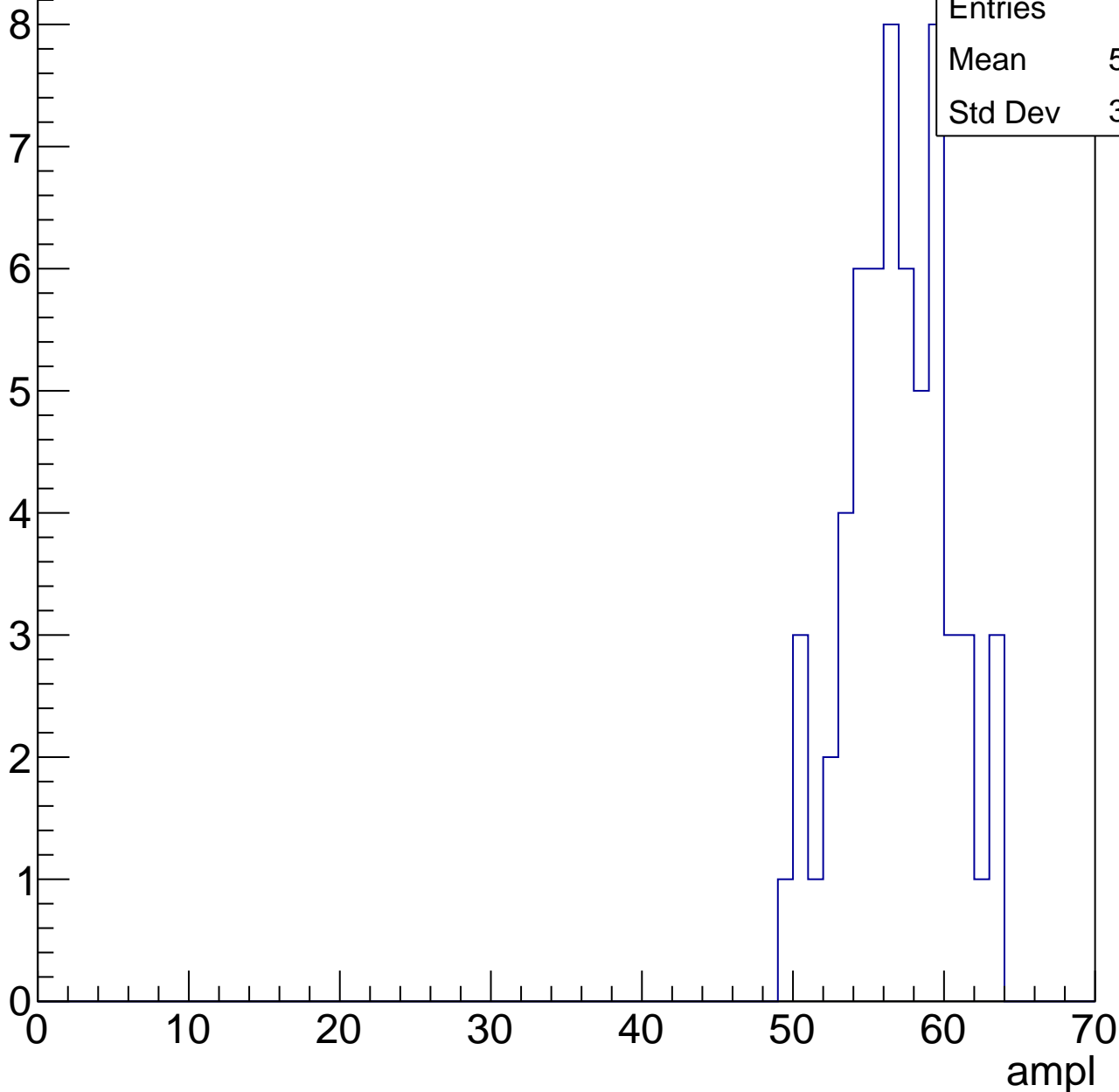


B0L002S, U5-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.43
Std Dev	3.348

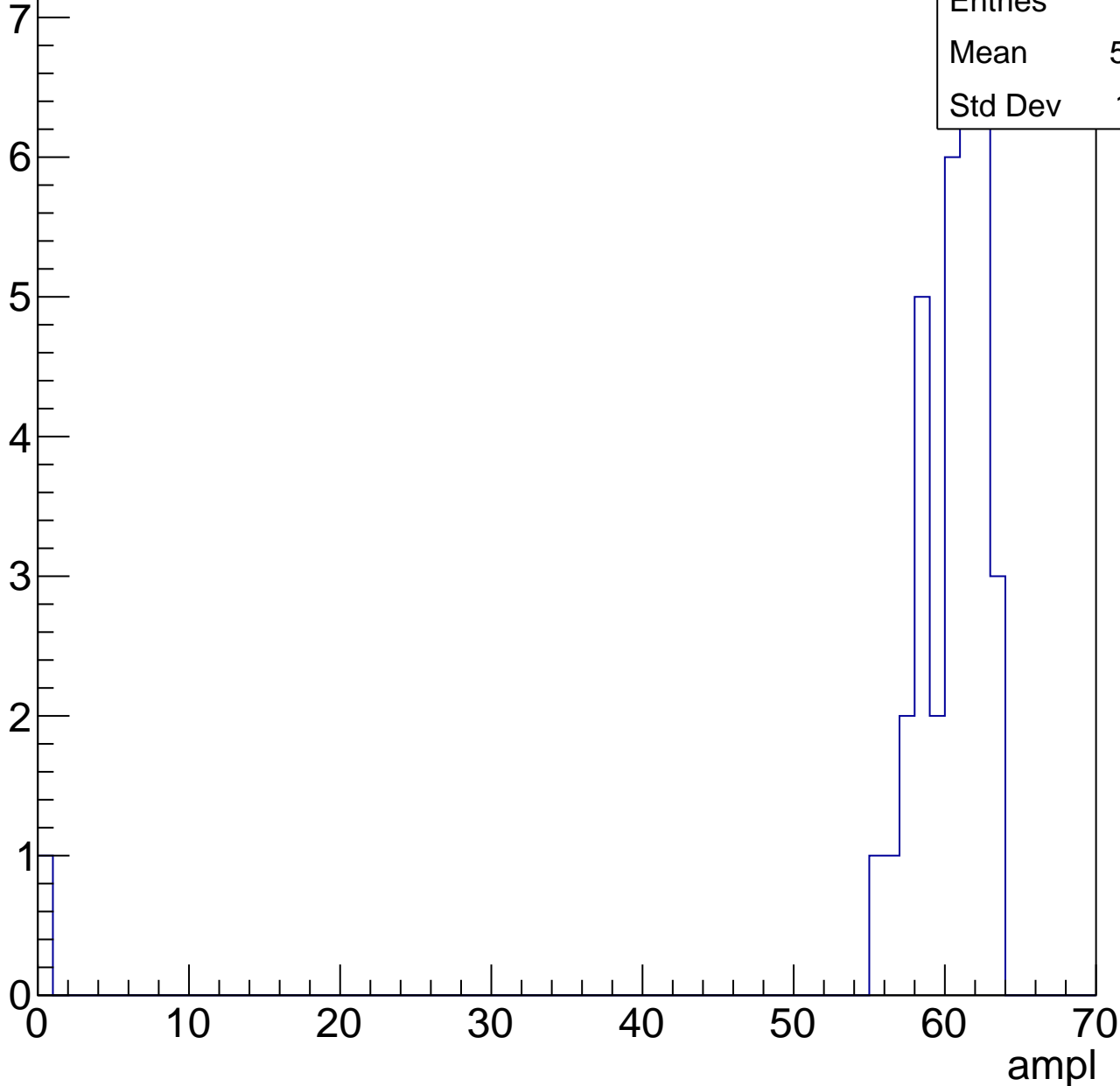


B0L002S, U5-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

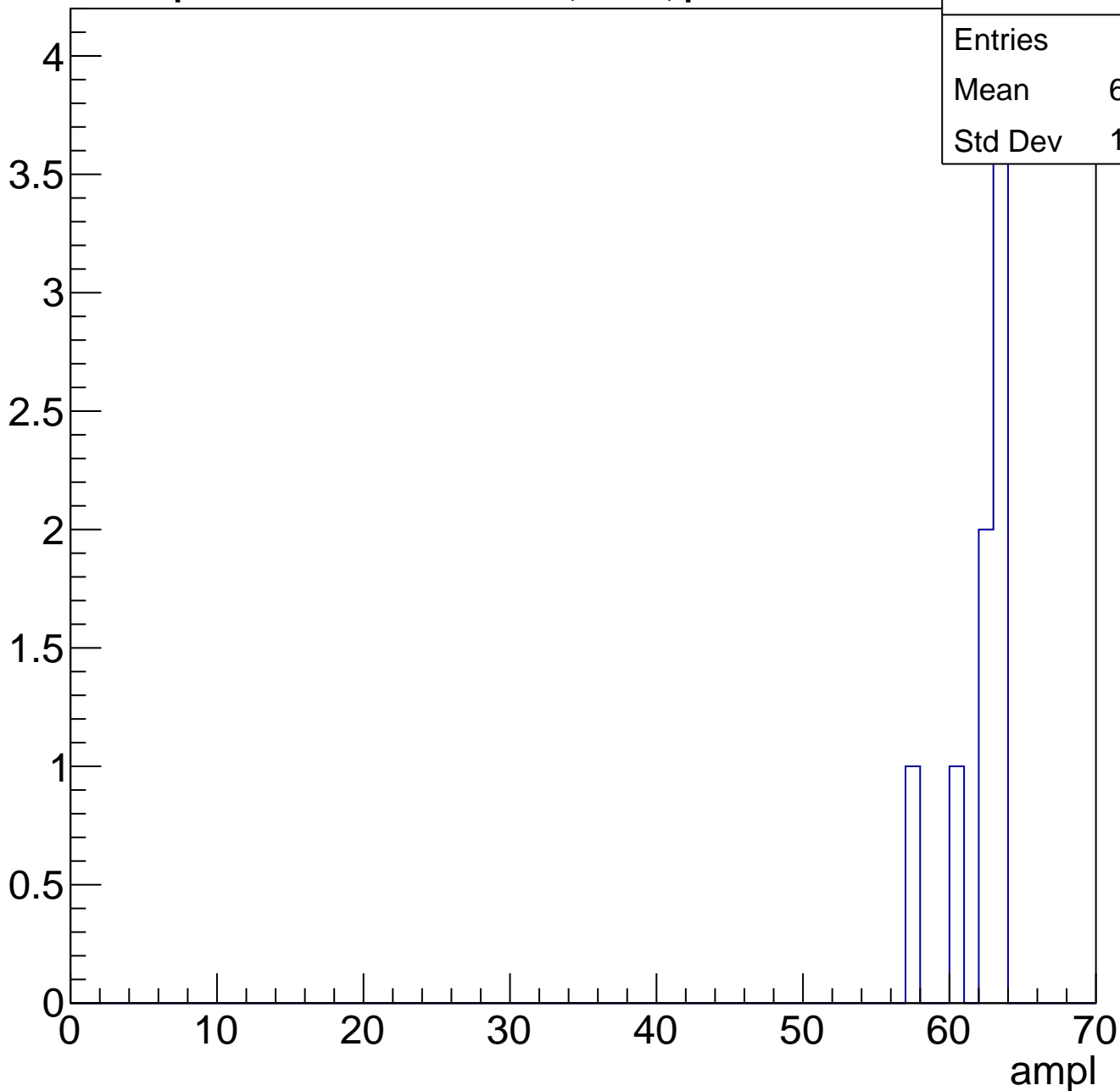
Entries	35
Mean	58.37
Std Dev	10.21



B0L002S, U5-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	8
Mean	61.62
Std Dev	1.996

B0L002S, U5-ch109, adc7

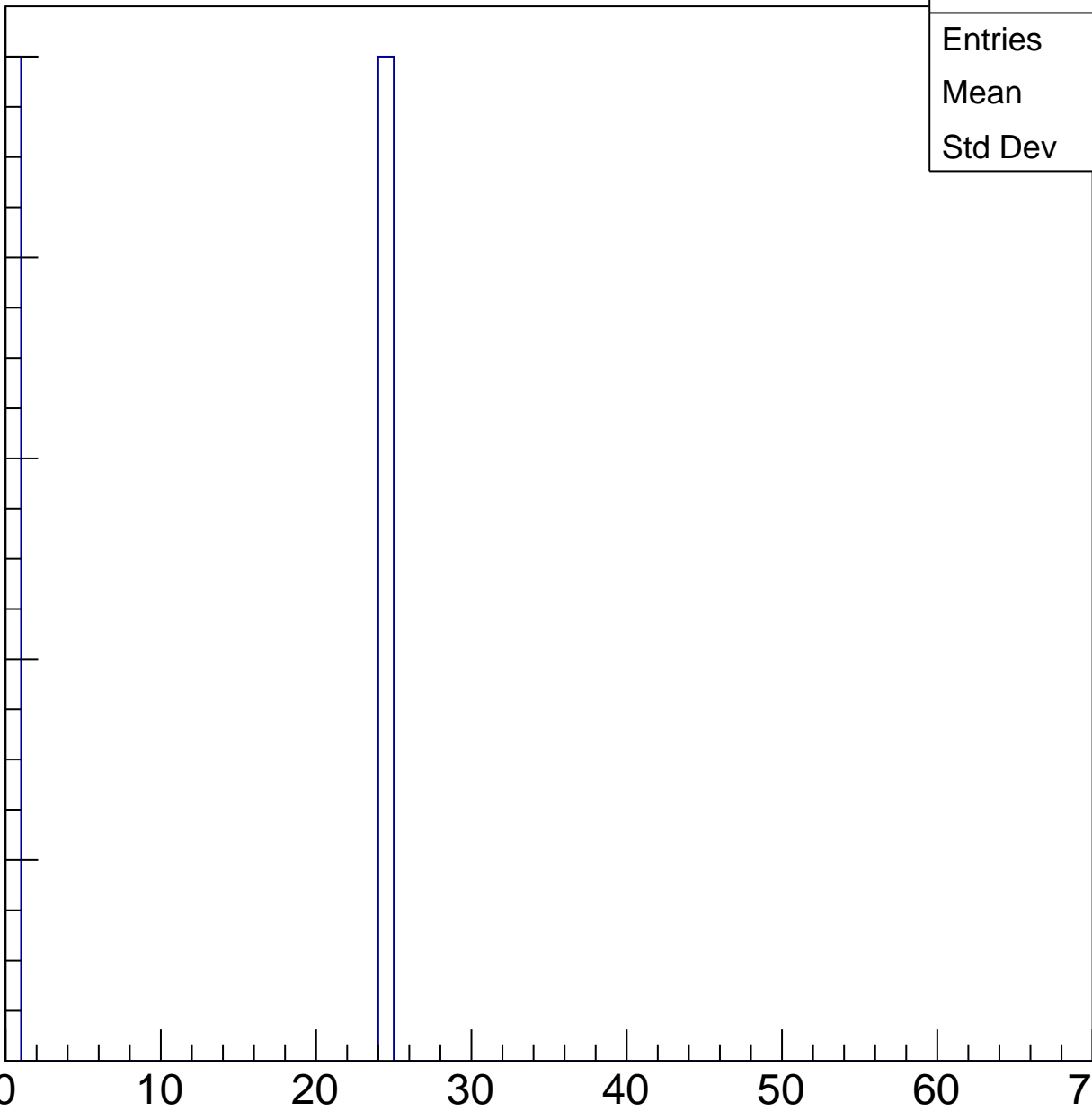
calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl



B0L002S, U5-ch110, adc0

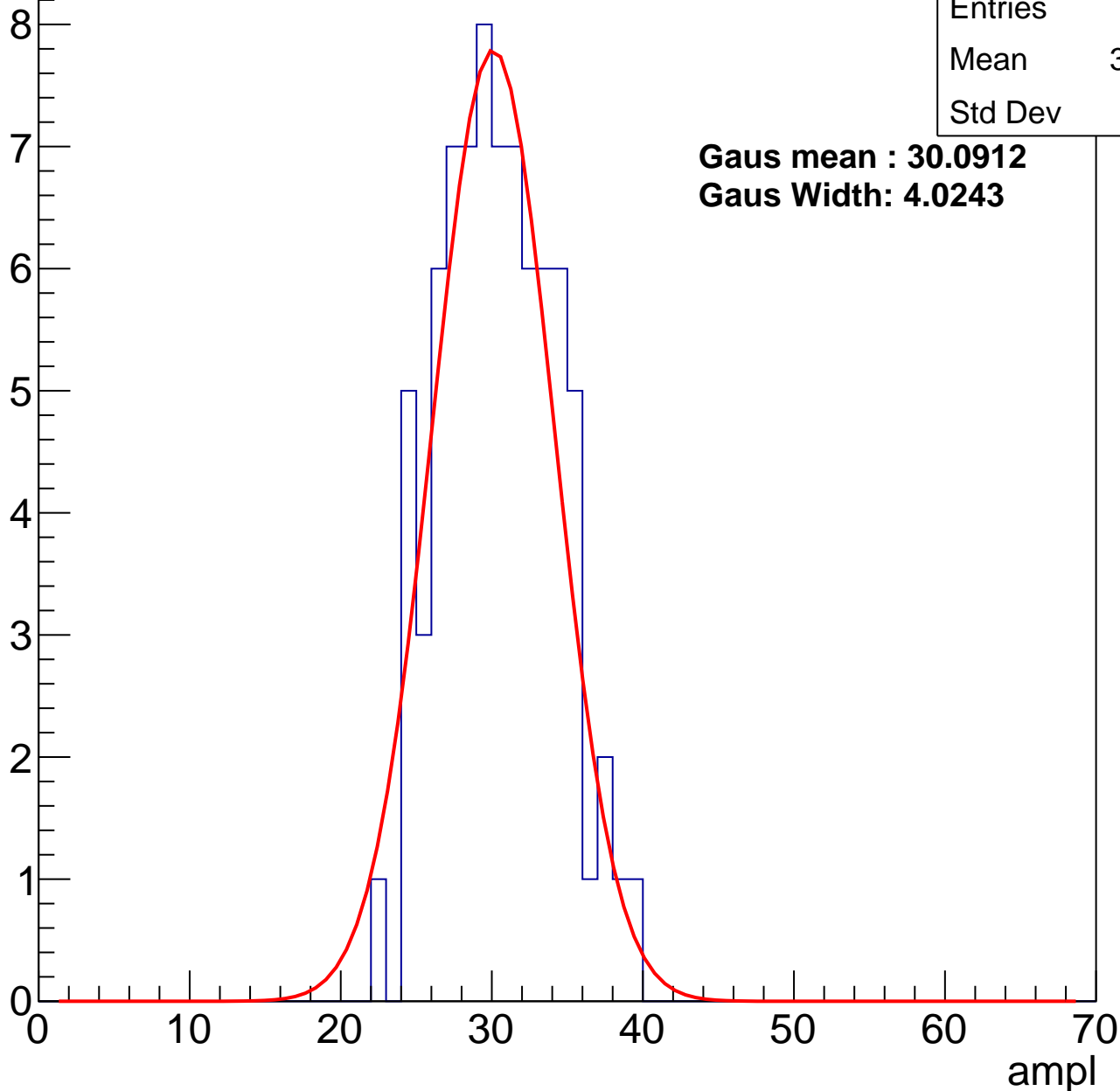
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.04
Std Dev	3.73

Gaus mean : 30.0912

Gaus Width: 4.0243



B0L002S, U5-ch110, adc1

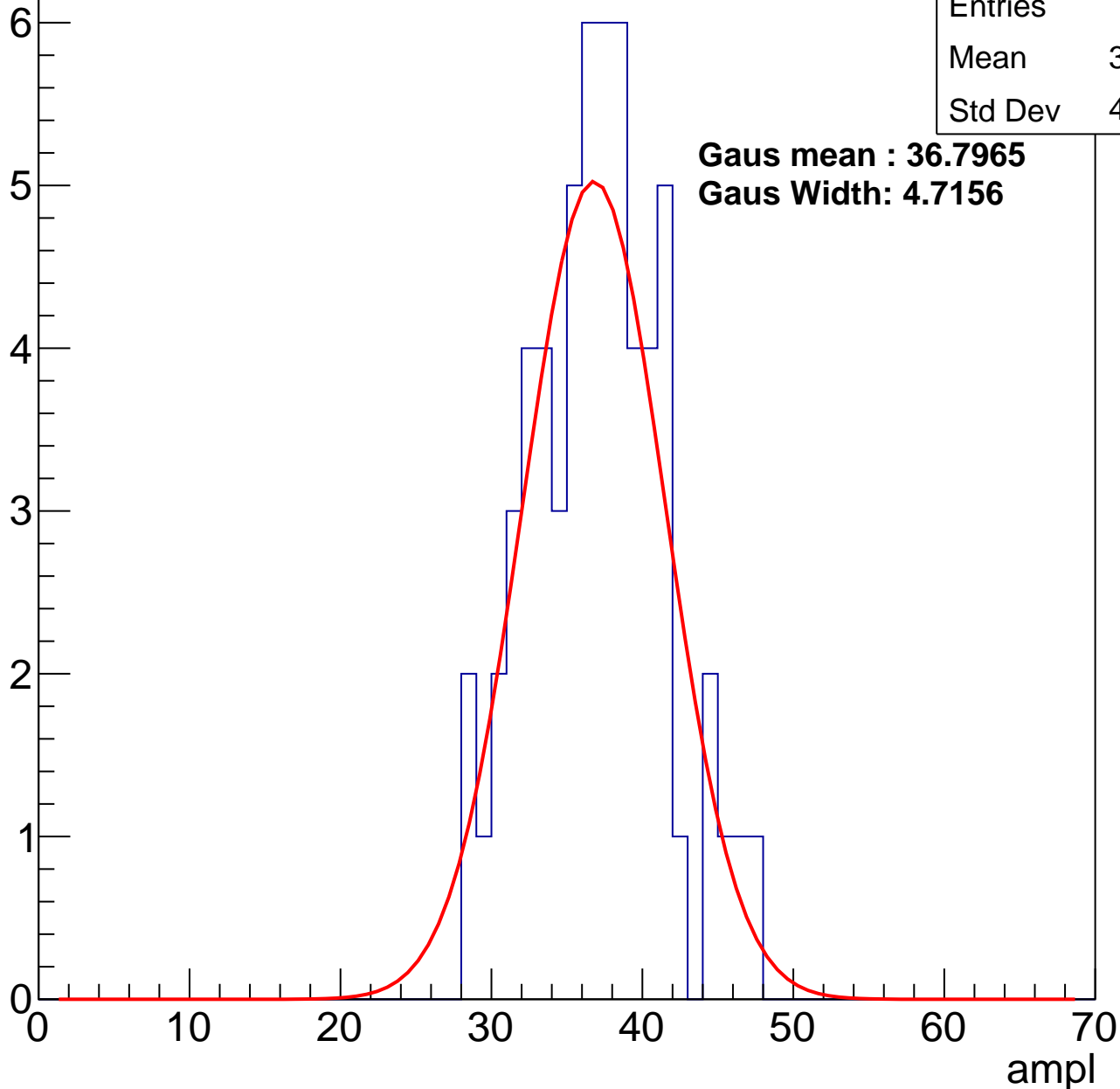
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.56
Std Dev	4.329

Gaus mean : 36.7965

Gaus Width: 4.7156



B0L002S, U5-ch110, adc2

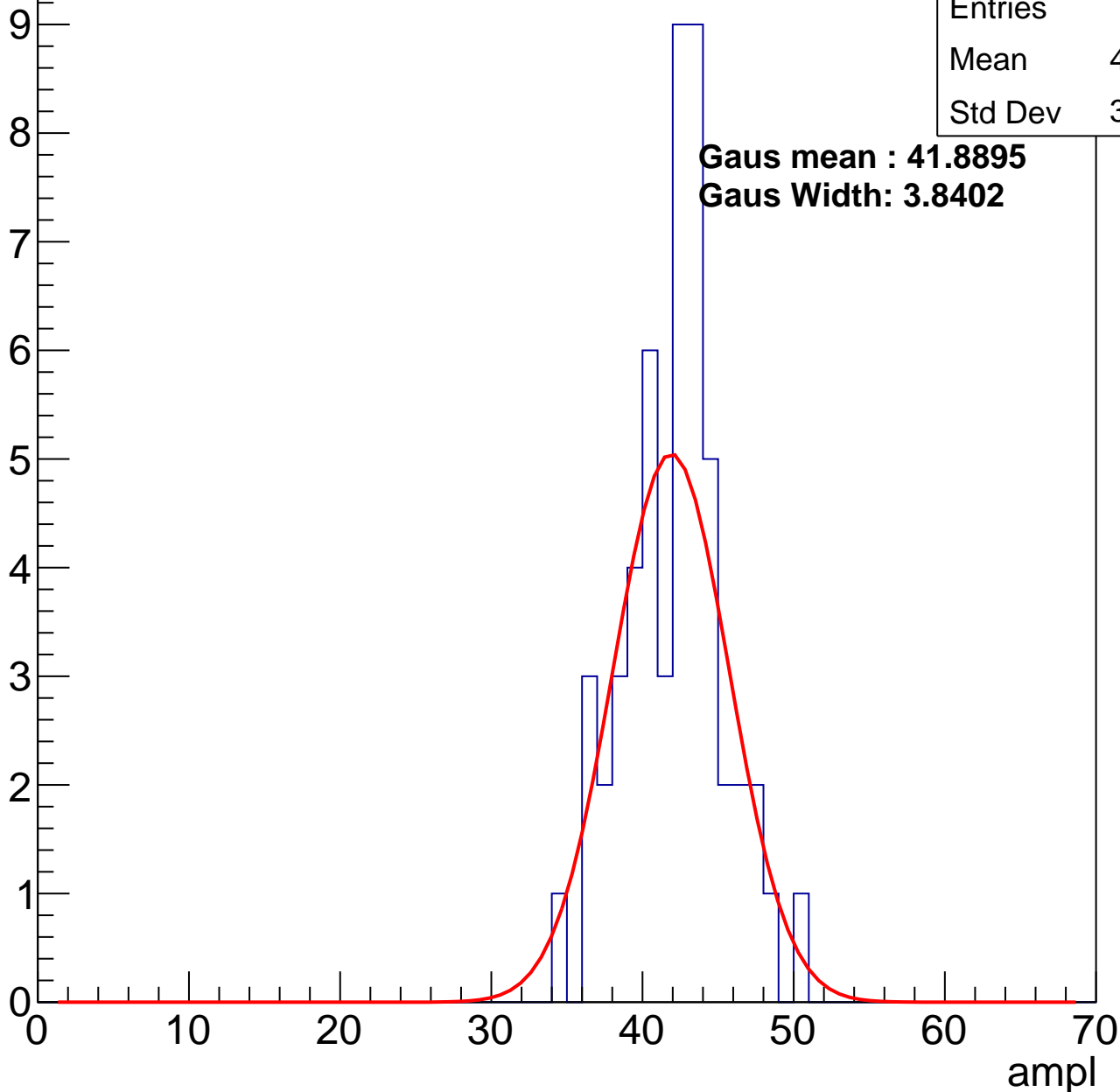
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	41.66
Std Dev	3.239

Gaus mean : 41.8895

Gaus Width: 3.8402



B0L002S, U5-ch110, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	48.57
Std Dev	3.324

Entry

10

8

6

4

2

0

0

10

20

30

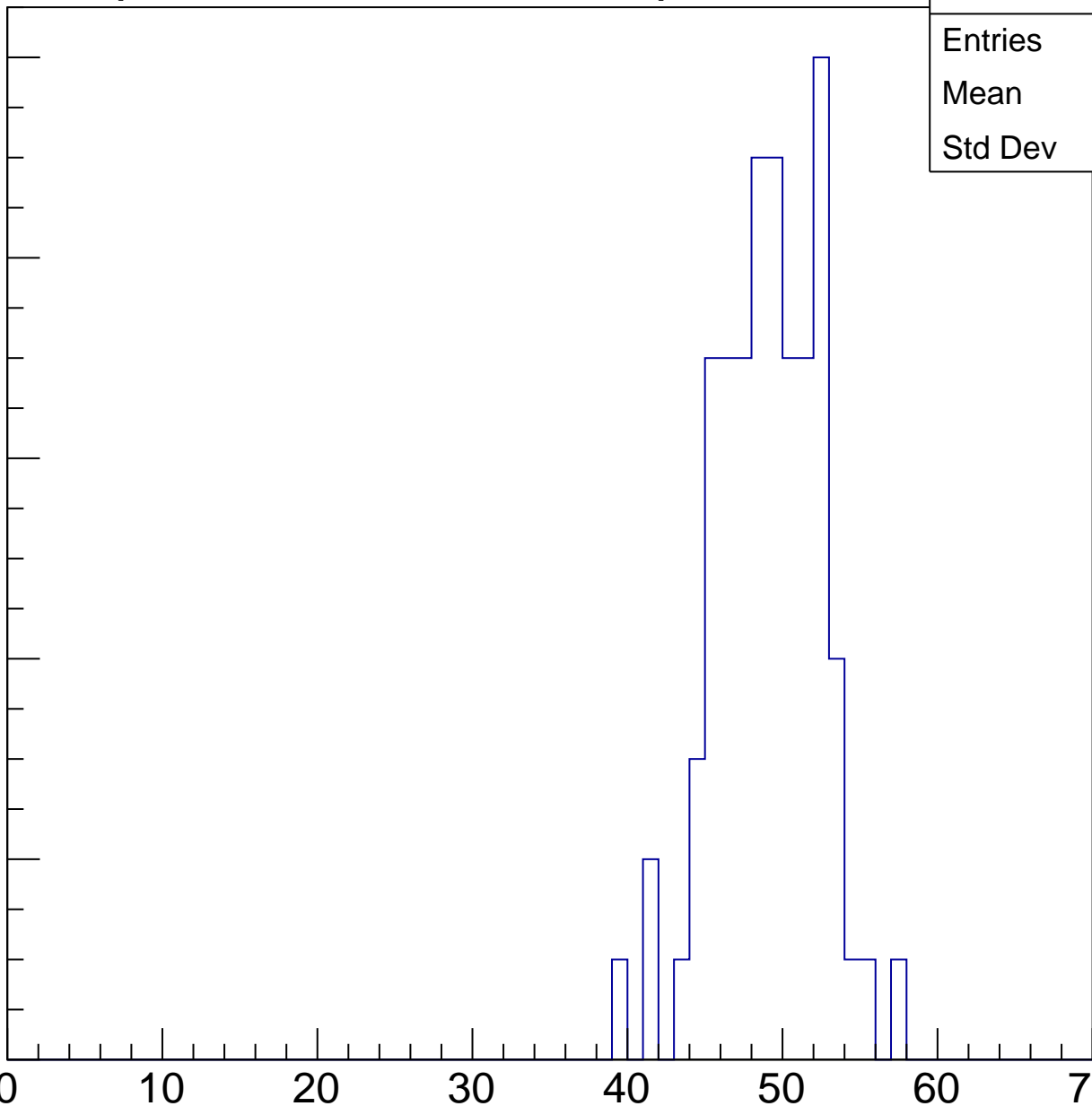
40

50

60

70

ampl

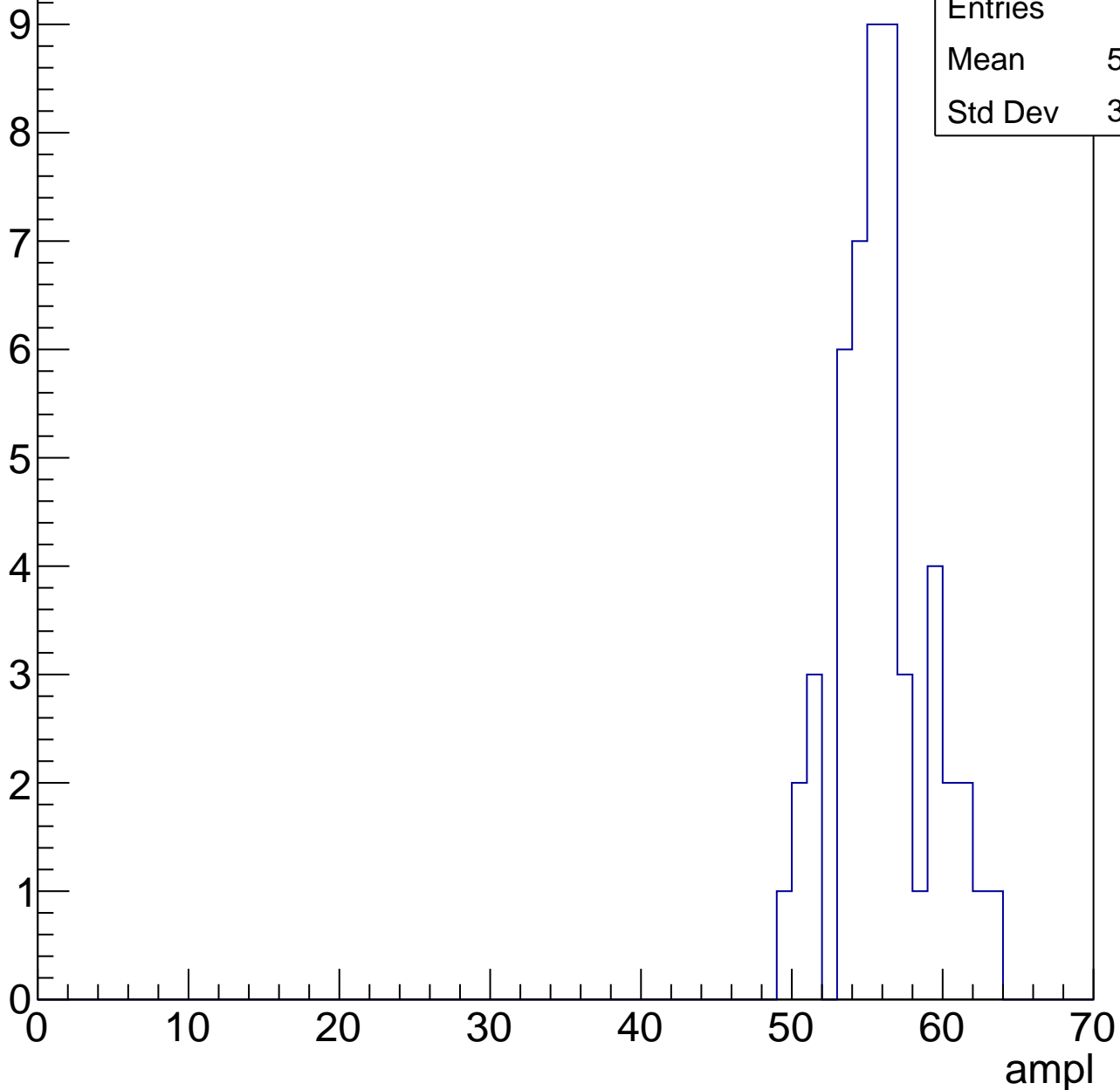


B0L002S, U5-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.47
Std Dev	3.076



B0L002S, U5-ch110, adc5

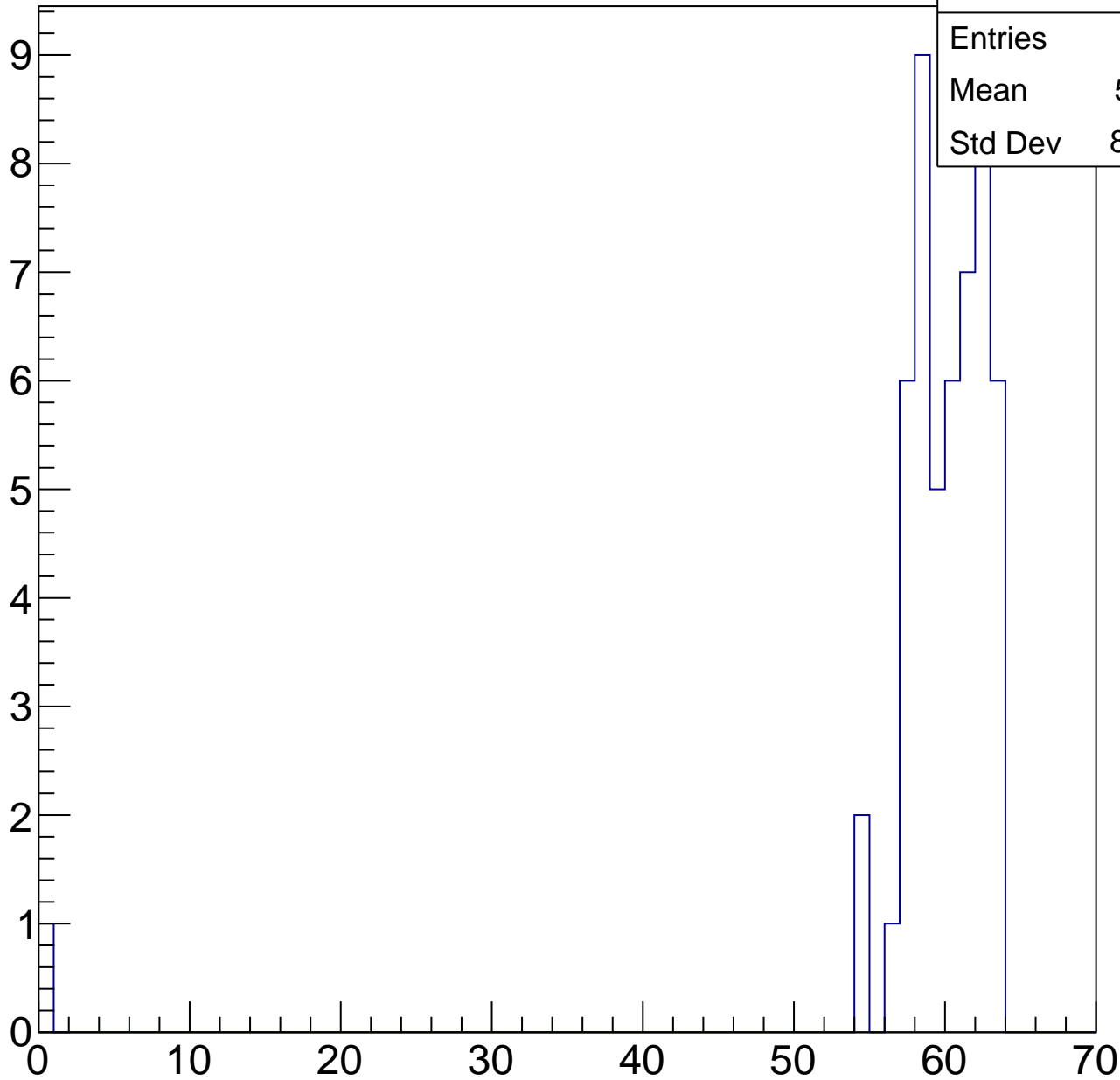
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.51
Std Dev	8.589

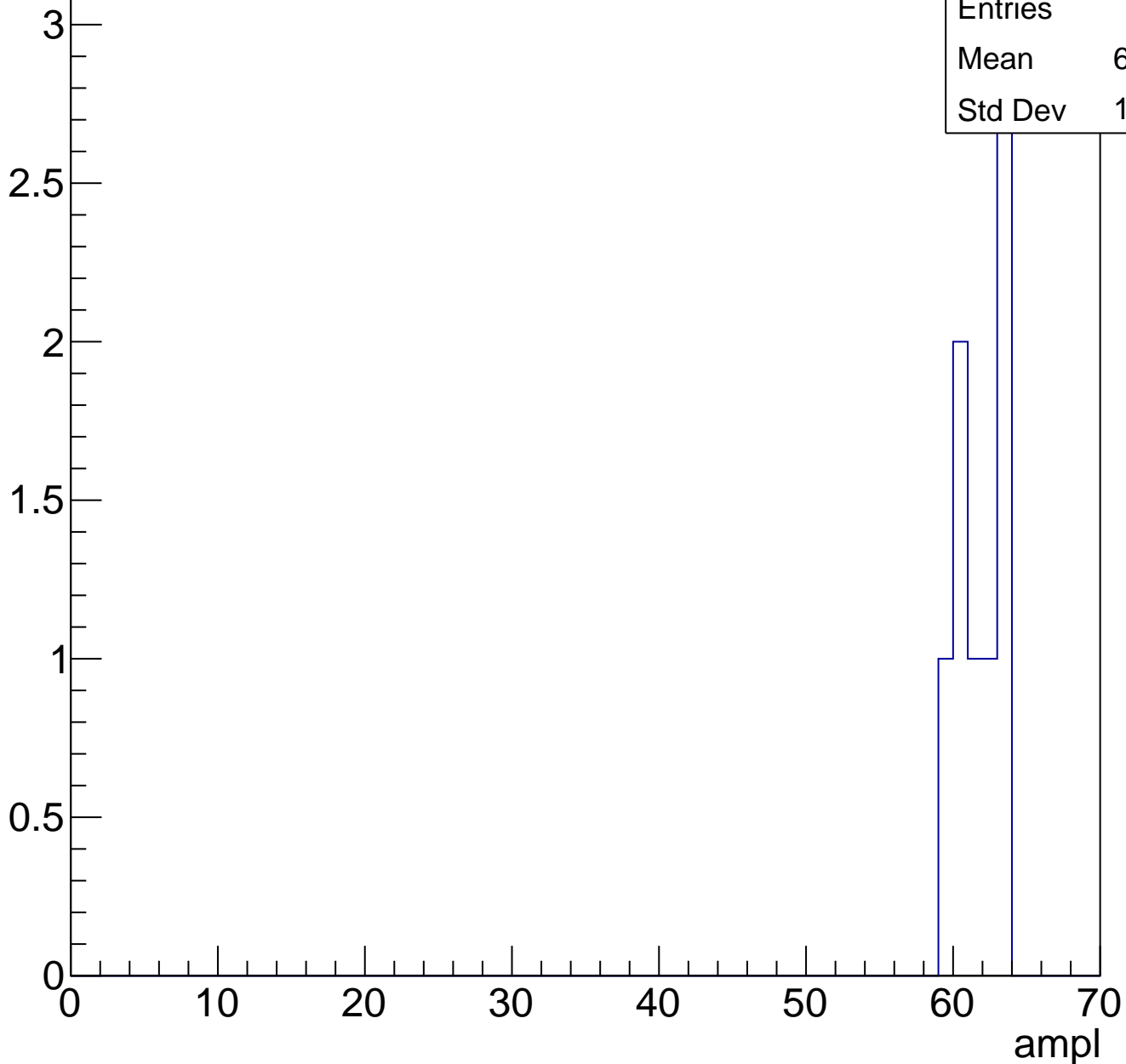
ampl



B0L002S, U5-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch110, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L002S, U5-ch111, adc0

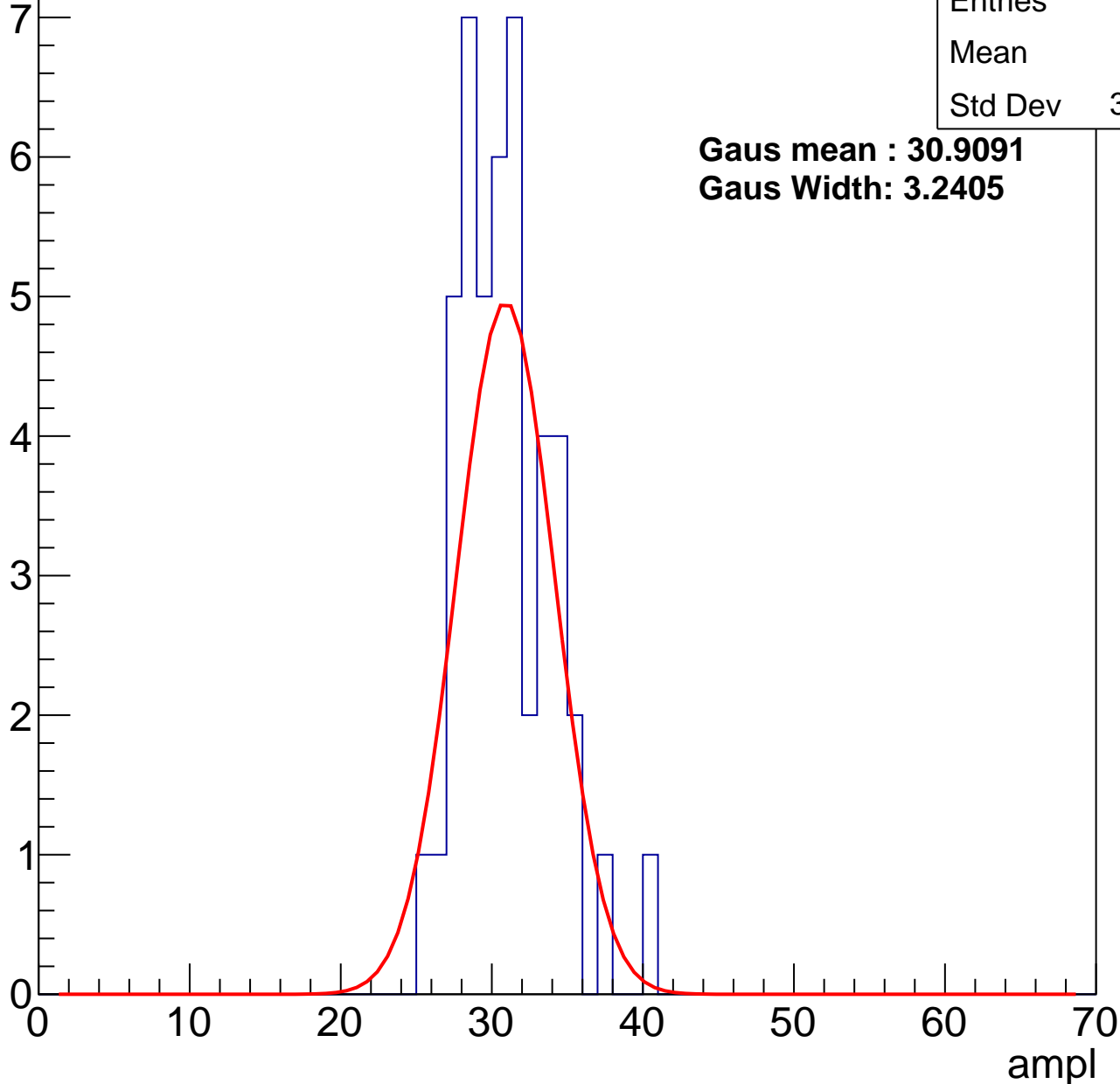
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	30.5
Std Dev	3.027

Gaus mean : 30.9091

Gaus Width: 3.2405



B0L002S, U5-ch111, adc1

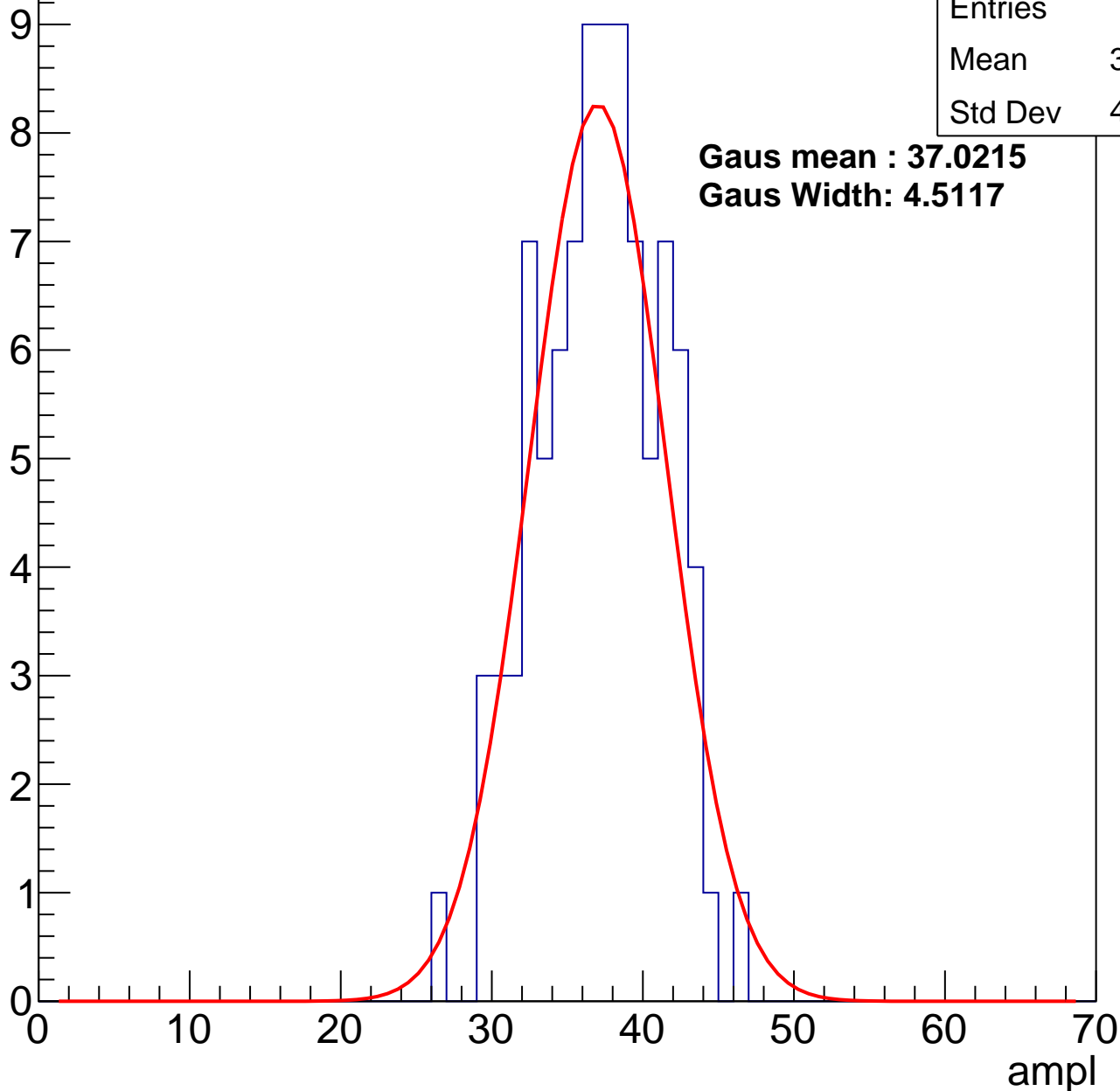
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	93
Mean	36.63
Std Dev	4.042

Gaus mean : 37.0215

Gaus Width: 4.5117



B0L002S, U5-ch111, adc2

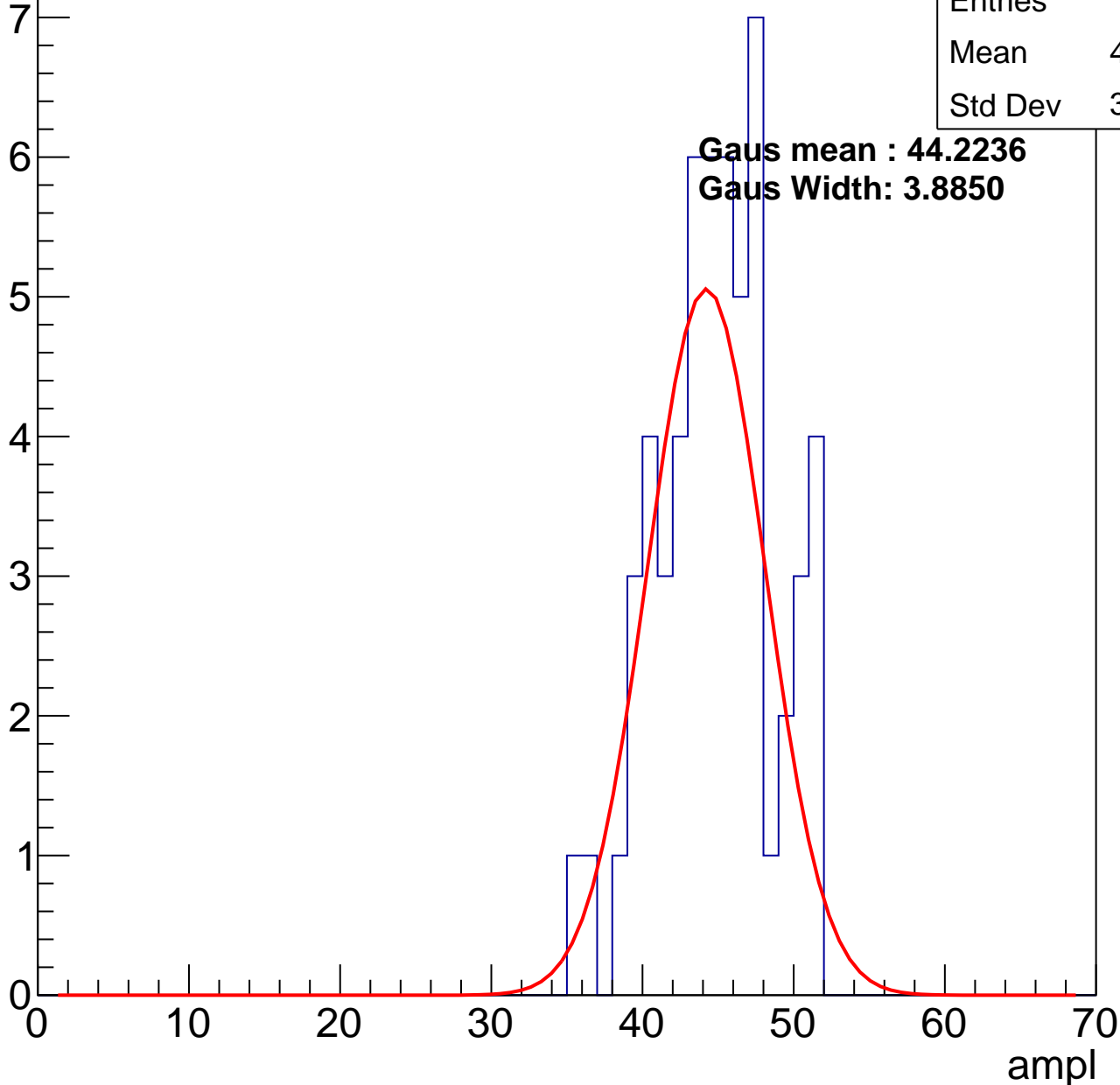
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.35
Std Dev	3.795

Gaus mean : 44.2236

Gaus Width: 3.8850

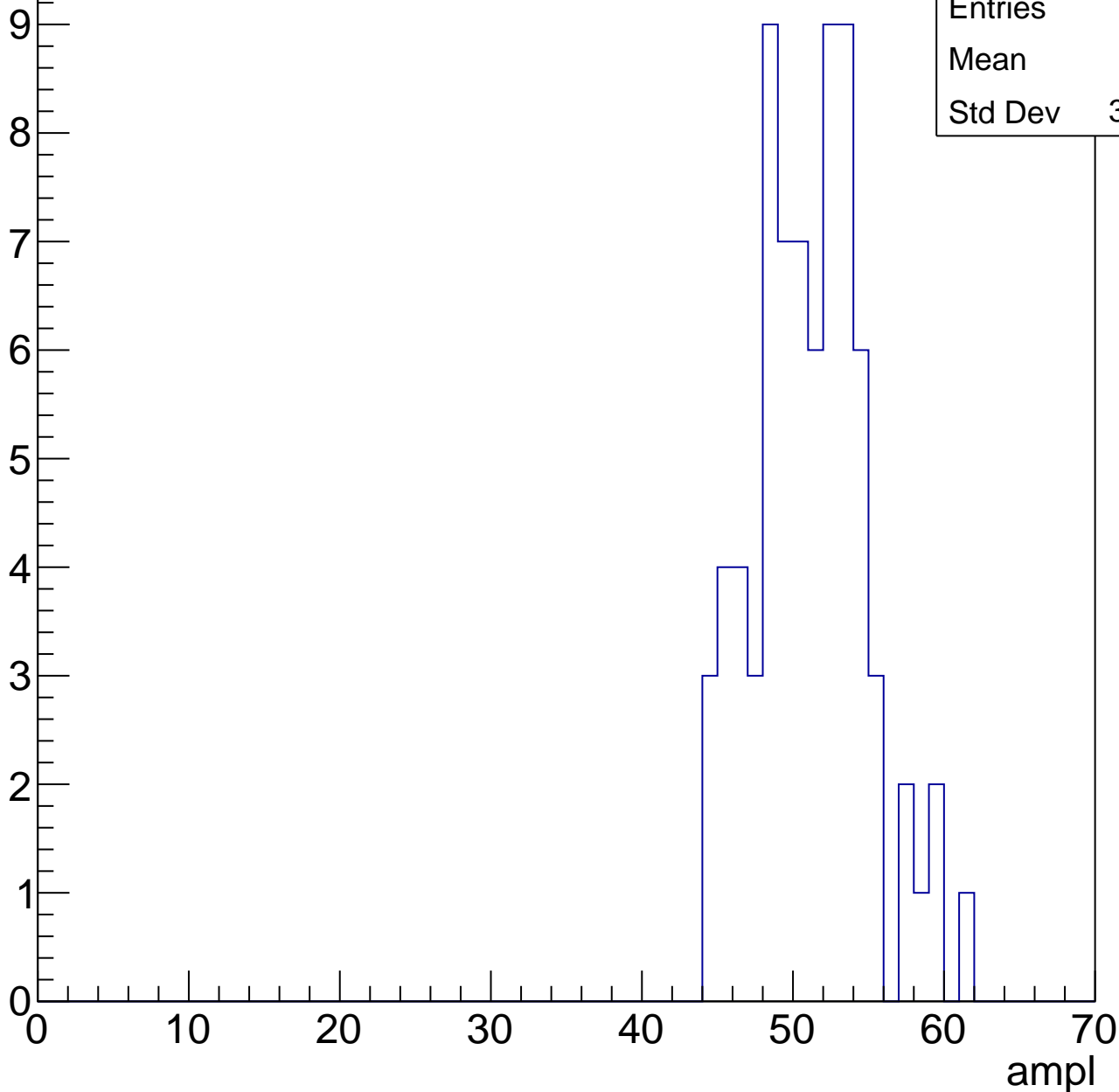


B0L002S, U5-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	50.7
Std Dev	3.706

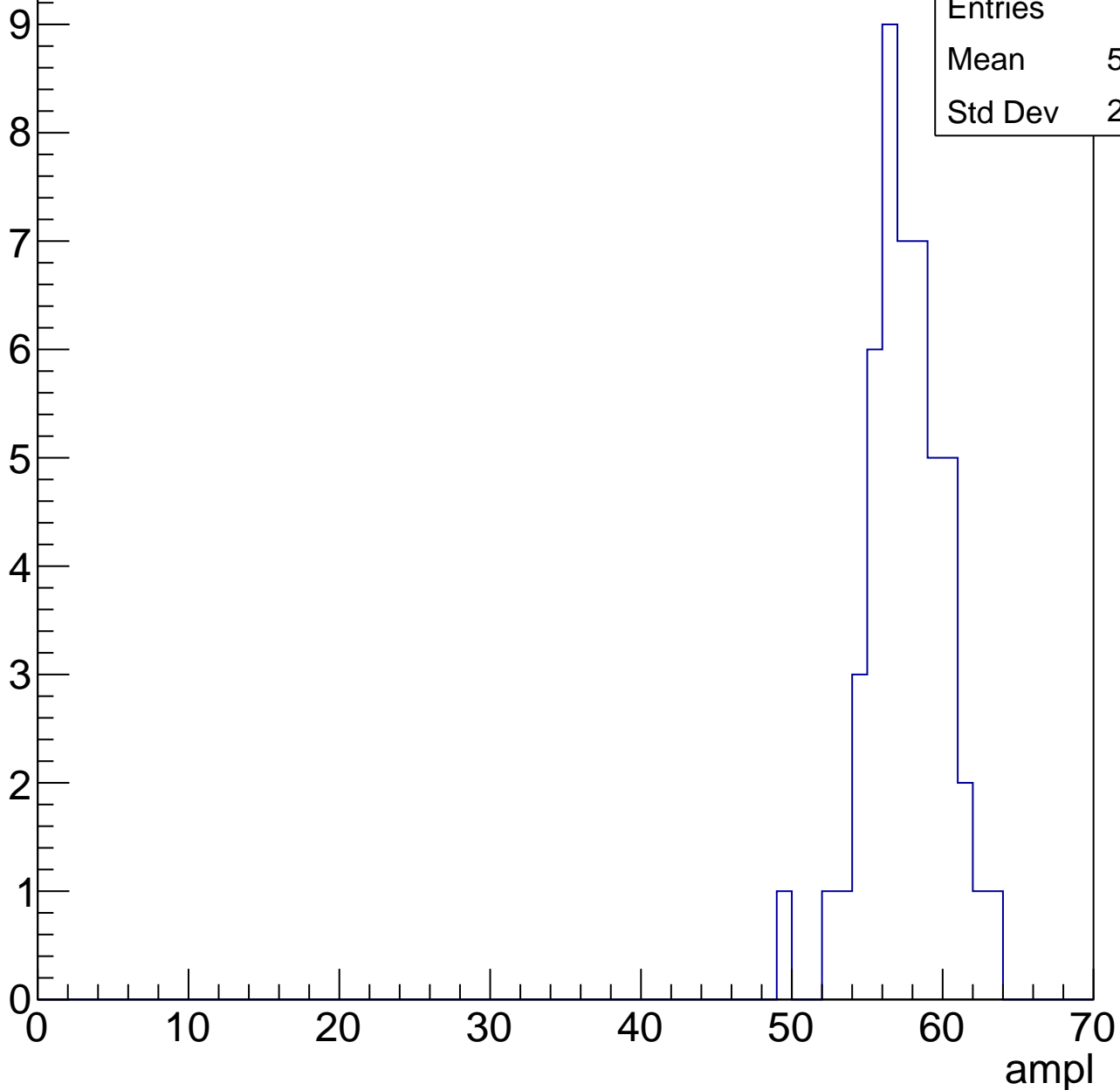


B0L002S, U5-ch111, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	57.08
Std Dev	2.594

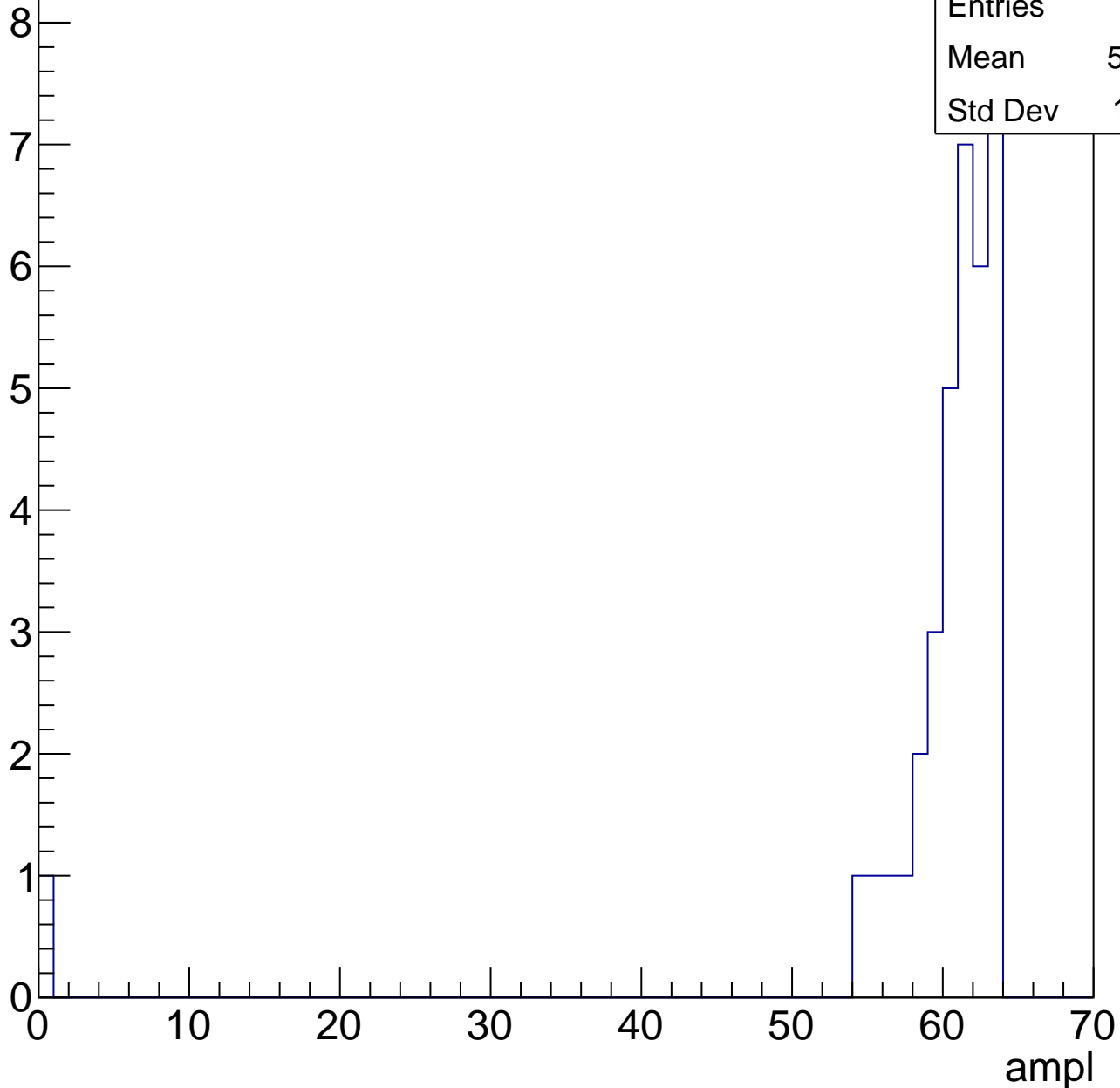


B0L002S, U5-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	58.83
Std Dev	10.21



B0L002S, U5-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

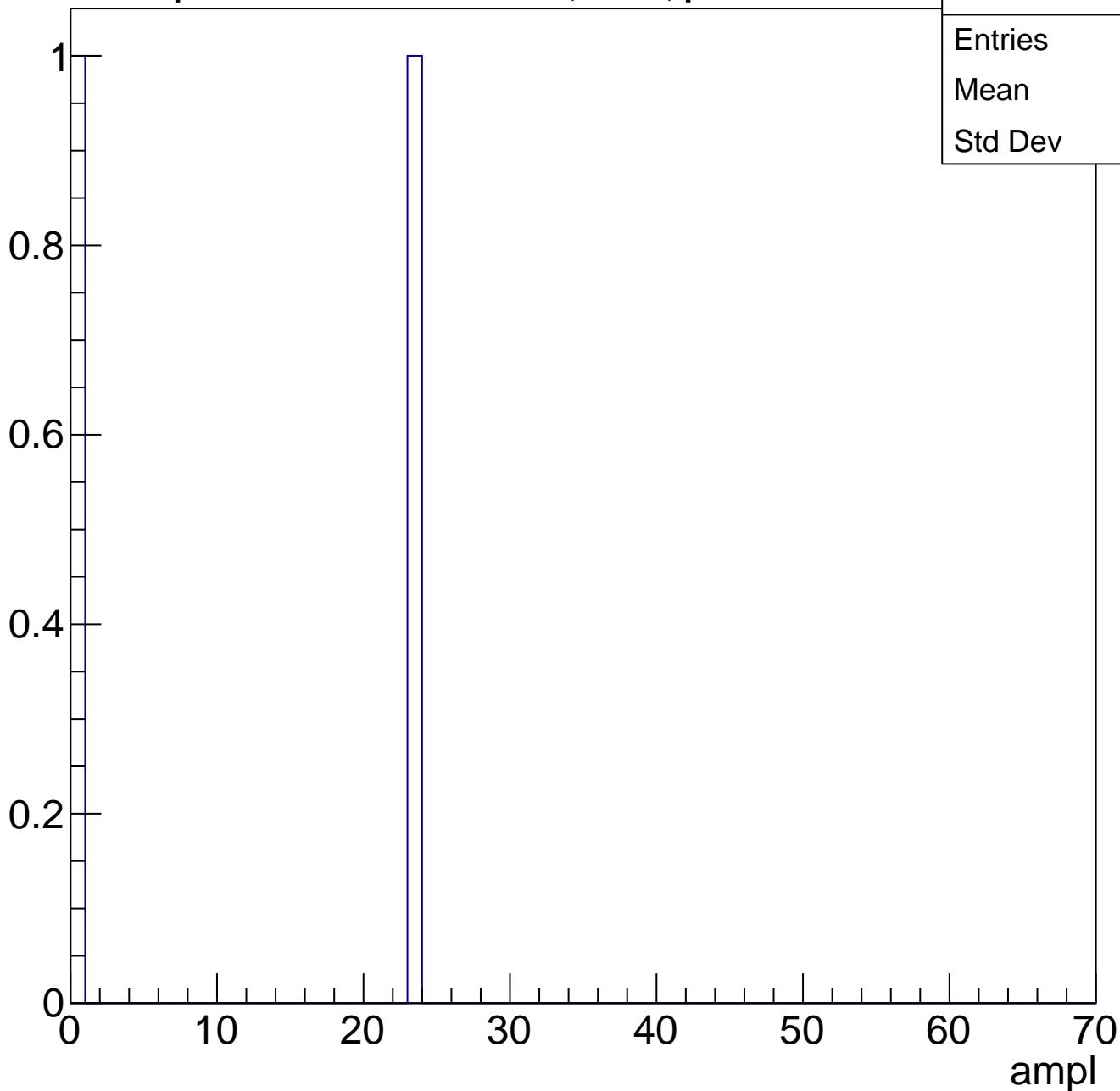
Entry



B0L002S, U5-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U5-ch112, adc0

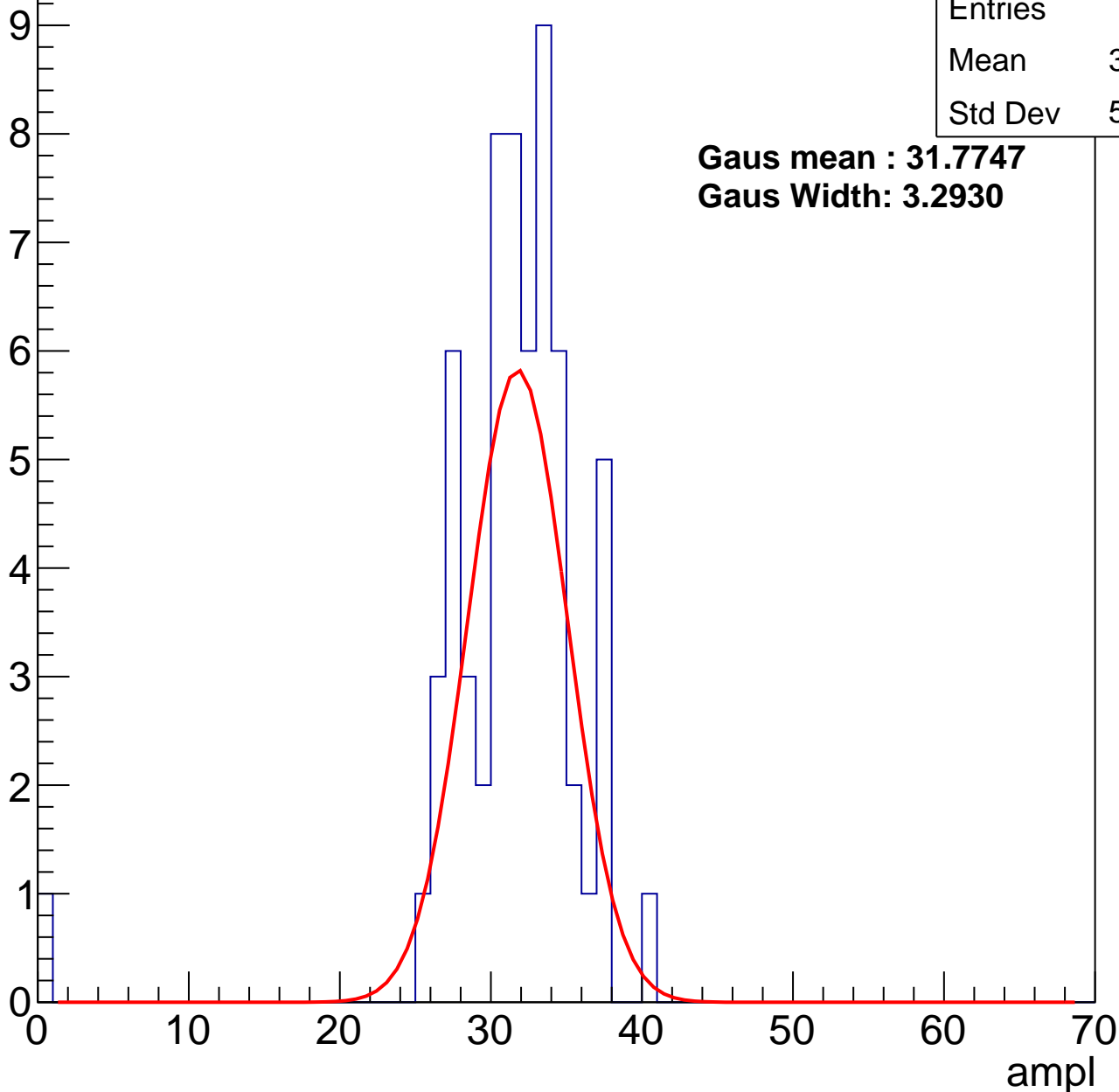
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.95
Std Dev	5.119

Gaus mean : 31.7747

Gaus Width: 3.2930



B0L002S, U5-ch112, adc1

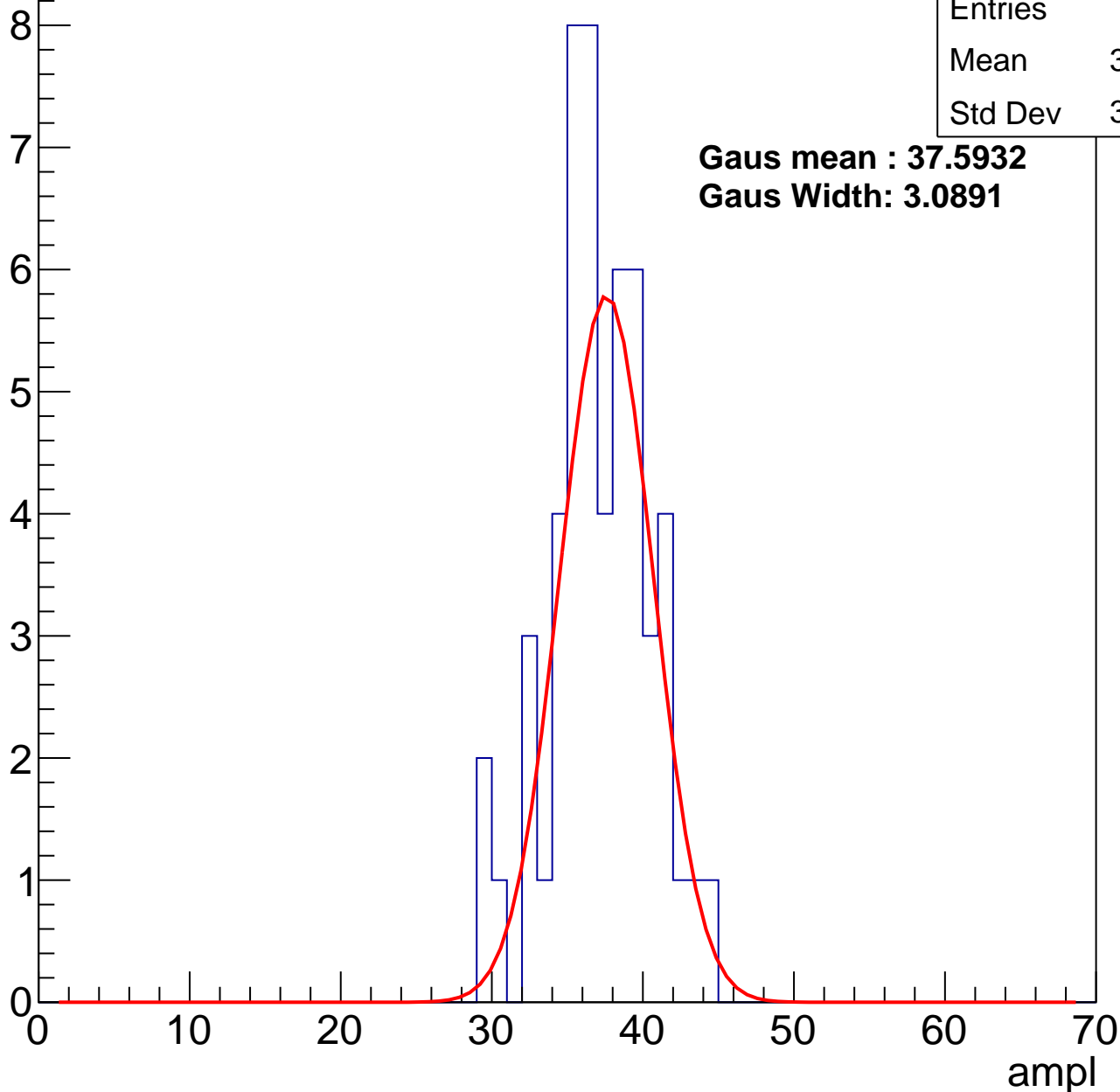
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	36.68
Std Dev	3.278

Gaus mean : 37.5932

Gaus Width: 3.0891



B0L002S, U5-ch112, adc2

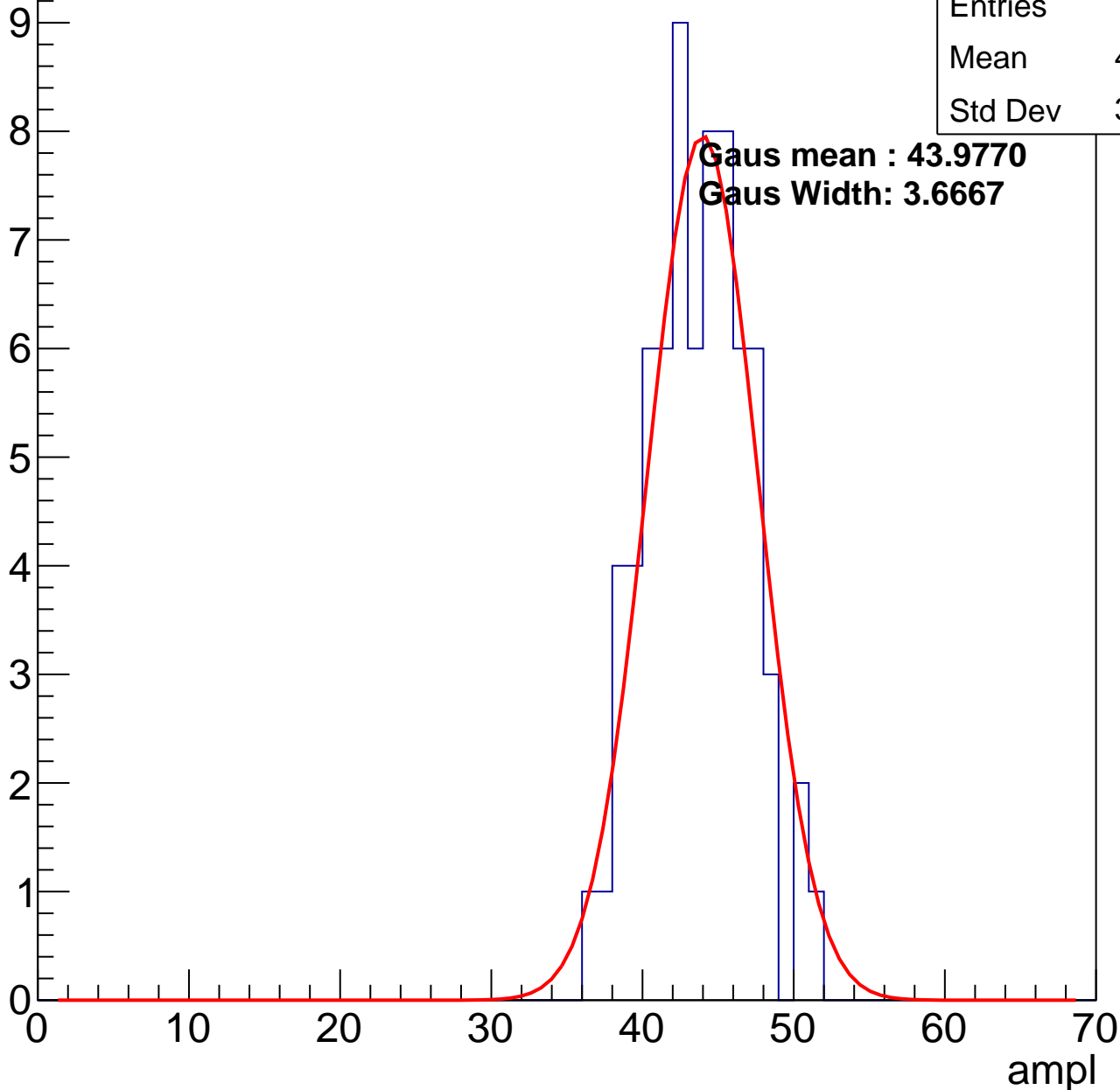
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.21
Std Dev	3.271

Gaus mean : 43.9770

Gaus Width: 3.6667

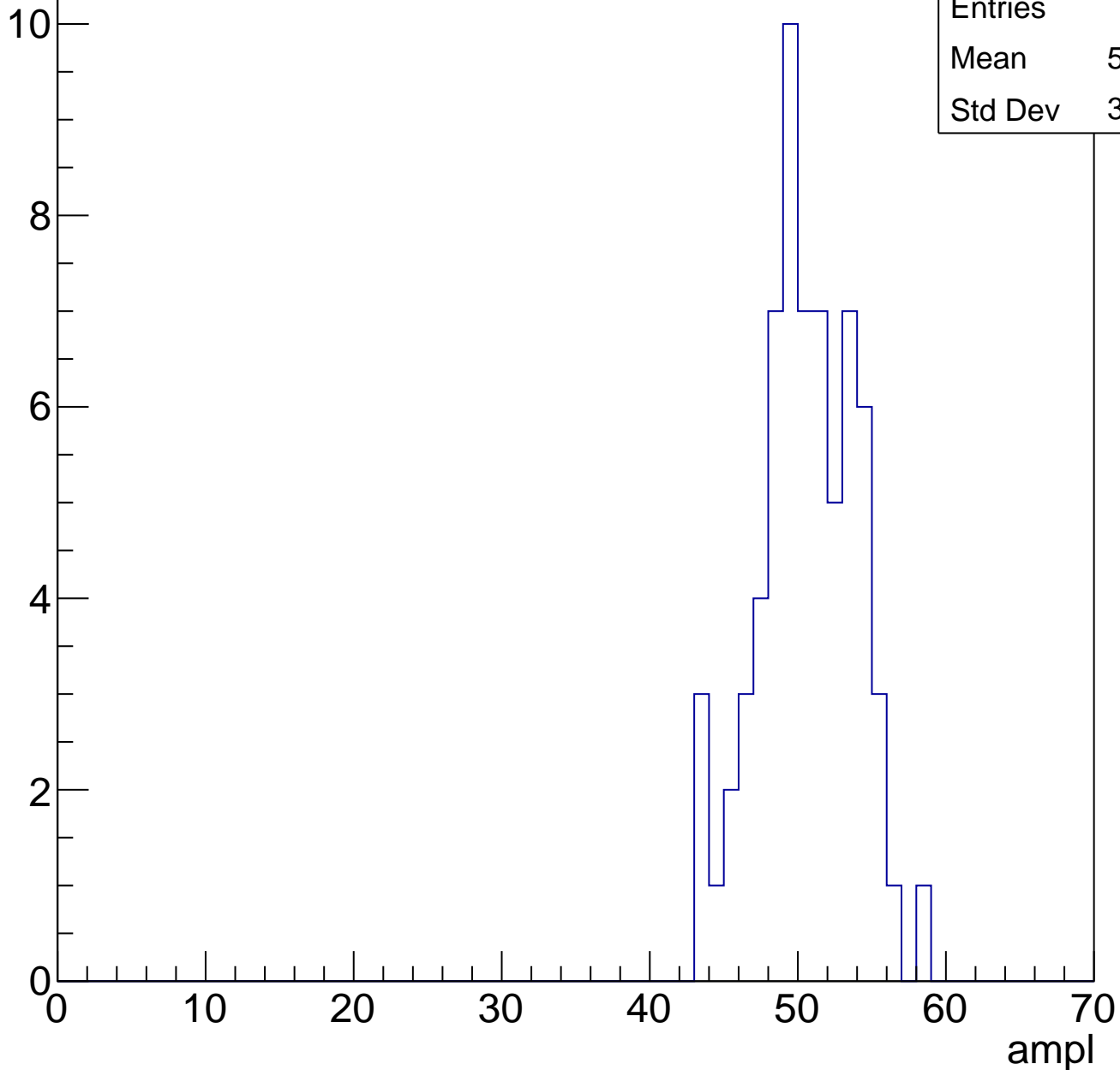


B0L002S, U5-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	50.09
Std Dev	3.268

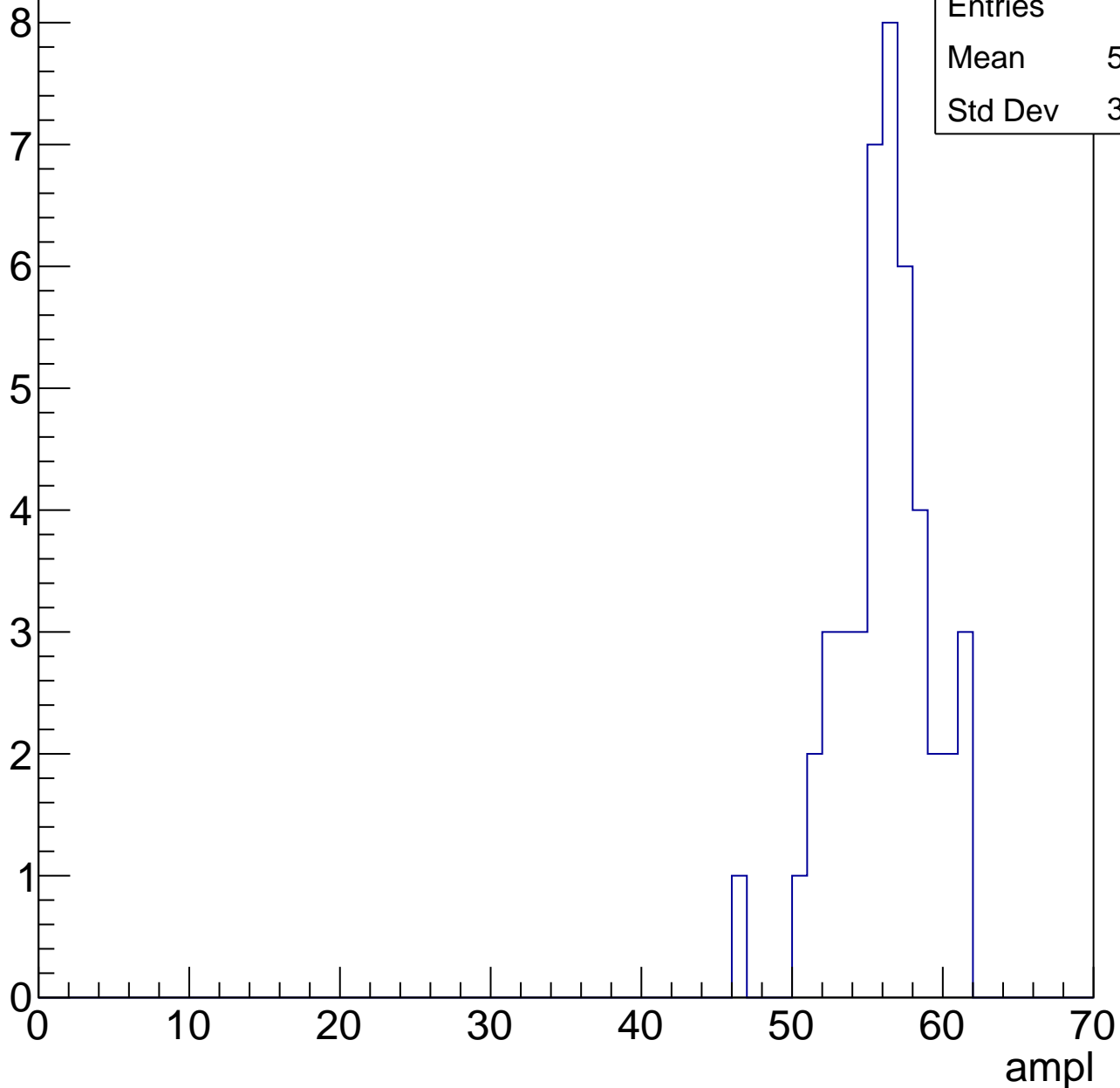
Entry



B0L002S, U5-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	45
Mean	55.62
Std Dev	3.057

B0L002S, U5-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	54
Mean	58.74
Std Dev	8.464

Entry

10

8

6

4

2

0

0

10

20

30

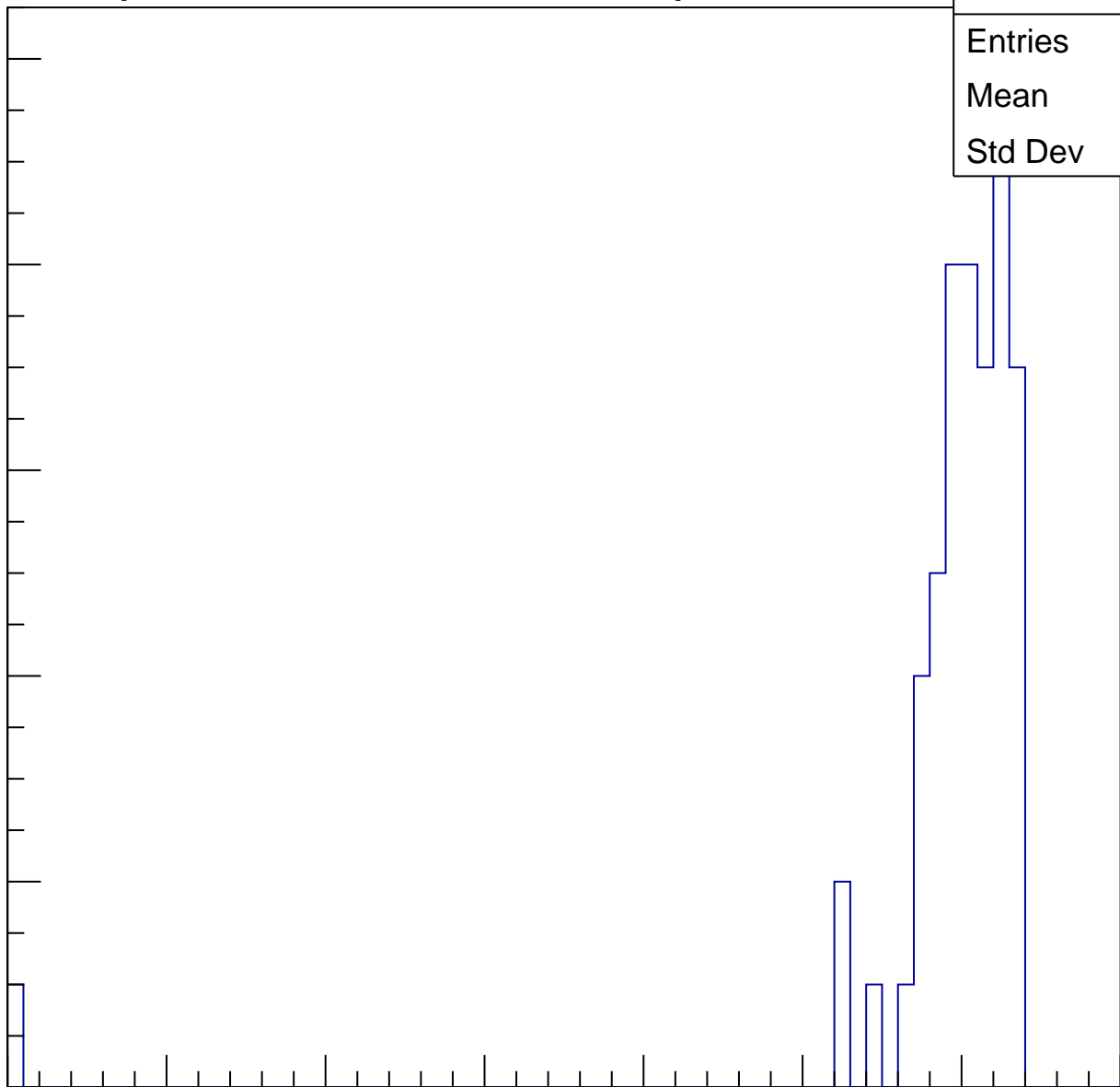
40

50

60

70

ampl



B0L002S, U5-ch112, adc6

calib_packv5_042523_0143.root, FC#8, port C1

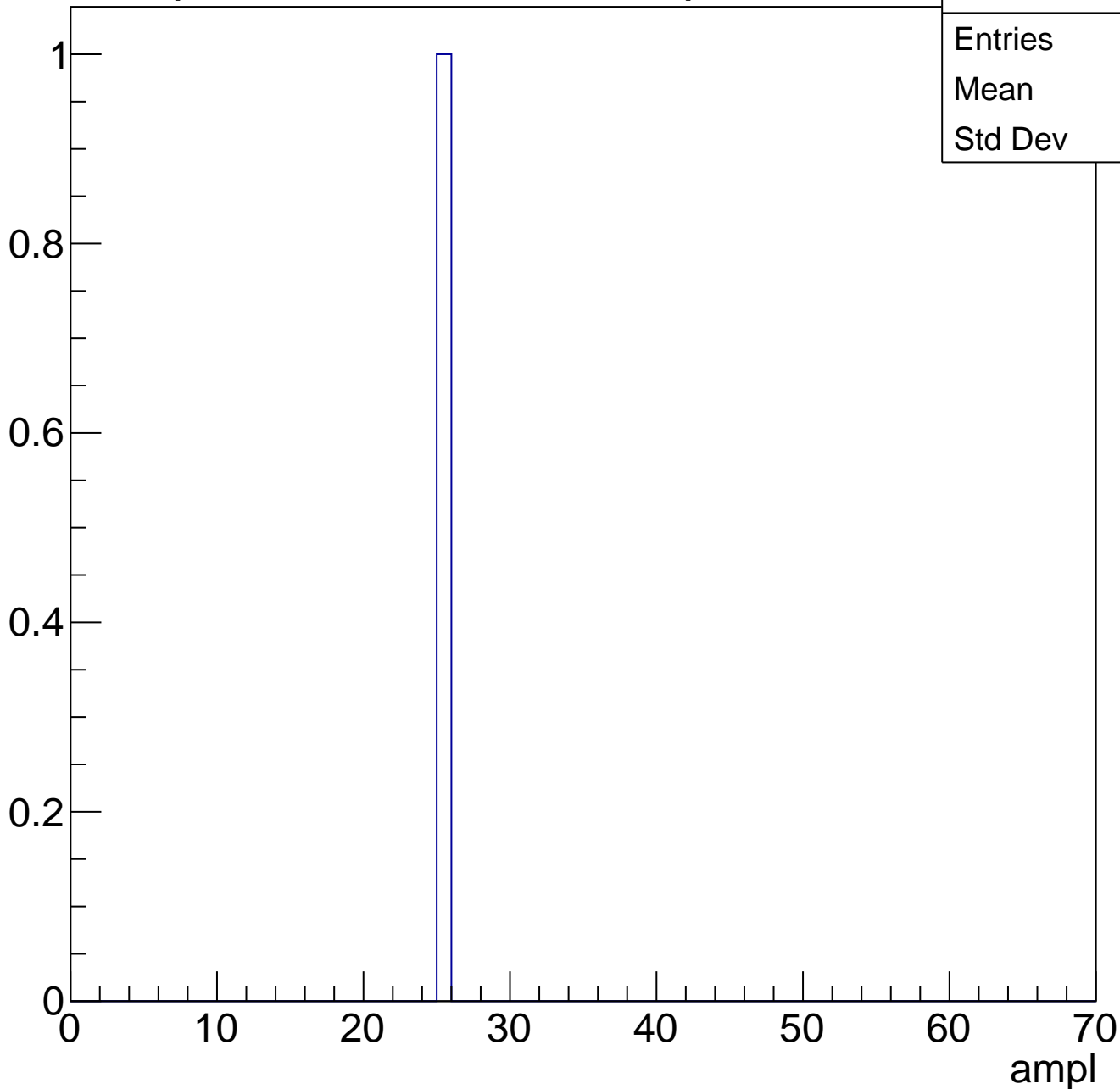
Entry



B0L002S, U5-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	25
Std Dev	0

B0L002S, U5-ch113, adc0

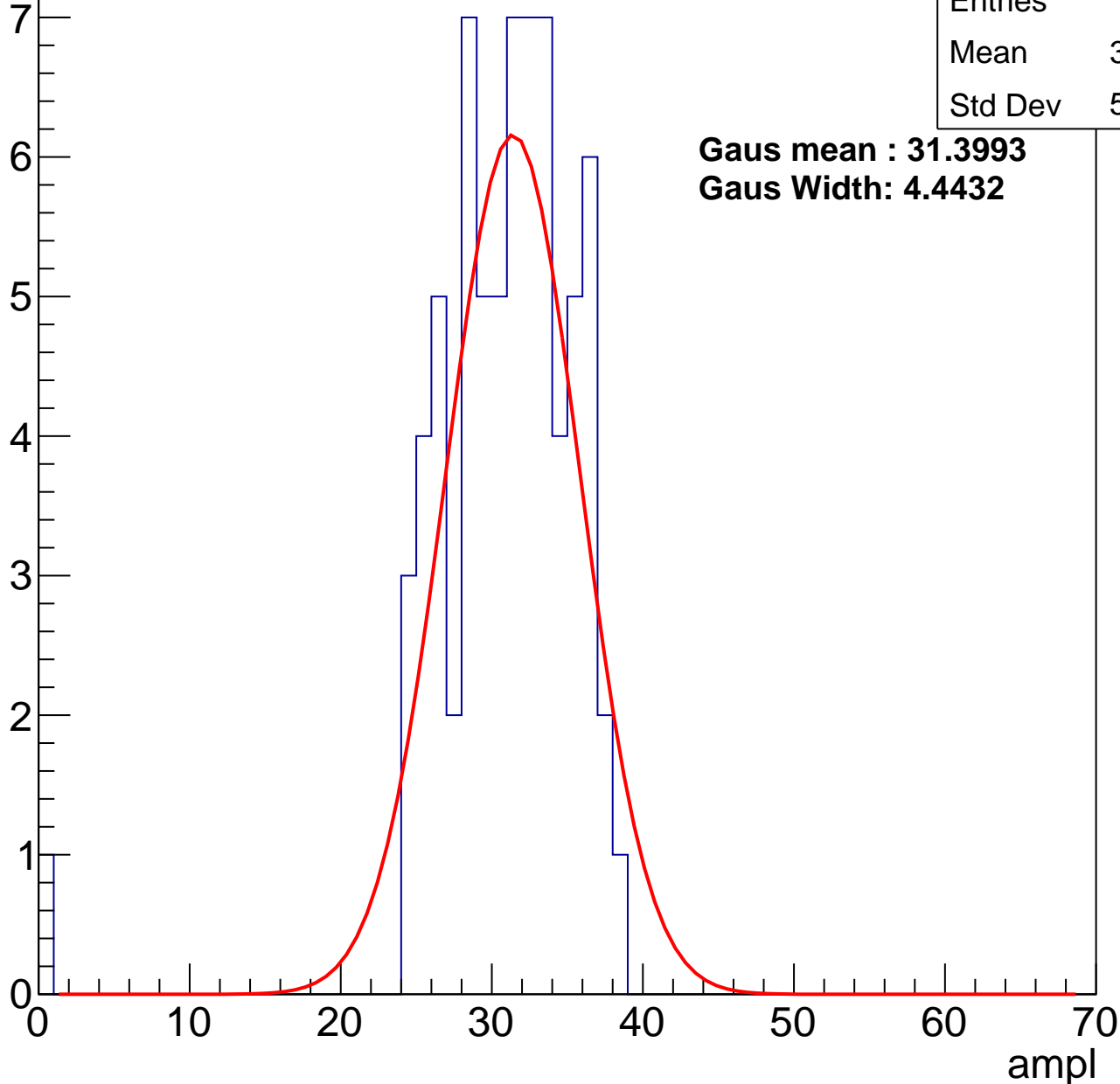
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.39
Std Dev	5.163

Gaus mean : 31.3993

Gaus Width: 4.4432



B0L002S, U5-ch113, adc1

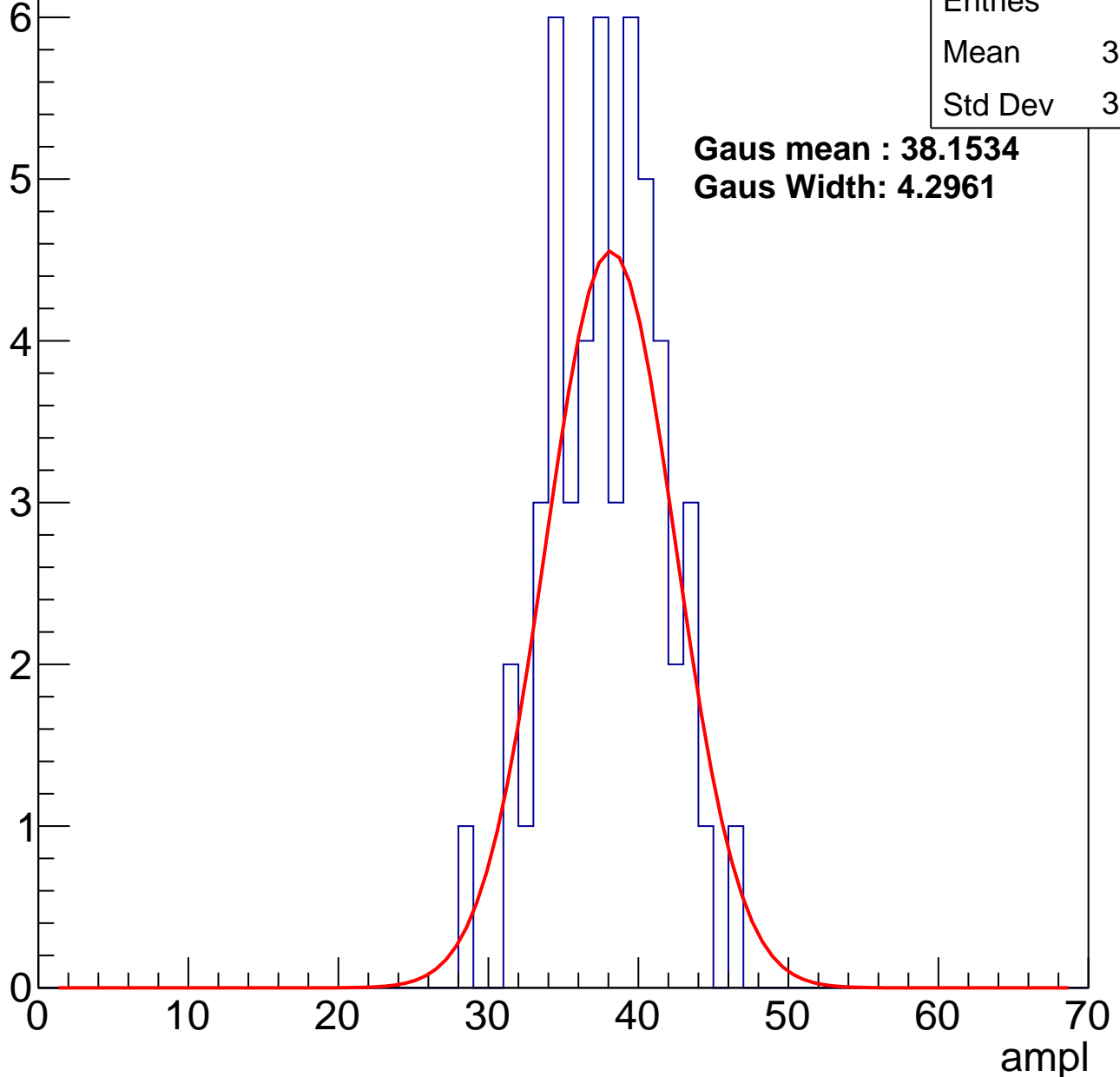
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	37.47
Std Dev	3.728

Gaus mean : 38.1534

Gaus Width: 4.2961



B0L002S, U5-ch113, adc2

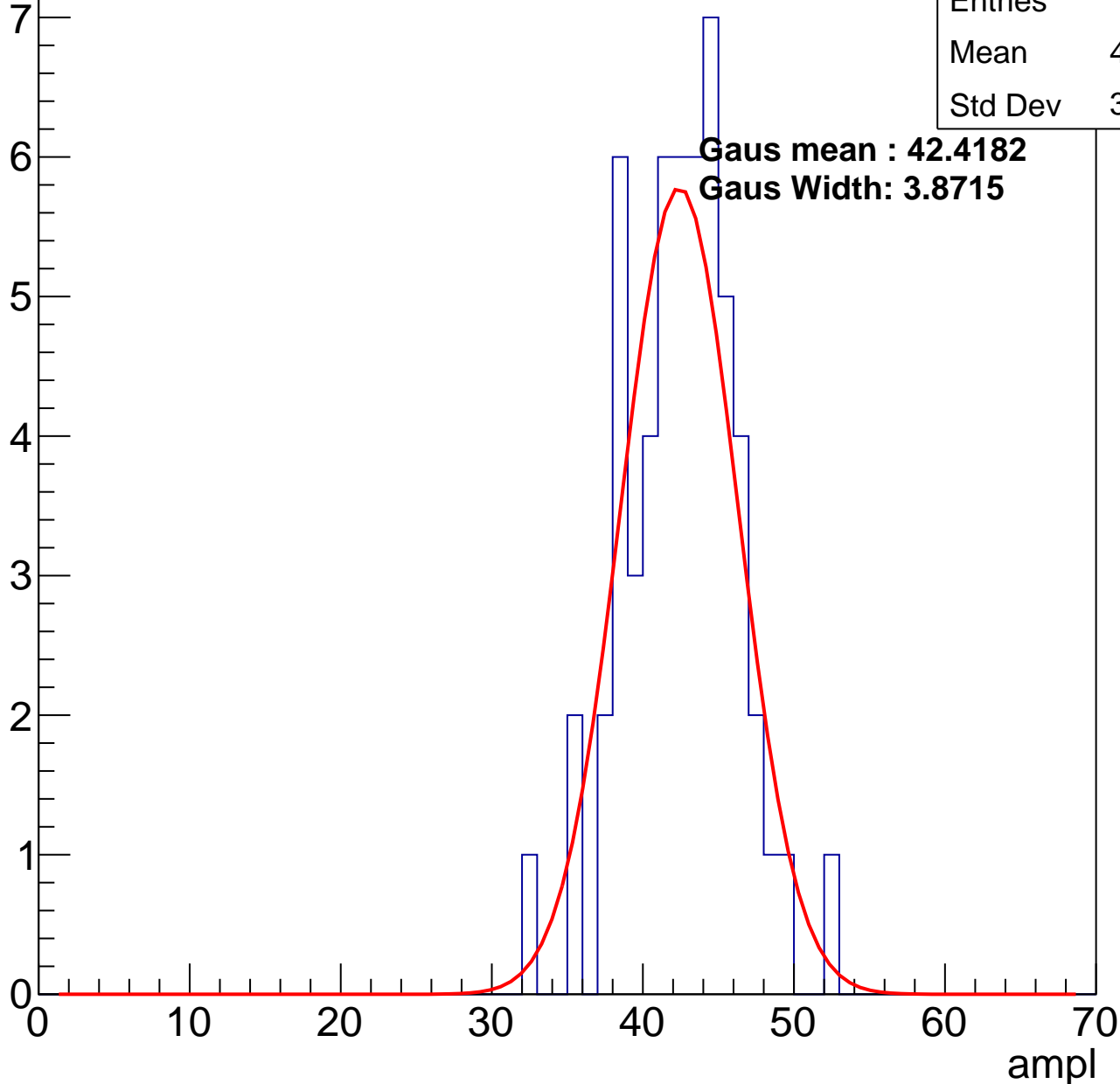
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.05
Std Dev	3.677

Gaus mean : 42.4182

Gaus Width: 3.8715

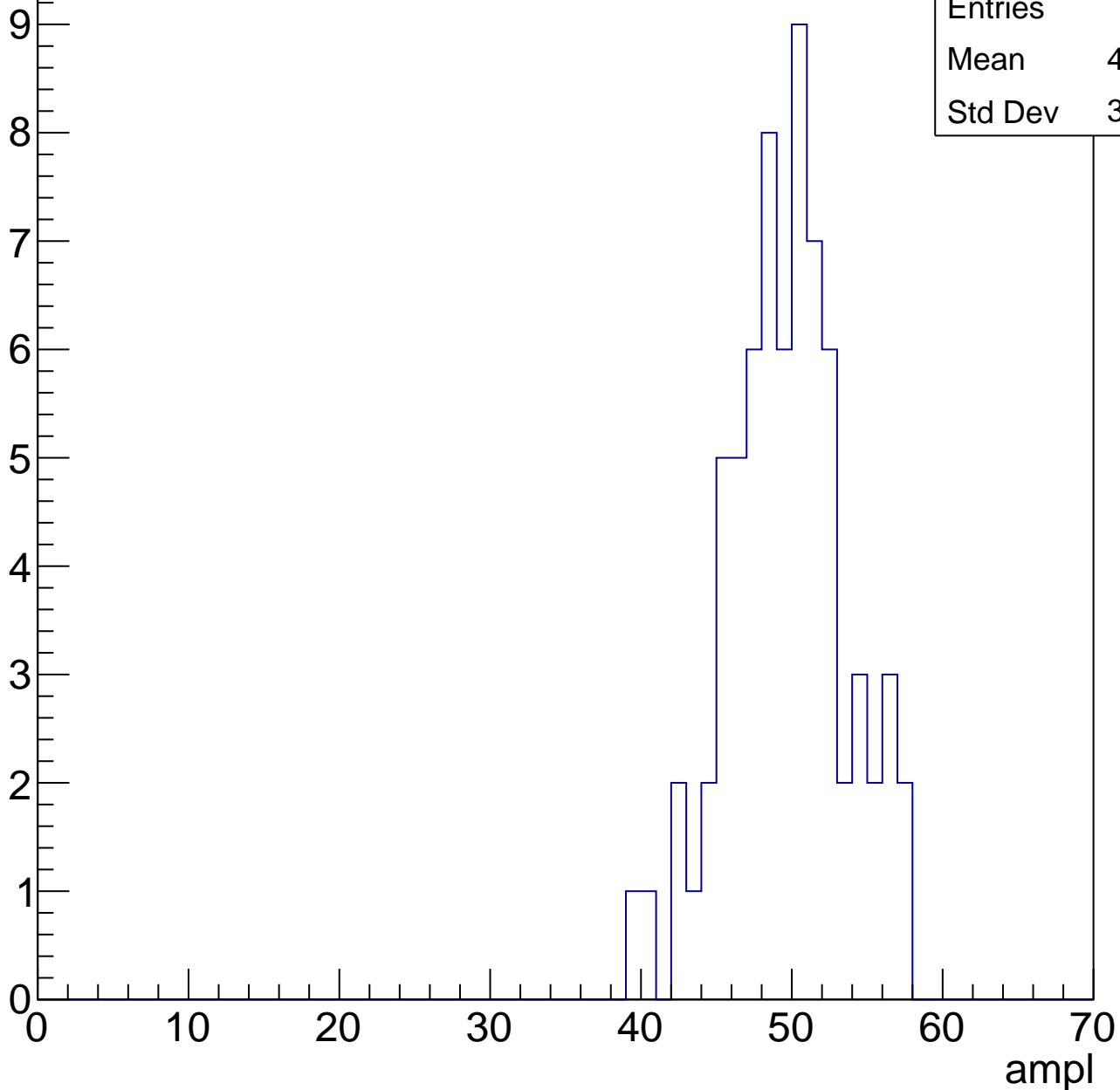


B0L002S, U5-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	49.13
Std Dev	3.875

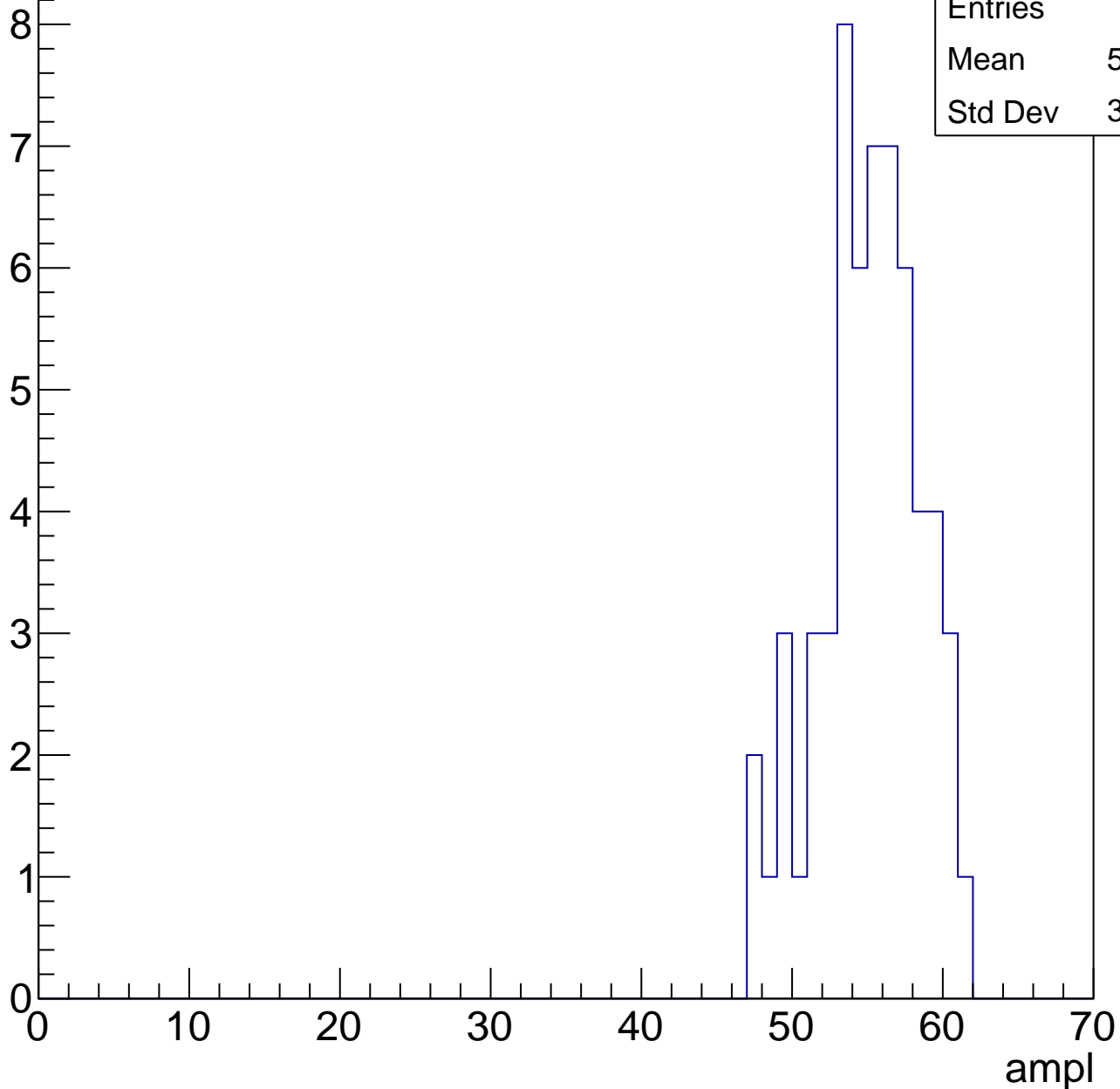


B0L002S, U5-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	54.64
Std Dev	3.338

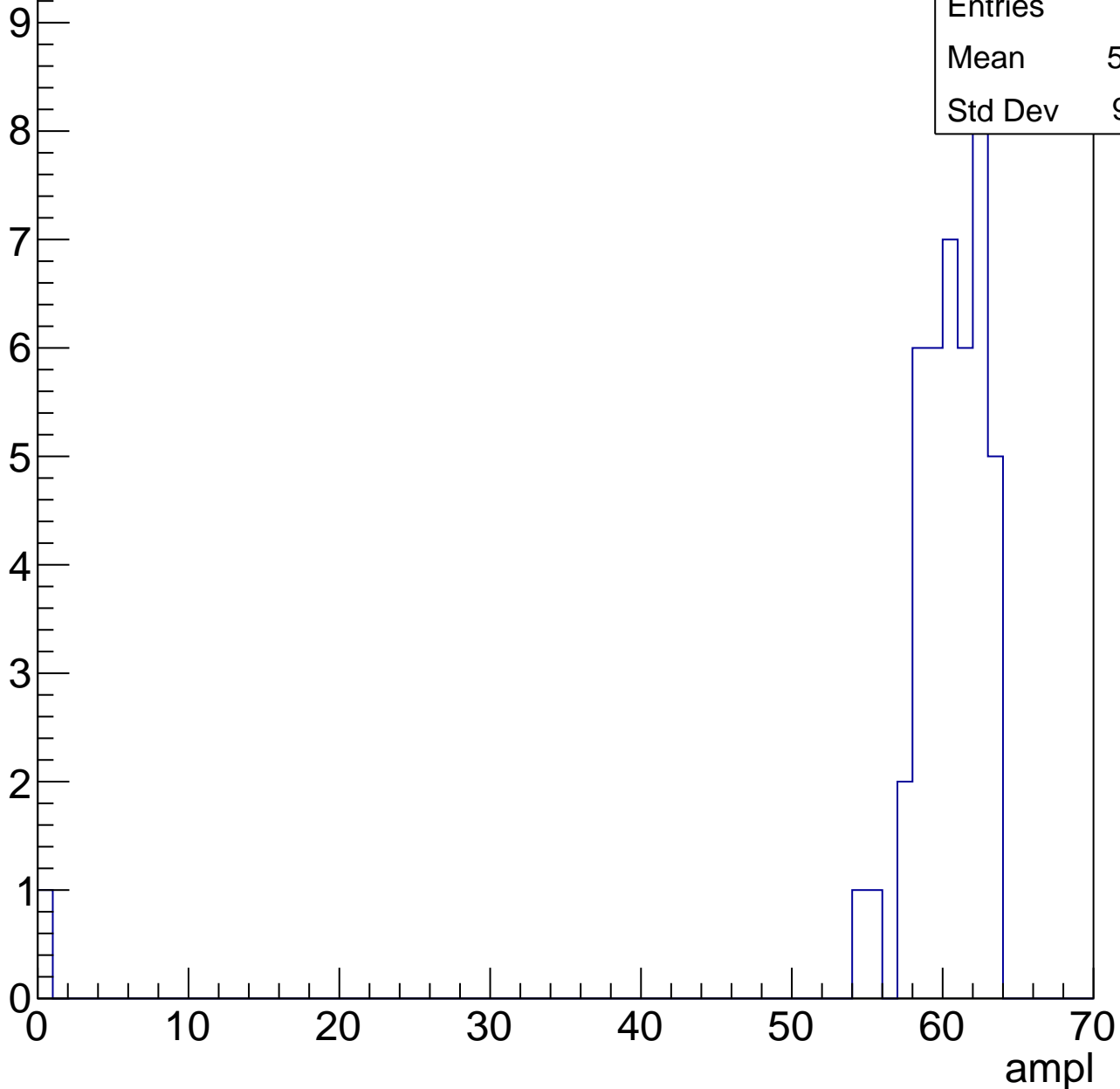


B0L002S, U5-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	58.73
Std Dev	9.201

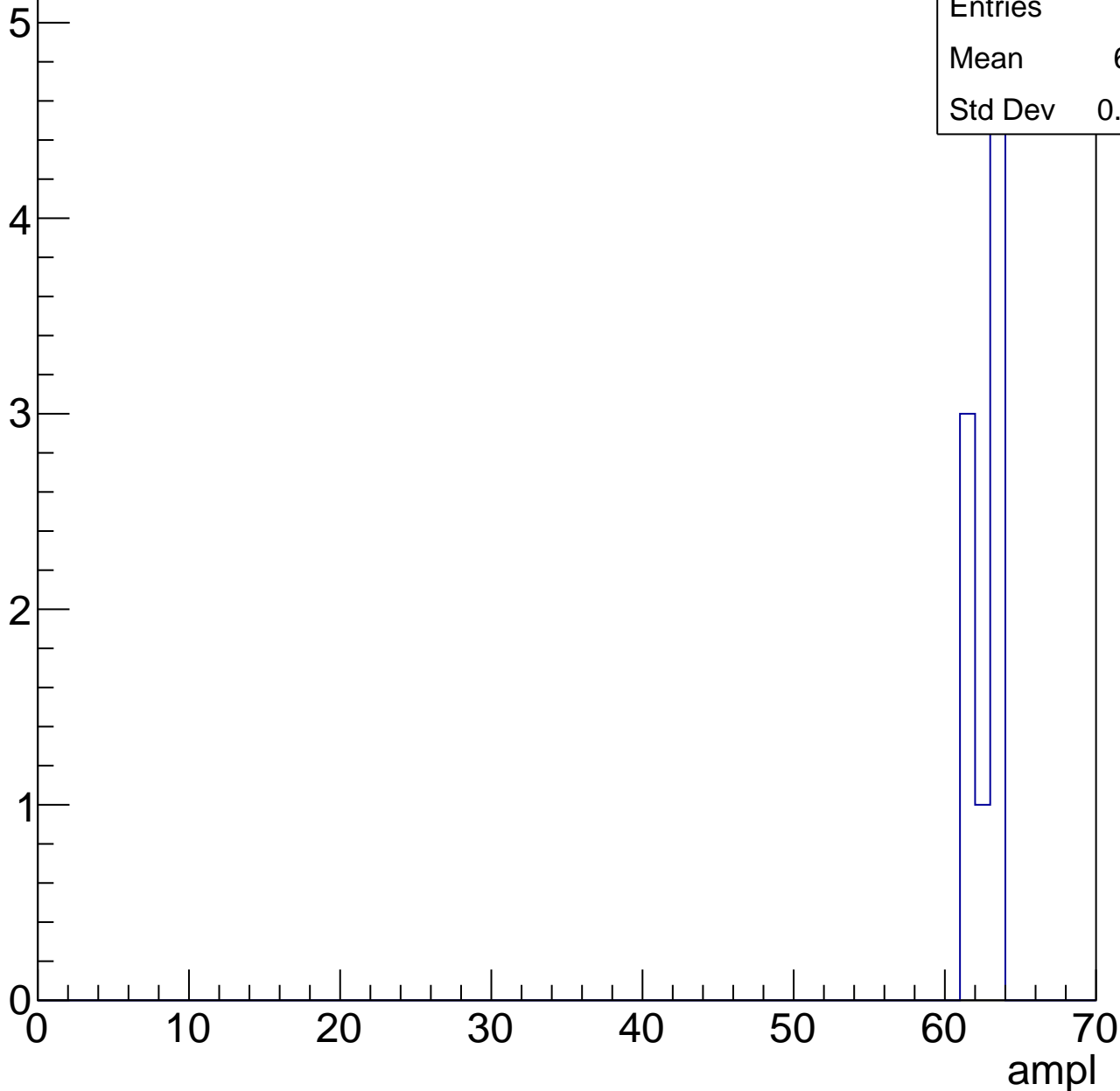


B0L002S, U5-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	62.22
Std Dev	0.9162



B0L002S, U5-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch114, adc0

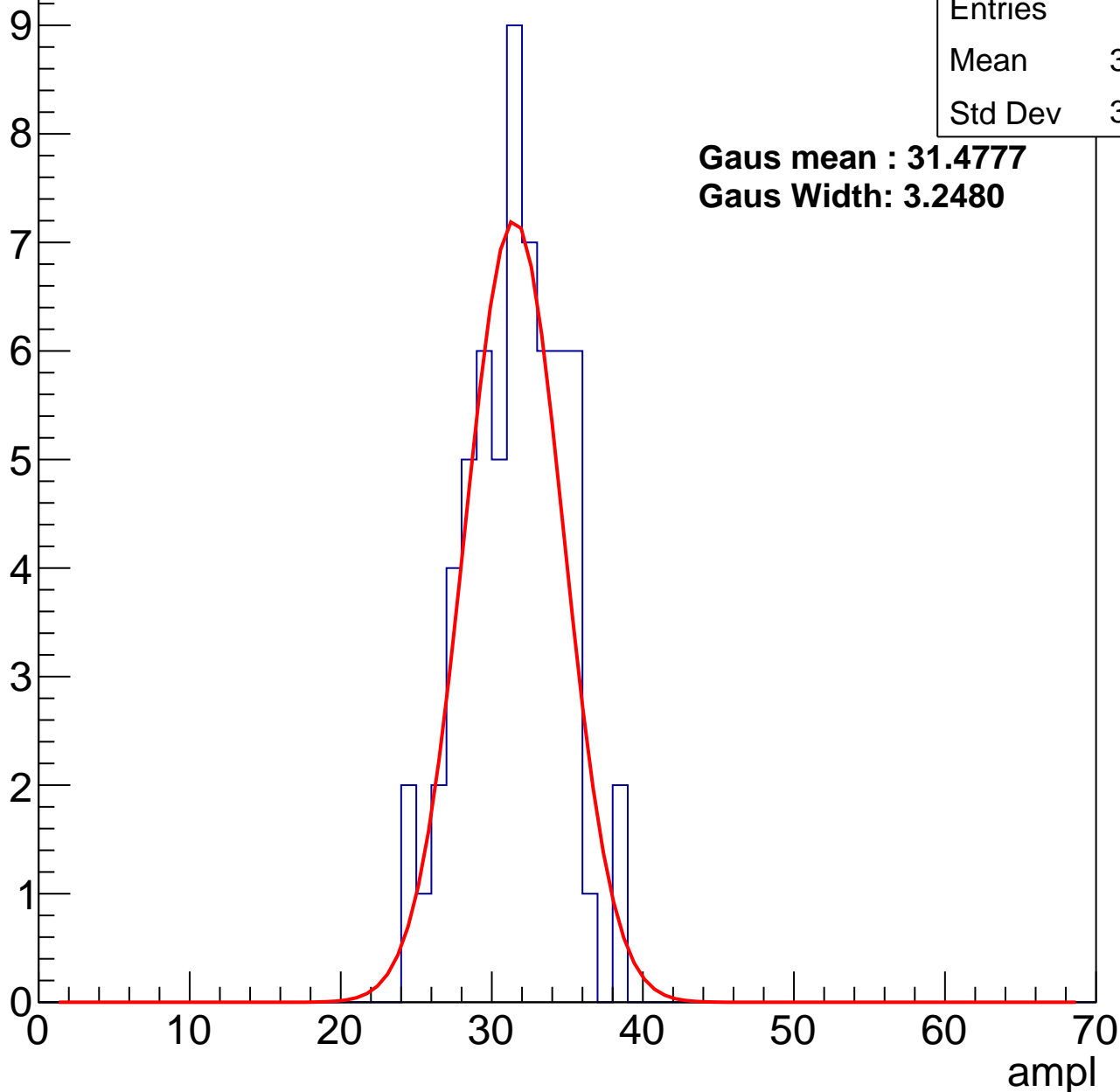
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.03
Std Dev	3.177

Gaus mean : 31.4777

Gaus Width: 3.2480



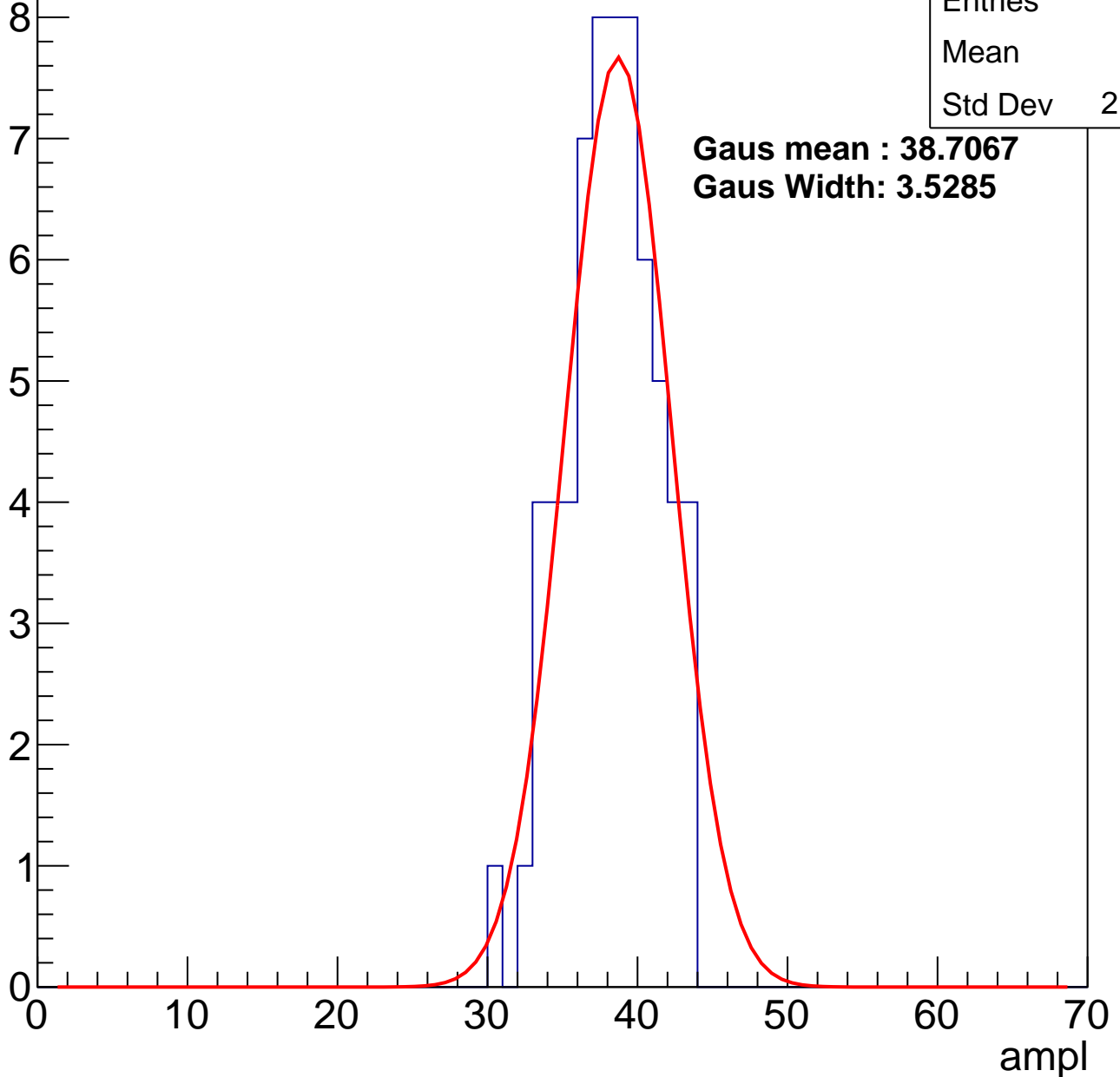
B0L002S, U5-ch114, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.8
Std Dev	2.996

Gaus mean : 38.7067
Gaus Width: 3.5285



B0L002S, U5-ch114, adc2

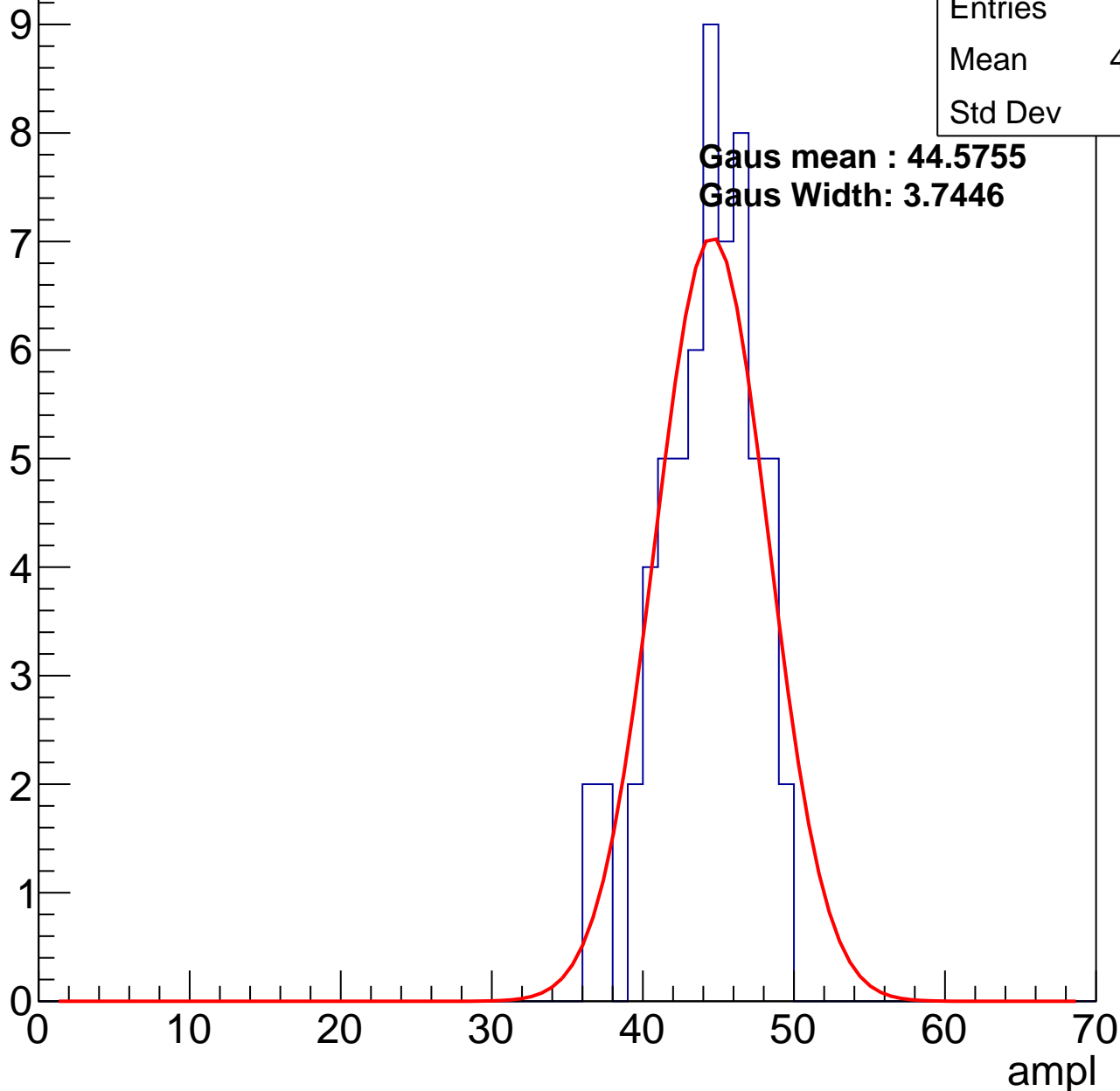
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.69
Std Dev	3.16

Gaus mean : 44.5755

Gaus Width: 3.7446

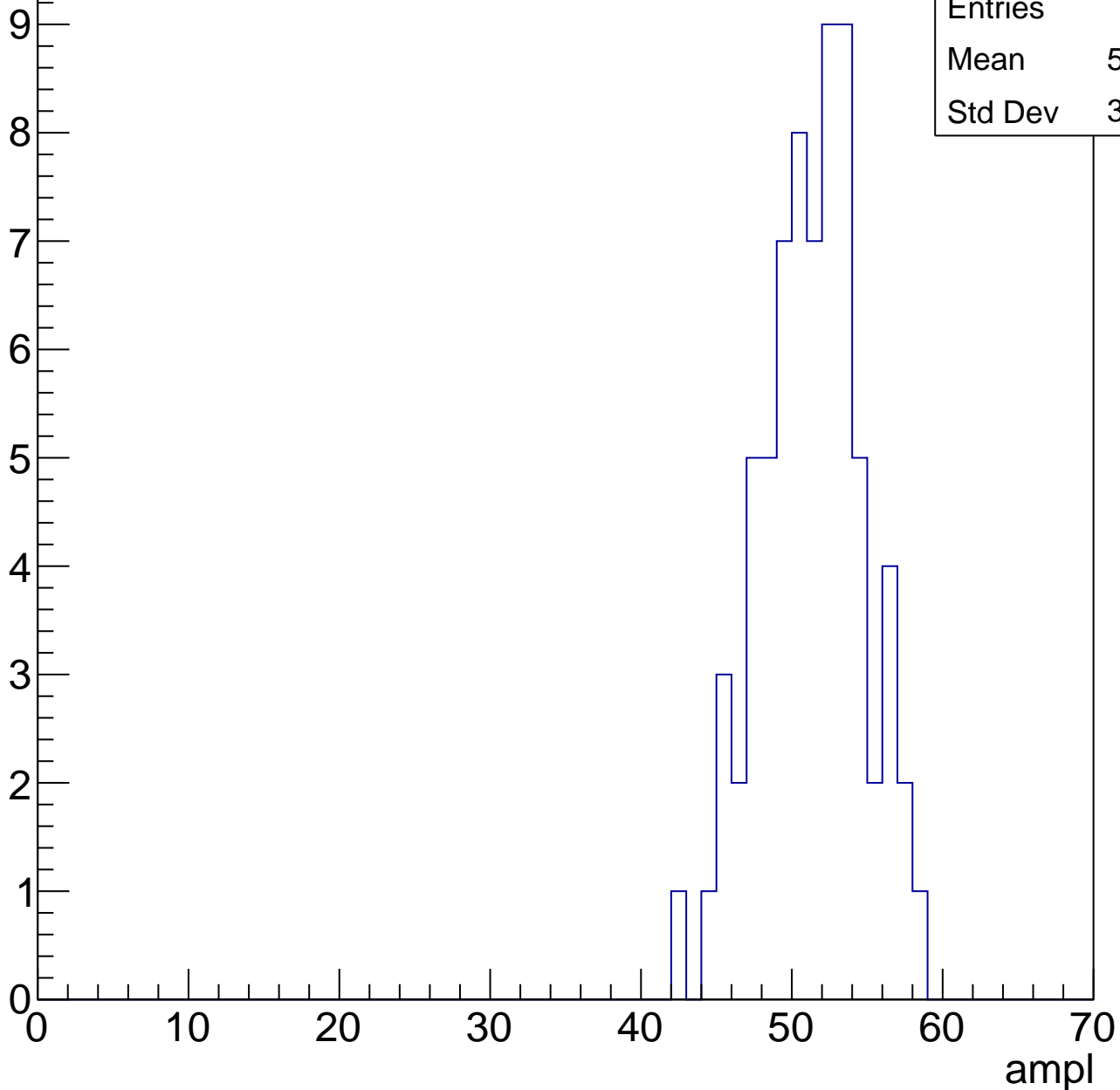


B0L002S, U5-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

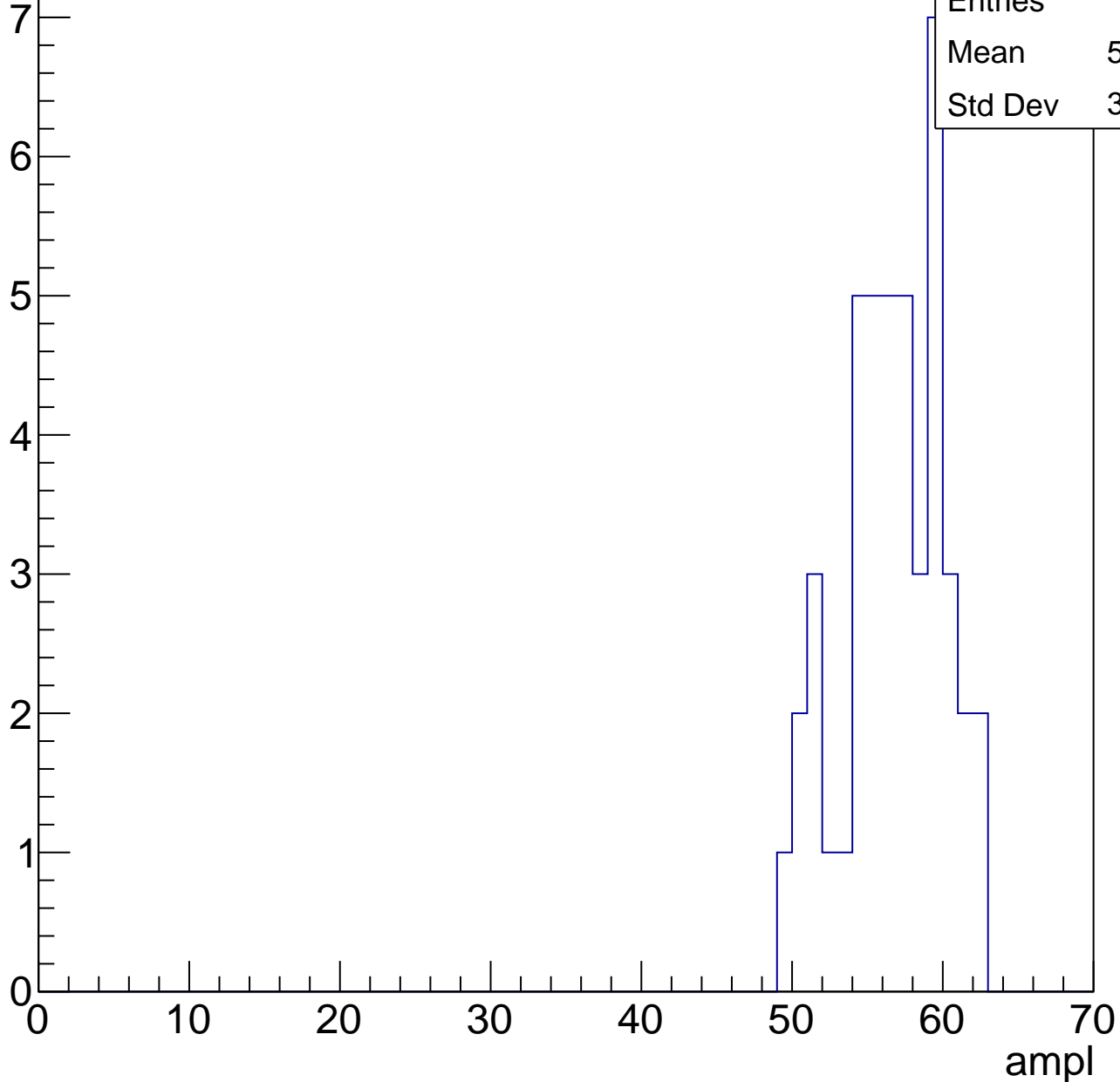
Entries	71
Mean	50.83
Std Dev	3.336



B0L002S, U5-ch114, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



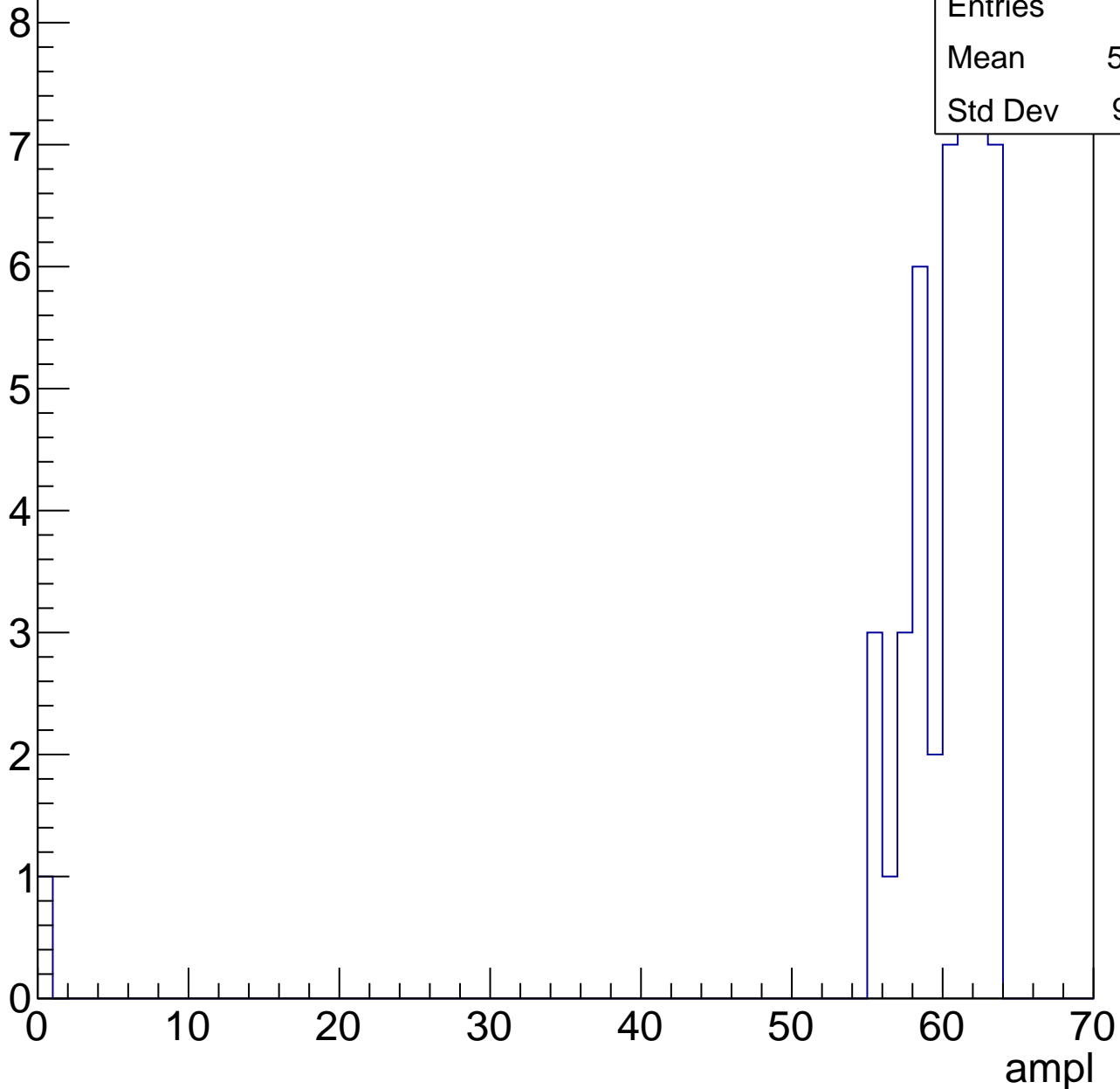
Entries	45
Mean	56.22
Std Dev	3.313

B0L002S, U5-ch114, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

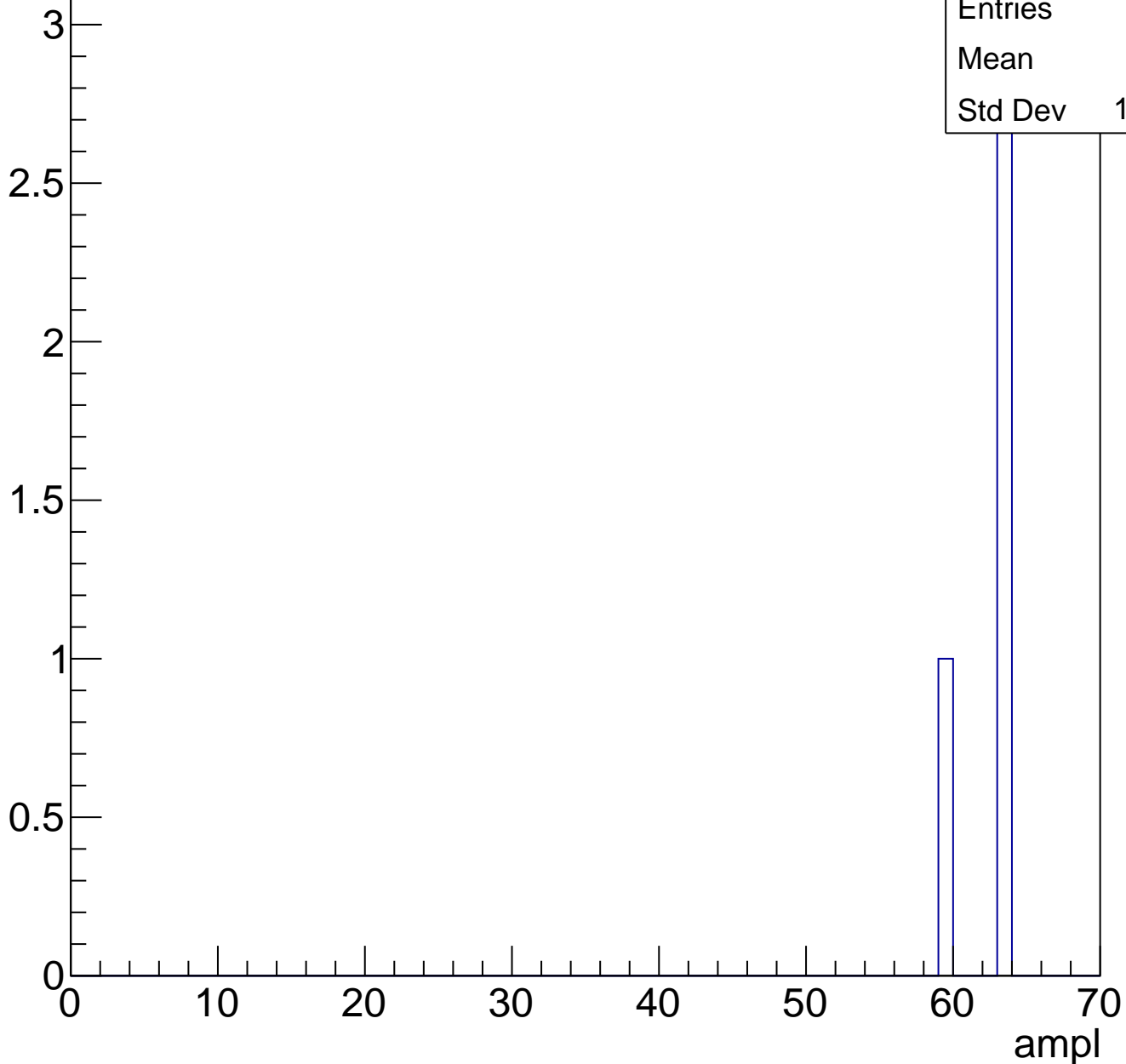
Entries	46
Mean	58.76
Std Dev	9.061



B0L002S, U5-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U5-ch115, adc0

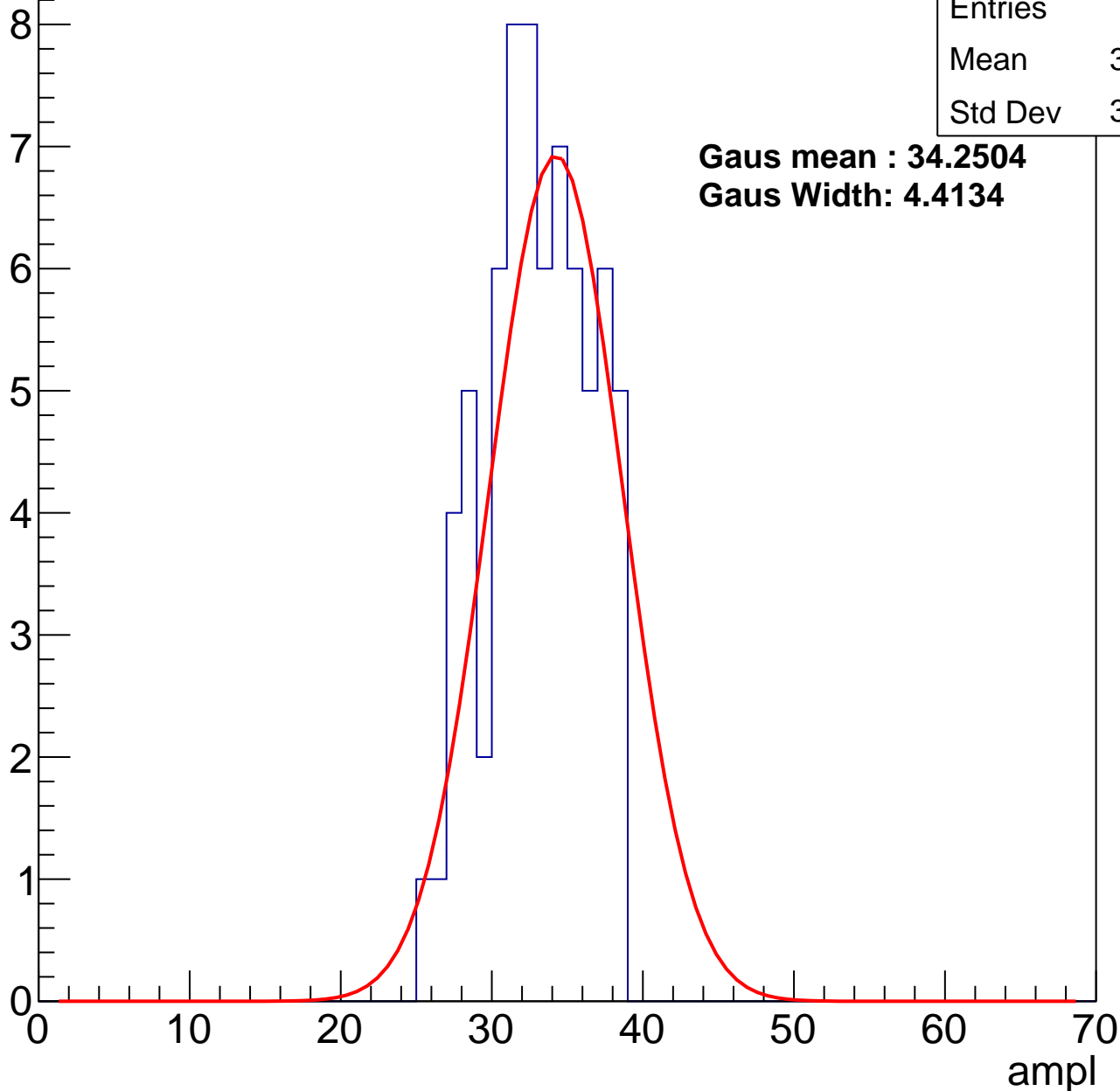
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	32.56
Std Dev	3.362

Gaus mean : 34.2504

Gaus Width: 4.4134



B0L002S, U5-ch115, adc1

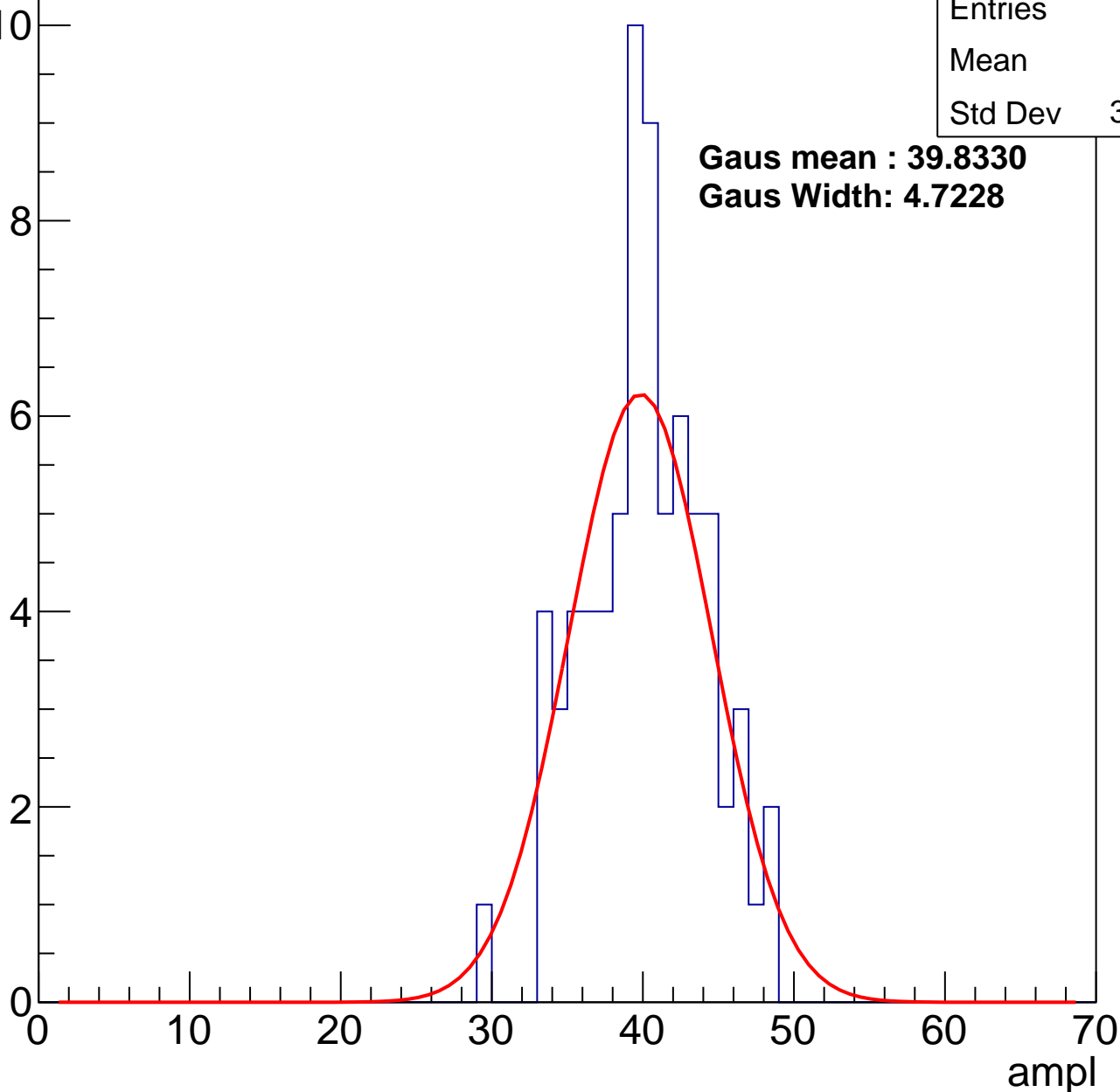
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	39.7
Std Dev	3.947

Gaus mean : 39.8330

Gaus Width: 4.7228



B0L002S, U5-ch115, adc2

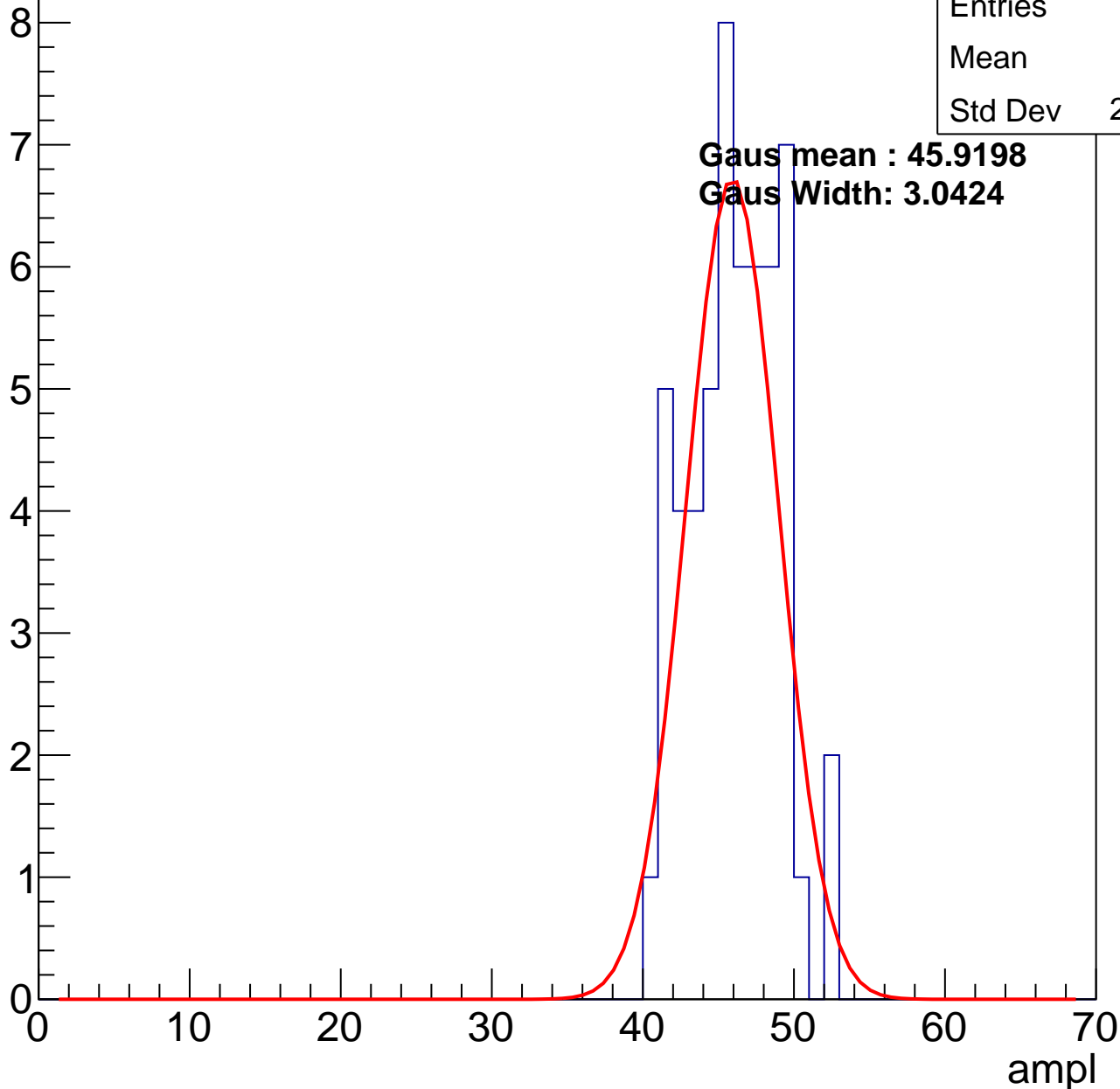
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	45.6
Std Dev	2.896

Gaus mean : 45.9198

Gaus Width: 3.0424

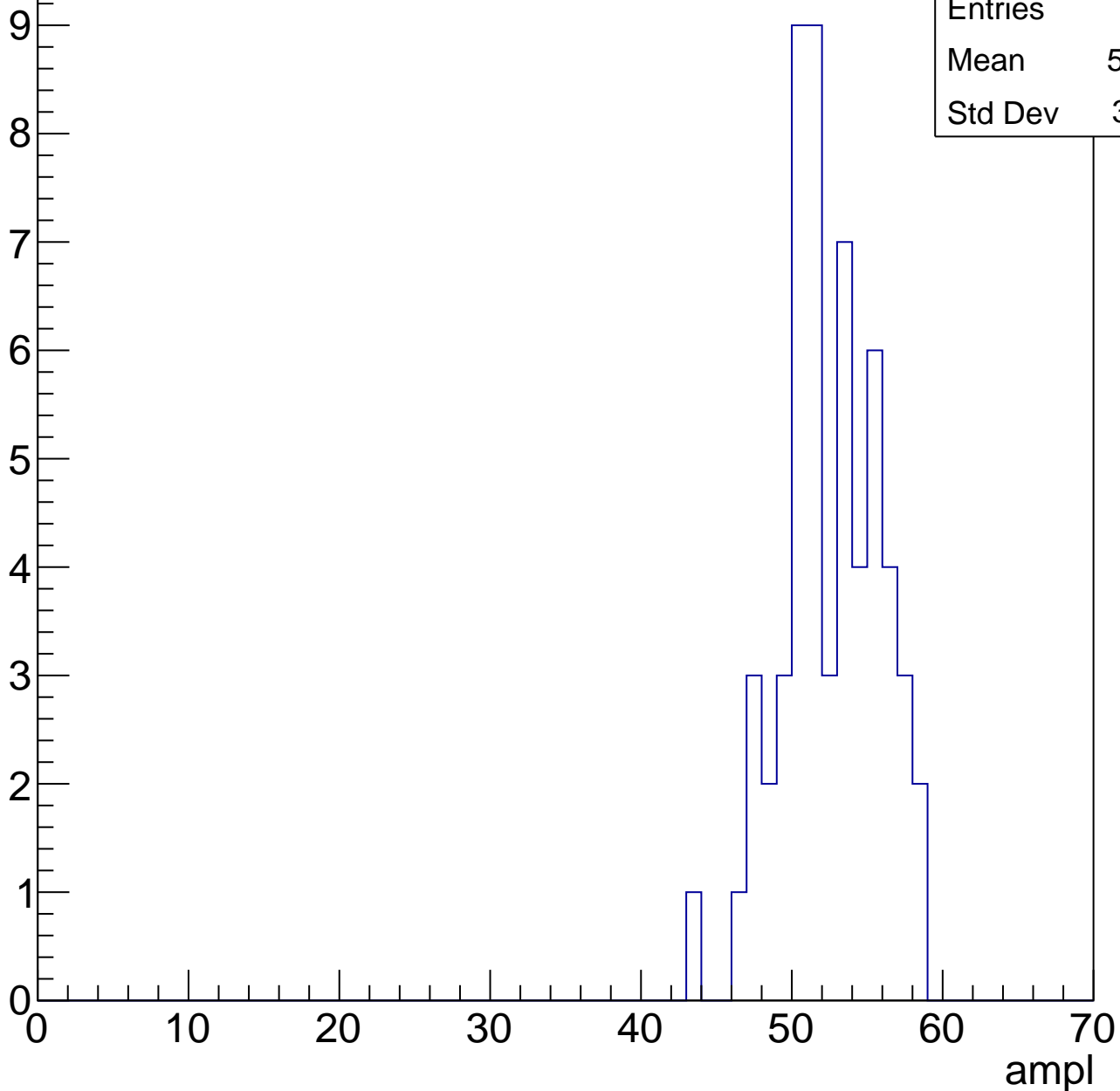


B0L002S, U5-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	52.04
Std Dev	3.201



B0L002S, U5-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries 63

Mean 57.24

Std Dev 2.985

8

6

4

2

0

0

10

20

30

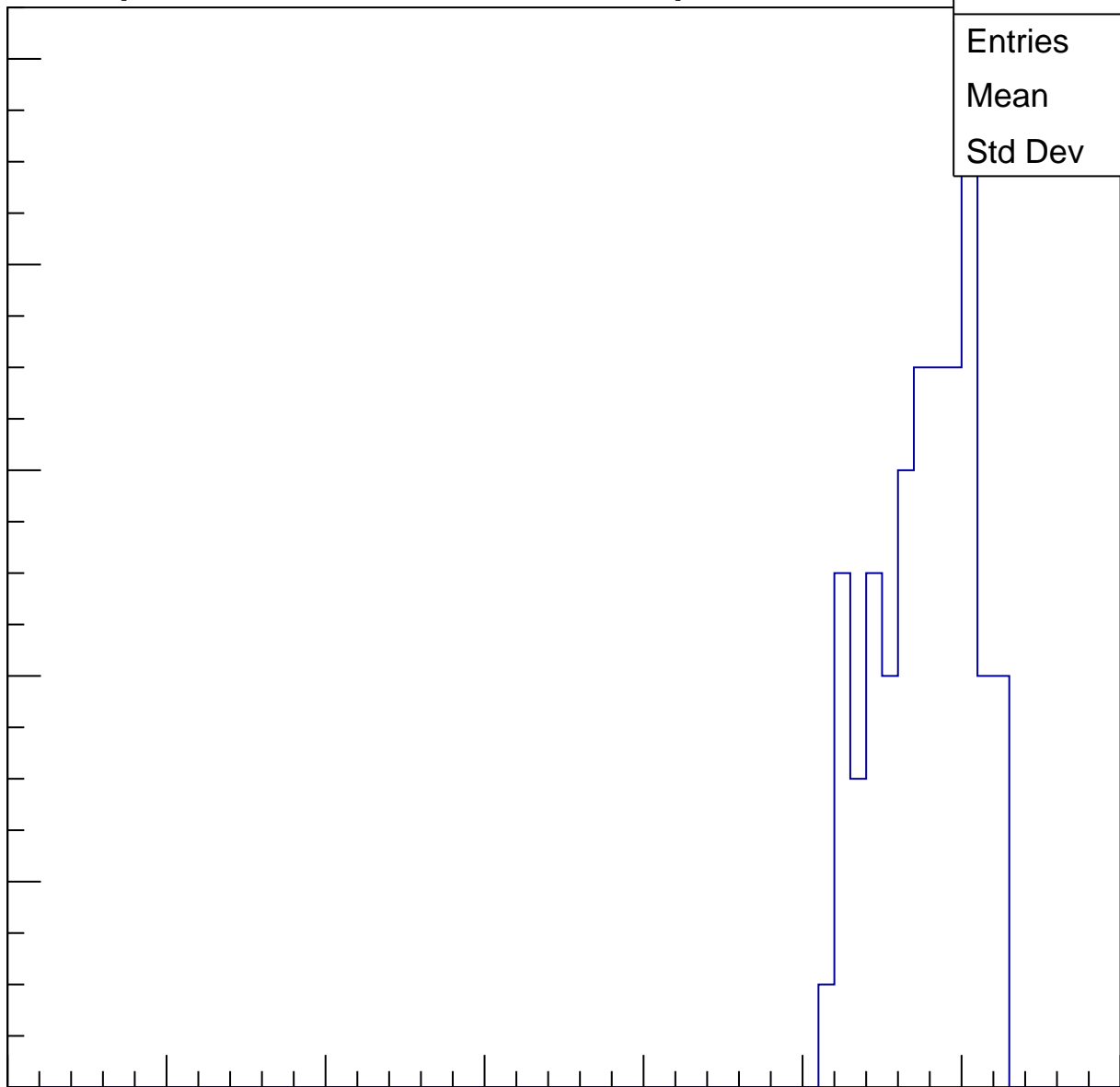
40

50

60

70

ampl

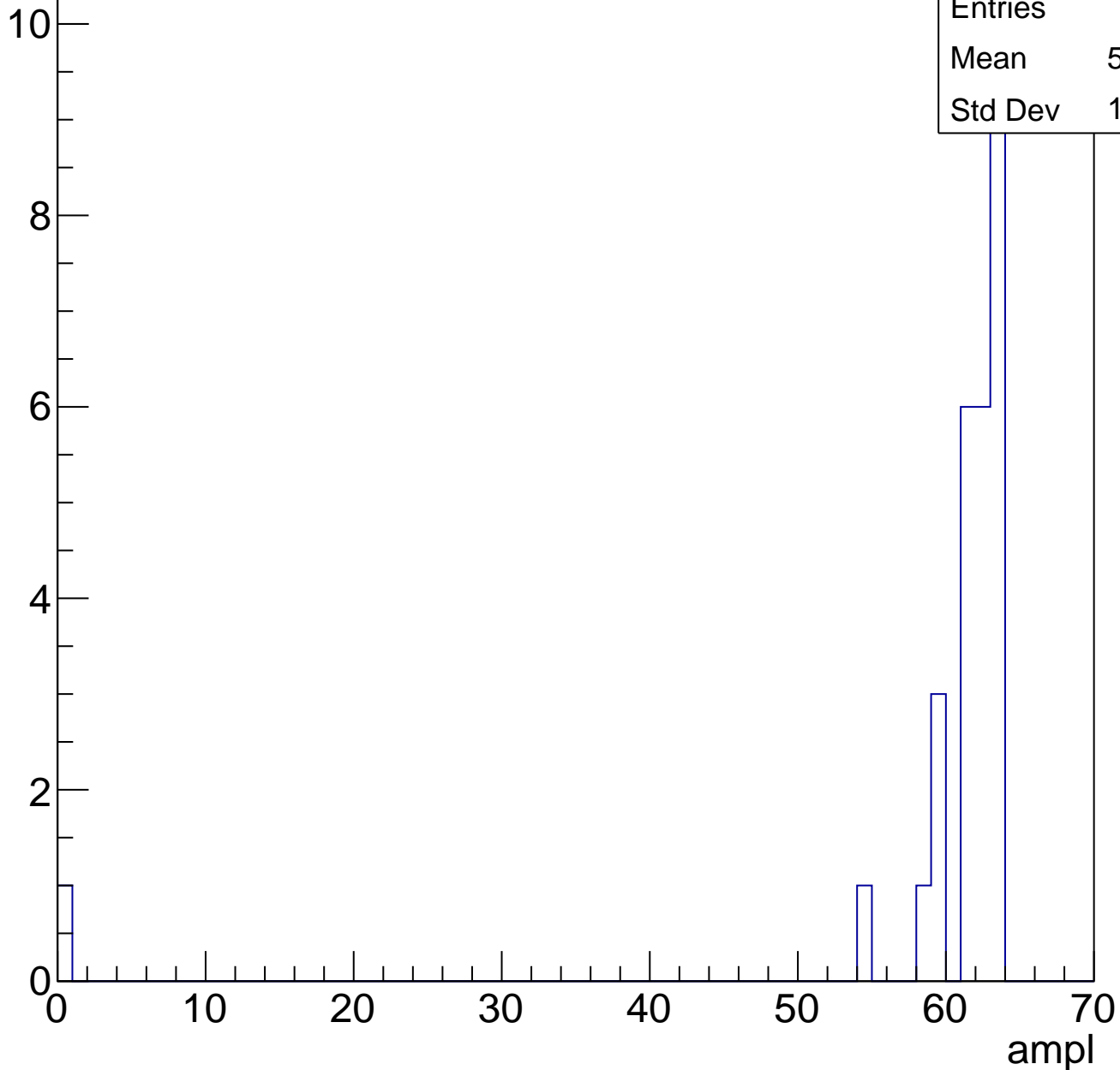


B0L002S, U5-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	28
Mean	59.18
Std Dev	11.56

Entry



B0L002S, U5-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch116, adc0

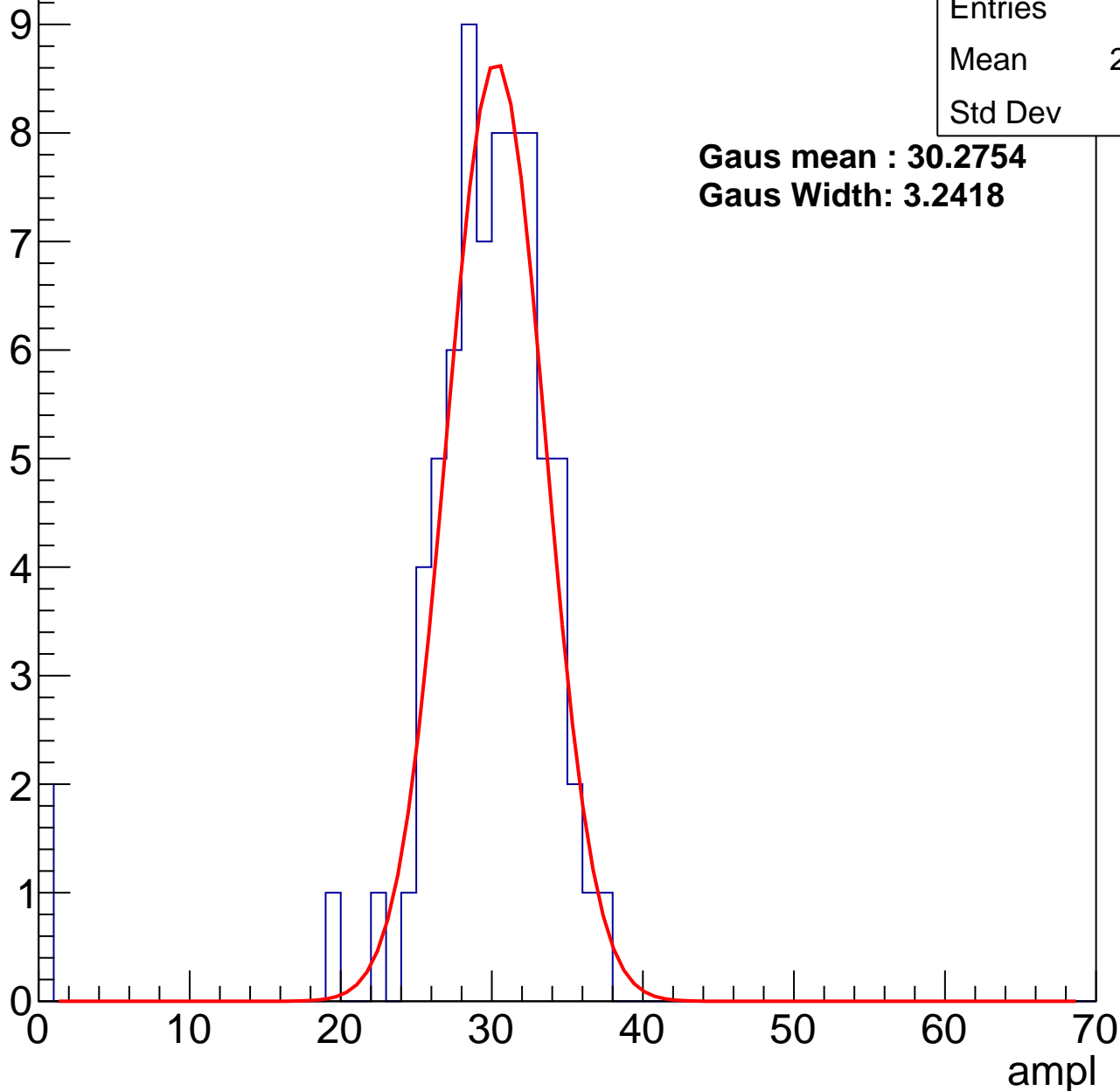
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	28.84
Std Dev	5.81

Gaus mean : 30.2754

Gaus Width: 3.2418



B0L002S, U5-ch116, adc1

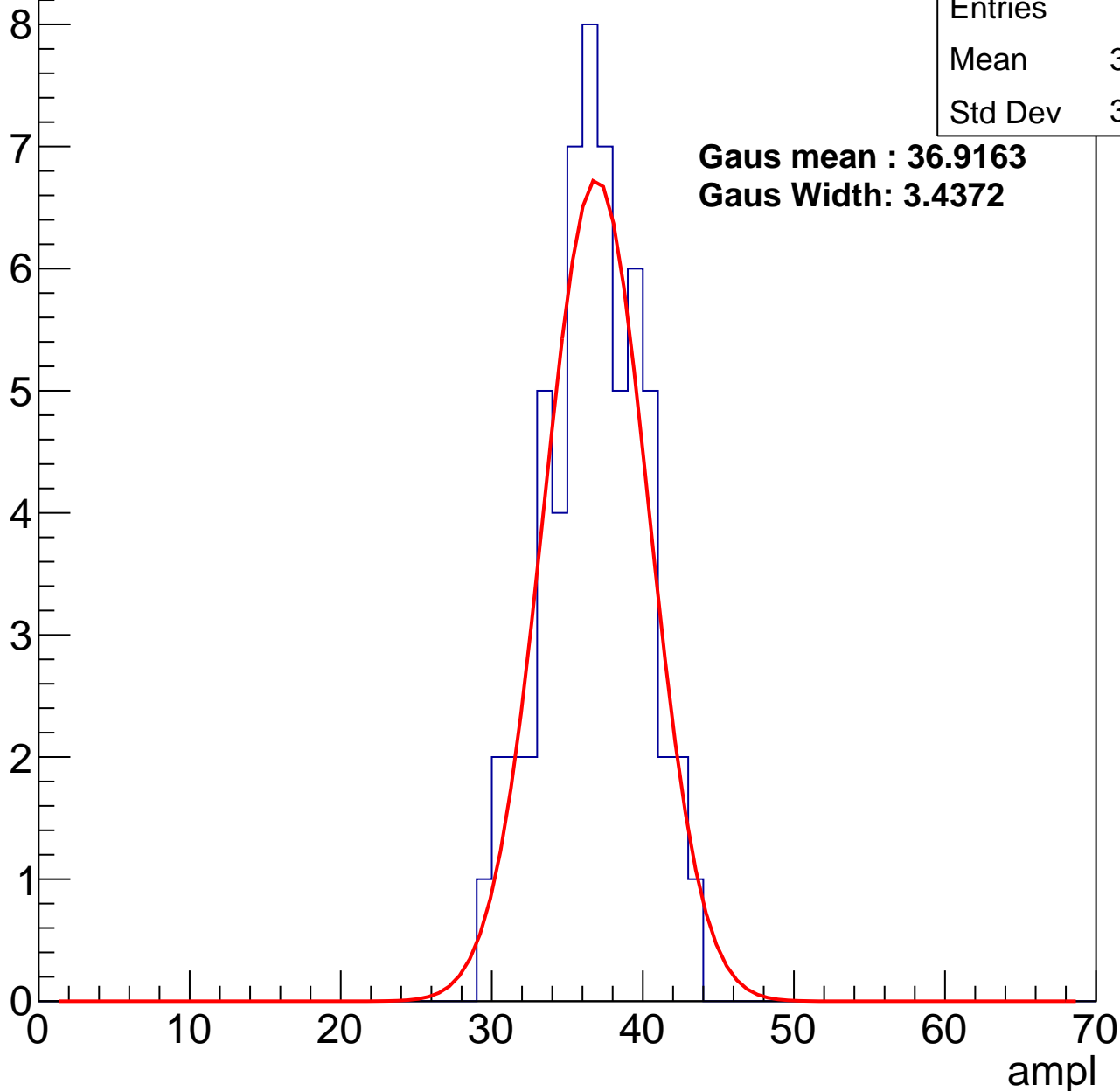
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.29
Std Dev	3.184

Gaus mean : 36.9163

Gaus Width: 3.4372



B0L002S, U5-ch116, adc2

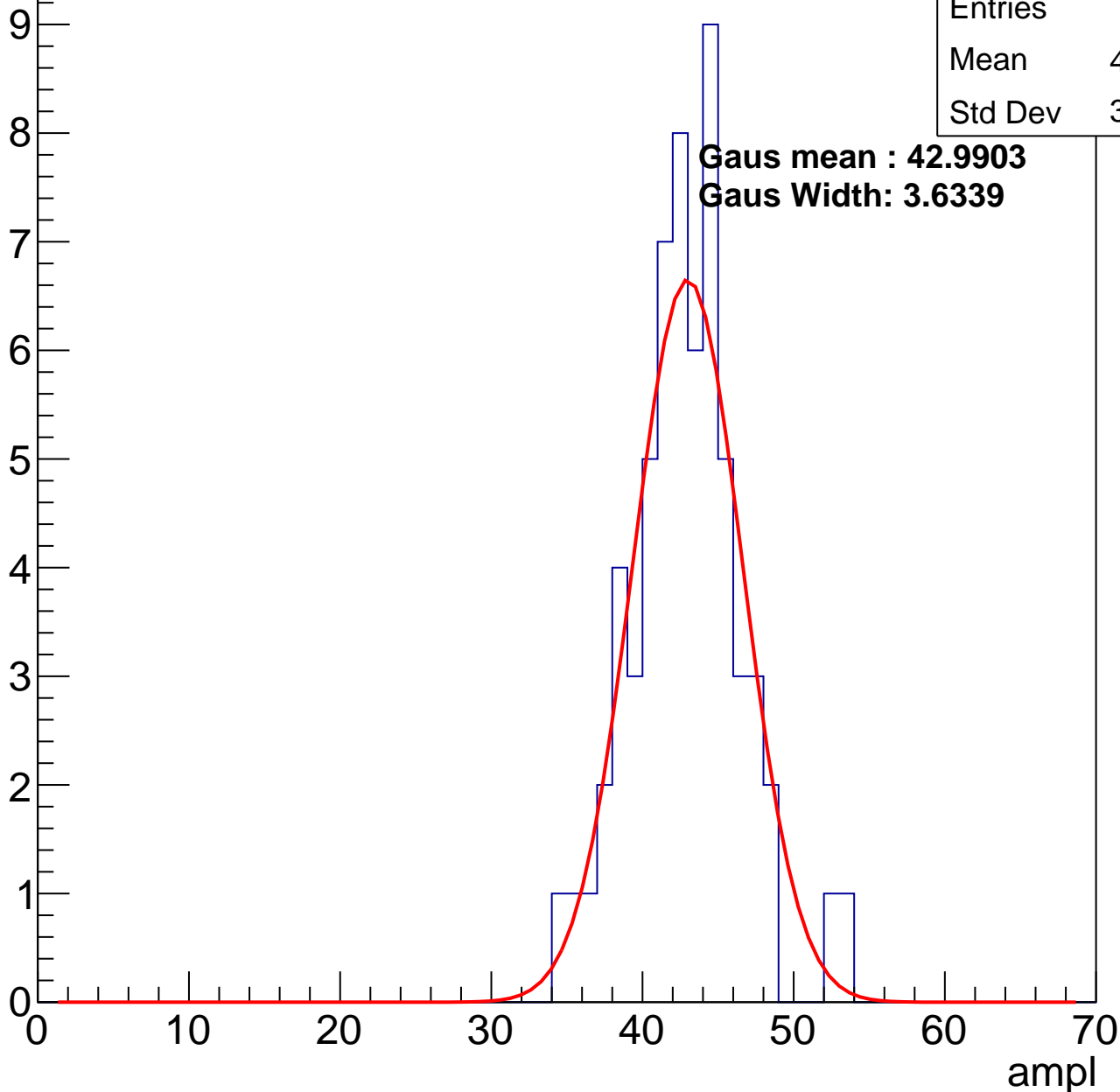
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	42.42
Std Dev	3.612

Gaus mean : 42.9903

Gaus Width: 3.6339

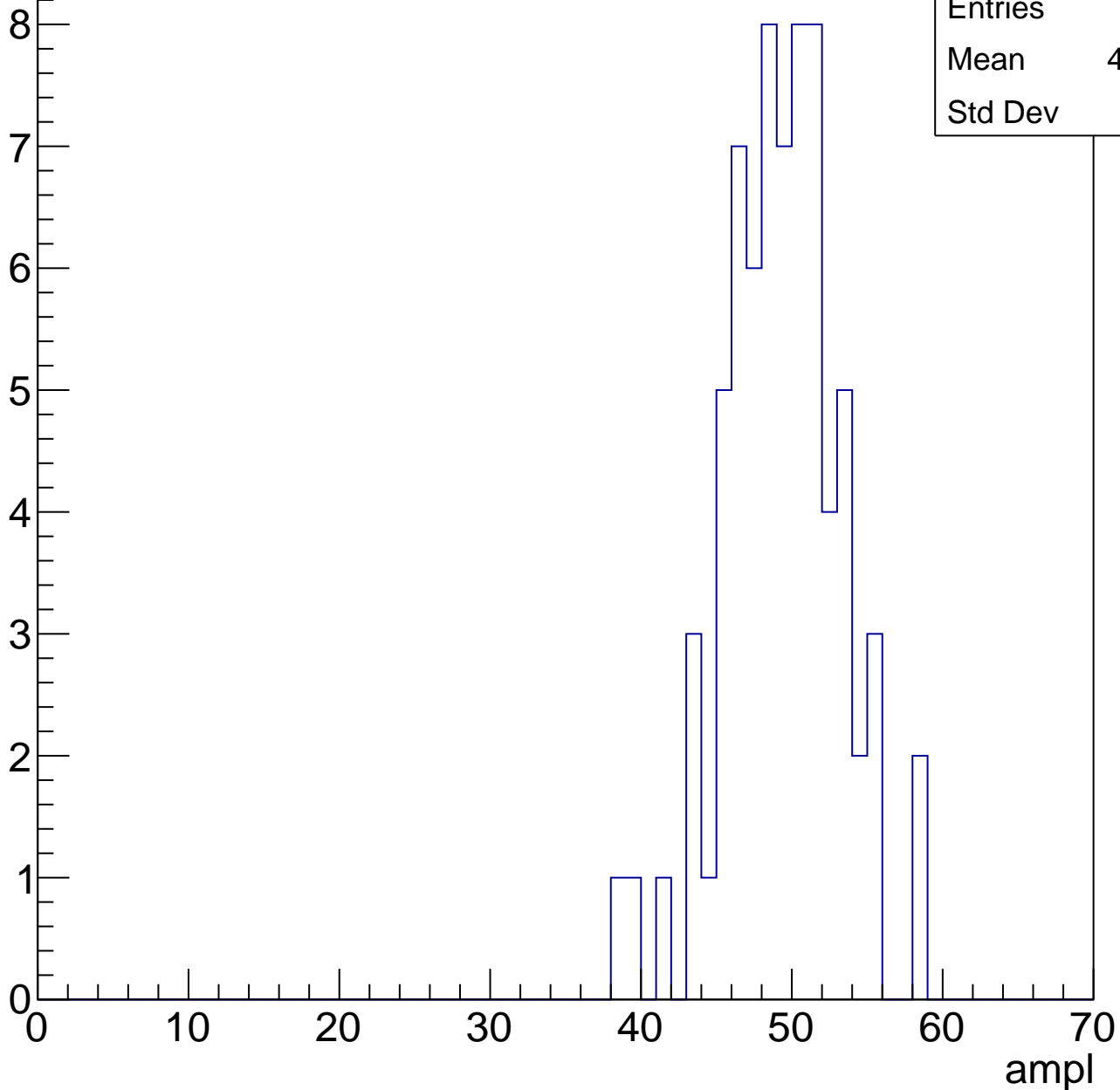


B0L002S, U5-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	48.85
Std Dev	3.85

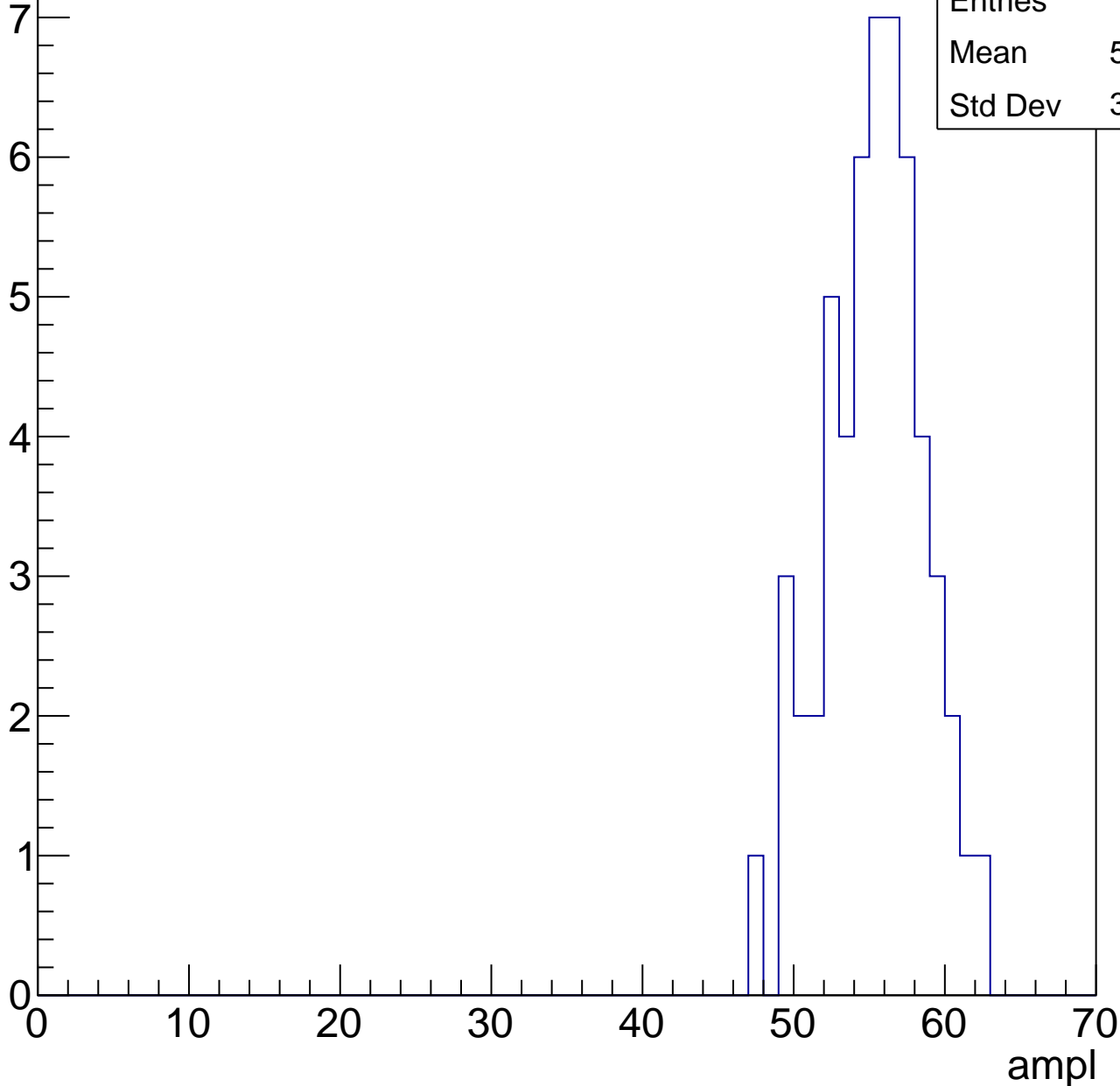


B0L002S, U5-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

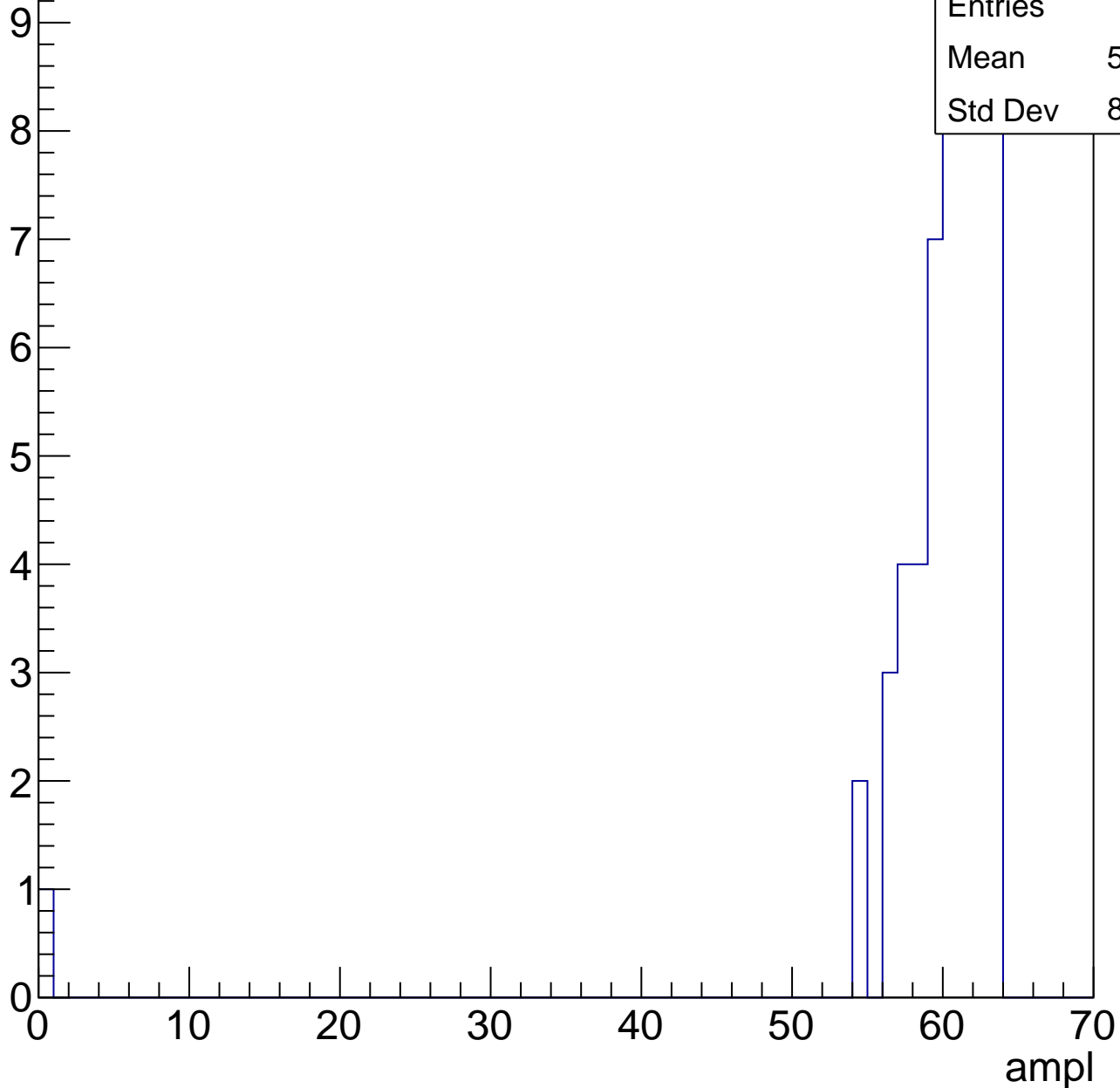
Entries	54
Mean	54.87
Std Dev	3.249



B0L002S, U5-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch116, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch117, adc0

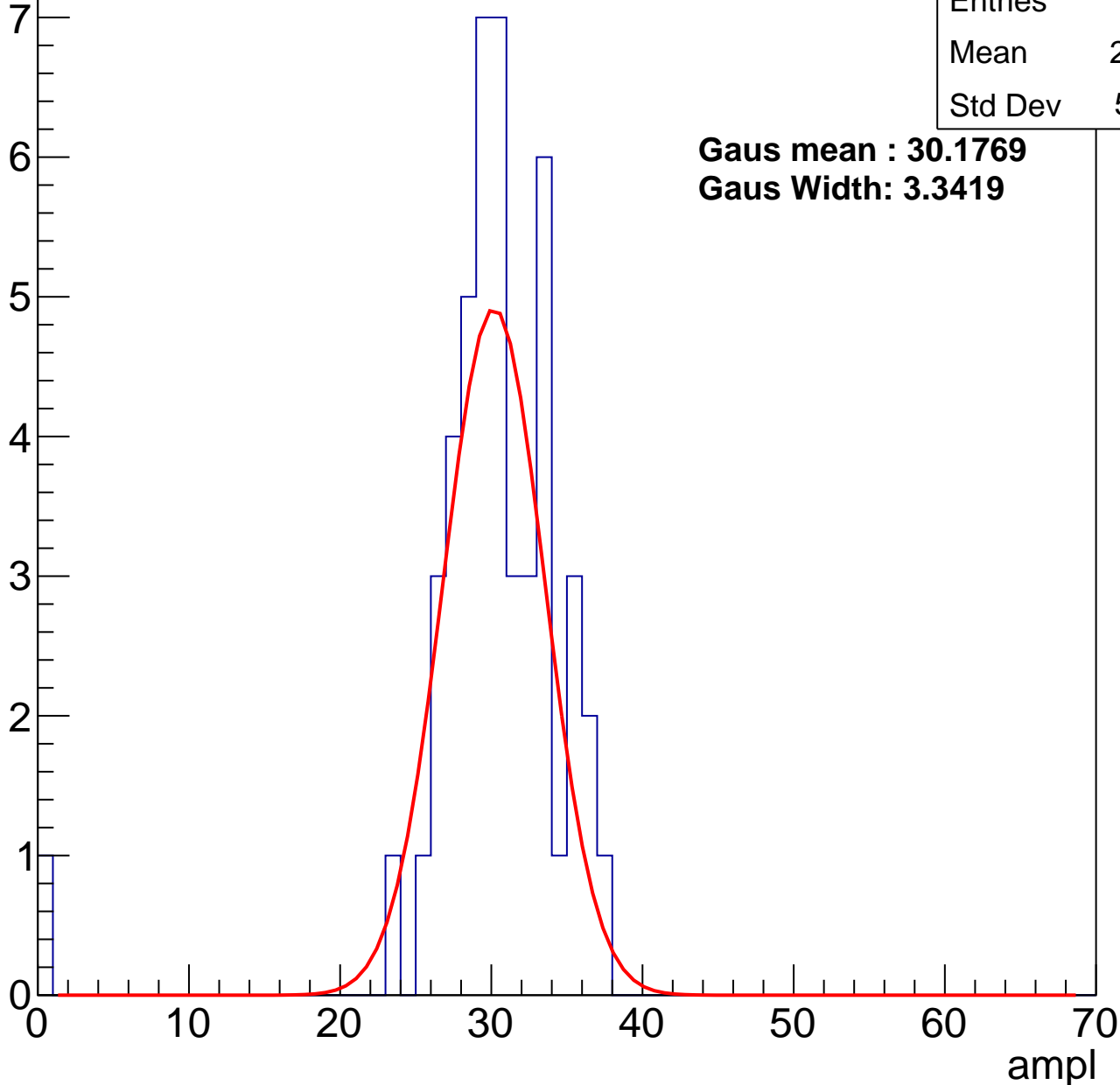
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	29.62
Std Dev	5.321

Gaus mean : 30.1769

Gaus Width: 3.3419



B0L002S, U5-ch117, adc1

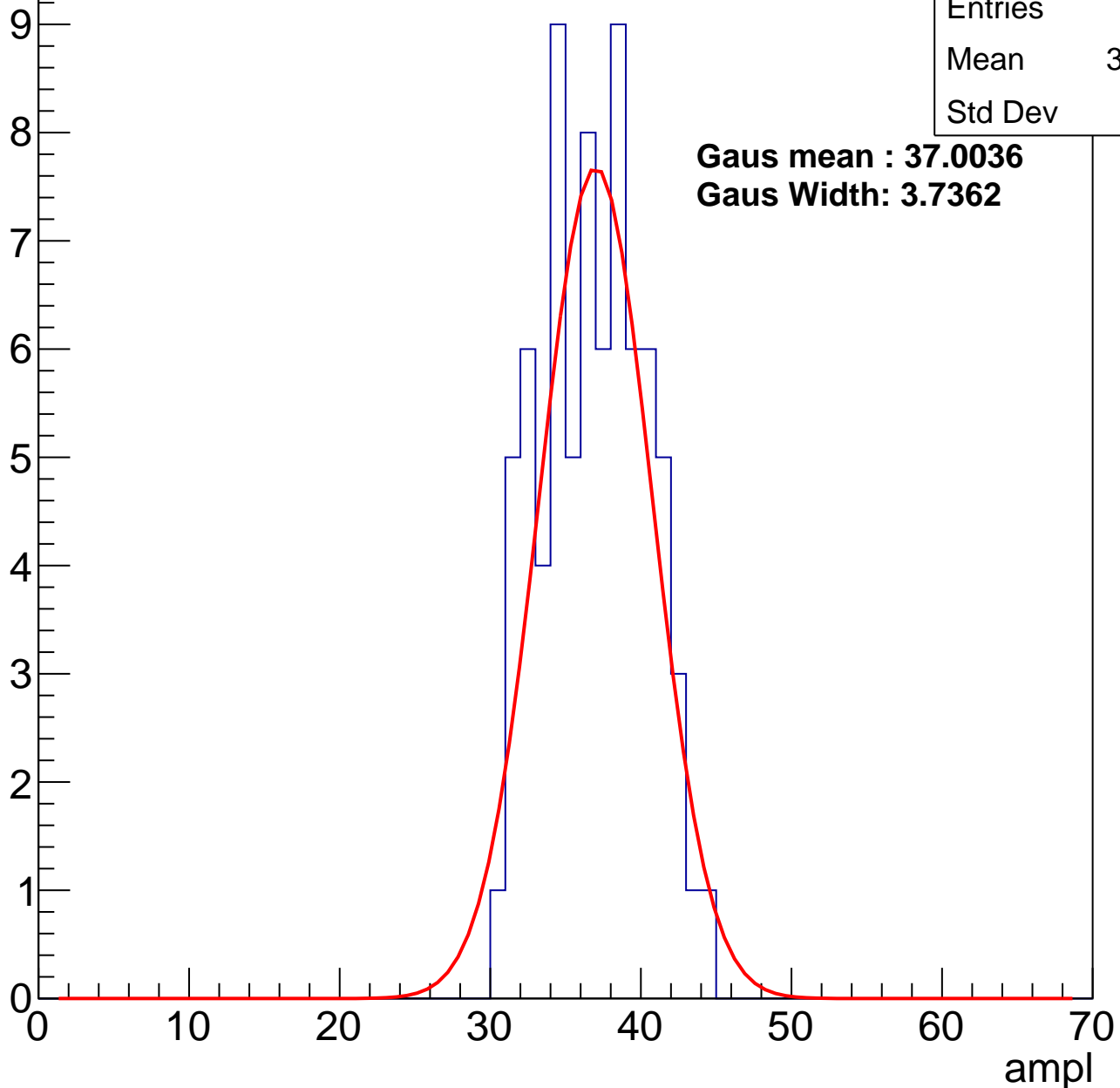
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	36.45
Std Dev	3.38

Gaus mean : 37.0036

Gaus Width: 3.7362



B0L002S, U5-ch117, adc2

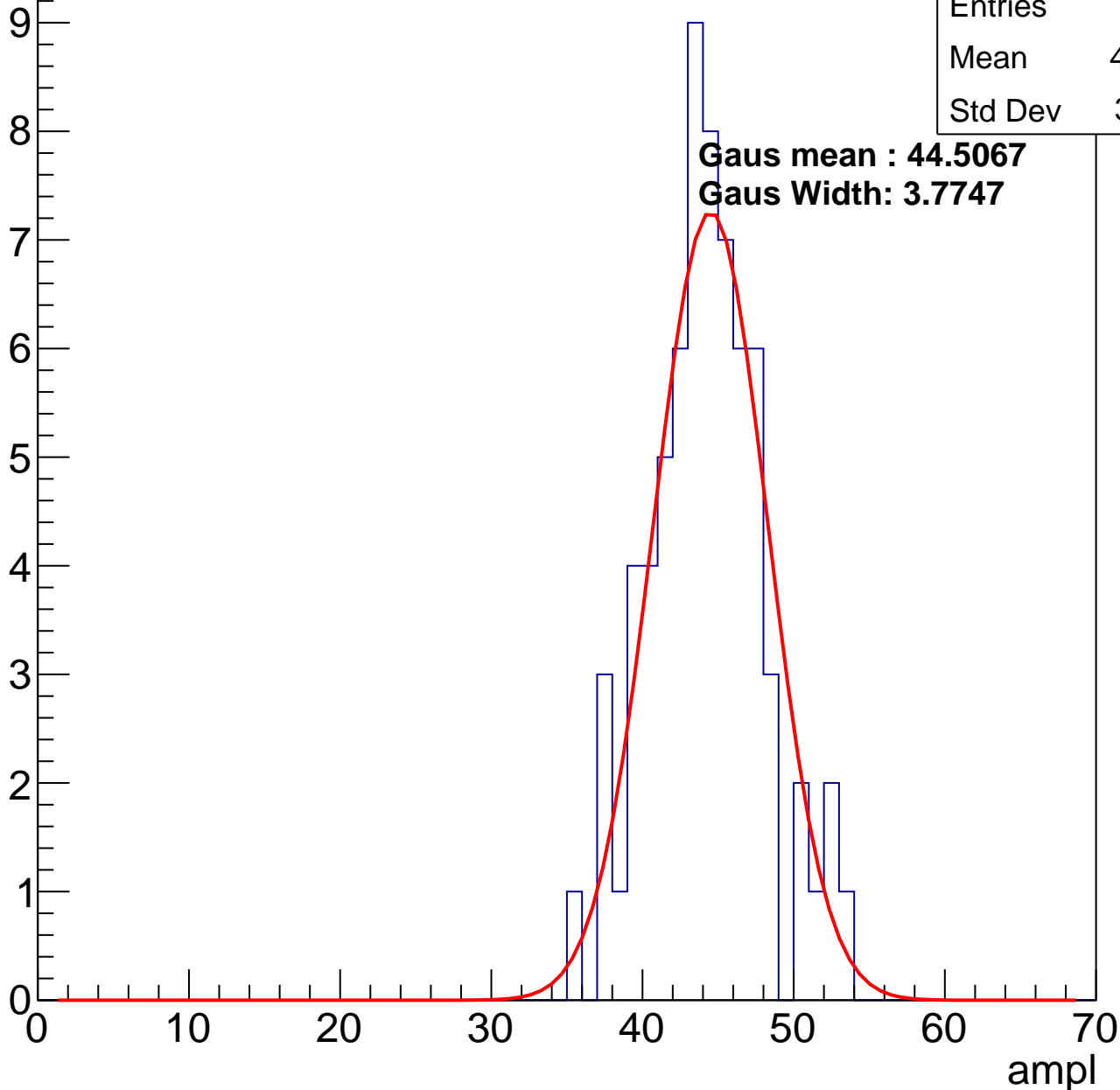
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.78
Std Dev	3.741

Gaus mean : 44.5067

Gaus Width: 3.7747

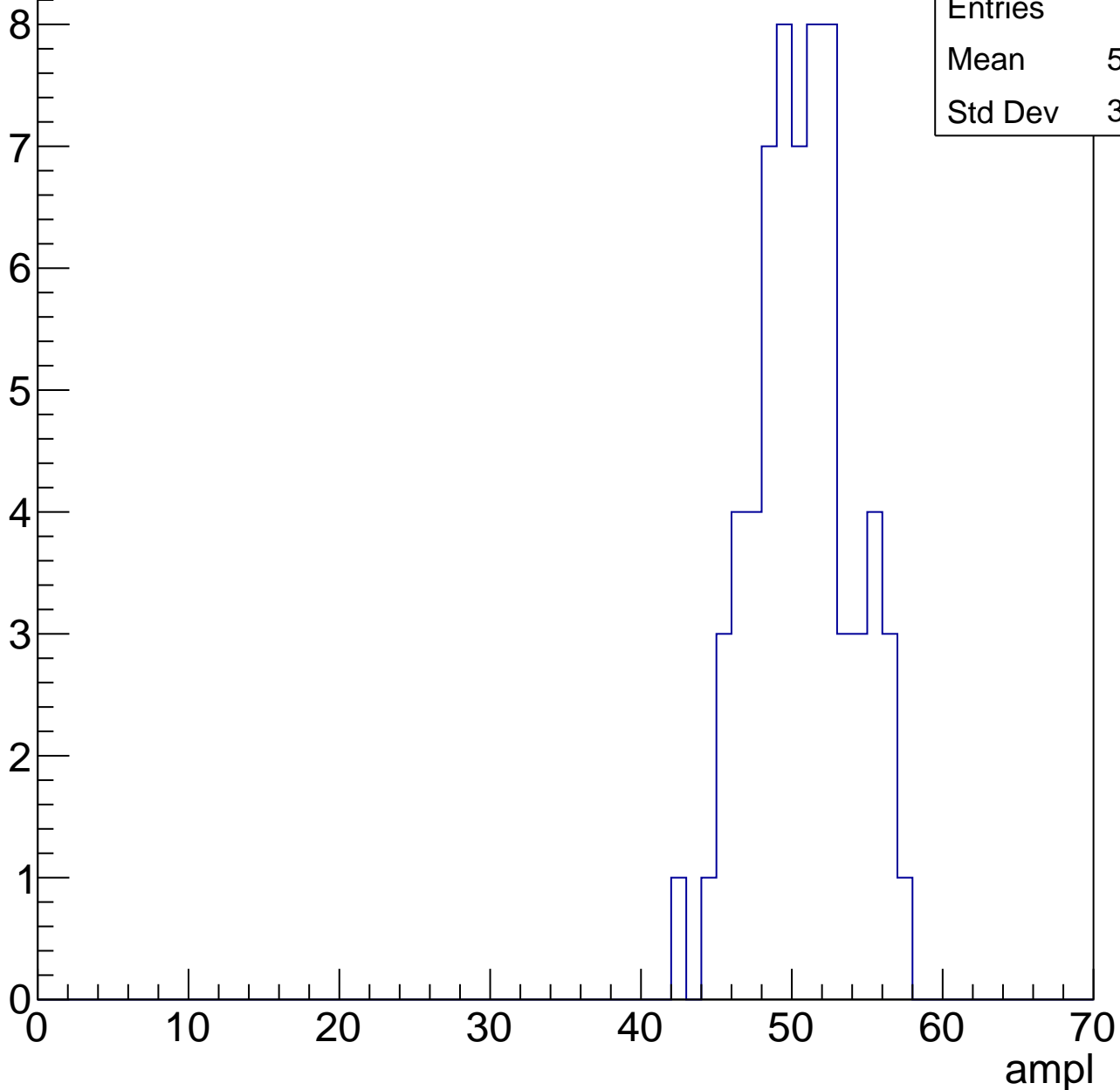


B0L002S, U5-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	50.17
Std Dev	3.237

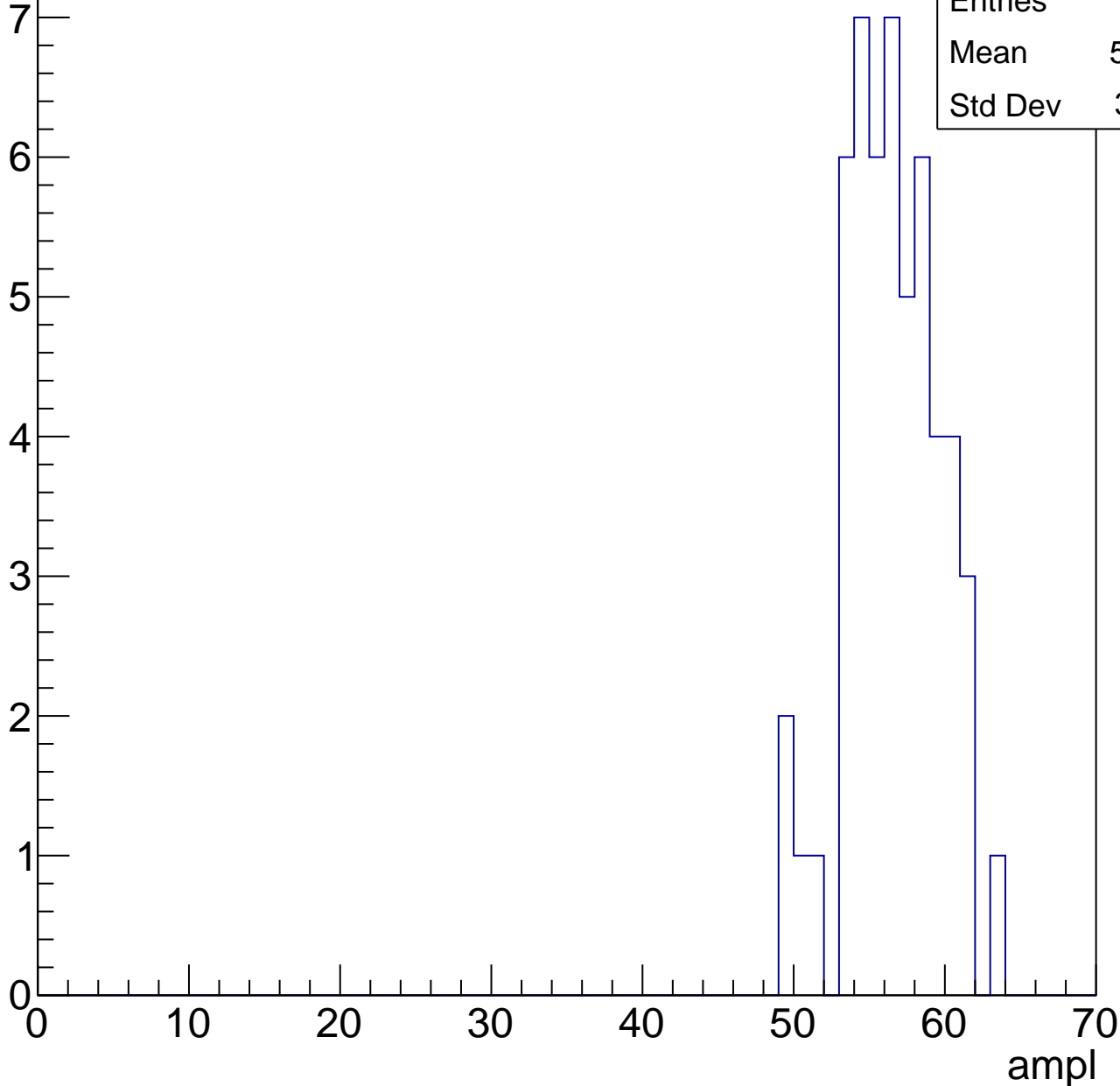


B0L002S, U5-ch117, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

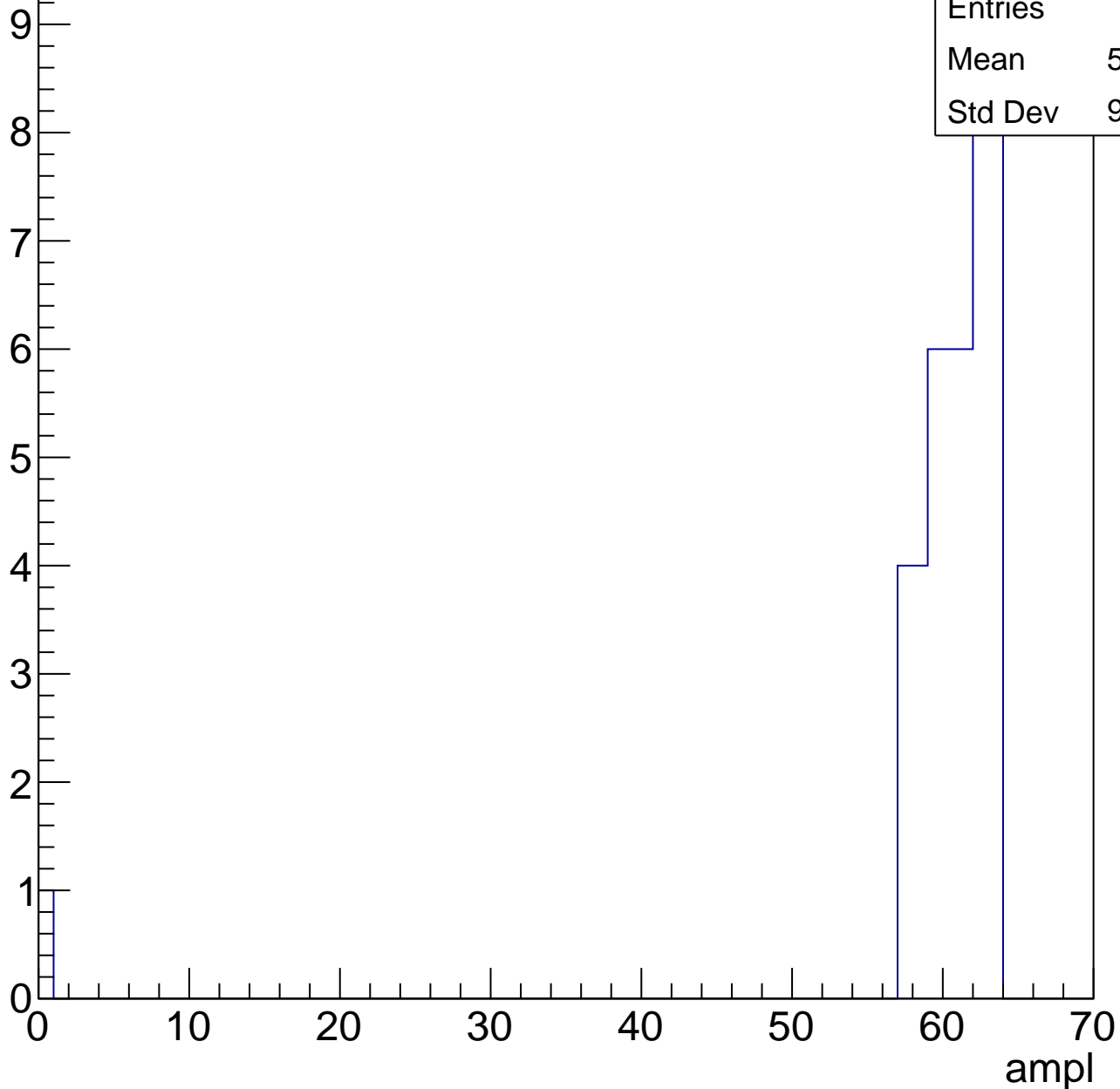
Entries	53
Mean	56.08
Std Dev	3.071



B0L002S, U5-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

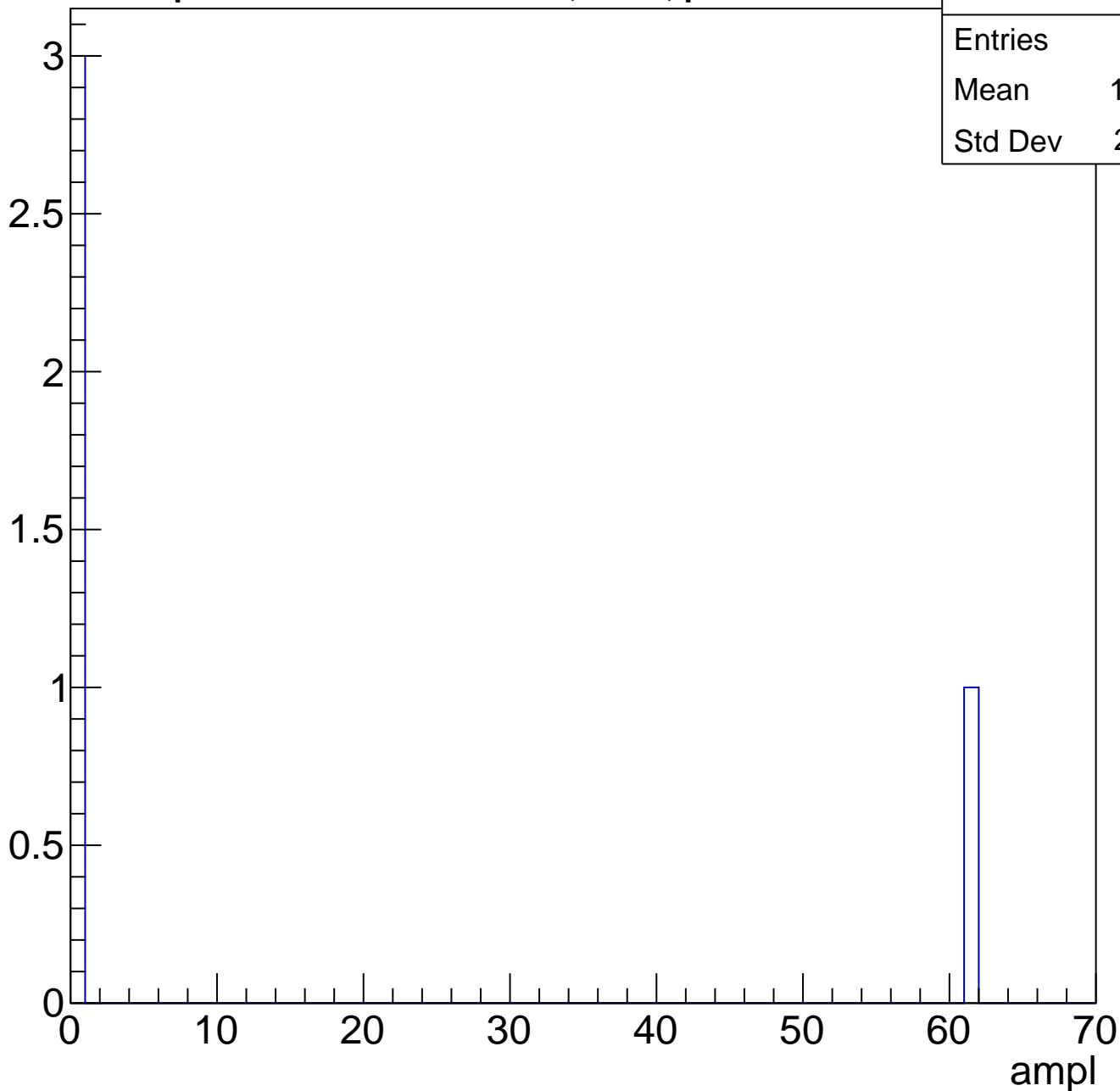
Entry



B0L002S, U5-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch118, adc0

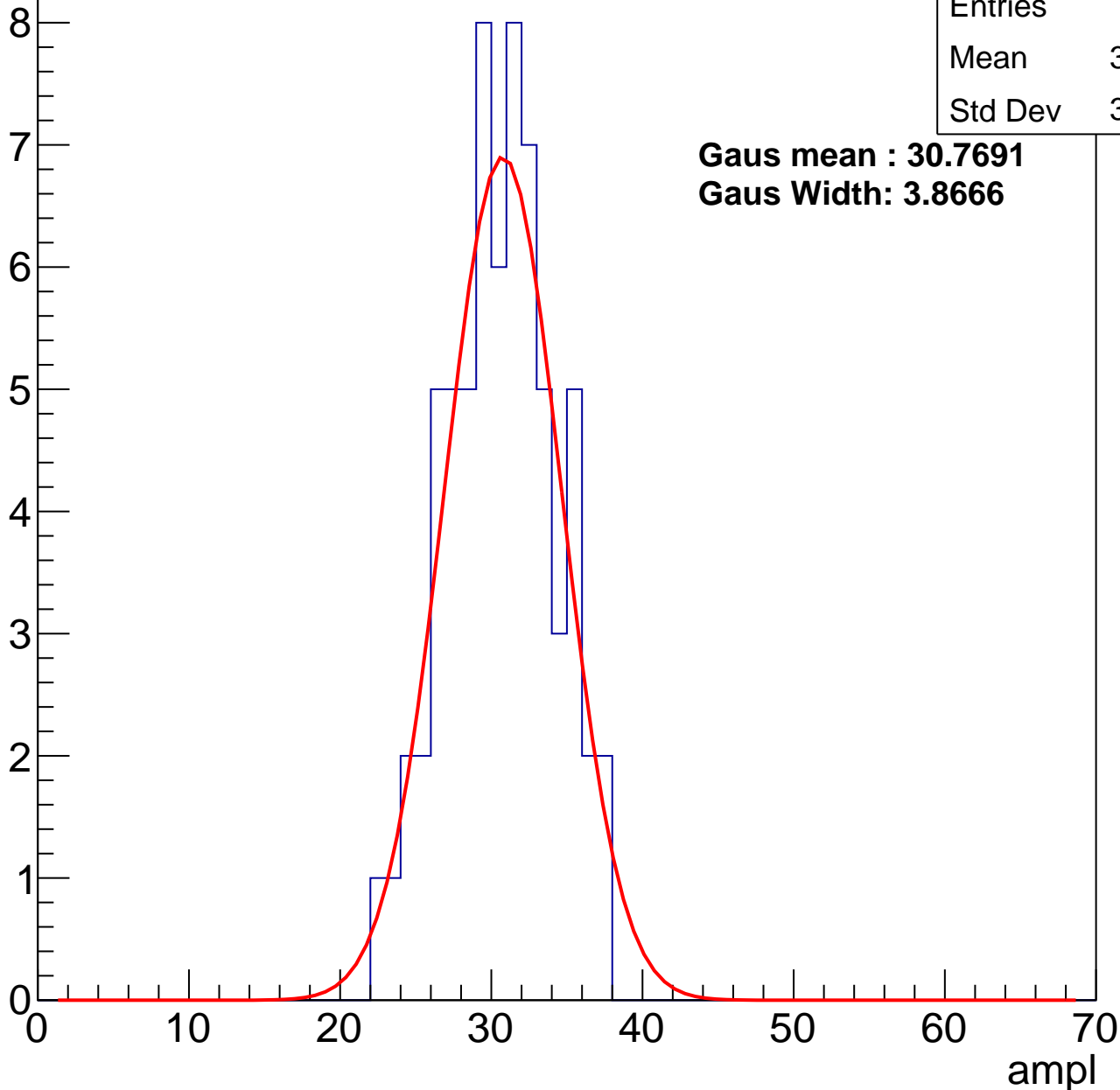
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.15
Std Dev	3.478

Gaus mean : 30.7691

Gaus Width: 3.8666



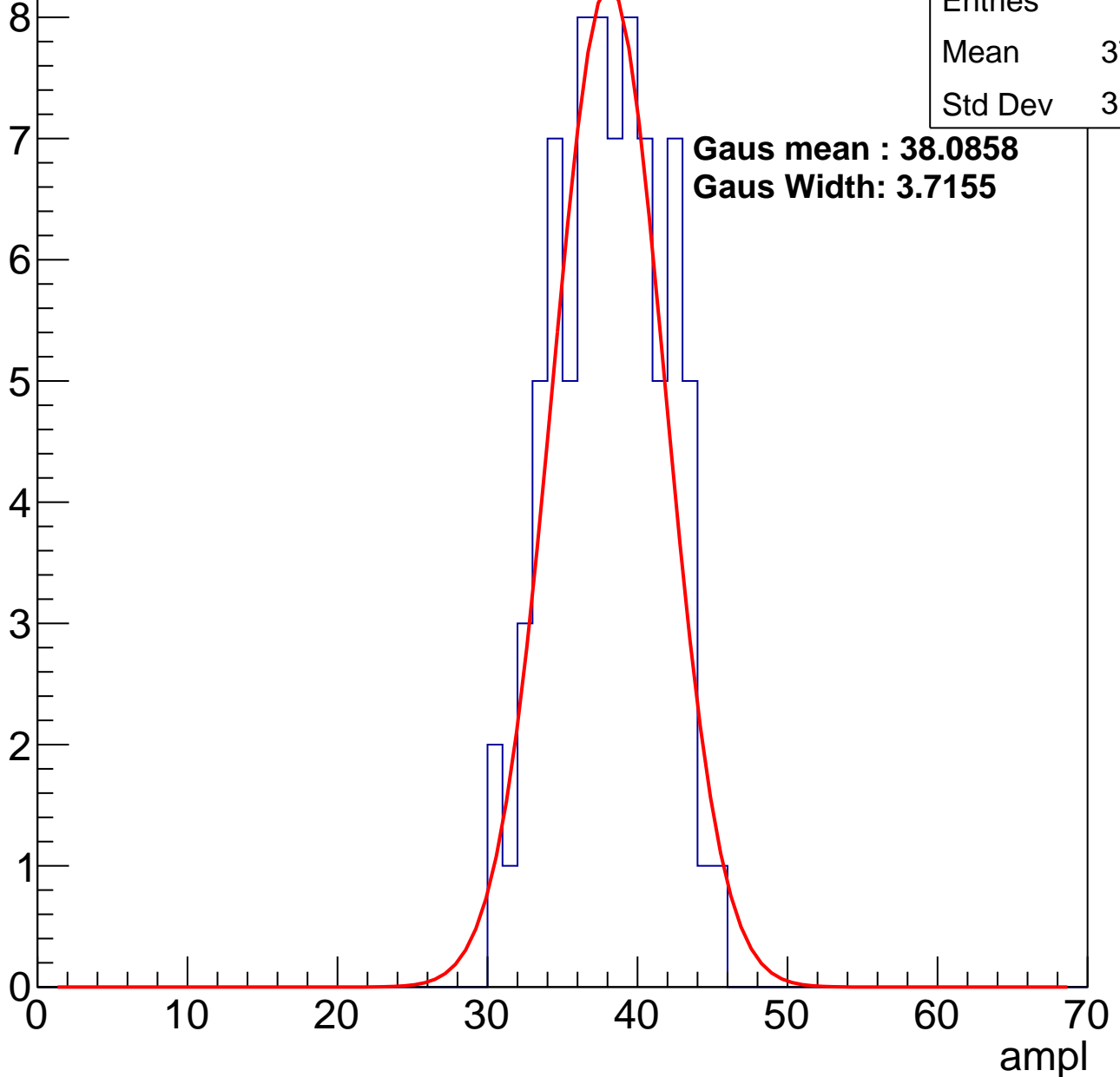
B0L002S, U5-ch118, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	37.62
Std Dev	3.533

Gaus mean : 38.0858
Gaus Width: 3.7155



B0L002S, U5-ch118, adc2

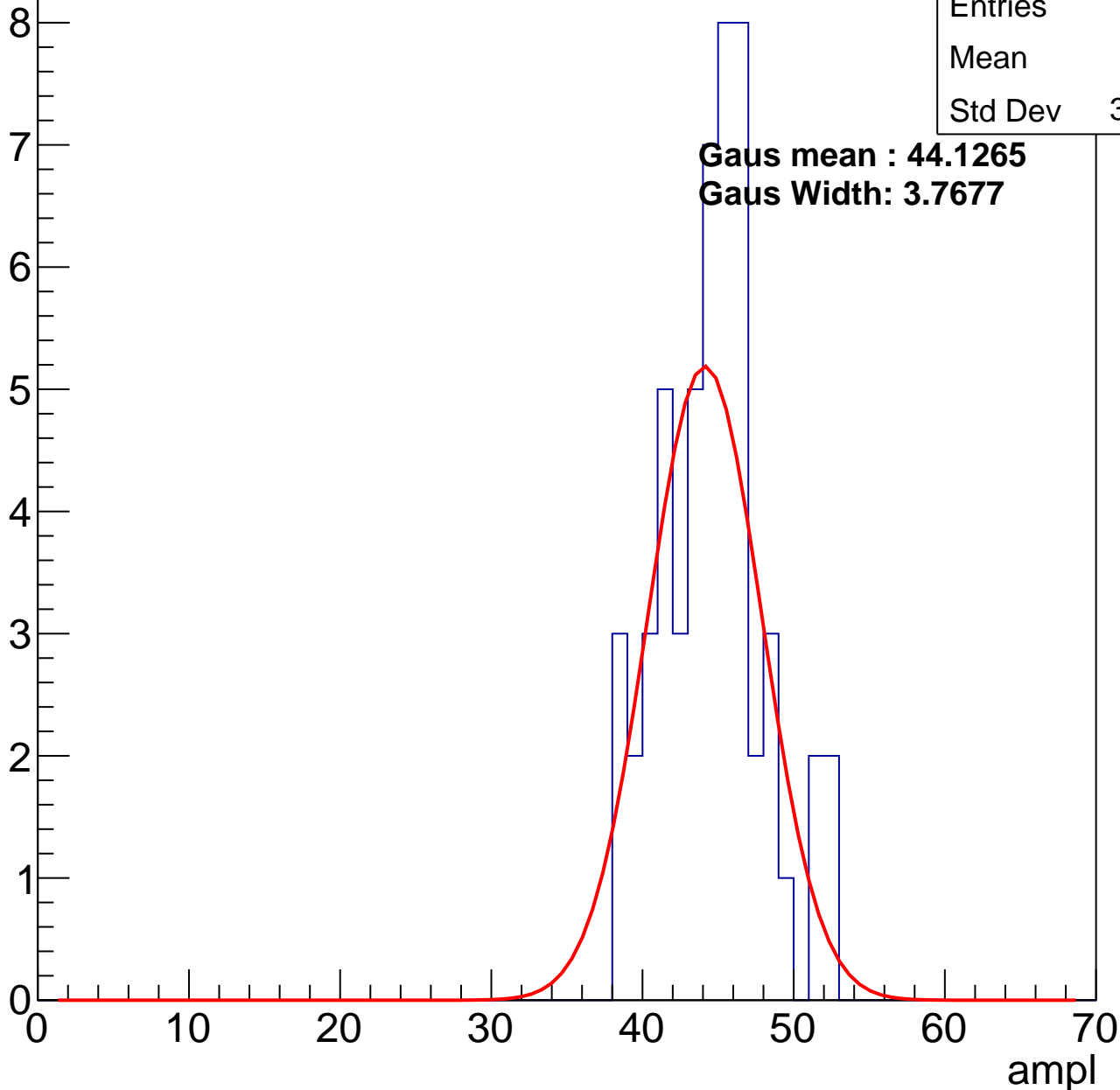
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	44.2
Std Dev	3.396

Gaus mean : 44.1265

Gaus Width: 3.7677



B0L002S, U5-ch118, adc3

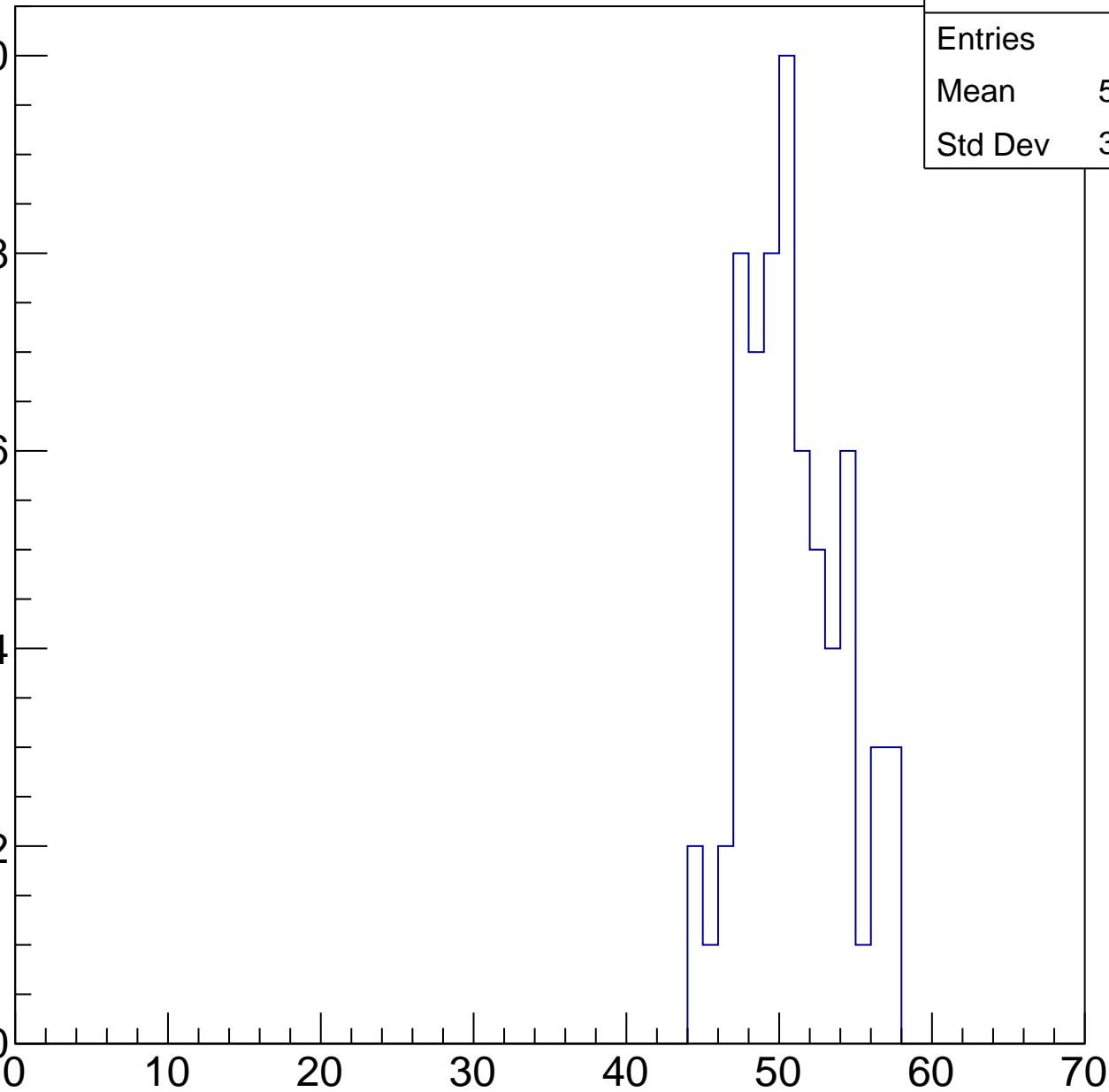
calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	50.38
Std Dev	3.175

Entry

10
8
6
4
2
0

ampl

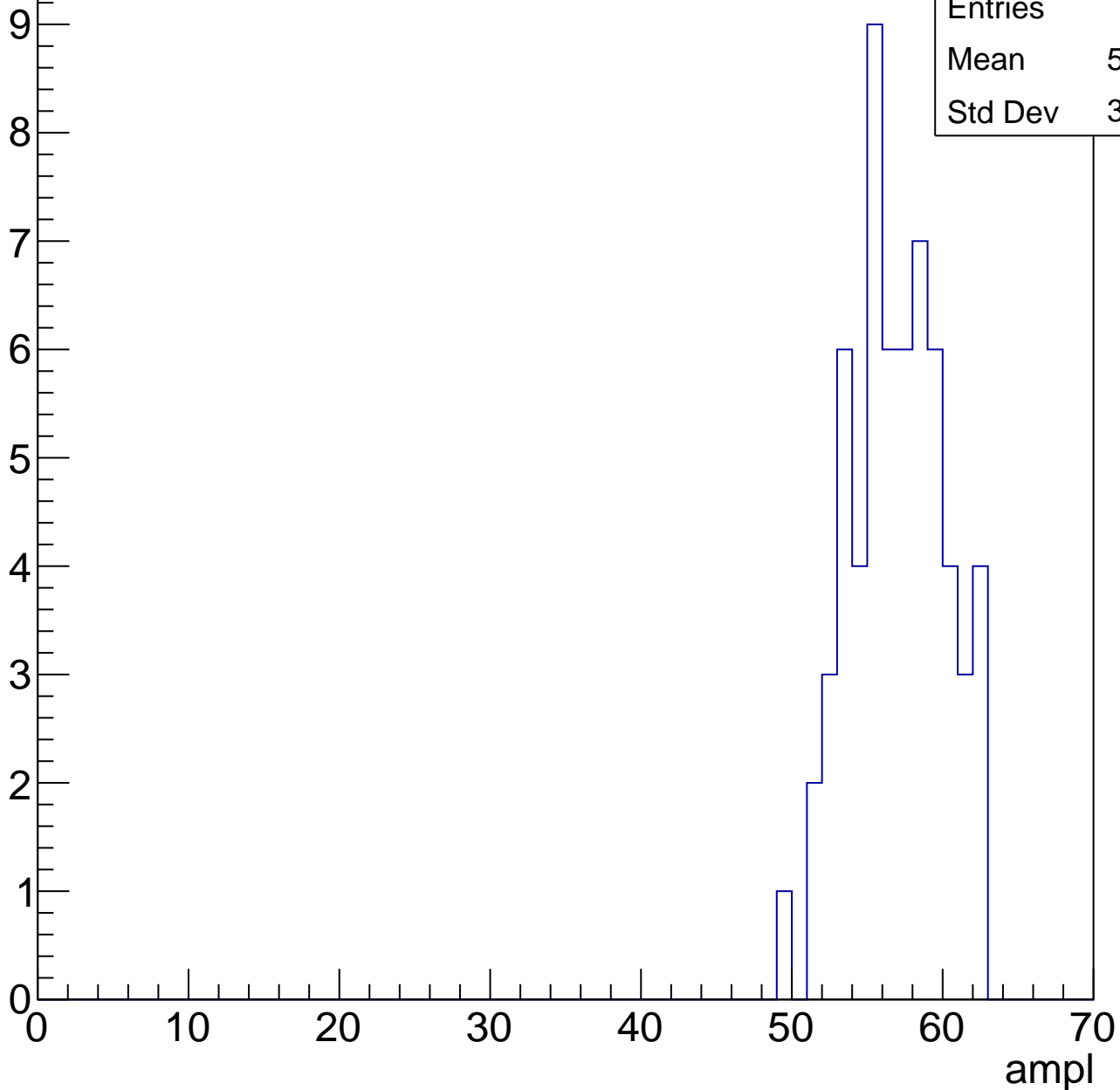


B0L002S, U5-ch118, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	56.48
Std Dev	3.092

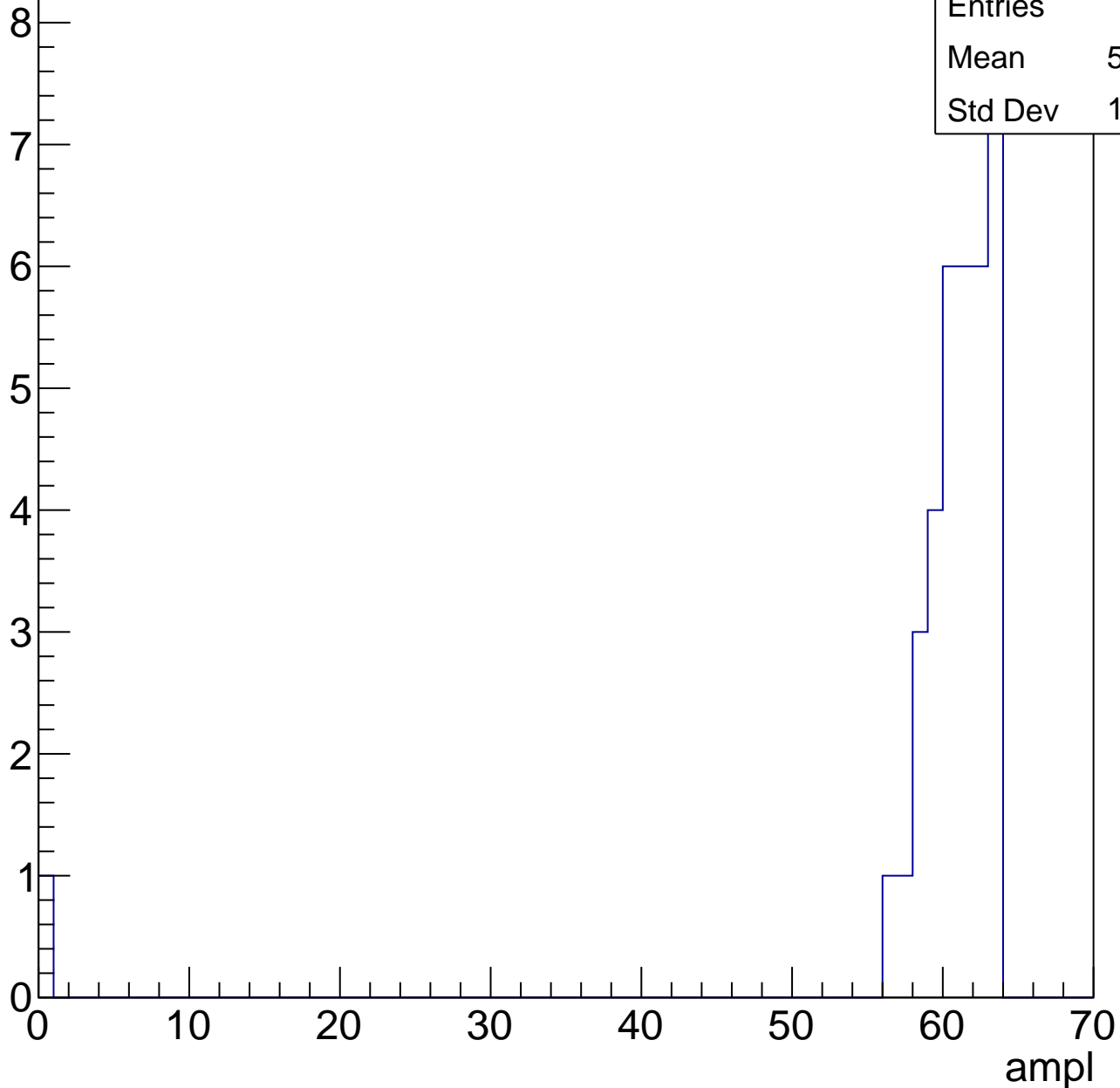


B0L002S, U5-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.03
Std Dev	10.15



B0L002S, U5-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U5-ch119, adc0

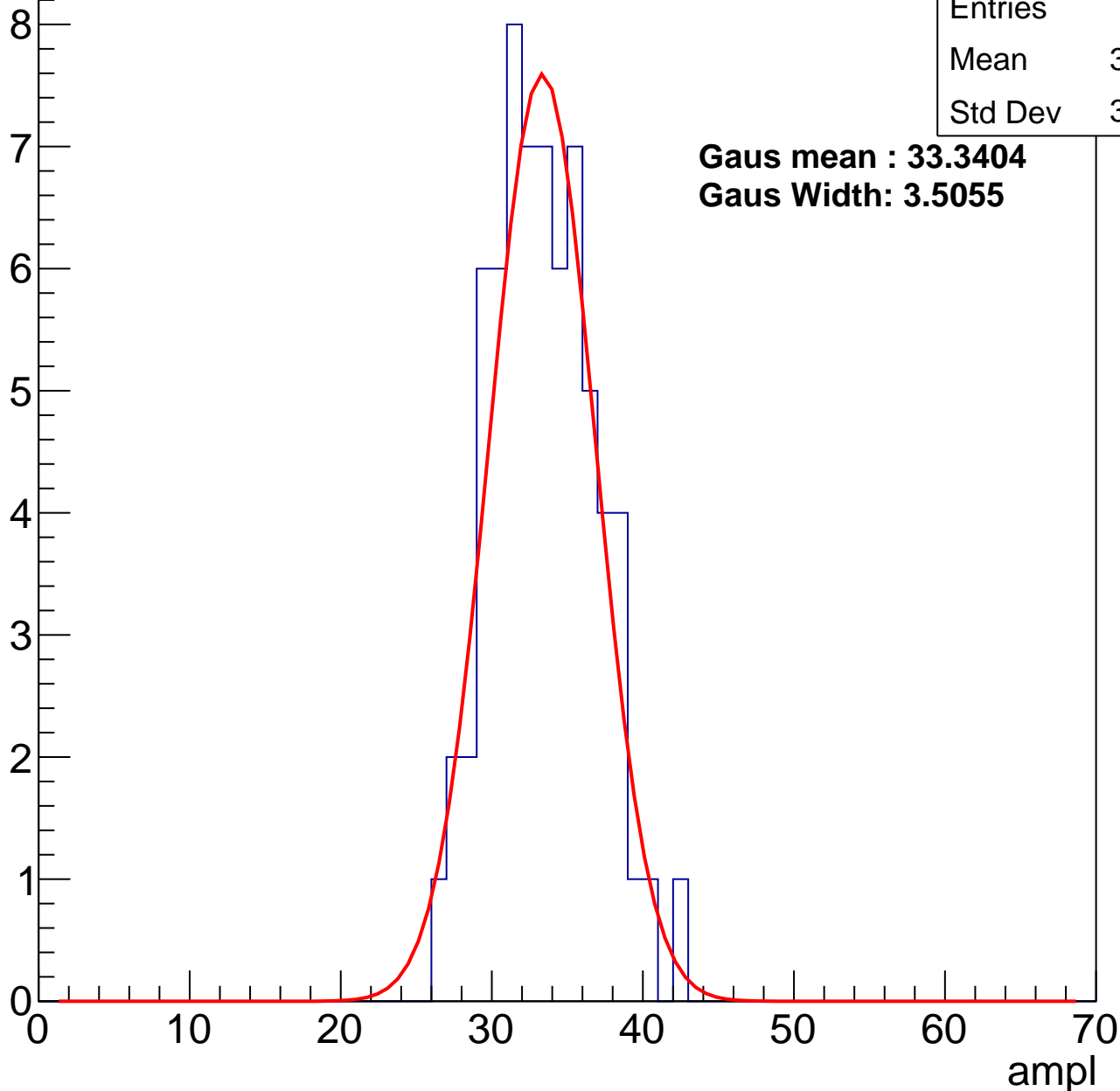
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	32.99
Std Dev	3.363

Gaus mean : 33.3404

Gaus Width: 3.5055



B0L002S, U5-ch119, adc1

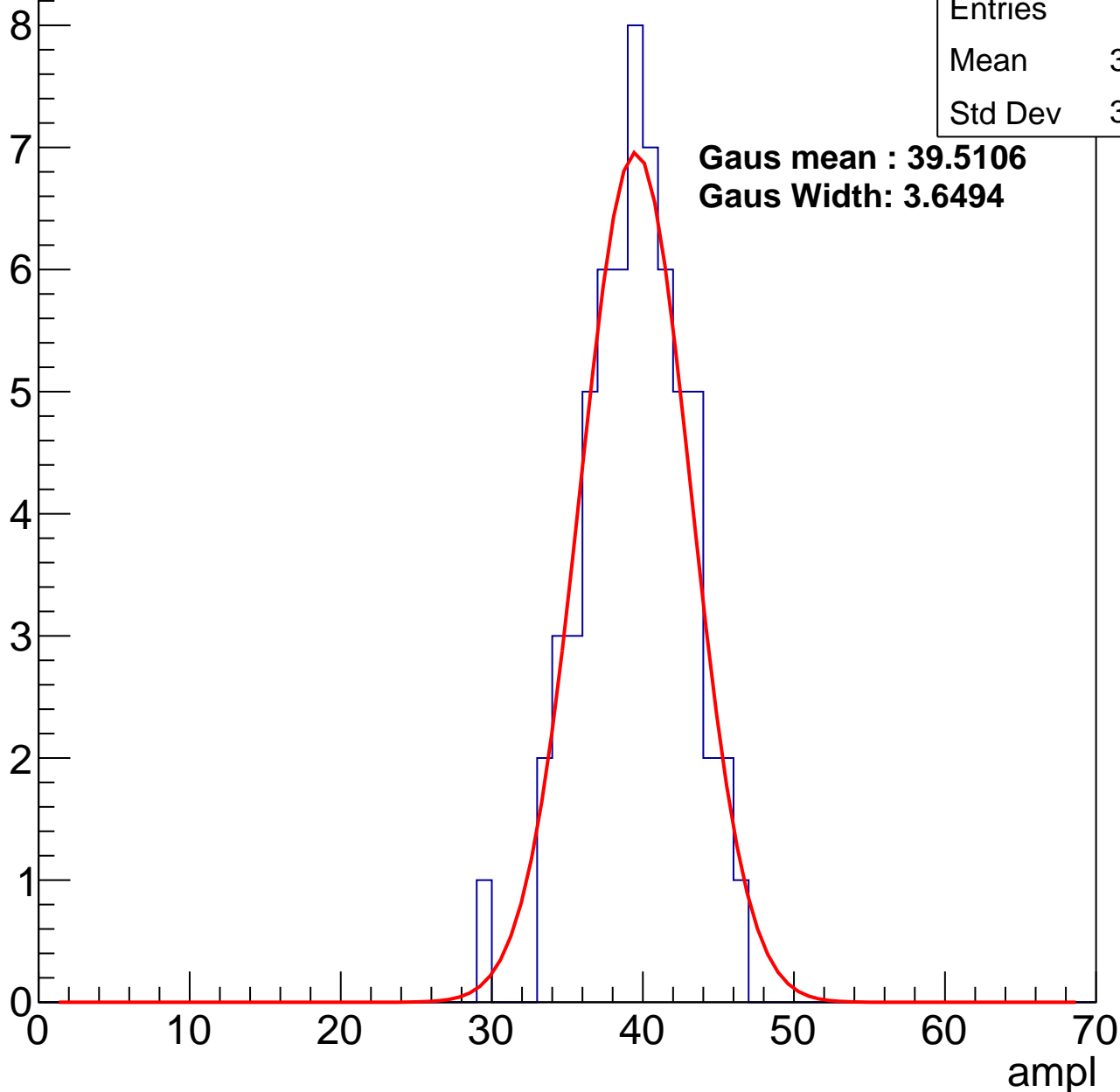
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	39.02
Std Dev	3.353

Gaus mean : 39.5106

Gaus Width: 3.6494



B0L002S, U5-ch119, adc2

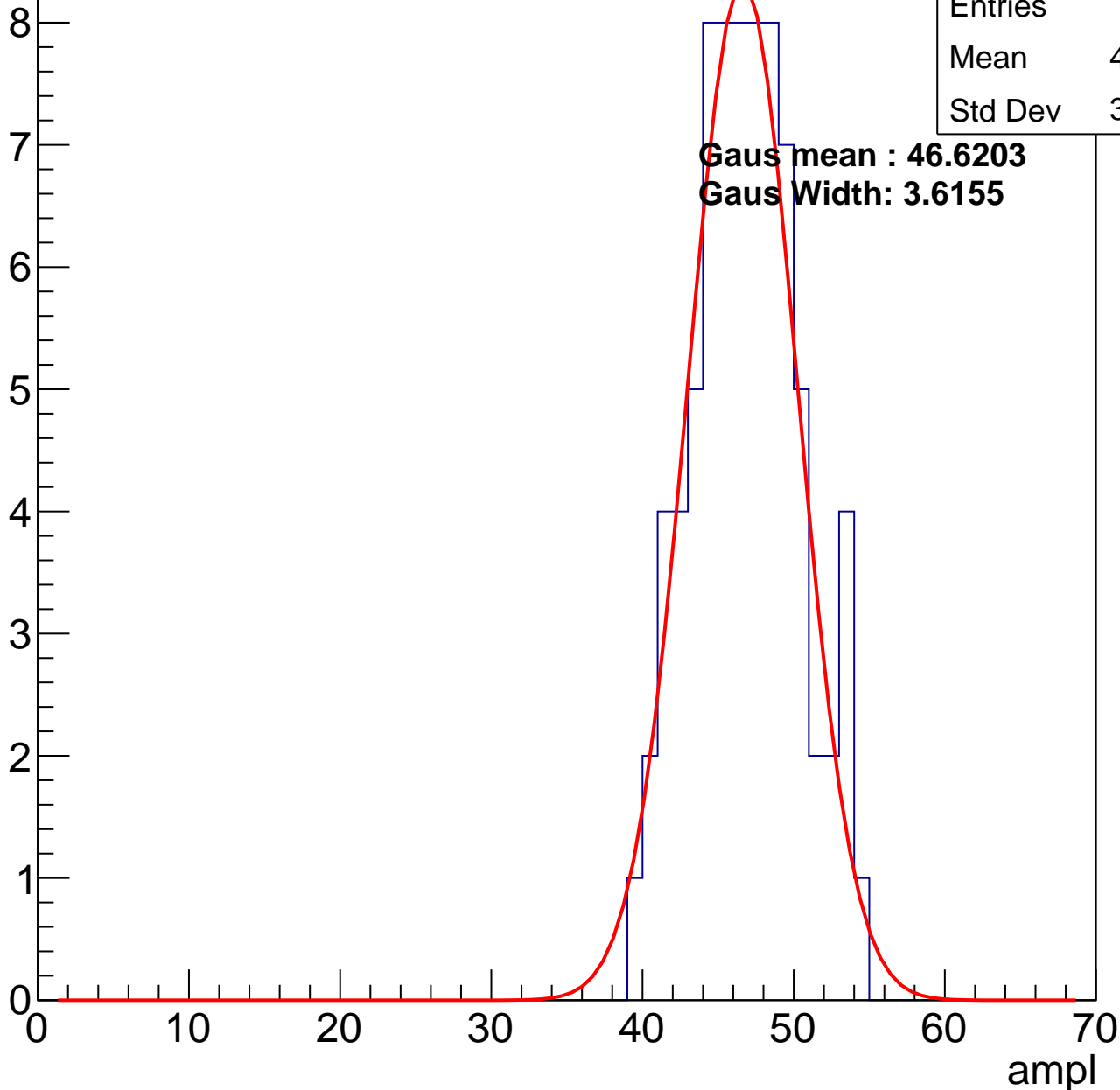
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	46.38
Std Dev	3.464

Gaus mean : 46.6203

Gaus Width: 3.6155

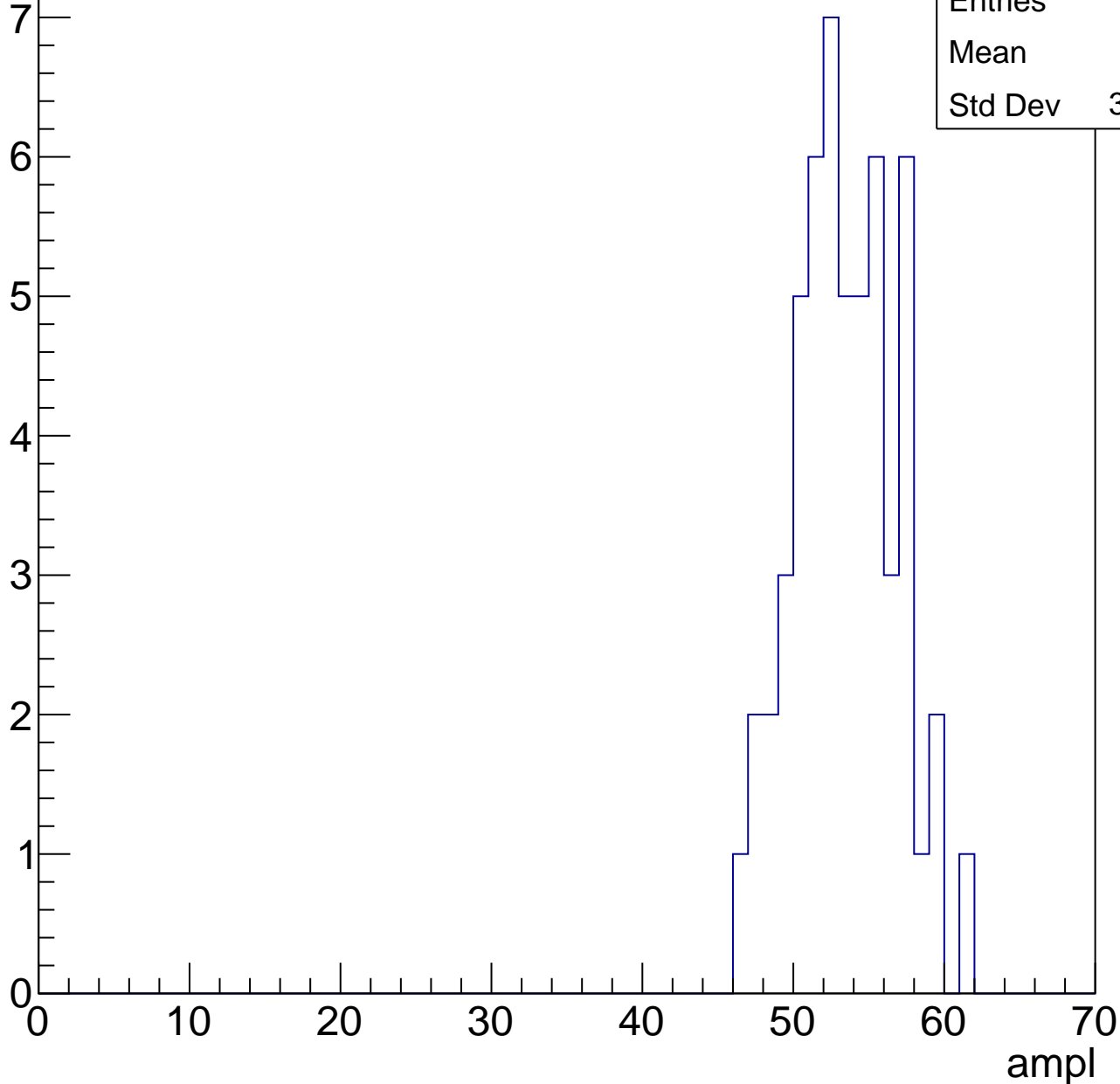


B0L002S, U5-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	53
Std Dev	3.325

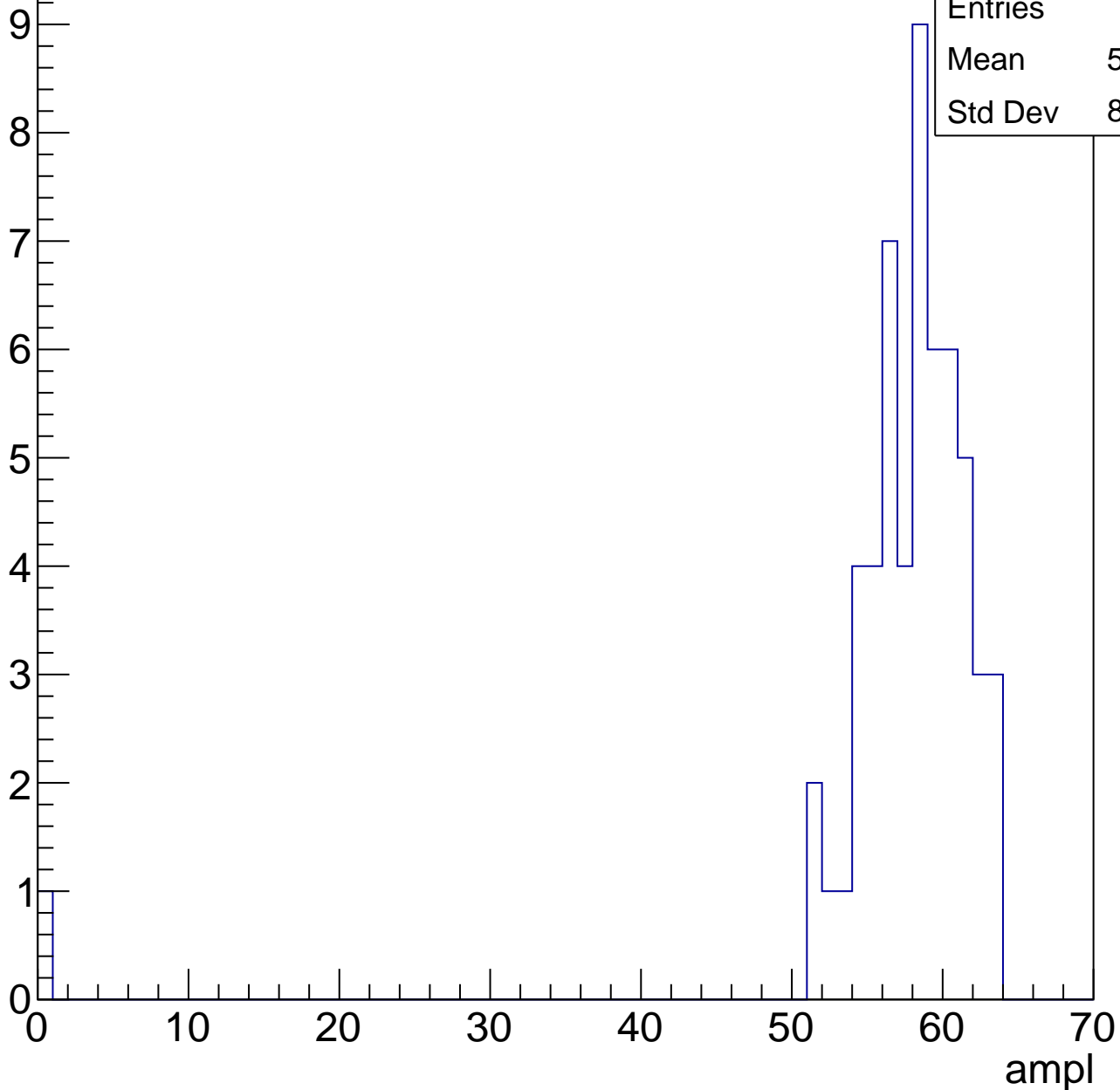


B0L002S, U5-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.77
Std Dev	8.203

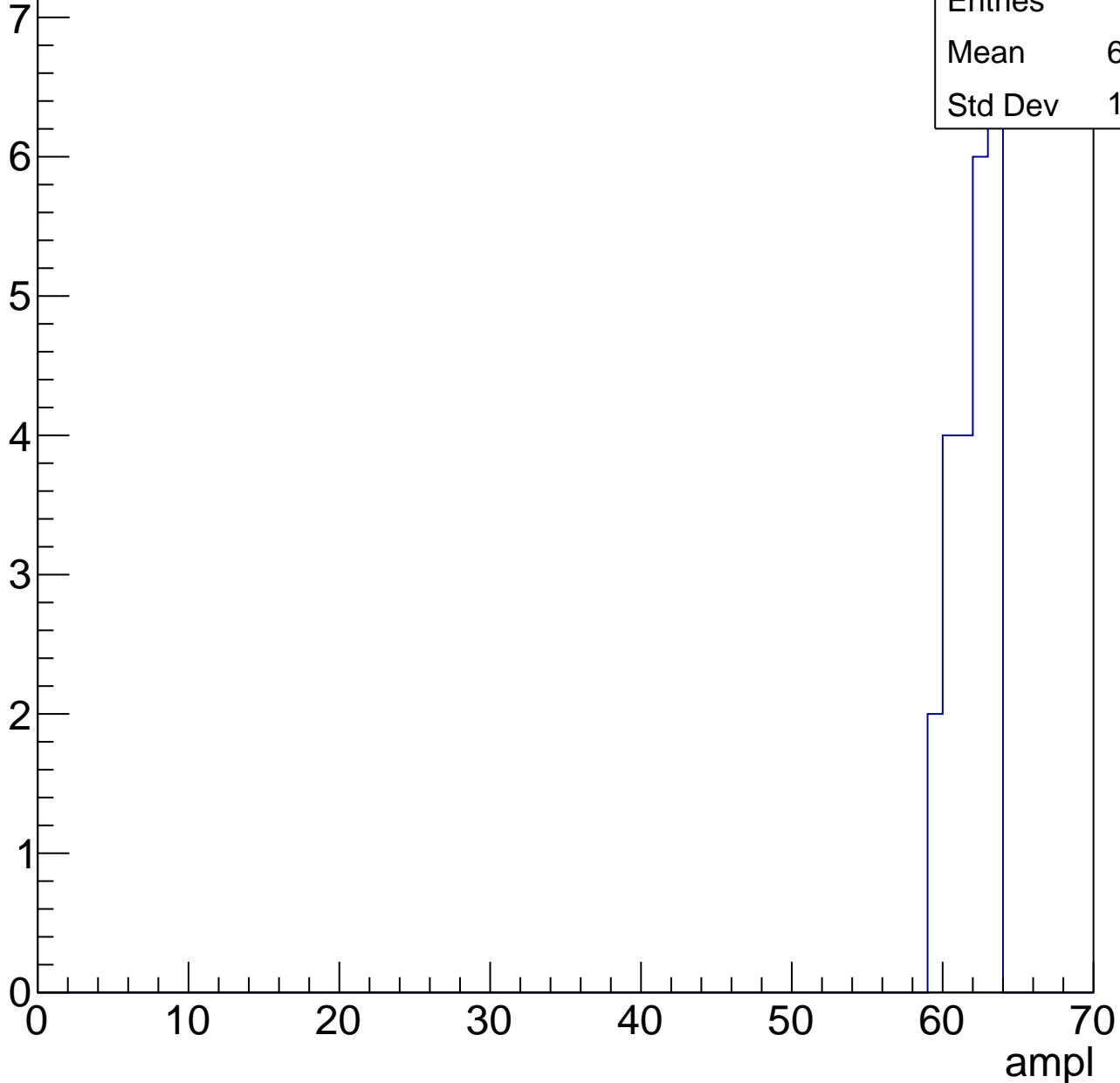


B0L002S, U5-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	61.52
Std Dev	1.314



B0L002S, U5-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch120, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	31.05
Std Dev	4.817

Gaus mean : 31.5501

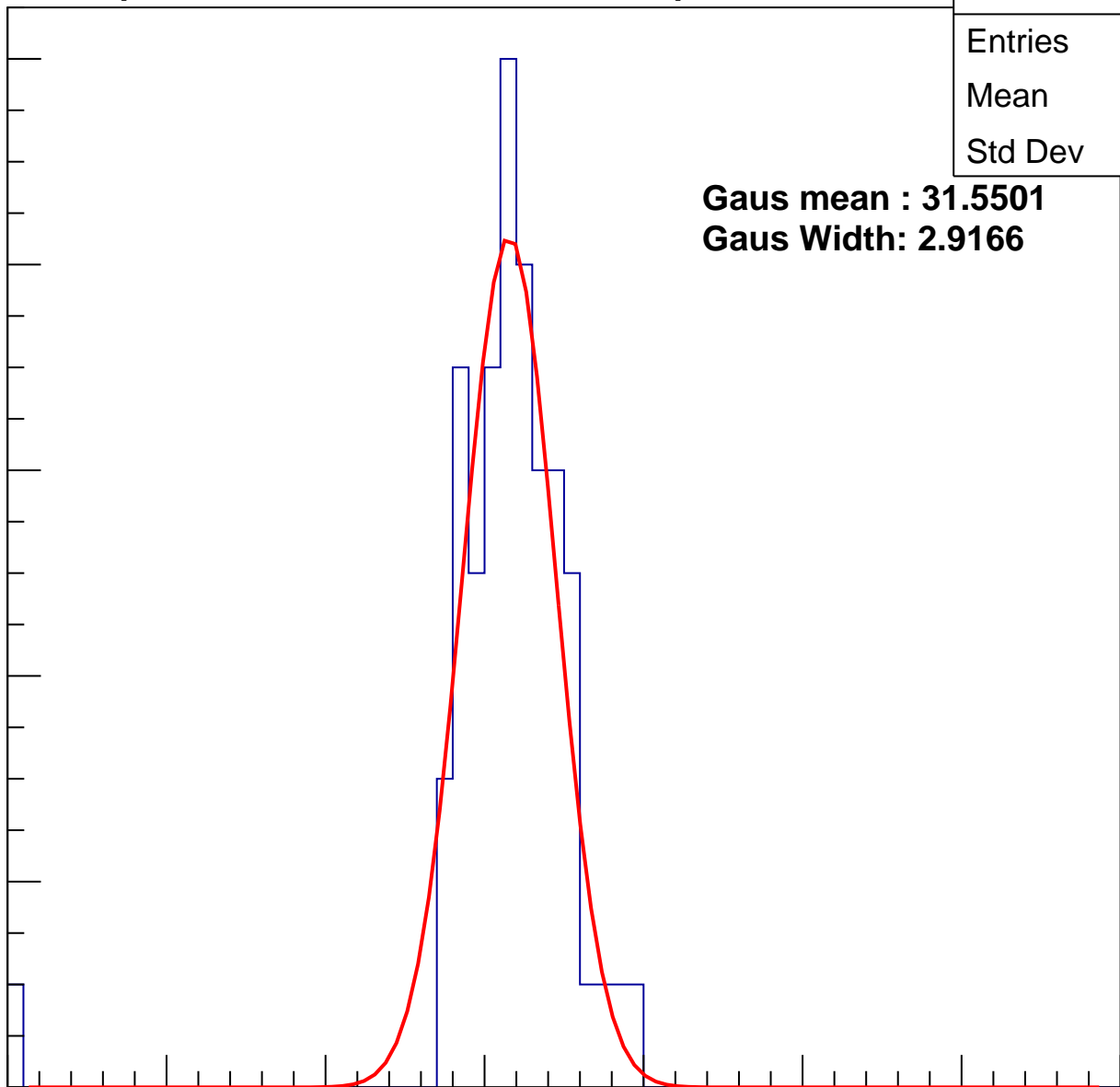
Gaus Width: 2.9166

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch120, adc1

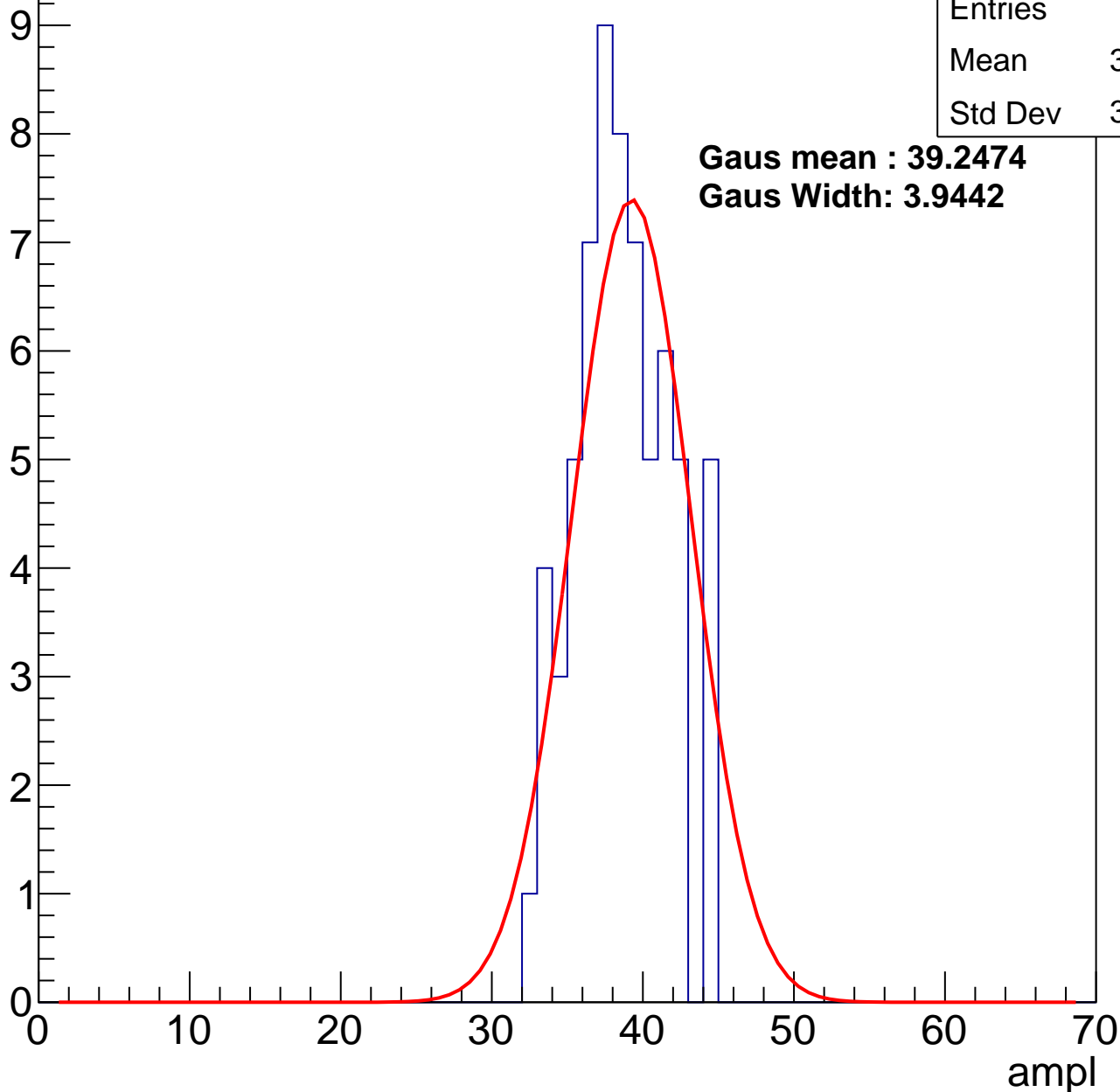
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	38.14
Std Dev	3.053

Gaus mean : 39.2474

Gaus Width: 3.9442



B0L002S, U5-ch120, adc2

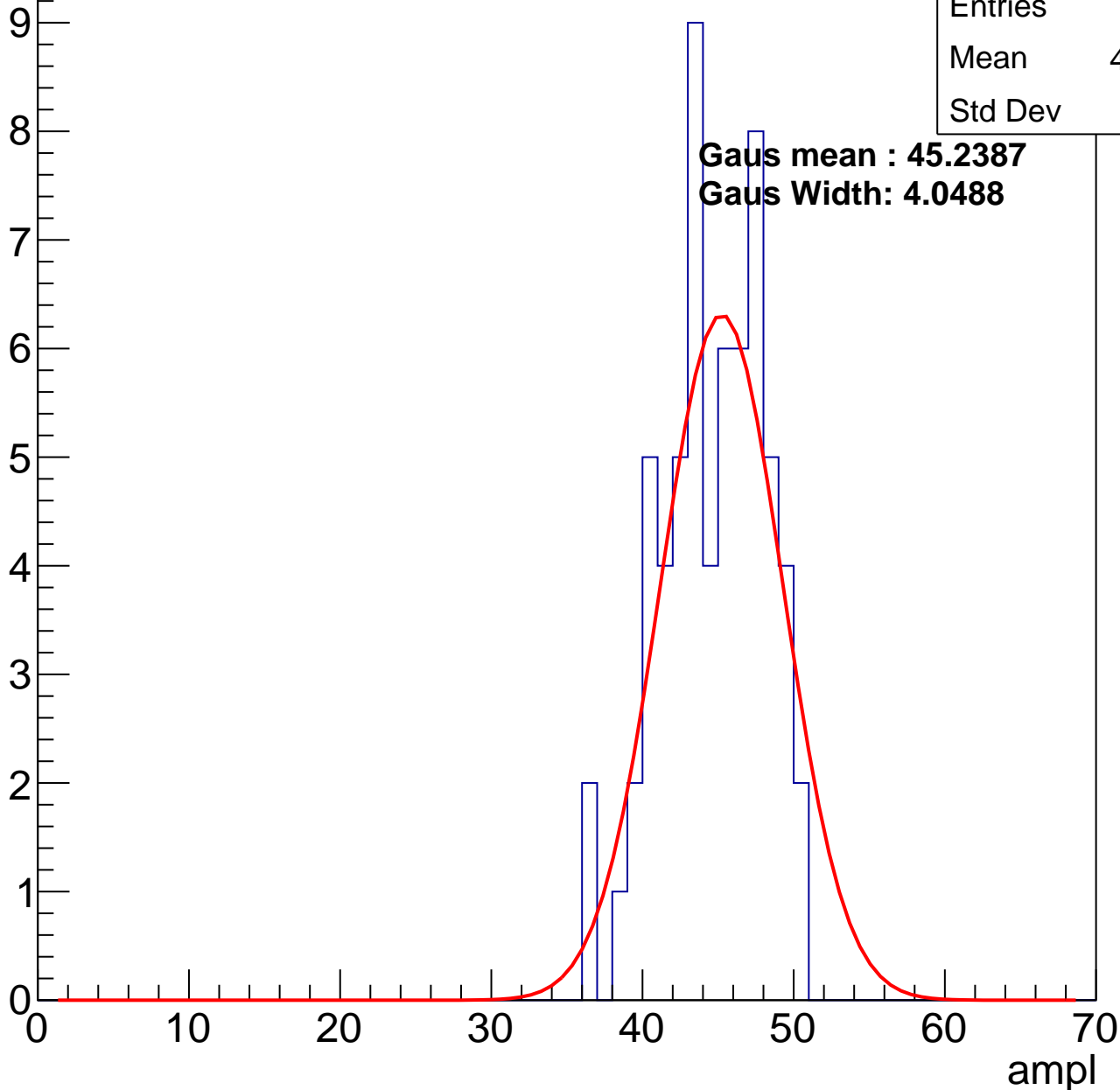
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.17
Std Dev	3.36

Gaus mean : 45.2387

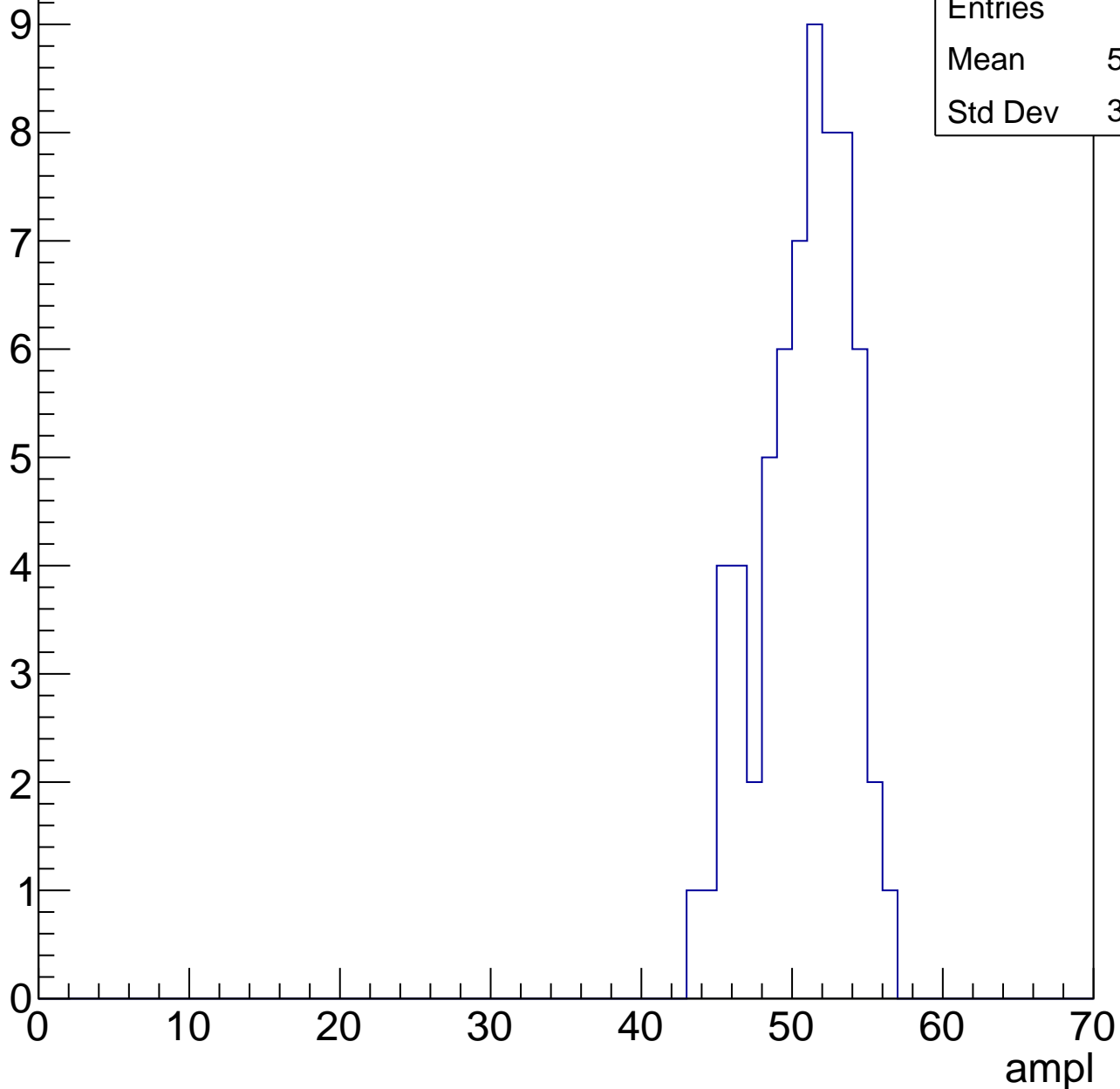
Gaus Width: 4.0488



B0L002S, U5-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



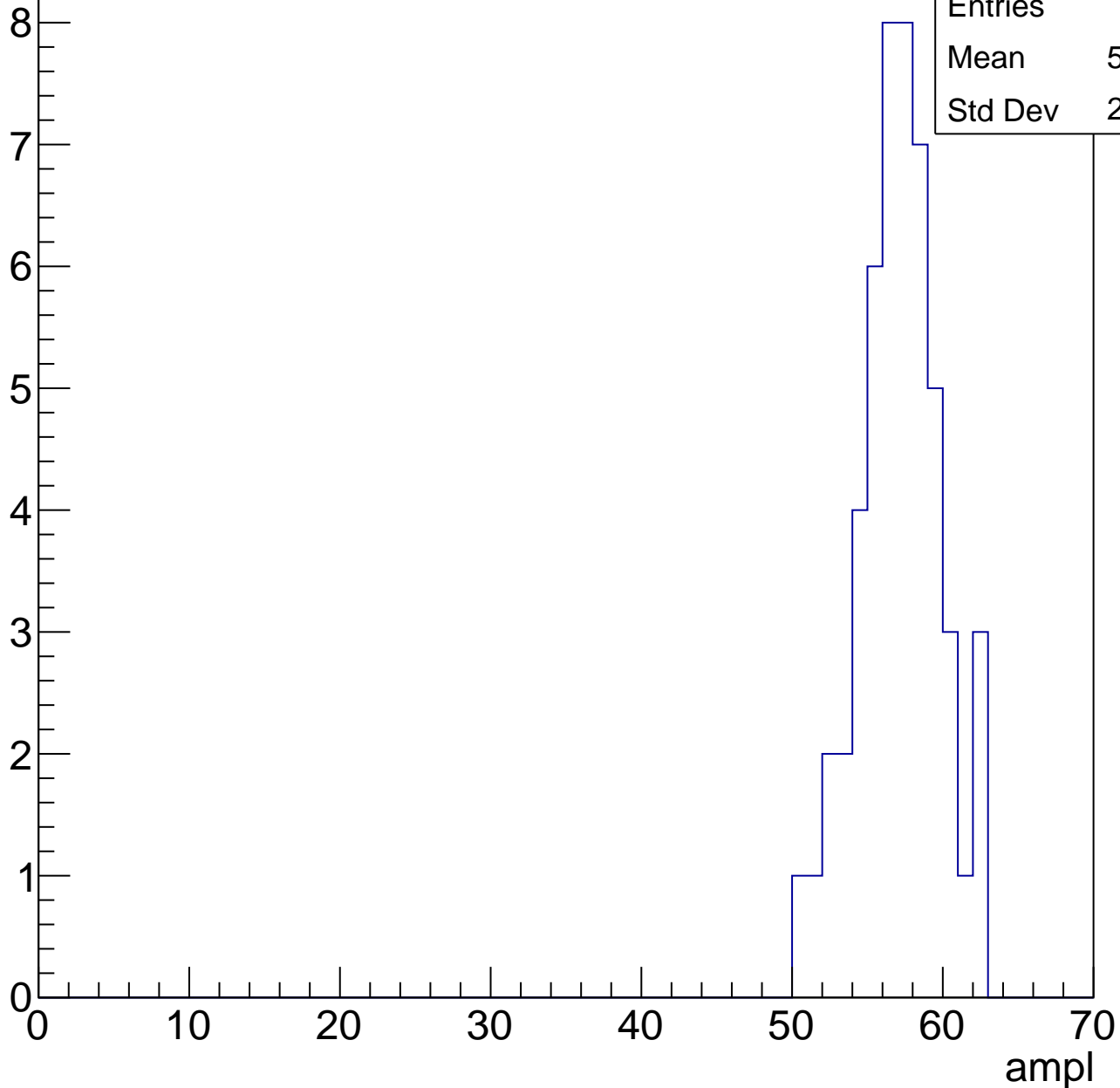
Entries	64
Mean	50.28
Std Dev	3.018

B0L002S, U5-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.65
Std Dev	2.707

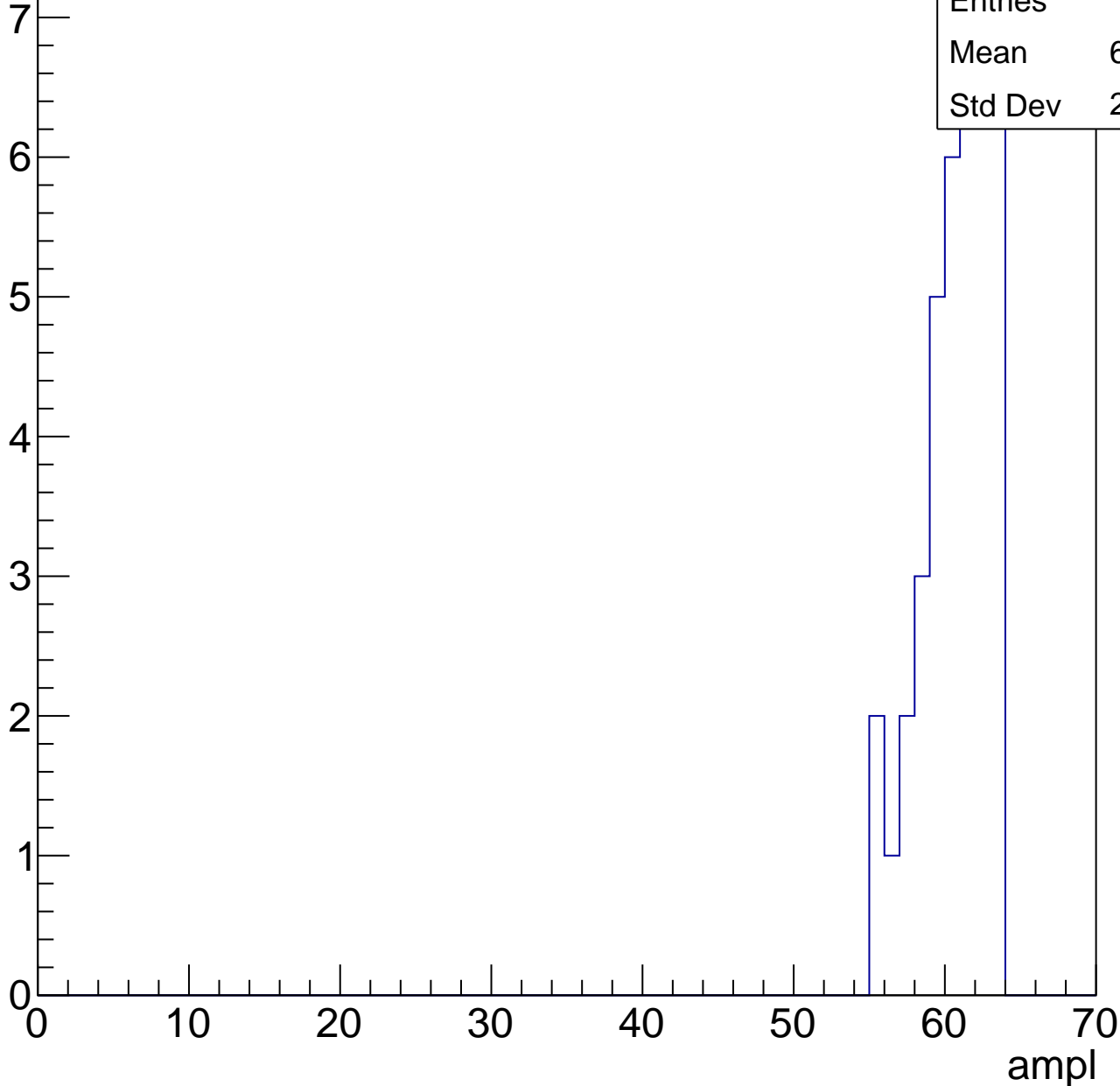


B0L002S, U5-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

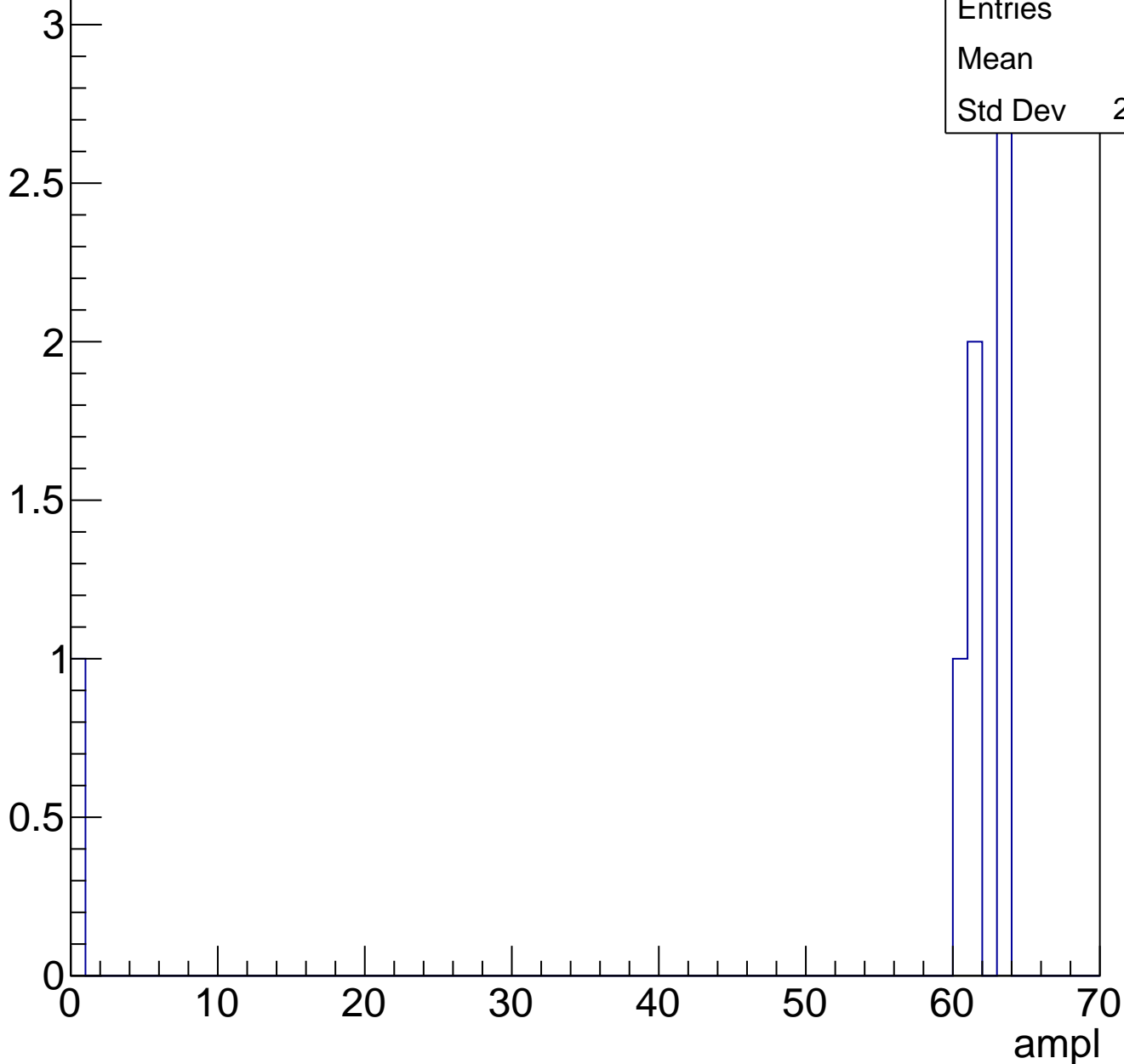
Entries	40
Mean	60.27
Std Dev	2.213



B0L002S, U5-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

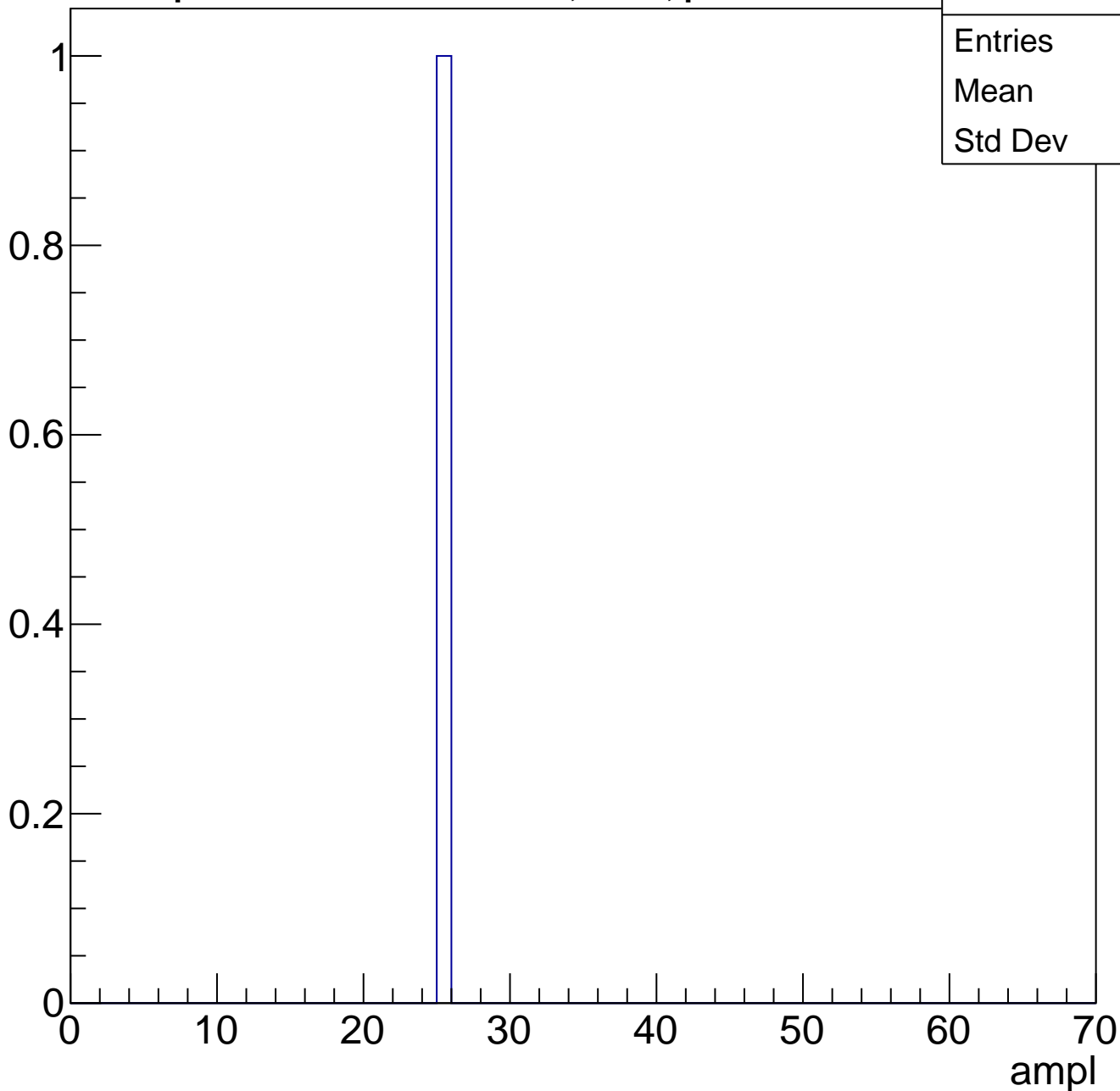
Entry



B0L002S, U5-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch121, adc0

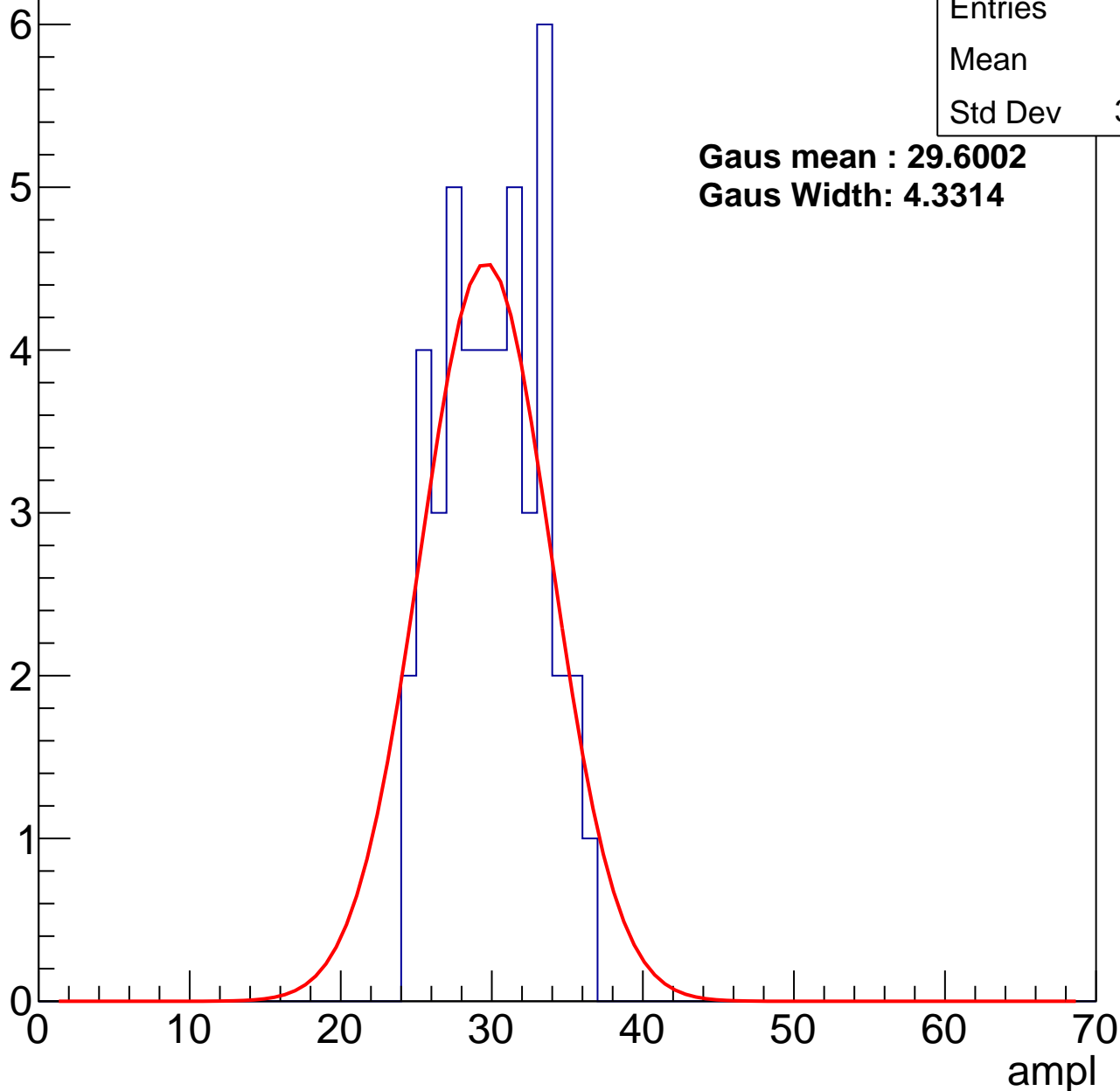
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	29.6
Std Dev	3.221

Gaus mean : 29.6002

Gaus Width: 4.3314



B0L002S, U5-ch121, adc1

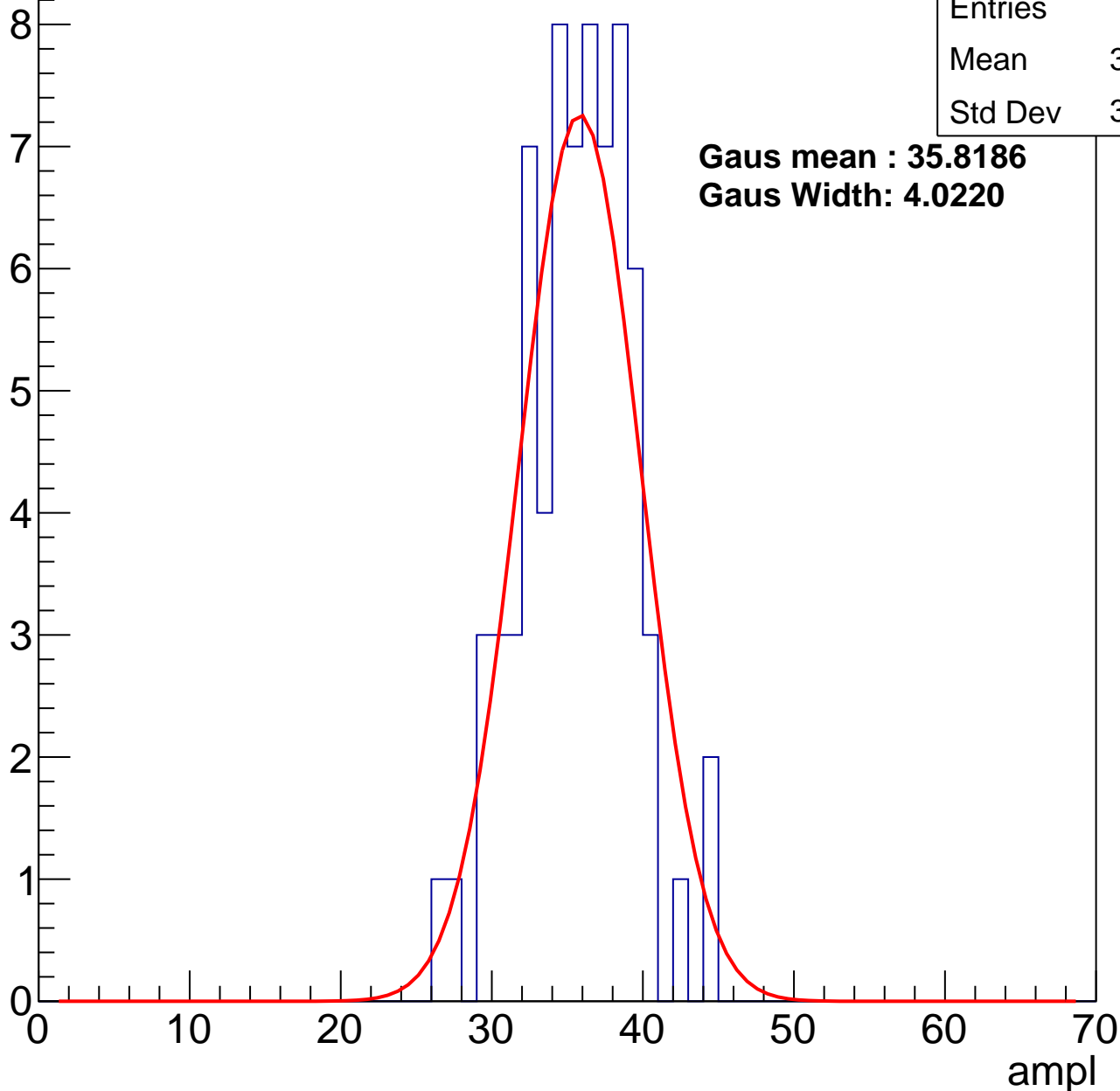
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	35.15
Std Dev	3.635

Gaus mean : 35.8186

Gaus Width: 4.0220



B0L002S, U5-ch121, adc2

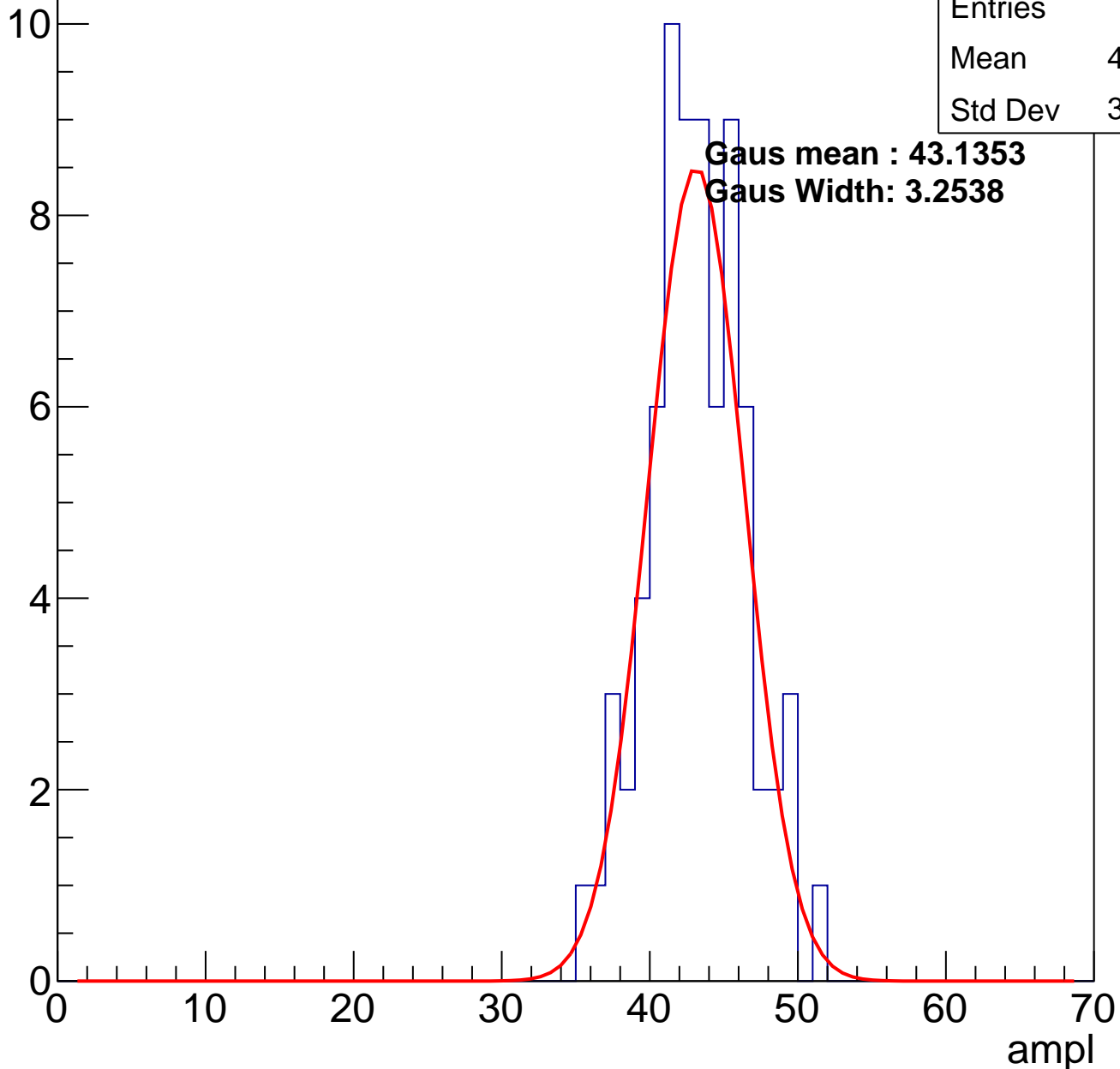
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	42.73
Std Dev	3.252

Gaus mean : 43.1353

Gaus Width: 3.2538

Entry

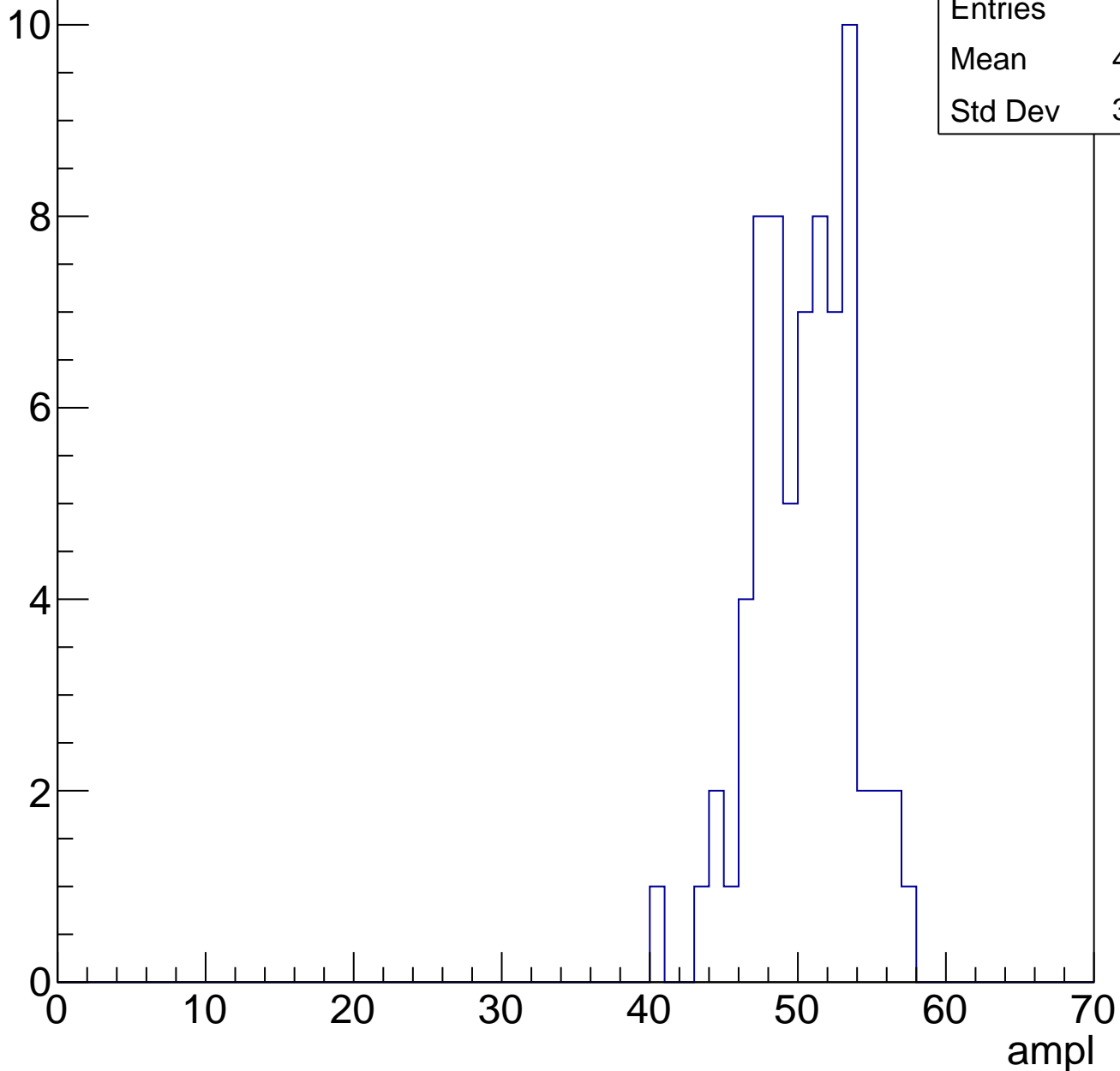


B0L002S, U5-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	49.91
Std Dev	3.291

Entry

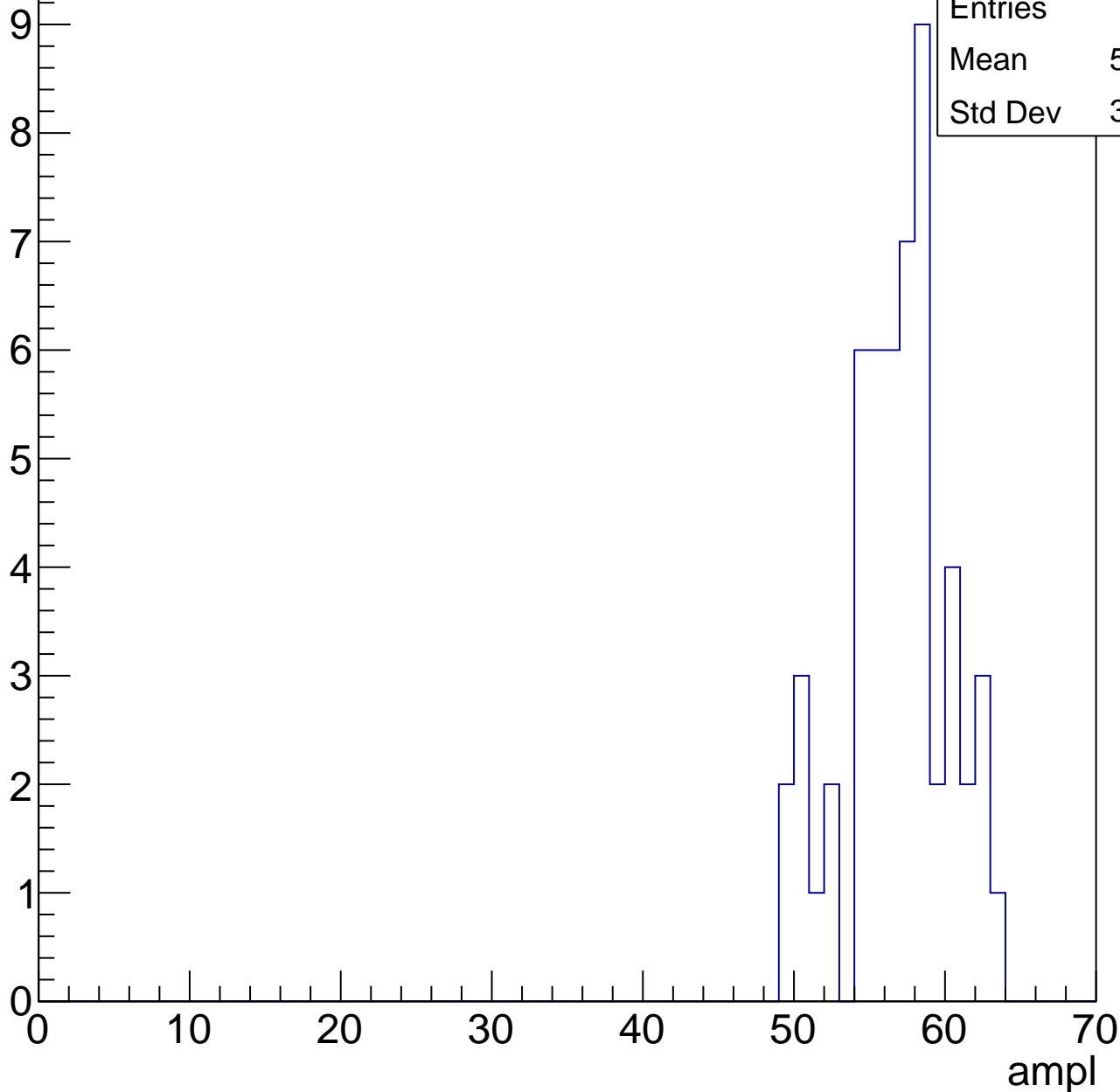


B0L002S, U5-ch121, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	56.35
Std Dev	3.384

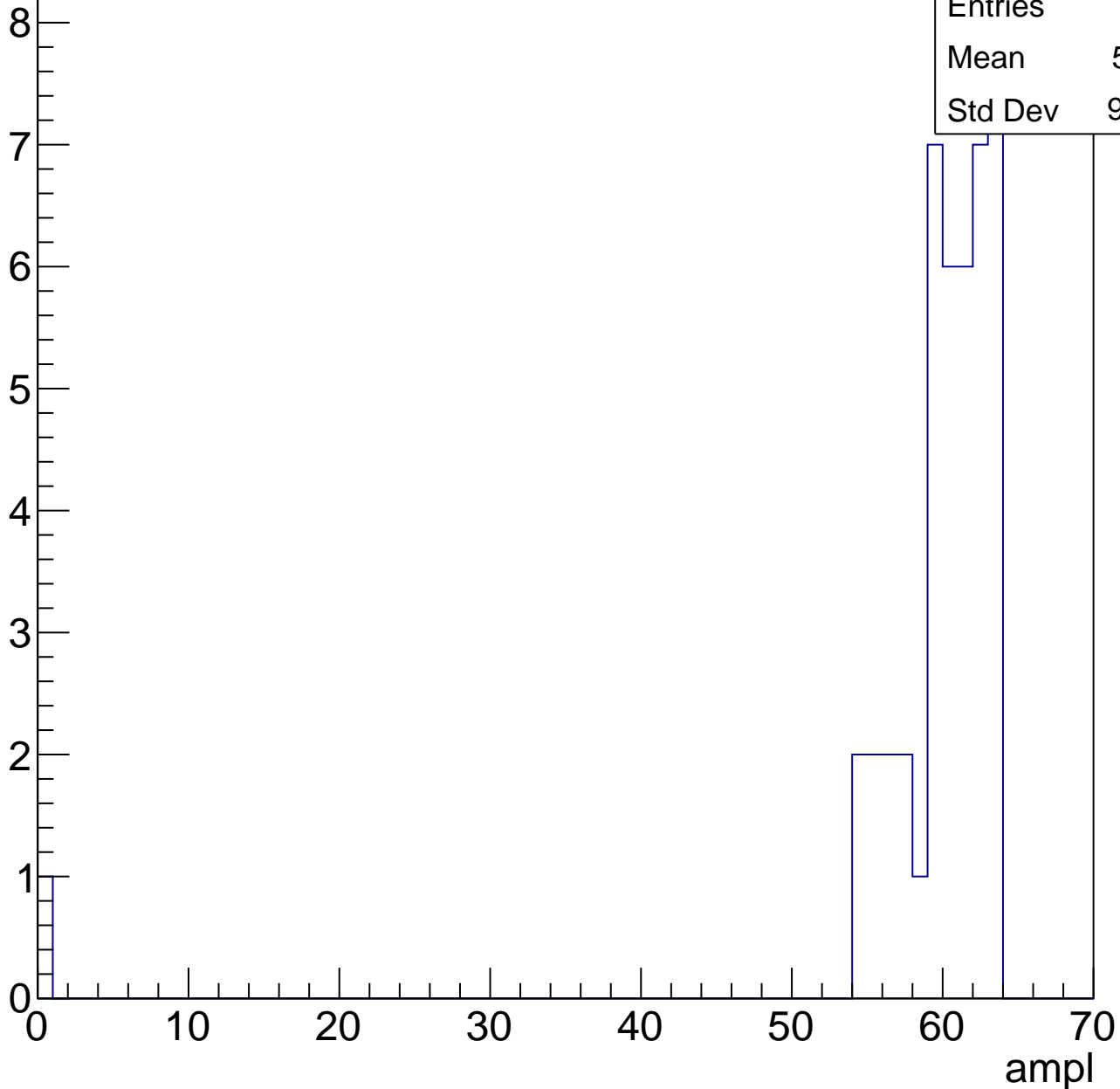


B0L002S, U5-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

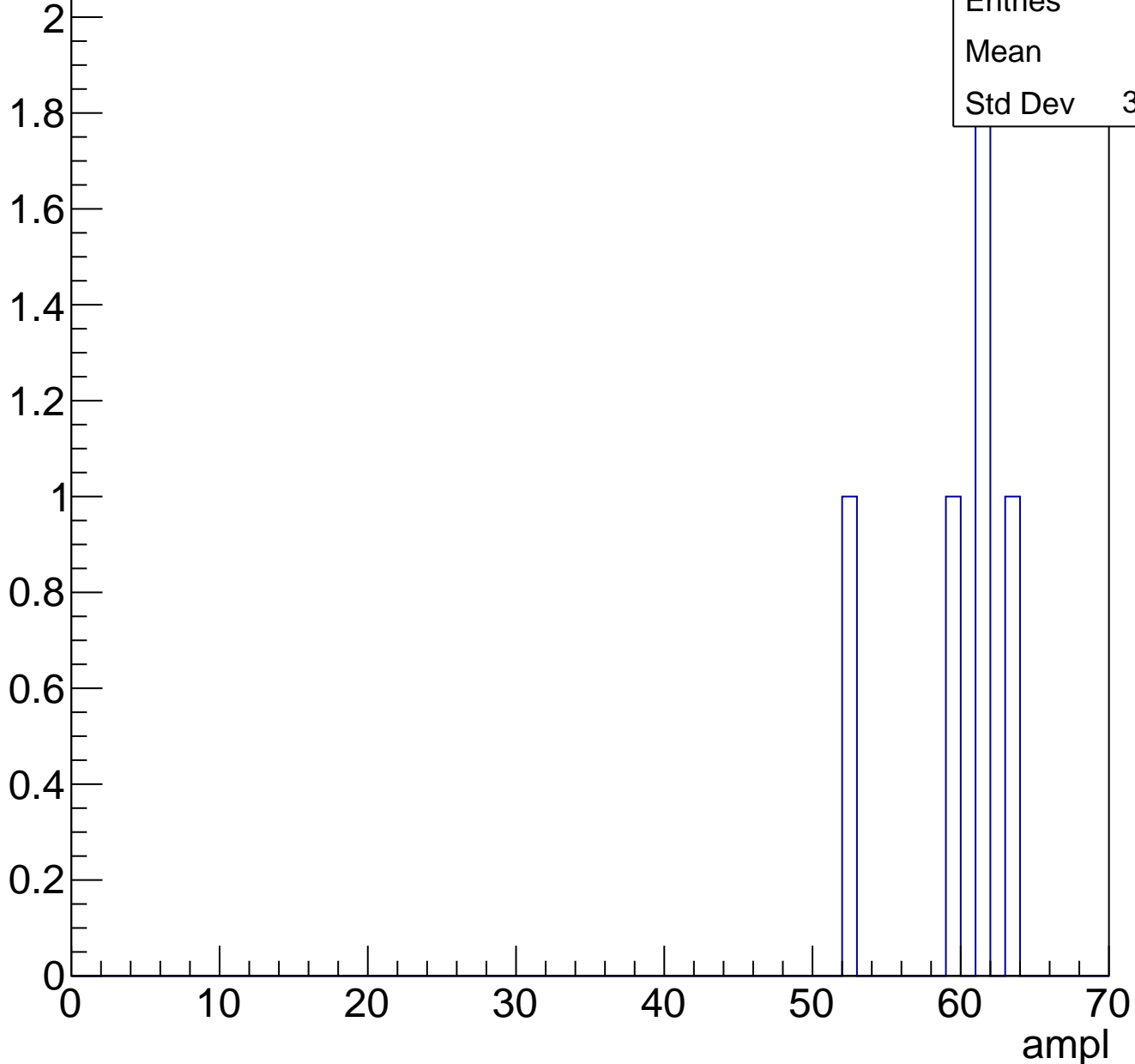
Entries	44
Mean	58.61
Std Dev	9.299



B0L002S, U5-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	59.2
Std Dev	3.816

B0L002S, U5-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U5-ch122, adc0

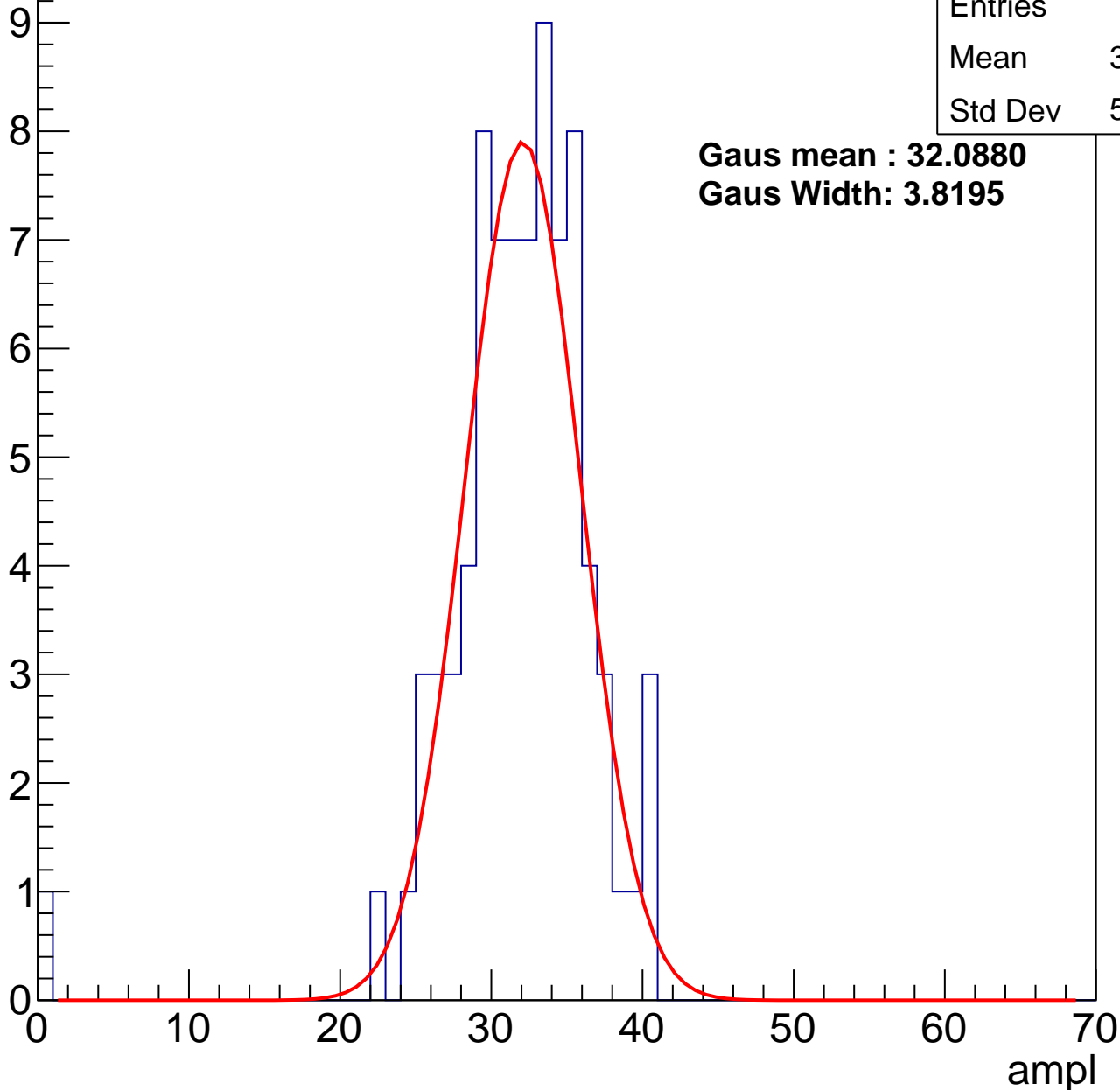
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	31.38
Std Dev	5.192

Gaus mean : 32.0880

Gaus Width: 3.8195



B0L002S, U5-ch122, adc1

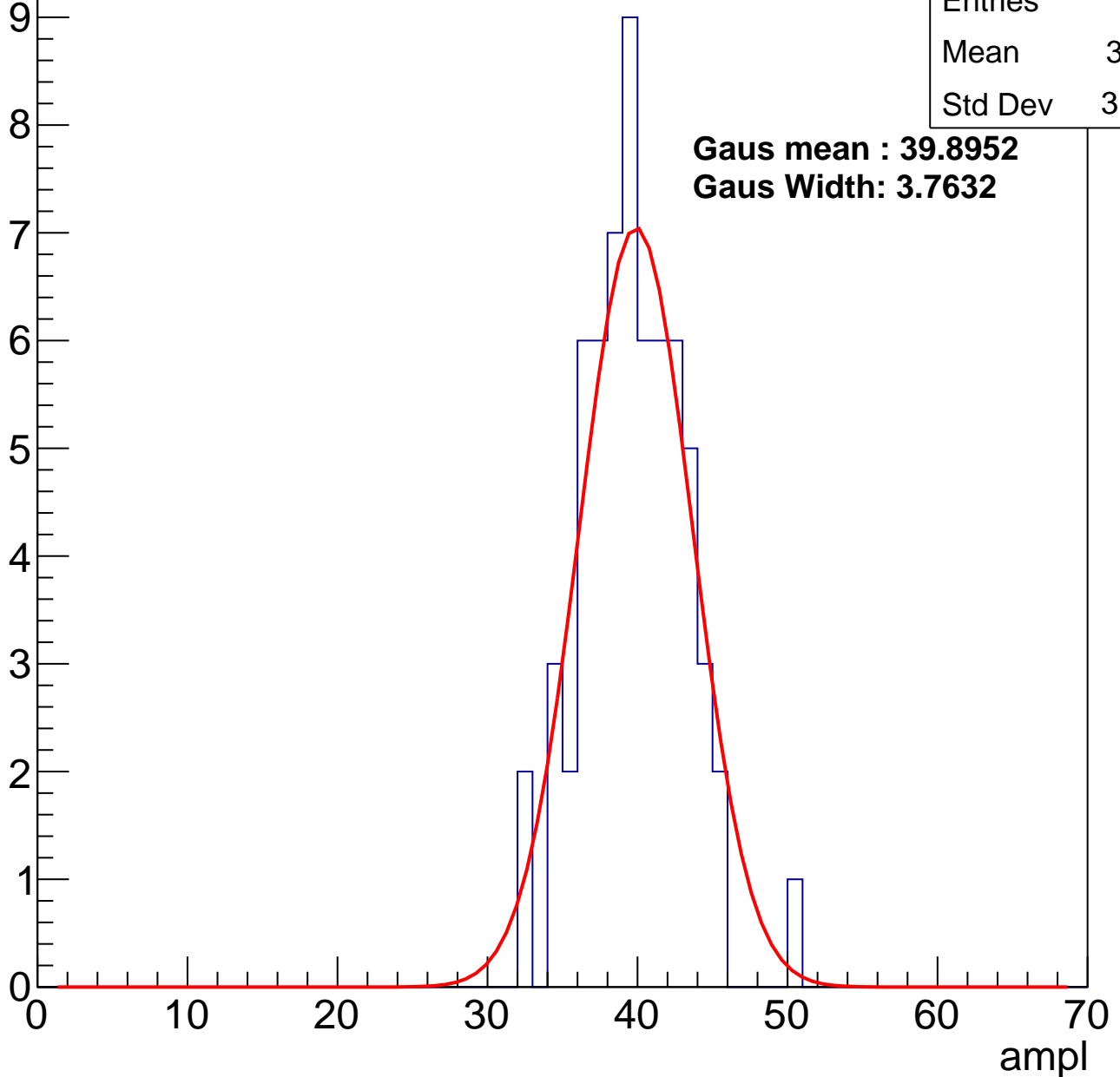
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	39.31
Std Dev	3.344

Gaus mean : 39.8952

Gaus Width: 3.7632



B0L002S, U5-ch122, adc2

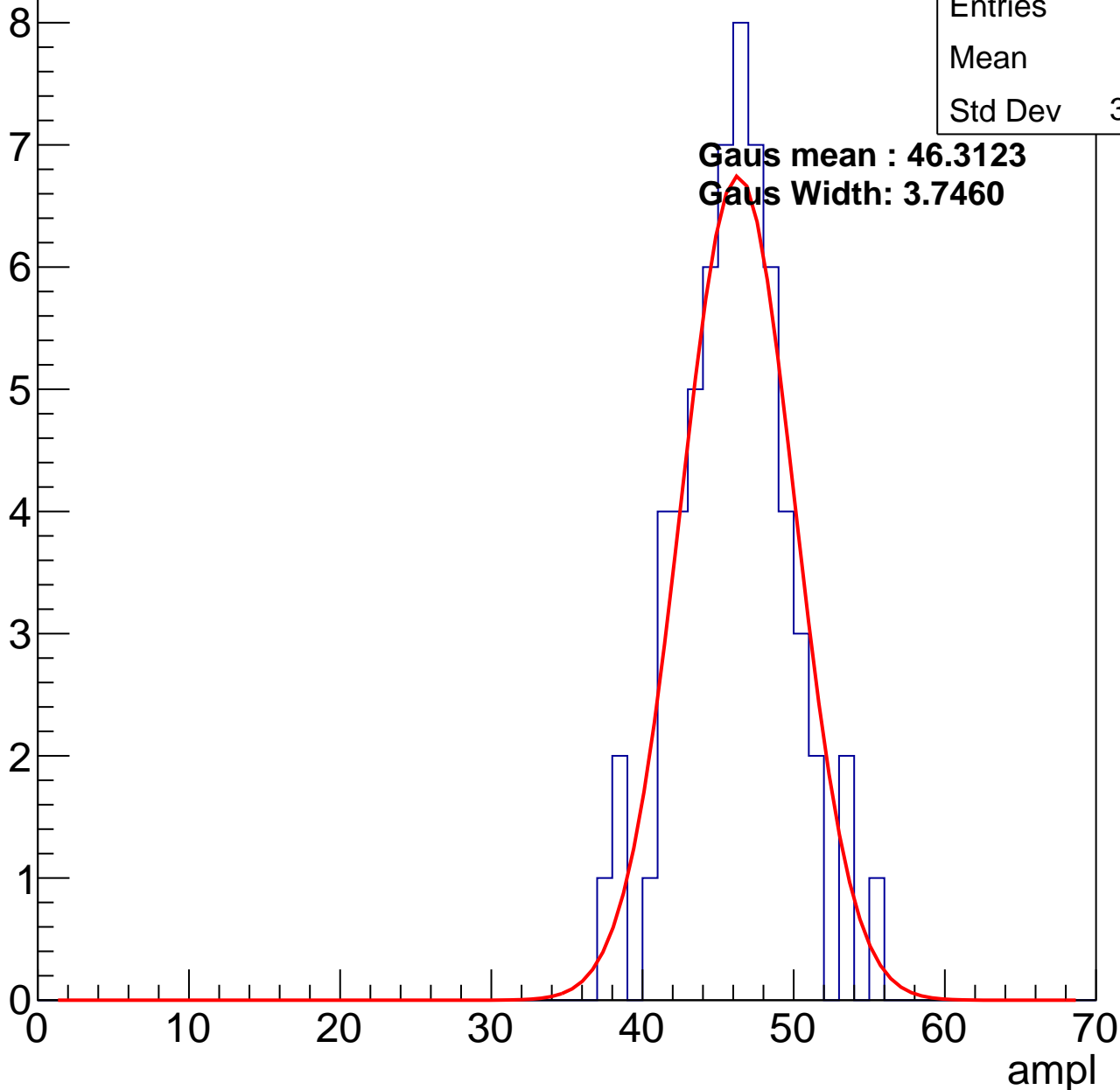
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	45.6
Std Dev	3.606

Gaus mean : 46.3123

Gaus Width: 3.7460

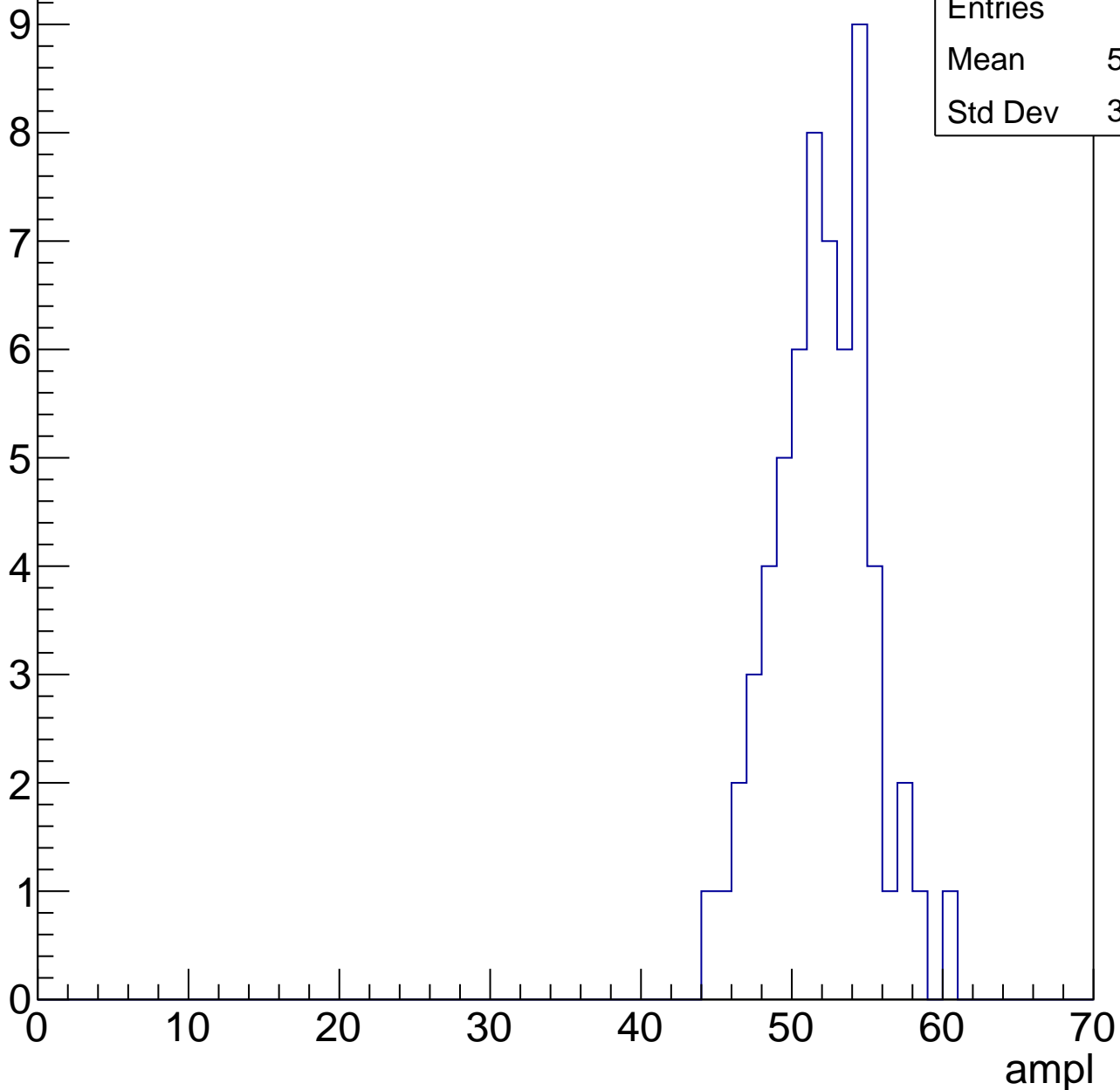


B0L002S, U5-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

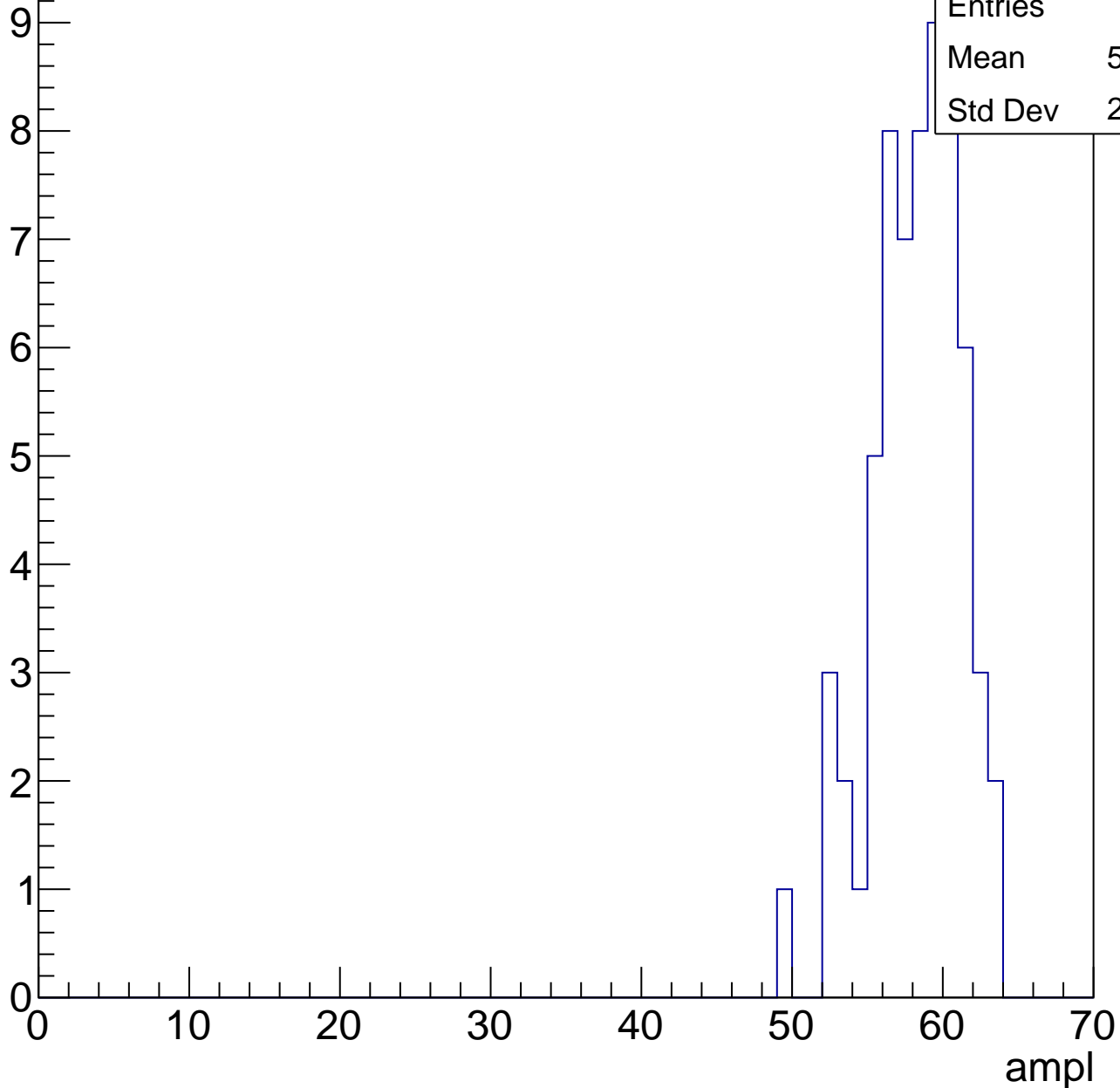
Entries	61
Mean	51.52
Std Dev	3.217



B0L002S, U5-ch122, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

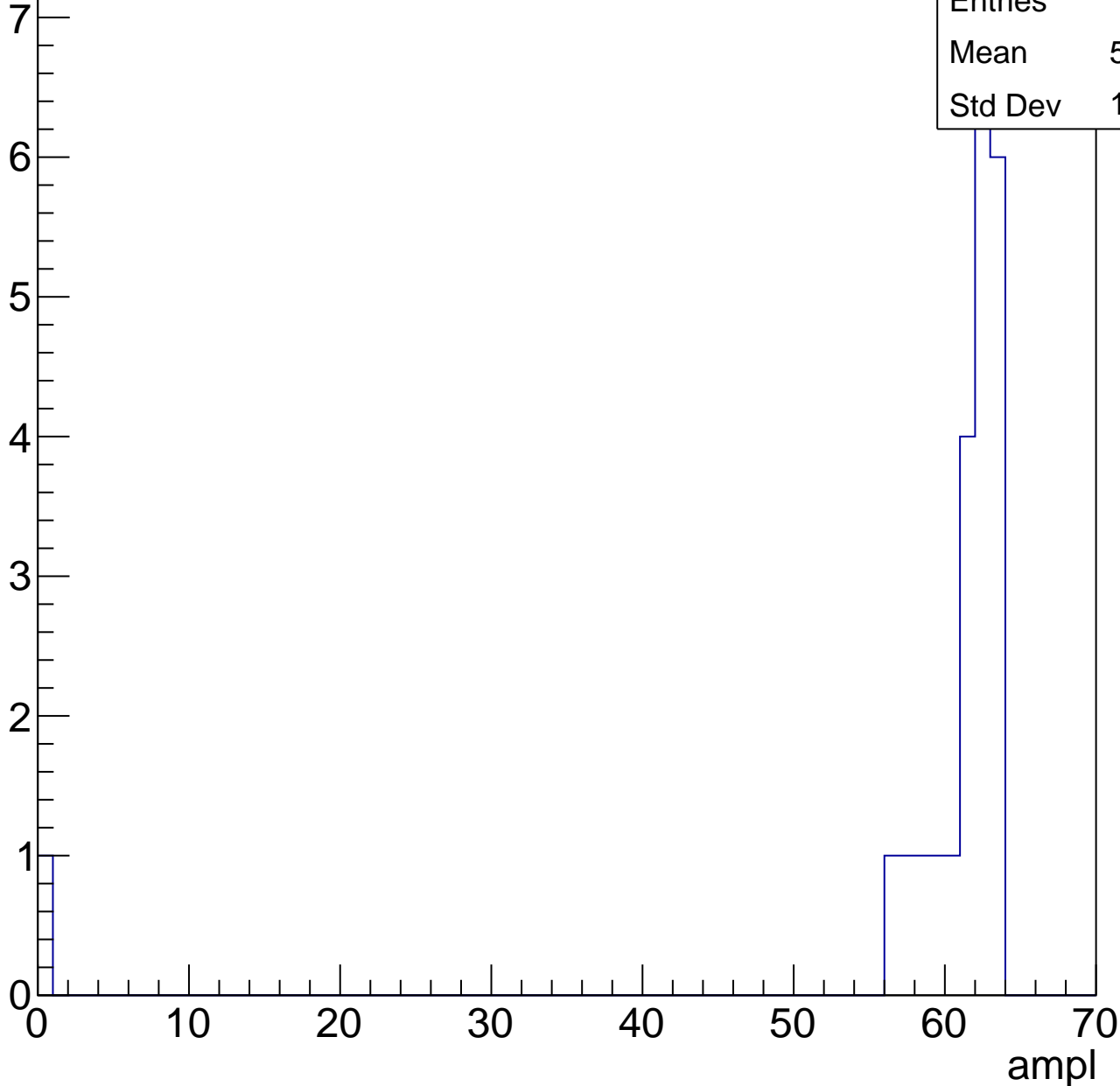


B0L002S, U5-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.52
Std Dev	12.62



B0L002S, U5-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

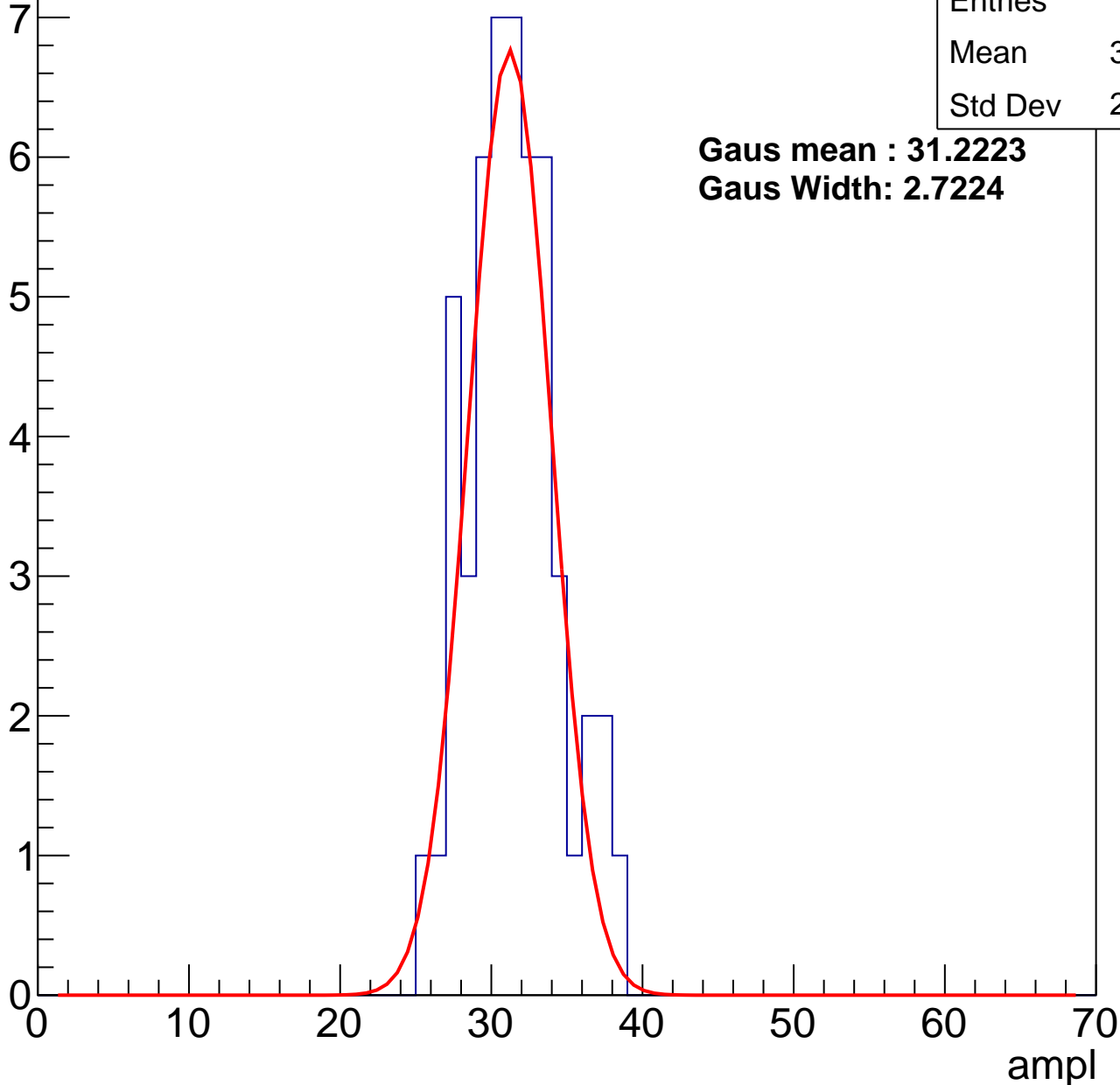
B0L002S, U5-ch123, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	31.02
Std Dev	2.947

Gaus mean : 31.2223
Gaus Width: 2.7224



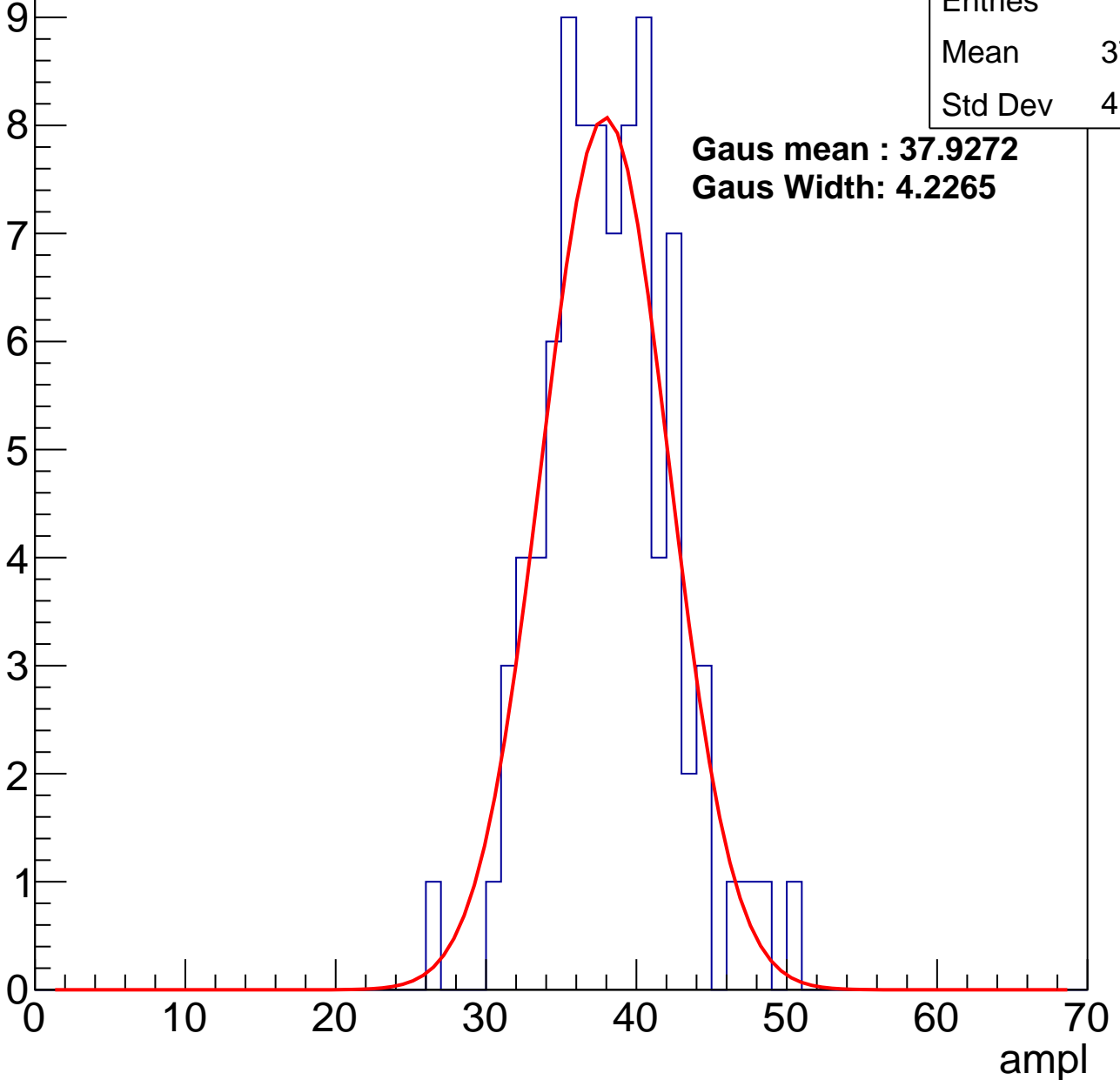
B0L002S, U5-ch123, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	88
Mean	37.69
Std Dev	4.192

Gaus mean : 37.9272
Gaus Width: 4.2265



B0L002S, U5-ch123, adc2

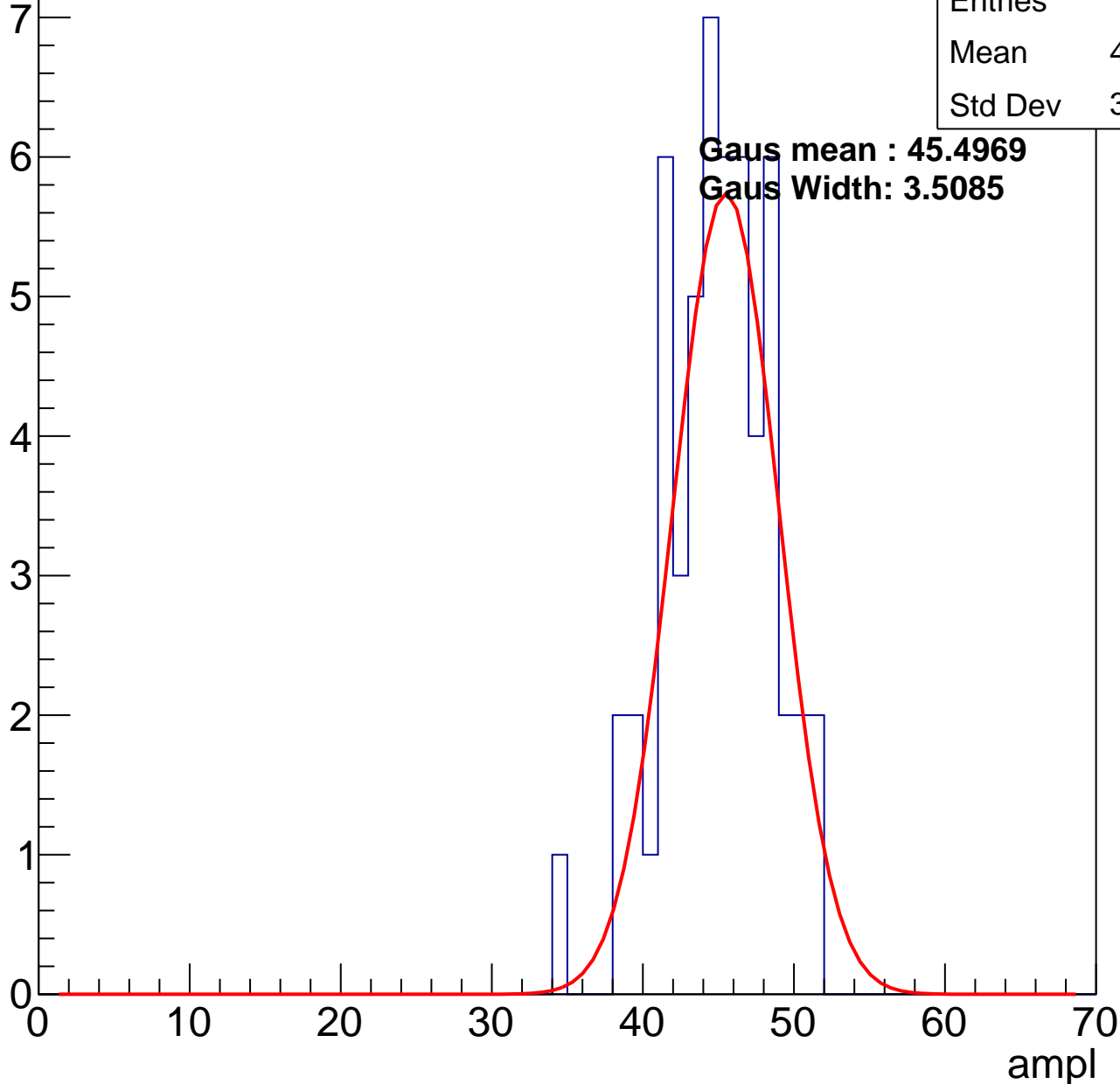
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.45
Std Dev	3.515

Gaus mean : 45.4969

Gaus Width: 3.5085

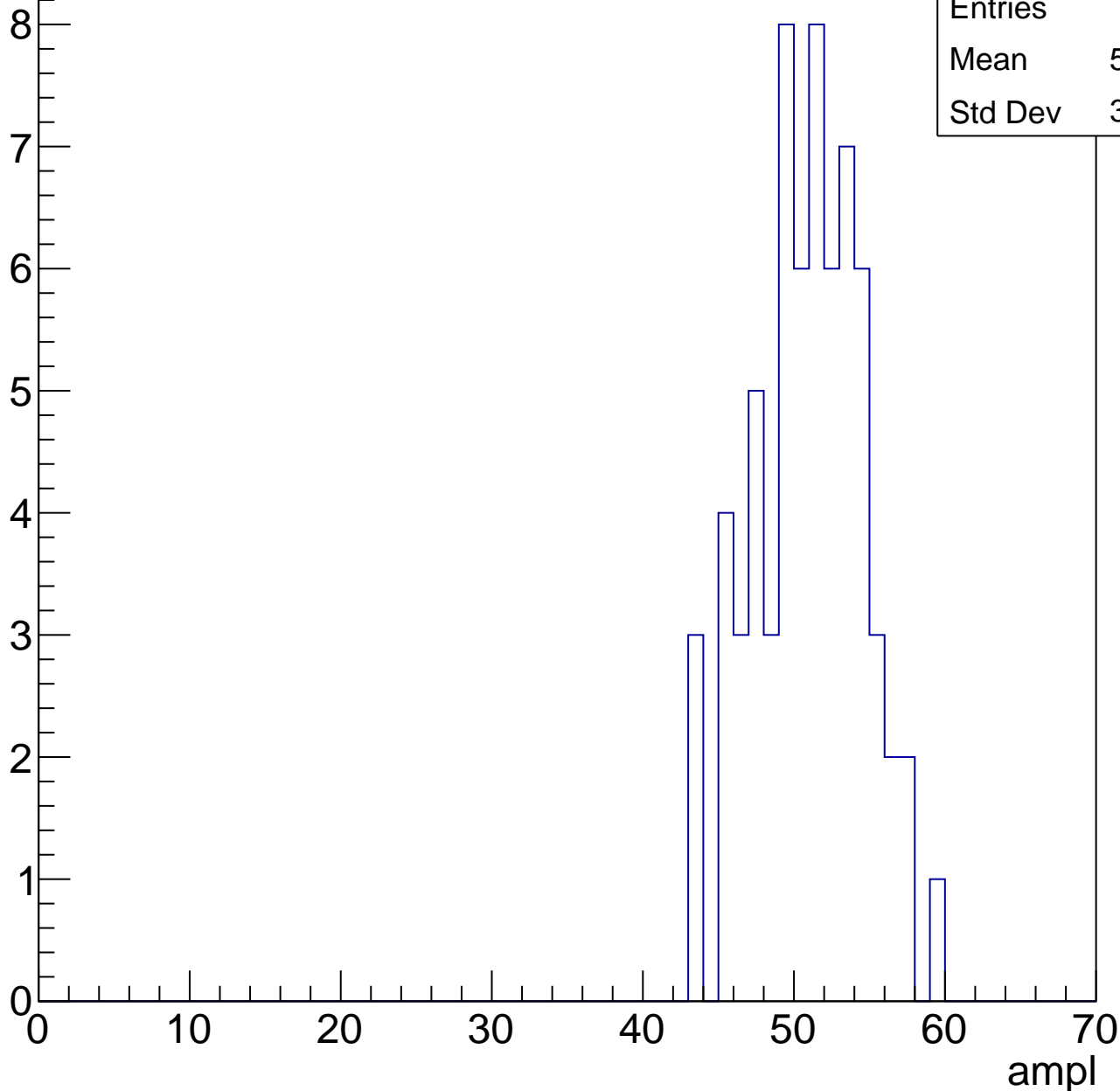


B0L002S, U5-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.49
Std Dev	3.584

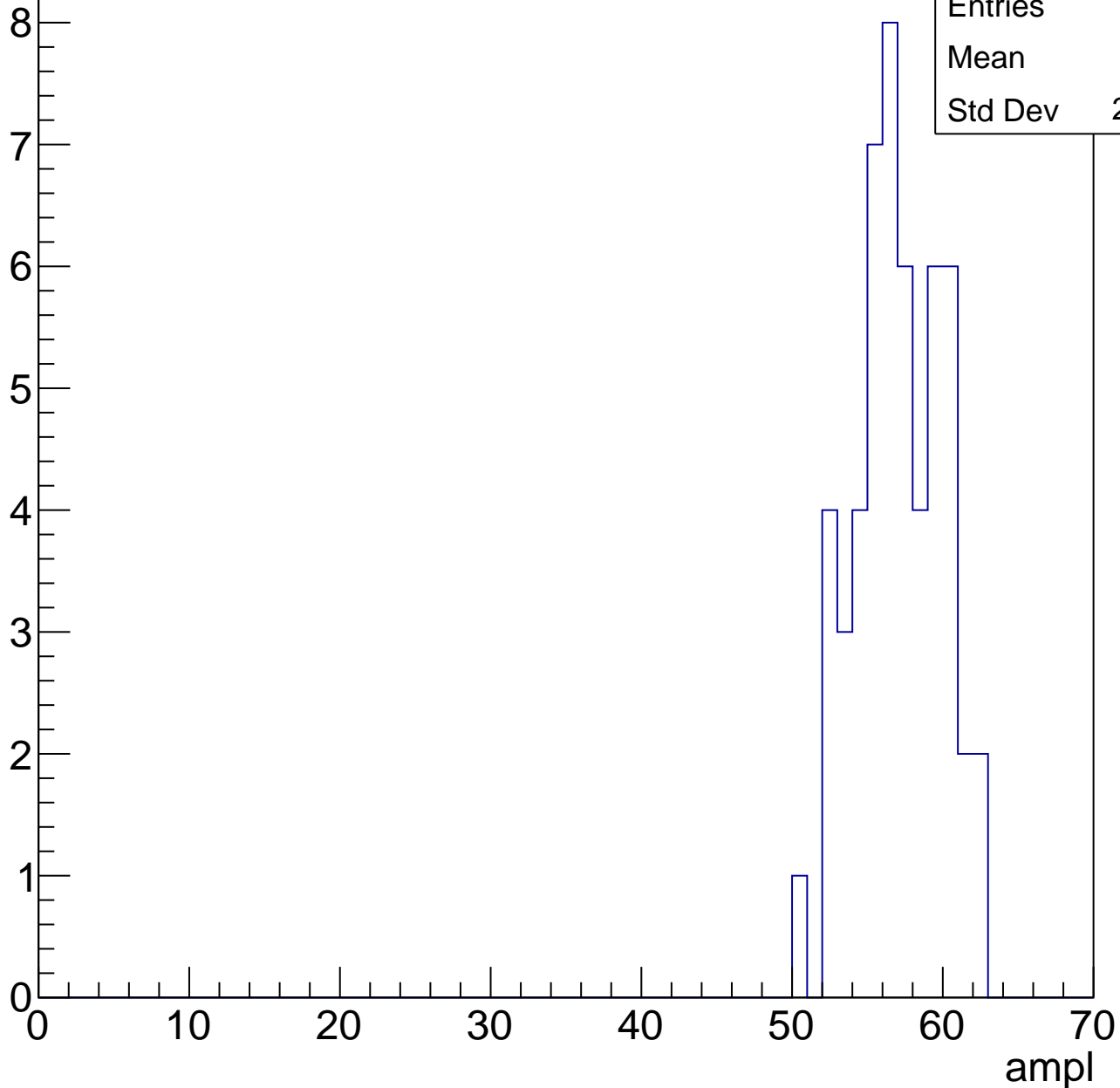


B0L002S, U5-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.6
Std Dev	2.831



B0L002S, U5-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

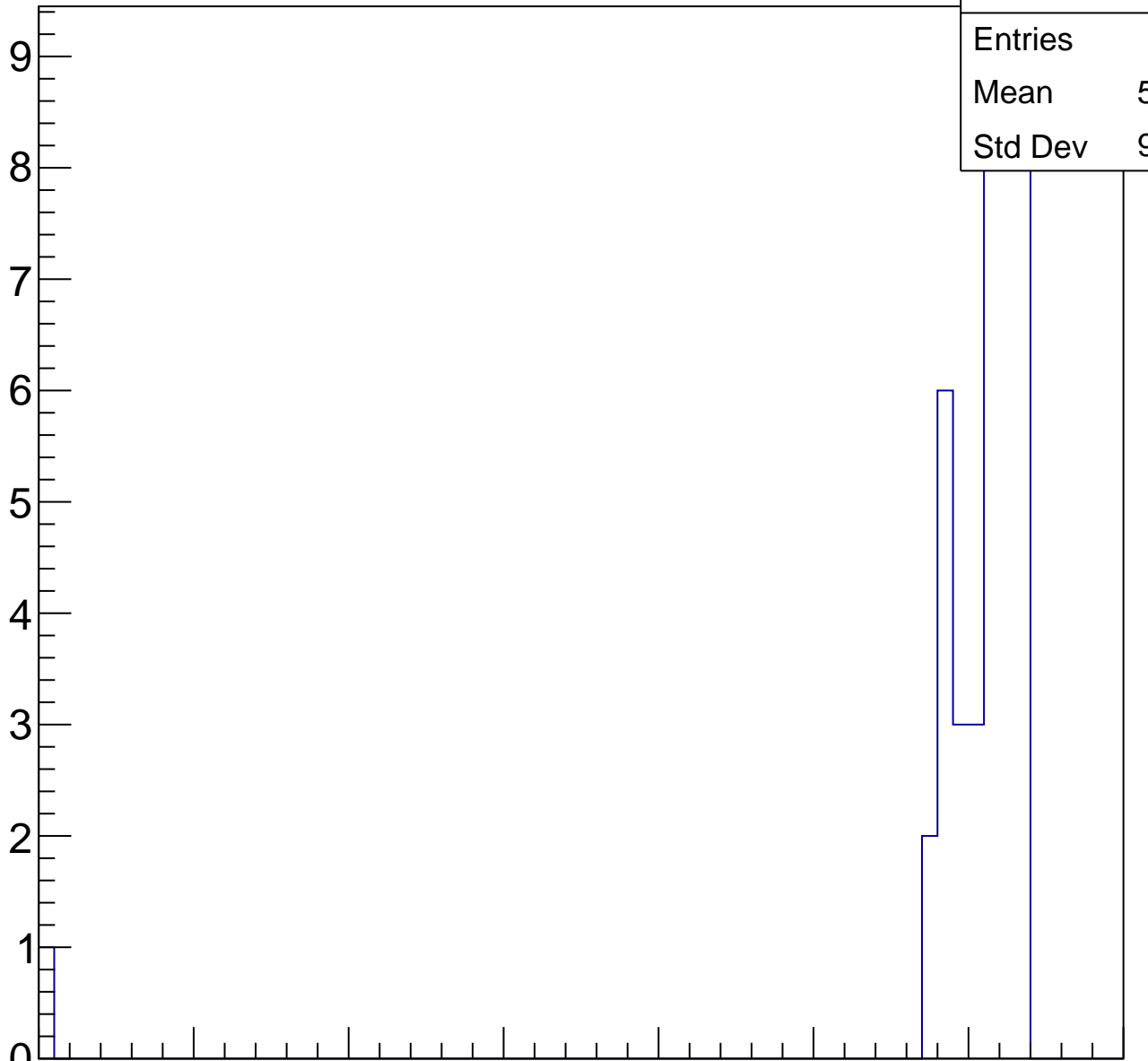
Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.25
Std Dev	9.674

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch124, adc0

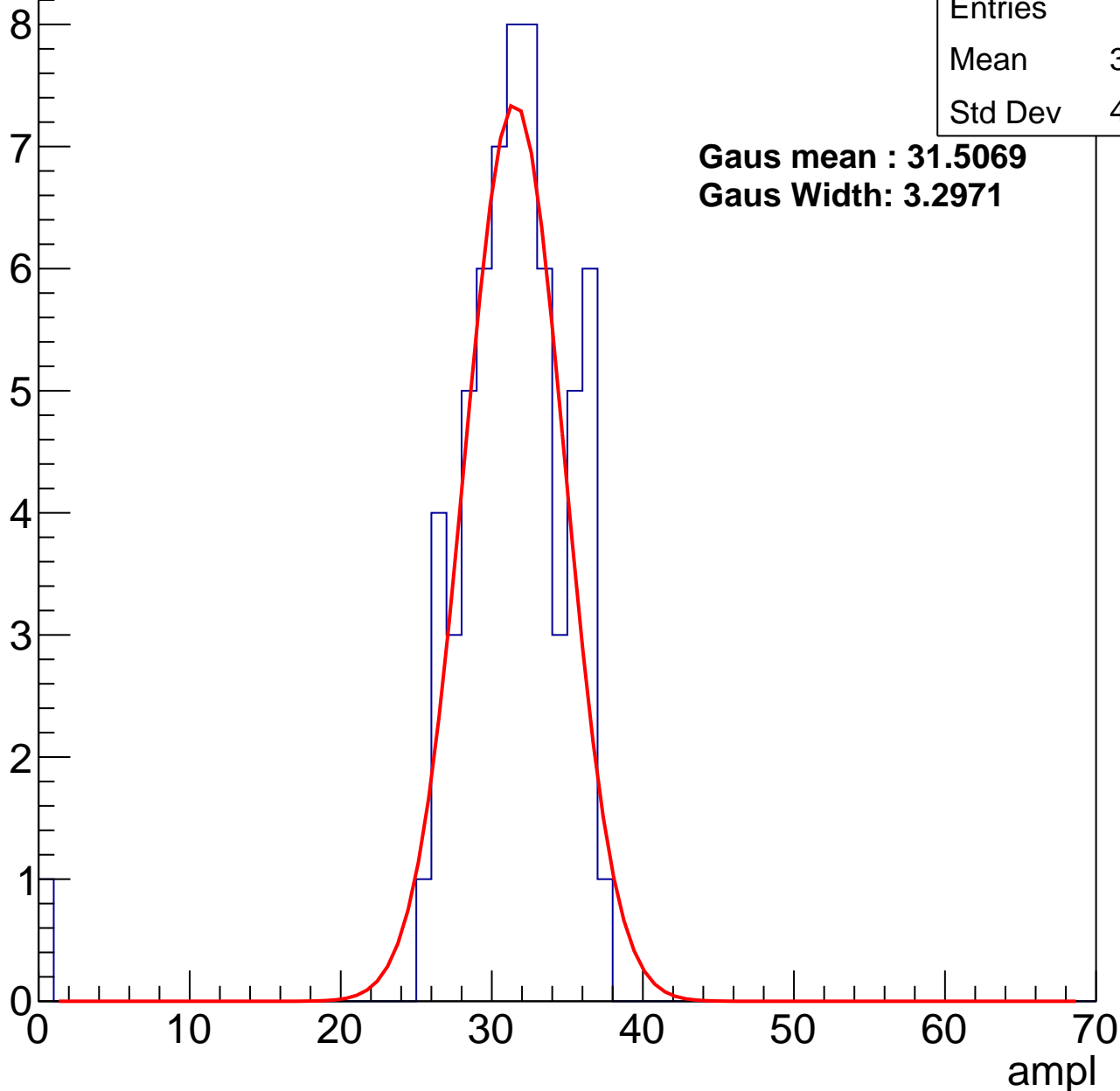
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.72
Std Dev	4.907

Gaus mean : 31.5069

Gaus Width: 3.2971



B0L002S, U5-ch124, adc1

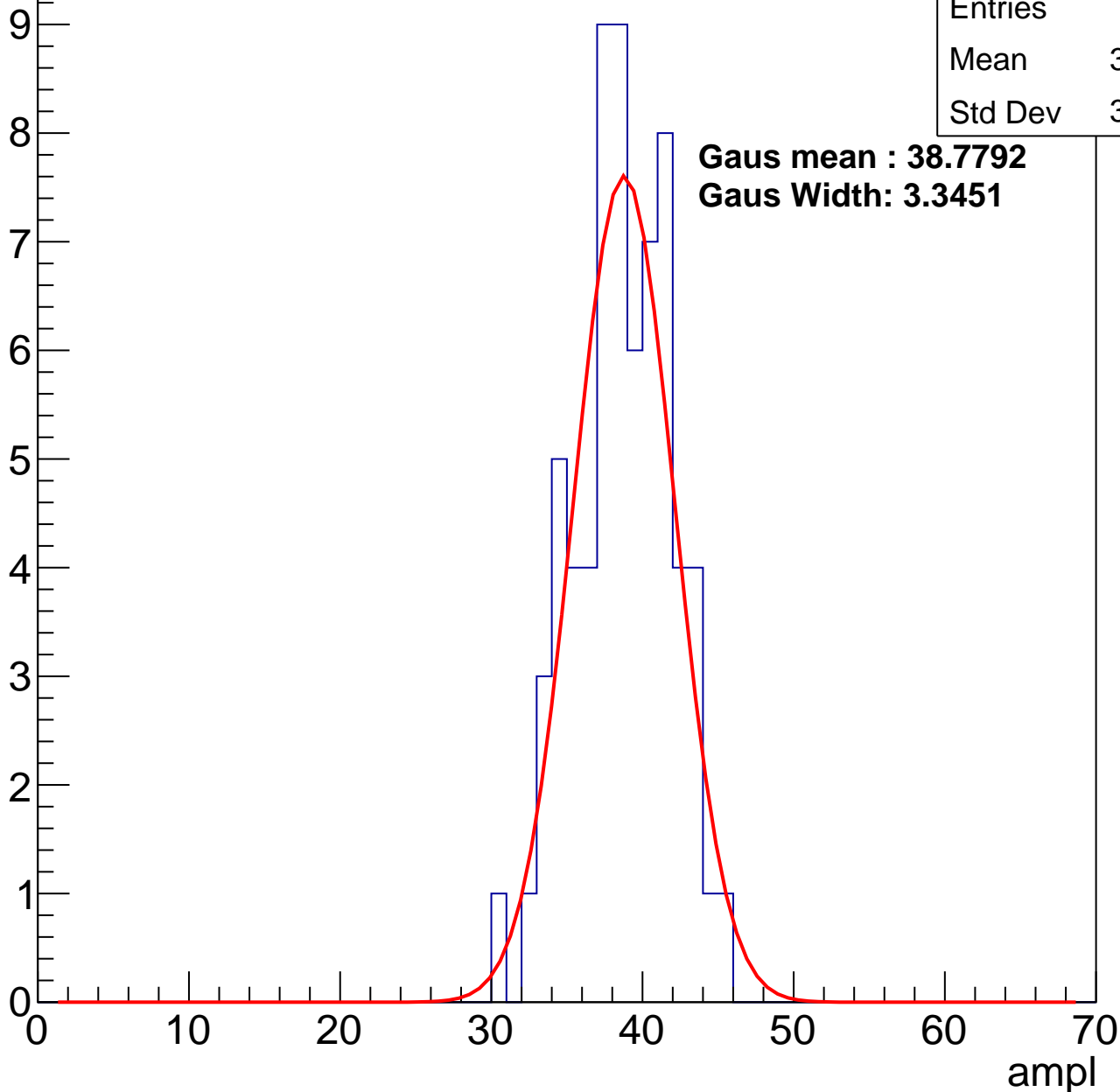
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	38.22
Std Dev	3.157

Gaus mean : 38.7792

Gaus Width: 3.3451



B0L002S, U5-ch124, adc2

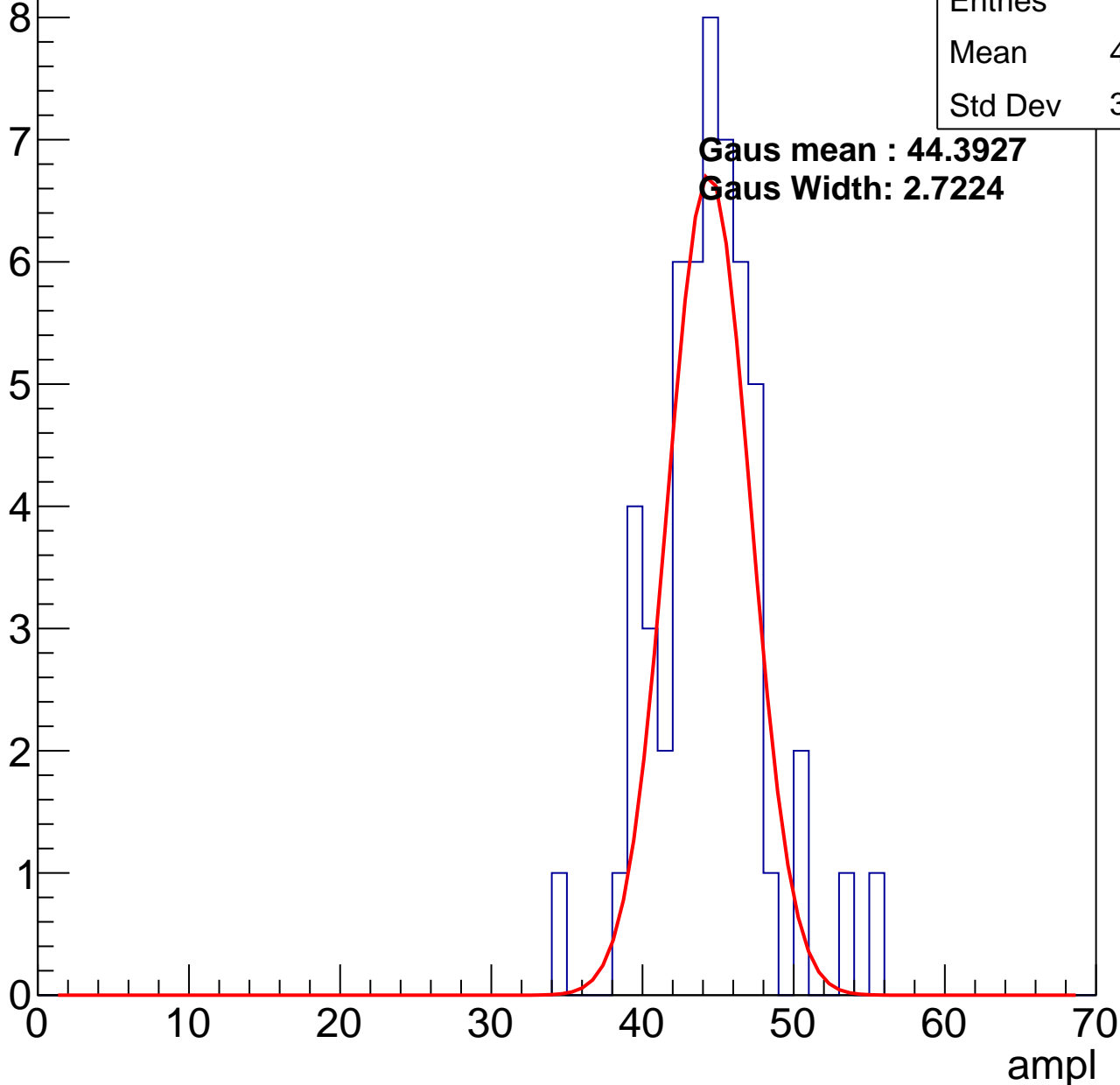
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.96
Std Dev	3.595

Gaus mean : 44.3927

Gaus Width: 2.7224



B0L002S, U5-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	50.53
Std Dev	3.234

Entry

10

8

6

4

2

0

0

10

20

30

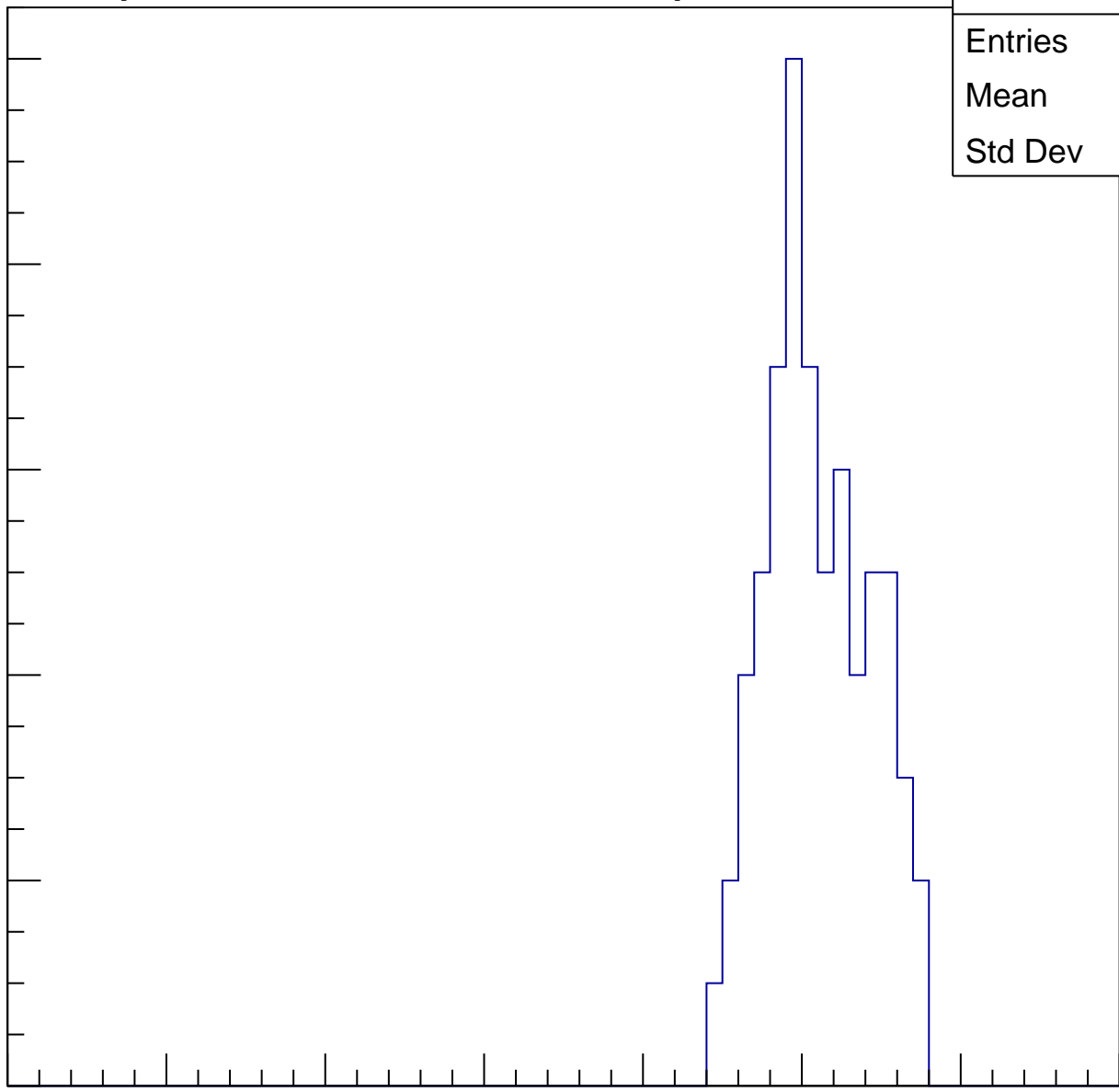
40

50

60

70

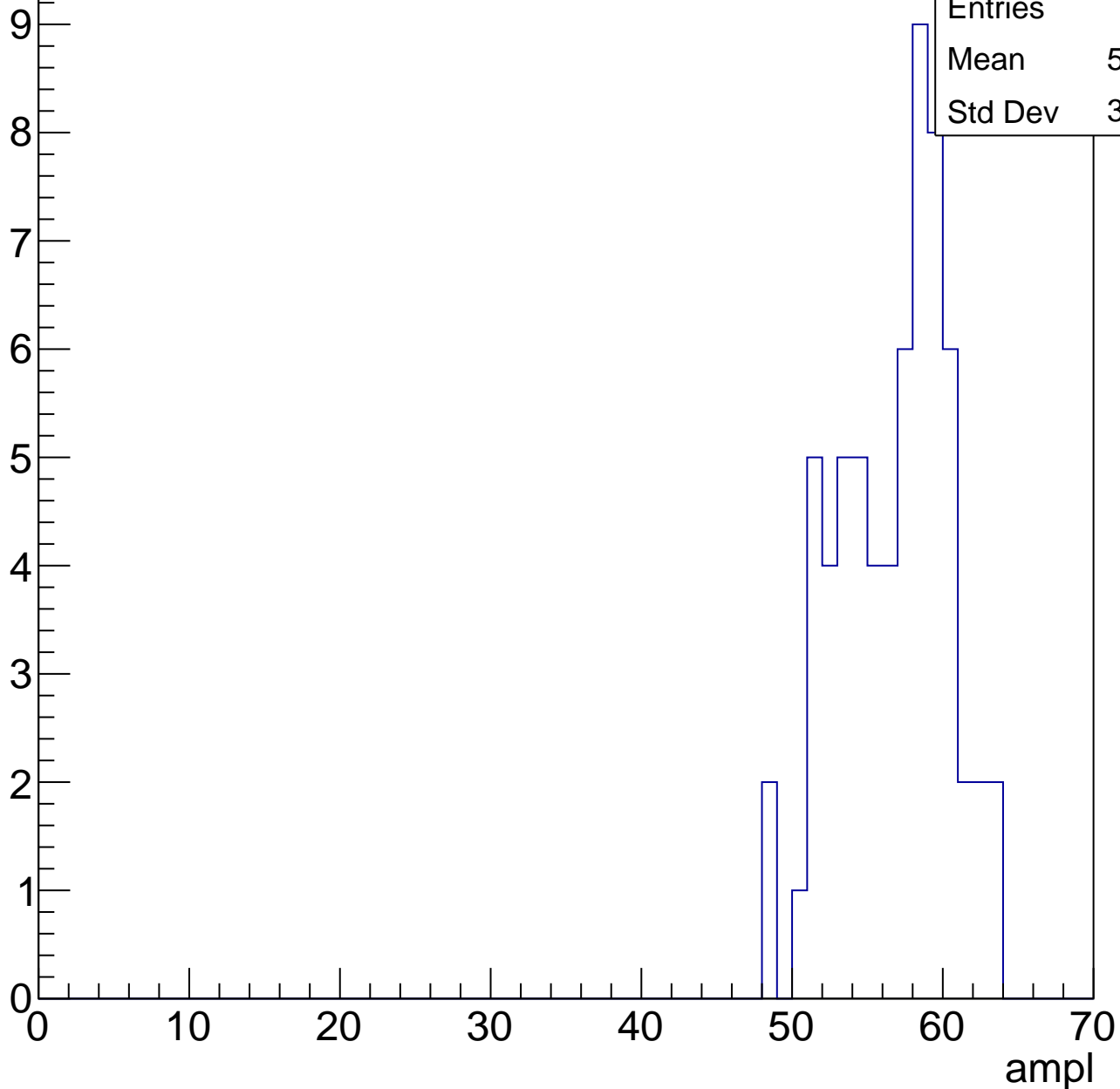
ampl



B0L002S, U5-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



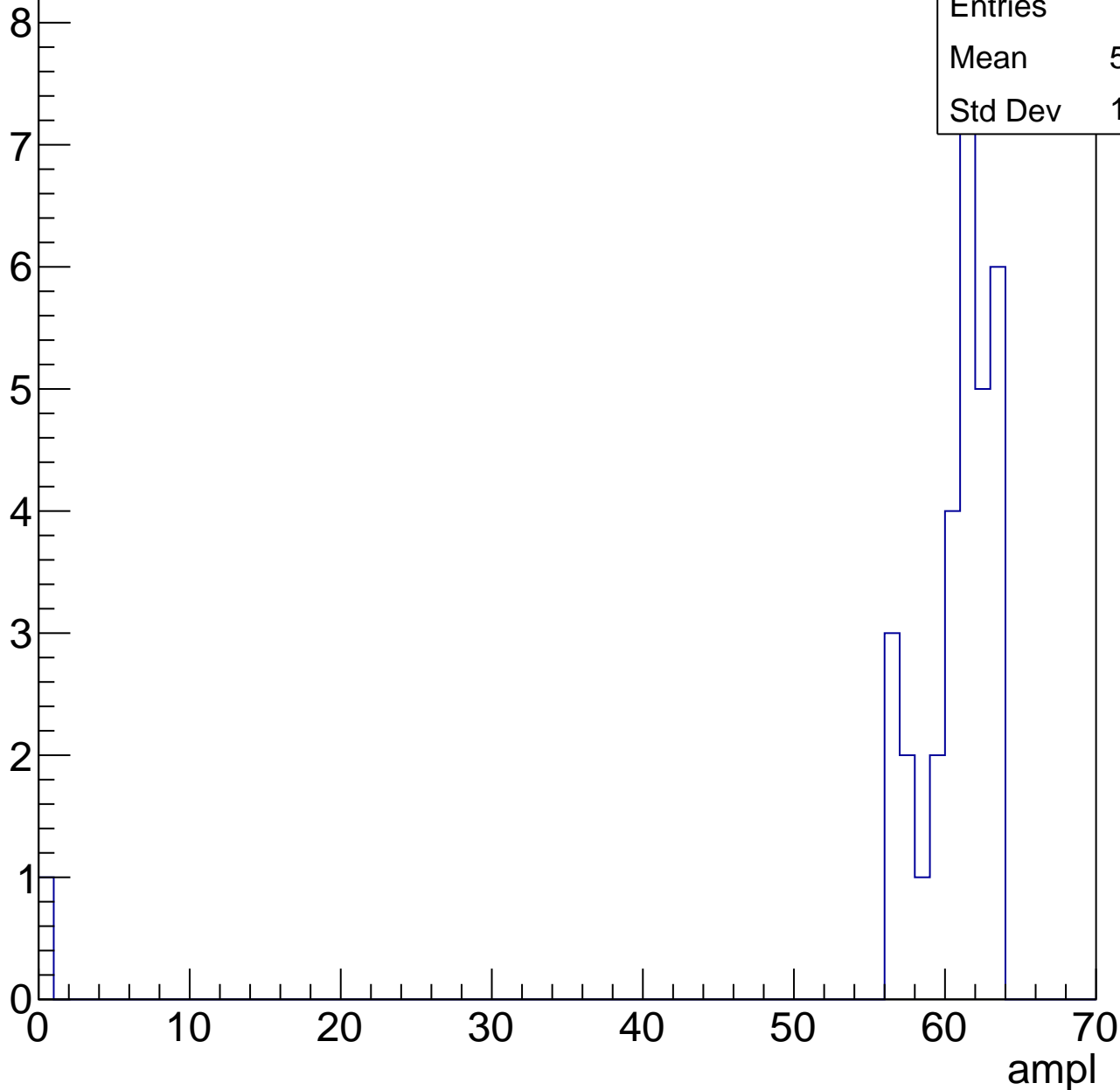
Entries	65
Mean	56.25
Std Dev	3.612

B0L002S, U5-ch124, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	58.56
Std Dev	10.74



B0L002S, U5-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch125, adc0

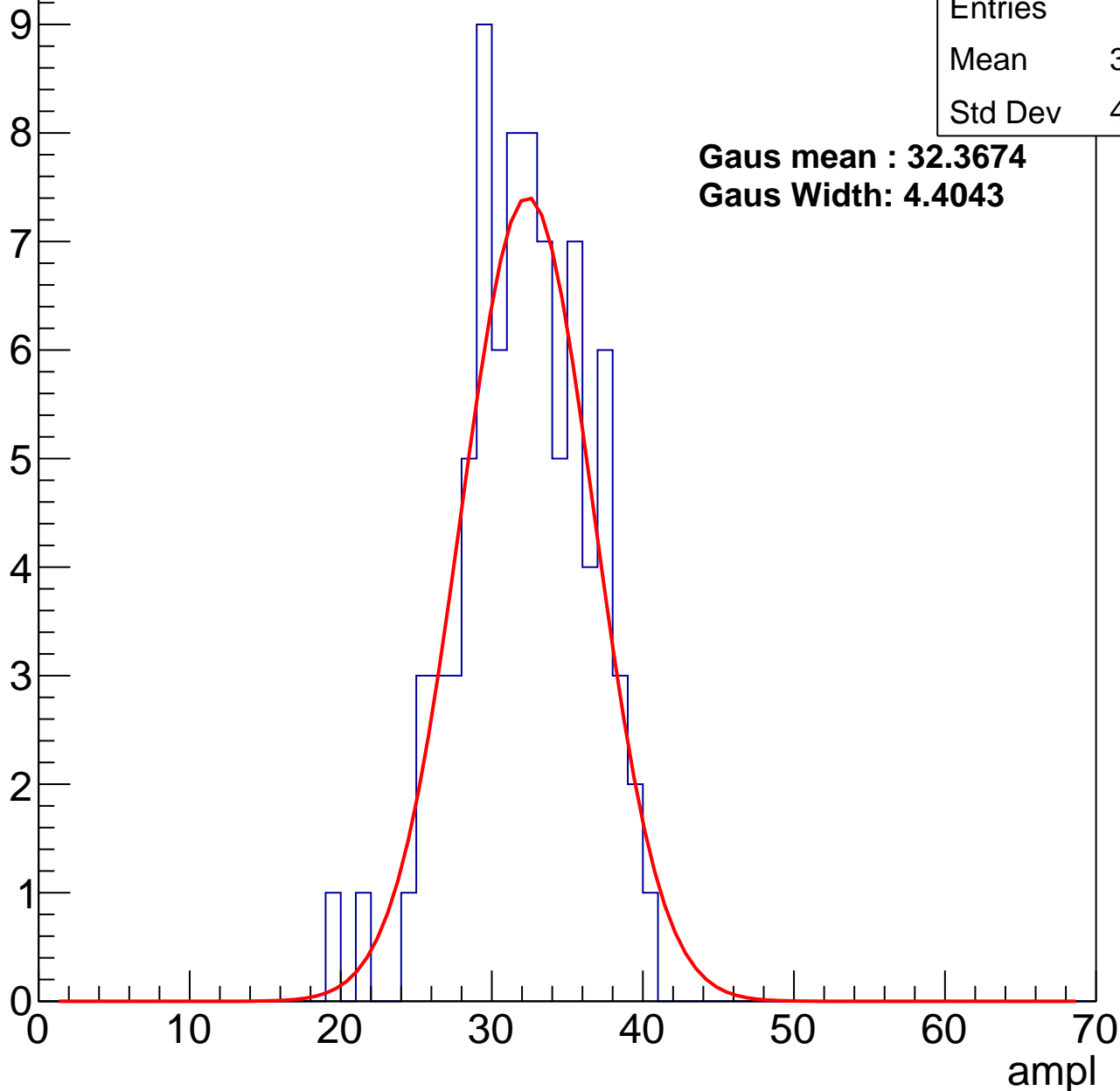
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	31.65
Std Dev	4.167

Gaus mean : 32.3674

Gaus Width: 4.4043



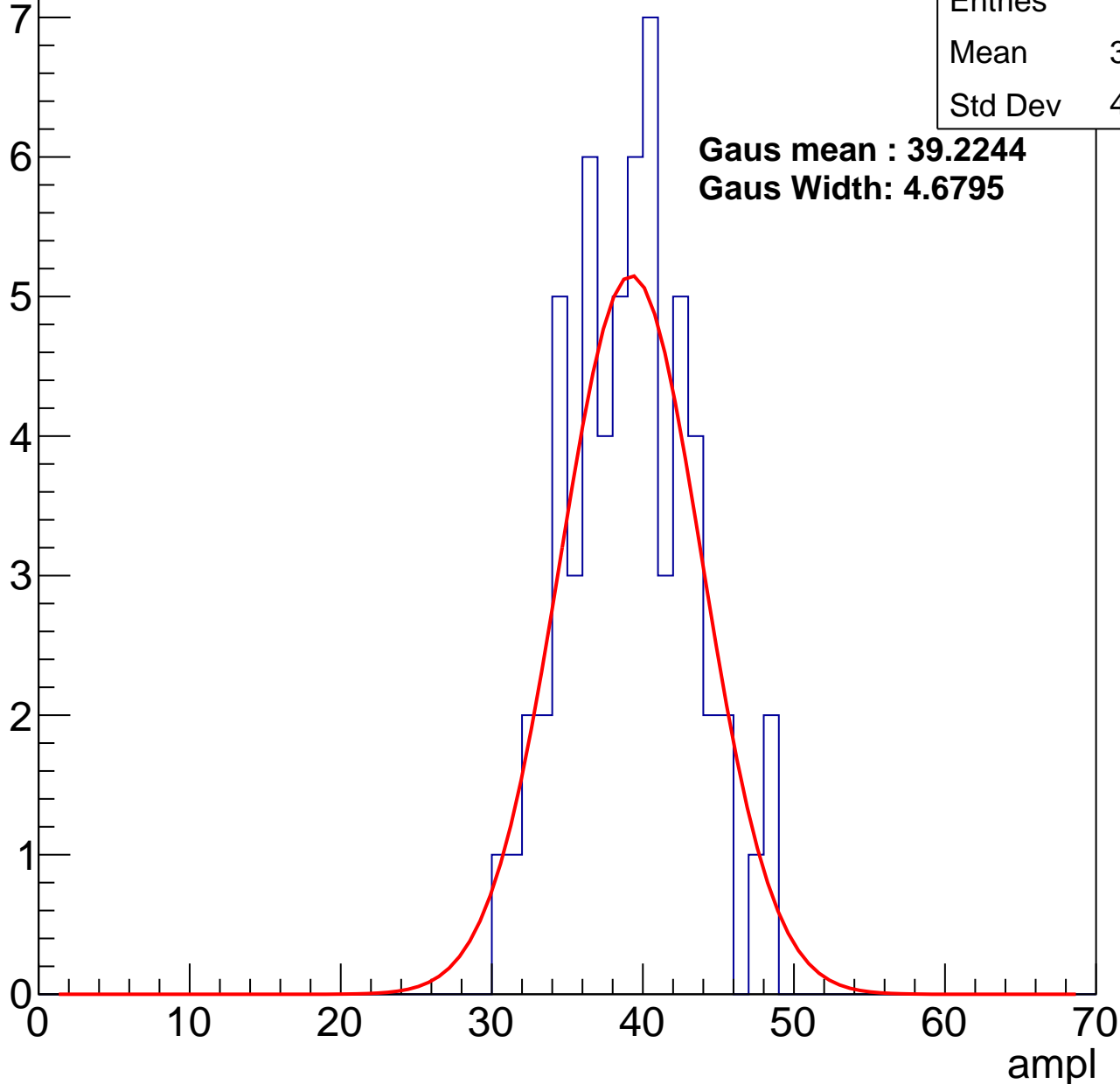
B0L002S, U5-ch125, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.69
Std Dev	4.115

Gaus mean : 39.2244
Gaus Width: 4.6795



B0L002S, U5-ch125, adc2

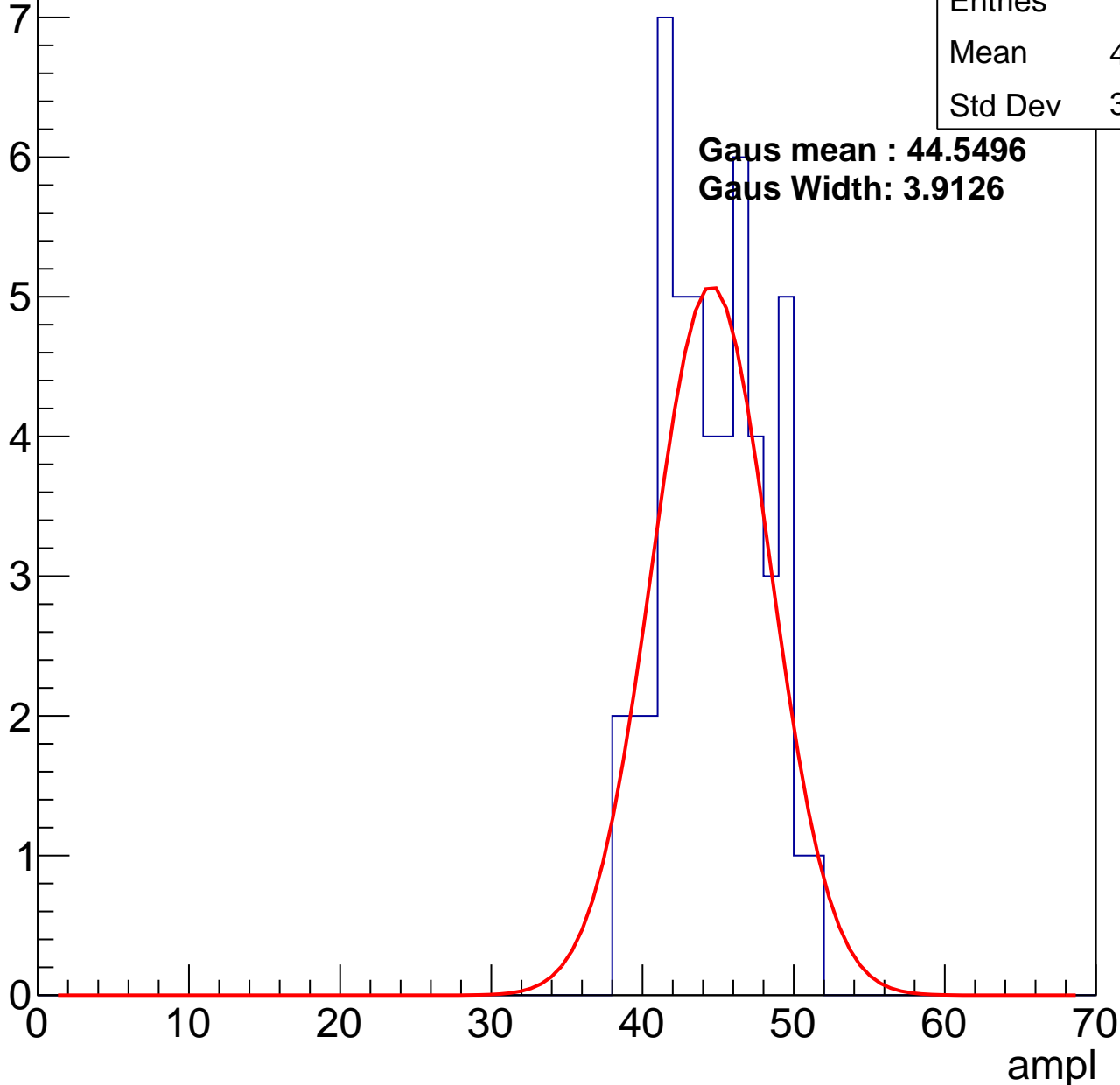
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	44.24
Std Dev	3.317

Gaus mean : 44.5496

Gaus Width: 3.9126

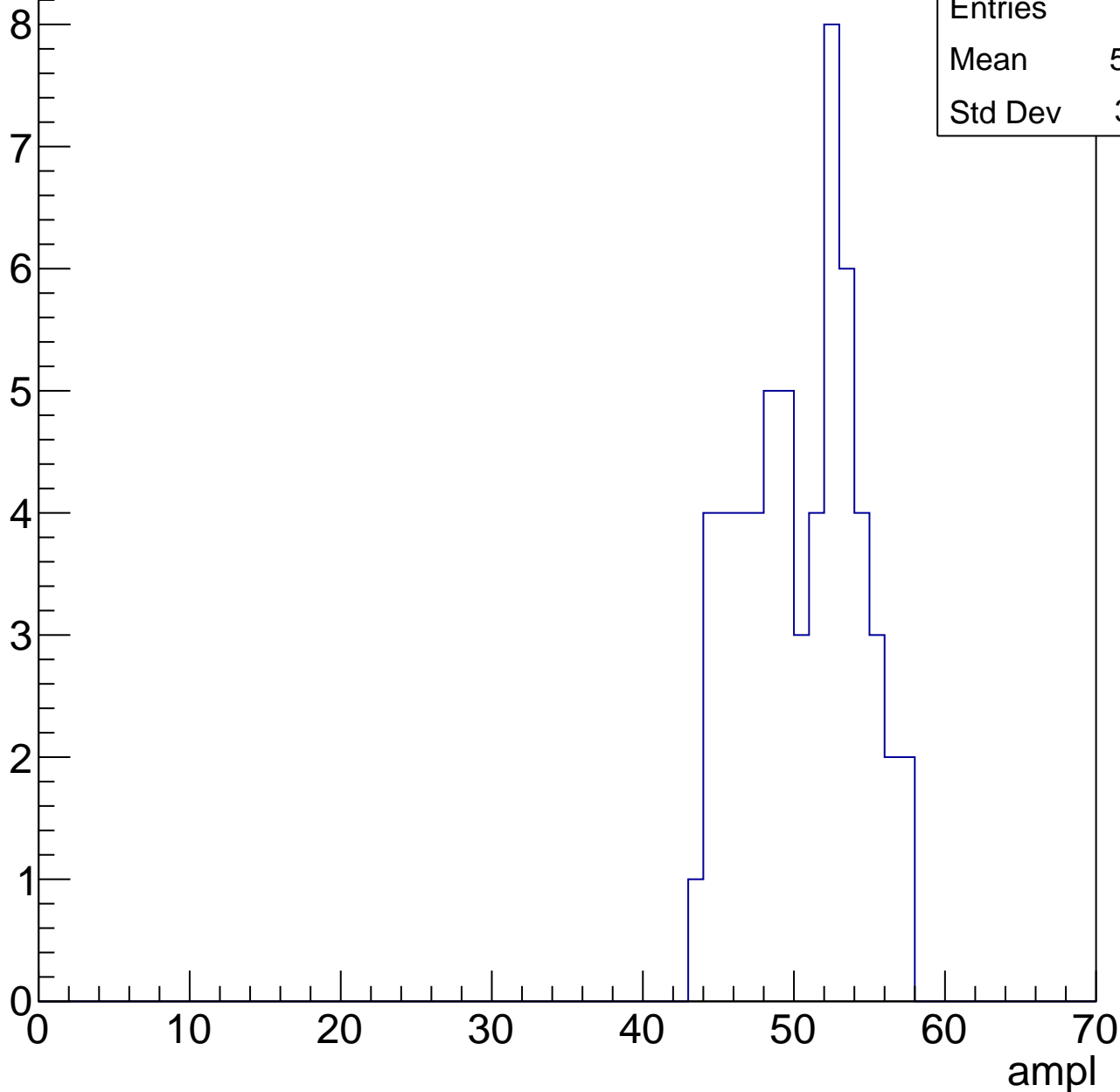


B0L002S, U5-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.02
Std Dev	3.721

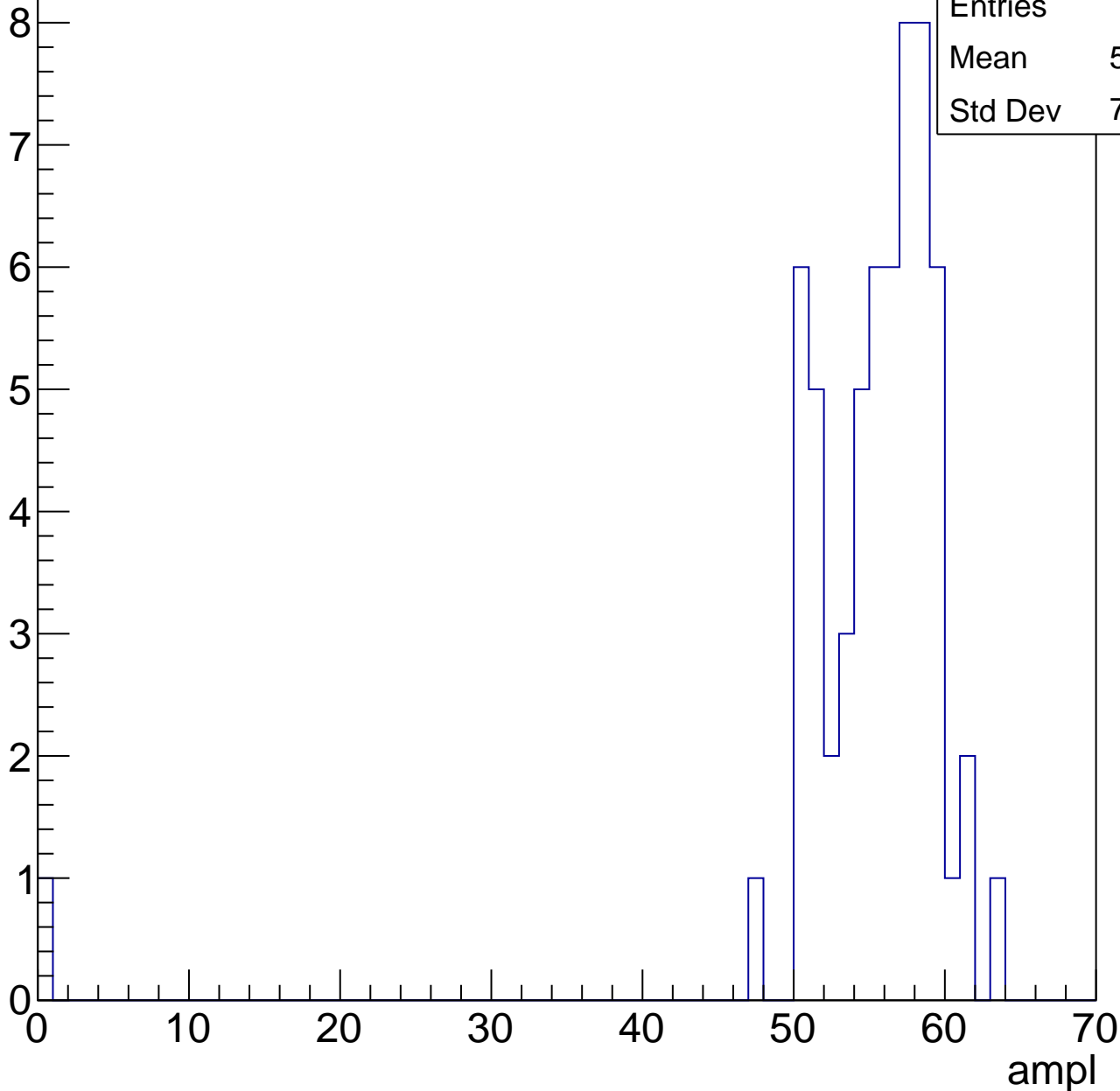


B0L002S, U5-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

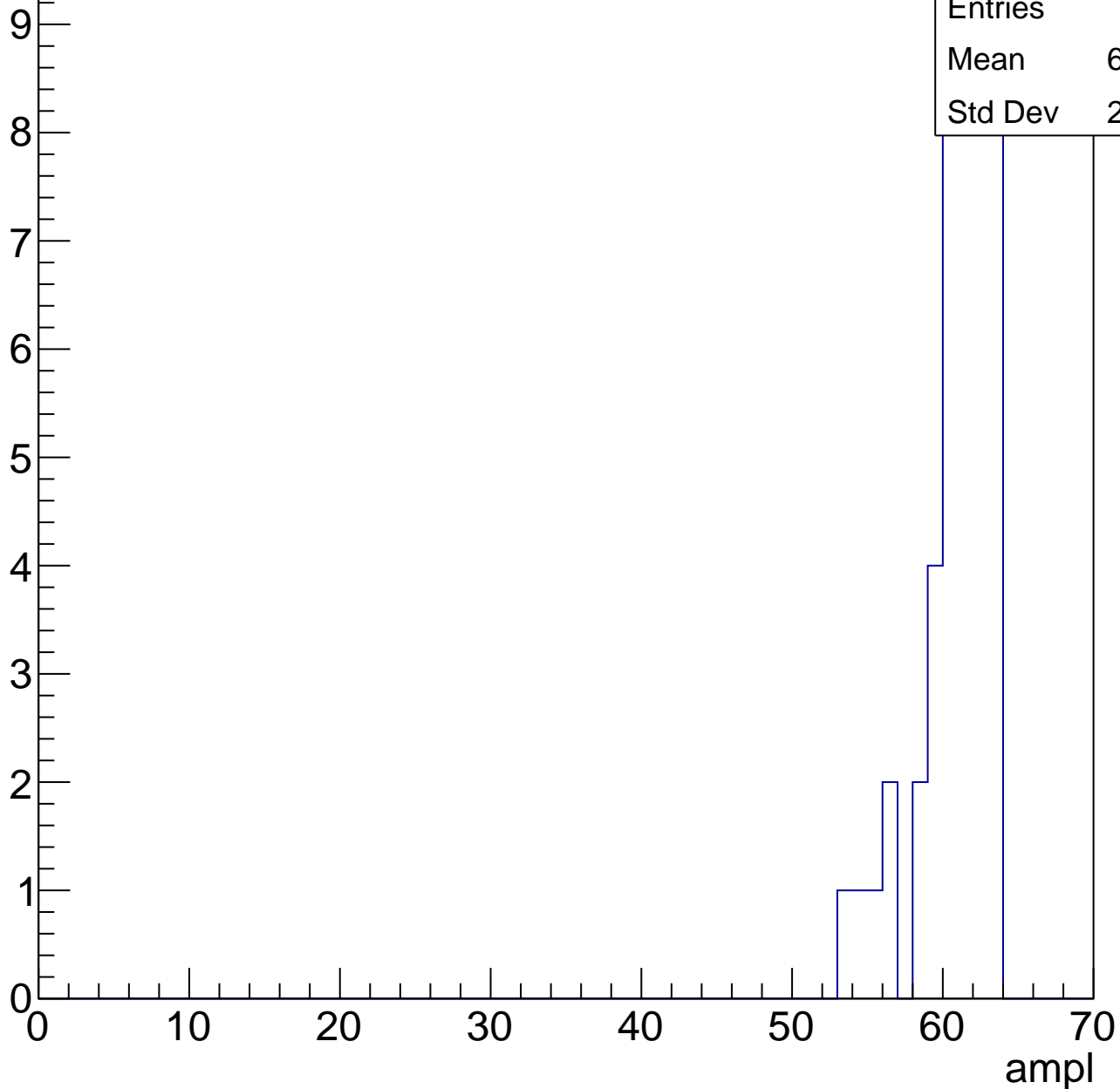
Entries	61
Mean	54.43
Std Dev	7.789



B0L002S, U5-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U5-ch126, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	29.3
Std Dev	5.056

Gaus mean : 30.3459

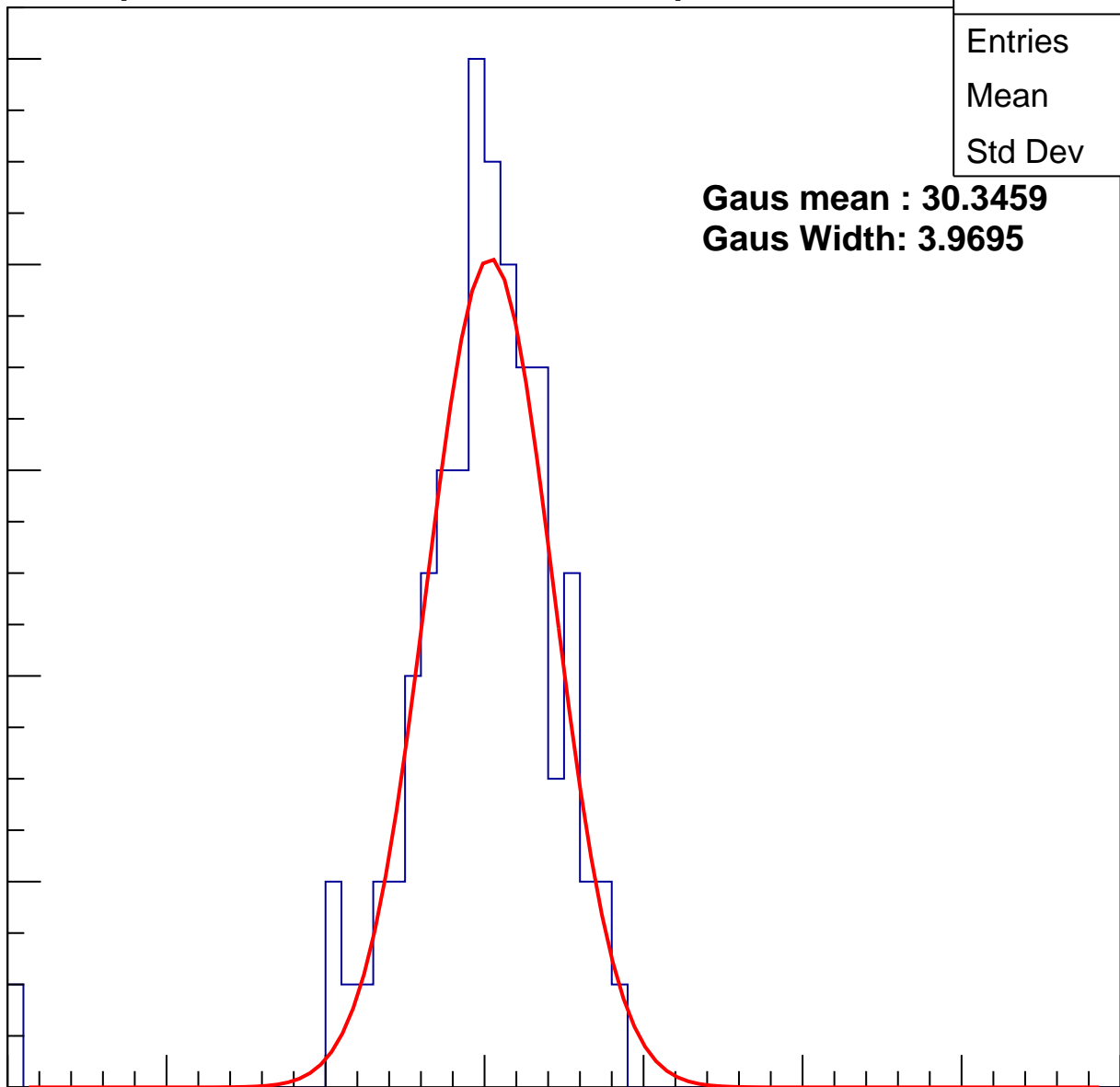
Gaus Width: 3.9695

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U5-ch126, adc1

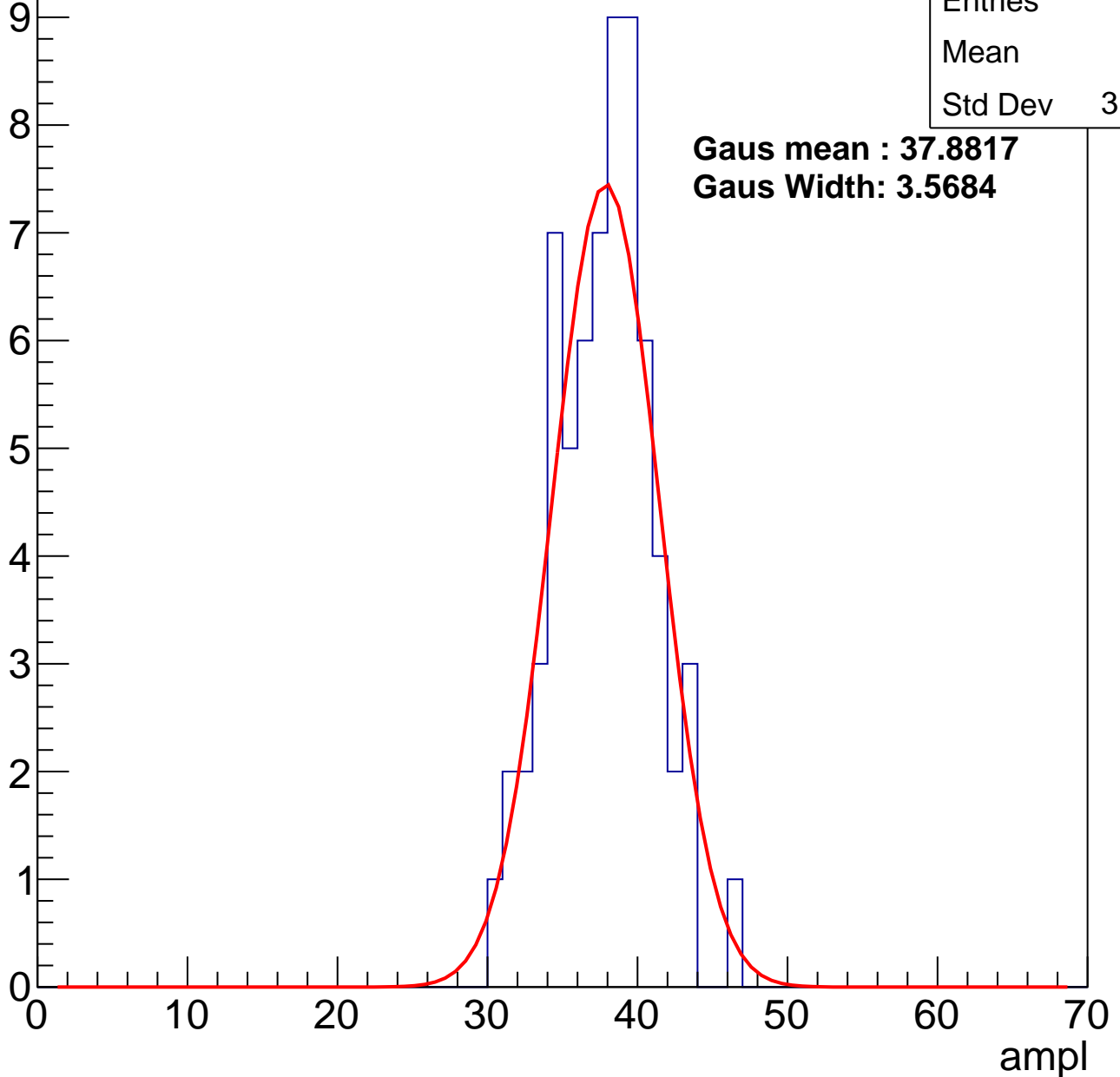
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.3
Std Dev	3.242

Gaus mean : 37.8817

Gaus Width: 3.5684



B0L002S, U5-ch126, adc2

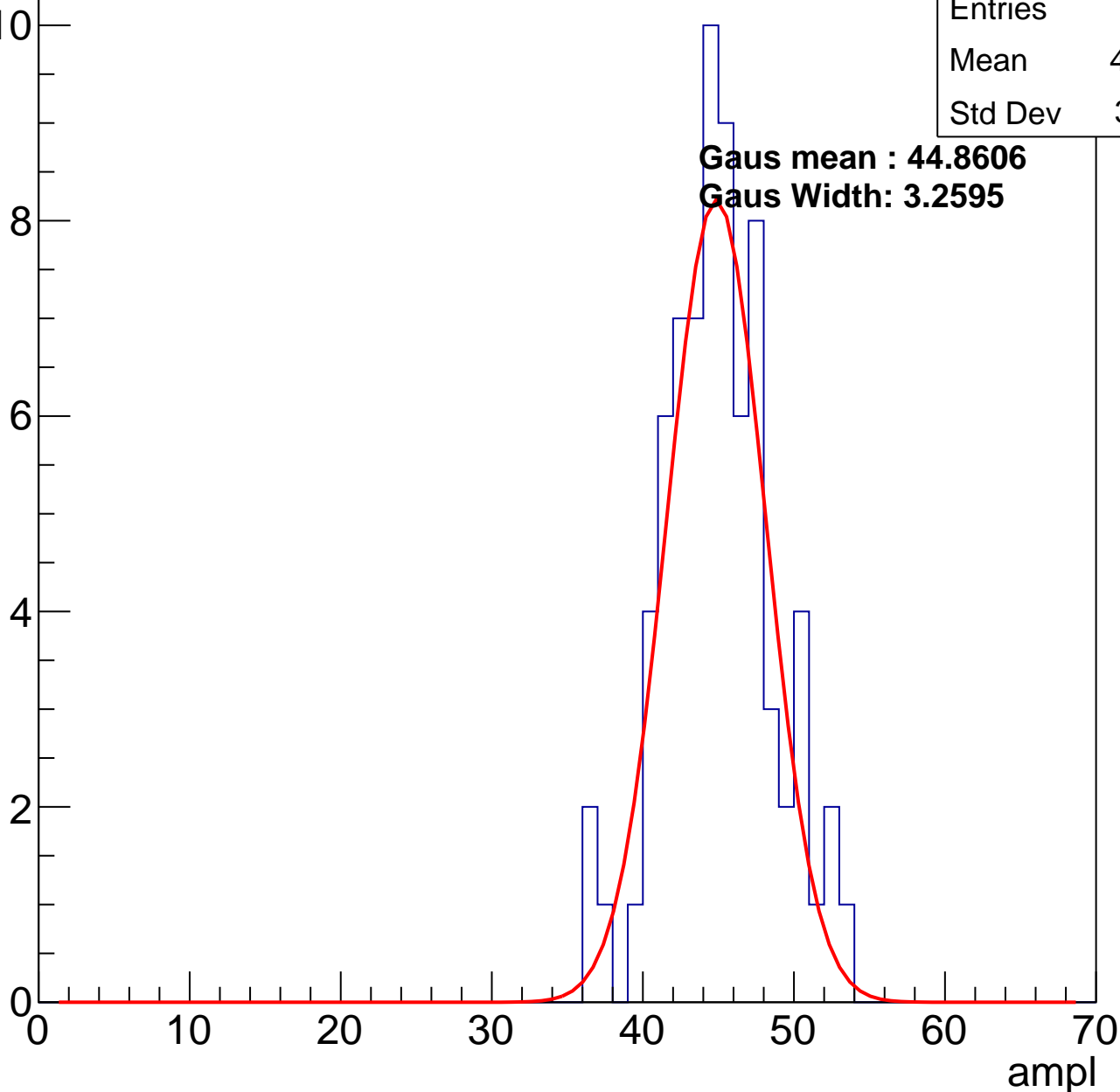
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	44.54
Std Dev	3.561

Gaus mean : 44.8606

Gaus Width: 3.2595

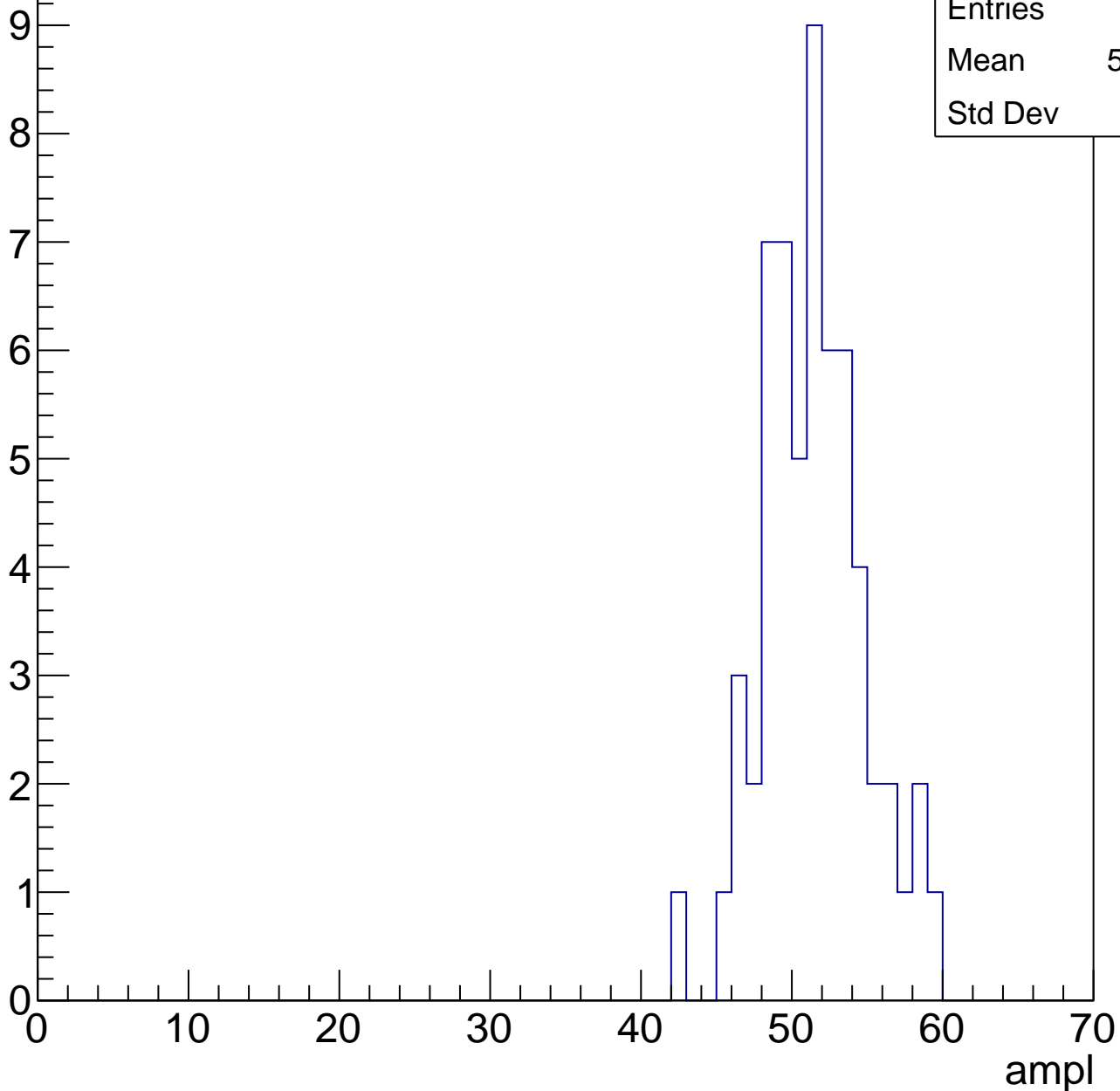


B0L002S, U5-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.97
Std Dev	3.36

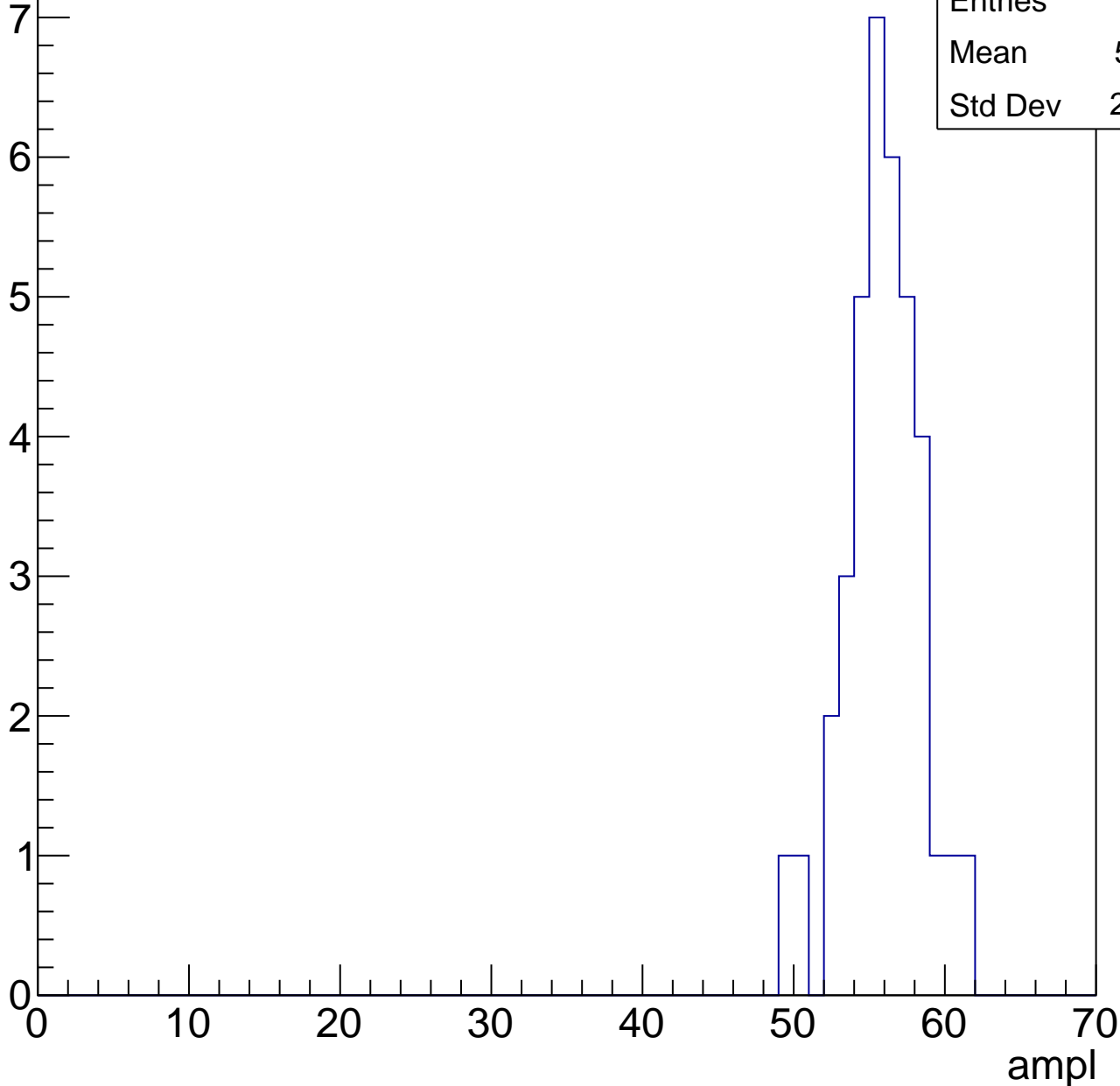


B0L002S, U5-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	55.41
Std Dev	2.487

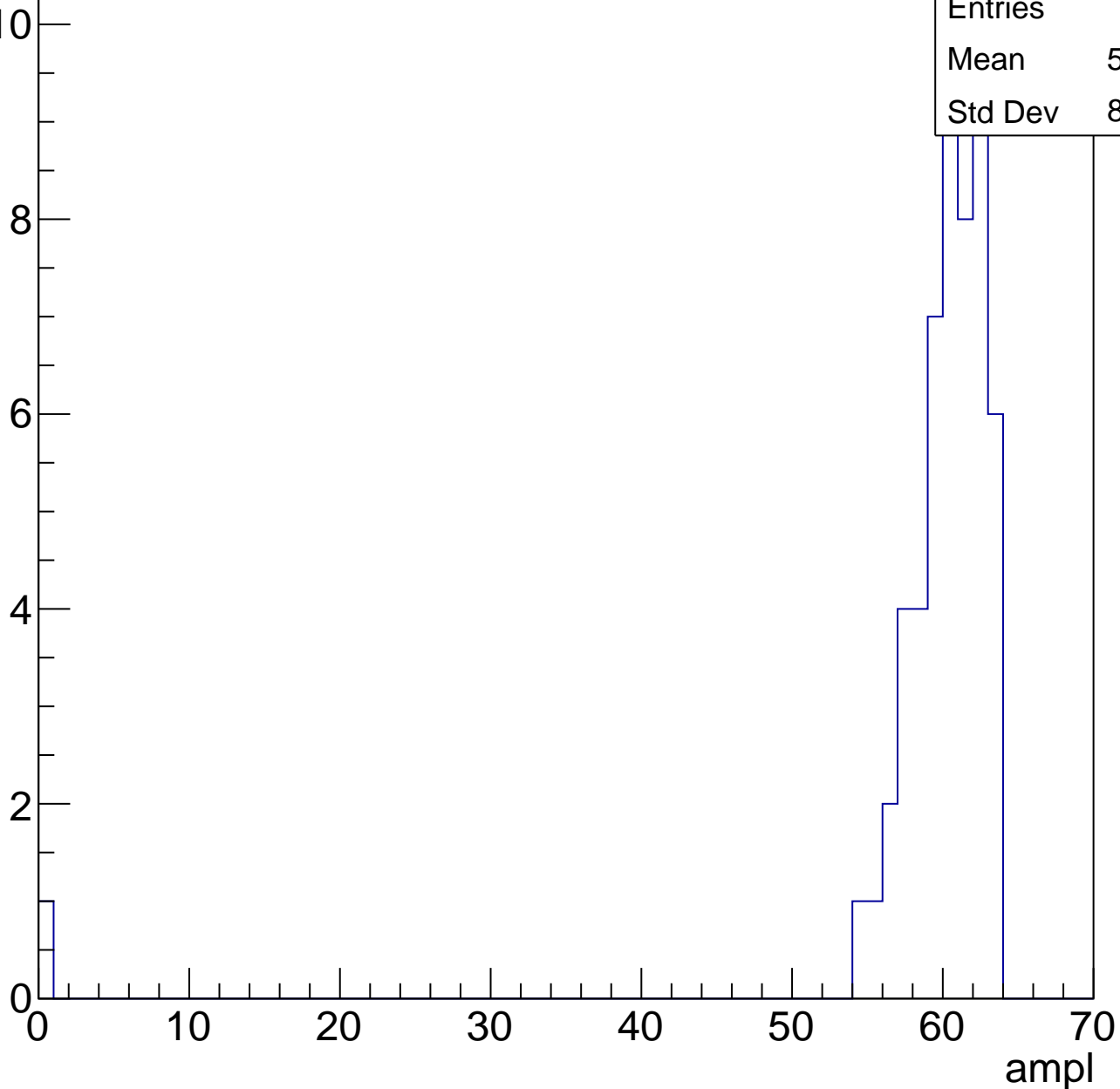


B0L002S, U5-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

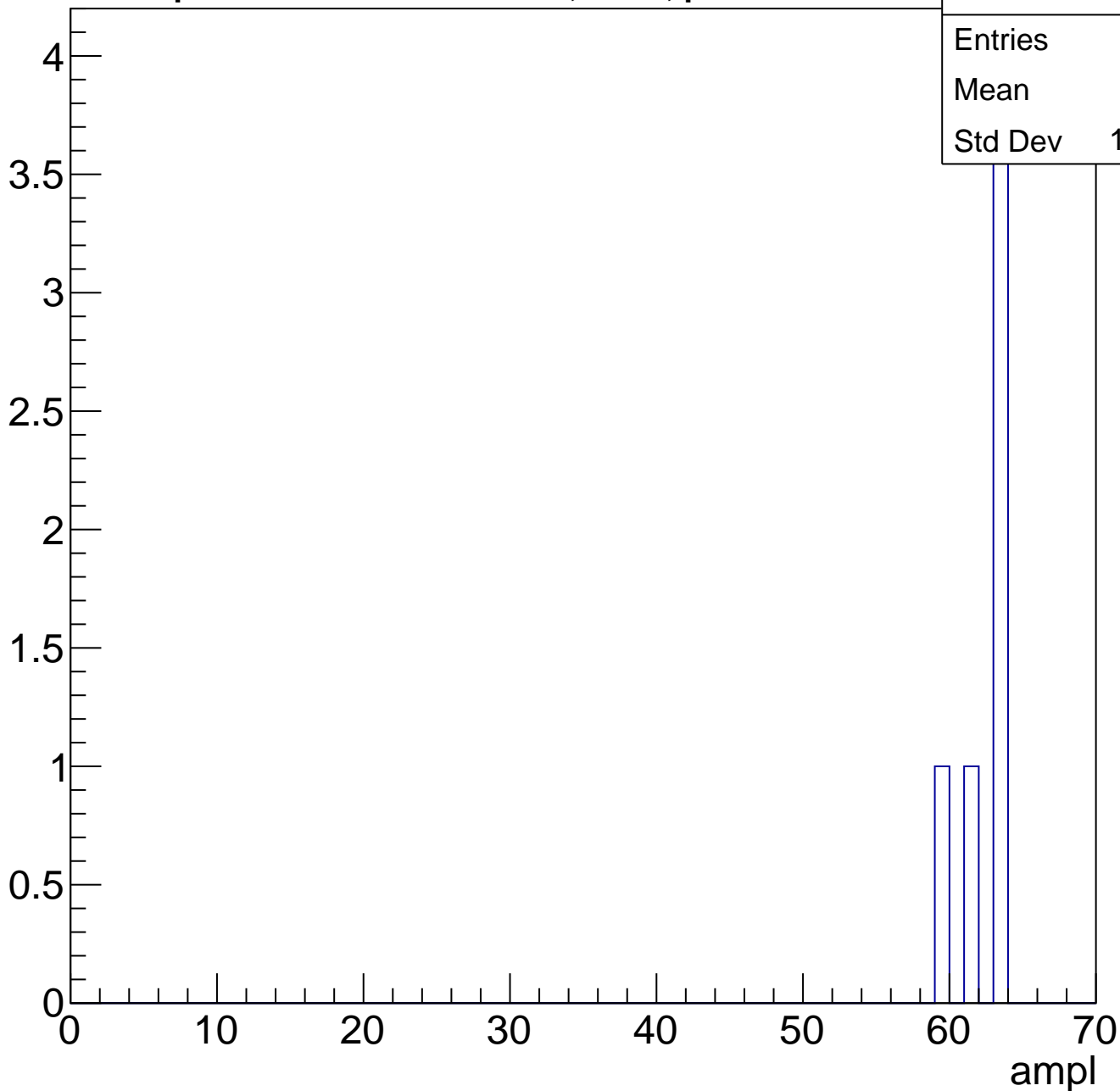
Entries	53
Mean	58.87
Std Dev	8.452



B0L002S, U5-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch127, adc0

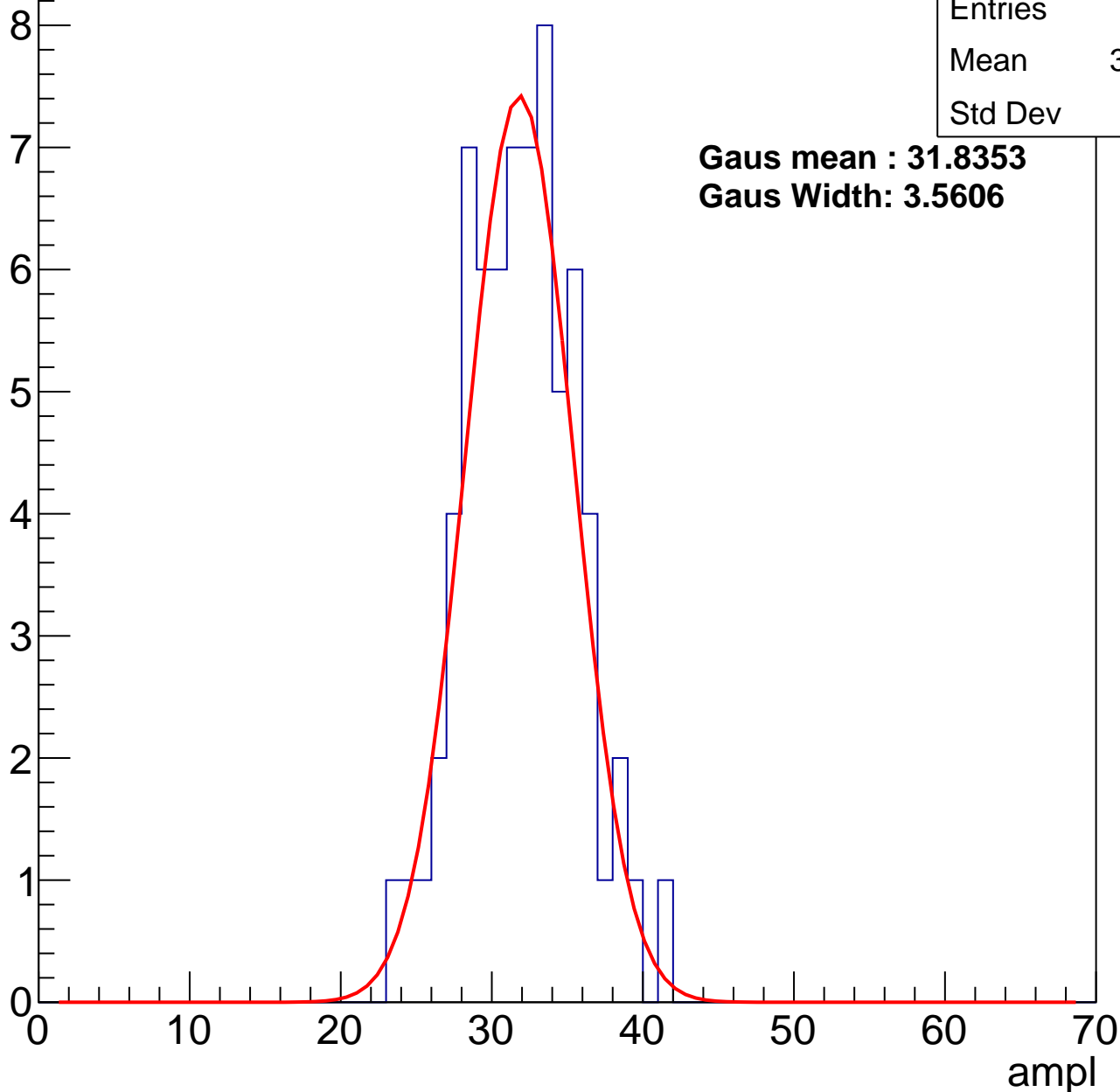
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	31.49
Std Dev	3.62

Gaus mean : 31.8353

Gaus Width: 3.5606



B0L002S, U5-ch127, adc1

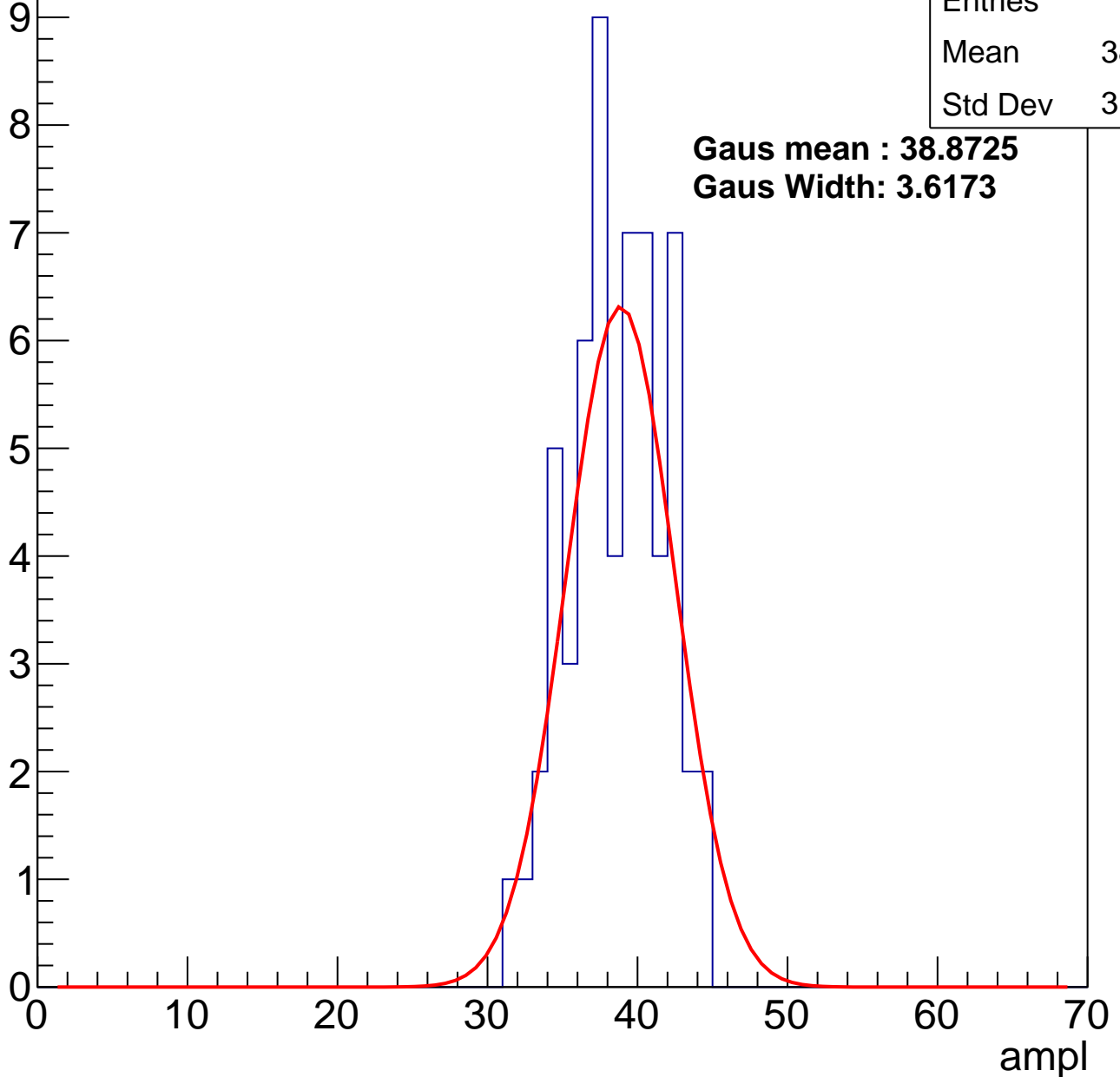
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	38.17
Std Dev	3.105

Gaus mean : 38.8725

Gaus Width: 3.6173



B0L002S, U5-ch127, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	44.13
Std Dev	3.326

Gaus mean : 44.7880

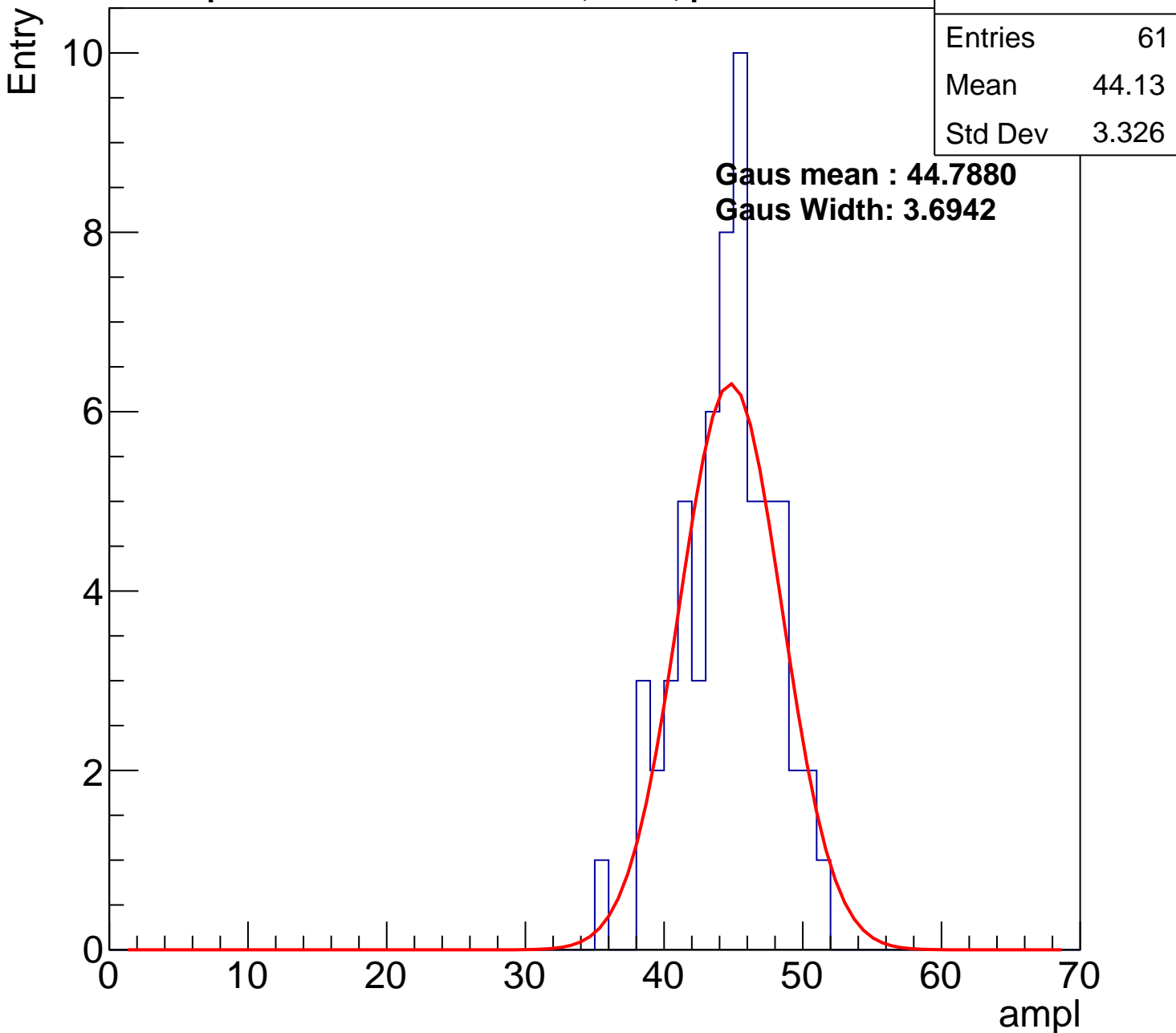
Gaus Width: 3.6942

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

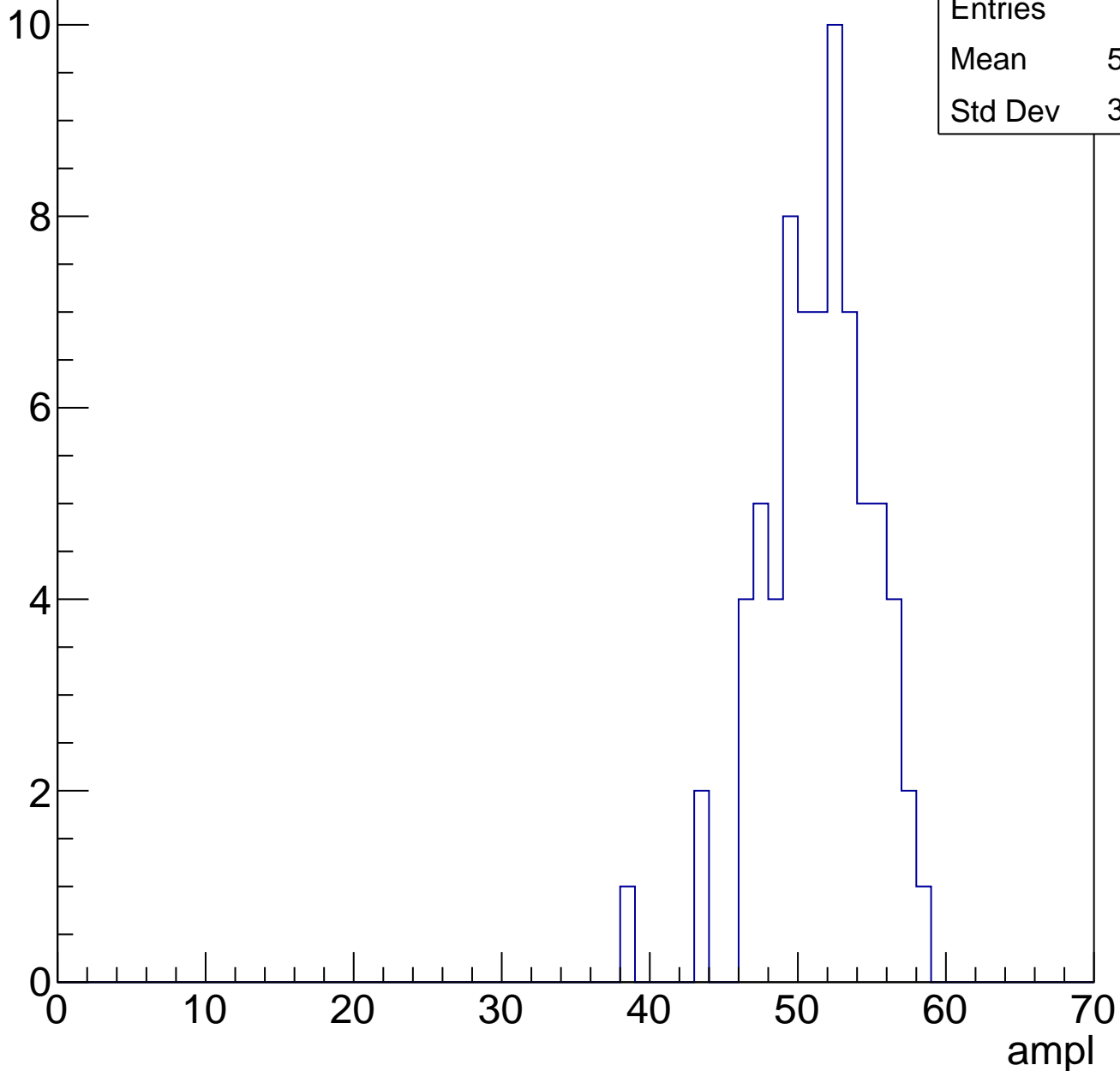


B0L002S, U5-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	50.92
Std Dev	3.605

Entry

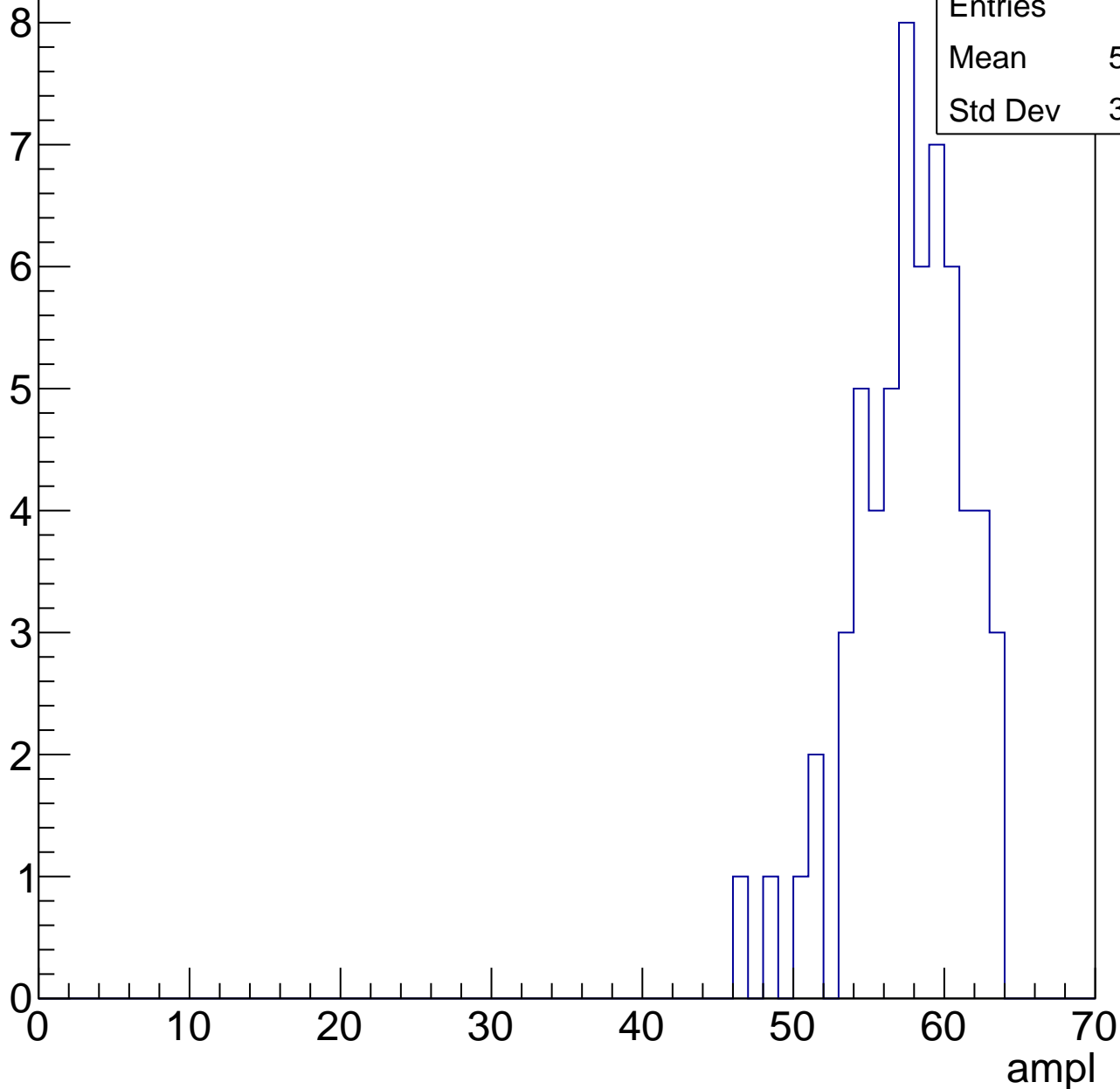


B0L002S, U5-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

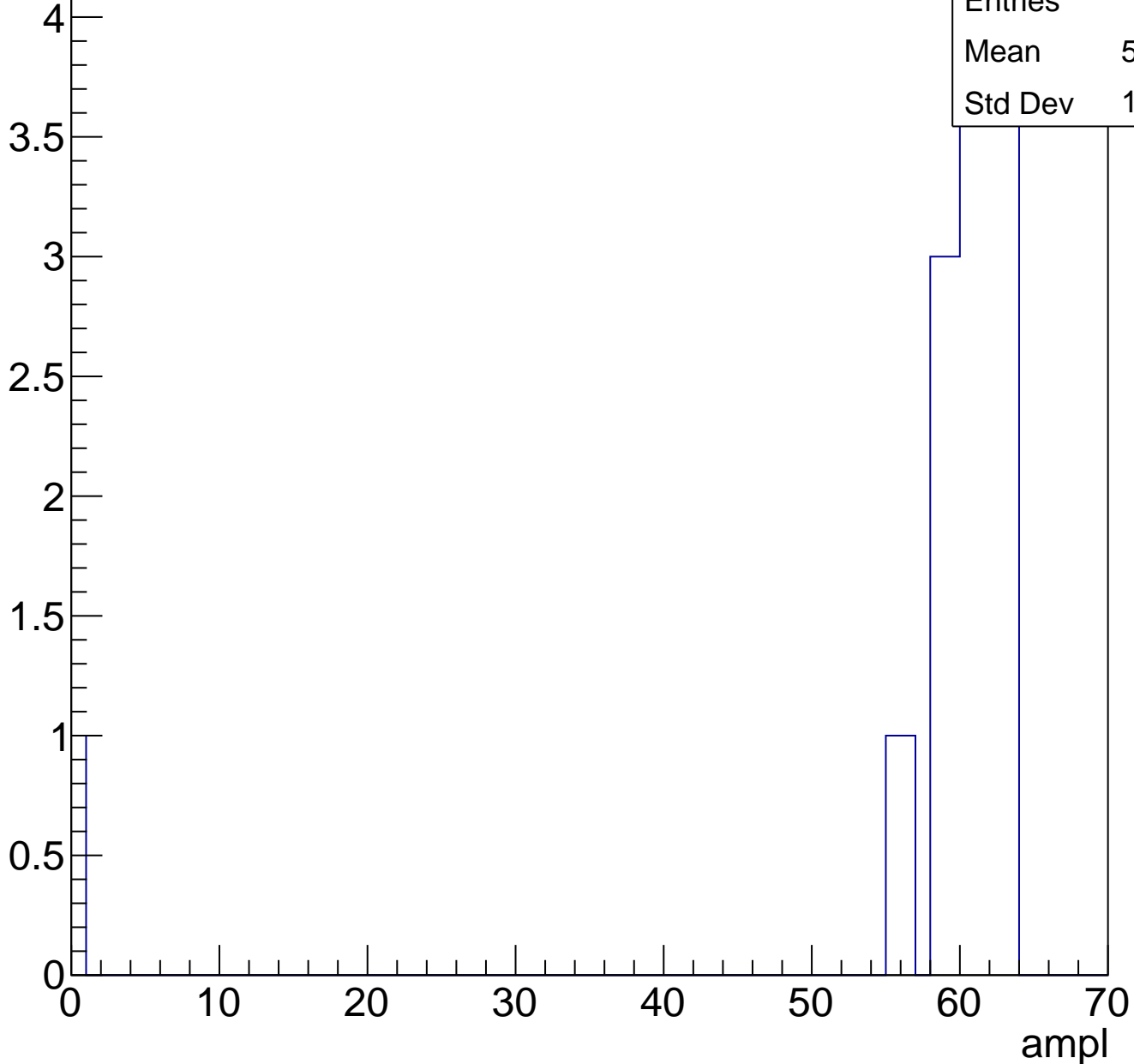
Entries	60
Mean	57.22
Std Dev	3.638



B0L002S, U5-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0