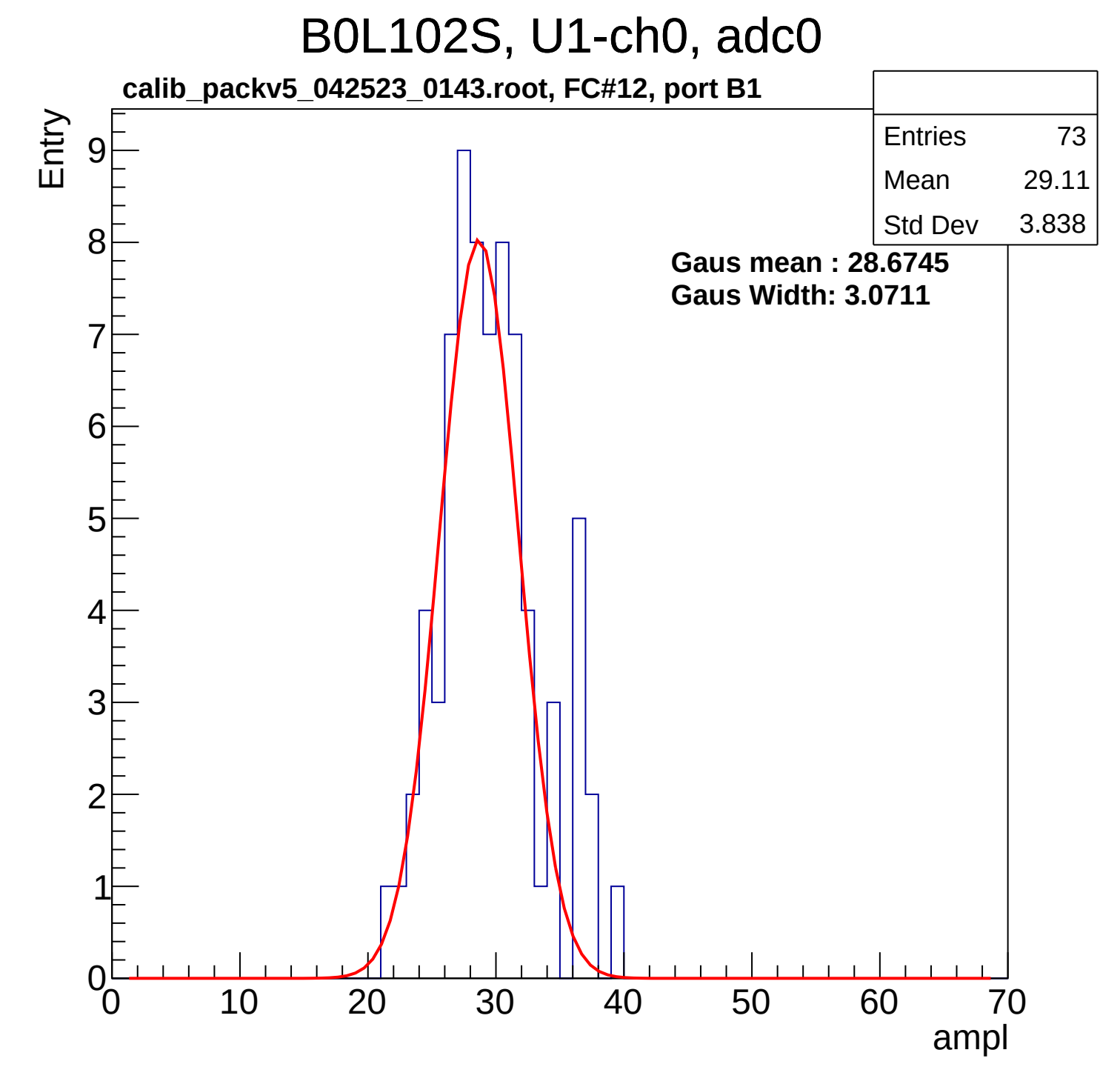
B0L102S, U1-ch0, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.11
Std Dev 3.838
Gaus mean : 28.6745
Gaus Width: 3.0711
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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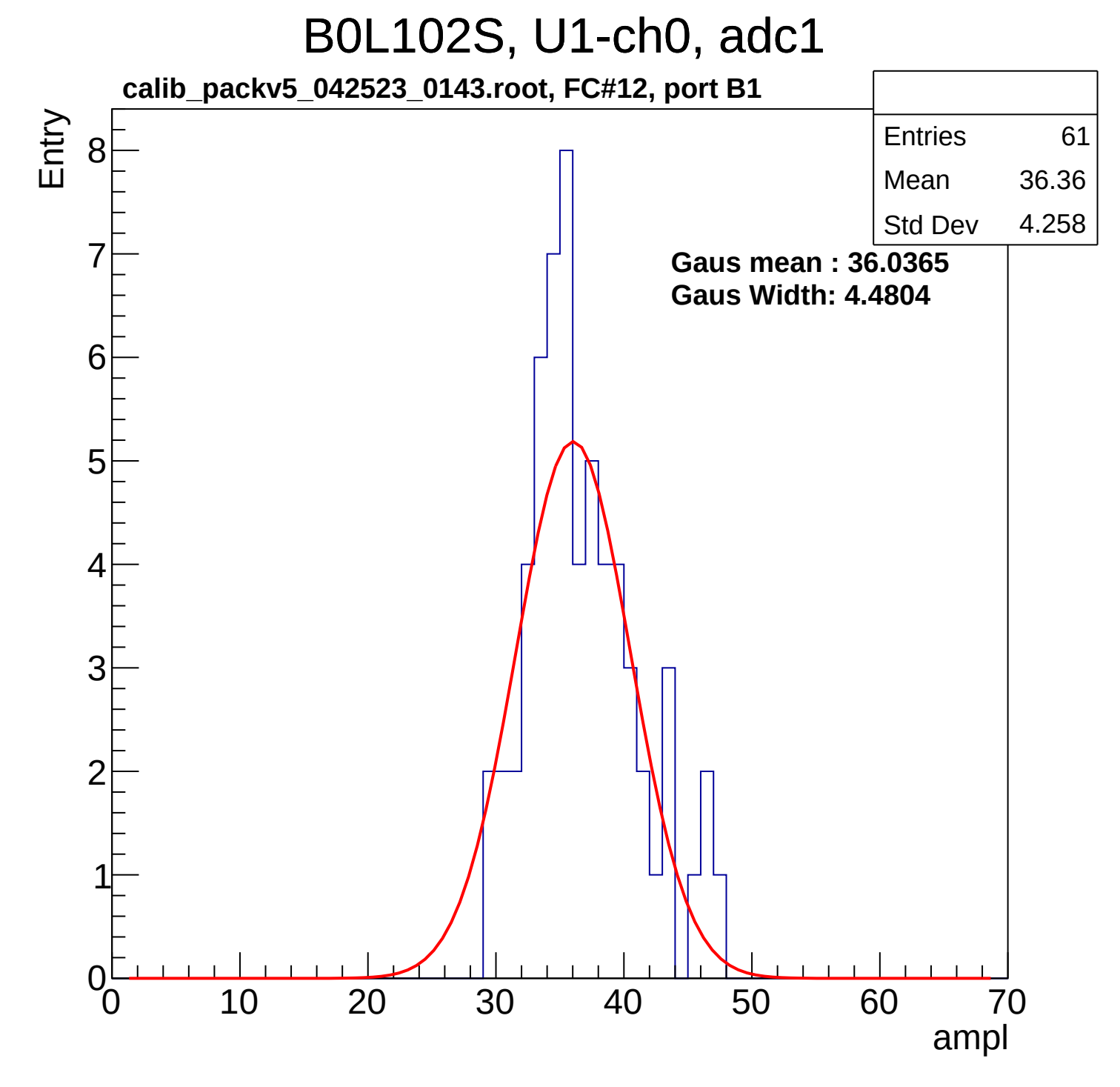
B0L102S, U1-ch0, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 36.36
Std Dev 4.258
Gaus mean : 36.0365
Gaus Width: 4.4804
Entry
ampl
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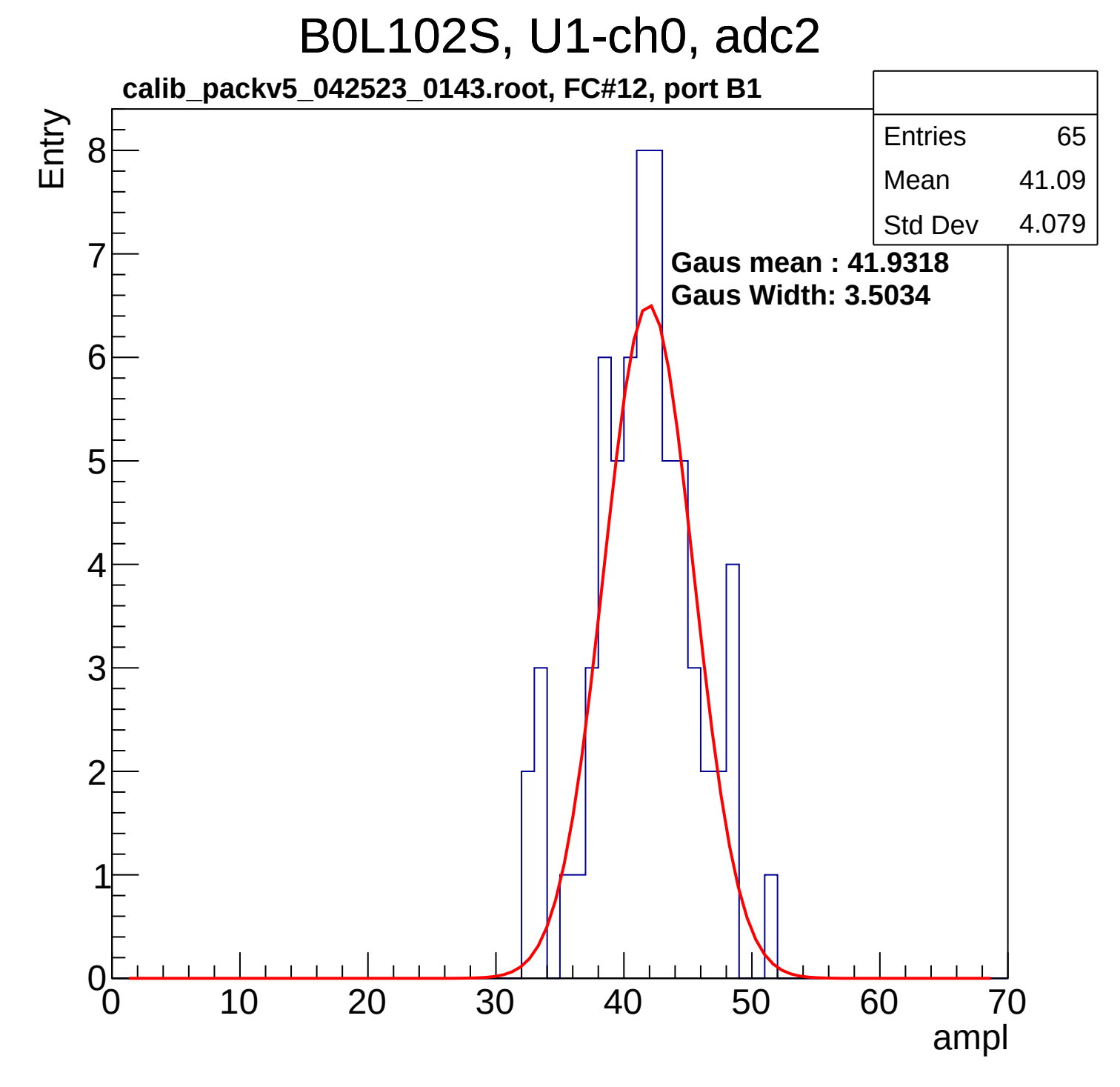
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch0, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 41.09
Std Dev 4.079
Gaus mean : 41.9318
Gaus Width: 3.5034
Entry
ampl
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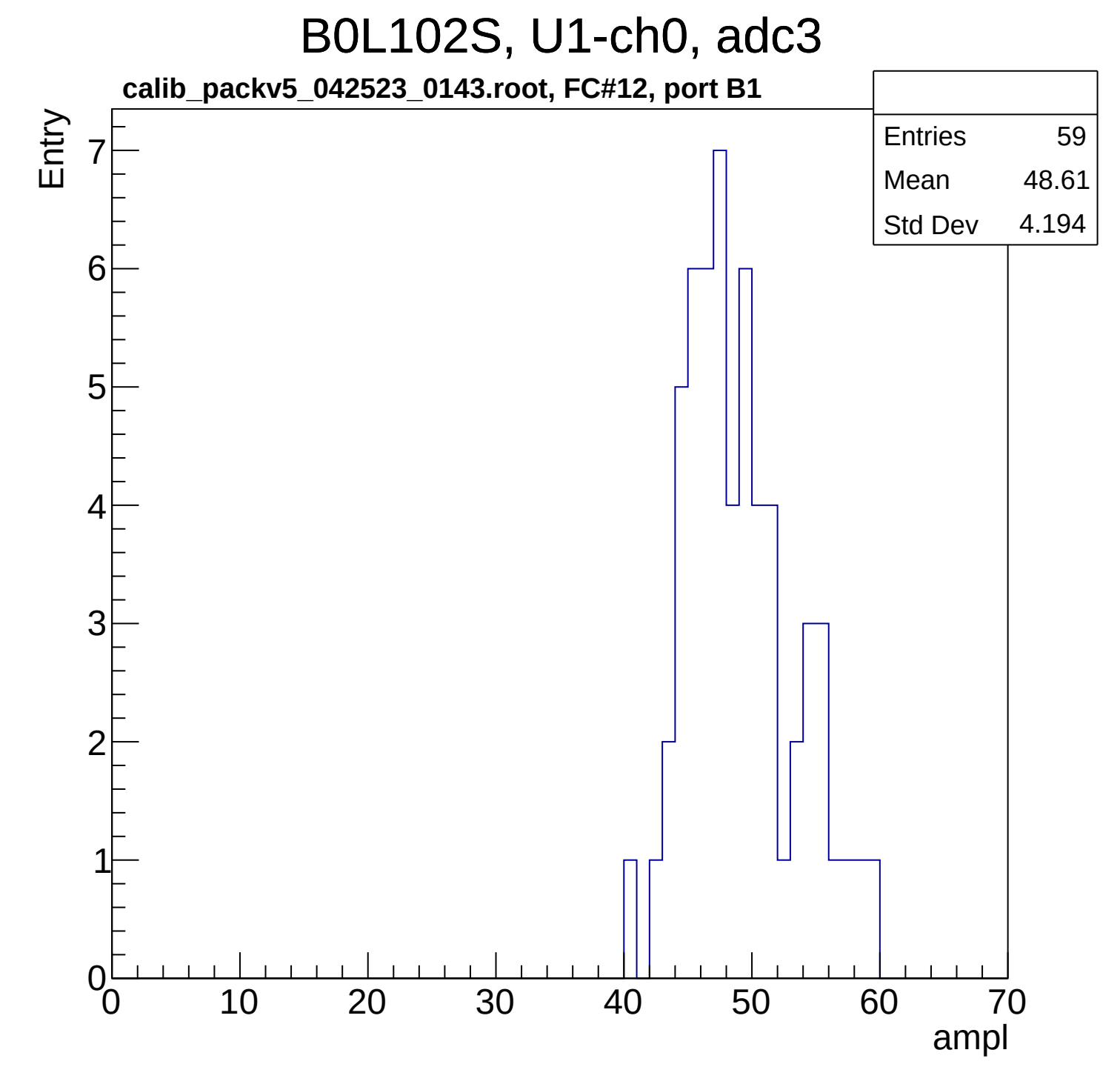
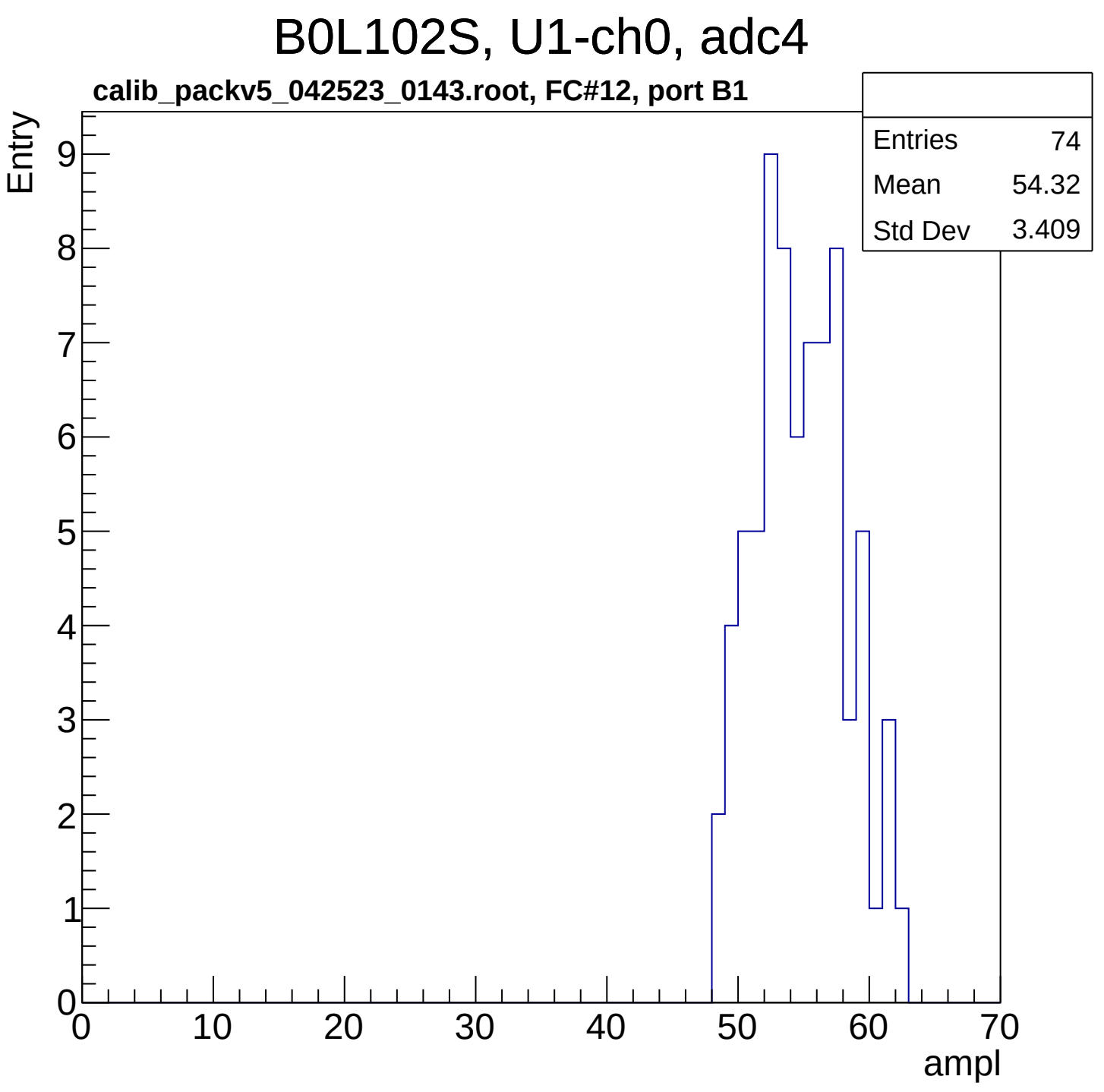
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch0, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.61
Std Dev 4.194
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch0, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 54.32
Std Dev 3.409
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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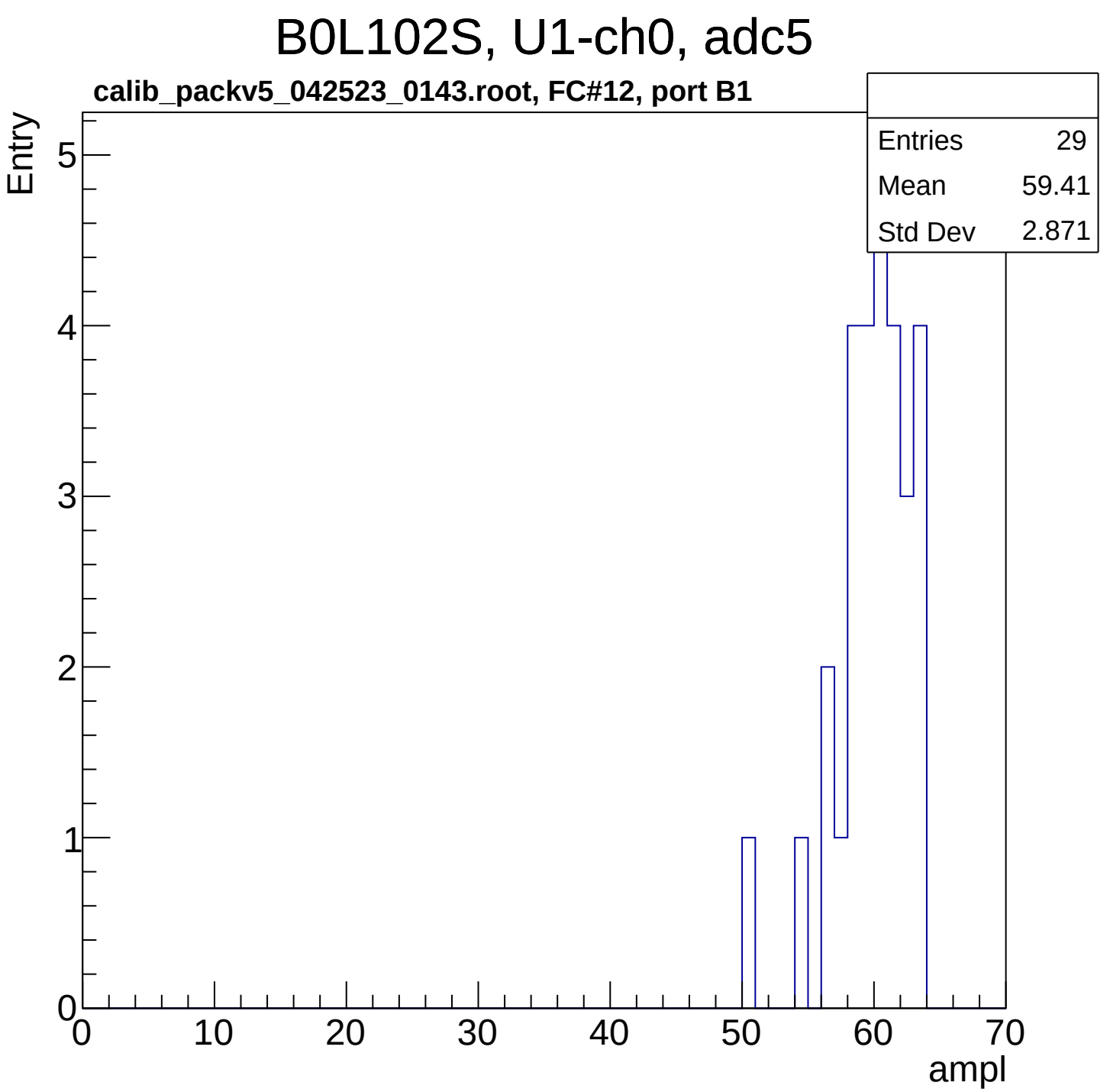
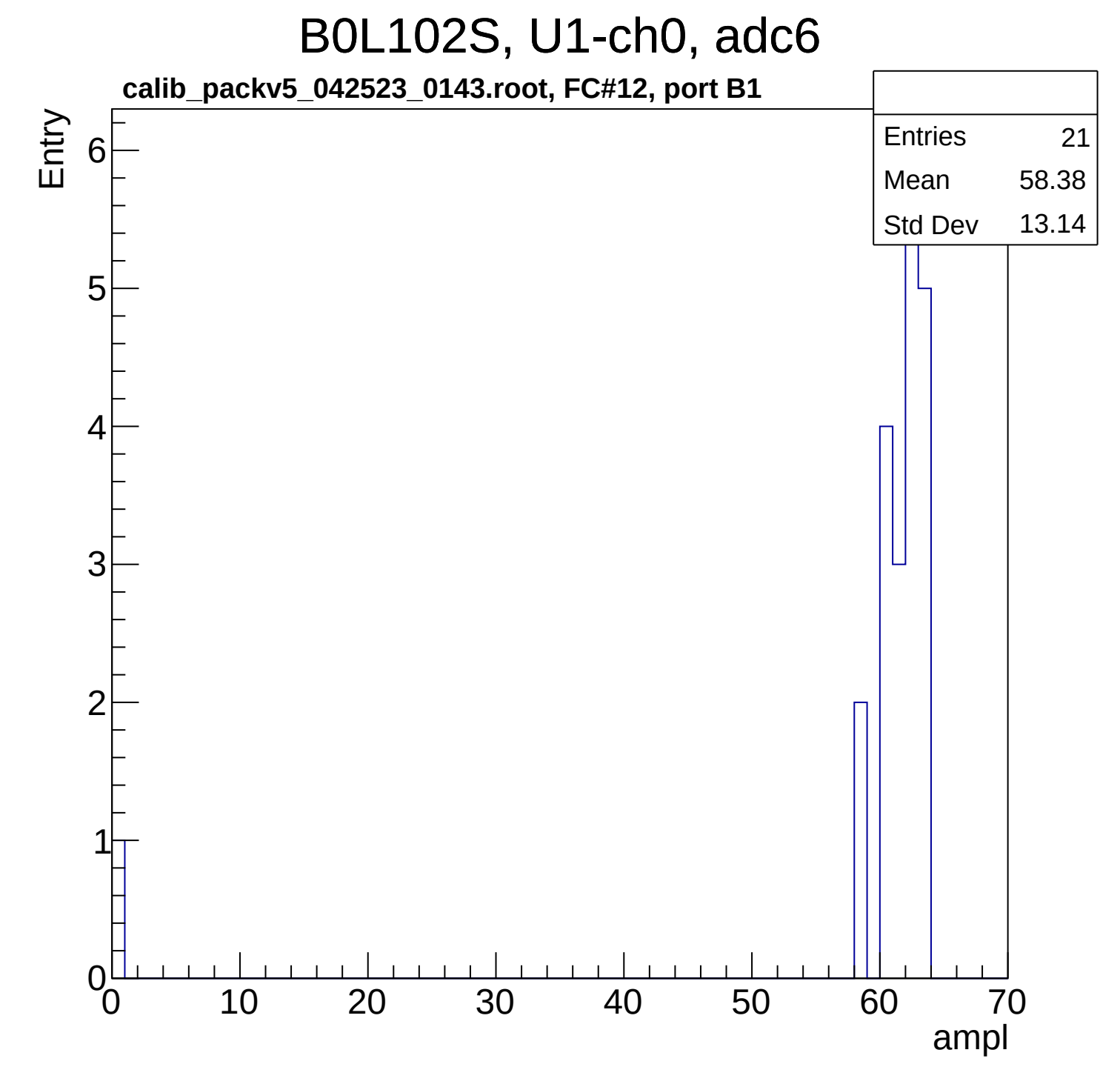

B0L102S, U1-ch0, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 29
Mean 59.41
Std Dev 2.871
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U1-ch0, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 21
Mean 58.38
Std Dev 13.14
Entry
0
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2
3
4
5
6
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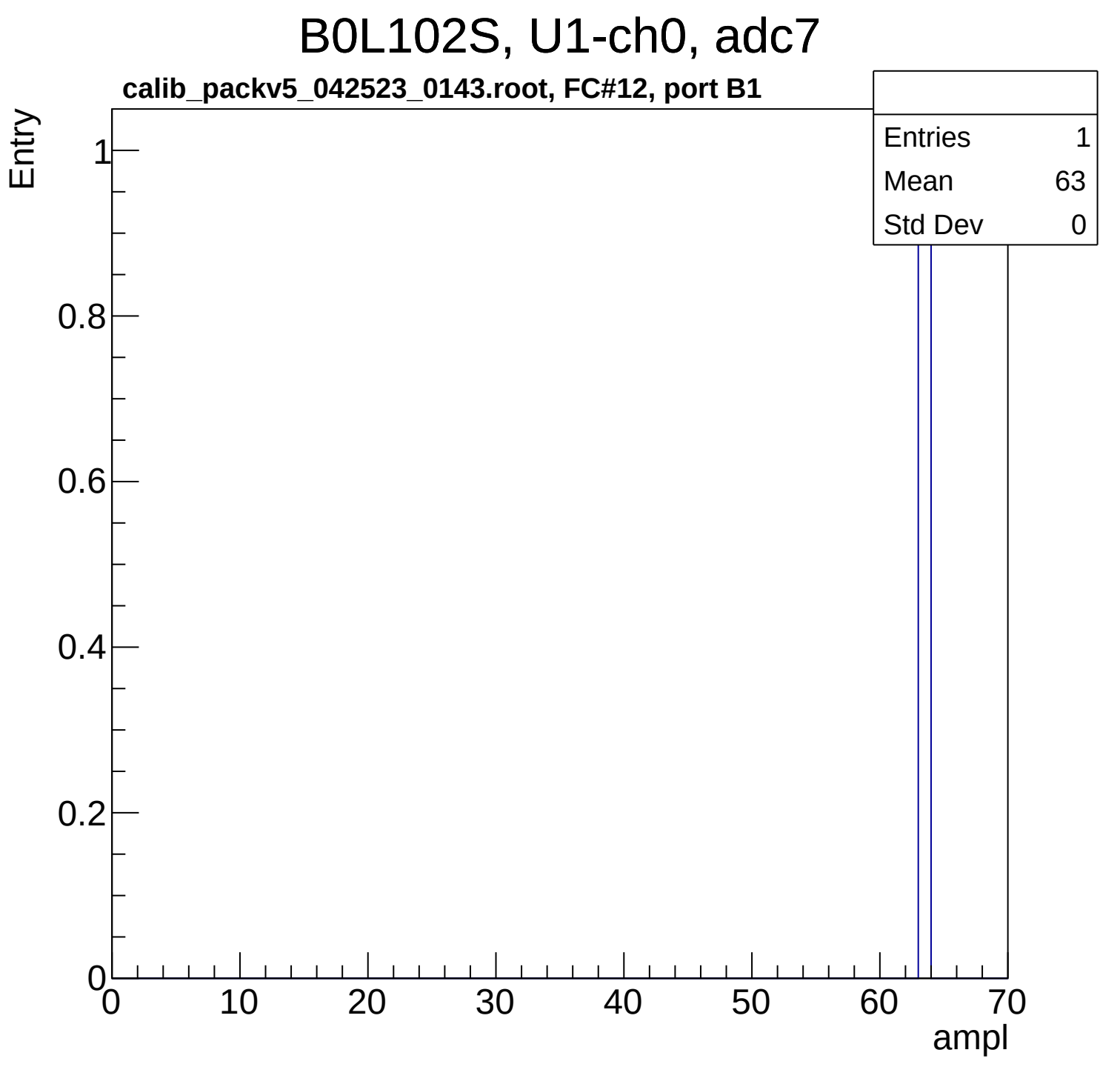
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch0, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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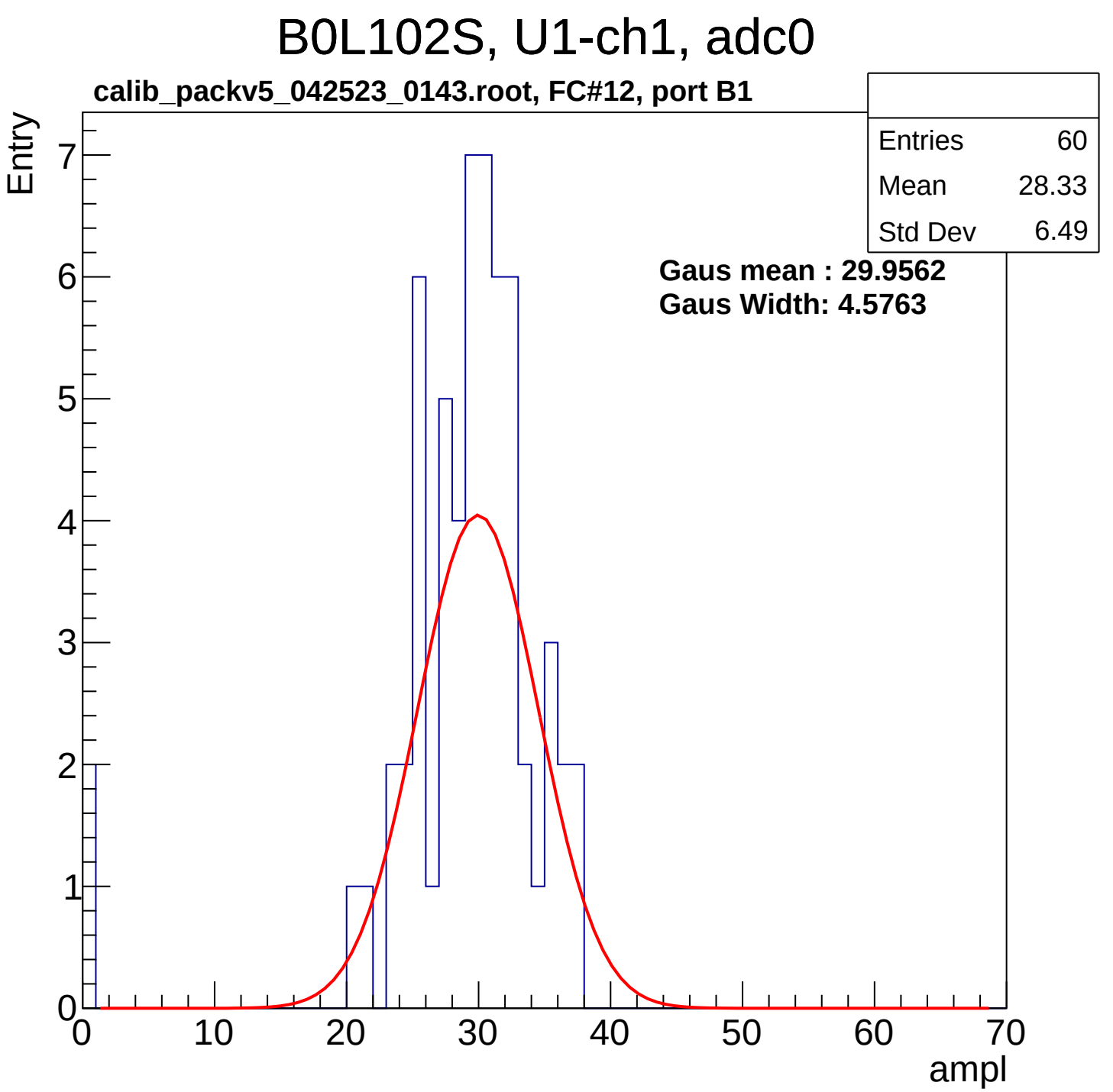
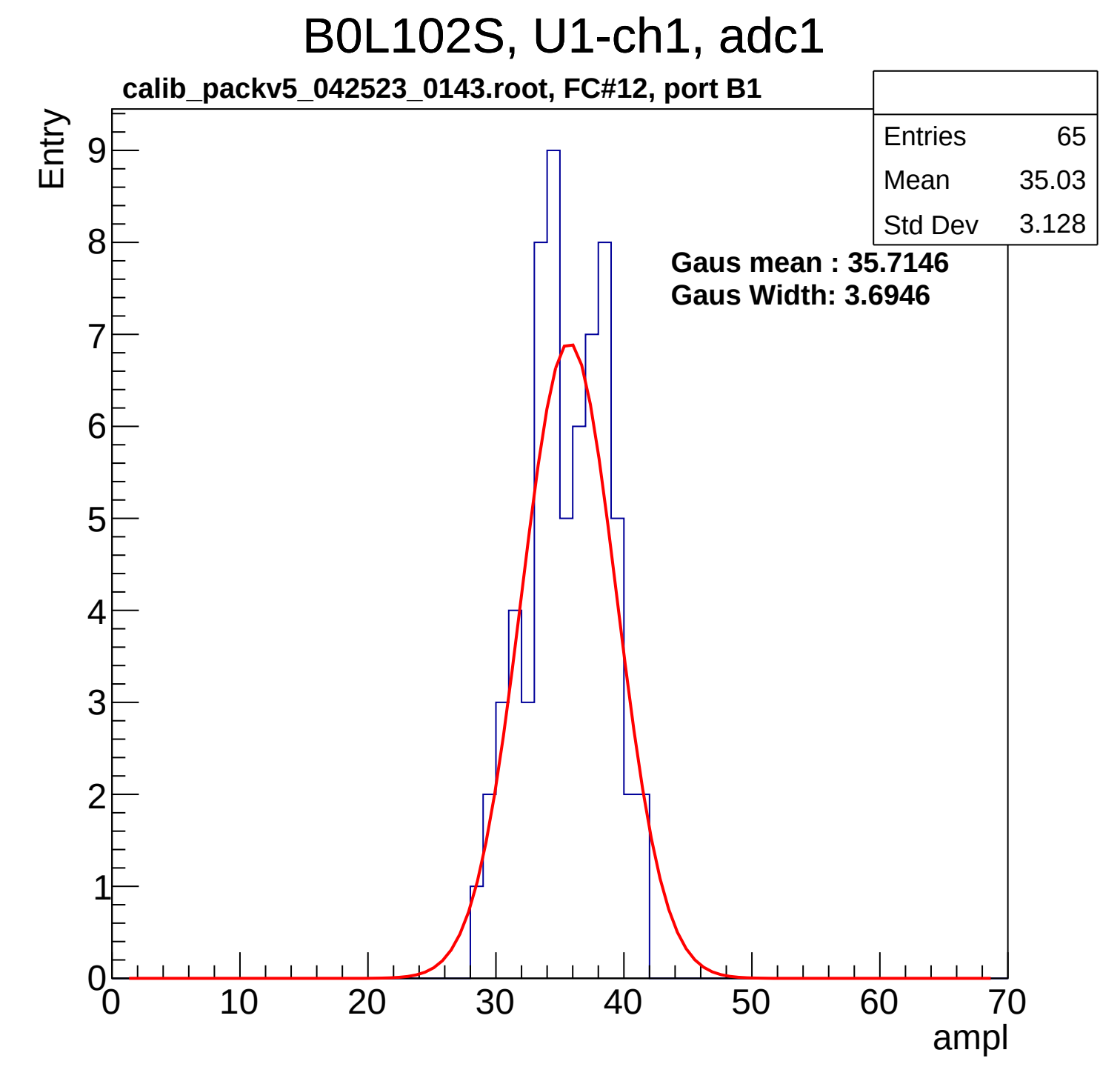
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch1, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 28.33
Std Dev 6.49
Gaus mean : 29.9562
Gaus Width: 4.5763
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch1, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.03
Std Dev 3.128
Gaus mean : 35.7146
Gaus Width: 3.6946
Entry
ampl
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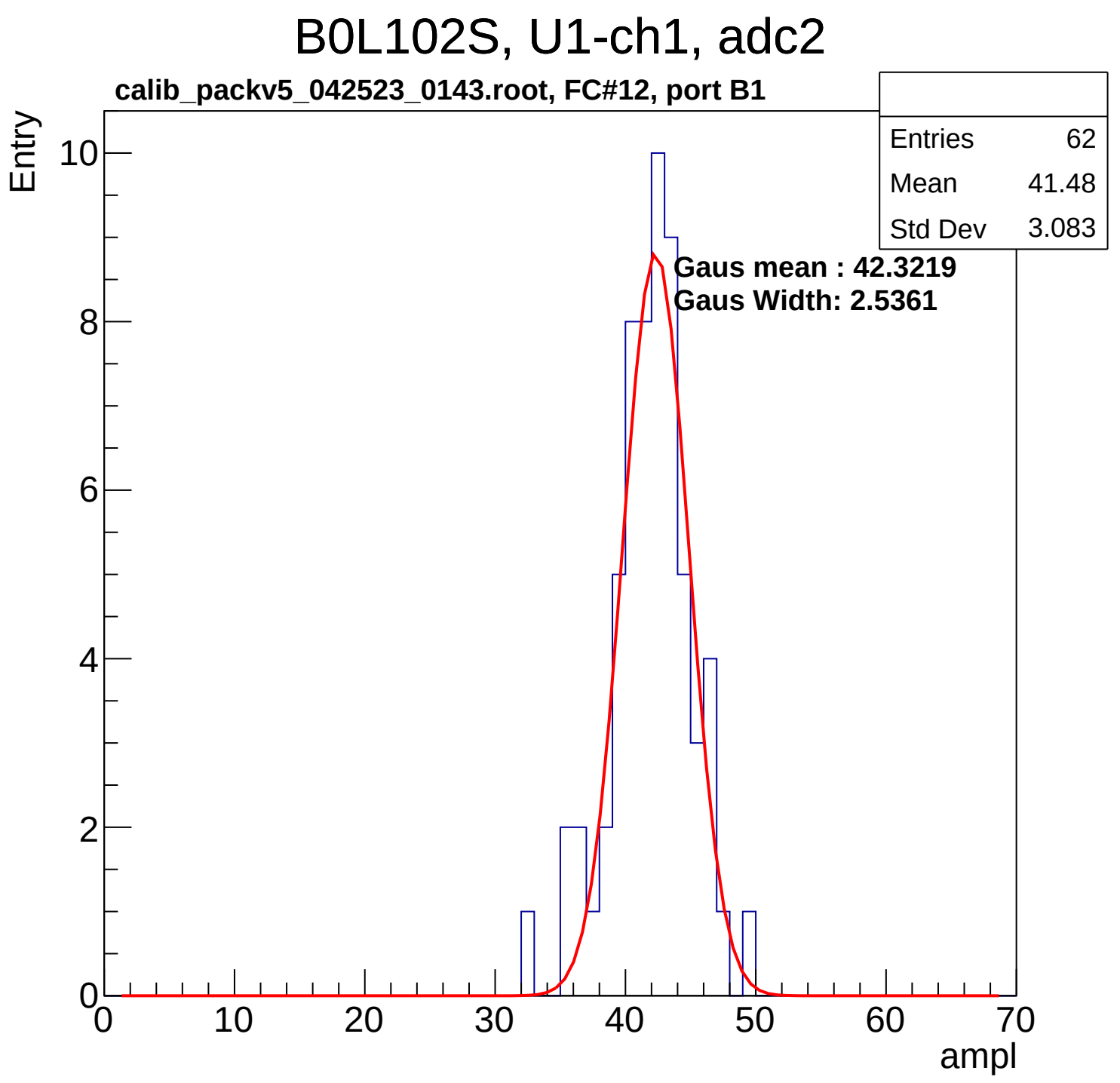
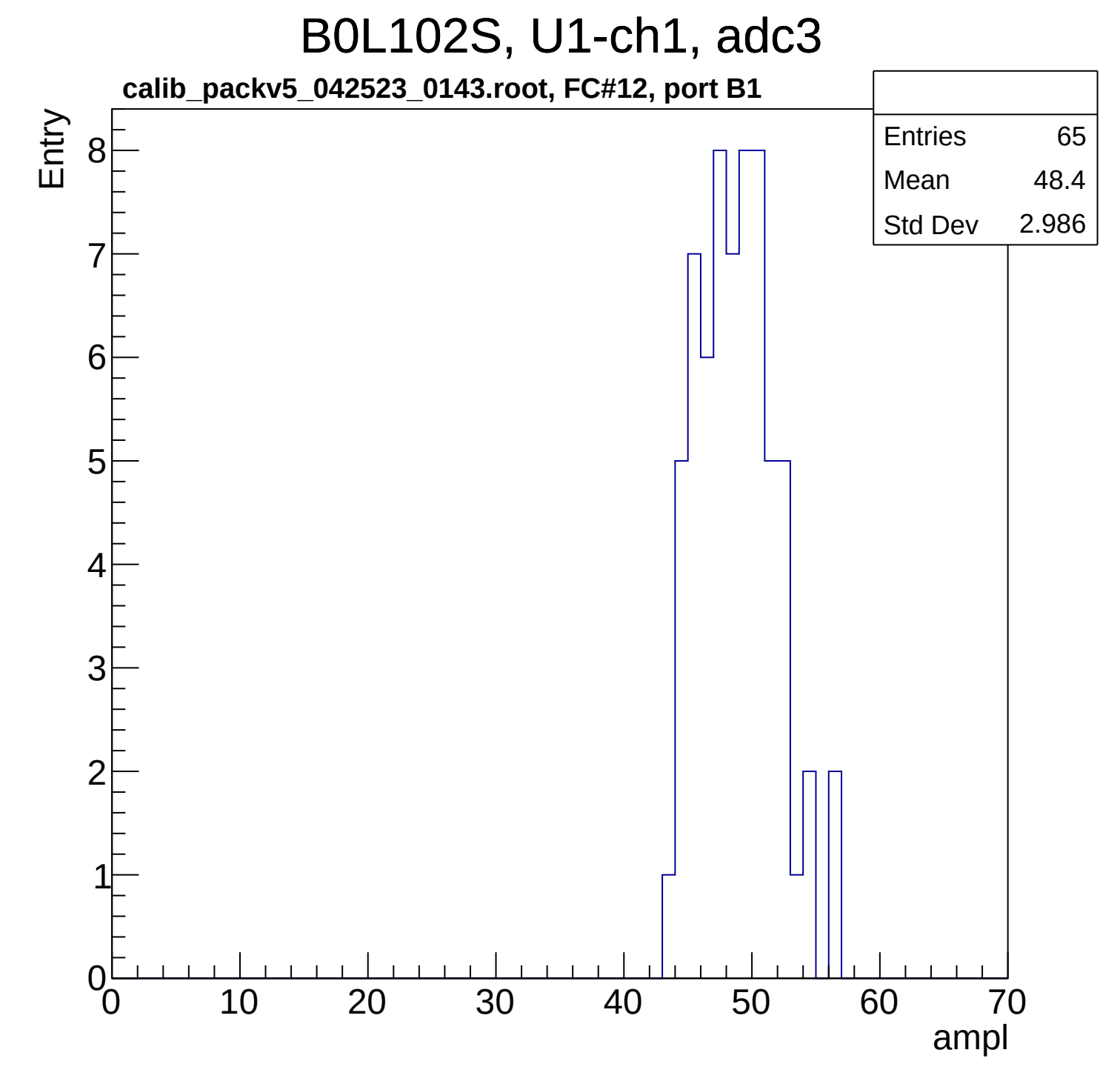
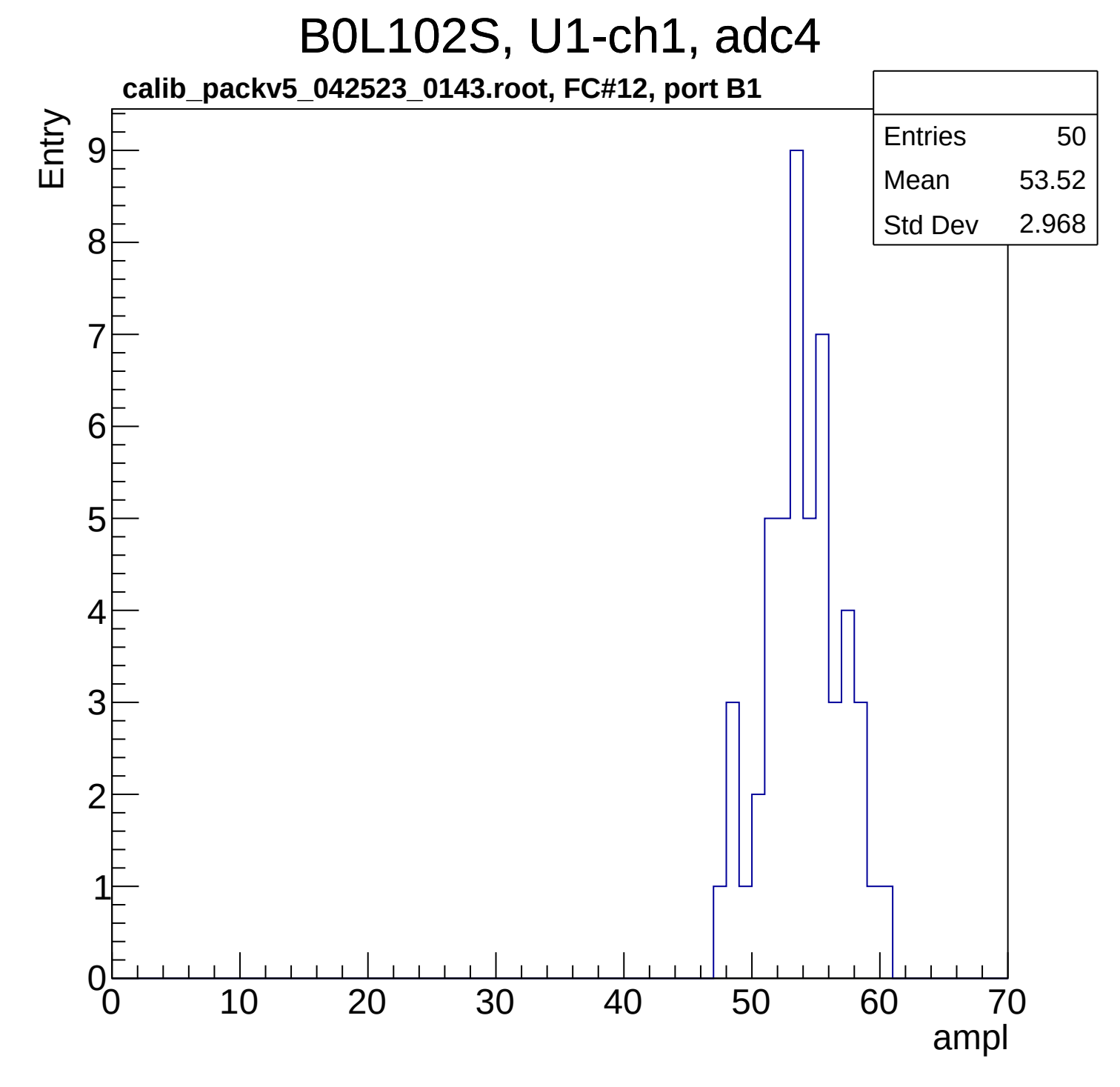
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch1, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 41.48
Std Dev 3.083
Gaus mean : 42.3219
Gaus Width: 2.5361
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch1, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.4
Std Dev 2.986
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch1, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 53.52
Std Dev 2.968
Entry
ampl
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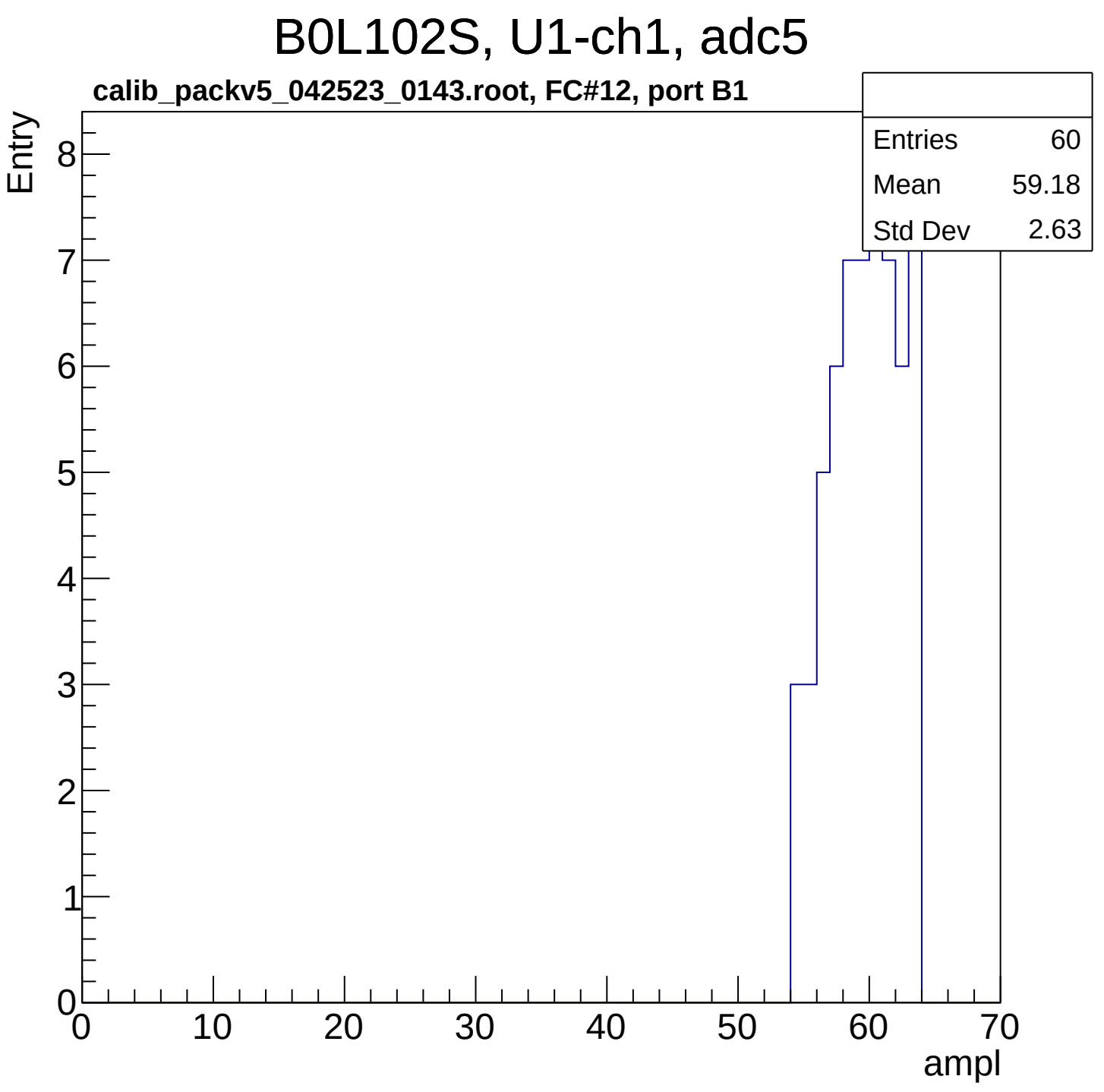
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch1, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 59.18
Std Dev 2.63
Entry
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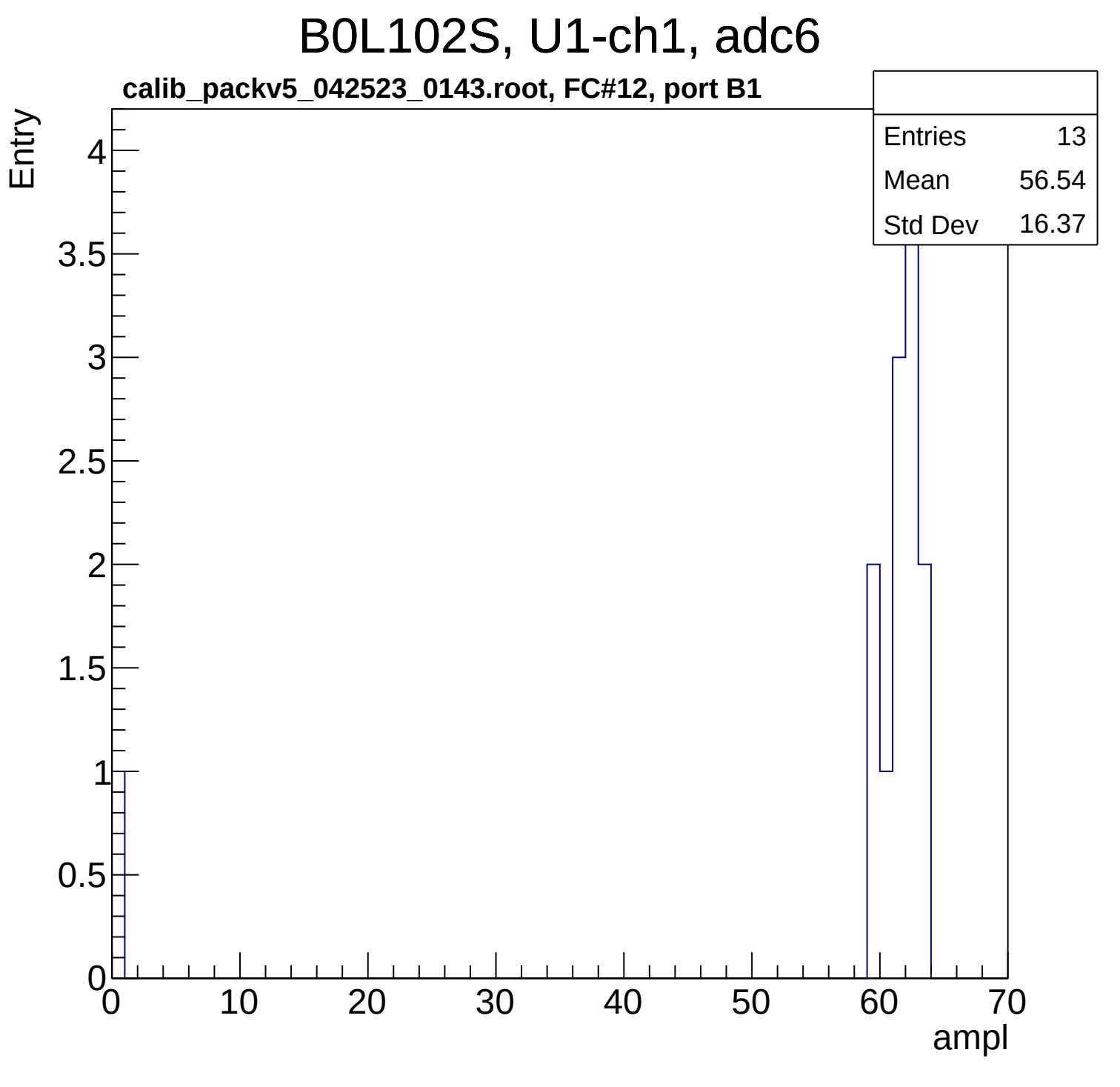
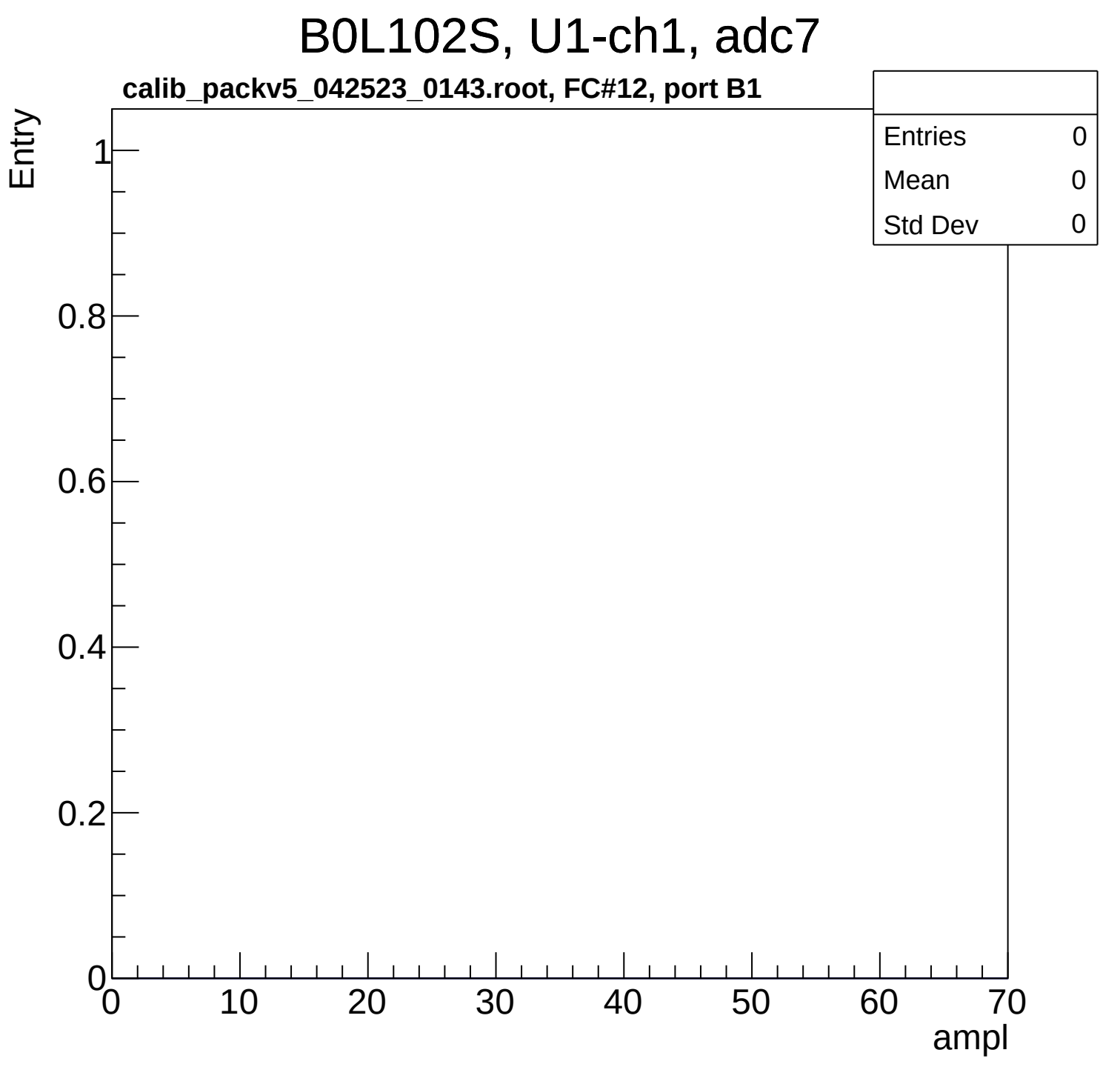
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch1, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.54
Std Dev 16.37
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch1, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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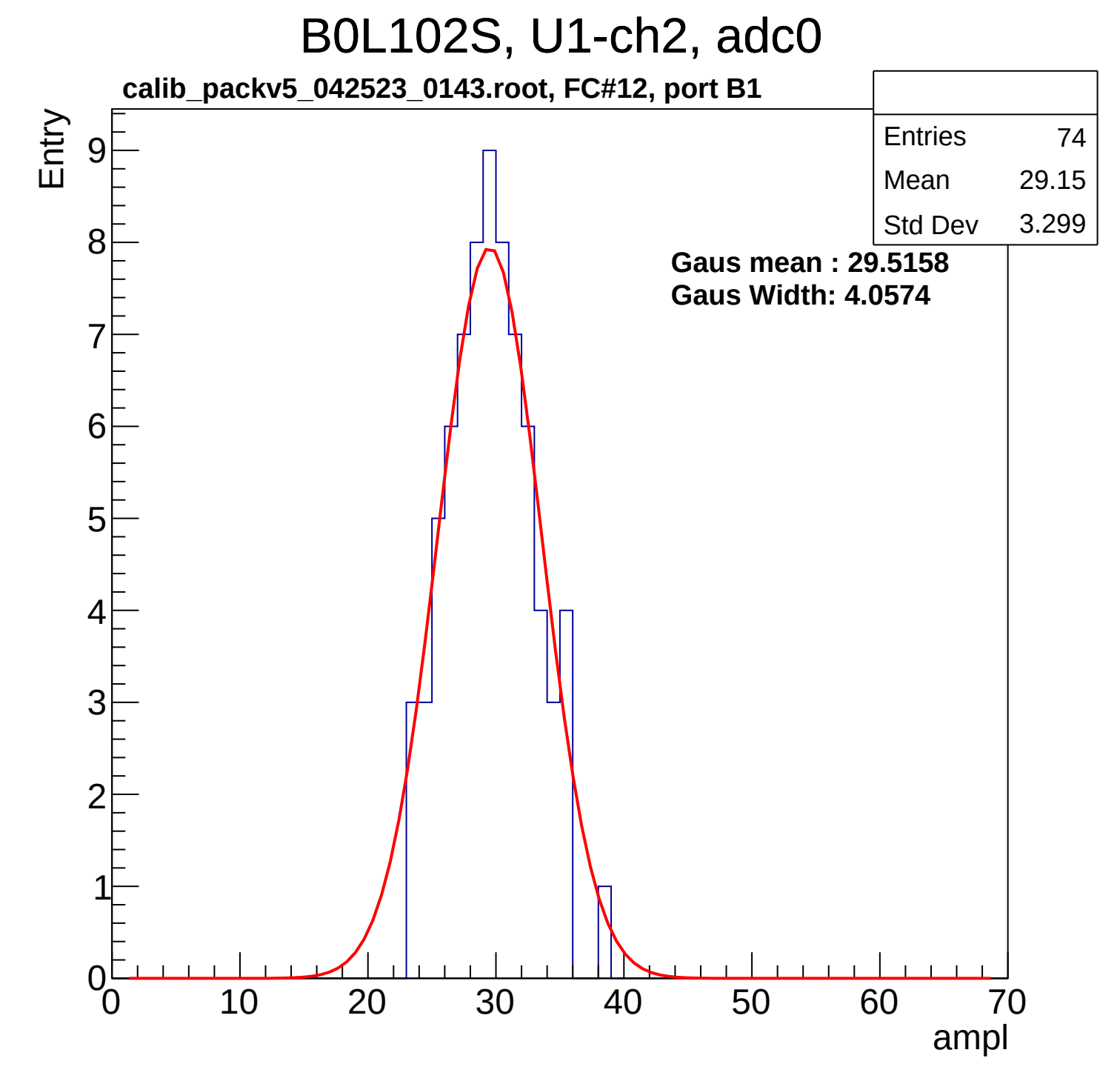
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch2, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 29.15
Std Dev 3.299
Gaus mean : 29.5158
Gaus Width: 4.0574
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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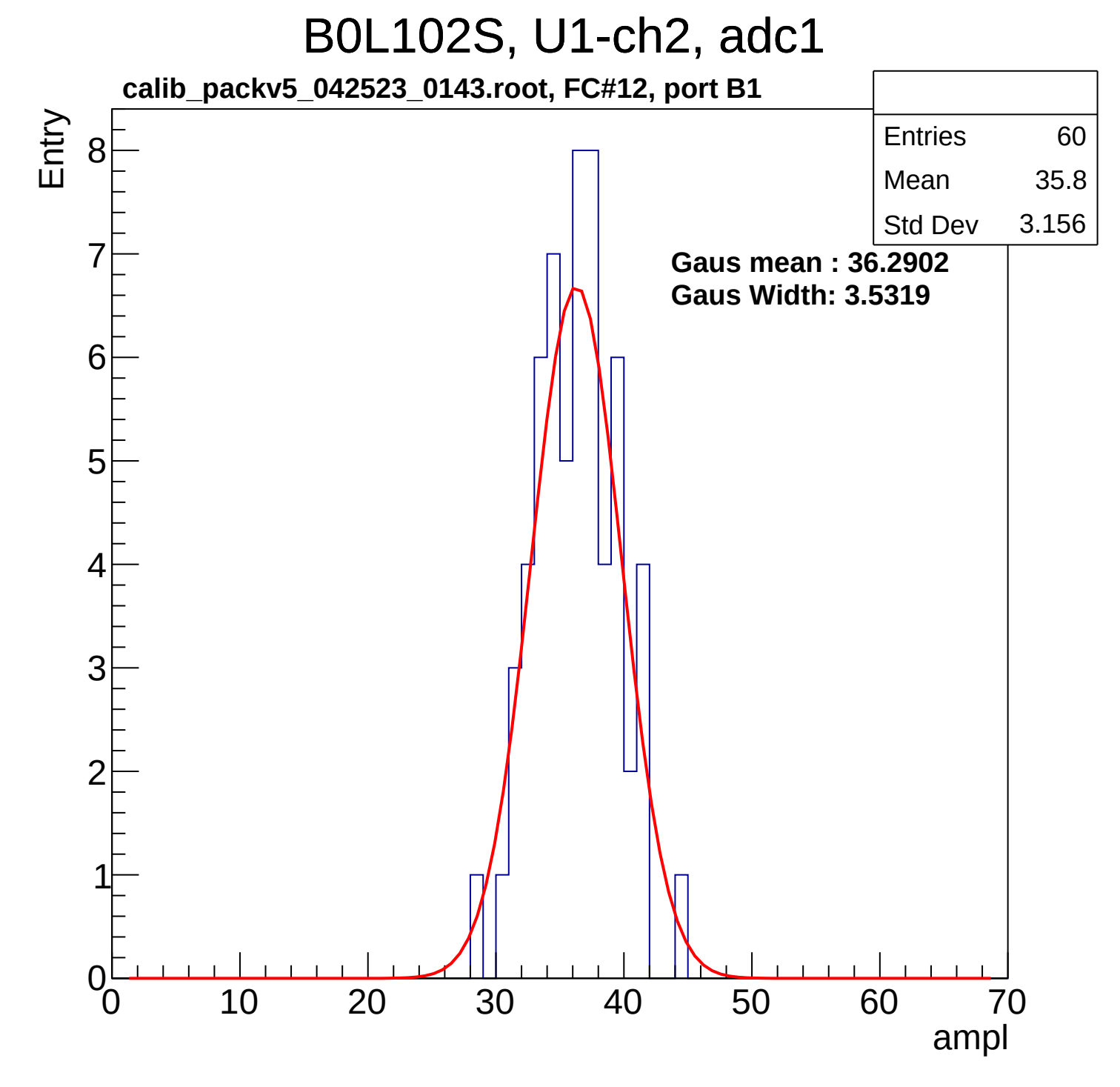
40
50
60
70


B0L102S, U1-ch2, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 35.8
Std Dev 3.156
Gaus mean : 36.2902
Gaus Width: 3.5319
Entry
ampl
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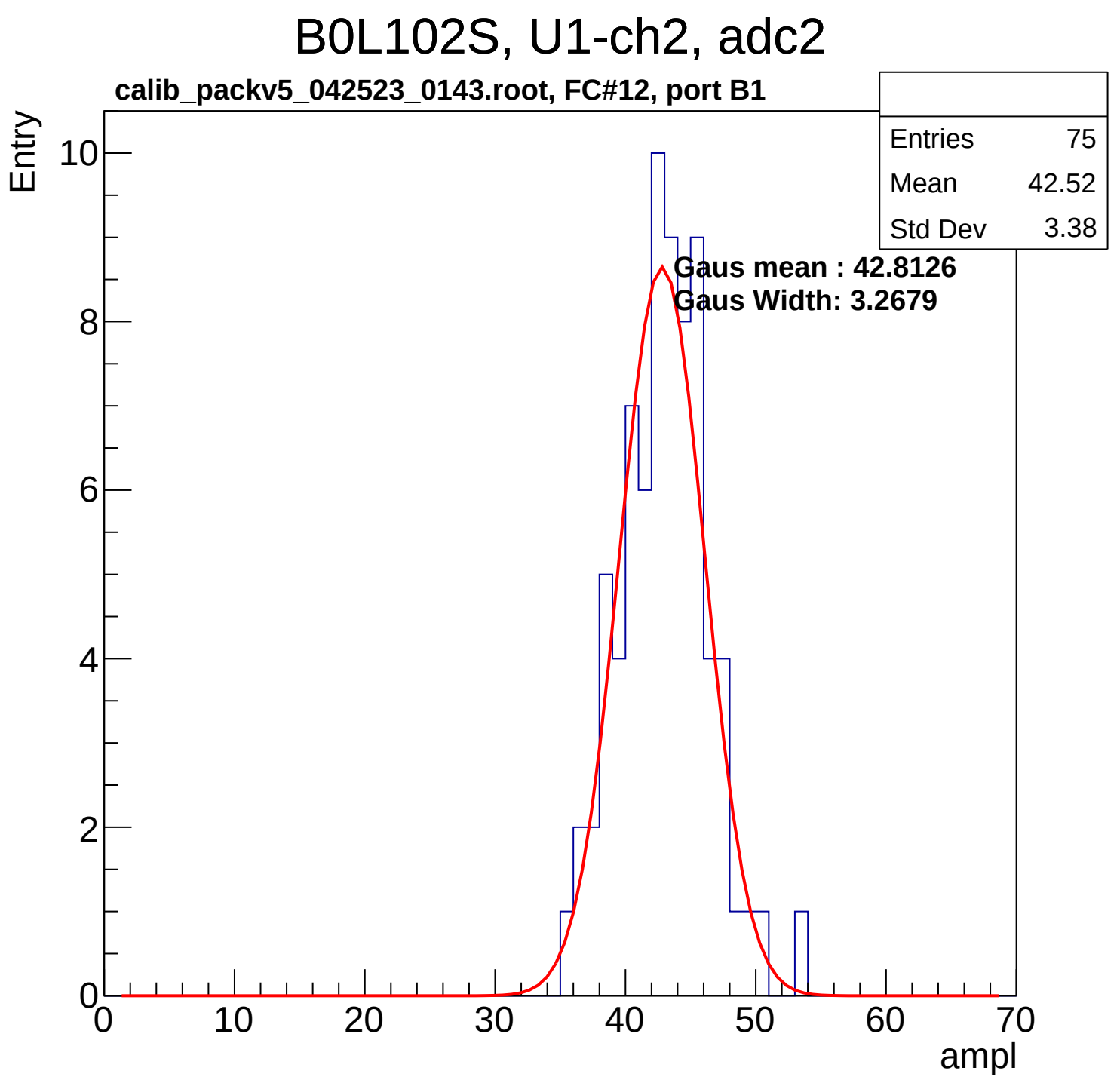
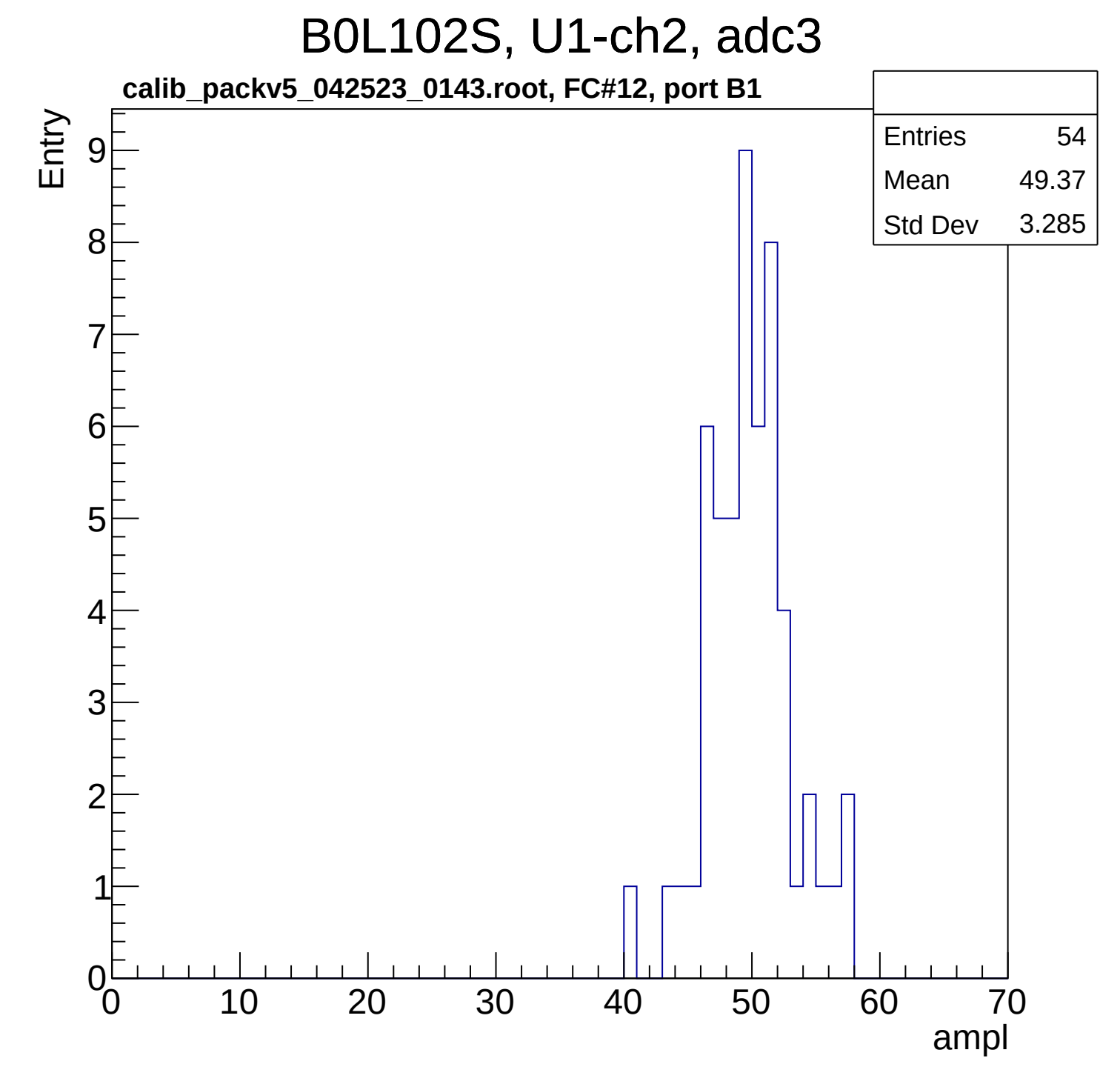
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch2, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 42.52
Std Dev 3.38
Gaus mean : 42.8126
Gaus Width: 3.2679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch2, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 49.37
Std Dev 3.285
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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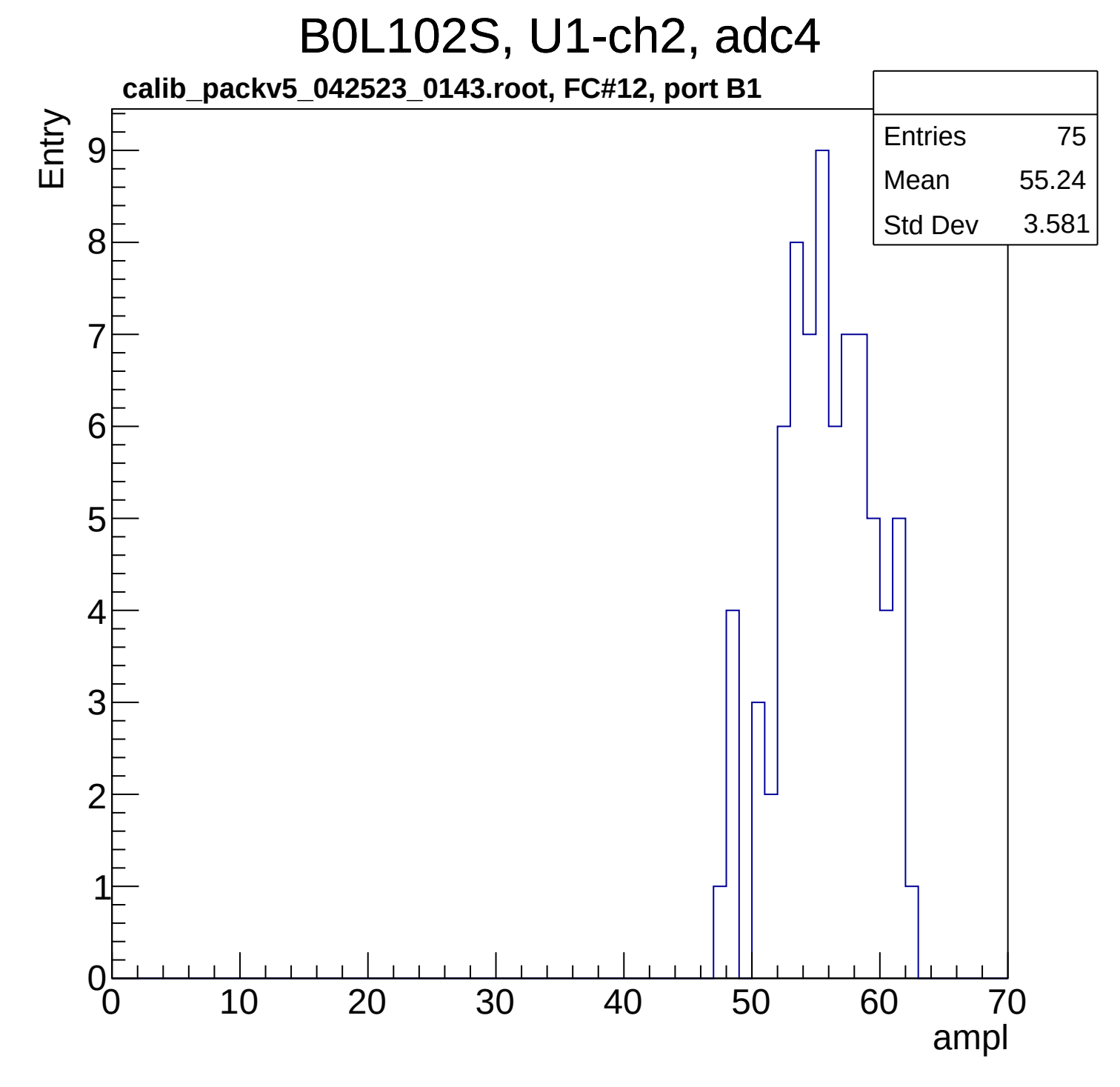
B0L102S, U1-ch2, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 55.24
Std Dev 3.581
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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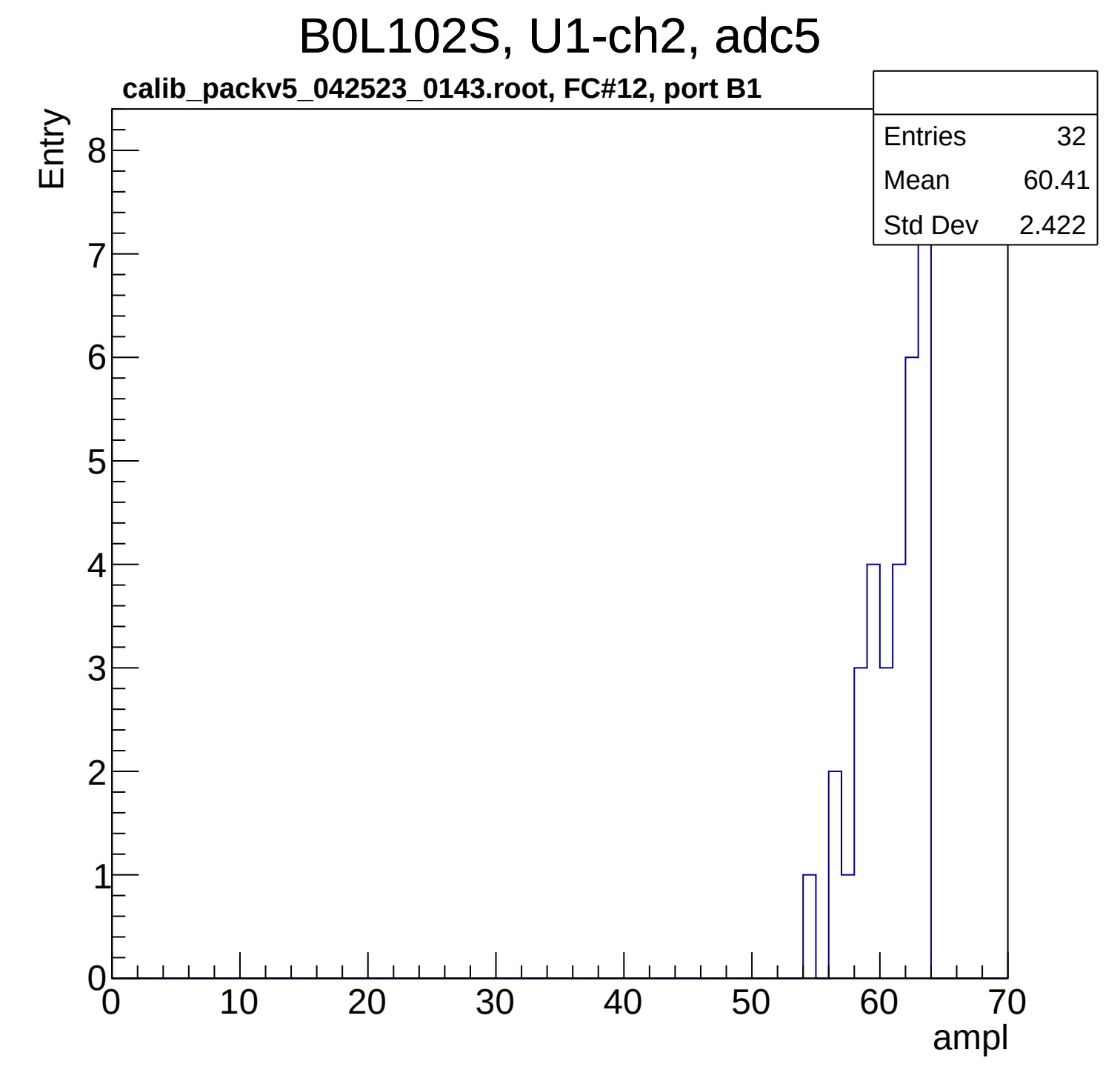
B0L102S, U1-ch2, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 60.41
Std Dev 2.422
Entry
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2
3
4
5
6
7
8
0
10
20
30
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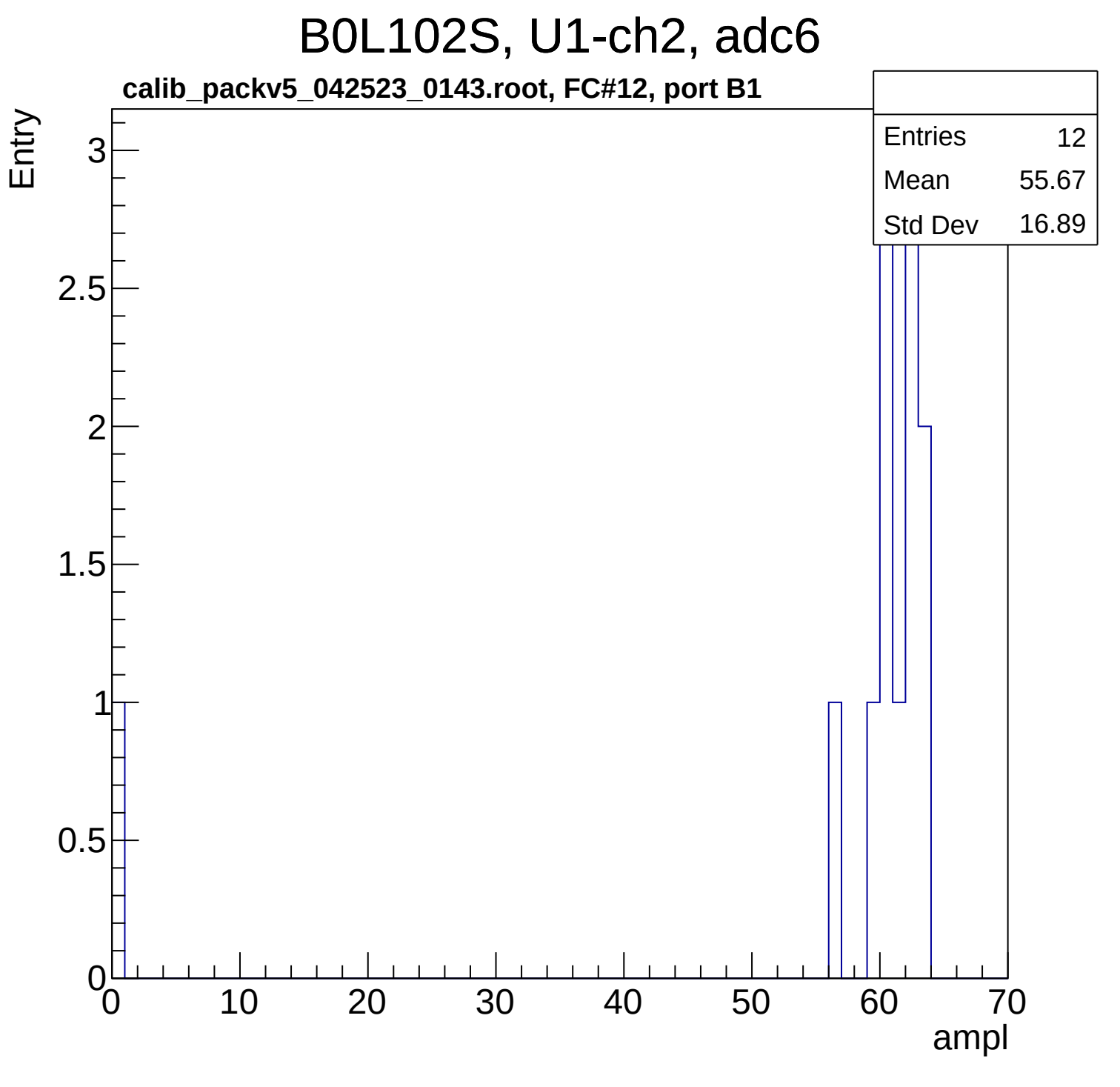
50
60
70
ampl


B0L102S, U1-ch2, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 55.67
Std Dev 16.89
Entry
ampl
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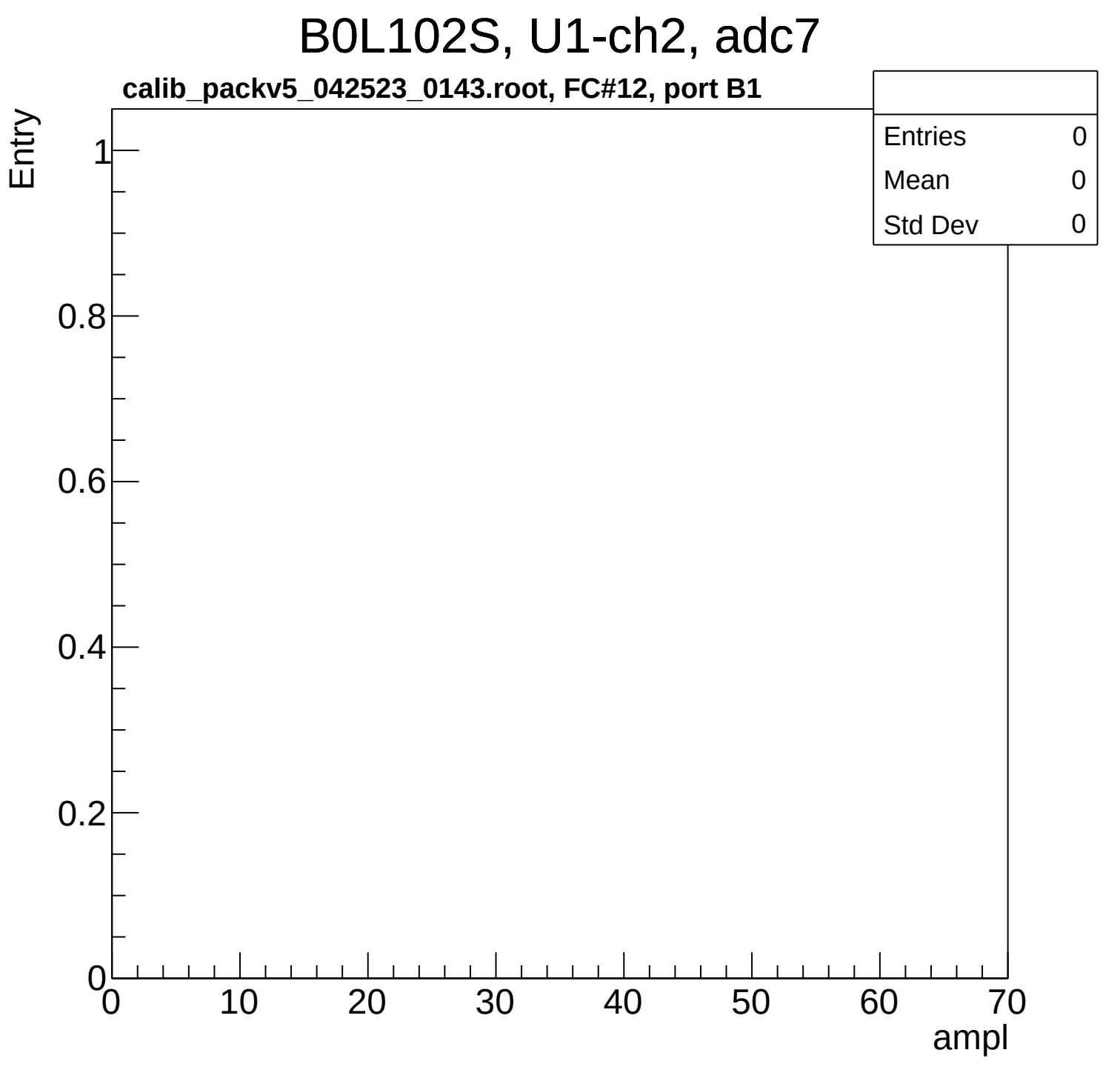
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch2, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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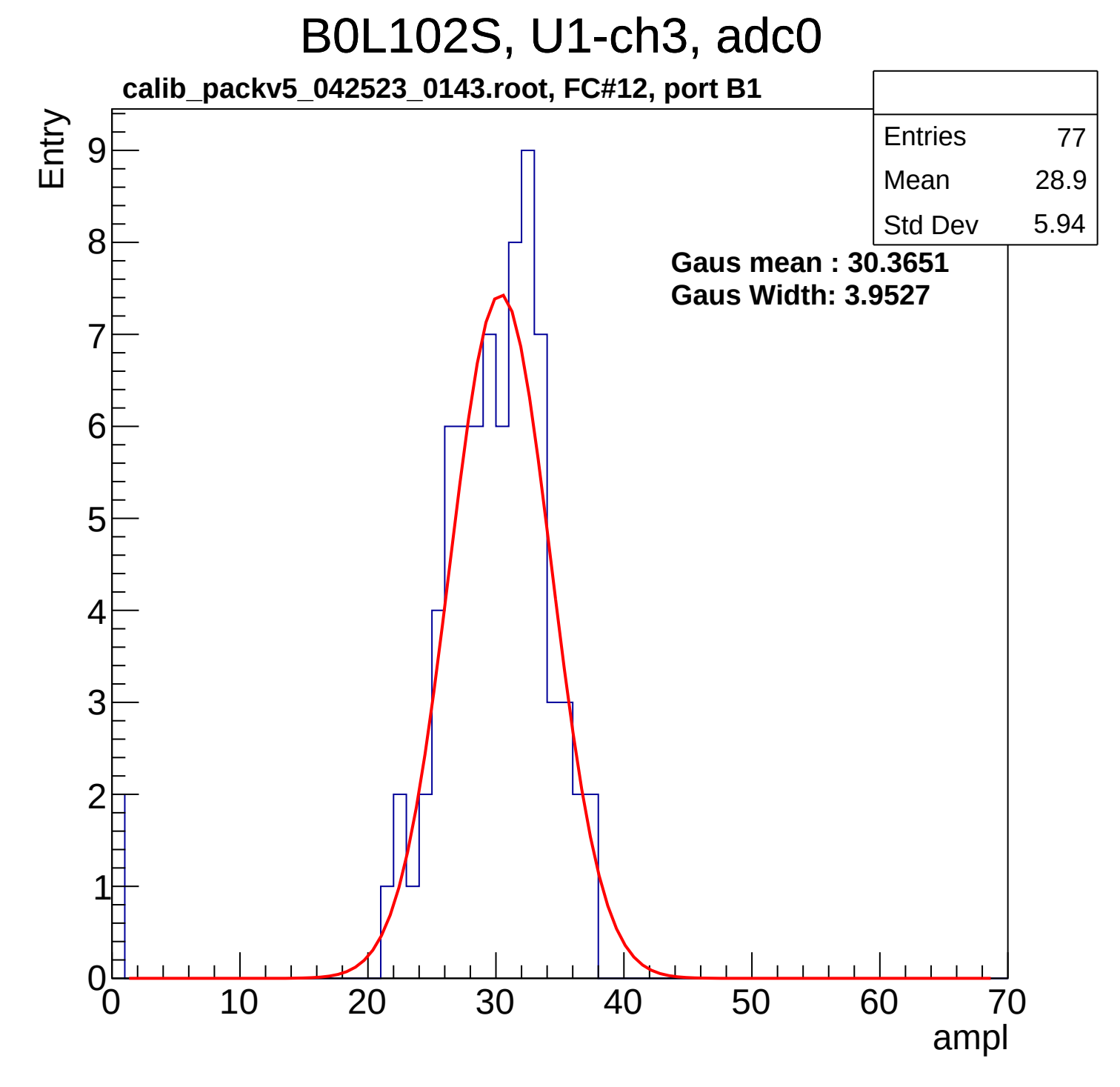
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch3, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 28.9
Std Dev 5.94
Gaus mean : 30.3651
Gaus Width: 3.9527
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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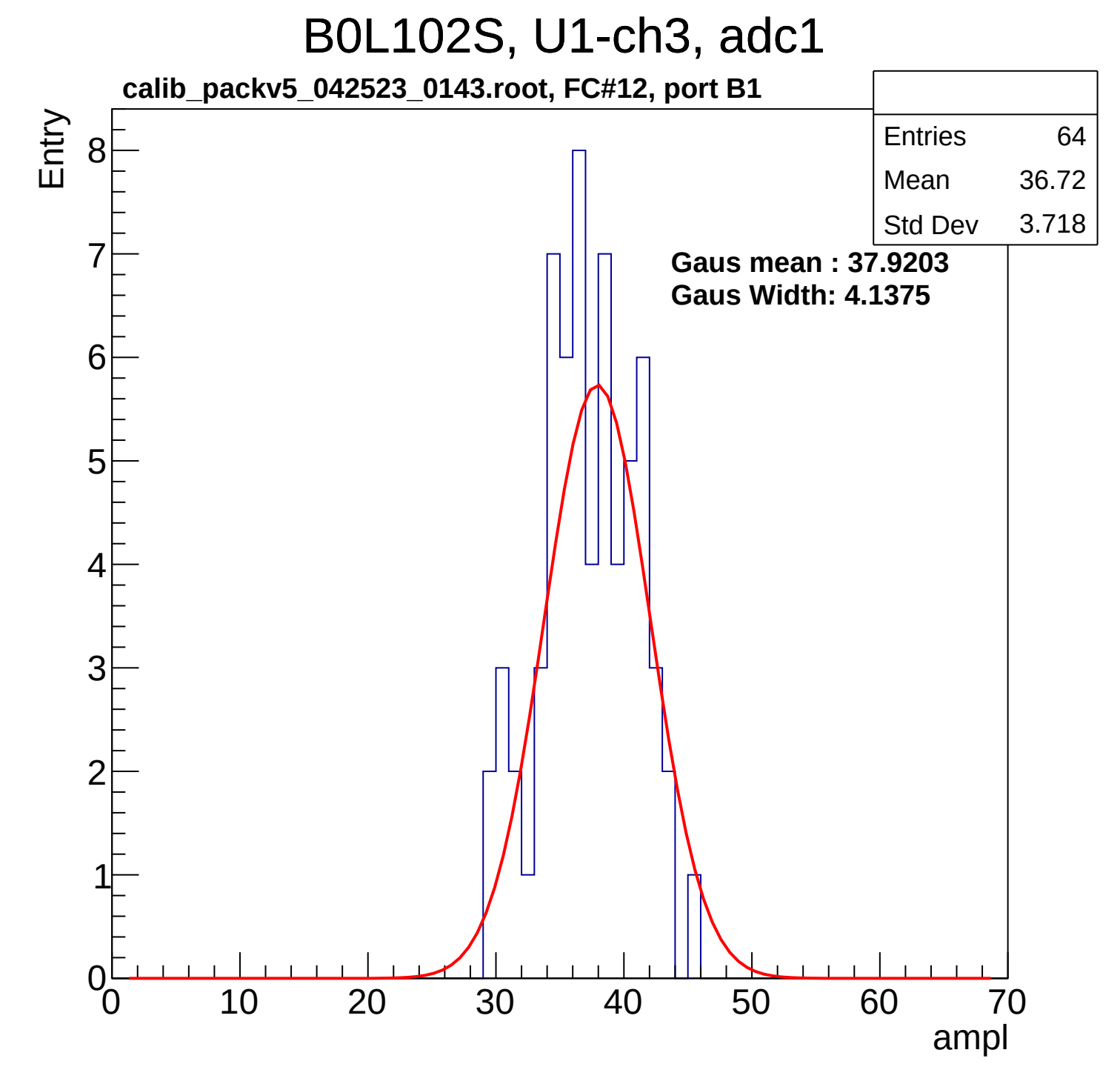
B0L102S, U1-ch3, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.72
Std Dev 3.718
Gaus mean : 37.9203
Gaus Width: 4.1375
Entry
ampl
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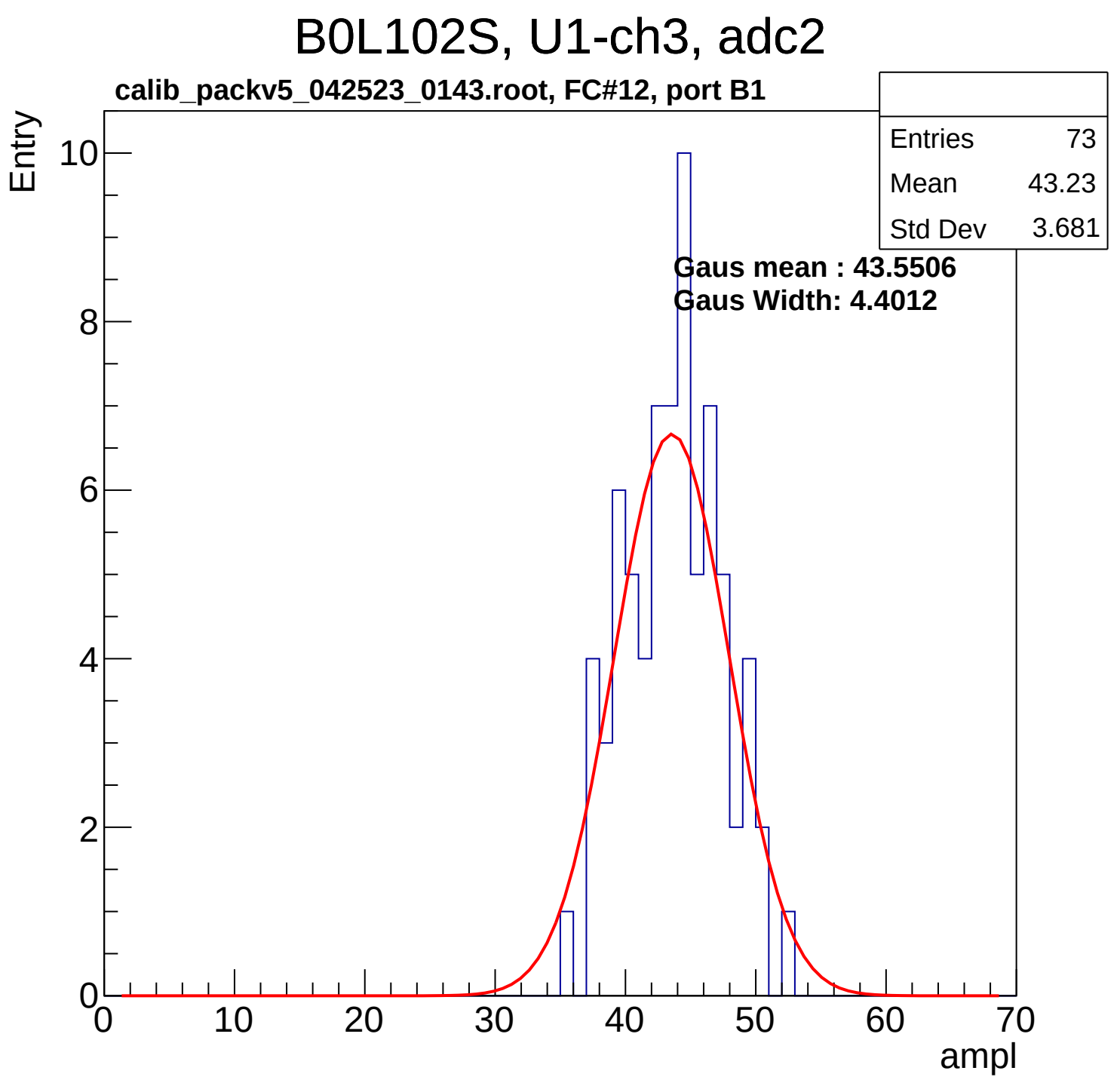
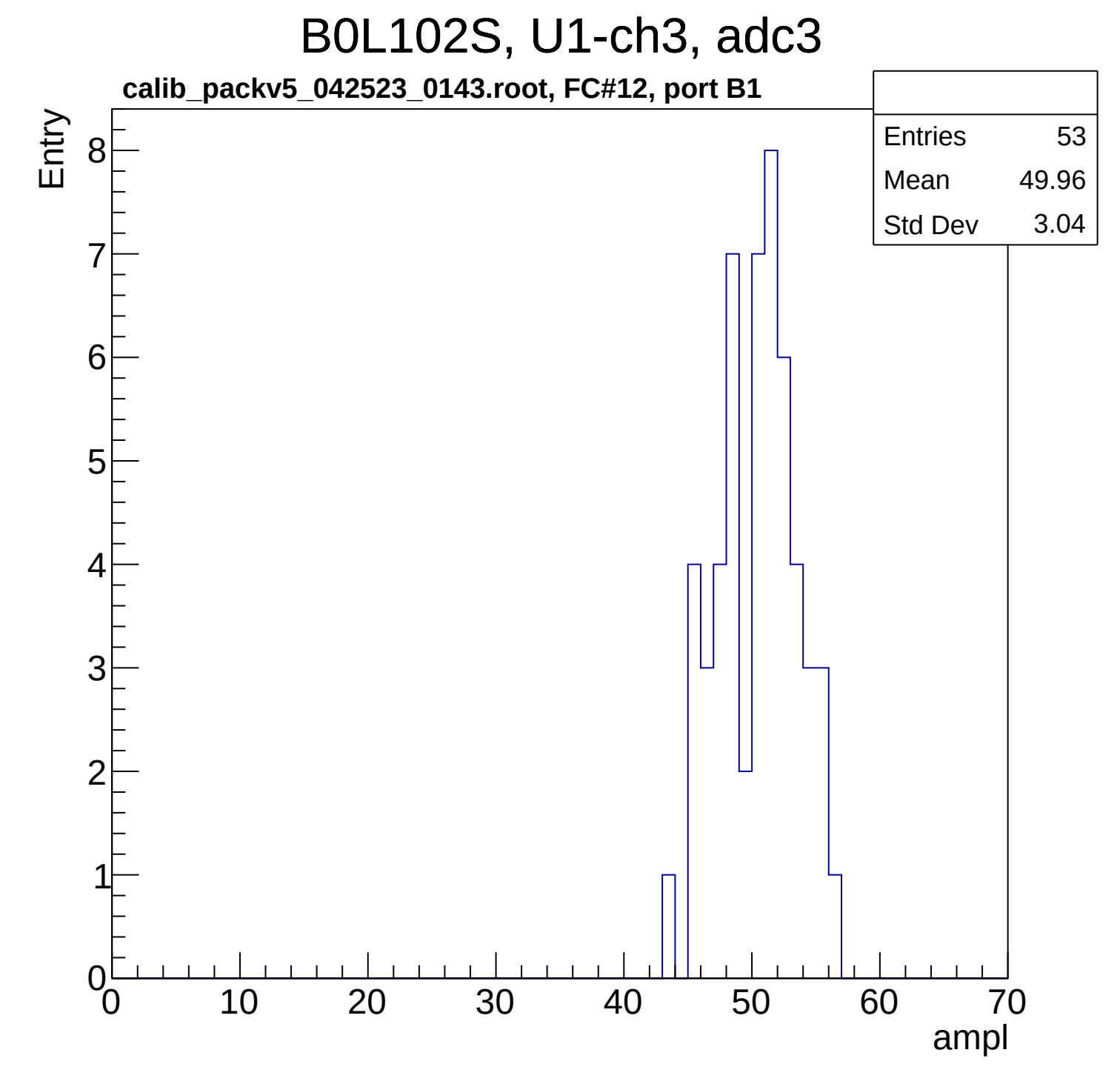
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch3, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 43.23
Std Dev 3.681
Gaus mean : 43.5506
Gaus Width: 4.4012
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch3, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 49.96
Std Dev 3.04
Entry
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4
5
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7
8
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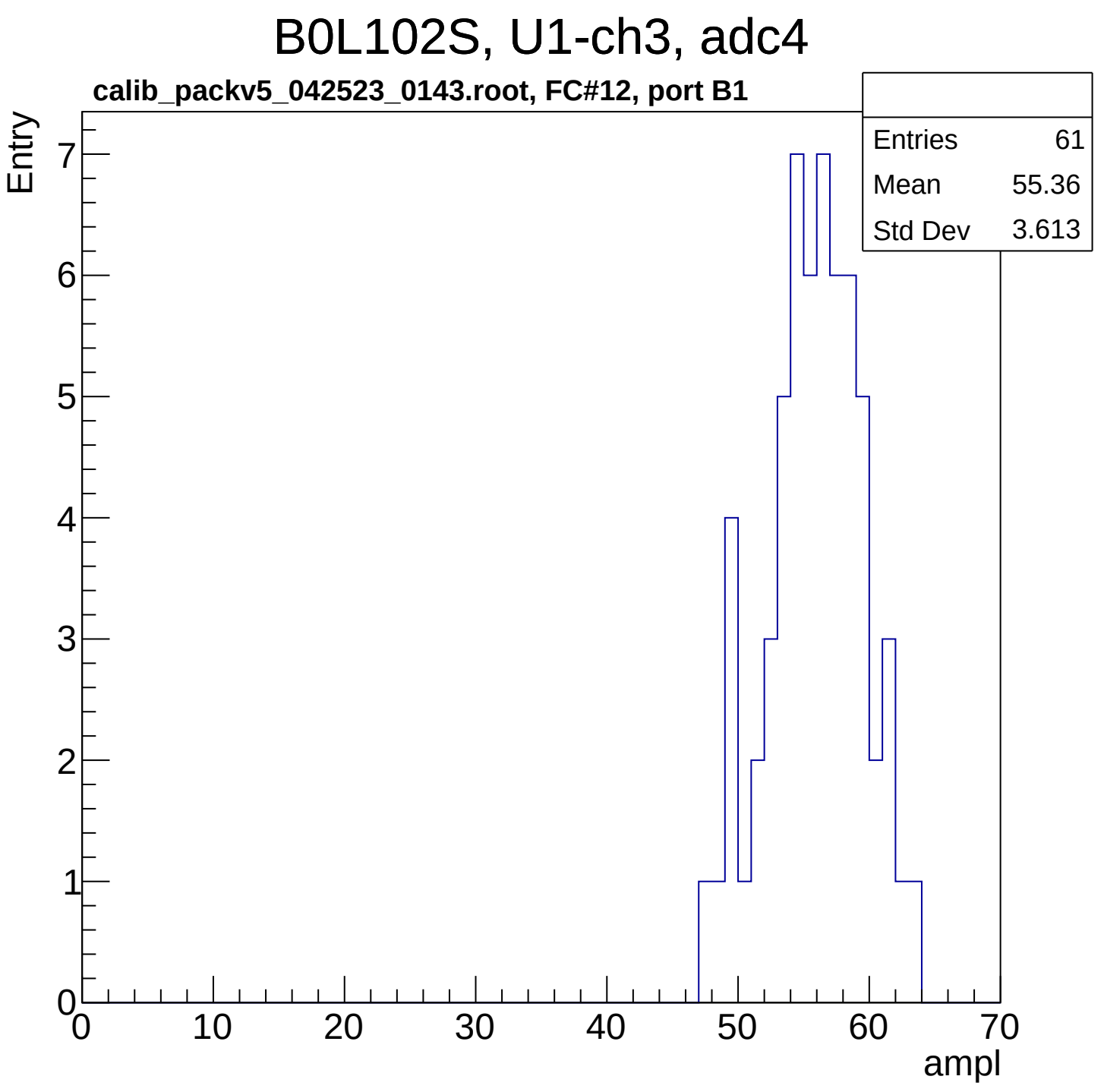
70
ampl


B0L102S, U1-ch3, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.36
Std Dev 3.613
Entry
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0
10
20
30
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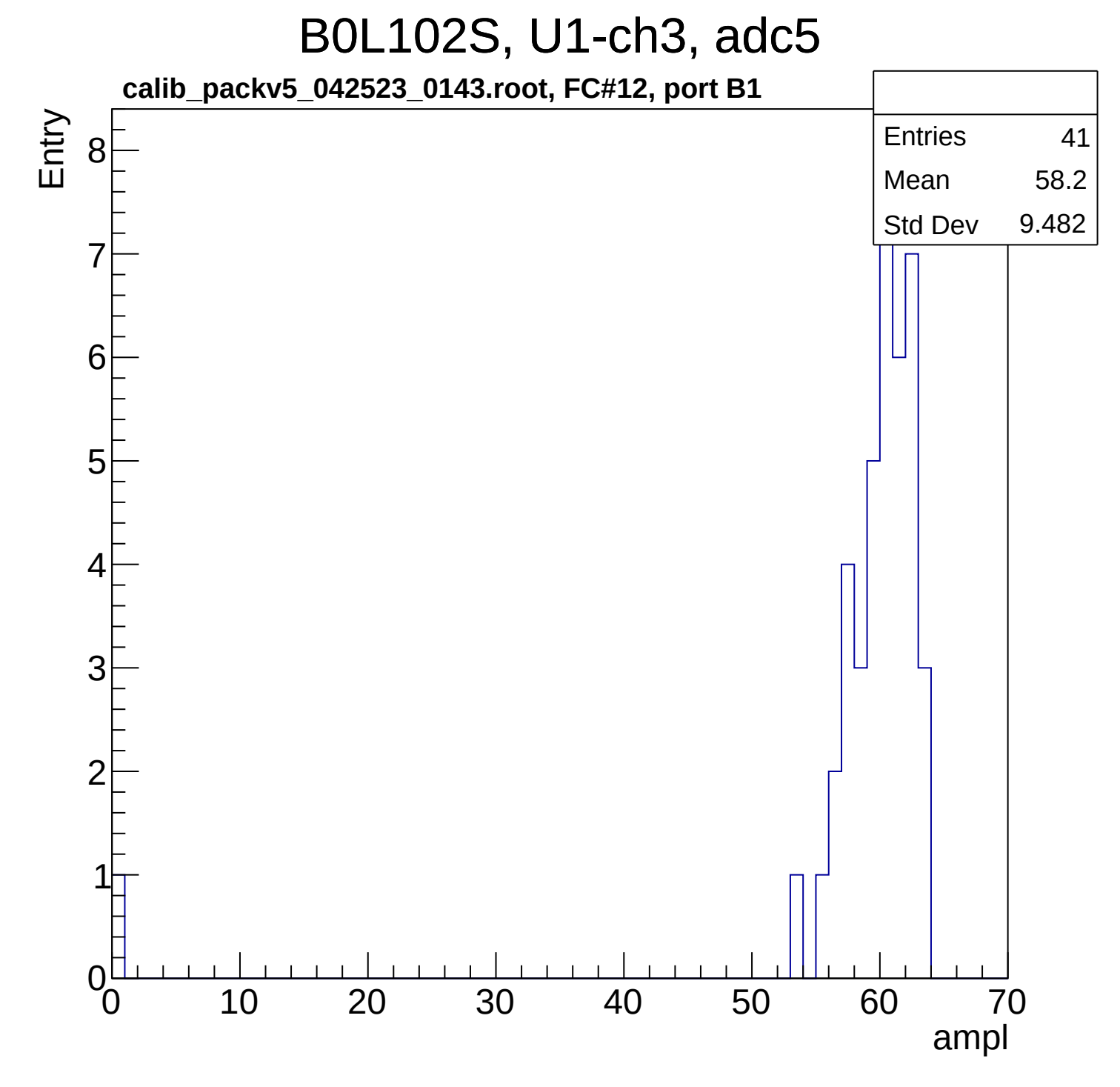
50
60
70
ampl


B0L102S, U1-ch3, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.2
Std Dev 9.482
Entry
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5
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7
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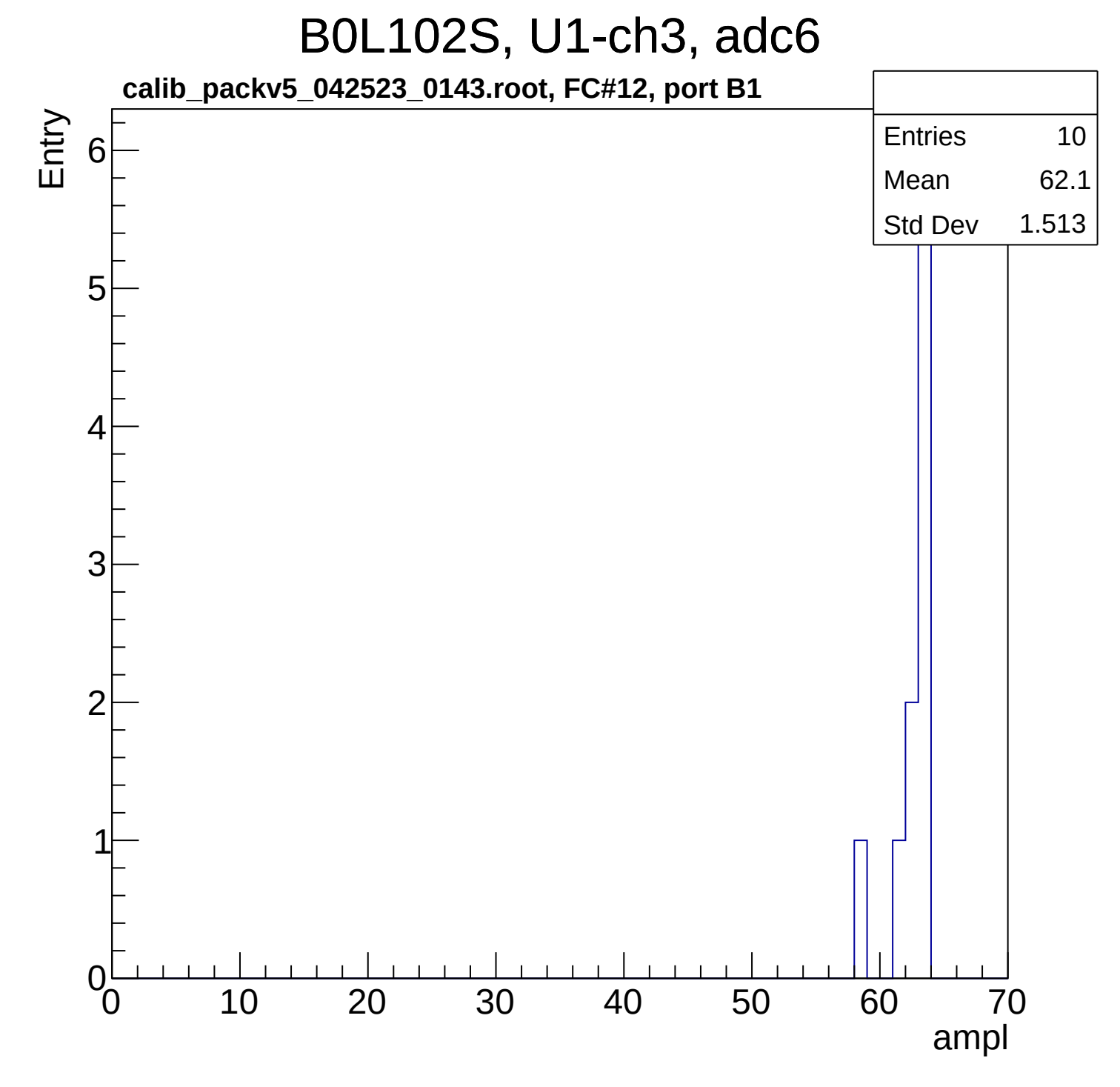
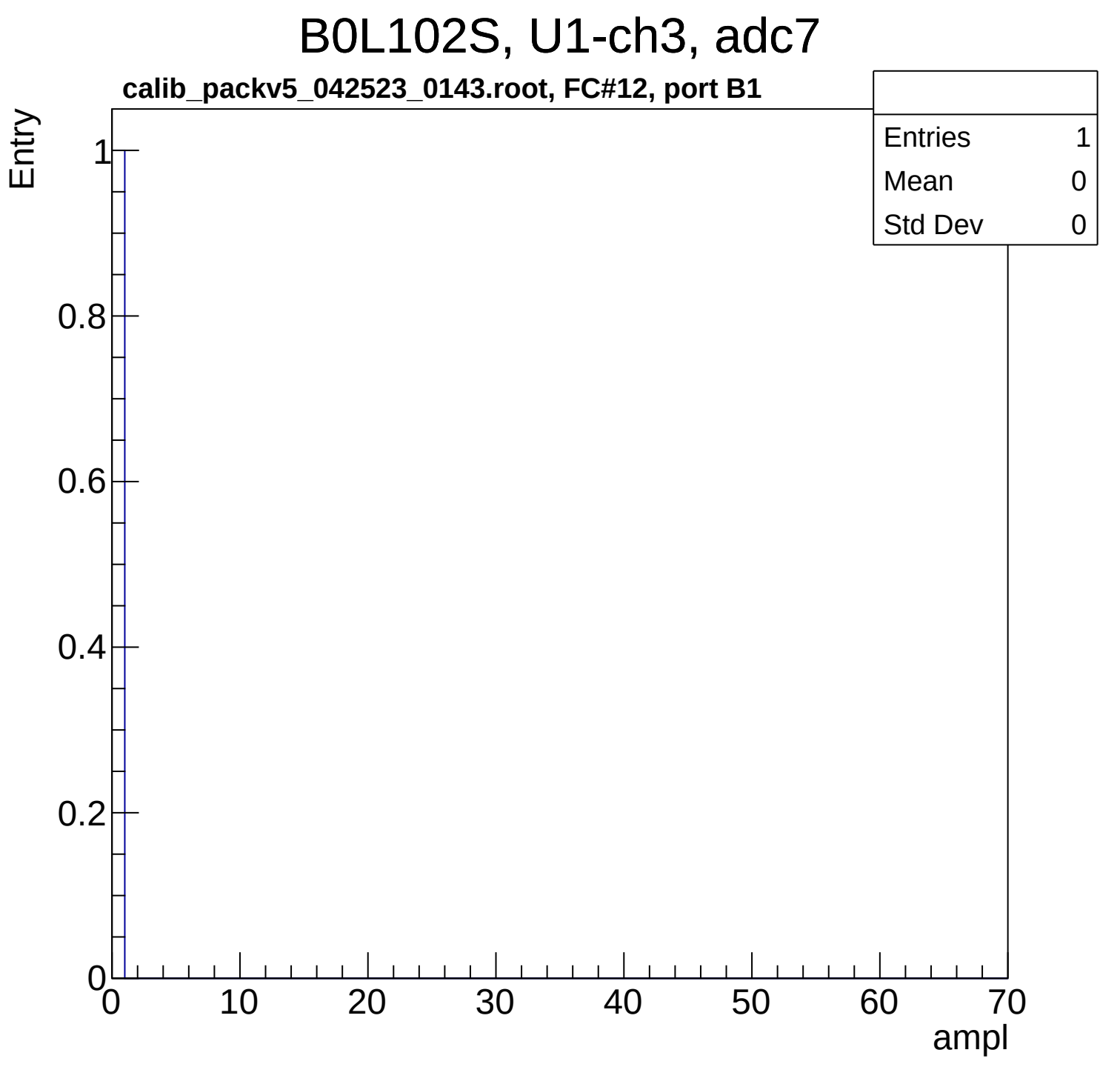
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch3, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 62.1
Std Dev 1.513
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch3, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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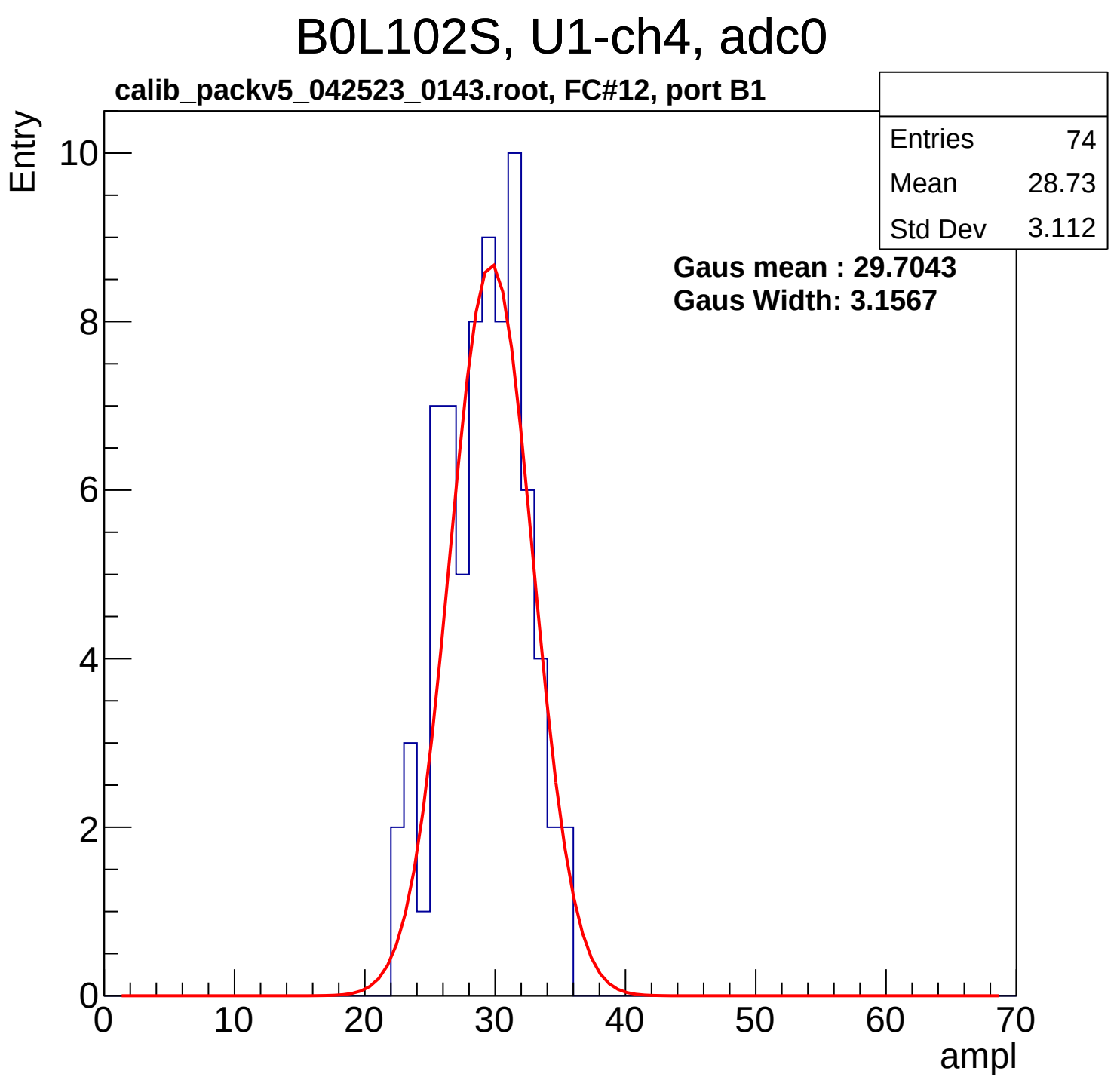
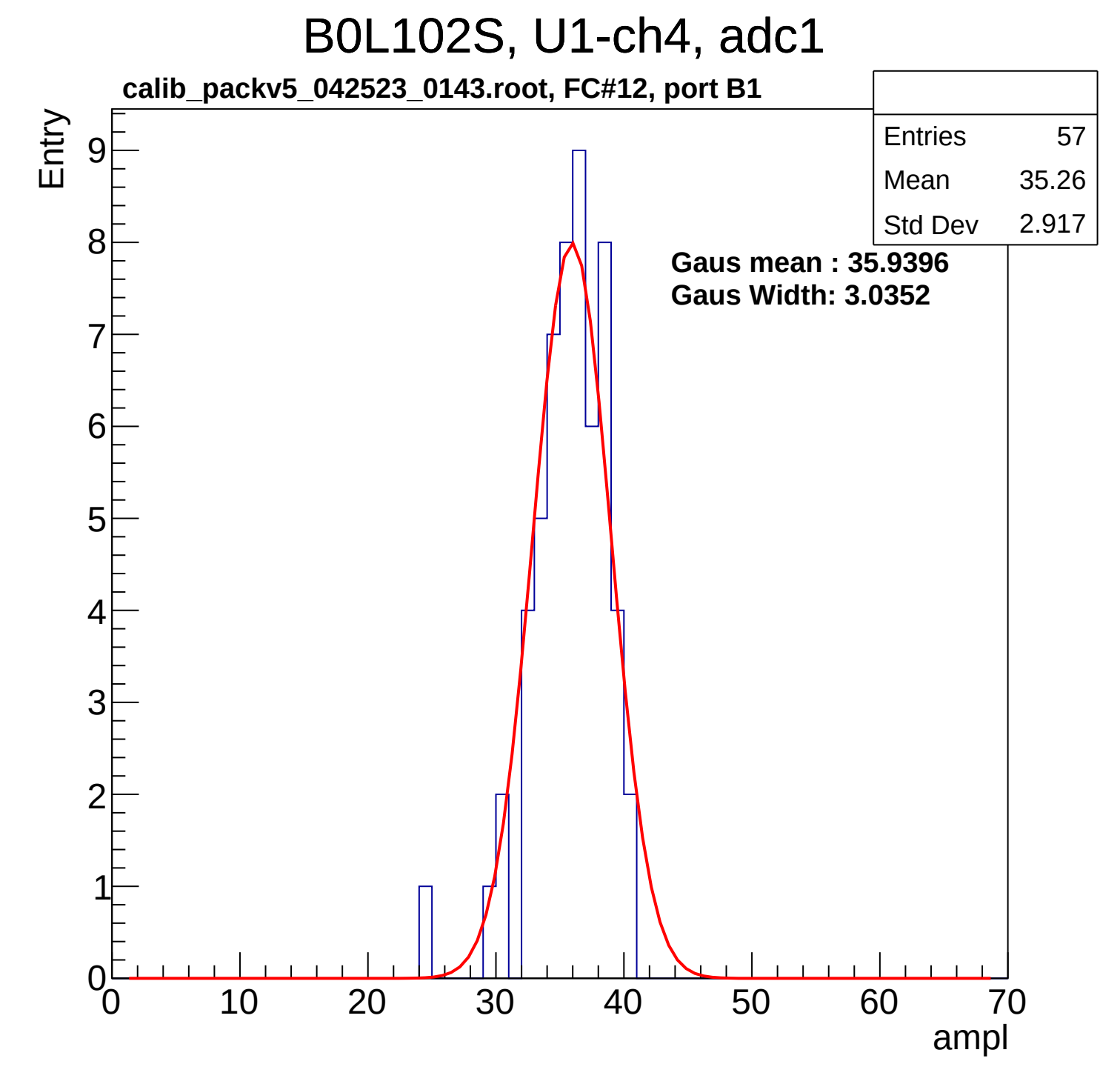
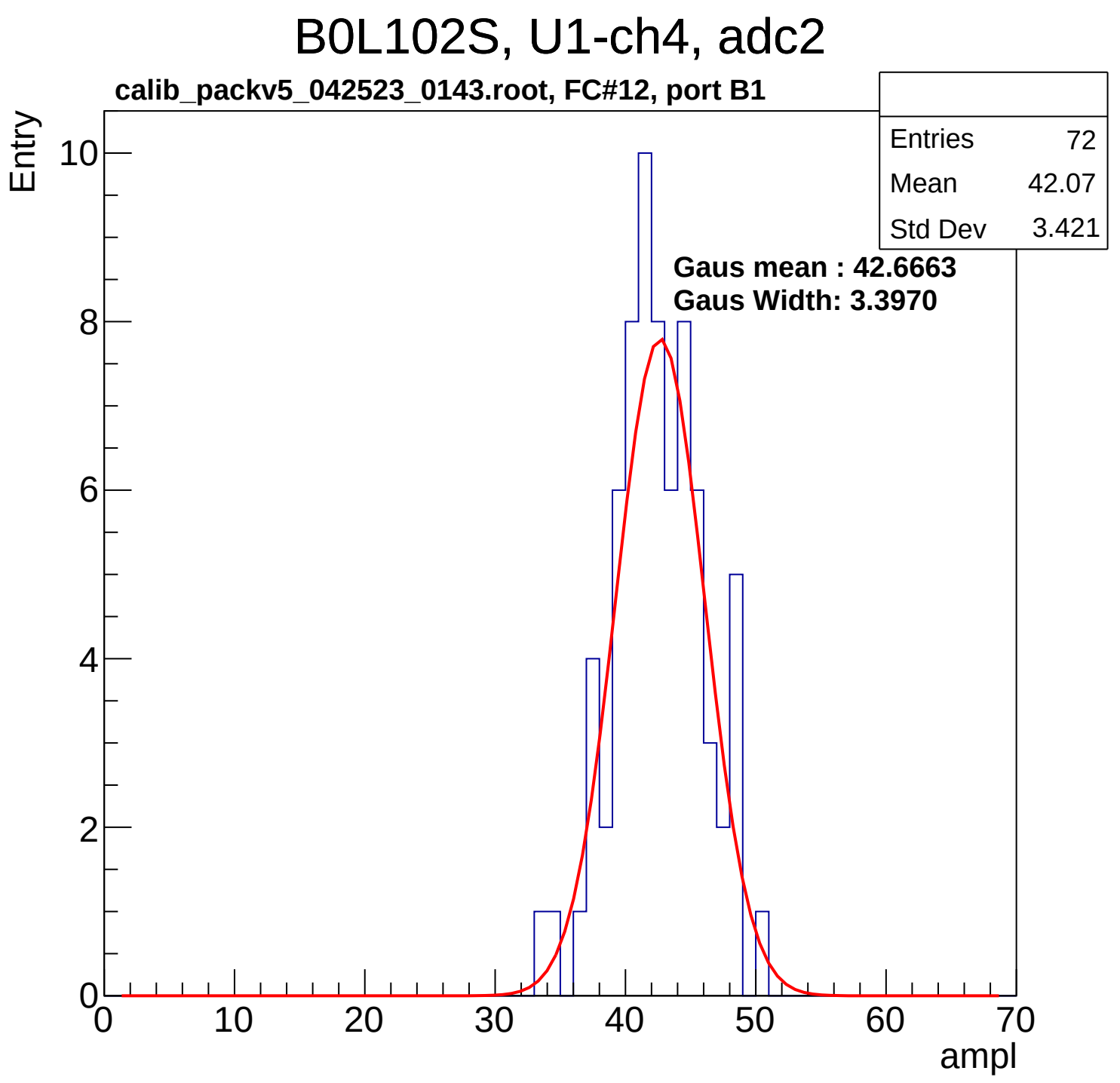
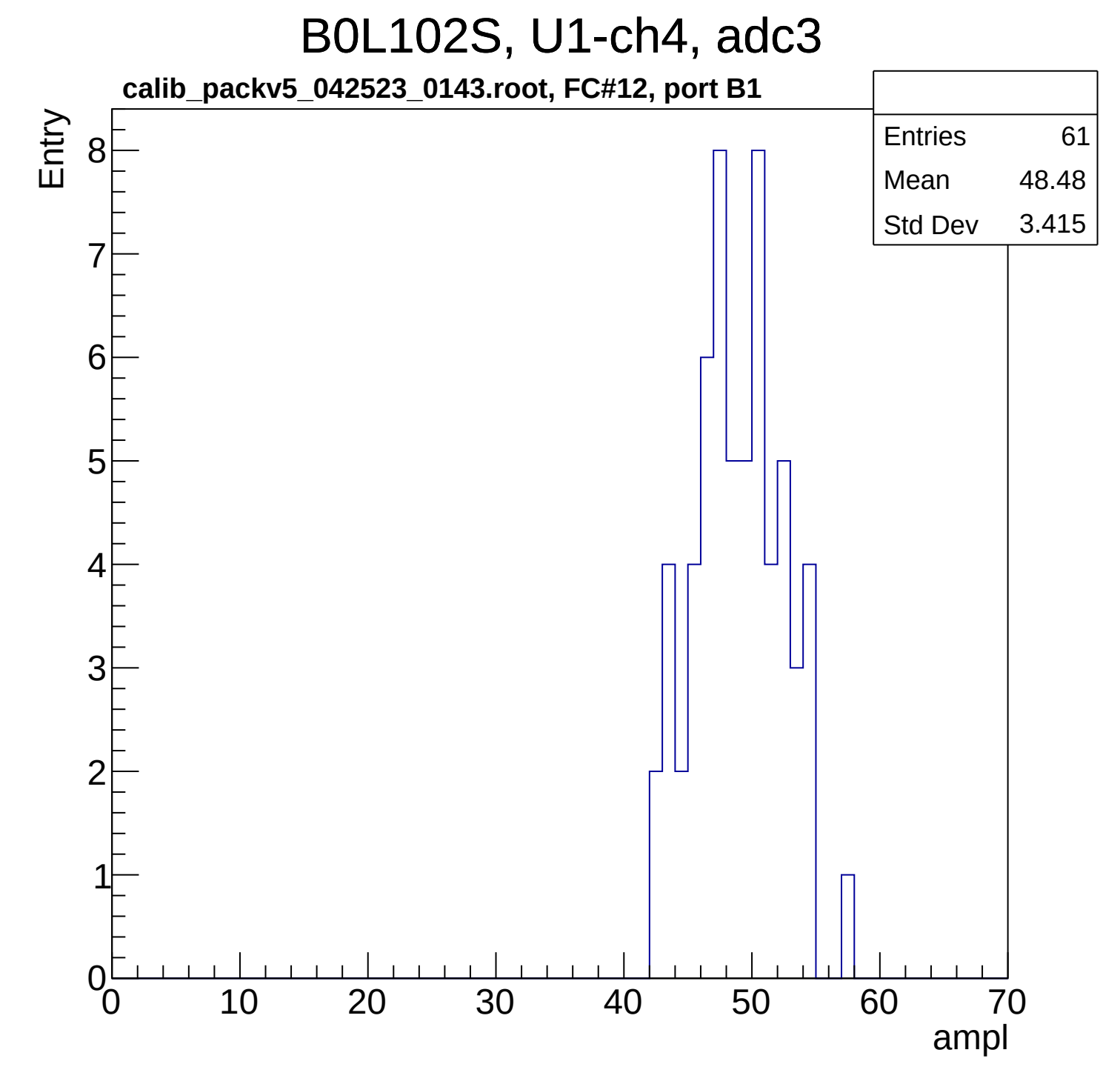
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch4, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 28.73
Std Dev 3.112
Gaus mean : 29.7043
Gaus Width: 3.1567
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch4, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 35.26
Std Dev 2.917
Gaus mean : 35.9396
Gaus Width: 3.0352
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch4, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 42.07
Std Dev 3.421
Gaus mean : 42.6663
Gaus Width: 3.3970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch4, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 48.48
Std Dev 3.415
Entry
ampl
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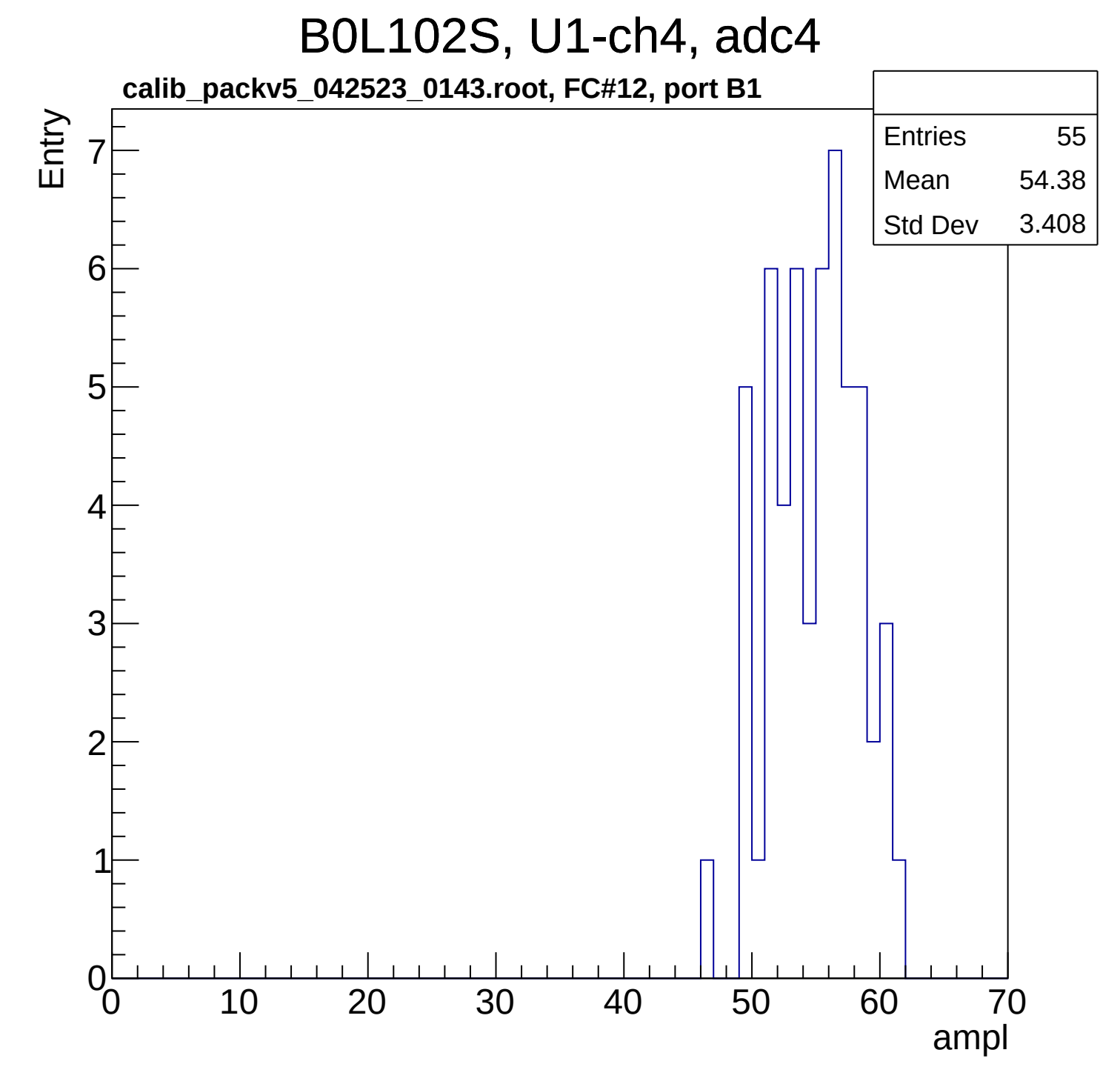
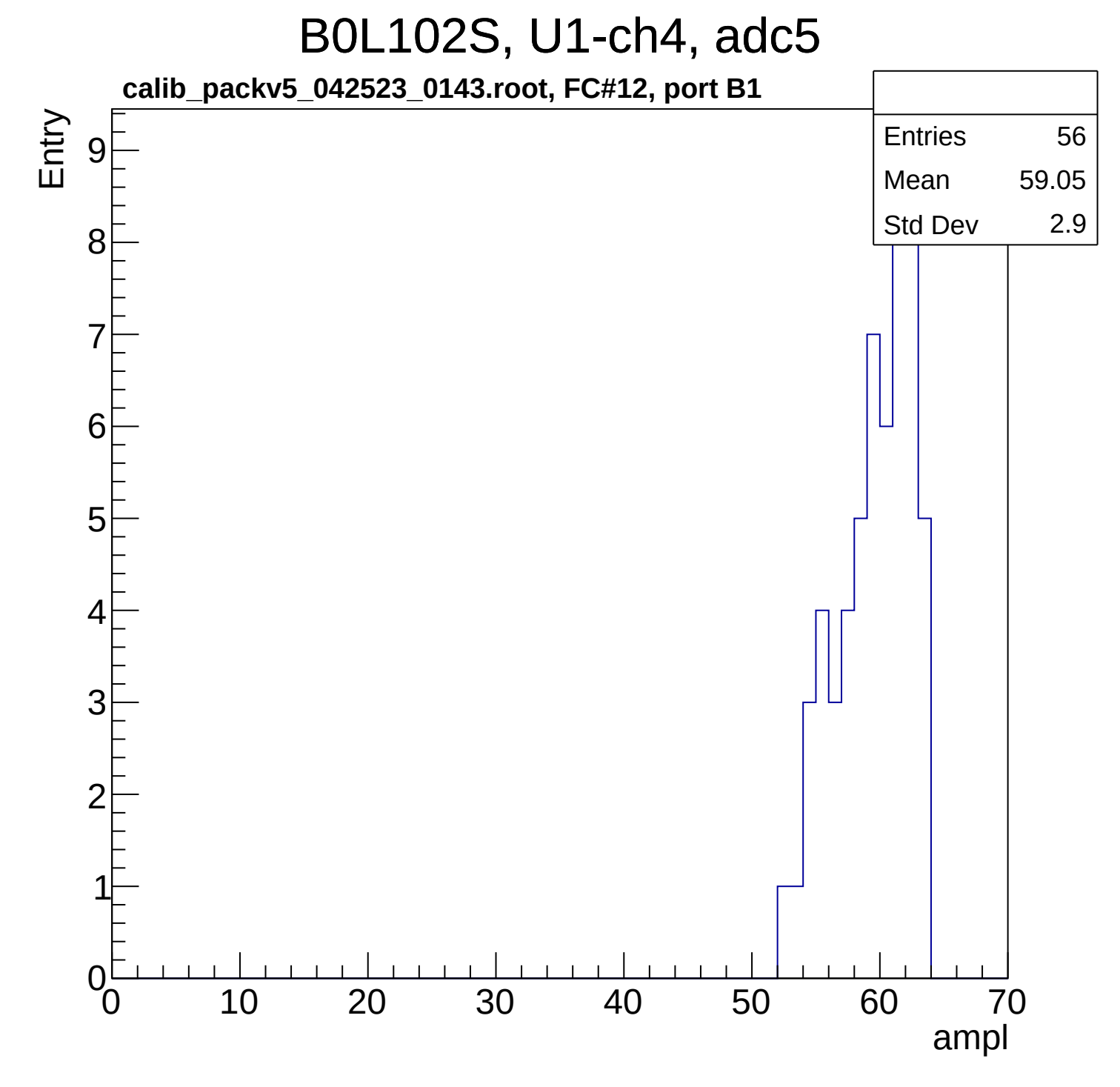
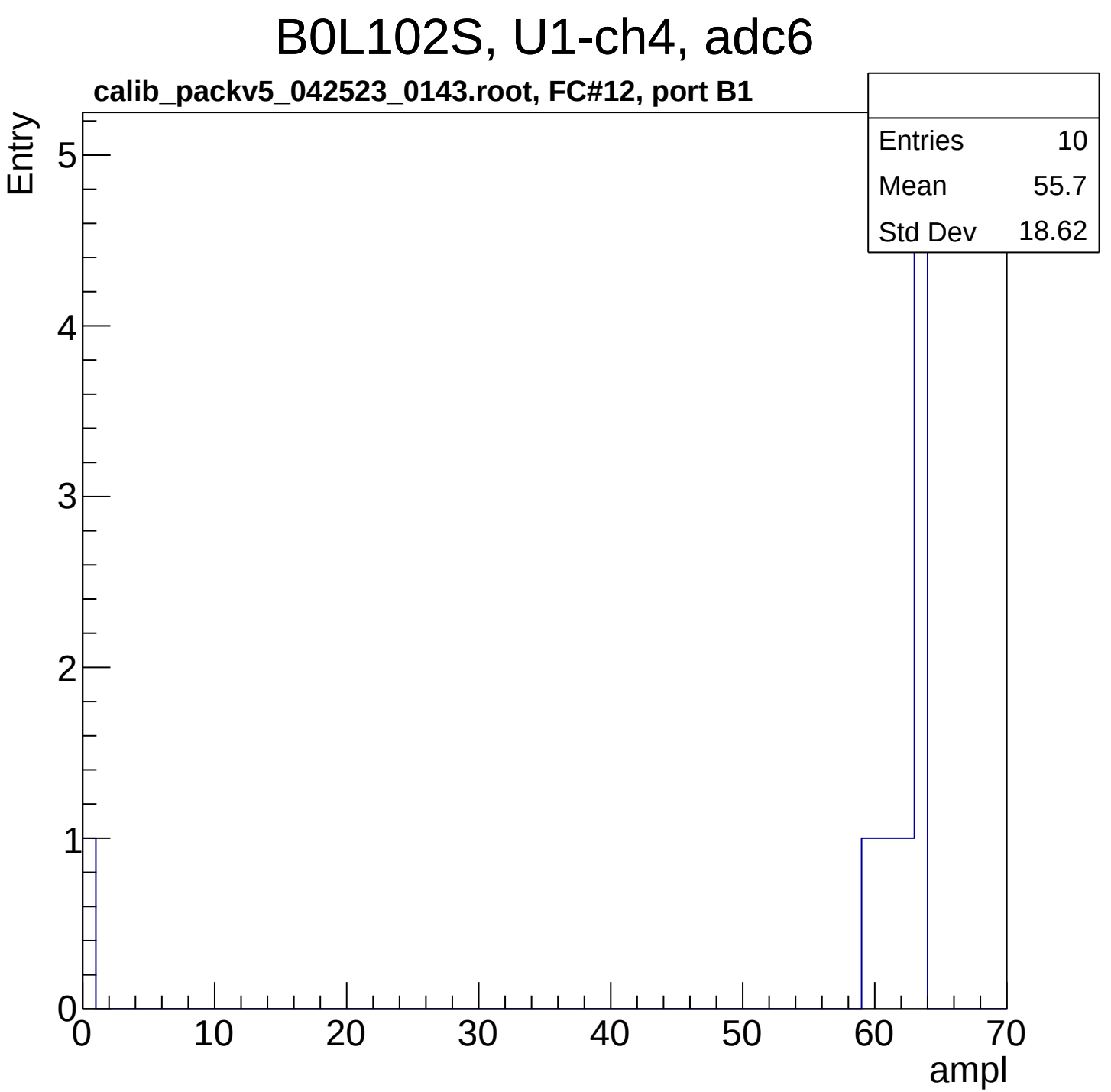
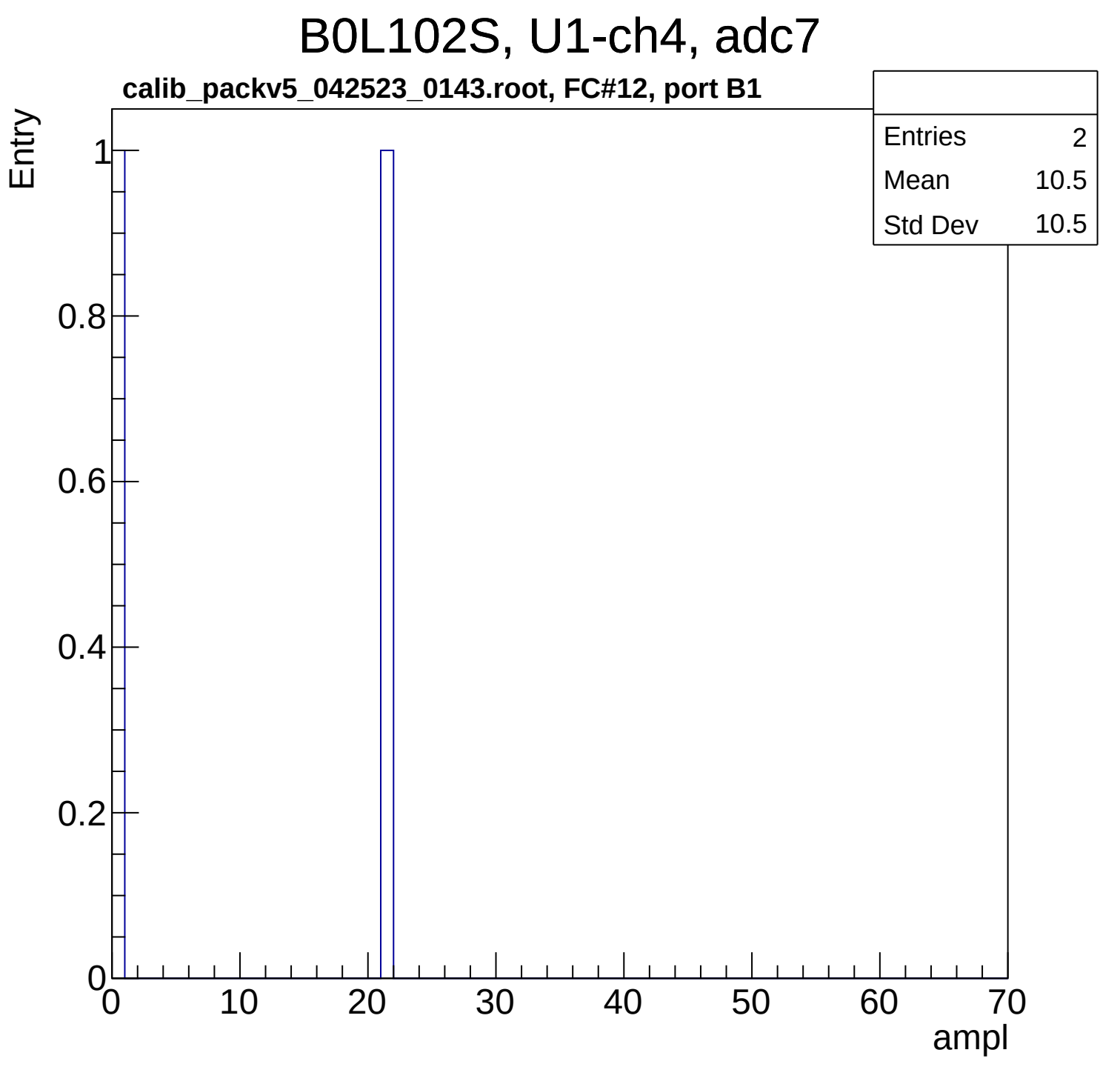
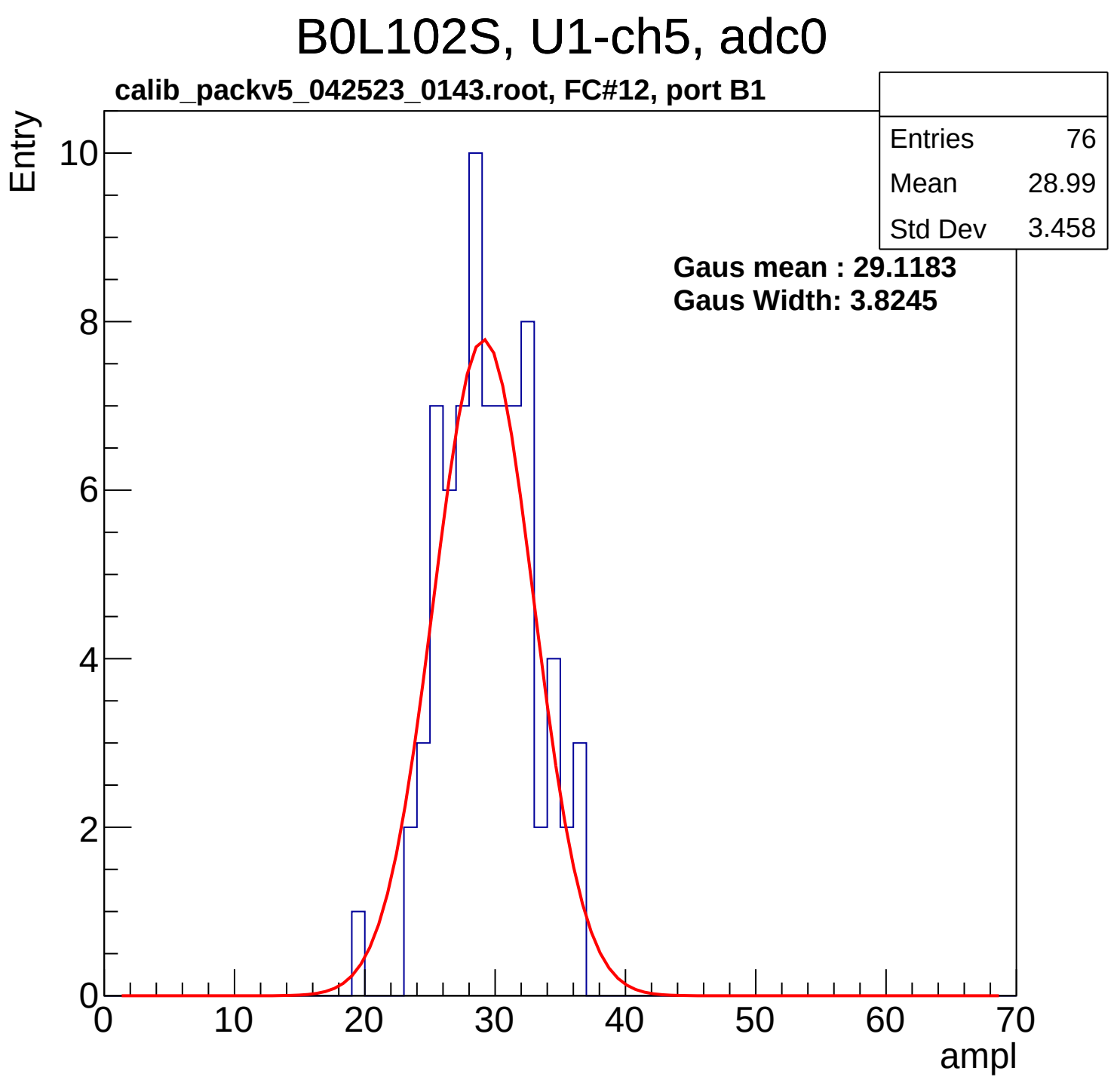
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

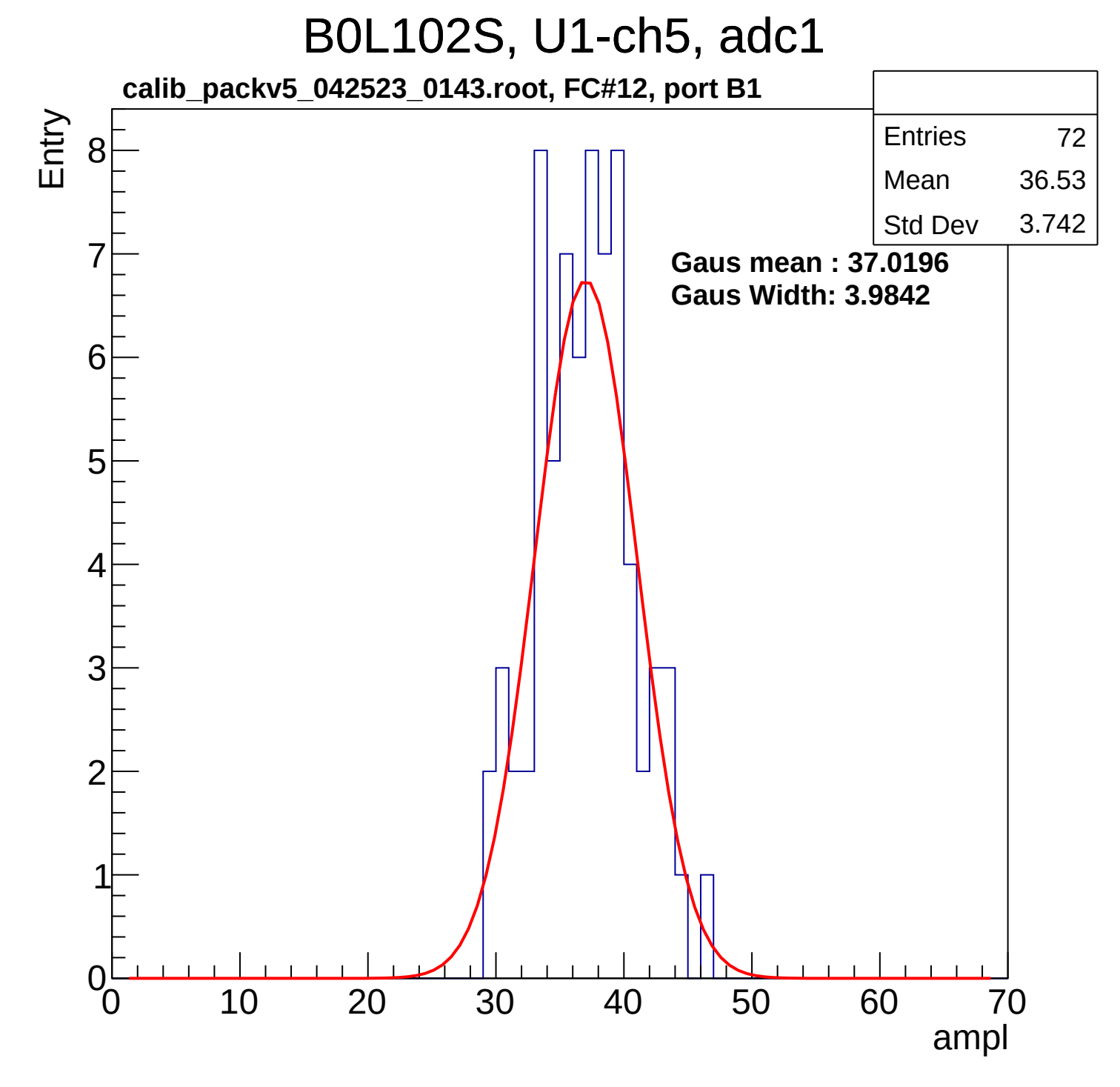

B0L102S, U1-ch4, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 54.38
Std Dev 3.408
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

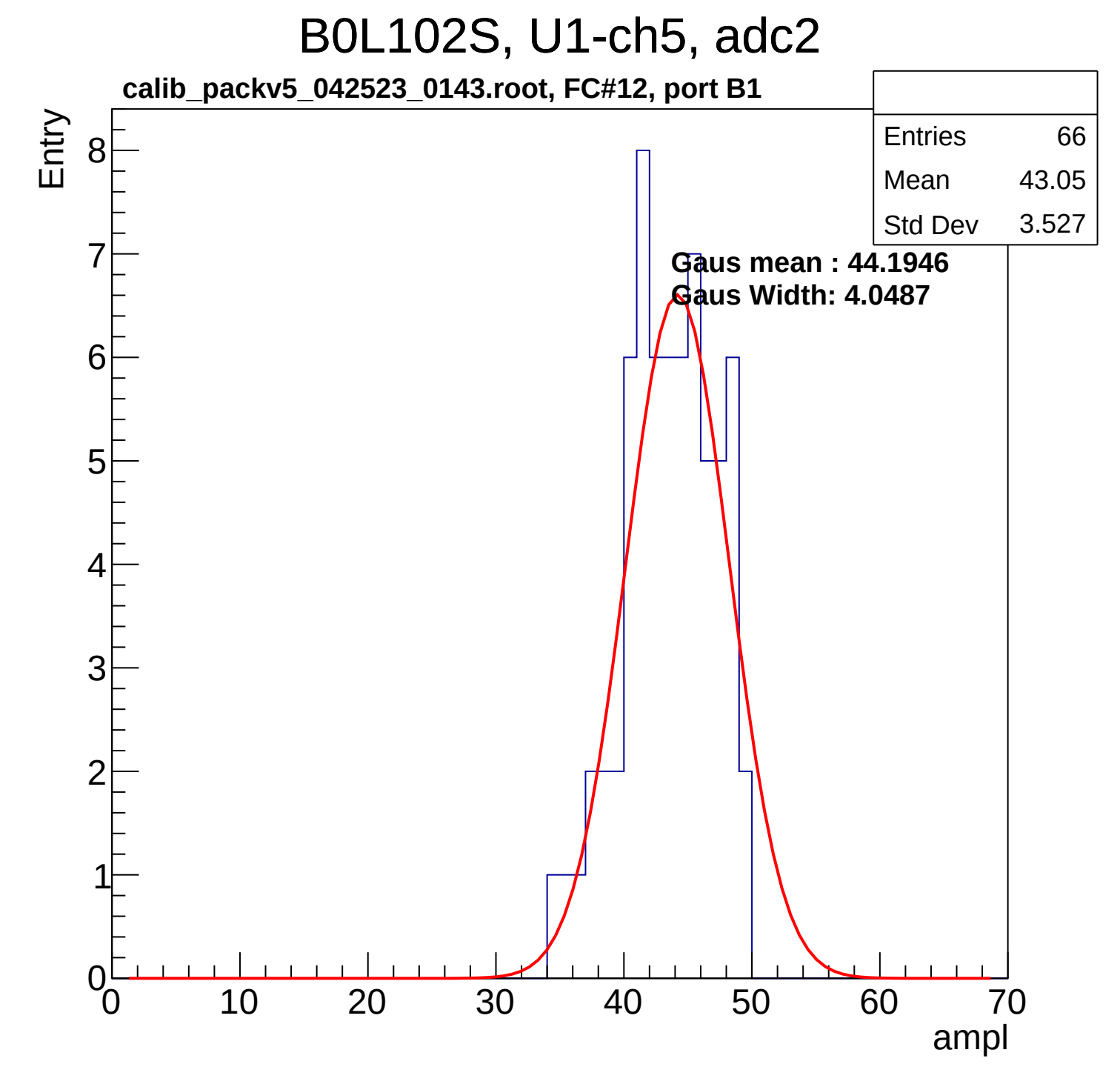

B0L102S, U1-ch4, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 59.05
Std Dev 2.9
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

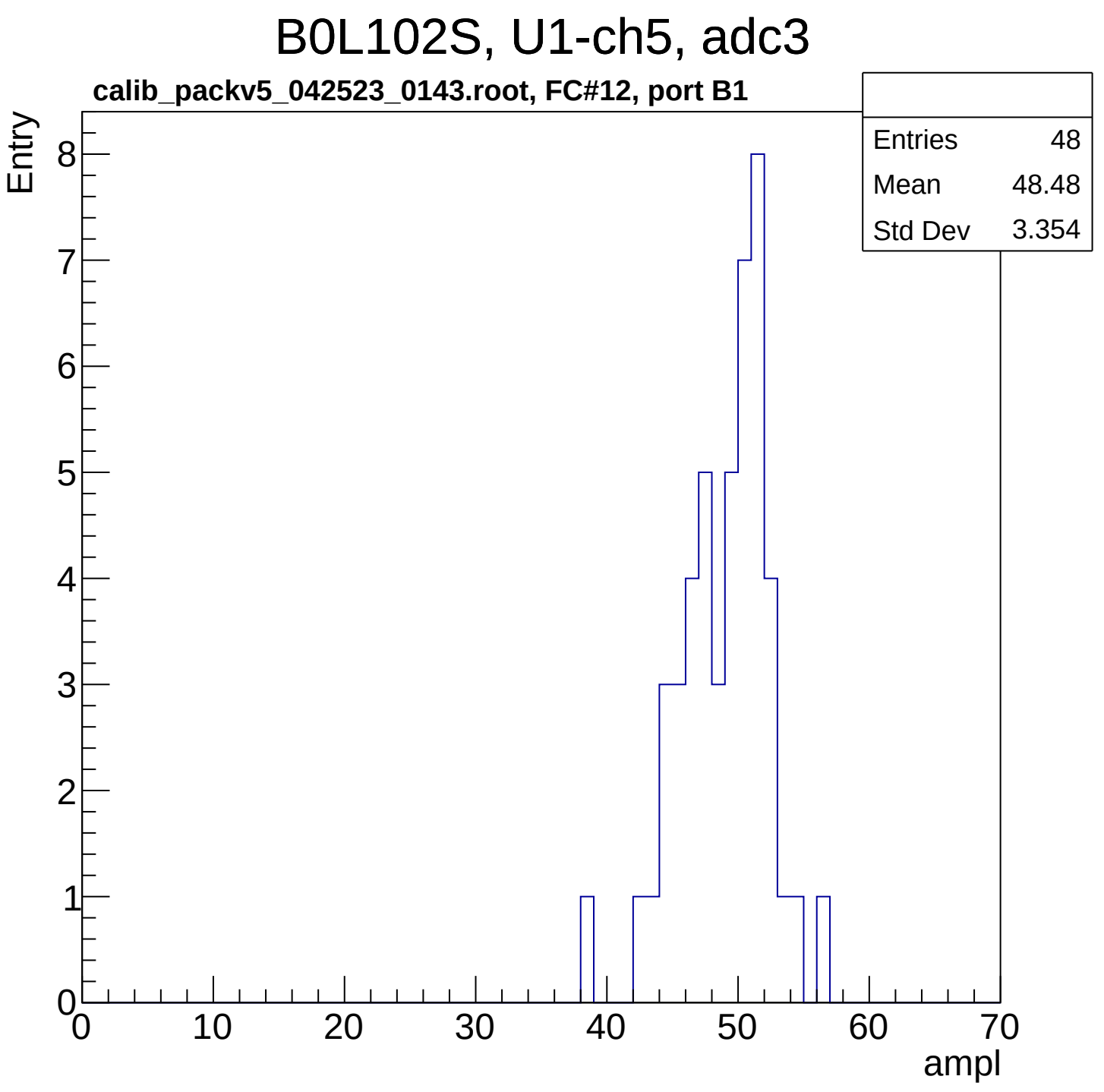

B0L102S, U1-ch4, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 55.7
Std Dev 18.62
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

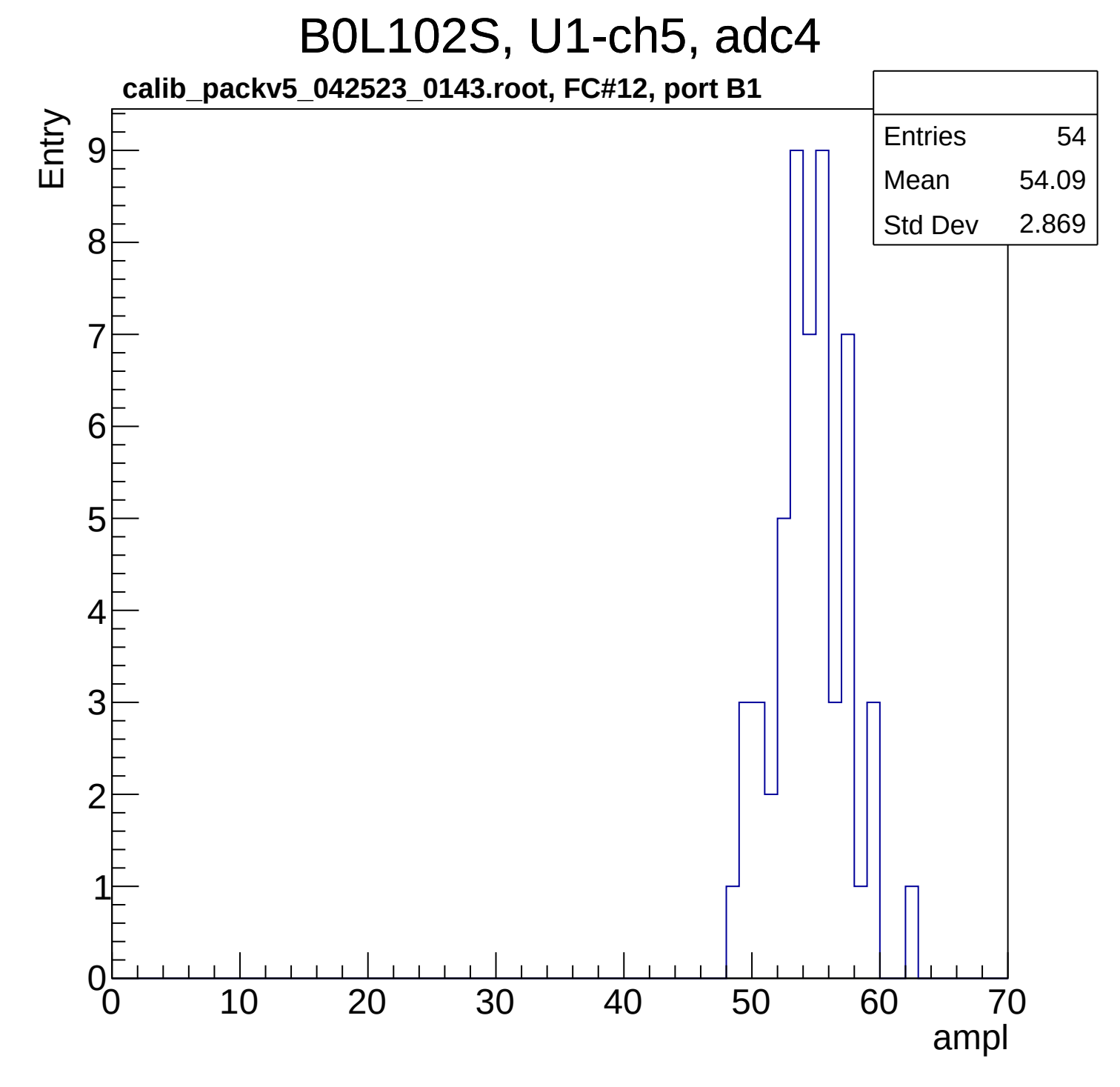

B0L102S, U1-ch4, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

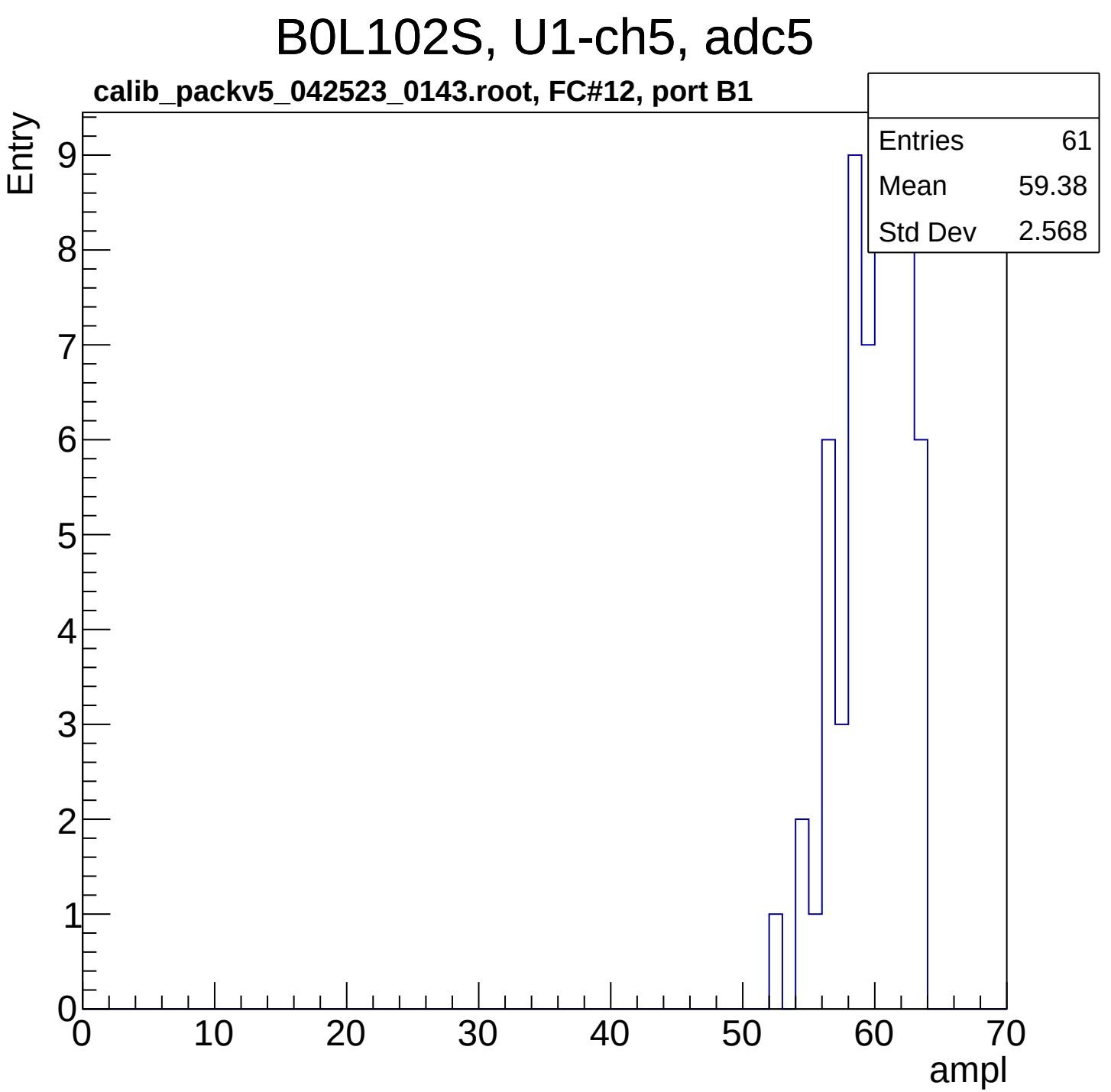

B0L102S, U1-ch5, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 28.99
Std Dev 3.458
Gaus mean : 29.1183
Gaus Width: 3.8245
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch5, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 36.53
Std Dev 3.742
Gaus mean : 37.0196
Gaus Width: 3.9842
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch5, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.05
Std Dev 3.527
Gaus mean : 44.1946
Gaus Width: 4.0487
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch5, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 48.48
Std Dev 3.354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch5, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.09
Std Dev 2.869
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch5, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 59.38
Std Dev 2.568
Entry
ampl
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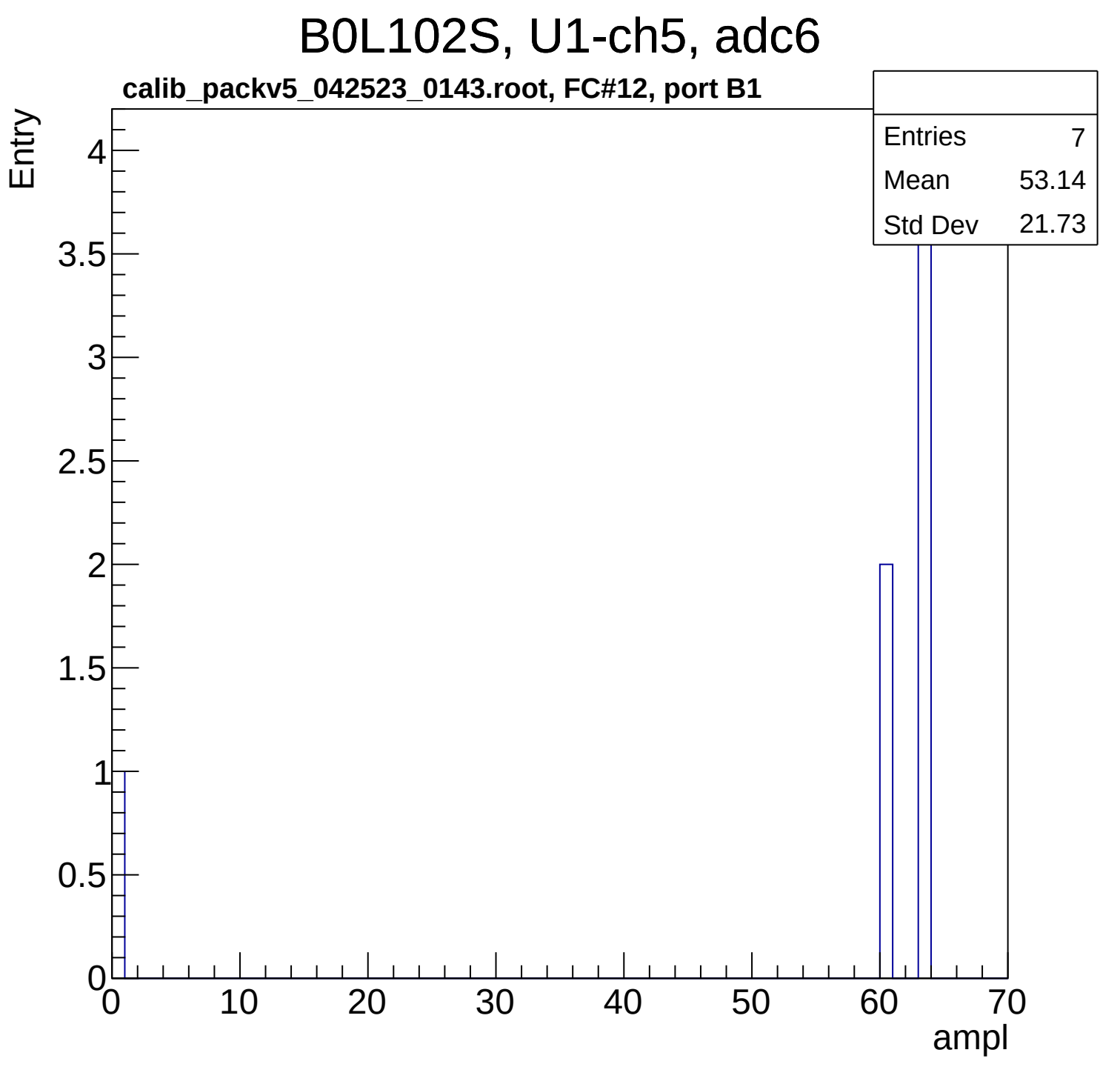
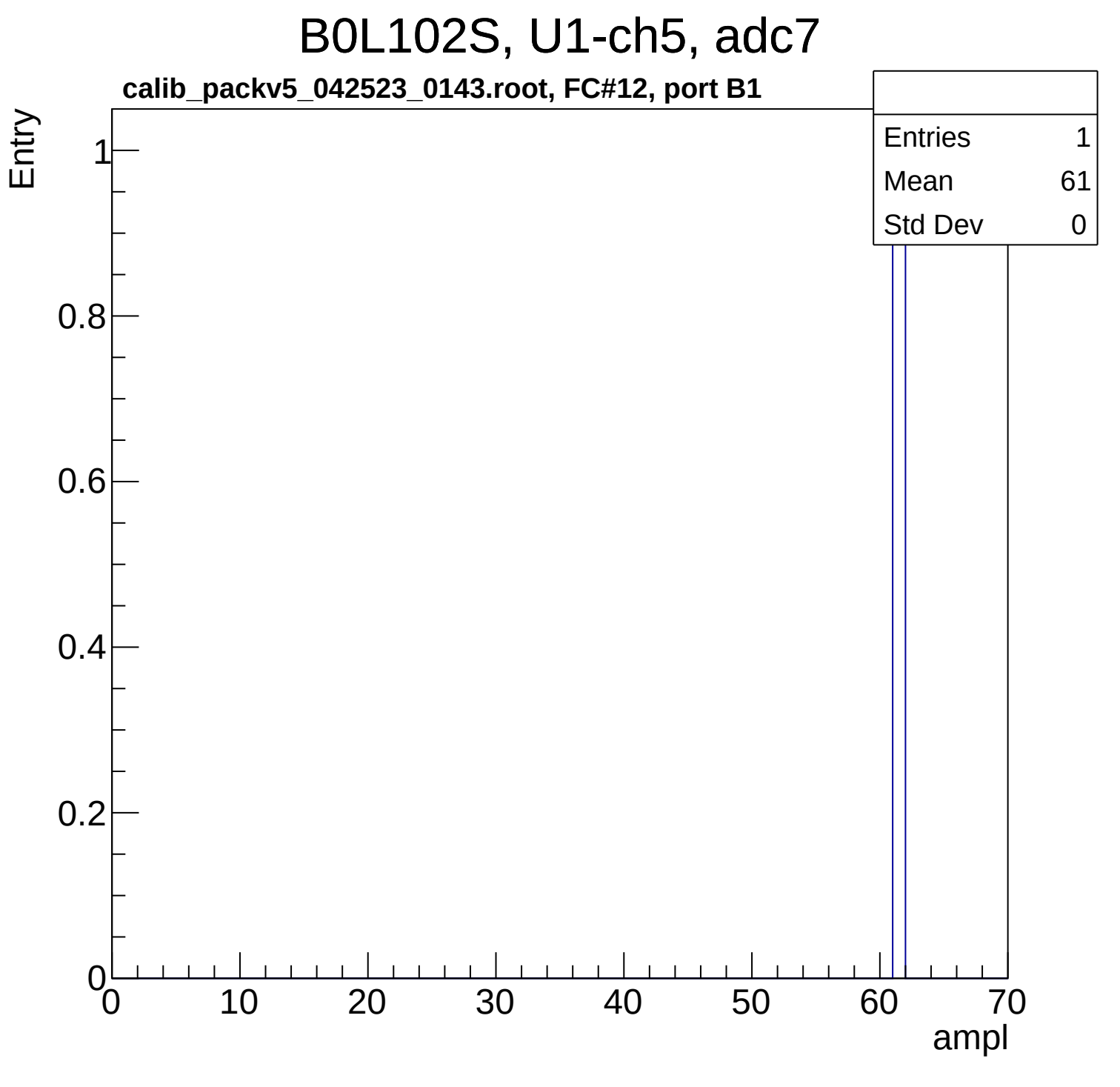
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch5, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.14
Std Dev 21.73
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch5, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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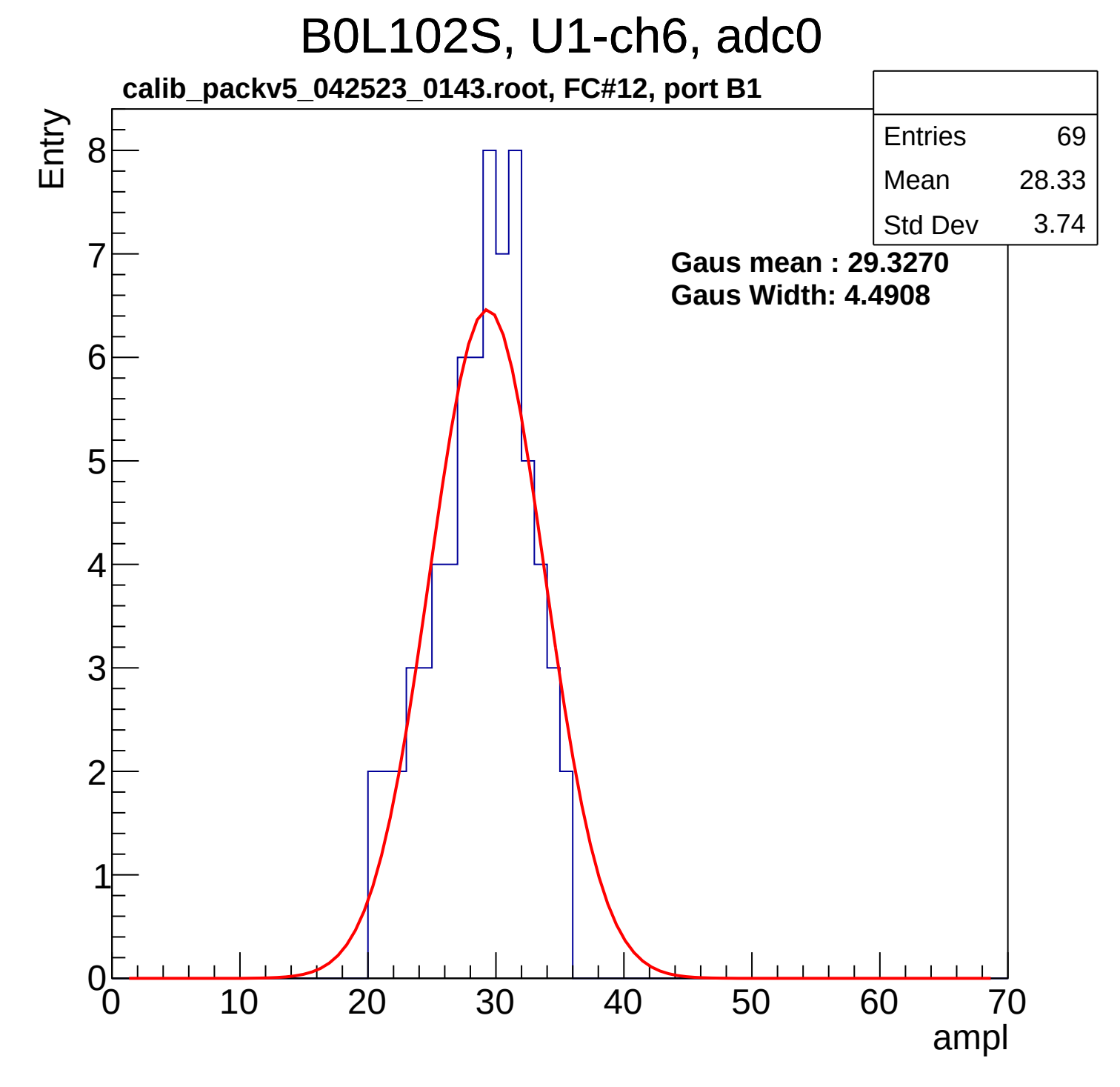
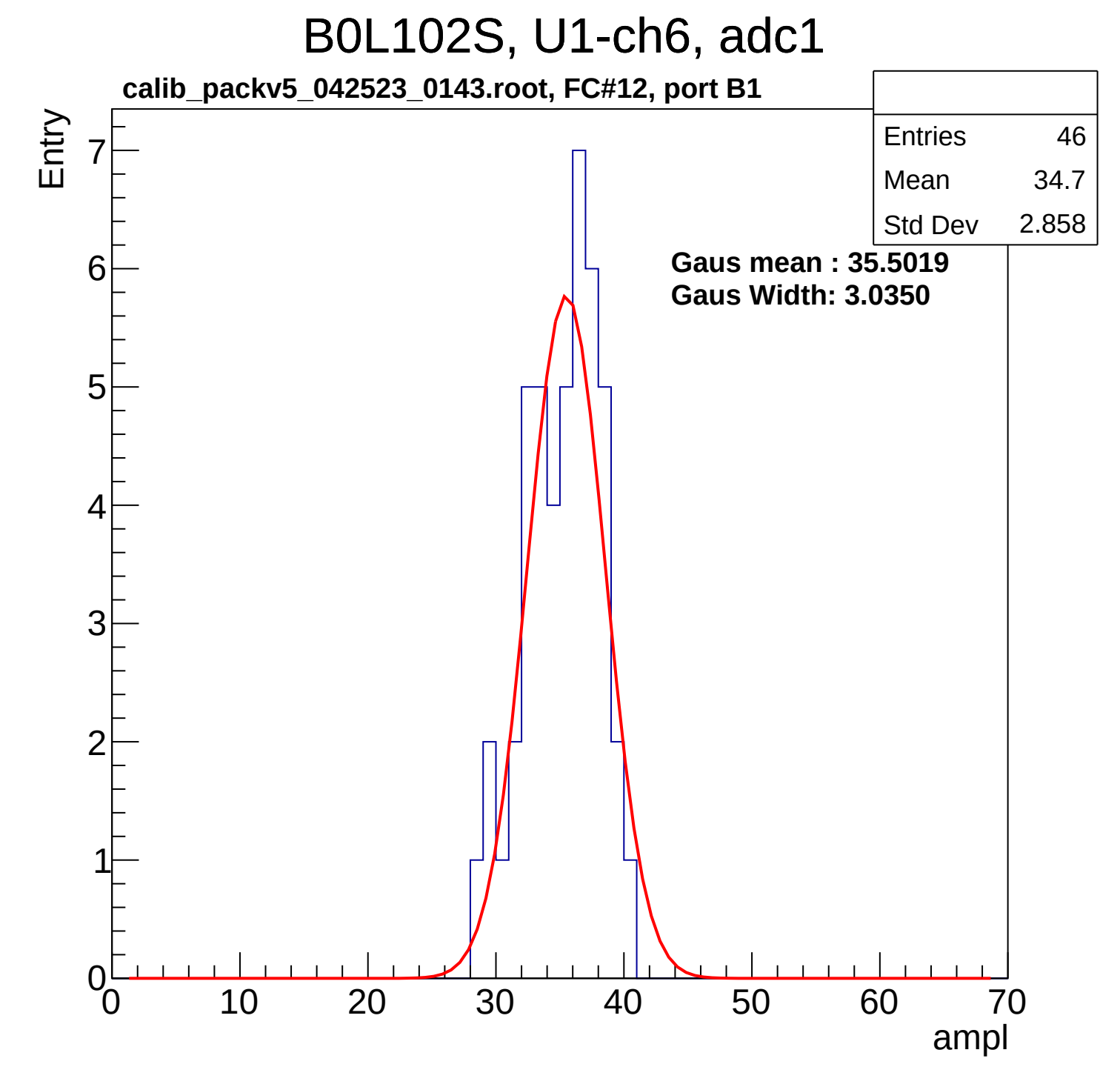
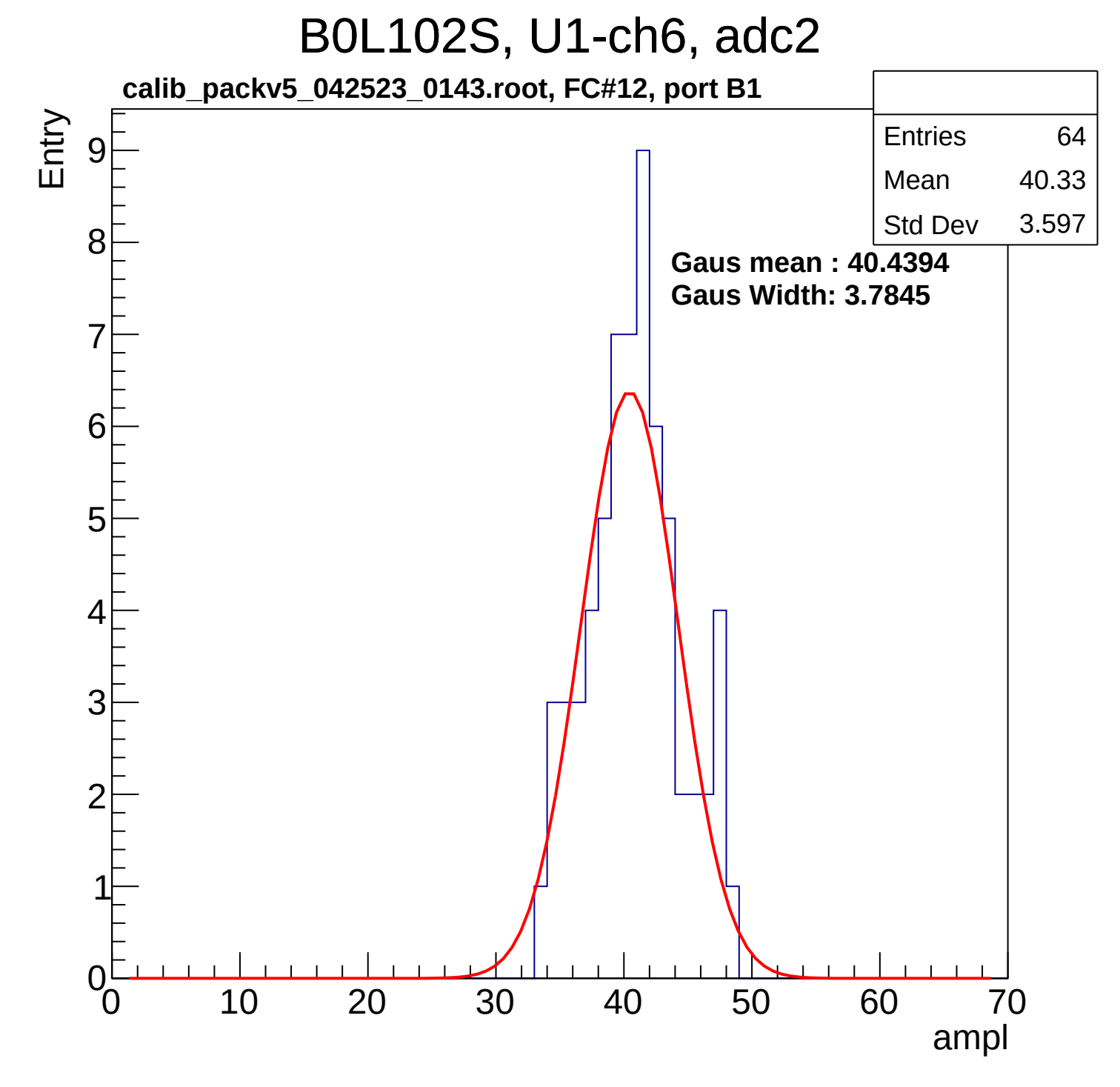
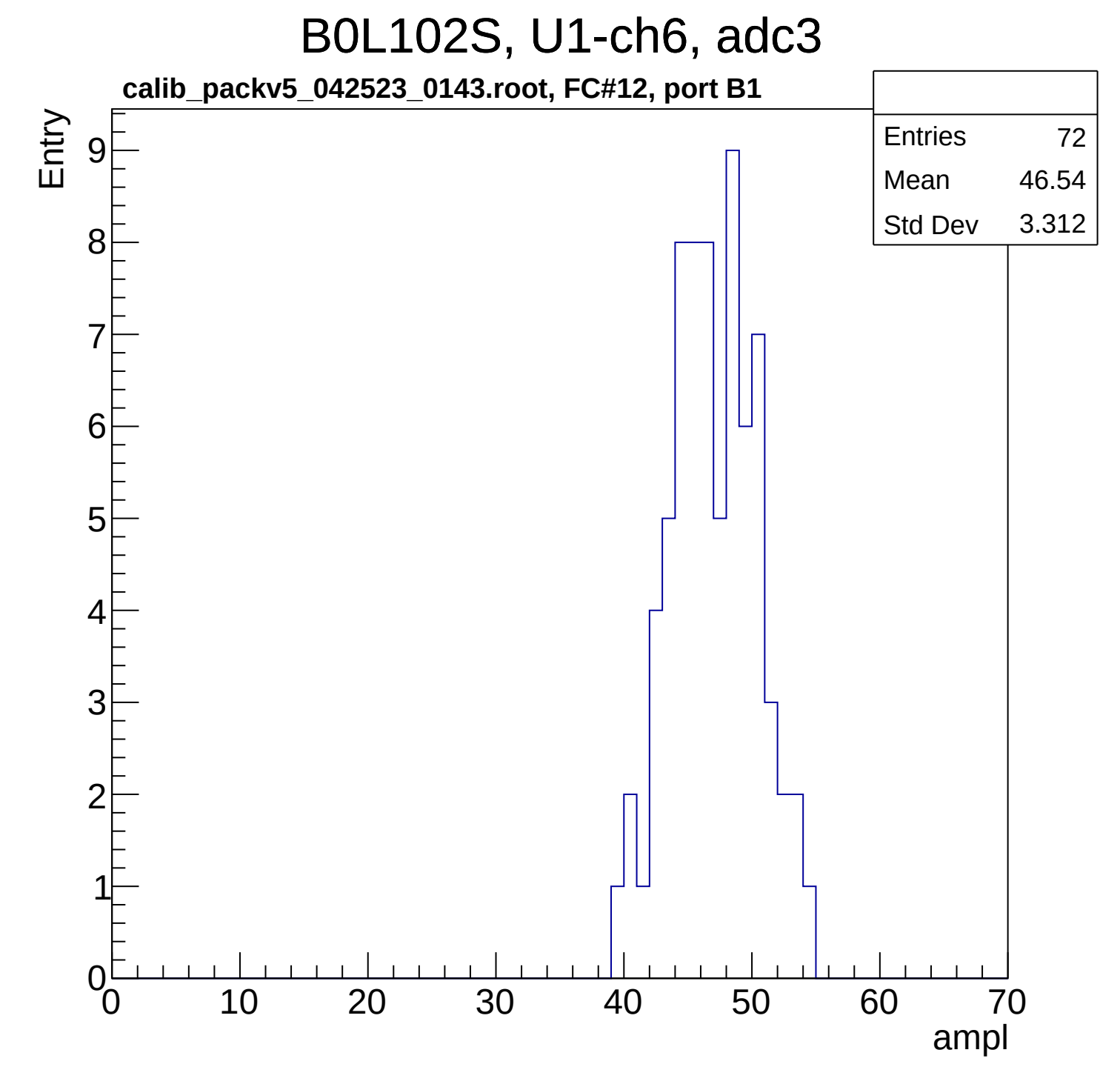
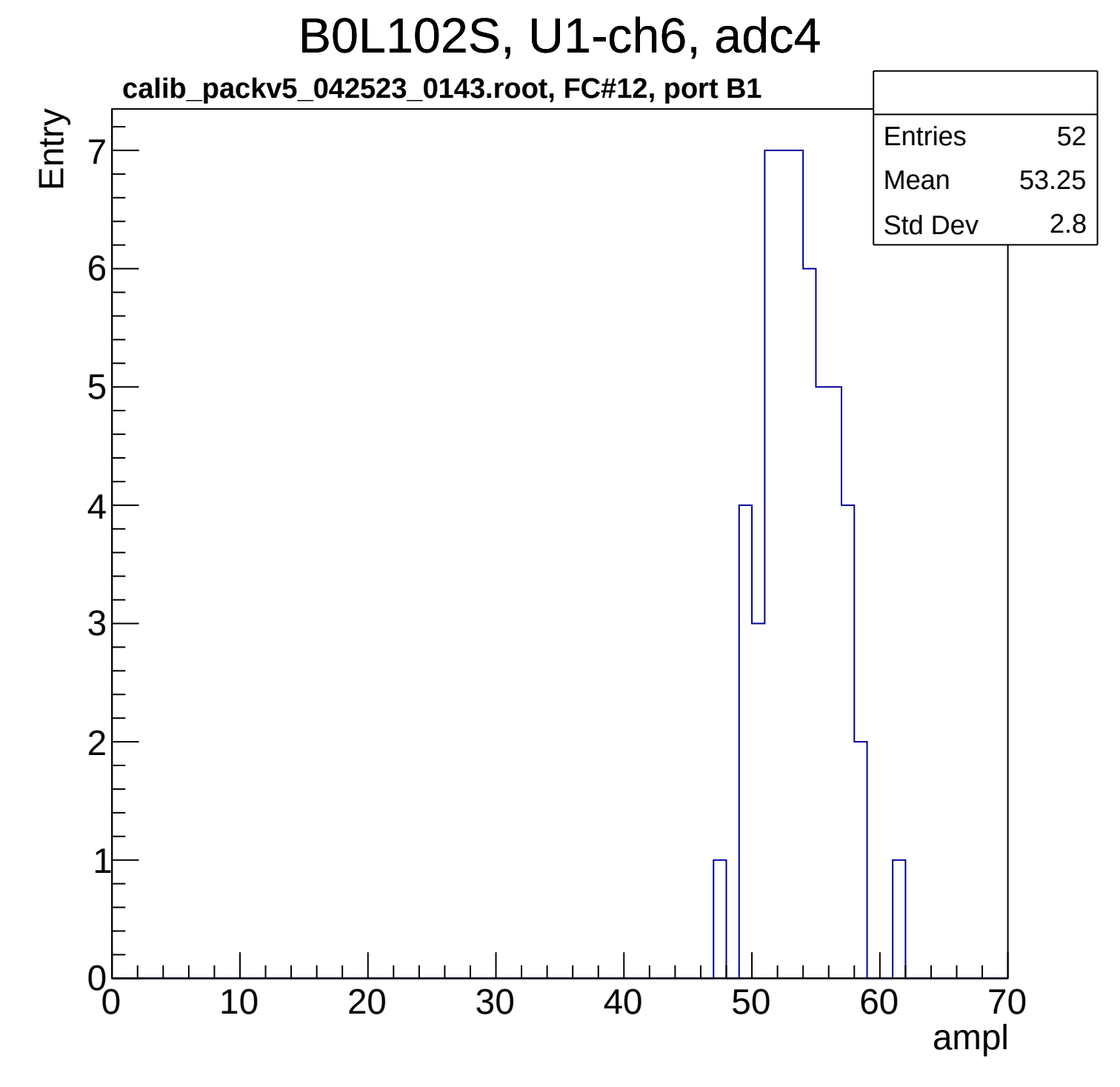
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

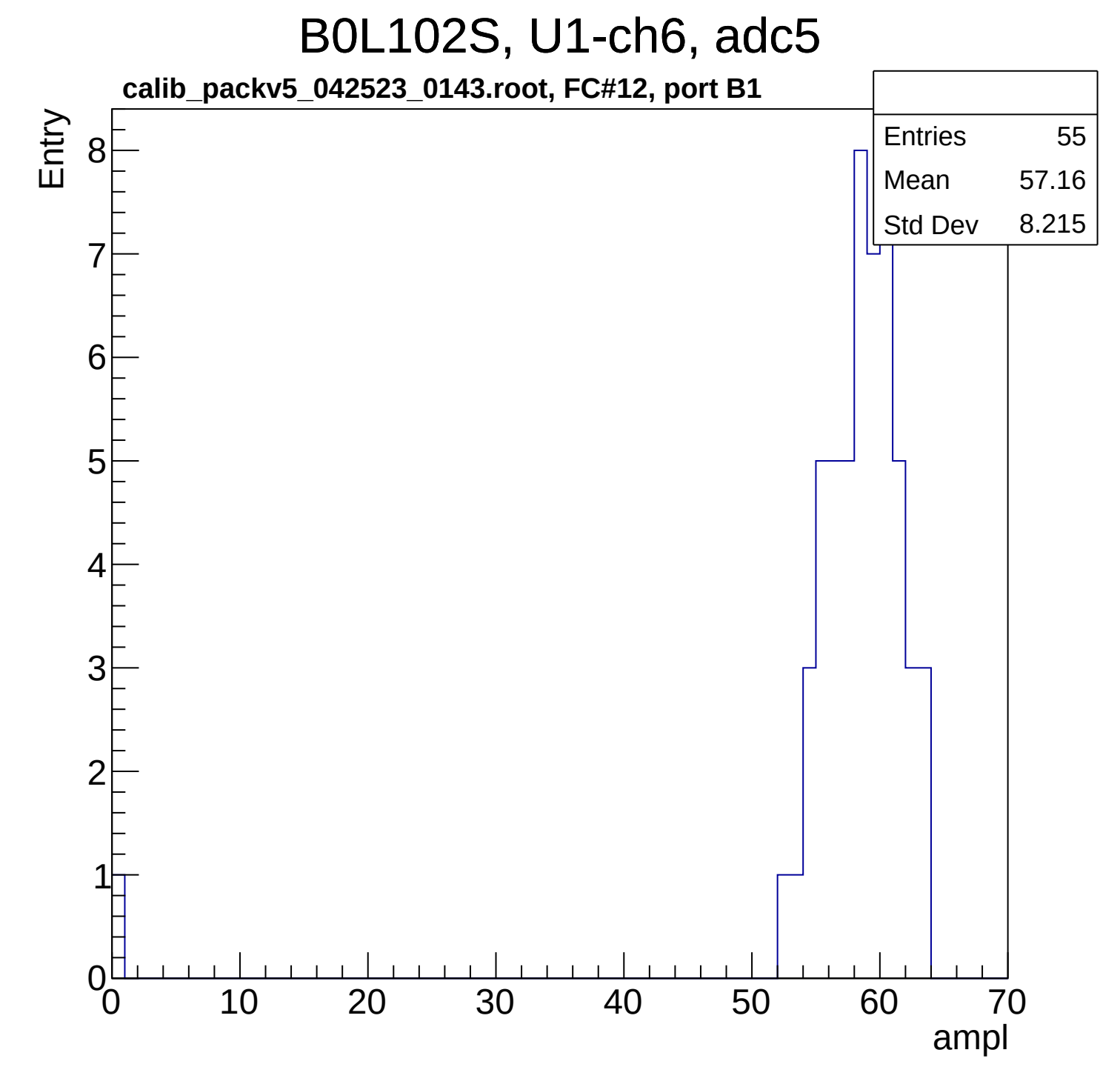

B0L102S, U1-ch6, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 28.33
Std Dev 3.74
Gaus mean : 29.3270
Gaus Width: 4.4908
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch6, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 34.7
Std Dev 2.858
Gaus mean : 35.5019
Gaus Width: 3.0350
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch6, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 40.33
Std Dev 3.597
Gaus mean : 40.4394
Gaus Width: 3.7845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch6, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 46.54
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch6, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 53.25
Std Dev 2.8
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch6, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 57.16
Std Dev 8.215
Entry
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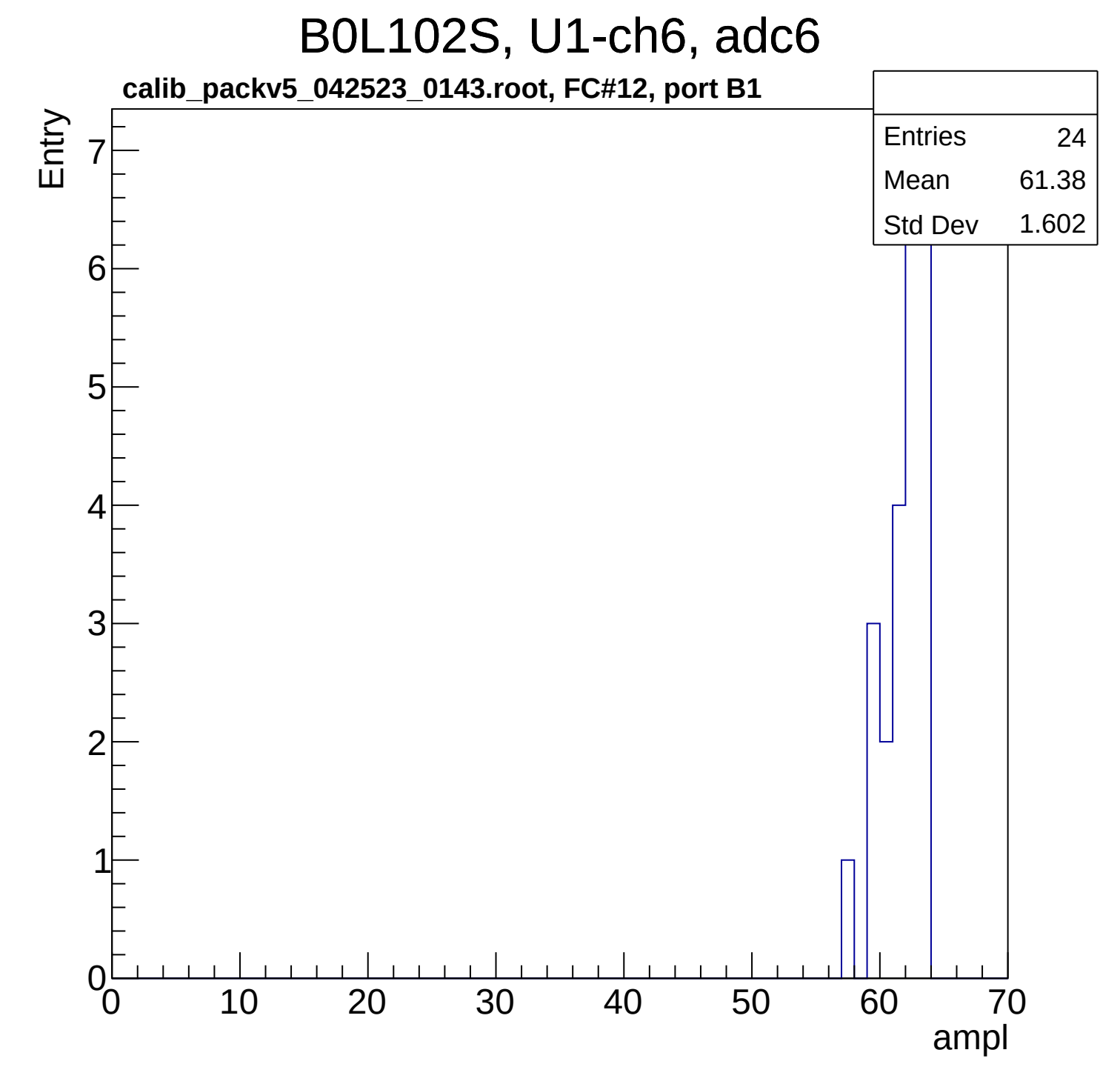
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch6, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 24
Mean 61.38
Std Dev 1.602
Entry
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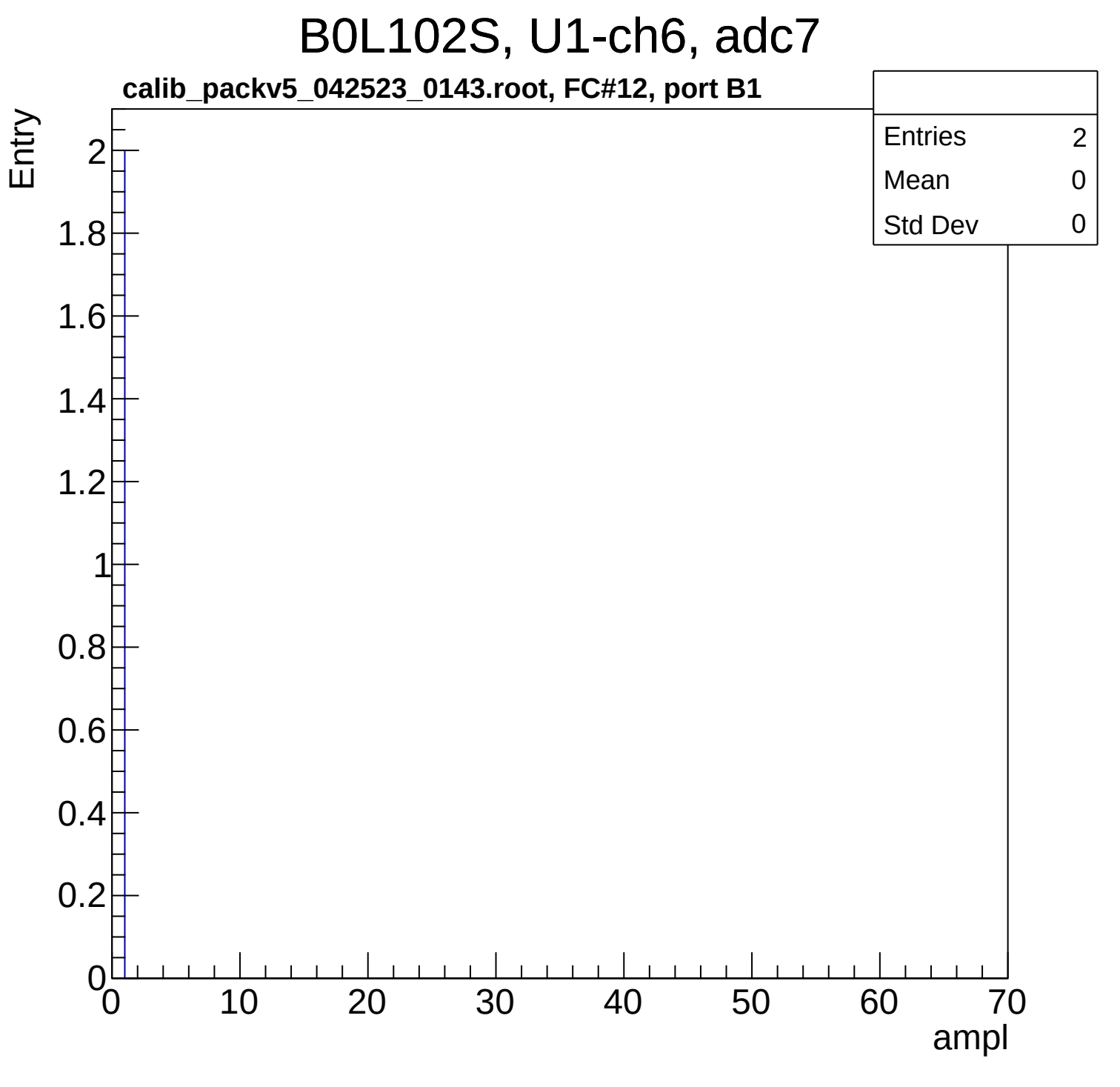
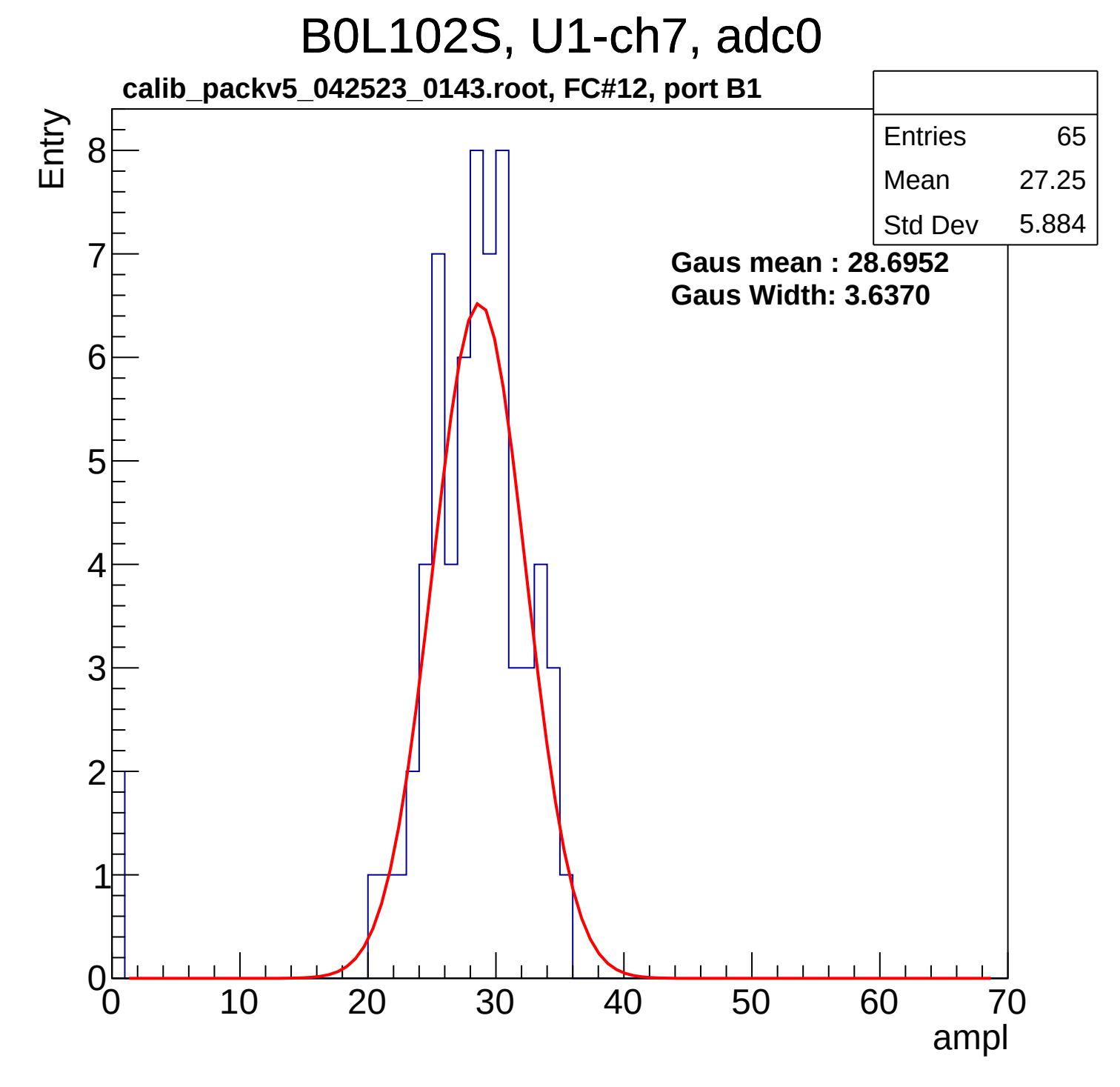
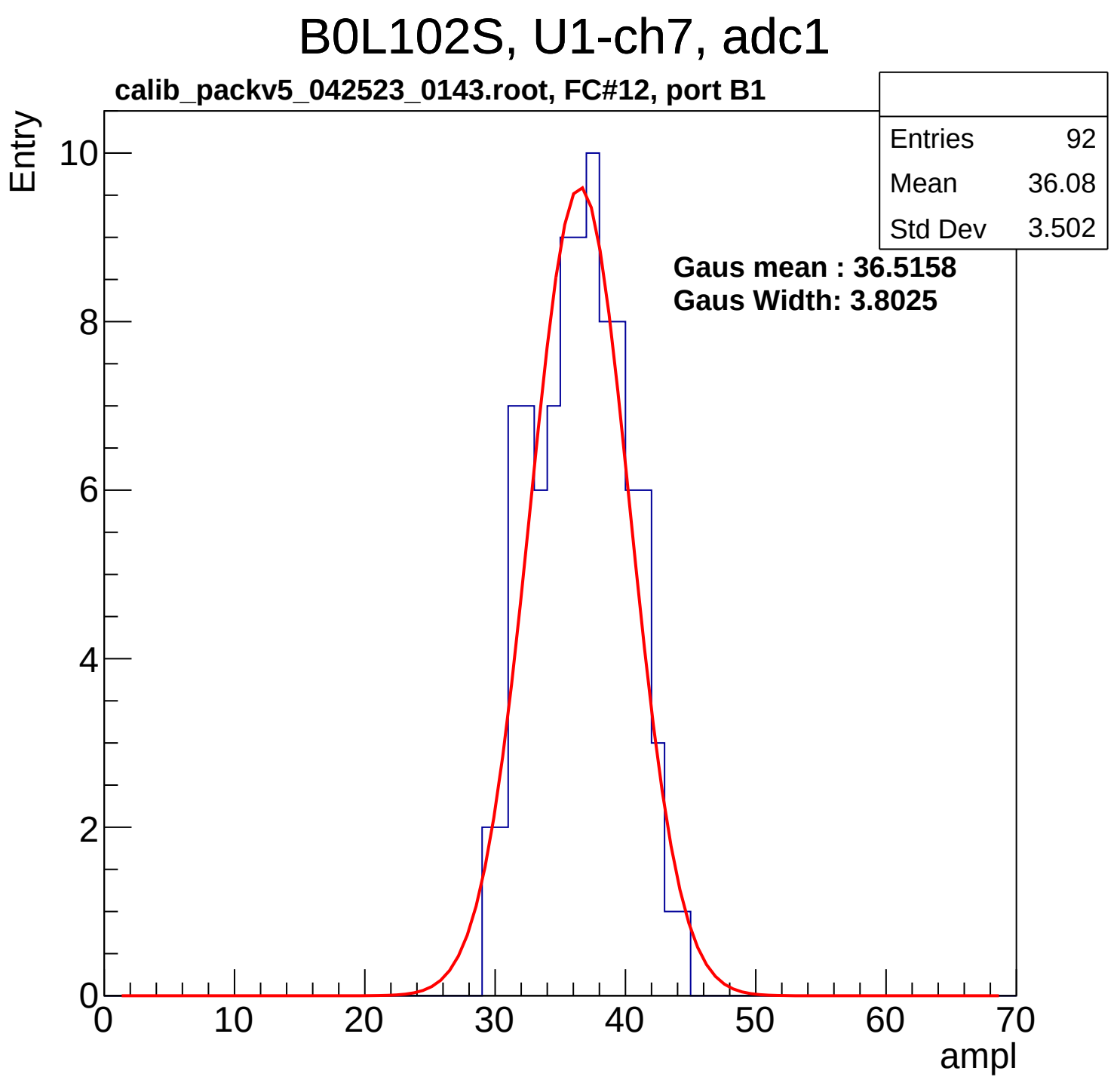
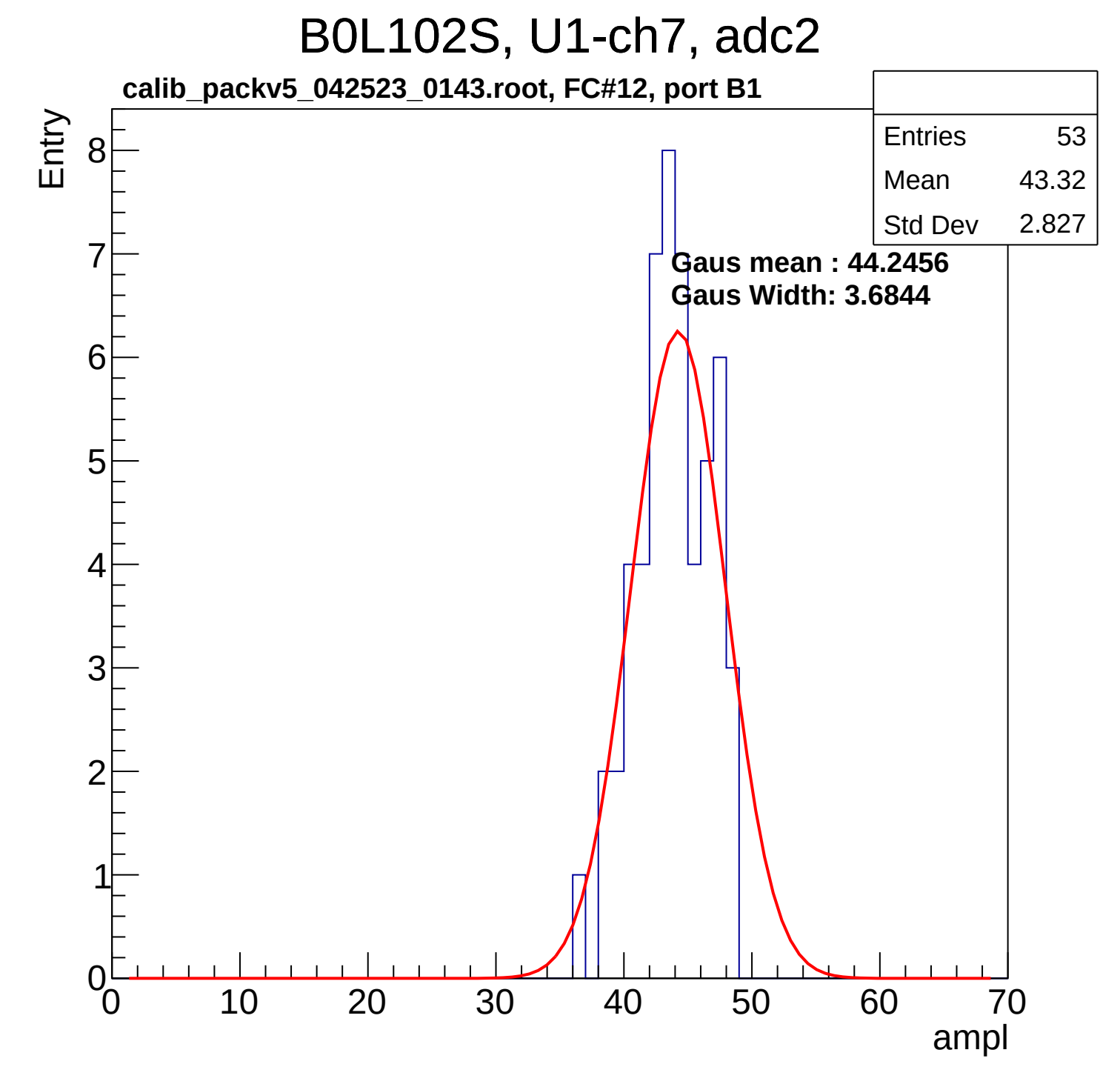
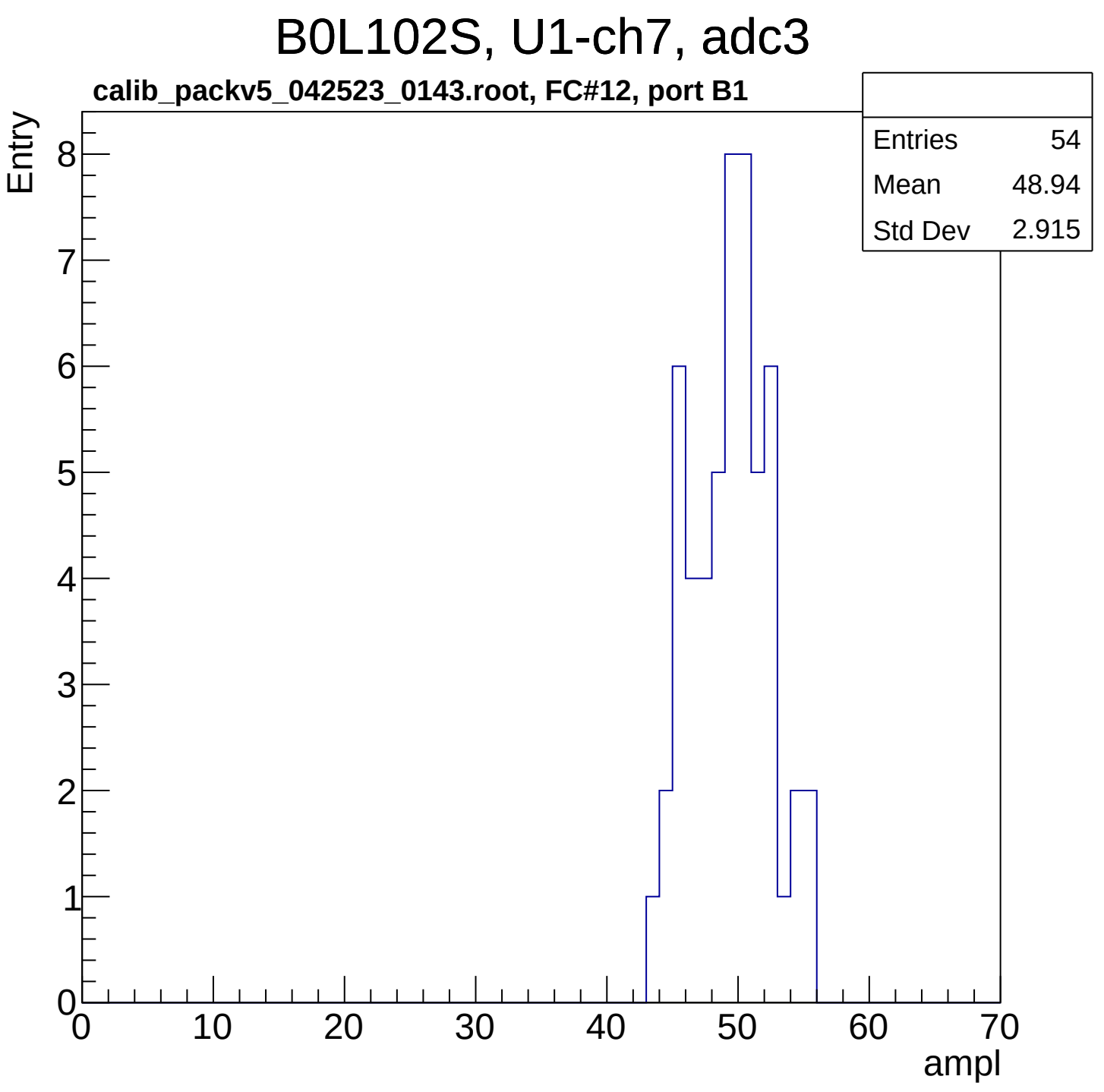
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch6, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch7, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 27.25
Std Dev 5.884
Gaus mean : 28.6952
Gaus Width: 3.6370
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch7, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 92
Mean 36.08
Std Dev 3.502
Gaus mean : 36.5158
Gaus Width: 3.8025
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch7, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 43.32
Std Dev 2.827
Gaus mean : 44.2456
Gaus Width: 3.6844
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch7, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 48.94
Std Dev 2.915
Entry
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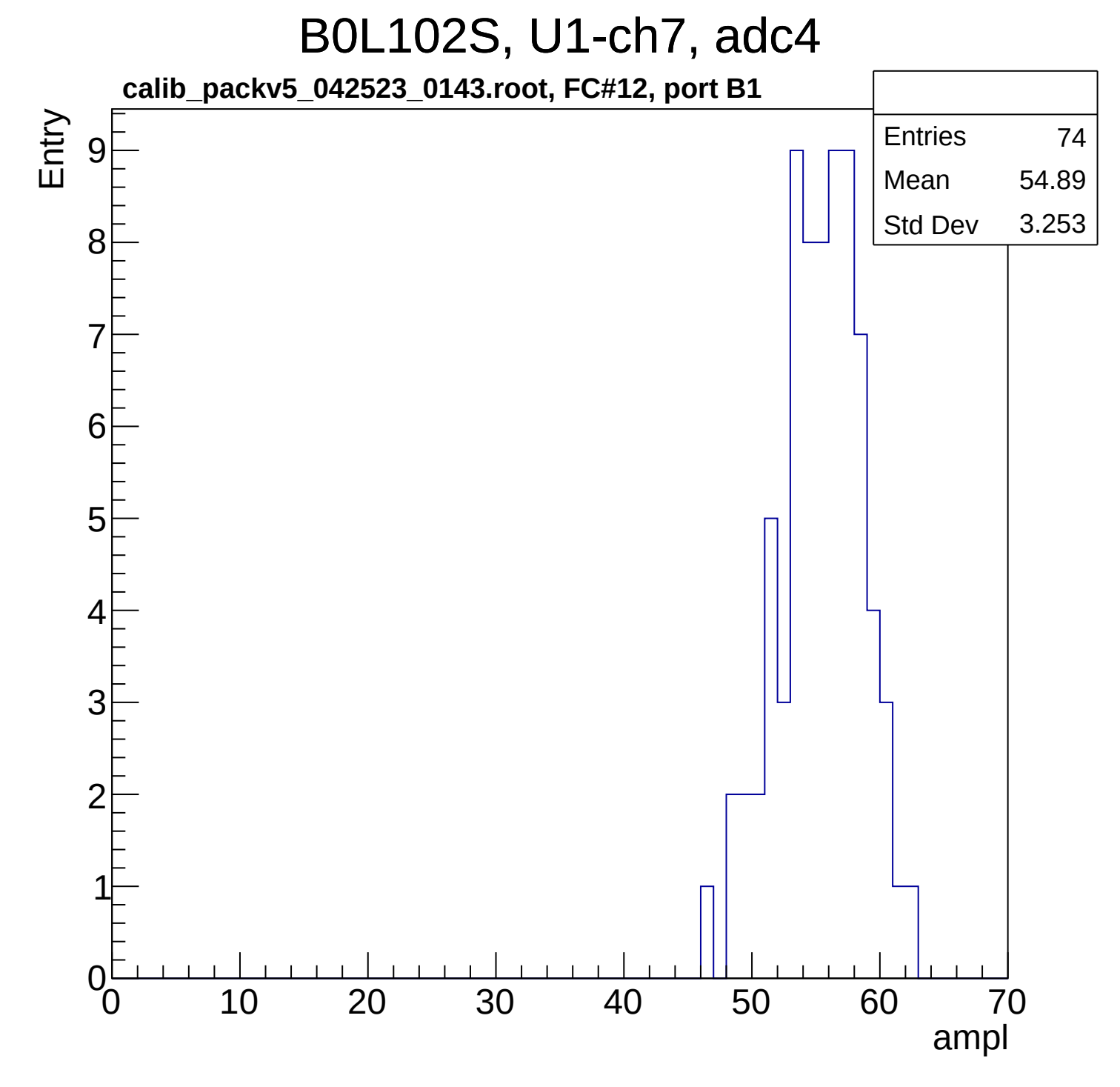
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch7, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 54.89
Std Dev 3.253
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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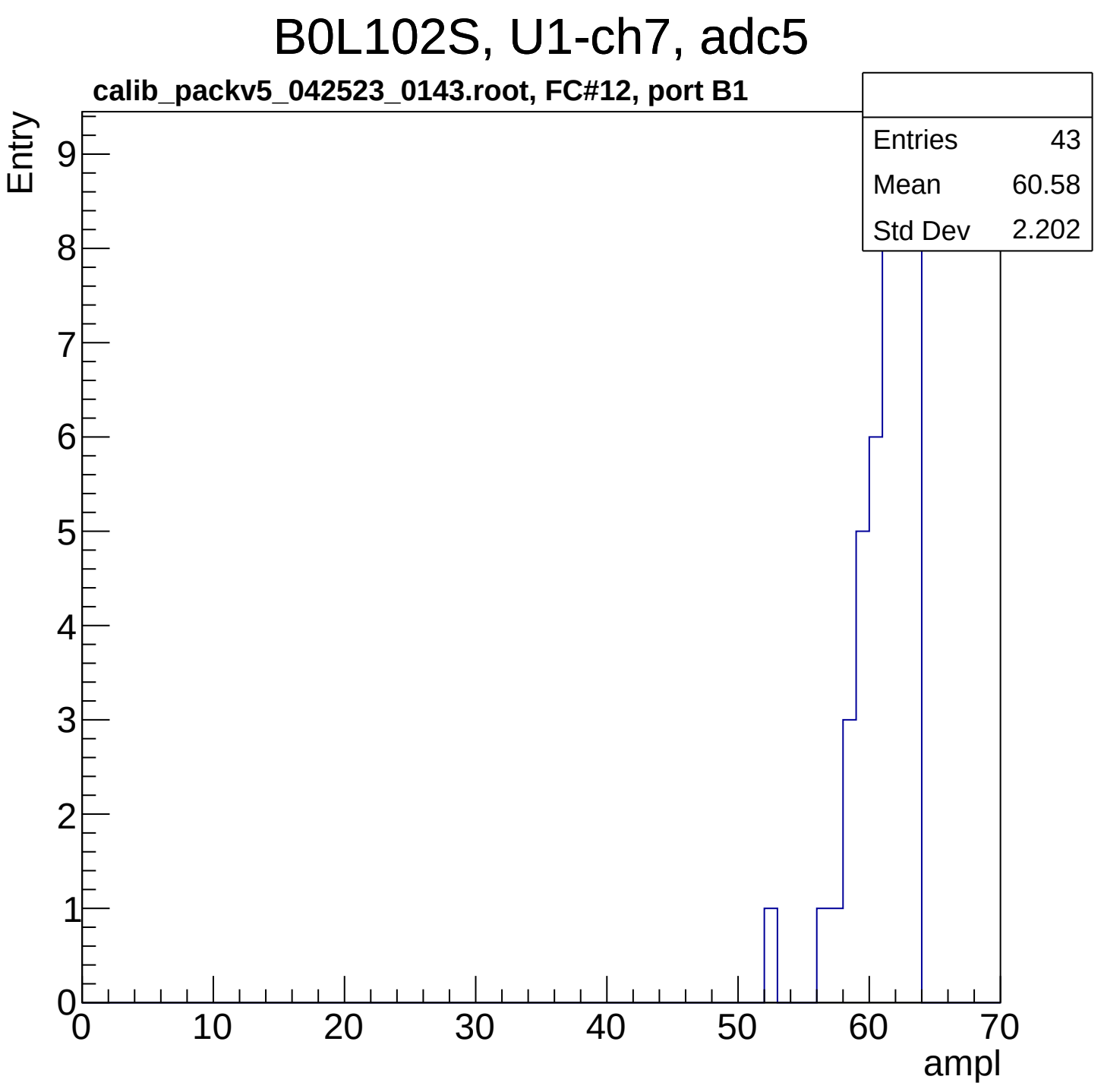
B0L102S, U1-ch7, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 60.58
Std Dev 2.202
Entry
ampl
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3
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0
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60
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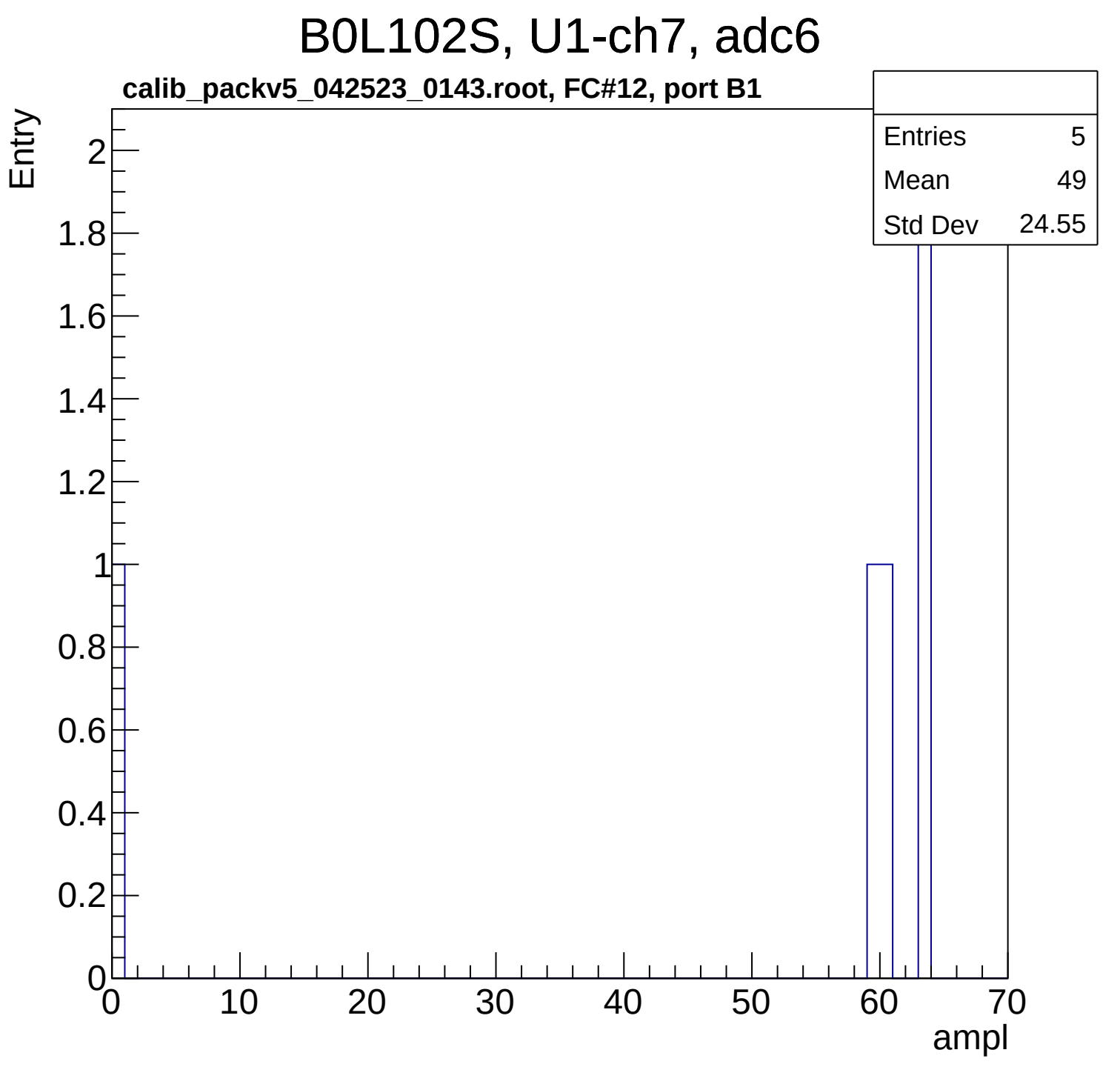
B0L102S, U1-ch7, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 49
Std Dev 24.55
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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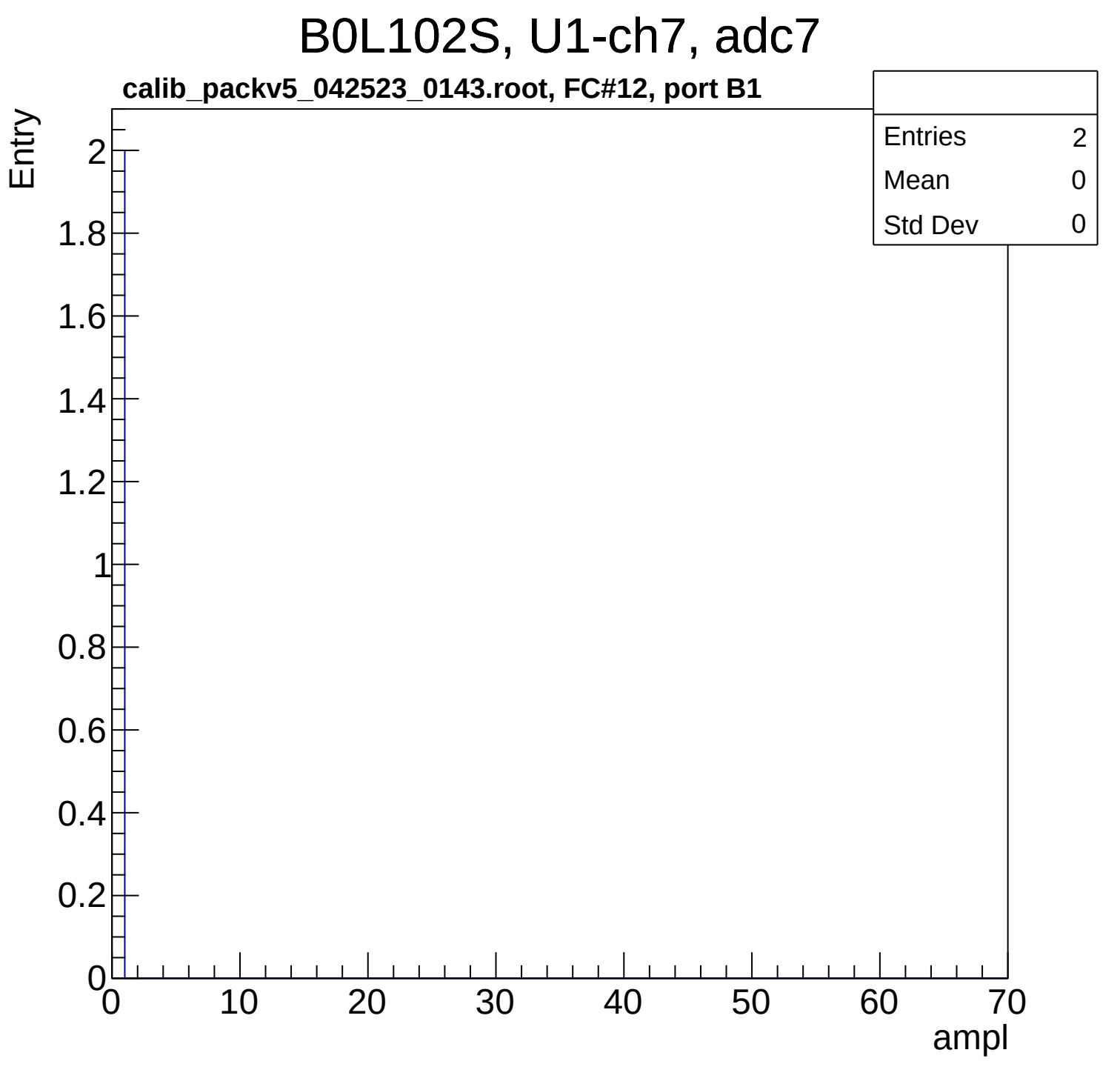
40
50
60
70


B0L102S, U1-ch7, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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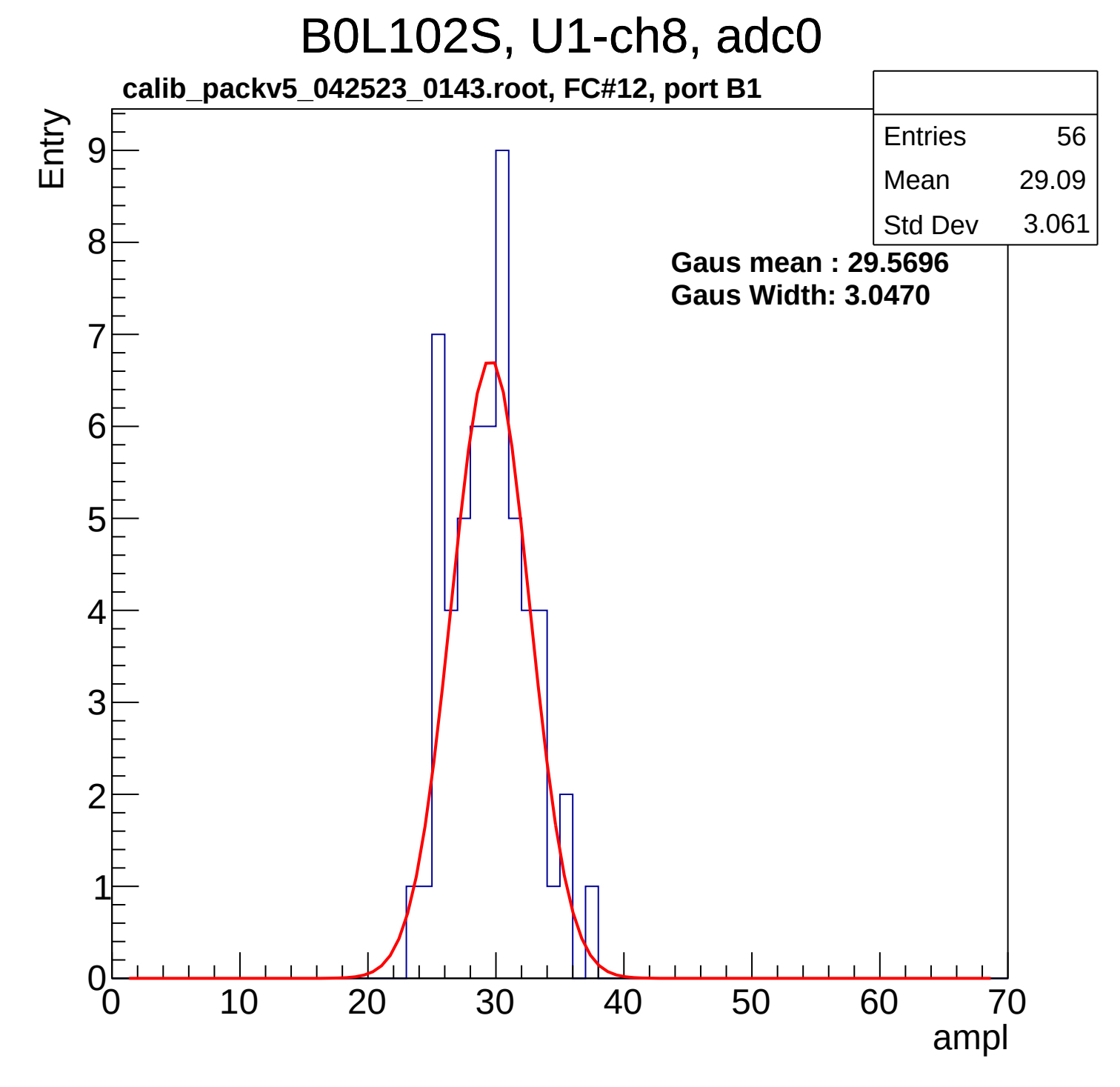
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch8, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 29.09
Std Dev 3.061
Gaus mean : 29.5696
Gaus Width: 3.0470
Entry
ampl
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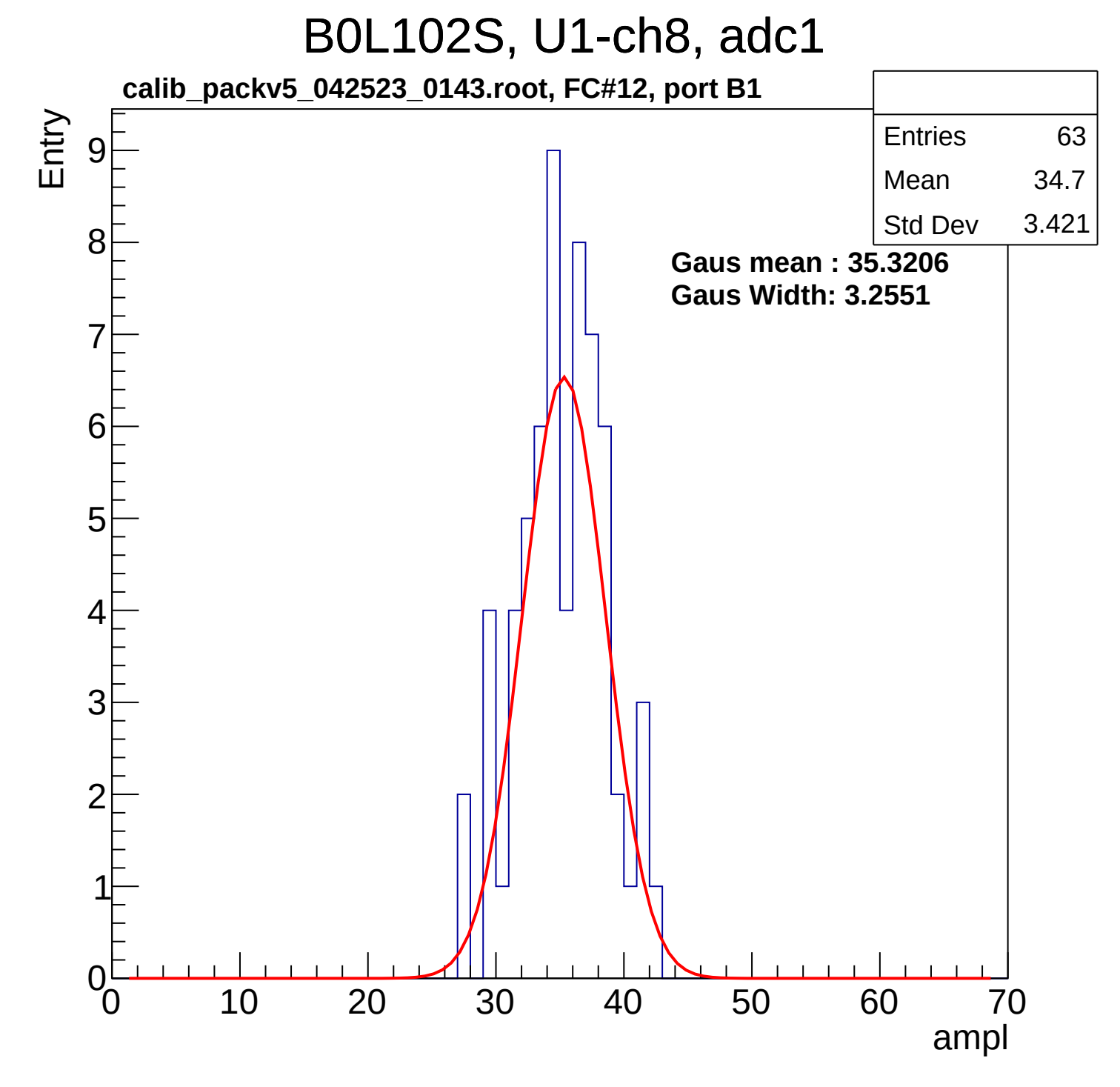
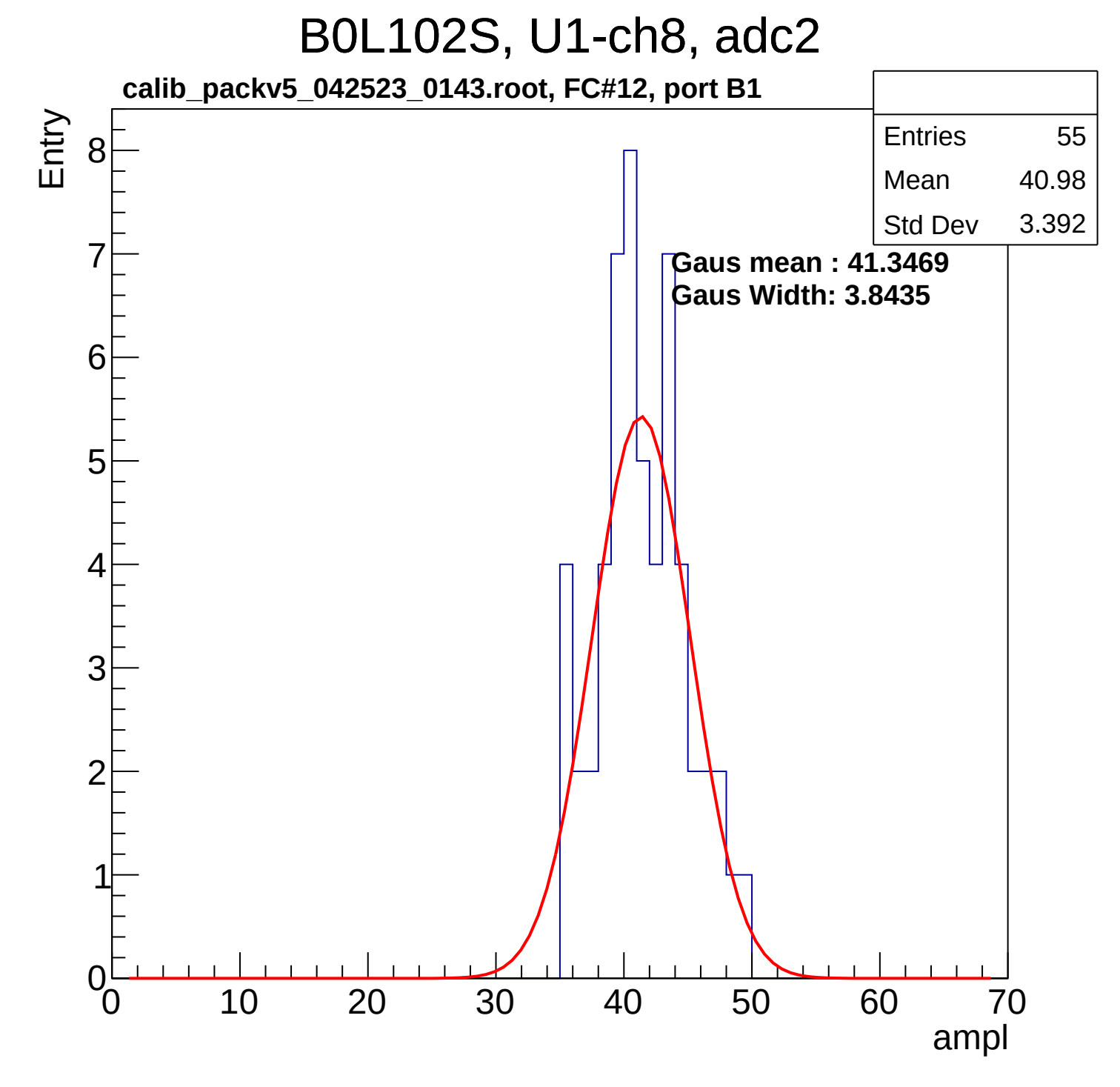
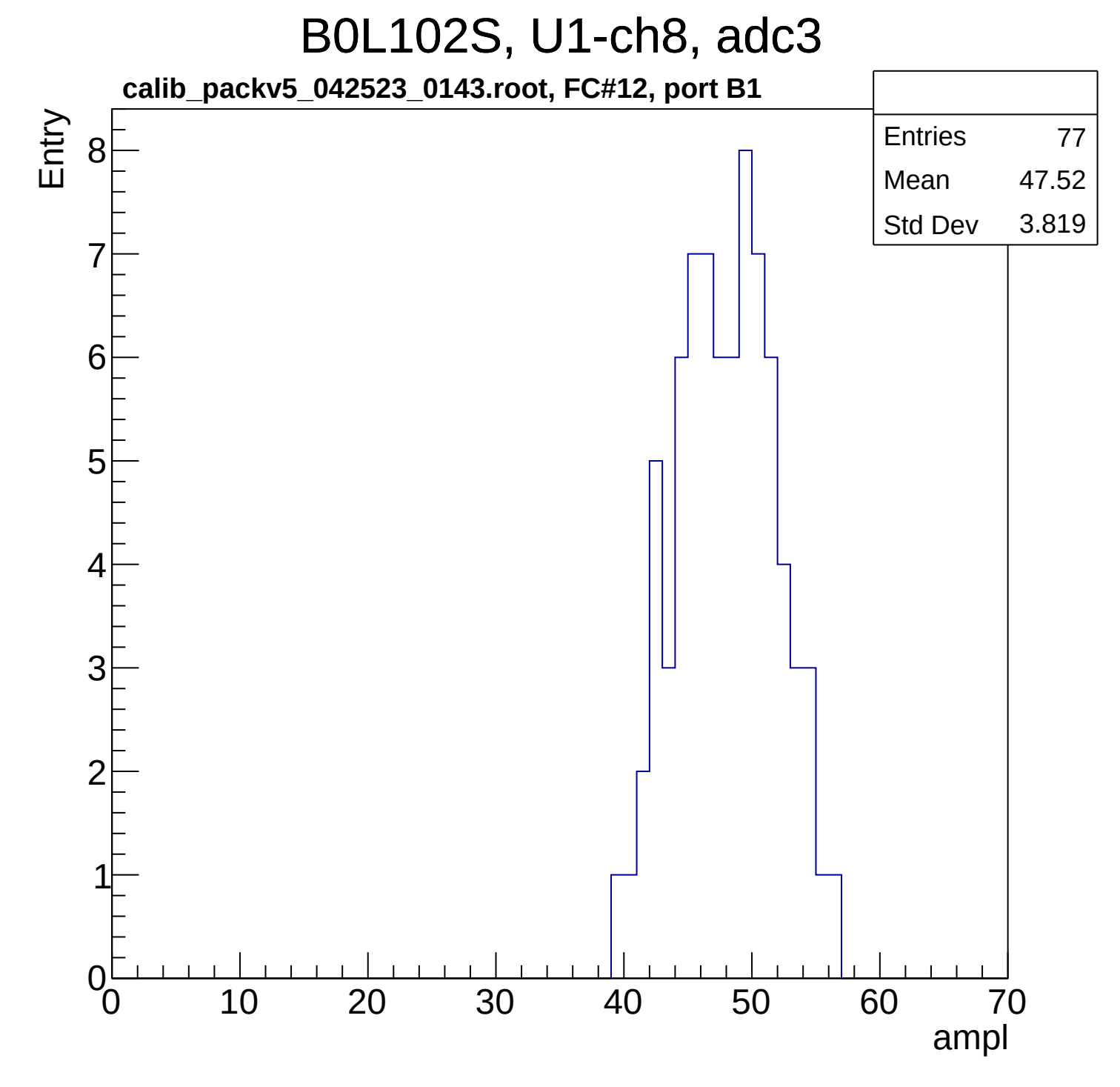
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch8, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 34.7
Std Dev 3.421
Gaus mean : 35.3206
Gaus Width: 3.2551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch8, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 40.98
Std Dev 3.392
Gaus mean : 41.3469
Gaus Width: 3.8435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch8, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 47.52
Std Dev 3.819
Entry
ampl
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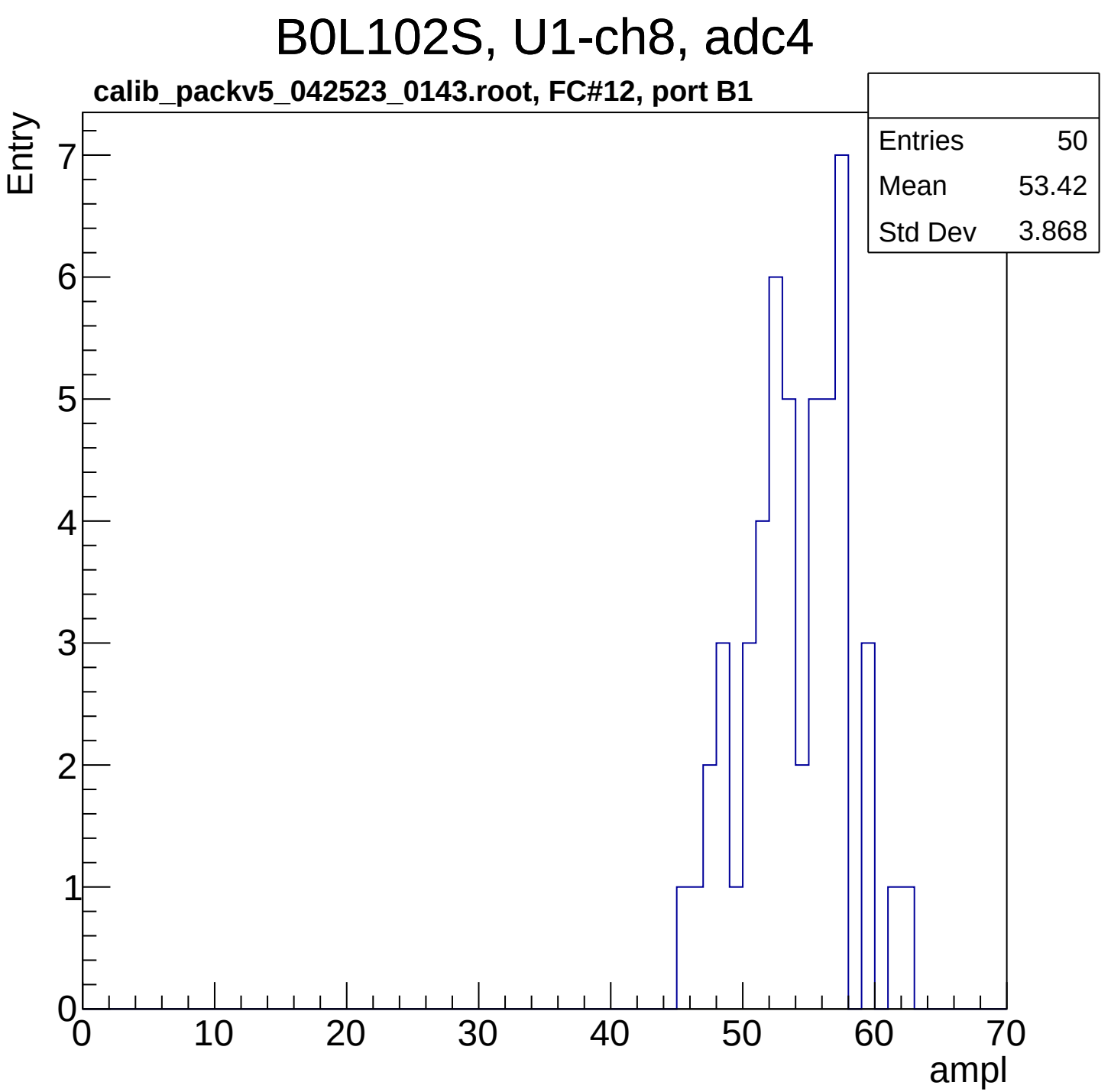
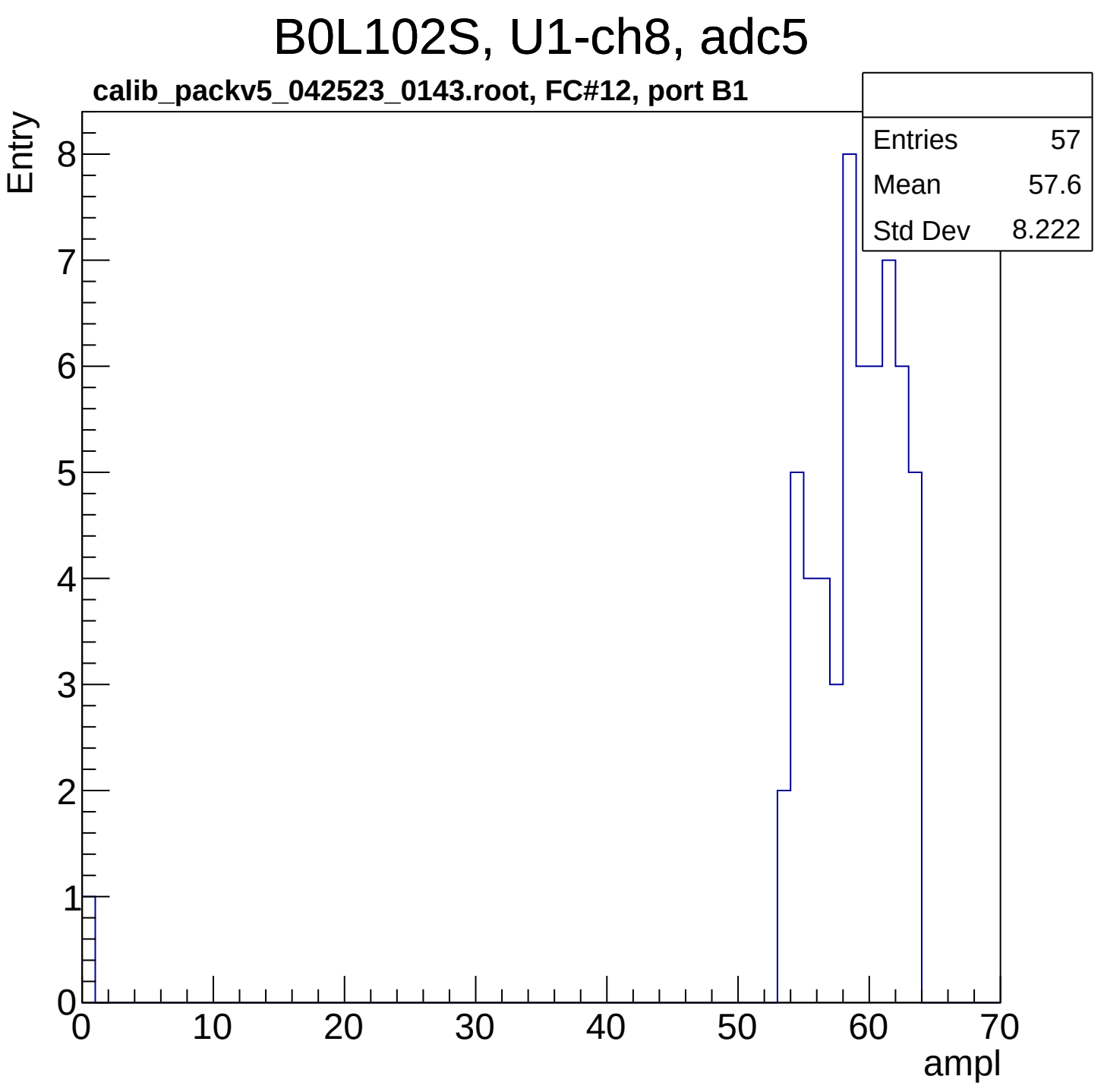
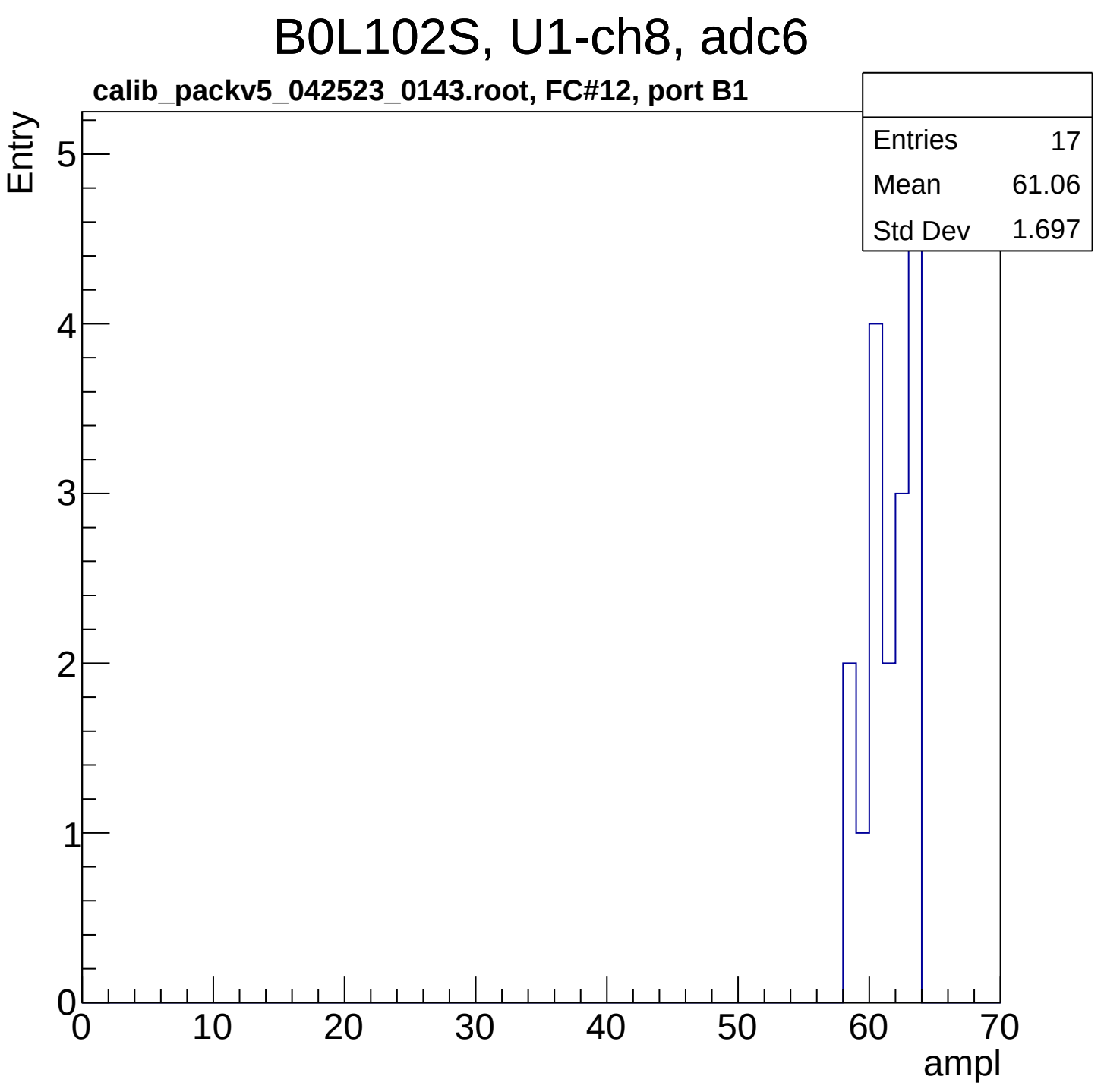
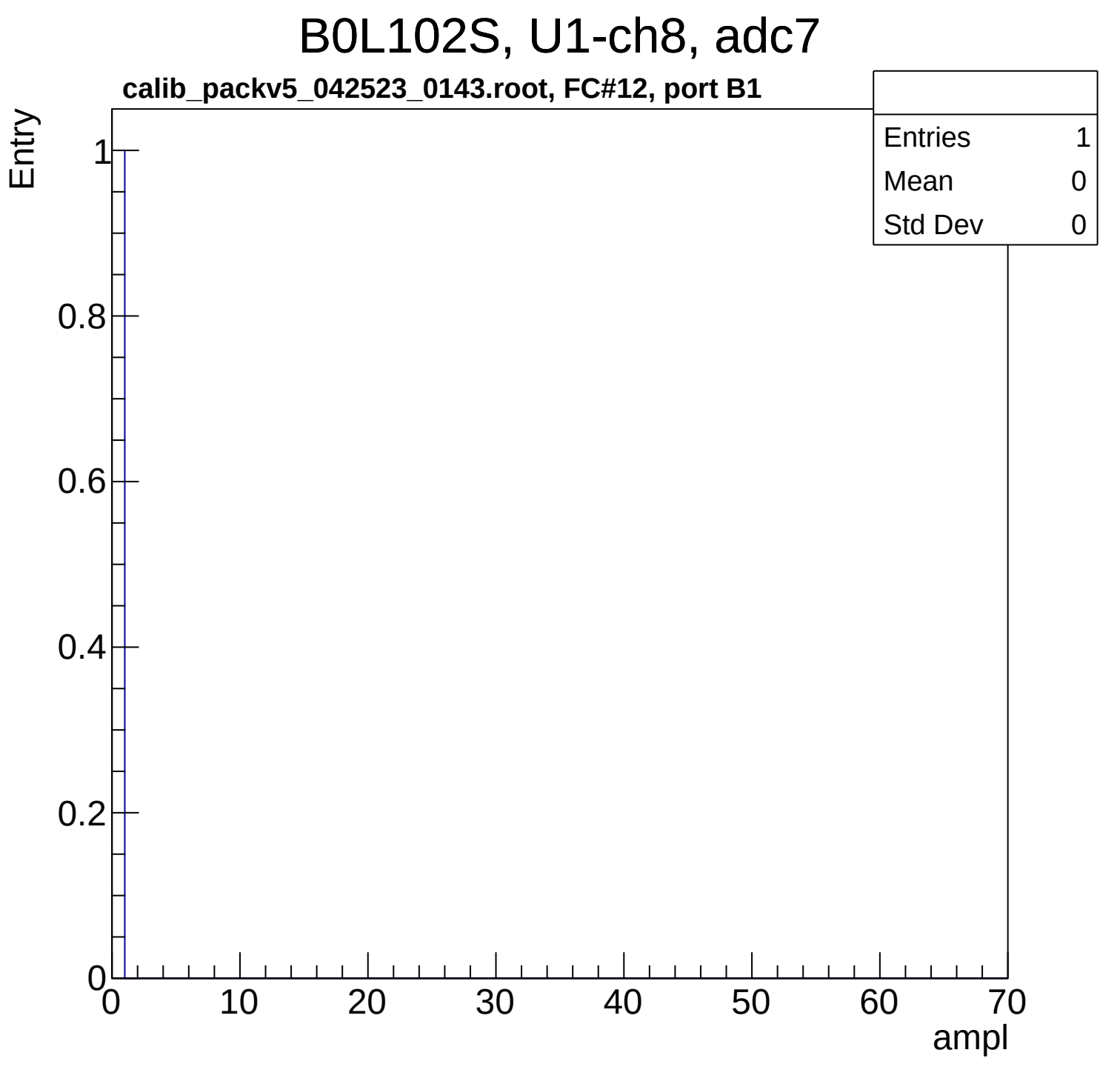
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch8, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 53.42
Std Dev 3.868
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch8, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 57.6
Std Dev 8.222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch8, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 61.06
Std Dev 1.697
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch8, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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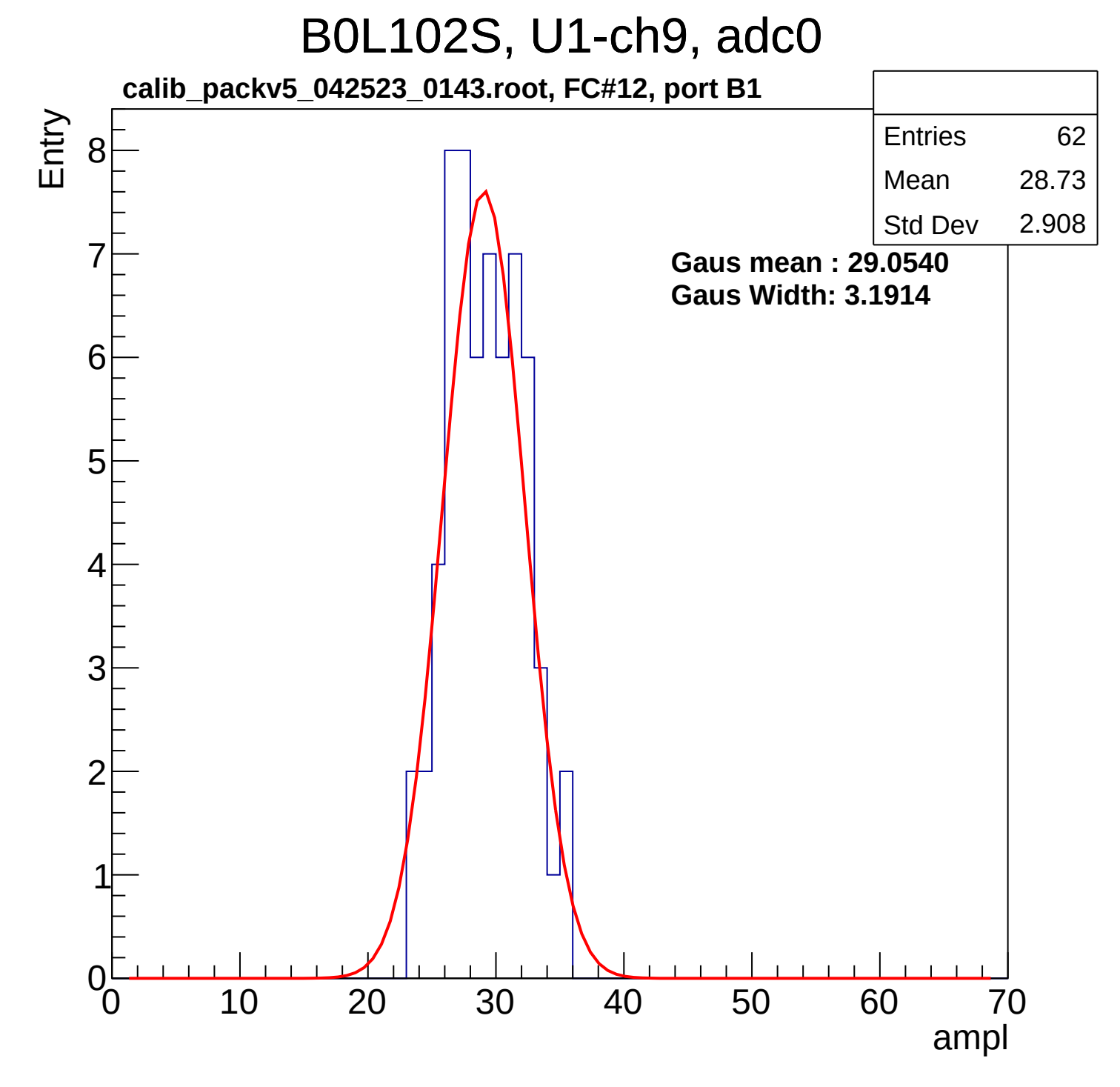
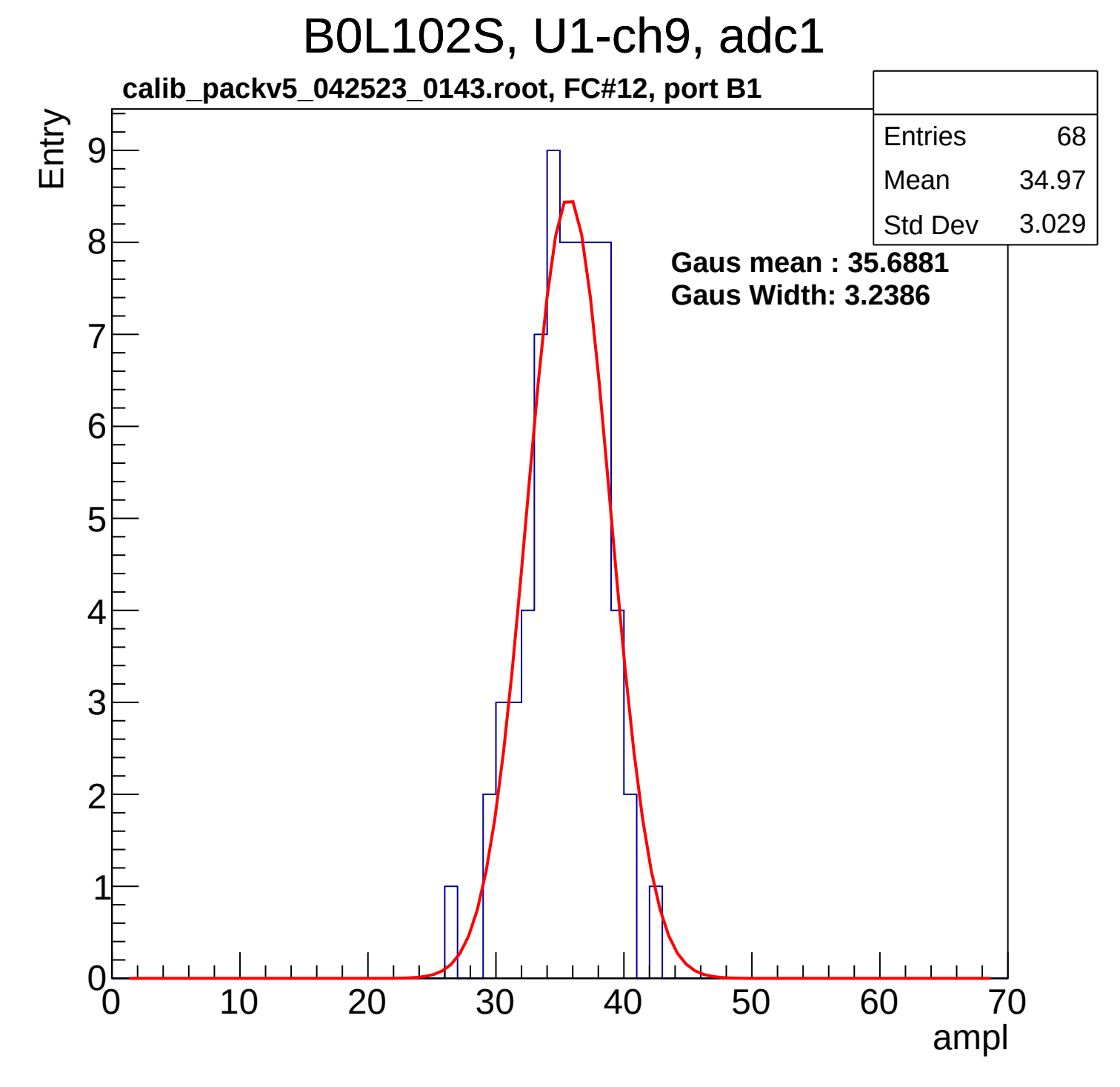
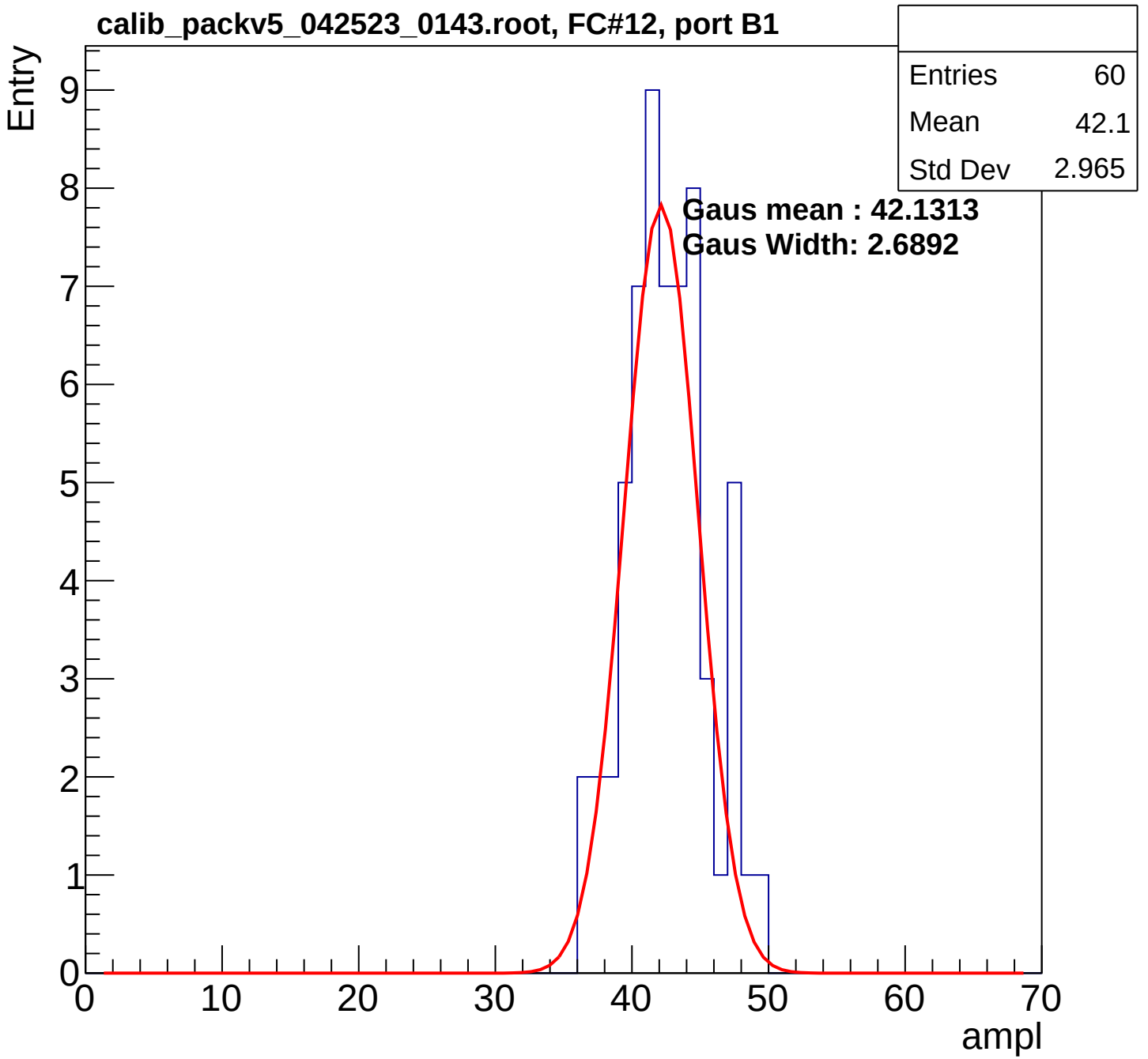
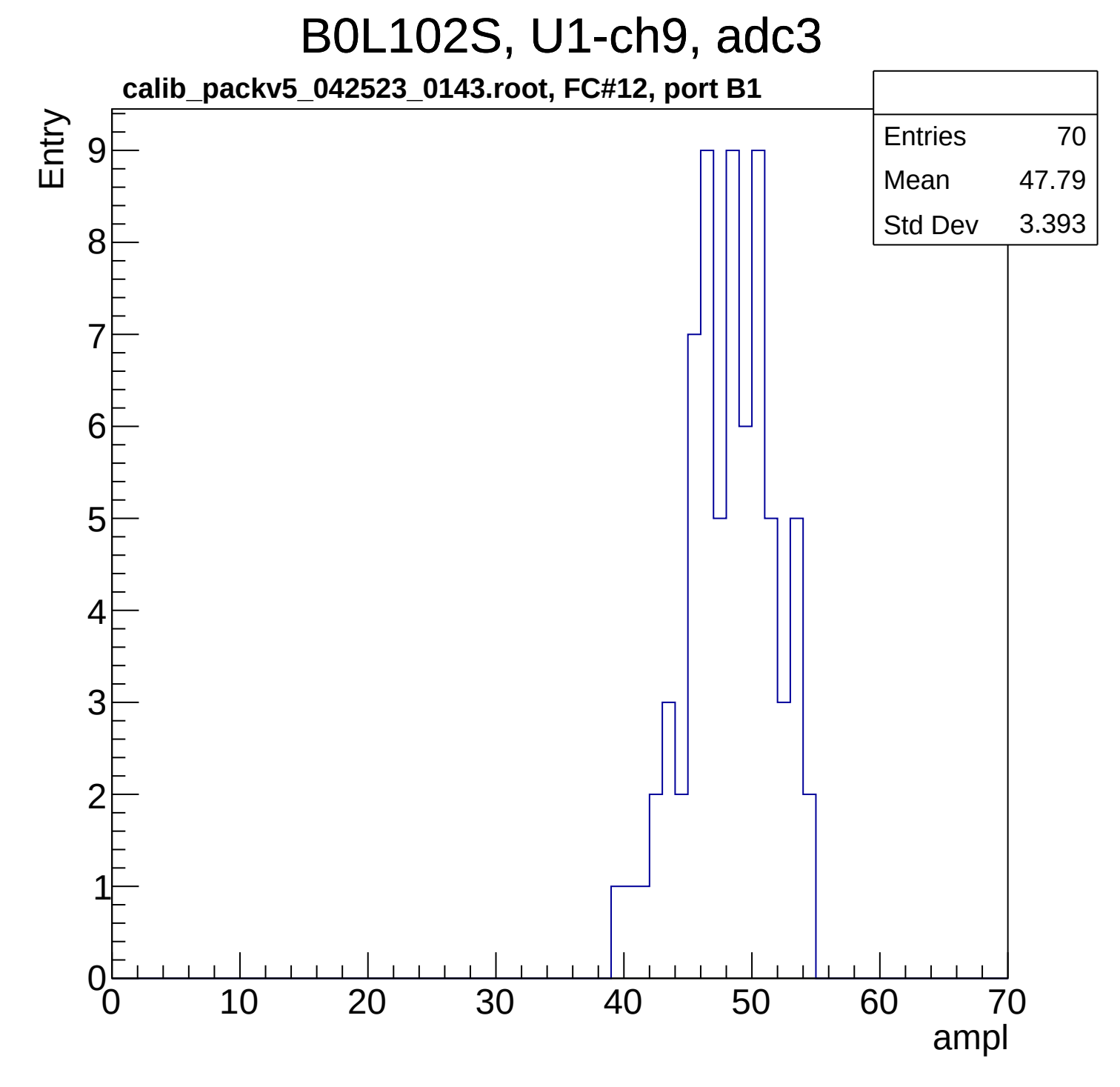
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch9, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 28.73
Std Dev 2.908
Gaus mean : 29.0540
Gaus Width: 3.1914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch9, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 34.97
Std Dev 3.029
Gaus mean : 35.6881
Gaus Width: 3.2386
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch9, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 42.1
Std Dev 2.965
Gaus mean : 42.1313
Gaus Width: 2.6892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch9, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 47.79
Std Dev 3.393
Entry
ampl
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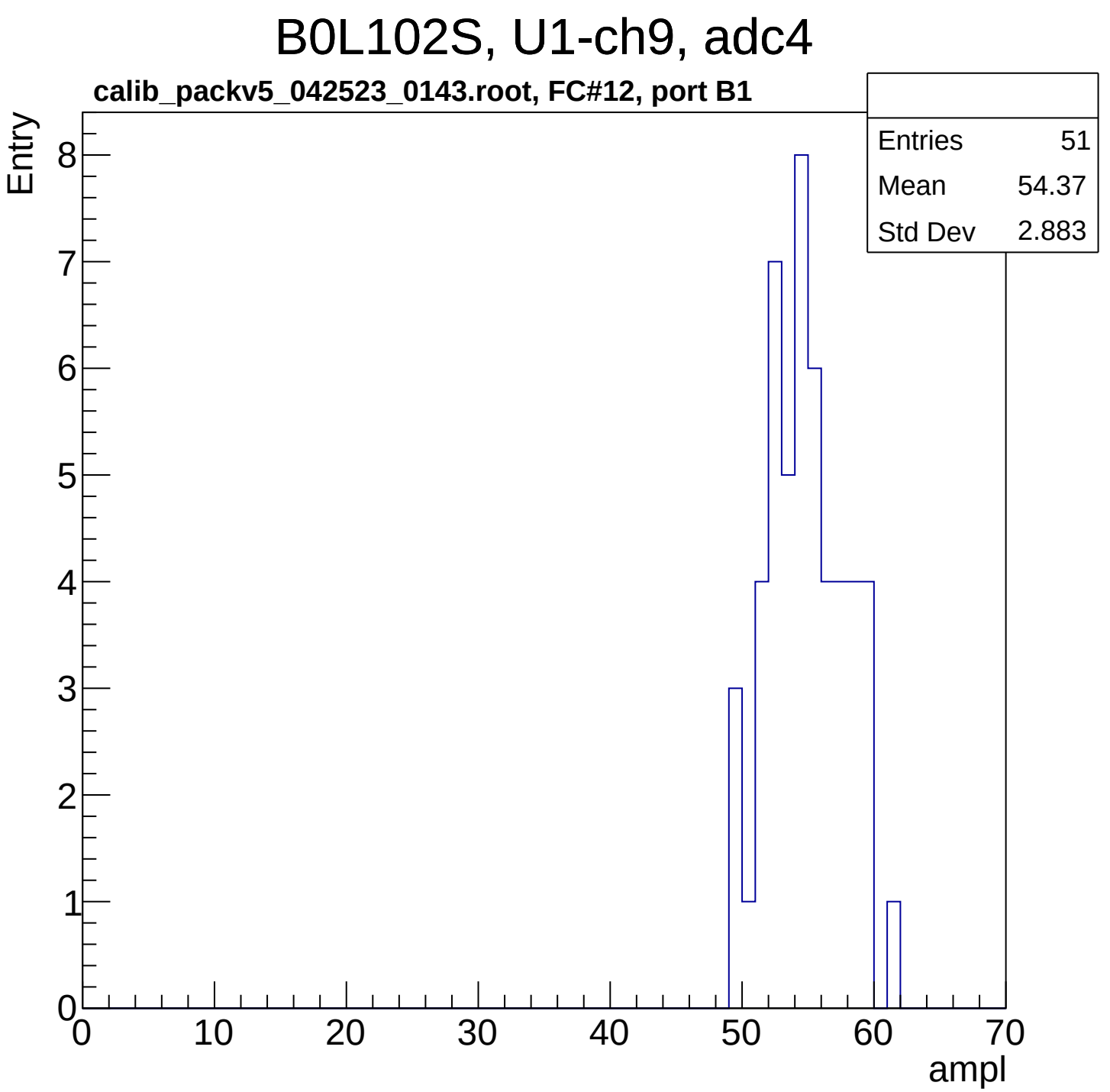
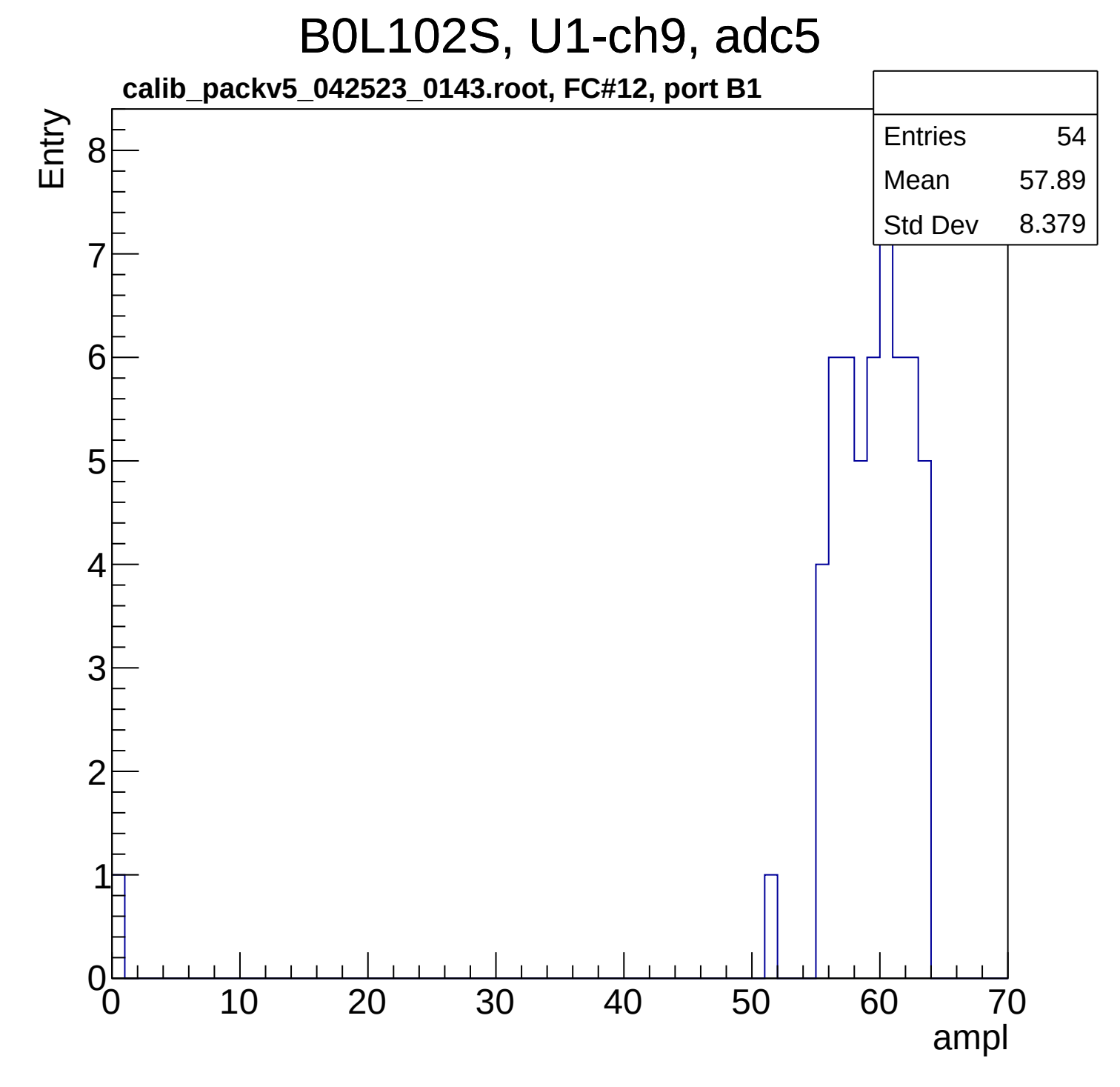
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch9, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 54.37
Std Dev 2.883
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch9, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 57.89
Std Dev 8.379
Entry
ampl
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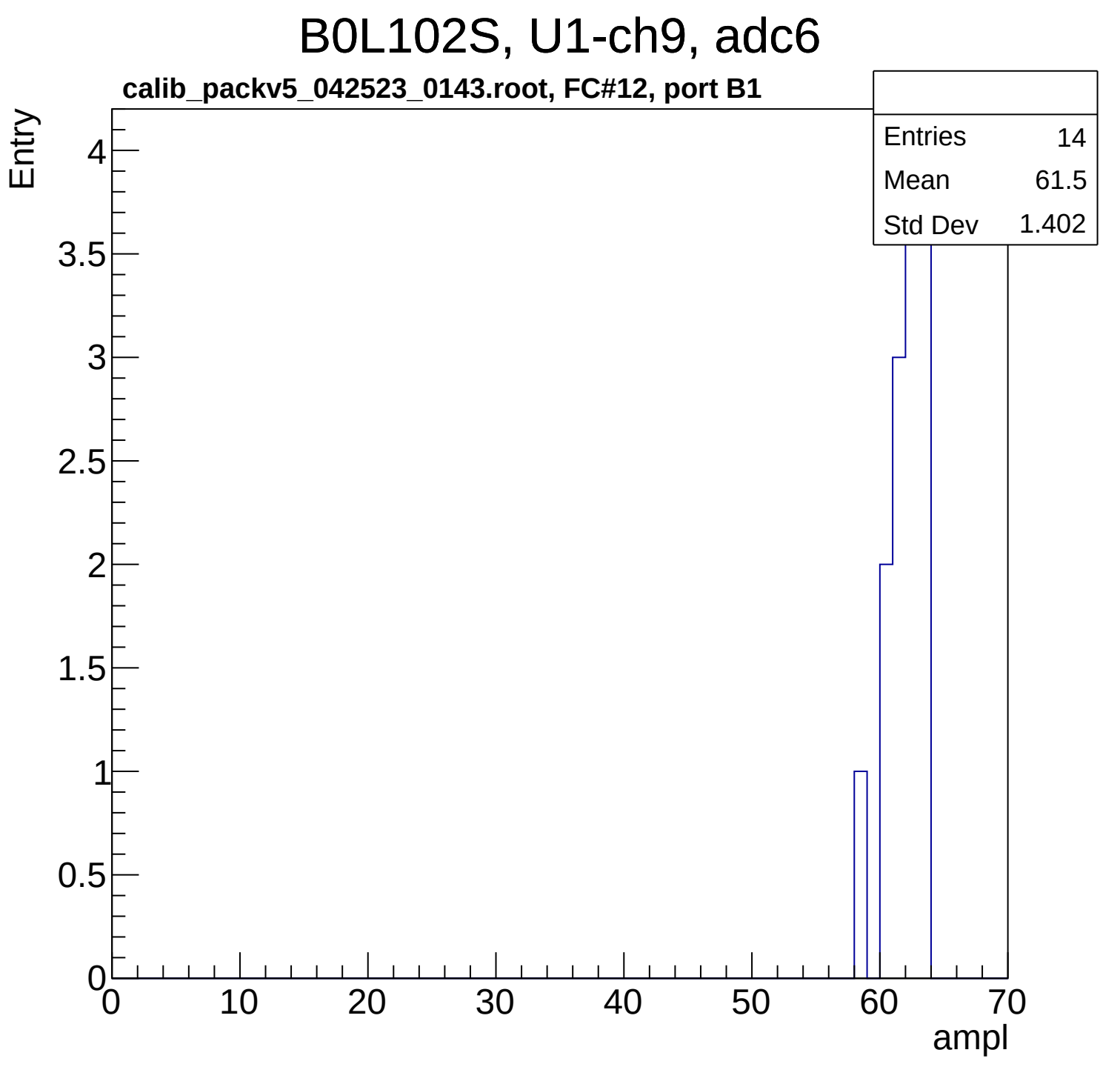
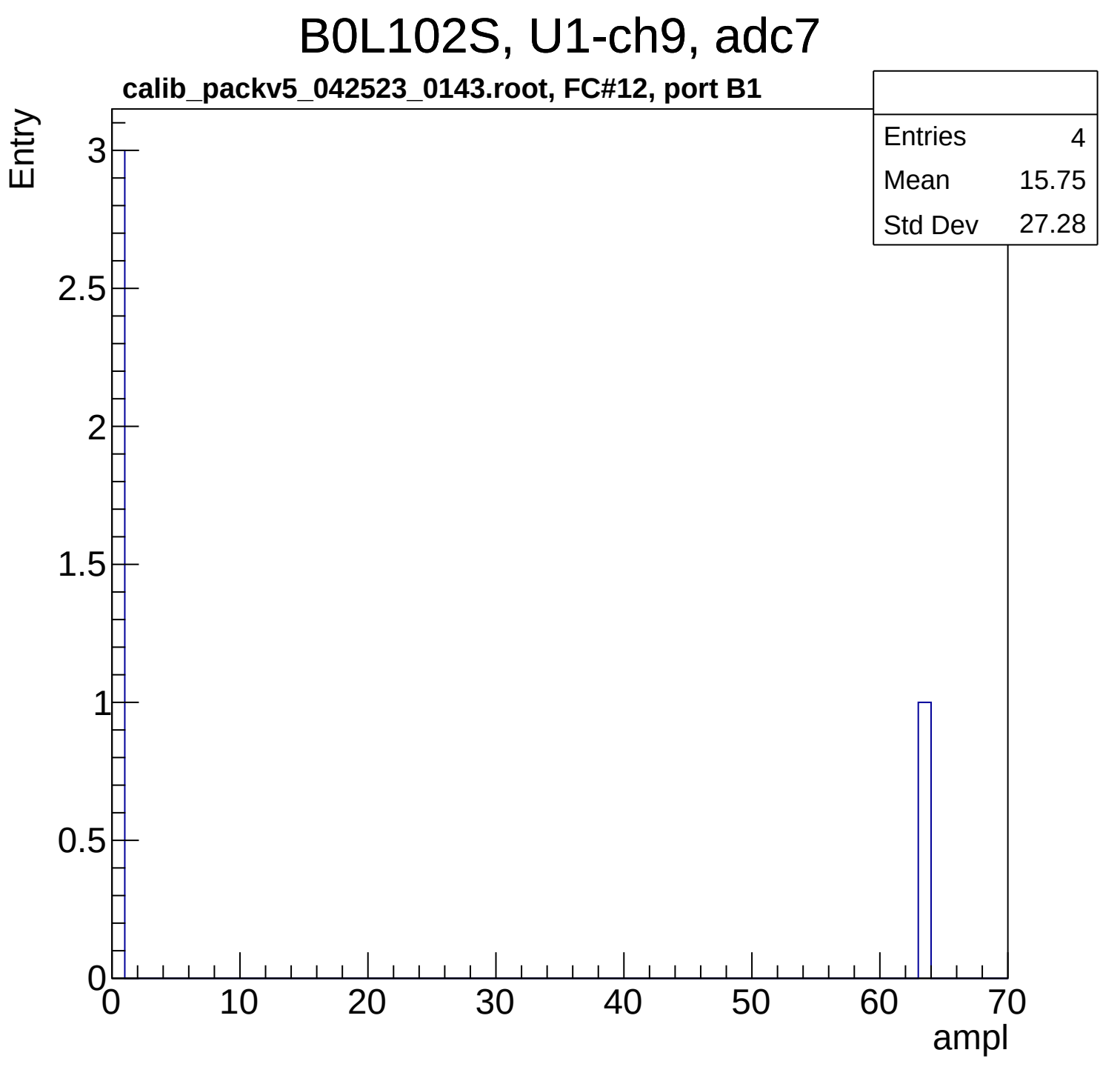
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch9, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.5
Std Dev 1.402
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch9, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 15.75
Std Dev 27.28
Entry
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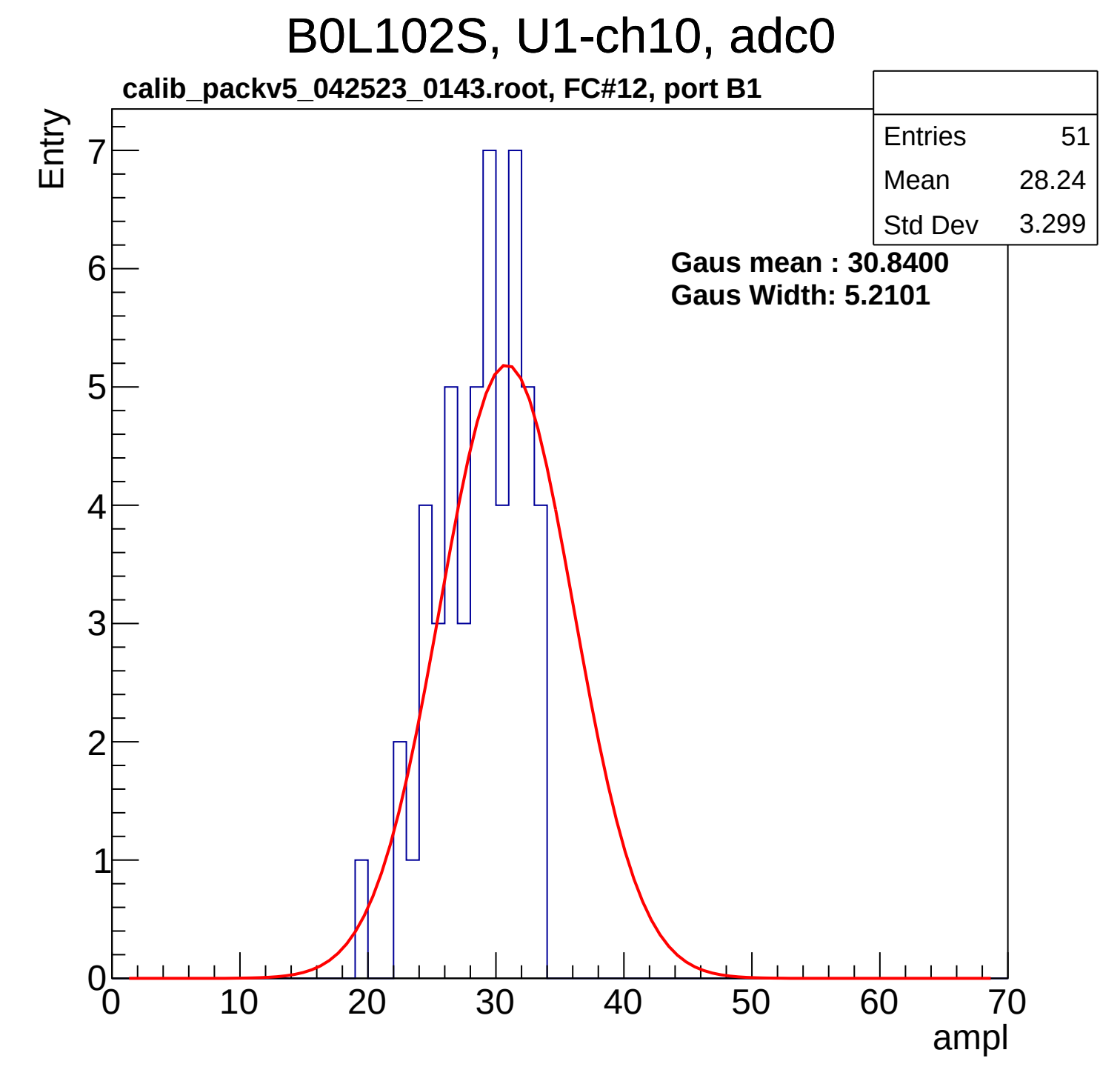
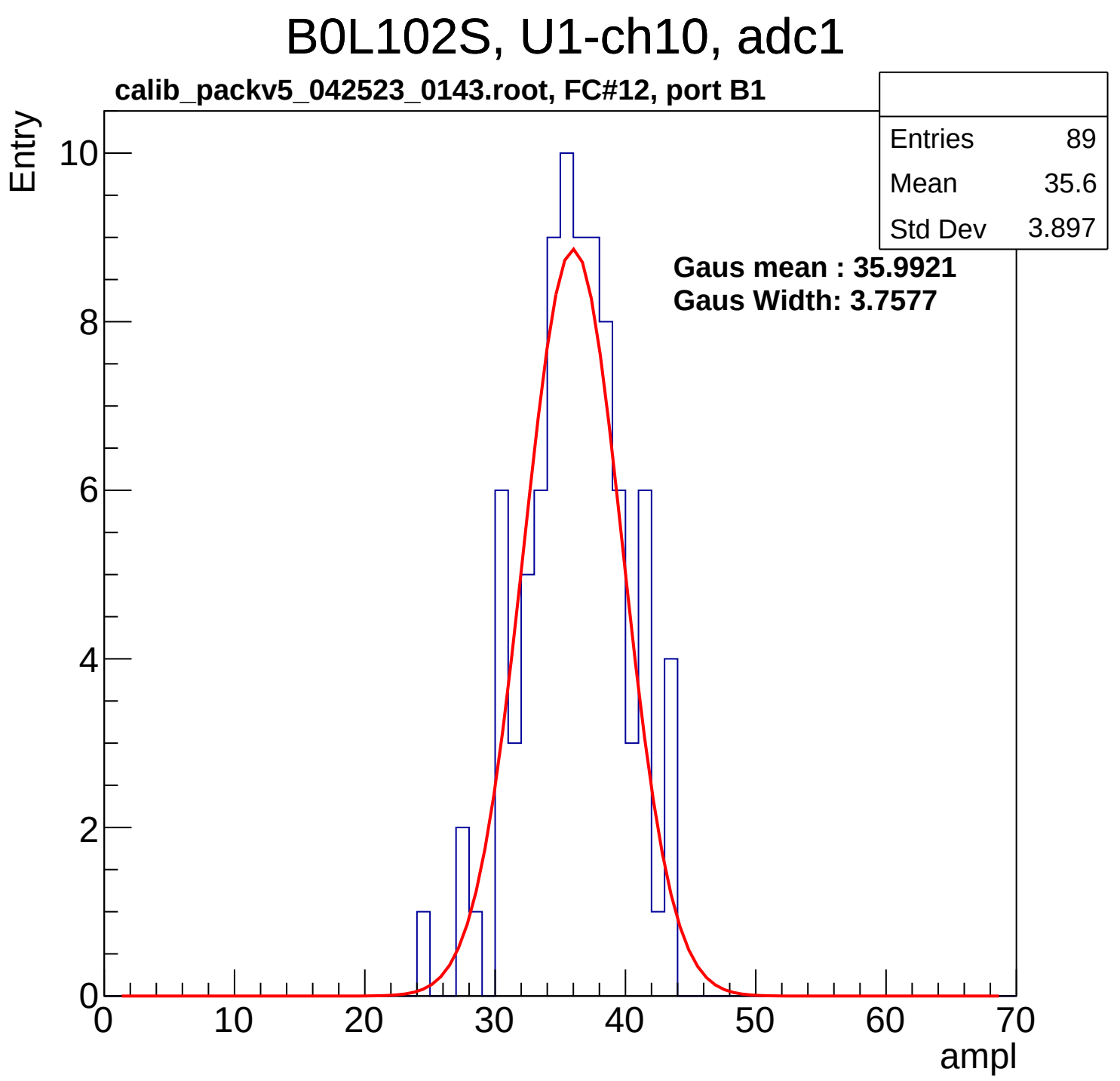
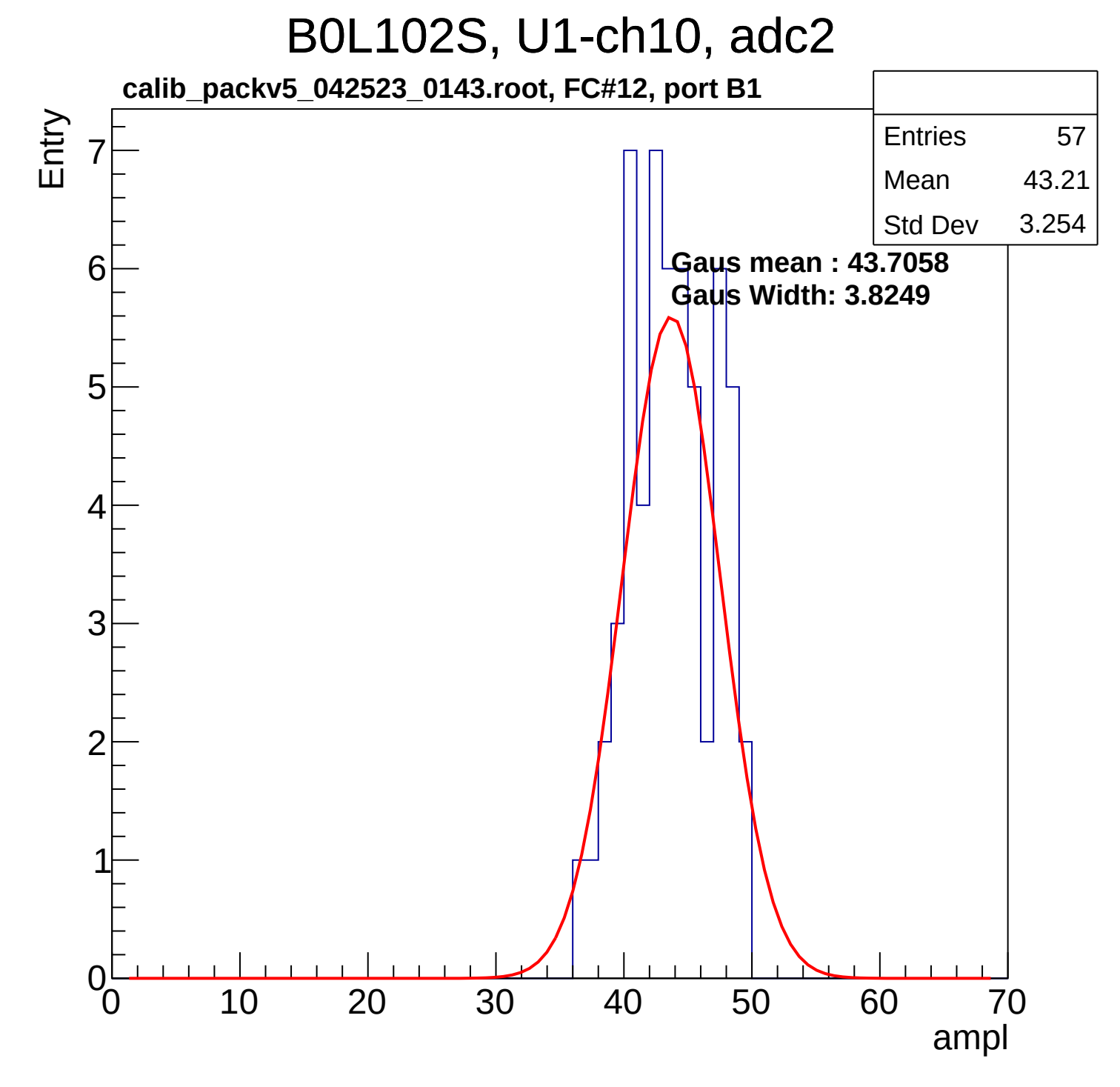
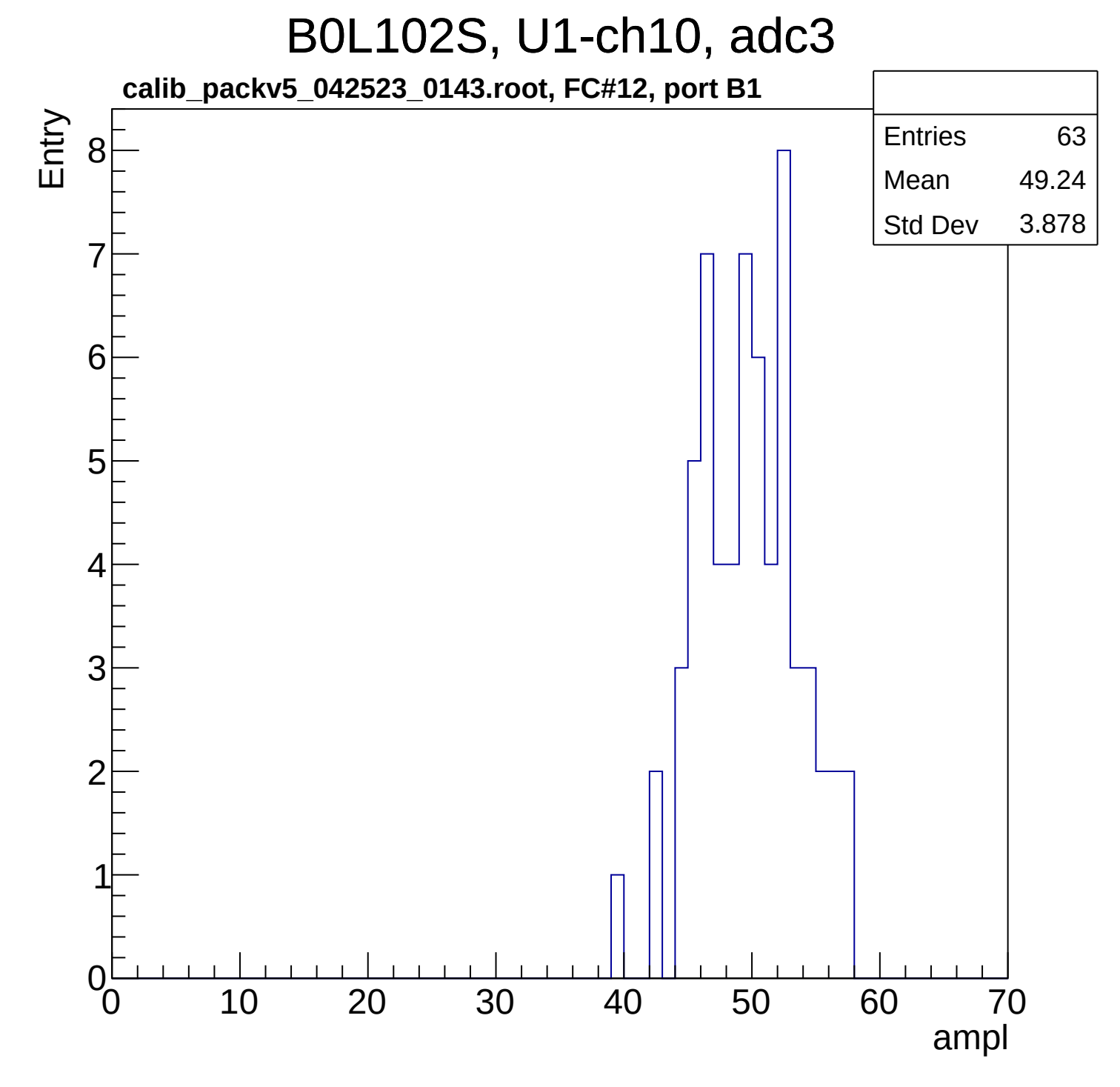
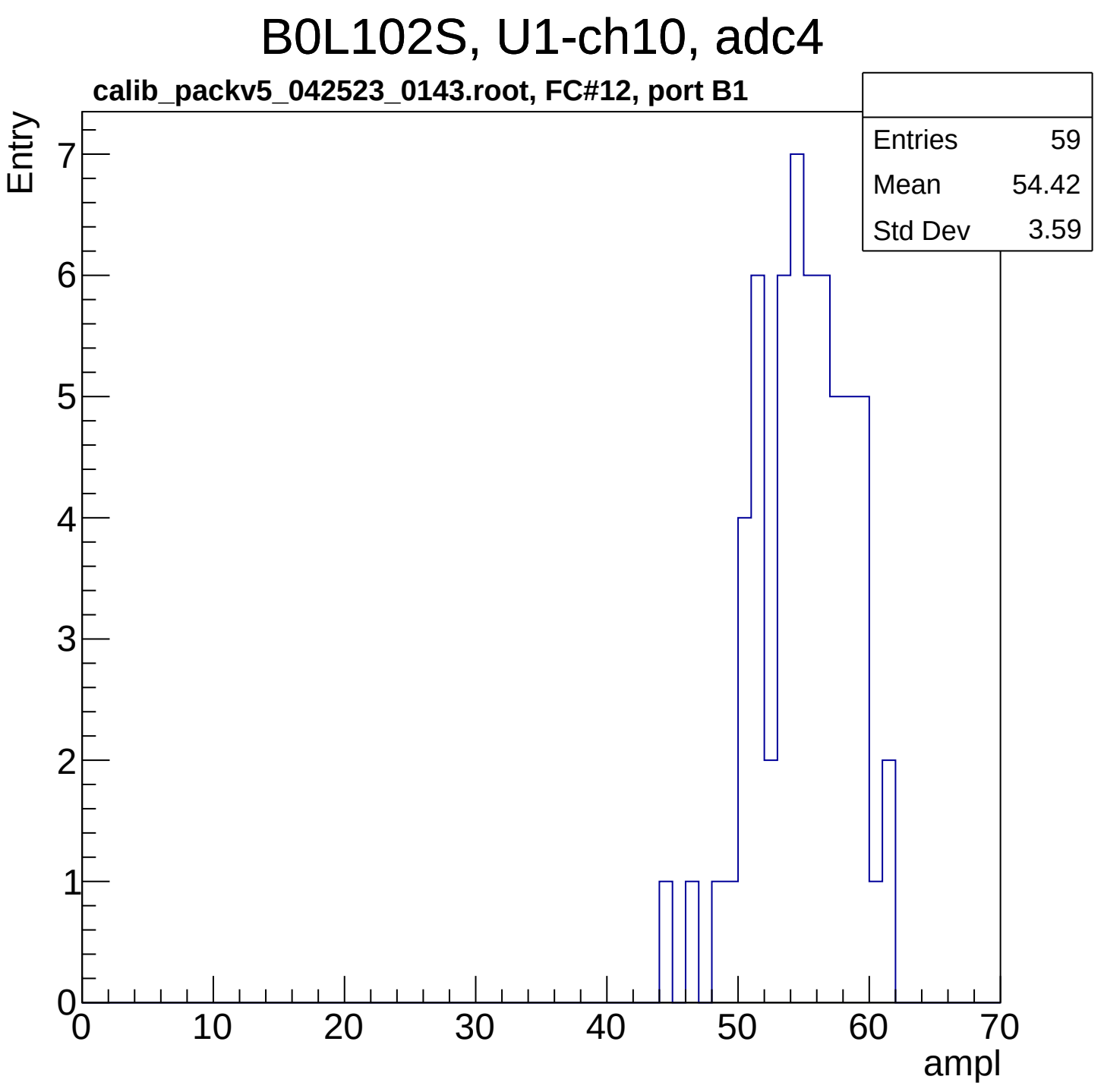
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

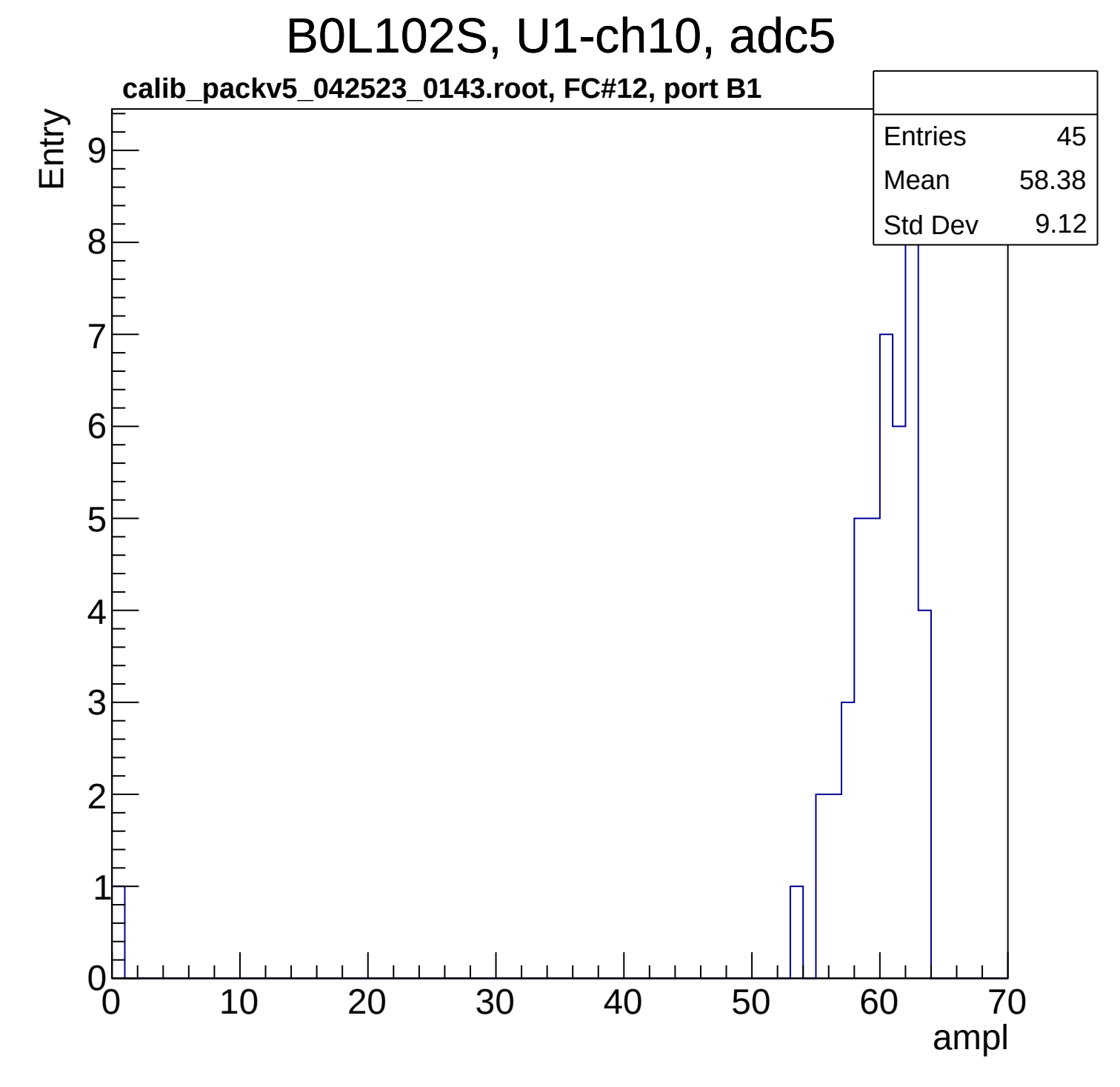

B0L102S, U1-ch10, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 28.24
Std Dev 3.299
Gaus mean : 30.8400
Gaus Width: 5.2101
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch10, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 35.6
Std Dev 3.897
Gaus mean : 35.9921
Gaus Width: 3.7577
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch10, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.21
Std Dev 3.254
Gaus mean : 43.7058
Gaus Width: 3.8249
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch10, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 49.24
Std Dev 3.878
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch10, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 54.42
Std Dev 3.59
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch10, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.38
Std Dev 9.12
Entry
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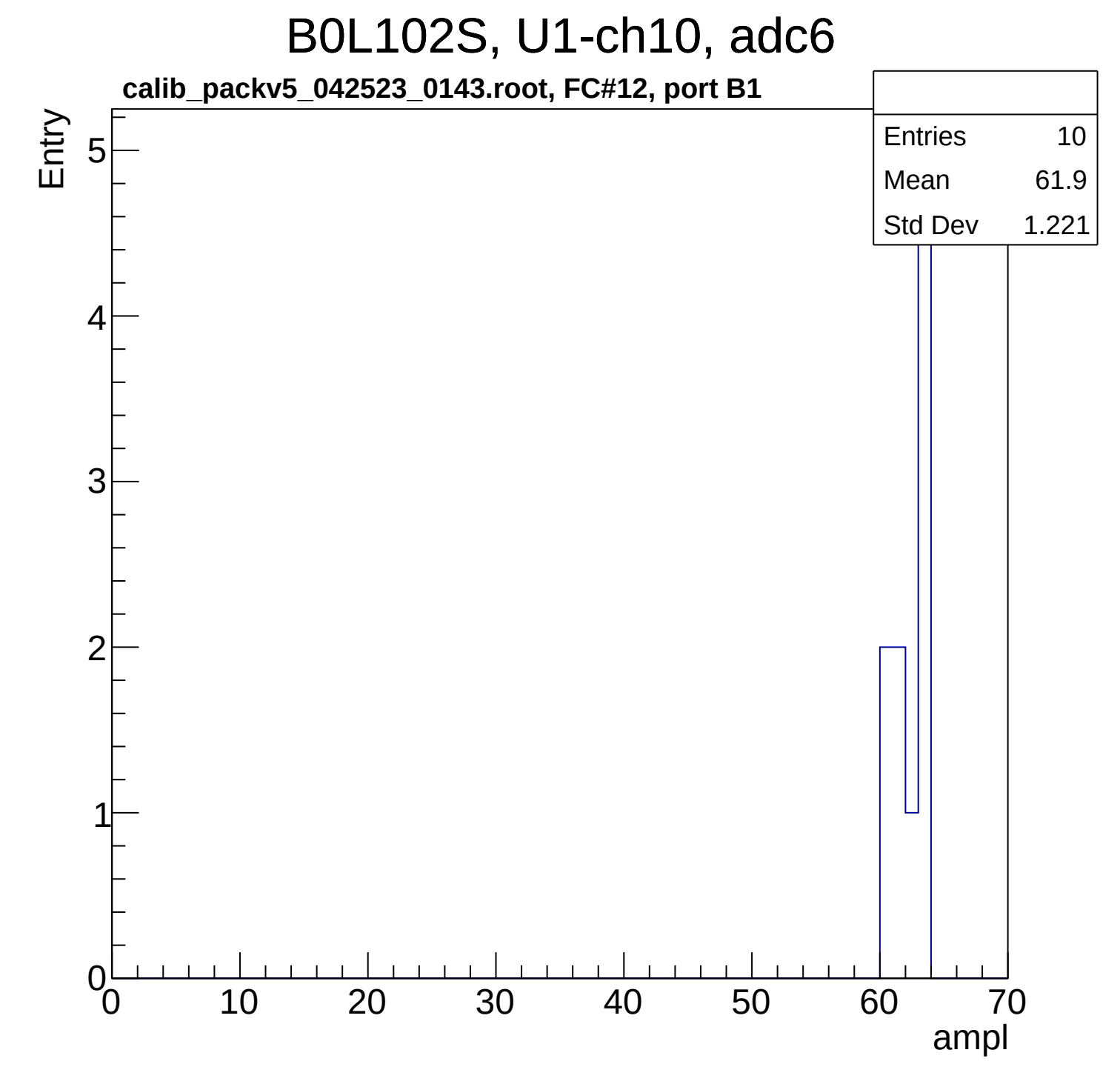
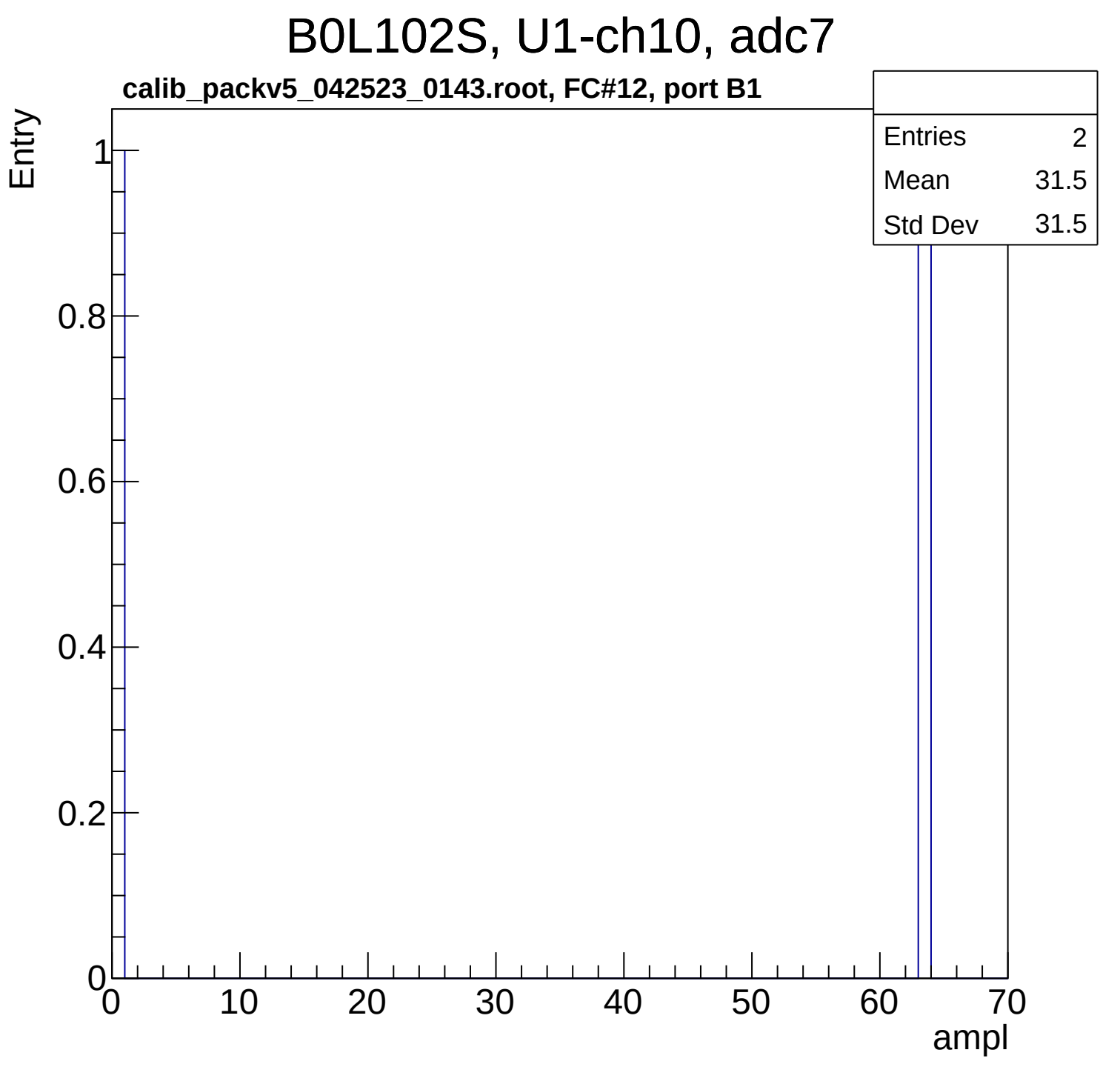
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch10, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 61.9
Std Dev 1.221
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch10, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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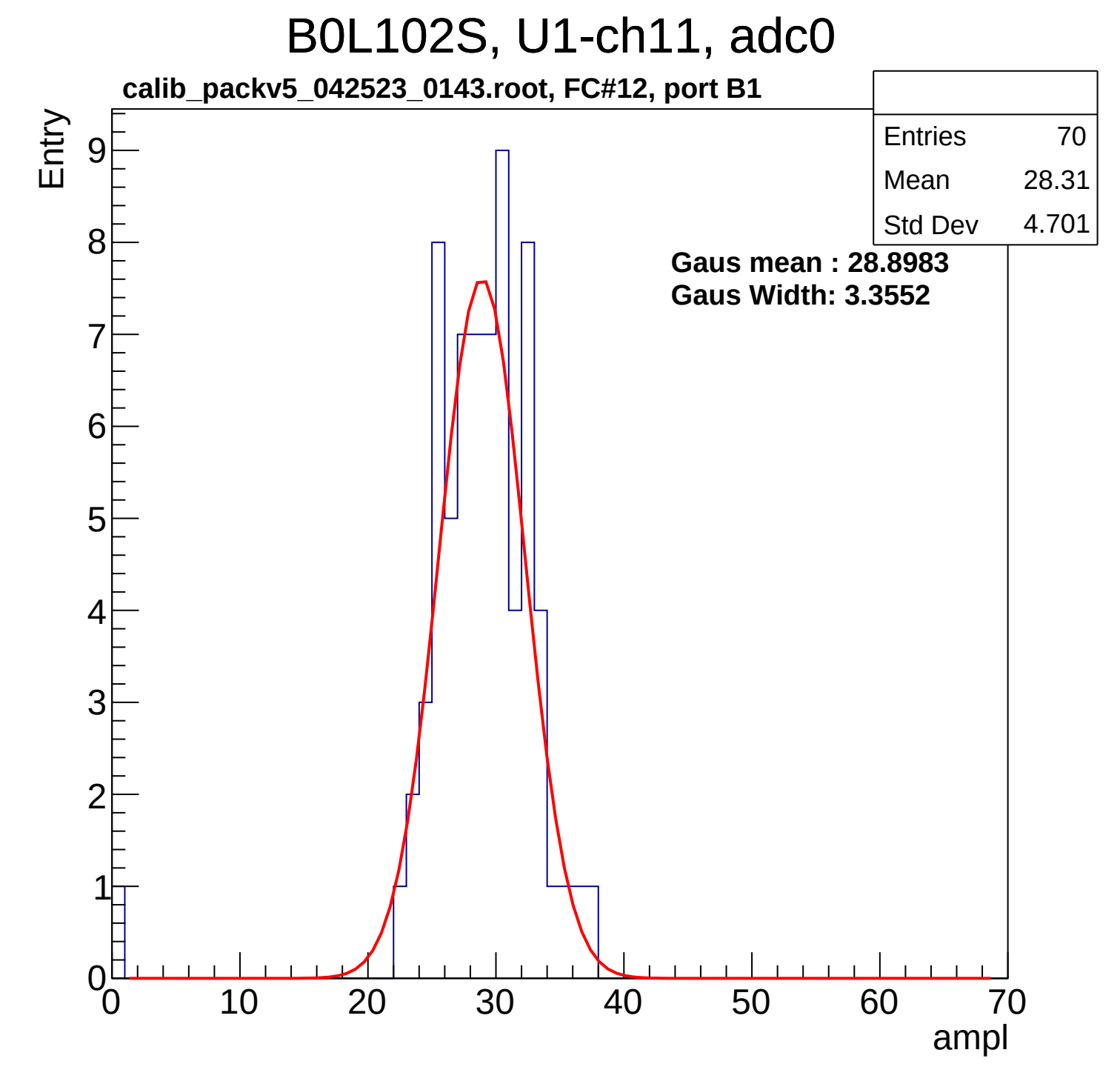
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch11, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 28.31
Std Dev 4.701
Gaus mean : 28.8983
Gaus Width: 3.3552
Entry
ampl
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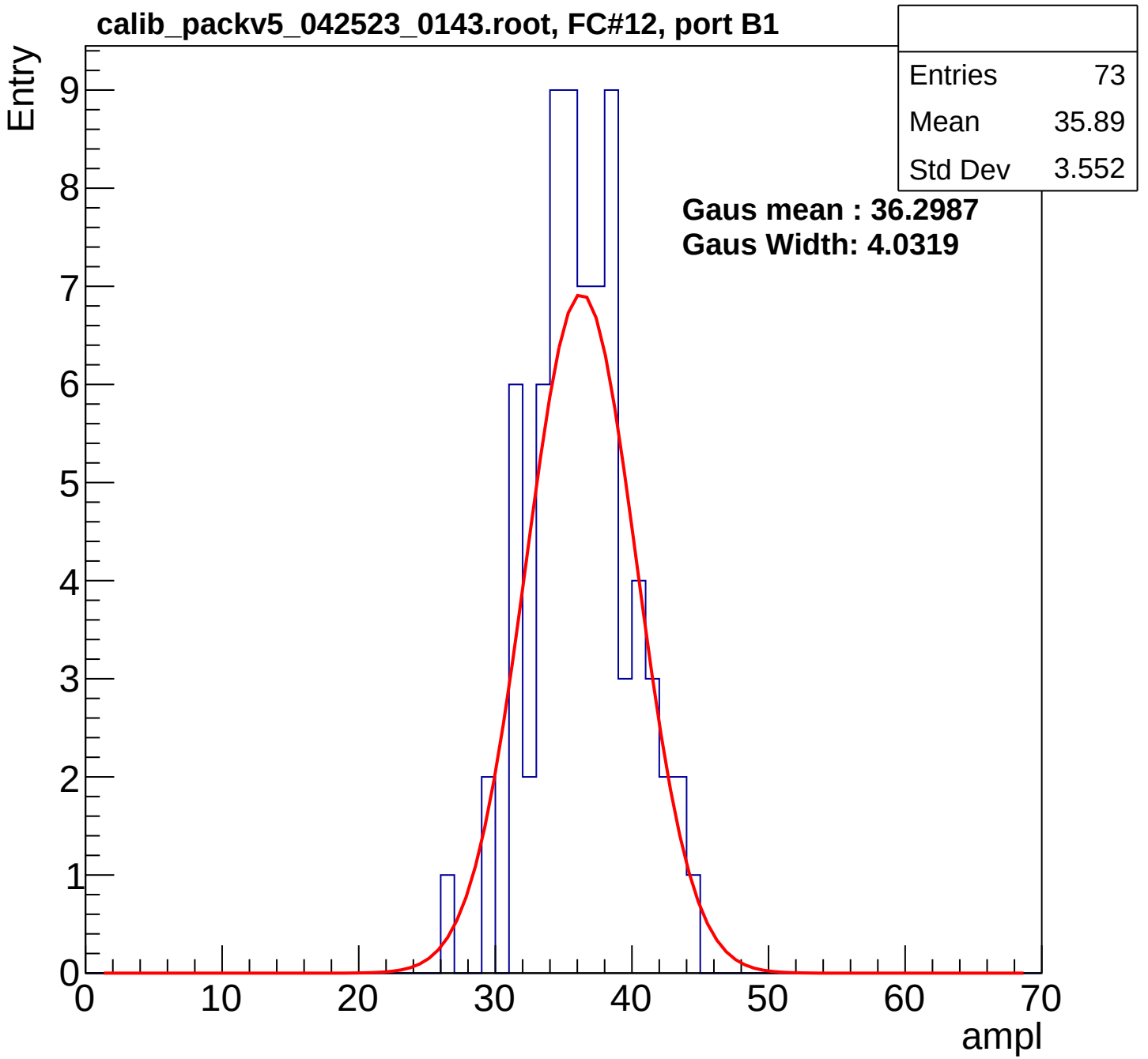
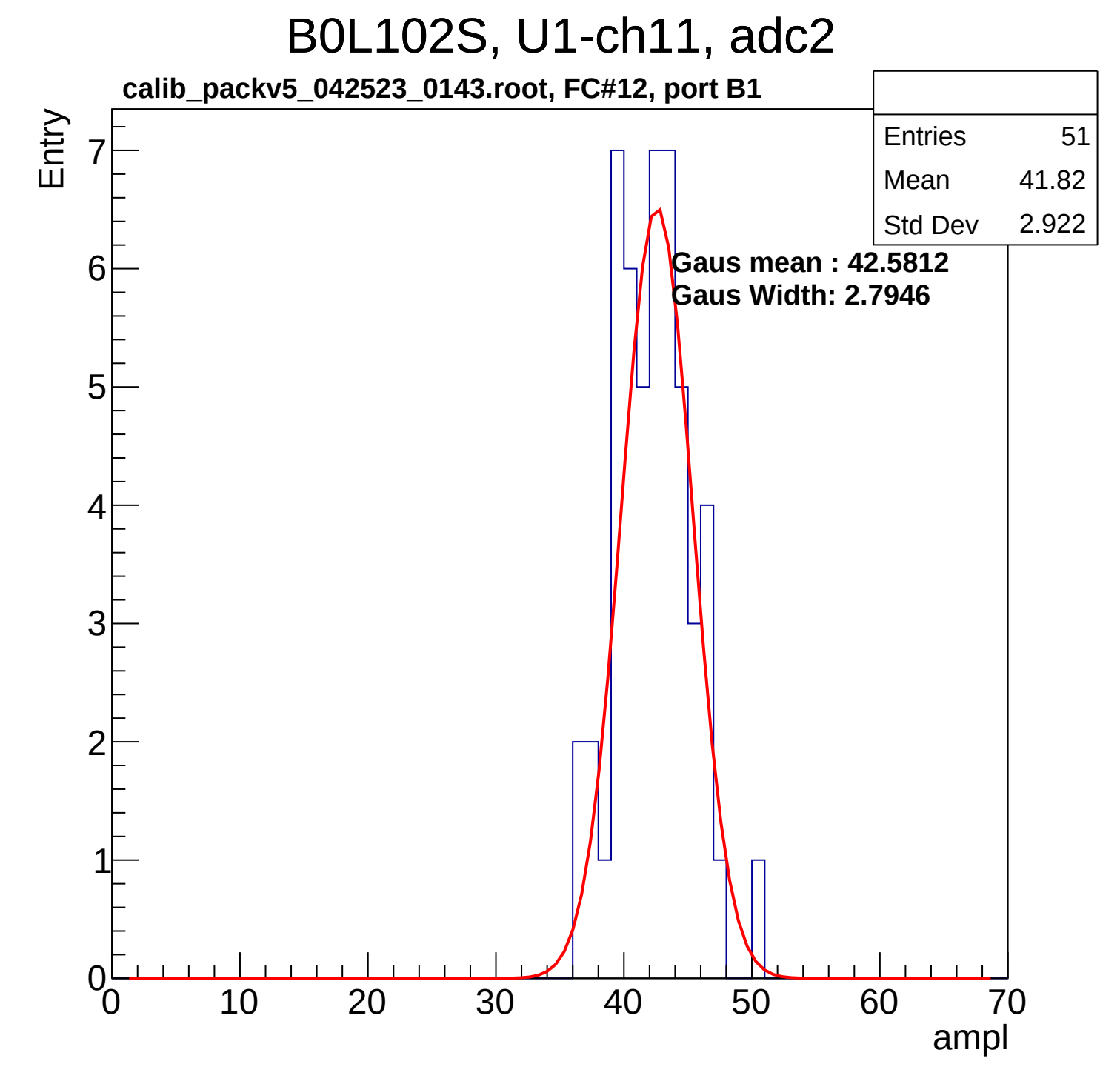
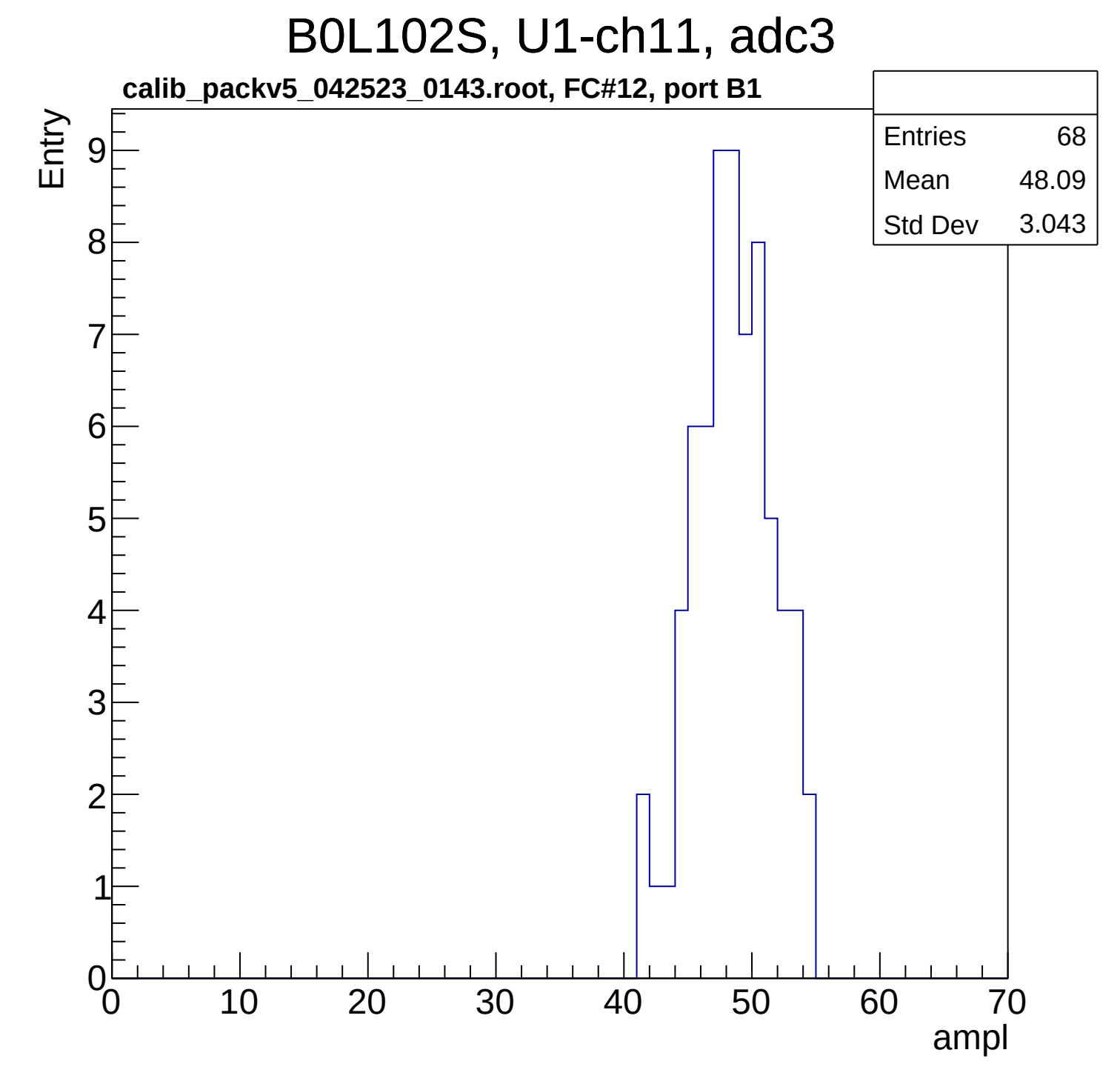
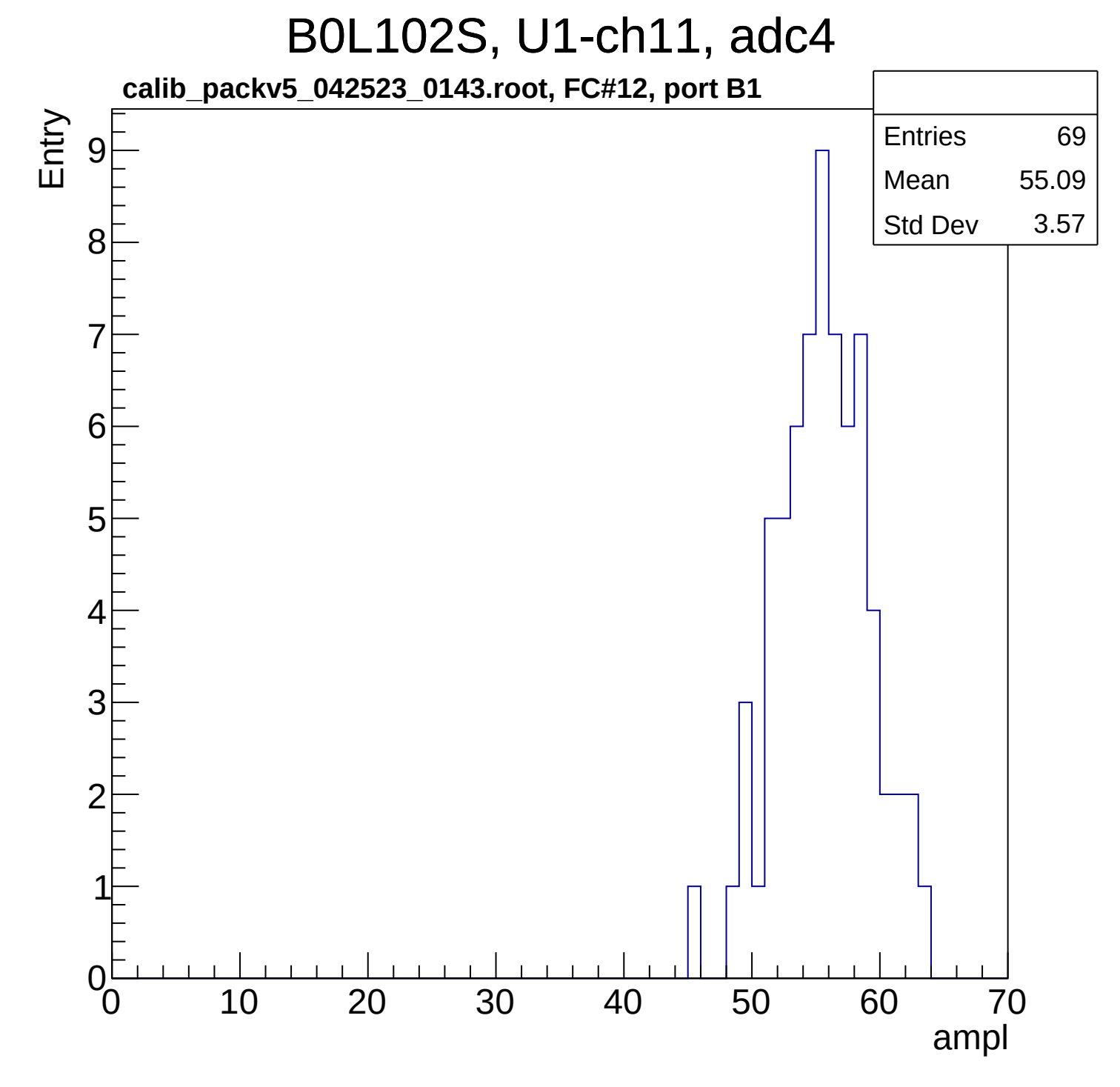
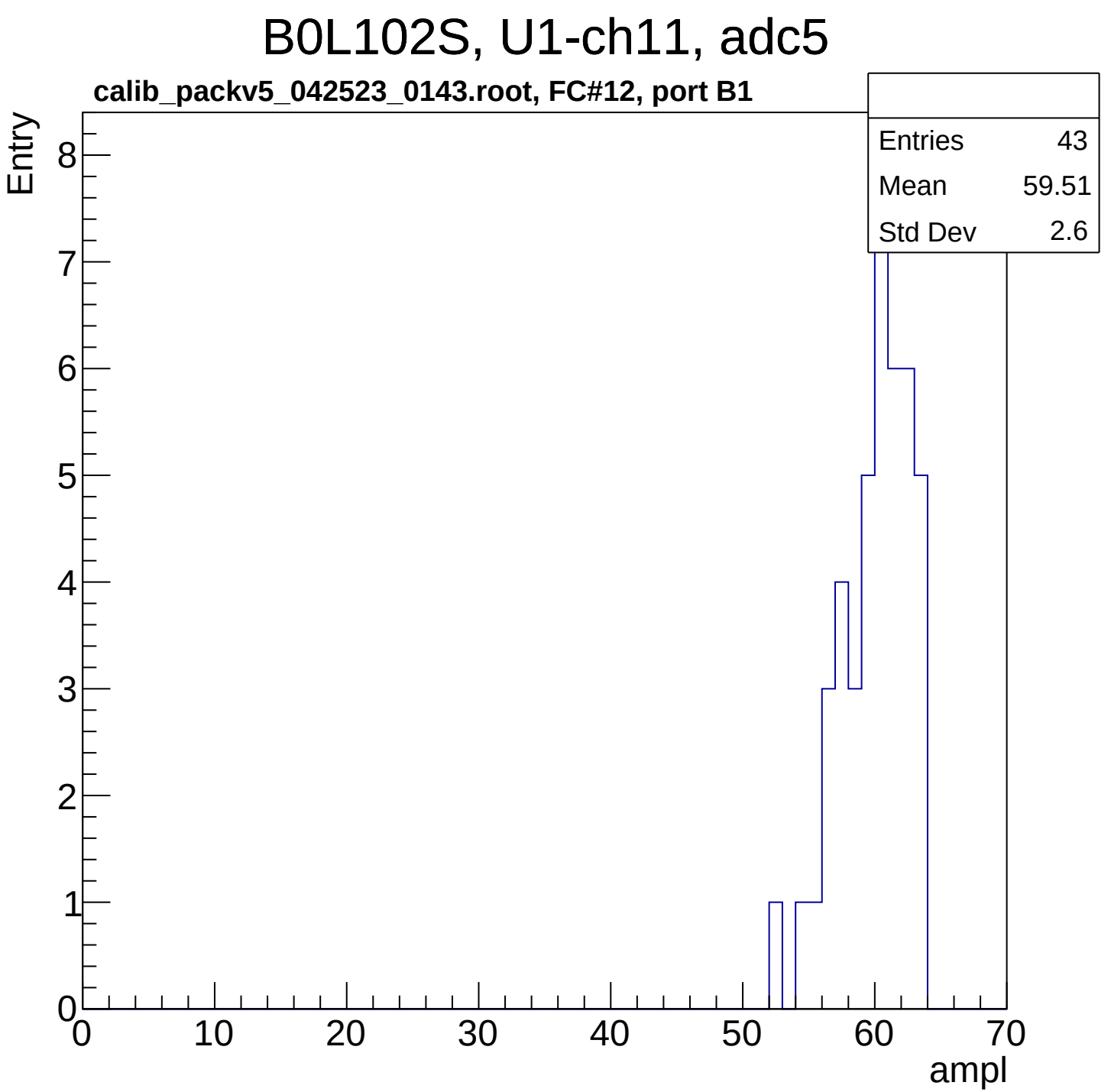
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

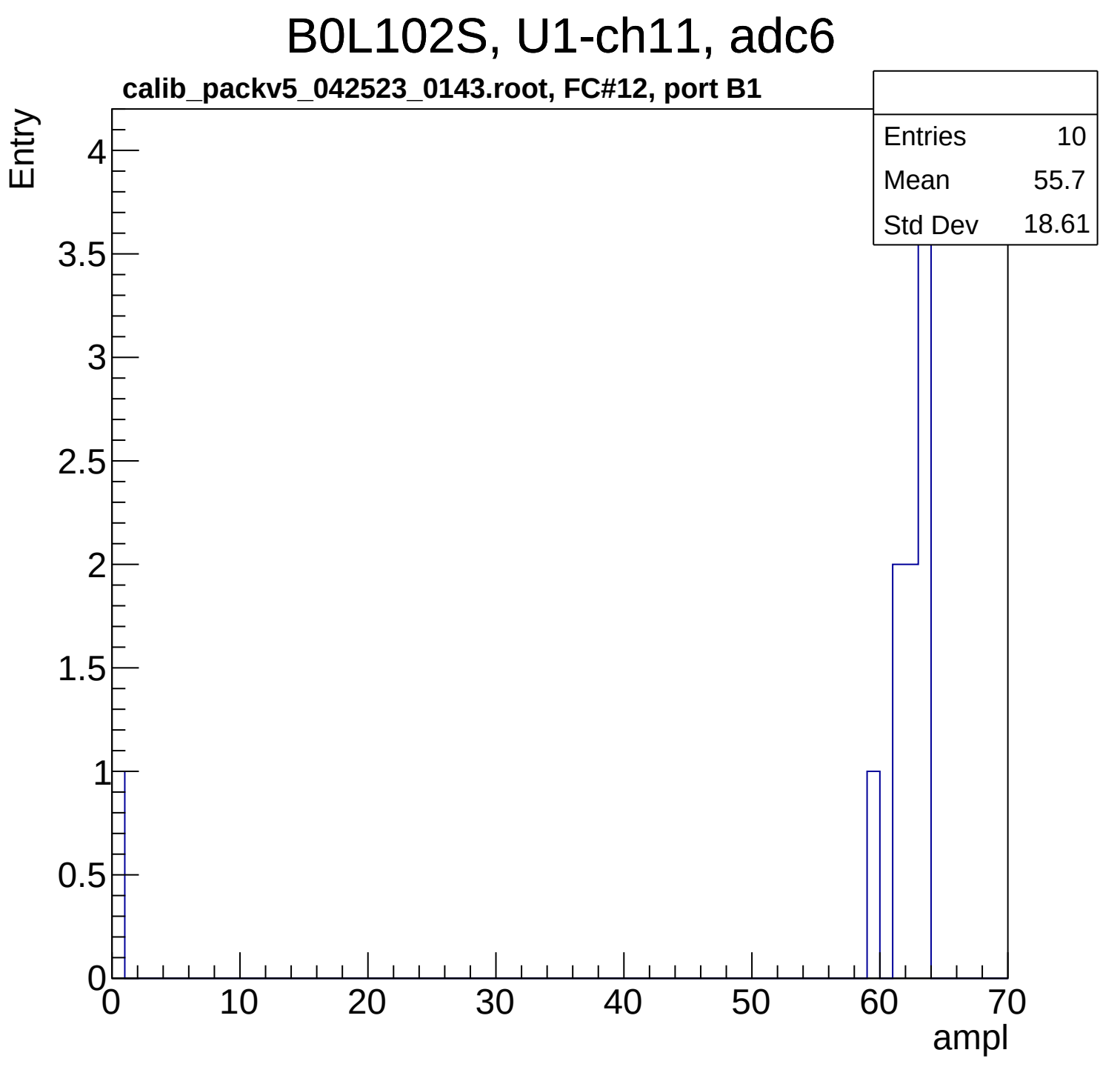

B0L102S, U1-ch11, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 35.89
Std Dev 3.552
Gaus mean : 36.2987
Gaus Width: 4.0319
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

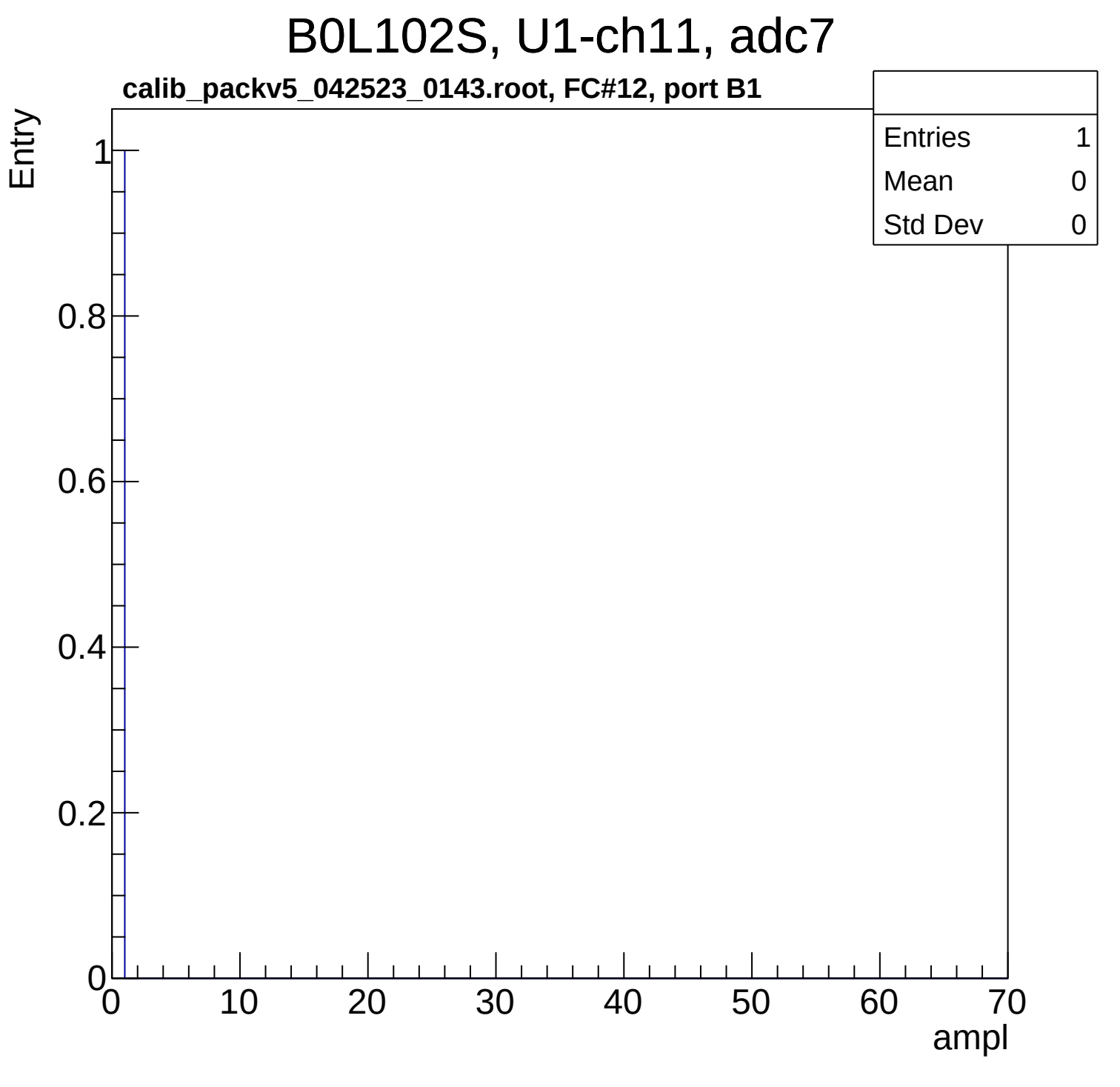

B0L102S, U1-ch11, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 41.82
Std Dev 2.922
Gaus mean : 42.5812
Gaus Width: 2.7946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

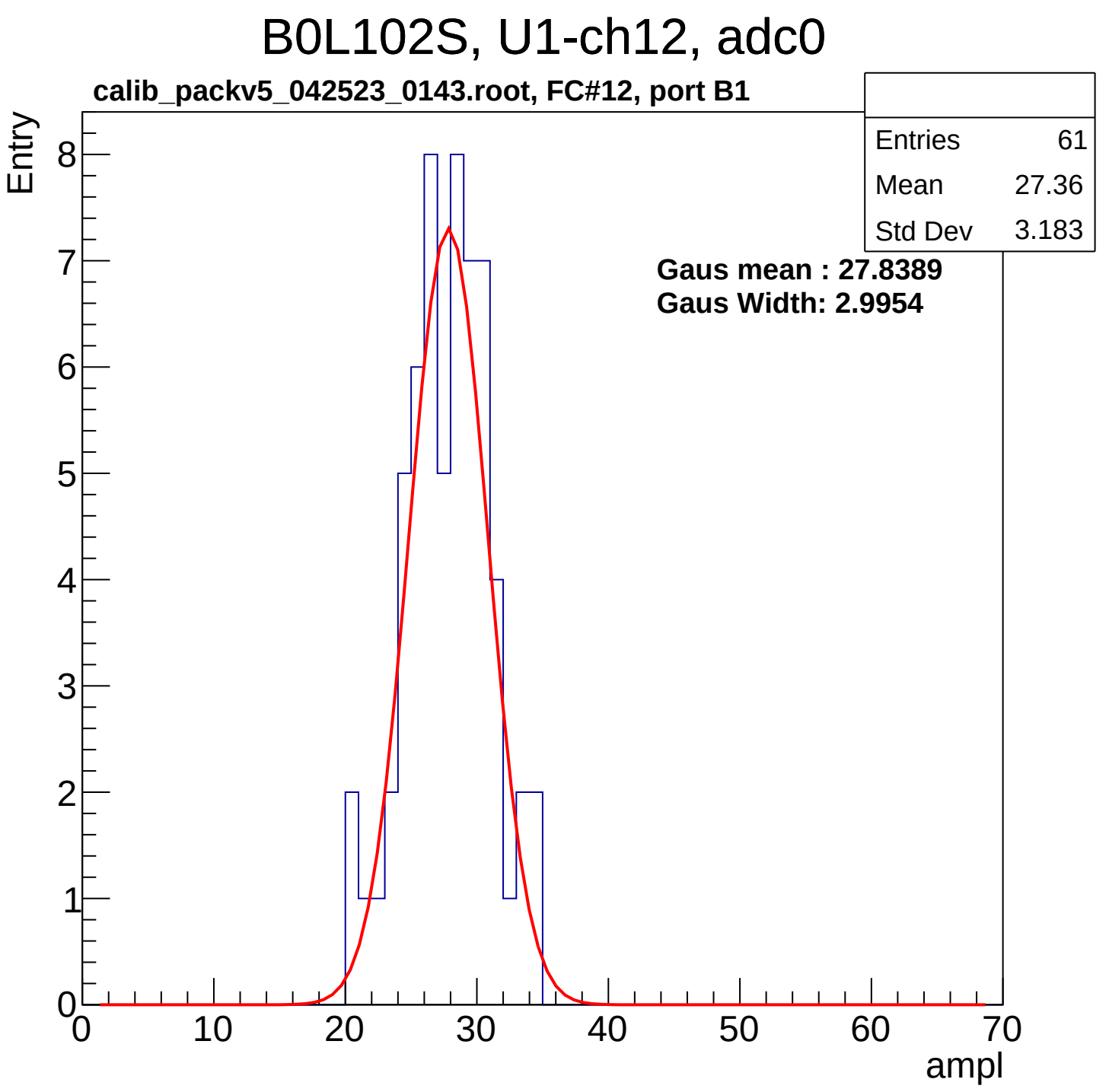

B0L102S, U1-ch11, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.09
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

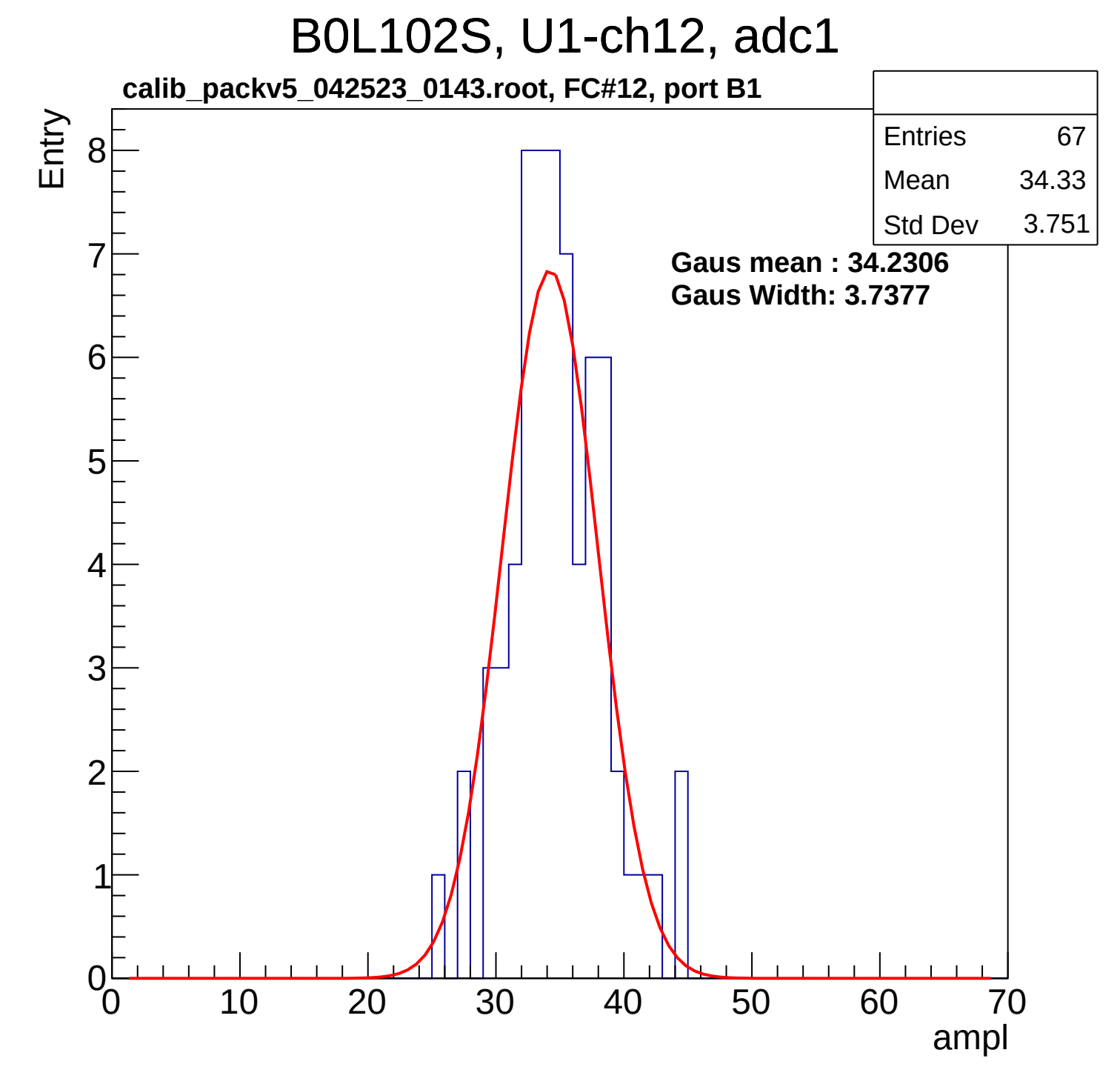

B0L102S, U1-ch11, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 55.09
Std Dev 3.57
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

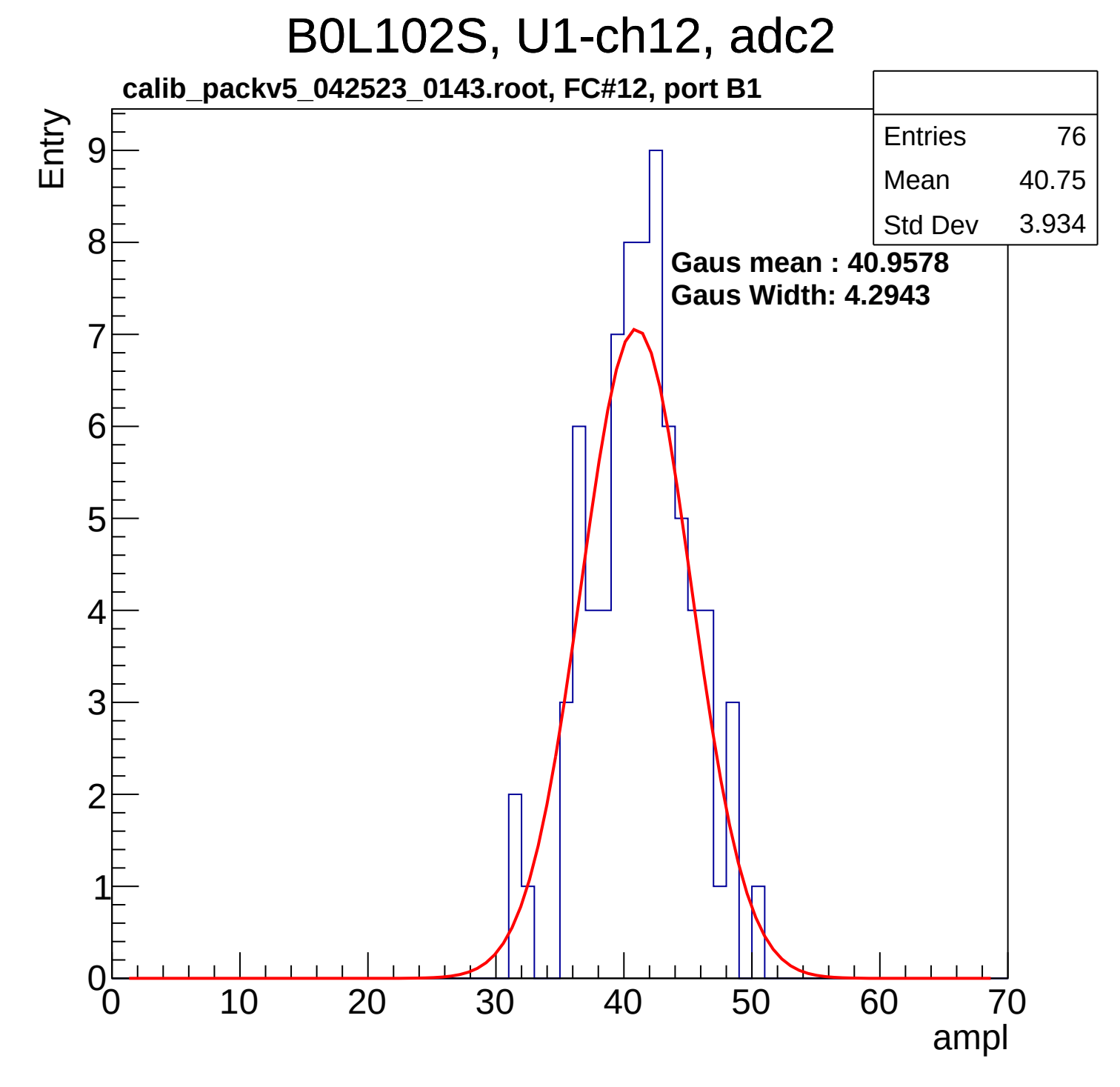

B0L102S, U1-ch11, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.51
Std Dev 2.6
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

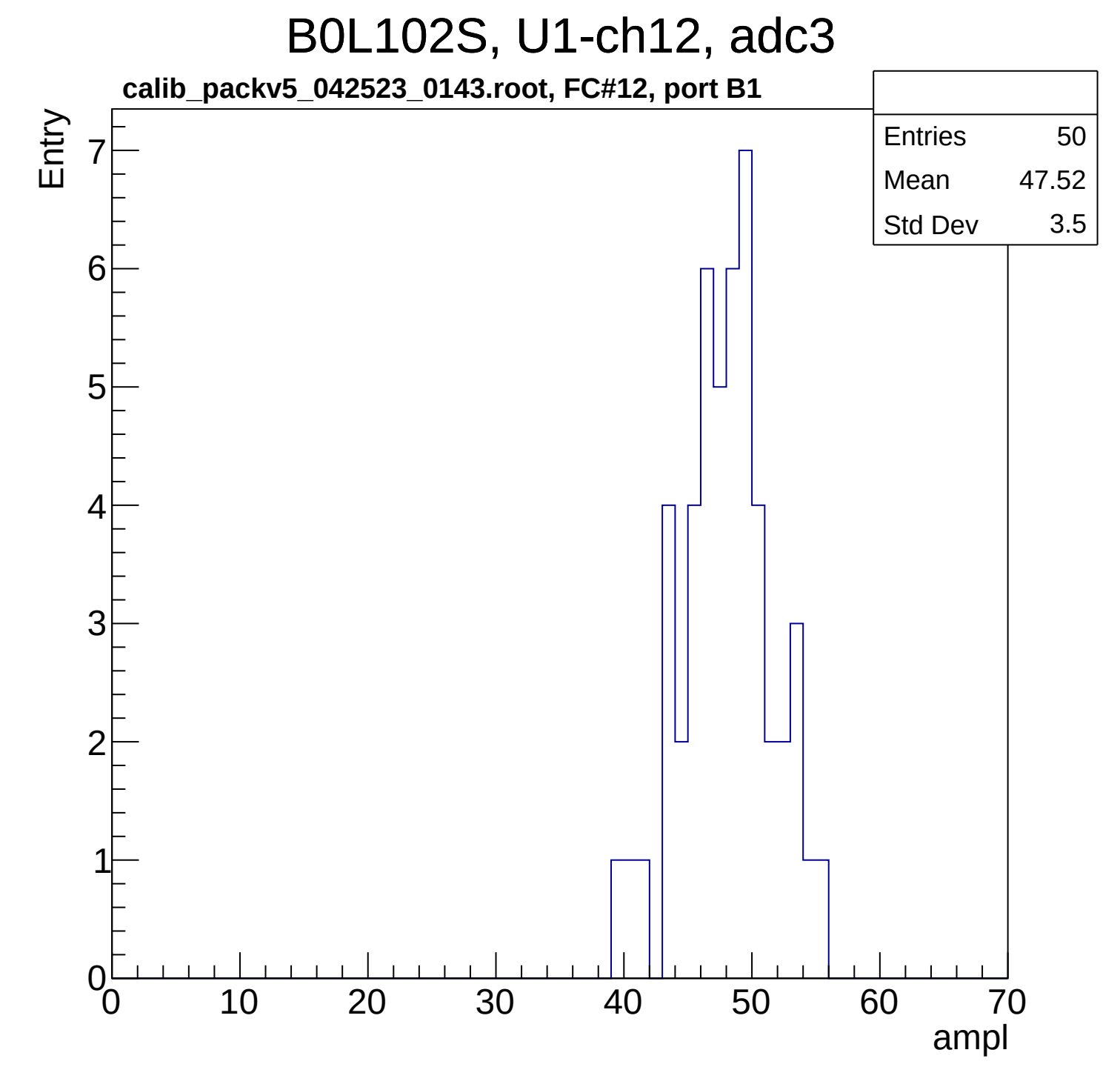

B0L102S, U1-ch11, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 55.7
Std Dev 18.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

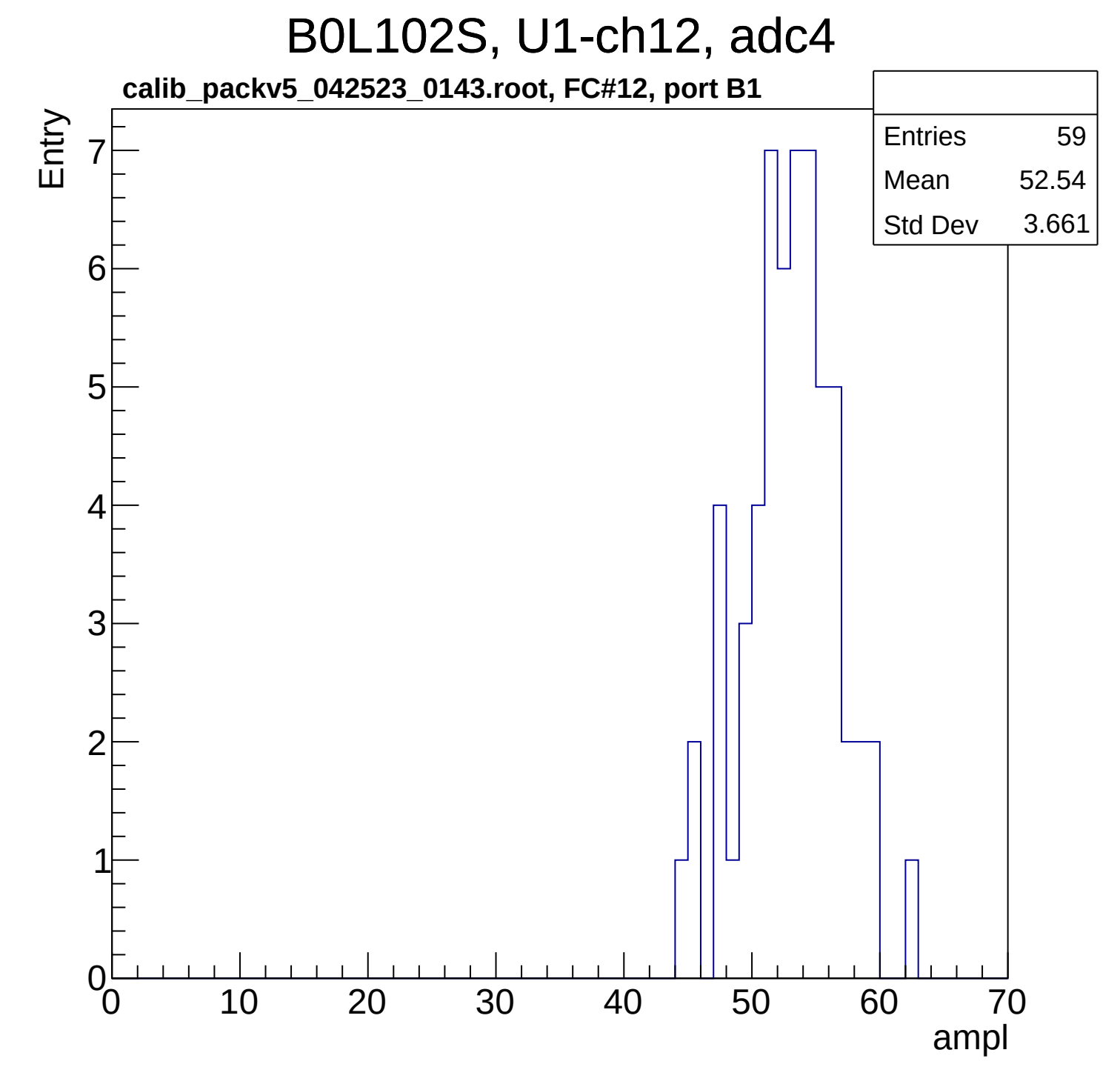

B0L102S, U1-ch11, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

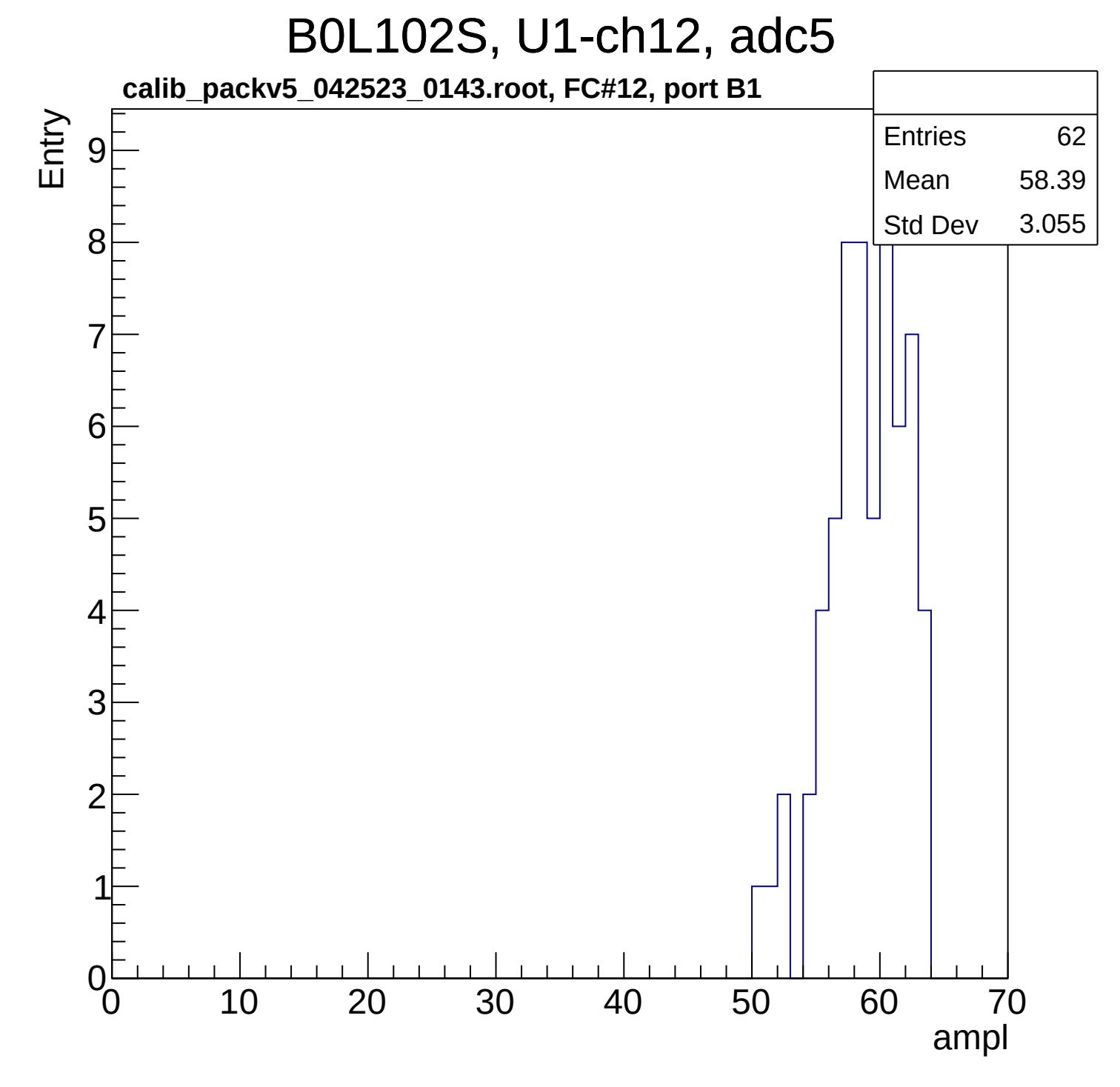

B0L102S, U1-ch12, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 27.36
Std Dev 3.183
Gaus mean : 27.8389
Gaus Width: 2.9954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

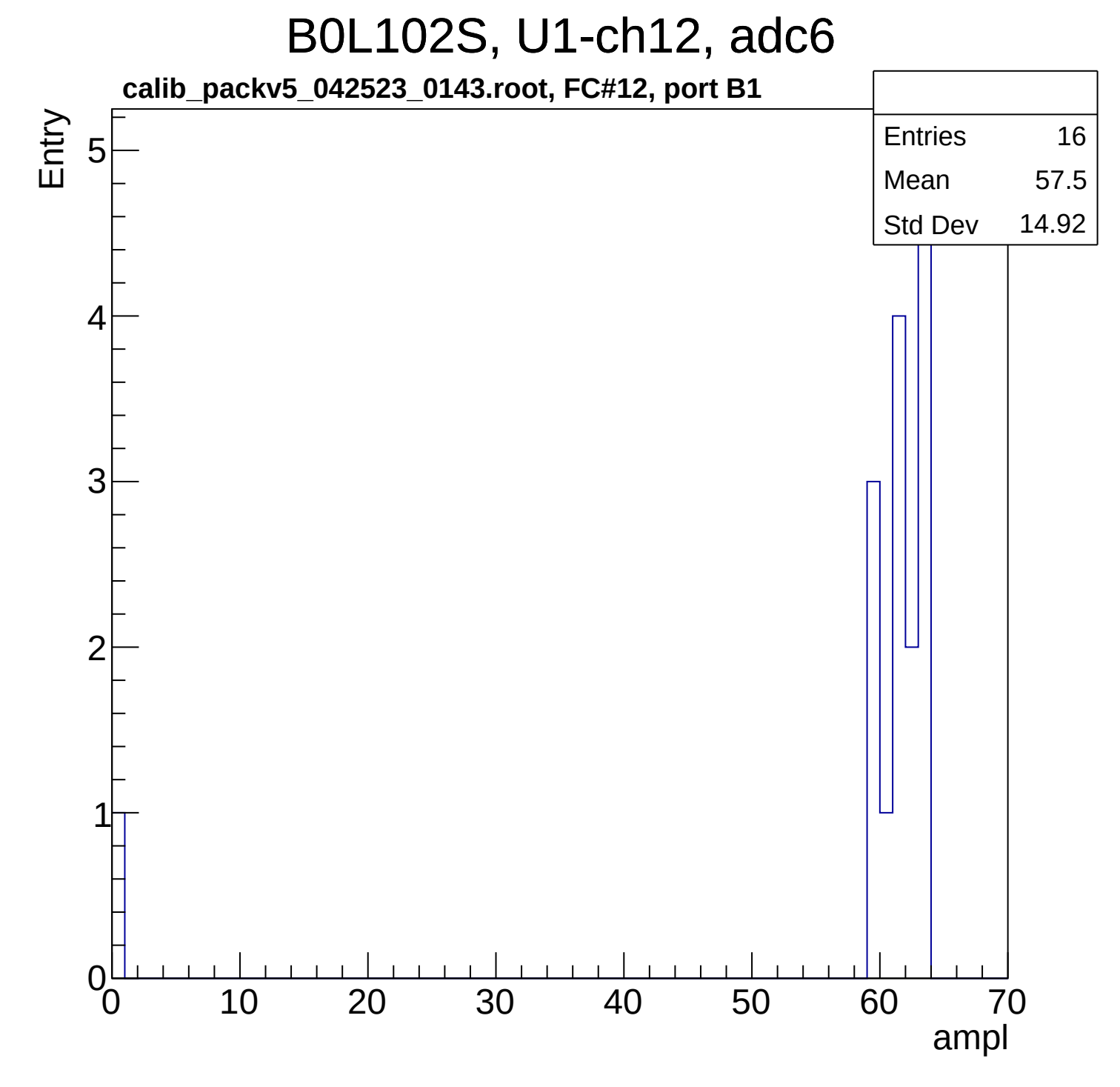

B0L102S, U1-ch12, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 34.33
Std Dev 3.751
Gaus mean : 34.2306
Gaus Width: 3.7377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

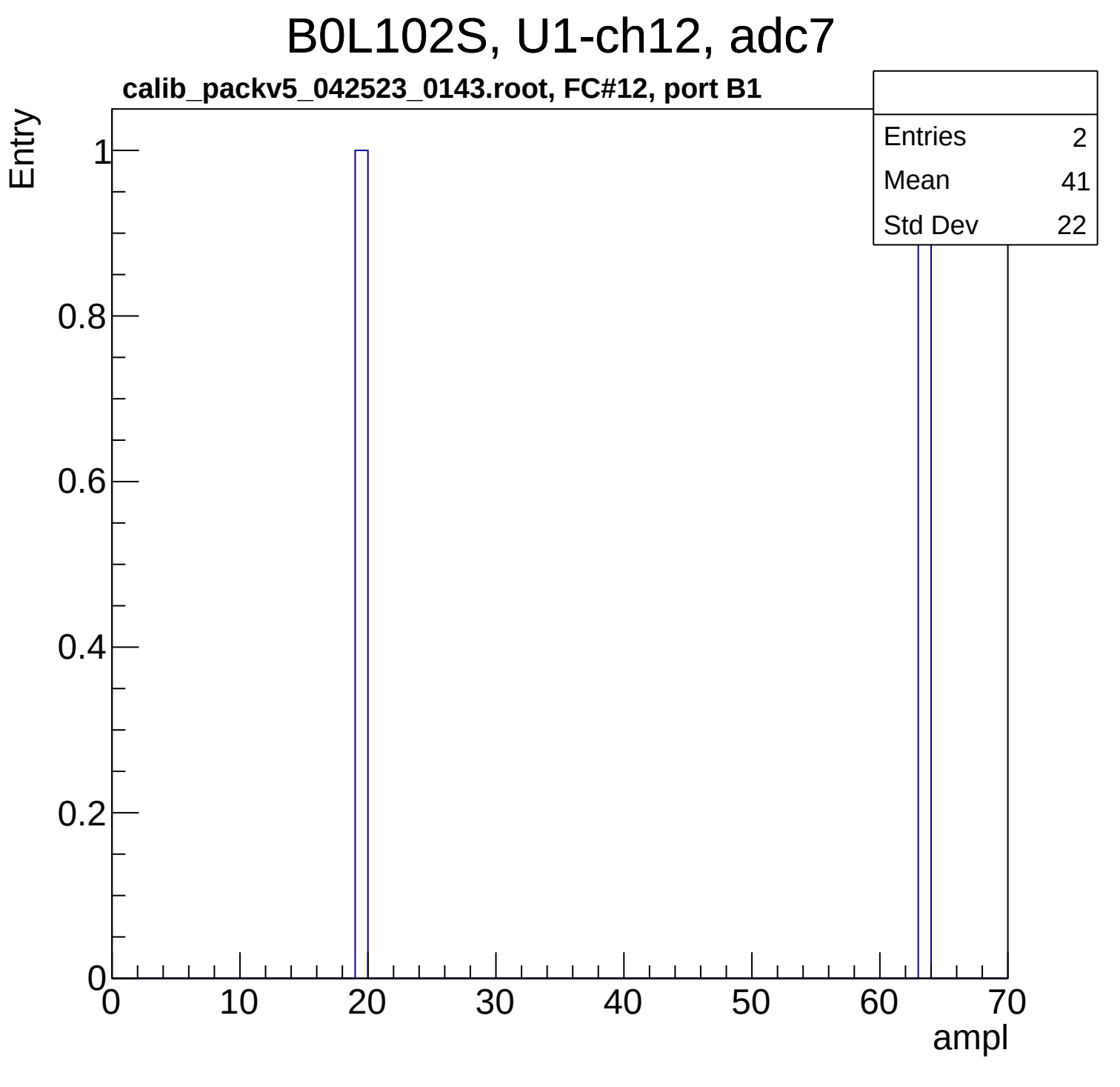

B0L102S, U1-ch12, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 40.75
Std Dev 3.934
Gaus mean : 40.9578
Gaus Width: 4.2943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

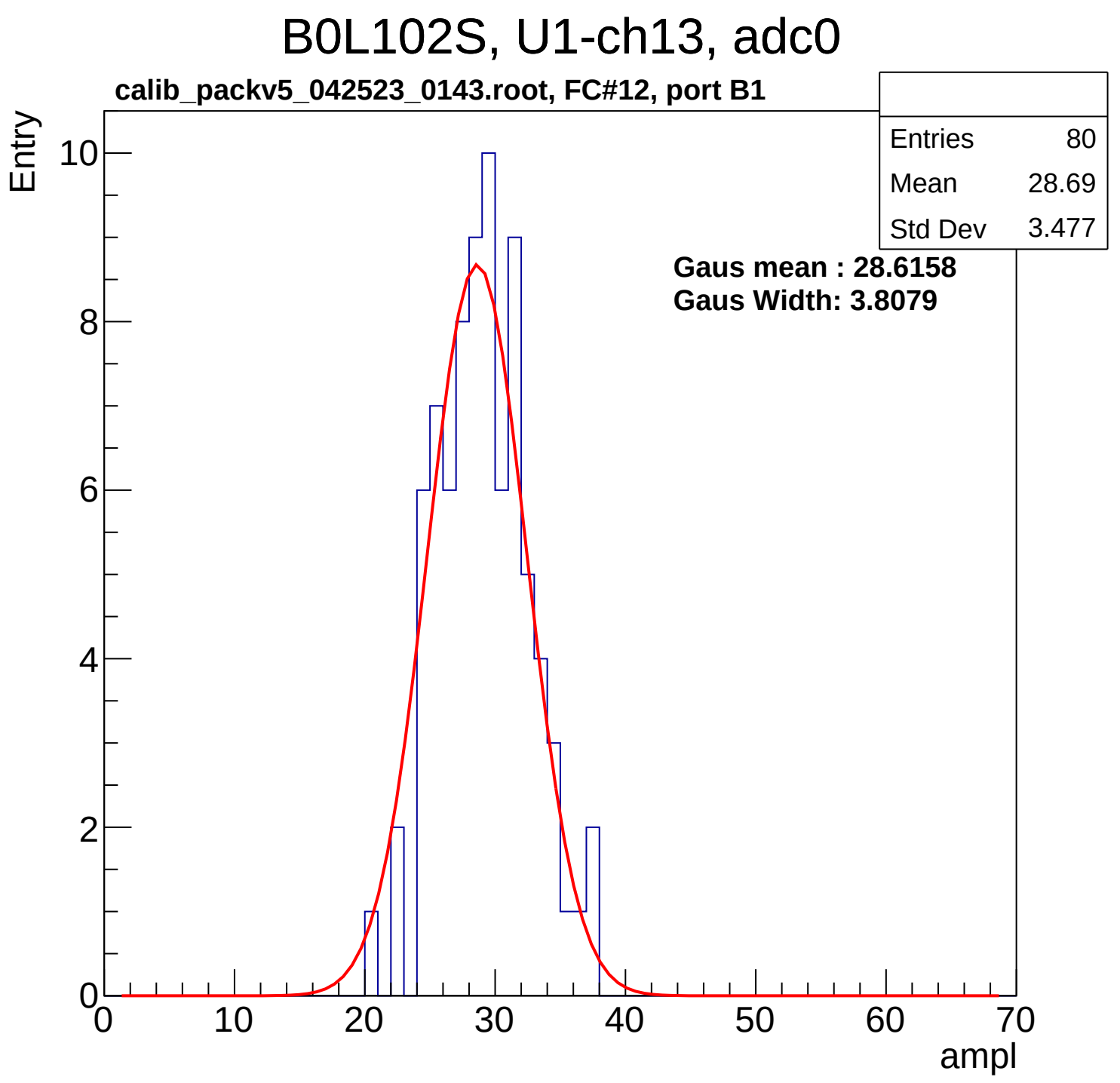

B0L102S, U1-ch12, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 47.52
Std Dev 3.5
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

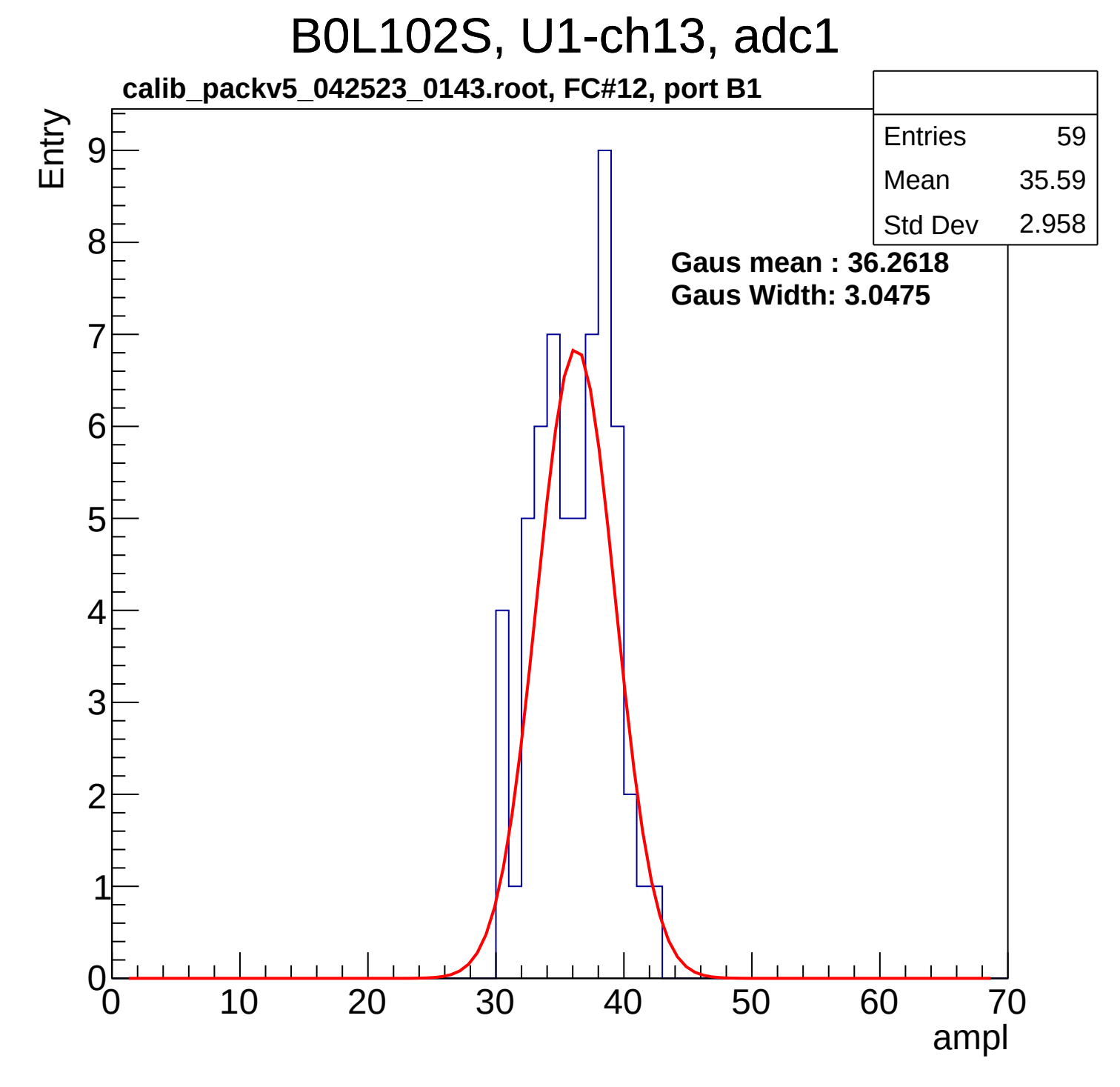

B0L102S, U1-ch12, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 52.54
Std Dev 3.661
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

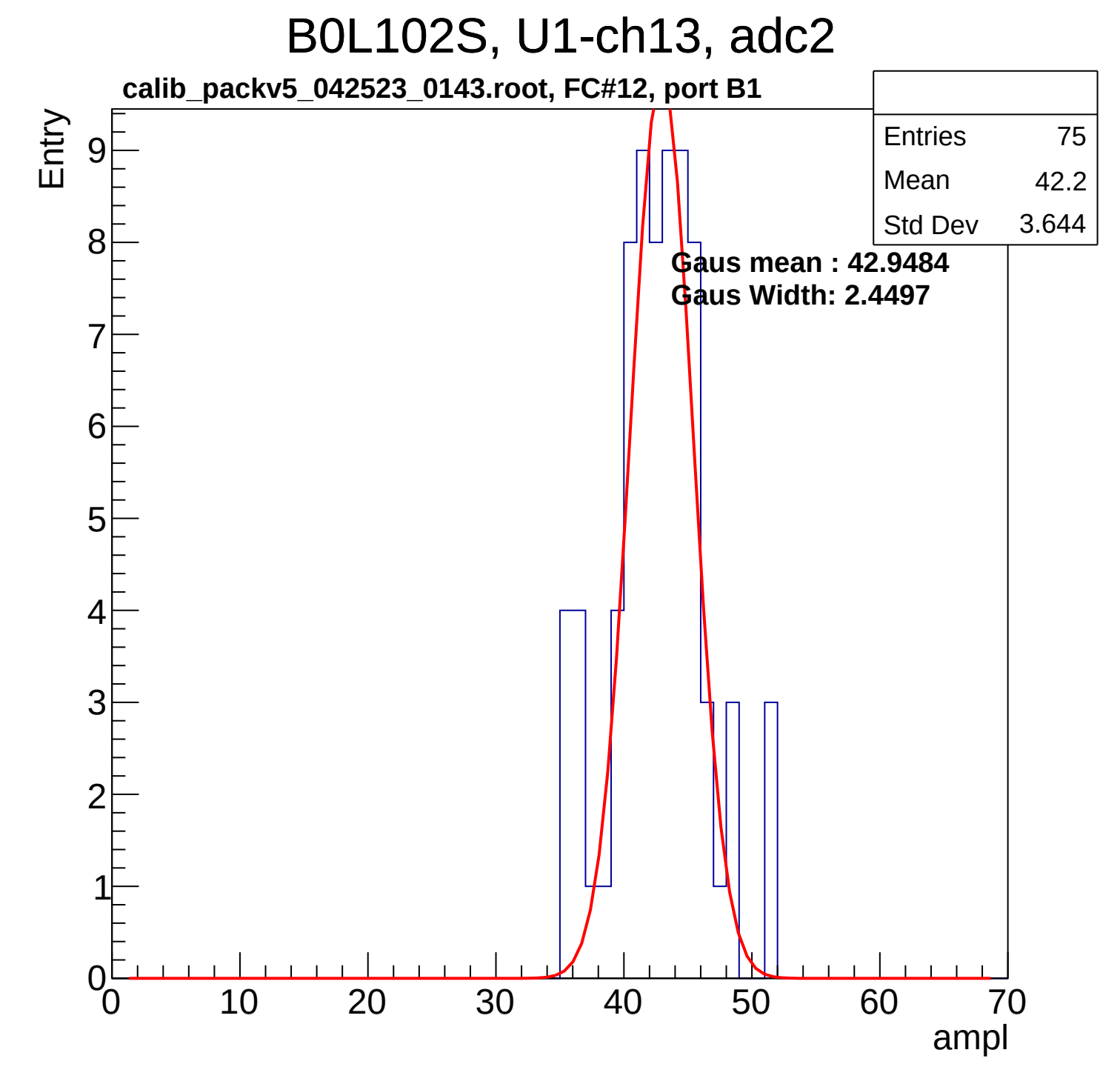

B0L102S, U1-ch12, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 58.39
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

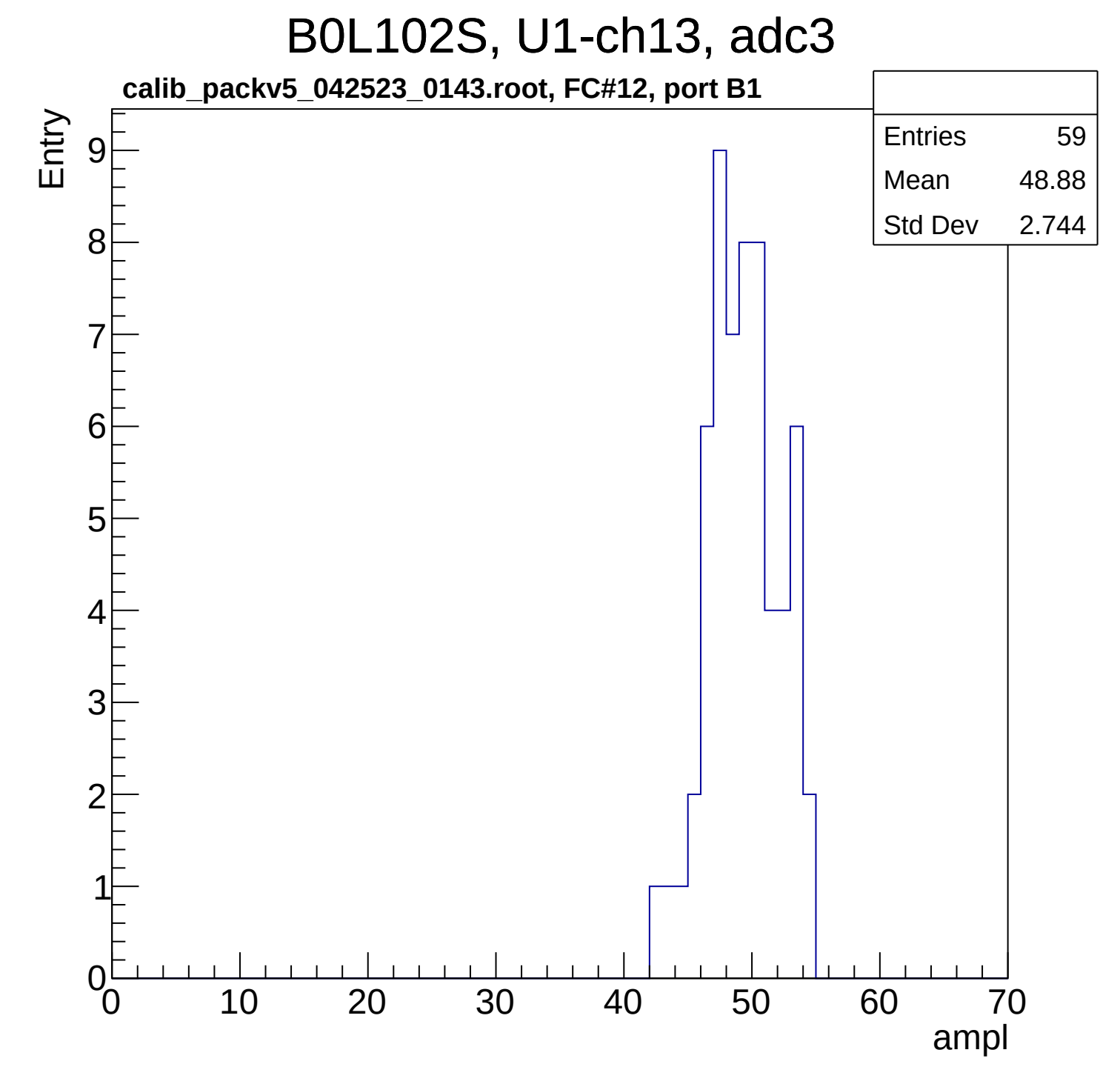

B0L102S, U1-ch12, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 57.5
Std Dev 14.92
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

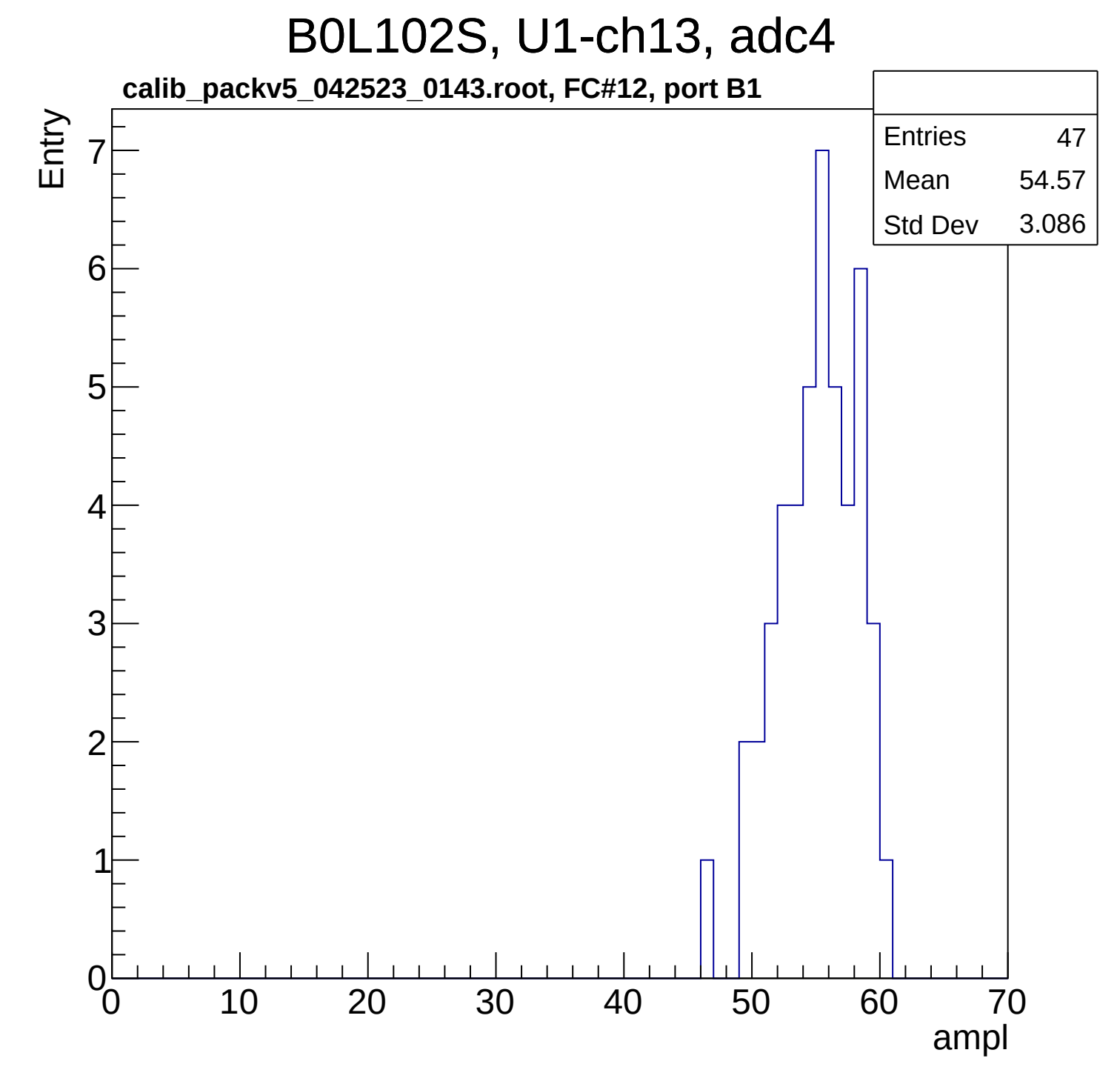

B0L102S, U1-ch12, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 41
Std Dev 22
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

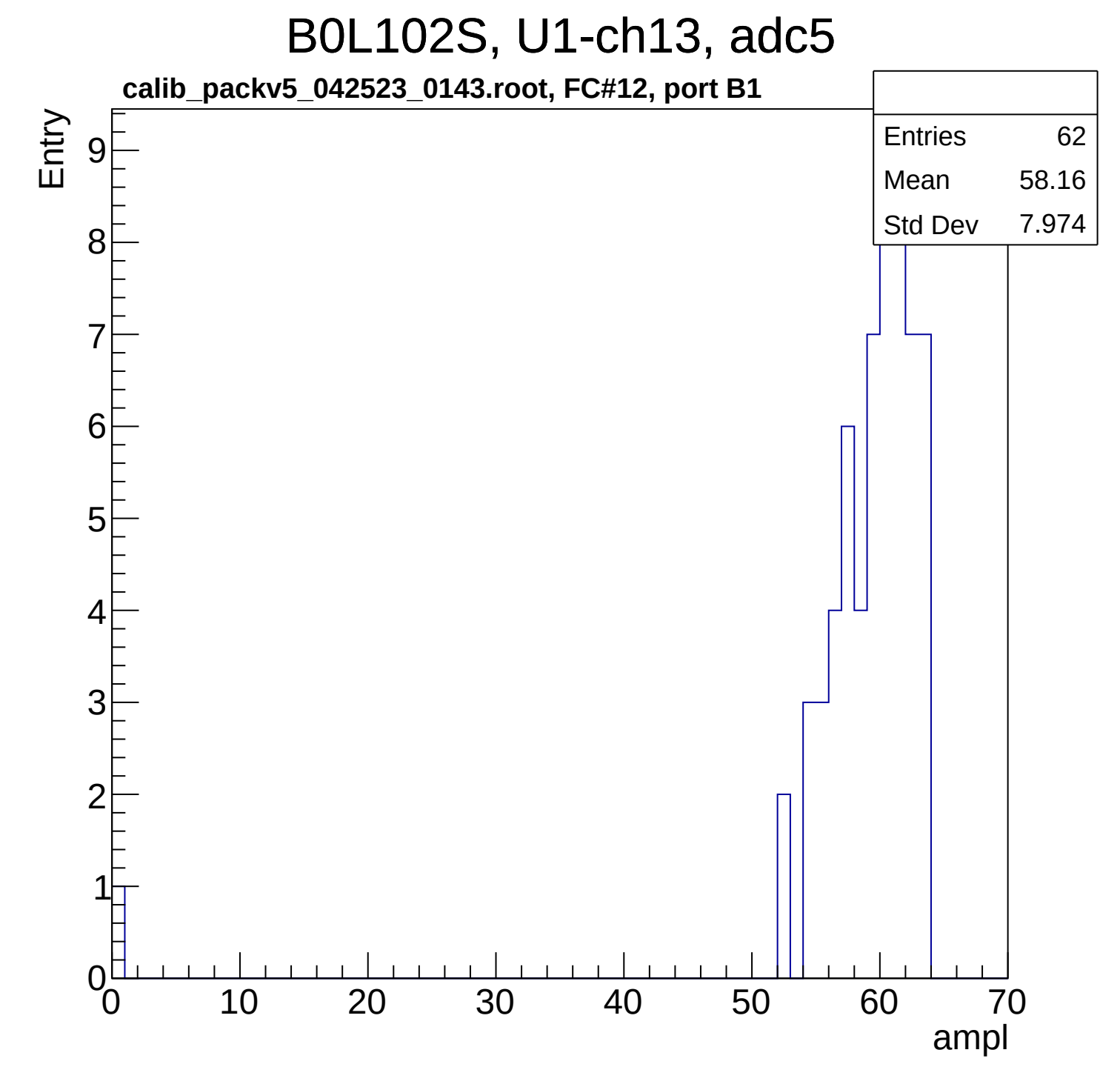

B0L102S, U1-ch13, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 28.69
Std Dev 3.477
Gaus mean : 28.6158
Gaus Width: 3.8079
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch13, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 35.59
Std Dev 2.958
Gaus mean : 36.2618
Gaus Width: 3.0475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch13, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 42.2
Std Dev 3.644
Gaus mean : 42.9484
Gaus Width: 2.4497
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch13, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.88
Std Dev 2.744
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch13, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 54.57
Std Dev 3.086
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch13, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 58.16
Std Dev 7.974
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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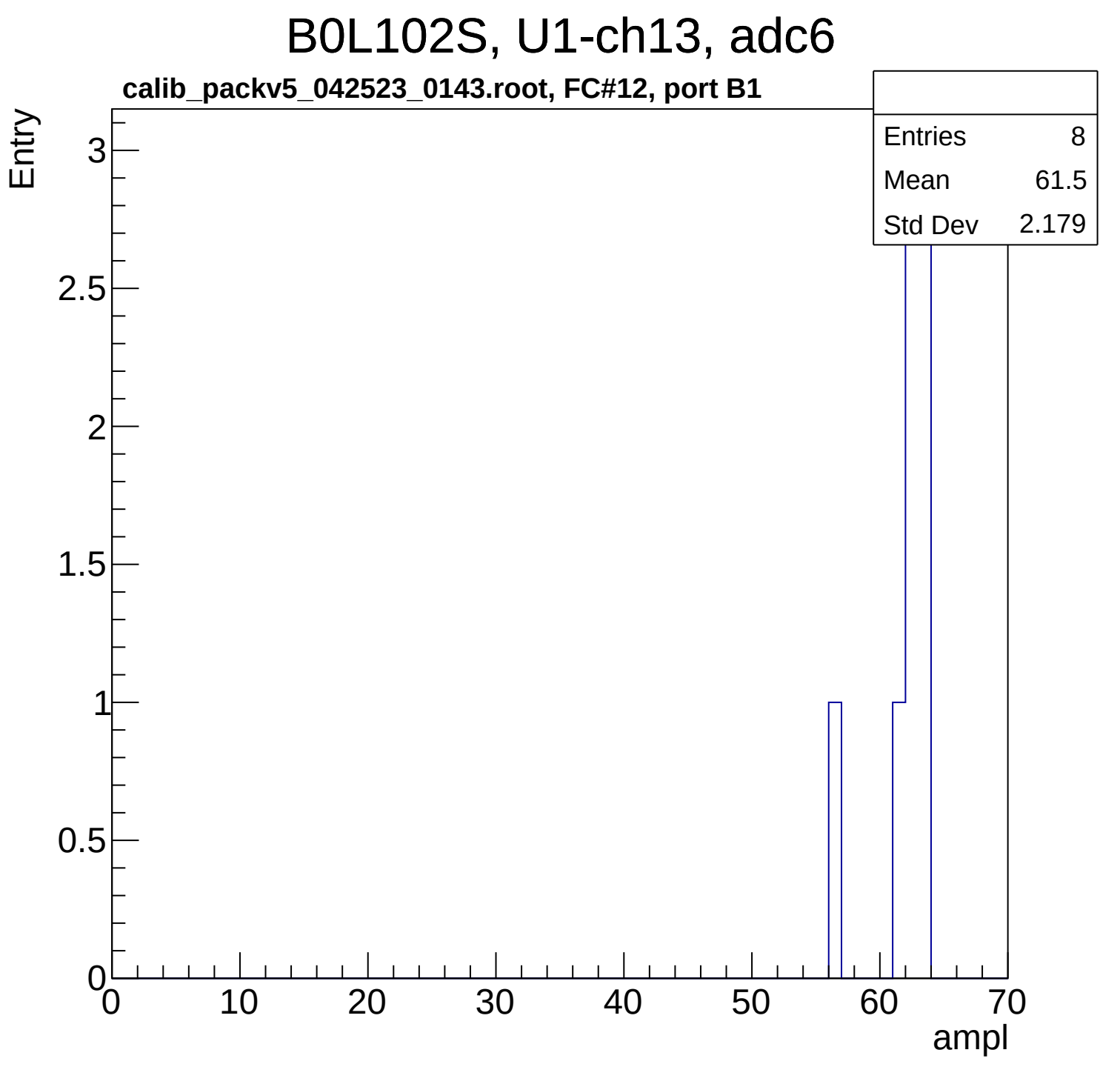
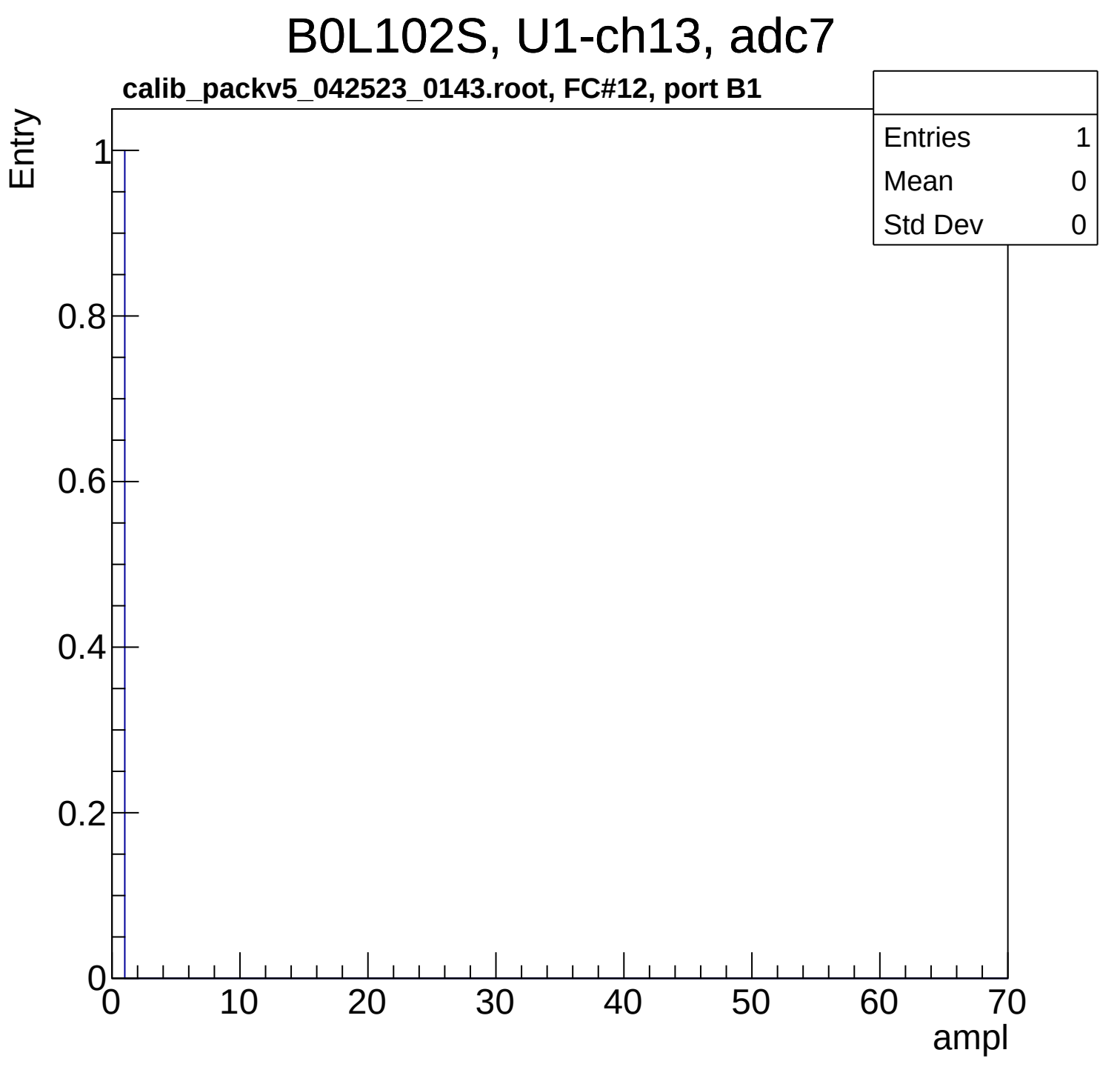
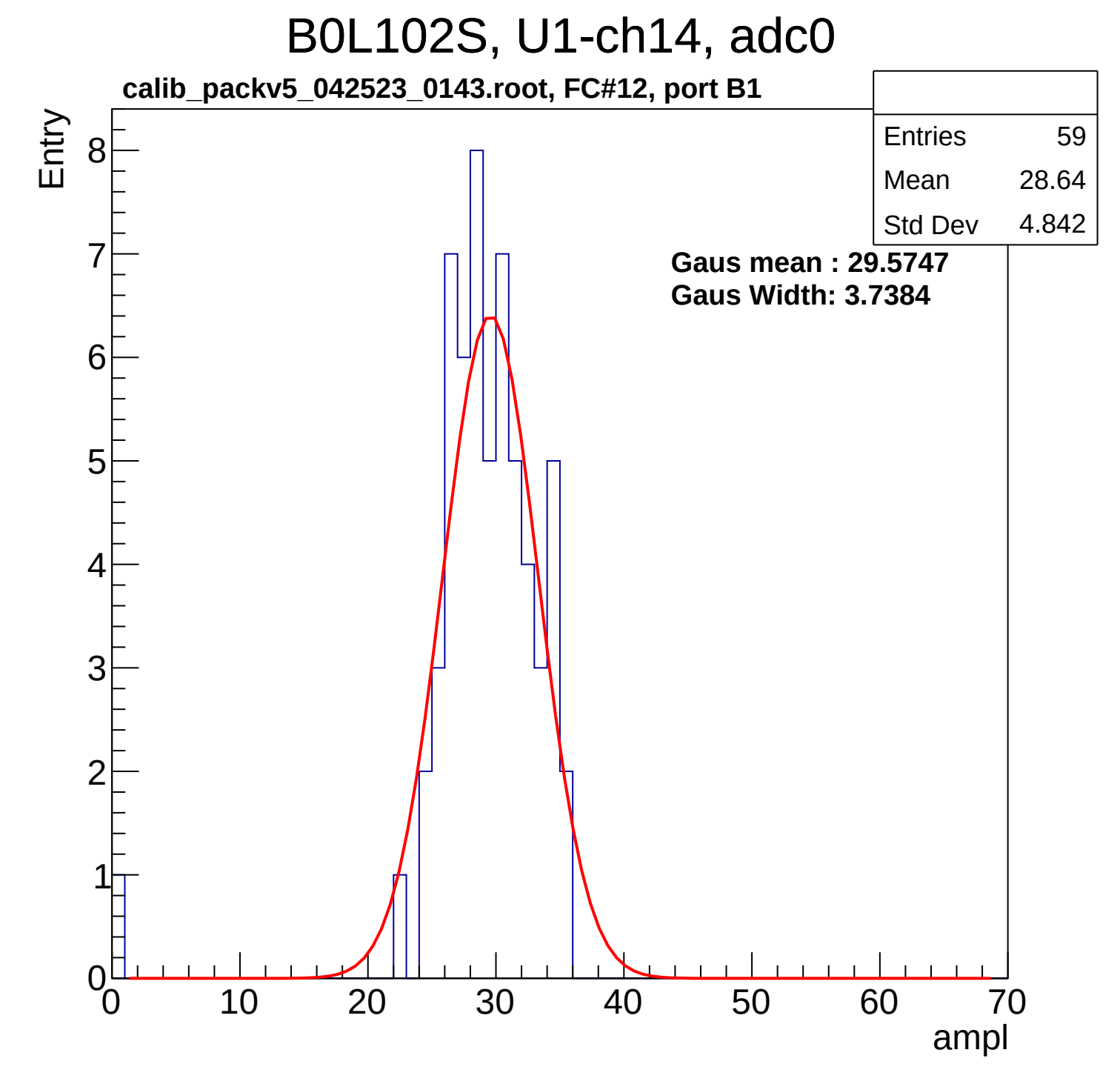
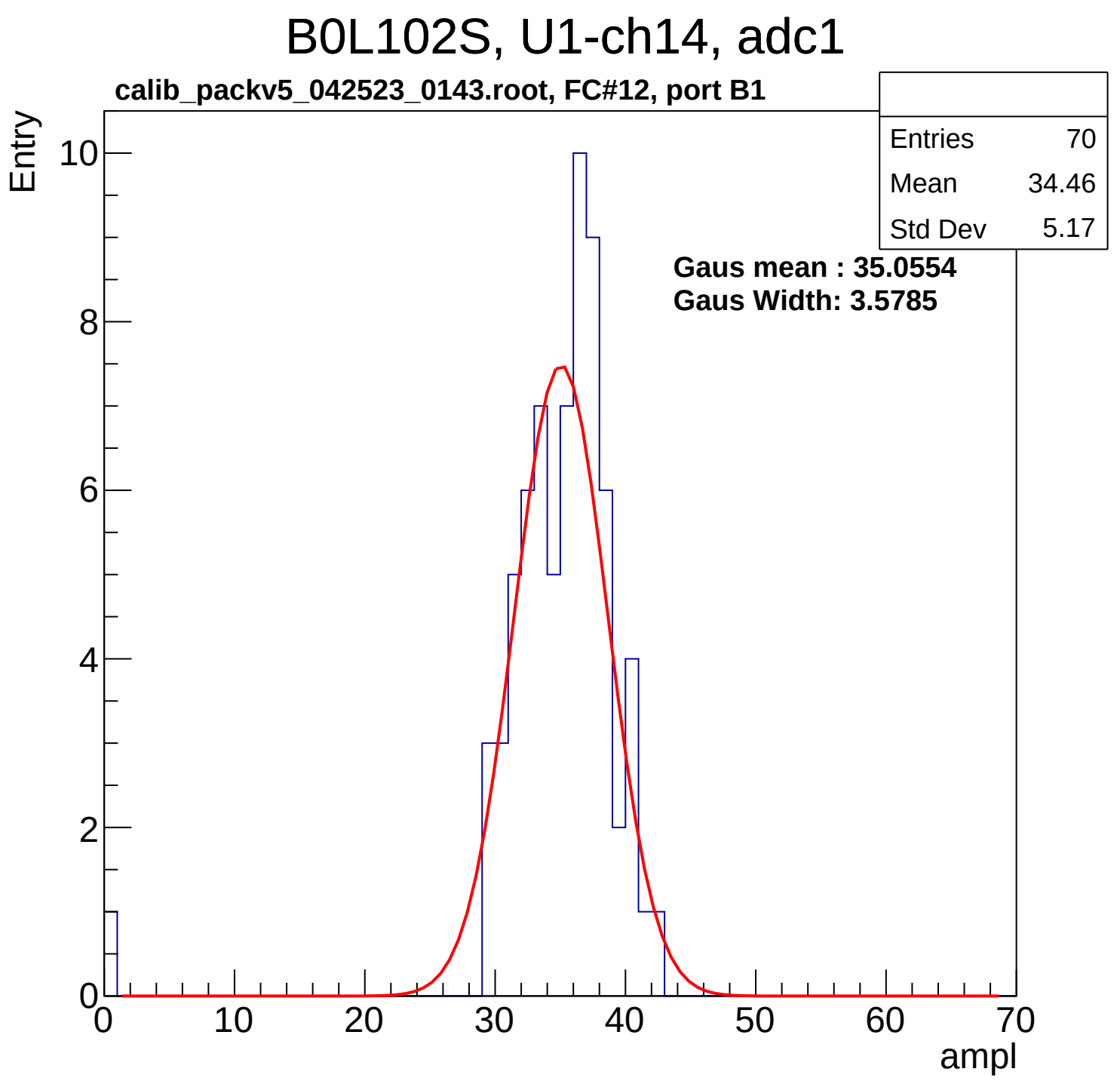
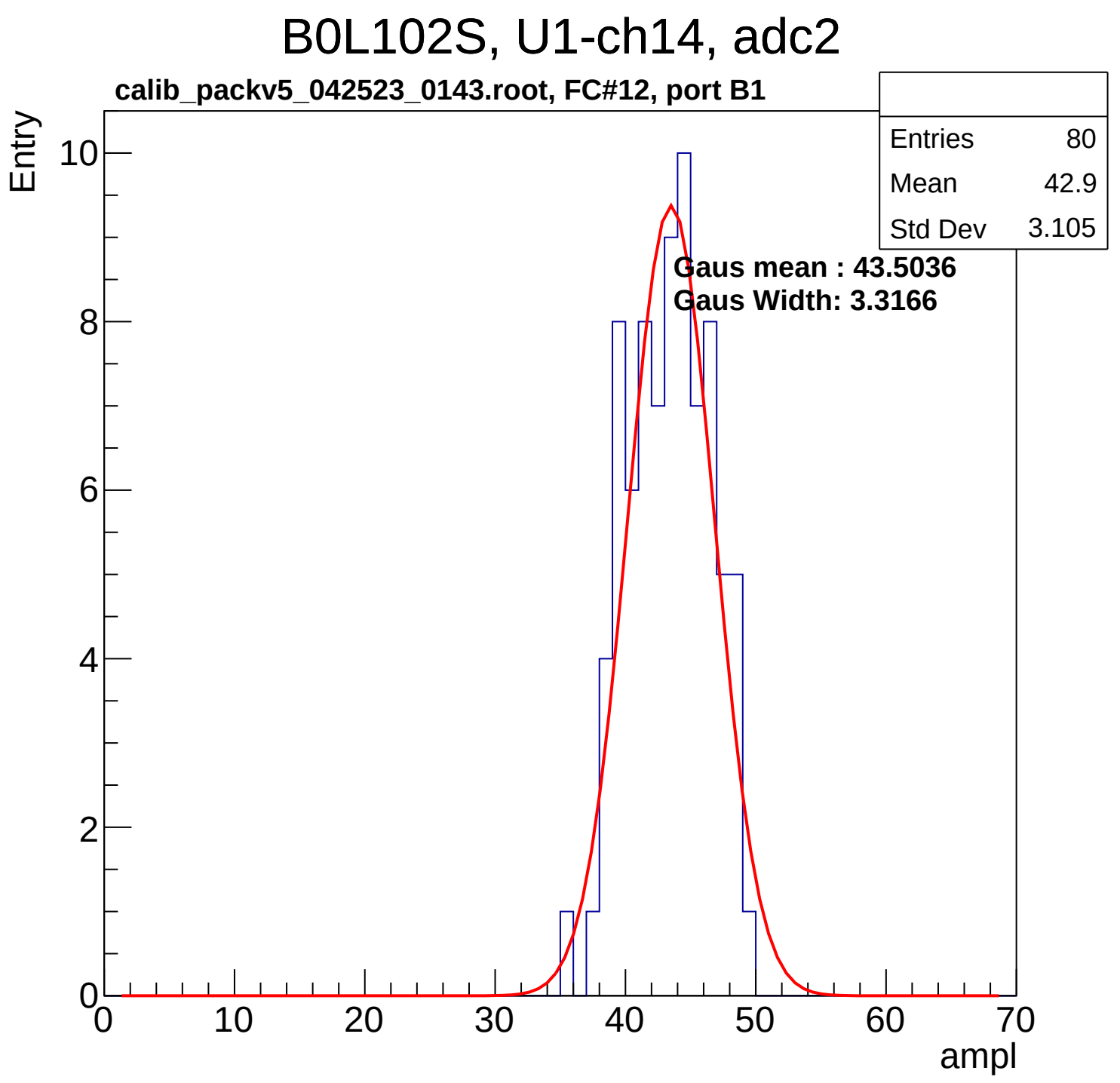
40
50
60
70

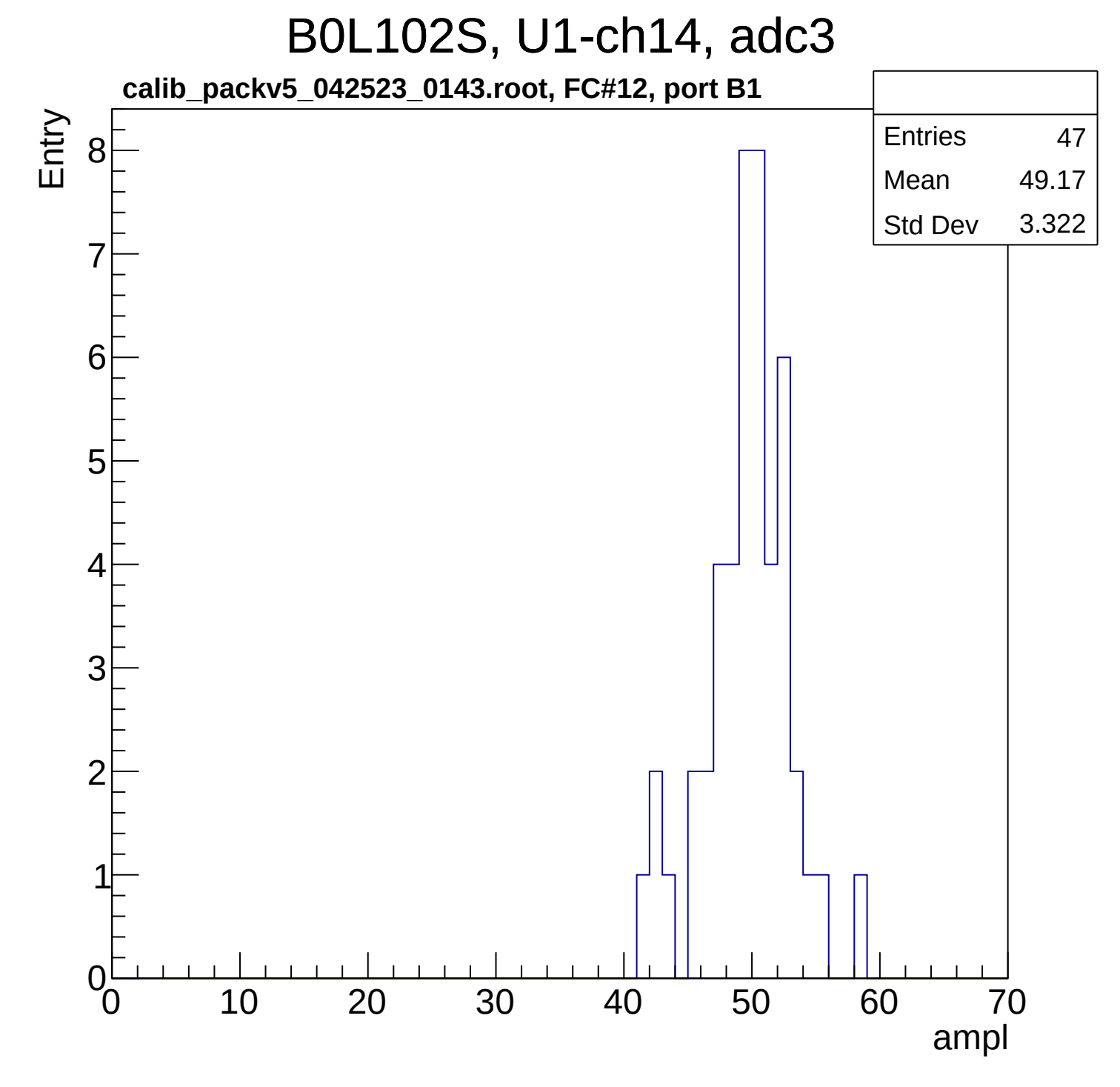

B0L102S, U1-ch13, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.5
Std Dev 2.179
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

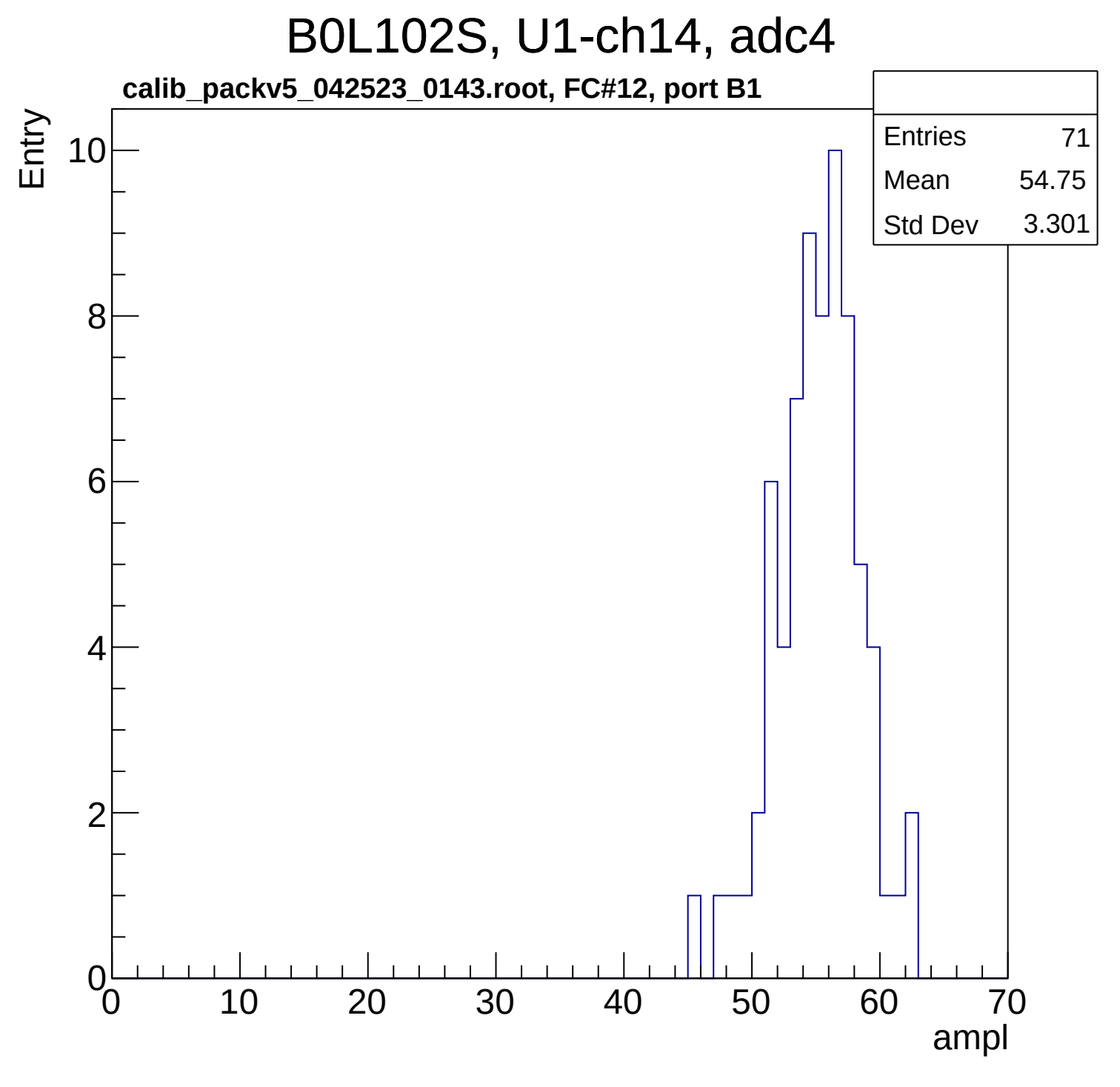

B0L102S, U1-ch13, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

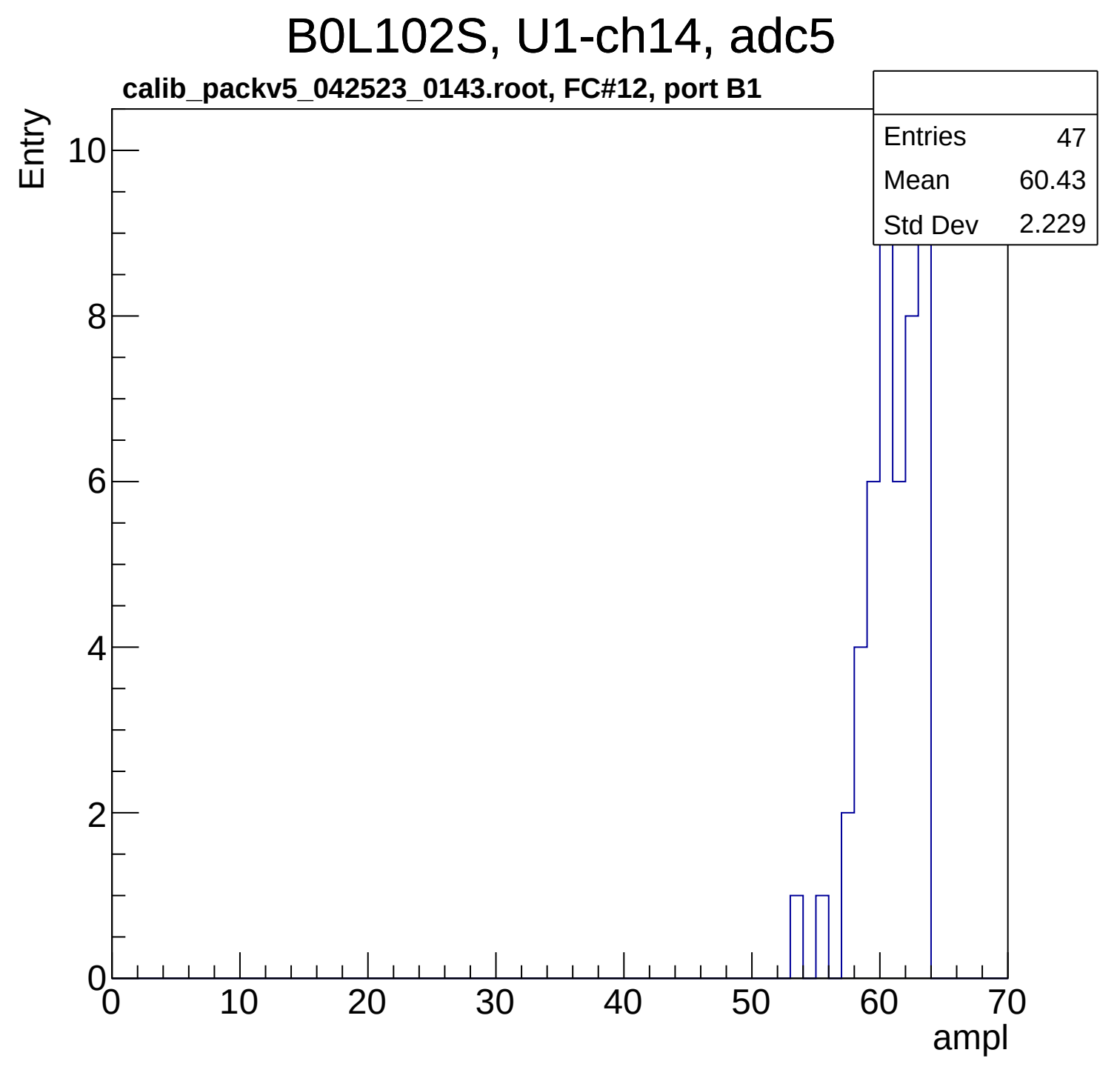

B0L102S, U1-ch14, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 28.64
Std Dev 4.842
Gaus mean : 29.5747
Gaus Width: 3.7384
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

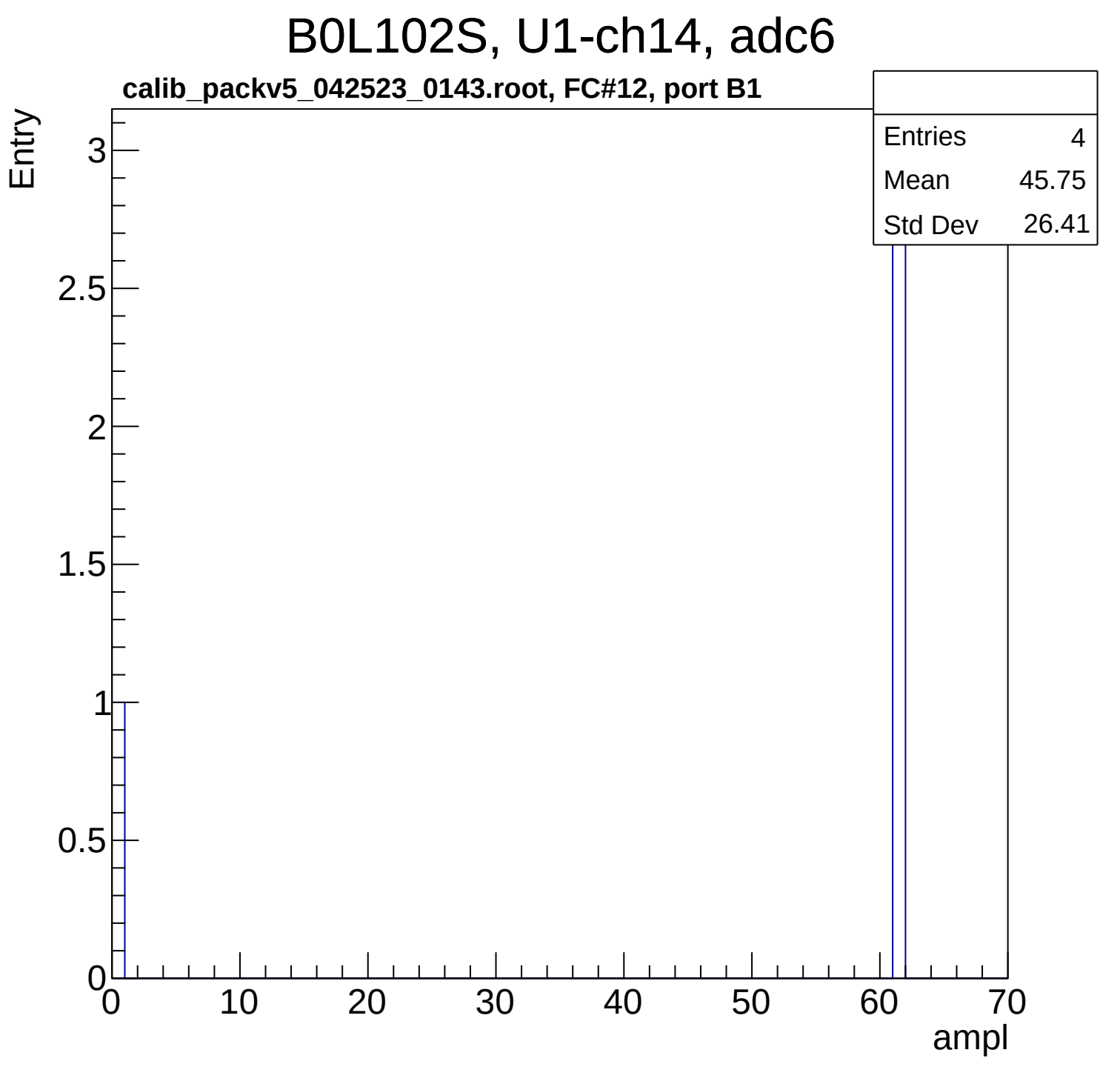

B0L102S, U1-ch14, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 34.46
Std Dev 5.17
Gaus mean : 35.0554
Gaus Width: 3.5785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

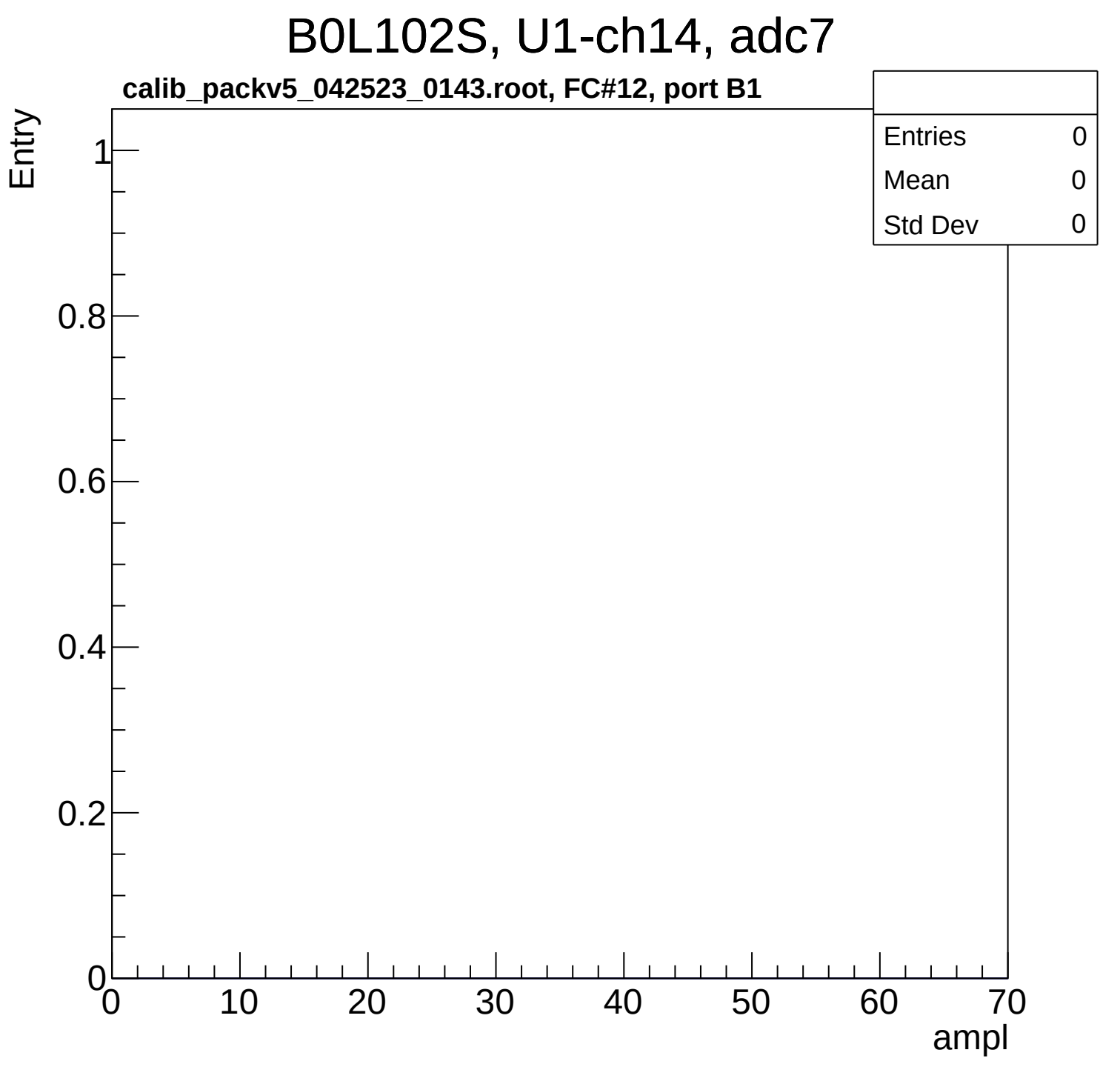

B0L102S, U1-ch14, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 42.9
Std Dev 3.105
Gaus mean : 43.5036
Gaus Width: 3.3166
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch14, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 49.17
Std Dev 3.322
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch14, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 54.75
Std Dev 3.301
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch14, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 60.43
Std Dev 2.229
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch14, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 45.75
Std Dev 26.41
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch14, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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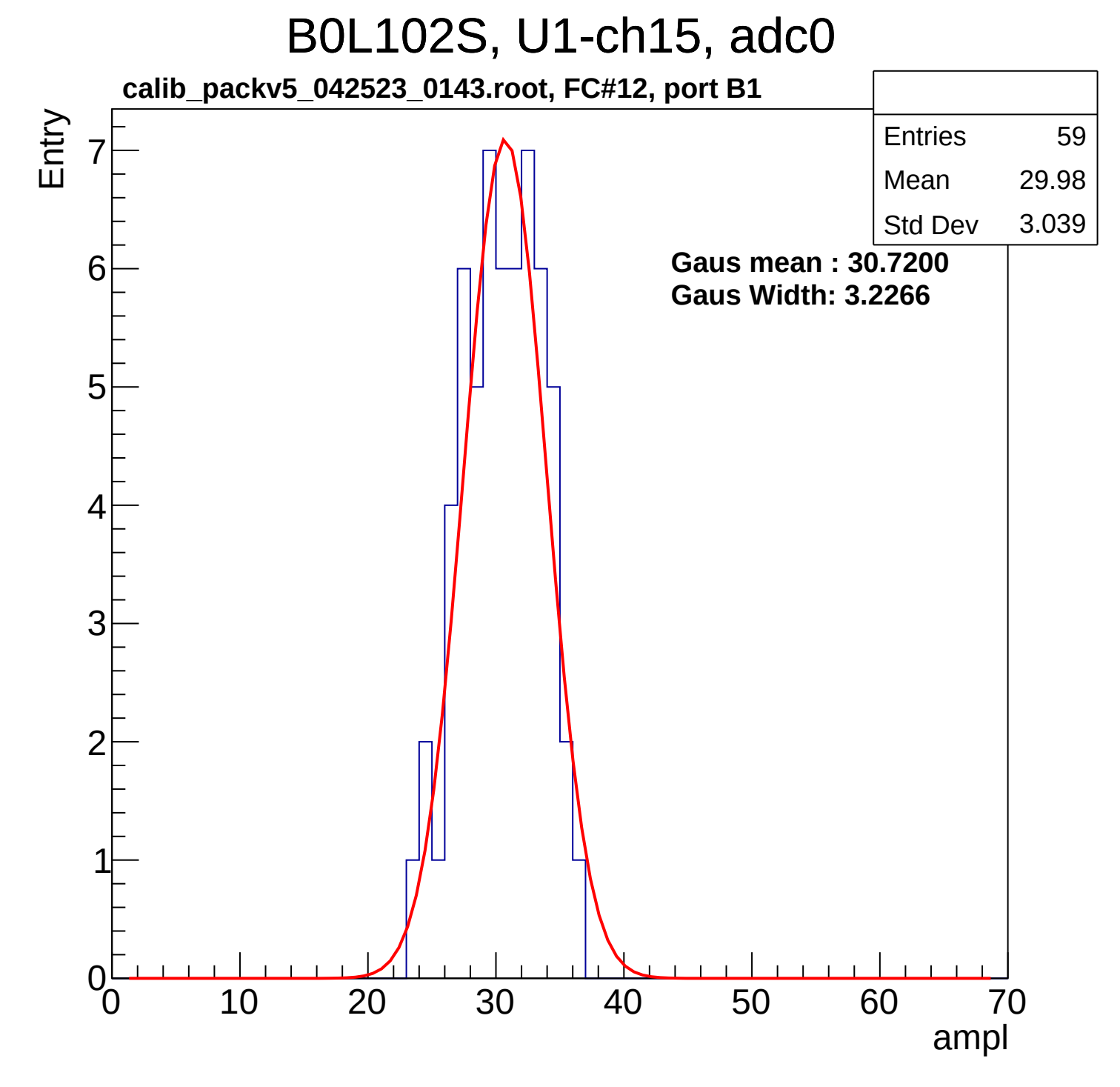
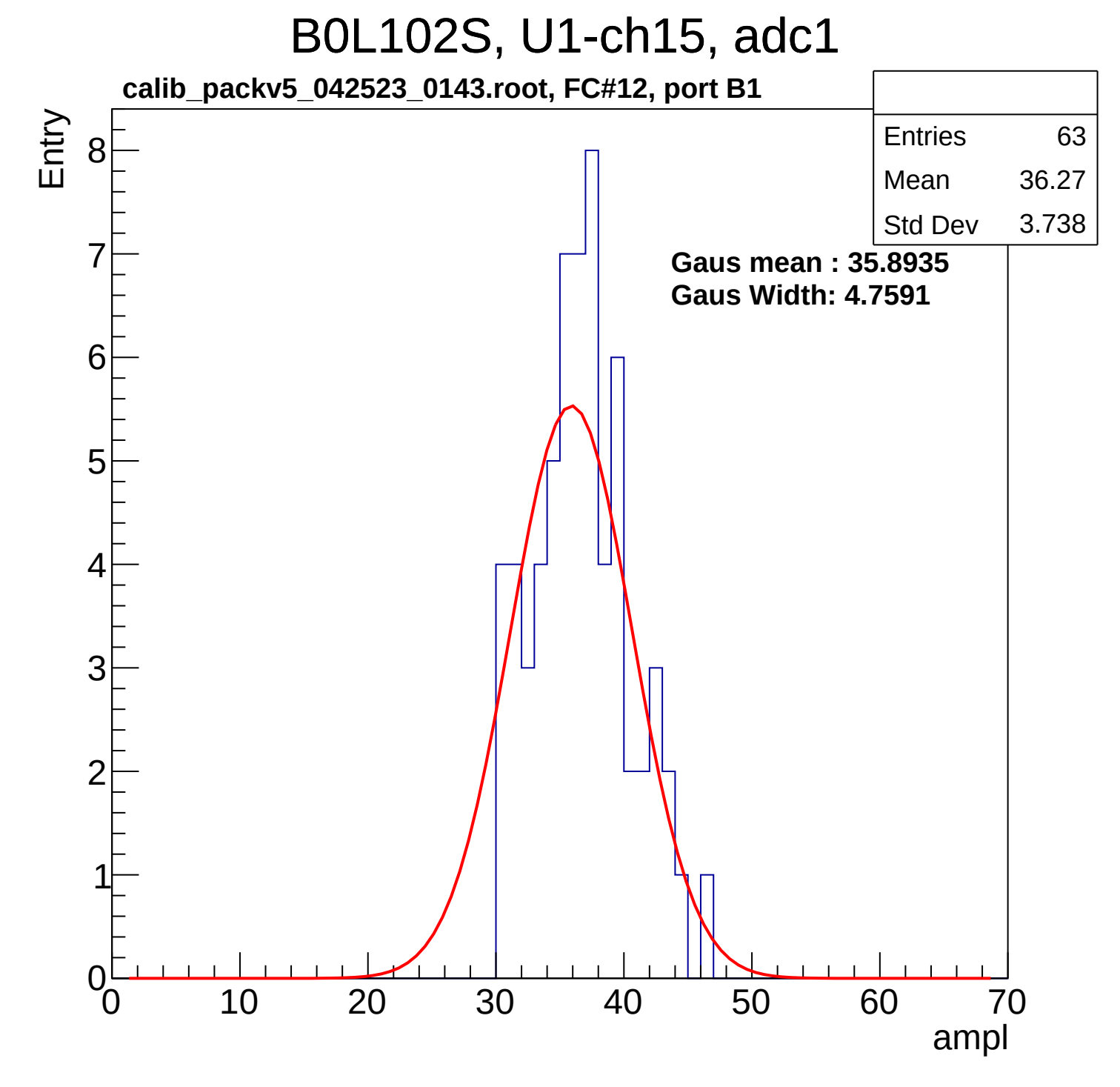
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch15, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 29.98
Std Dev 3.039
Gaus mean : 30.7200
Gaus Width: 3.2266
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch15, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 36.27
Std Dev 3.738
Gaus mean : 35.8935
Gaus Width: 4.7591
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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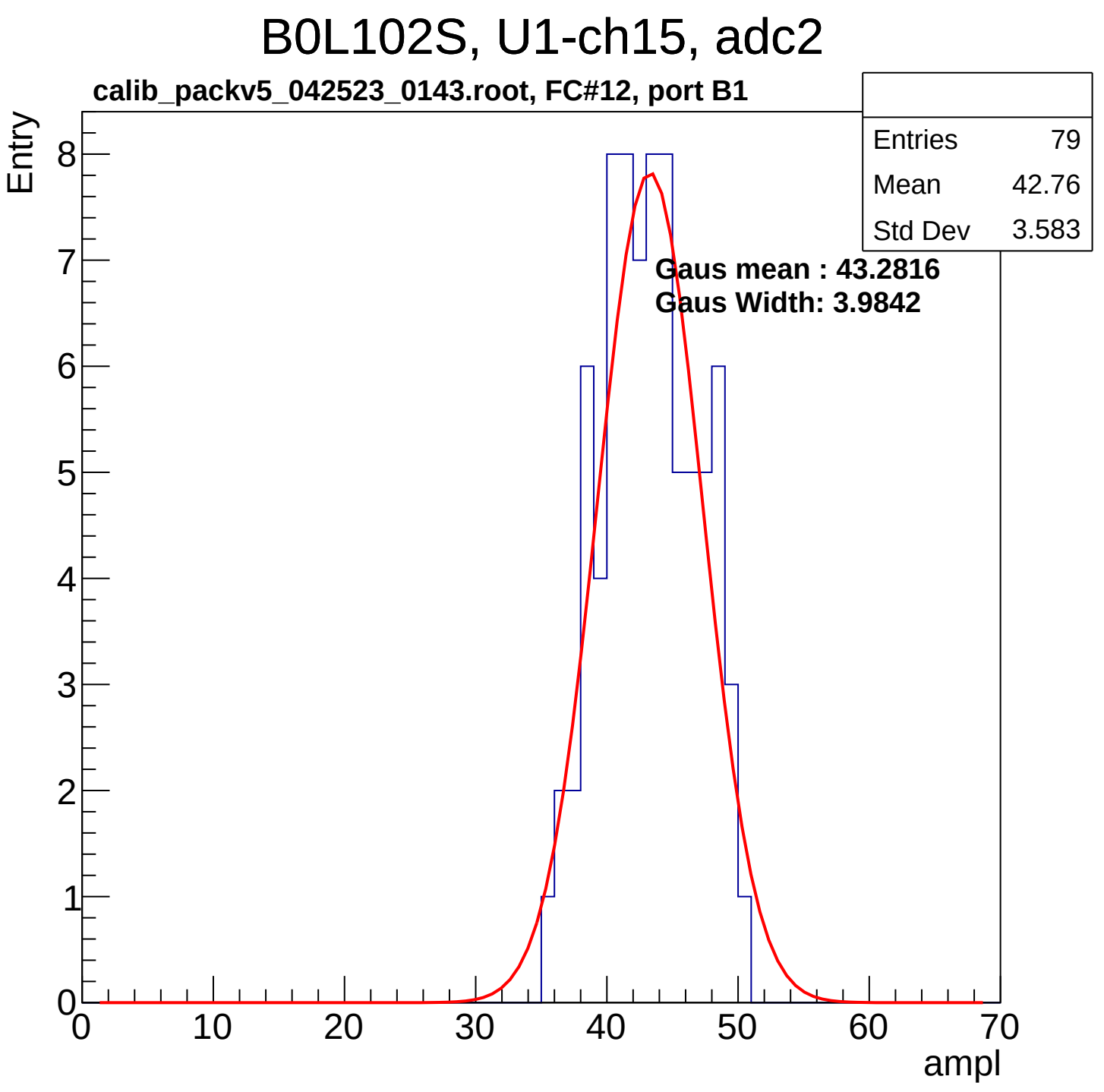
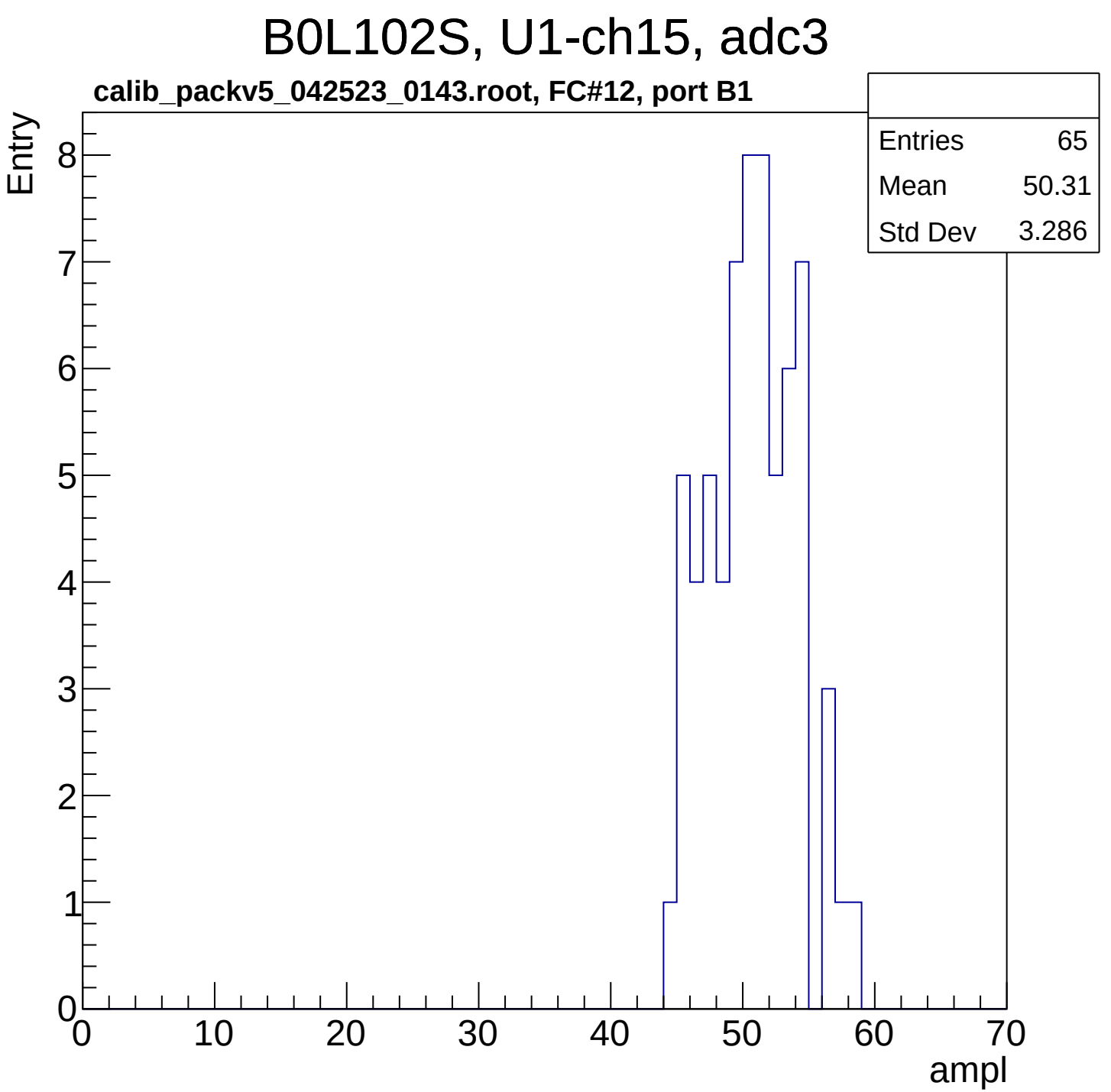
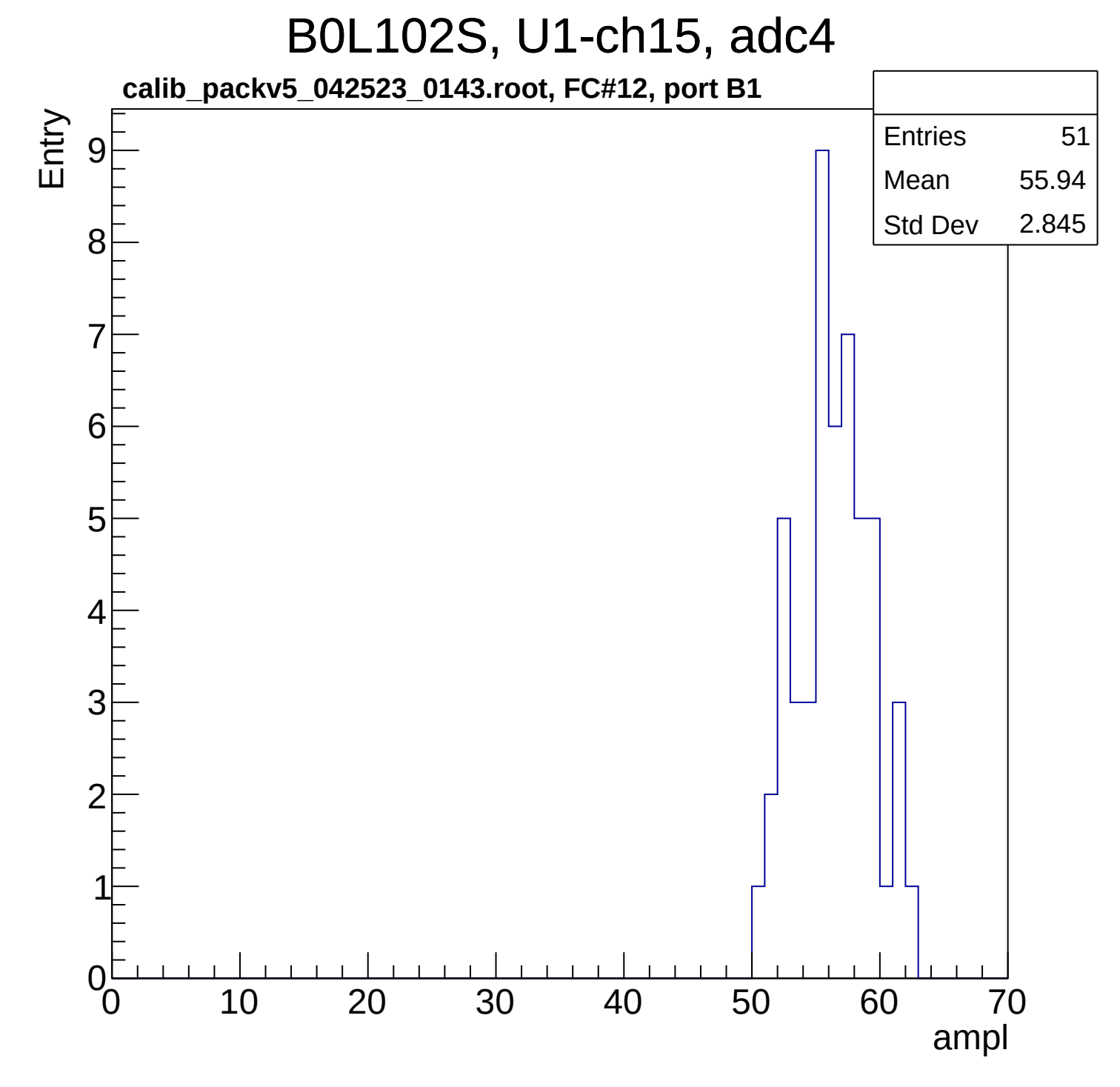
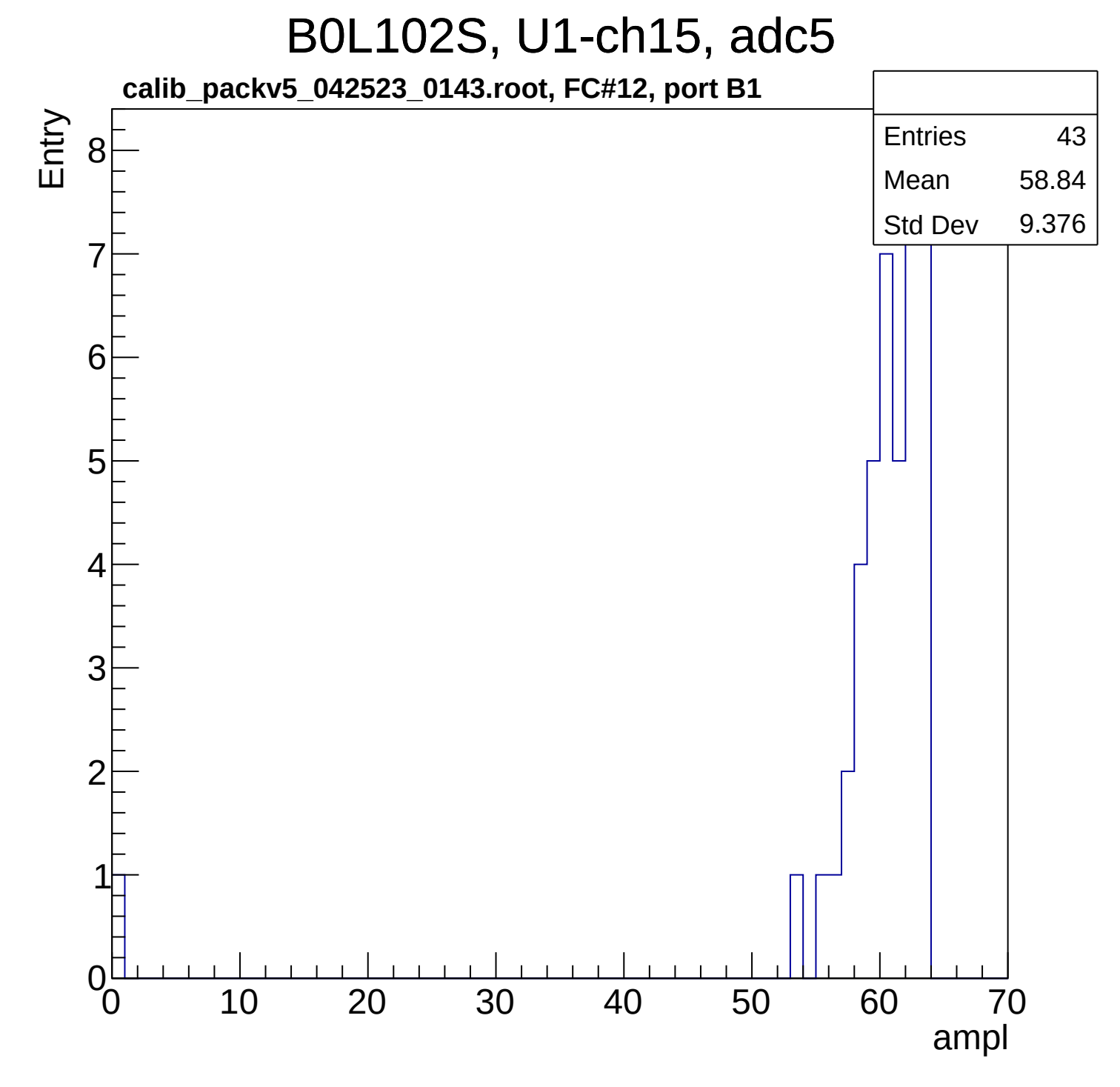

B0L102S, U1-ch15, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 42.76
Std Dev 3.583
Gaus mean : 43.2816
Gaus Width: 3.9842
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch15, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 50.31
Std Dev 3.286
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch15, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 55.94
Std Dev 2.845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch15, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.84
Std Dev 9.376
Entry
ampl
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2
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60
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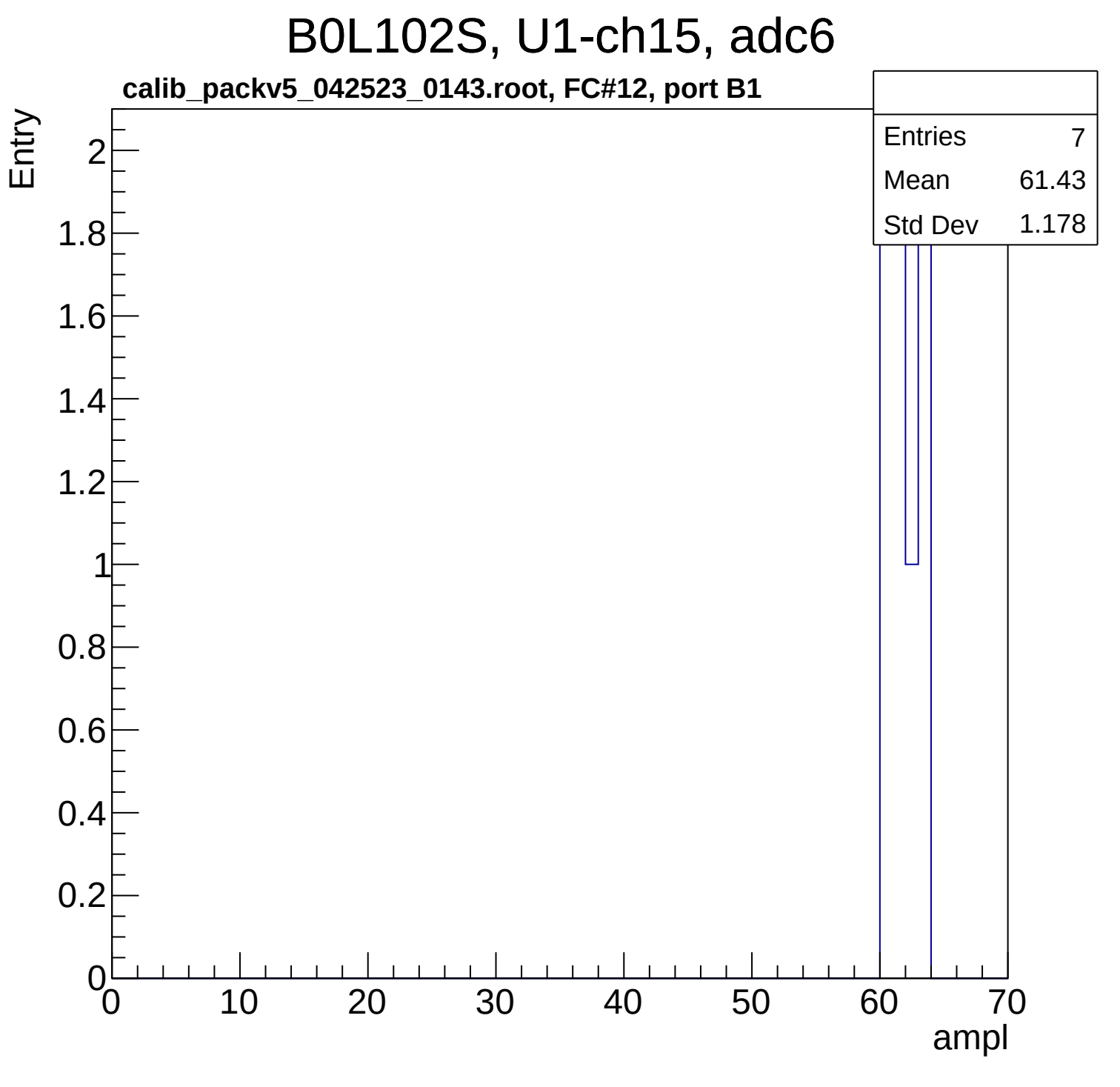
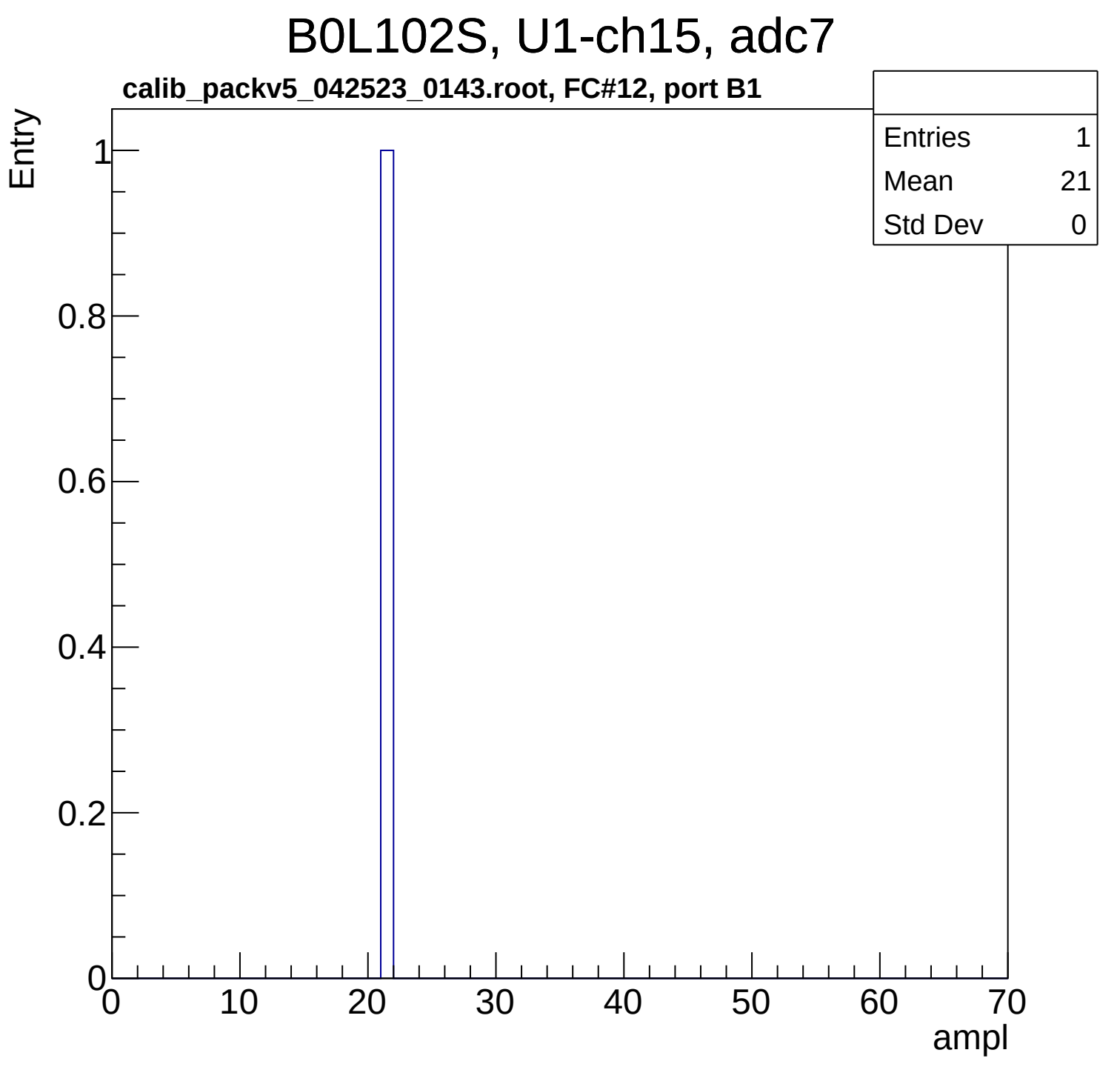
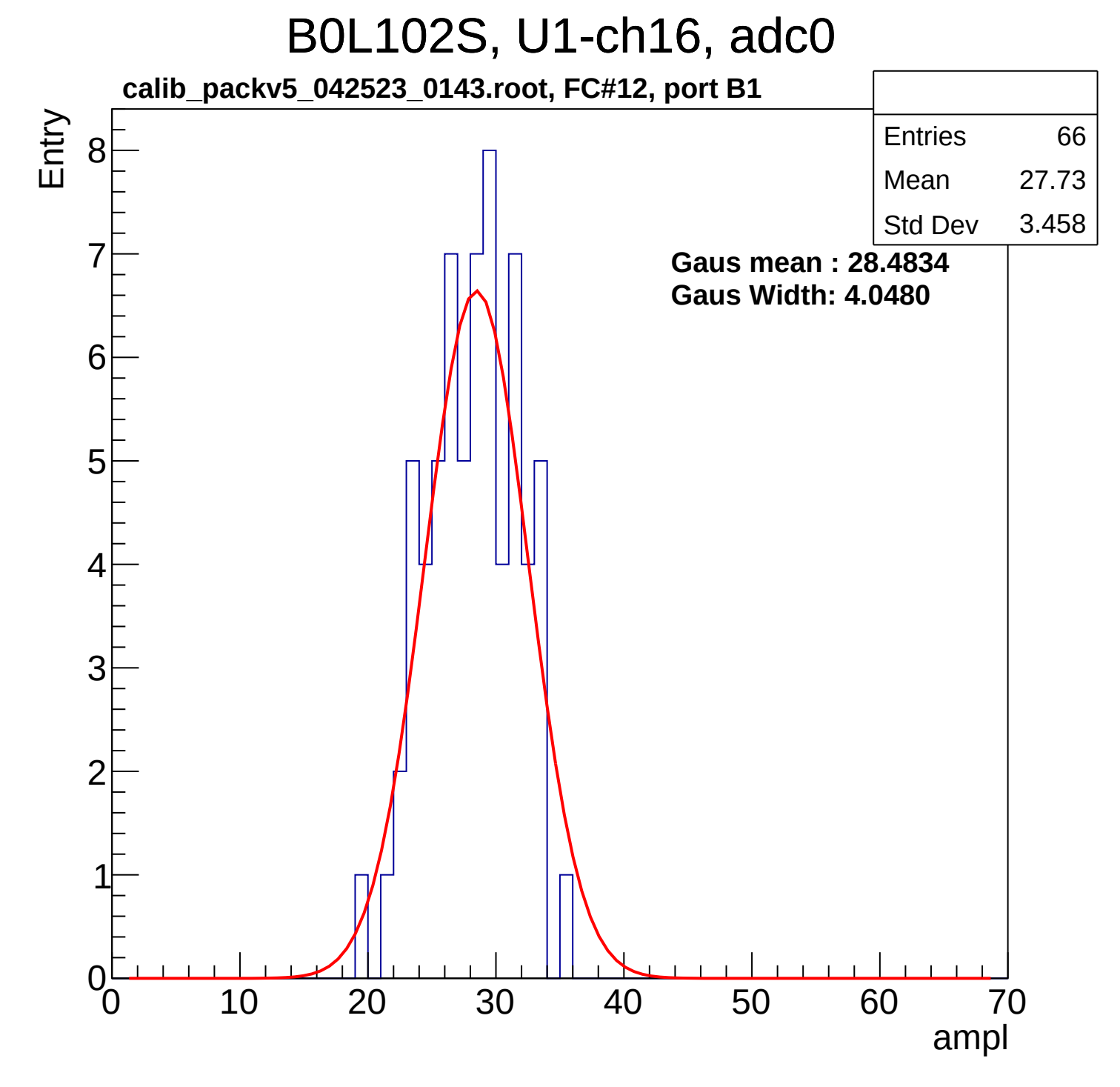
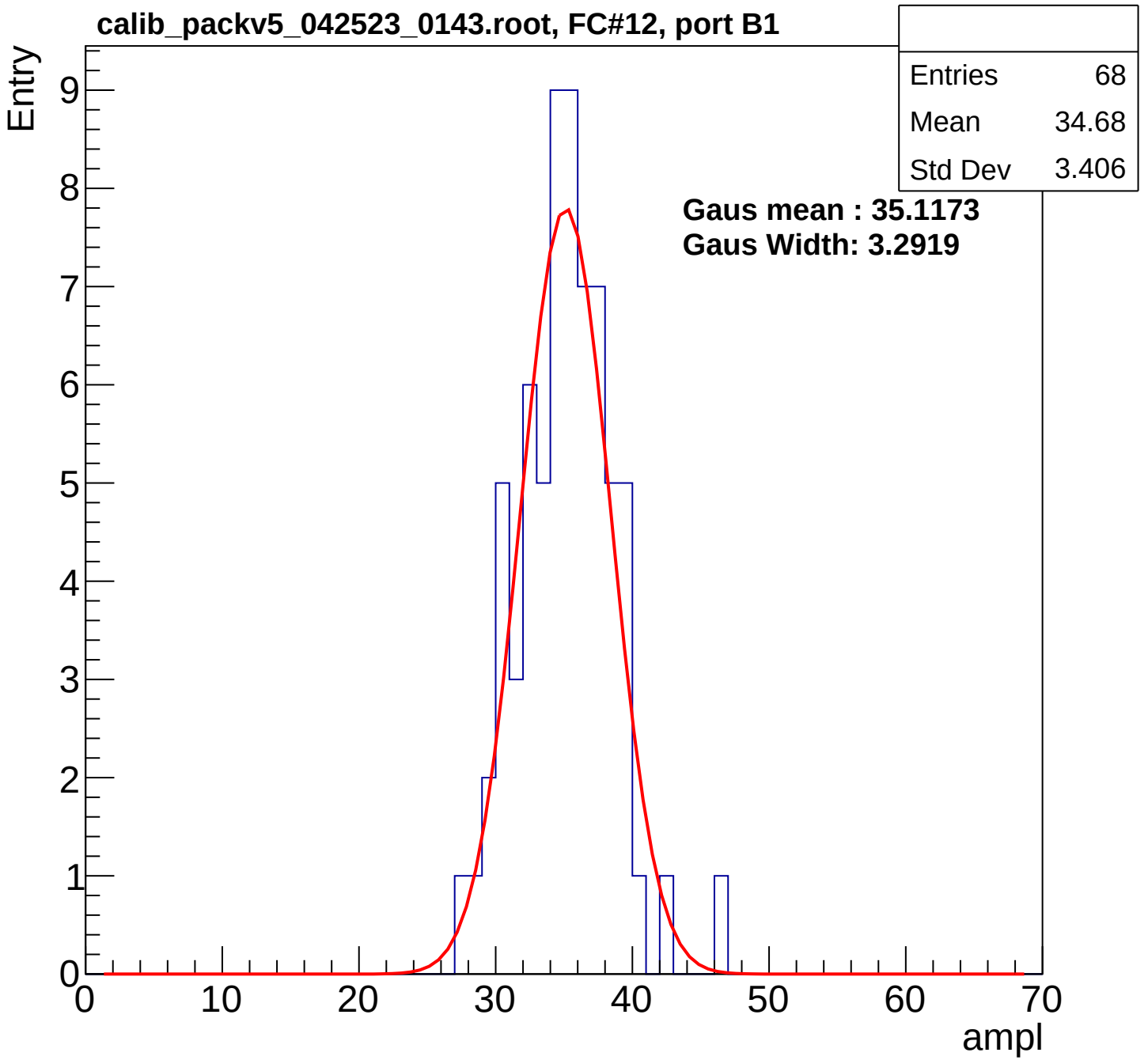
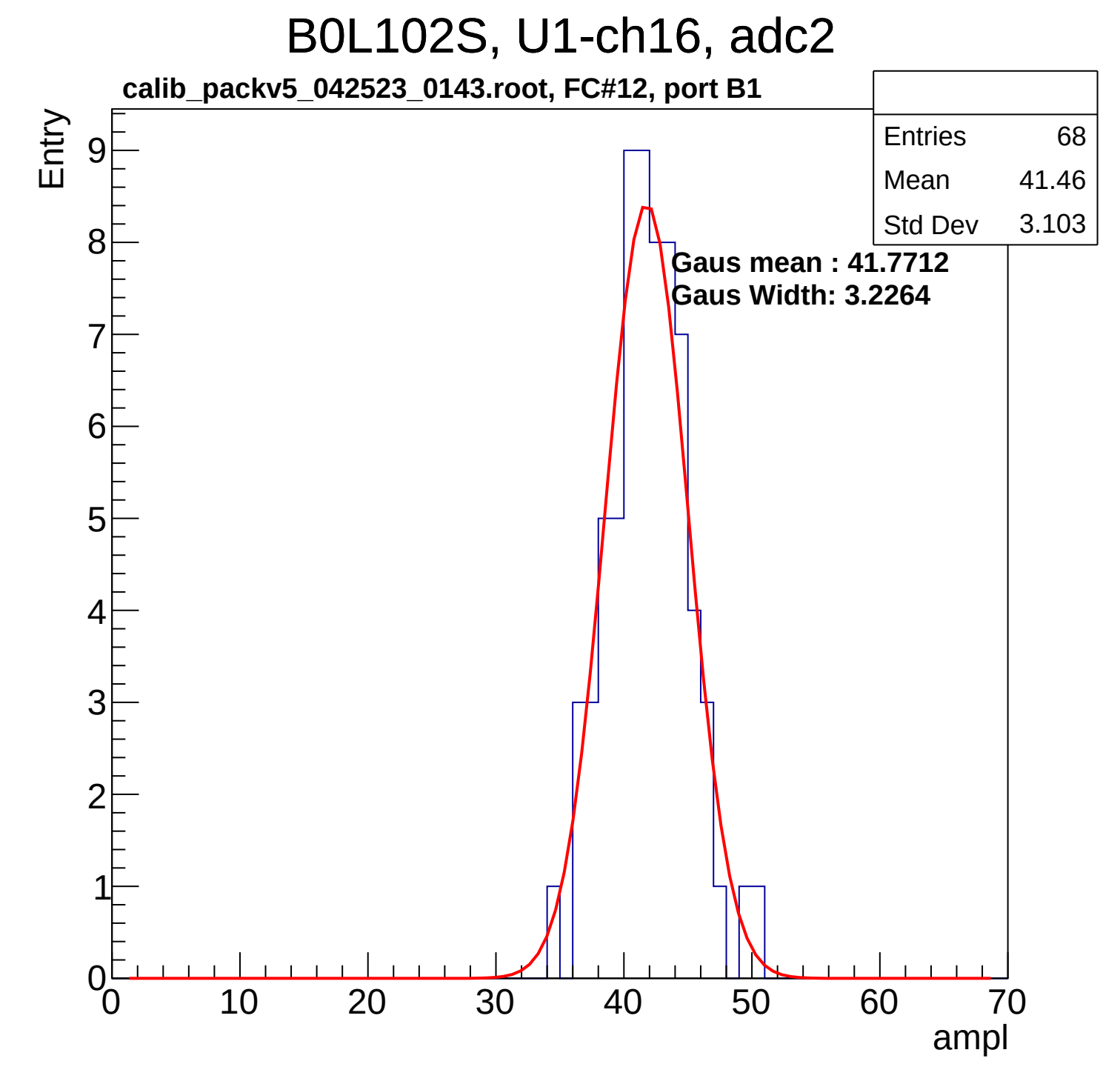
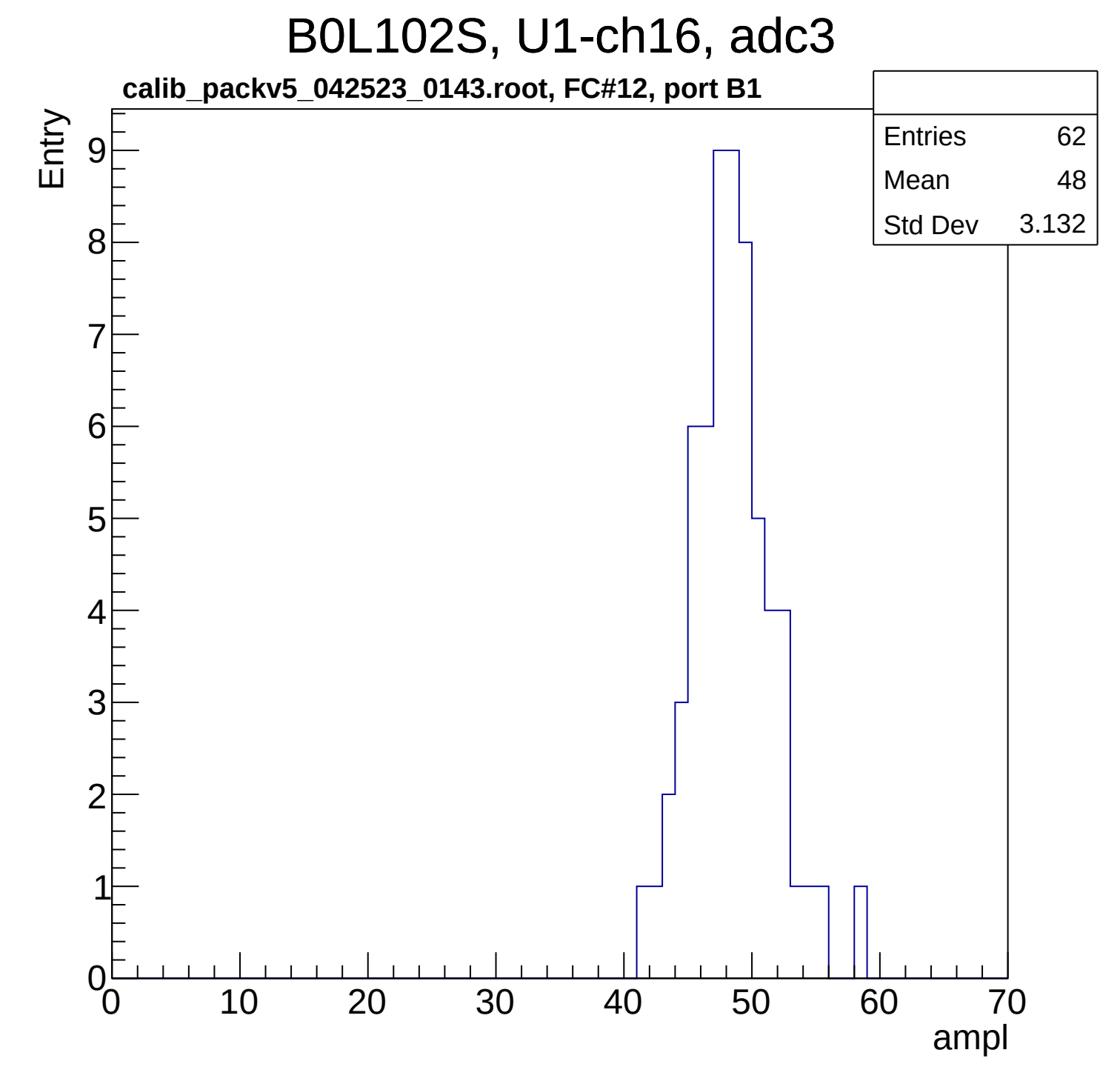

B0L102S, U1-ch15, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.43
Std Dev 1.178
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

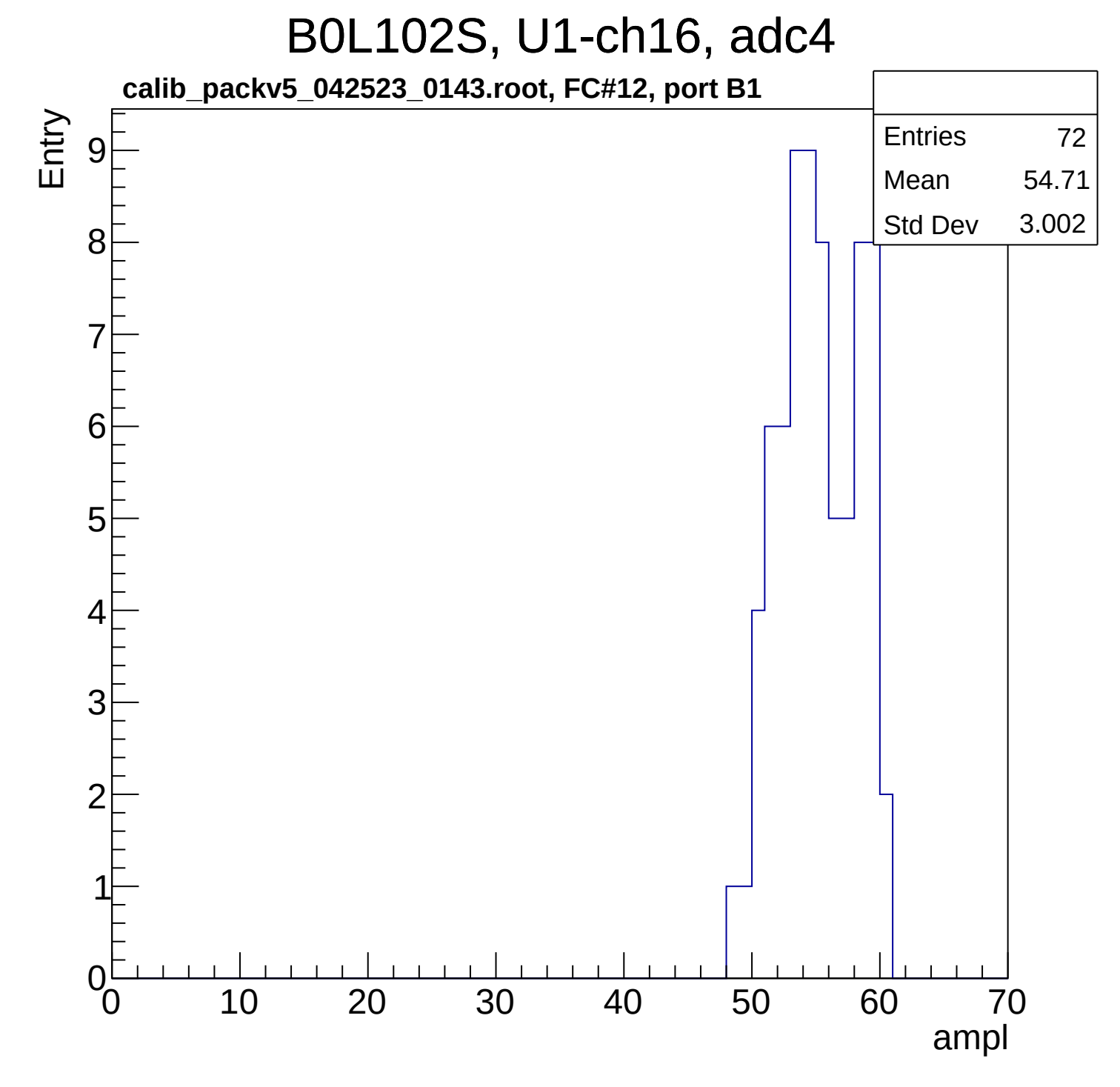

B0L102S, U1-ch15, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

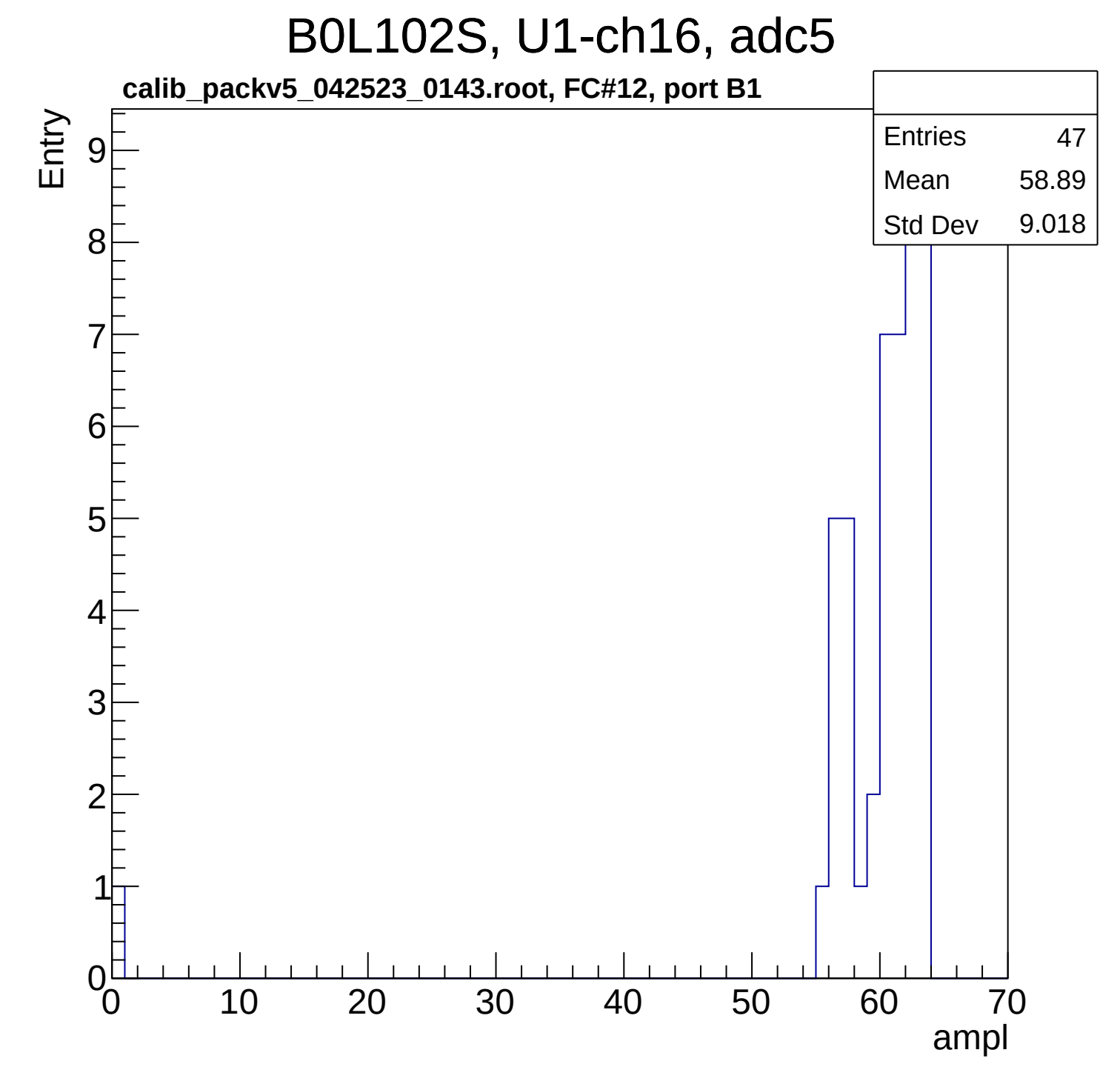

B0L102S, U1-ch16, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 27.73
Std Dev 3.458
Gaus mean : 28.4834
Gaus Width: 4.0480
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

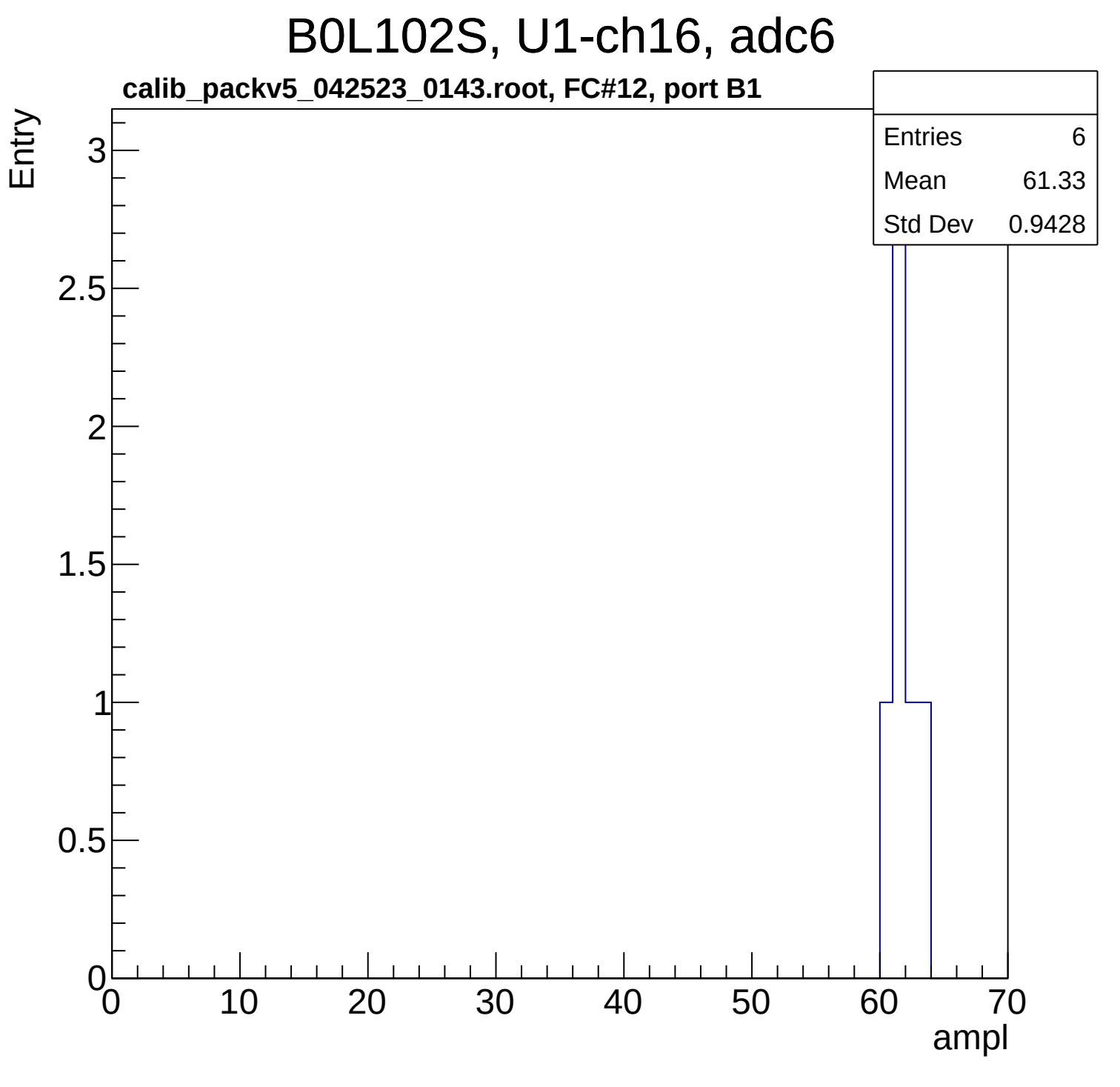

B0L102S, U1-ch16, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 34.68
Std Dev 3.406
Gaus mean : 35.1173
Gaus Width: 3.2919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

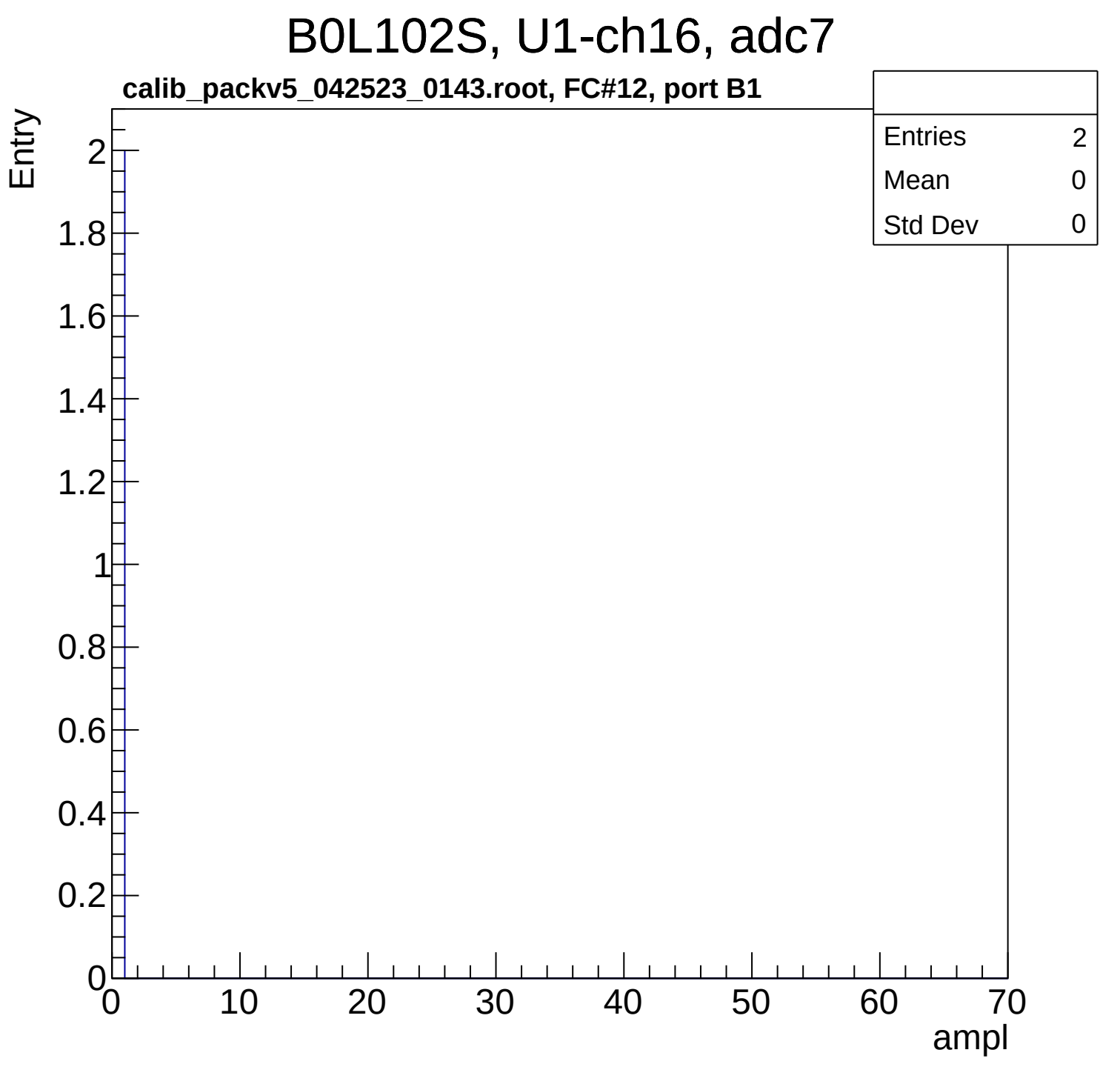

B0L102S, U1-ch16, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.46
Std Dev 3.103
Gaus mean : 41.7712
Gaus Width: 3.2264
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

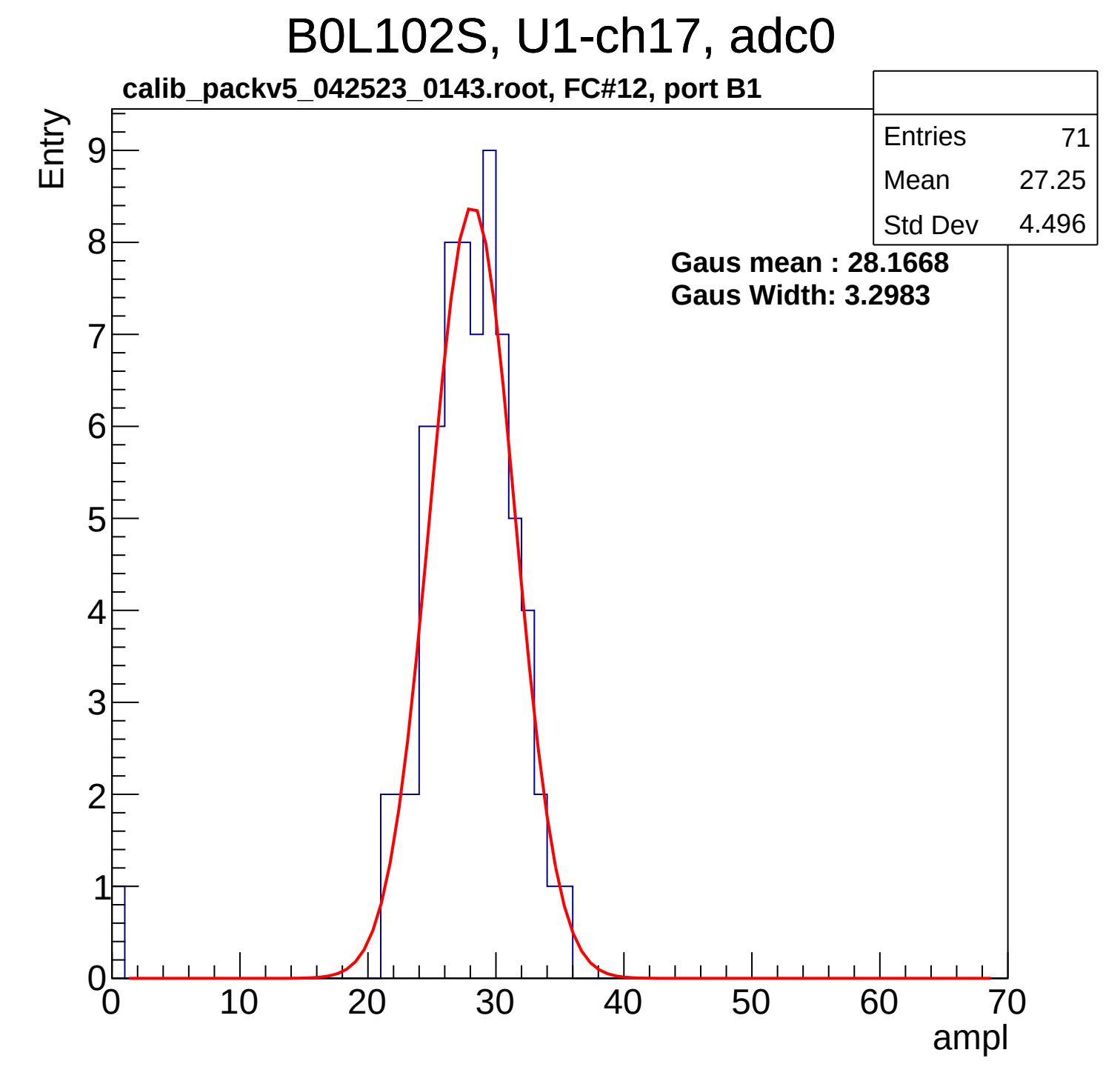

B0L102S, U1-ch16, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 48
Std Dev 3.132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

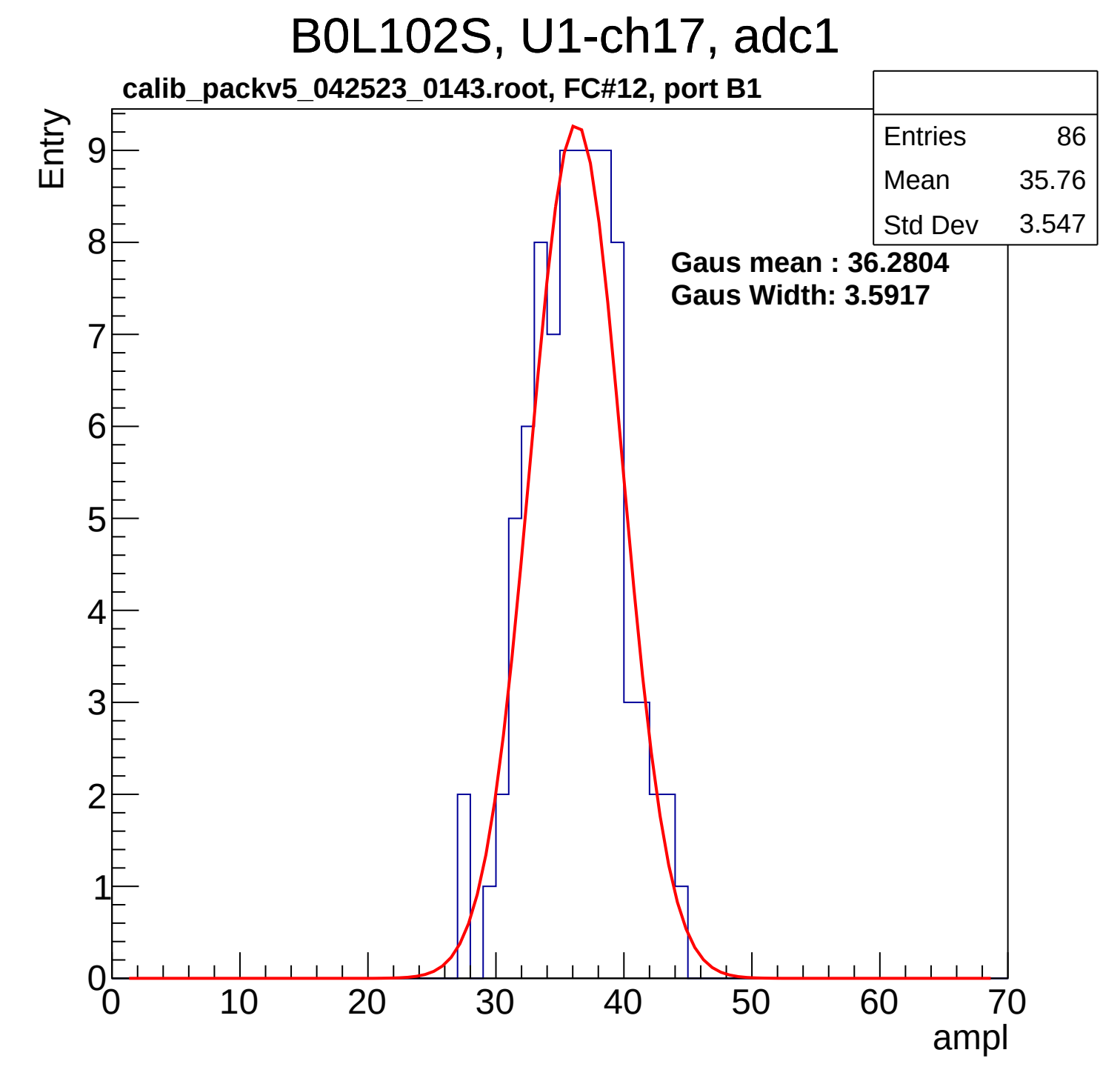

B0L102S, U1-ch16, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 54.71
Std Dev 3.002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

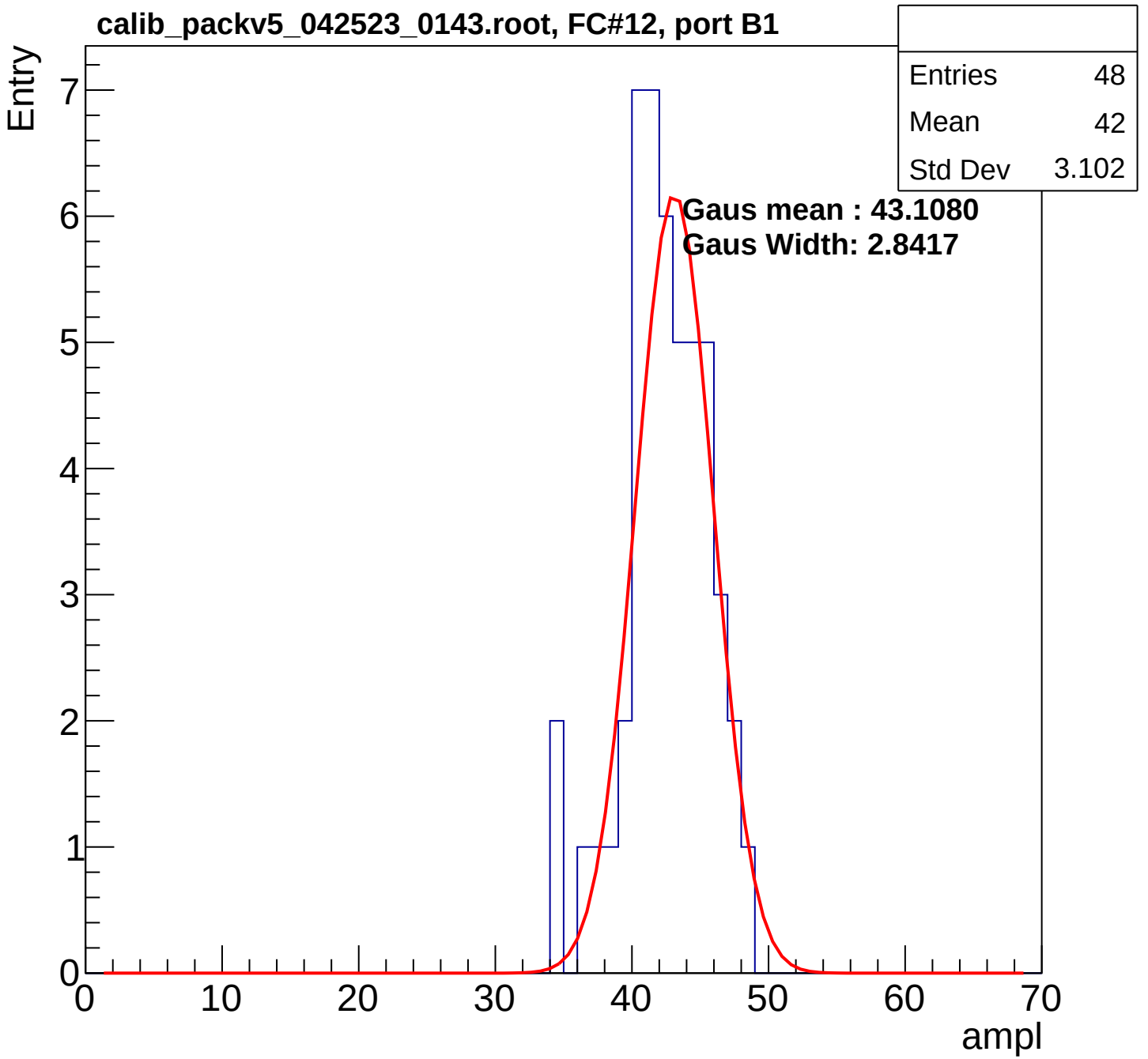

B0L102S, U1-ch16, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.89
Std Dev 9.018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

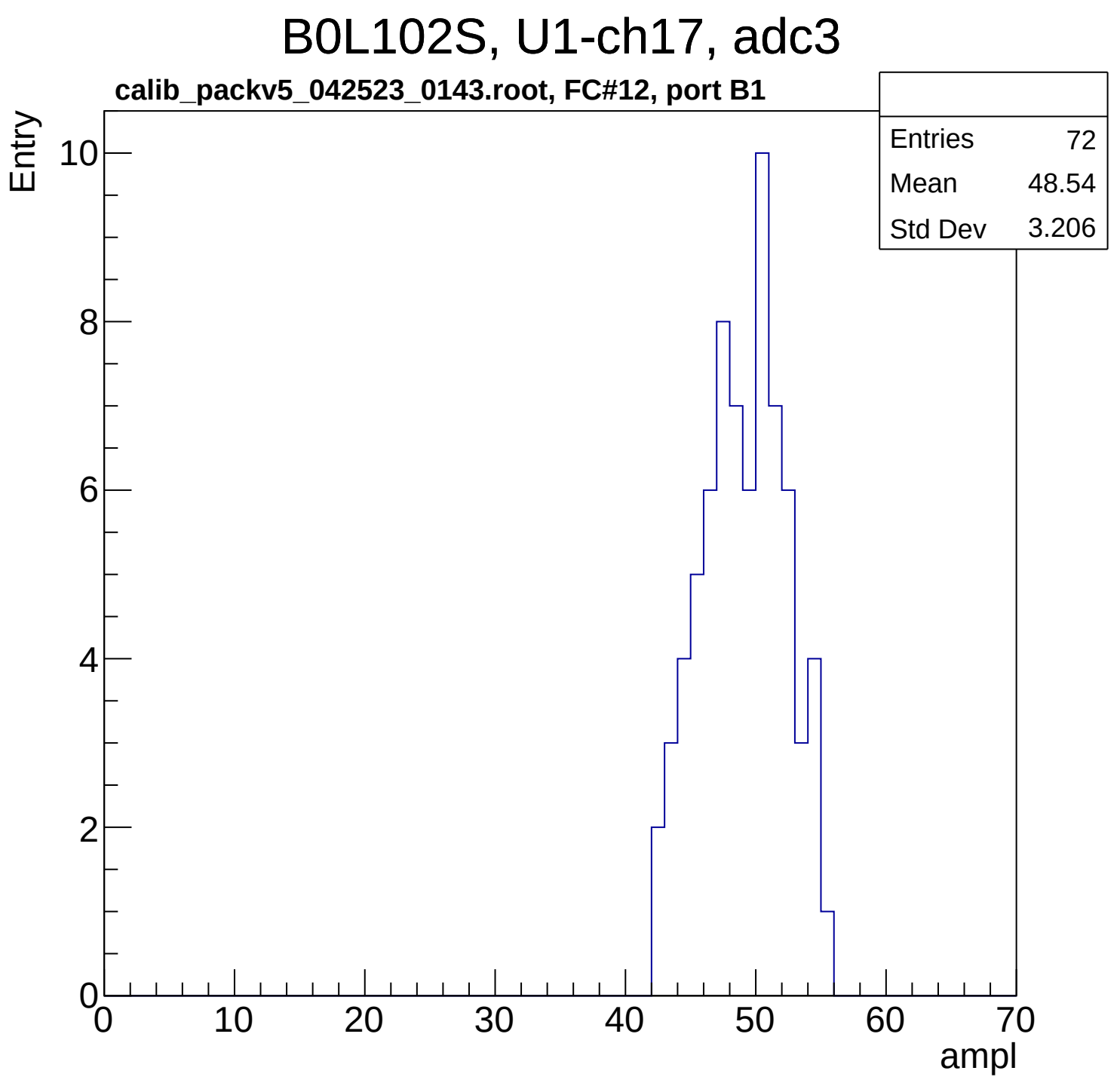

B0L102S, U1-ch16, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.33
Std Dev 0.9428
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

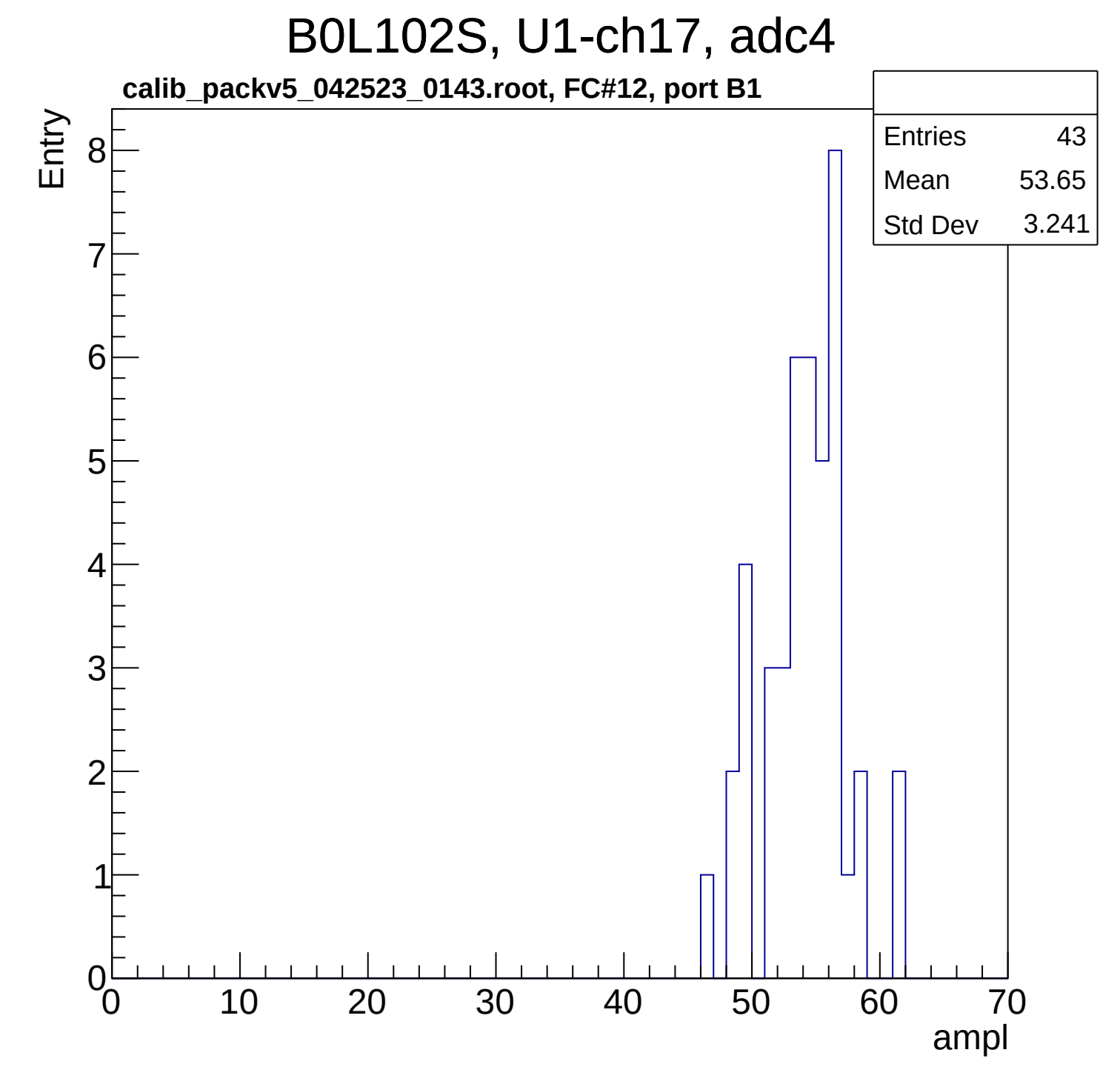

B0L102S, U1-ch16, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch17, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 27.25
Std Dev 4.496
Gaus mean : 28.1668
Gaus Width: 3.2983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch17, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 35.76
Std Dev 3.547
Gaus mean : 36.2804
Gaus Width: 3.5917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch17, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 42
Std Dev 3.102
Gaus mean : 43.1080
Gaus Width: 2.8417
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch17, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 48.54
Std Dev 3.206
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch17, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 53.65
Std Dev 3.241
Entry
ampl
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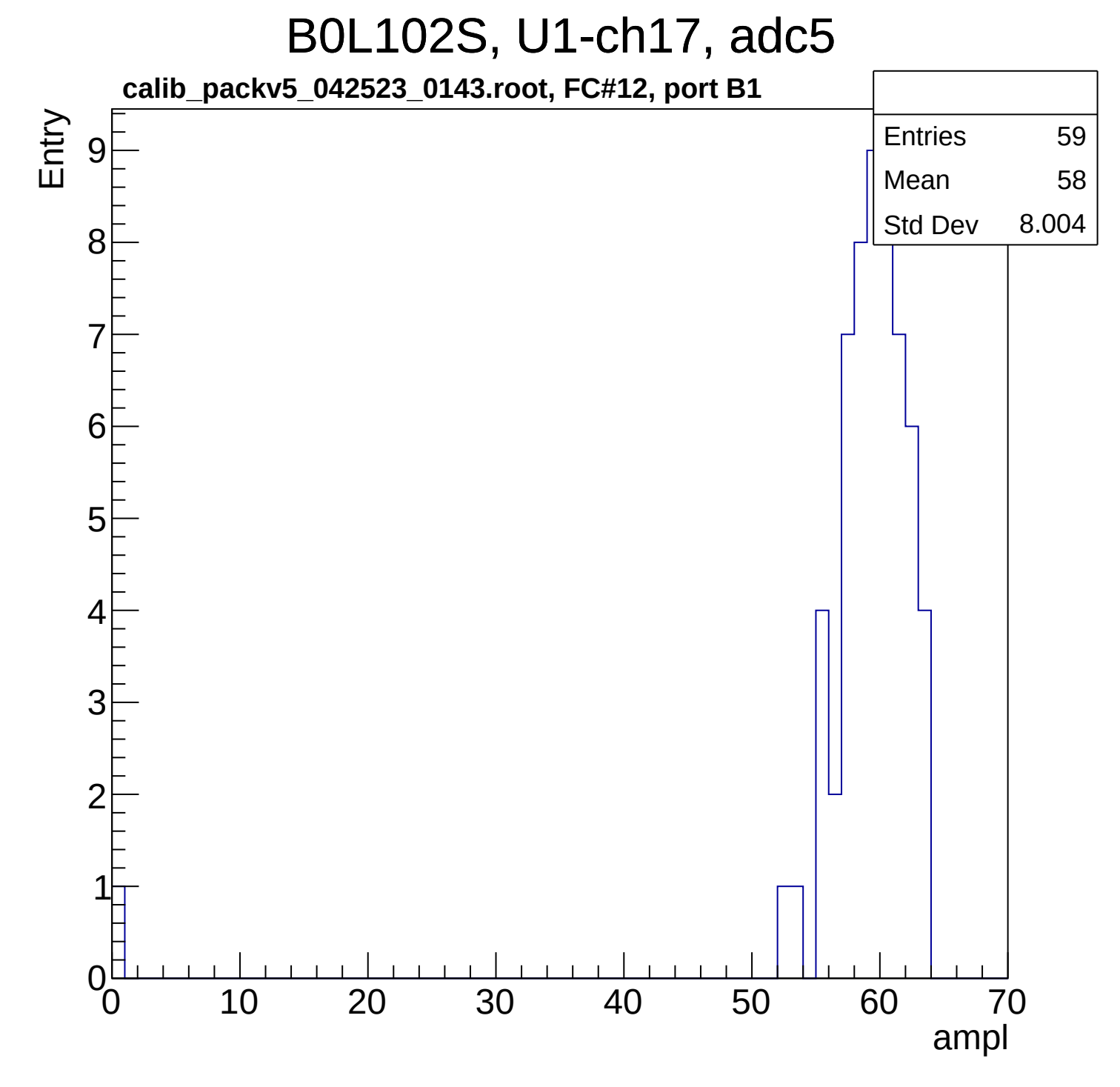
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch17, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 58
Std Dev 8.004
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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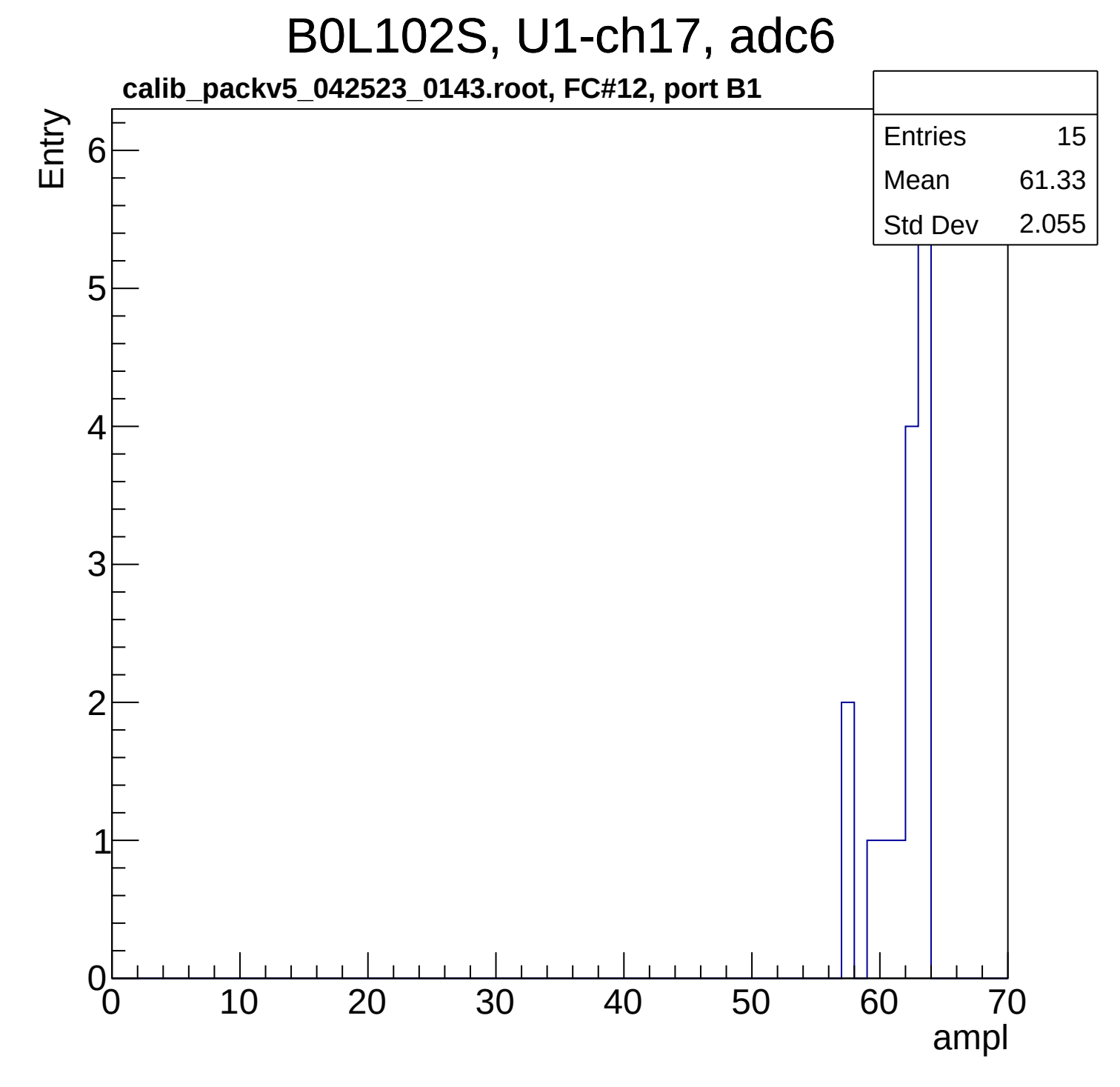
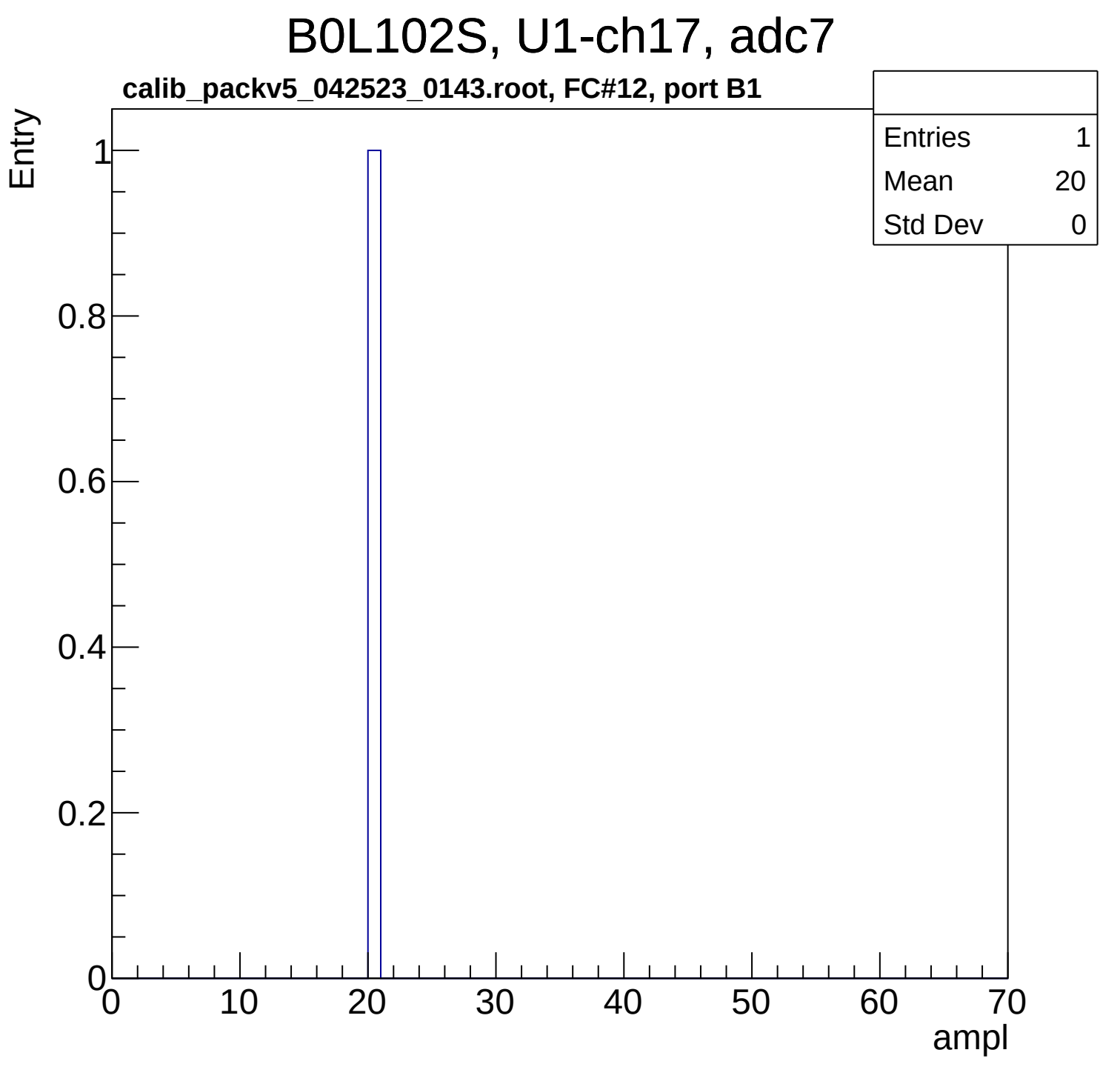
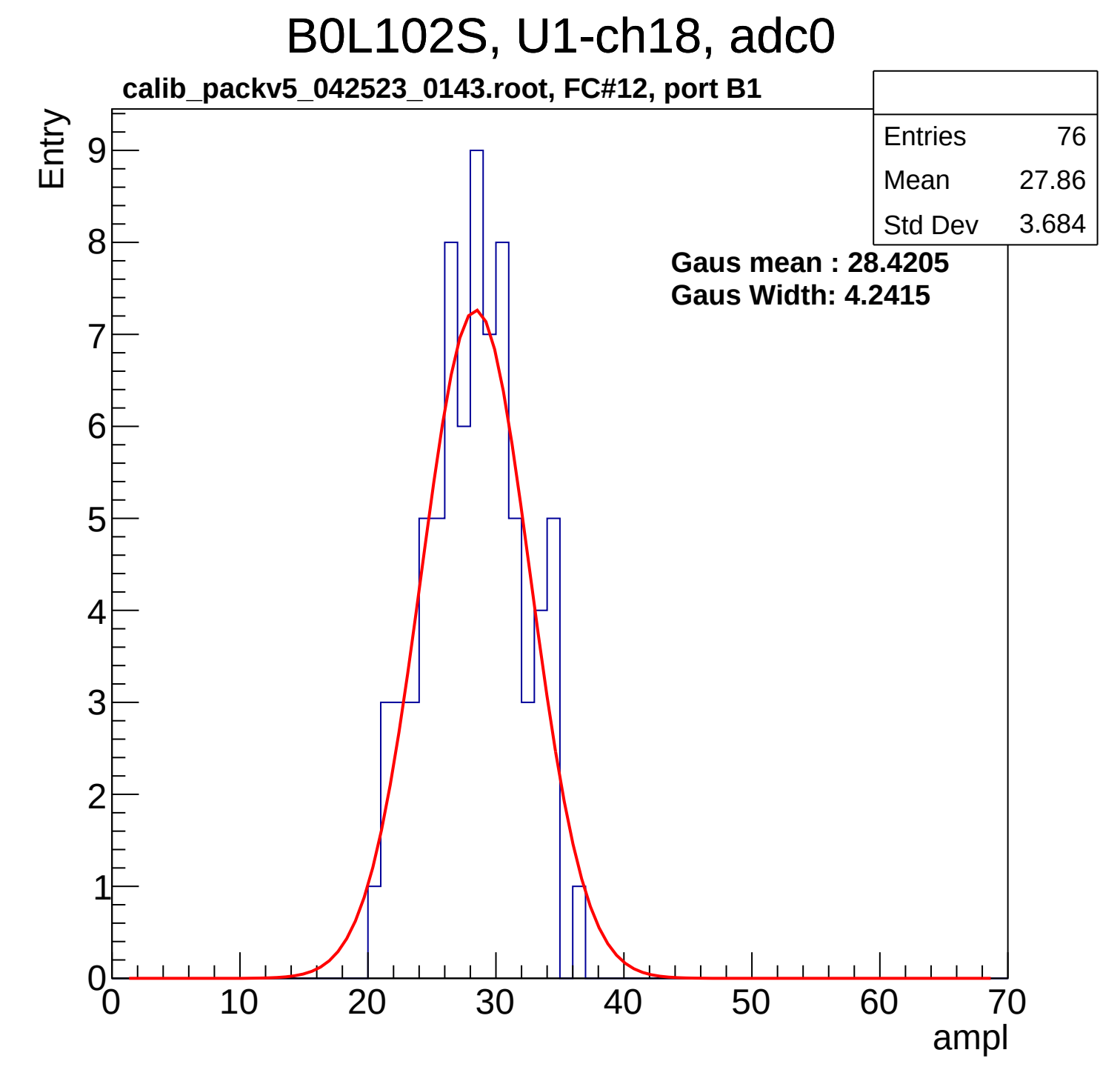
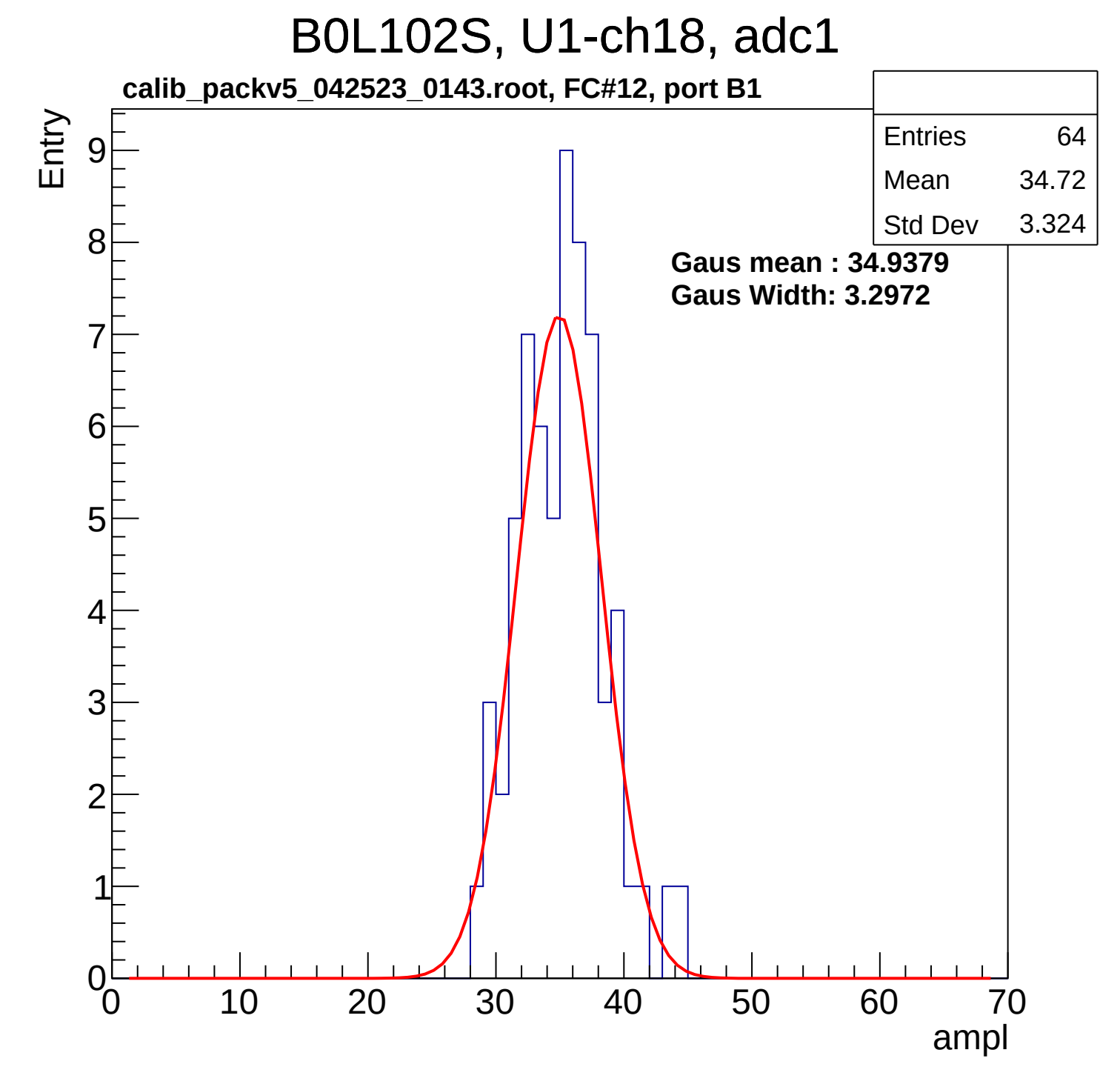
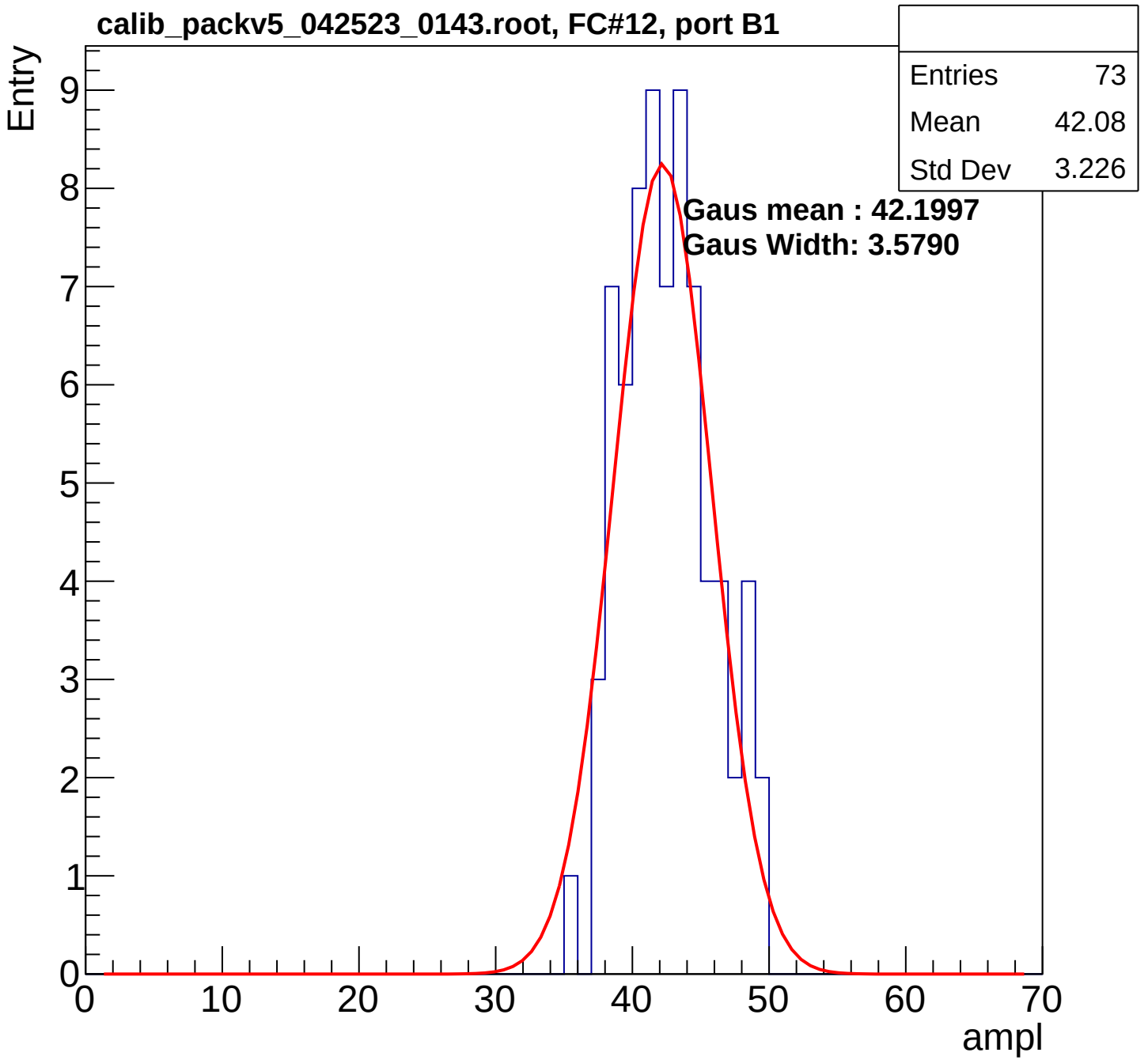
50
60
70
ampl

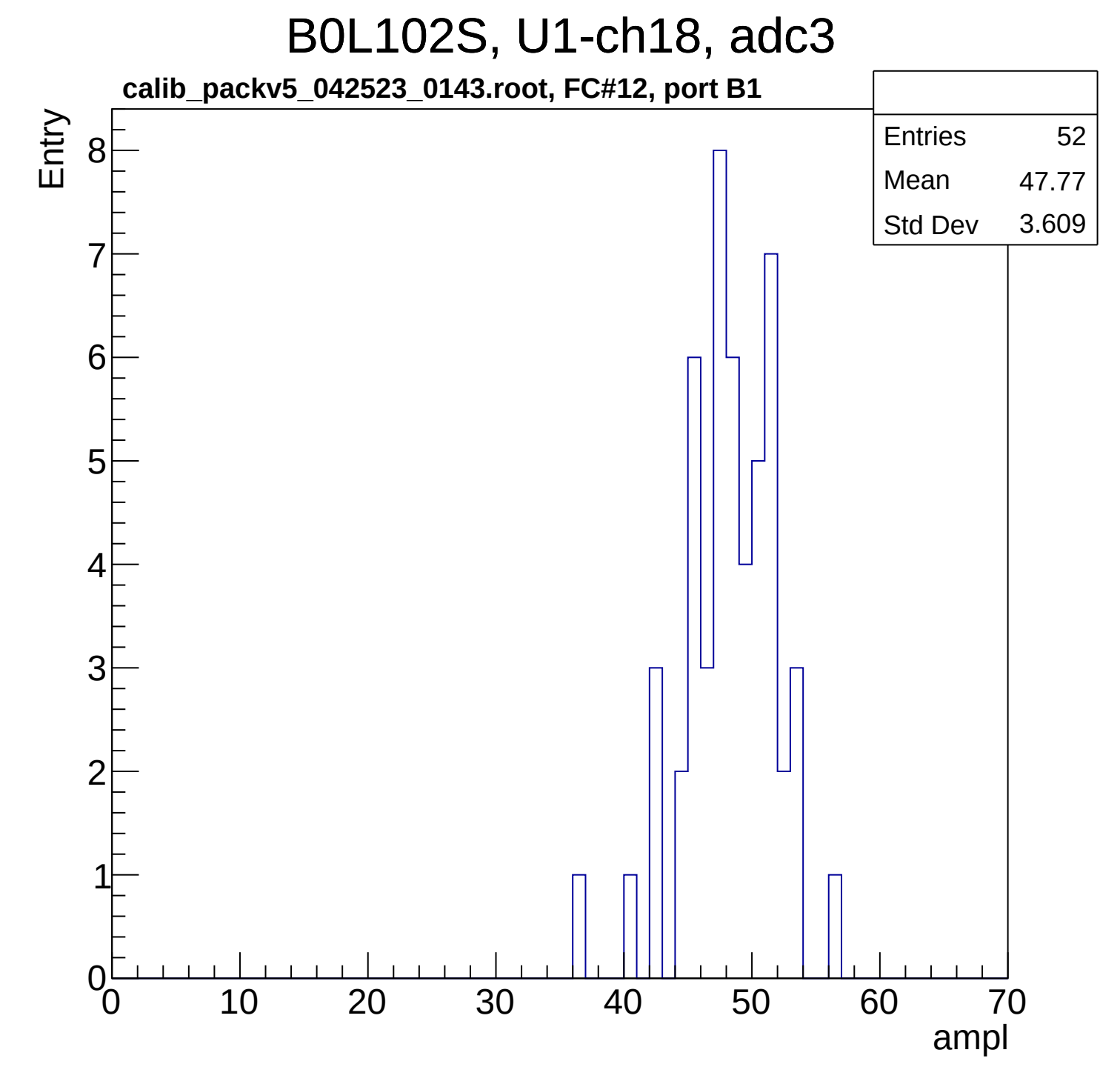

B0L102S, U1-ch17, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 61.33
Std Dev 2.055
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

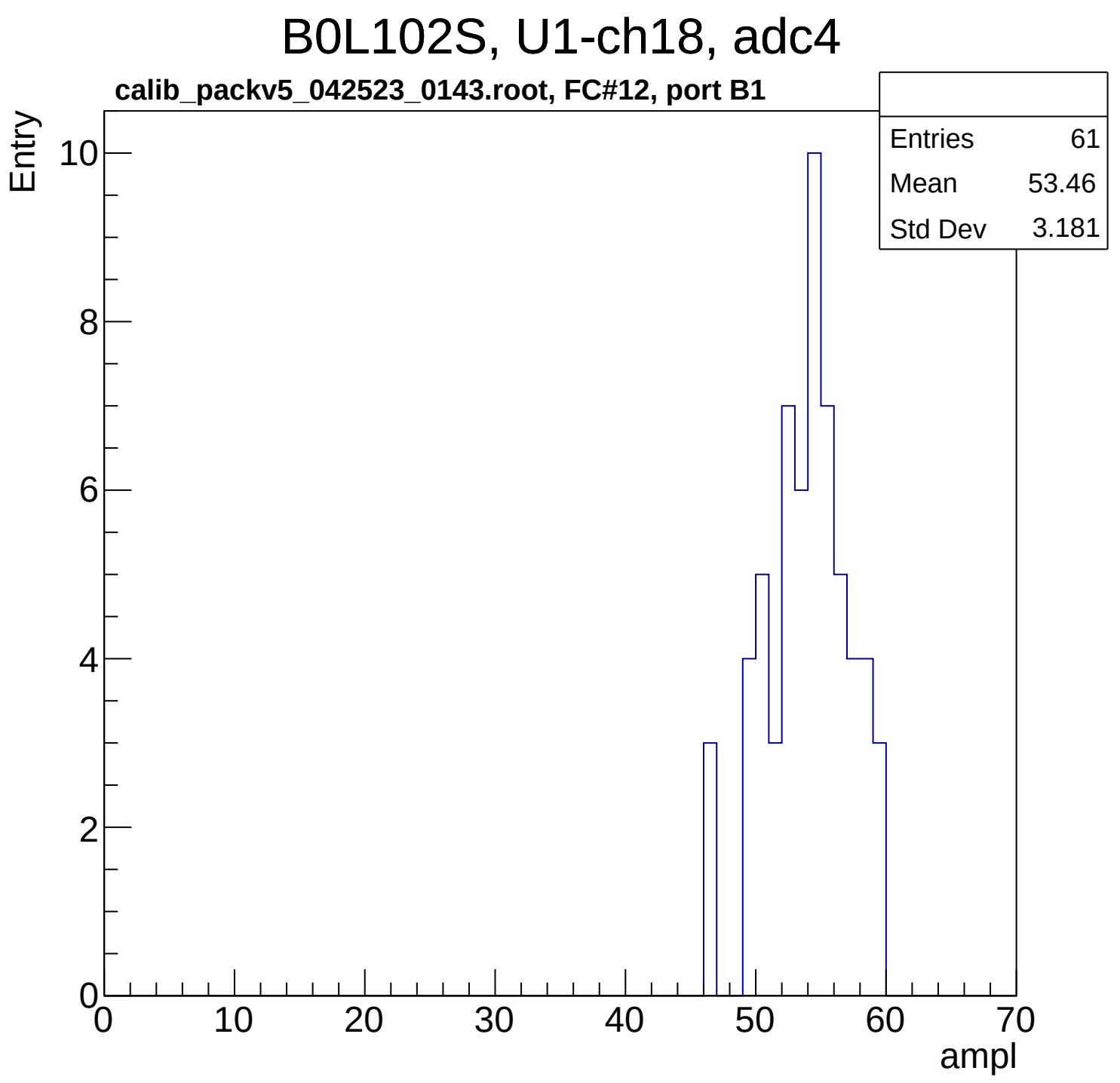

B0L102S, U1-ch17, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

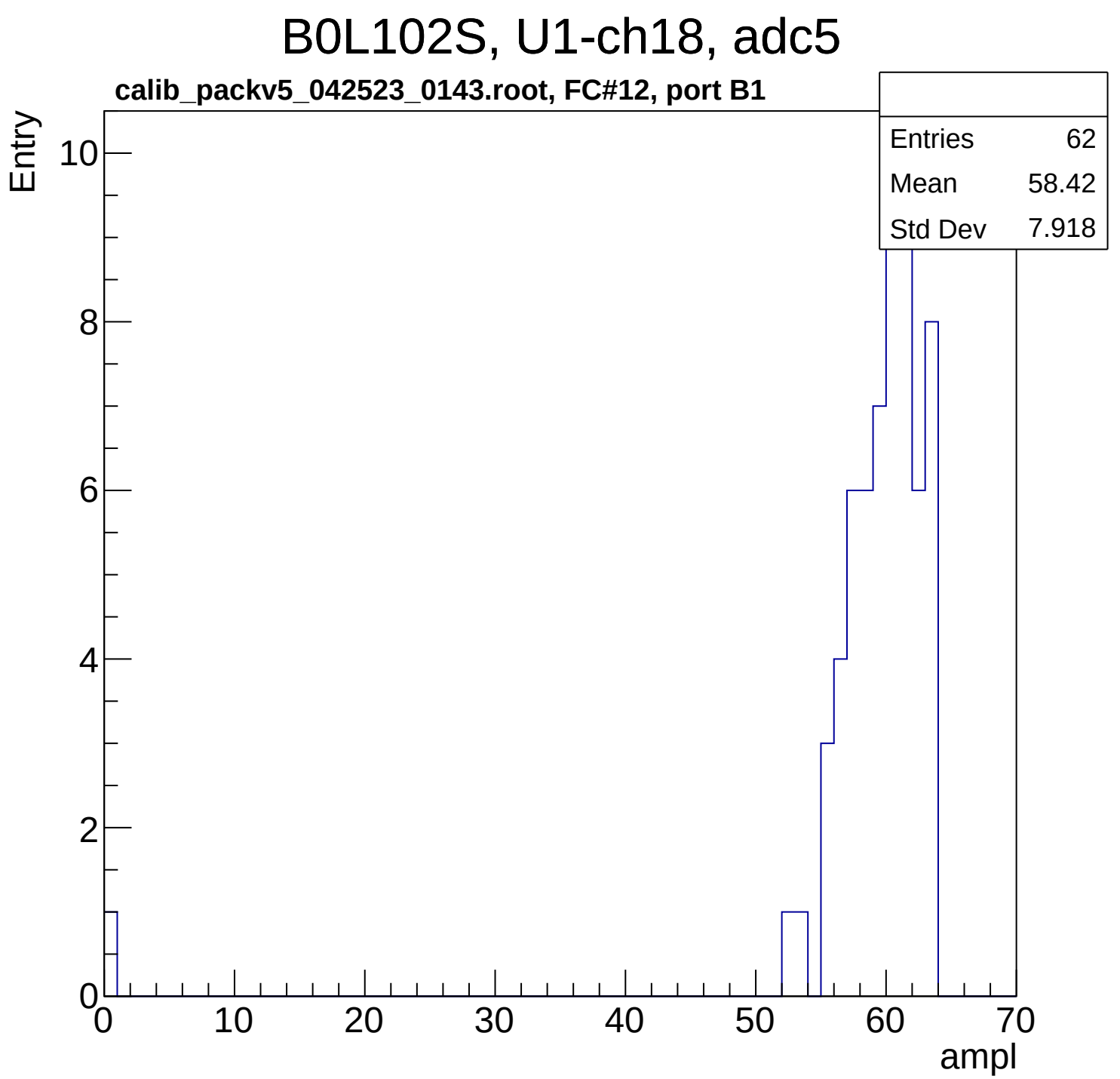

B0L102S, U1-ch18, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 27.86
Std Dev 3.684
Gaus mean : 28.4205
Gaus Width: 4.2415
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

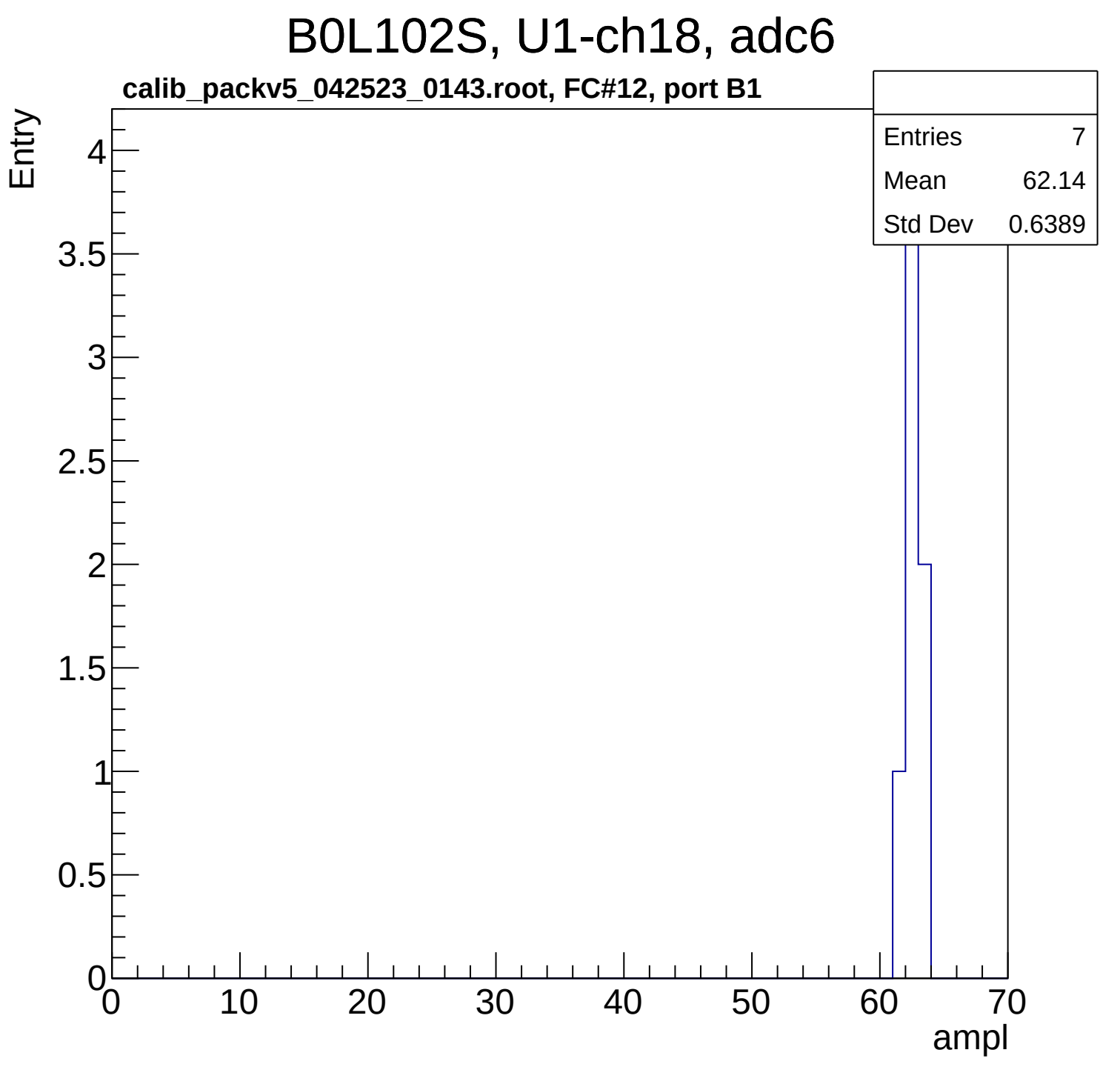

B0L102S, U1-ch18, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 34.72
Std Dev 3.324
Gaus mean : 34.9379
Gaus Width: 3.2972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

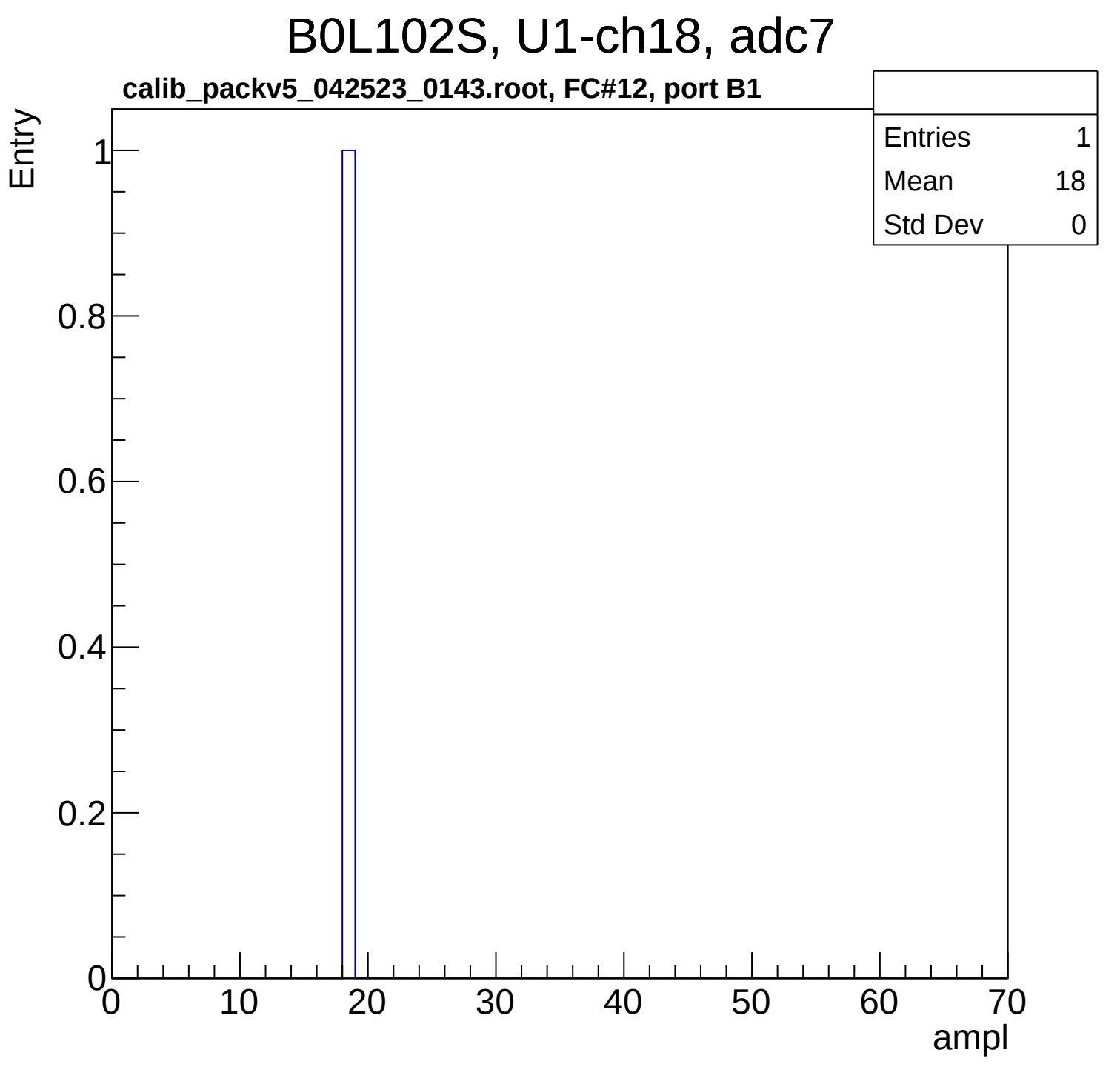

B0L102S, U1-ch18, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 42.08
Std Dev 3.226
Gaus mean : 42.1997
Gaus Width: 3.5790
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch18, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 47.77
Std Dev 3.609
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch18, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 53.46
Std Dev 3.181
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch18, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 58.42
Std Dev 7.918
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch18, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.14
Std Dev 0.6389
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch18, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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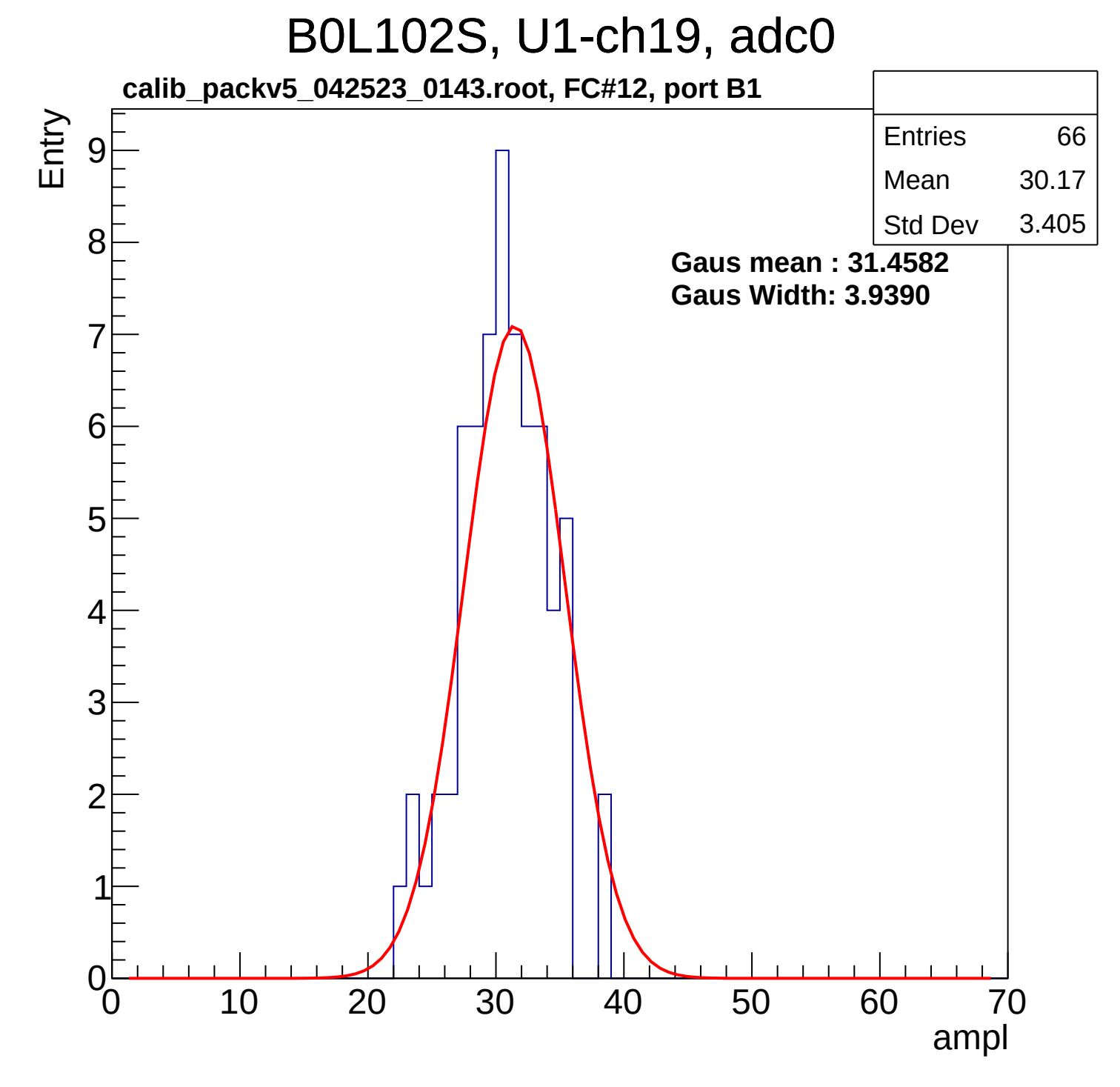
50
60
70
ampl


B0L102S, U1-ch19, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 30.17
Std Dev 3.405
Gaus mean : 31.4582
Gaus Width: 3.9390
Entry
ampl
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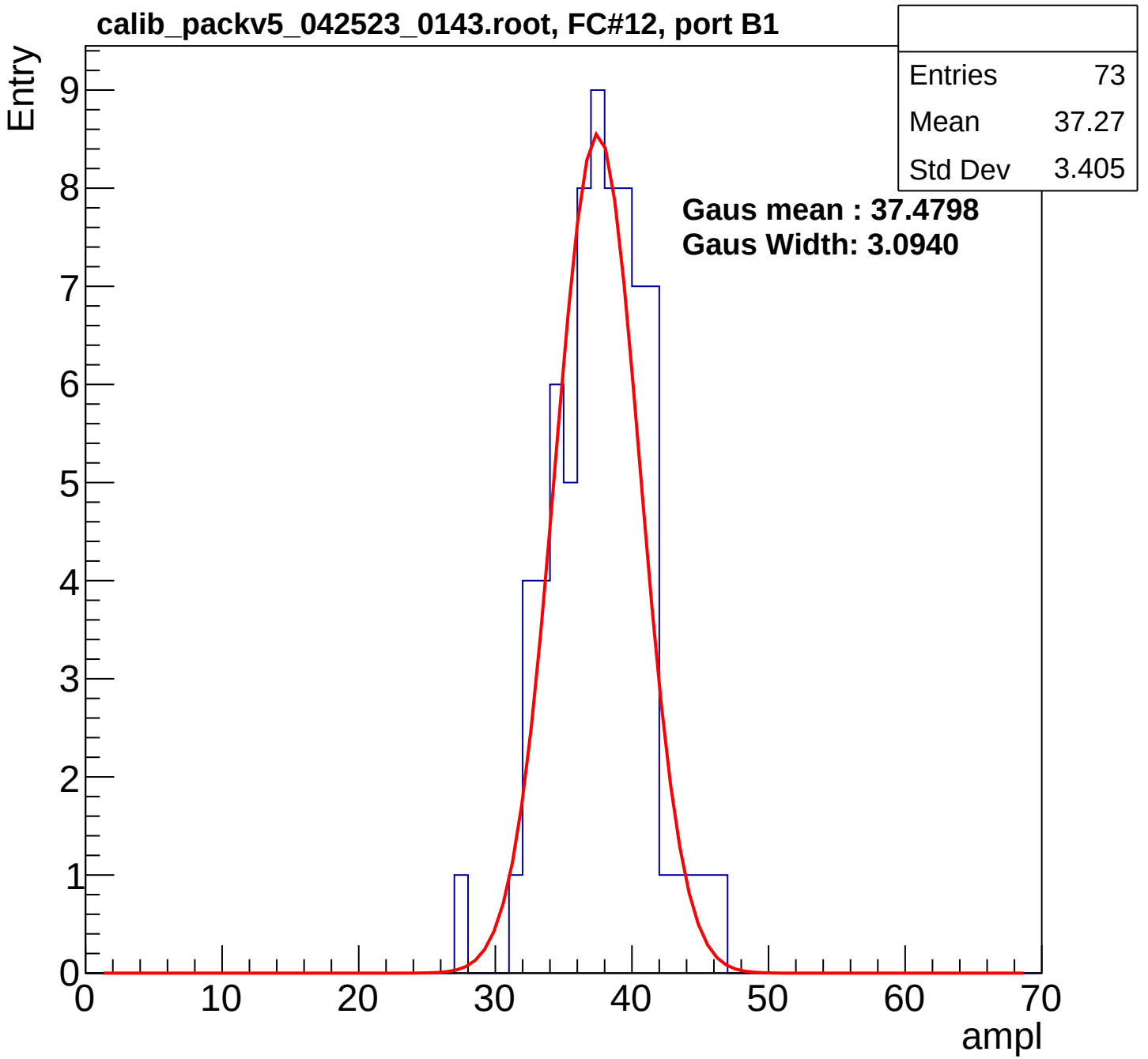
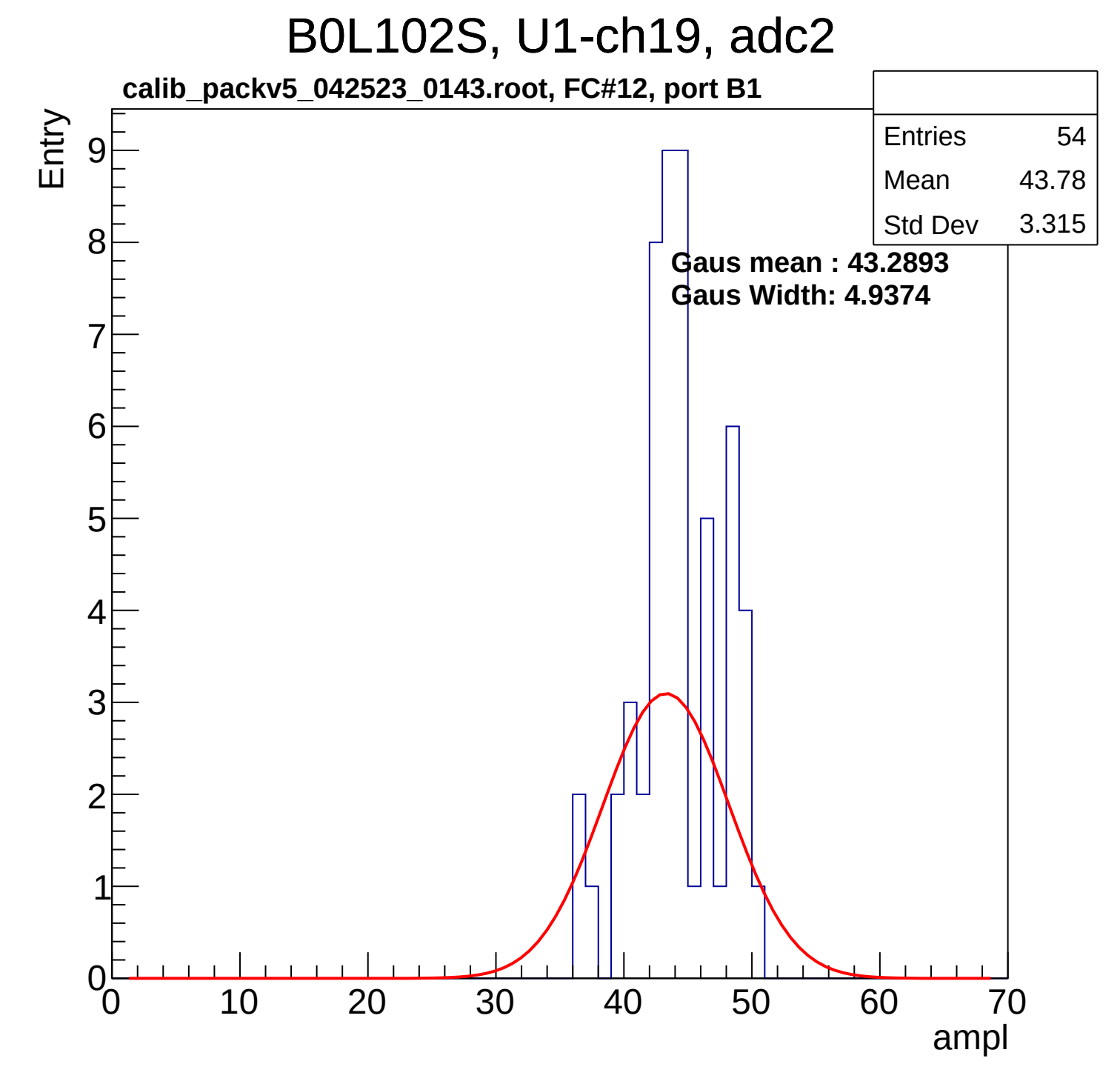
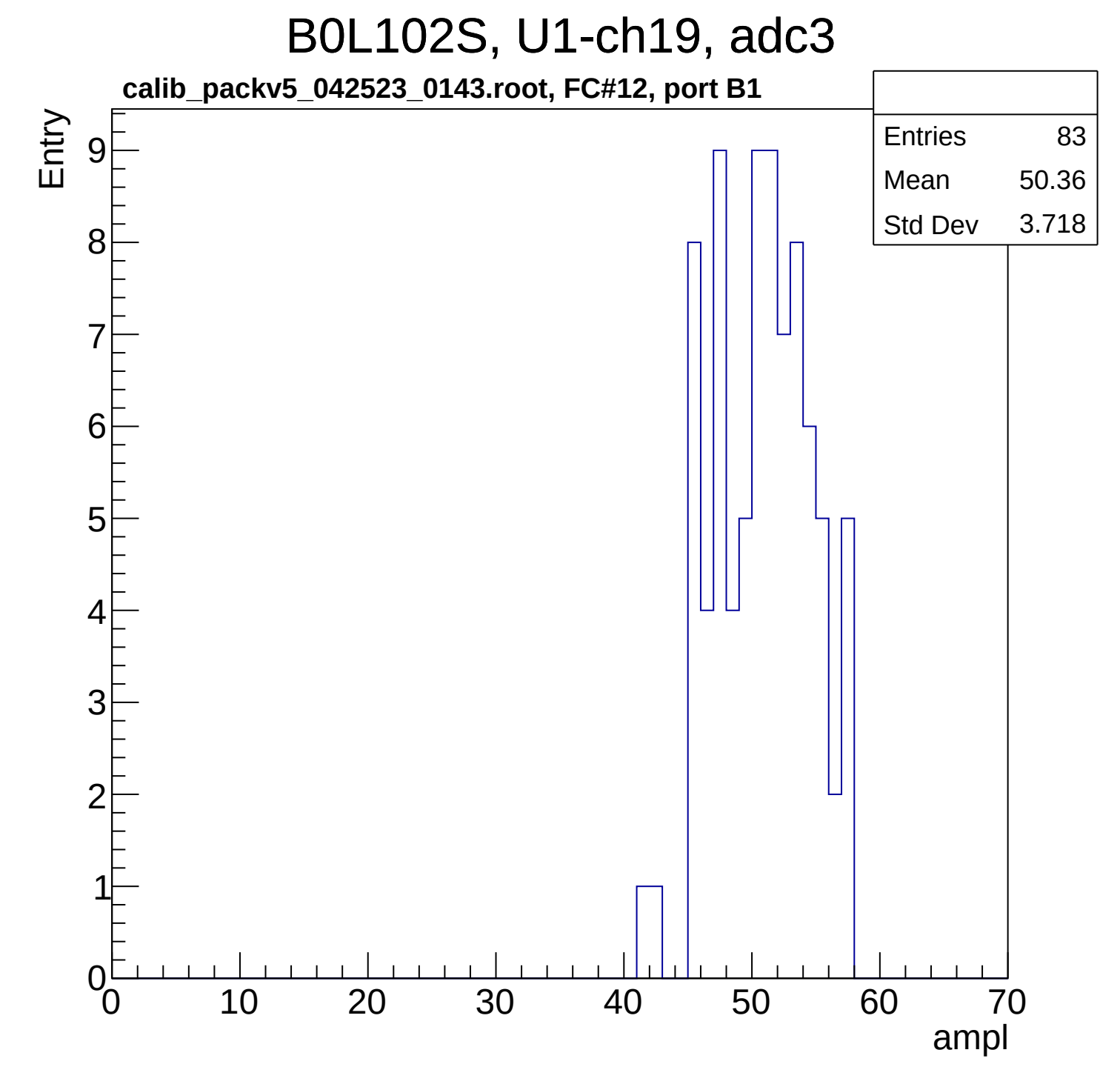
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch19, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 37.27
Std Dev 3.405
Gaus mean : 37.4798
Gaus Width: 3.0940
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch19, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43.78
Std Dev 3.315
Gaus mean : 43.2893
Gaus Width: 4.9374
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch19, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 50.36
Std Dev 3.718
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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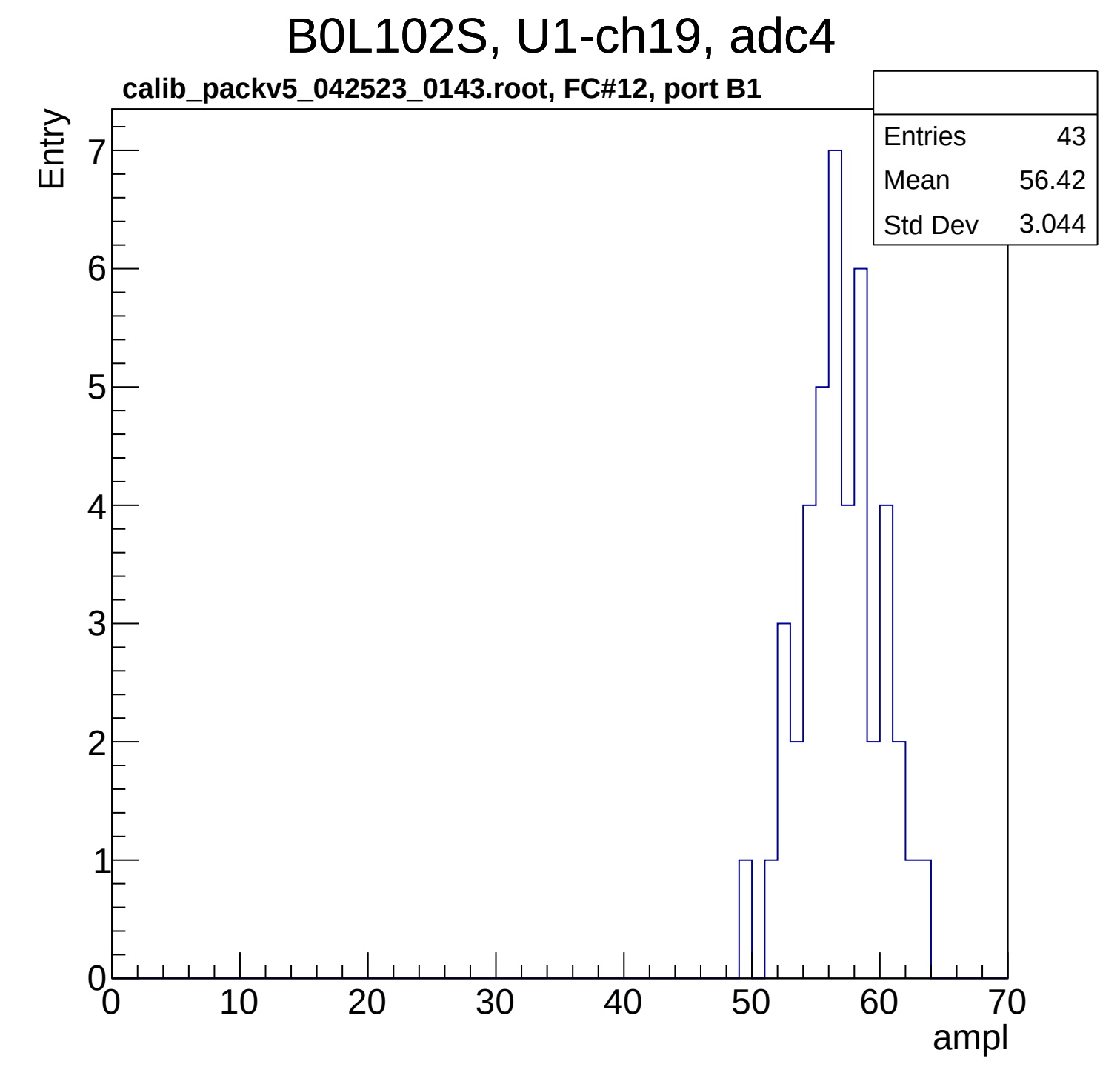
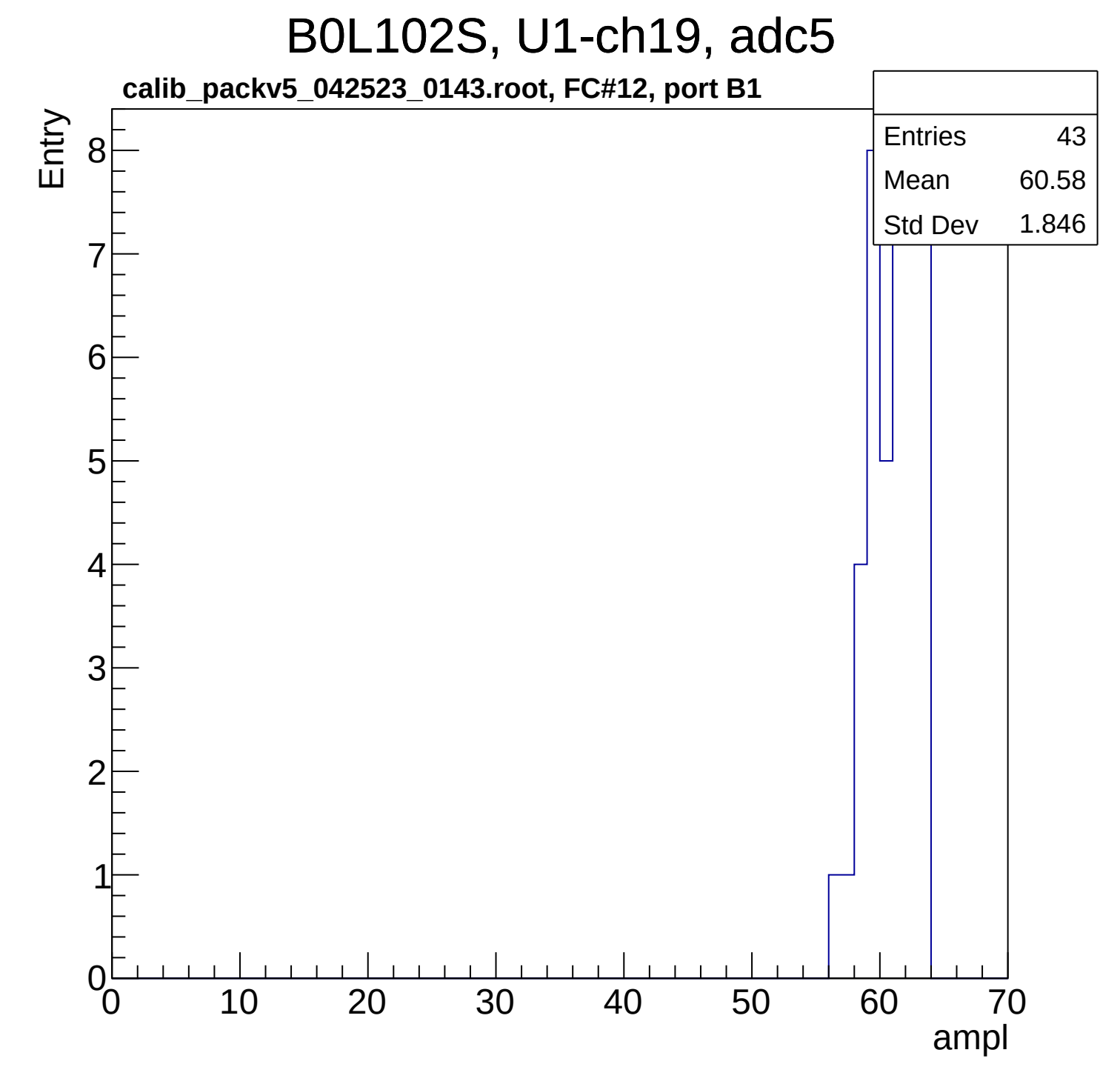

B0L102S, U1-ch19, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 56.42
Std Dev 3.044
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch19, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 60.58
Std Dev 1.846
Entry
ampl
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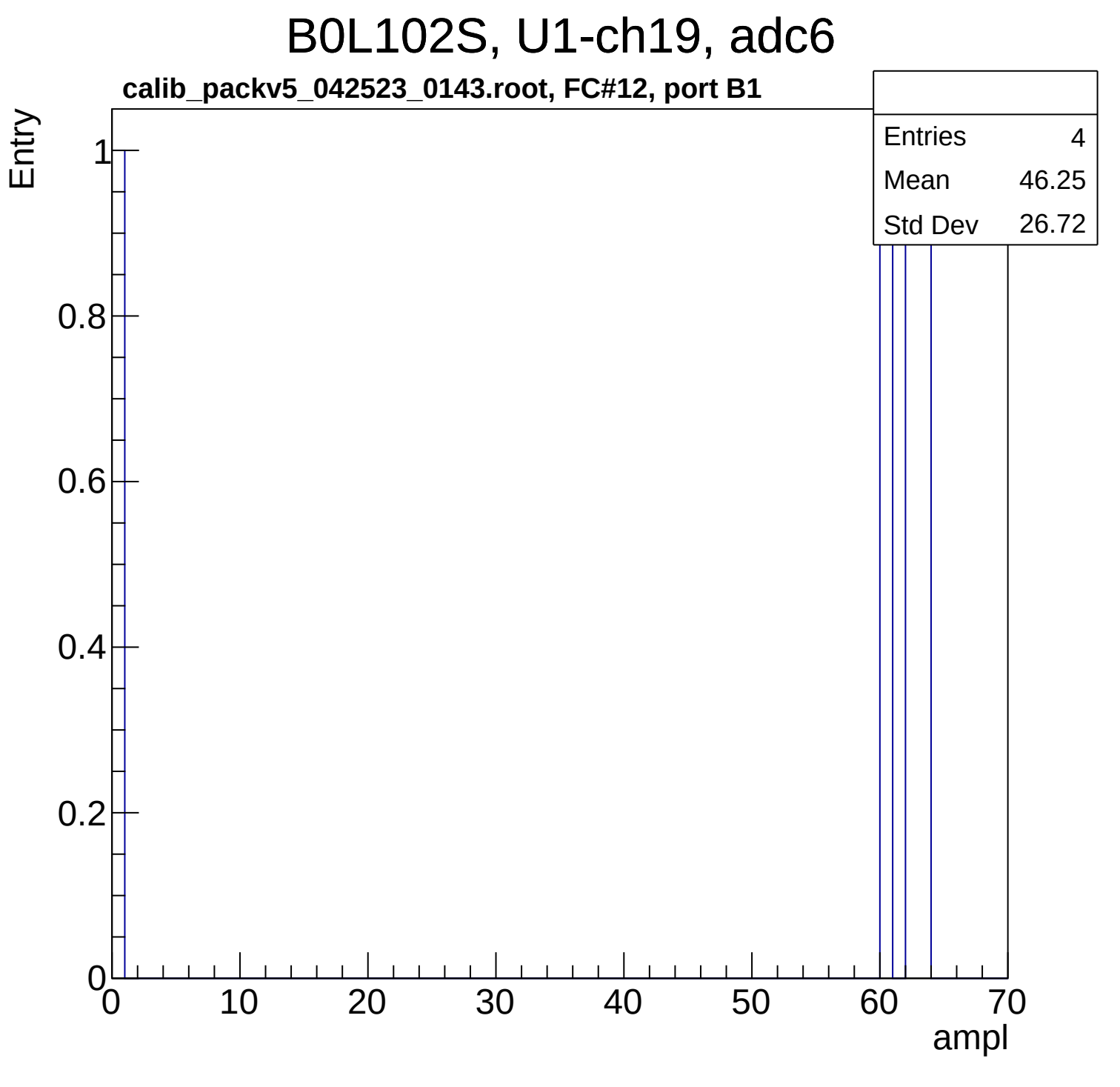
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch19, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 46.25
Std Dev 26.72
Entry
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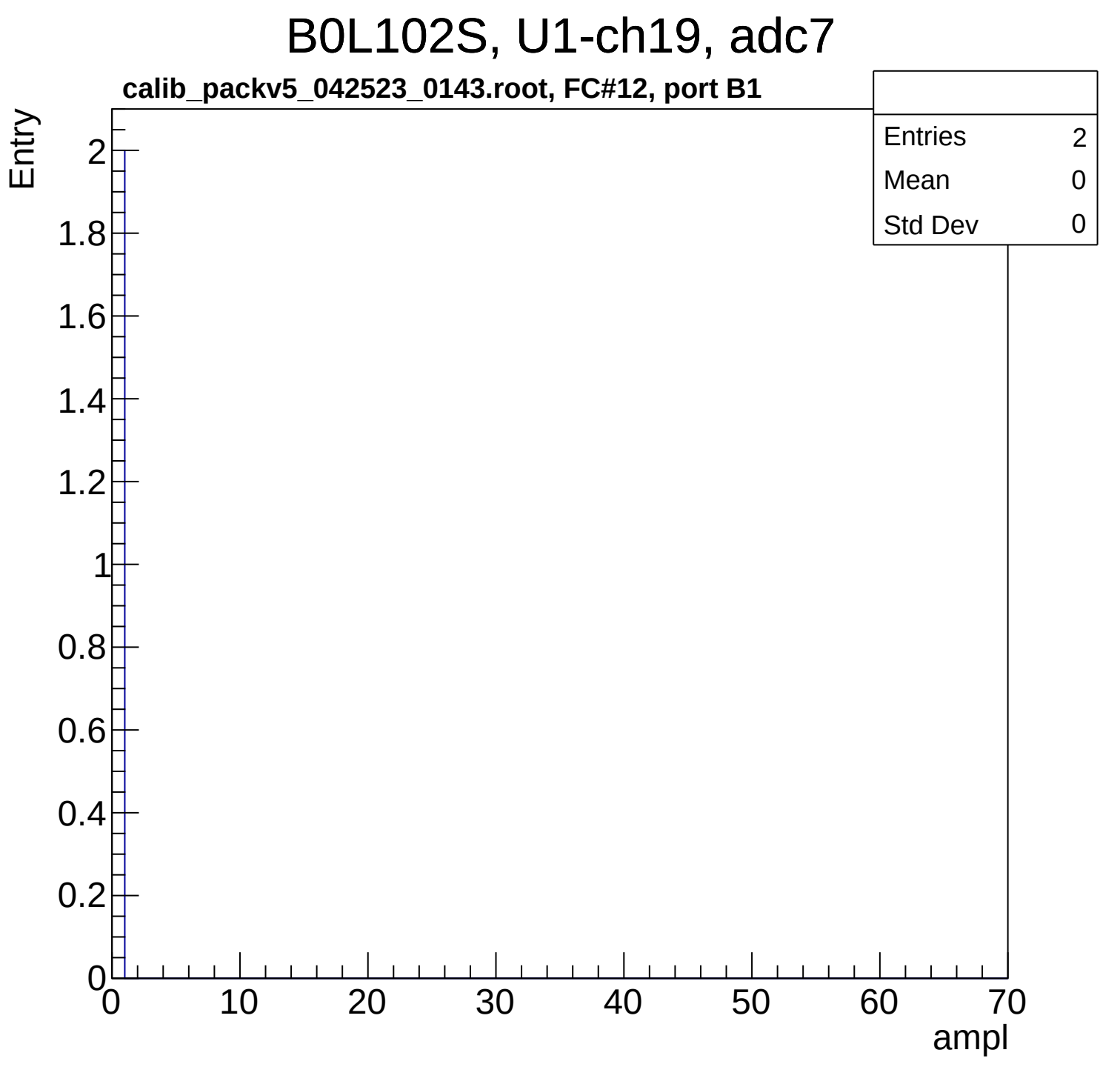
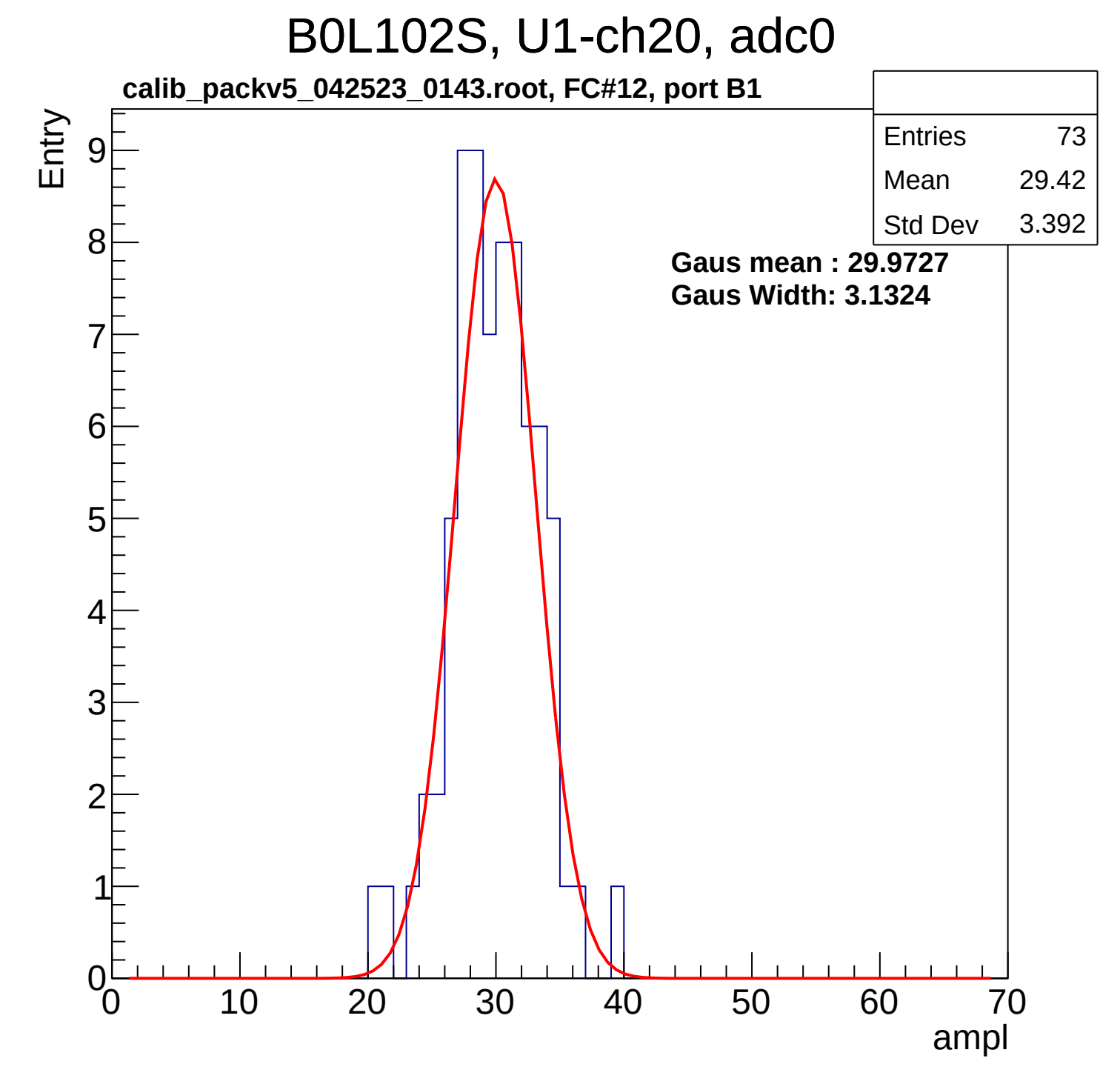
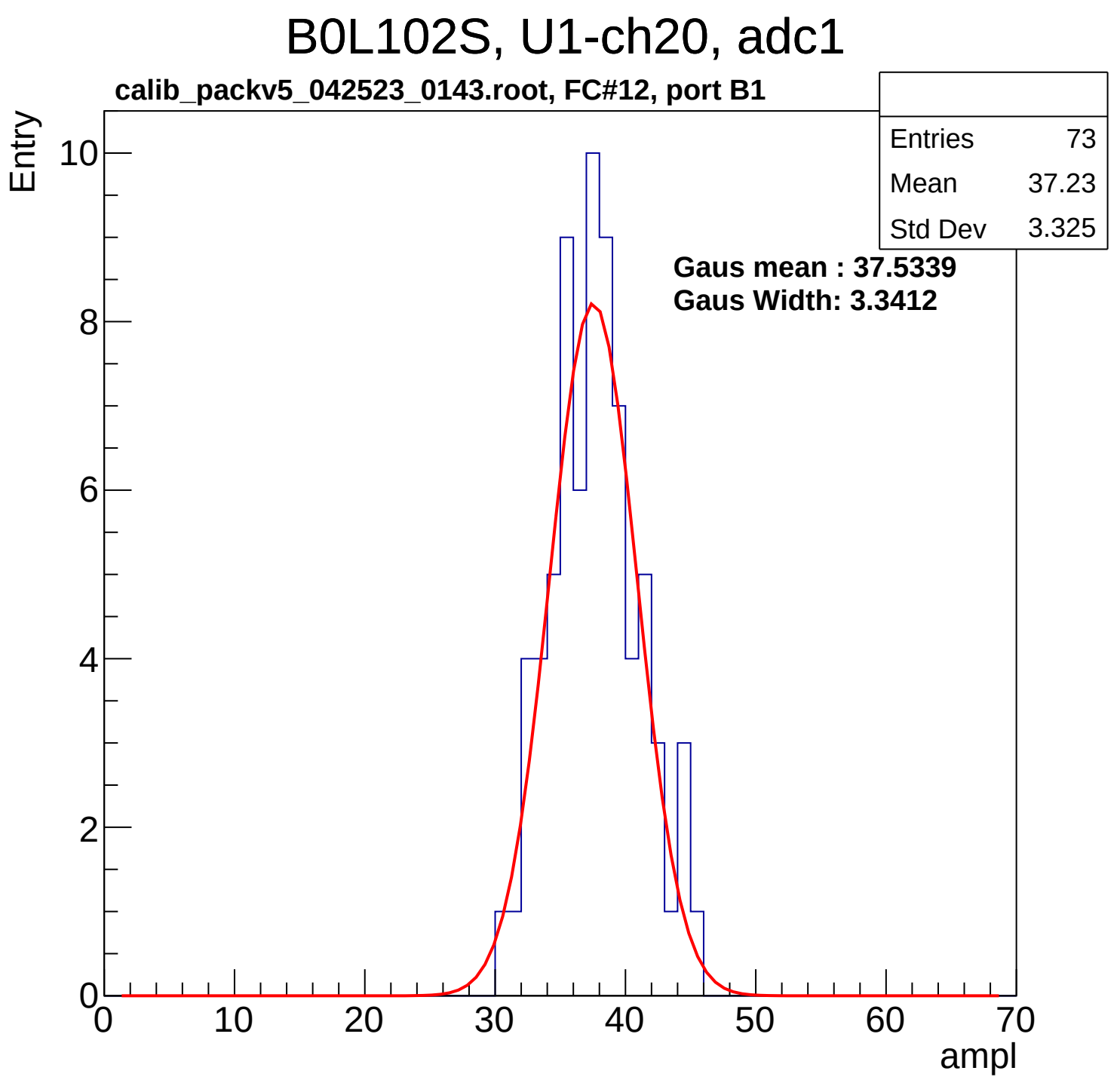
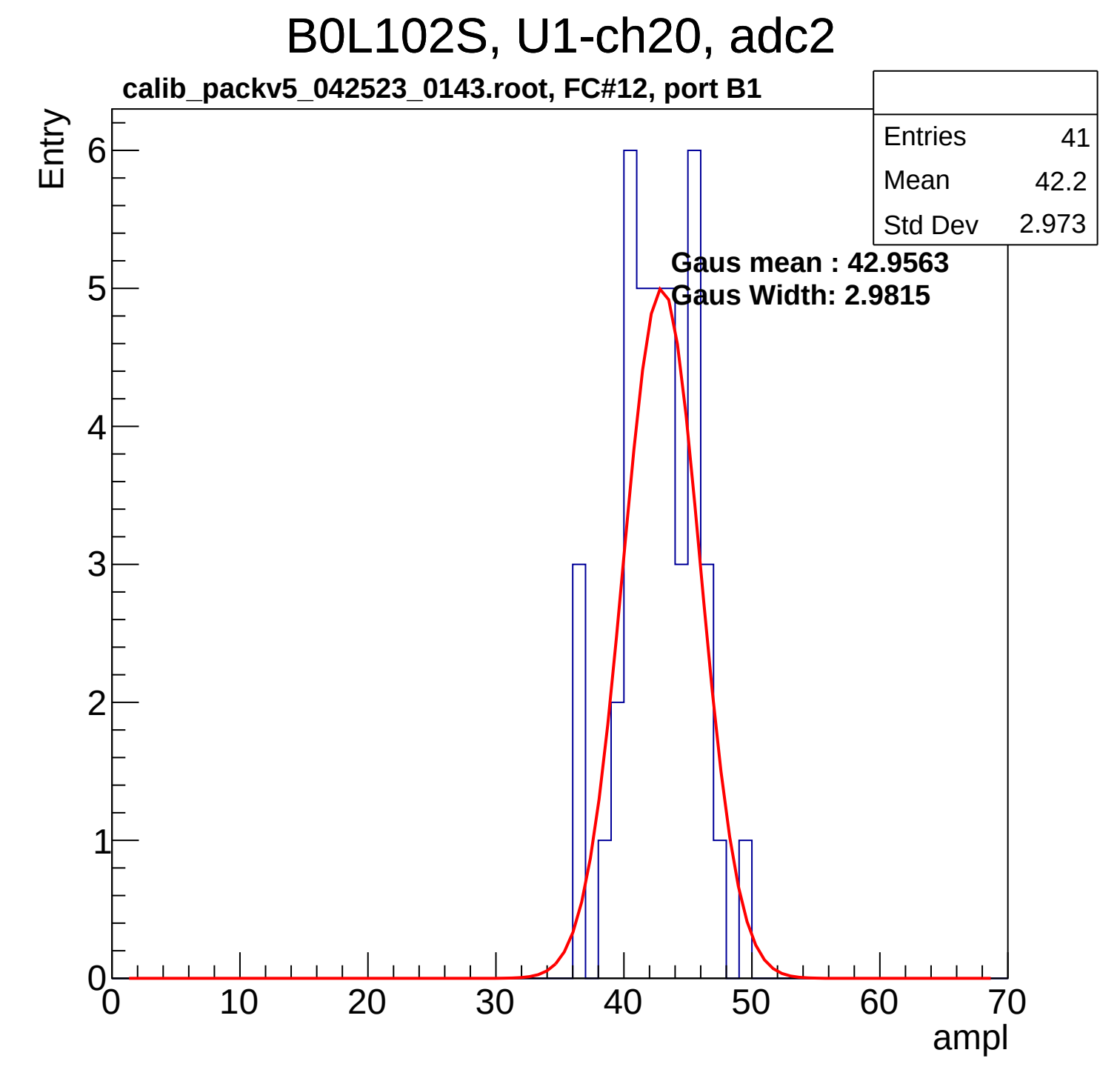
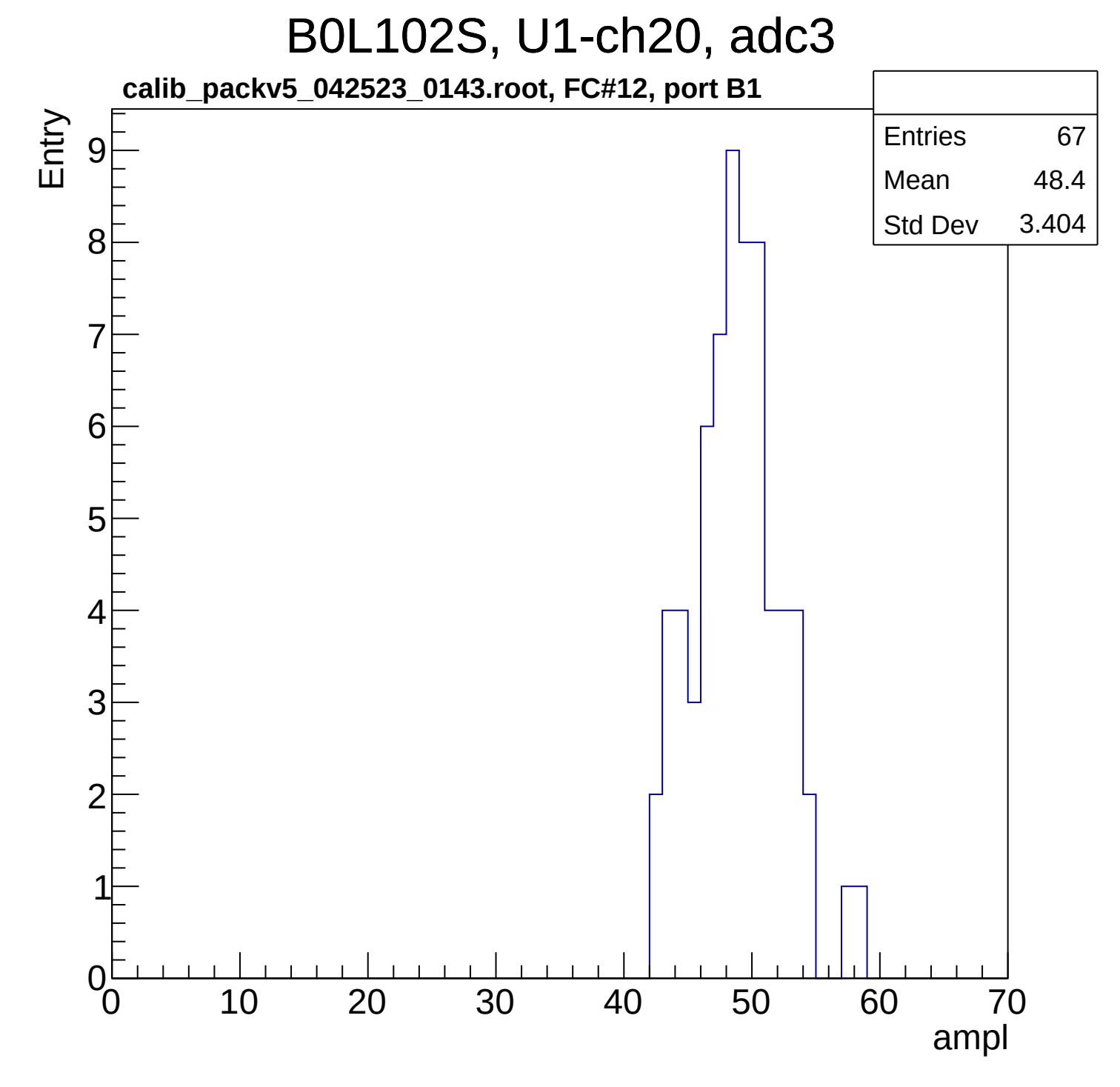
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

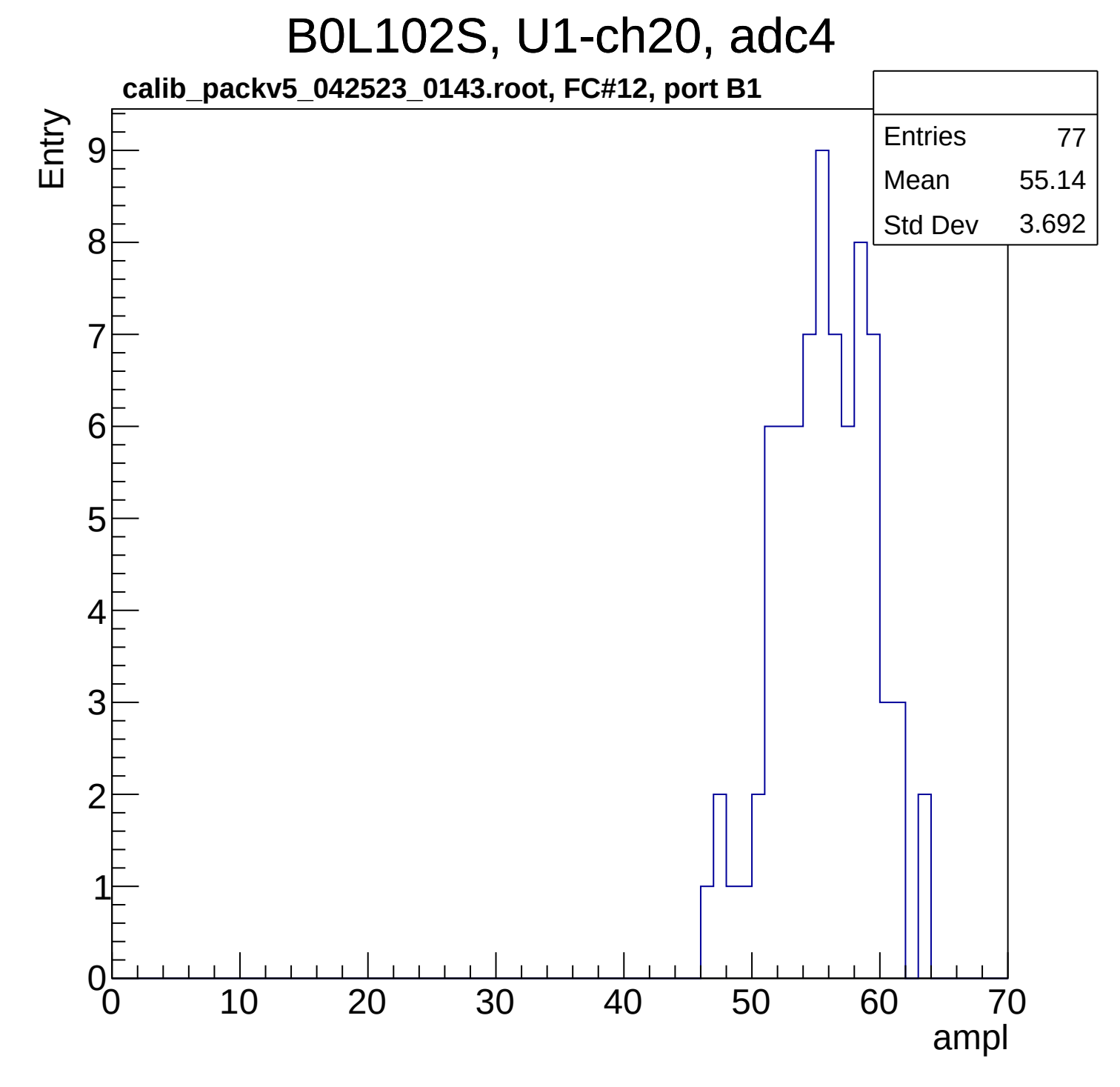

B0L102S, U1-ch19, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch20, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.42
Std Dev 3.392
Gaus mean : 29.9727
Gaus Width: 3.1324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch20, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 37.23
Std Dev 3.325
Gaus mean : 37.5339
Gaus Width: 3.3412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch20, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 42.2
Std Dev 2.973
Gaus mean : 42.9563
Gaus Width: 2.9815
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U1-ch20, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 48.4
Std Dev 3.404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch20, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 55.14
Std Dev 3.692
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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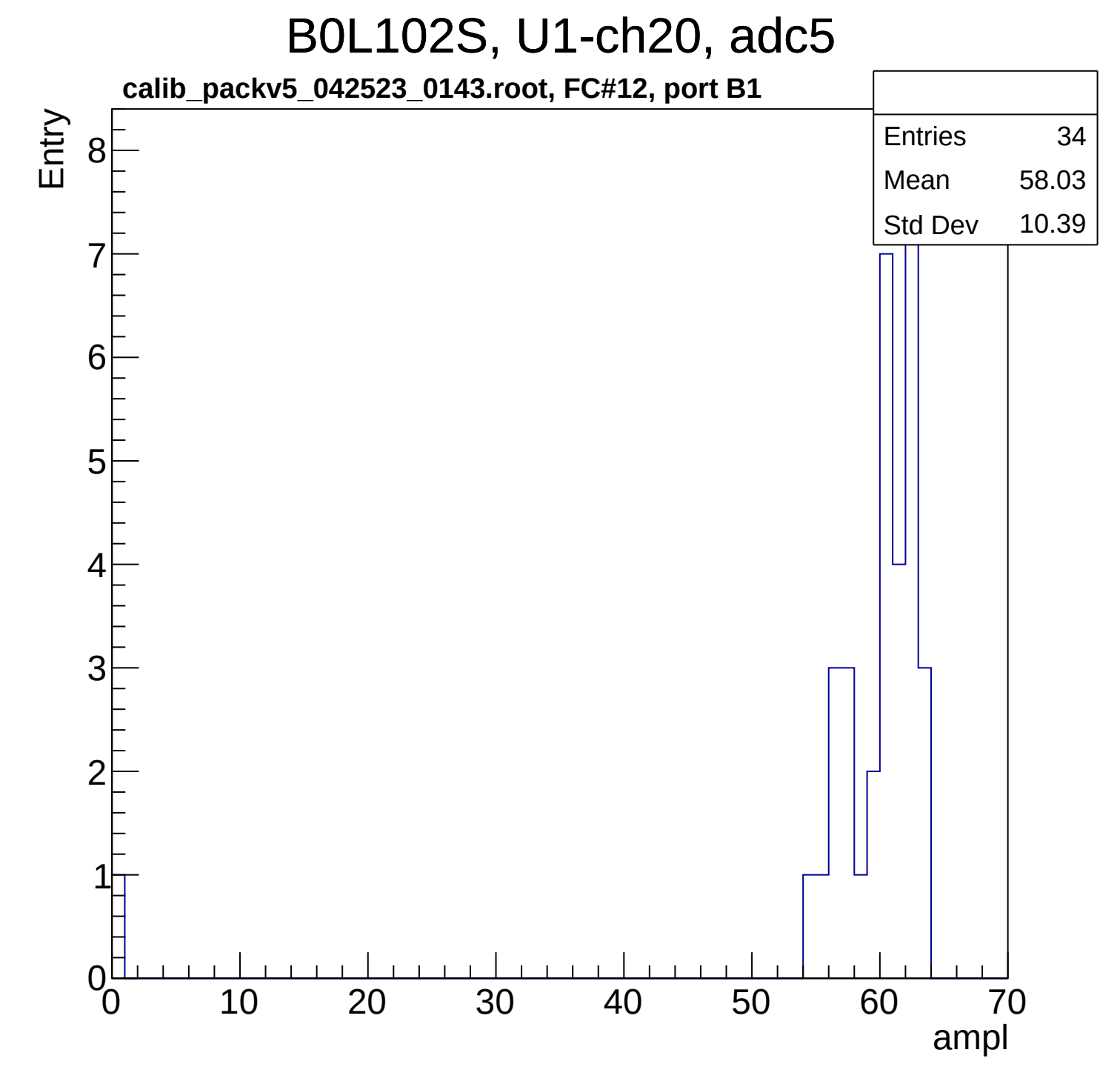
B0L102S, U1-ch20, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 58.03
Std Dev 10.39
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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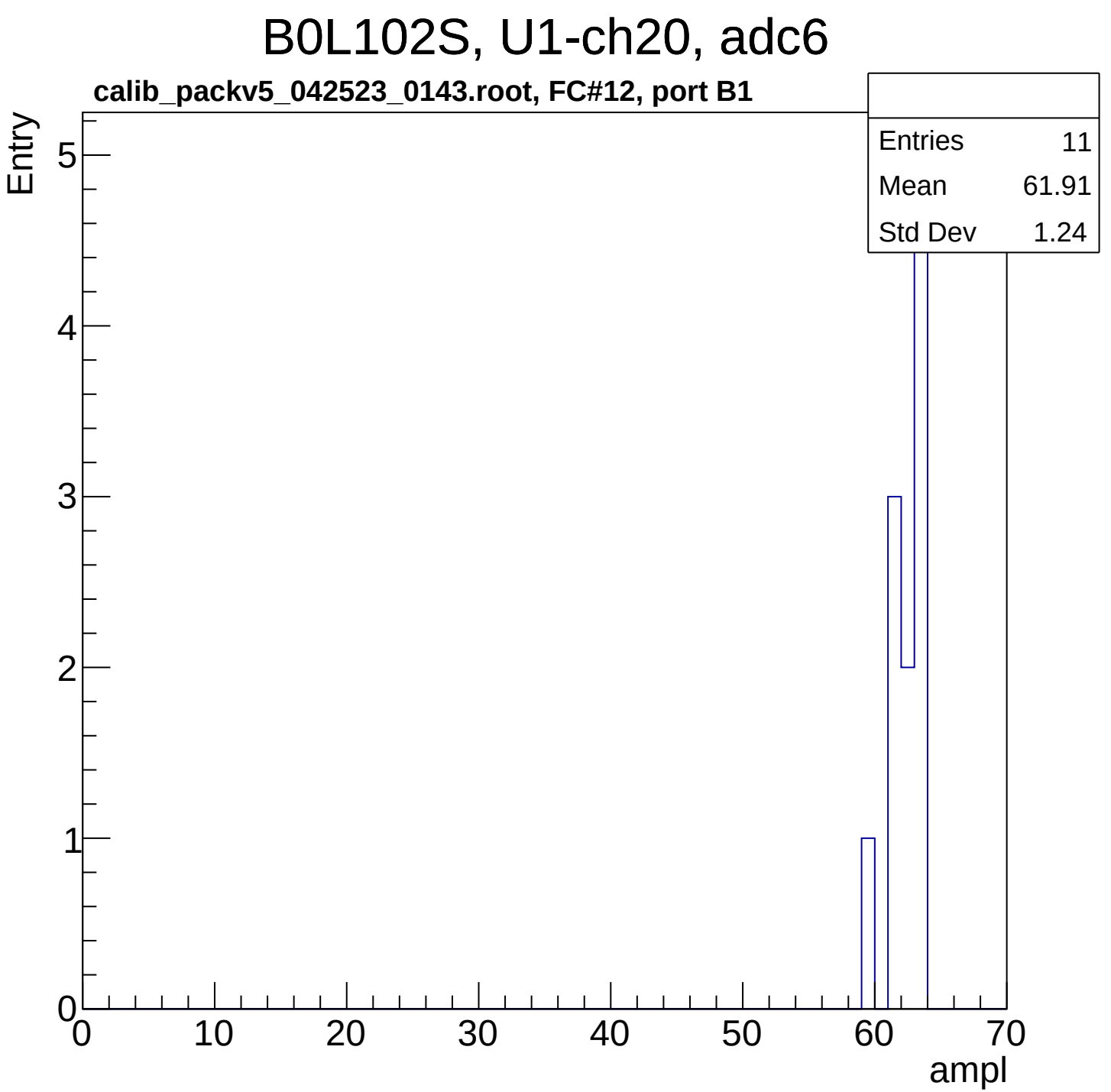
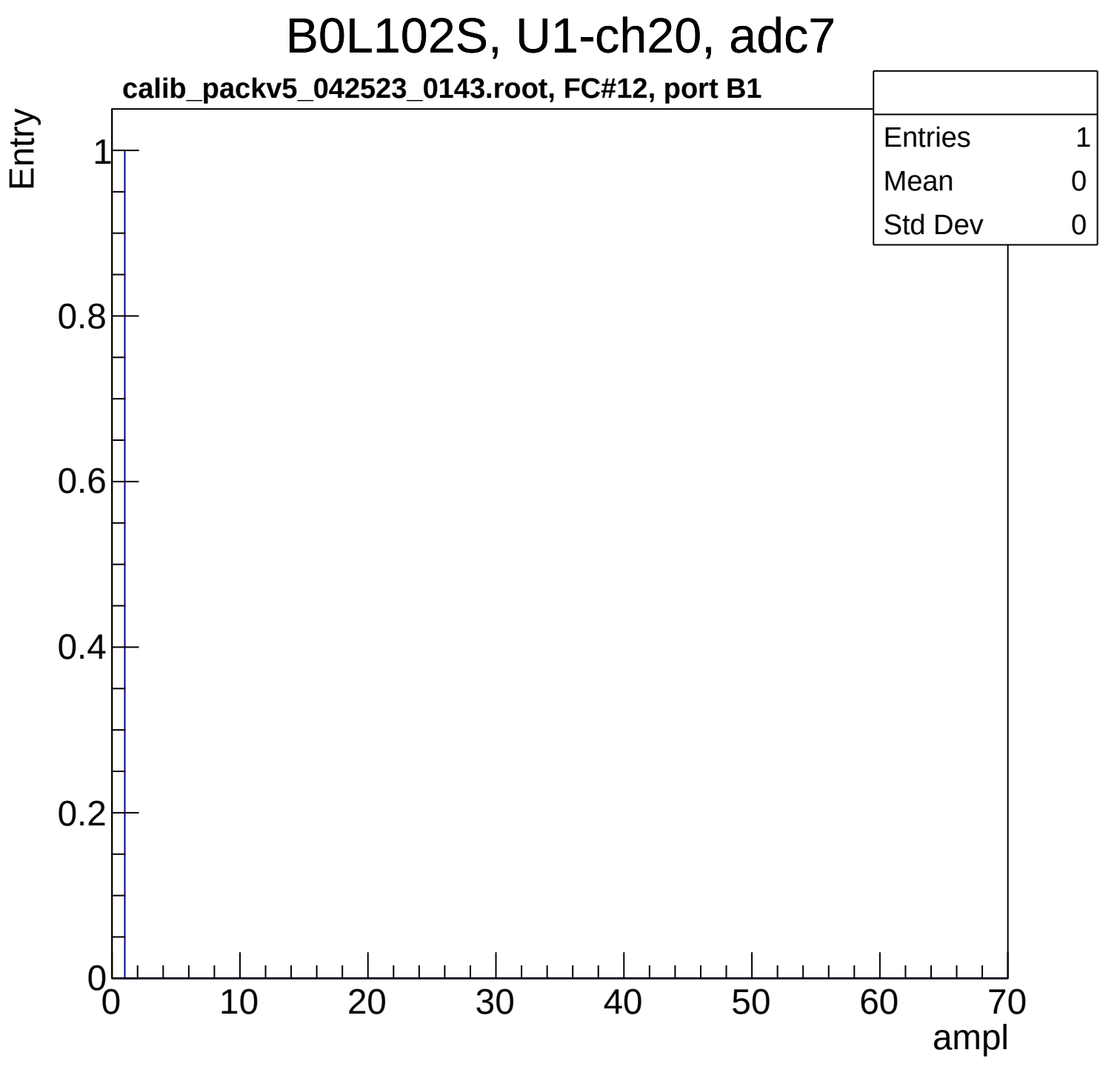
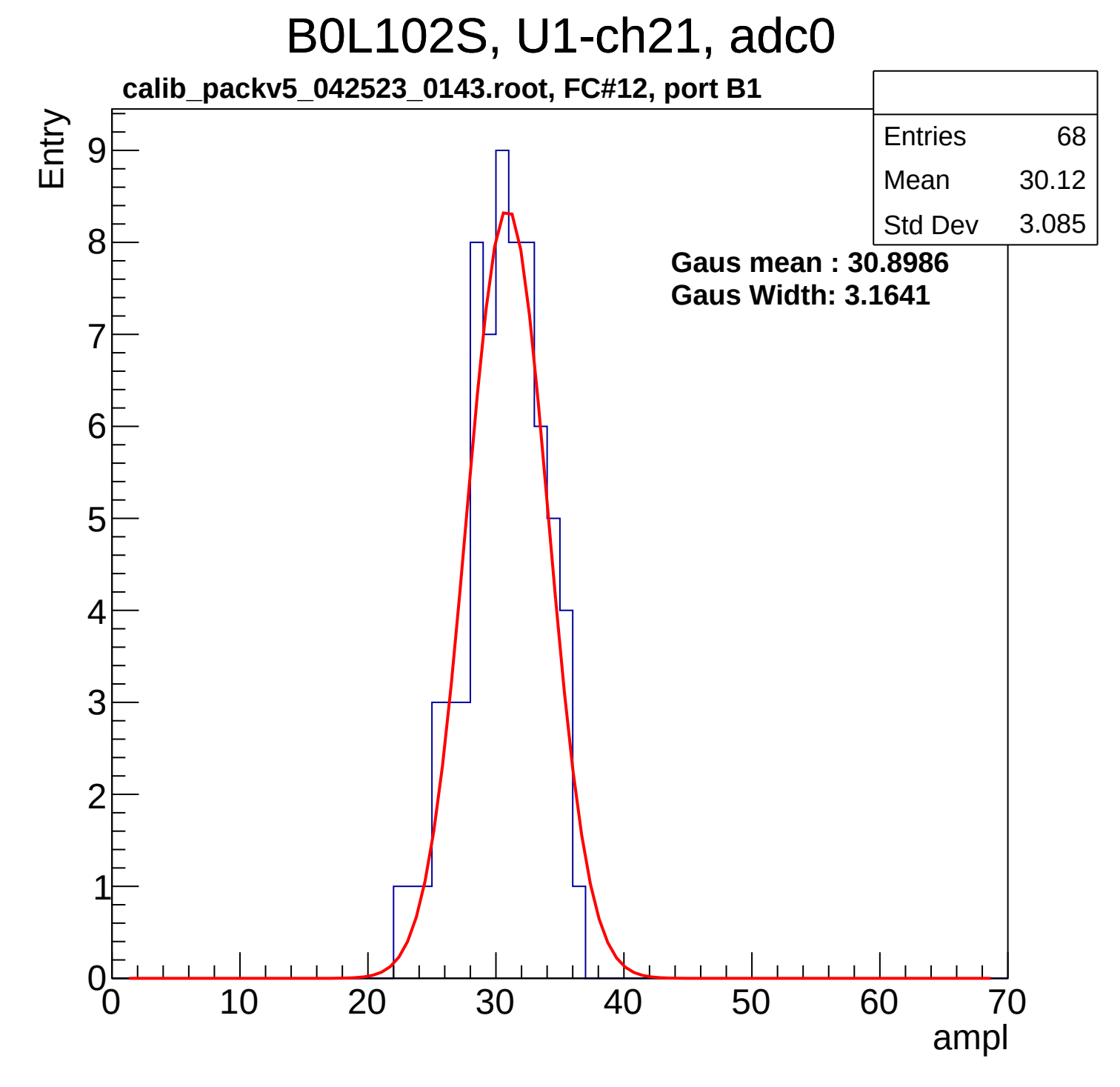

B0L102S, U1-ch20, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61.91
Std Dev 1.24
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch20, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch21, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 30.12
Std Dev 3.085
Gaus mean : 30.8986
Gaus Width: 3.1641
Entry
ampl
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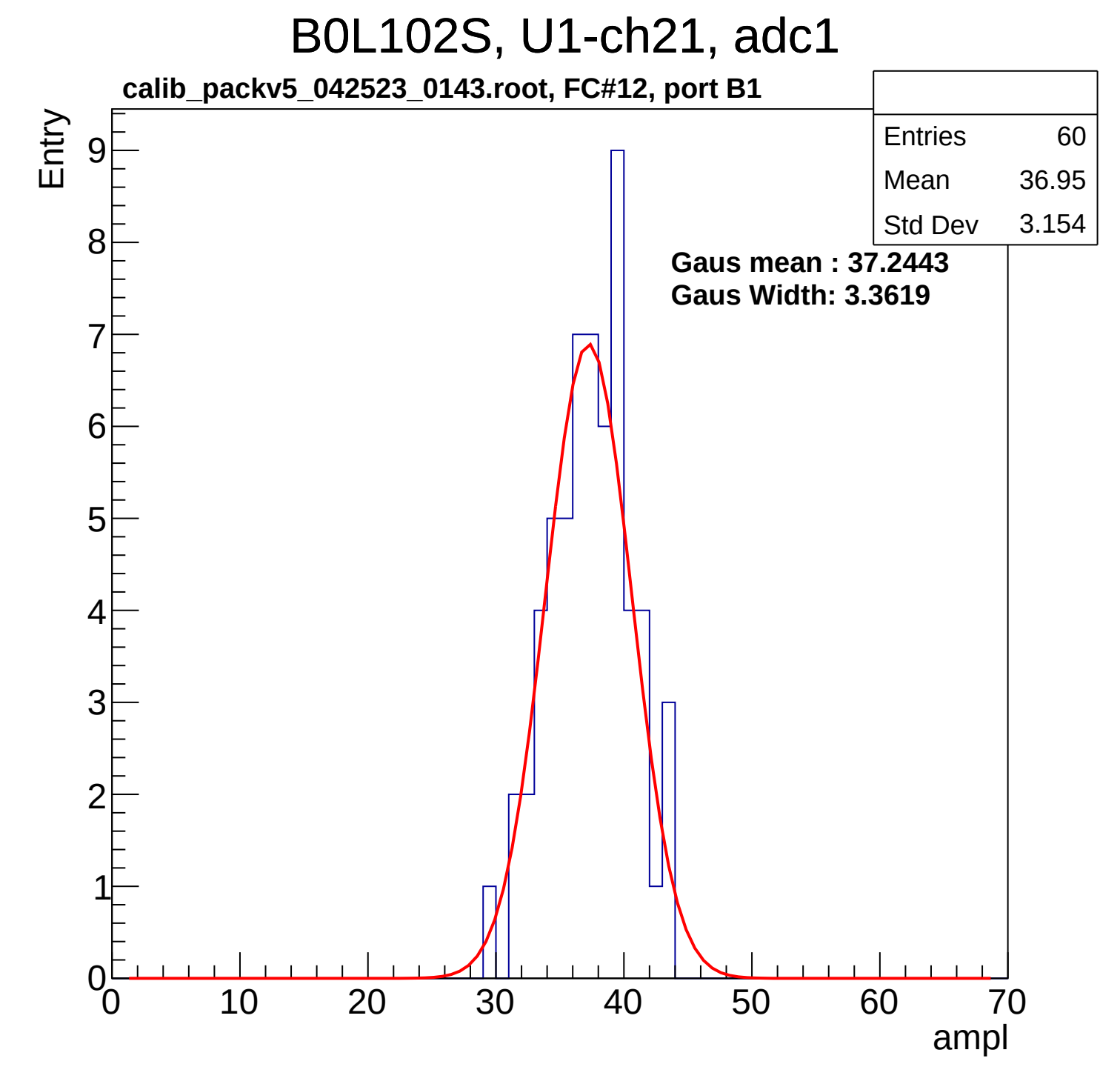
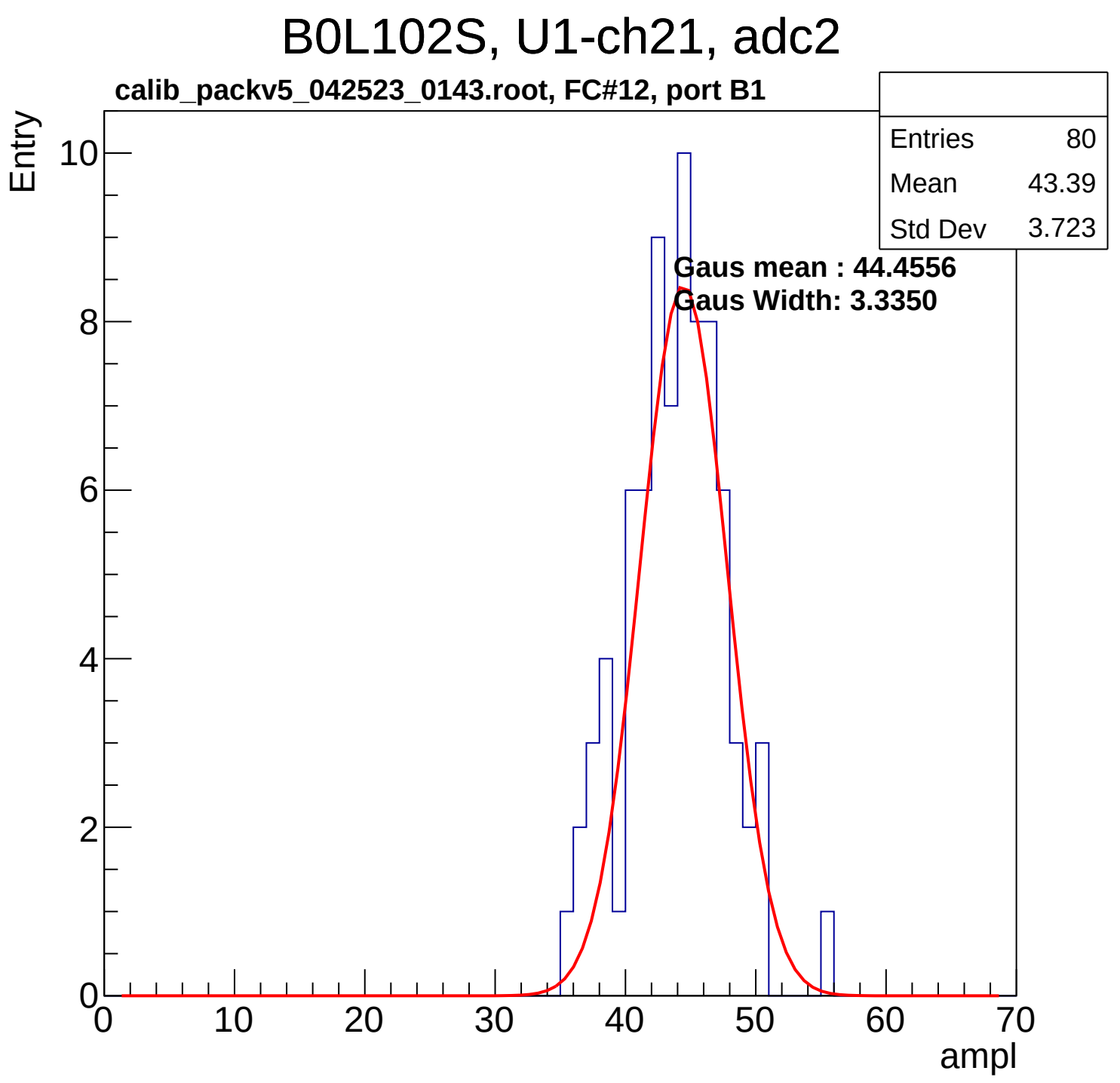
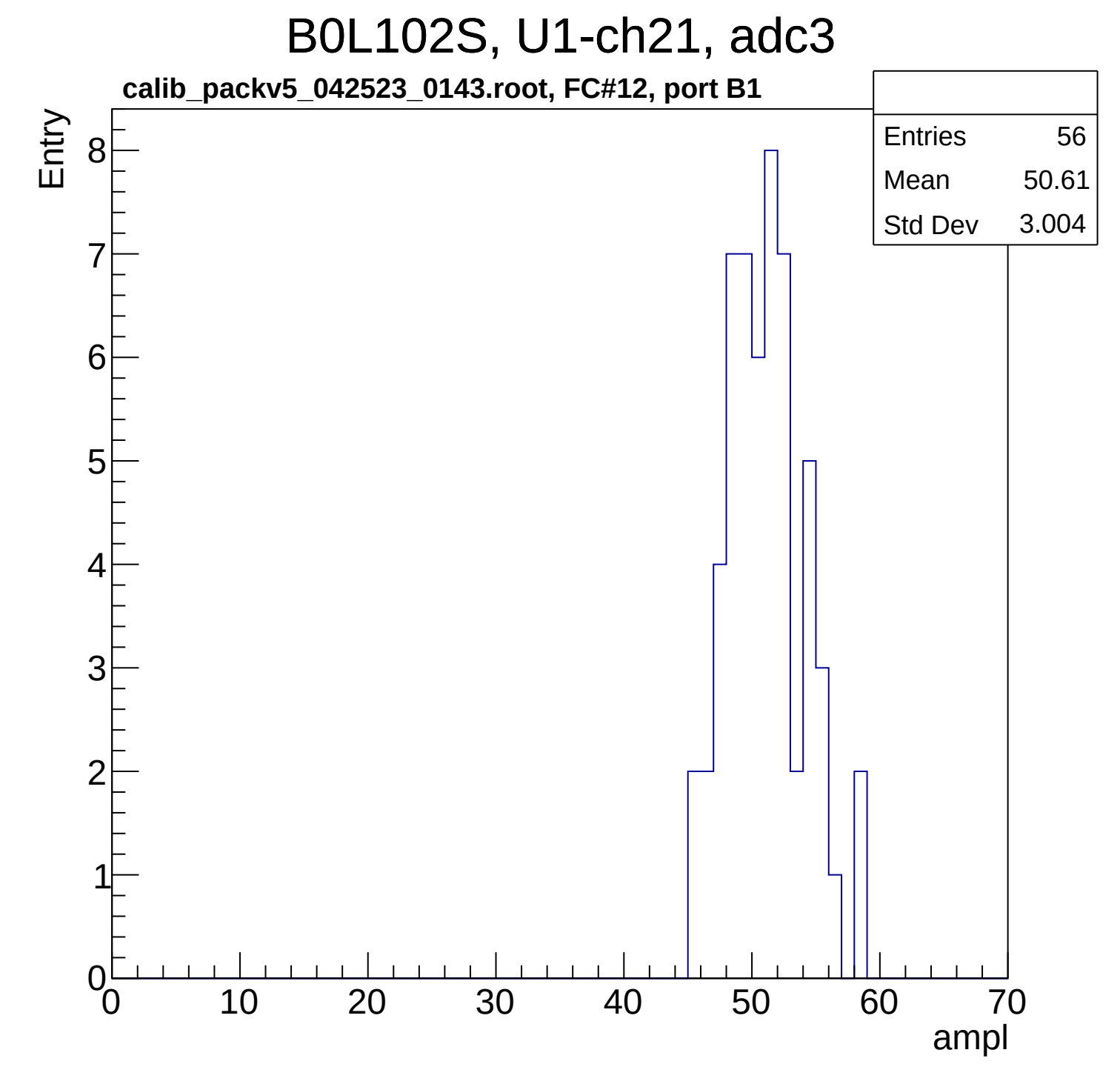
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch21, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 36.95
Std Dev 3.154
Gaus mean : 37.2443
Gaus Width: 3.3619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch21, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 43.39
Std Dev 3.723
Gaus mean : 44.4556
Gaus Width: 3.3350
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch21, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 50.61
Std Dev 3.004
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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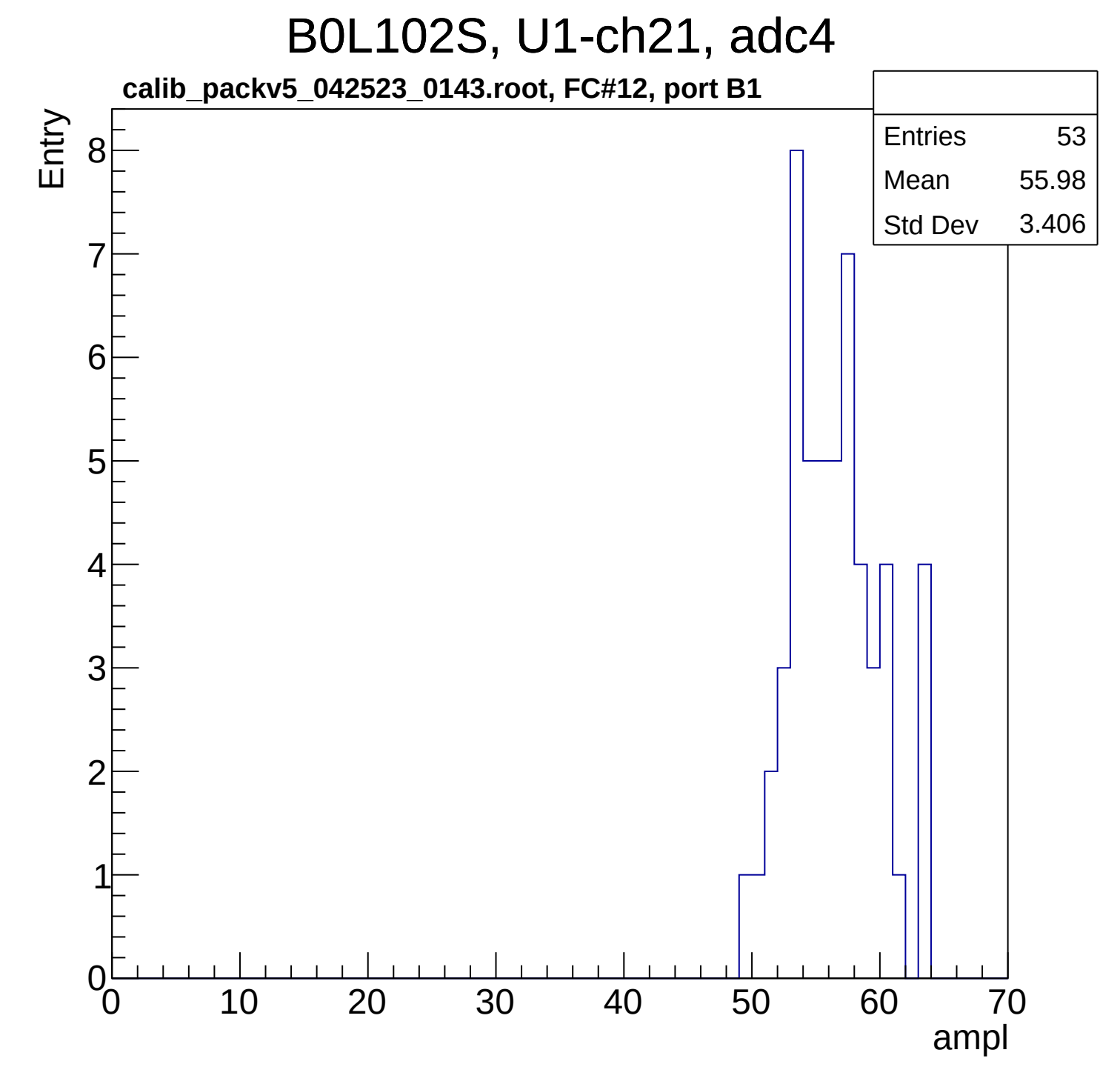
B0L102S, U1-ch21, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 55.98
Std Dev 3.406
Entry
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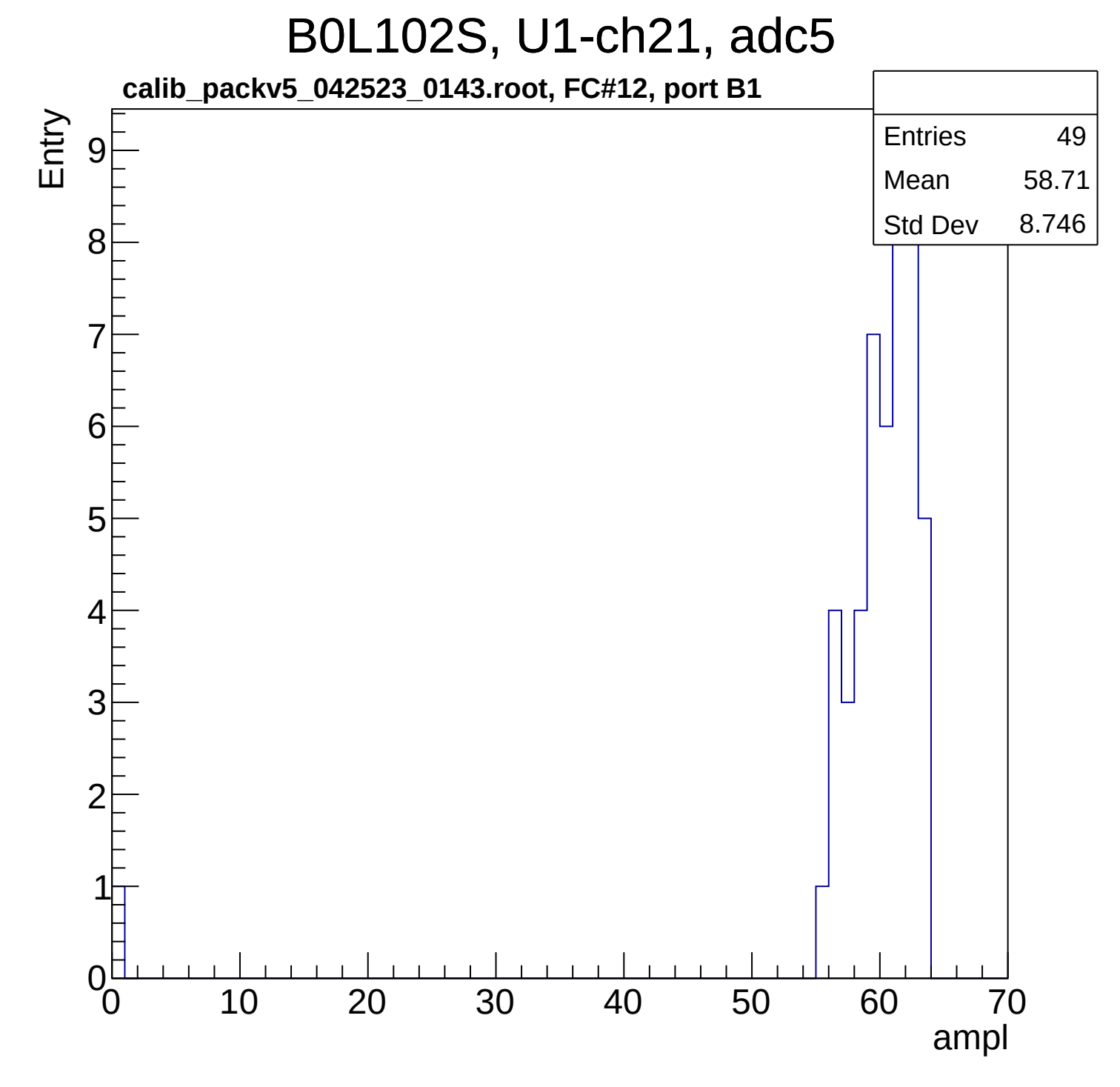
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch21, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 58.71
Std Dev 8.746
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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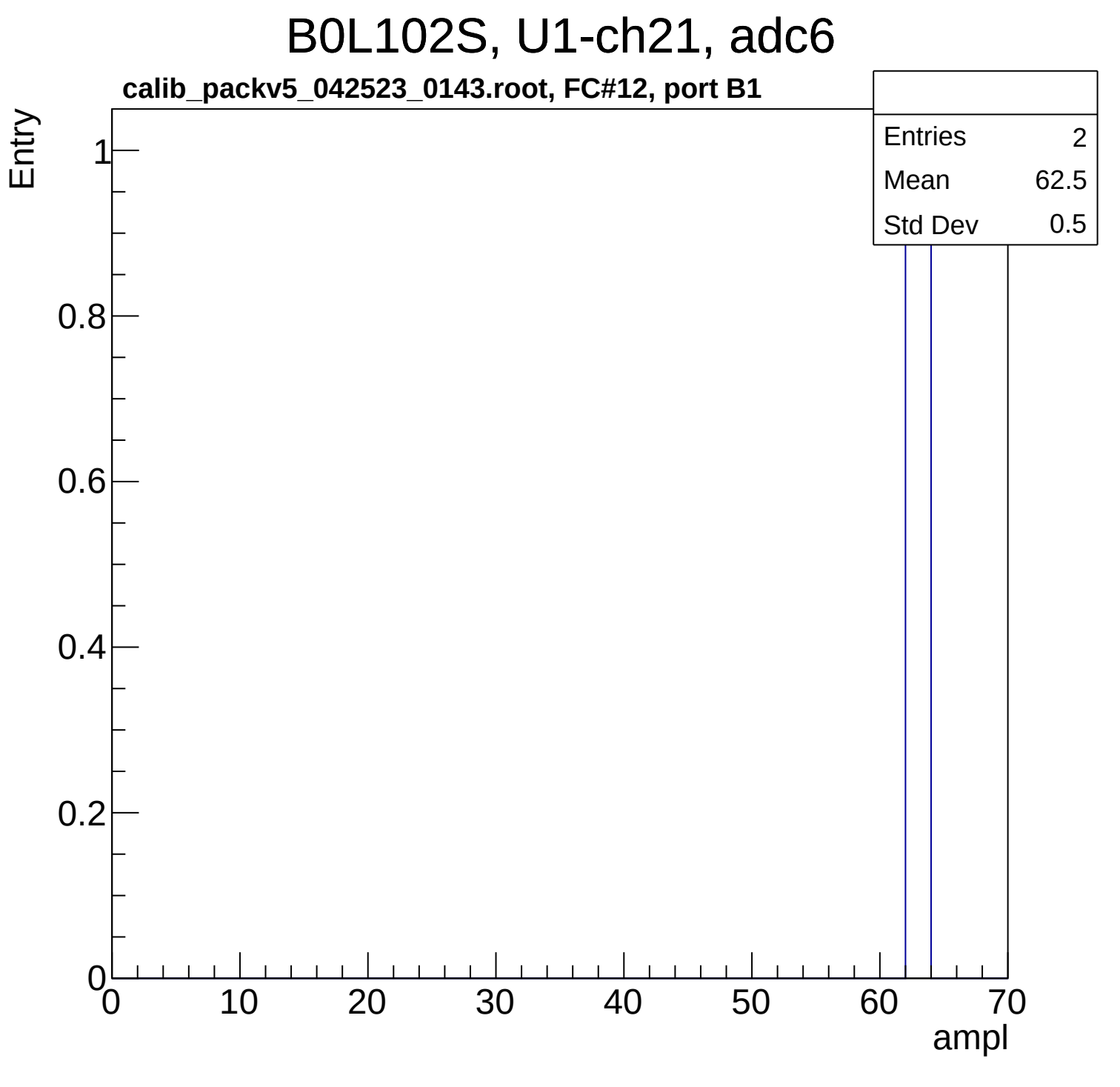
60
70


B0L102S, U1-ch21, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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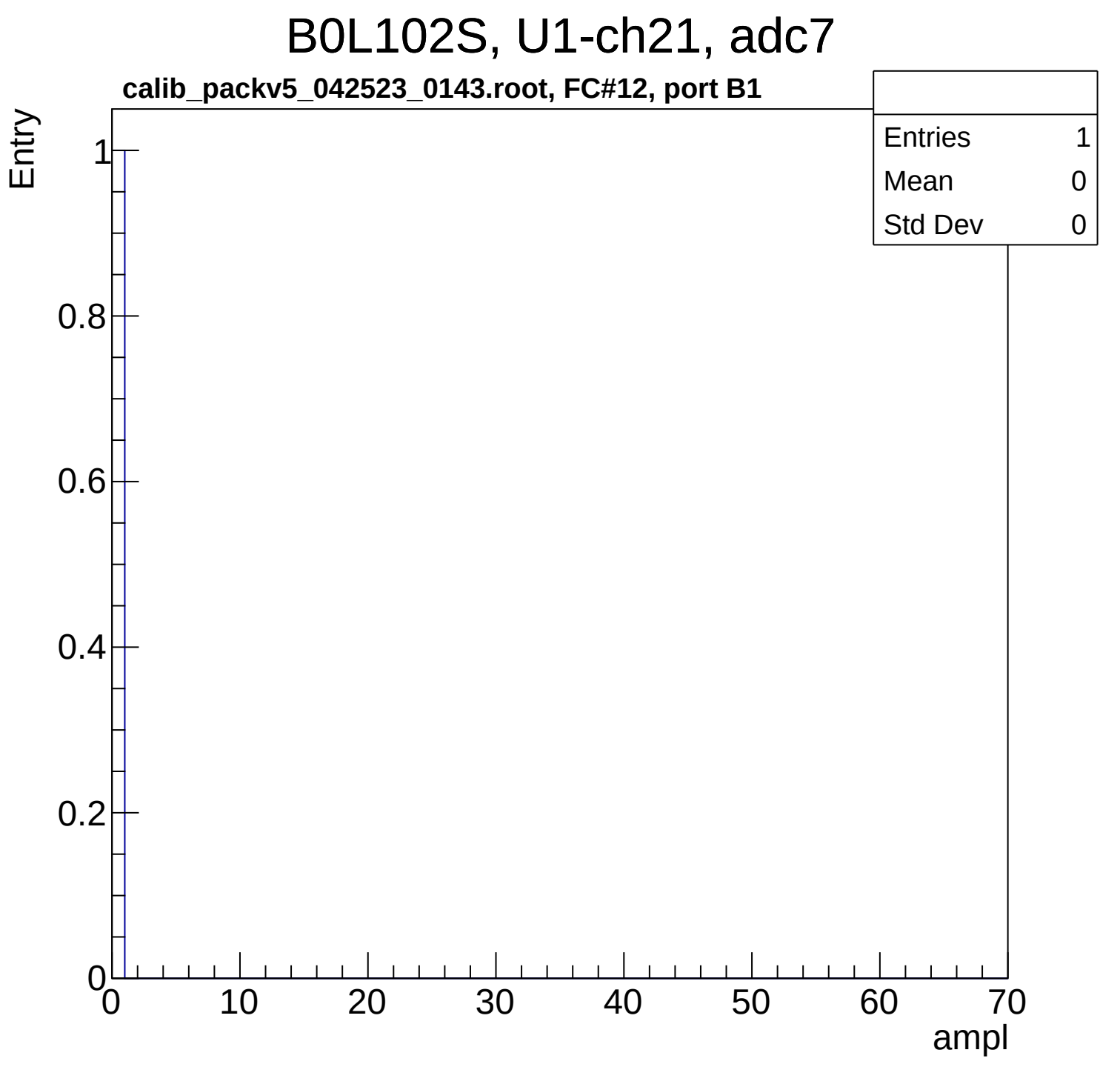
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch21, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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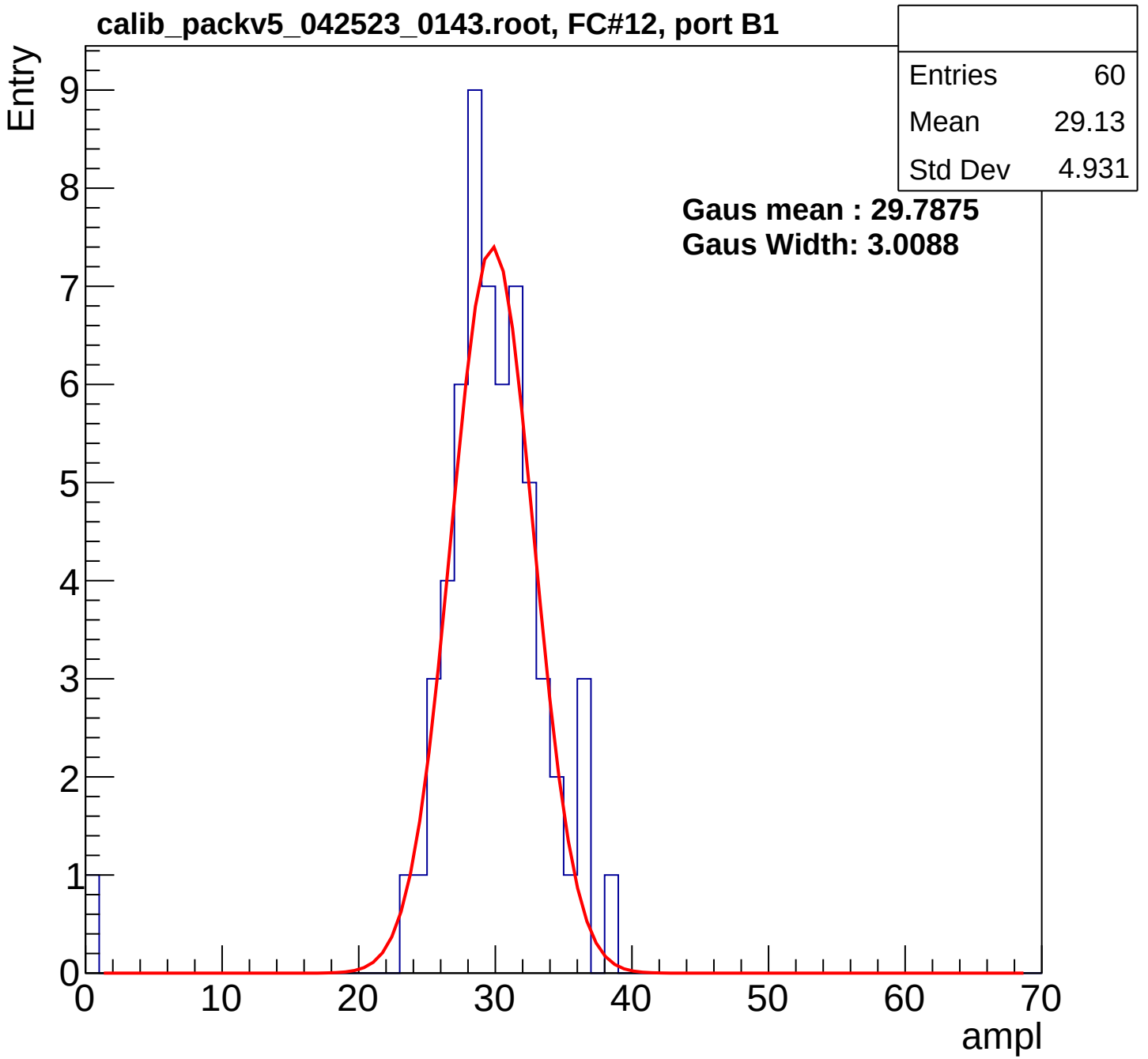
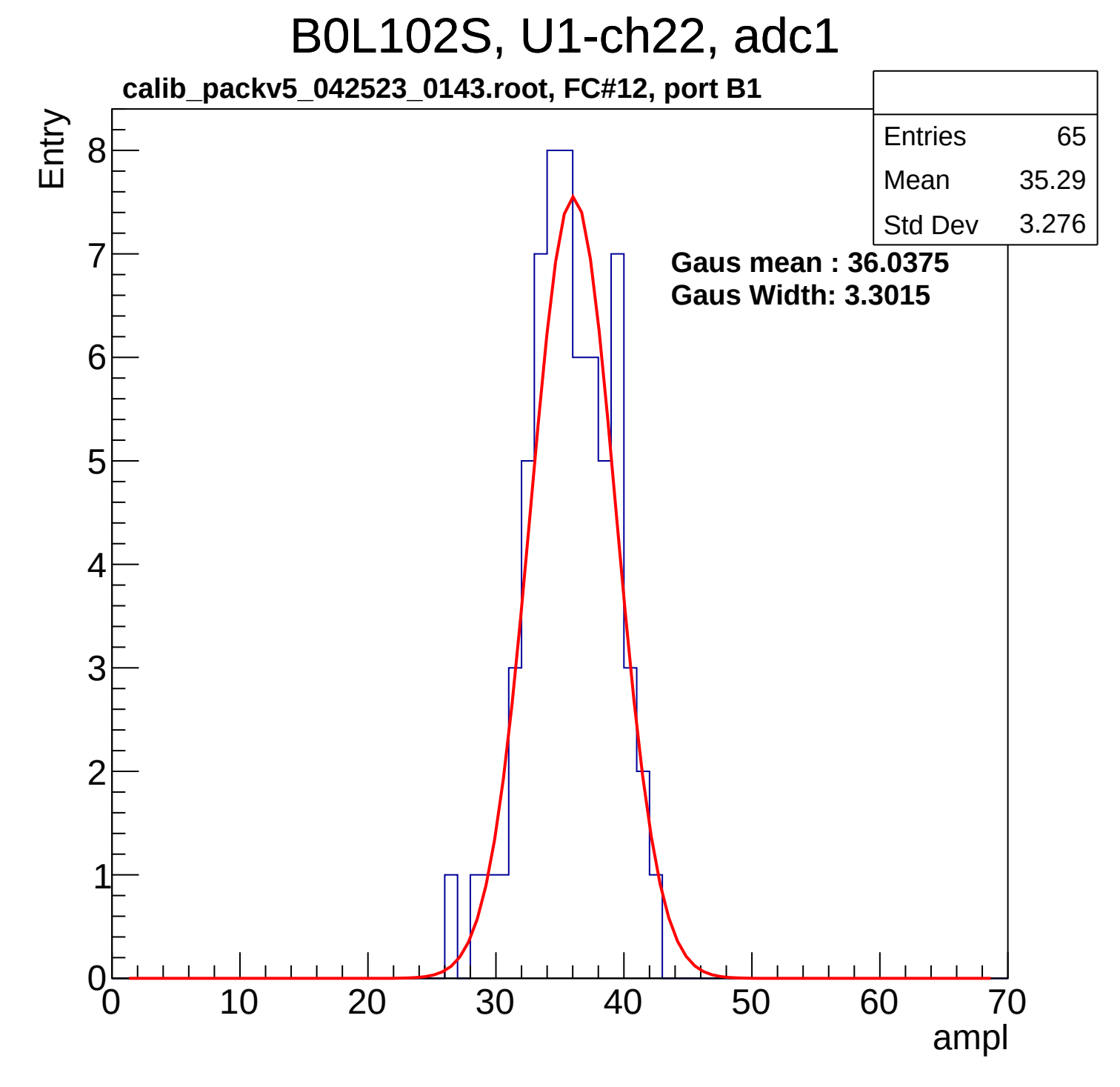
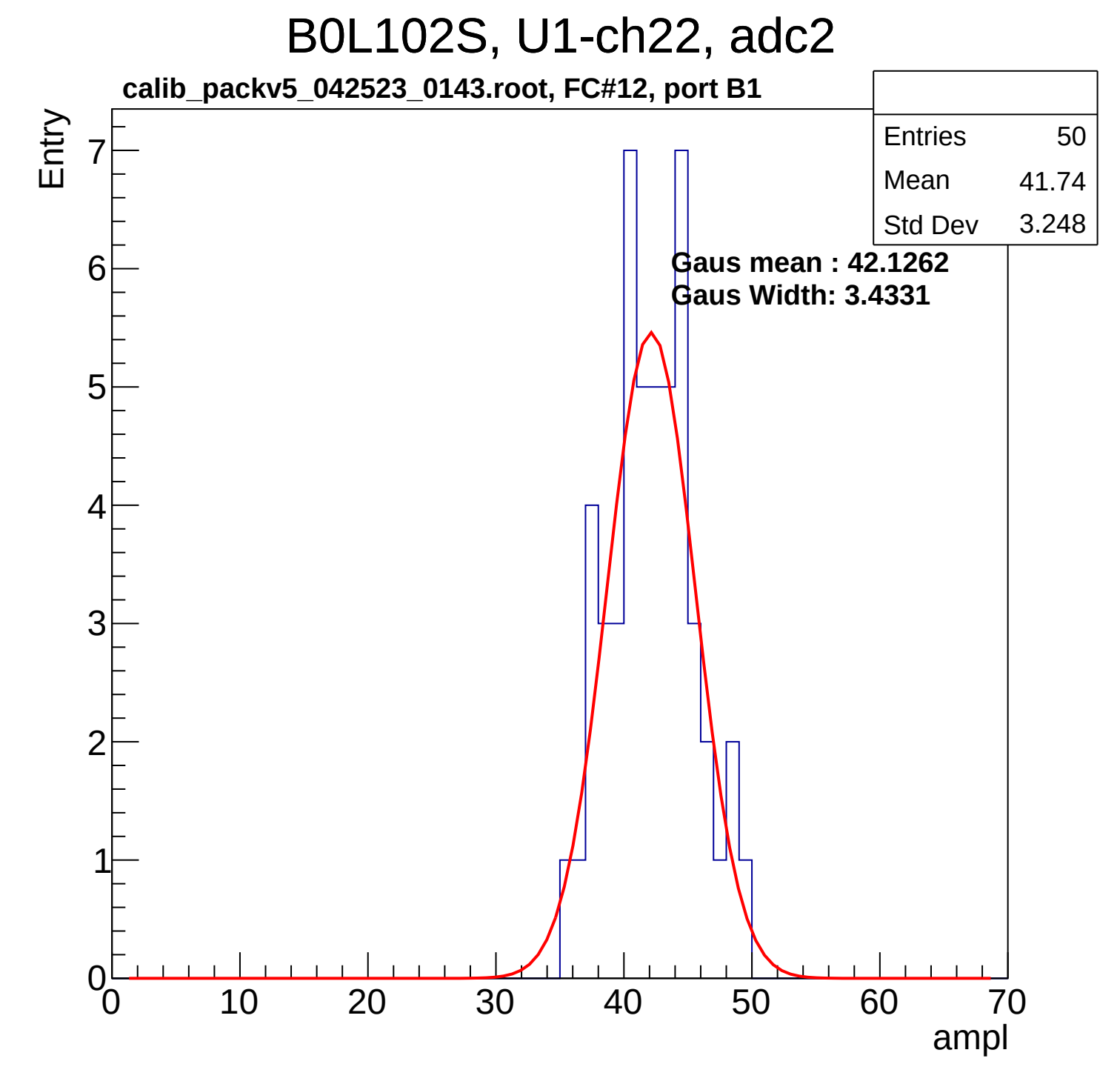
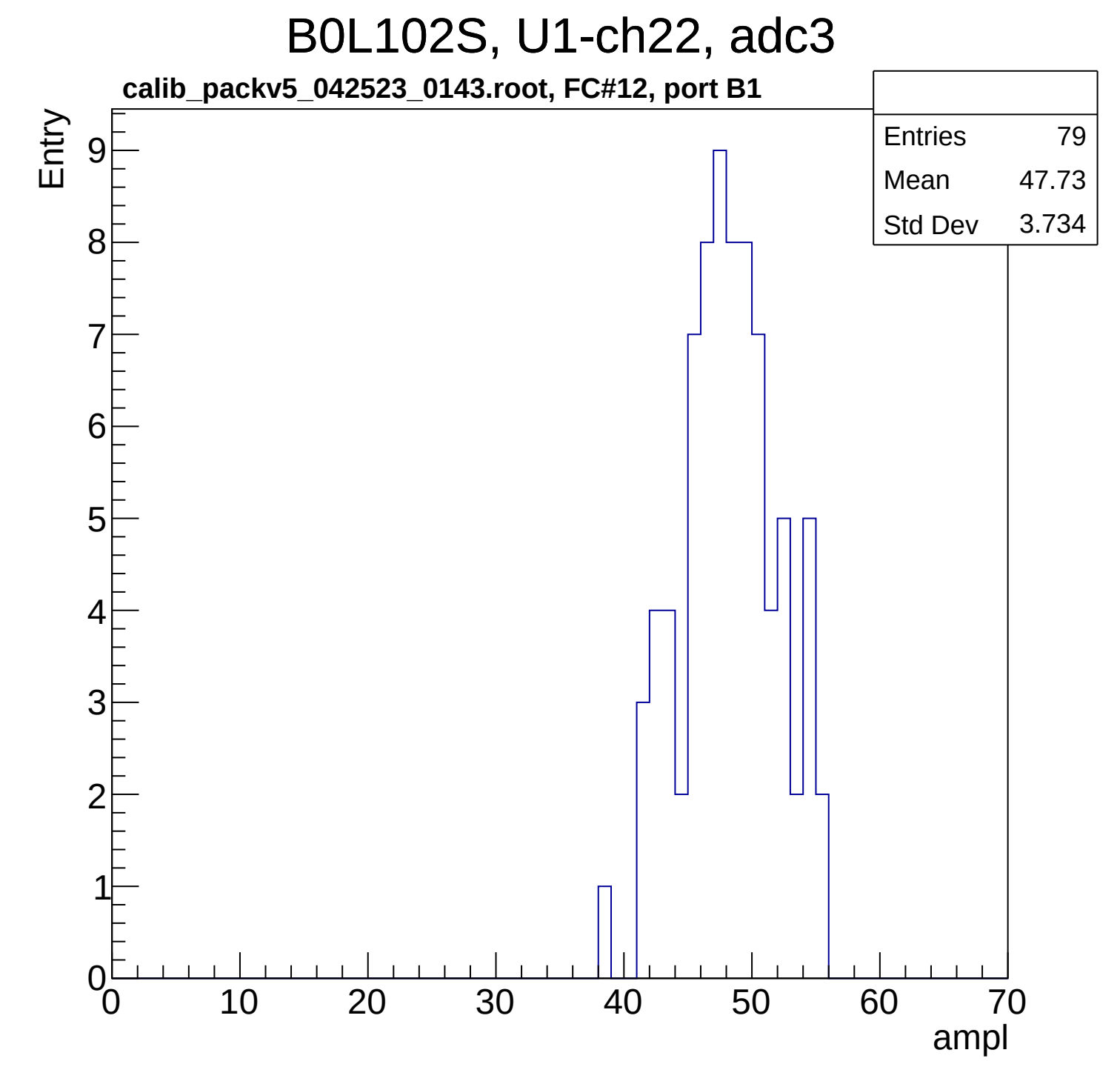
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch22, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 29.13
Std Dev 4.931
Gaus mean : 29.7875
Gaus Width: 3.0088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch22, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.29
Std Dev 3.276
Gaus mean : 36.0375
Gaus Width: 3.3015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch22, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 41.74
Std Dev 3.248
Gaus mean : 42.1262
Gaus Width: 3.4331
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch22, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 47.73
Std Dev 3.734
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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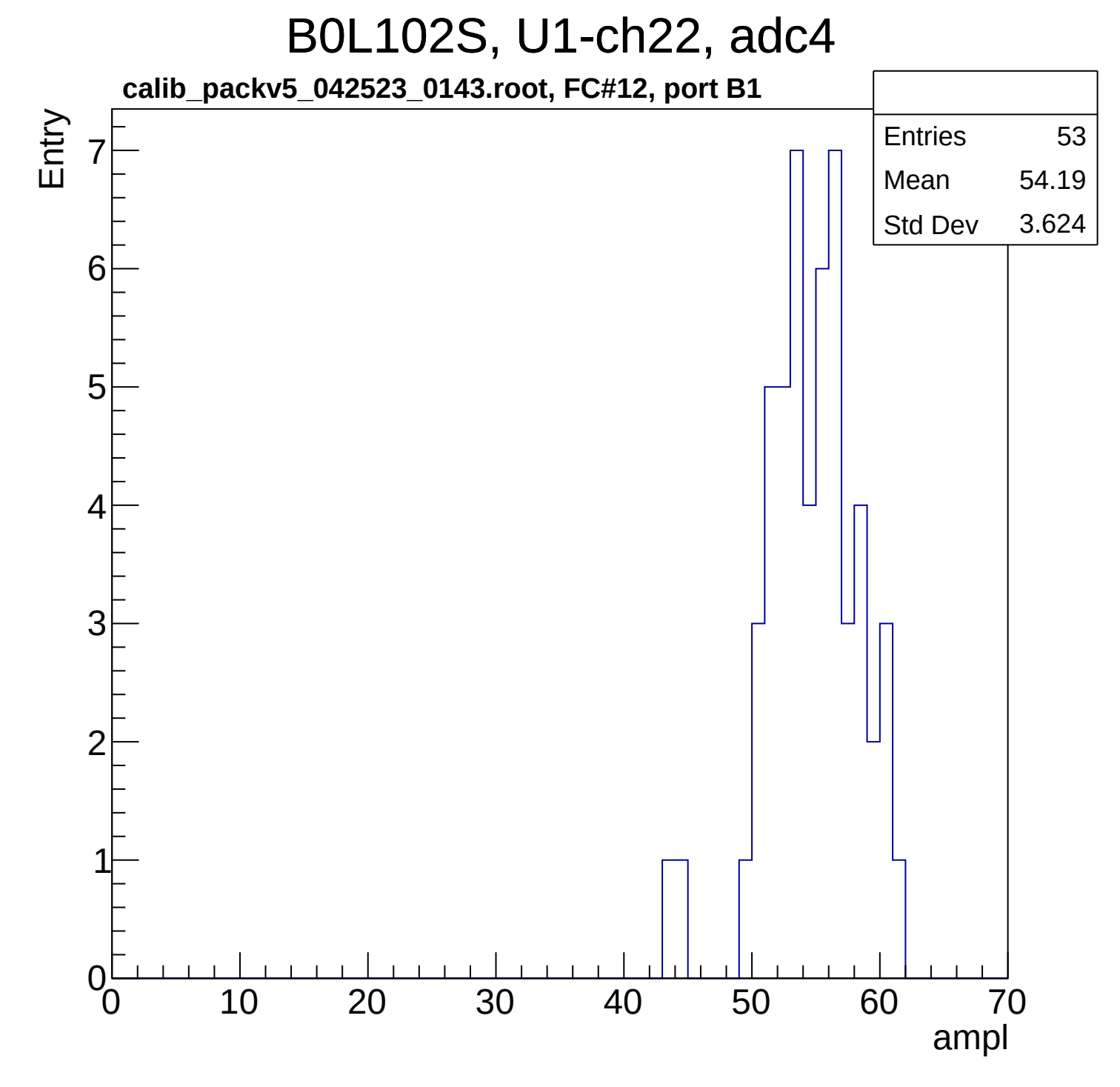
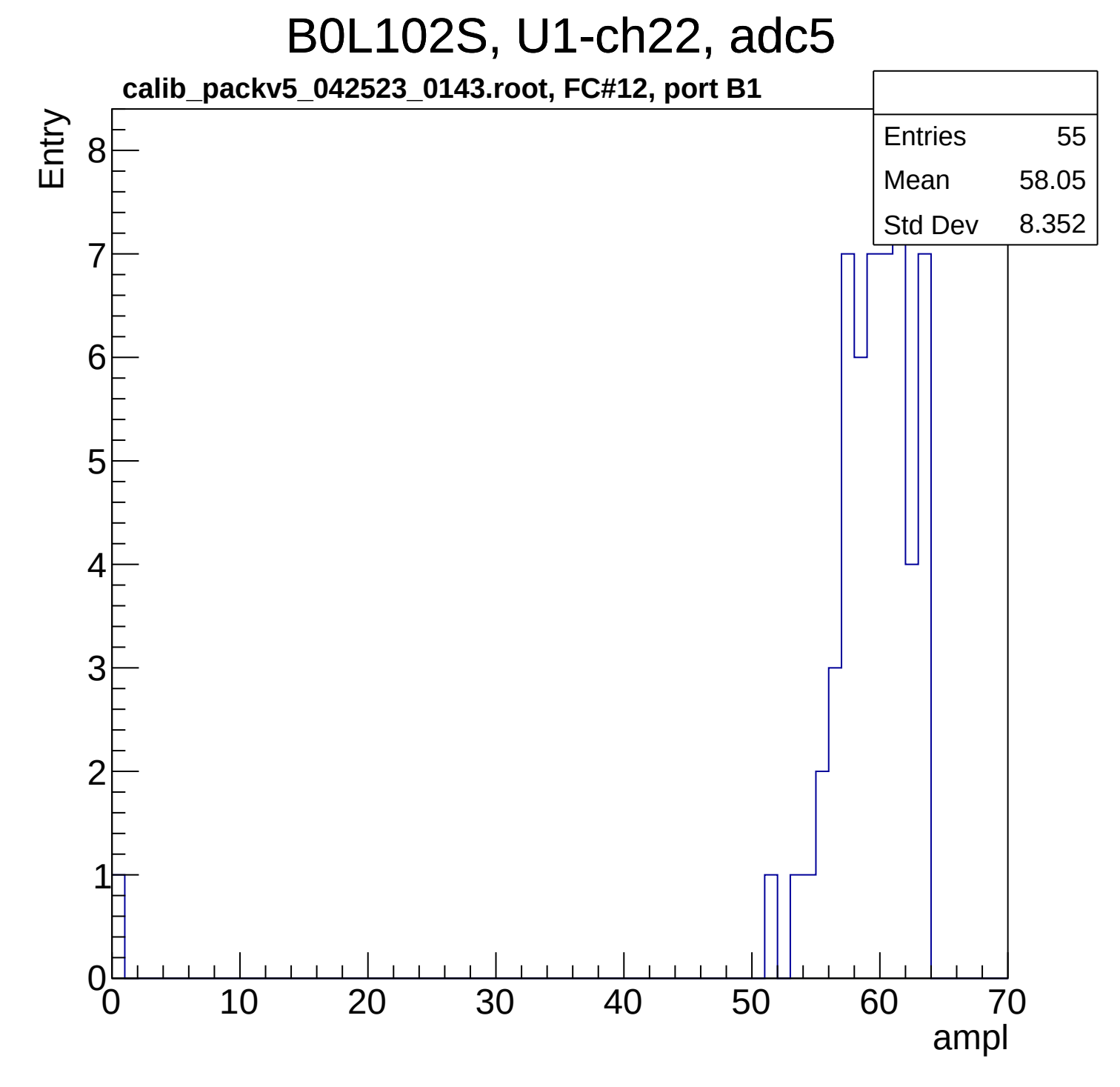

B0L102S, U1-ch22, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 54.19
Std Dev 3.624
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch22, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 58.05
Std Dev 8.352
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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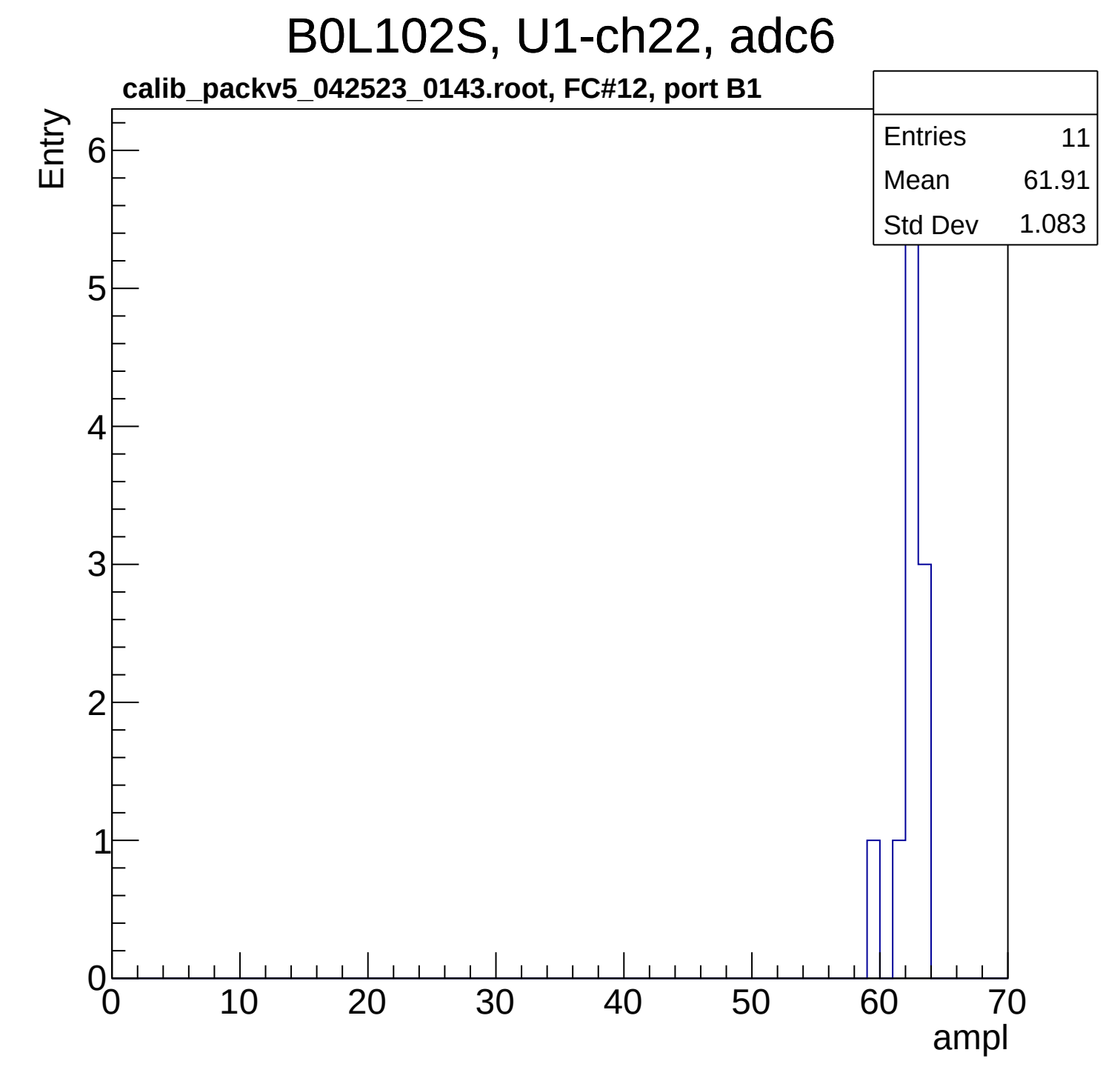
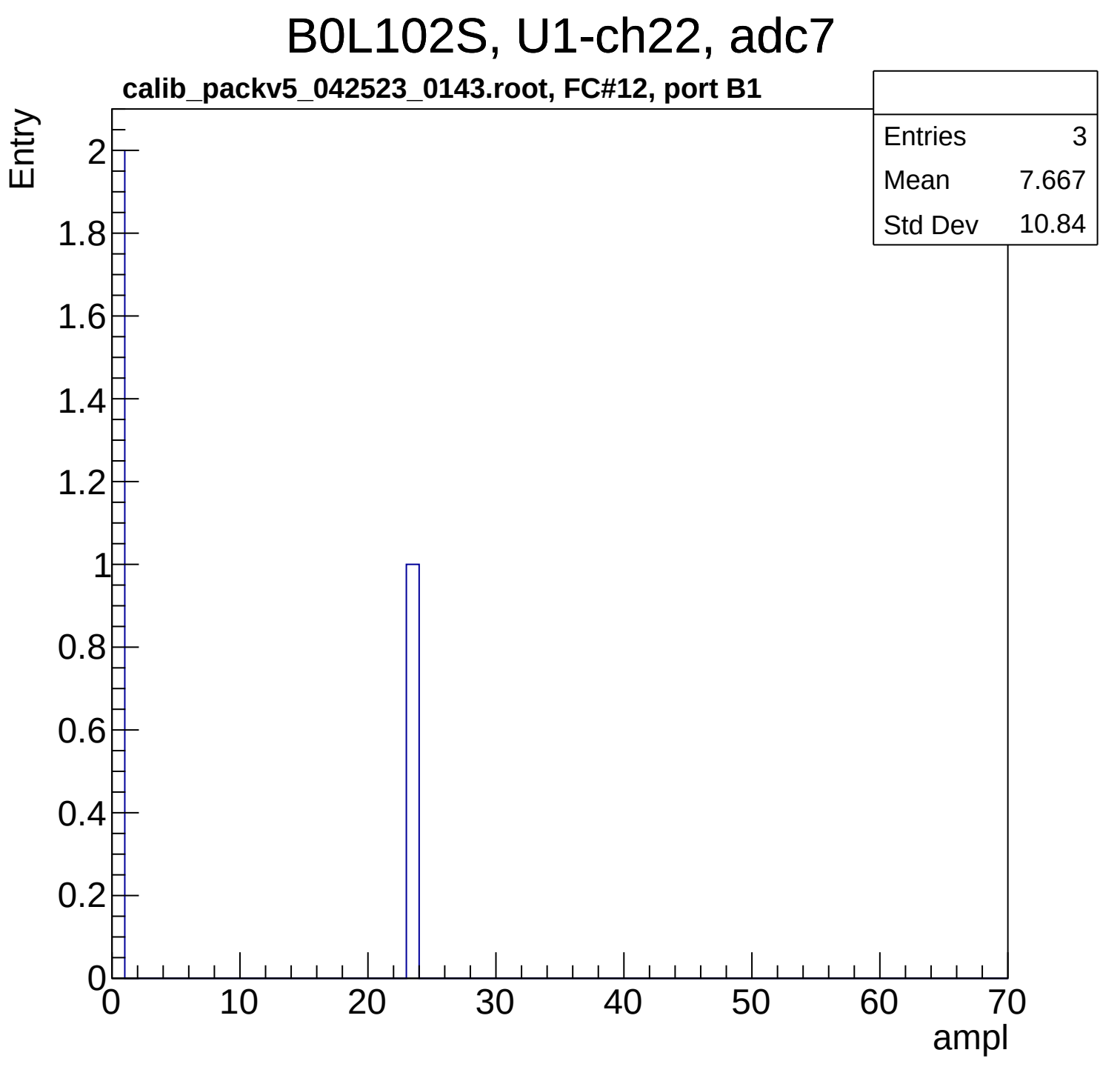

B0L102S, U1-ch22, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61.91
Std Dev 1.083
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch22, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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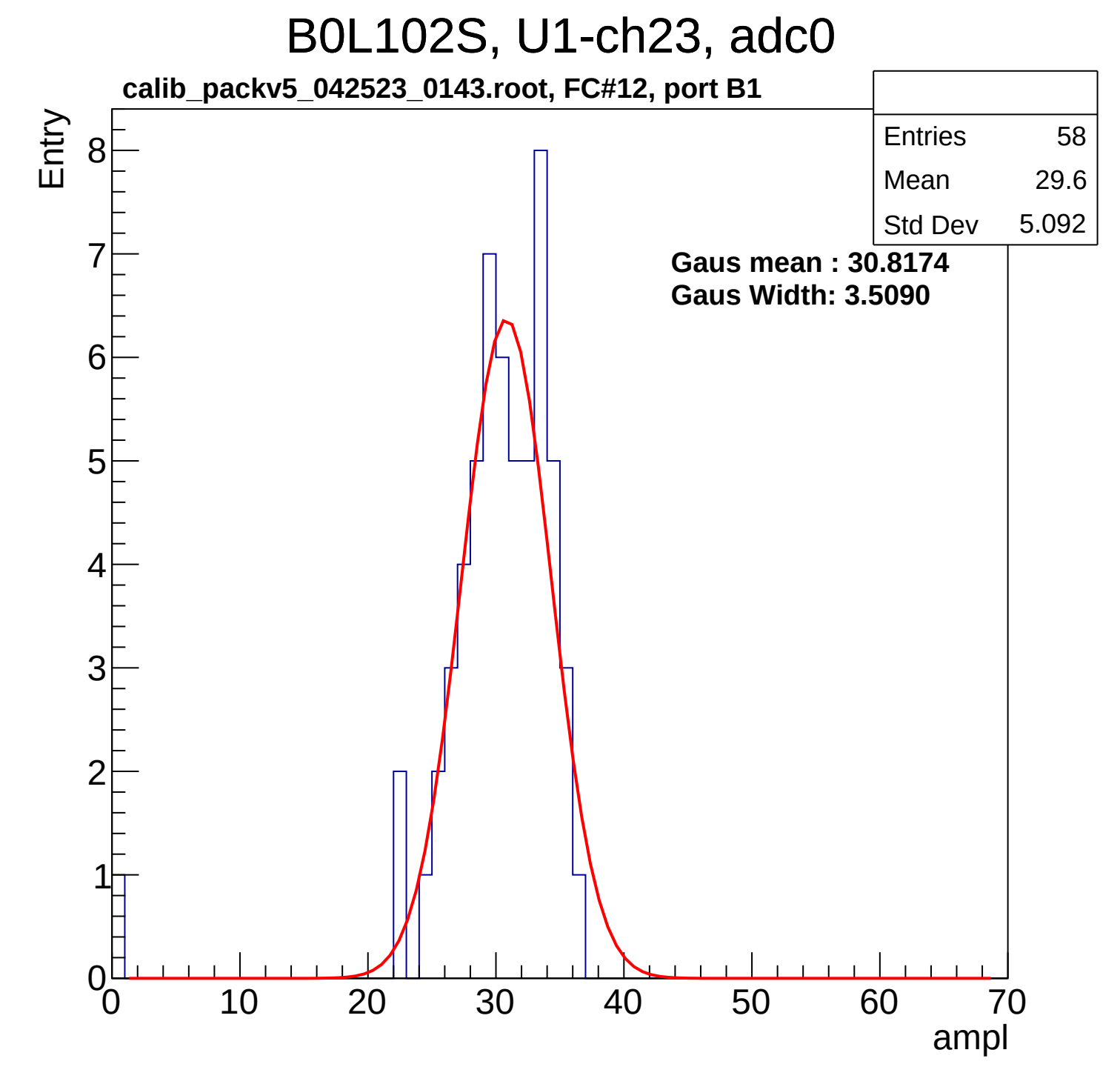
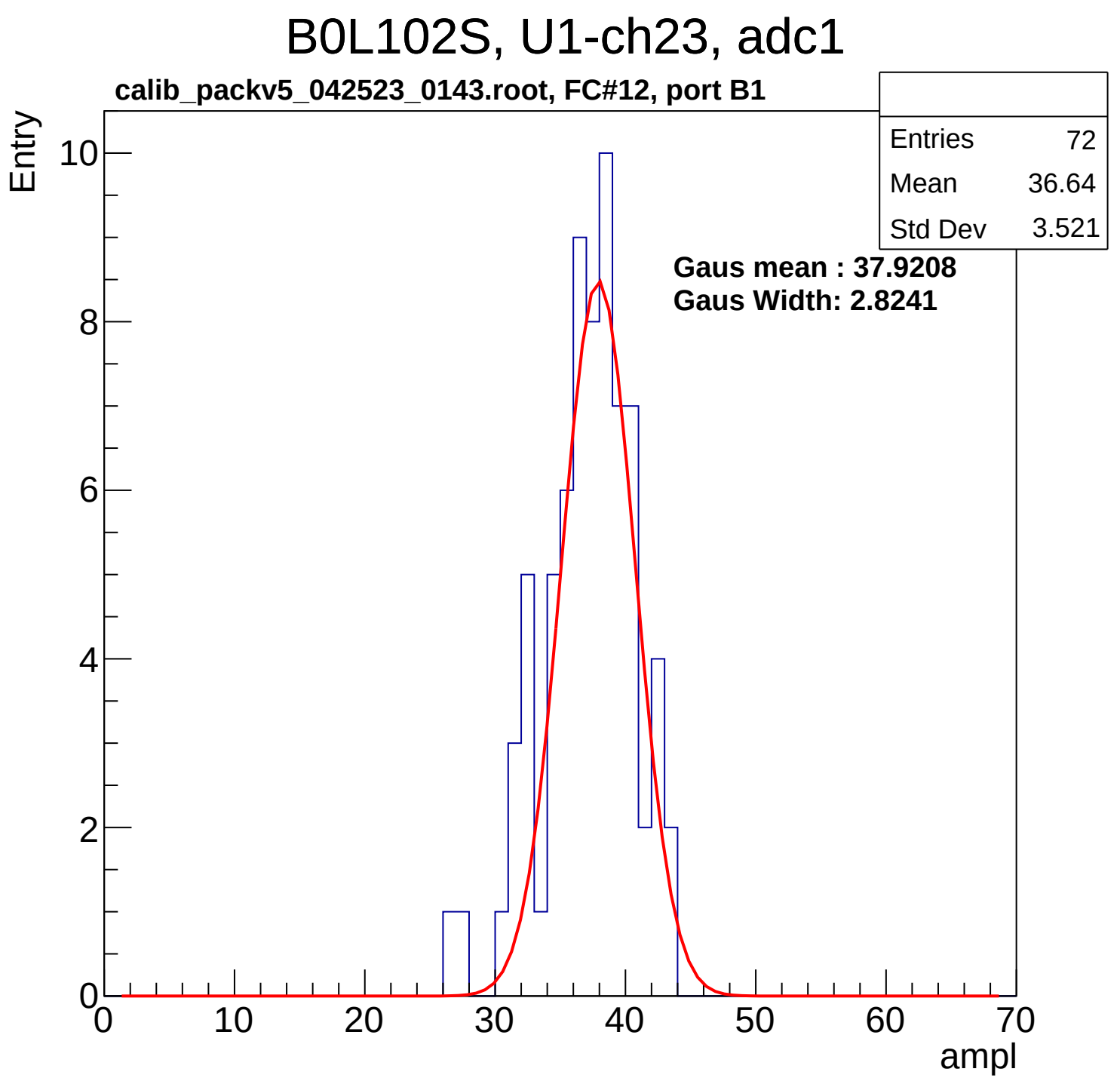
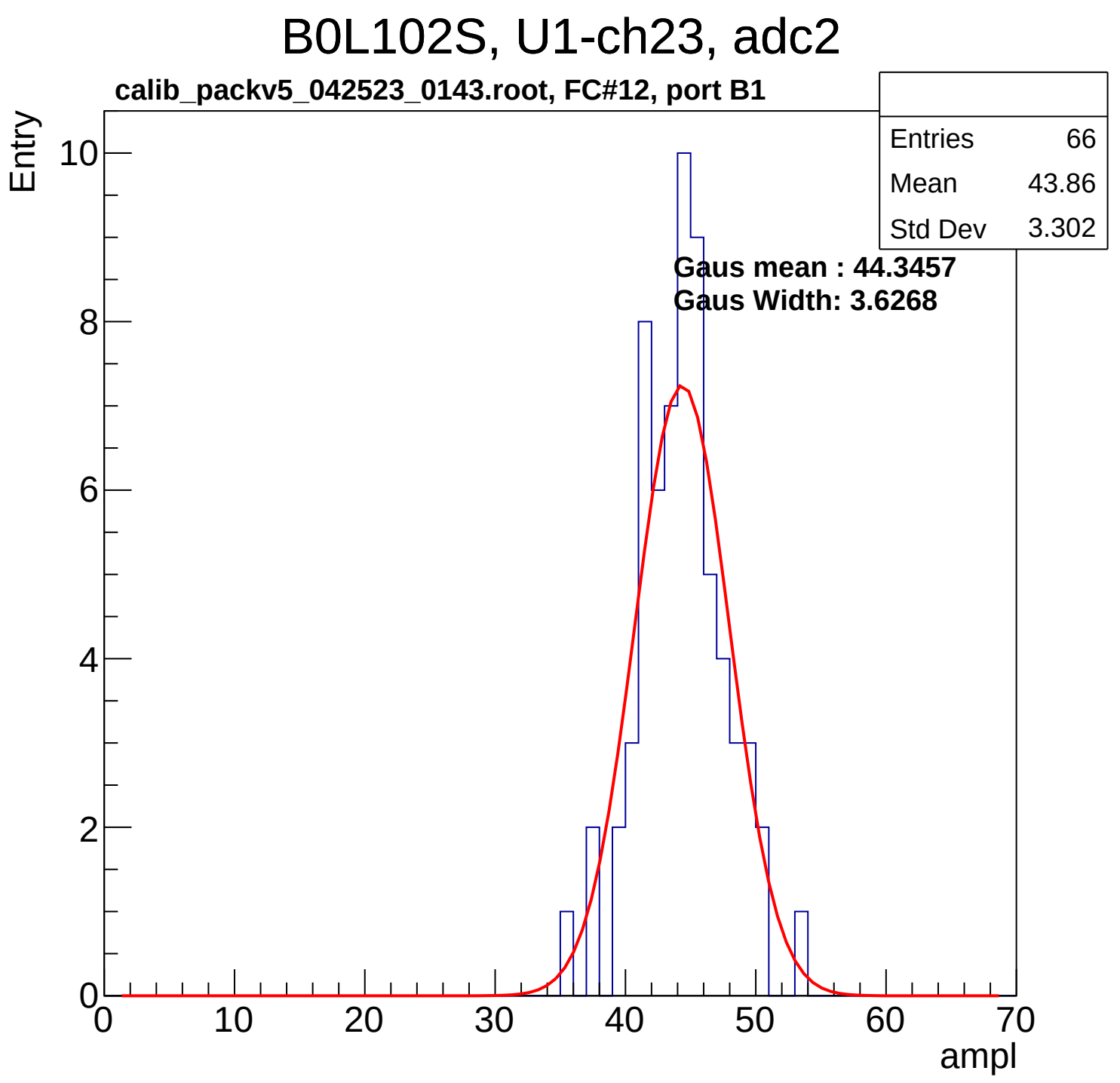
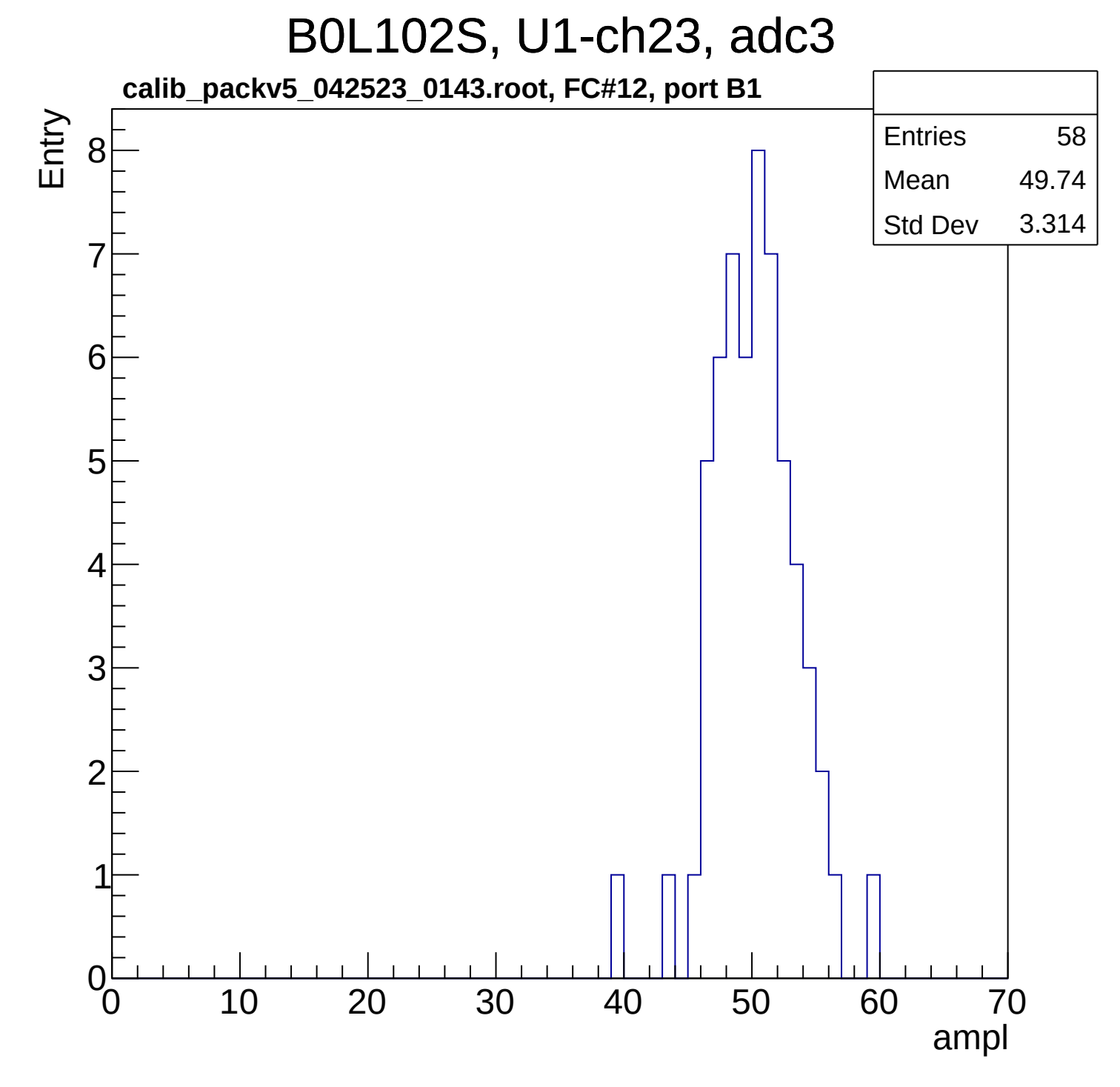
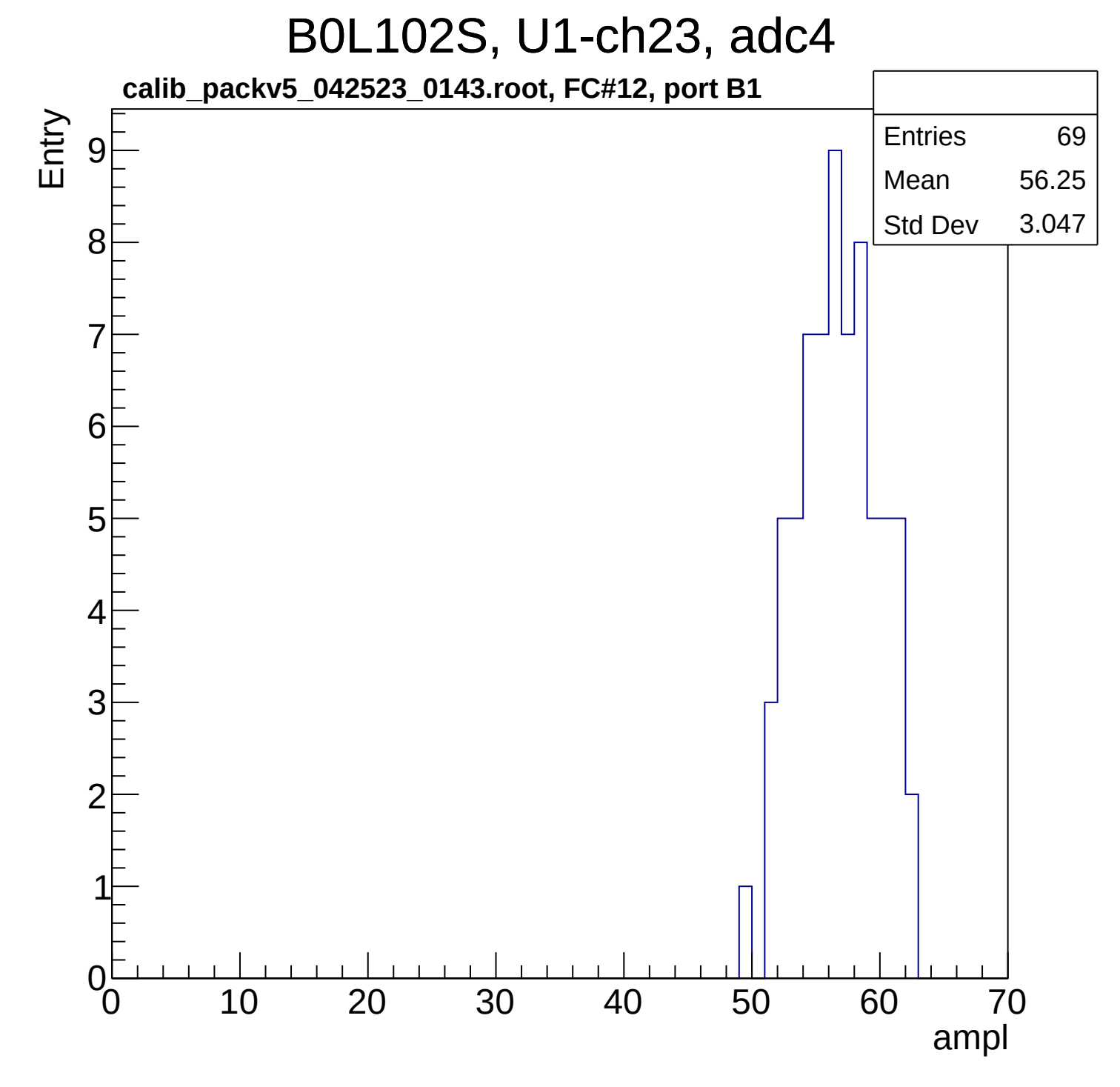
40
50
60
70

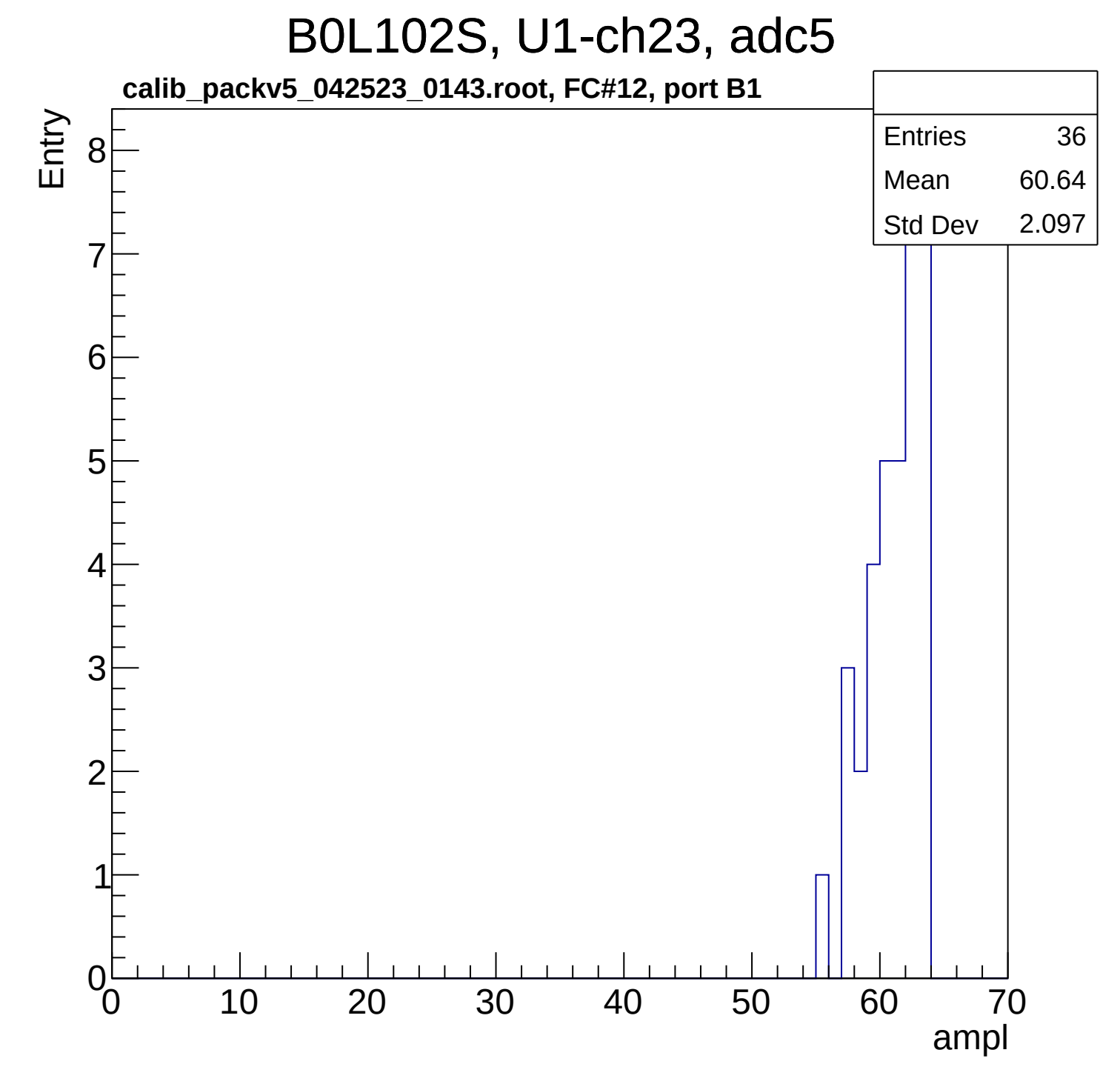

B0L102S, U1-ch23, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 29.6
Std Dev 5.092
Gaus mean : 30.8174
Gaus Width: 3.5090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

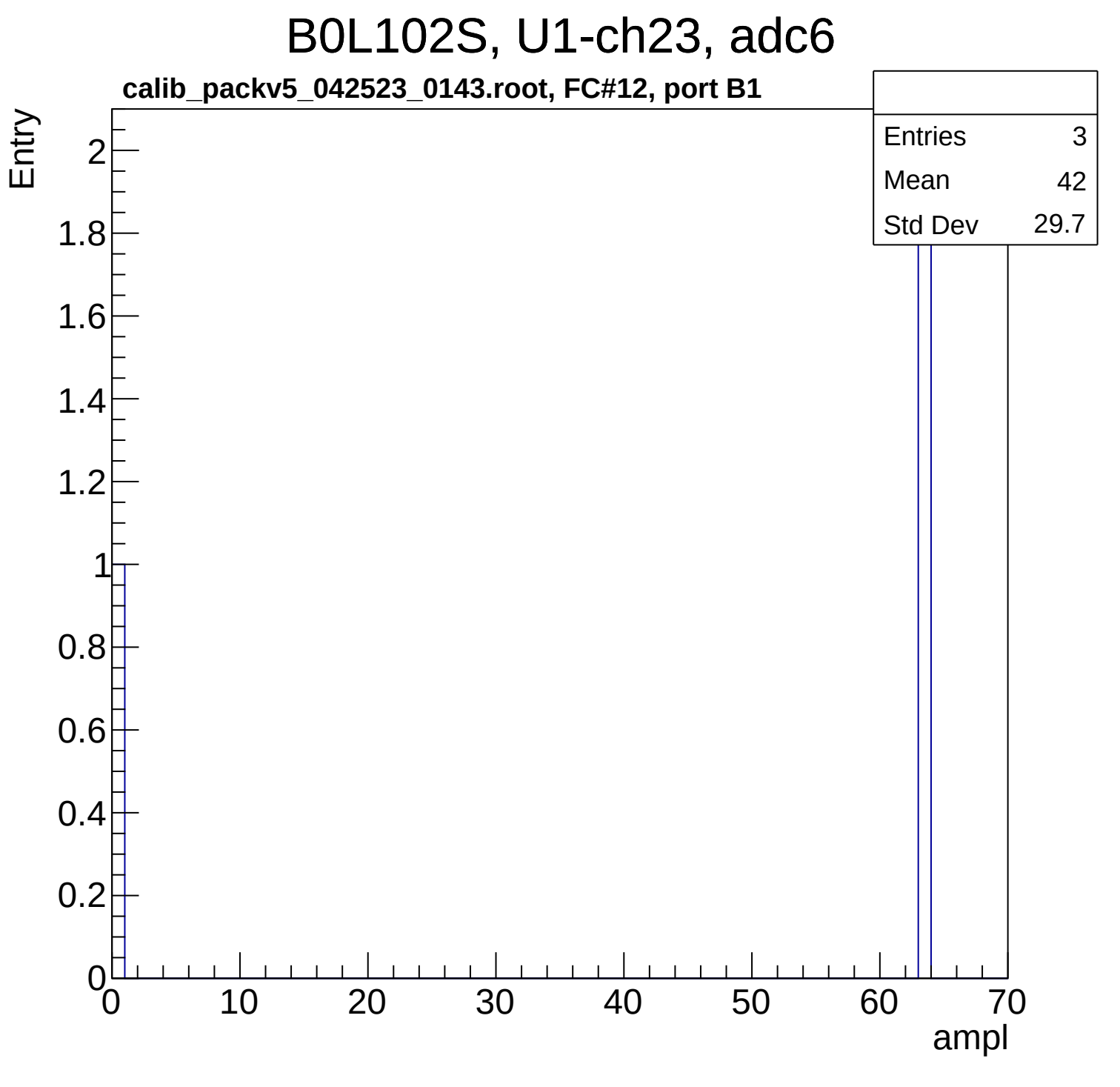

B0L102S, U1-ch23, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 36.64
Std Dev 3.521
Gaus mean : 37.9208
Gaus Width: 2.8241
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

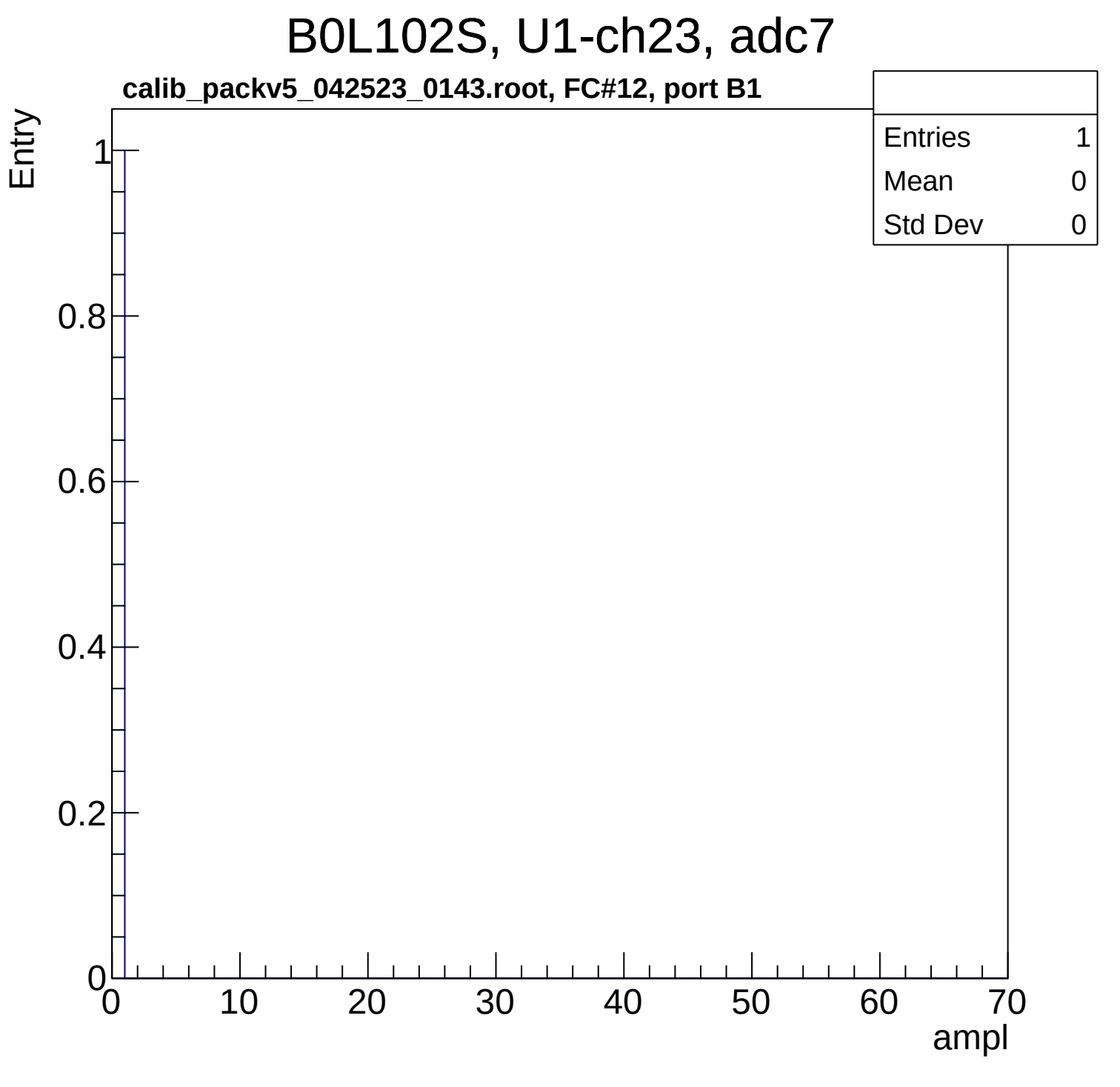

B0L102S, U1-ch23, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.86
Std Dev 3.302
Gaus mean : 44.3457
Gaus Width: 3.6268
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

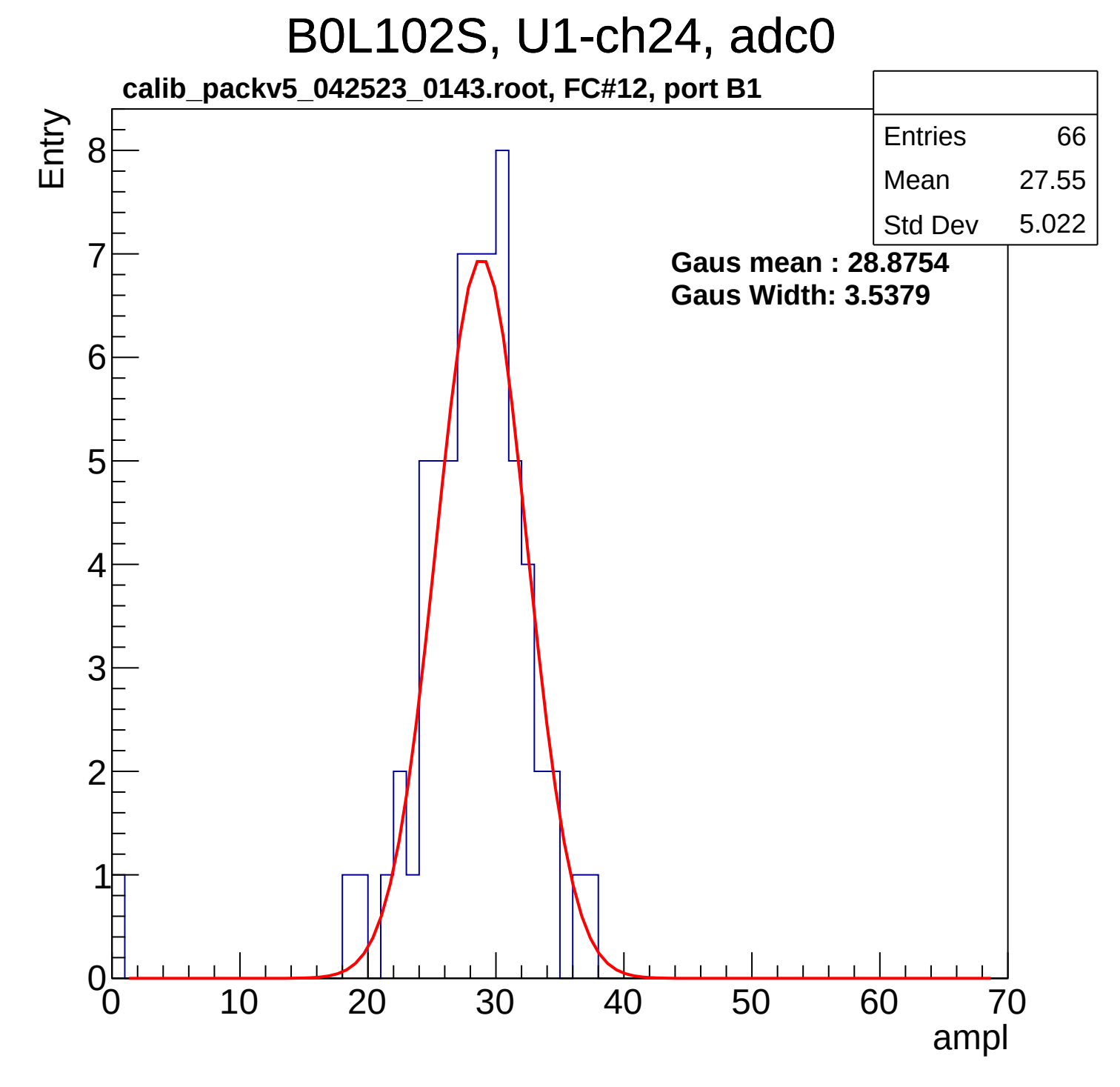

B0L102S, U1-ch23, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 49.74
Std Dev 3.314
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

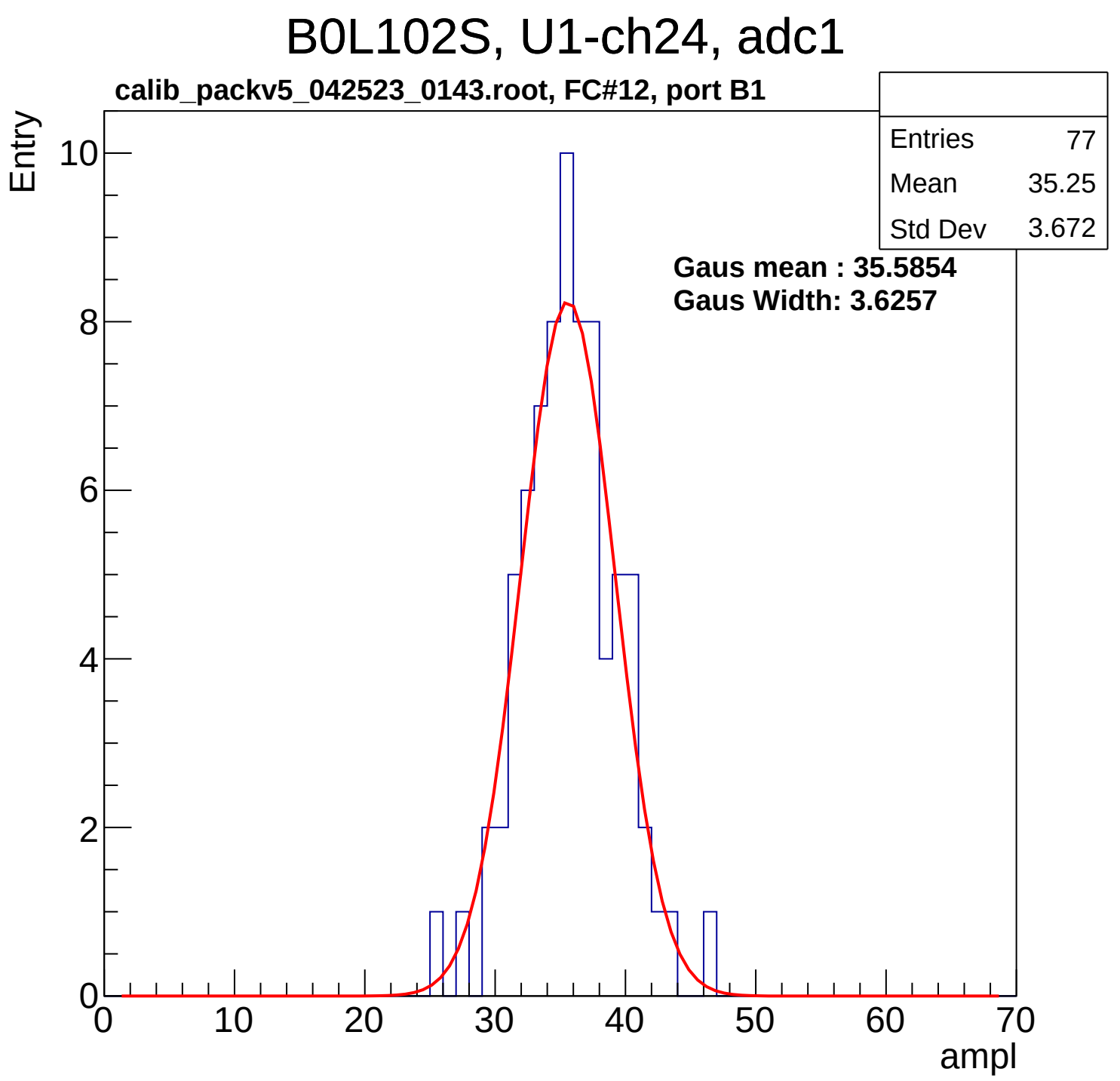

B0L102S, U1-ch23, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 56.25
Std Dev 3.047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

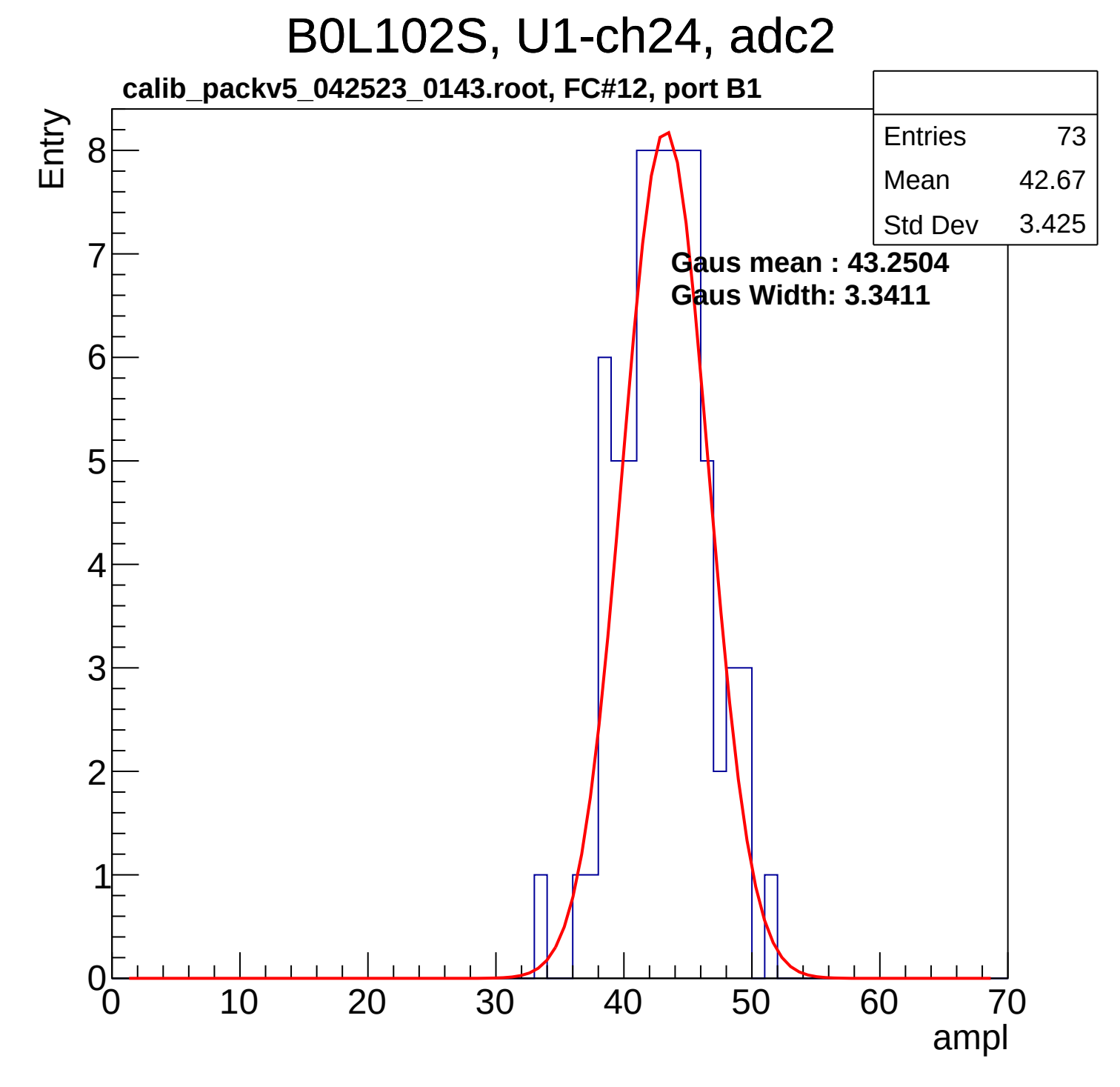

B0L102S, U1-ch23, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 60.64
Std Dev 2.097
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

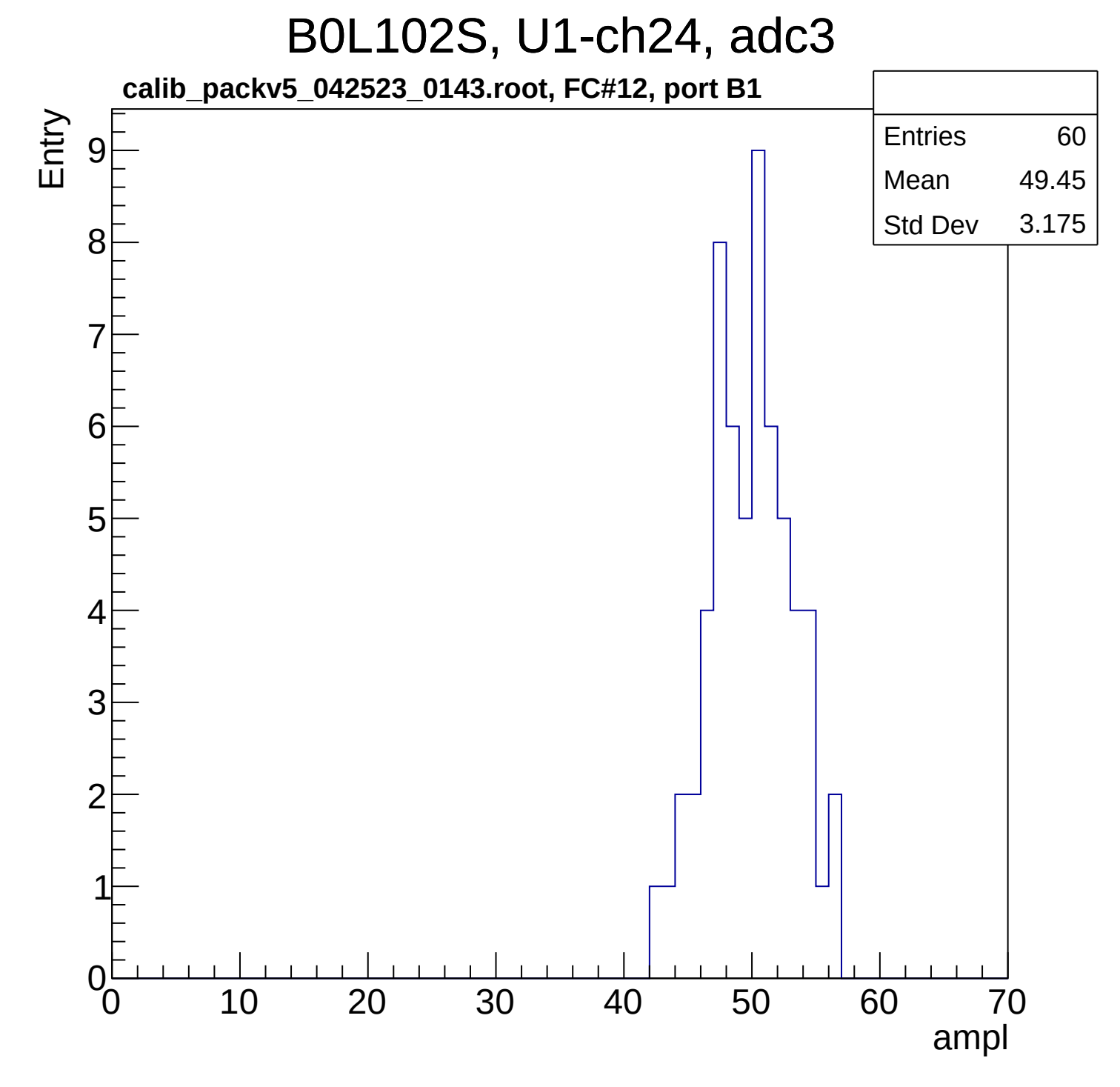

B0L102S, U1-ch23, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch23, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch24, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 27.55
Std Dev 5.022
Gaus mean : 28.8754
Gaus Width: 3.5379
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch24, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 35.25
Std Dev 3.672
Gaus mean : 35.5854
Gaus Width: 3.6257
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch24, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 42.67
Std Dev 3.425
Gaus mean : 43.2504
Gaus Width: 3.3411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch24, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 49.45
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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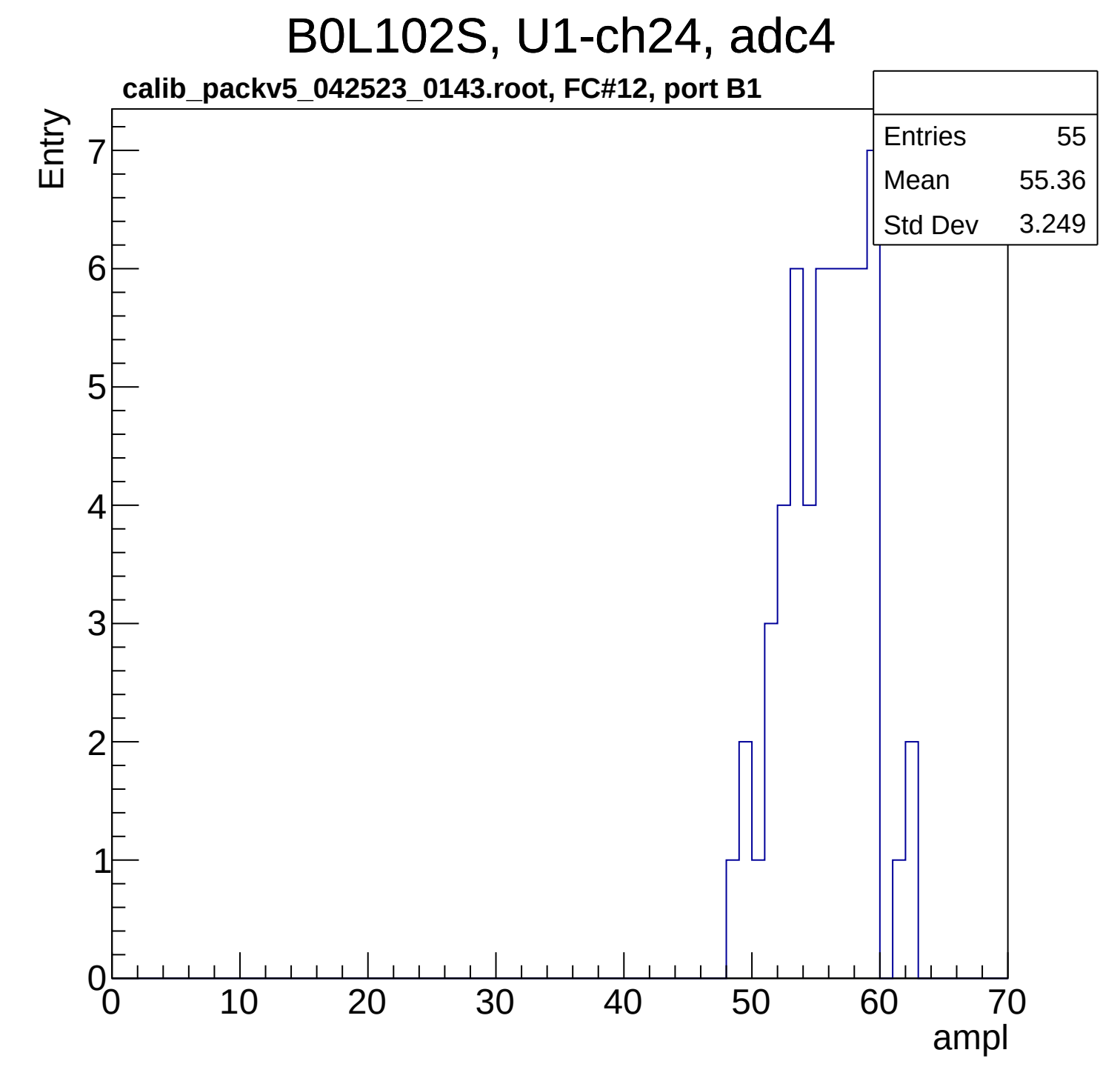
B0L102S, U1-ch24, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.36
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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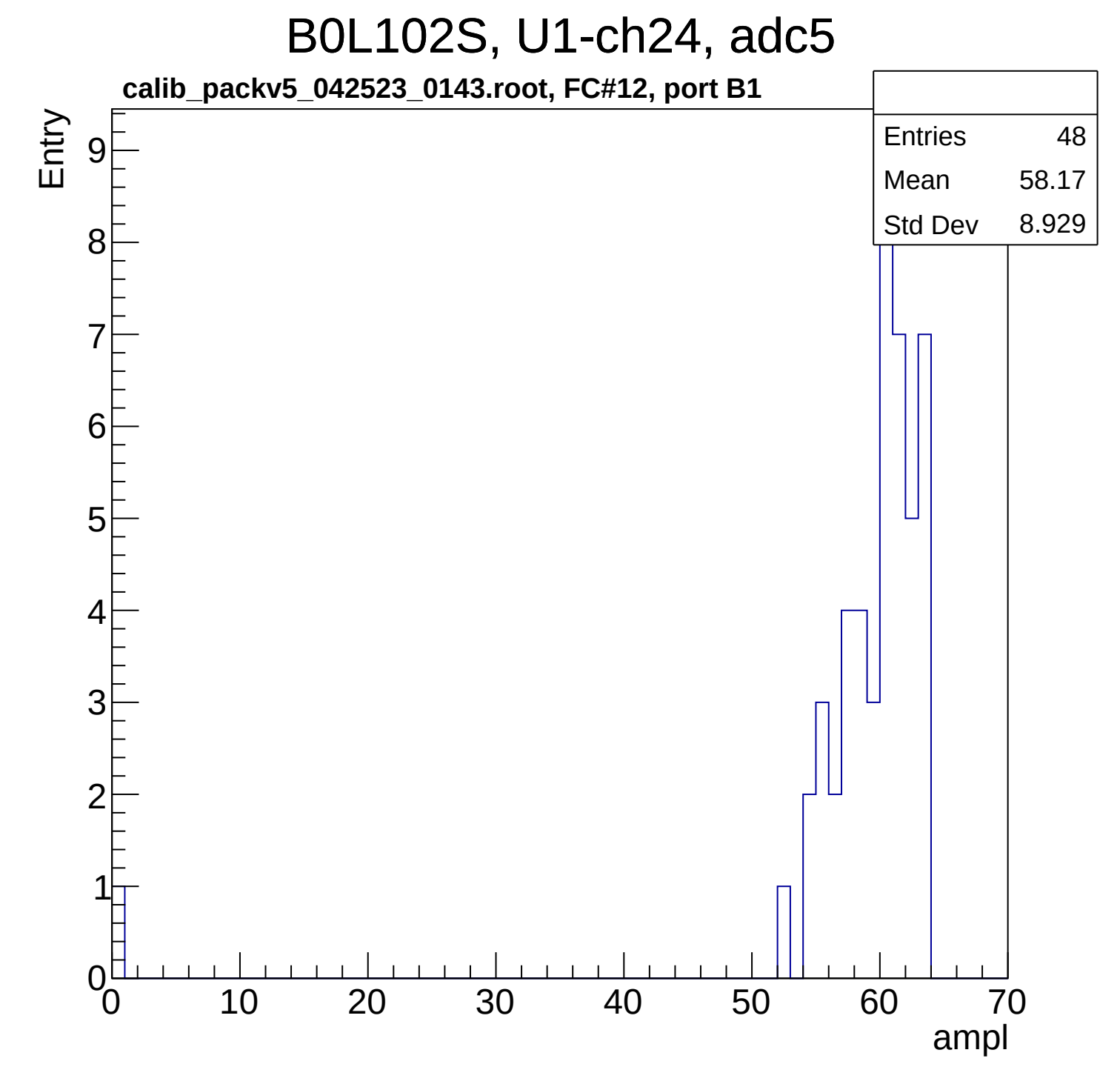
B0L102S, U1-ch24, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.17
Std Dev 8.929
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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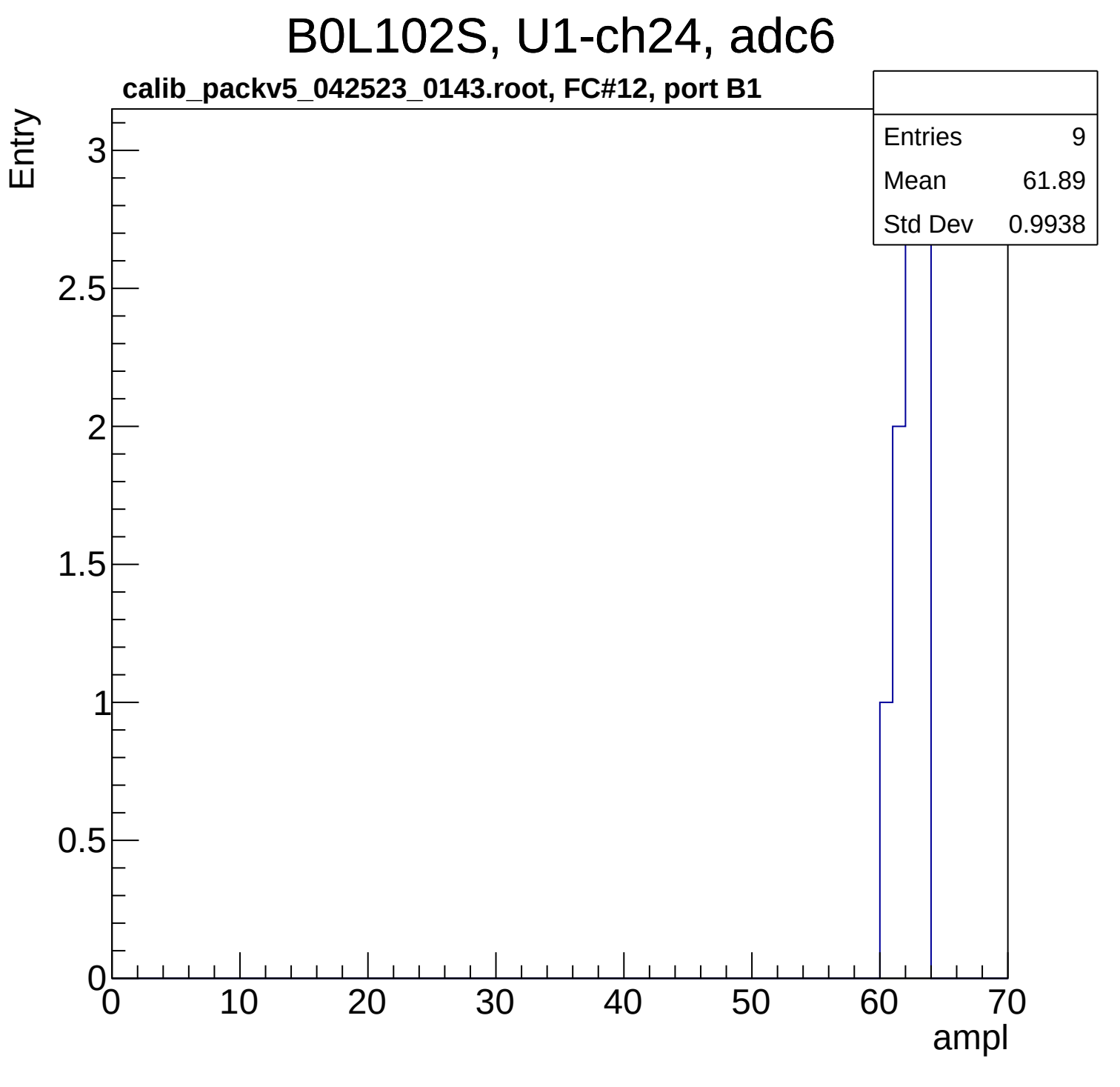
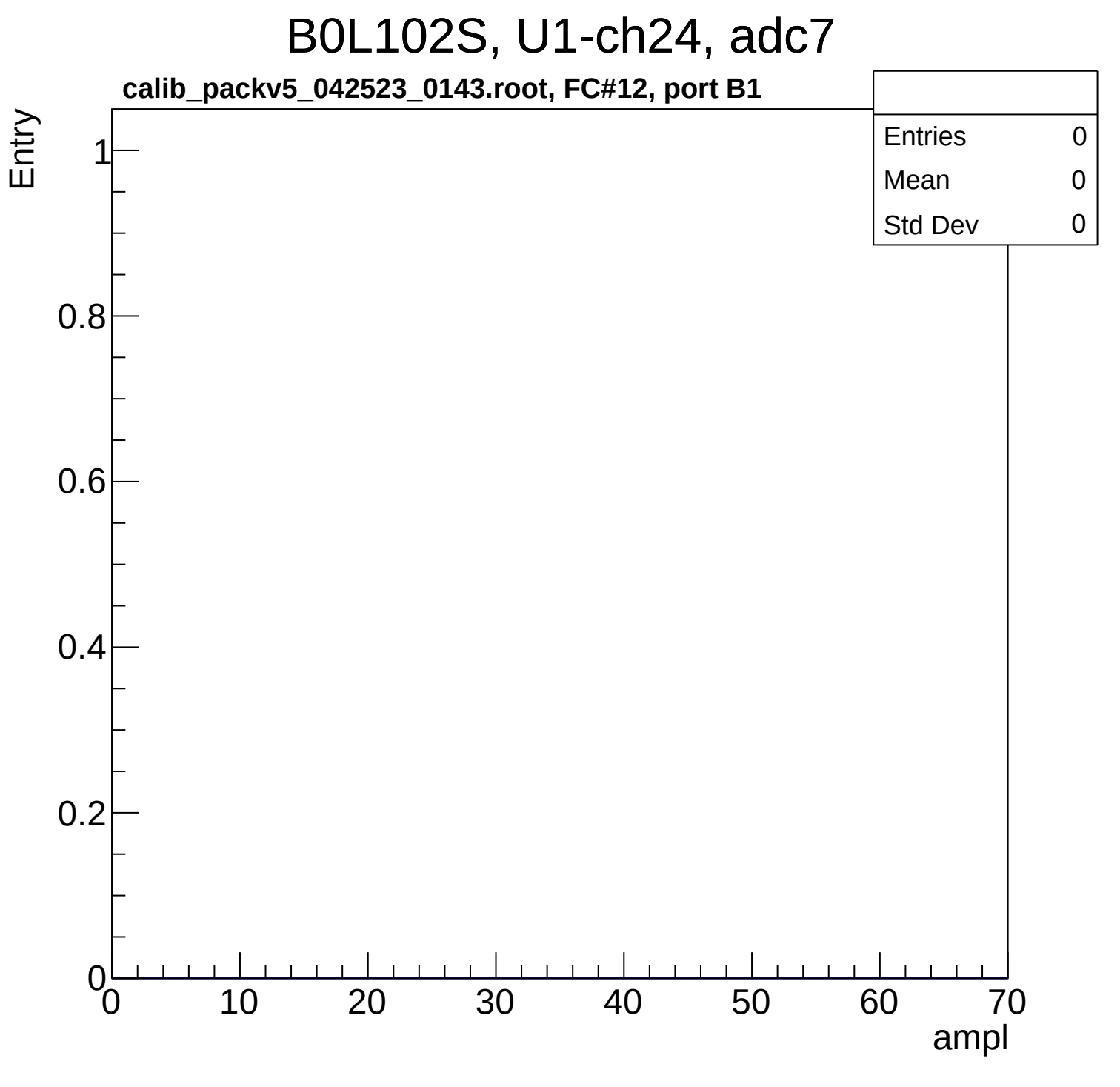
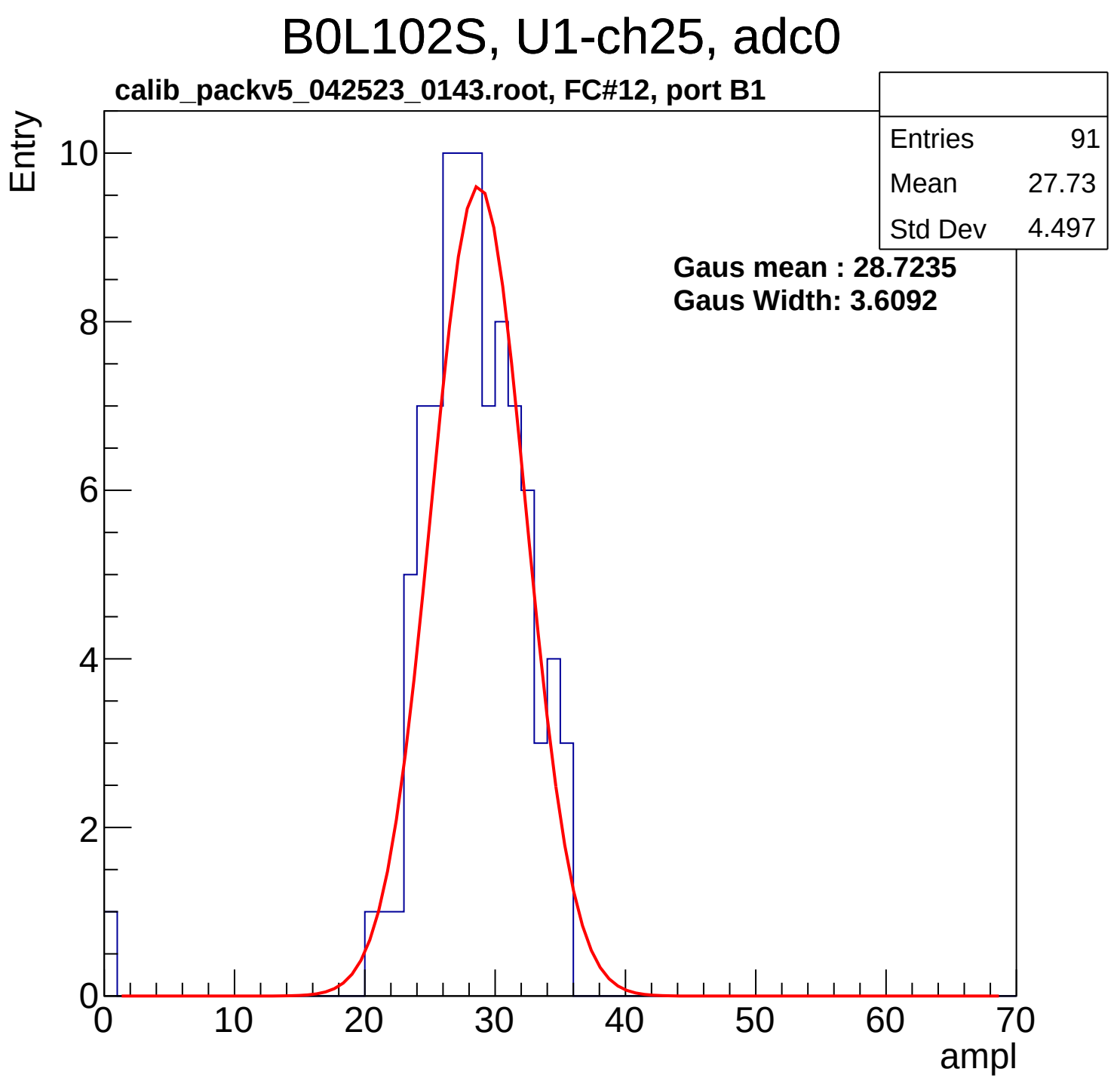
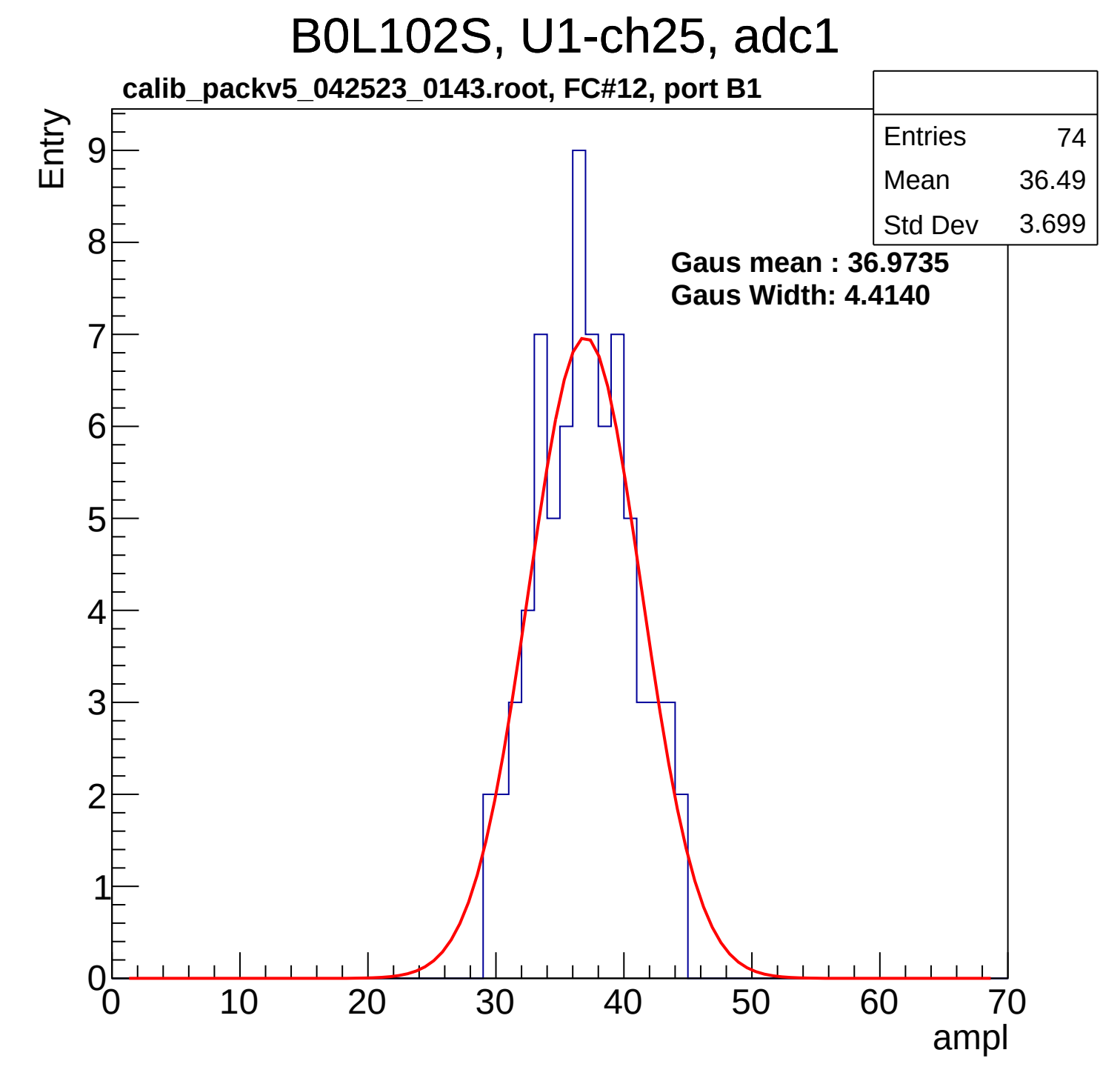
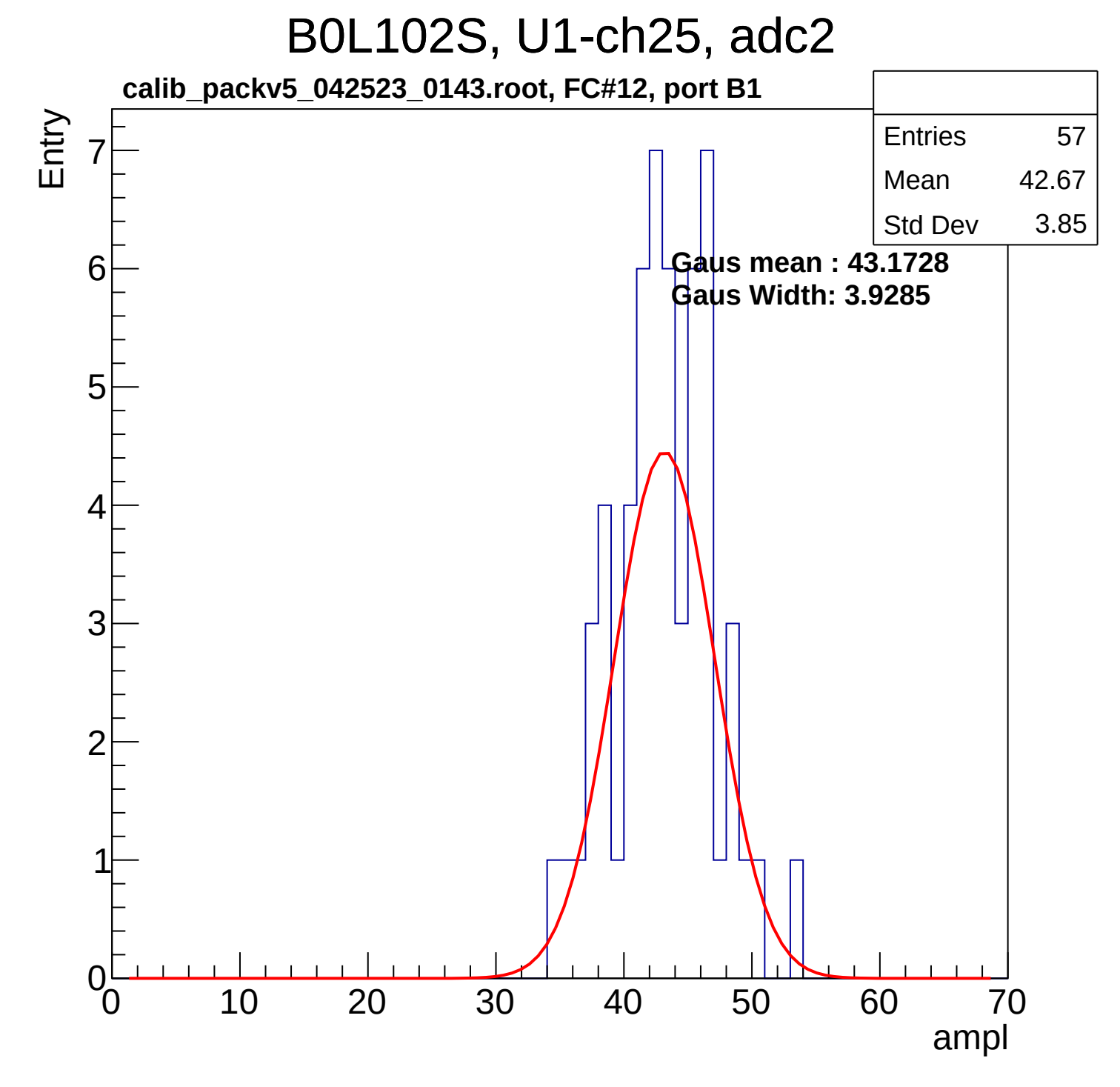
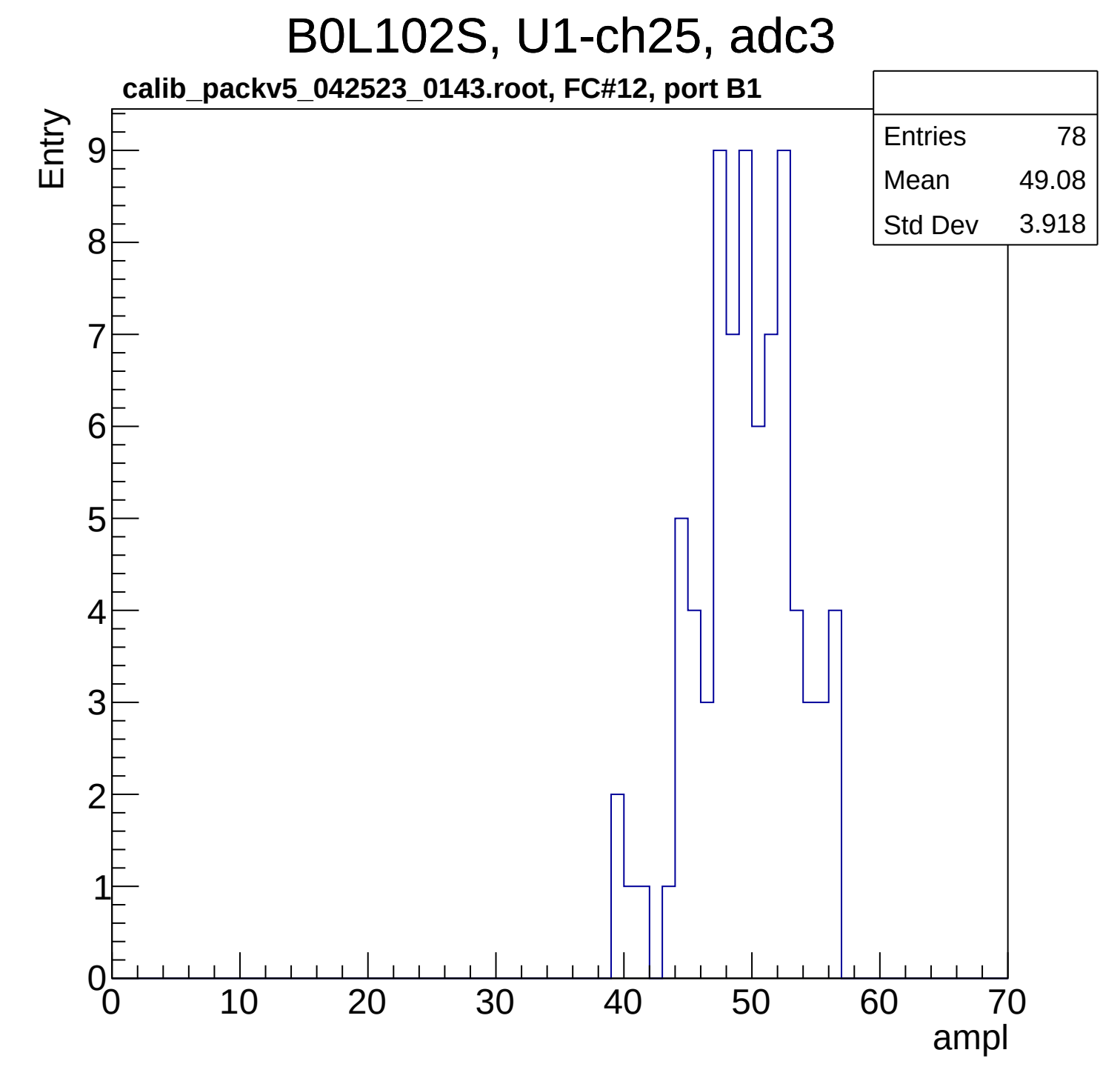

B0L102S, U1-ch24, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.89
Std Dev 0.9938
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch24, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch25, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 91
Mean 27.73
Std Dev 4.497
Gaus mean : 28.7235
Gaus Width: 3.6092
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch25, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.49
Std Dev 3.699
Gaus mean : 36.9735
Gaus Width: 4.4140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch25, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 42.67
Std Dev 3.85
Gaus mean : 43.1728
Gaus Width: 3.9285
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch25, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 49.08
Std Dev 3.918
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
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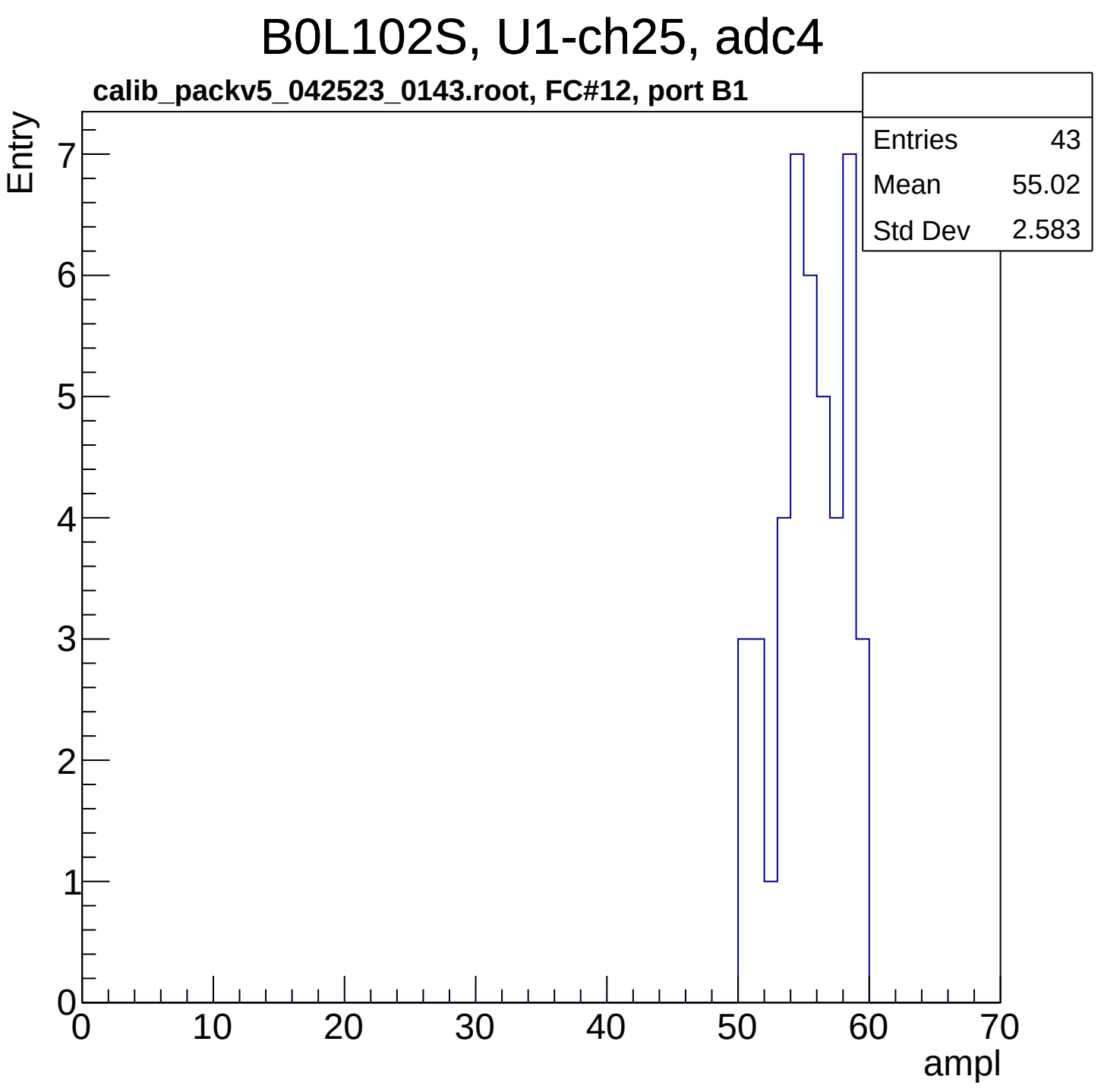
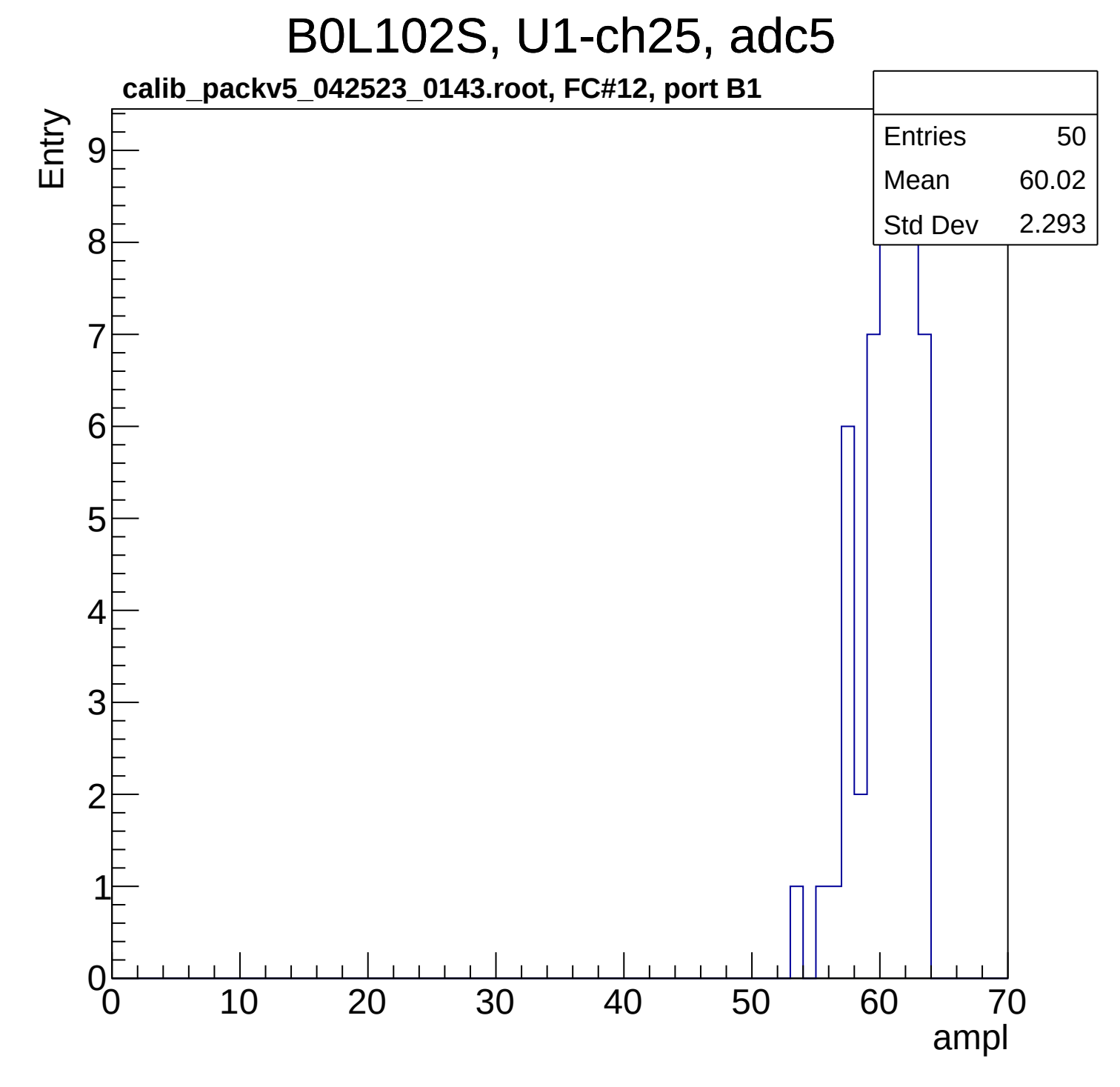
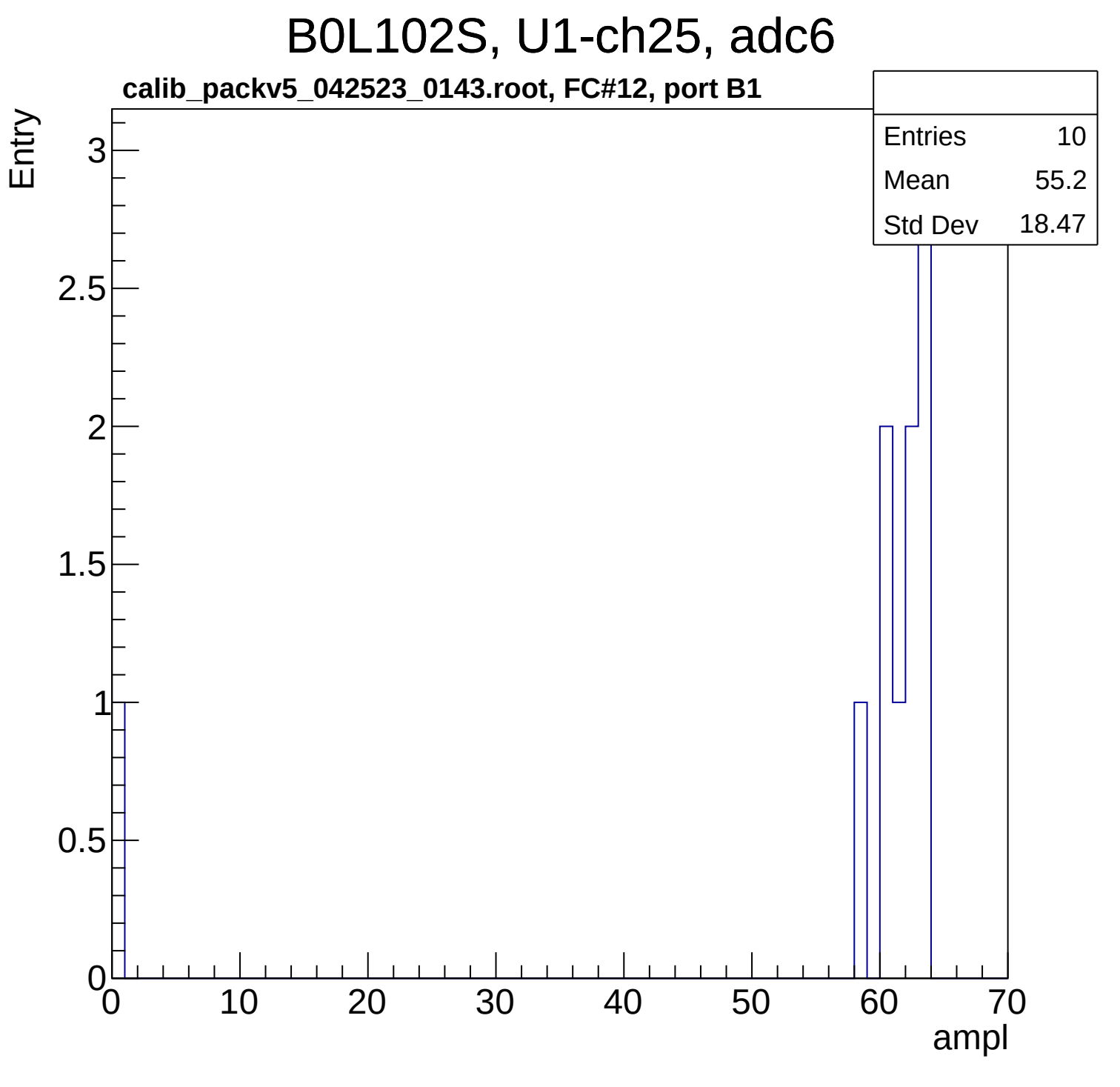
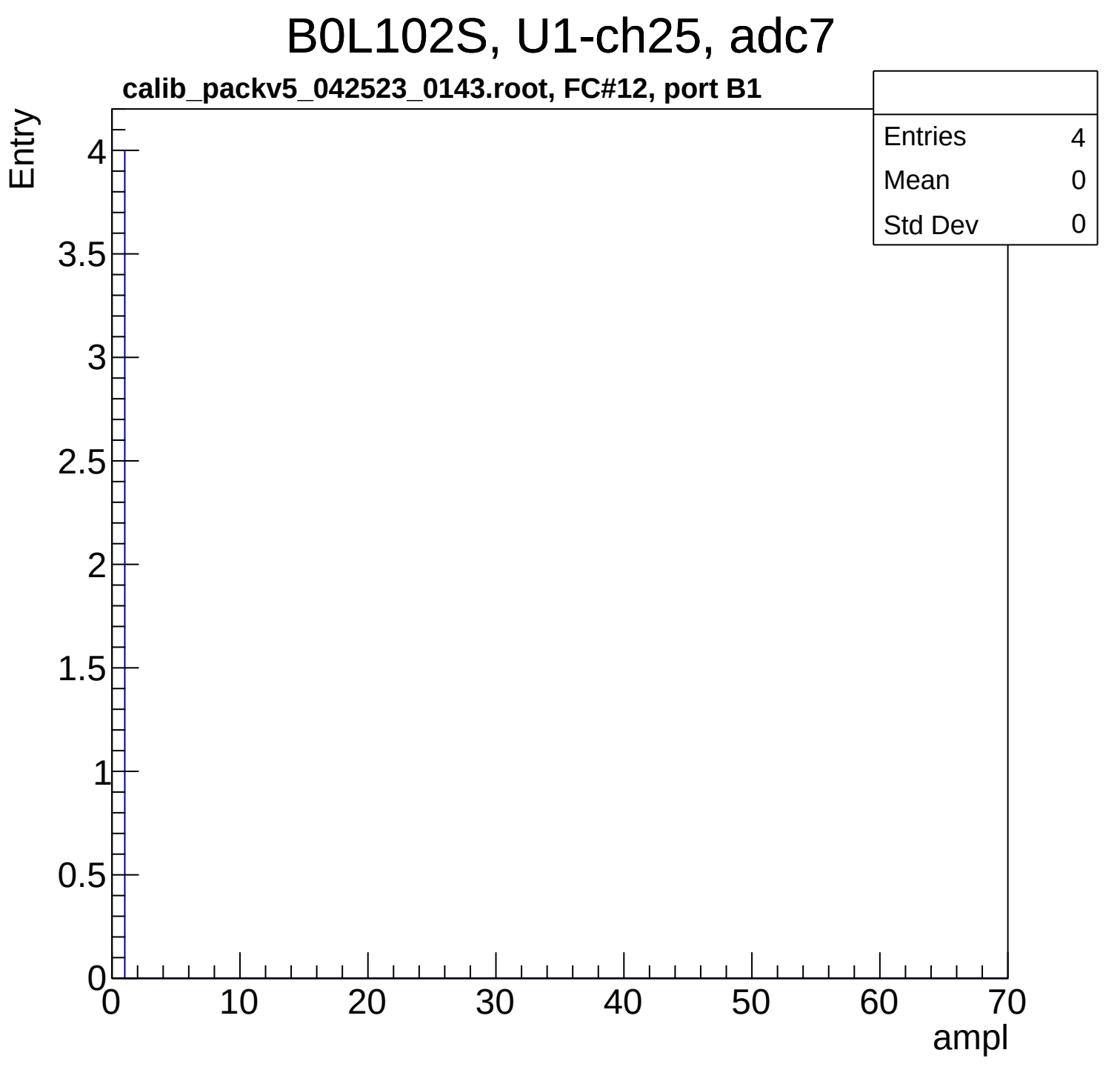
60
70


B0L102S, U1-ch25, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.02
Std Dev 2.583
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch25, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 60.02
Std Dev 2.293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch25, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 55.2
Std Dev 18.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch25, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 0
Std Dev 0
Entry
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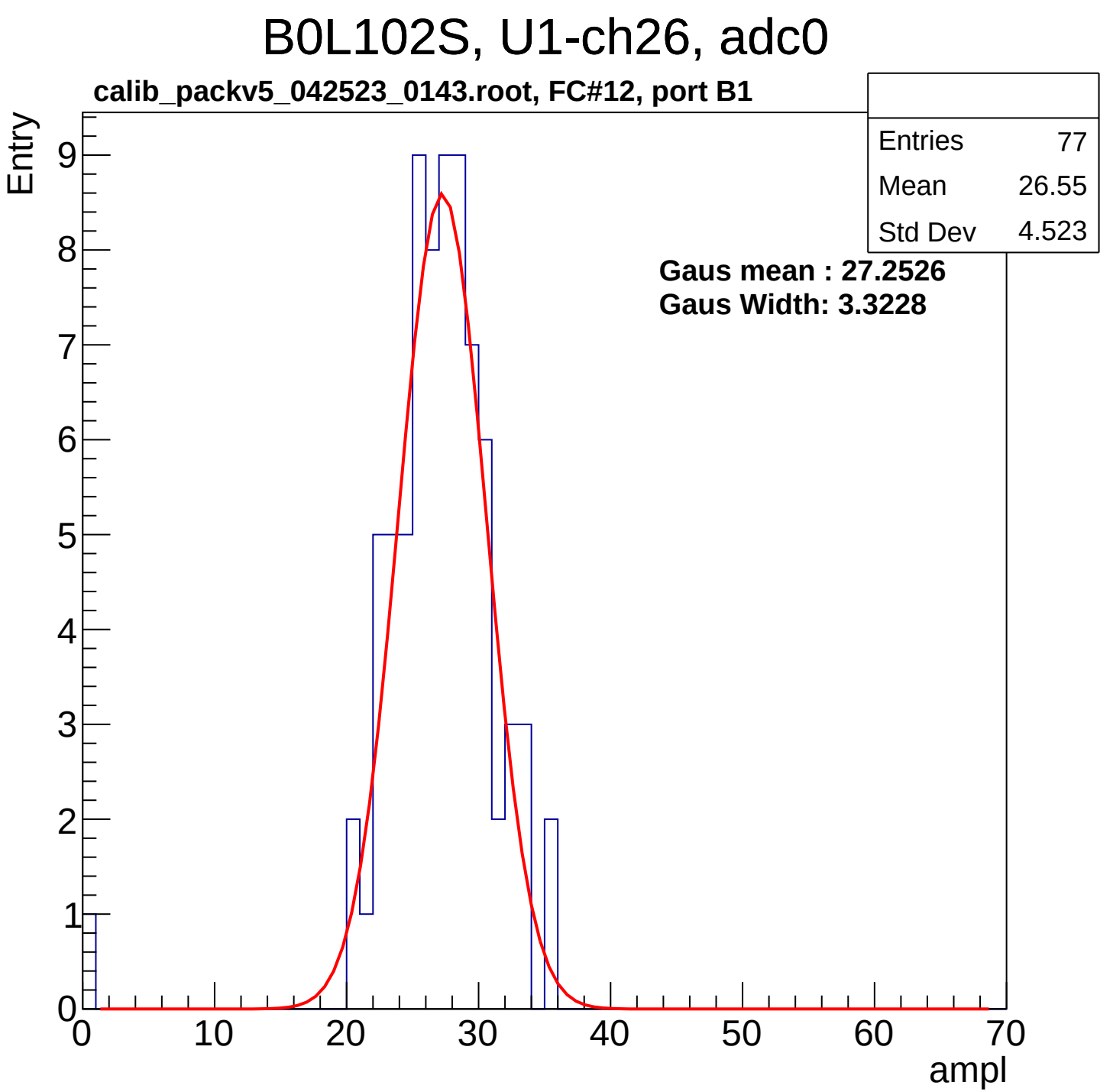
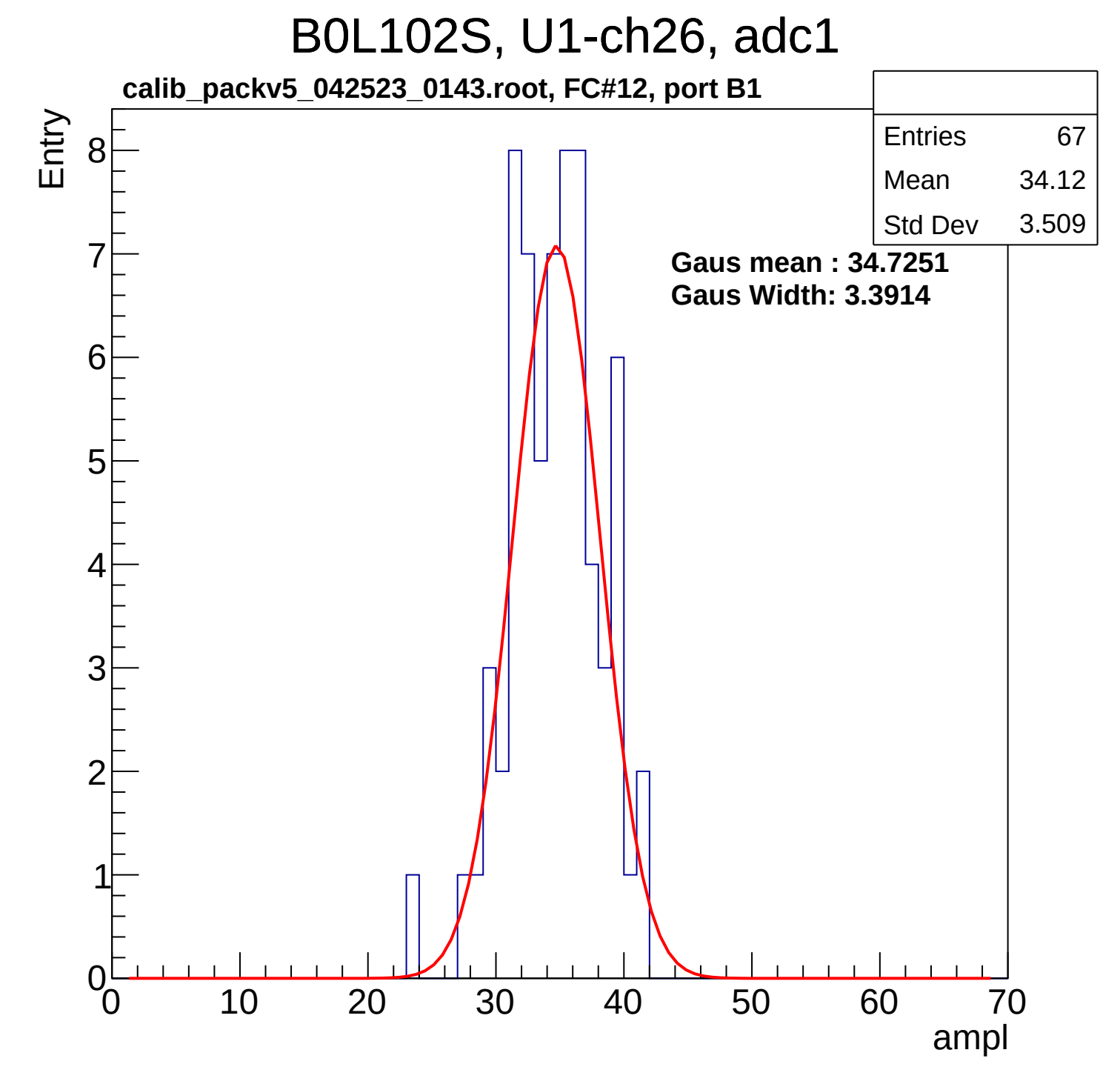
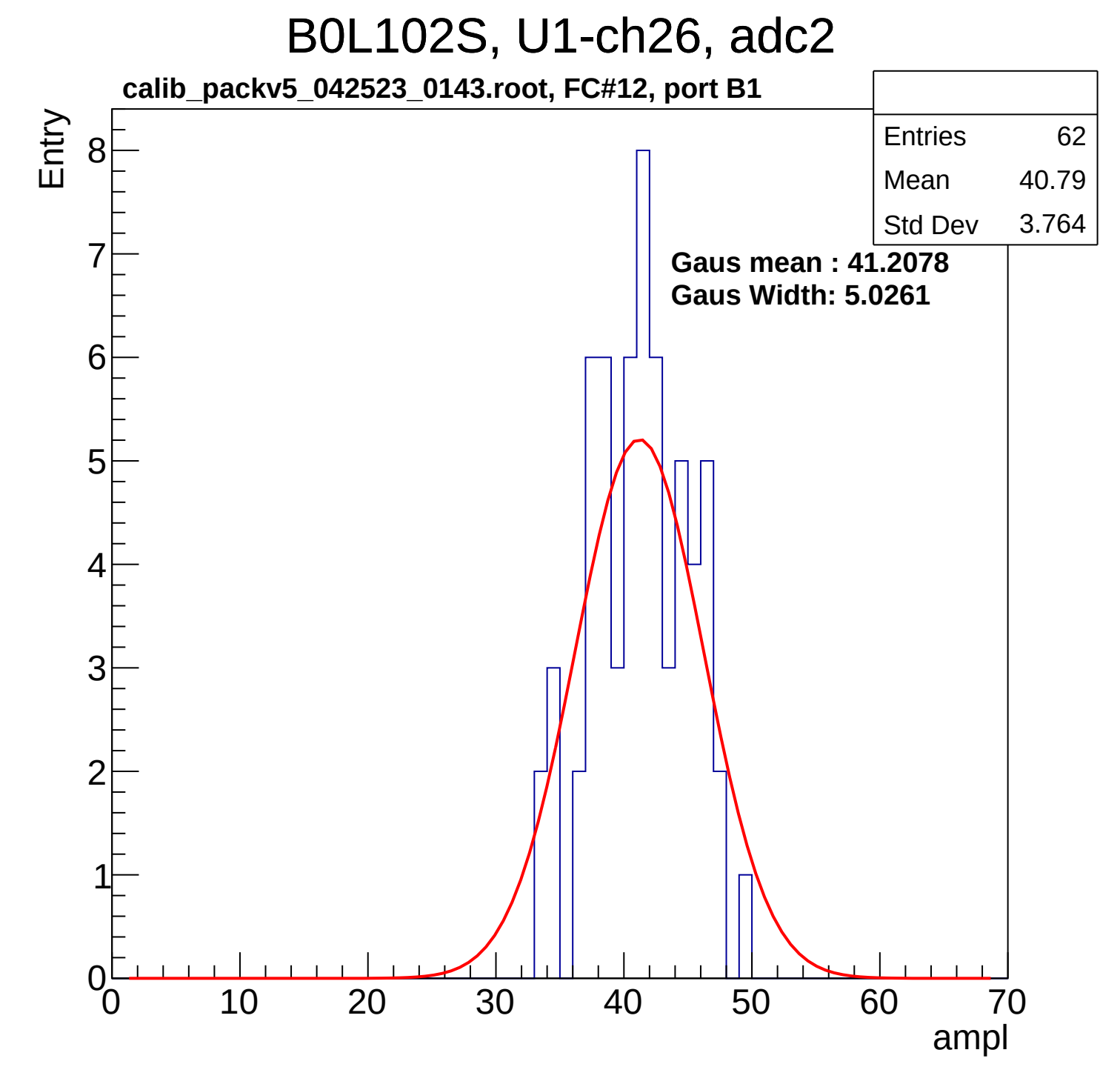
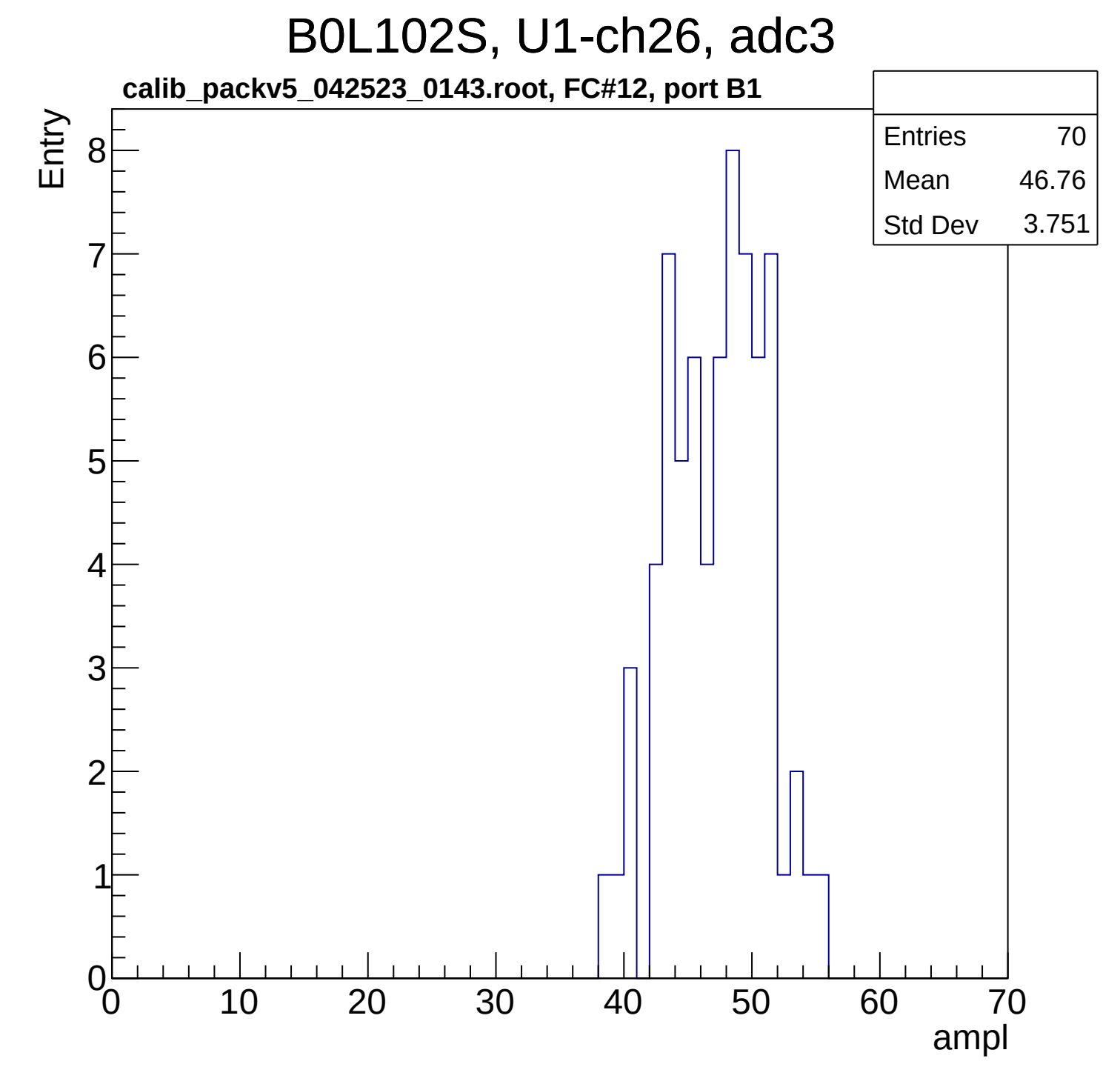
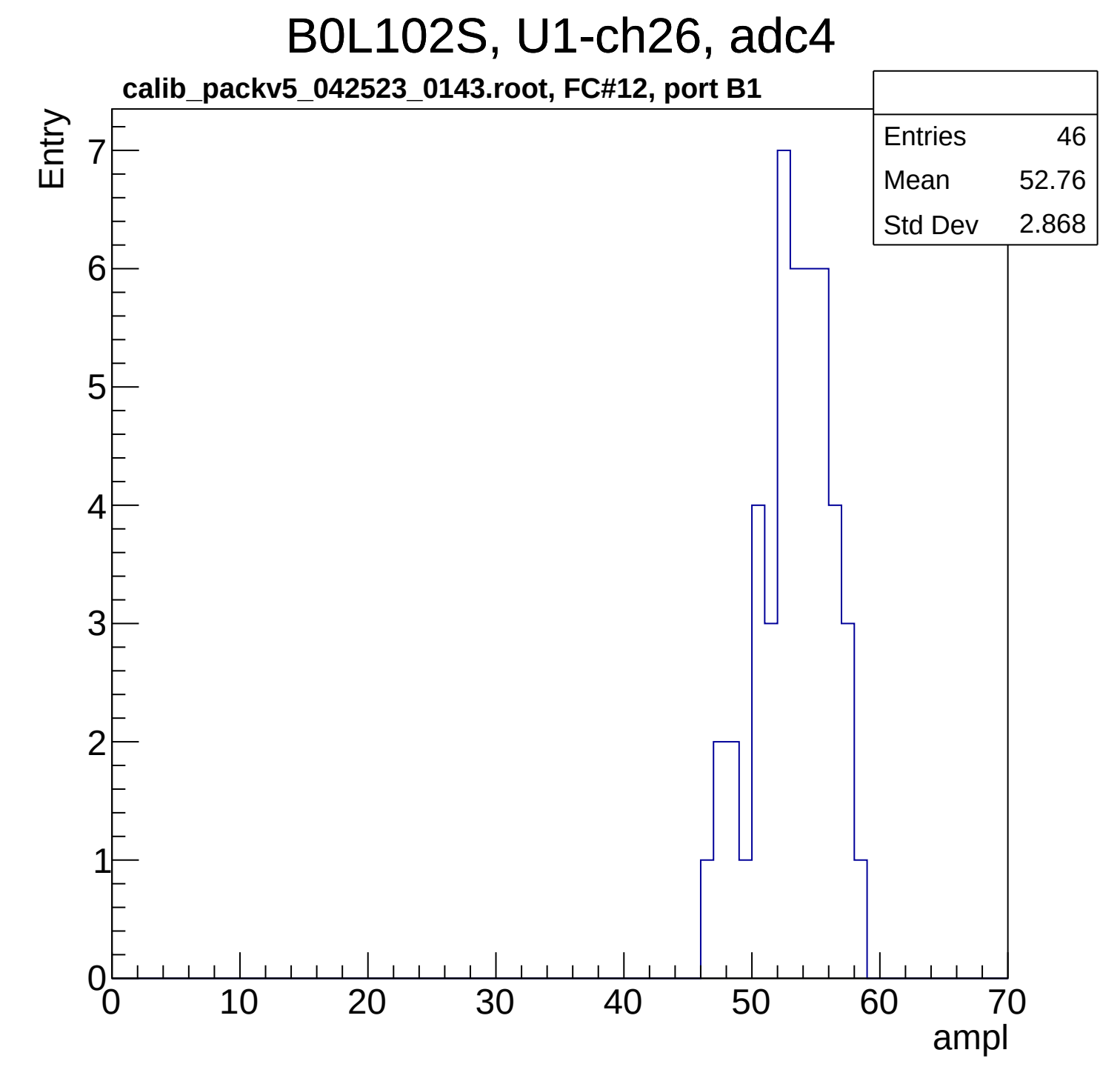
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

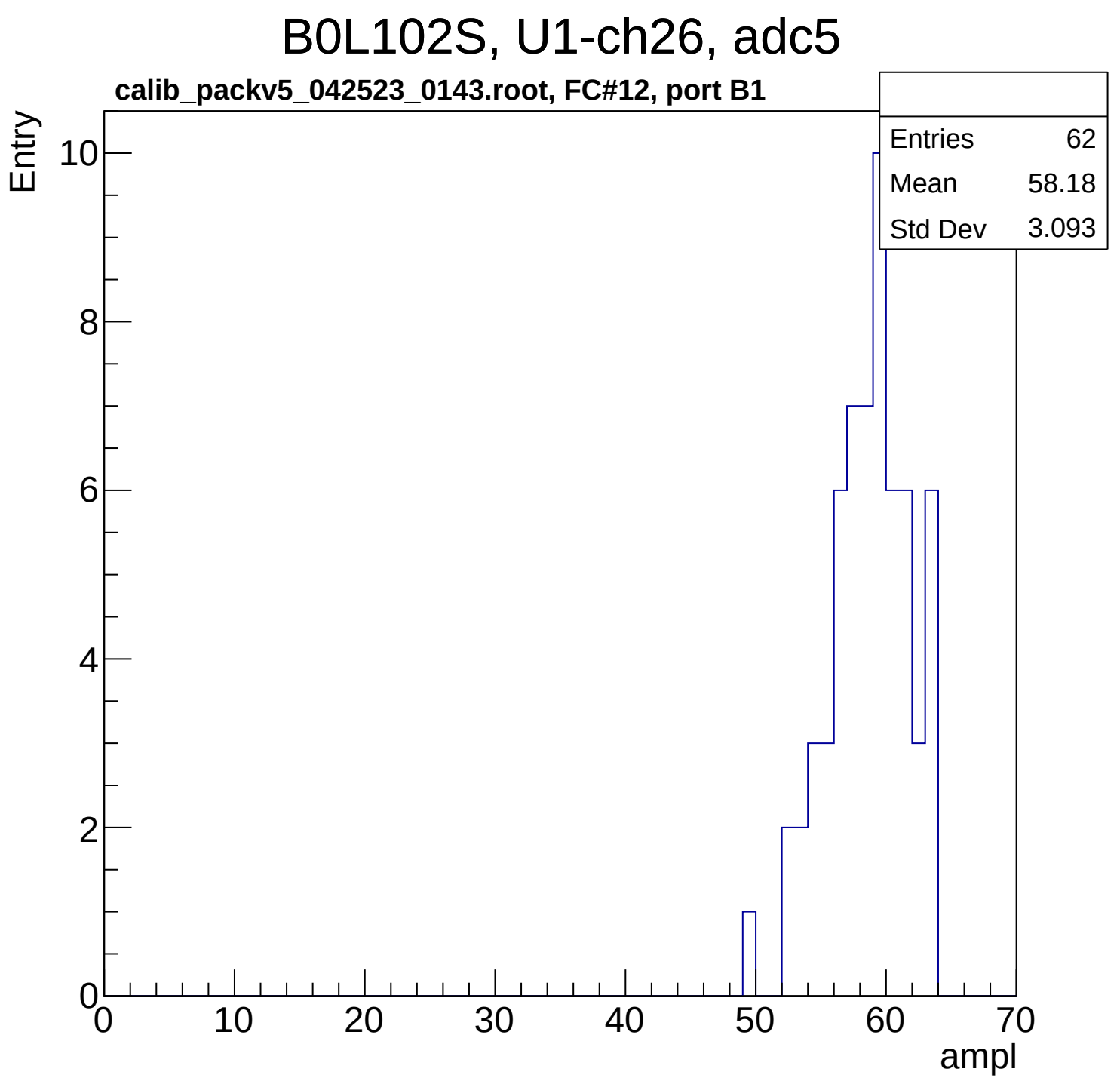

B0L102S, U1-ch26, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 26.55
Std Dev 4.523
Gaus mean : 27.2526
Gaus Width: 3.3228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

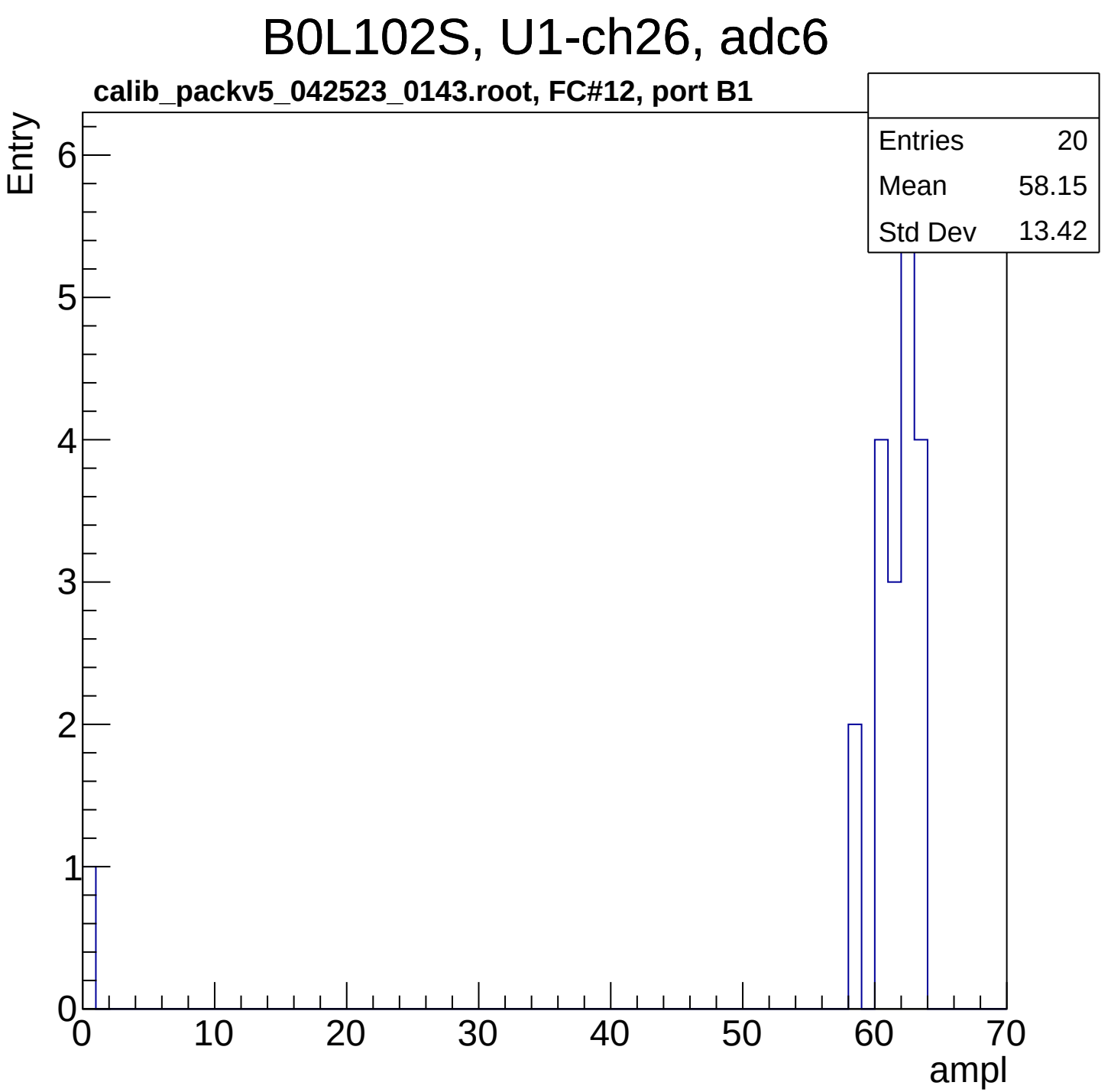

B0L102S, U1-ch26, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 34.12
Std Dev 3.509
Gaus mean : 34.7251
Gaus Width: 3.3914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

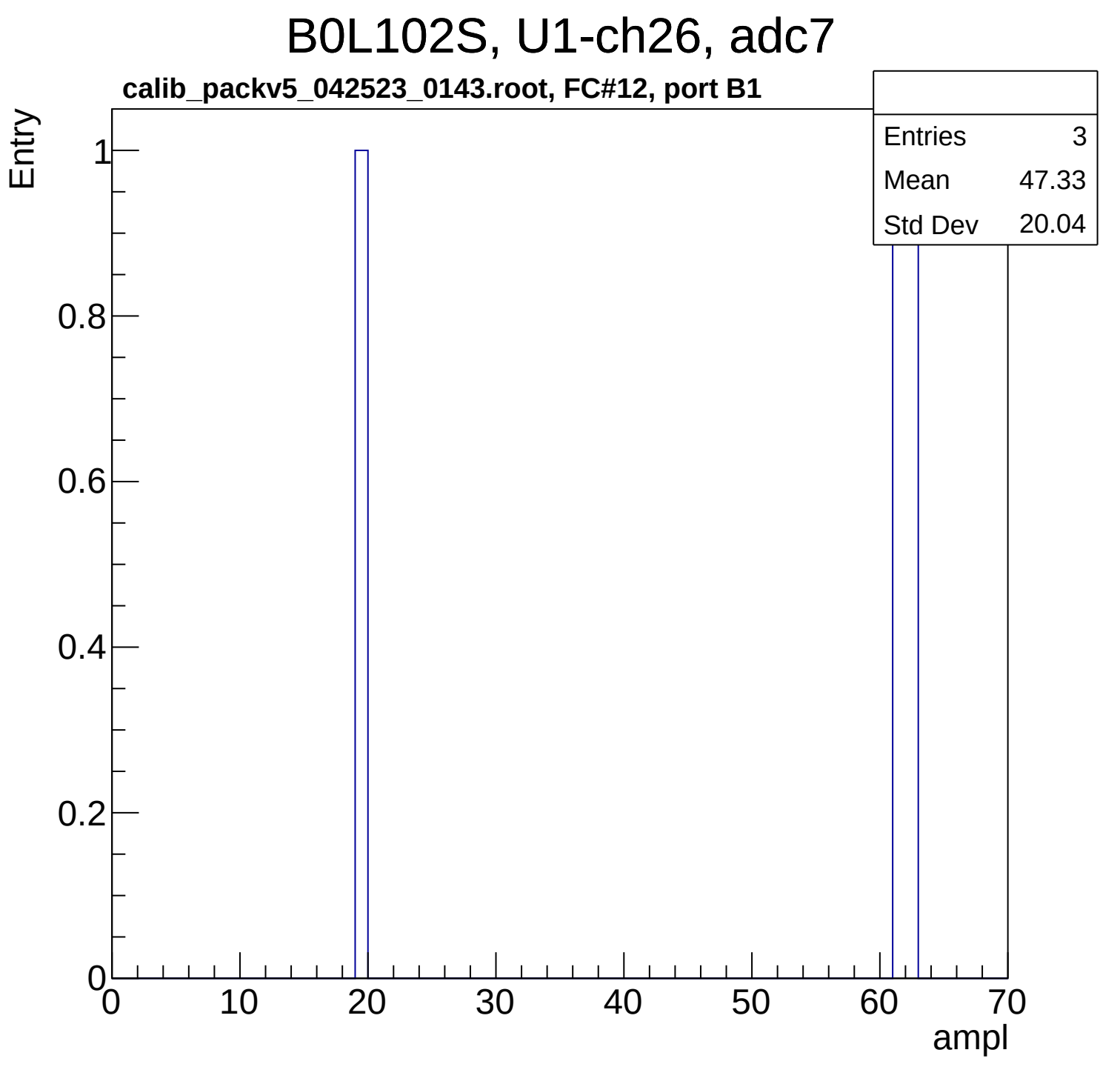

B0L102S, U1-ch26, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 40.79
Std Dev 3.764
Gaus mean : 41.2078
Gaus Width: 5.0261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

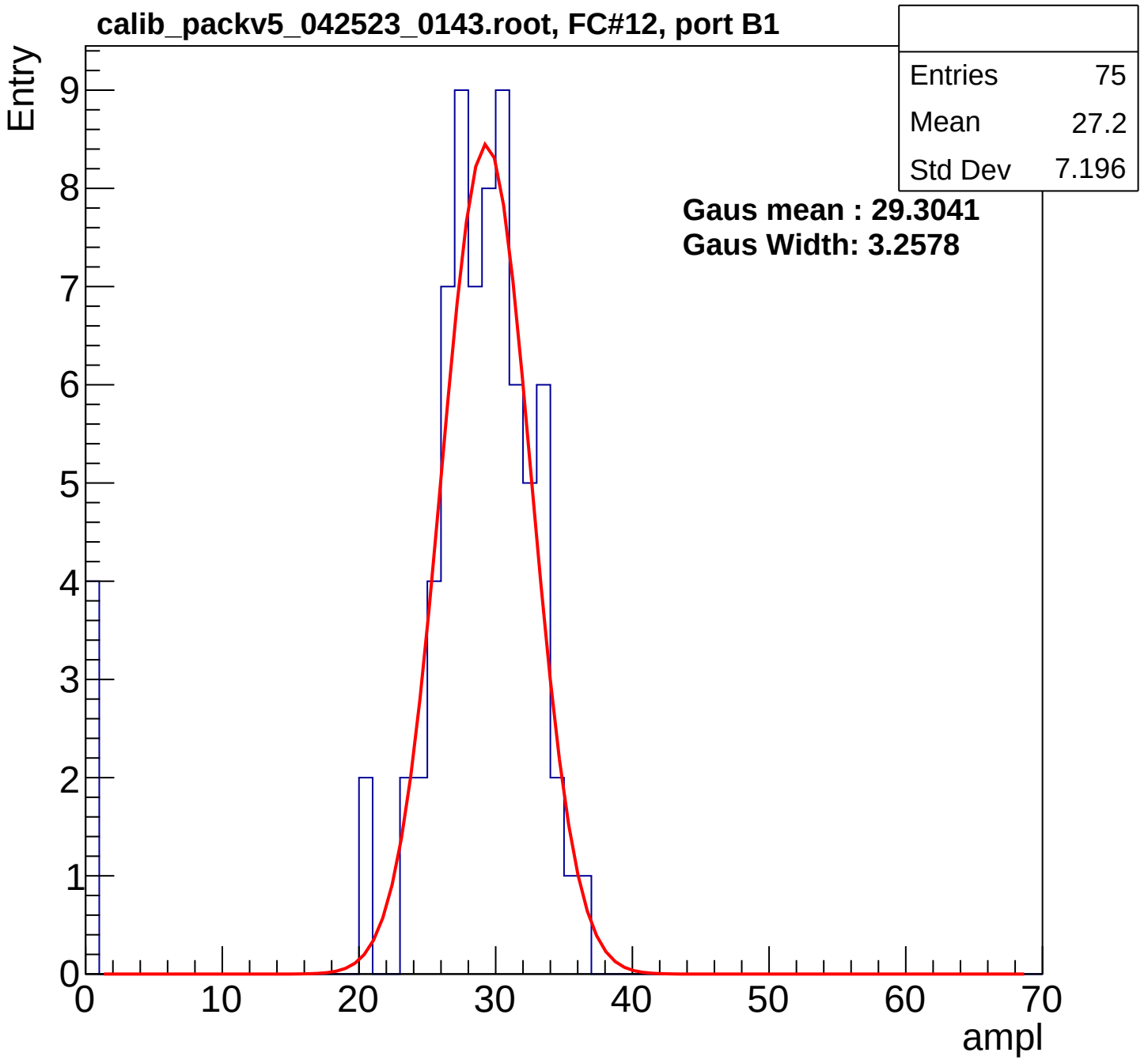

B0L102S, U1-ch26, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 46.76
Std Dev 3.751
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

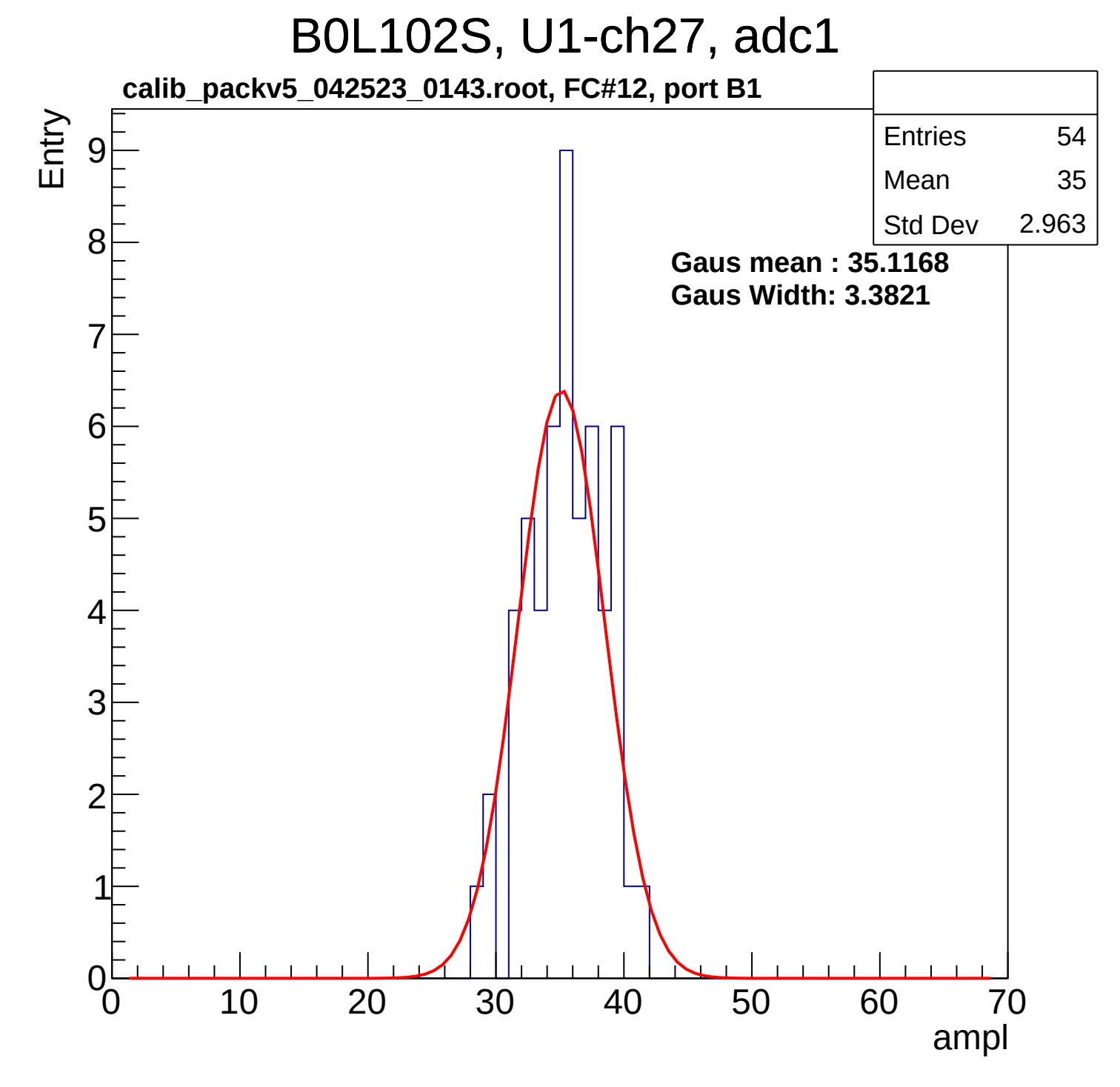

B0L102S, U1-ch26, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 52.76
Std Dev 2.868
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

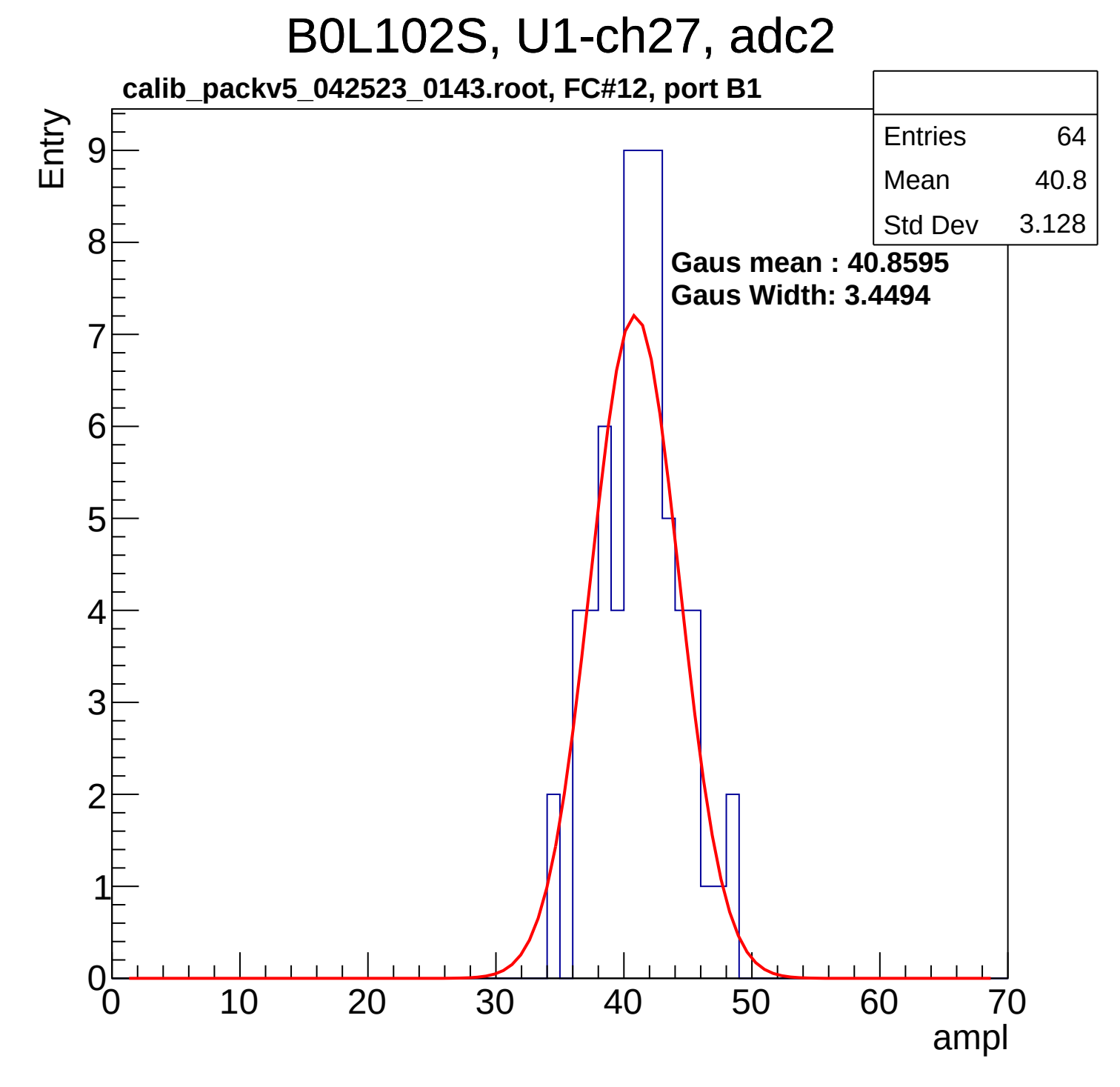

B0L102S, U1-ch26, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 58.18
Std Dev 3.093
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

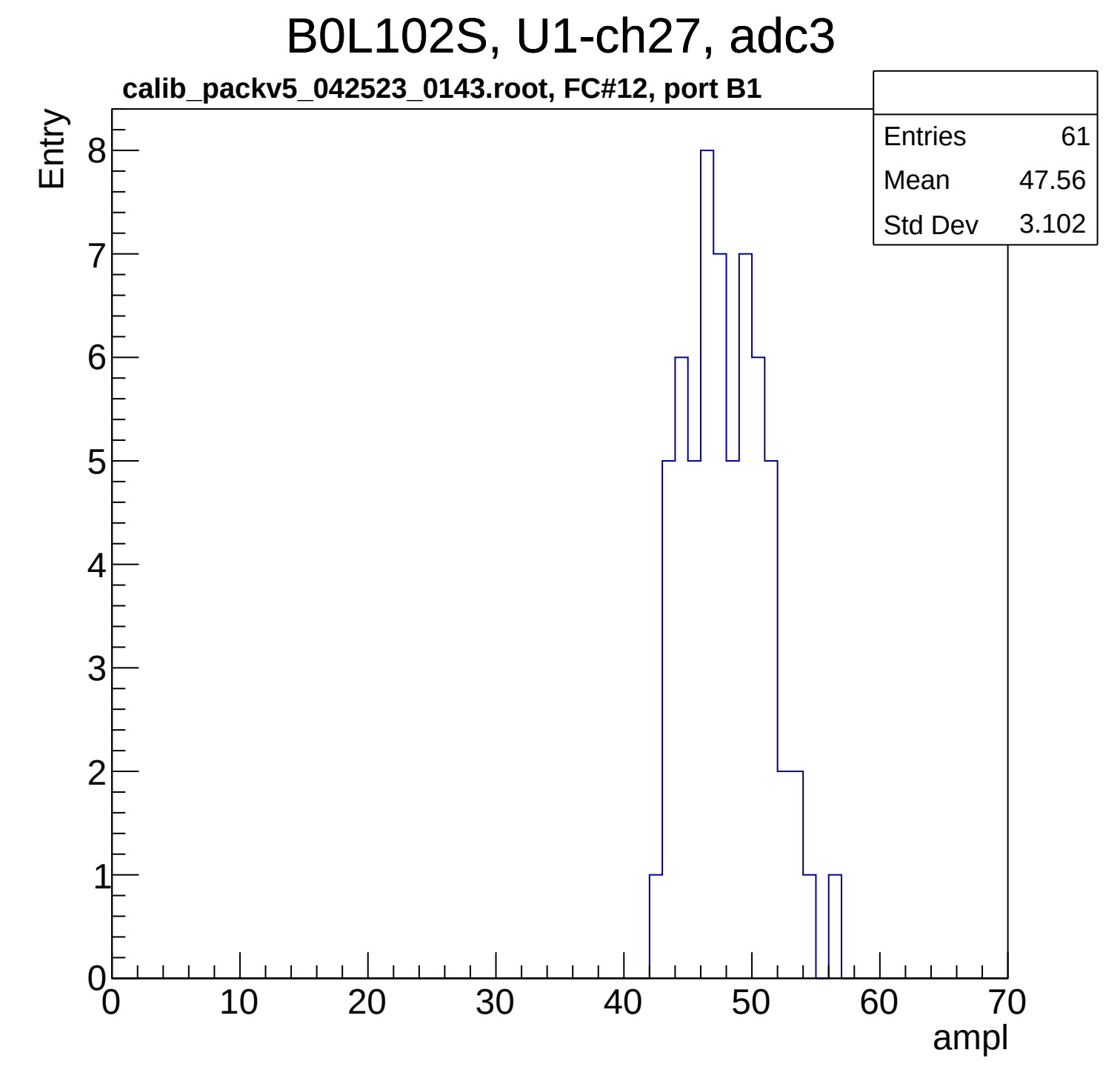

B0L102S, U1-ch26, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 20
Mean 58.15
Std Dev 13.42
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

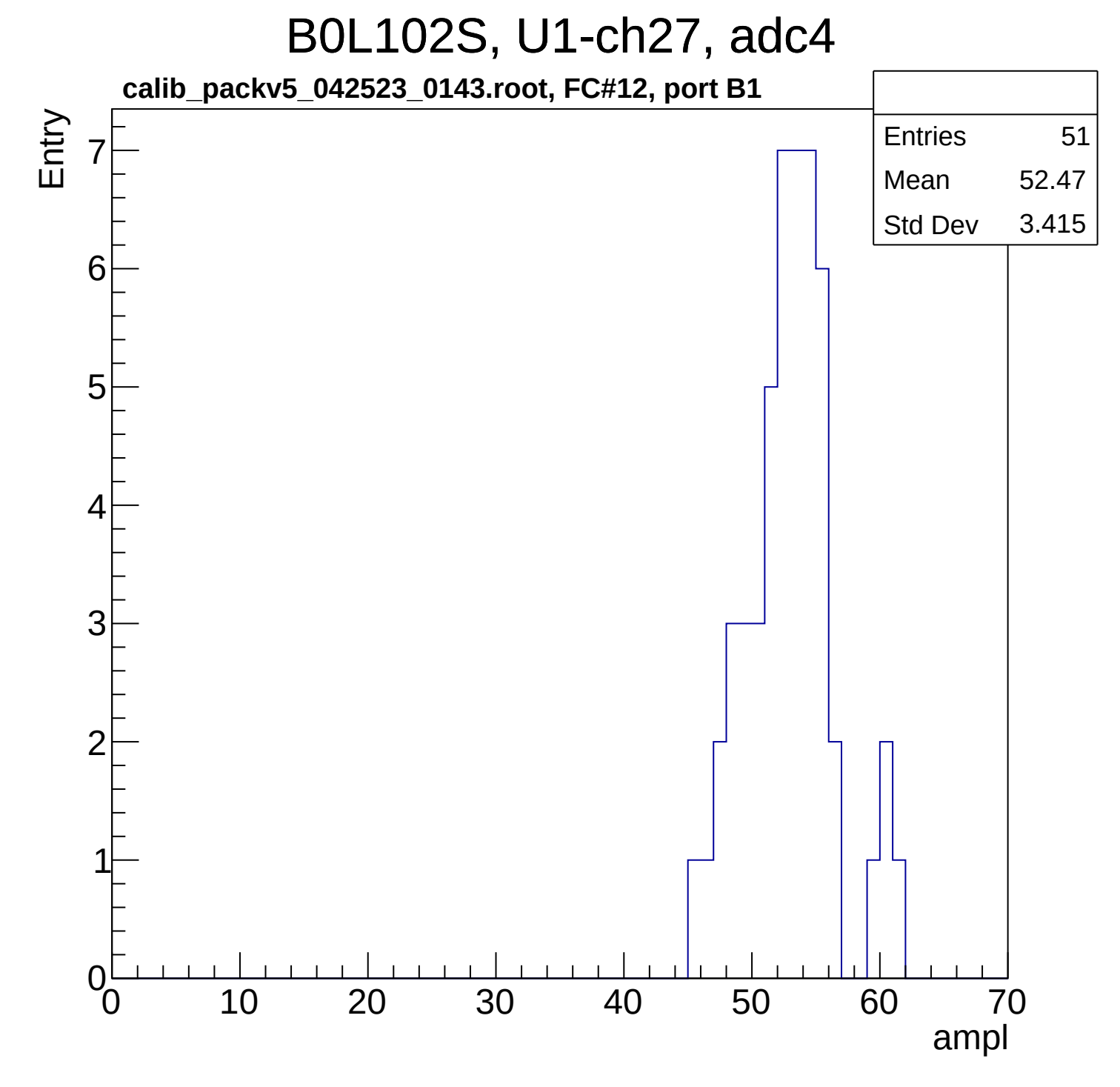

B0L102S, U1-ch26, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 47.33
Std Dev 20.04
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

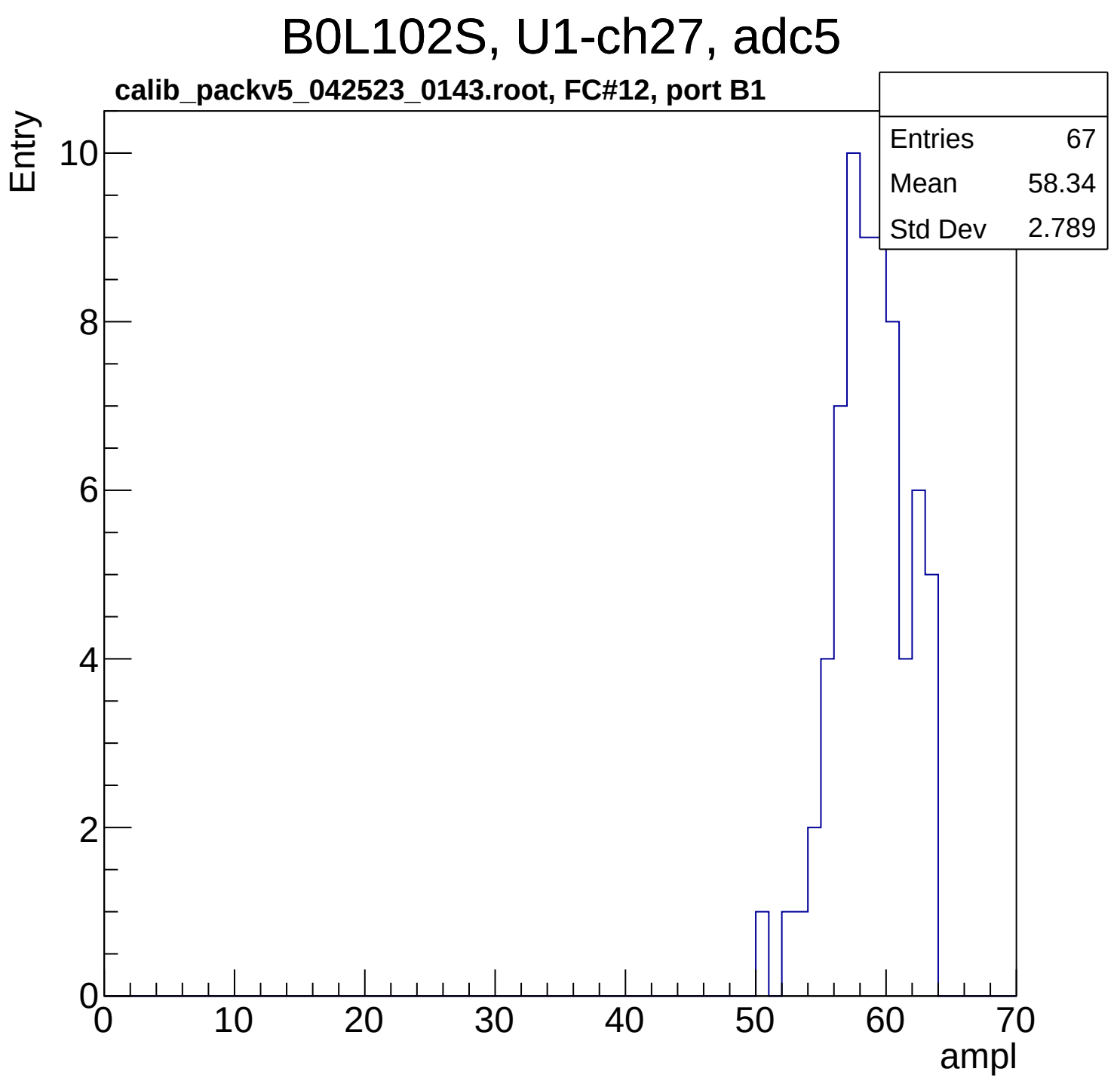

B0L102S, U1-ch27, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 27.2
Std Dev 7.196
Gaus mean : 29.3041
Gaus Width: 3.2578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

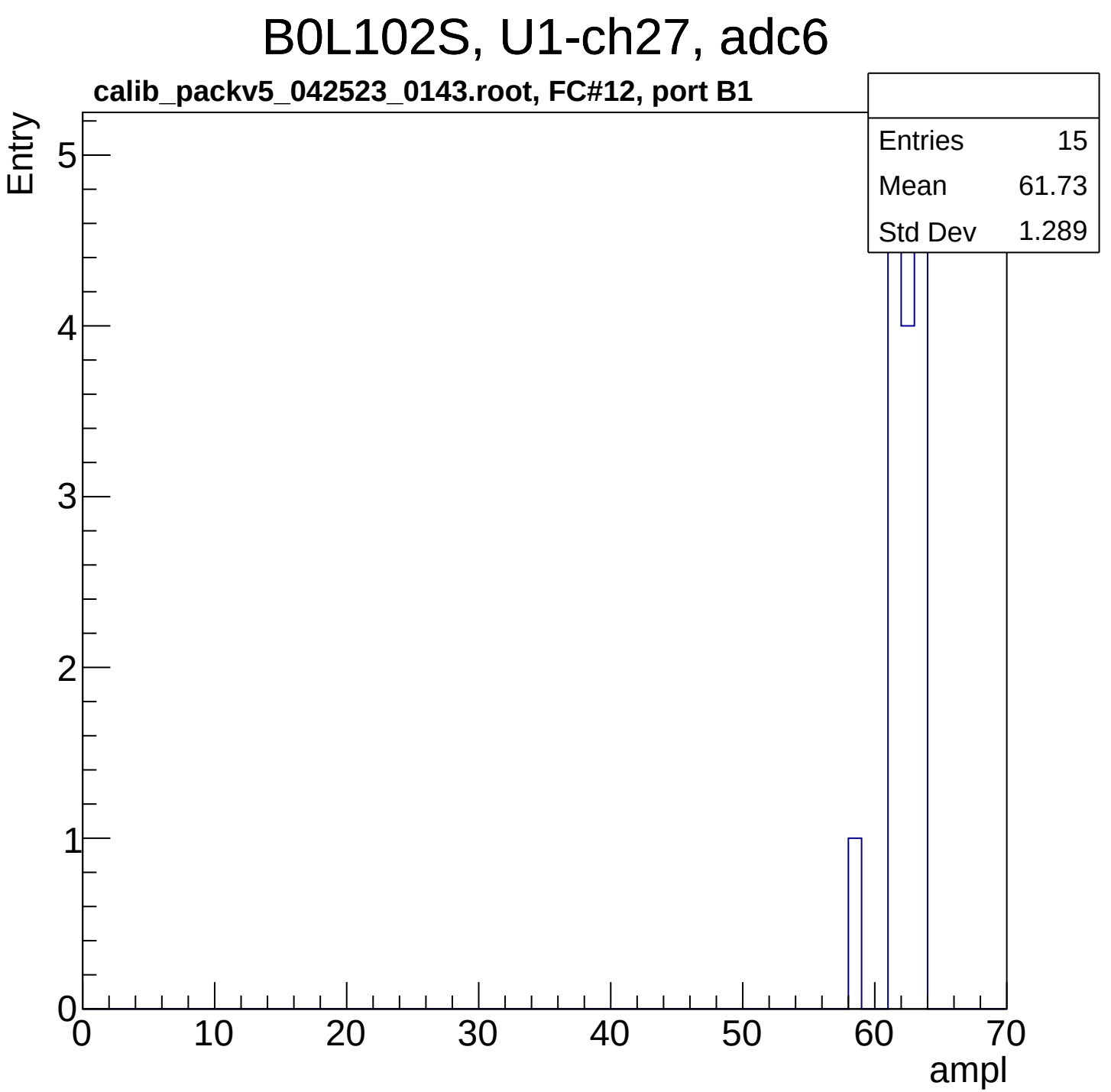

B0L102S, U1-ch27, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 35
Std Dev 2.963
Gaus mean : 35.1168
Gaus Width: 3.3821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

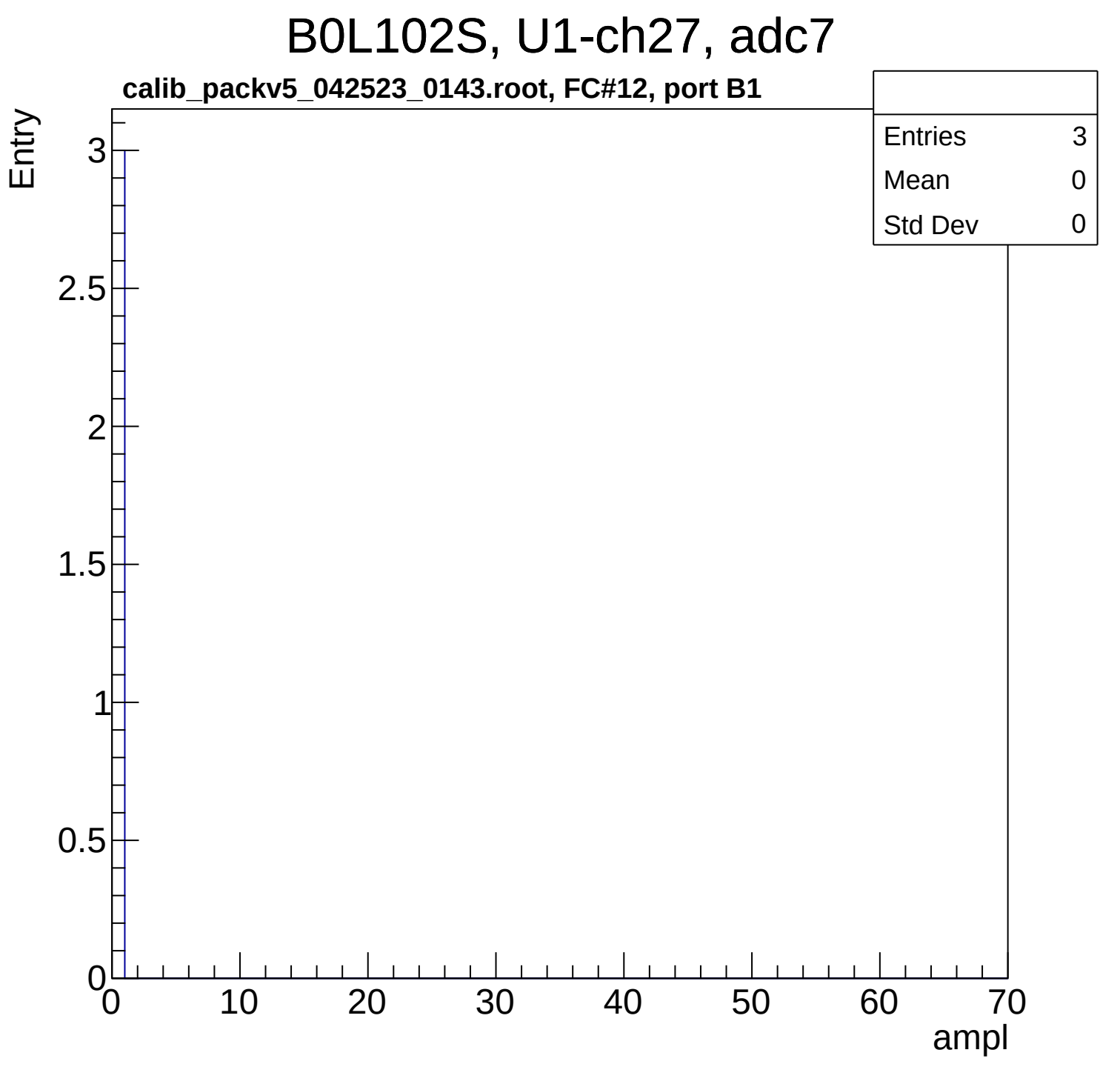

B0L102S, U1-ch27, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 40.8
Std Dev 3.128
Gaus mean : 40.8595
Gaus Width: 3.4494
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

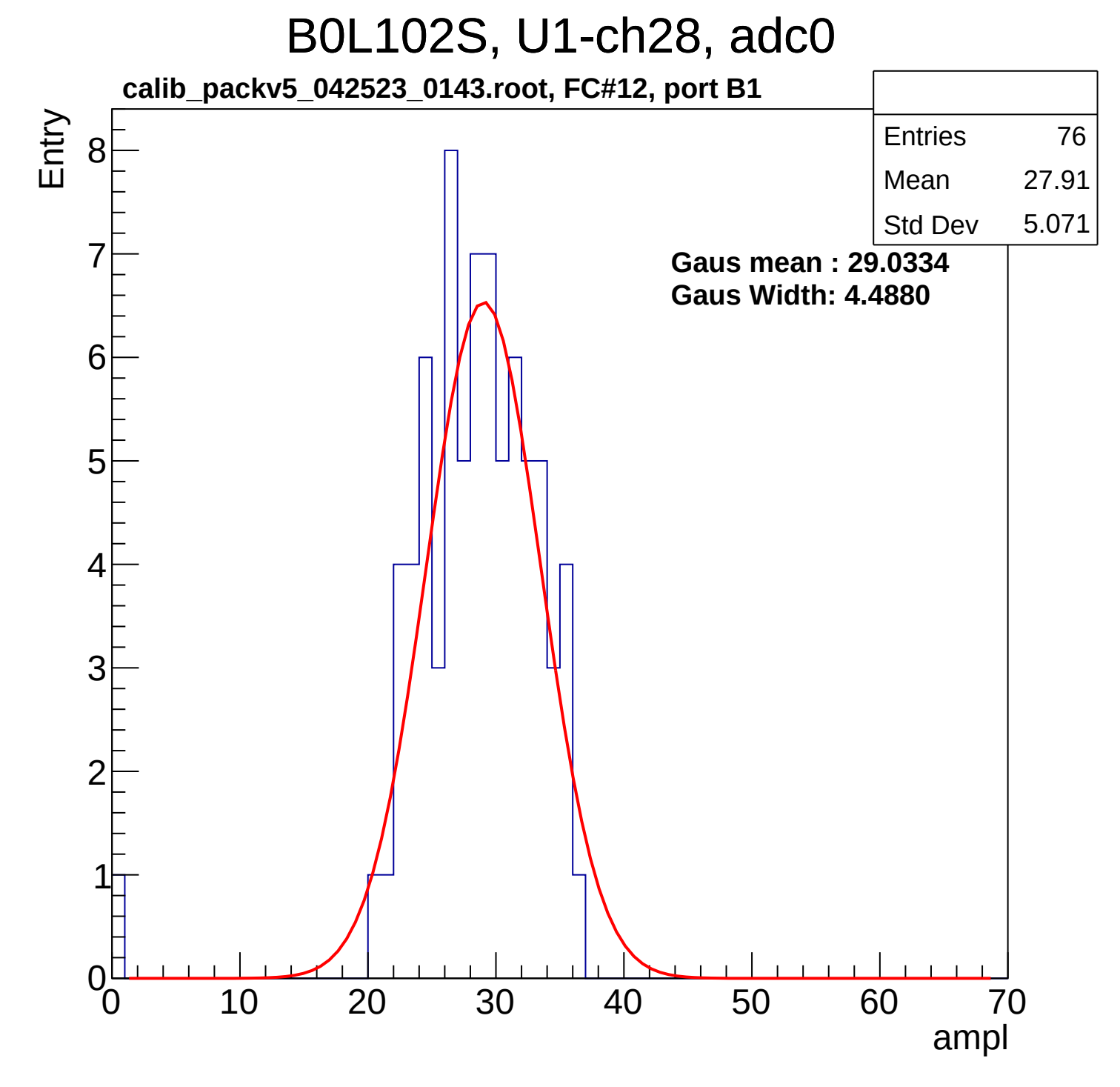

B0L102S, U1-ch27, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 47.56
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

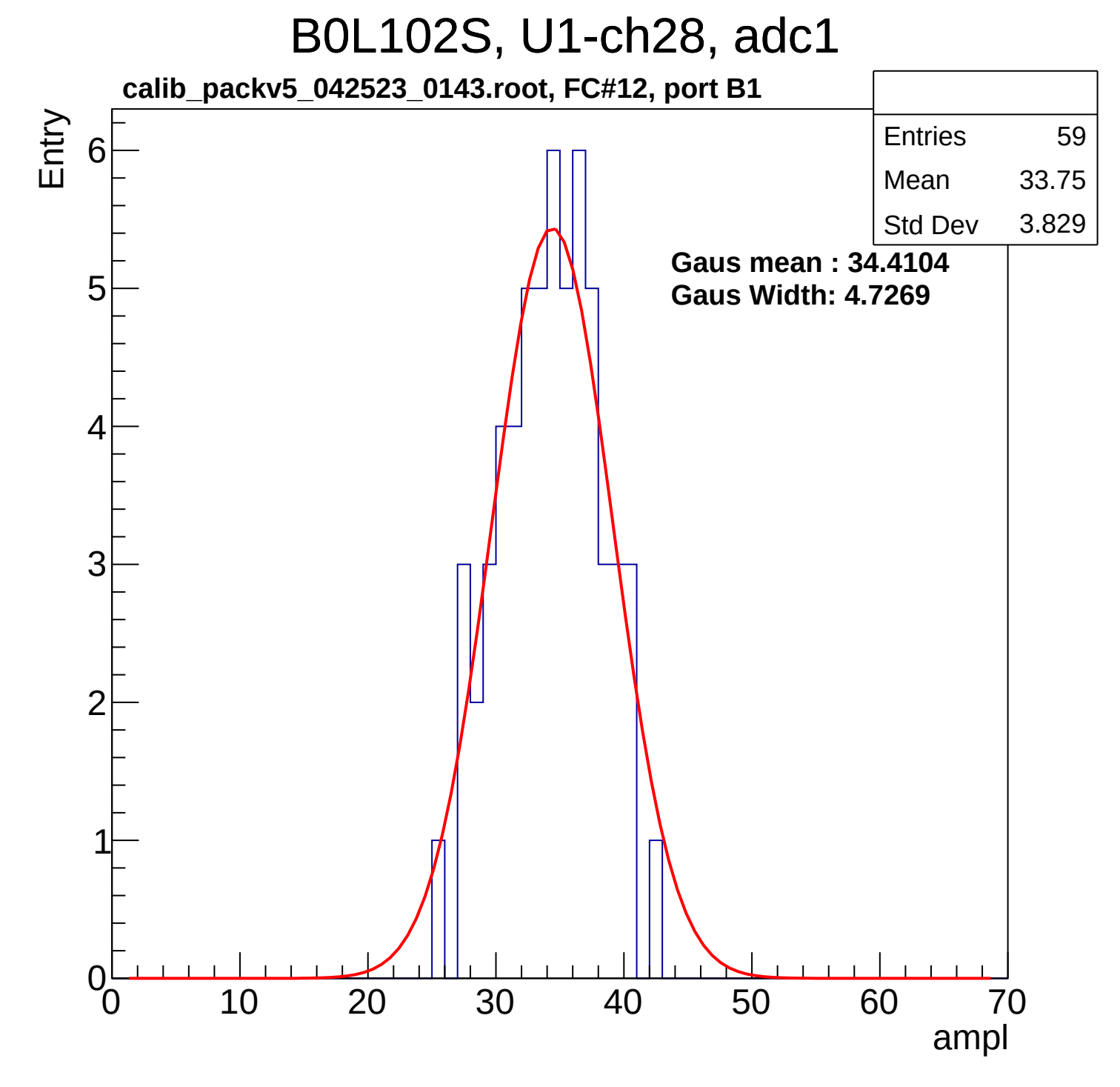

B0L102S, U1-ch27, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 52.47
Std Dev 3.415
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

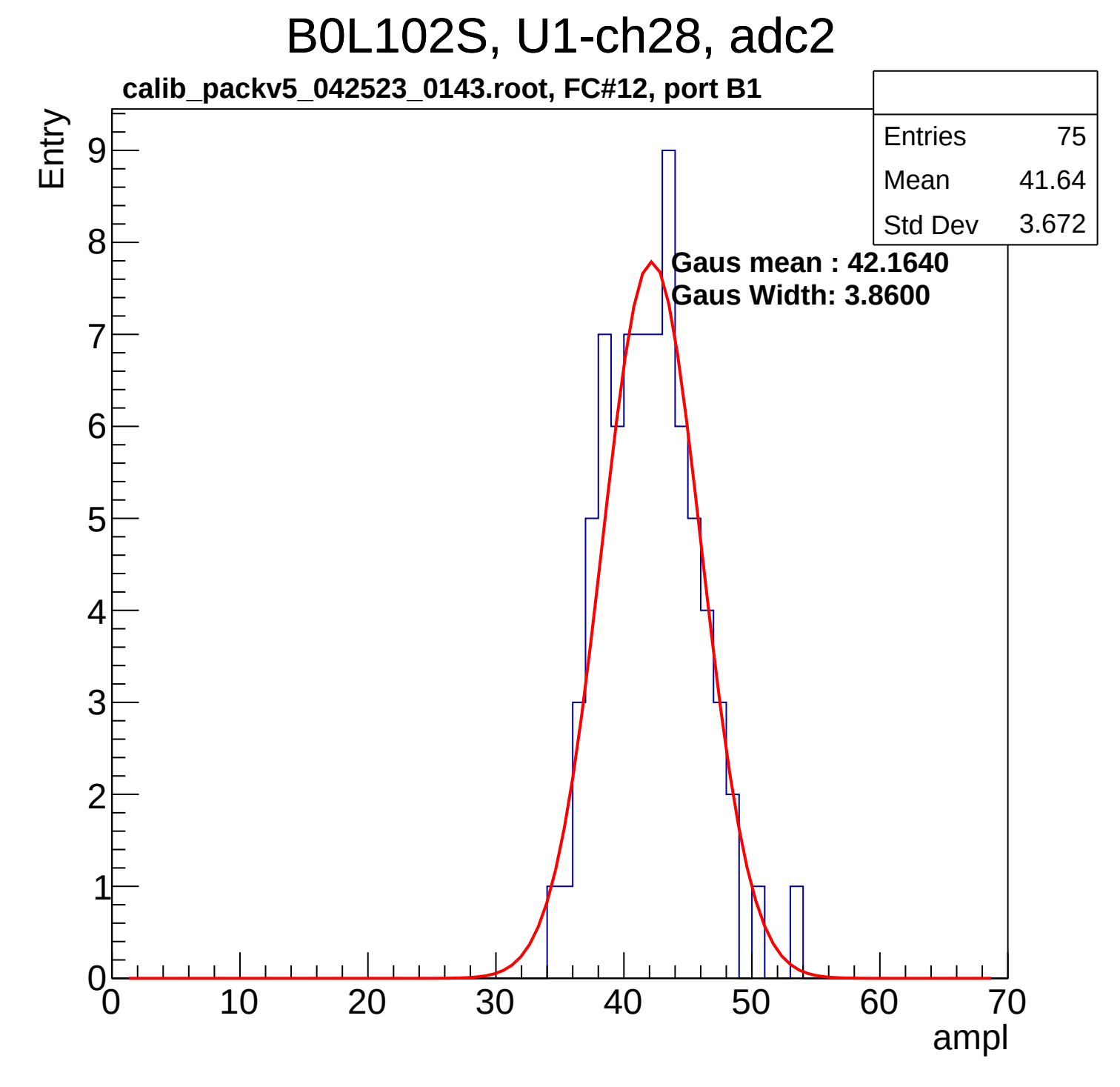

B0L102S, U1-ch27, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 58.34
Std Dev 2.789
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

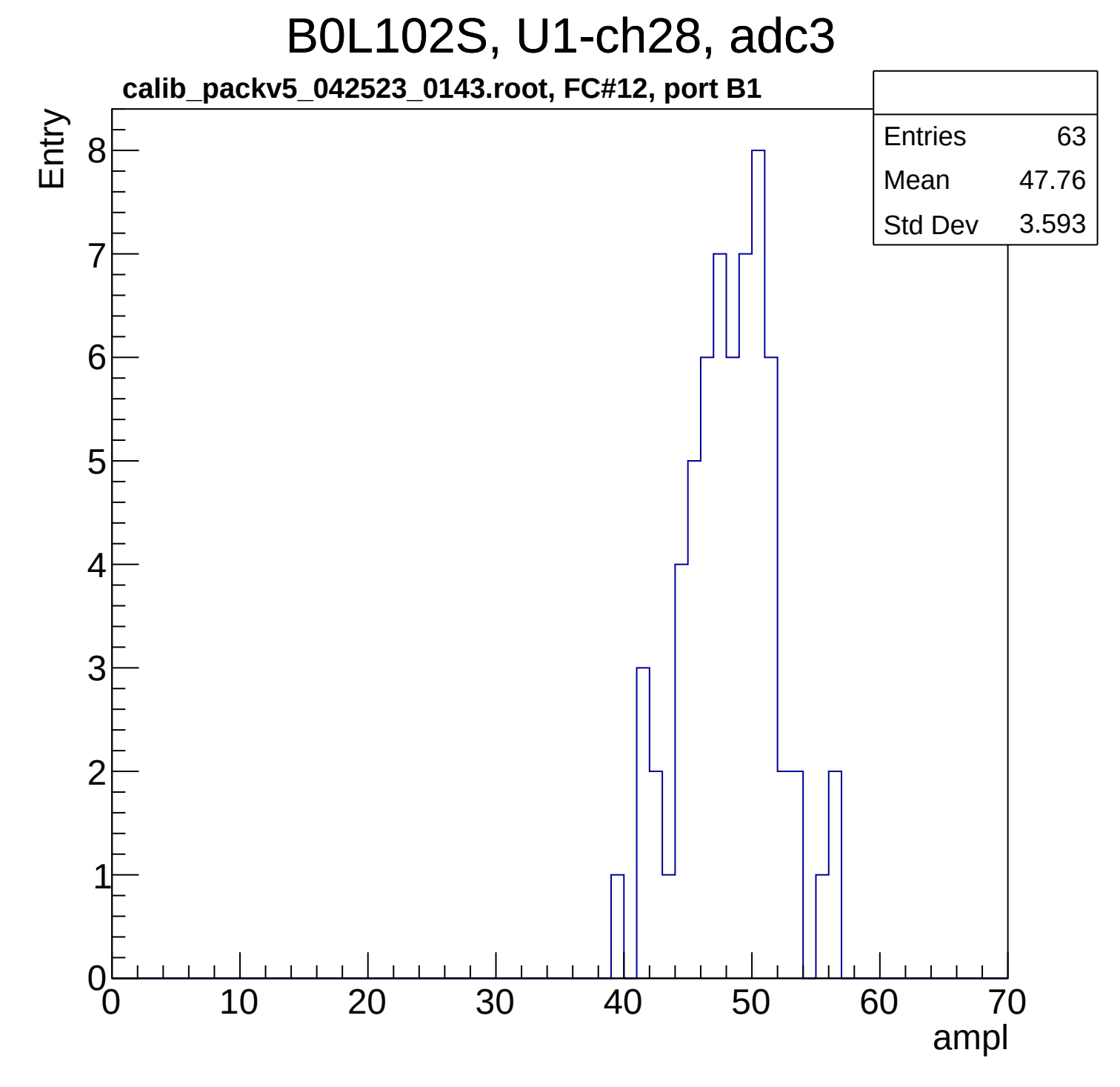

B0L102S, U1-ch27, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 61.73
Std Dev 1.289
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

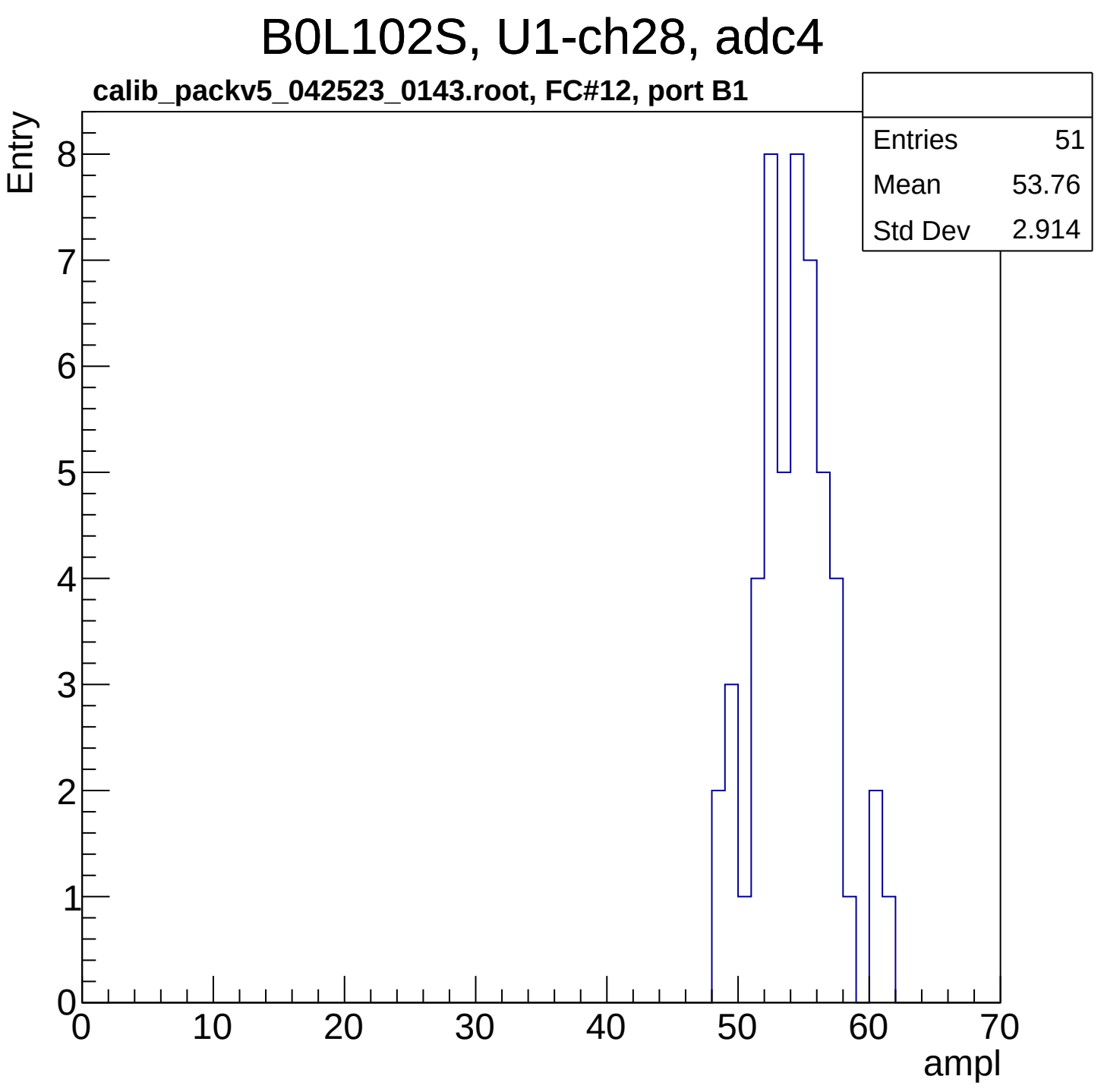

B0L102S, U1-ch27, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

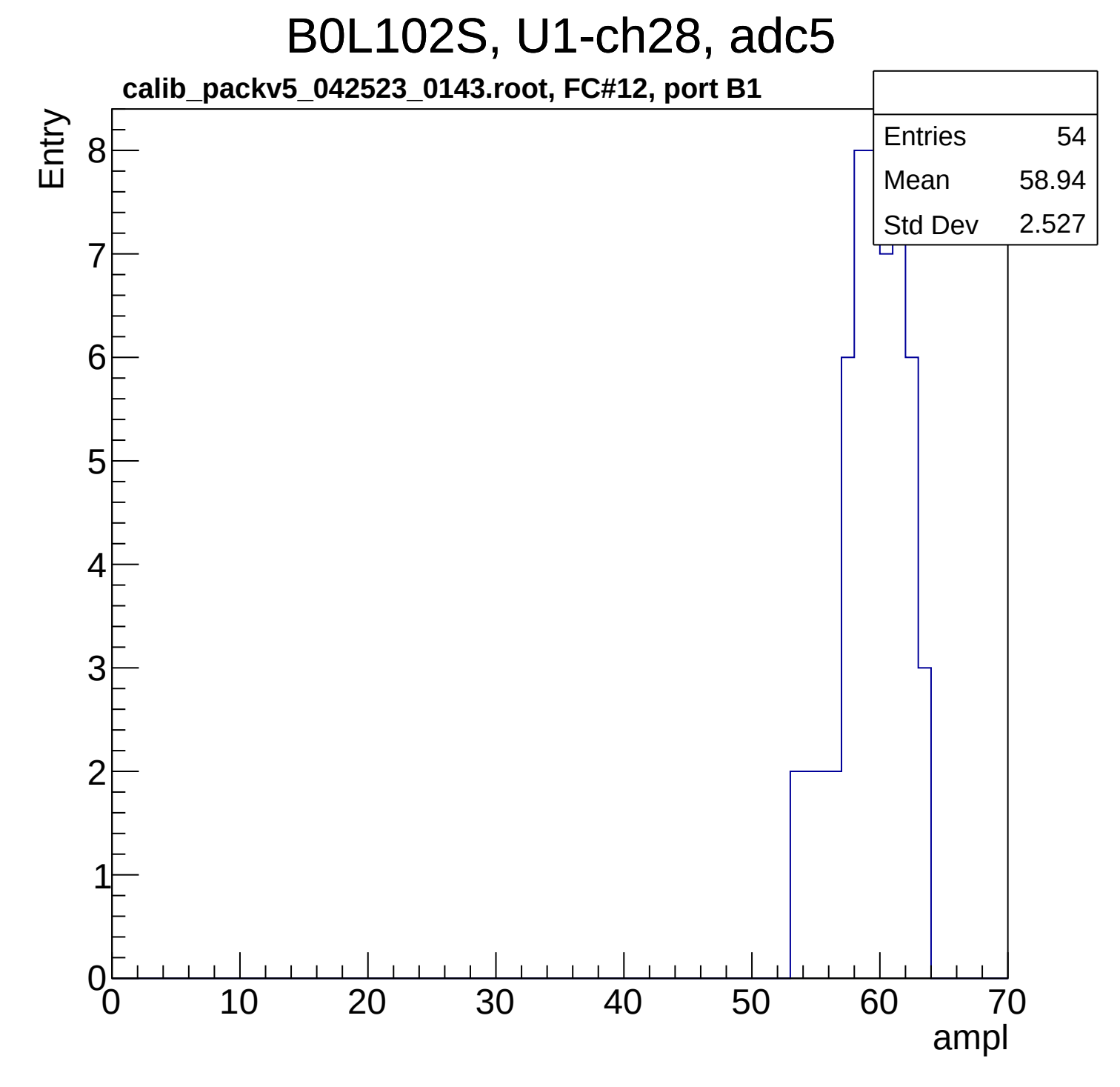

B0L102S, U1-ch28, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 27.91
Std Dev 5.071
Gaus mean : 29.0334
Gaus Width: 4.4880
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch28, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 33.75
Std Dev 3.829
Gaus mean : 34.4104
Gaus Width: 4.7269
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U1-ch28, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 41.64
Std Dev 3.672
Gaus mean : 42.1640
Gaus Width: 3.8600
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch28, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 47.76
Std Dev 3.593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch28, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 53.76
Std Dev 2.914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch28, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 58.94
Std Dev 2.527
Entry
ampl
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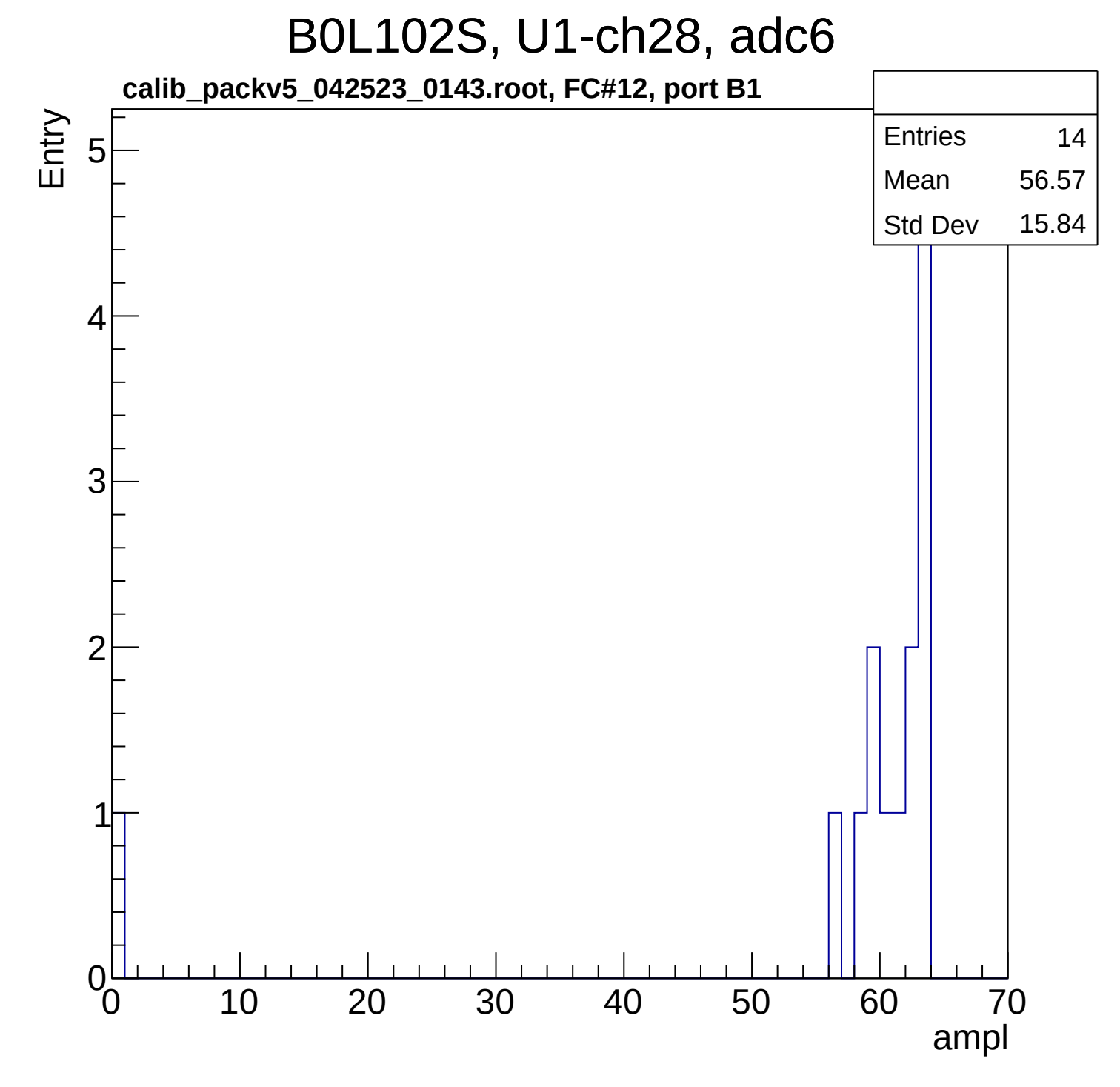
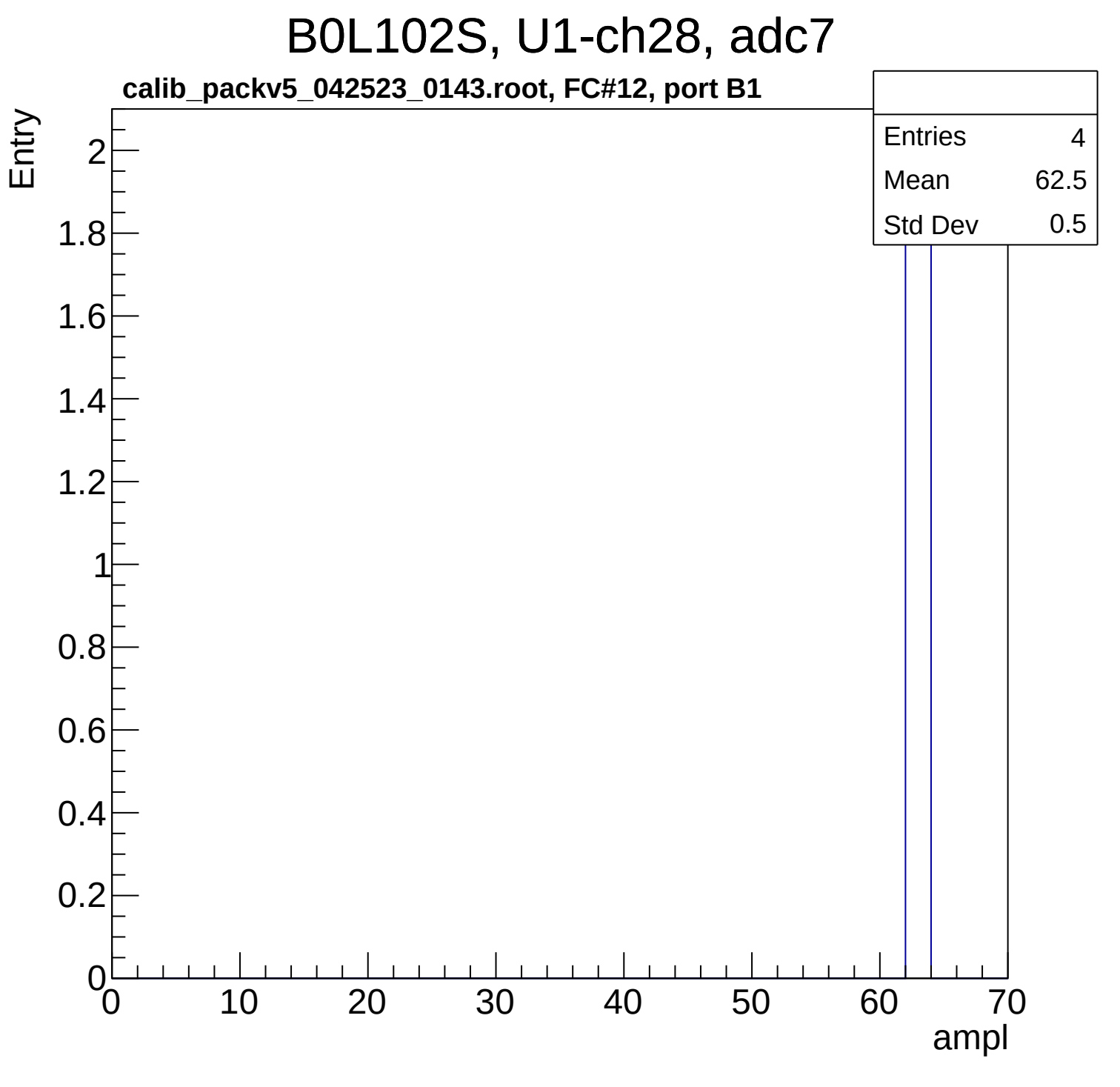
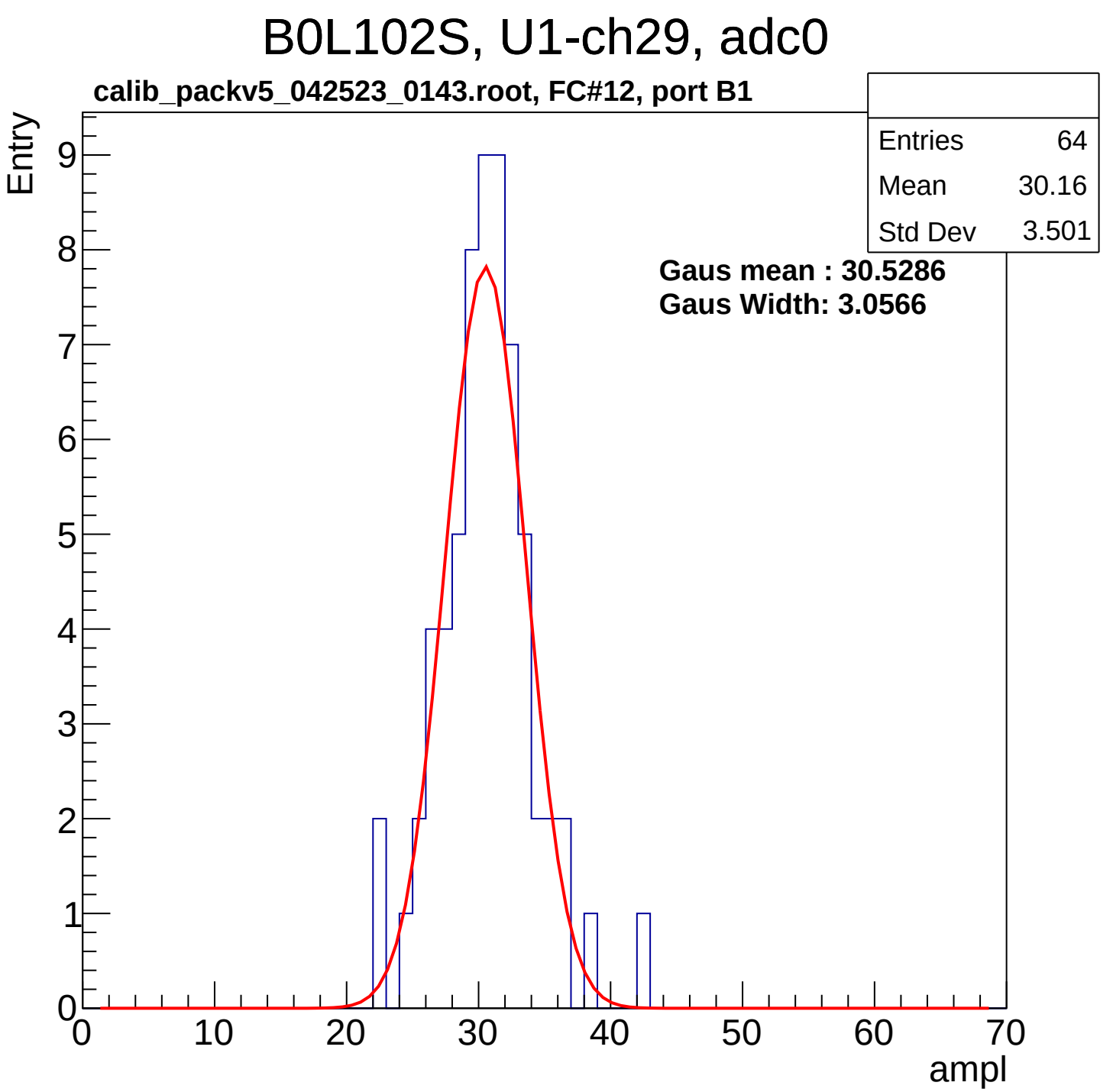
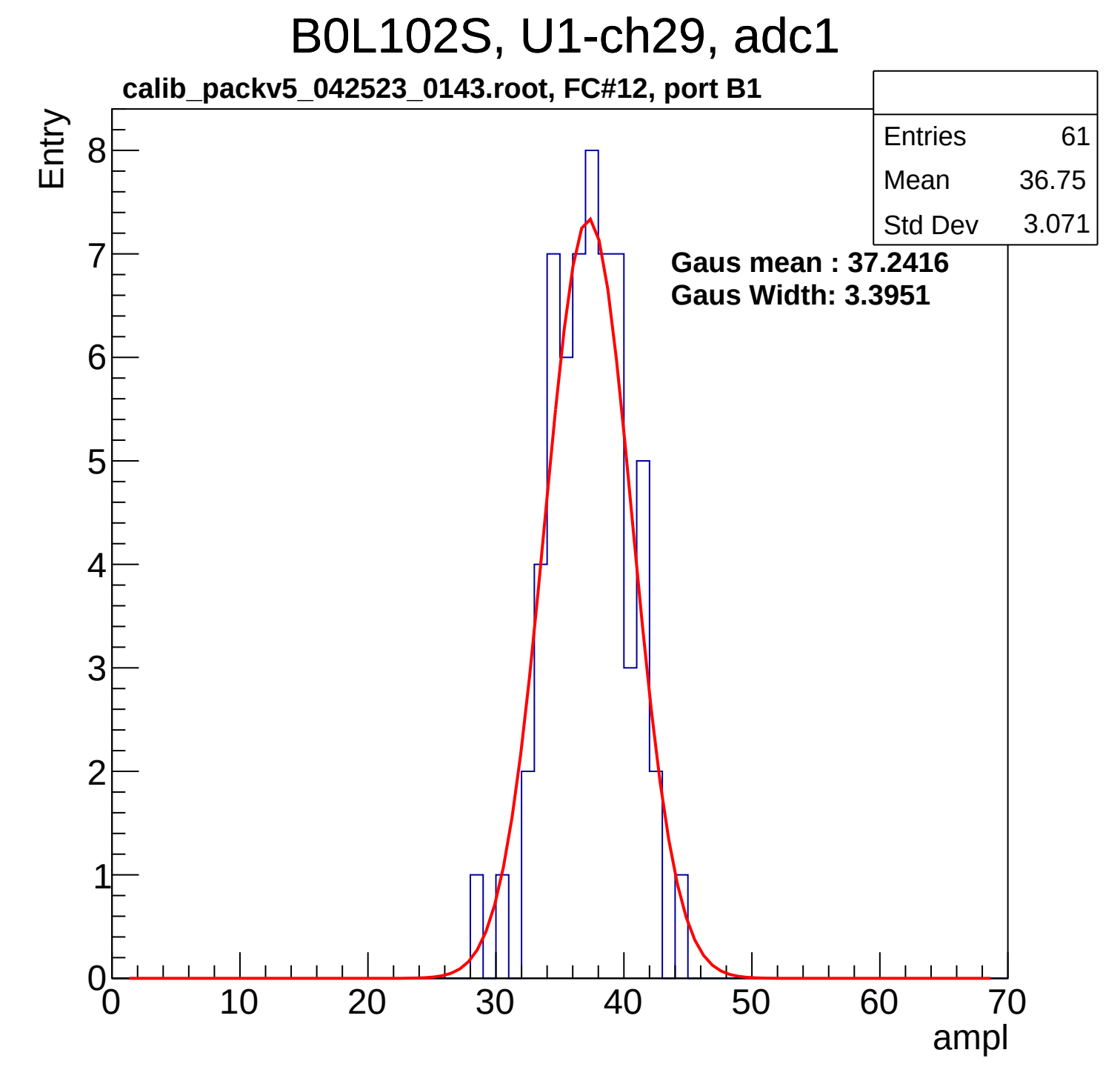
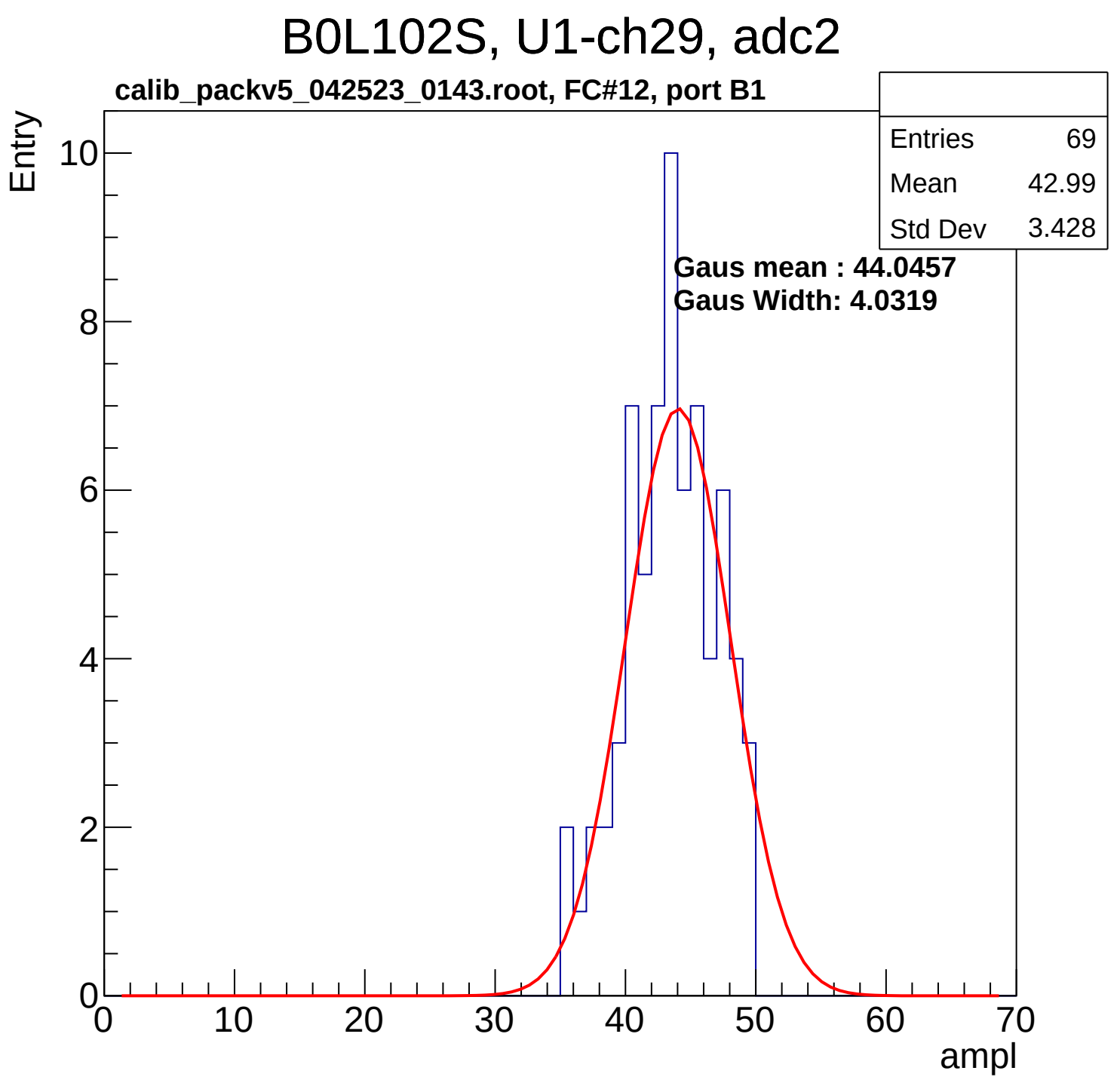
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

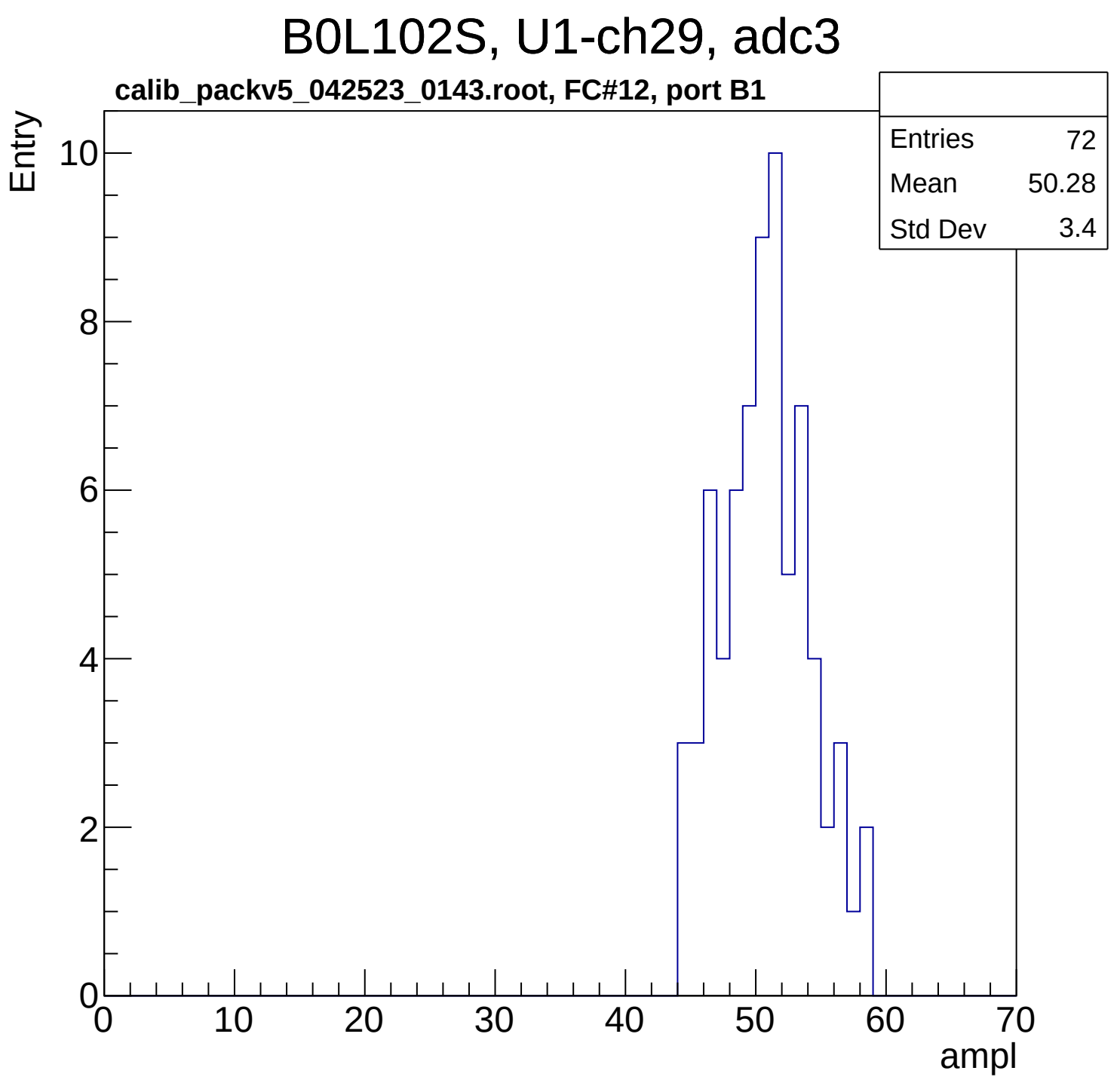

B0L102S, U1-ch28, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 56.57
Std Dev 15.84
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

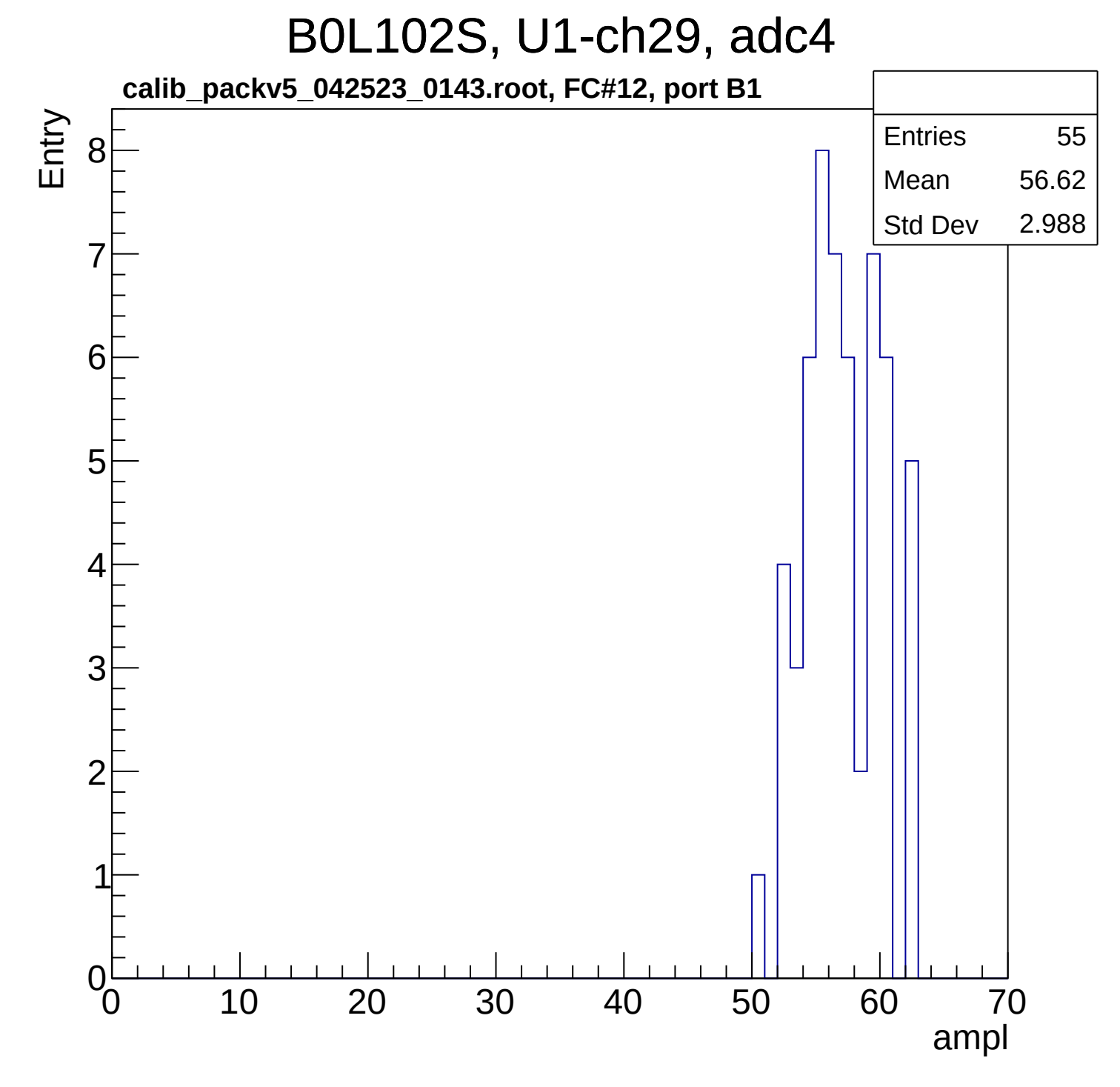

B0L102S, U1-ch28, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch29, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 30.16
Std Dev 3.501
Gaus mean : 30.5286
Gaus Width: 3.0566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch29, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 36.75
Std Dev 3.071
Gaus mean : 37.2416
Gaus Width: 3.3951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch29, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 42.99
Std Dev 3.428
Gaus mean : 44.0457
Gaus Width: 4.0319
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch29, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 50.28
Std Dev 3.4
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch29, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.62
Std Dev 2.988
Entry
ampl
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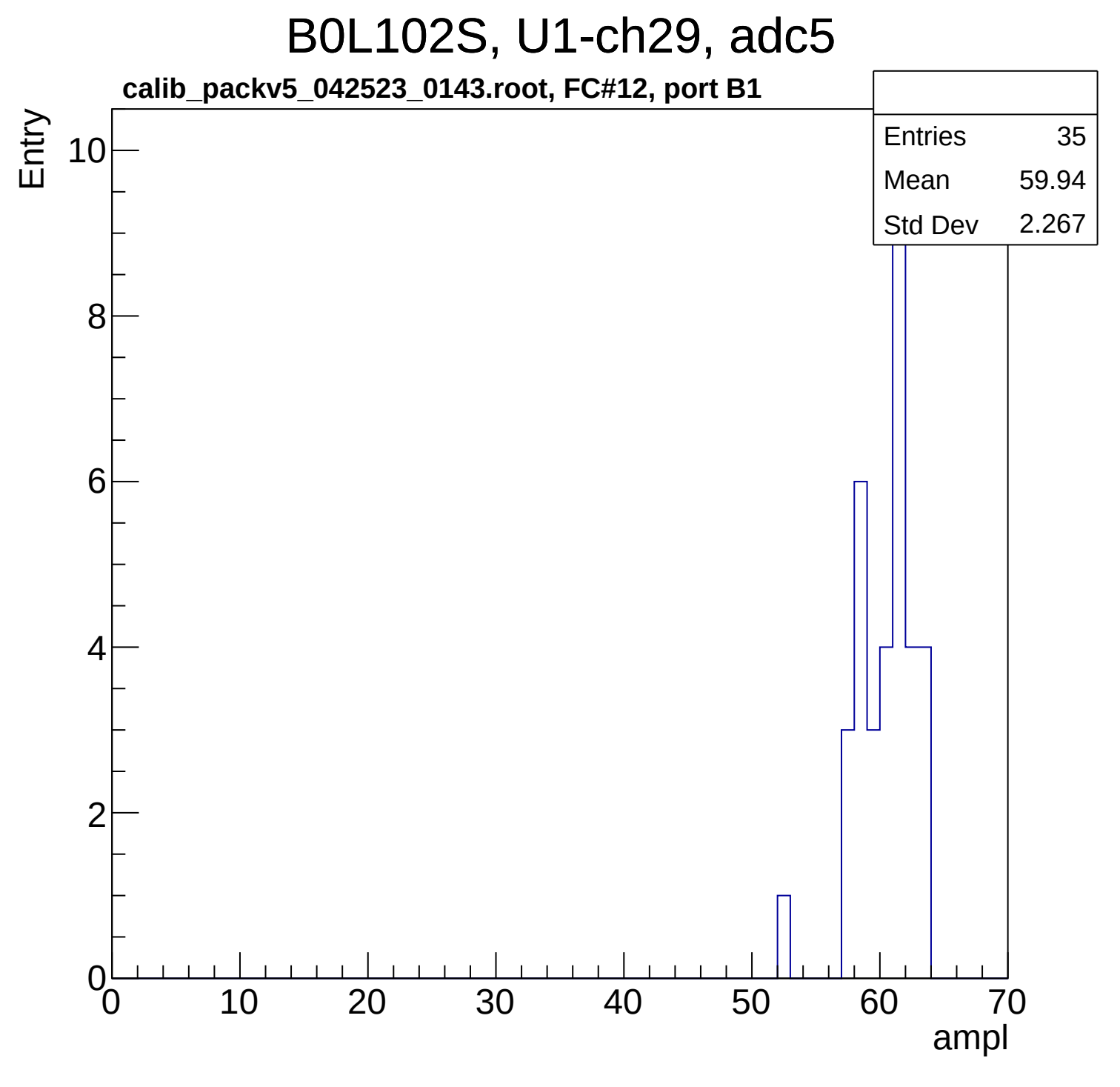
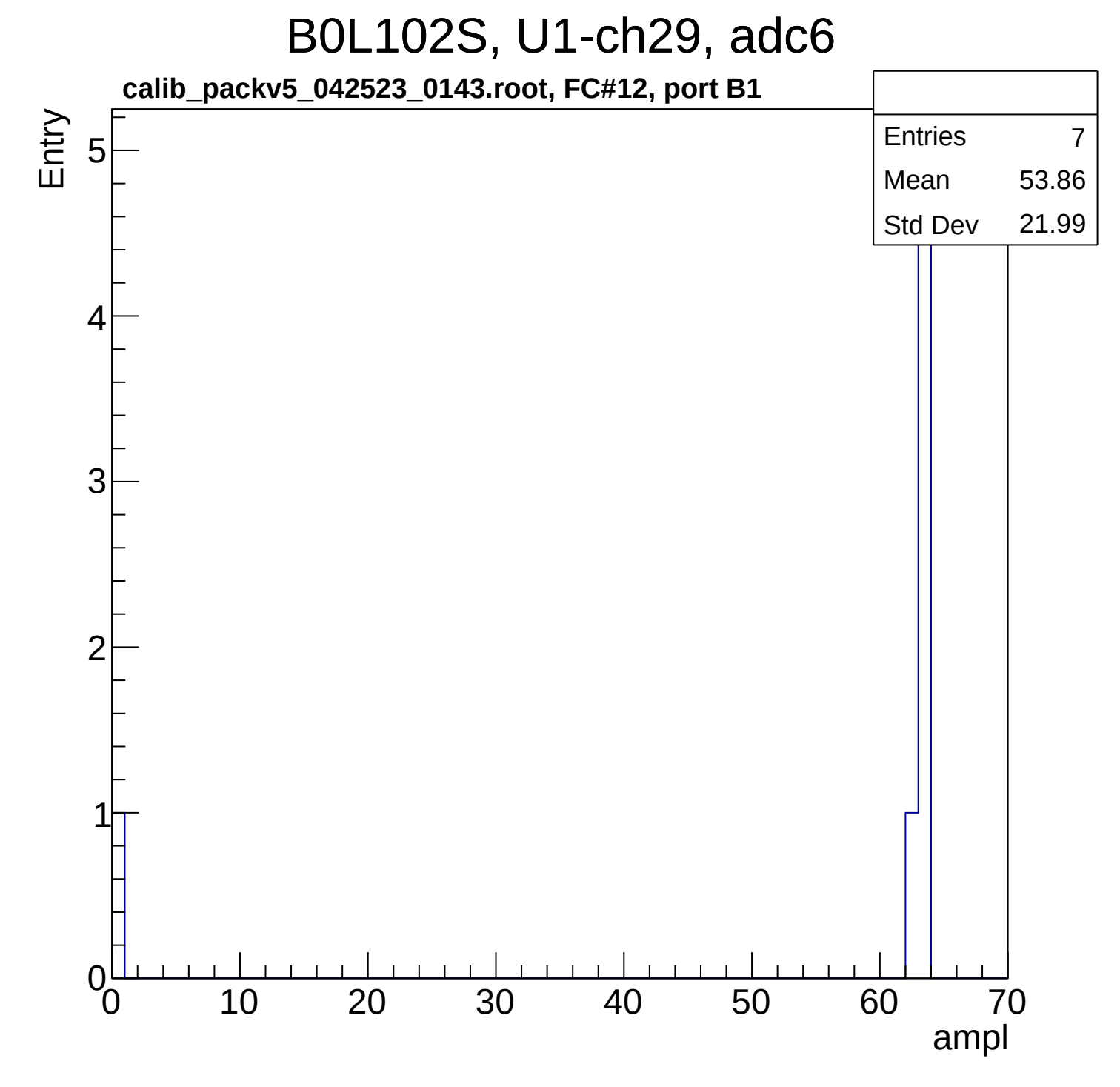
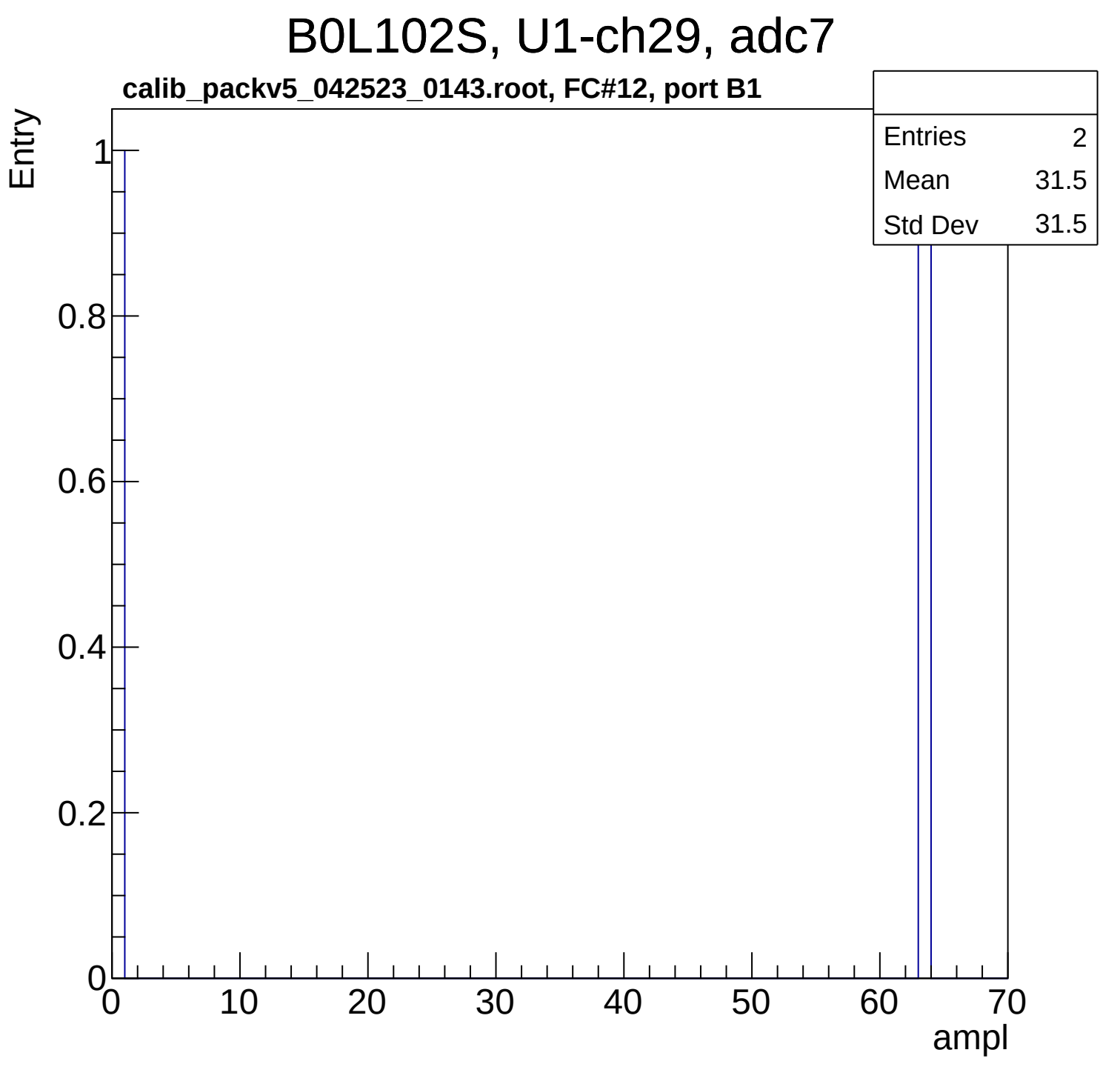
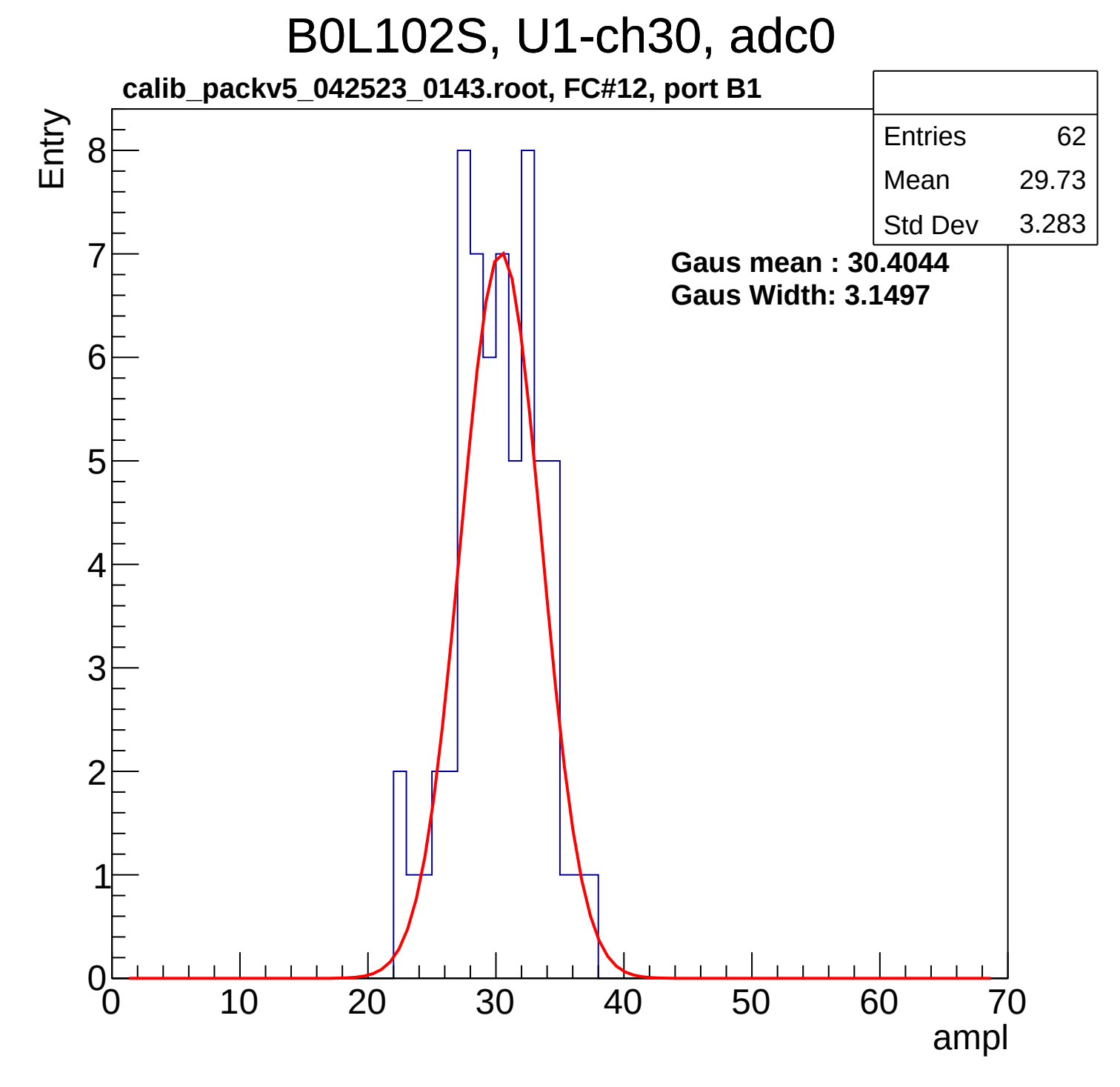
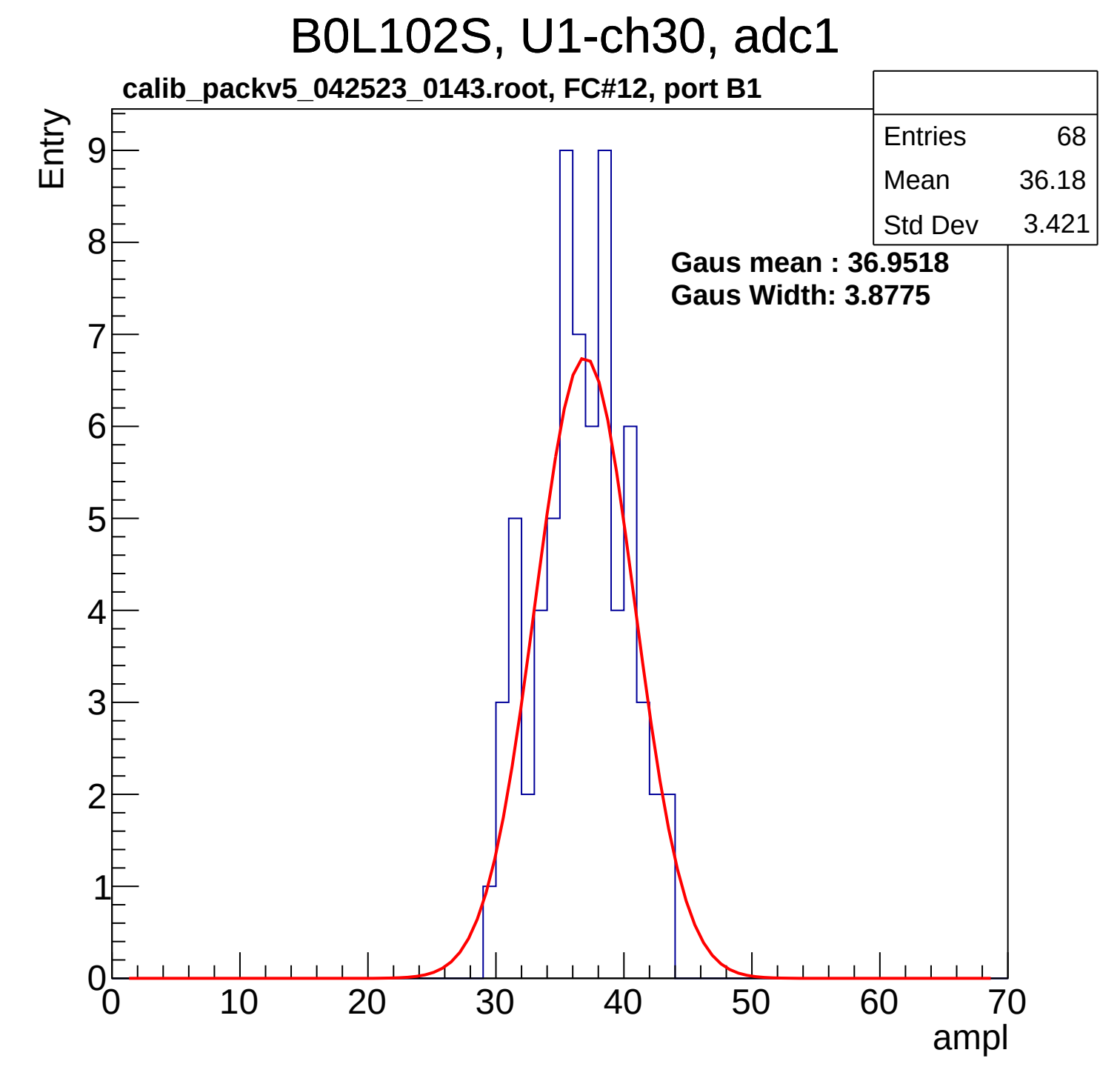
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

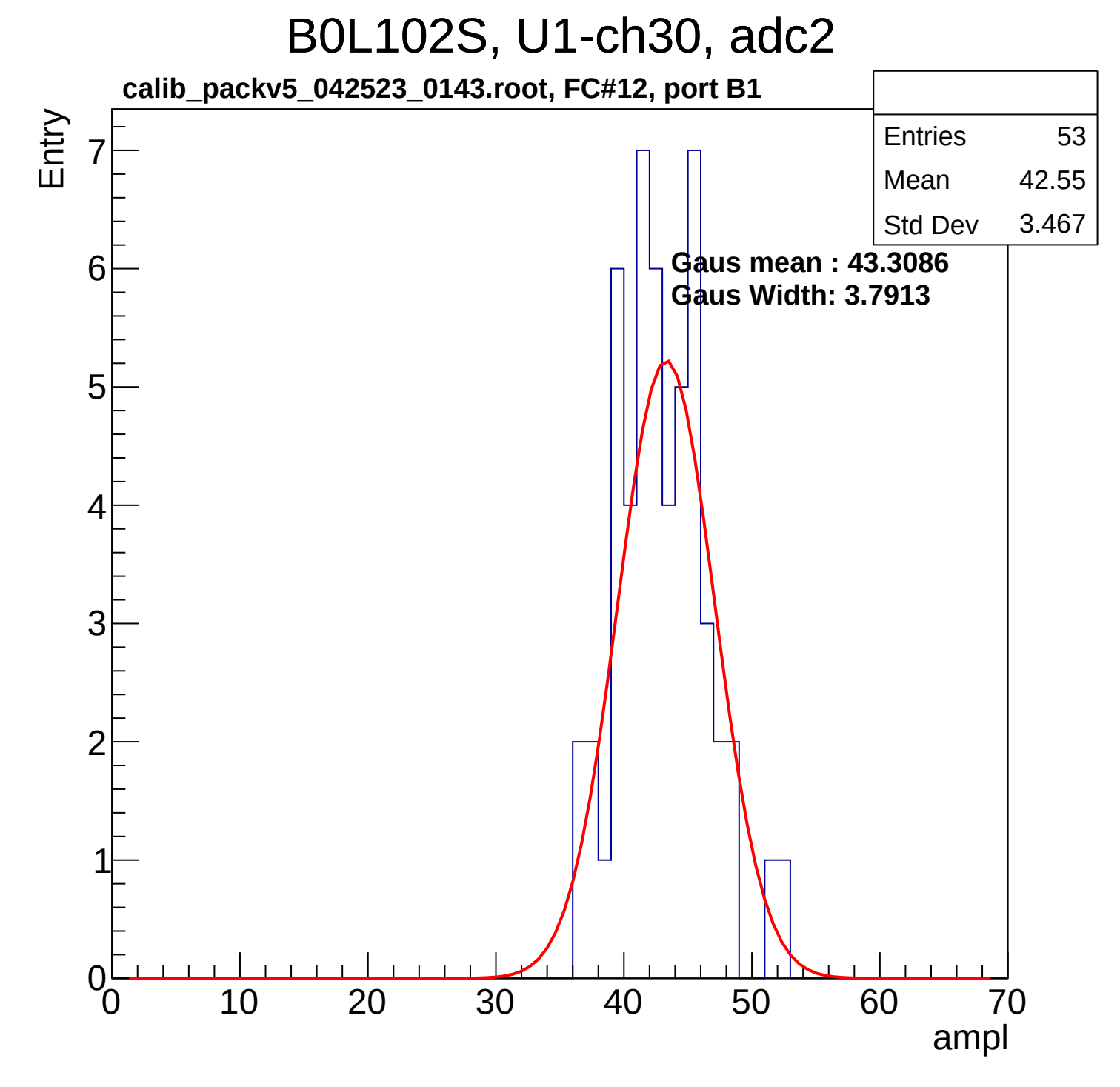

B0L102S, U1-ch29, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 59.94
Std Dev 2.267
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch29, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.86
Std Dev 21.99
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch29, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch30, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 29.73
Std Dev 3.283
Gaus mean : 30.4044
Gaus Width: 3.1497
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch30, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 36.18
Std Dev 3.421
Gaus mean : 36.9518
Gaus Width: 3.8775
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch30, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 42.55
Std Dev 3.467
Gaus mean : 43.3086
Gaus Width: 3.7913
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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30
40
50
60
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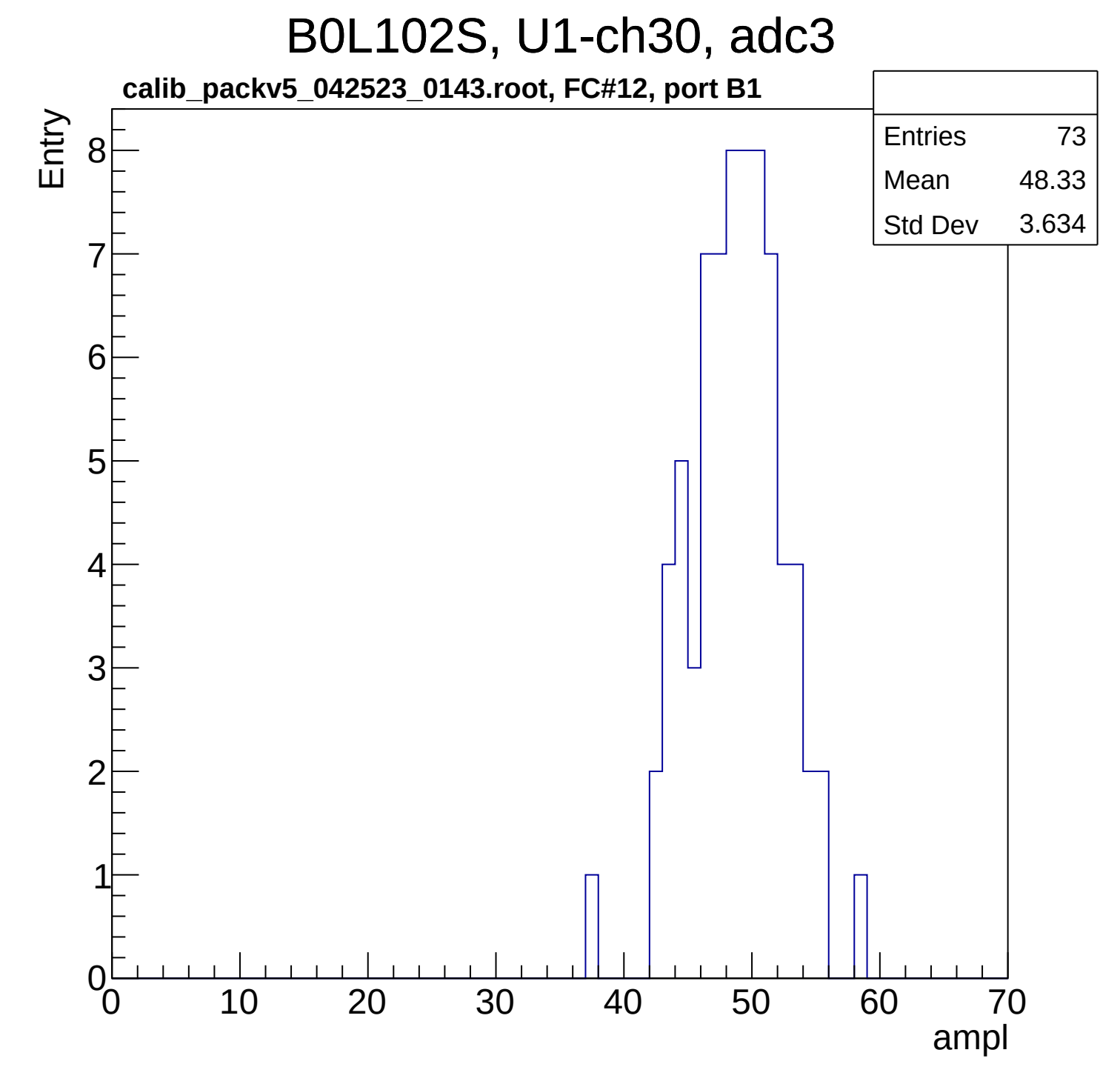
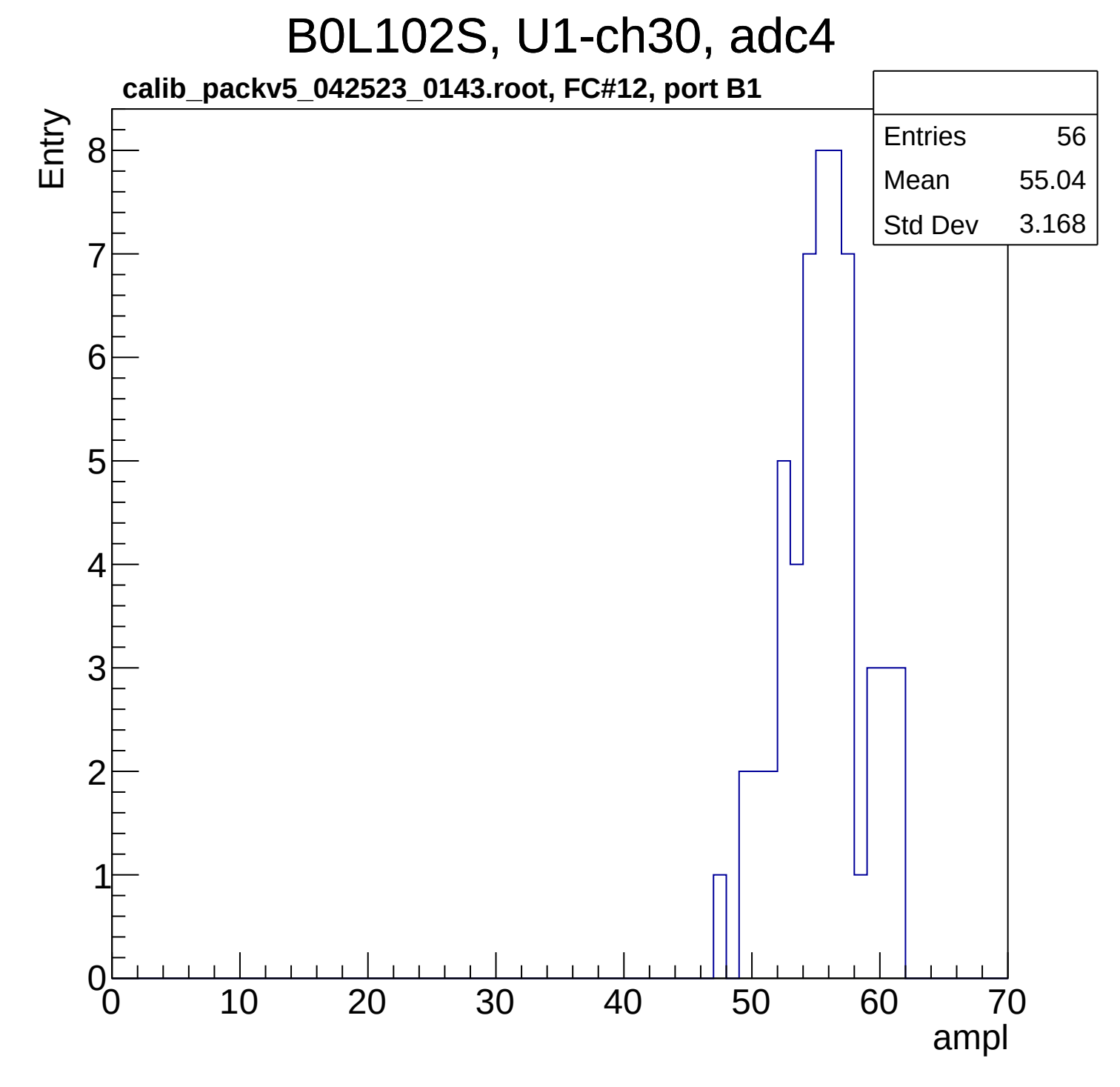

B0L102S, U1-ch30, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 48.33
Std Dev 3.634
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch30, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.04
Std Dev 3.168
Entry
ampl
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1
2
3
4
5
6
7
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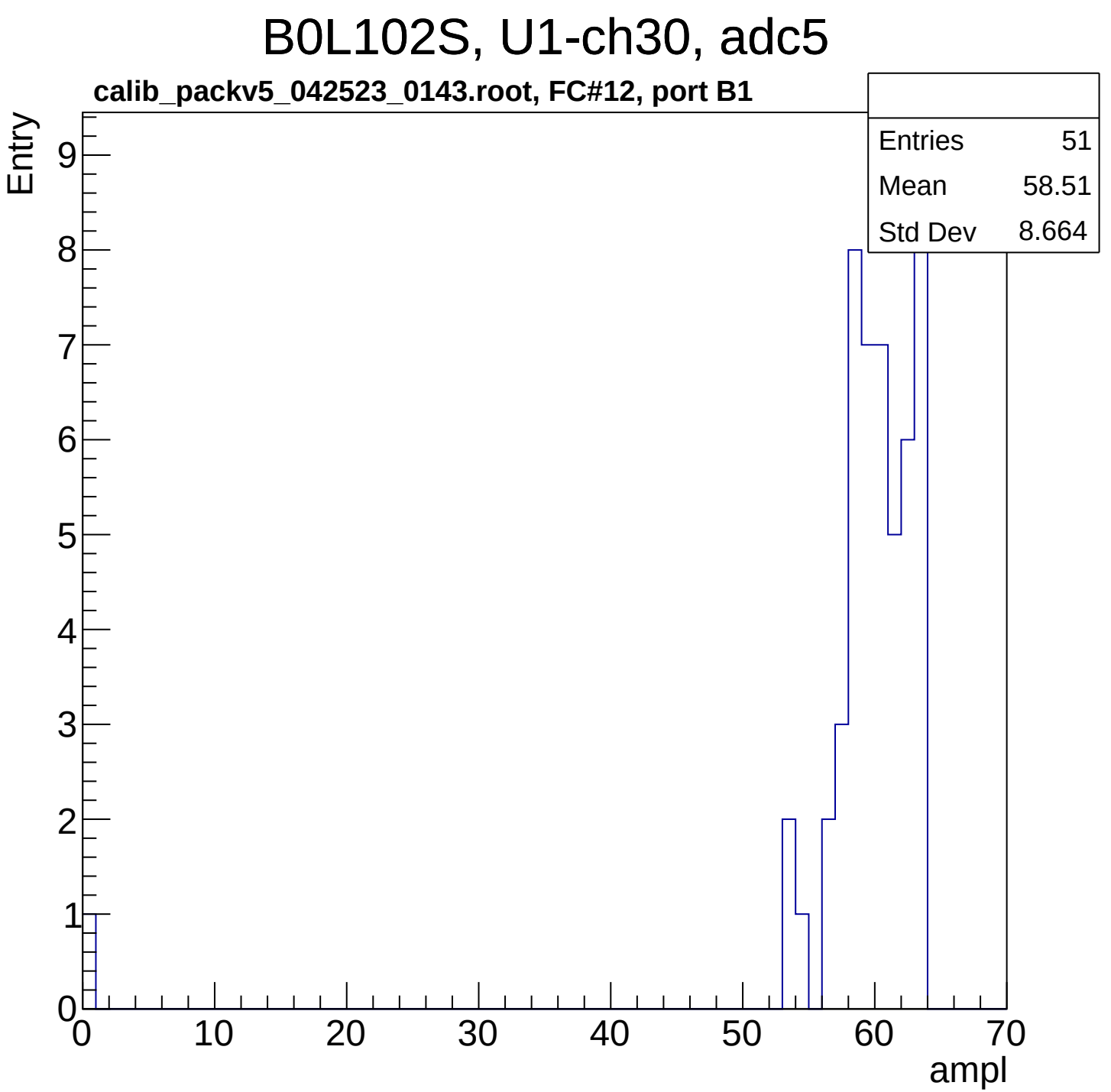
0
10
20
30
40
50
60
70


B0L102S, U1-ch30, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 58.51
Std Dev 8.664
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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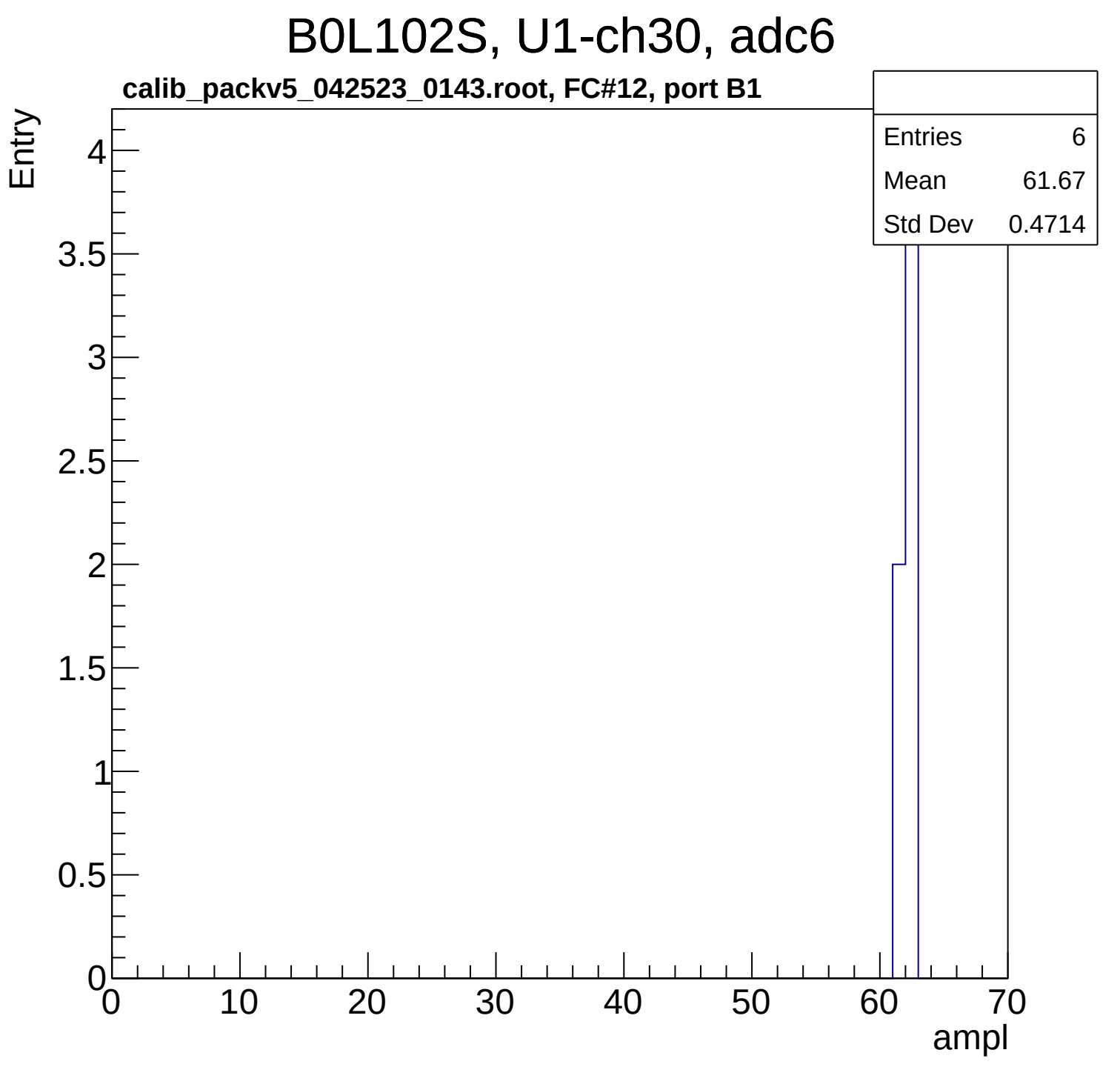
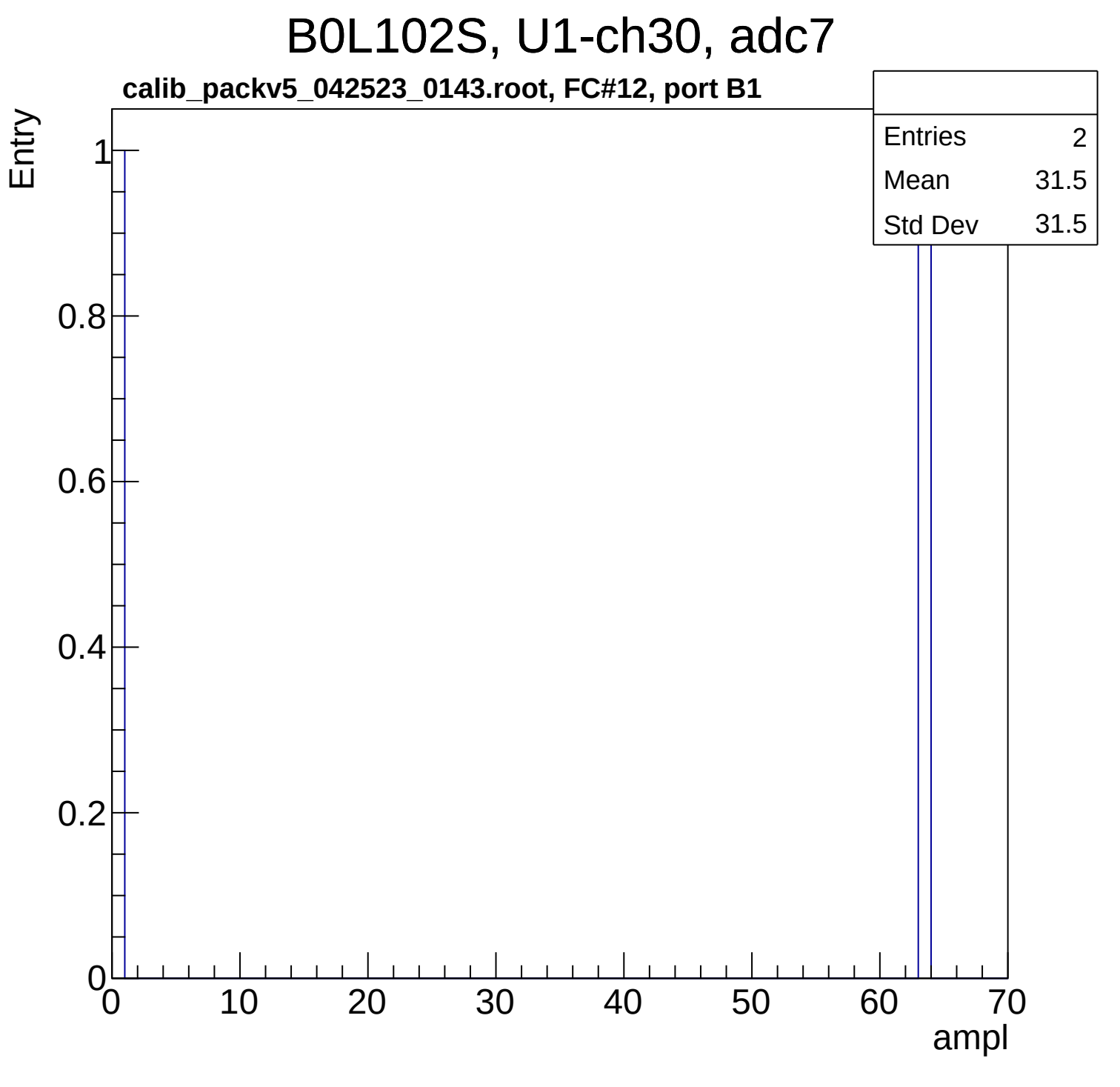
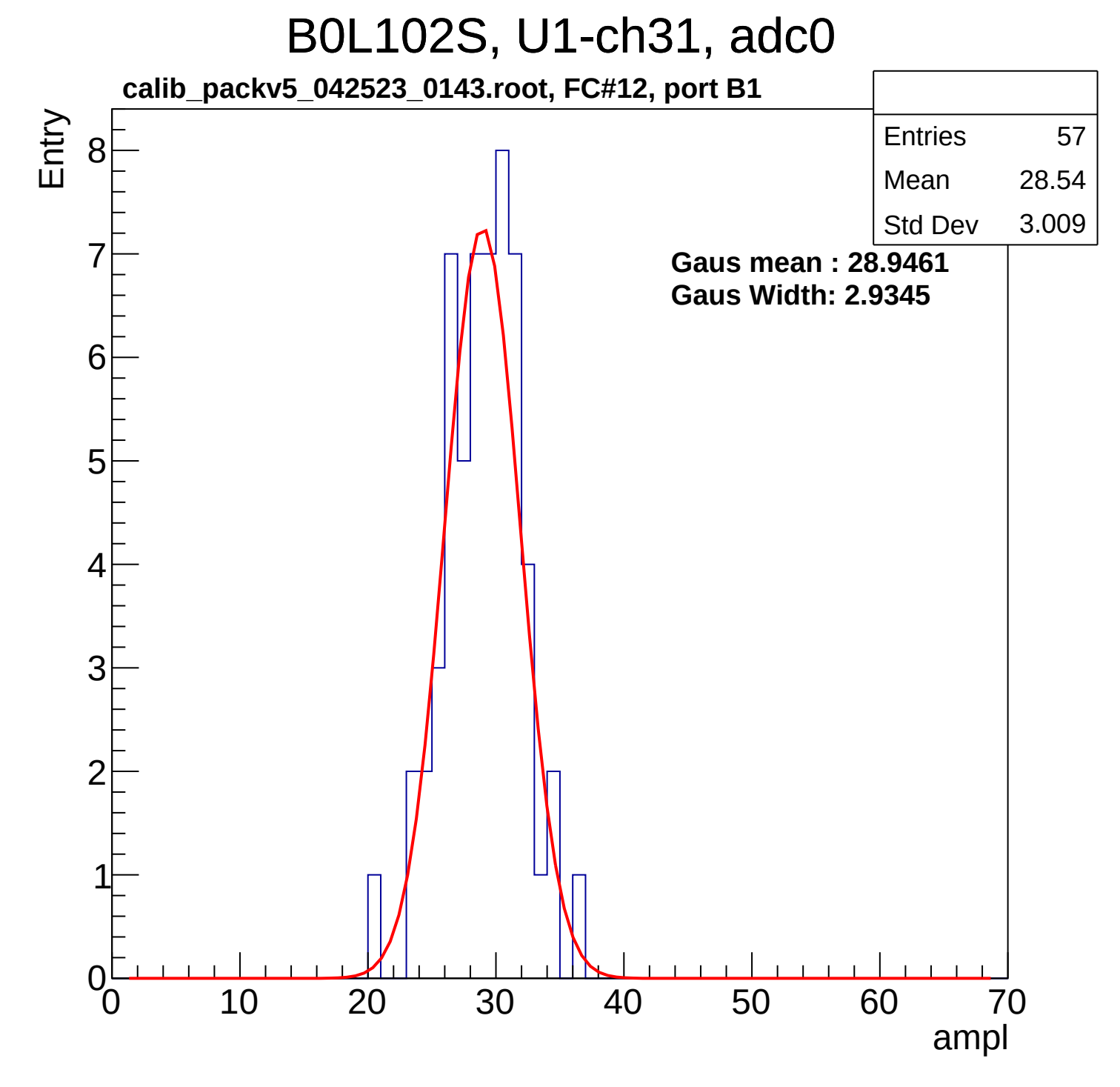
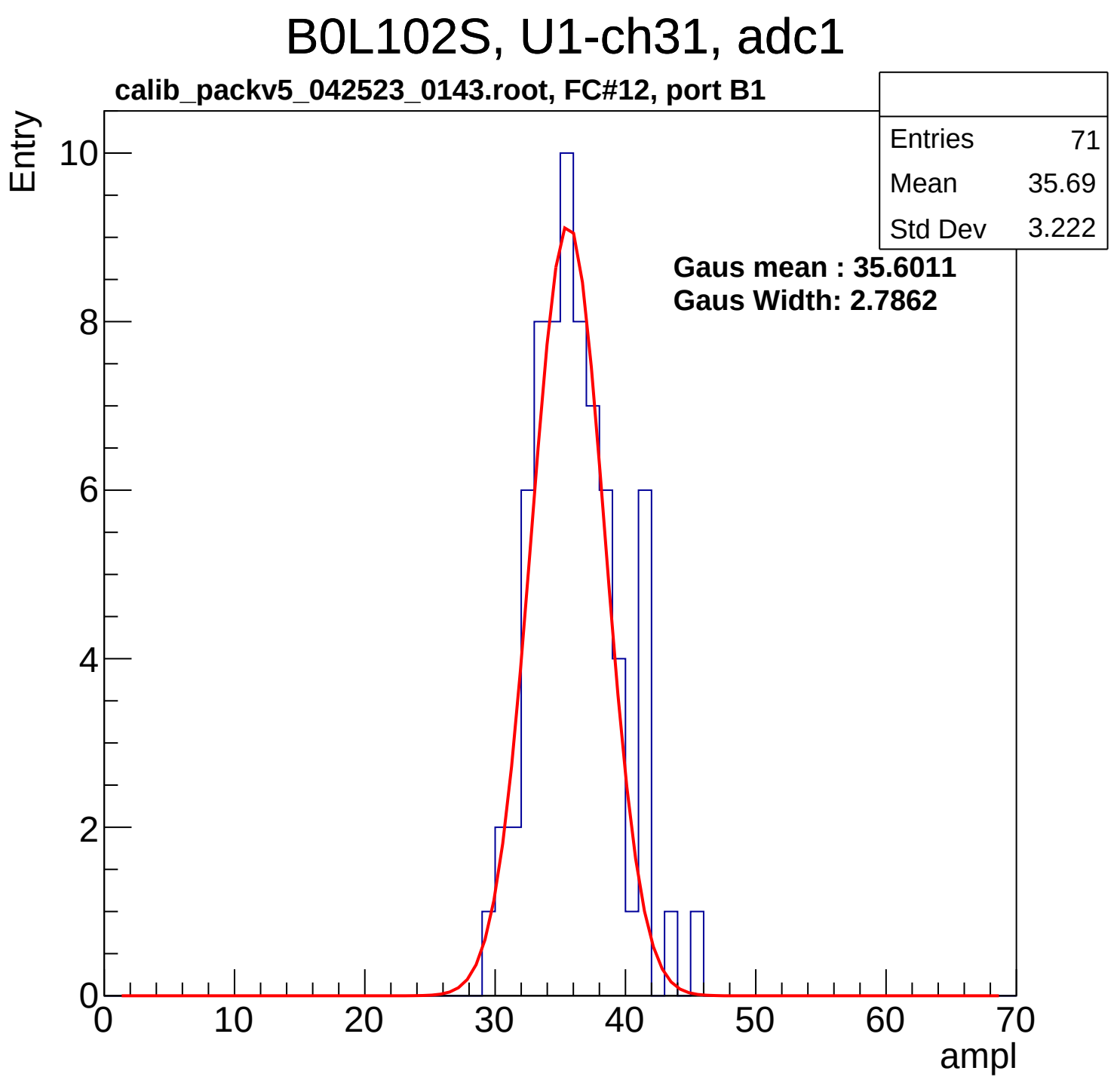
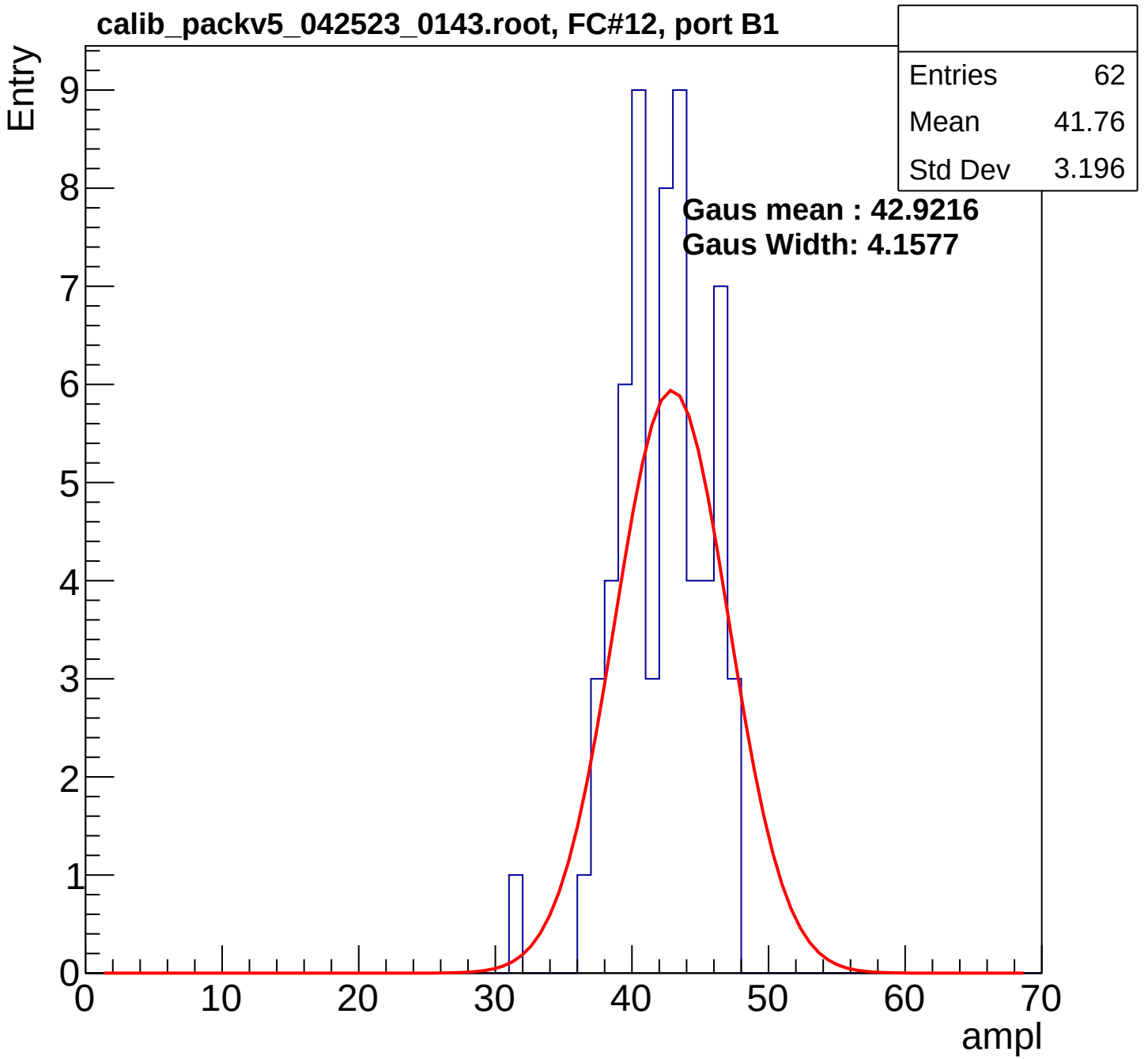
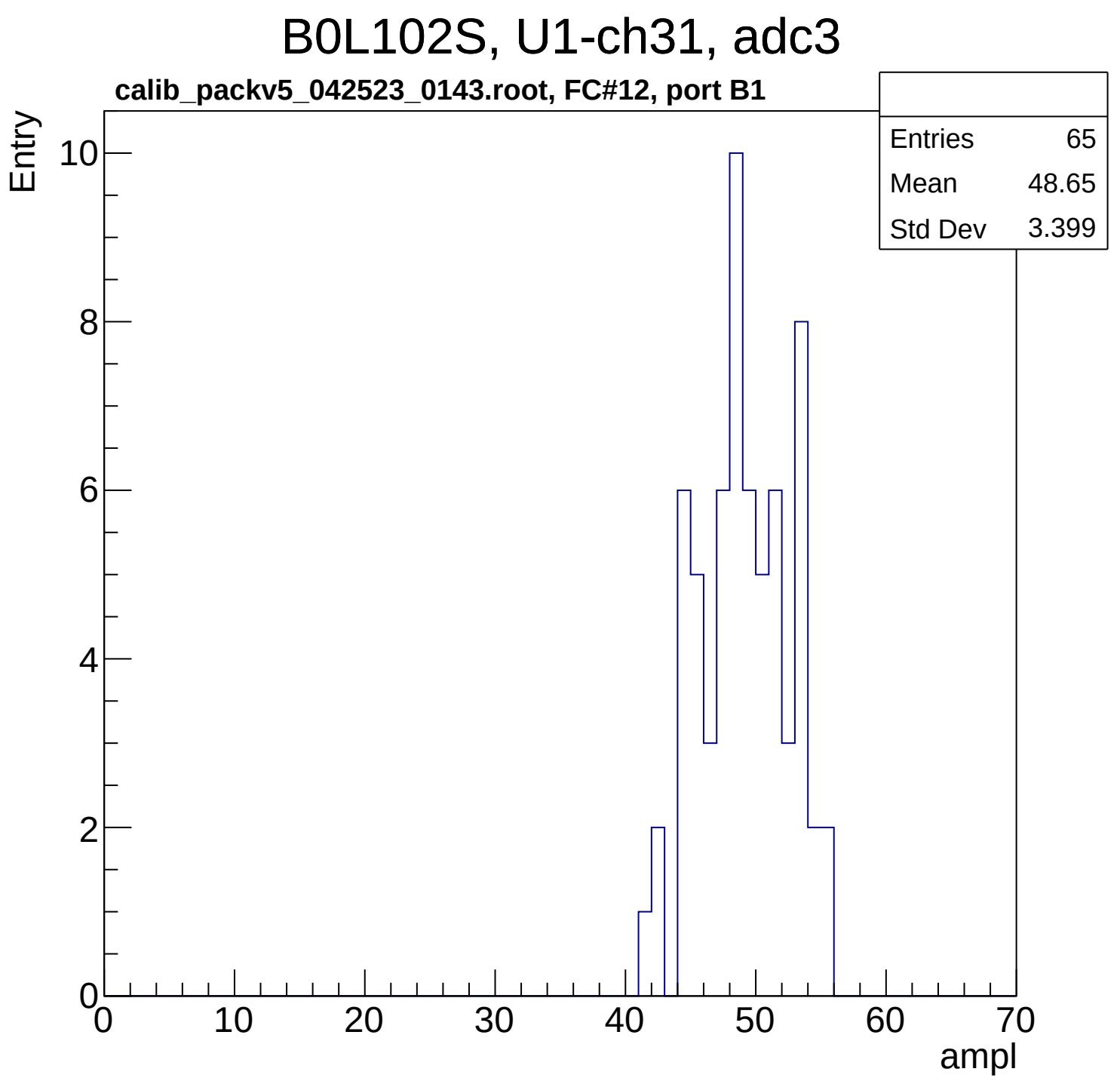

B0L102S, U1-ch30, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

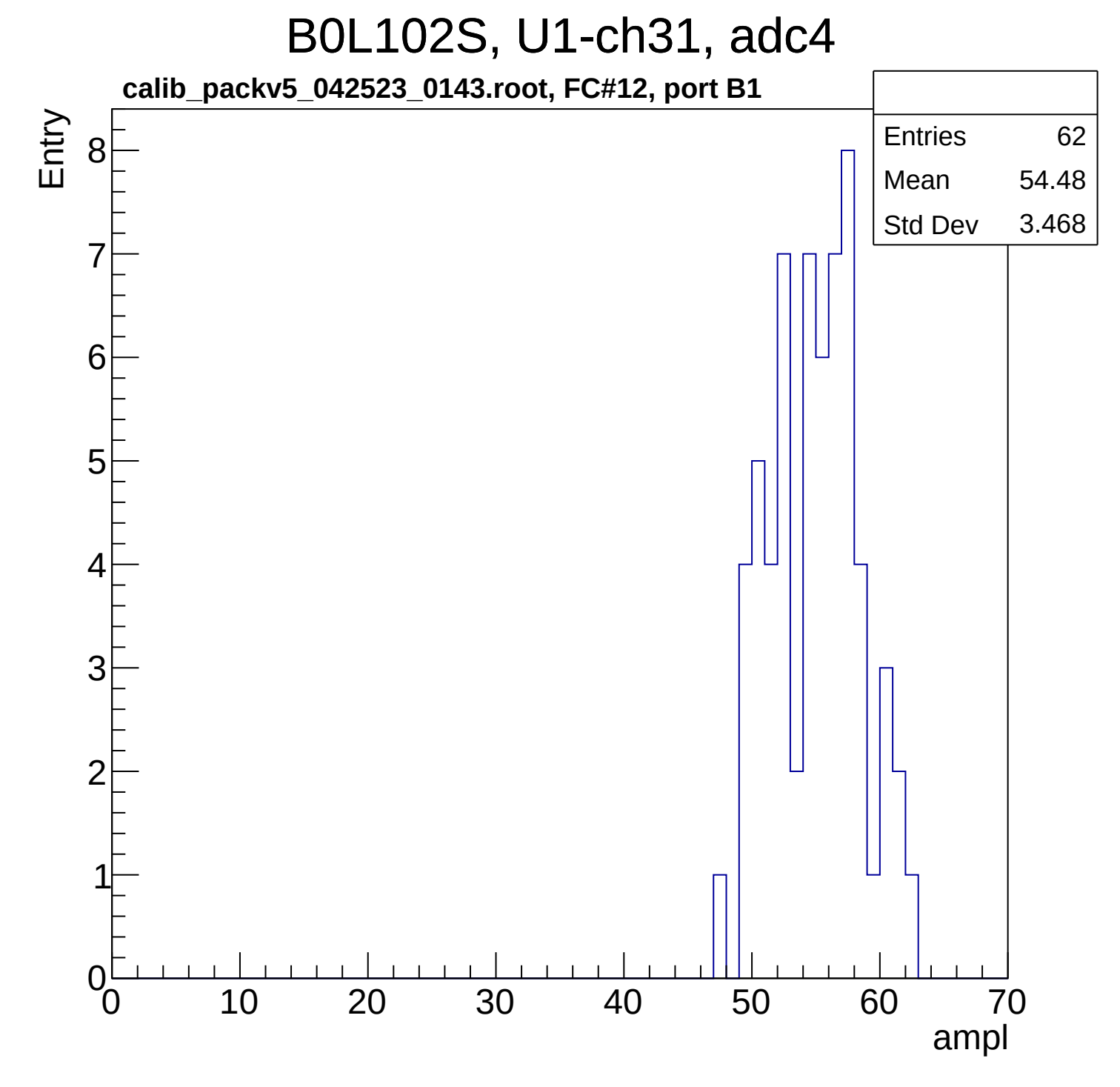

B0L102S, U1-ch30, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

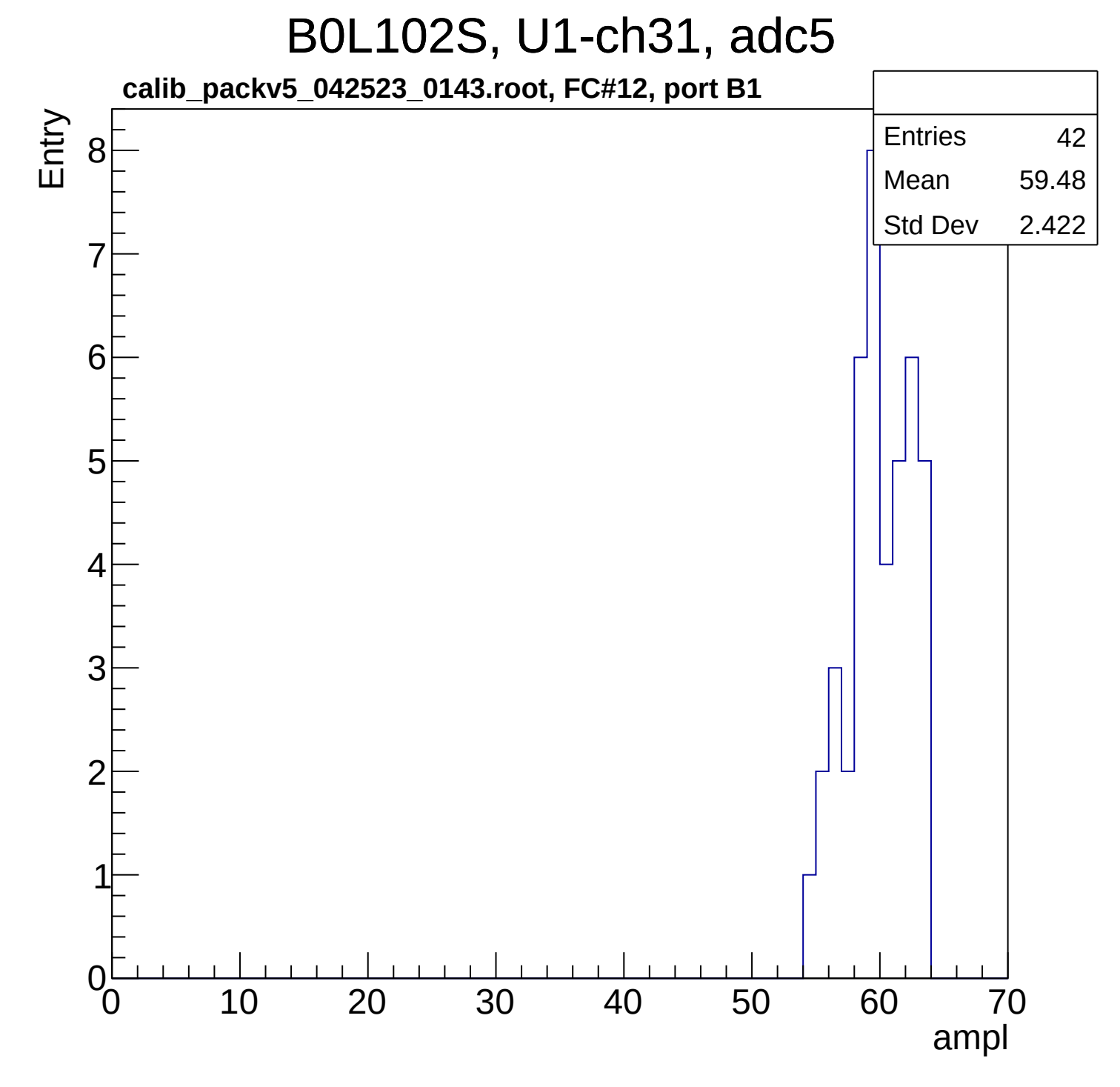

B0L102S, U1-ch31, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 28.54
Std Dev 3.009
Gaus mean : 28.9461
Gaus Width: 2.9345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch31, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 35.69
Std Dev 3.222
Gaus mean : 35.6011
Gaus Width: 2.7862
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch31, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 41.76
Std Dev 3.196
Gaus mean : 42.9216
Gaus Width: 4.1577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch31, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.65
Std Dev 3.399
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch31, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 54.48
Std Dev 3.468
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch31, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 59.48
Std Dev 2.422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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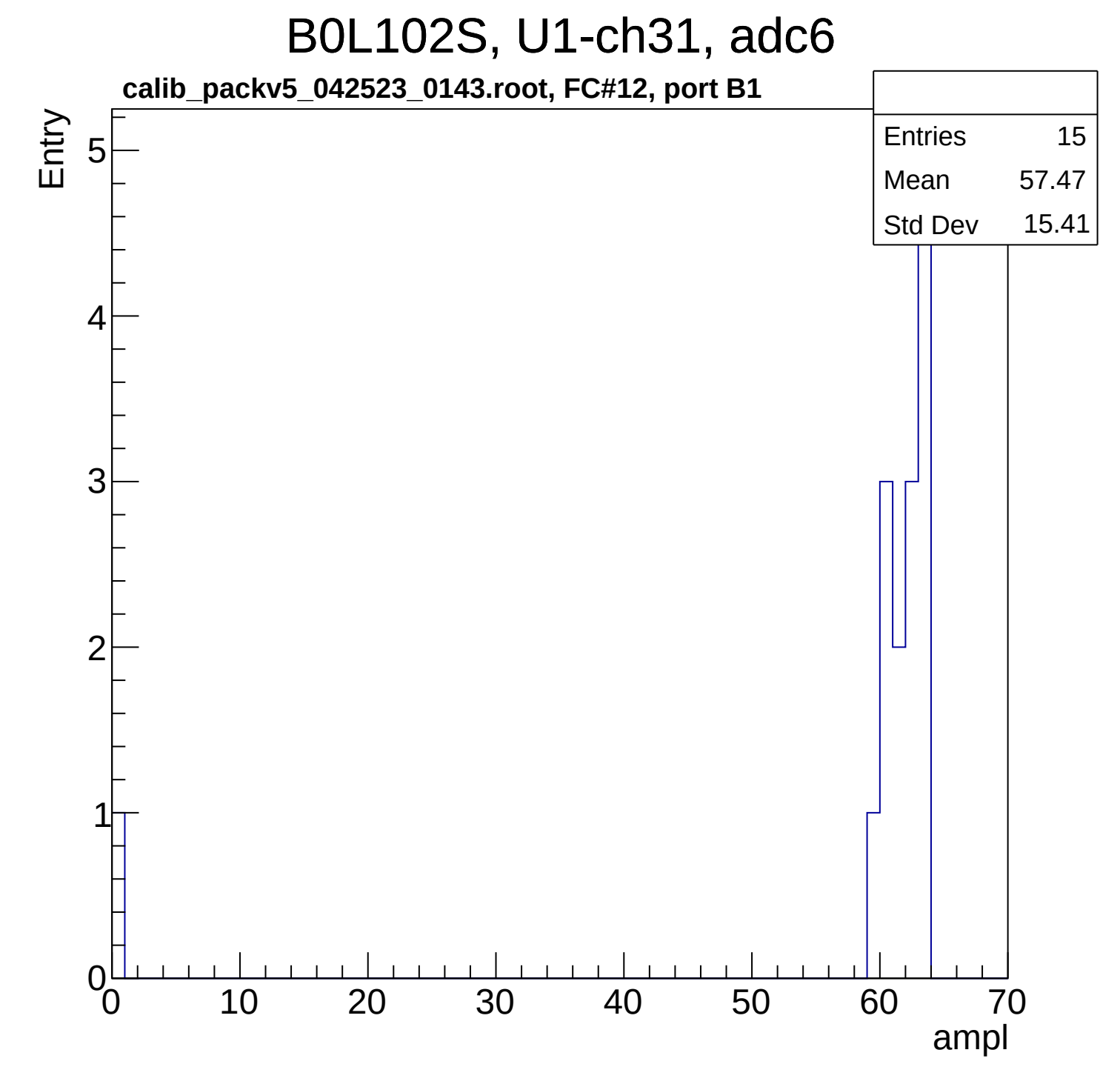
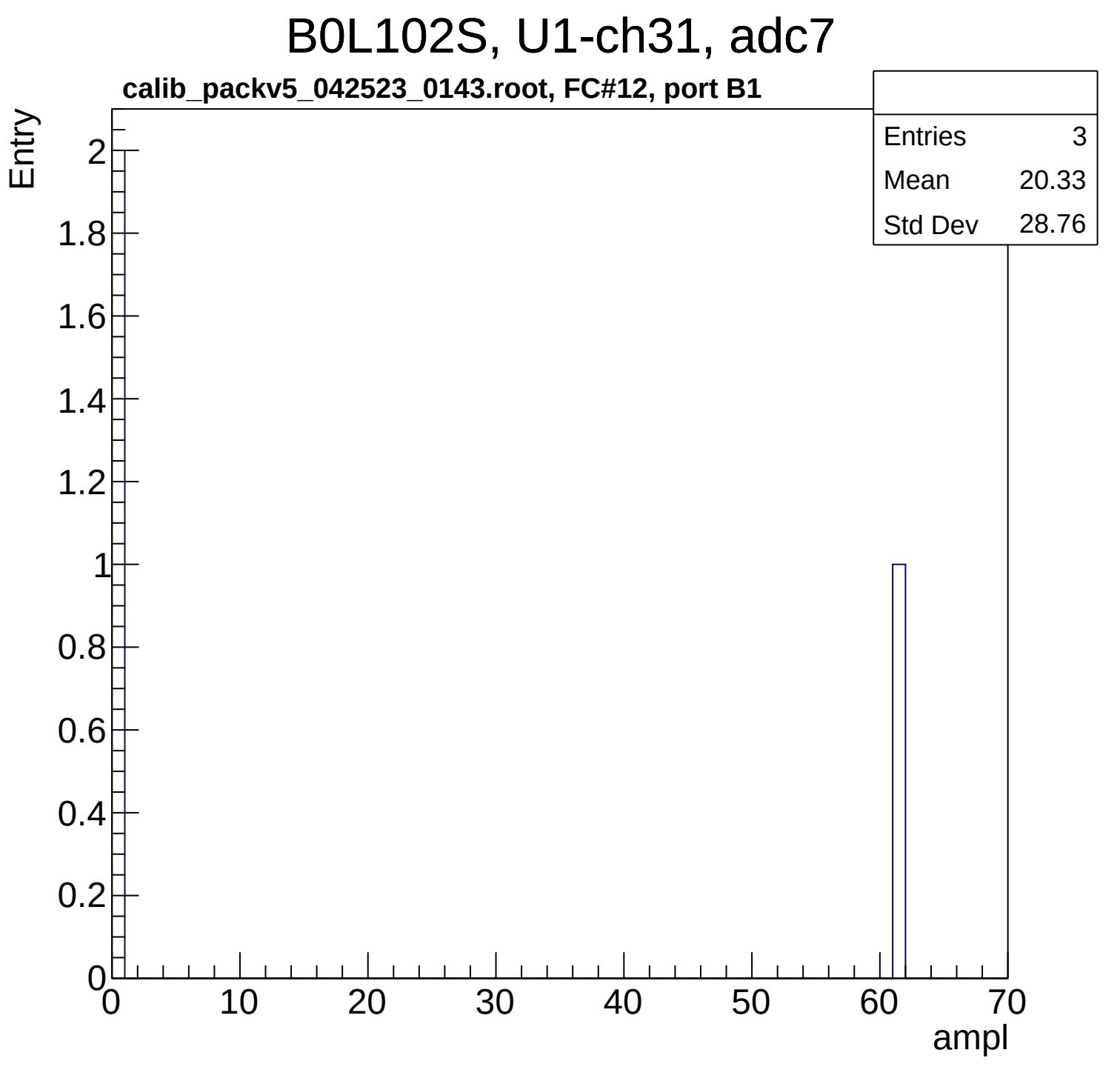
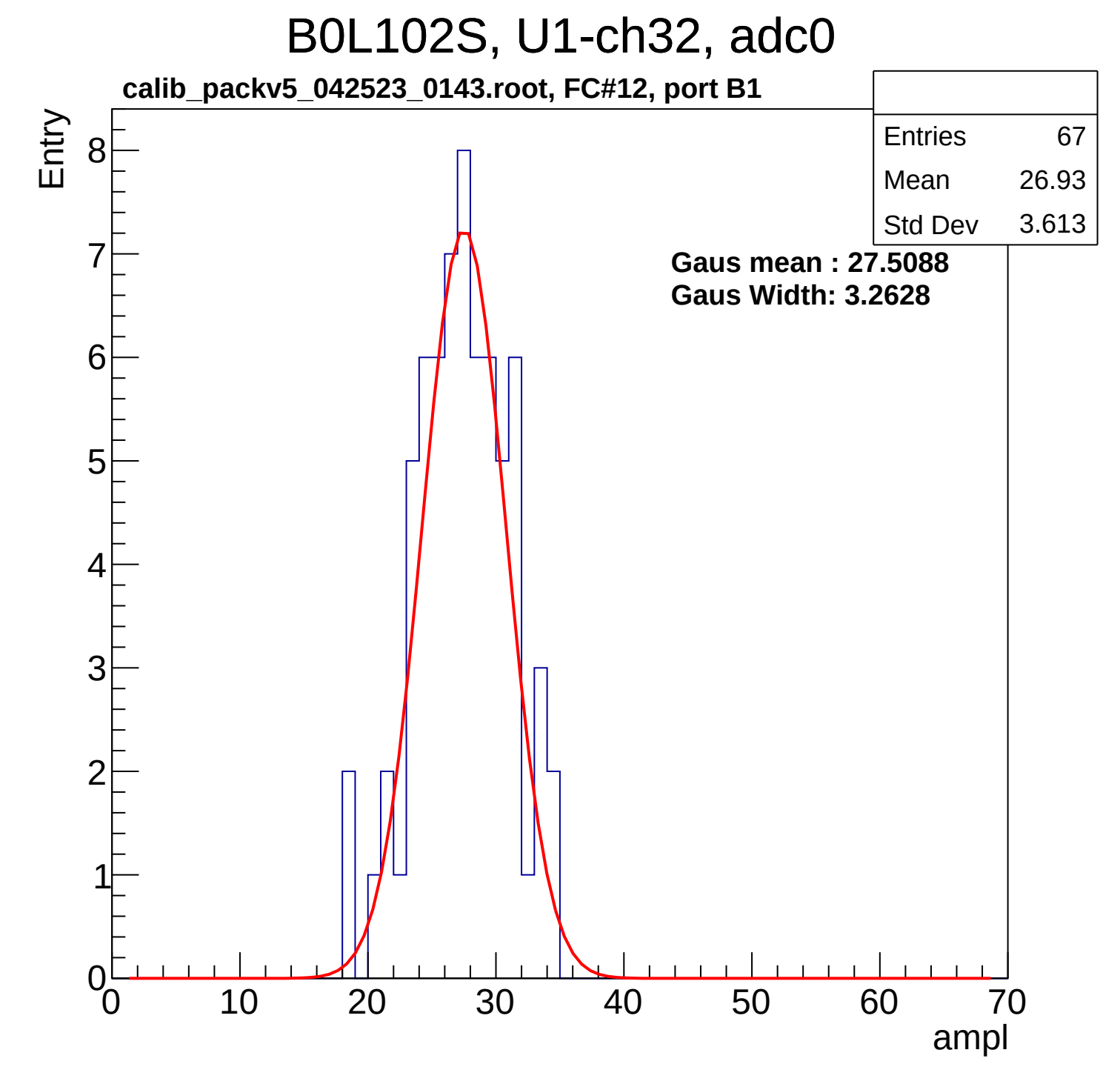
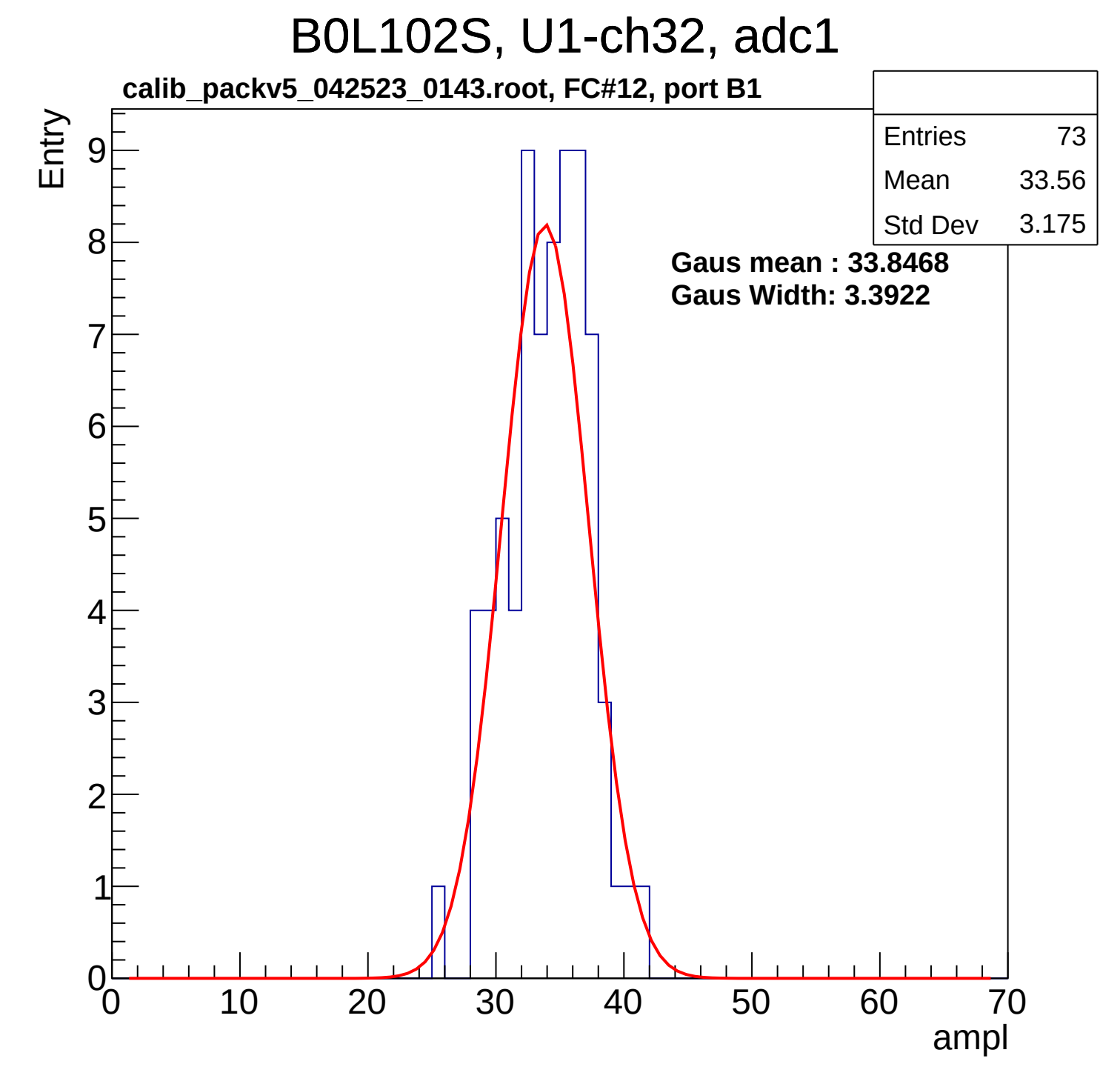
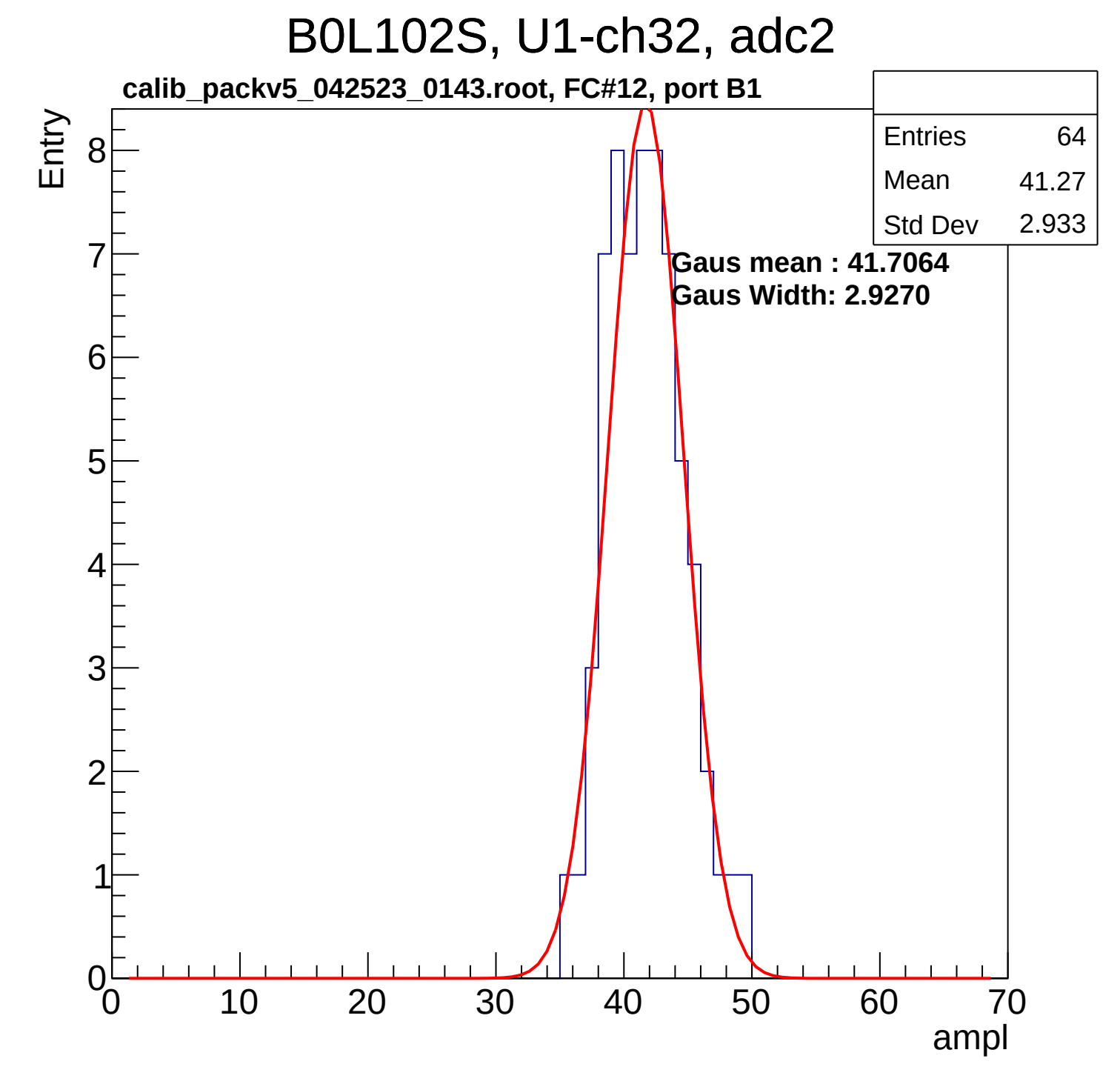
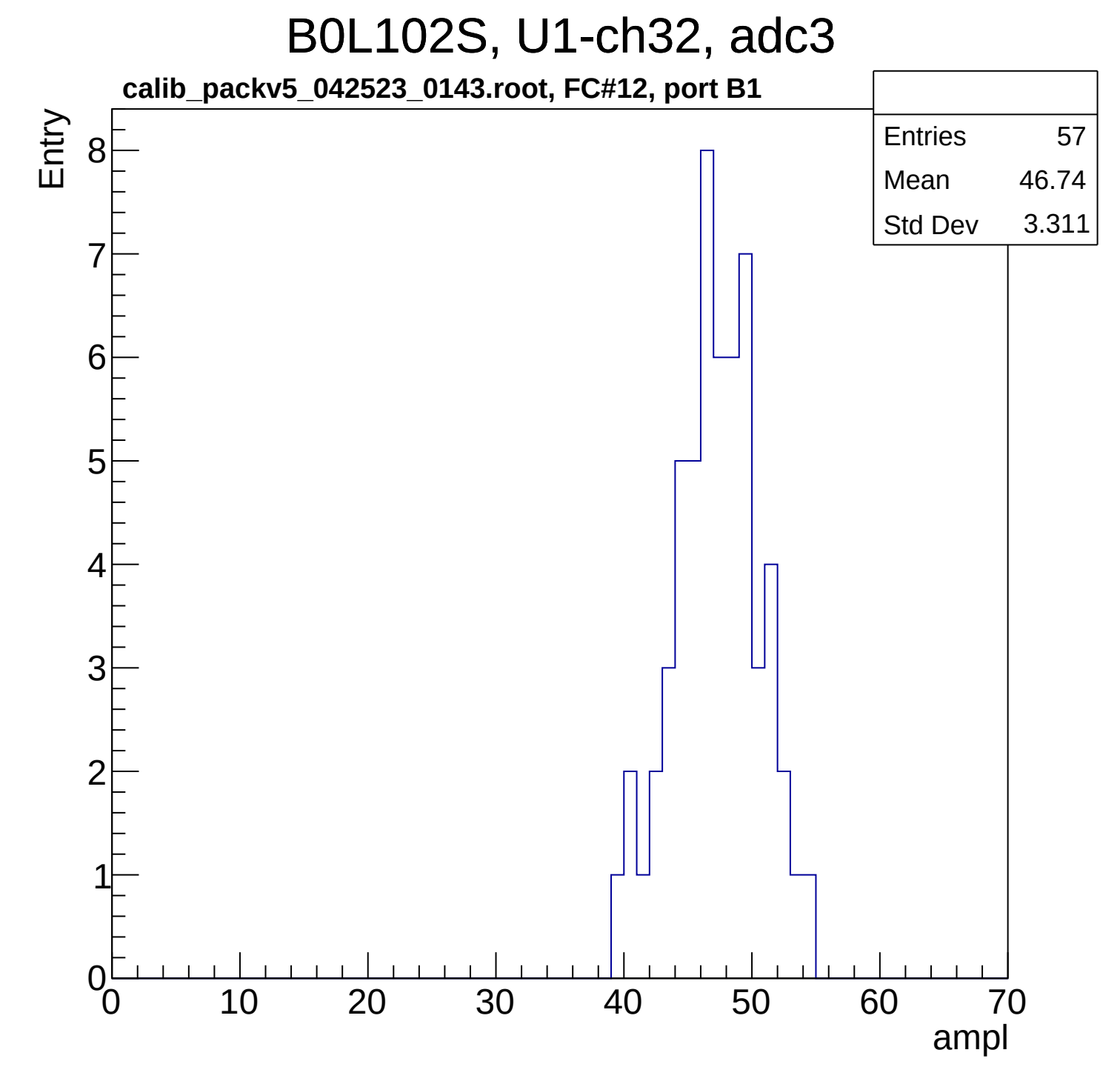

B0L102S, U1-ch31, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 57.47
Std Dev 15.41
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch31, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch32, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 26.93
Std Dev 3.613
Gaus mean : 27.5088
Gaus Width: 3.2628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch32, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 33.56
Std Dev 3.175
Gaus mean : 33.8468
Gaus Width: 3.3922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch32, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 41.27
Std Dev 2.933
Gaus mean : 41.7064
Gaus Width: 2.9270
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch32, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 46.74
Std Dev 3.311
Entry
ampl
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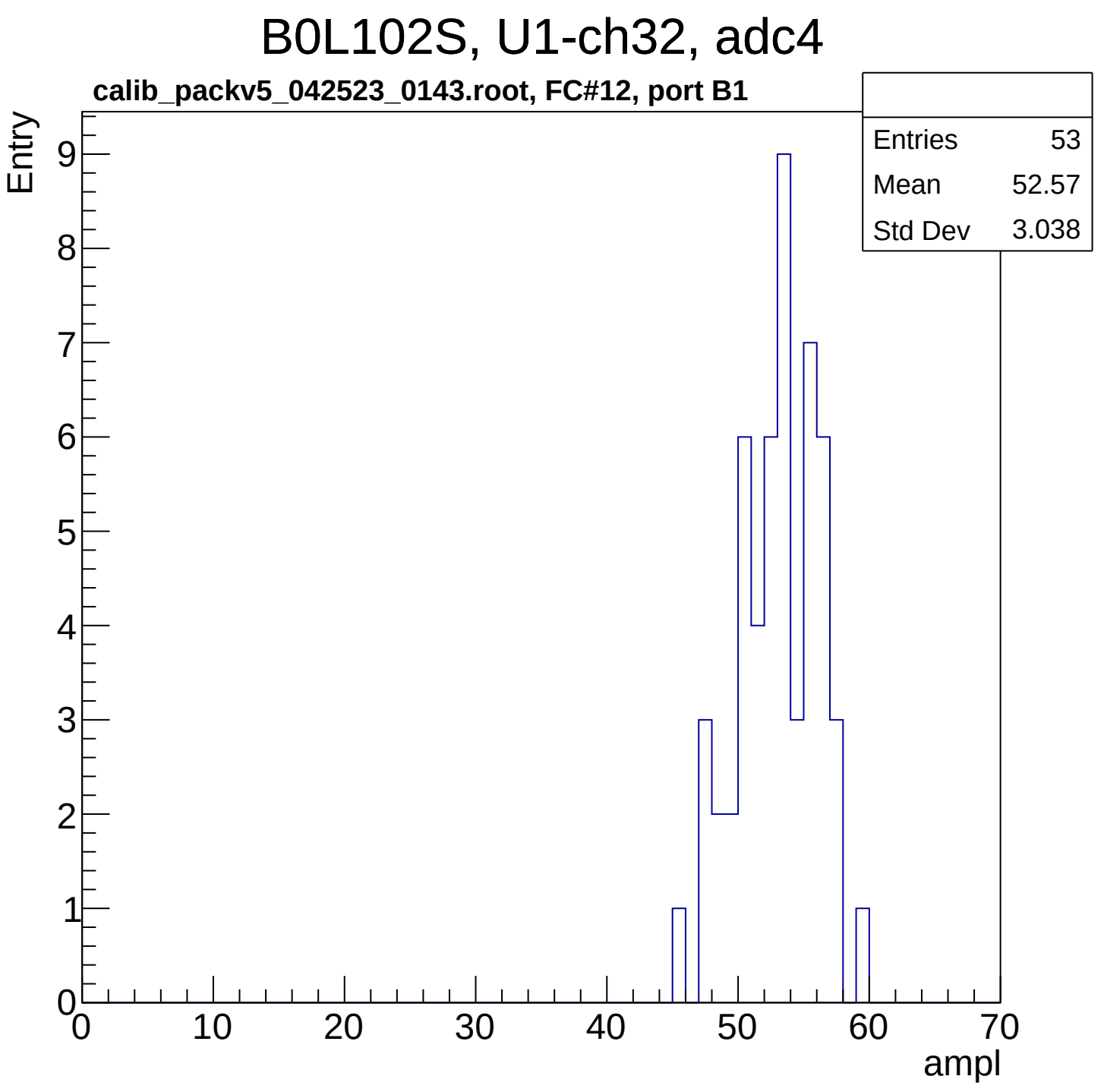
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch32, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 52.57
Std Dev 3.038
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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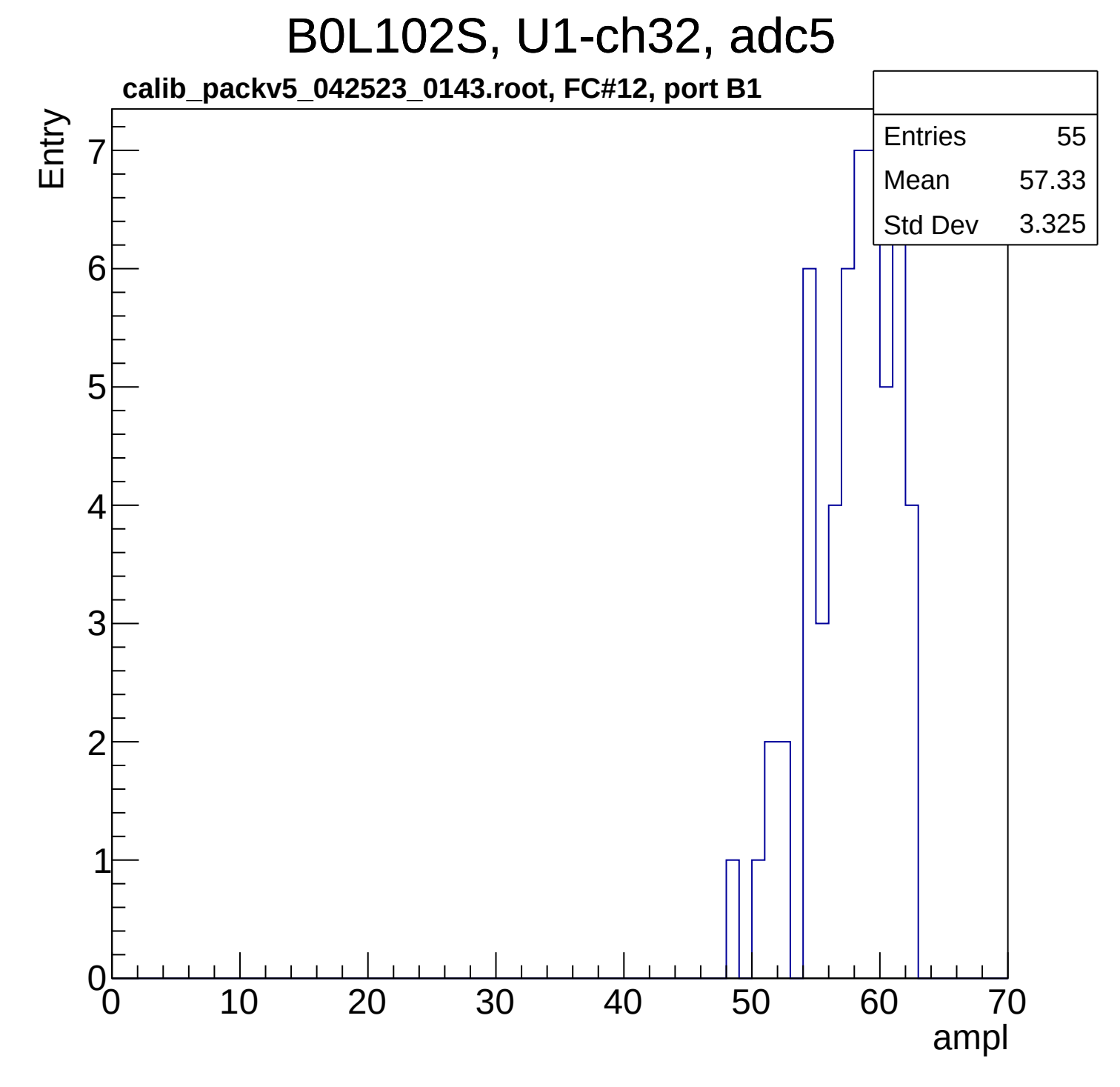
60
70


B0L102S, U1-ch32, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 57.33
Std Dev 3.325
Entry
ampl
0
1
2
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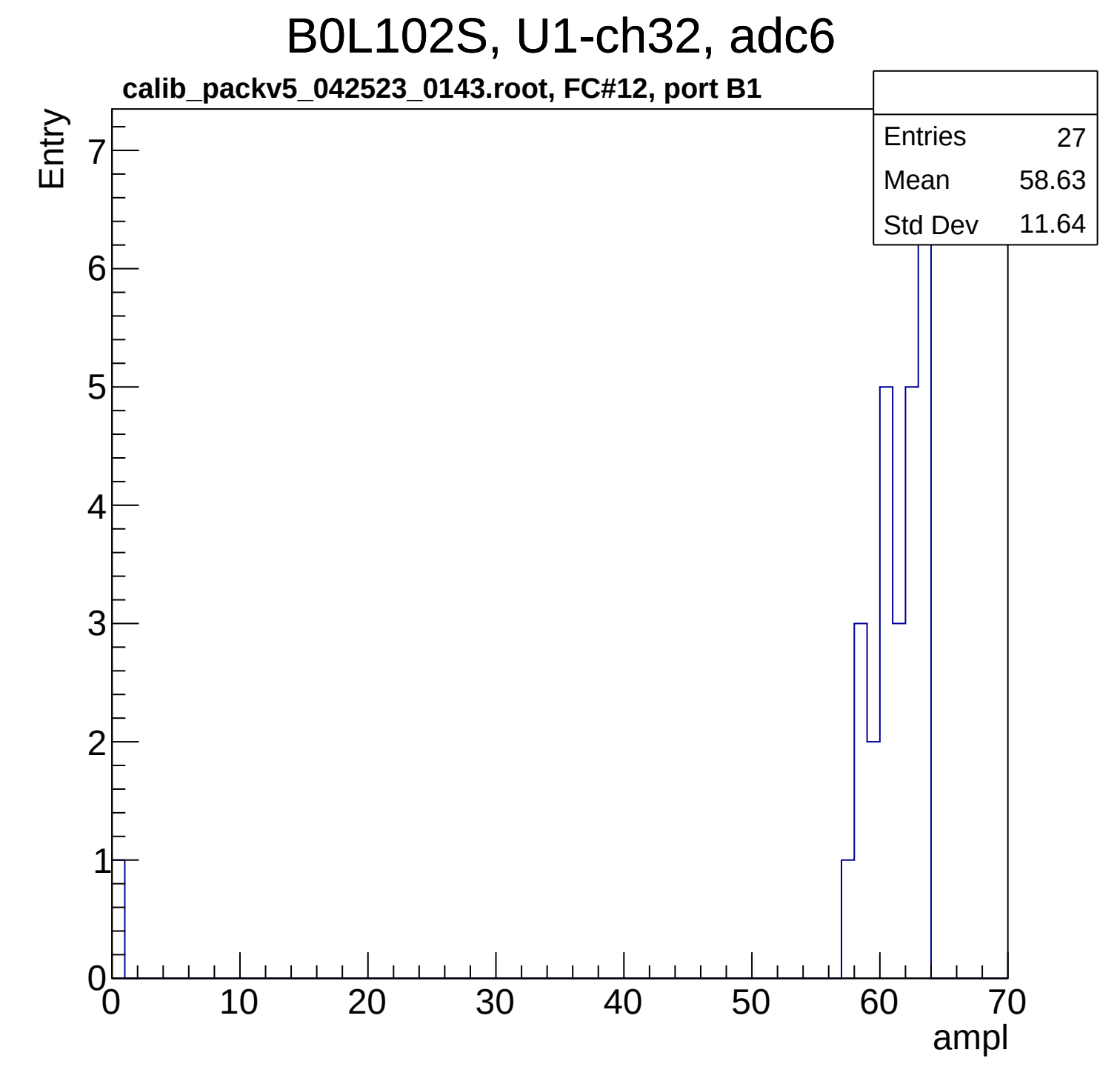
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch32, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 27
Mean 58.63
Std Dev 11.64
Entry
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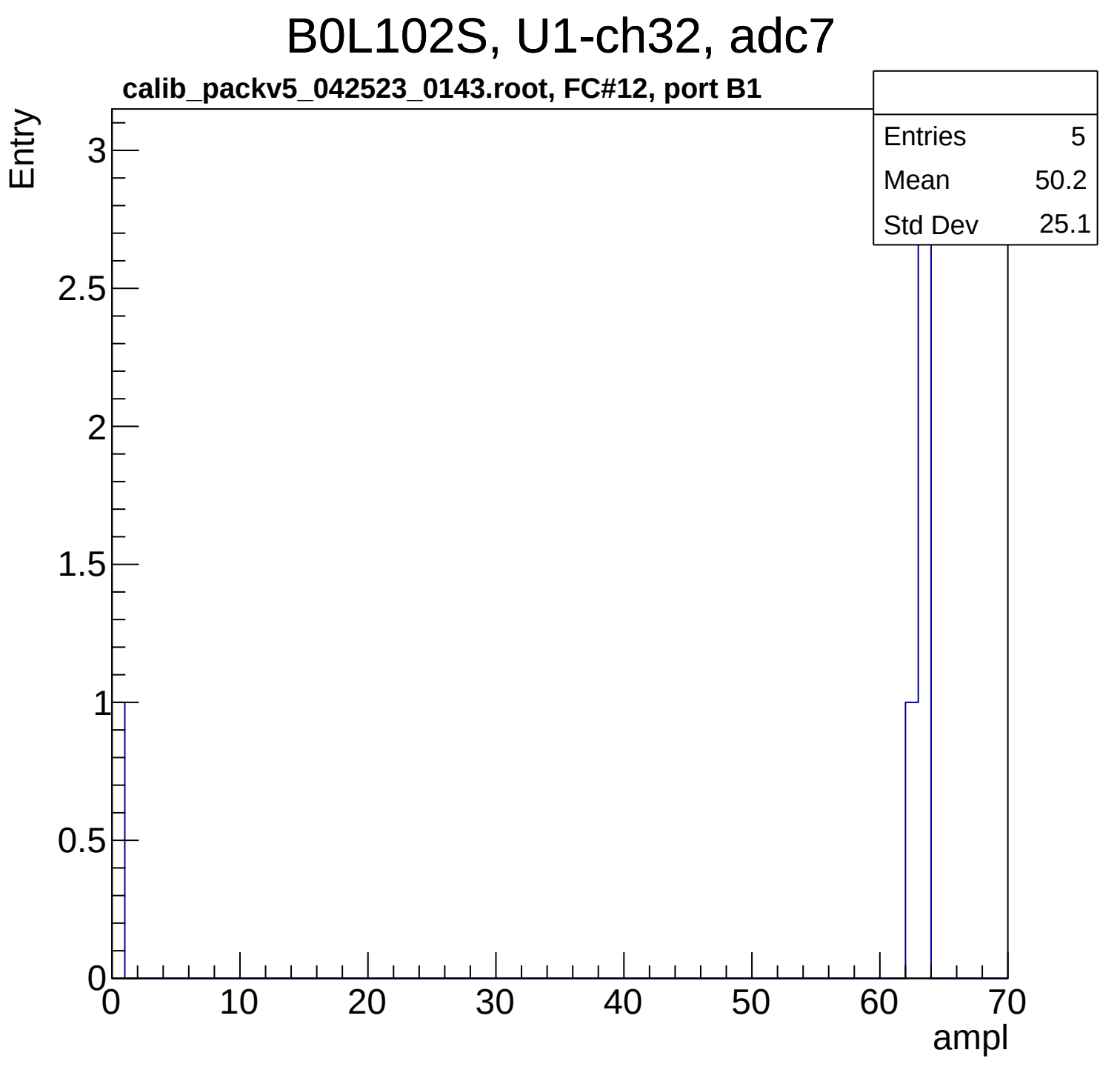
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch32, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 50.2
Std Dev 25.1
Entry
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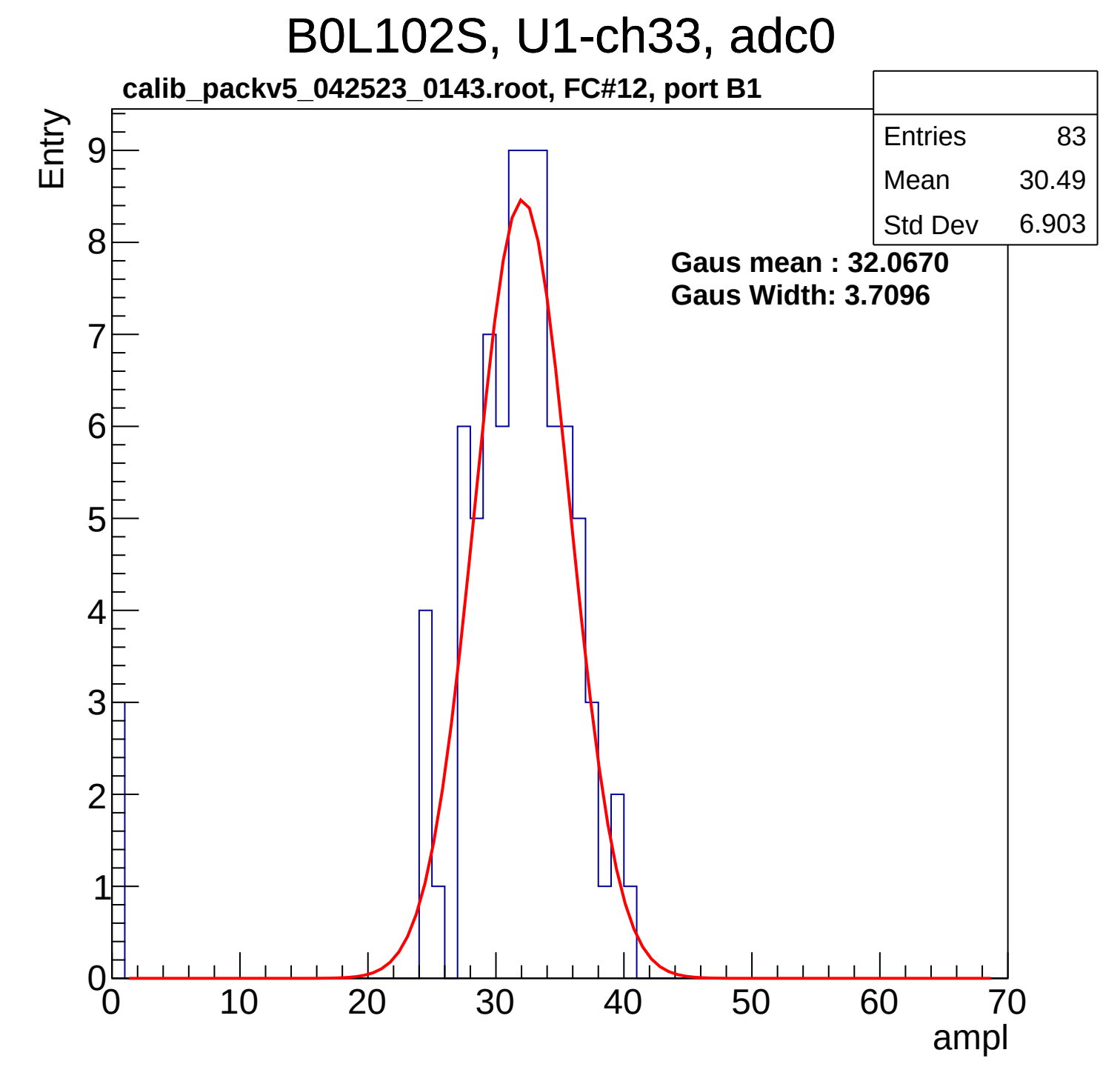
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch33, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 30.49
Std Dev 6.903
Gaus mean : 32.0670
Gaus Width: 3.7096
Entry
ampl
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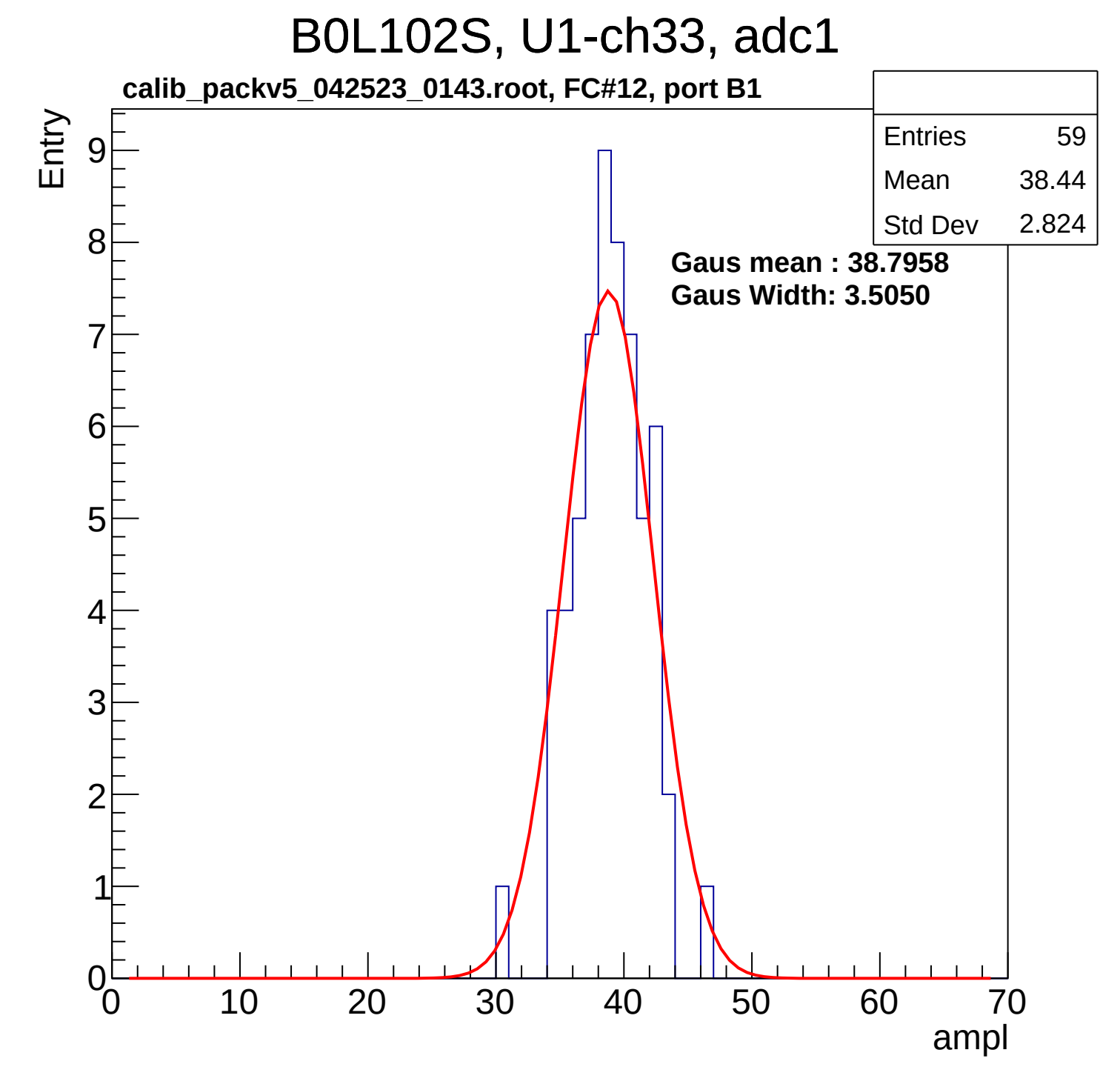
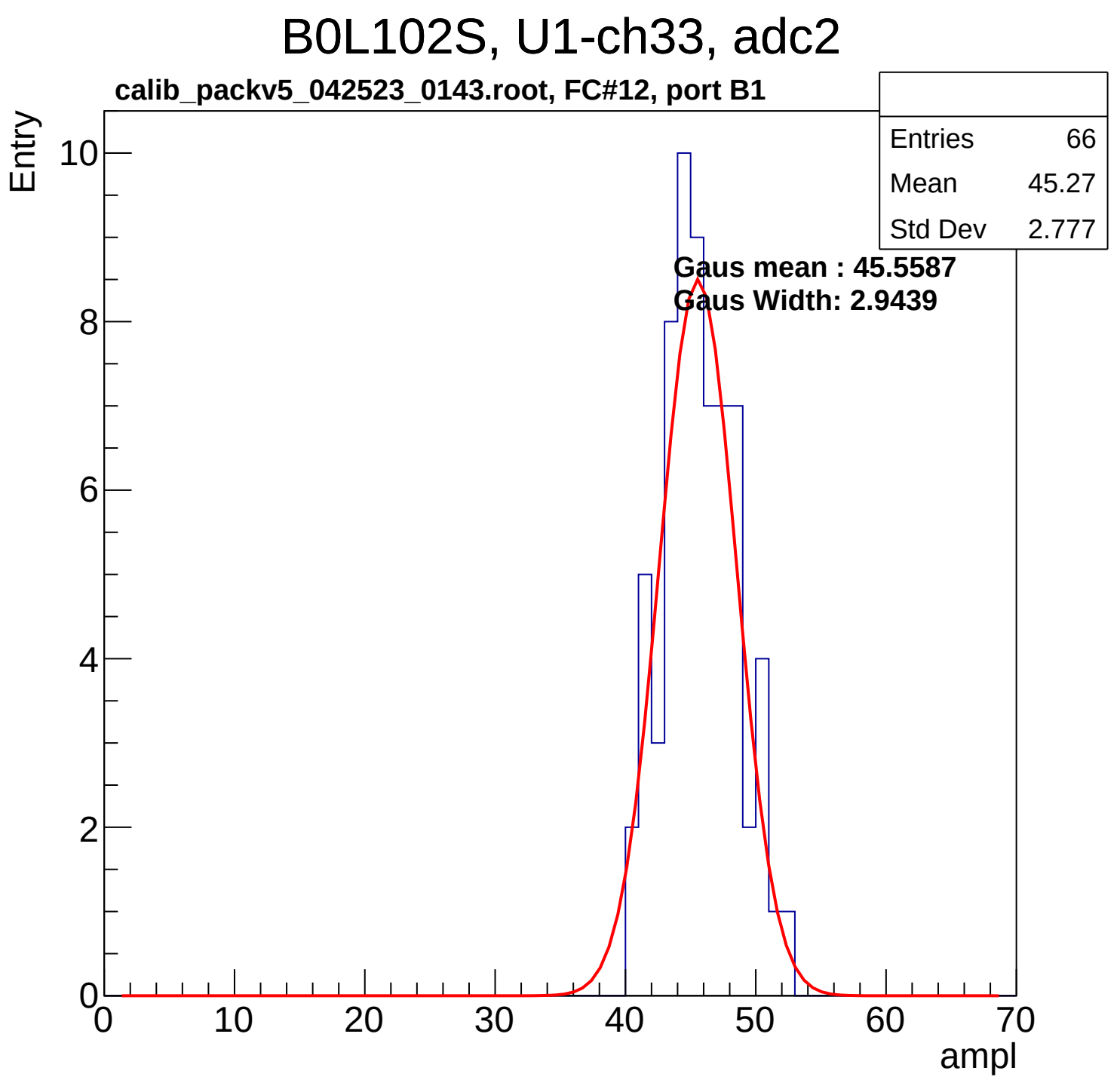
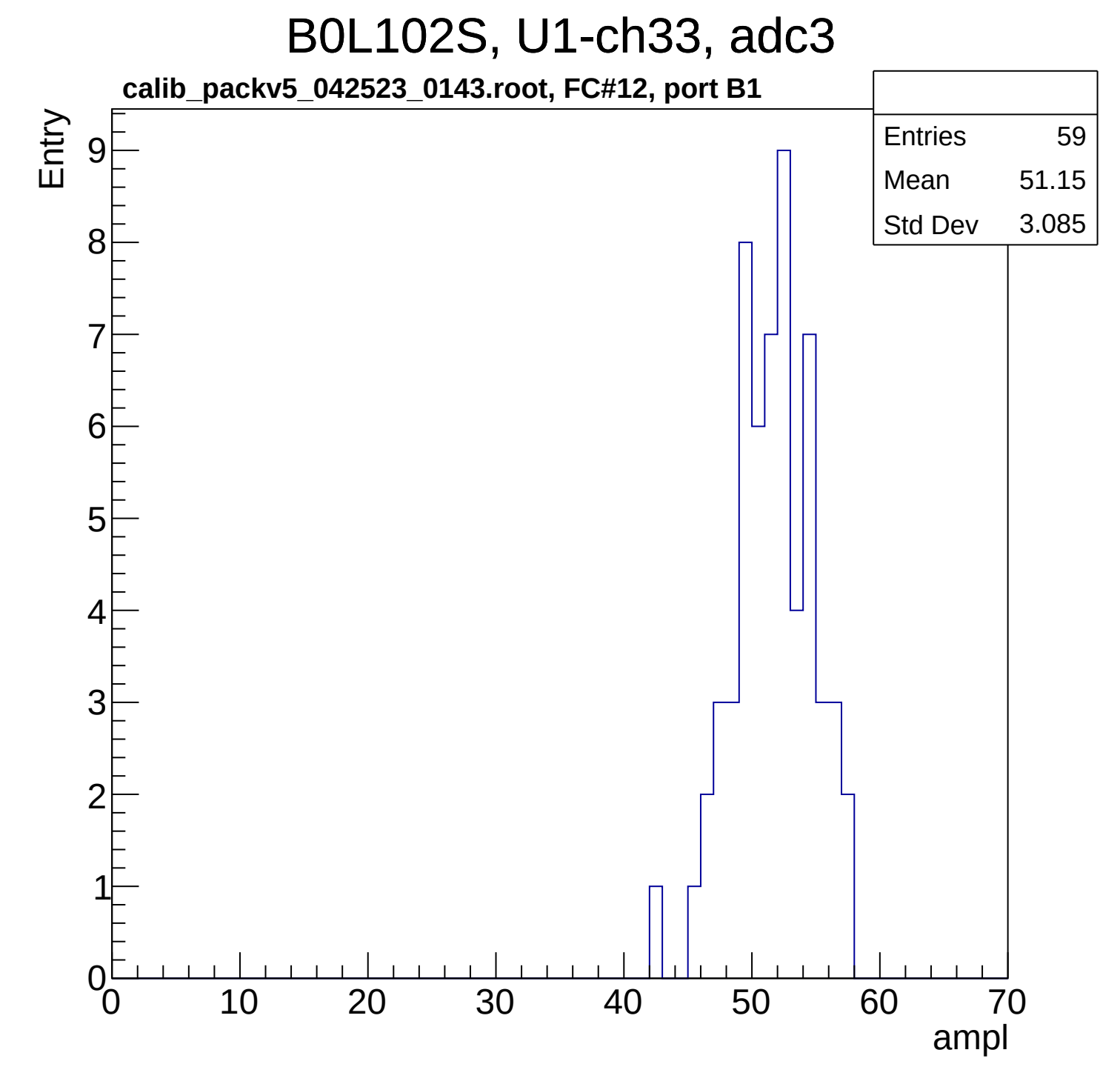
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch33, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 38.44
Std Dev 2.824
Gaus mean : 38.7958
Gaus Width: 3.5050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch33, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 45.27
Std Dev 2.777
Gaus mean : 45.5587
Gaus Width: 2.9439
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch33, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 51.15
Std Dev 3.085
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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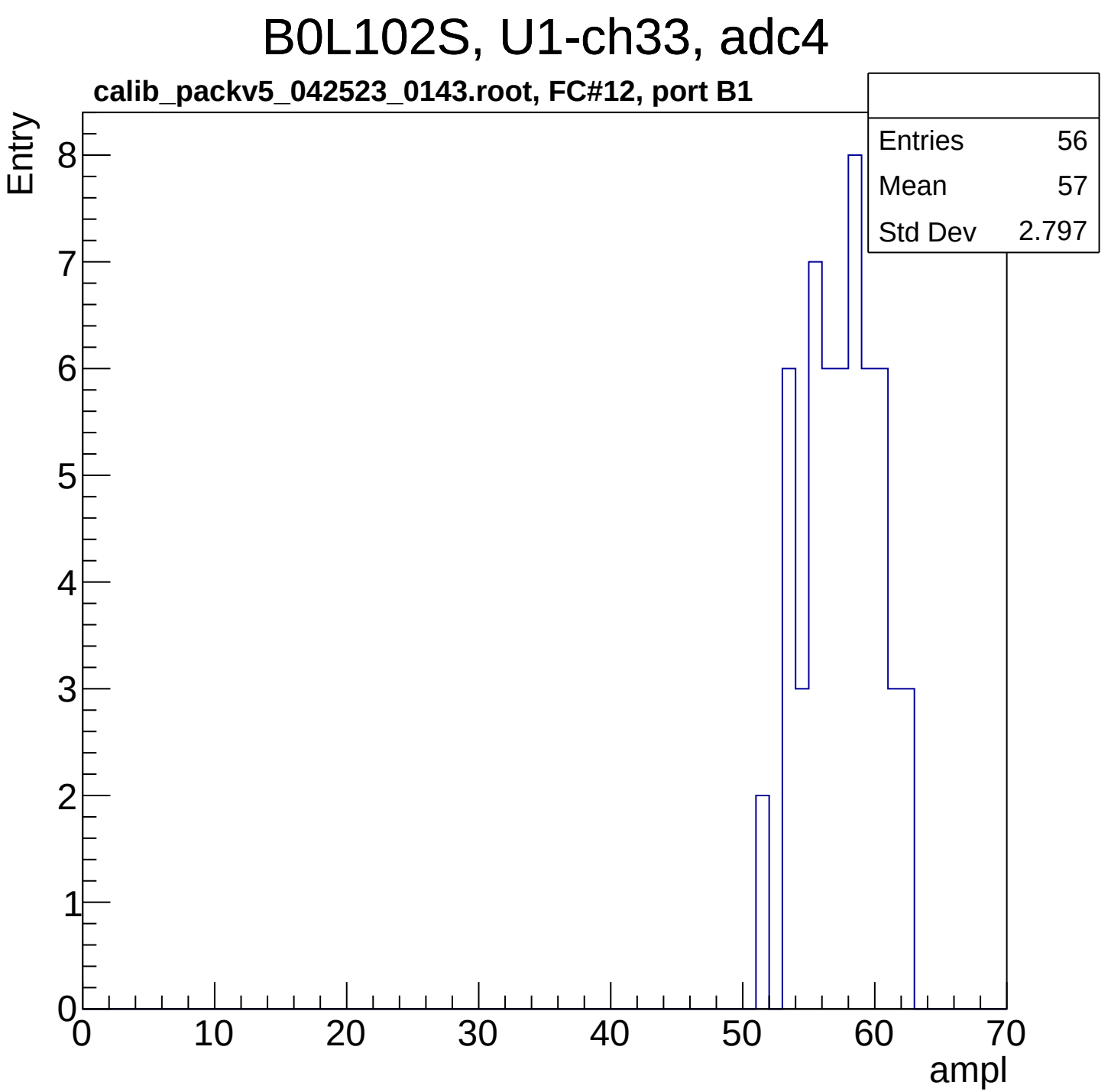
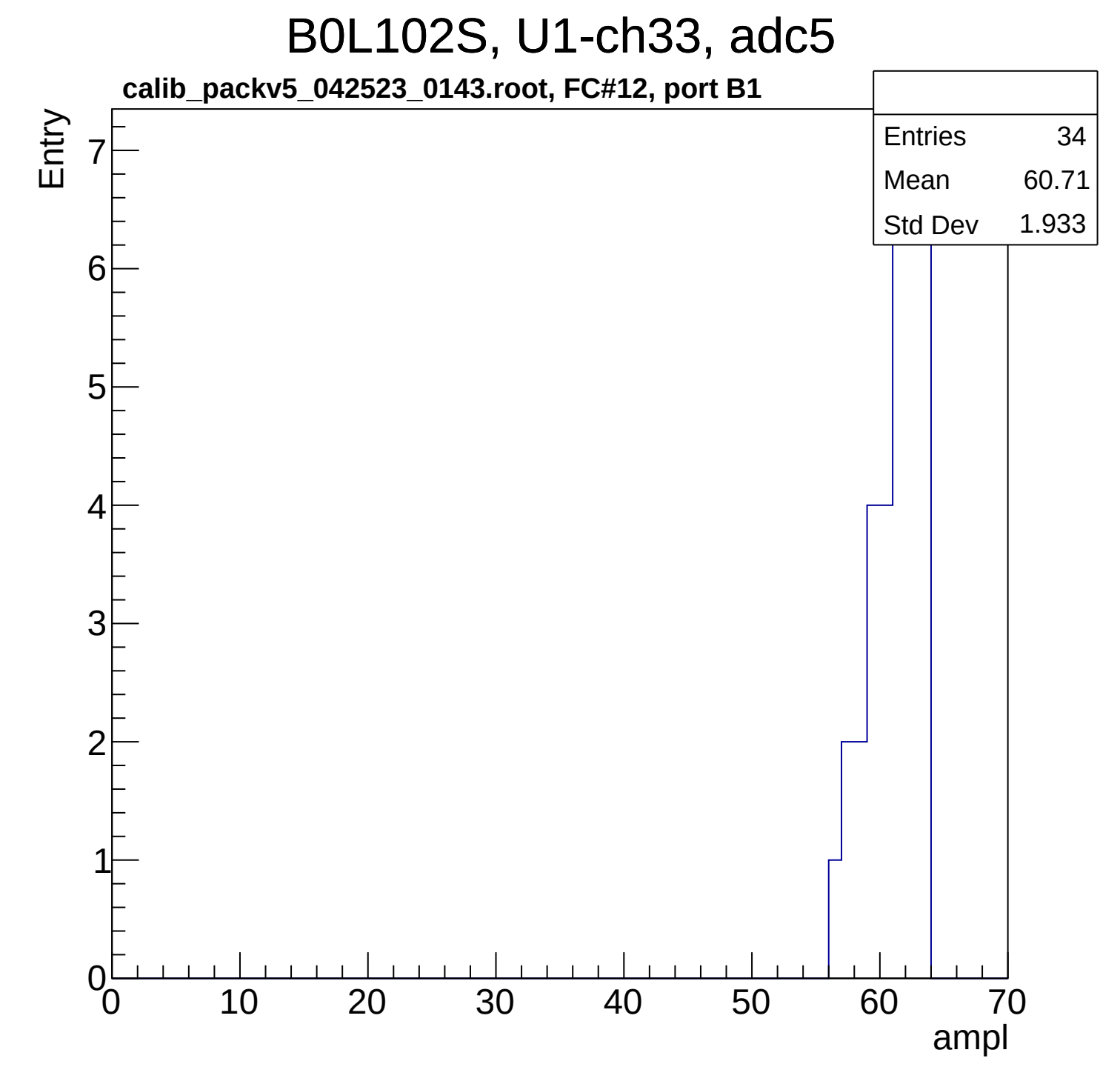

B0L102S, U1-ch33, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 57
Std Dev 2.797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch33, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 60.71
Std Dev 1.933
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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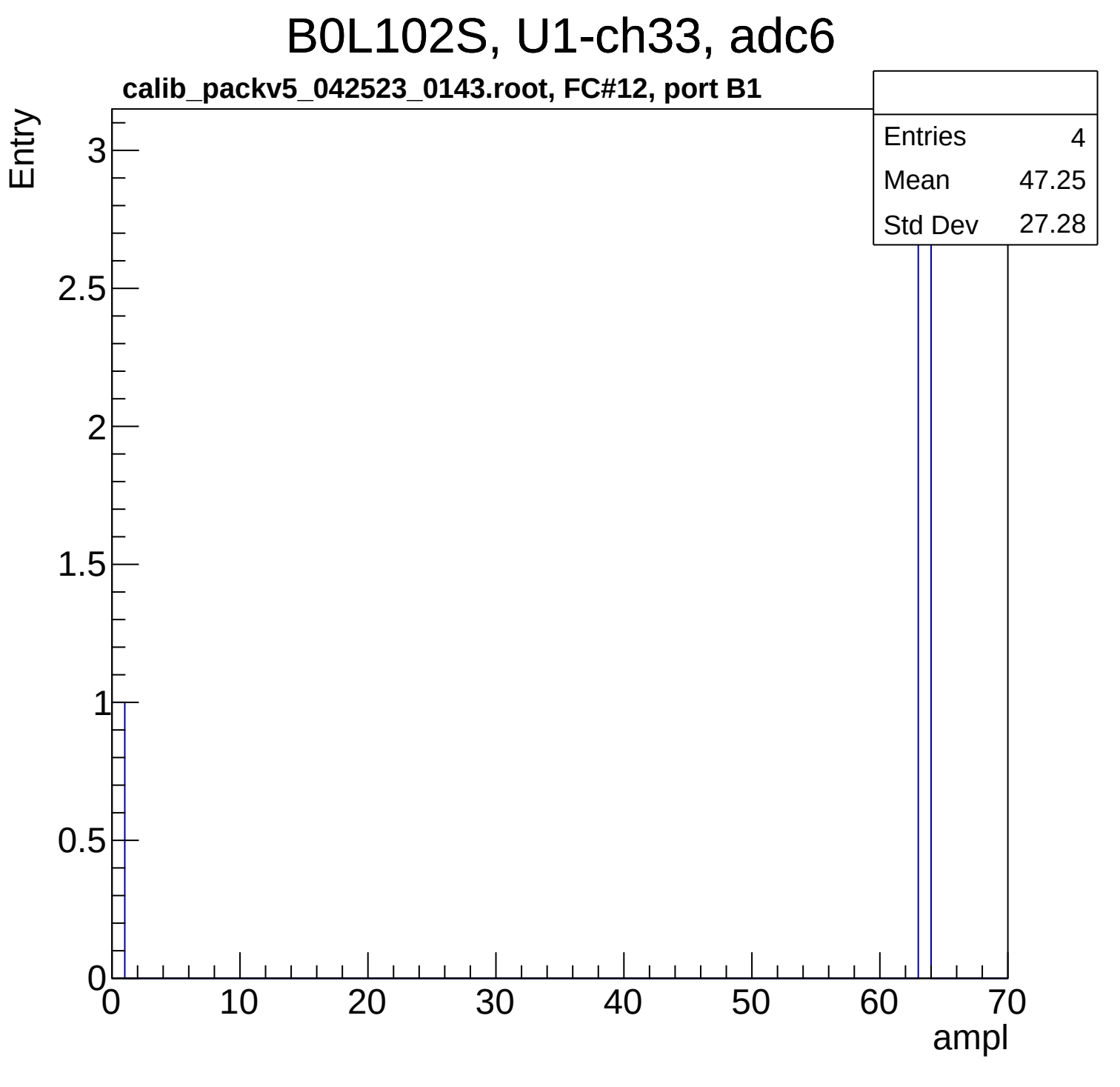
B0L102S, U1-ch33, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
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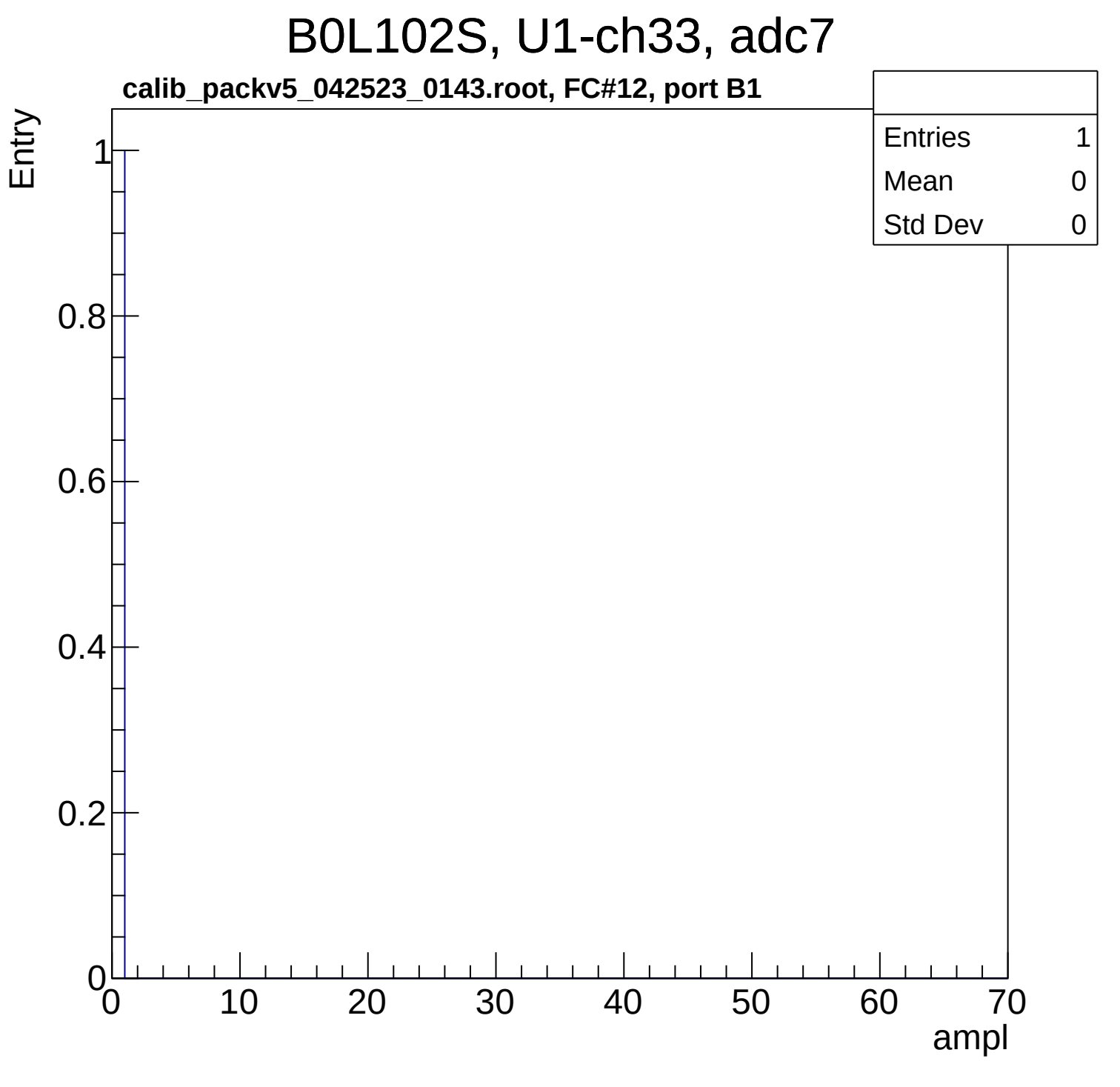
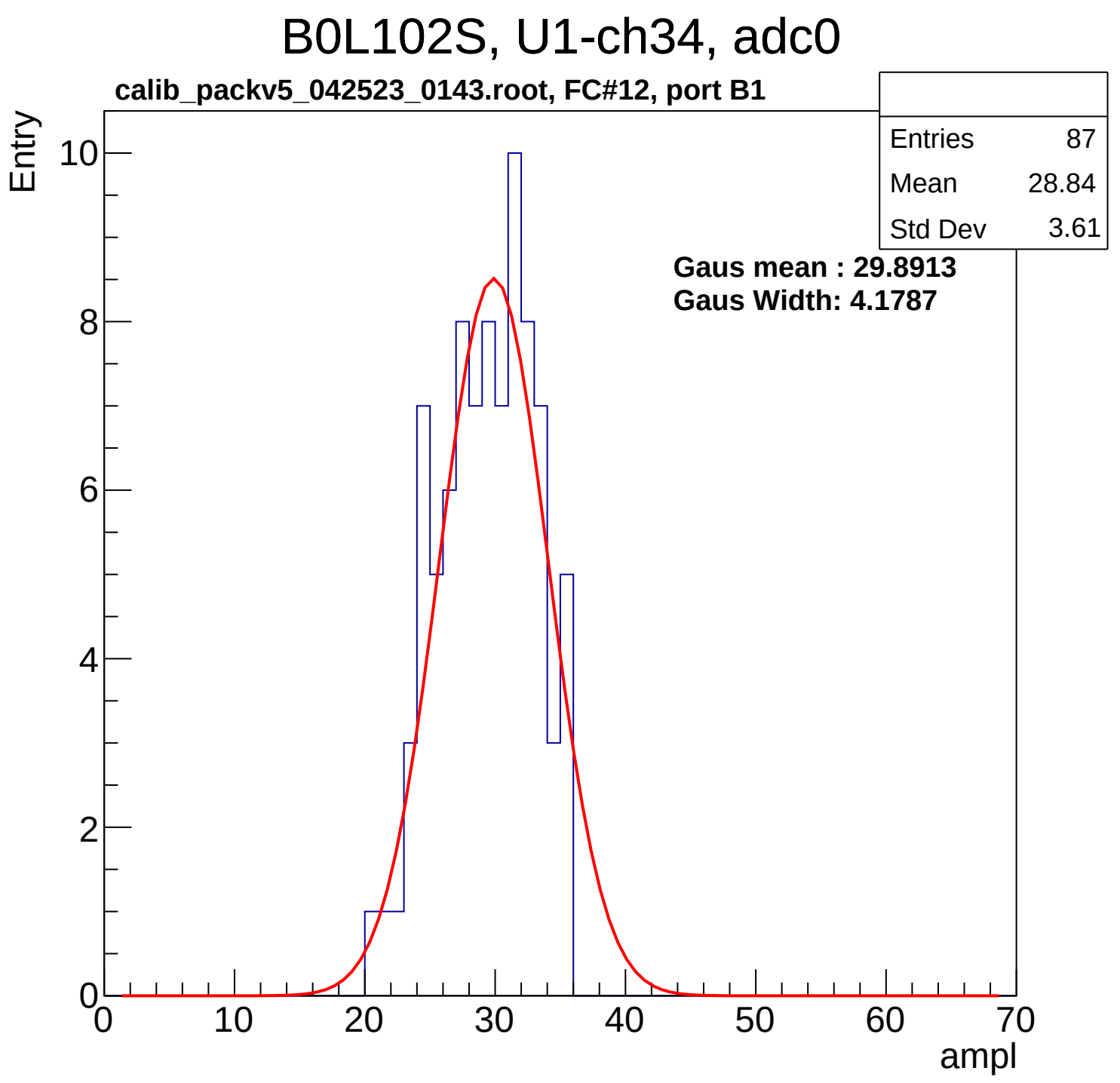
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch33, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch34, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 28.84
Std Dev 3.61
Gaus mean : 29.8913
Gaus Width: 4.1787
Entry
ampl
0
2
4
6
8
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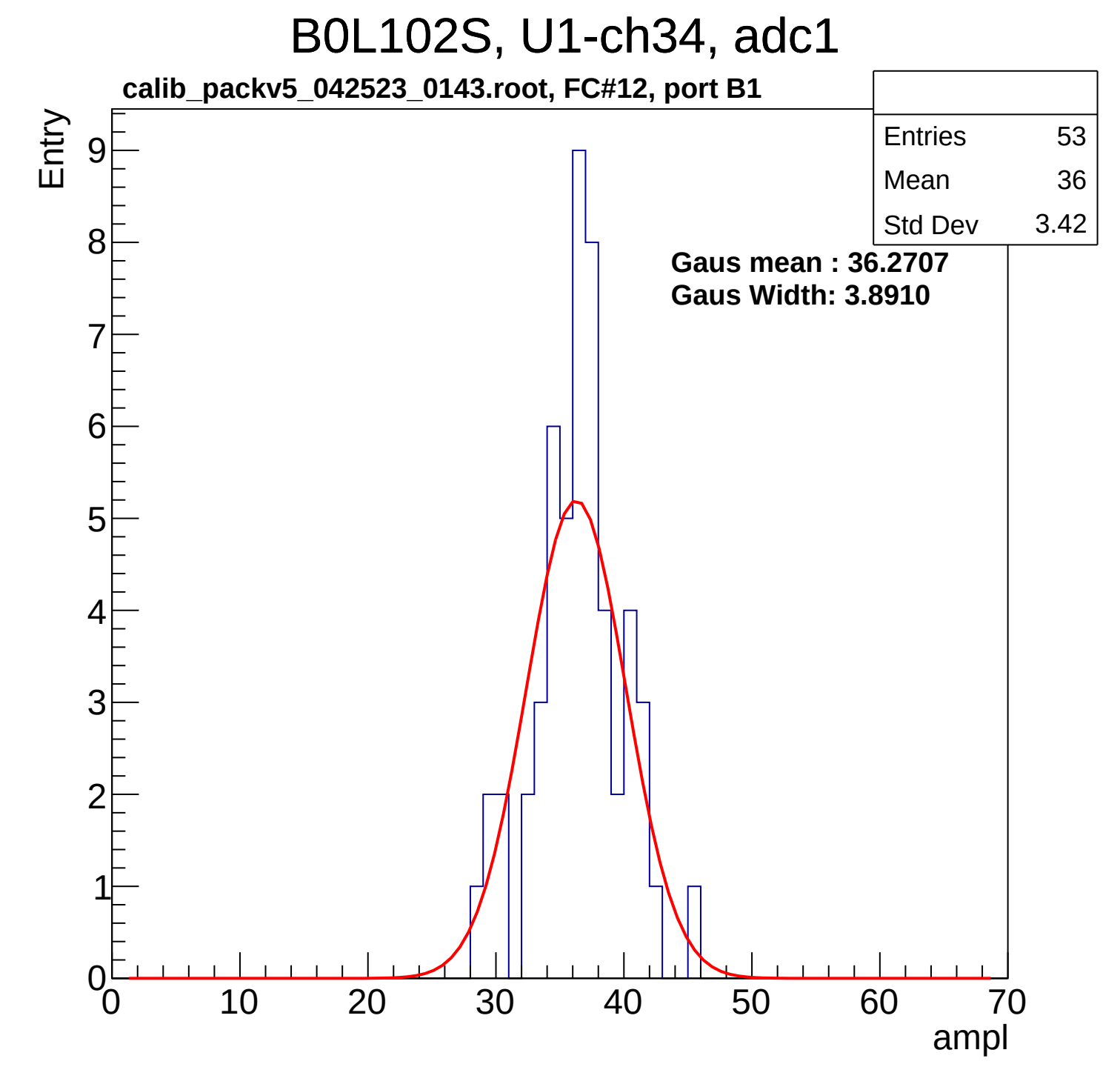
0
10
20
30
40
50
60
70


B0L102S, U1-ch34, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 36
Std Dev 3.42
Gaus mean : 36.2707
Gaus Width: 3.8910
Entry
ampl
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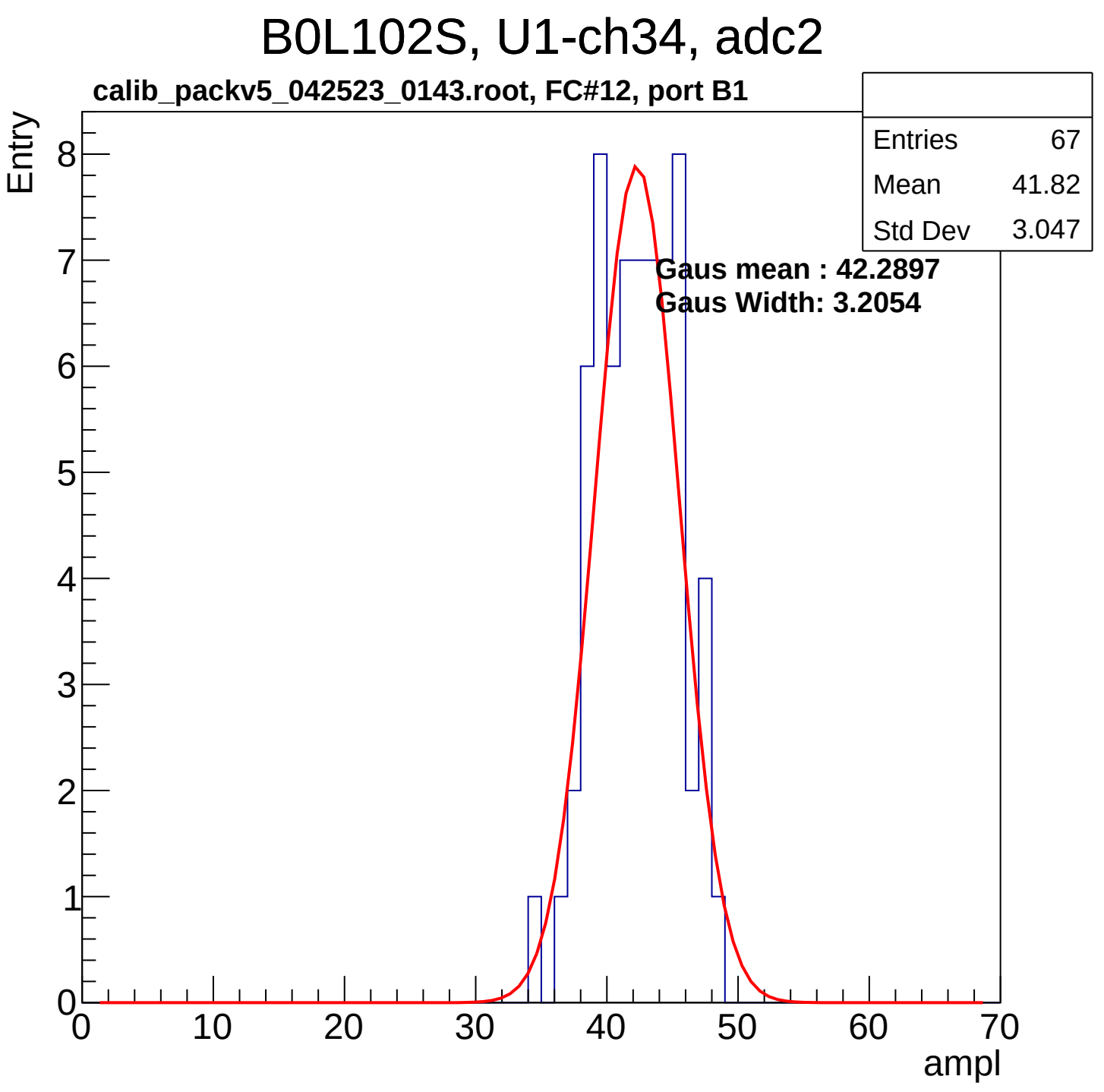
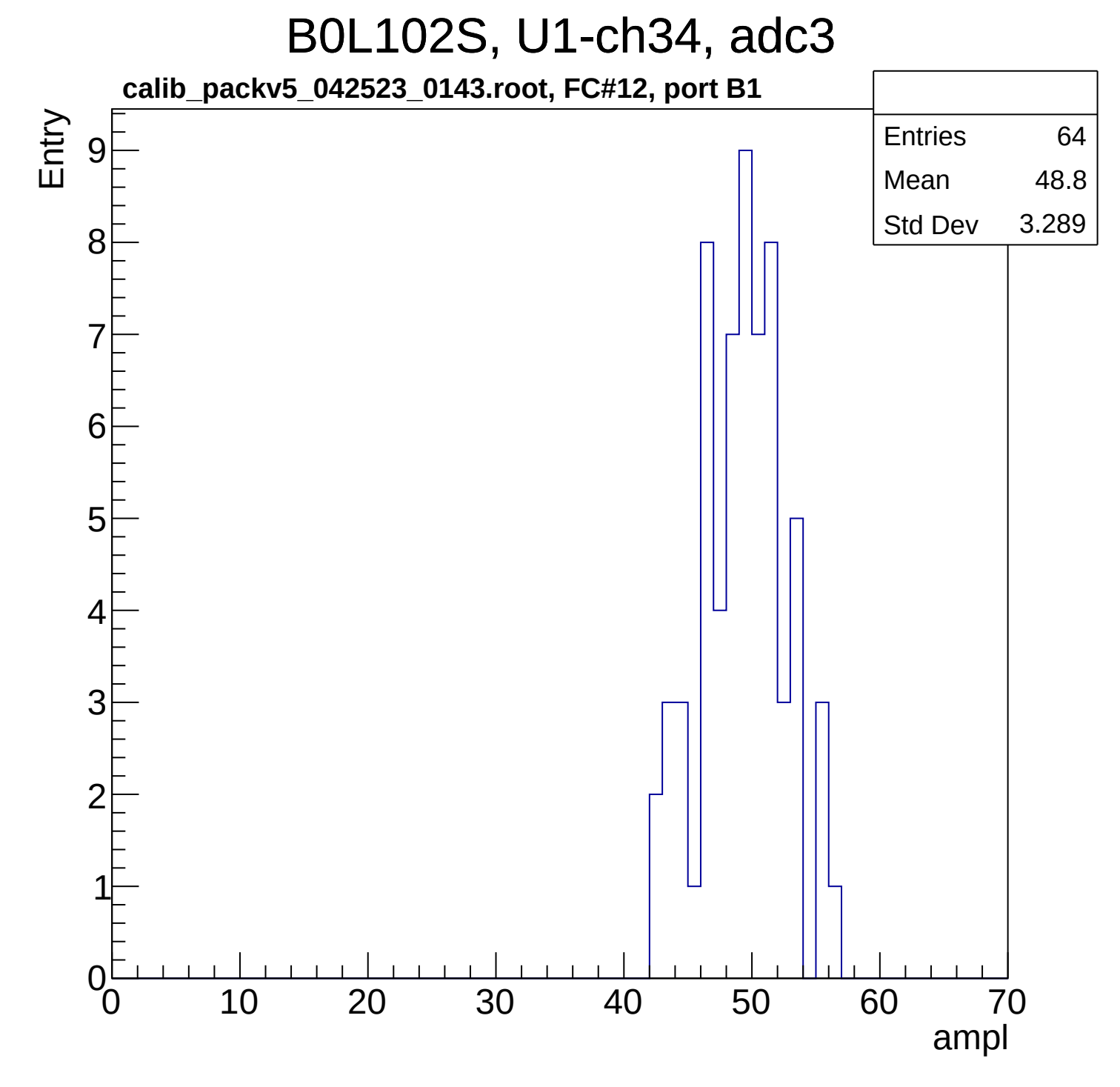
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch34, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 41.82
Std Dev 3.047
Gaus mean : 42.2897
Gaus Width: 3.2054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch34, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 48.8
Std Dev 3.289
Entry
ampl
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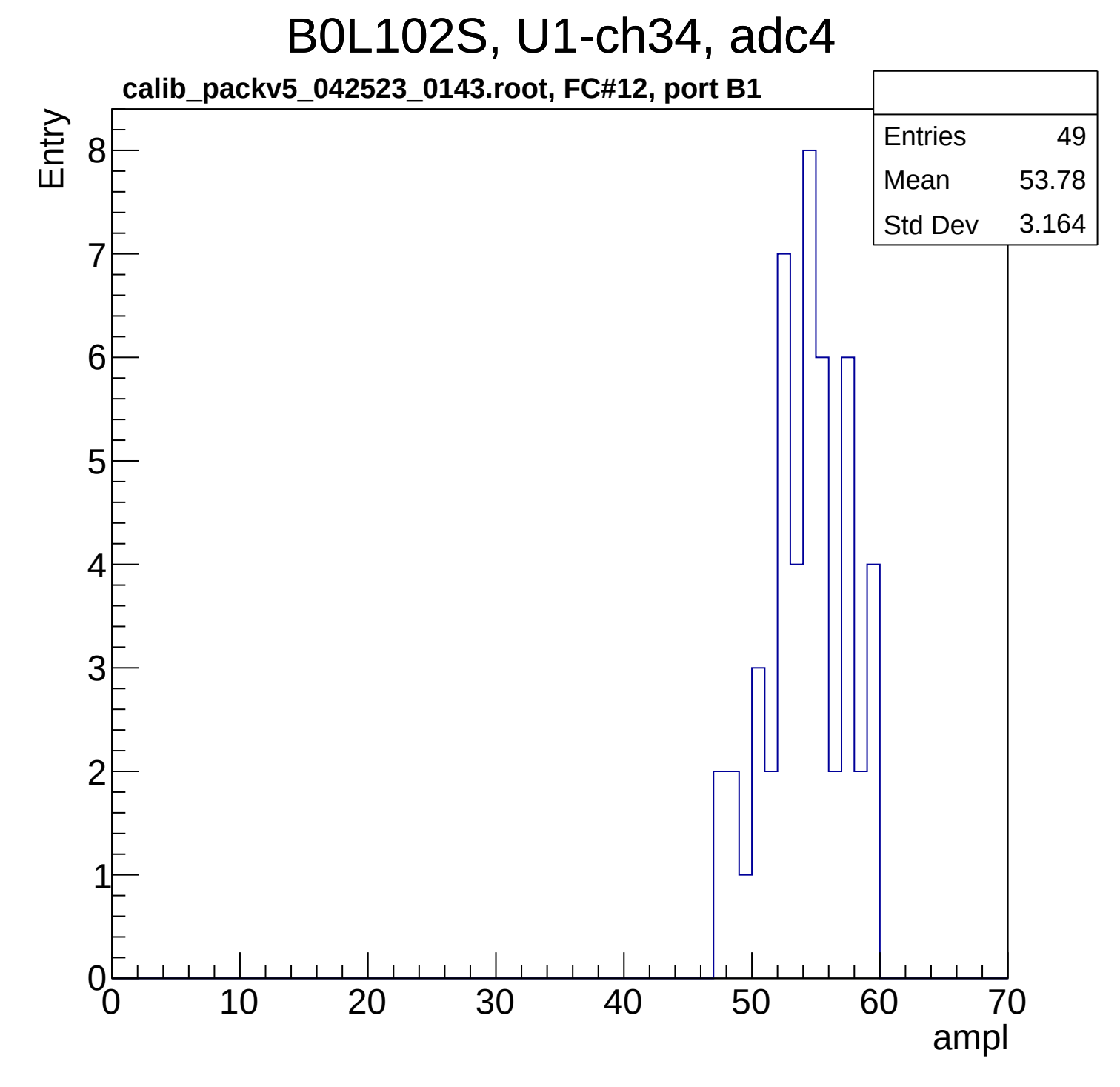
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch34, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 53.78
Std Dev 3.164
Entry
ampl
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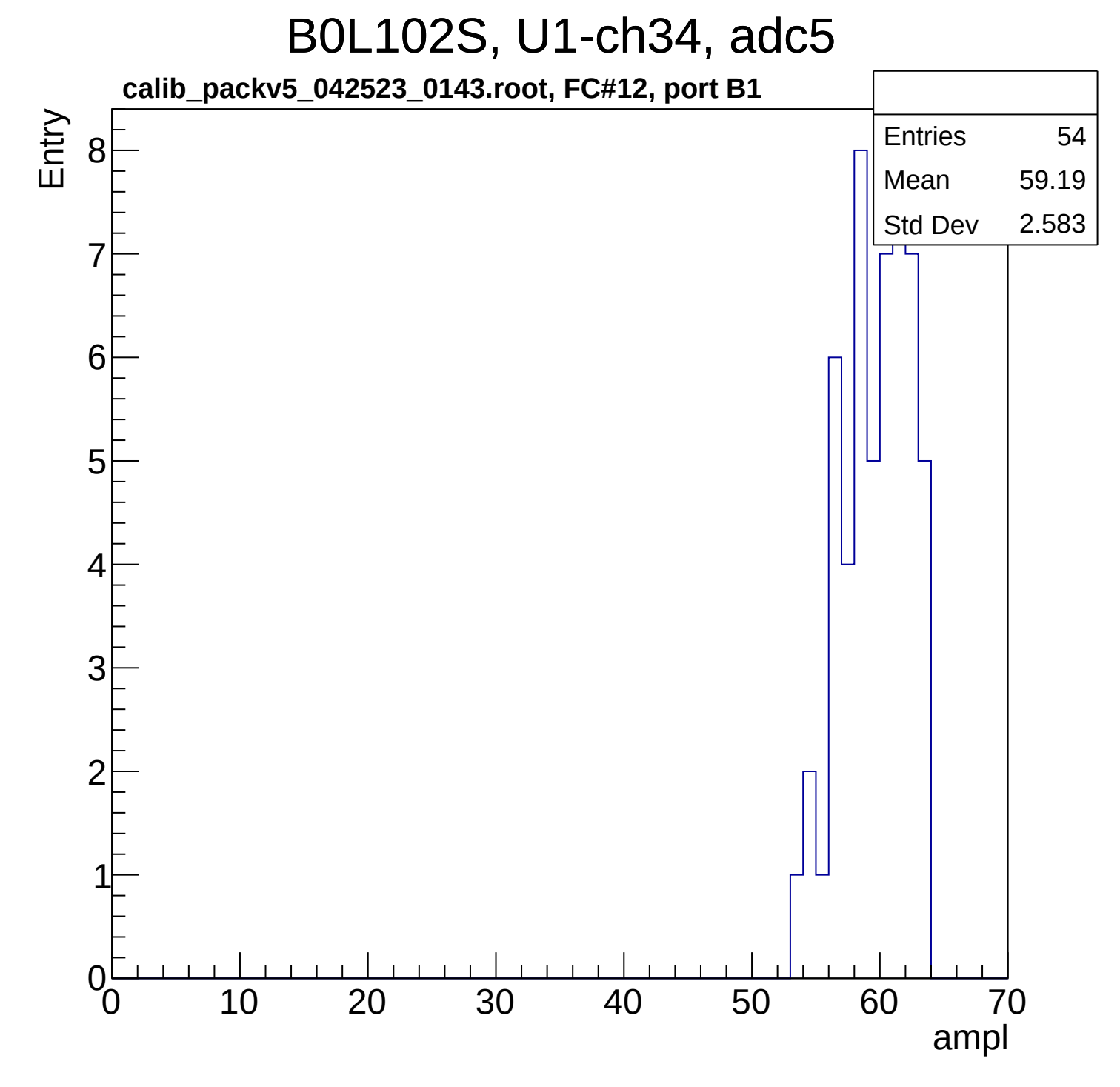
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch34, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 59.19
Std Dev 2.583
Entry
ampl
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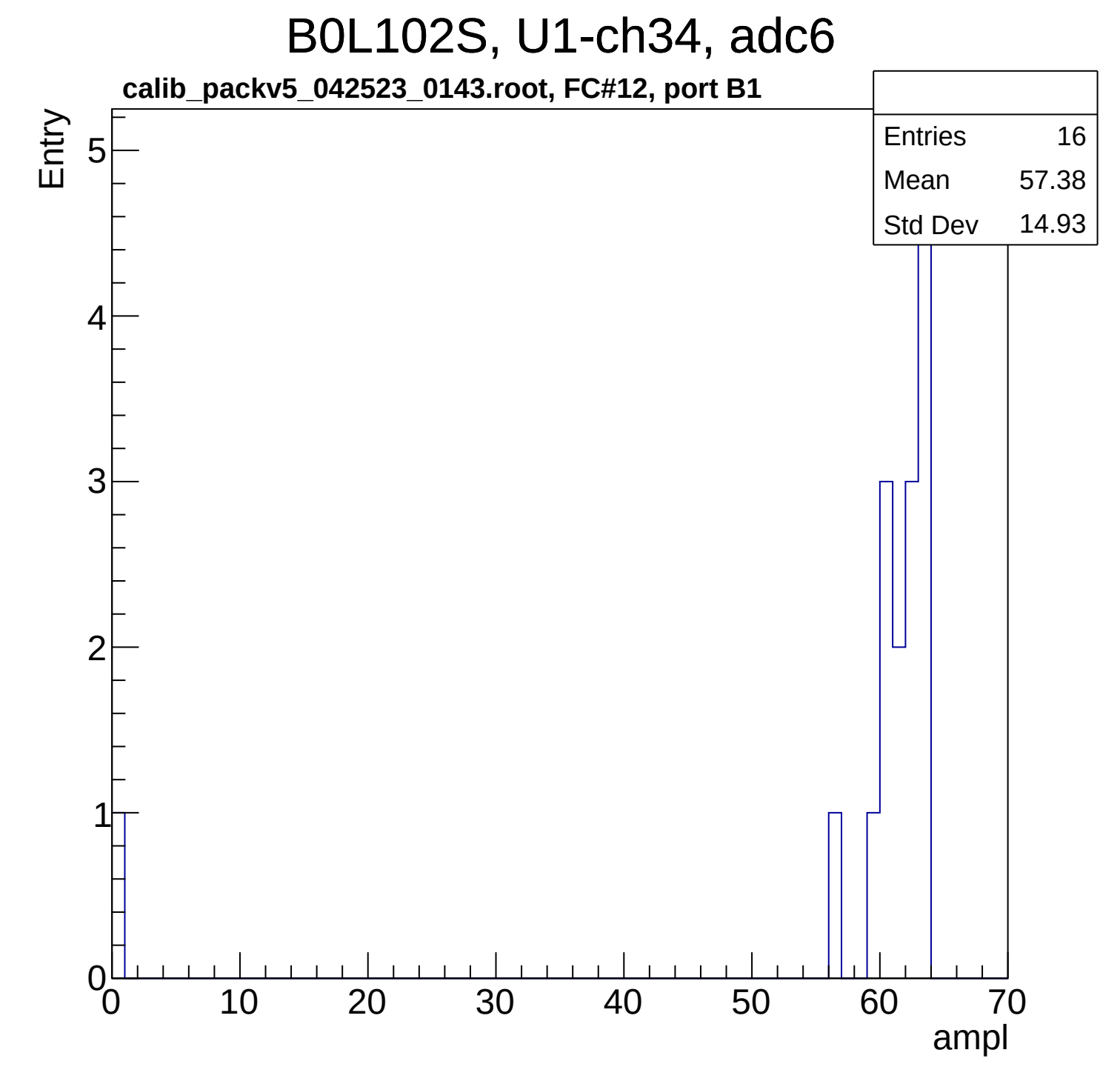
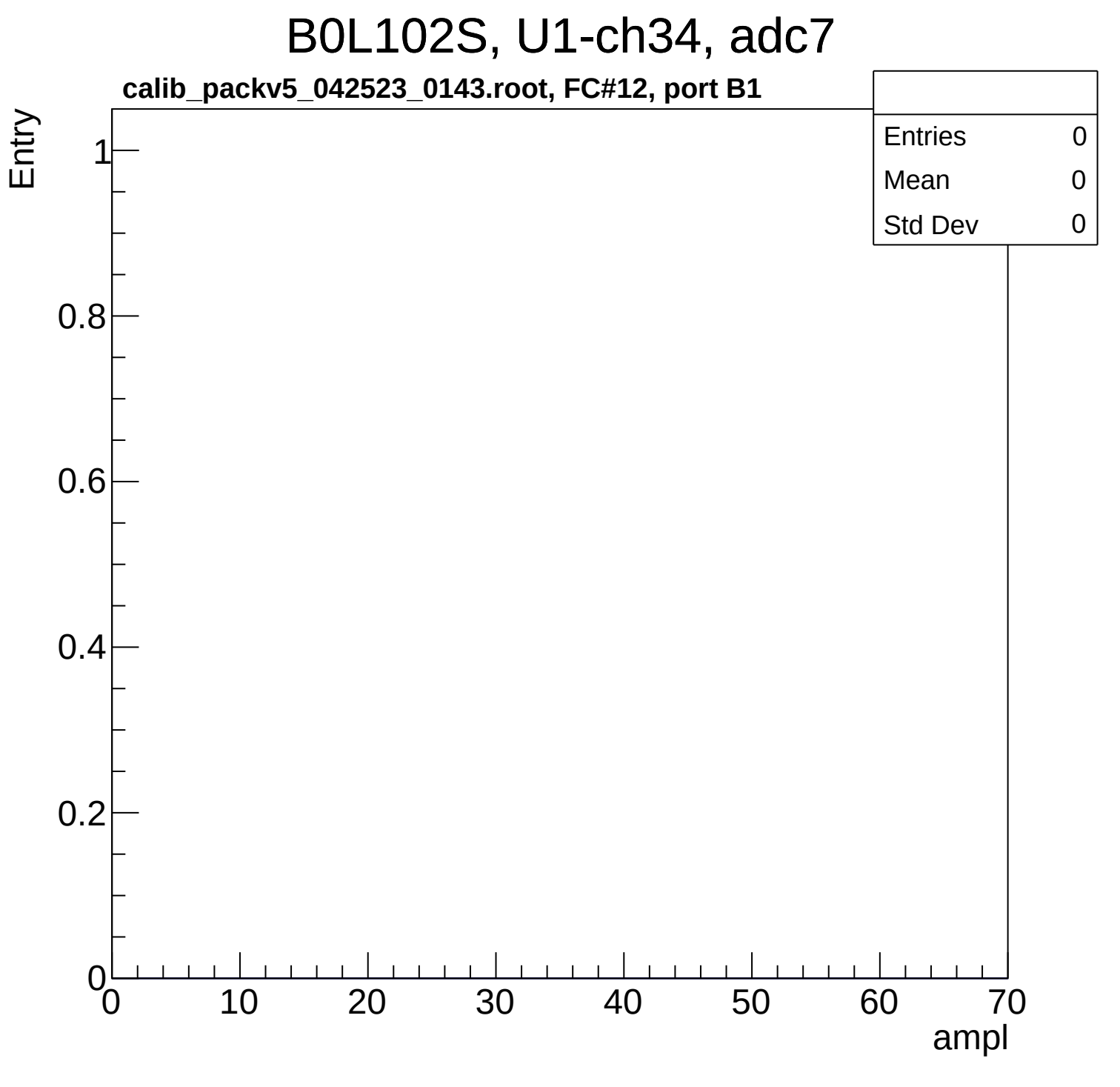
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch34, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 57.38
Std Dev 14.93
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch34, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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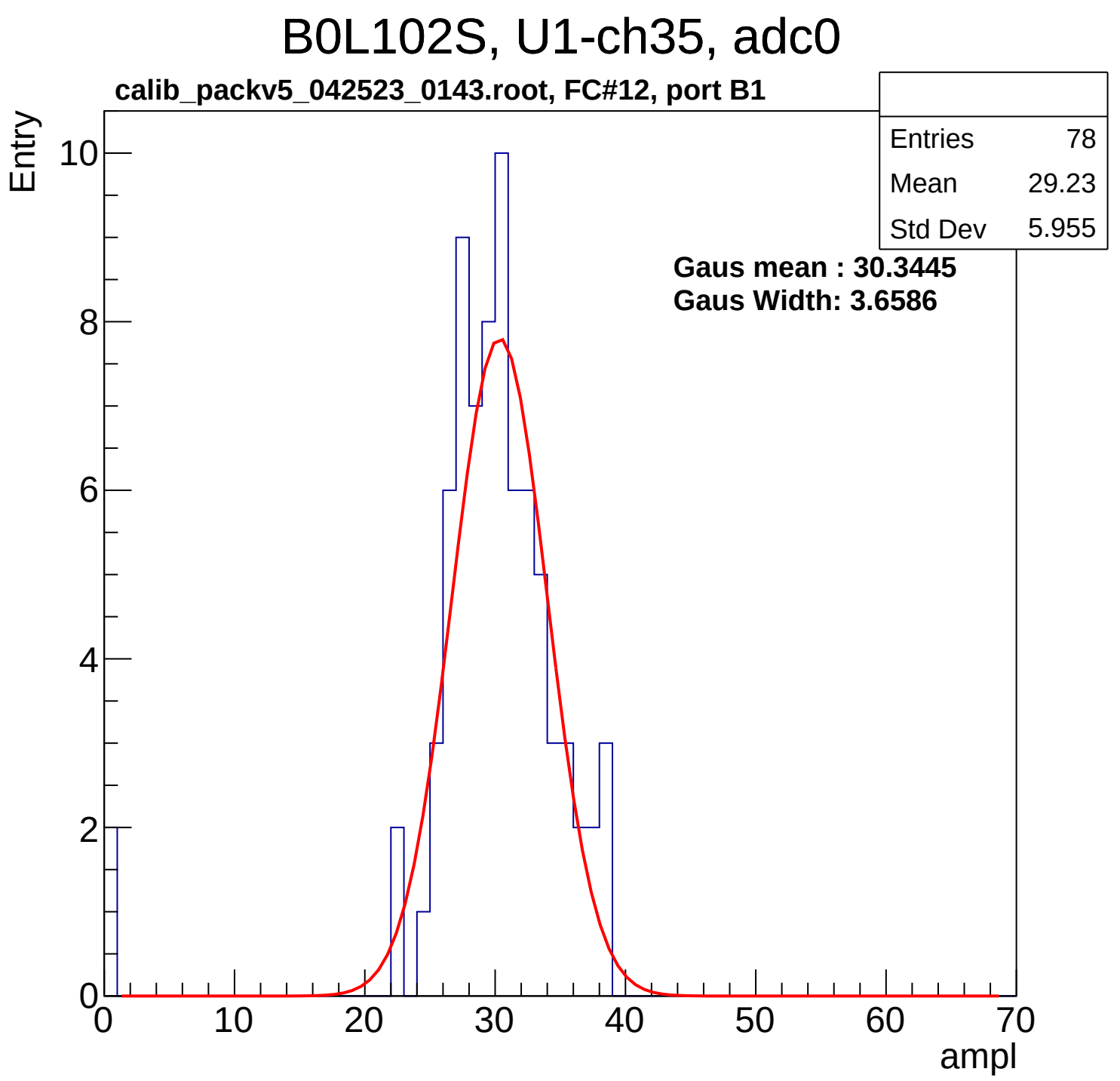
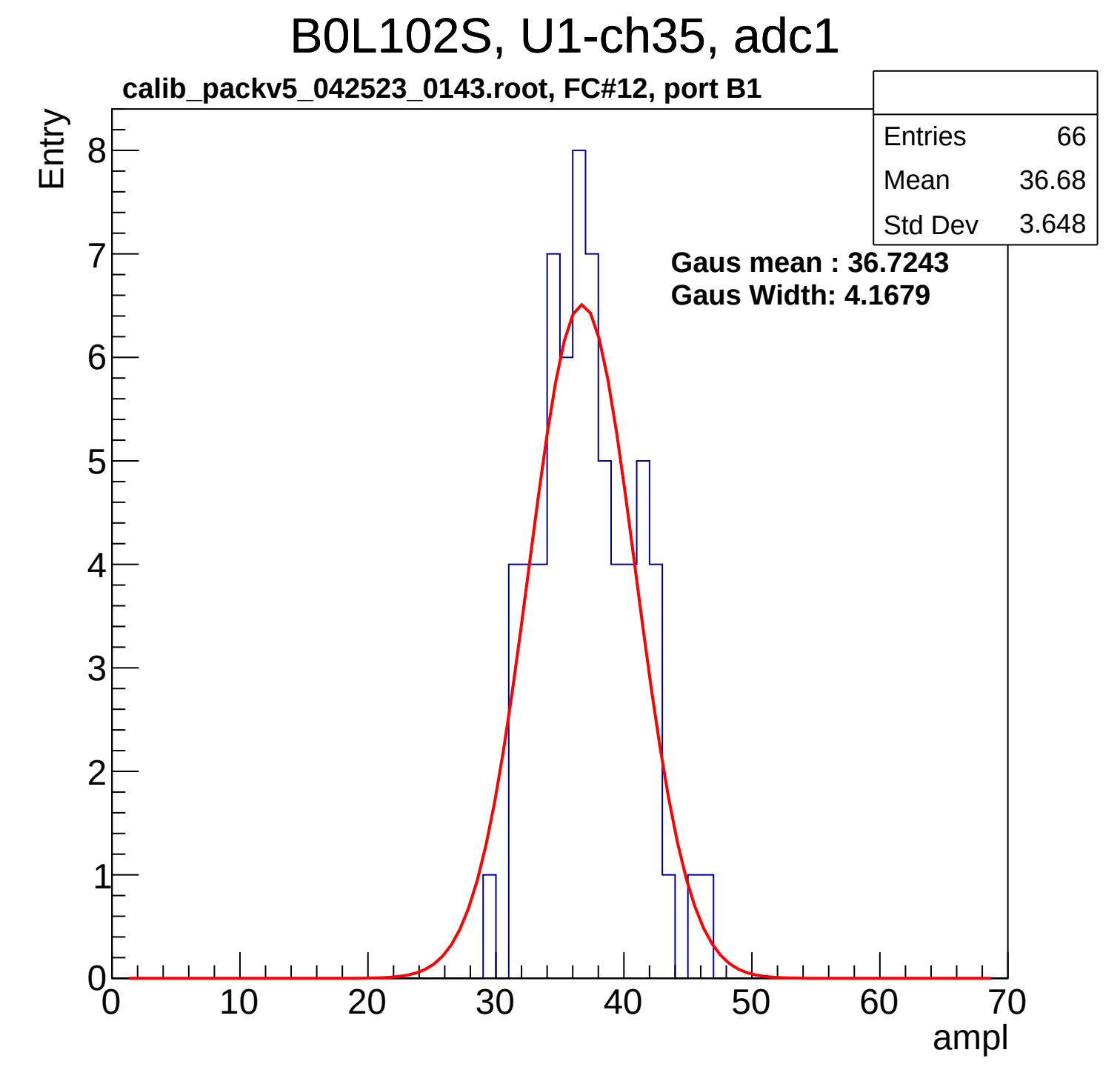
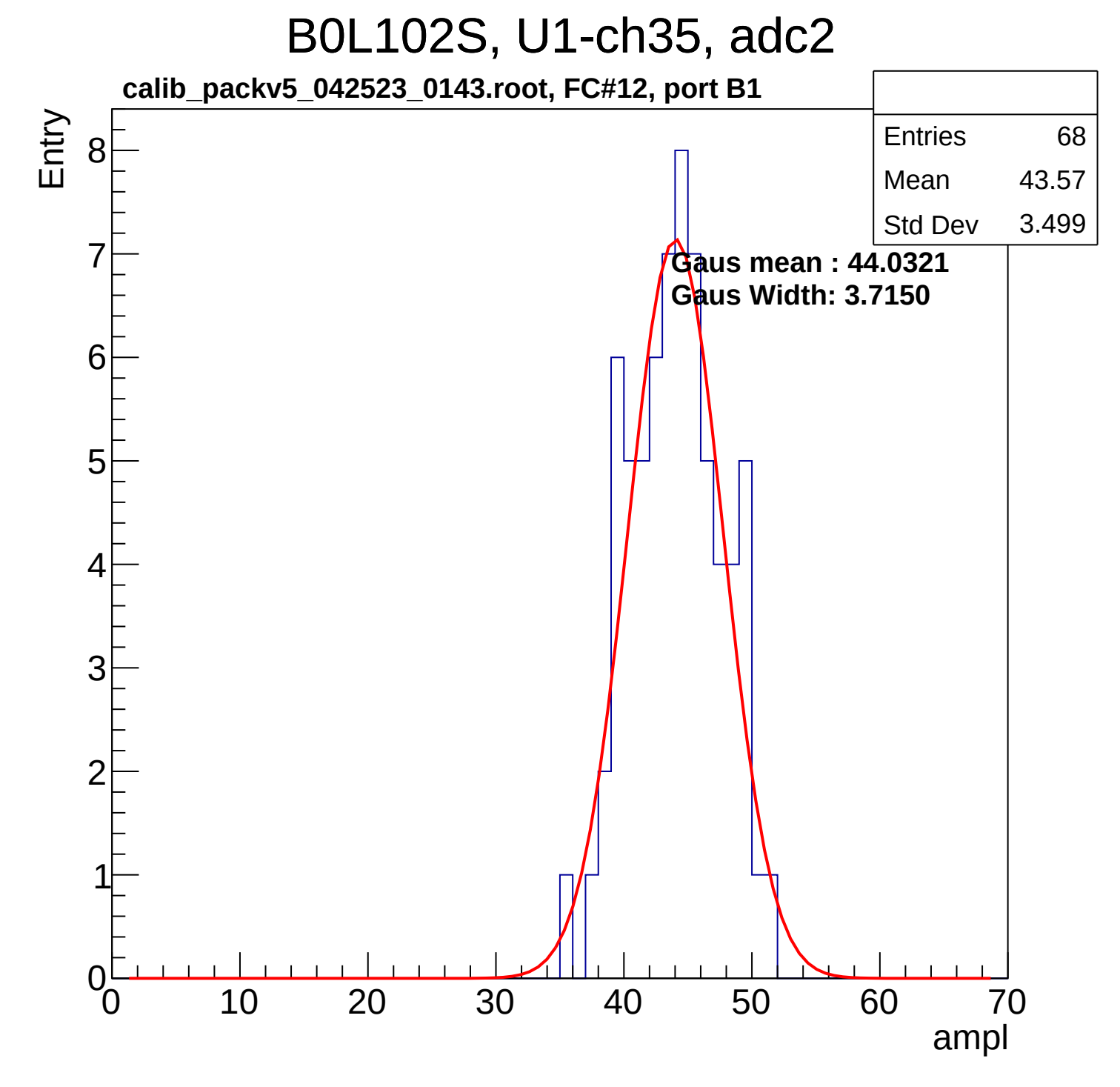
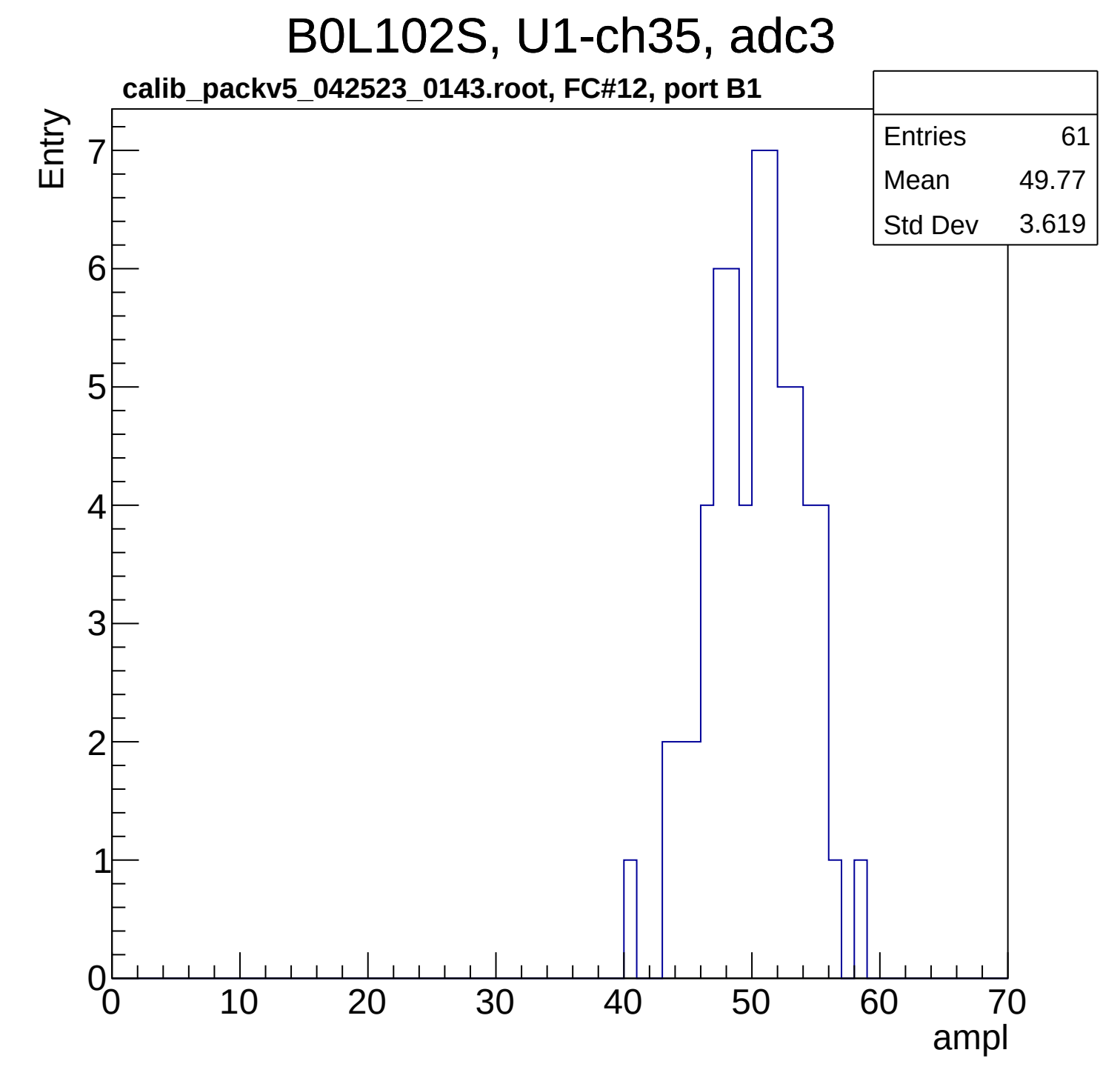
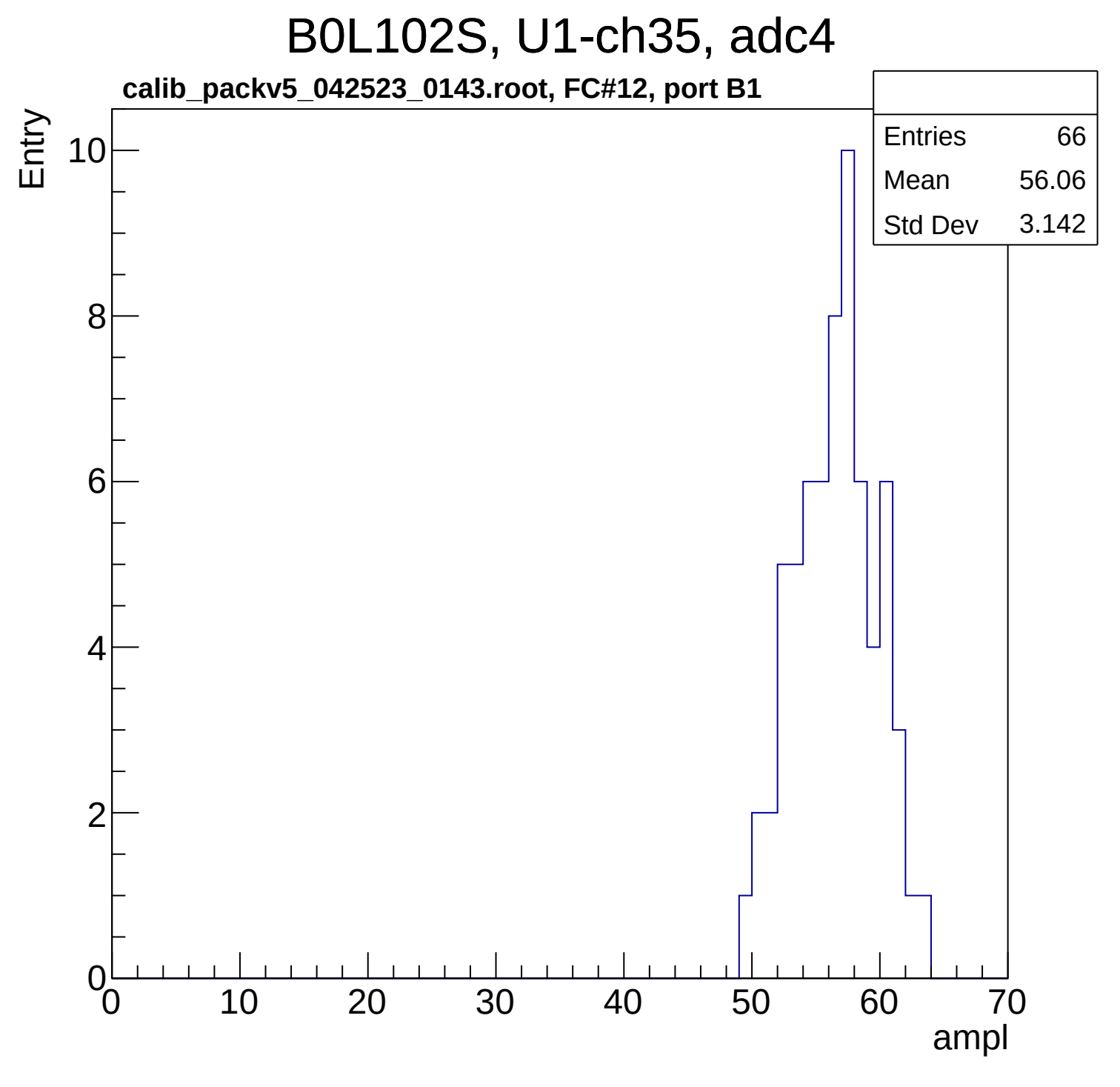
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

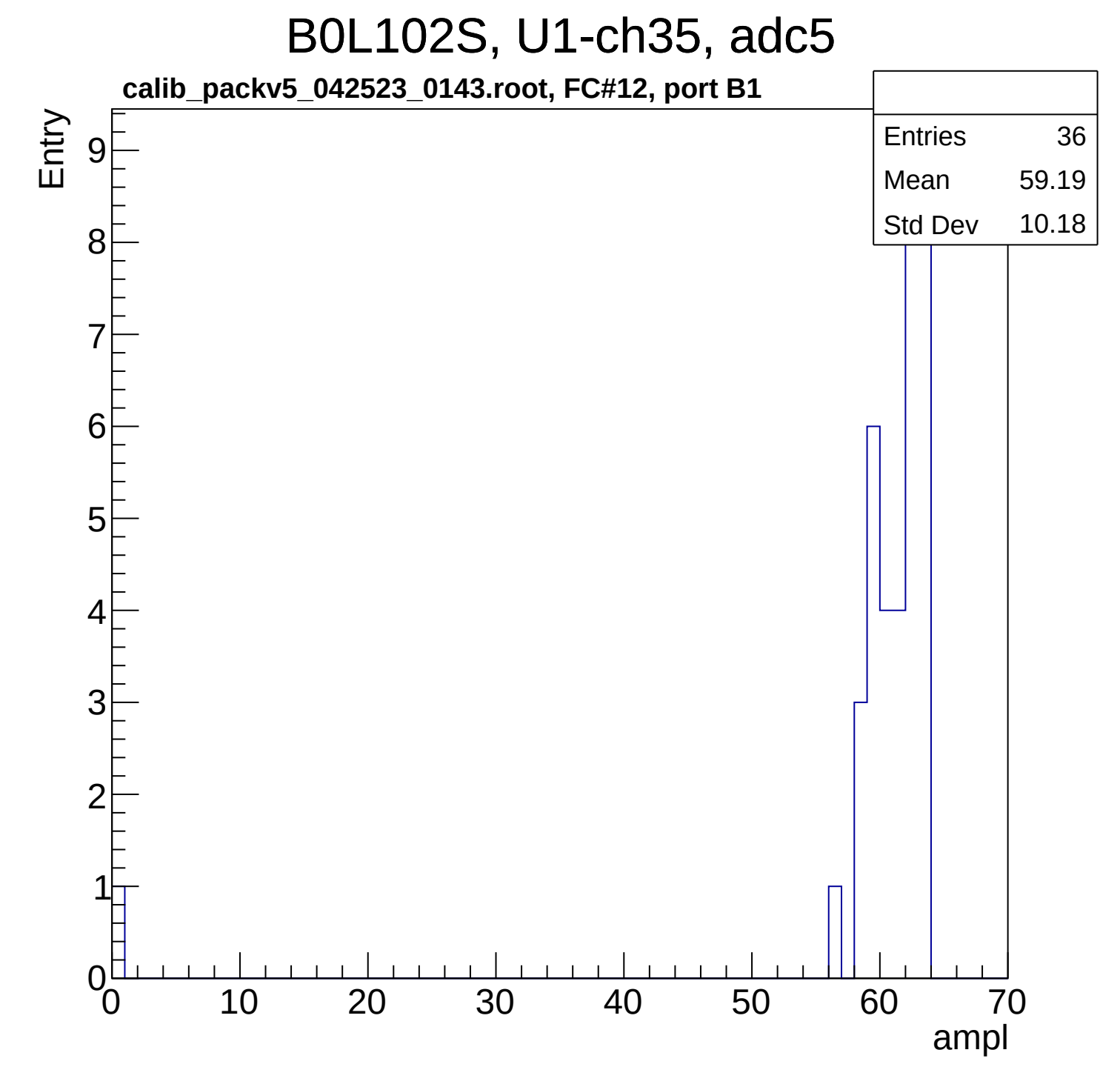

B0L102S, U1-ch35, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 29.23
Std Dev 5.955
Gaus mean : 30.3445
Gaus Width: 3.6586
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

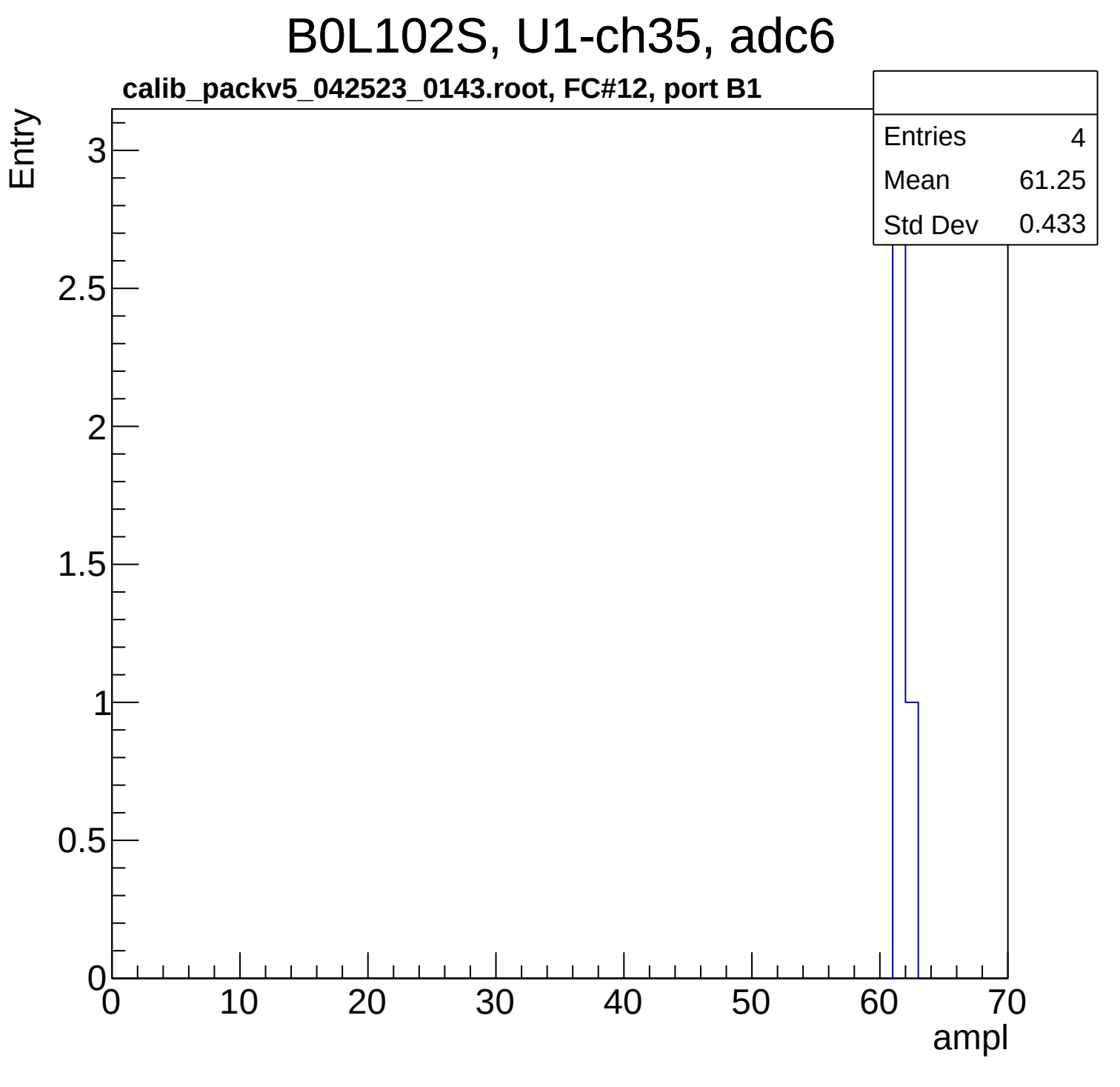

B0L102S, U1-ch35, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.68
Std Dev 3.648
Gaus mean : 36.7243
Gaus Width: 4.1679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch35, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 43.57
Std Dev 3.499
Gaus mean : 44.0321
Gaus Width: 3.7150
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch35, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 49.77
Std Dev 3.619
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch35, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 56.06
Std Dev 3.142
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch35, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 59.19
Std Dev 10.18
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch35, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.25
Std Dev 0.433
Entry
ampl
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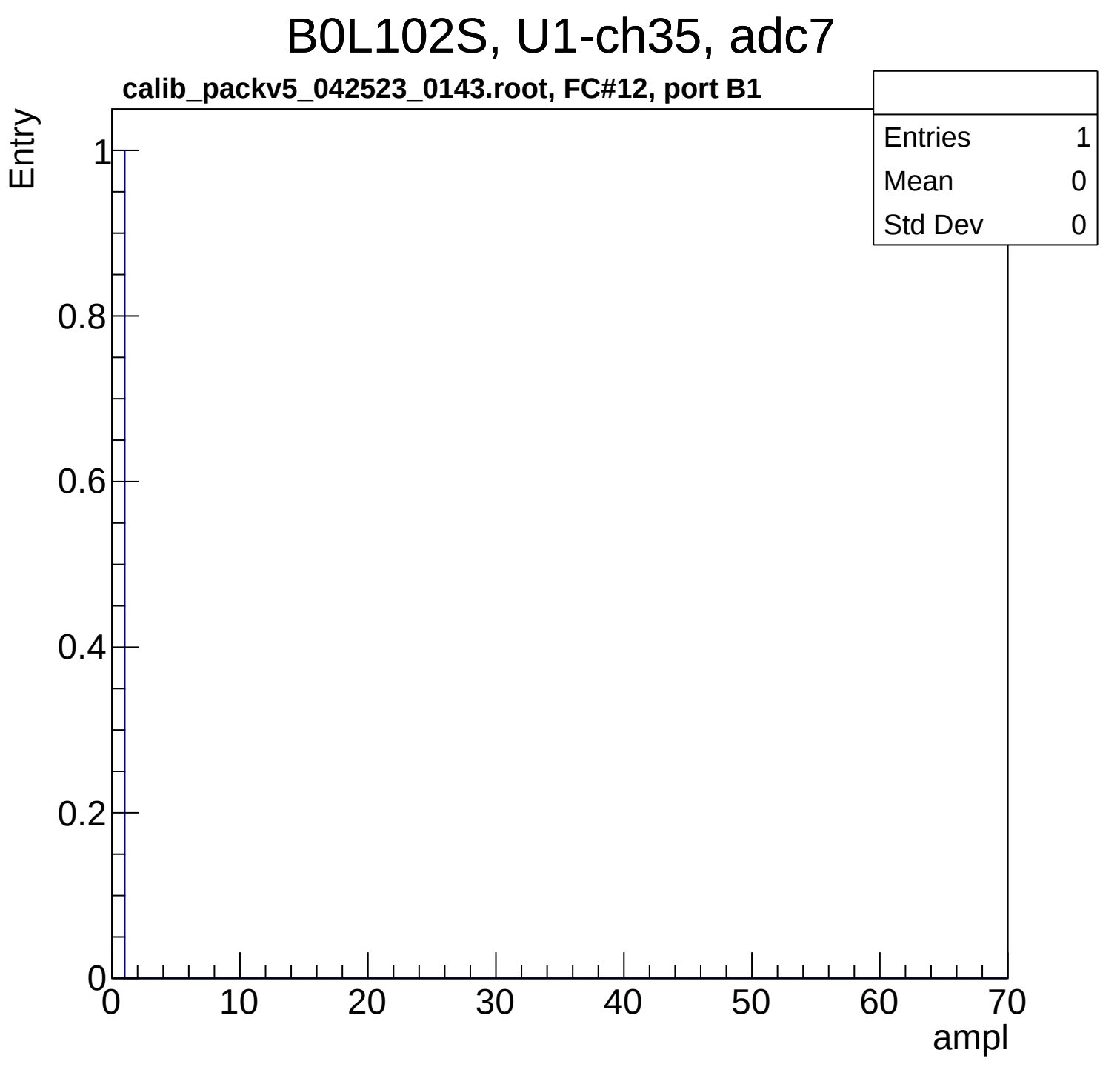
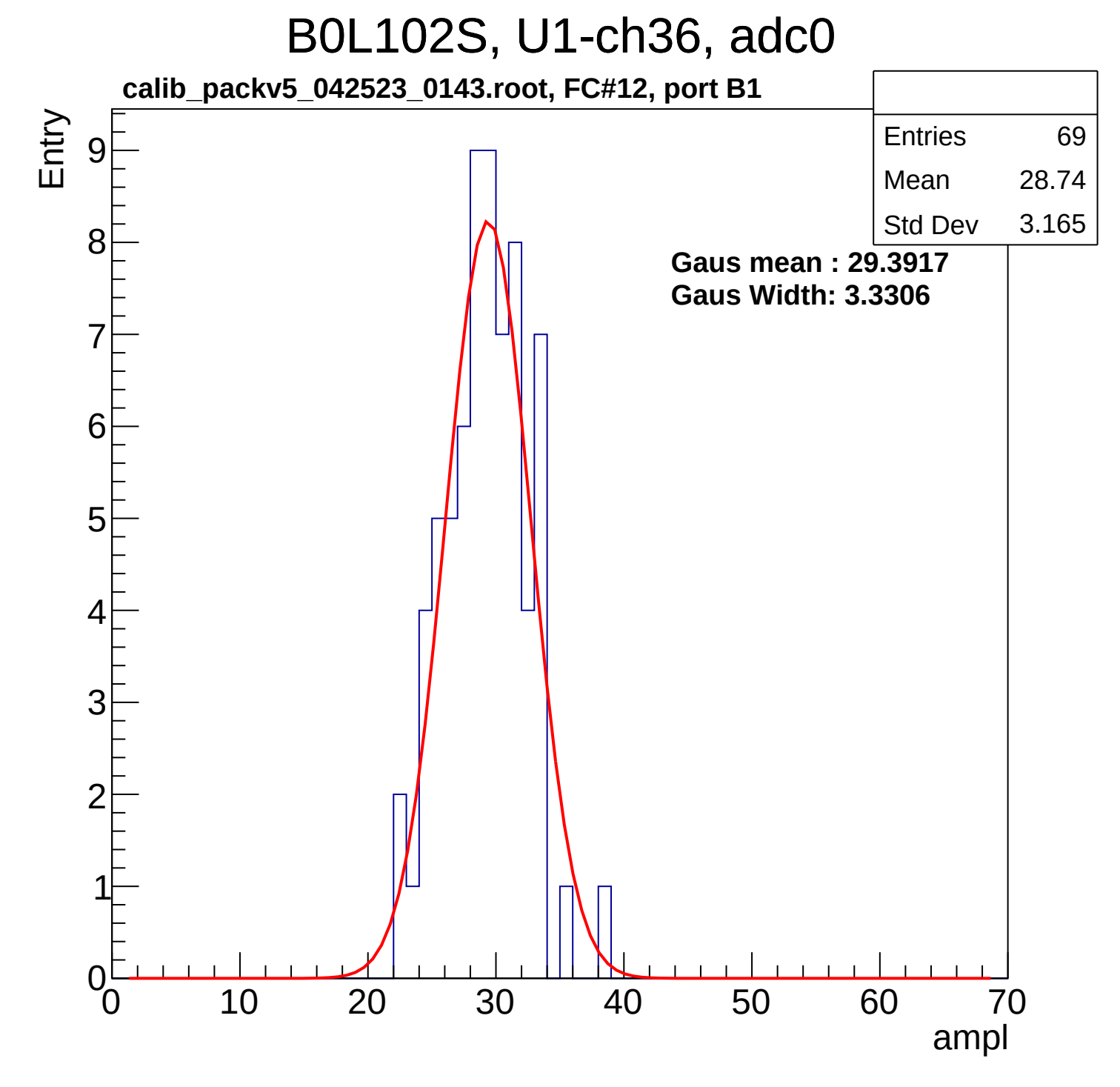
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch35, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch36, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 28.74
Std Dev 3.165
Gaus mean : 29.3917
Gaus Width: 3.3306
Entry
ampl
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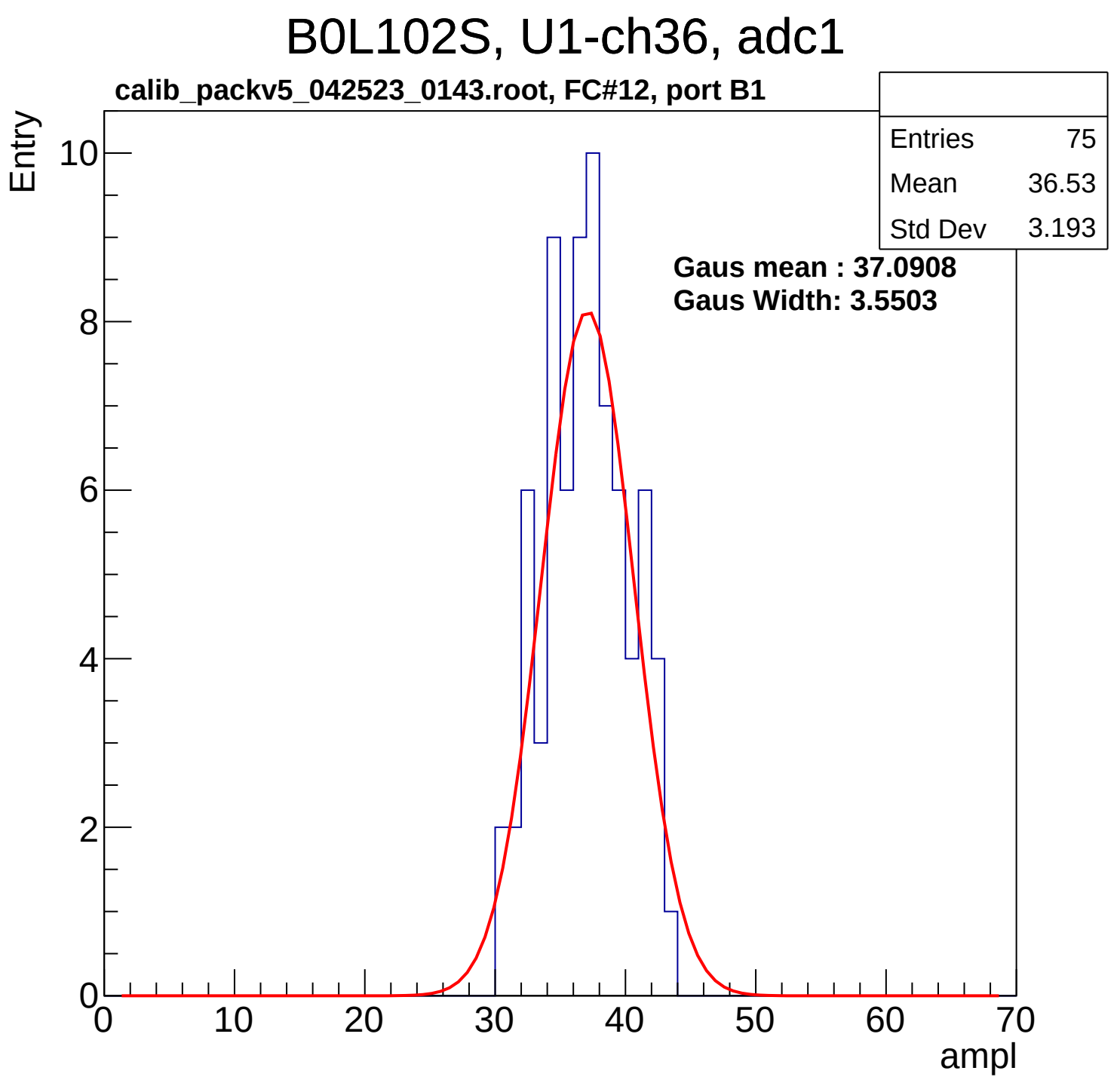
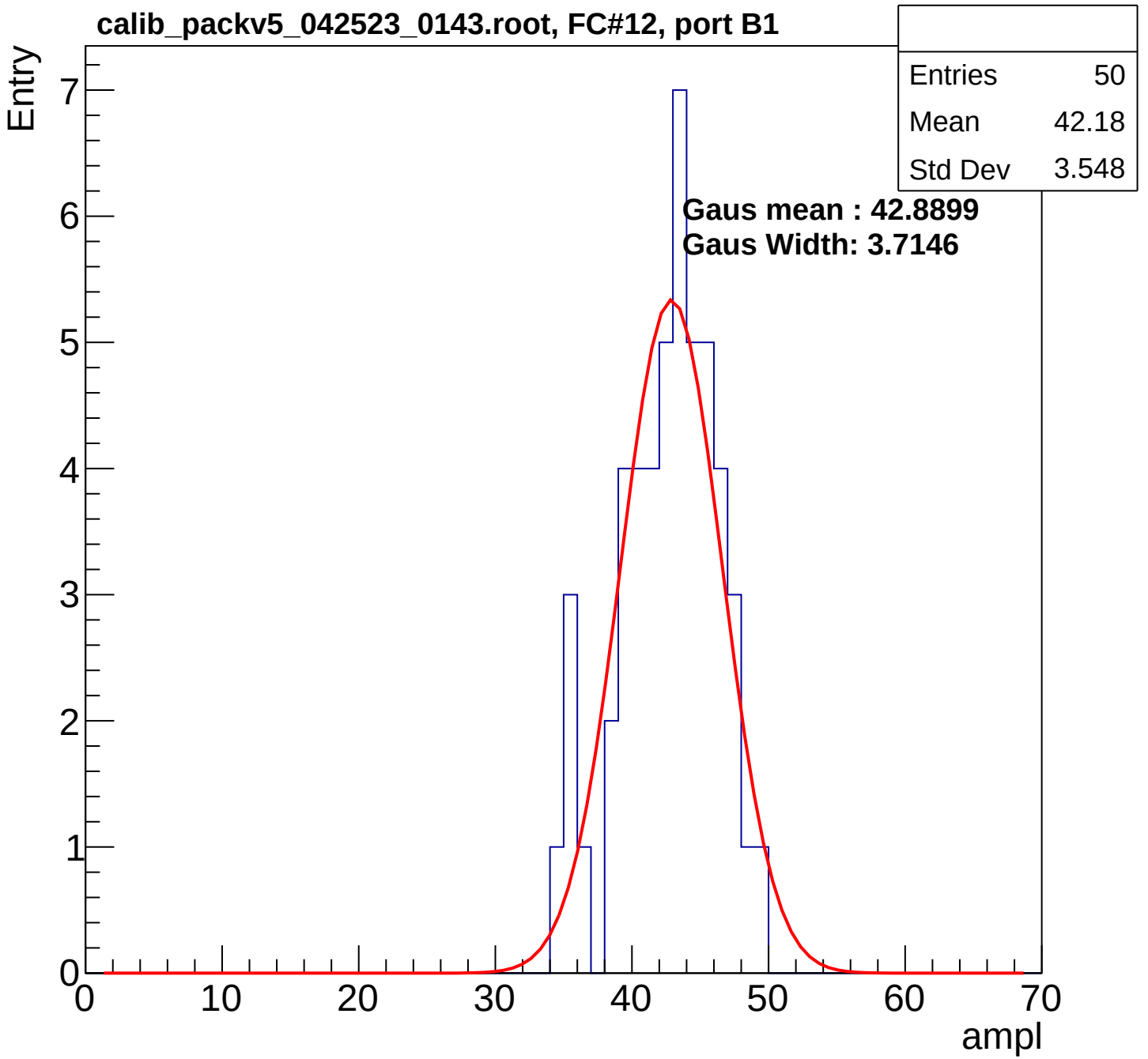
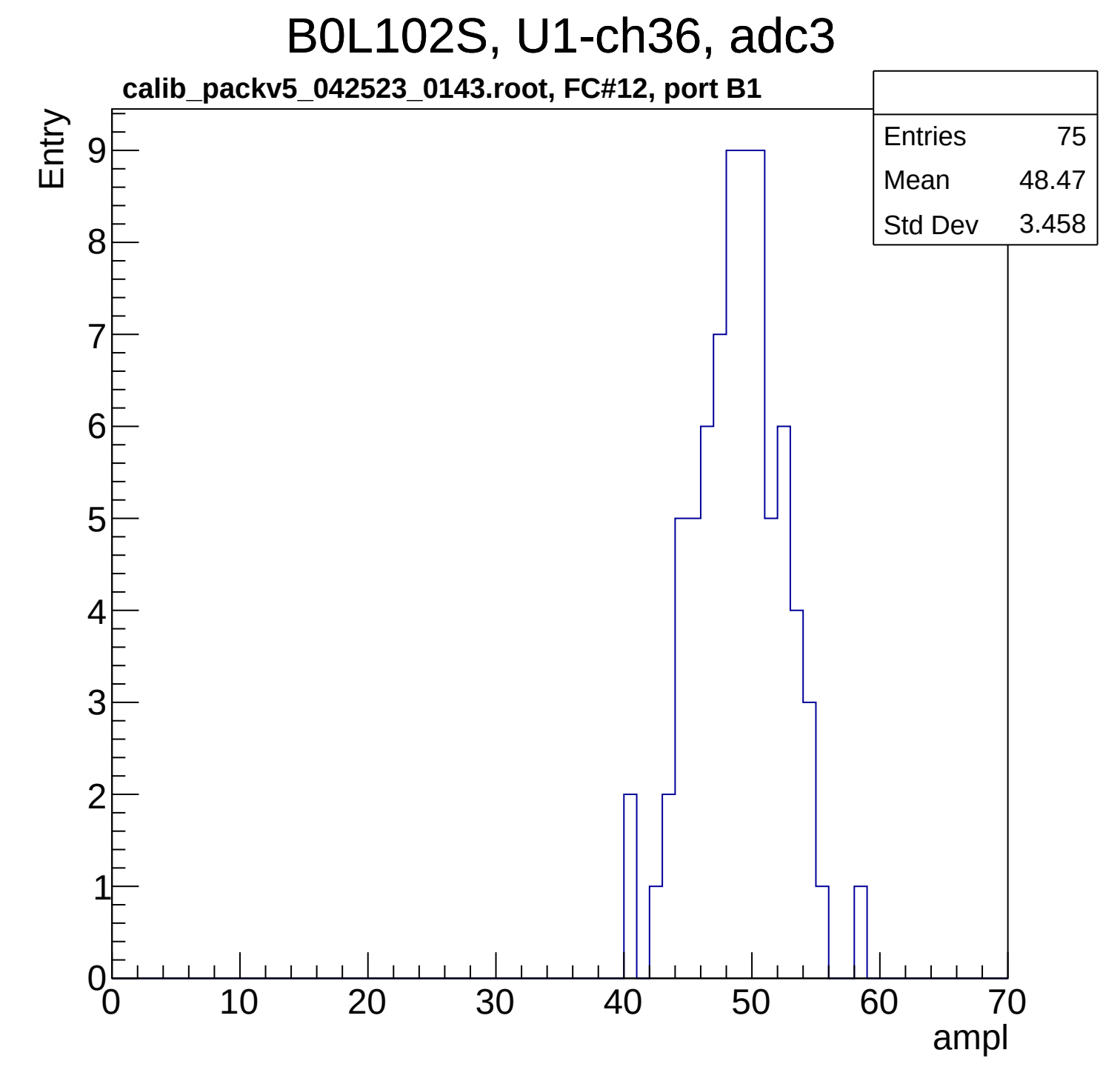
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch36, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.53
Std Dev 3.193
Gaus mean : 37.0908
Gaus Width: 3.5503
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch36, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 42.18
Std Dev 3.548
Gaus mean : 42.8899
Gaus Width: 3.7146
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch36, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 48.47
Std Dev 3.458
Entry
ampl
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2
3
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6
7
8
9
0
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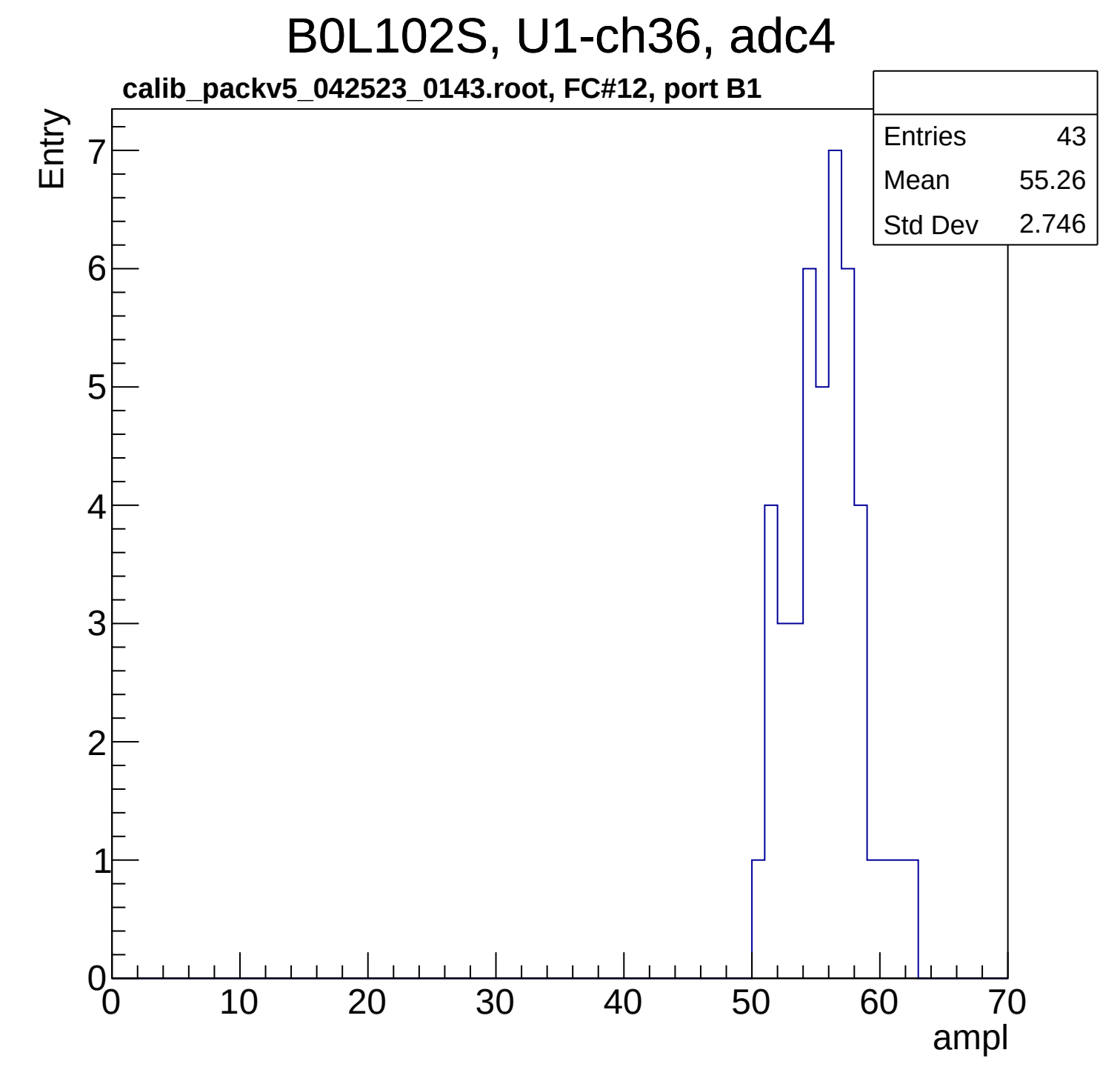
B0L102S, U1-ch36, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.26
Std Dev 2.746
Entry
ampl
0
1
2
3
4
5
6
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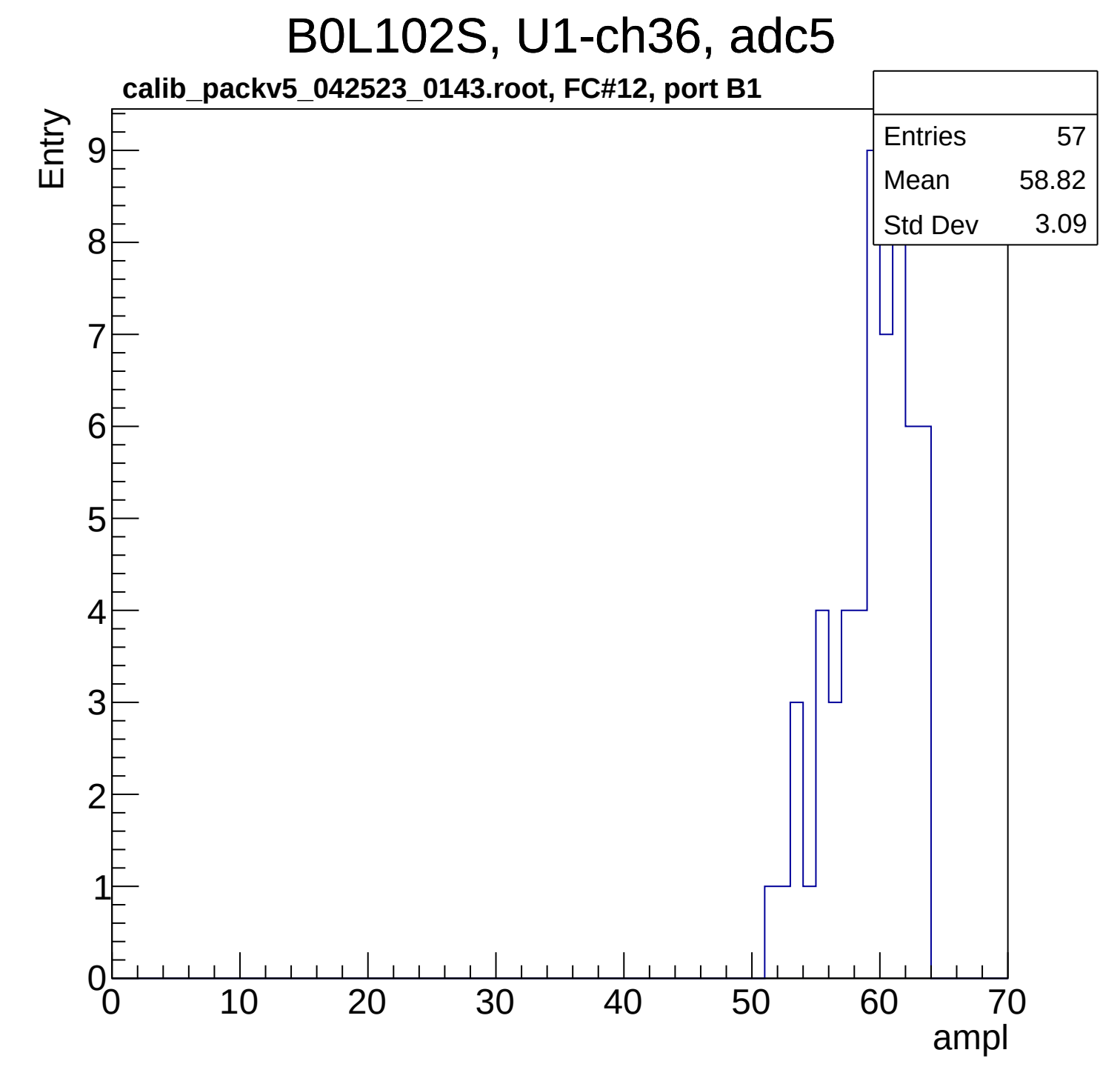
0
10
20
30
40
50
60
70


B0L102S, U1-ch36, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 58.82
Std Dev 3.09
Entry
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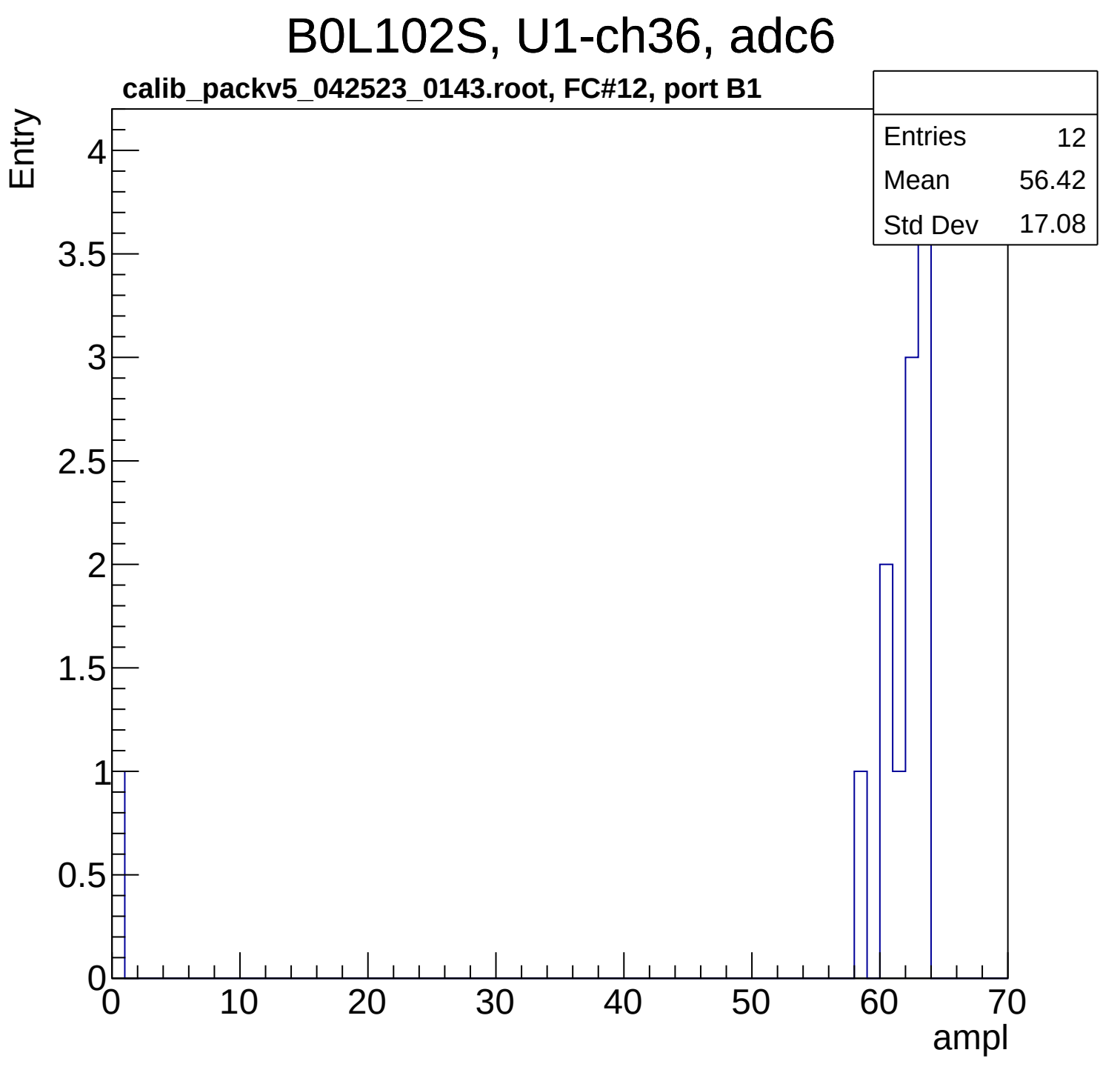
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch36, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 56.42
Std Dev 17.08
Entry
ampl
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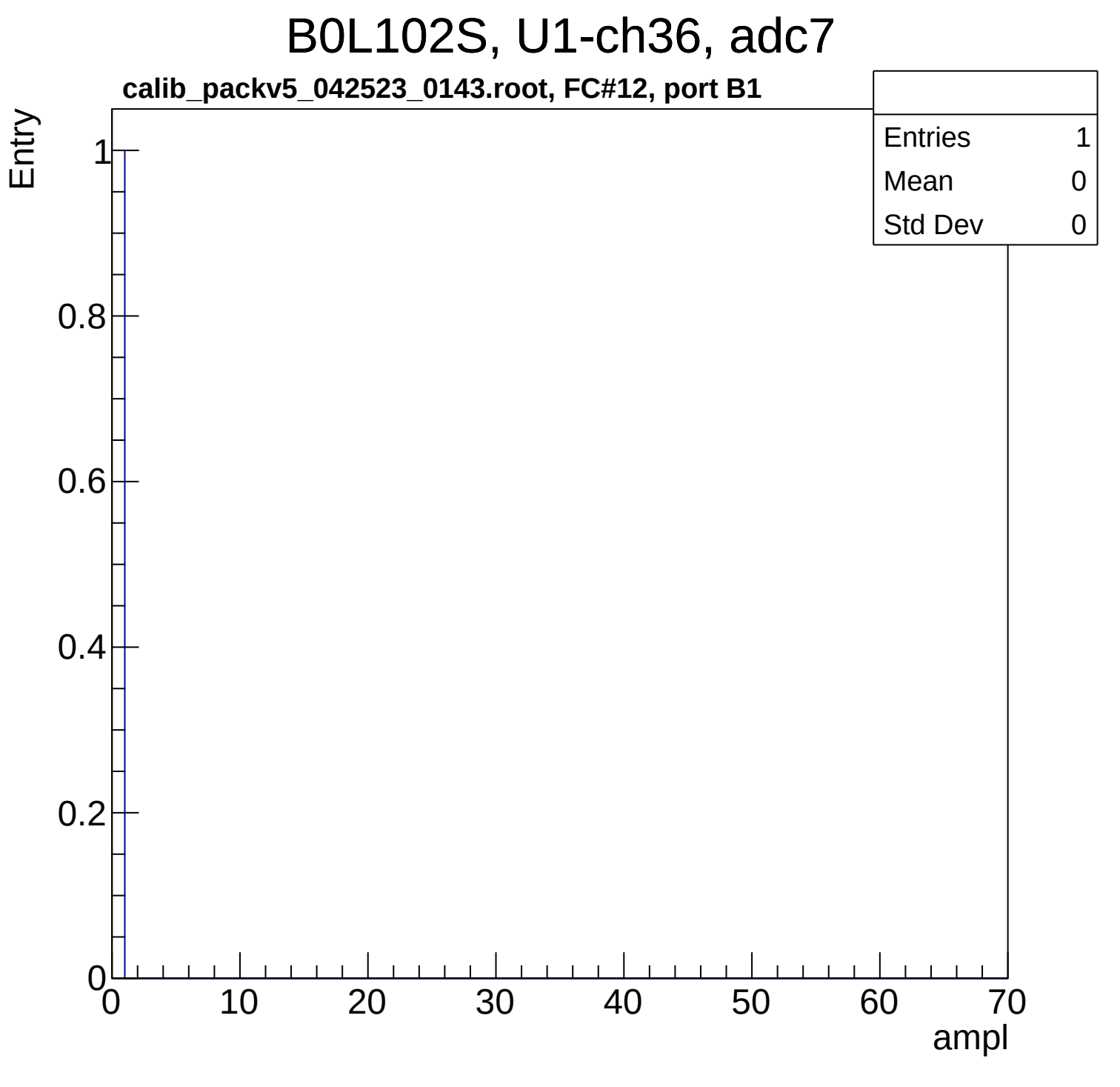
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch36, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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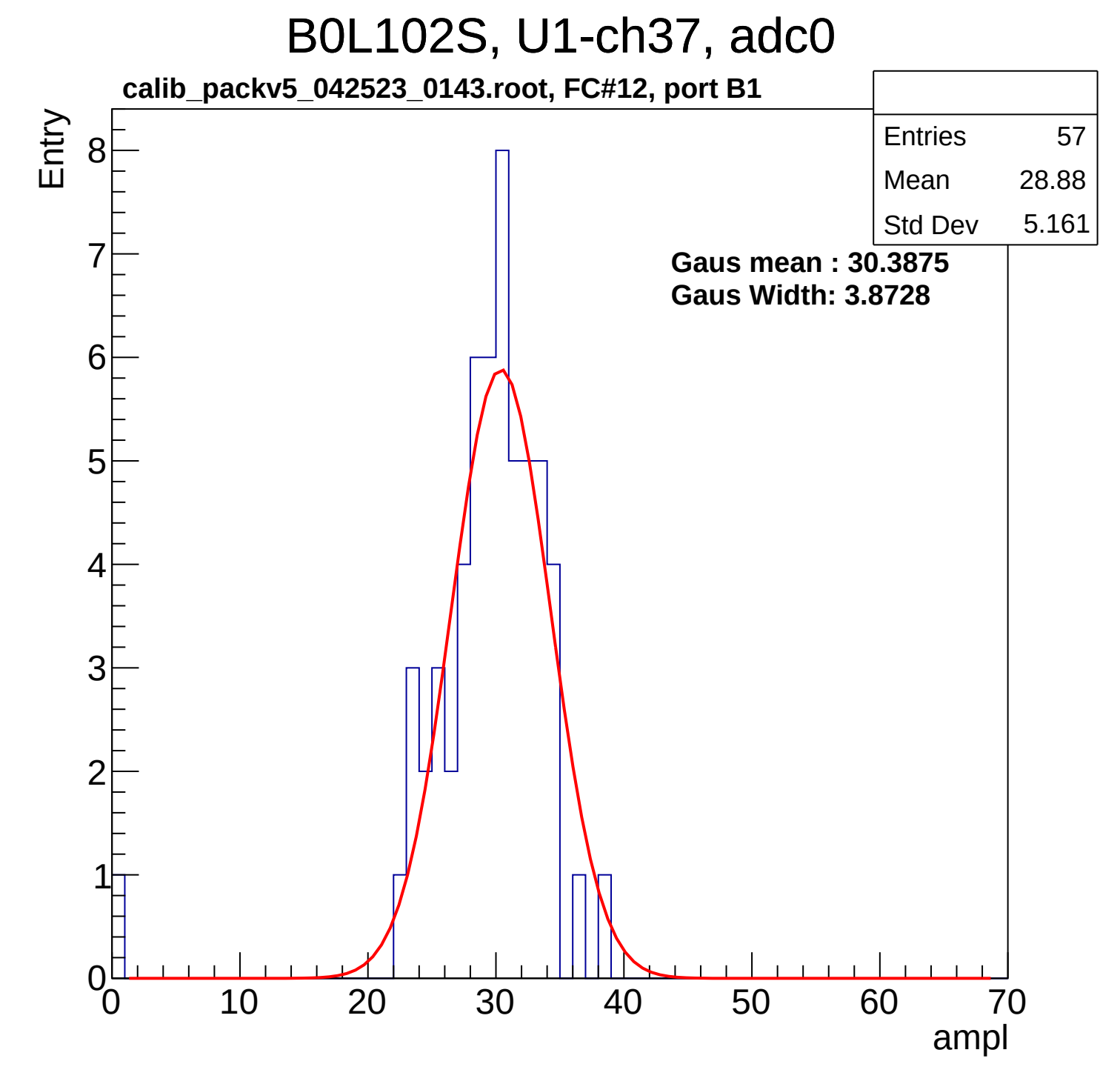
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch37, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 28.88
Std Dev 5.161
Gaus mean : 30.3875
Gaus Width: 3.8728
Entry
ampl
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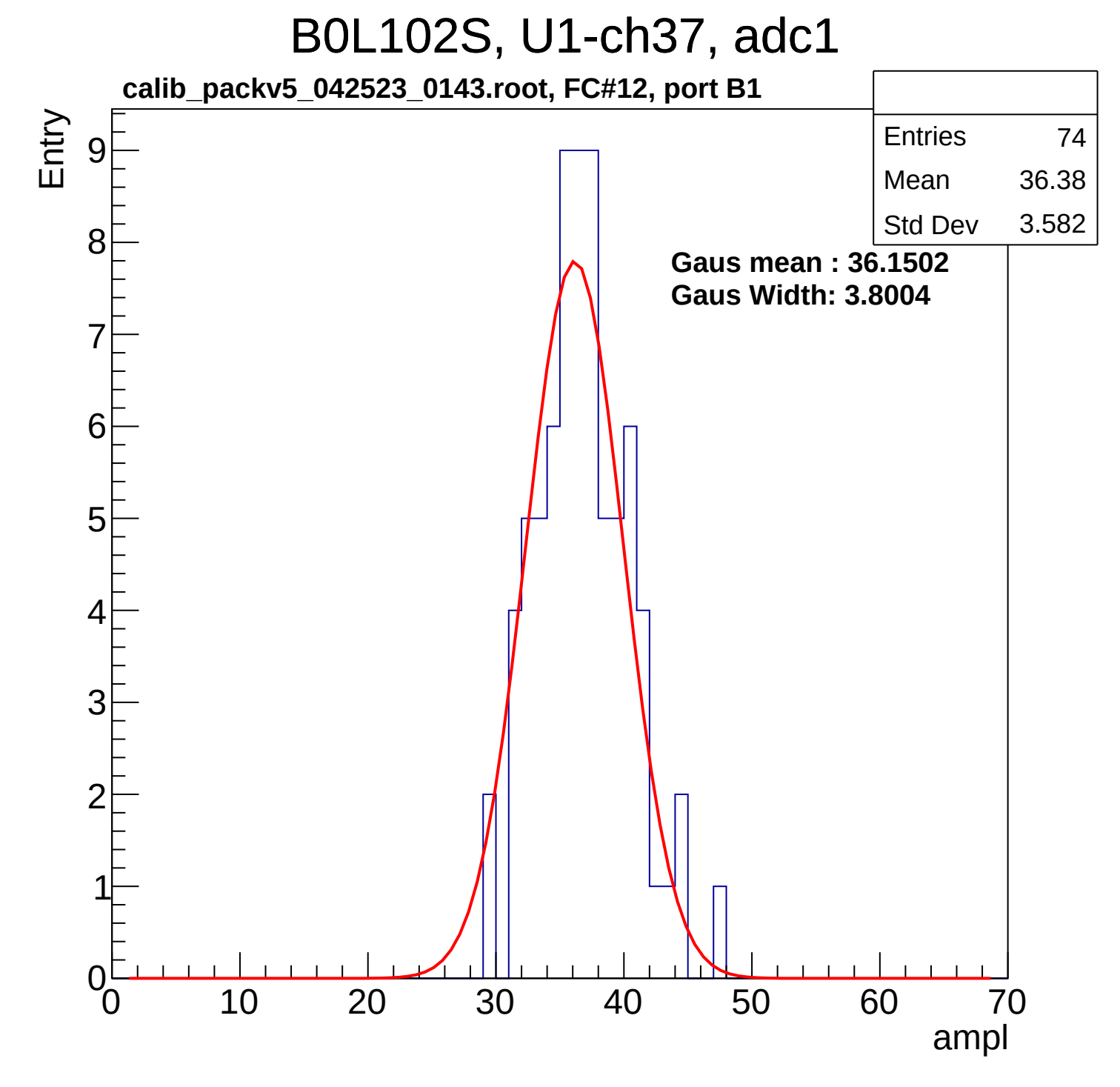
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch37, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.38
Std Dev 3.582
Gaus mean : 36.1502
Gaus Width: 3.8004
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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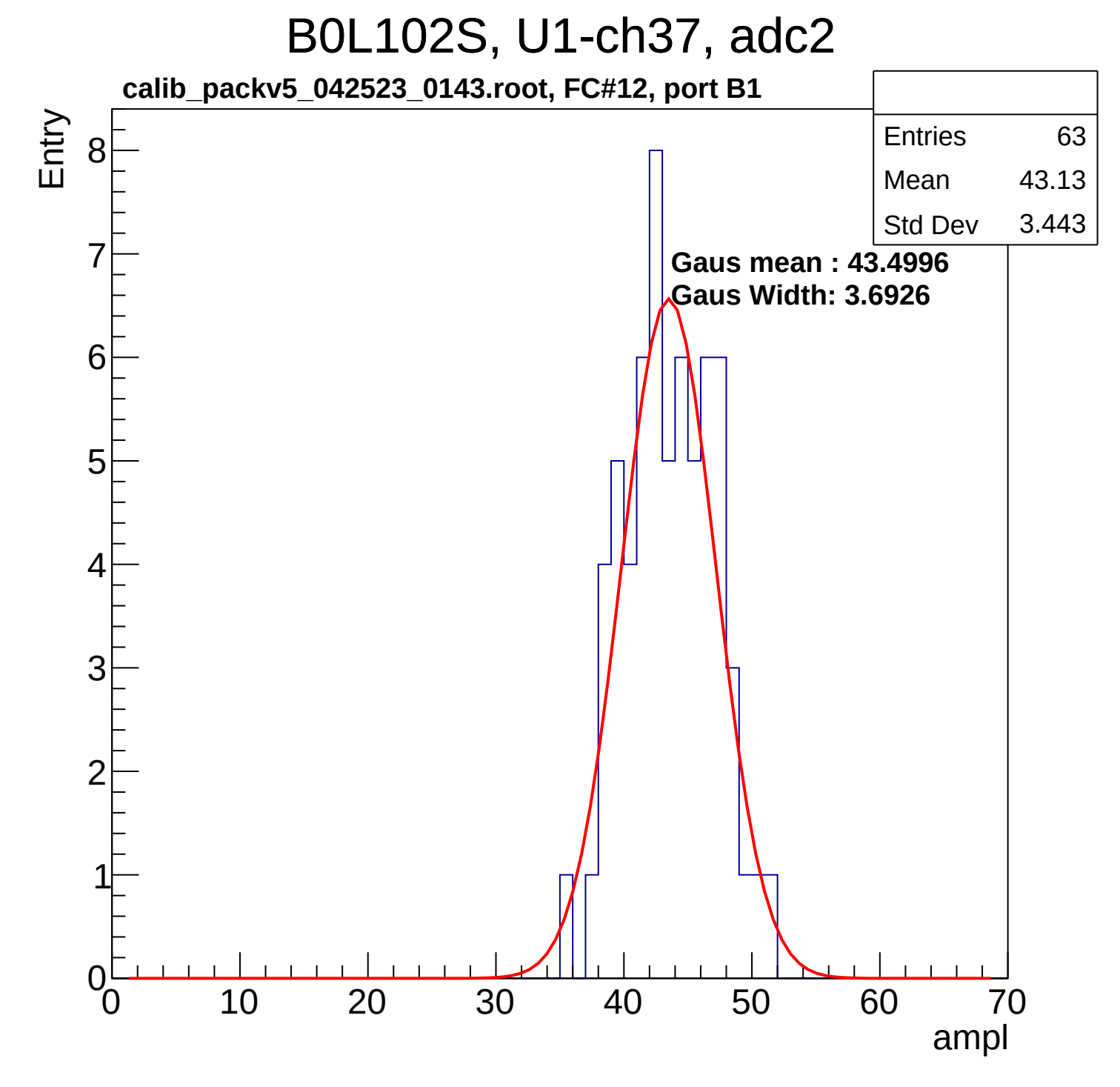
60
70


B0L102S, U1-ch37, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 43.13
Std Dev 3.443
Gaus mean : 43.4996
Gaus Width: 3.6926
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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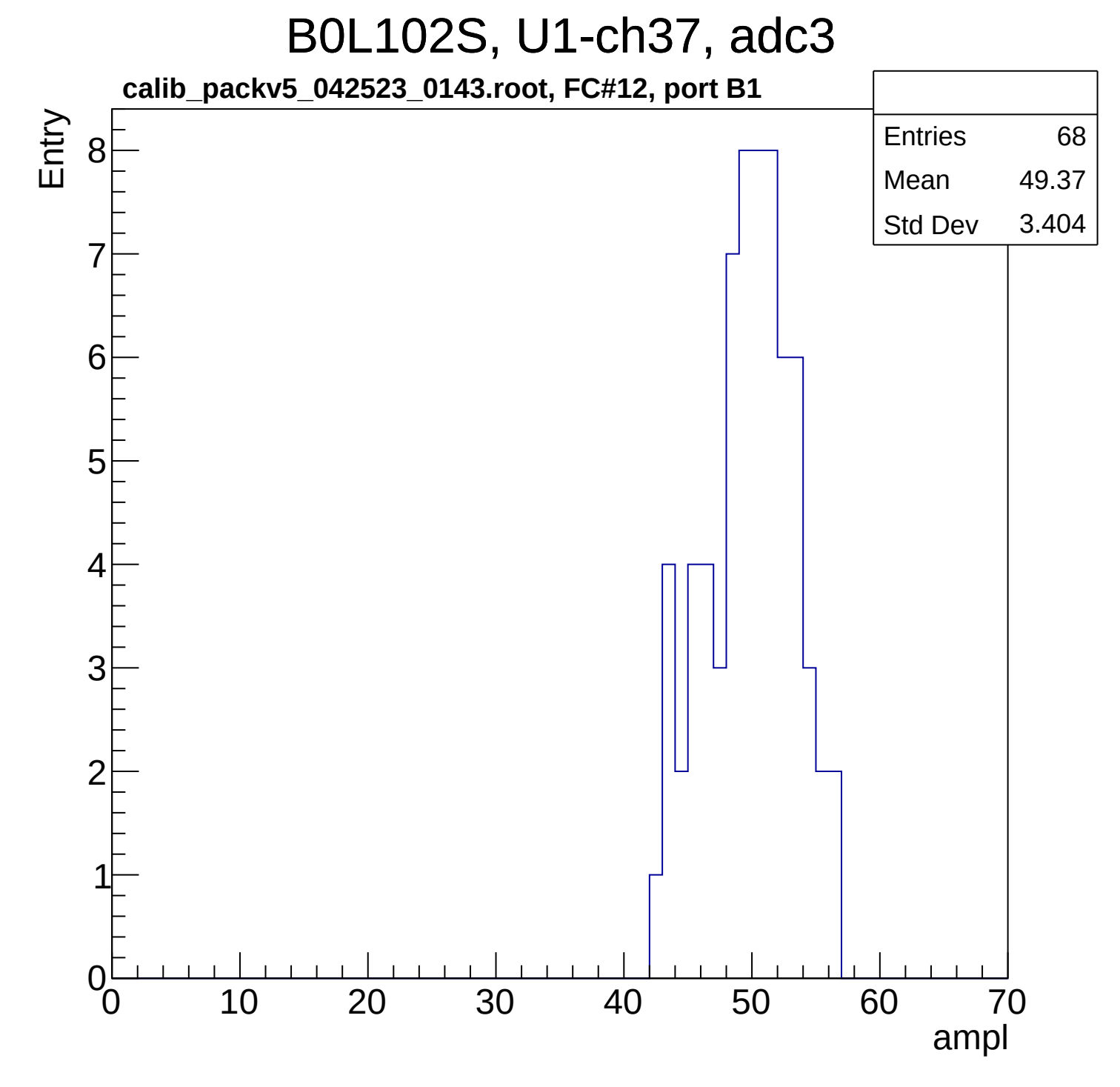
40
50
60
70


B0L102S, U1-ch37, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.37
Std Dev 3.404
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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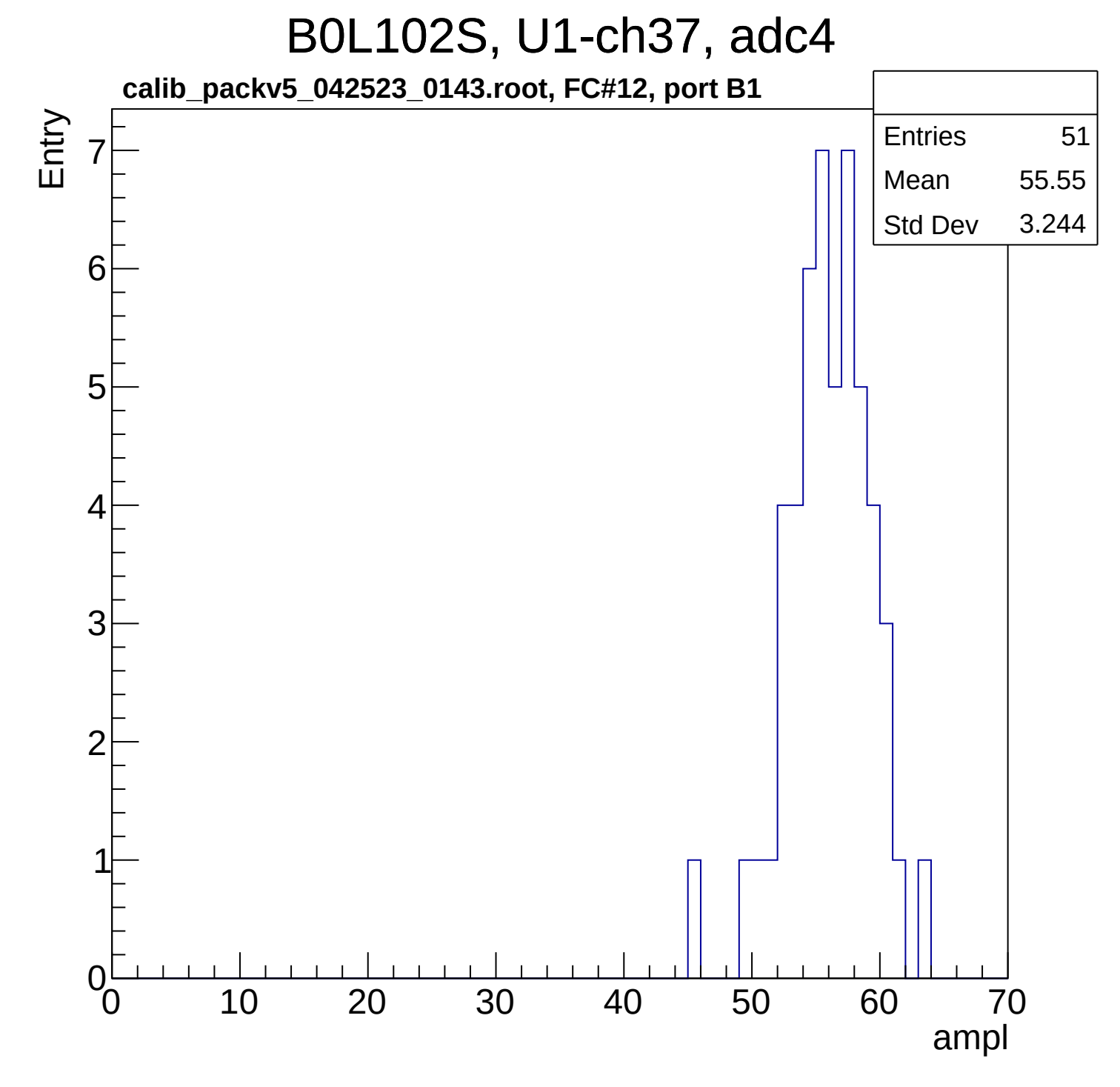
B0L102S, U1-ch37, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 55.55
Std Dev 3.244
Entry
ampl
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10
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40
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60
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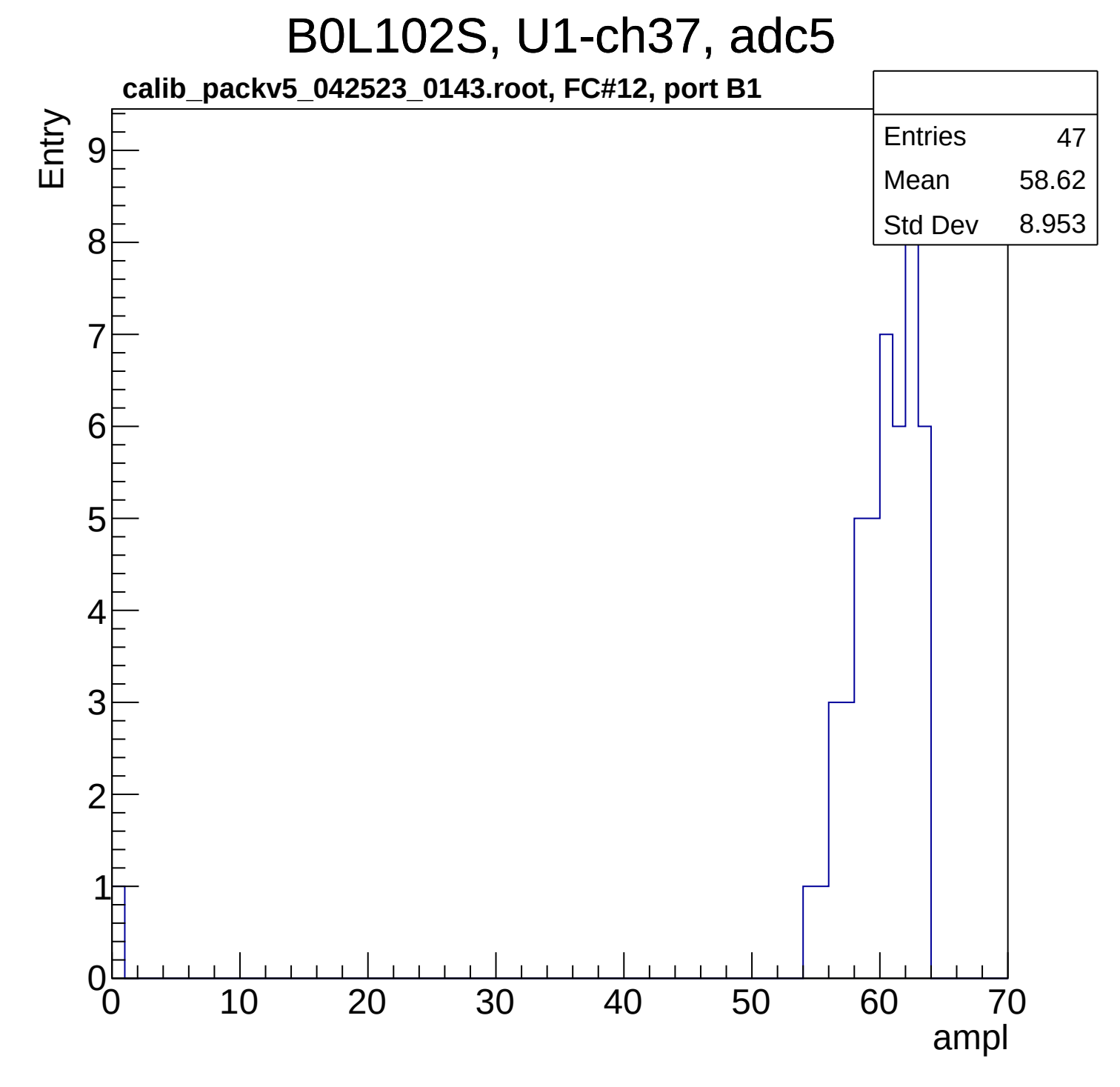
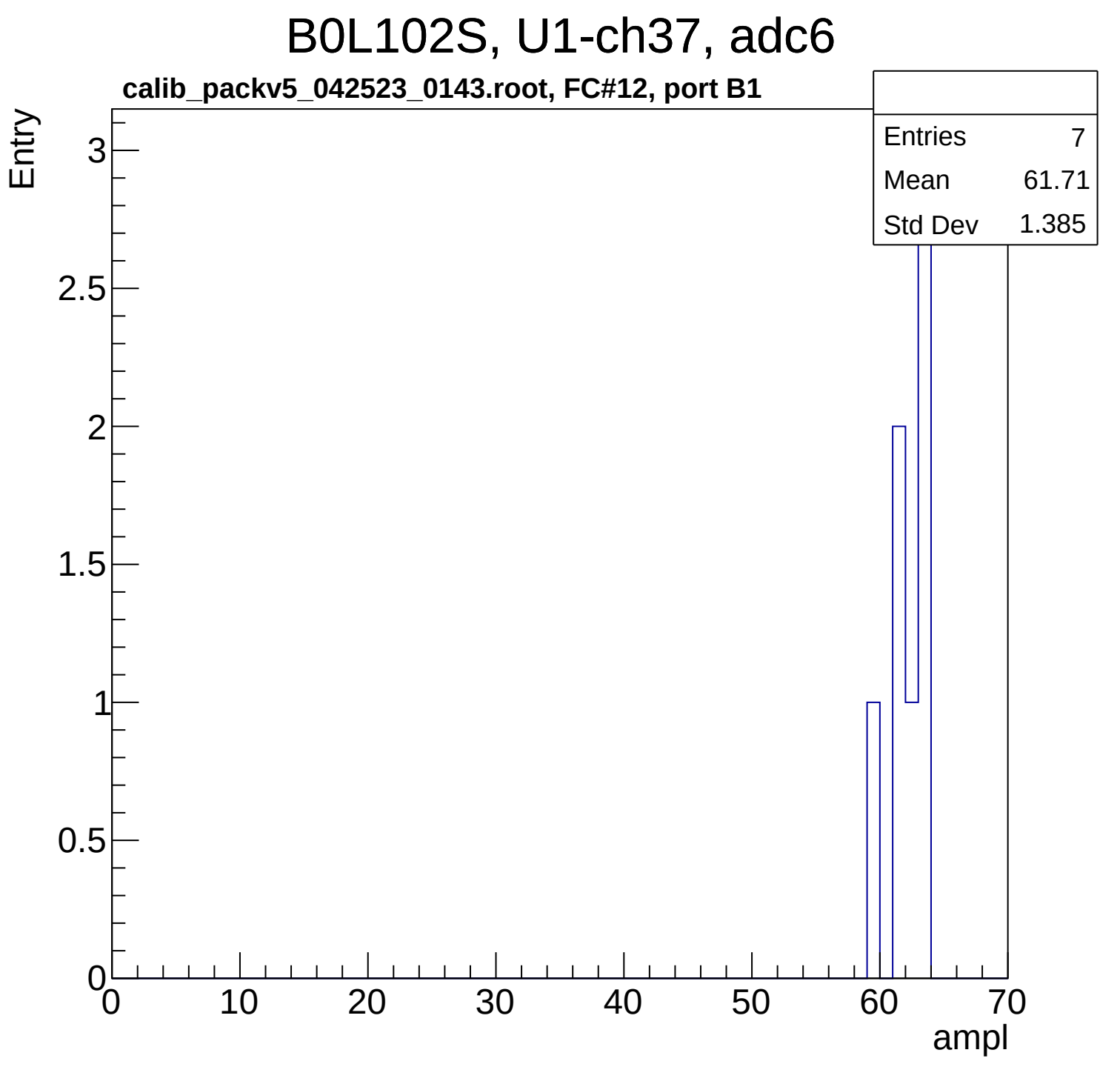
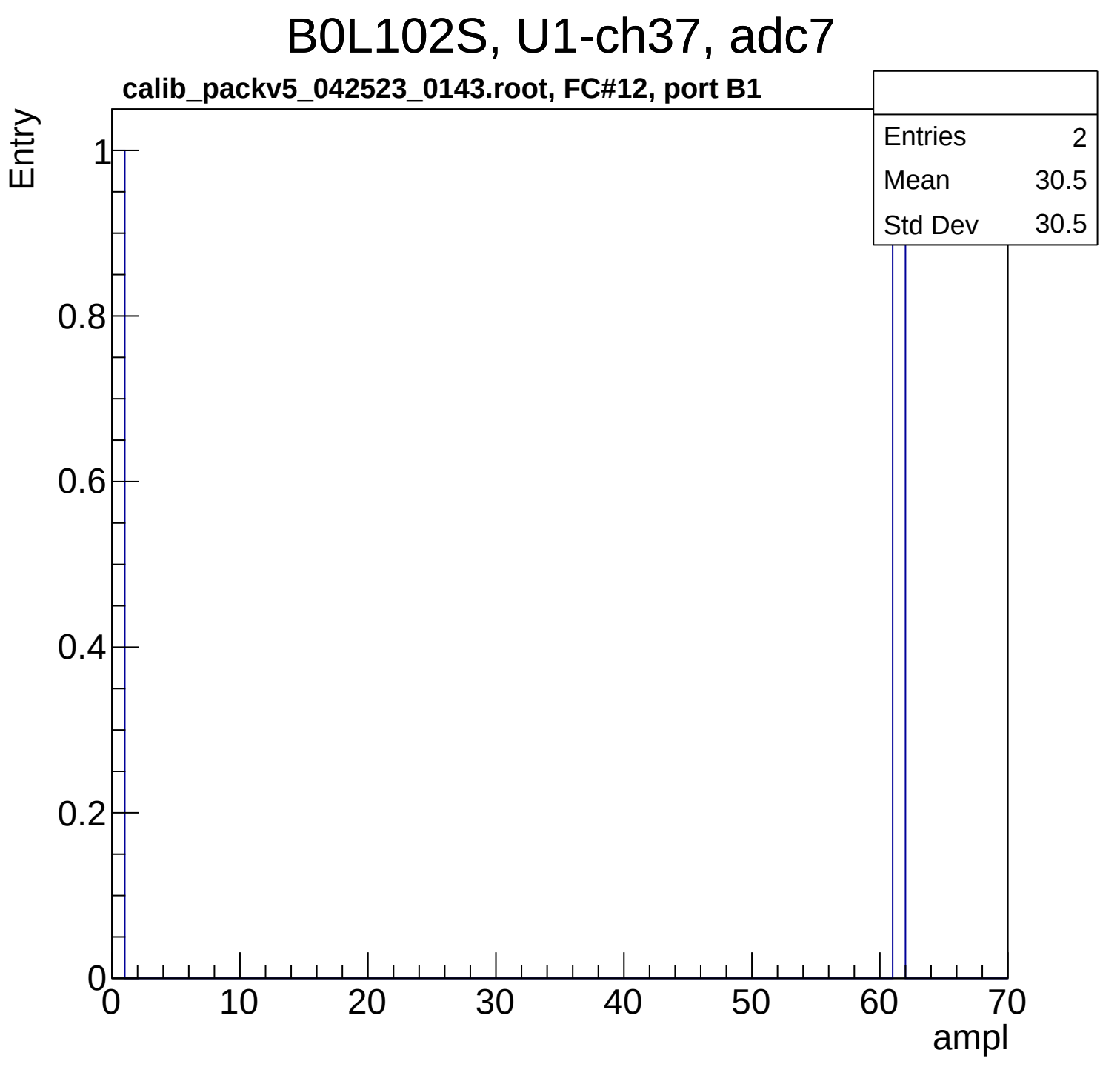
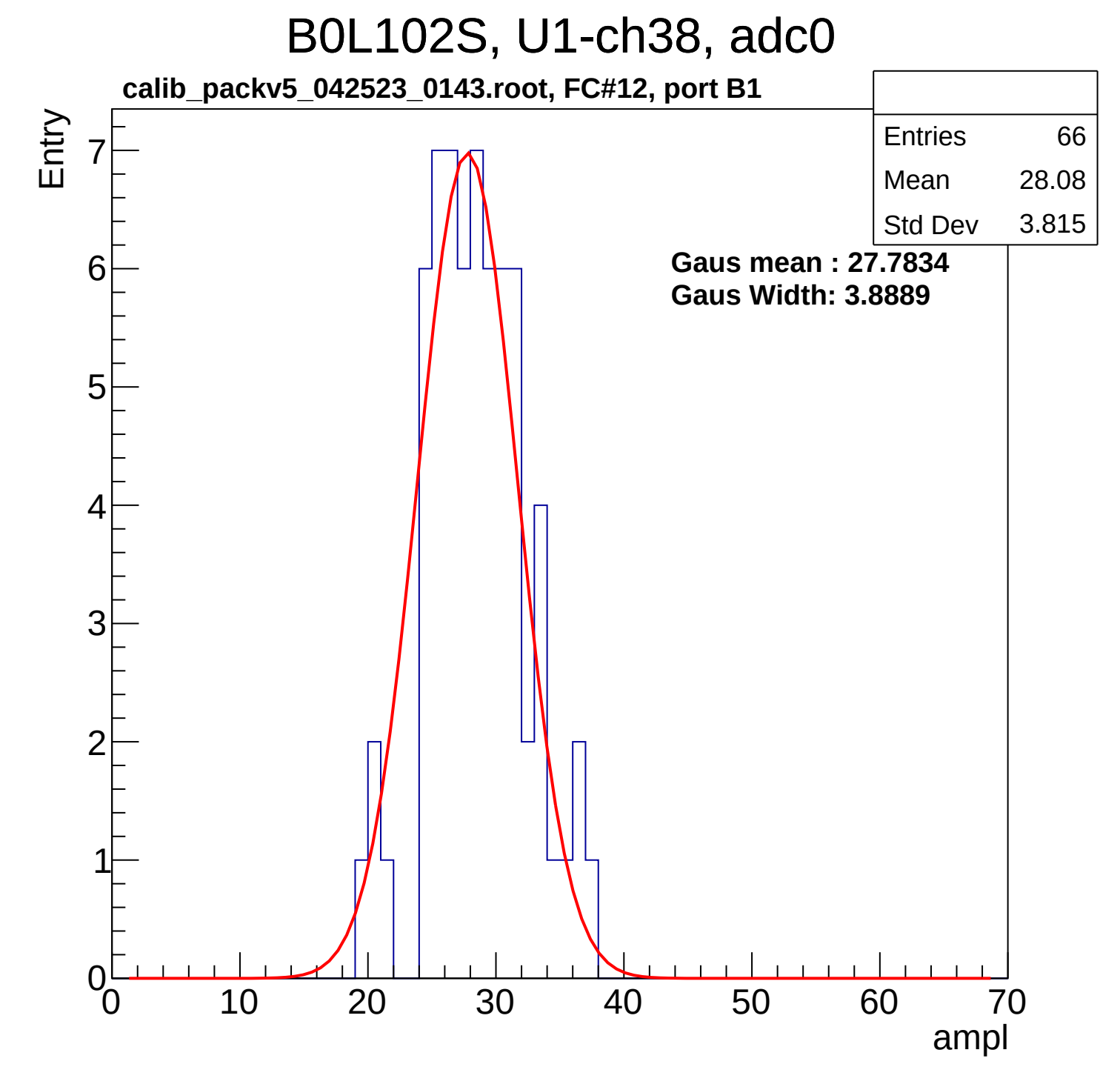
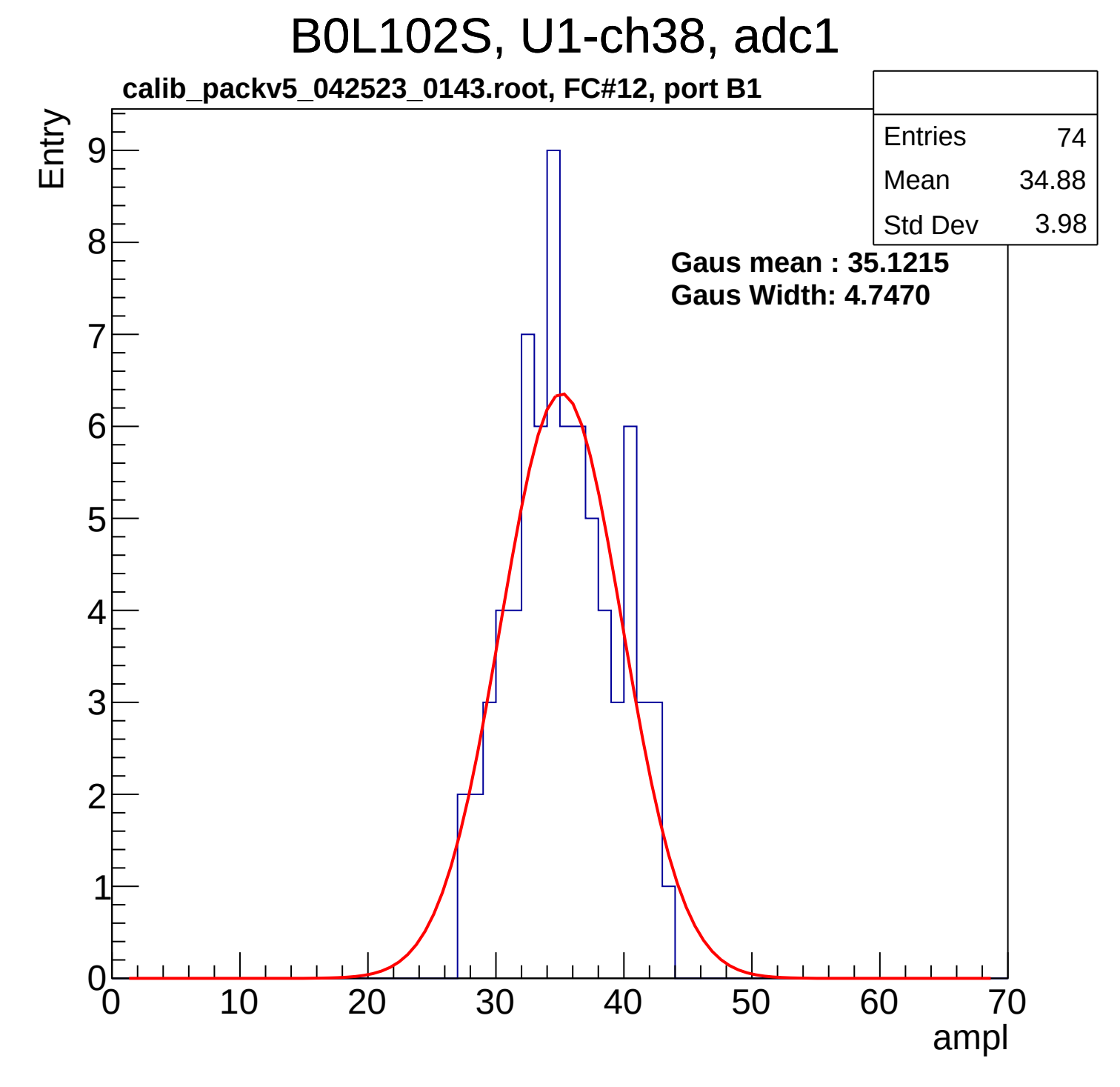

B0L102S, U1-ch37, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.62
Std Dev 8.953
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch37, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.71
Std Dev 1.385
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch37, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch38, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 28.08
Std Dev 3.815
Gaus mean : 27.7834
Gaus Width: 3.8889
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch38, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 34.88
Std Dev 3.98
Gaus mean : 35.1215
Gaus Width: 4.7470
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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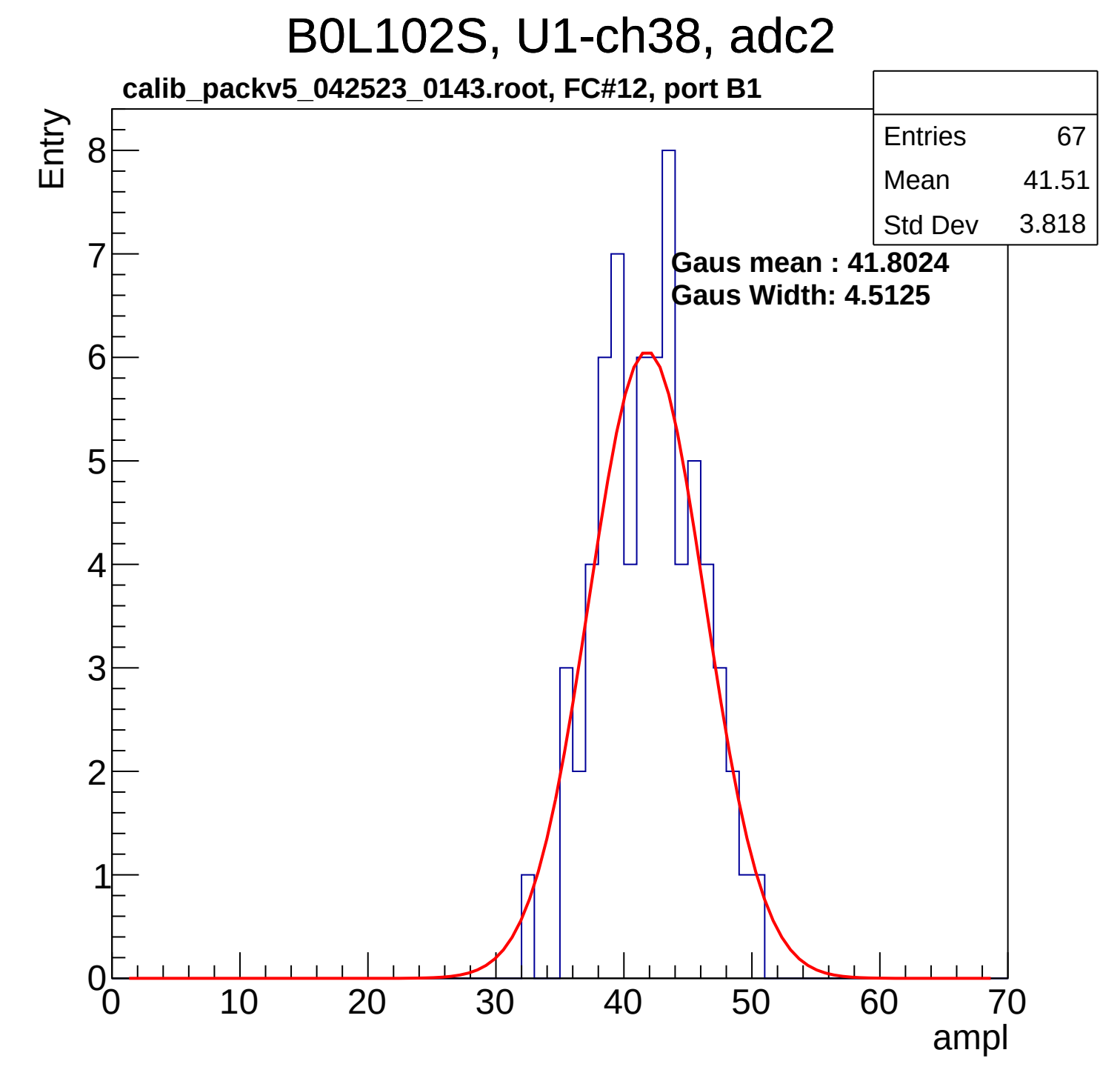
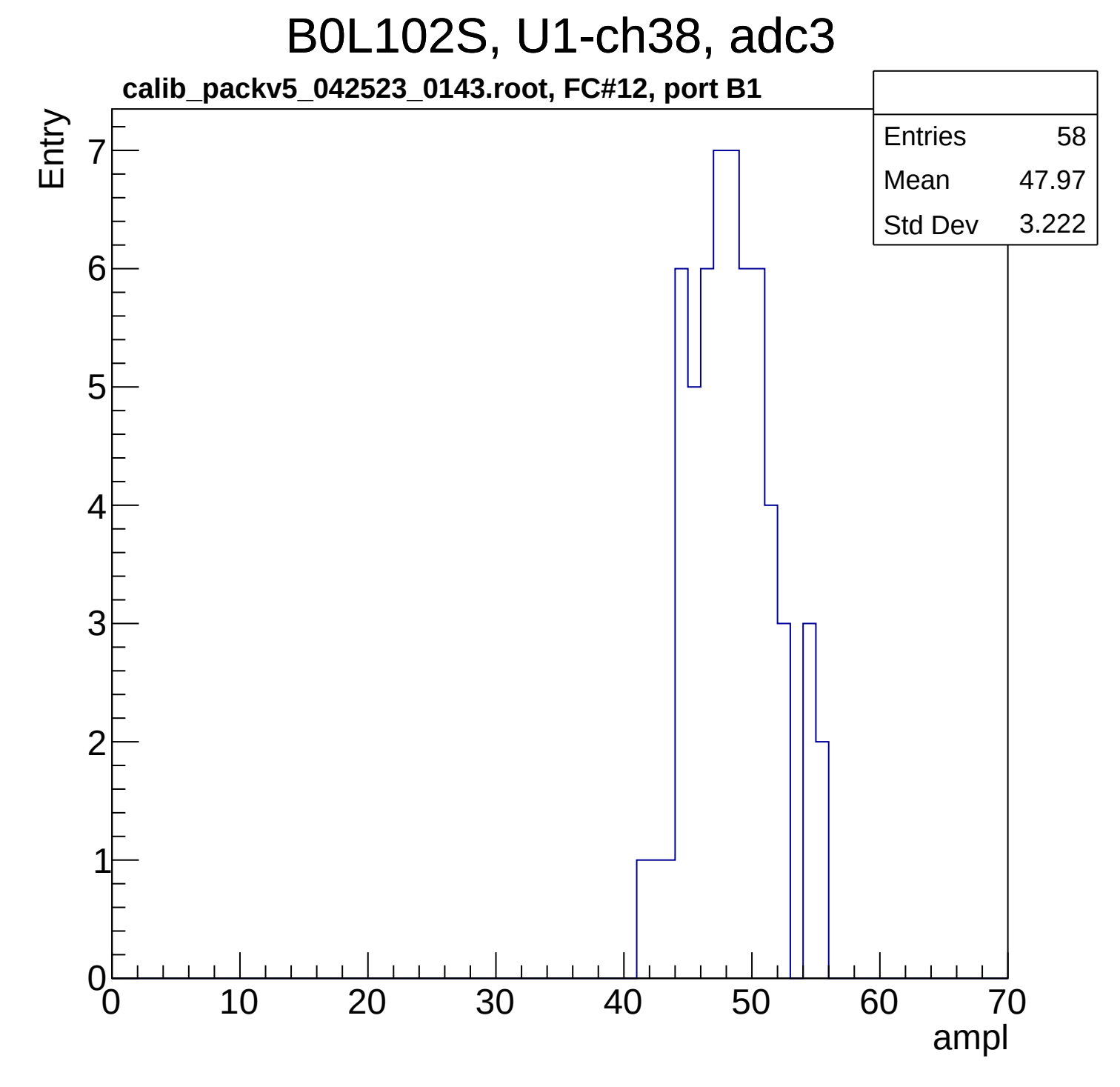
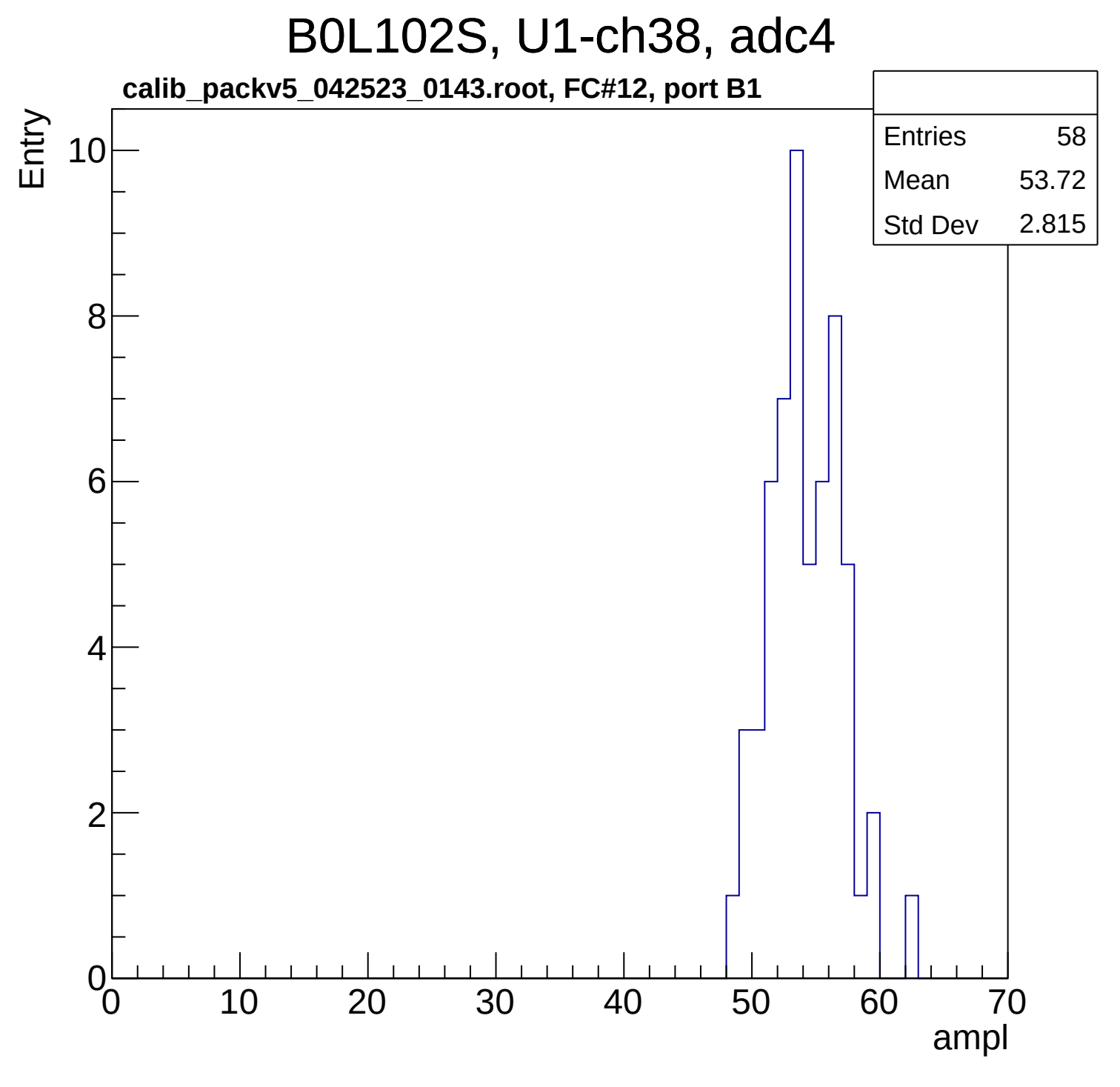
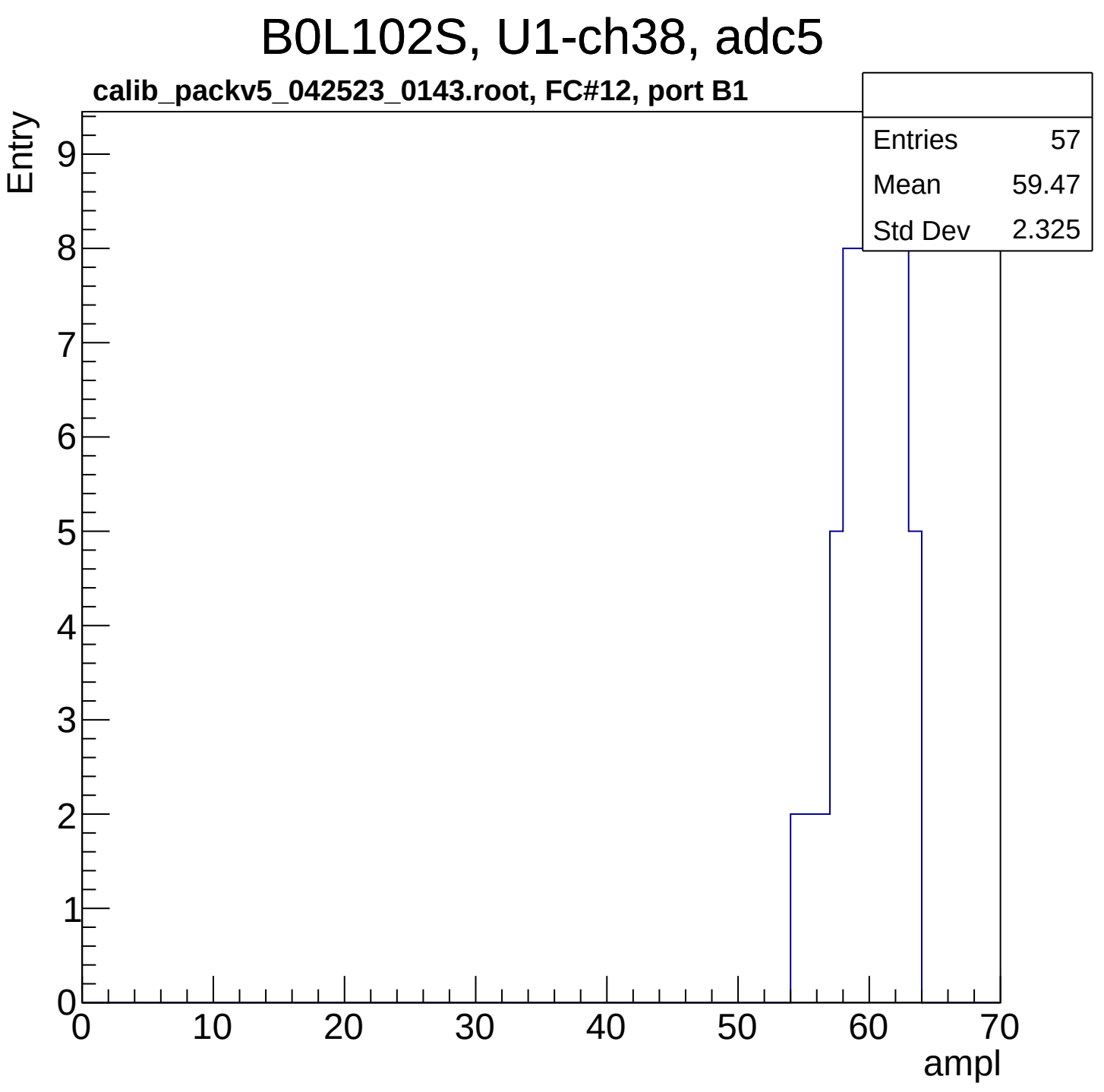

B0L102S, U1-ch38, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 41.51
Std Dev 3.818
Gaus mean : 41.8024
Gaus Width: 4.5125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch38, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 47.97
Std Dev 3.222
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch38, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 53.72
Std Dev 2.815
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch38, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 59.47
Std Dev 2.325
Entry
0
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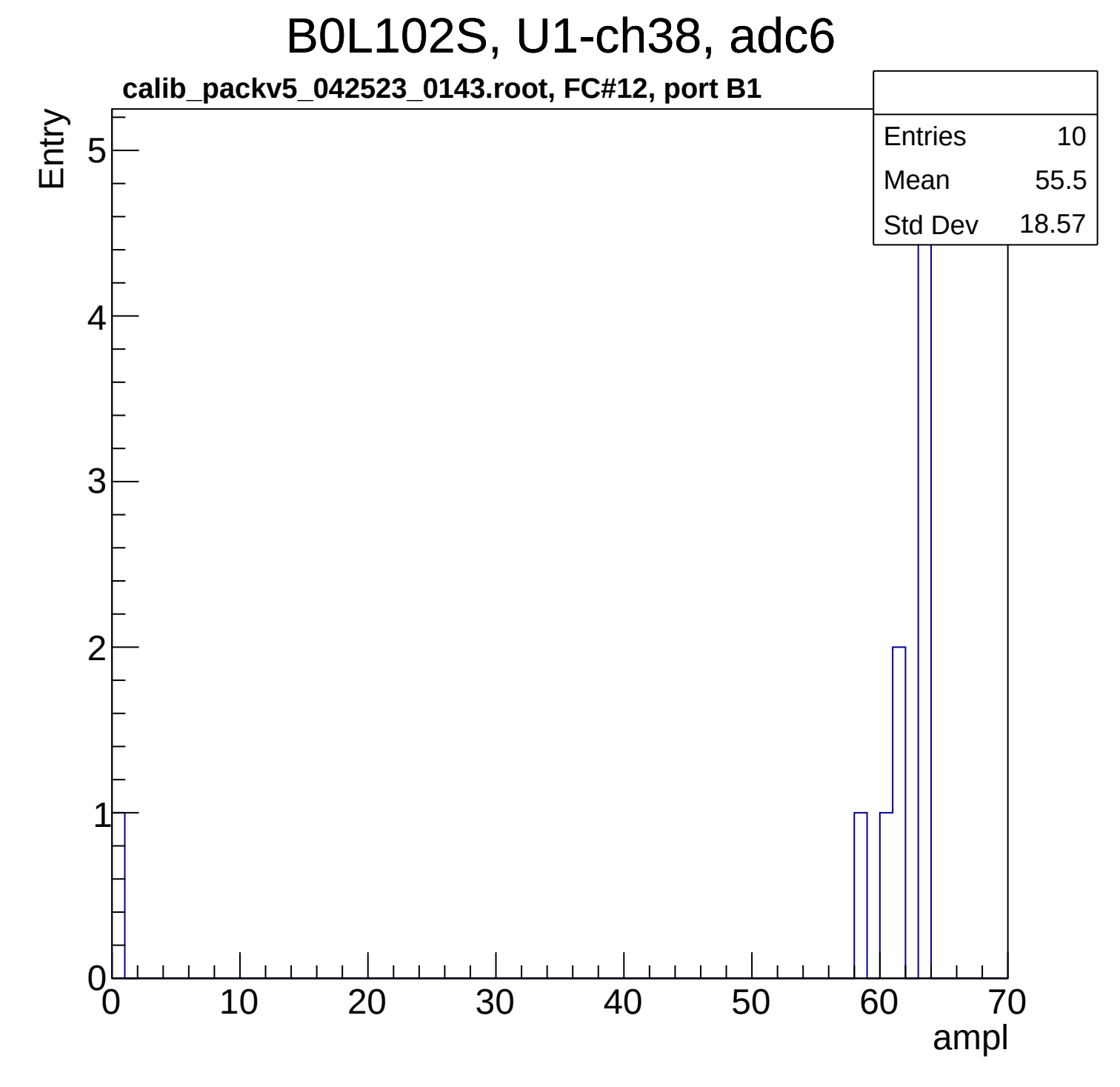
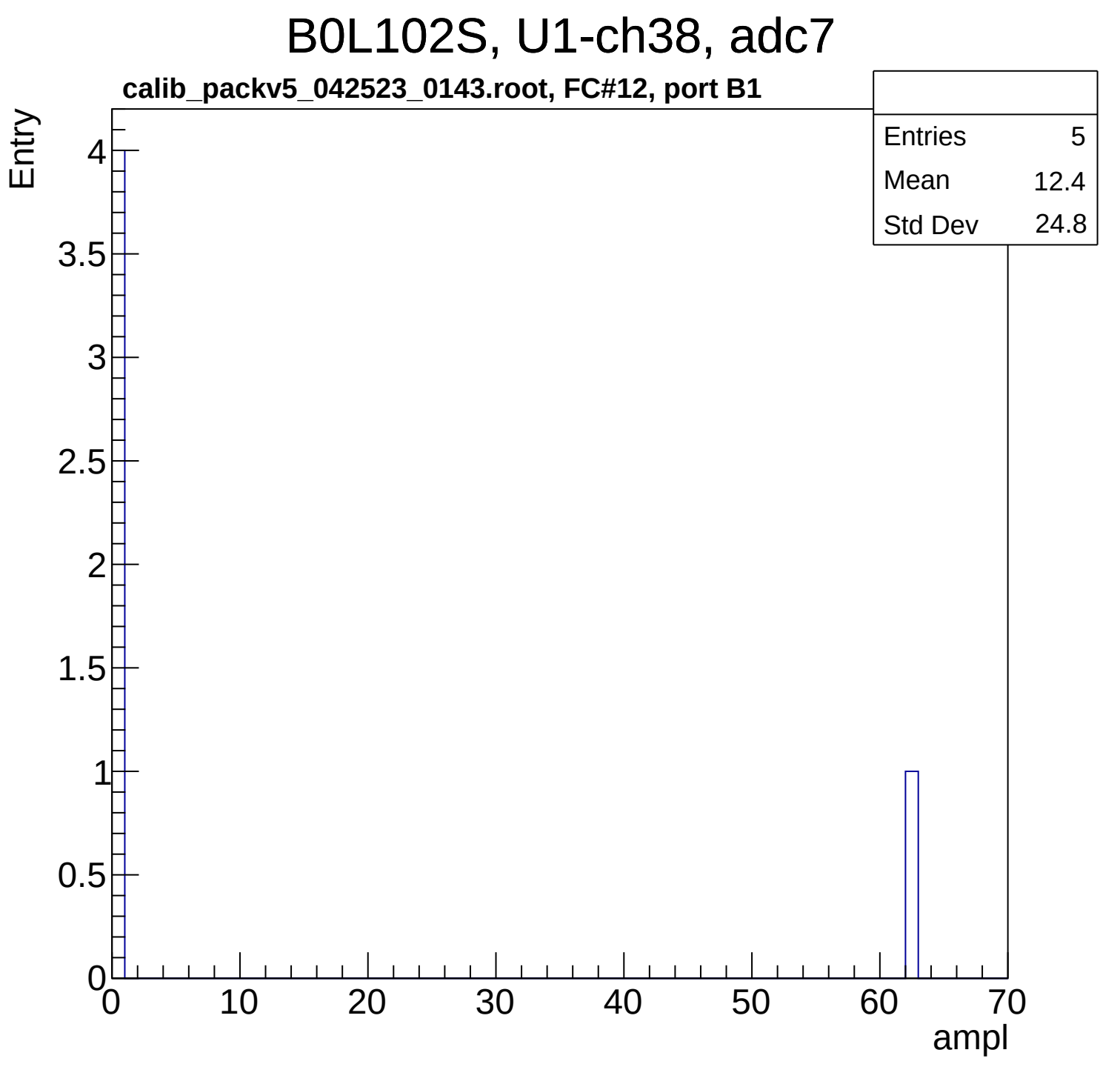
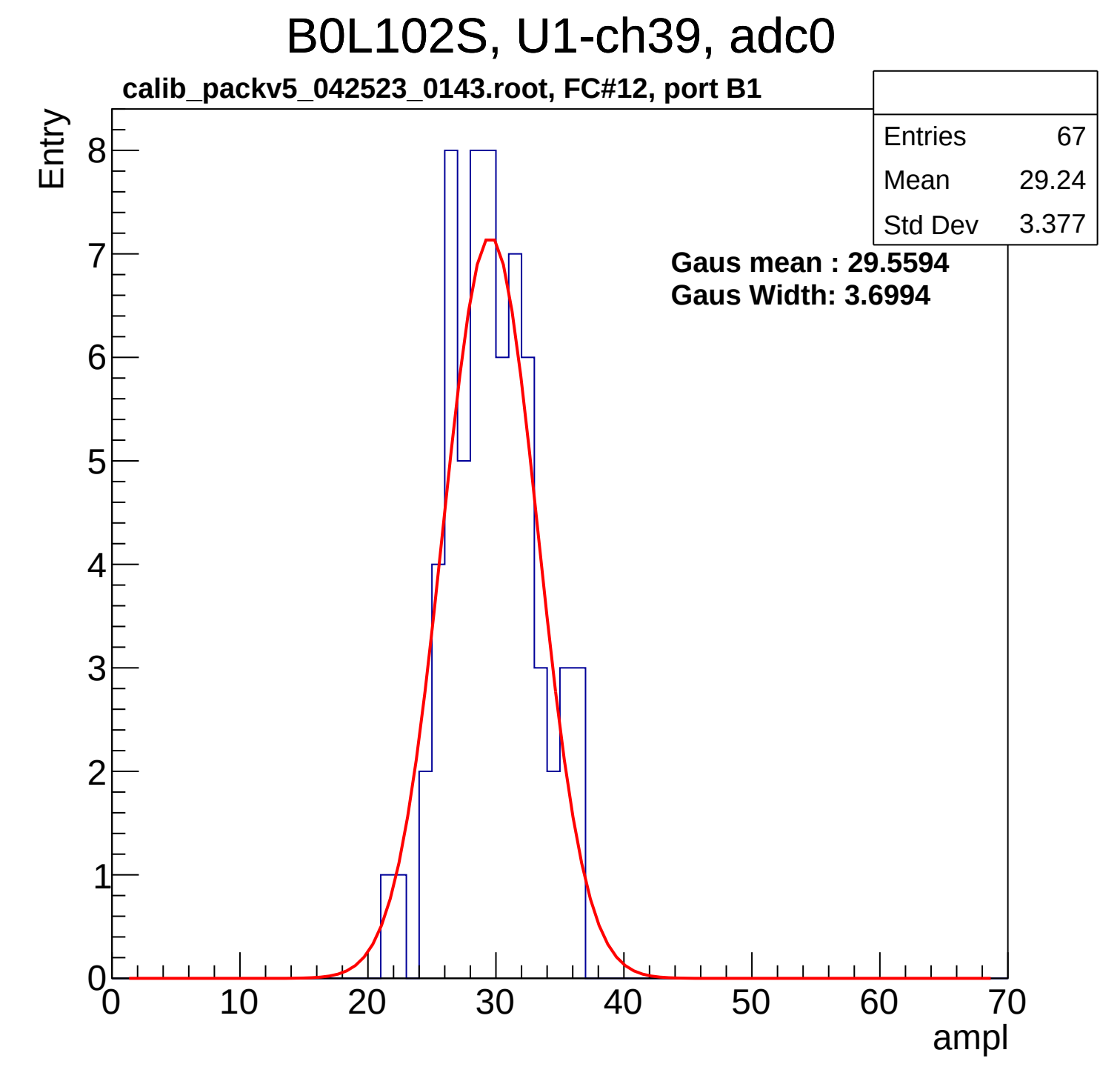
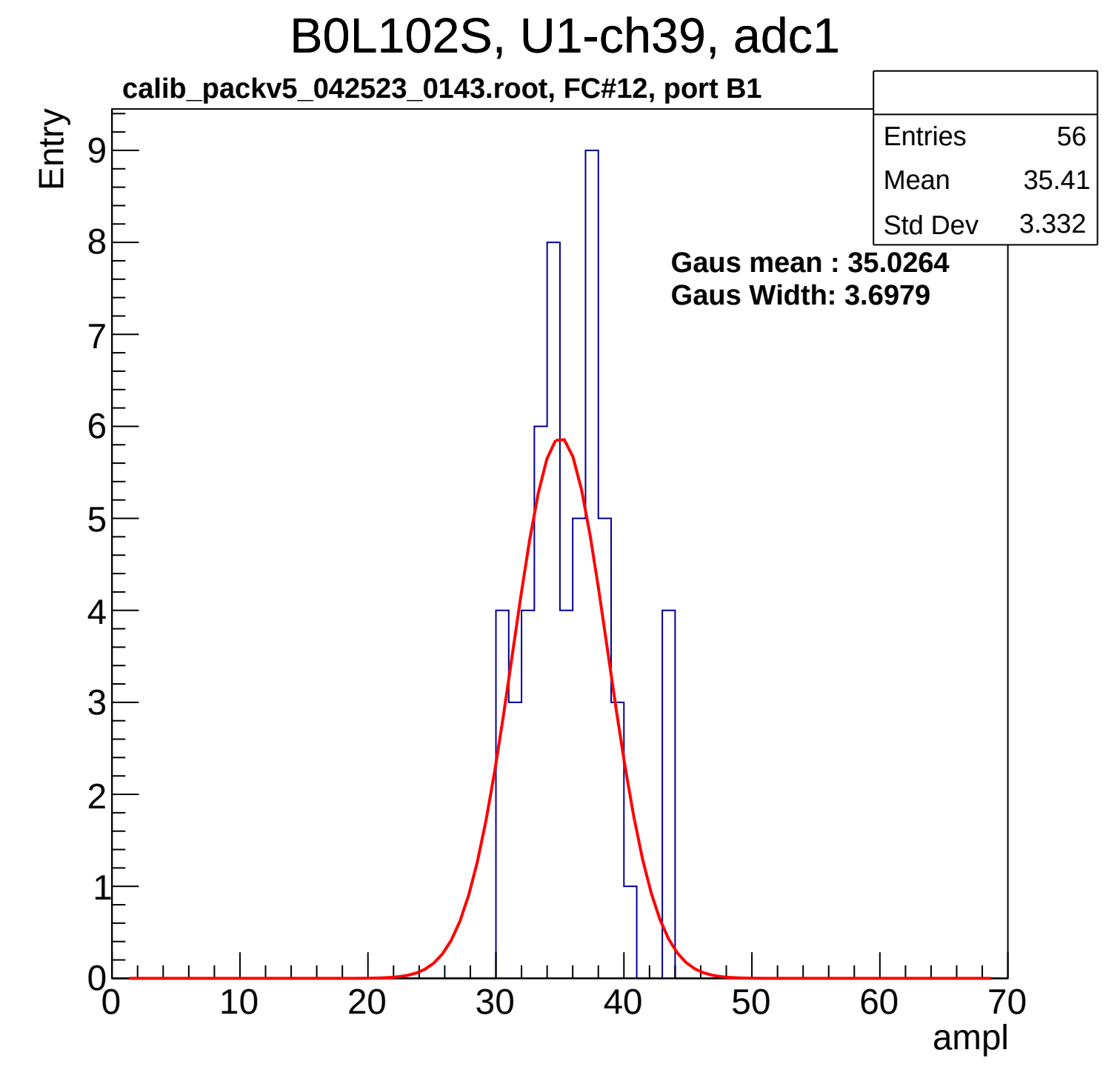
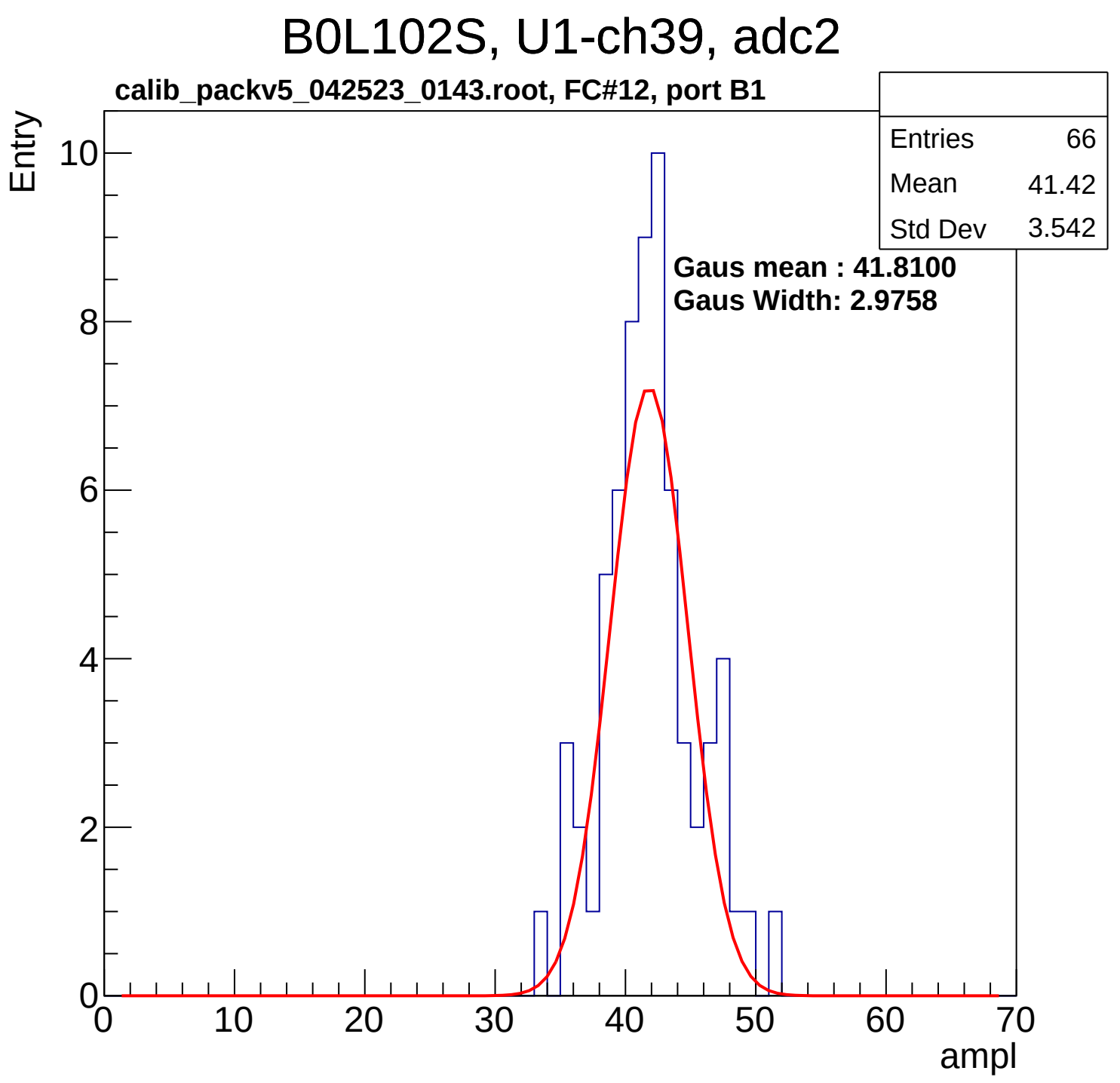
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

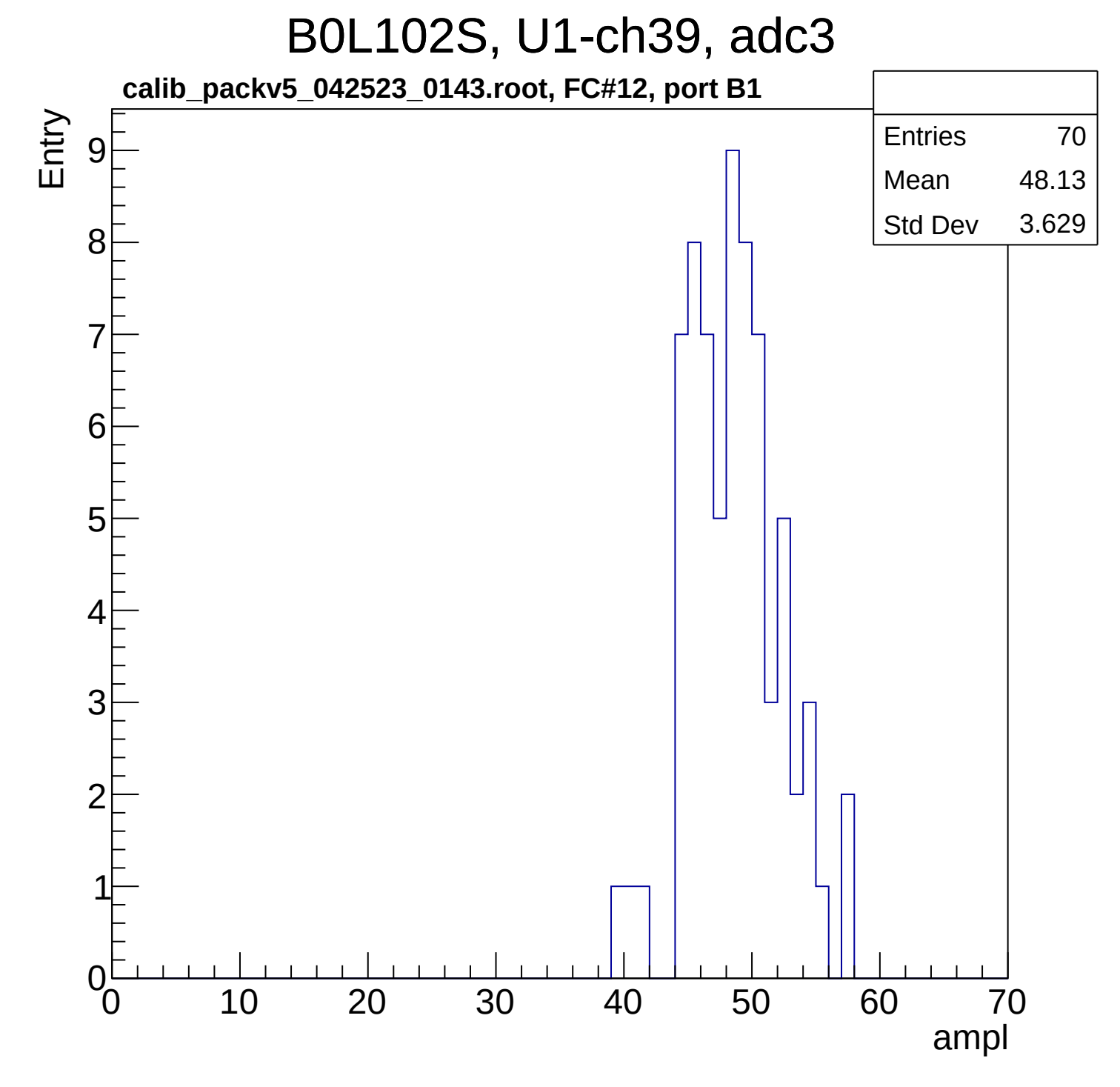

B0L102S, U1-ch38, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 55.5
Std Dev 18.57
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

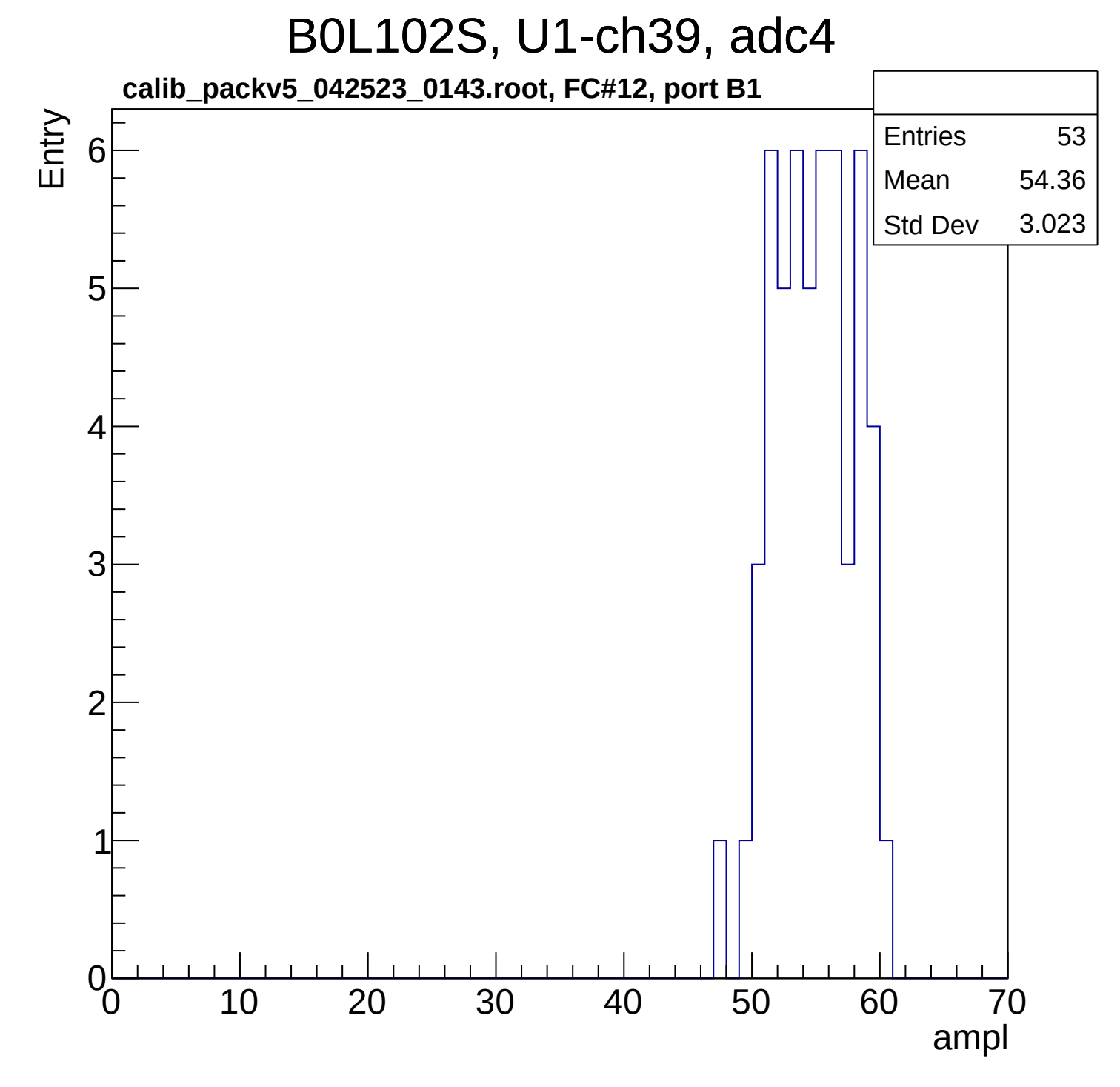

B0L102S, U1-ch38, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 12.4
Std Dev 24.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

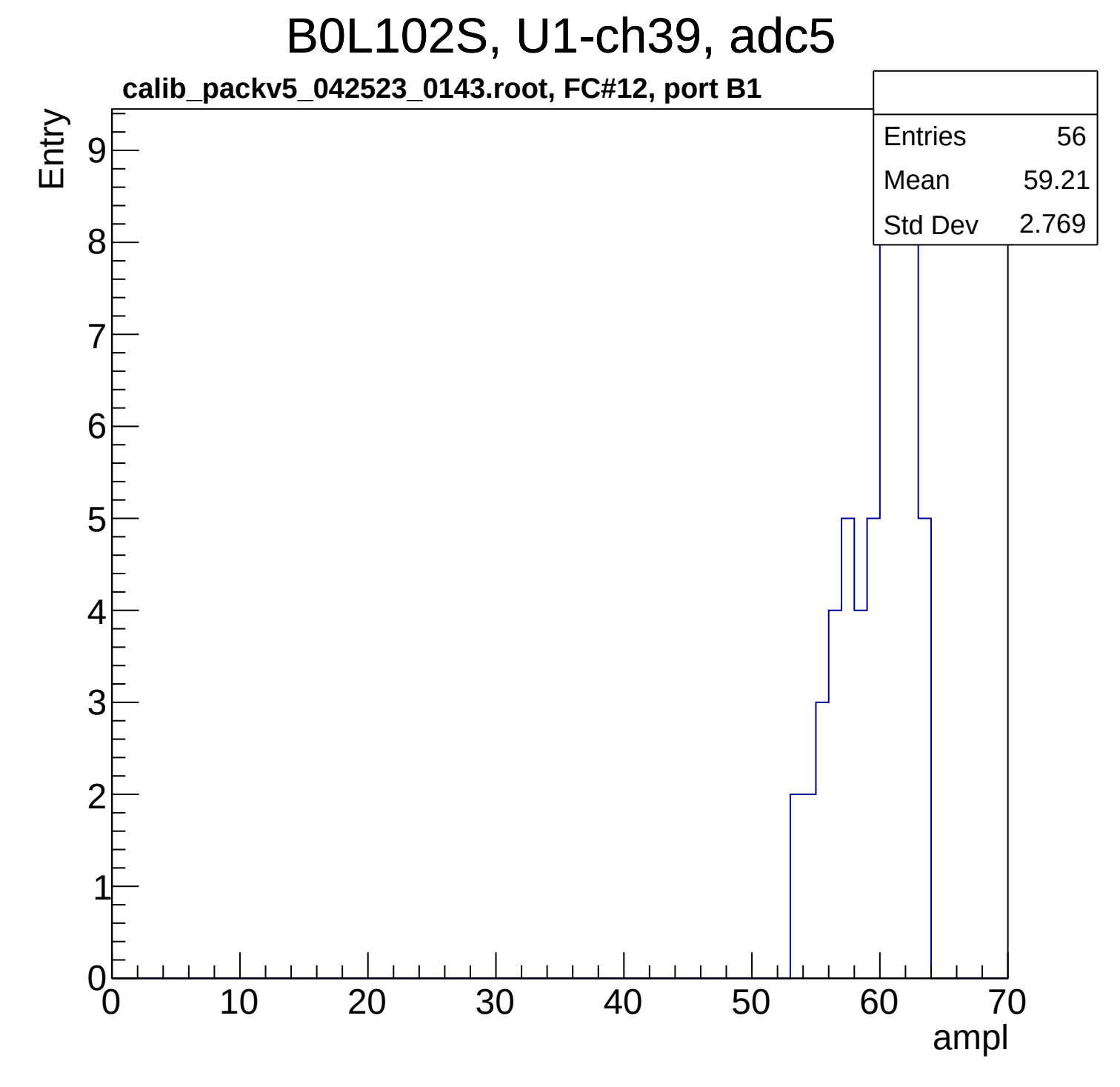

B0L102S, U1-ch39, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.24
Std Dev 3.377
Gaus mean : 29.5594
Gaus Width: 3.6994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

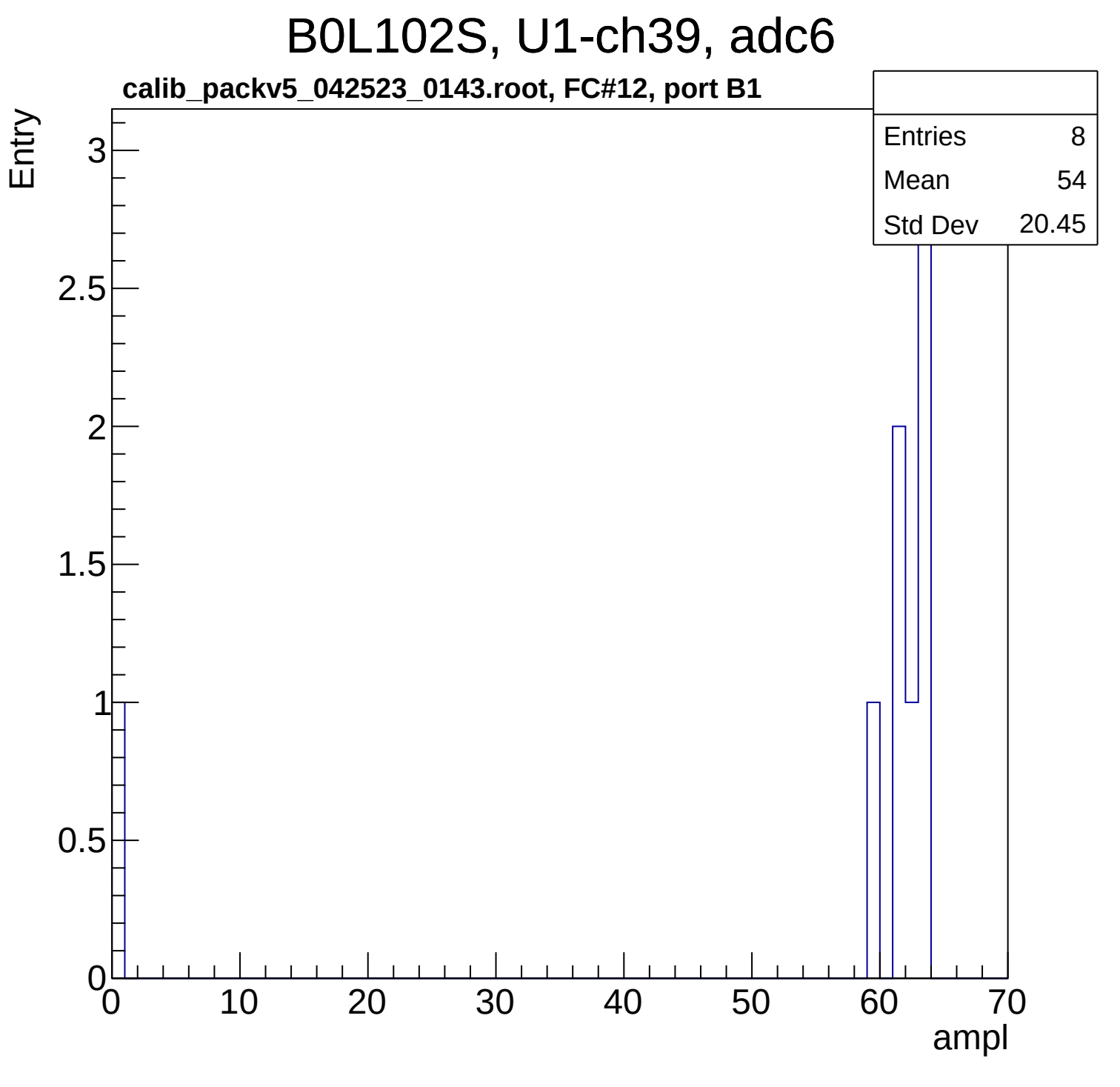

B0L102S, U1-ch39, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 35.41
Std Dev 3.332
Gaus mean : 35.0264
Gaus Width: 3.6979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

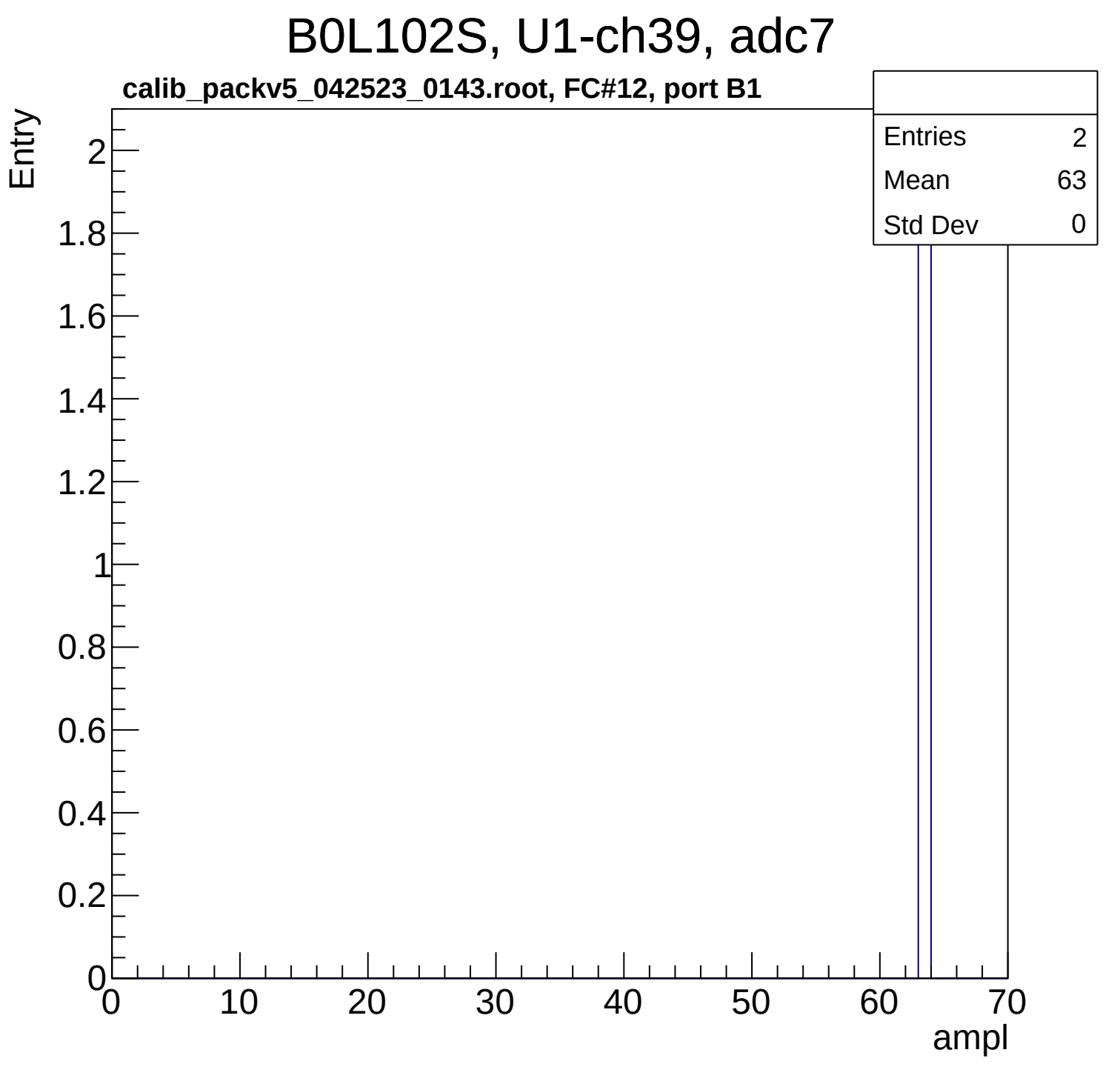

B0L102S, U1-ch39, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 41.42
Std Dev 3.542
Gaus mean : 41.8100
Gaus Width: 2.9758
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

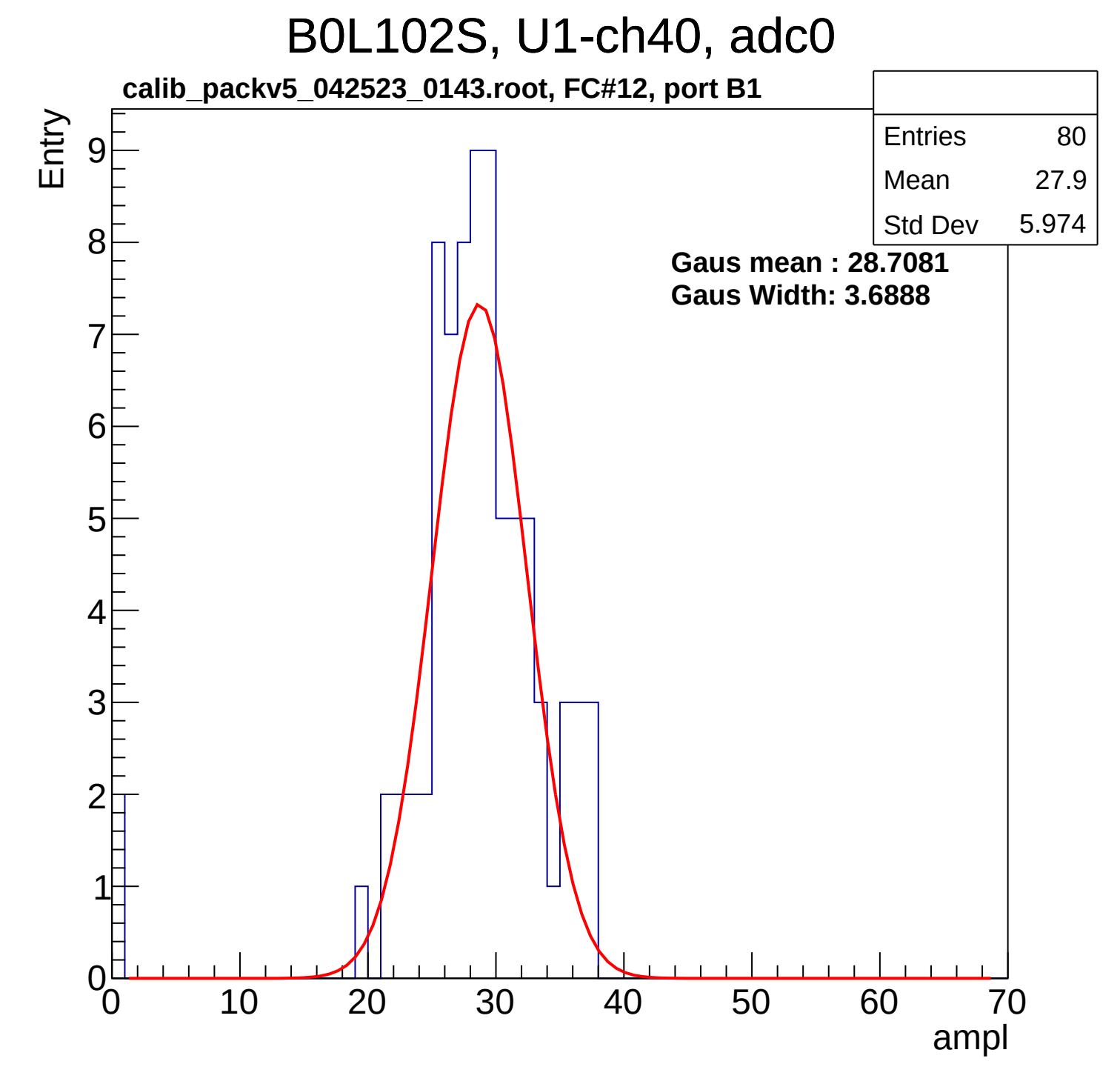

B0L102S, U1-ch39, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 48.13
Std Dev 3.629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

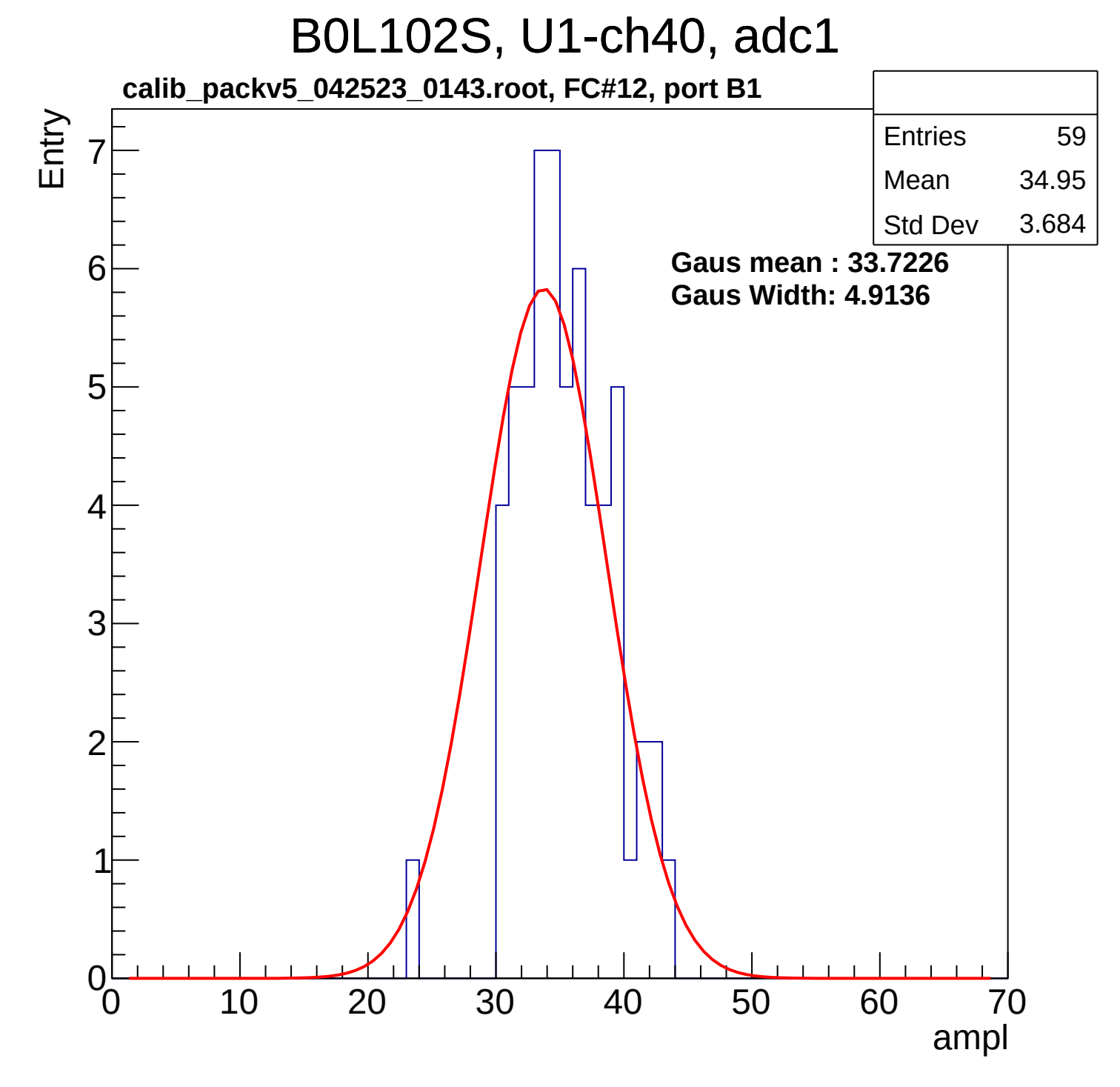

B0L102S, U1-ch39, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 54.36
Std Dev 3.023
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

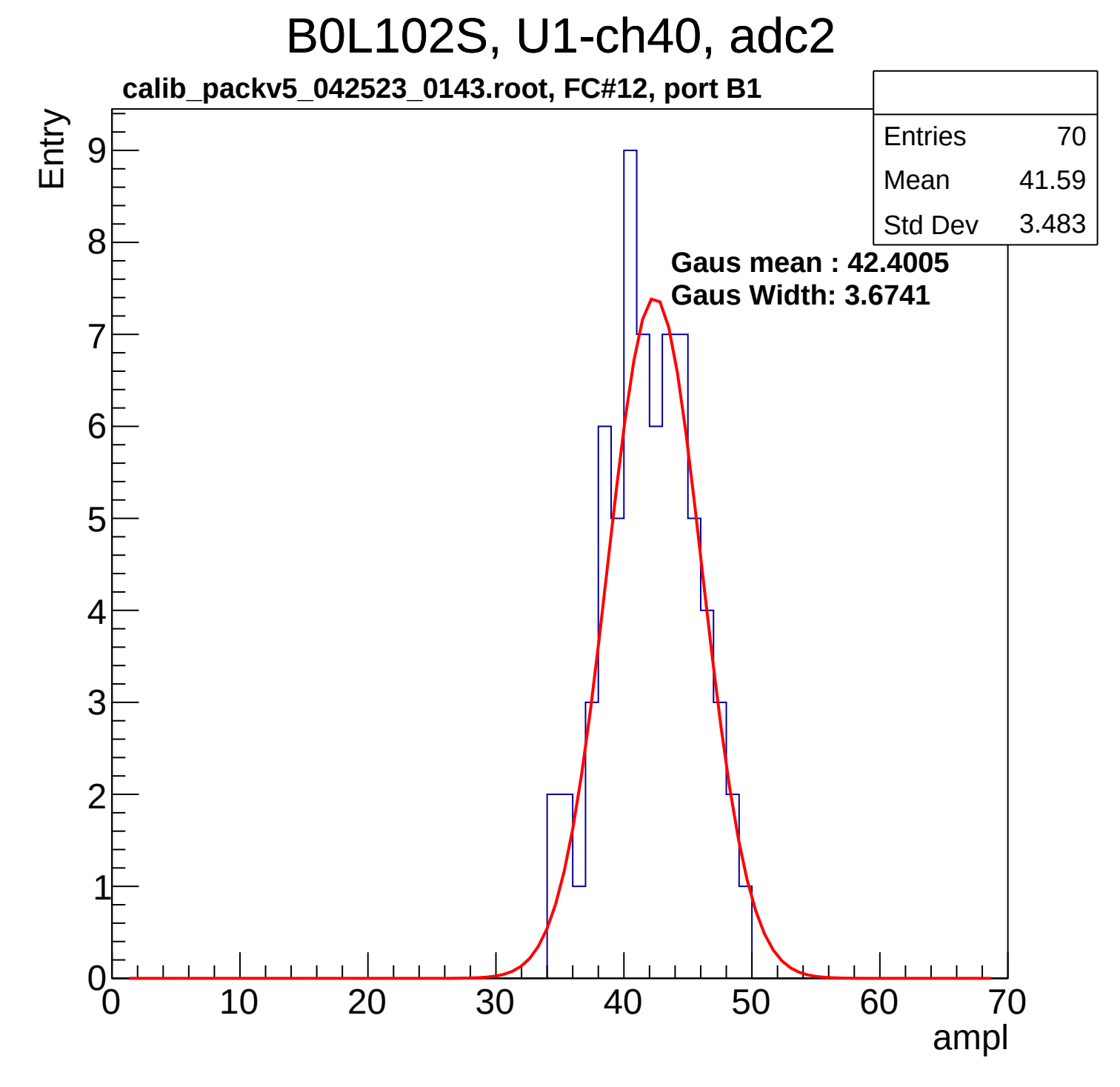

B0L102S, U1-ch39, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 59.21
Std Dev 2.769
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

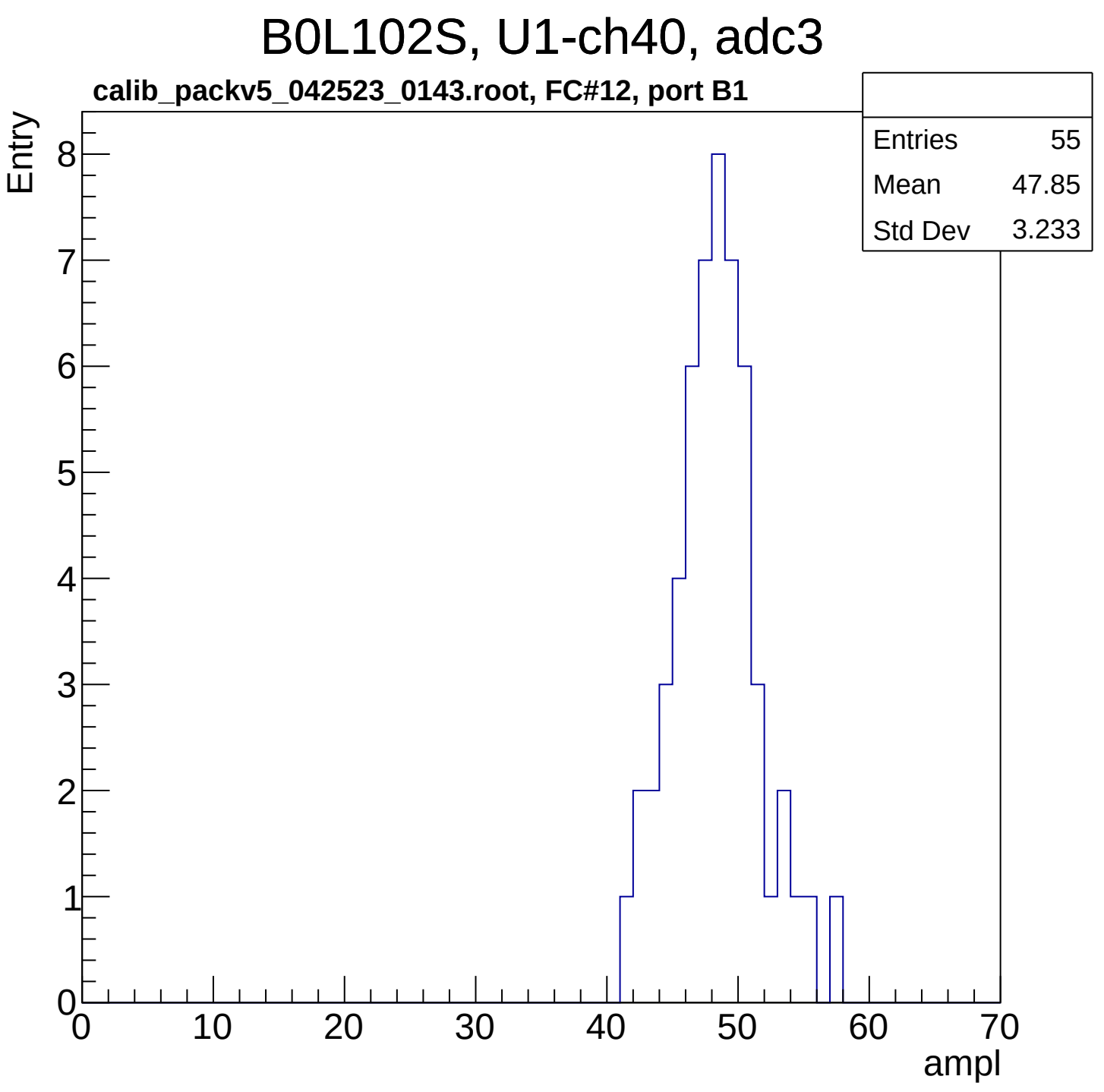

B0L102S, U1-ch39, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 54
Std Dev 20.45
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch39, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch40, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 27.9
Std Dev 5.974
Gaus mean : 28.7081
Gaus Width: 3.6888
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch40, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 34.95
Std Dev 3.684
Gaus mean : 33.7226
Gaus Width: 4.9136
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch40, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 41.59
Std Dev 3.483
Gaus mean : 42.4005
Gaus Width: 3.6741
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch40, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 47.85
Std Dev 3.233
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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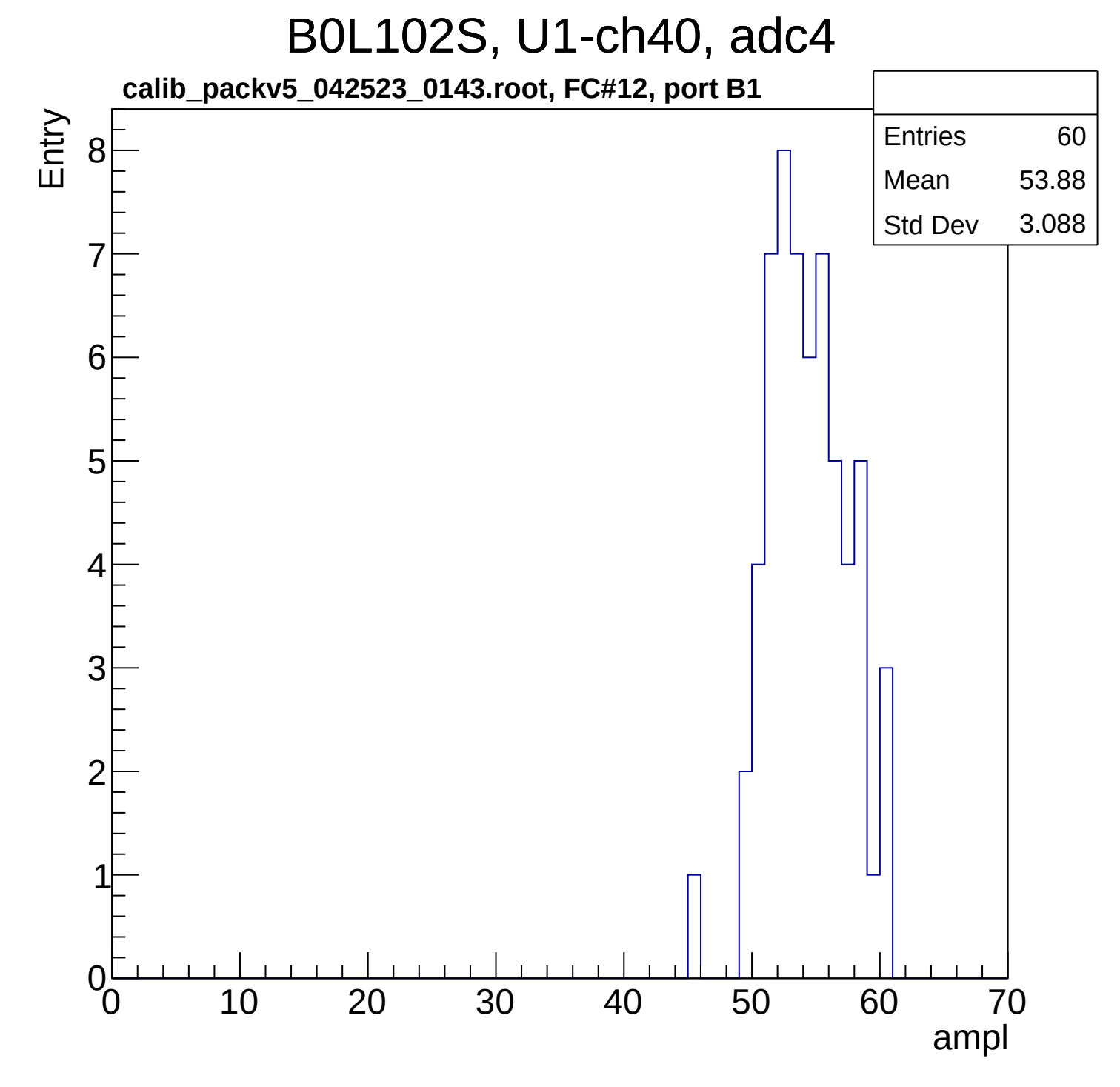
40
50
60
70


B0L102S, U1-ch40, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 53.88
Std Dev 3.088
Entry
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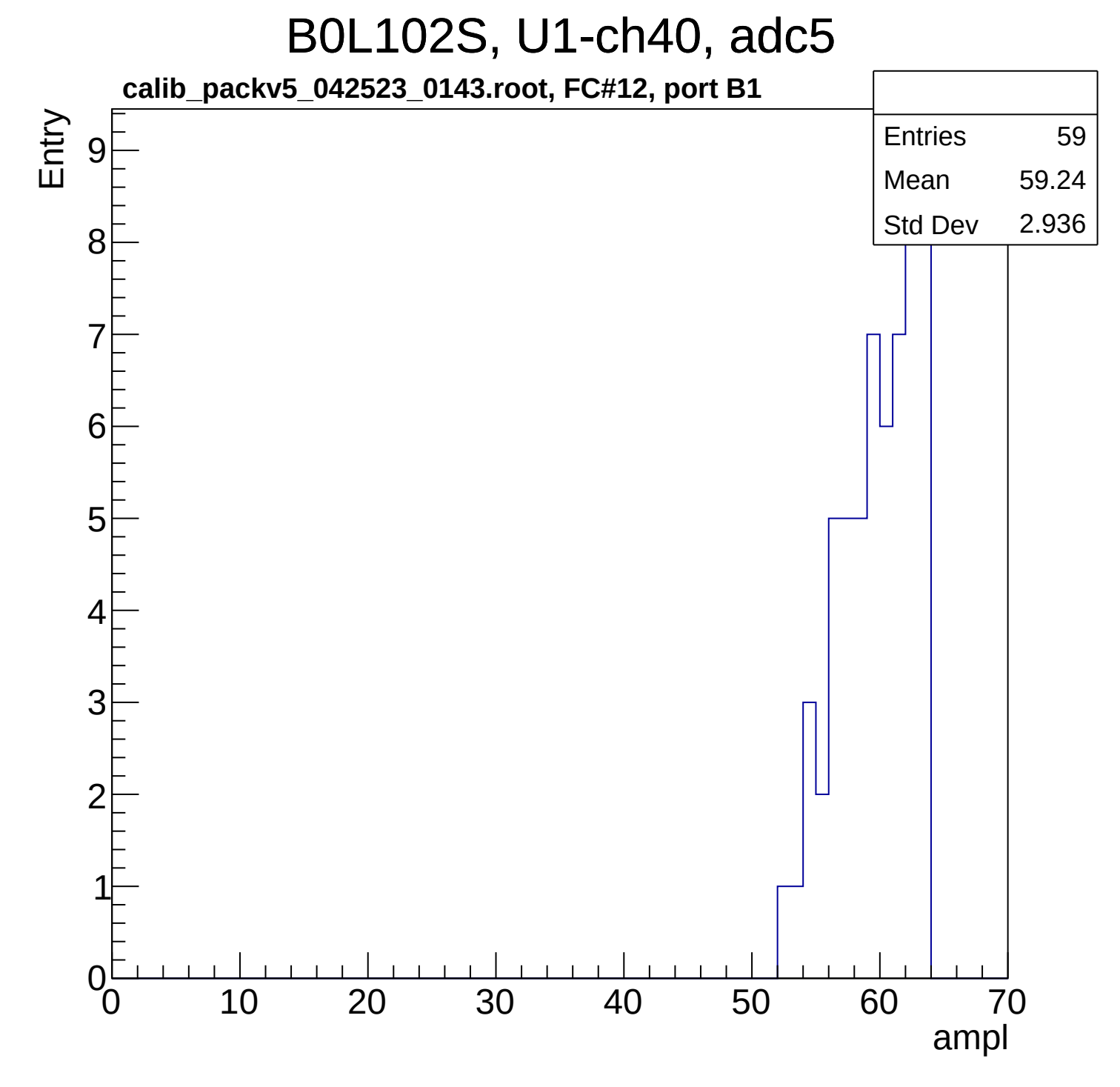
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch40, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 59.24
Std Dev 2.936
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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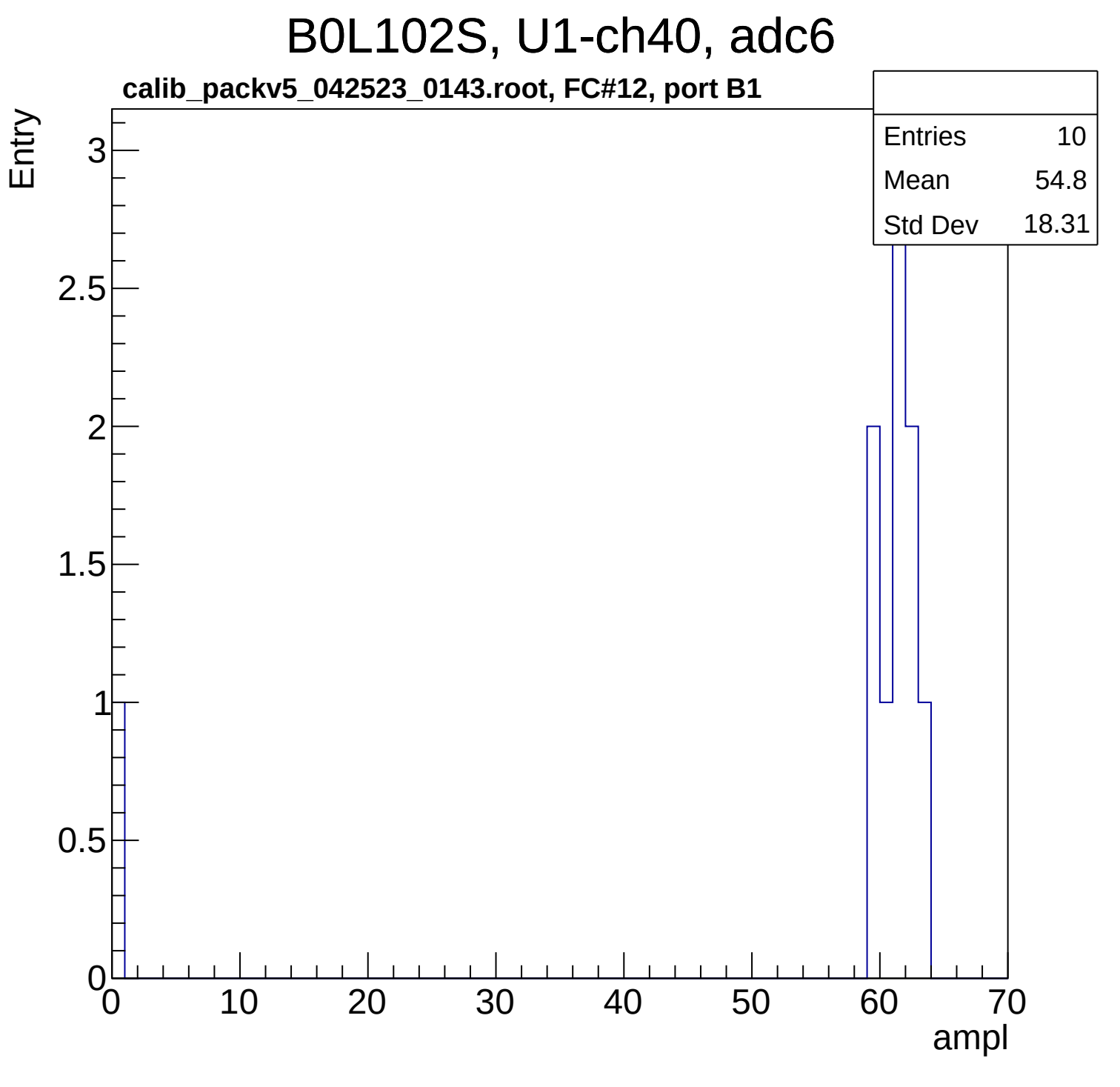
B0L102S, U1-ch40, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 54.8
Std Dev 18.31
Entry
ampl
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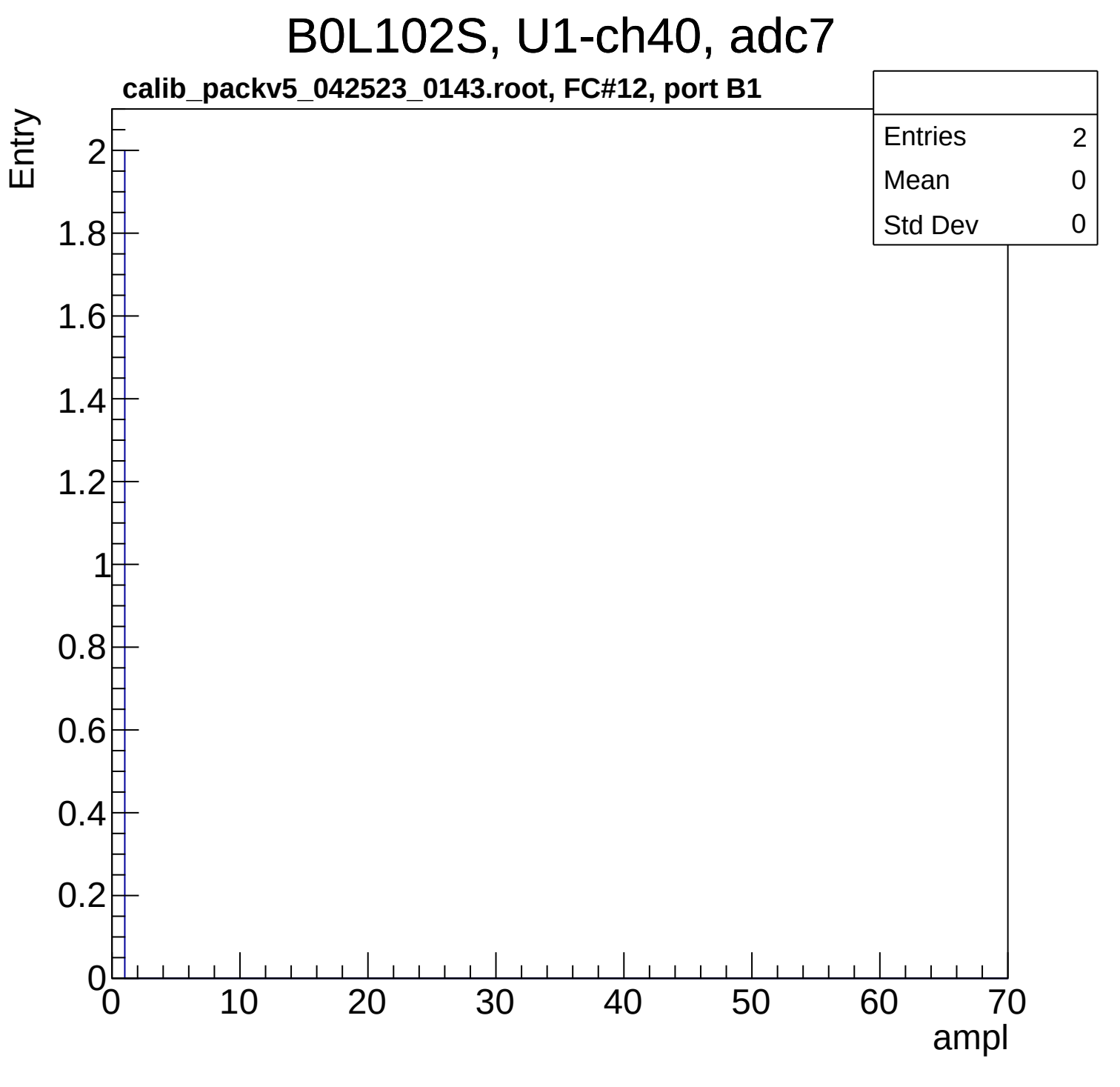
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch40, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
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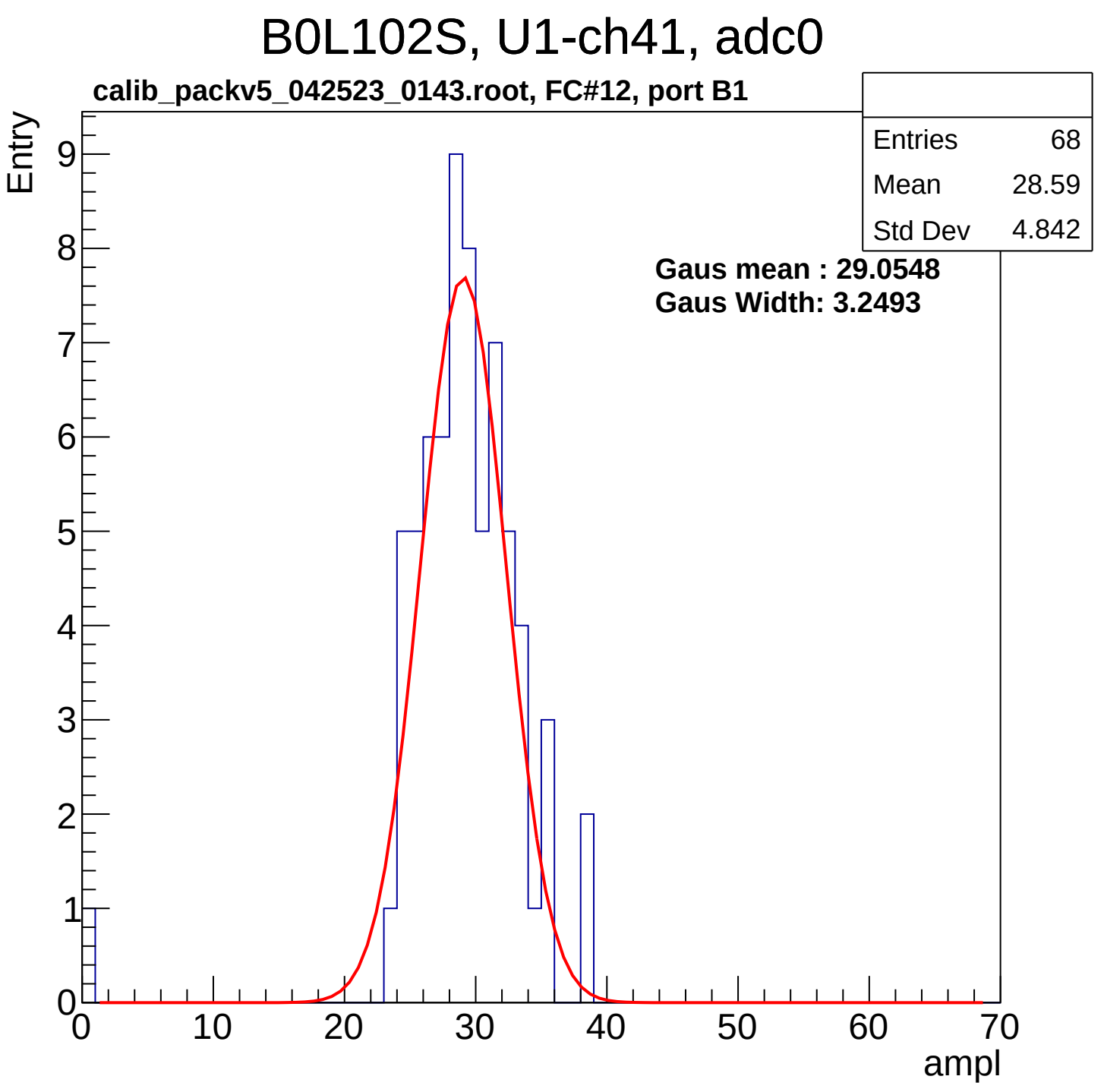
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch41, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 28.59
Std Dev 4.842
Gaus mean : 29.0548
Gaus Width: 3.2493
Entry
ampl
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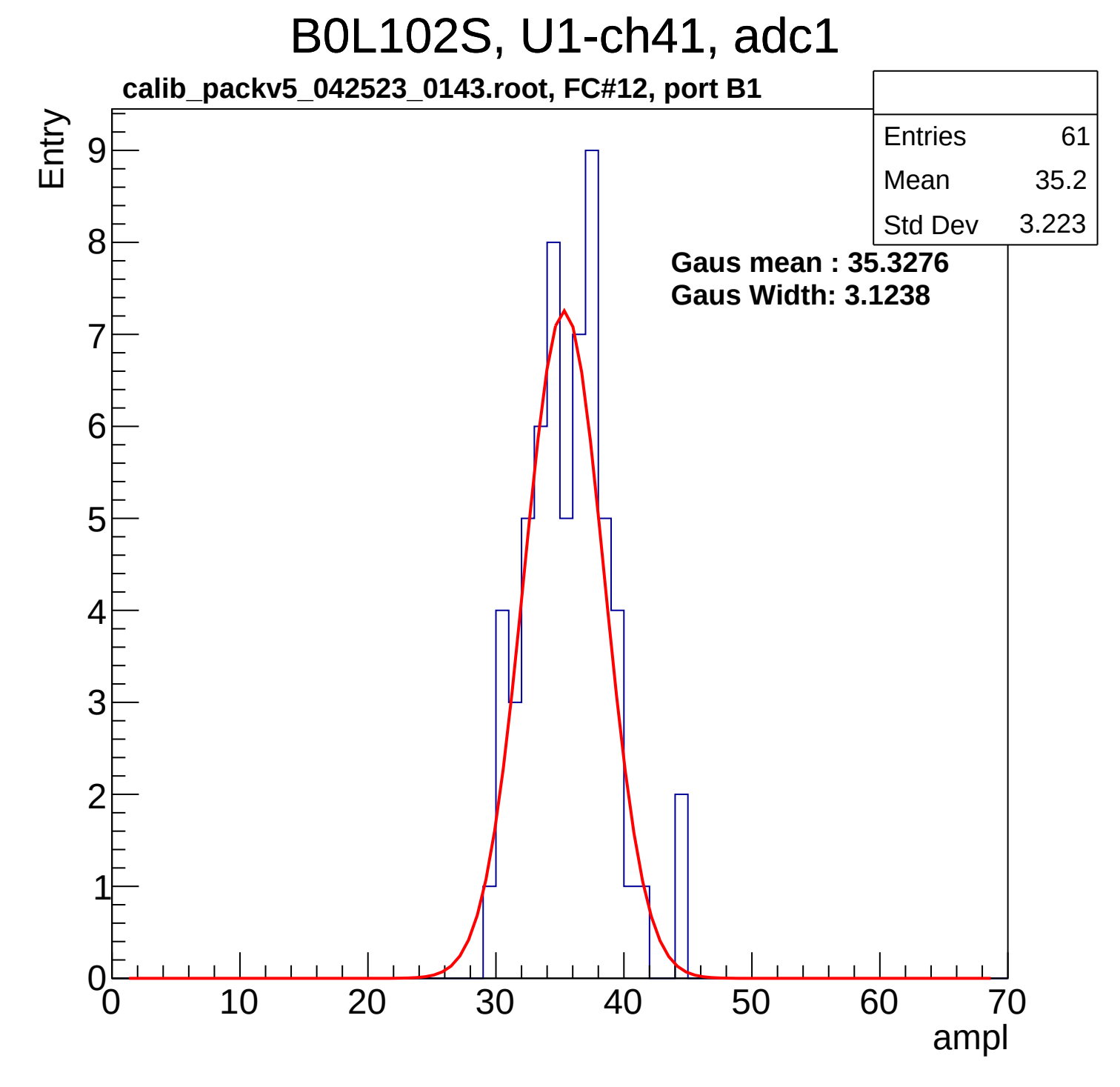
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch41, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 35.2
Std Dev 3.223
Gaus mean : 35.3276
Gaus Width: 3.1238
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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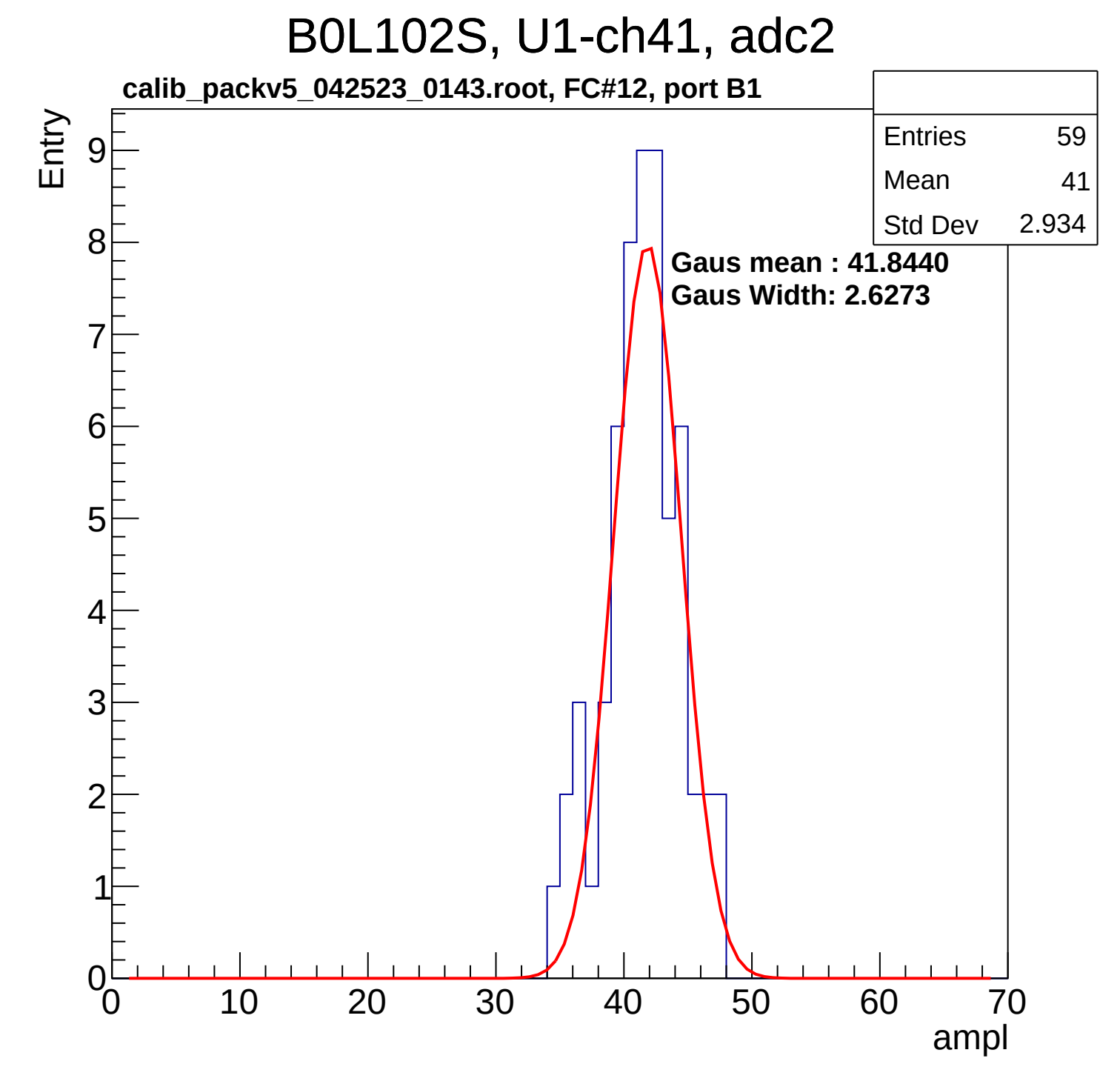
B0L102S, U1-ch41, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 41
Std Dev 2.934
Gaus mean : 41.8440
Gaus Width: 2.6273
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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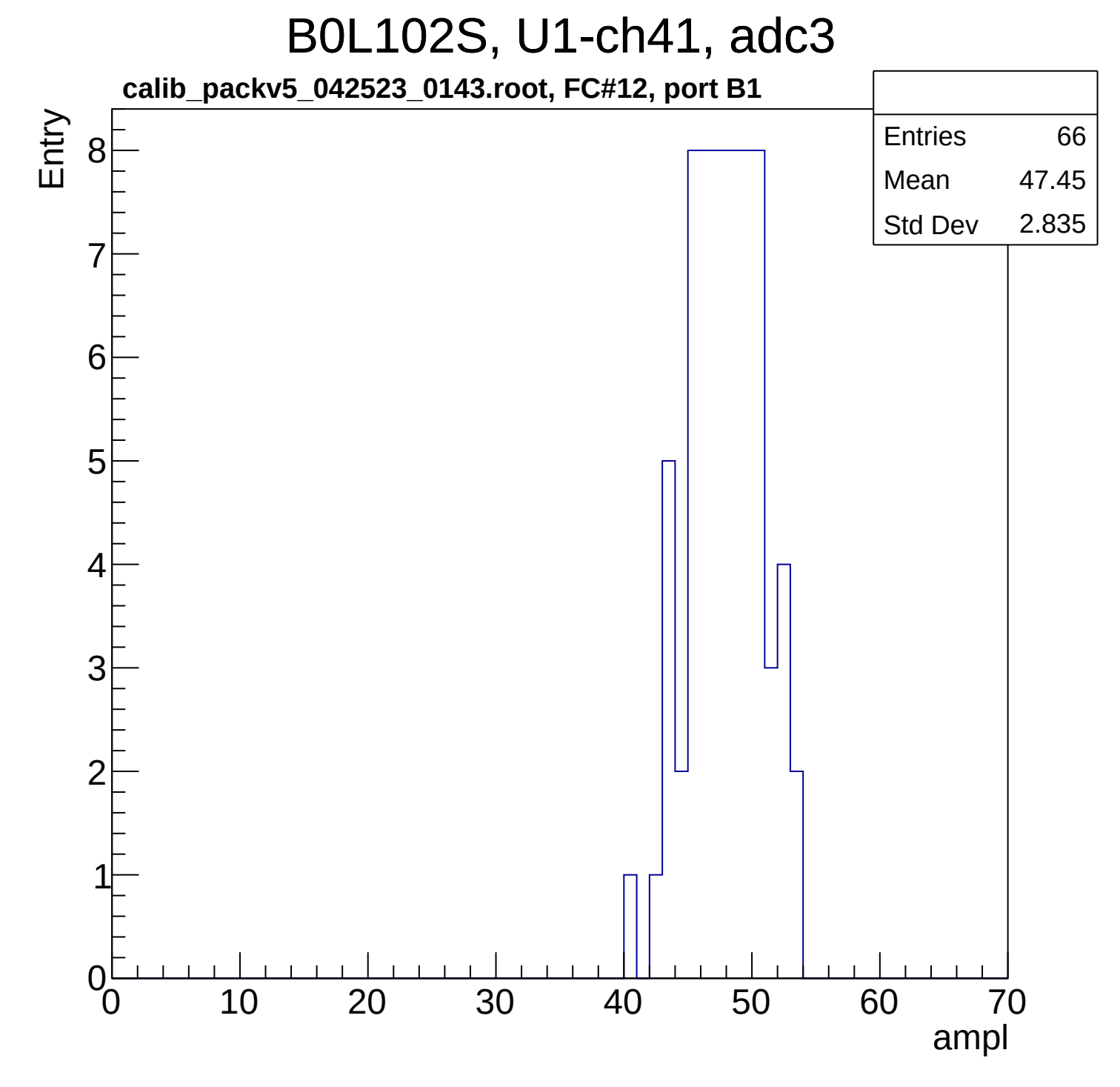
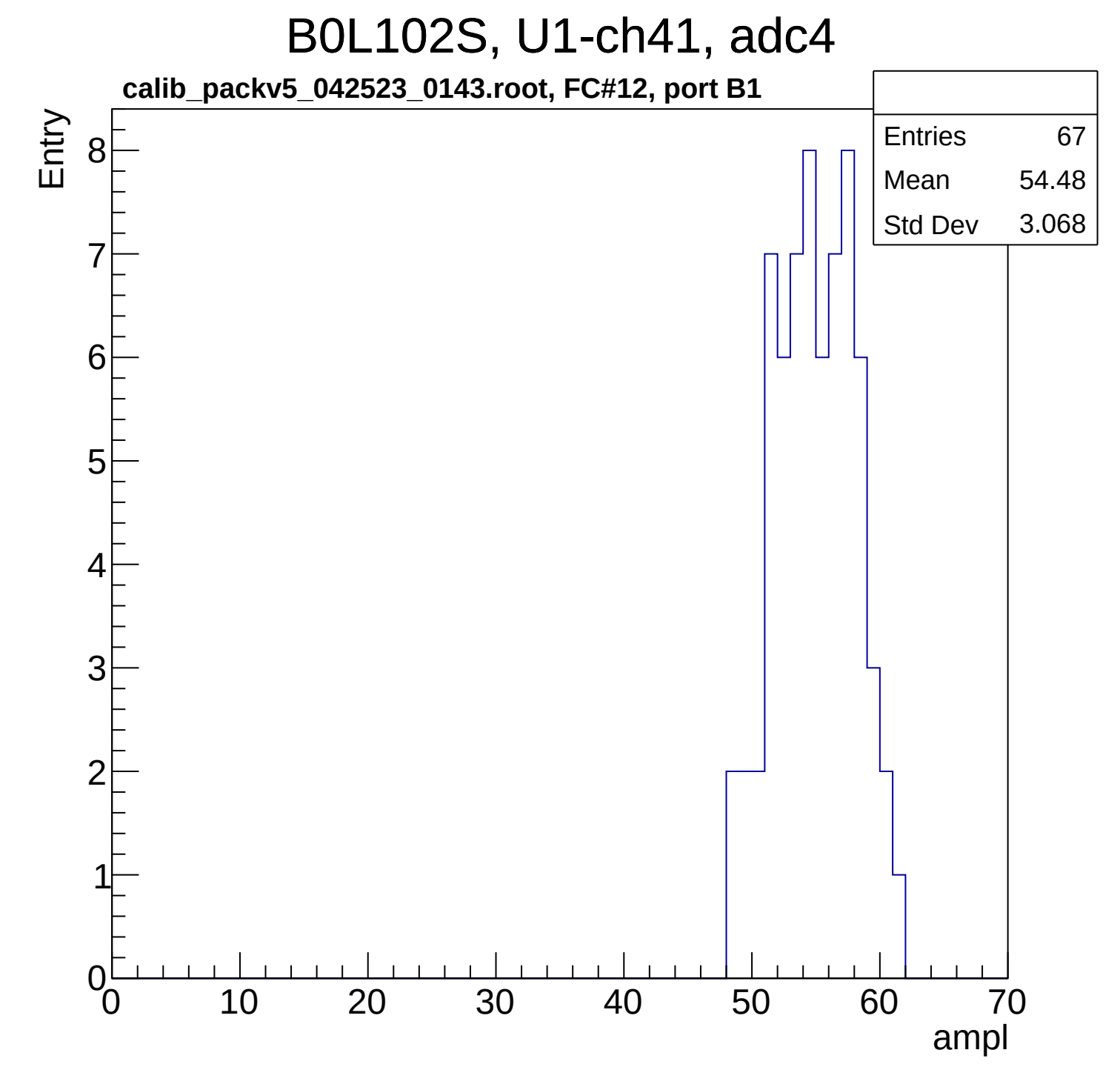
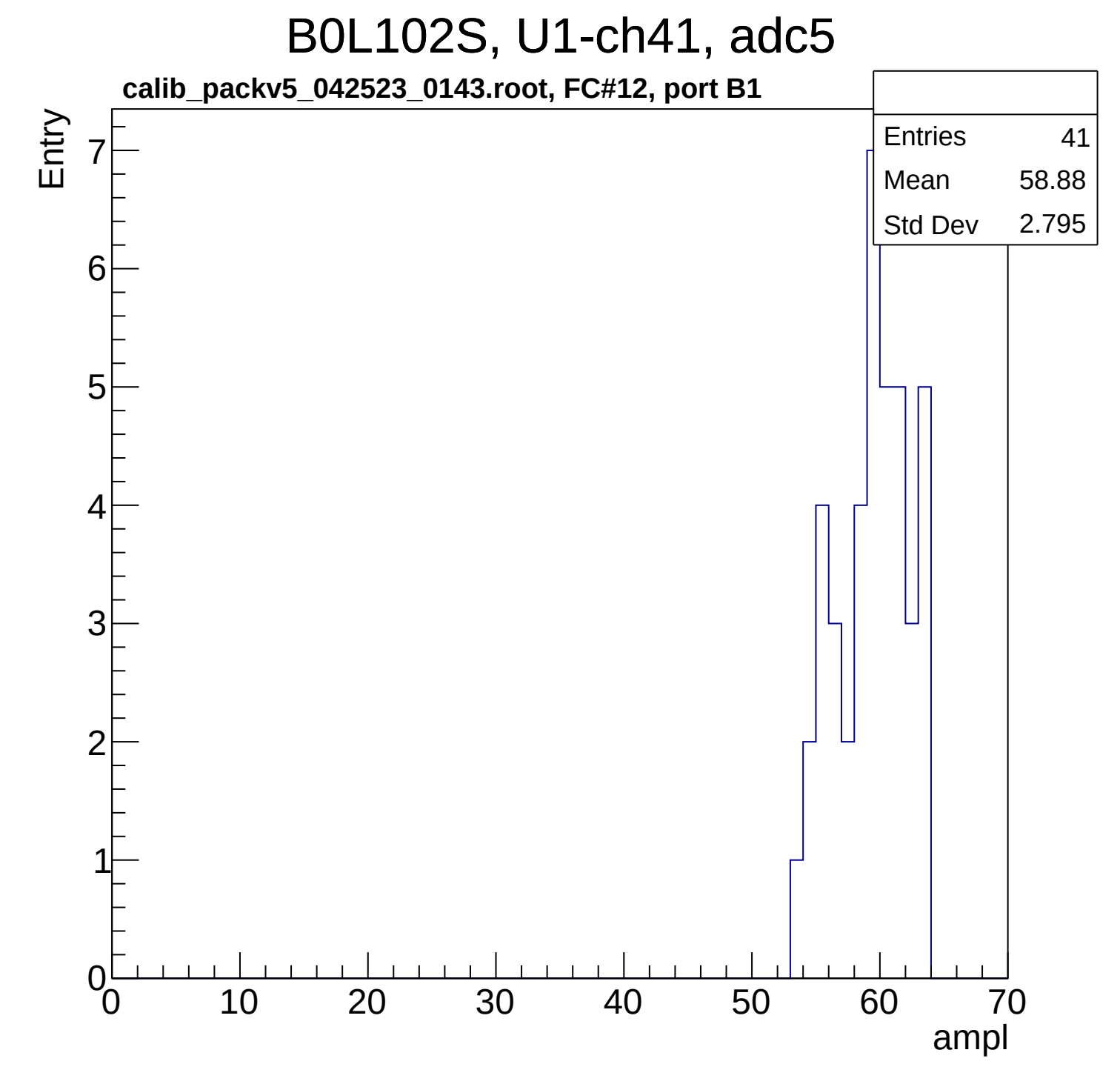

B0L102S, U1-ch41, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 47.45
Std Dev 2.835
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch41, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 54.48
Std Dev 3.068
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch41, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.88
Std Dev 2.795
Entry
ampl
0
1
2
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4
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6
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0
10
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40
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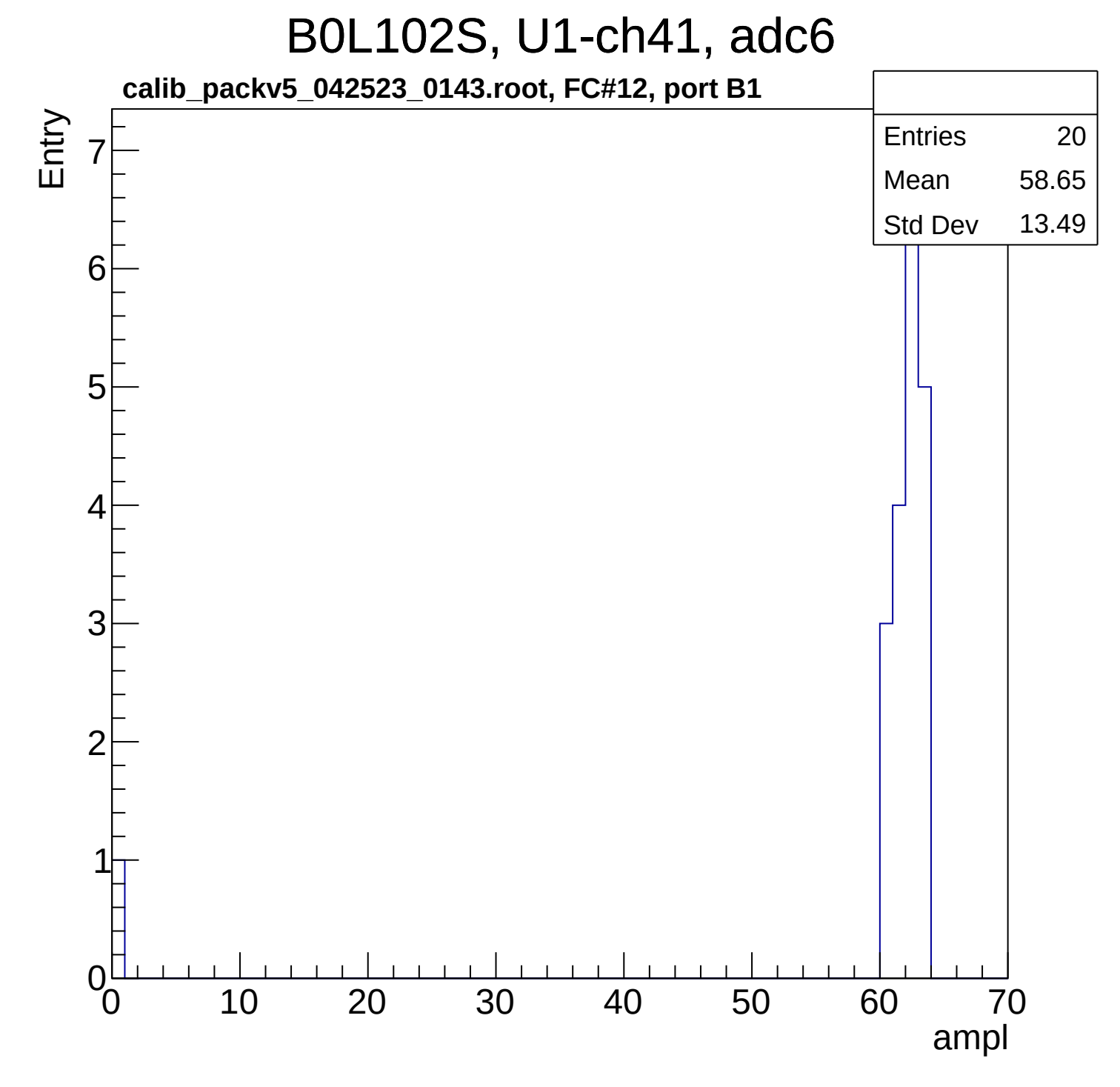
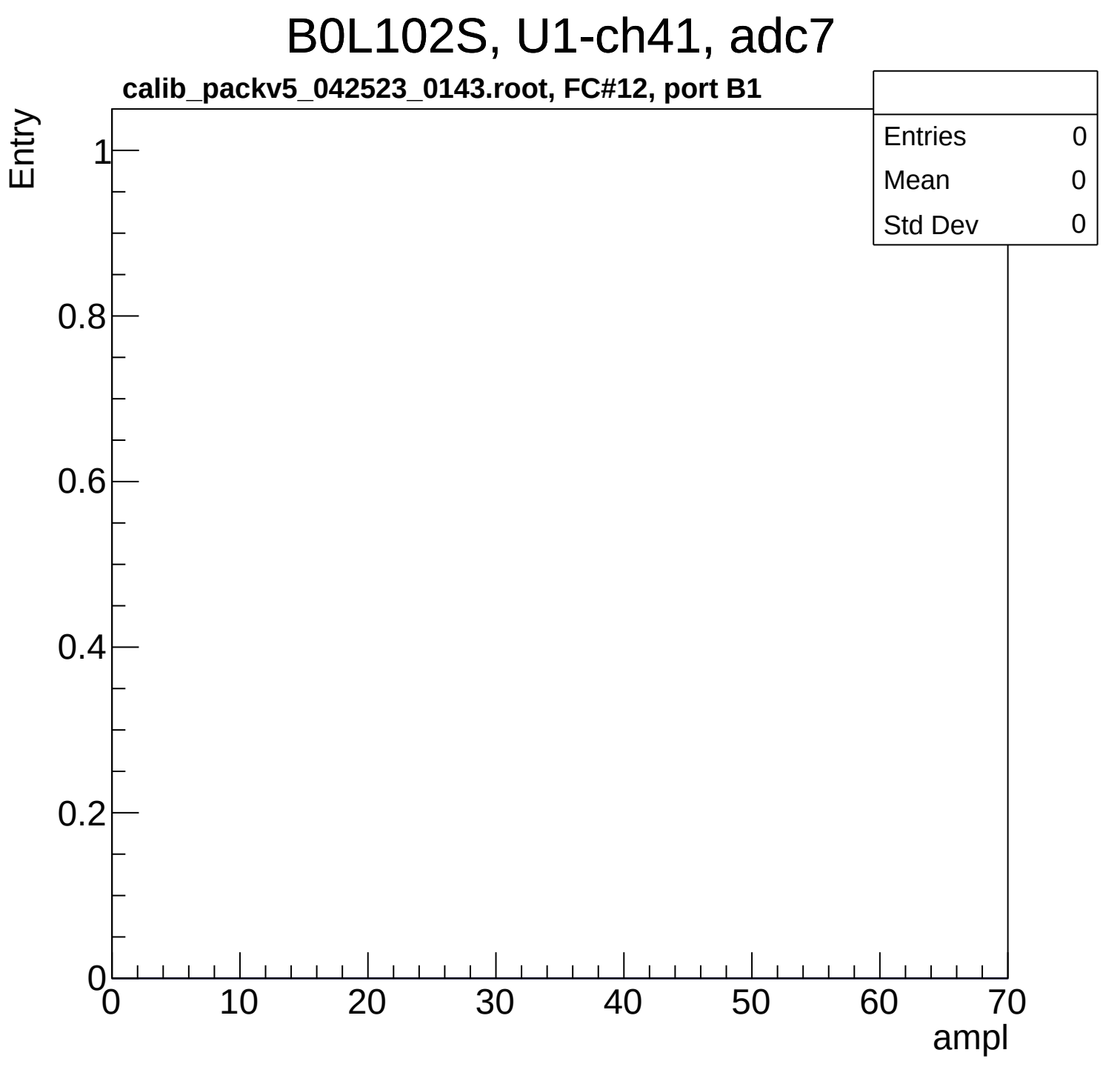
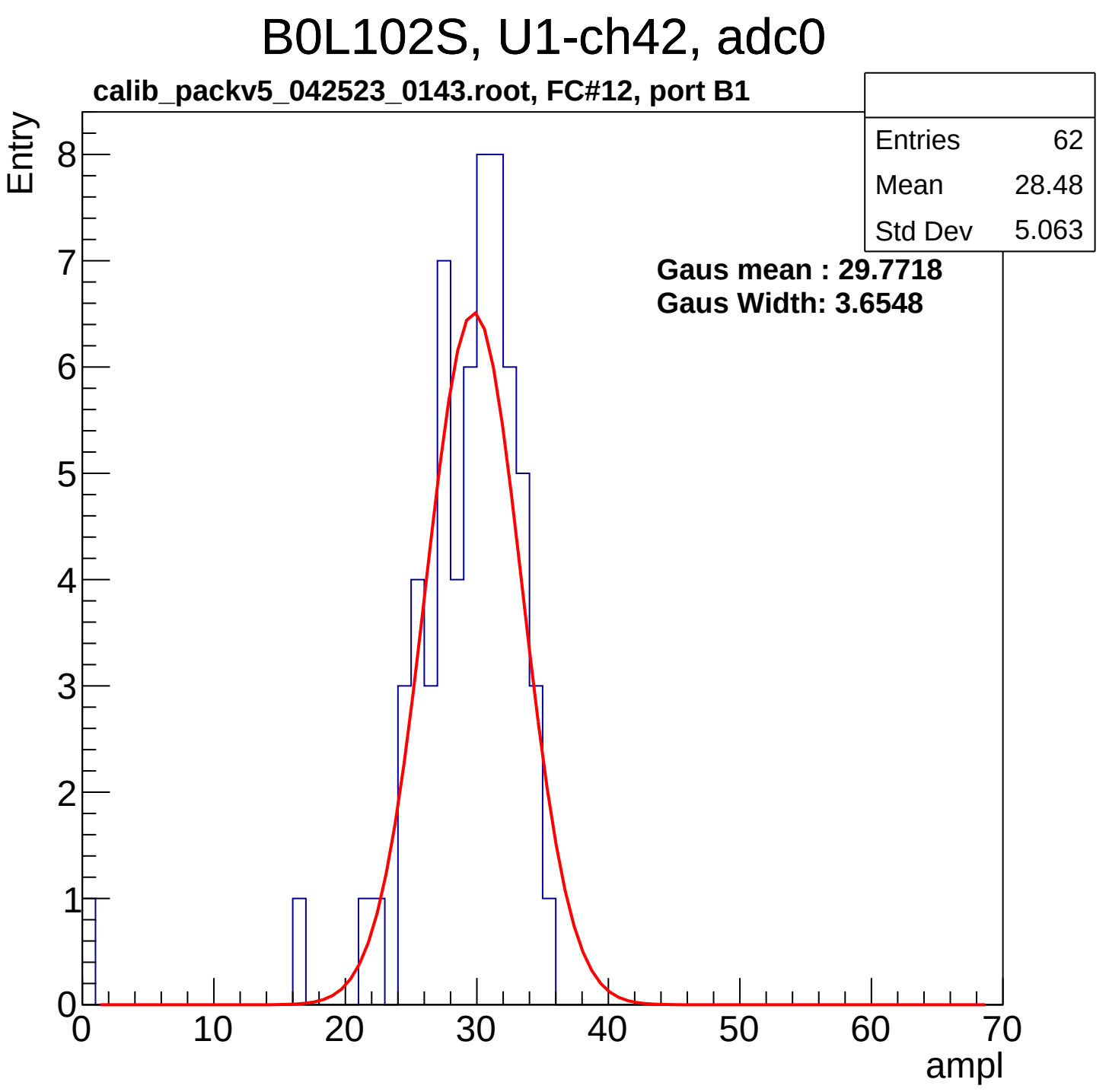
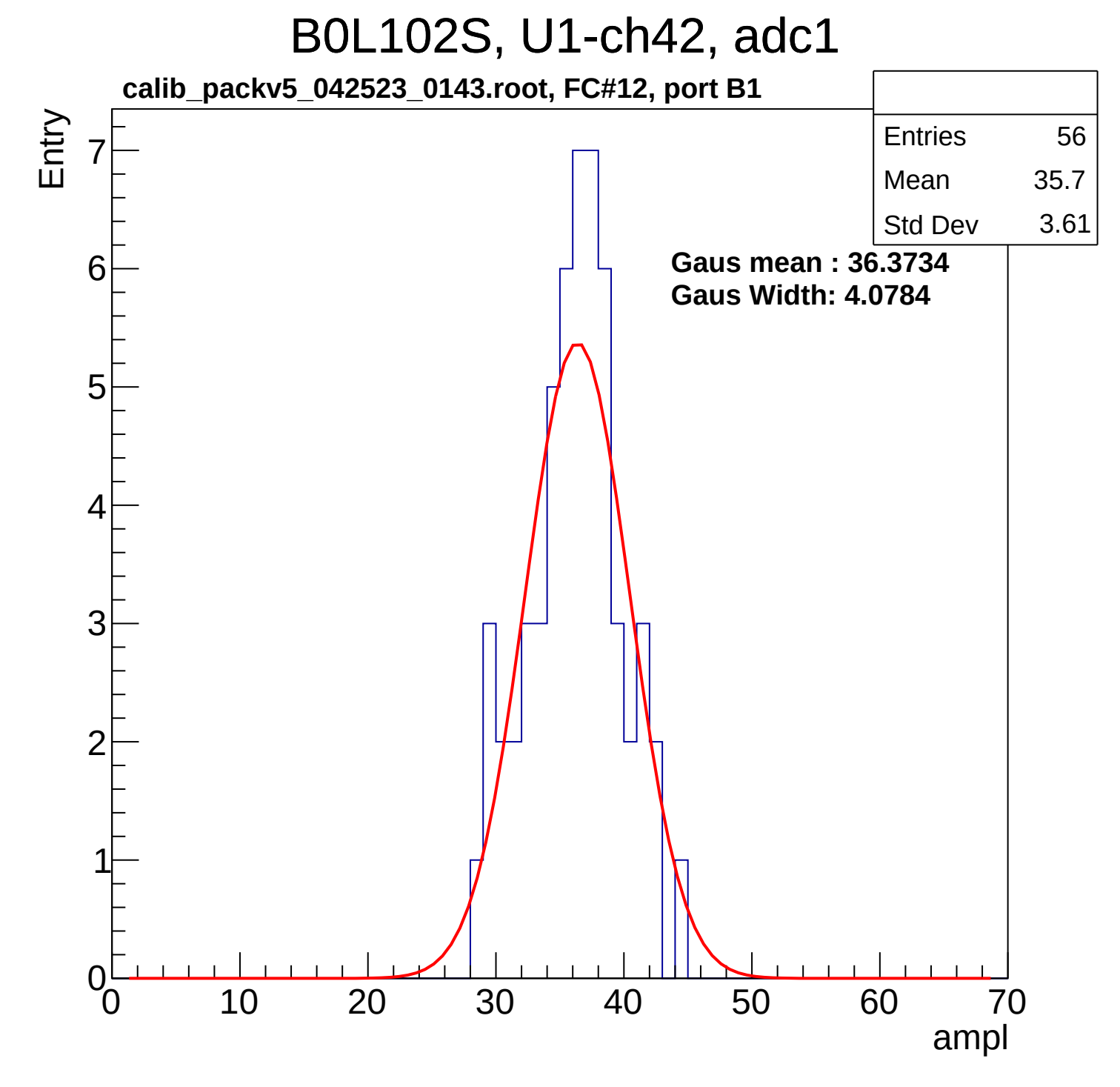
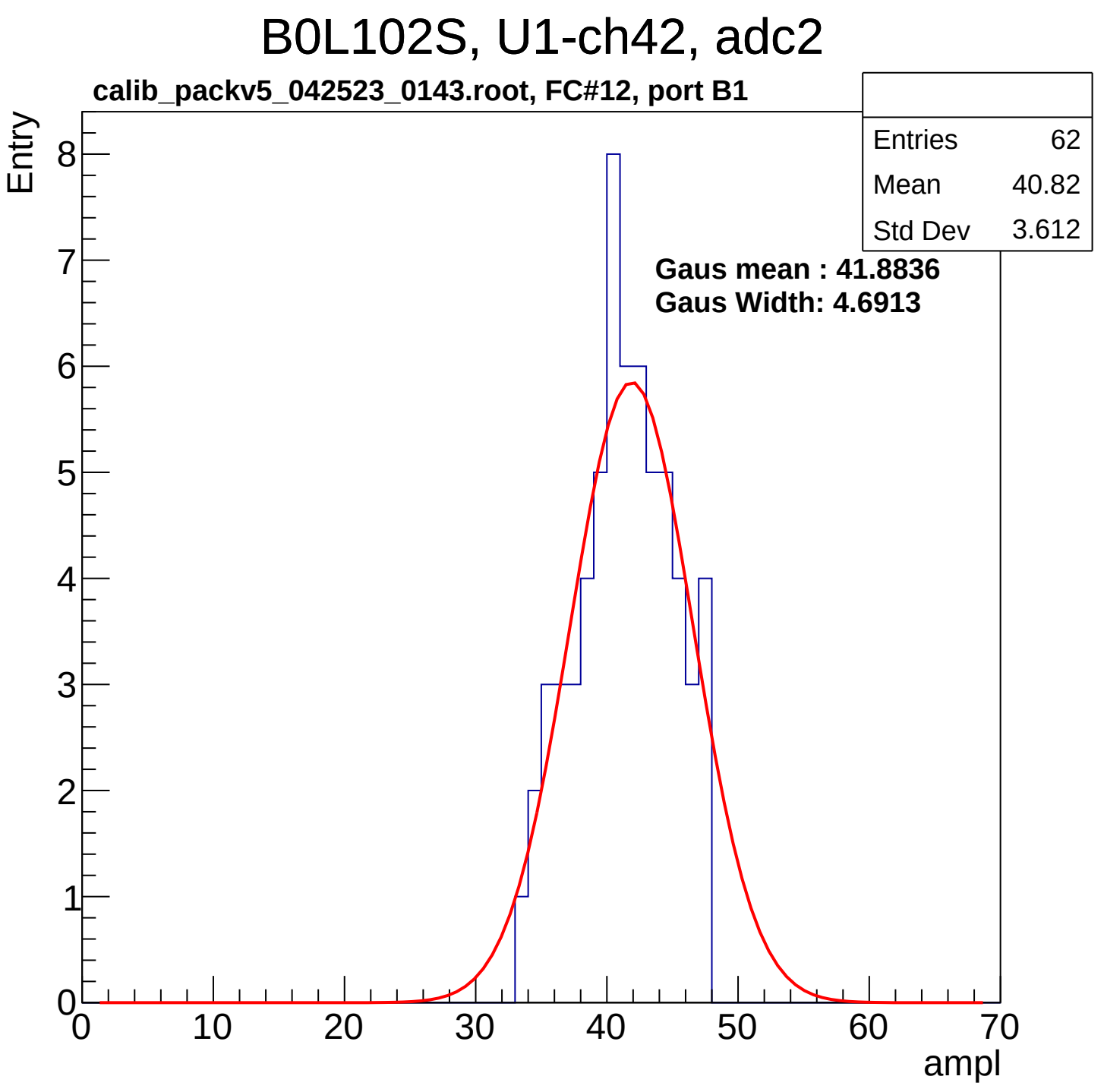
60
70

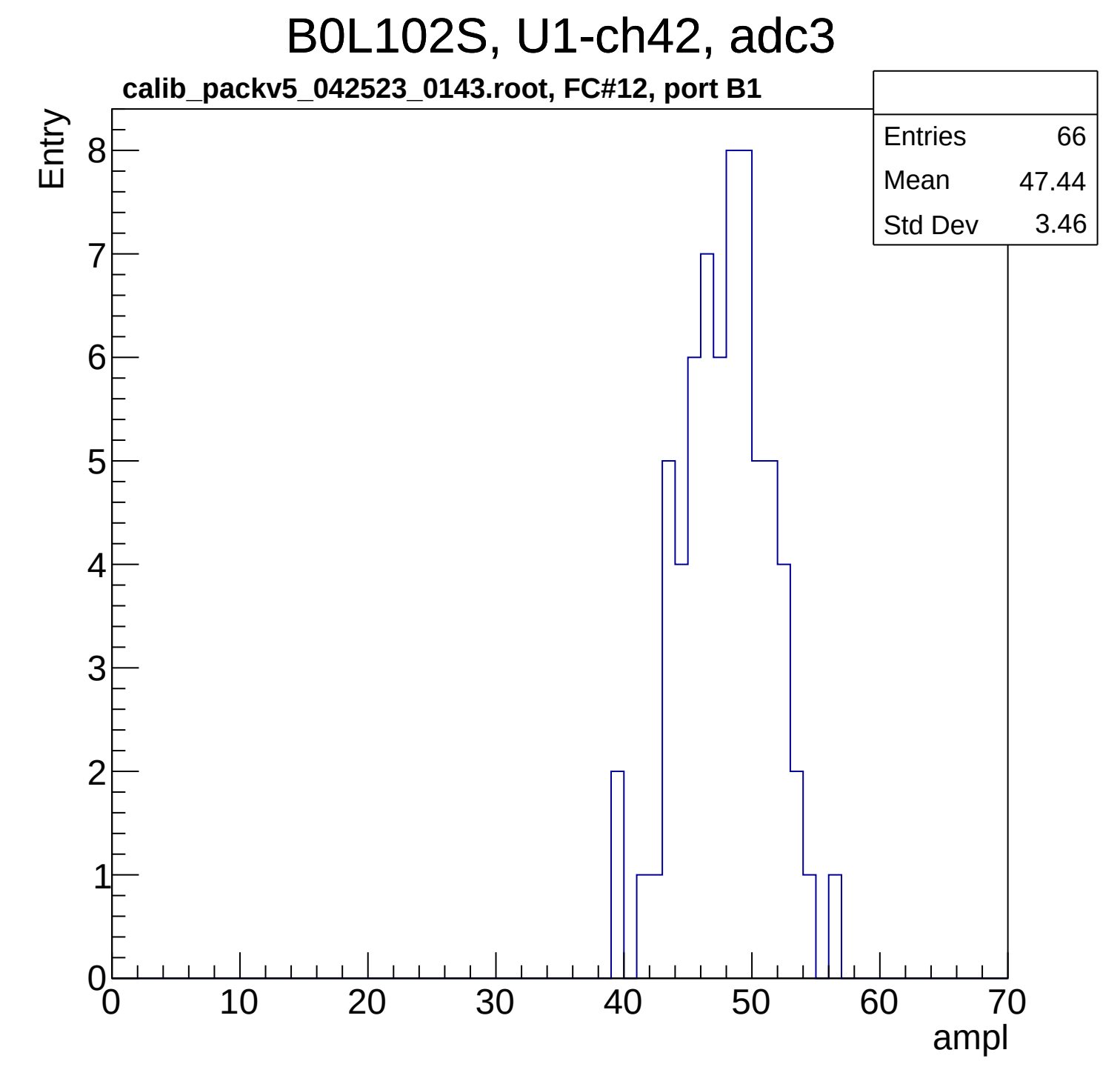

B0L102S, U1-ch41, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 20
Mean 58.65
Std Dev 13.49
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch41, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch42, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 28.48
Std Dev 5.063
Gaus mean : 29.7718
Gaus Width: 3.6548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch42, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 35.7
Std Dev 3.61
Gaus mean : 36.3734
Gaus Width: 4.0784
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch42, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 40.82
Std Dev 3.612
Gaus mean : 41.8836
Gaus Width: 4.6913
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch42, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 47.44
Std Dev 3.46
Entry
ampl
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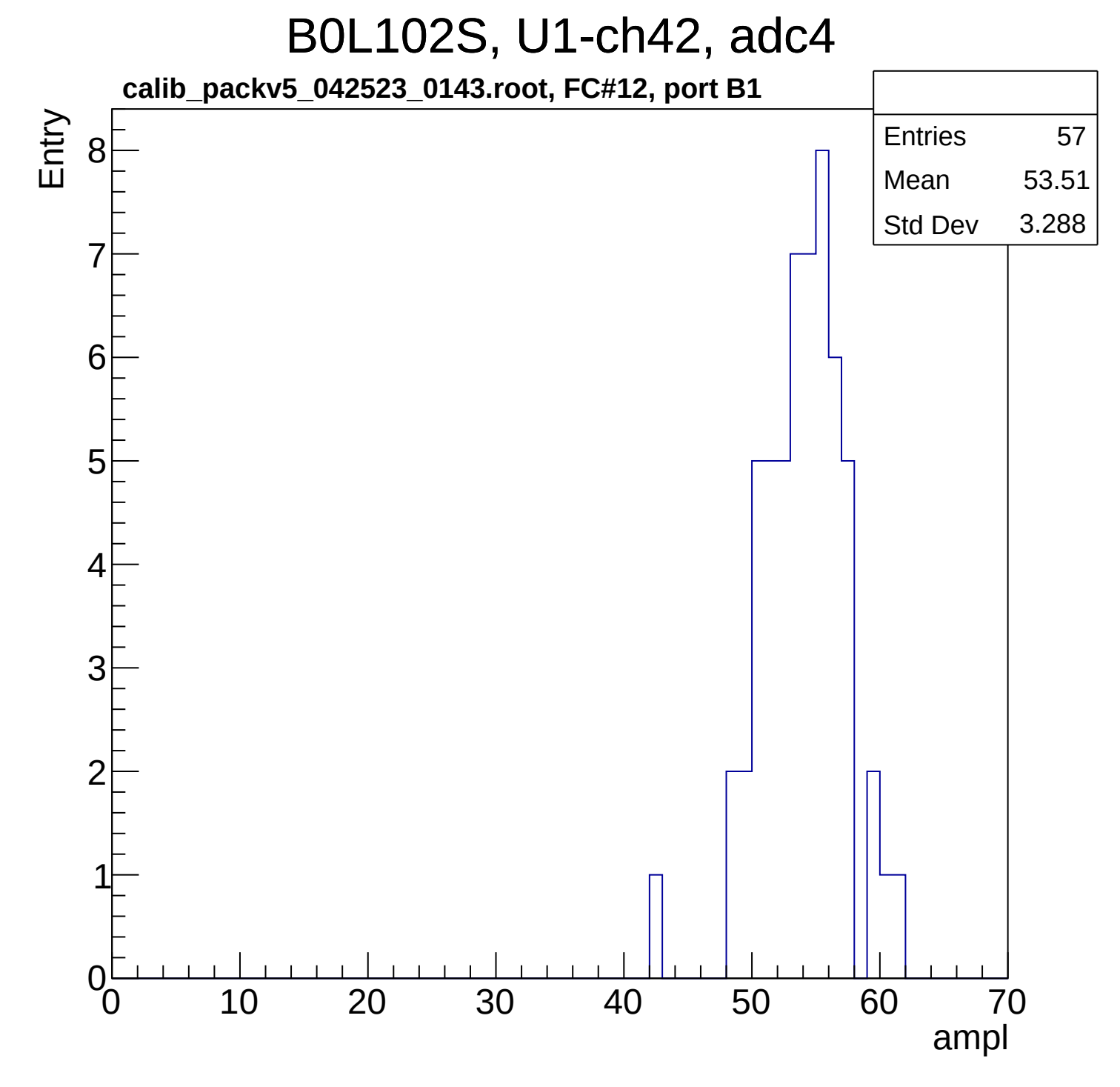
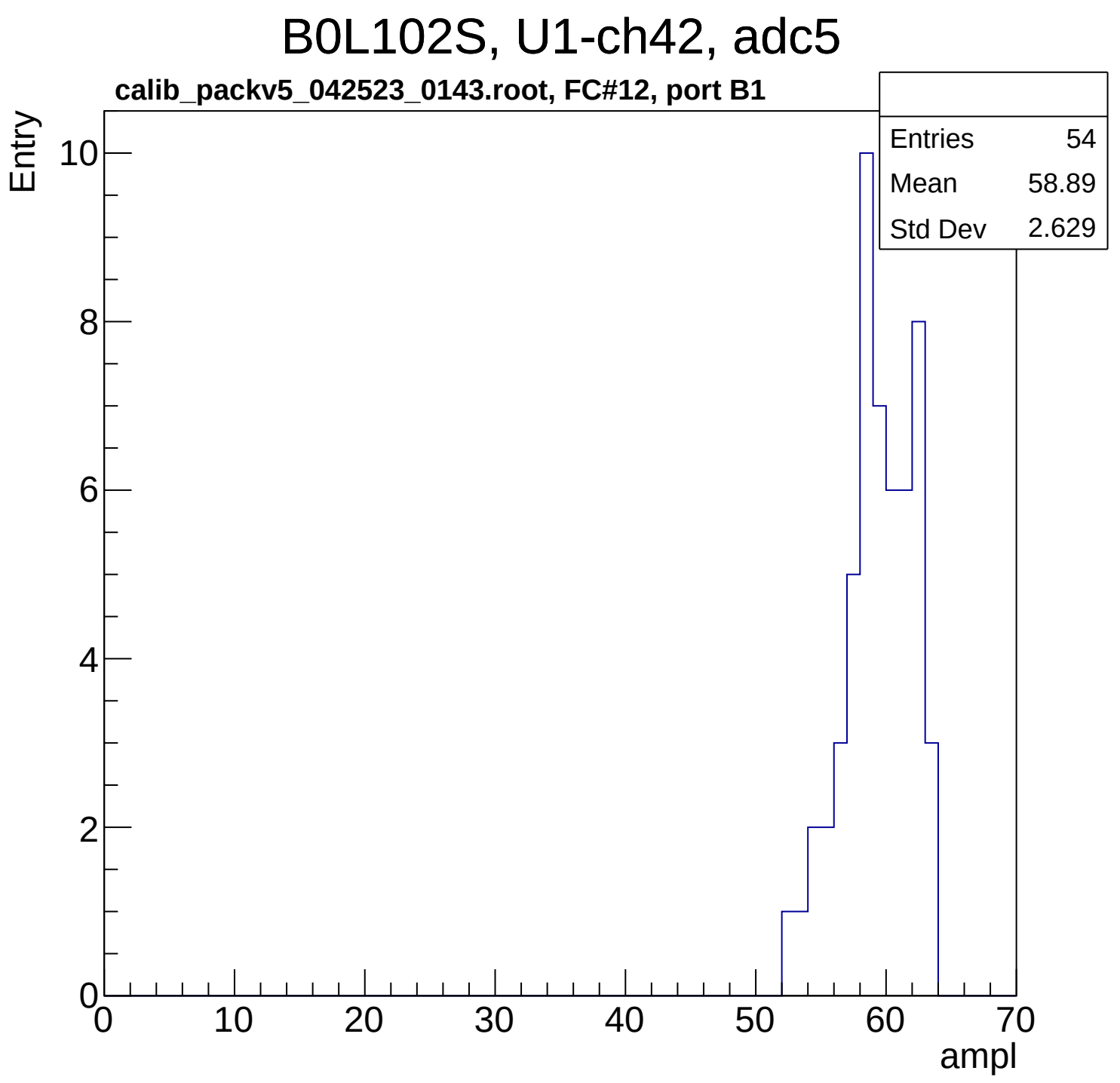
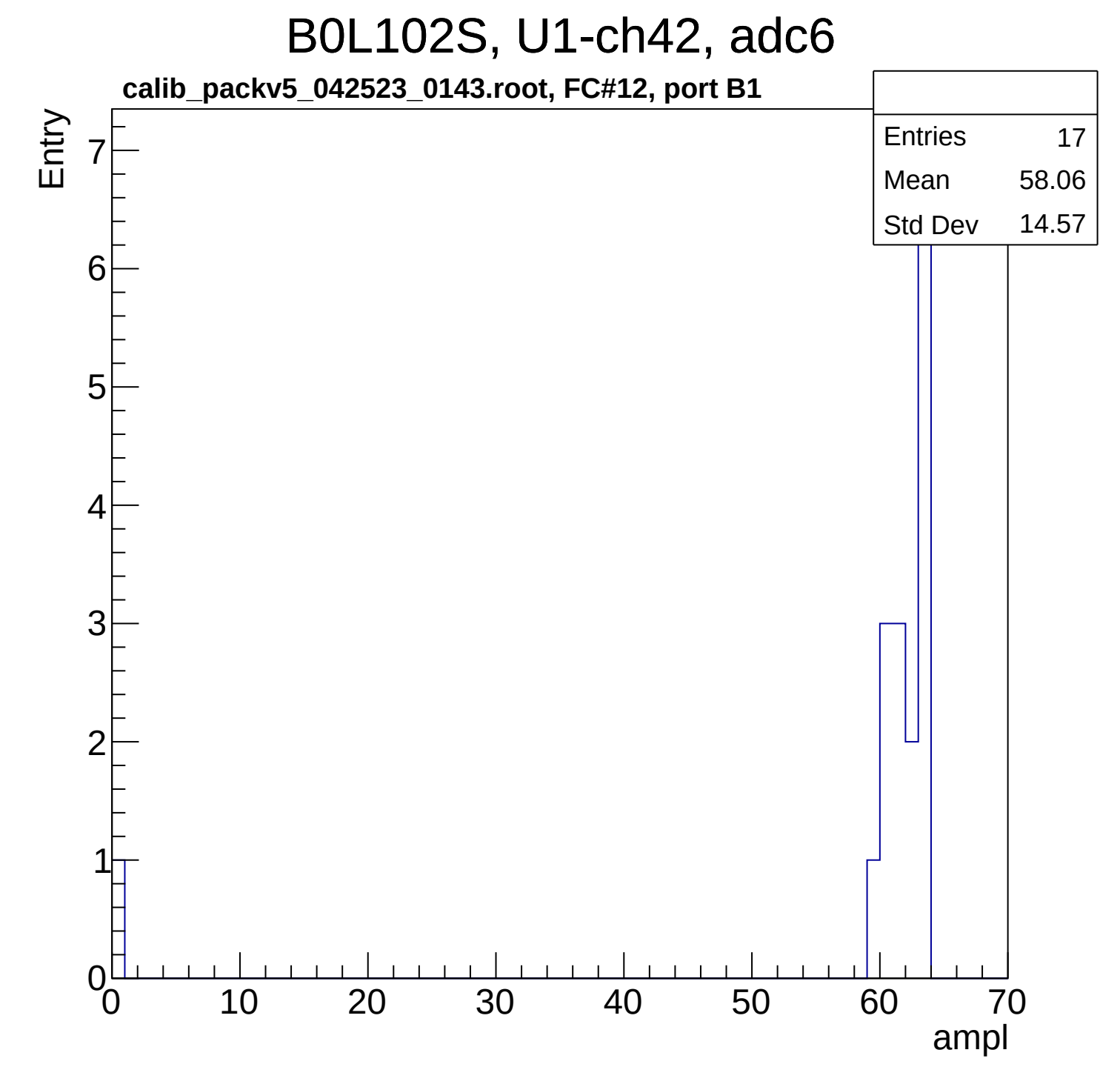
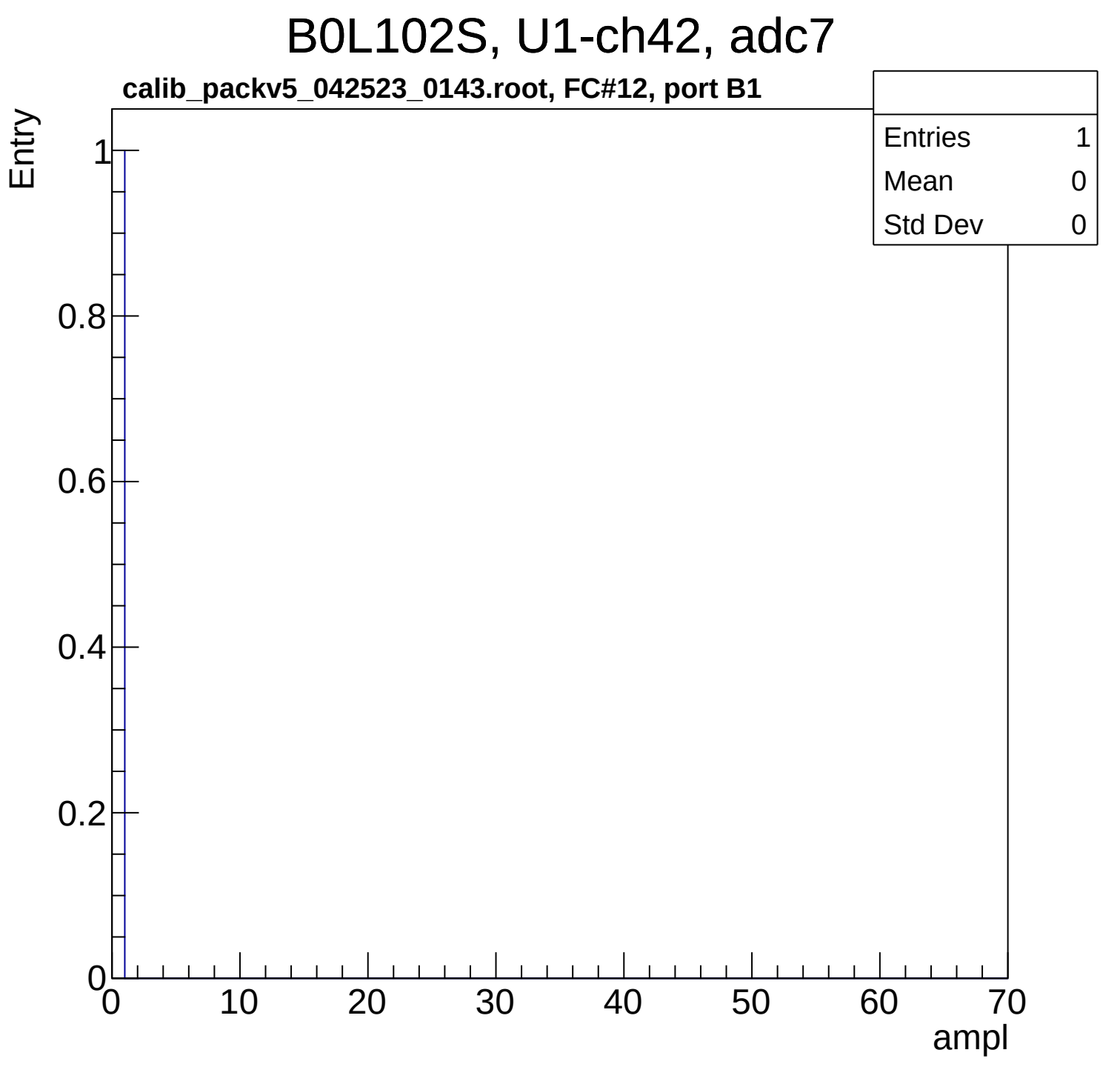
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch42, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 53.51
Std Dev 3.288
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch42, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 58.89
Std Dev 2.629
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch42, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 58.06
Std Dev 14.57
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch42, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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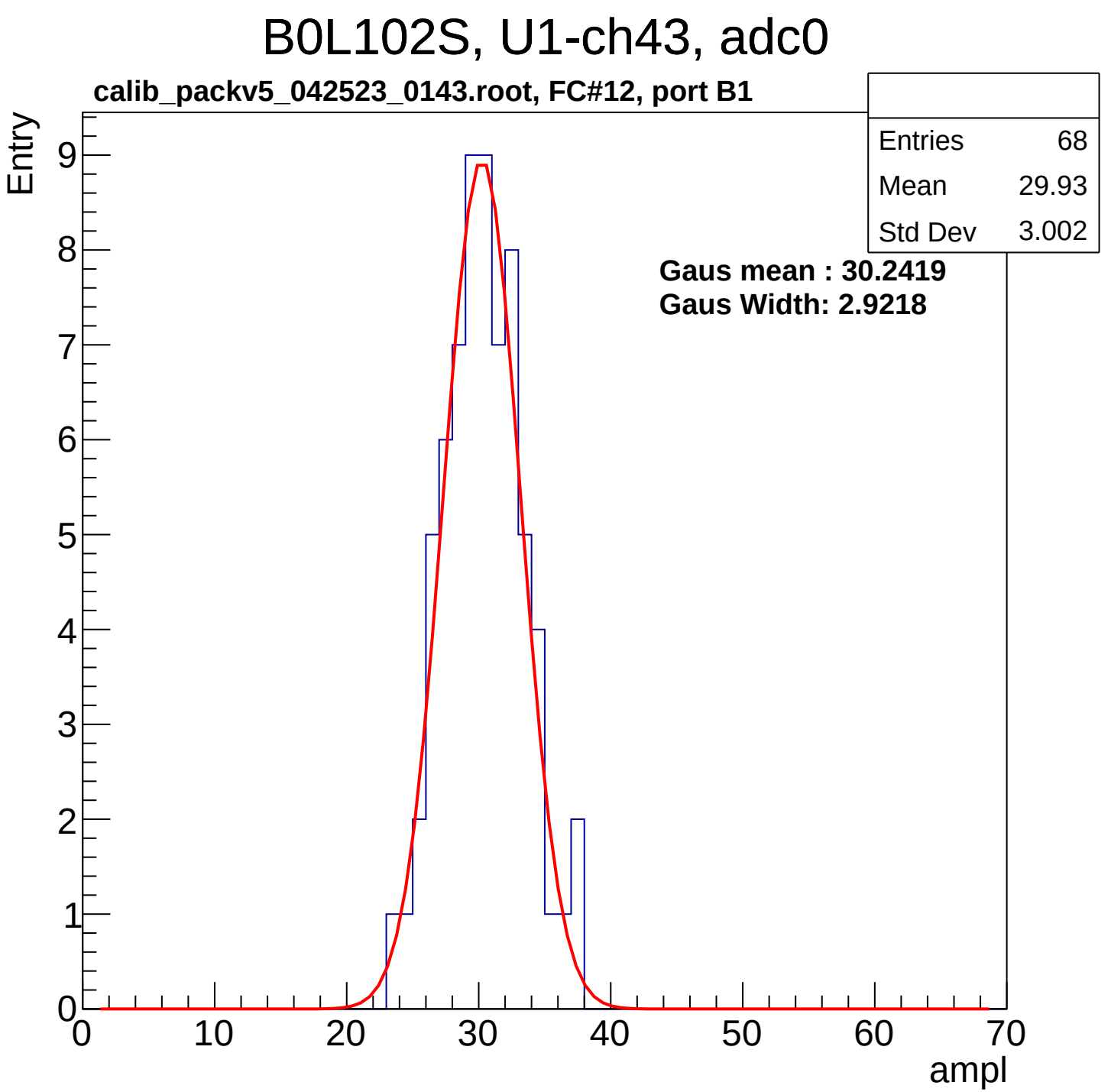
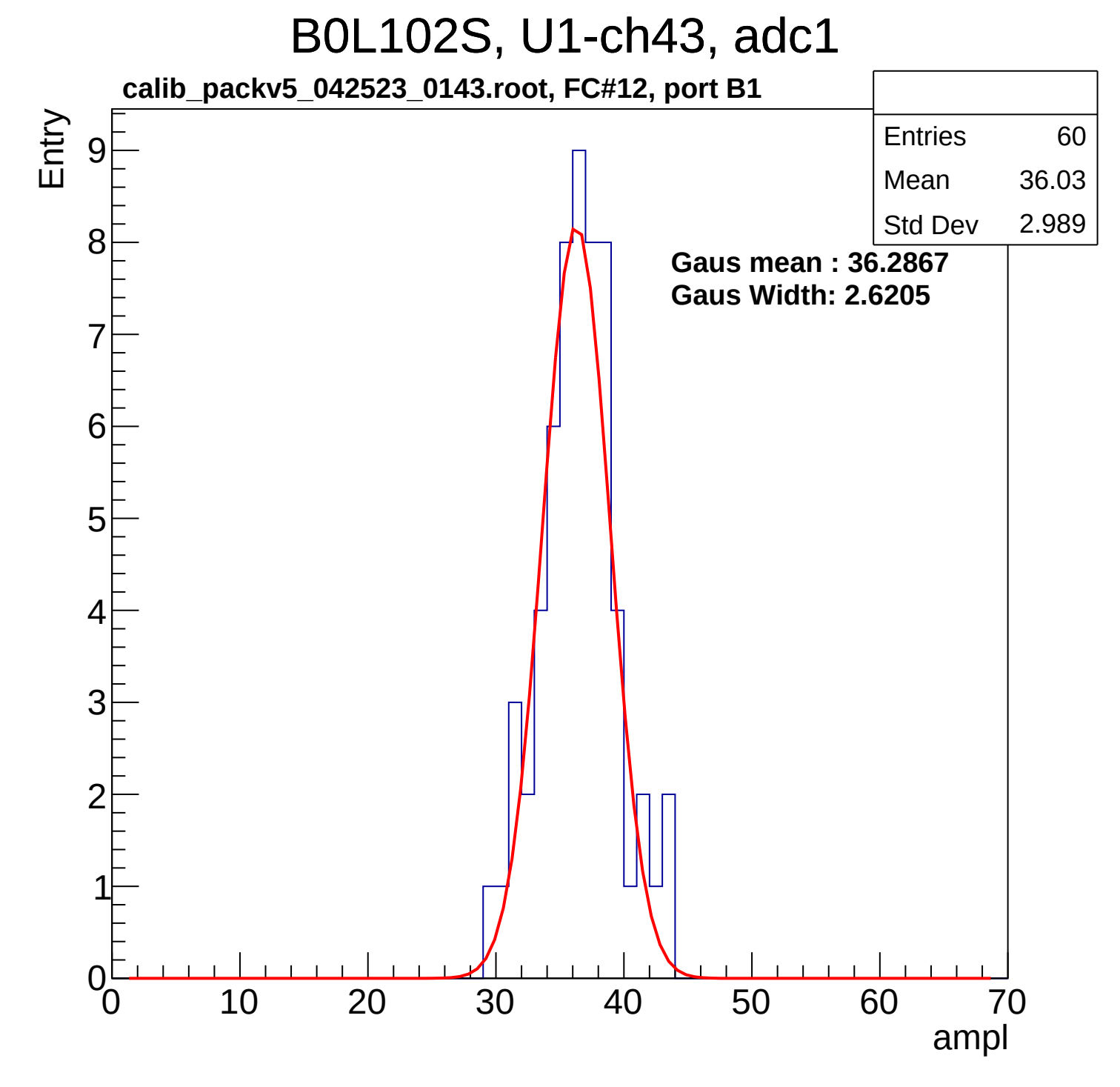
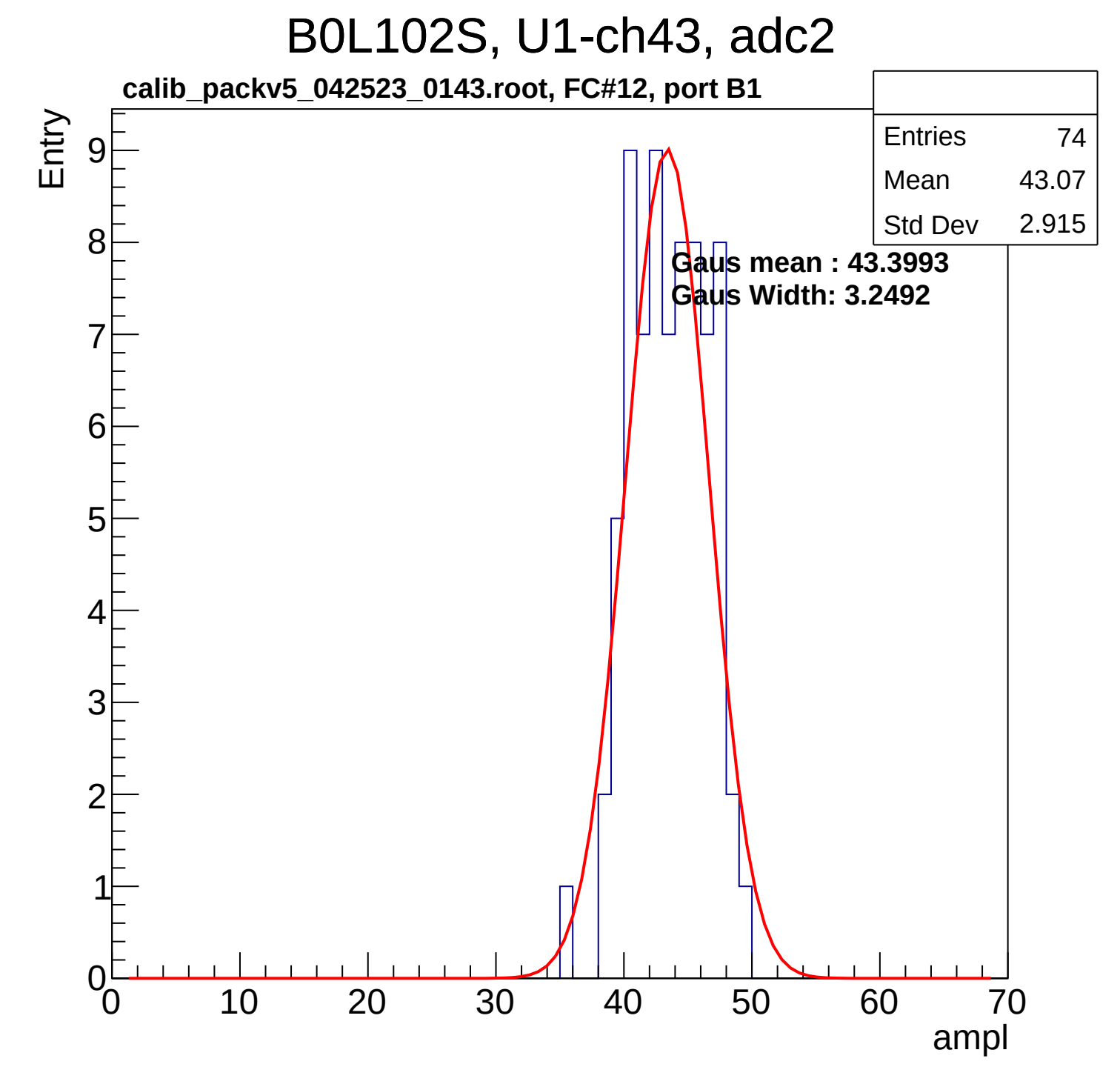
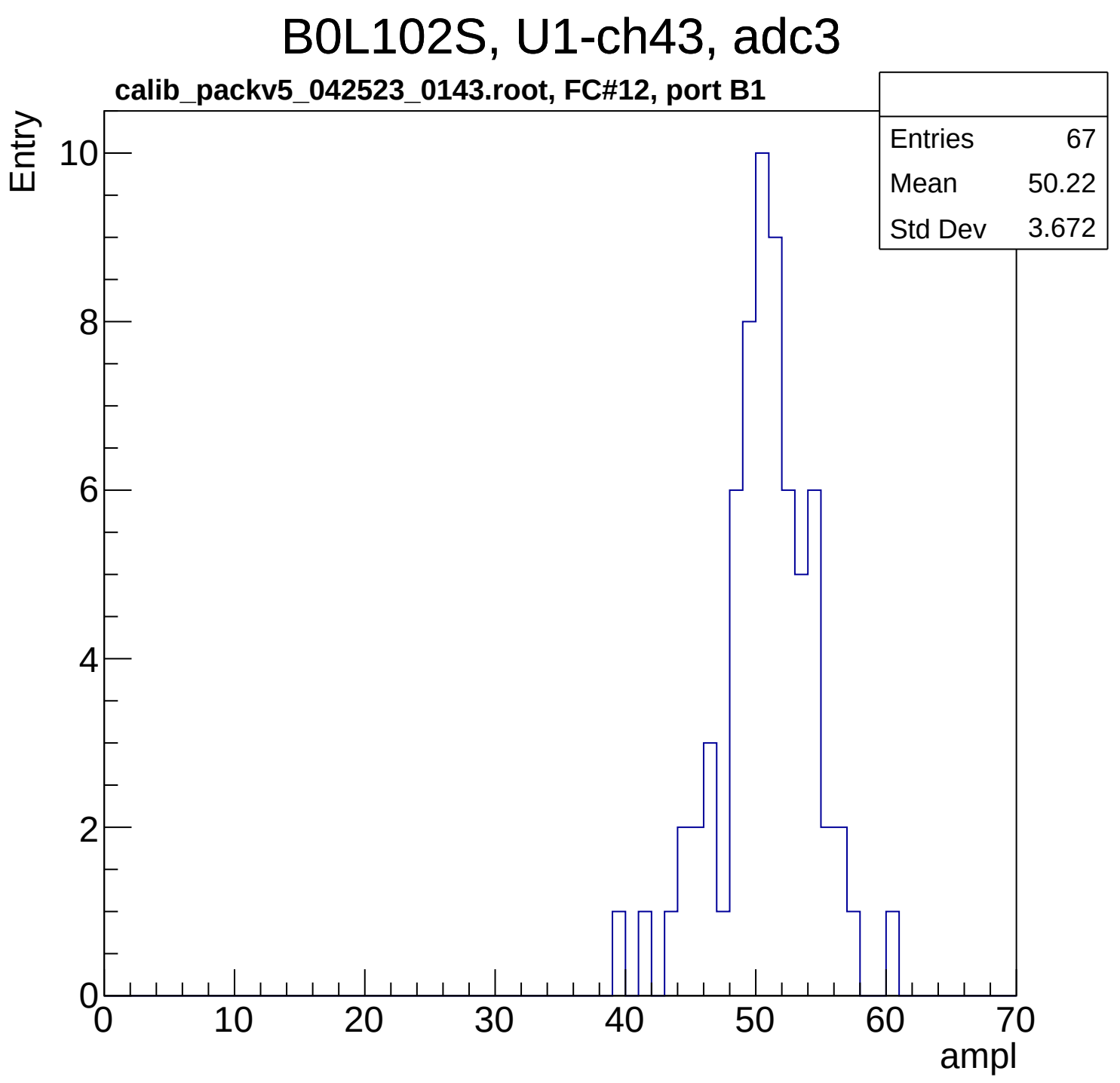
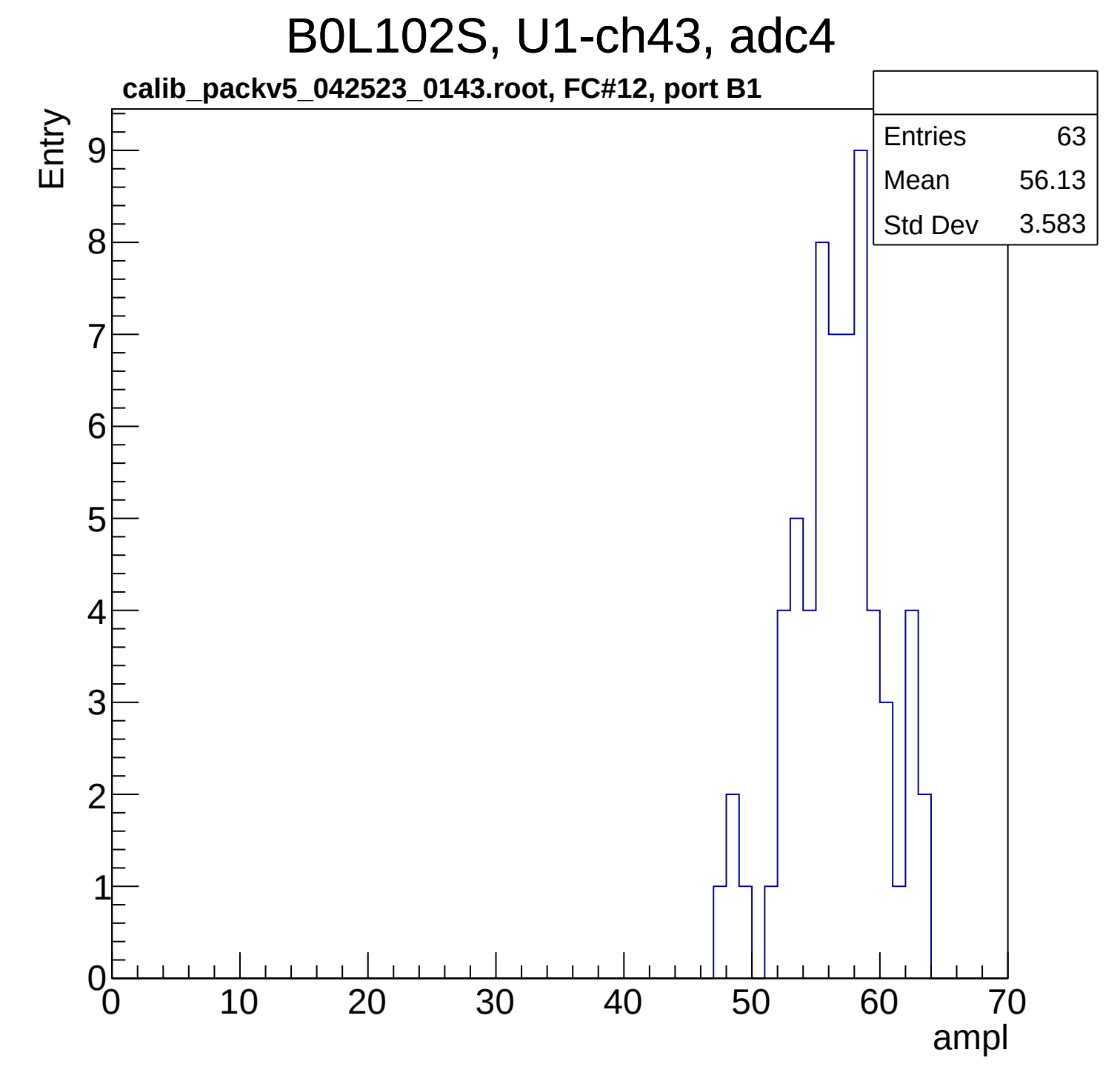
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

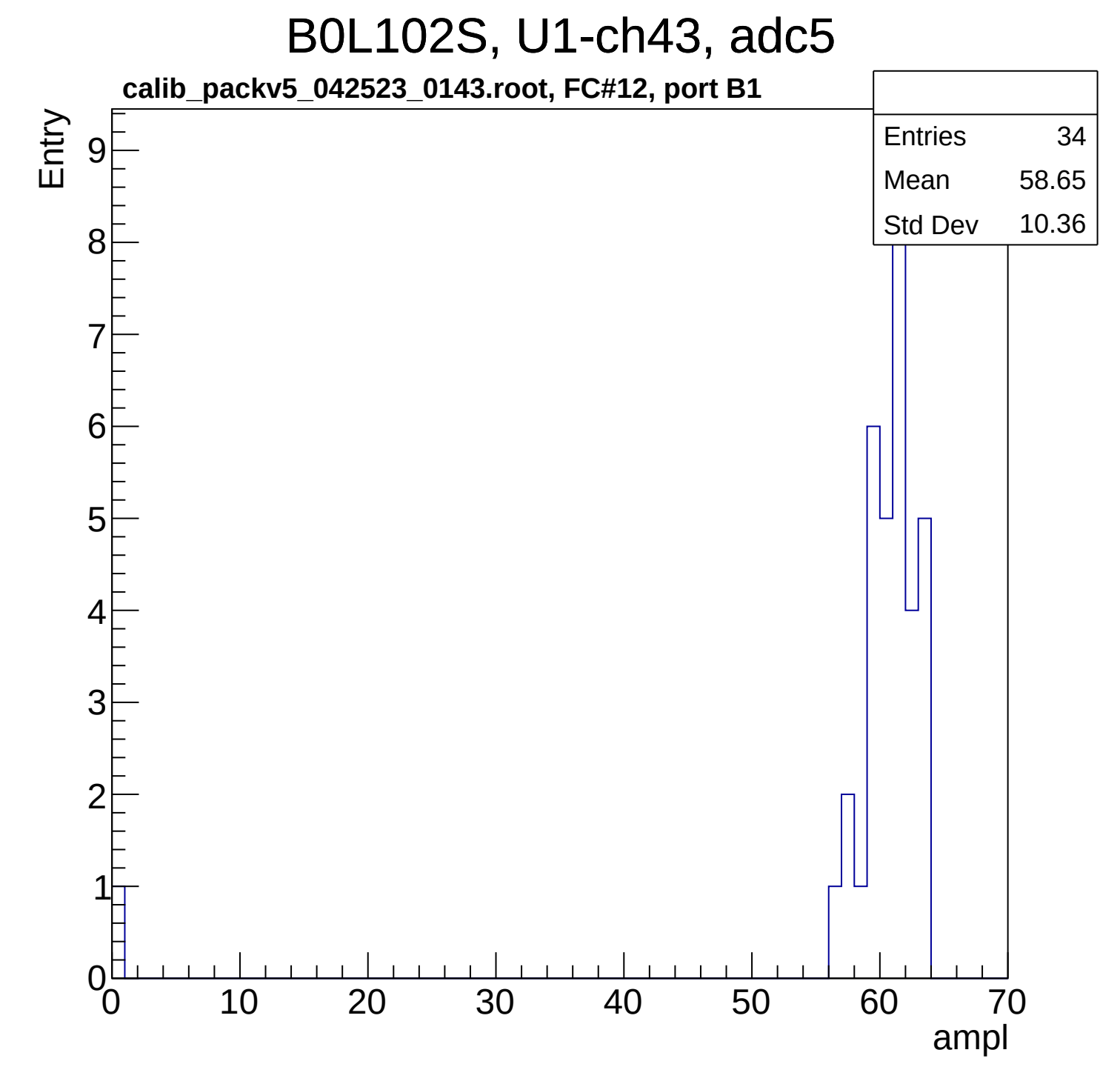

B0L102S, U1-ch43, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 29.93
Std Dev 3.002
Gaus mean : 30.2419
Gaus Width: 2.9218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

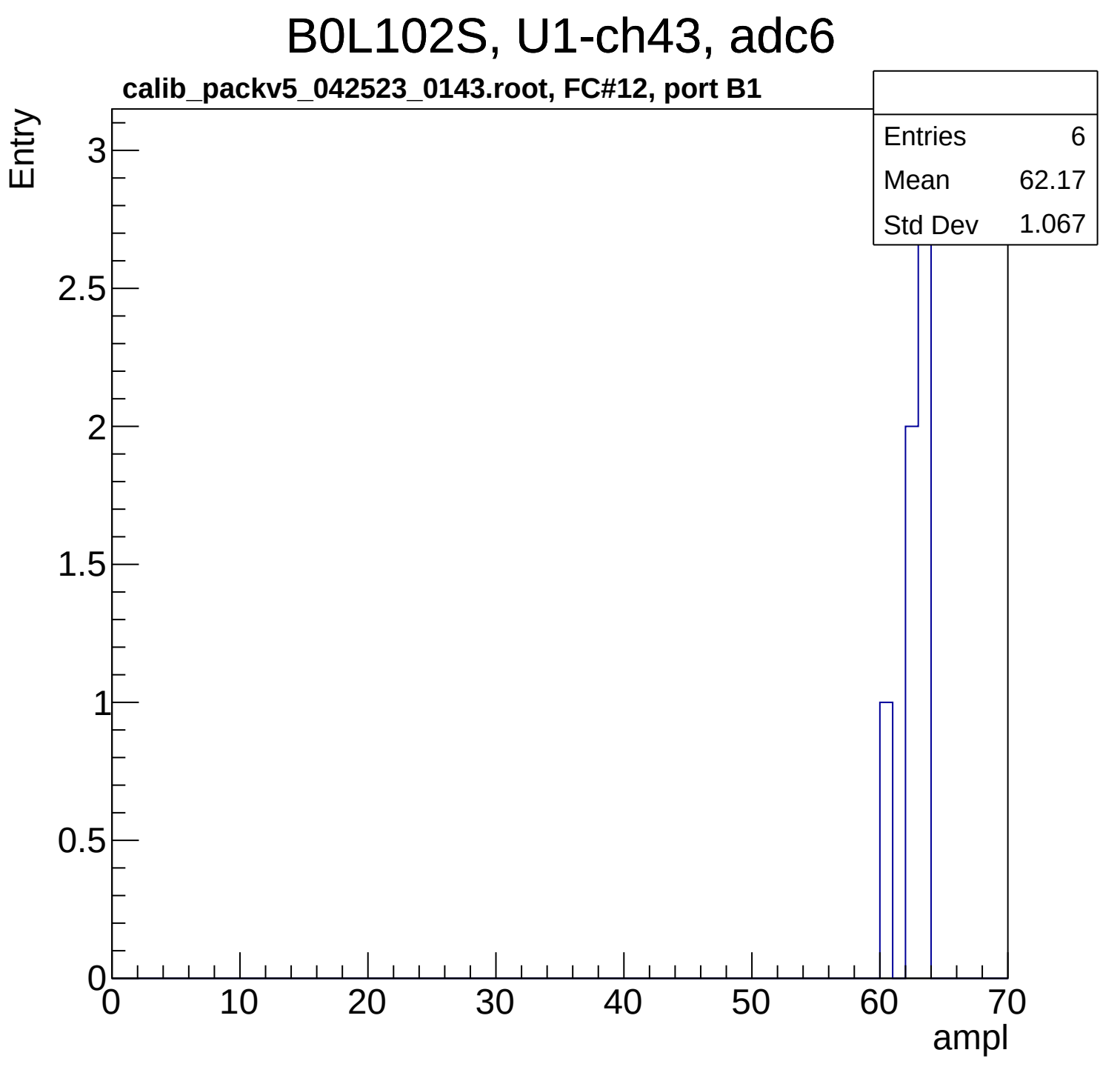

B0L102S, U1-ch43, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 36.03
Std Dev 2.989
Gaus mean : 36.2867
Gaus Width: 2.6205
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch43, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 43.07
Std Dev 2.915
Gaus mean : 43.3993
Gaus Width: 3.2492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch43, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.22
Std Dev 3.672
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch43, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 56.13
Std Dev 3.583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch43, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 58.65
Std Dev 10.36
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch43, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
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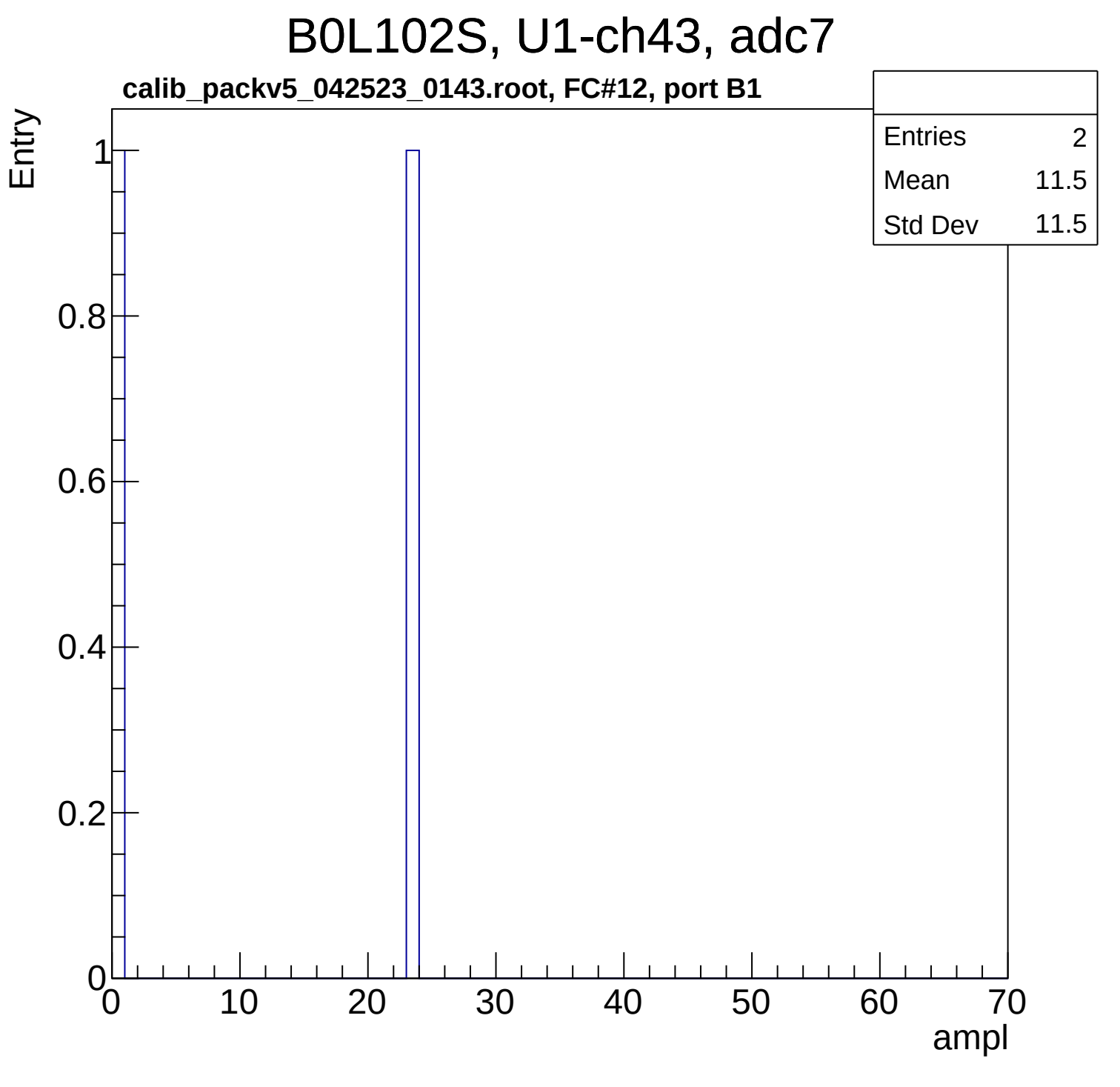
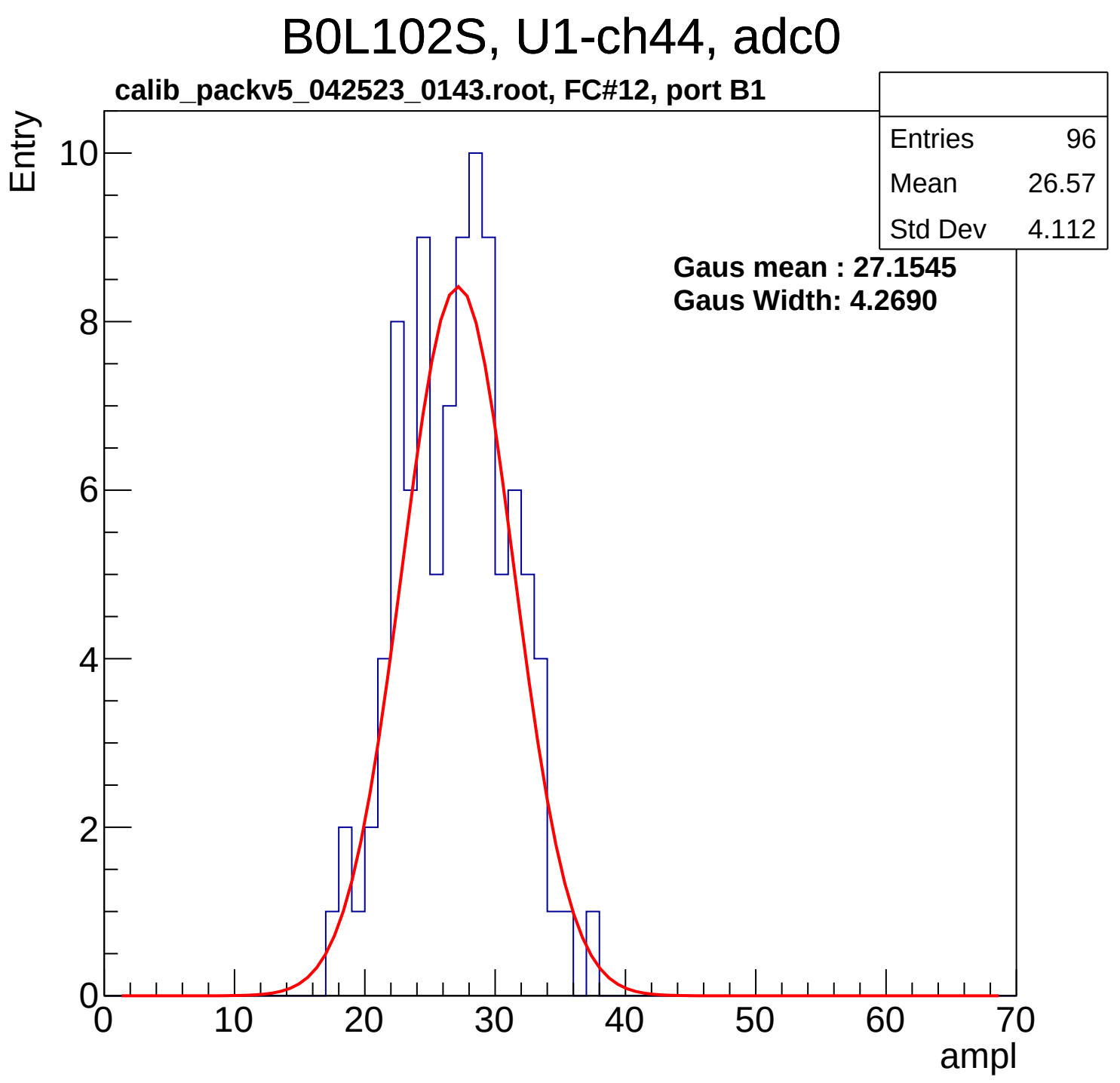
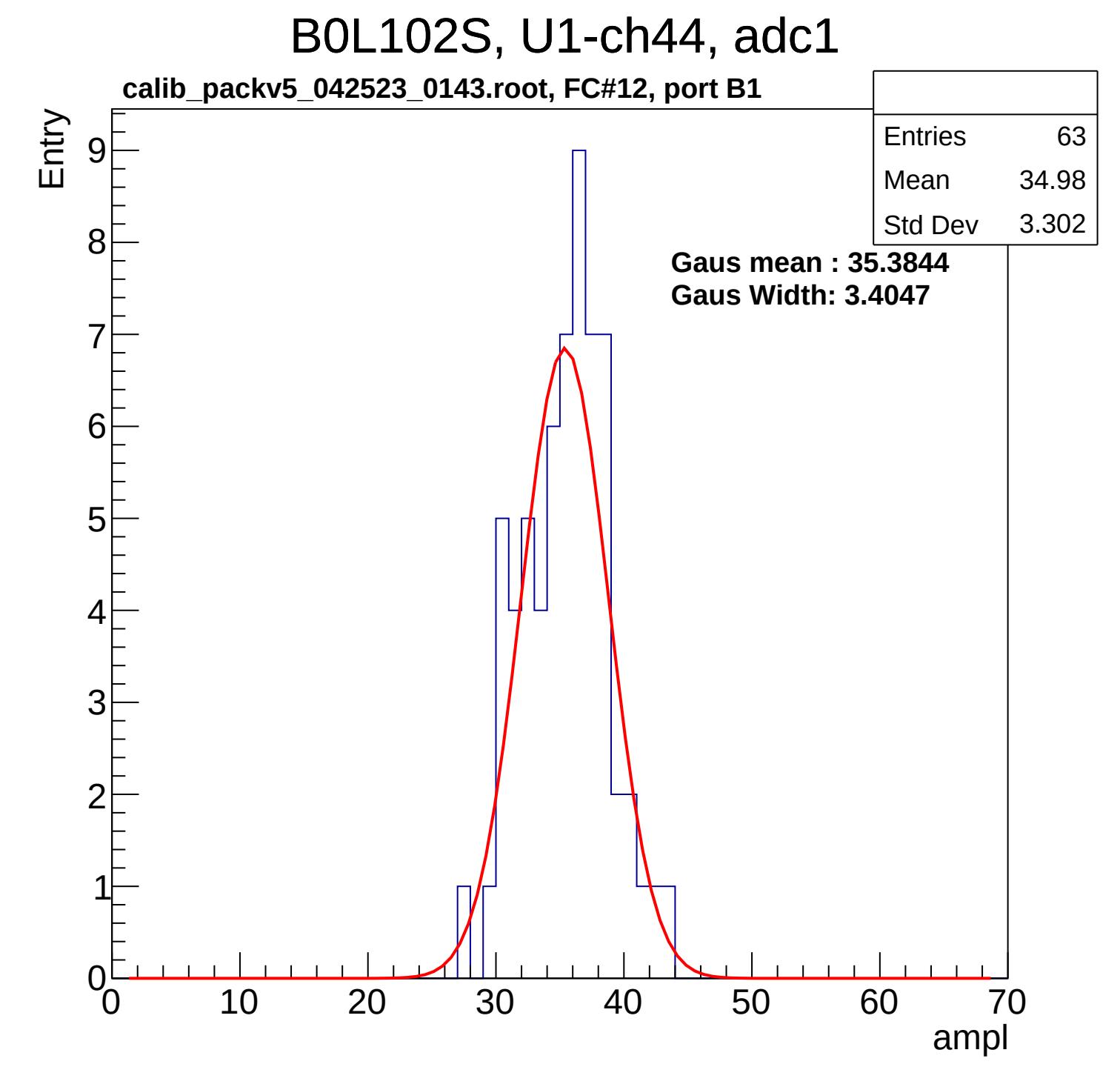
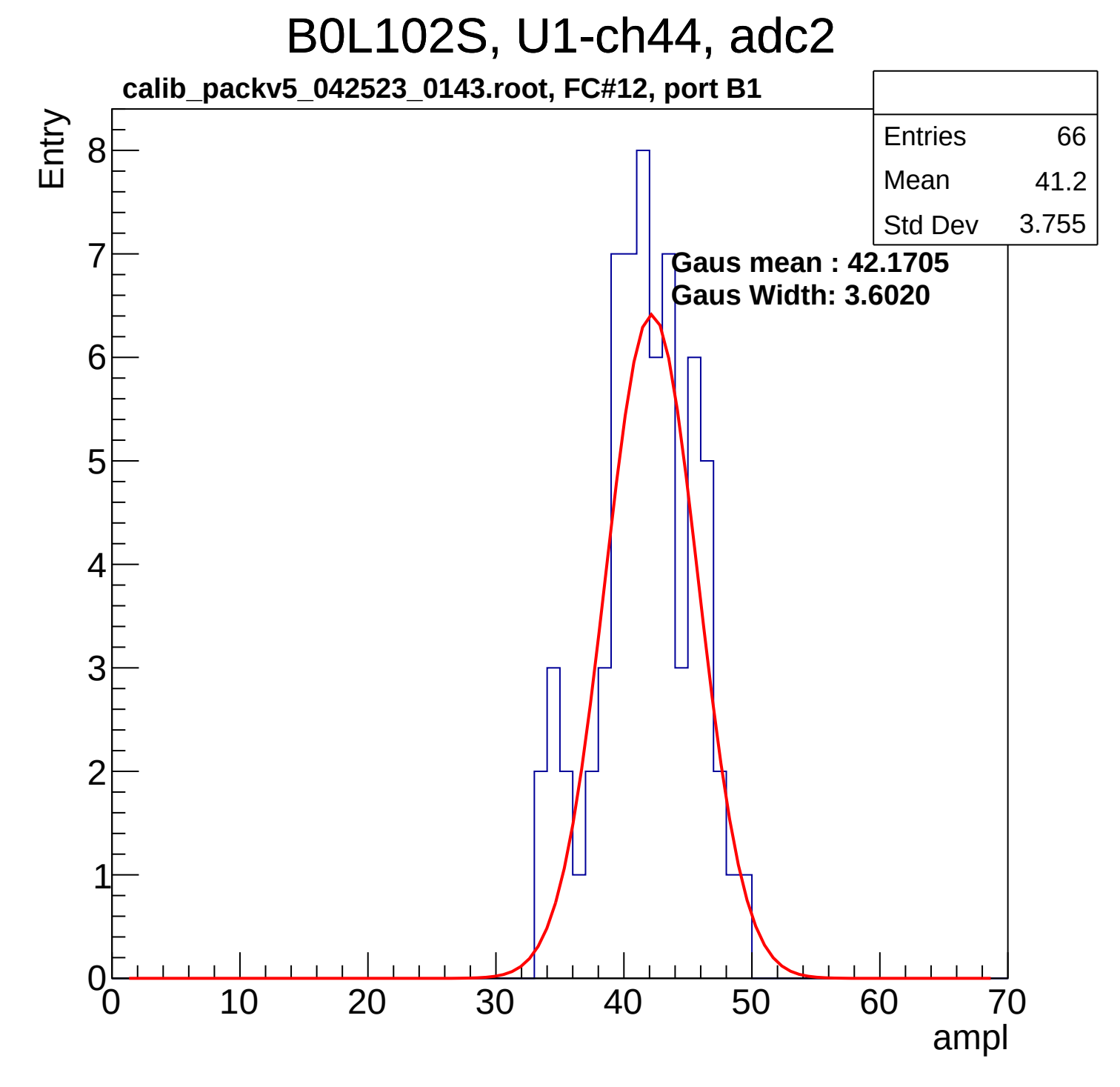
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch43, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch44, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 96
Mean 26.57
Std Dev 4.112
Gaus mean : 27.1545
Gaus Width: 4.2690
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch44, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 34.98
Std Dev 3.302
Gaus mean : 35.3844
Gaus Width: 3.4047
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch44, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 41.2
Std Dev 3.755
Gaus mean : 42.1705
Gaus Width: 3.6020
Entry
ampl
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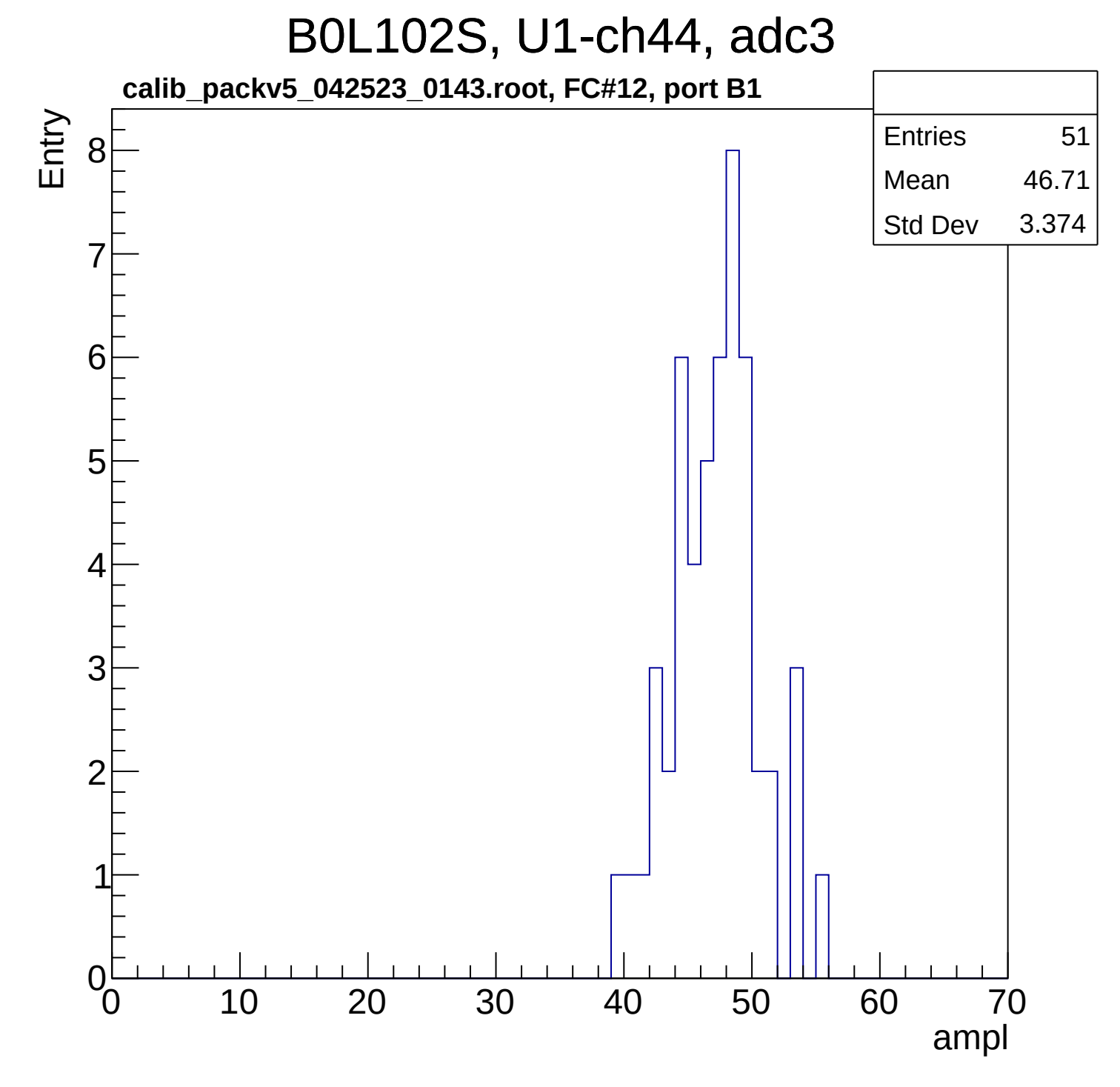
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch44, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 46.71
Std Dev 3.374
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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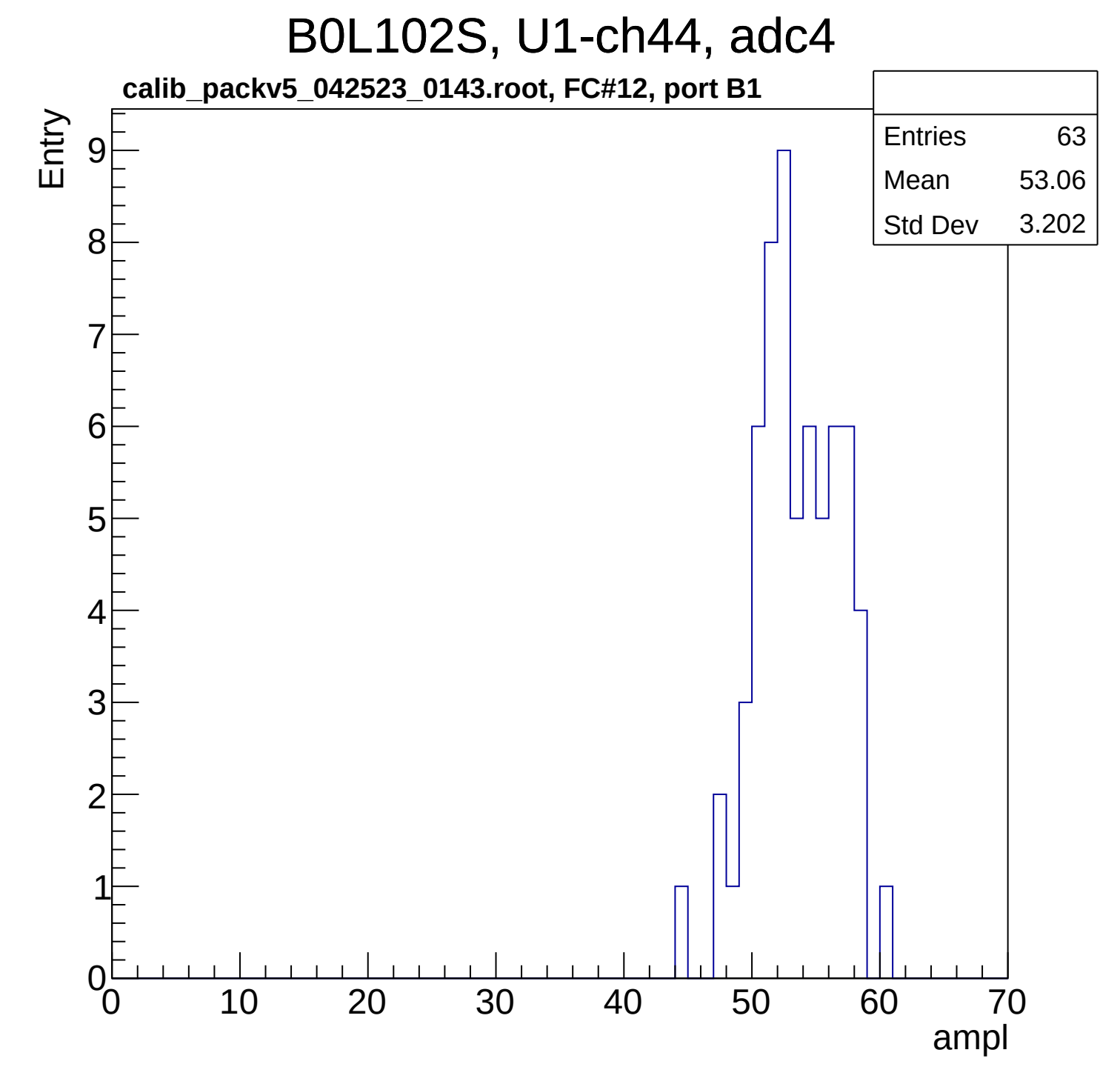
B0L102S, U1-ch44, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 53.06
Std Dev 3.202
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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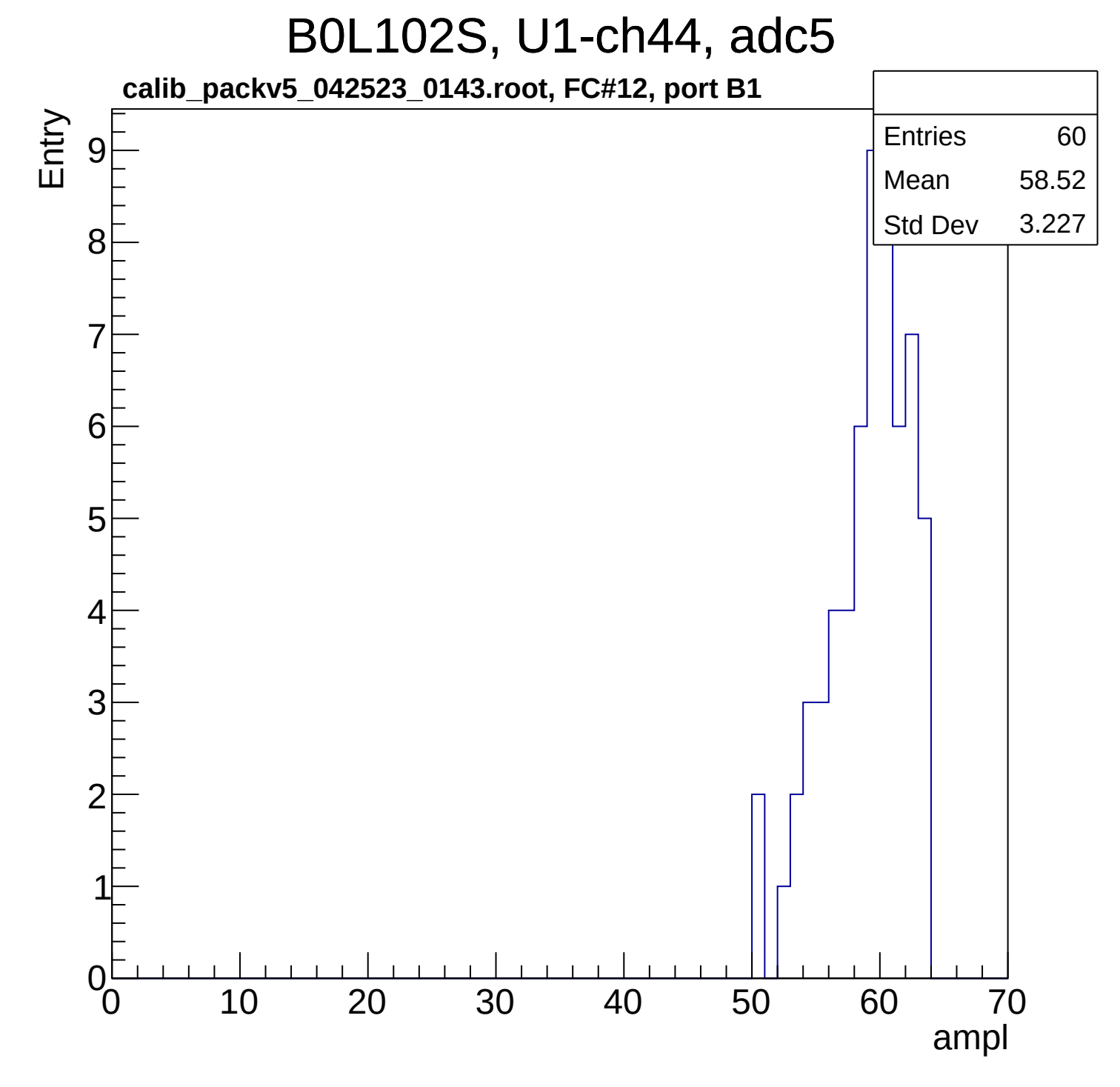
30
40
50
60
70


B0L102S, U1-ch44, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.52
Std Dev 3.227
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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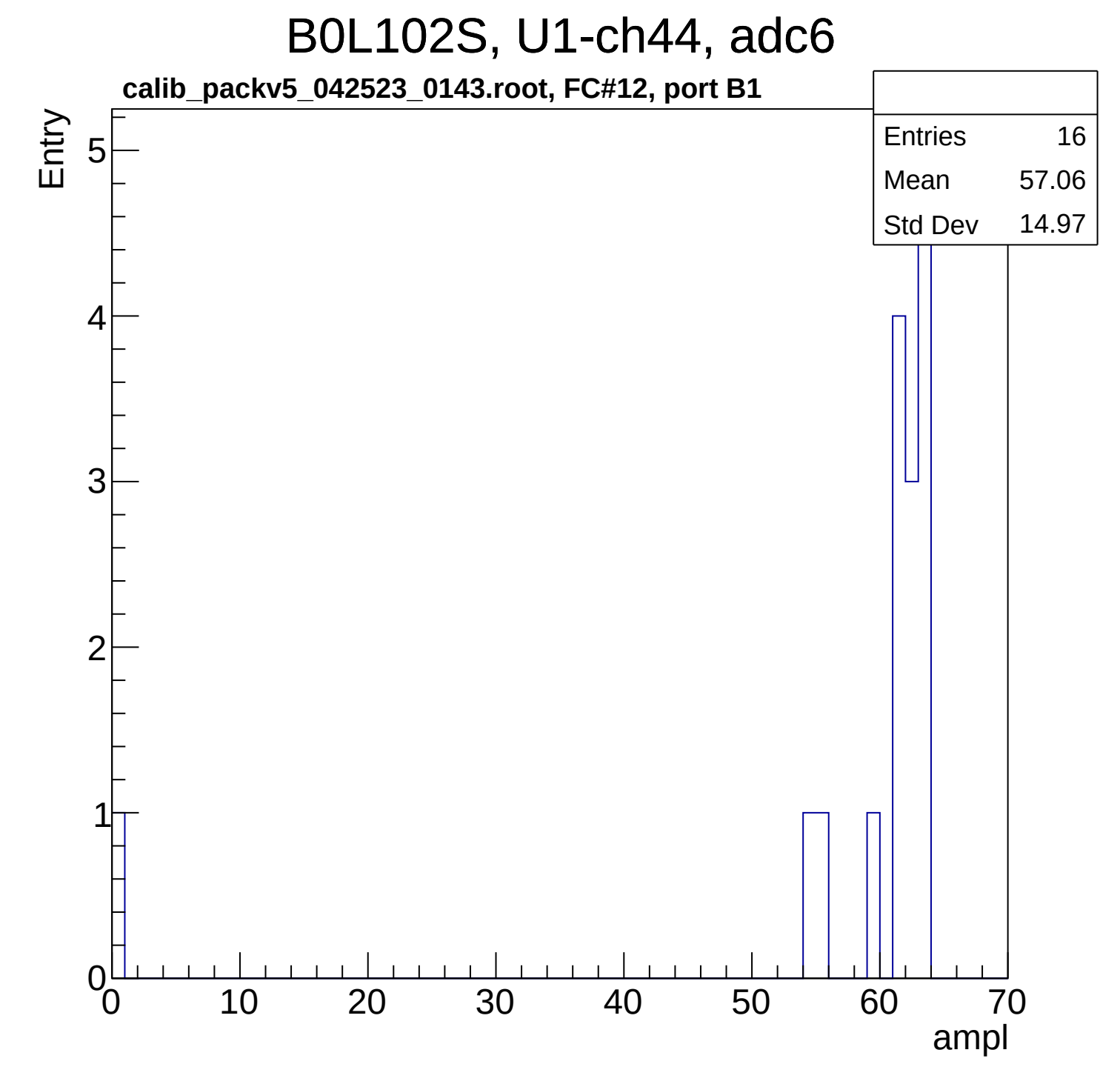
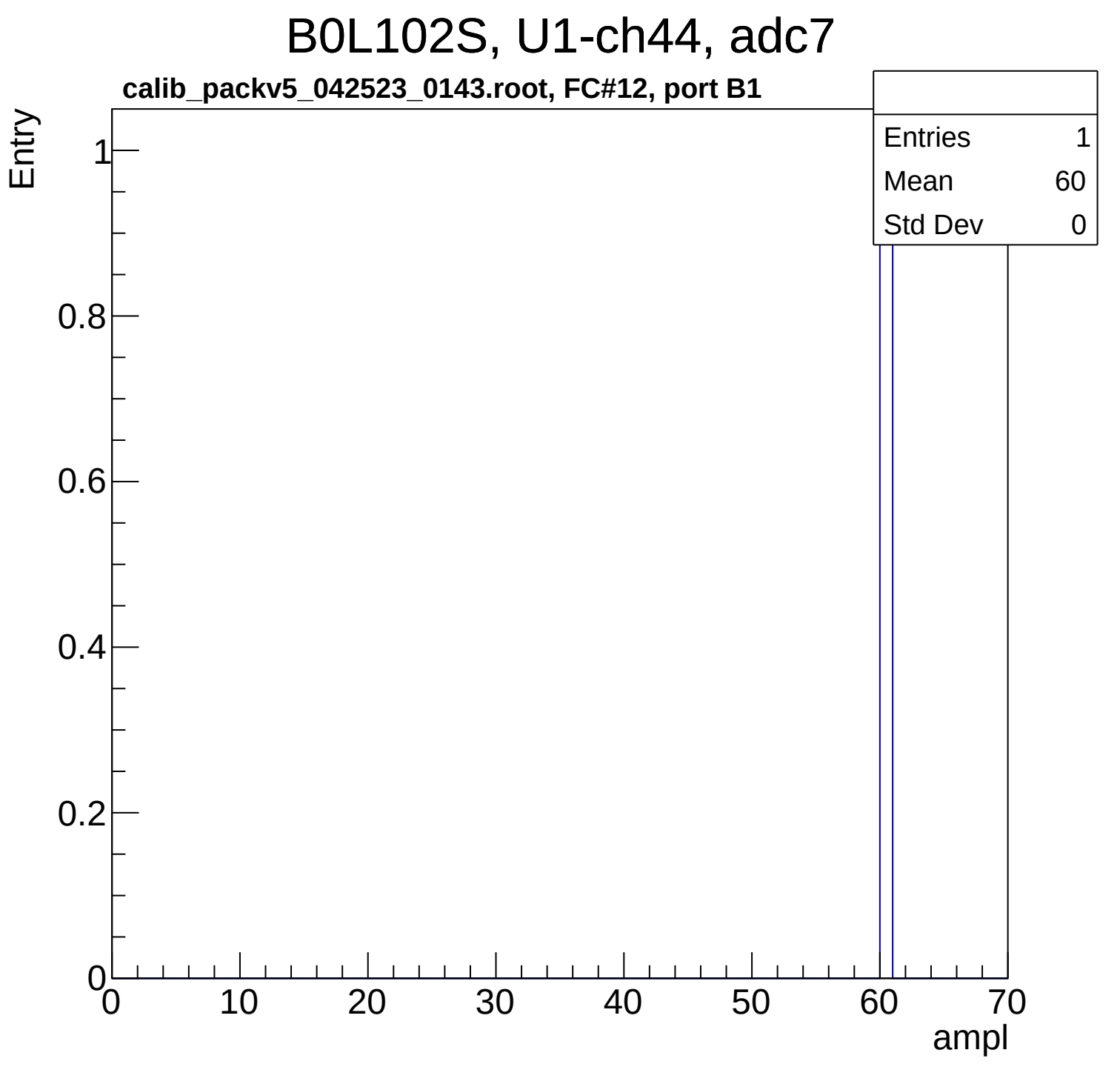
40
50
60
70


B0L102S, U1-ch44, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 57.06
Std Dev 14.97
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch44, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 60
Std Dev 0
Entry
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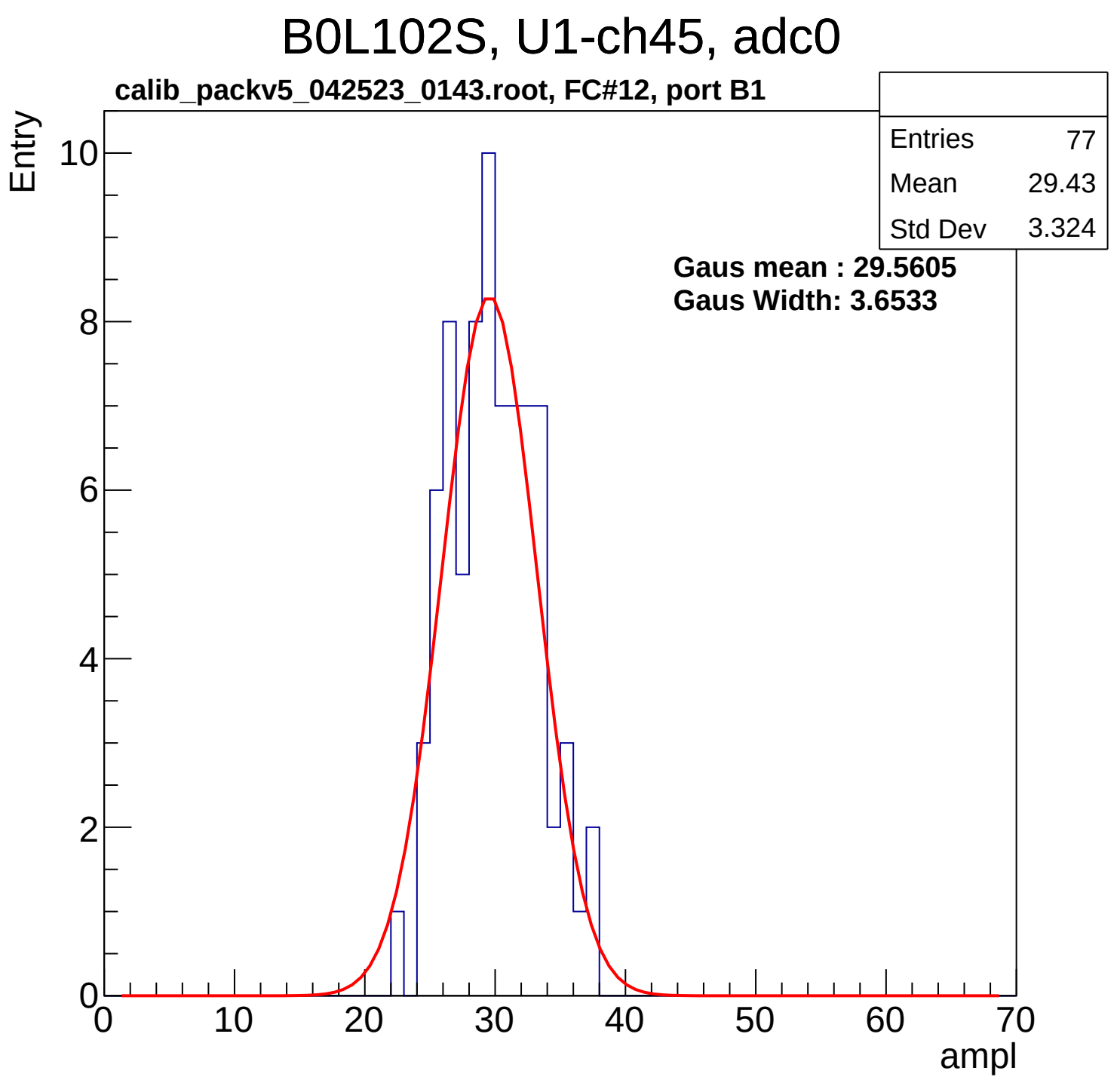
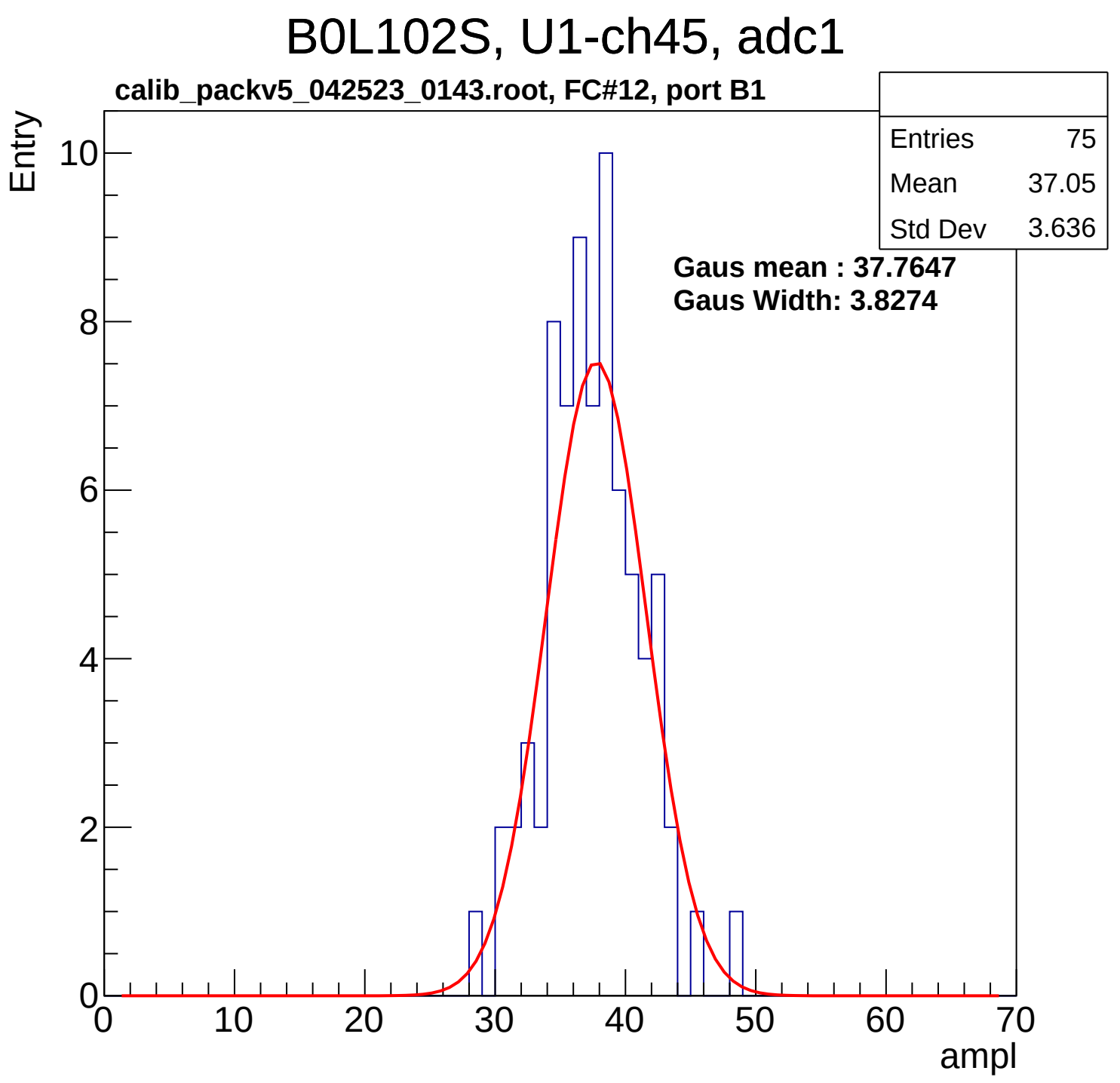
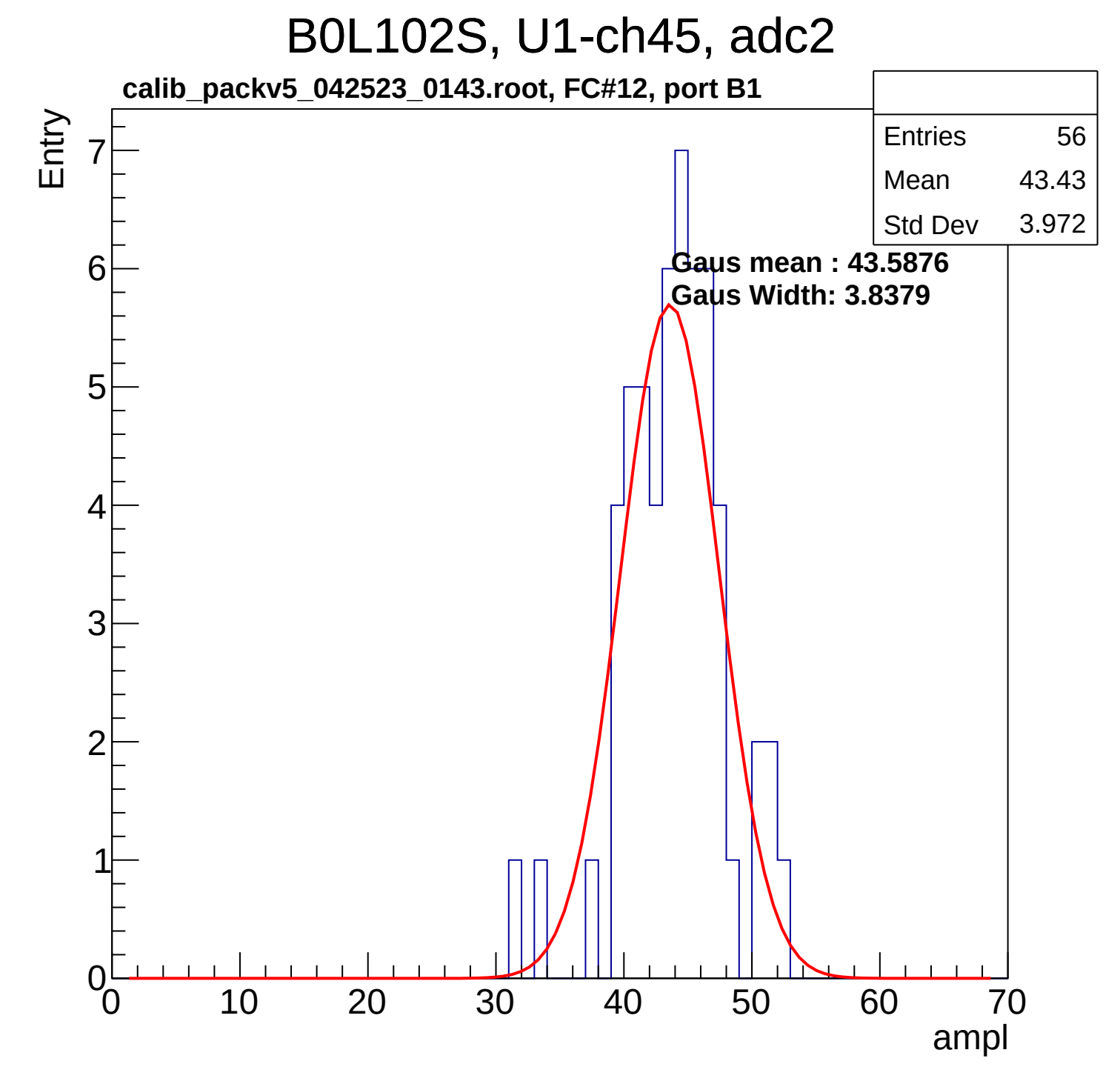
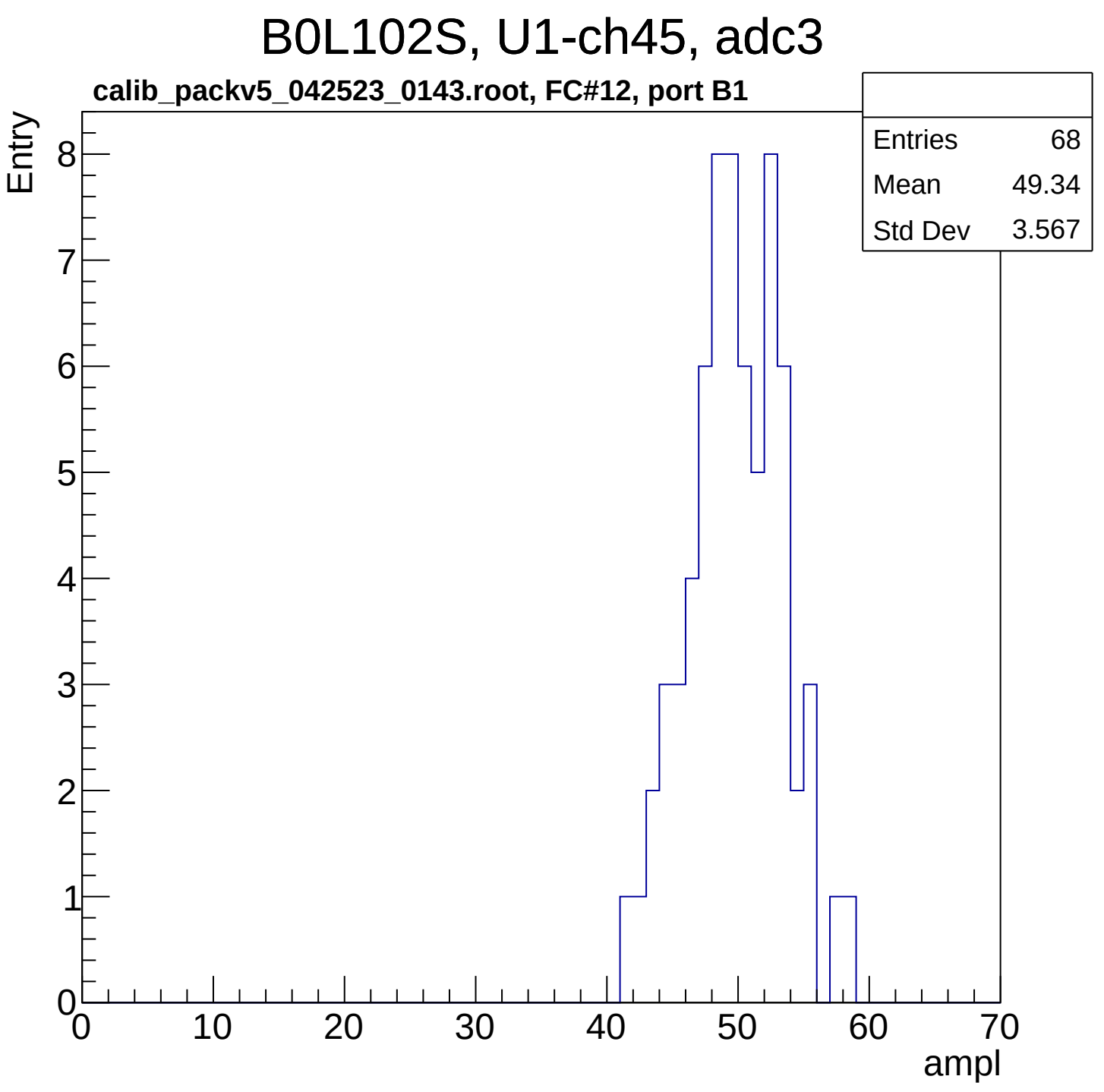
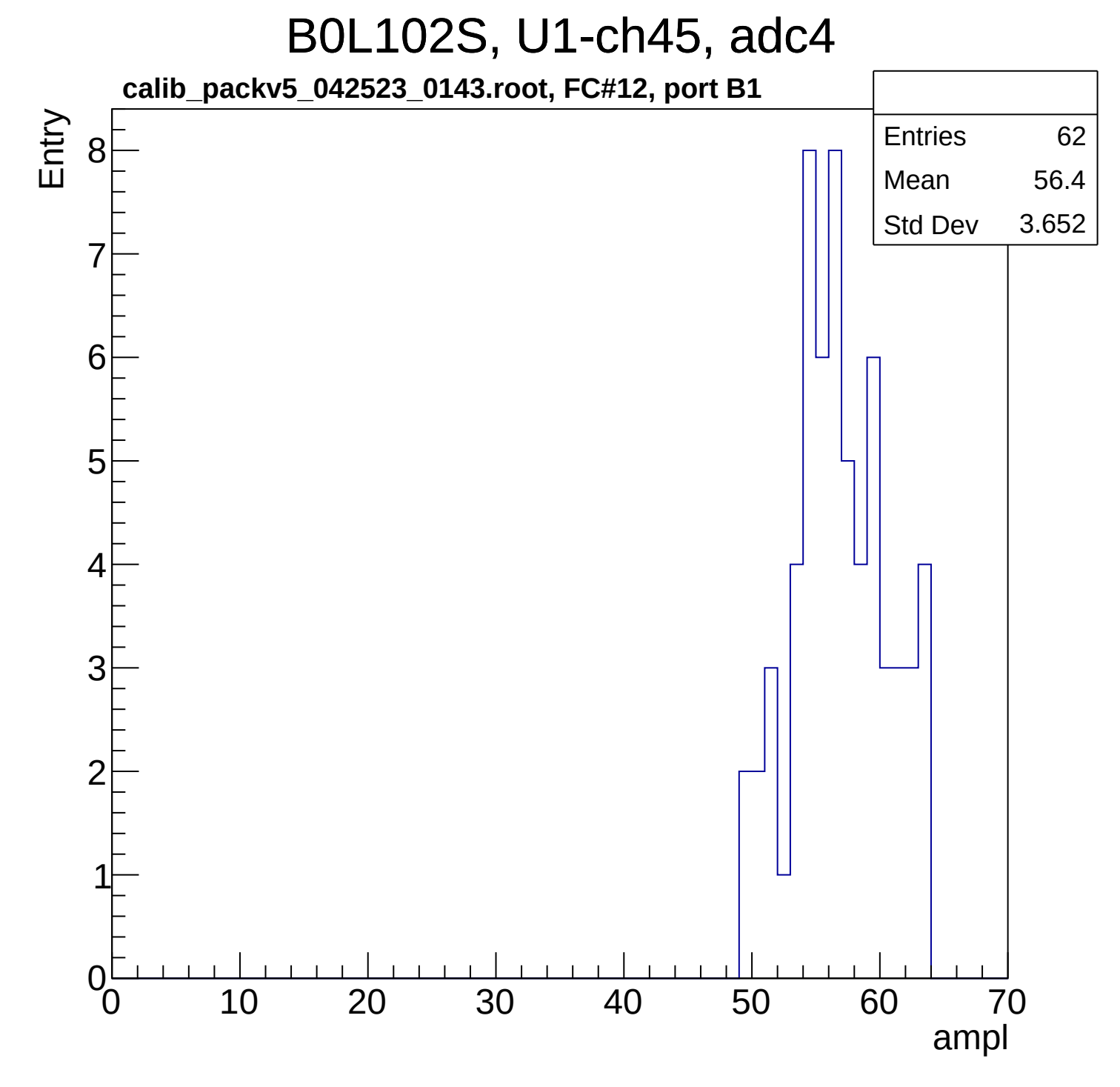
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

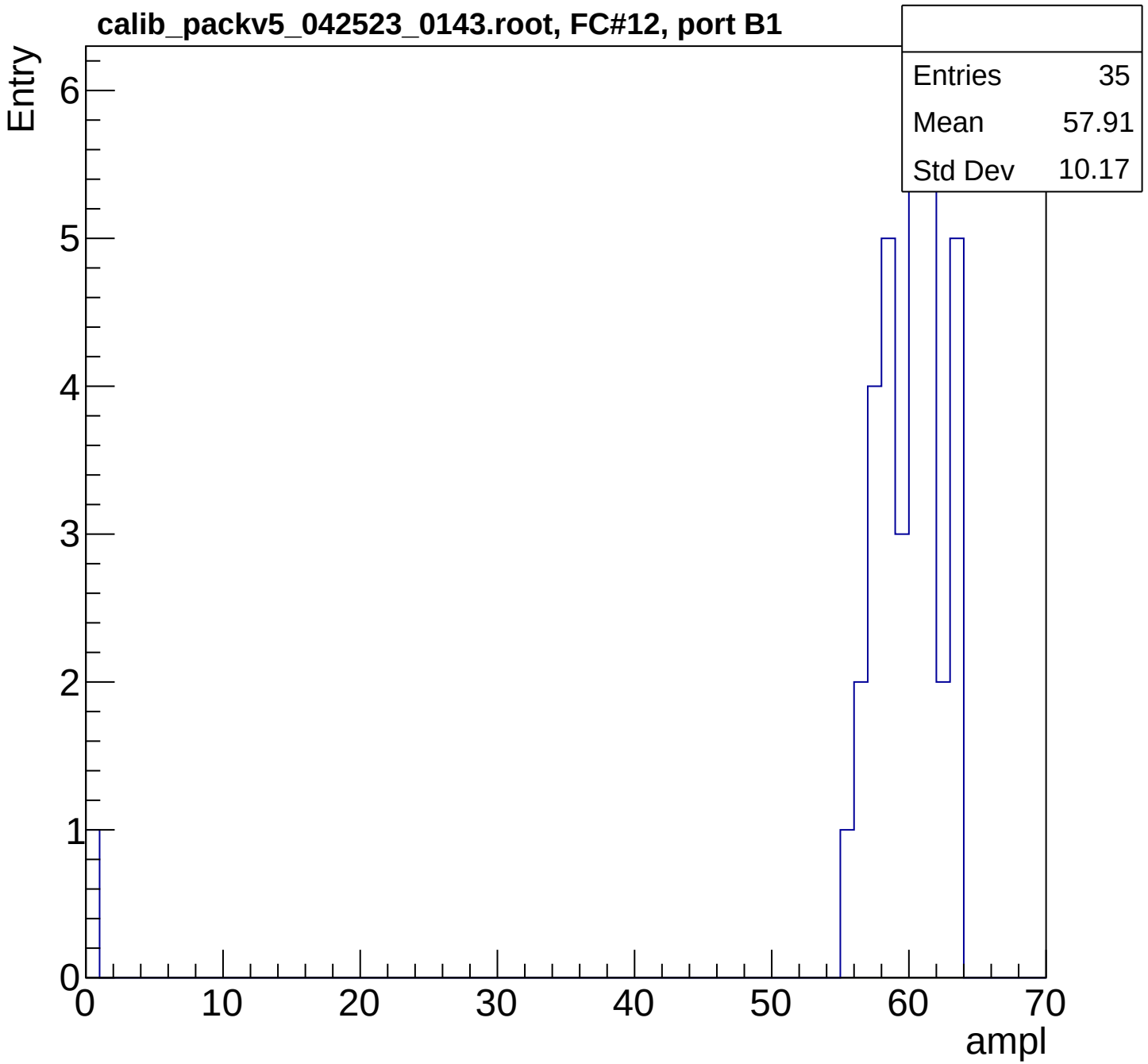

B0L102S, U1-ch45, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 29.43
Std Dev 3.324
Gaus mean : 29.5605
Gaus Width: 3.6533
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

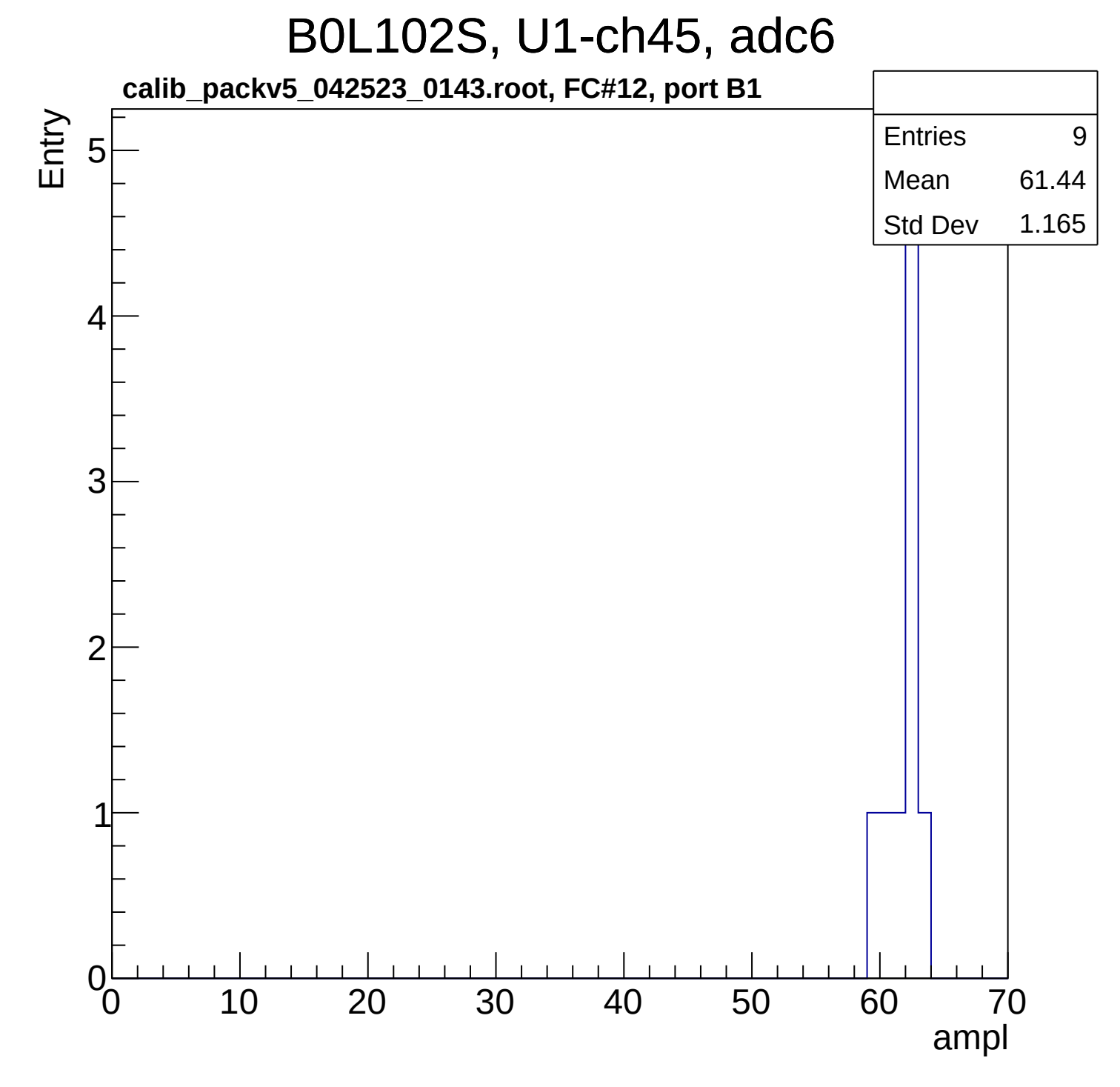

B0L102S, U1-ch45, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.05
Std Dev 3.636
Gaus mean : 37.7647
Gaus Width: 3.8274
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

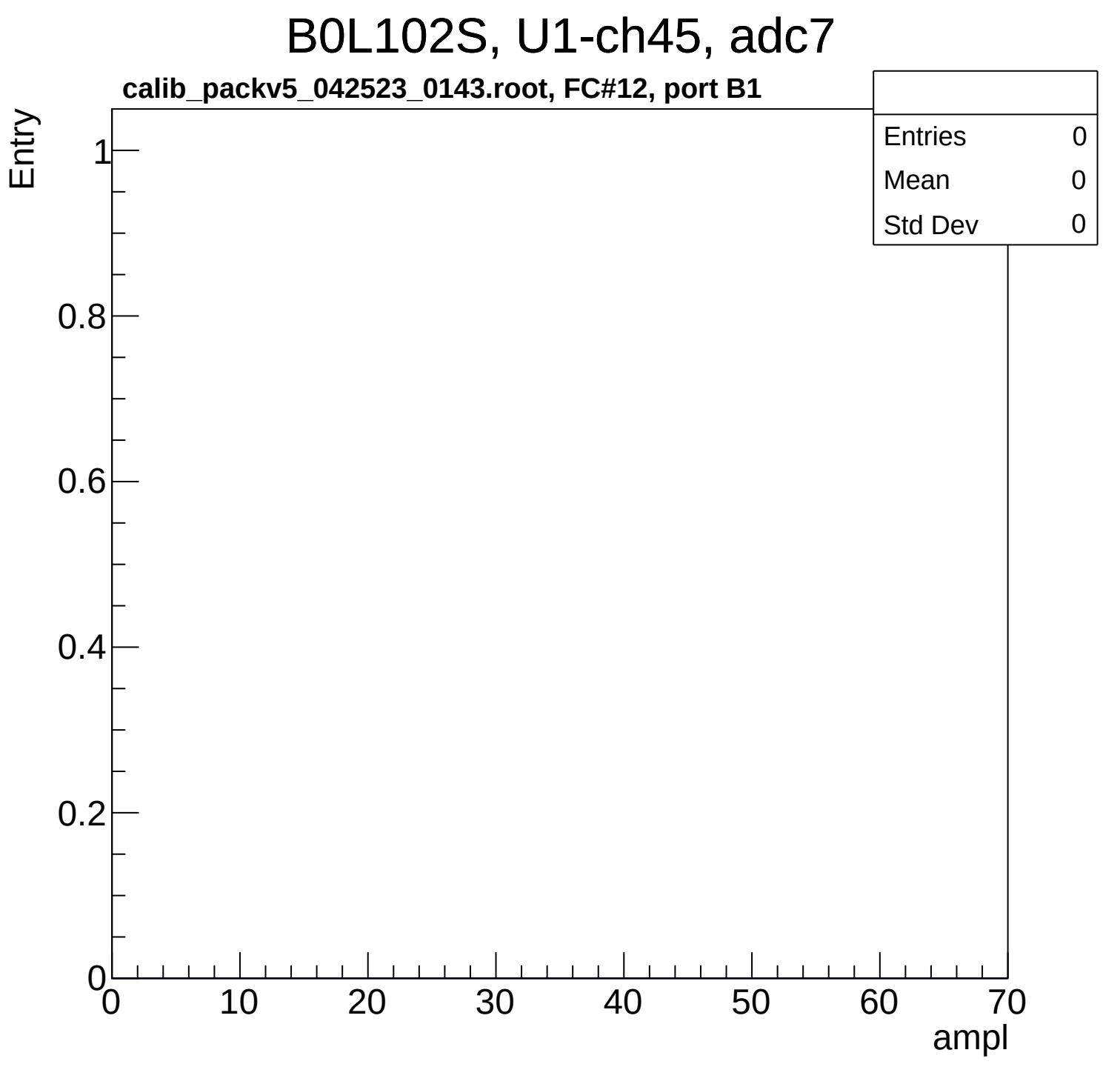

B0L102S, U1-ch45, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 43.43
Std Dev 3.972
Gaus mean : 43.5876
Gaus Width: 3.8379
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

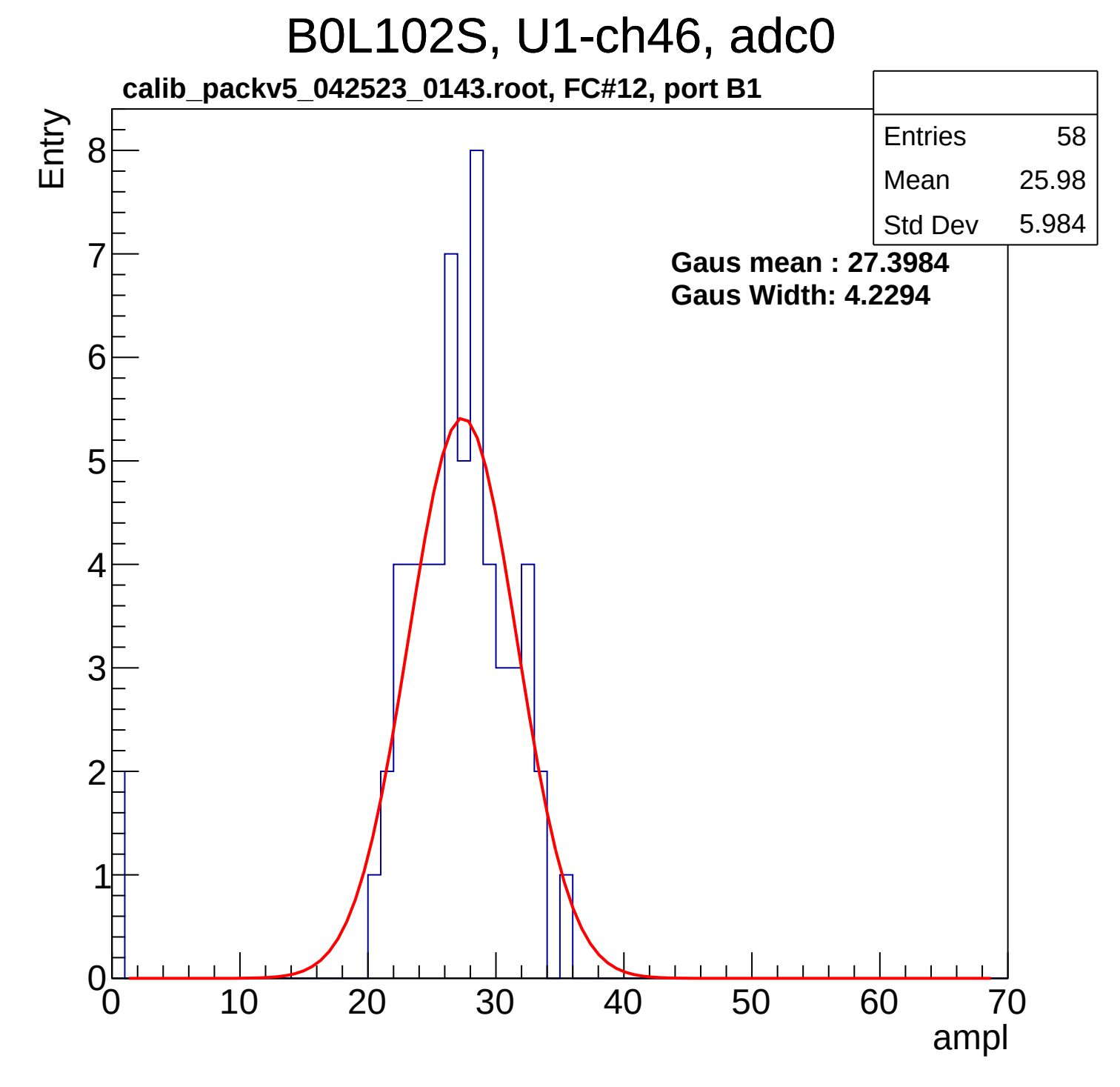

B0L102S, U1-ch45, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.34
Std Dev 3.567
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

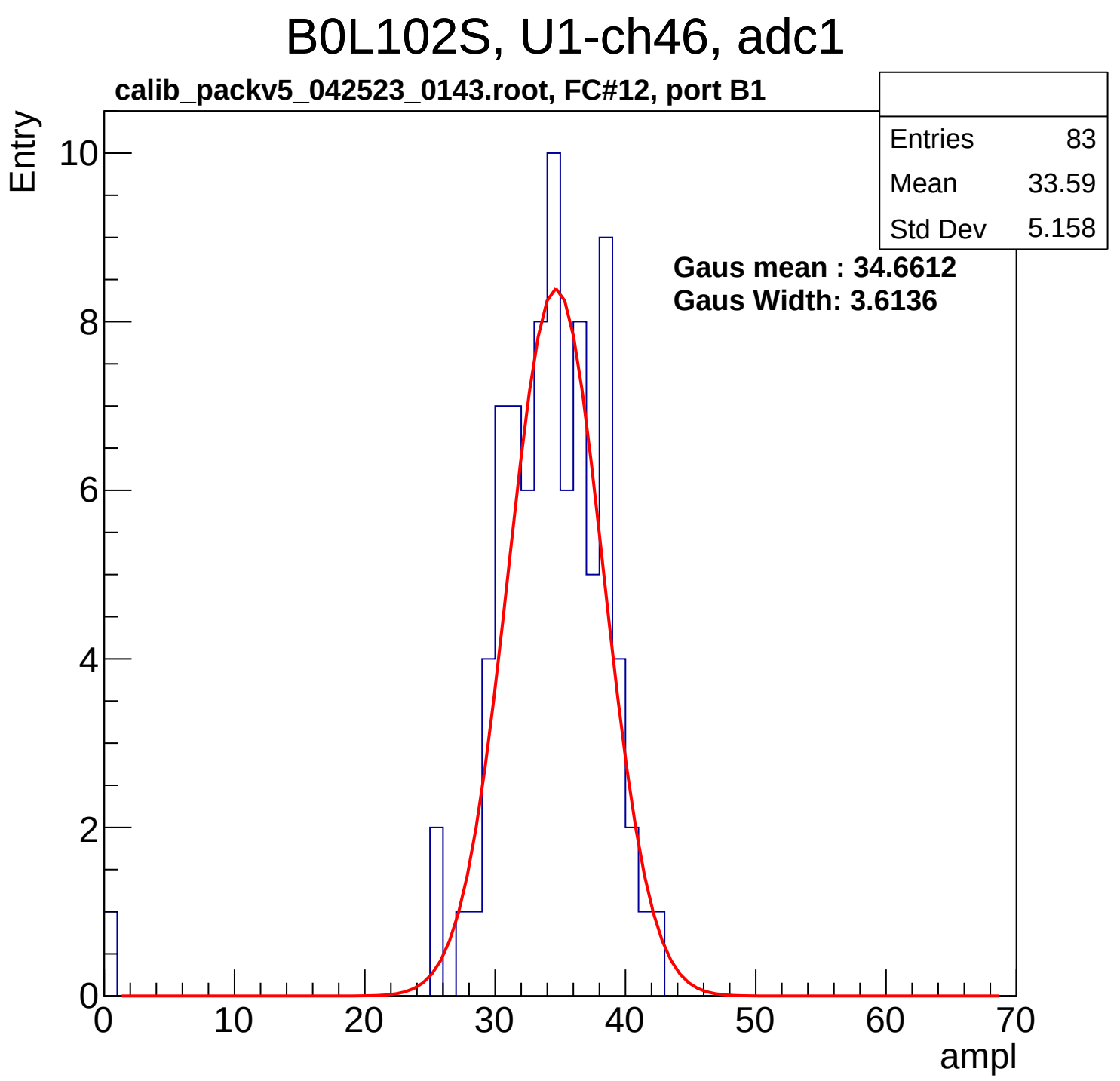

B0L102S, U1-ch45, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 56.4
Std Dev 3.652
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

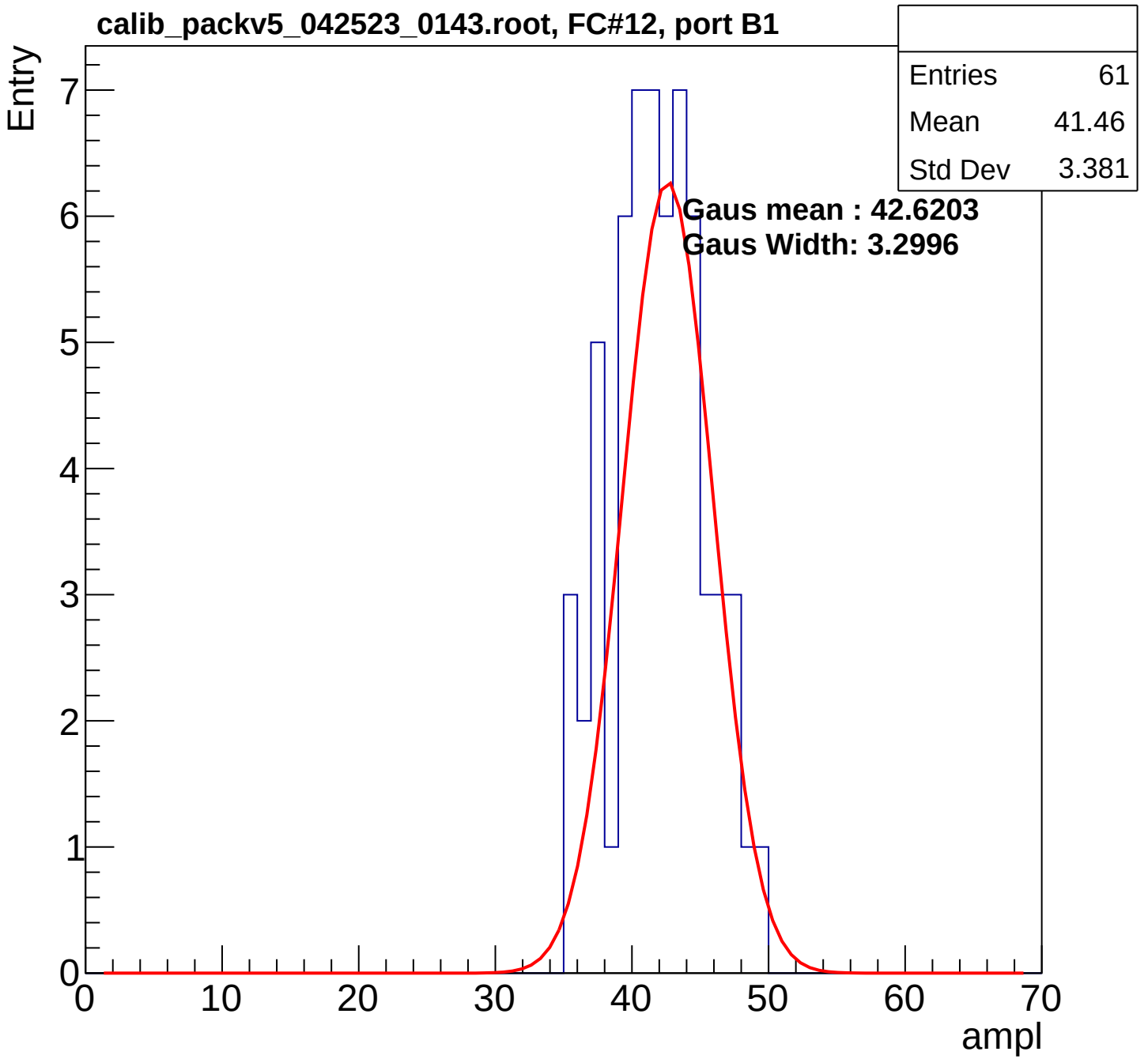

B0L102S, U1-ch45, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 57.91
Std Dev 10.17
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

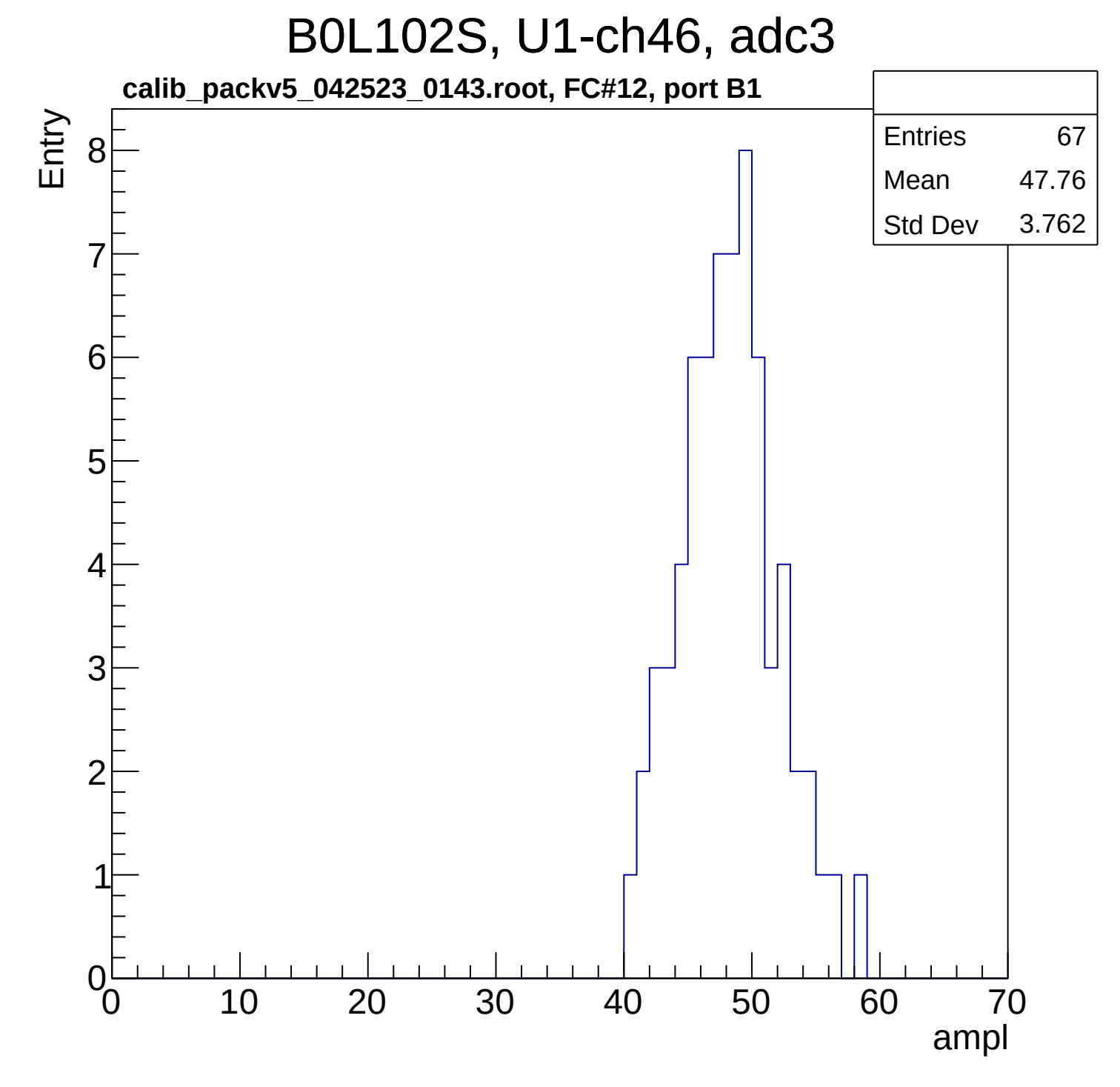

B0L102S, U1-ch45, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.44
Std Dev 1.165
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

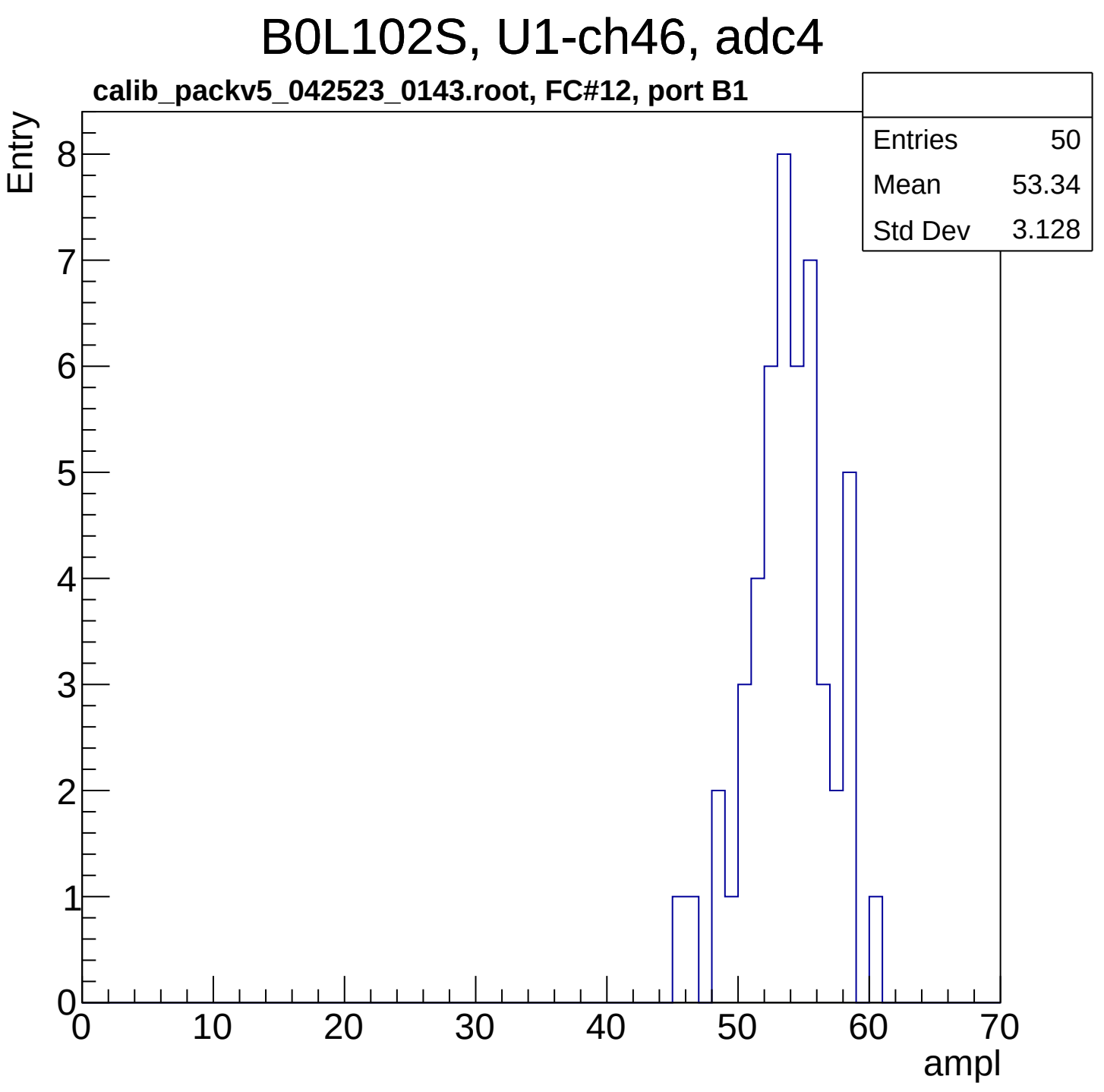

B0L102S, U1-ch45, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

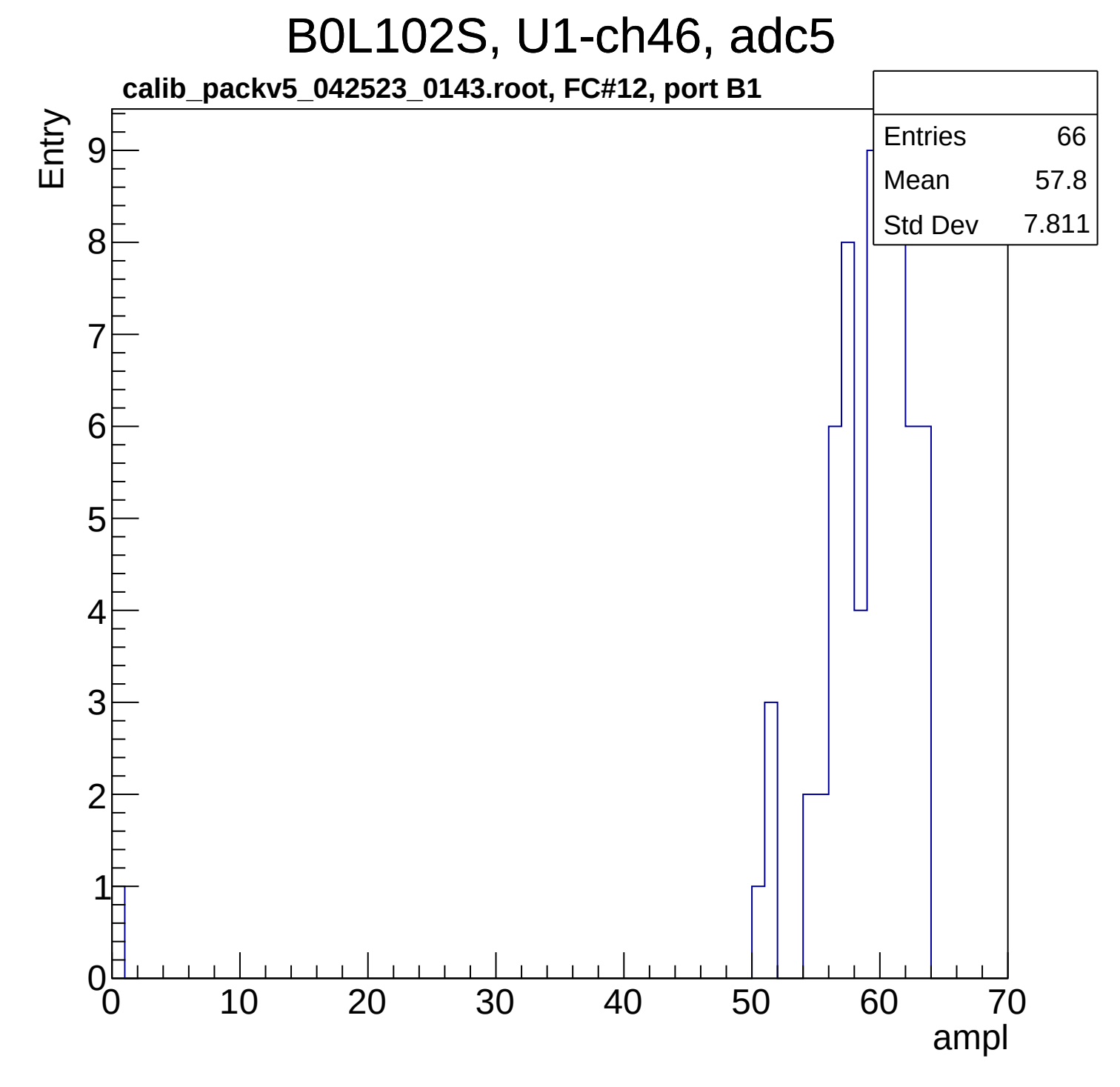

B0L102S, U1-ch46, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 25.98
Std Dev 5.984
Gaus mean : 27.3984
Gaus Width: 4.2294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

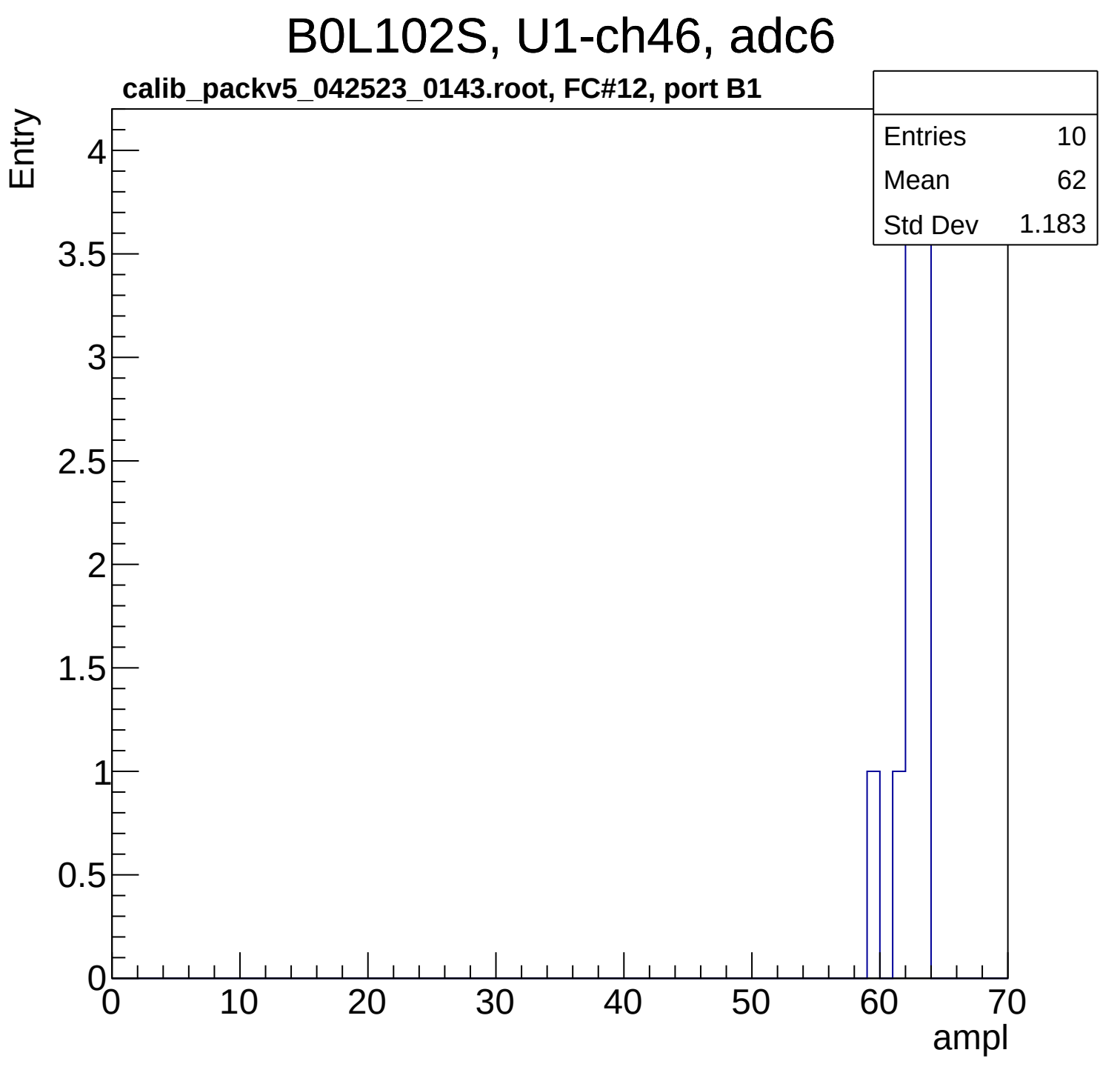

B0L102S, U1-ch46, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 33.59
Std Dev 5.158
Gaus mean : 34.6612
Gaus Width: 3.6136
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

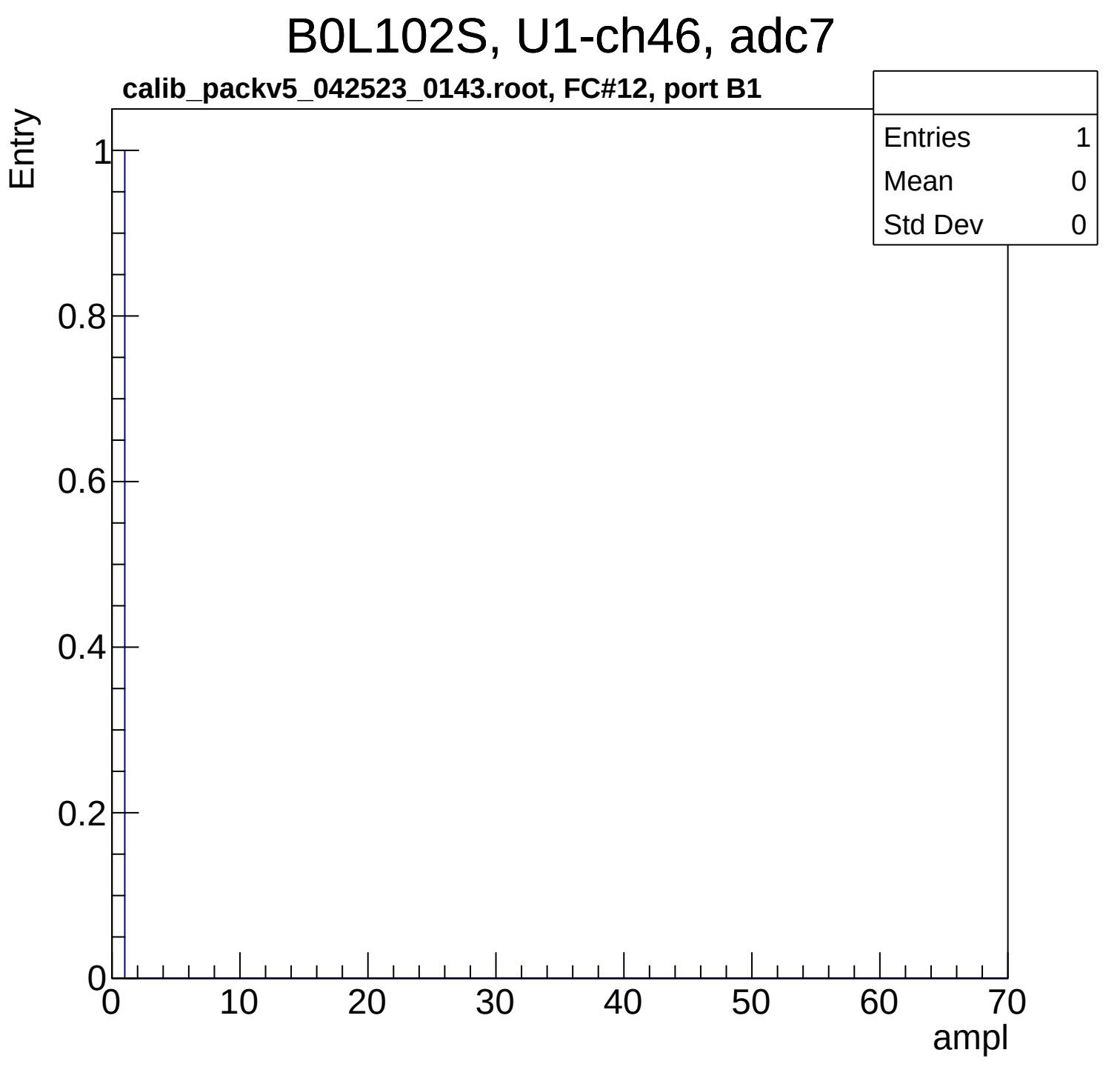

B0L102S, U1-ch46, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 41.46
Std Dev 3.381
Gaus mean : 42.6203
Gaus Width: 3.2996
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

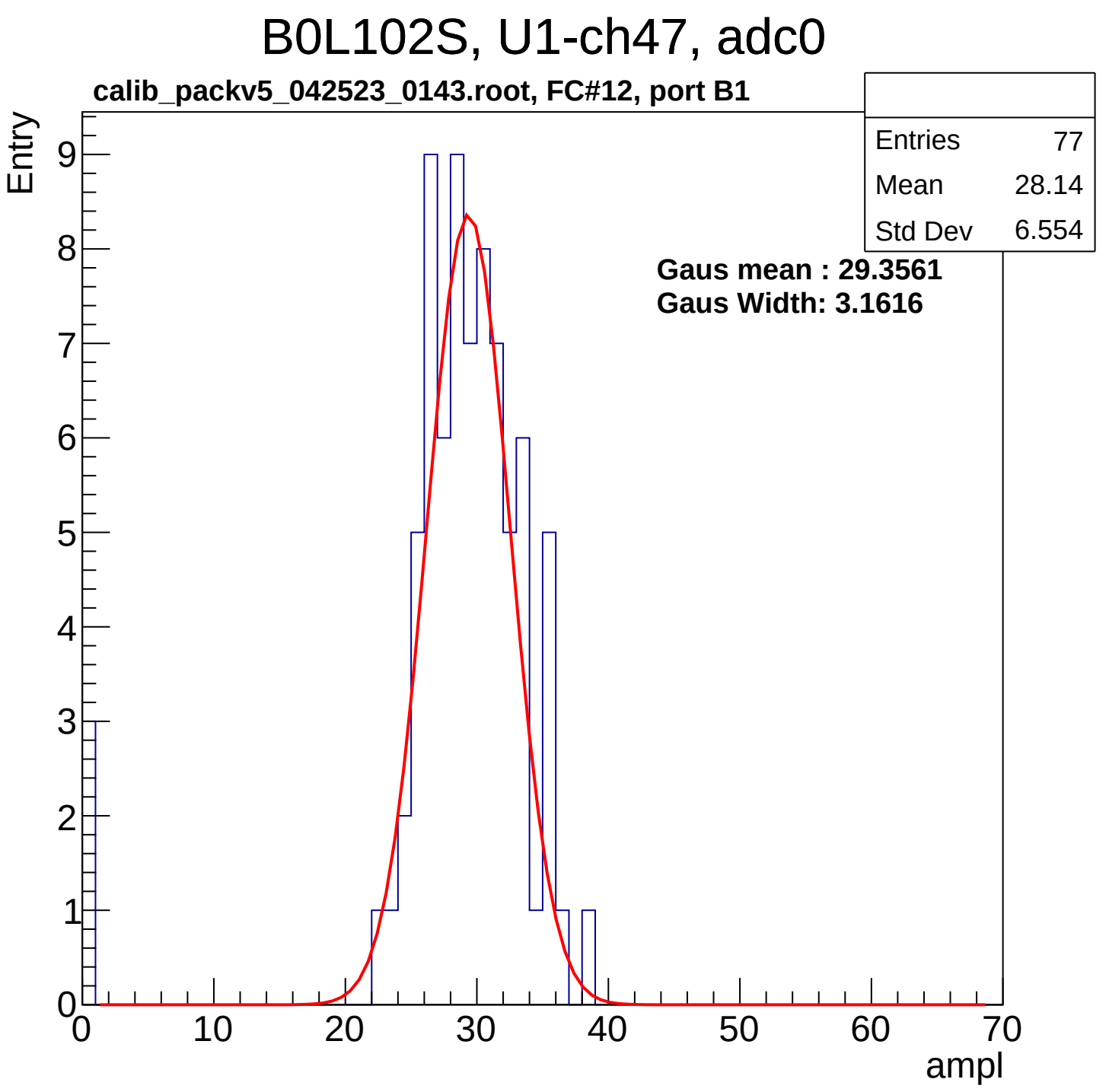

B0L102S, U1-ch46, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 47.76
Std Dev 3.762
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

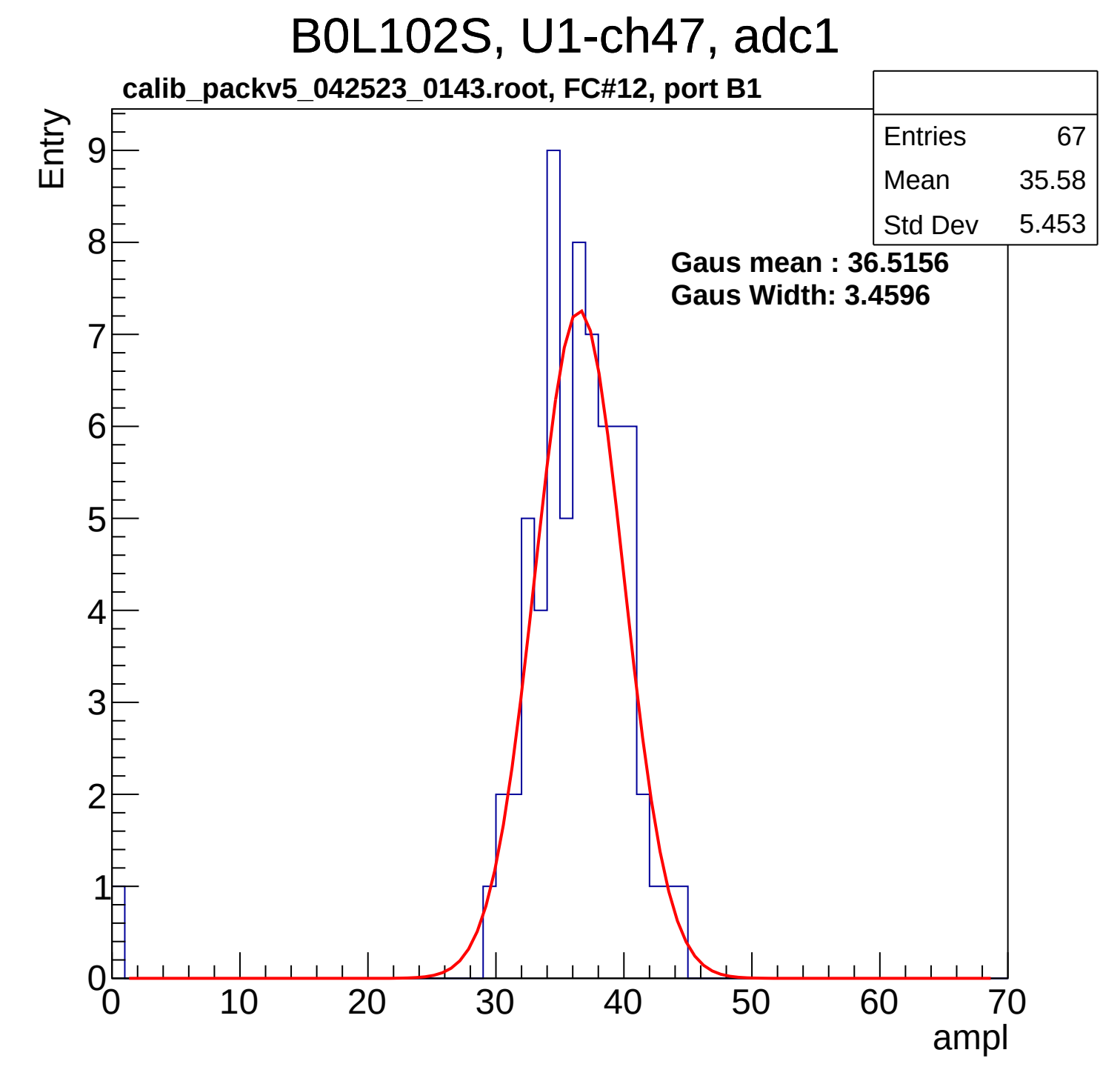

B0L102S, U1-ch46, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 53.34
Std Dev 3.128
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

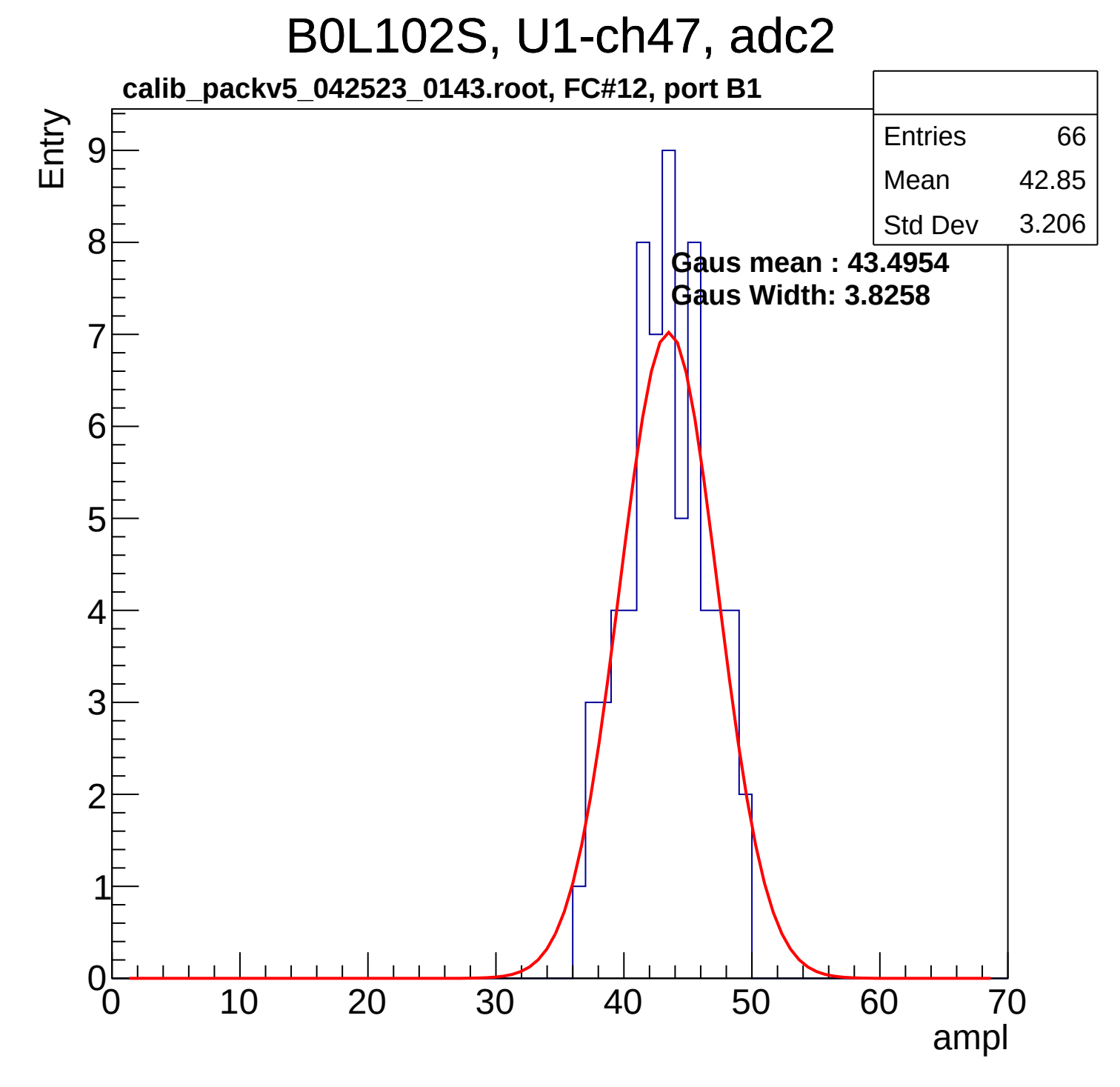

B0L102S, U1-ch46, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 57.8
Std Dev 7.811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

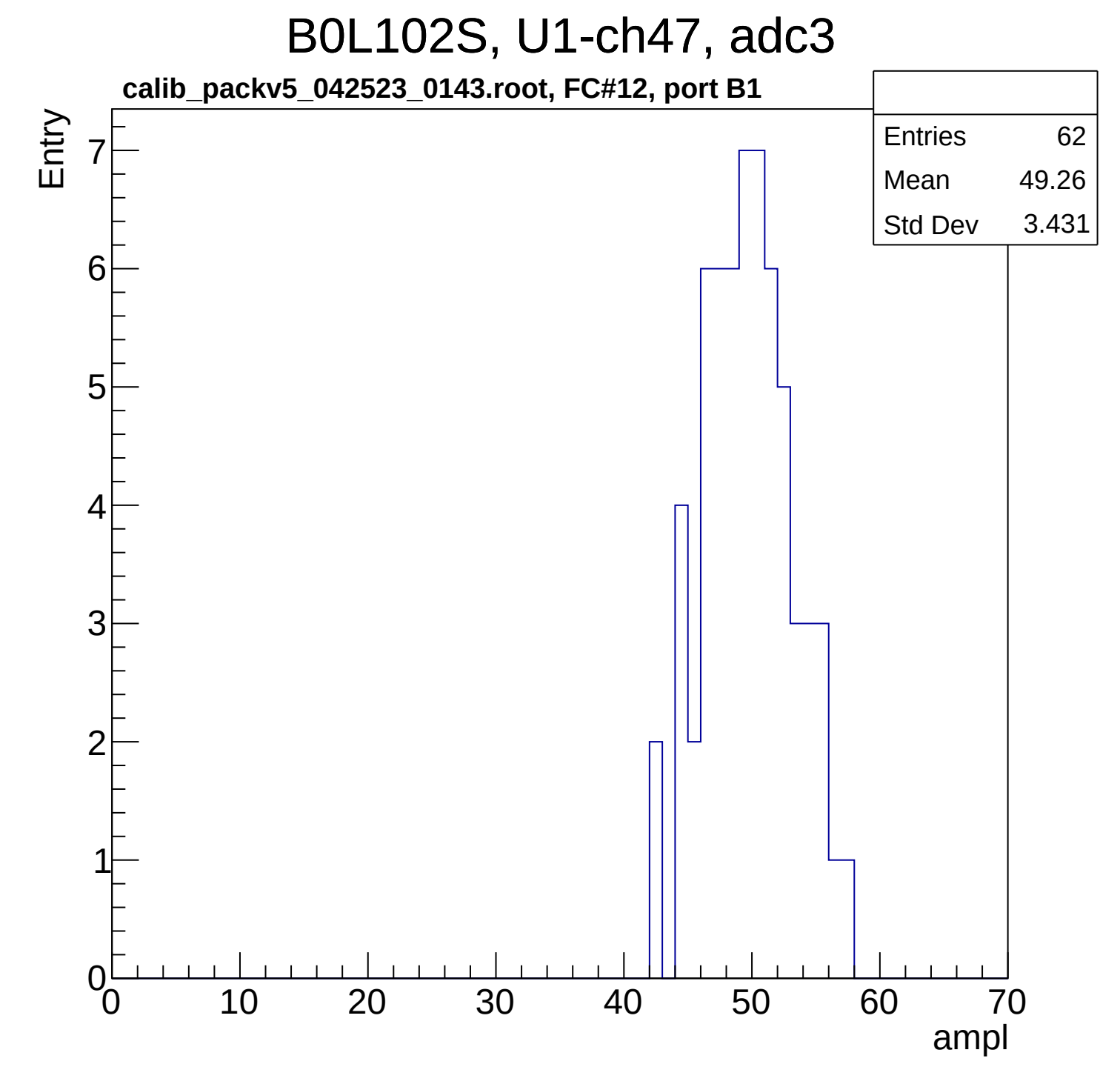

B0L102S, U1-ch46, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 62
Std Dev 1.183
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

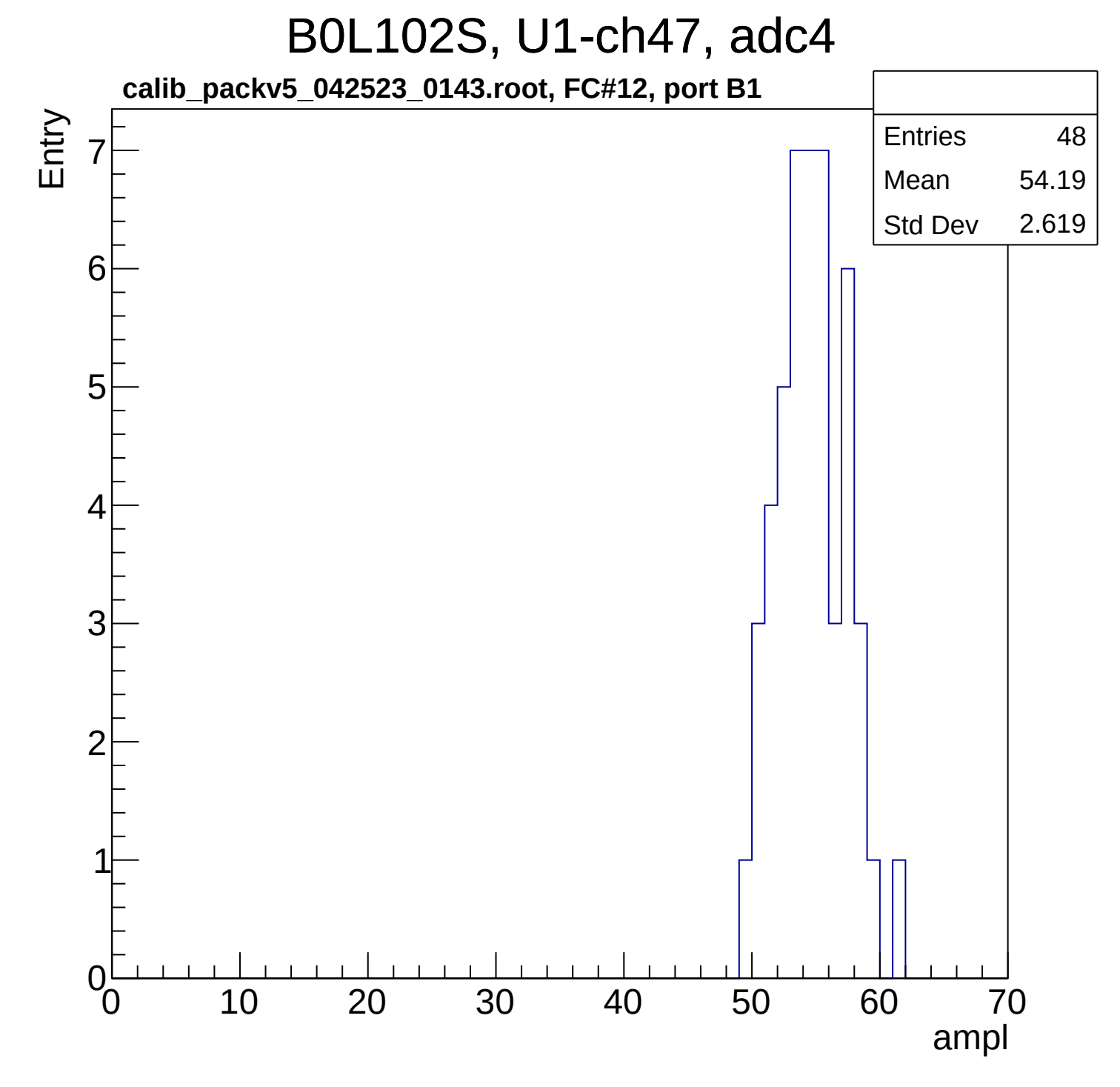

B0L102S, U1-ch46, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

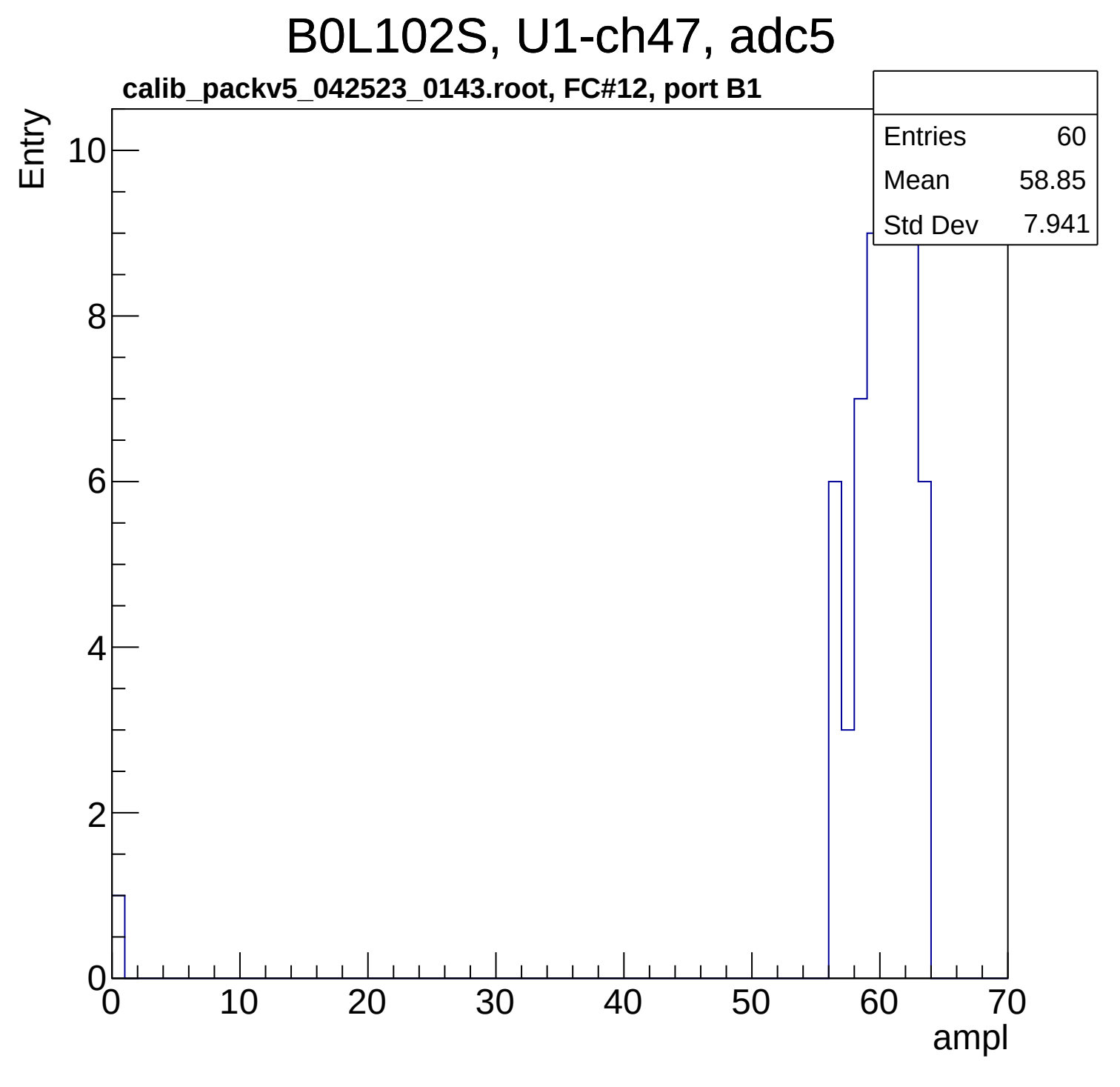

B0L102S, U1-ch47, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 28.14
Std Dev 6.554
Gaus mean : 29.3561
Gaus Width: 3.1616
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

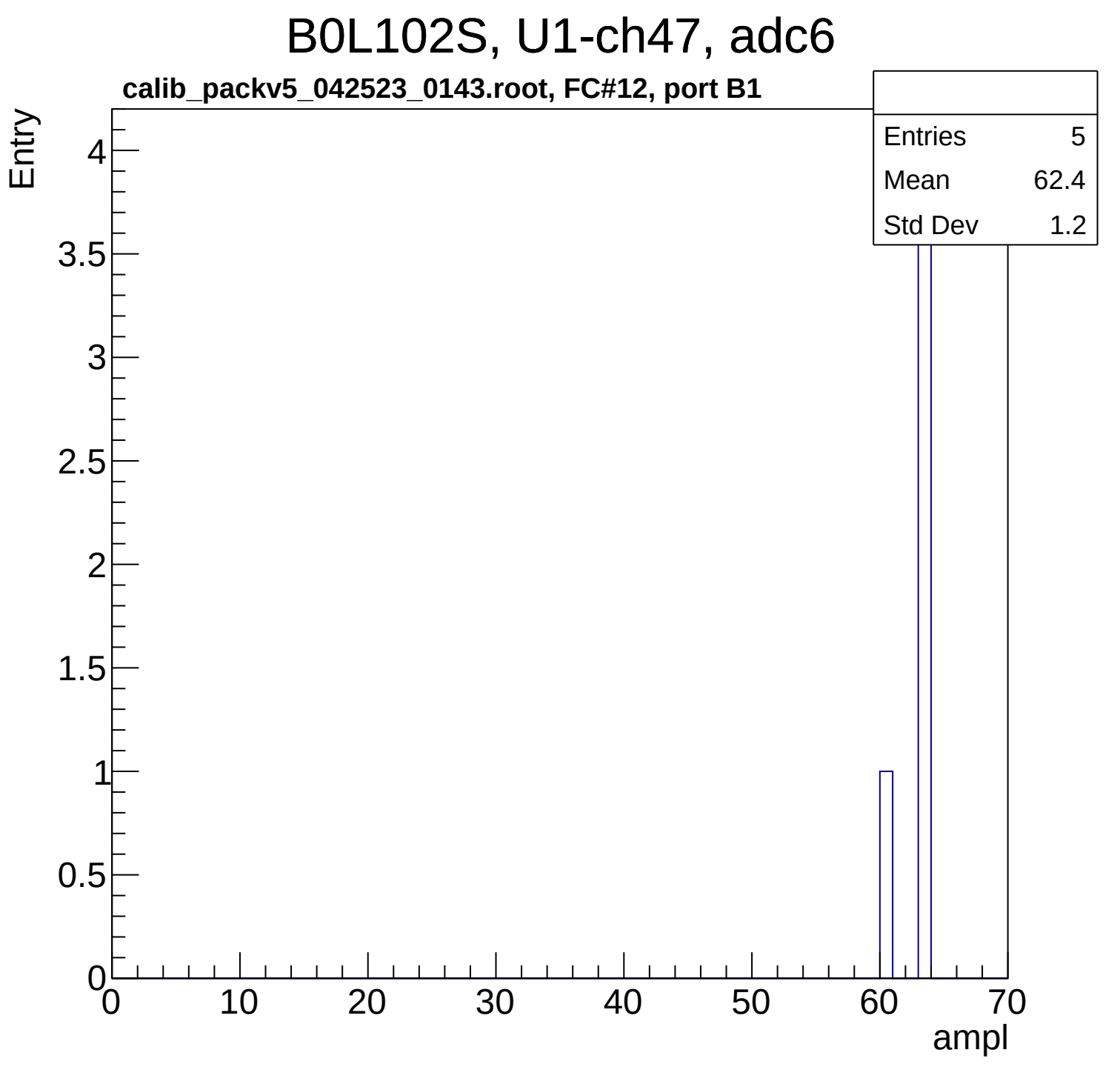

B0L102S, U1-ch47, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.58
Std Dev 5.453
Gaus mean : 36.5156
Gaus Width: 3.4596
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

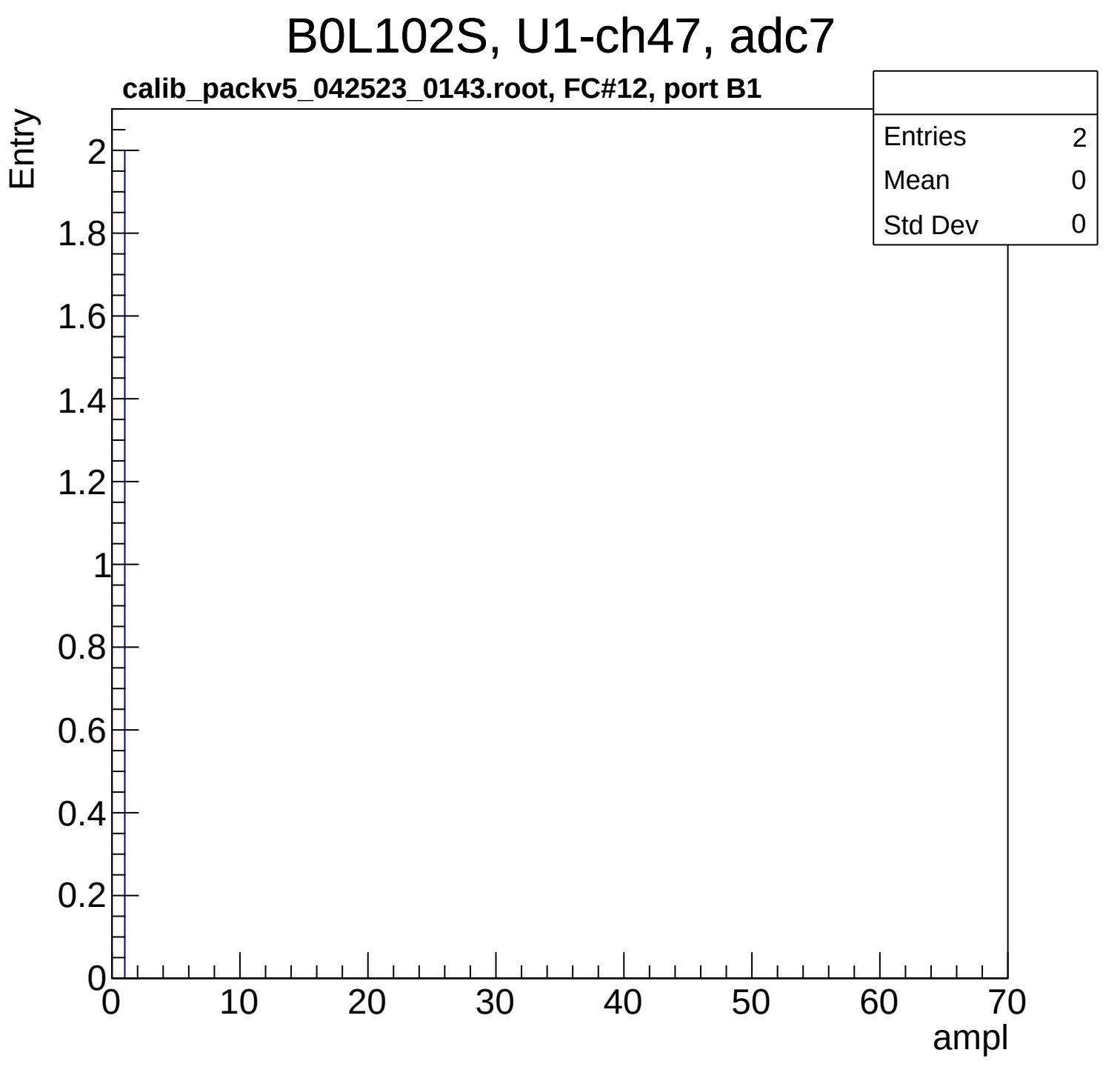

B0L102S, U1-ch47, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 42.85
Std Dev 3.206
Gaus mean : 43.4954
Gaus Width: 3.8258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

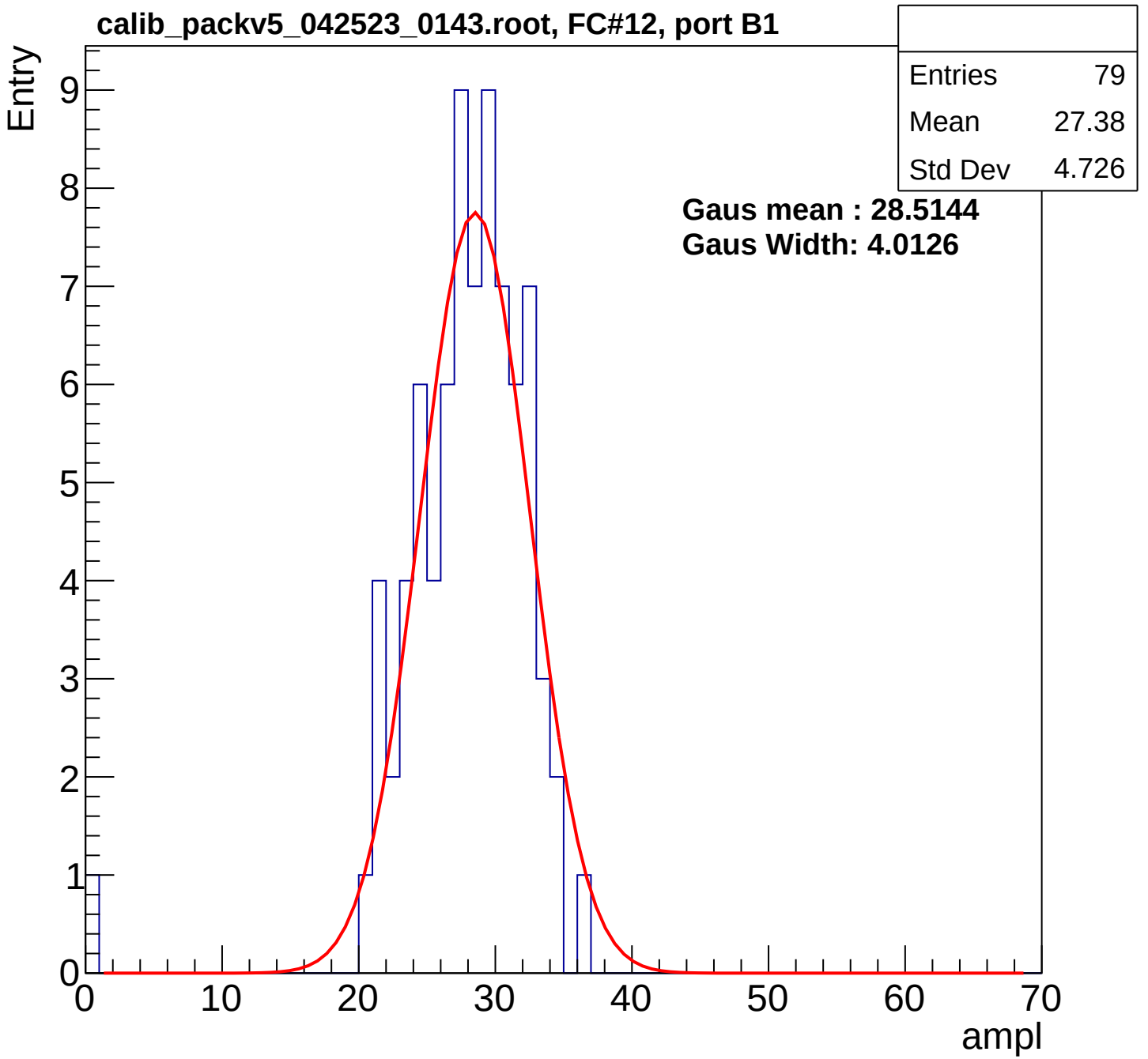

B0L102S, U1-ch47, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 49.26
Std Dev 3.431
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

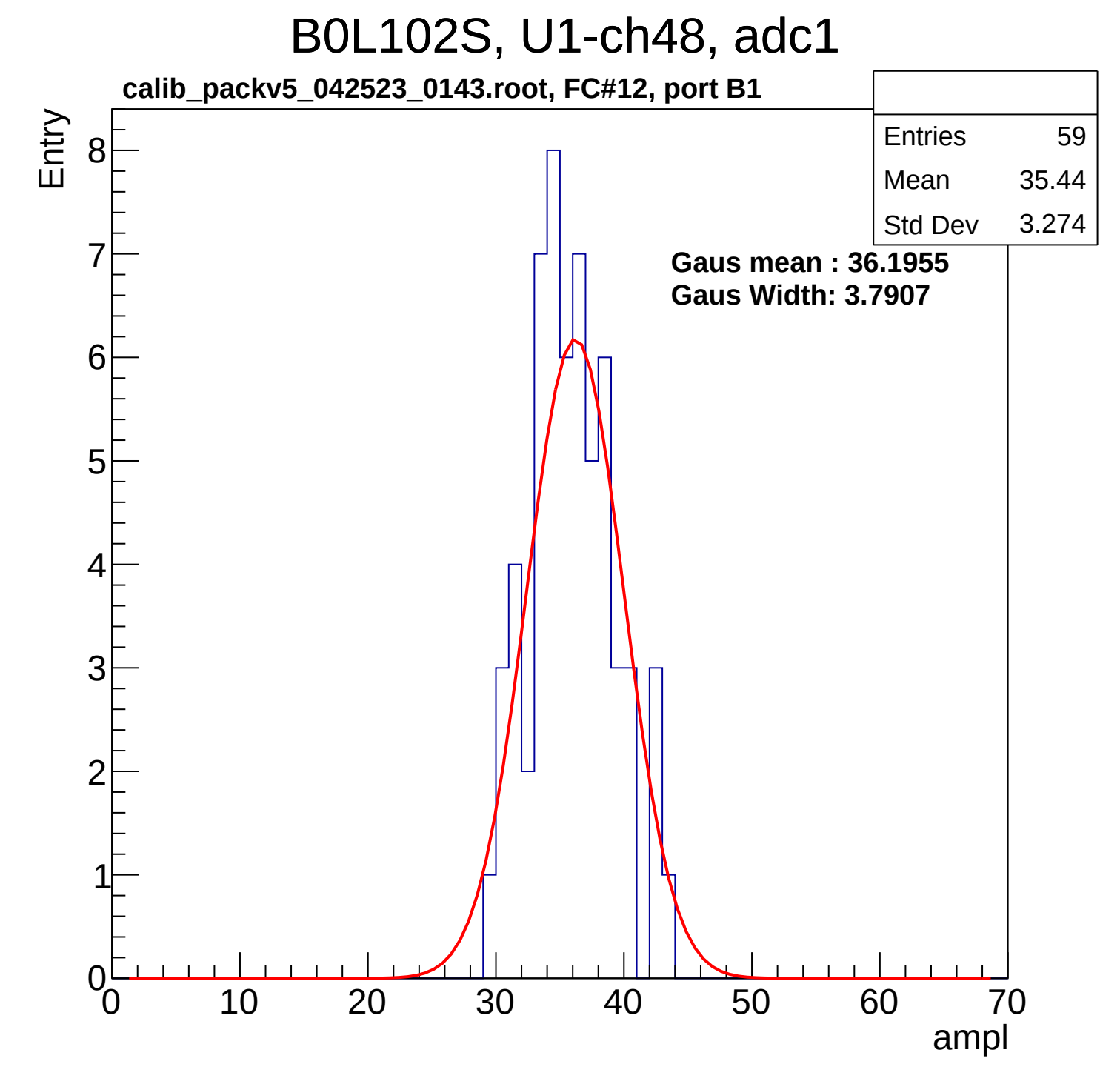

B0L102S, U1-ch47, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 54.19
Std Dev 2.619
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

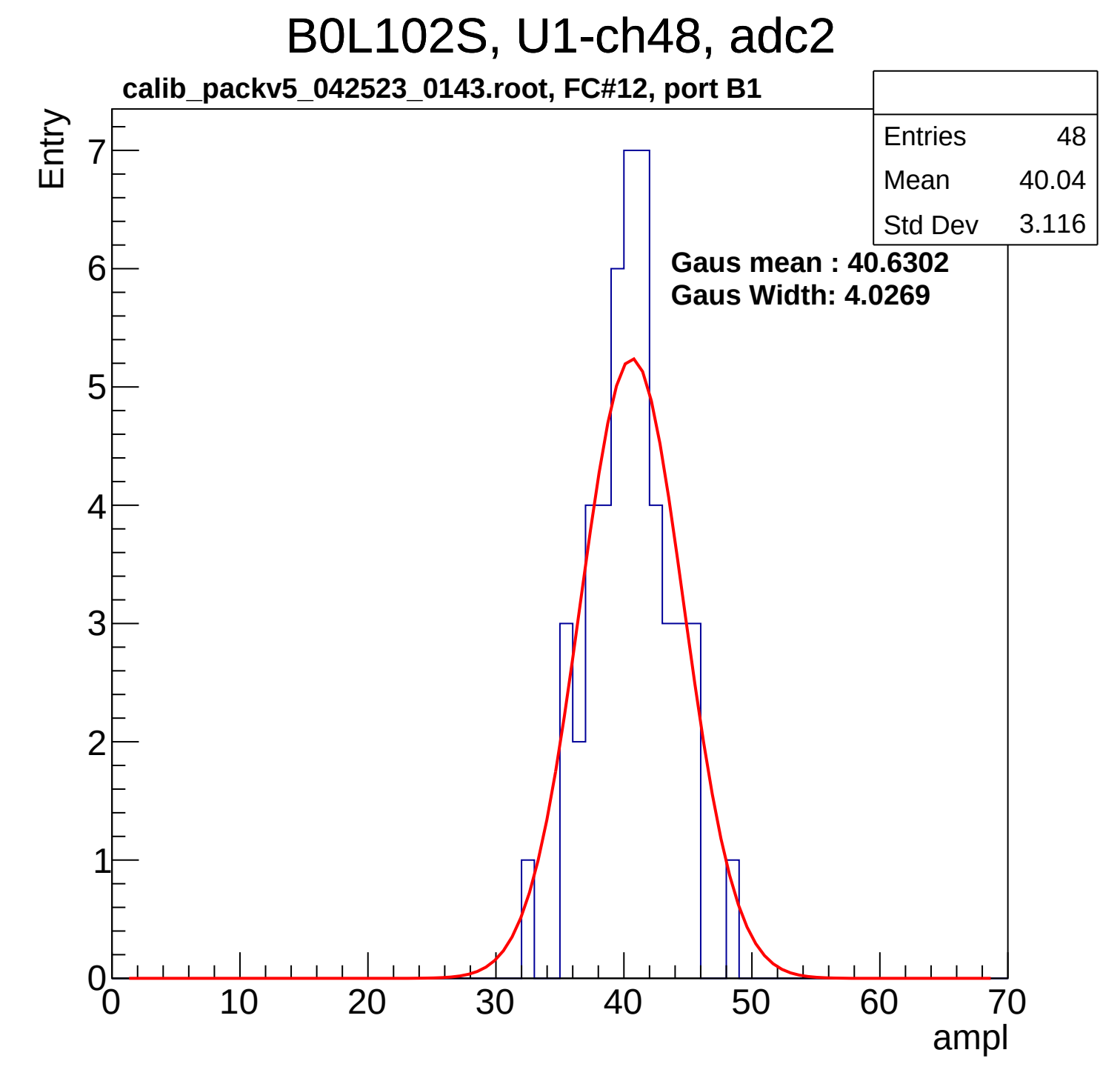

B0L102S, U1-ch47, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.85
Std Dev 7.941
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

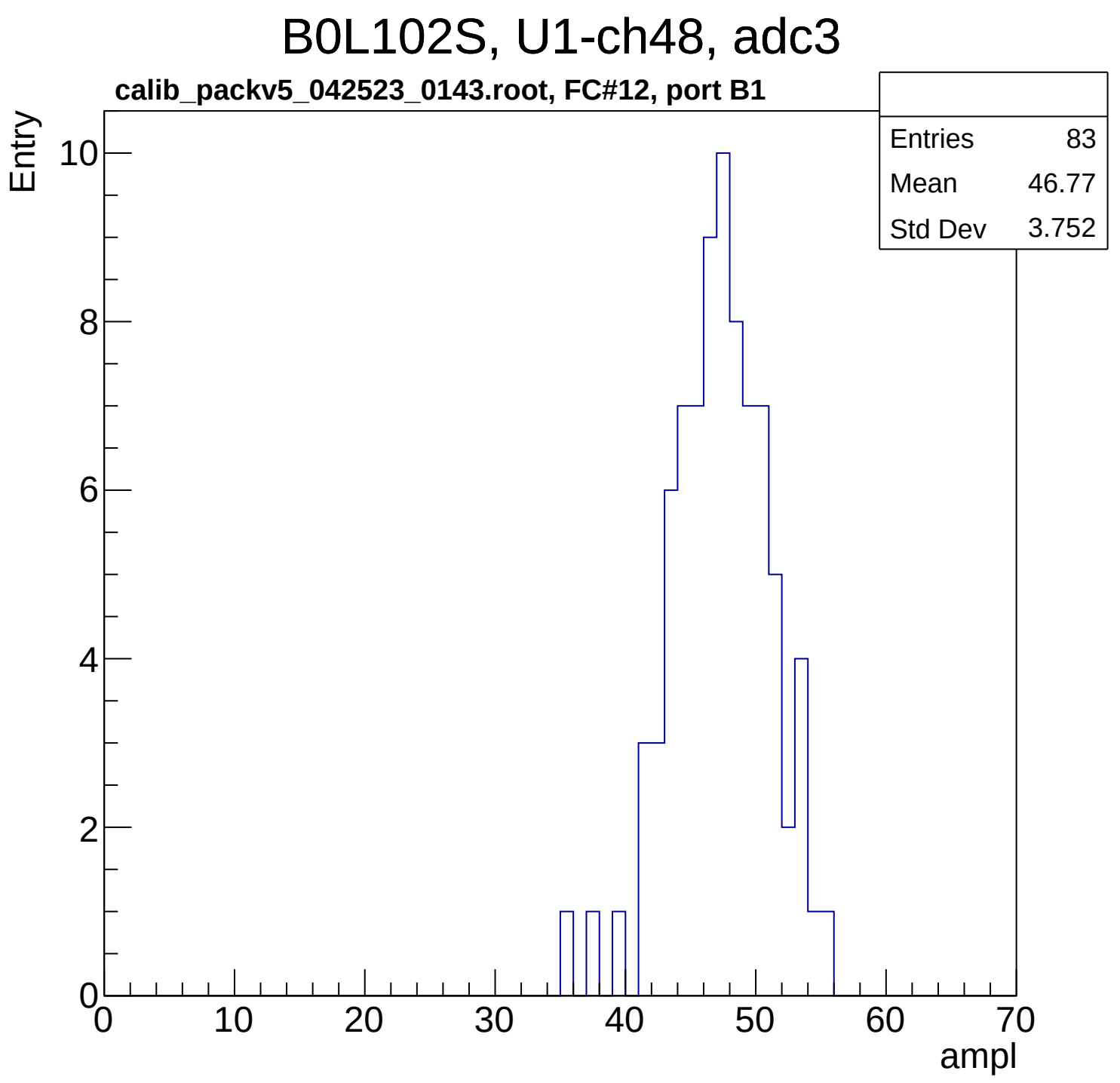

B0L102S, U1-ch47, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.4
Std Dev 1.2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

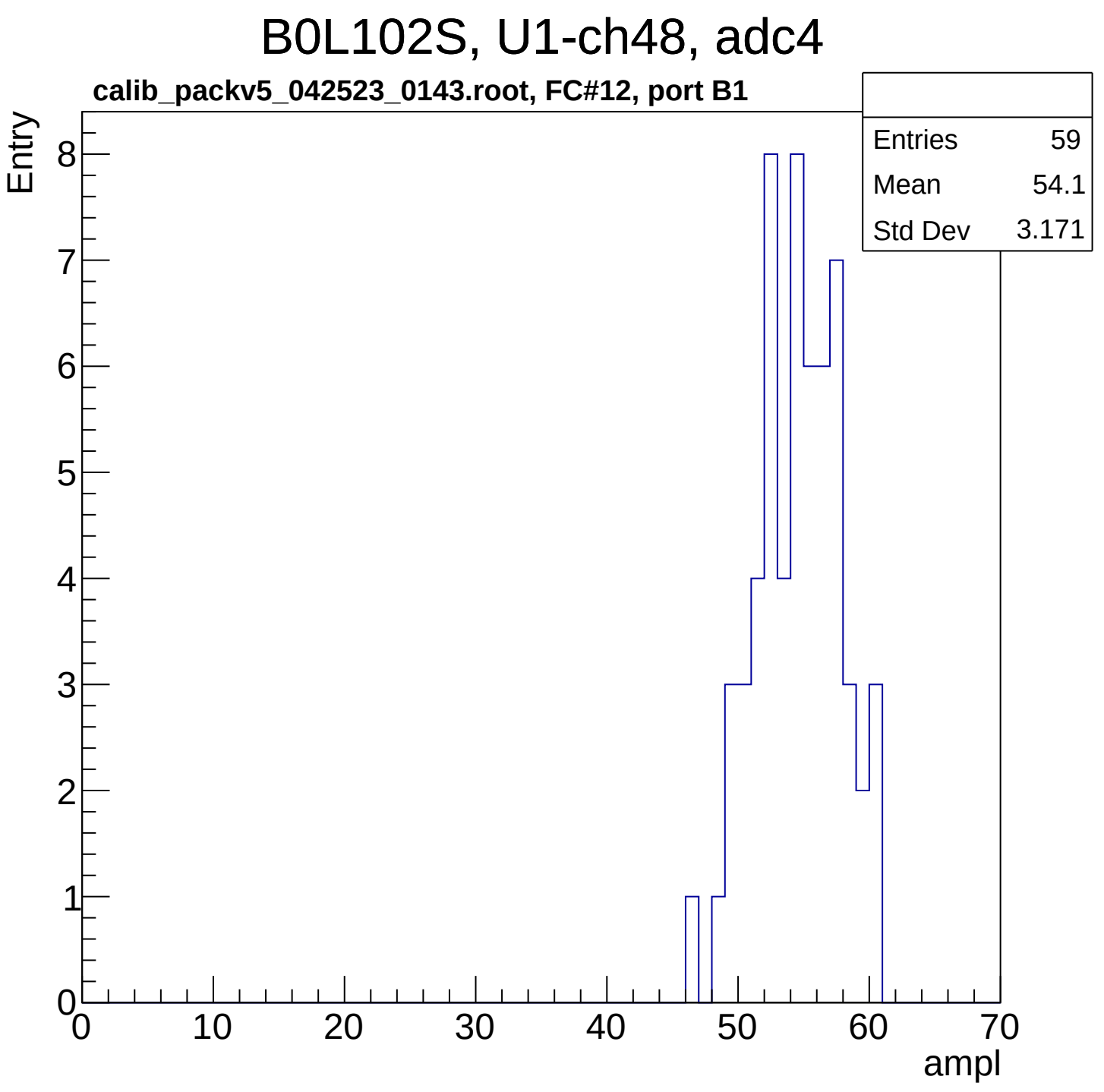

B0L102S, U1-ch47, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch48, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 27.38
Std Dev 4.726
Gaus mean : 28.5144
Gaus Width: 4.0126
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch48, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 35.44
Std Dev 3.274
Gaus mean : 36.1955
Gaus Width: 3.7907
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch48, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 40.04
Std Dev 3.116
Gaus mean : 40.6302
Gaus Width: 4.0269
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch48, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 46.77
Std Dev 3.752
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch48, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 54.1
Std Dev 3.171
Entry
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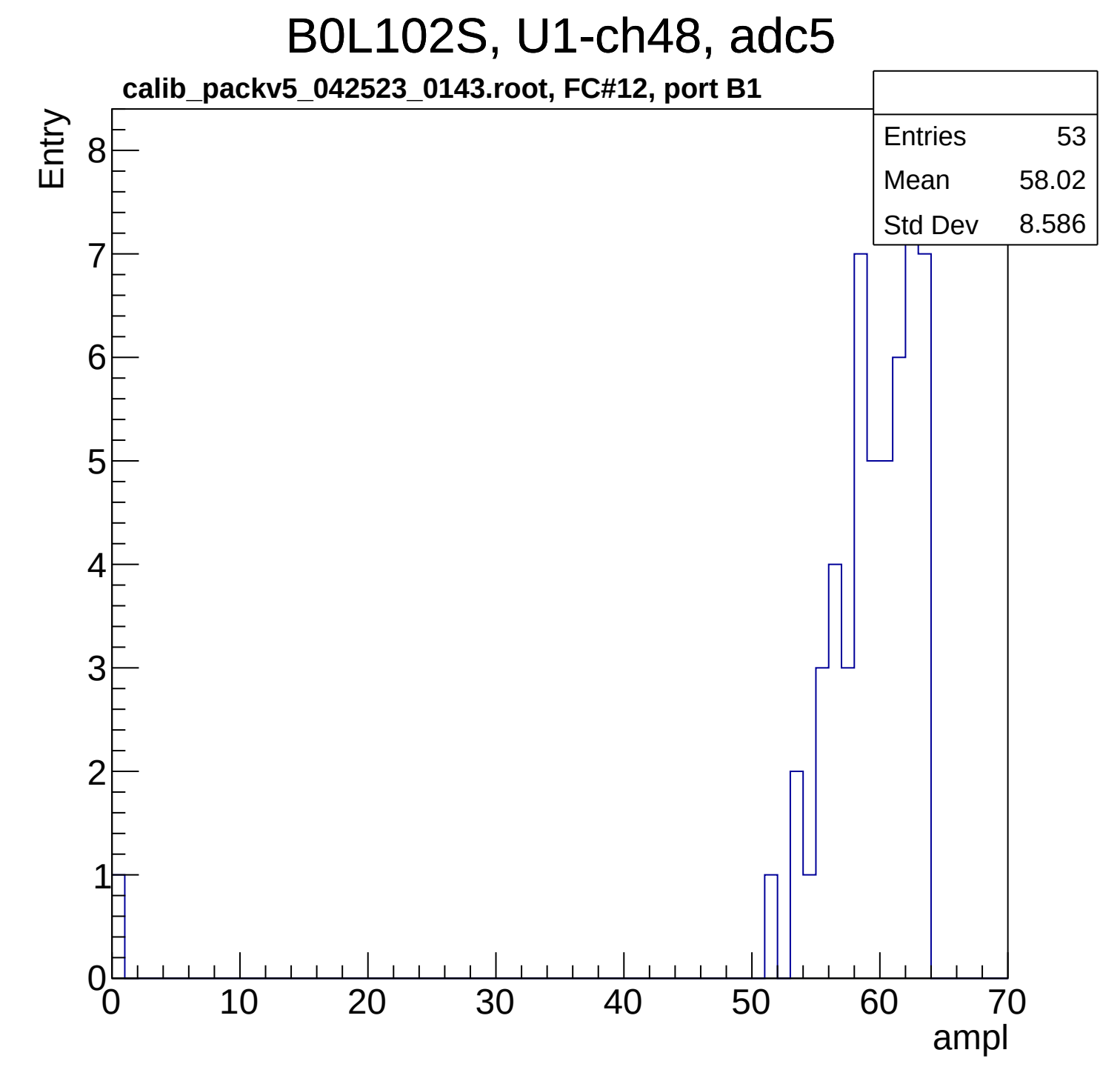
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch48, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 58.02
Std Dev 8.586
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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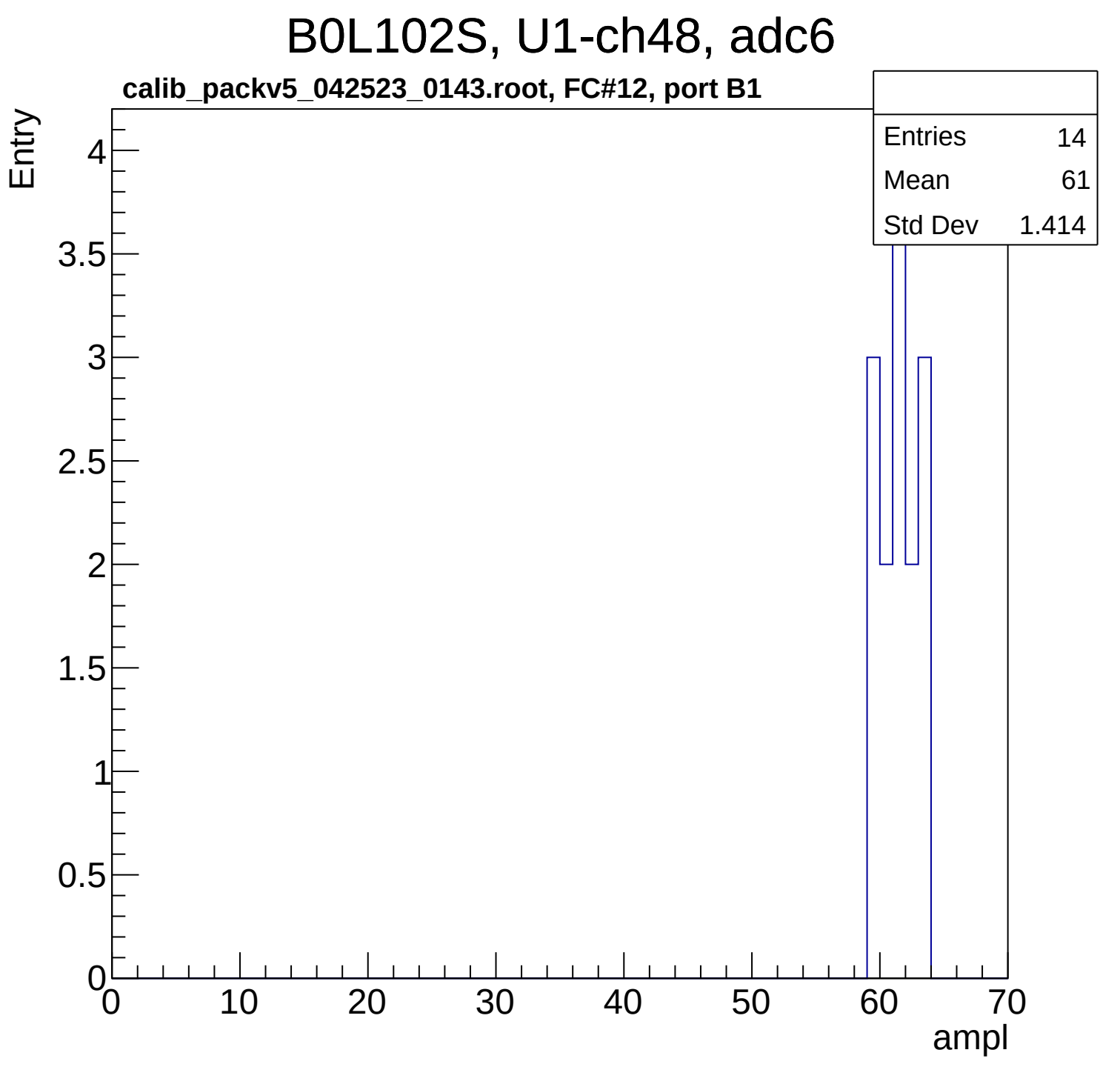
40
50
60
70


B0L102S, U1-ch48, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61
Std Dev 1.414
Entry
ampl
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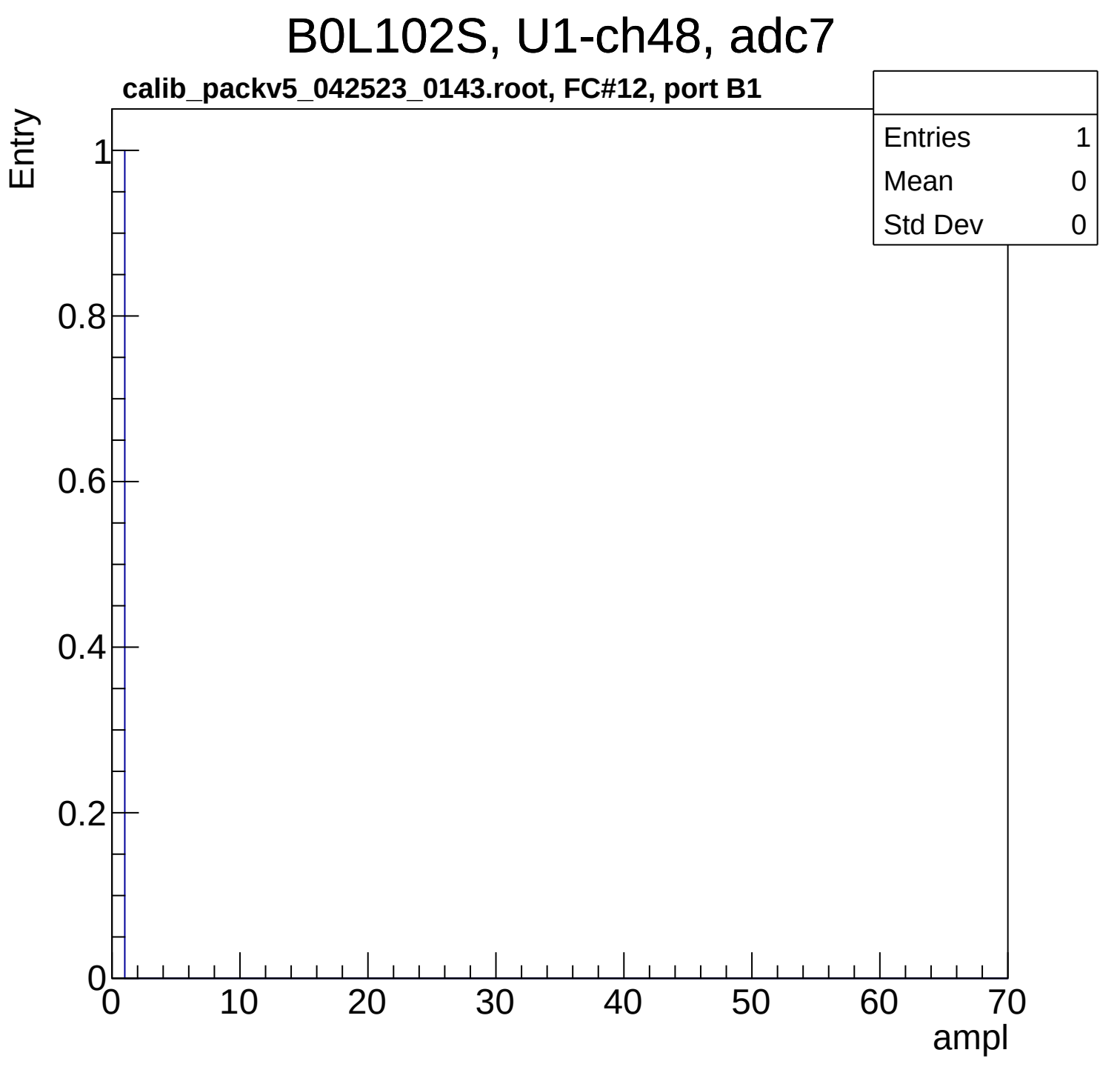
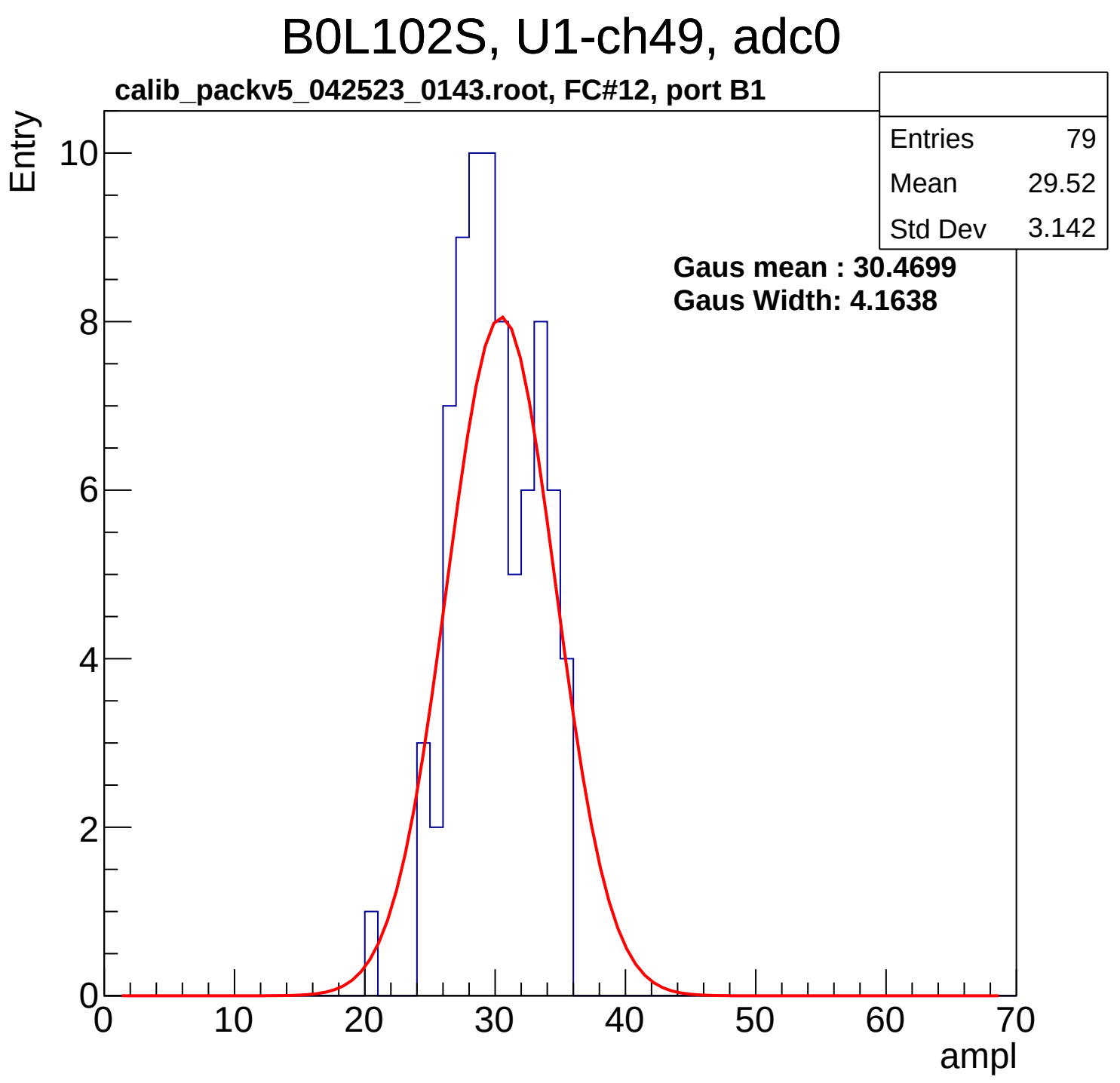
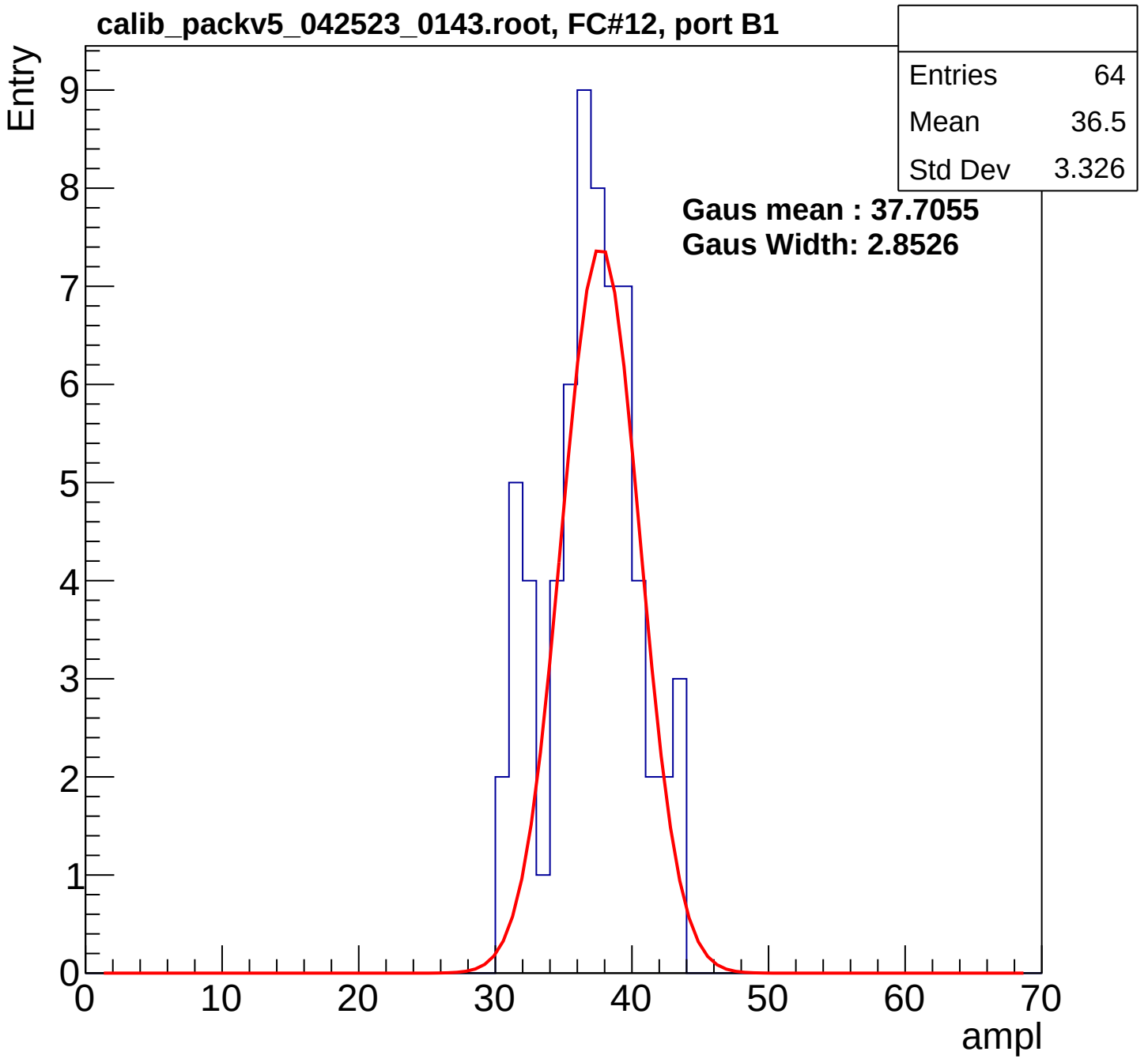
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch48, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch49, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 29.52
Std Dev 3.142
Gaus mean : 30.4699
Gaus Width: 4.1638
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch49, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.5
Std Dev 3.326
Gaus mean : 37.7055
Gaus Width: 2.8526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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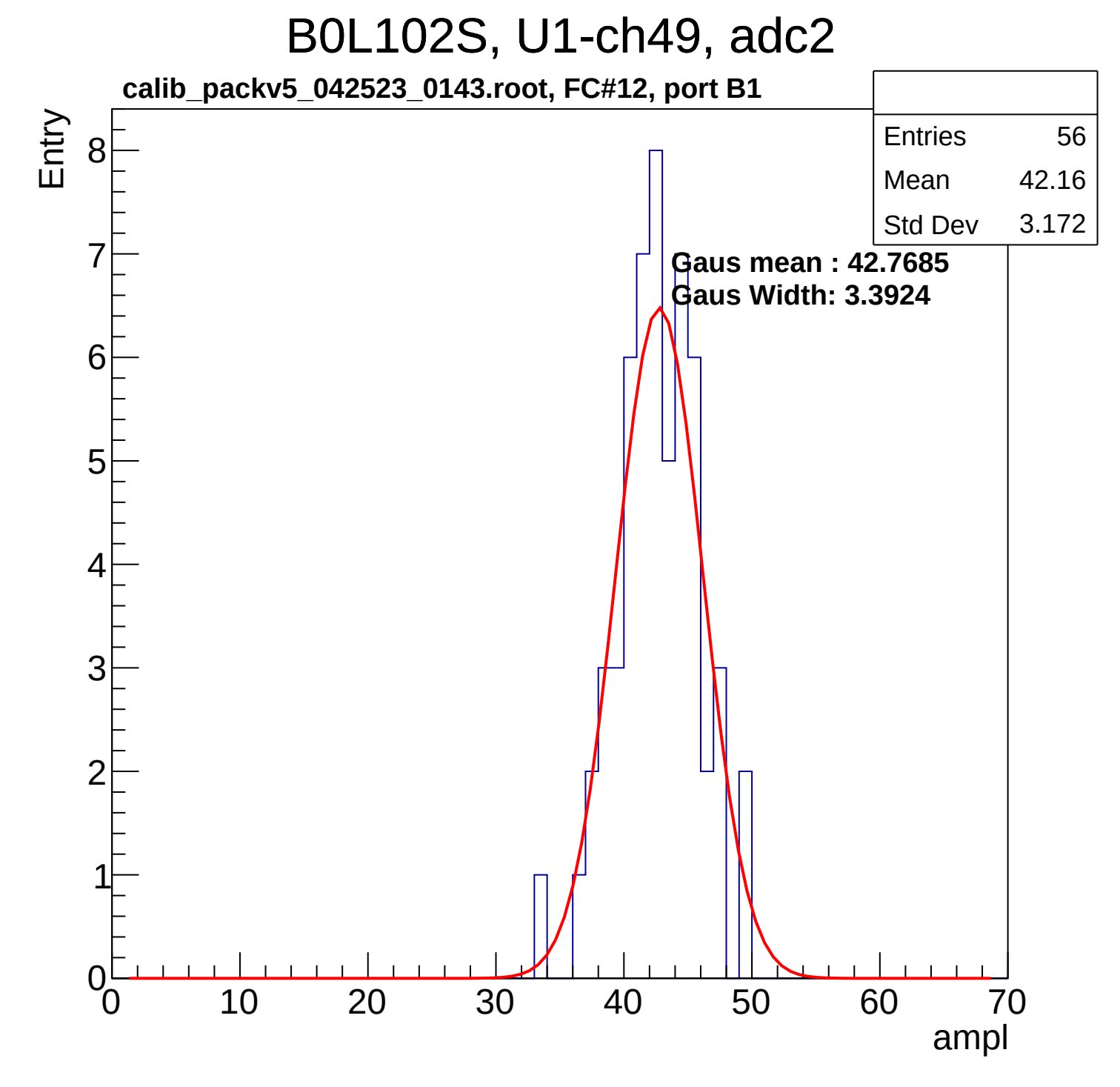
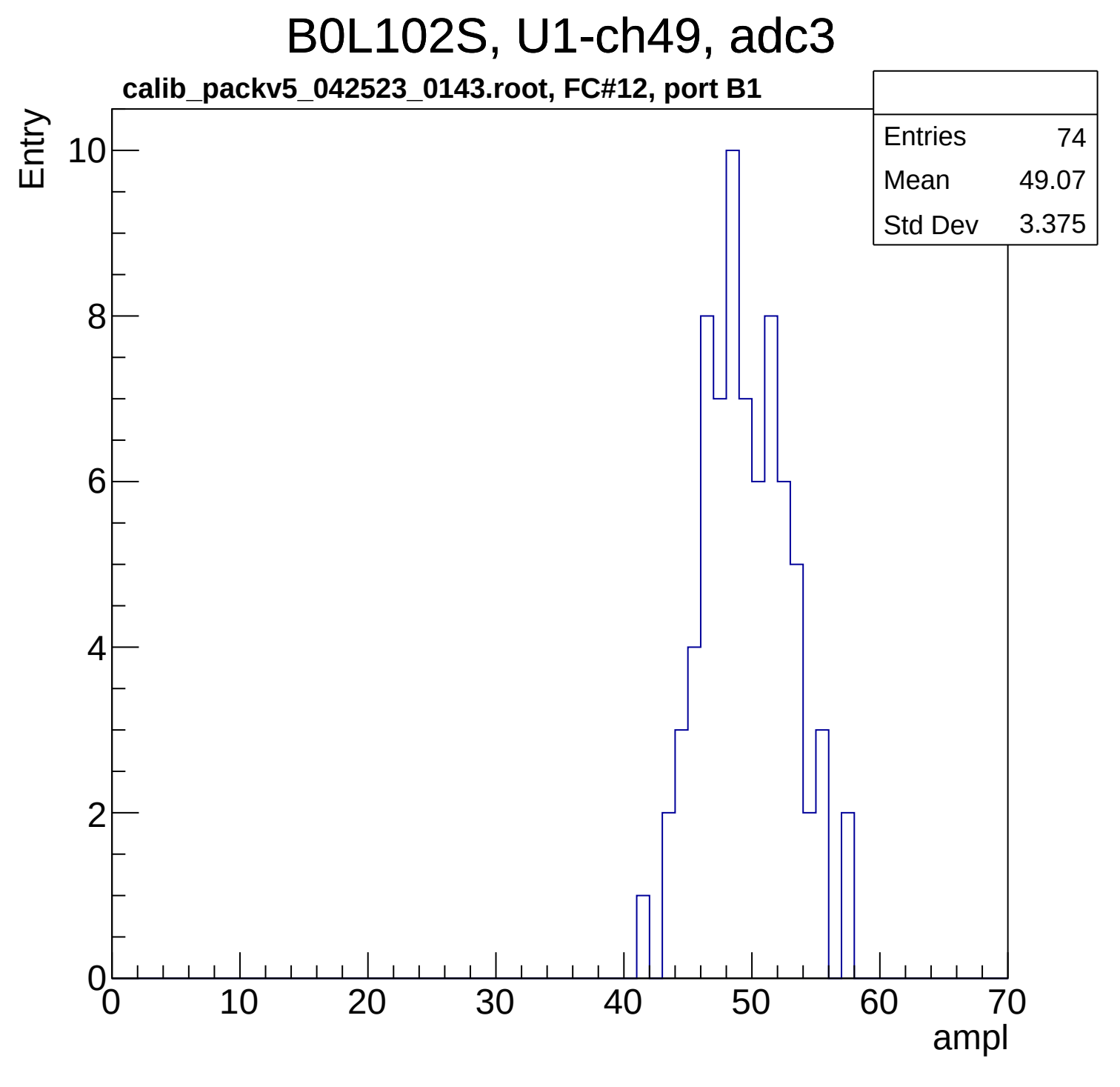
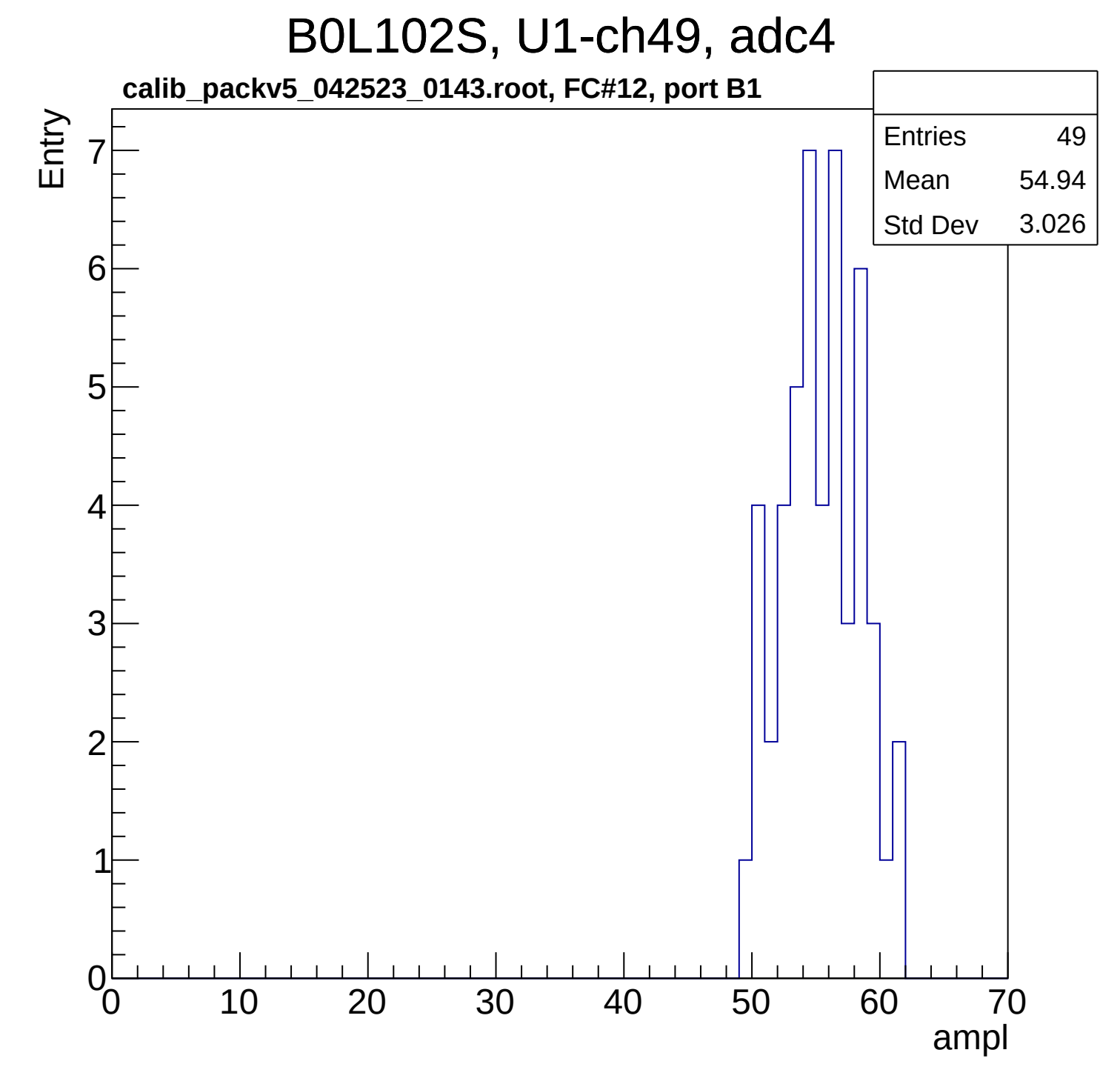
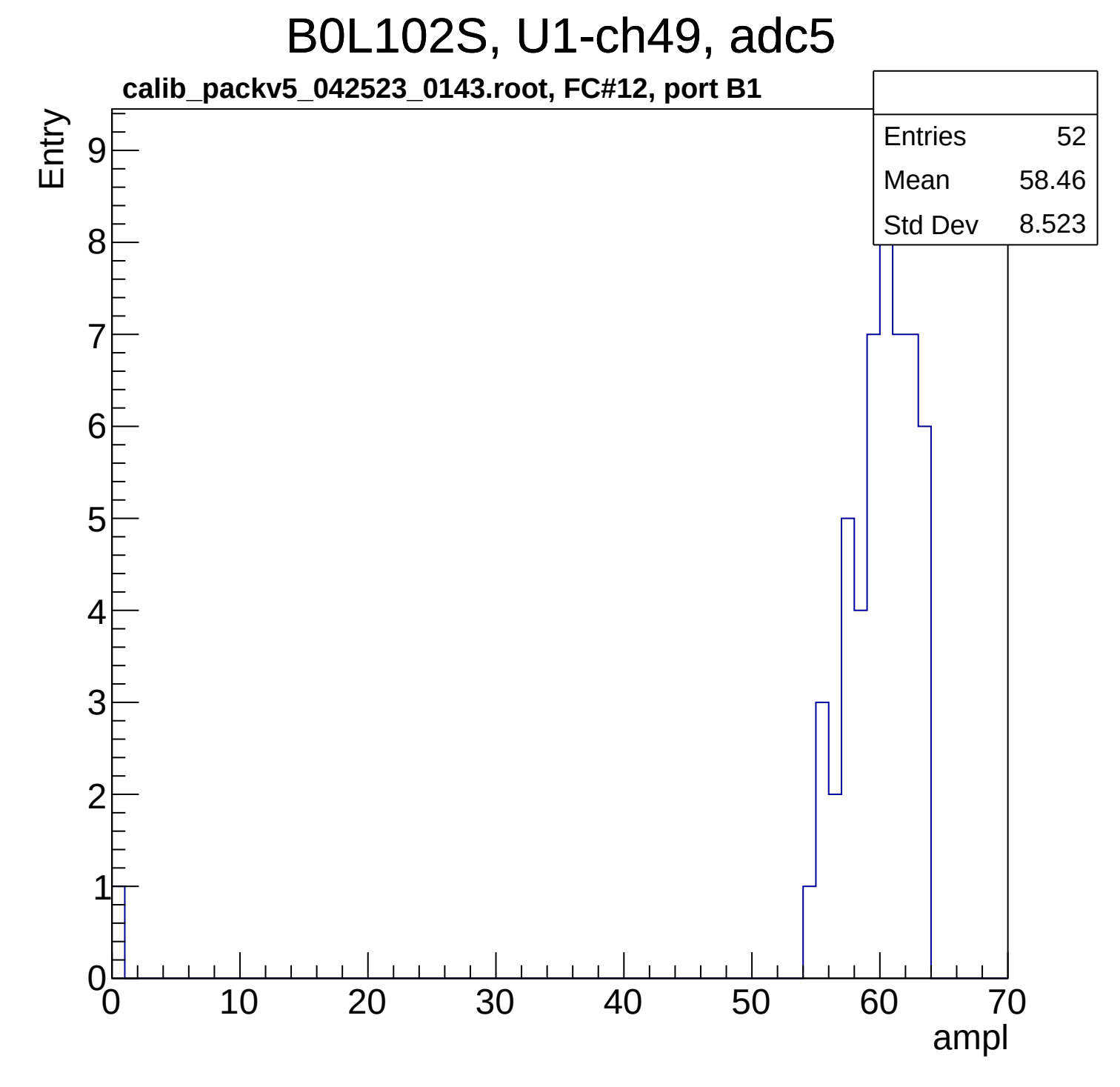
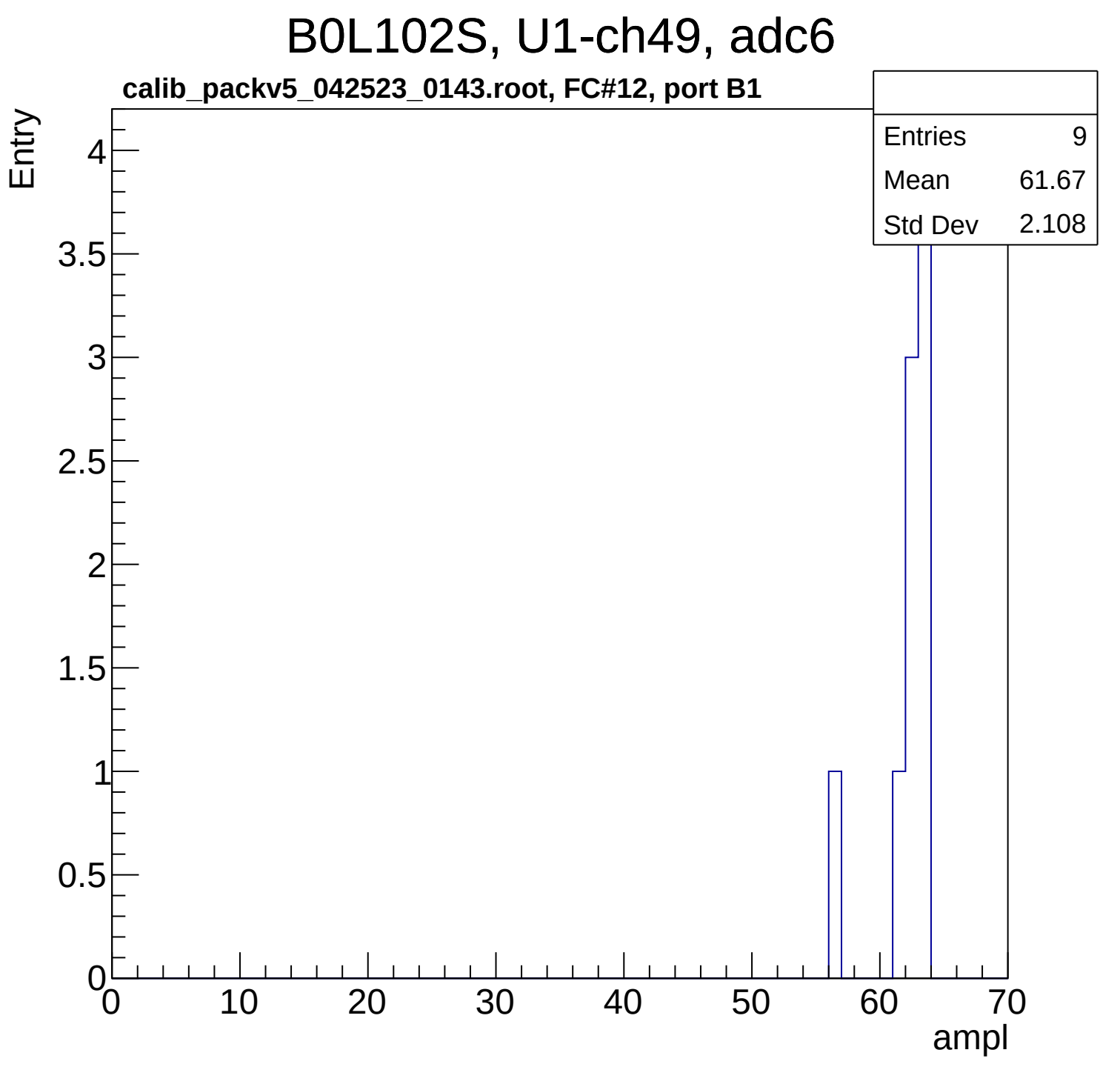

B0L102S, U1-ch49, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 42.16
Std Dev 3.172
Gaus mean : 42.7685
Gaus Width: 3.3924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch49, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 49.07
Std Dev 3.375
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch49, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 54.94
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch49, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.46
Std Dev 8.523
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch49, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.67
Std Dev 2.108
Entry
ampl
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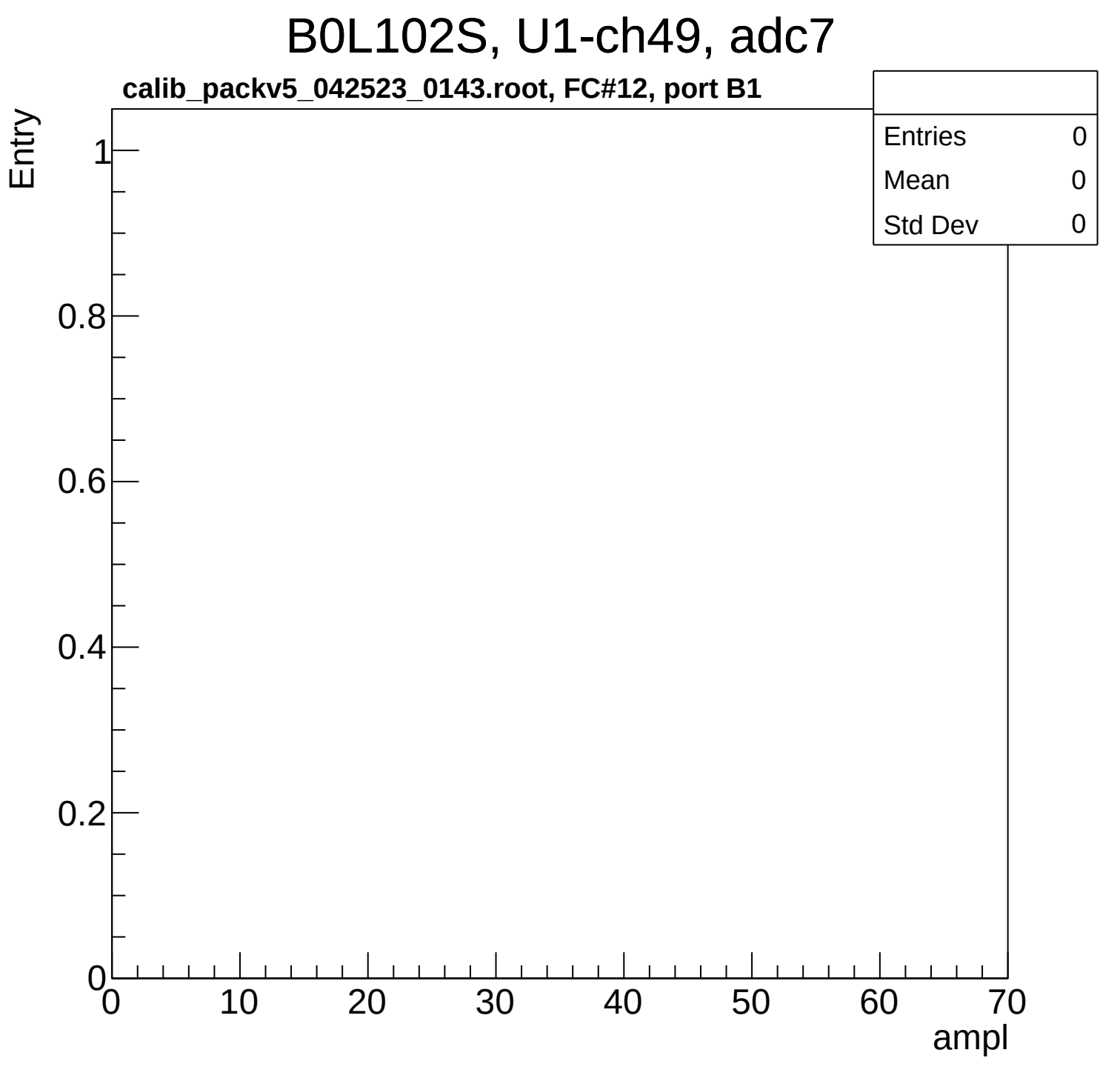
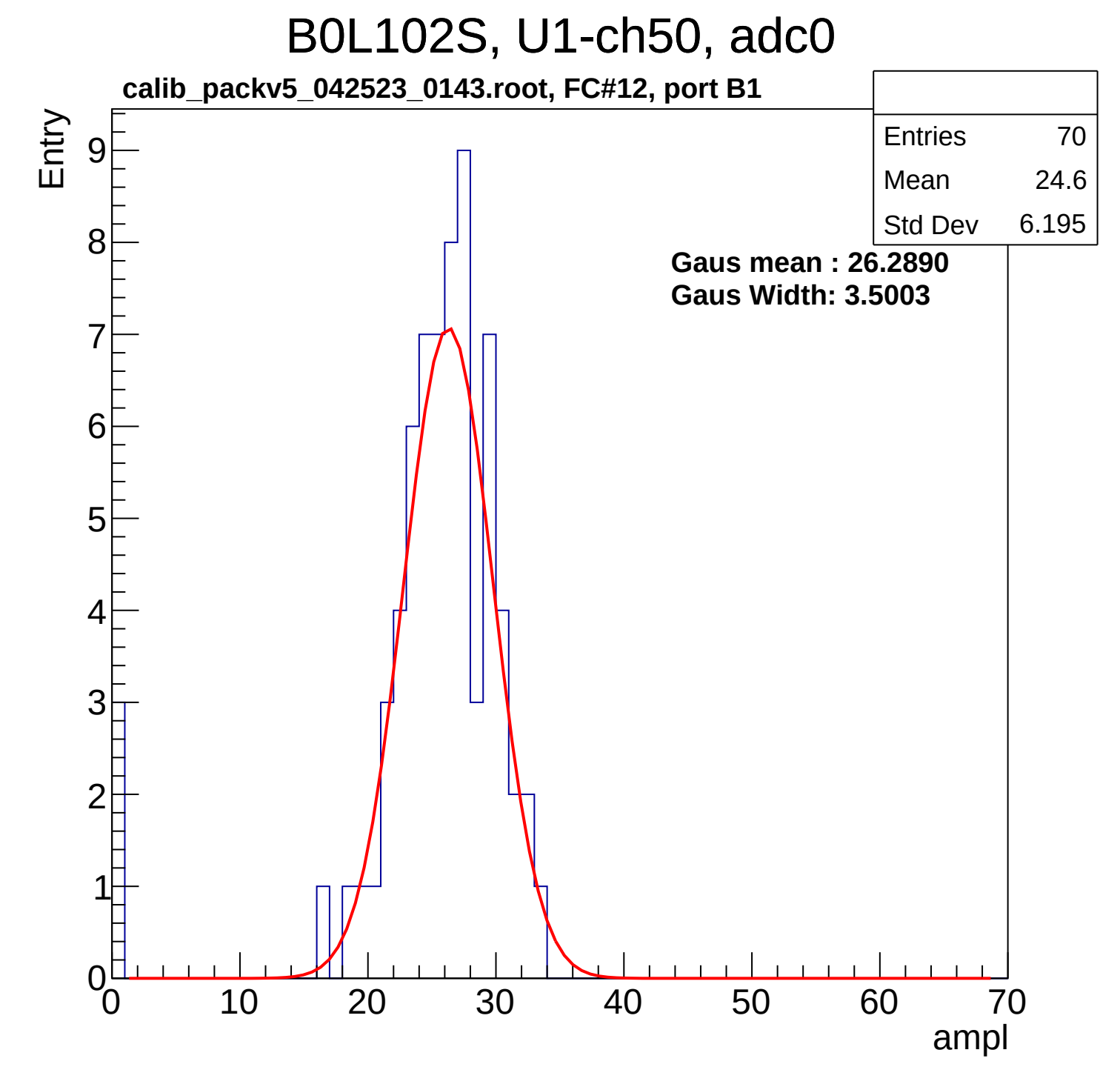
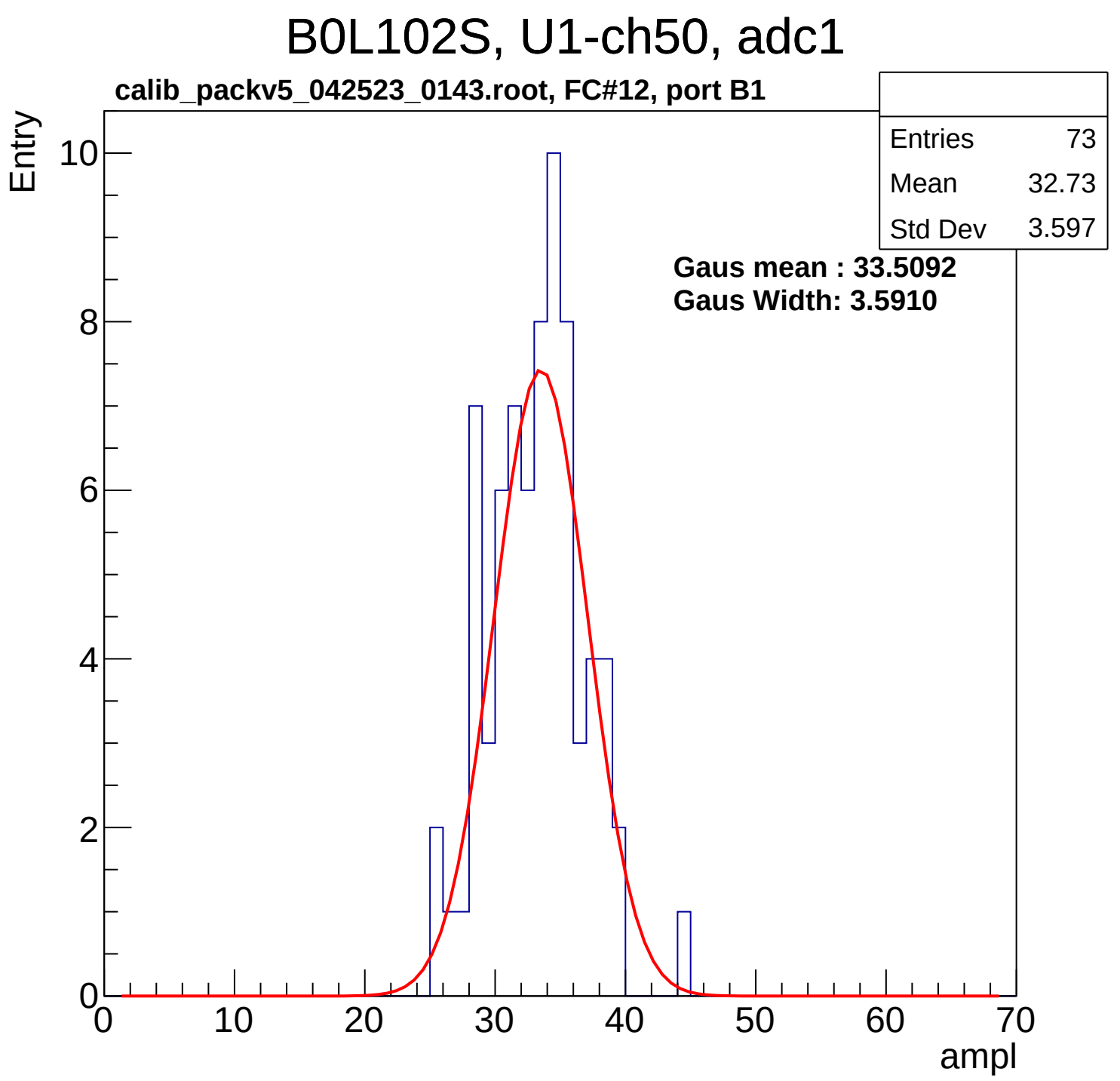
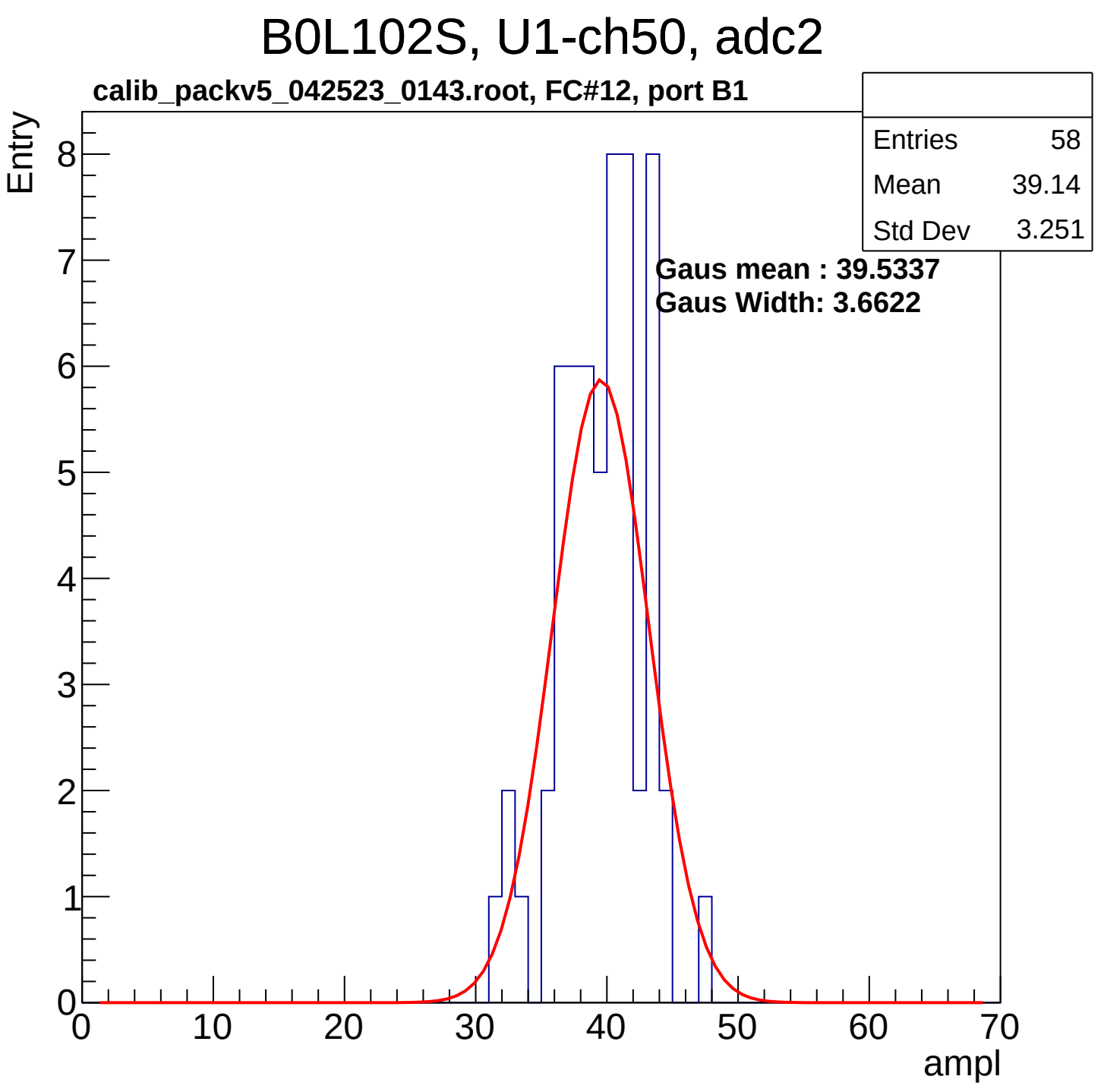
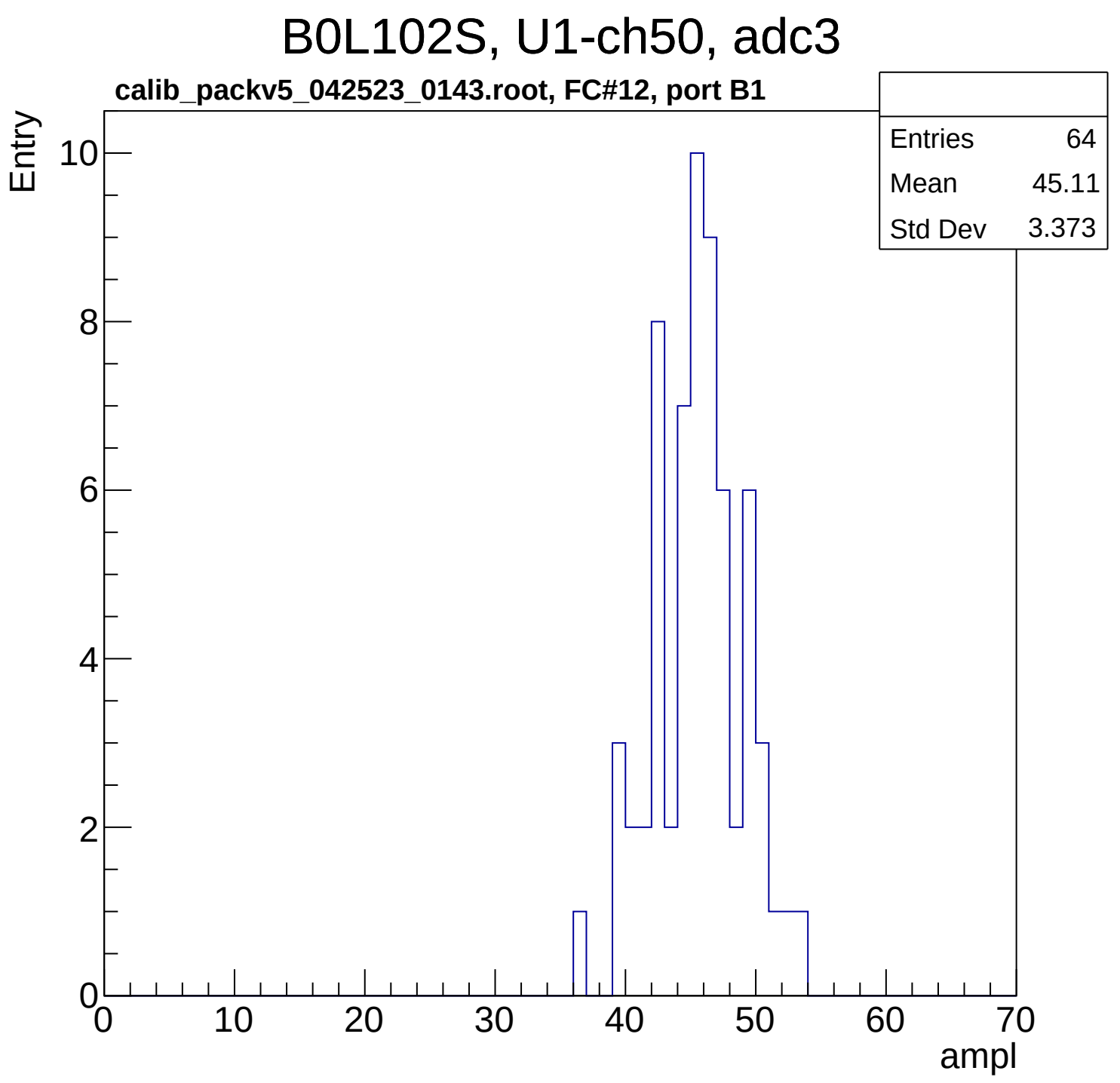
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

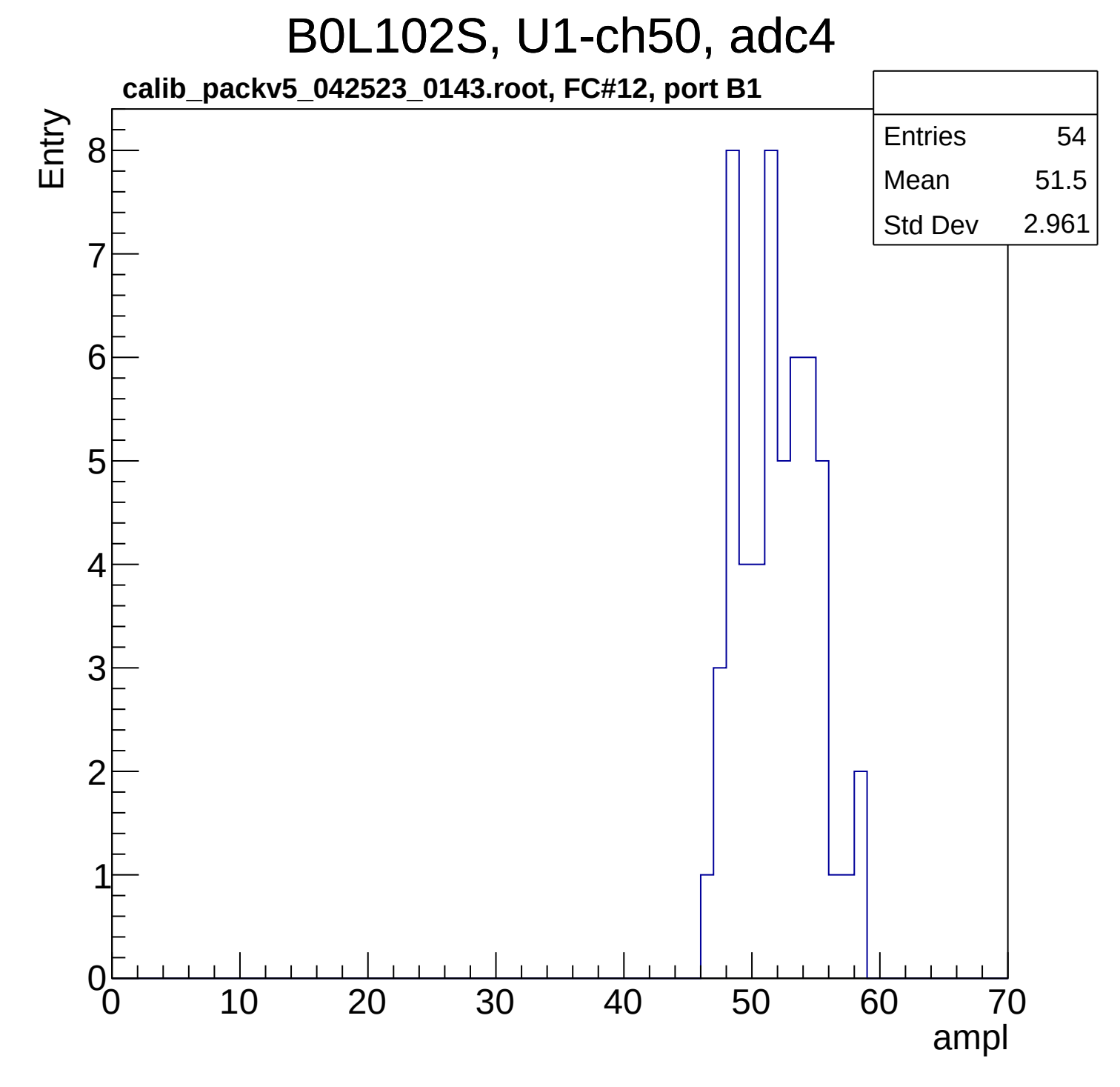

B0L102S, U1-ch49, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

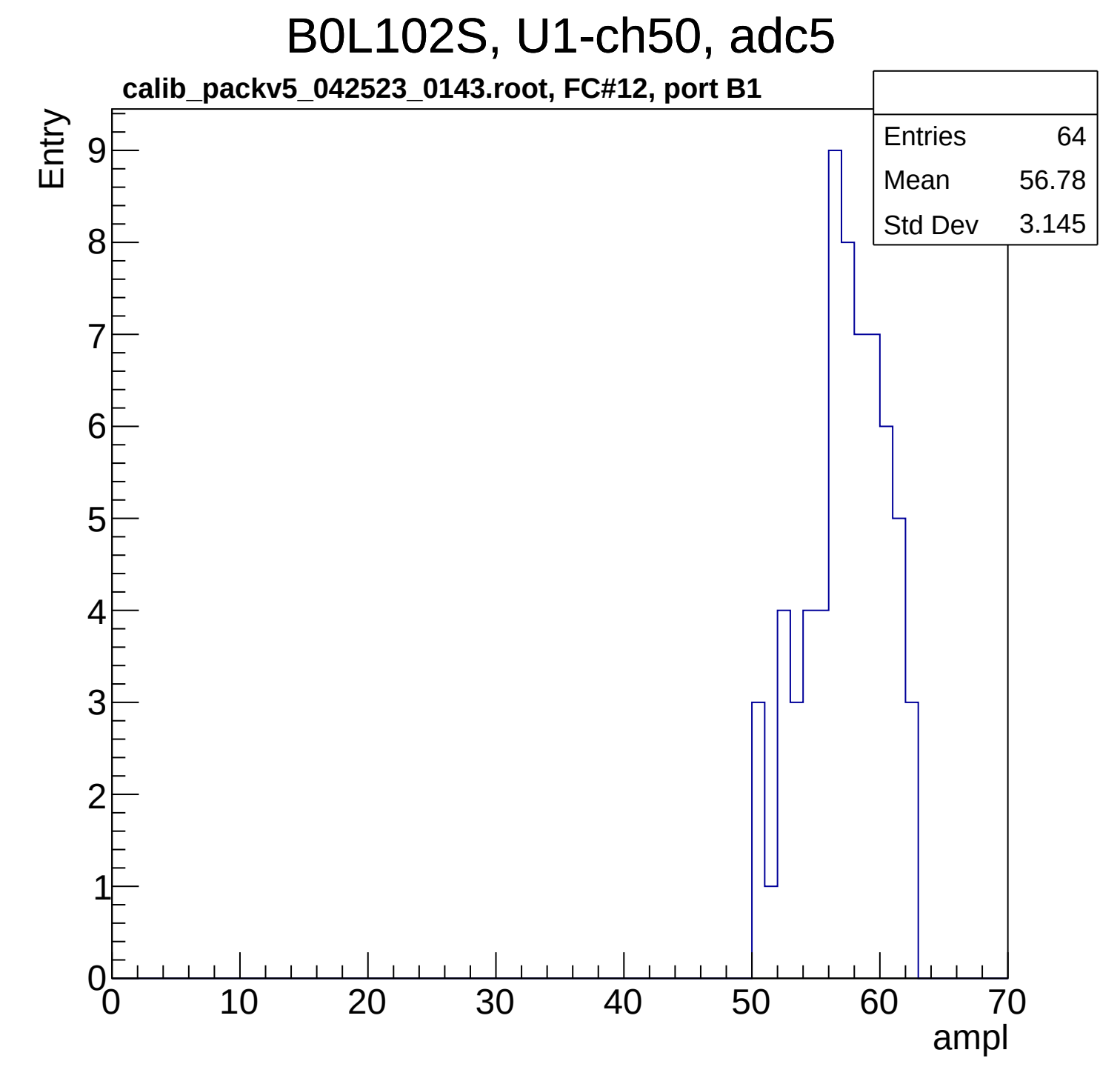

B0L102S, U1-ch50, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 24.6
Std Dev 6.195
Gaus mean : 26.2890
Gaus Width: 3.5003
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch50, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 32.73
Std Dev 3.597
Gaus mean : 33.5092
Gaus Width: 3.5910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch50, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 39.14
Std Dev 3.251
Gaus mean : 39.5337
Gaus Width: 3.6622
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch50, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 45.11
Std Dev 3.373
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch50, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 51.5
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch50, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.78
Std Dev 3.145
Entry
ampl
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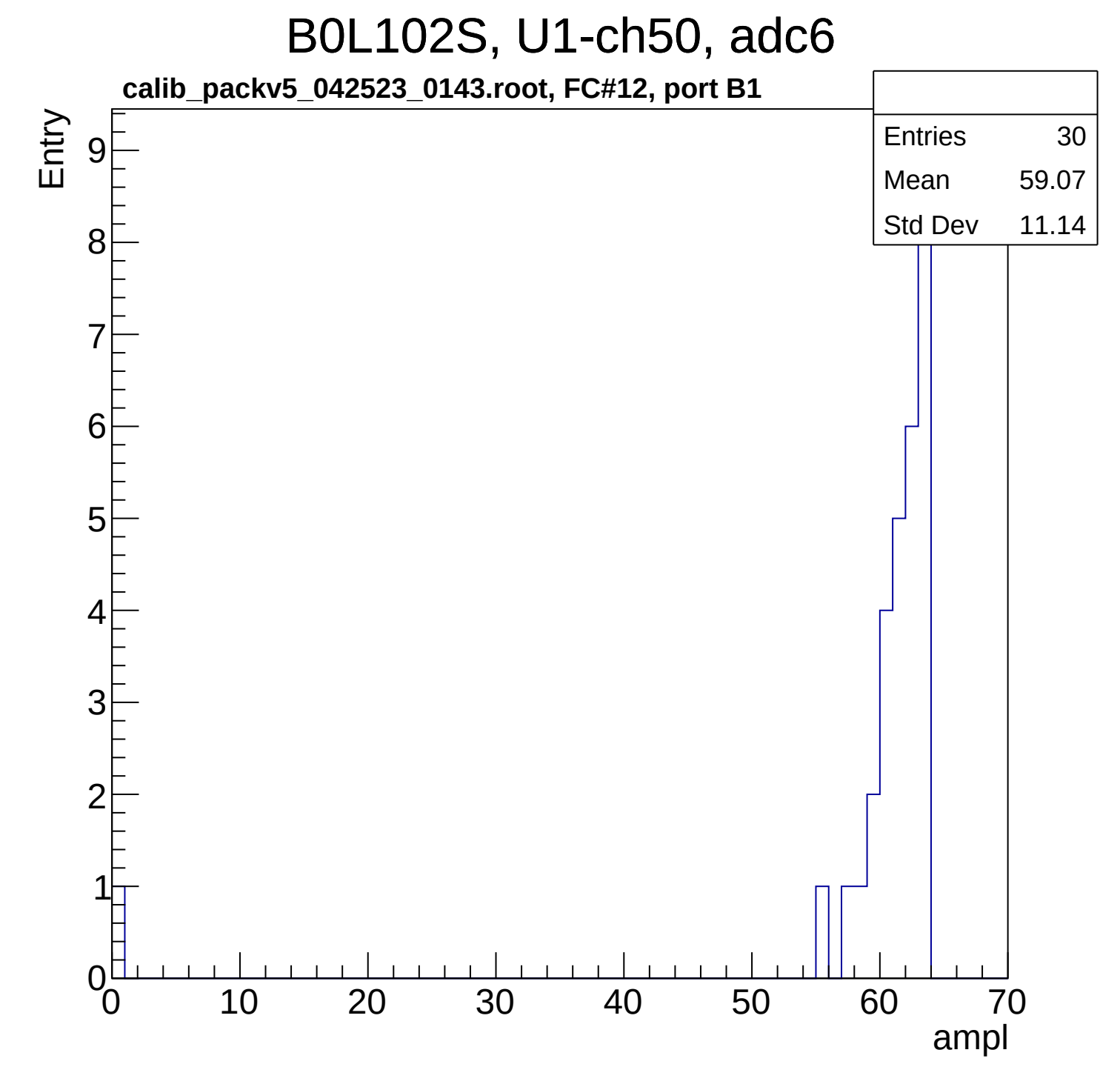
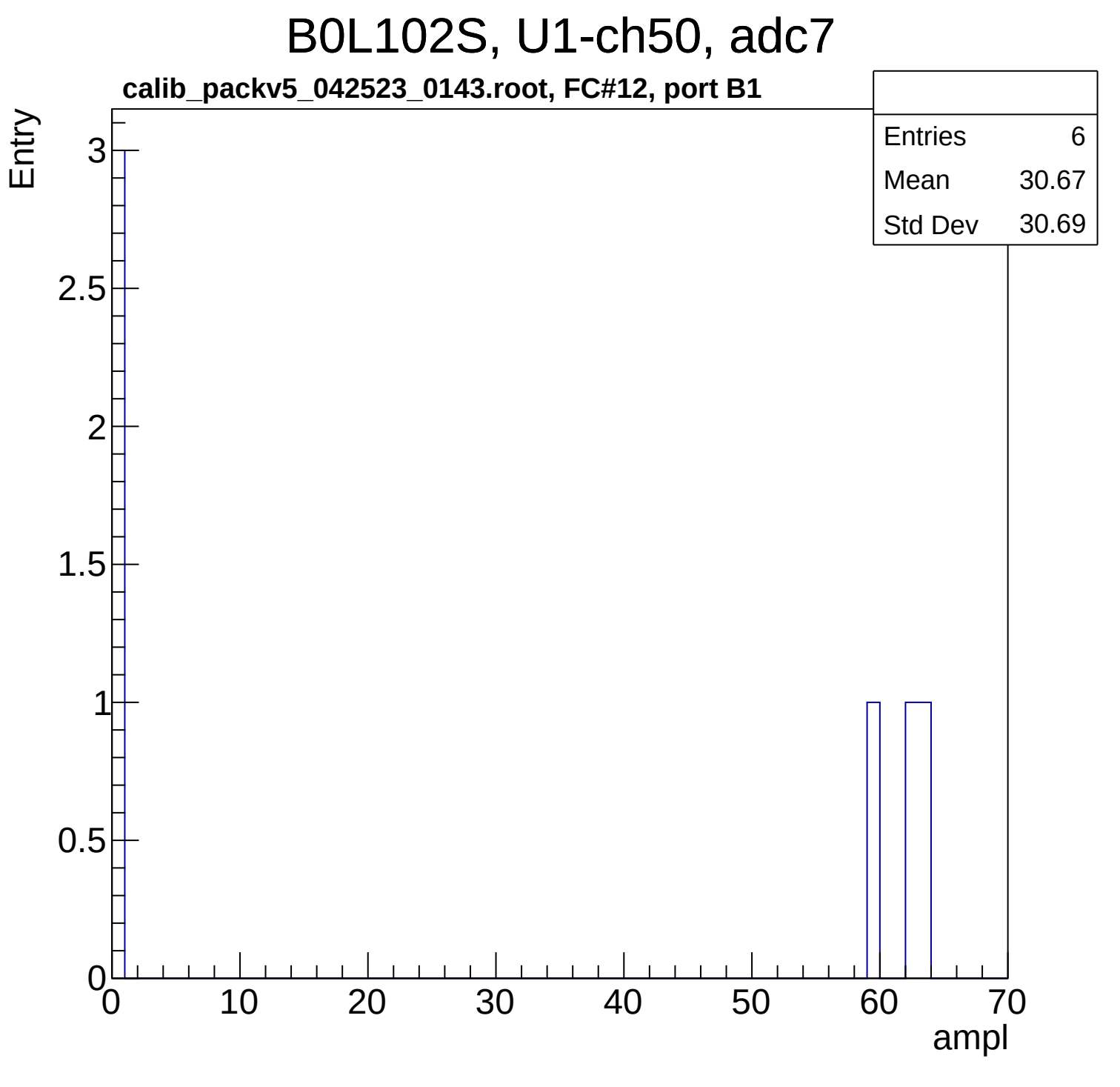
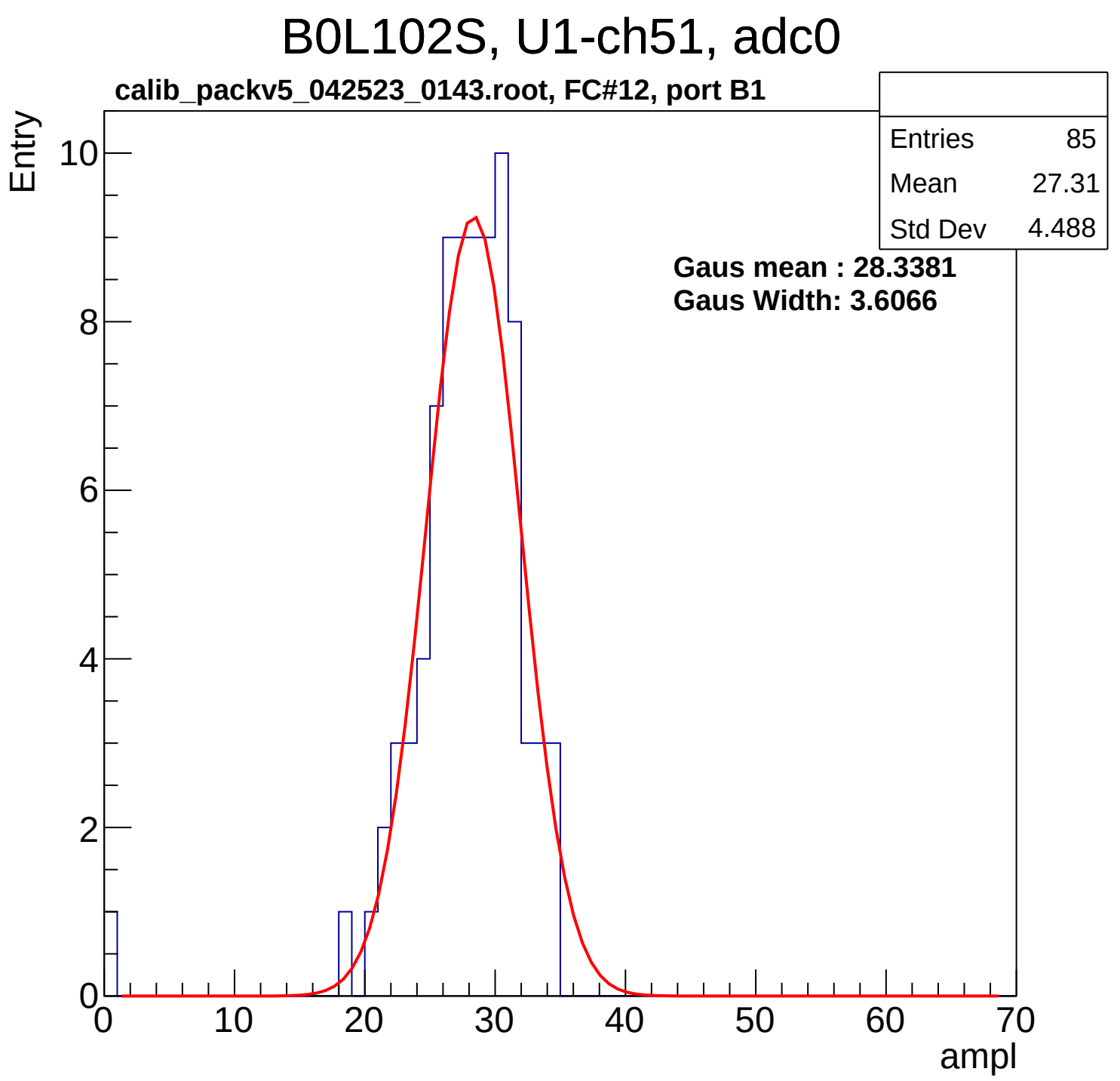
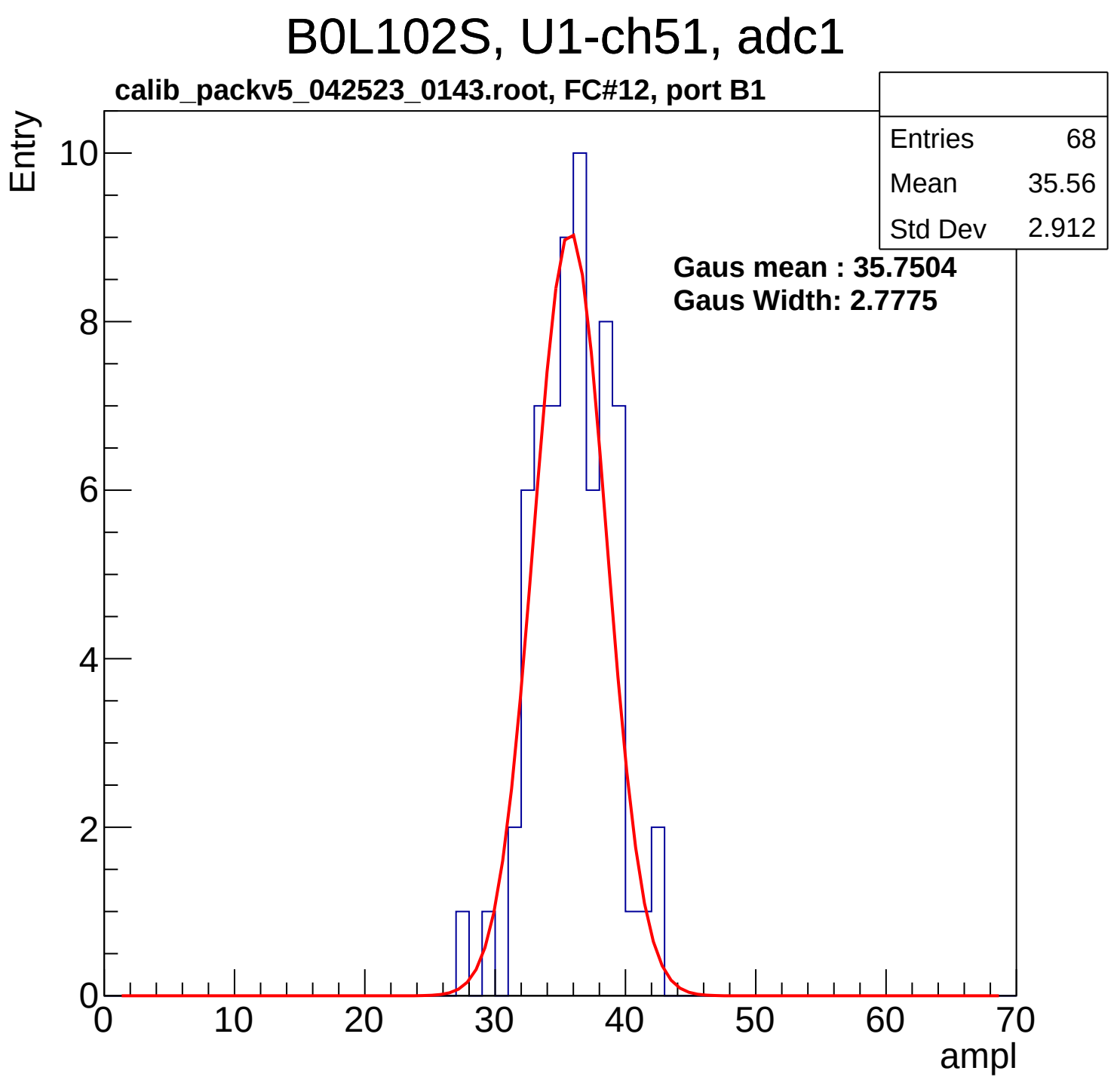
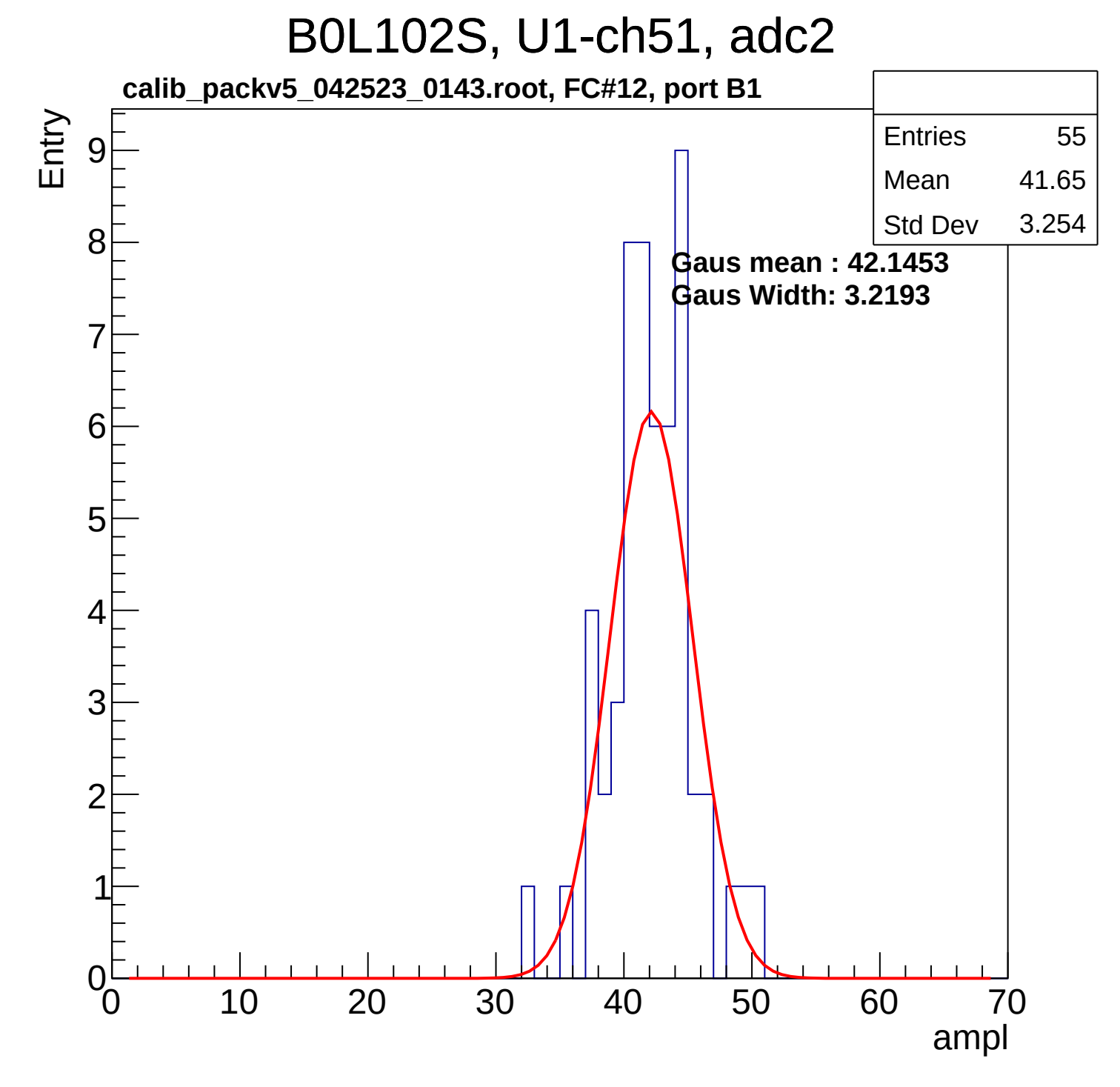
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

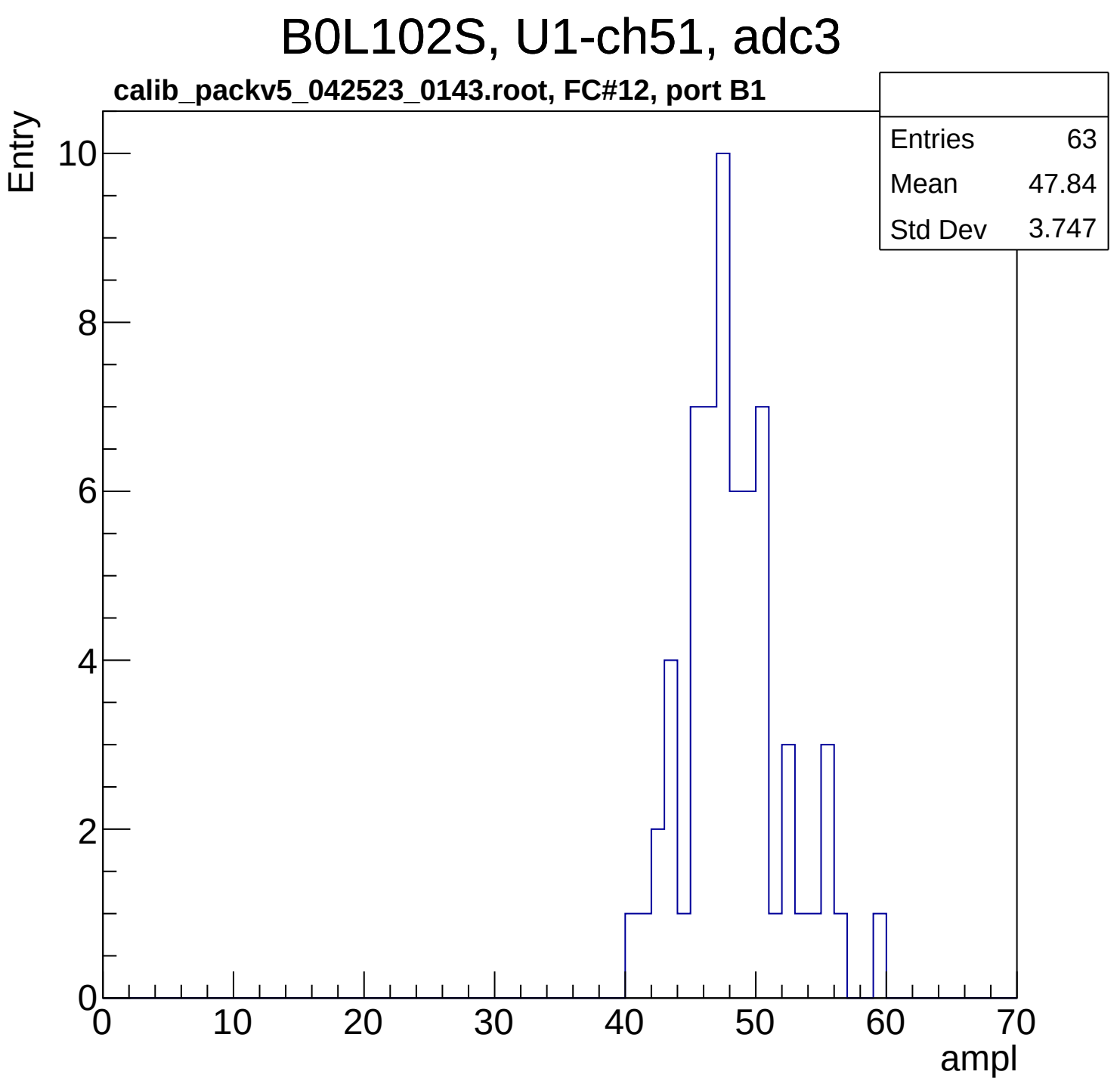

B0L102S, U1-ch50, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 59.07
Std Dev 11.14
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

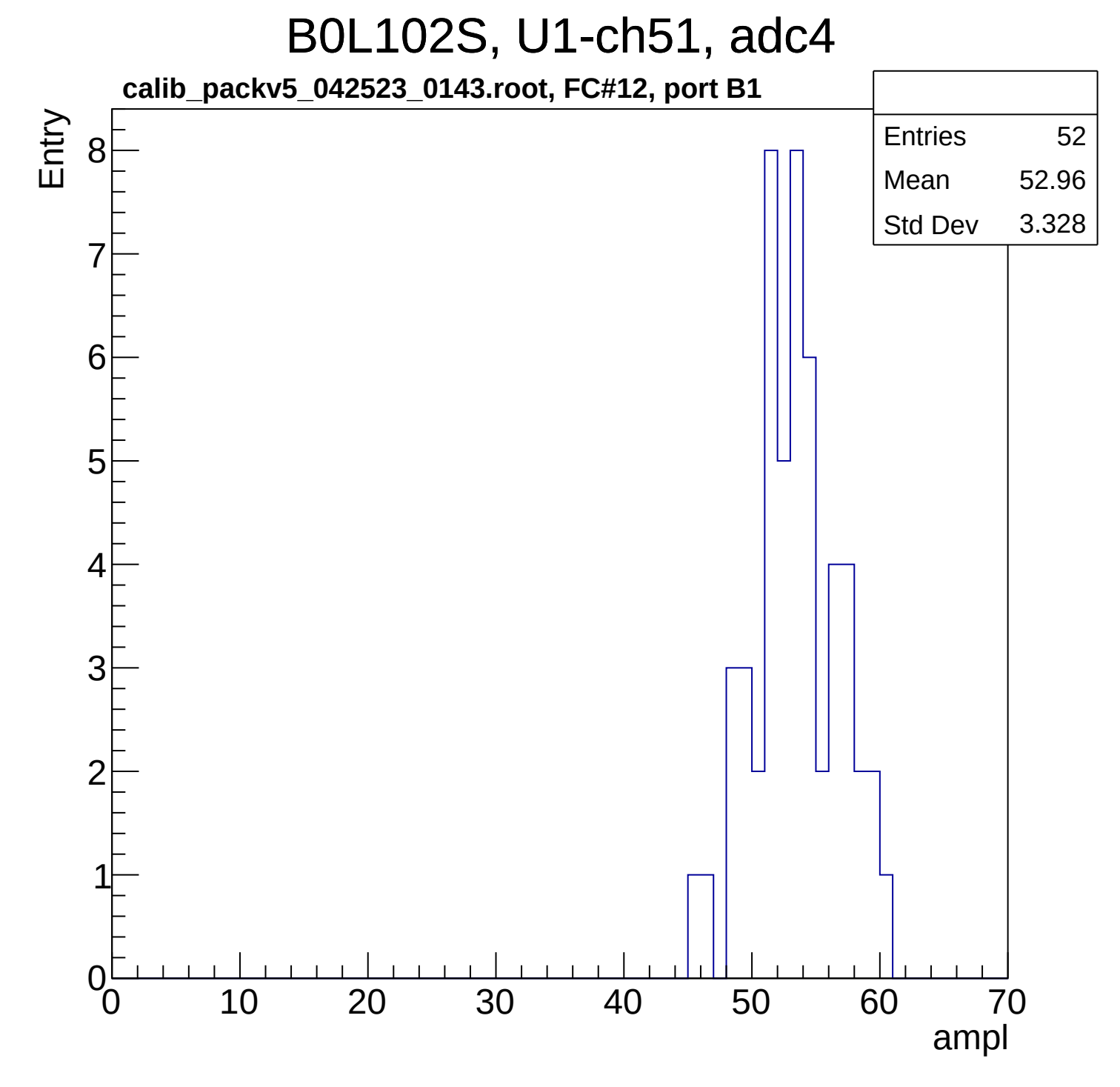

B0L102S, U1-ch50, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 30.67
Std Dev 30.69
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch51, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 27.31
Std Dev 4.488
Gaus mean : 28.3381
Gaus Width: 3.6066
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch51, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 35.56
Std Dev 2.912
Gaus mean : 35.7504
Gaus Width: 2.7775
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch51, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 41.65
Std Dev 3.254
Gaus mean : 42.1453
Gaus Width: 3.2193
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch51, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 47.84
Std Dev 3.747
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch51, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 52.96
Std Dev 3.328
Entry
ampl
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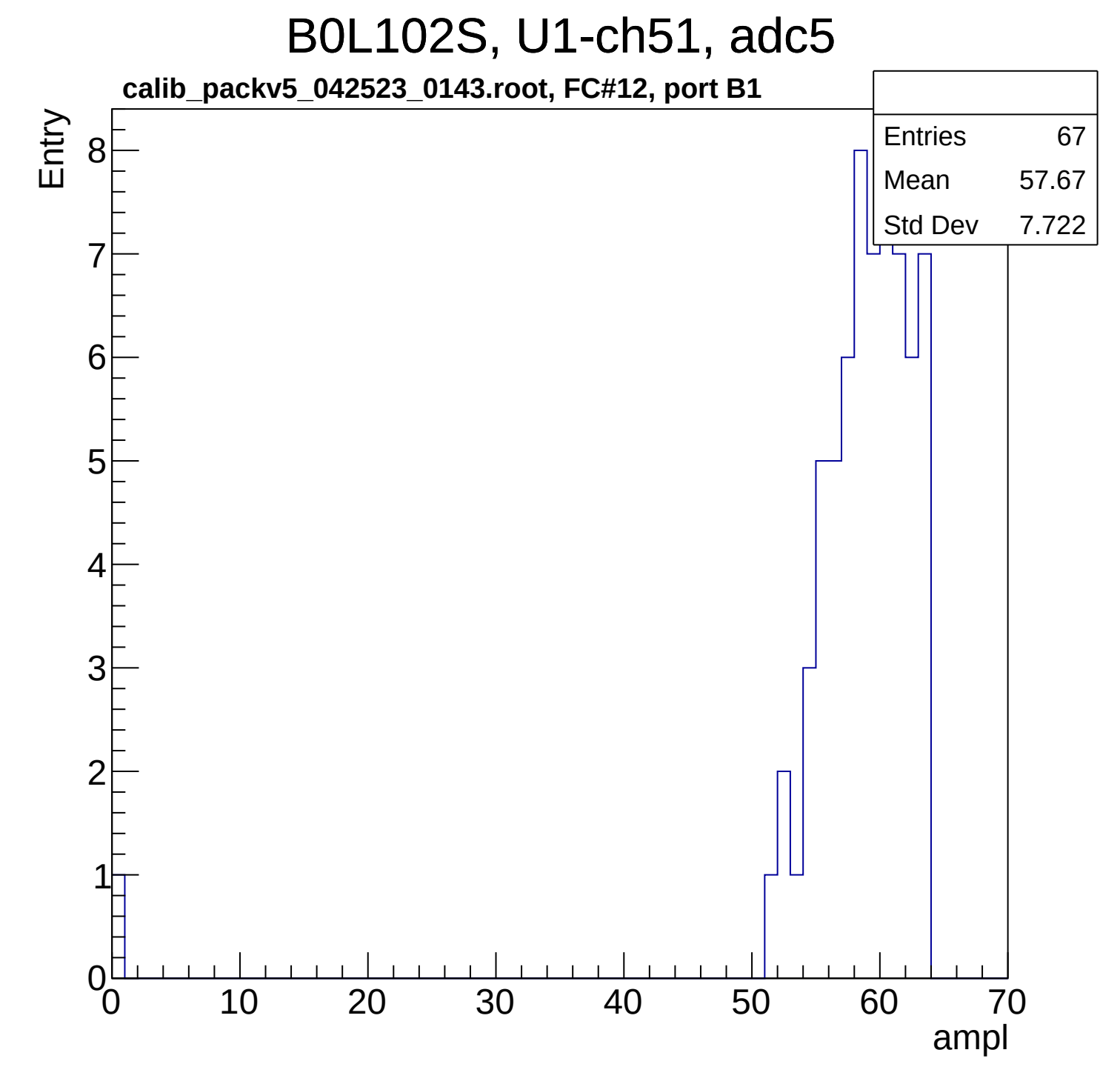
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch51, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 57.67
Std Dev 7.722
Entry
ampl
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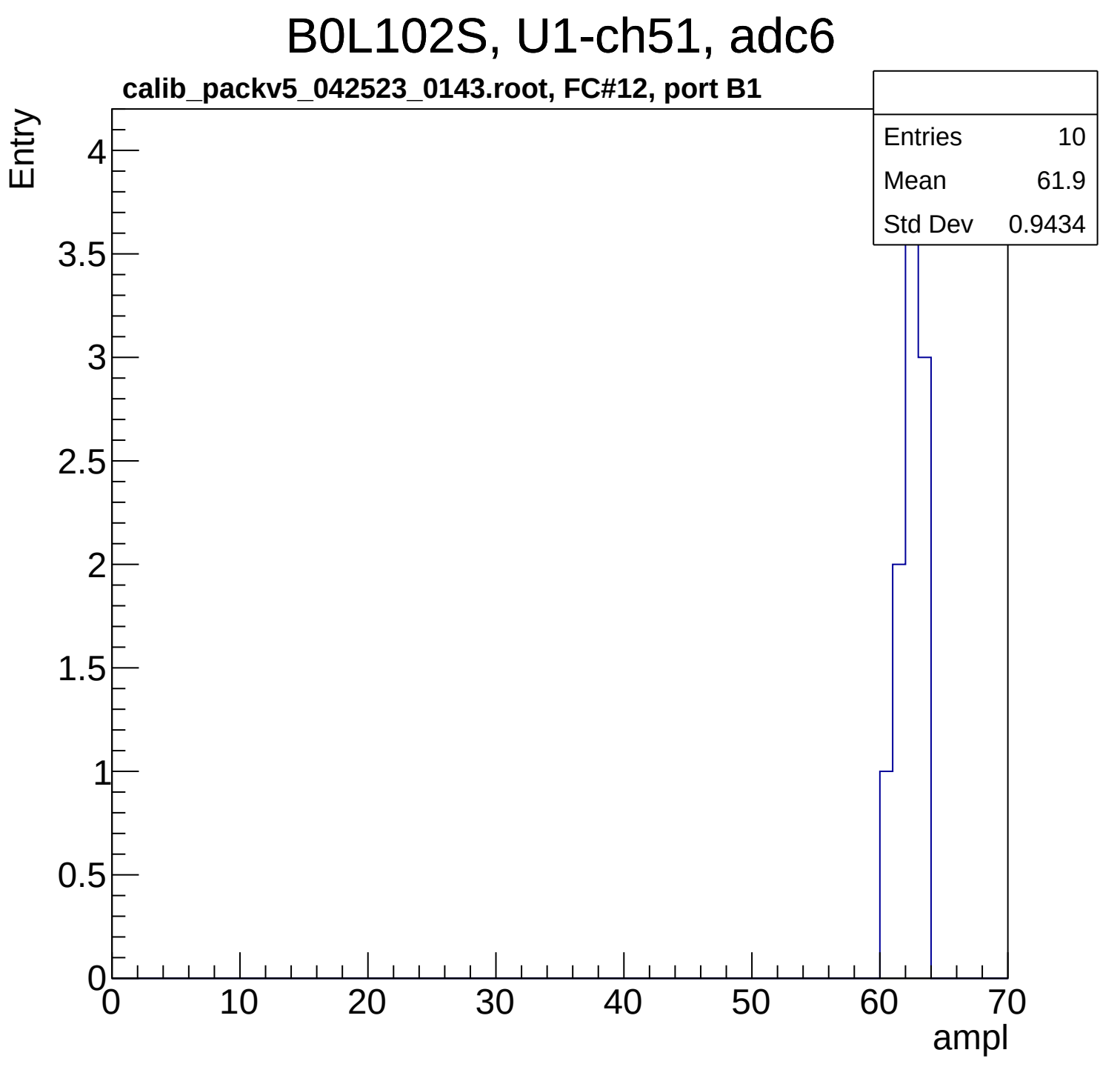
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch51, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 61.9
Std Dev 0.9434
Entry
ampl
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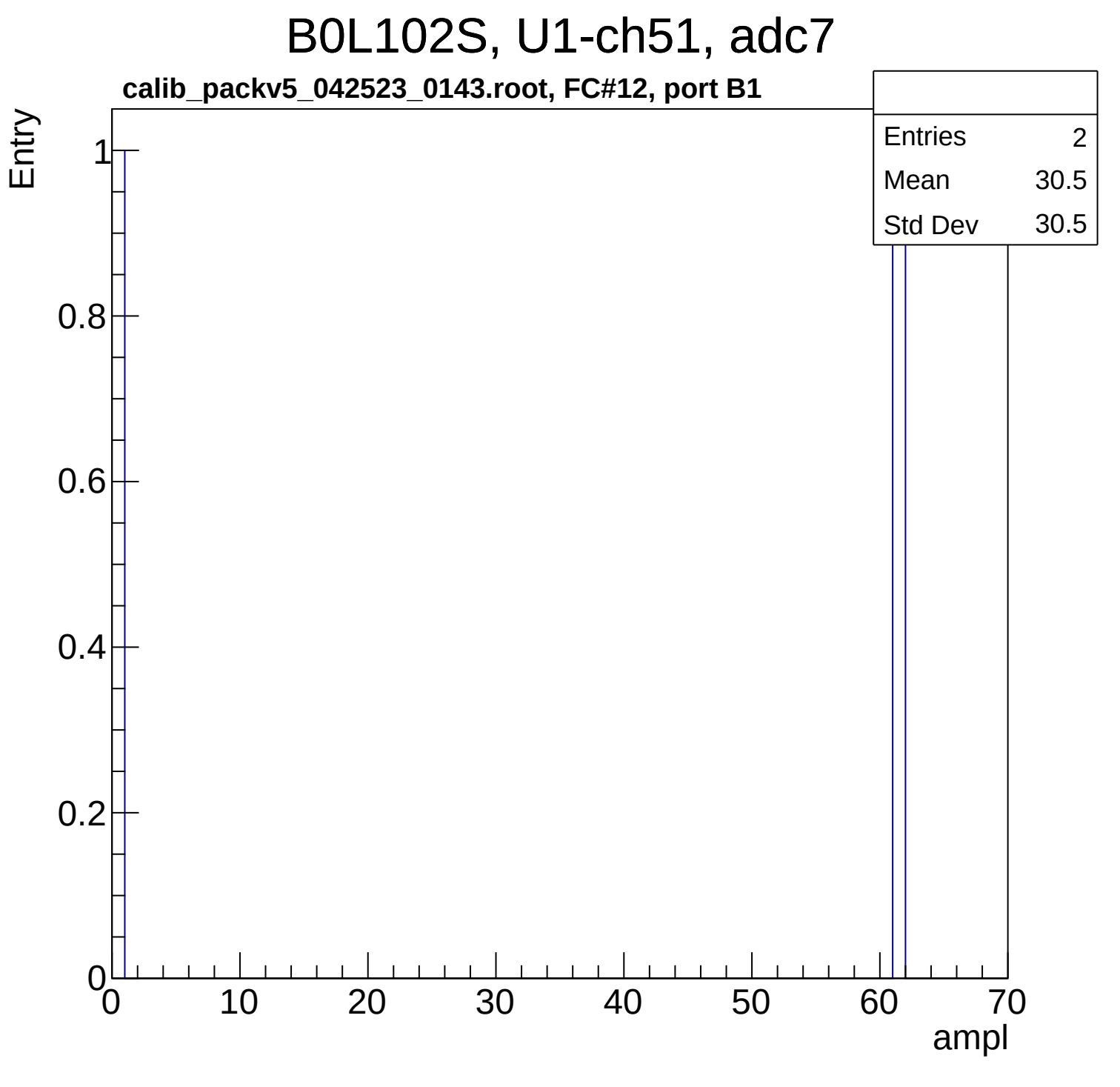
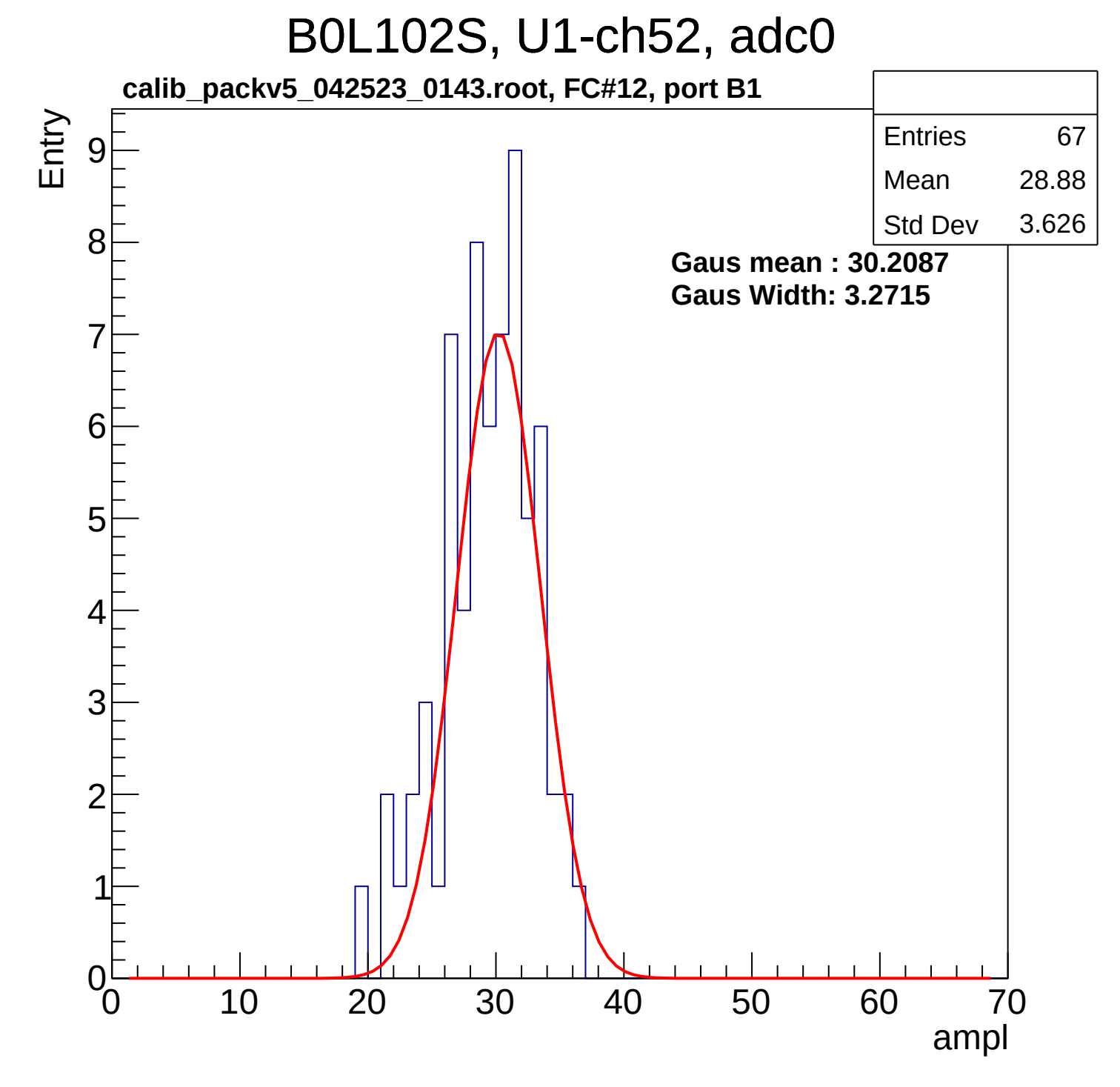
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch51, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch52, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.88
Std Dev 3.626
Gaus mean : 30.2087
Gaus Width: 3.2715
Entry
ampl
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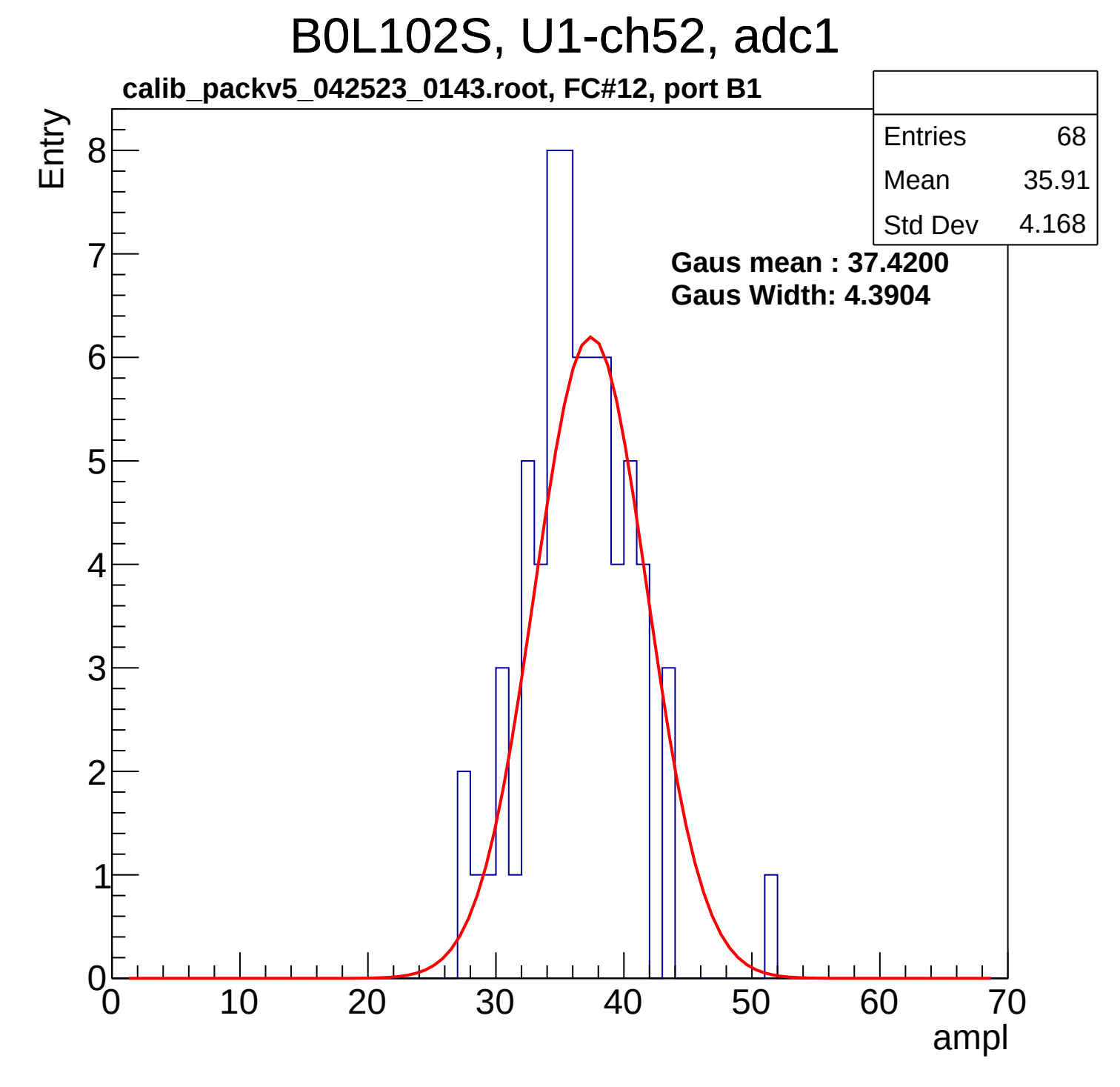
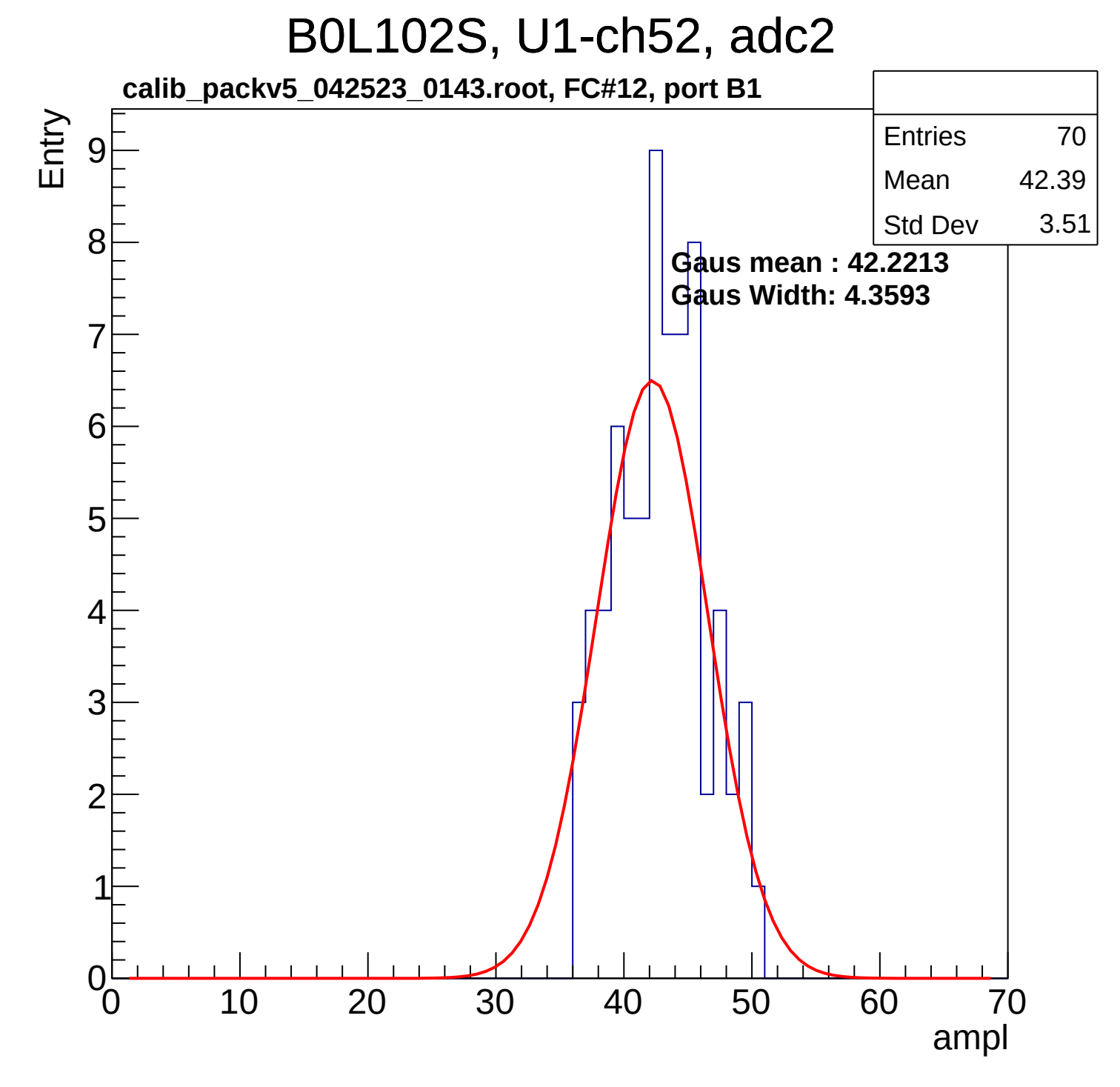
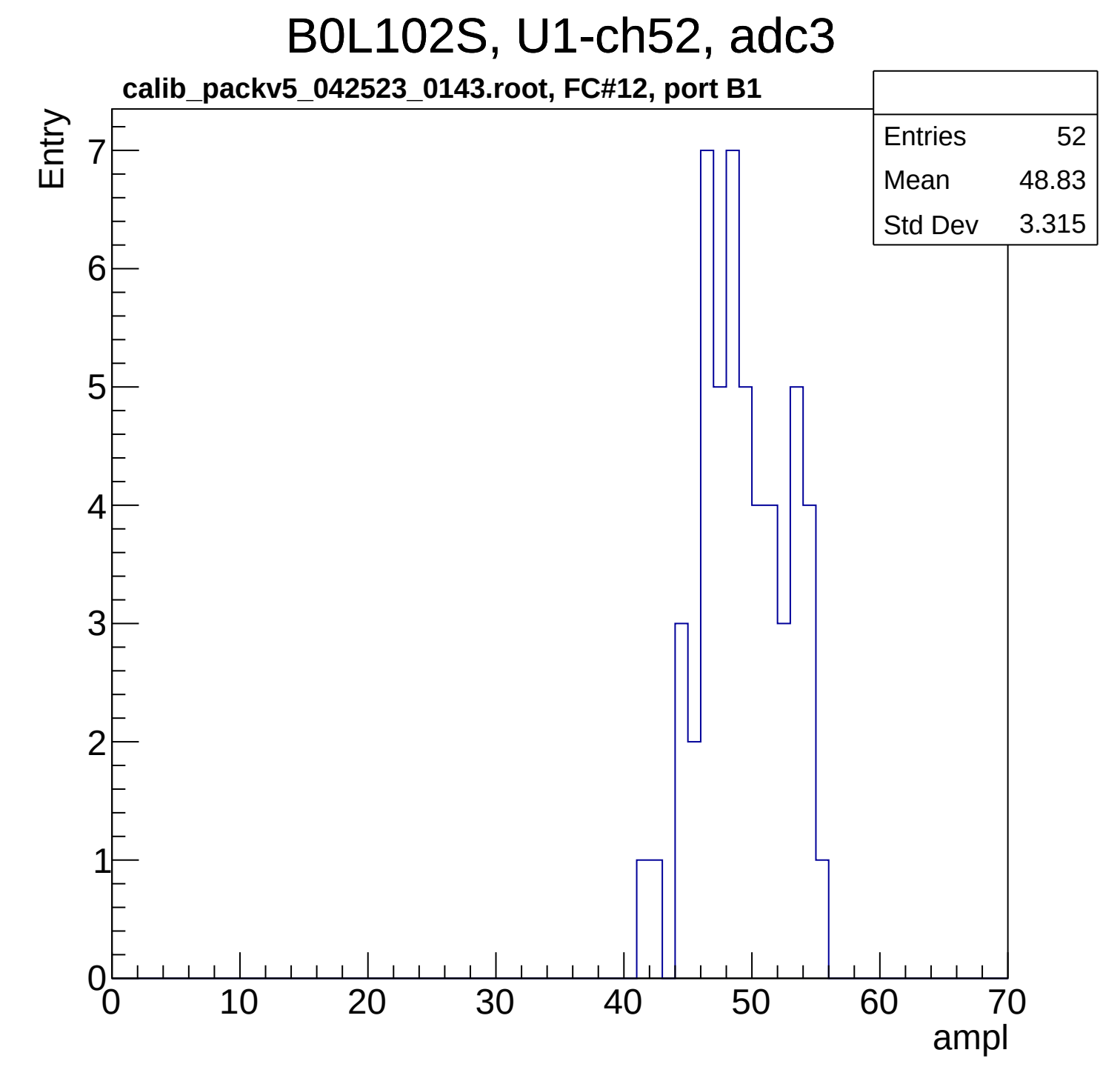
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch52, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 35.91
Std Dev 4.168
Gaus mean : 37.4200
Gaus Width: 4.3904
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch52, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 42.39
Std Dev 3.51
Gaus mean : 42.2213
Gaus Width: 4.3593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch52, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 48.83
Std Dev 3.315
Entry
ampl
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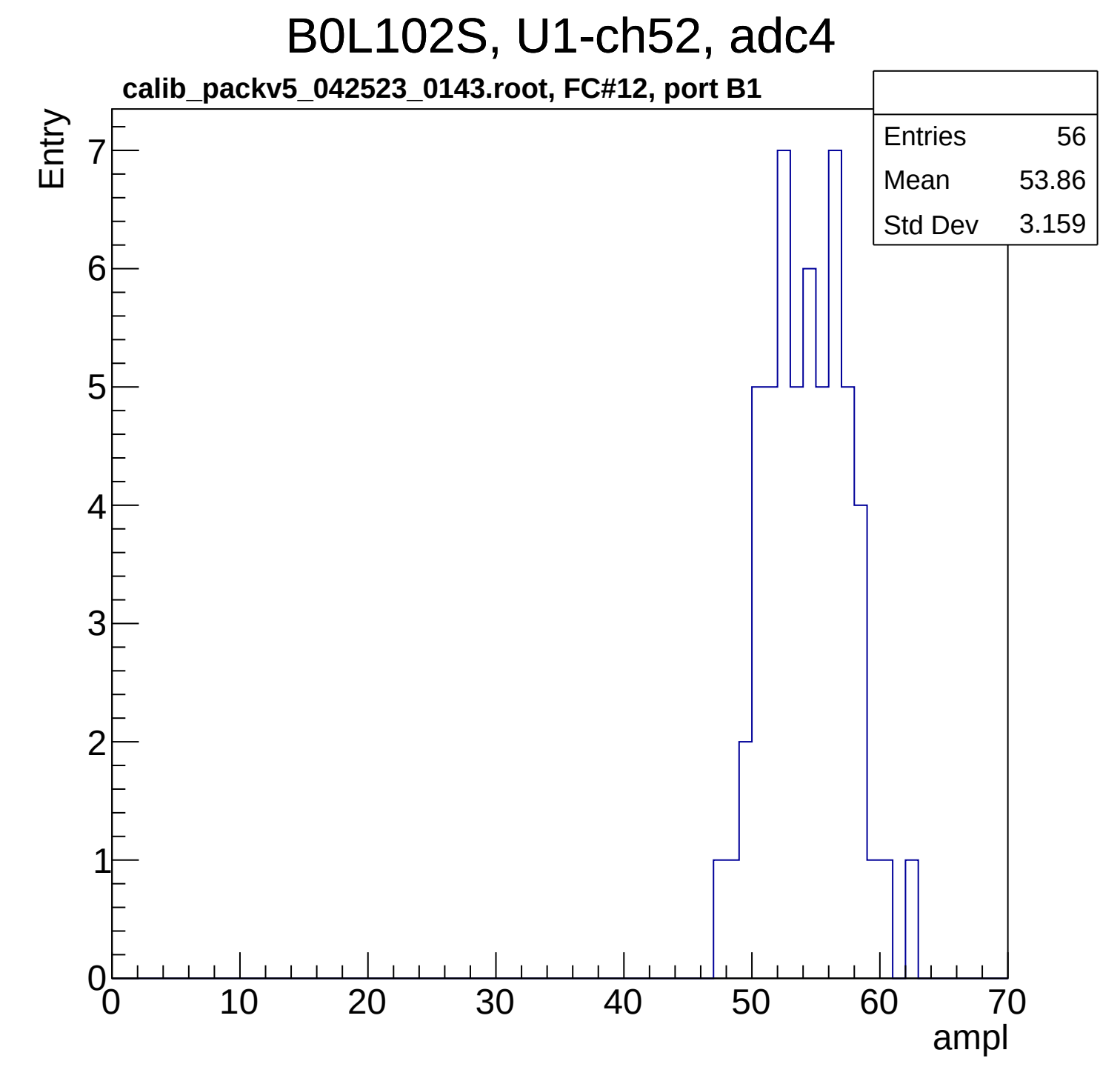
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch52, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 53.86
Std Dev 3.159
Entry
ampl
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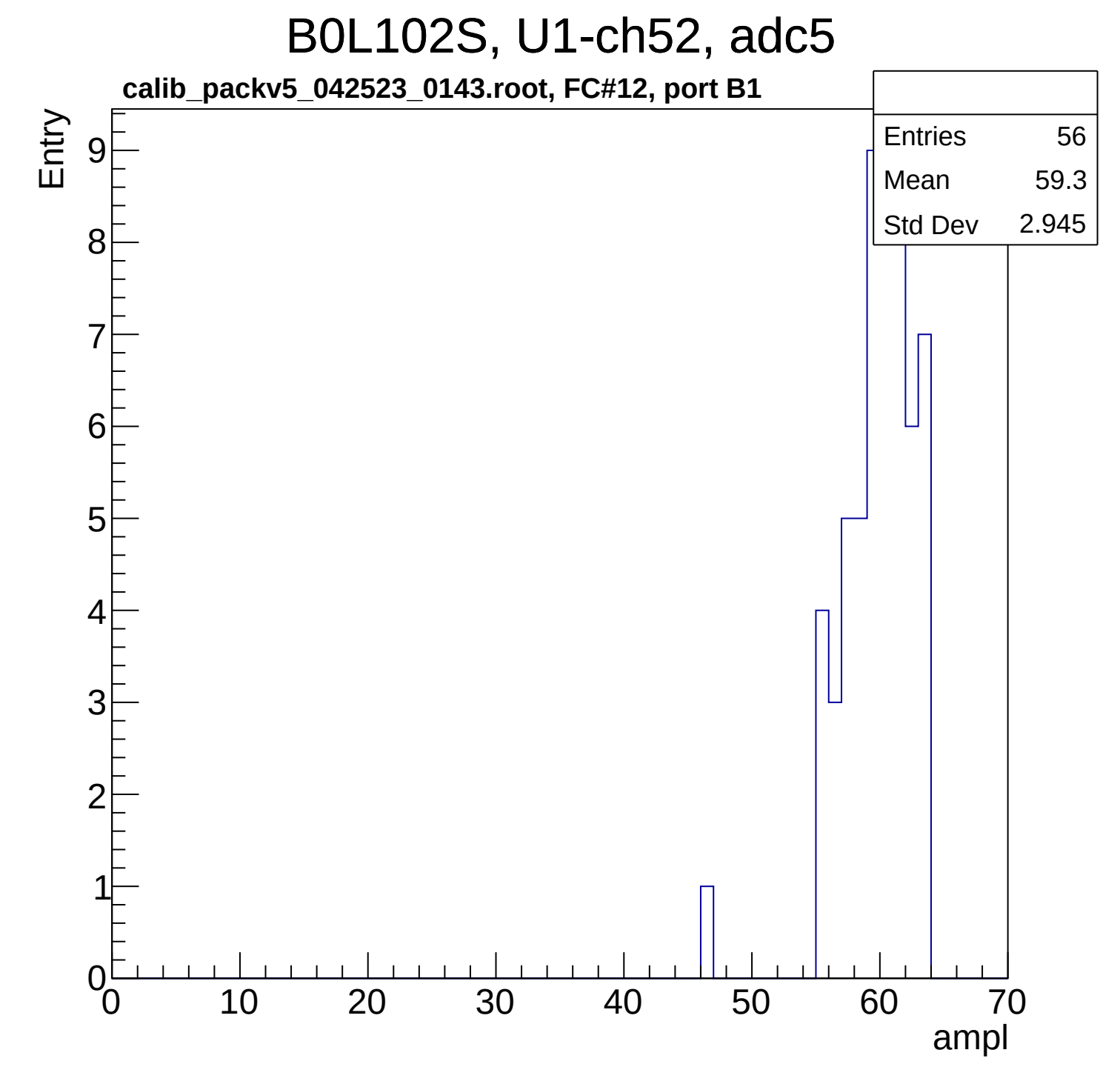
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch52, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 59.3
Std Dev 2.945
Entry
ampl
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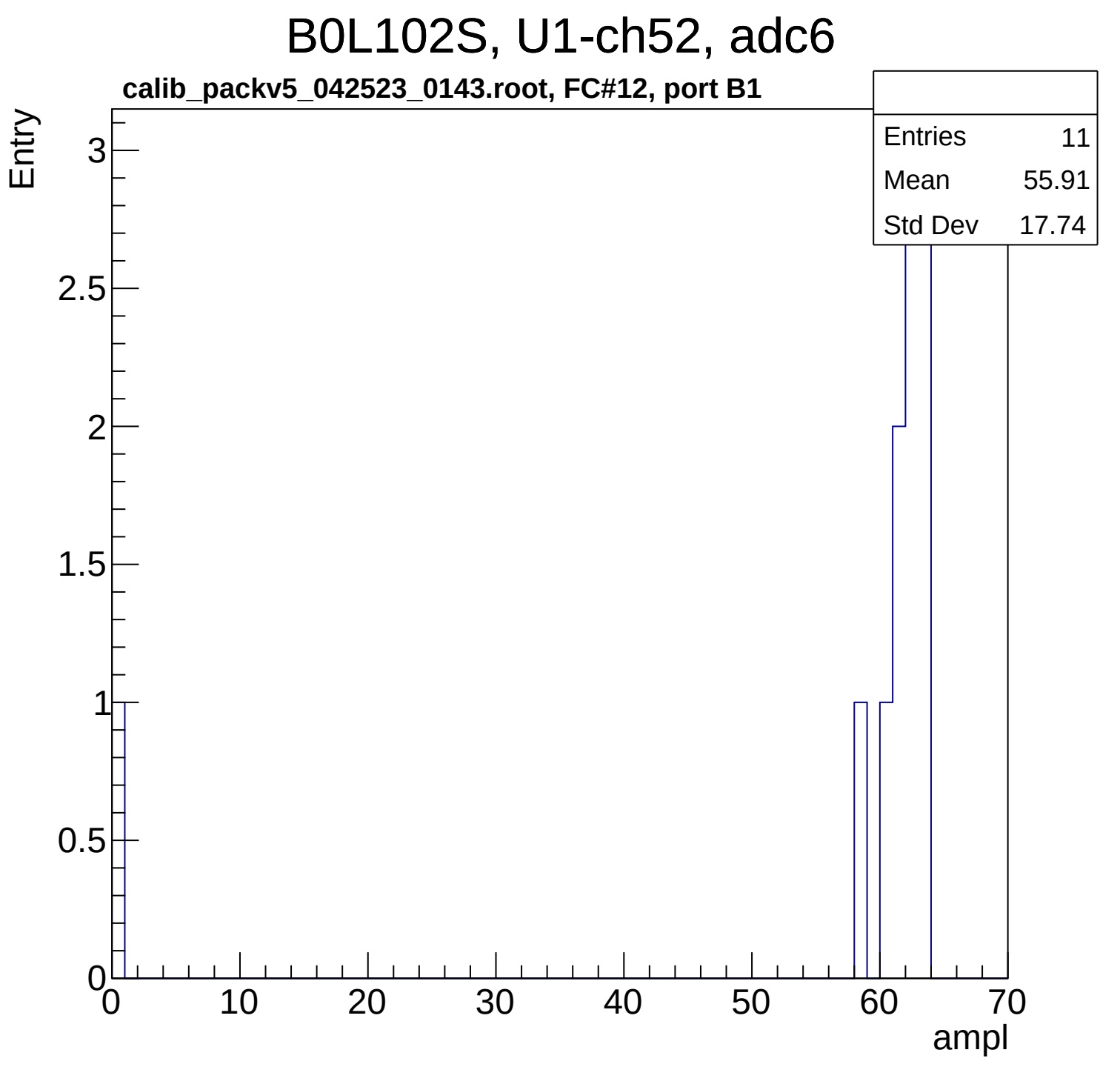
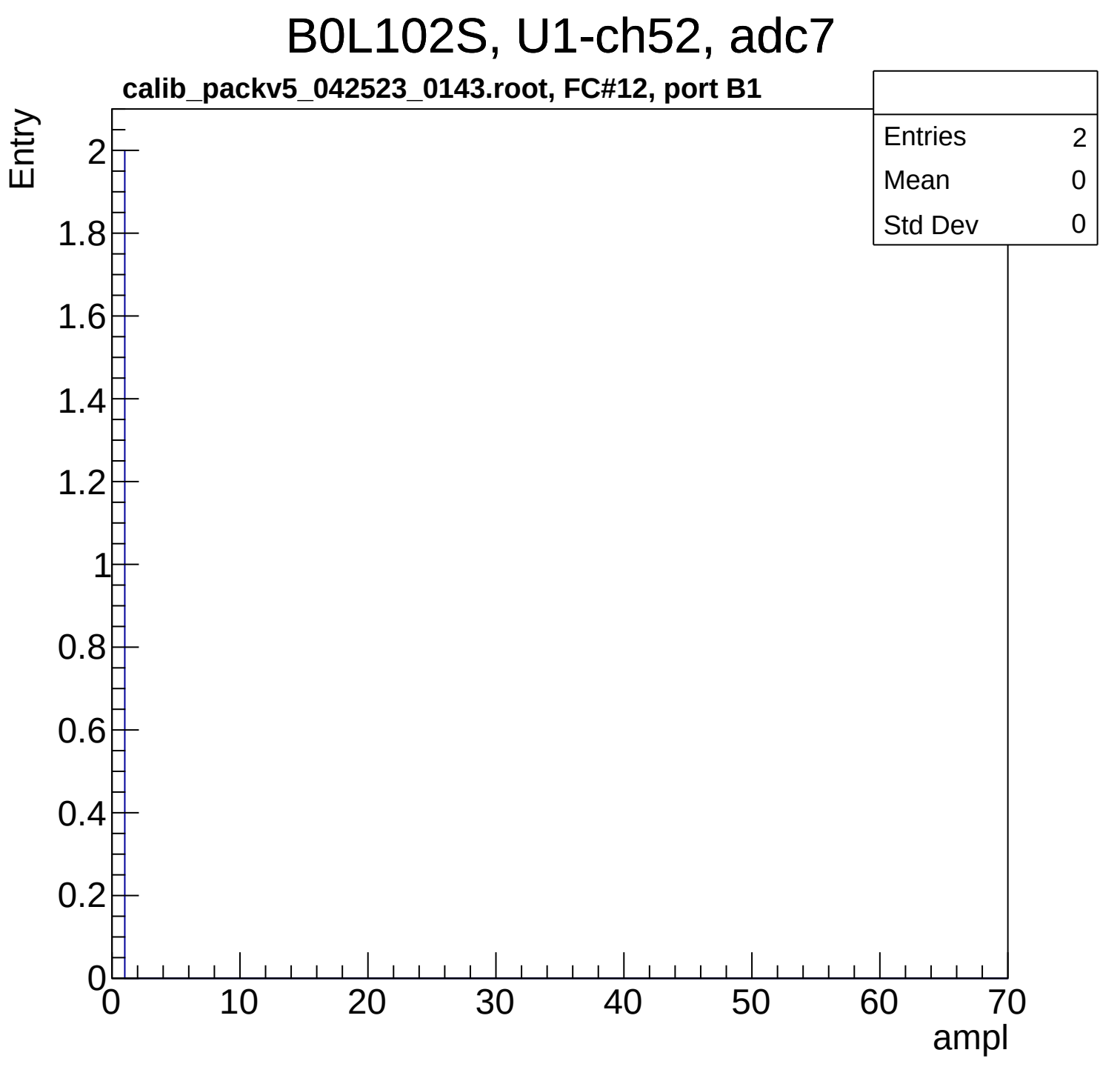
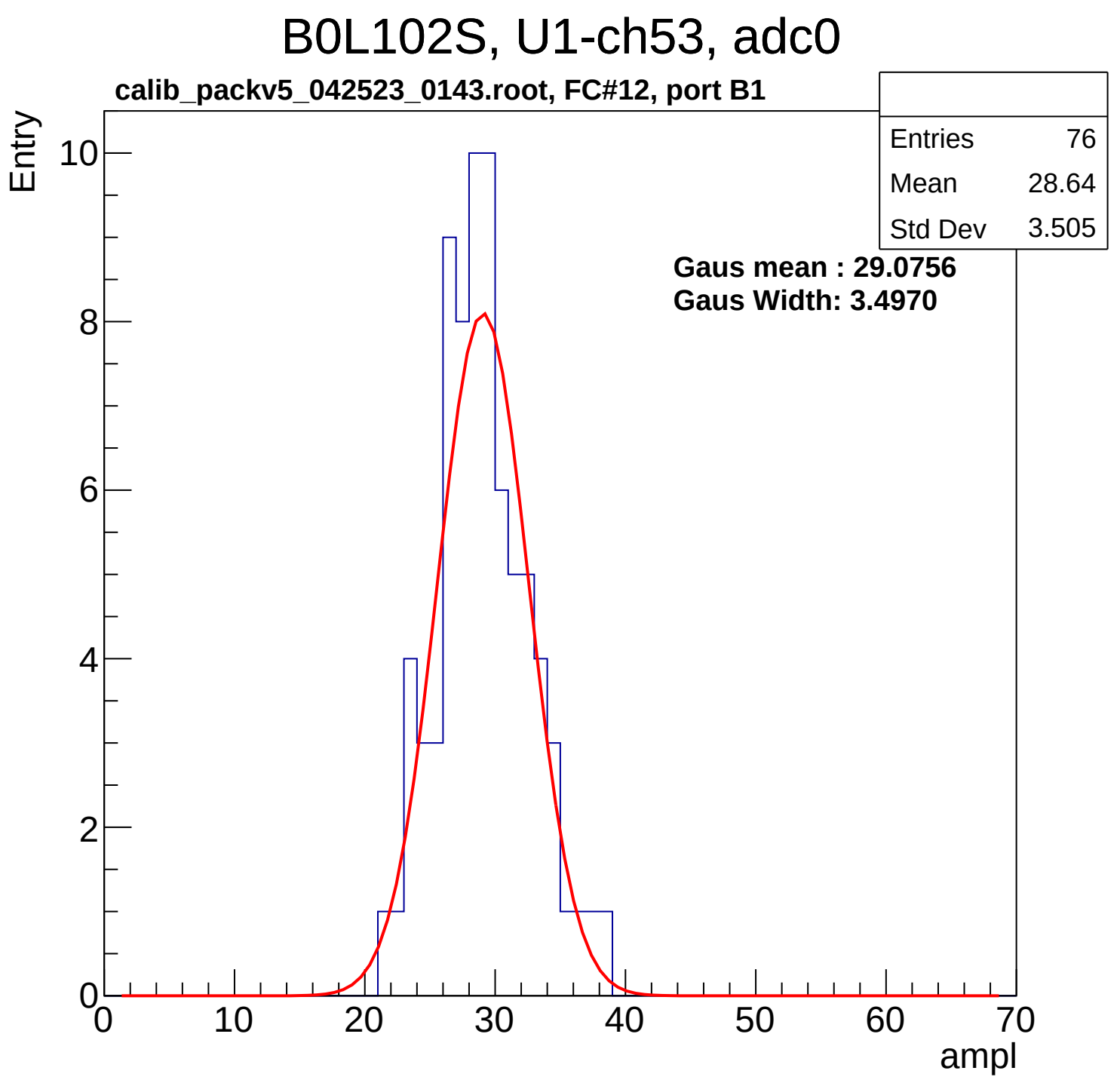
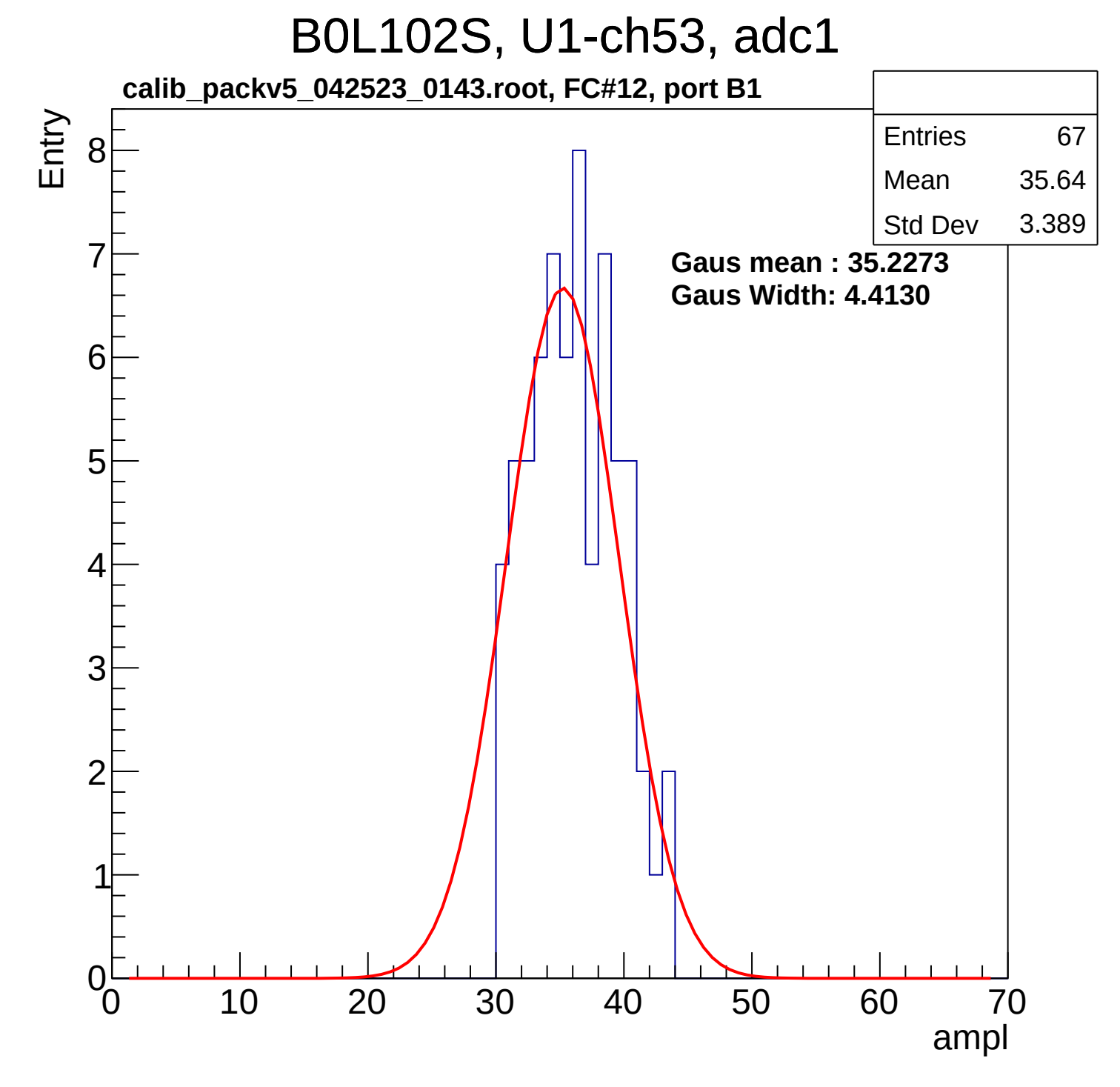
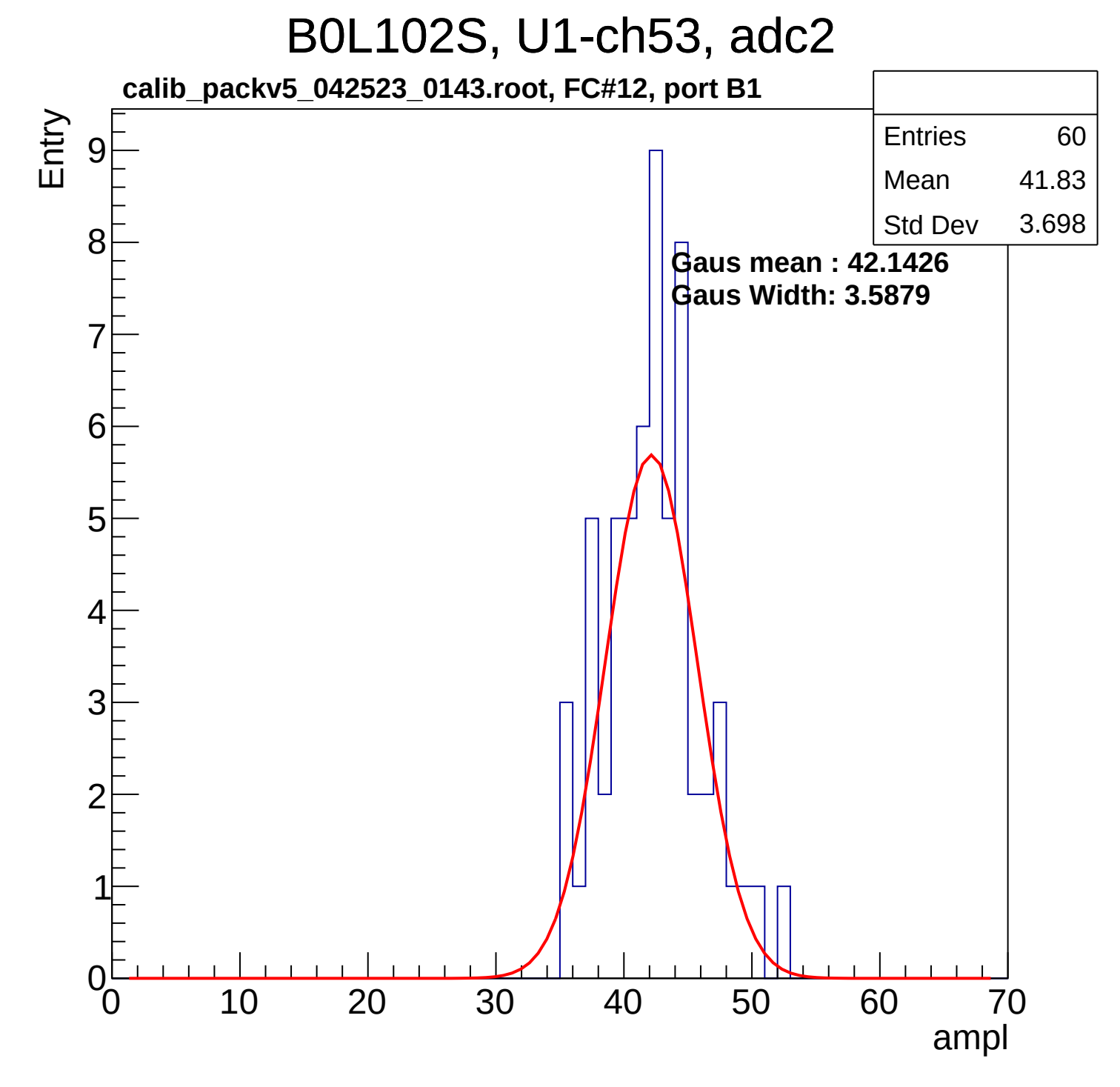
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

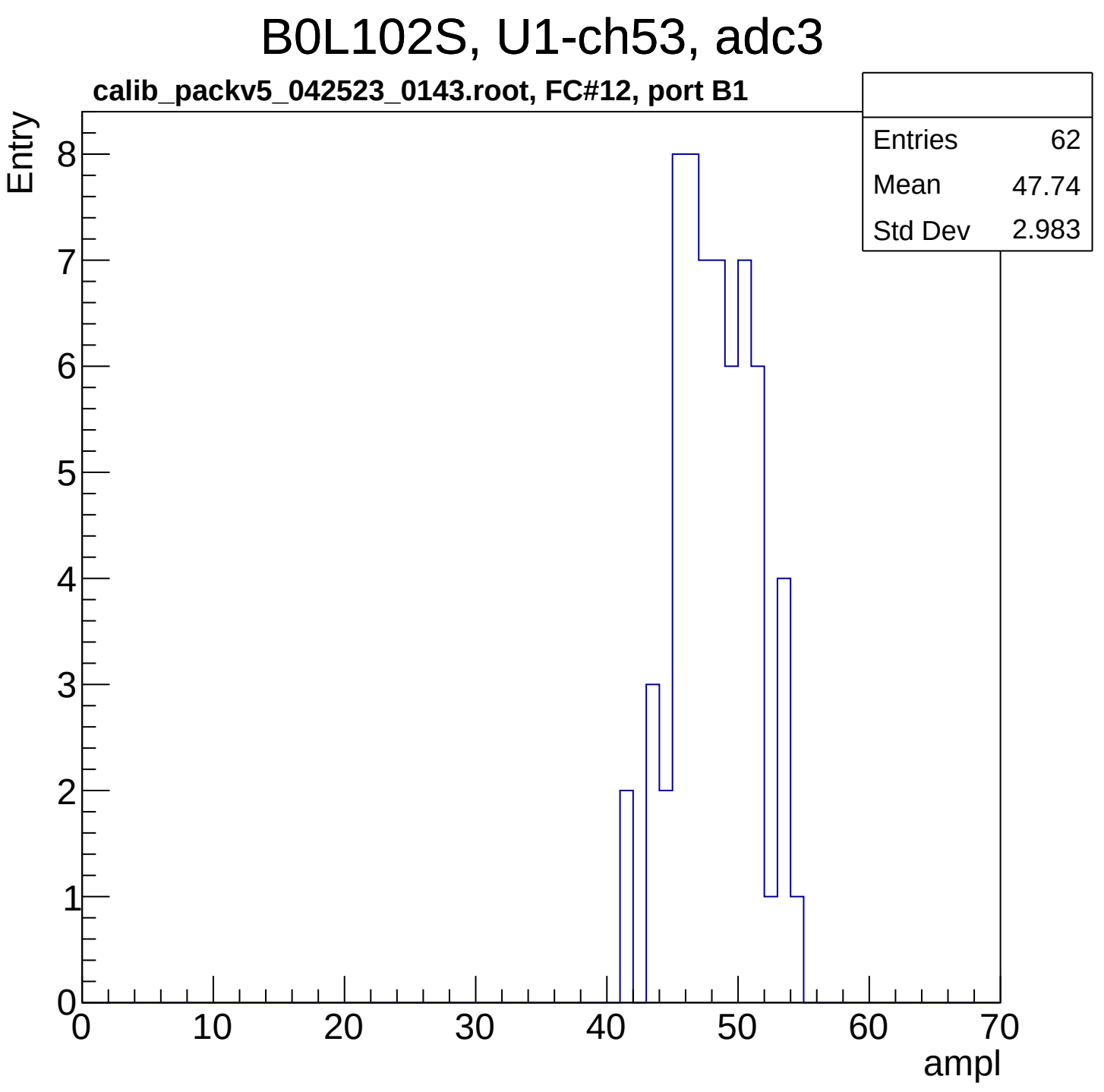

B0L102S, U1-ch52, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 55.91
Std Dev 17.74
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

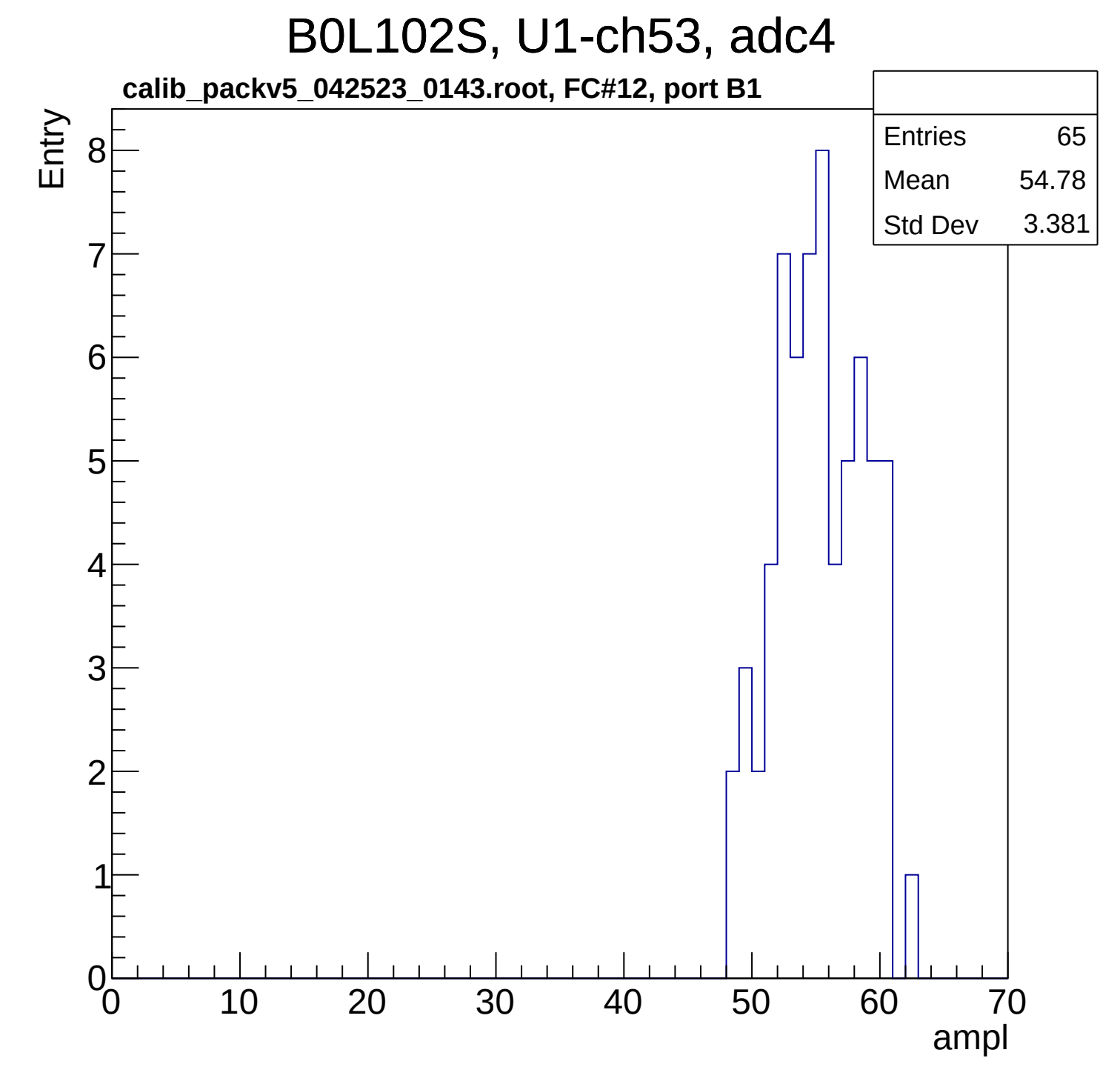

B0L102S, U1-ch52, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

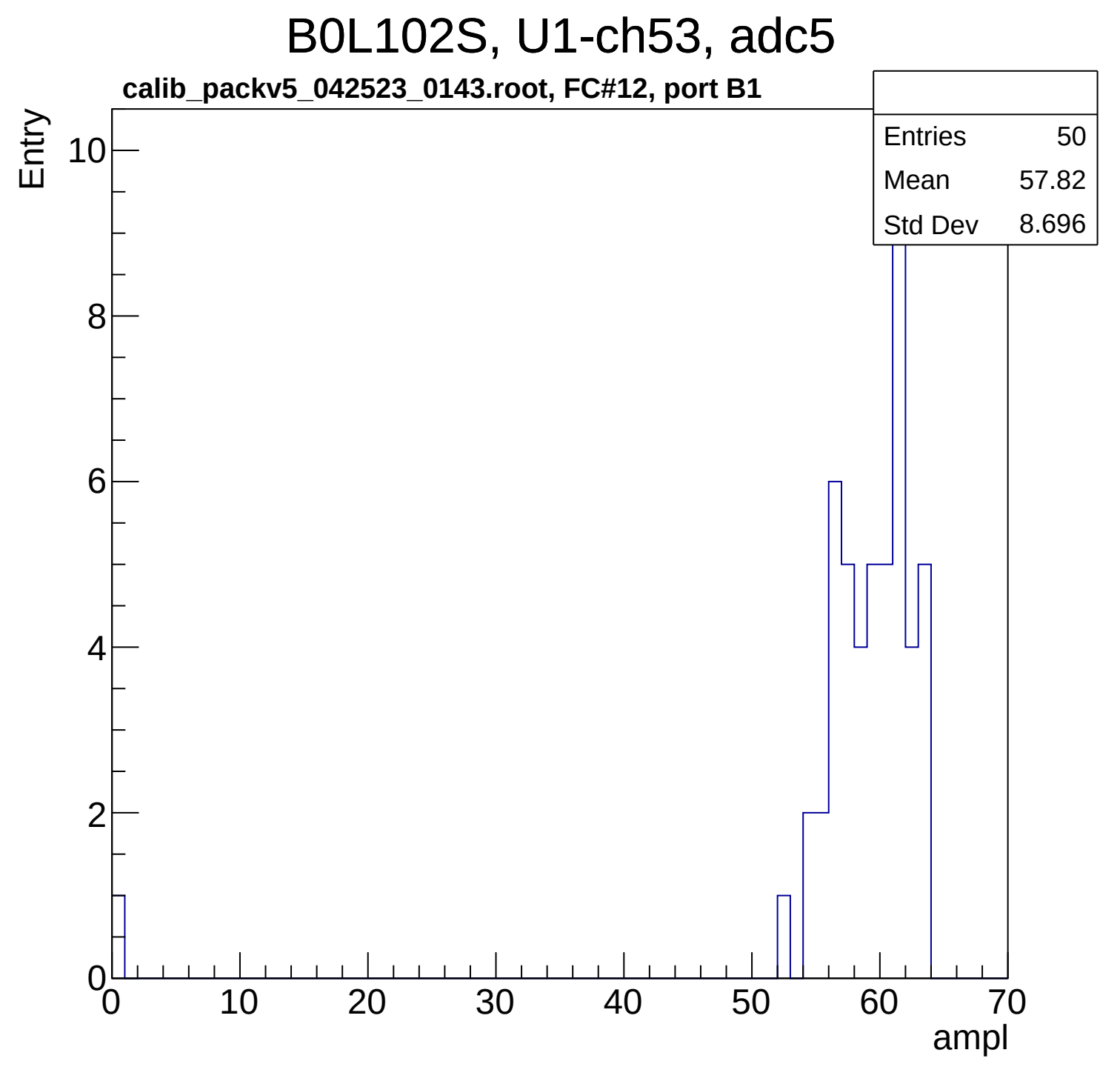

B0L102S, U1-ch53, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 28.64
Std Dev 3.505
Gaus mean : 29.0756
Gaus Width: 3.4970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

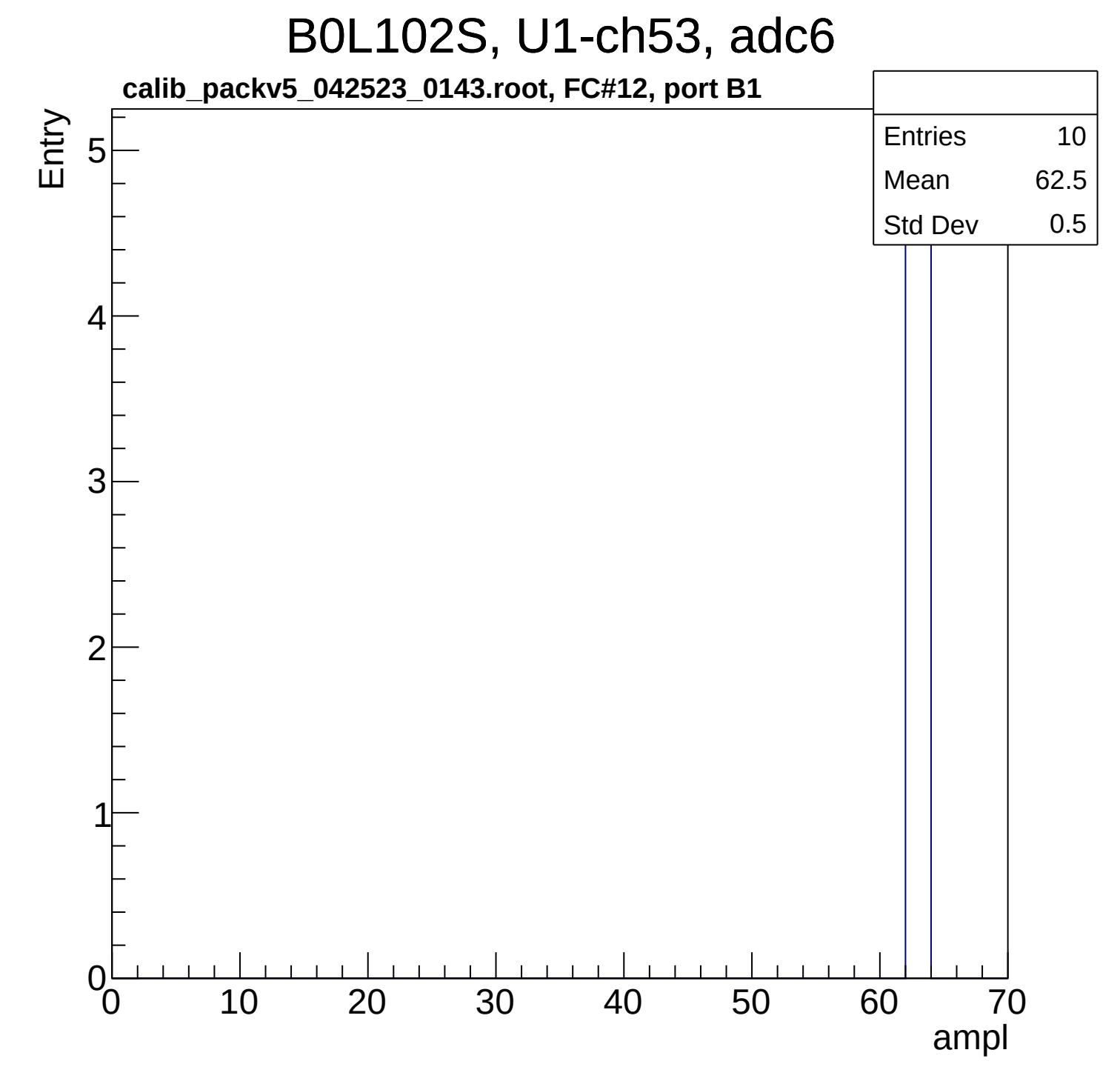

B0L102S, U1-ch53, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.64
Std Dev 3.389
Gaus mean : 35.2273
Gaus Width: 4.4130
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

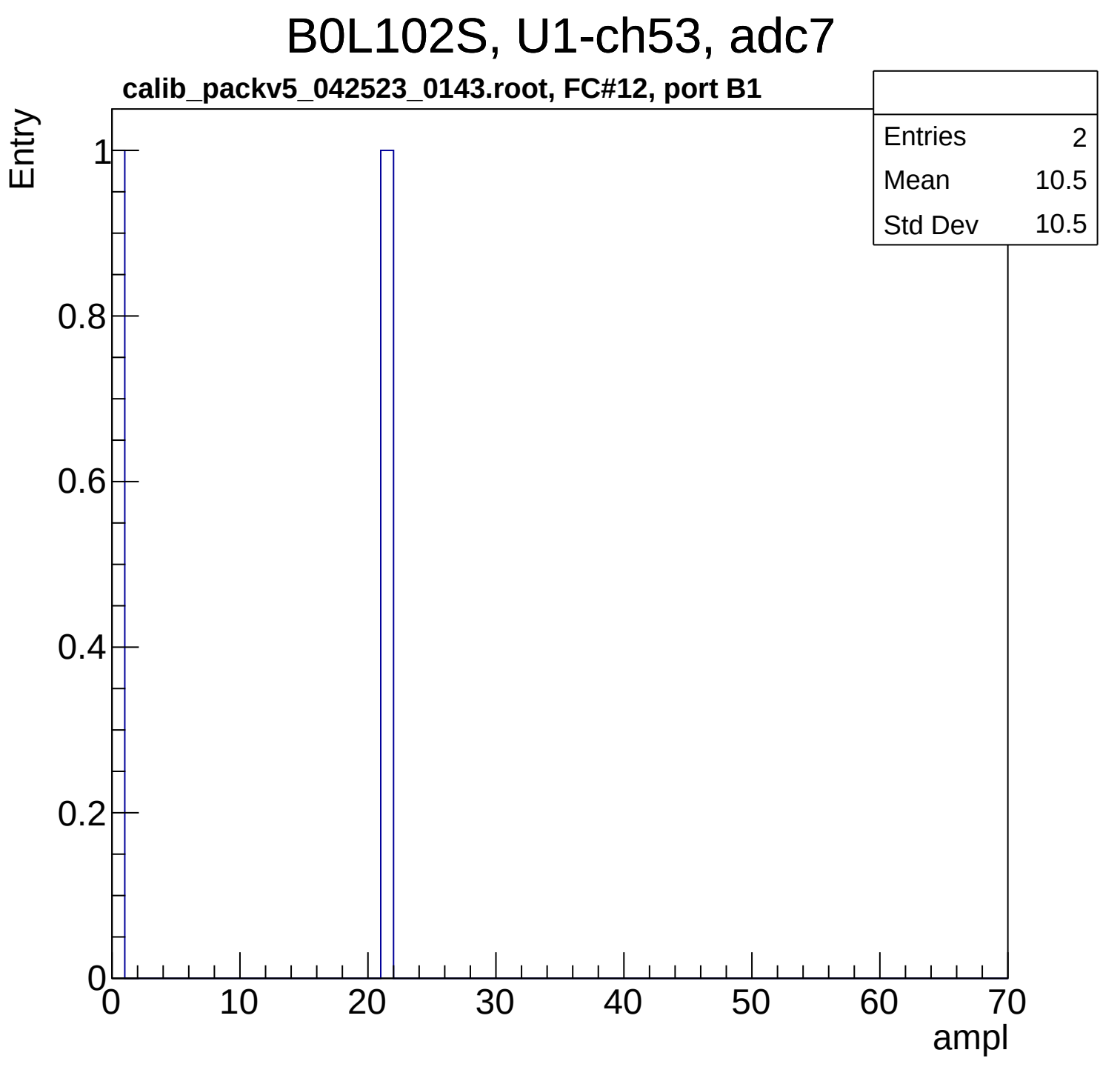

B0L102S, U1-ch53, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 41.83
Std Dev 3.698
Gaus mean : 42.1426
Gaus Width: 3.5879
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

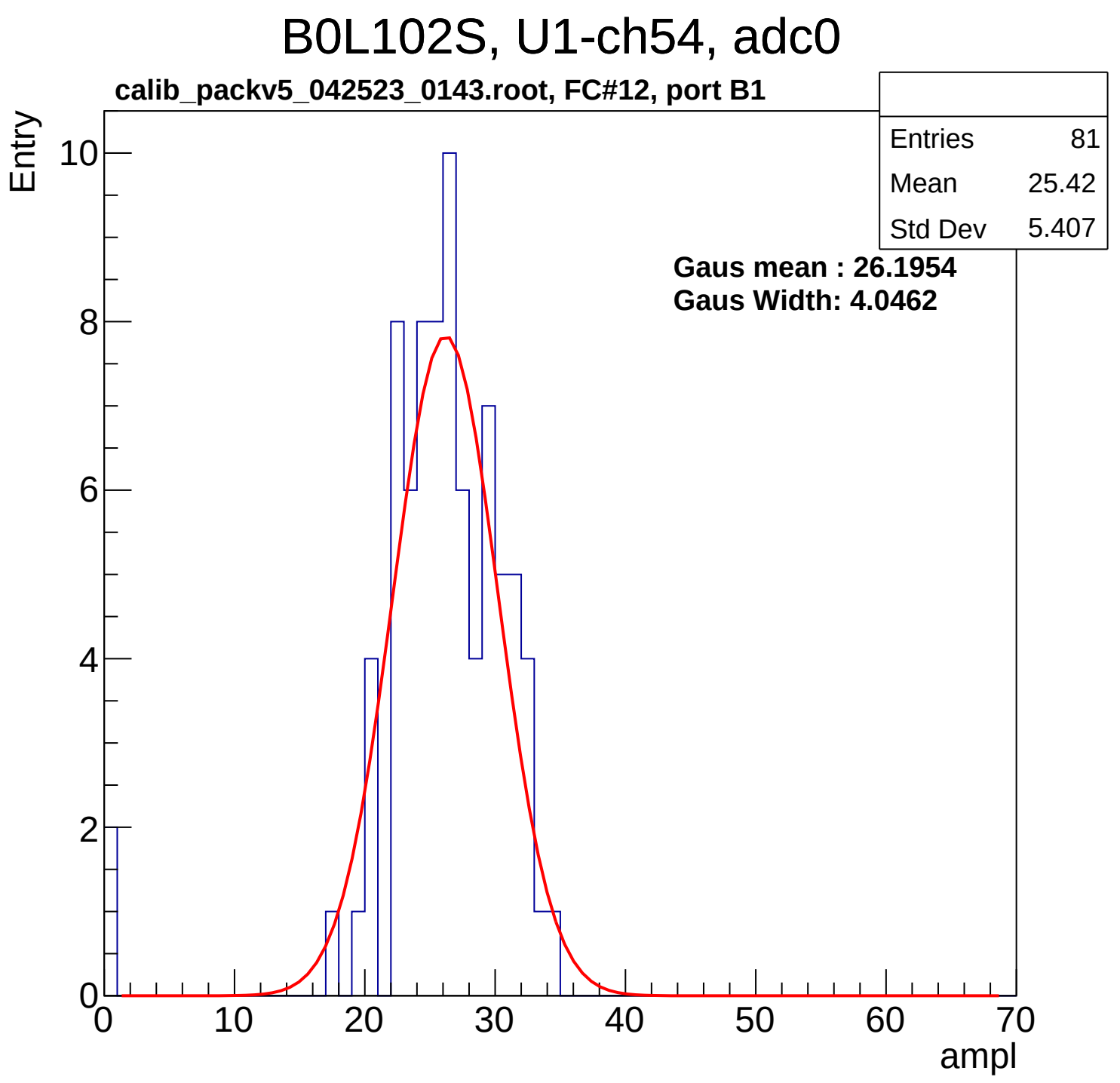

B0L102S, U1-ch53, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 47.74
Std Dev 2.983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

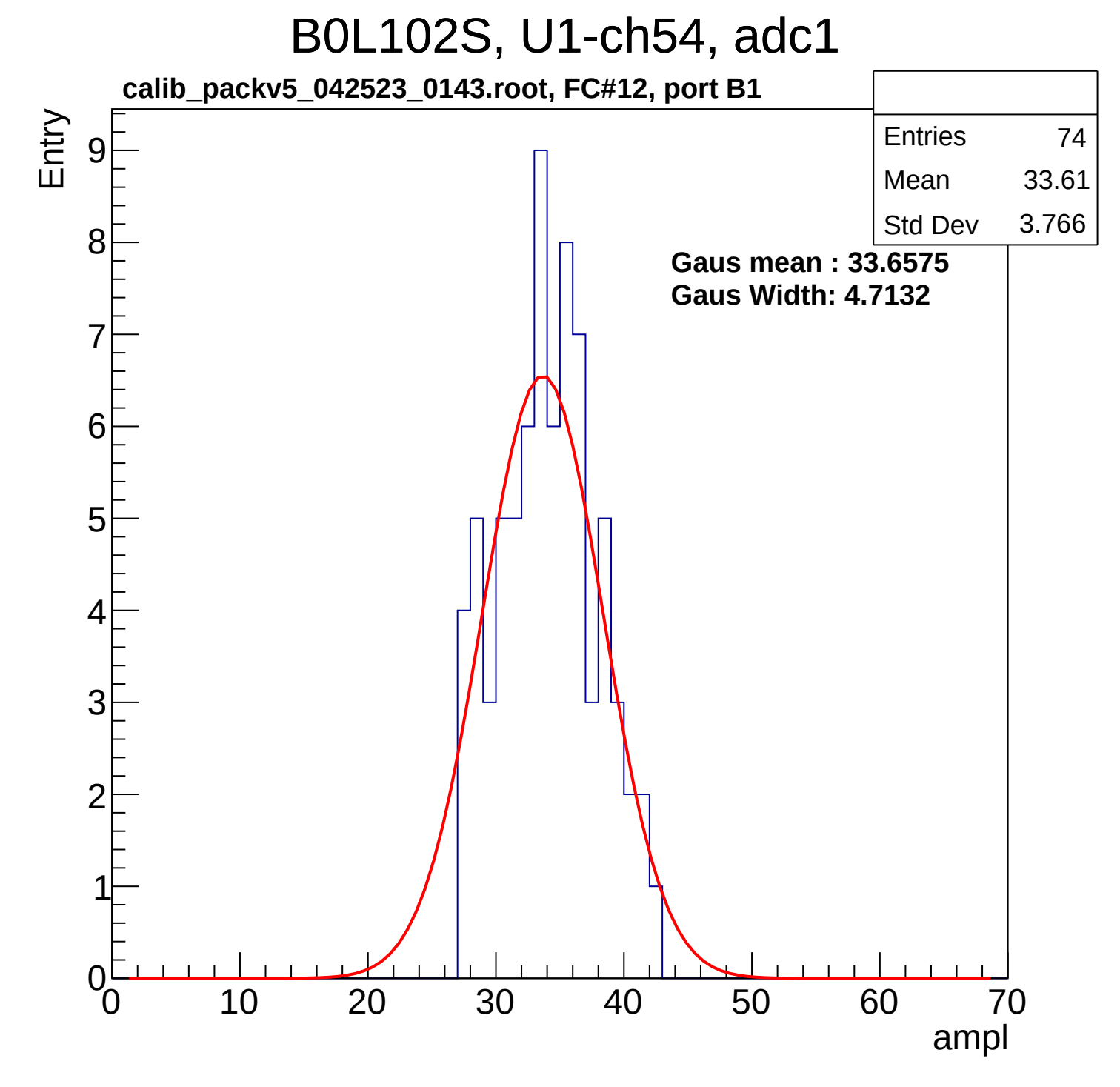

B0L102S, U1-ch53, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 54.78
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

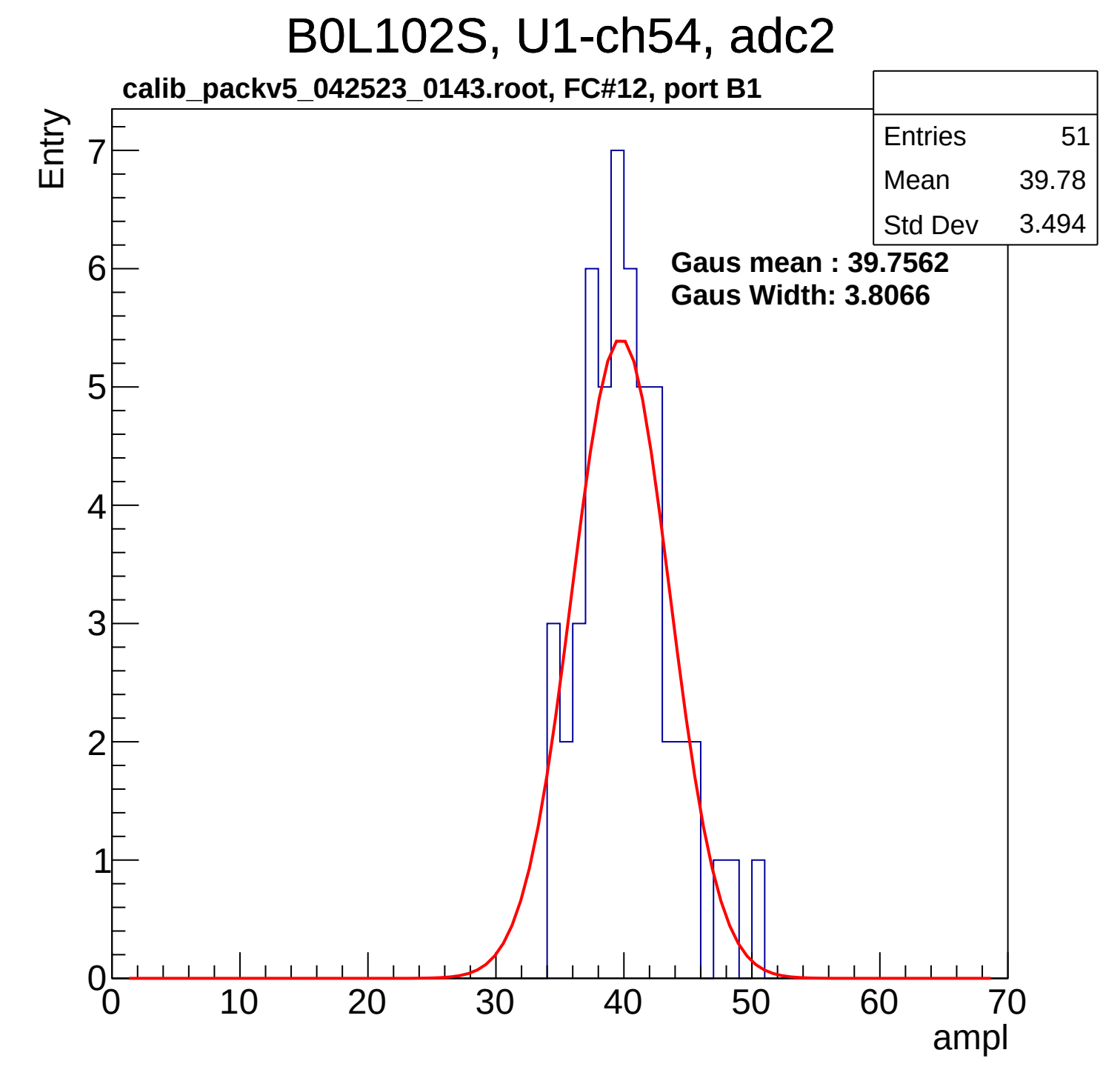

B0L102S, U1-ch53, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 57.82
Std Dev 8.696
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

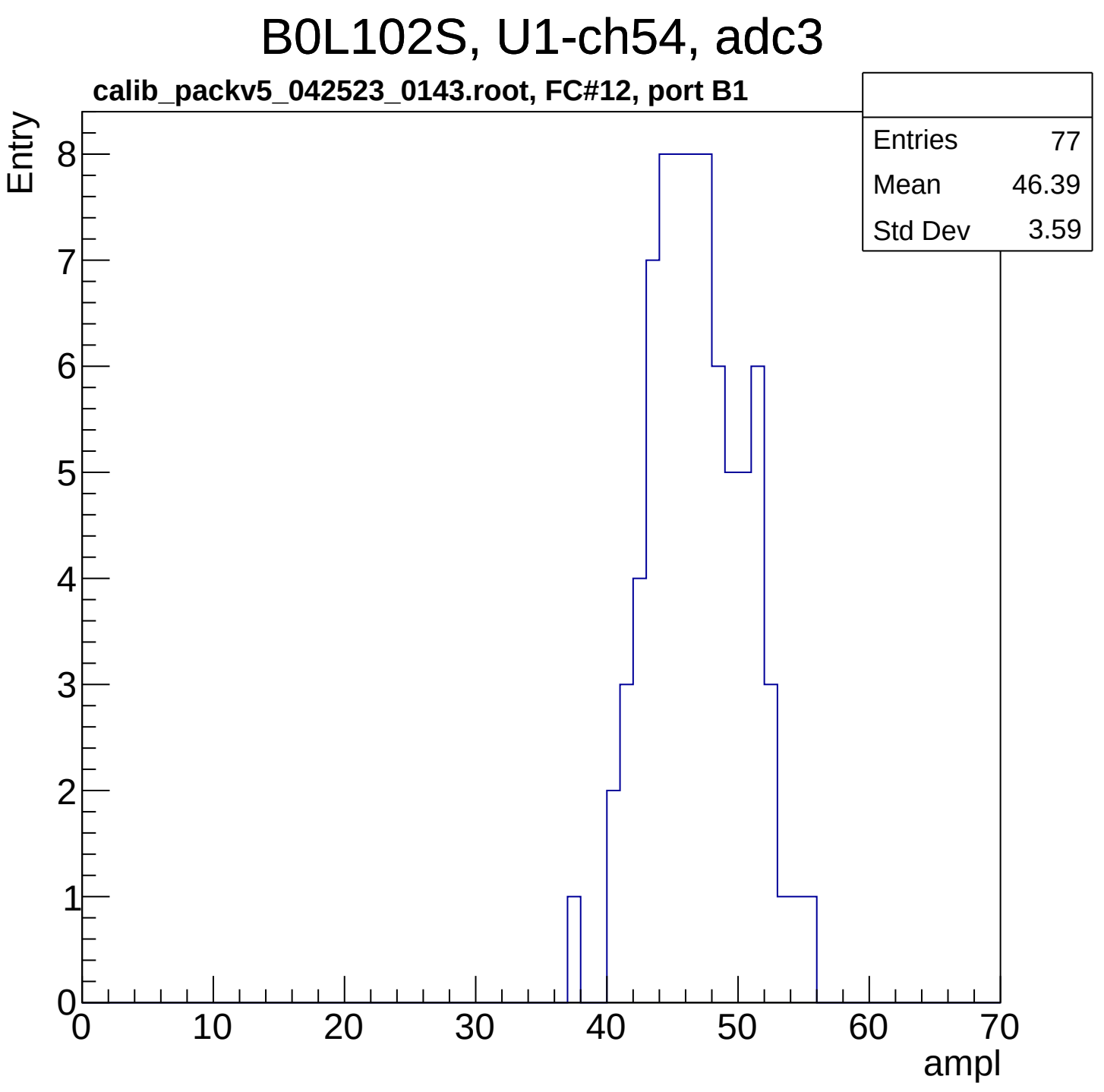

B0L102S, U1-ch53, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 62.5
Std Dev 0.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

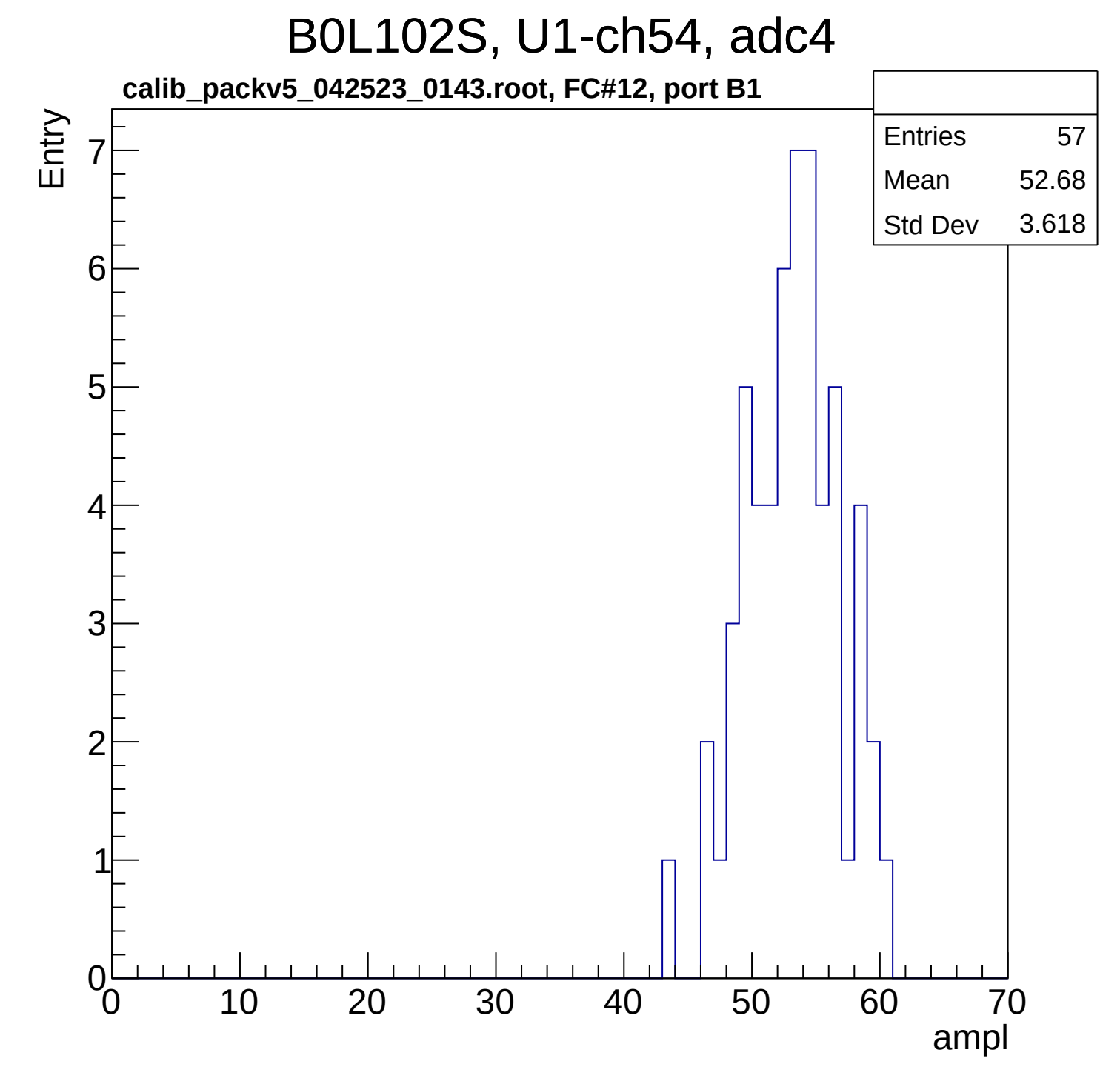

B0L102S, U1-ch53, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

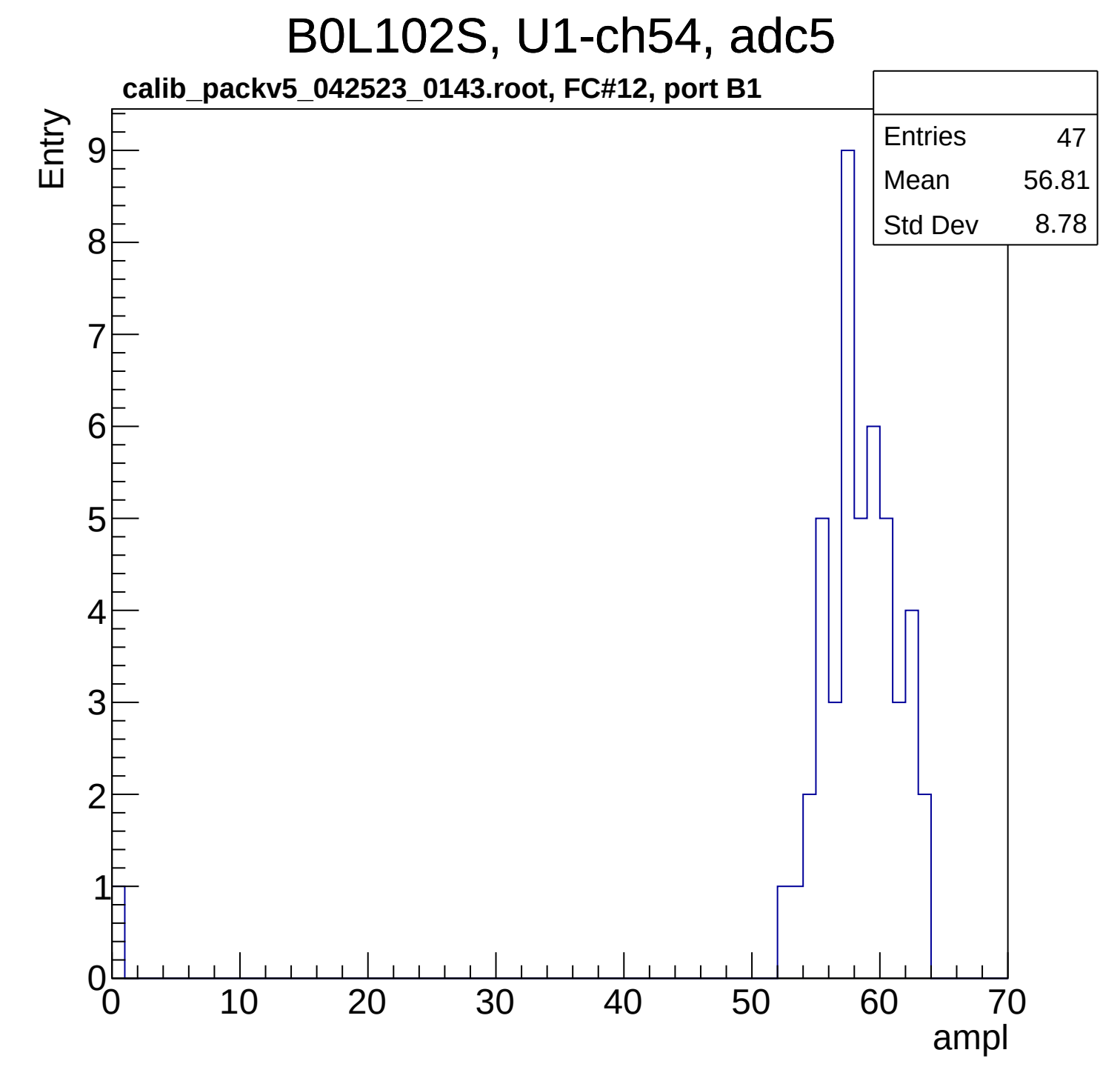

B0L102S, U1-ch54, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 25.42
Std Dev 5.407
Gaus mean : 26.1954
Gaus Width: 4.0462
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

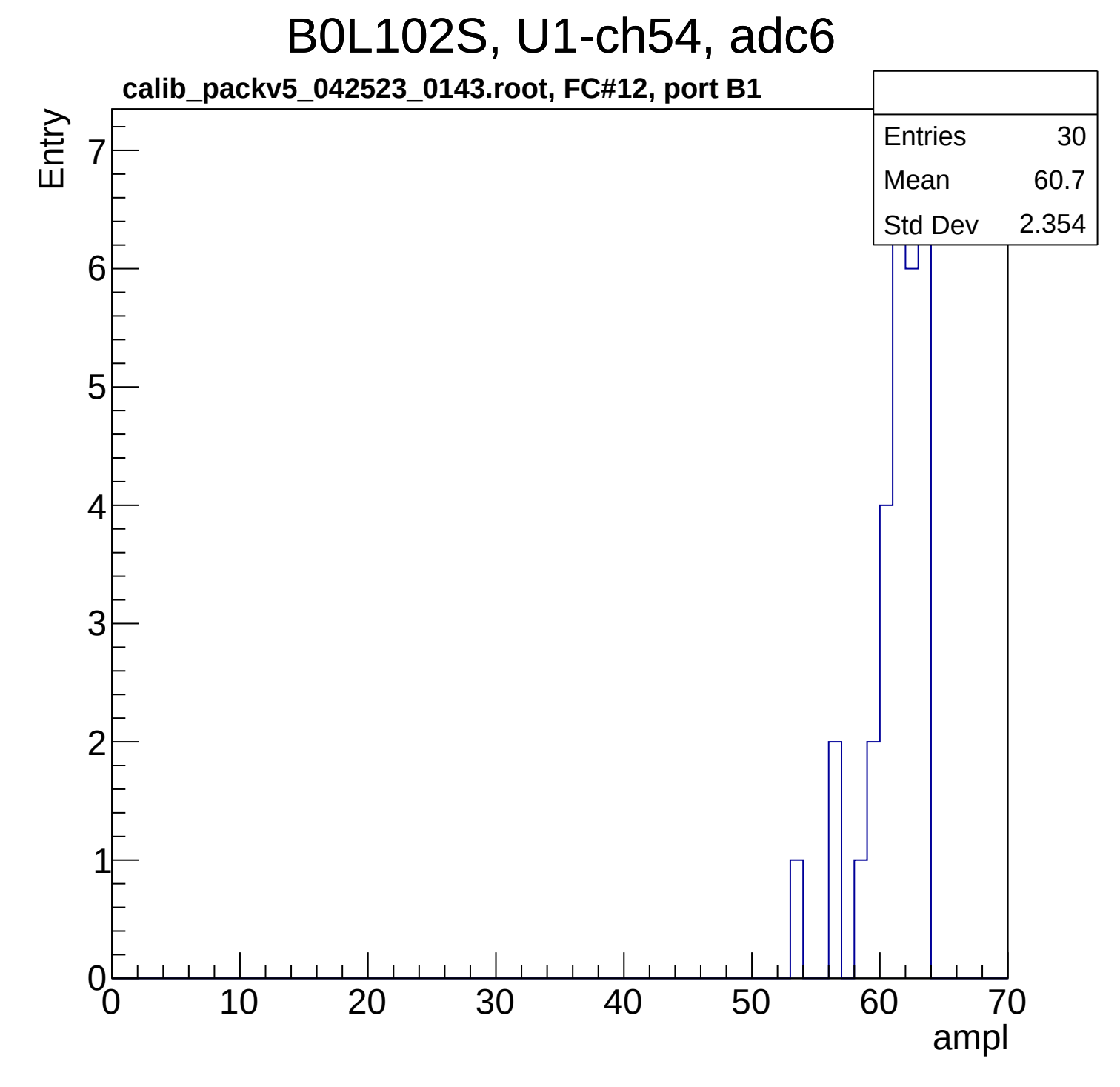

B0L102S, U1-ch54, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 33.61
Std Dev 3.766
Gaus mean : 33.6575
Gaus Width: 4.7132
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

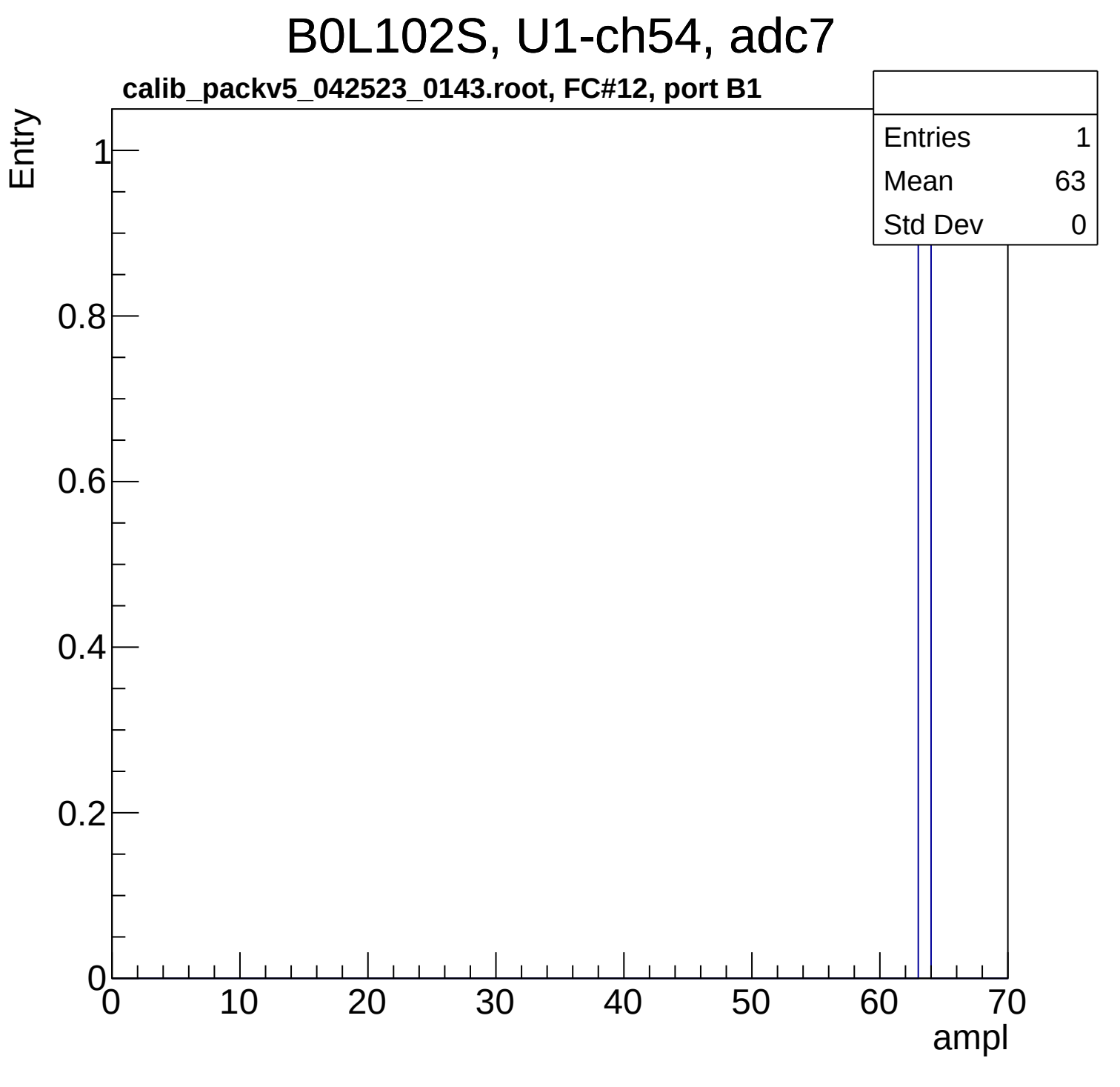

B0L102S, U1-ch54, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 39.78
Std Dev 3.494
Gaus mean : 39.7562
Gaus Width: 3.8066
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

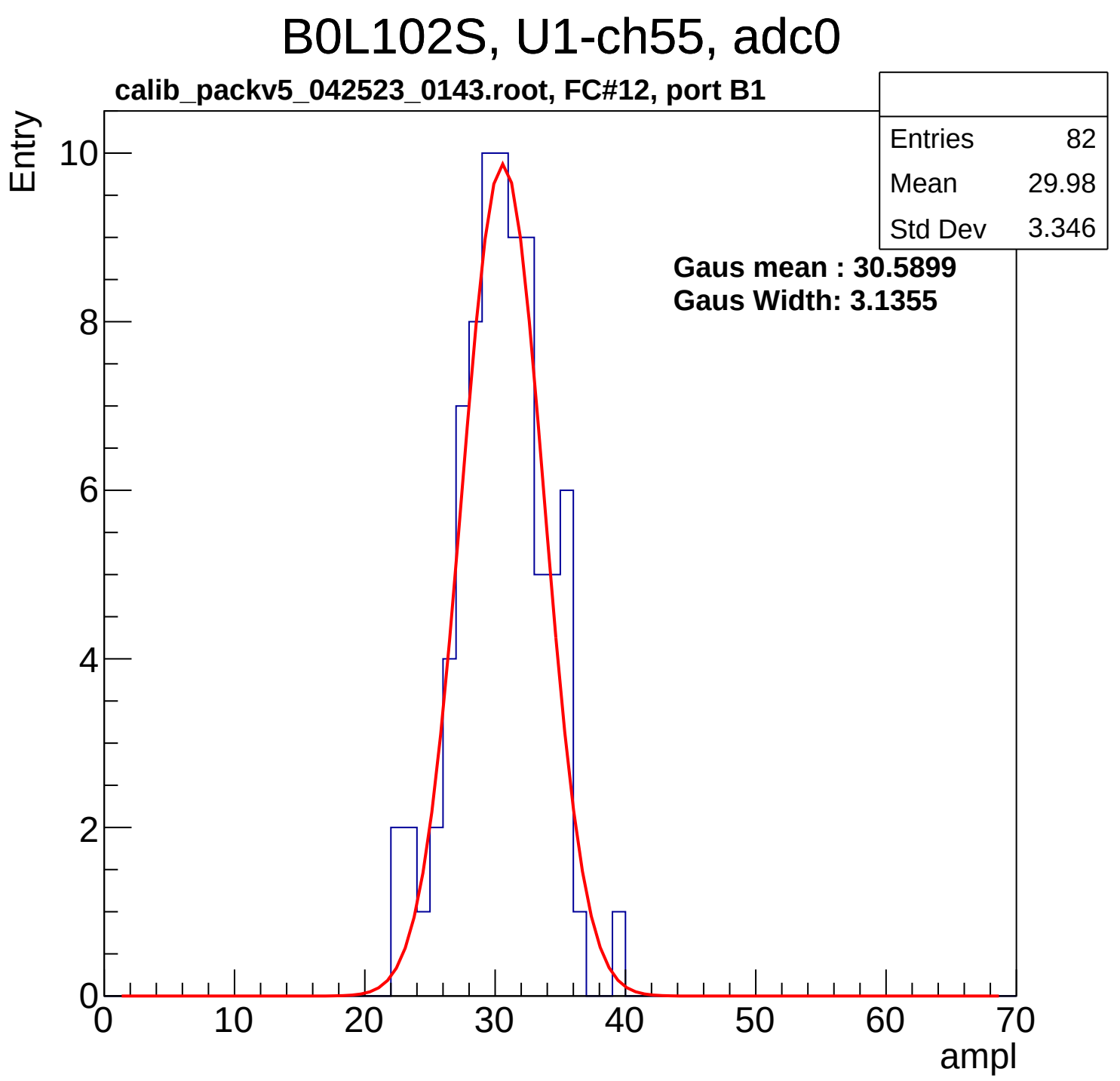

B0L102S, U1-ch54, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 46.39
Std Dev 3.59
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

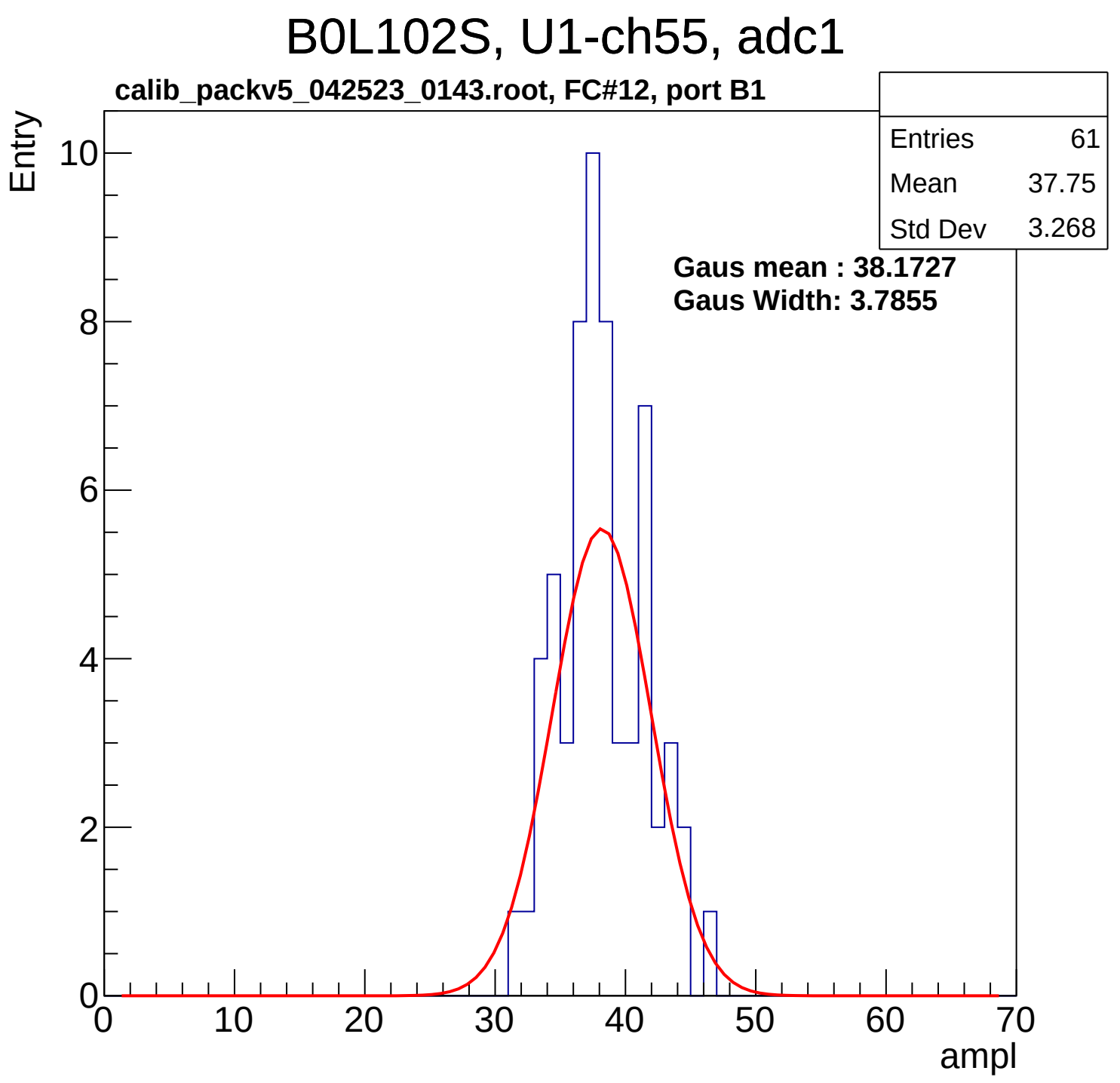

B0L102S, U1-ch54, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 52.68
Std Dev 3.618
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

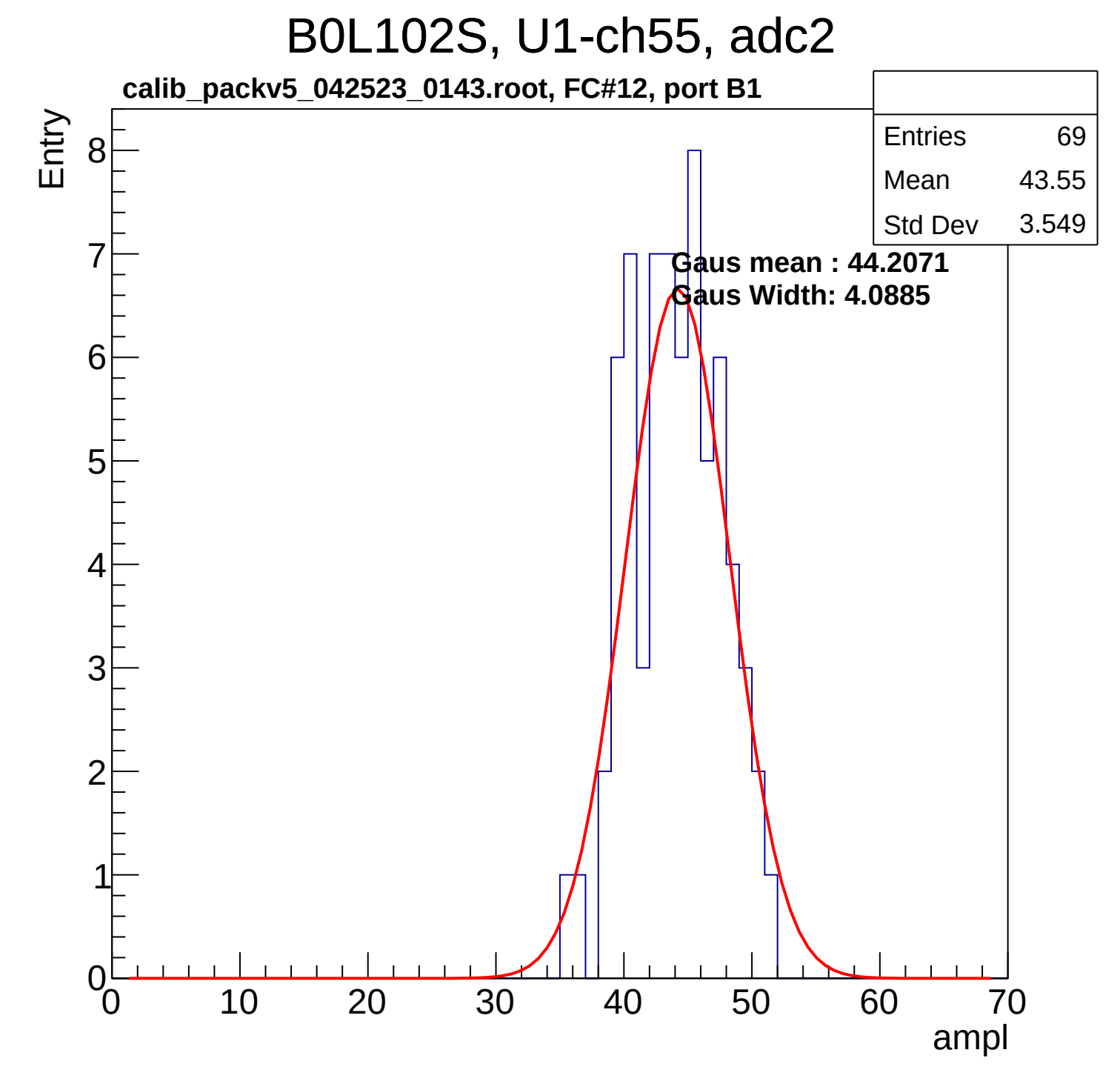

B0L102S, U1-ch54, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 56.81
Std Dev 8.78
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

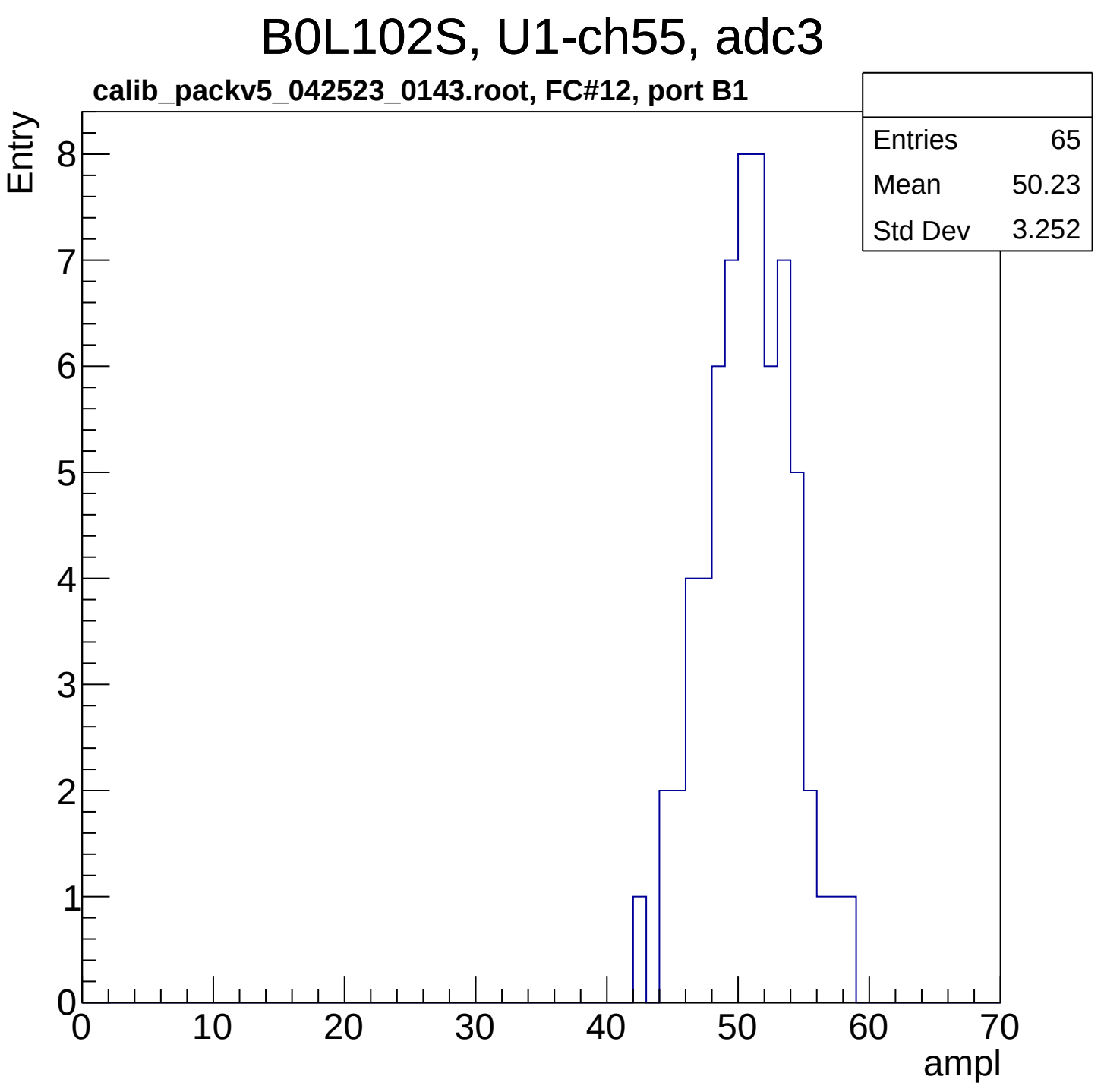

B0L102S, U1-ch54, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 60.7
Std Dev 2.354
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch54, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch55, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 29.98
Std Dev 3.346
Gaus mean : 30.5899
Gaus Width: 3.1355
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch55, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 37.75
Std Dev 3.268
Gaus mean : 38.1727
Gaus Width: 3.7855
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch55, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 43.55
Std Dev 3.549
Gaus mean : 44.2071
Gaus Width: 4.0885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch55, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 50.23
Std Dev 3.252
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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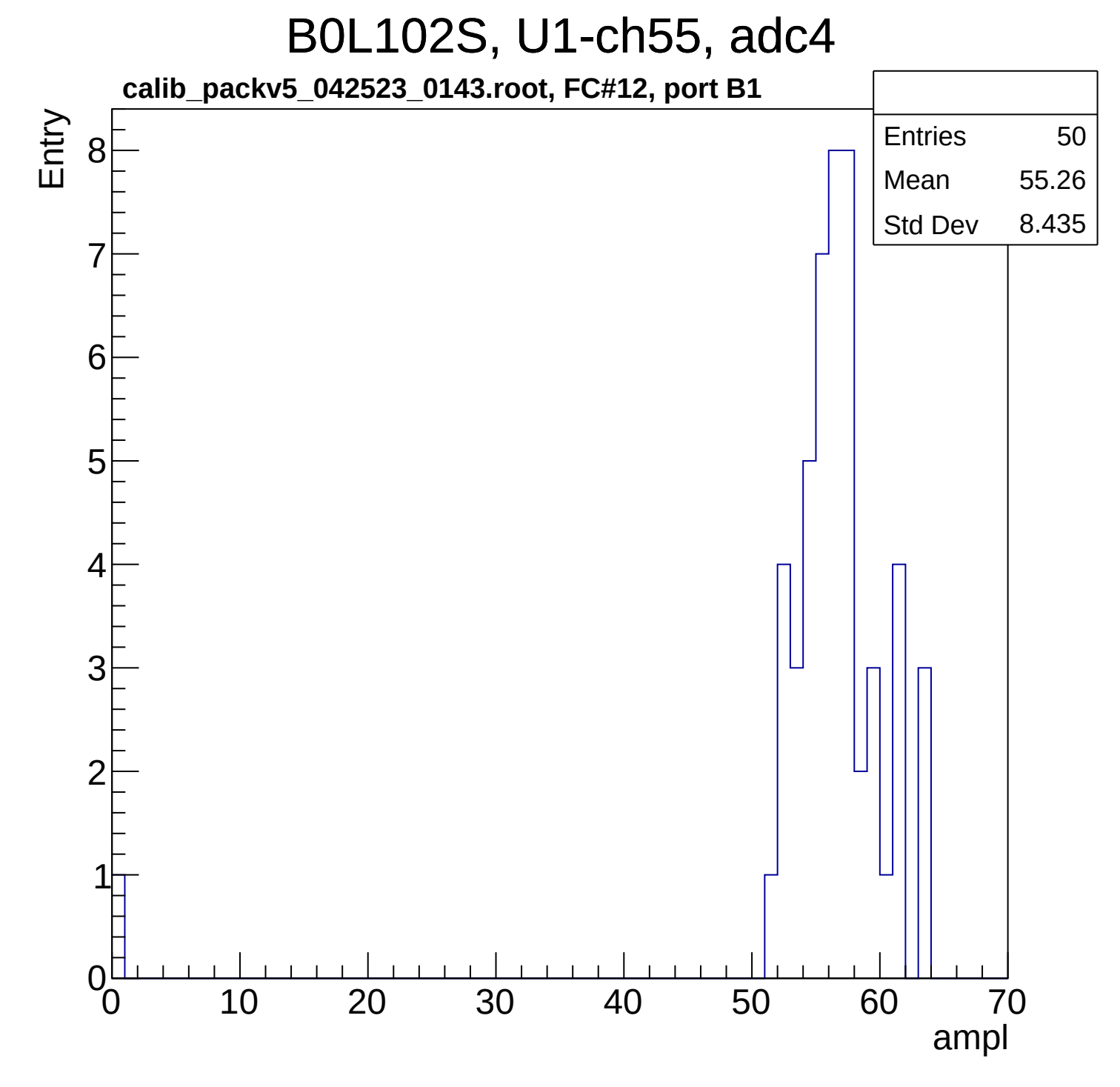
40
50
60
70


B0L102S, U1-ch55, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 55.26
Std Dev 8.435
Entry
ampl
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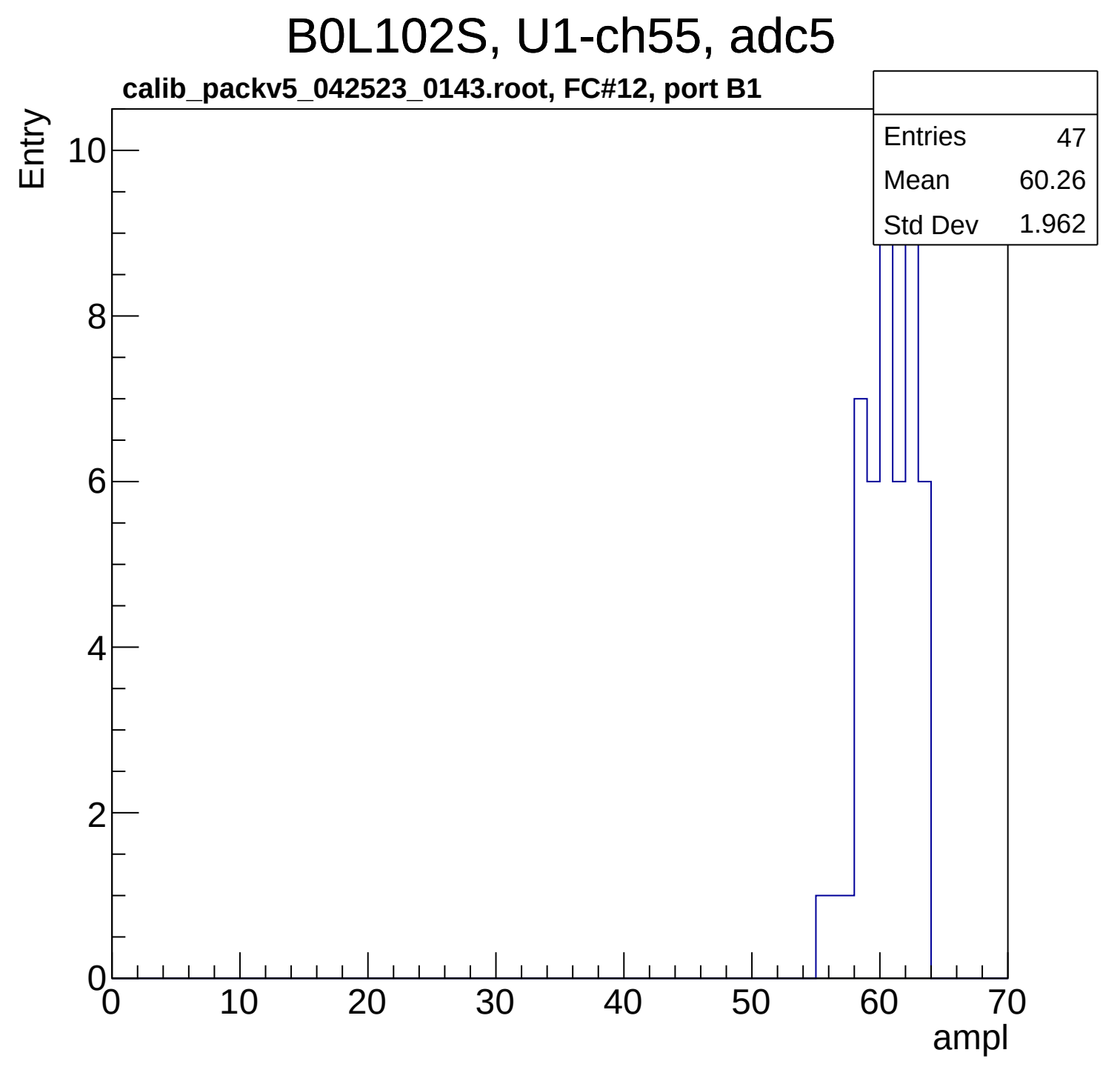
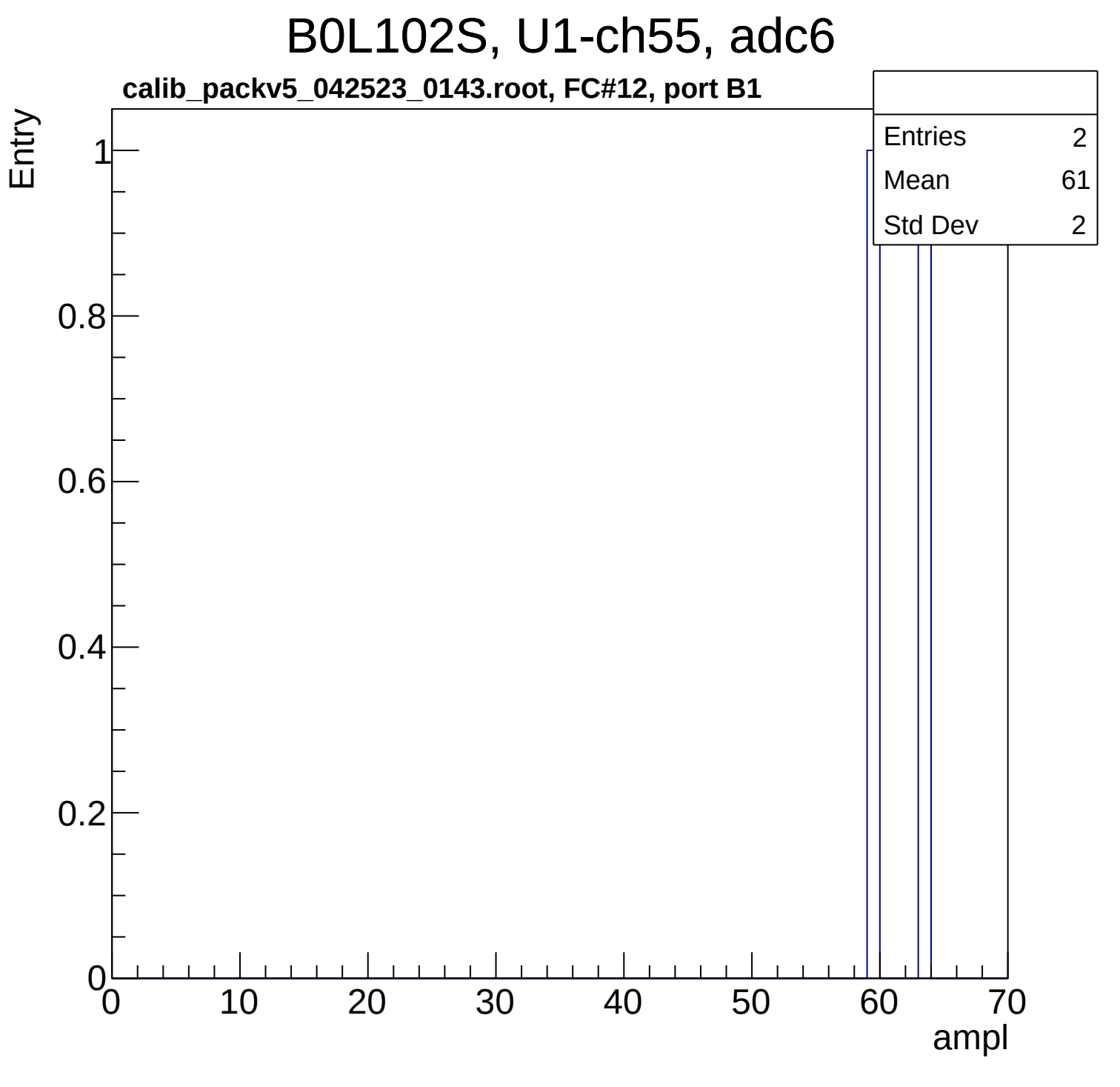
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch55, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 60.26
Std Dev 1.962
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch55, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61
Std Dev 2
Entry
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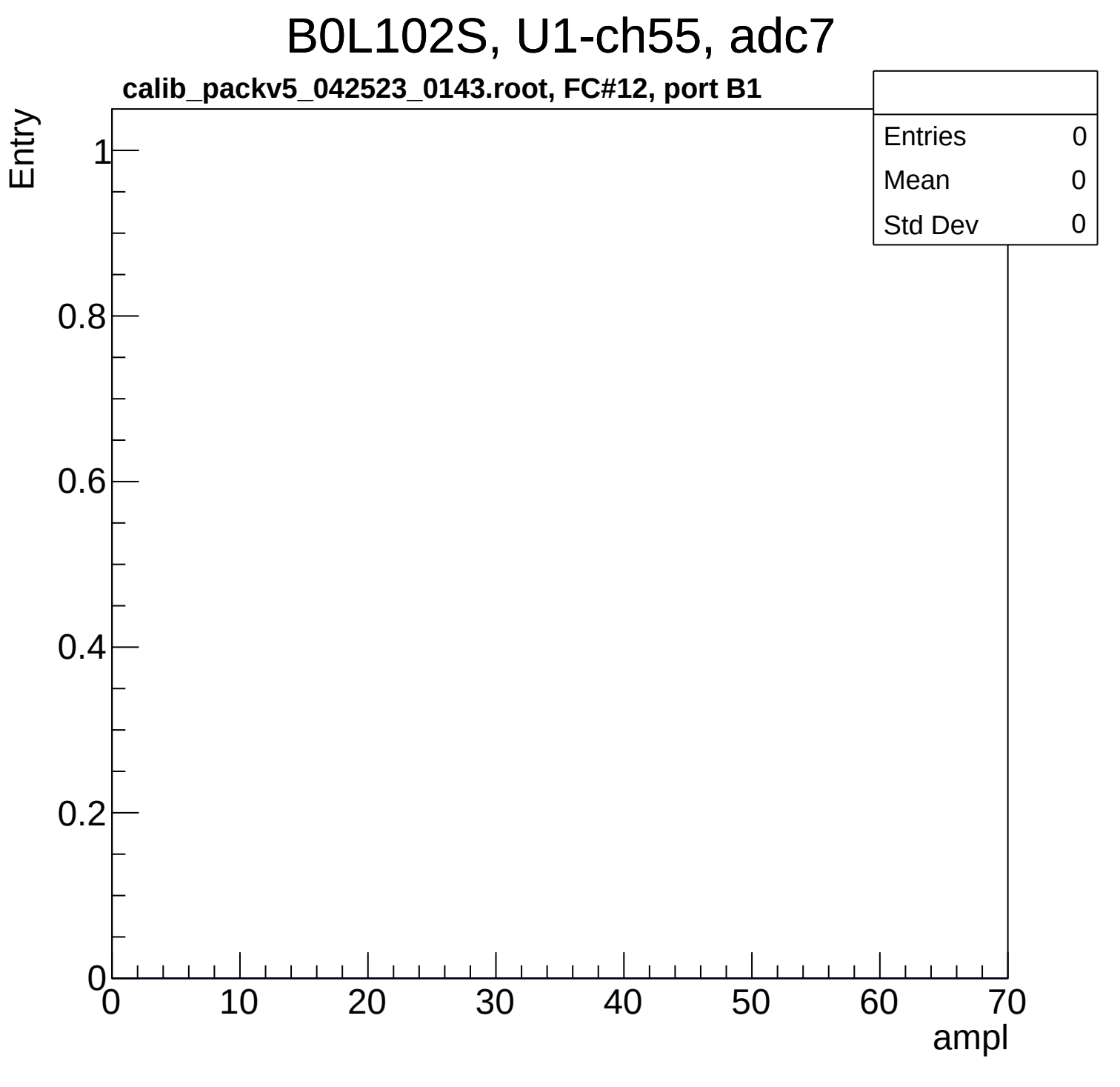
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch55, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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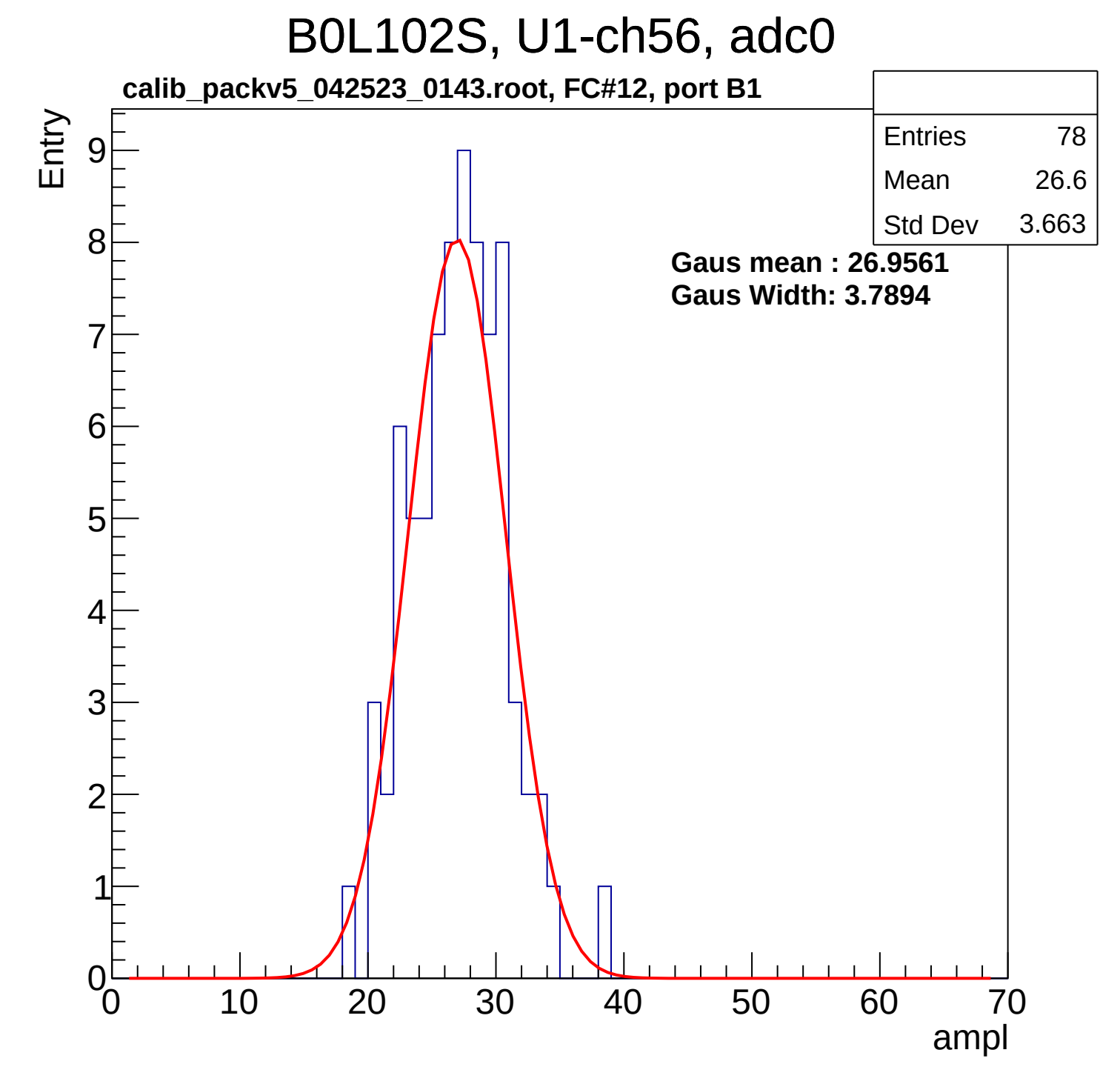
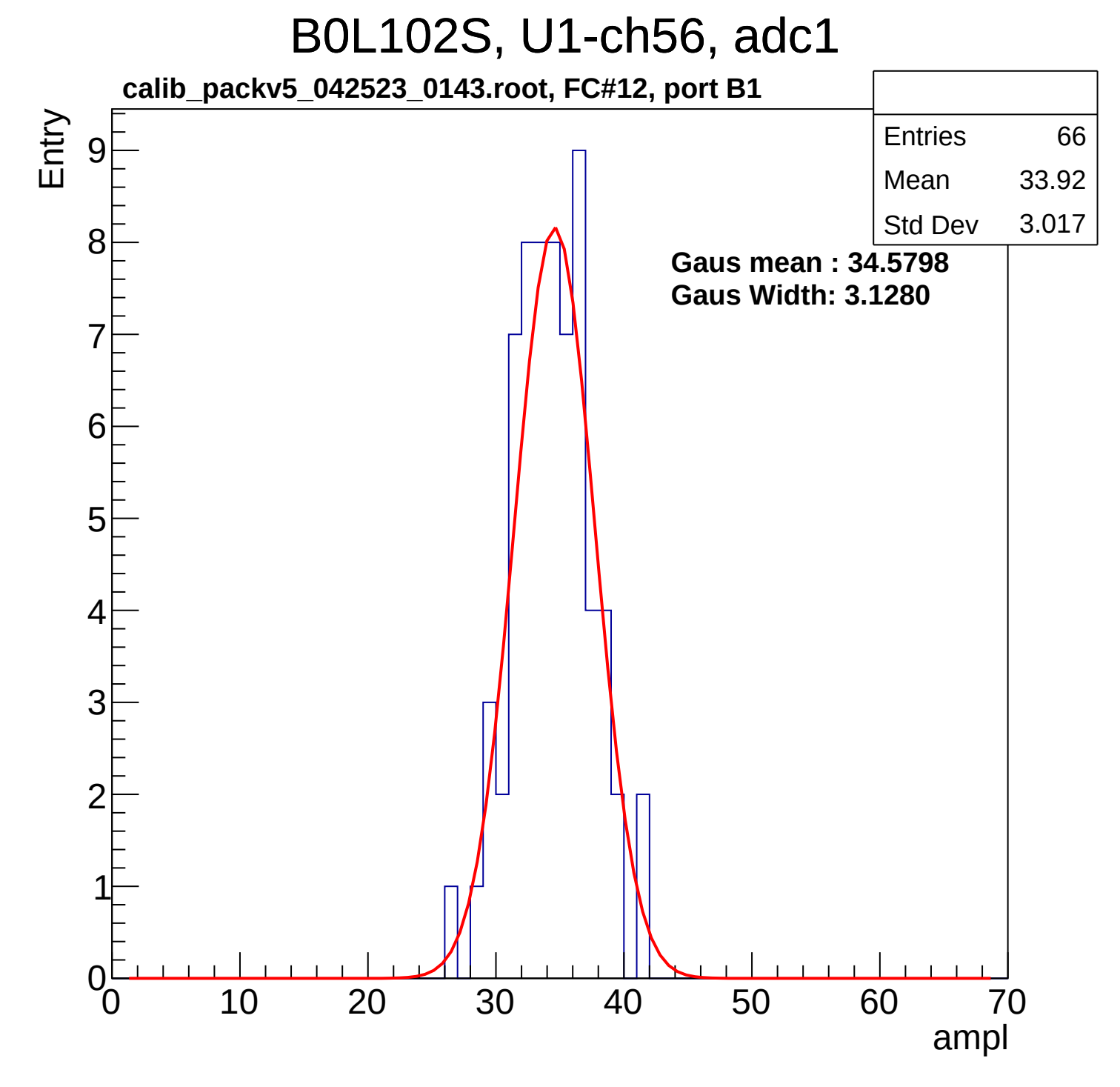
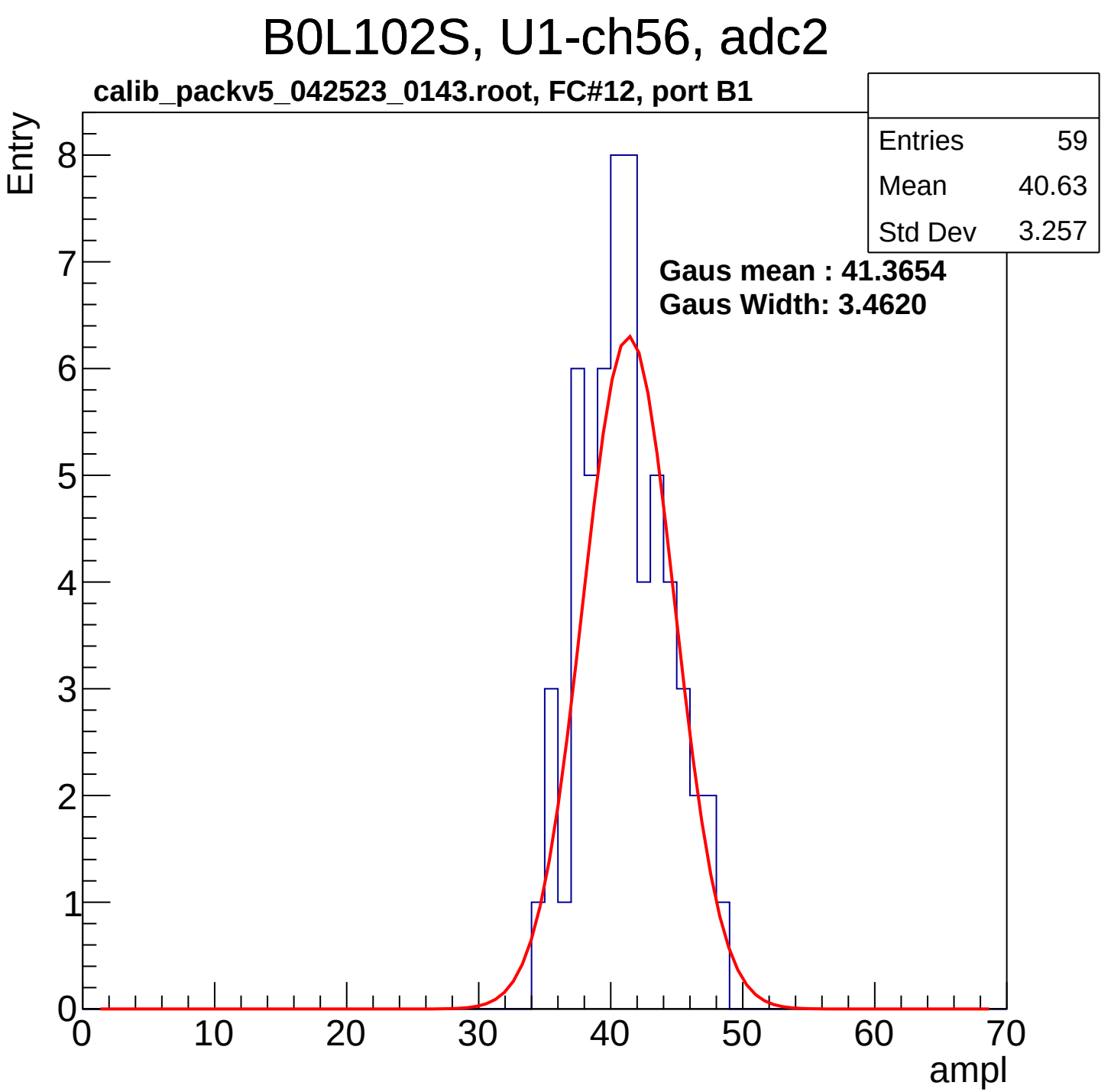
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch56, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 26.6
Std Dev 3.663
Gaus mean : 26.9561
Gaus Width: 3.7894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch56, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 33.92
Std Dev 3.017
Gaus mean : 34.5798
Gaus Width: 3.1280
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch56, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 40.63
Std Dev 3.257
Gaus mean : 41.3654
Gaus Width: 3.4620
Entry
ampl
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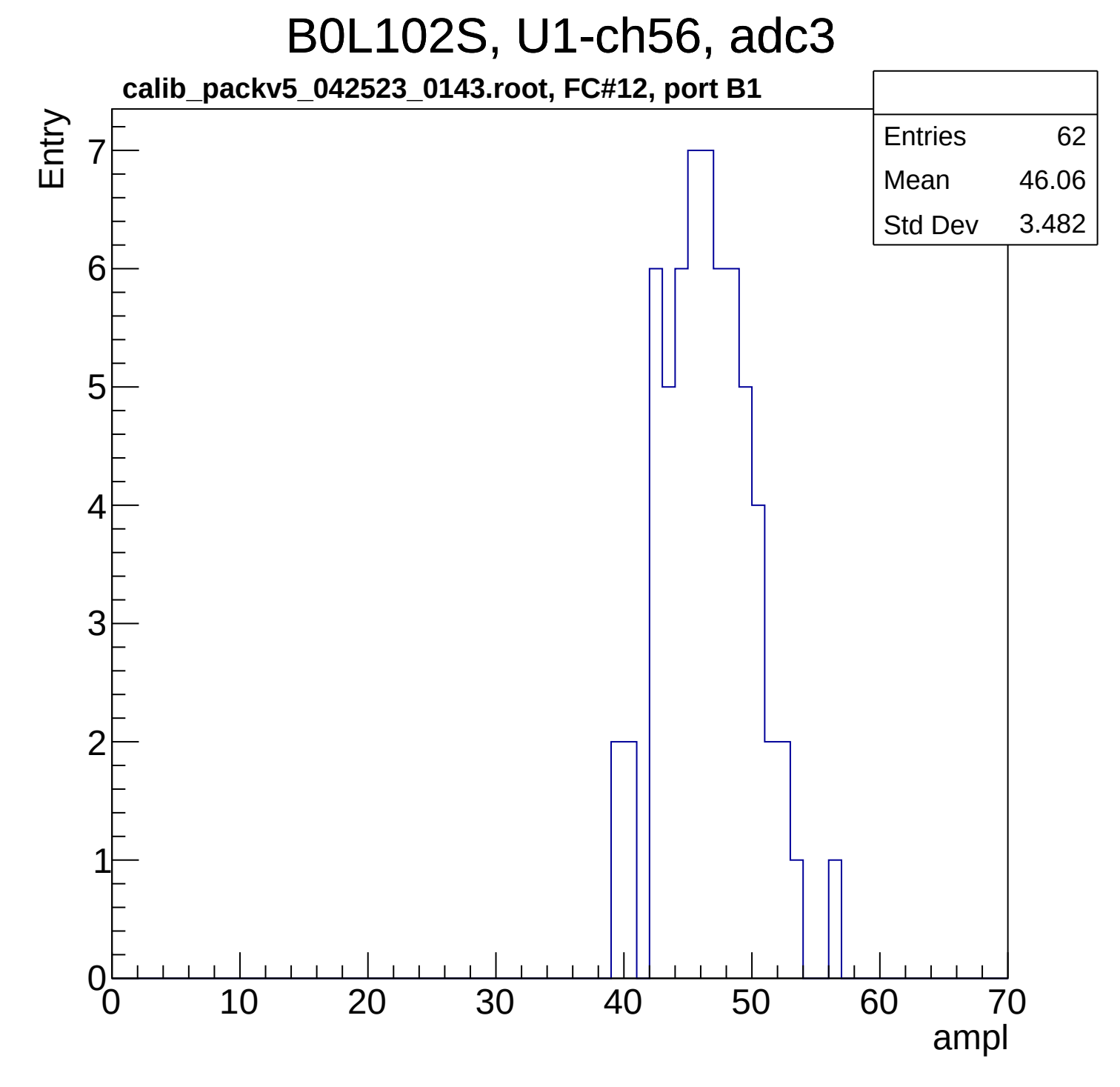
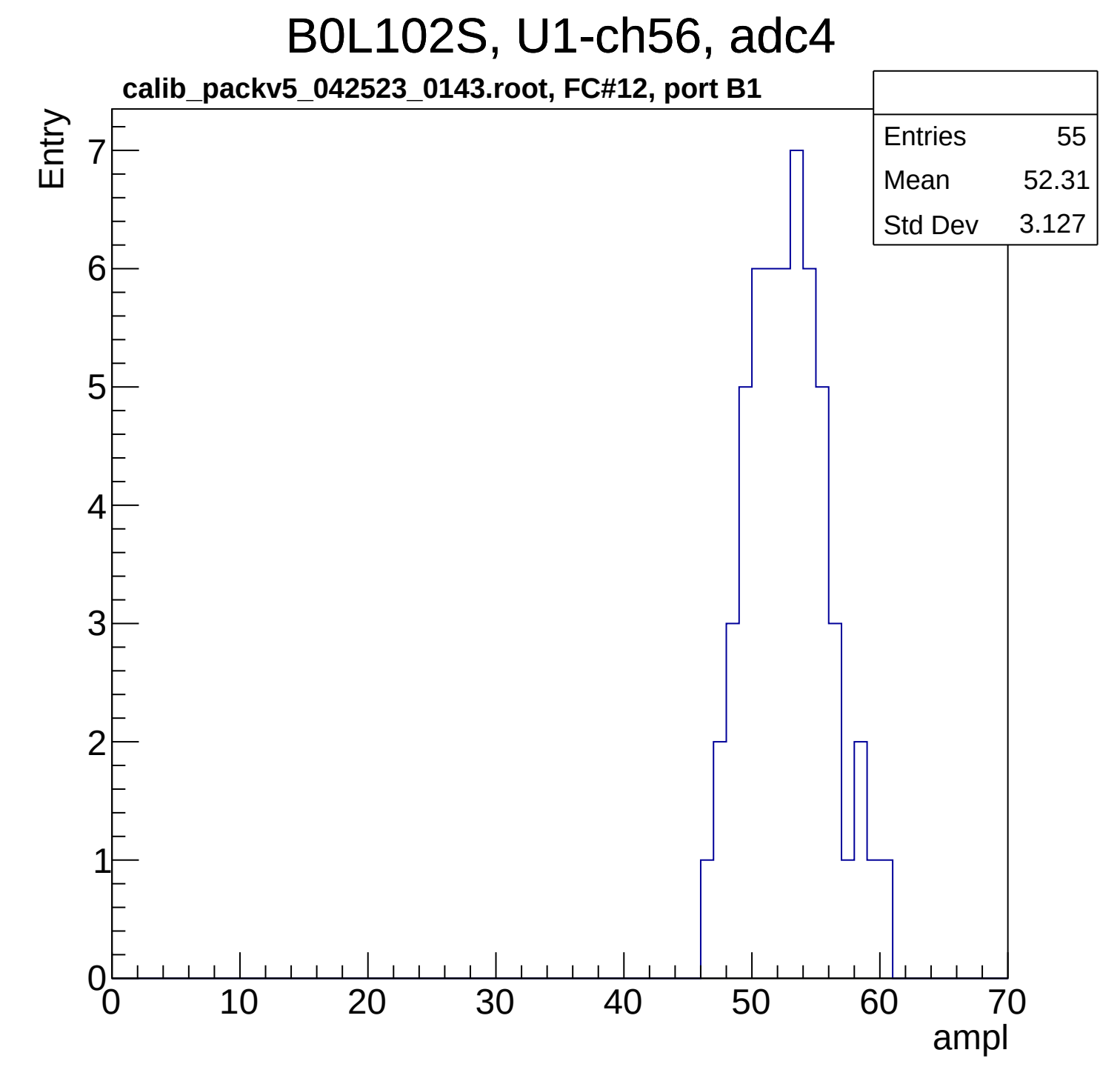
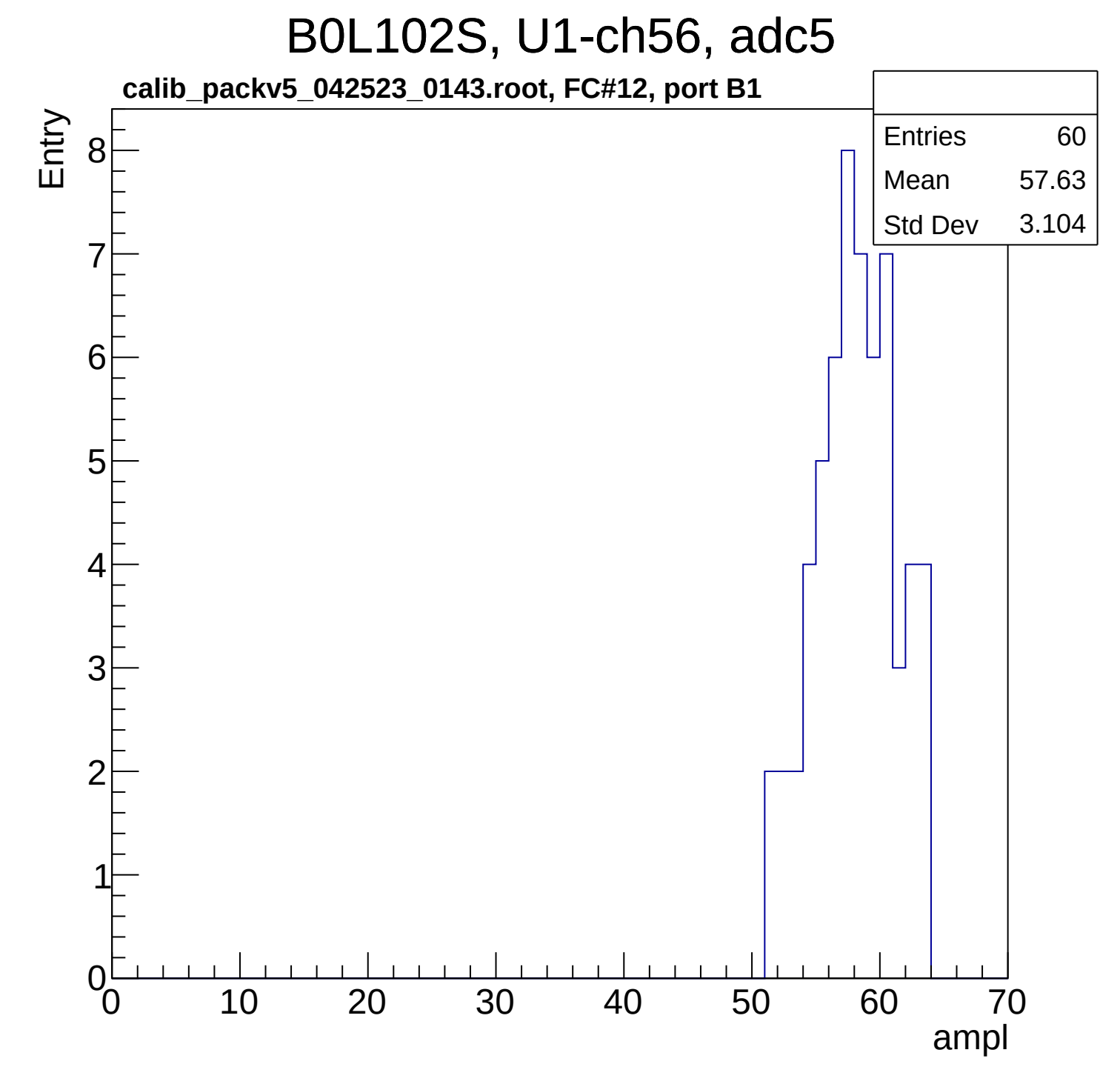
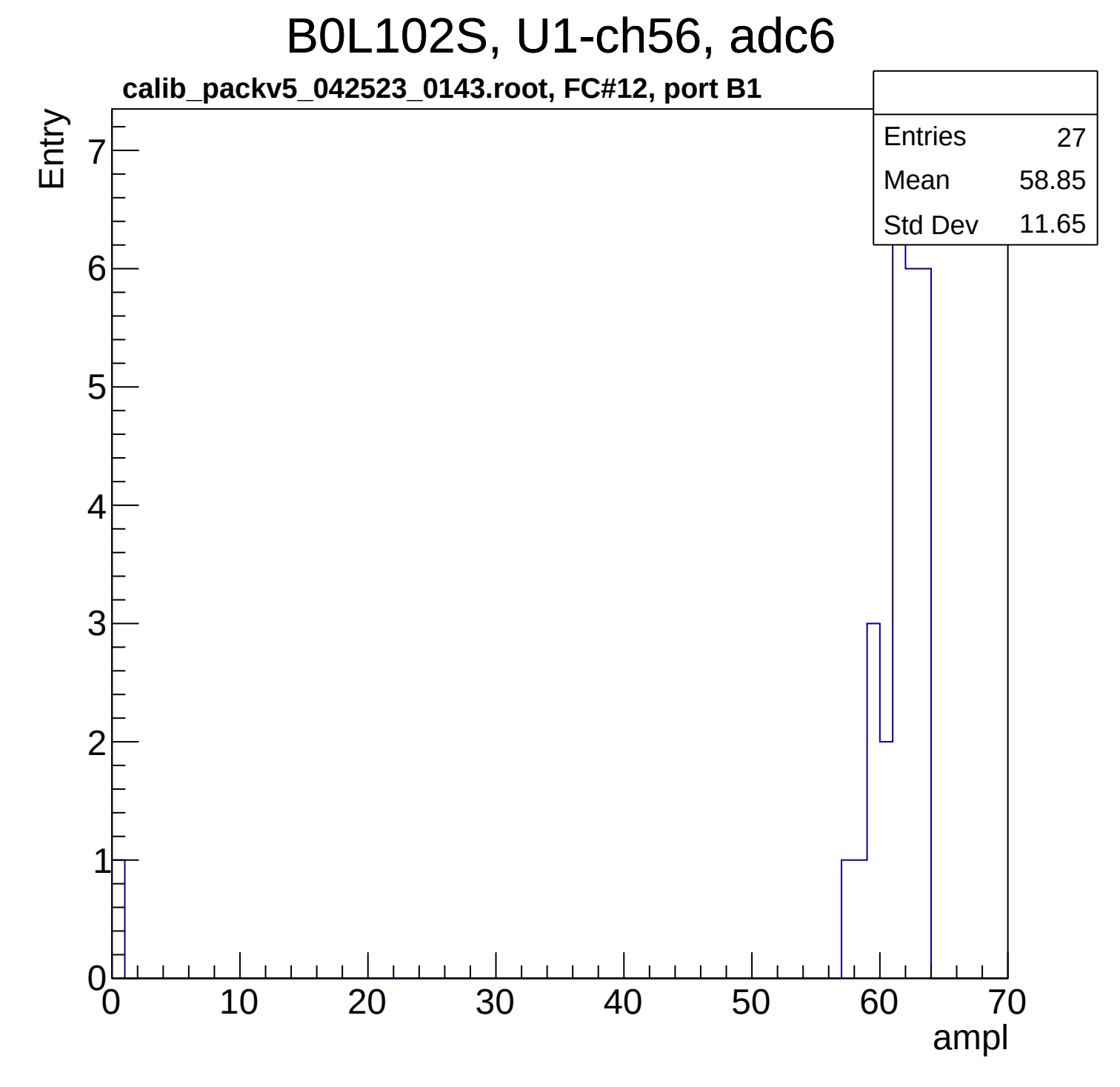
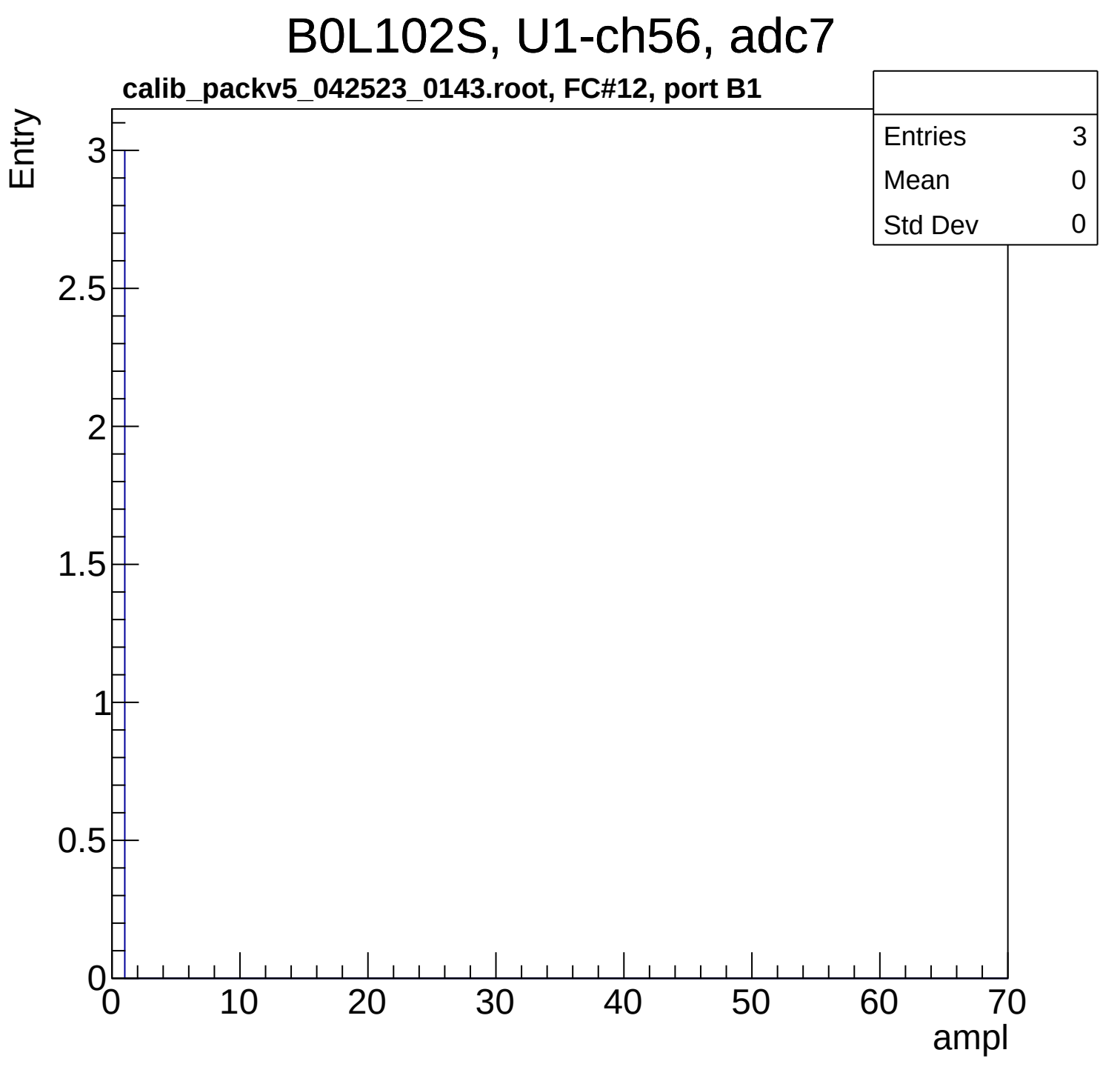
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch56, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 46.06
Std Dev 3.482
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch56, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 52.31
Std Dev 3.127
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch56, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 57.63
Std Dev 3.104
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch56, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 27
Mean 58.85
Std Dev 11.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch56, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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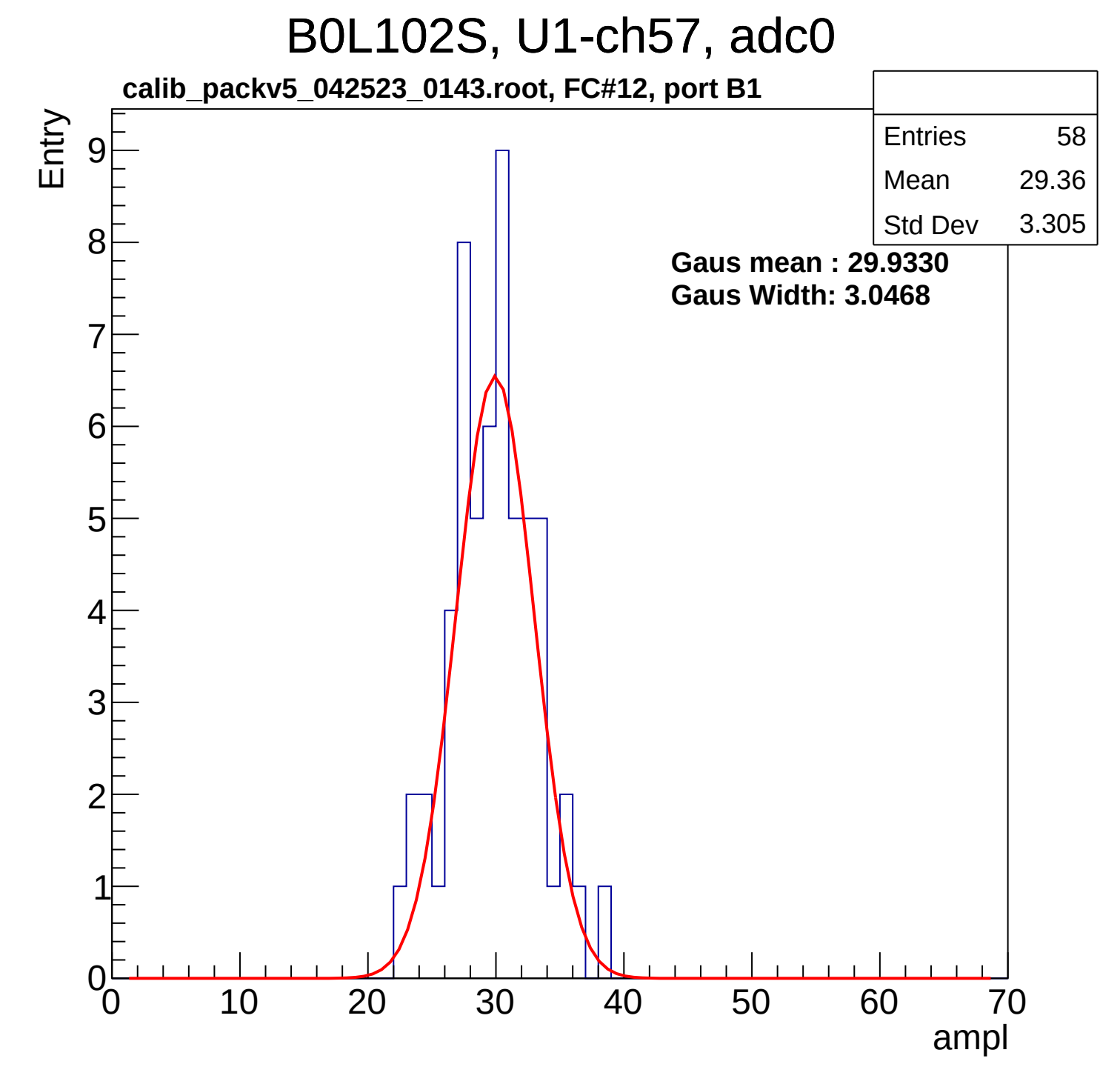
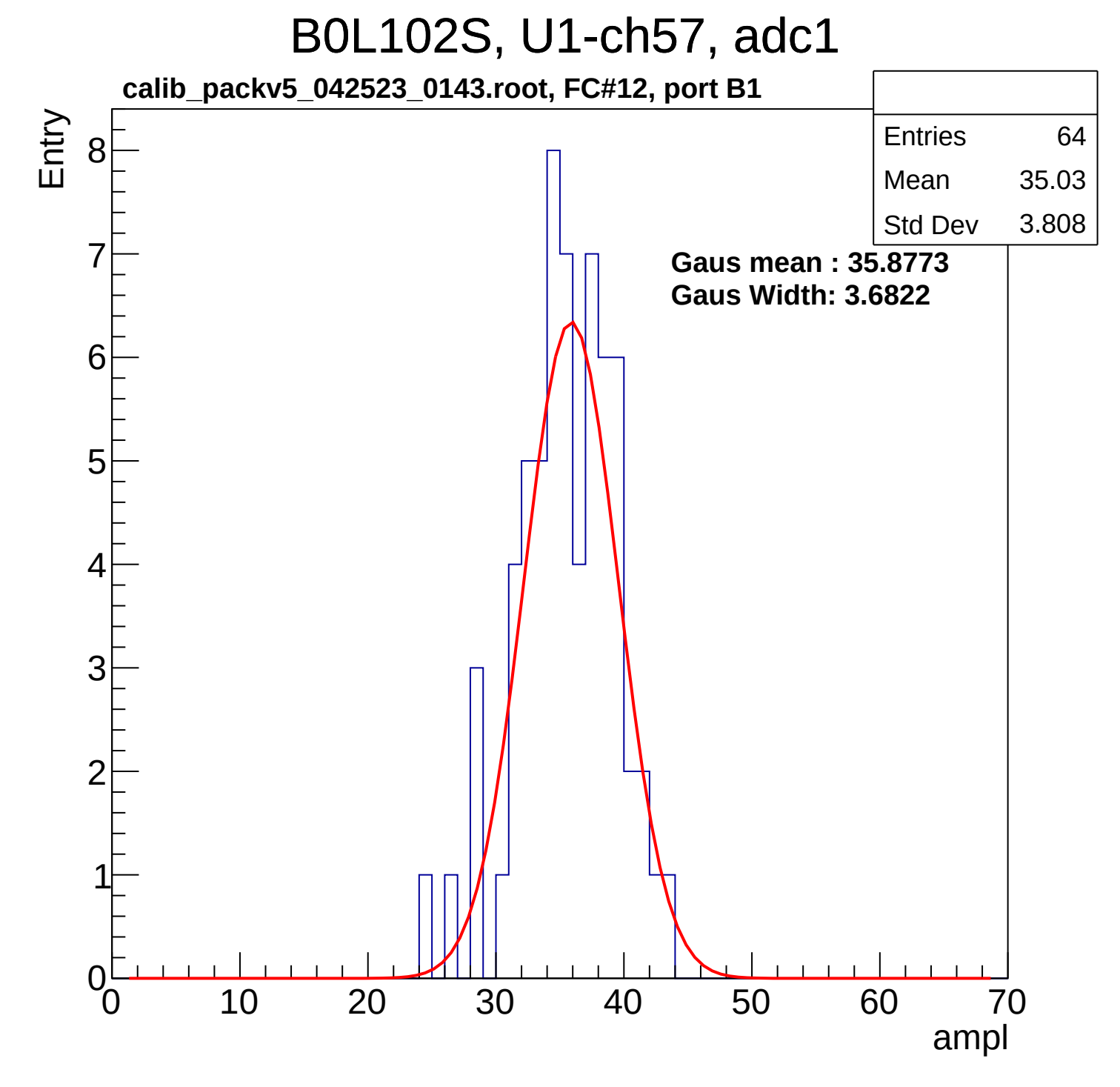
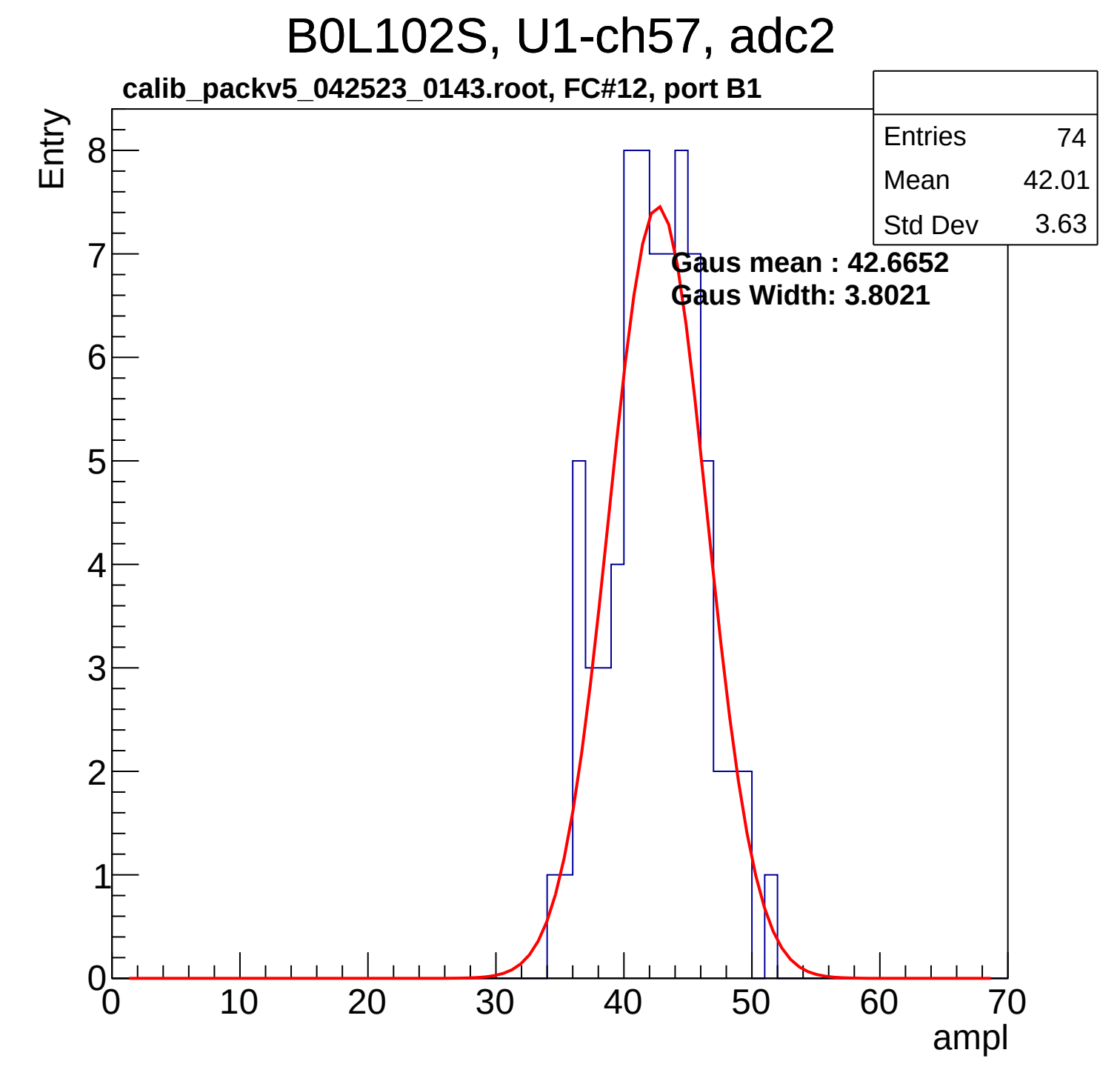
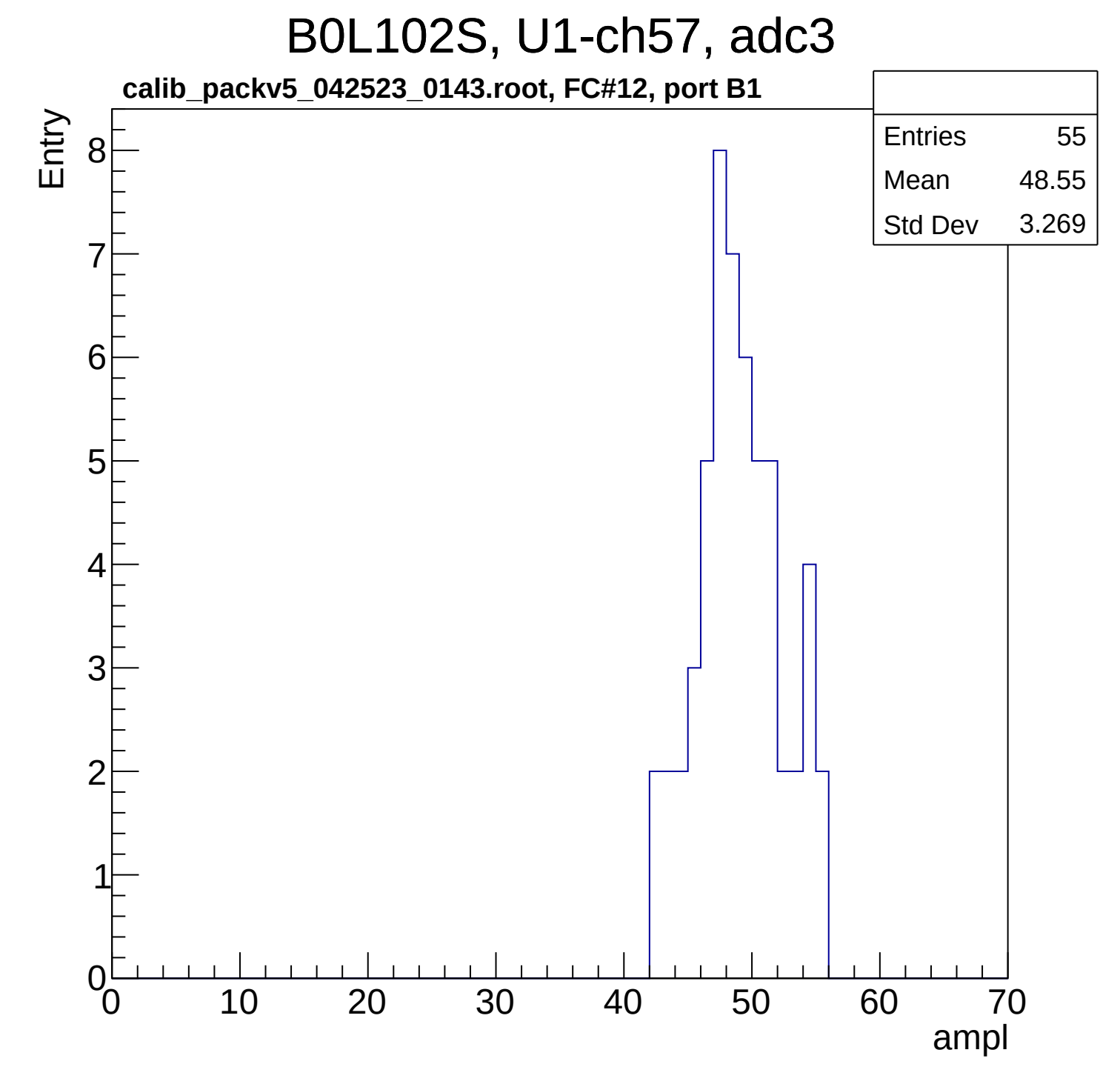
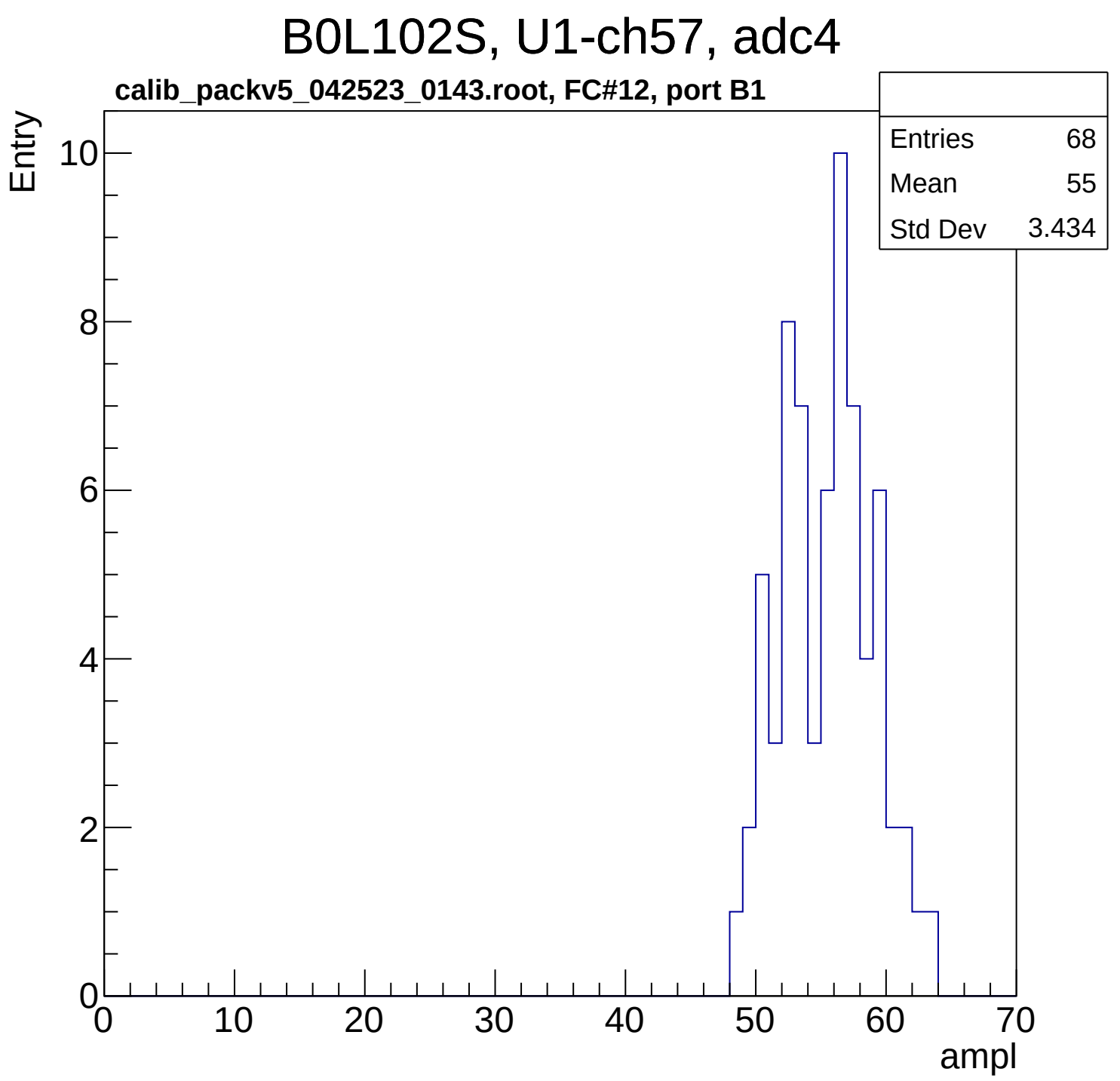
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

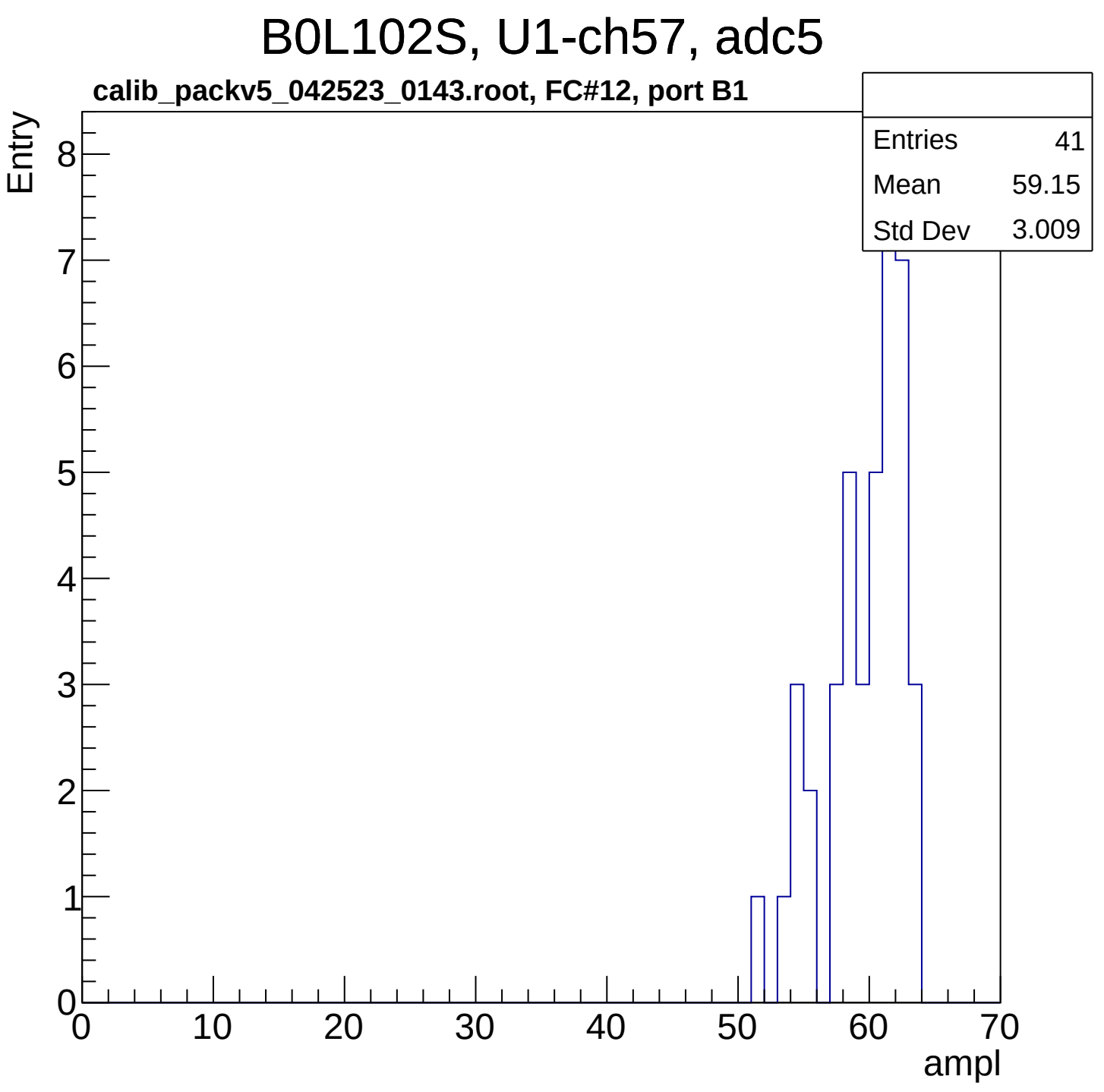

B0L102S, U1-ch57, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 29.36
Std Dev 3.305
Gaus mean : 29.9330
Gaus Width: 3.0468
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

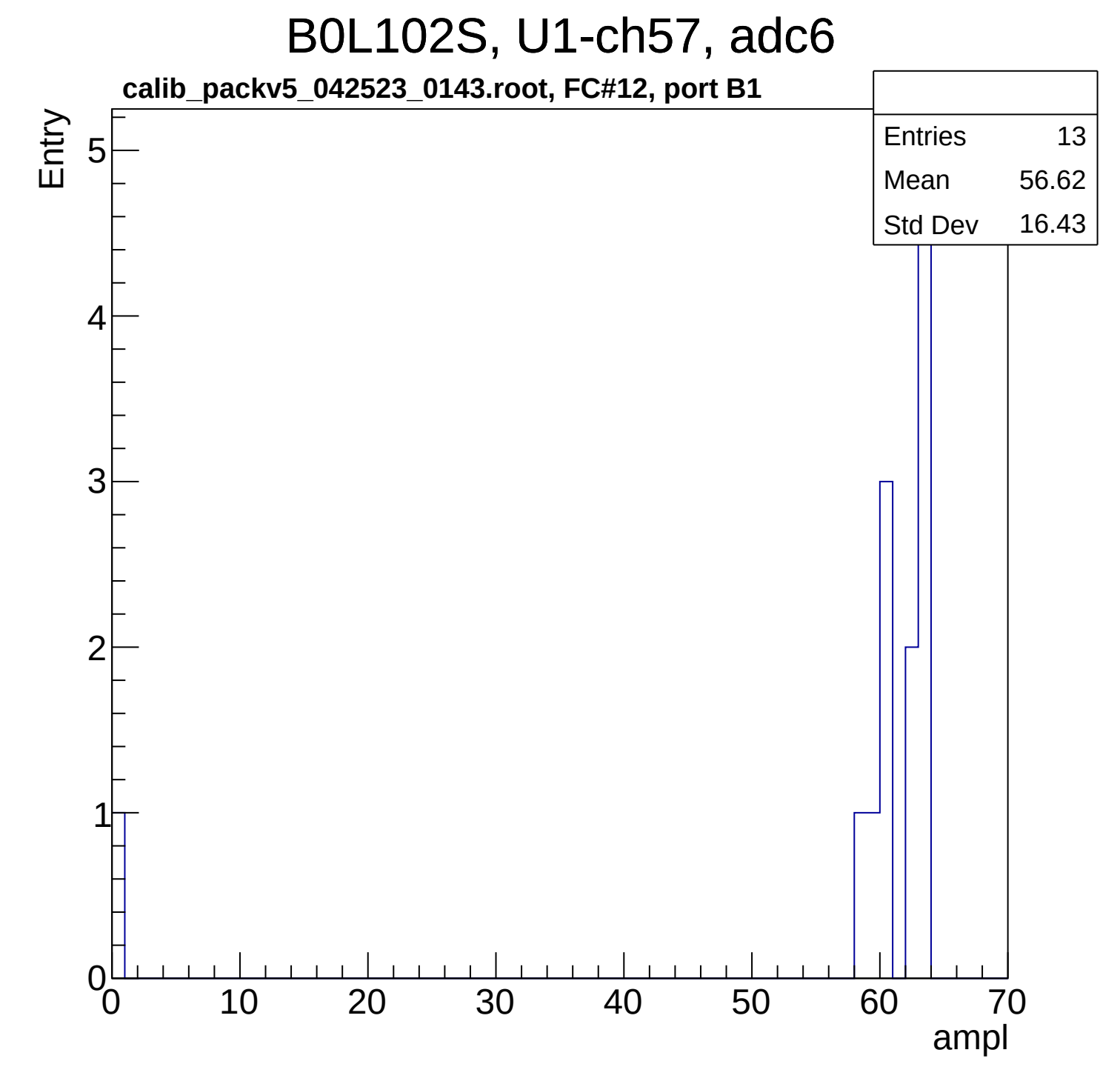

B0L102S, U1-ch57, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 35.03
Std Dev 3.808
Gaus mean : 35.8773
Gaus Width: 3.6822
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

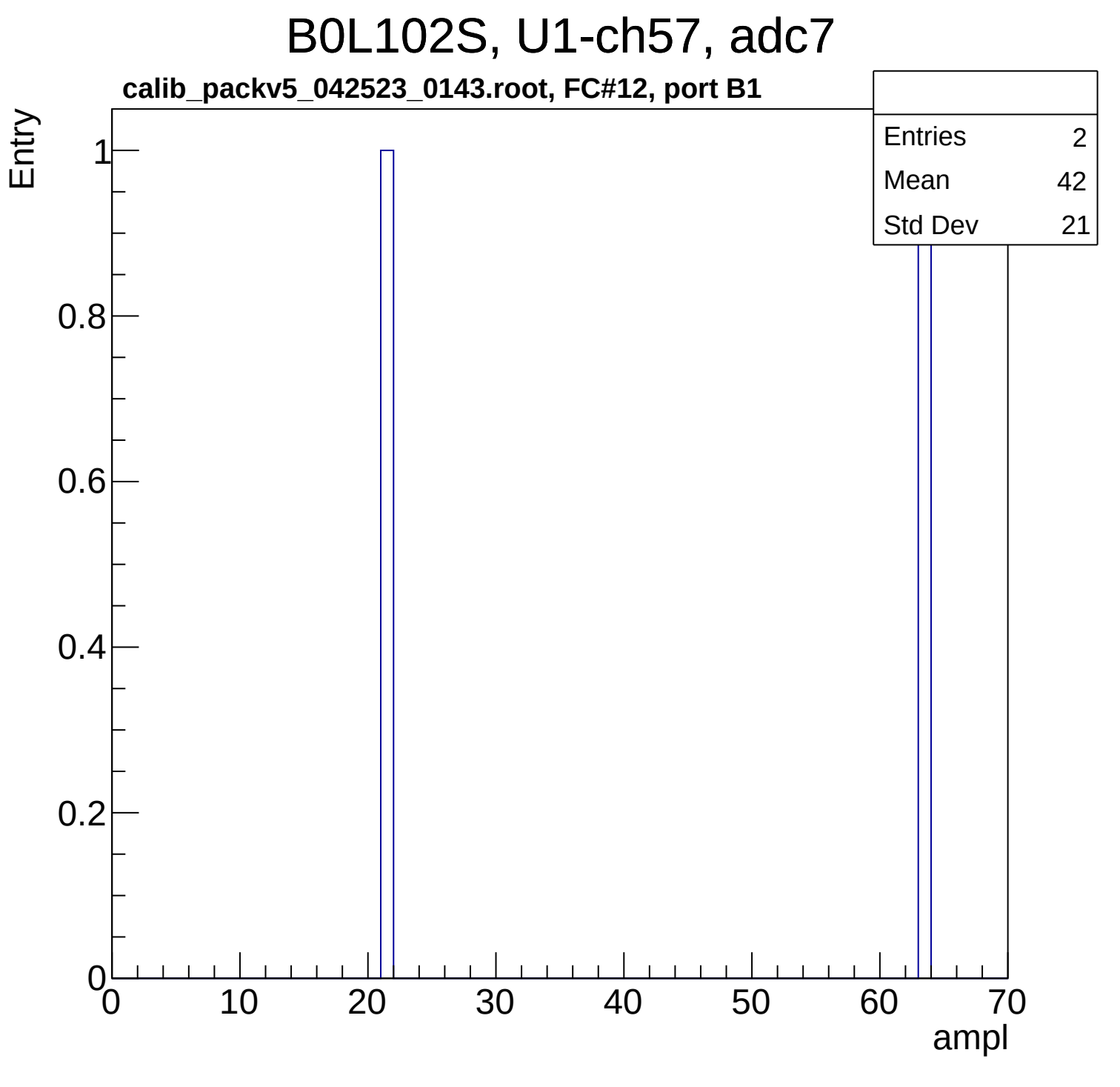

B0L102S, U1-ch57, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 42.01
Std Dev 3.63
Gaus mean : 42.6652
Gaus Width: 3.8021
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

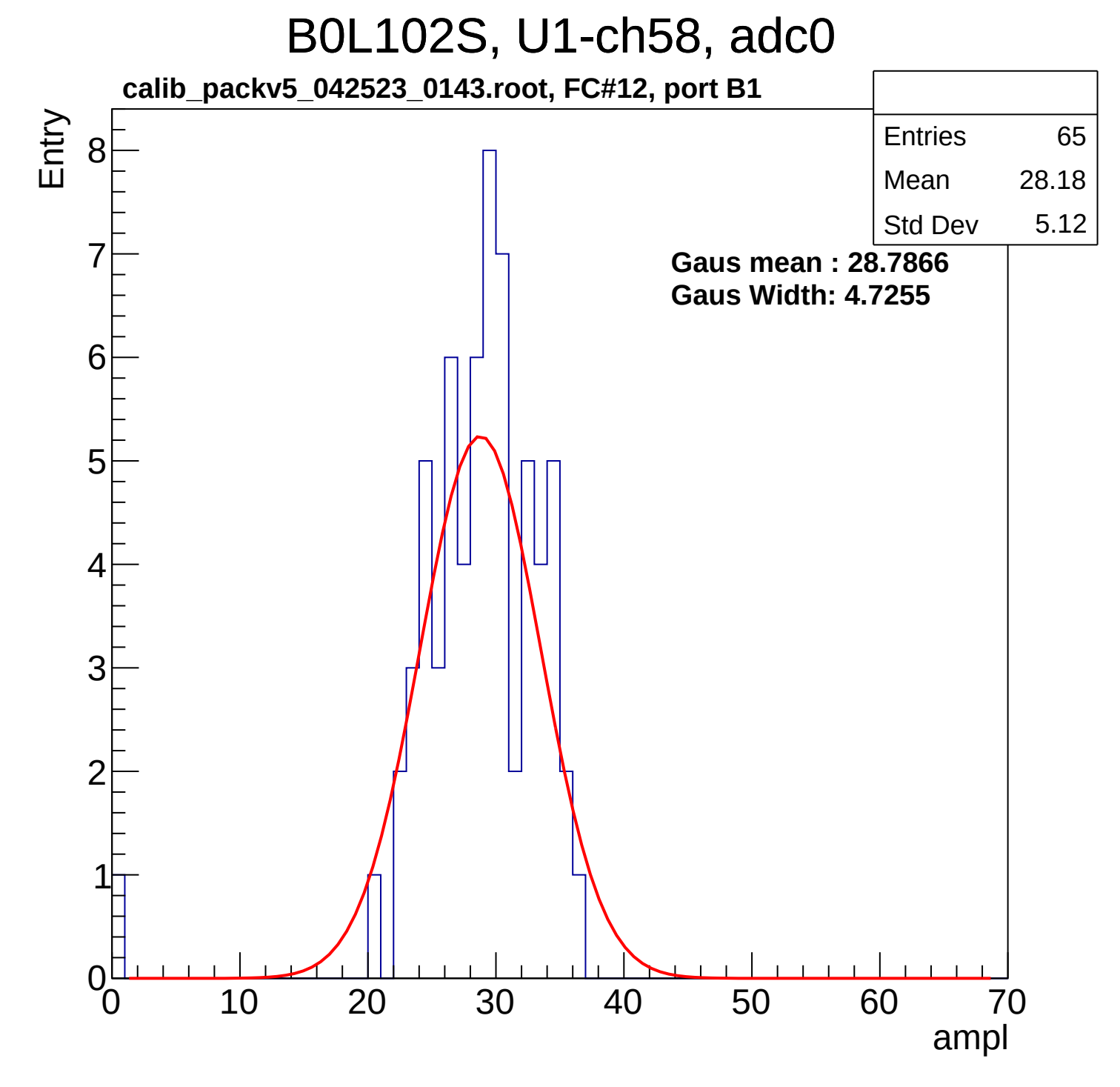

B0L102S, U1-ch57, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 48.55
Std Dev 3.269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

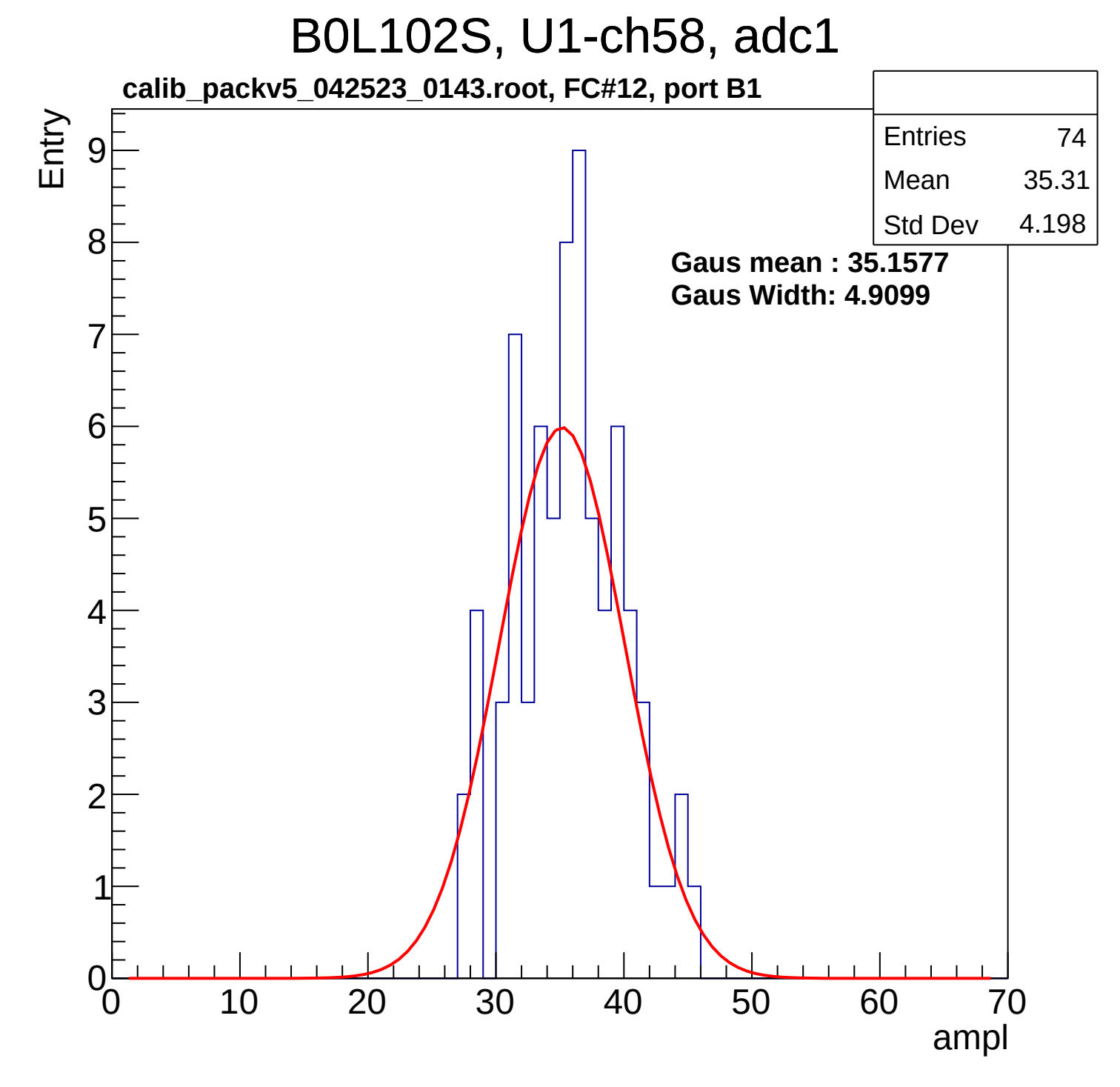

B0L102S, U1-ch57, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 55
Std Dev 3.434
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

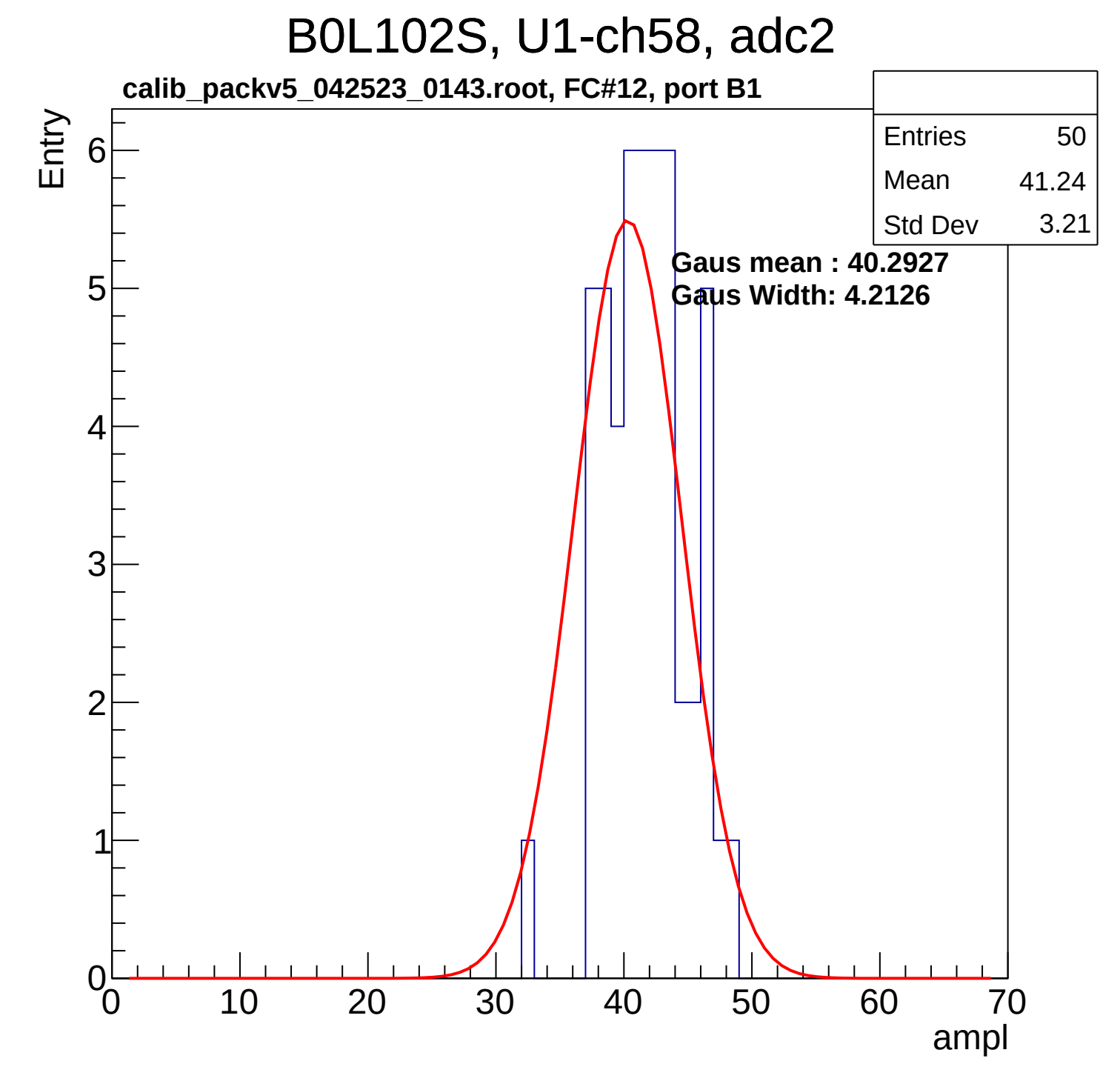

B0L102S, U1-ch57, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 59.15
Std Dev 3.009
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

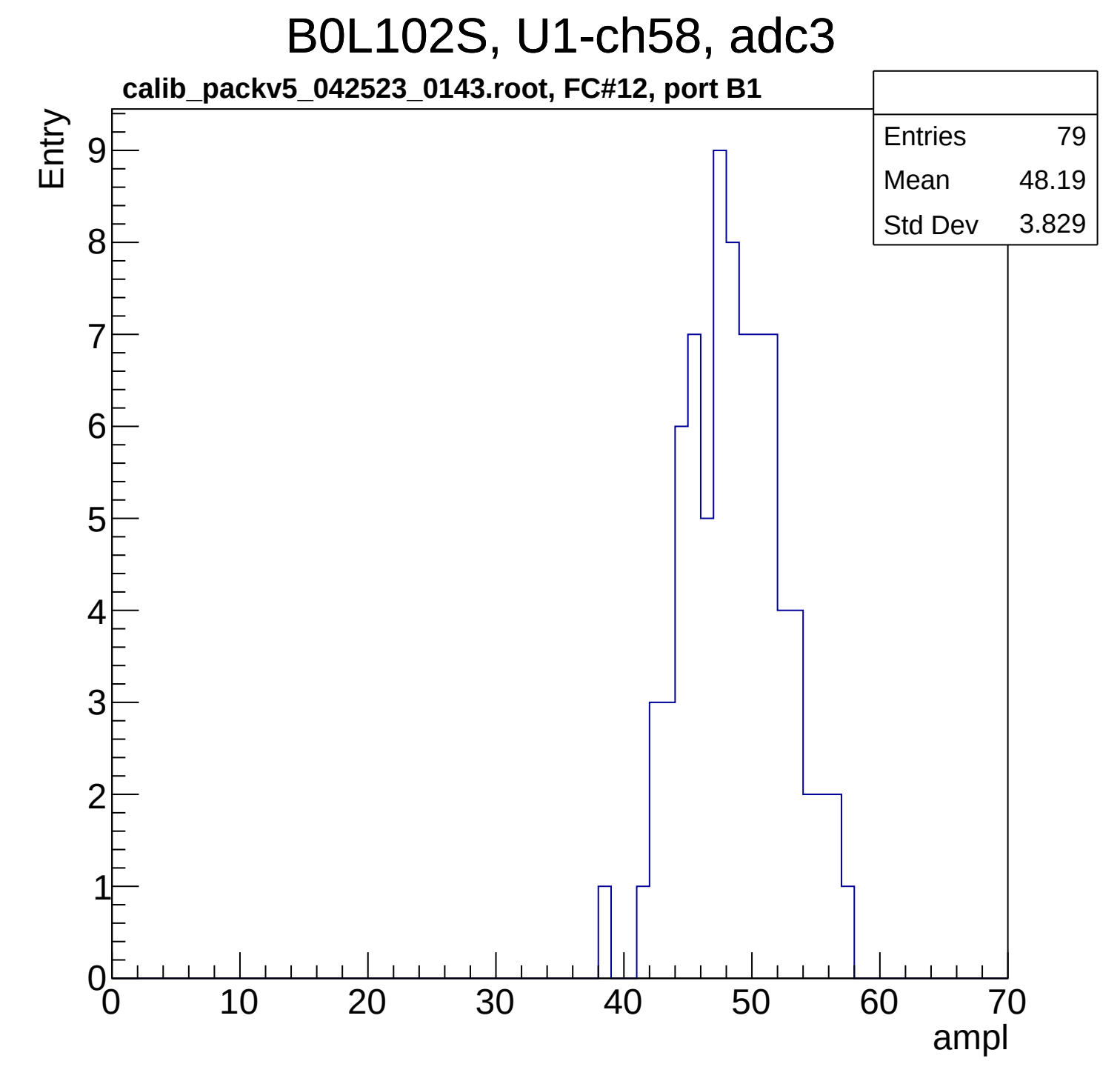

B0L102S, U1-ch57, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.62
Std Dev 16.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

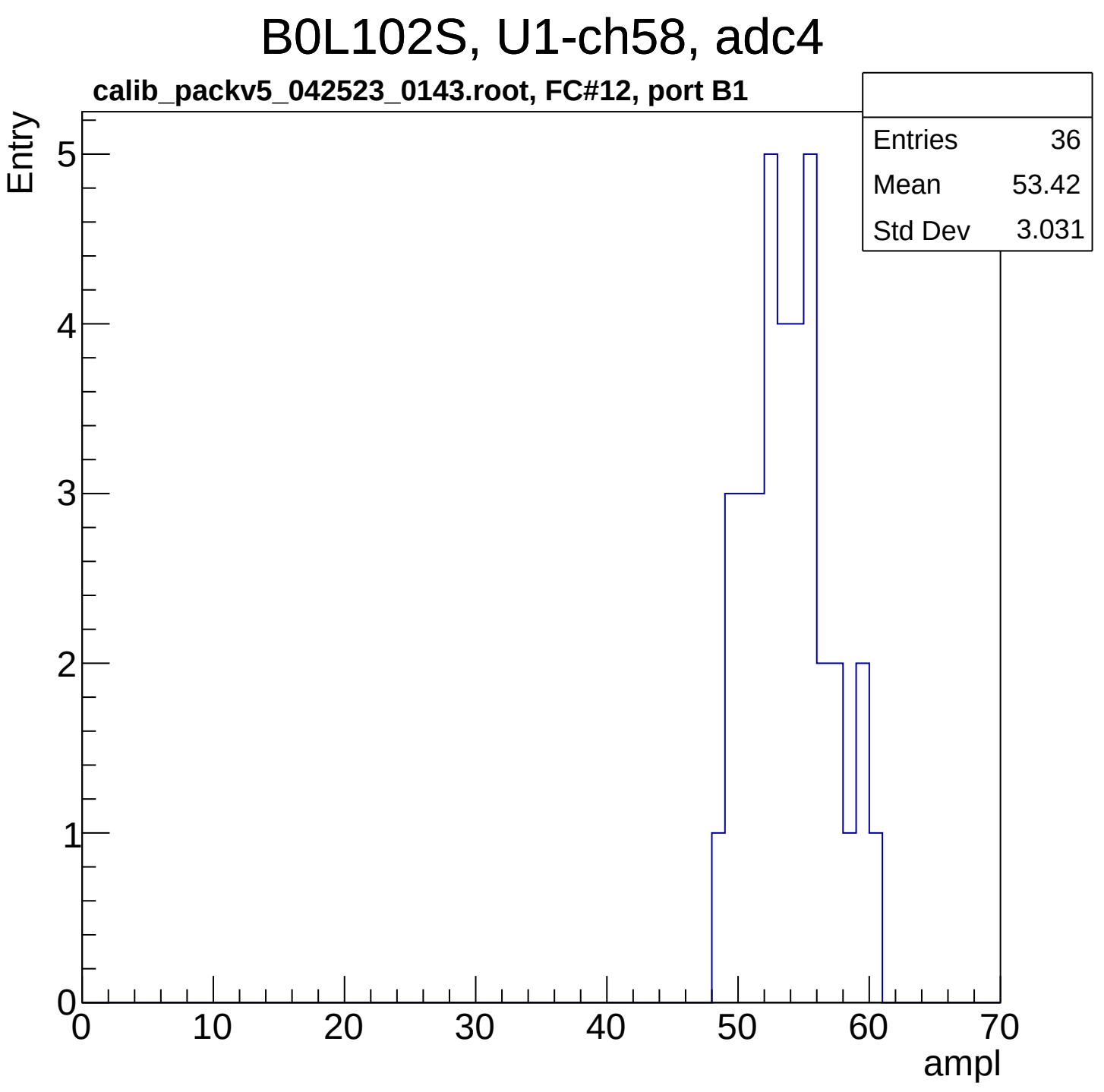

B0L102S, U1-ch57, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 42
Std Dev 21
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

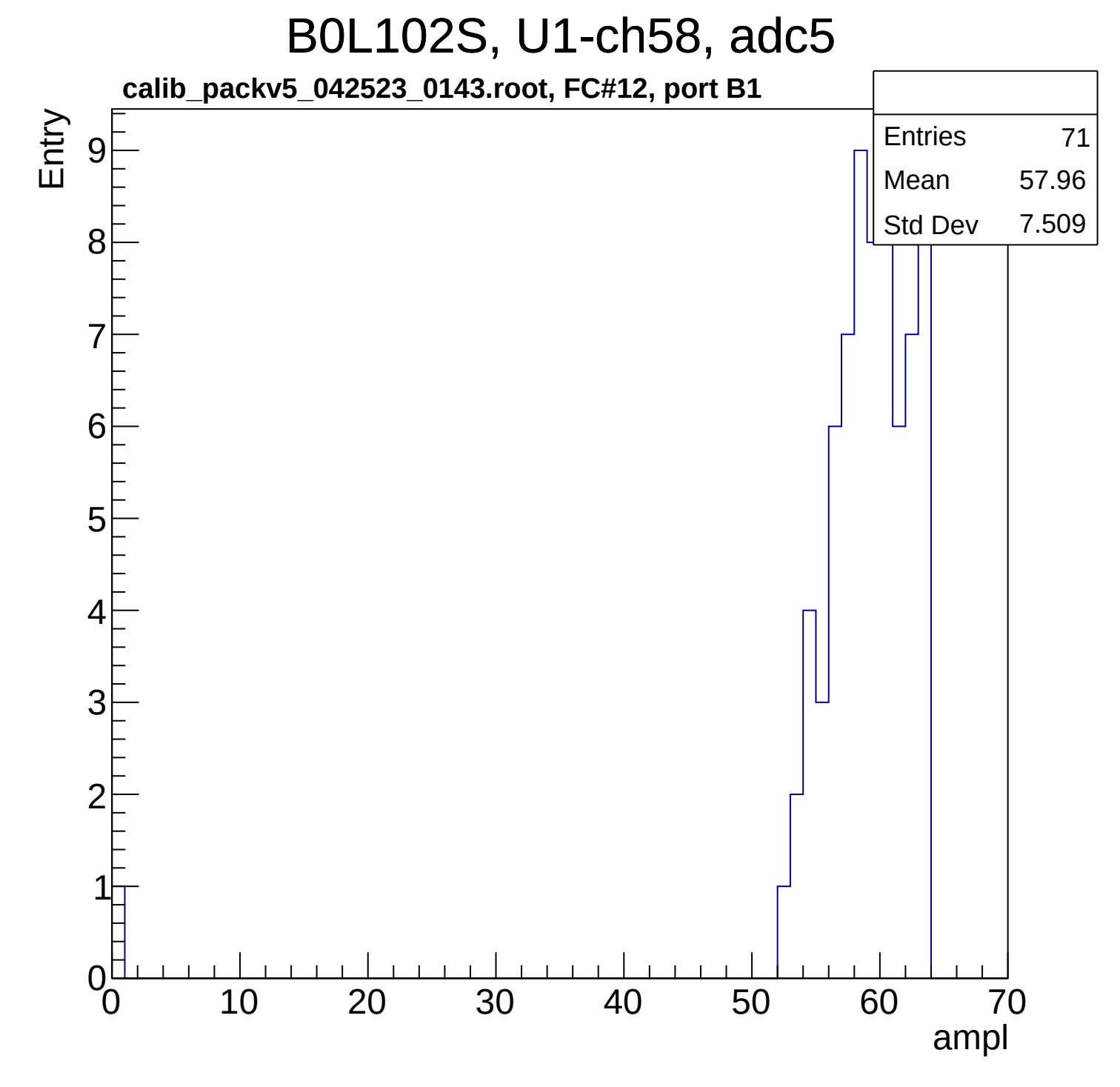

B0L102S, U1-ch58, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 28.18
Std Dev 5.12
Gaus mean : 28.7866
Gaus Width: 4.7255
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch58, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 35.31
Std Dev 4.198
Gaus mean : 35.1577
Gaus Width: 4.9099
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch58, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 41.24
Std Dev 3.21
Gaus mean : 40.2927
Gaus Width: 4.2126
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U1-ch58, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 48.19
Std Dev 3.829
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch58, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 53.42
Std Dev 3.031
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch58, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 57.96
Std Dev 7.509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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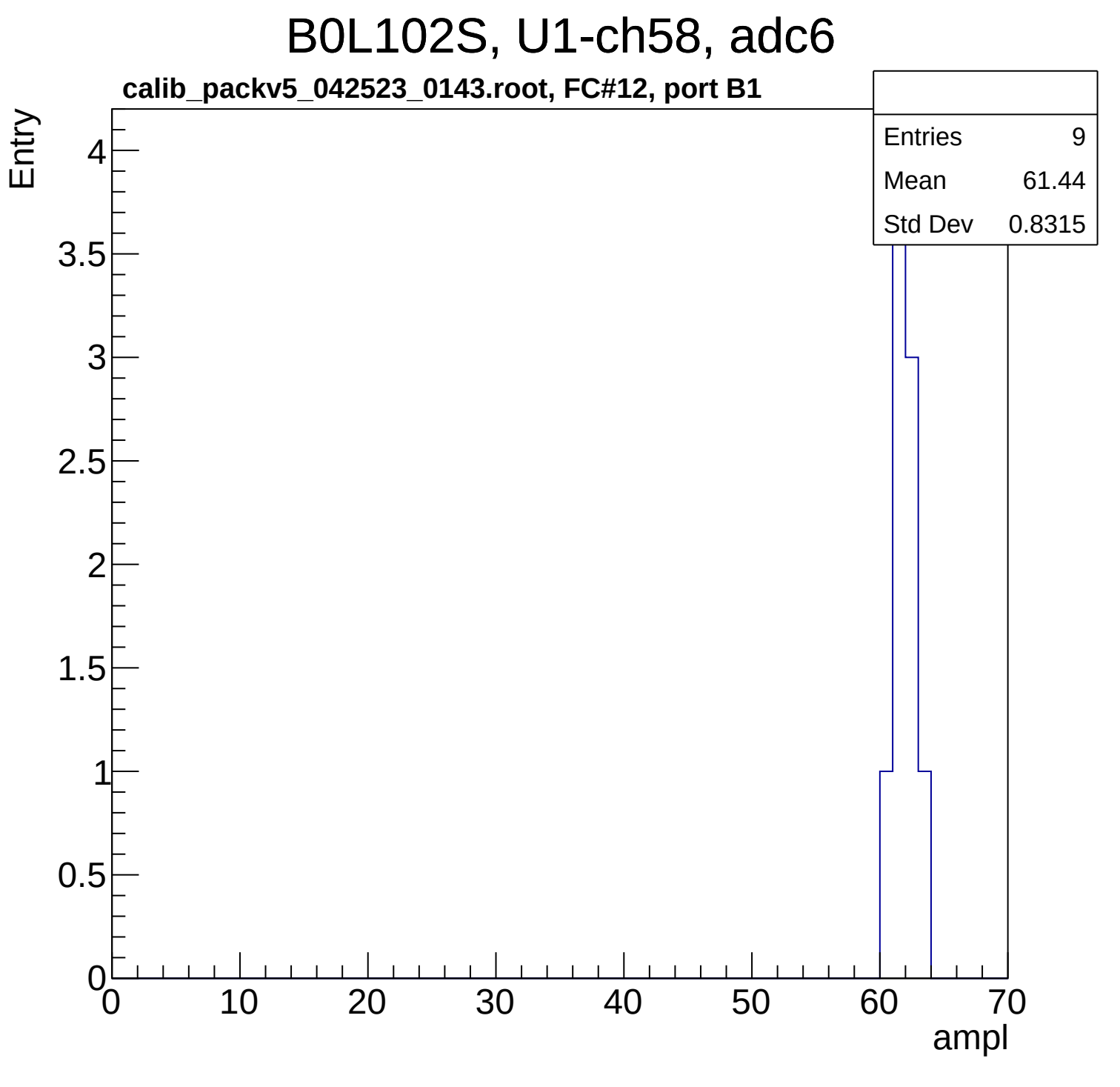
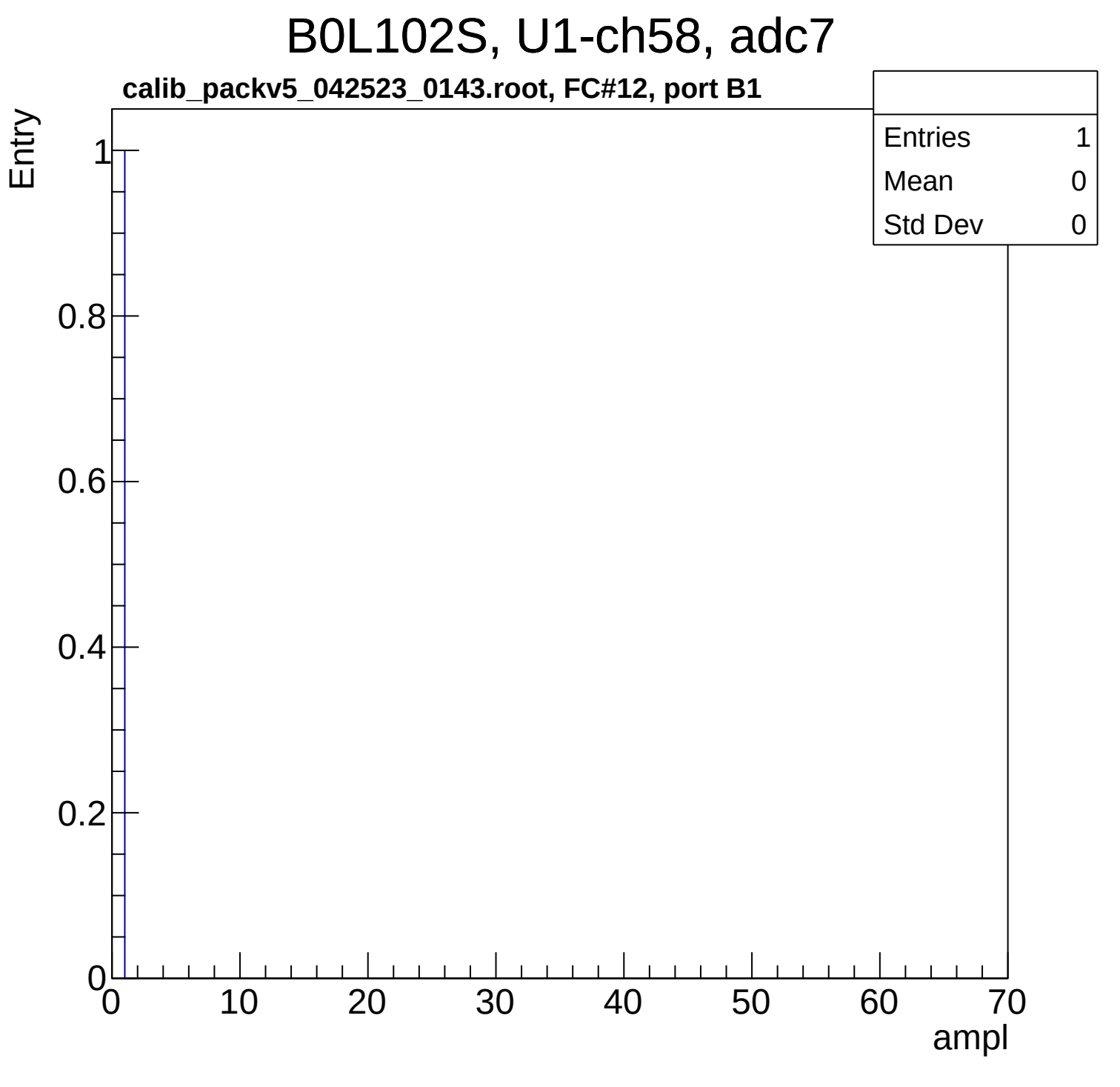

B0L102S, U1-ch58, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.44
Std Dev 0.8315
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch58, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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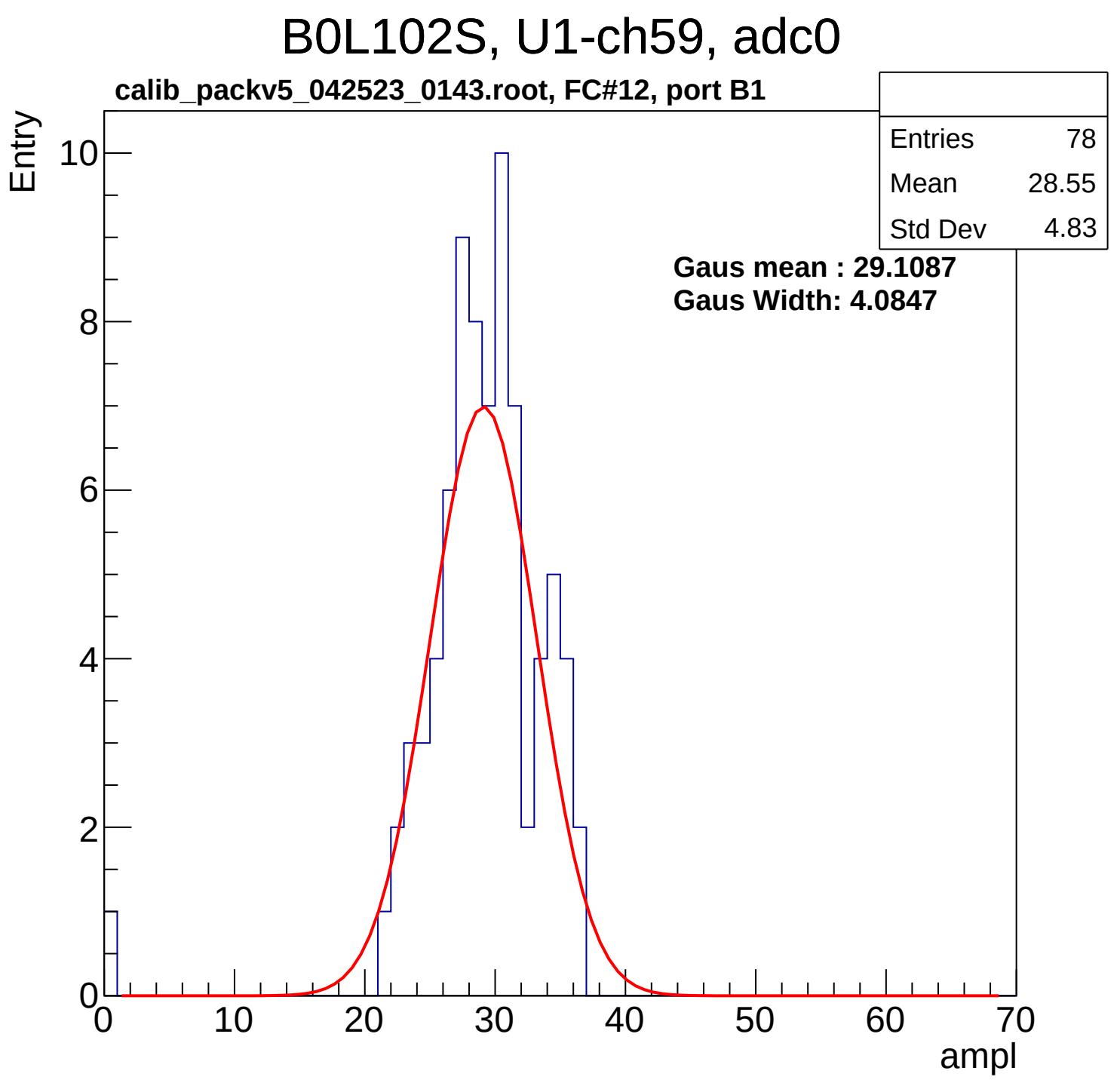
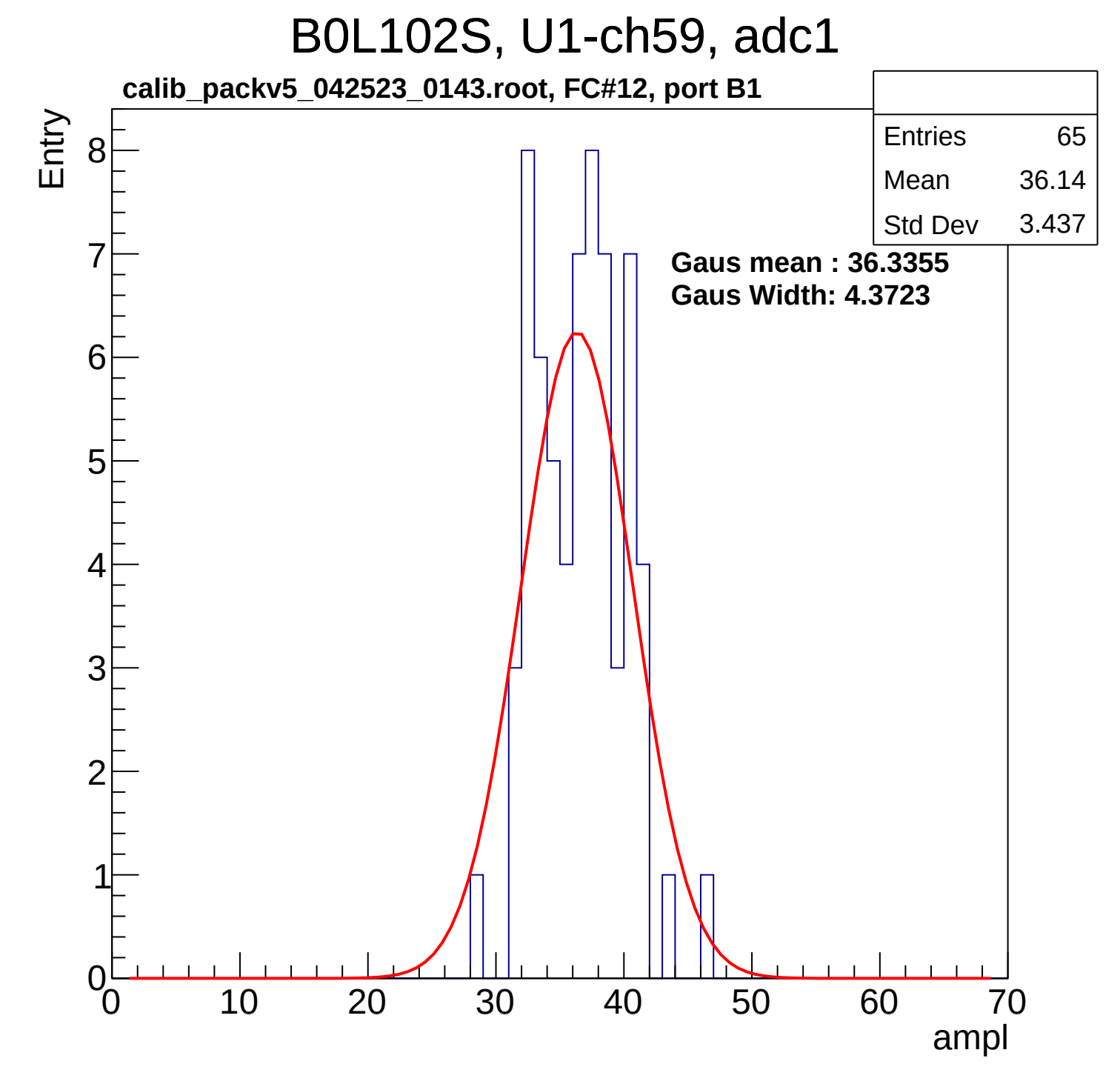
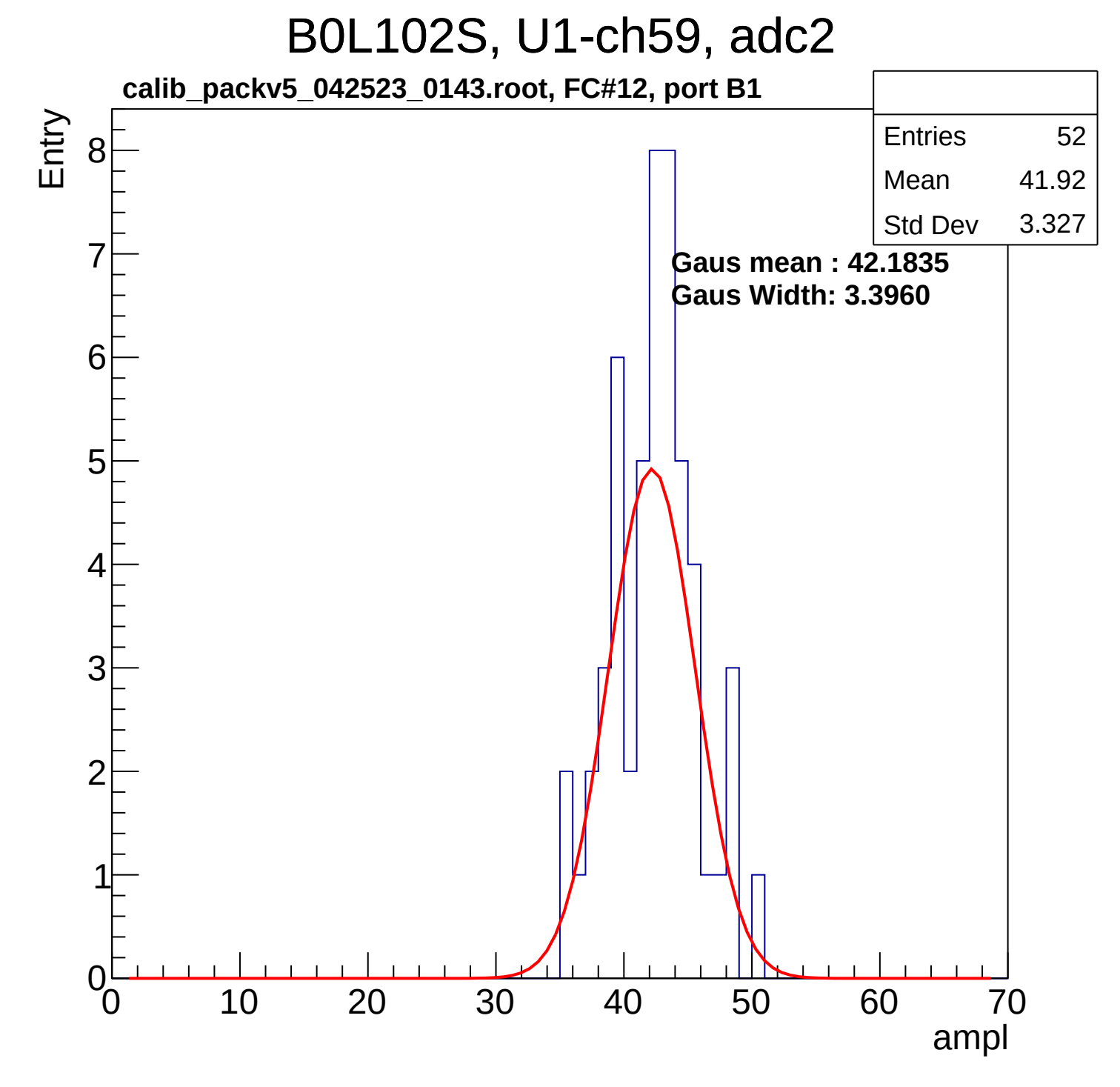
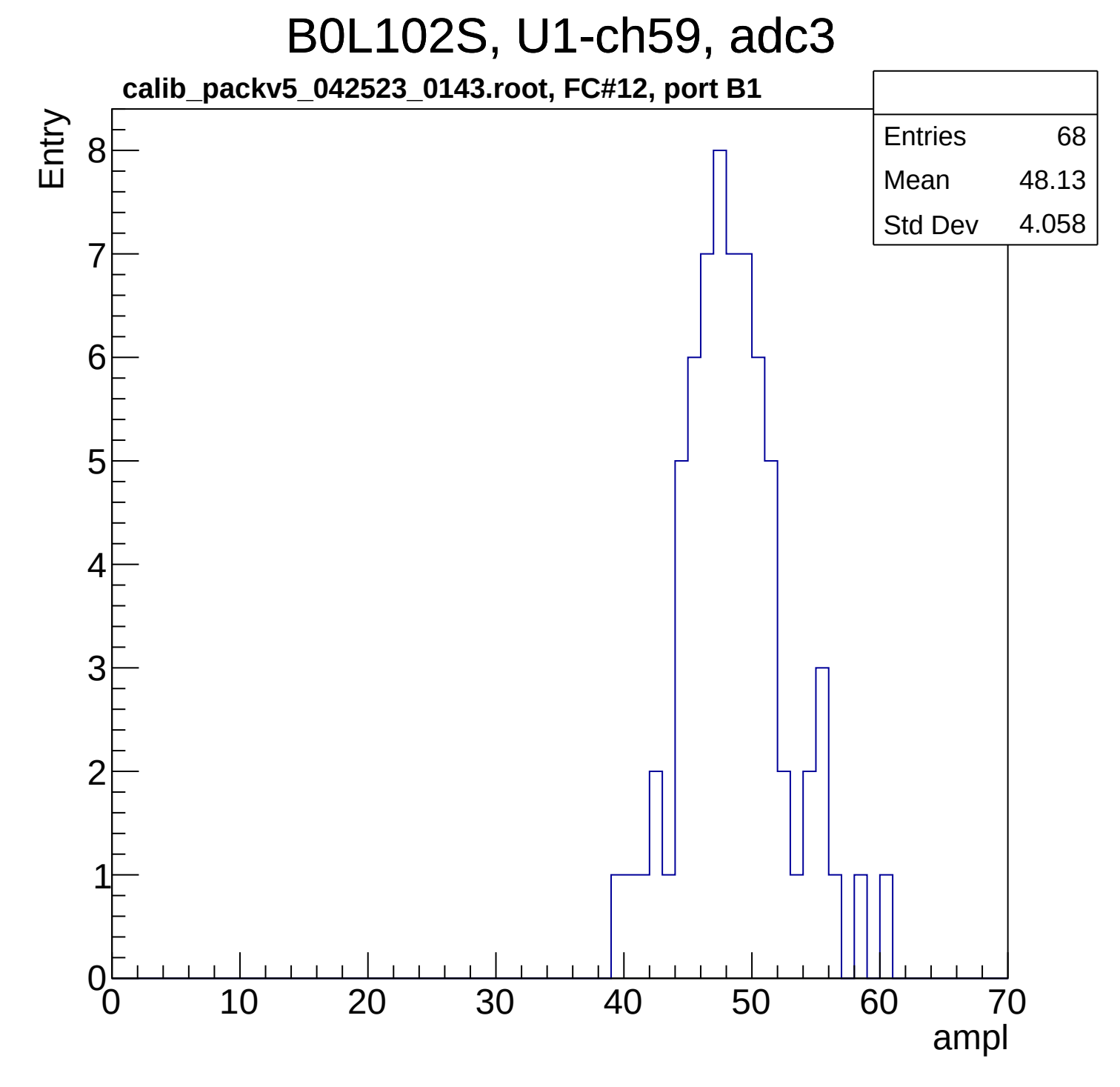
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch59, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 28.55
Std Dev 4.83
Gaus mean : 29.1087
Gaus Width: 4.0847
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch59, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 36.14
Std Dev 3.437
Gaus mean : 36.3355
Gaus Width: 4.3723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch59, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 41.92
Std Dev 3.327
Gaus mean : 42.1835
Gaus Width: 3.3960
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch59, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.13
Std Dev 4.058
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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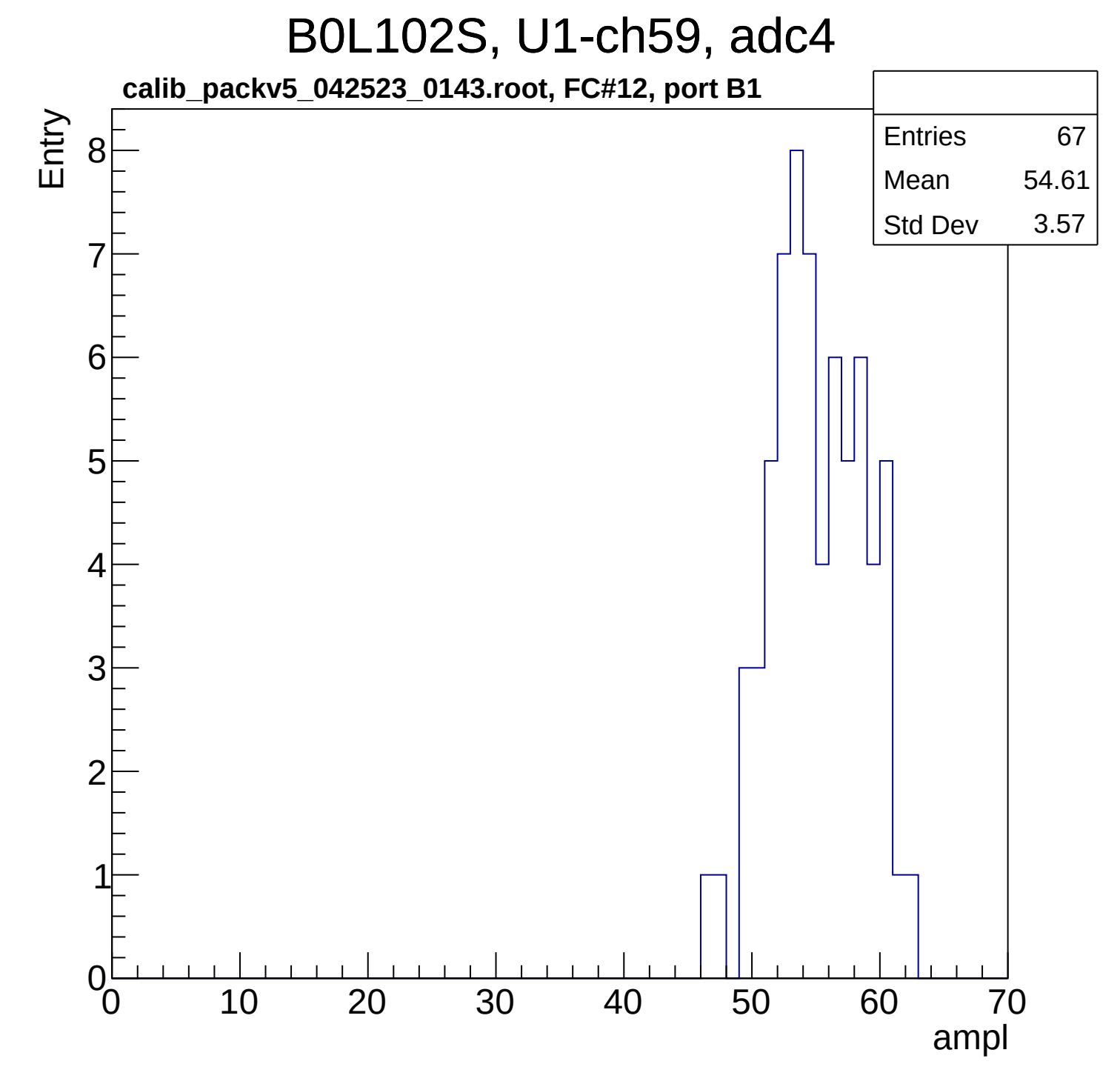
40
50
60
70


B0L102S, U1-ch59, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 54.61
Std Dev 3.57
Entry
ampl
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2
3
4
5
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7
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0
10
20
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40
50
60
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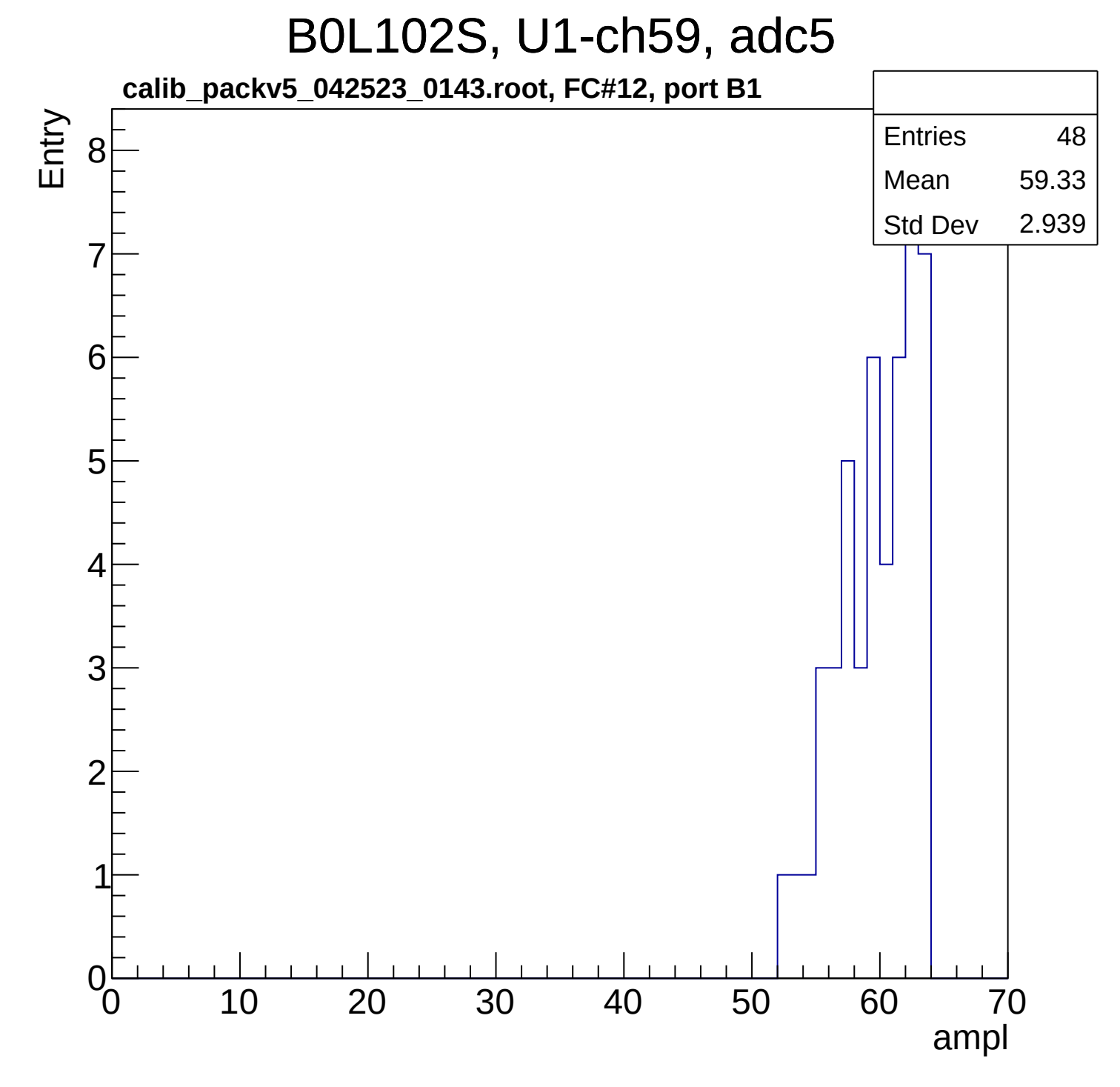
B0L102S, U1-ch59, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 59.33
Std Dev 2.939
Entry
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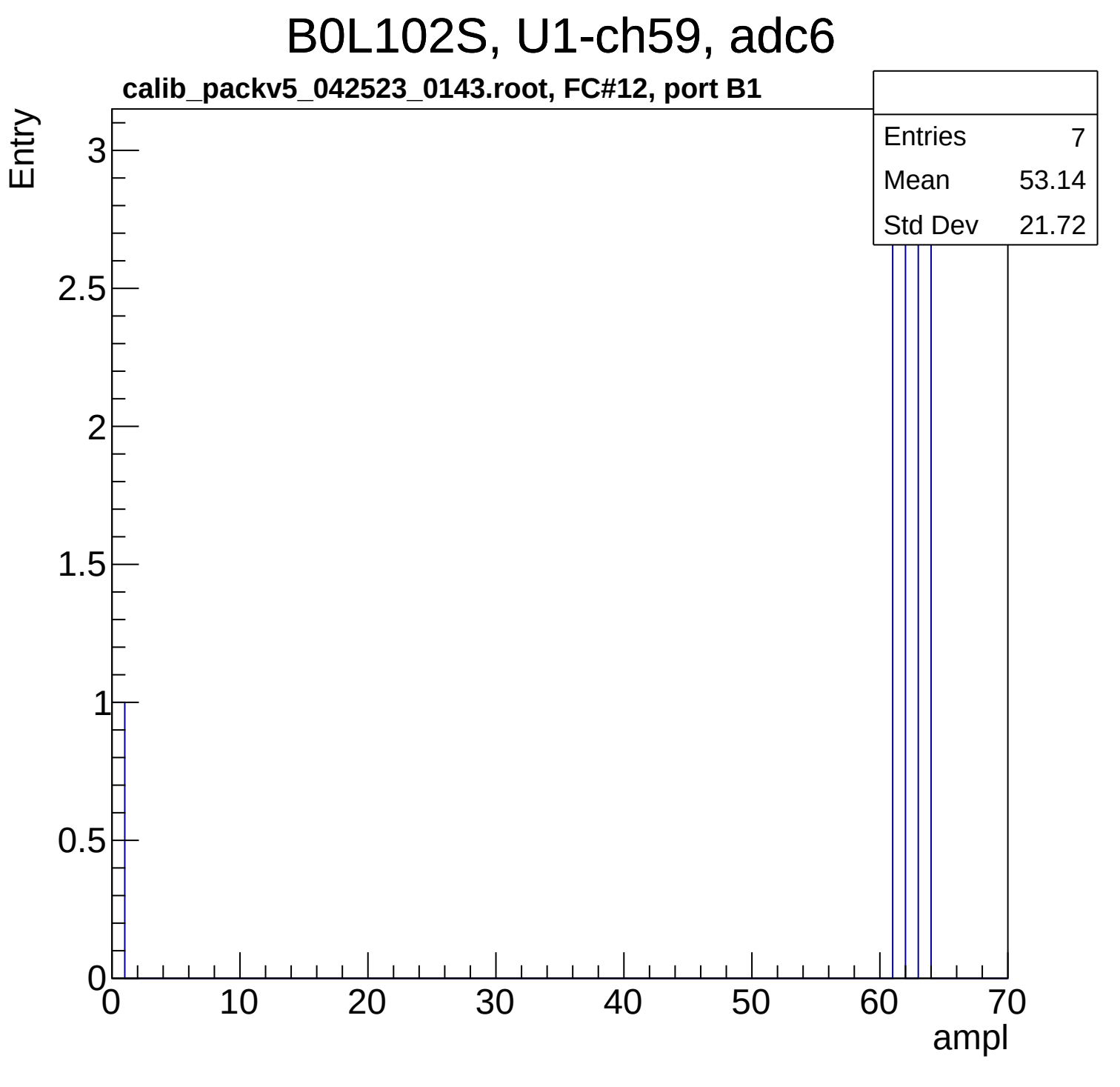
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch59, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.14
Std Dev 21.72
Entry
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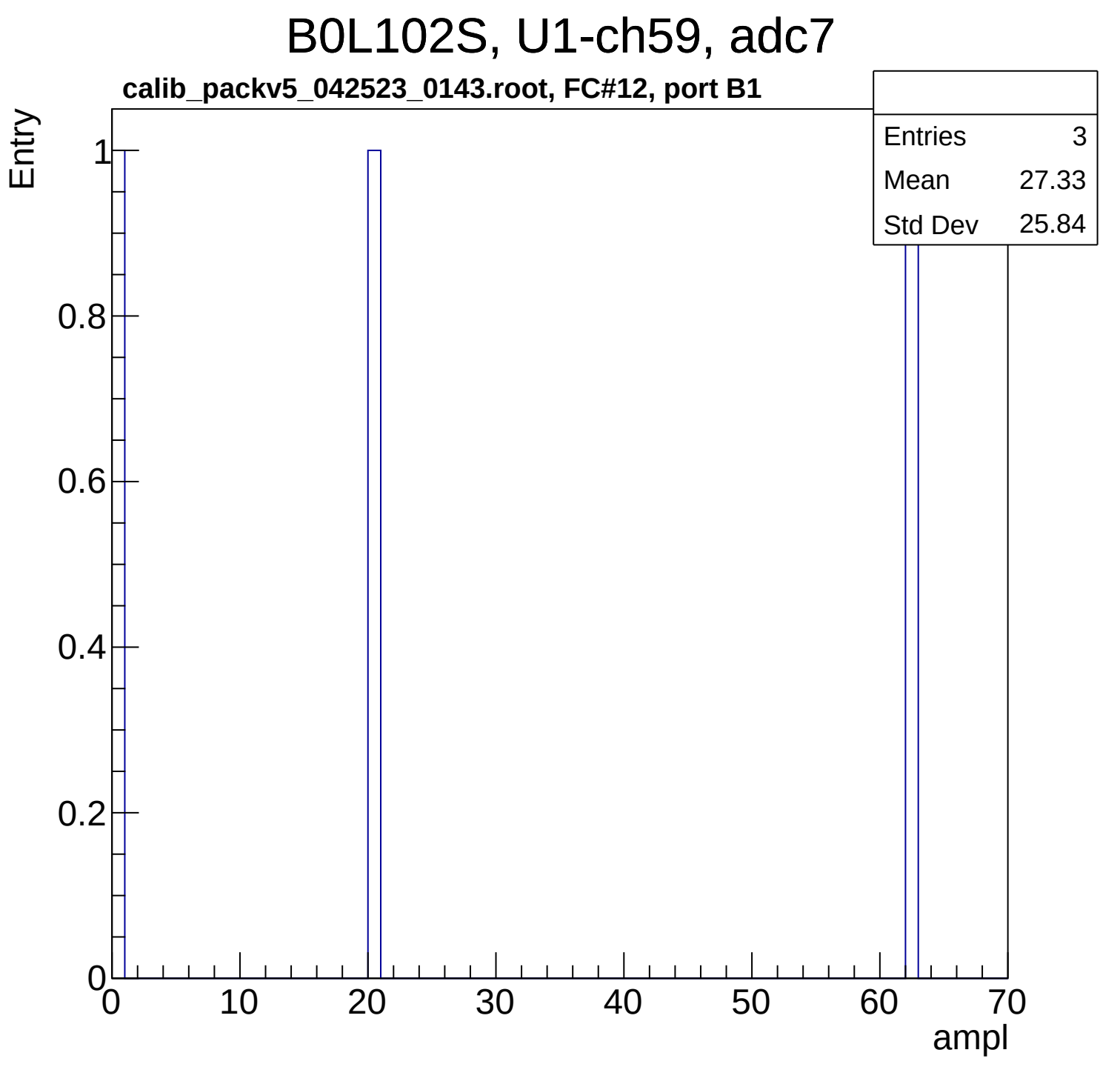
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch59, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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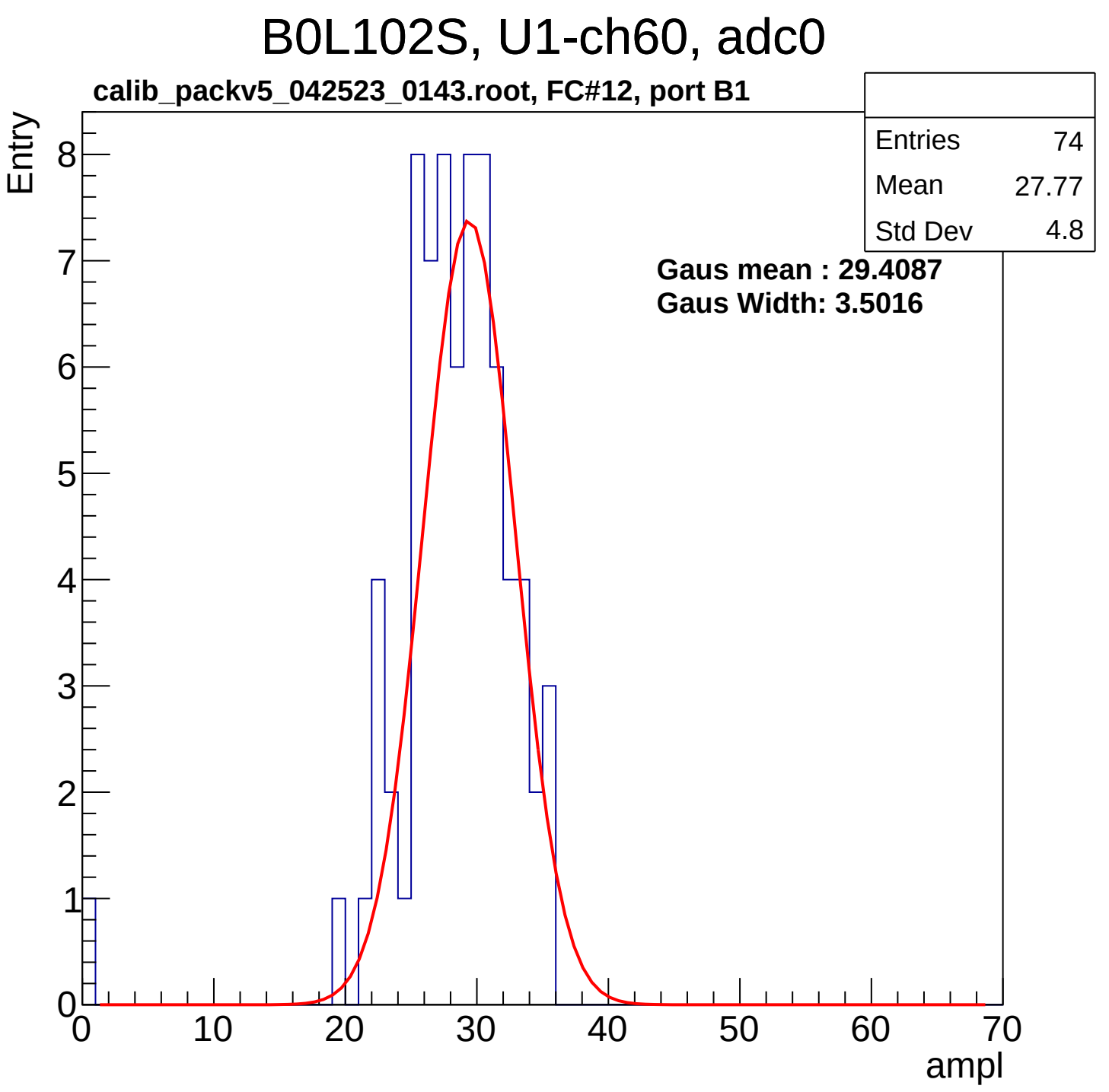
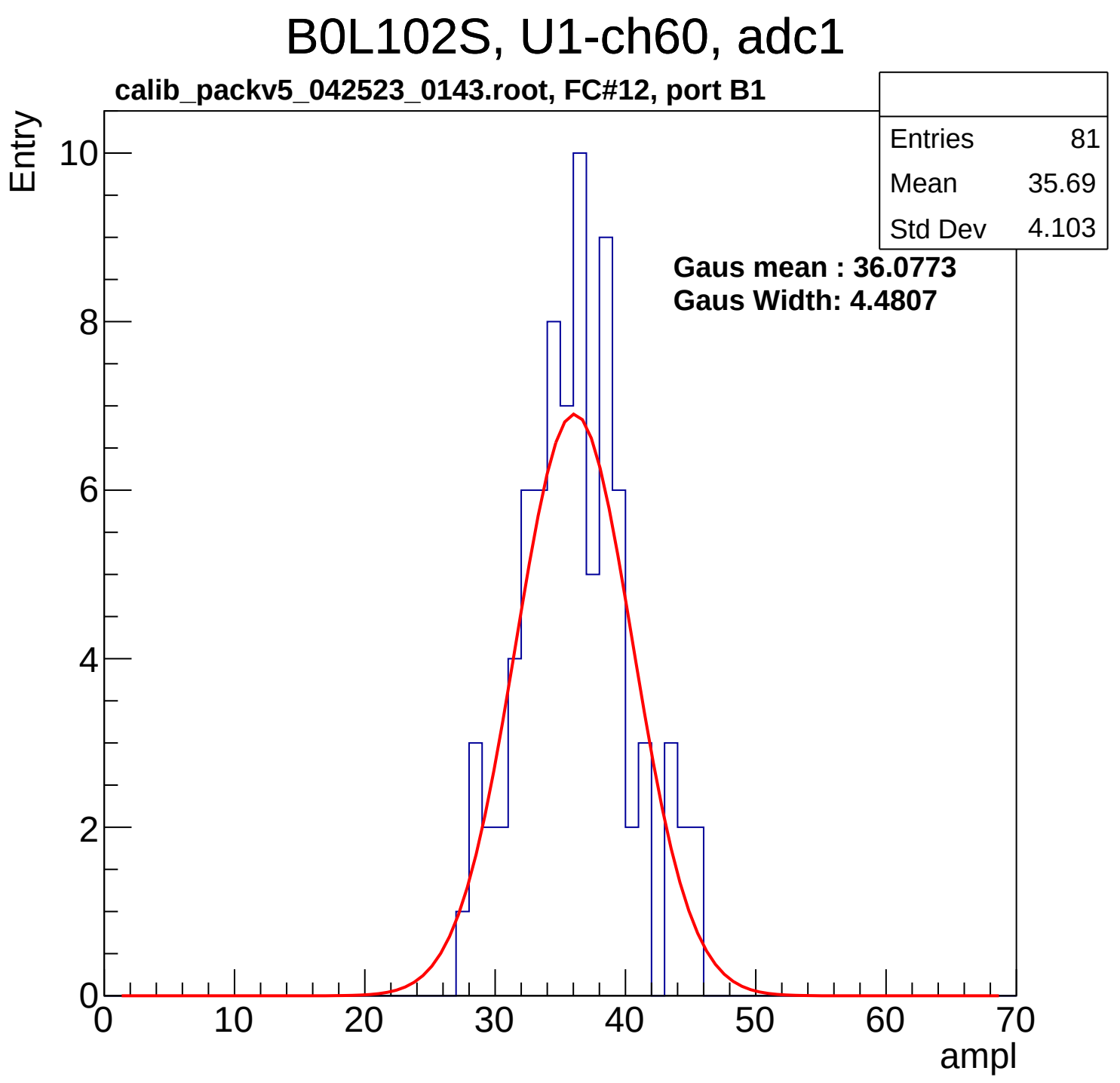
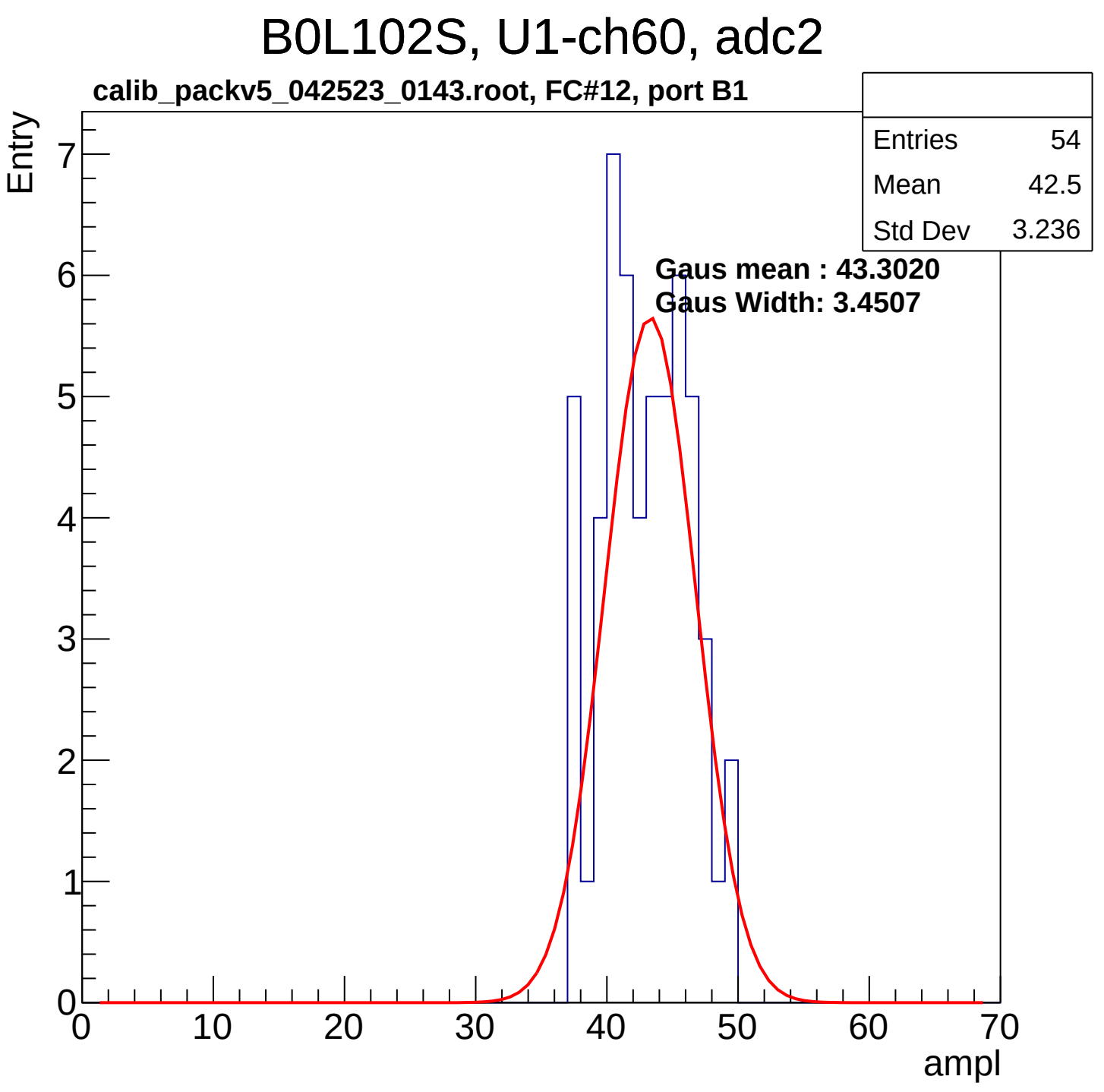
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U1-ch60, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 27.77
Std Dev 4.8
Gaus mean : 29.4087
Gaus Width: 3.5016
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch60, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 35.69
Std Dev 4.103
Gaus mean : 36.0773
Gaus Width: 4.4807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch60, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 42.5
Std Dev 3.236
Gaus mean : 43.3020
Gaus Width: 3.4507
Entry
ampl
0
1
2
3
4
5
6
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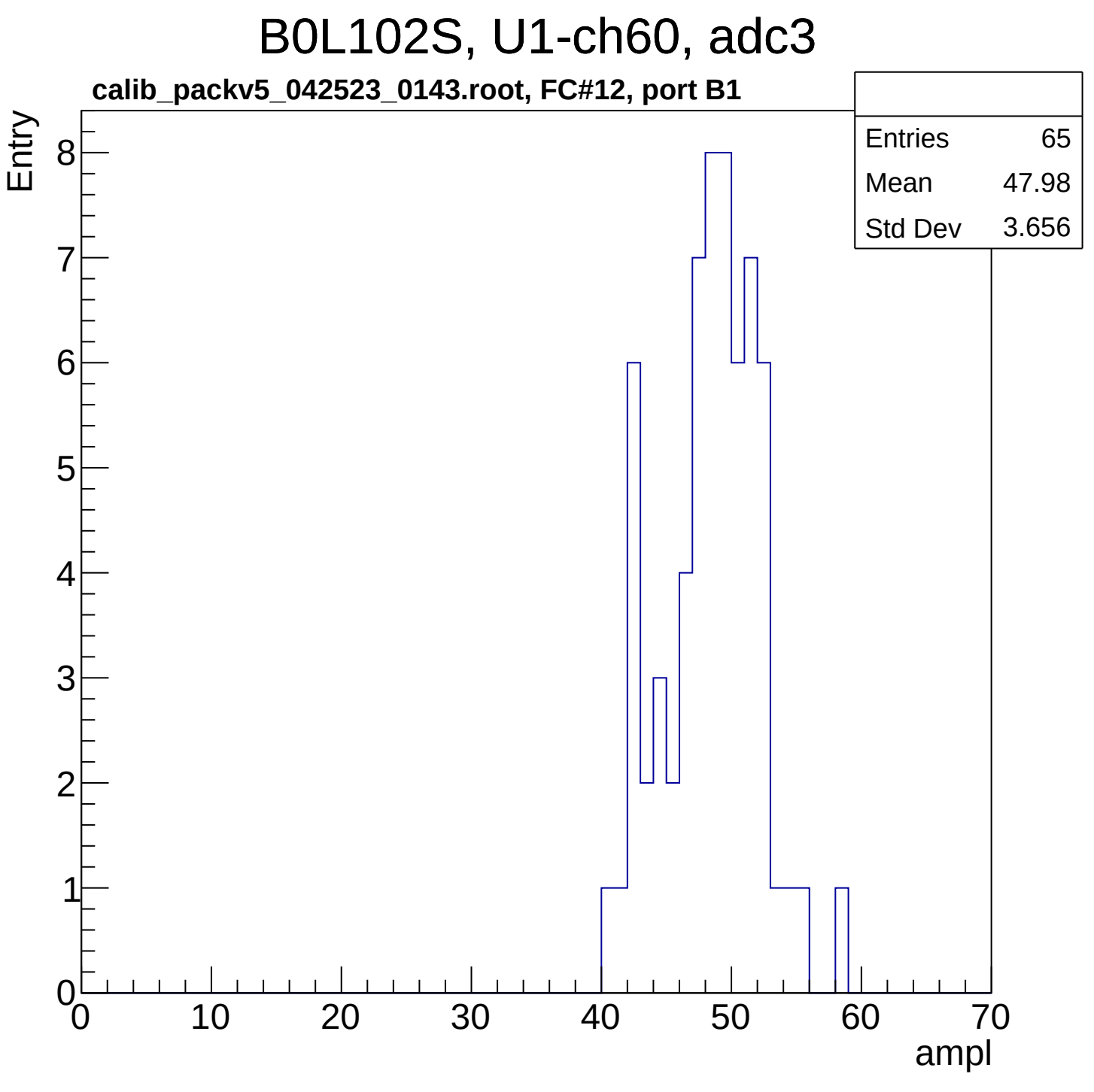
0
10
20
30
40
50
60
70


B0L102S, U1-ch60, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 47.98
Std Dev 3.656
Entry
ampl
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1
2
3
4
5
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7
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0
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40
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60
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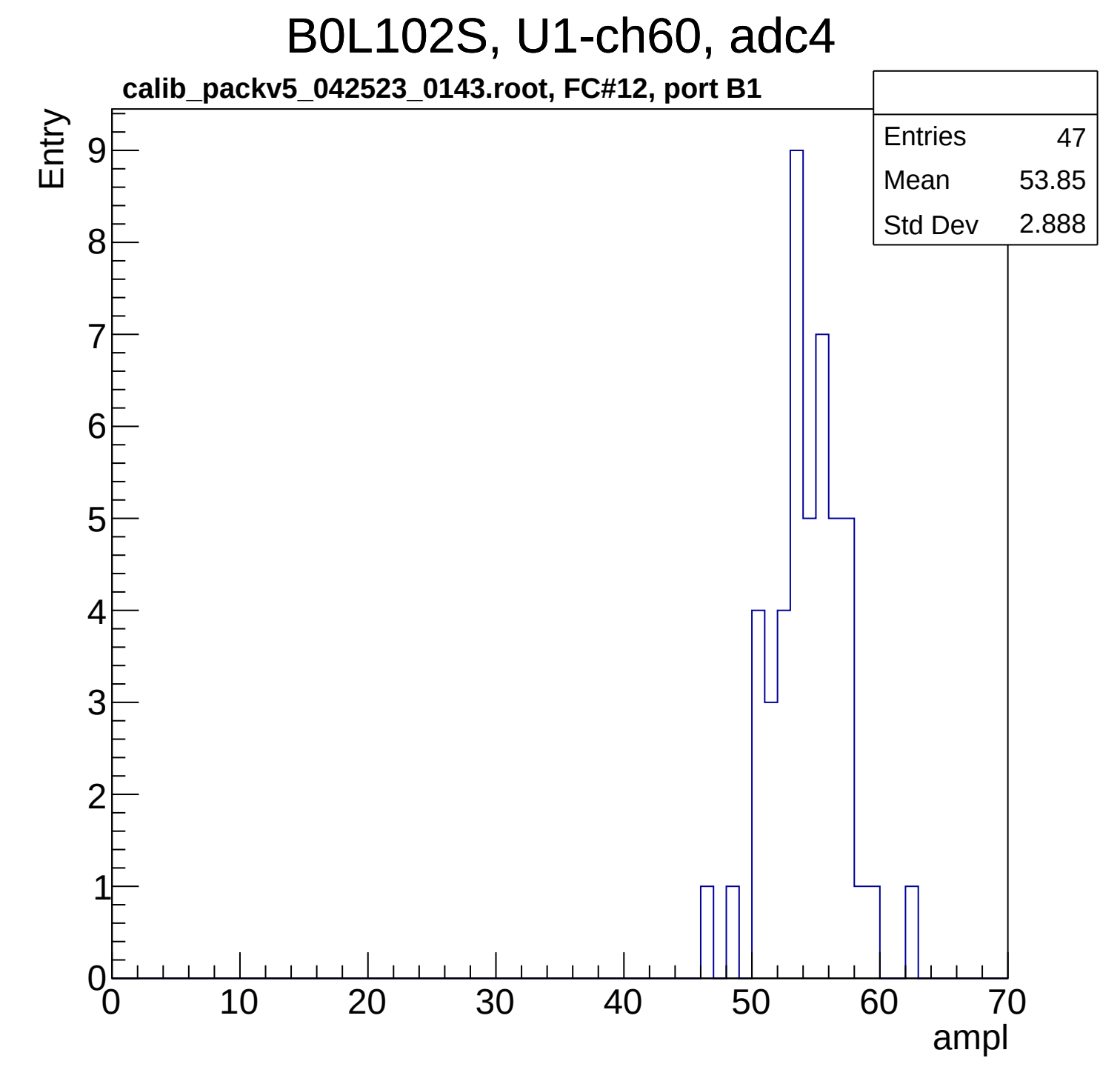
B0L102S, U1-ch60, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 53.85
Std Dev 2.888
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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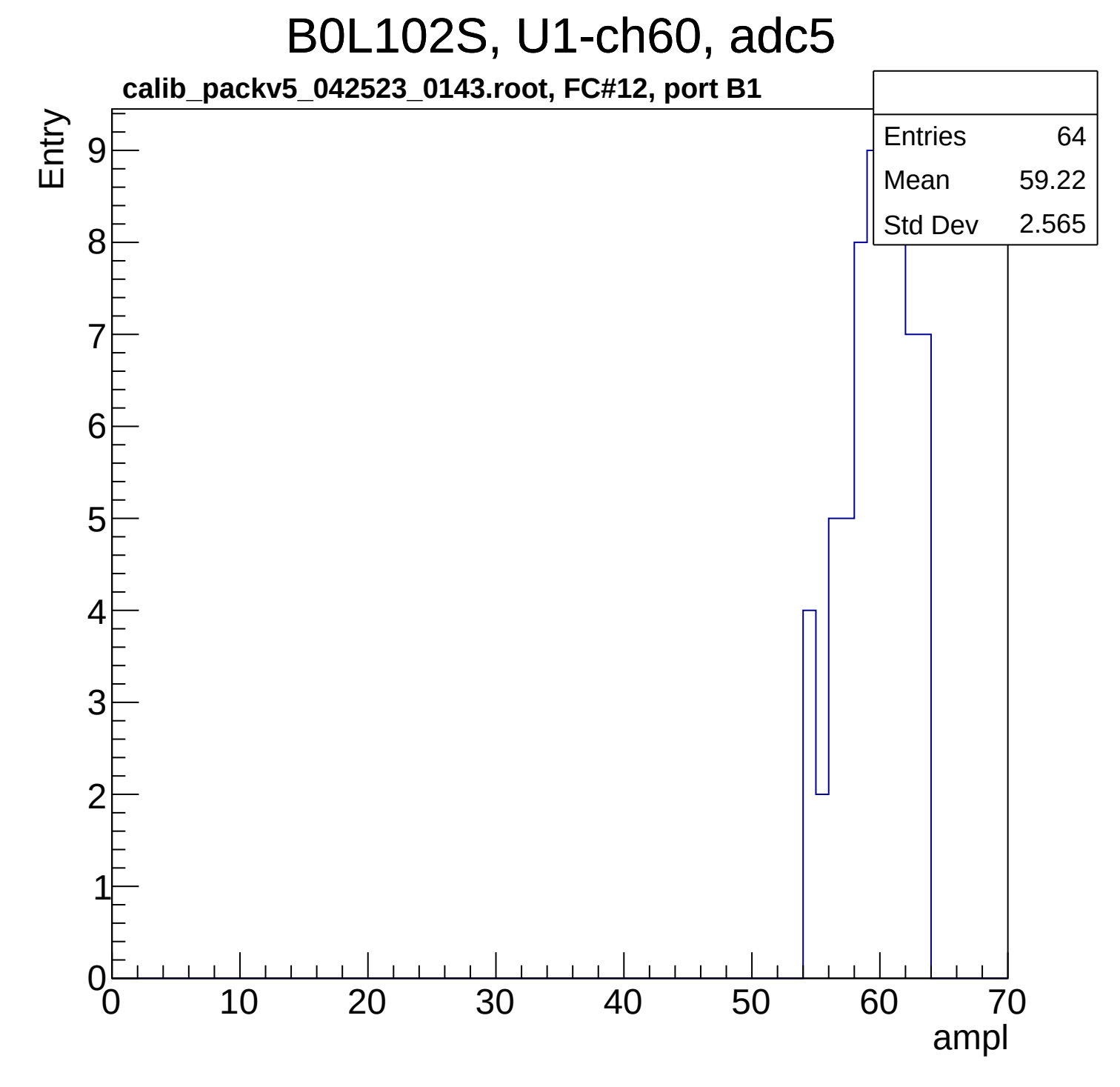
B0L102S, U1-ch60, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 59.22
Std Dev 2.565
Entry
ampl
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4
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6
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60
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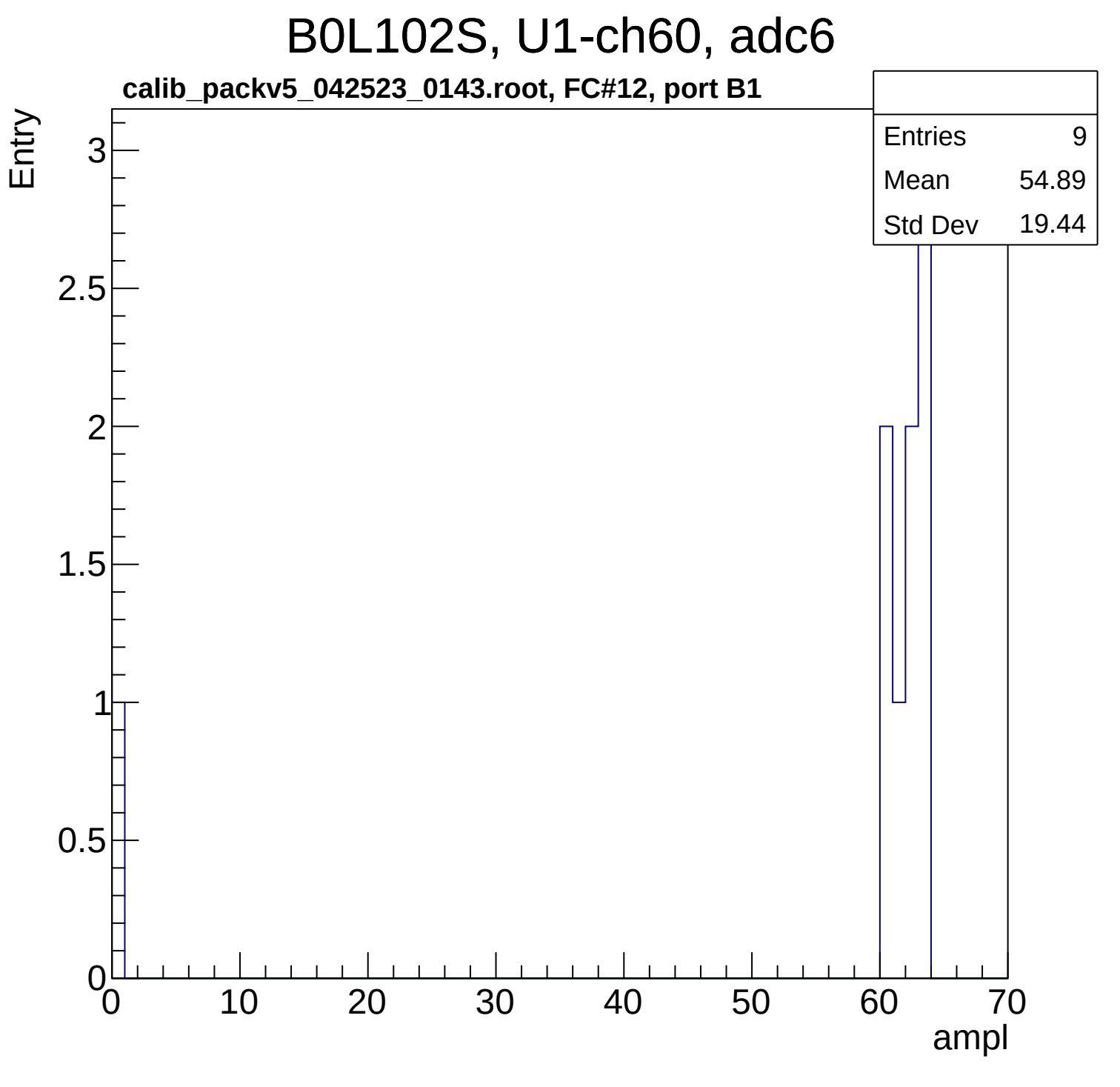
B0L102S, U1-ch60, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
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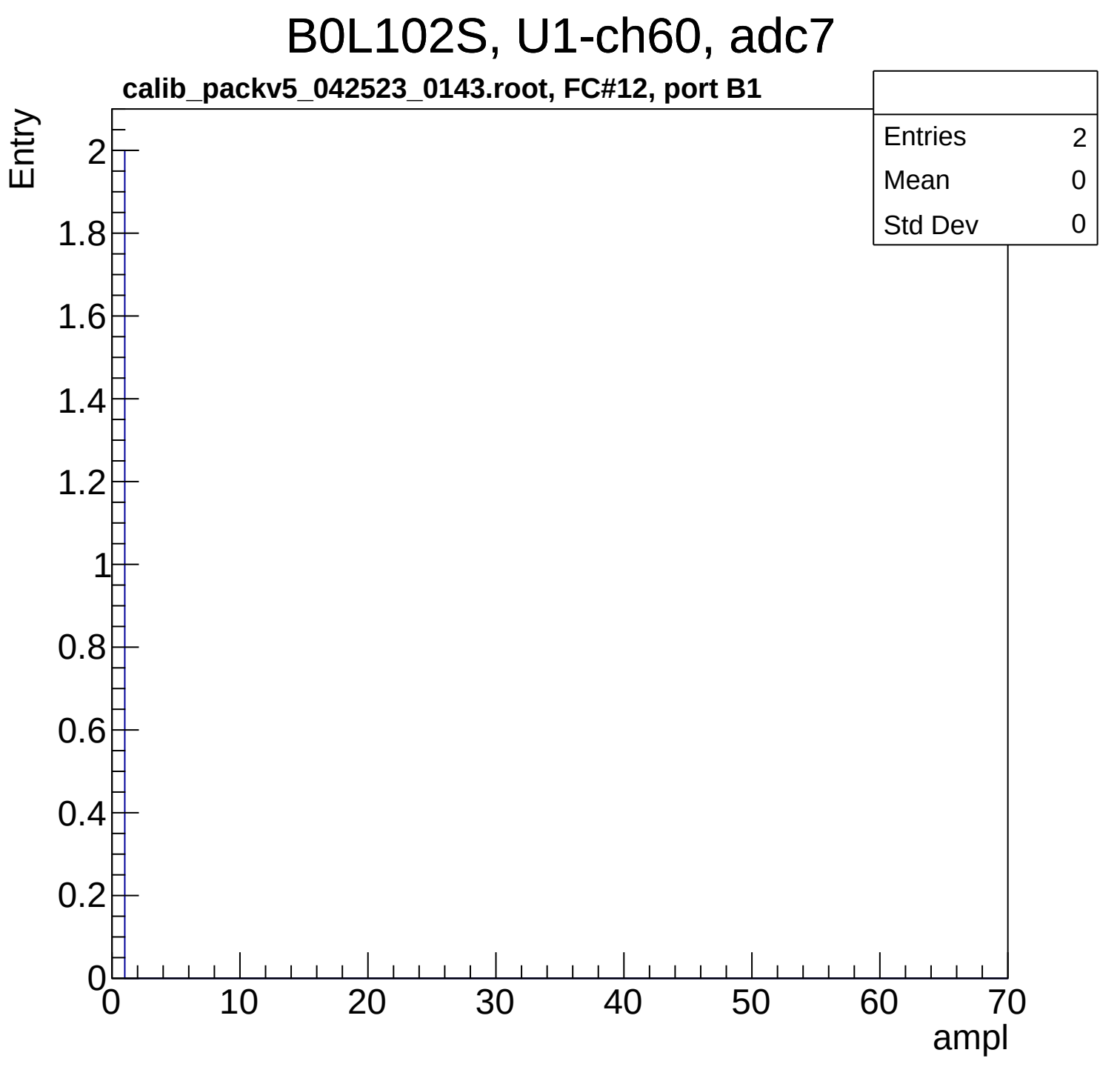
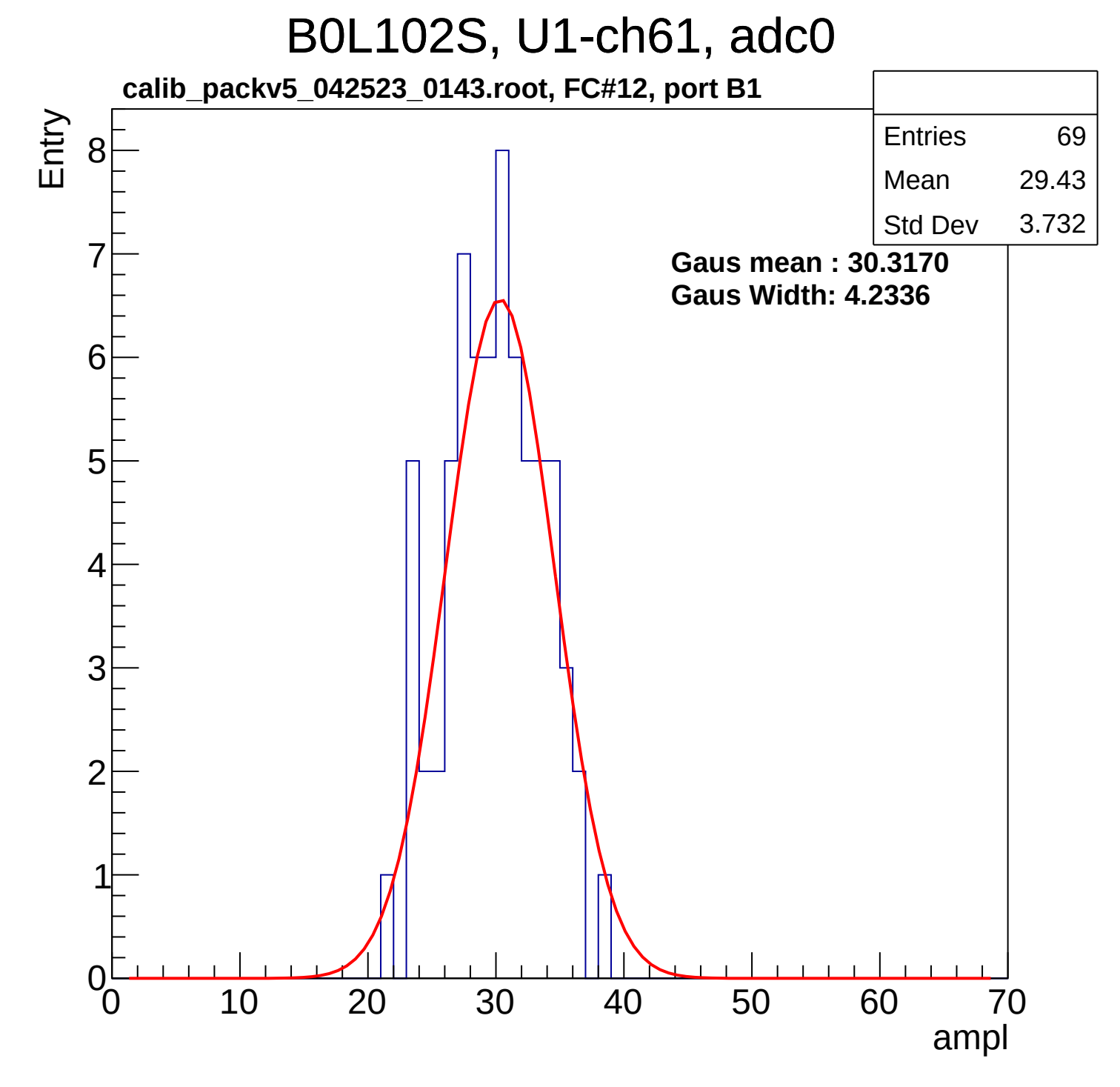
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch60, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch61, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 29.43
Std Dev 3.732
Gaus mean : 30.3170
Gaus Width: 4.2336
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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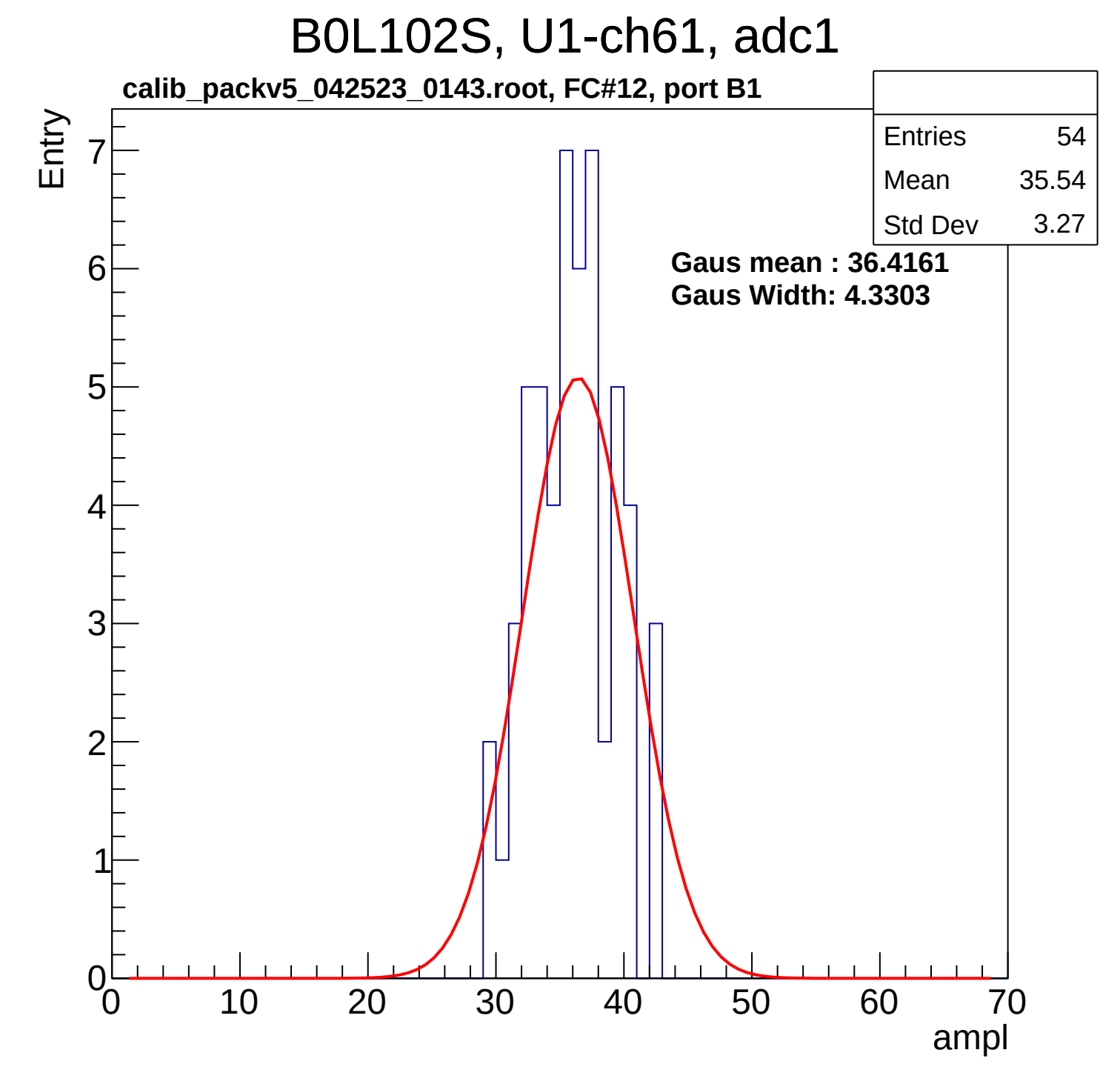
B0L102S, U1-ch61, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 35.54
Std Dev 3.27
Gaus mean : 36.4161
Gaus Width: 4.3303
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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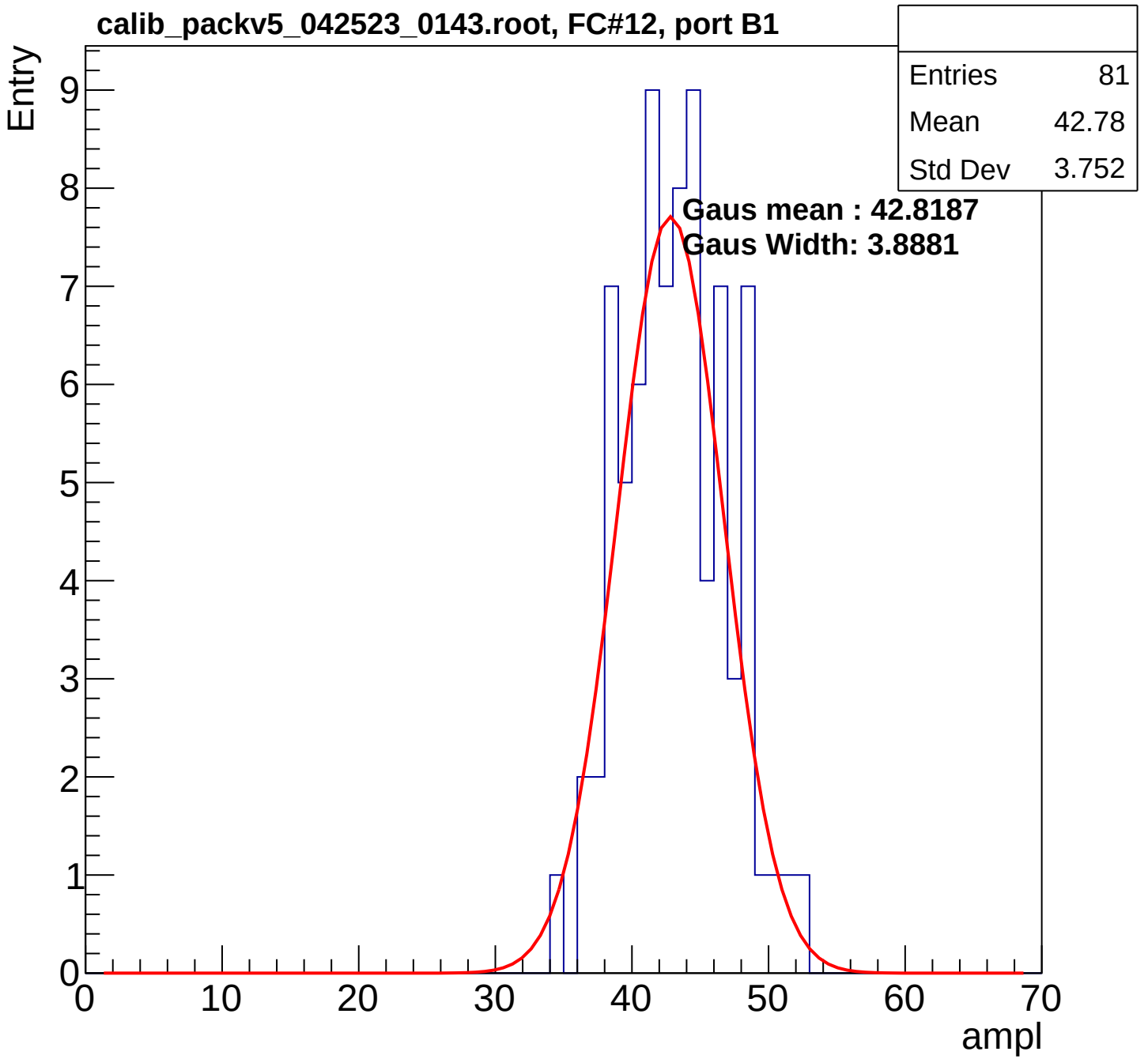
B0L102S, U1-ch61, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 42.78
Std Dev 3.752
Gaus mean : 42.8187
Gaus Width: 3.8881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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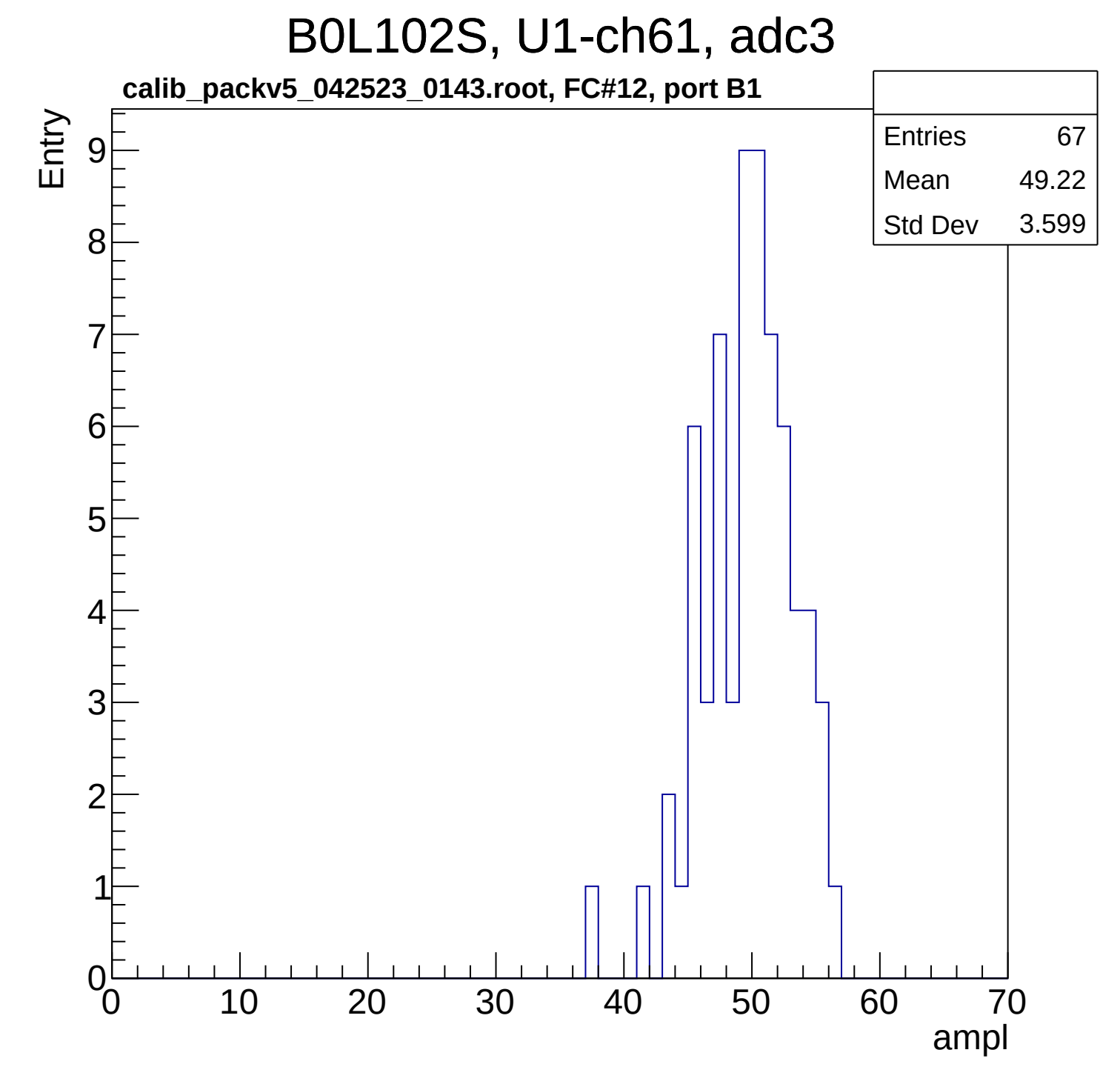
B0L102S, U1-ch61, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 49.22
Std Dev 3.599
Entry
ampl
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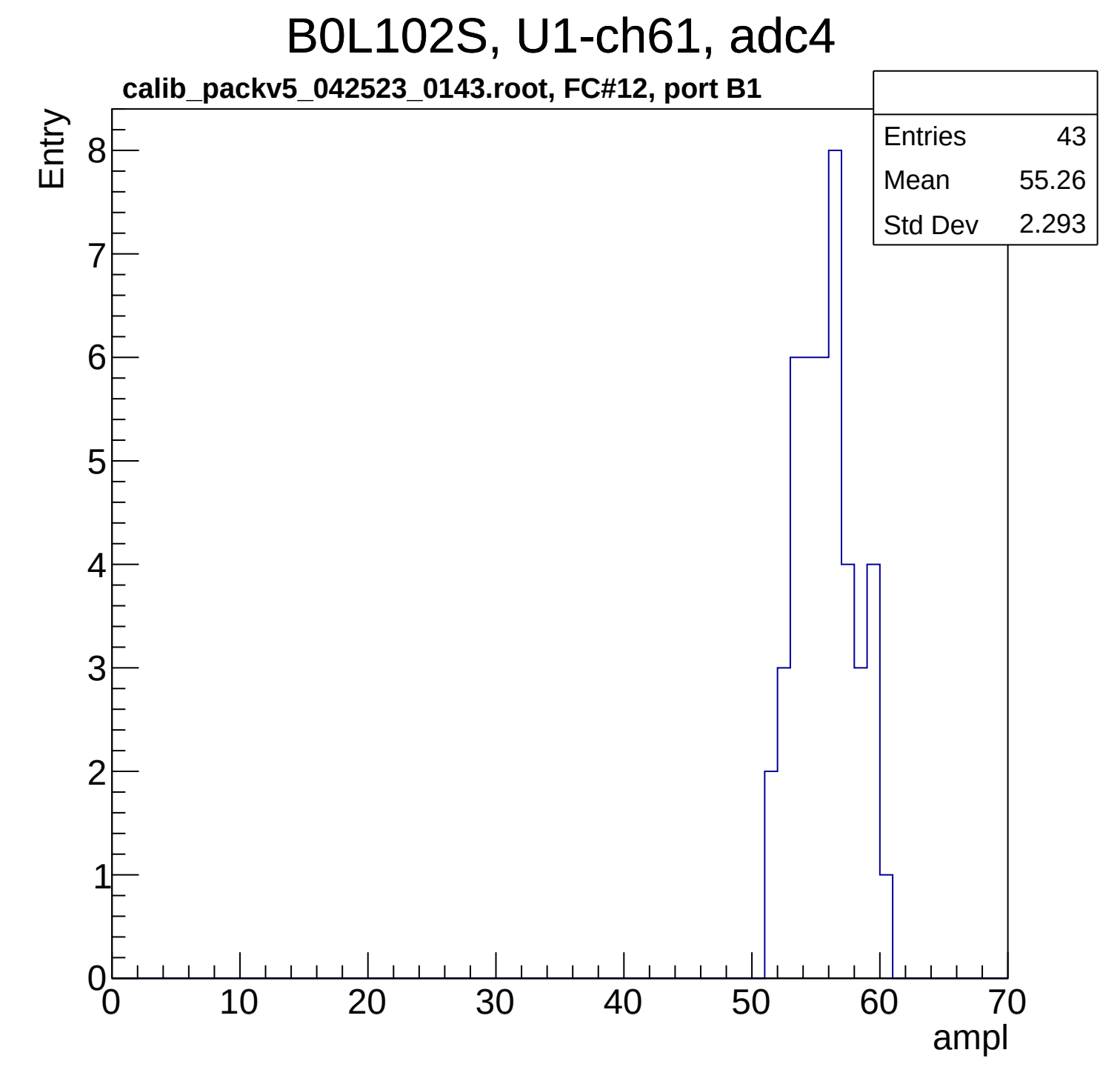
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch61, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.26
Std Dev 2.293
Entry
ampl
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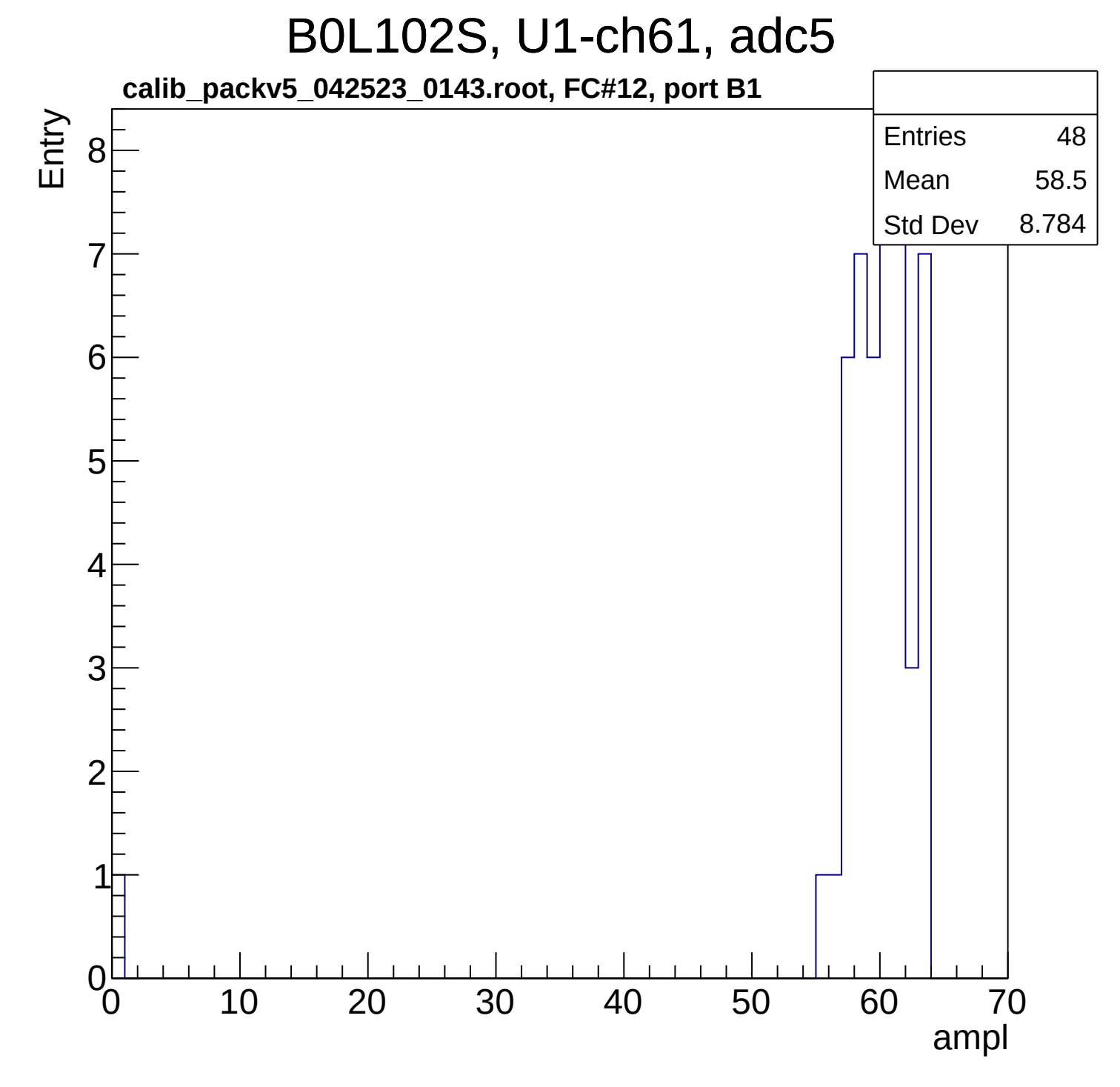
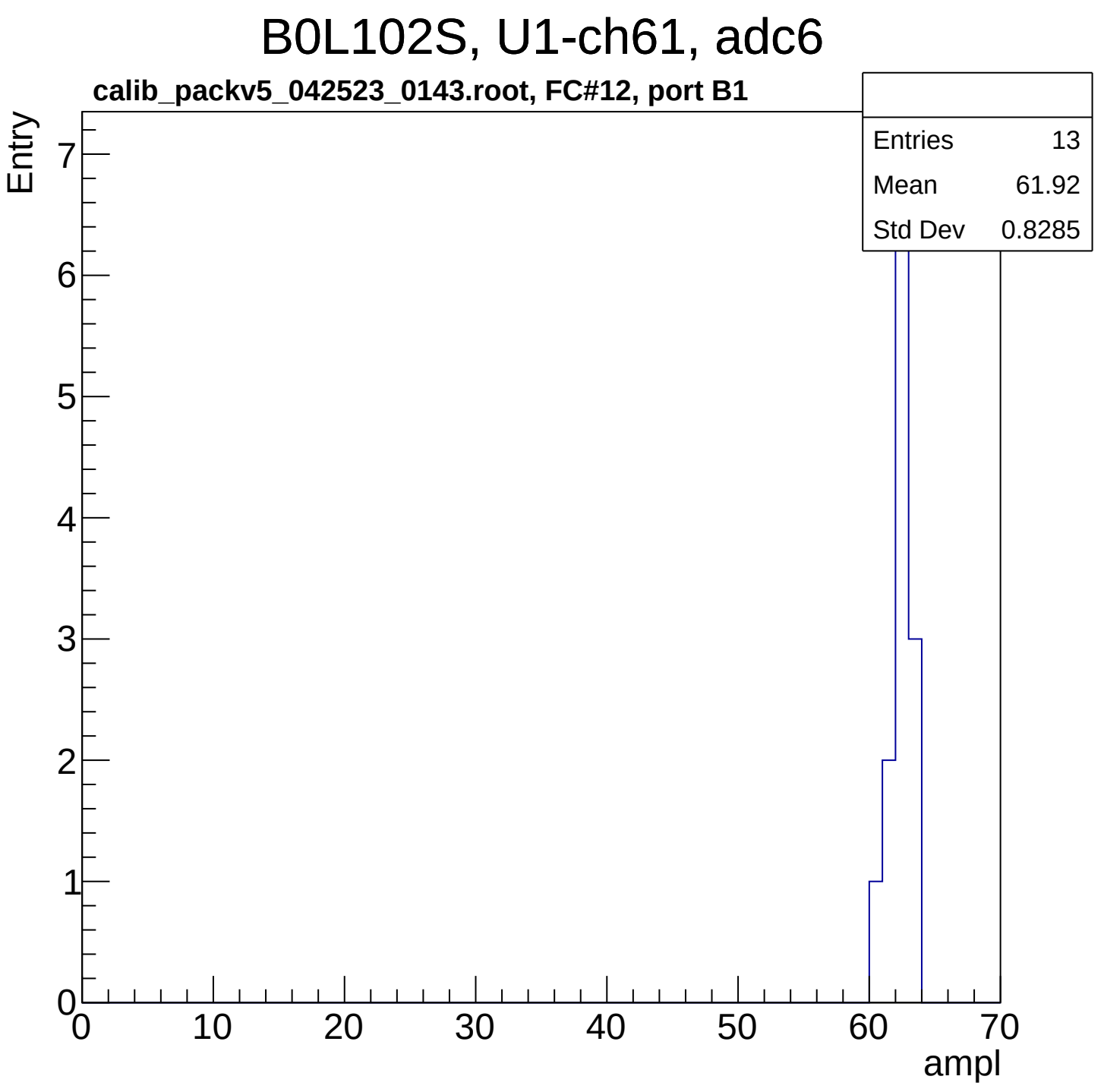
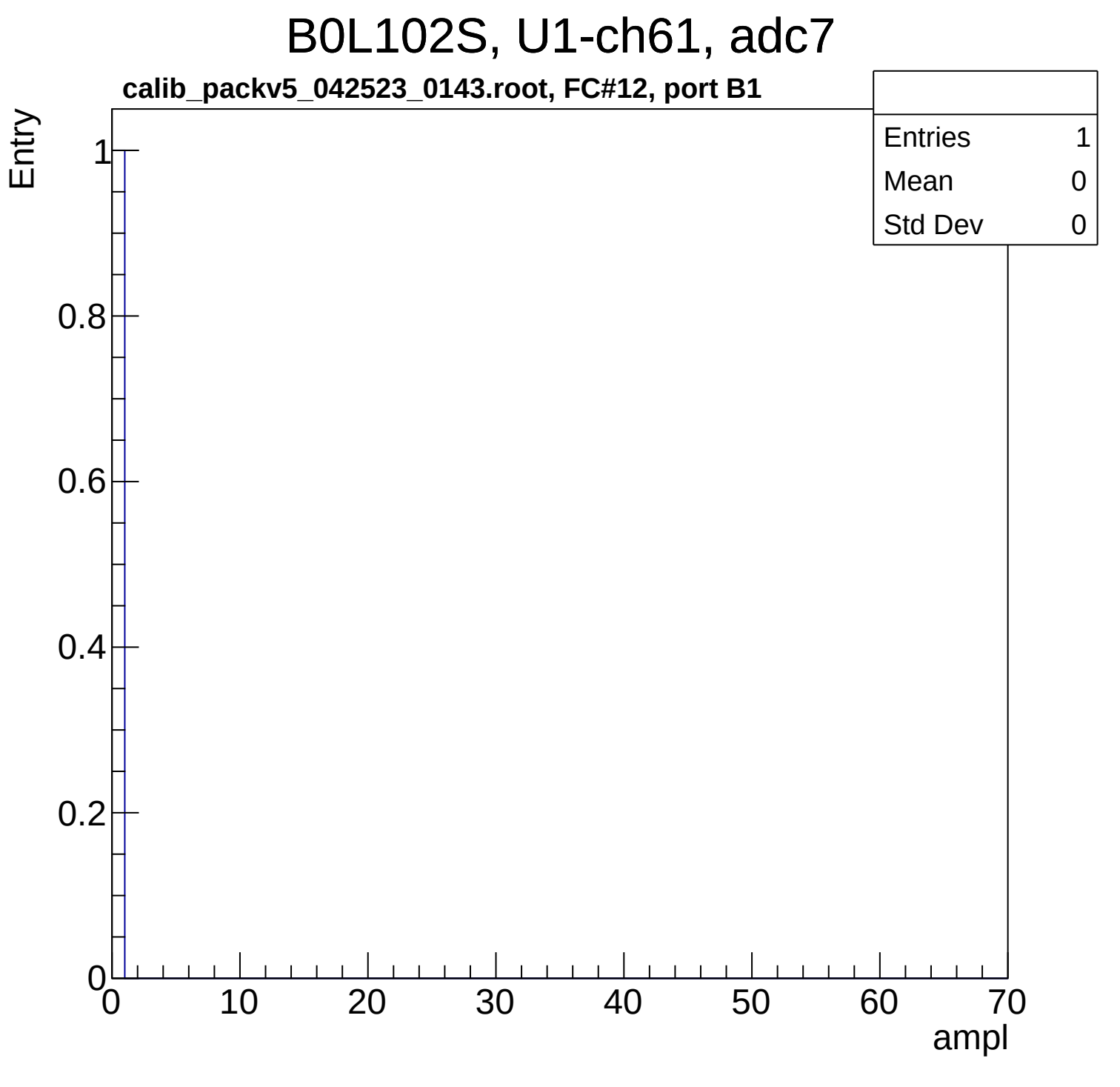
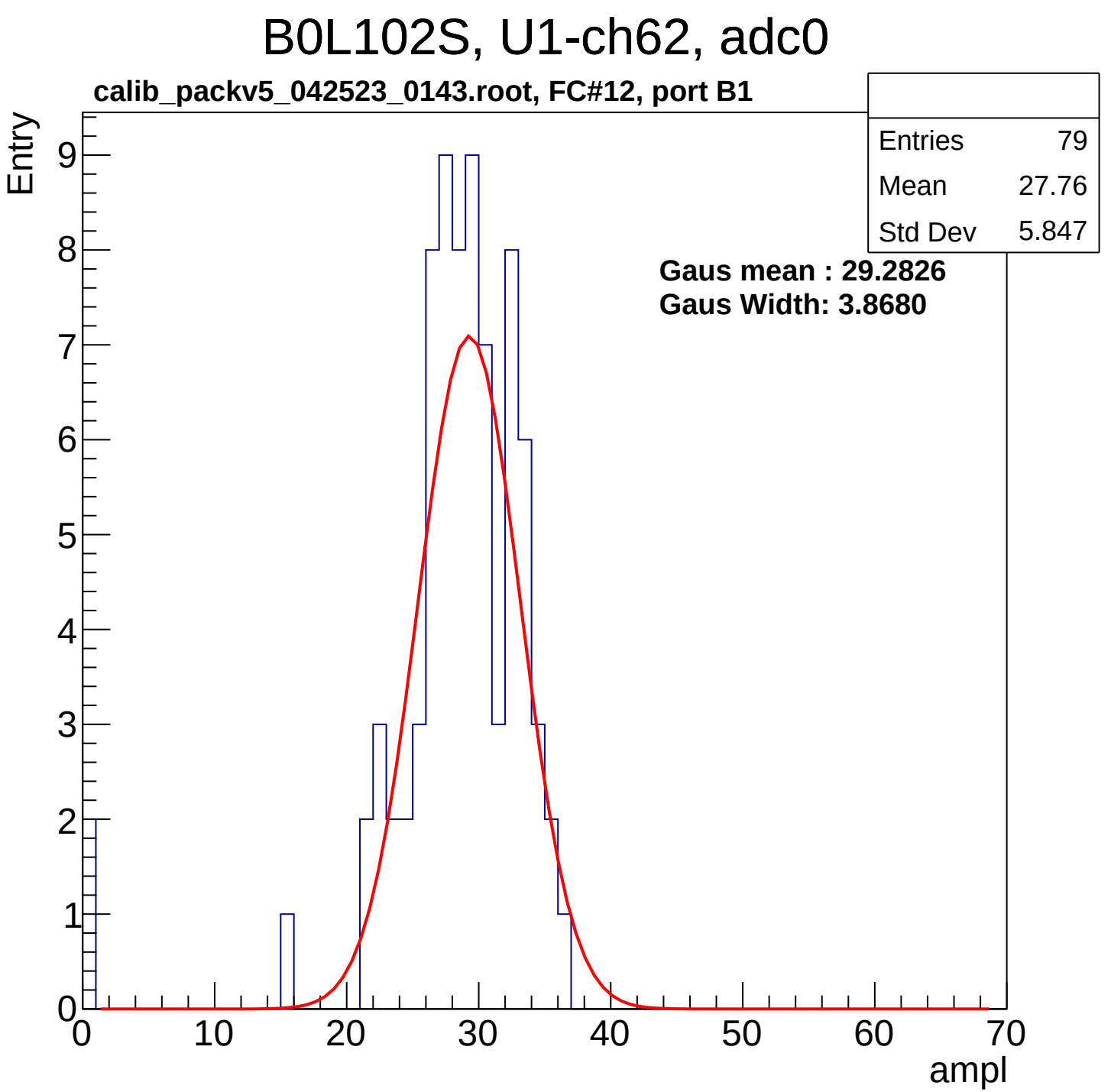
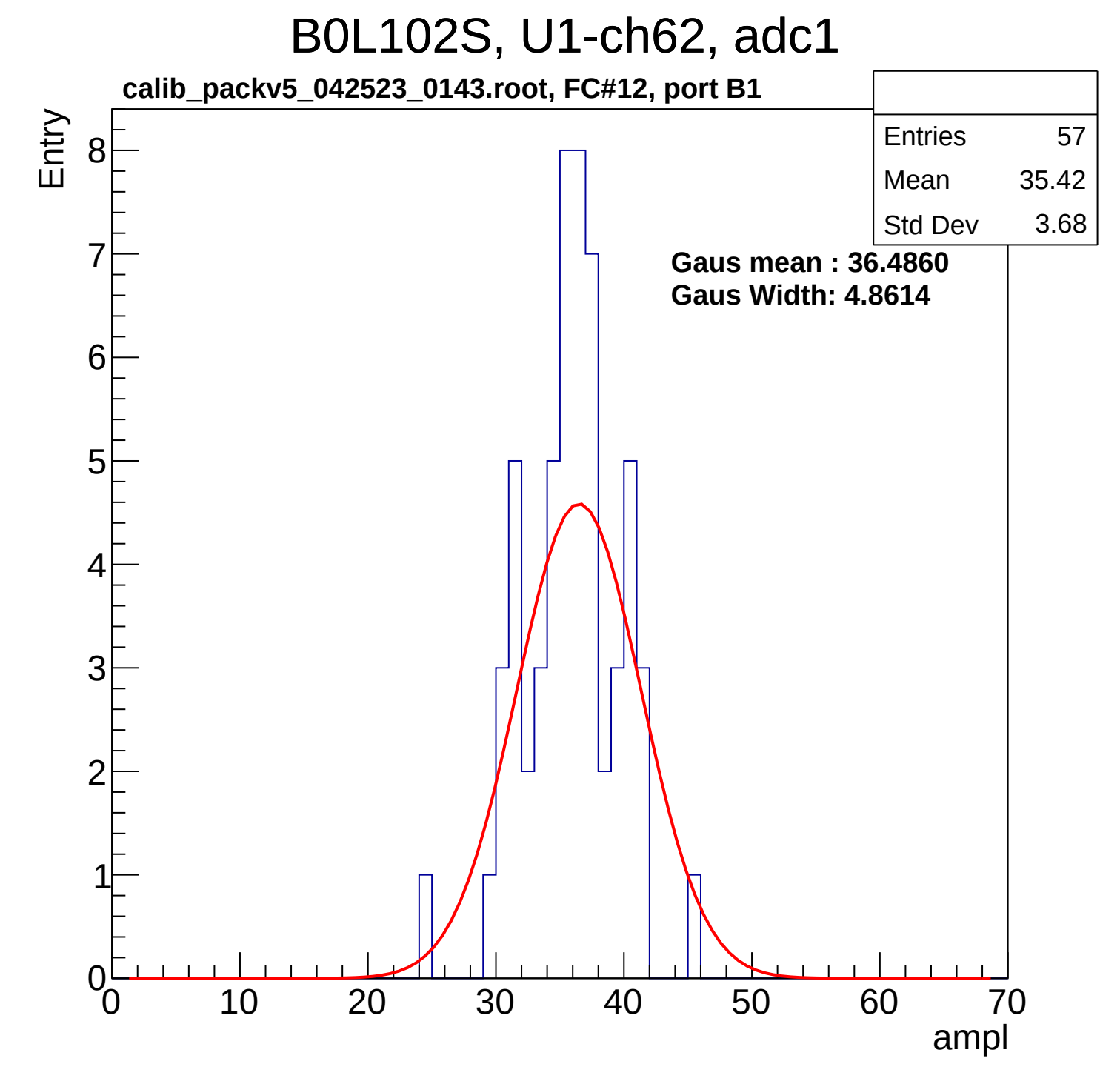
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch61, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.5
Std Dev 8.784
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch61, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 61.92
Std Dev 0.8285
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch61, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch62, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 27.76
Std Dev 5.847
Gaus mean : 29.2826
Gaus Width: 3.8680
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch62, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 35.42
Std Dev 3.68
Gaus mean : 36.4860
Gaus Width: 4.8614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
50
60
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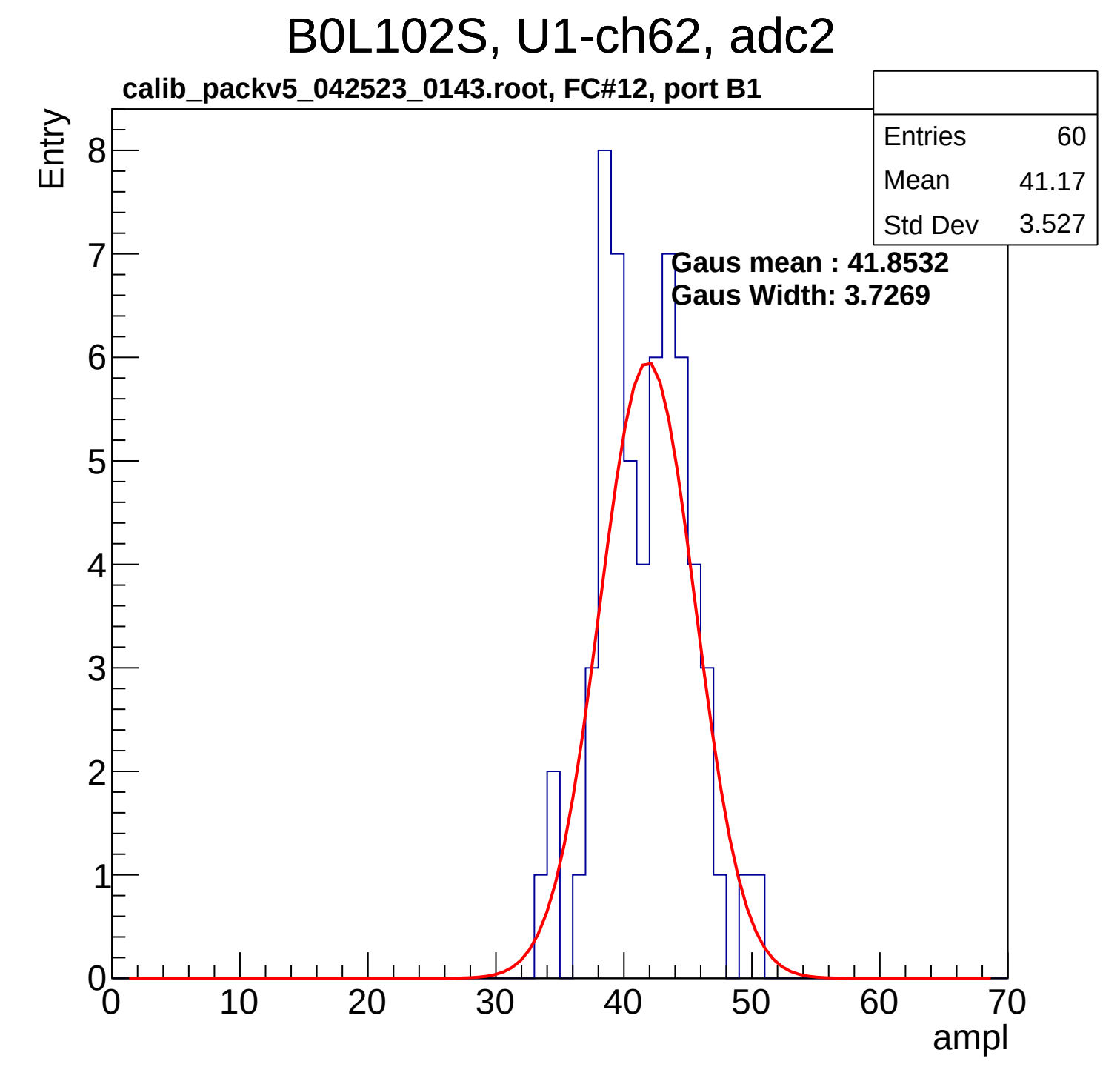
B0L102S, U1-ch62, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 41.17
Std Dev 3.527
Gaus mean : 41.8532
Gaus Width: 3.7269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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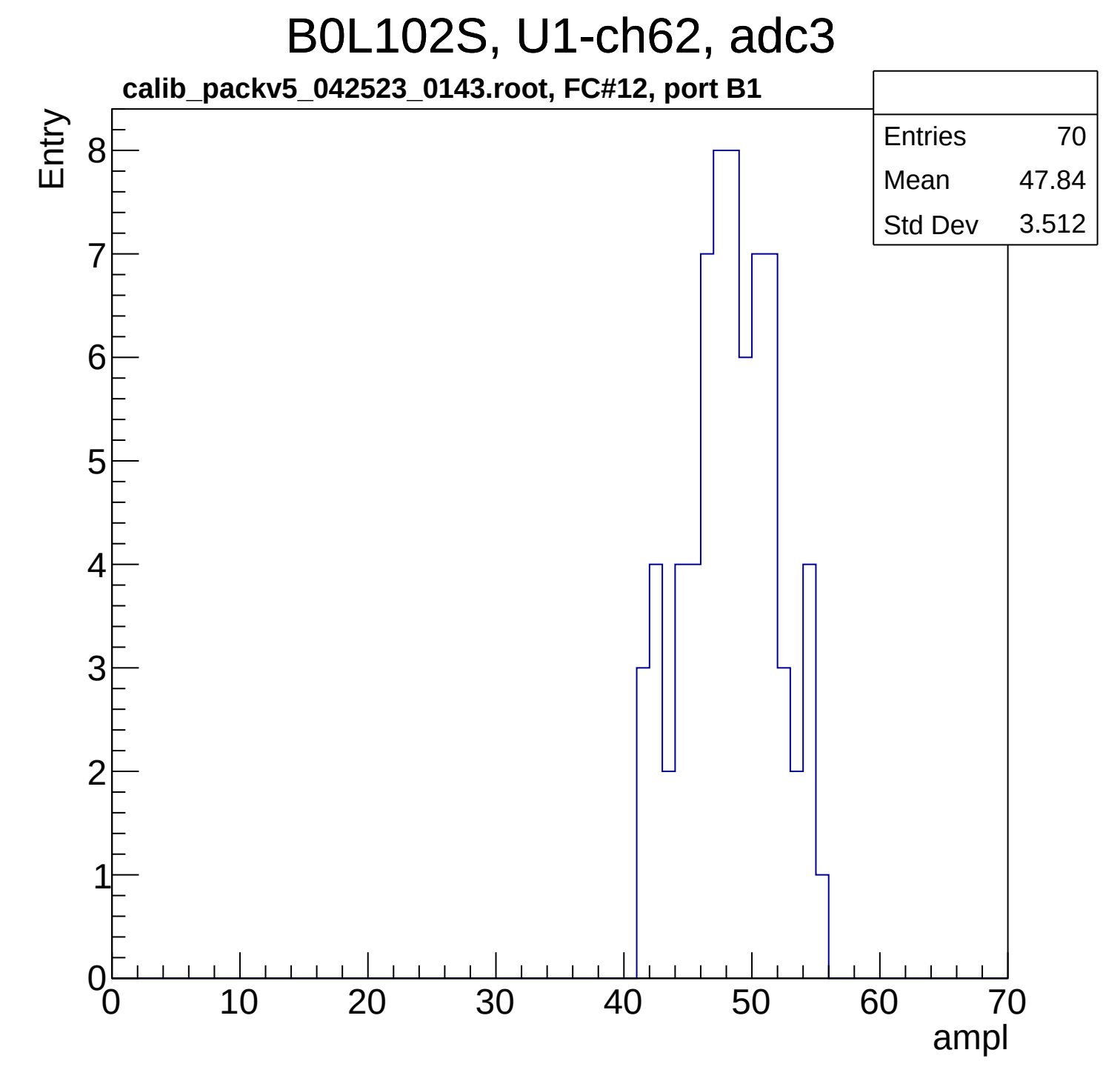
B0L102S, U1-ch62, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 47.84
Std Dev 3.512
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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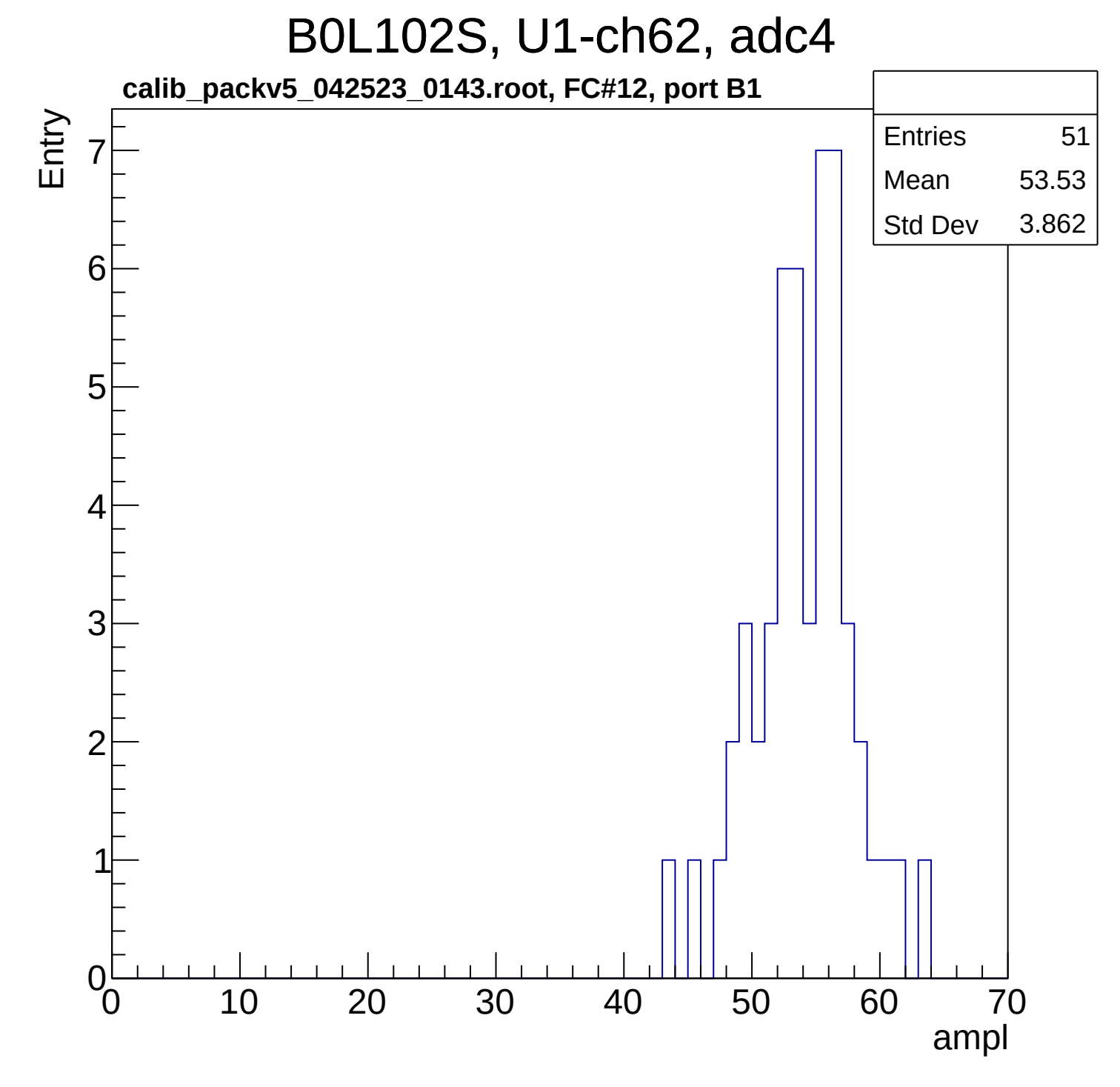
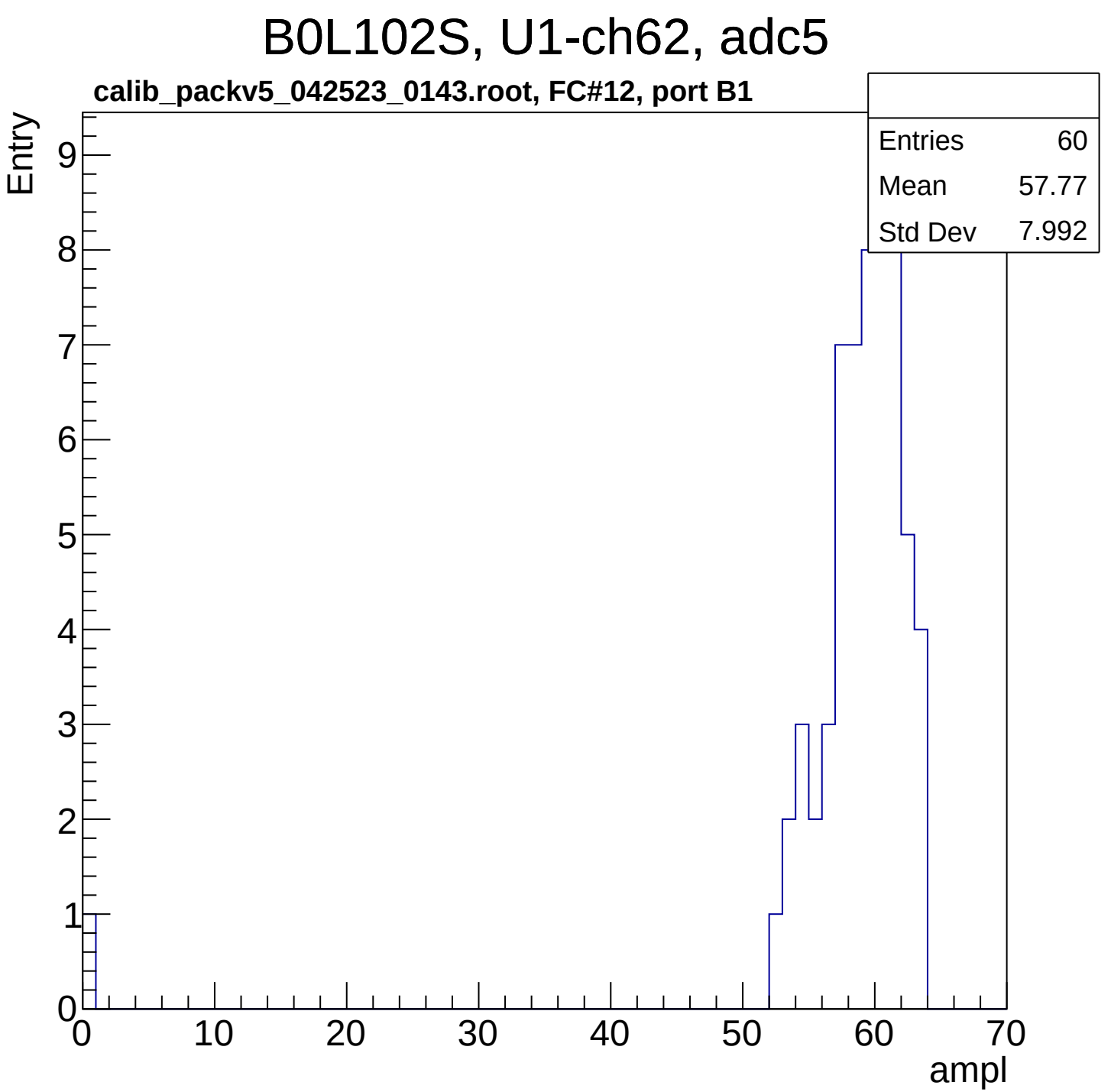
40
50
60
70


B0L102S, U1-ch62, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 53.53
Std Dev 3.862
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch62, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 57.77
Std Dev 7.992
Entry
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3
4
5
6
7
8
9
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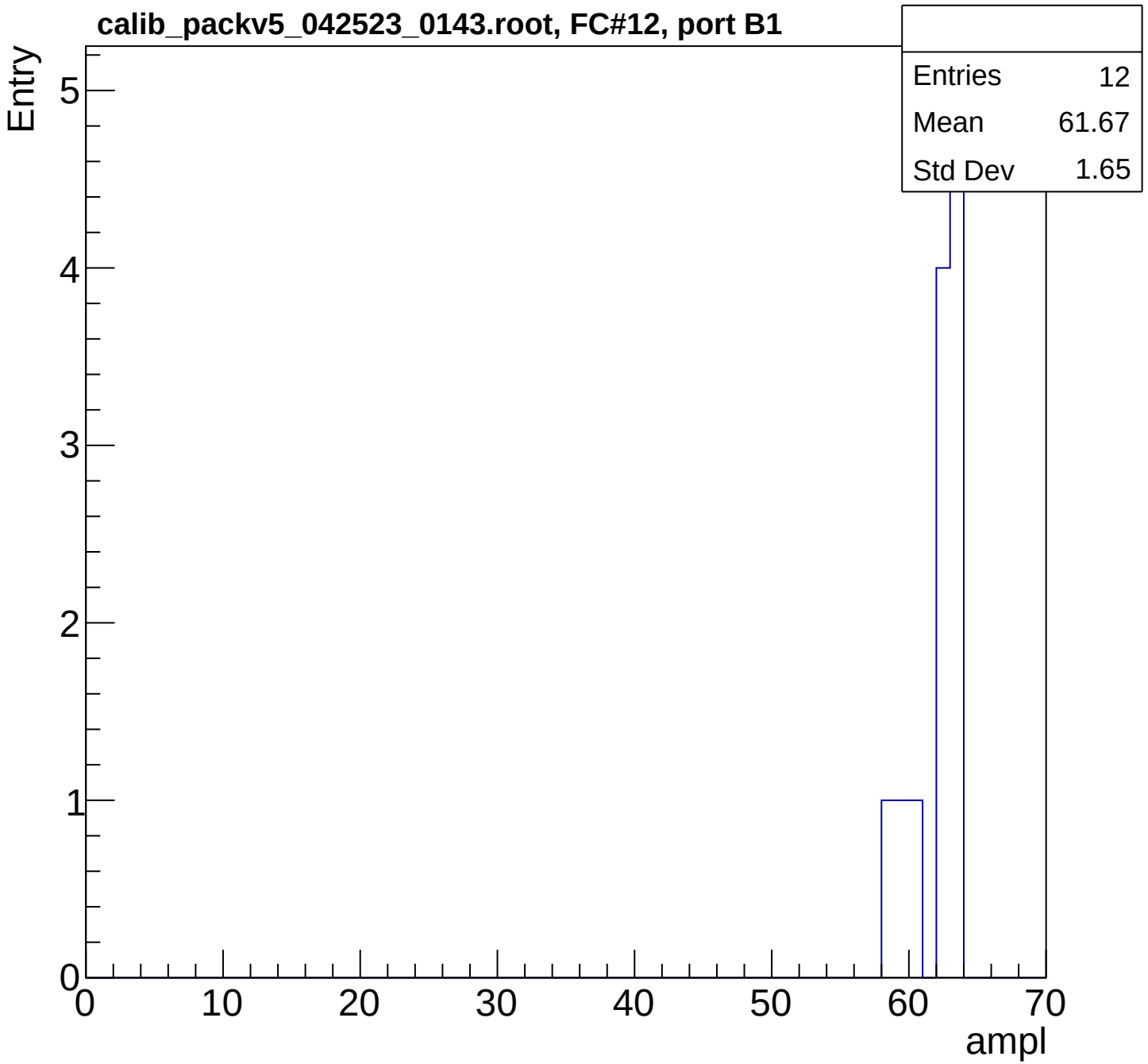
50
60
70
ampl


B0L102S, U1-ch62, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 61.67
Std Dev 1.65
Entry
0
1
2
3
4
5
0
10
20
30
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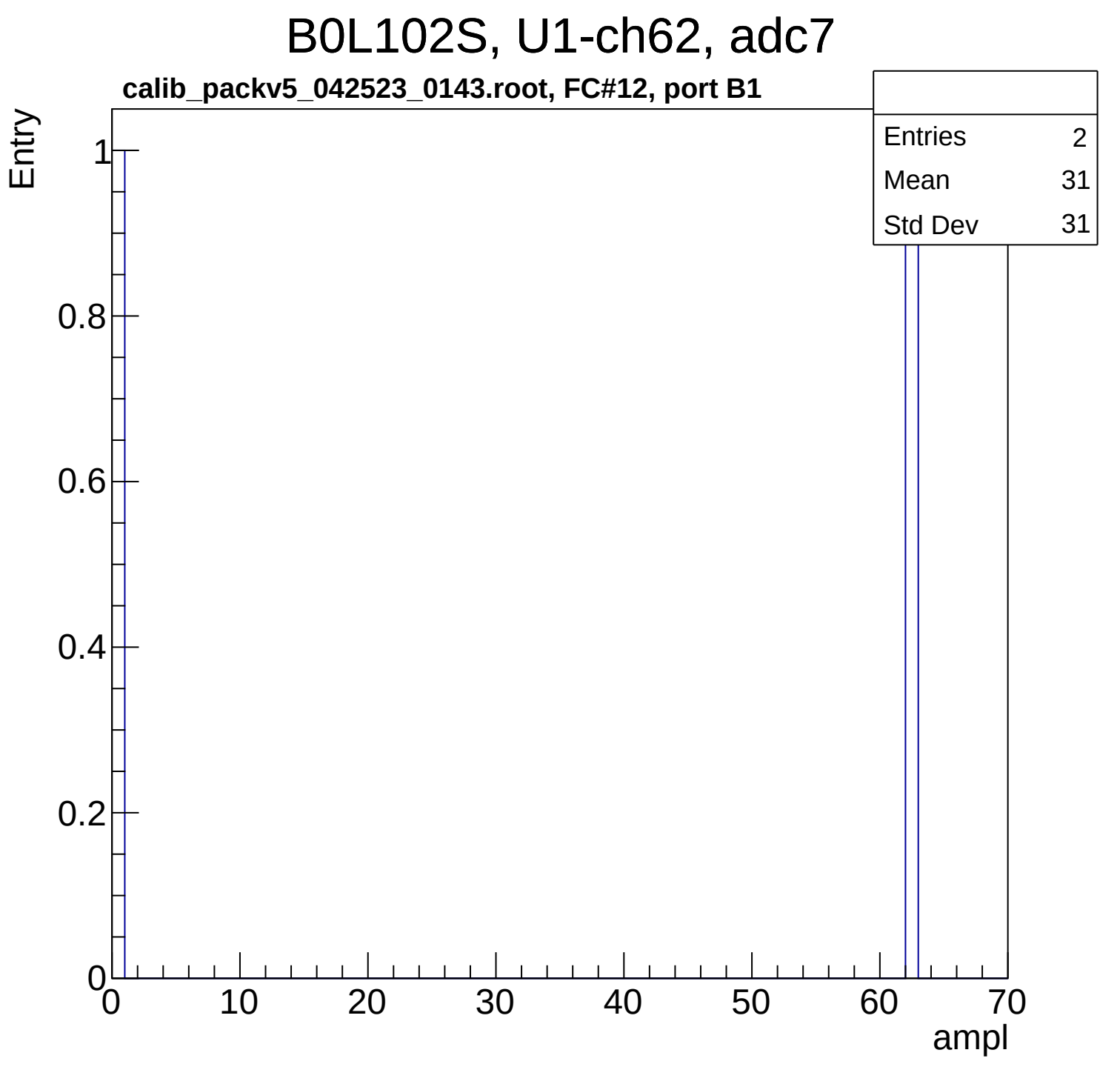
50
60
70
ampl


B0L102S, U1-ch62, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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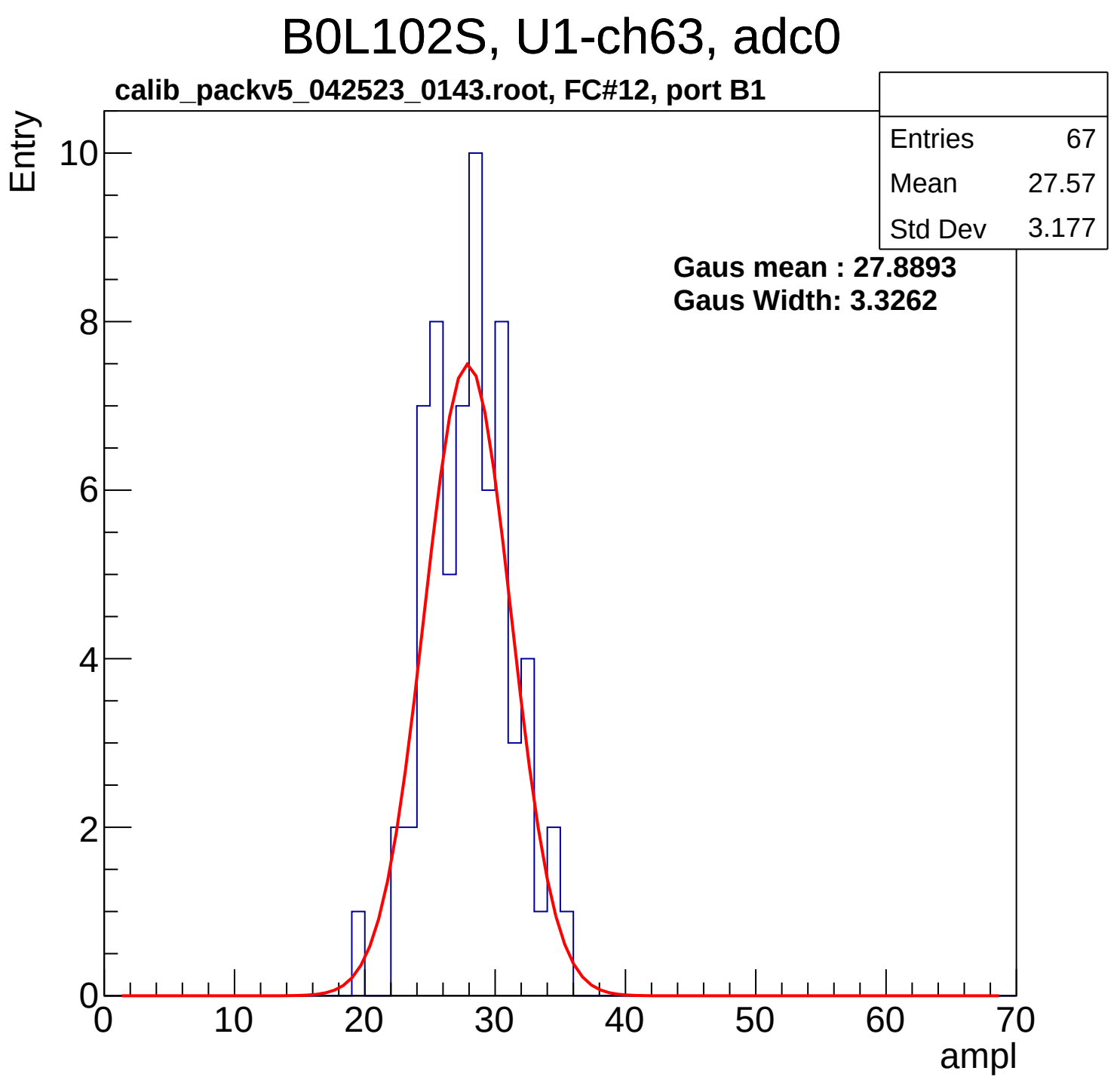
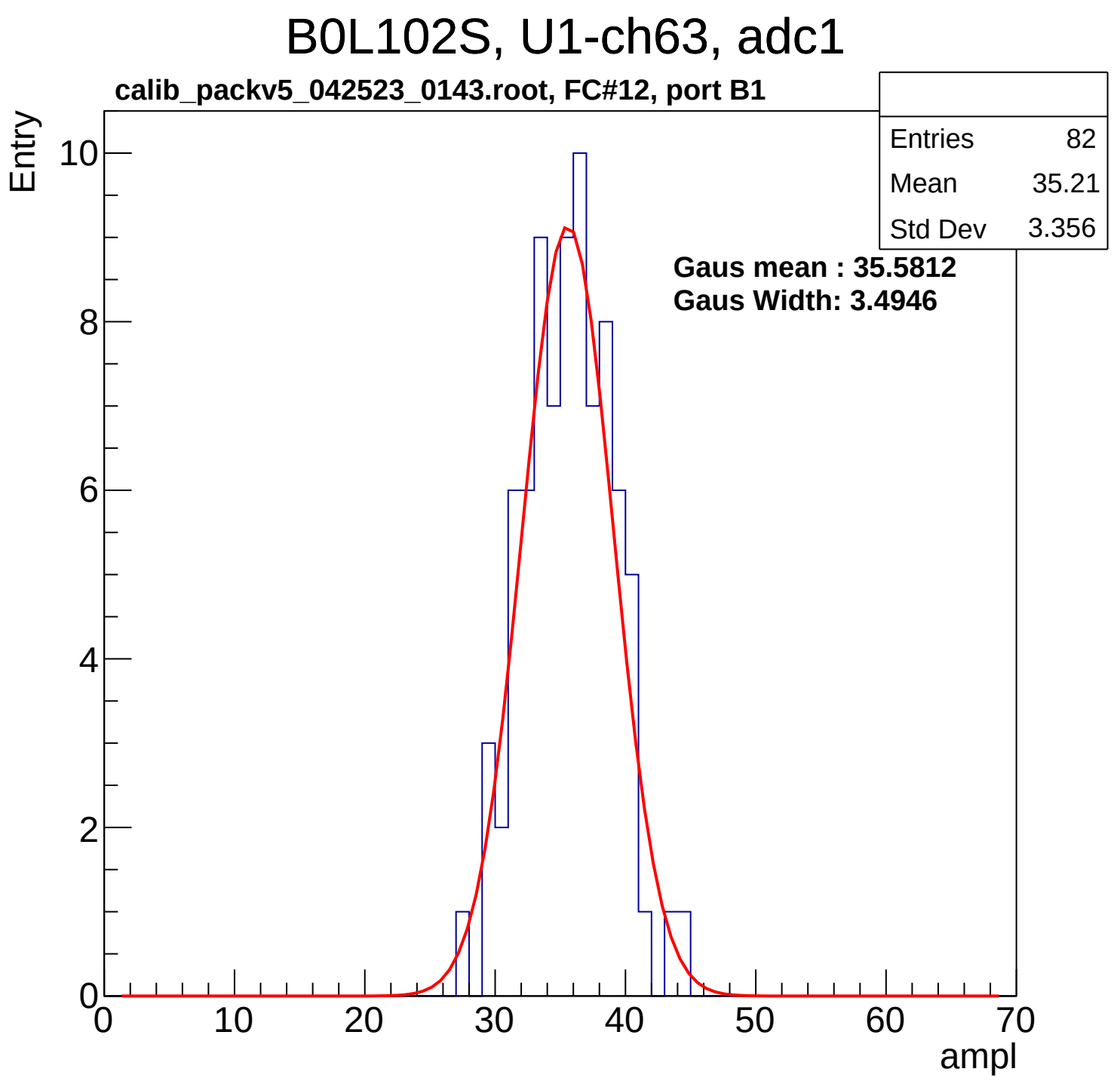
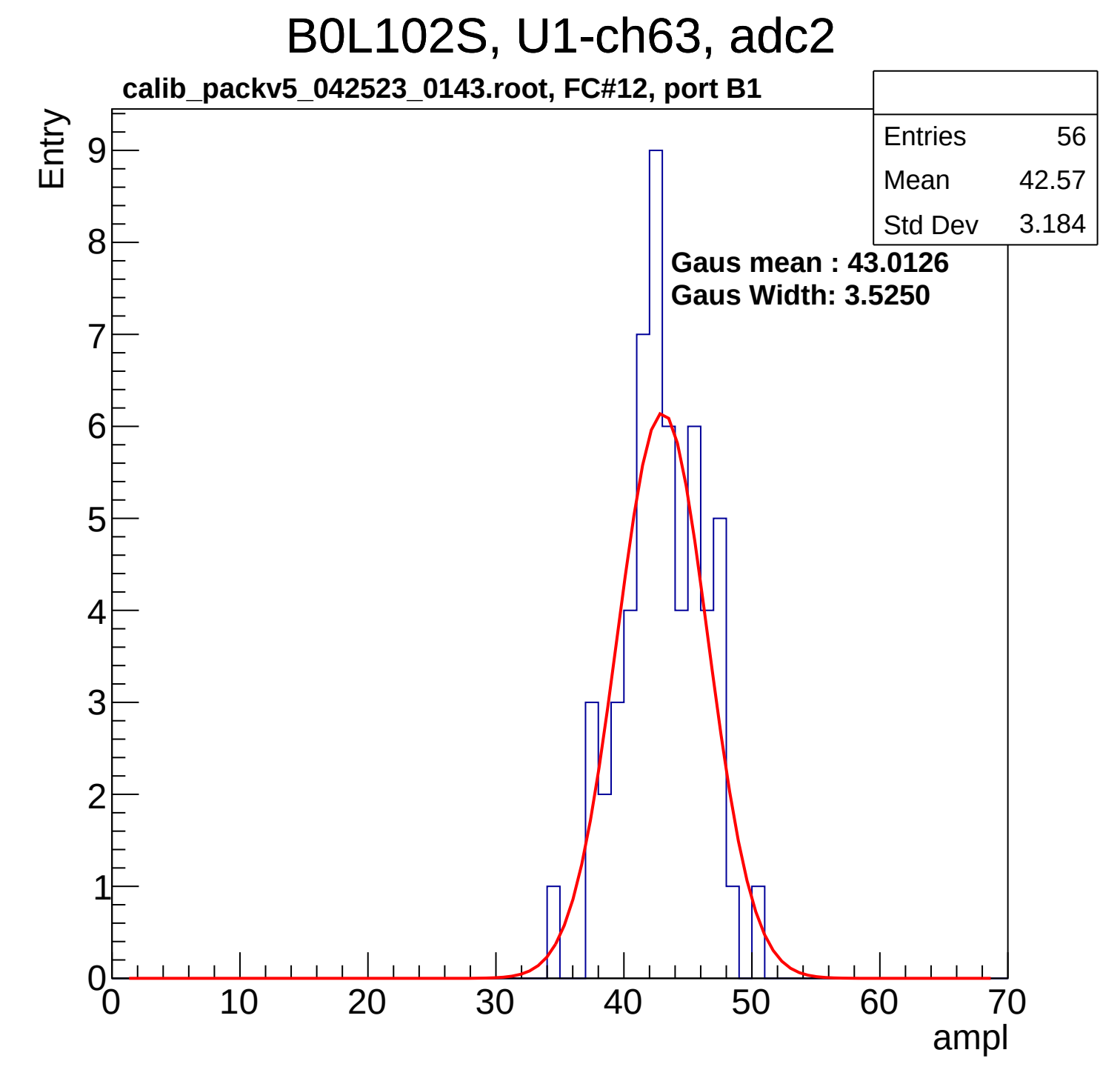
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch63, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 27.57
Std Dev 3.177
Gaus mean : 27.8893
Gaus Width: 3.3262
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch63, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 35.21
Std Dev 3.356
Gaus mean : 35.5812
Gaus Width: 3.4946
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch63, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 42.57
Std Dev 3.184
Gaus mean : 43.0126
Gaus Width: 3.5250
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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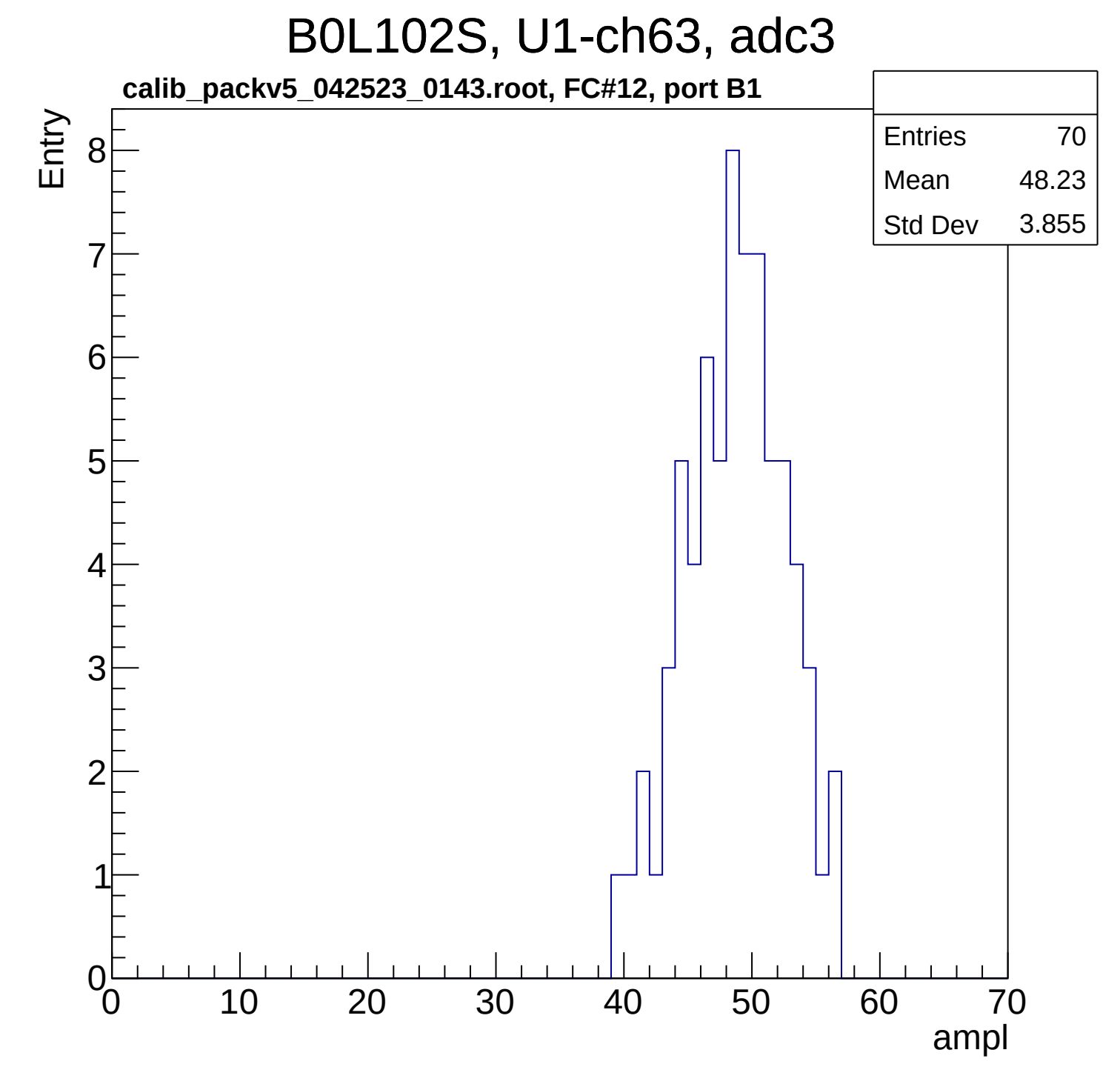
60
70


B0L102S, U1-ch63, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 48.23
Std Dev 3.855
Entry
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2
3
4
5
6
7
8
0
10
20
30
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50
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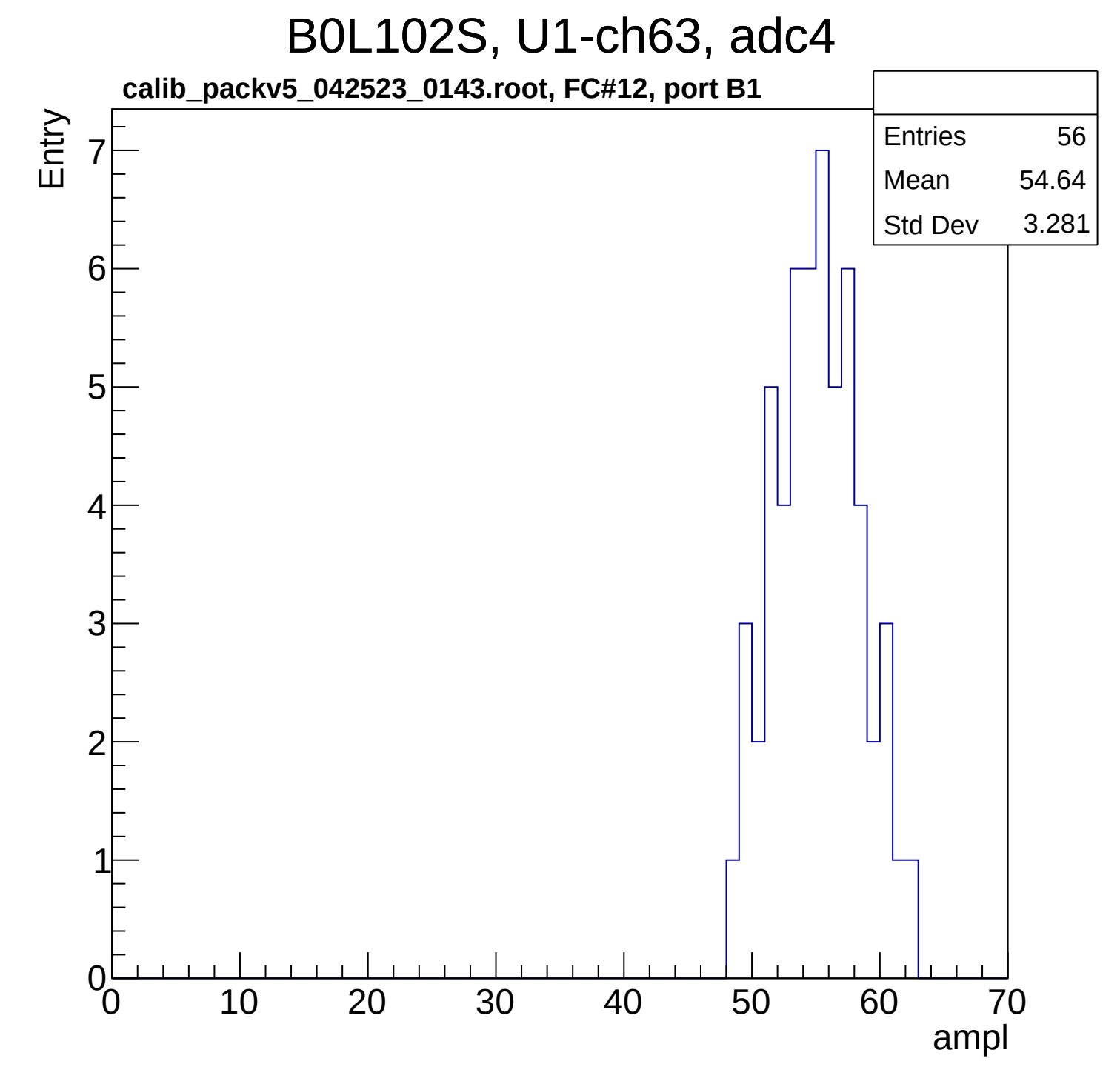
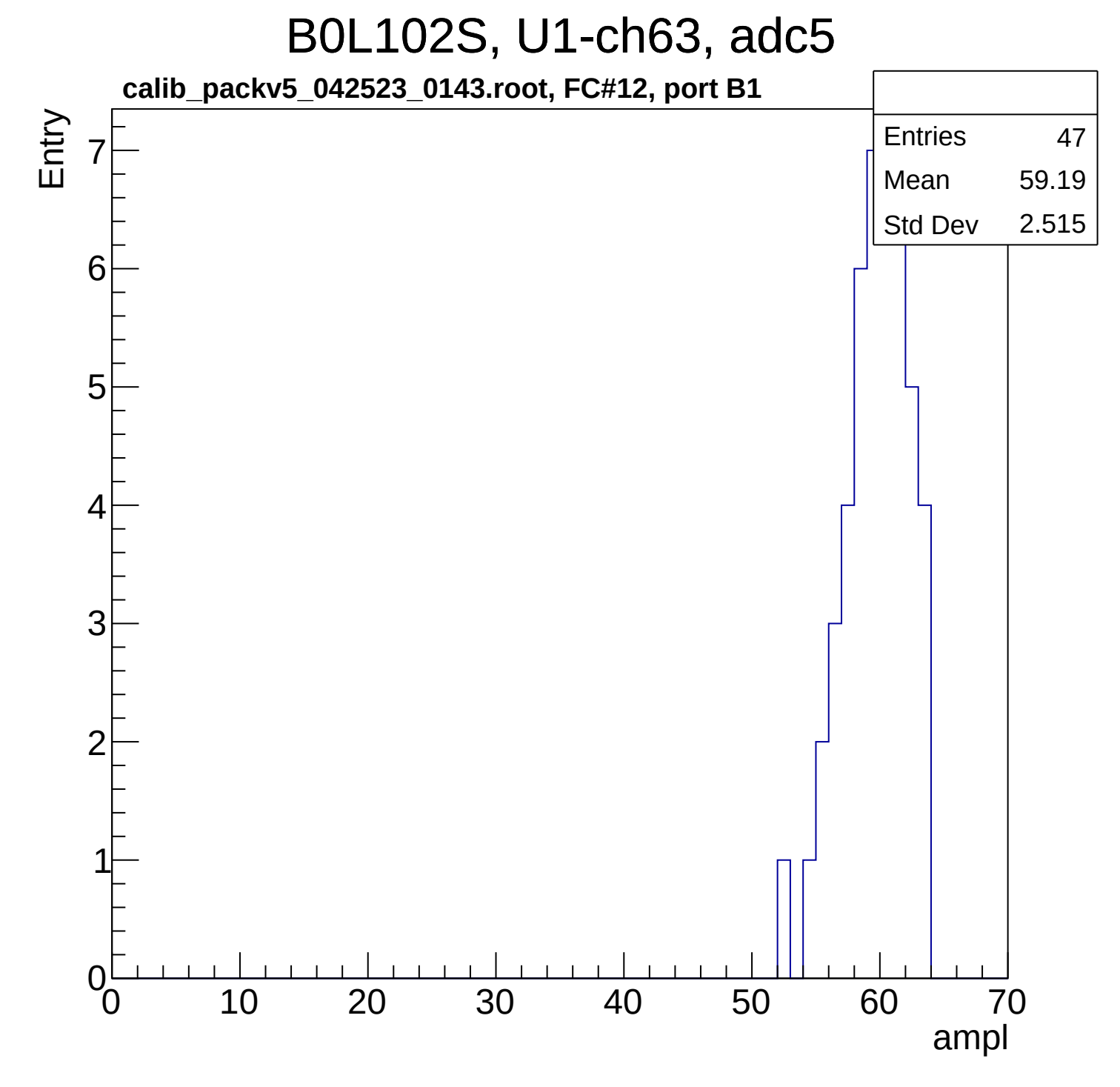
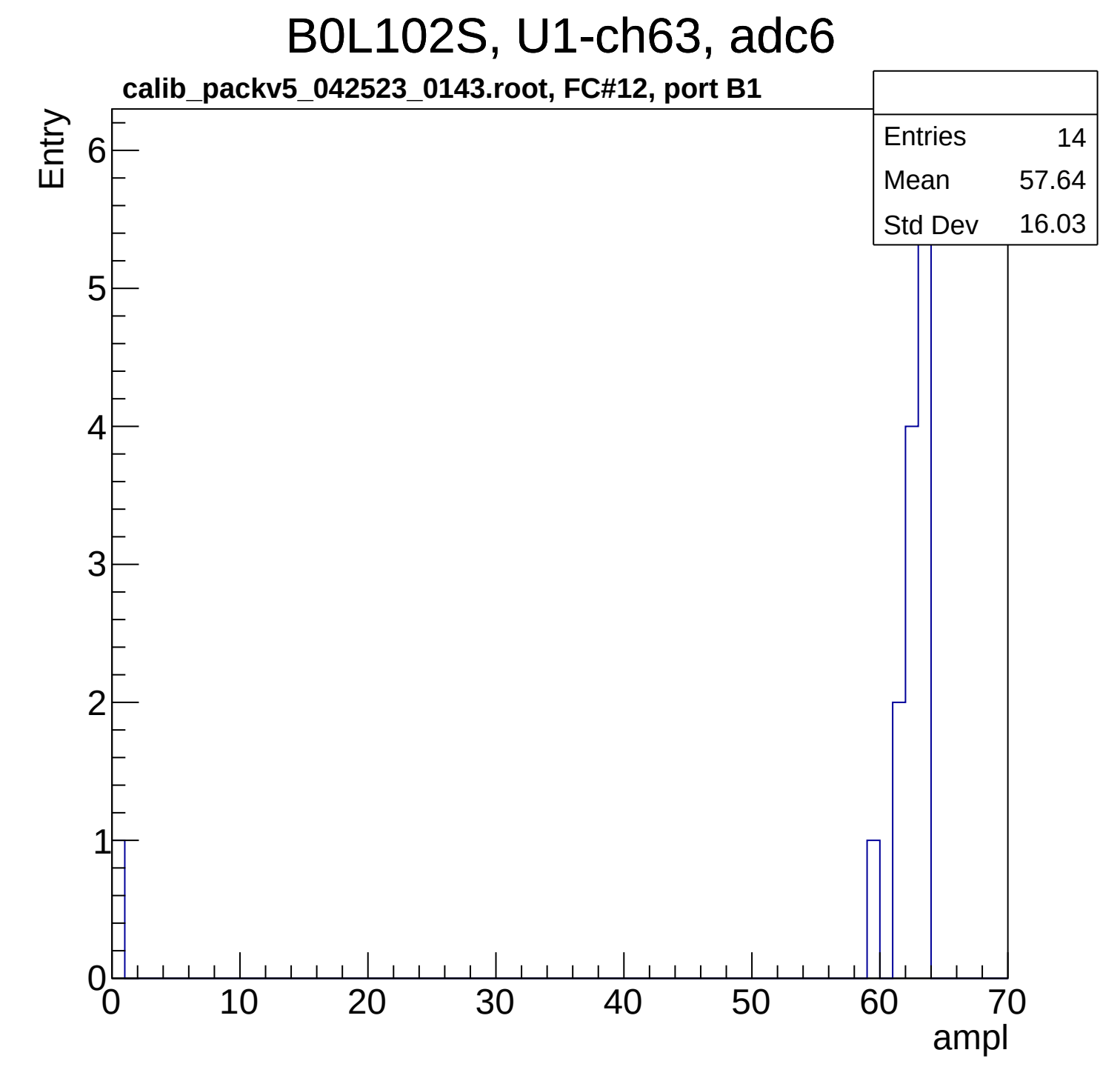
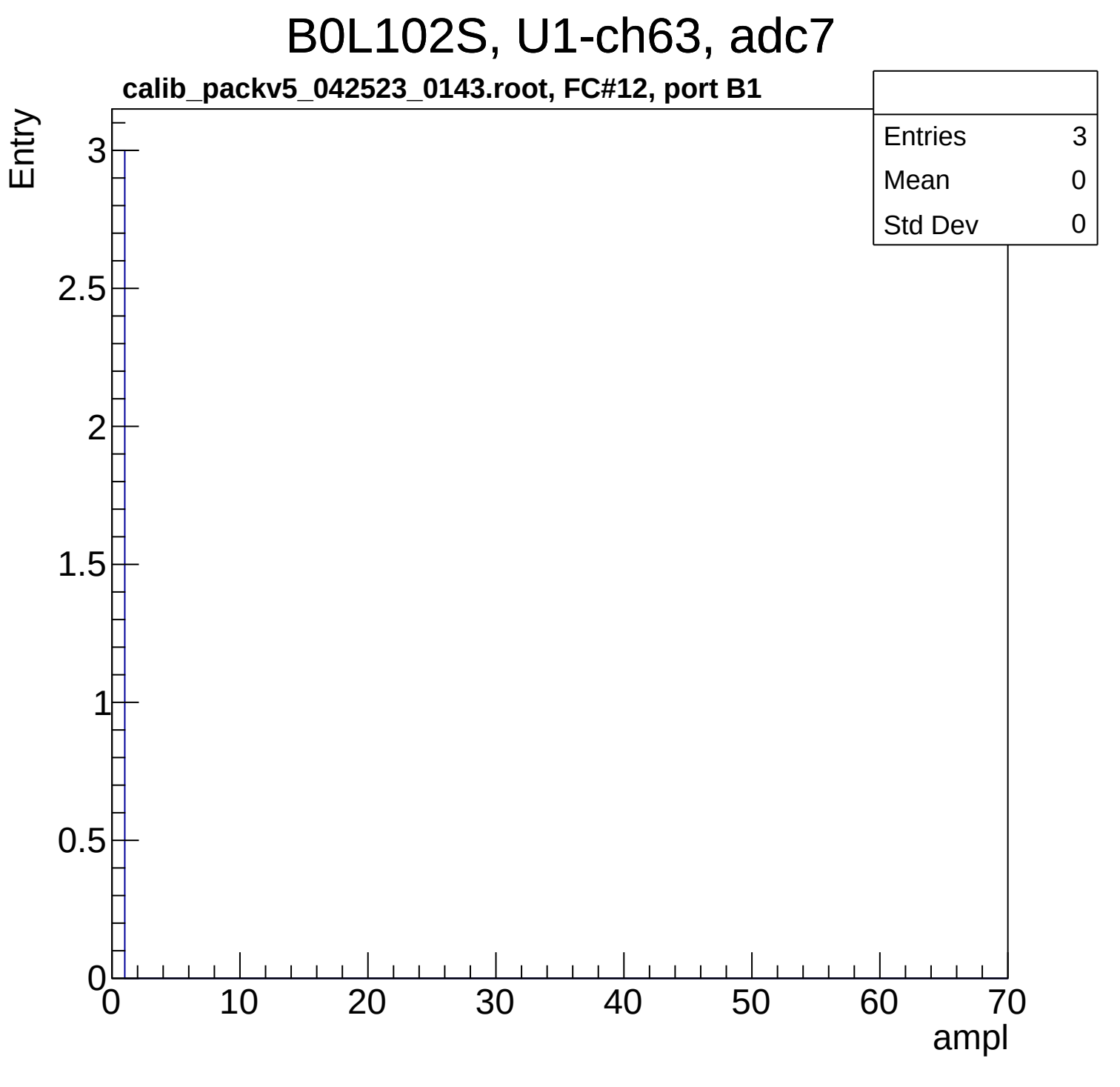
70
ampl


B0L102S, U1-ch63, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 54.64
Std Dev 3.281
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch63, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.19
Std Dev 2.515
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch63, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 57.64
Std Dev 16.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch63, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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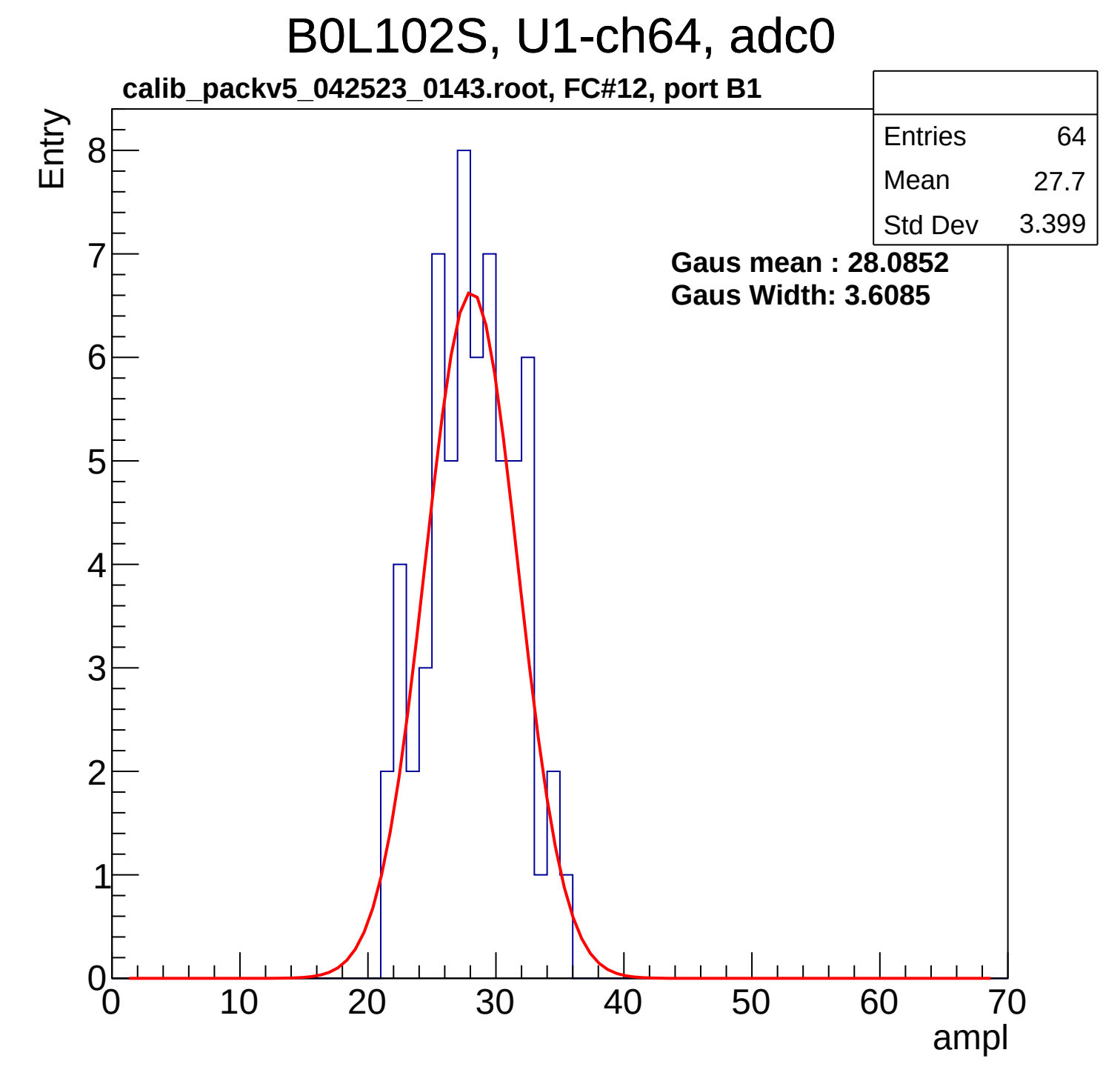
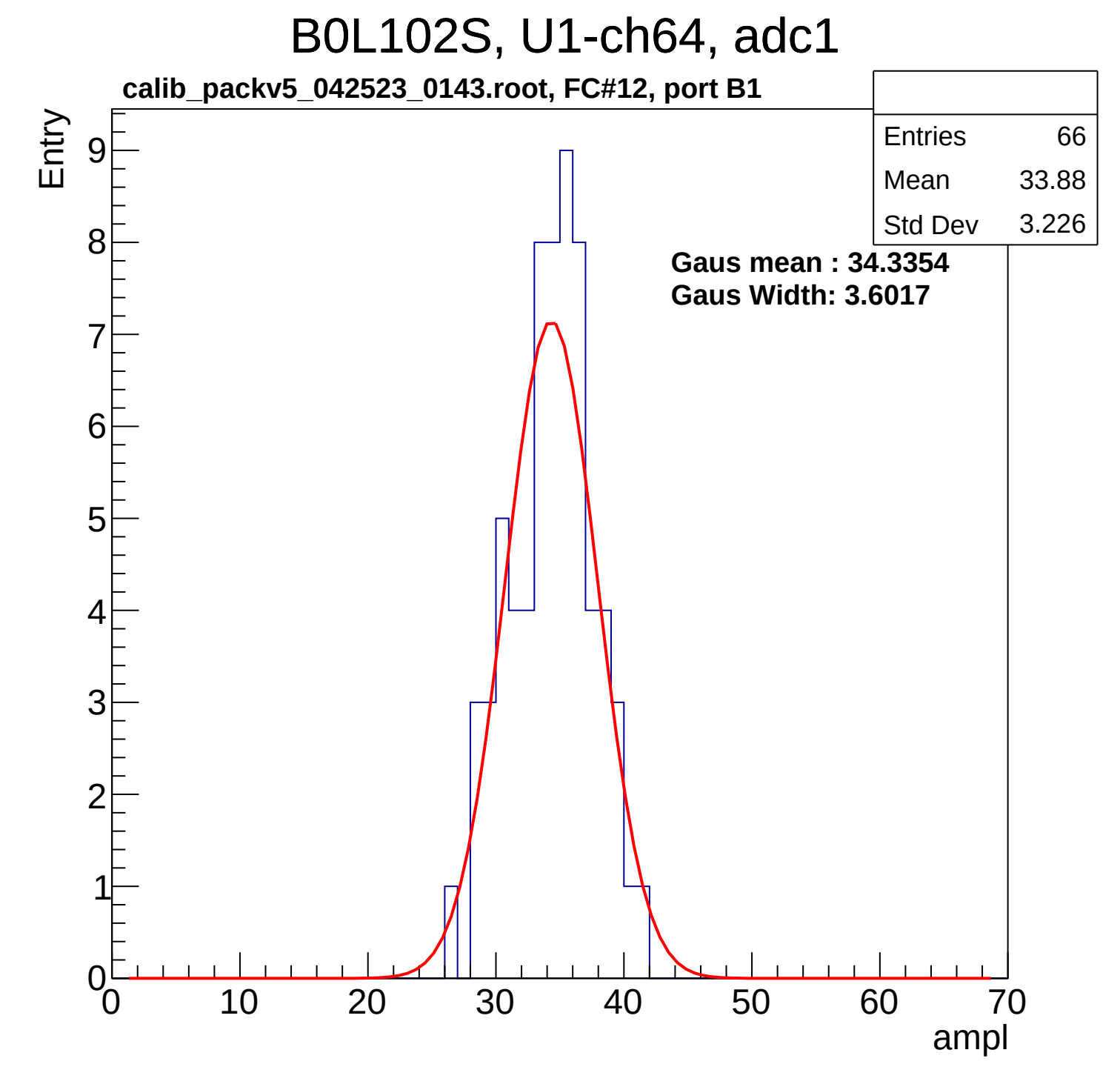
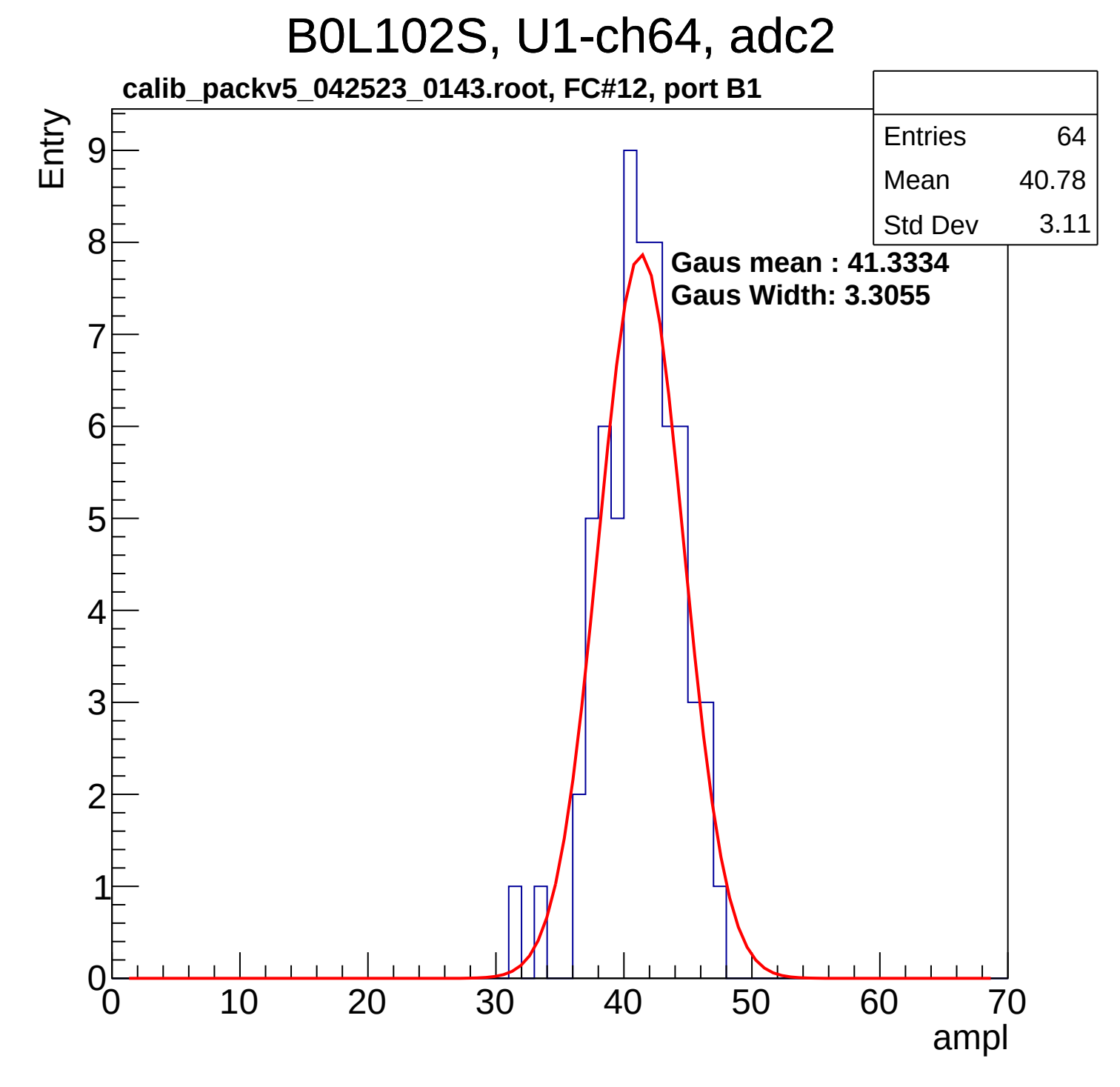
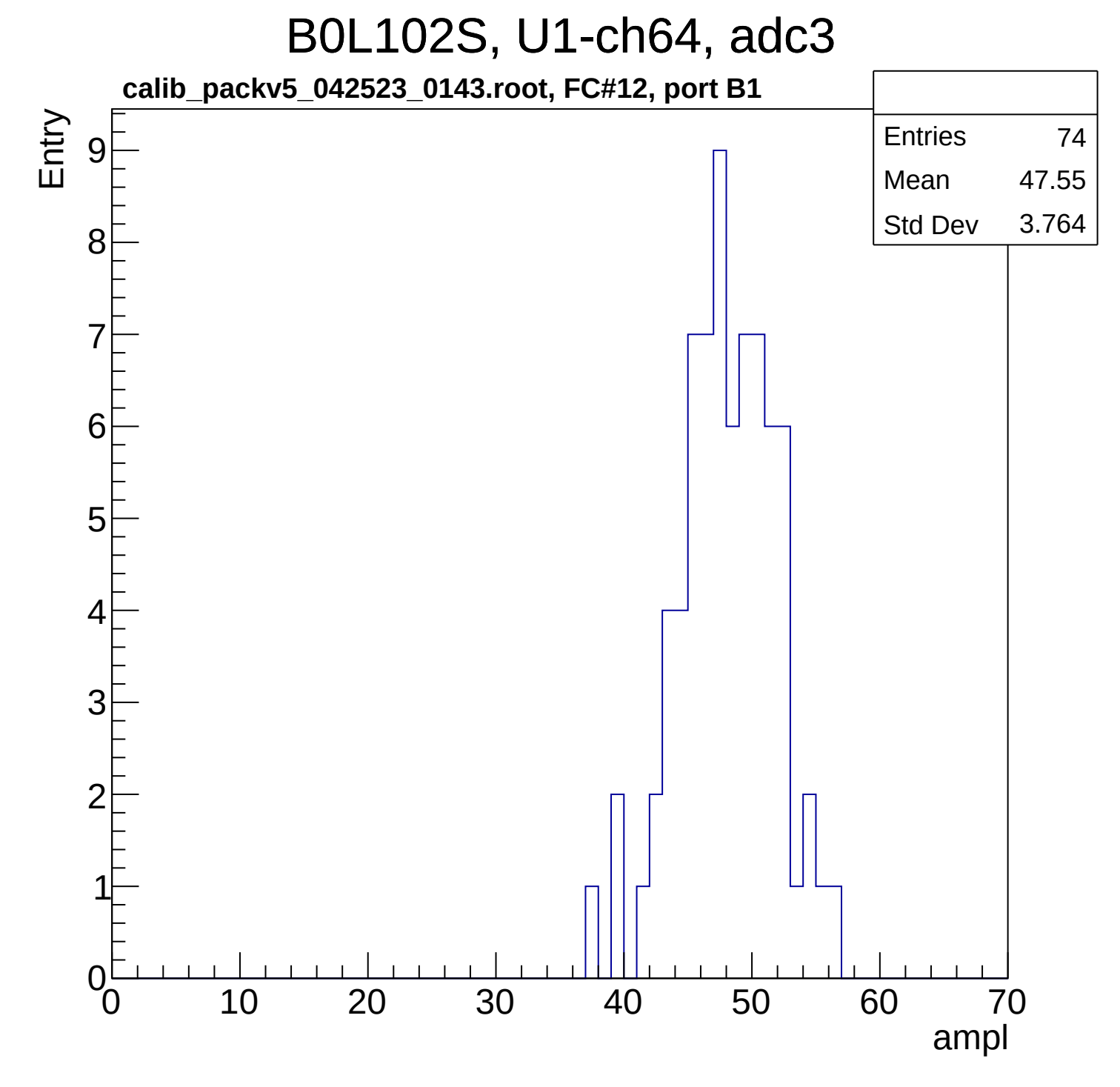
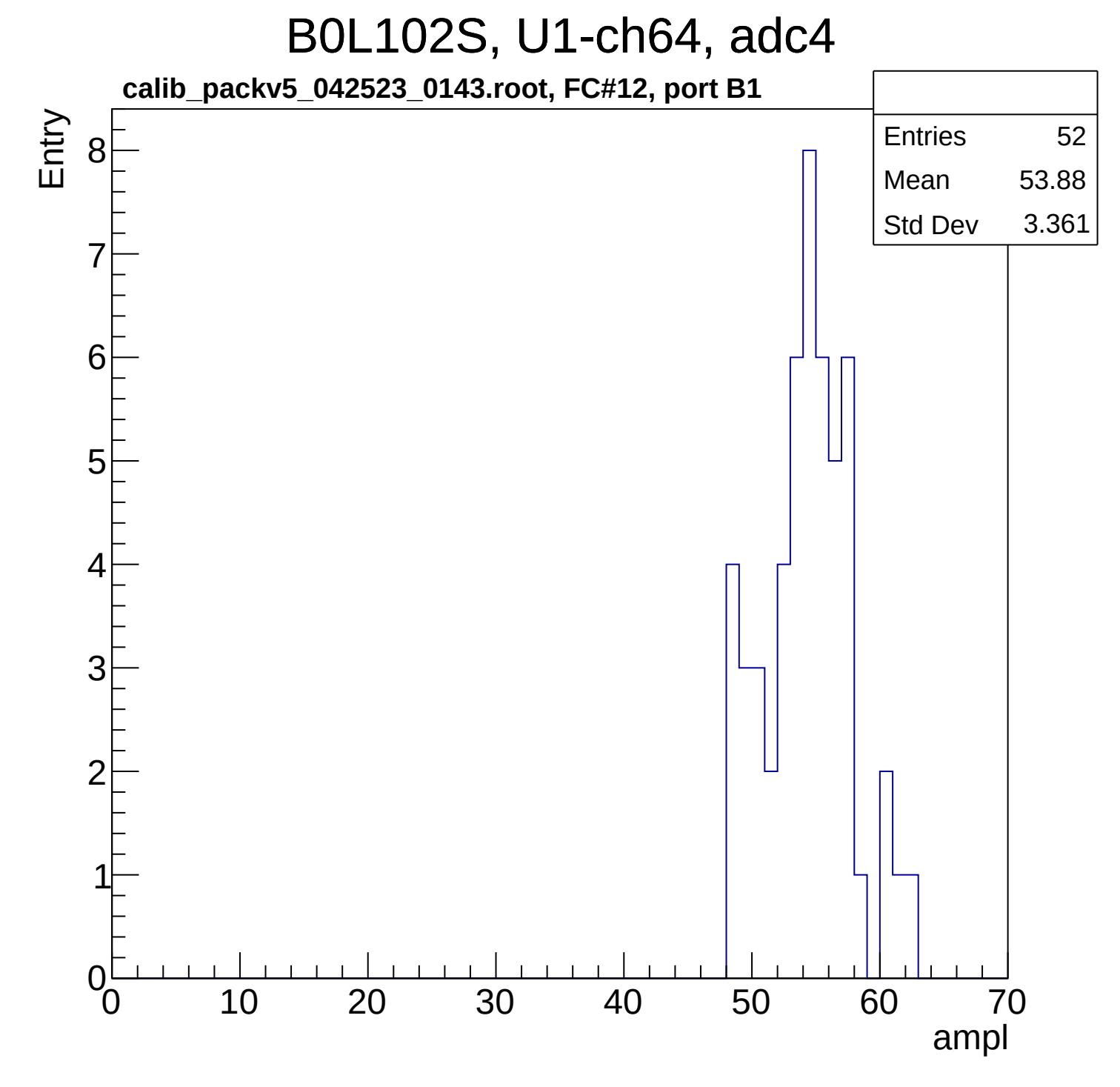
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

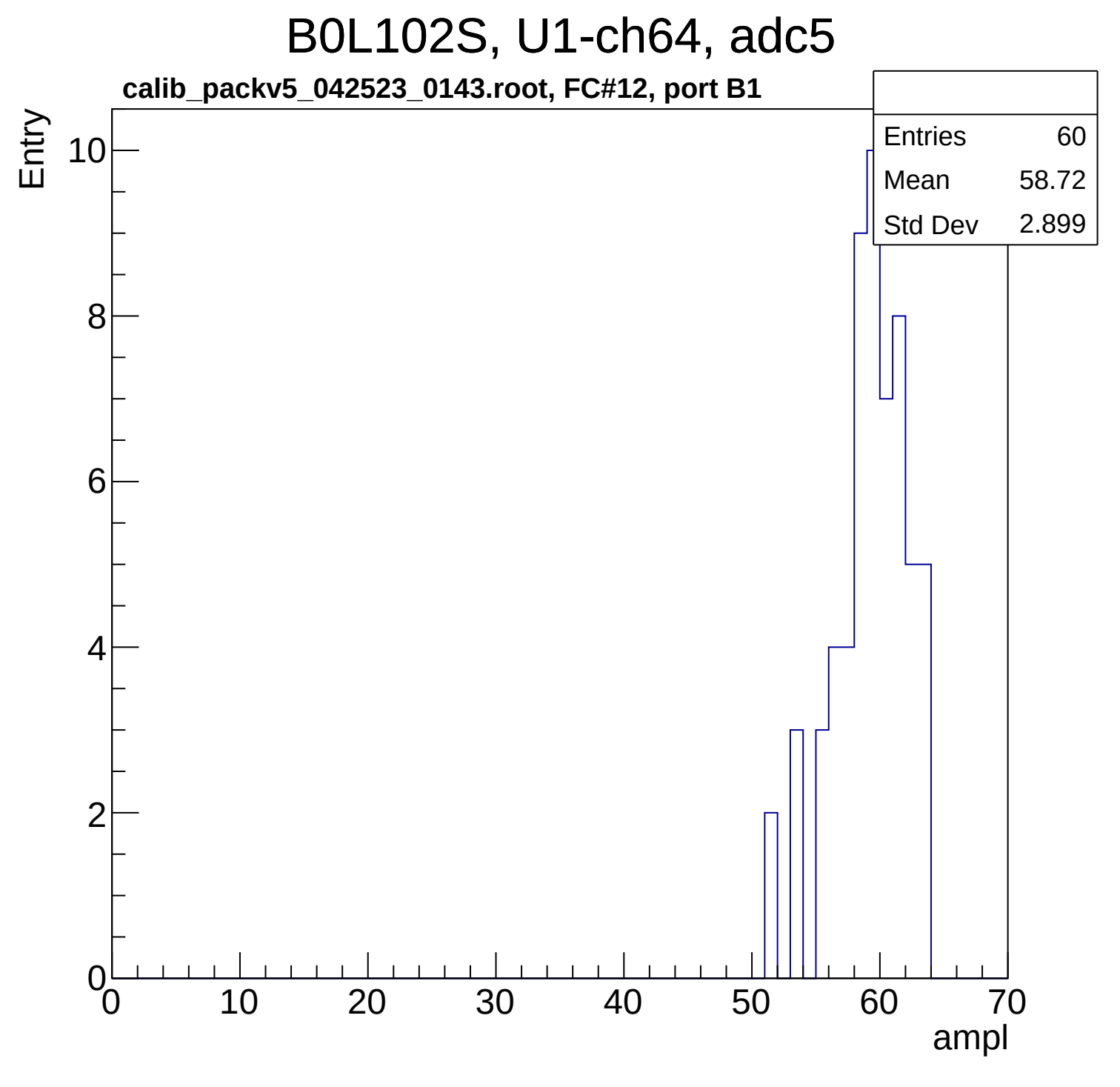

B0L102S, U1-ch64, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 27.7
Std Dev 3.399
Gaus mean : 28.0852
Gaus Width: 3.6085
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

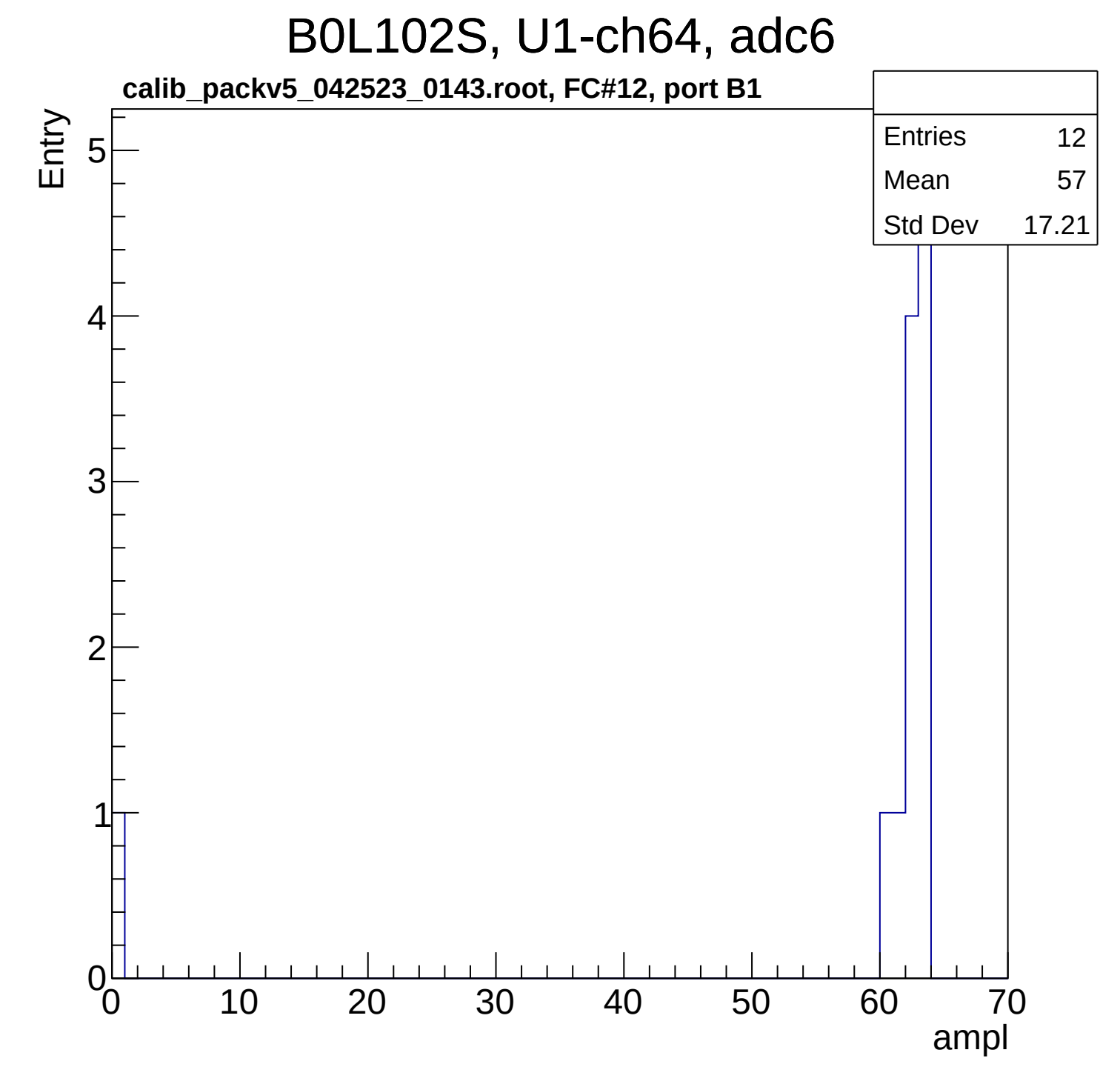

B0L102S, U1-ch64, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 33.88
Std Dev 3.226
Gaus mean : 34.3354
Gaus Width: 3.6017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

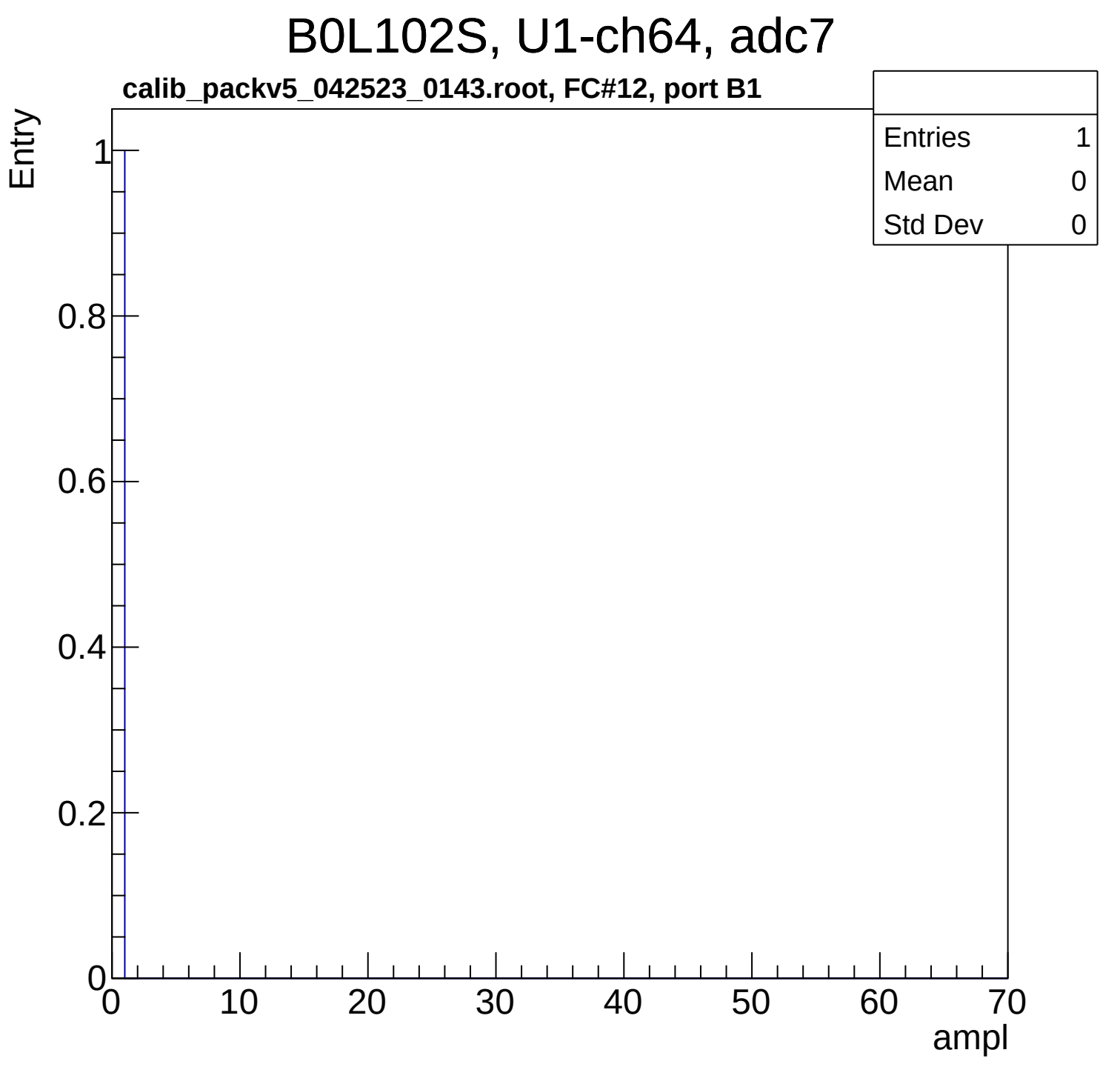

B0L102S, U1-ch64, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 40.78
Std Dev 3.11
Gaus mean : 41.3334
Gaus Width: 3.3055
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

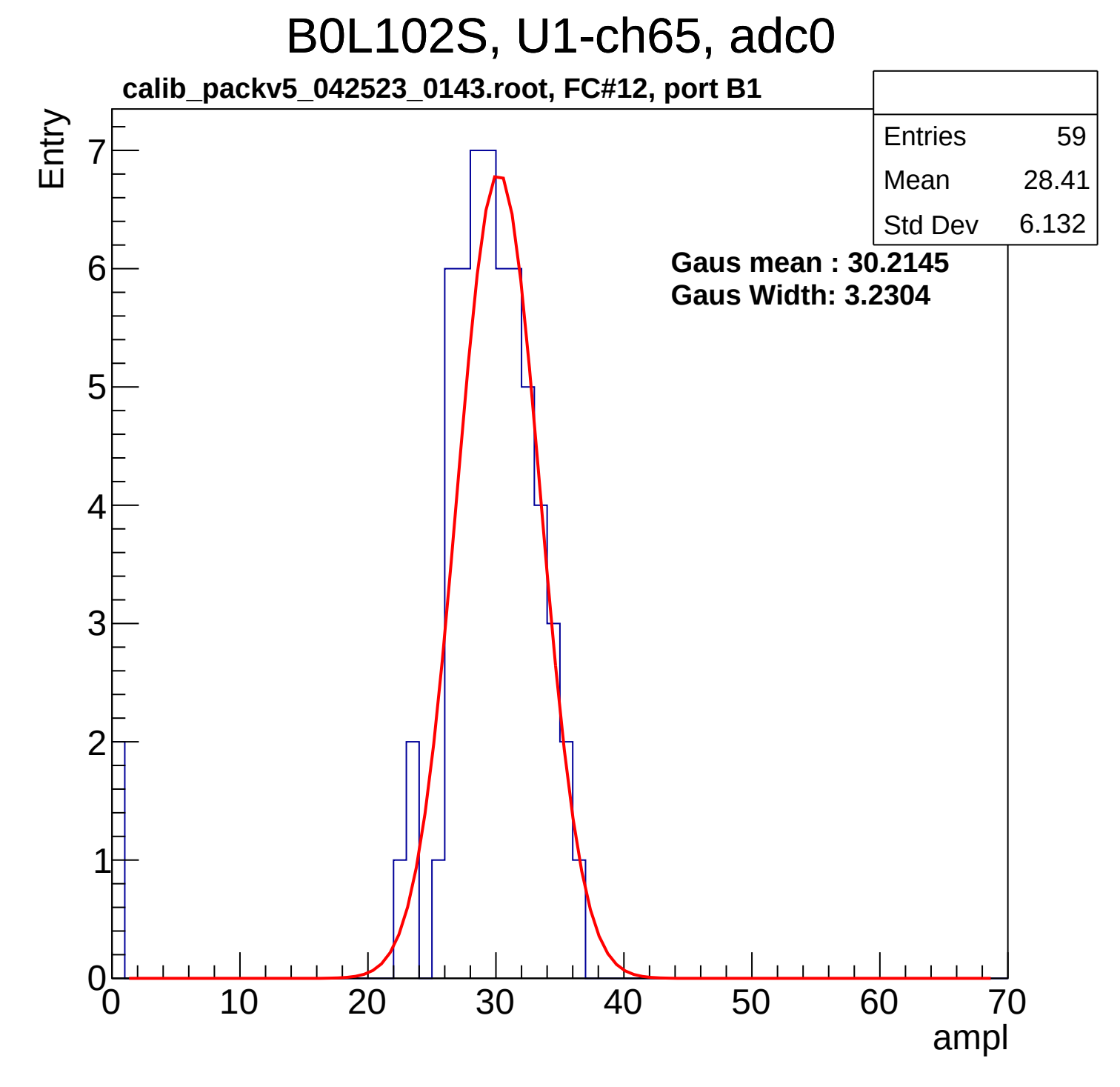

B0L102S, U1-ch64, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 47.55
Std Dev 3.764
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

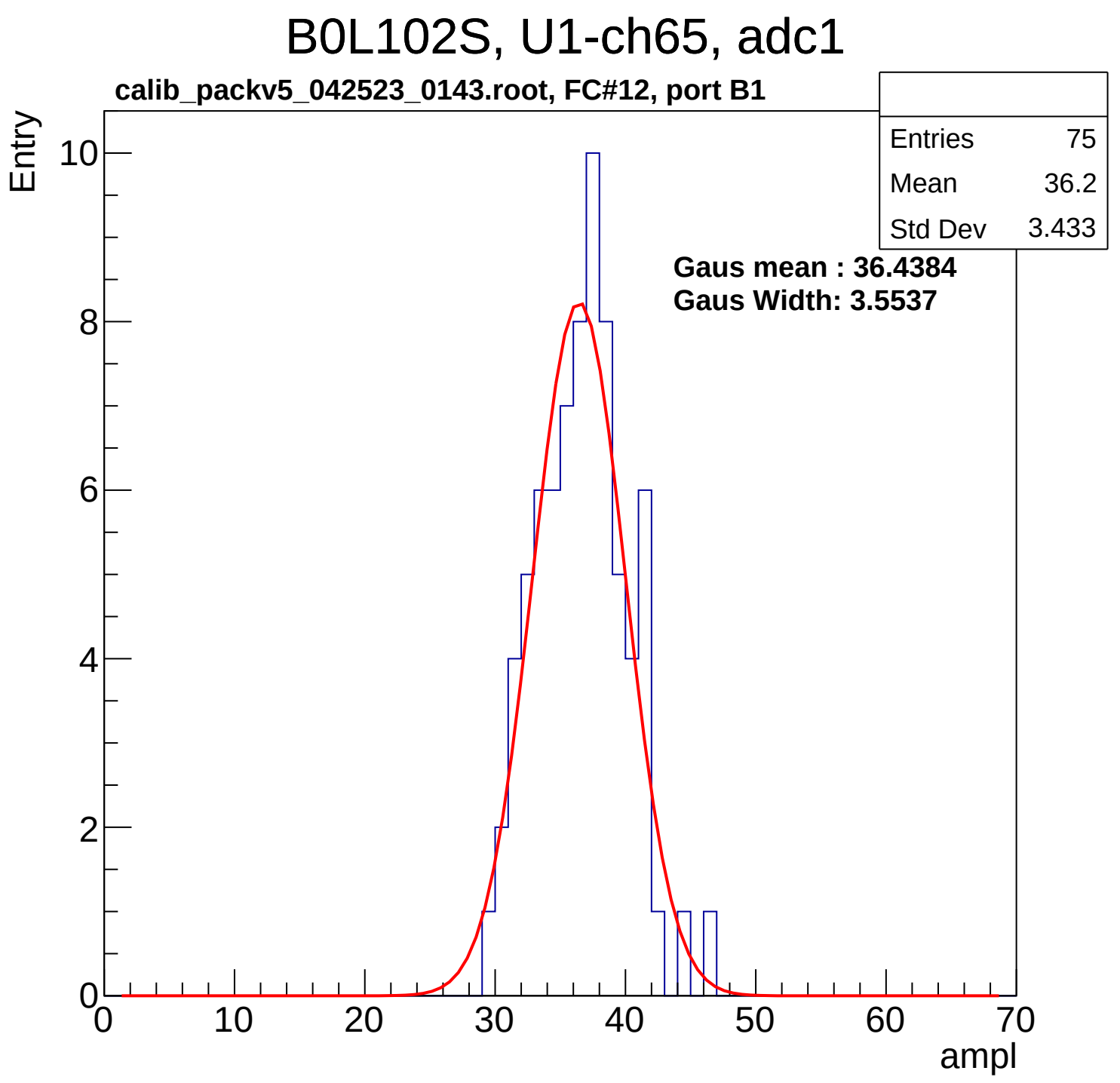

B0L102S, U1-ch64, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 53.88
Std Dev 3.361
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

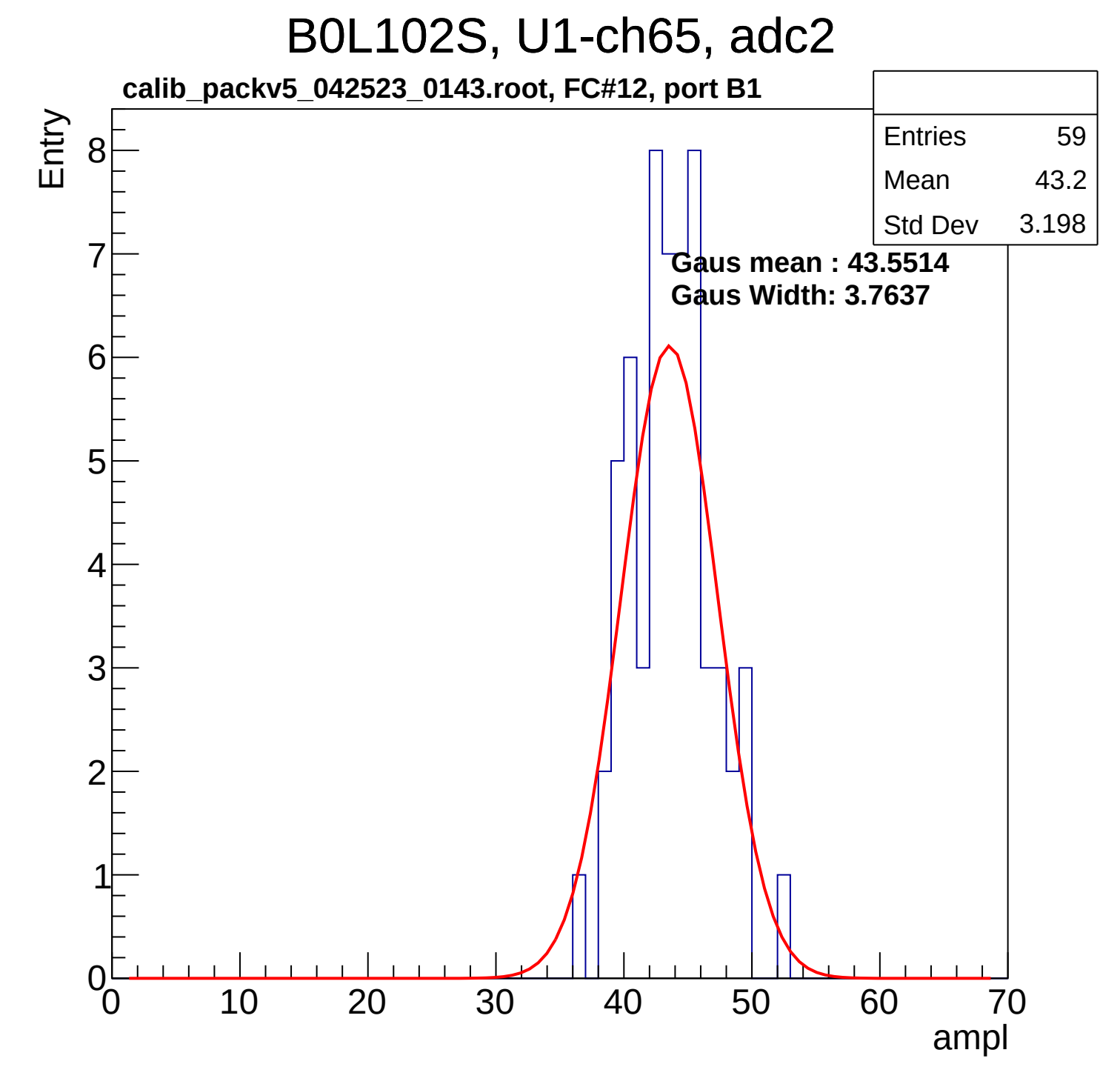

B0L102S, U1-ch64, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.72
Std Dev 2.899
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

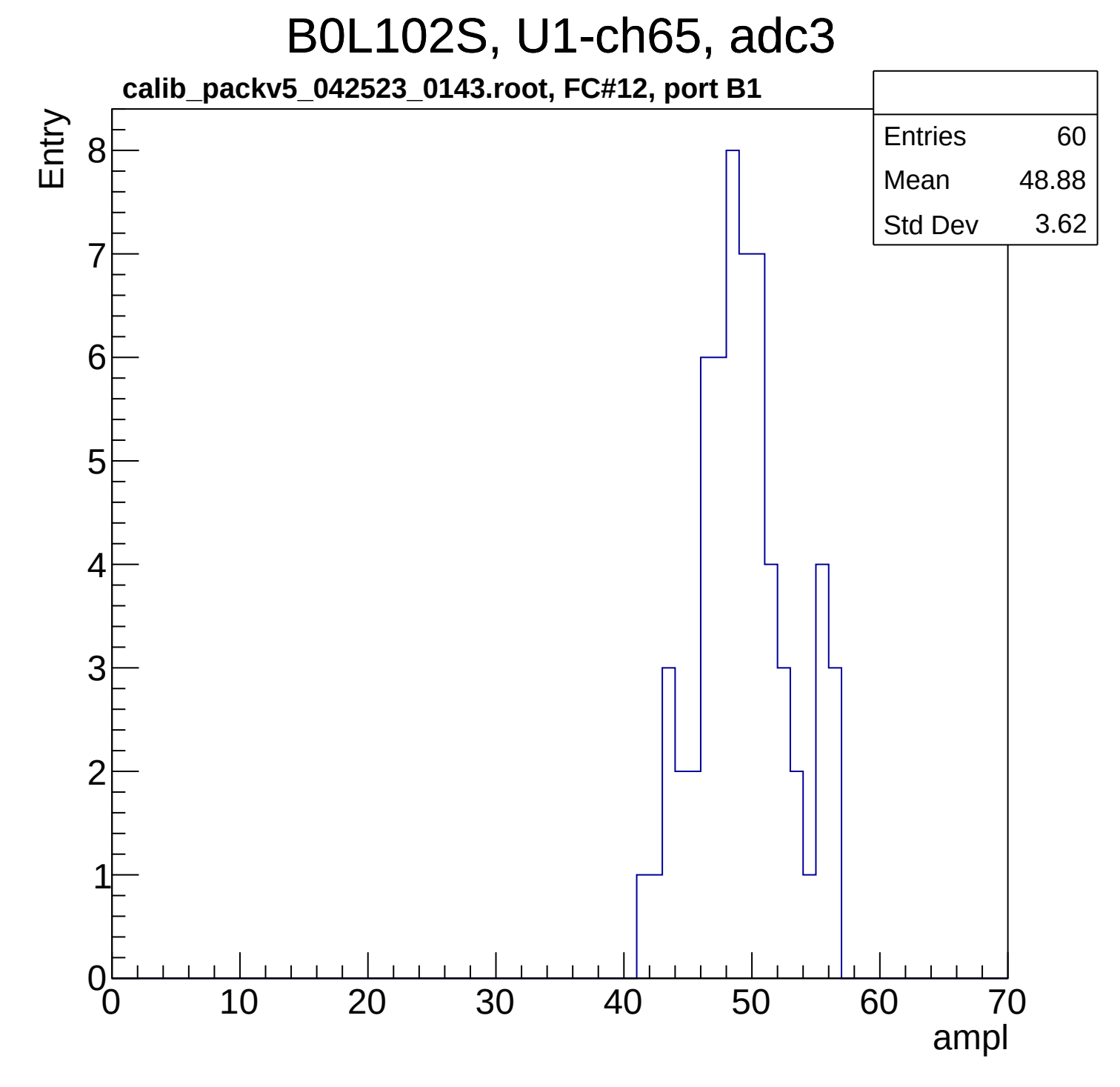

B0L102S, U1-ch64, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 57
Std Dev 17.21
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

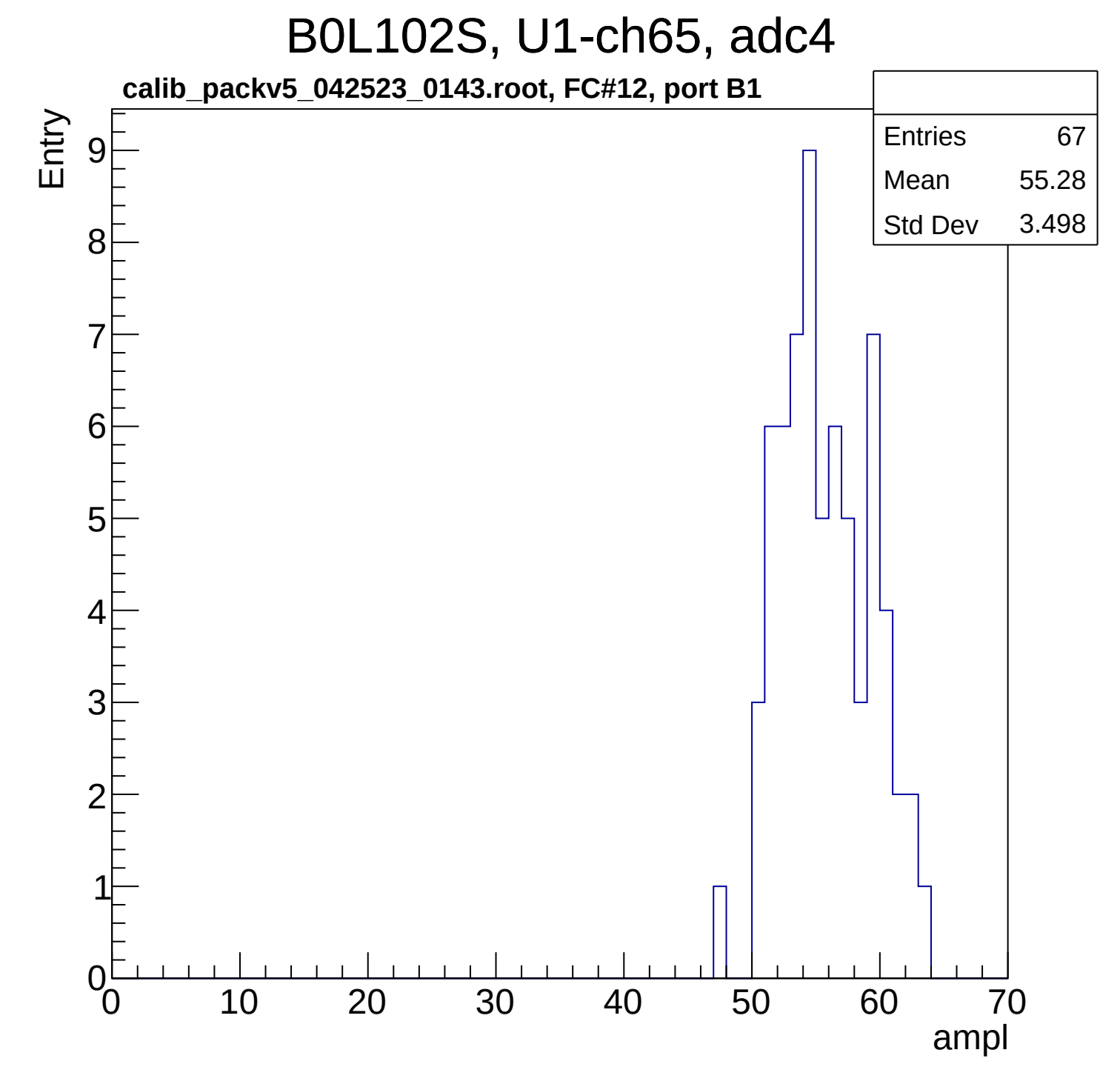

B0L102S, U1-ch64, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

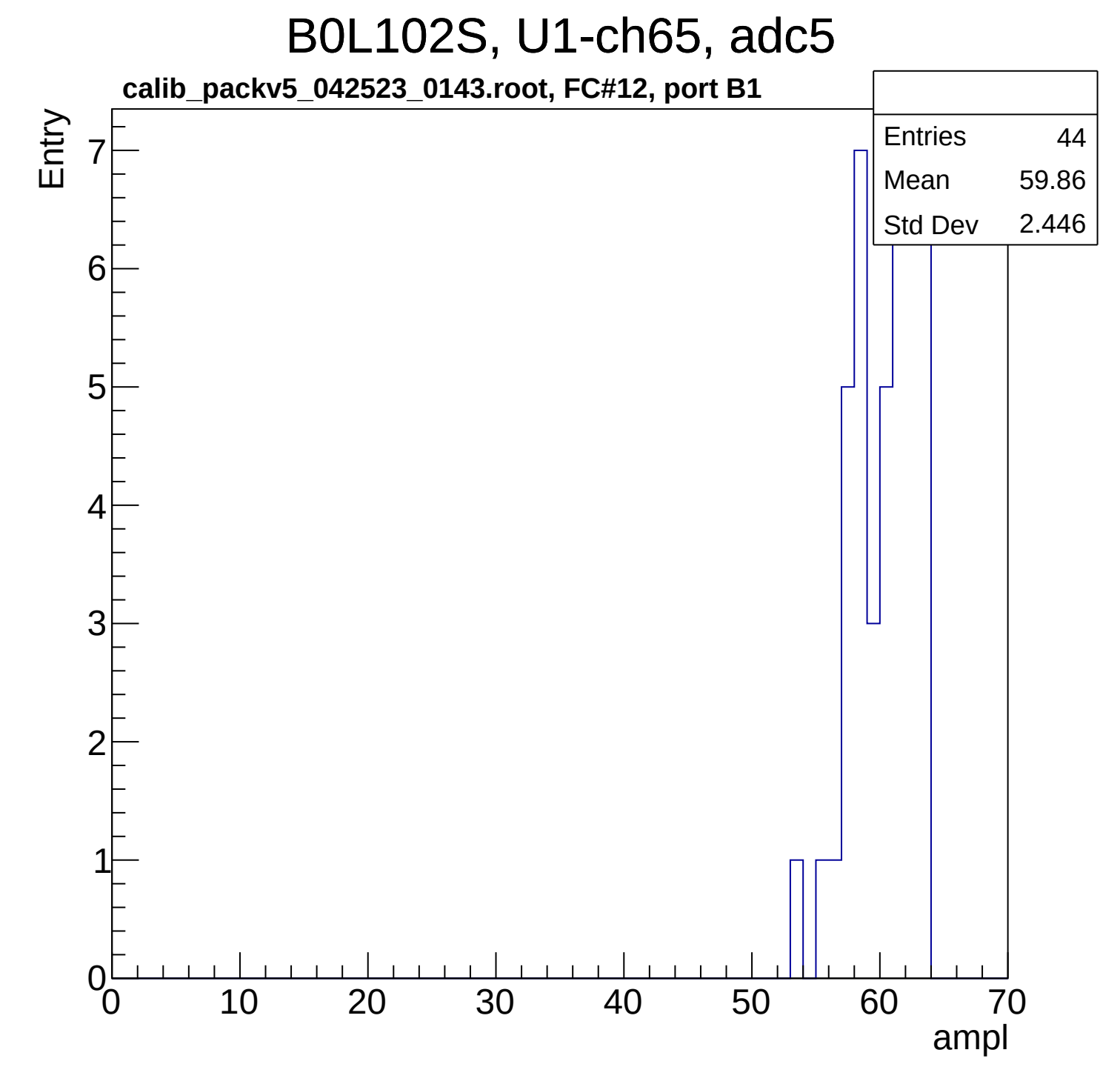

B0L102S, U1-ch65, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 28.41
Std Dev 6.132
Gaus mean : 30.2145
Gaus Width: 3.2304
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

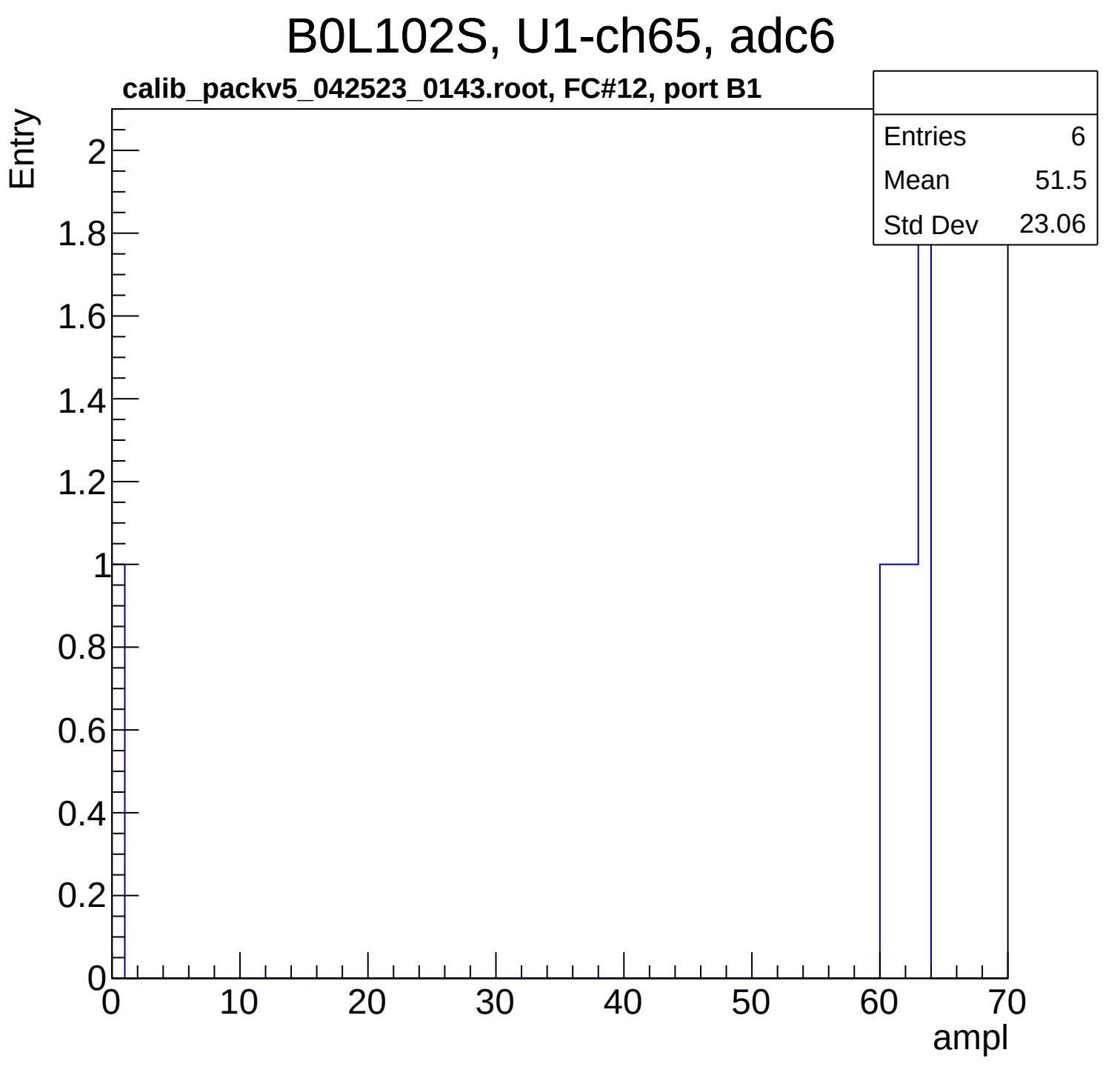

B0L102S, U1-ch65, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.2
Std Dev 3.433
Gaus mean : 36.4384
Gaus Width: 3.5537
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

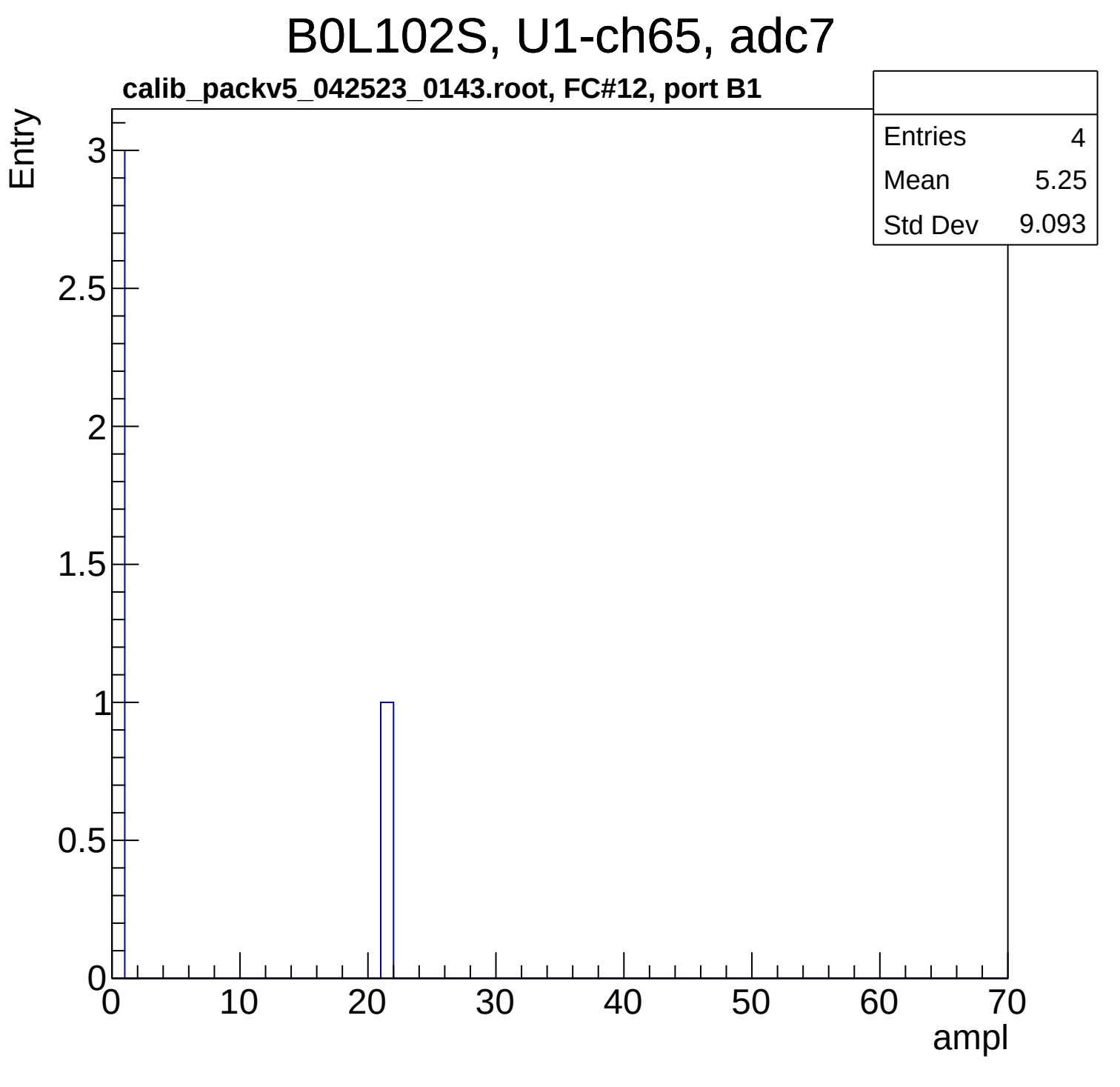

B0L102S, U1-ch65, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 43.2
Std Dev 3.198
Gaus mean : 43.5514
Gaus Width: 3.7637
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

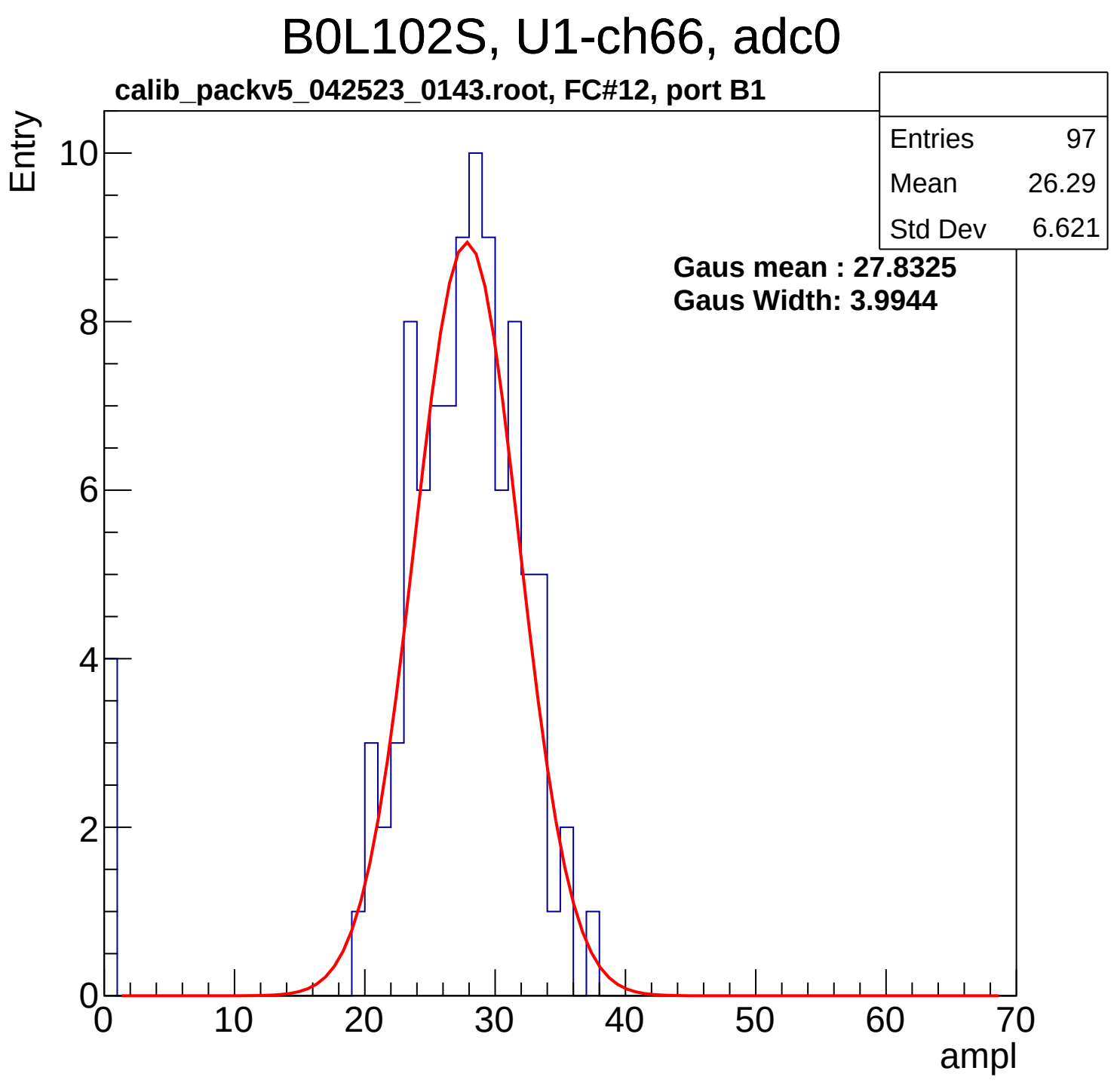

B0L102S, U1-ch65, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 48.88
Std Dev 3.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

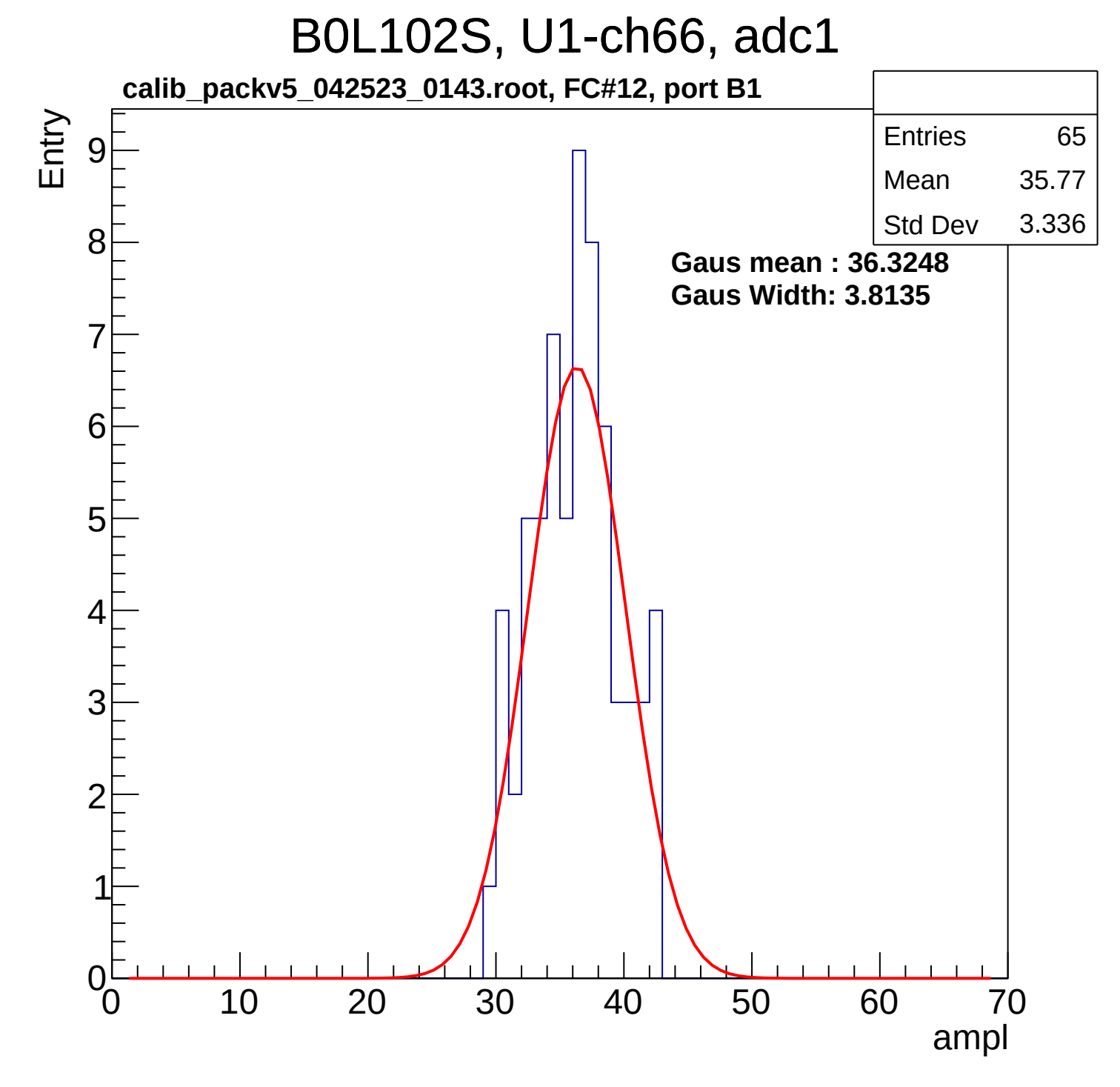

B0L102S, U1-ch65, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 55.28
Std Dev 3.498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

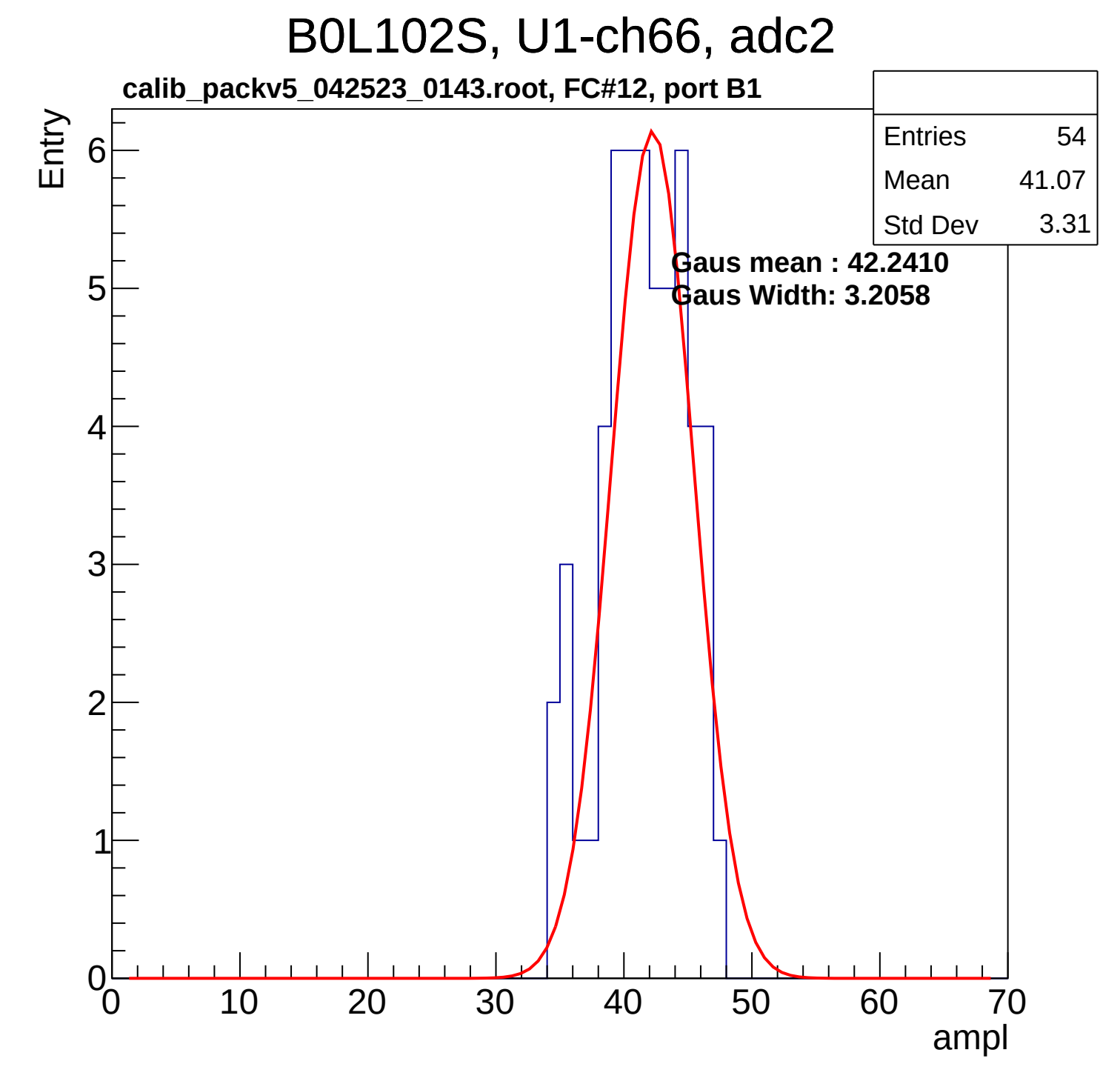

B0L102S, U1-ch65, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.86
Std Dev 2.446
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

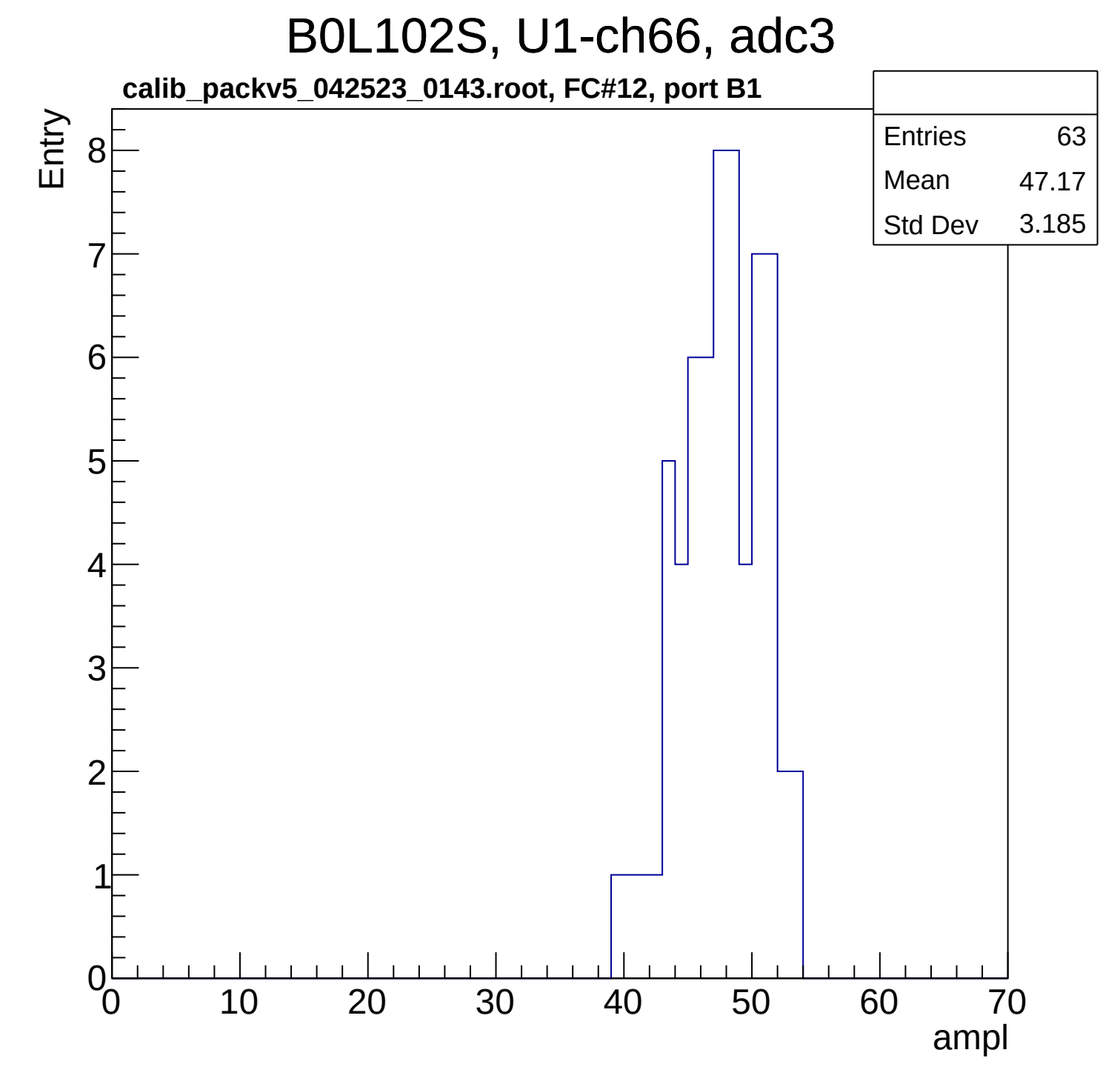

B0L102S, U1-ch65, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 51.5
Std Dev 23.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch65, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 5.25
Std Dev 9.093
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch66, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 97
Mean 26.29
Std Dev 6.621
Gaus mean : 27.8325
Gaus Width: 3.9944
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch66, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.77
Std Dev 3.336
Gaus mean : 36.3248
Gaus Width: 3.8135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch66, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 41.07
Std Dev 3.31
Gaus mean : 42.2410
Gaus Width: 3.2058
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U1-ch66, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 47.17
Std Dev 3.185
Entry
ampl
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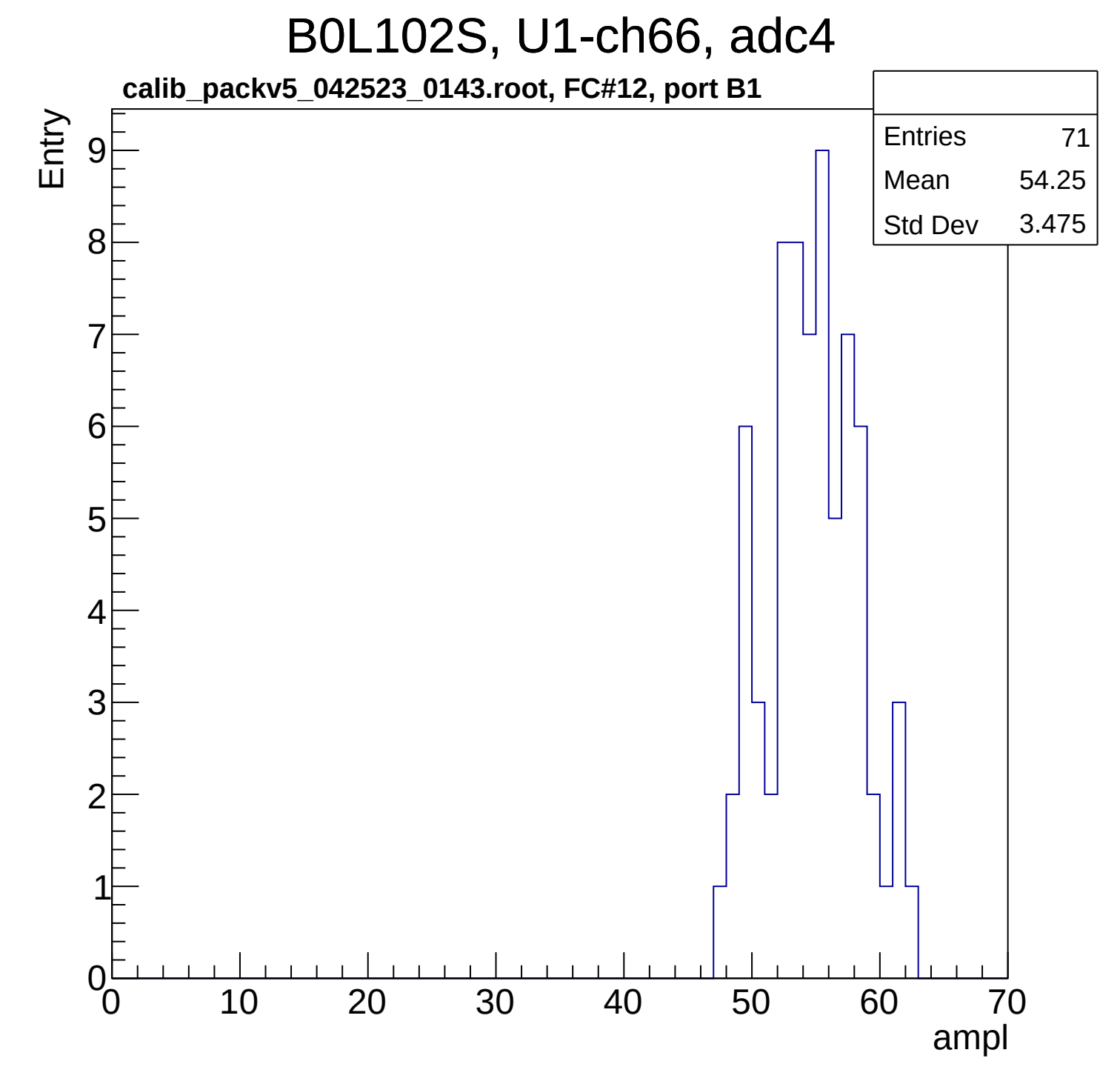
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch66, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 54.25
Std Dev 3.475
Entry
ampl
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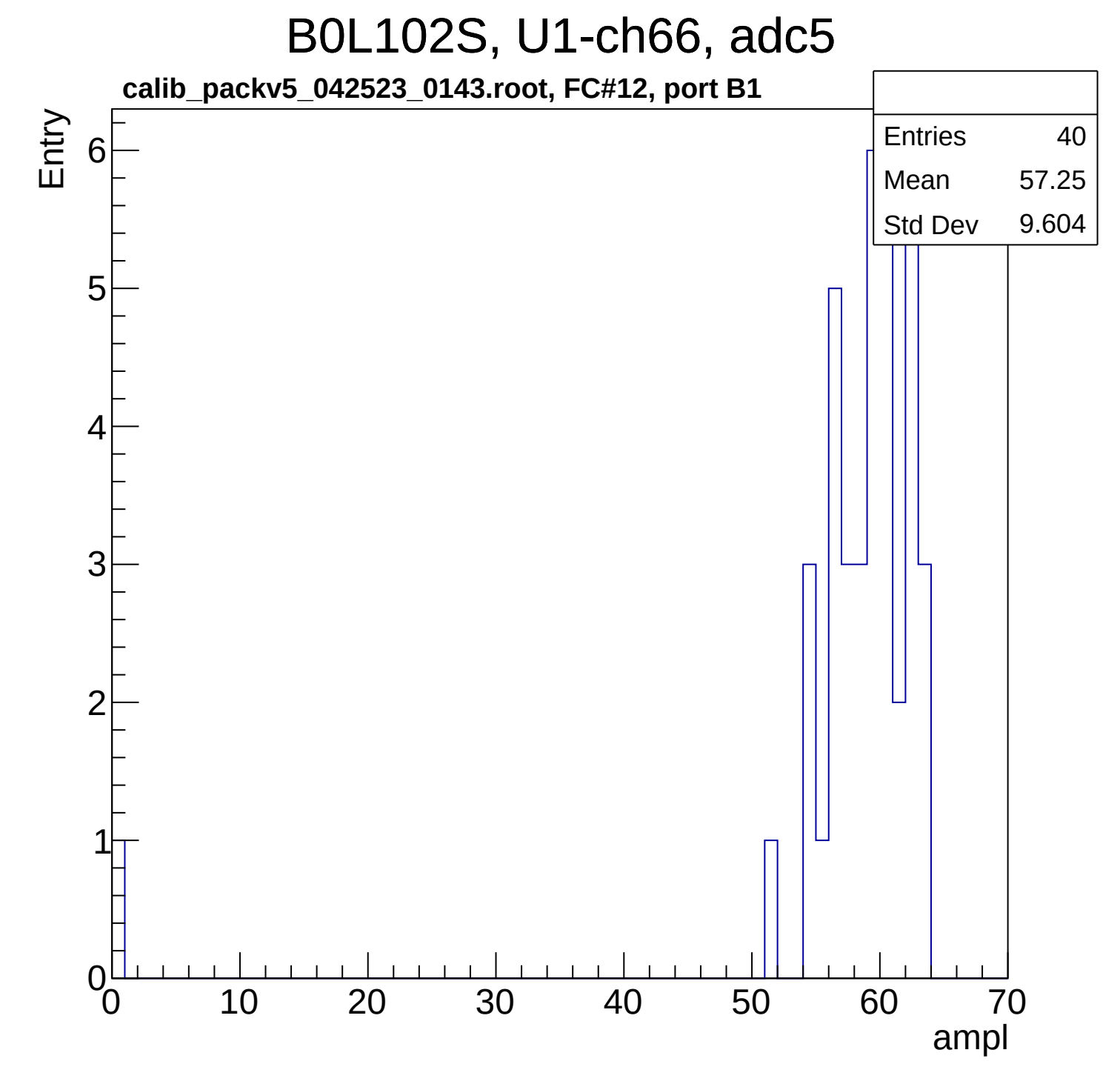
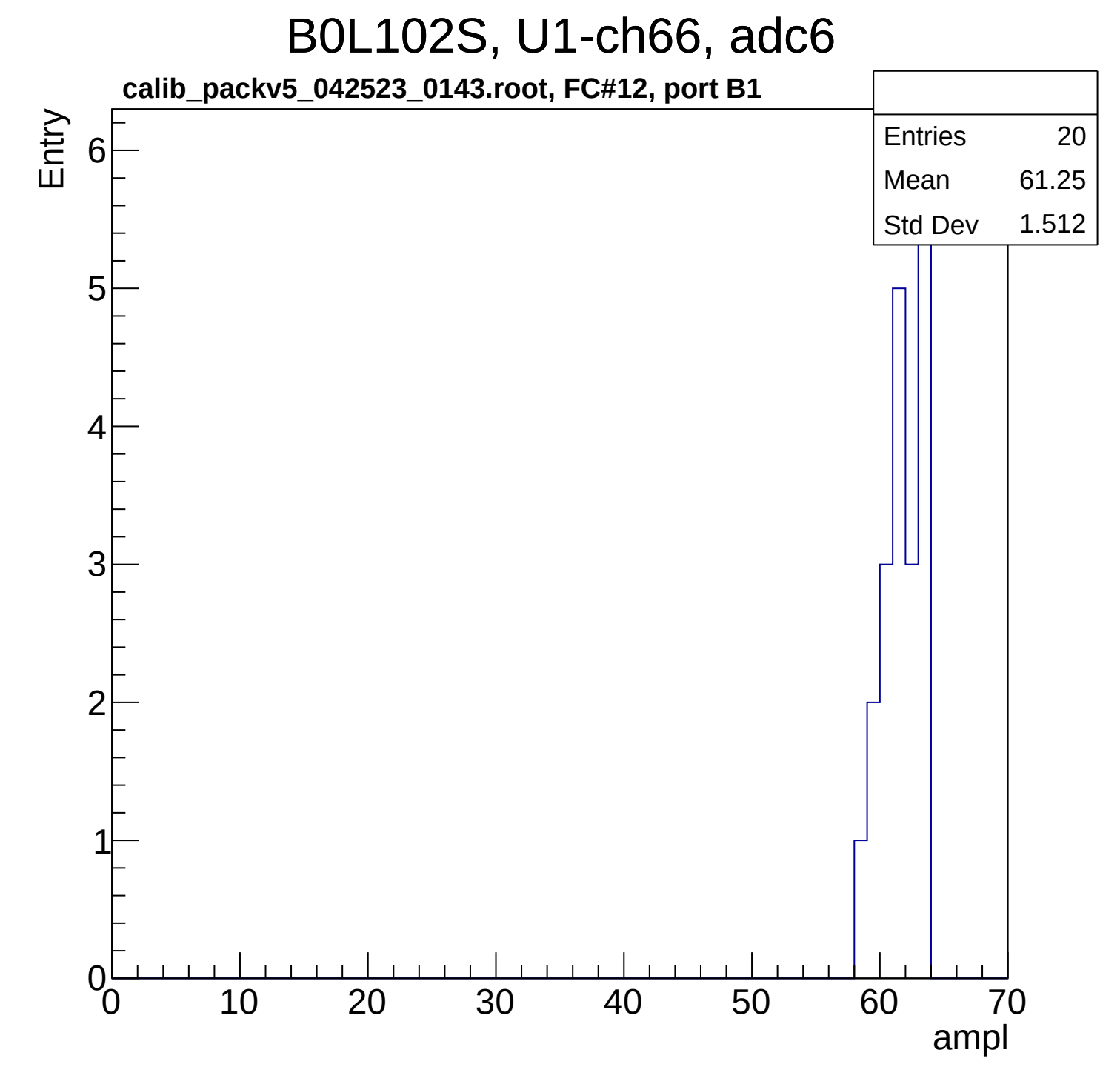
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch66, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 57.25
Std Dev 9.604
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch66, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 20
Mean 61.25
Std Dev 1.512
Entry
0
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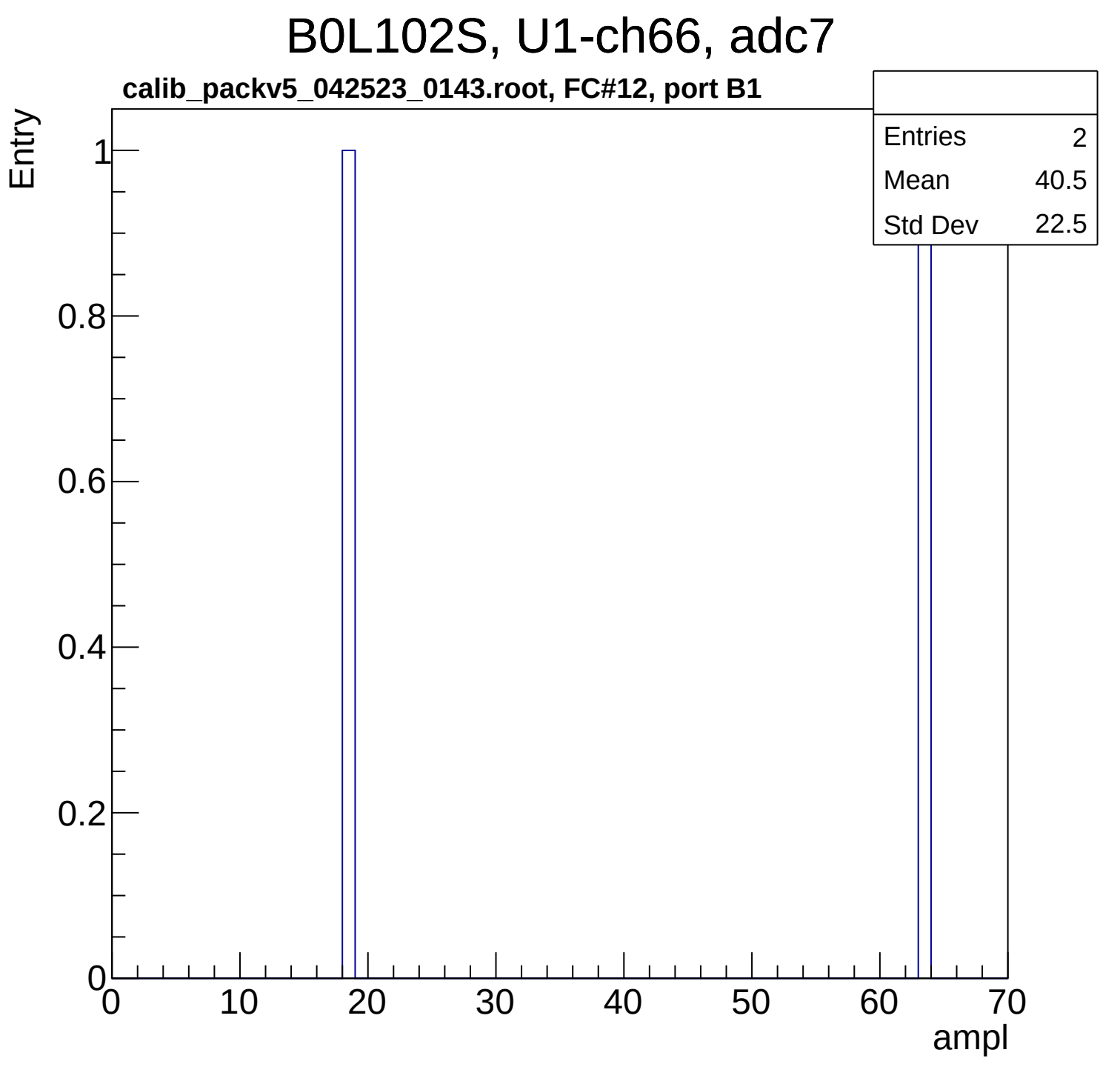
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch66, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 40.5
Std Dev 22.5
Entry
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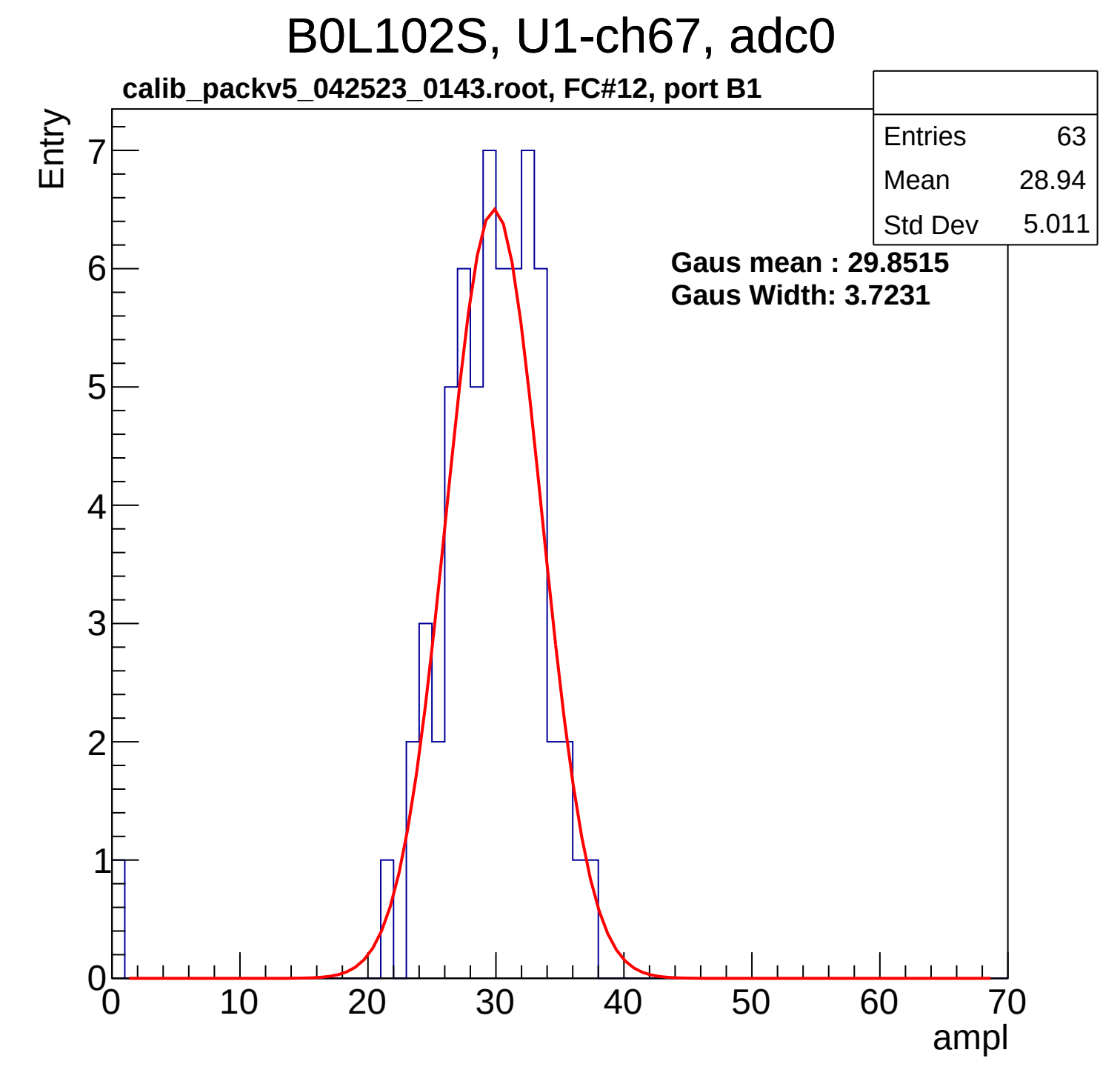
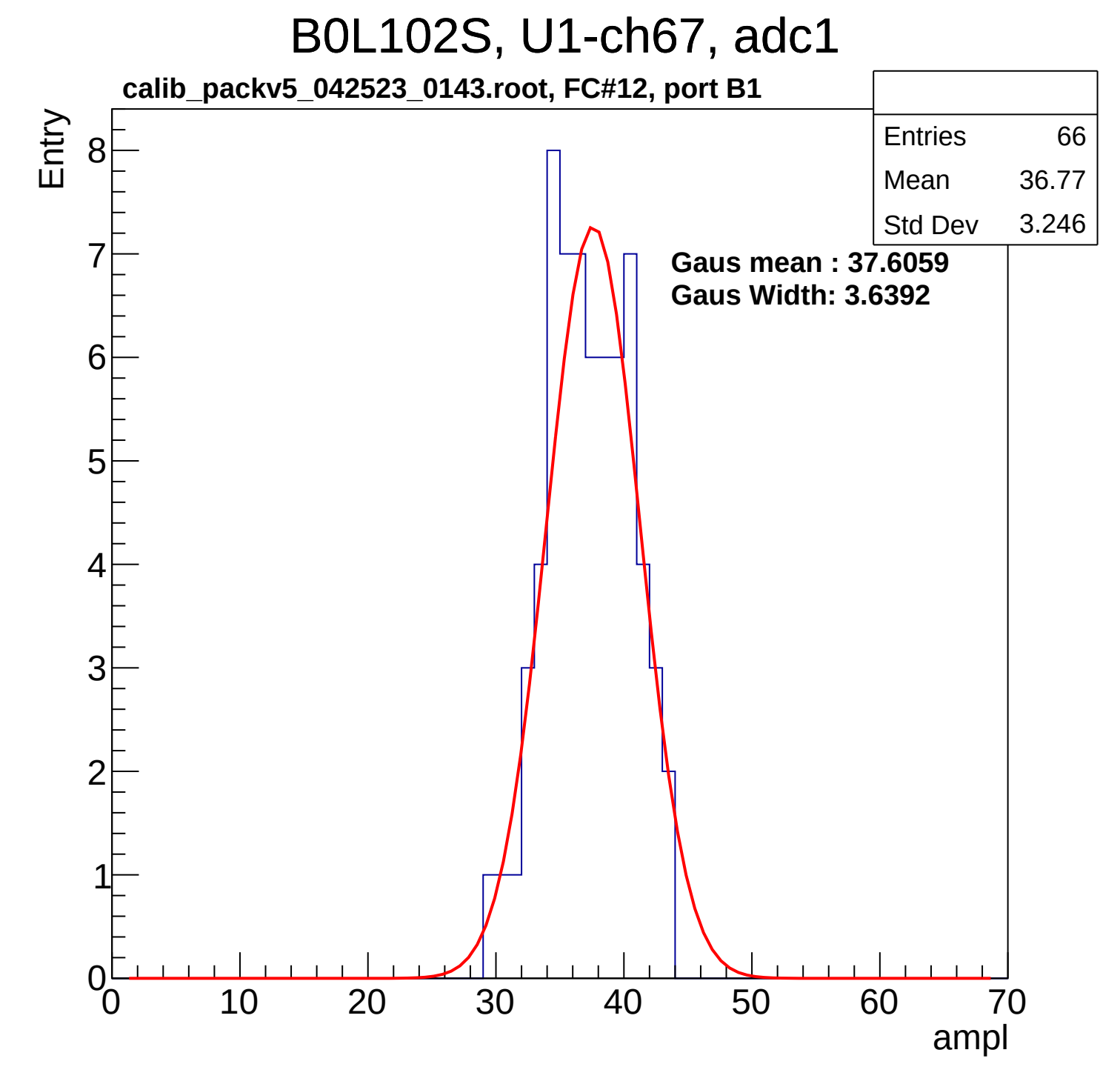
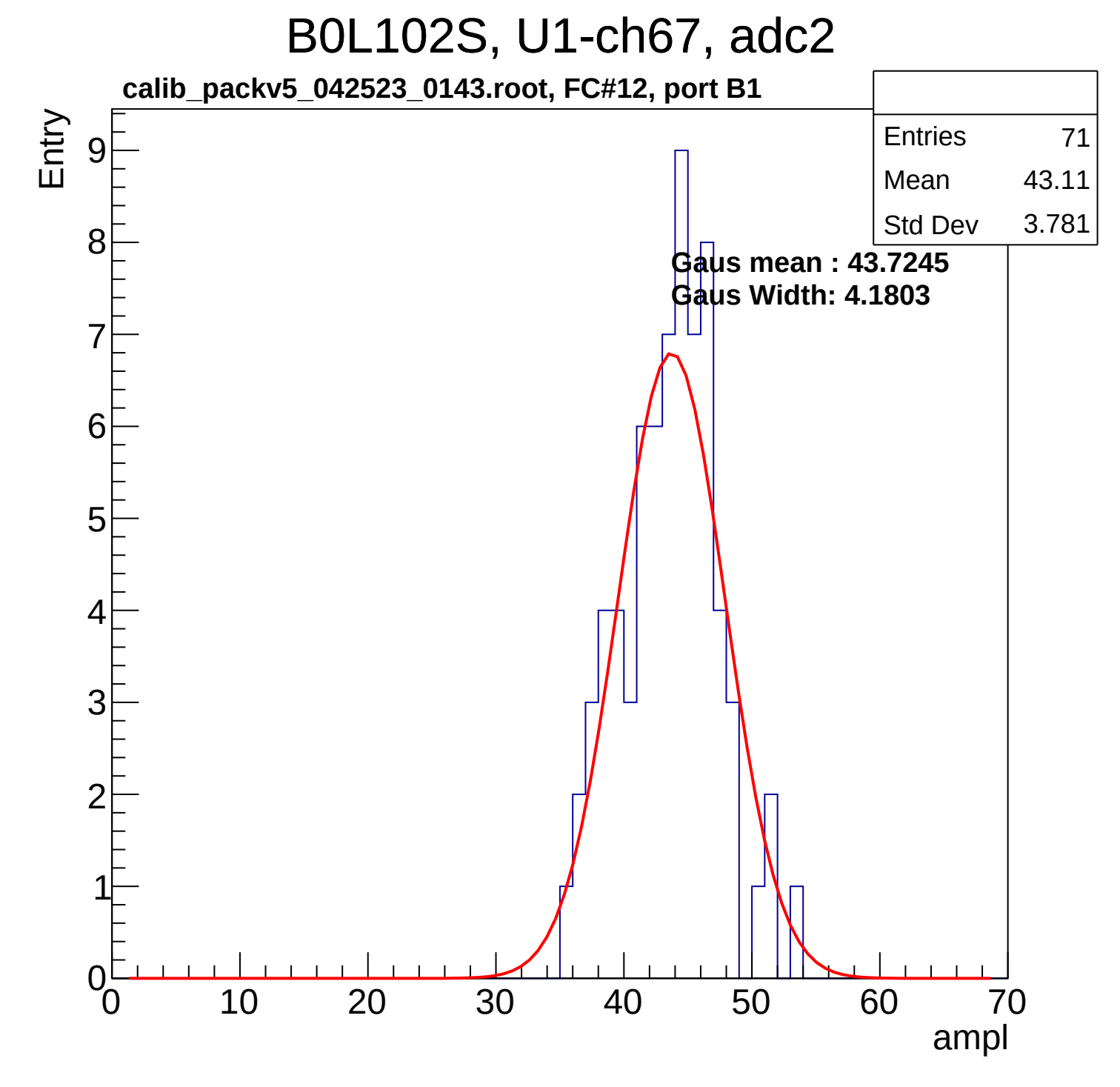
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch67, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.94
Std Dev 5.011
Gaus mean : 29.8515
Gaus Width: 3.7231
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch67, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.77
Std Dev 3.246
Gaus mean : 37.6059
Gaus Width: 3.6392
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch67, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 43.11
Std Dev 3.781
Gaus mean : 43.7245
Gaus Width: 4.1803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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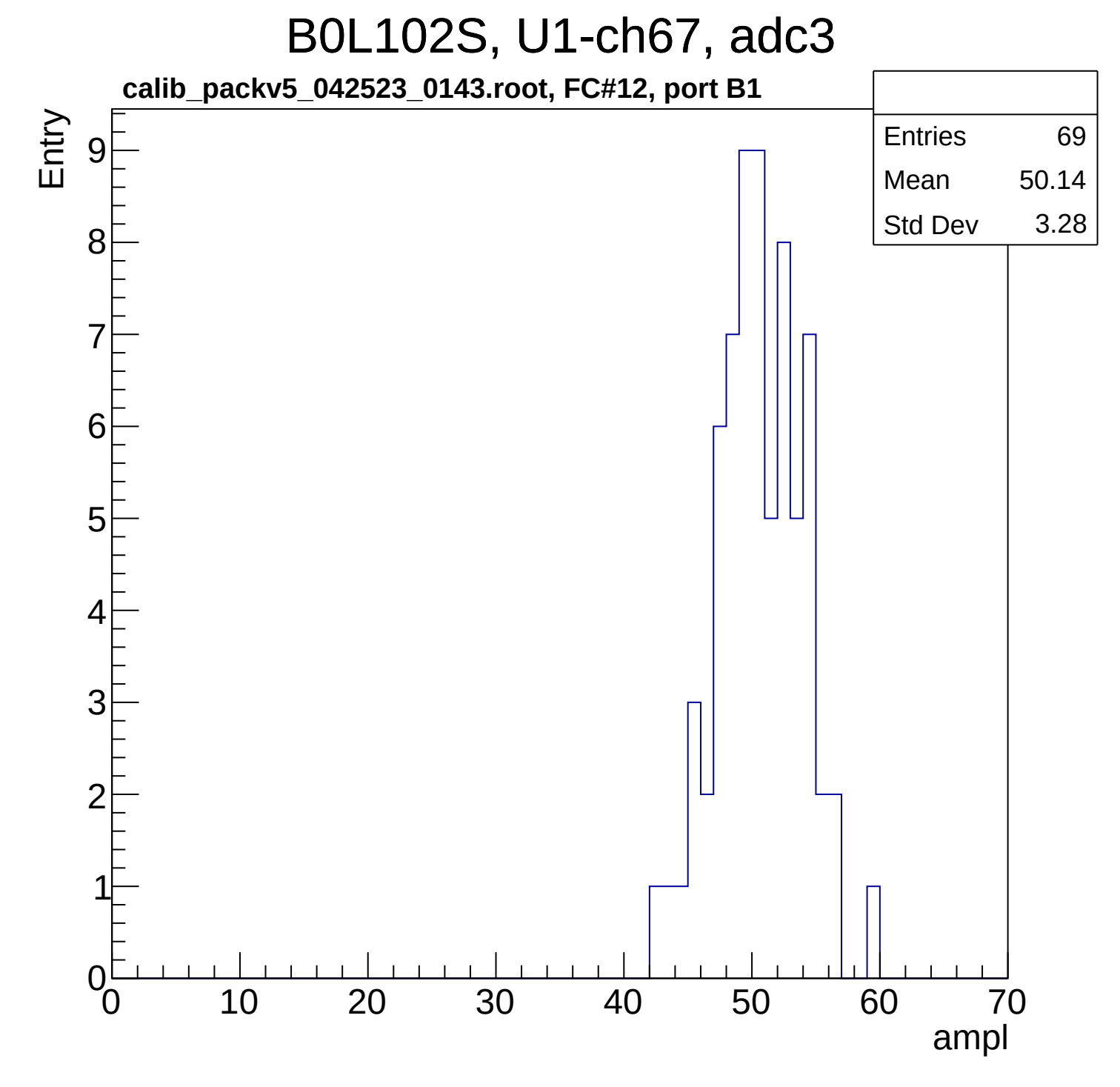
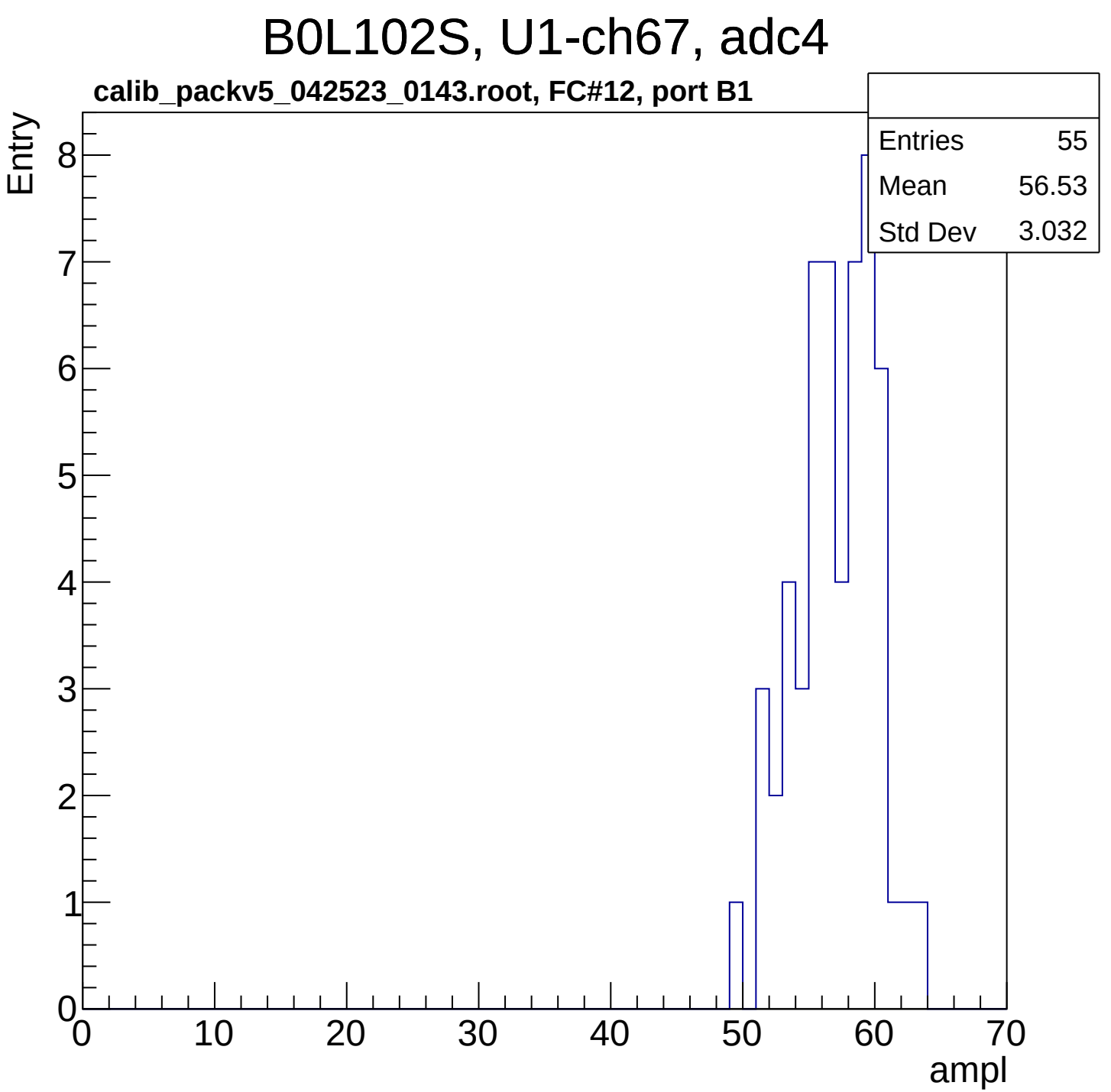
50
60
70


B0L102S, U1-ch67, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 50.14
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch67, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.53
Std Dev 3.032
Entry
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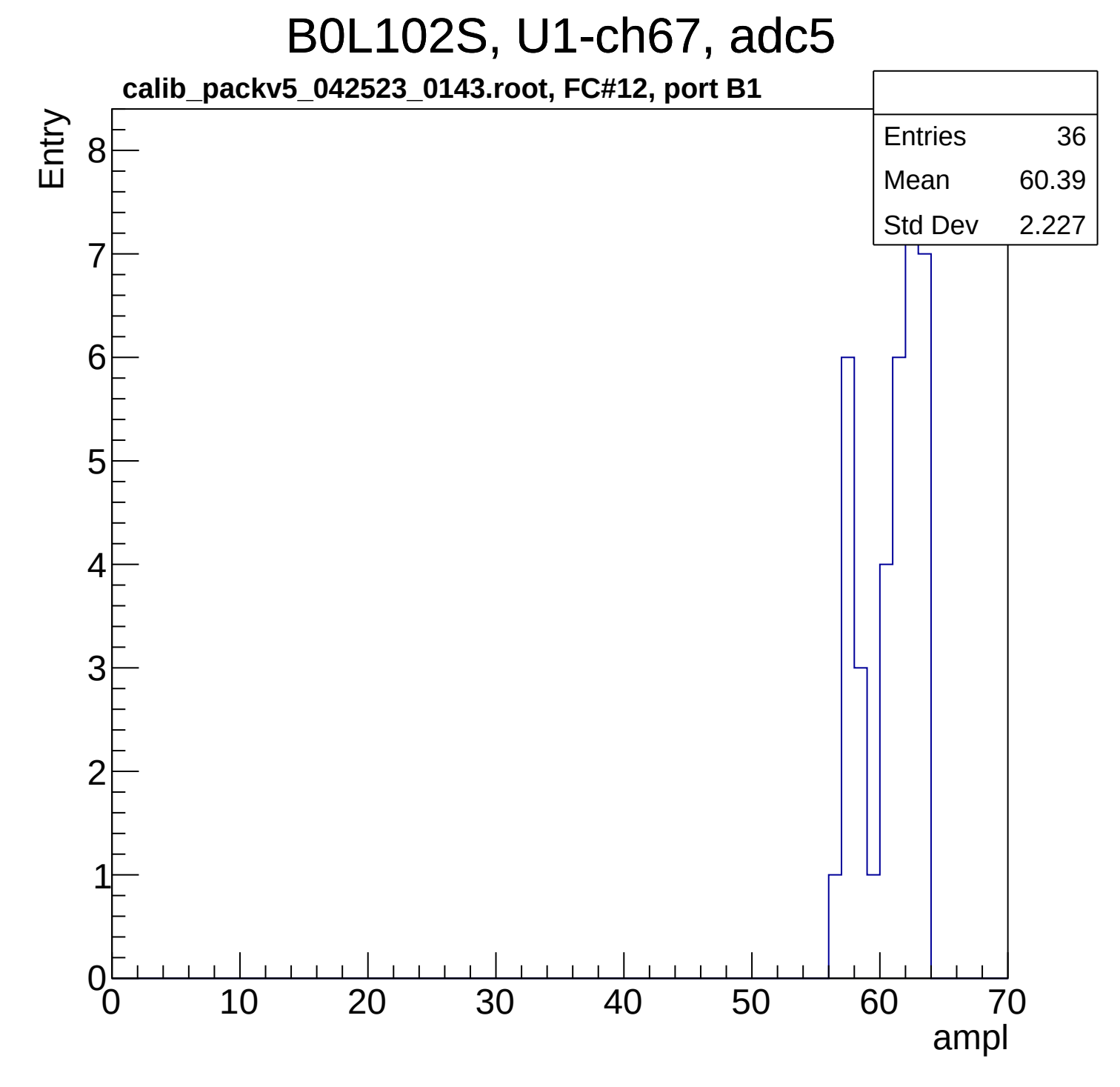
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch67, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 60.39
Std Dev 2.227
Entry
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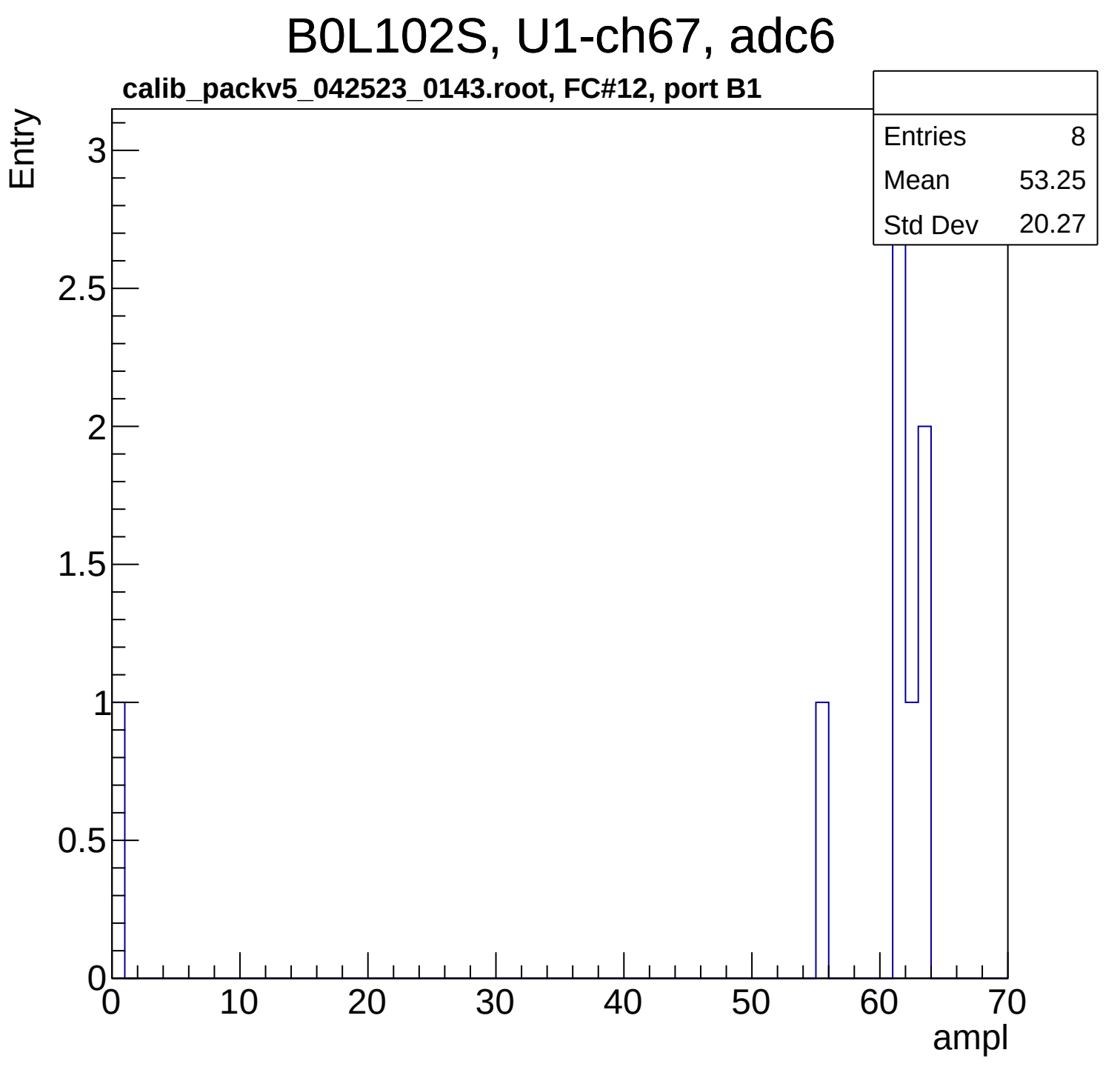
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch67, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 53.25
Std Dev 20.27
Entry
ampl
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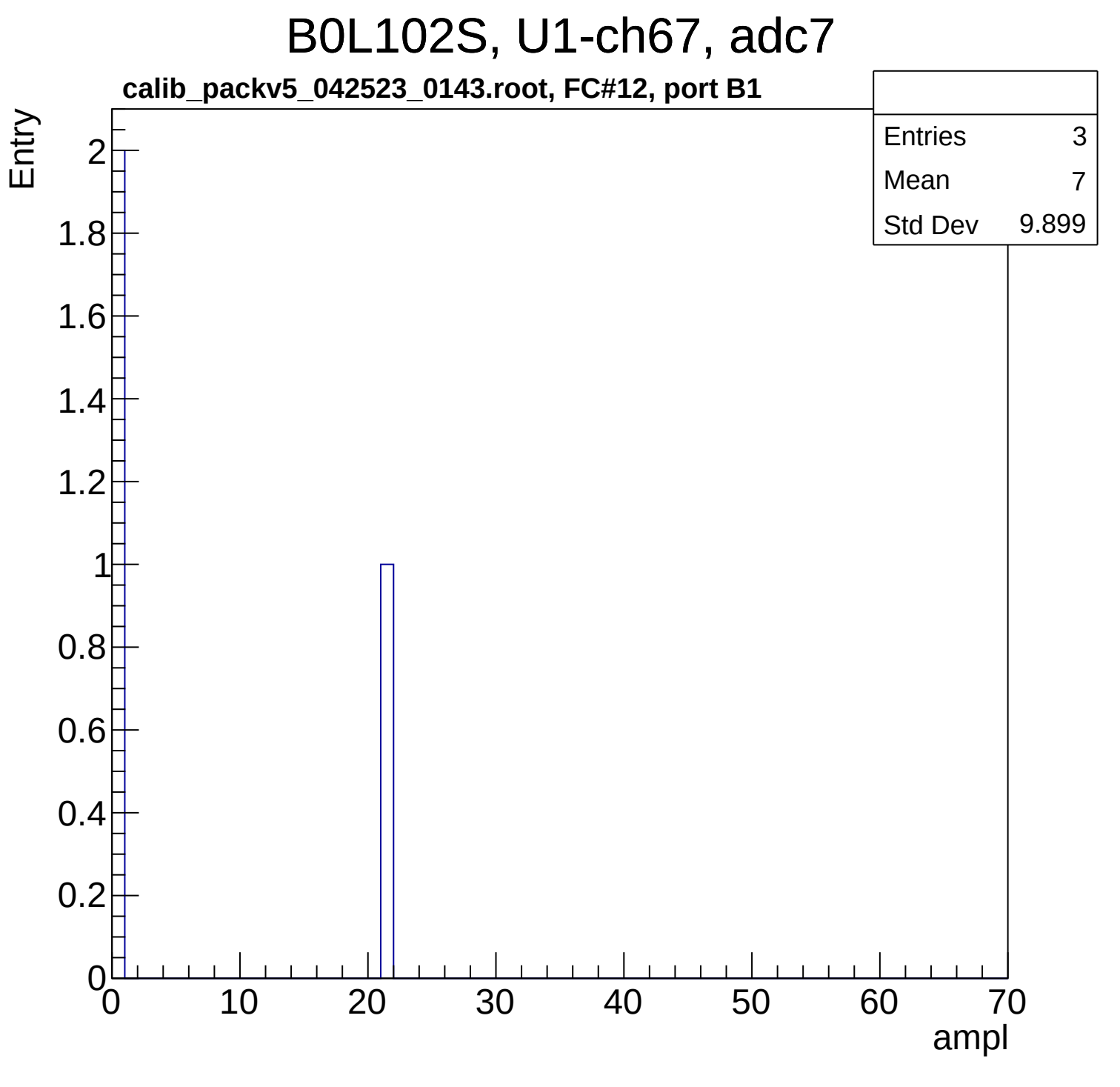
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch67, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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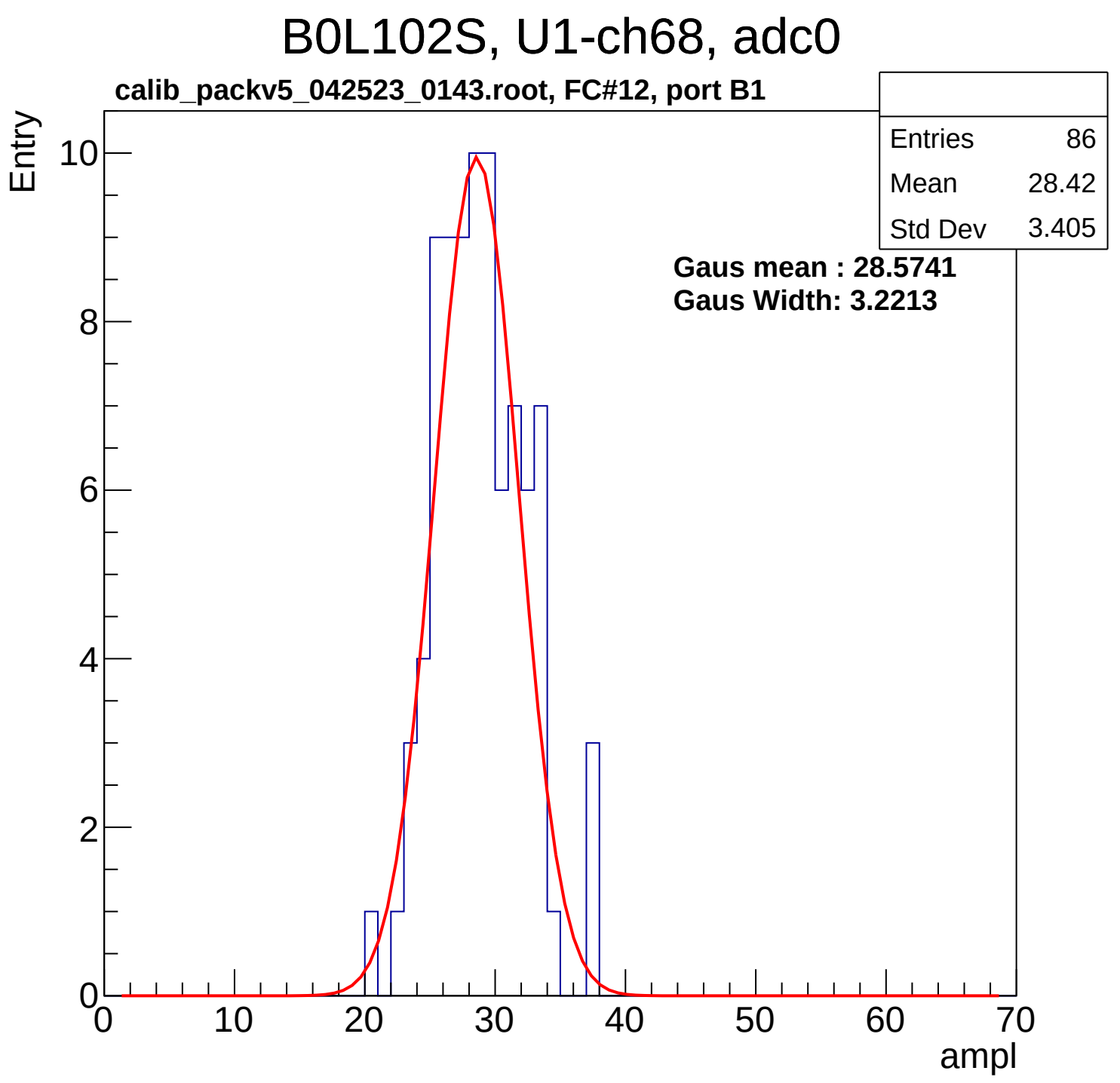
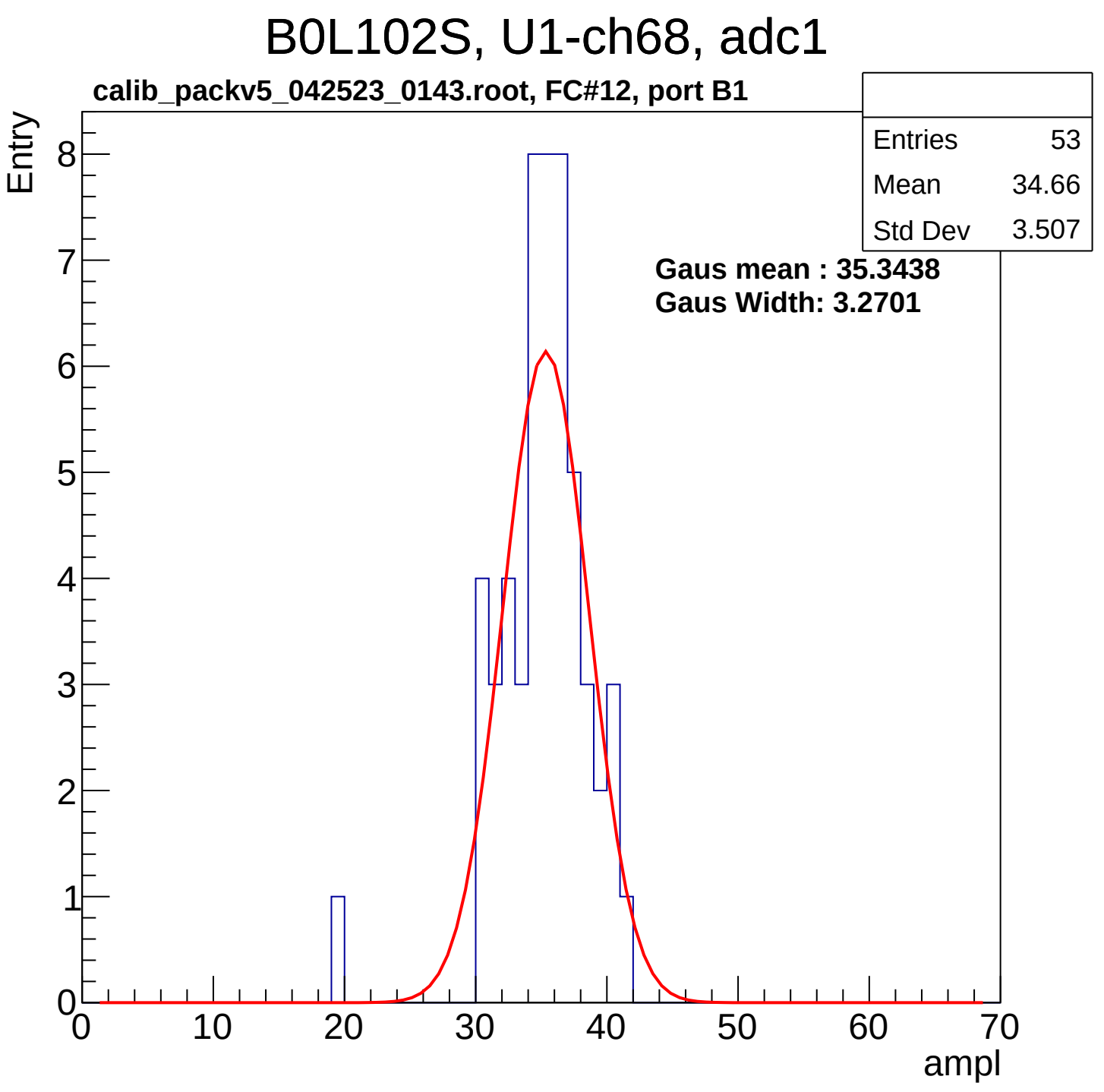
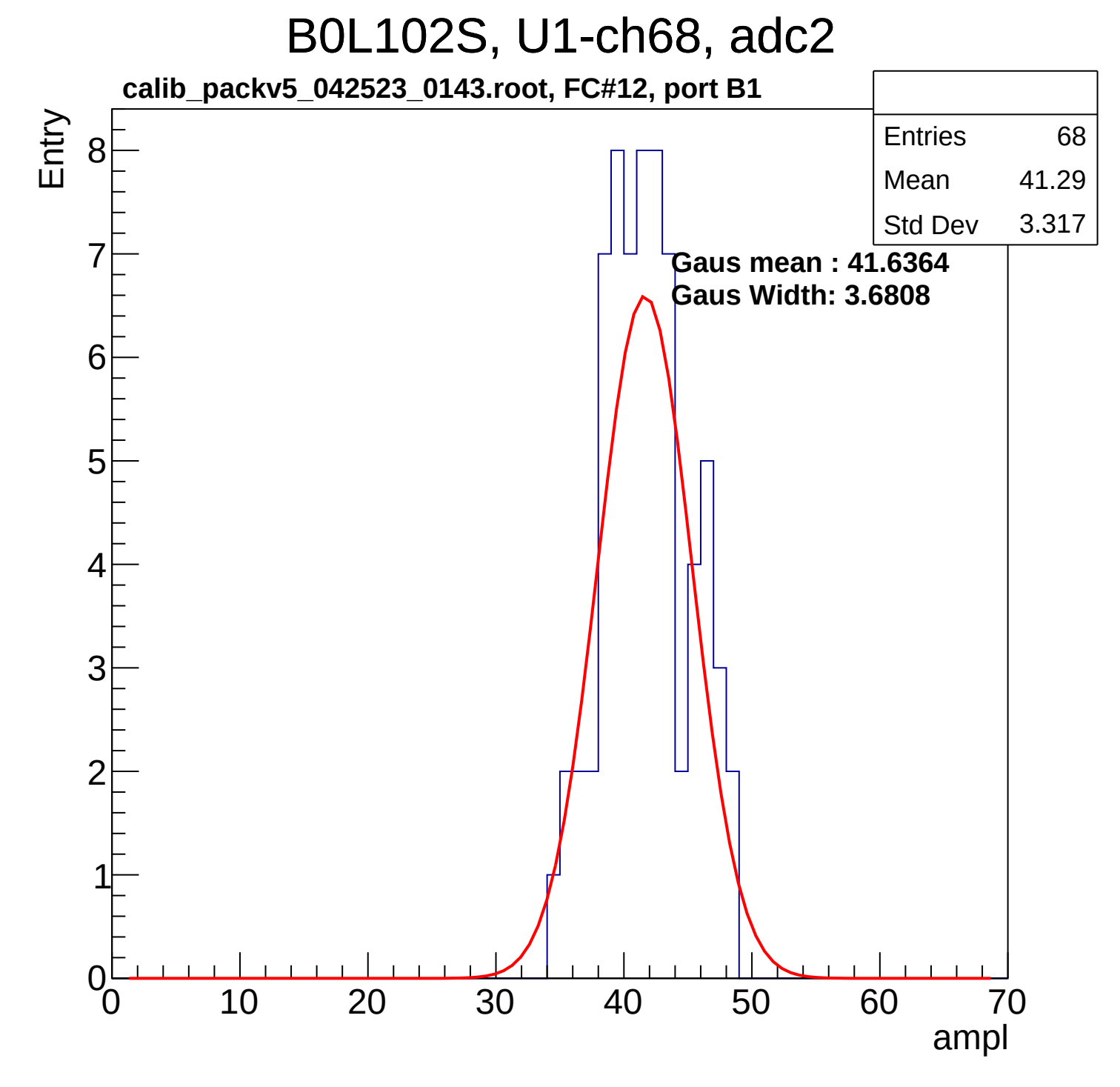
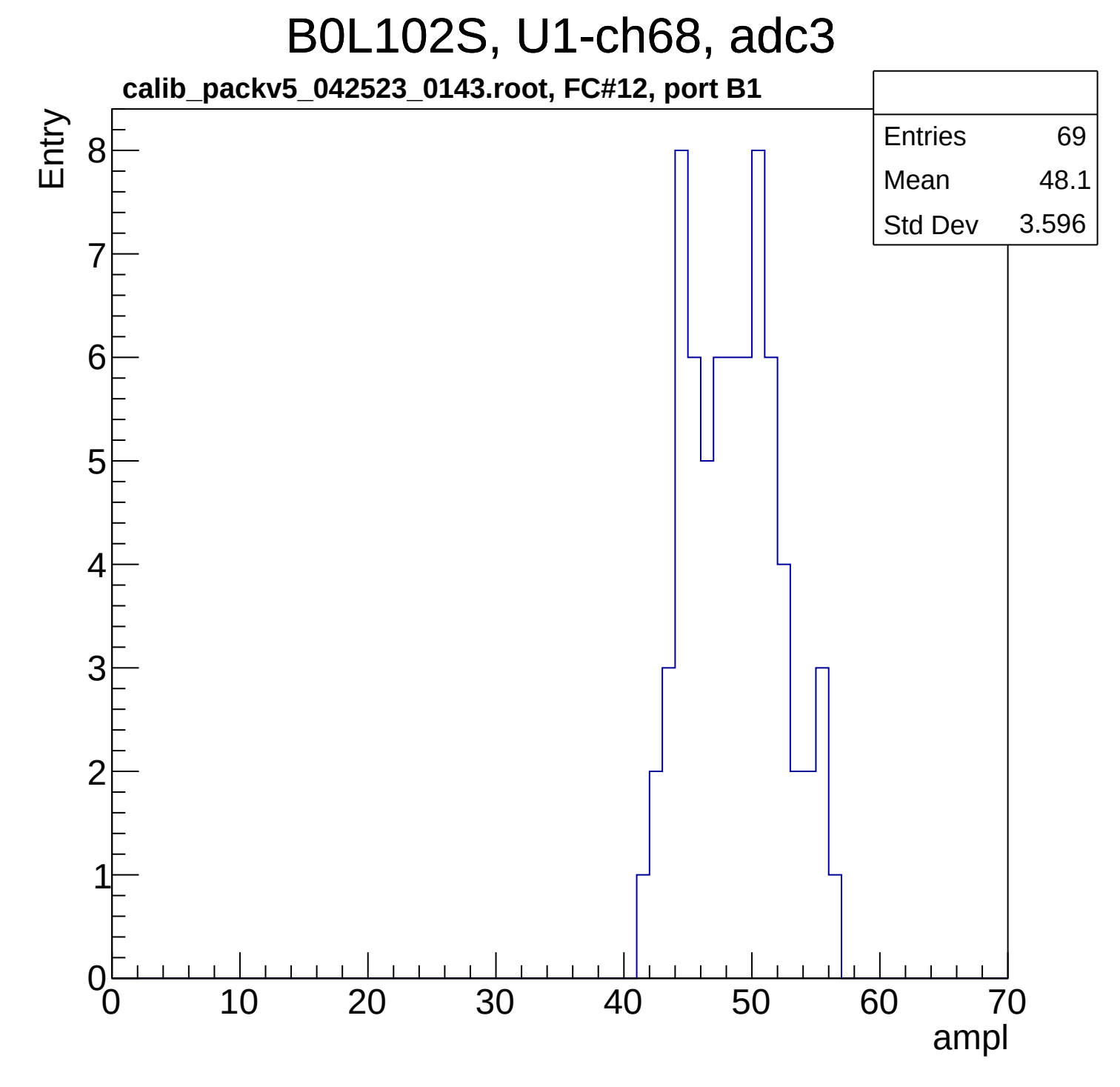
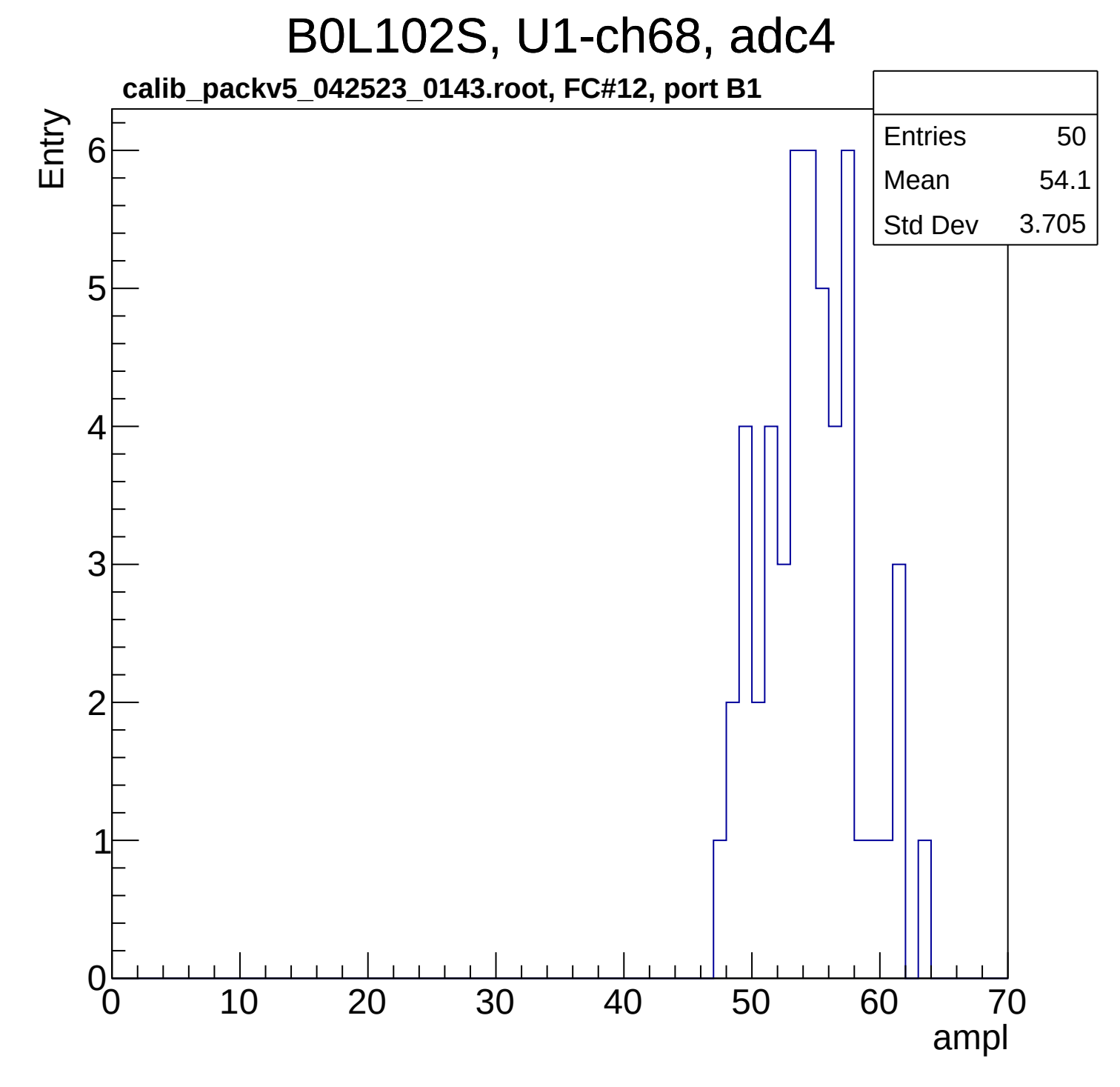
40
50
60
70

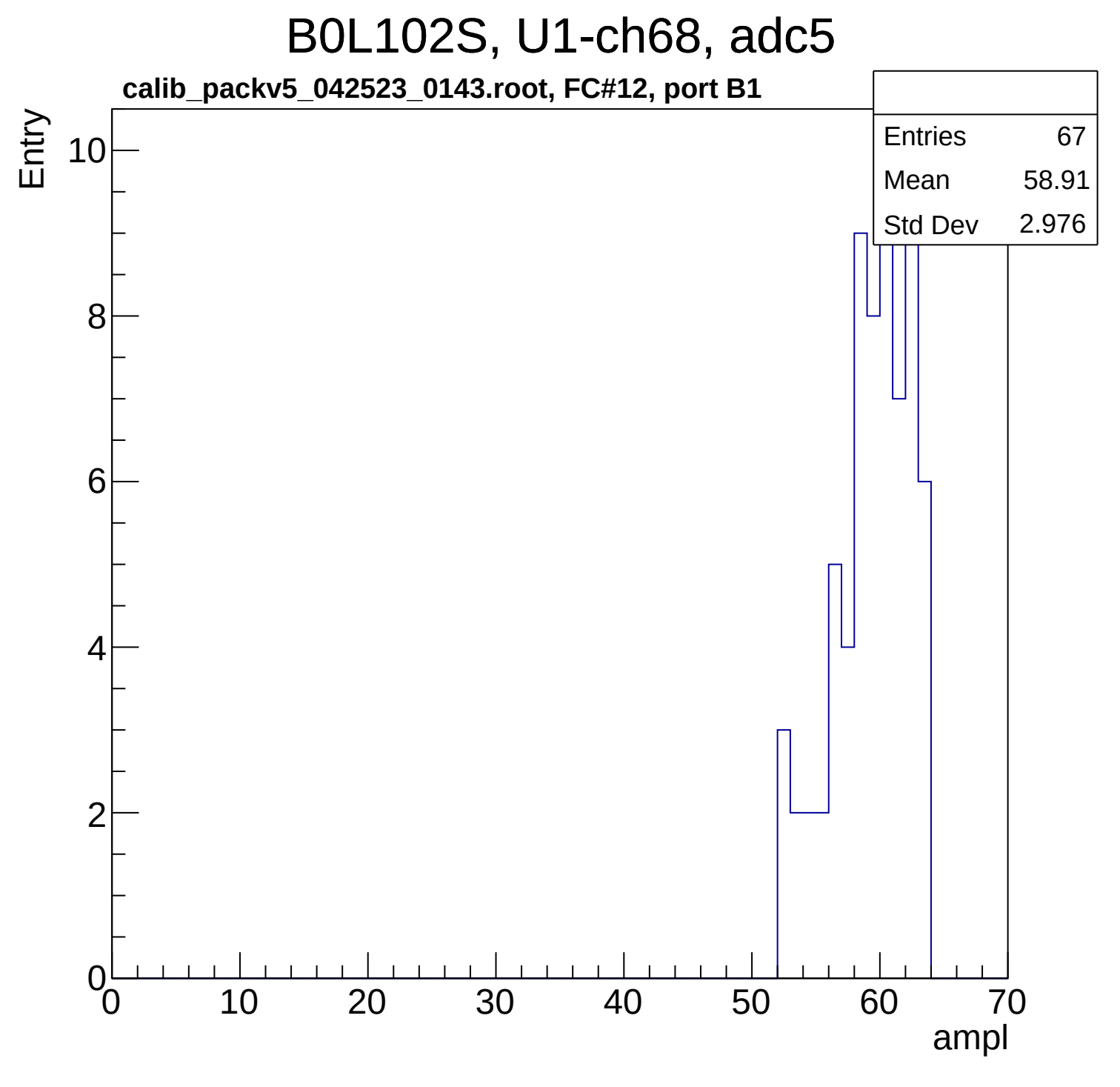

B0L102S, U1-ch68, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 28.42
Std Dev 3.405
Gaus mean : 28.5741
Gaus Width: 3.2213
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

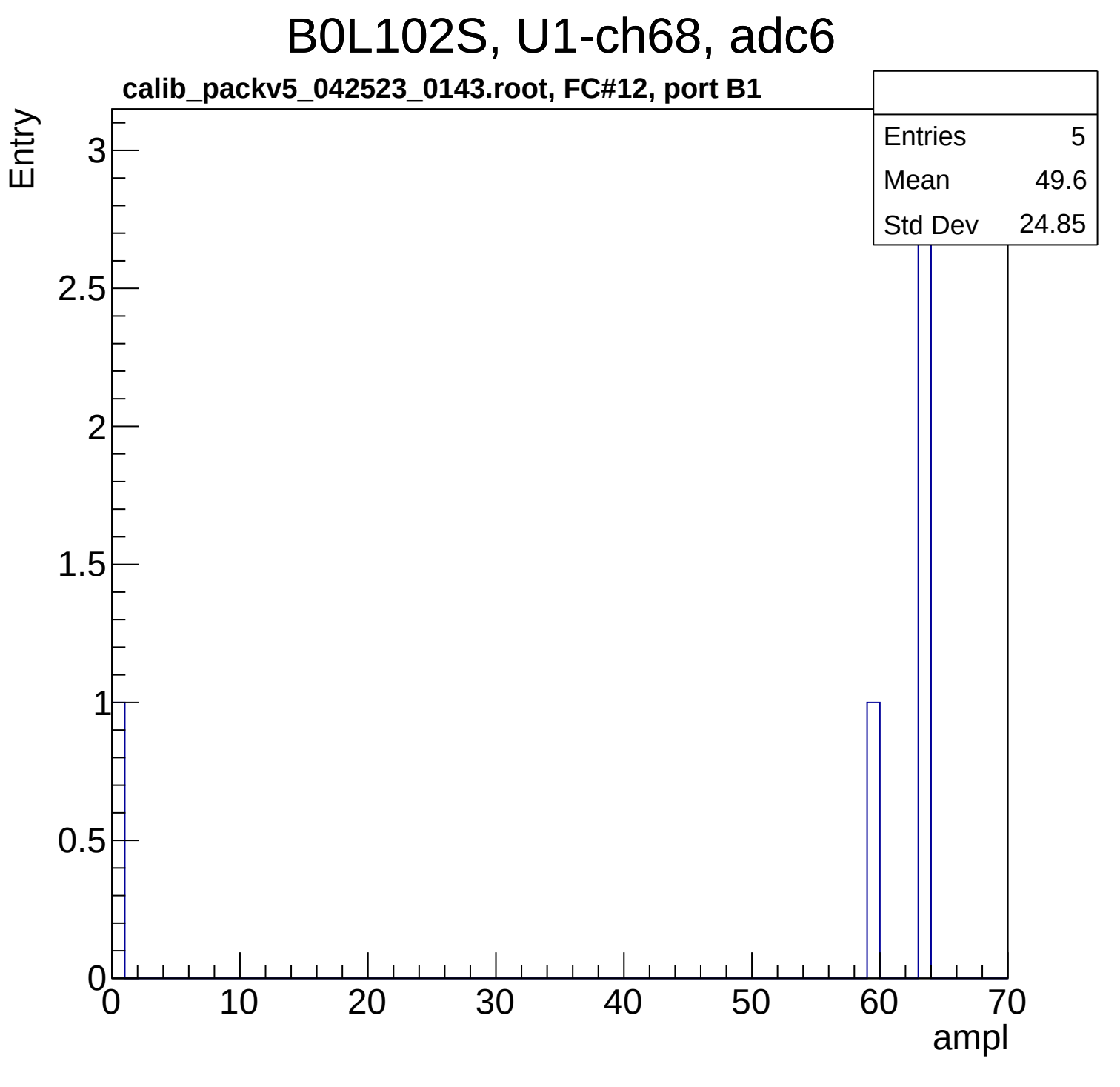

B0L102S, U1-ch68, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 34.66
Std Dev 3.507
Gaus mean : 35.3438
Gaus Width: 3.2701
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

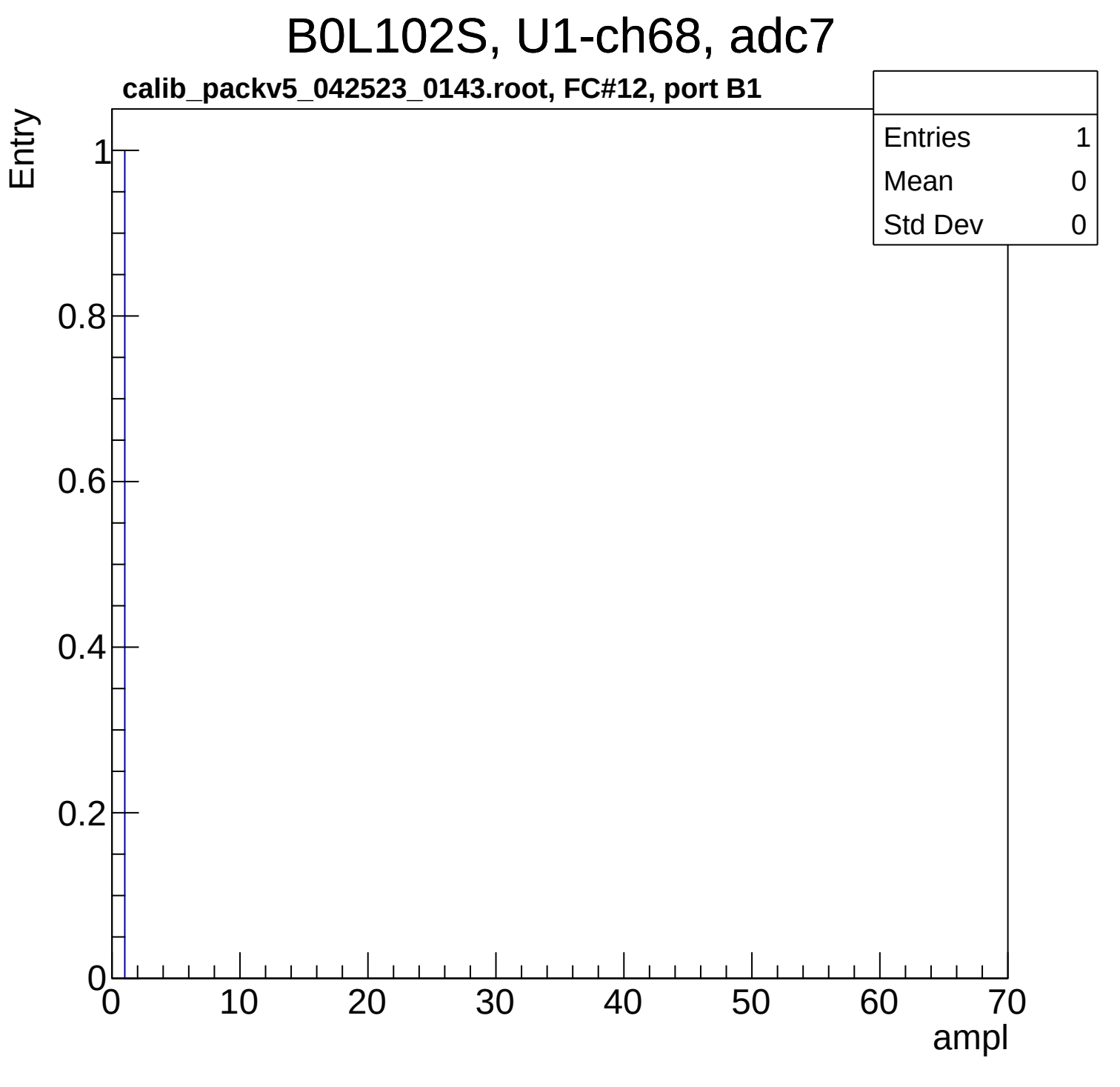

B0L102S, U1-ch68, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.29
Std Dev 3.317
Gaus mean : 41.6364
Gaus Width: 3.6808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

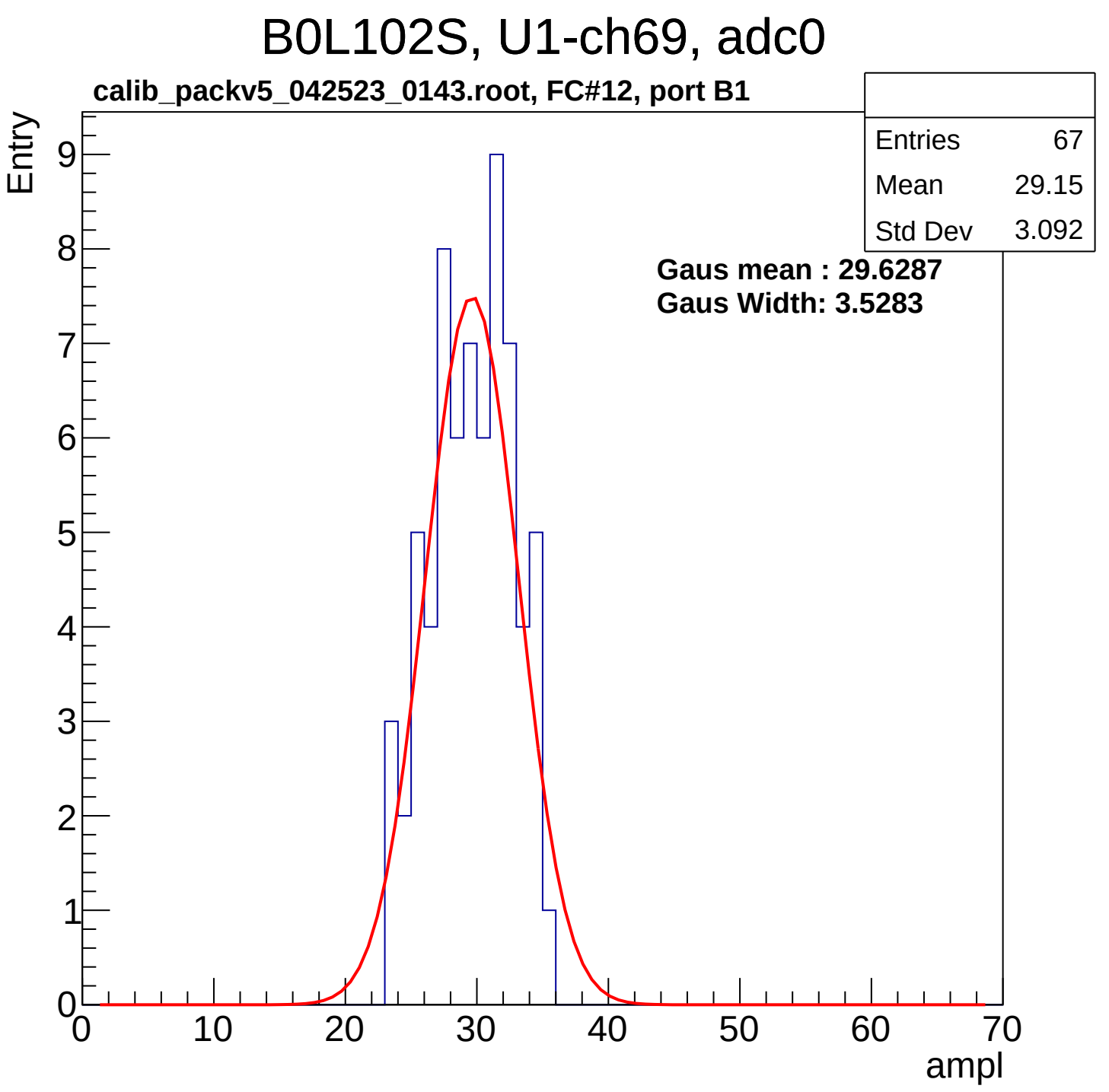

B0L102S, U1-ch68, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 48.1
Std Dev 3.596
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

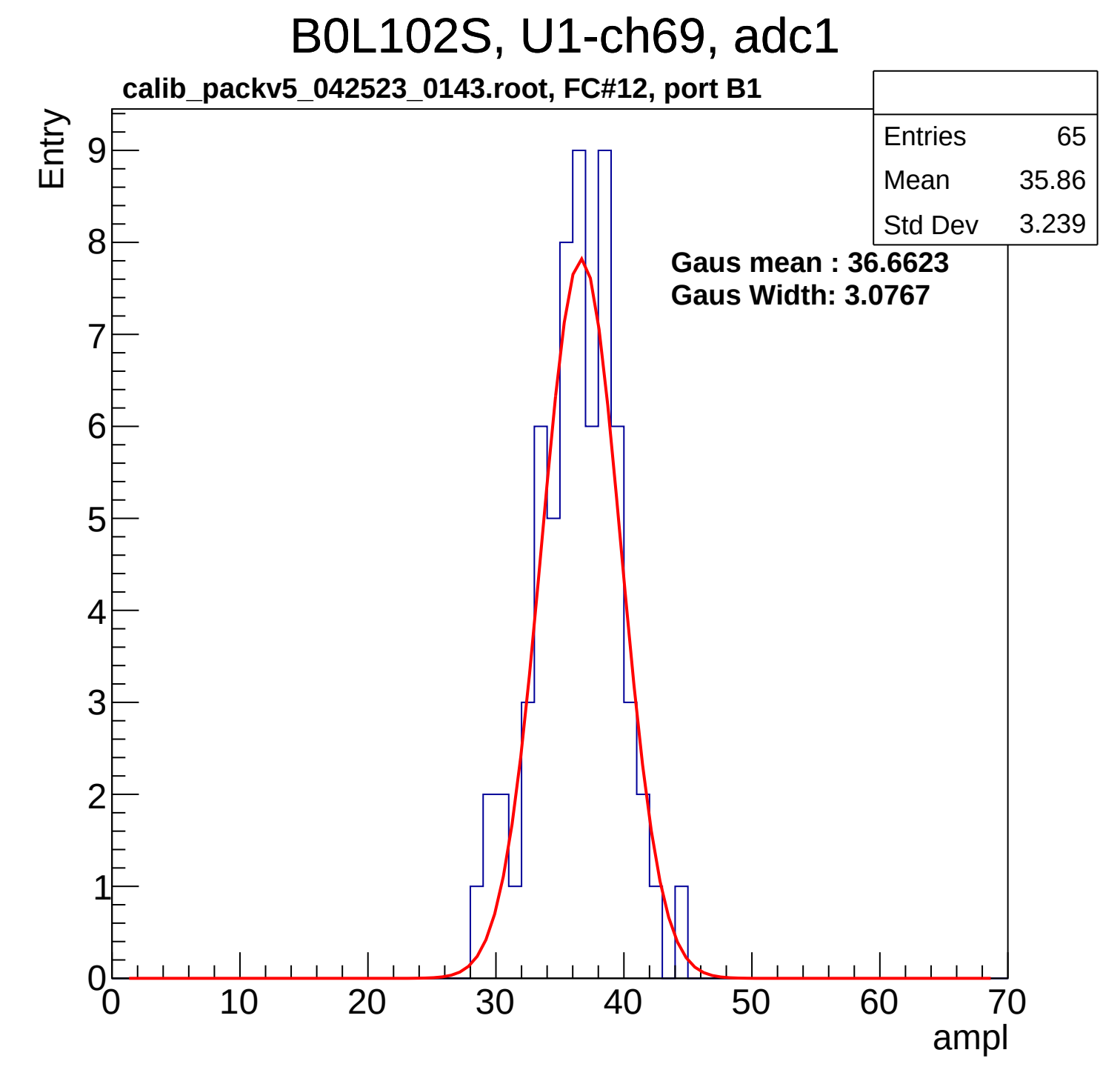

B0L102S, U1-ch68, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 54.1
Std Dev 3.705
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

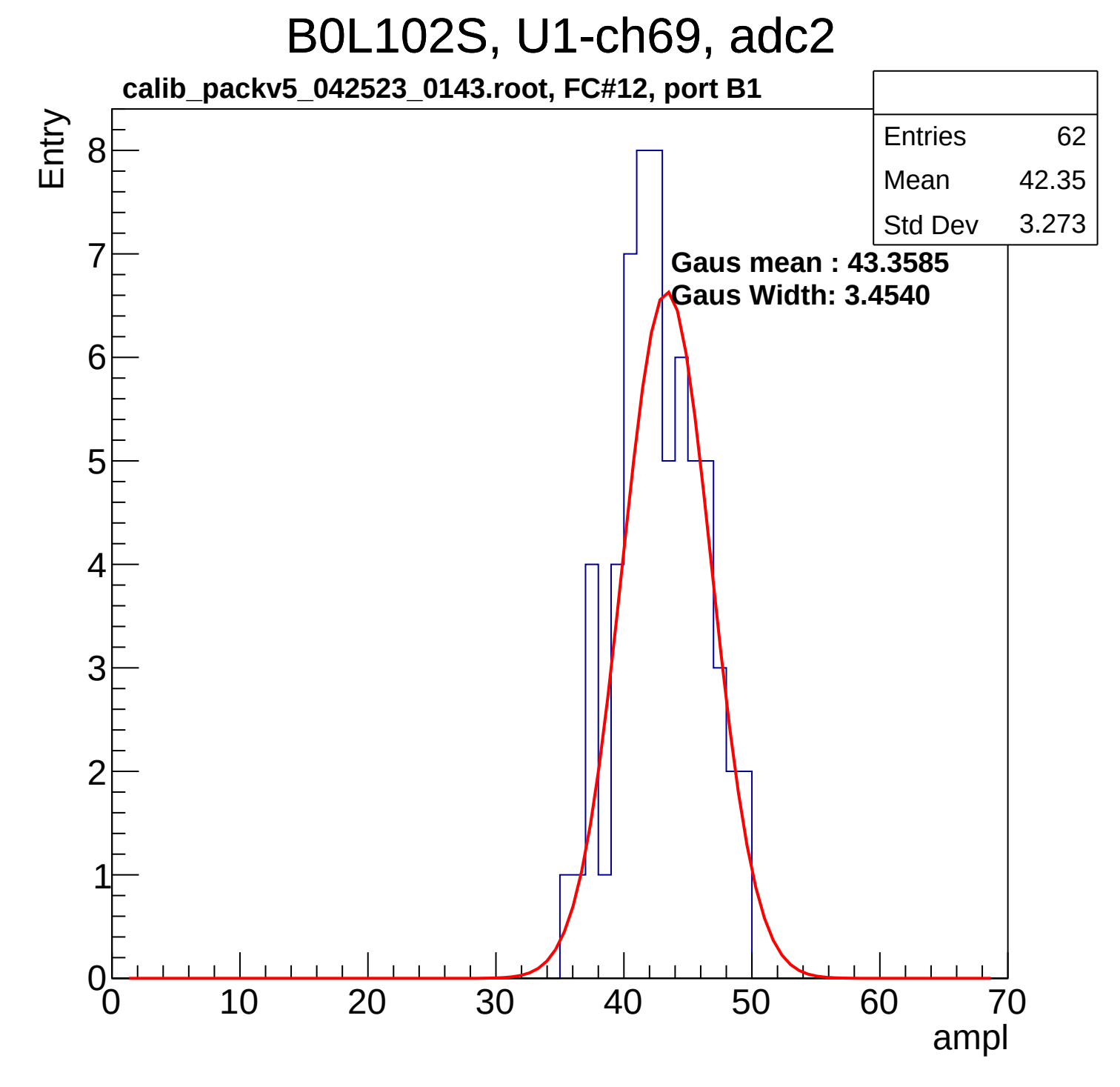

B0L102S, U1-ch68, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 58.91
Std Dev 2.976
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

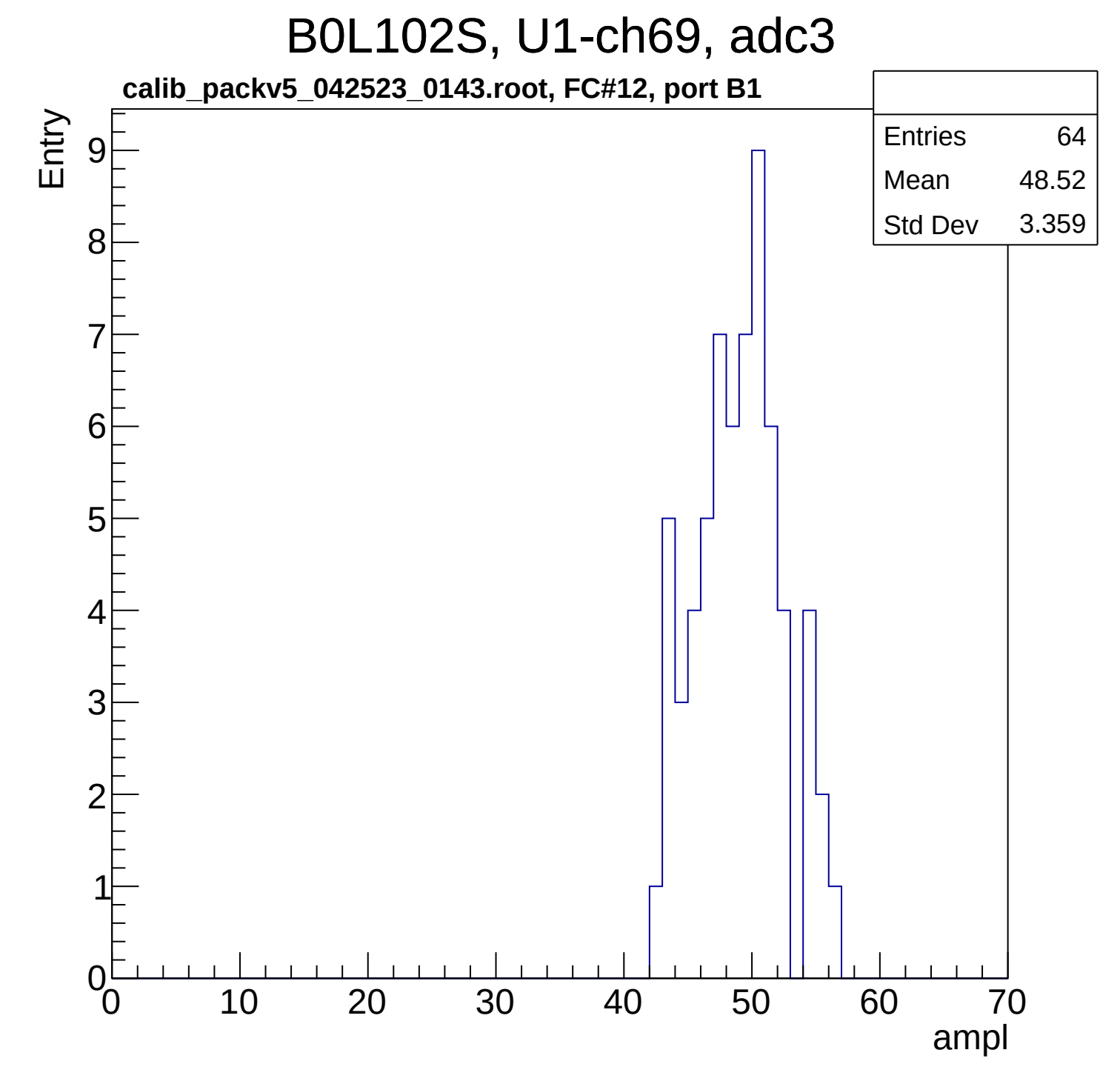

B0L102S, U1-ch68, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 49.6
Std Dev 24.85
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

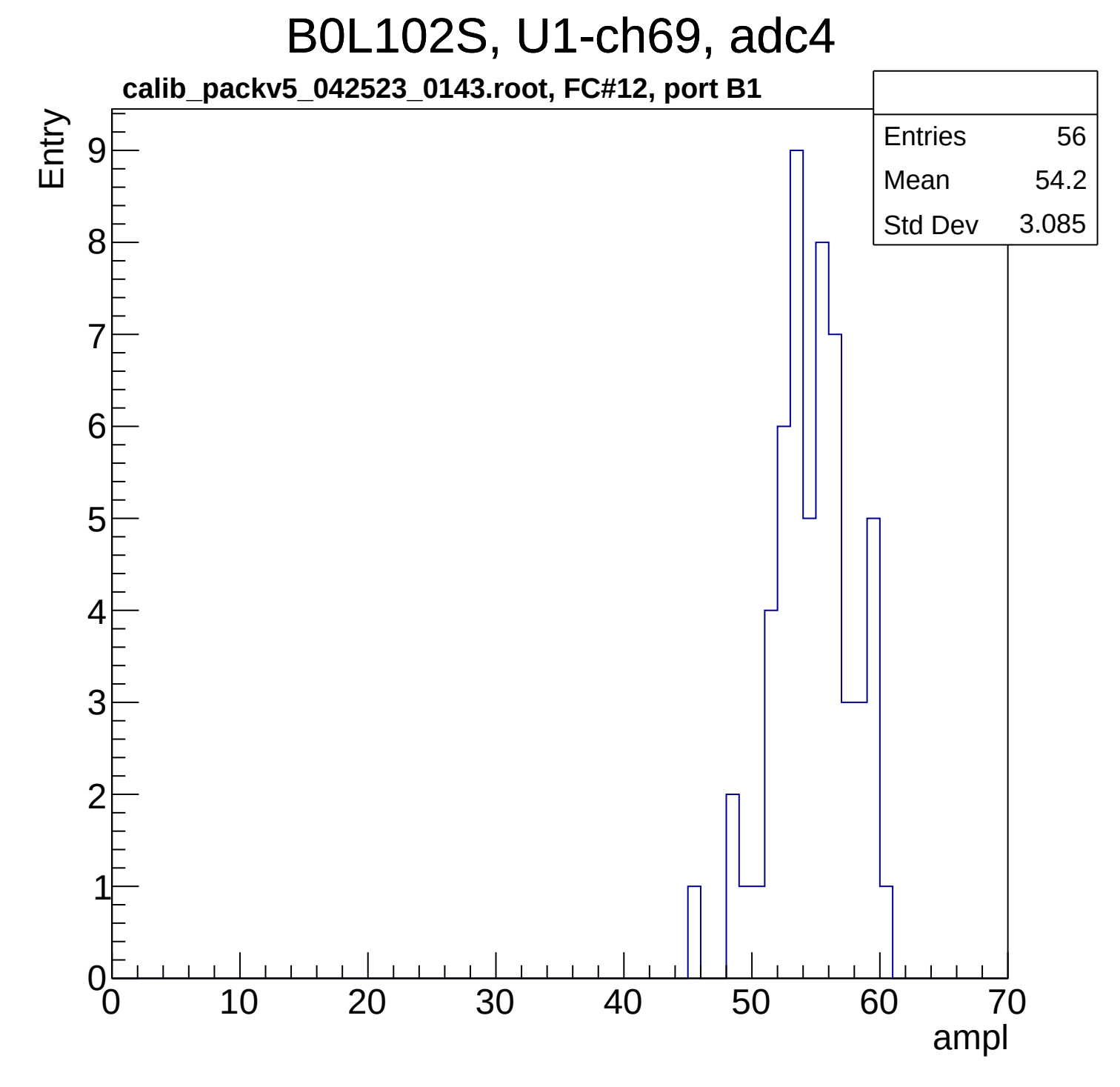

B0L102S, U1-ch68, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

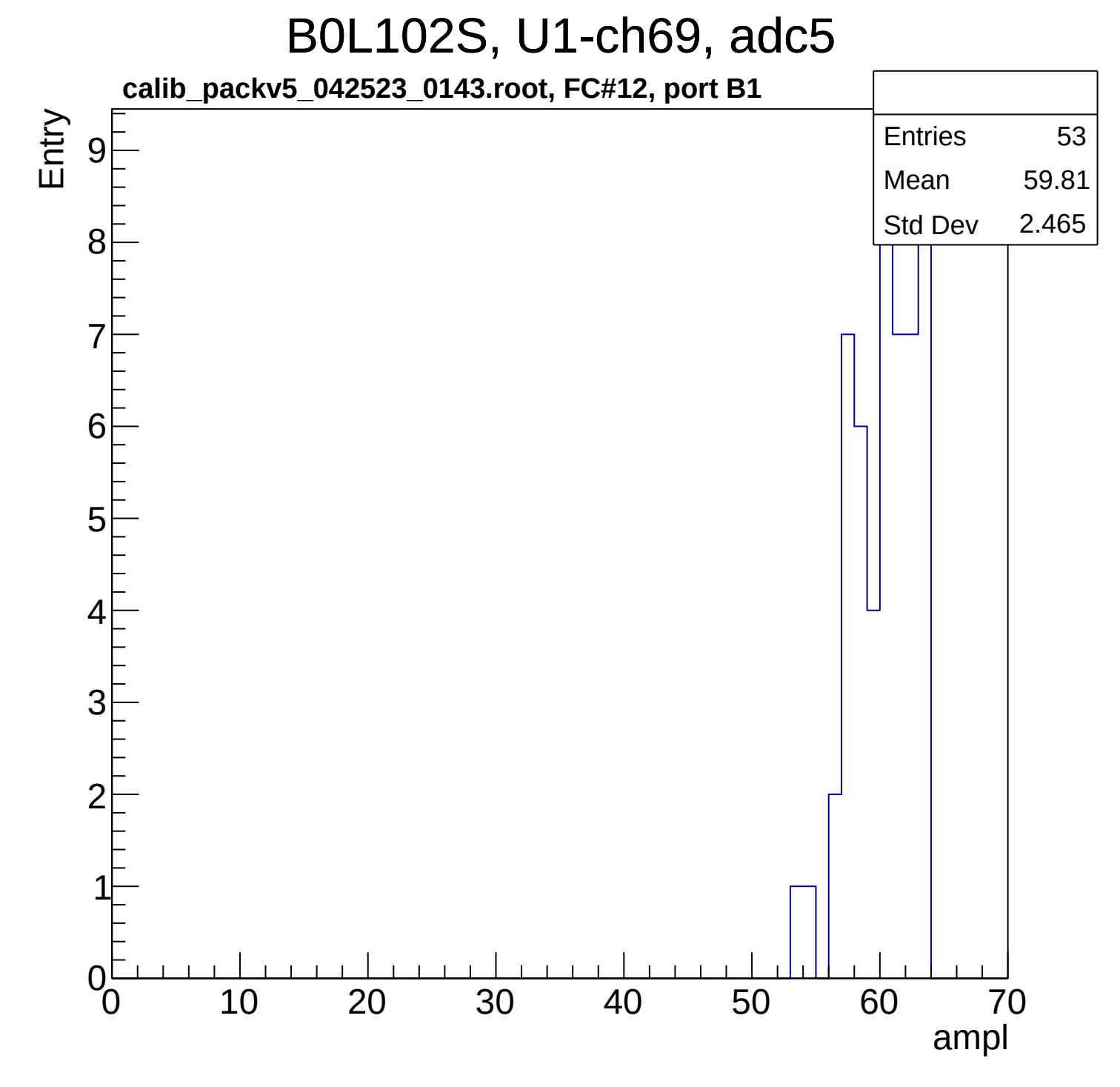

B0L102S, U1-ch69, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.15
Std Dev 3.092
Gaus mean : 29.6287
Gaus Width: 3.5283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

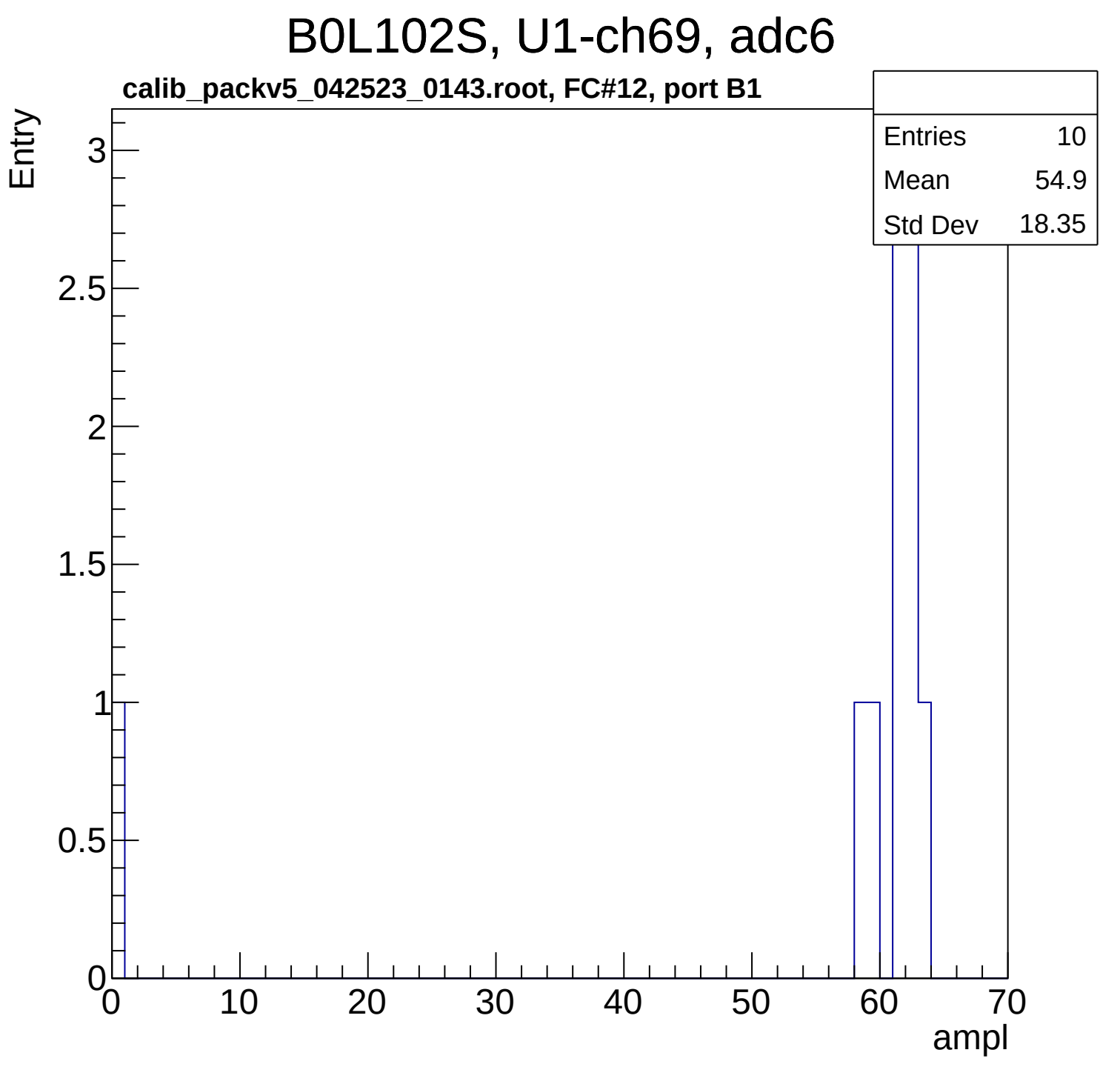

B0L102S, U1-ch69, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.86
Std Dev 3.239
Gaus mean : 36.6623
Gaus Width: 3.0767
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

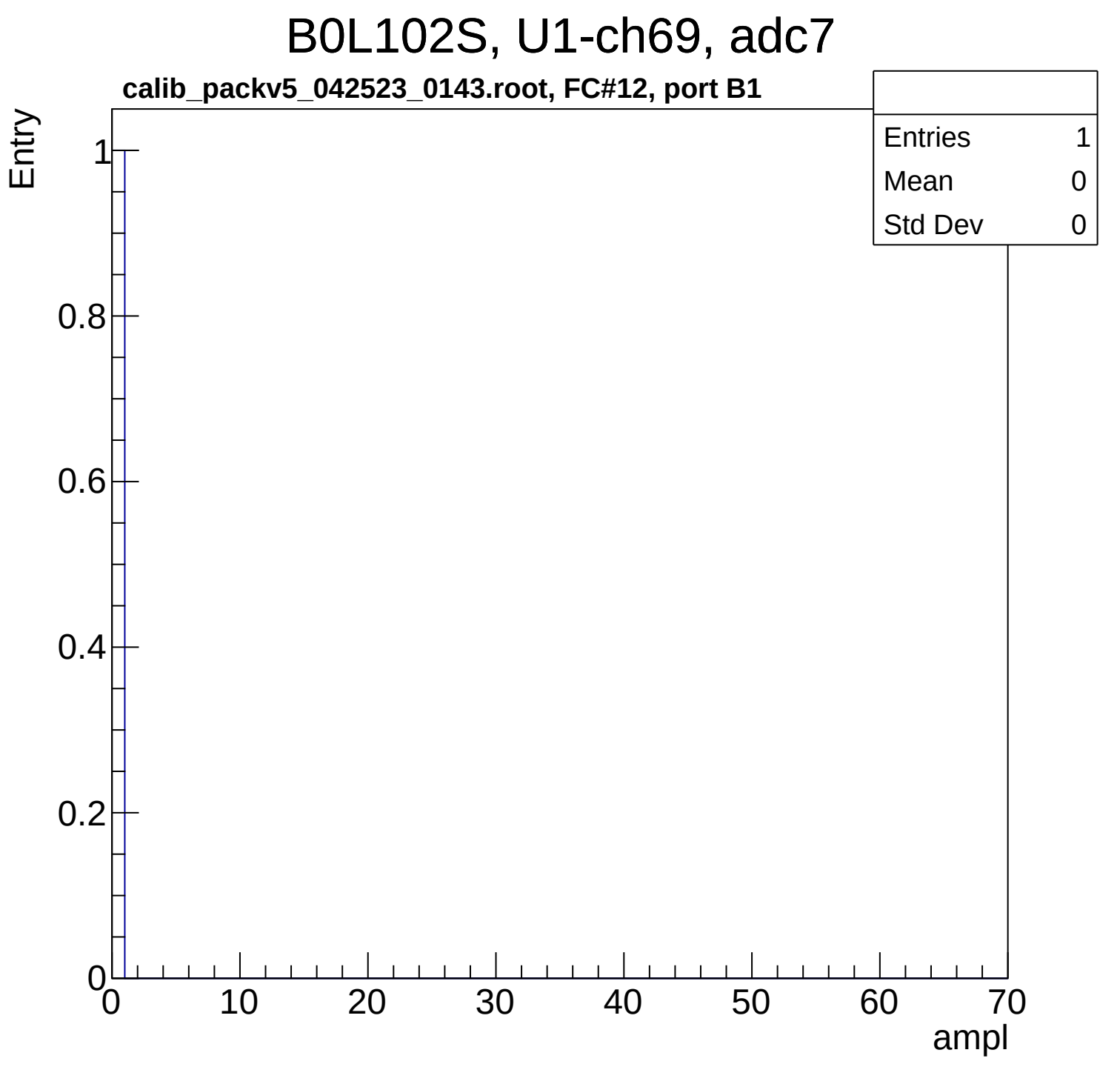

B0L102S, U1-ch69, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.35
Std Dev 3.273
Gaus mean : 43.3585
Gaus Width: 3.4540
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

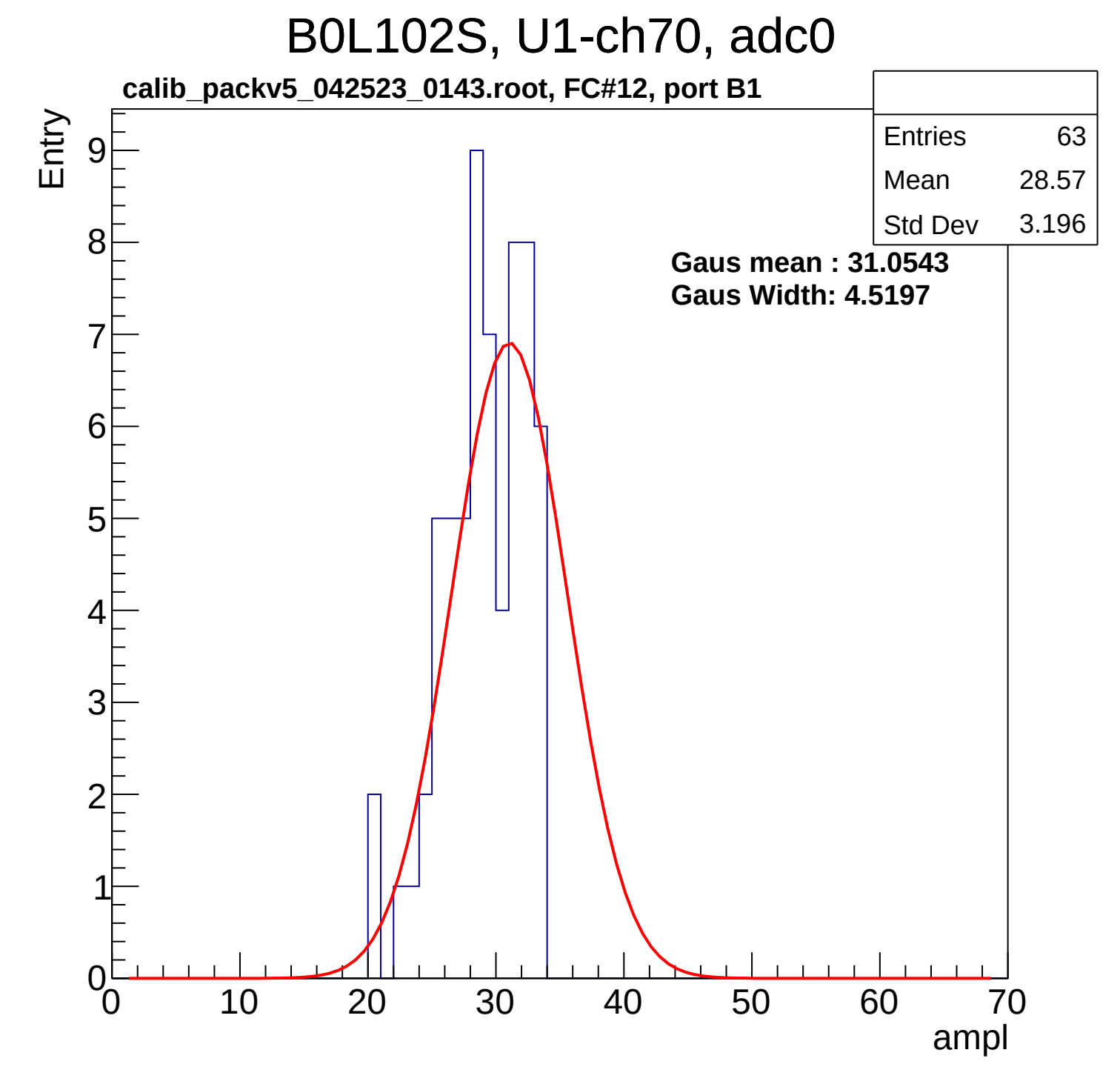

B0L102S, U1-ch69, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 48.52
Std Dev 3.359
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

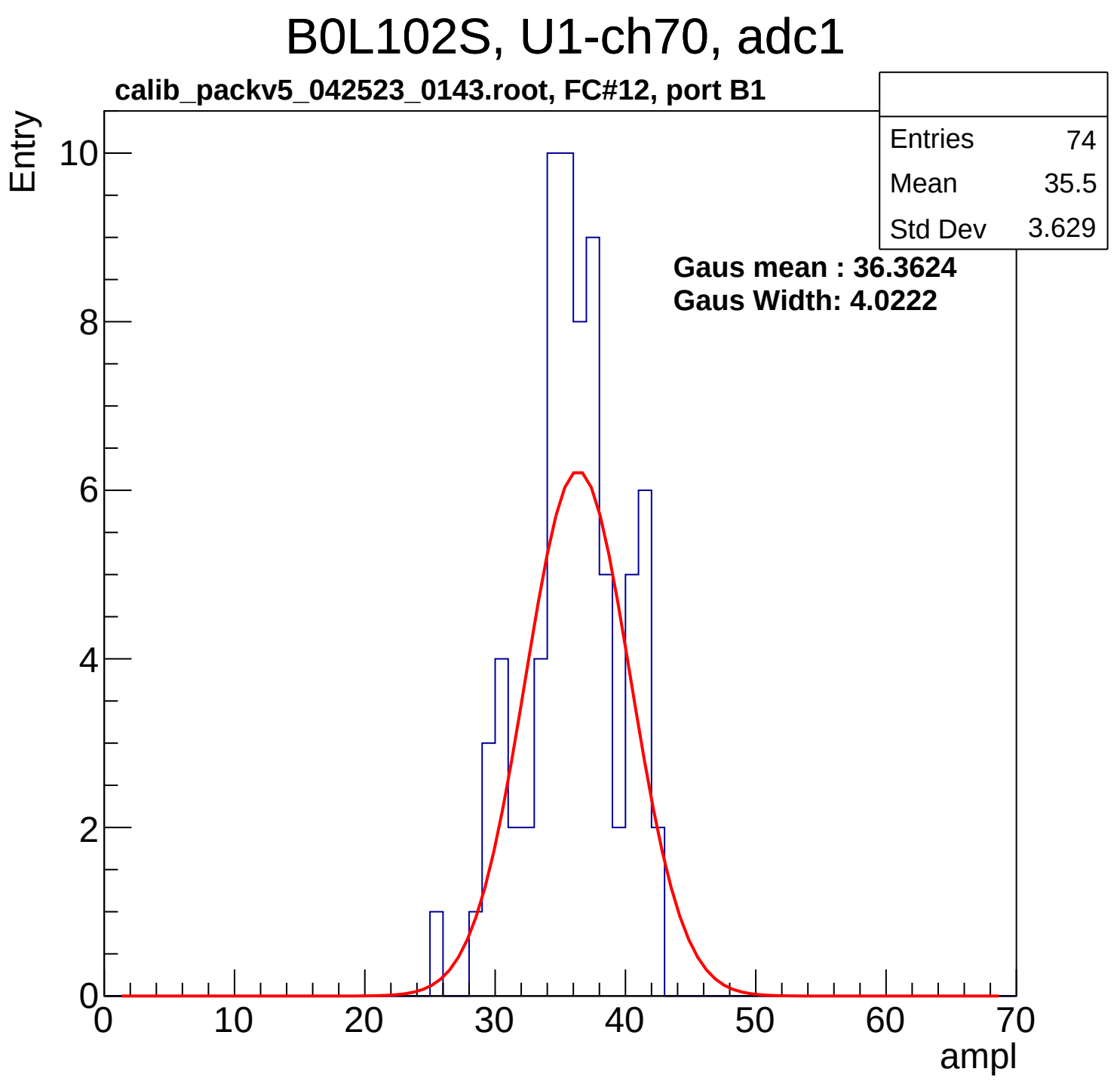

B0L102S, U1-ch69, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 54.2
Std Dev 3.085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

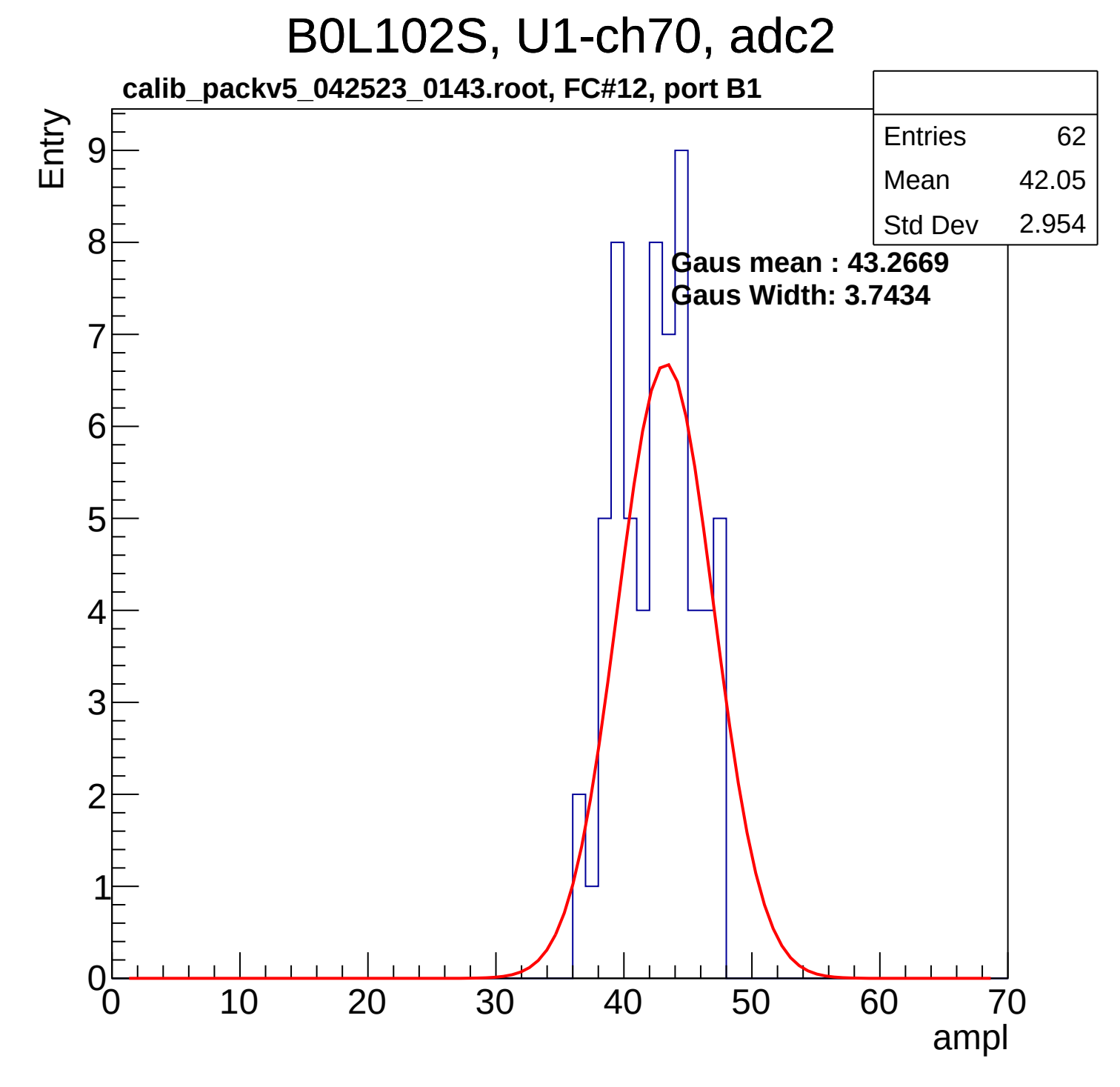

B0L102S, U1-ch69, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 59.81
Std Dev 2.465
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

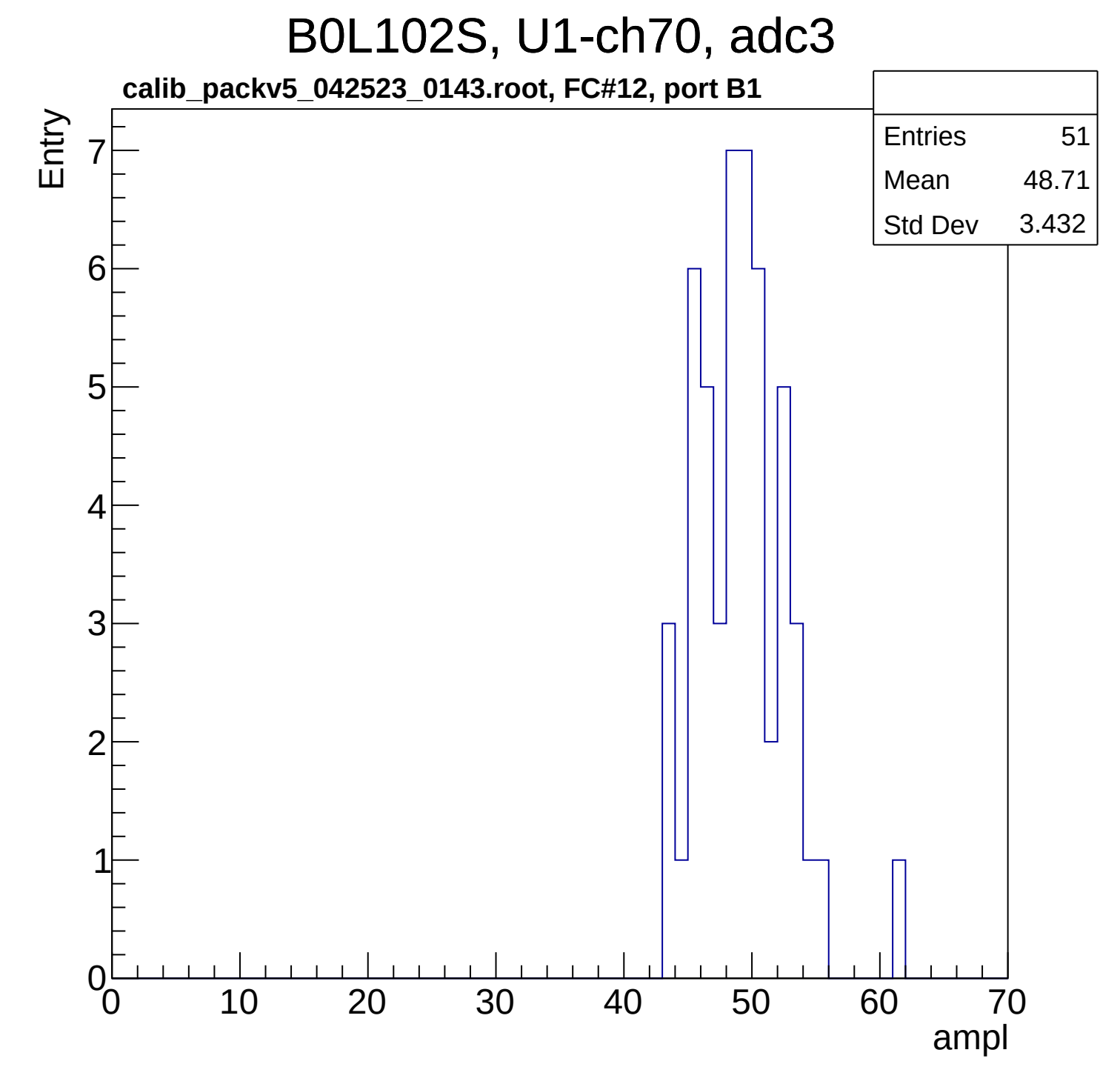

B0L102S, U1-ch69, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 54.9
Std Dev 18.35
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

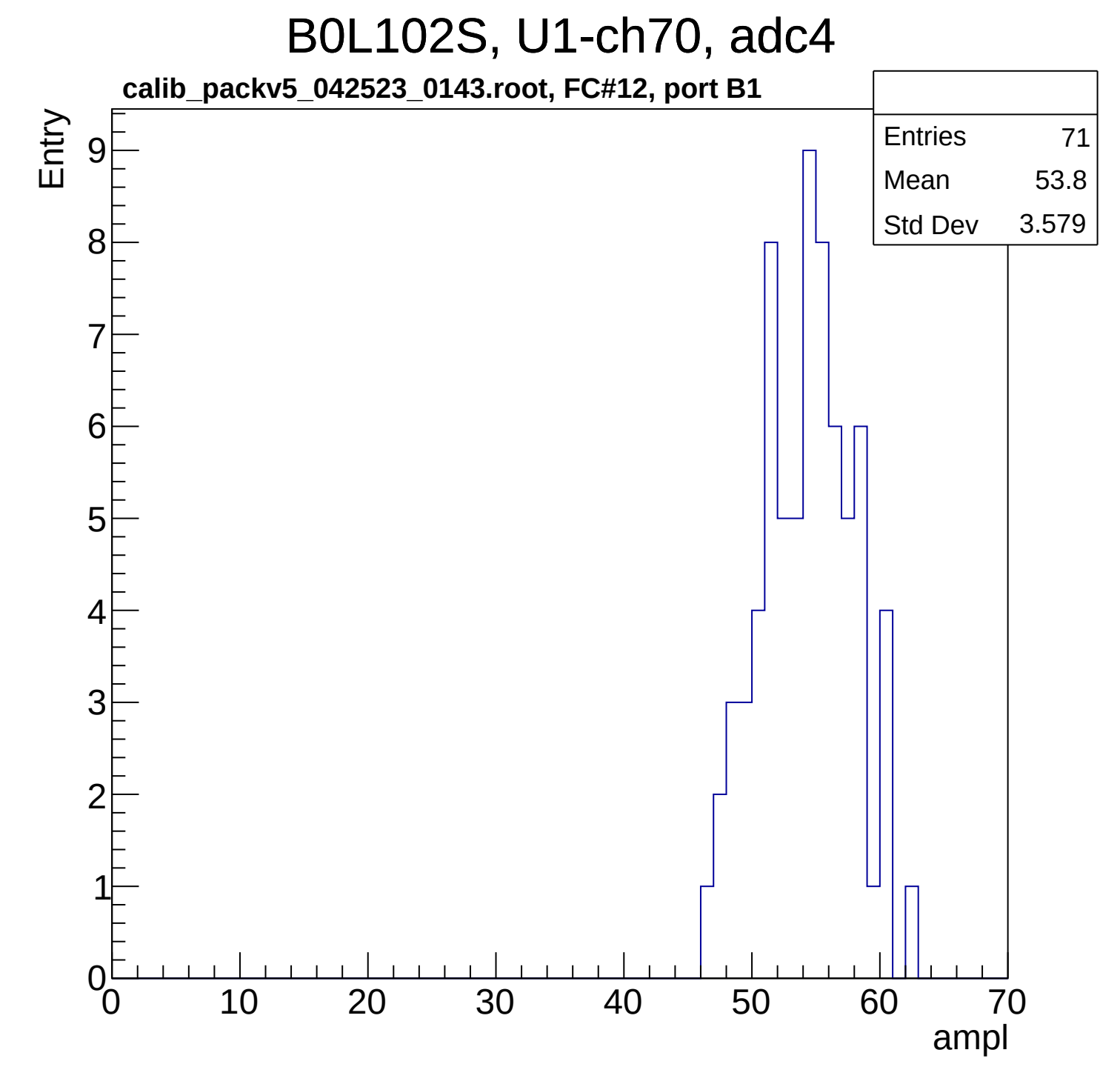

B0L102S, U1-ch69, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch70, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.57
Std Dev 3.196
Gaus mean : 31.0543
Gaus Width: 4.5197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch70, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 35.5
Std Dev 3.629
Gaus mean : 36.3624
Gaus Width: 4.0222
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch70, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.05
Std Dev 2.954
Gaus mean : 43.2669
Gaus Width: 3.7434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch70, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 48.71
Std Dev 3.432
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch70, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 53.8
Std Dev 3.579
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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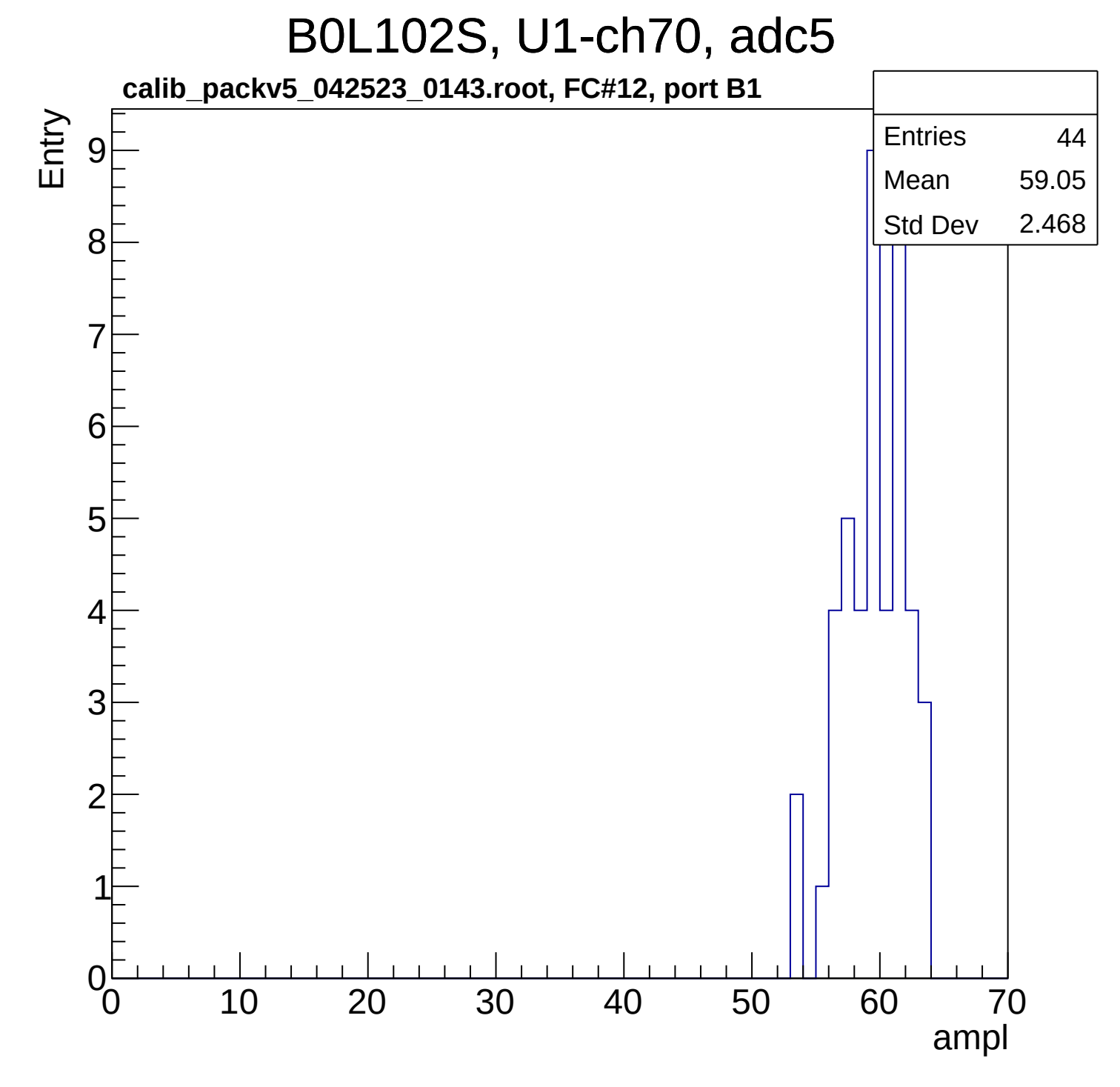
B0L102S, U1-ch70, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.05
Std Dev 2.468
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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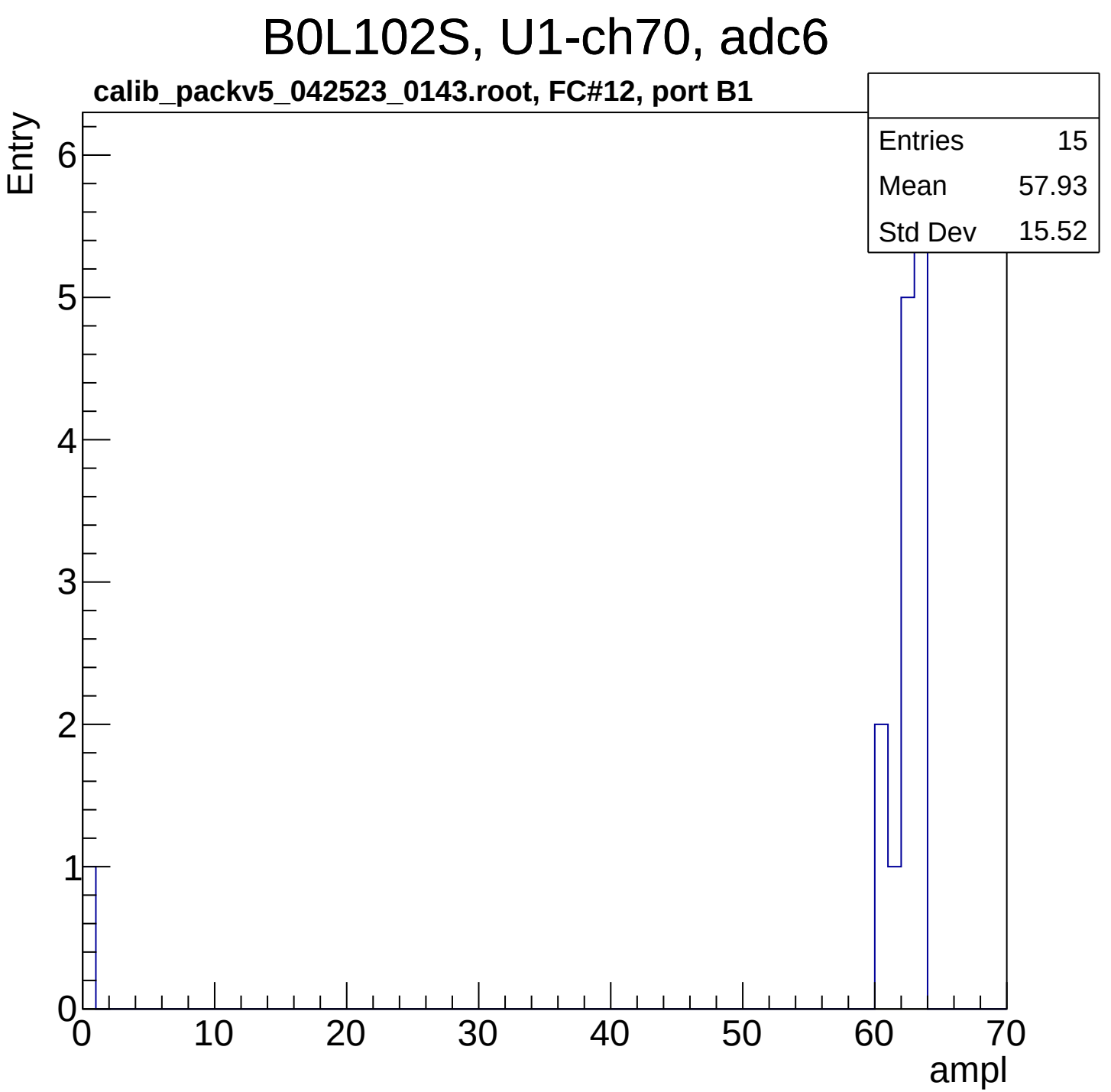
60
70


B0L102S, U1-ch70, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 57.93
Std Dev 15.52
Entry
0
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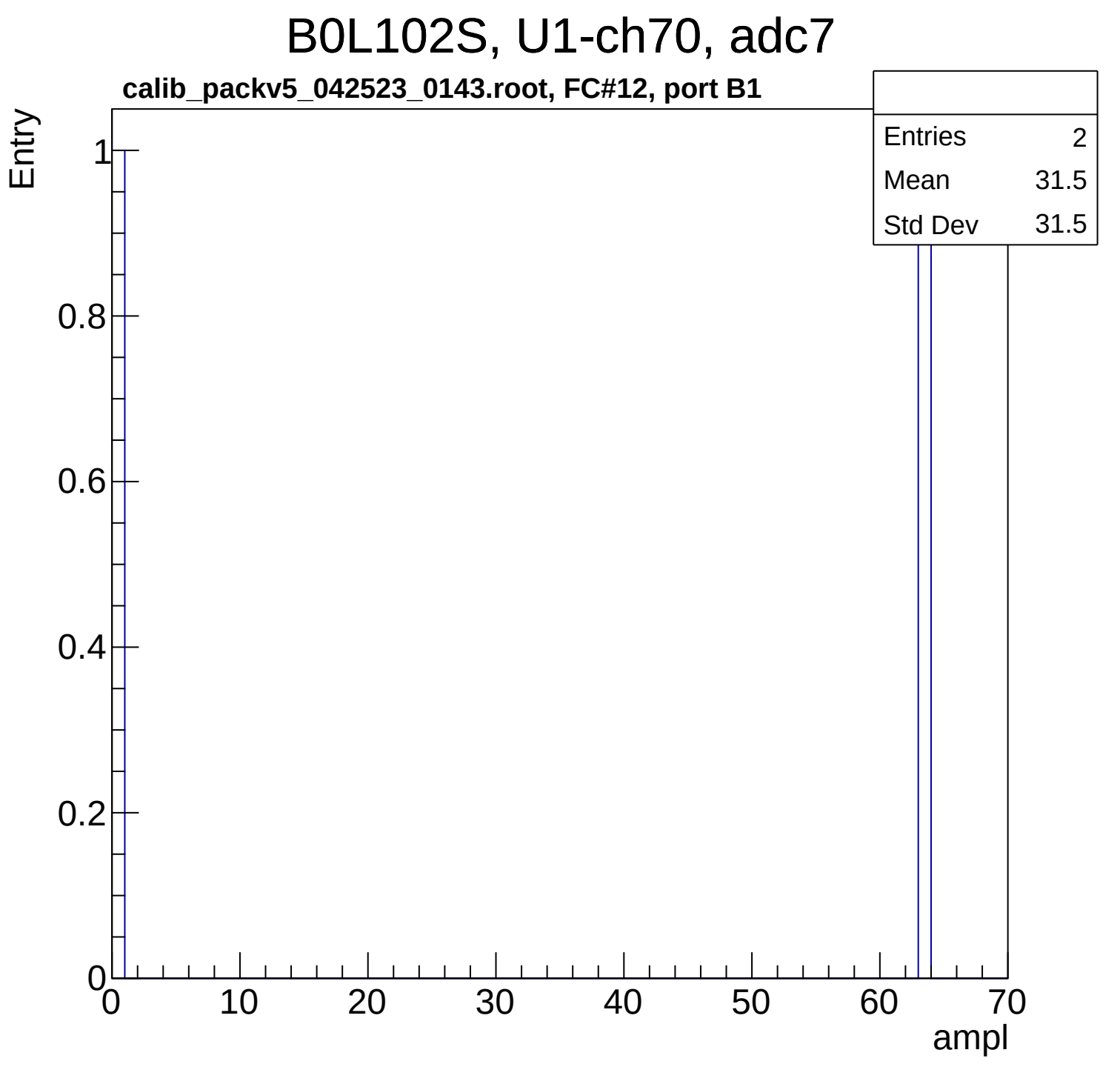
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch70, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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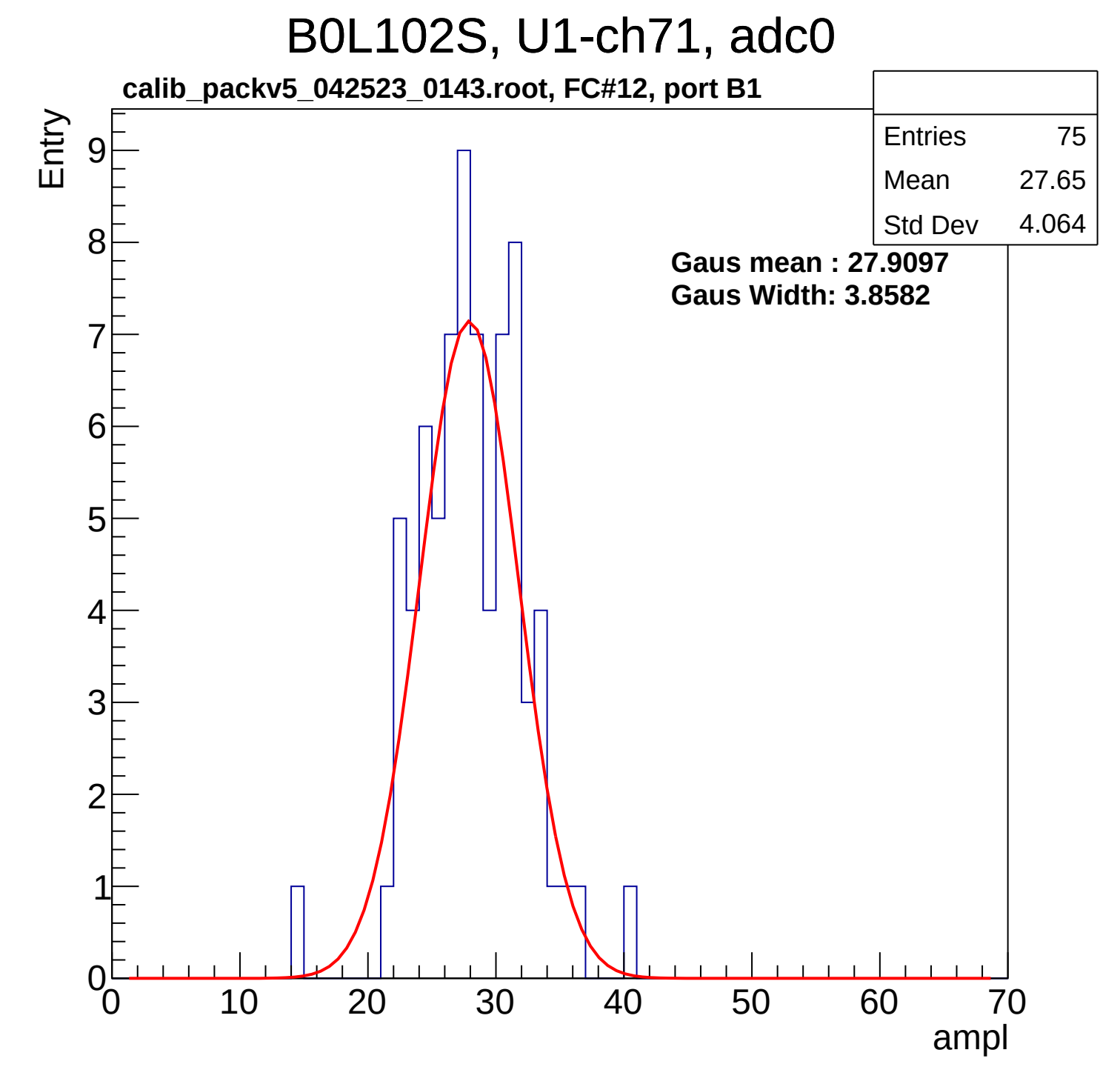
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch71, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 27.65
Std Dev 4.064
Gaus mean : 27.9097
Gaus Width: 3.8582
Entry
ampl
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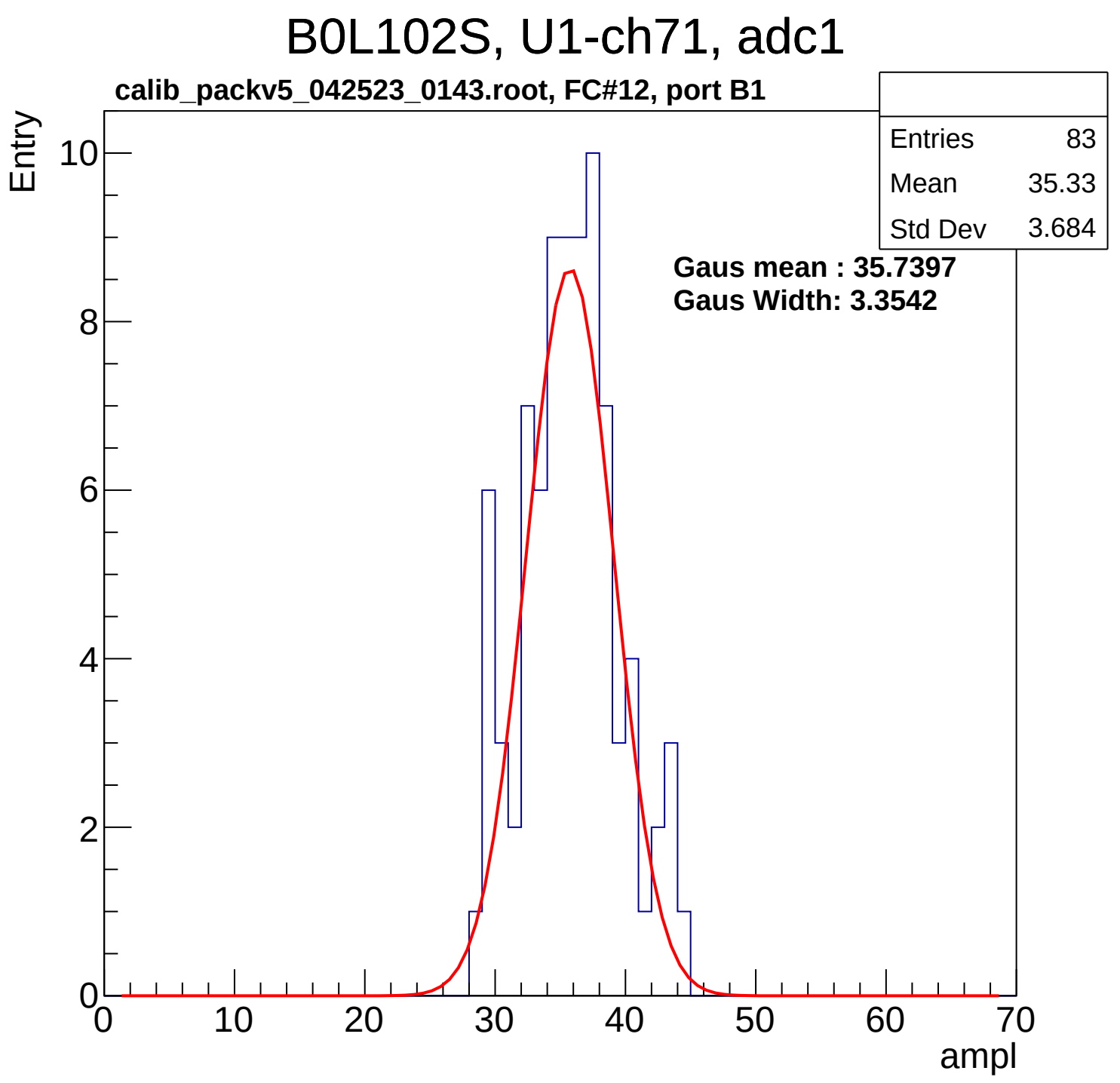
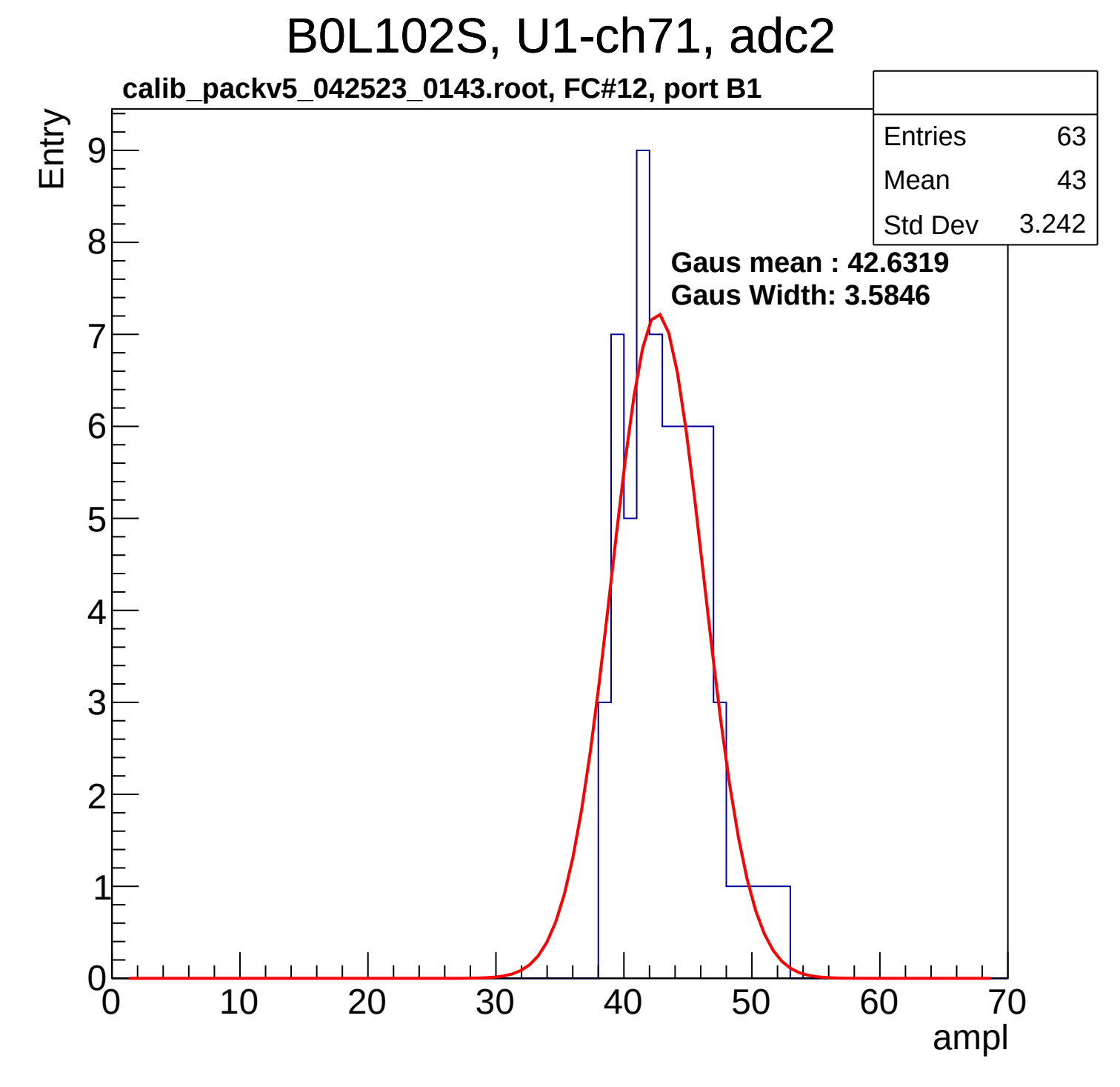
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch71, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 35.33
Std Dev 3.684
Gaus mean : 35.7397
Gaus Width: 3.3542
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch71, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 43
Std Dev 3.242
Gaus mean : 42.6319
Gaus Width: 3.5846
Entry
ampl
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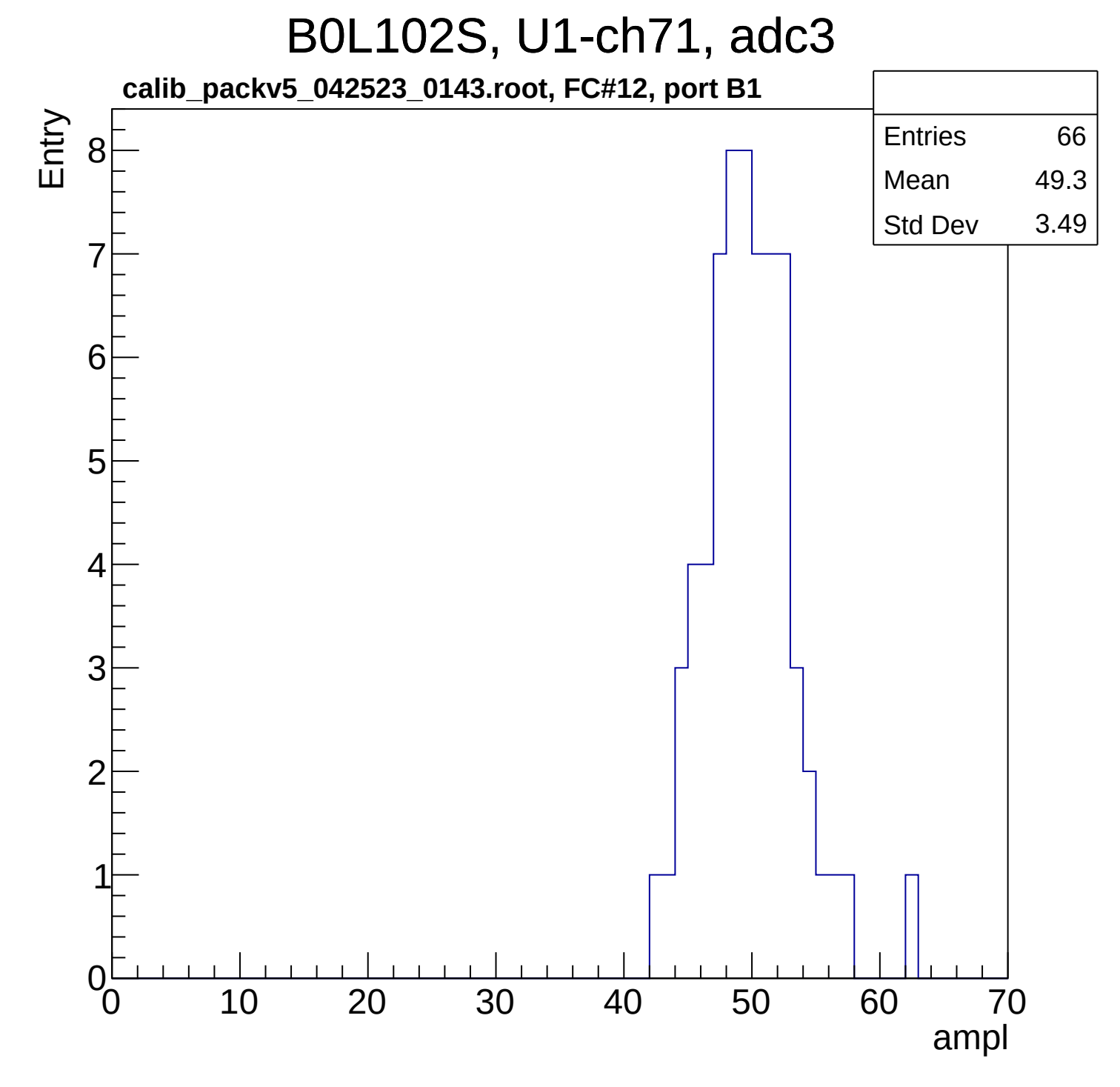
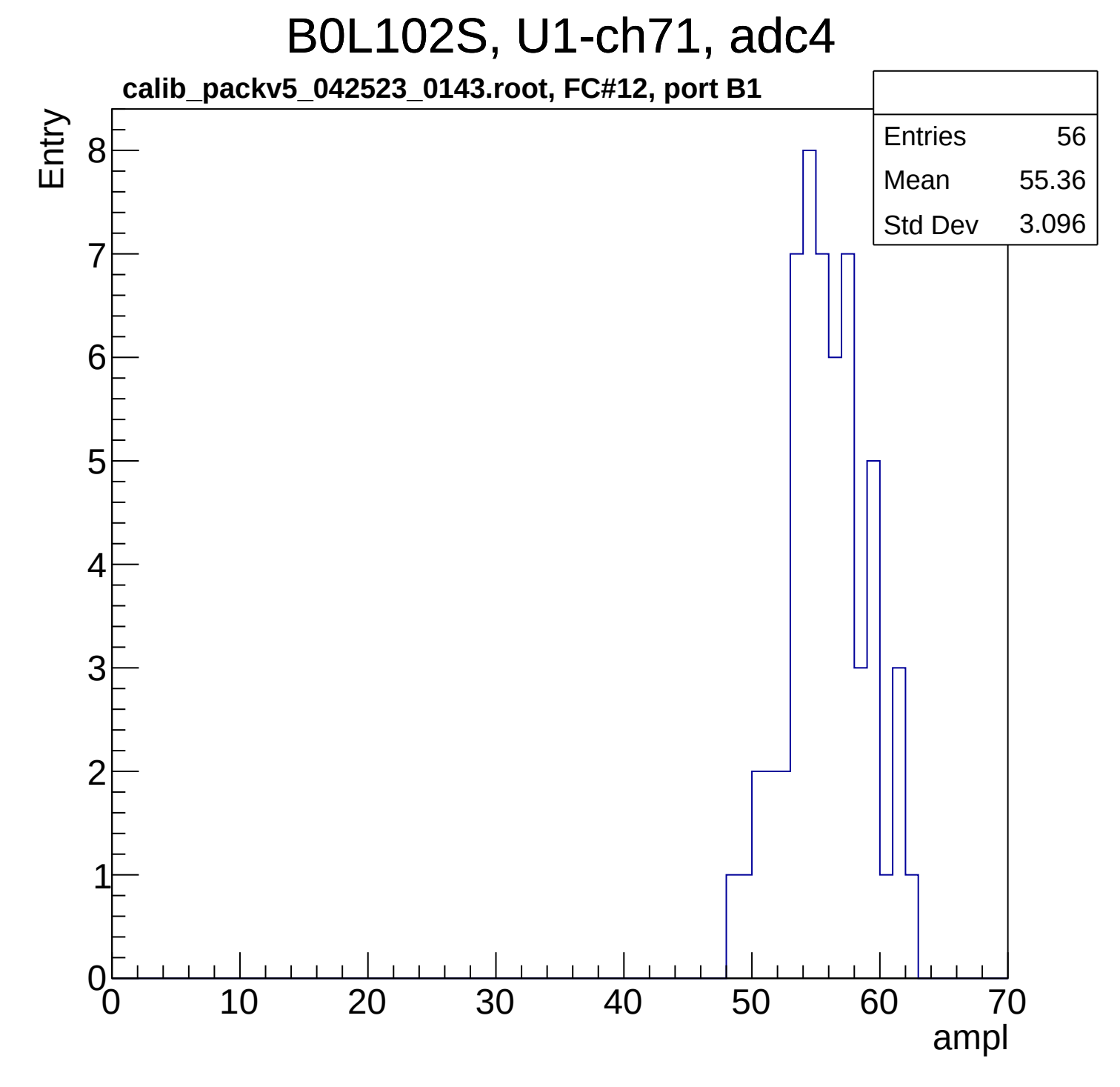
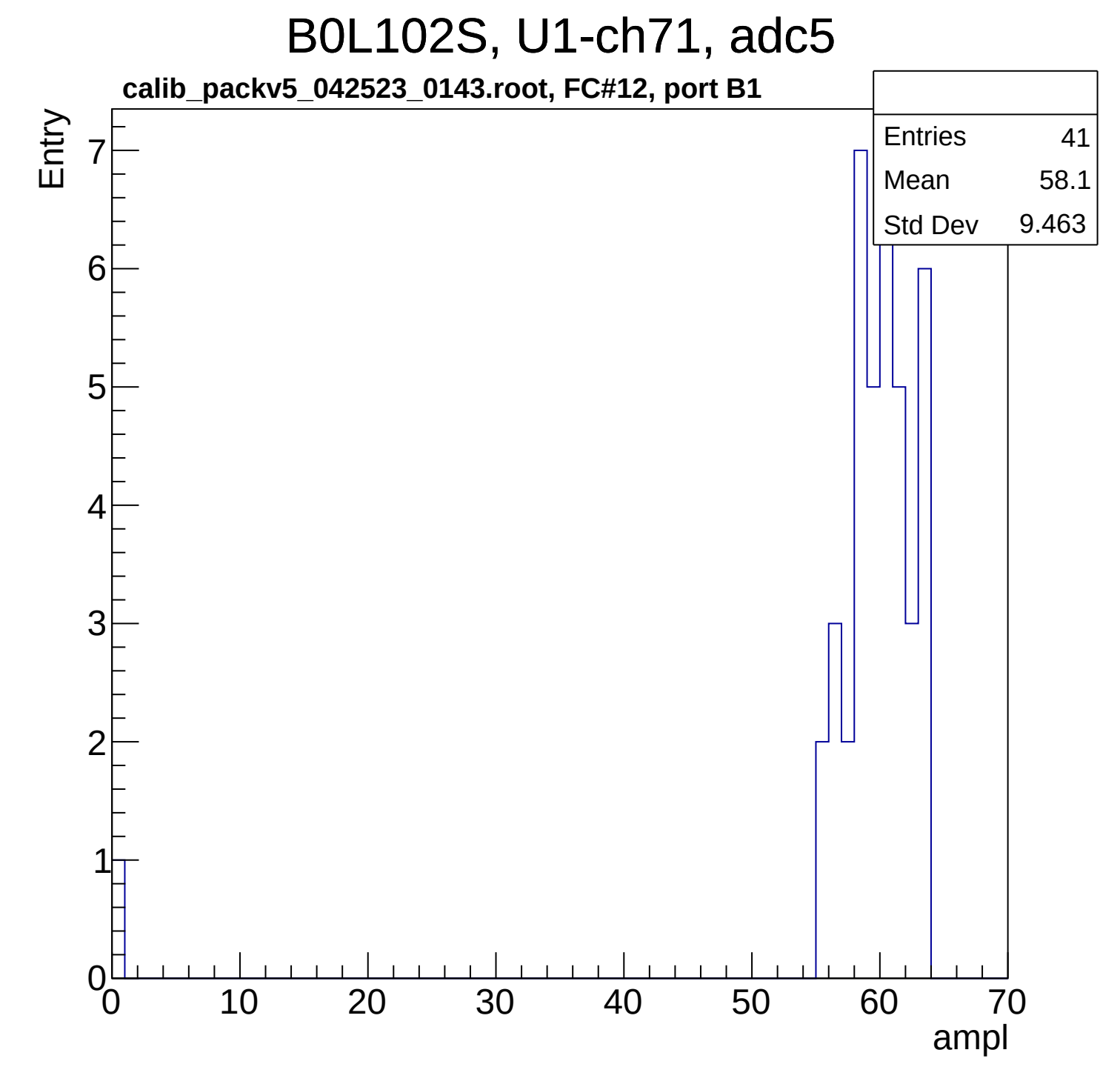
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch71, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.3
Std Dev 3.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch71, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 55.36
Std Dev 3.096
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch71, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.1
Std Dev 9.463
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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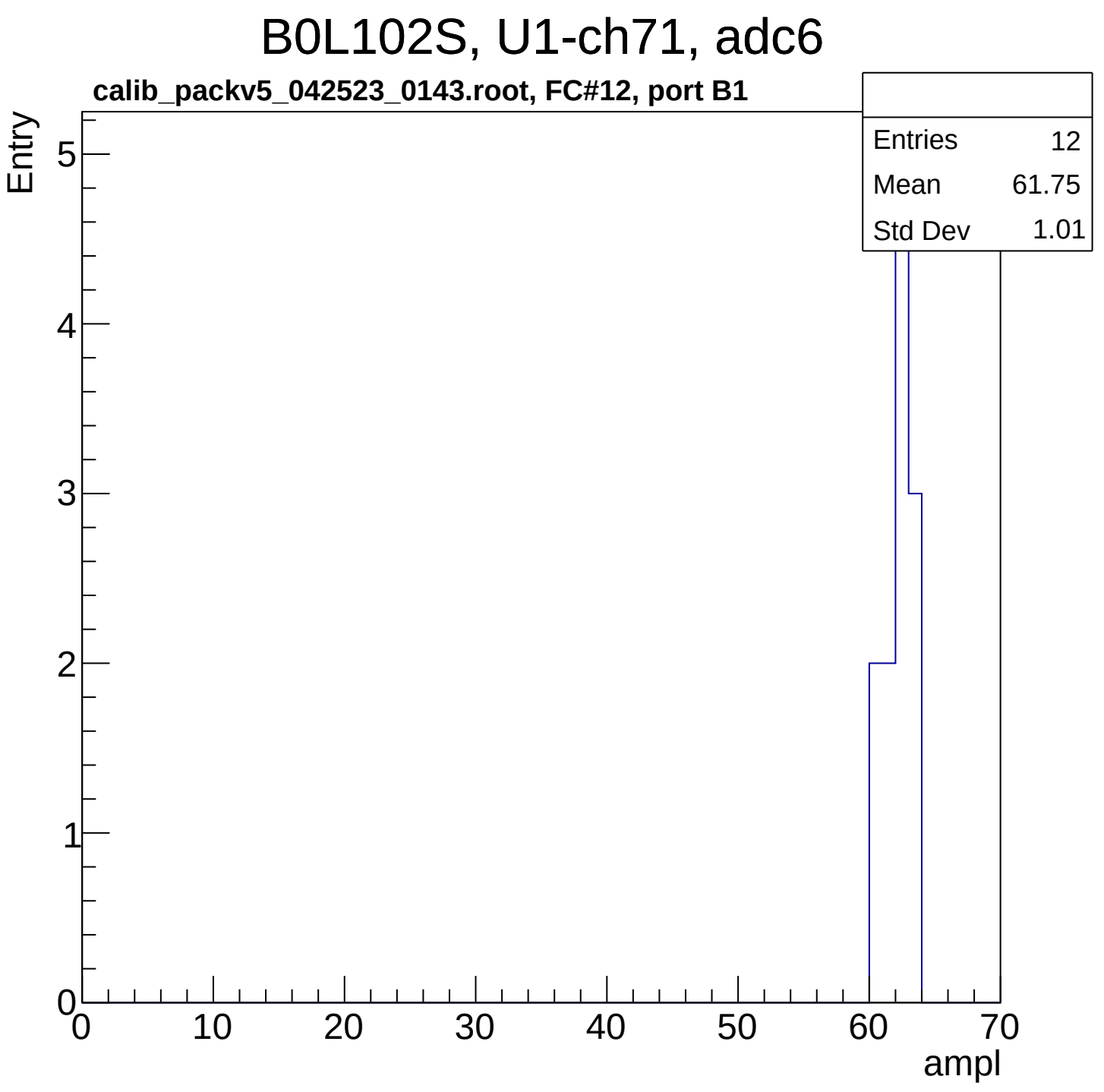
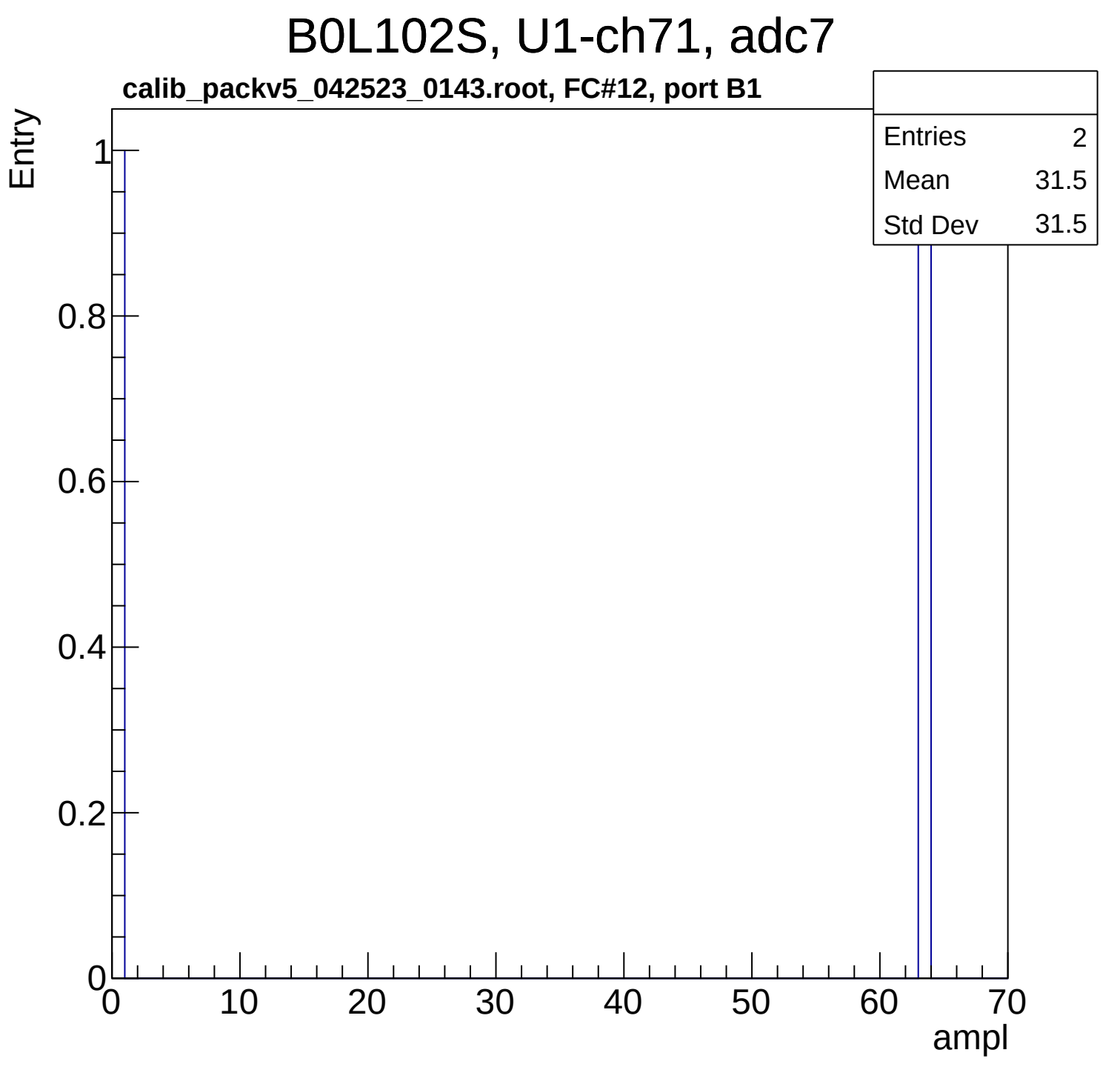
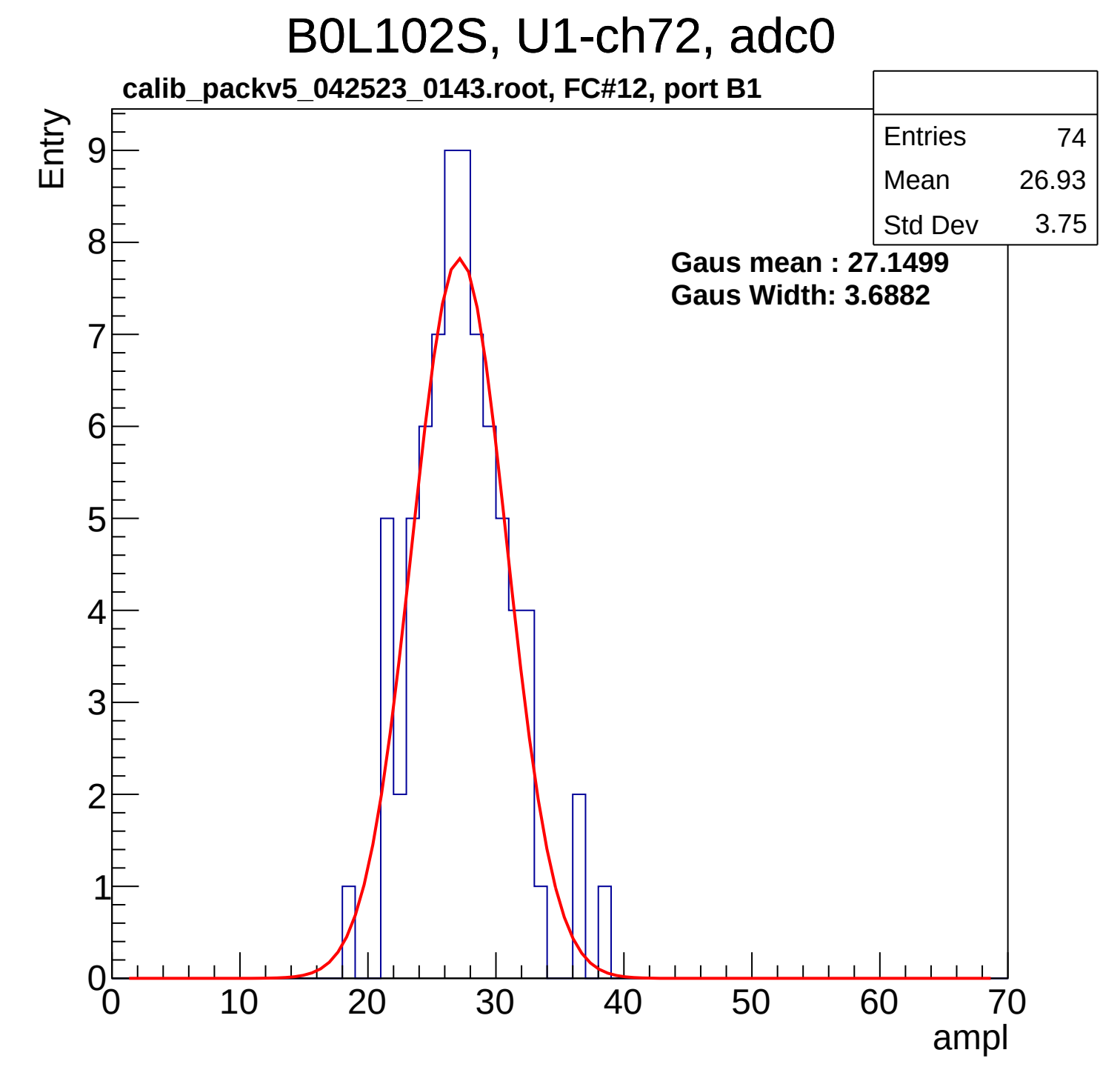
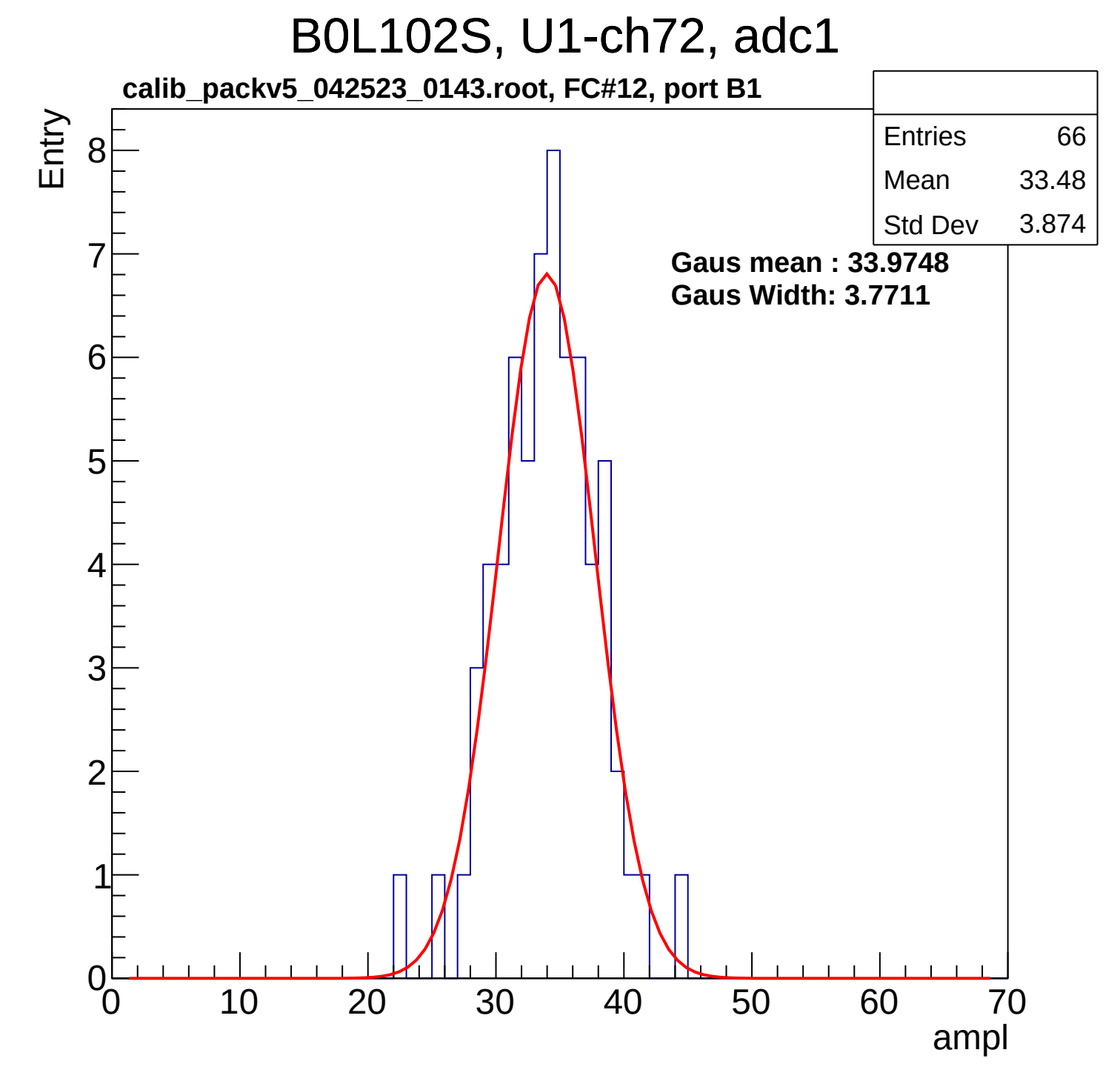
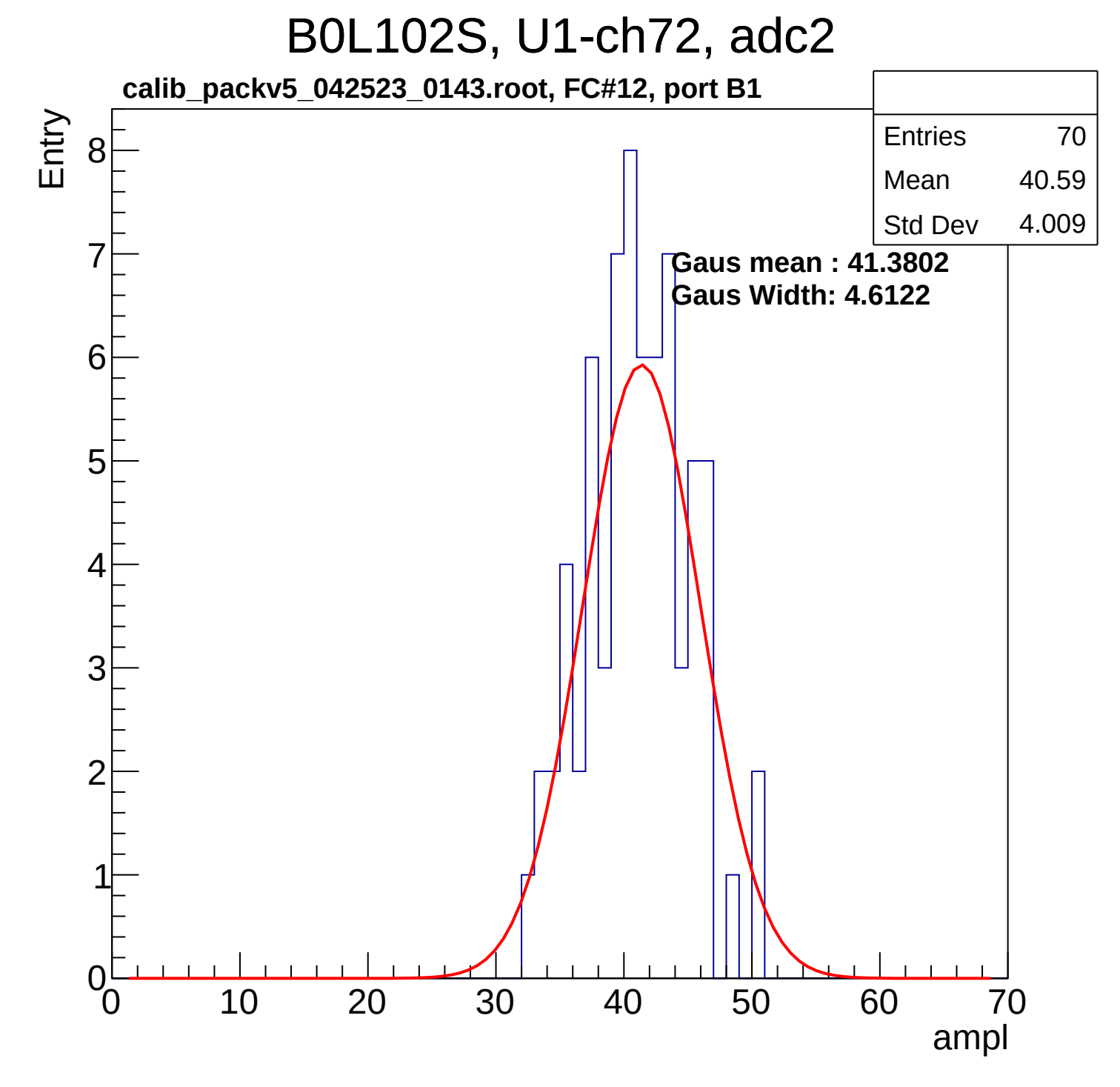
60
70

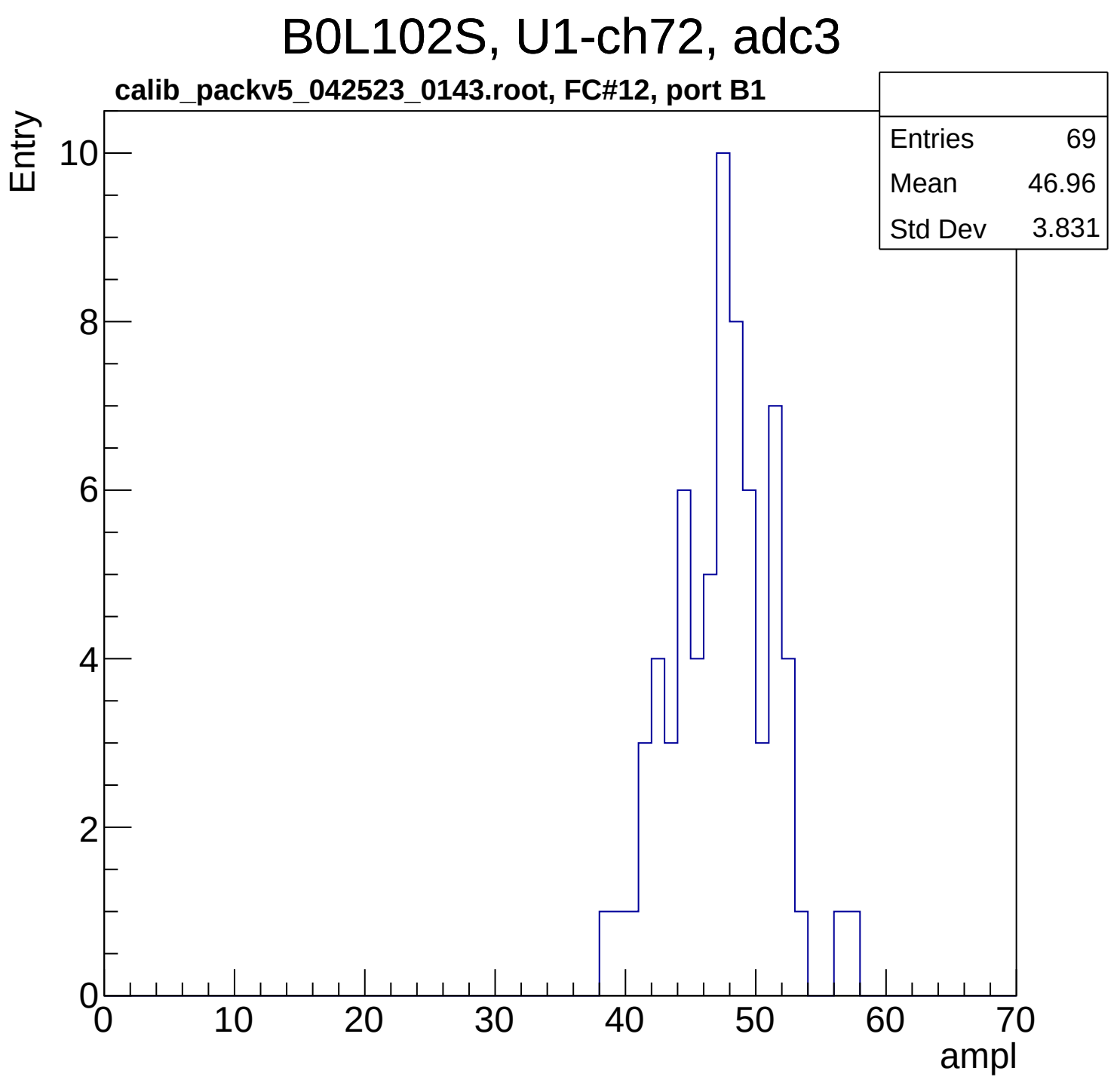

B0L102S, U1-ch71, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 61.75
Std Dev 1.01
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

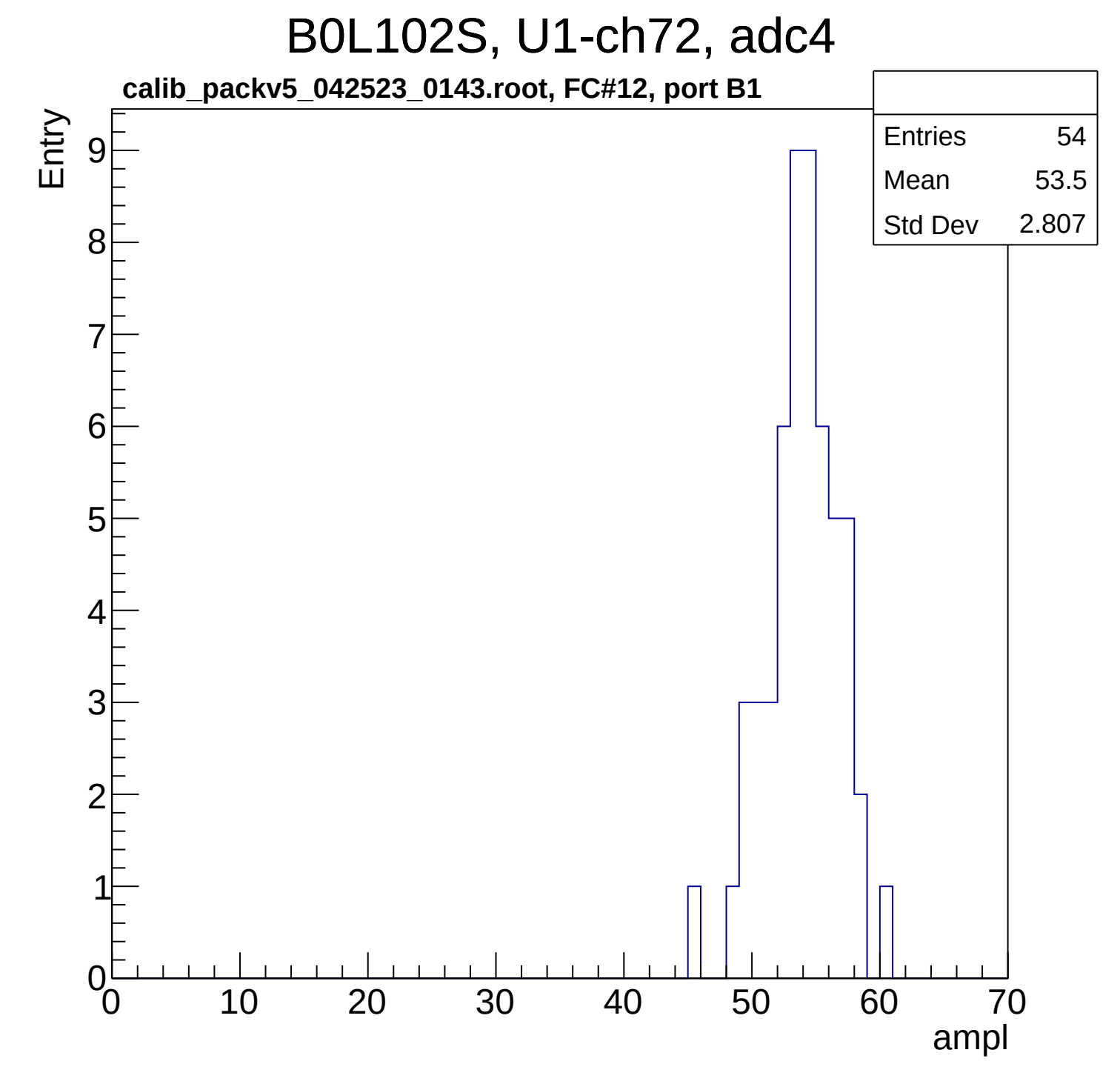

B0L102S, U1-ch71, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

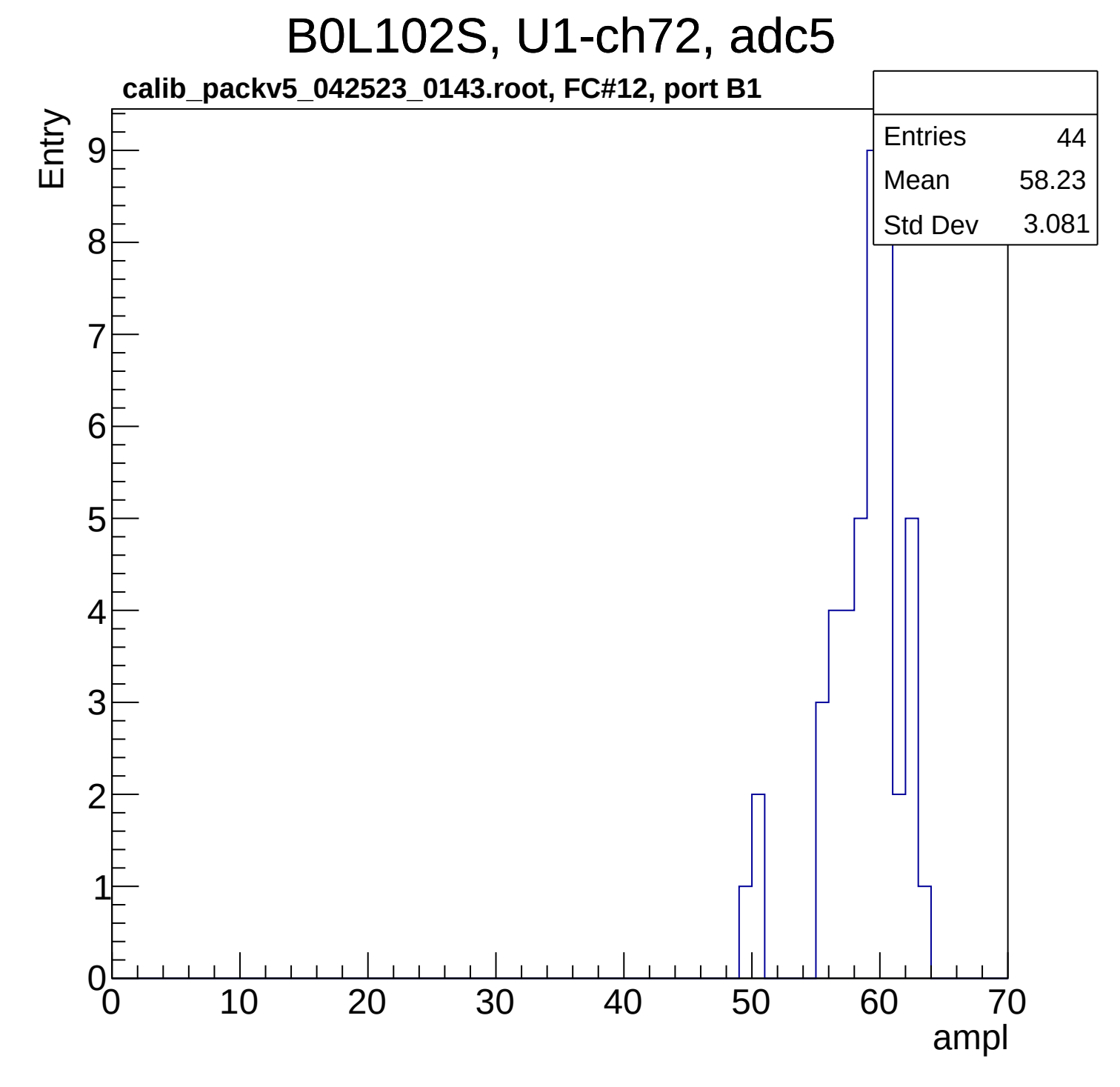

B0L102S, U1-ch72, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 26.93
Std Dev 3.75
Gaus mean : 27.1499
Gaus Width: 3.6882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

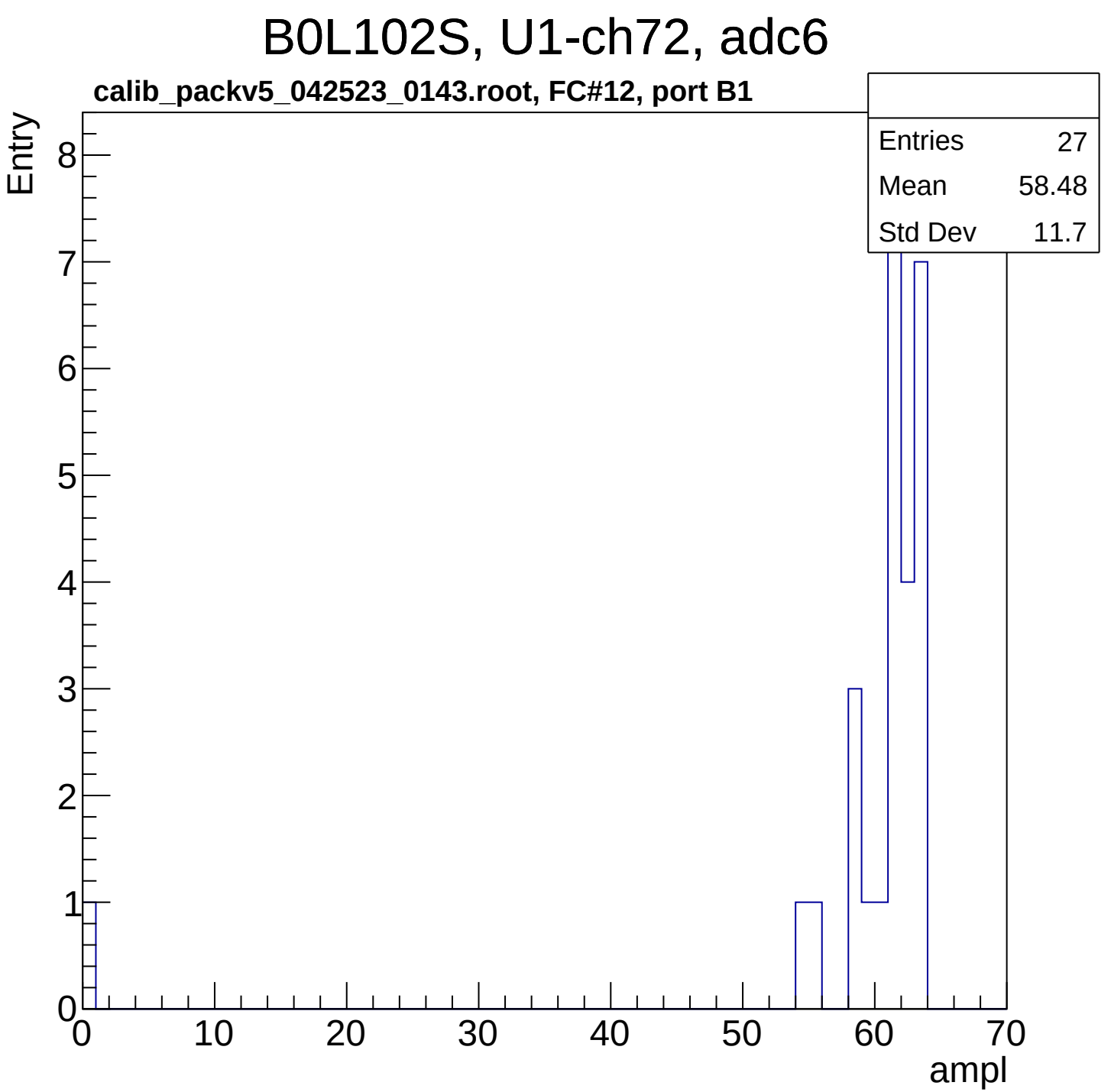

B0L102S, U1-ch72, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 33.48
Std Dev 3.874
Gaus mean : 33.9748
Gaus Width: 3.7711
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

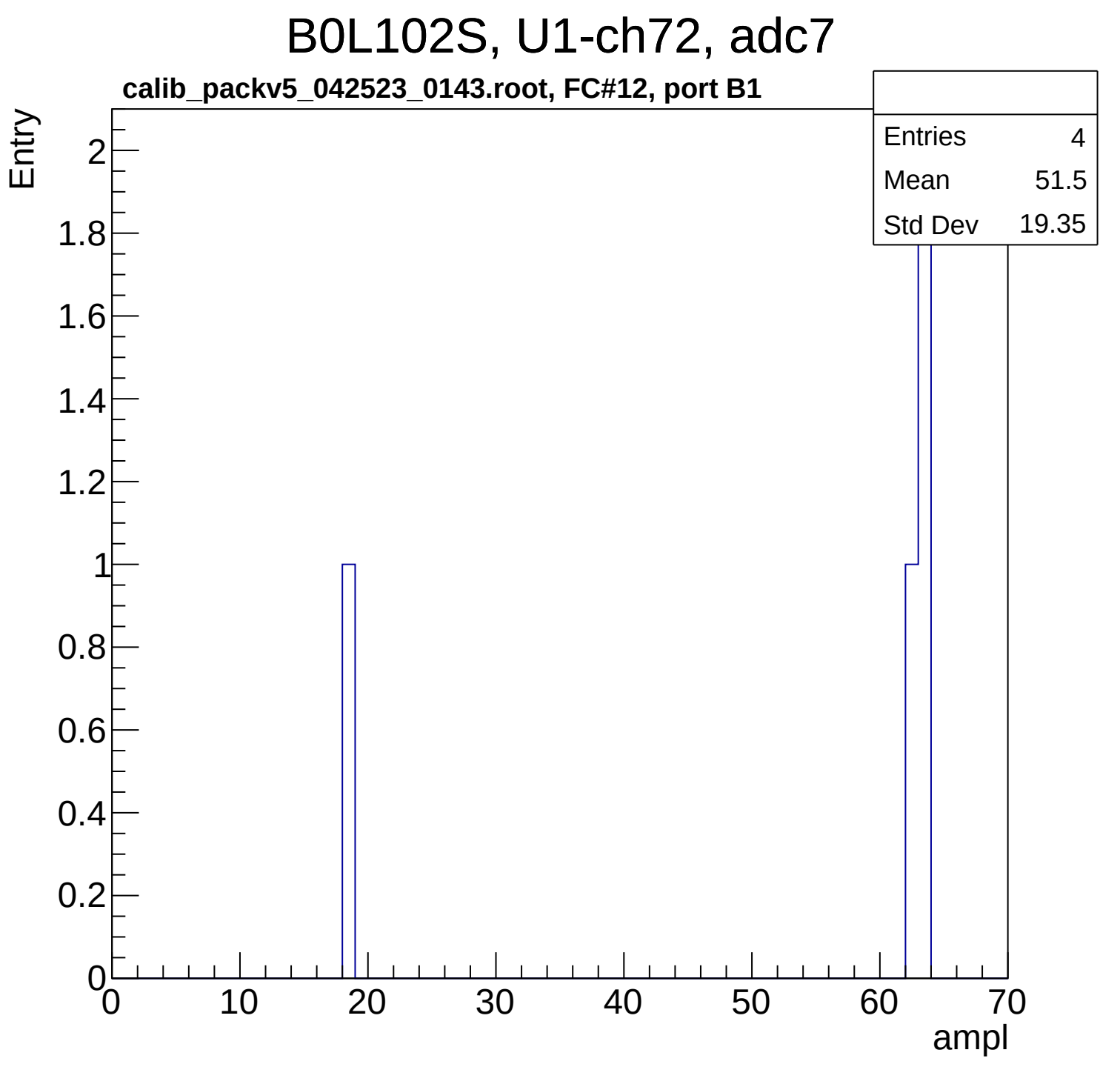

B0L102S, U1-ch72, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 40.59
Std Dev 4.009
Gaus mean : 41.3802
Gaus Width: 4.6122
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

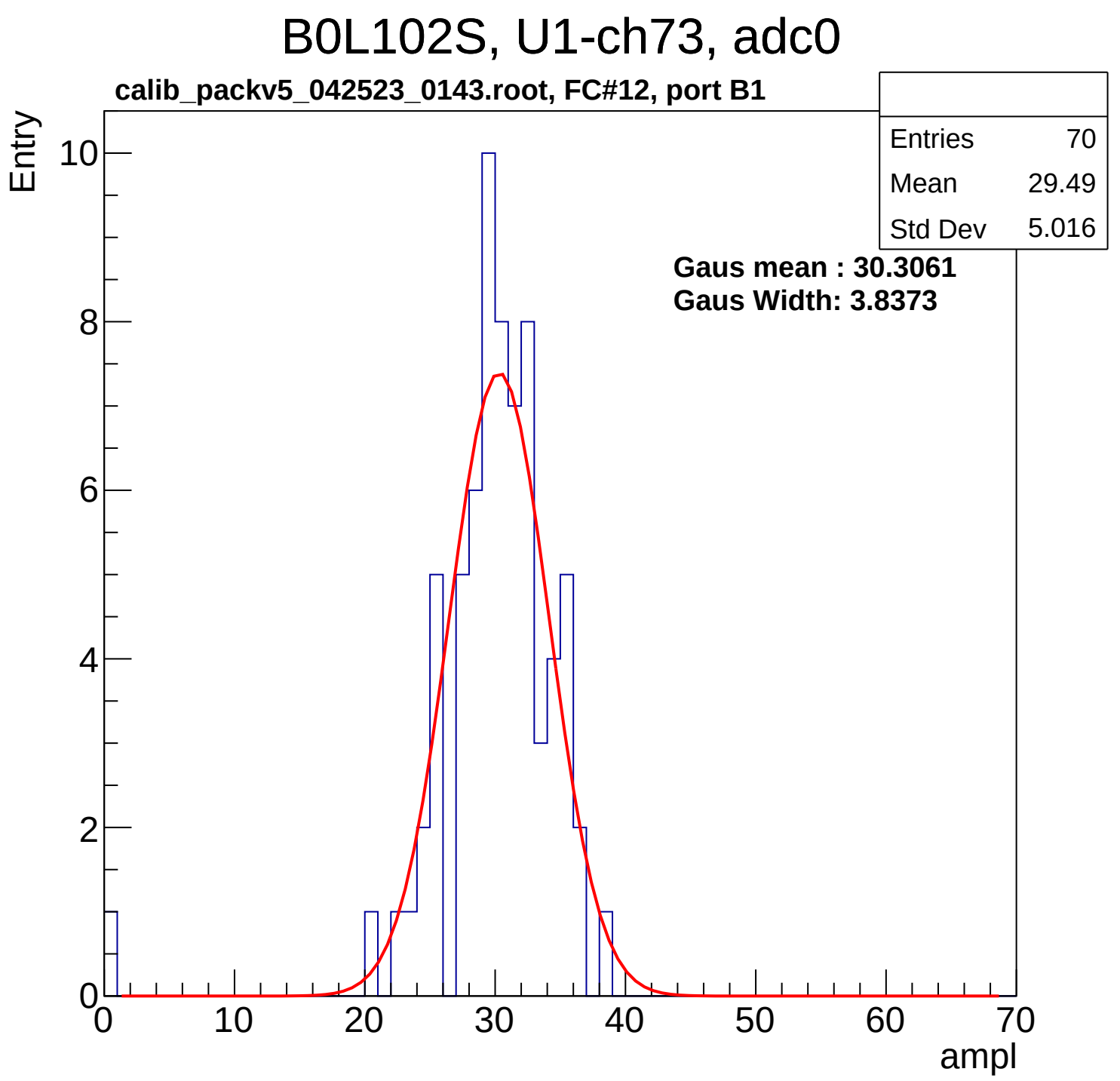

B0L102S, U1-ch72, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 46.96
Std Dev 3.831
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

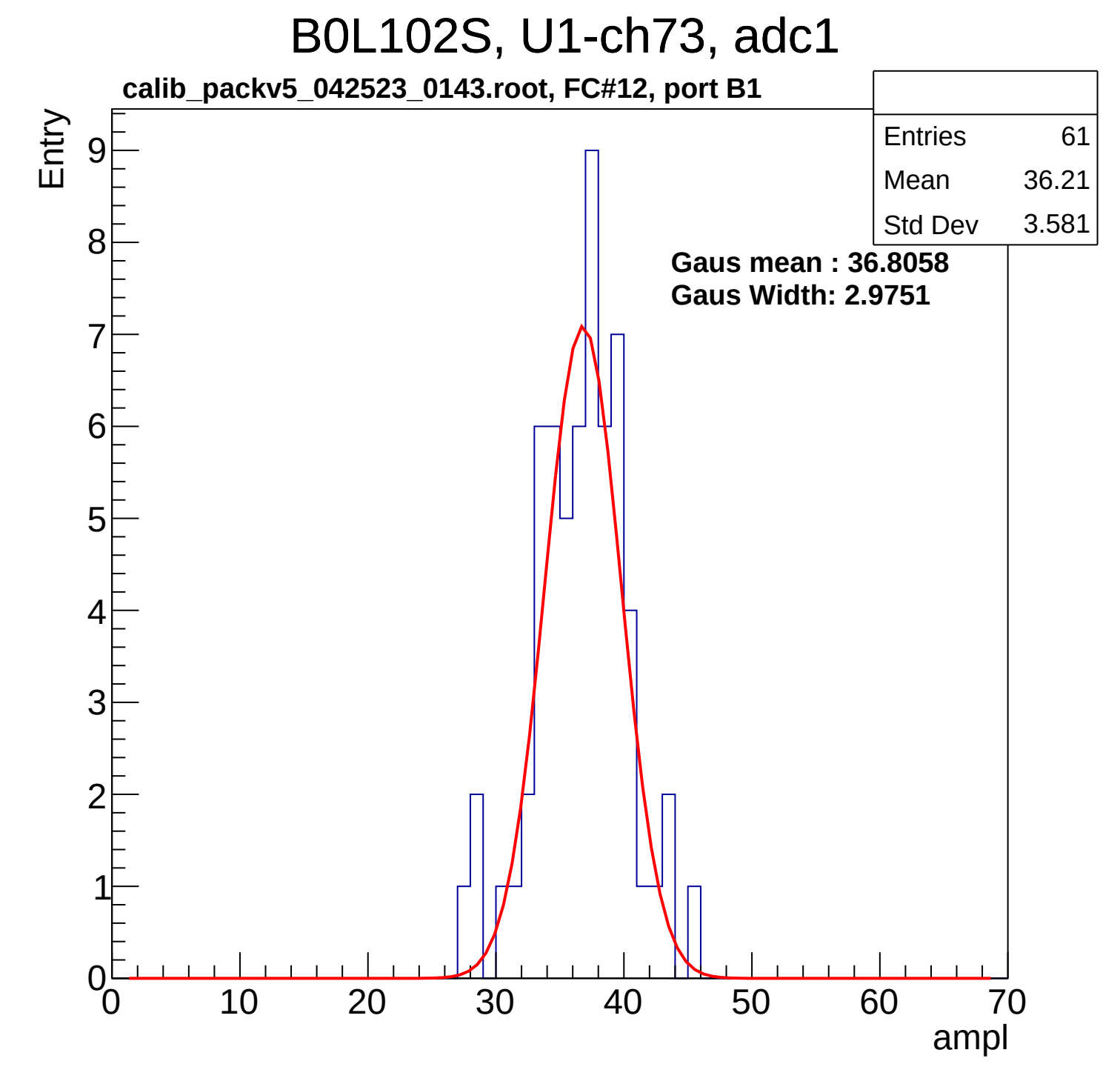

B0L102S, U1-ch72, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 53.5
Std Dev 2.807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

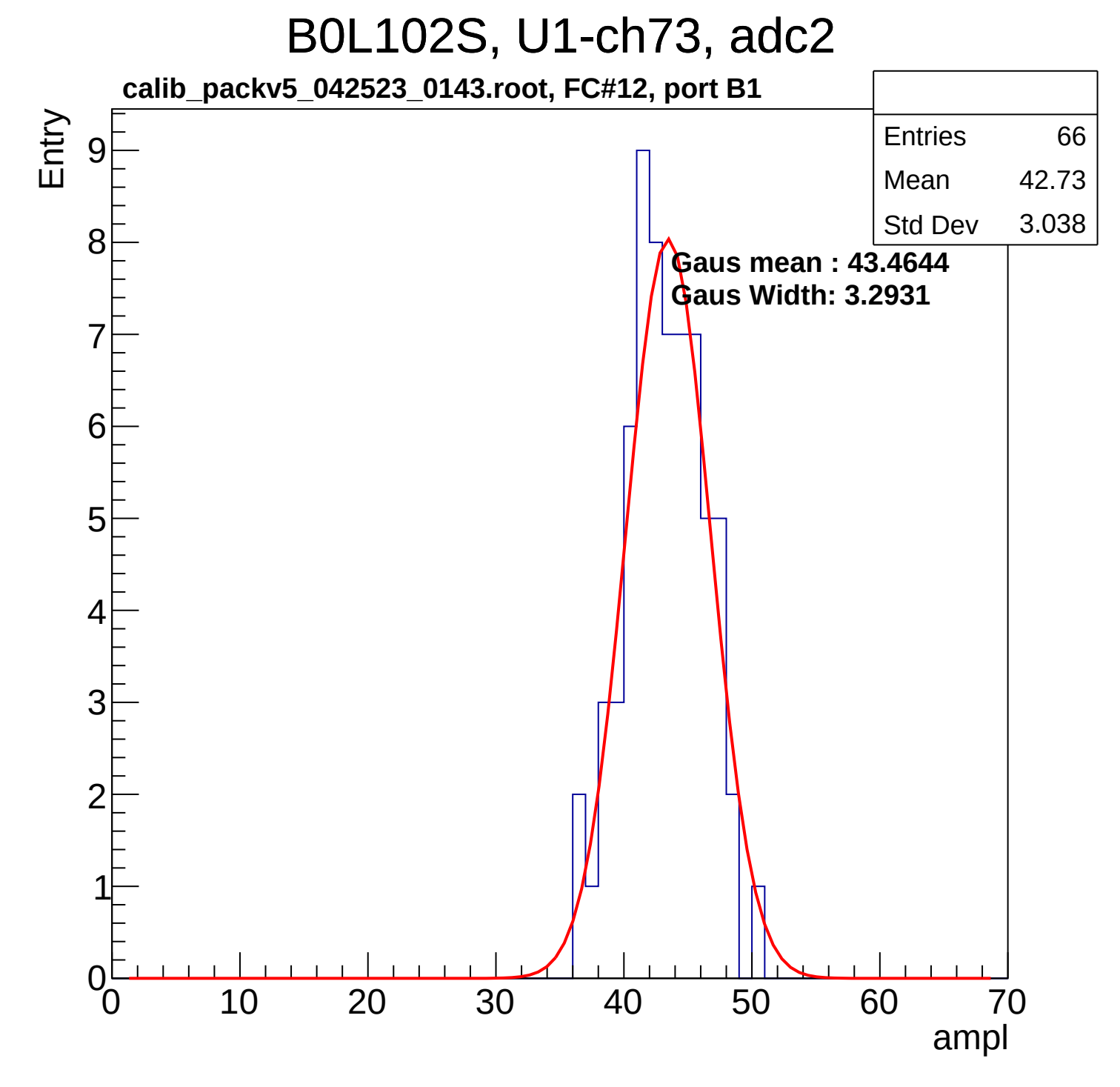

B0L102S, U1-ch72, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.23
Std Dev 3.081
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

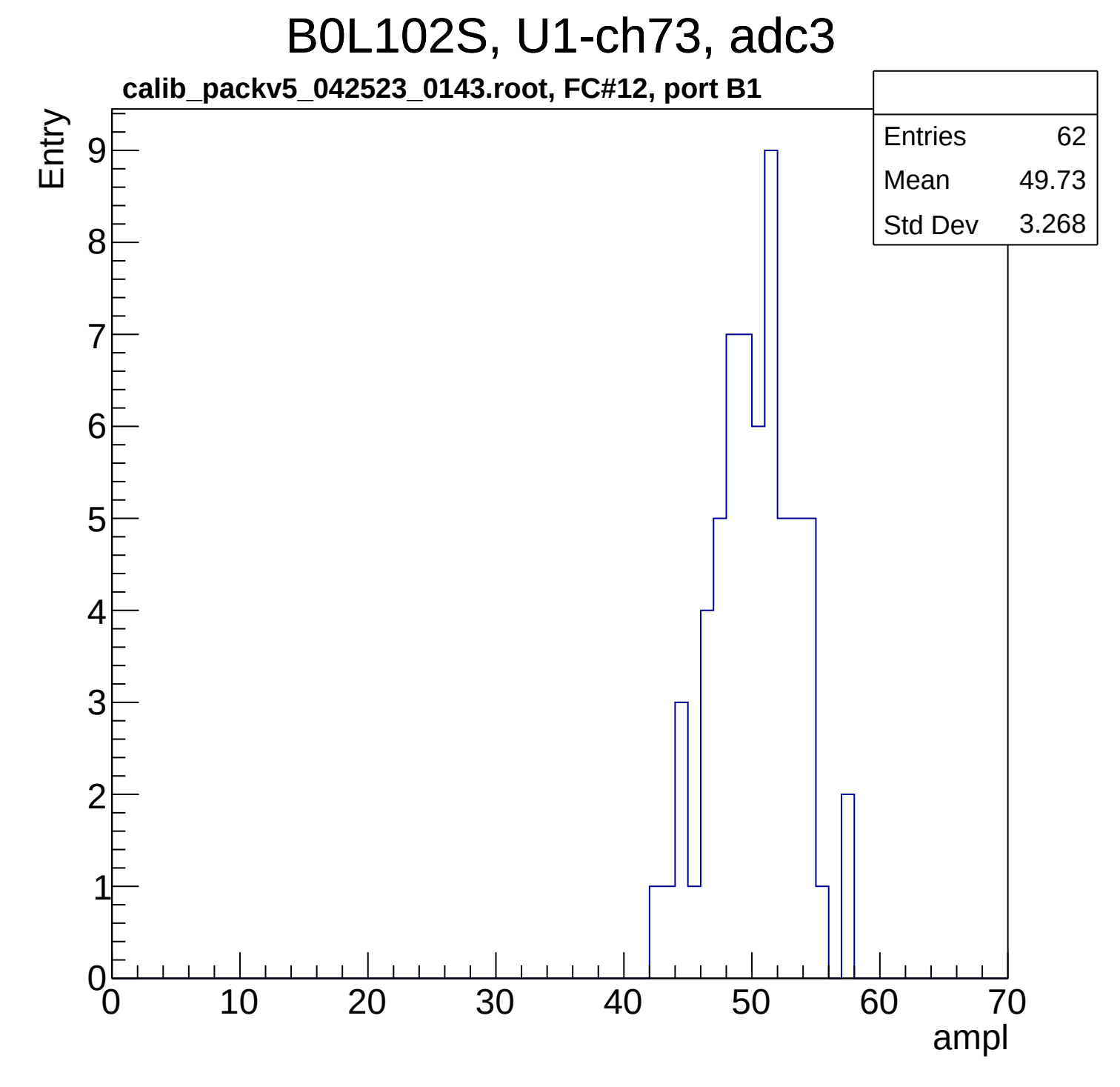

B0L102S, U1-ch72, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 27
Mean 58.48
Std Dev 11.7
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

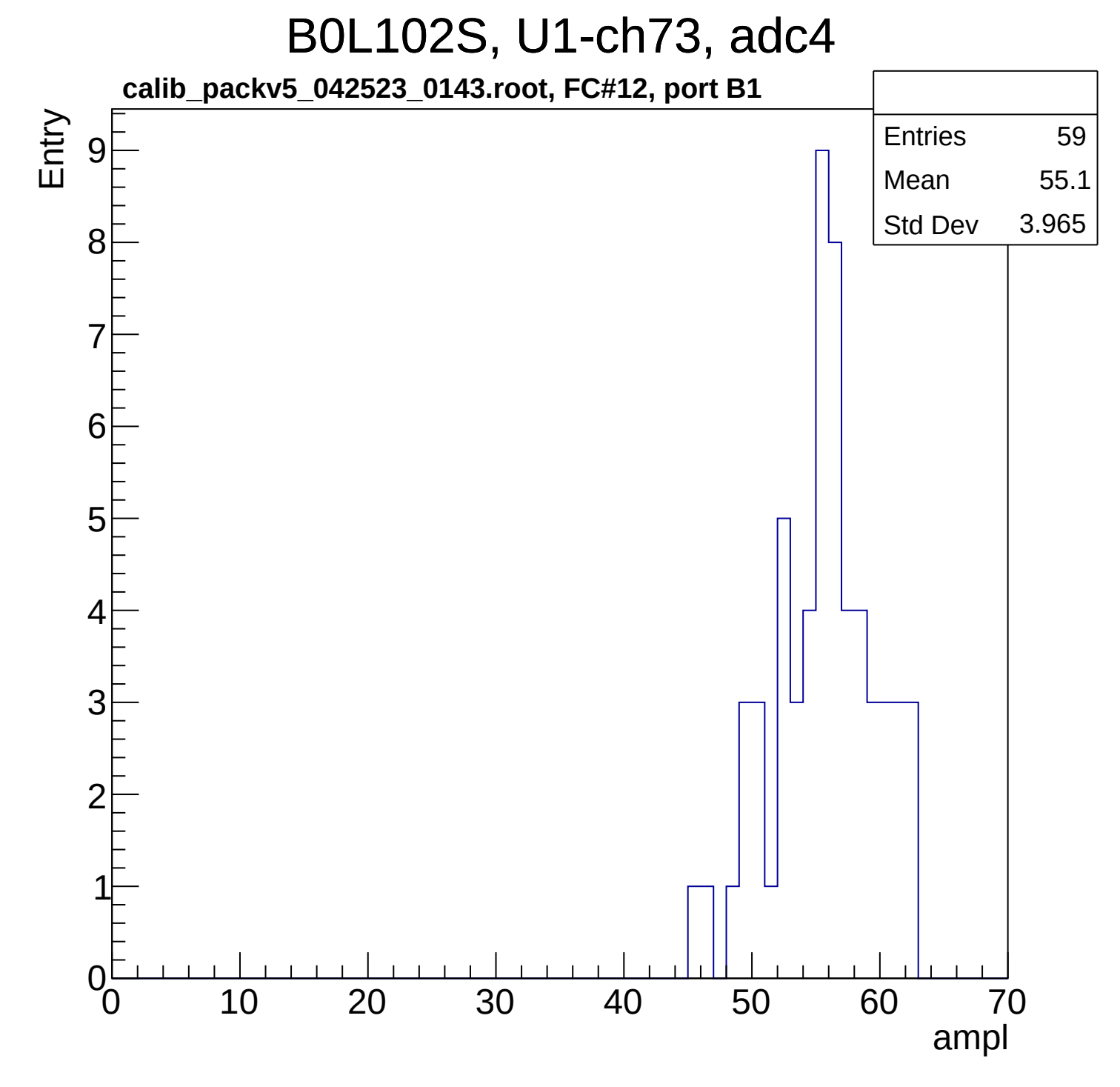

B0L102S, U1-ch72, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 51.5
Std Dev 19.35
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

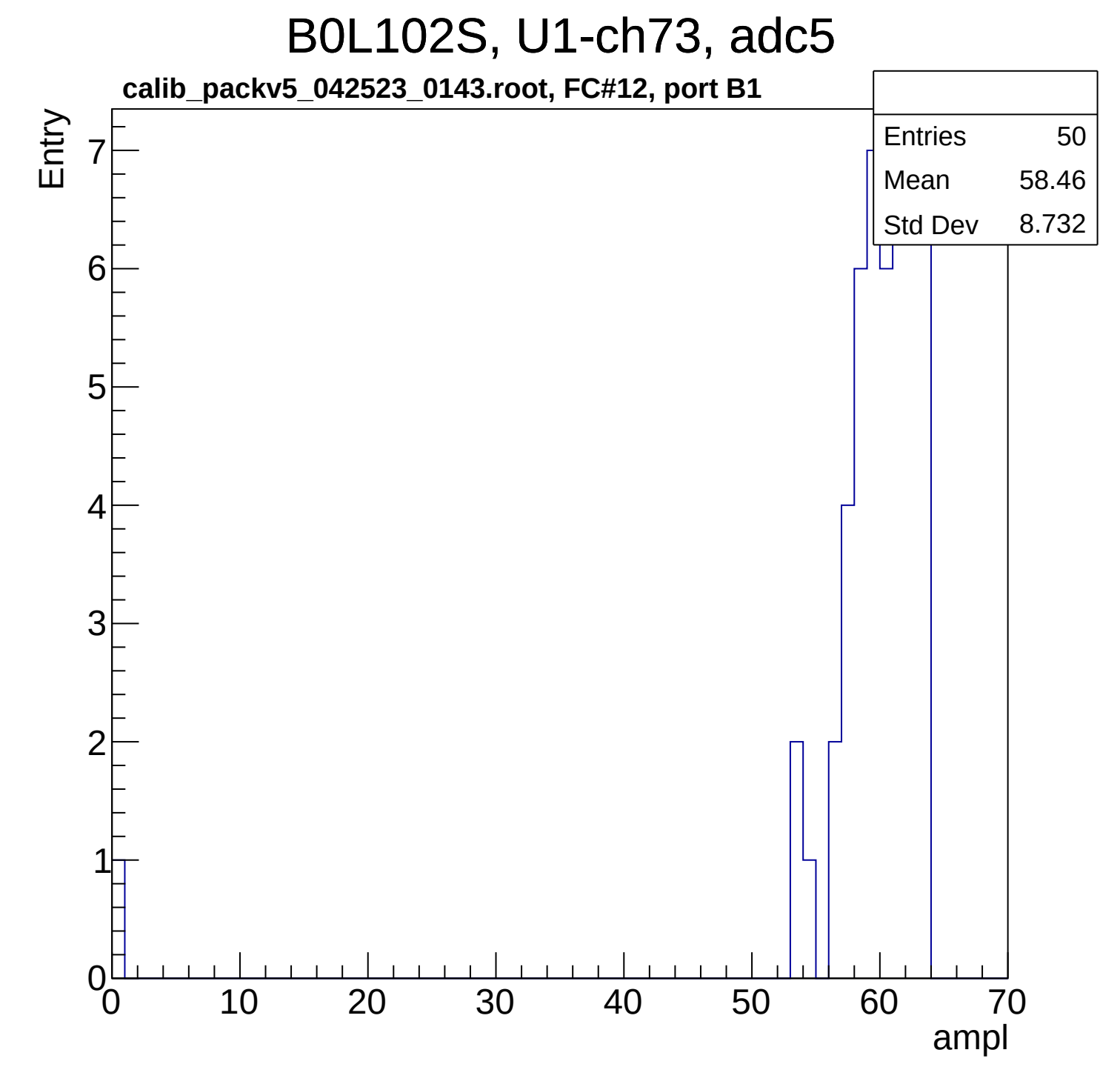

B0L102S, U1-ch73, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 29.49
Std Dev 5.016
Gaus mean : 30.3061
Gaus Width: 3.8373
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

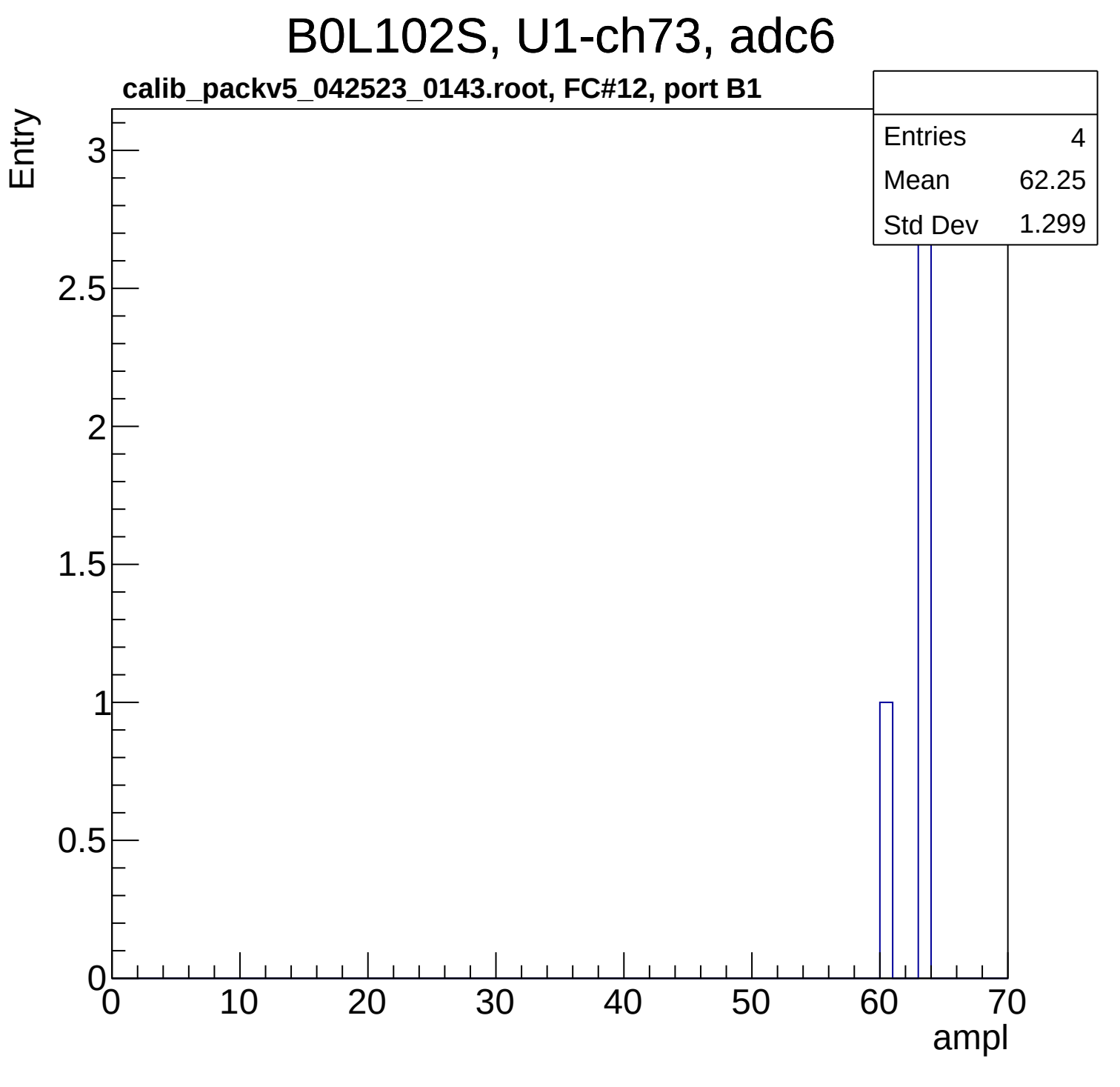

B0L102S, U1-ch73, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 36.21
Std Dev 3.581
Gaus mean : 36.8058
Gaus Width: 2.9751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch73, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 42.73
Std Dev 3.038
Gaus mean : 43.4644
Gaus Width: 3.2931
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch73, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 49.73
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch73, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.1
Std Dev 3.965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch73, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.46
Std Dev 8.732
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch73, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
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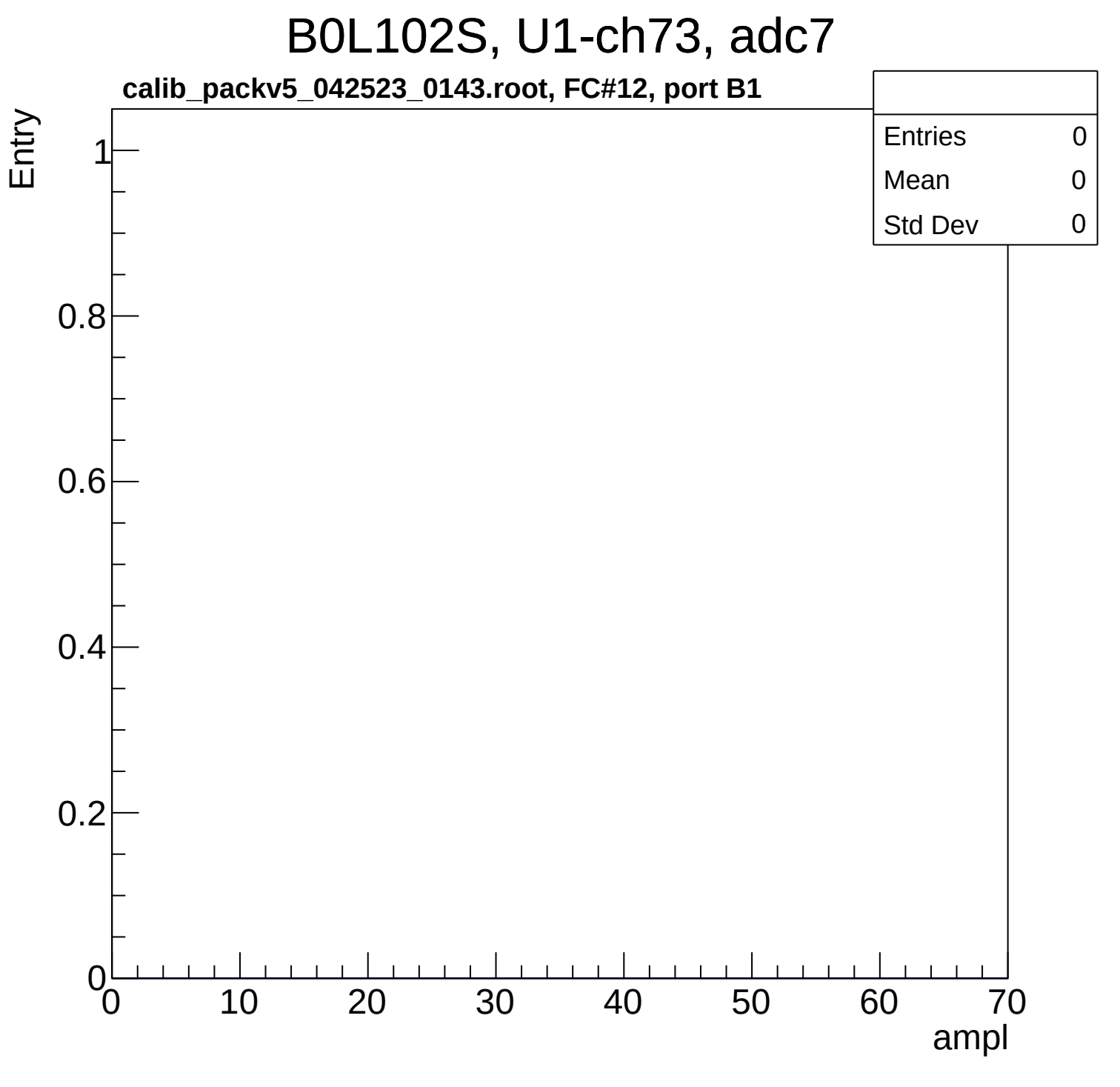
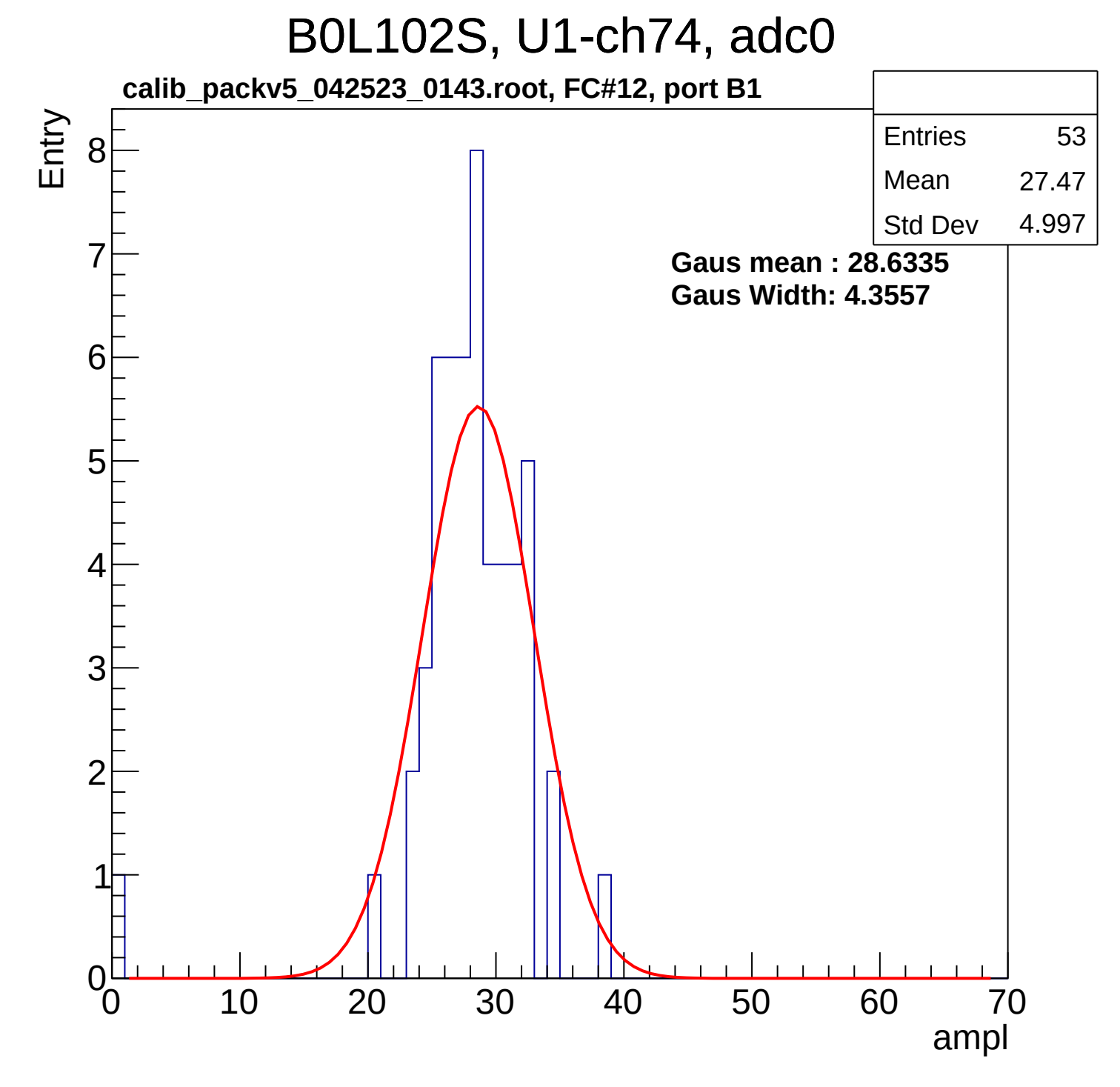
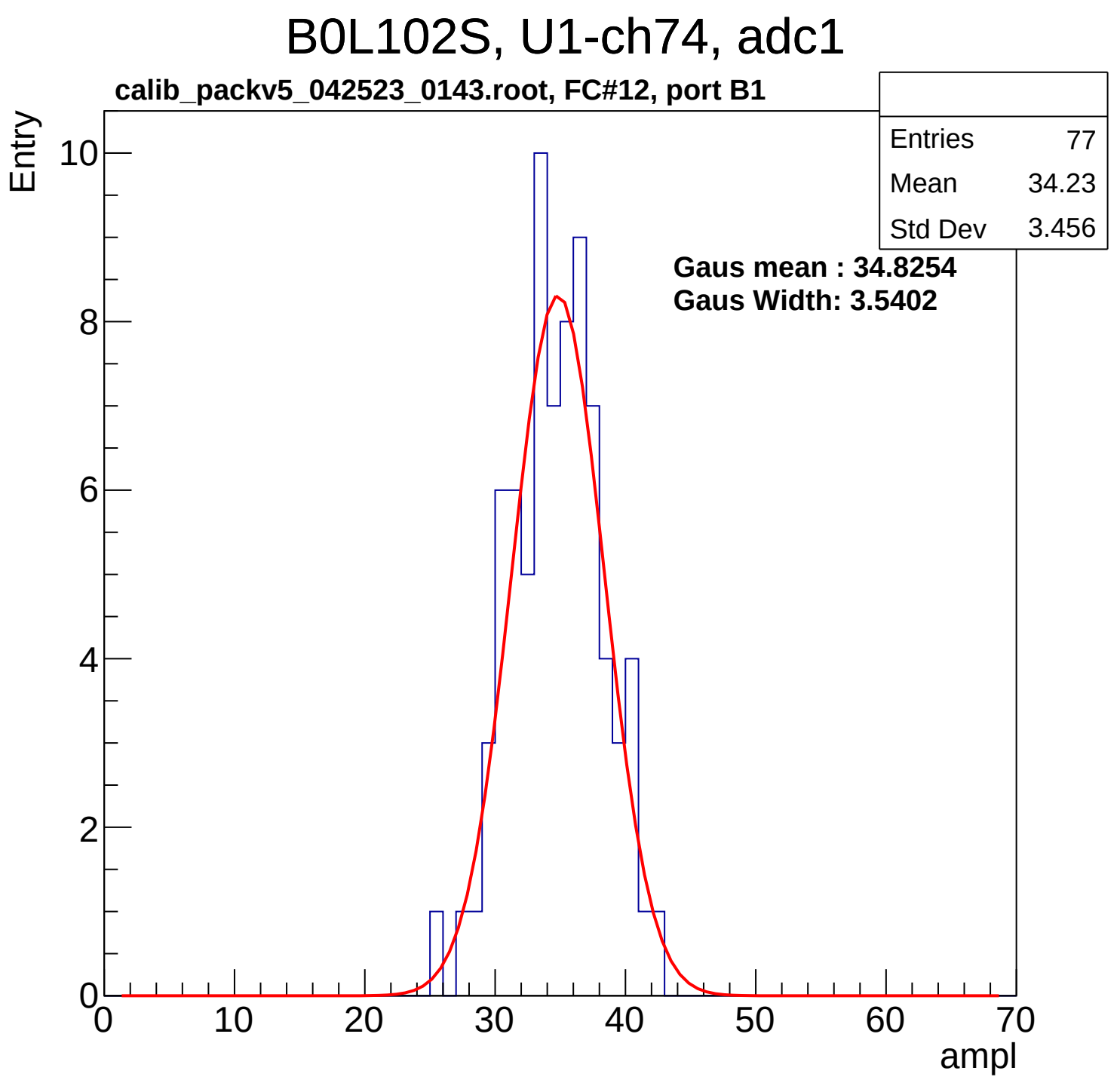
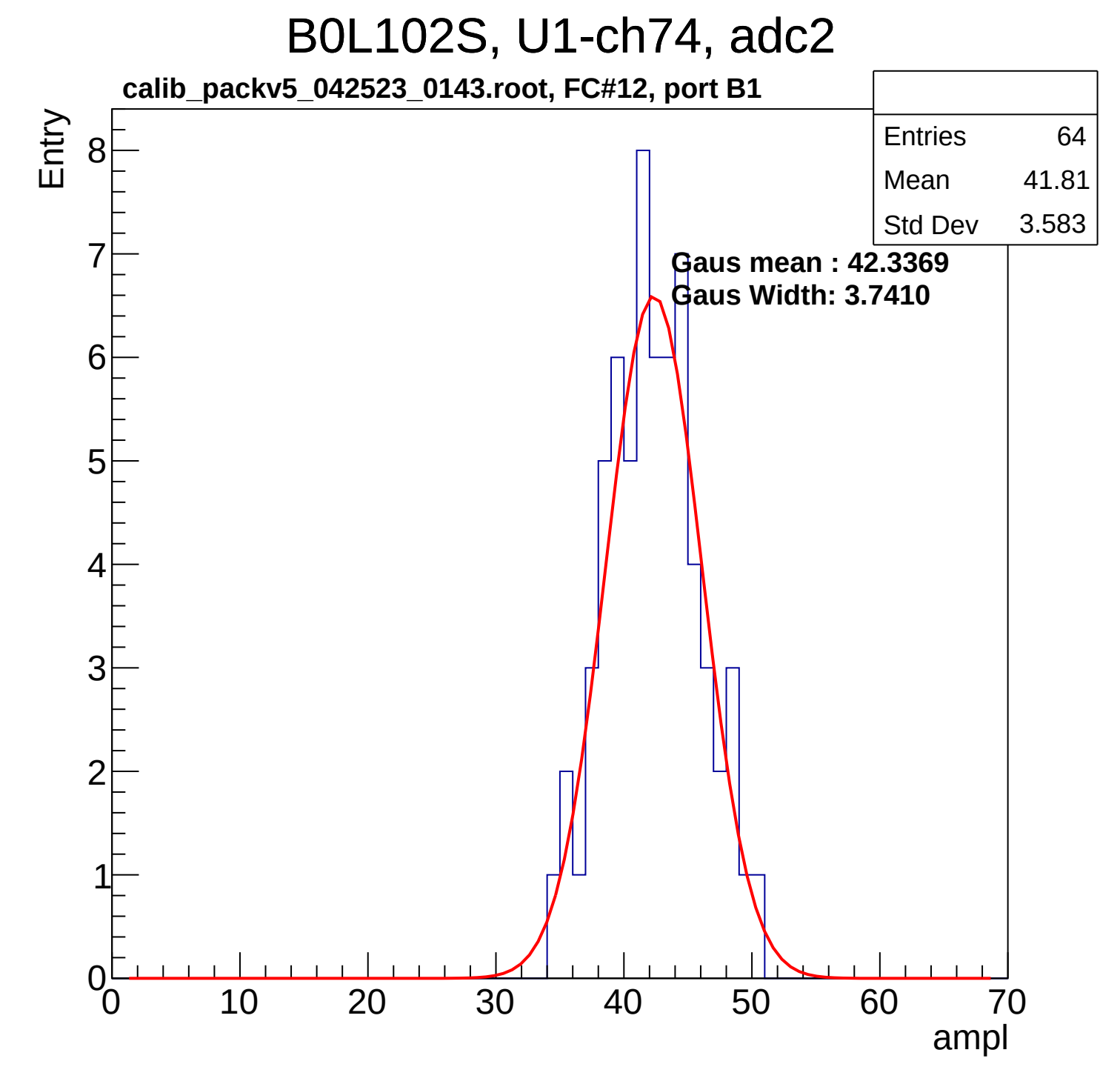
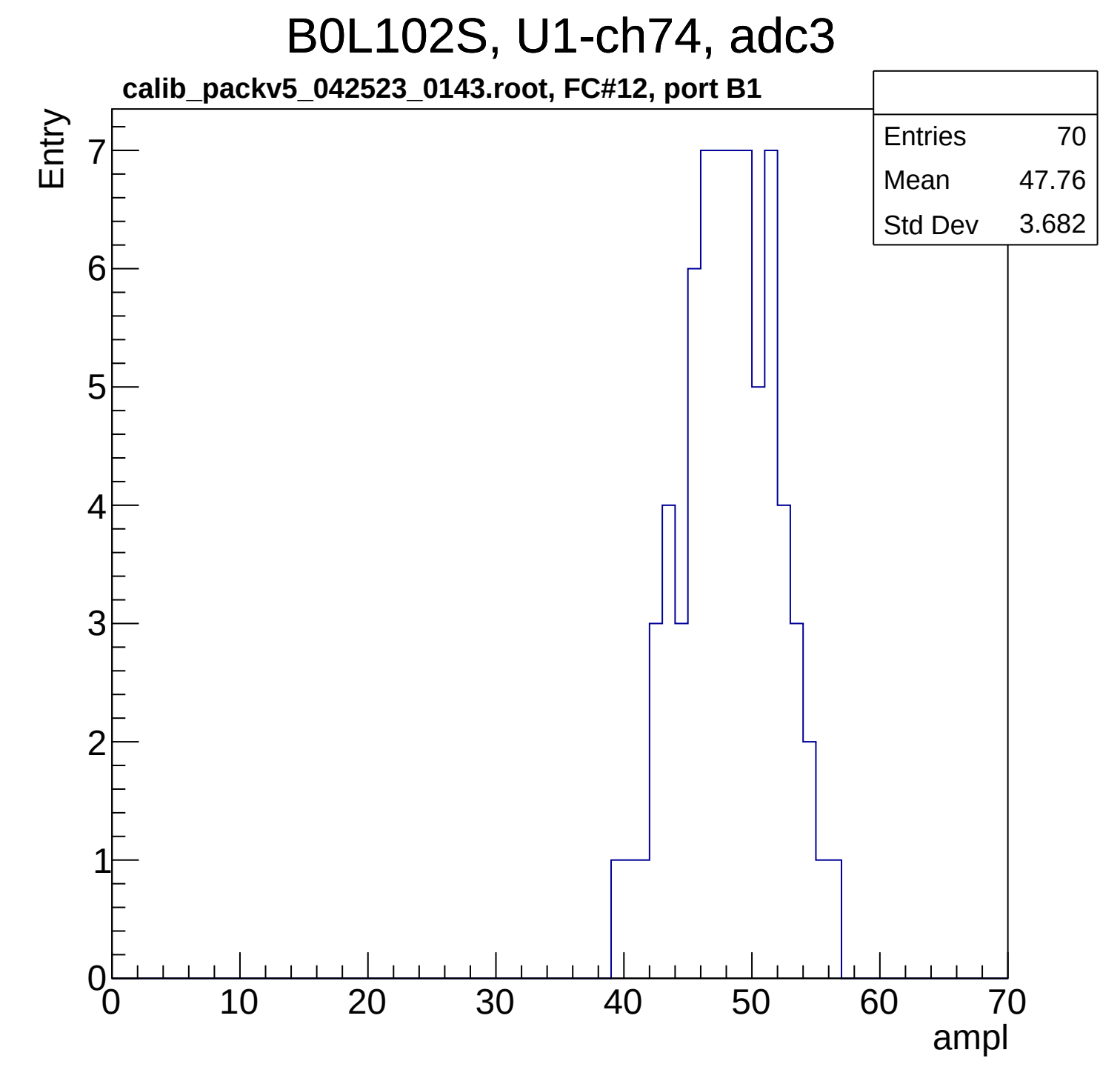
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

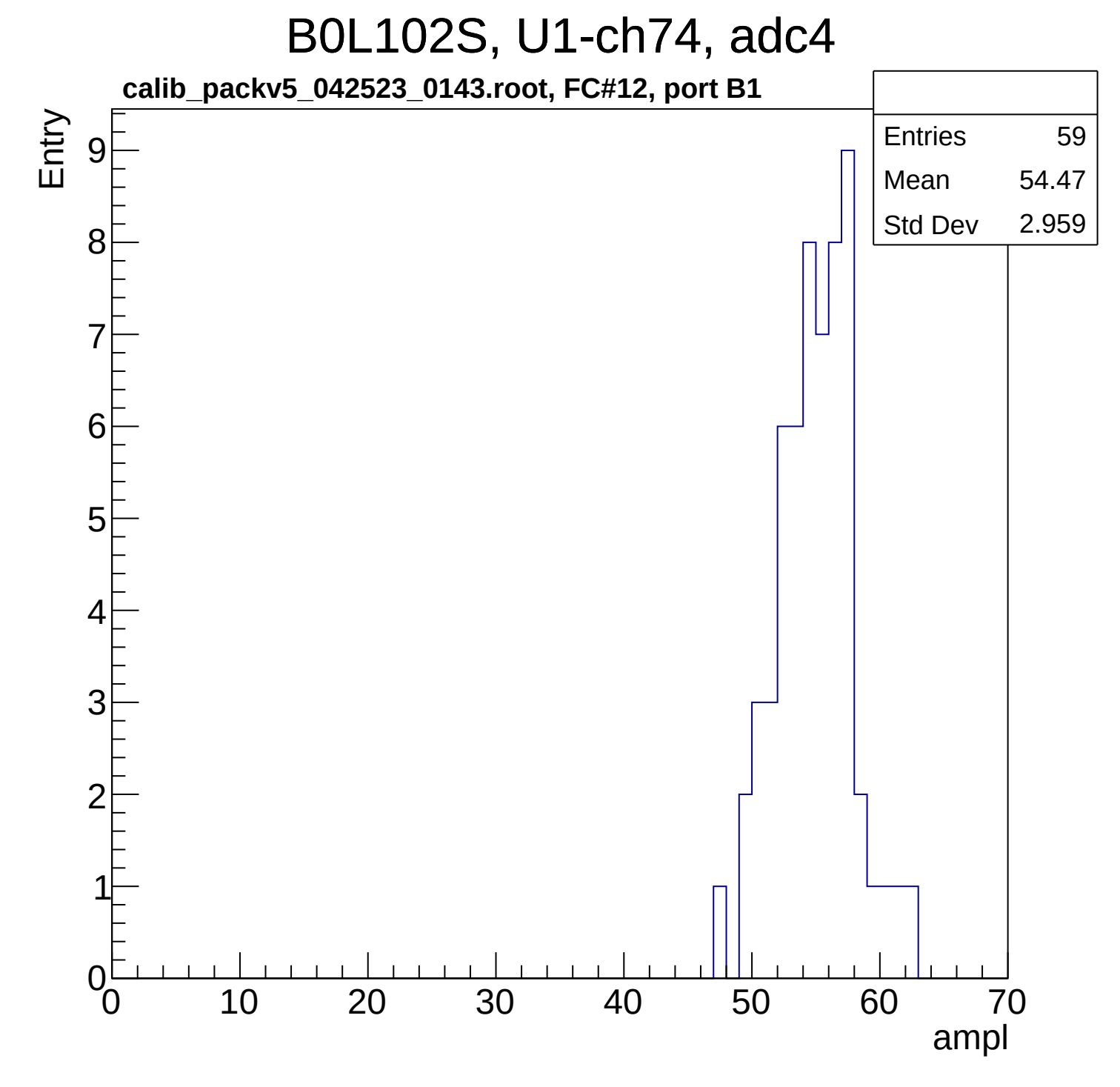

B0L102S, U1-ch73, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch74, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 27.47
Std Dev 4.997
Gaus mean : 28.6335
Gaus Width: 4.3557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch74, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 34.23
Std Dev 3.456
Gaus mean : 34.8254
Gaus Width: 3.5402
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch74, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 41.81
Std Dev 3.583
Gaus mean : 42.3369
Gaus Width: 3.7410
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch74, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 47.76
Std Dev 3.682
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch74, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 54.47
Std Dev 2.959
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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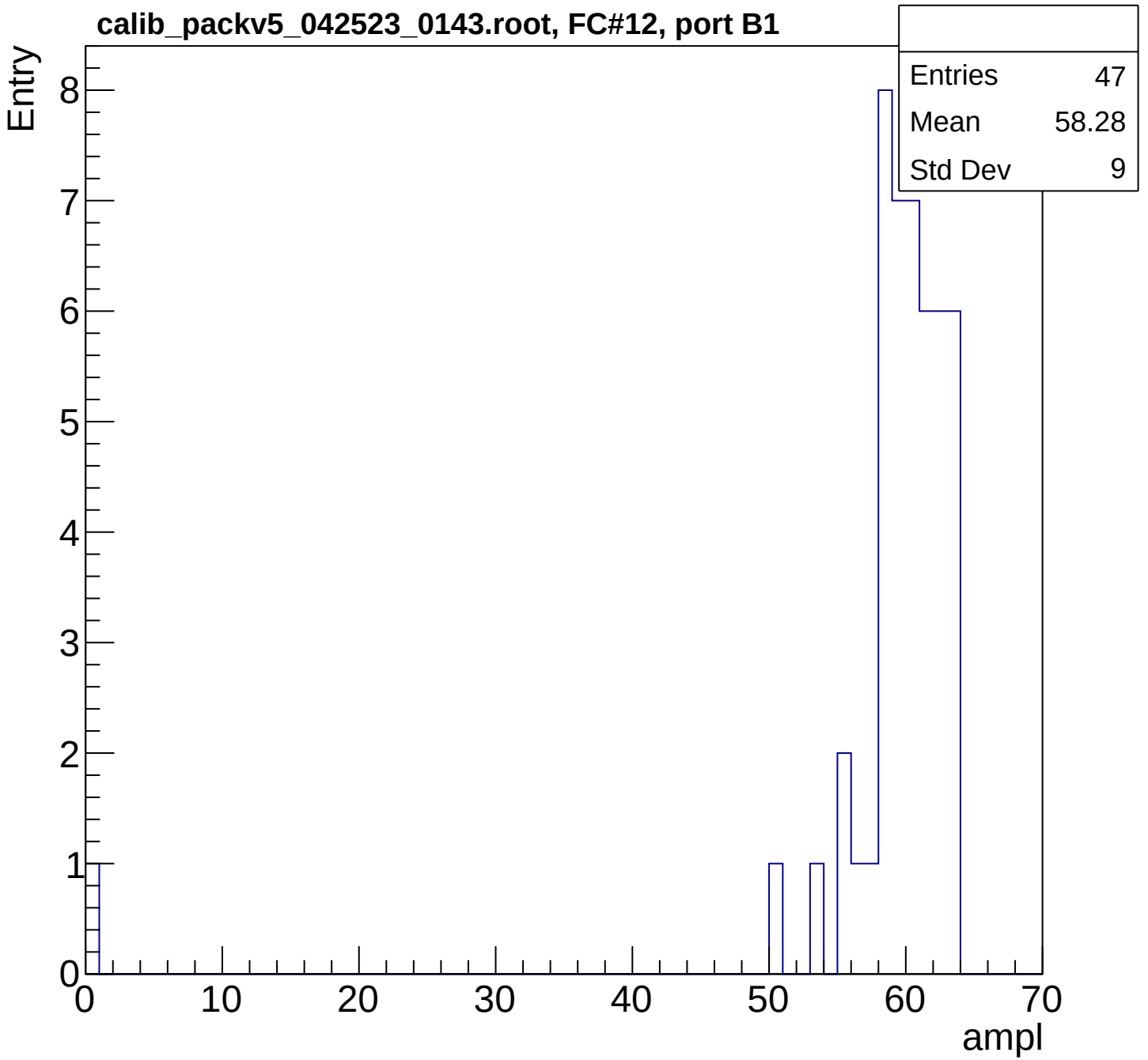
B0L102S, U1-ch74, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.28
Std Dev 9
Entry
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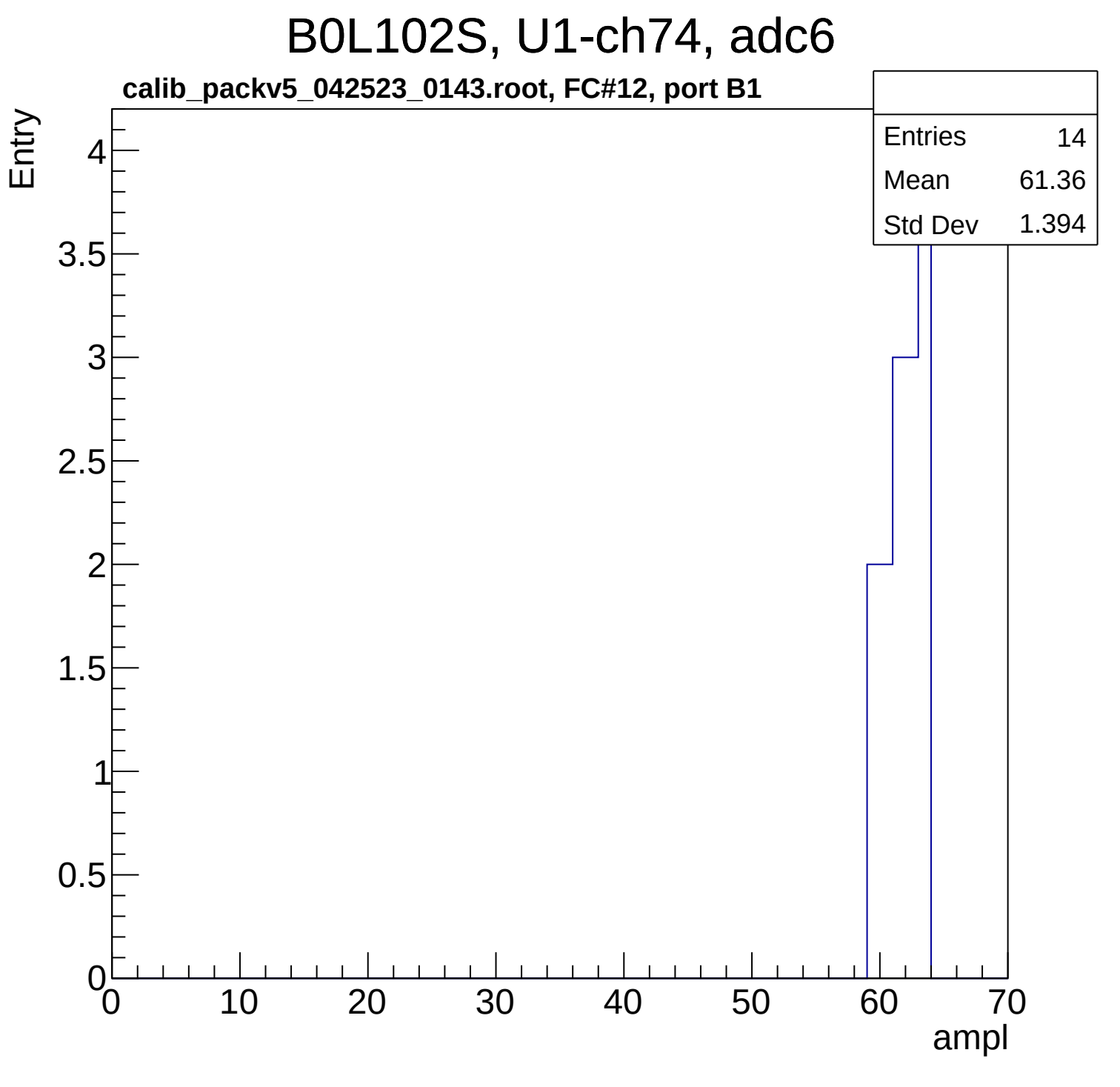
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch74, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.36
Std Dev 1.394
Entry
ampl
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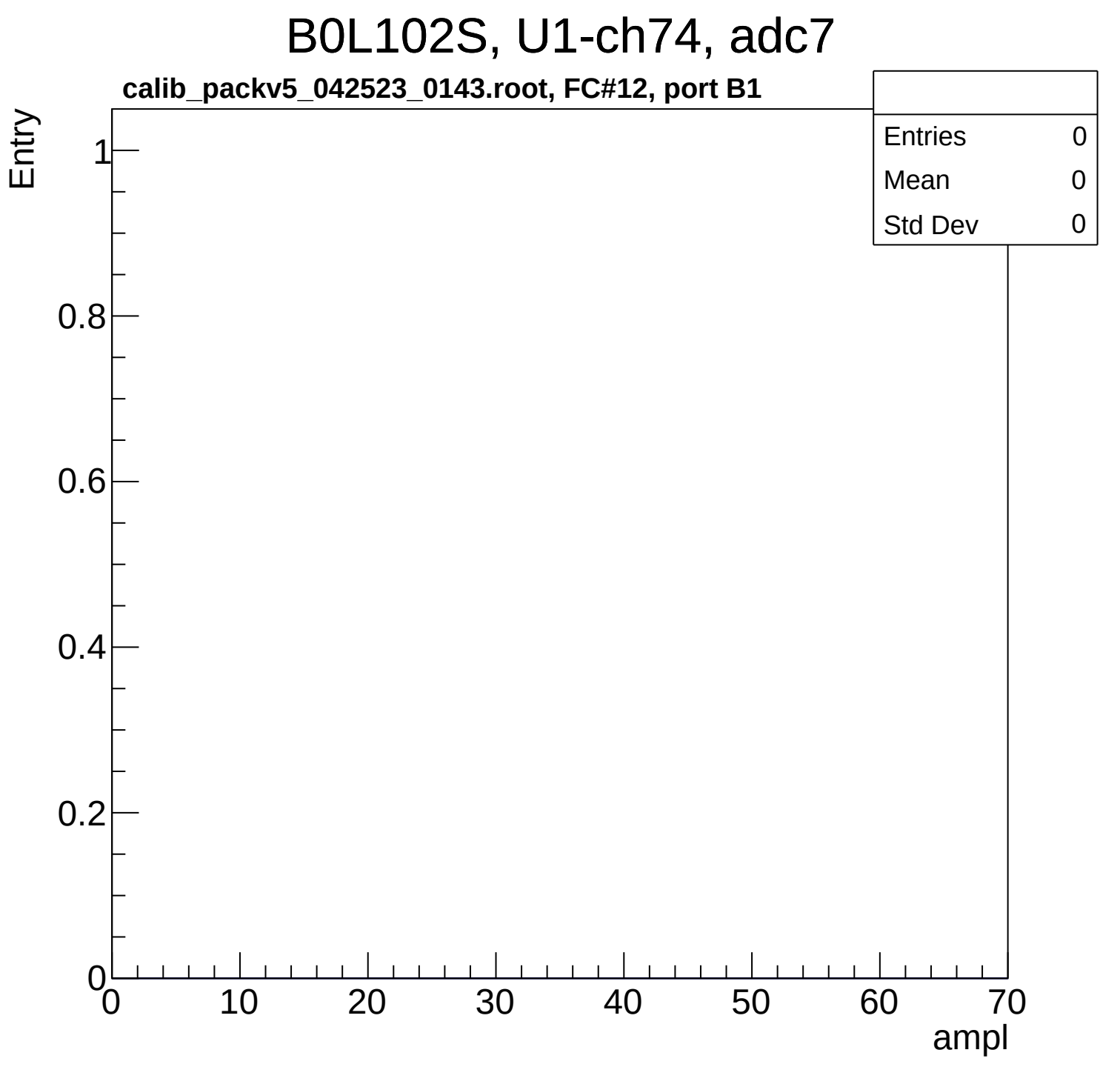
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch74, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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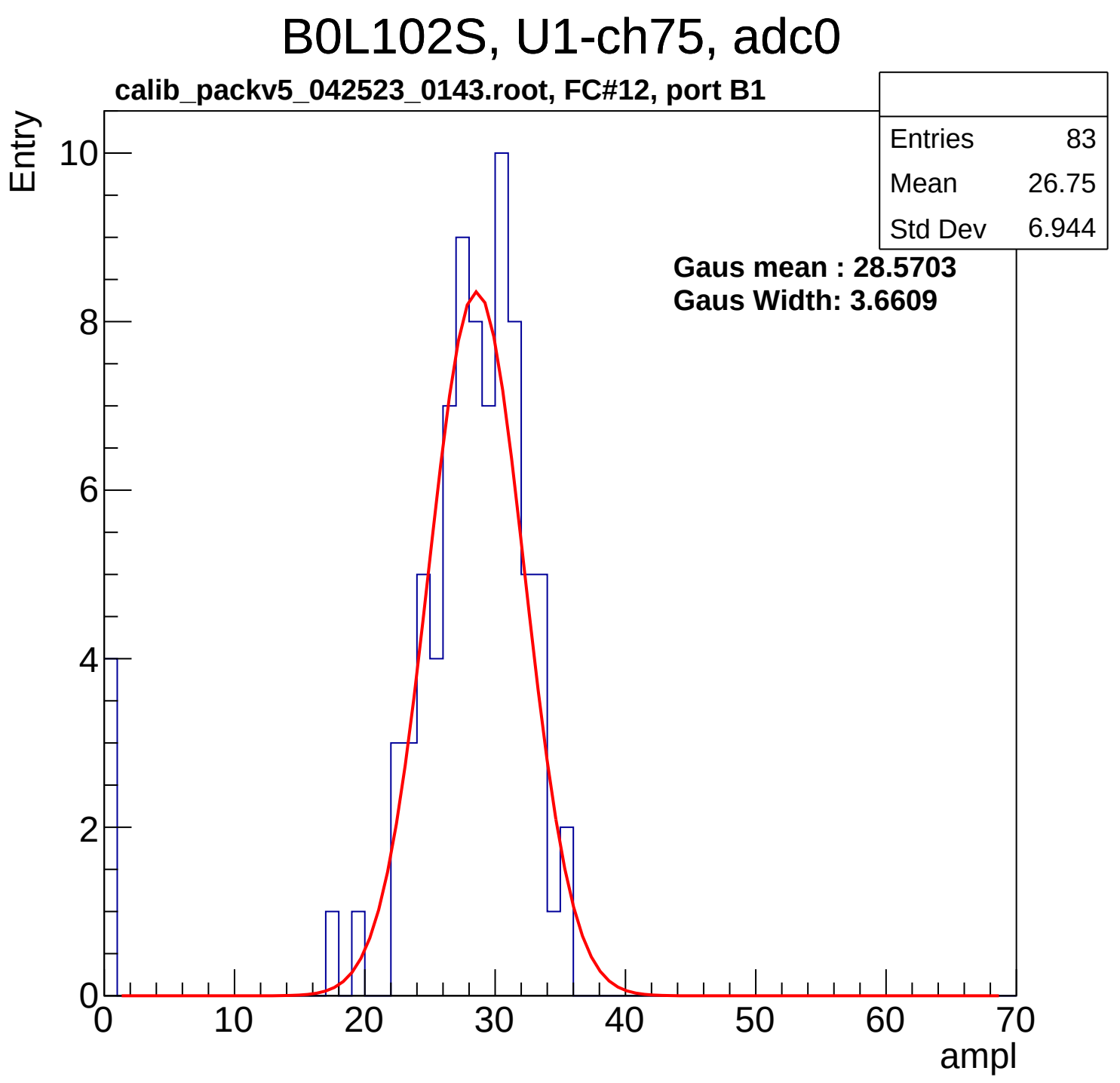
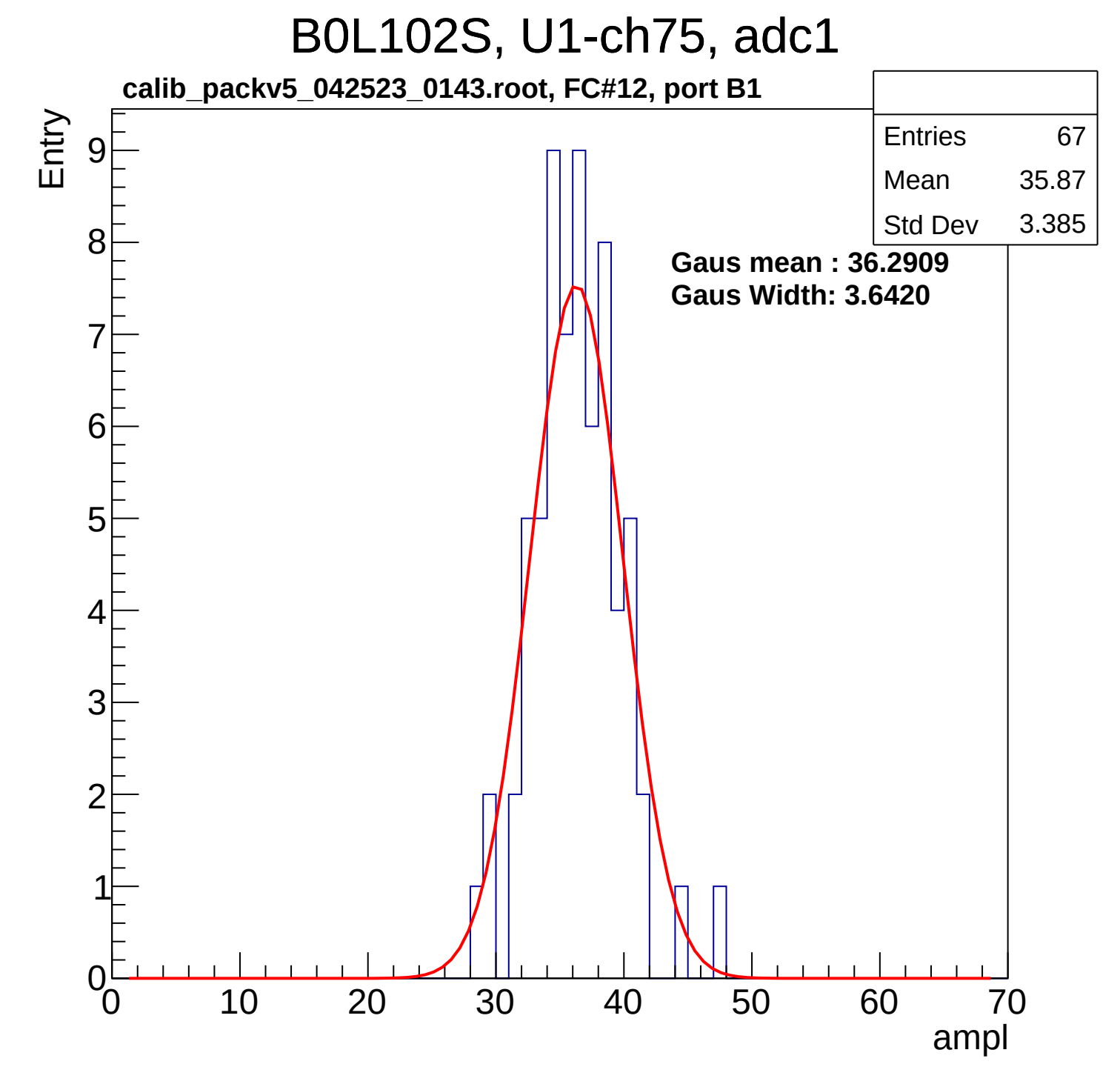
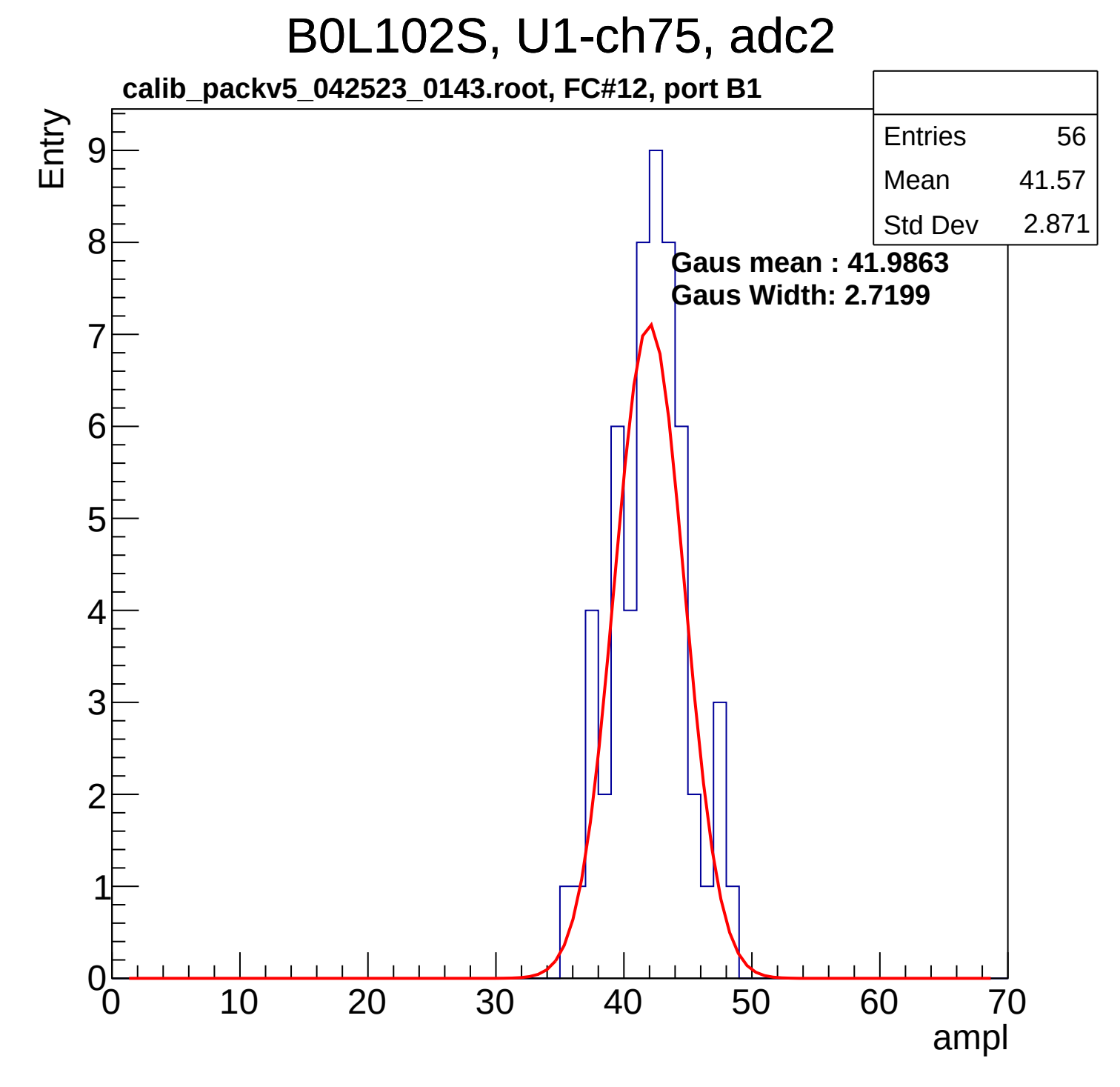
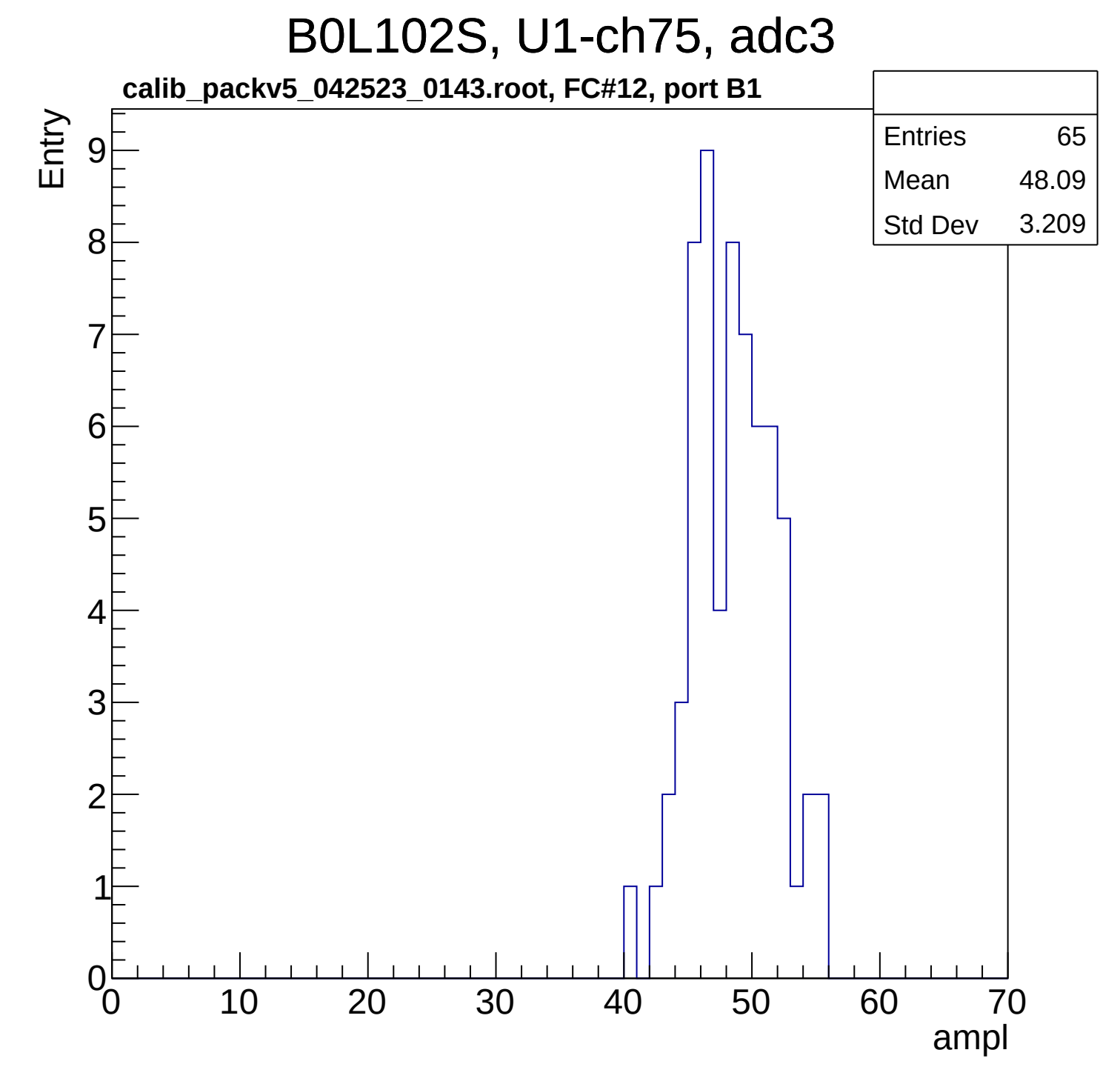
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch75, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 26.75
Std Dev 6.944
Gaus mean : 28.5703
Gaus Width: 3.6609
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch75, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.87
Std Dev 3.385
Gaus mean : 36.2909
Gaus Width: 3.6420
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch75, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 41.57
Std Dev 2.871
Gaus mean : 41.9863
Gaus Width: 2.7199
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch75, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.09
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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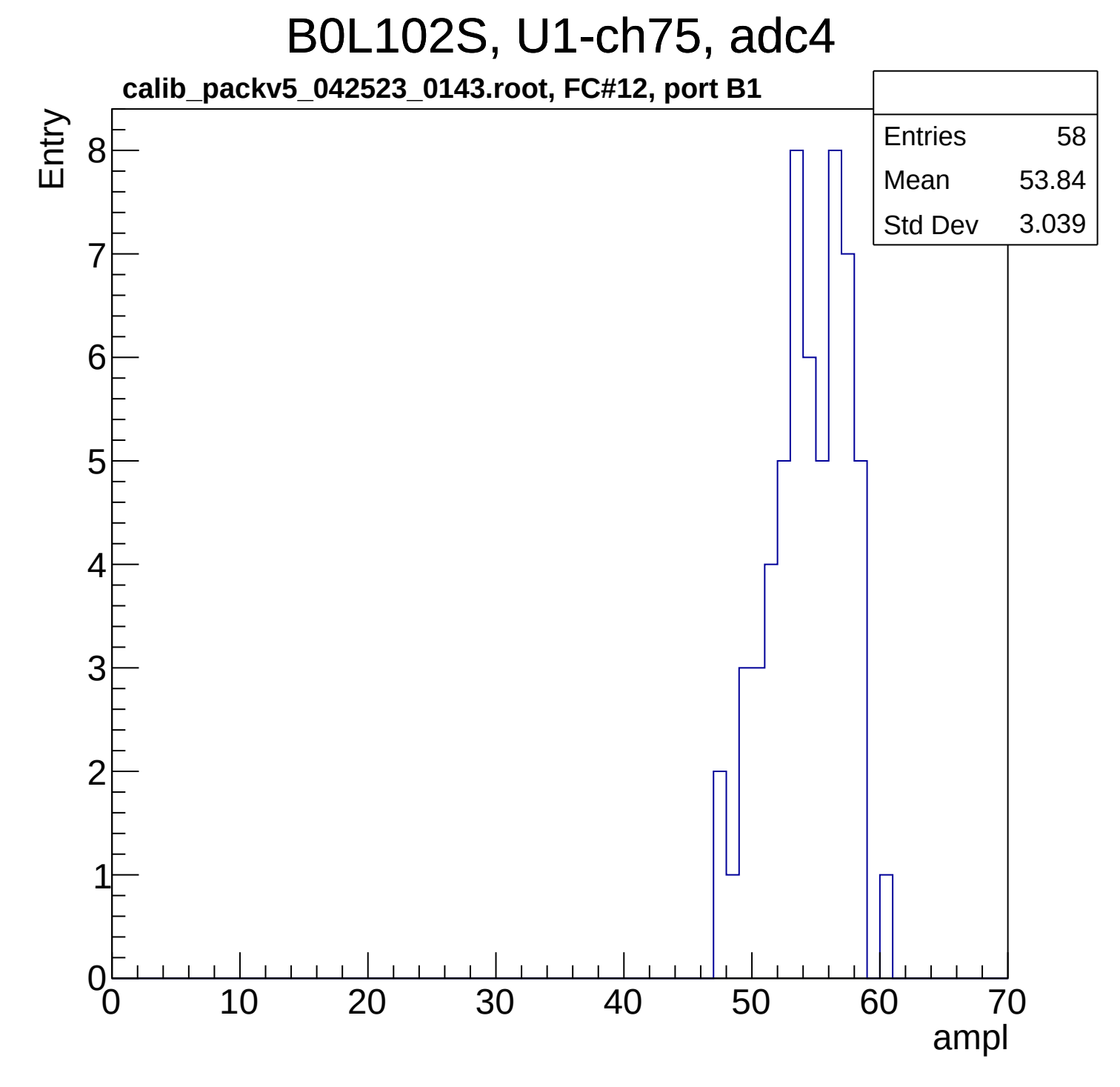
B0L102S, U1-ch75, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 53.84
Std Dev 3.039
Entry
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2
3
4
5
6
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8
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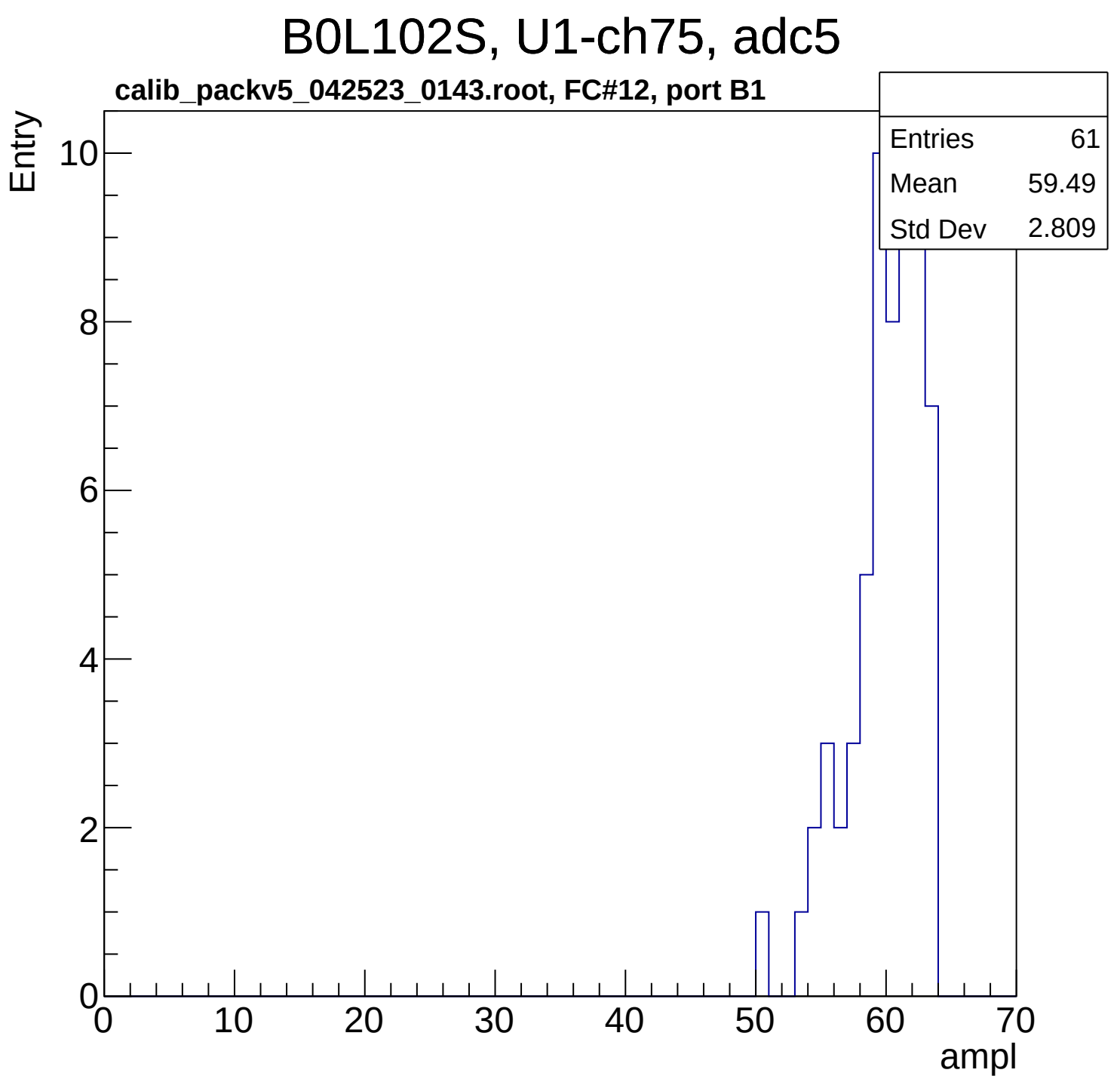
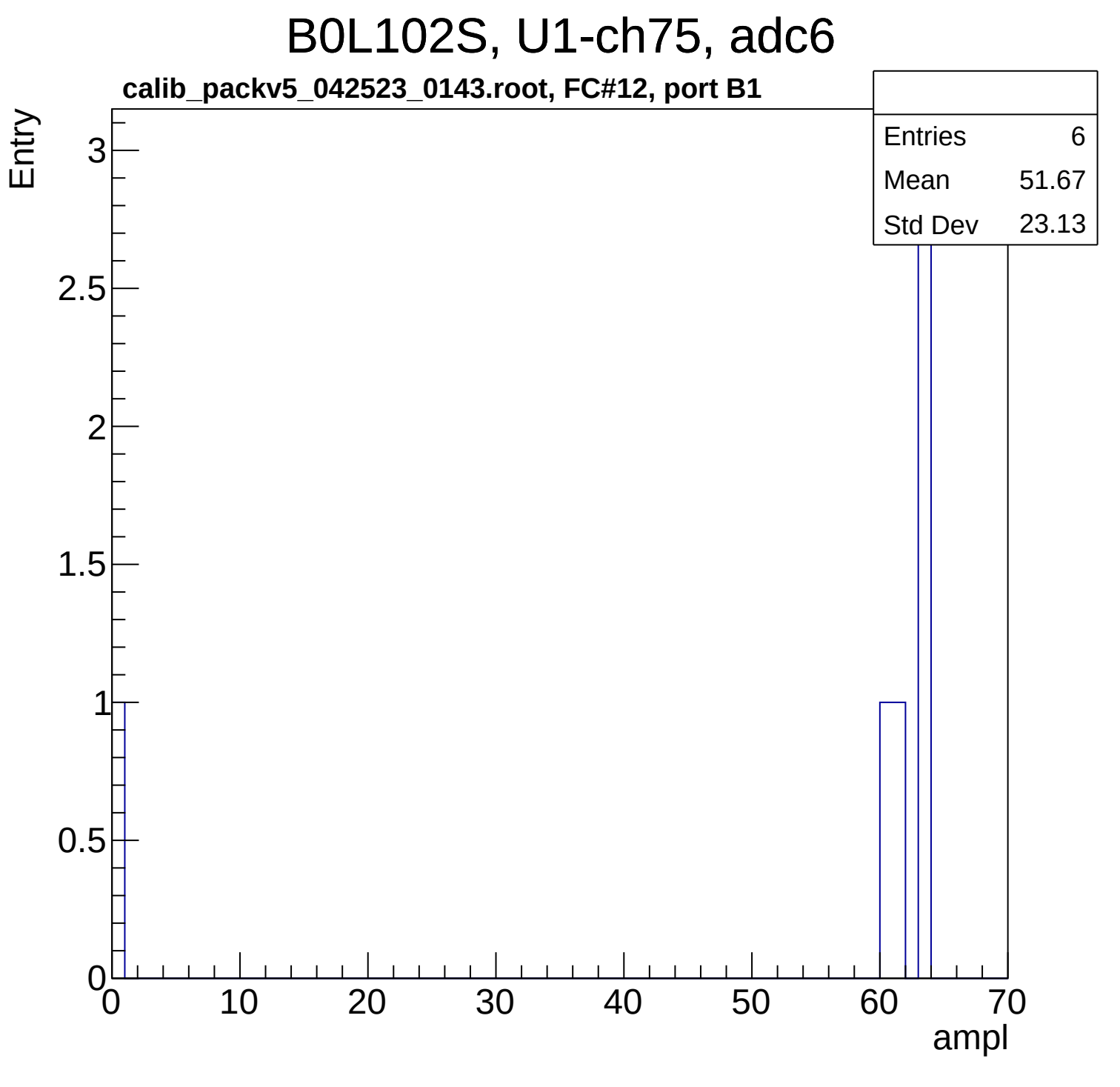
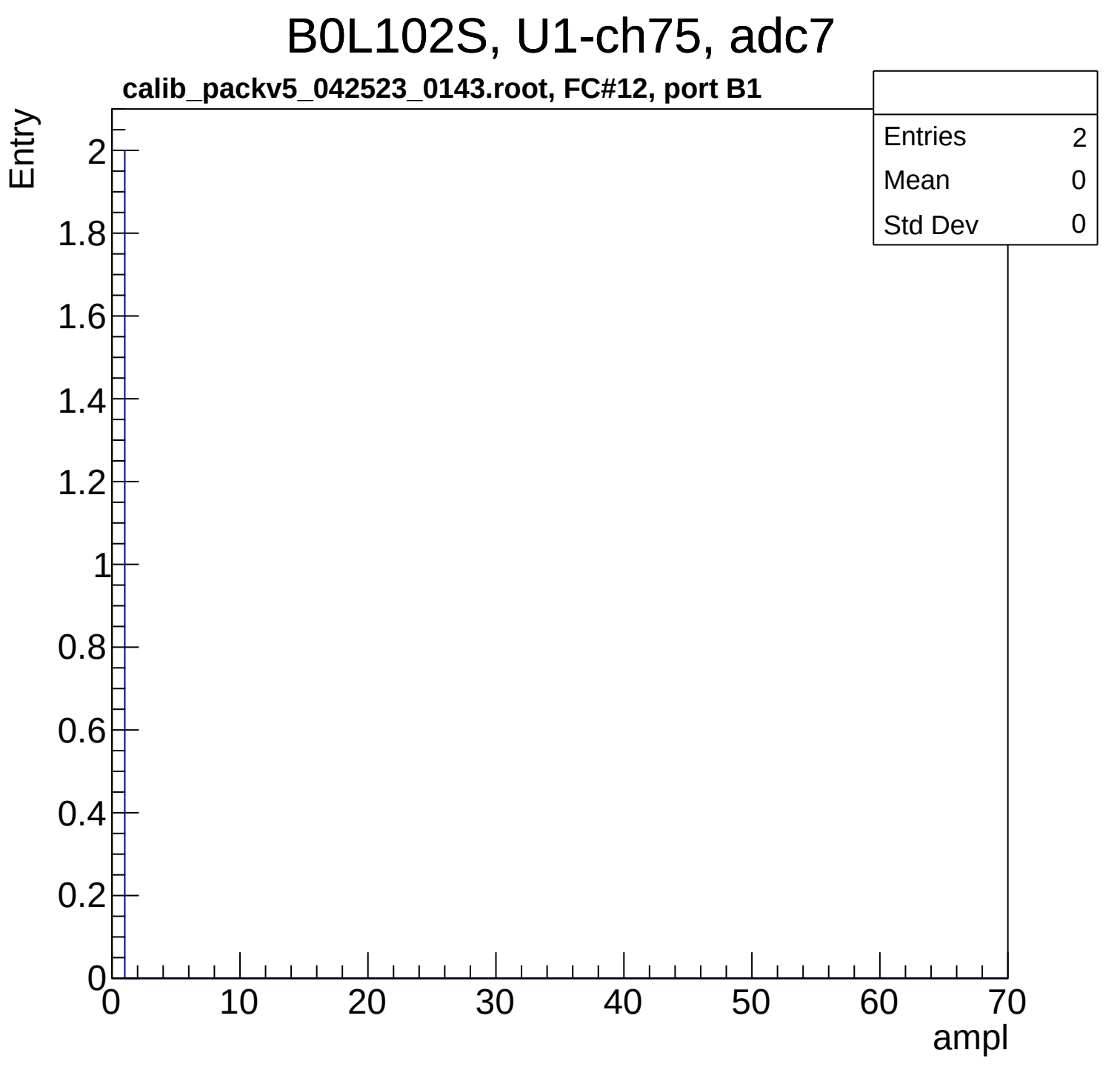
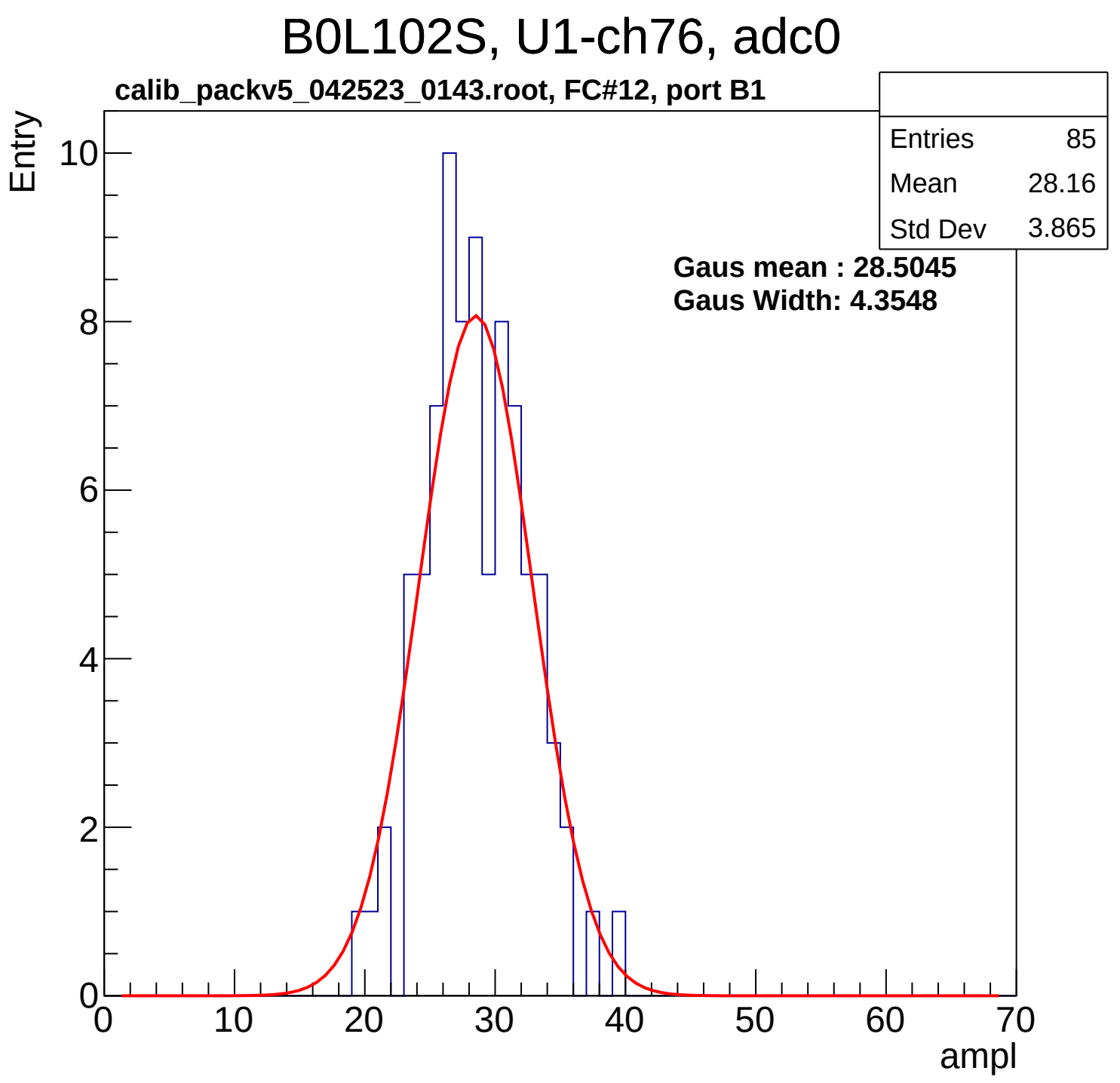
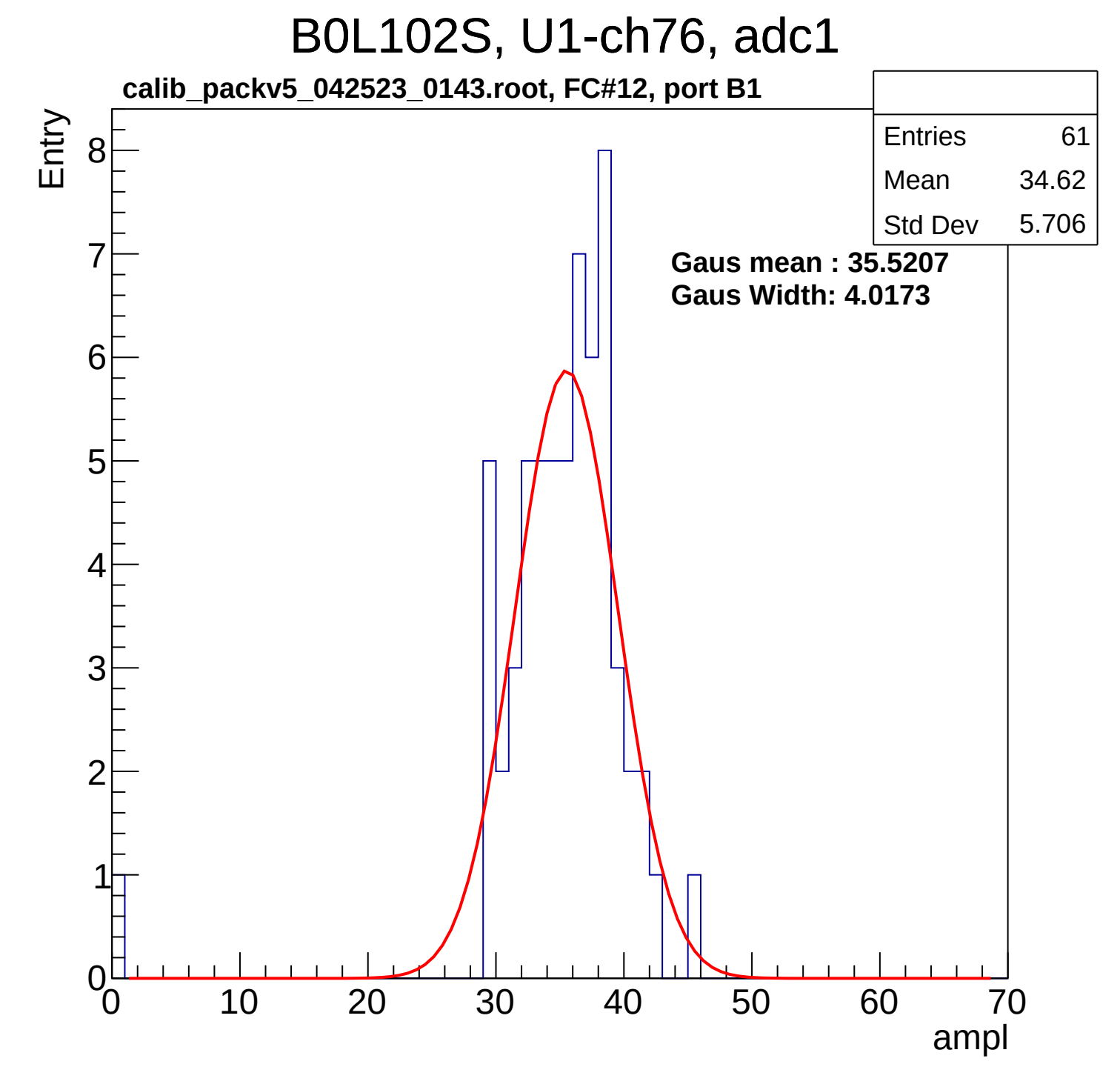
10
20
30
40
50
60
70
ampl

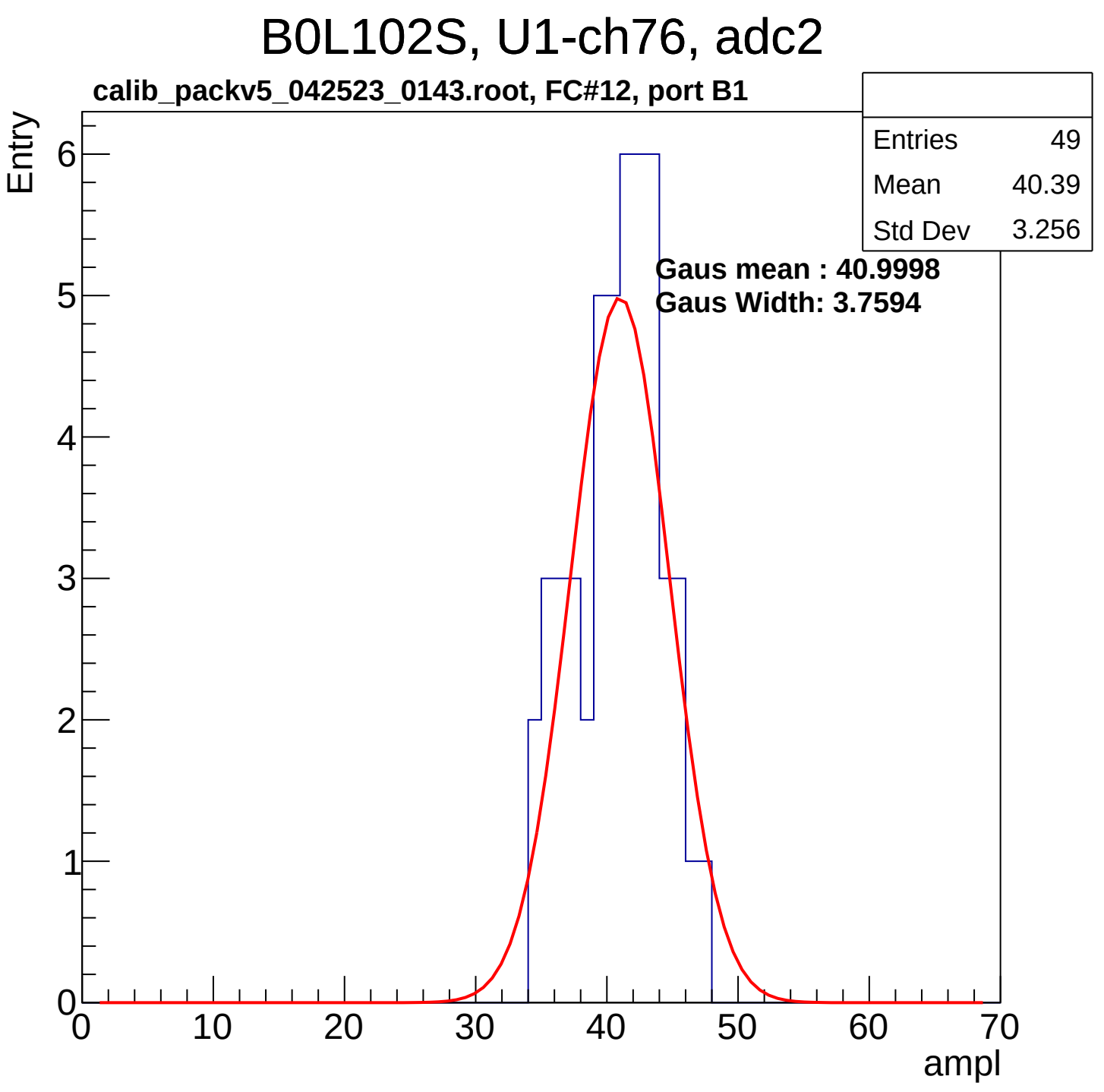

B0L102S, U1-ch75, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 59.49
Std Dev 2.809
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

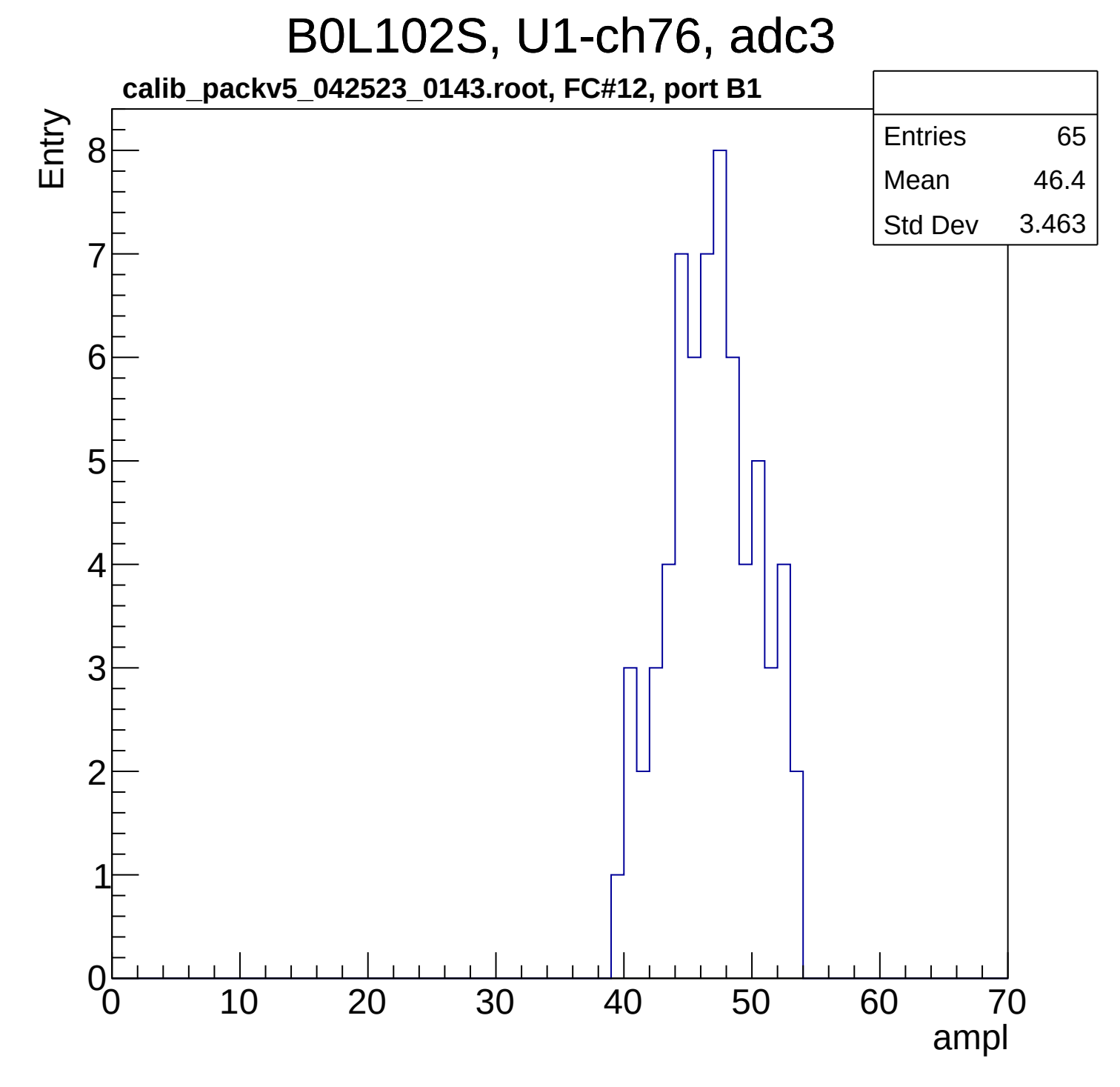

B0L102S, U1-ch75, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 51.67
Std Dev 23.13
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

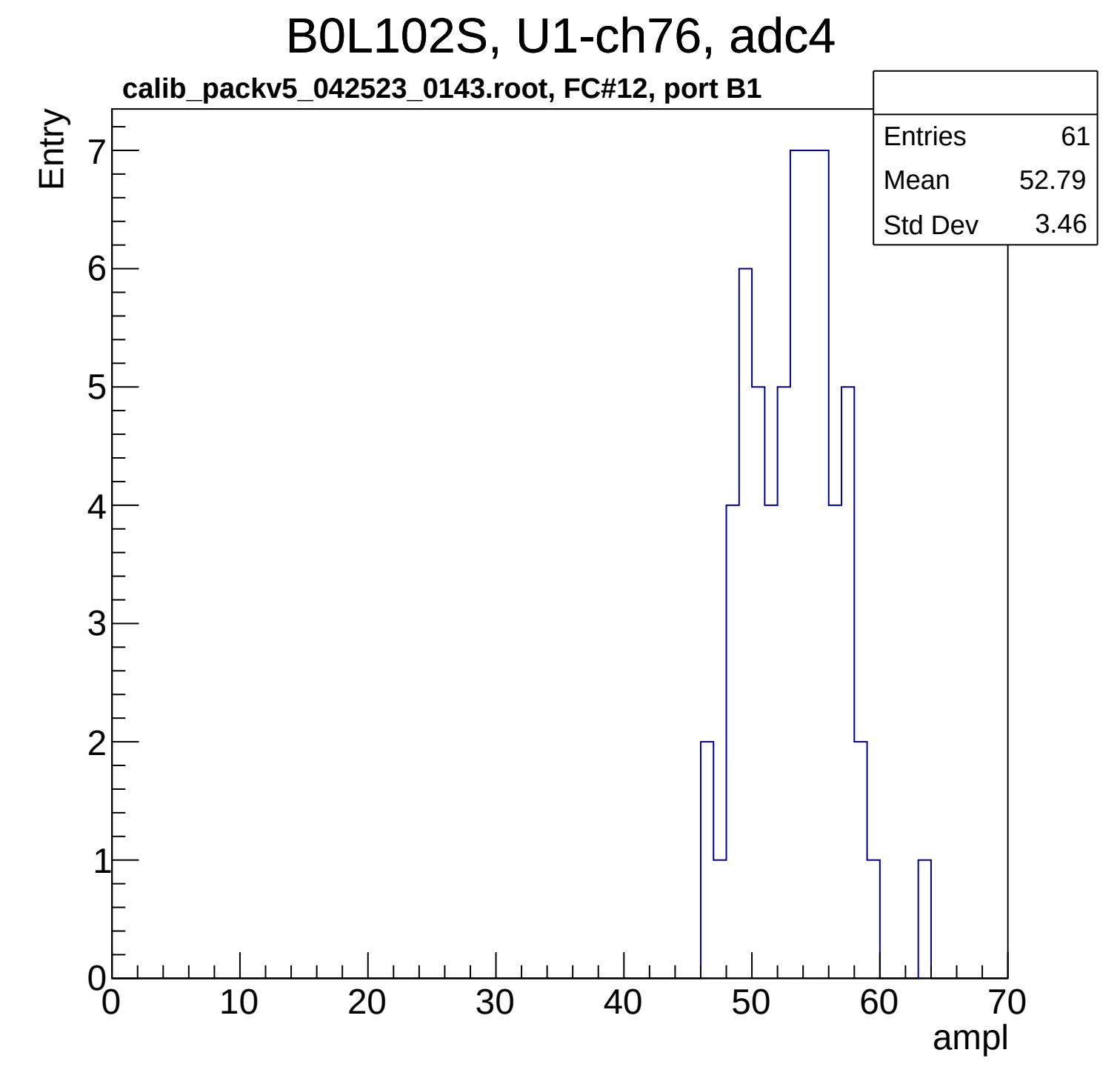

B0L102S, U1-ch75, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

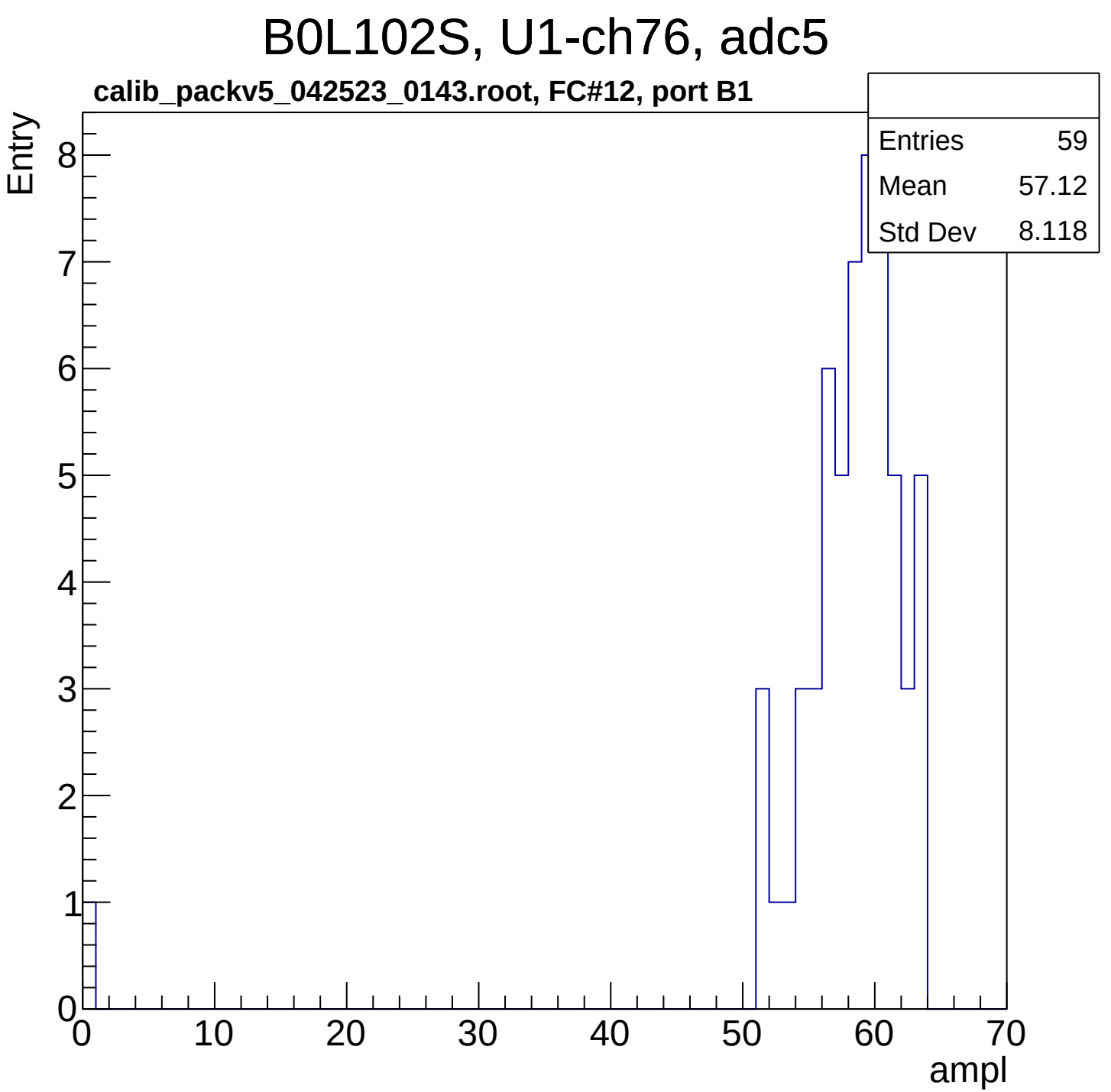

B0L102S, U1-ch76, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 28.16
Std Dev 3.865
Gaus mean : 28.5045
Gaus Width: 4.3548
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch76, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 34.62
Std Dev 5.706
Gaus mean : 35.5207
Gaus Width: 4.0173
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch76, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 40.39
Std Dev 3.256
Gaus mean : 40.9998
Gaus Width: 3.7594
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U1-ch76, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 46.4
Std Dev 3.463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch76, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 52.79
Std Dev 3.46
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch76, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 57.12
Std Dev 8.118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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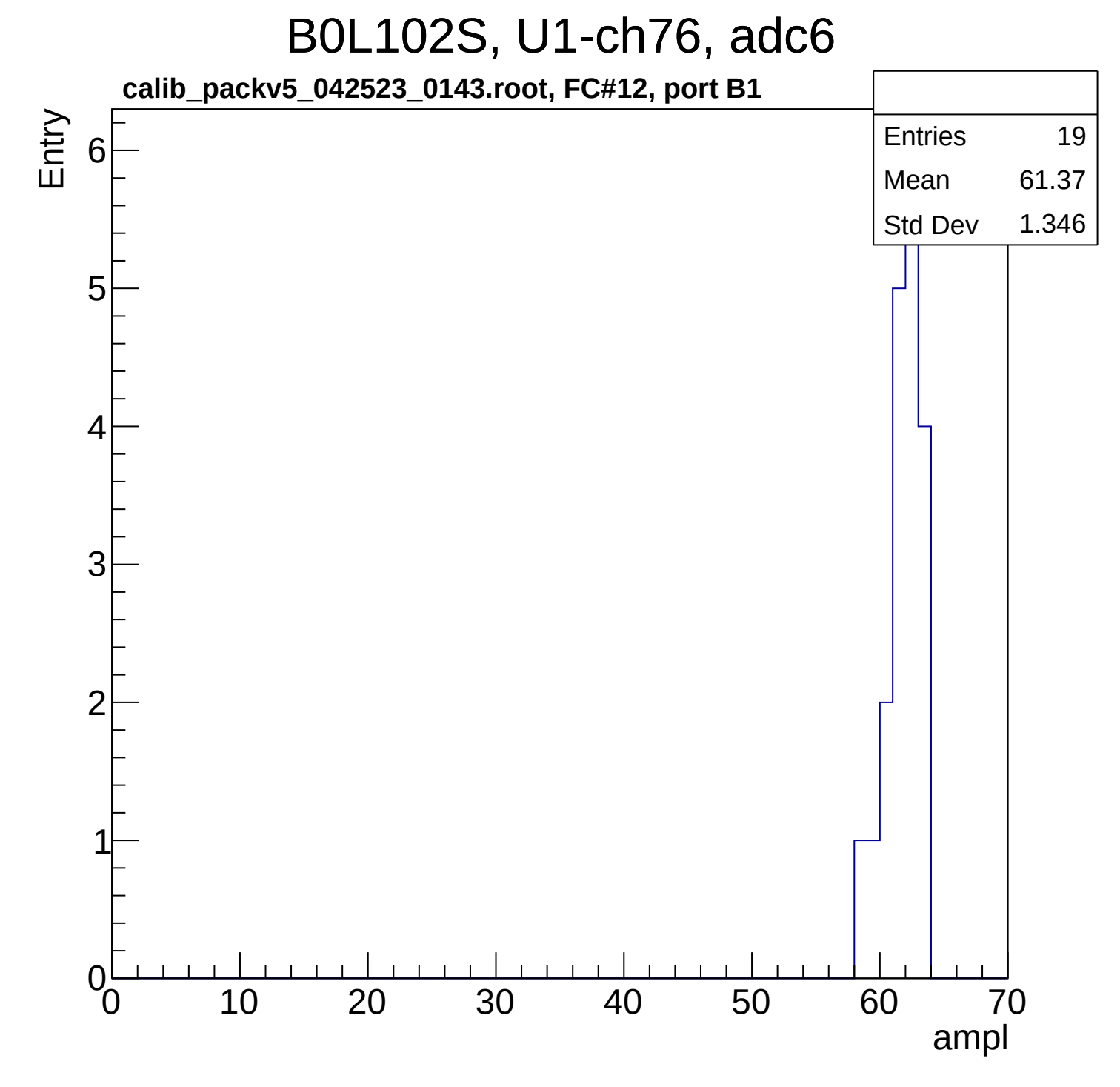
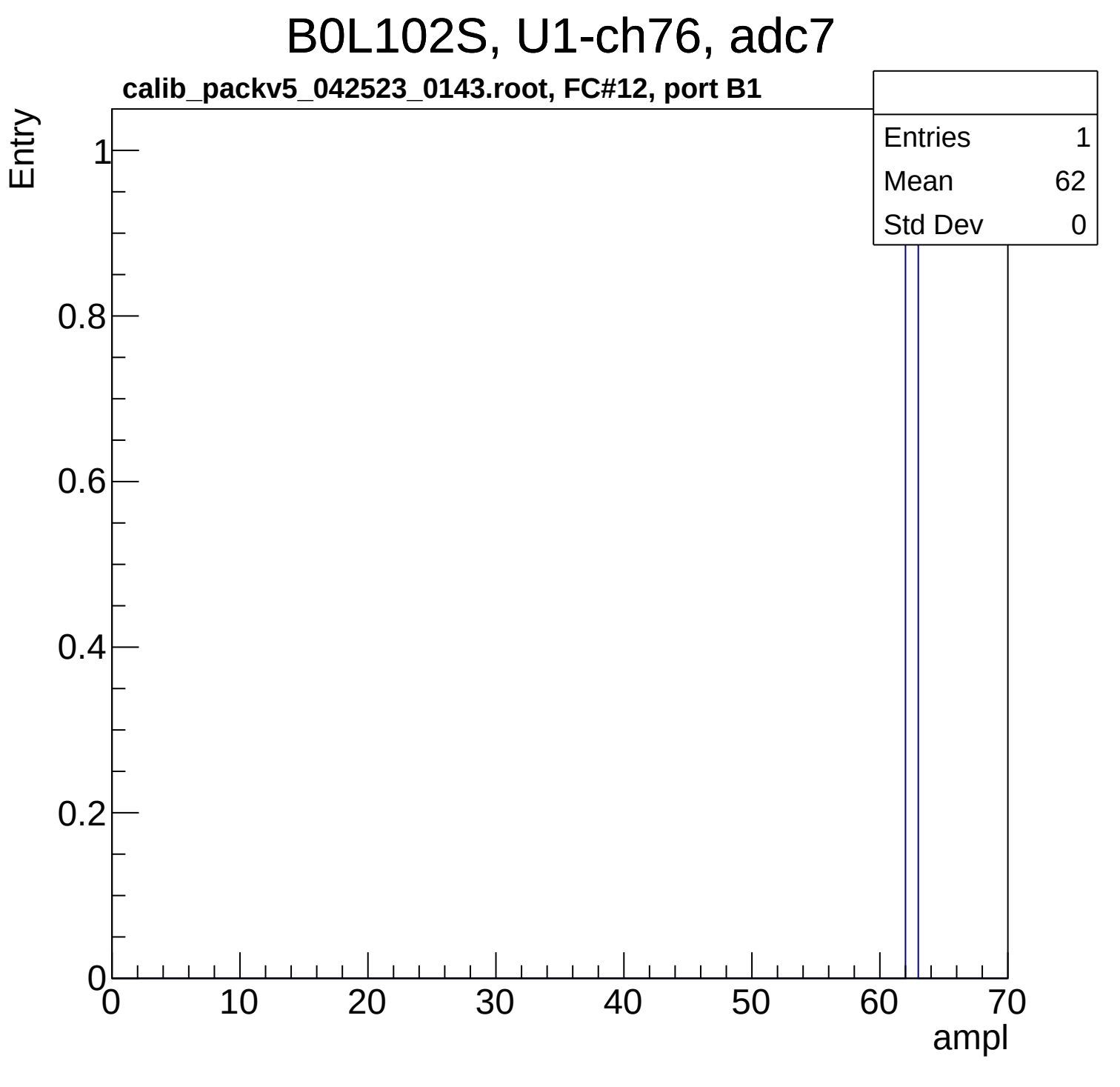
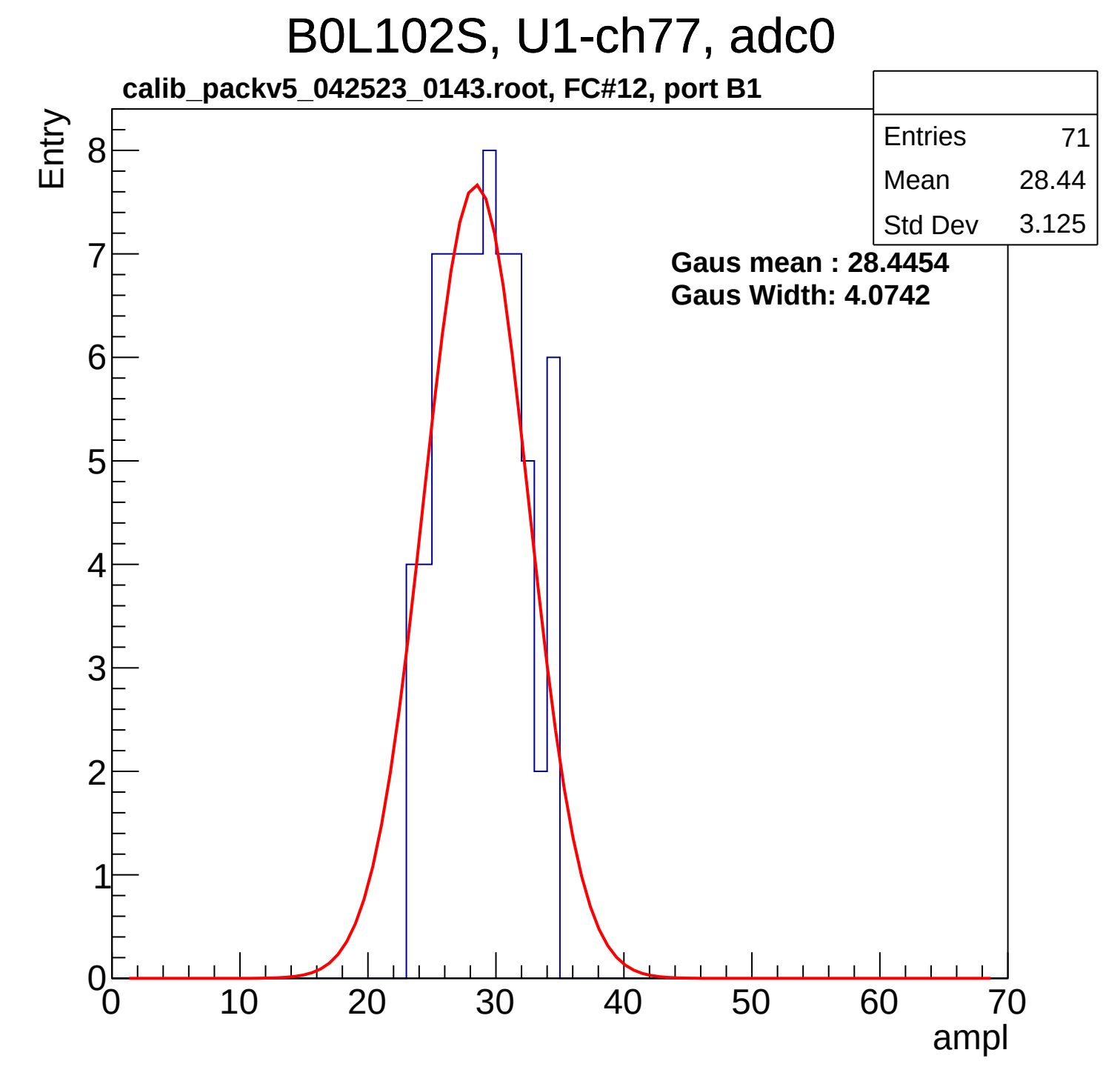

B0L102S, U1-ch76, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 19
Mean 61.37
Std Dev 1.346
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch76, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch77, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 28.44
Std Dev 3.125
Gaus mean : 28.4454
Gaus Width: 4.0742
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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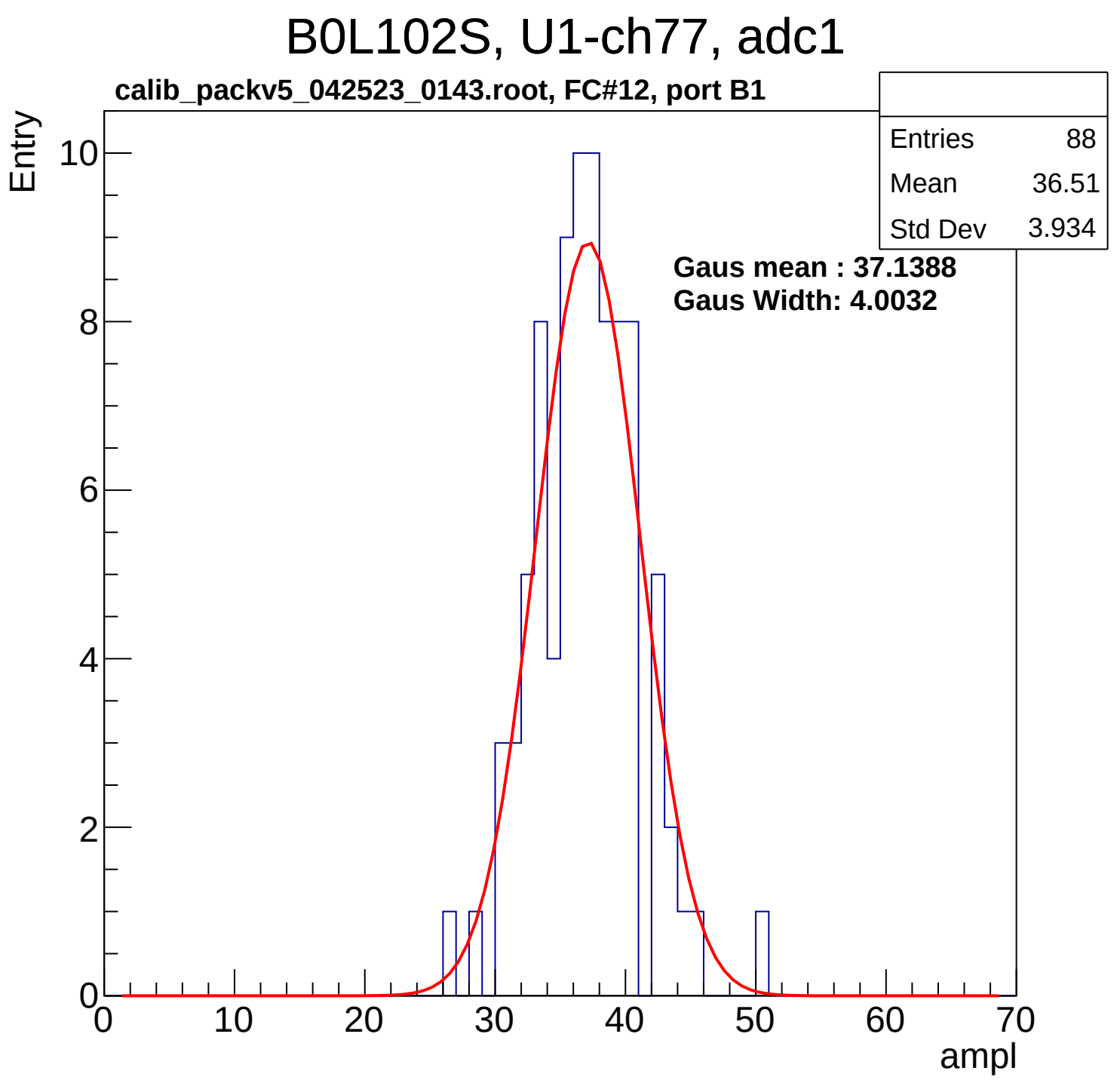
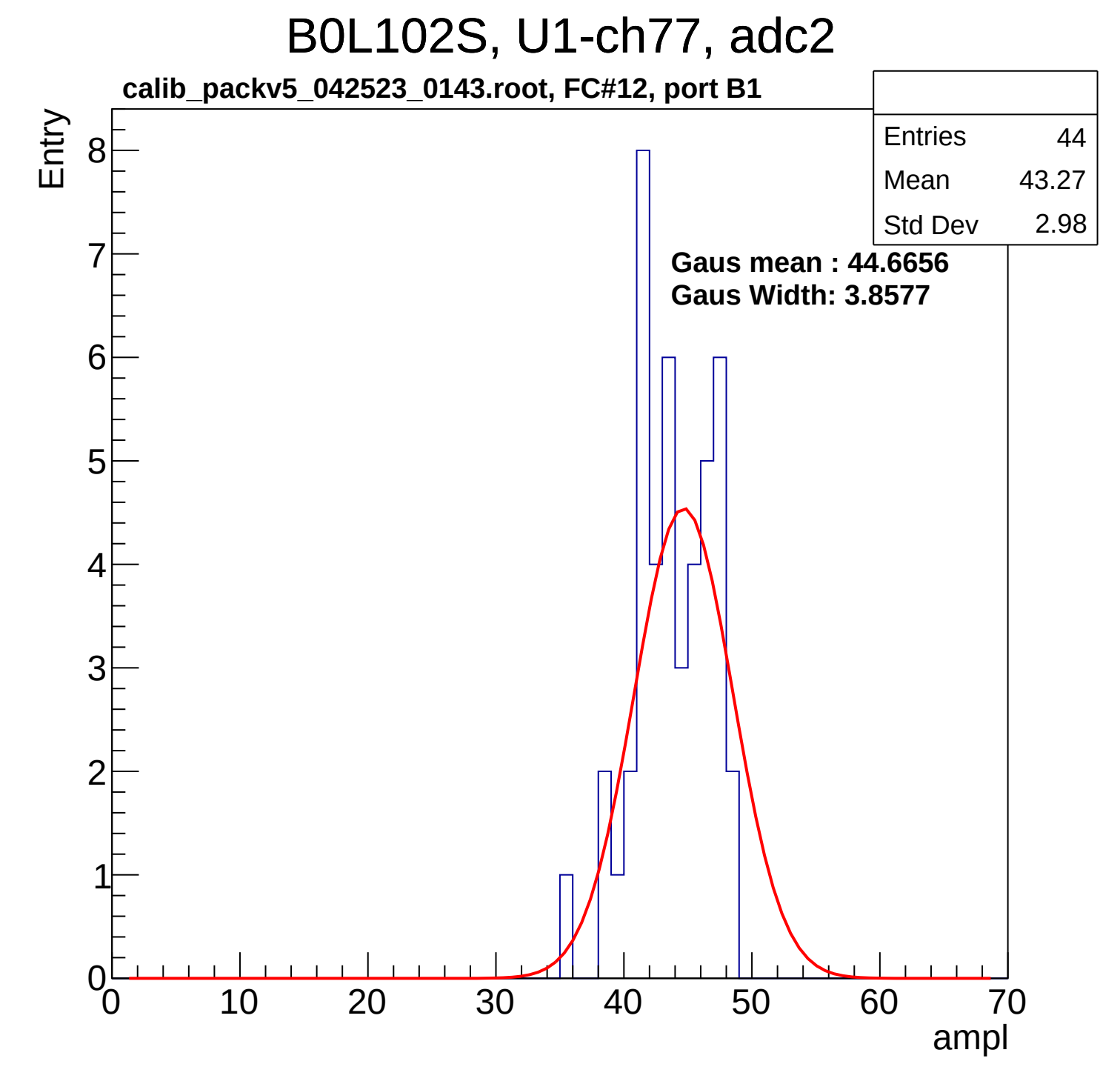
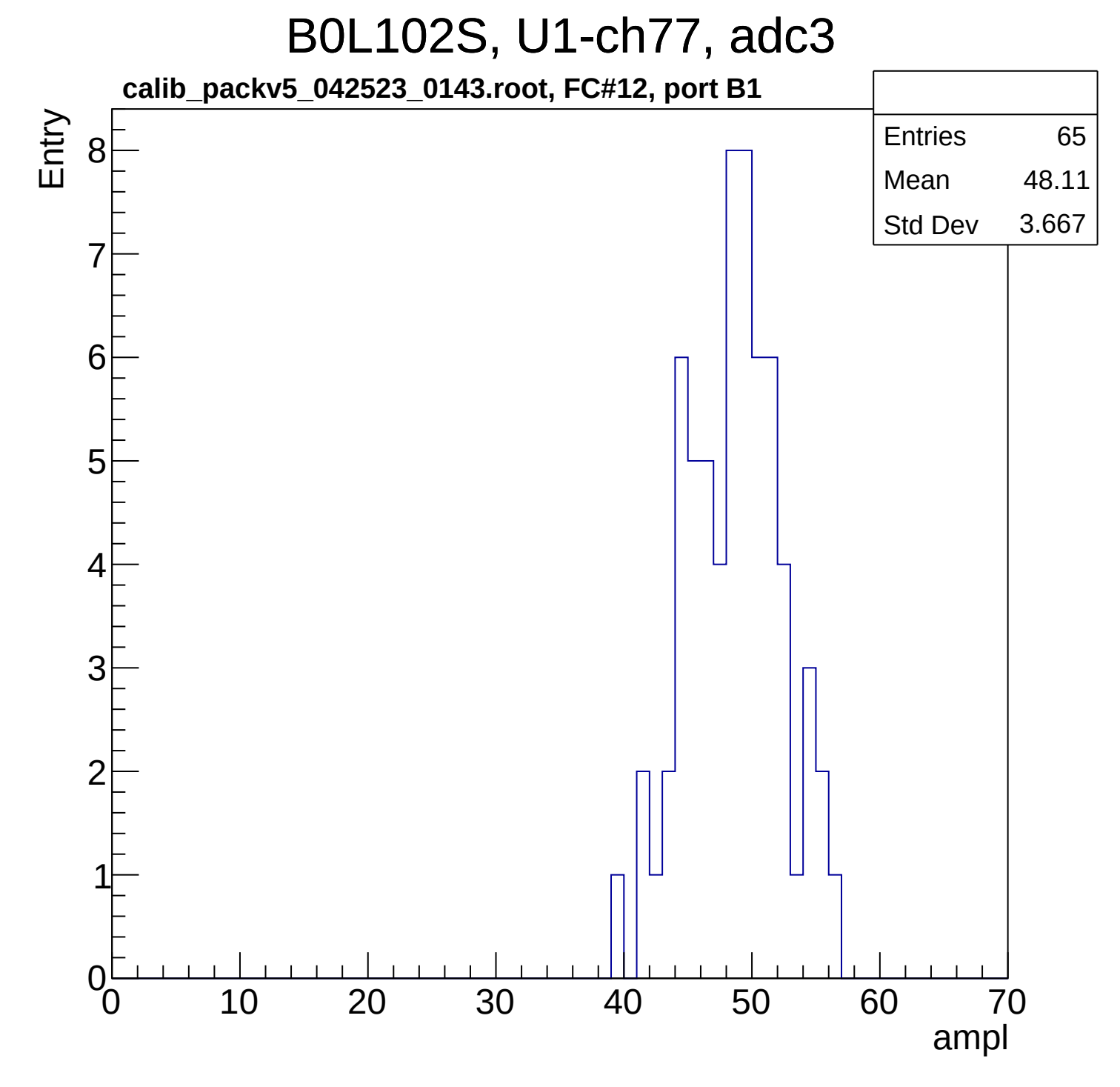
40
50
60
70


B0L102S, U1-ch77, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 36.51
Std Dev 3.934
Gaus mean : 37.1388
Gaus Width: 4.0032
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch77, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 43.27
Std Dev 2.98
Gaus mean : 44.6656
Gaus Width: 3.8577
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch77, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.11
Std Dev 3.667
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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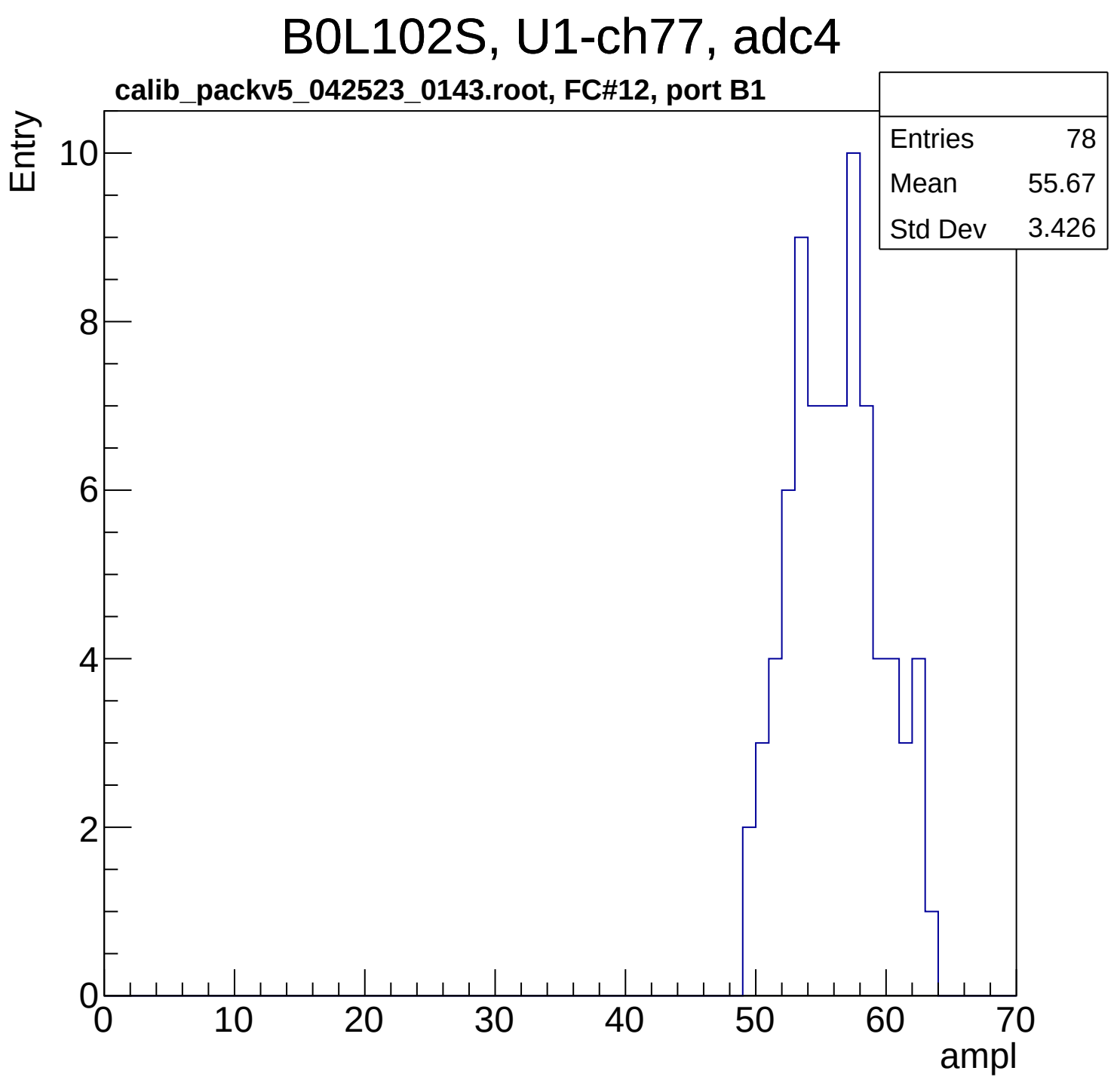
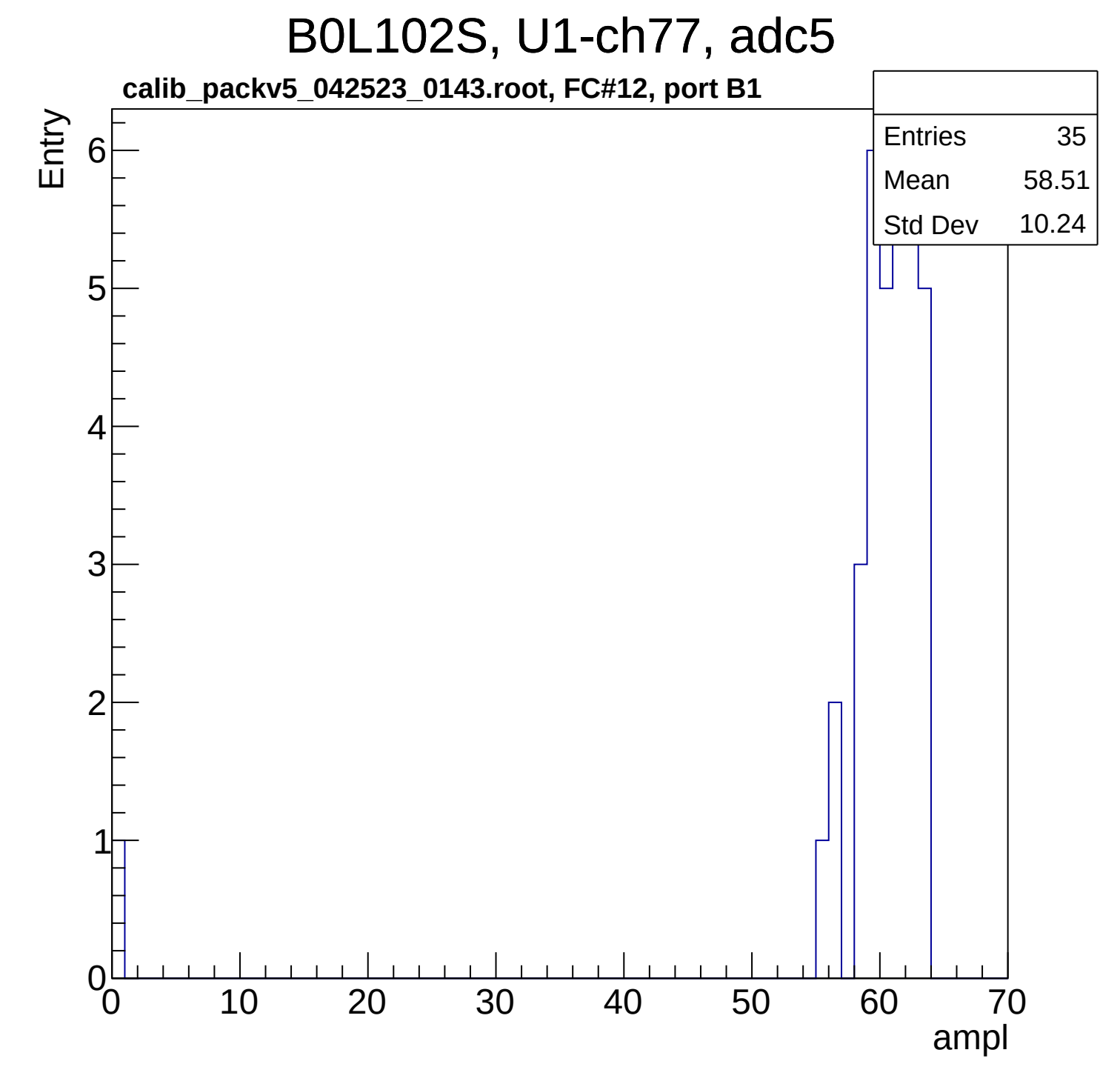
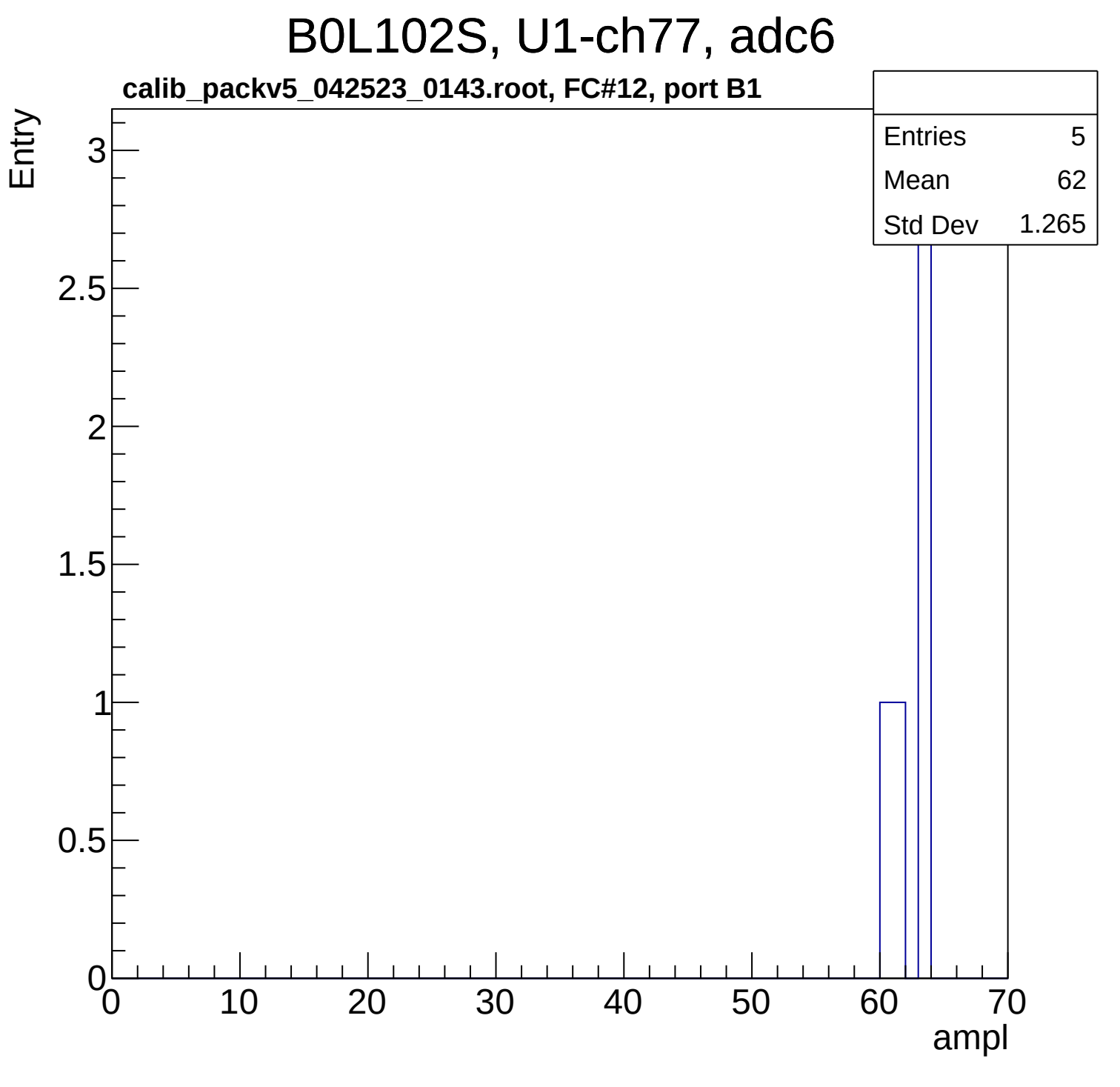
40
50
60
70


B0L102S, U1-ch77, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 55.67
Std Dev 3.426
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch77, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 58.51
Std Dev 10.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch77, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62
Std Dev 1.265
Entry
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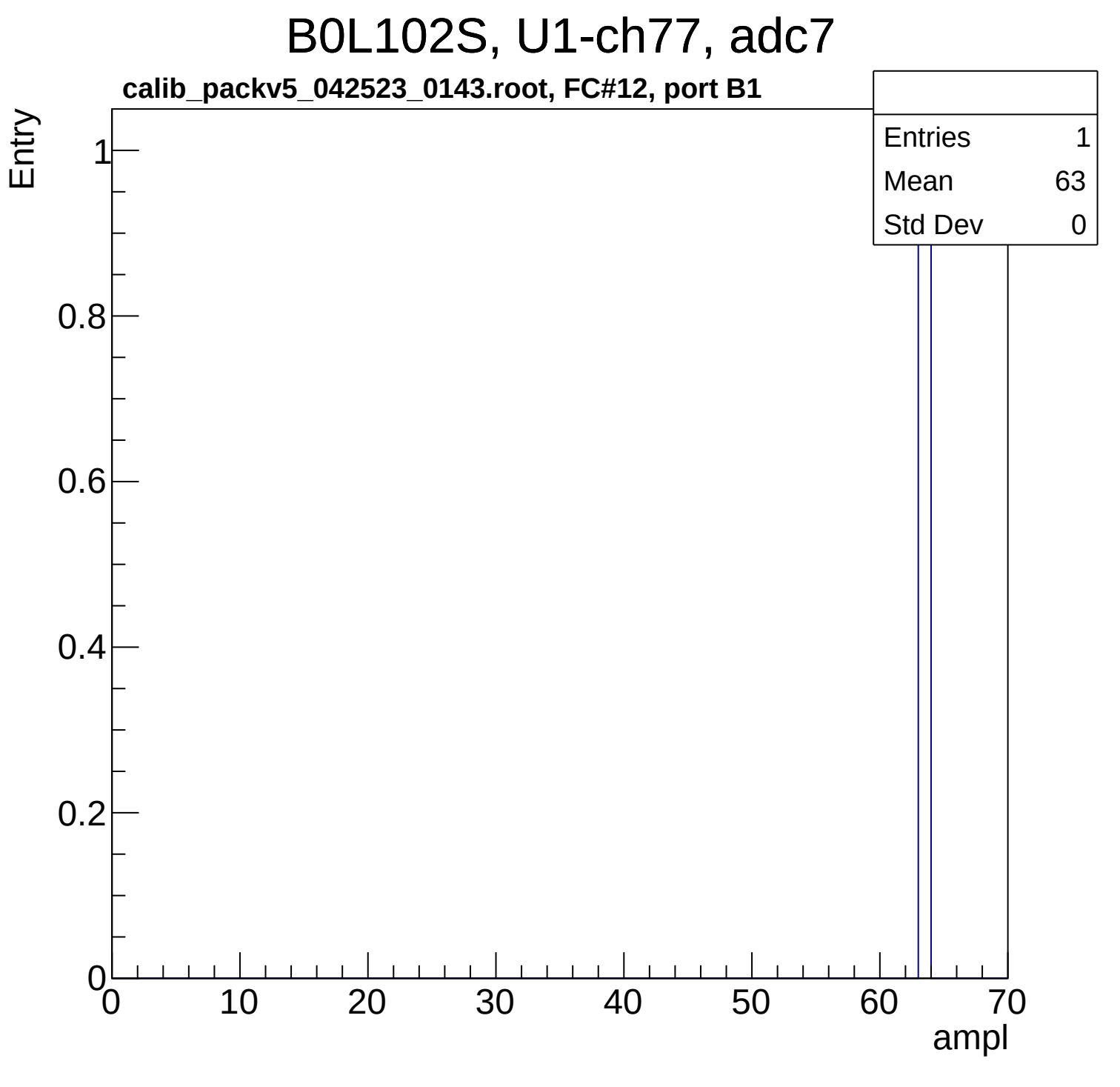
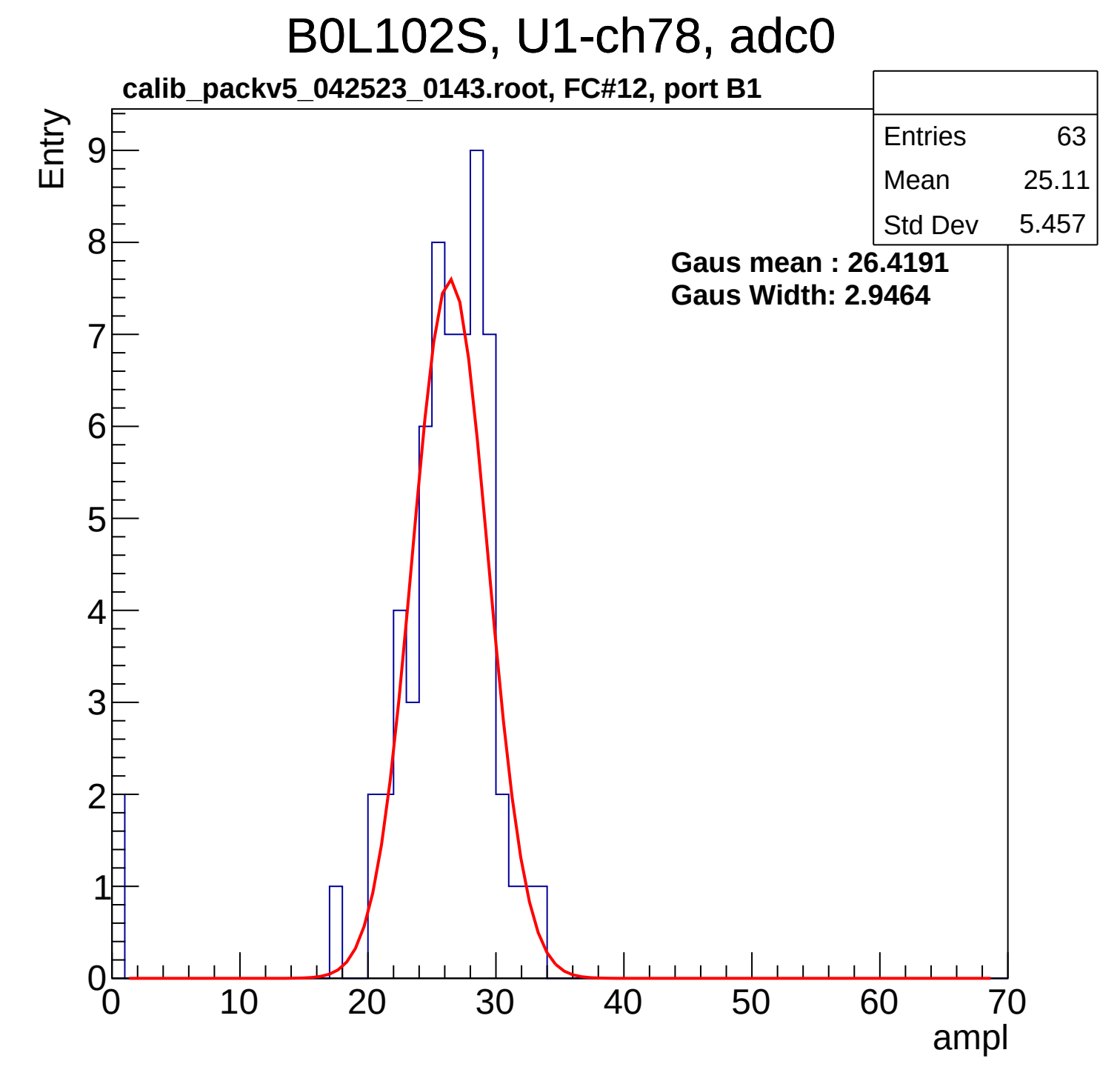
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch77, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch78, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 25.11
Std Dev 5.457
Gaus mean : 26.4191
Gaus Width: 2.9464
Entry
ampl
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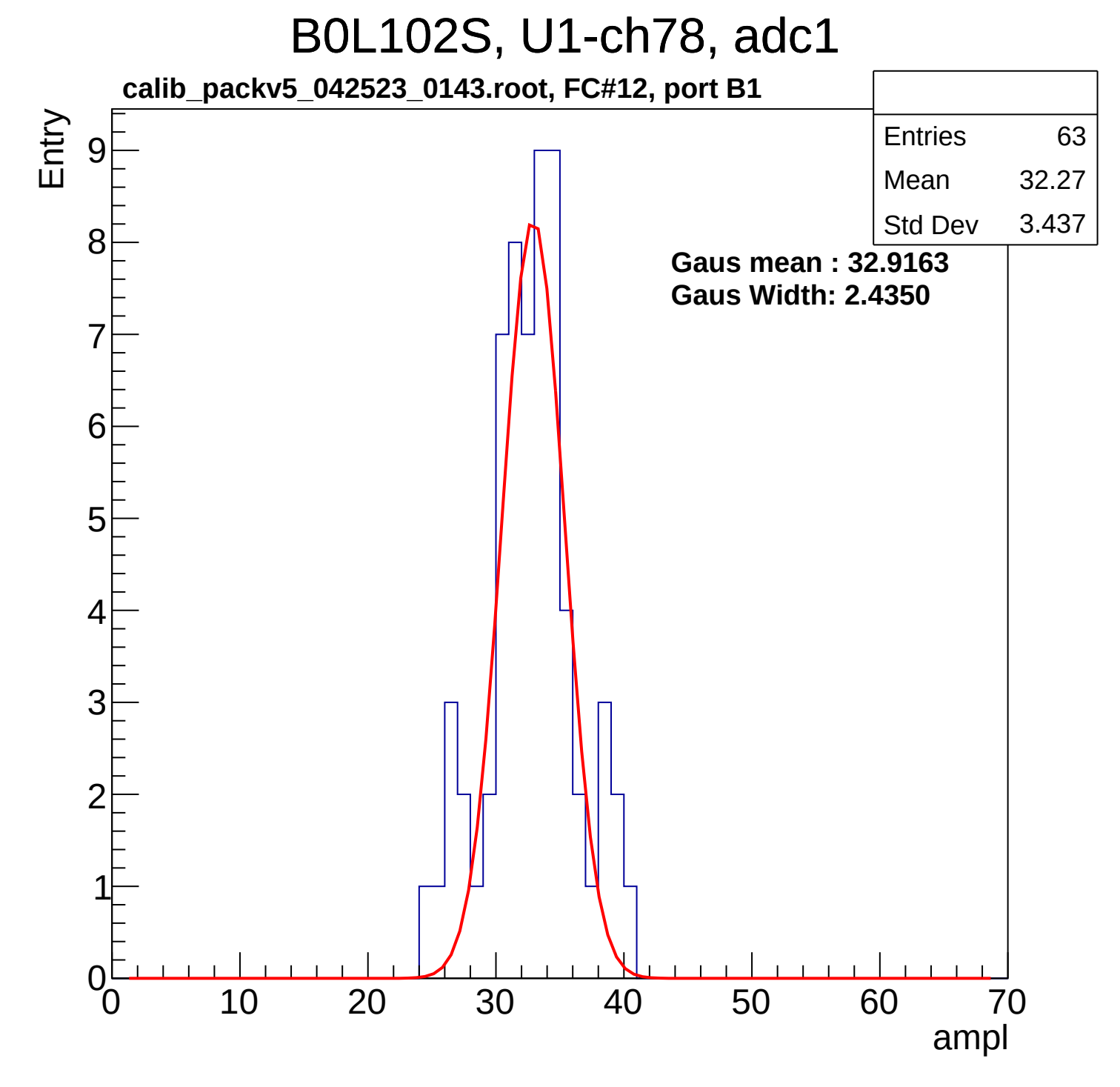
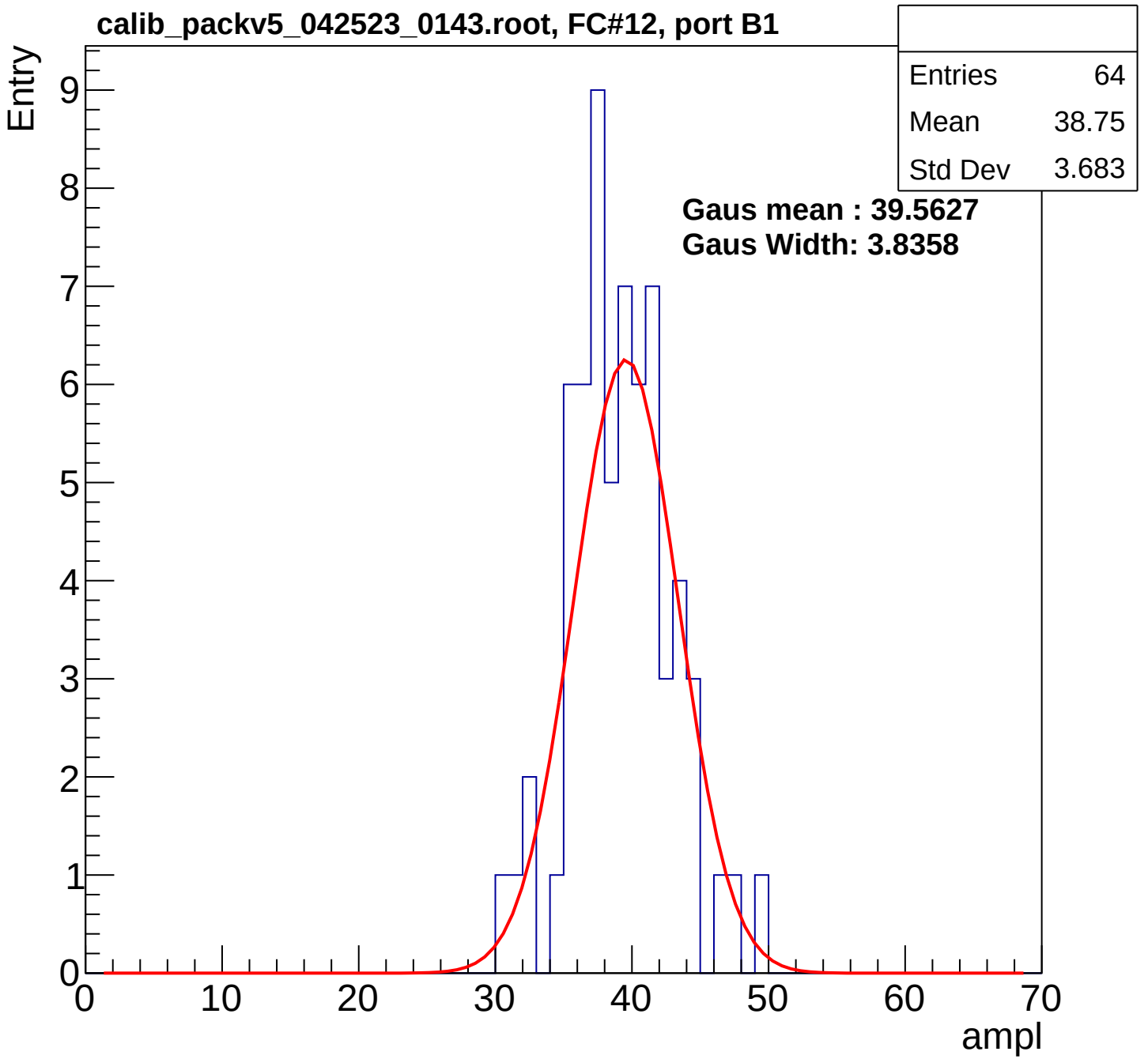
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch78, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 32.27
Std Dev 3.437
Gaus mean : 32.9163
Gaus Width: 2.4350
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch78, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 38.75
Std Dev 3.683
Gaus mean : 39.5627
Gaus Width: 3.8358
Entry
ampl
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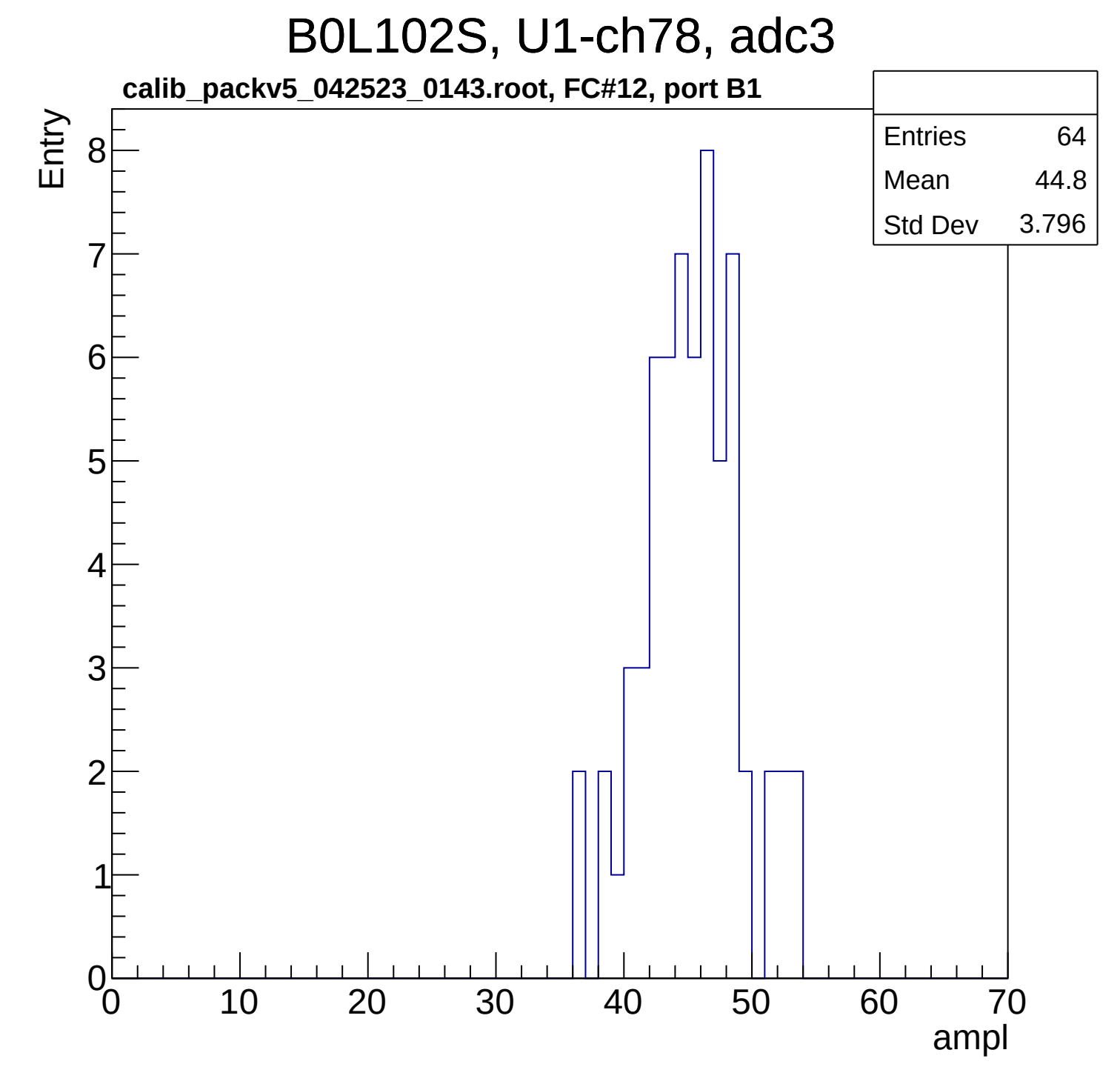
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch78, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 44.8
Std Dev 3.796
Entry
ampl
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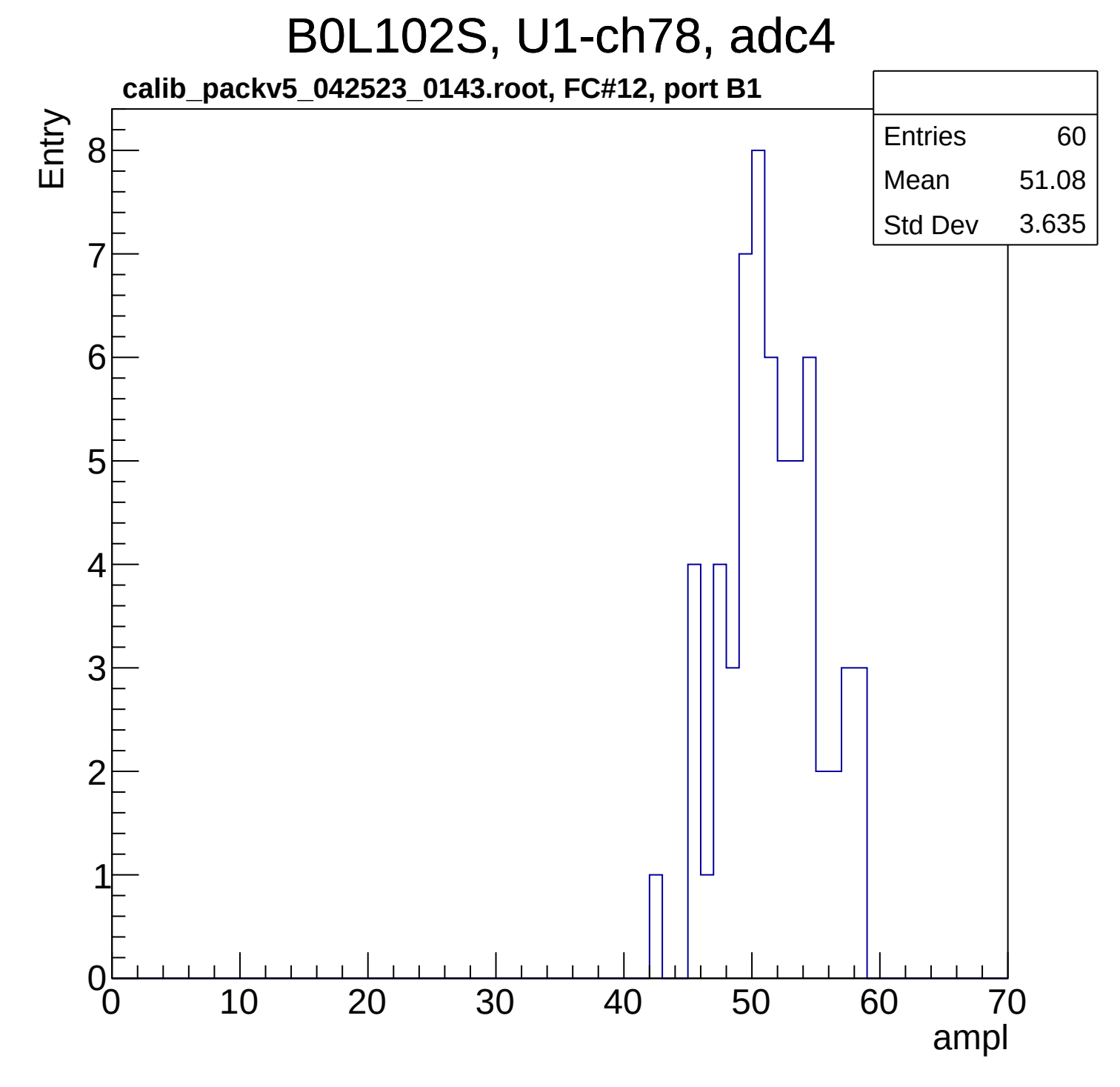
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch78, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 51.08
Std Dev 3.635
Entry
ampl
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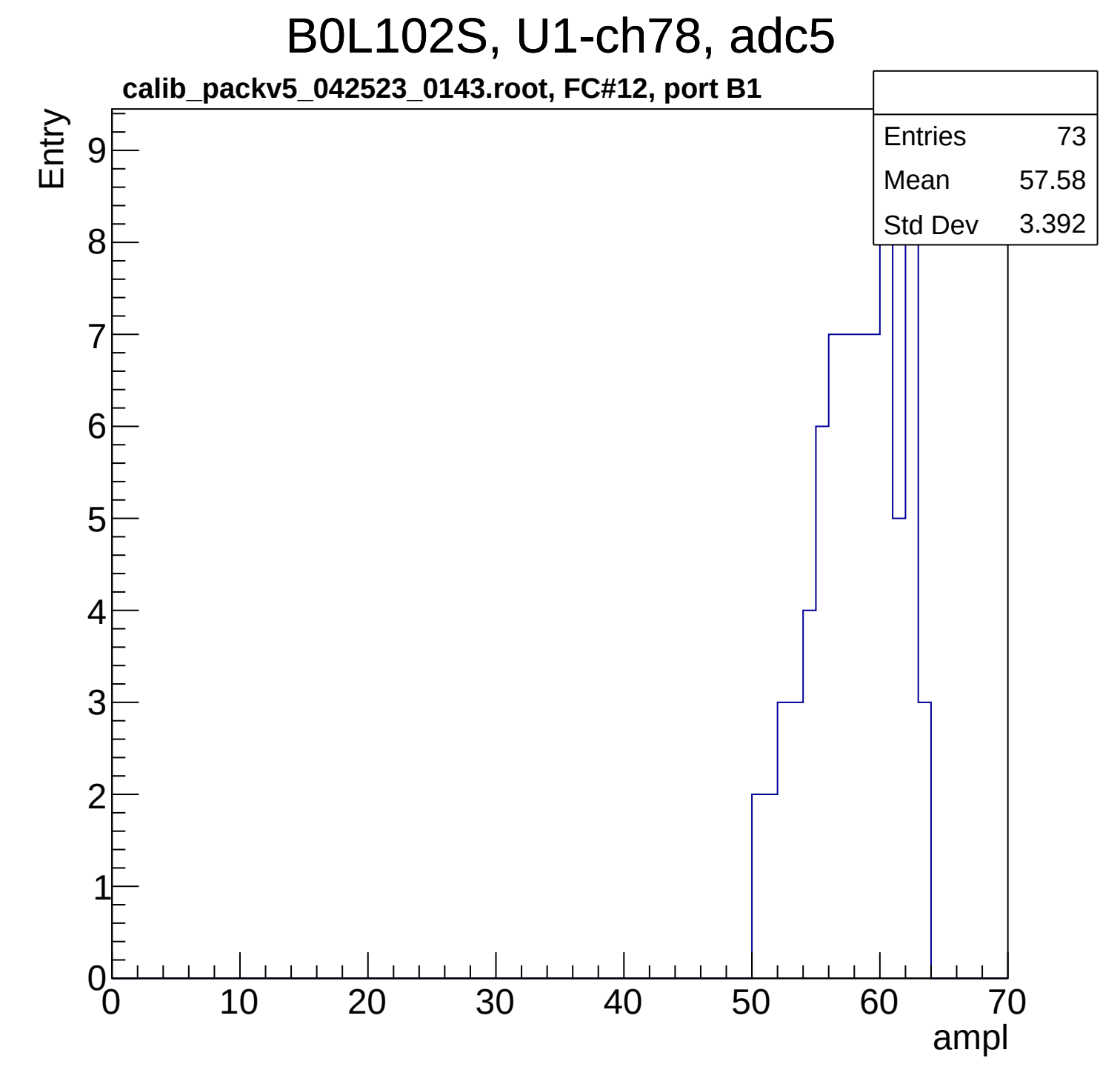
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch78, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 57.58
Std Dev 3.392
Entry
ampl
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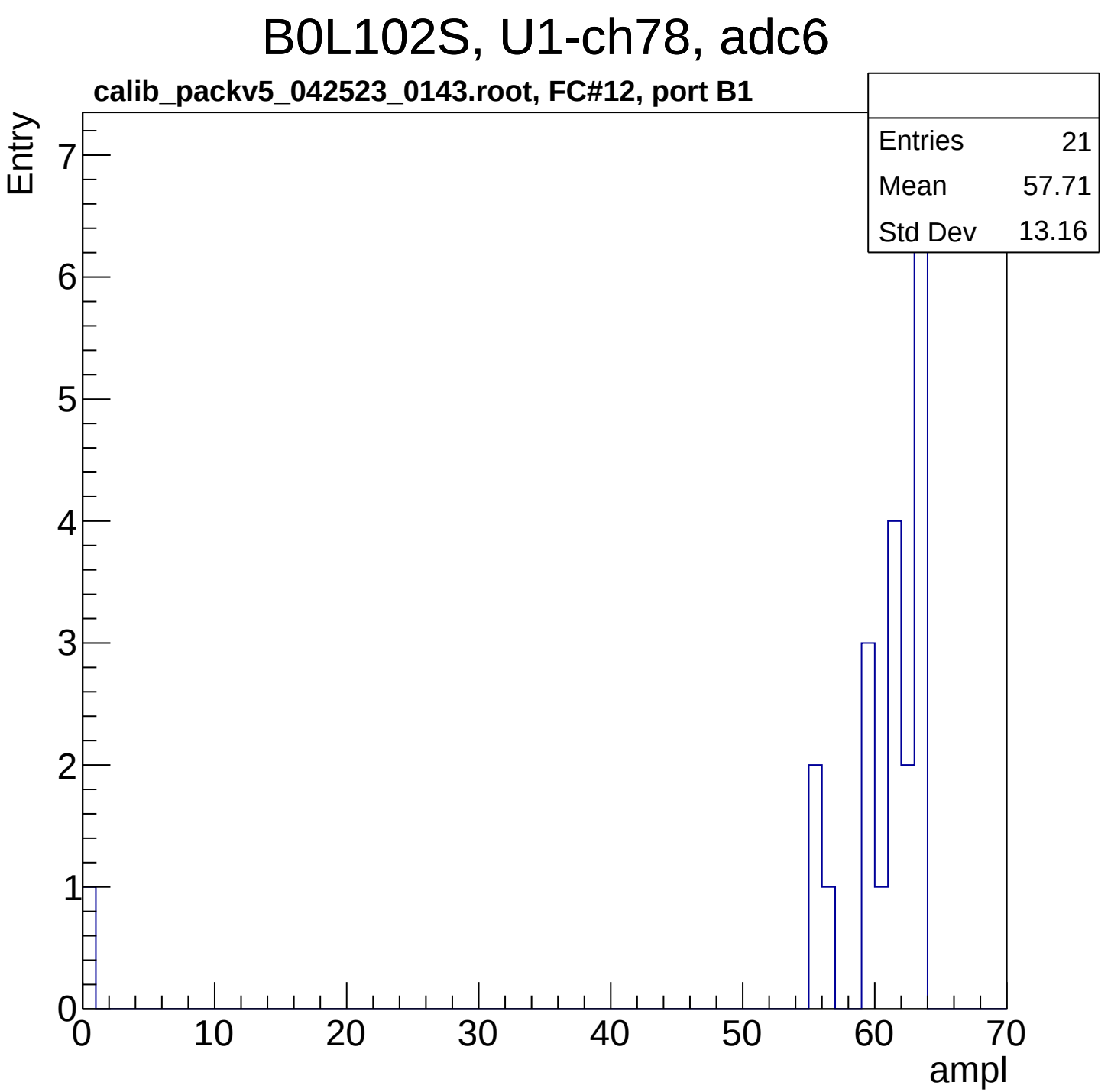
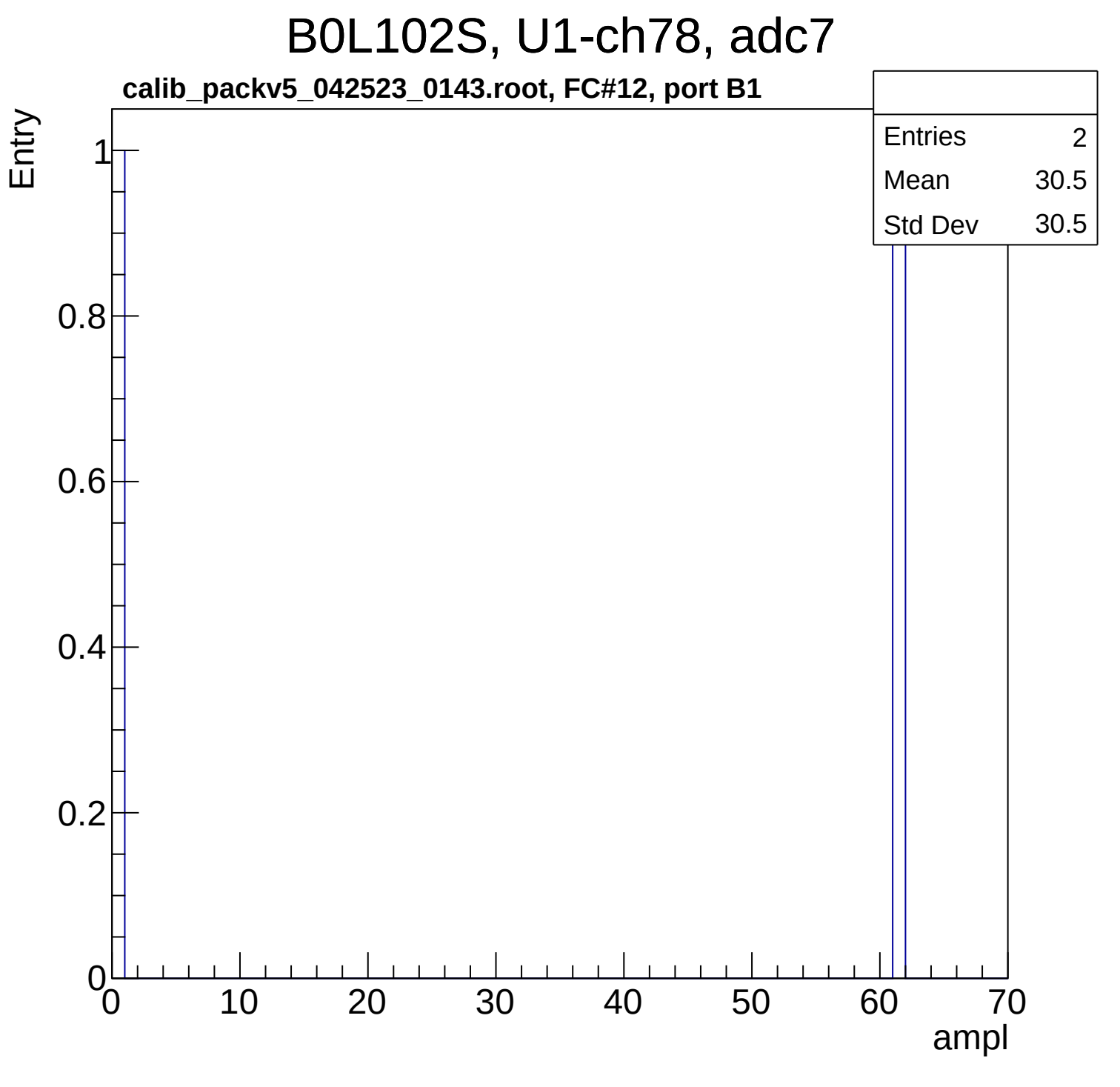
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch78, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 21
Mean 57.71
Std Dev 13.16
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch78, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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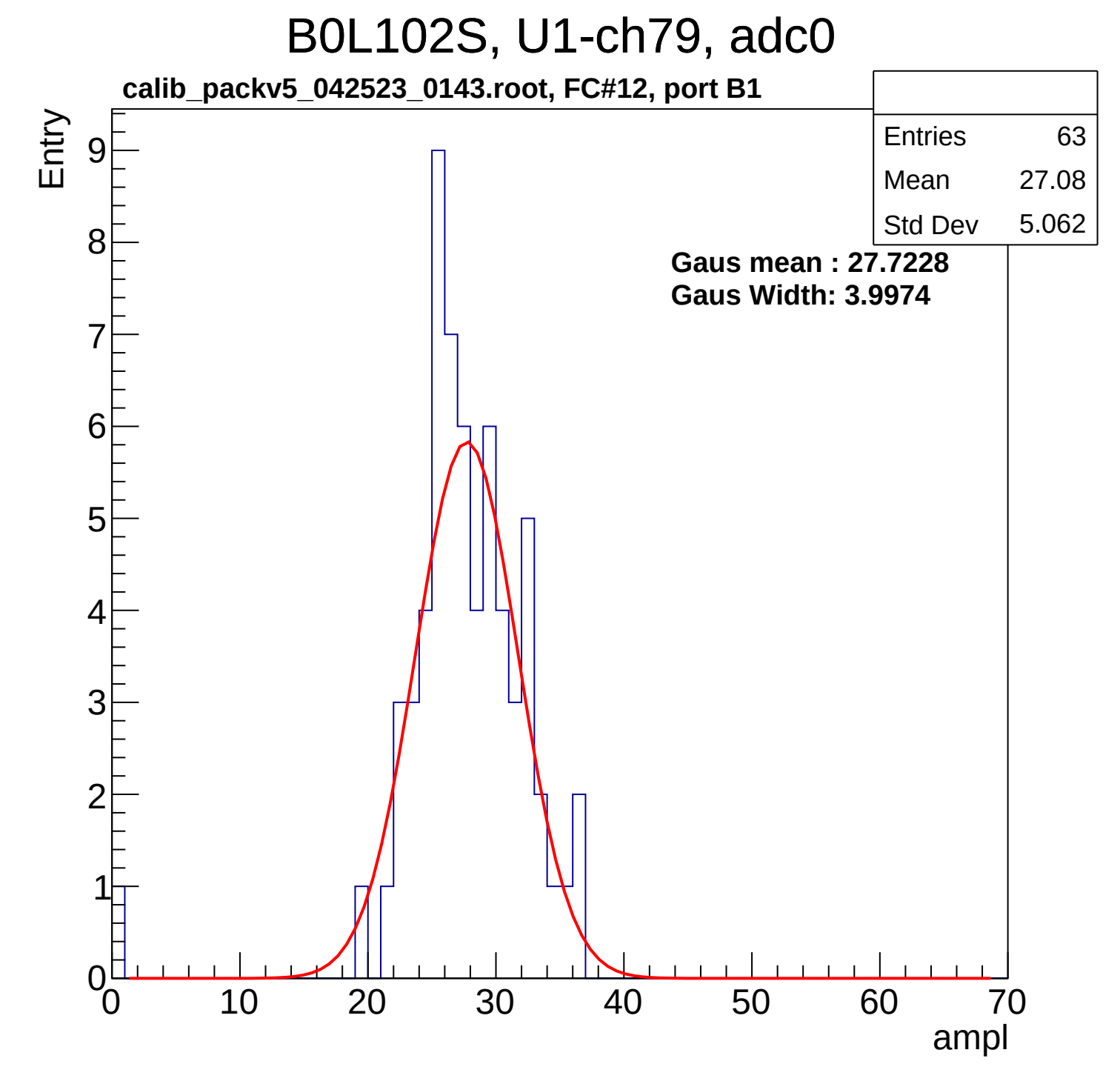
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch79, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 27.08
Std Dev 5.062
Gaus mean : 27.7228
Gaus Width: 3.9974
Entry
ampl
0
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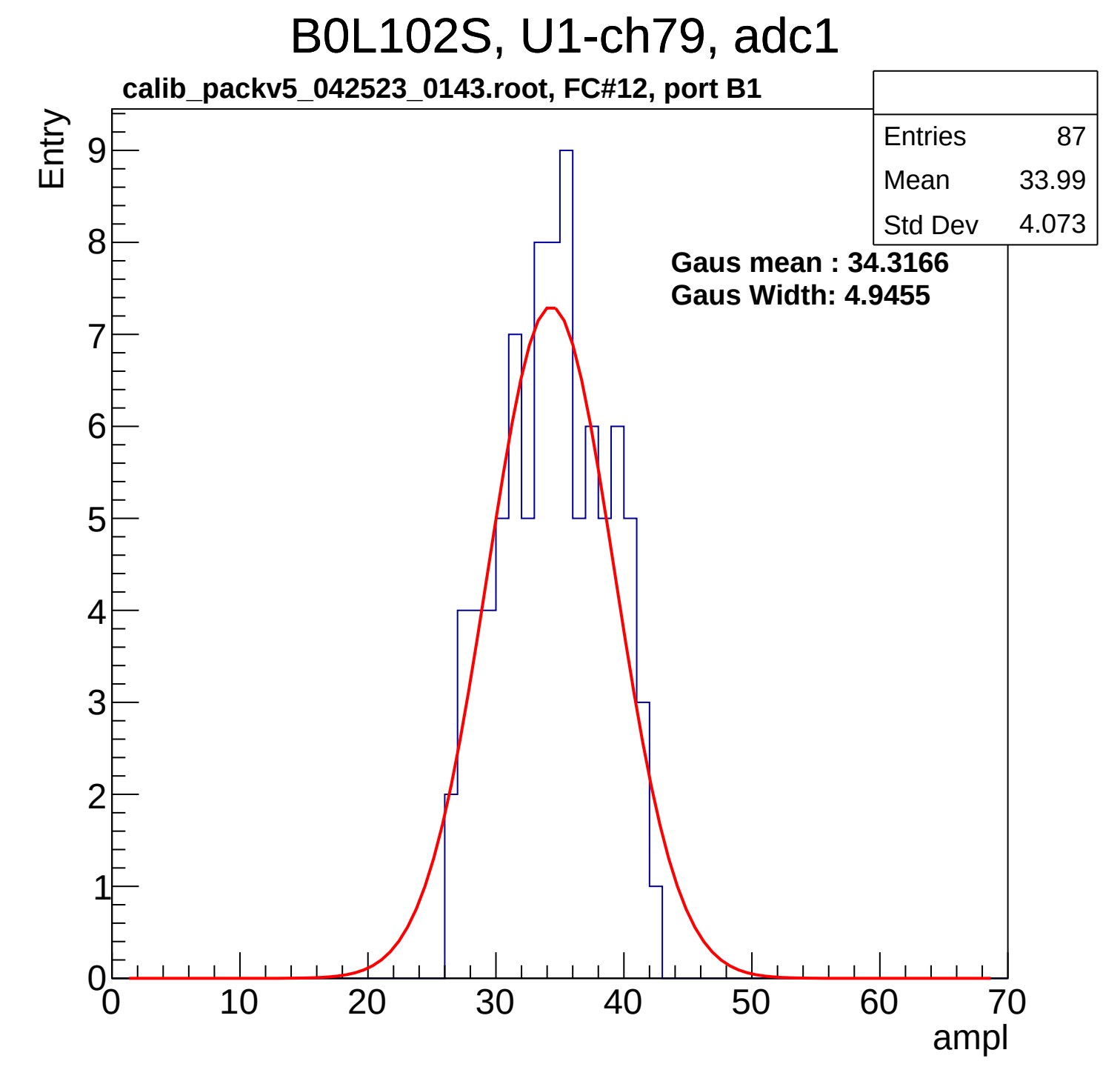
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch79, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 33.99
Std Dev 4.073
Gaus mean : 34.3166
Gaus Width: 4.9455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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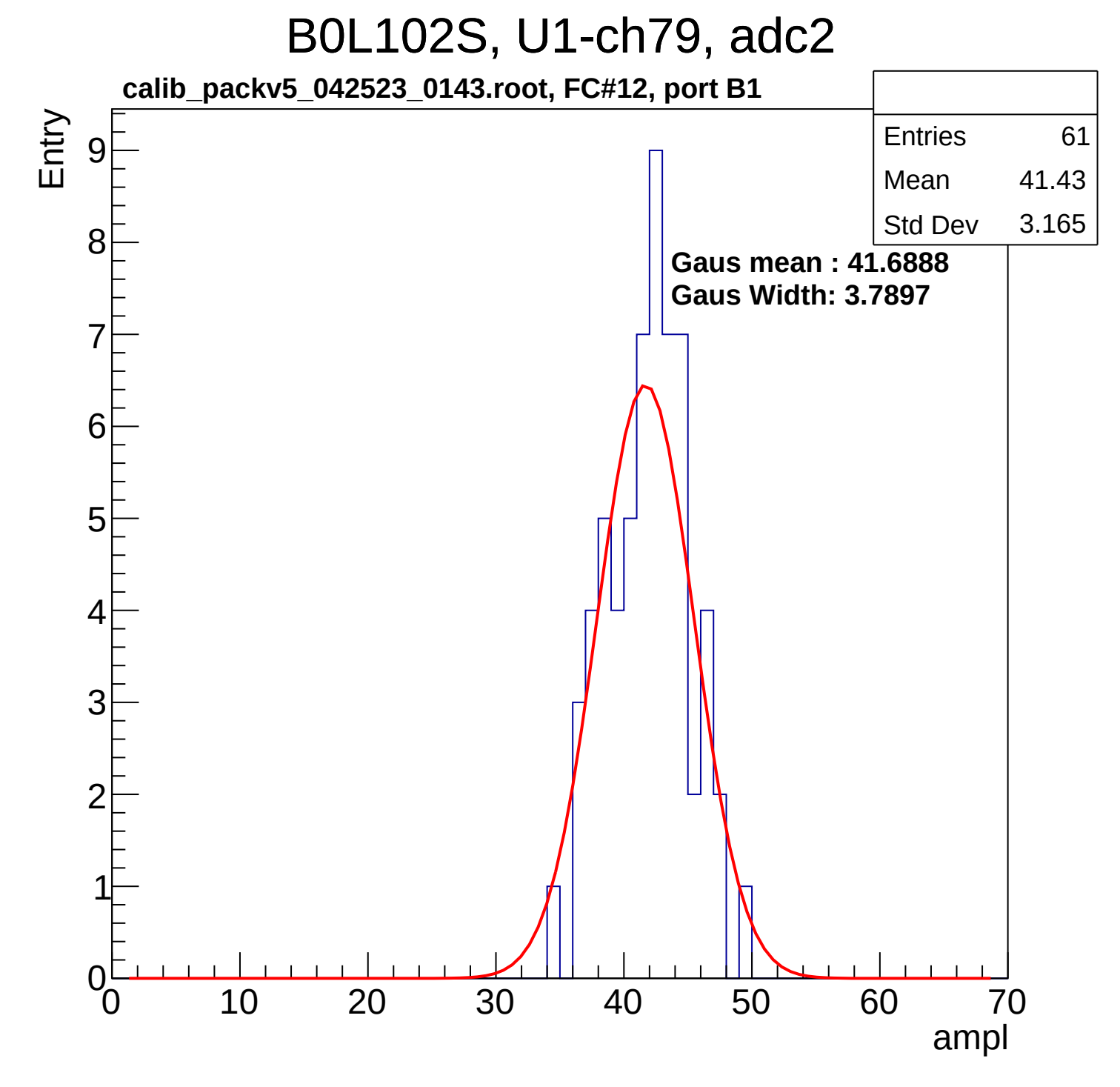
B0L102S, U1-ch79, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 41.43
Std Dev 3.165
Gaus mean : 41.6888
Gaus Width: 3.7897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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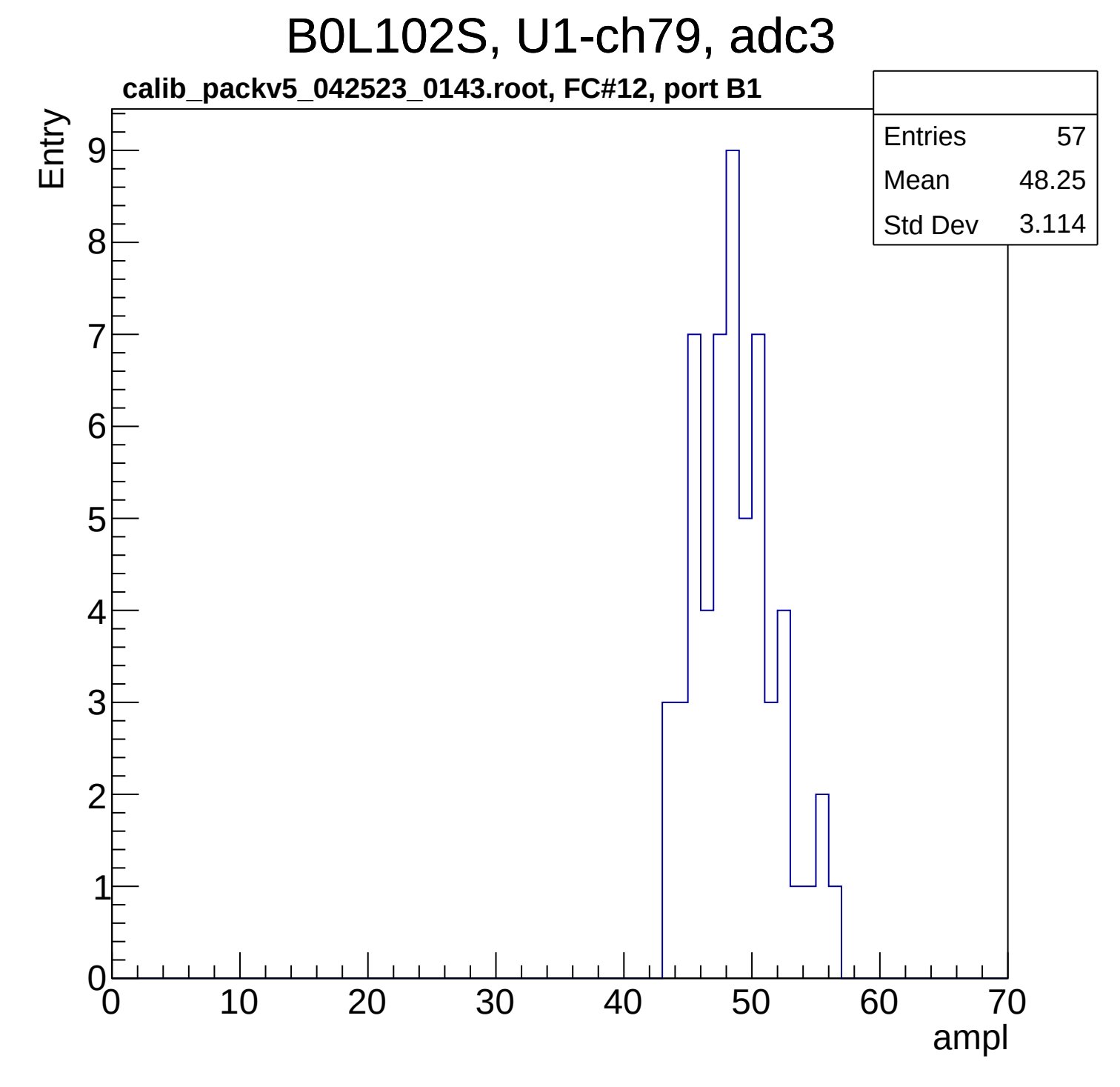
B0L102S, U1-ch79, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 48.25
Std Dev 3.114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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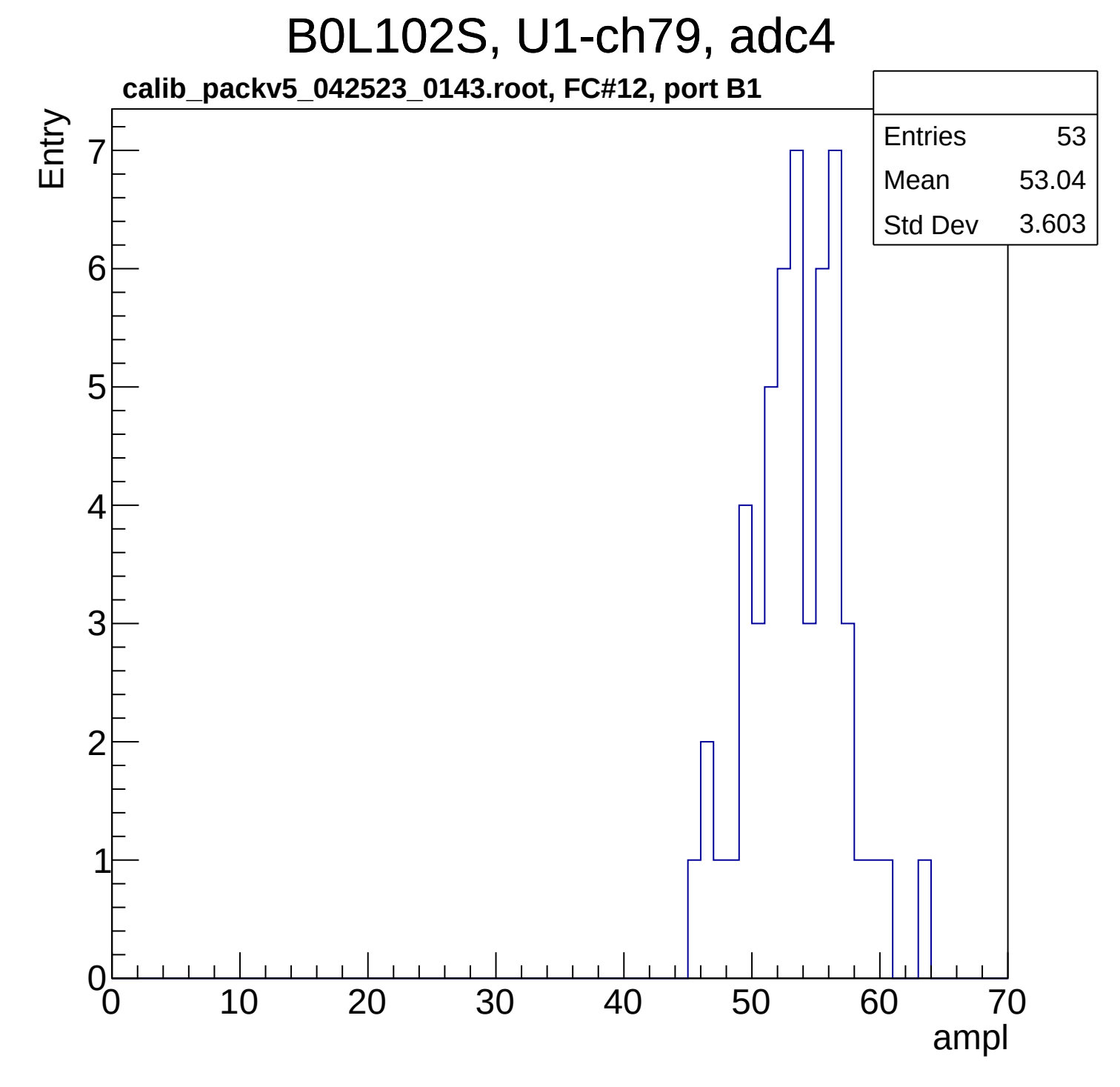
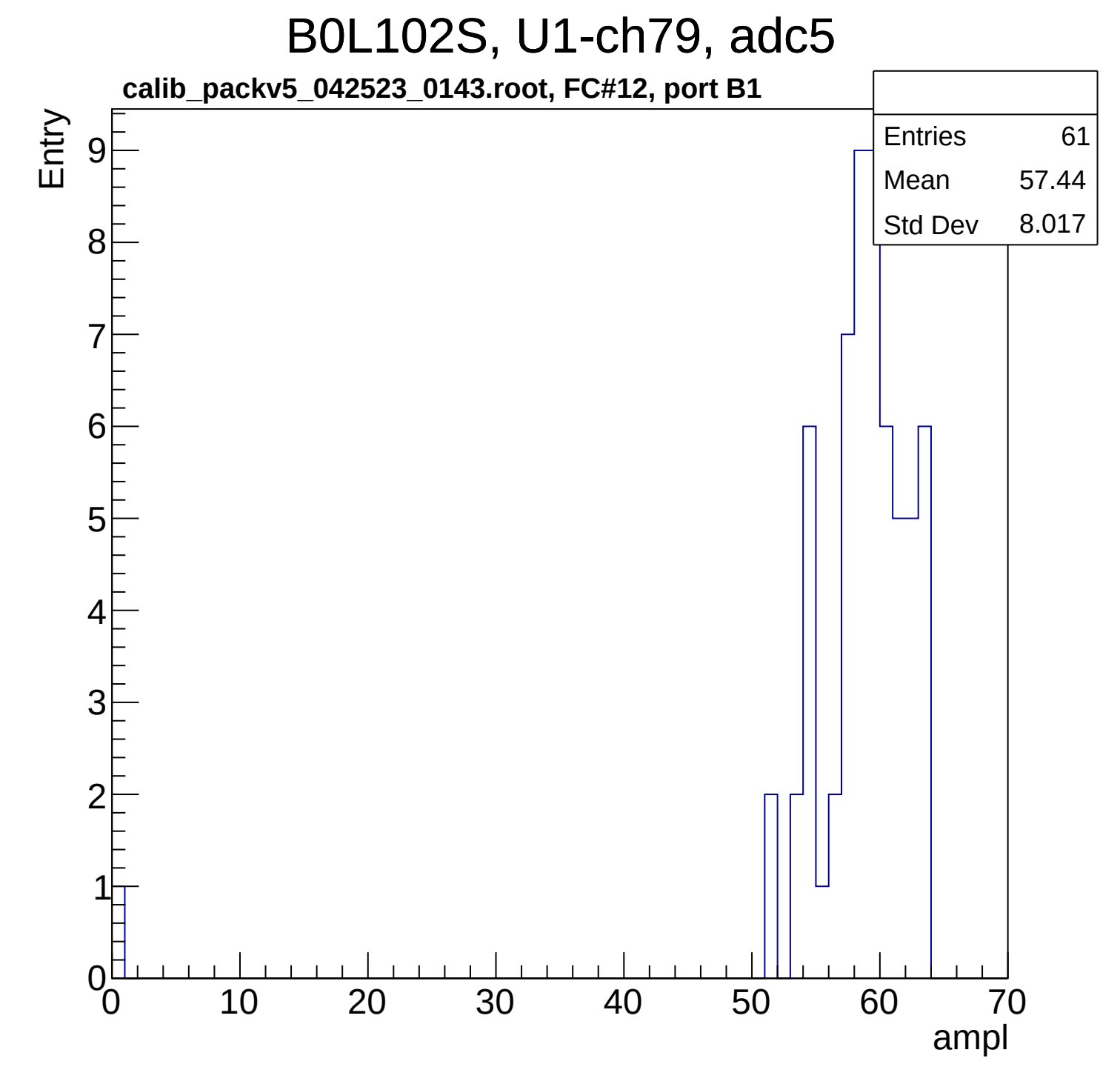

B0L102S, U1-ch79, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 53.04
Std Dev 3.603
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch79, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 57.44
Std Dev 8.017
Entry
ampl
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3
4
5
6
7
8
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0
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60
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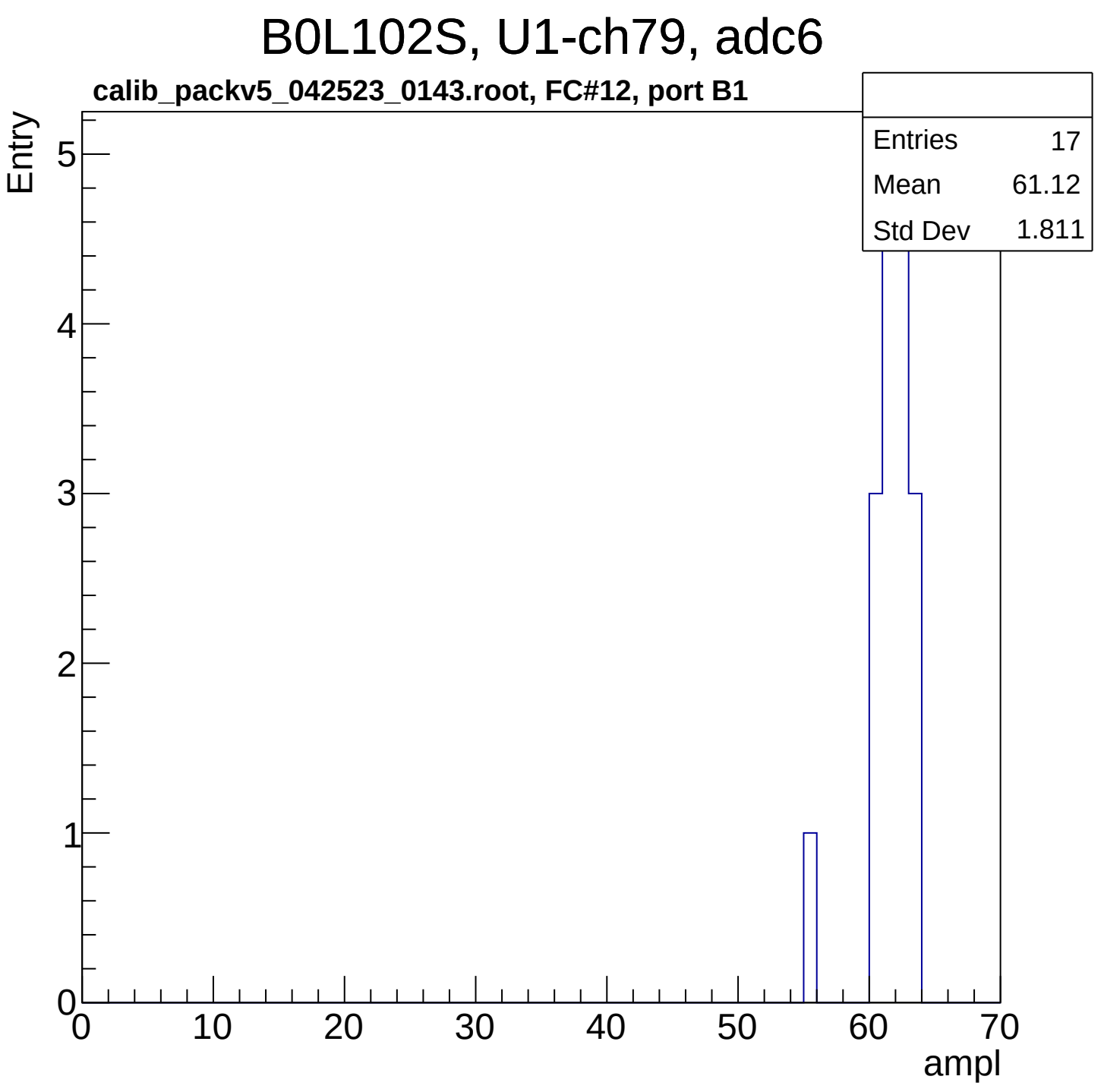
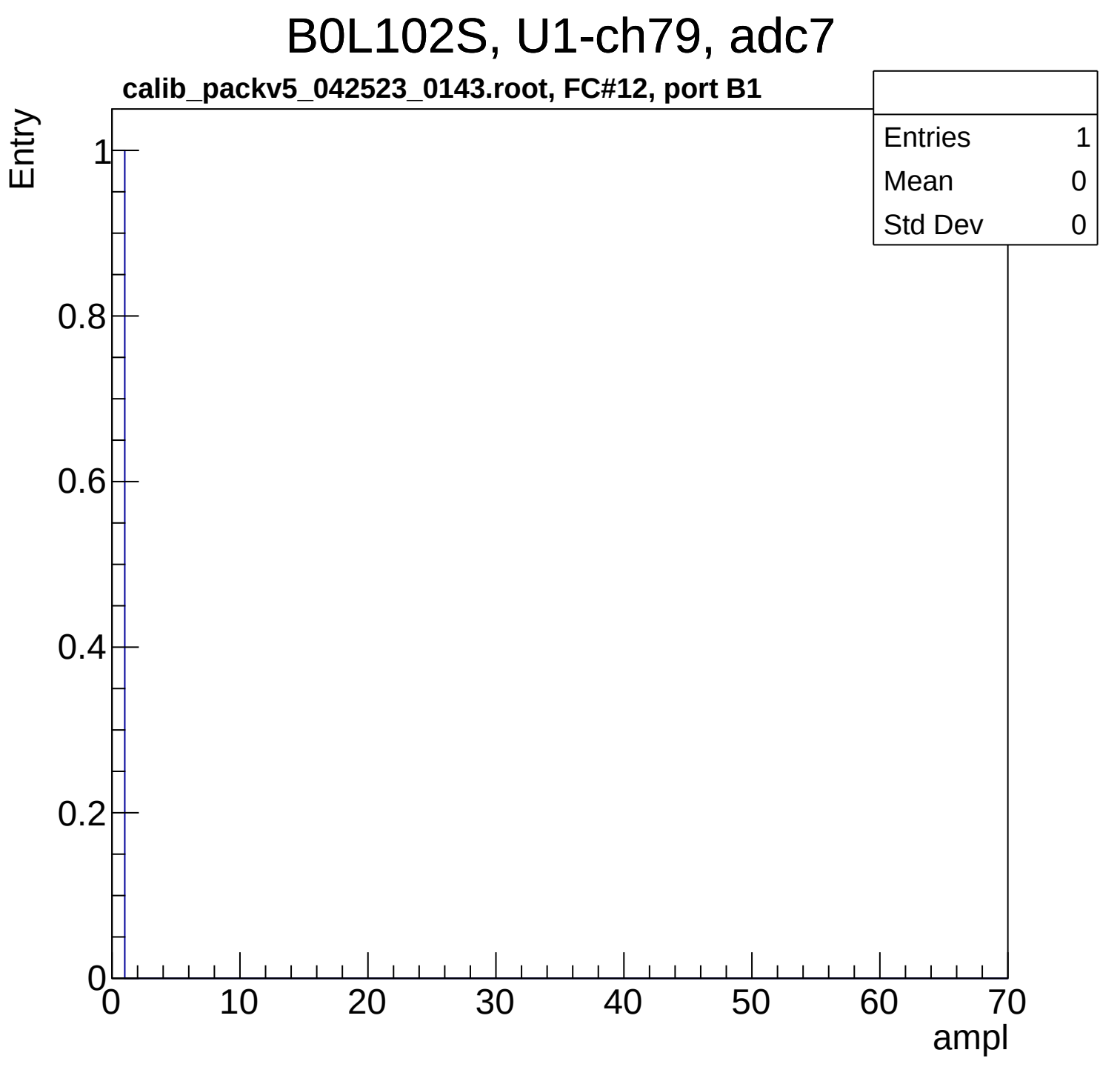

B0L102S, U1-ch79, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 61.12
Std Dev 1.811
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch79, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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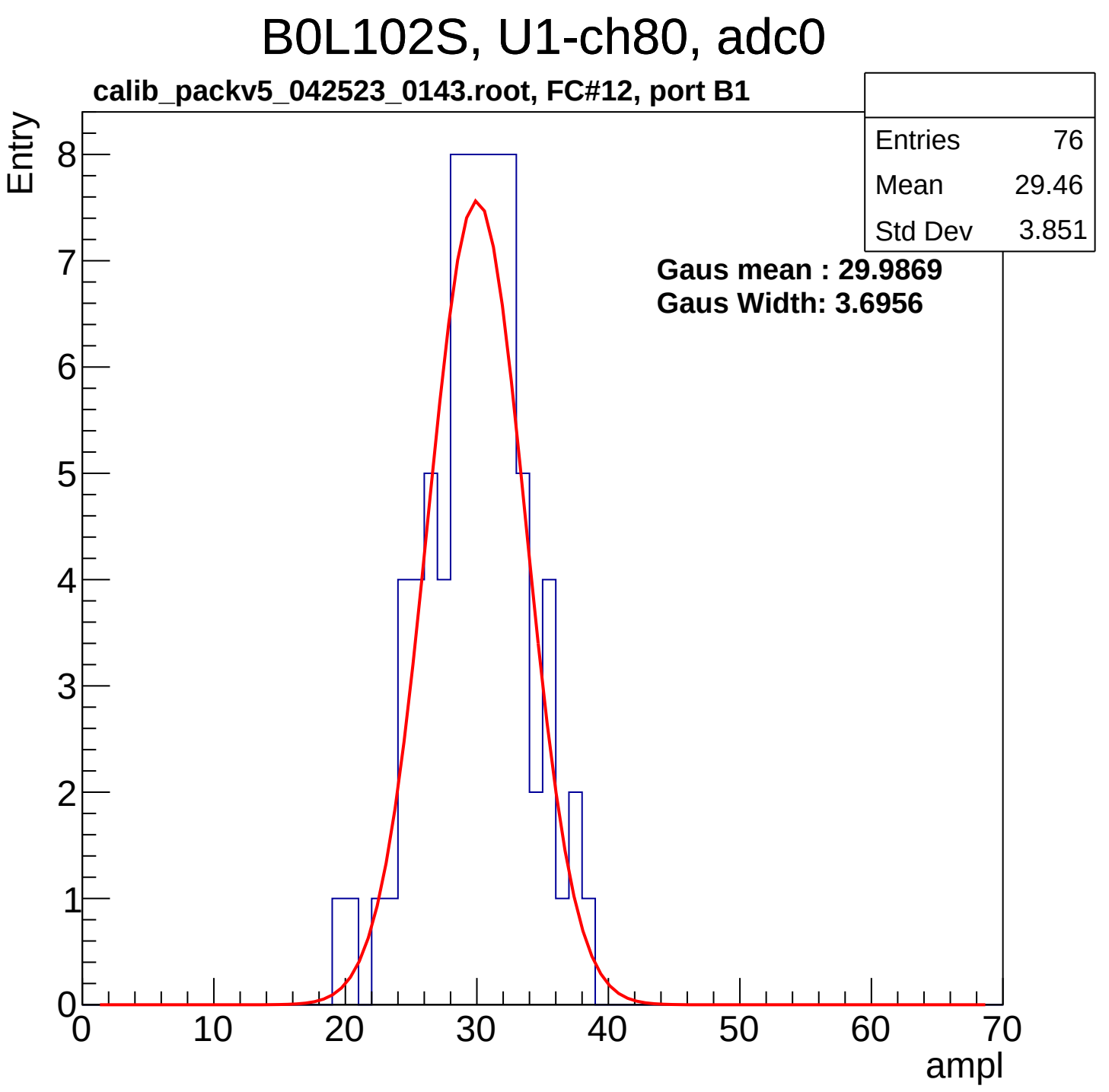
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch80, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.46
Std Dev 3.851
Gaus mean : 29.9869
Gaus Width: 3.6956
Entry
ampl
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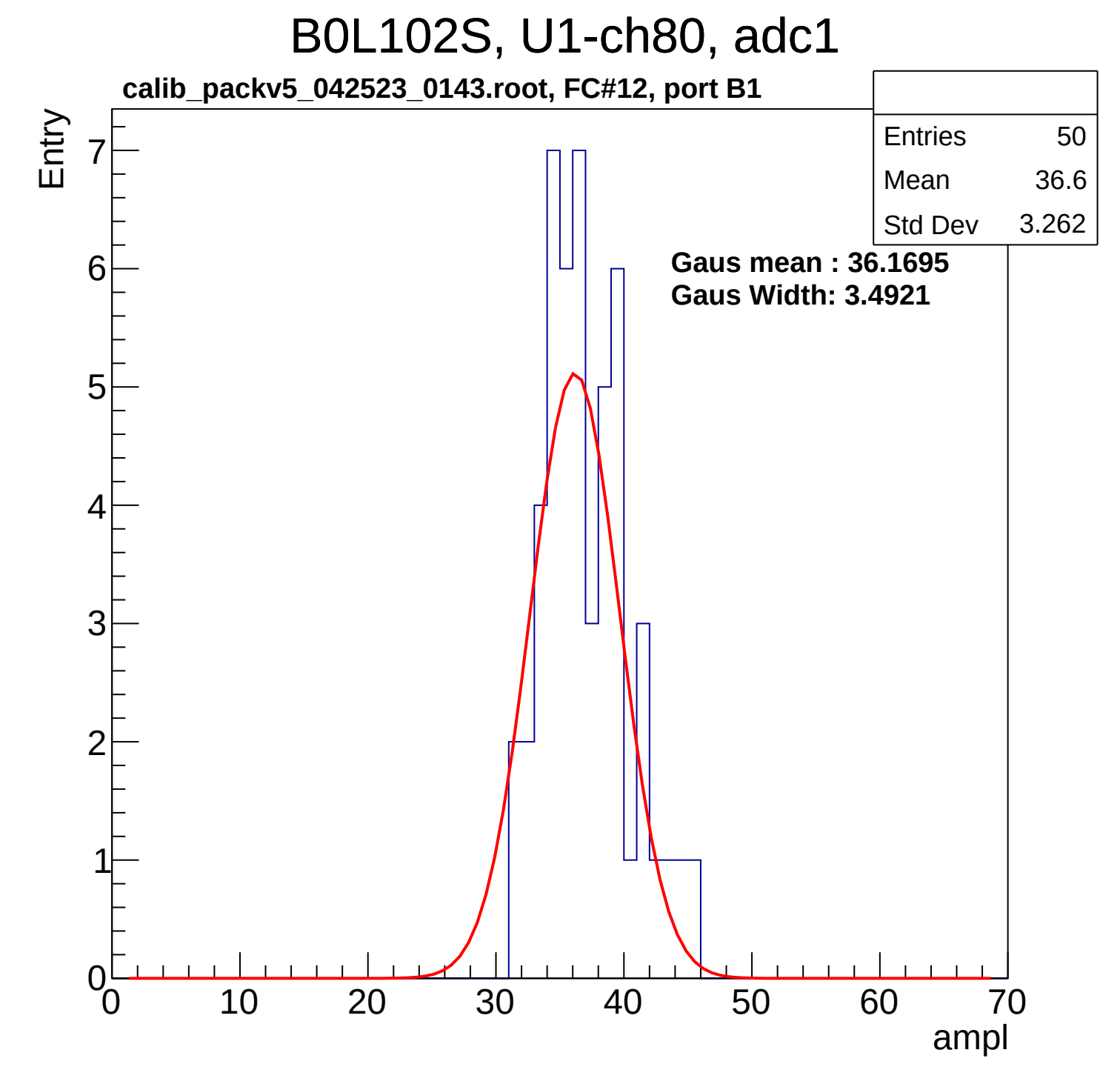
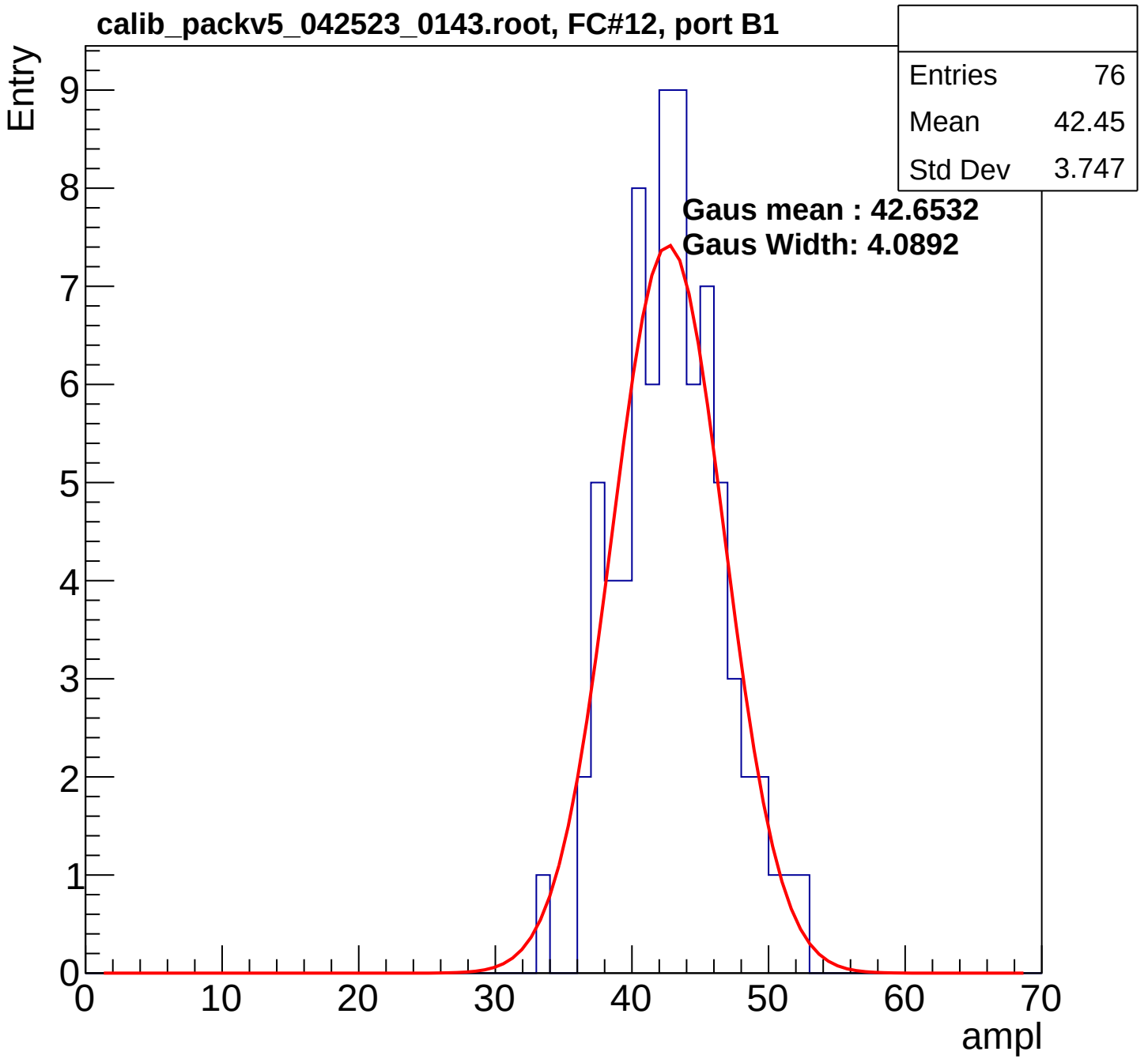
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch80, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 36.6
Std Dev 3.262
Gaus mean : 36.1695
Gaus Width: 3.4921
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch80, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 42.45
Std Dev 3.747
Gaus mean : 42.6532
Gaus Width: 4.0892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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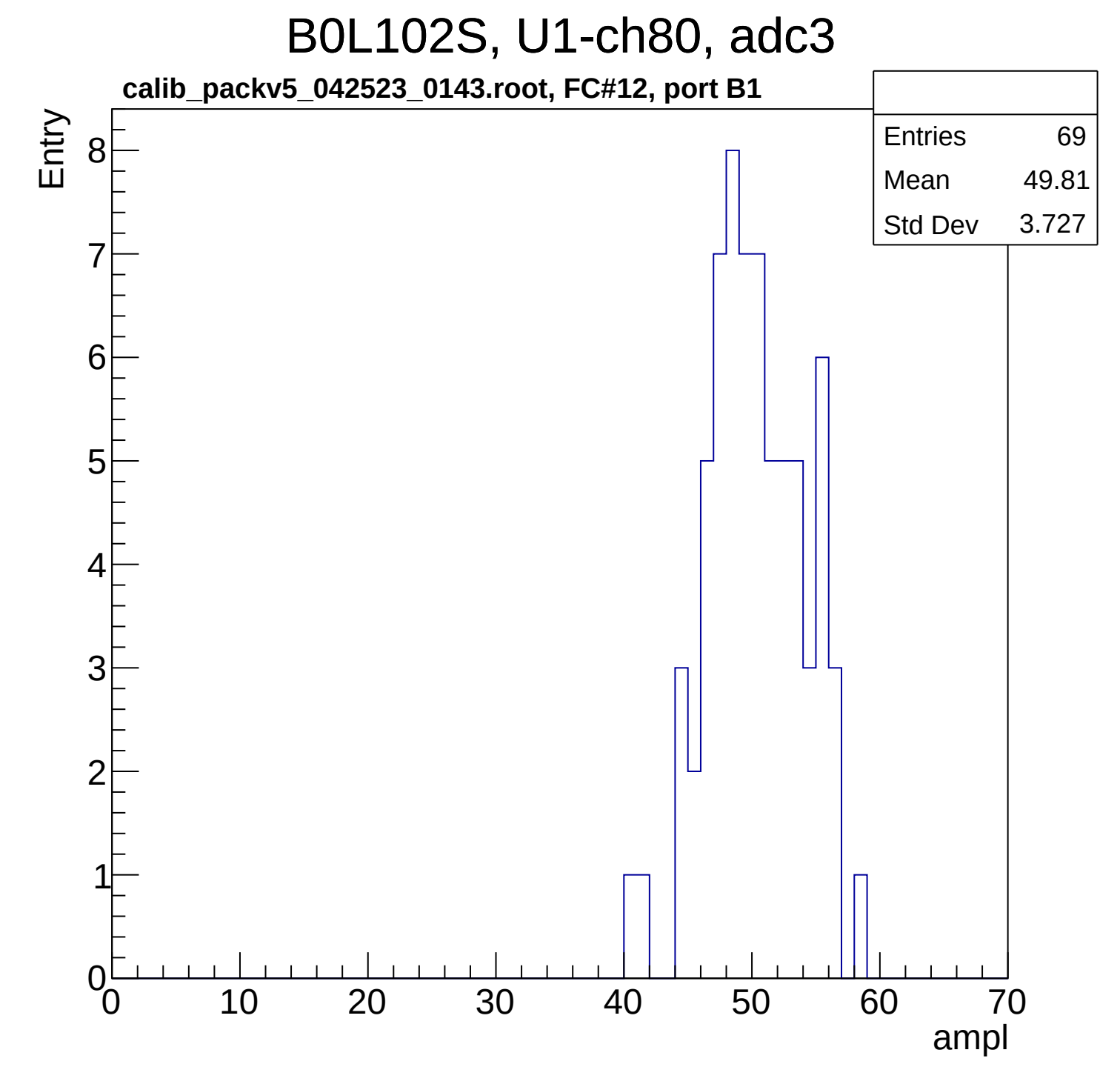
B0L102S, U1-ch80, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 49.81
Std Dev 3.727
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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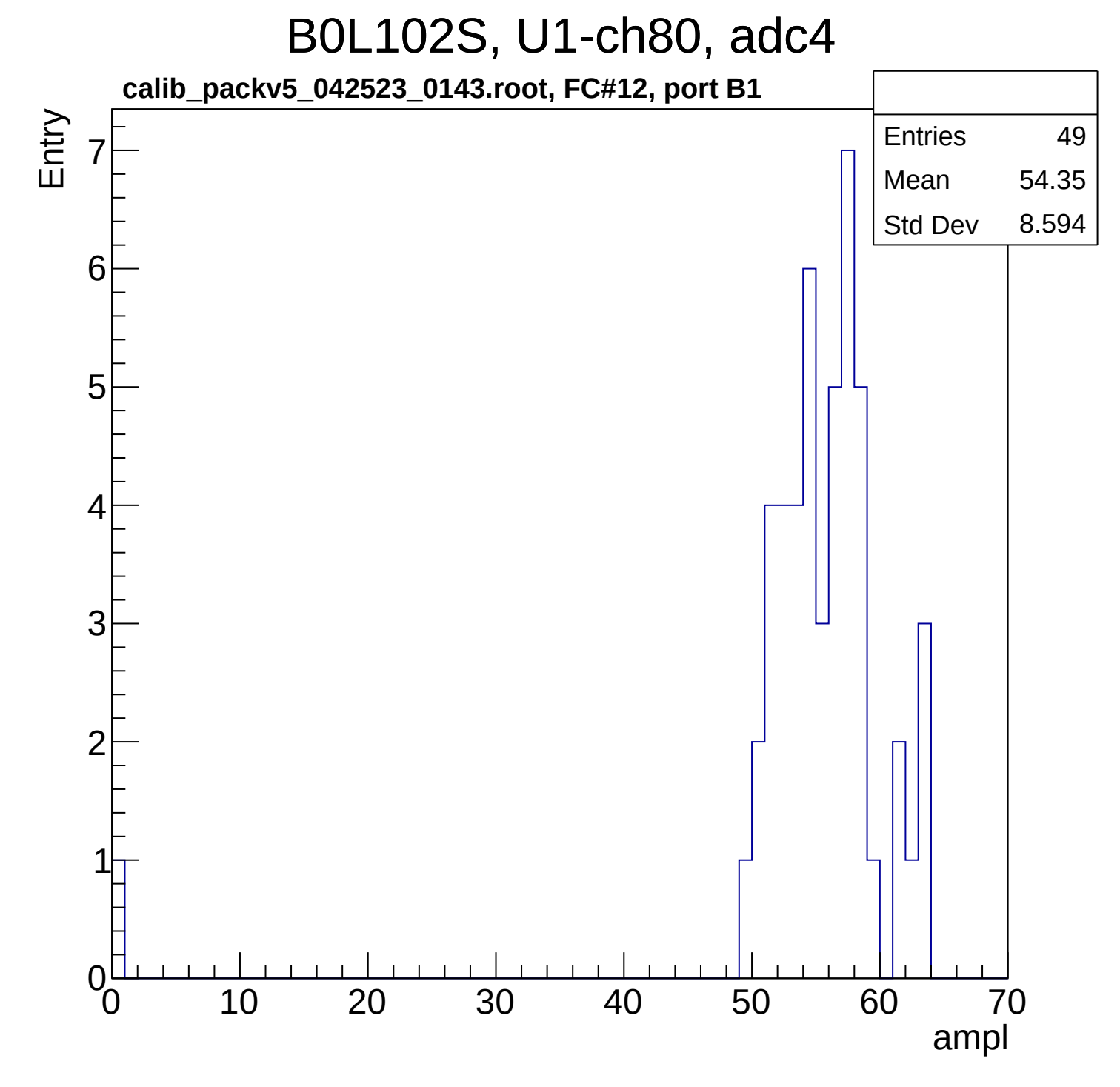
B0L102S, U1-ch80, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 54.35
Std Dev 8.594
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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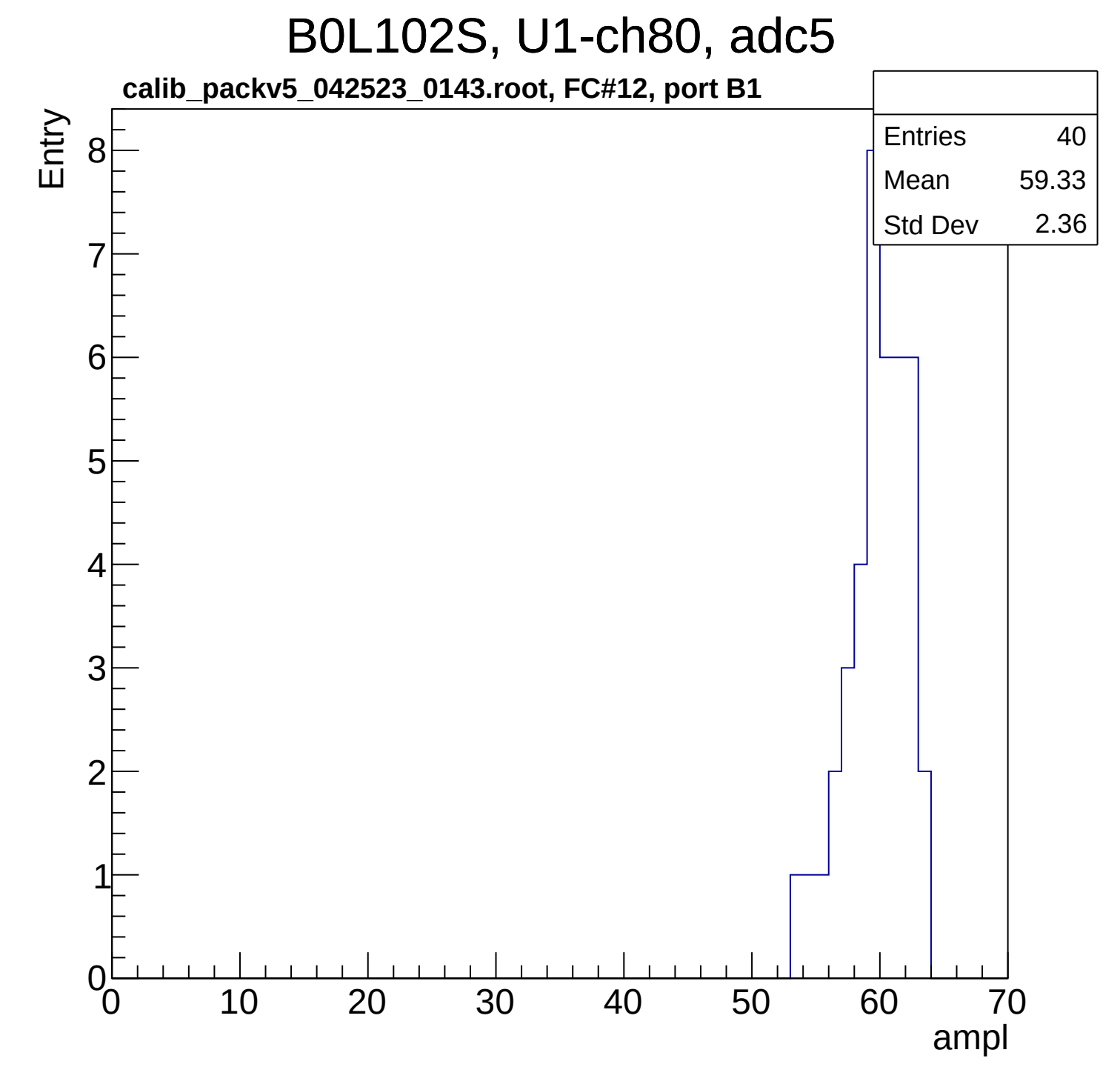
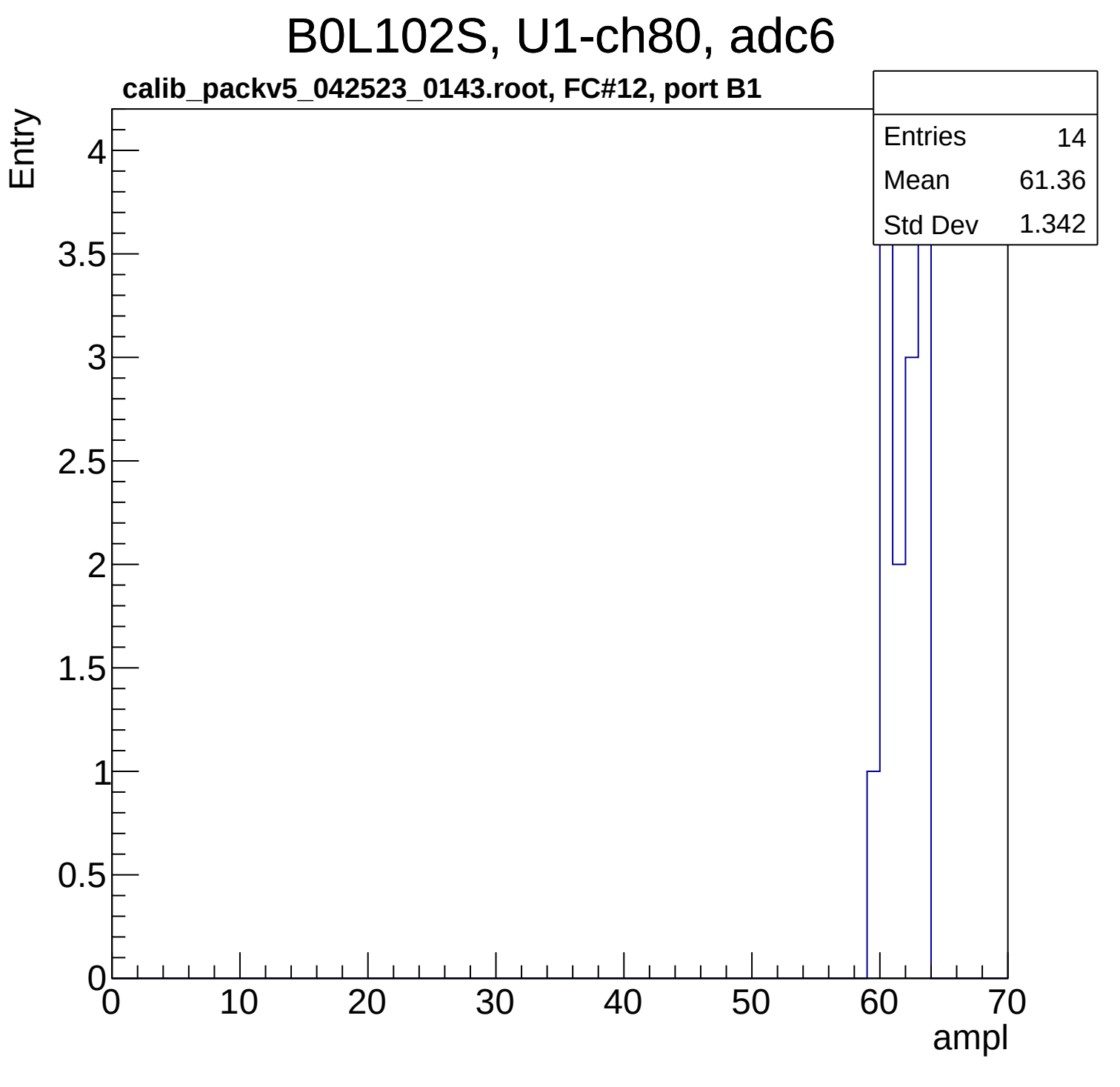

B0L102S, U1-ch80, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 59.33
Std Dev 2.36
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch80, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.36
Std Dev 1.342
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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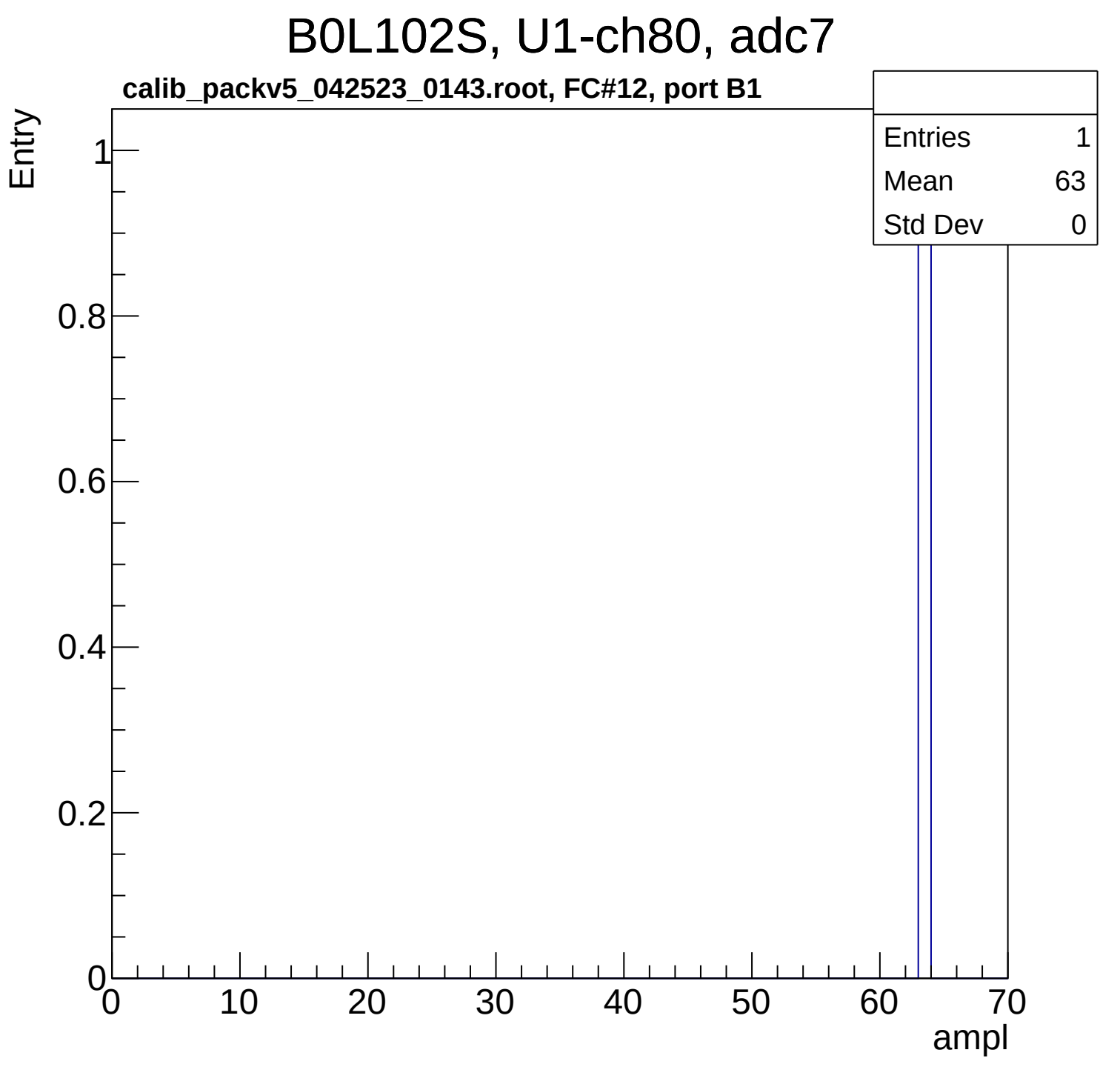
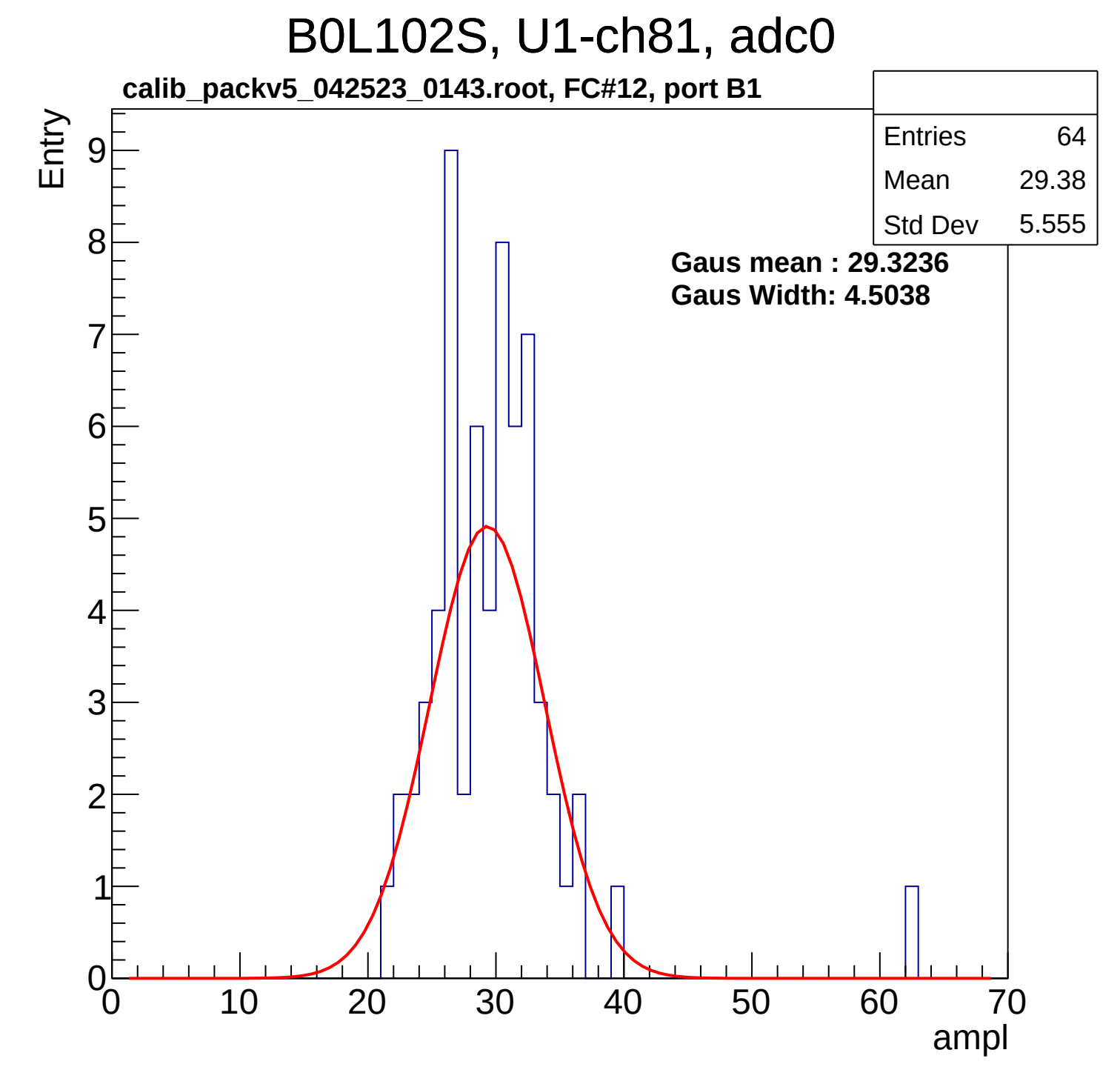
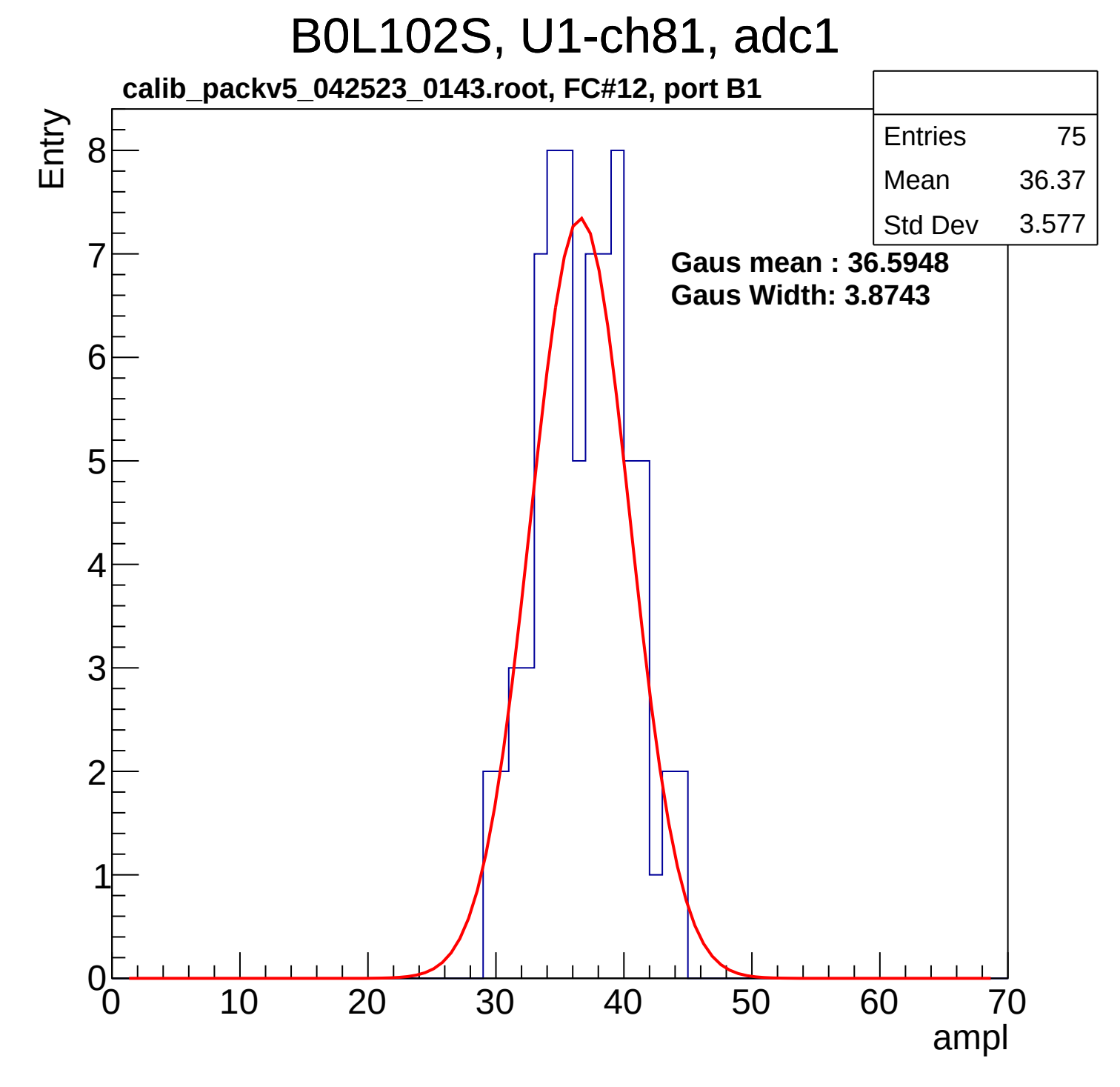
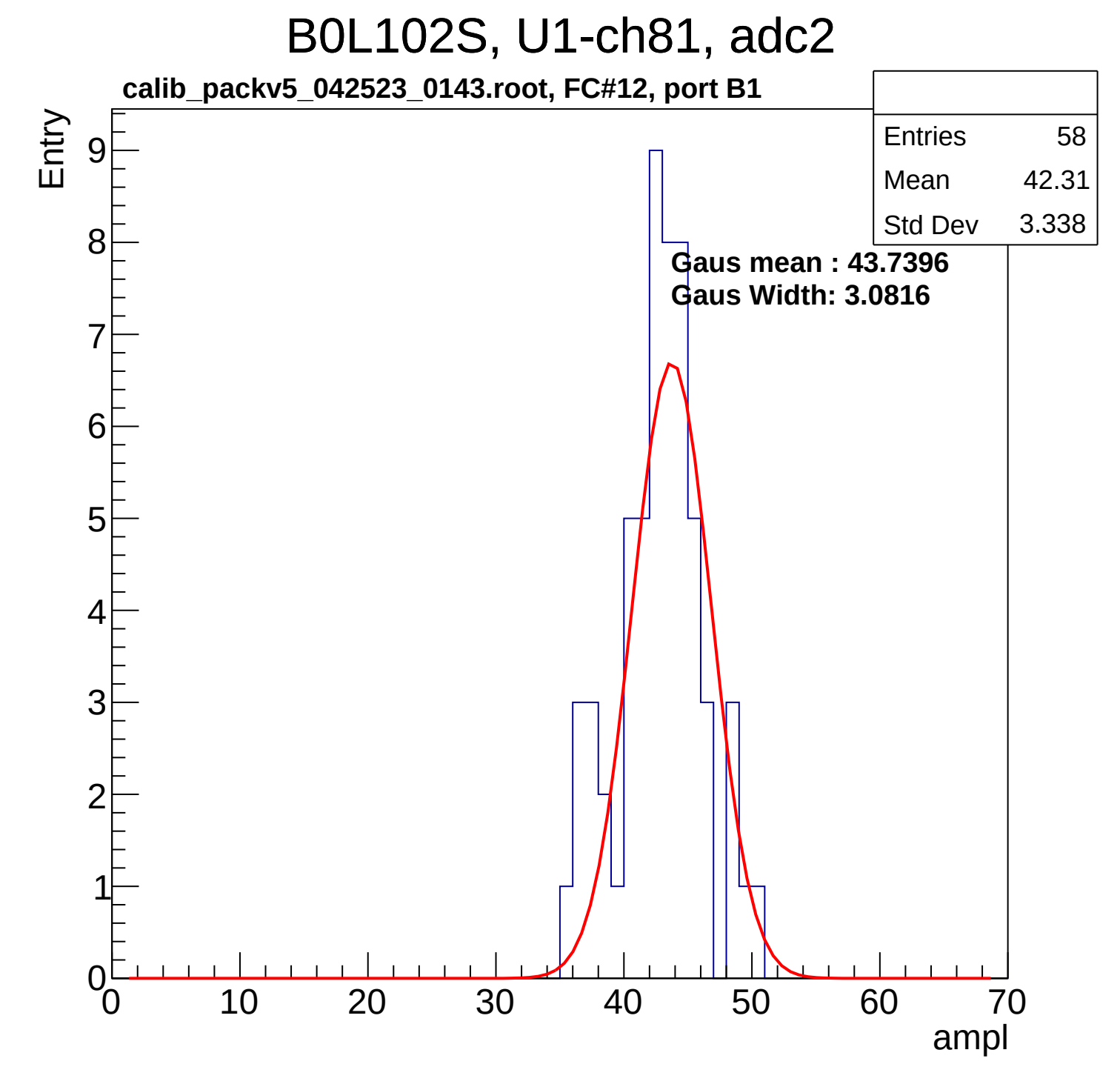
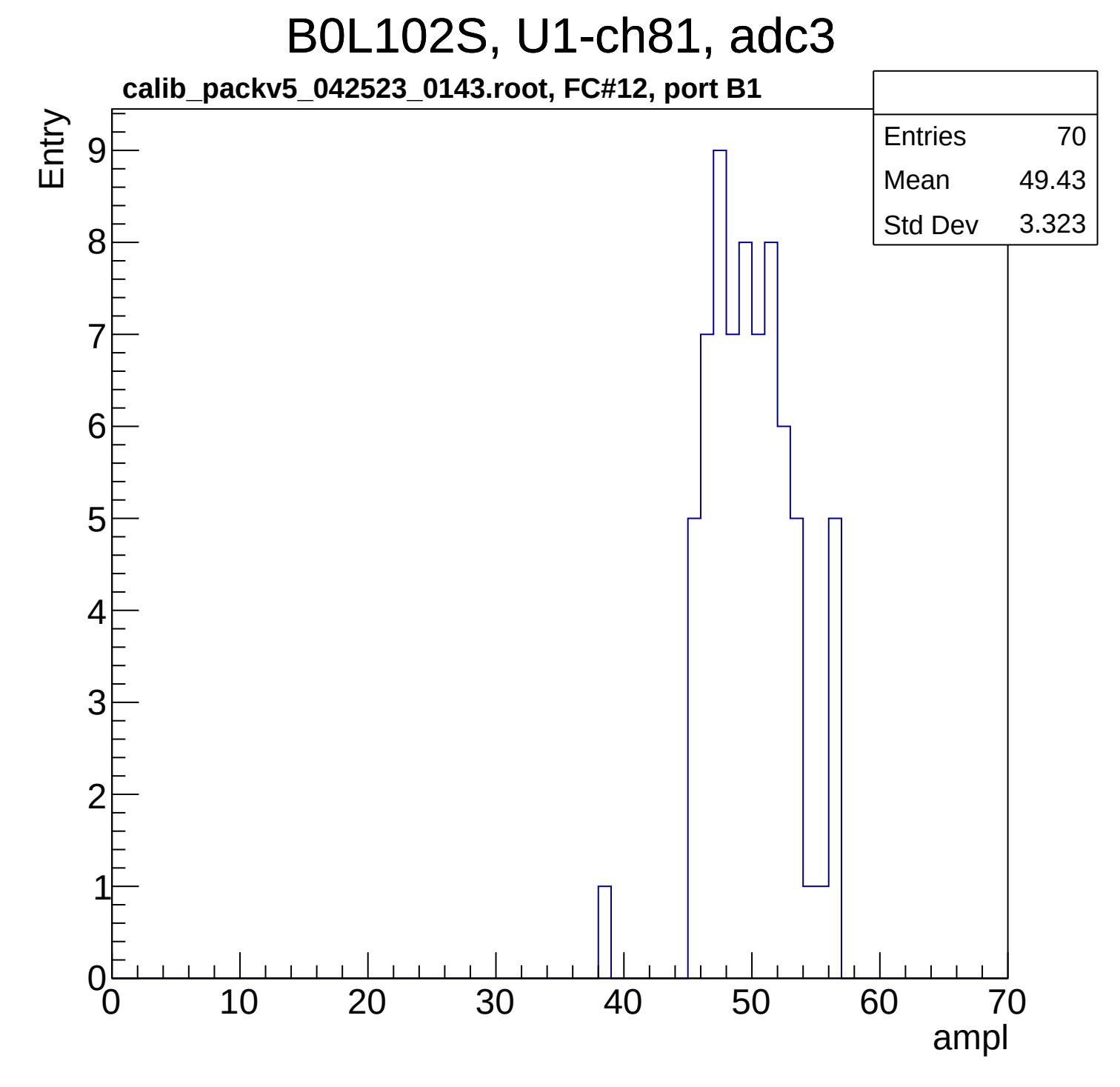
20
30
40
50
60
70


B0L102S, U1-ch80, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch81, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 29.38
Std Dev 5.555
Gaus mean : 29.3236
Gaus Width: 4.5038
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch81, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.37
Std Dev 3.577
Gaus mean : 36.5948
Gaus Width: 3.8743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch81, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 42.31
Std Dev 3.338
Gaus mean : 43.7396
Gaus Width: 3.0816
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch81, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 49.43
Std Dev 3.323
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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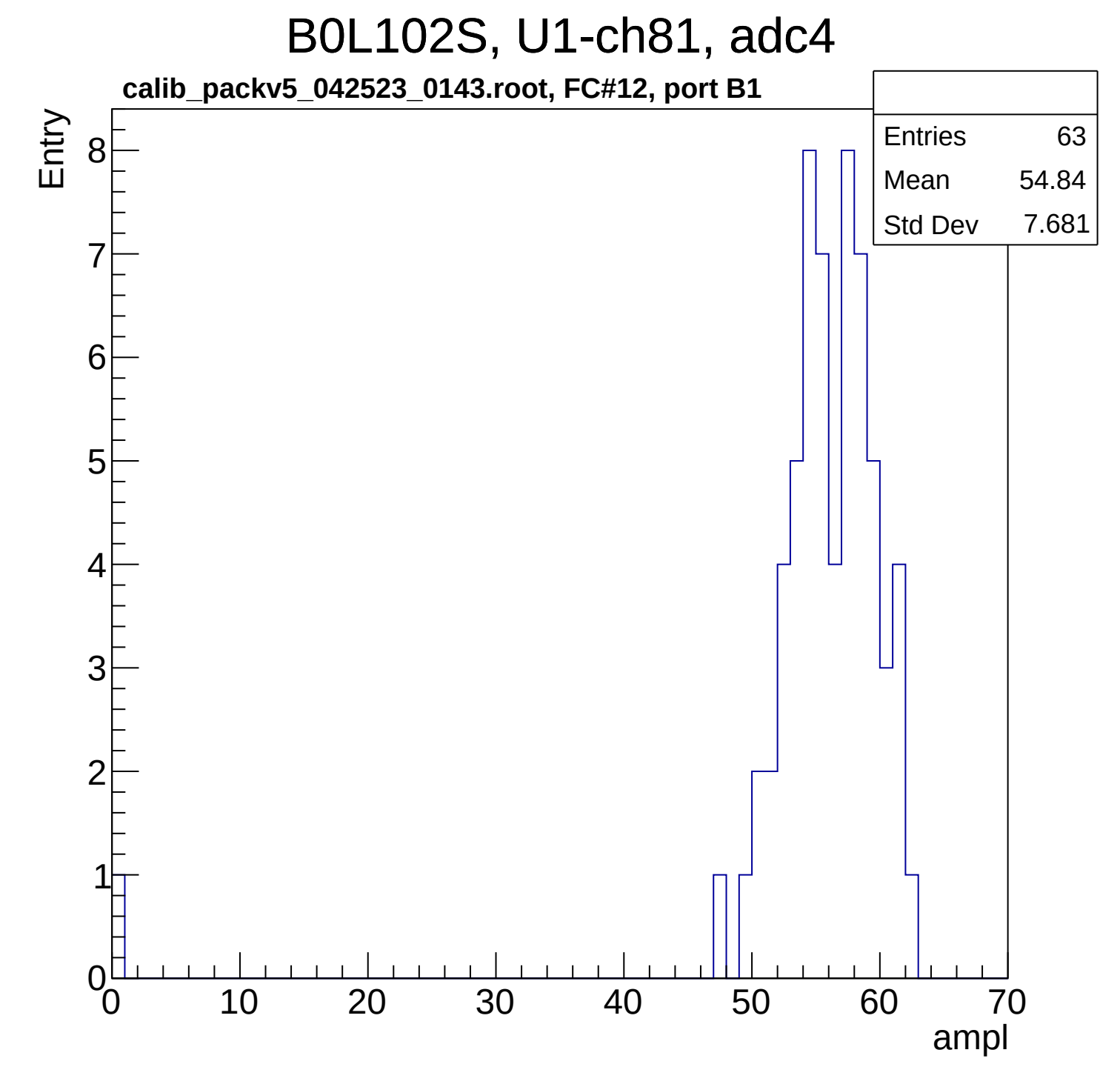
B0L102S, U1-ch81, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54.84
Std Dev 7.681
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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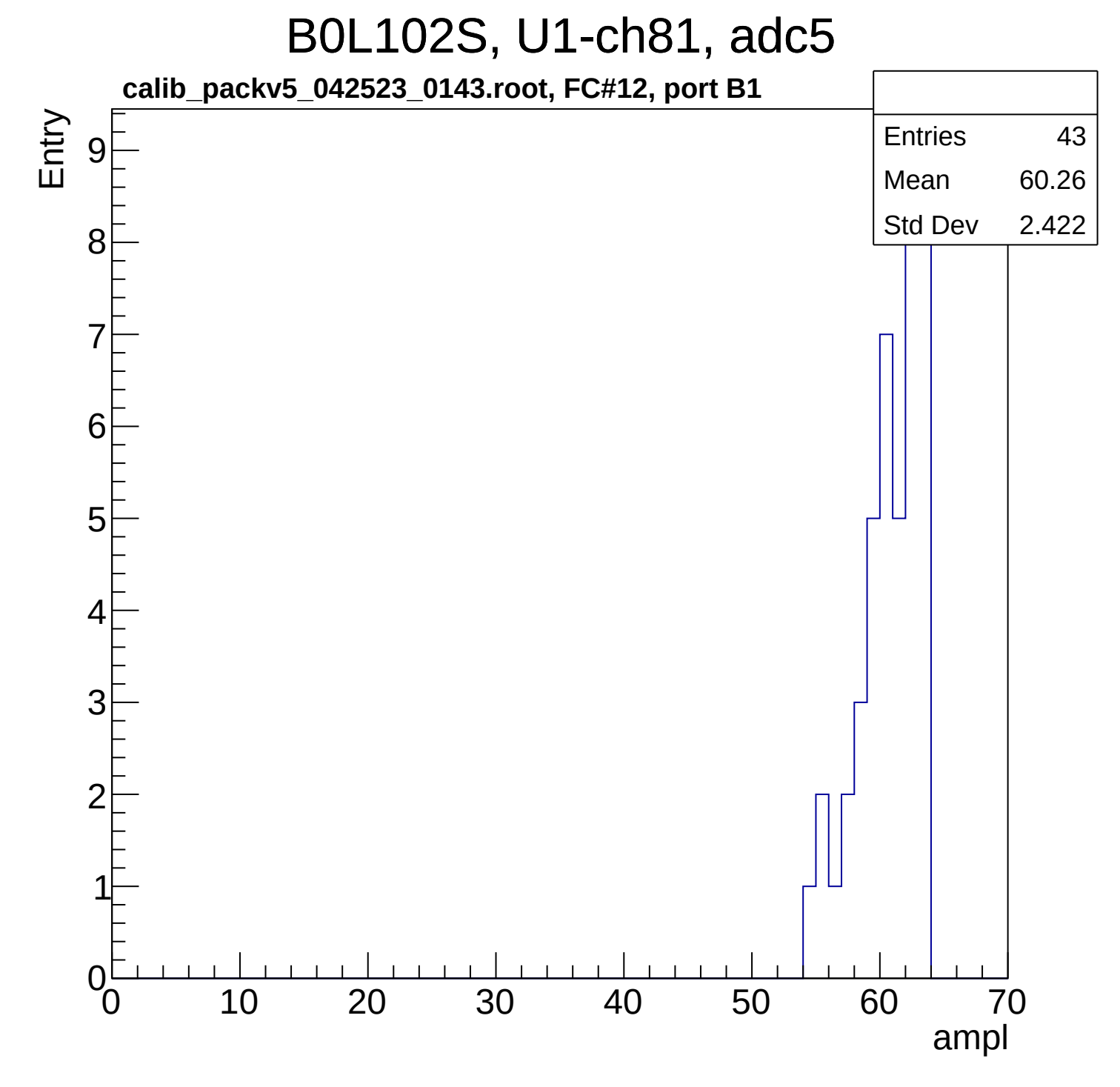
B0L102S, U1-ch81, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 60.26
Std Dev 2.422
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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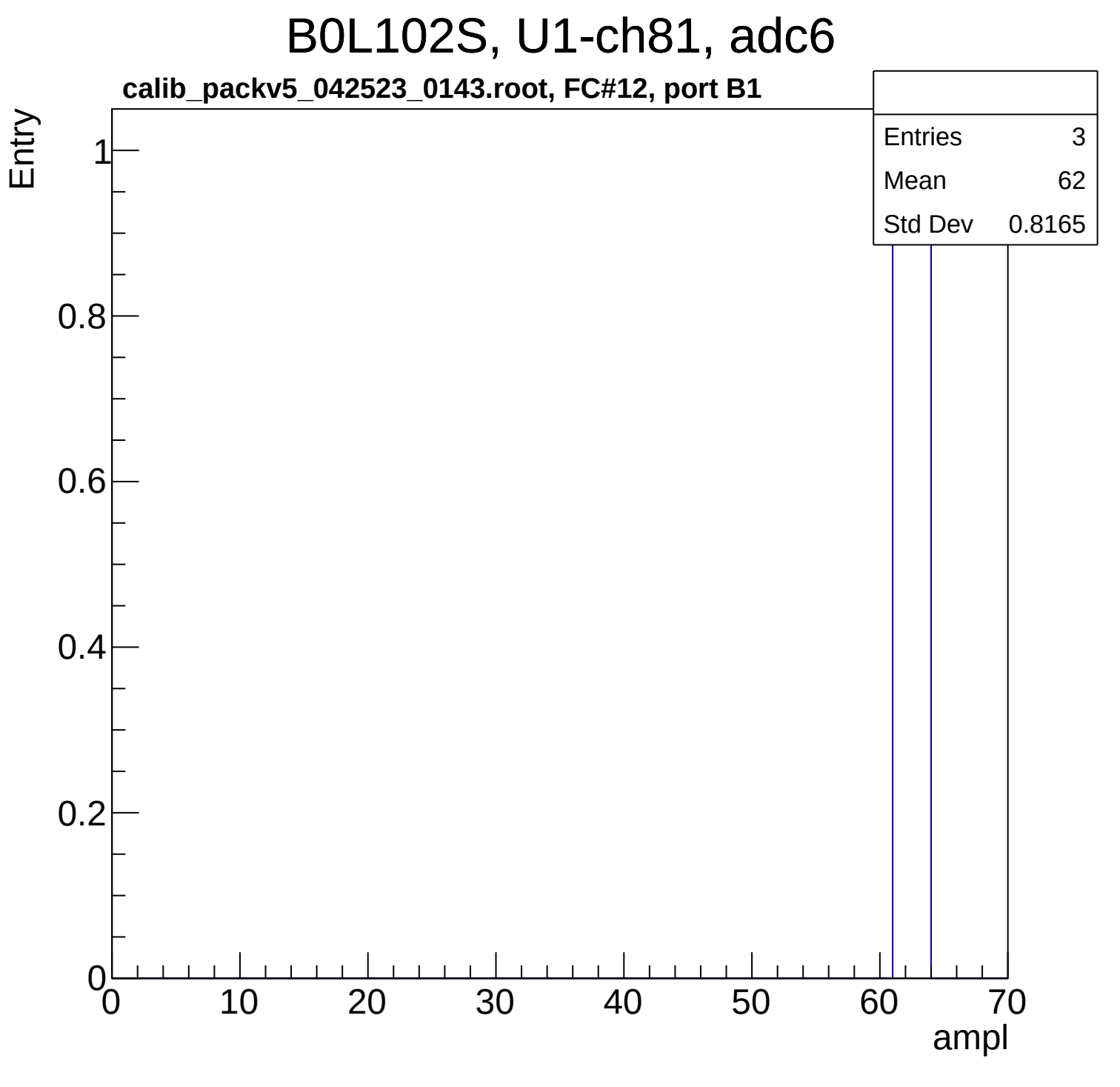
B0L102S, U1-ch81, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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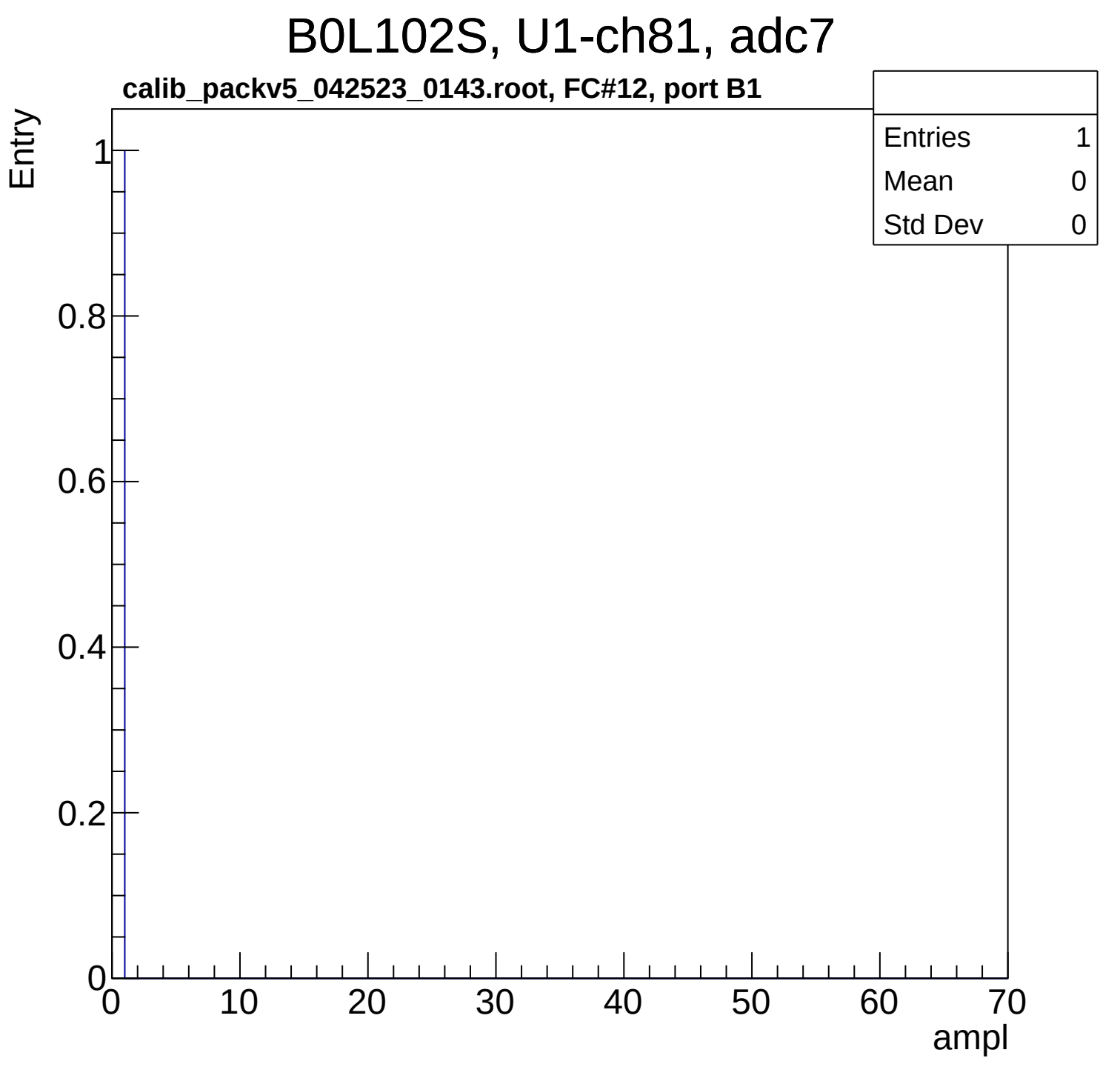
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch81, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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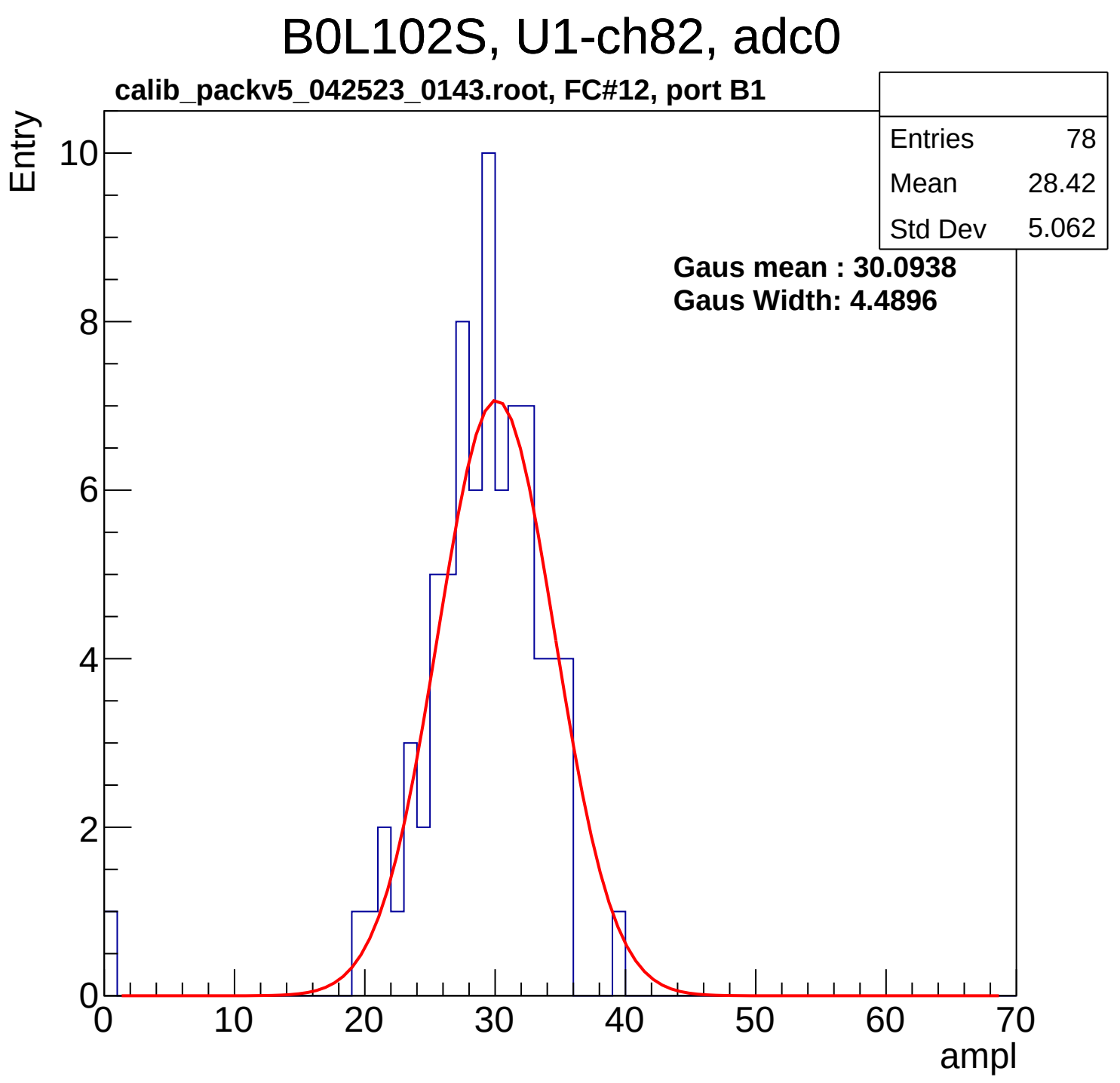
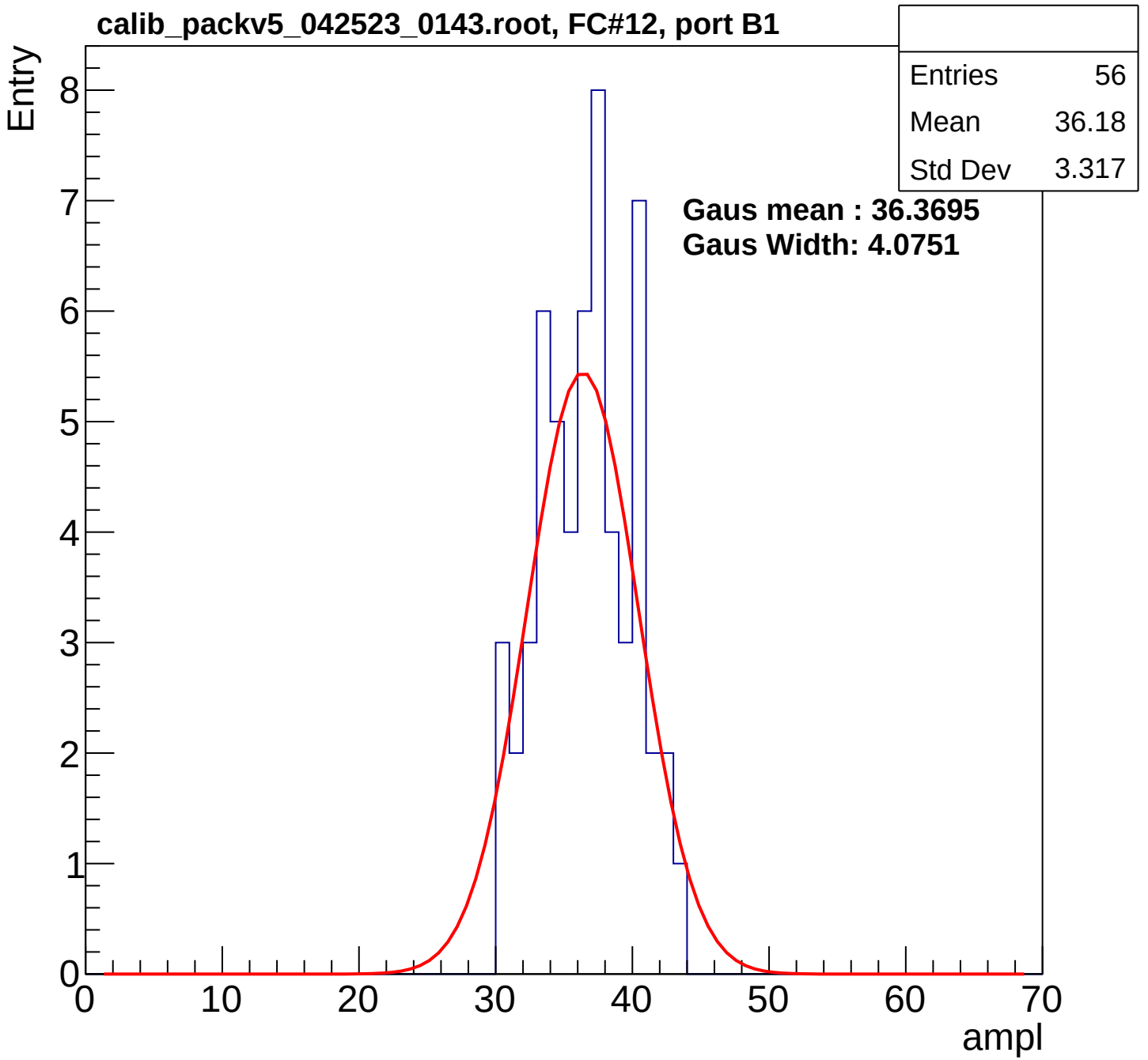
50
60
70
ampl


B0L102S, U1-ch82, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 28.42
Std Dev 5.062
Gaus mean : 30.0938
Gaus Width: 4.4896
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch82, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 36.18
Std Dev 3.317
Gaus mean : 36.3695
Gaus Width: 4.0751
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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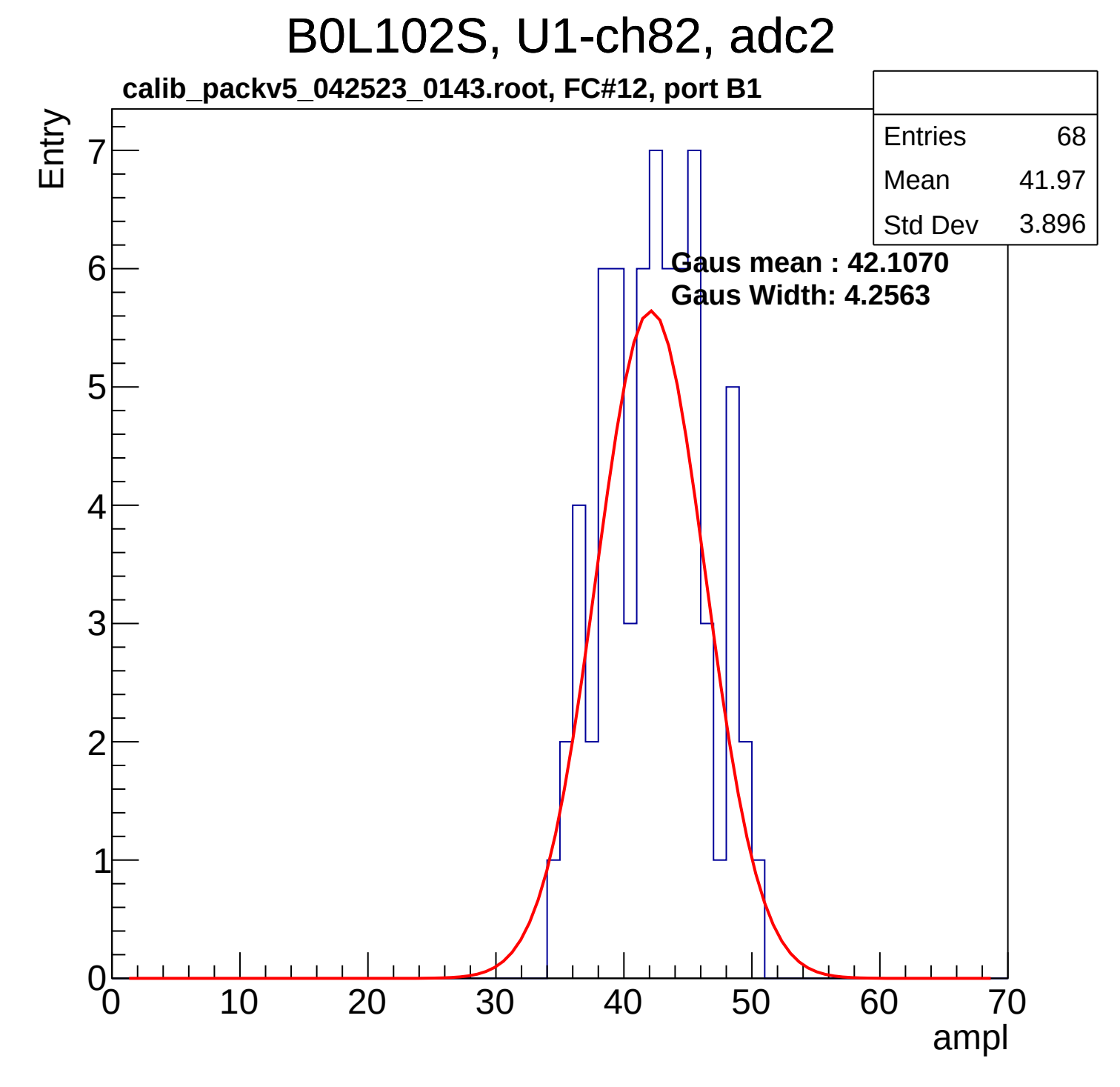
40
50
60
70


B0L102S, U1-ch82, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.97
Std Dev 3.896
Gaus mean : 42.1070
Gaus Width: 4.2563
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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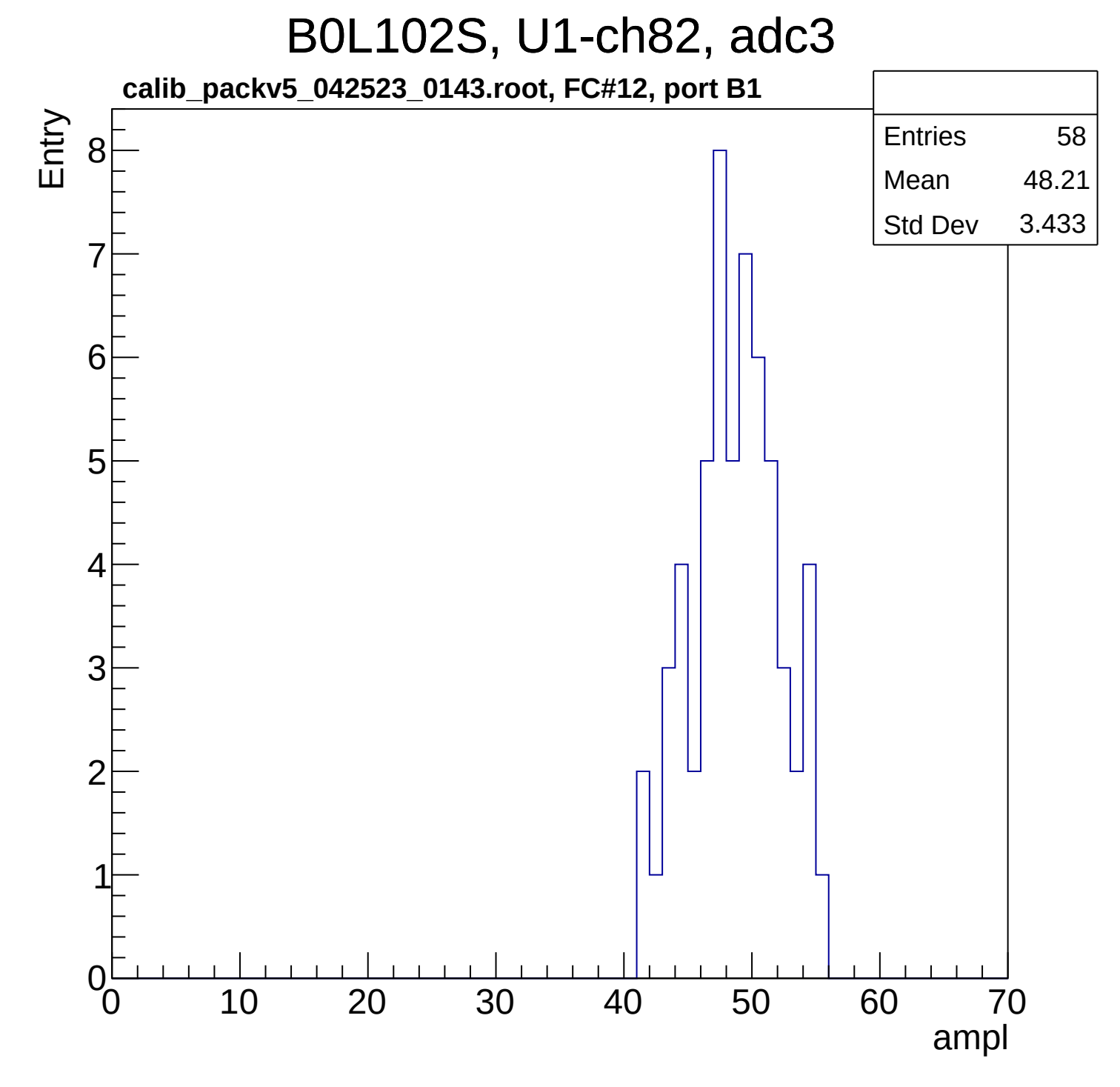
60
70


B0L102S, U1-ch82, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 48.21
Std Dev 3.433
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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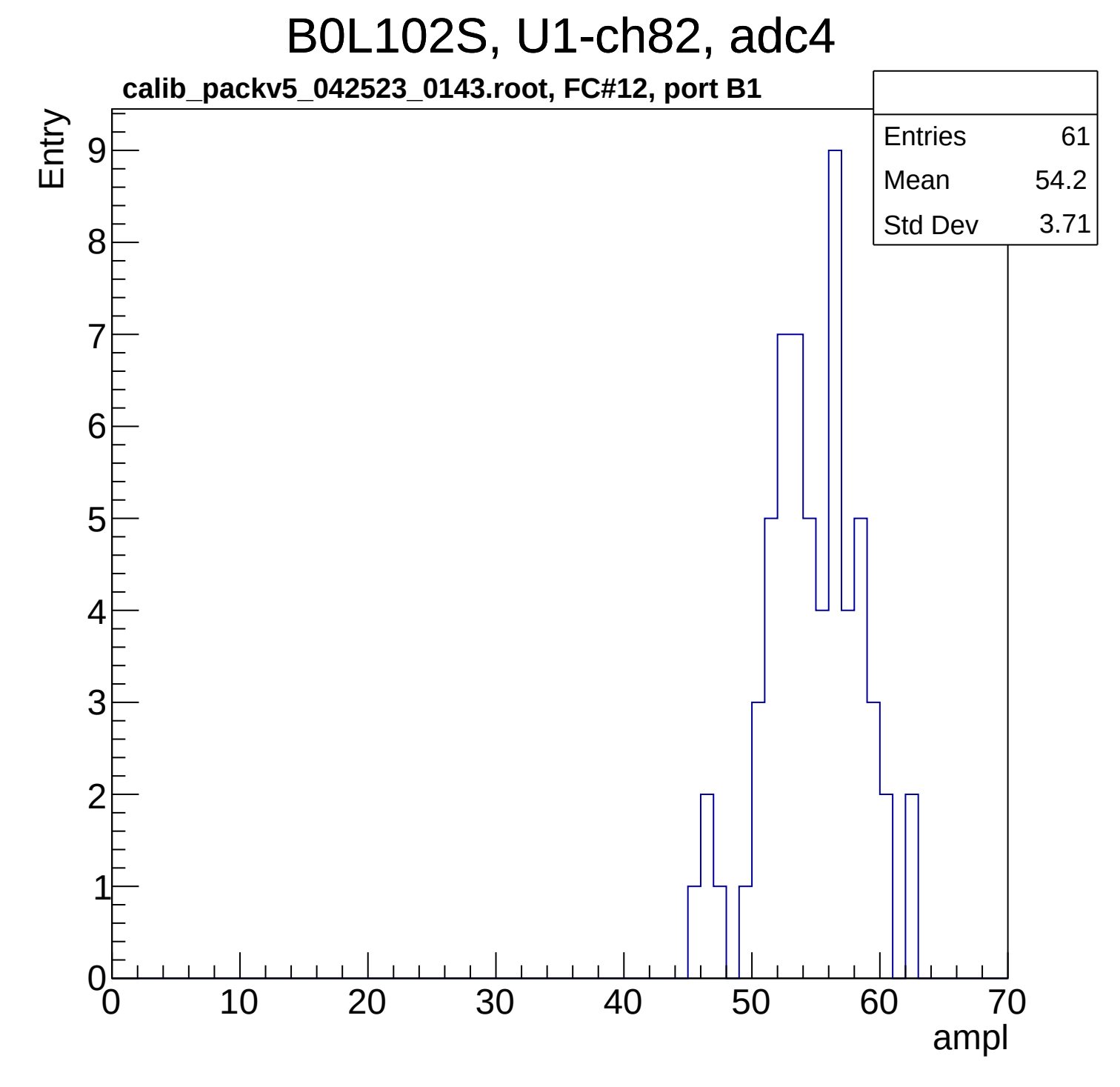
B0L102S, U1-ch82, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 54.2
Std Dev 3.71
Entry
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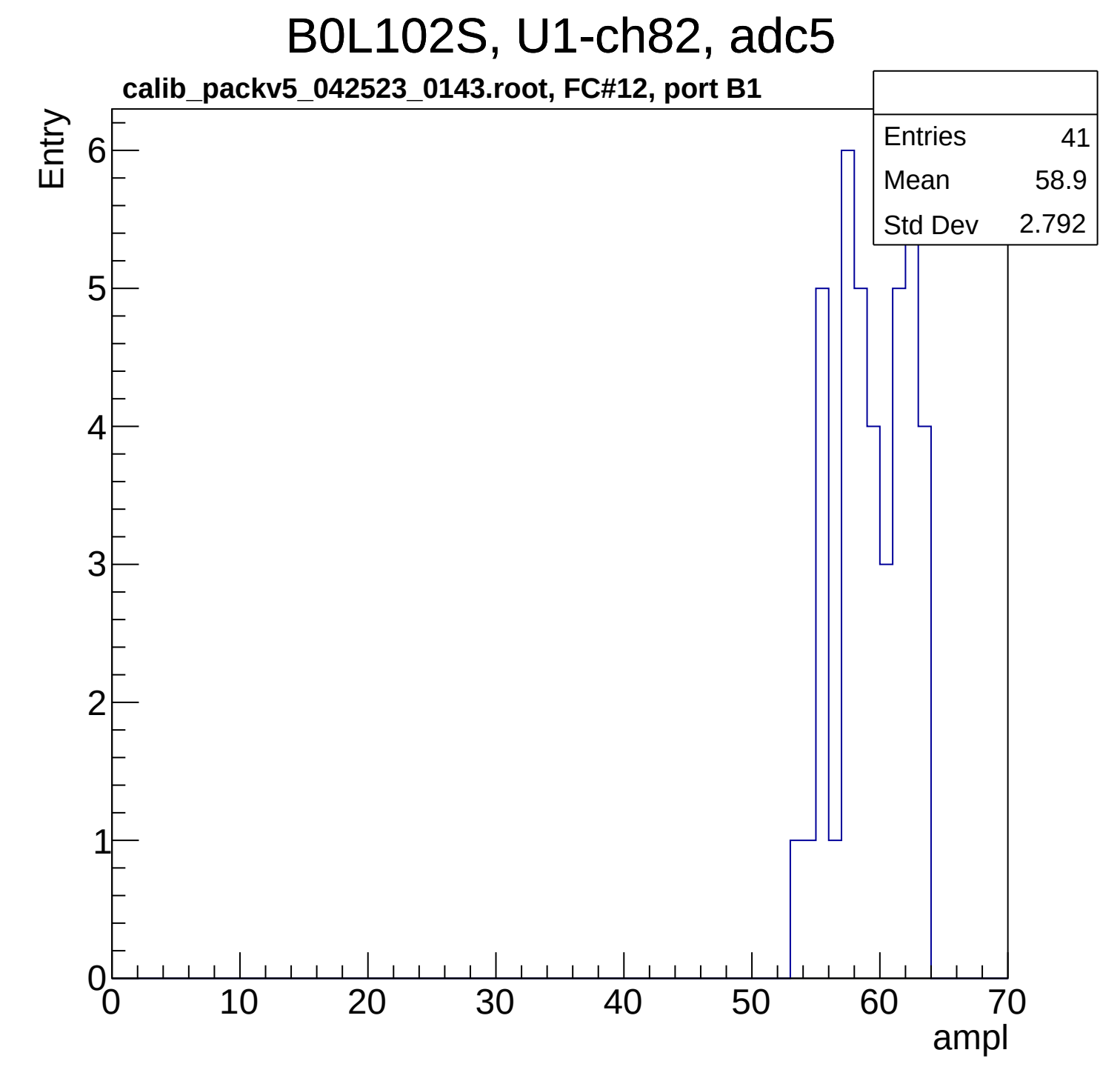
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch82, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.9
Std Dev 2.792
Entry
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2
3
4
5
6
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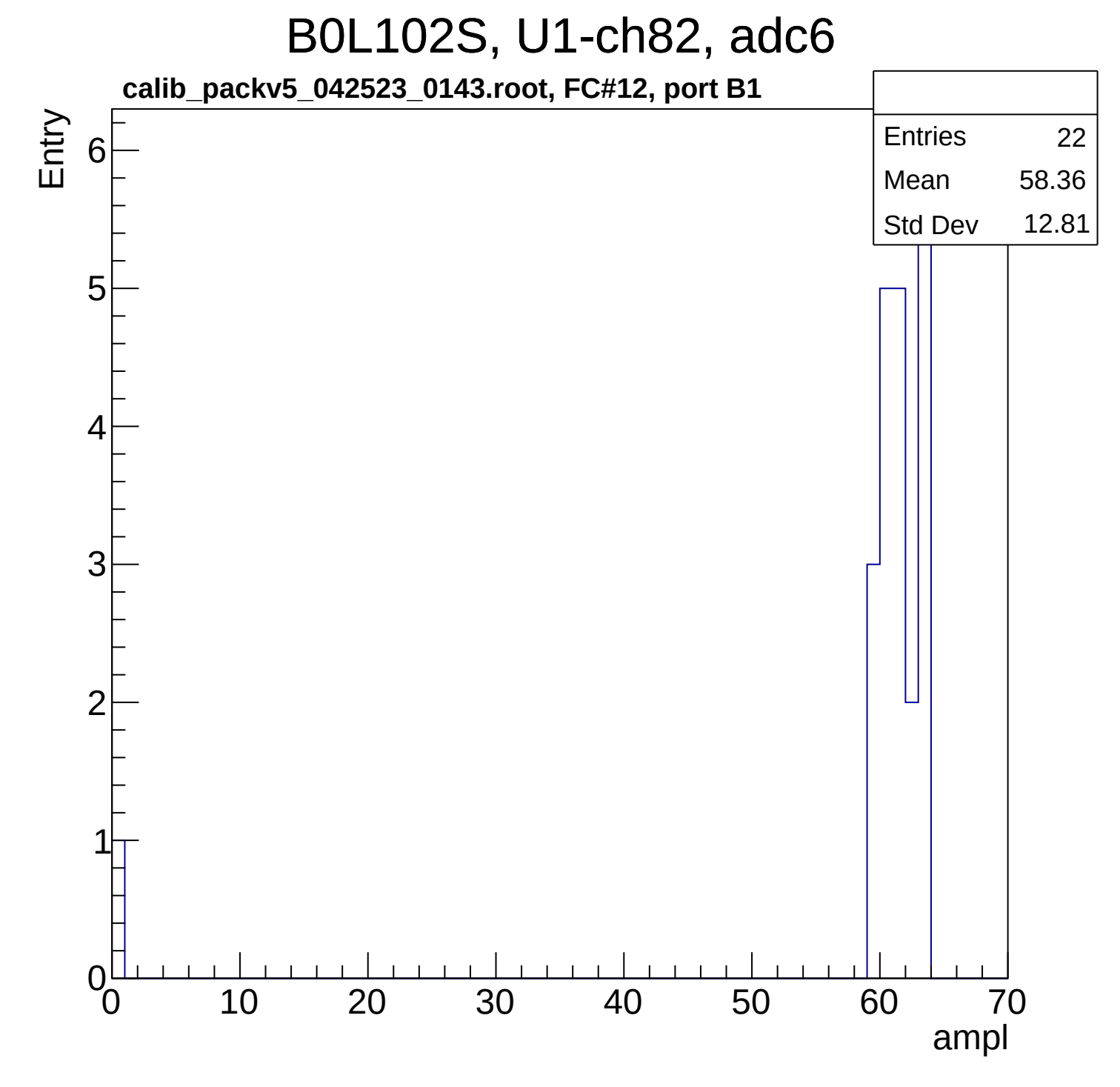
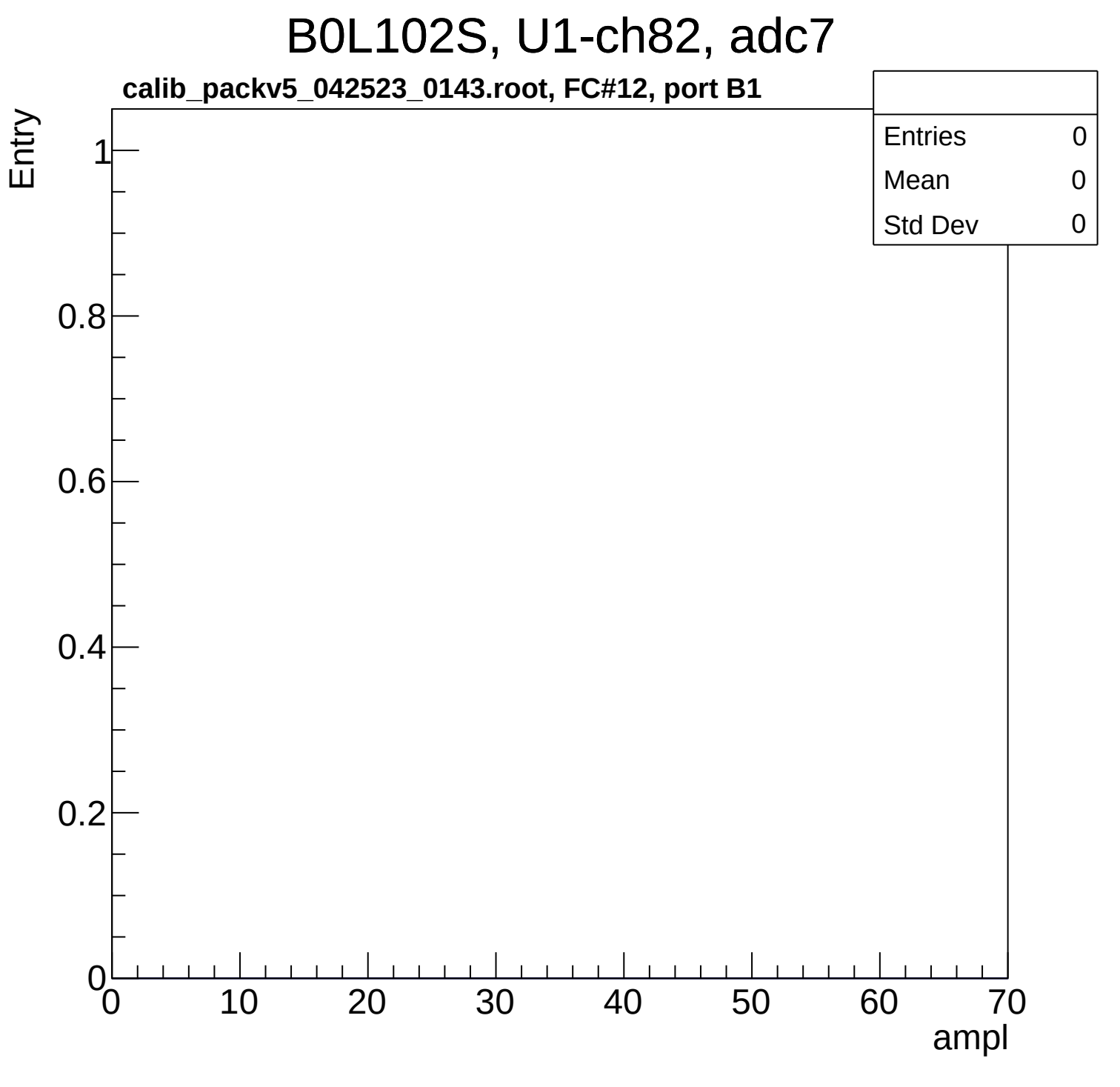
20
30
40
50
60
70
ampl


B0L102S, U1-ch82, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 22
Mean 58.36
Std Dev 12.81
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch82, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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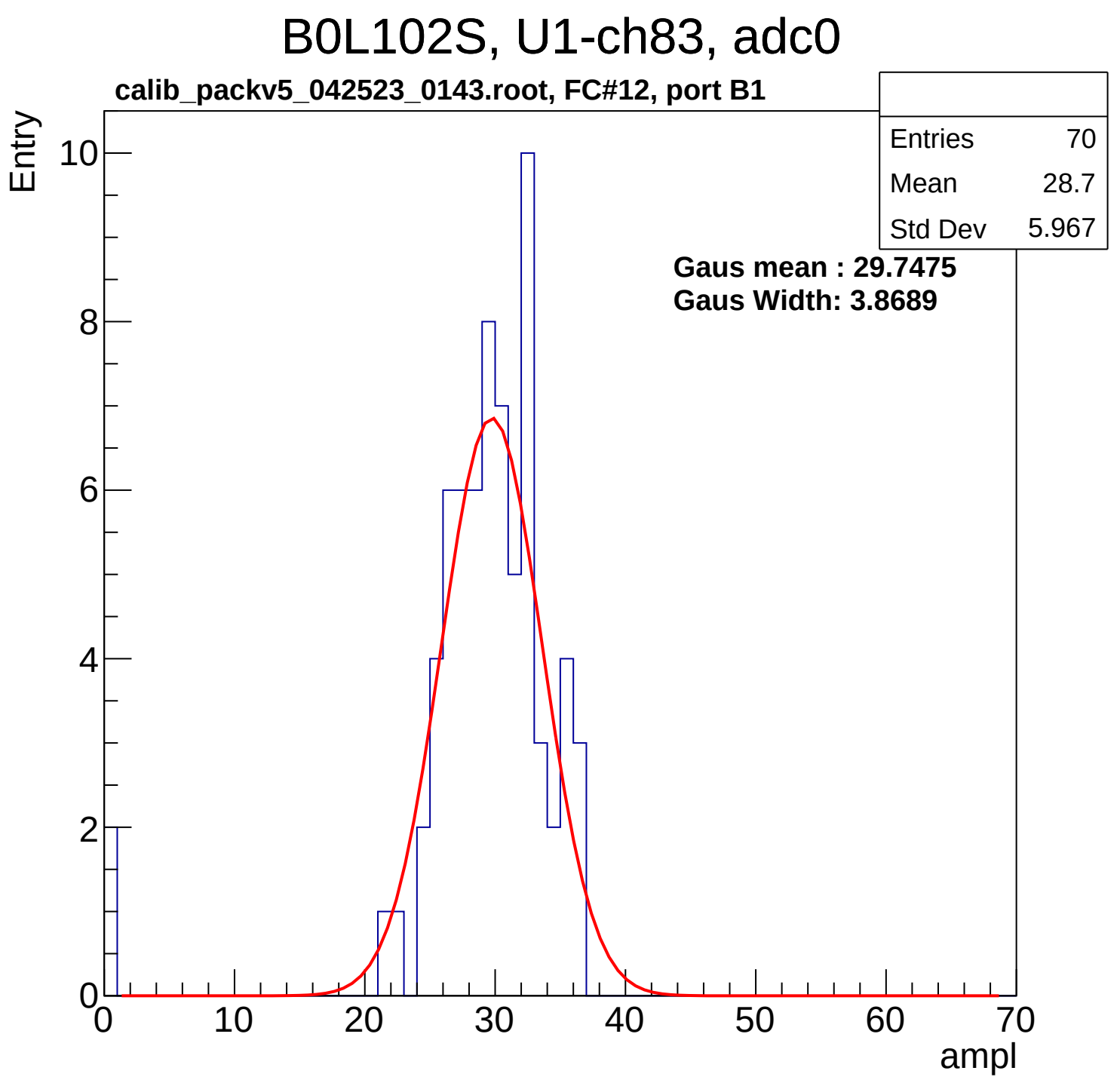
50
60
70
ampl


B0L102S, U1-ch83, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 28.7
Std Dev 5.967
Gaus mean : 29.7475
Gaus Width: 3.8689
Entry
ampl
0
2
4
6
8
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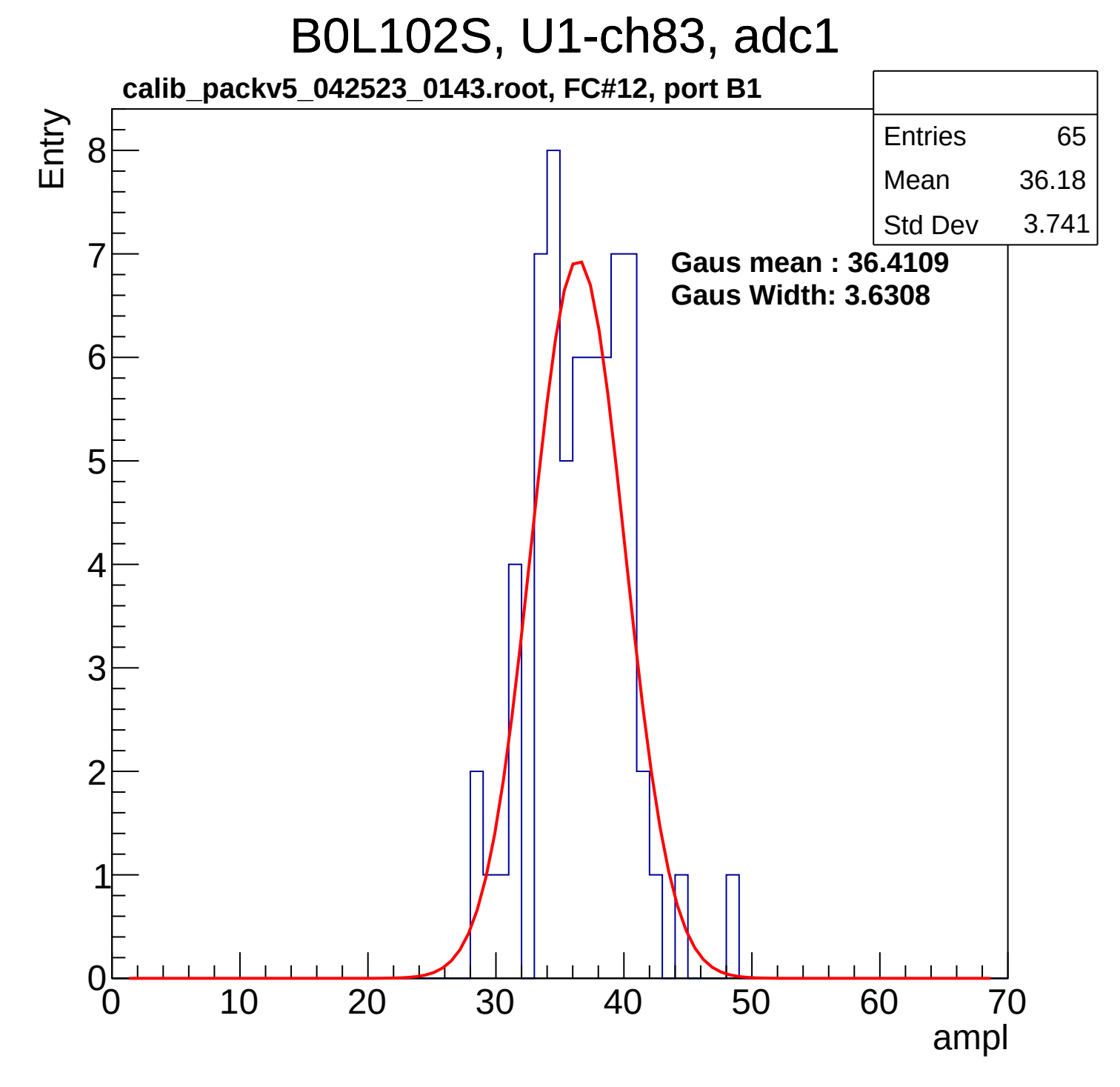
0
10
20
30
40
50
60
70


B0L102S, U1-ch83, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 36.18
Std Dev 3.741
Gaus mean : 36.4109
Gaus Width: 3.6308
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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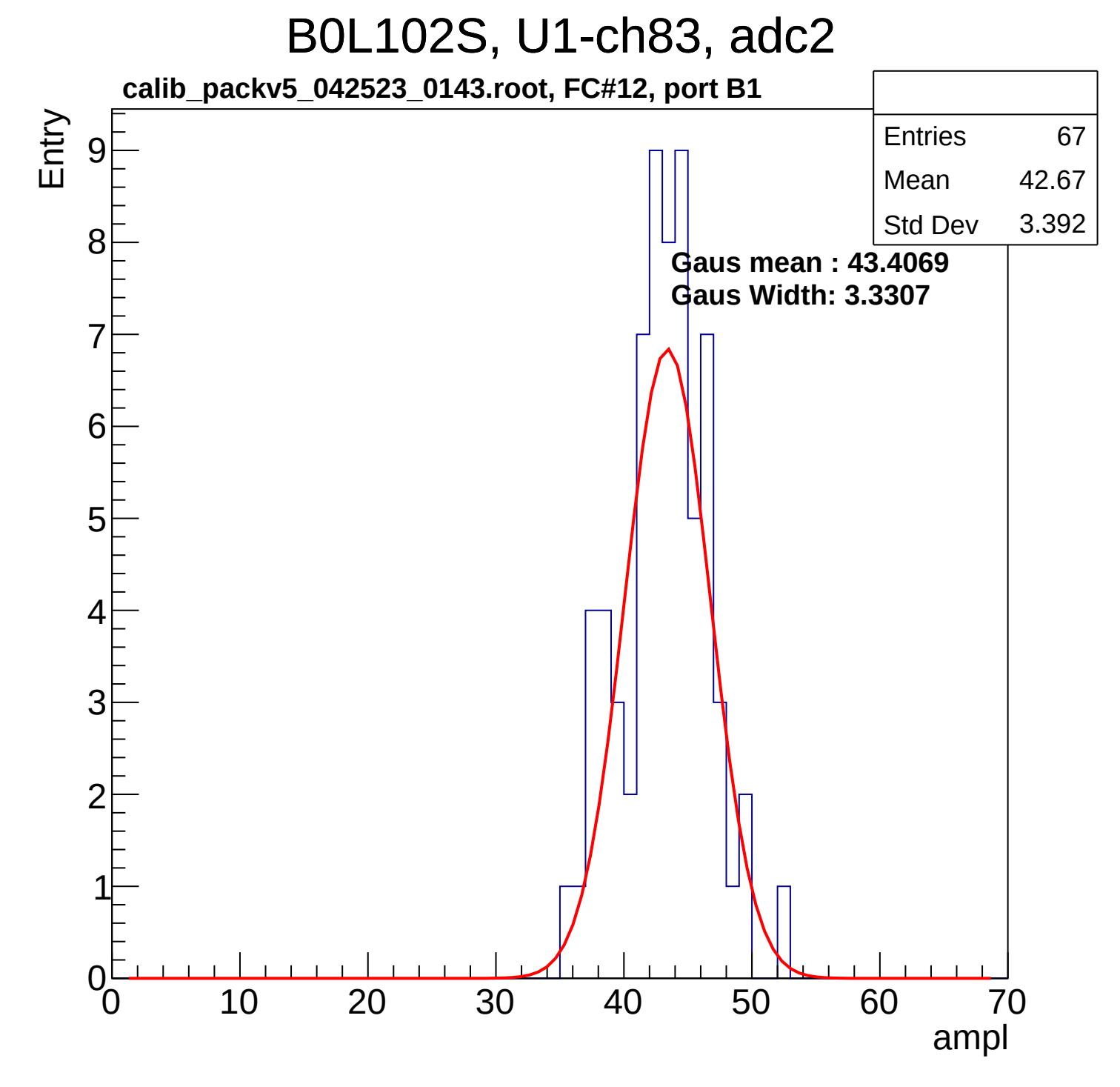
B0L102S, U1-ch83, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 42.67
Std Dev 3.392
Gaus mean : 43.4069
Gaus Width: 3.3307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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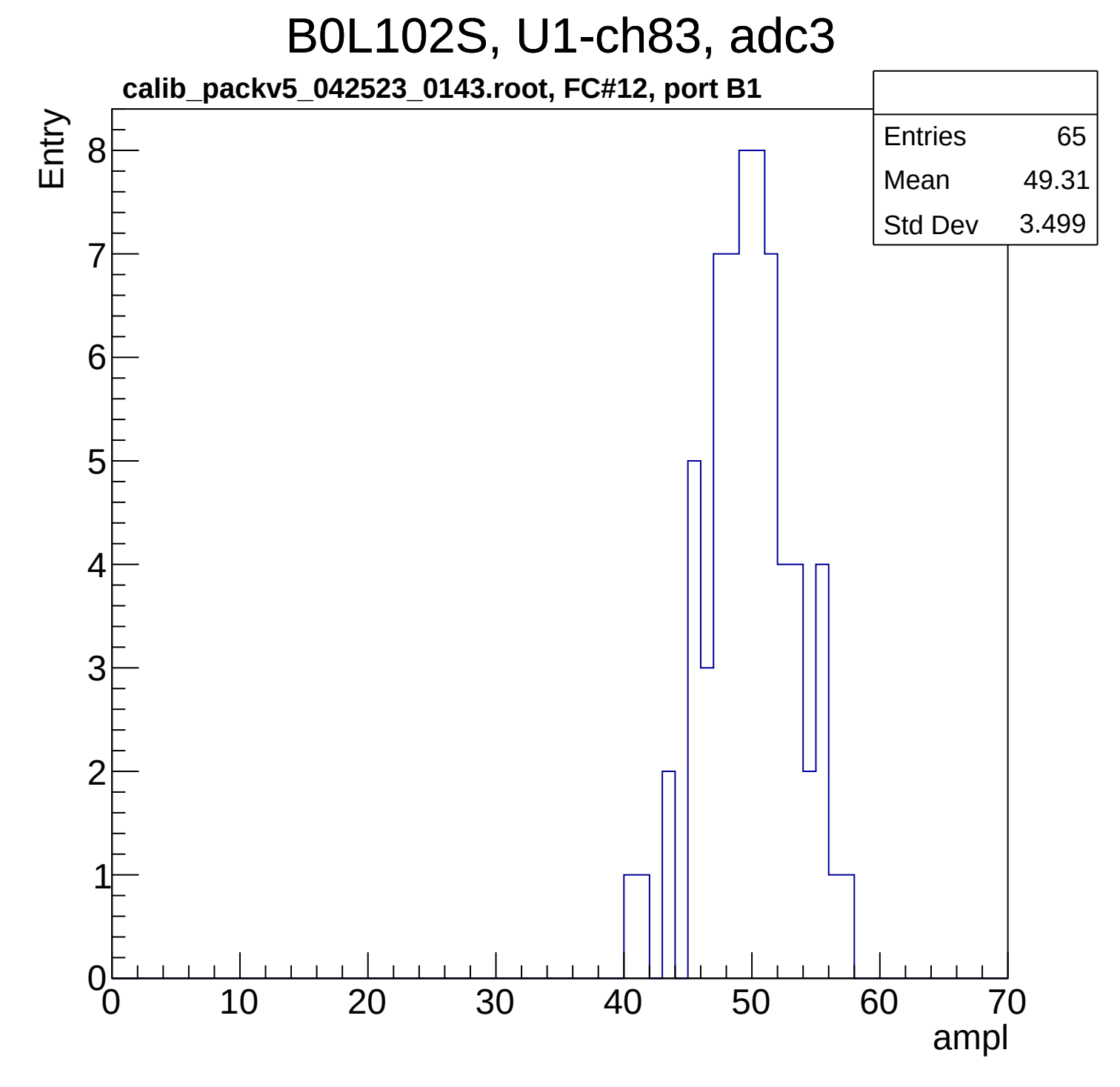
B0L102S, U1-ch83, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 49.31
Std Dev 3.499
Entry
ampl
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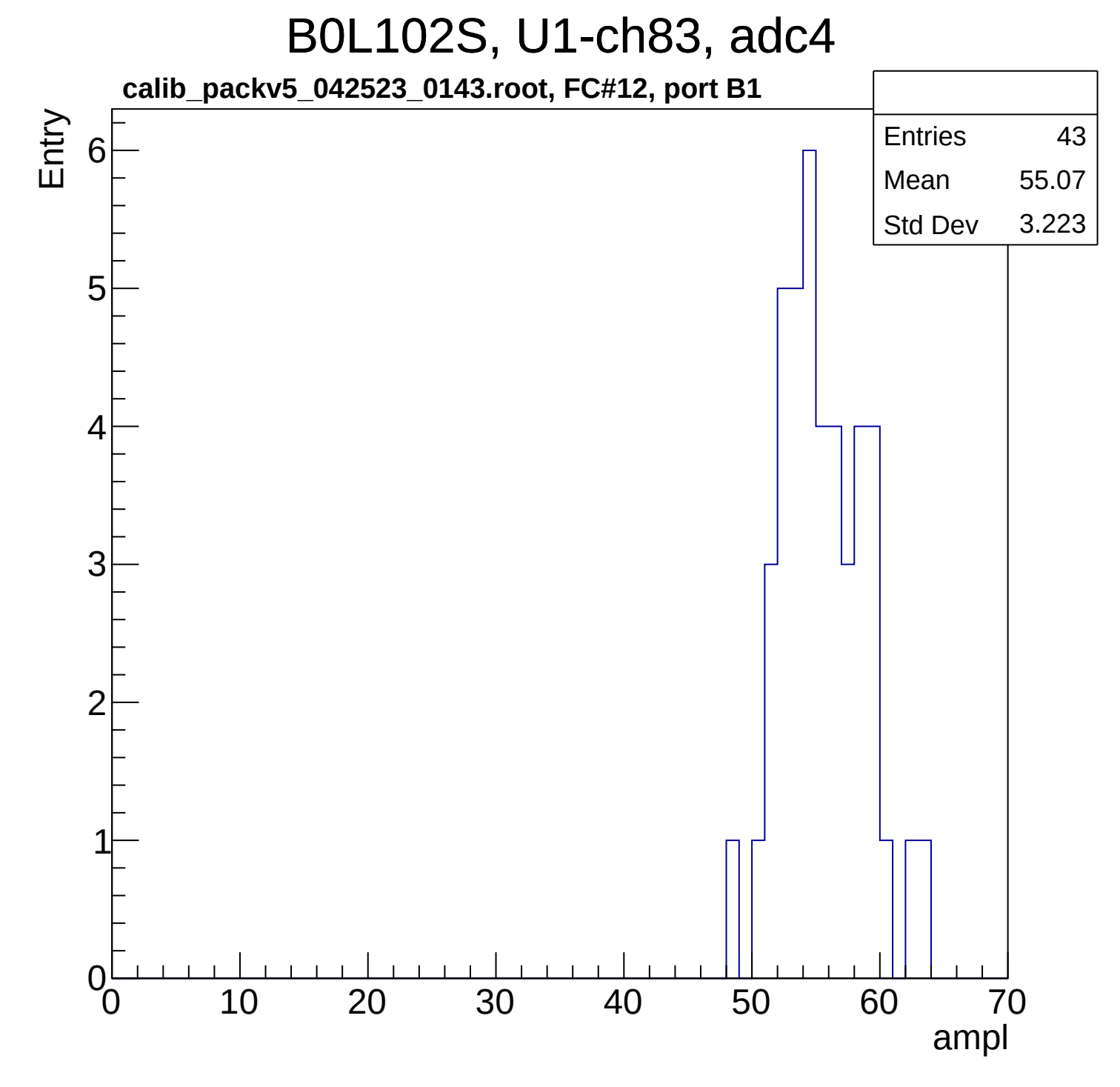
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch83, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.07
Std Dev 3.223
Entry
0
1
2
3
4
5
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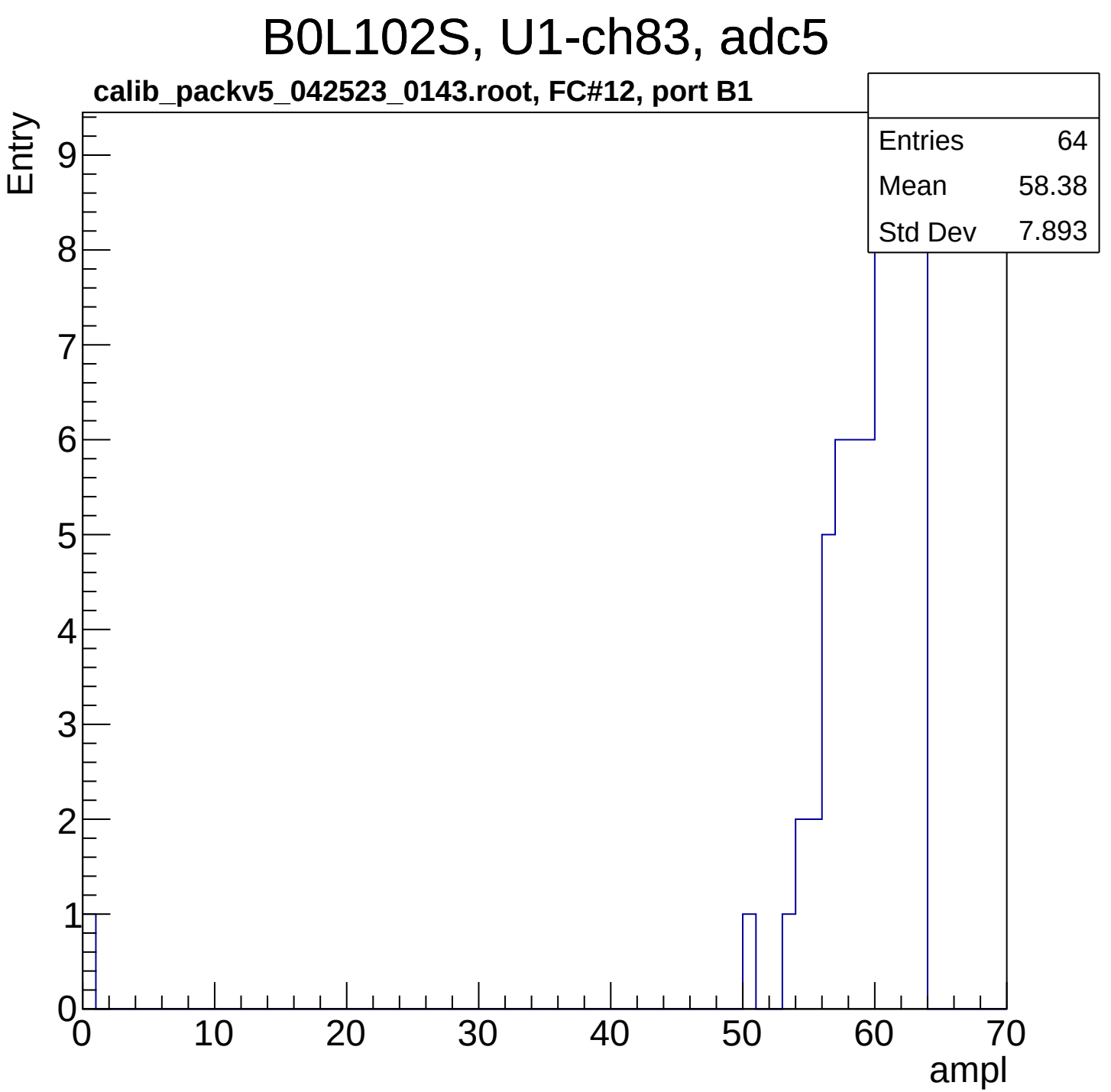
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch83, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 58.38
Std Dev 7.893
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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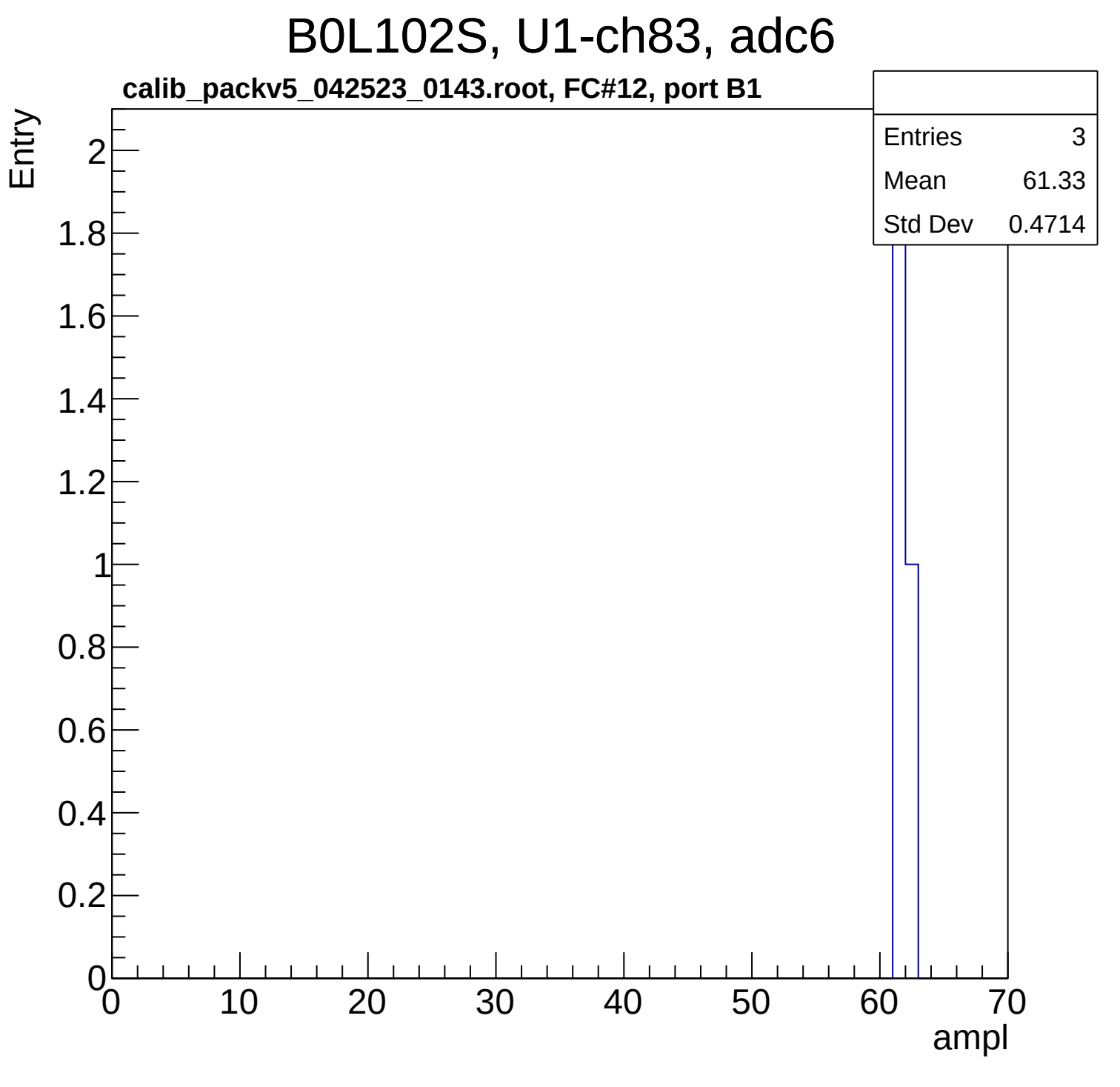
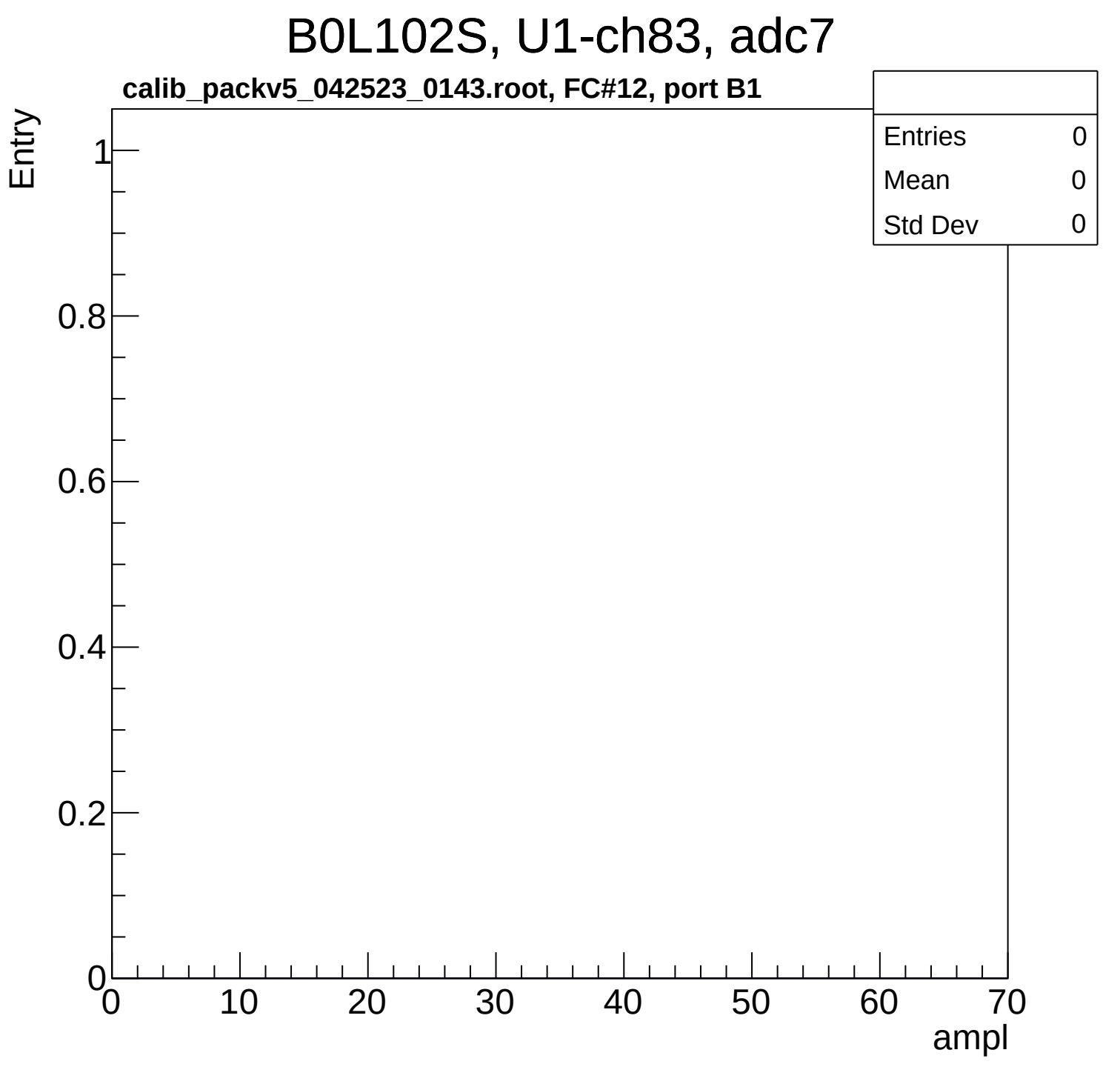

B0L102S, U1-ch83, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch83, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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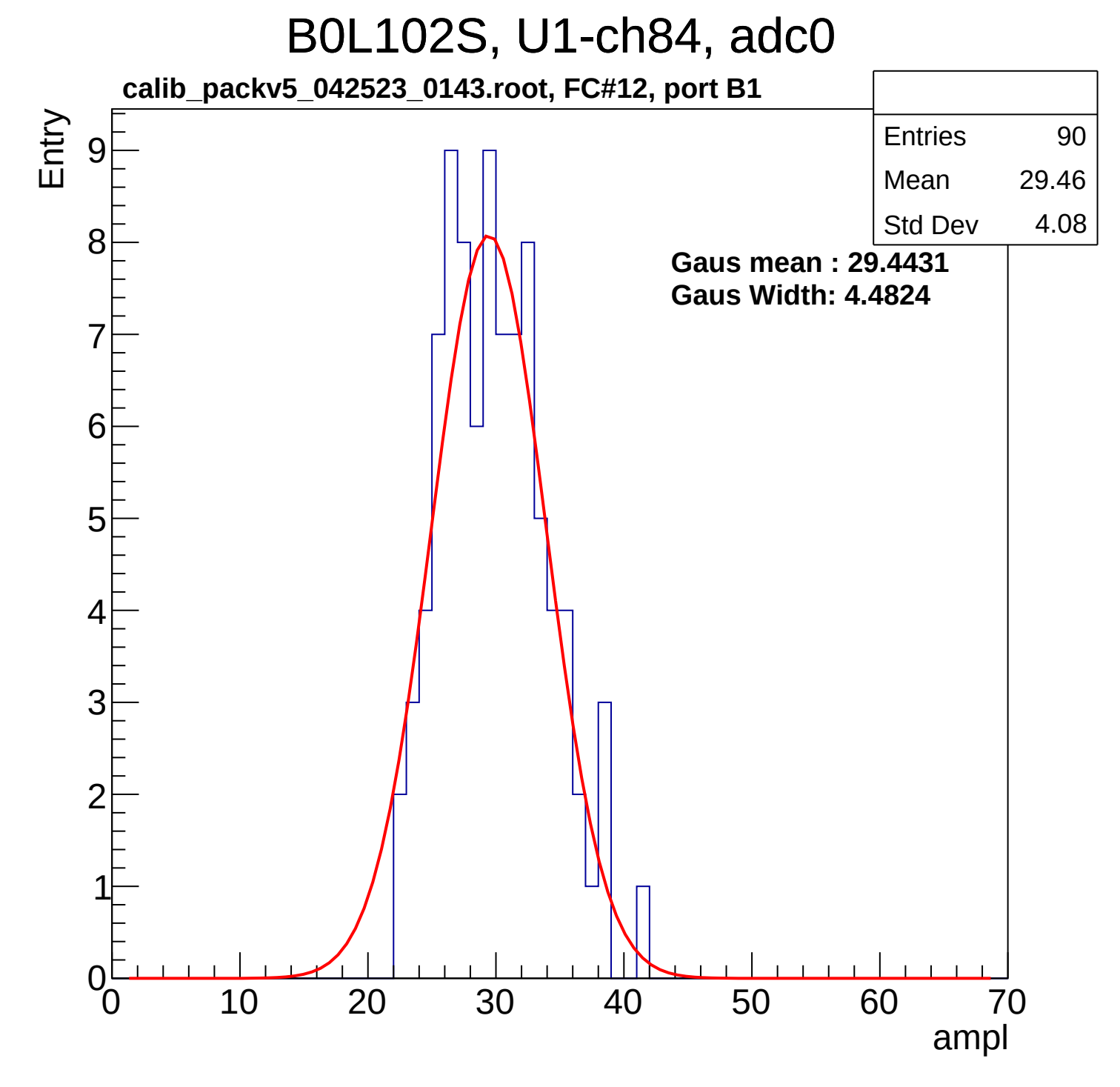
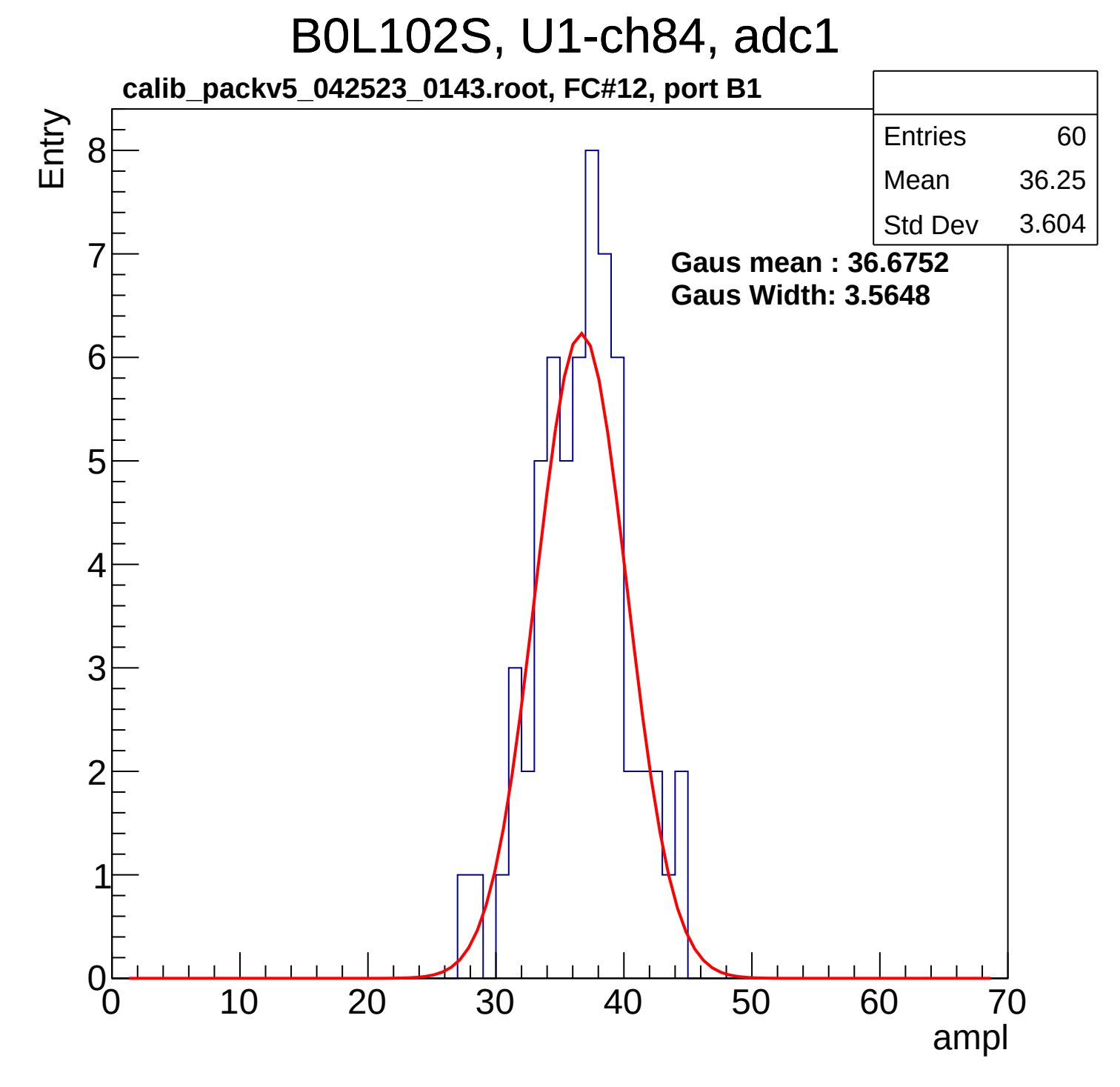
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch84, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 90
Mean 29.46
Std Dev 4.08
Gaus mean : 29.4431
Gaus Width: 4.4824
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch84, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 36.25
Std Dev 3.604
Gaus mean : 36.6752
Gaus Width: 3.5648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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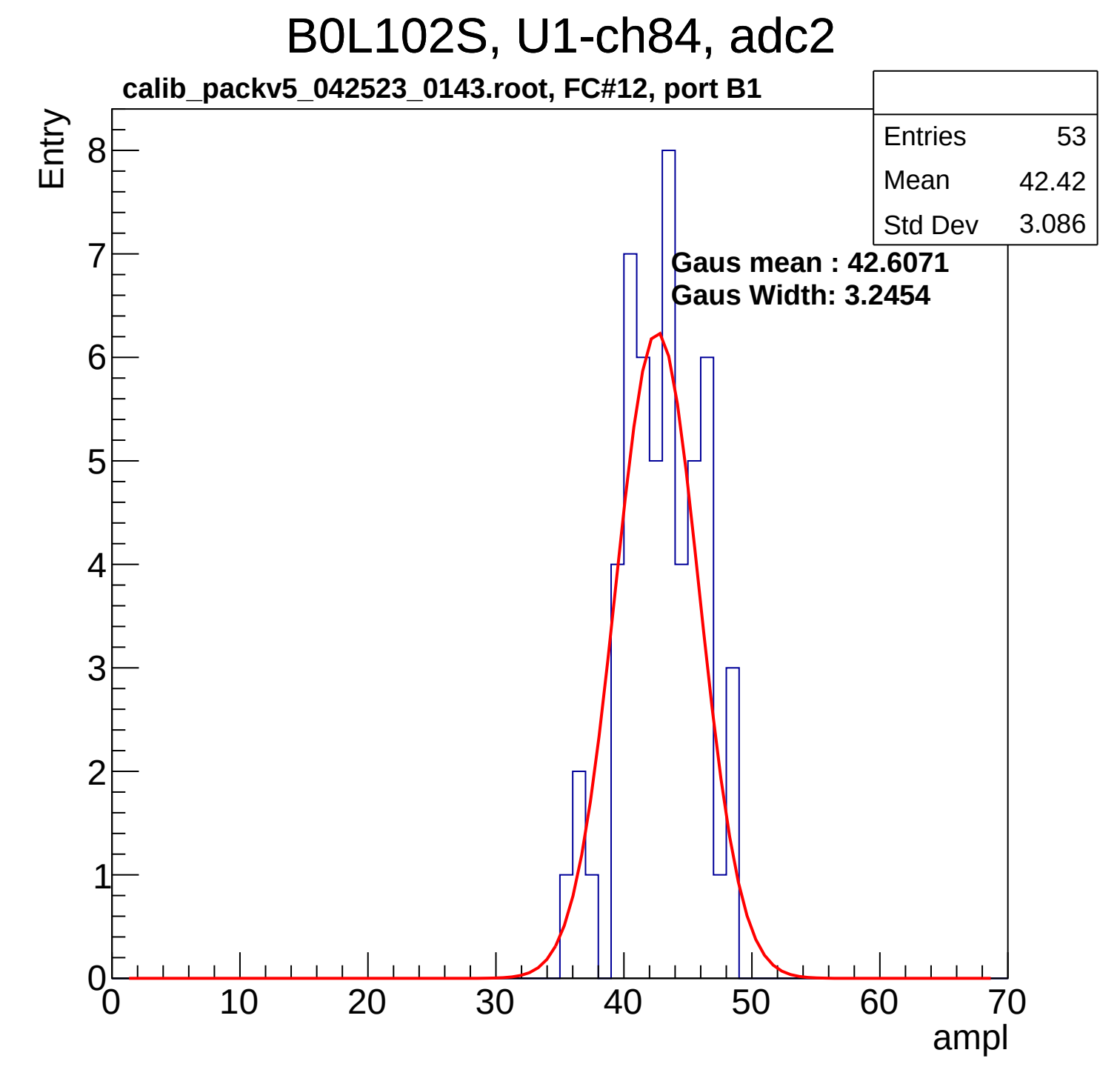
B0L102S, U1-ch84, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 42.42
Std Dev 3.086
Gaus mean : 42.6071
Gaus Width: 3.2454
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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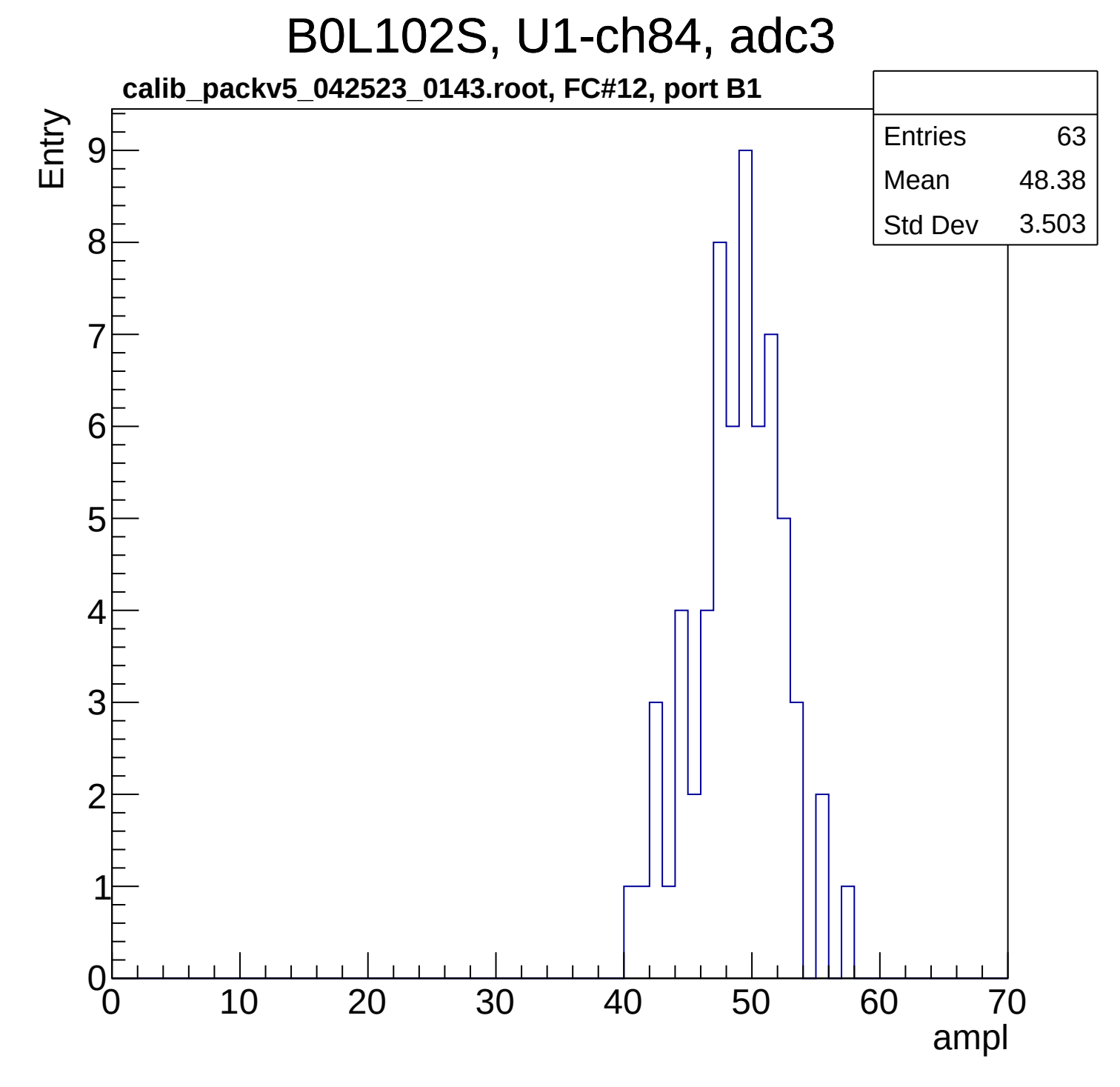
B0L102S, U1-ch84, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 48.38
Std Dev 3.503
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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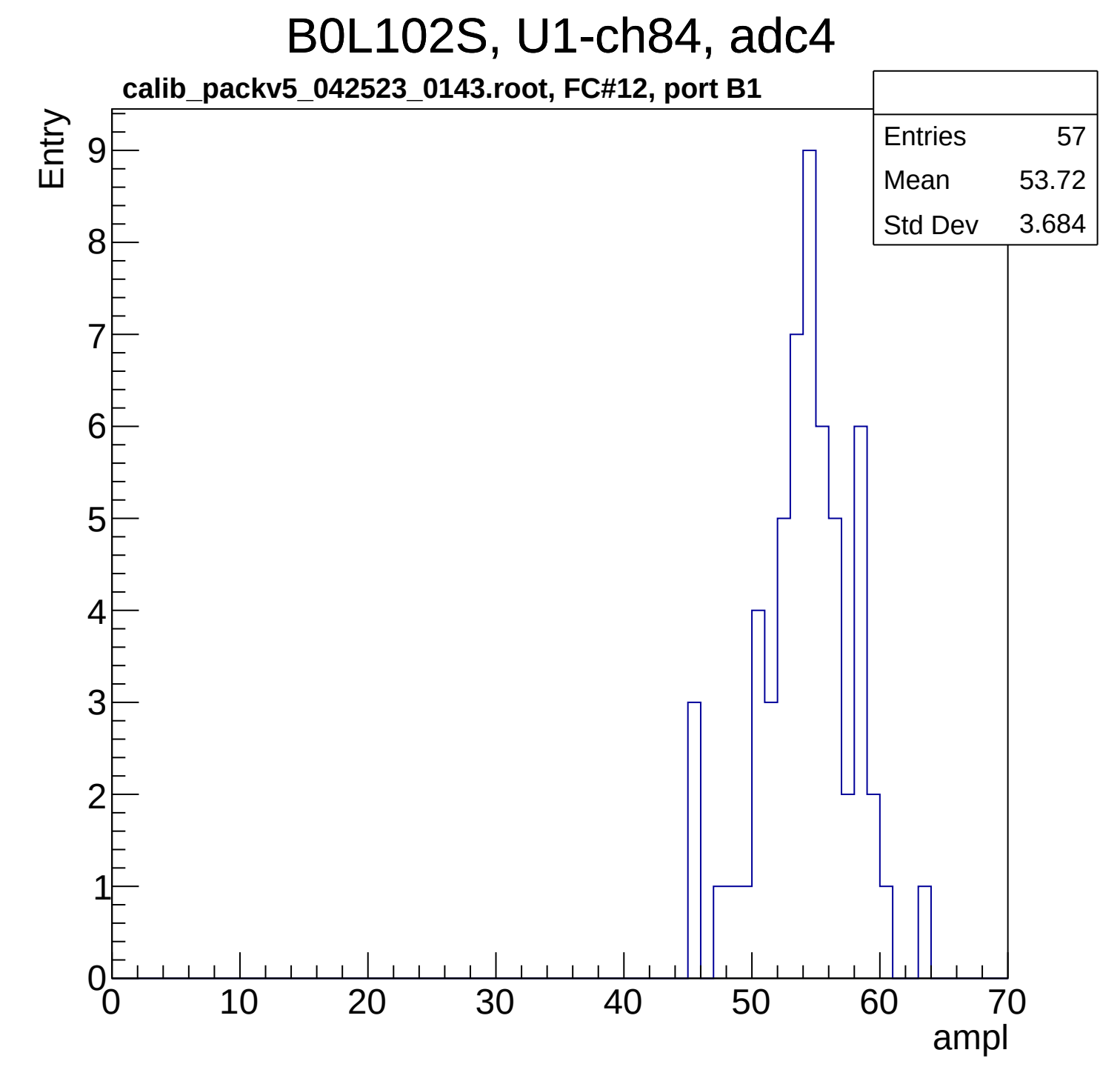
B0L102S, U1-ch84, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 53.72
Std Dev 3.684
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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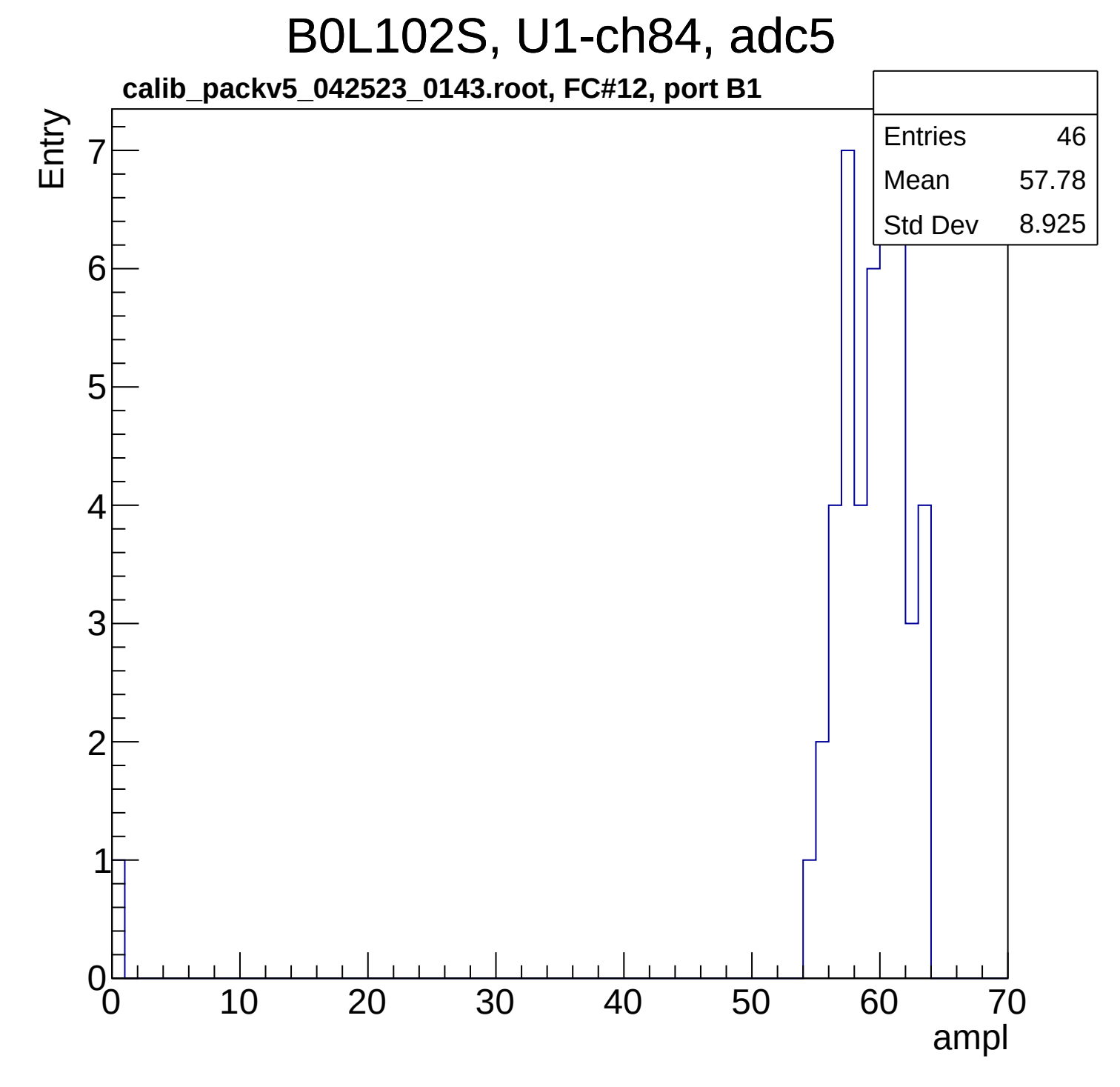
40
50
60
70


B0L102S, U1-ch84, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 57.78
Std Dev 8.925
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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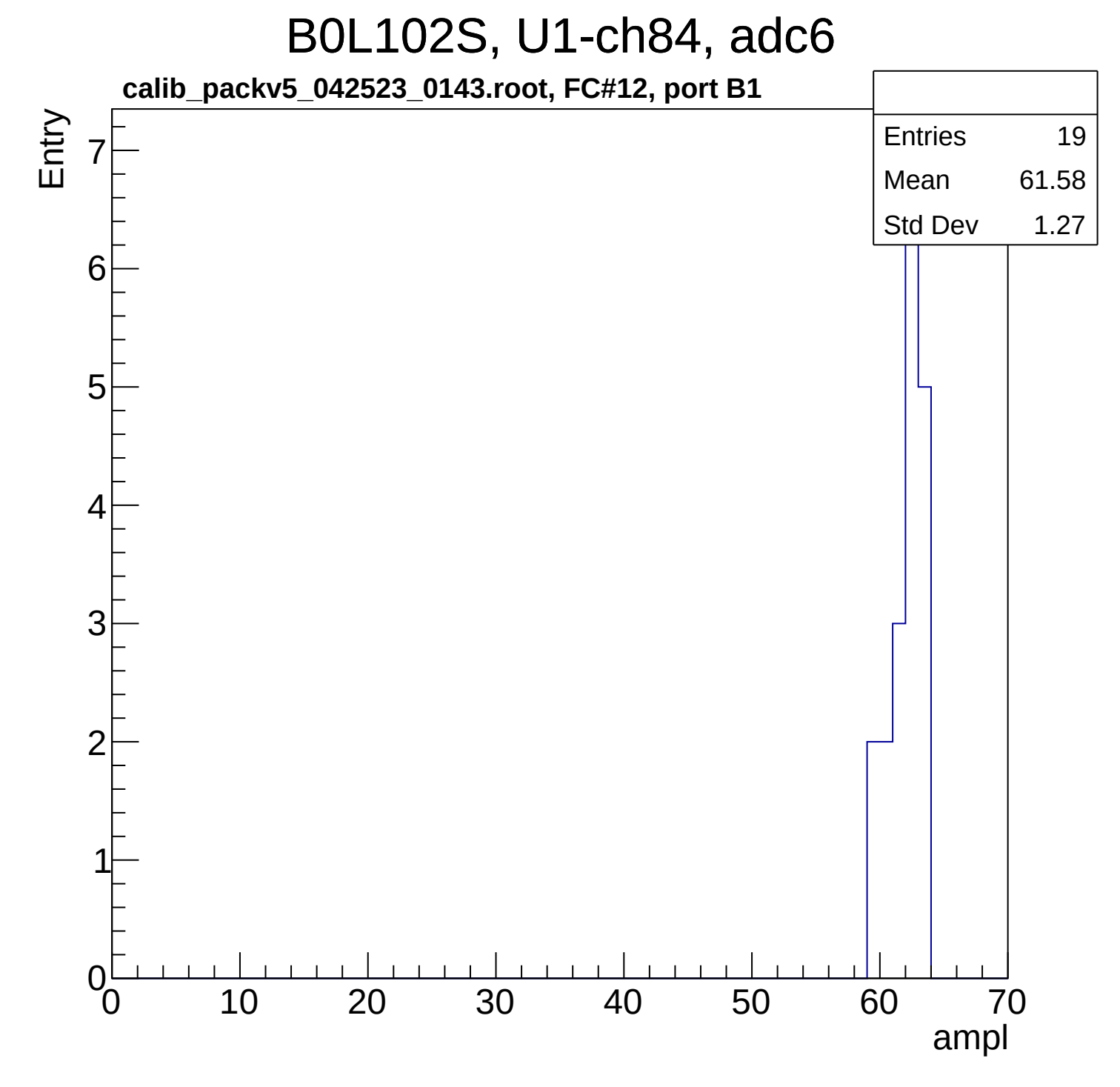
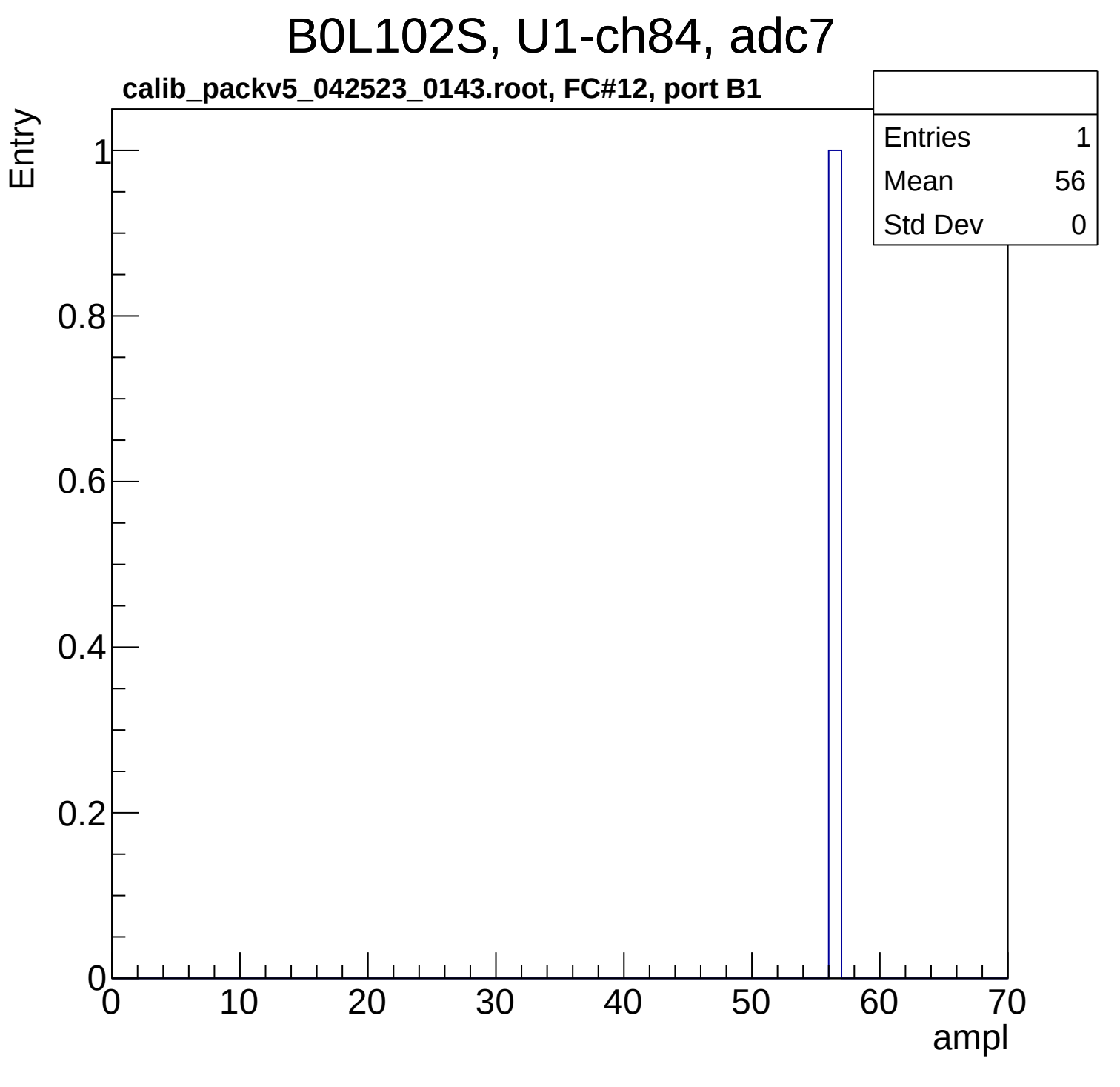

B0L102S, U1-ch84, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 19
Mean 61.58
Std Dev 1.27
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch84, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 56
Std Dev 0
Entry
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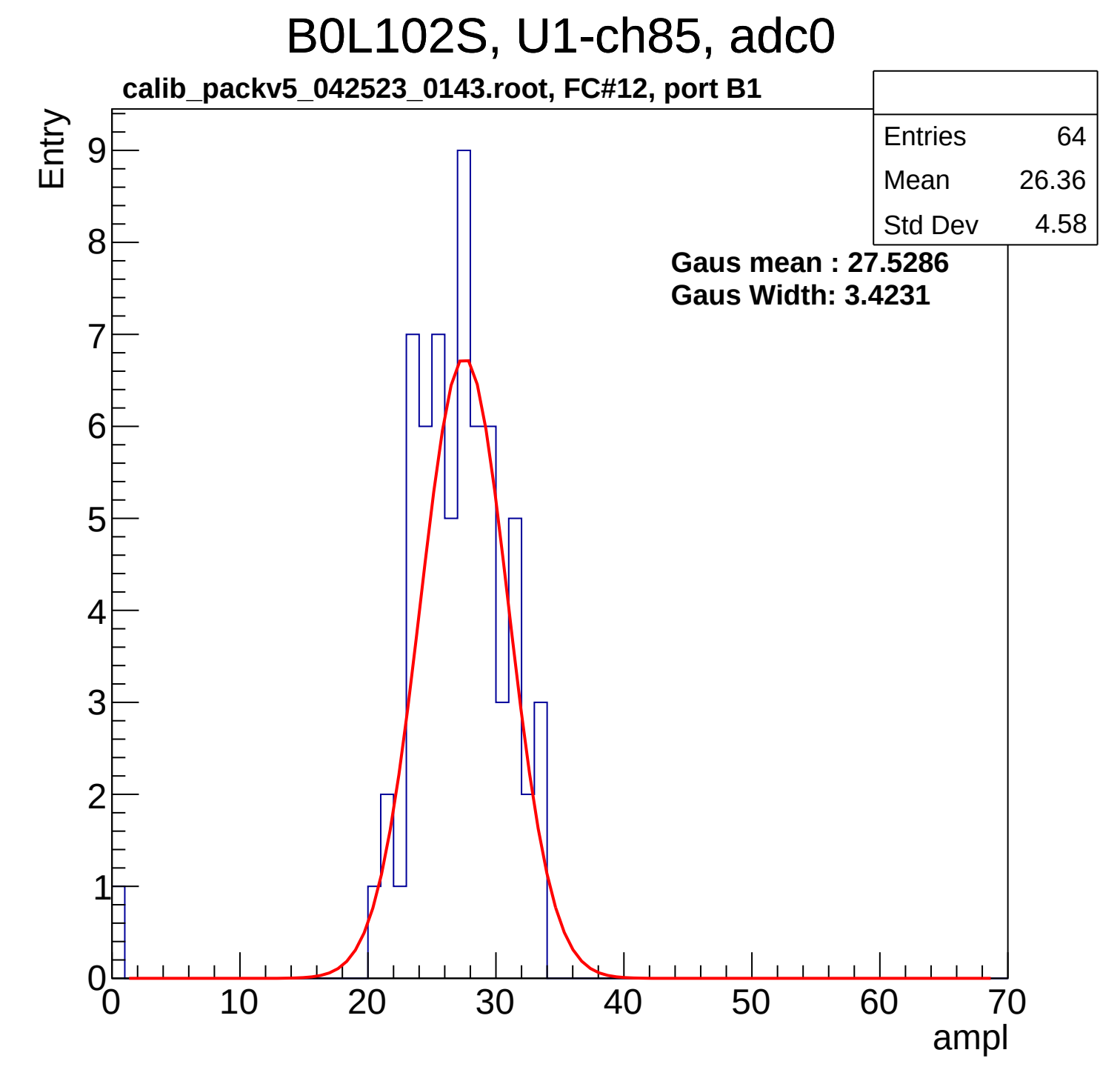
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch85, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 26.36
Std Dev 4.58
Gaus mean : 27.5286
Gaus Width: 3.4231
Entry
ampl
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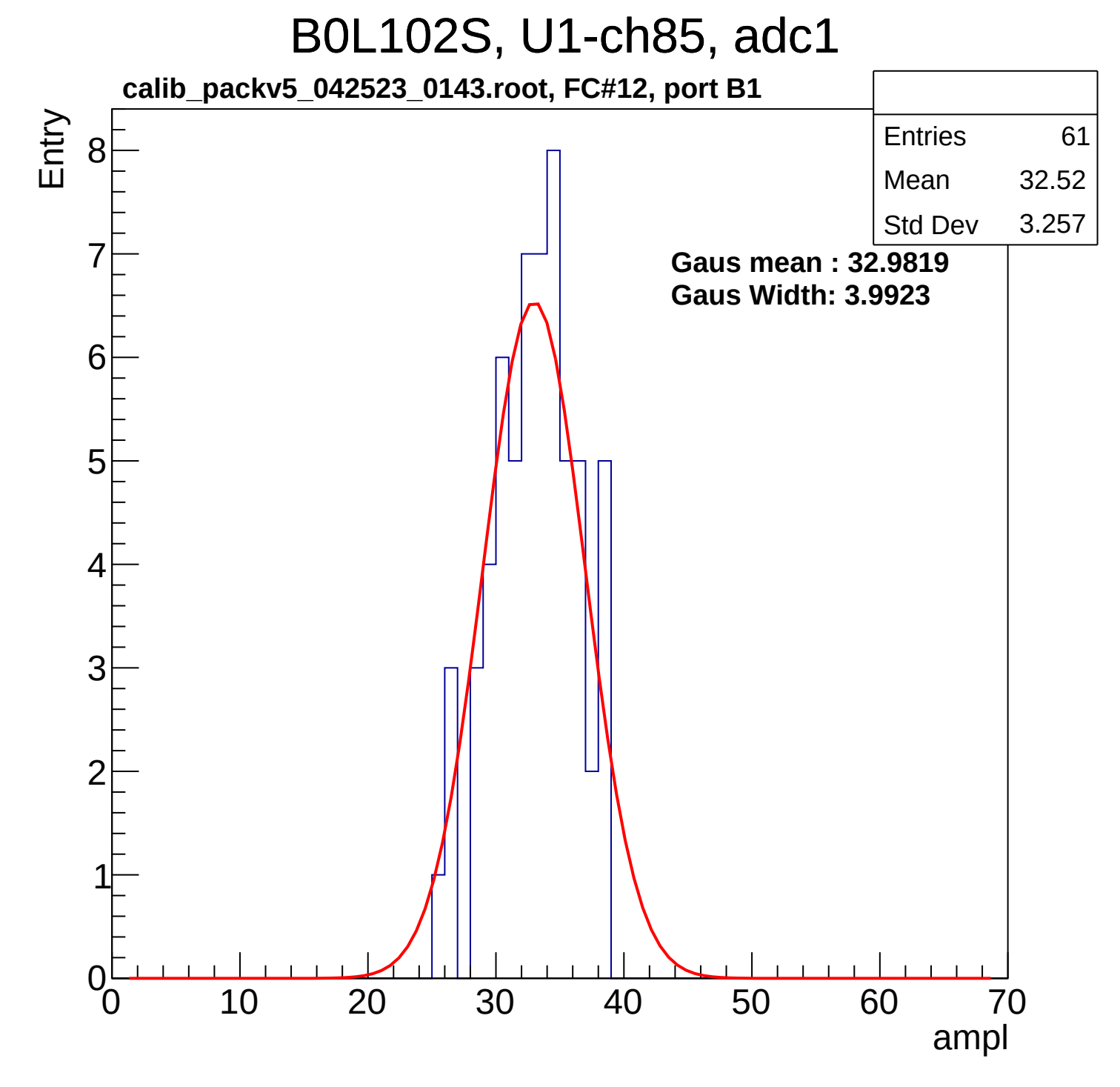
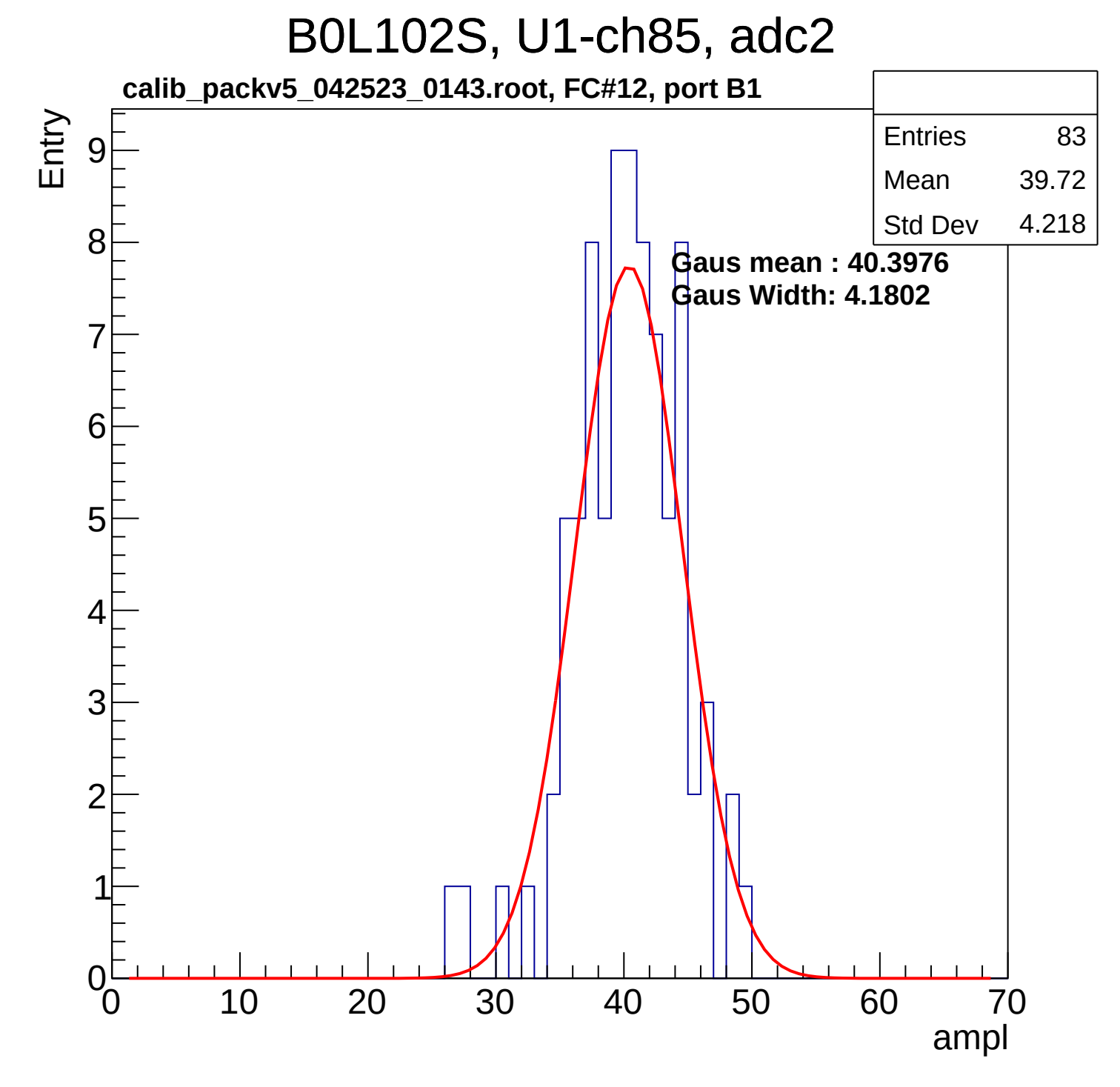
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch85, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 32.52
Std Dev 3.257
Gaus mean : 32.9819
Gaus Width: 3.9923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch85, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 39.72
Std Dev 4.218
Gaus mean : 40.3976
Gaus Width: 4.1802
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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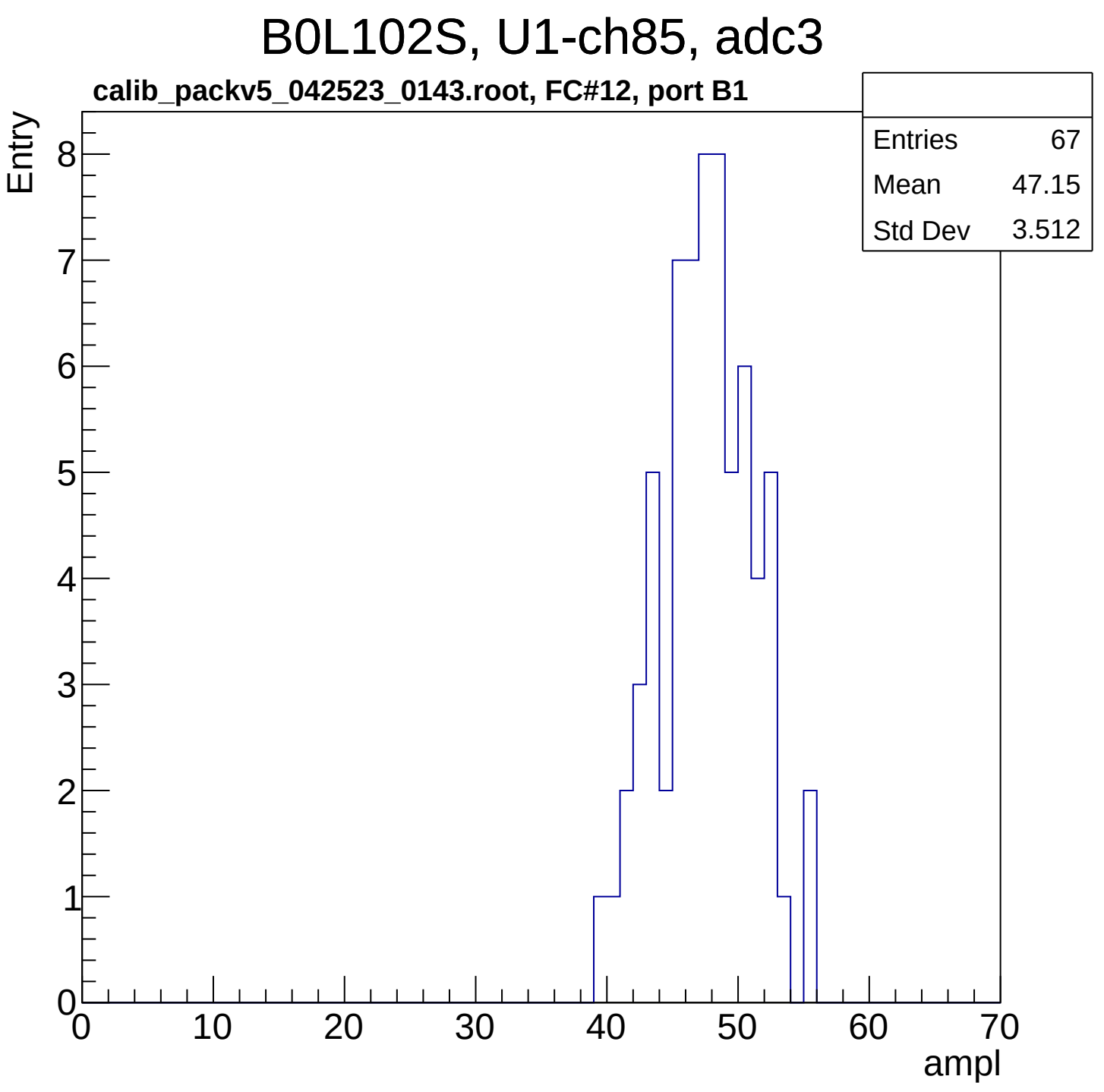
B0L102S, U1-ch85, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 47.15
Std Dev 3.512
Entry
ampl
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1
2
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0
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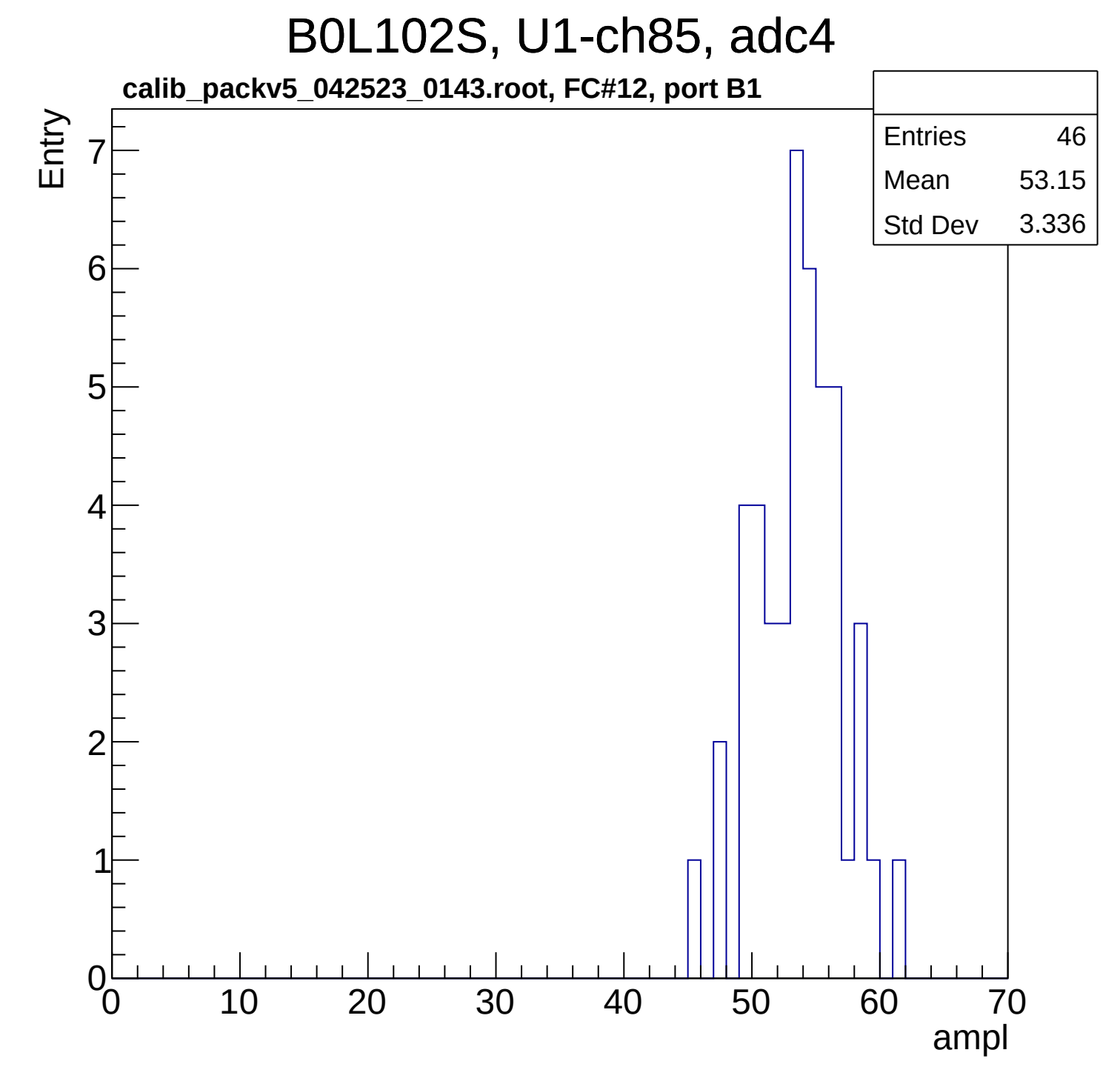
B0L102S, U1-ch85, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 53.15
Std Dev 3.336
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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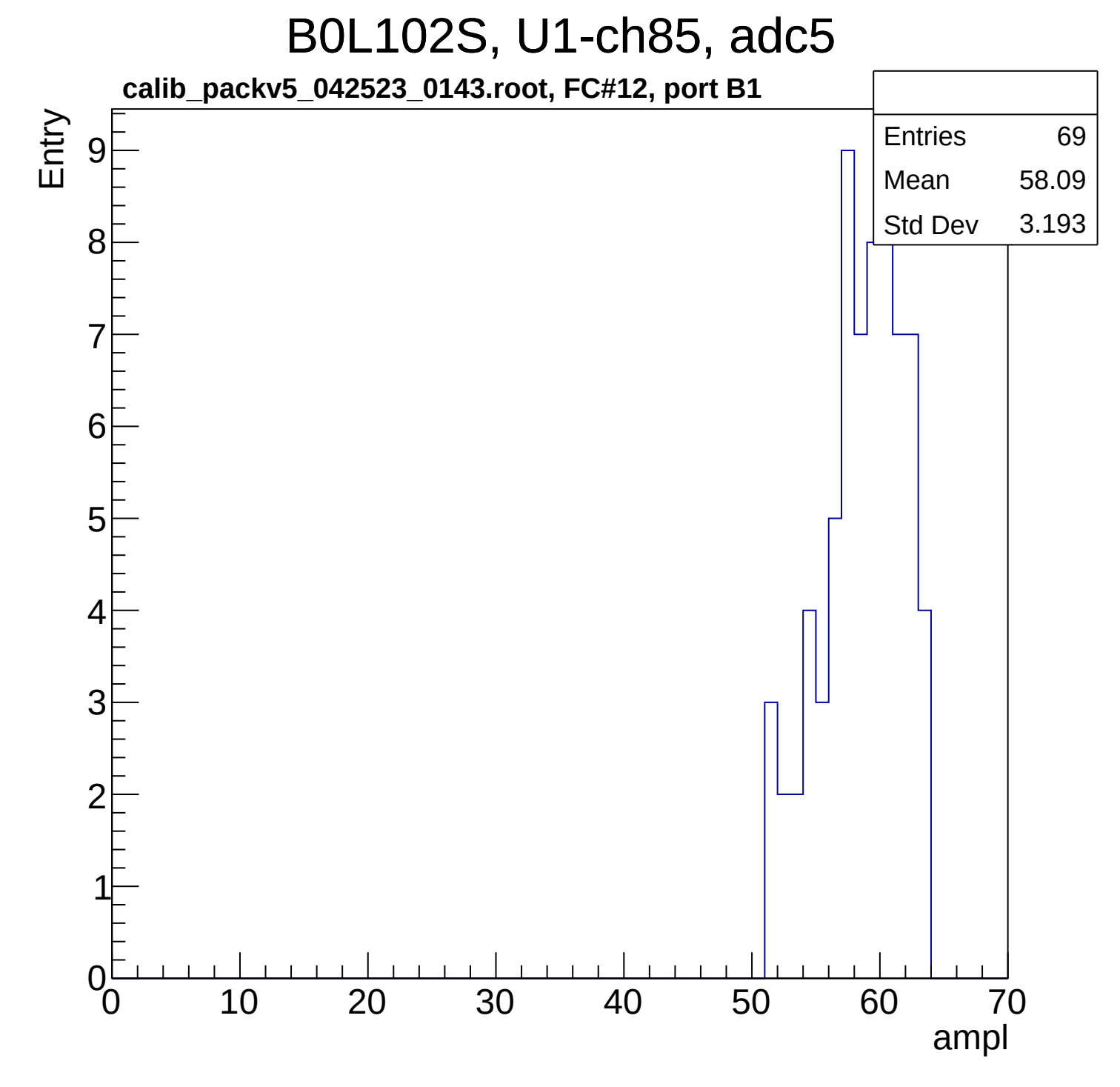
B0L102S, U1-ch85, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 58.09
Std Dev 3.193
Entry
ampl
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0
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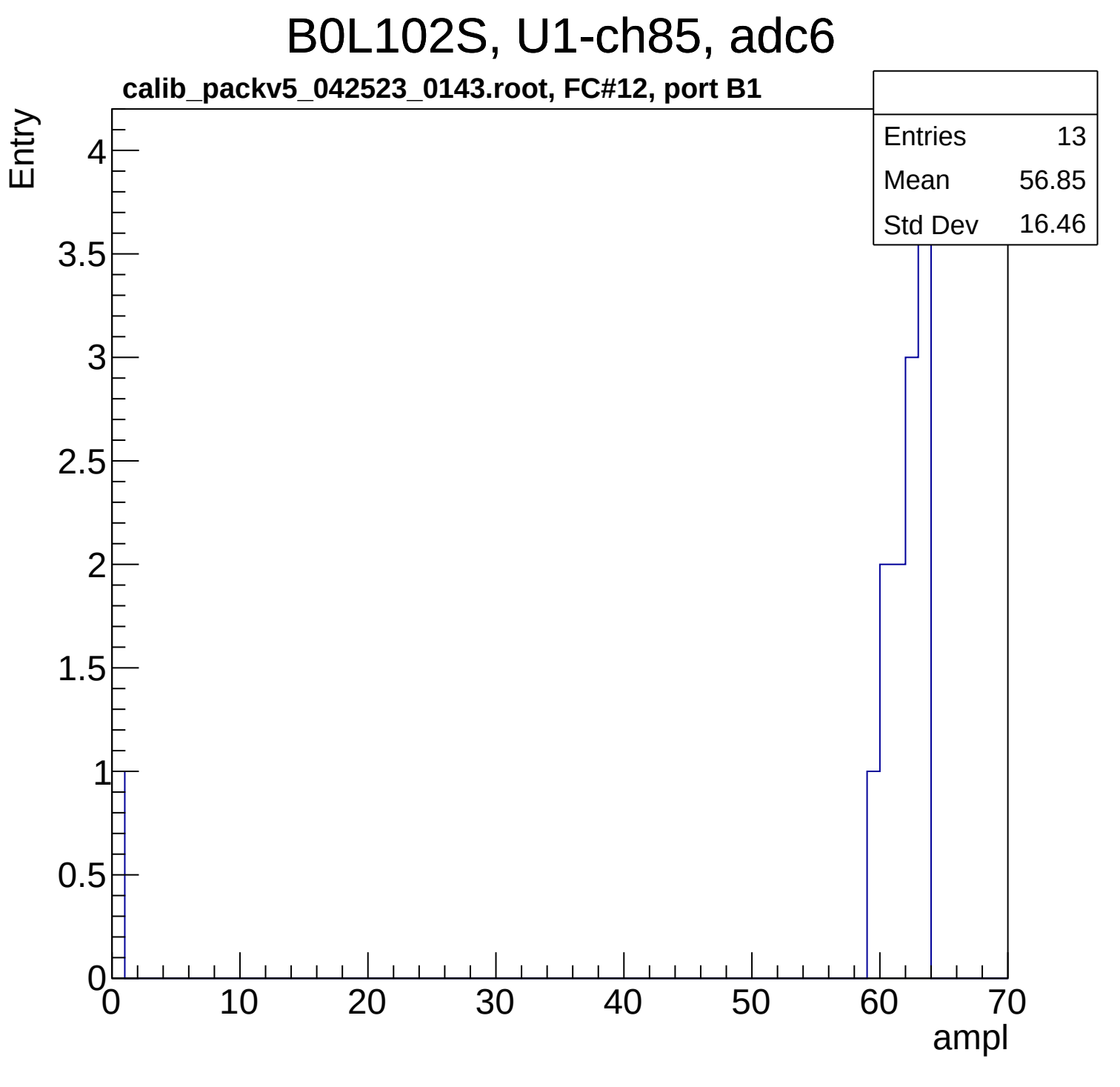
B0L102S, U1-ch85, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.85
Std Dev 16.46
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
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40
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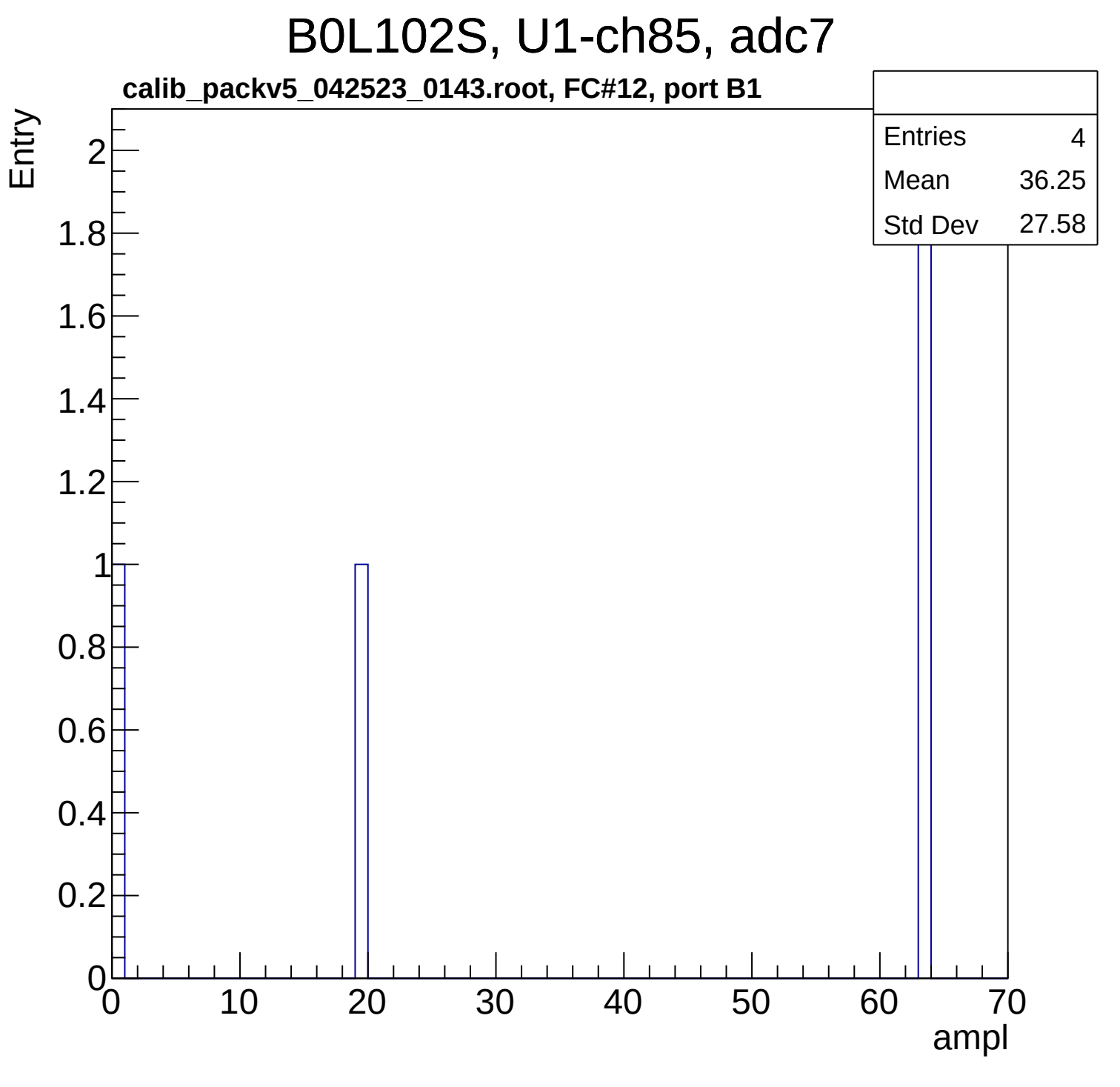
B0L102S, U1-ch85, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 36.25
Std Dev 27.58
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
0
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60
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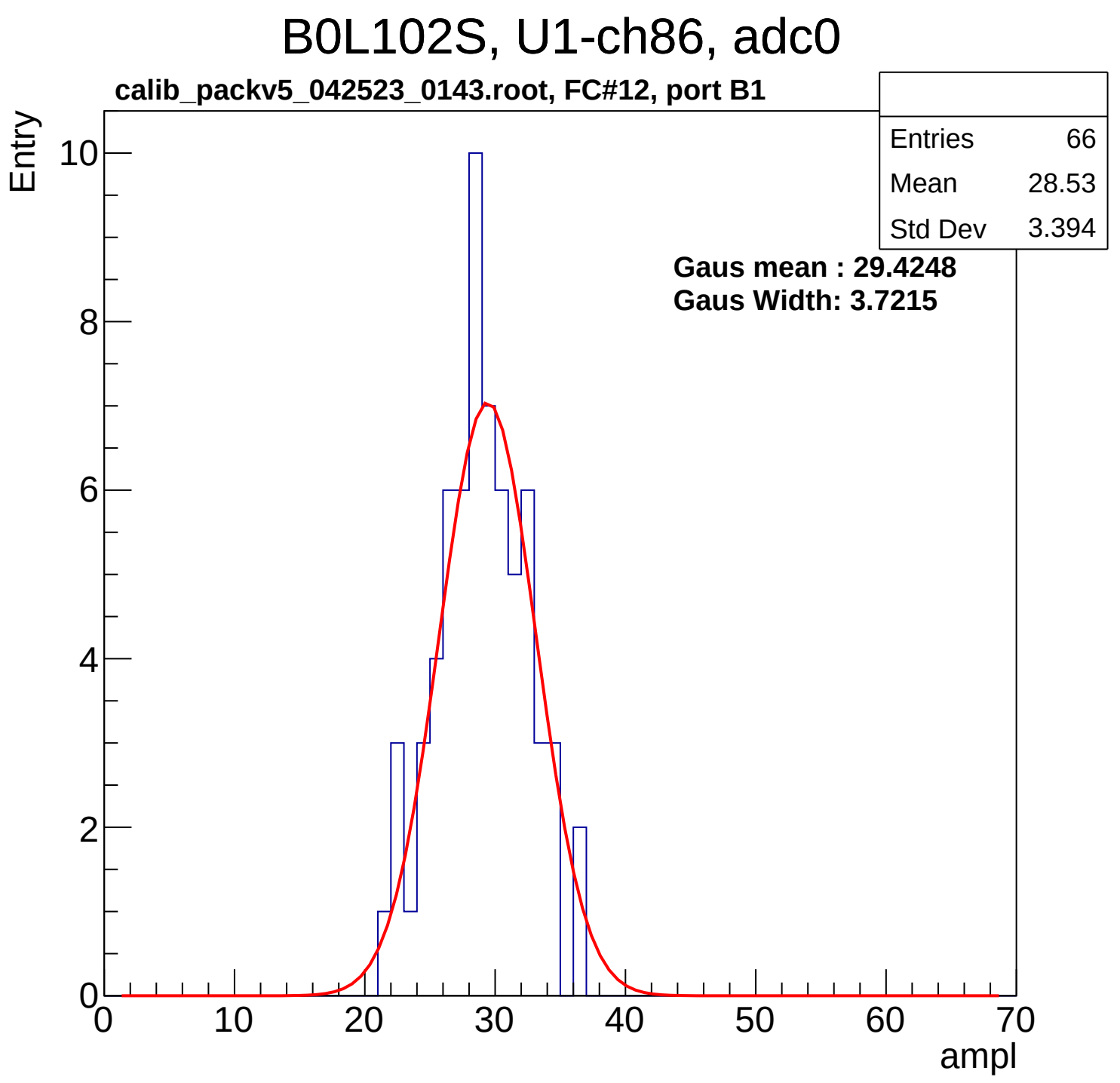
B0L102S, U1-ch86, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 28.53
Std Dev 3.394
Gaus mean : 29.4248
Gaus Width: 3.7215
Entry
ampl
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6
8
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0
10
20
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60
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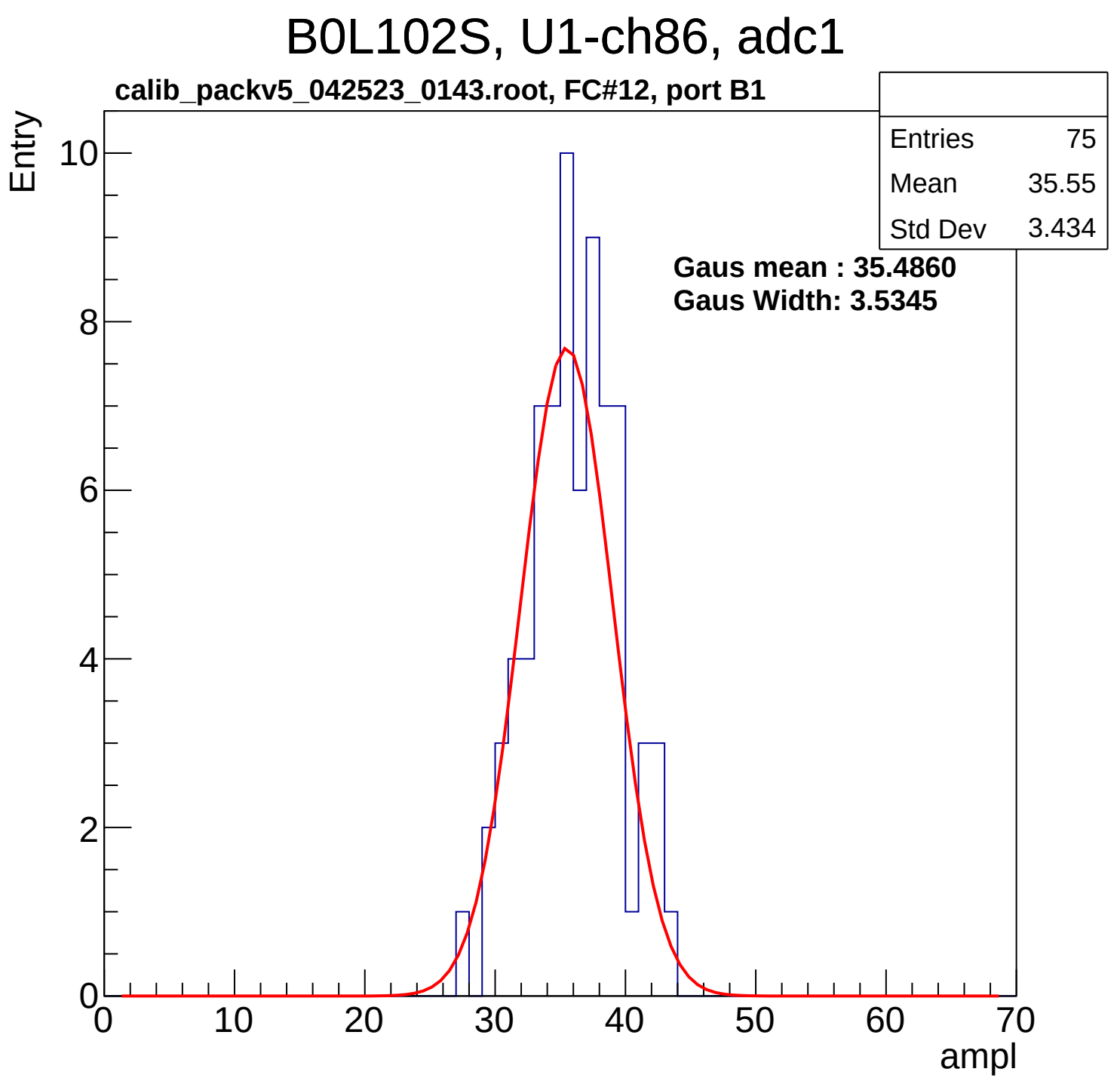
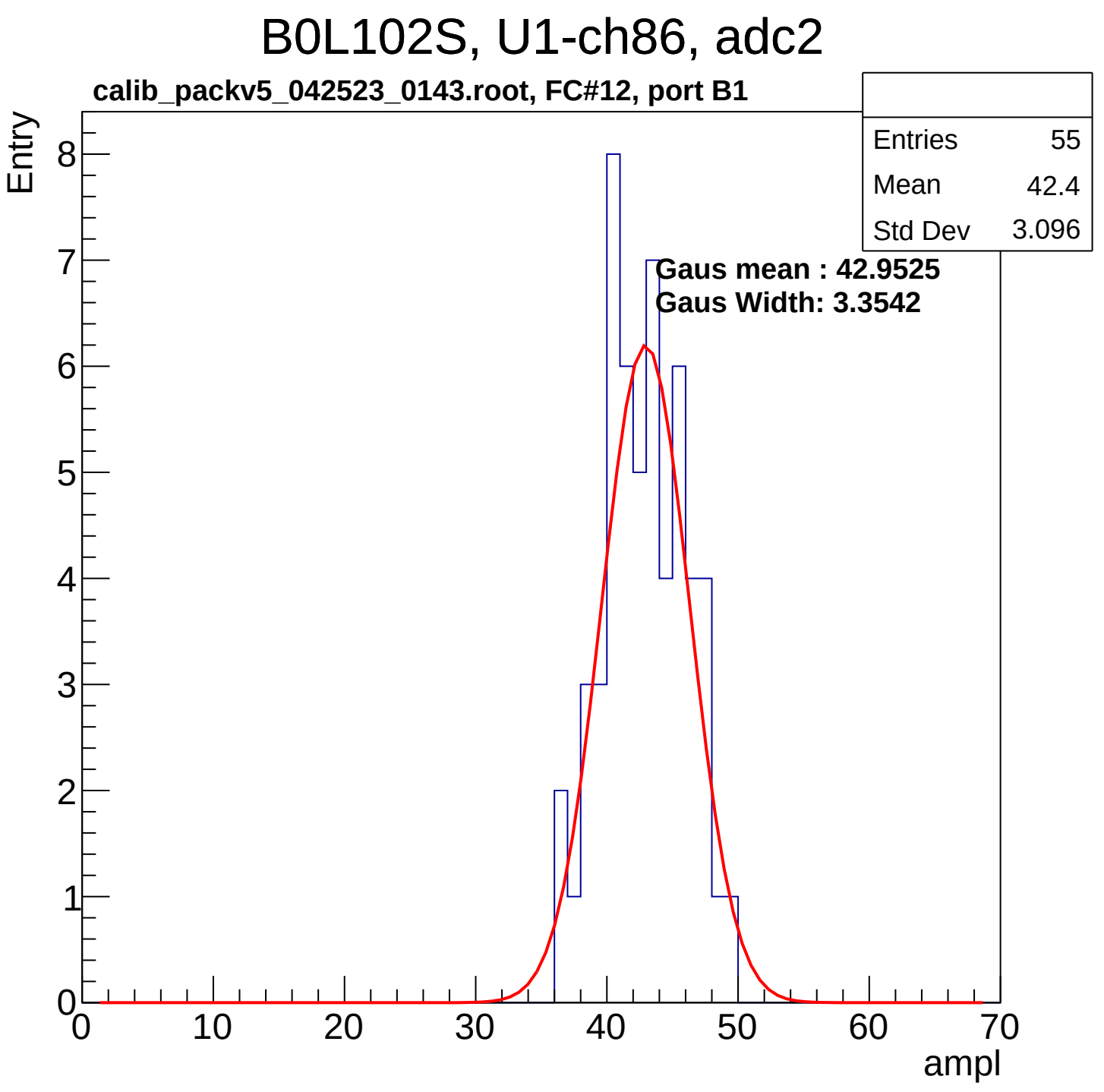

B0L102S, U1-ch86, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 35.55
Std Dev 3.434
Gaus mean : 35.4860
Gaus Width: 3.5345
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch86, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 42.4
Std Dev 3.096
Gaus mean : 42.9525
Gaus Width: 3.3542
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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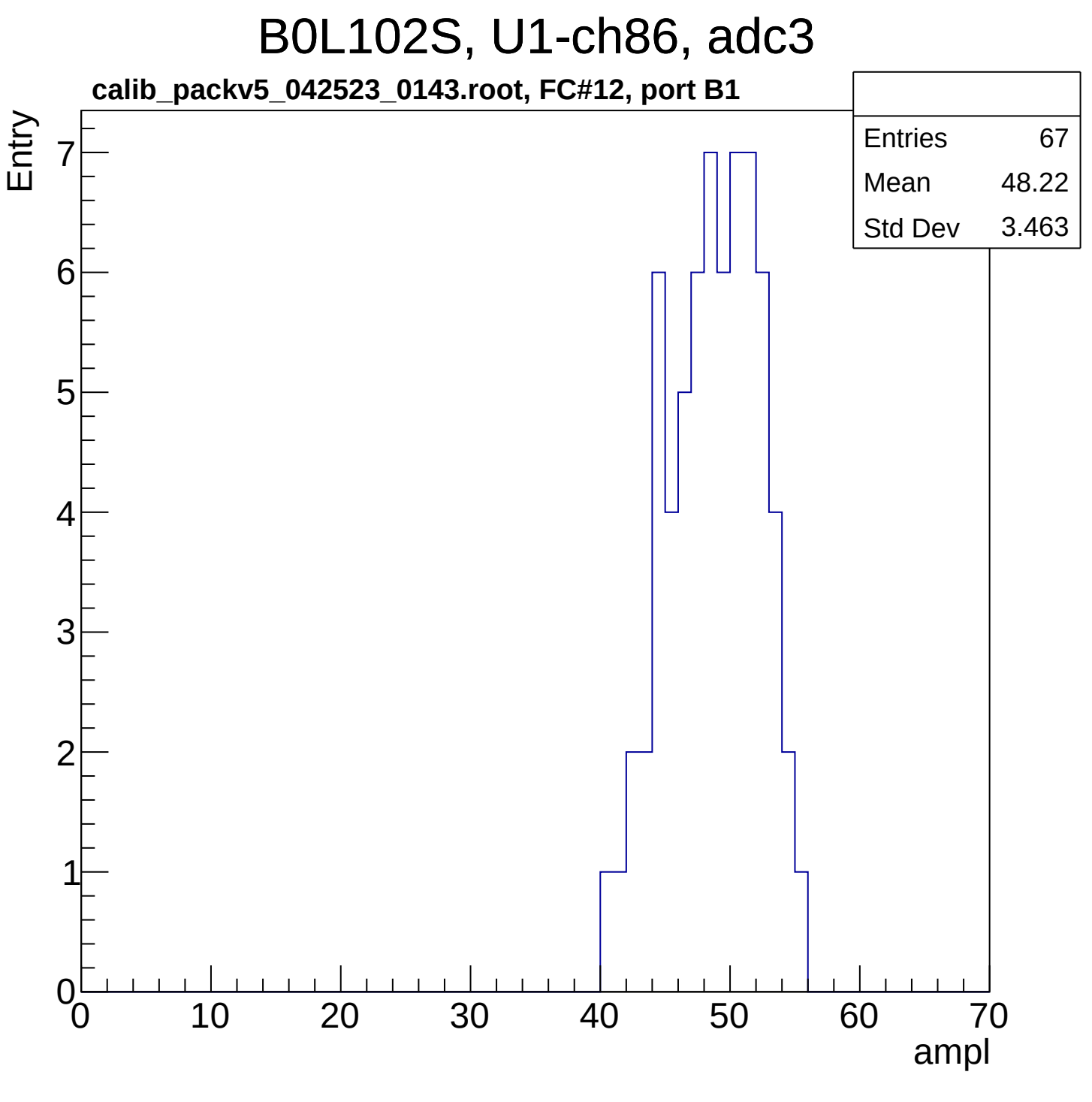
B0L102S, U1-ch86, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 48.22
Std Dev 3.463
Entry
ampl
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3
4
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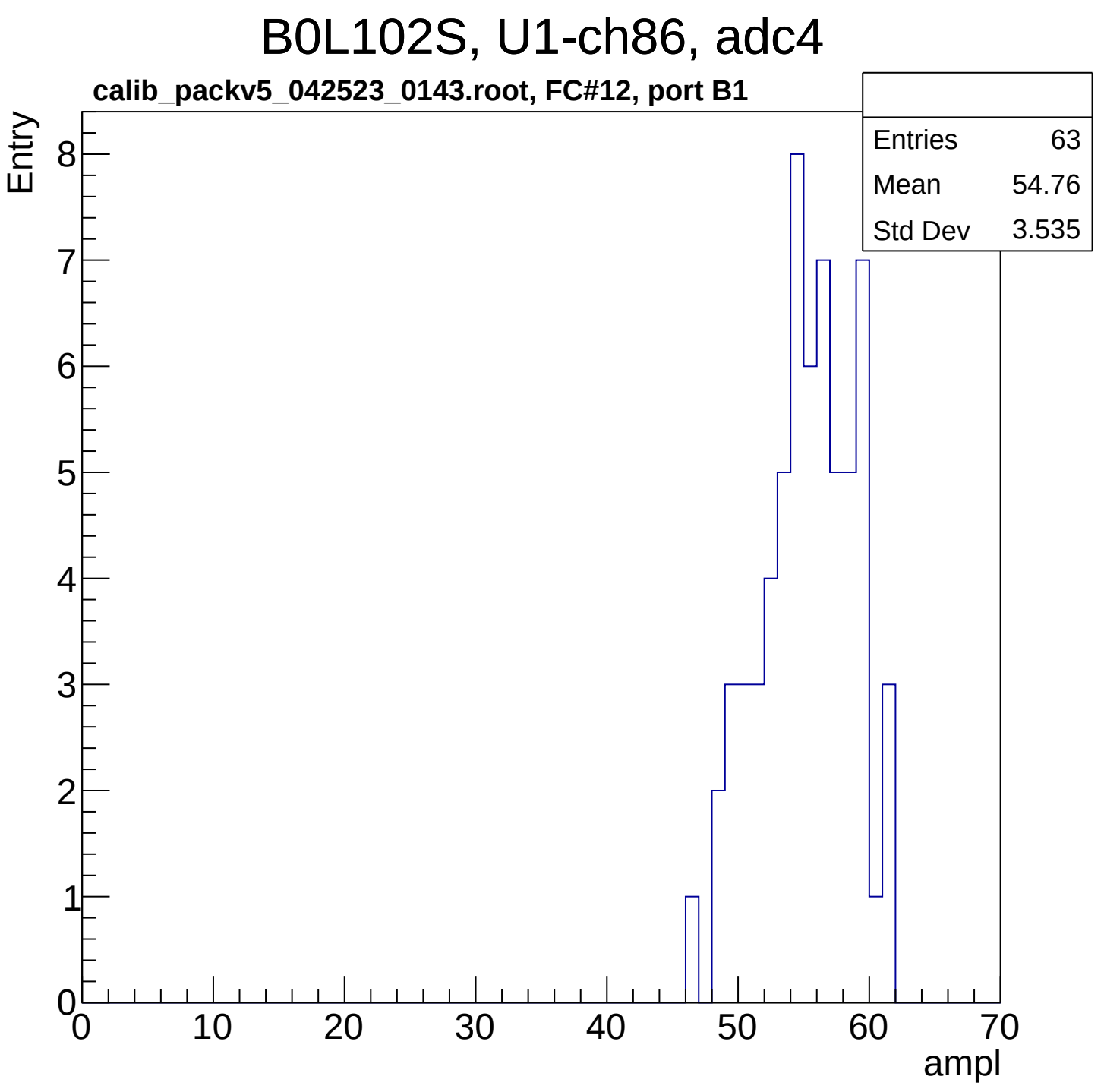
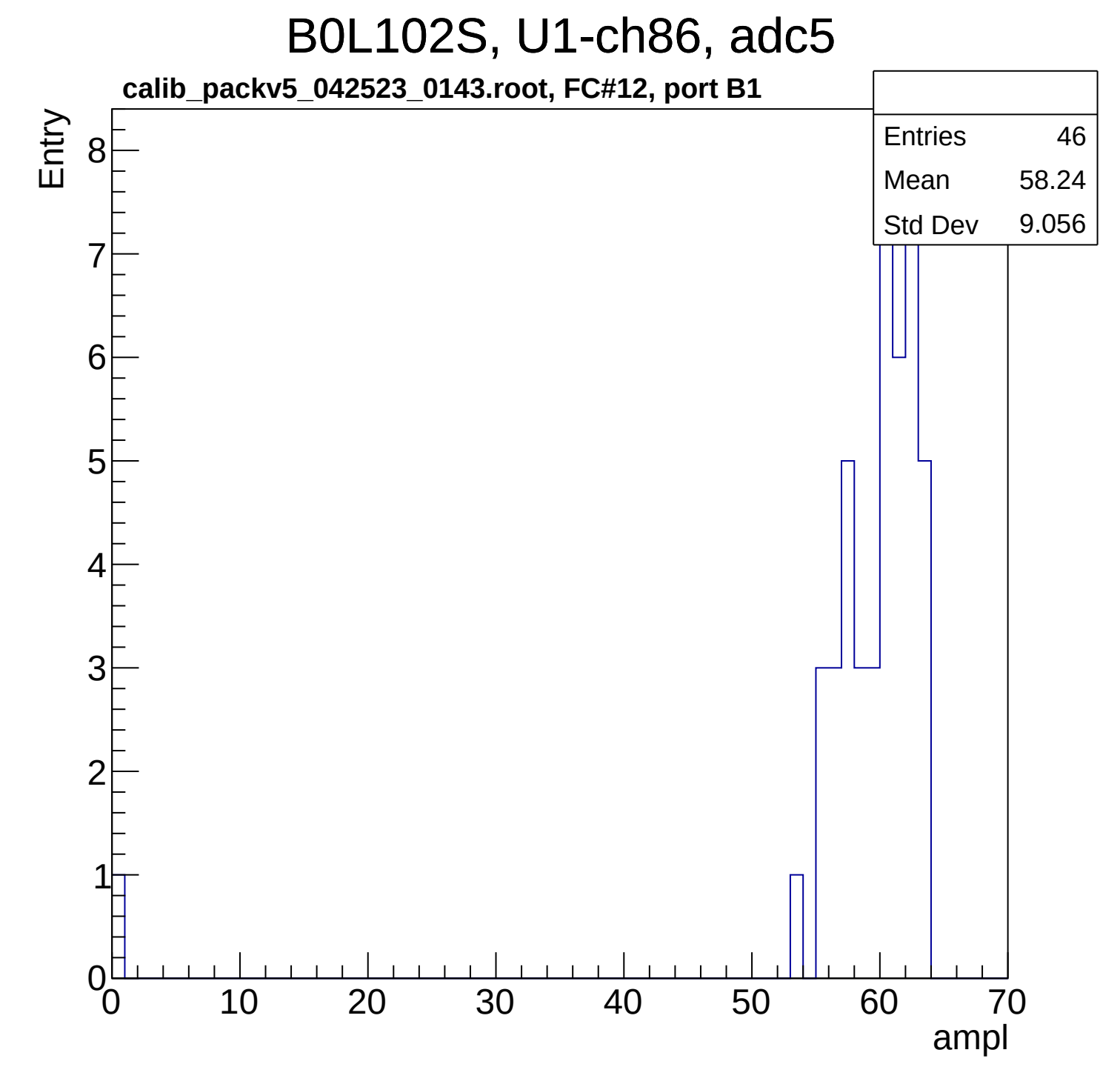

B0L102S, U1-ch86, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54.76
Std Dev 3.535
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch86, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.24
Std Dev 9.056
Entry
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2
3
4
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6
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8
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10
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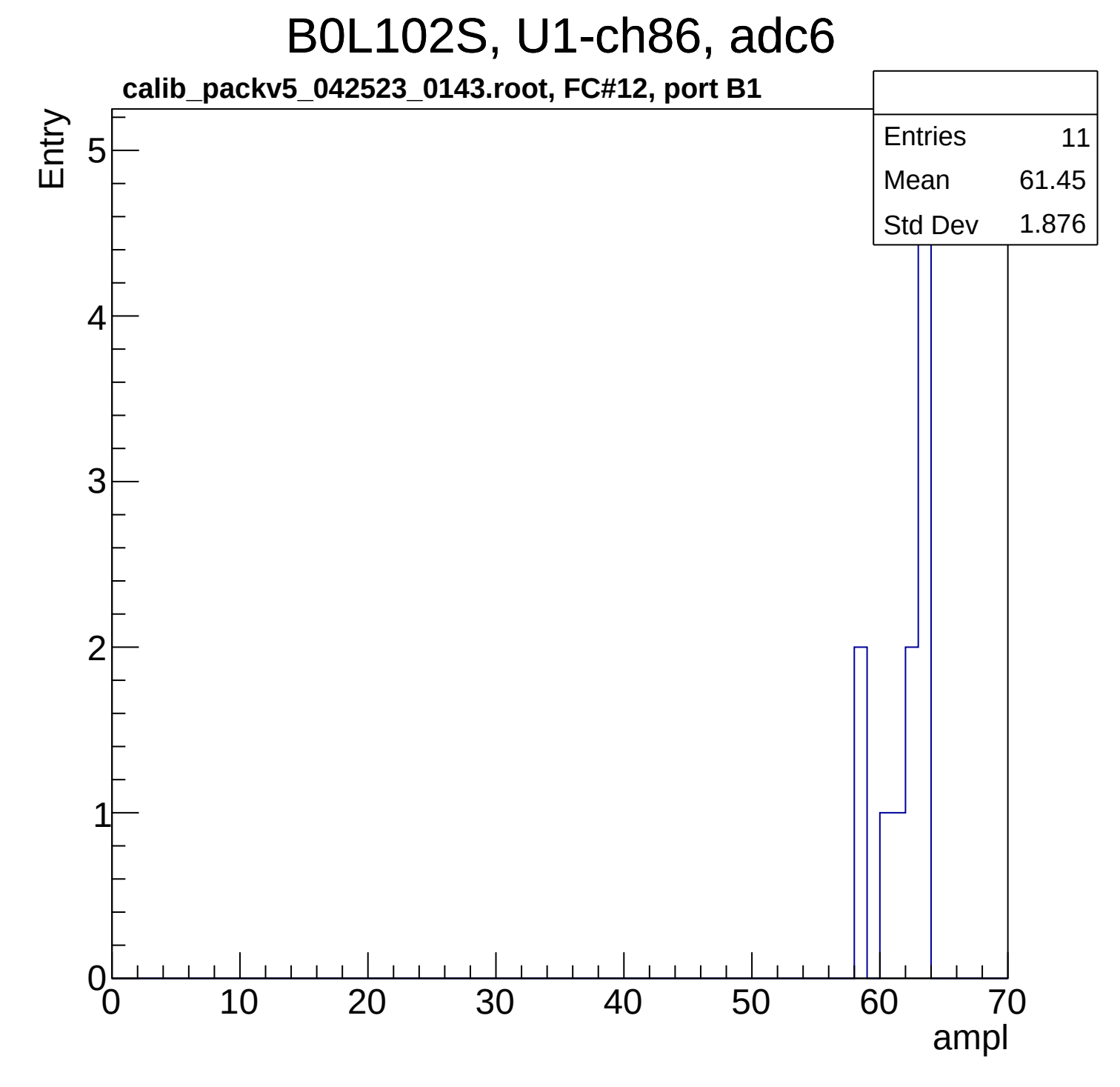
70
ampl


B0L102S, U1-ch86, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61.45
Std Dev 1.876
Entry
0
1
2
3
4
5
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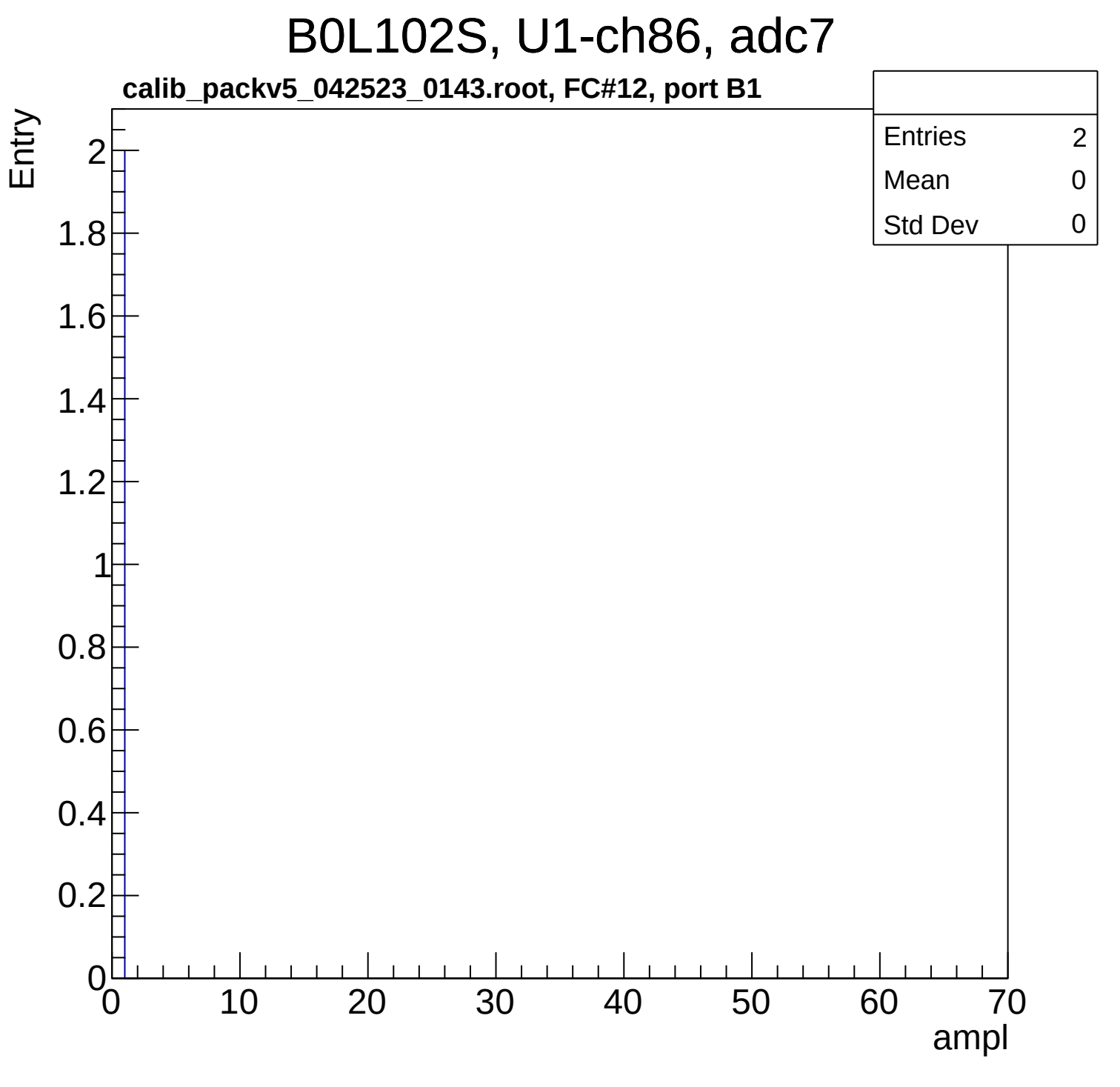
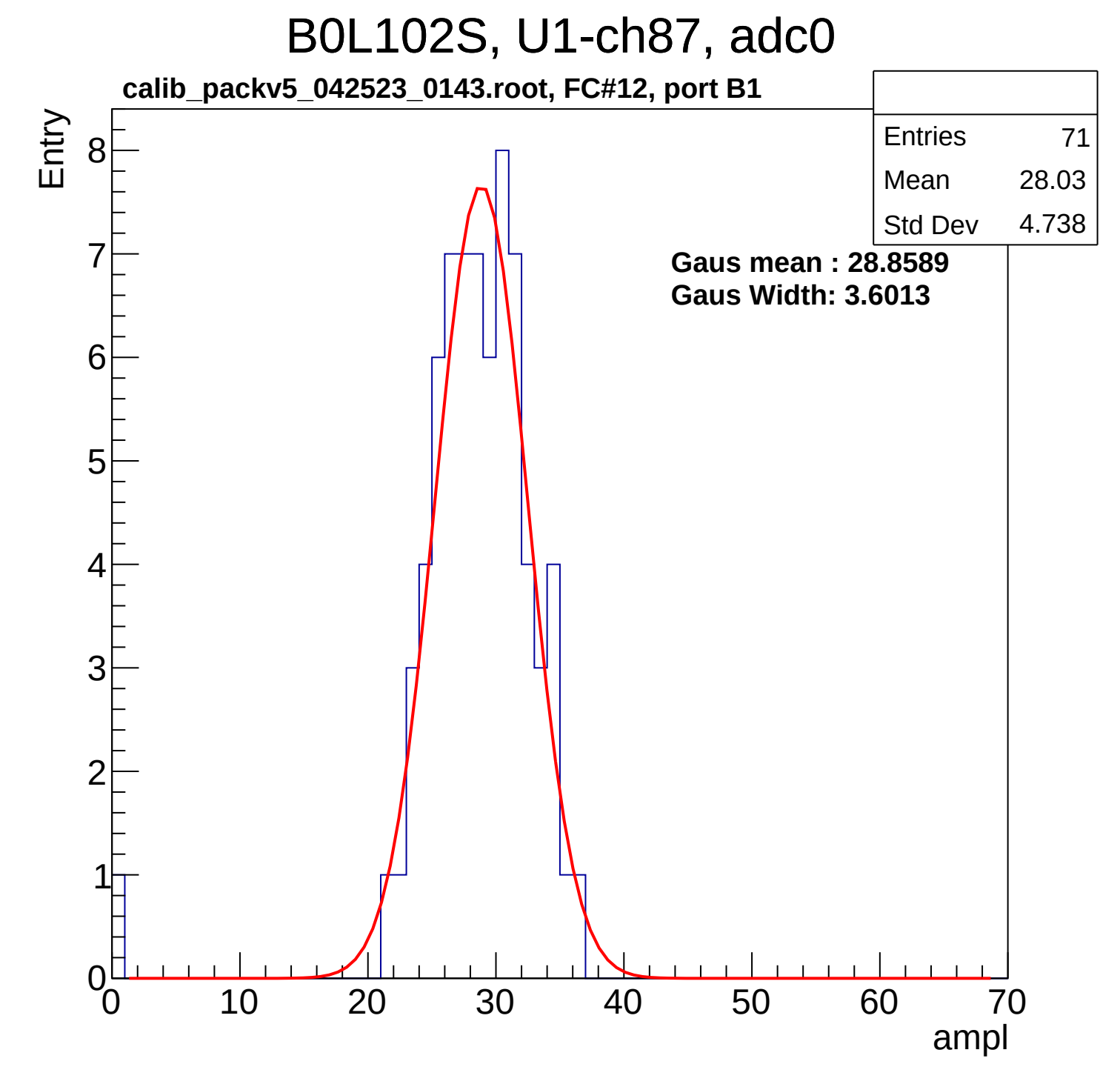
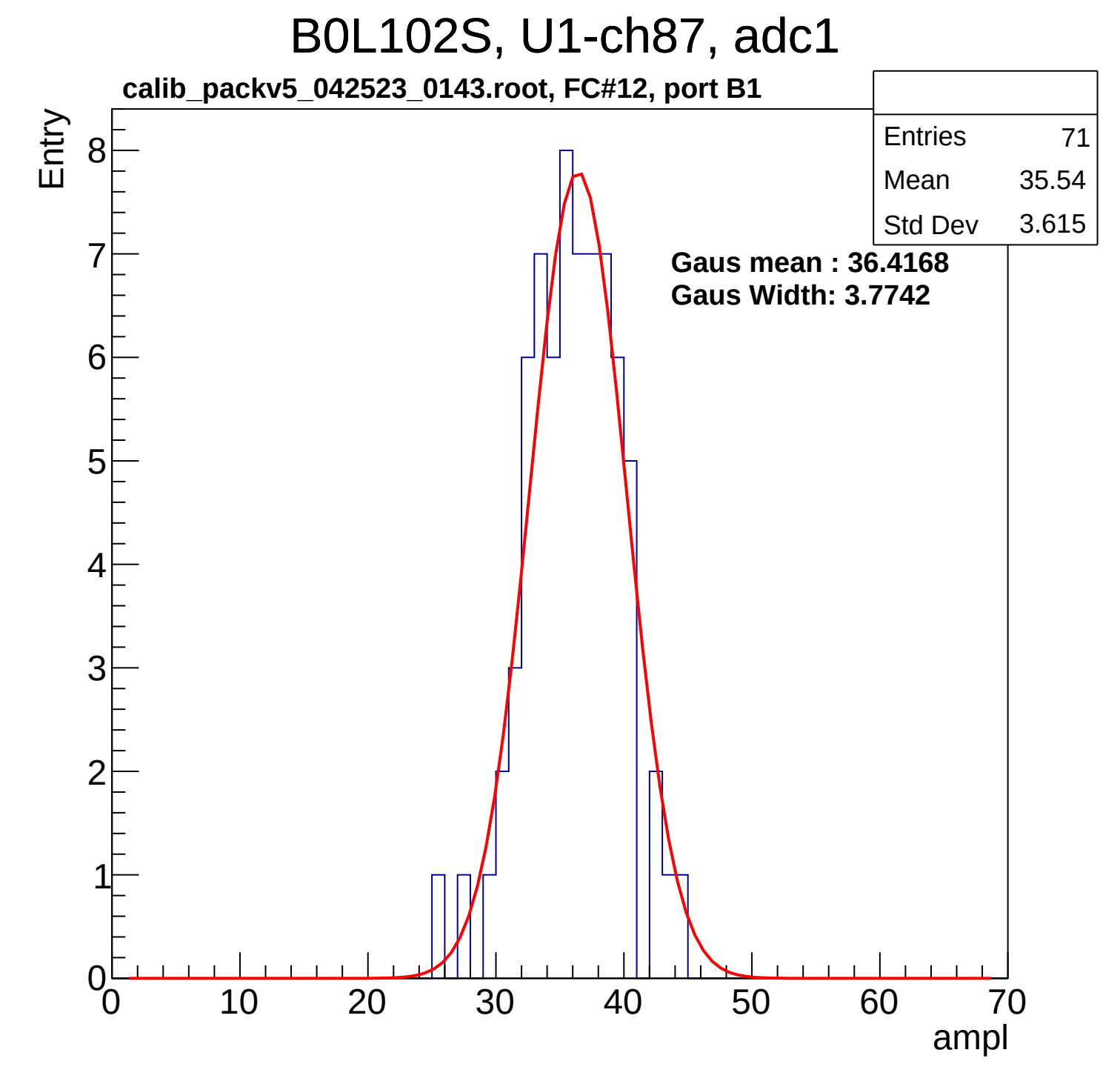
50
60
70
ampl


B0L102S, U1-ch86, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch87, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 28.03
Std Dev 4.738
Gaus mean : 28.8589
Gaus Width: 3.6013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch87, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 35.54
Std Dev 3.615
Gaus mean : 36.4168
Gaus Width: 3.7742
Entry
ampl
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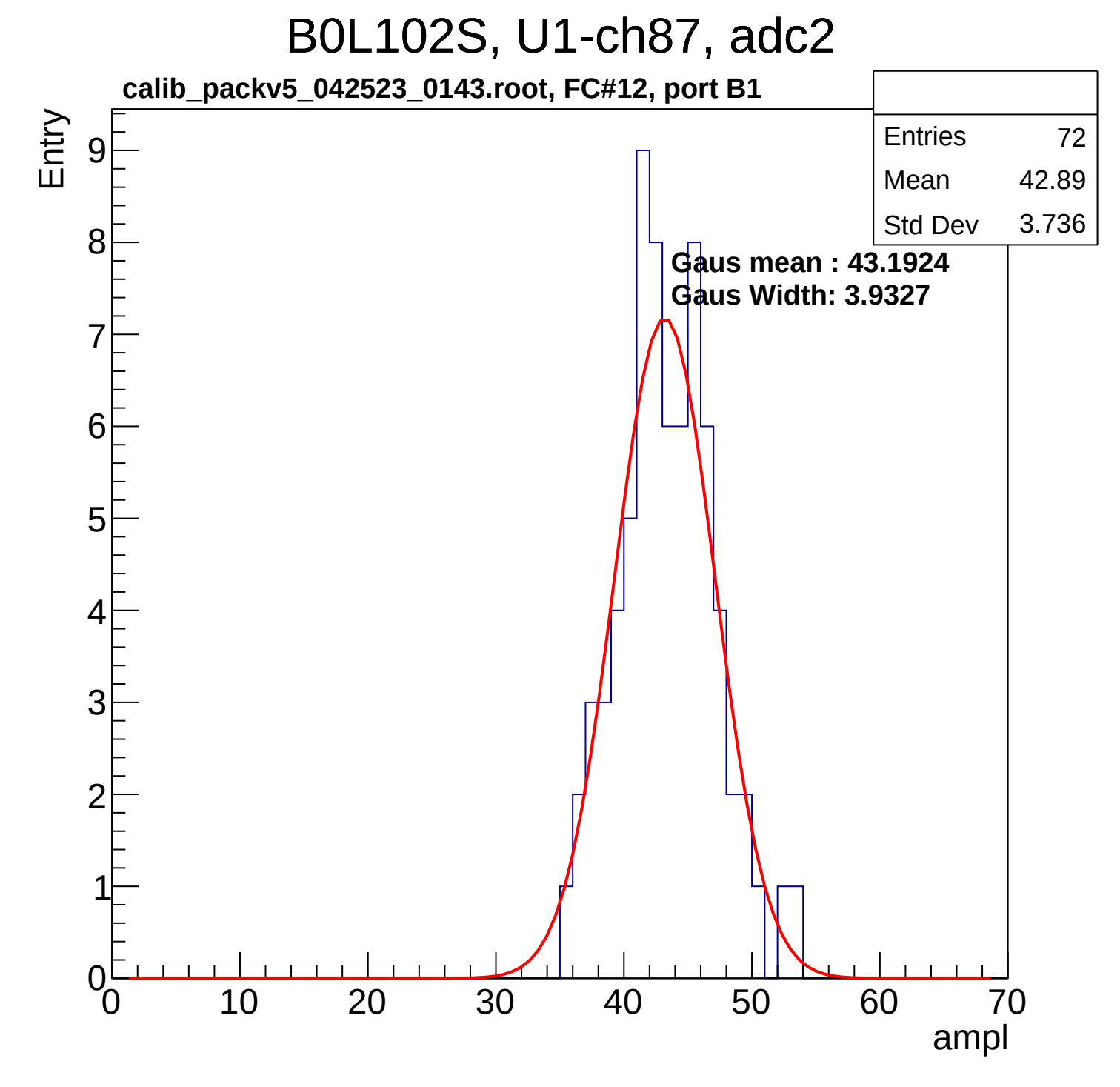
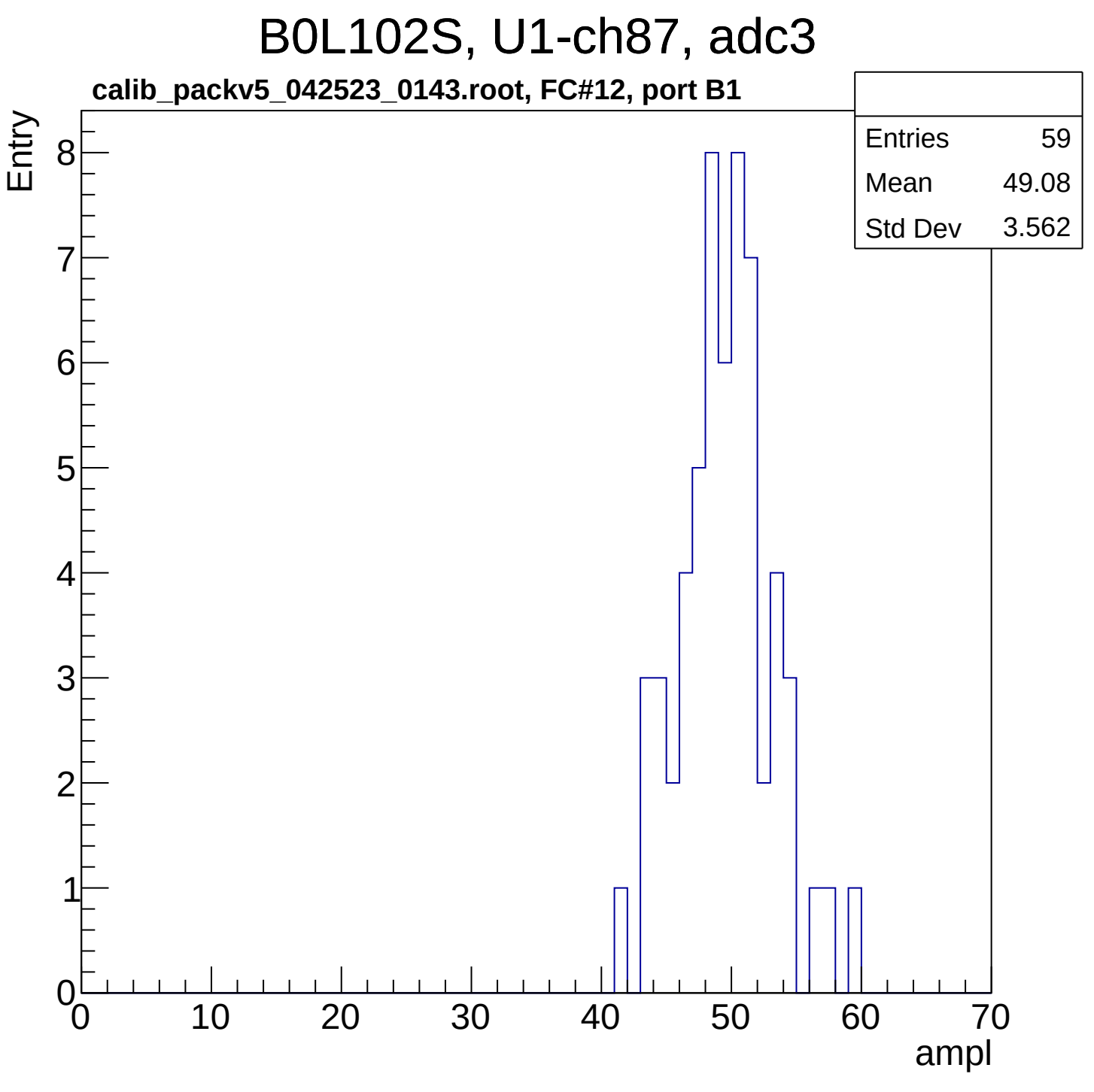
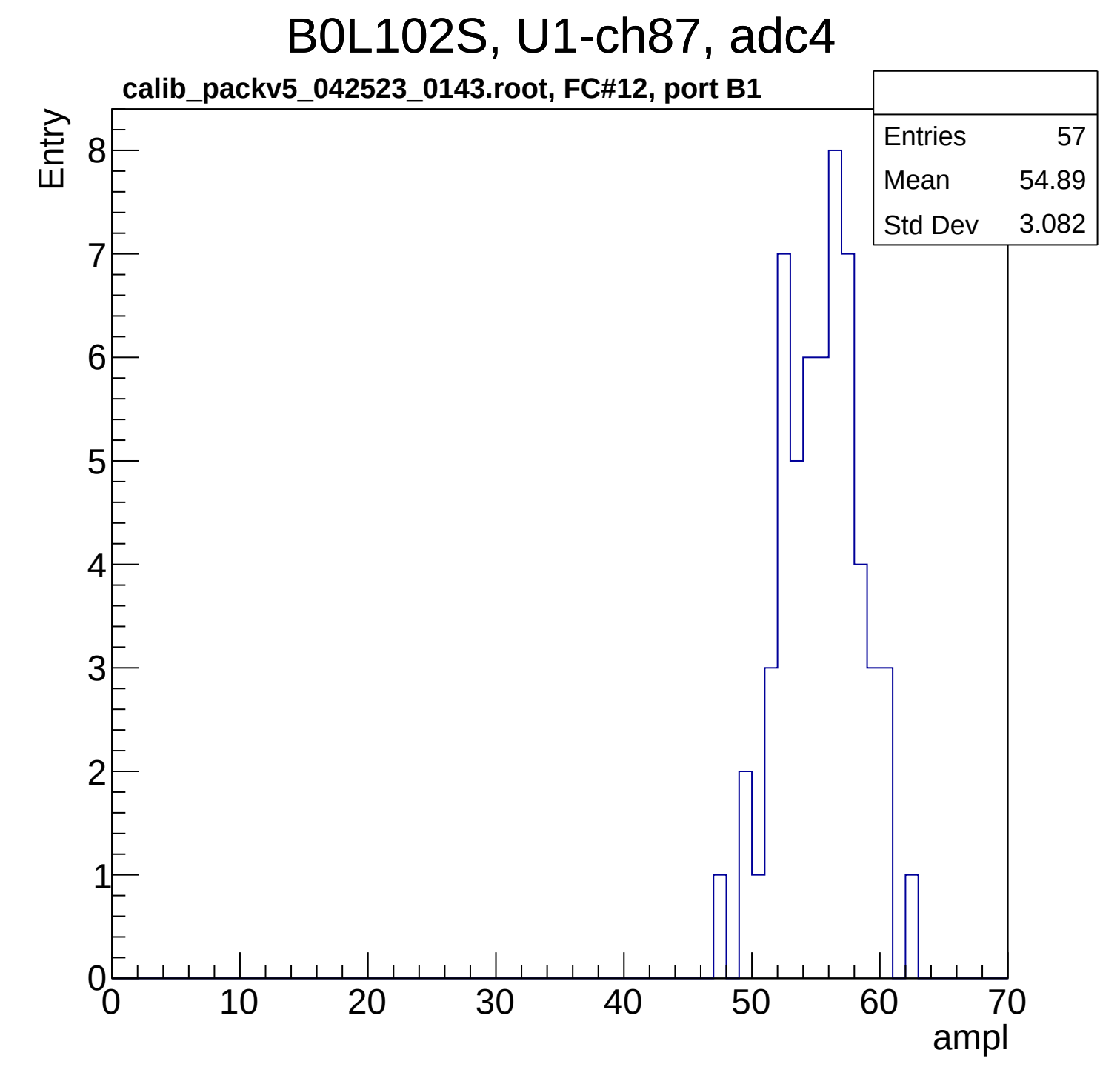
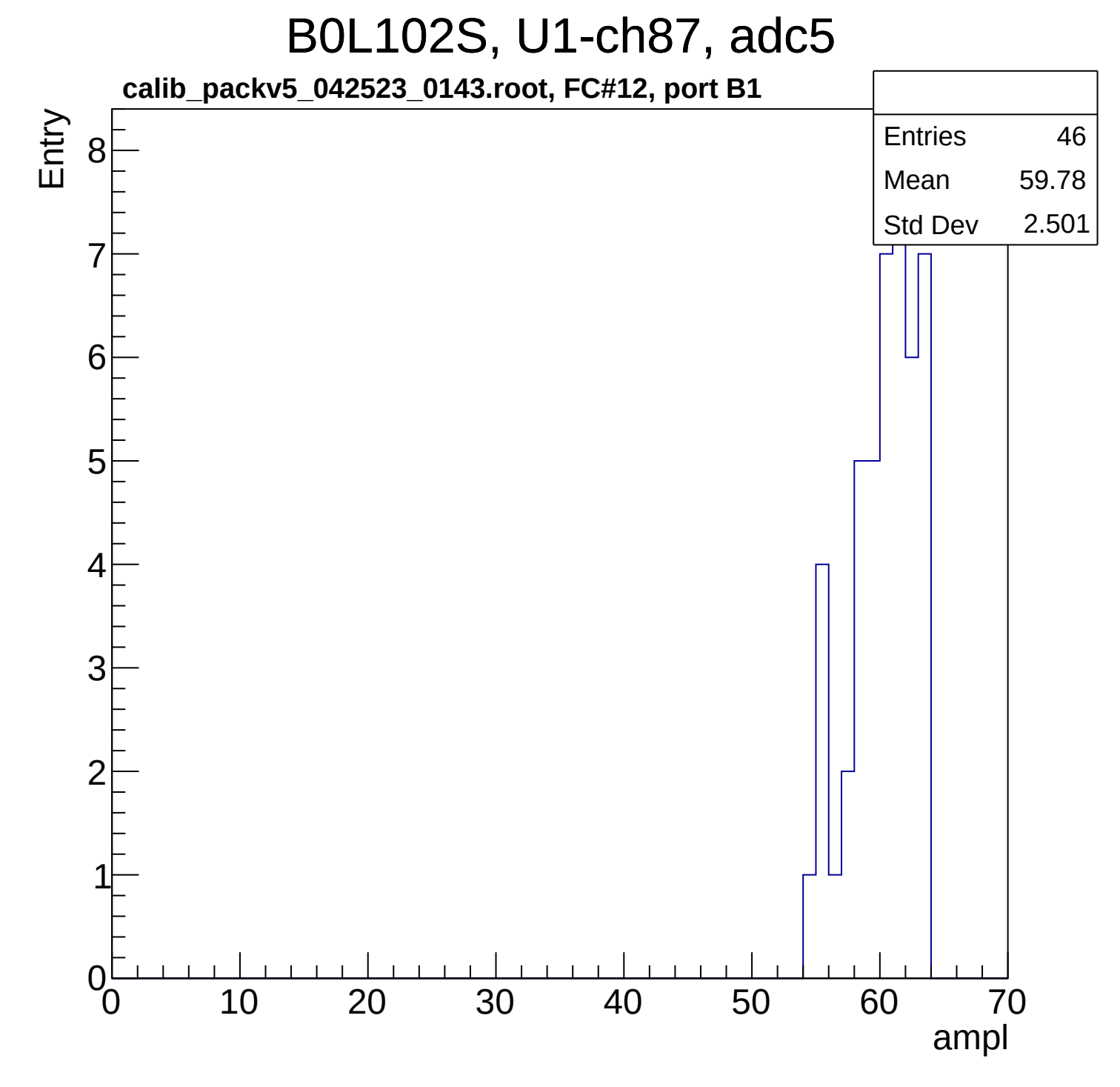
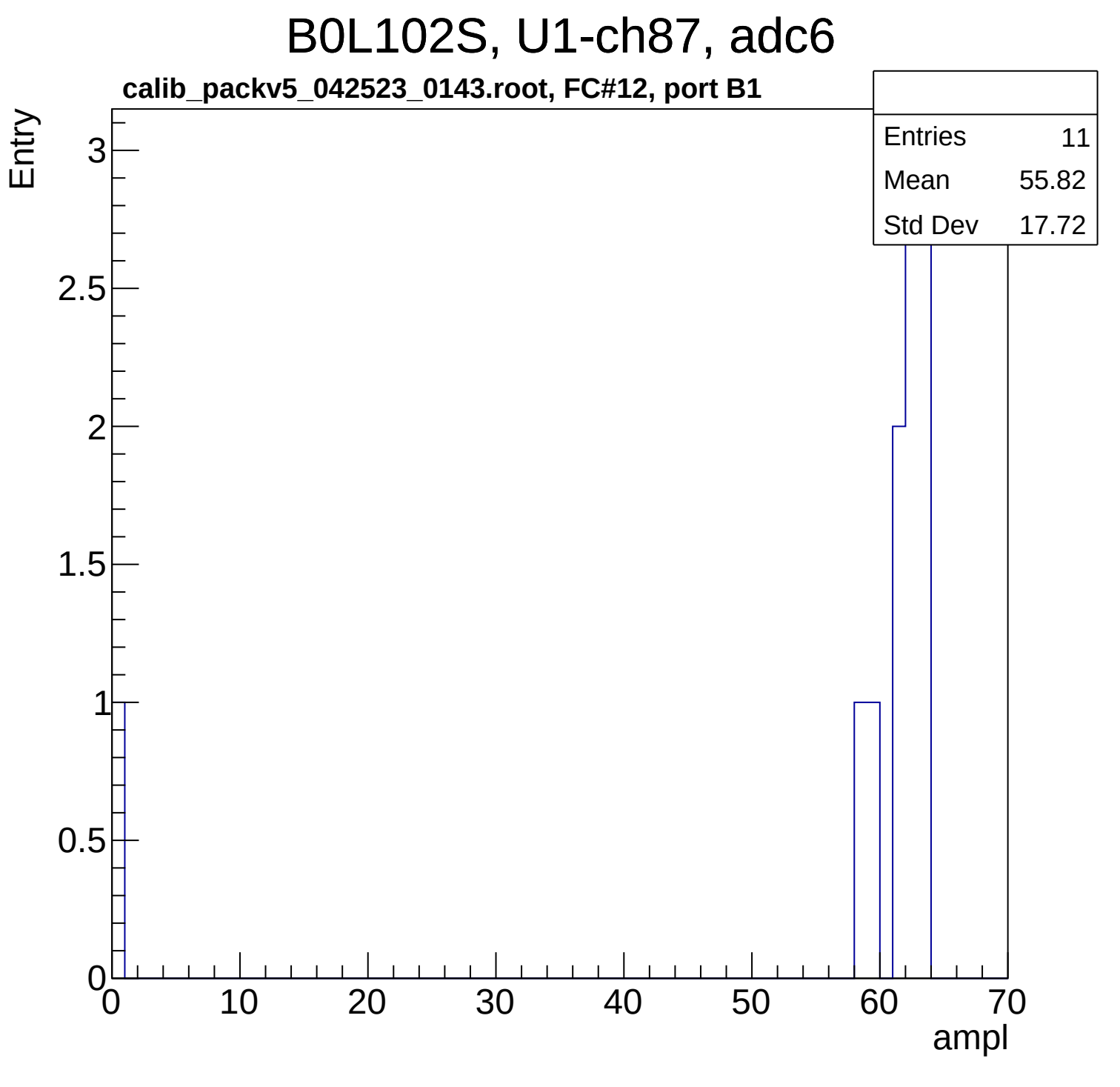
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

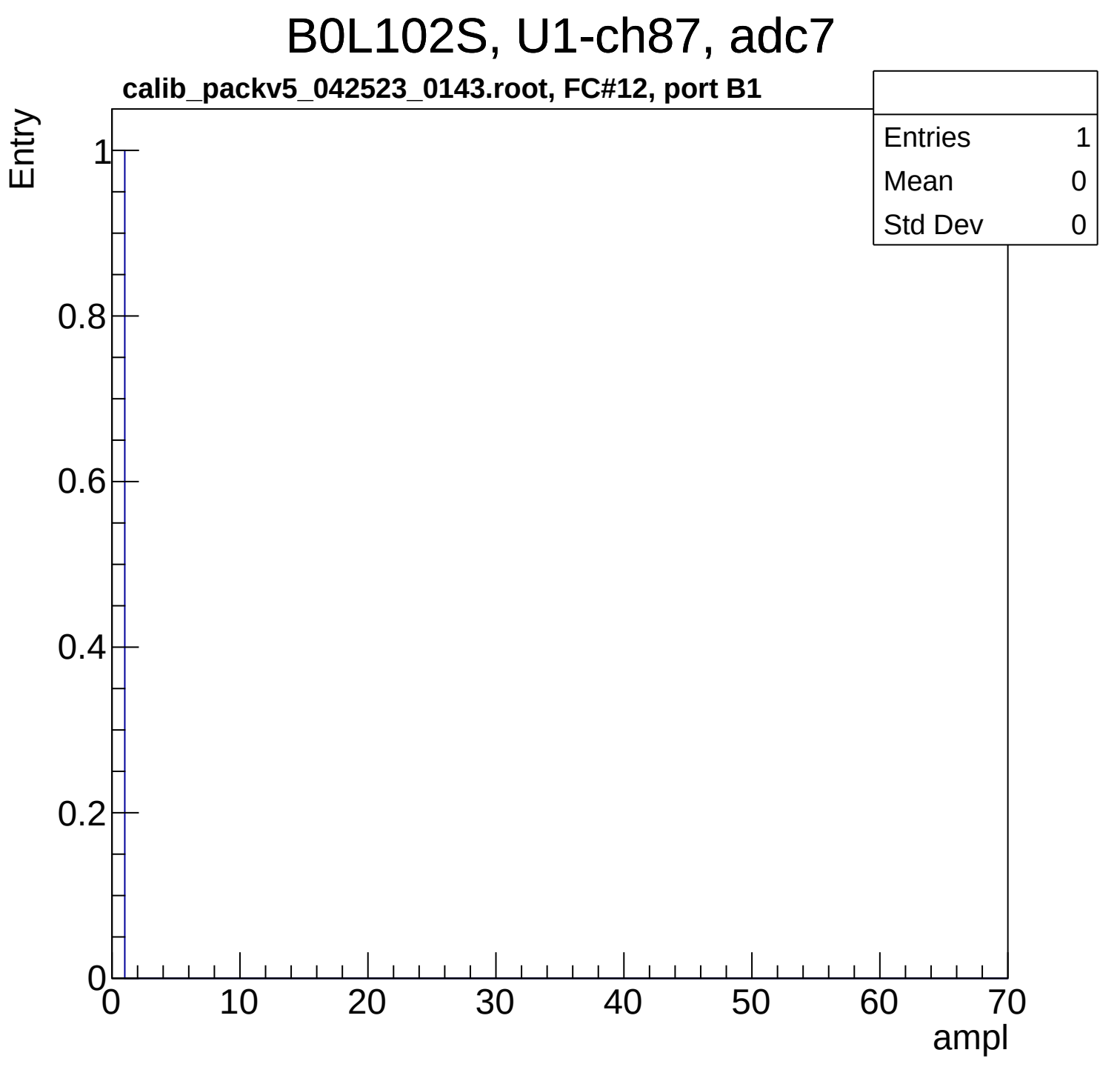

B0L102S, U1-ch87, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 42.89
Std Dev 3.736
Gaus mean : 43.1924
Gaus Width: 3.9327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

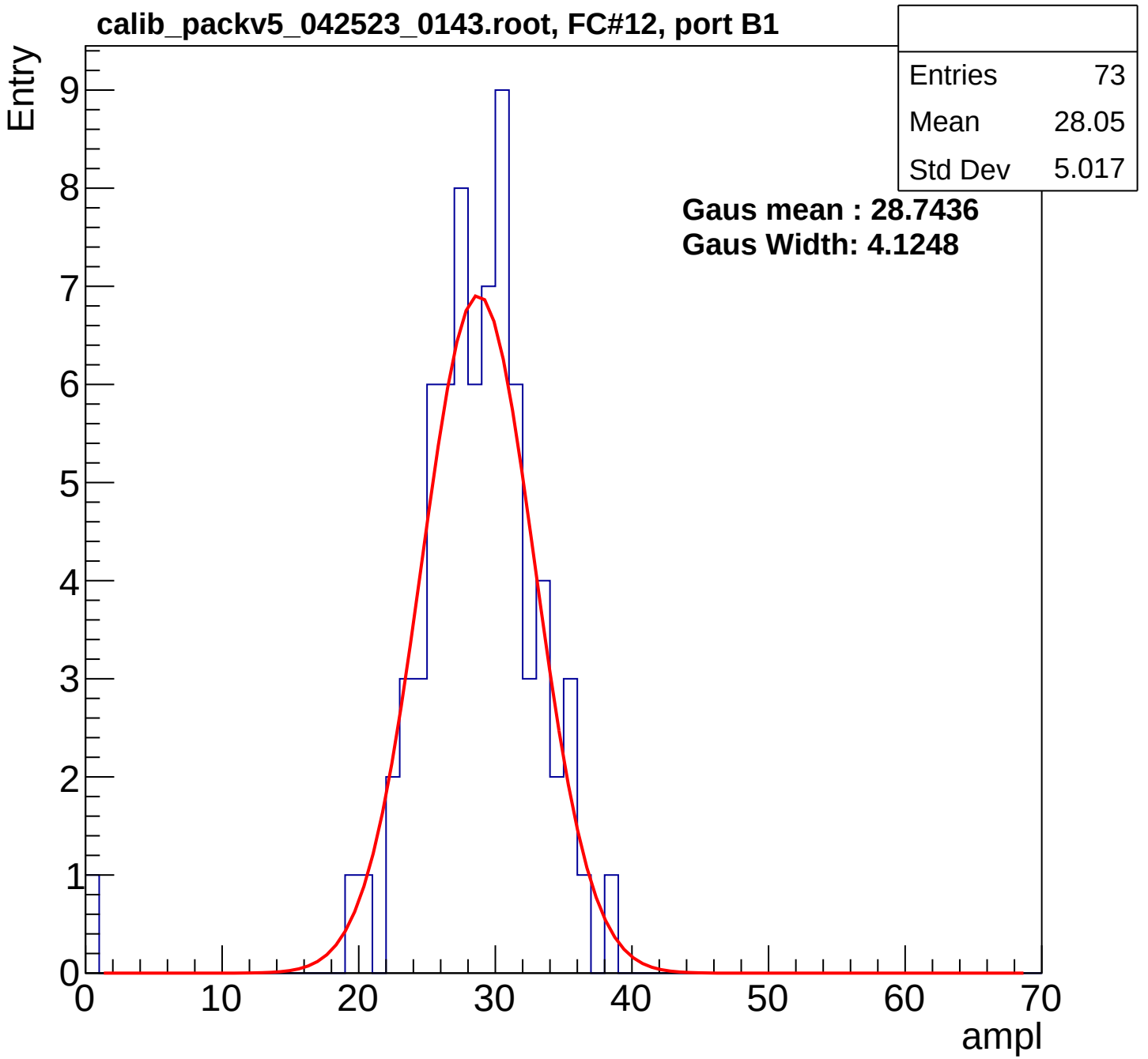

B0L102S, U1-ch87, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 49.08
Std Dev 3.562
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

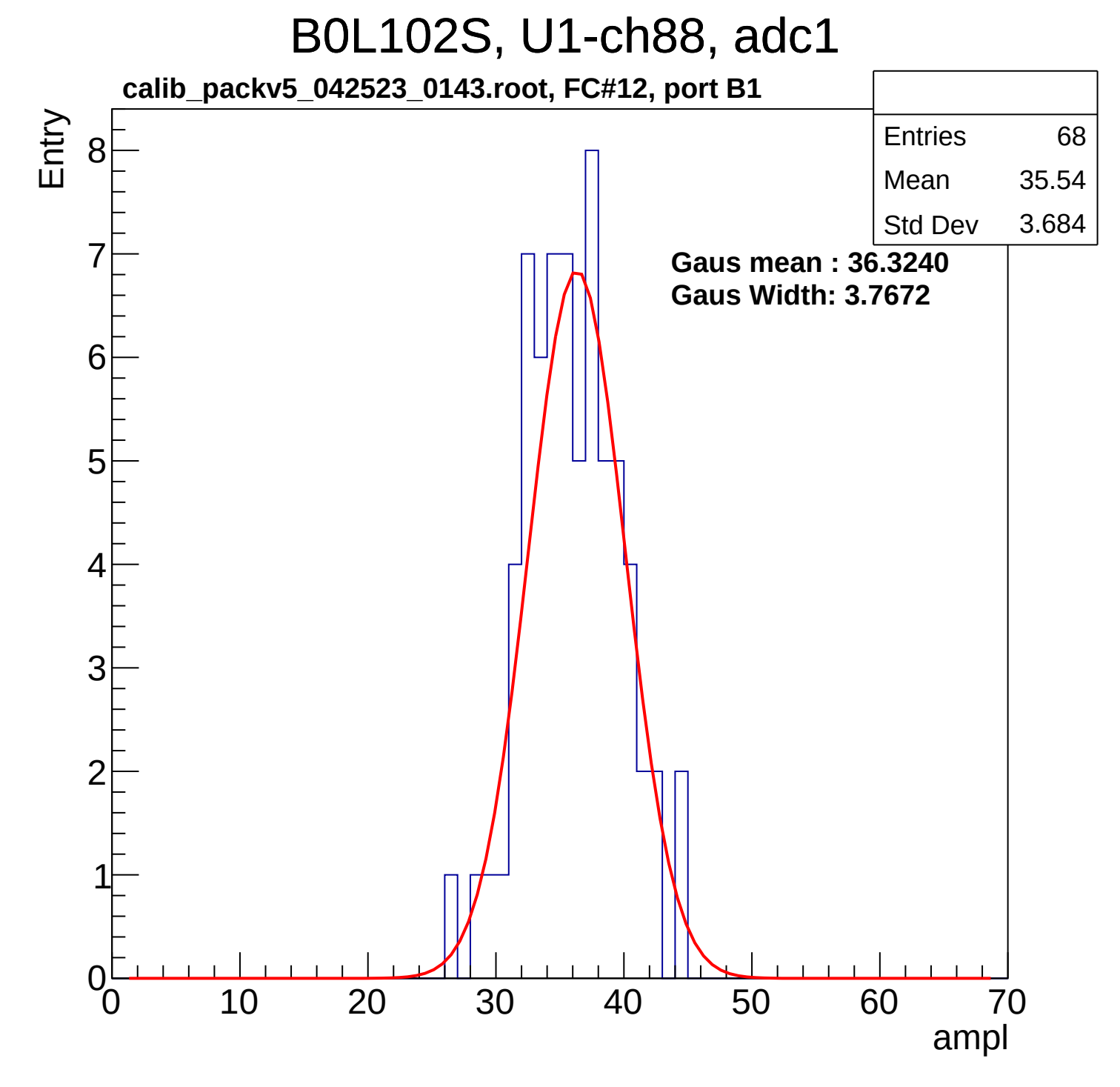

B0L102S, U1-ch87, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 54.89
Std Dev 3.082
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

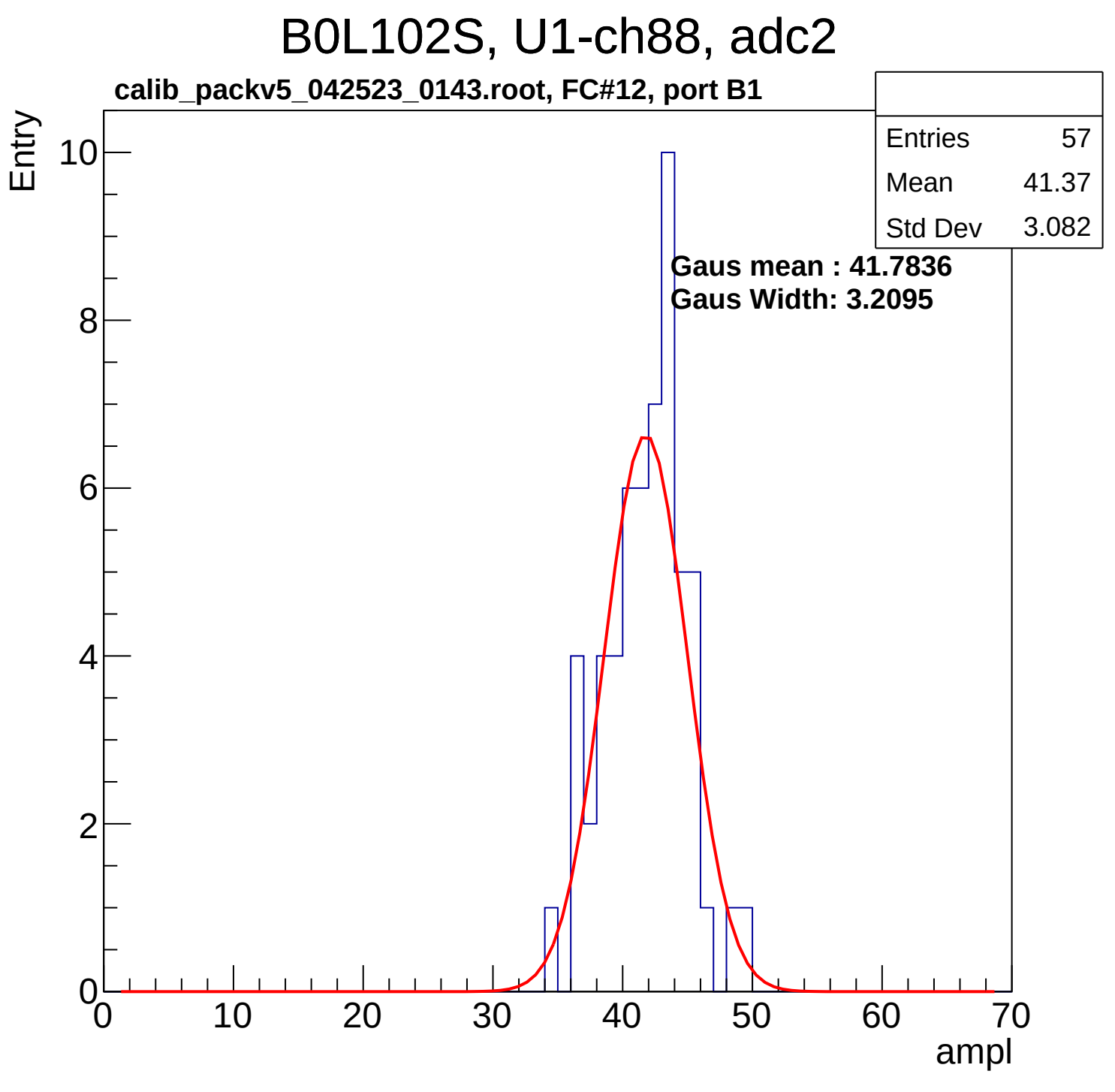

B0L102S, U1-ch87, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.78
Std Dev 2.501
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

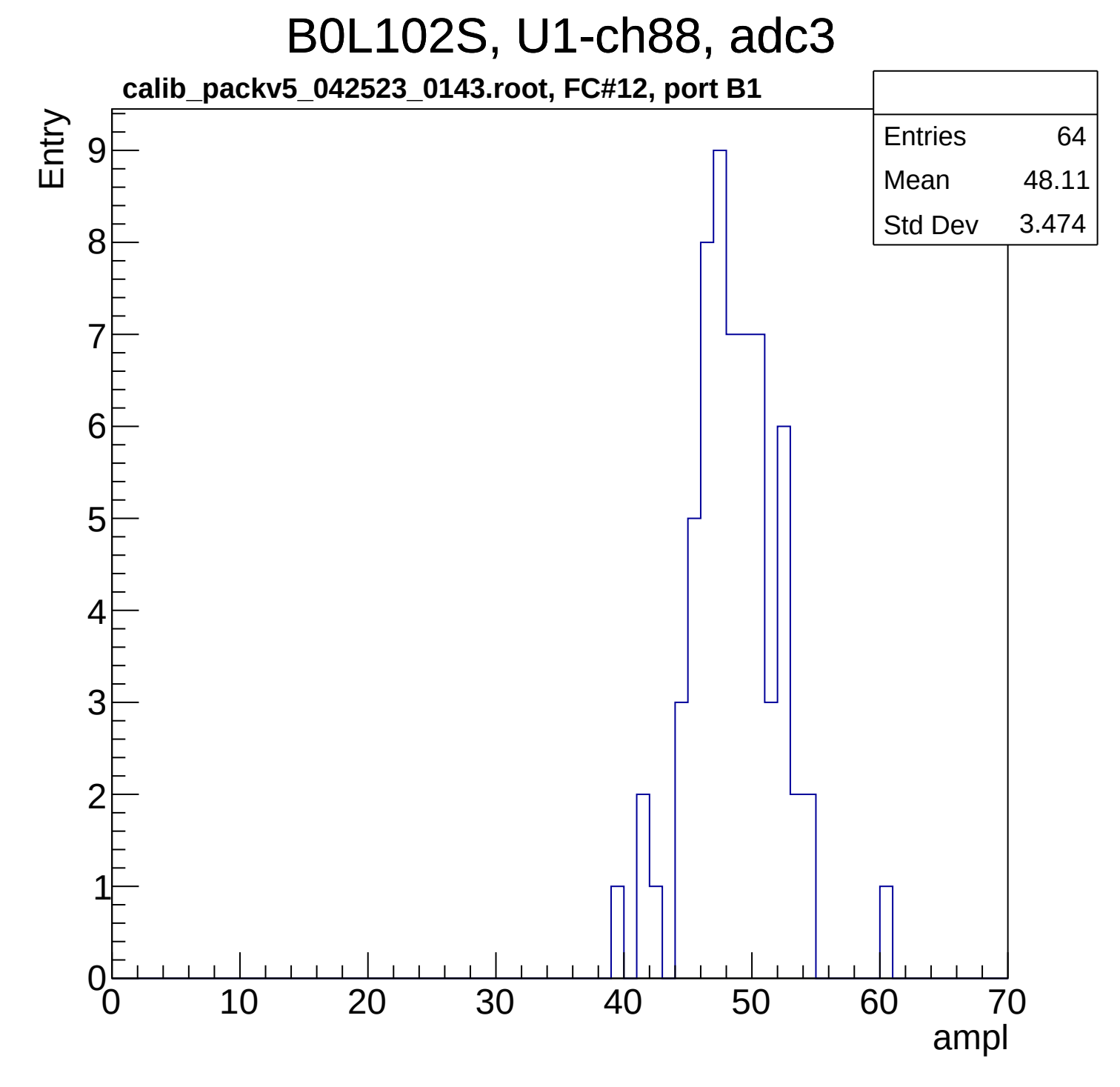

B0L102S, U1-ch87, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 55.82
Std Dev 17.72
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch87, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch88, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 28.05
Std Dev 5.017
Gaus mean : 28.7436
Gaus Width: 4.1248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch88, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 35.54
Std Dev 3.684
Gaus mean : 36.3240
Gaus Width: 3.7672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch88, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 41.37
Std Dev 3.082
Gaus mean : 41.7836
Gaus Width: 3.2095
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch88, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 48.11
Std Dev 3.474
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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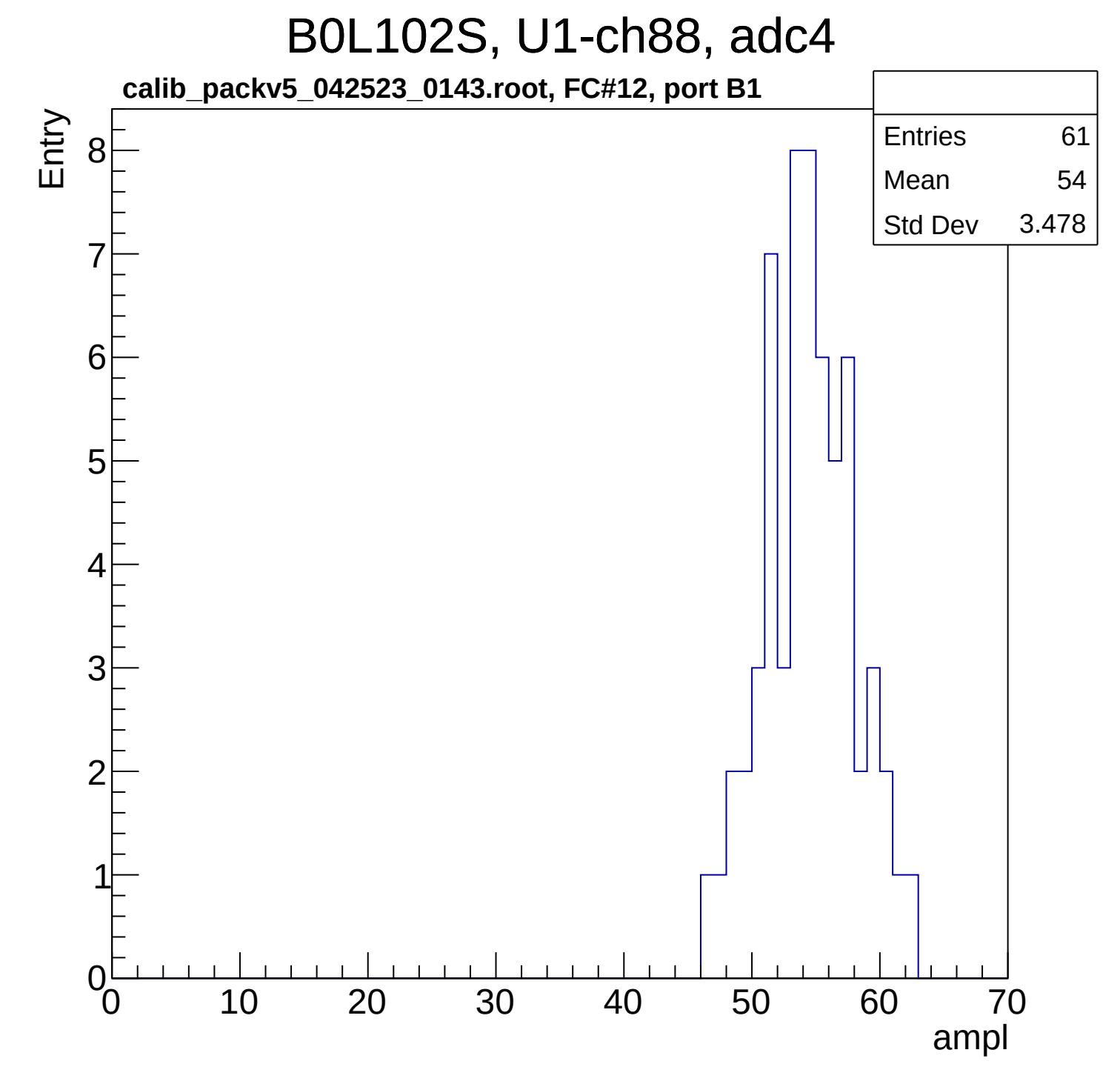
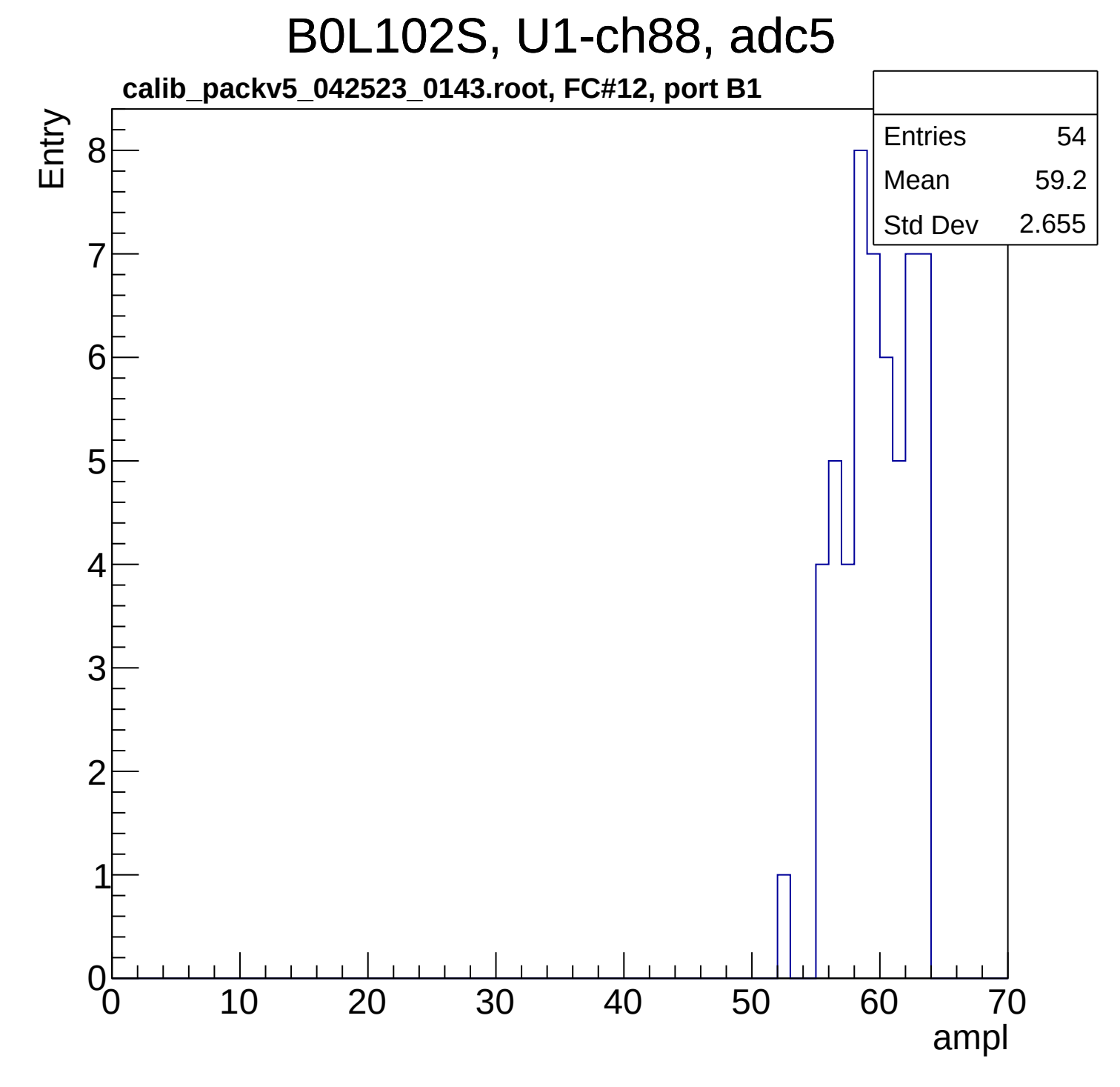
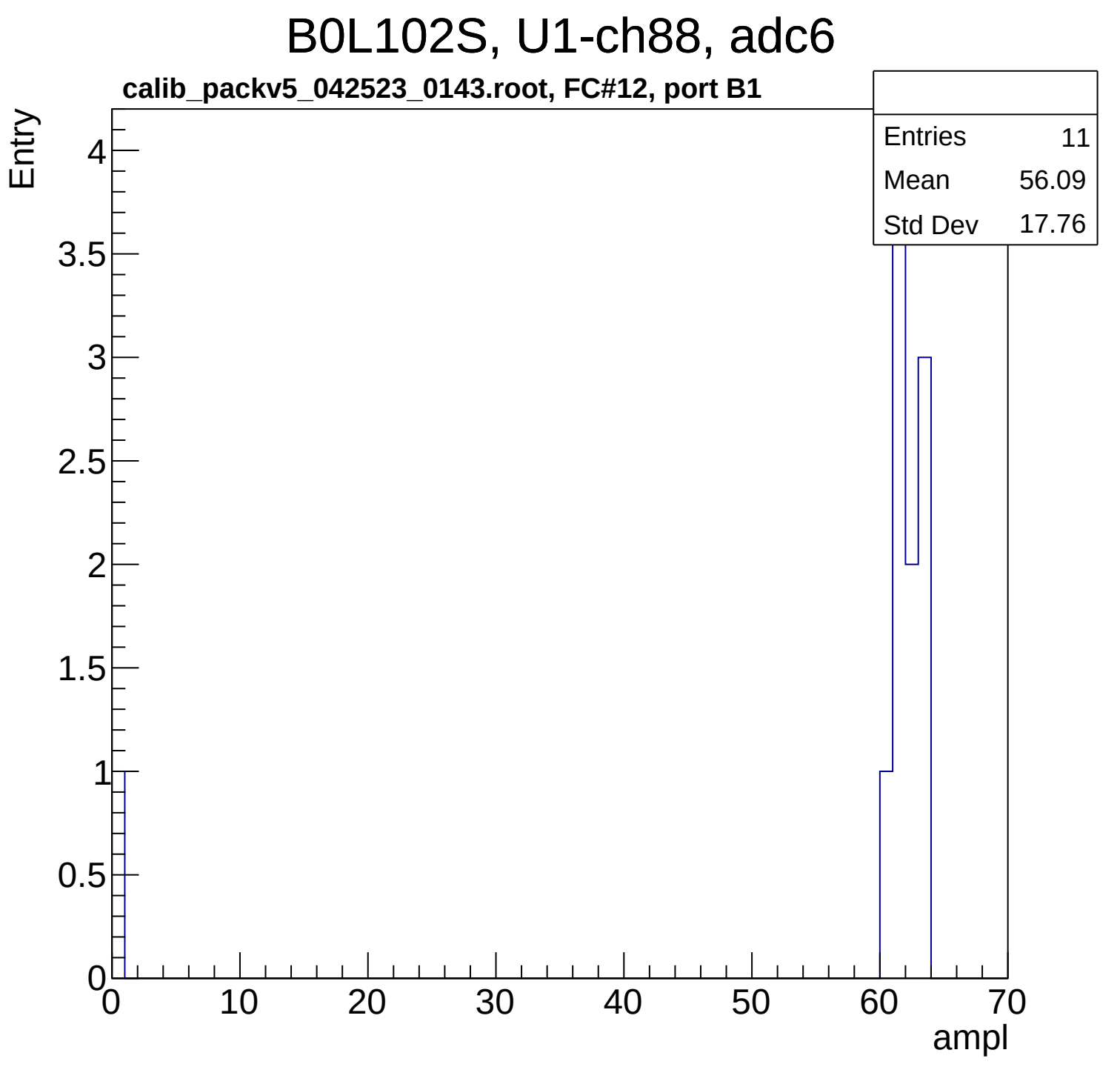
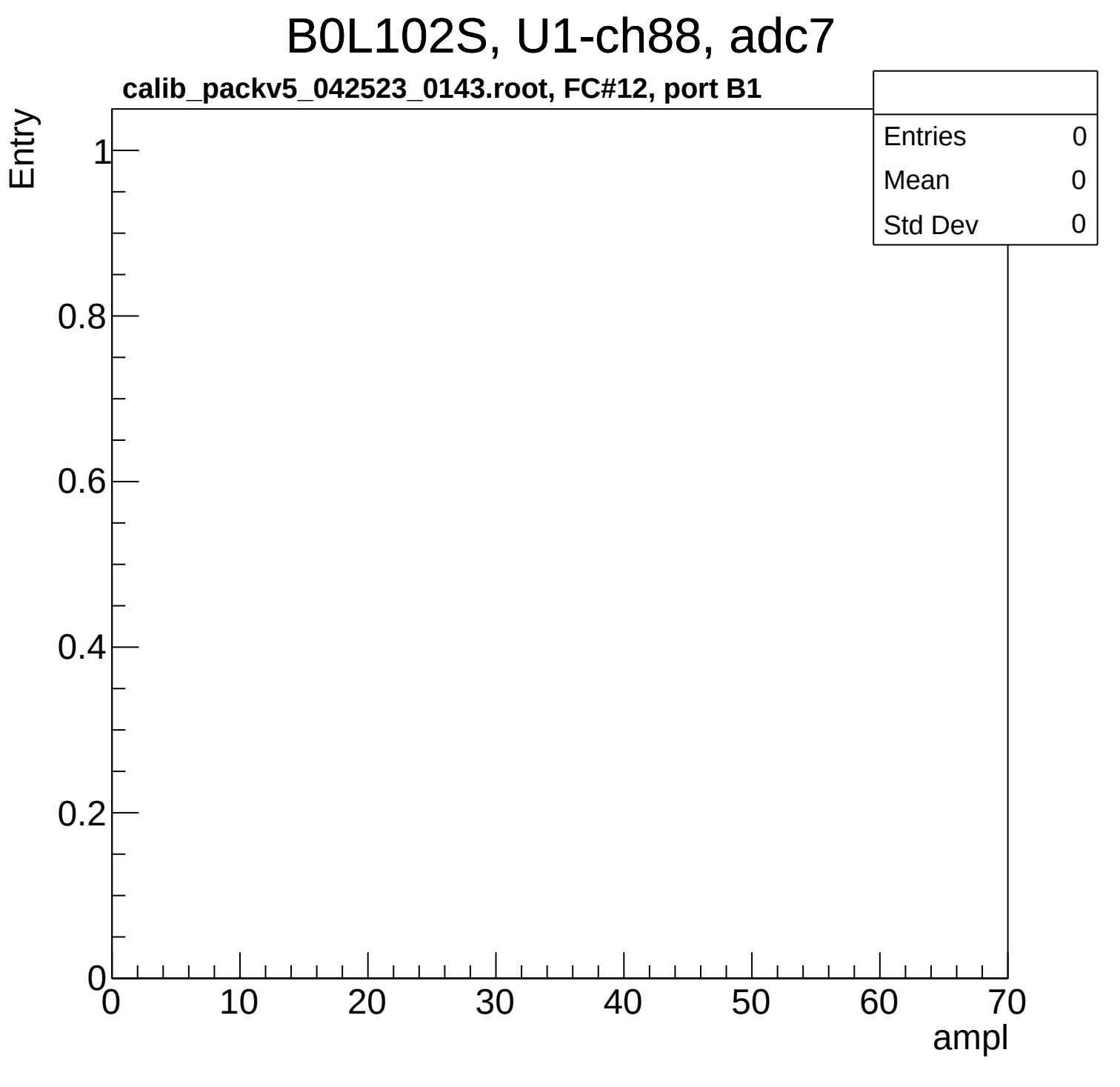
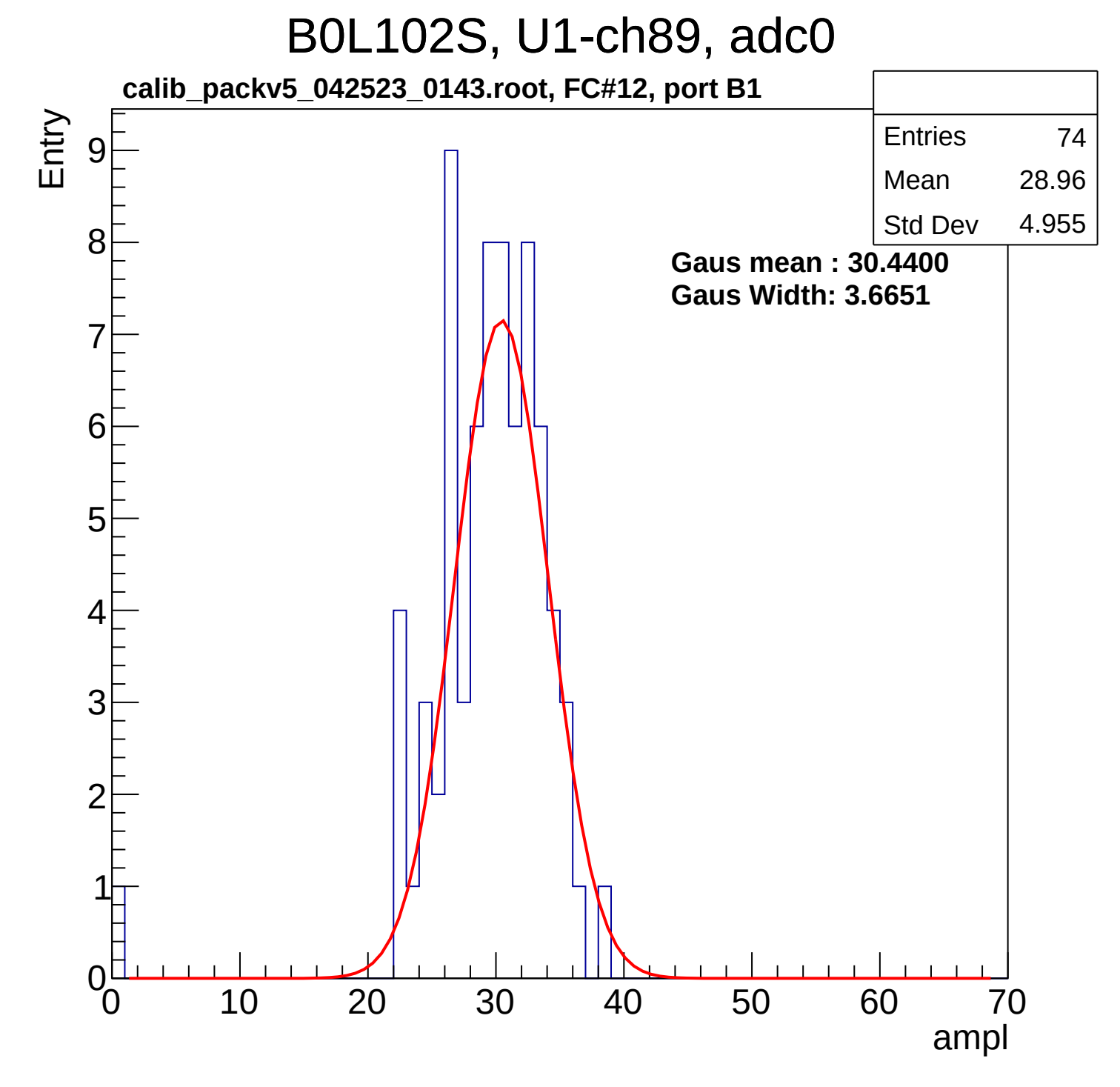

B0L102S, U1-ch88, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 54
Std Dev 3.478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch88, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 59.2
Std Dev 2.655
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch88, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 56.09
Std Dev 17.76
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch88, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch89, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 28.96
Std Dev 4.955
Gaus mean : 30.4400
Gaus Width: 3.6651
Entry
ampl
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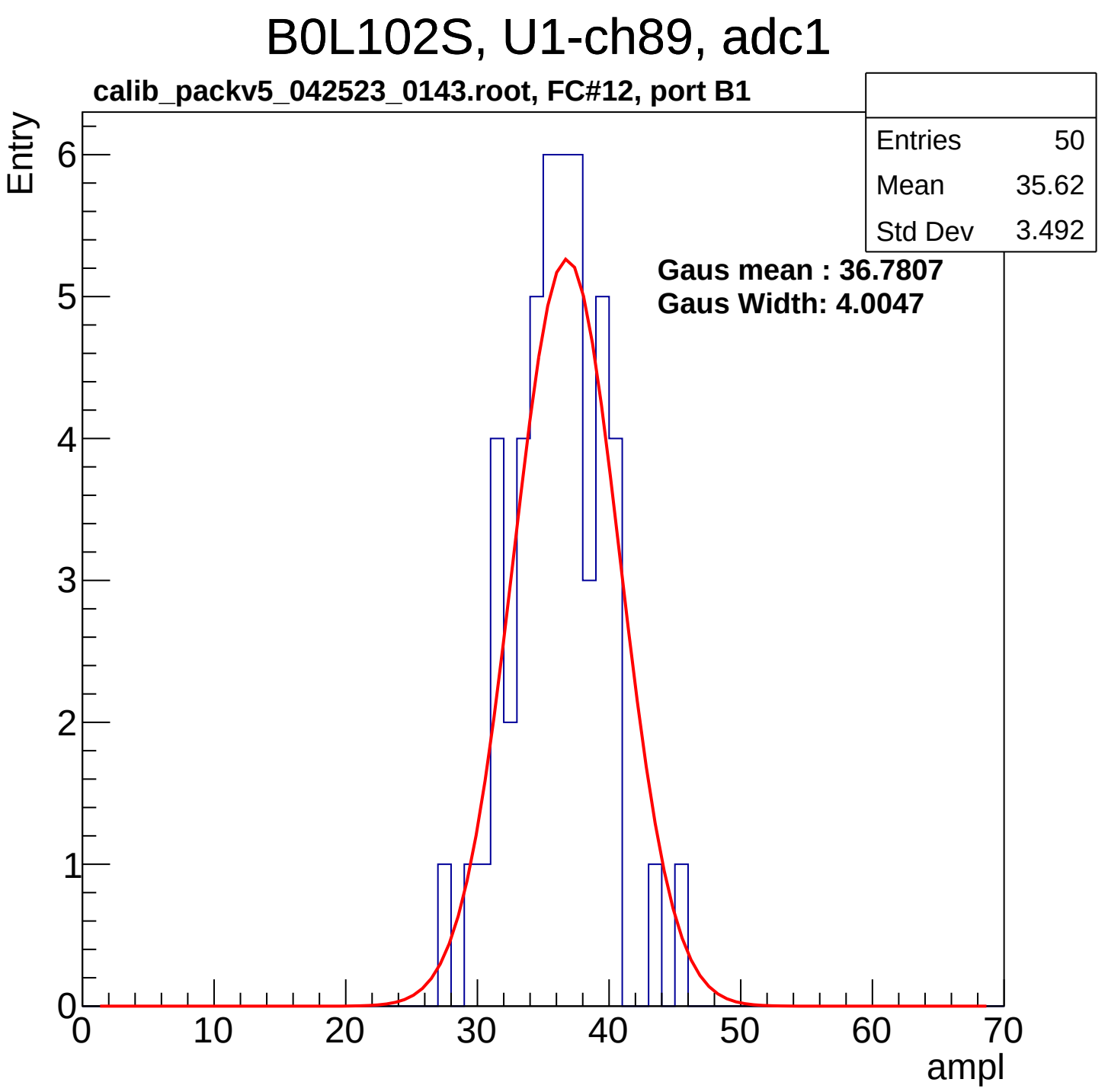
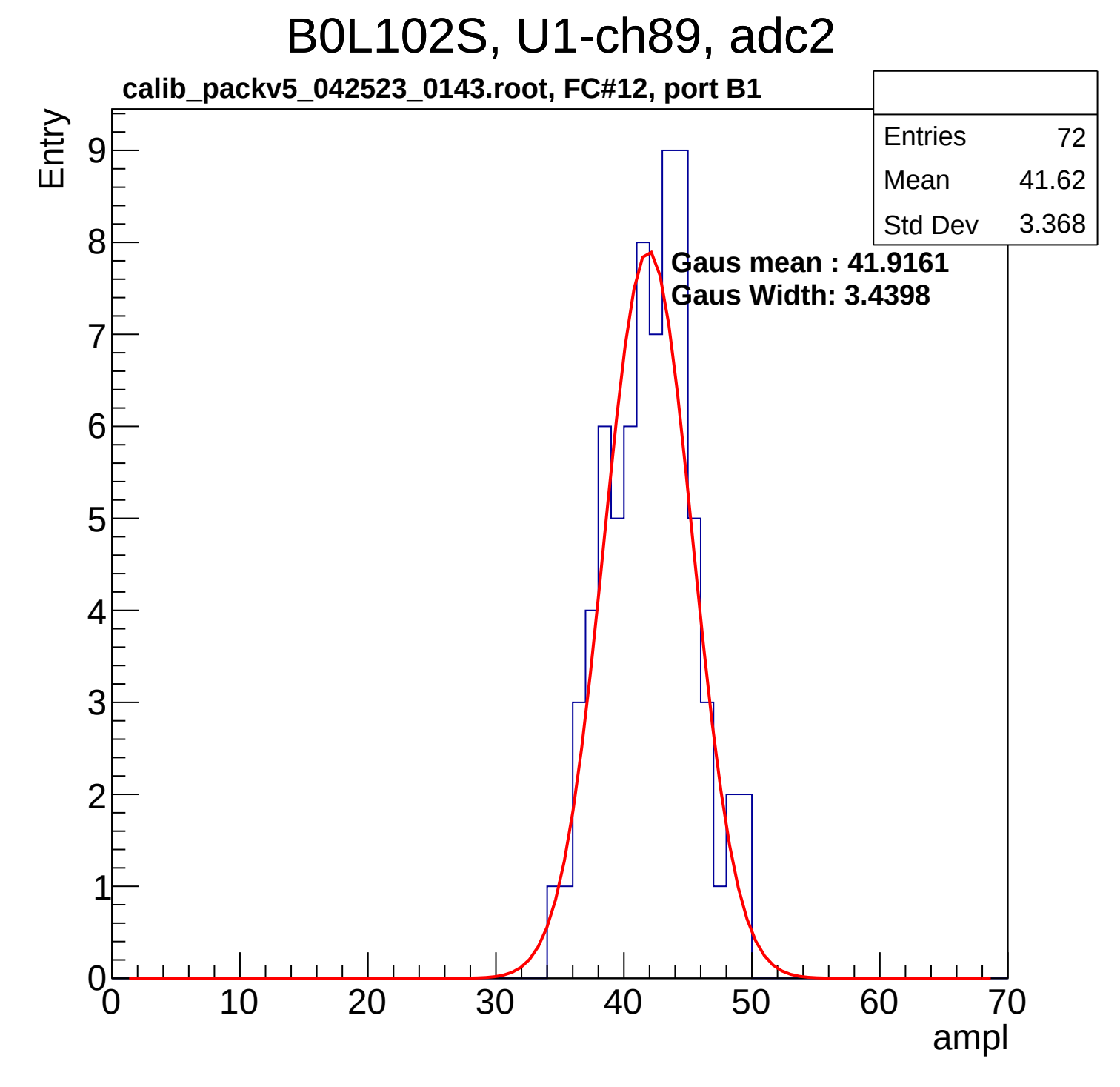
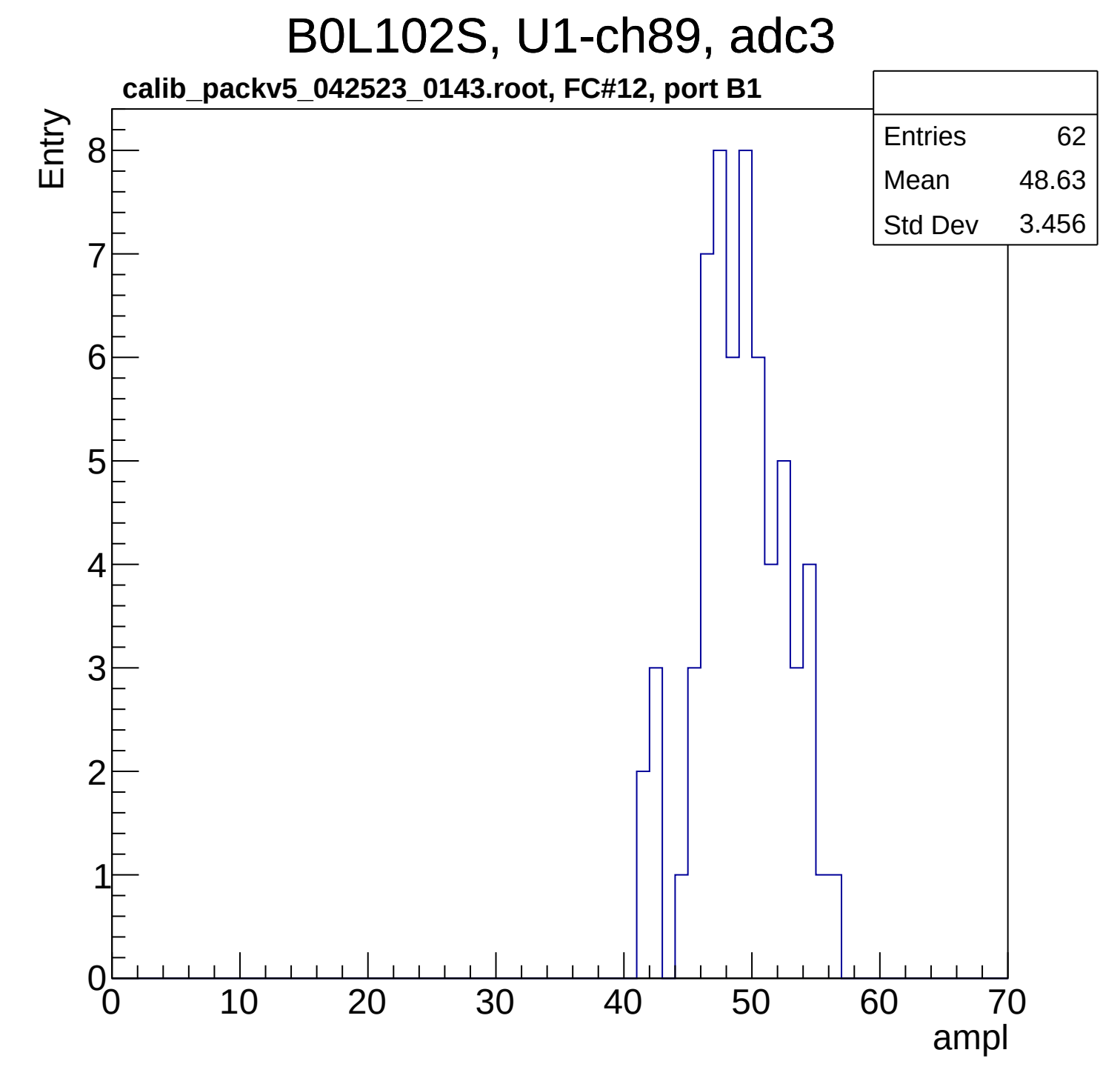
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch89, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 35.62
Std Dev 3.492
Gaus mean : 36.7807
Gaus Width: 4.0047
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U1-ch89, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 41.62
Std Dev 3.368
Gaus mean : 41.9161
Gaus Width: 3.4398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch89, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 48.63
Std Dev 3.456
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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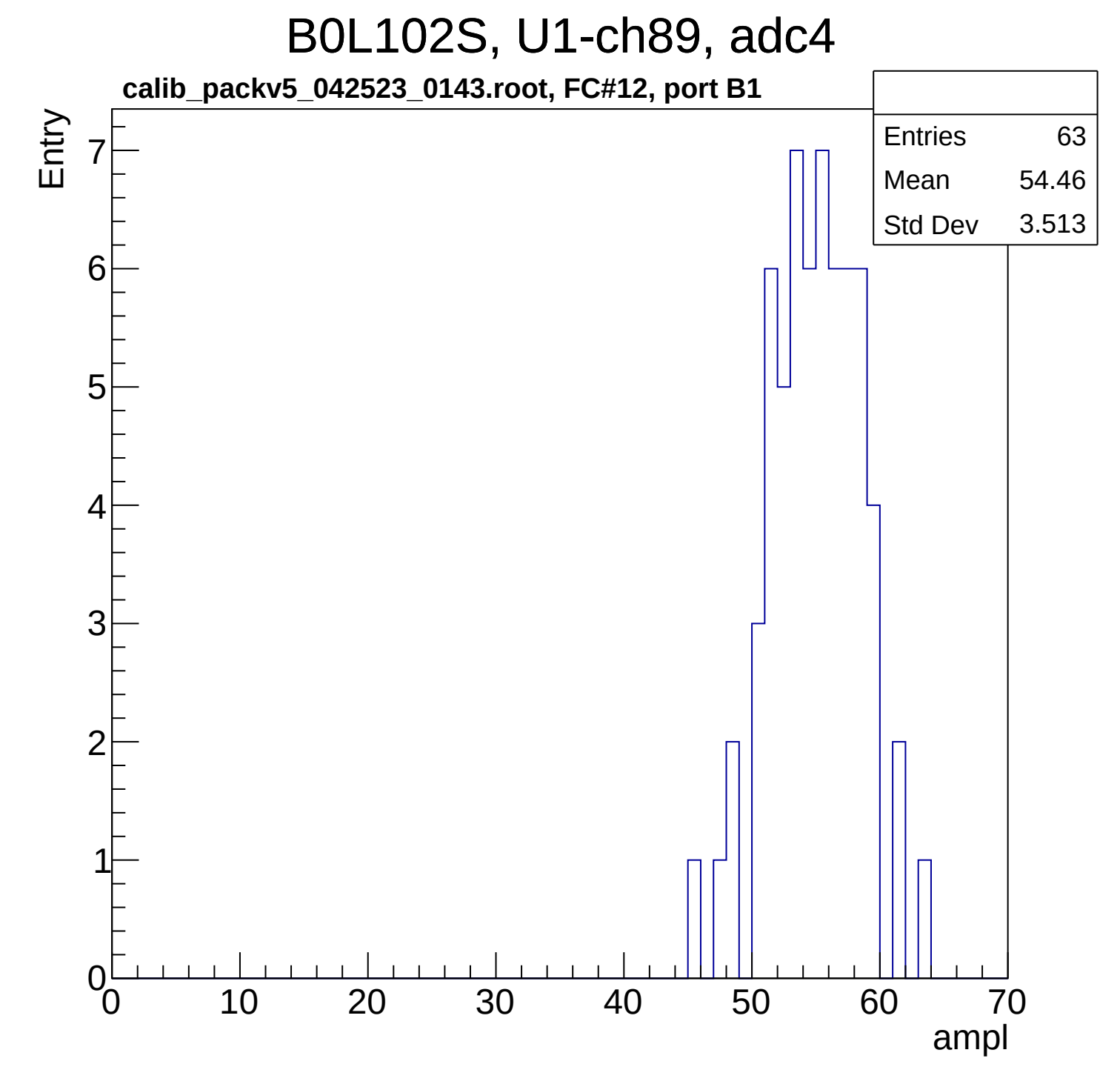
40
50
60
70


B0L102S, U1-ch89, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54.46
Std Dev 3.513
Entry
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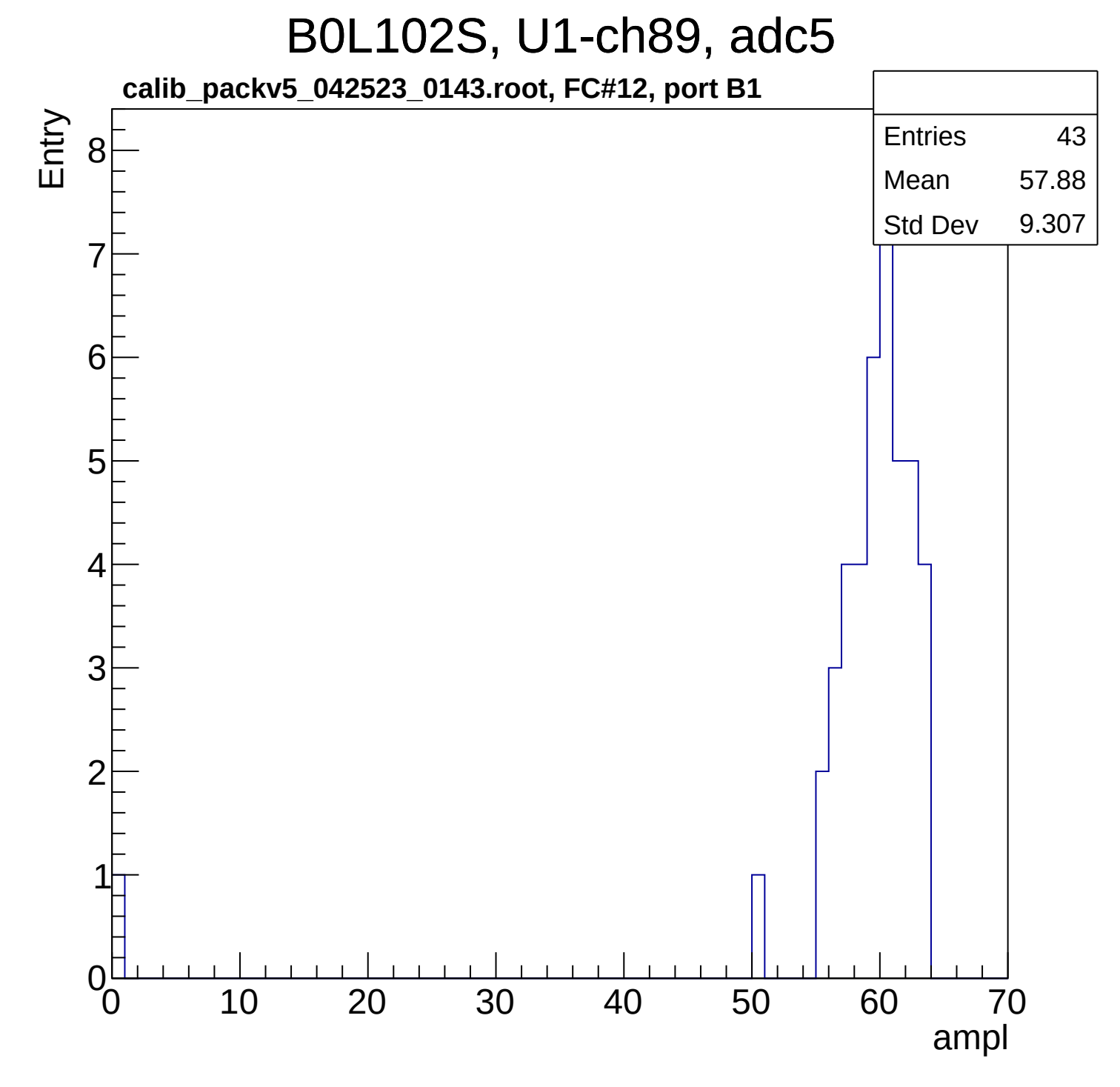
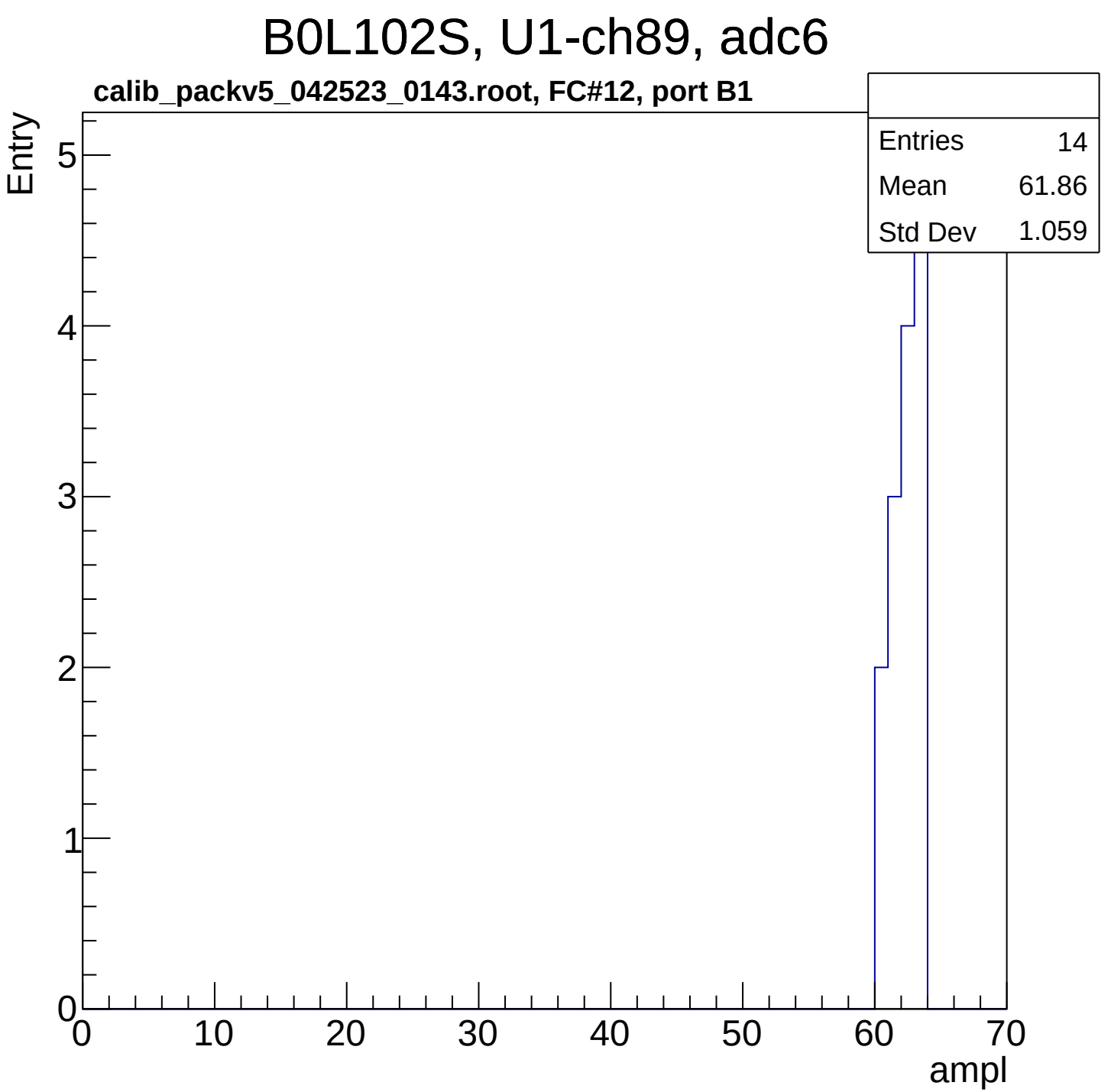
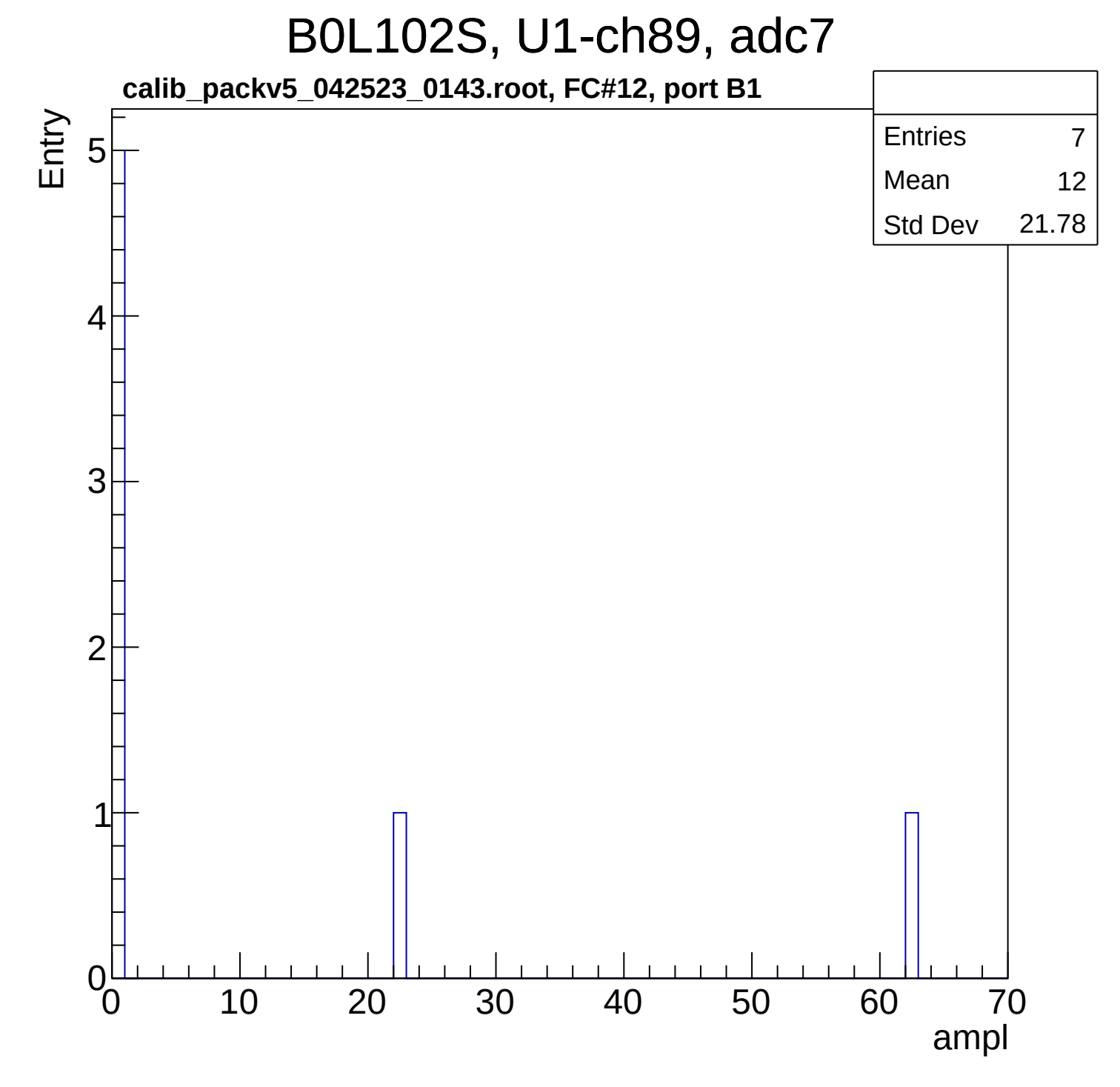
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch89, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 57.88
Std Dev 9.307
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch89, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.86
Std Dev 1.059
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch89, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 12
Std Dev 21.78
Entry
0
1
2
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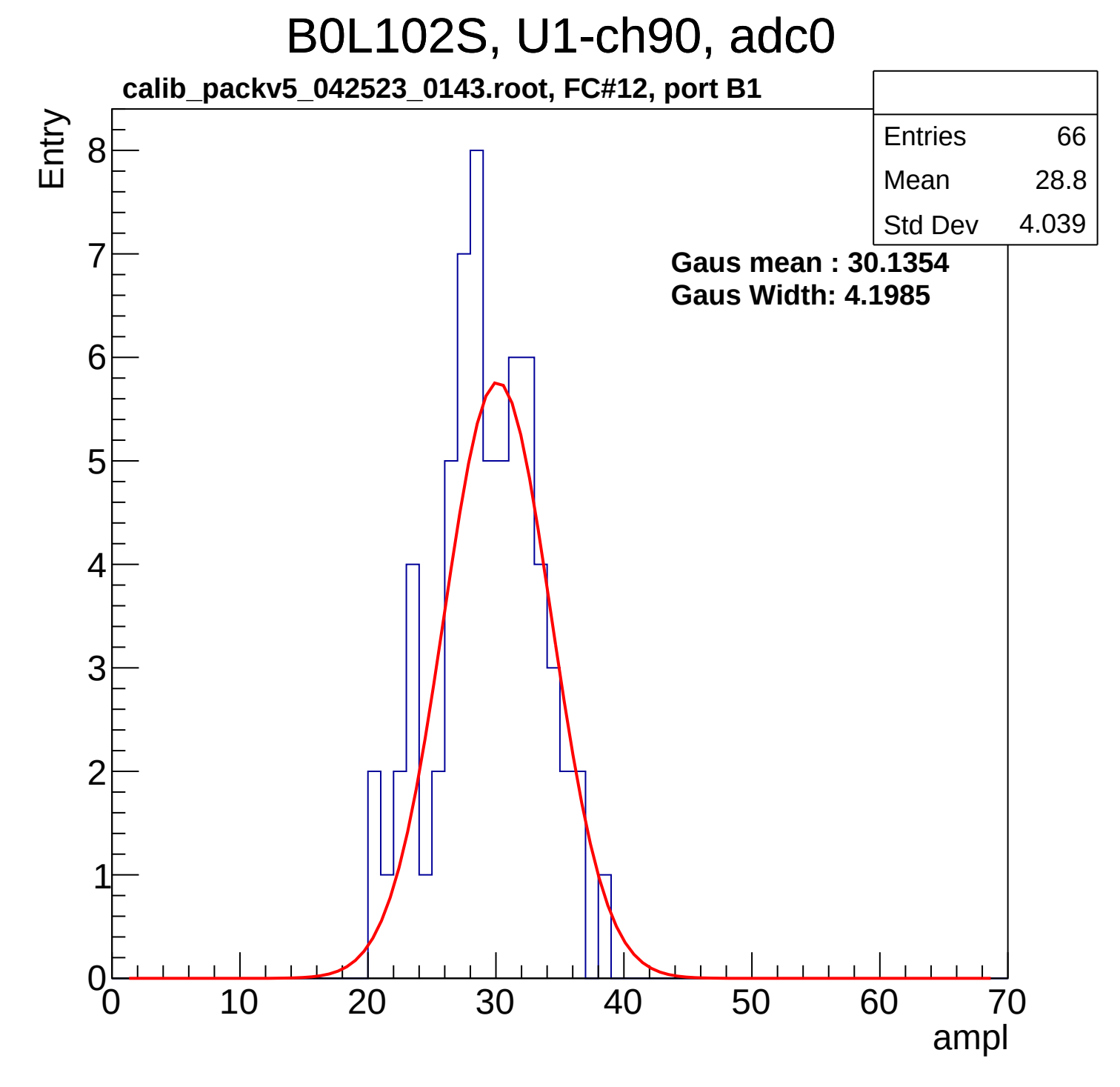
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch90, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 28.8
Std Dev 4.039
Gaus mean : 30.1354
Gaus Width: 4.1985
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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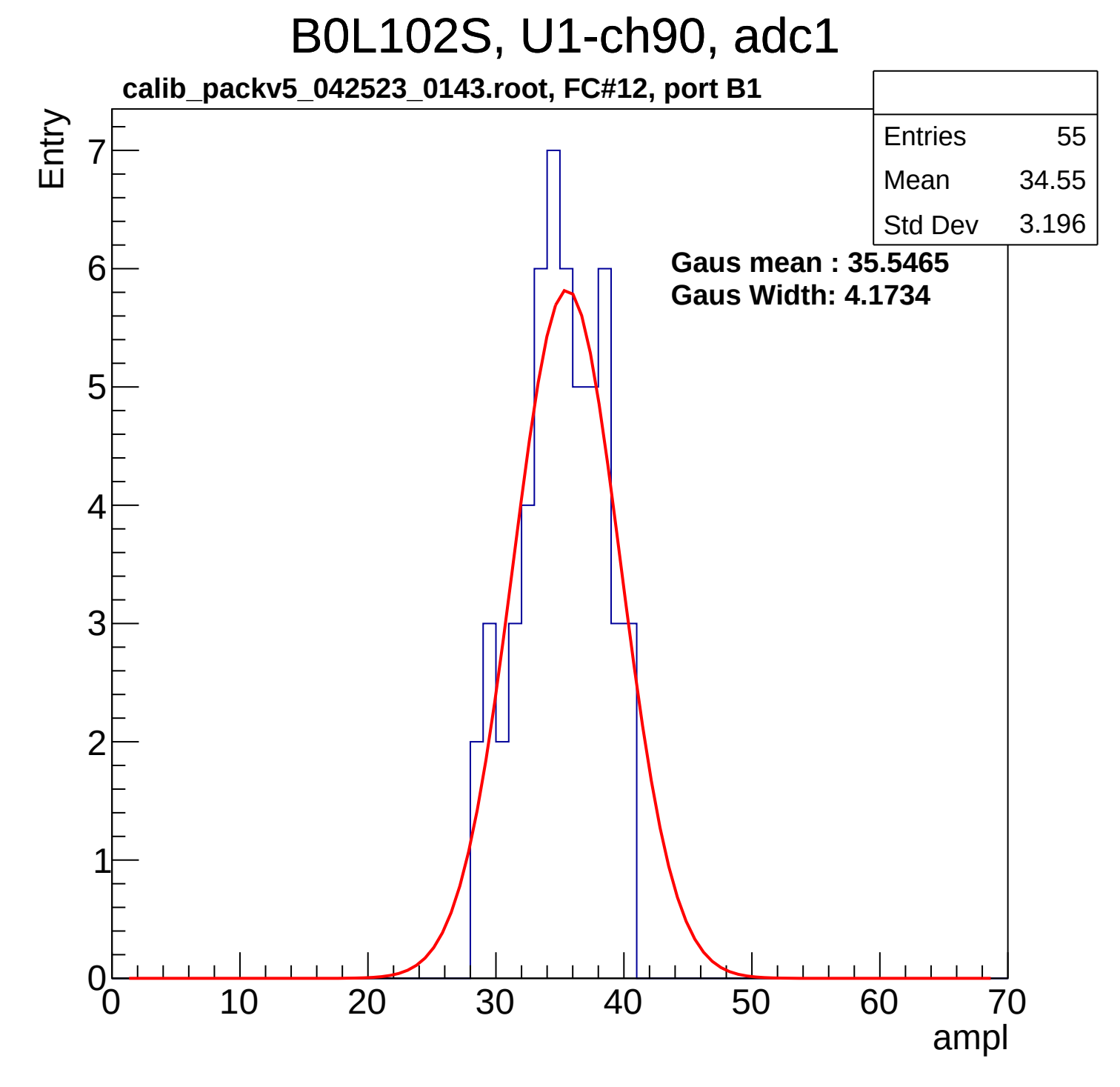
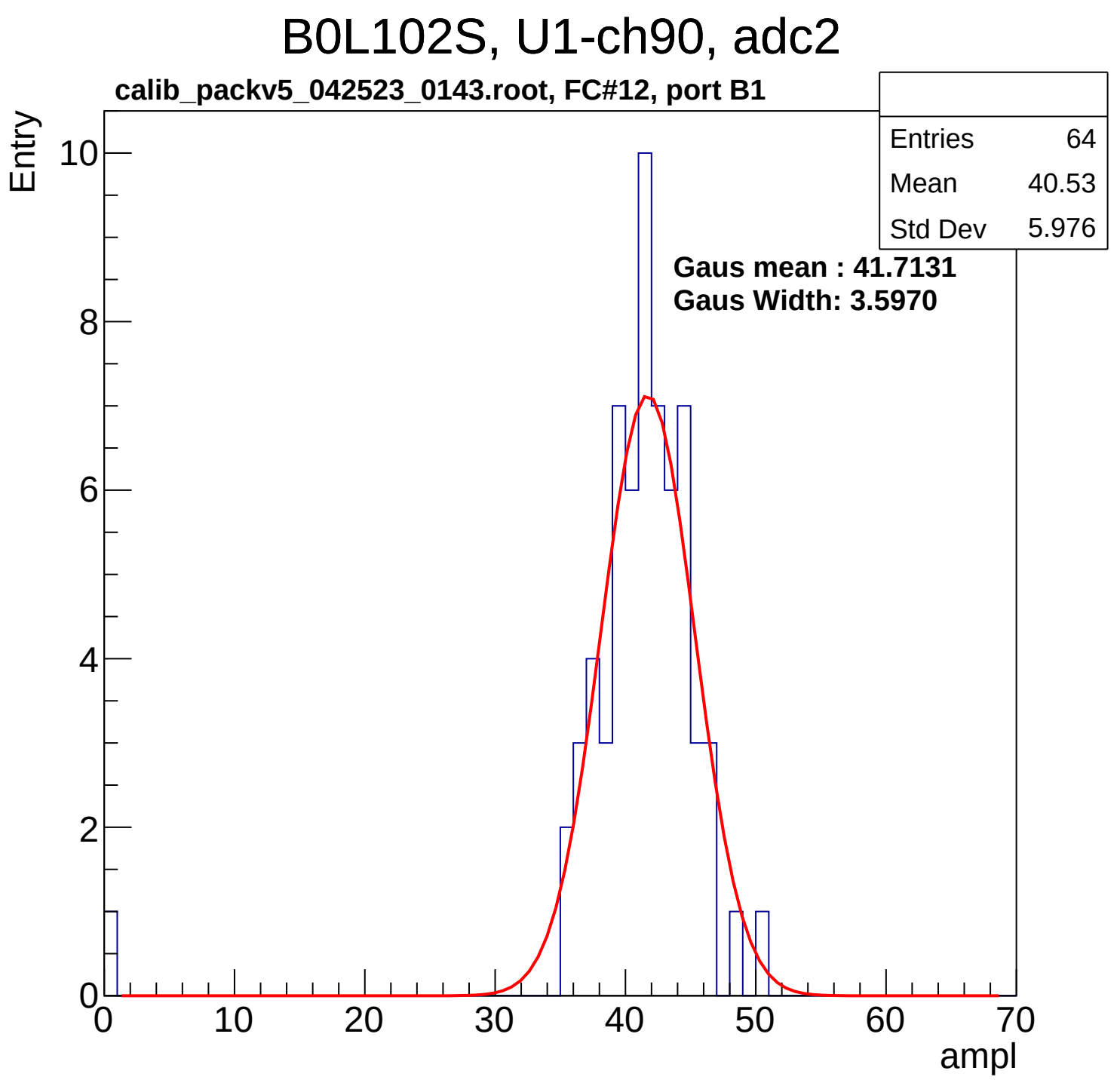
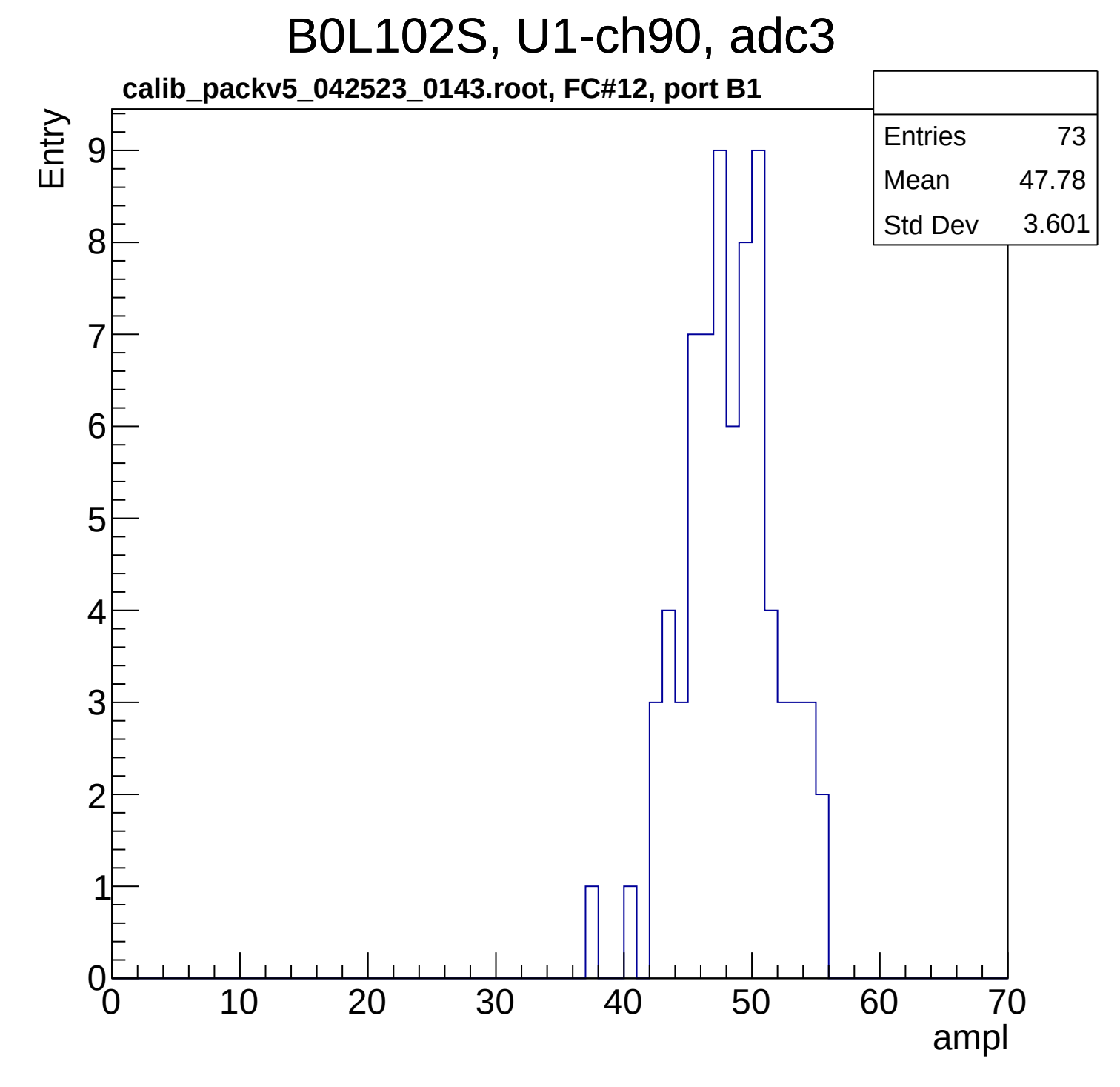
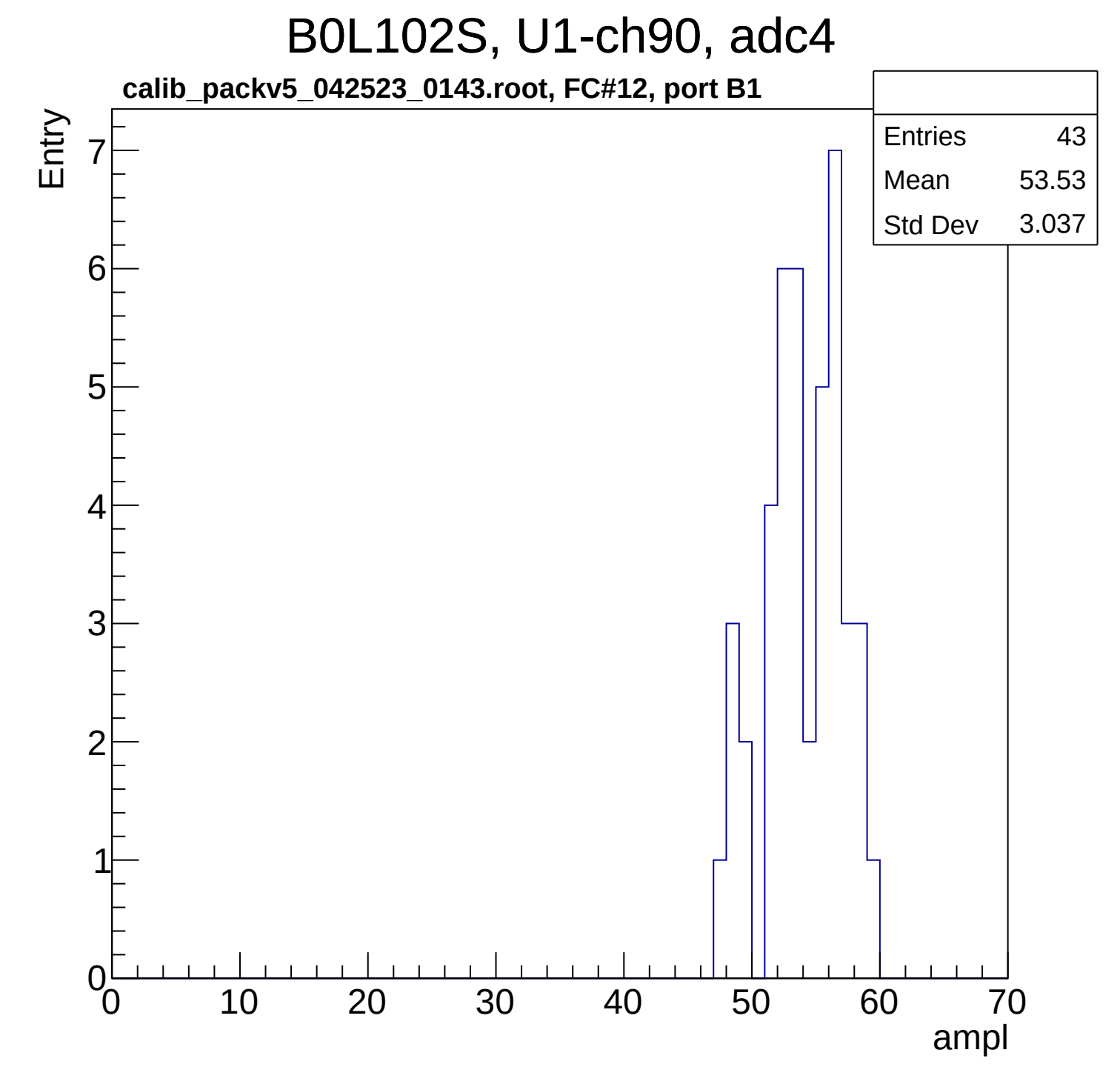
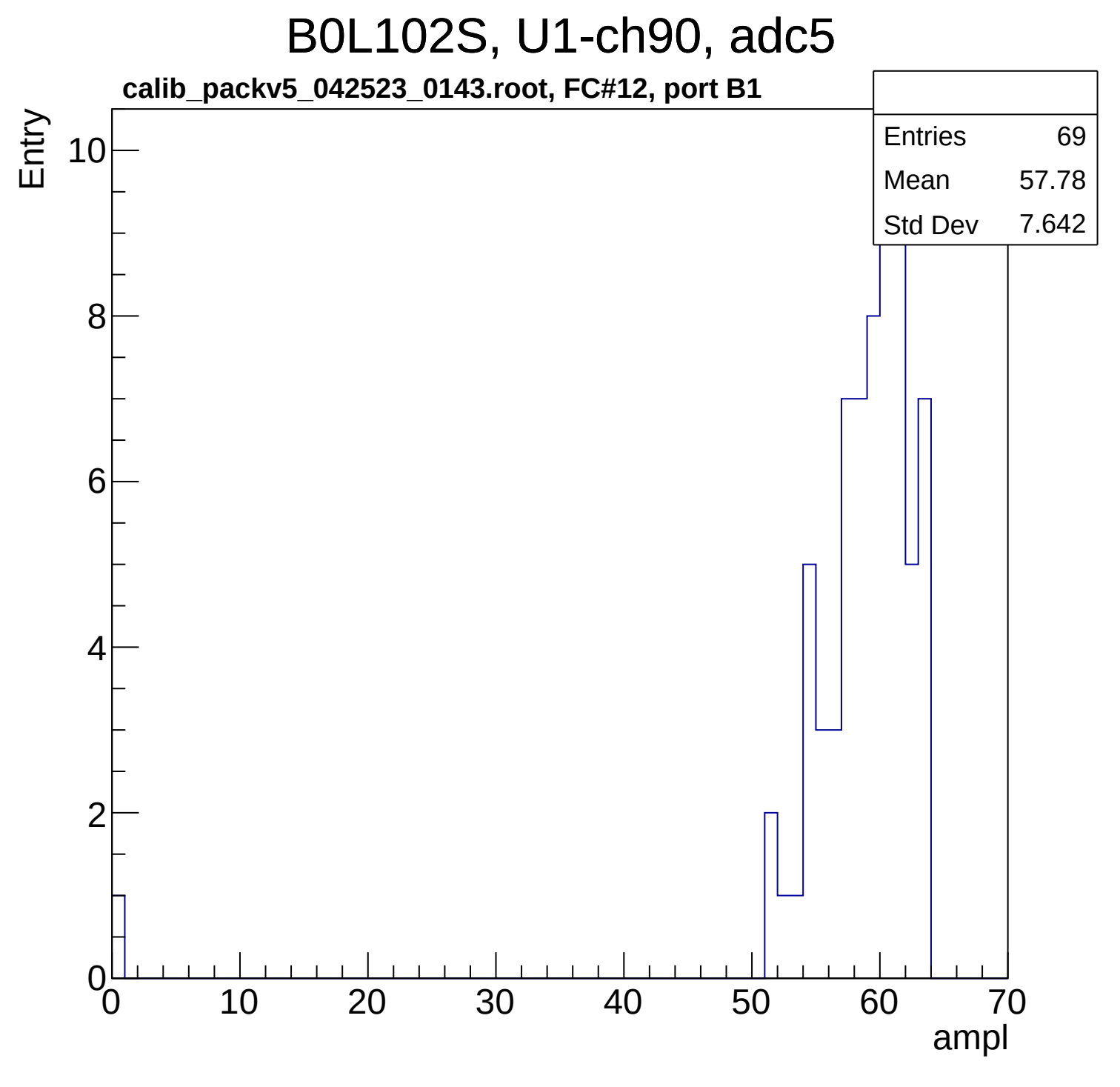
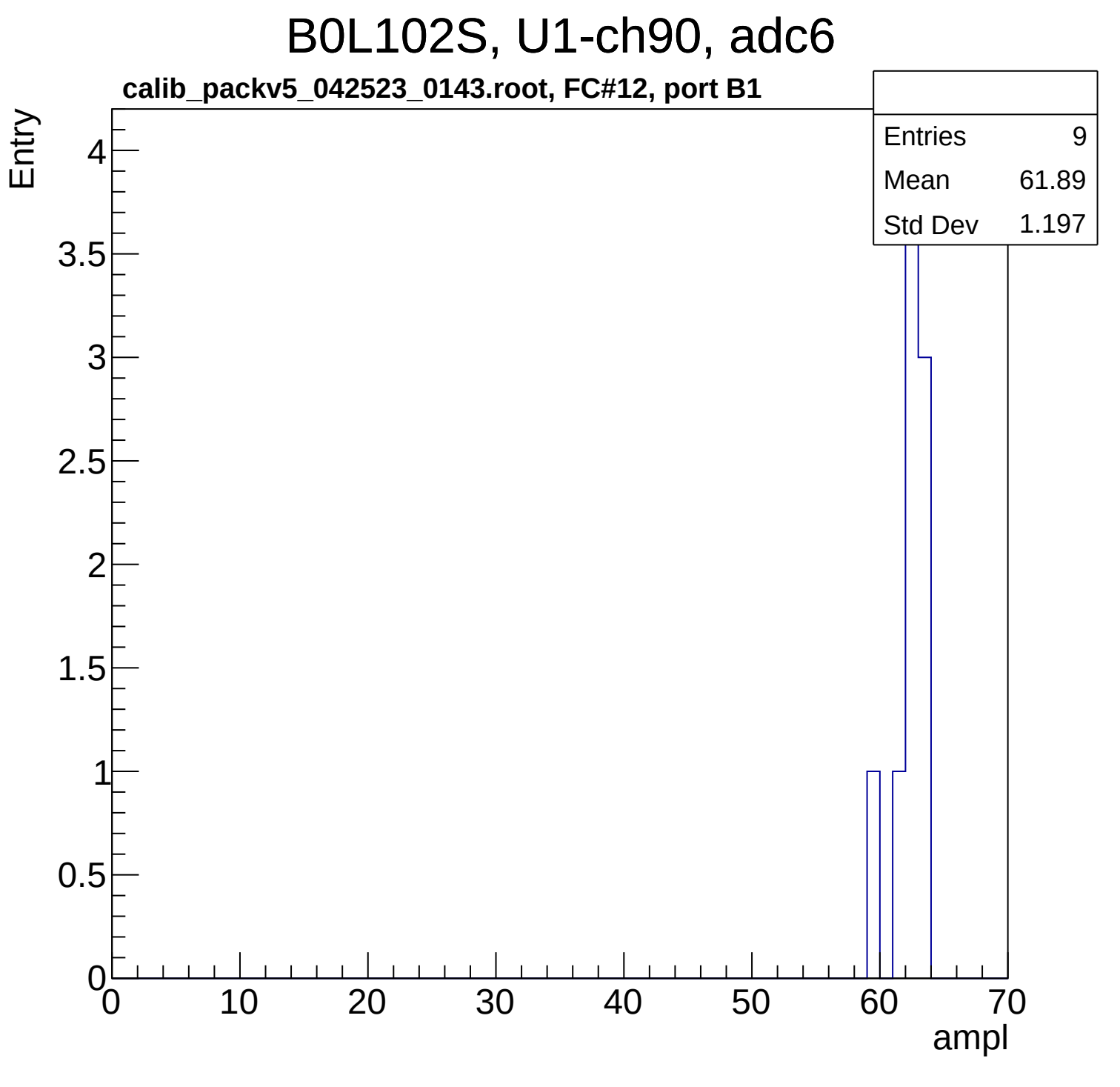

B0L102S, U1-ch90, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 34.55
Std Dev 3.196
Gaus mean : 35.5465
Gaus Width: 4.1734
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

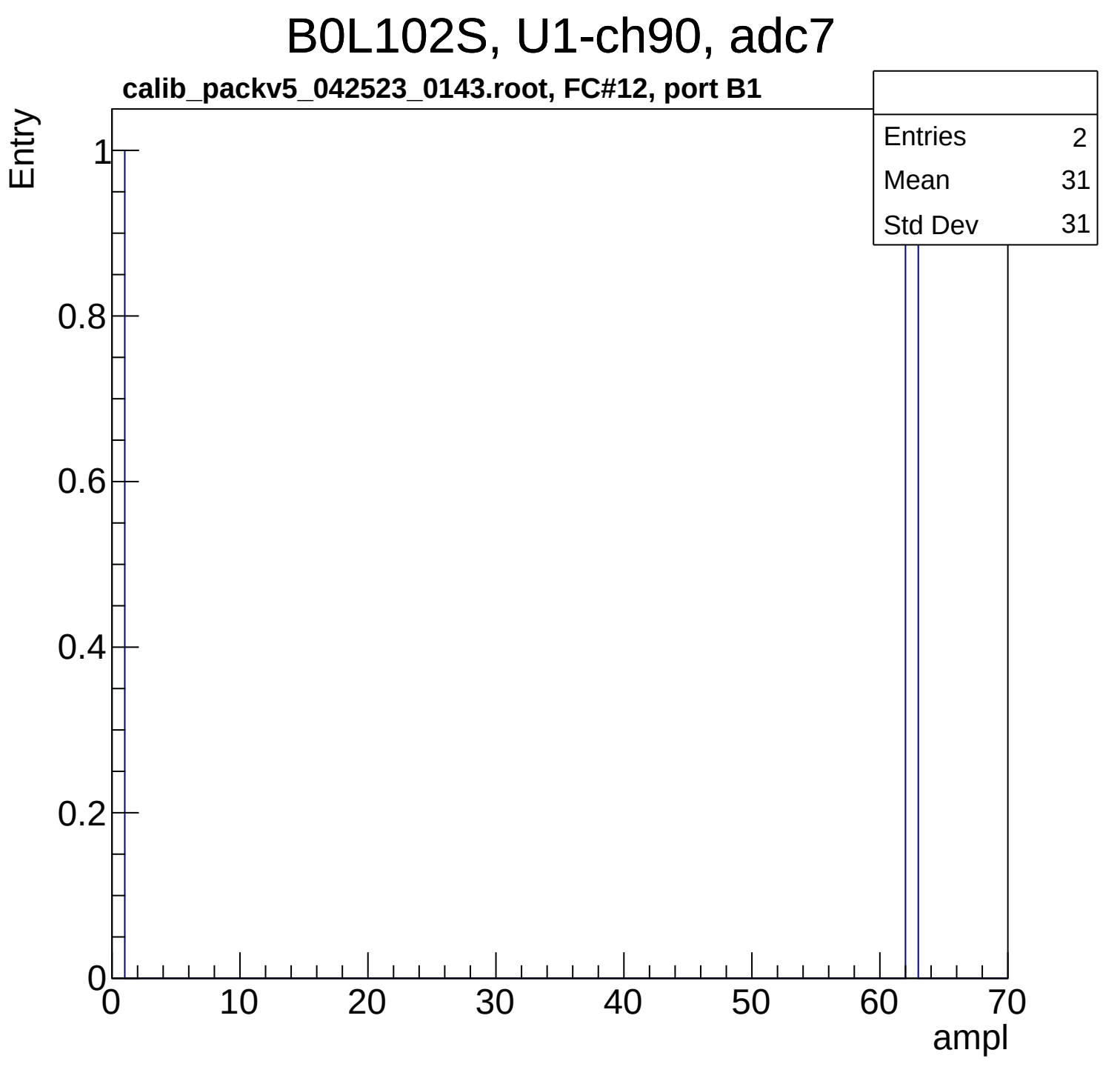

B0L102S, U1-ch90, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 40.53
Std Dev 5.976
Gaus mean : 41.7131
Gaus Width: 3.5970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

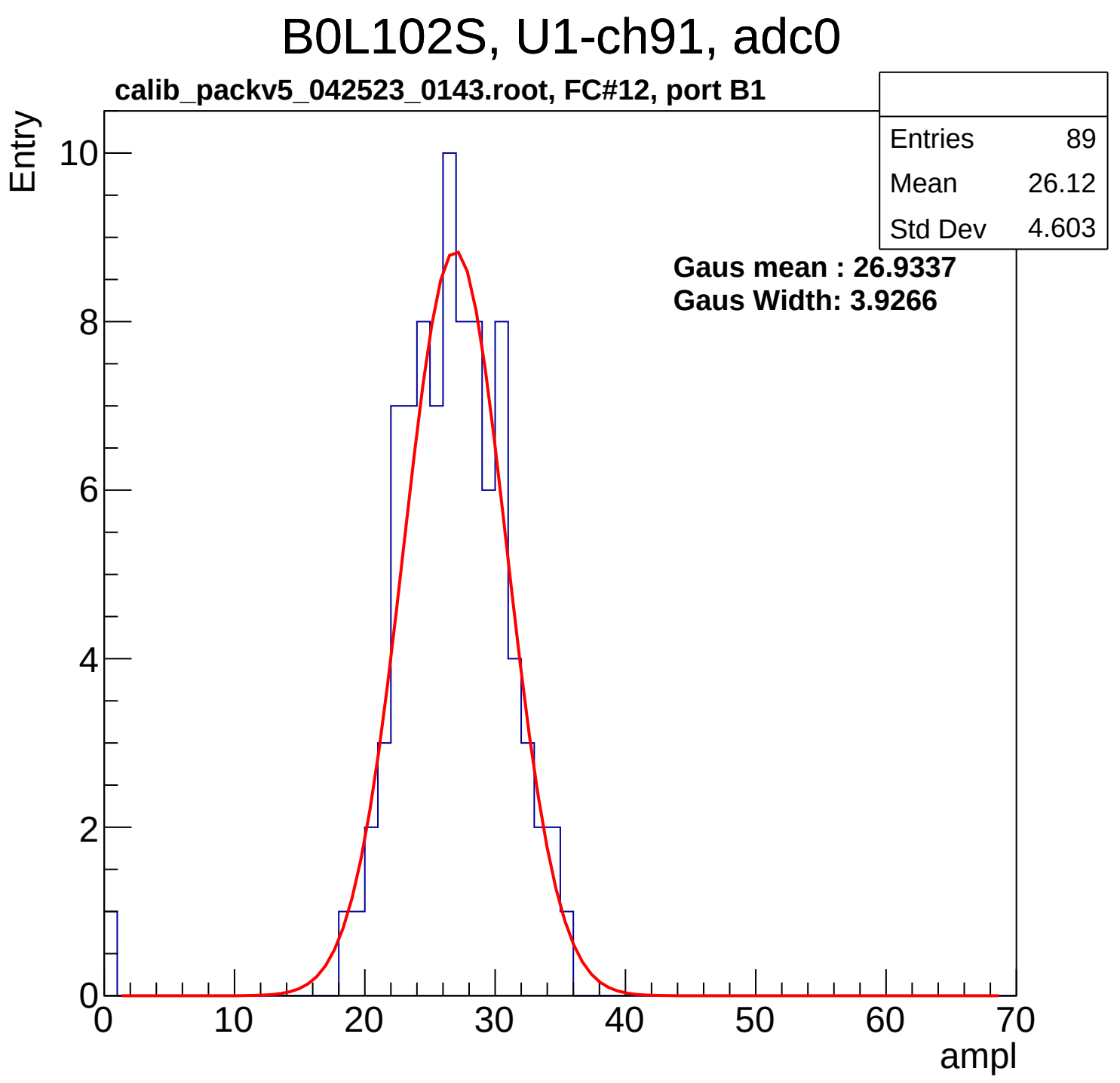

B0L102S, U1-ch90, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 47.78
Std Dev 3.601
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

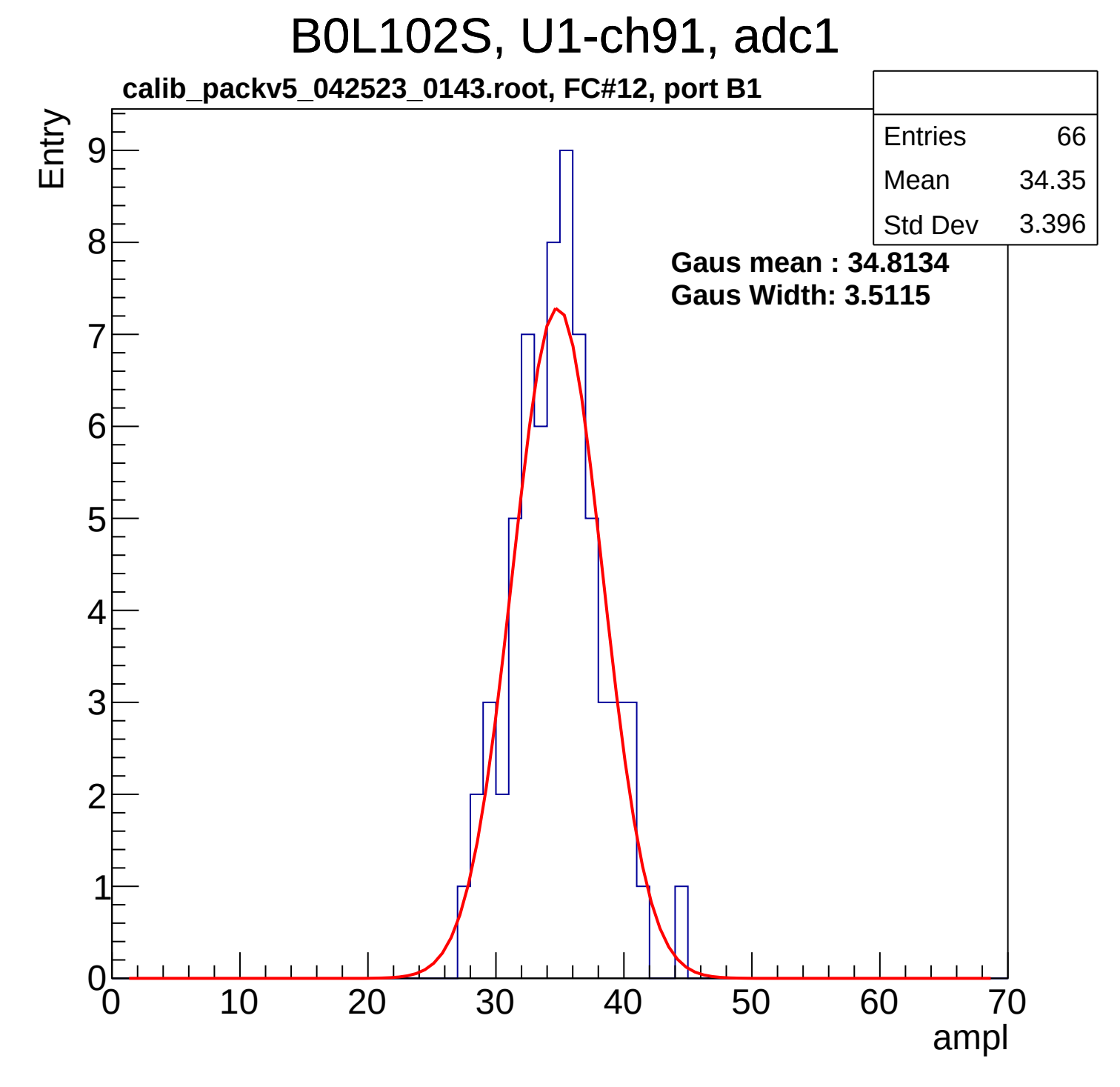

B0L102S, U1-ch90, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 53.53
Std Dev 3.037
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

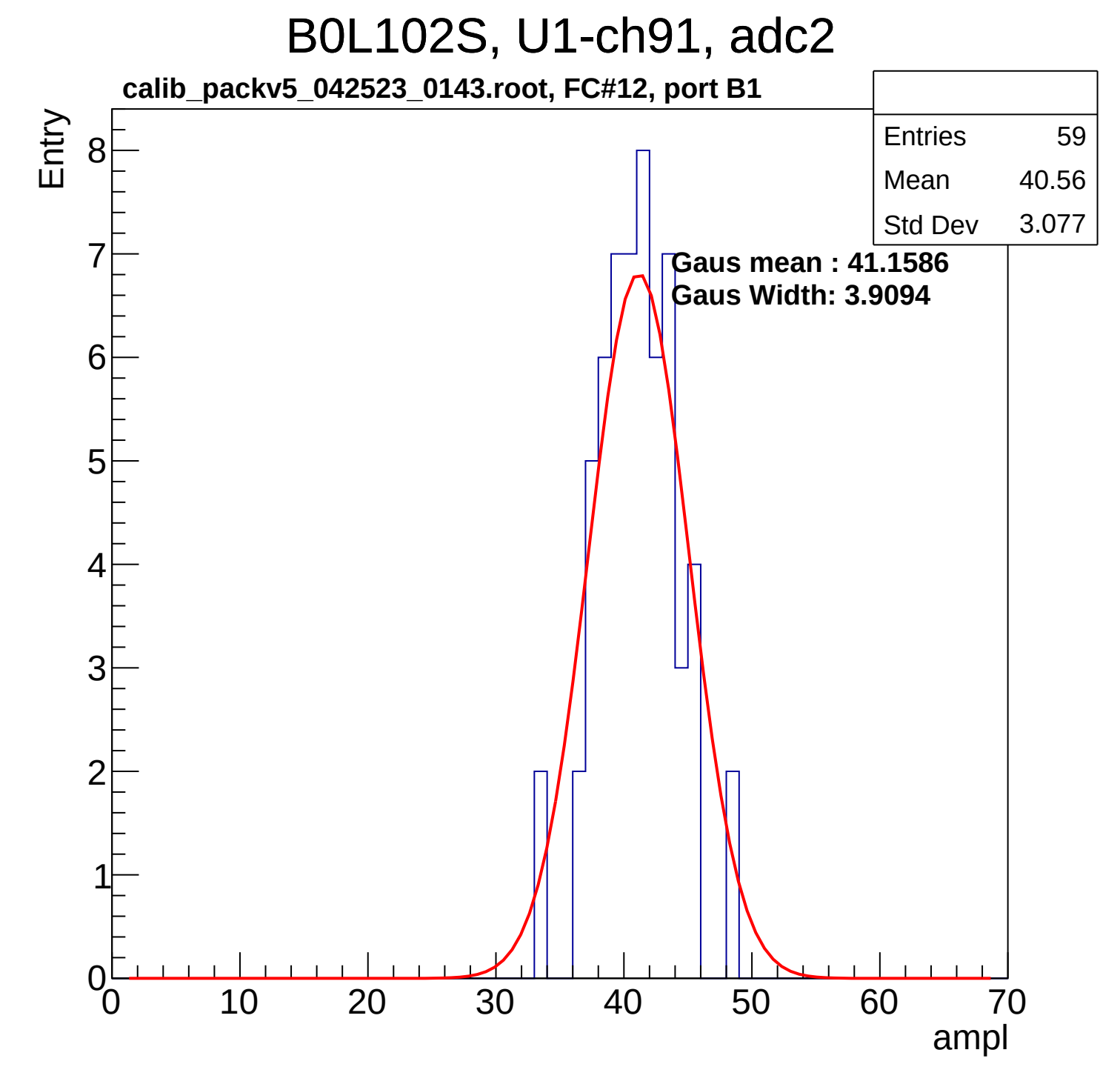

B0L102S, U1-ch90, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 57.78
Std Dev 7.642
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

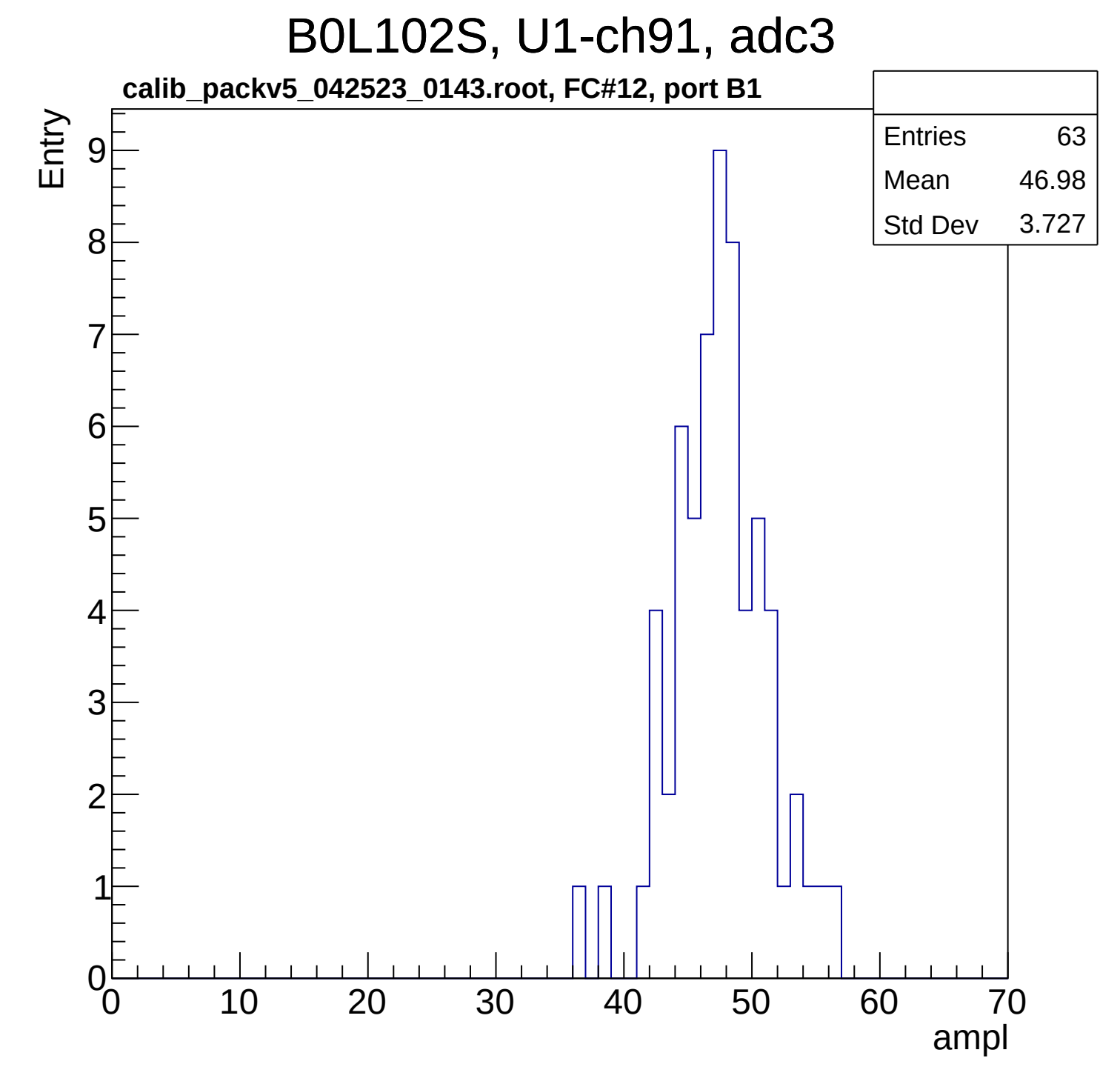

B0L102S, U1-ch90, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.89
Std Dev 1.197
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

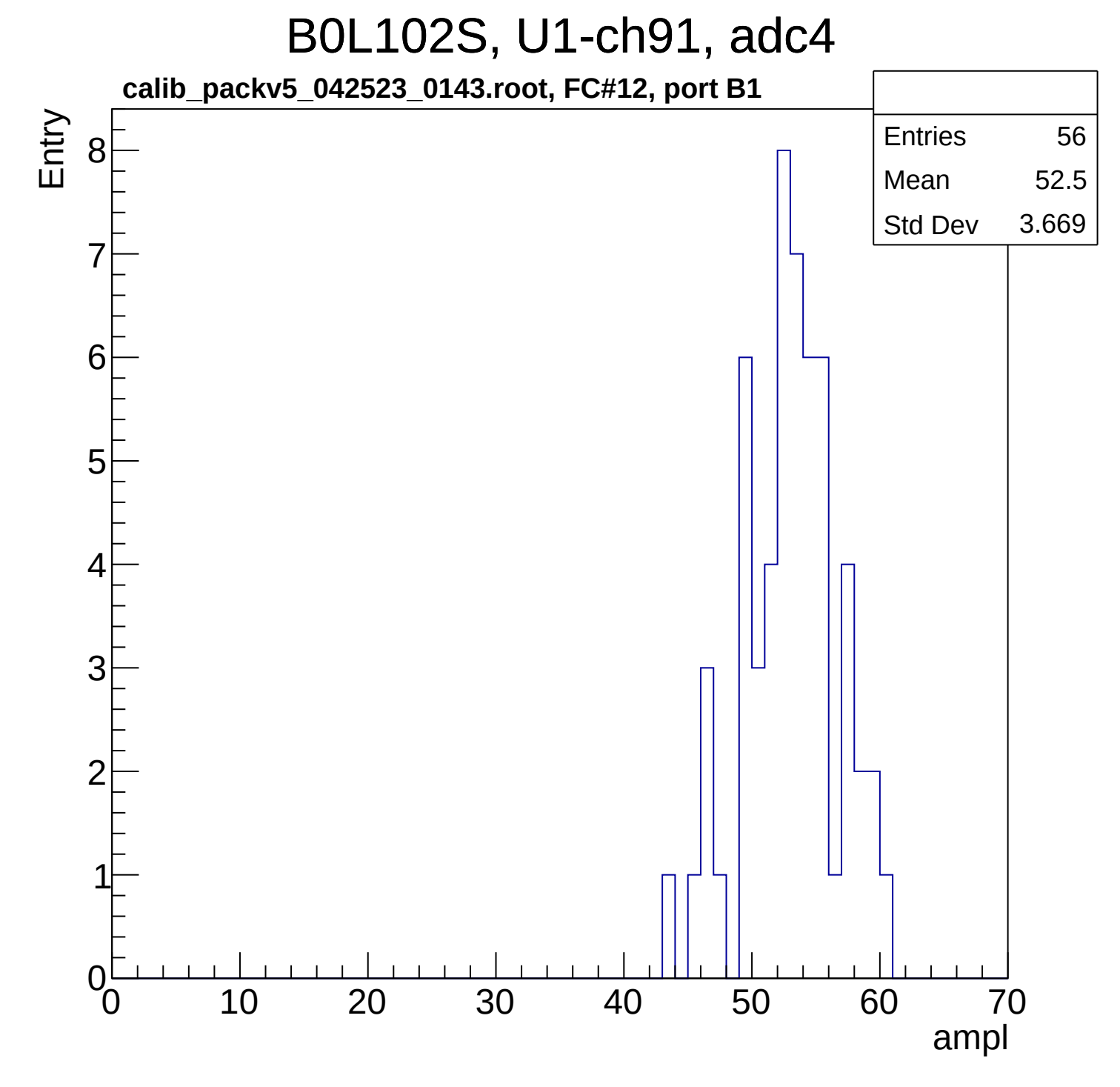

B0L102S, U1-ch90, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

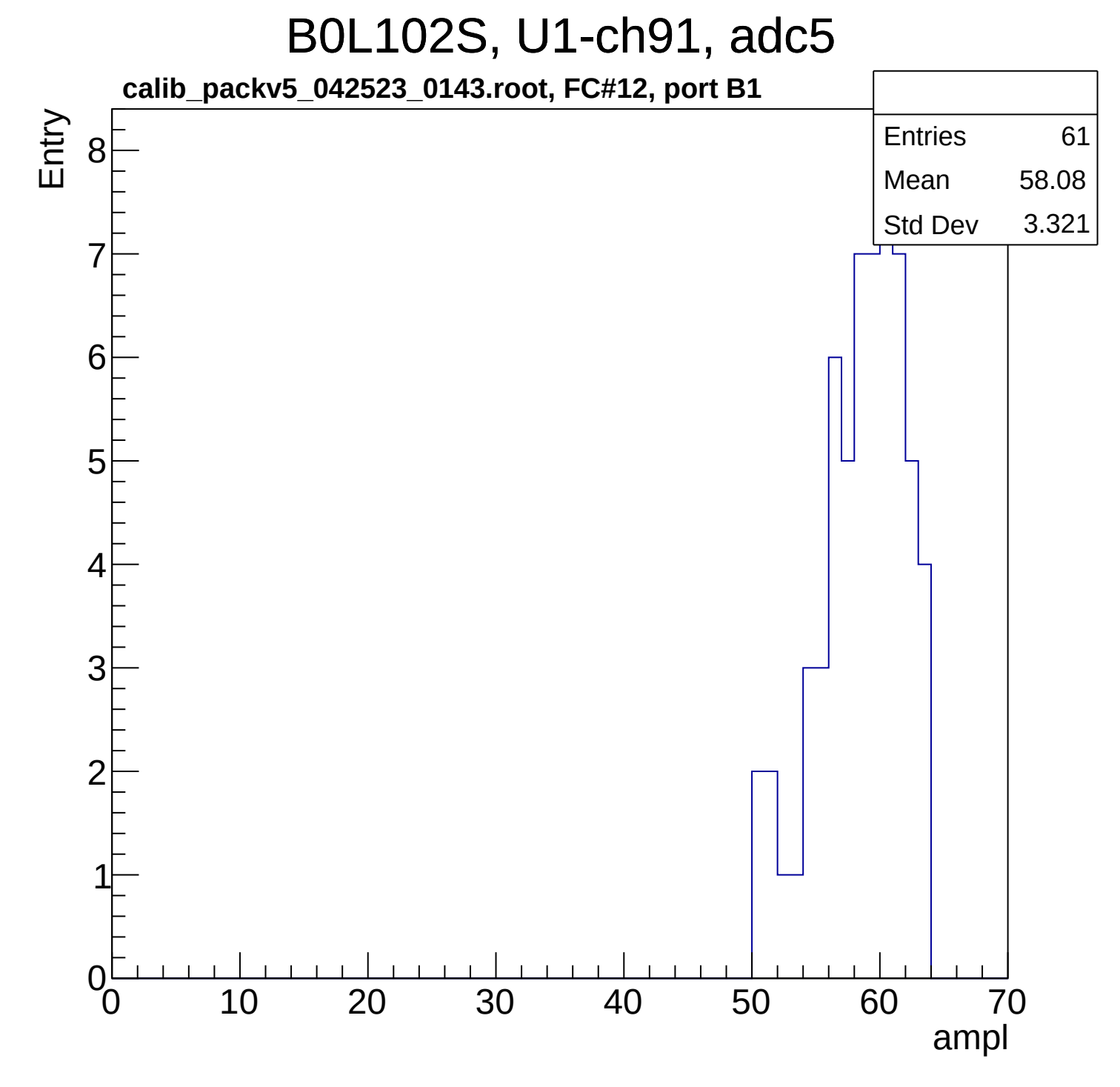

B0L102S, U1-ch91, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 26.12
Std Dev 4.603
Gaus mean : 26.9337
Gaus Width: 3.9266
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

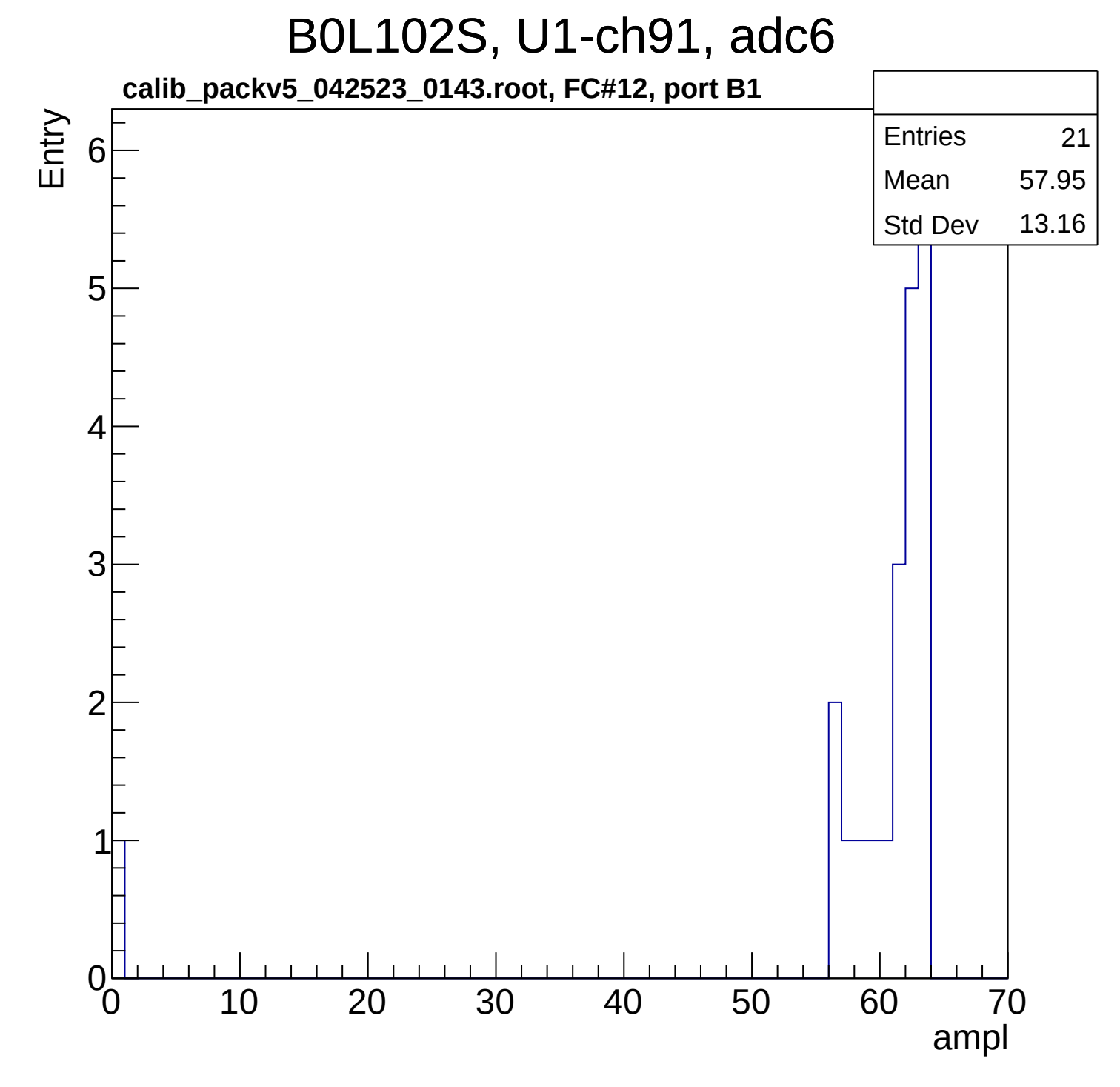

B0L102S, U1-ch91, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 34.35
Std Dev 3.396
Gaus mean : 34.8134
Gaus Width: 3.5115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

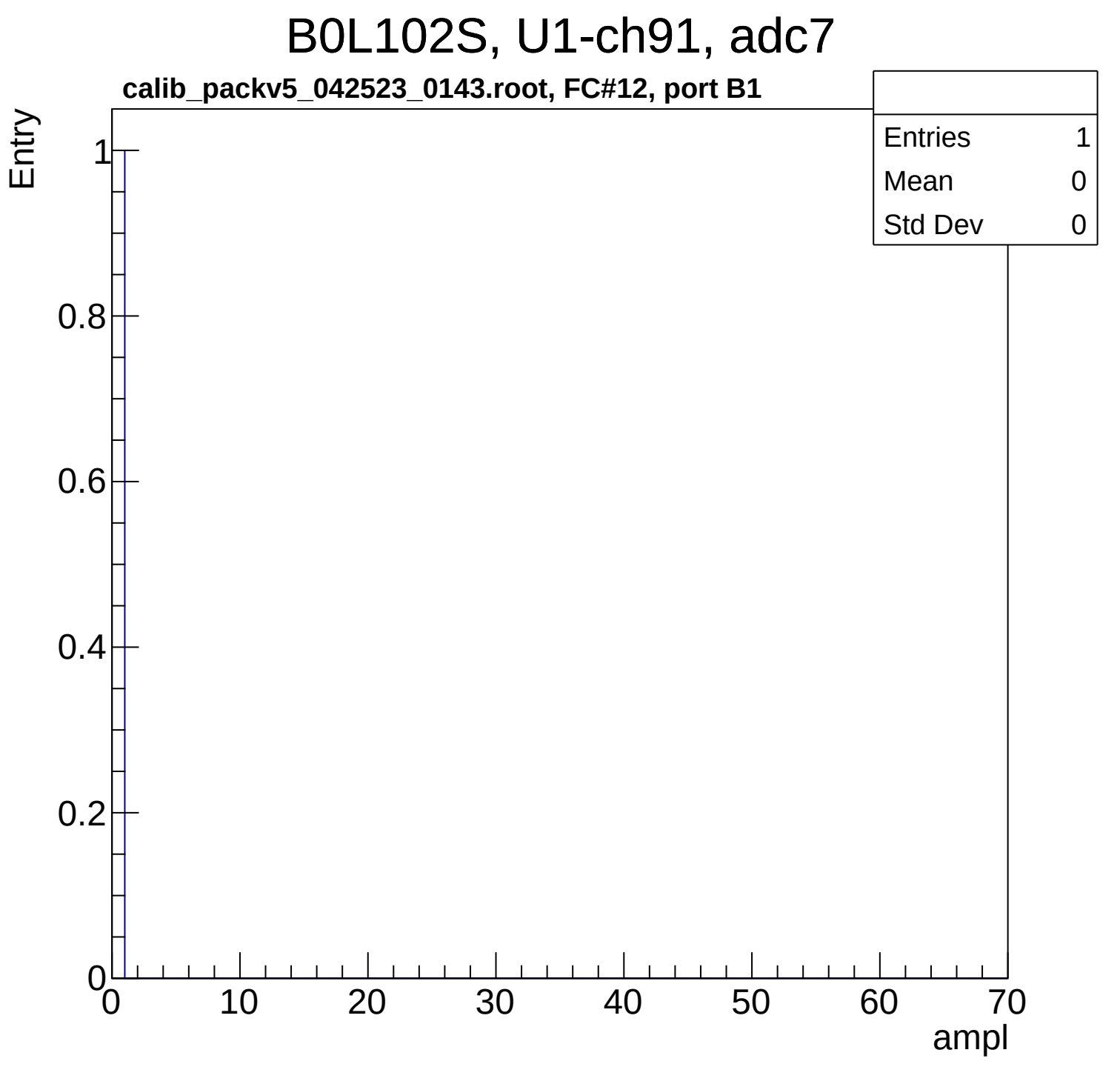

B0L102S, U1-ch91, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 40.56
Std Dev 3.077
Gaus mean : 41.1586
Gaus Width: 3.9094
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

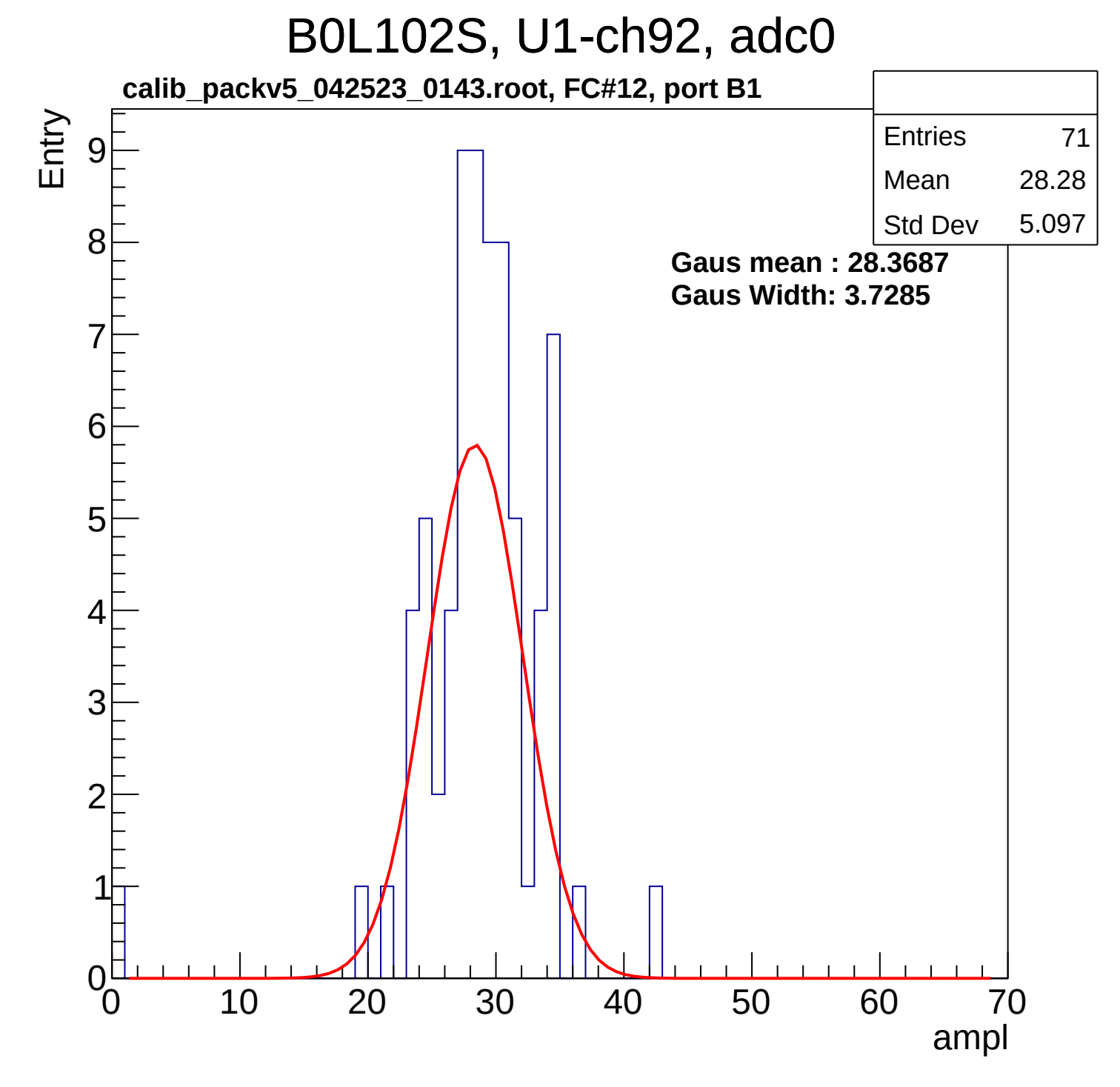

B0L102S, U1-ch91, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 46.98
Std Dev 3.727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

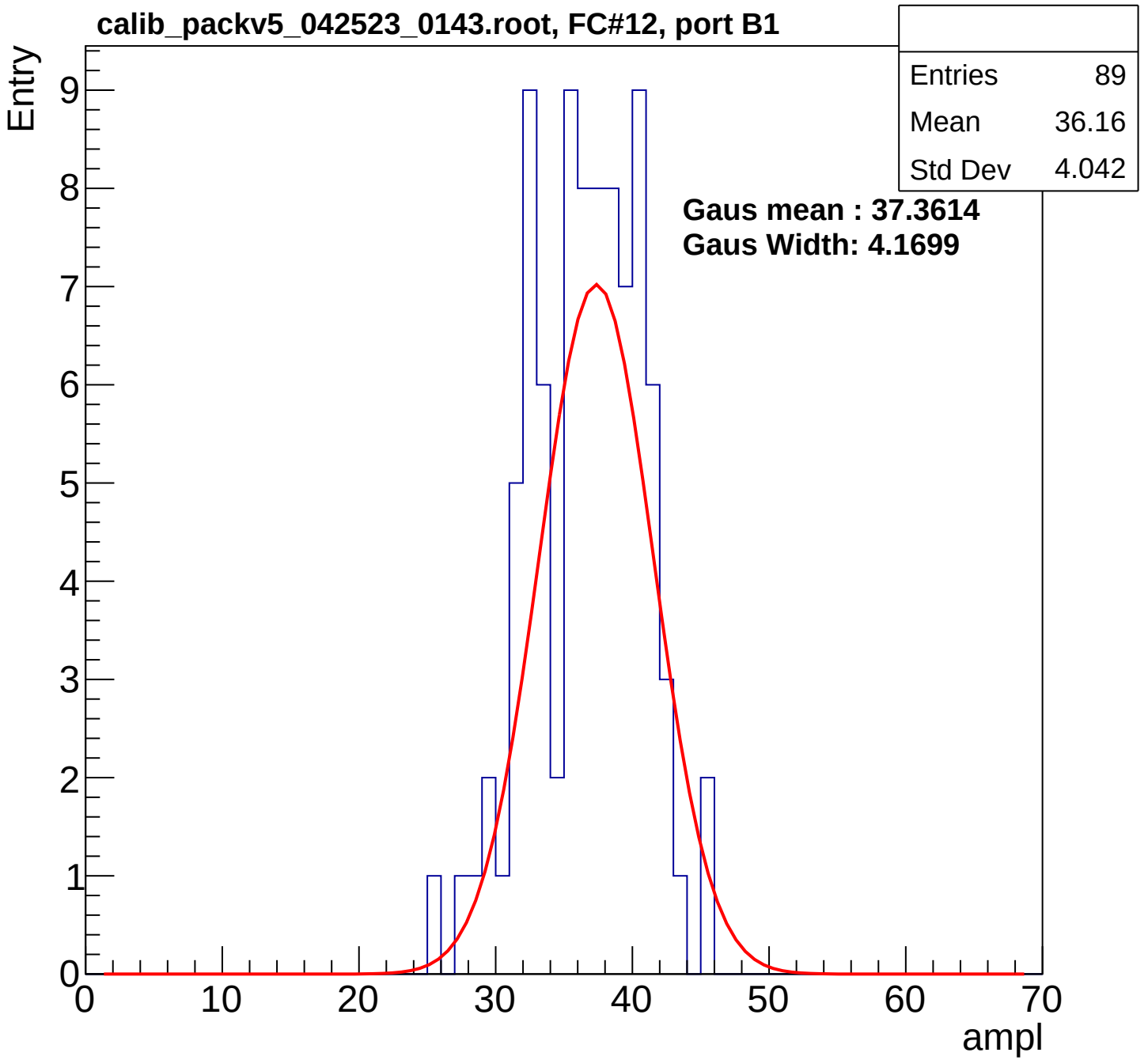

B0L102S, U1-ch91, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 52.5
Std Dev 3.669
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

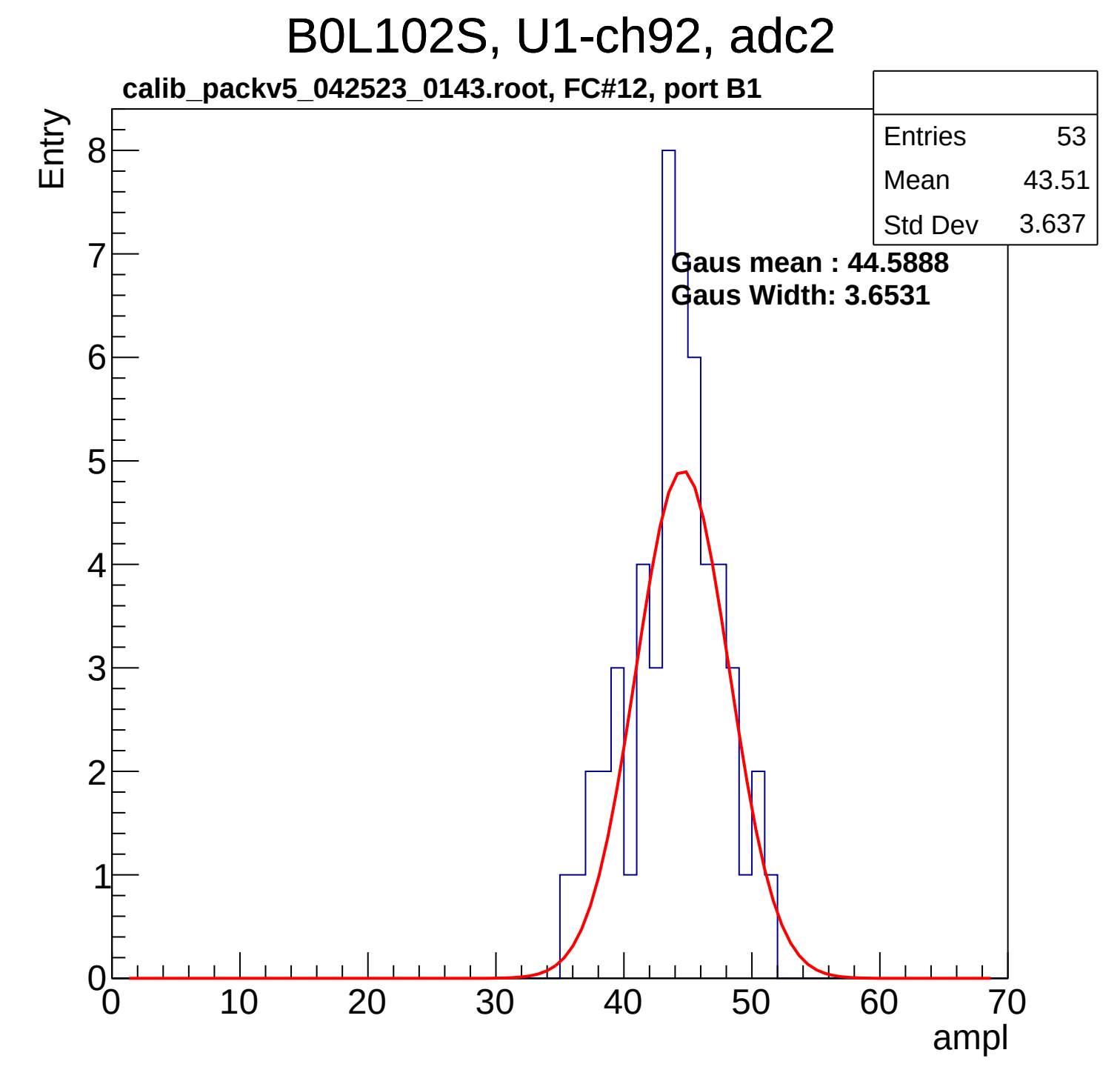

B0L102S, U1-ch91, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 58.08
Std Dev 3.321
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

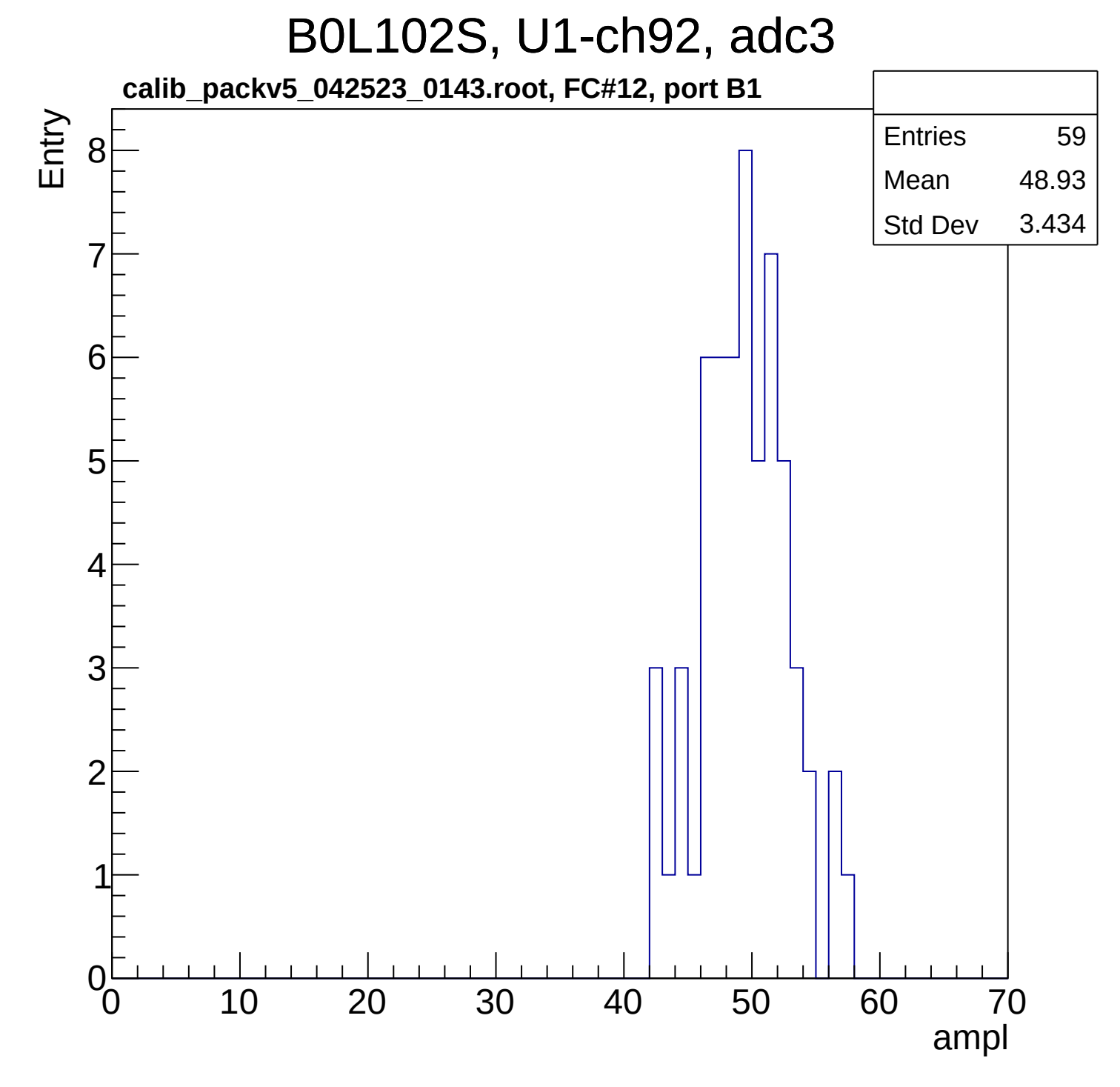

B0L102S, U1-ch91, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 21
Mean 57.95
Std Dev 13.16
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

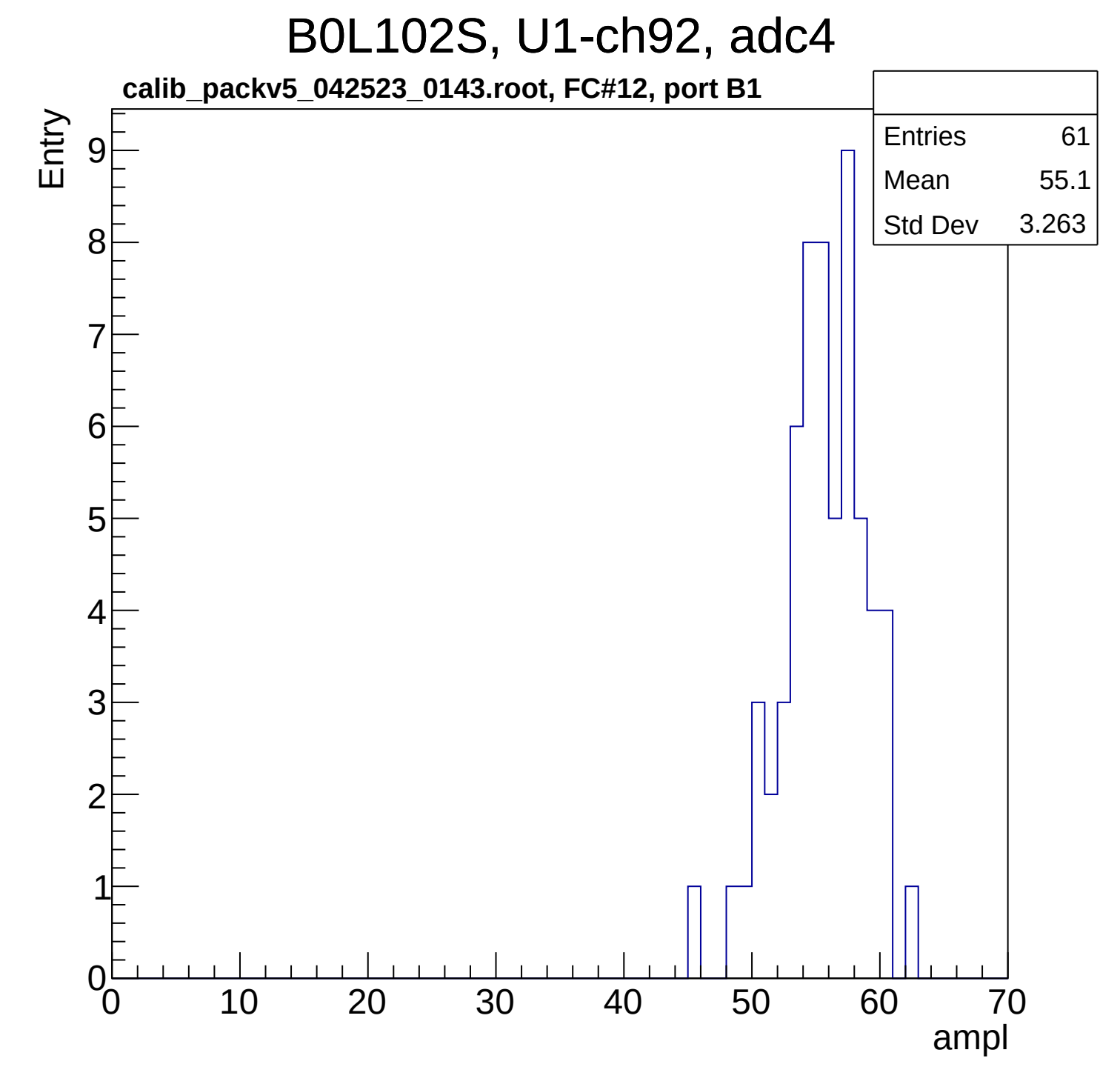

B0L102S, U1-ch91, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

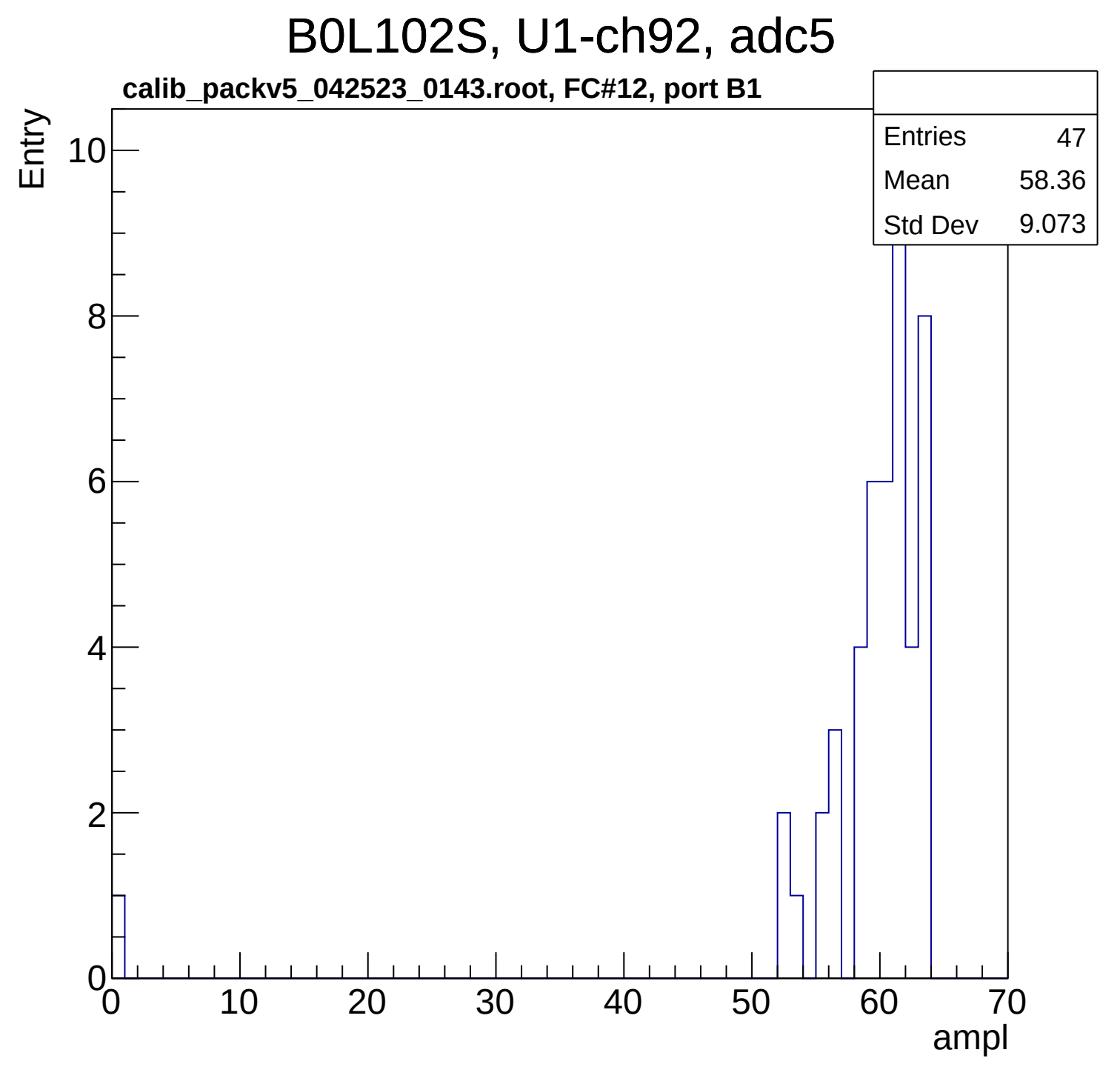

B0L102S, U1-ch92, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 28.28
Std Dev 5.097
Gaus mean : 28.3687
Gaus Width: 3.7285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

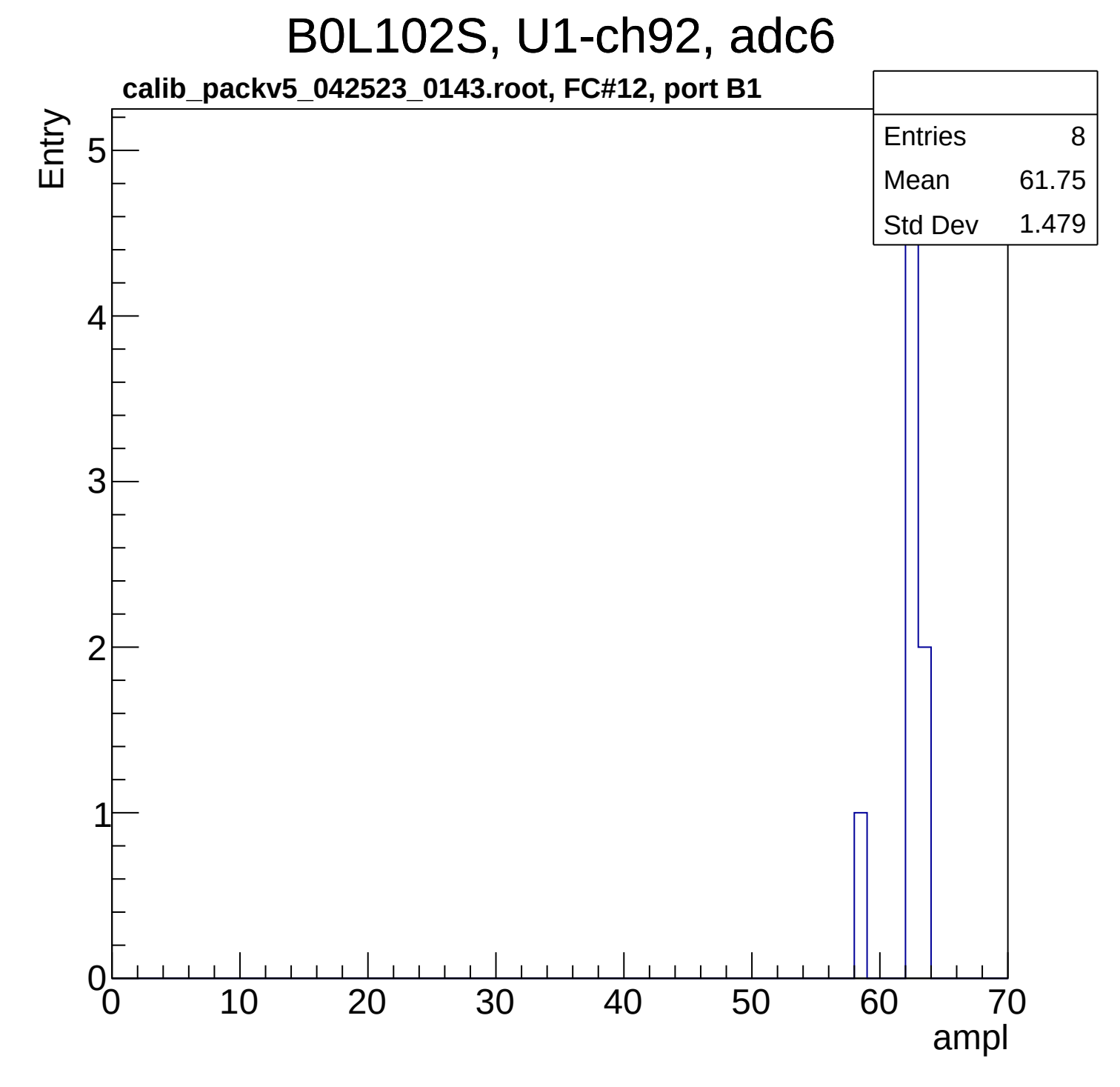

B0L102S, U1-ch92, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 36.16
Std Dev 4.042
Gaus mean : 37.3614
Gaus Width: 4.1699
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

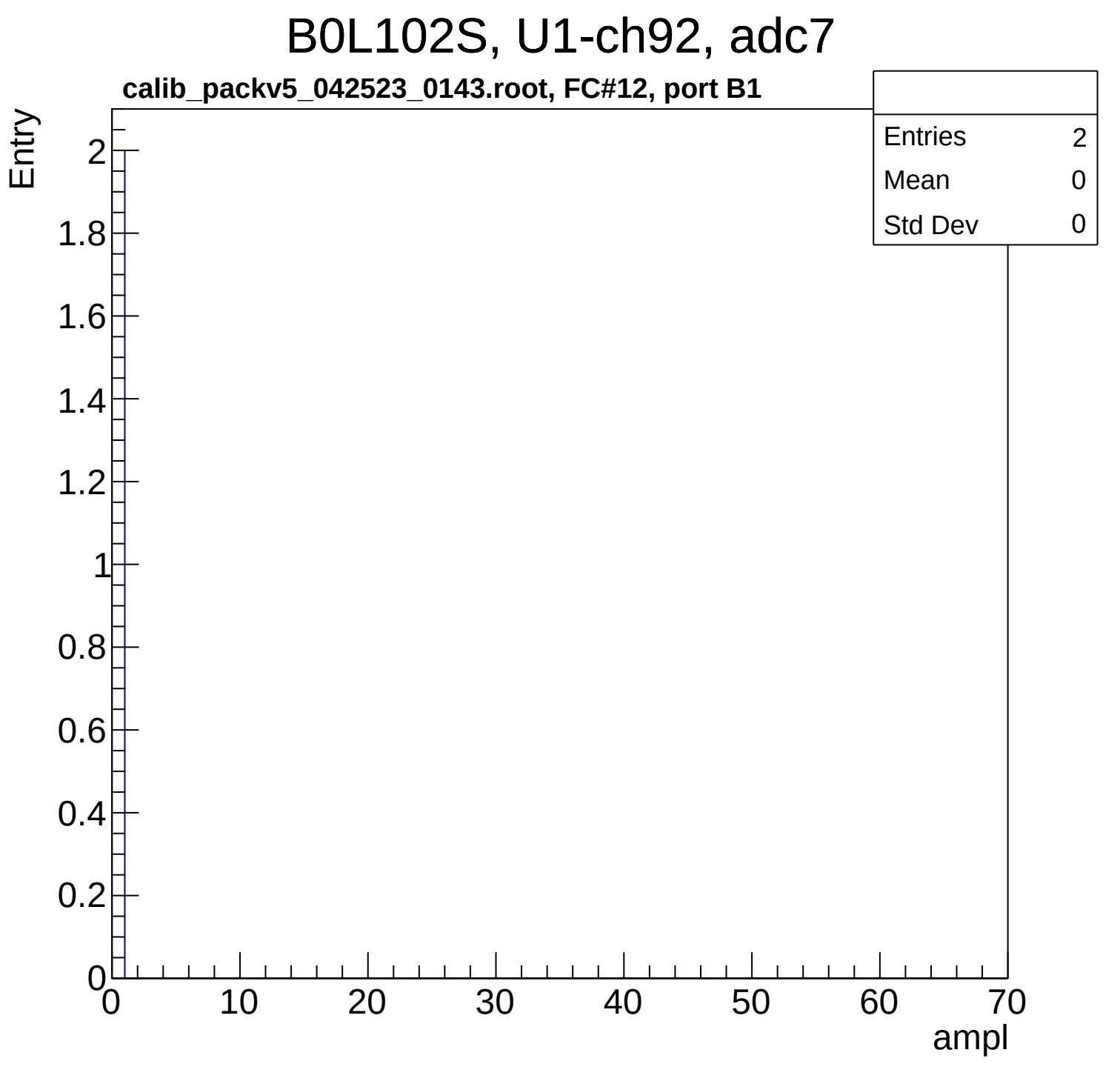

B0L102S, U1-ch92, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 43.51
Std Dev 3.637
Gaus mean : 44.5888
Gaus Width: 3.6531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

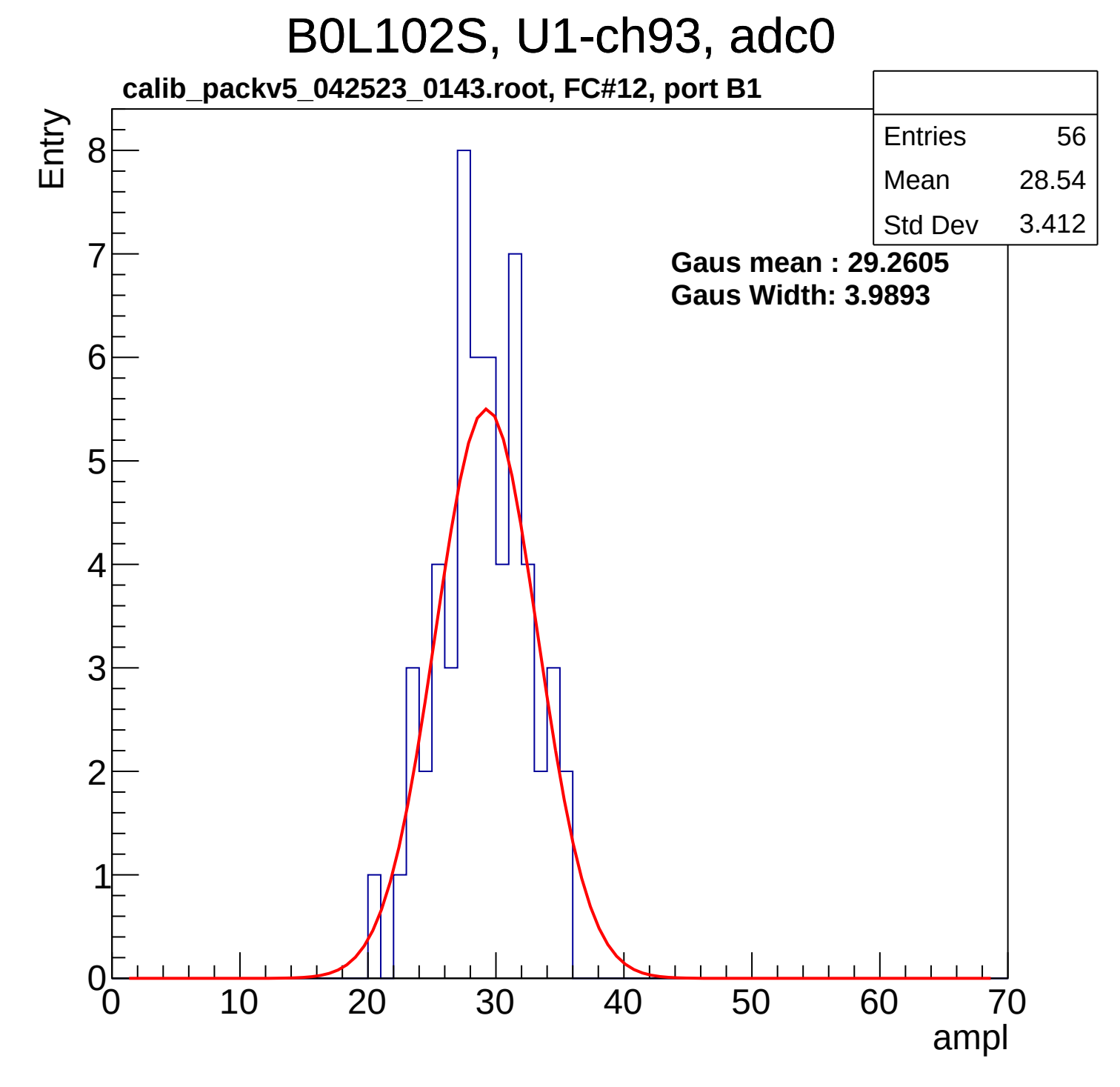

B0L102S, U1-ch92, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.93
Std Dev 3.434
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch92, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.1
Std Dev 3.263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch92, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.36
Std Dev 9.073
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch92, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.75
Std Dev 1.479
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U1-ch92, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch93, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 28.54
Std Dev 3.412
Gaus mean : 29.2605
Gaus Width: 3.9893
Entry
ampl
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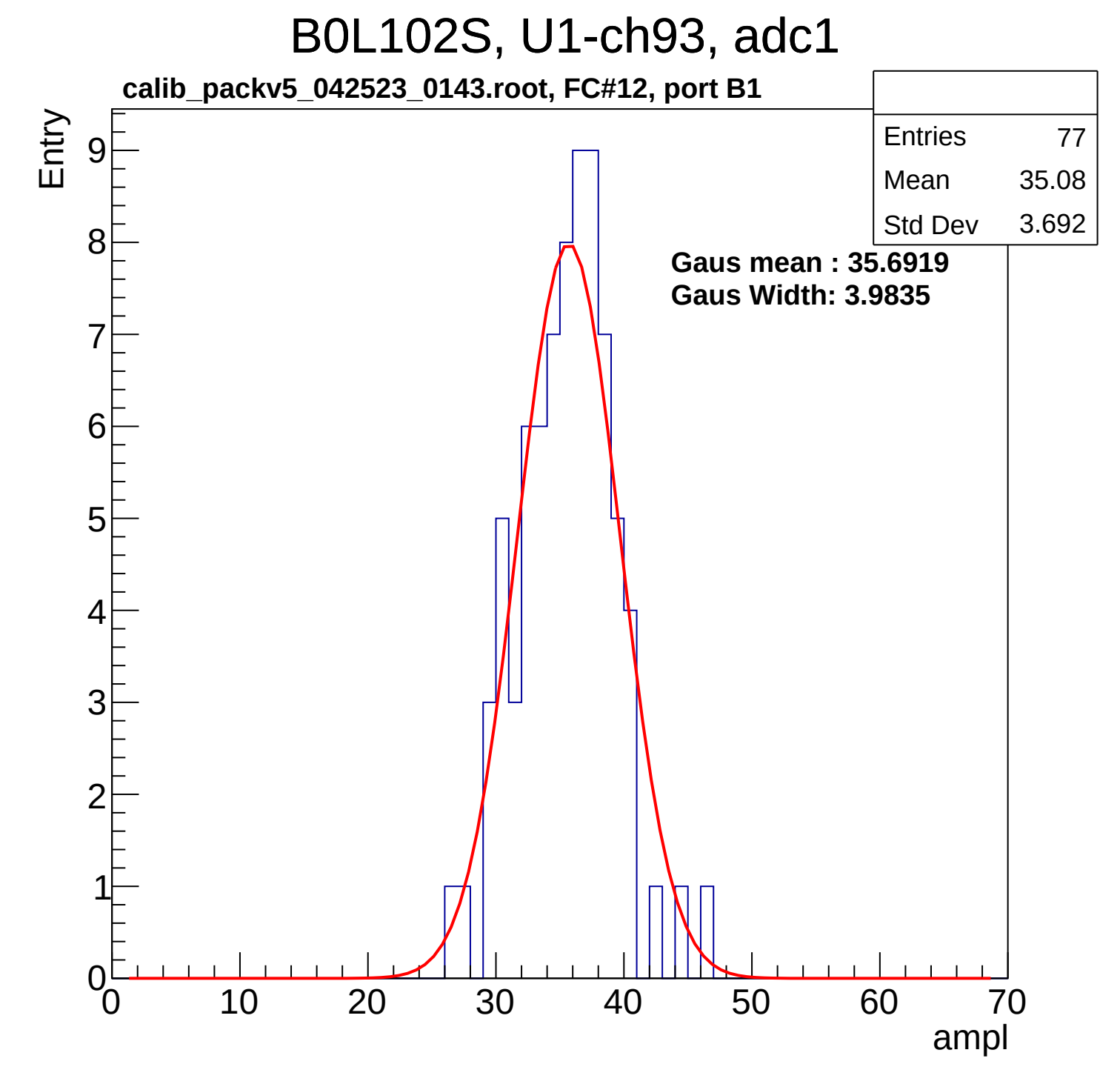
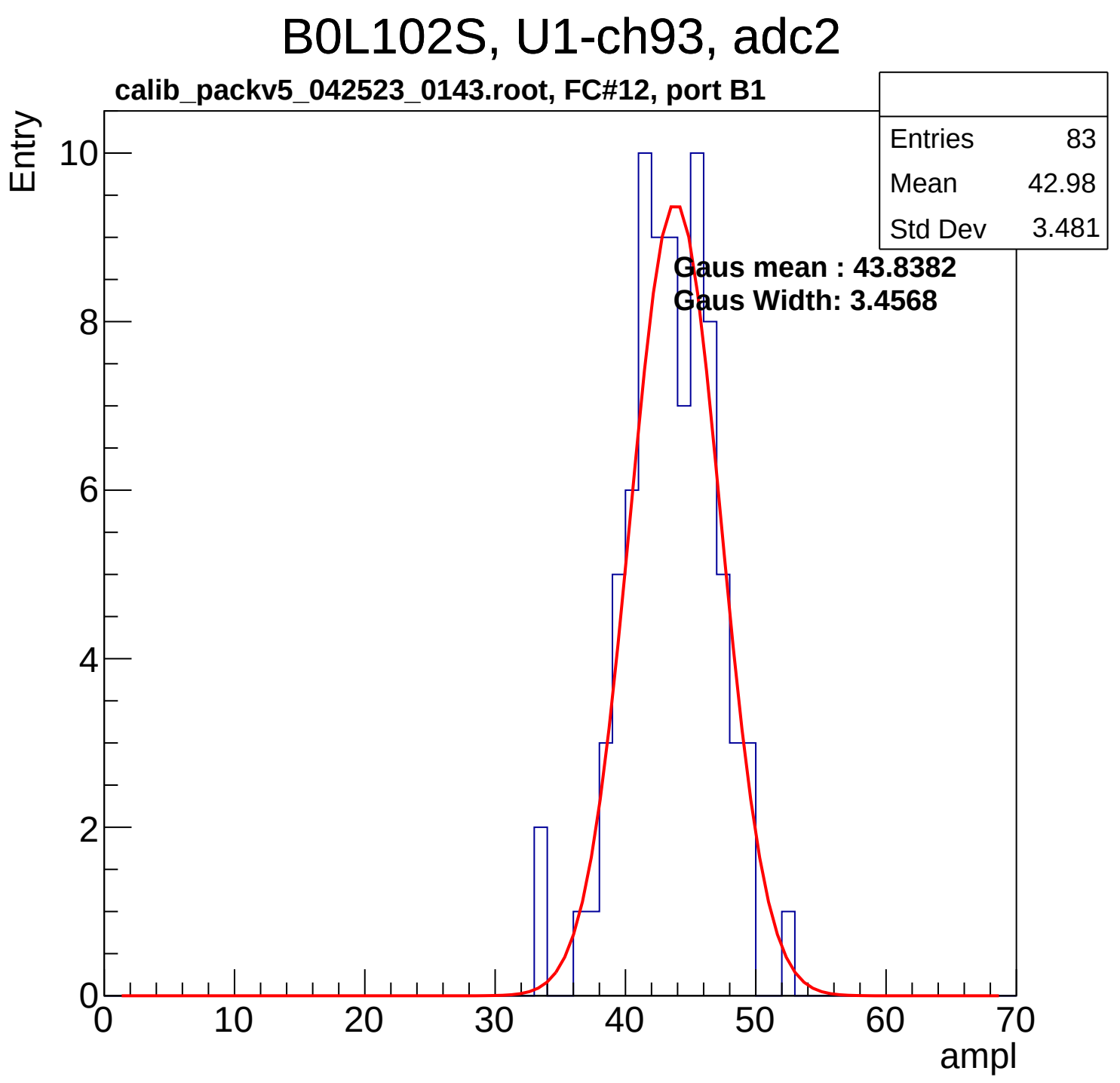
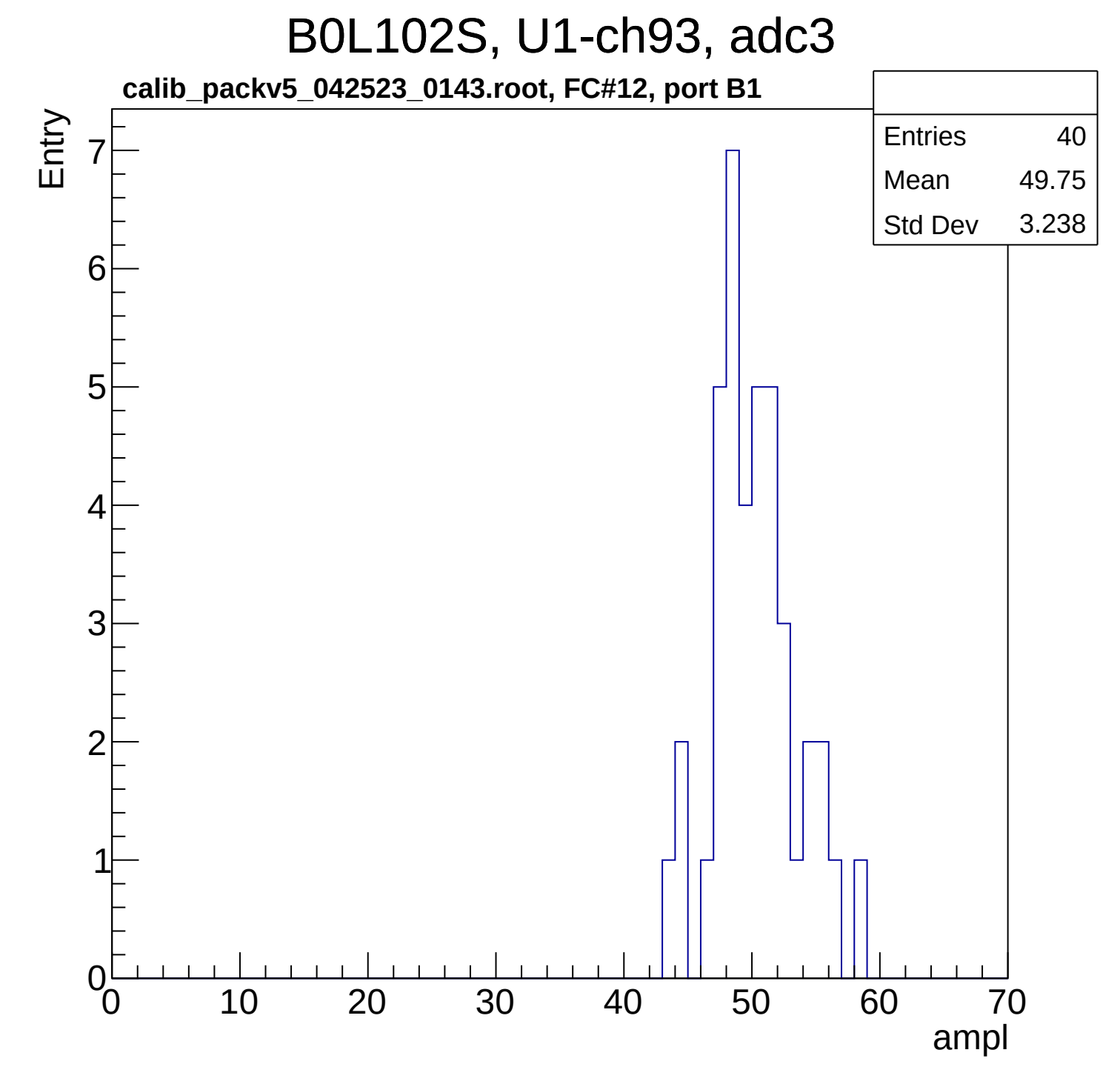
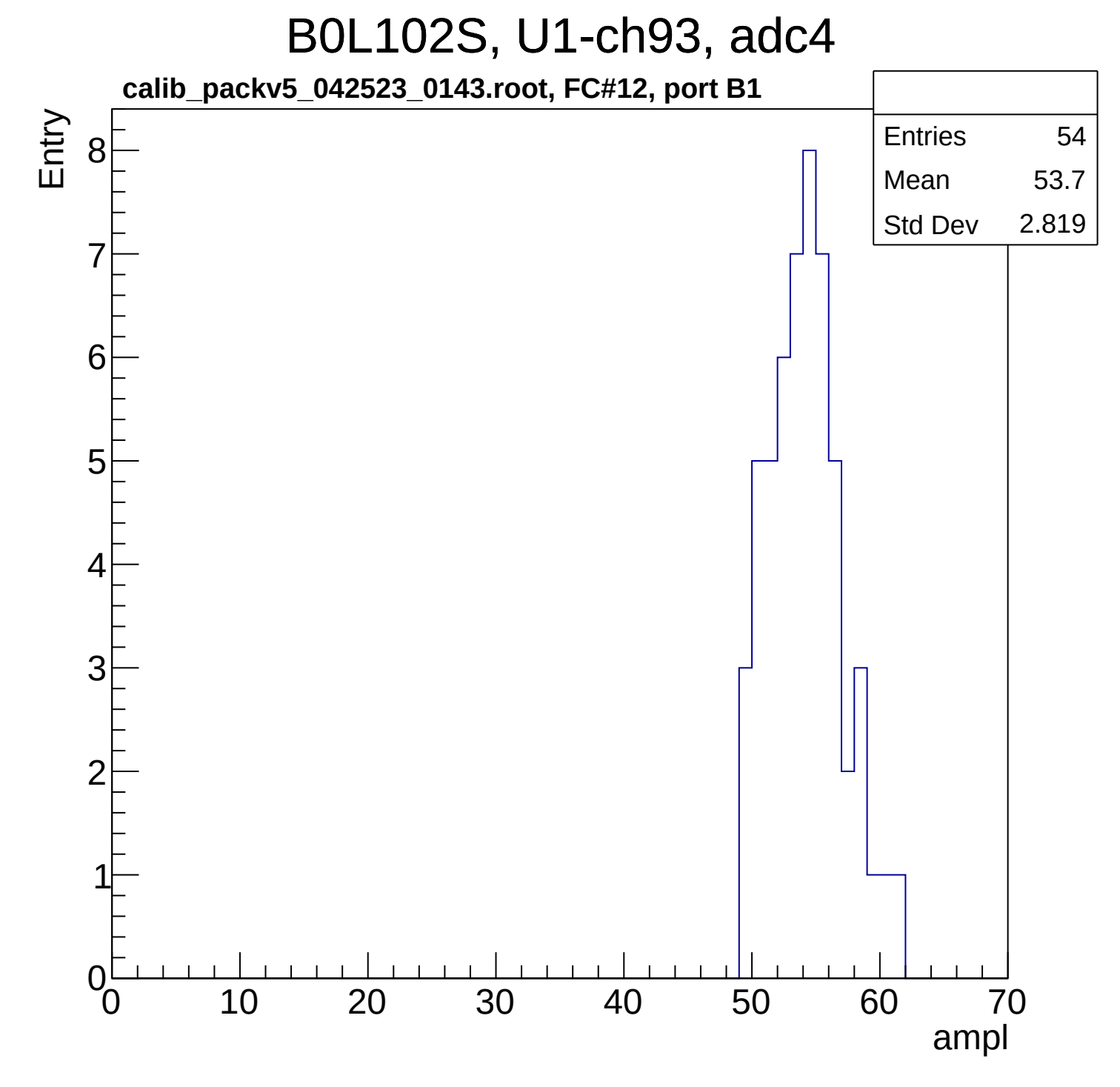
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch93, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 35.08
Std Dev 3.692
Gaus mean : 35.6919
Gaus Width: 3.9835
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch93, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 42.98
Std Dev 3.481
Gaus mean : 43.8382
Gaus Width: 3.4568
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch93, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 49.75
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch93, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 53.7
Std Dev 2.819
Entry
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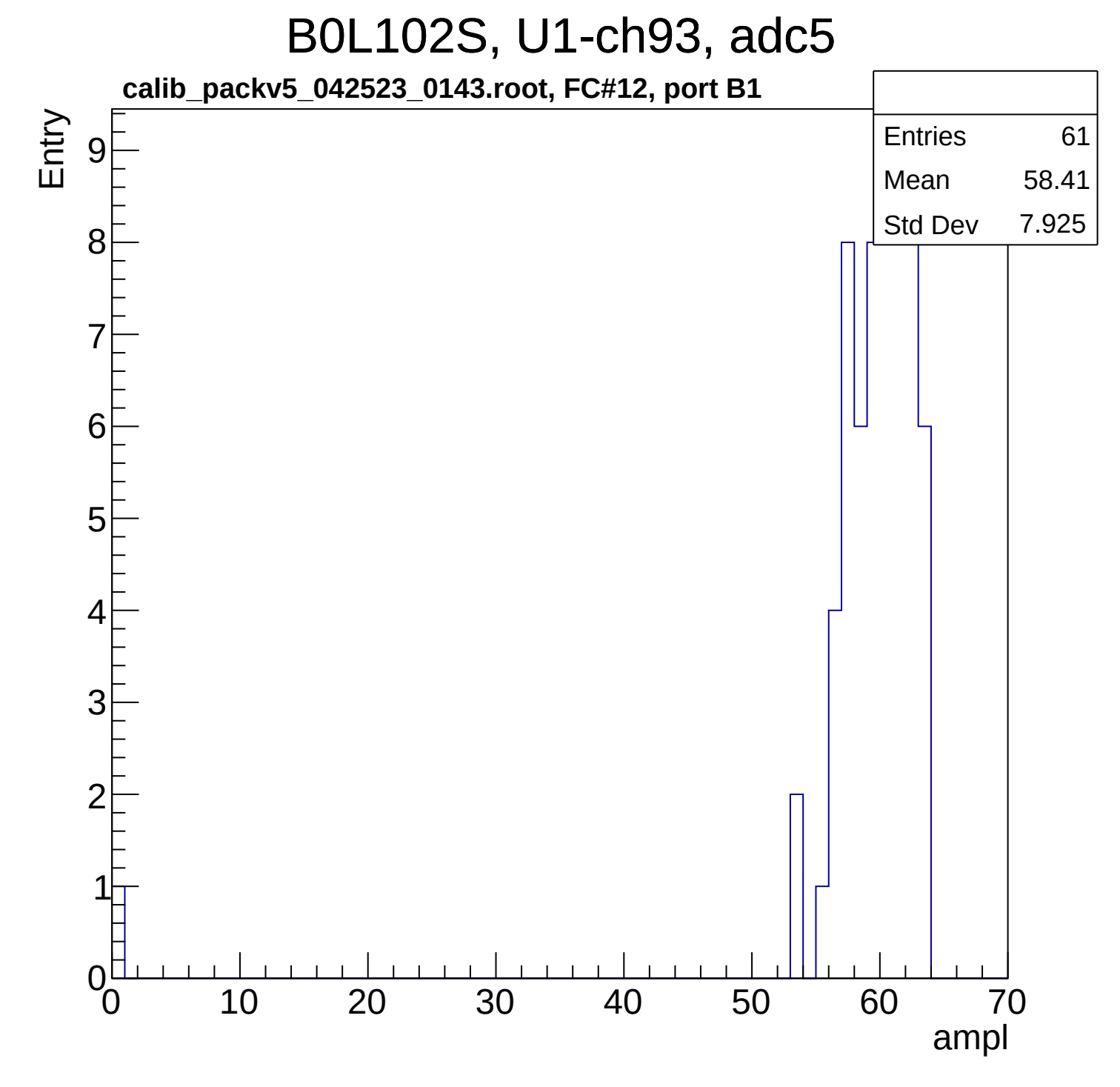
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch93, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 58.41
Std Dev 7.925
Entry
ampl
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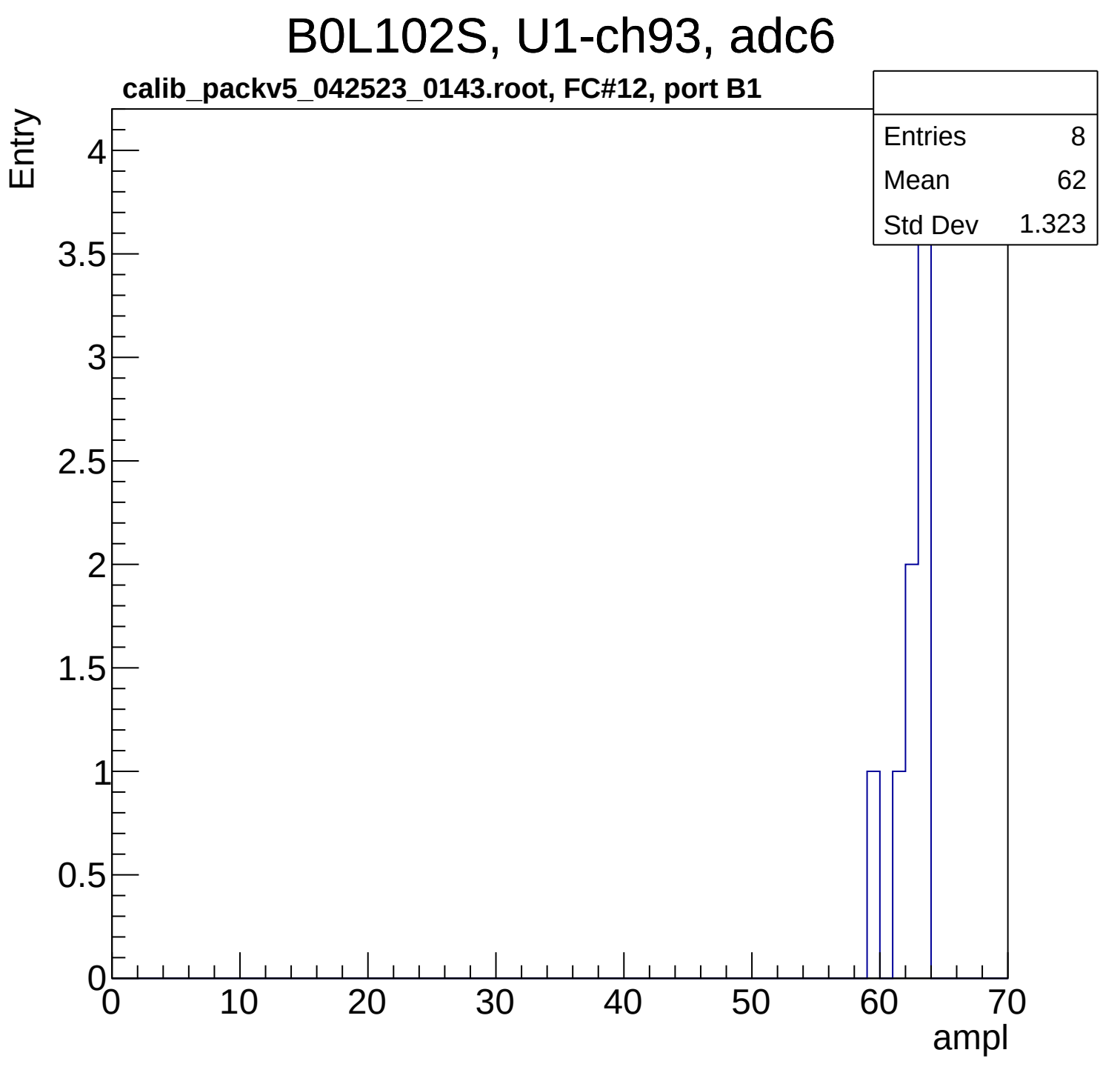
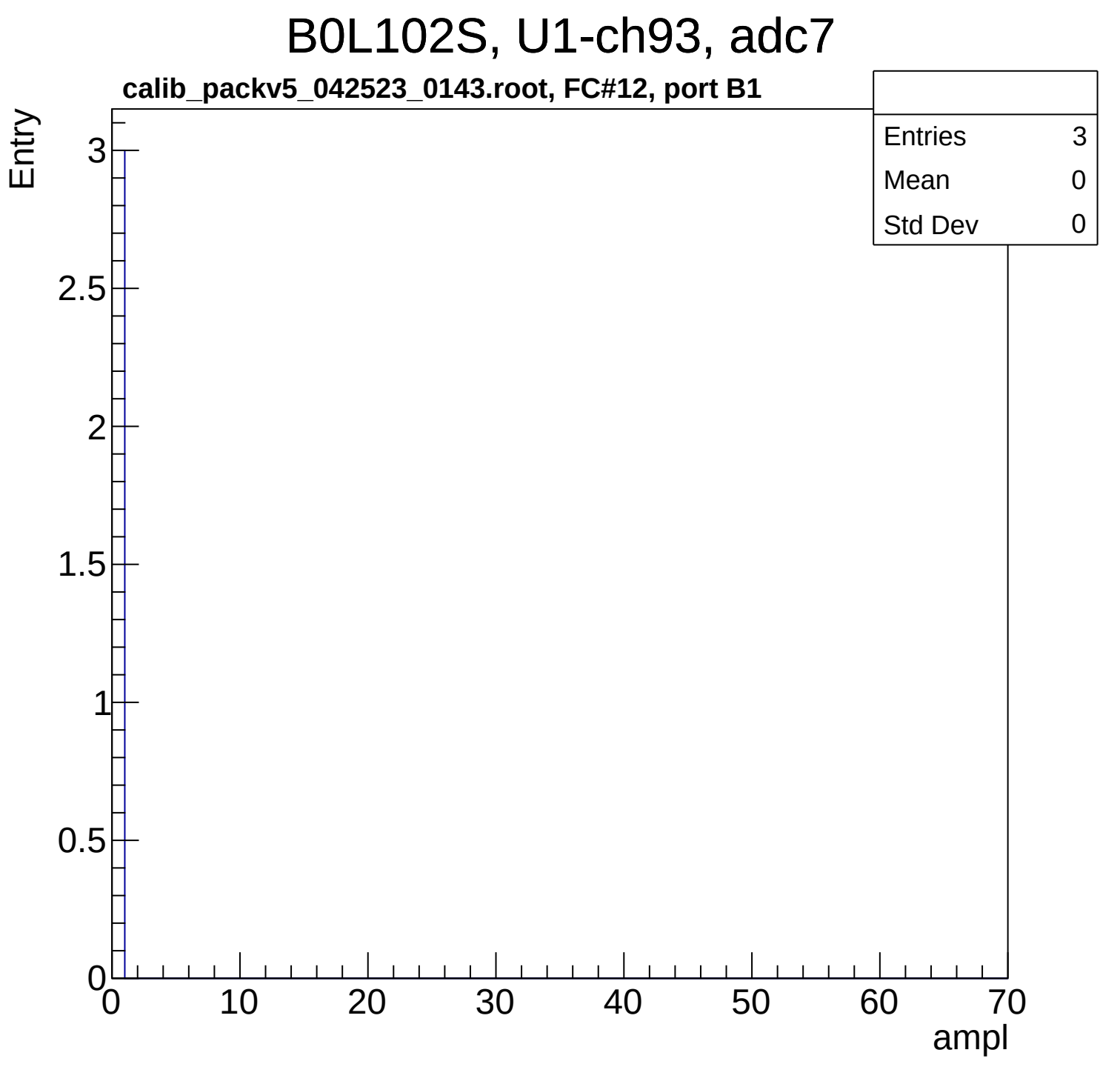
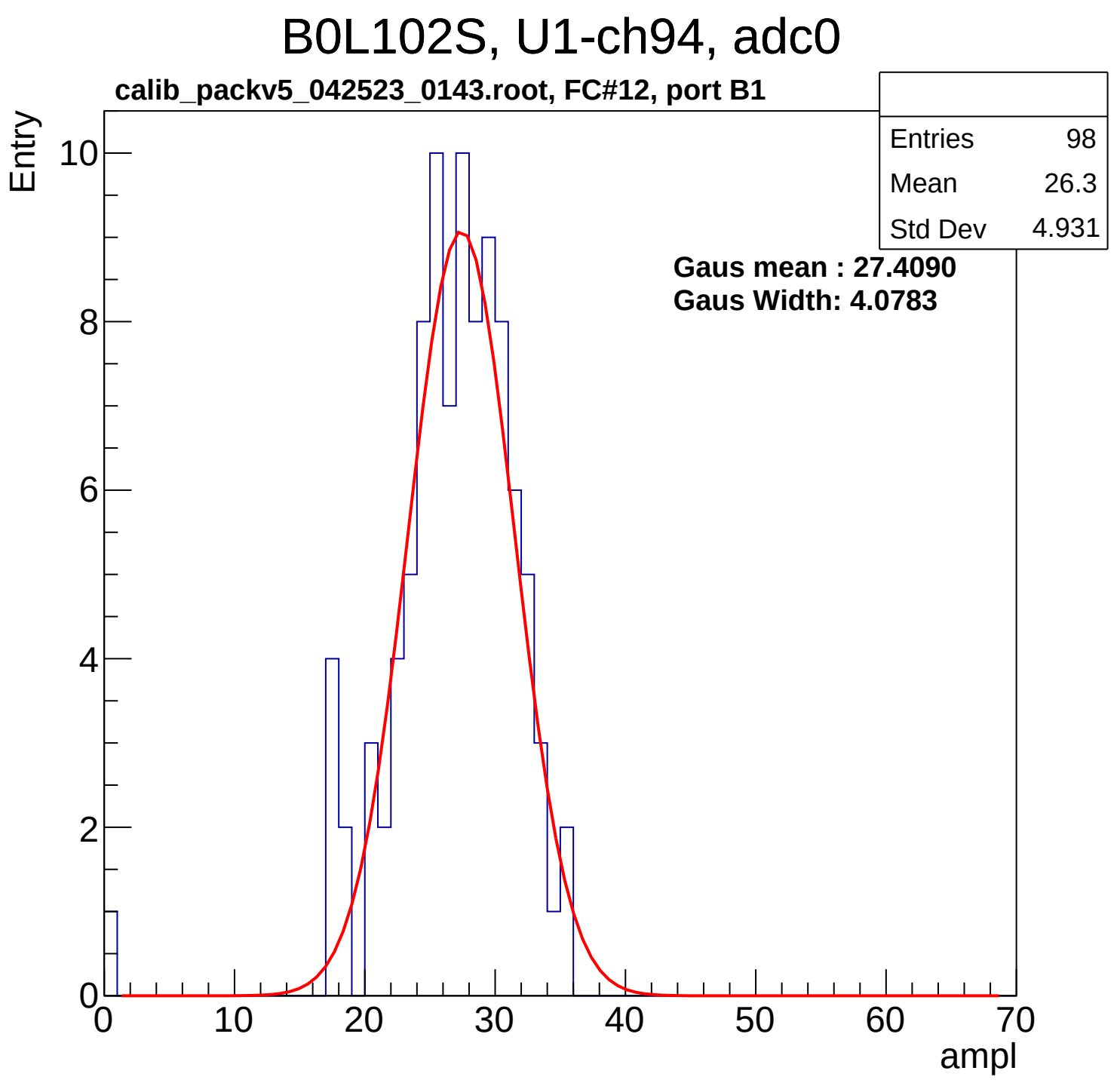
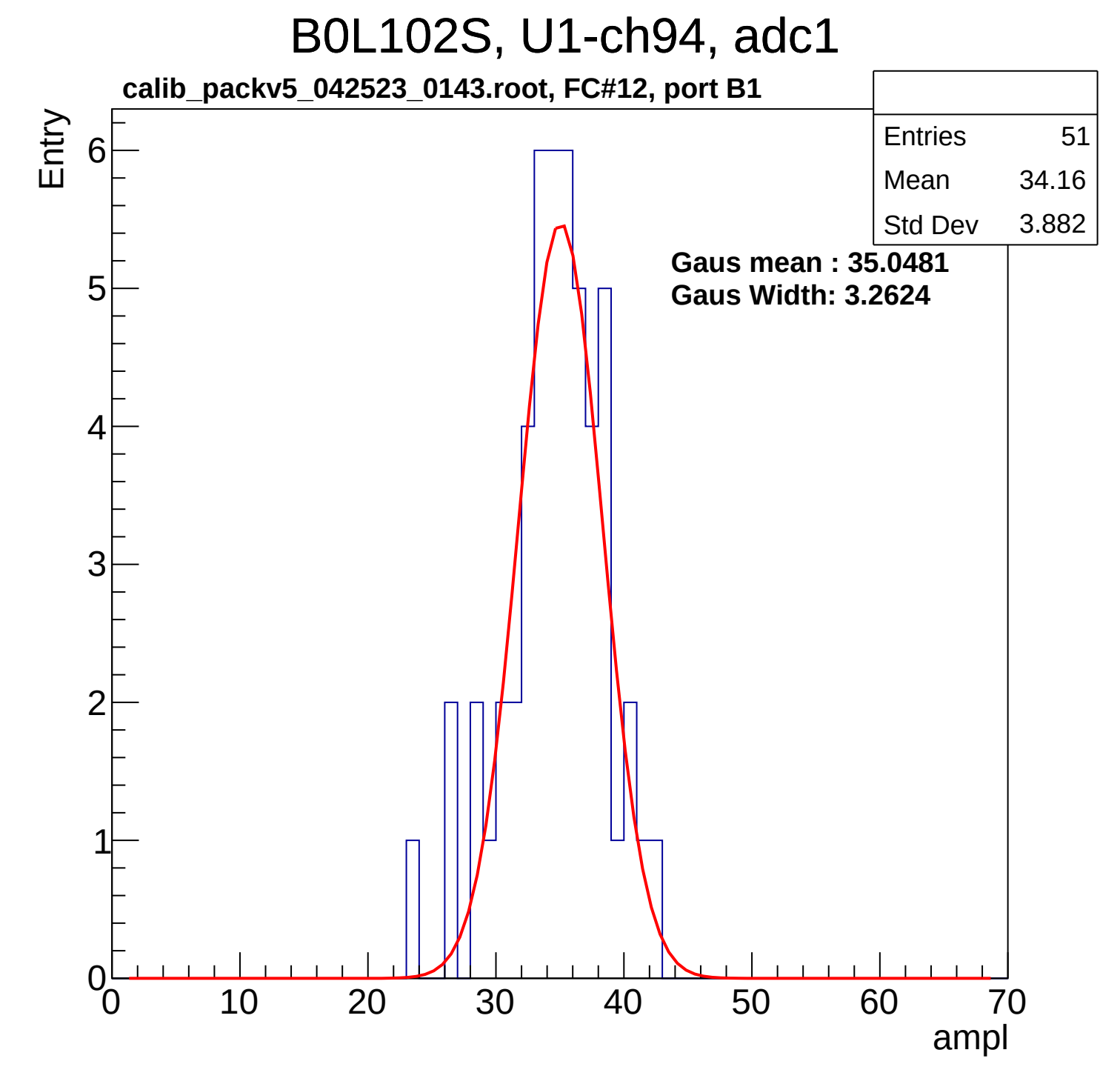
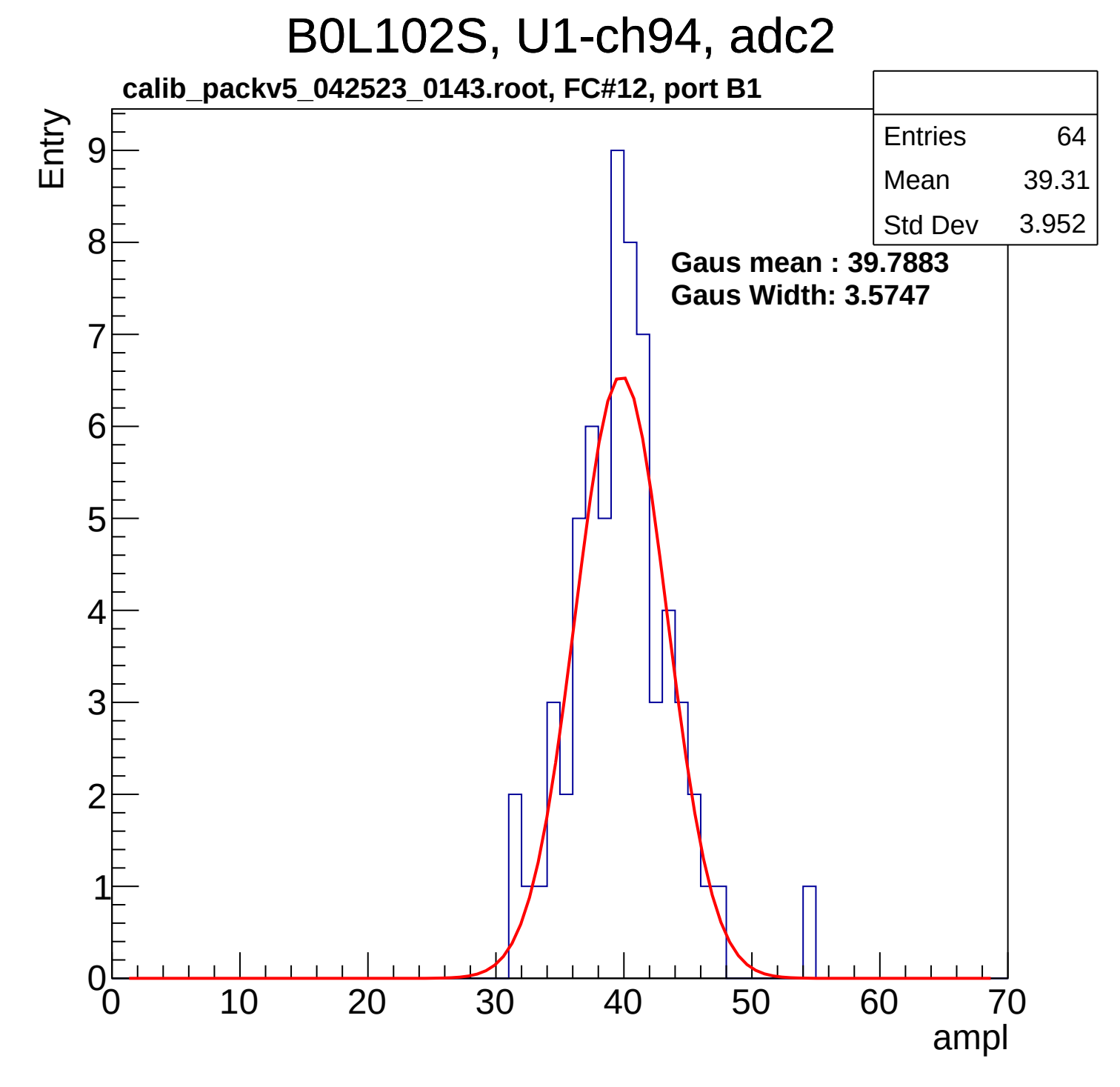
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

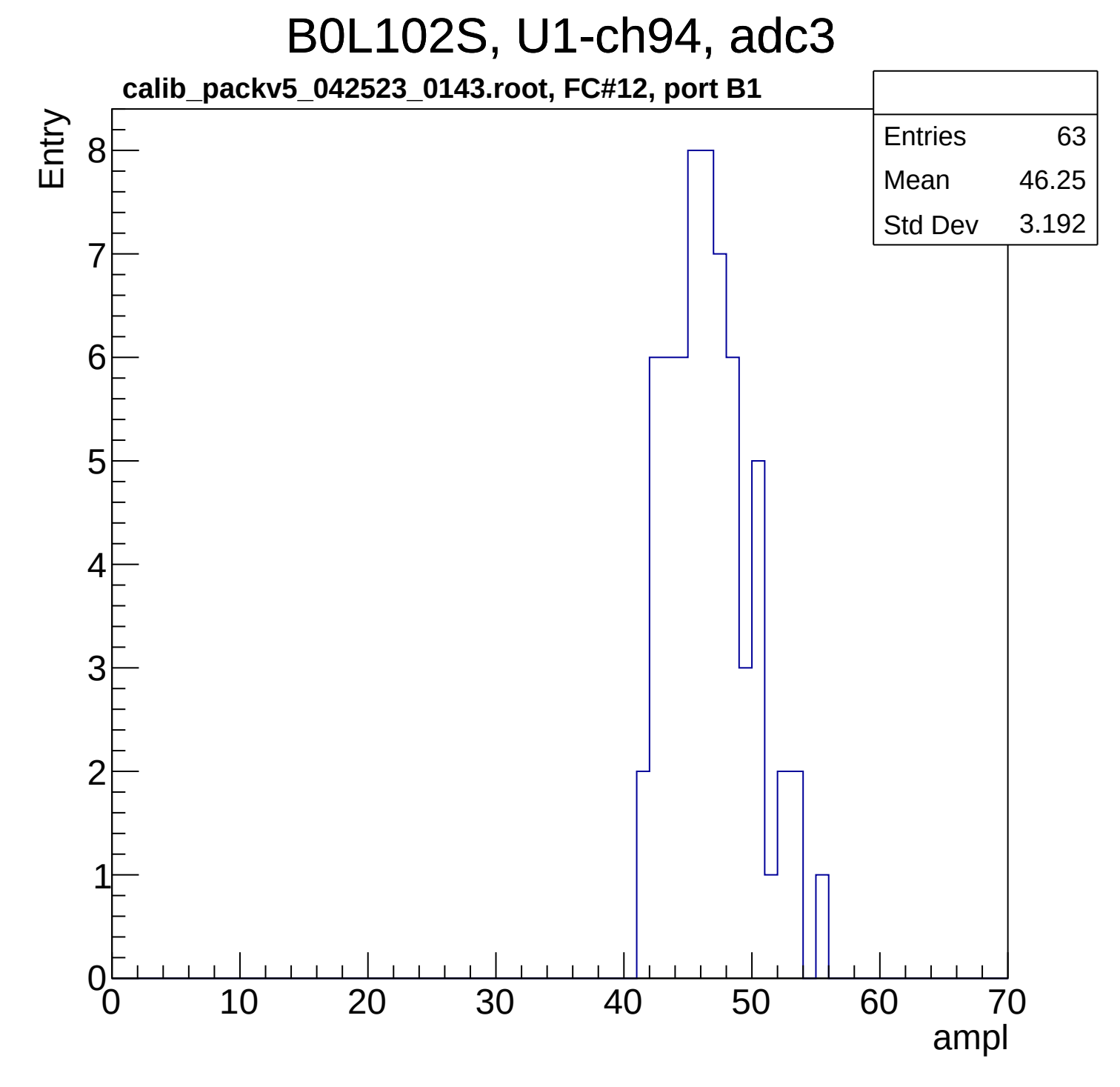

B0L102S, U1-ch93, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62
Std Dev 1.323
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

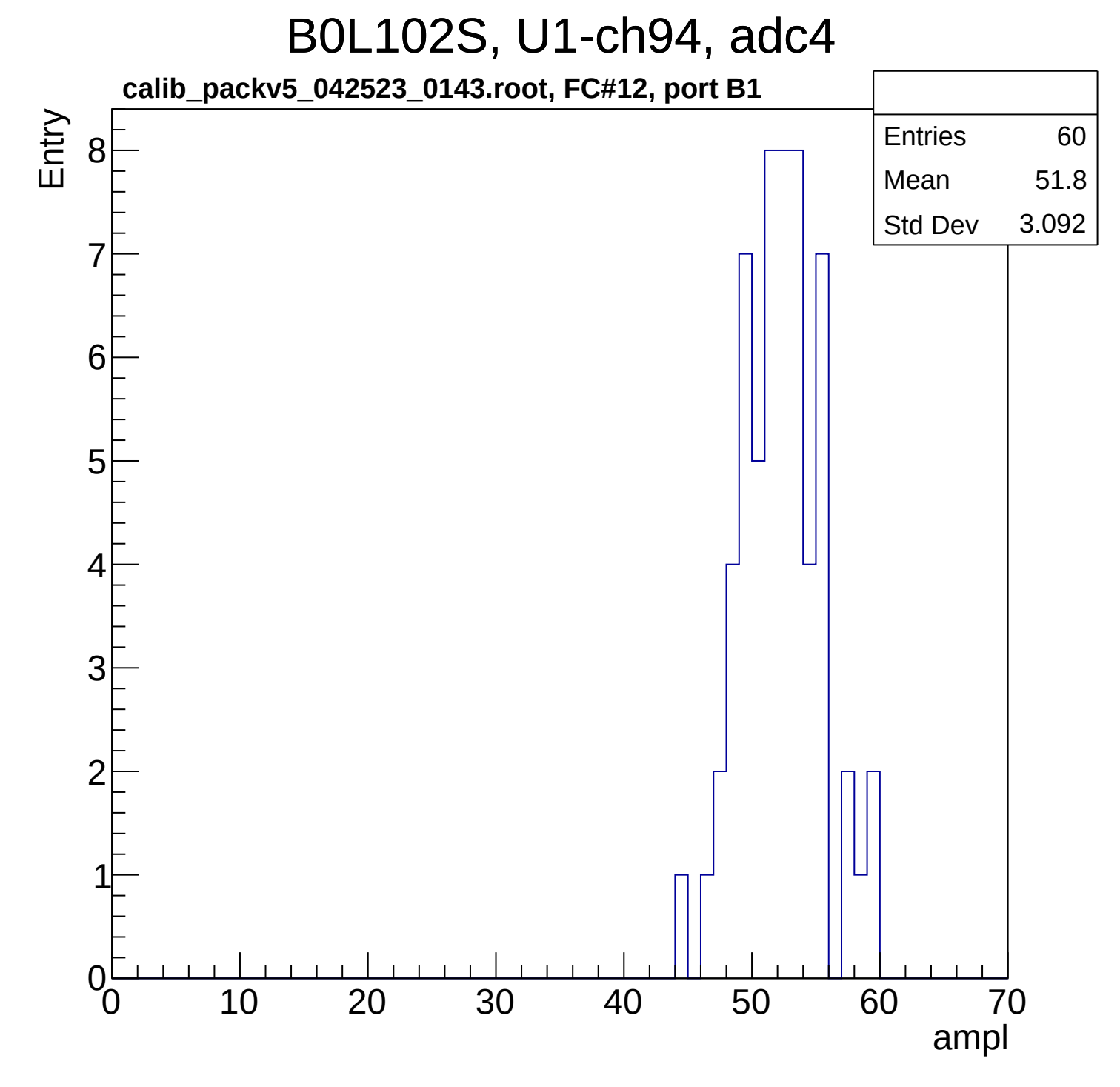

B0L102S, U1-ch93, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

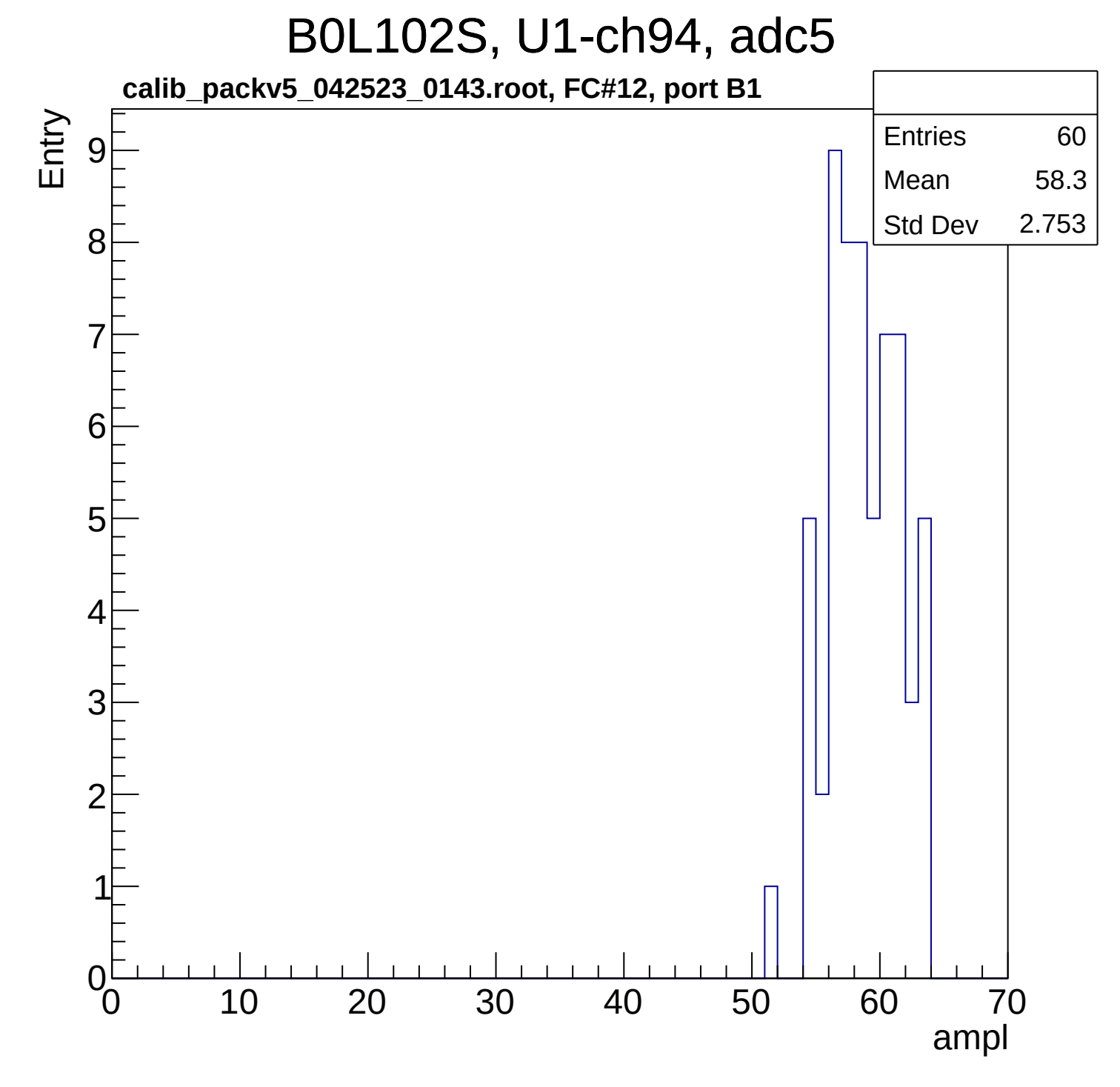

B0L102S, U1-ch94, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 98
Mean 26.3
Std Dev 4.931
Gaus mean : 27.4090
Gaus Width: 4.0783
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

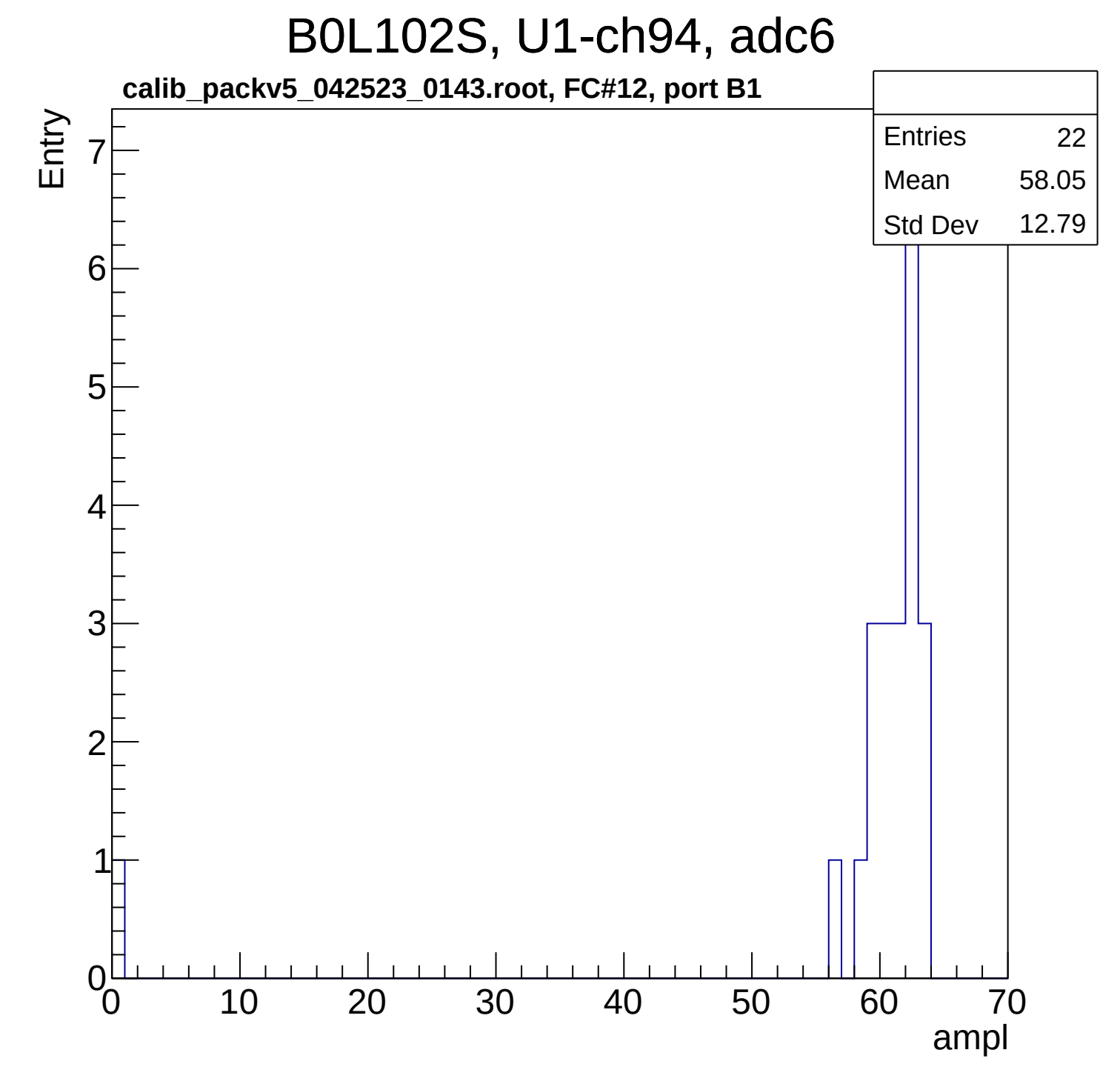

B0L102S, U1-ch94, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 34.16
Std Dev 3.882
Gaus mean : 35.0481
Gaus Width: 3.2624
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

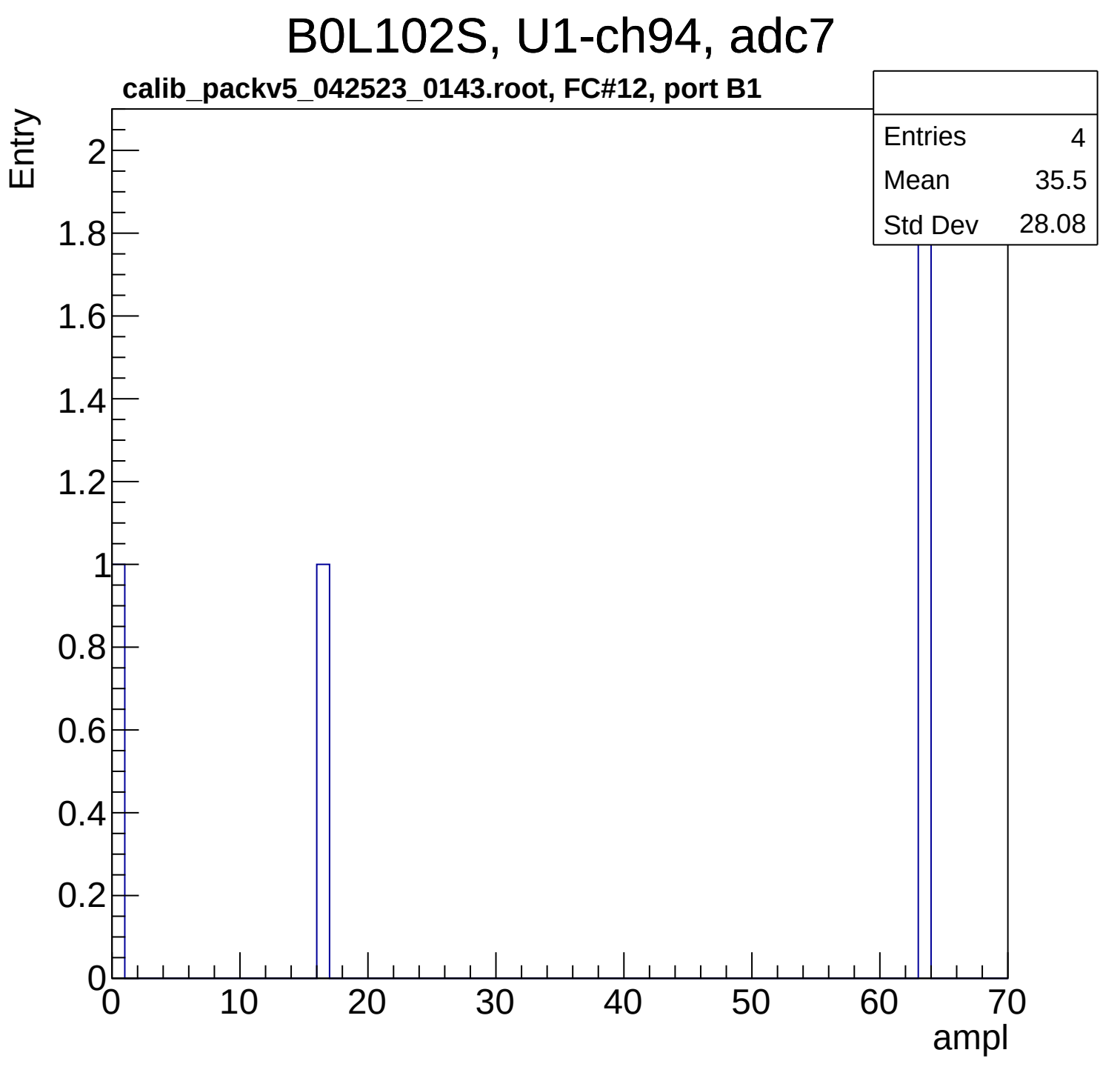

B0L102S, U1-ch94, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 39.31
Std Dev 3.952
Gaus mean : 39.7883
Gaus Width: 3.5747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

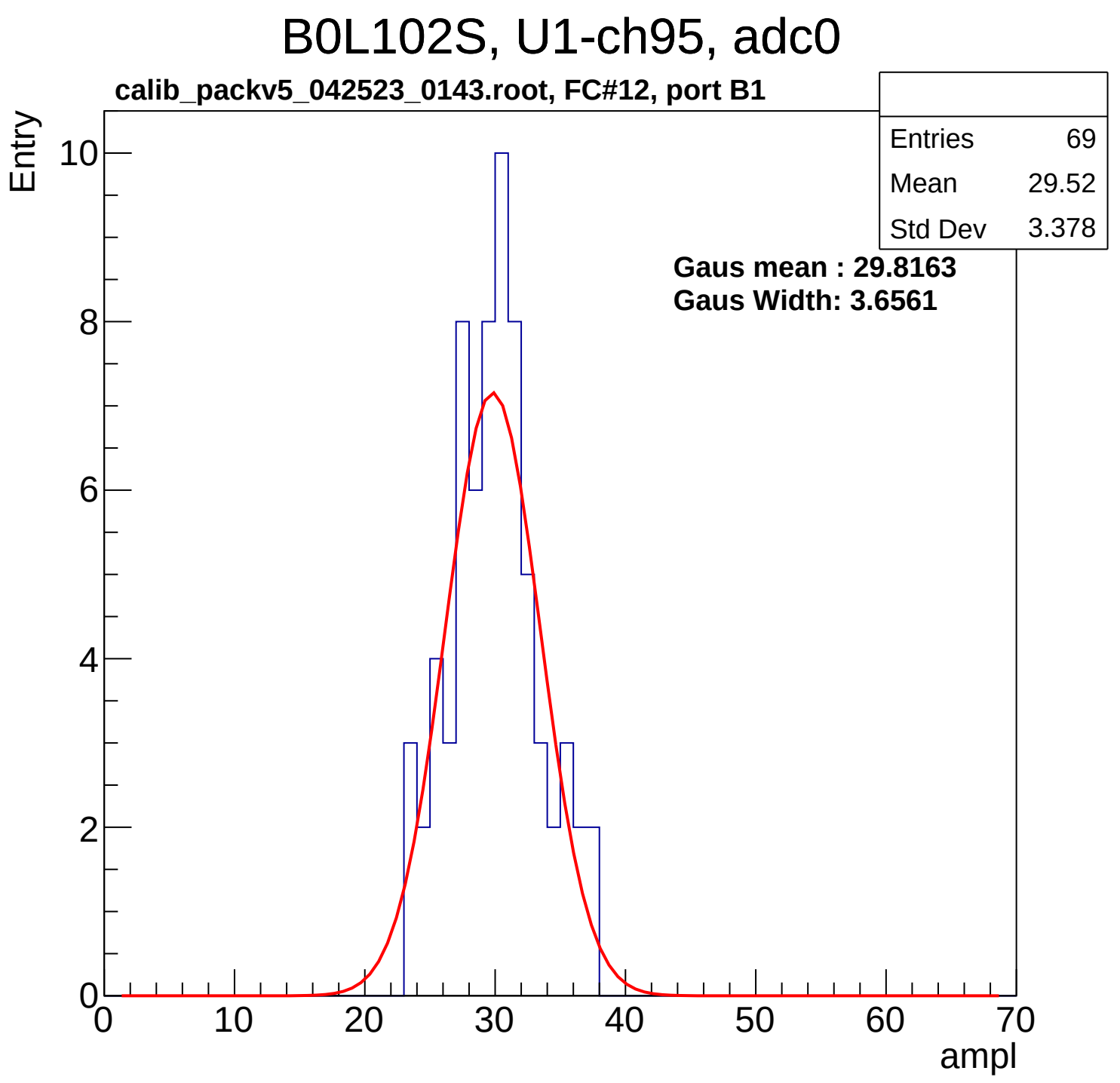

B0L102S, U1-ch94, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 46.25
Std Dev 3.192
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

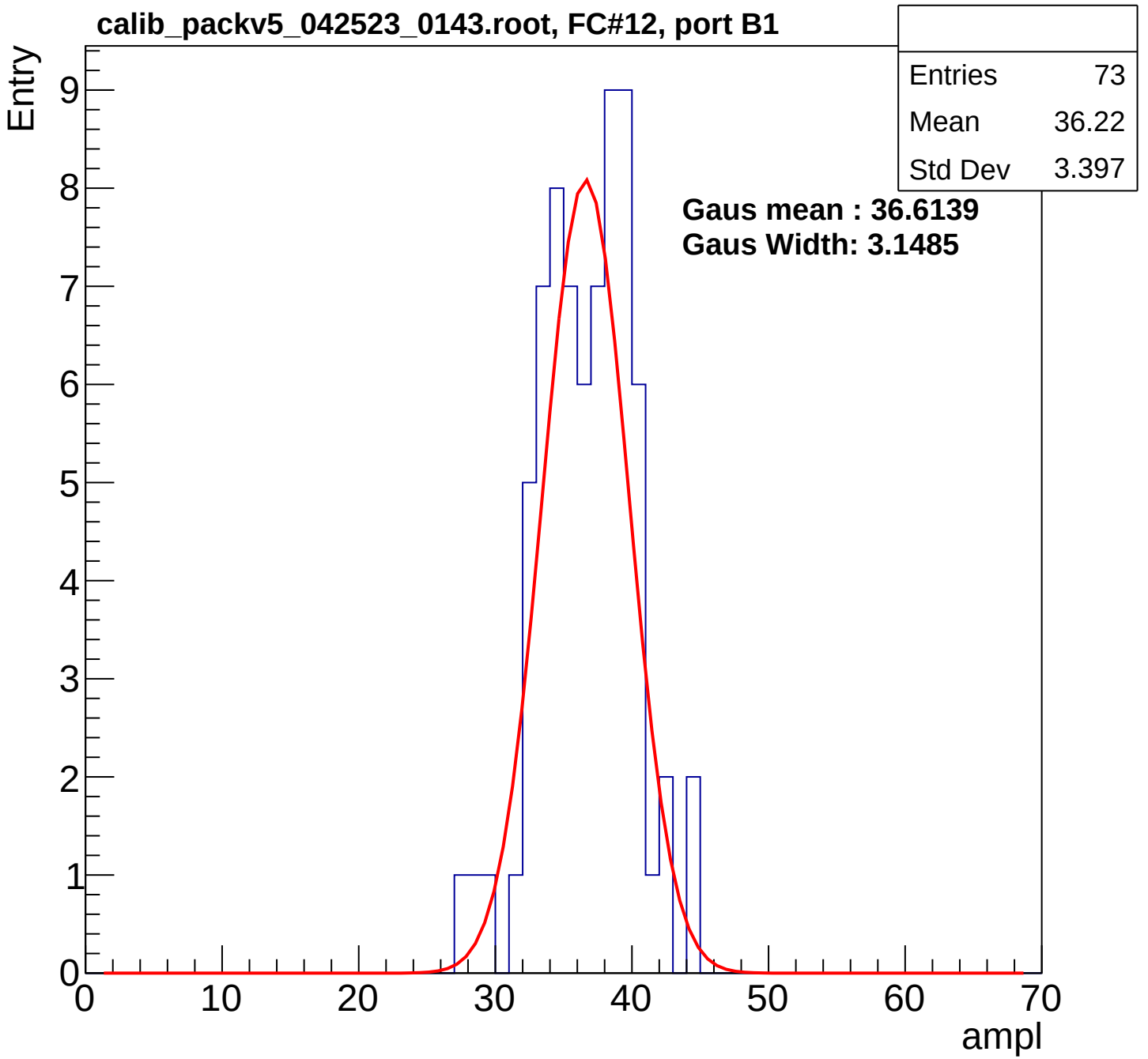

B0L102S, U1-ch94, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 51.8
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

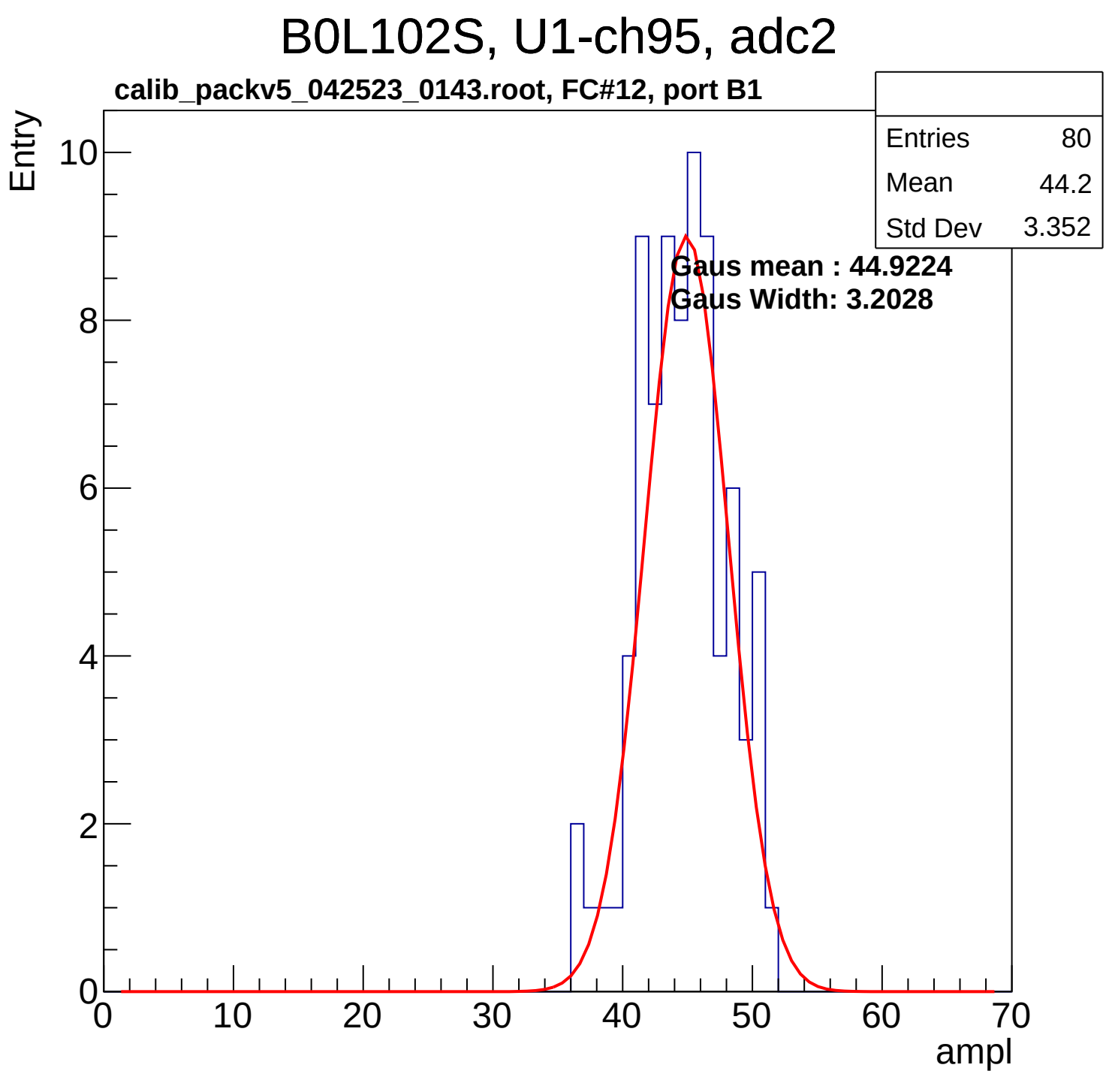

B0L102S, U1-ch94, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.3
Std Dev 2.753
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

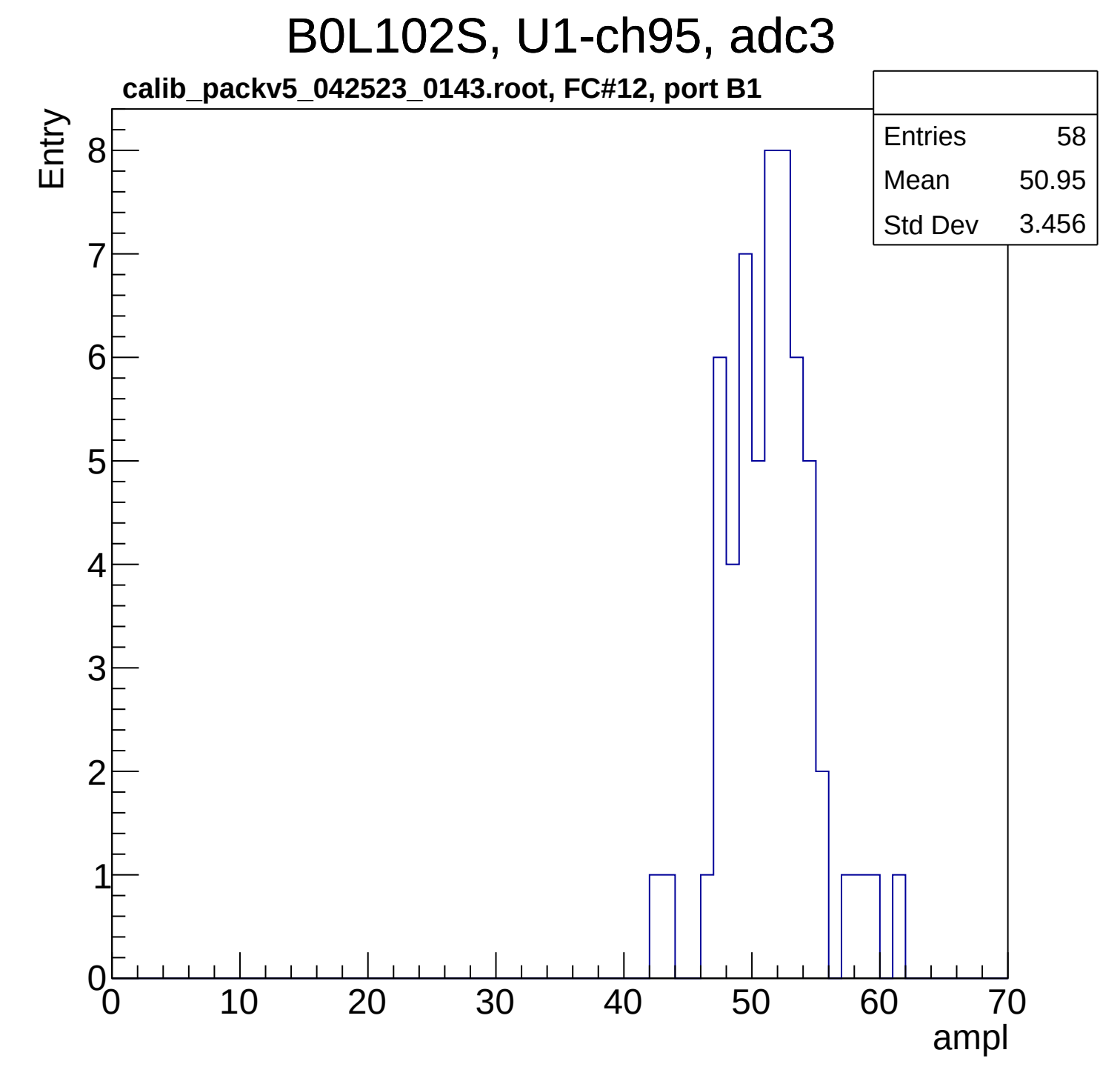

B0L102S, U1-ch94, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 22
Mean 58.05
Std Dev 12.79
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

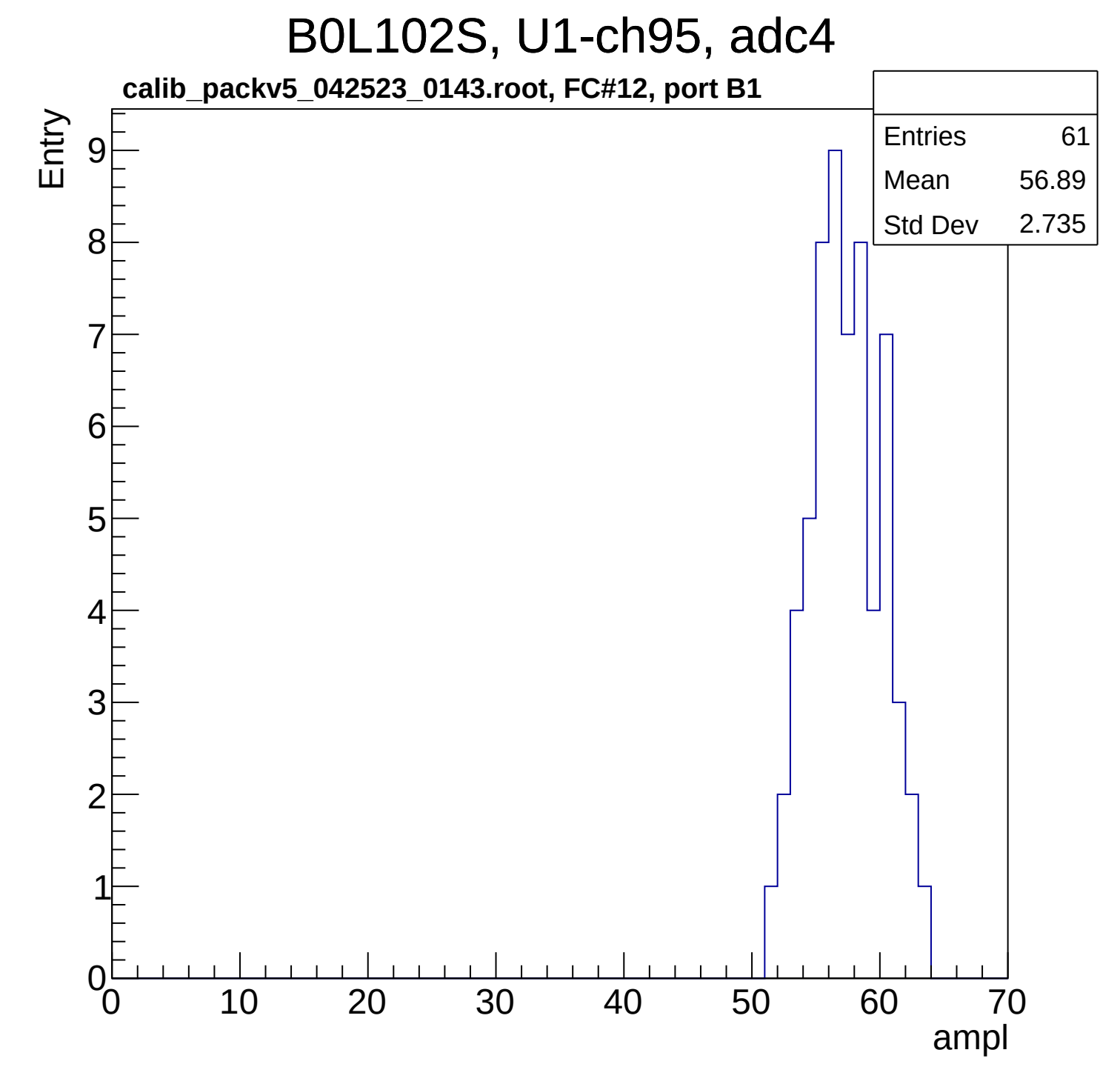

B0L102S, U1-ch94, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 35.5
Std Dev 28.08
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

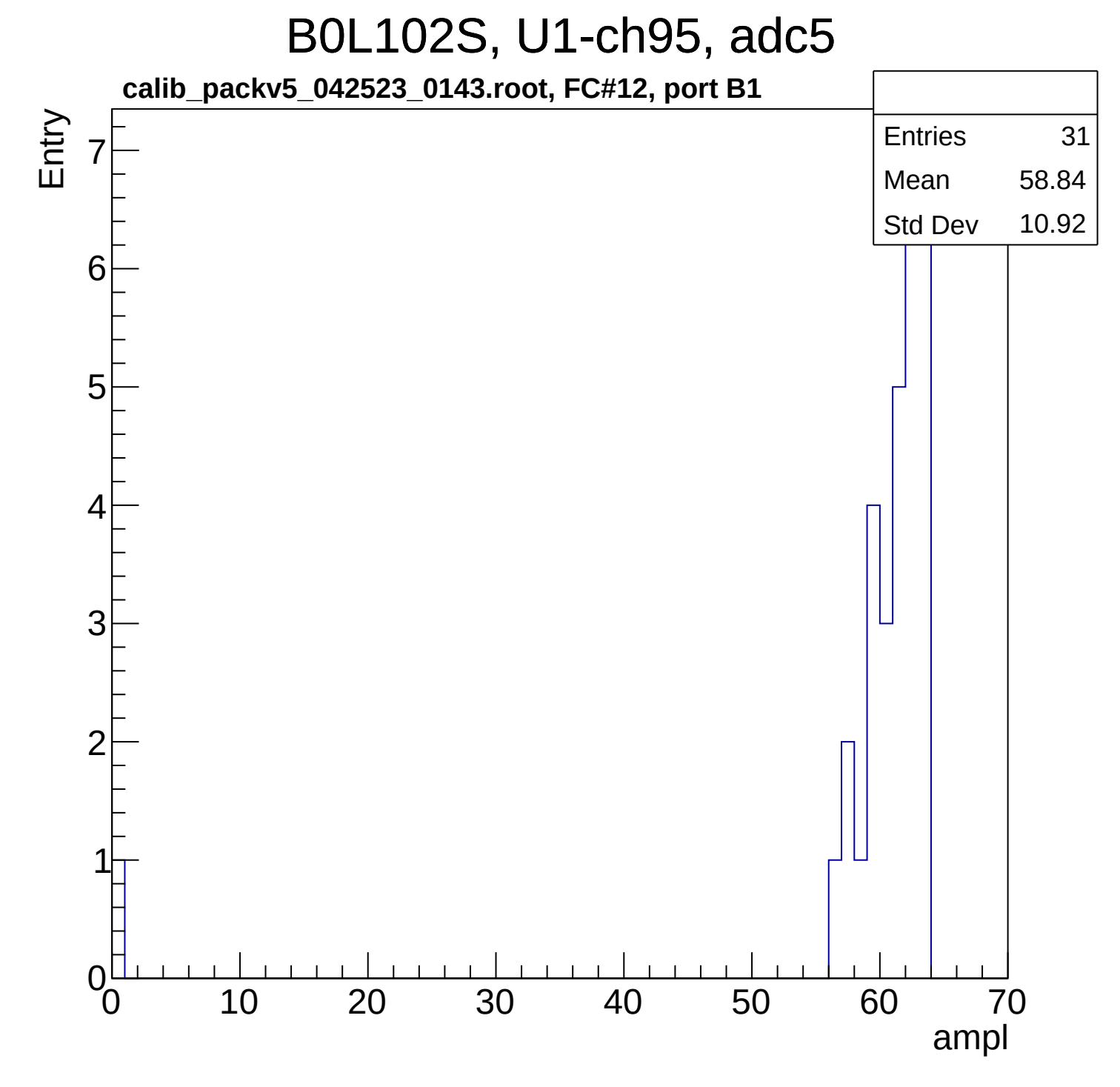

B0L102S, U1-ch95, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 29.52
Std Dev 3.378
Gaus mean : 29.8163
Gaus Width: 3.6561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

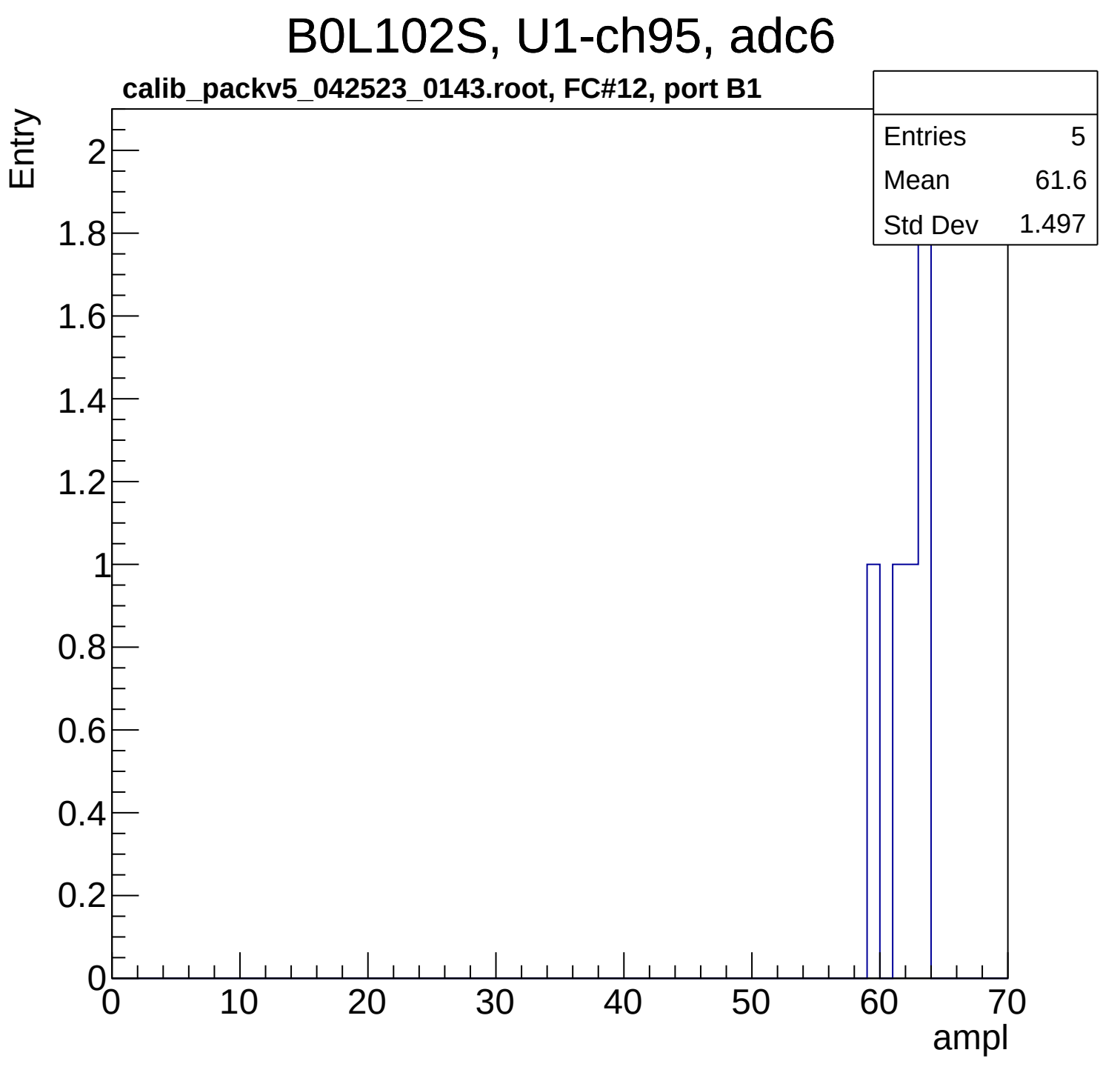

B0L102S, U1-ch95, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 36.22
Std Dev 3.397
Gaus mean : 36.6139
Gaus Width: 3.1485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

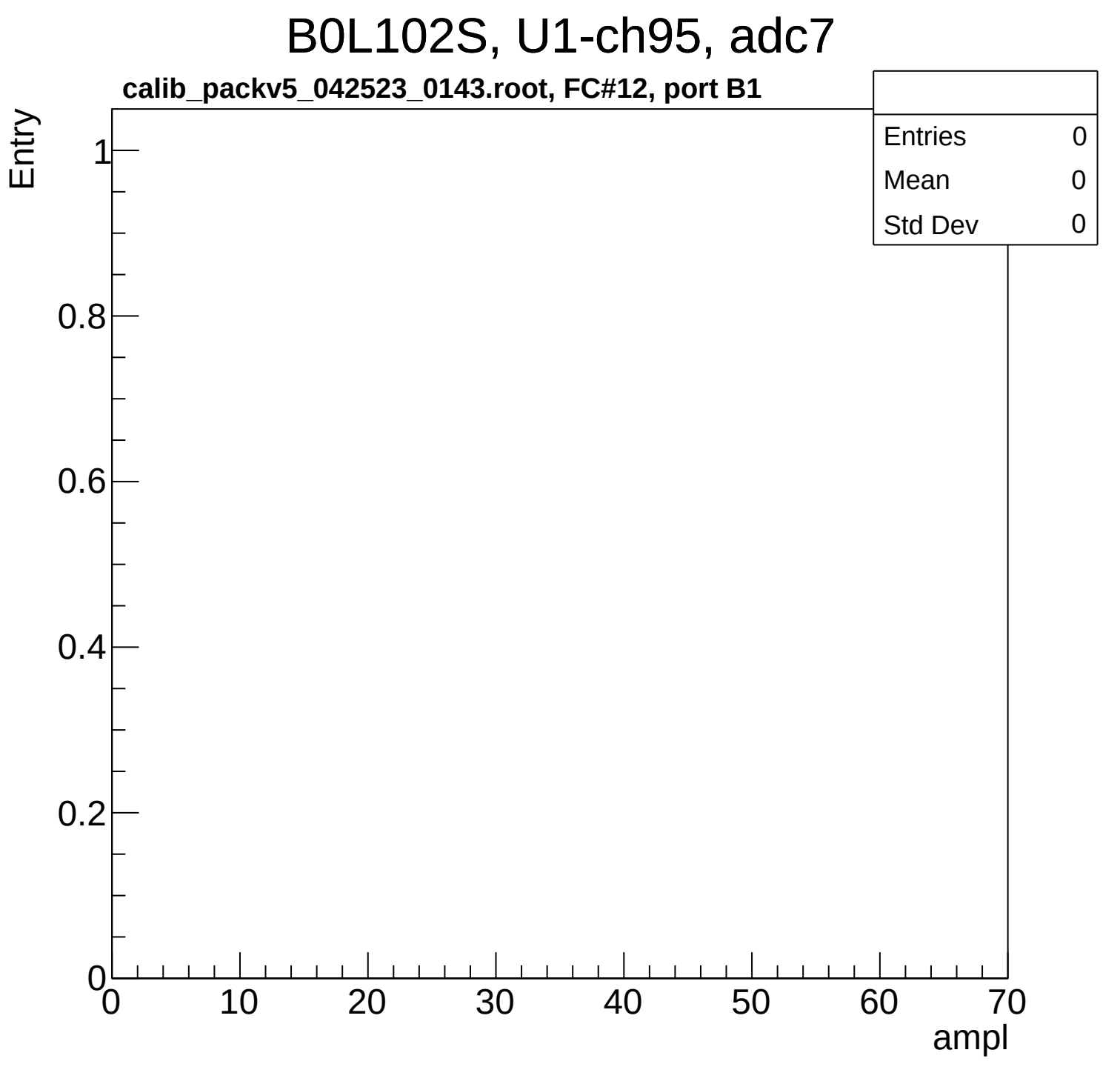

B0L102S, U1-ch95, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 44.2
Std Dev 3.352
Gaus mean : 44.9224
Gaus Width: 3.2028
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

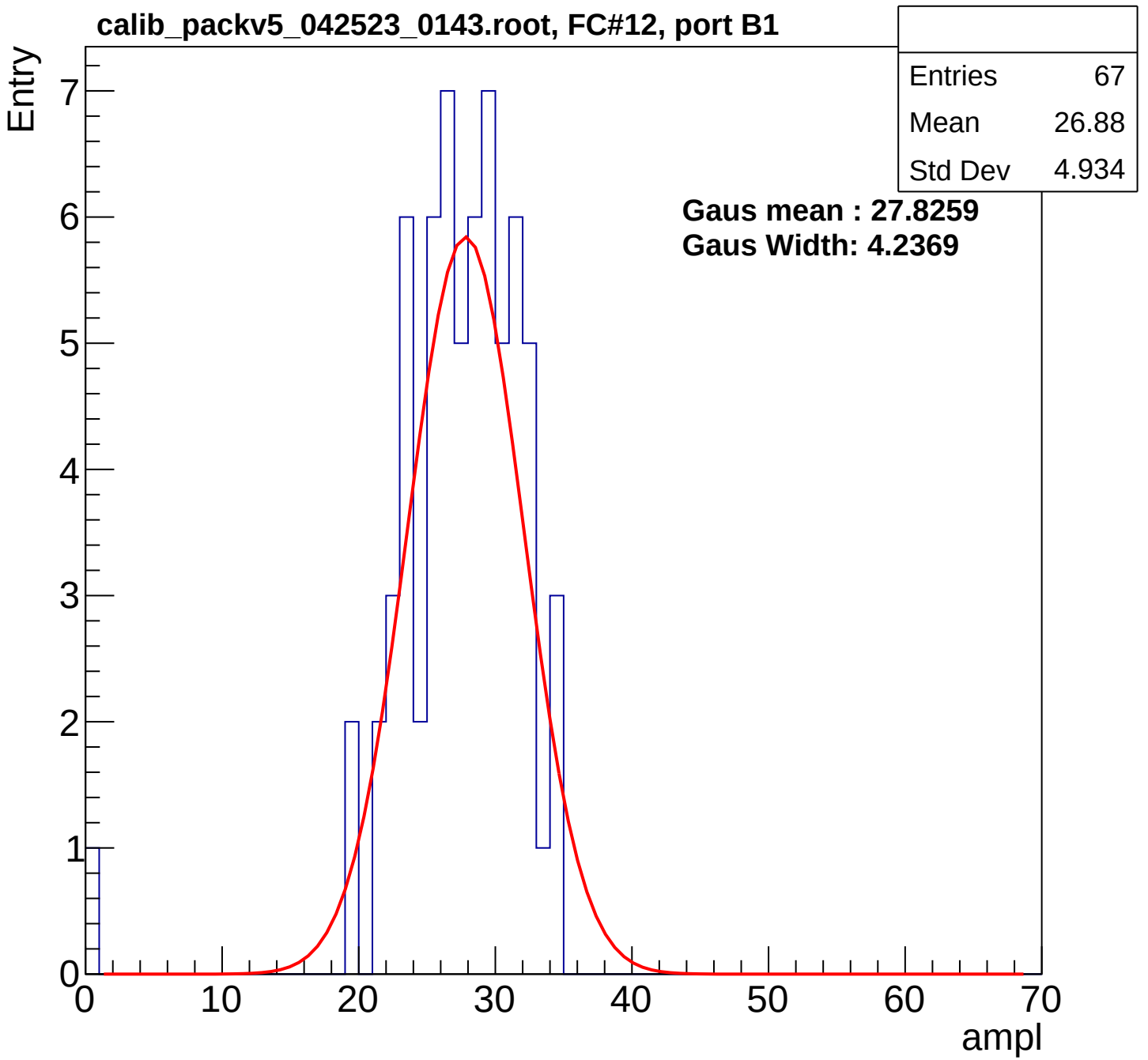

B0L102S, U1-ch95, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 50.95
Std Dev 3.456
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

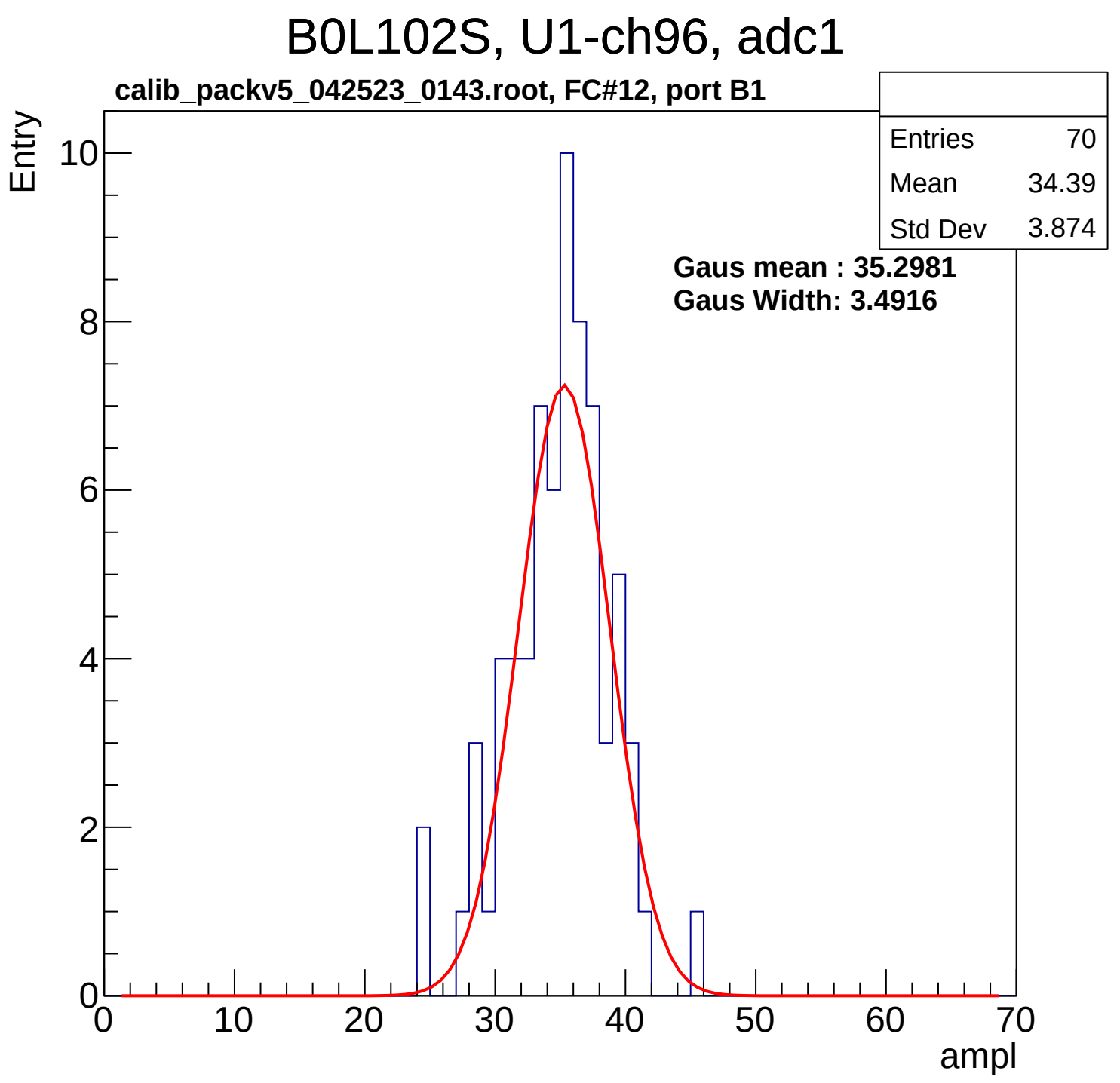

B0L102S, U1-ch95, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 56.89
Std Dev 2.735
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

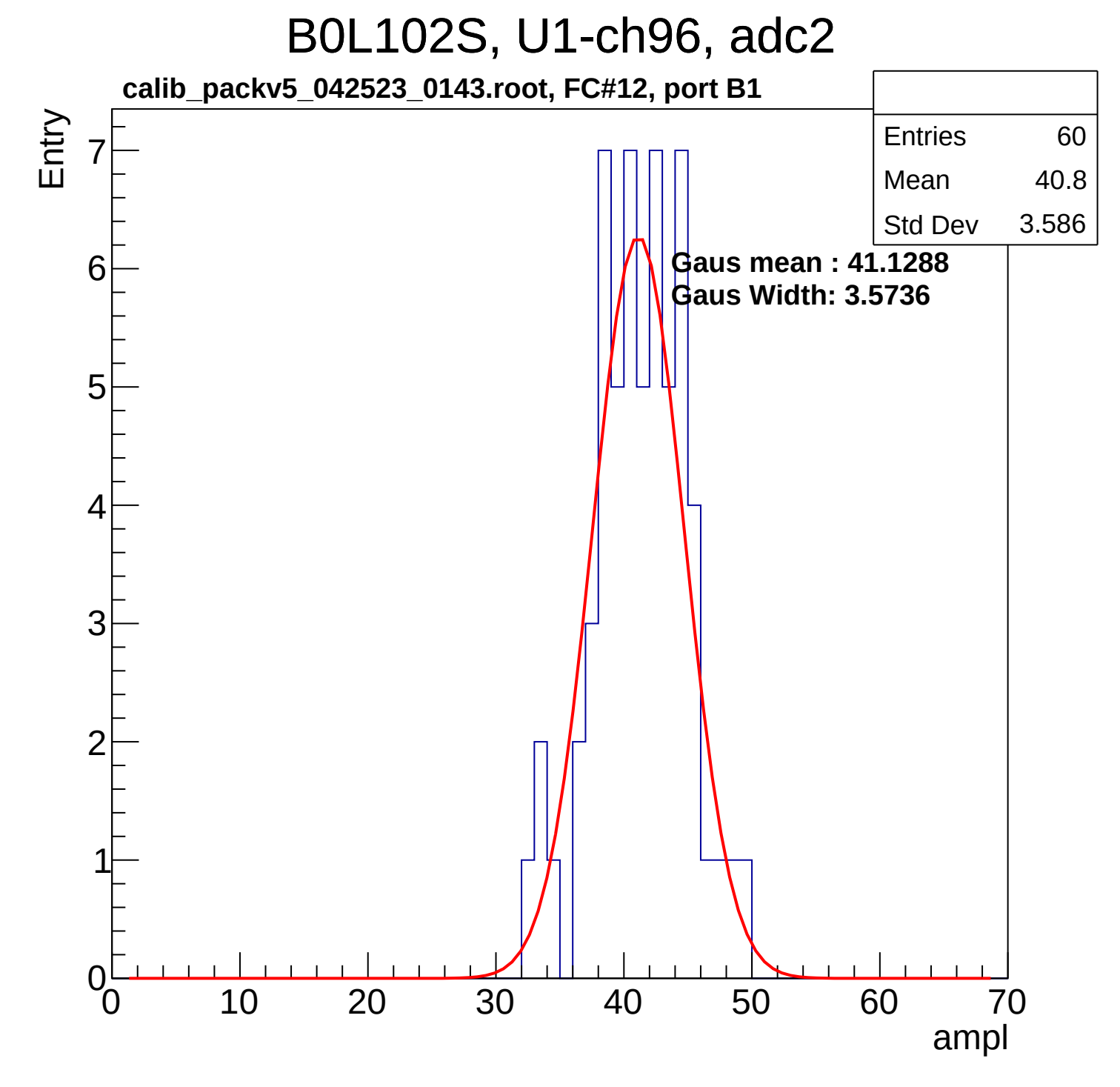

B0L102S, U1-ch95, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 58.84
Std Dev 10.92
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

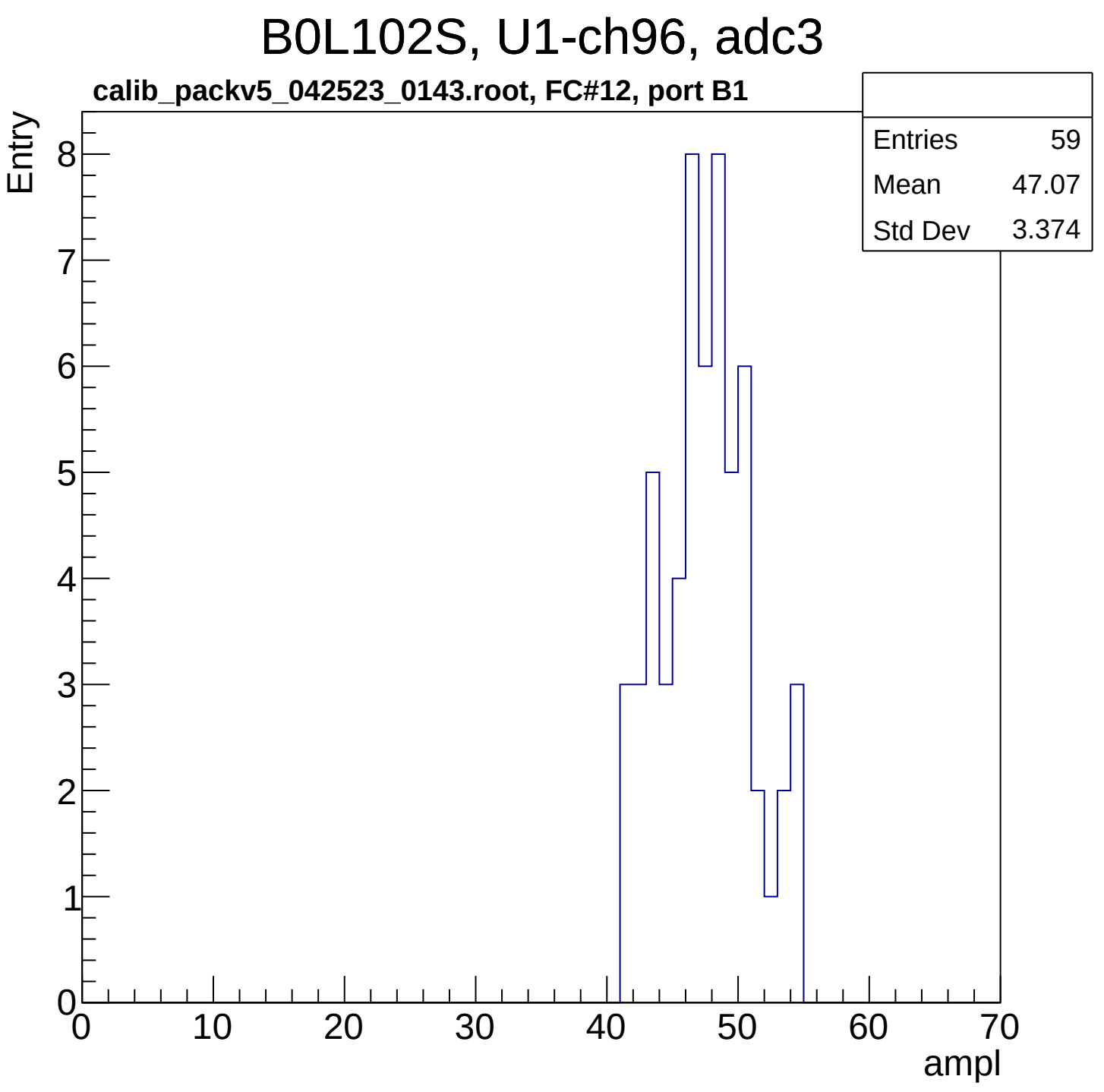

B0L102S, U1-ch95, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.6
Std Dev 1.497
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

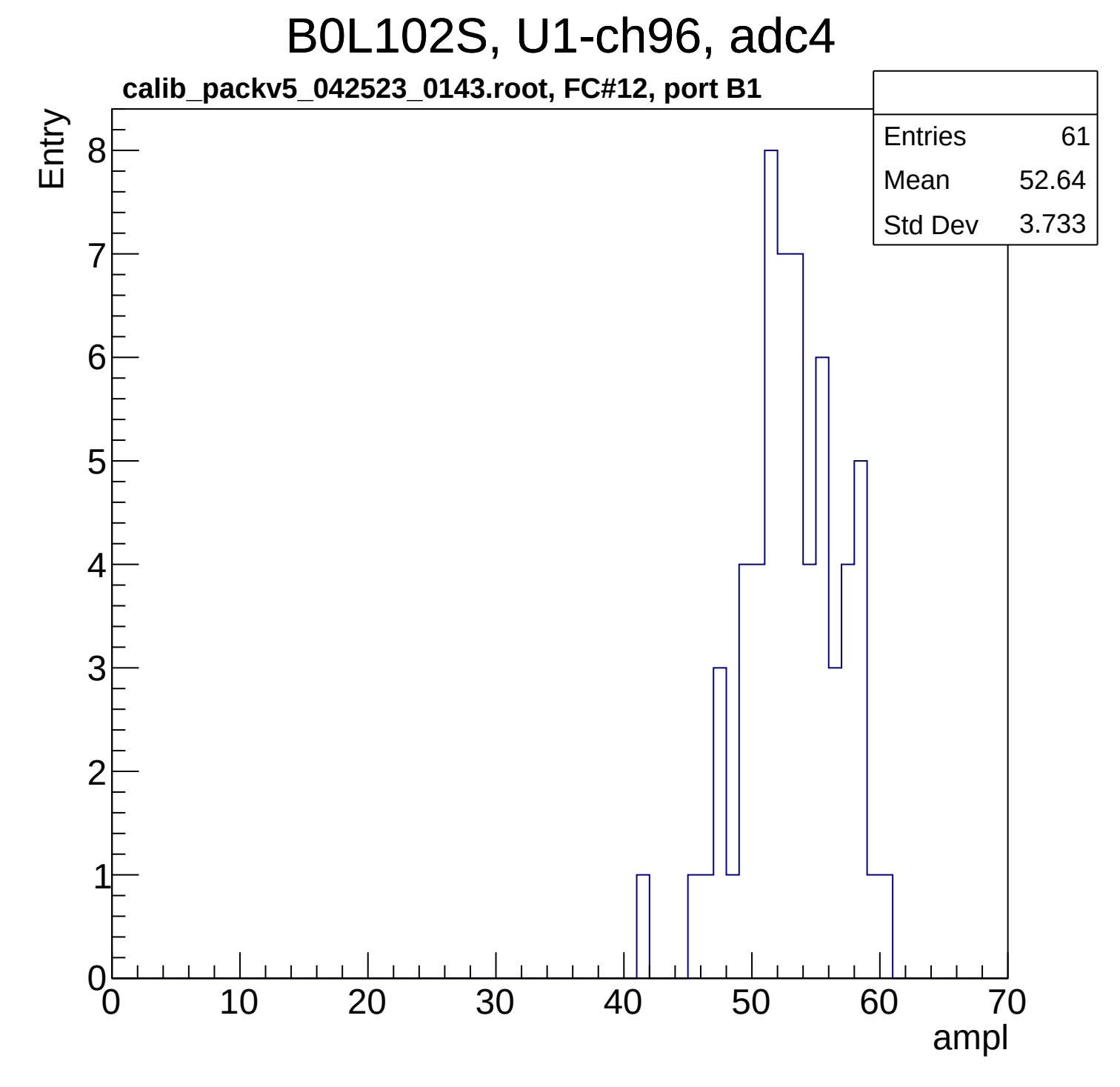

B0L102S, U1-ch95, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

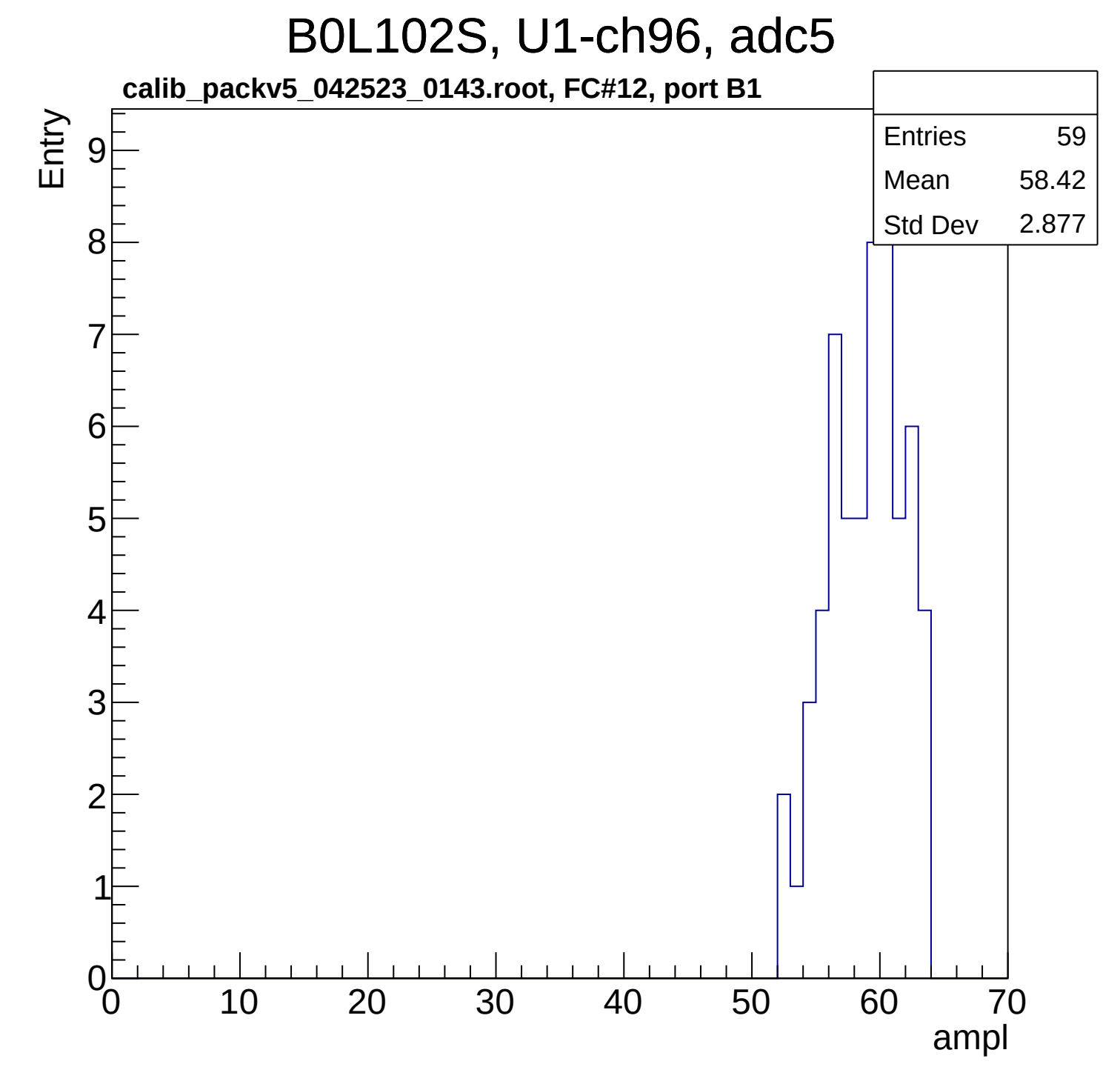

B0L102S, U1-ch96, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 26.88
Std Dev 4.934
Gaus mean : 27.8259
Gaus Width: 4.2369
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch96, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 34.39
Std Dev 3.874
Gaus mean : 35.2981
Gaus Width: 3.4916
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch96, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 40.8
Std Dev 3.586
Gaus mean : 41.1288
Gaus Width: 3.5736
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch96, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 47.07
Std Dev 3.374
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch96, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 52.64
Std Dev 3.733
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch96, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 58.42
Std Dev 2.877
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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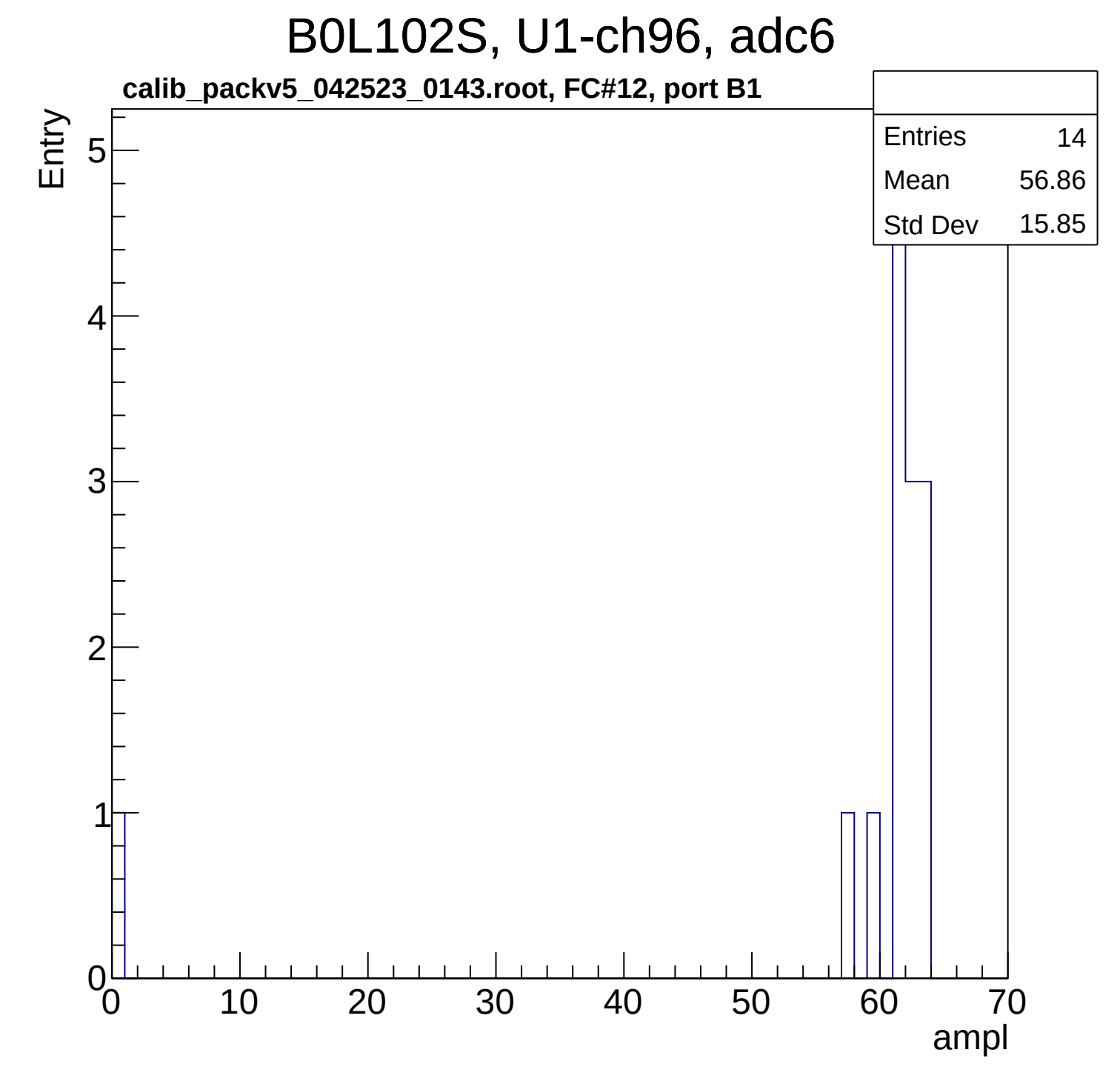
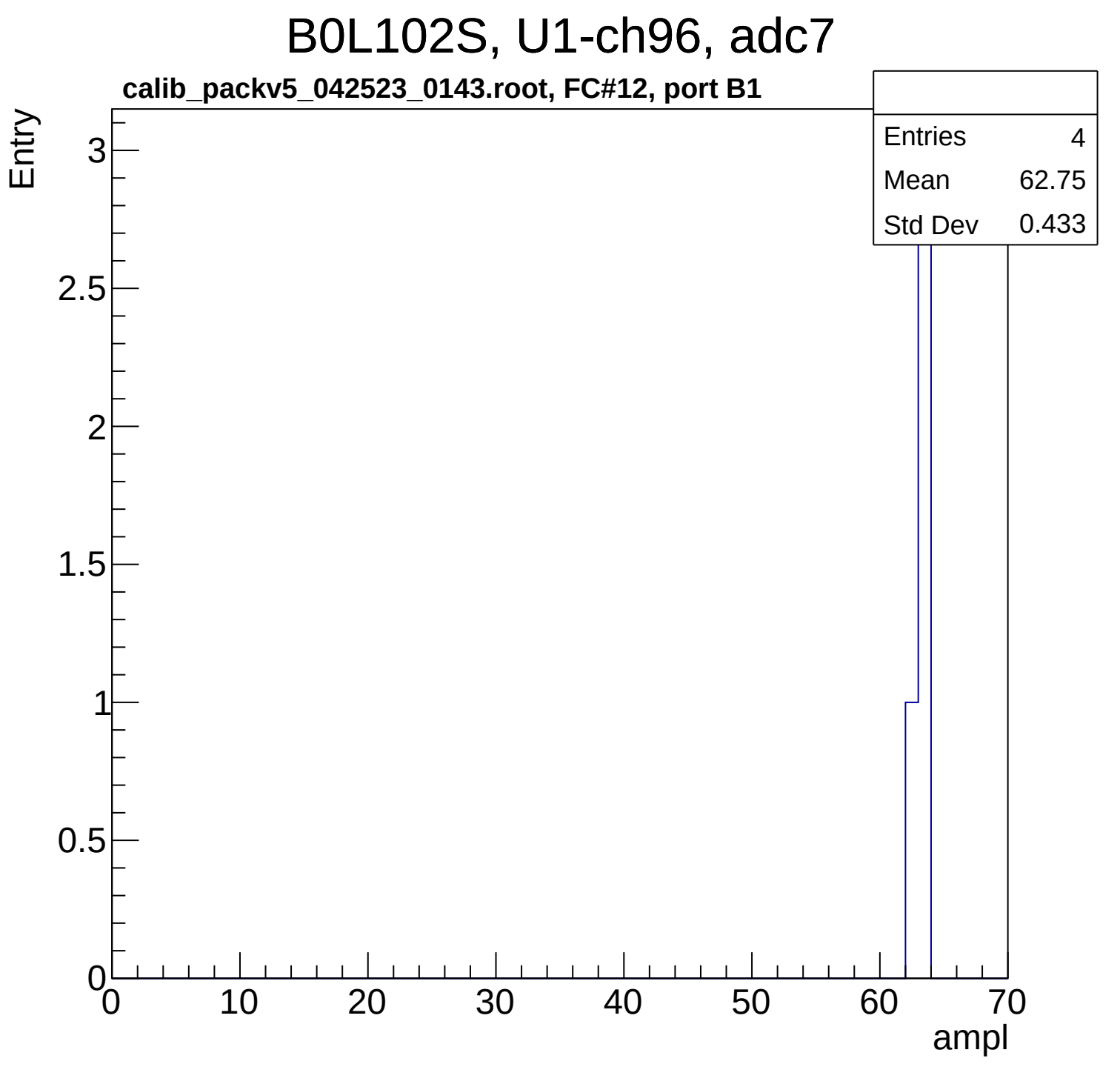
40
50
60
70


B0L102S, U1-ch96, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 56.86
Std Dev 15.85
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch96, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
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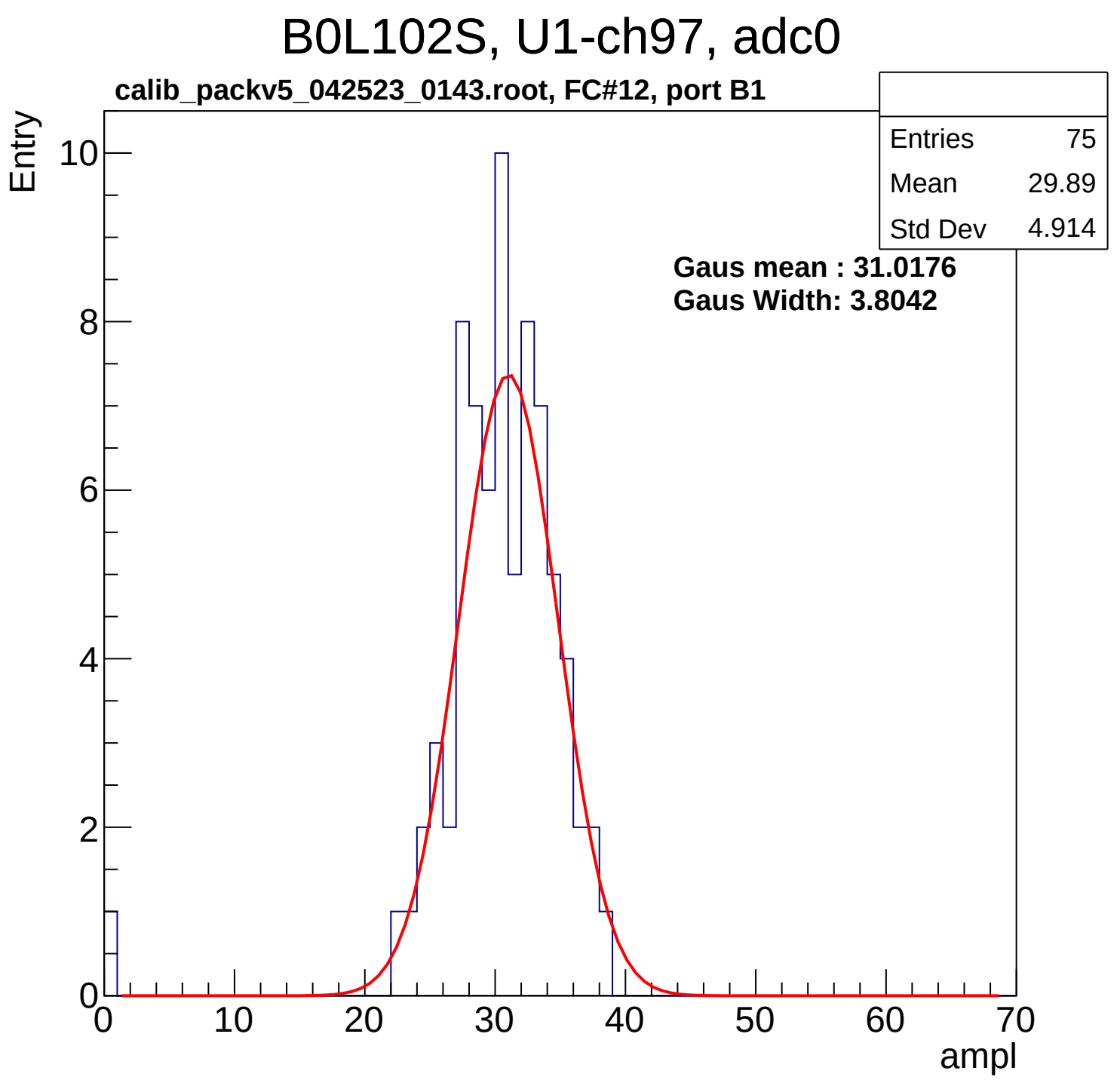
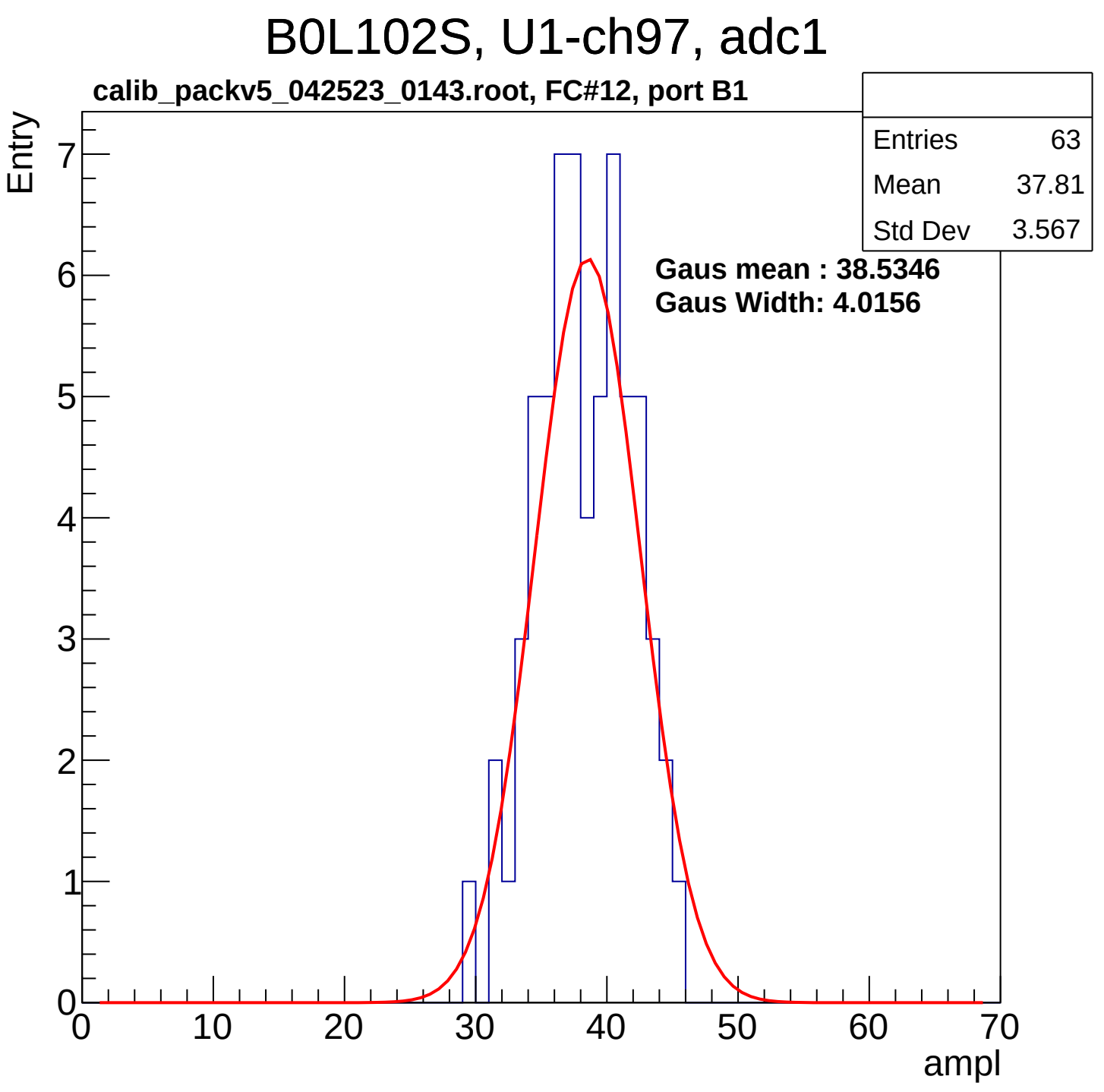
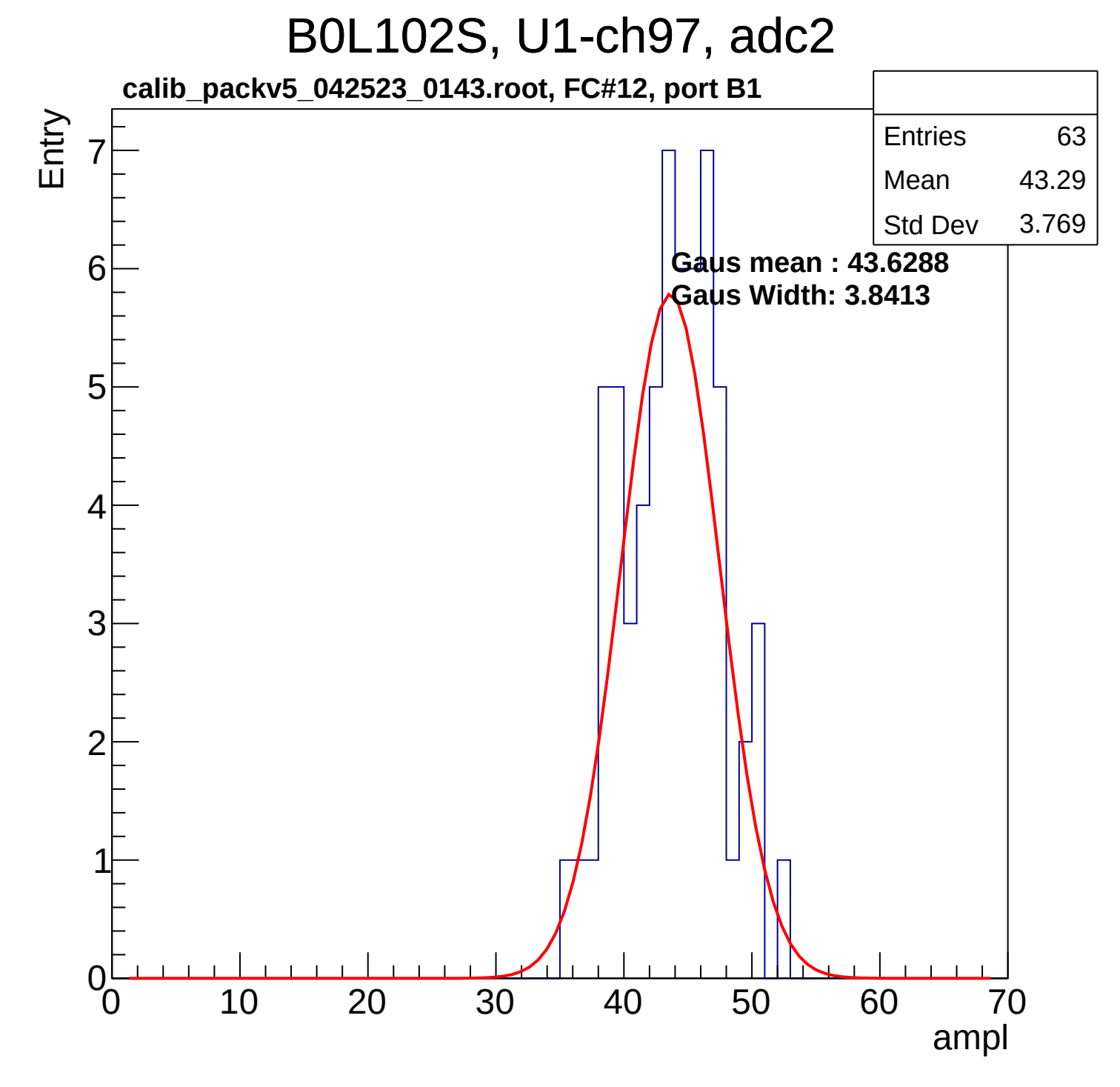
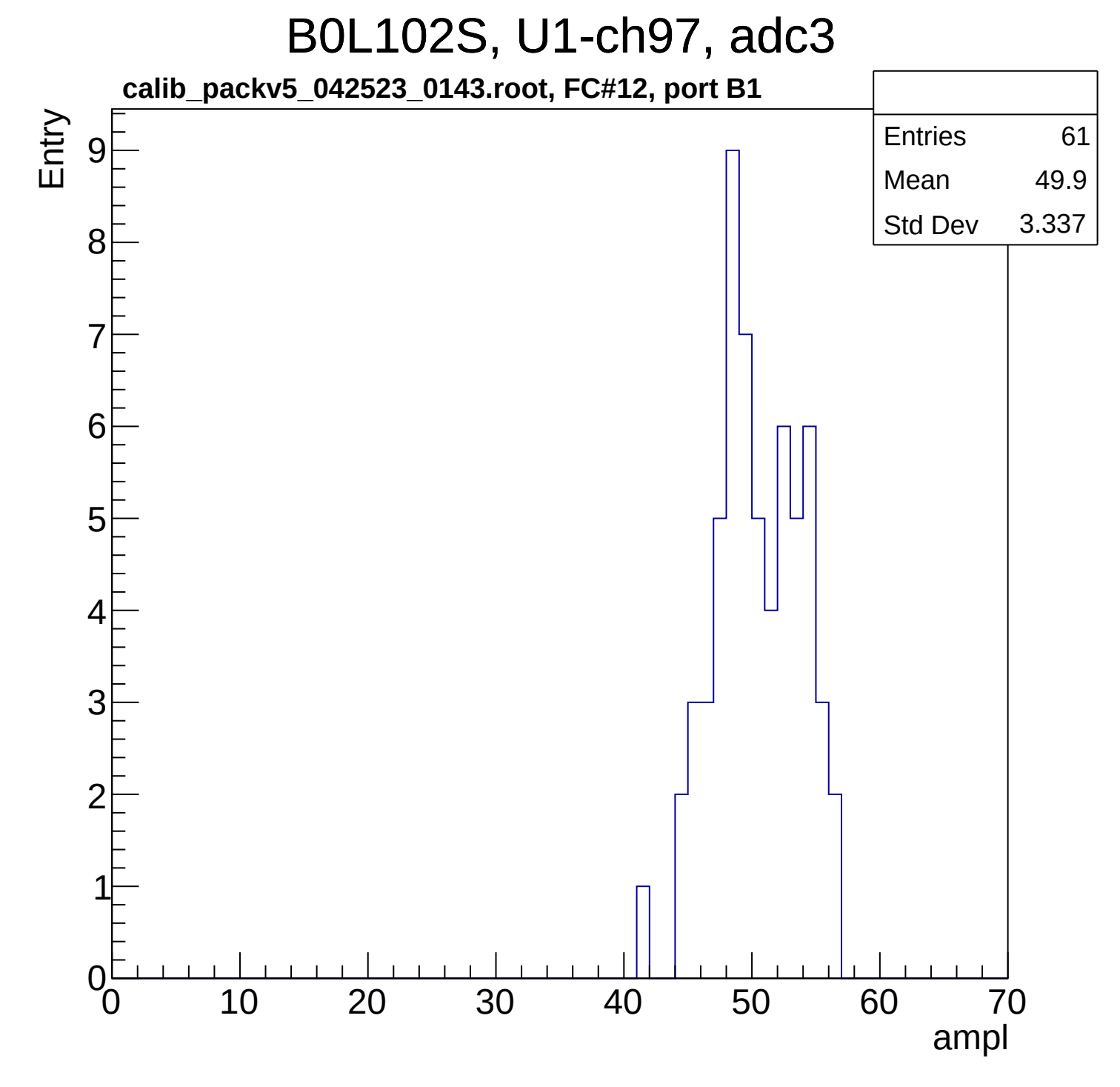
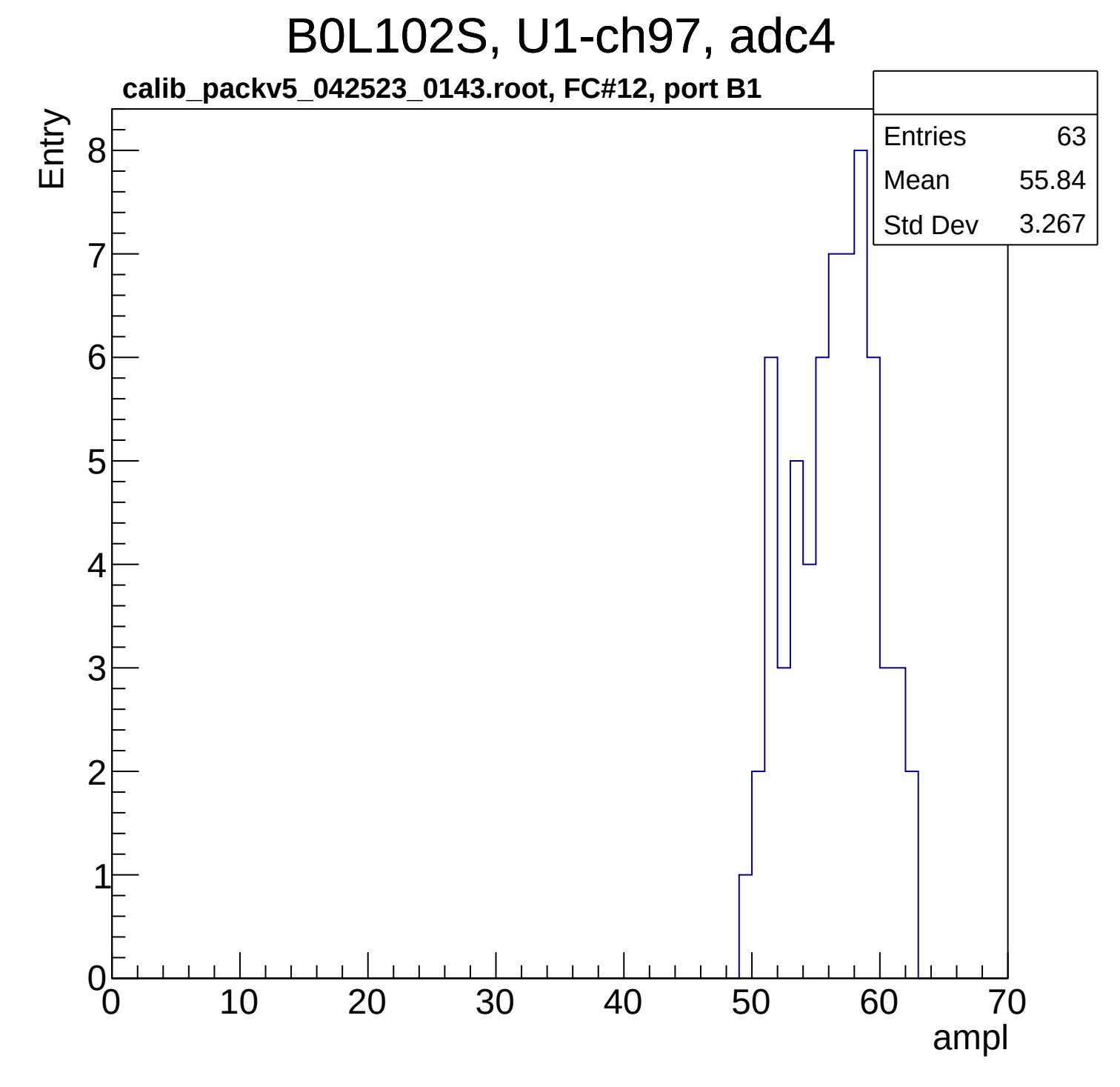
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

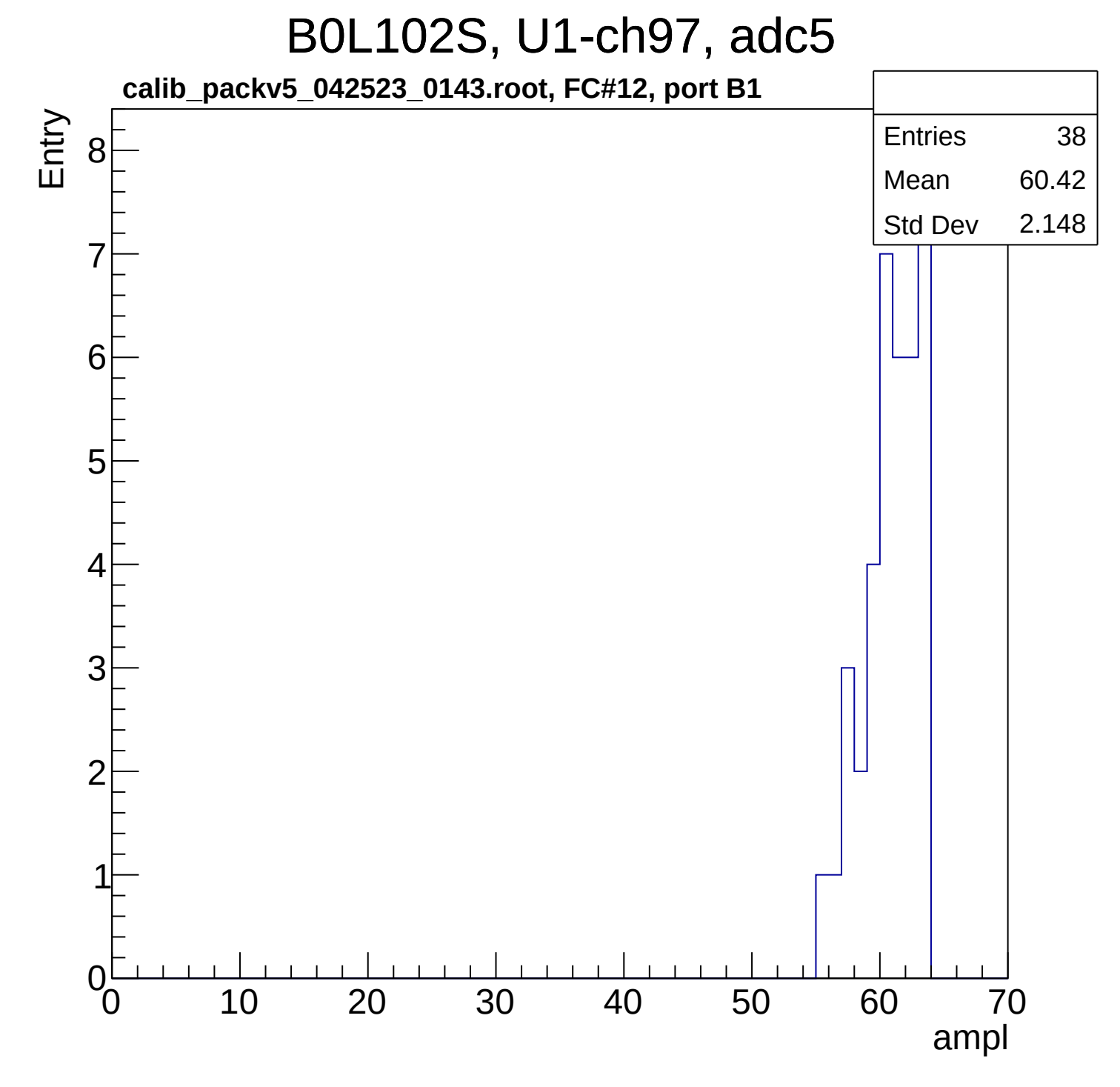

B0L102S, U1-ch97, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 29.89
Std Dev 4.914
Gaus mean : 31.0176
Gaus Width: 3.8042
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

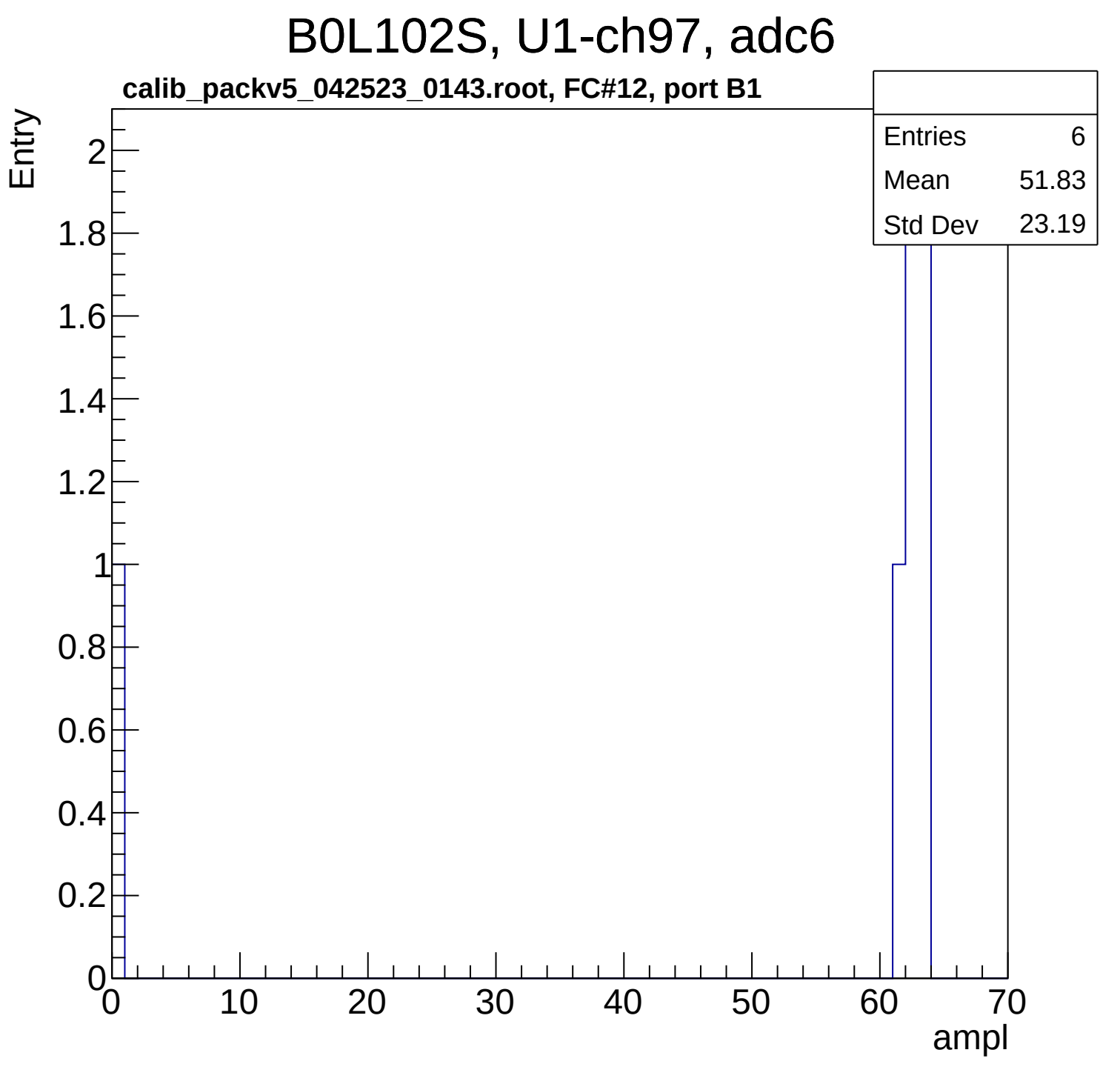

B0L102S, U1-ch97, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 37.81
Std Dev 3.567
Gaus mean : 38.5346
Gaus Width: 4.0156
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

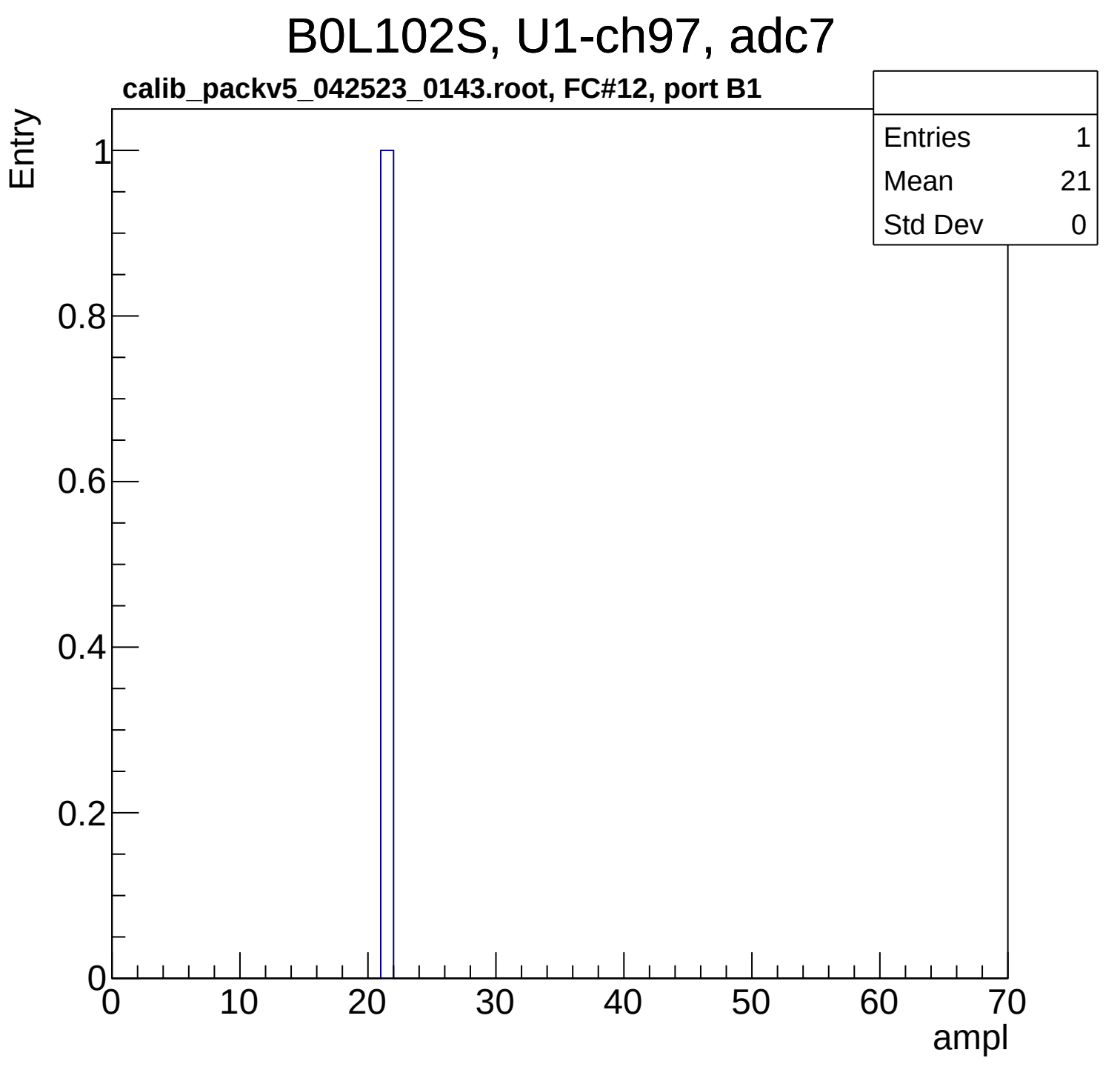

B0L102S, U1-ch97, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 43.29
Std Dev 3.769
Gaus mean : 43.6288
Gaus Width: 3.8413
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch97, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 49.9
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch97, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 55.84
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch97, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 60.42
Std Dev 2.148
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch97, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch97, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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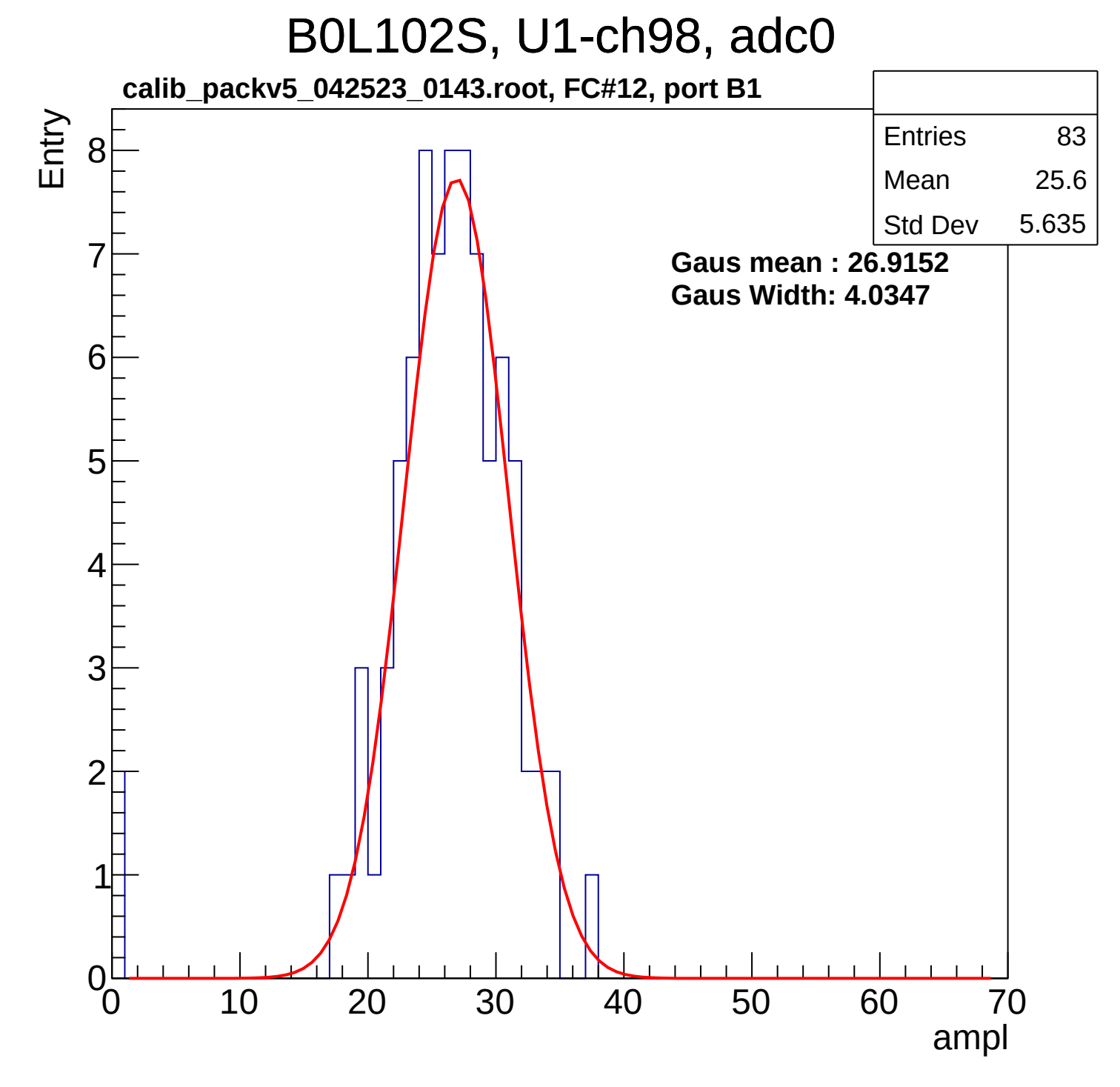
70
ampl


B0L102S, U1-ch98, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 25.6
Std Dev 5.635
Gaus mean : 26.9152
Gaus Width: 4.0347
Entry
ampl
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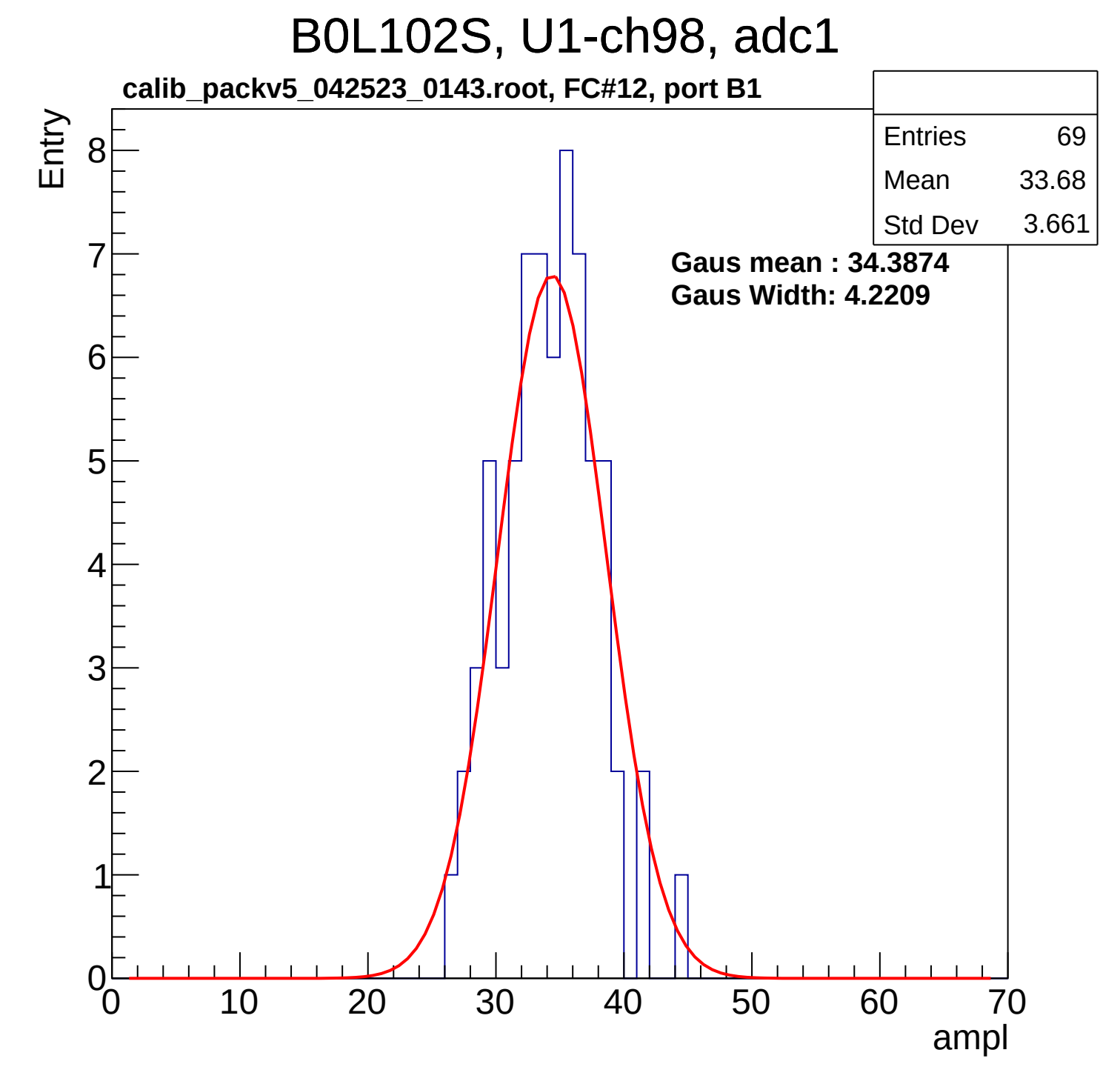
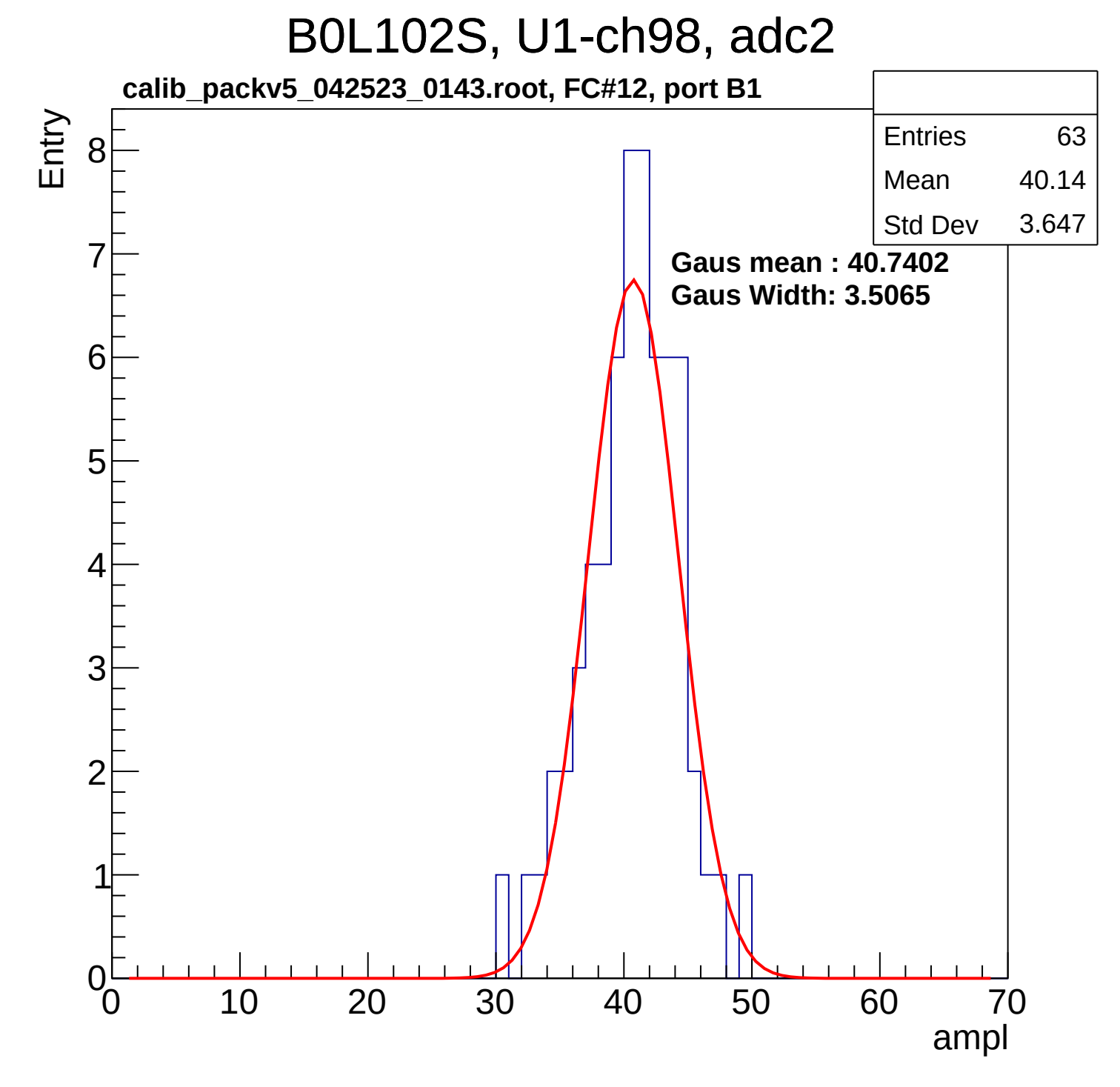
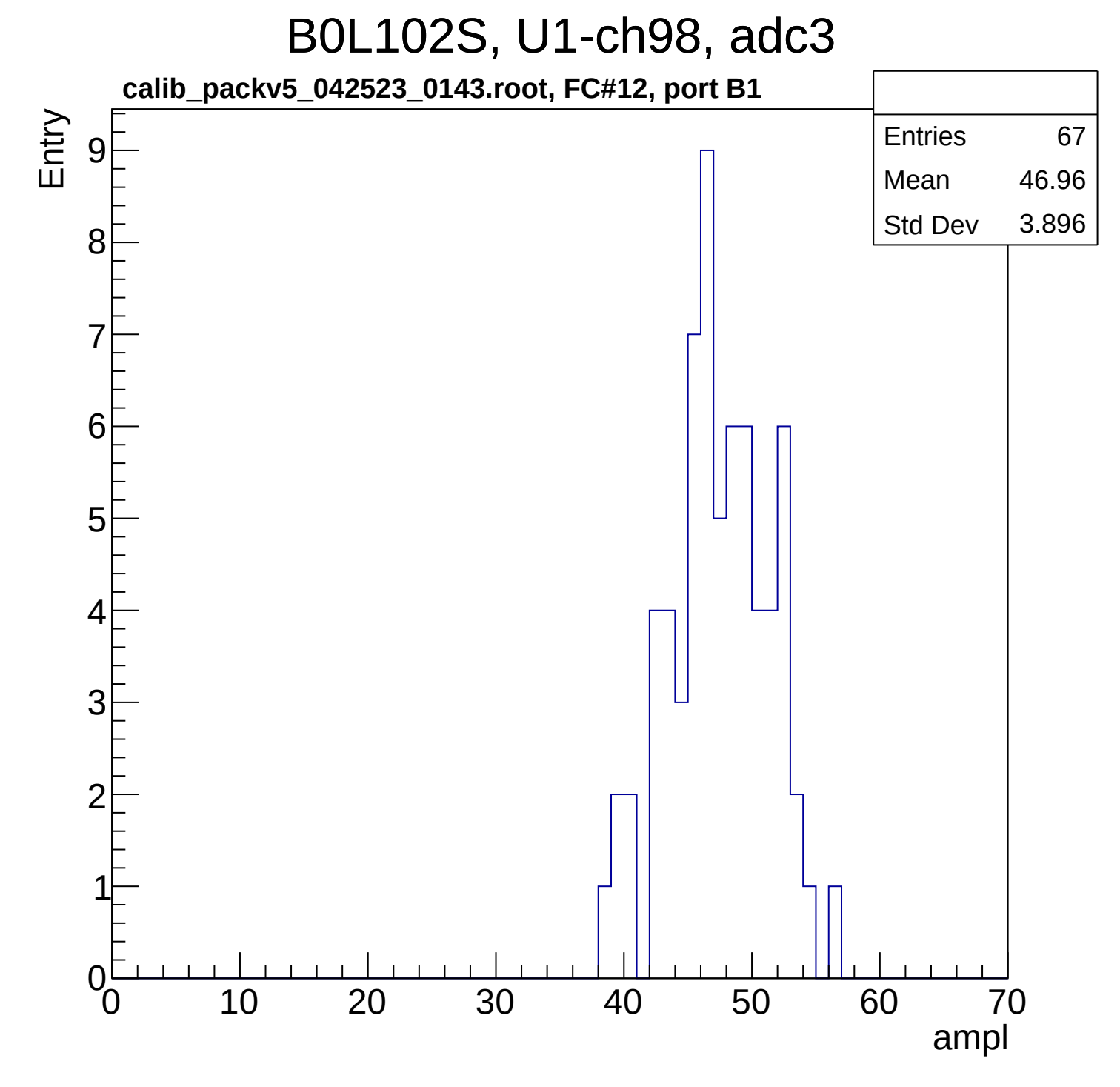
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch98, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 33.68
Std Dev 3.661
Gaus mean : 34.3874
Gaus Width: 4.2209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch98, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 40.14
Std Dev 3.647
Gaus mean : 40.7402
Gaus Width: 3.5065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch98, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 46.96
Std Dev 3.896
Entry
ampl
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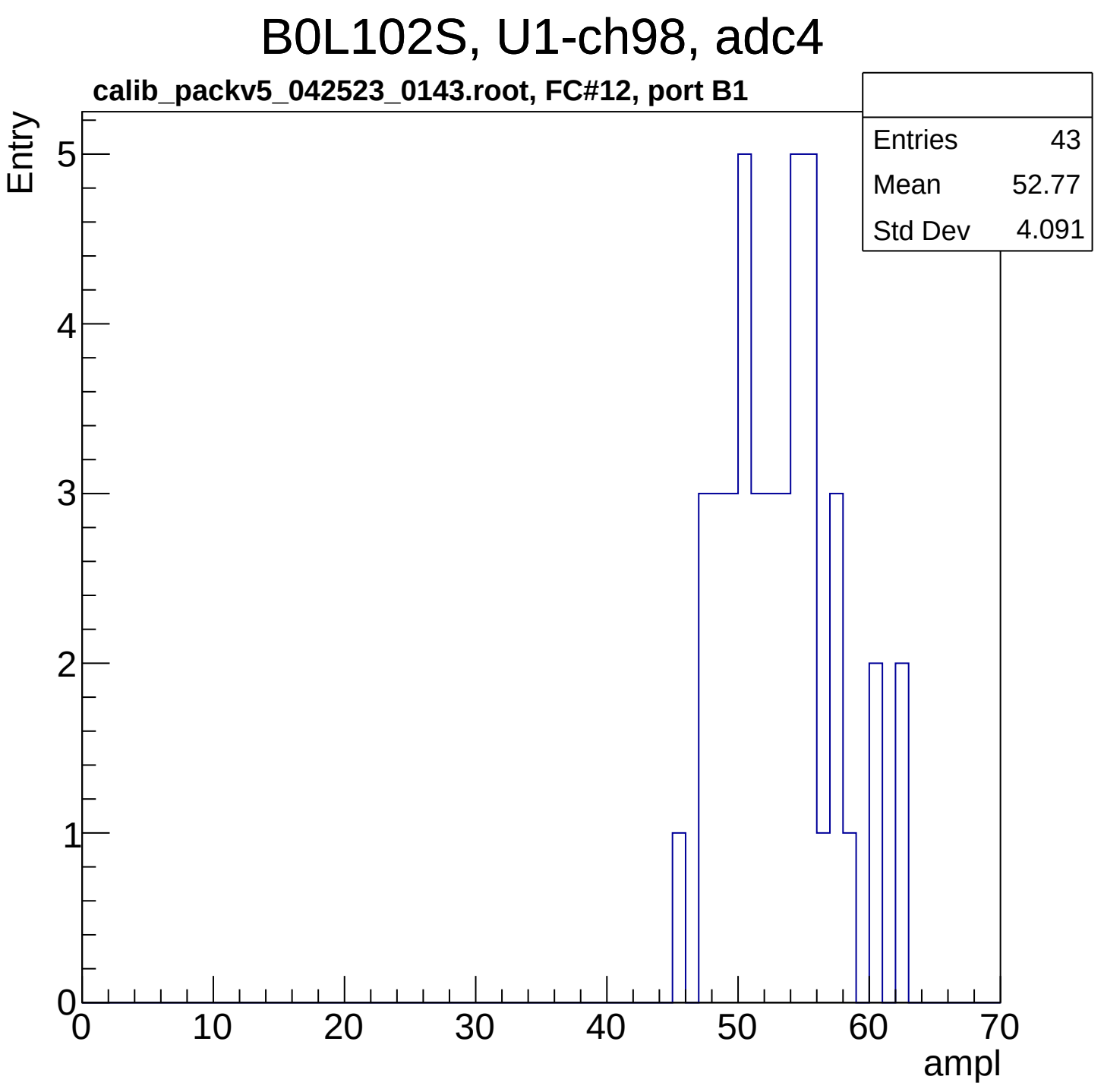
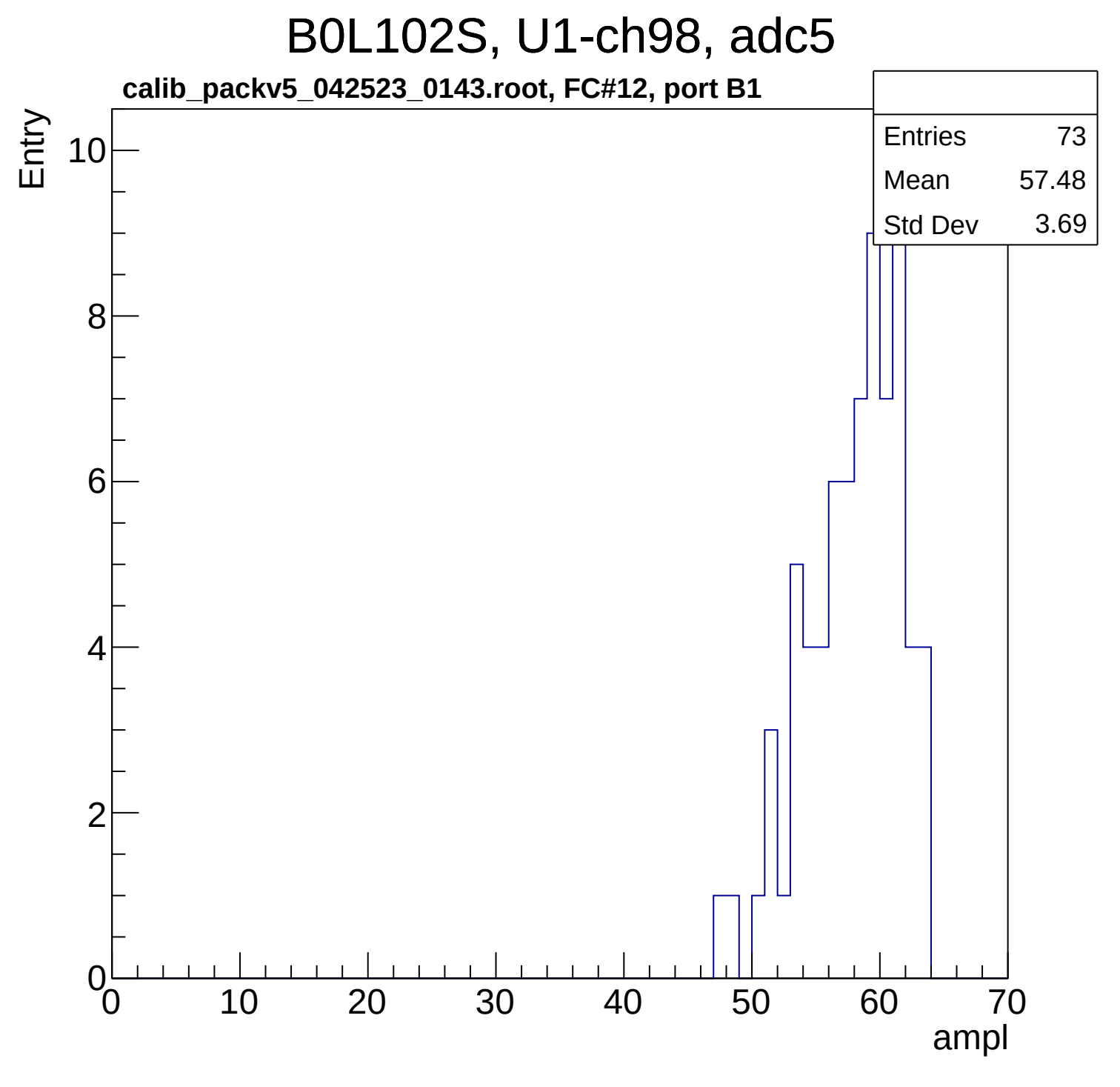
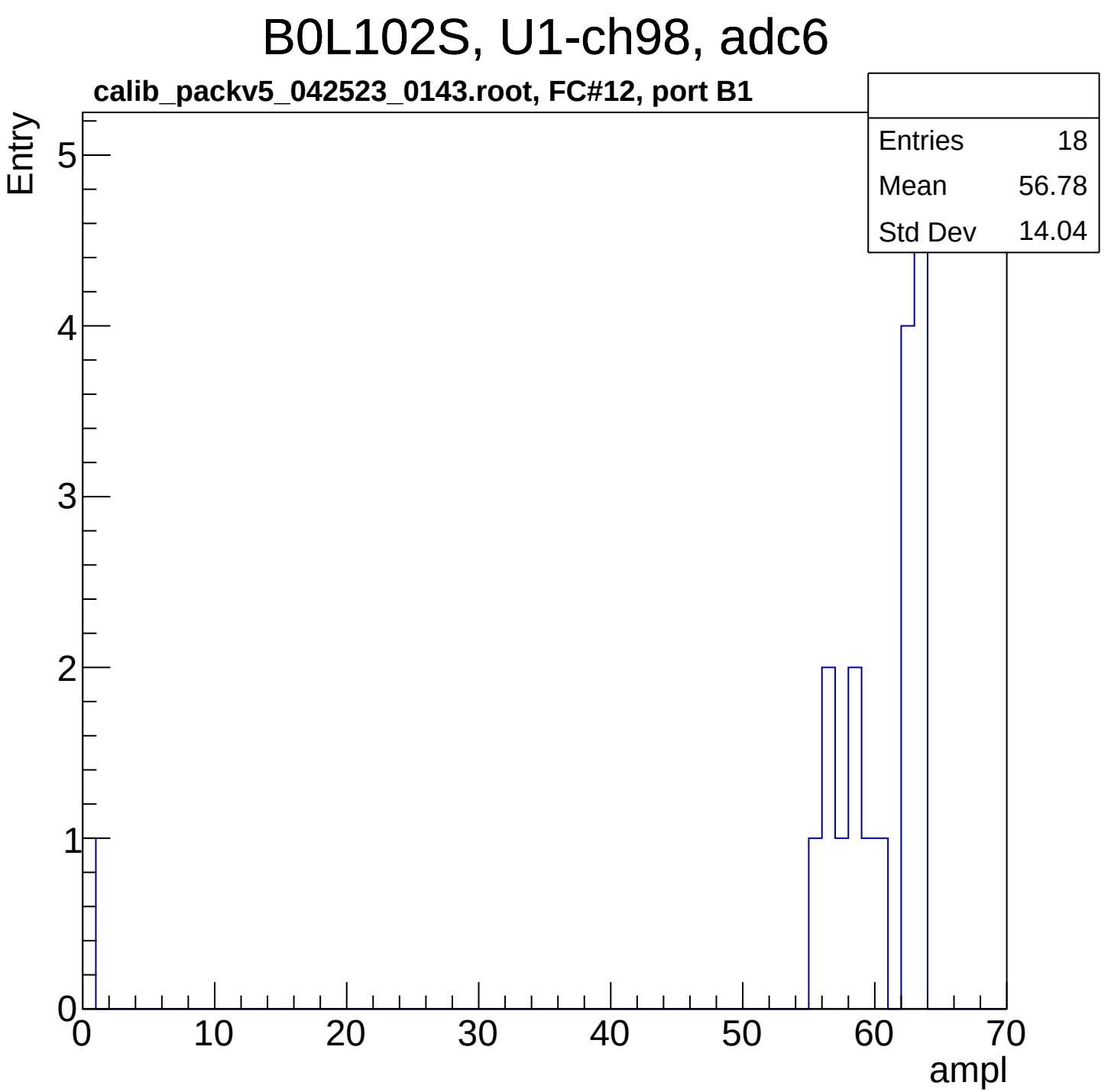
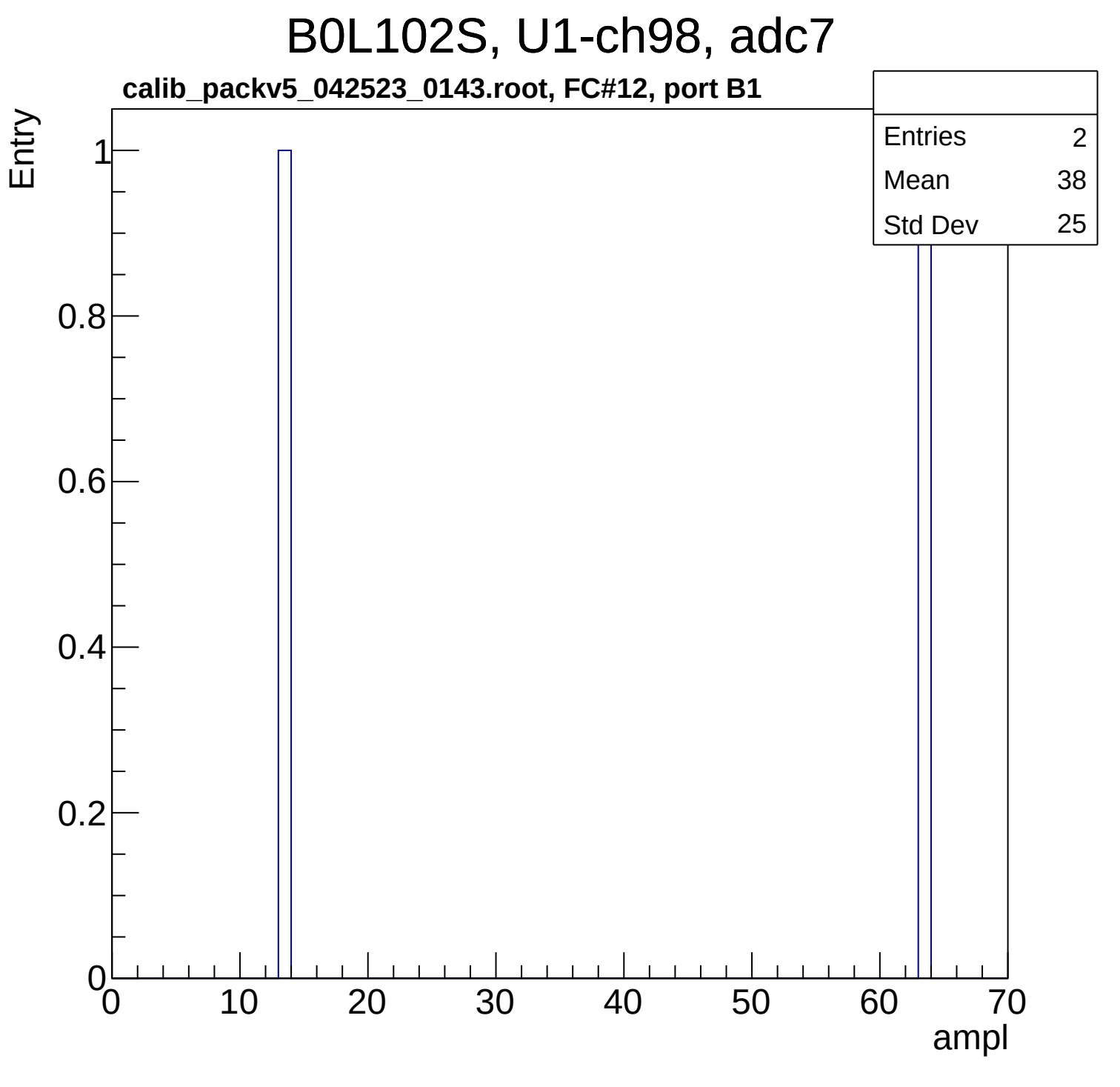
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch98, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 52.77
Std Dev 4.091
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch98, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 57.48
Std Dev 3.69
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch98, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 18
Mean 56.78
Std Dev 14.04
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch98, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 38
Std Dev 25
Entry
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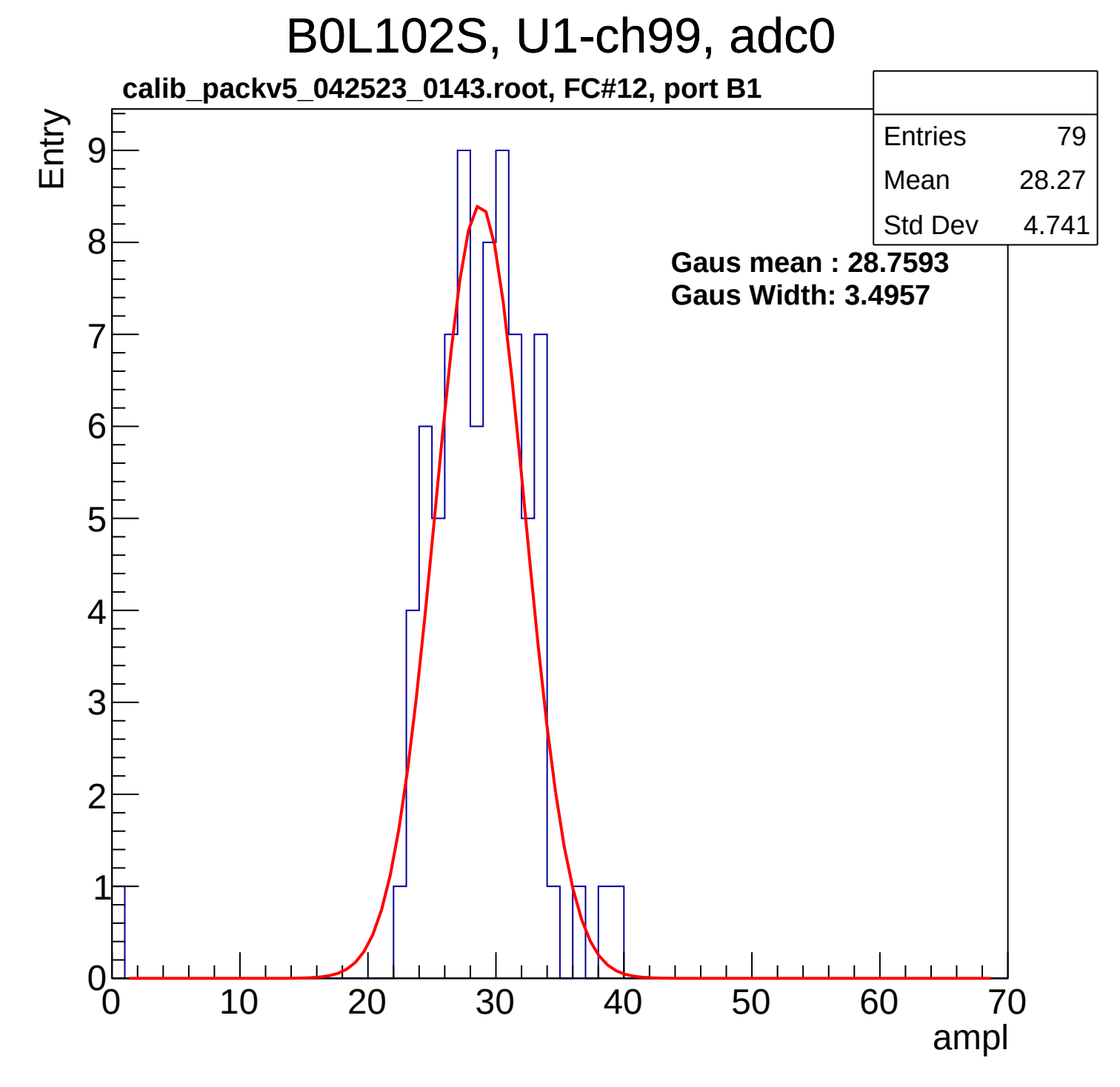
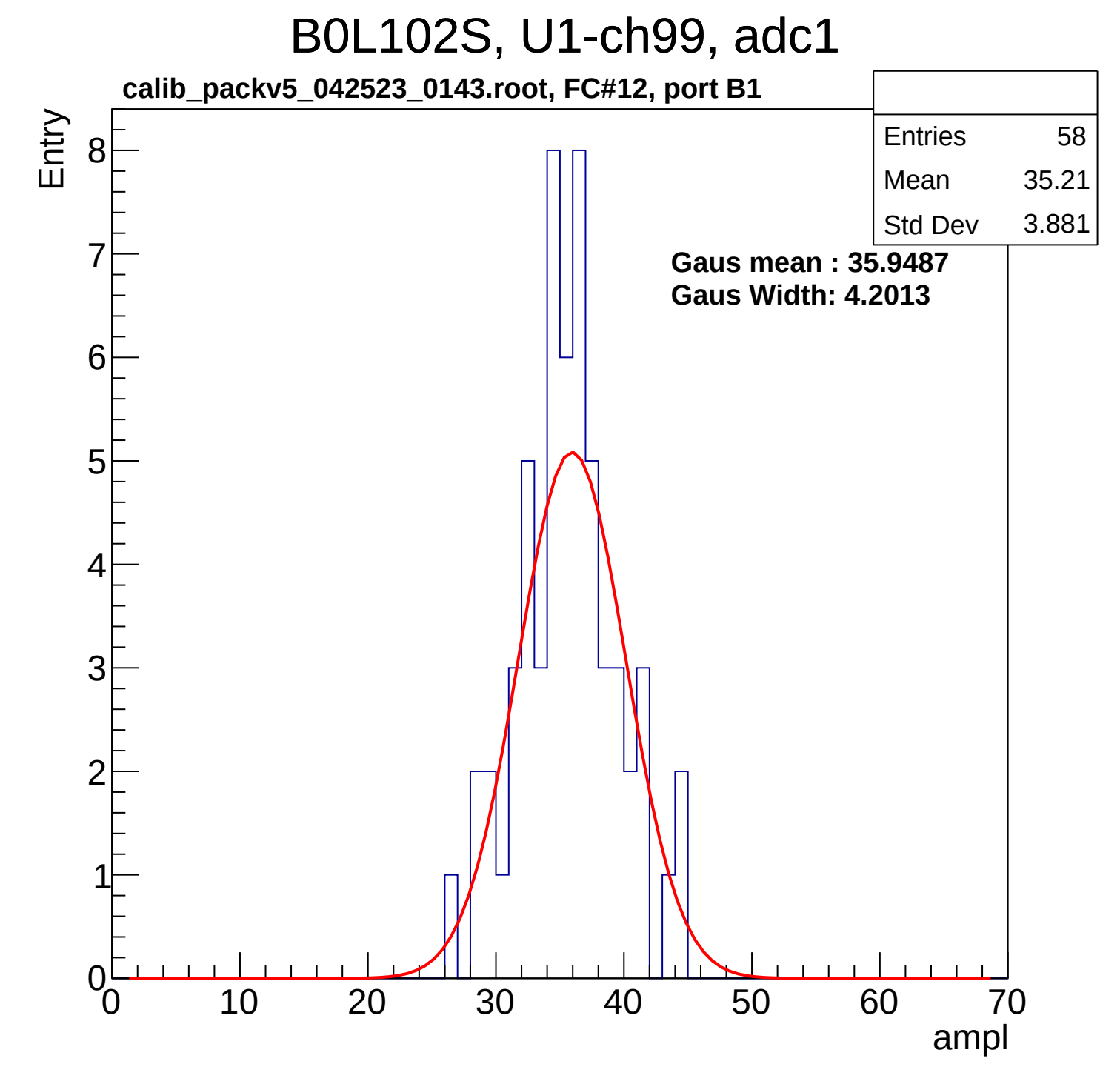
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch99, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 28.27
Std Dev 4.741
Gaus mean : 28.7593
Gaus Width: 3.4957
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch99, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 35.21
Std Dev 3.881
Gaus mean : 35.9487
Gaus Width: 4.2013
Entry
ampl
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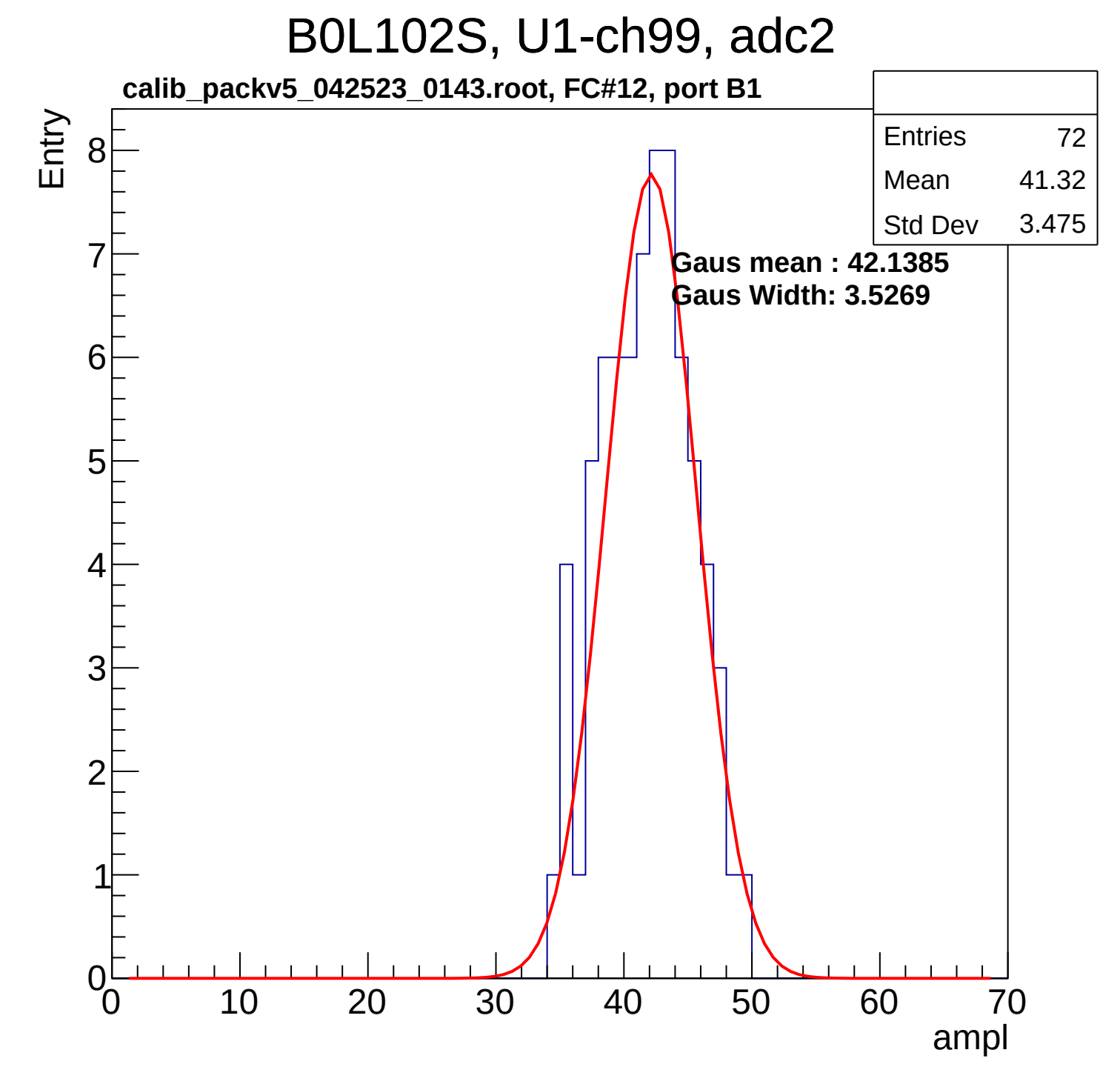
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch99, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 41.32
Std Dev 3.475
Gaus mean : 42.1385
Gaus Width: 3.5269
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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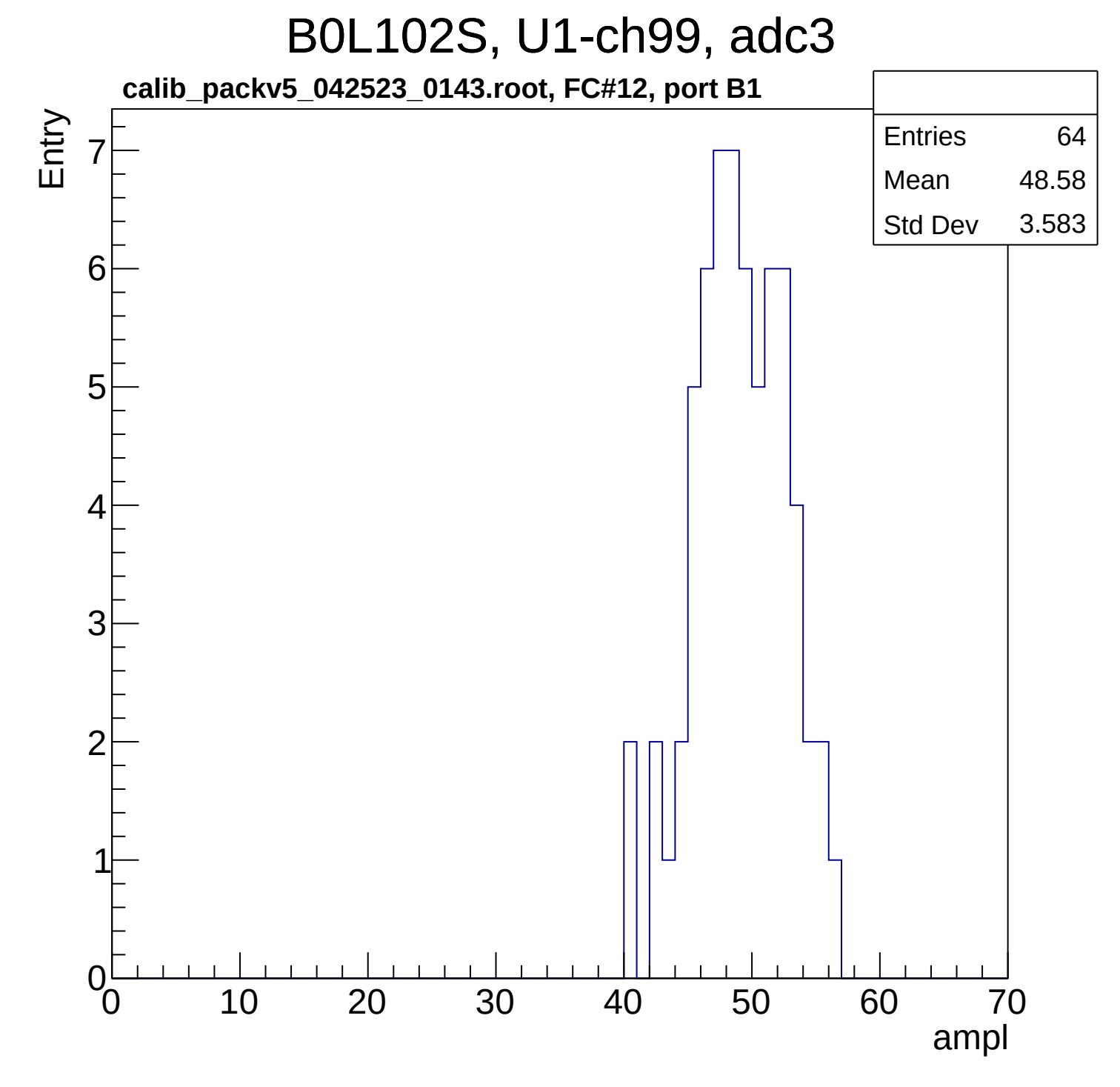
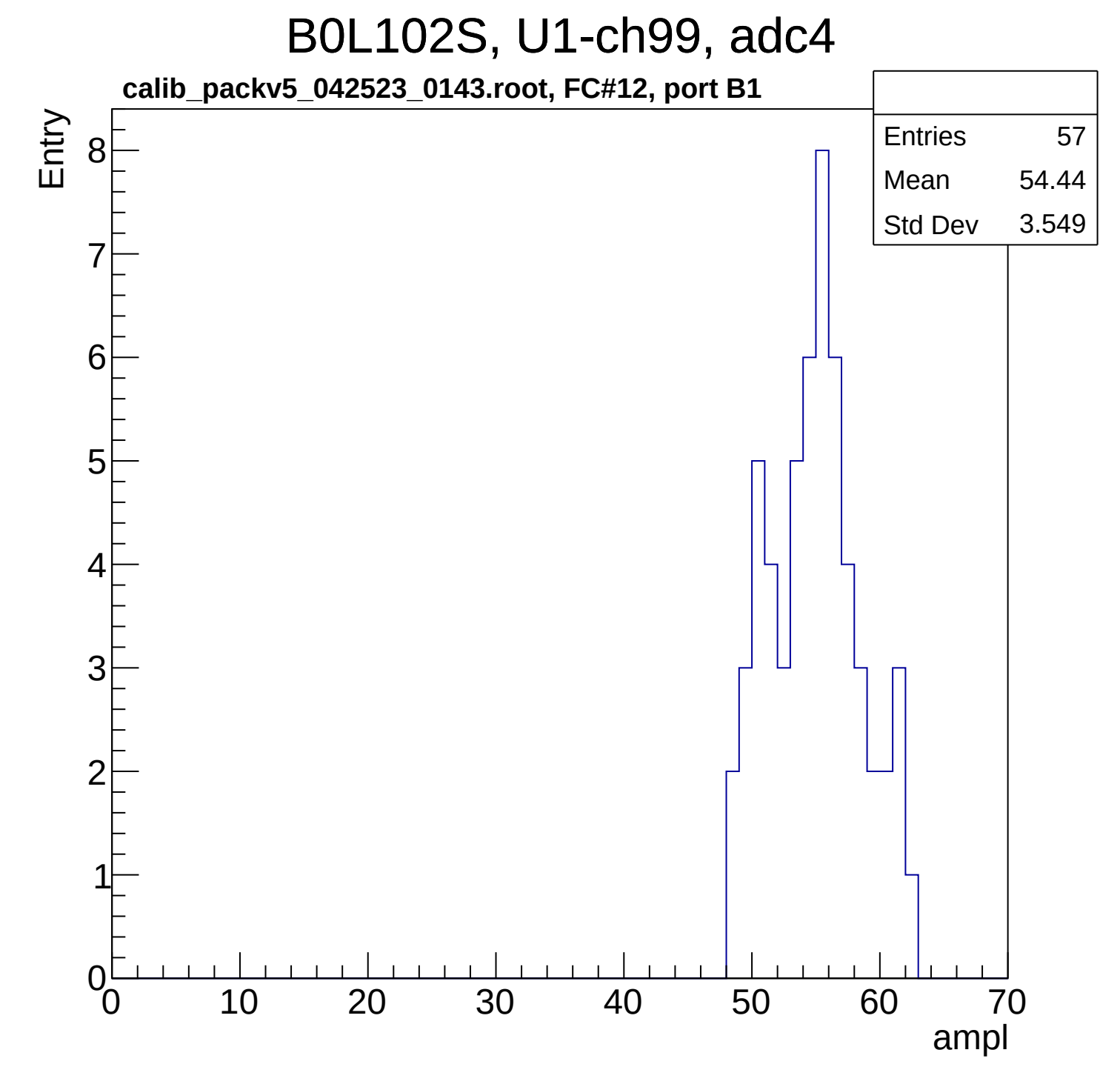

B0L102S, U1-ch99, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 48.58
Std Dev 3.583
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch99, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 54.44
Std Dev 3.549
Entry
ampl
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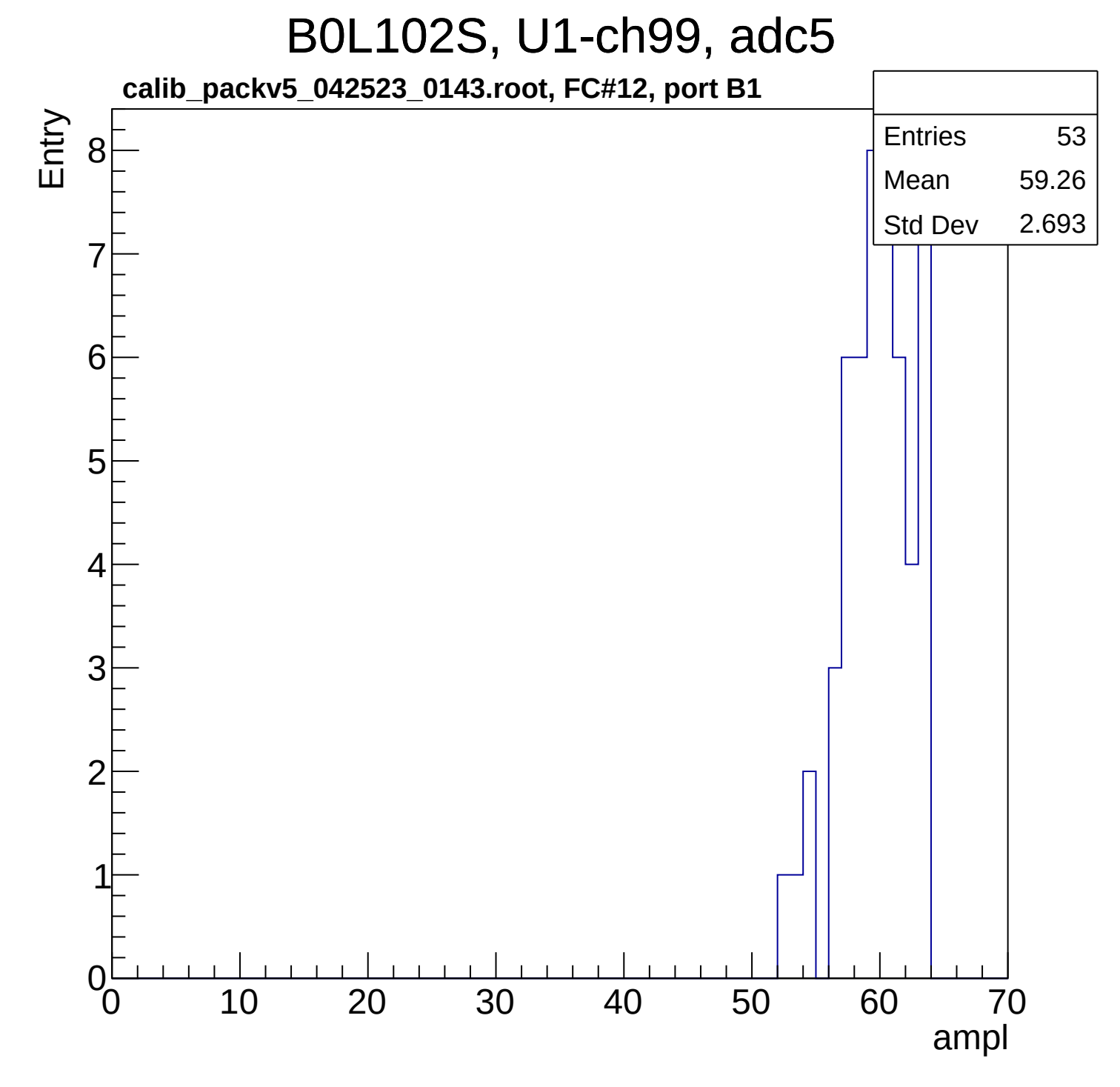
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch99, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 59.26
Std Dev 2.693
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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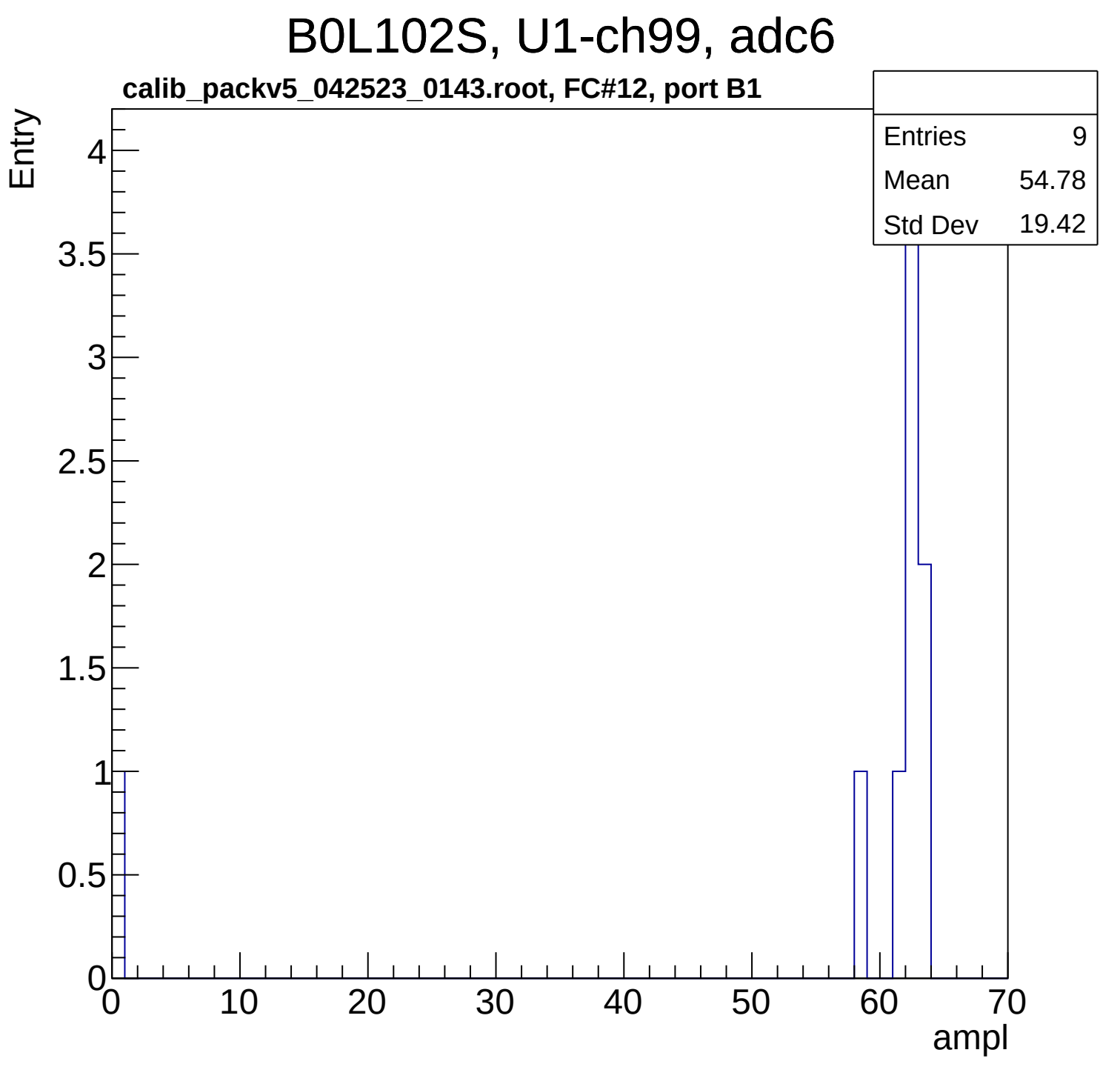
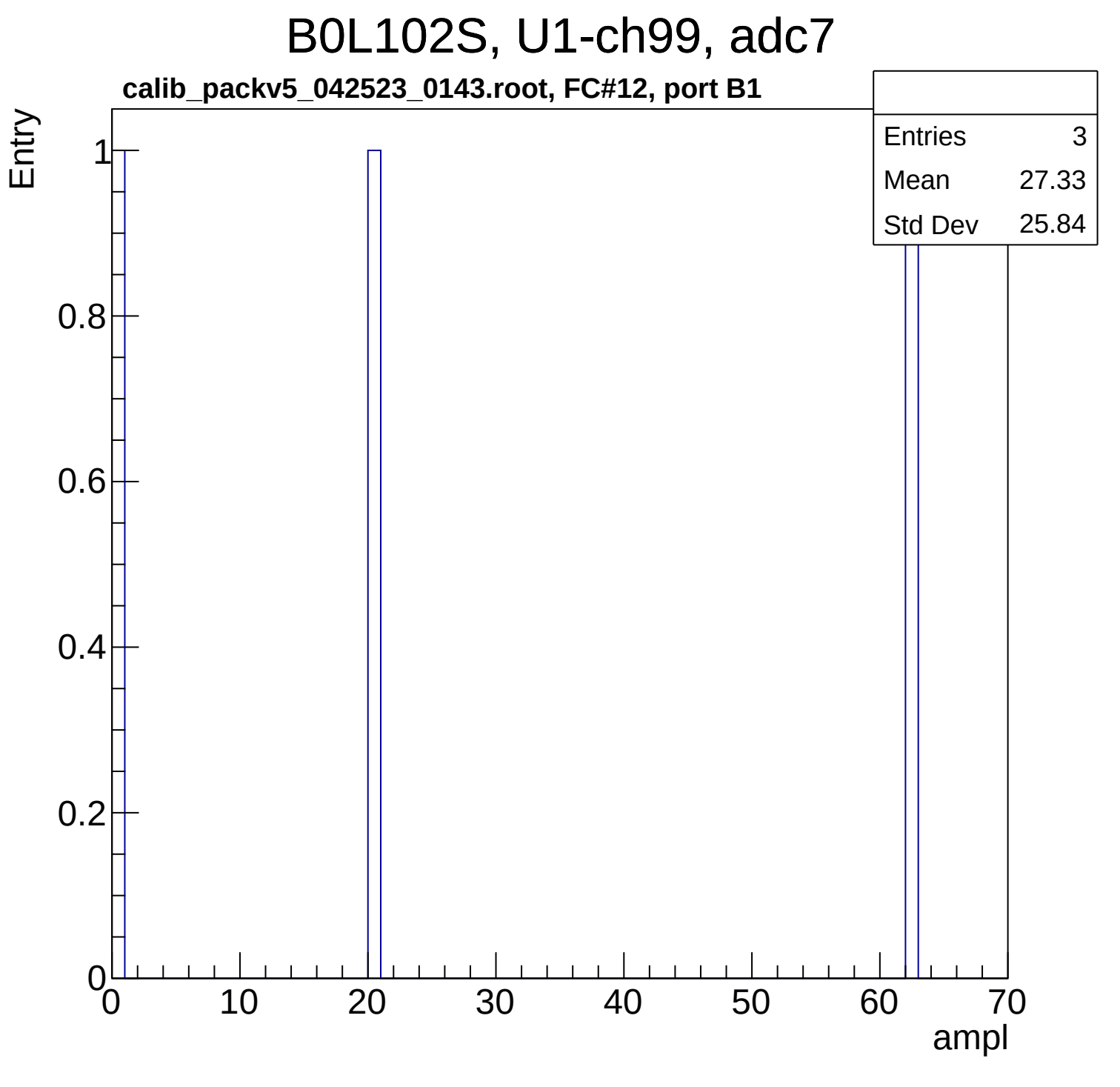

B0L102S, U1-ch99, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 54.78
Std Dev 19.42
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch99, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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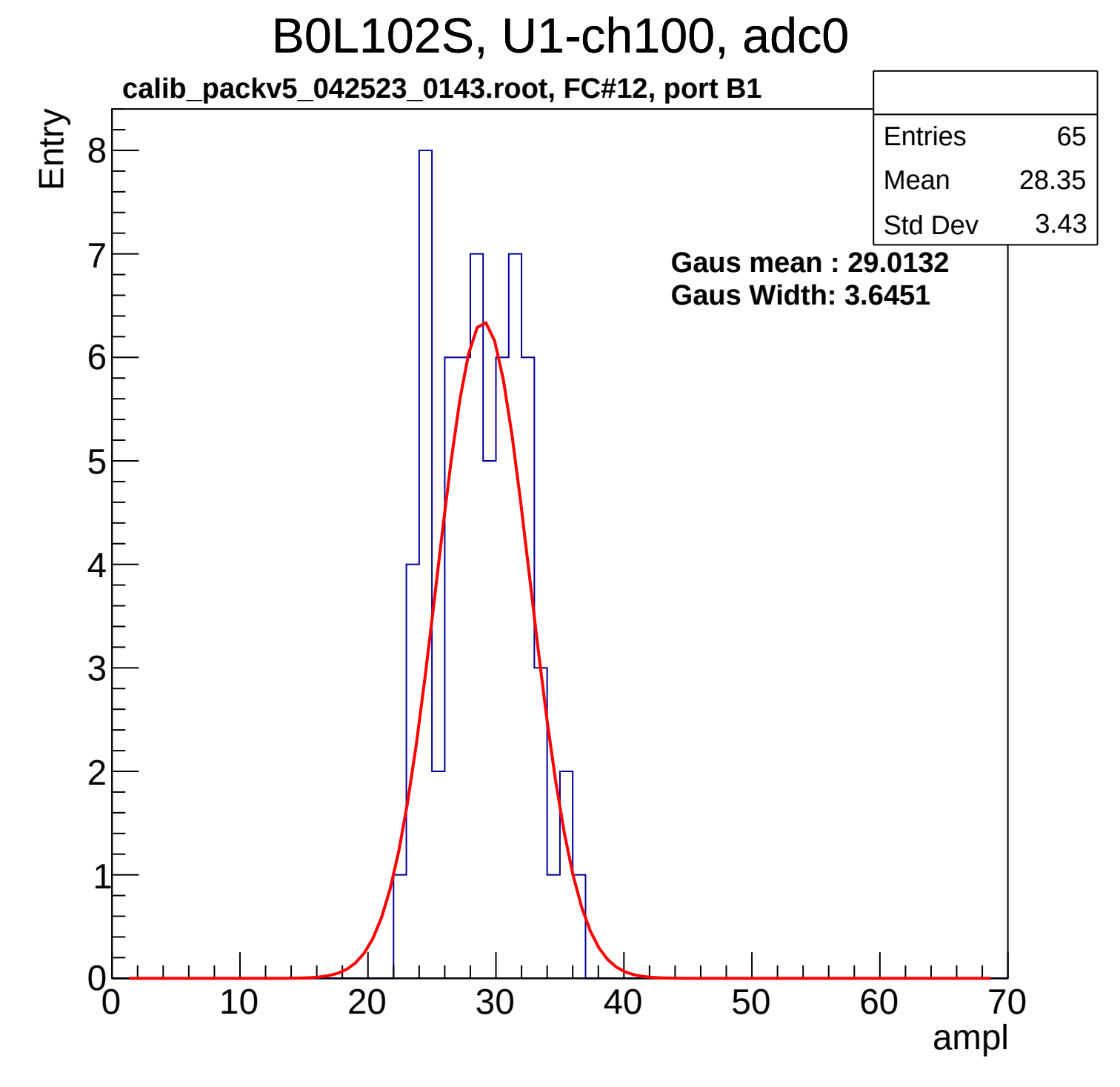
40
50
60
70


B0L102S, U1-ch100, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 28.35
Std Dev 3.43
Gaus mean : 29.0132
Gaus Width: 3.6451
Entry
ampl
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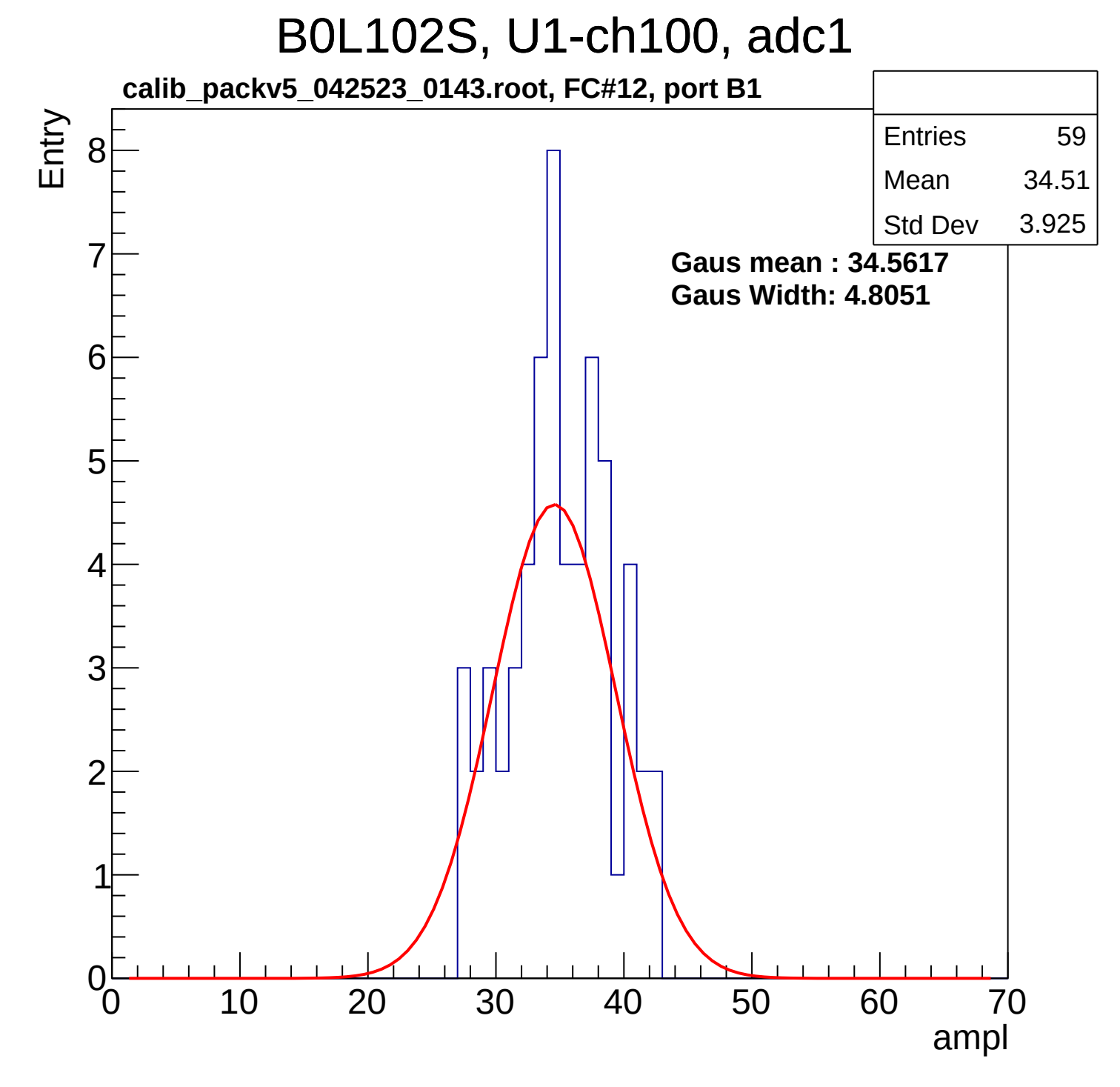
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch100, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 34.51
Std Dev 3.925
Gaus mean : 34.5617
Gaus Width: 4.8051
Entry
ampl
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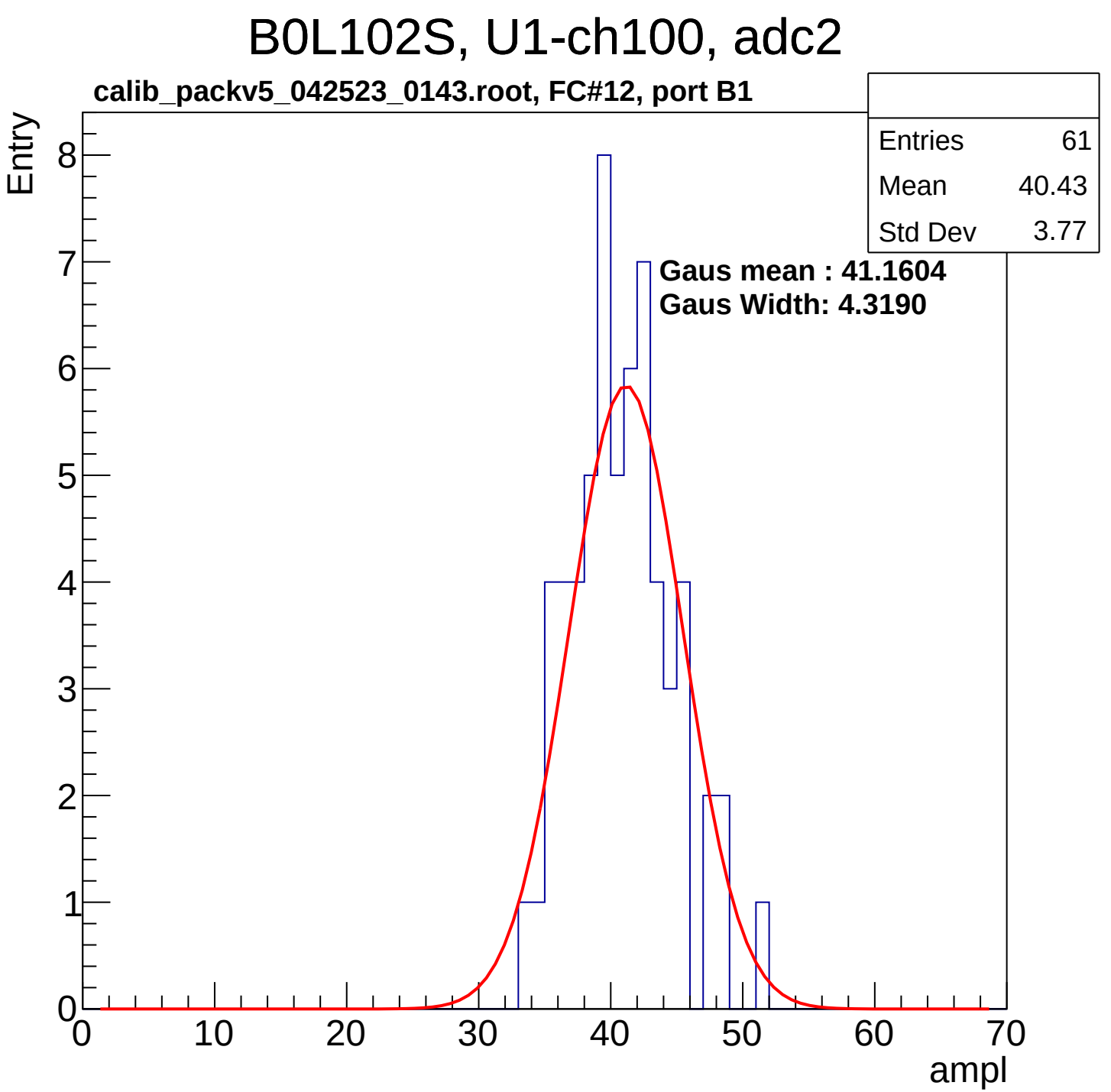
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch100, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 40.43
Std Dev 3.77
Gaus mean : 41.1604
Gaus Width: 4.3190
Entry
ampl
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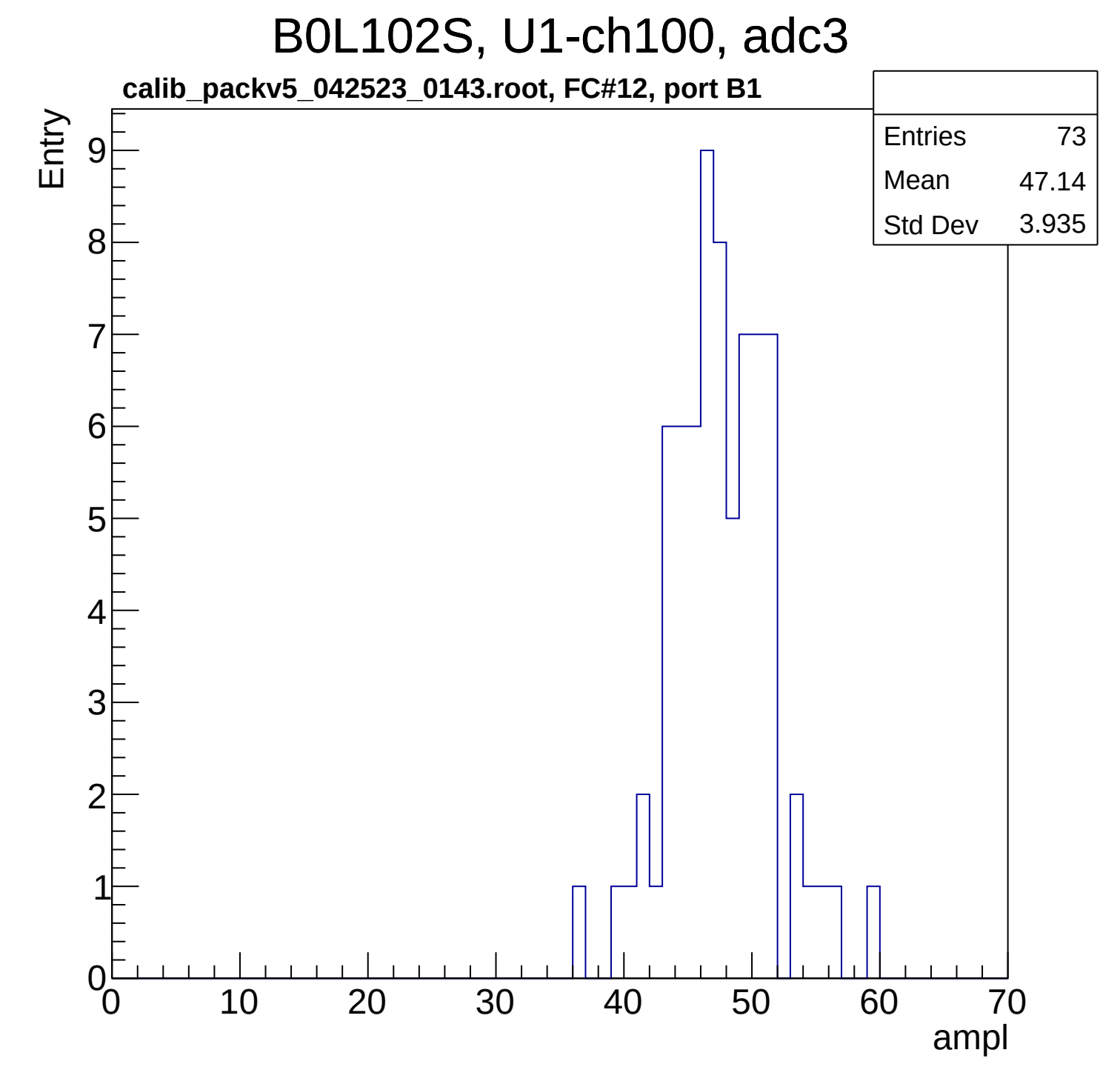
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch100, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 47.14
Std Dev 3.935
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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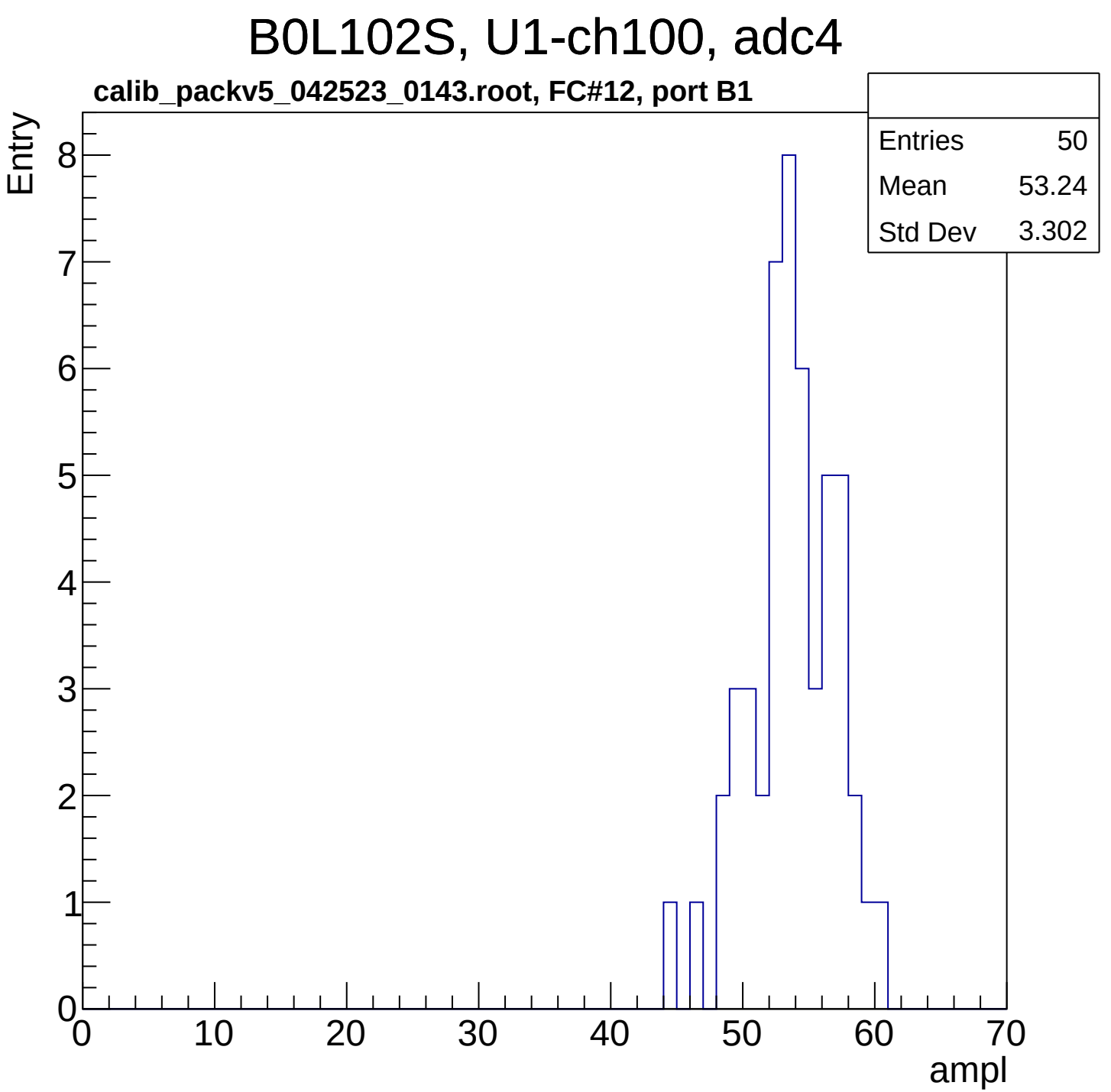
B0L102S, U1-ch100, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 53.24
Std Dev 3.302
Entry
ampl
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2
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4
5
6
7
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0
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20
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40
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60
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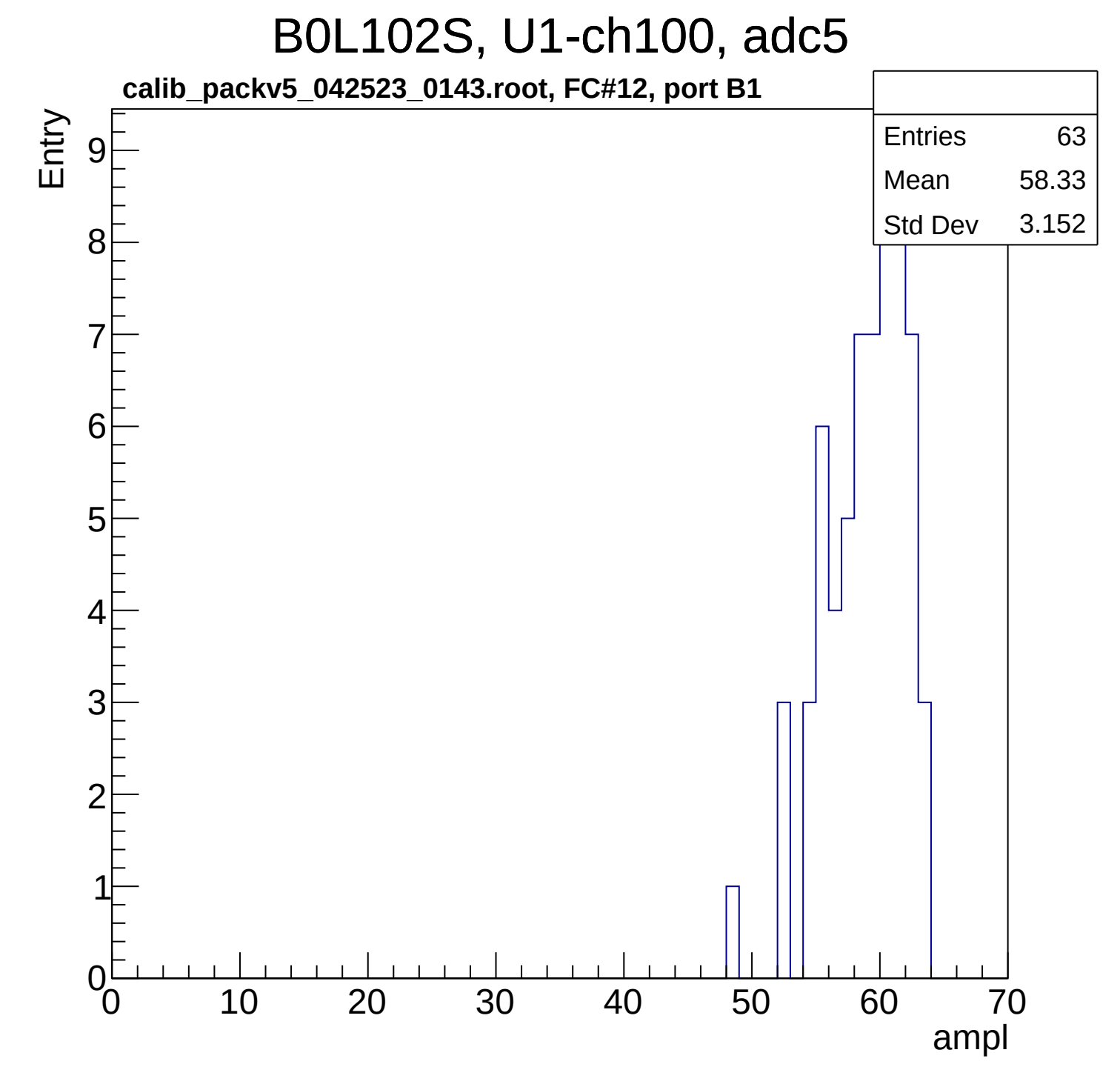
B0L102S, U1-ch100, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 58.33
Std Dev 3.152
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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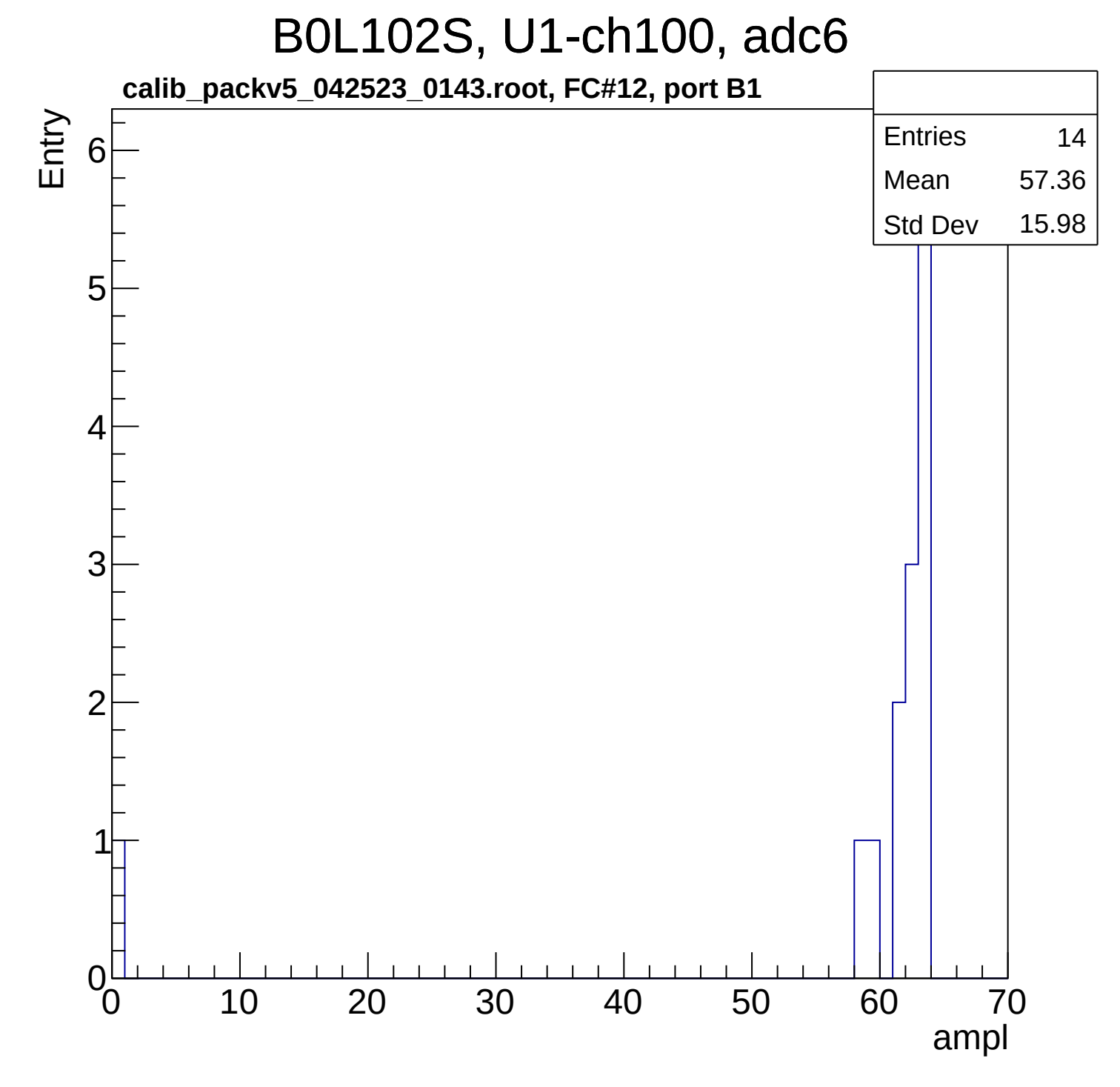
B0L102S, U1-ch100, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 57.36
Std Dev 15.98
Entry
0
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2
3
4
5
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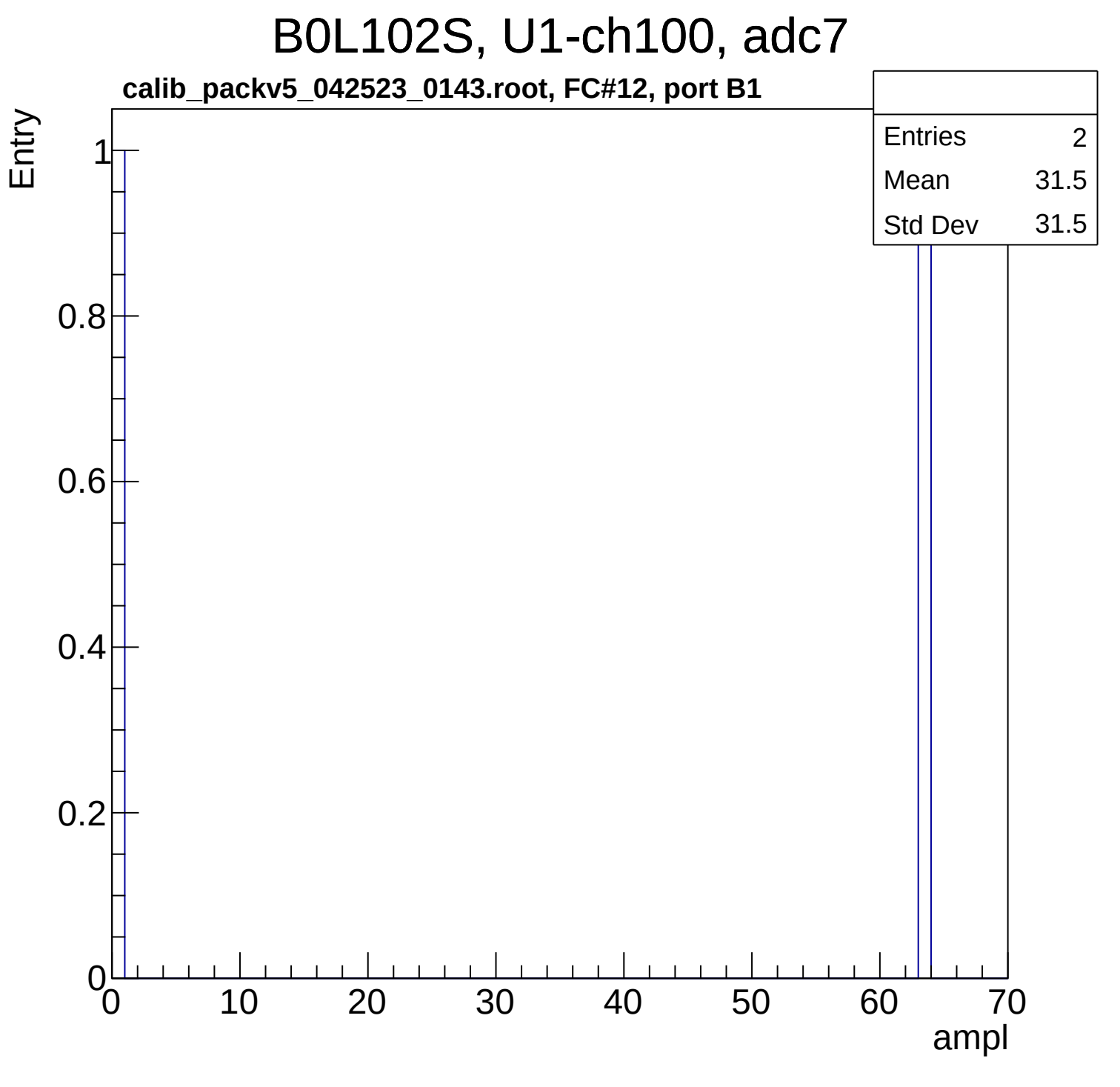
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch100, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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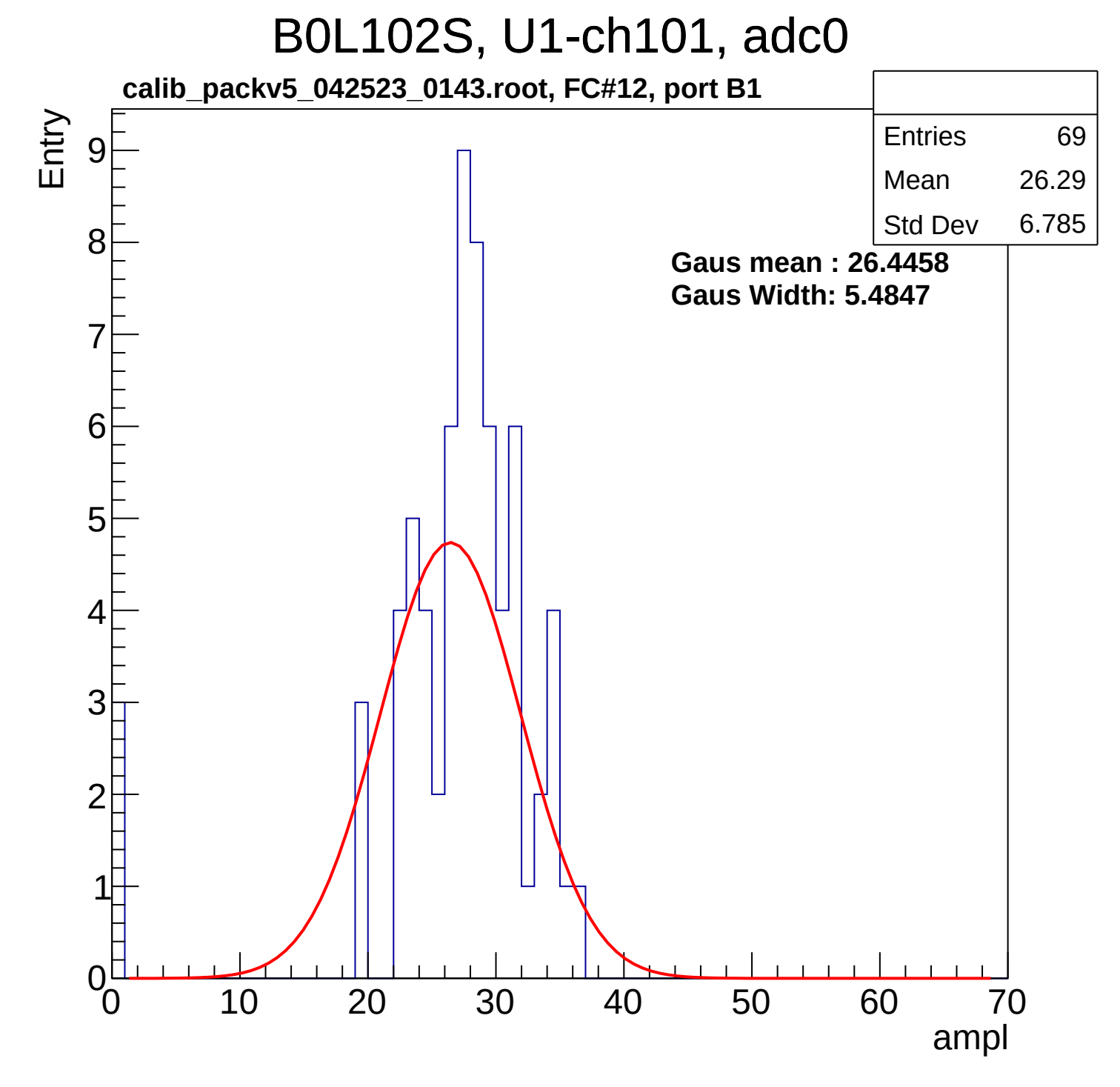
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch101, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 26.29
Std Dev 6.785
Gaus mean : 26.4458
Gaus Width: 5.4847
Entry
ampl
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2
3
4
5
6
7
8
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0
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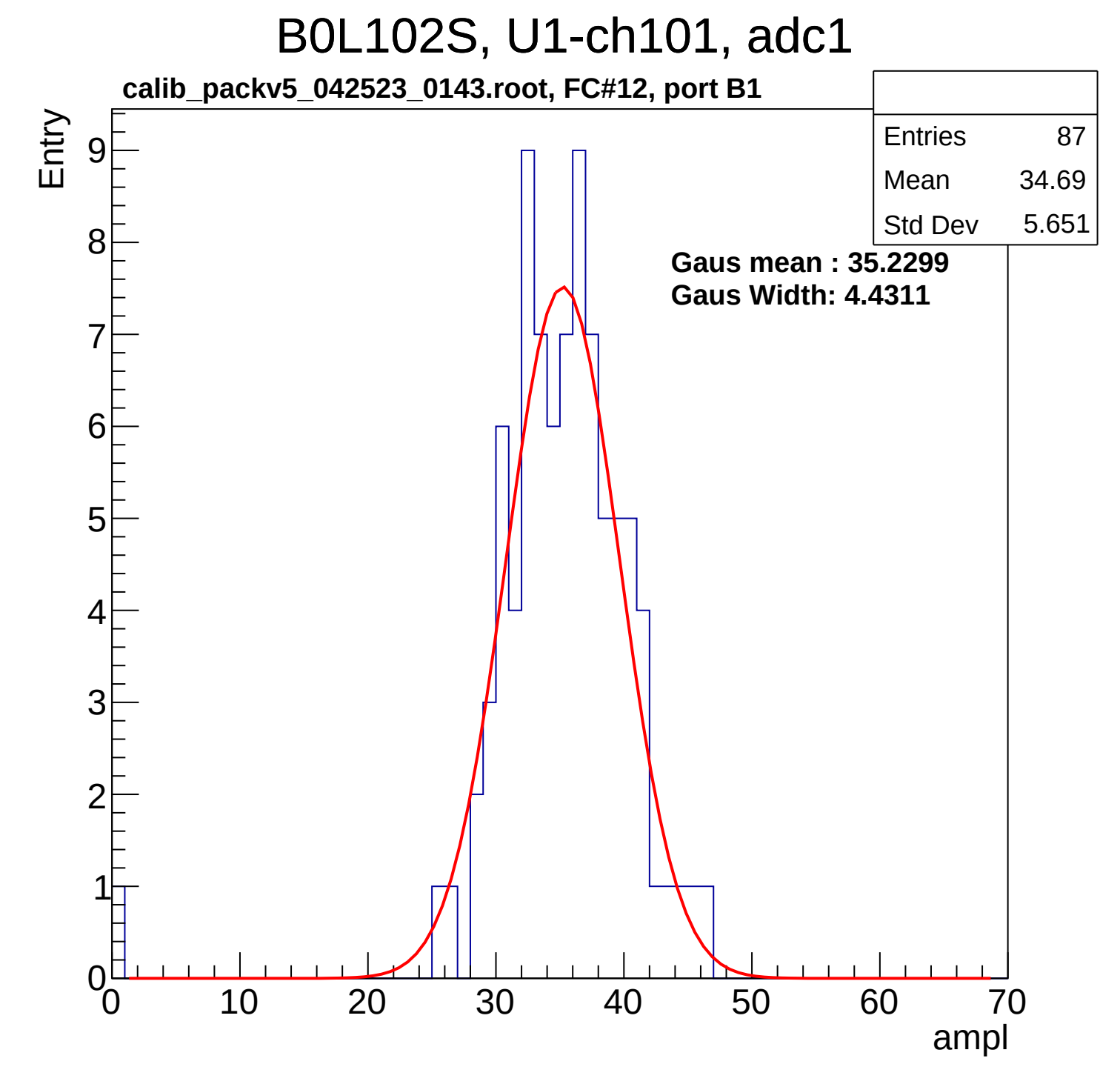
B0L102S, U1-ch101, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 34.69
Std Dev 5.651
Gaus mean : 35.2299
Gaus Width: 4.4311
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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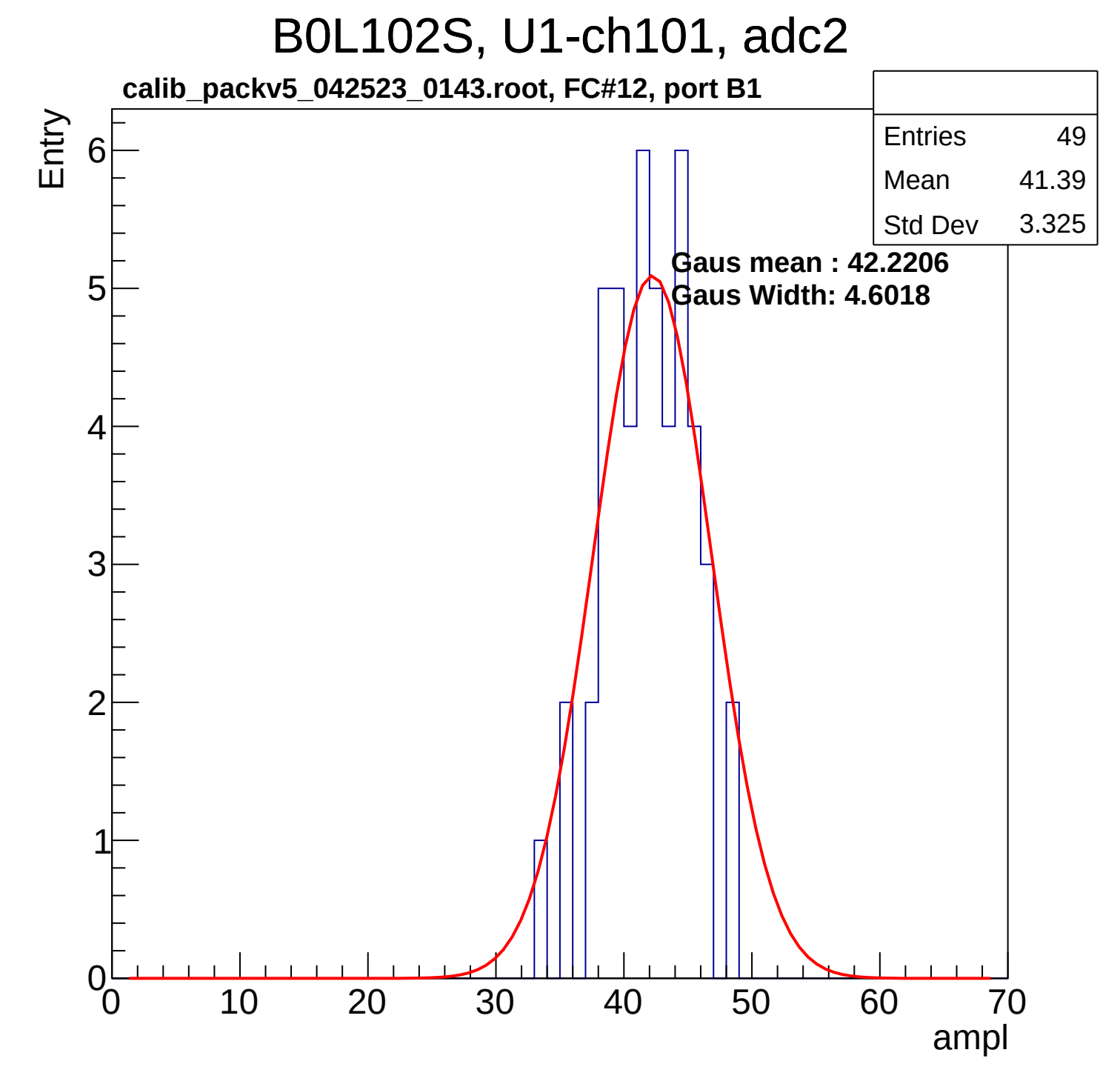
40
50
60
70


B0L102S, U1-ch101, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 41.39
Std Dev 3.325
Gaus mean : 42.2206
Gaus Width: 4.6018
Entry
ampl
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2
3
4
5
6
0
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20
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40
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60
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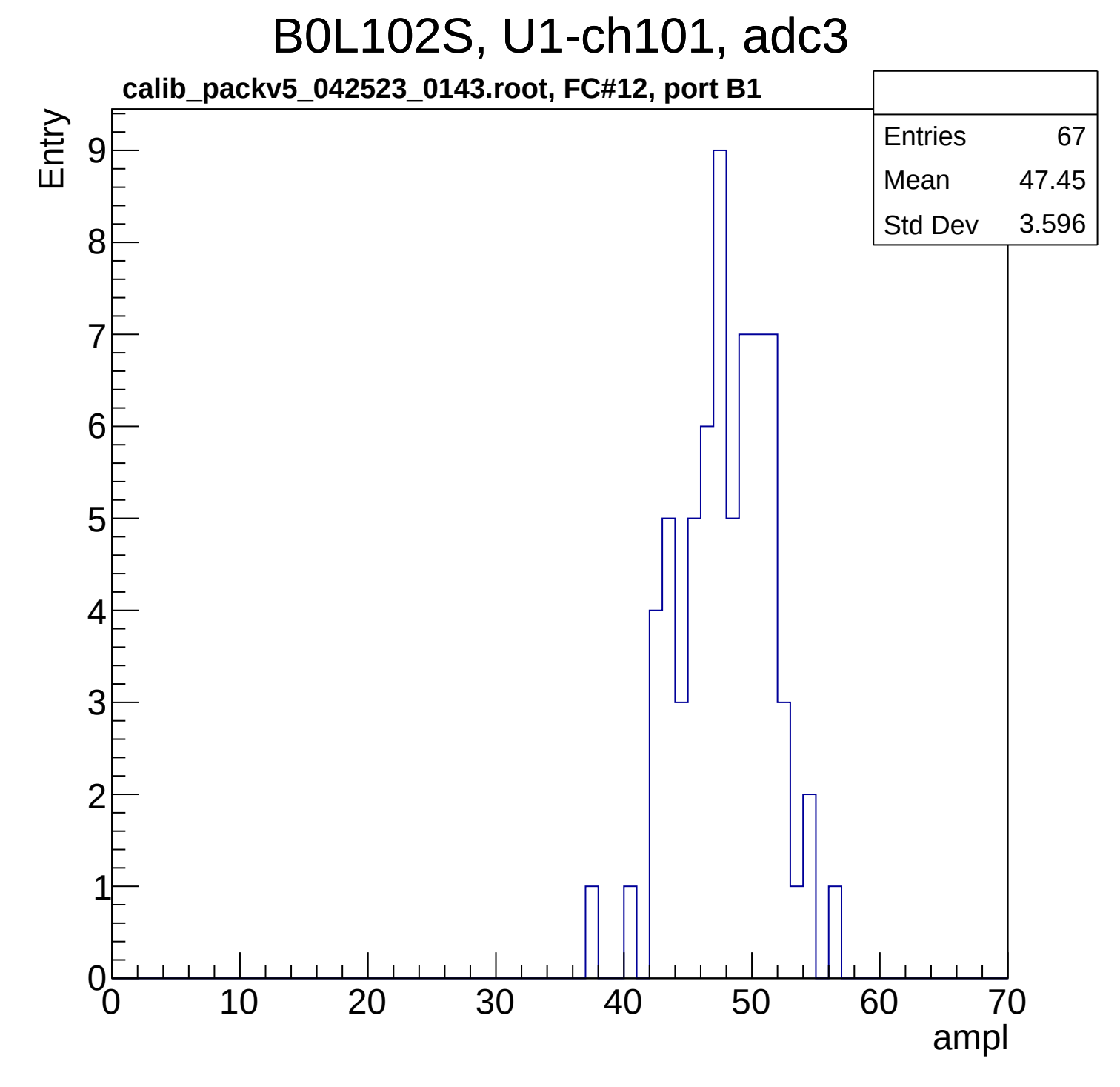
B0L102S, U1-ch101, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 47.45
Std Dev 3.596
Entry
ampl
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2
3
4
5
6
7
8
9
0
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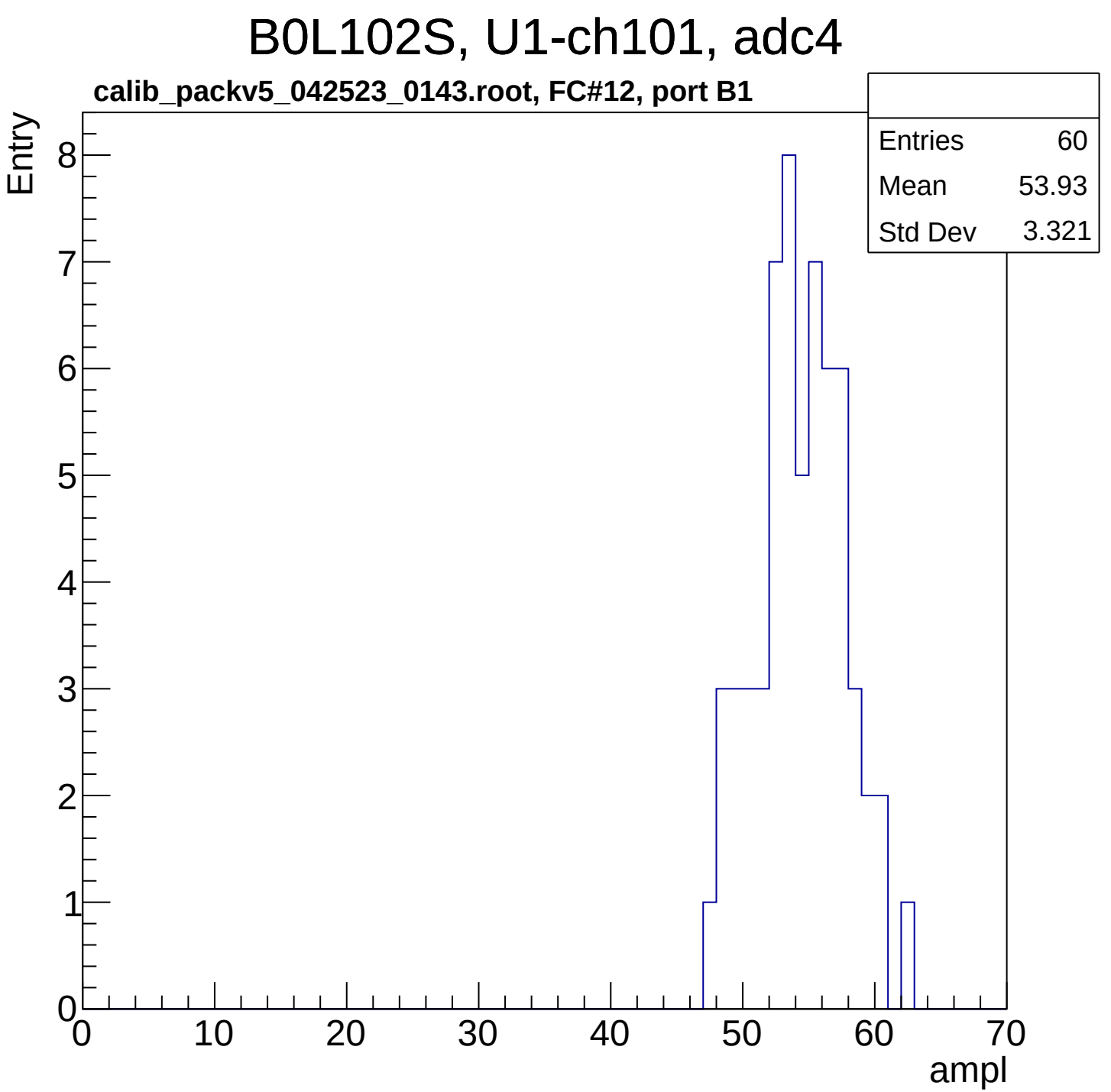
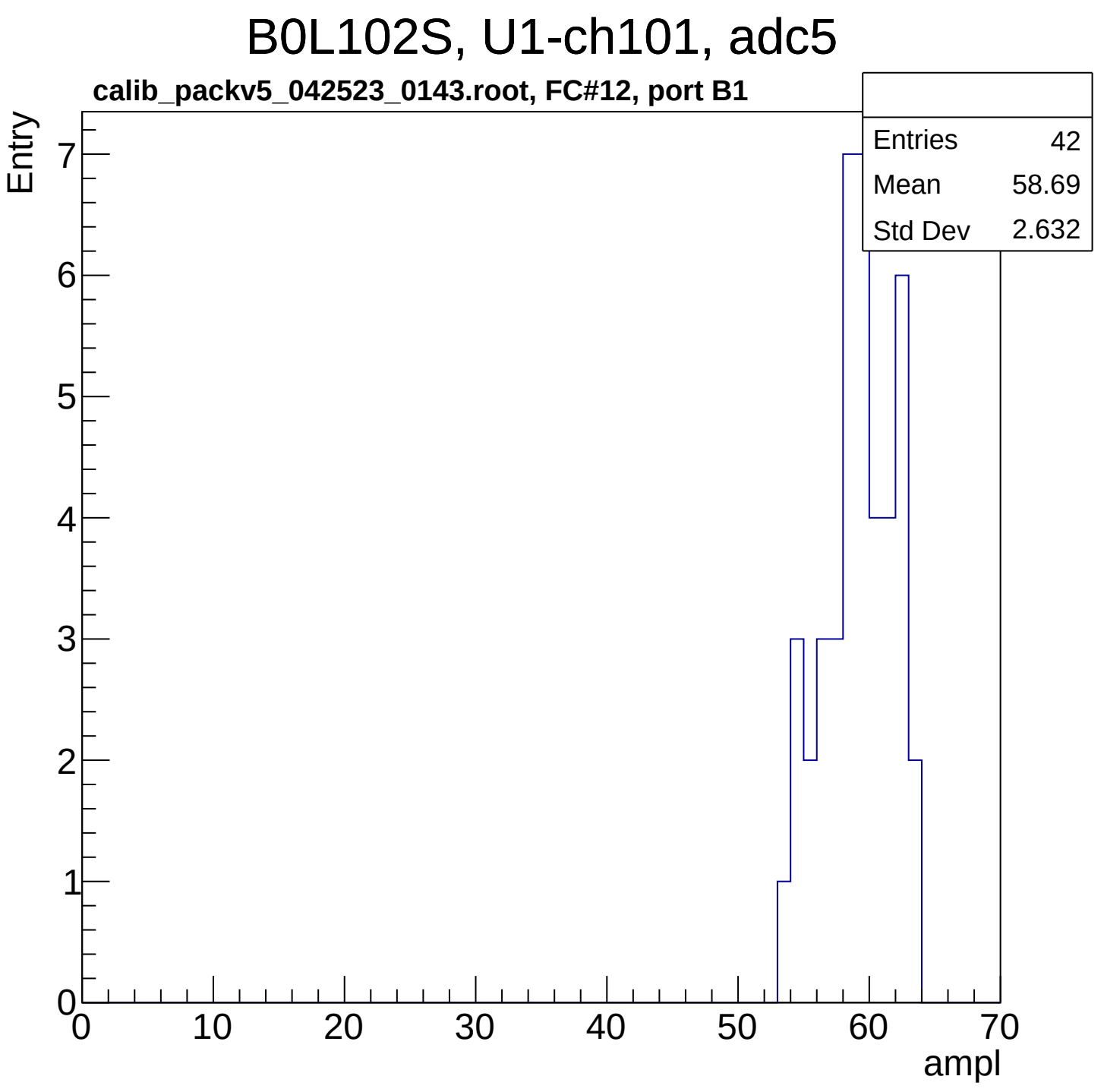

B0L102S, U1-ch101, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 53.93
Std Dev 3.321
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch101, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 58.69
Std Dev 2.632
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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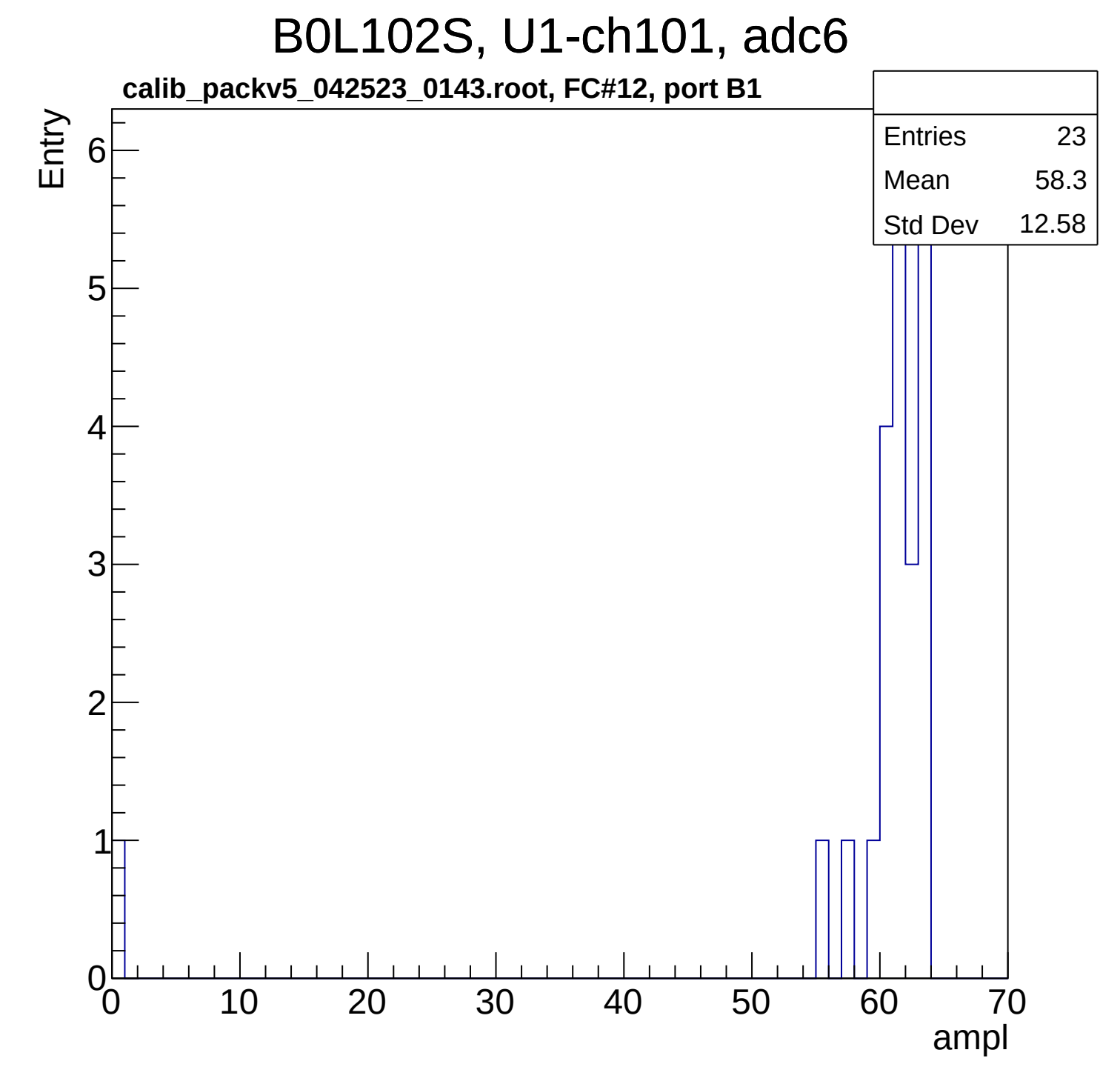
40
50
60
70


B0L102S, U1-ch101, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 23
Mean 58.3
Std Dev 12.58
Entry
0
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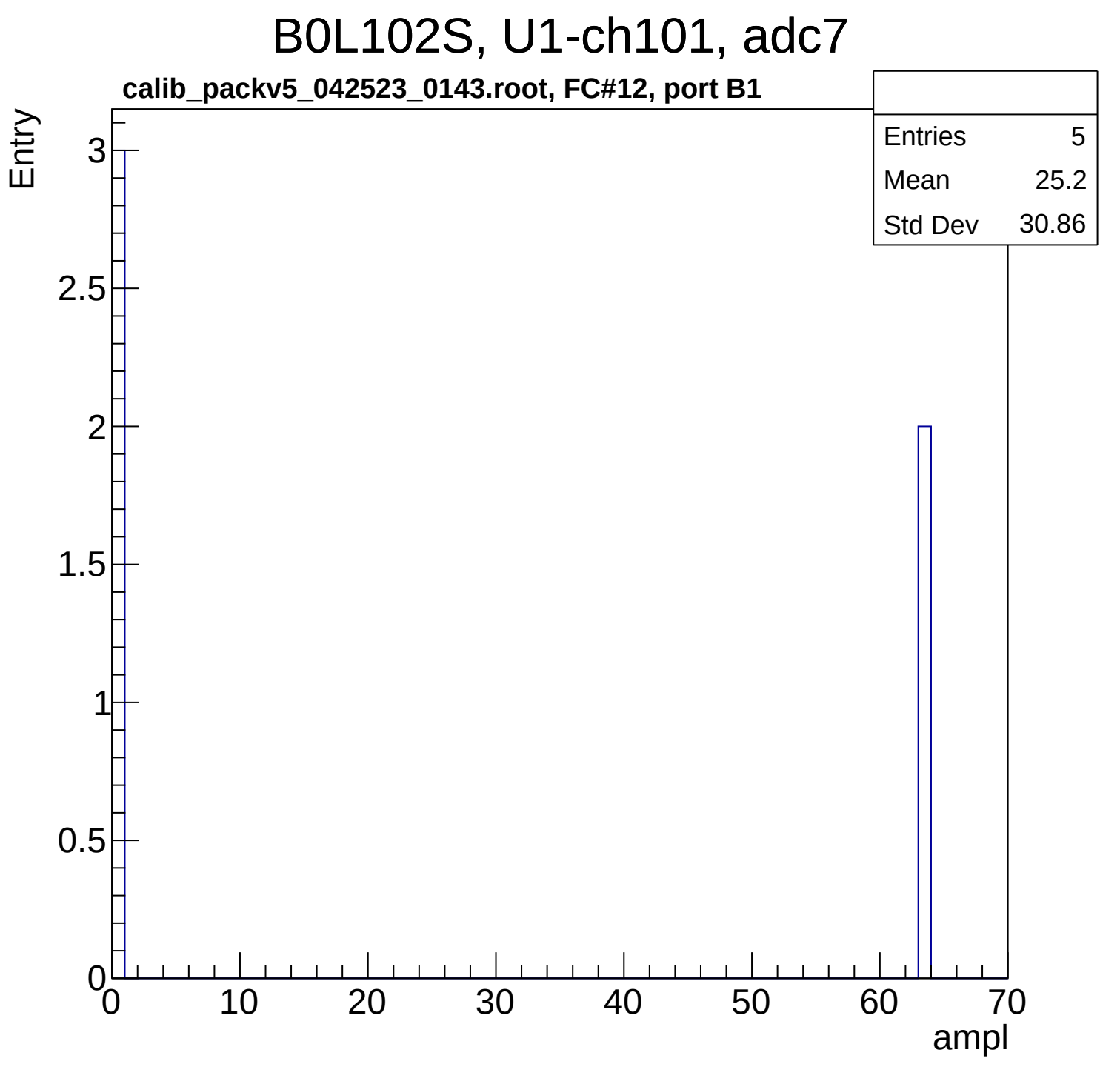
40
50
60
70
ampl


B0L102S, U1-ch101, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 25.2
Std Dev 30.86
Entry
ampl
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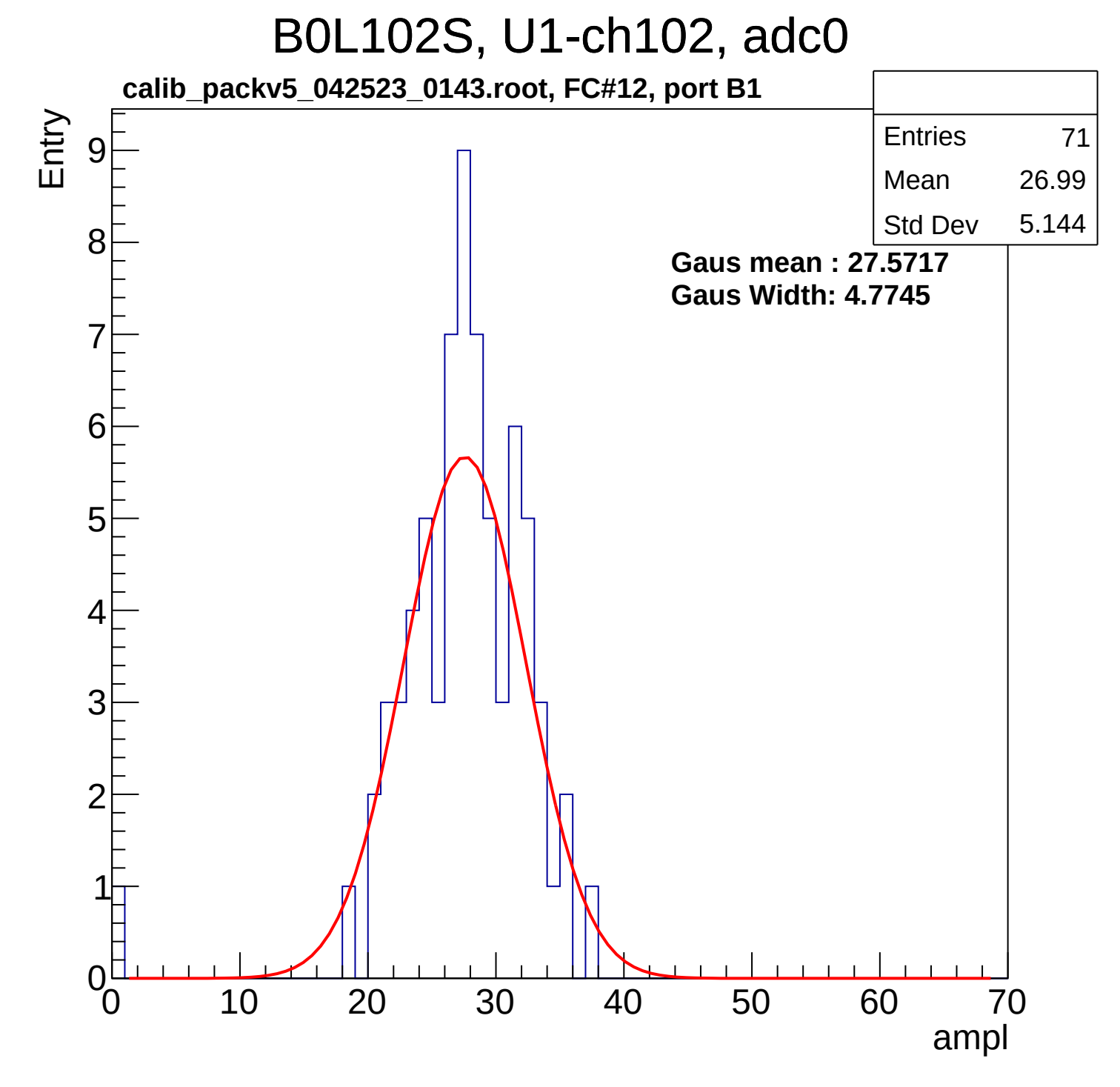
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch102, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 26.99
Std Dev 5.144
Gaus mean : 27.5717
Gaus Width: 4.7745
Entry
ampl
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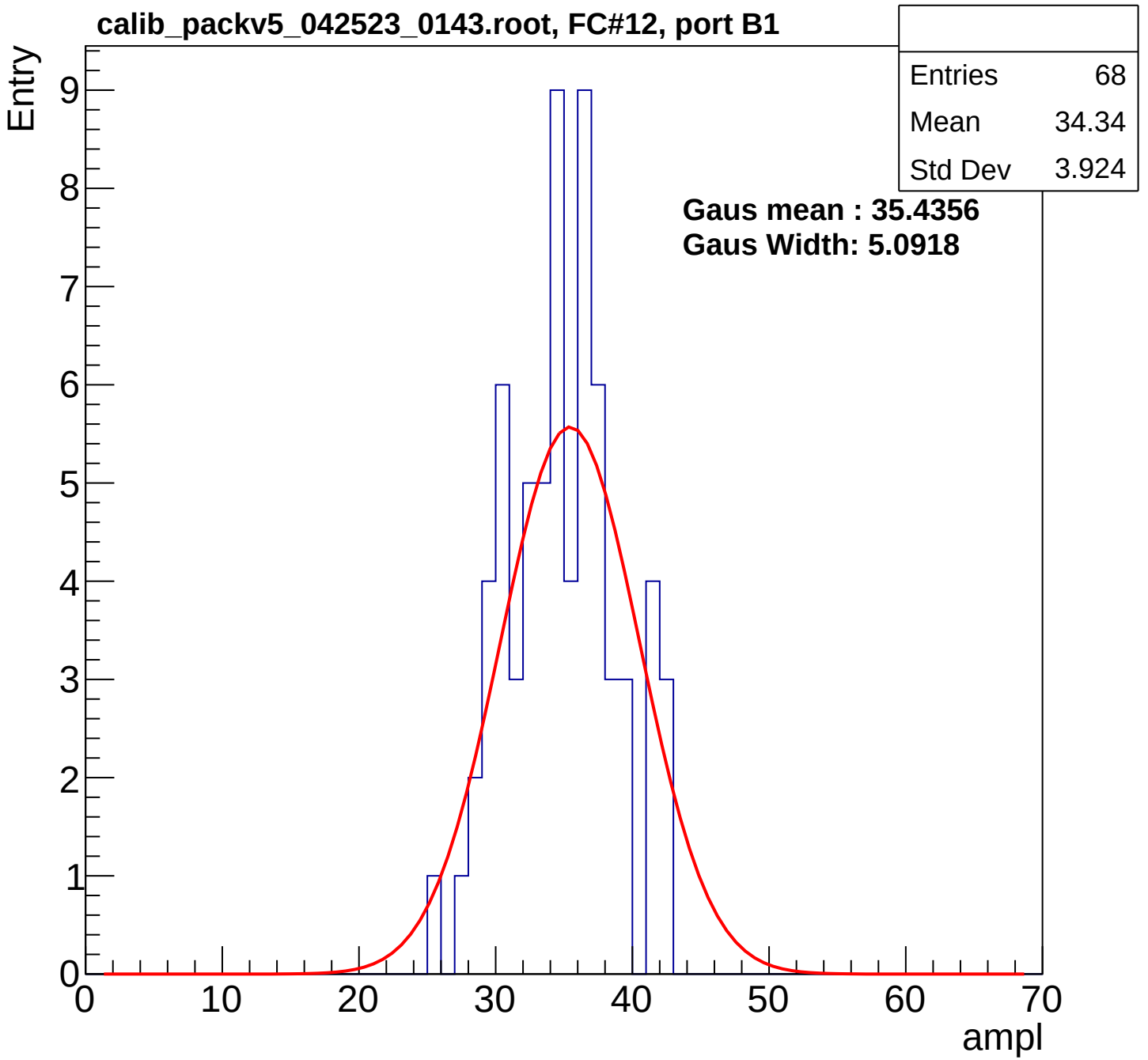
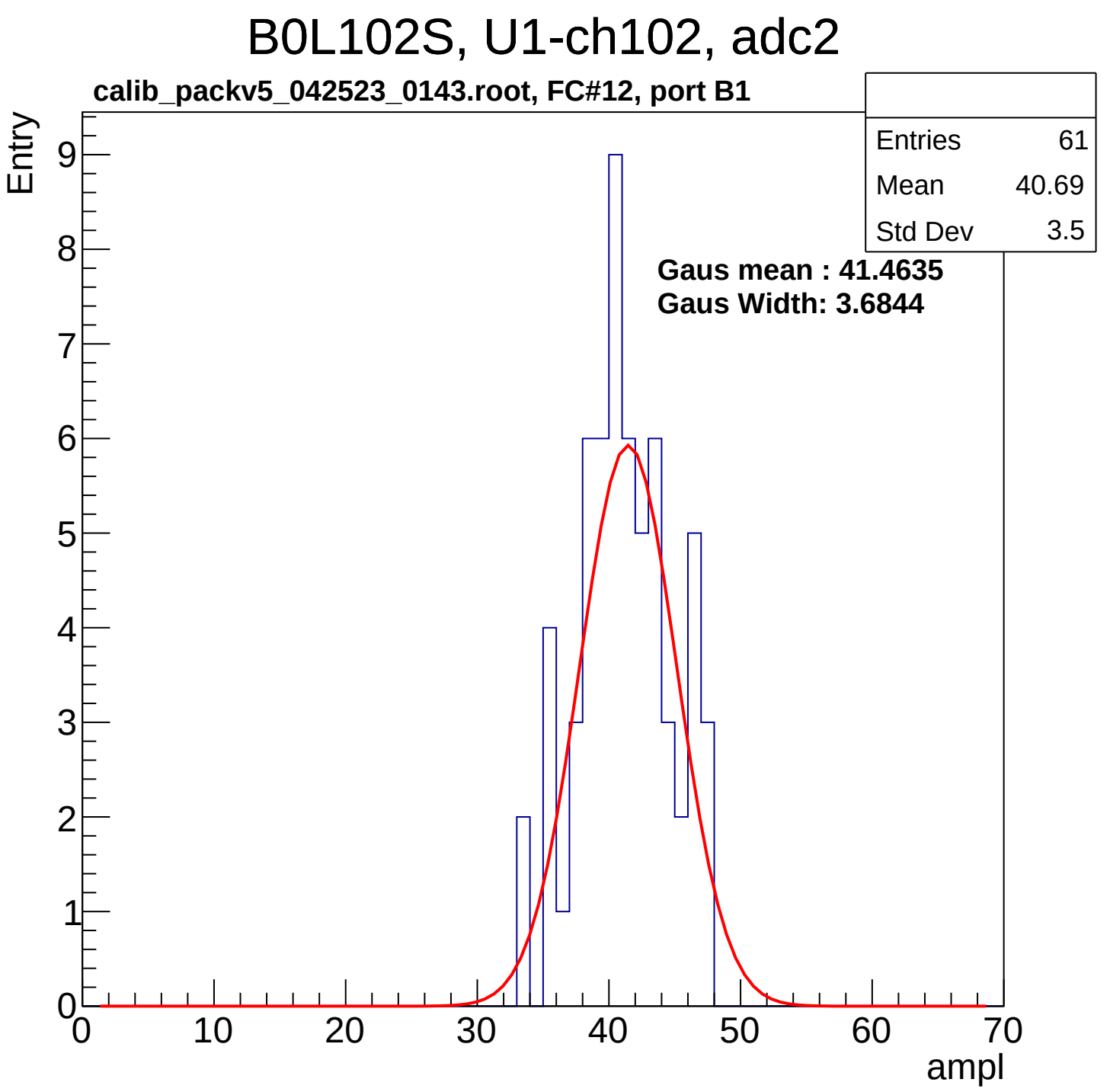
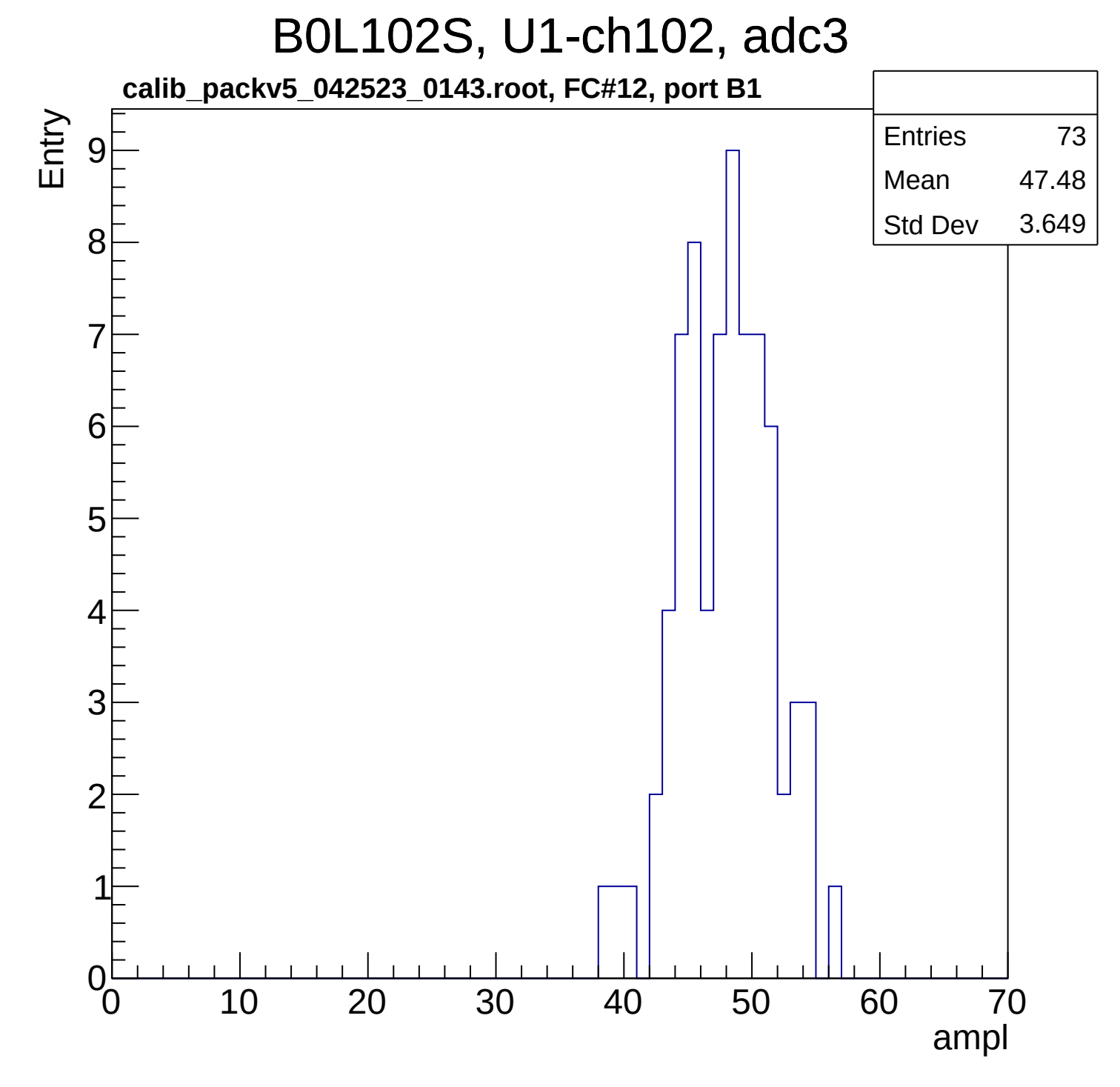
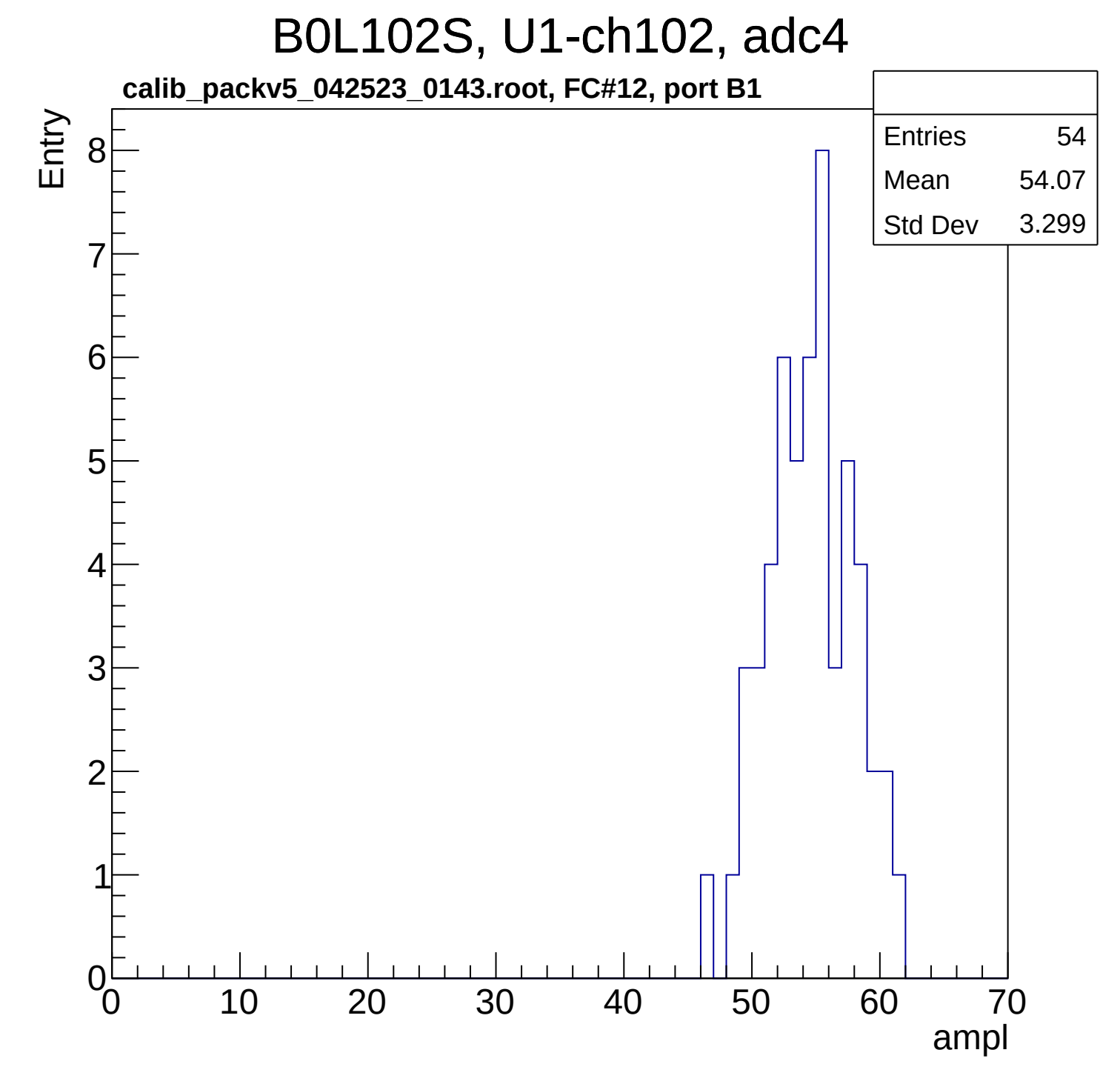
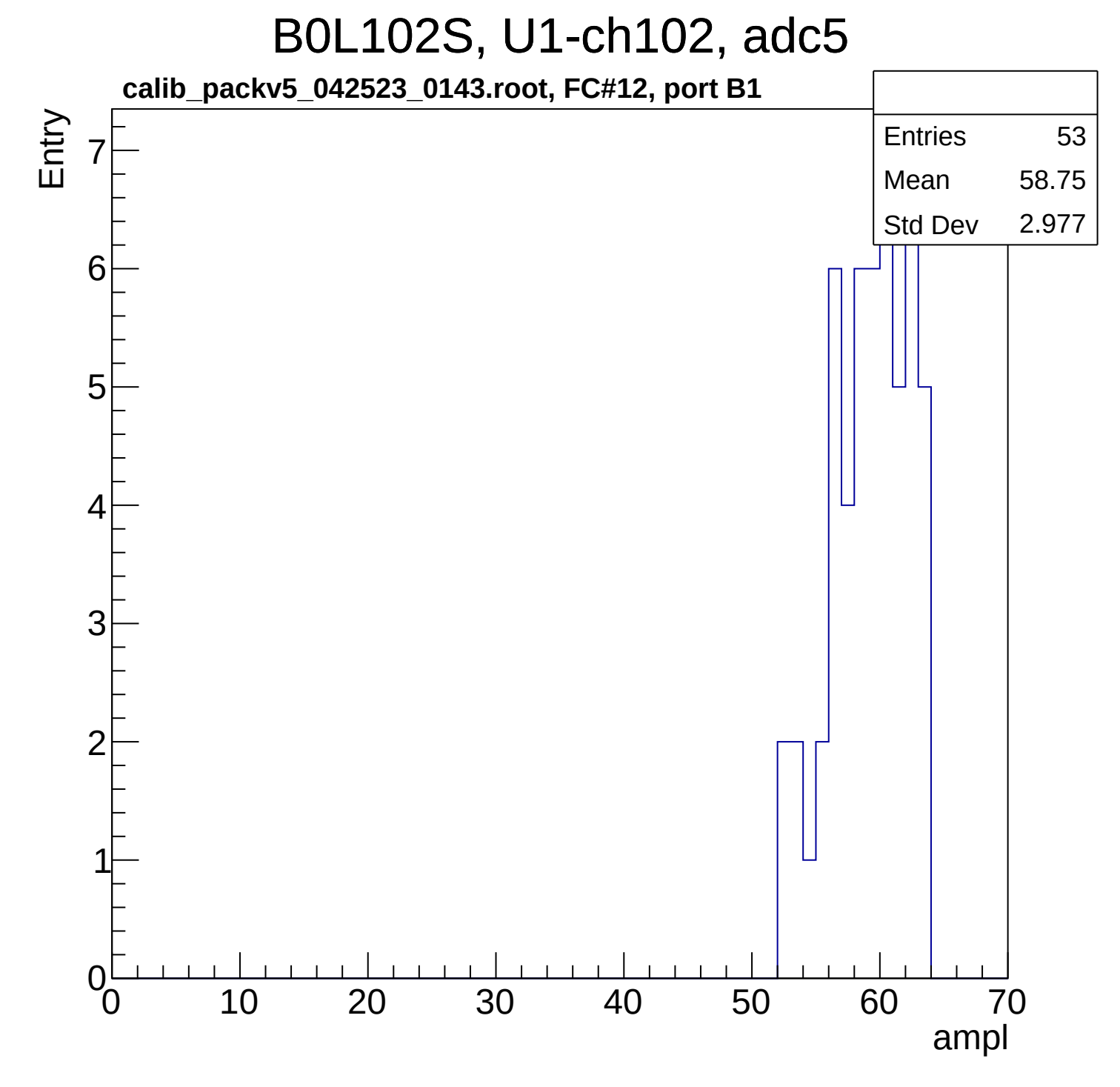
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

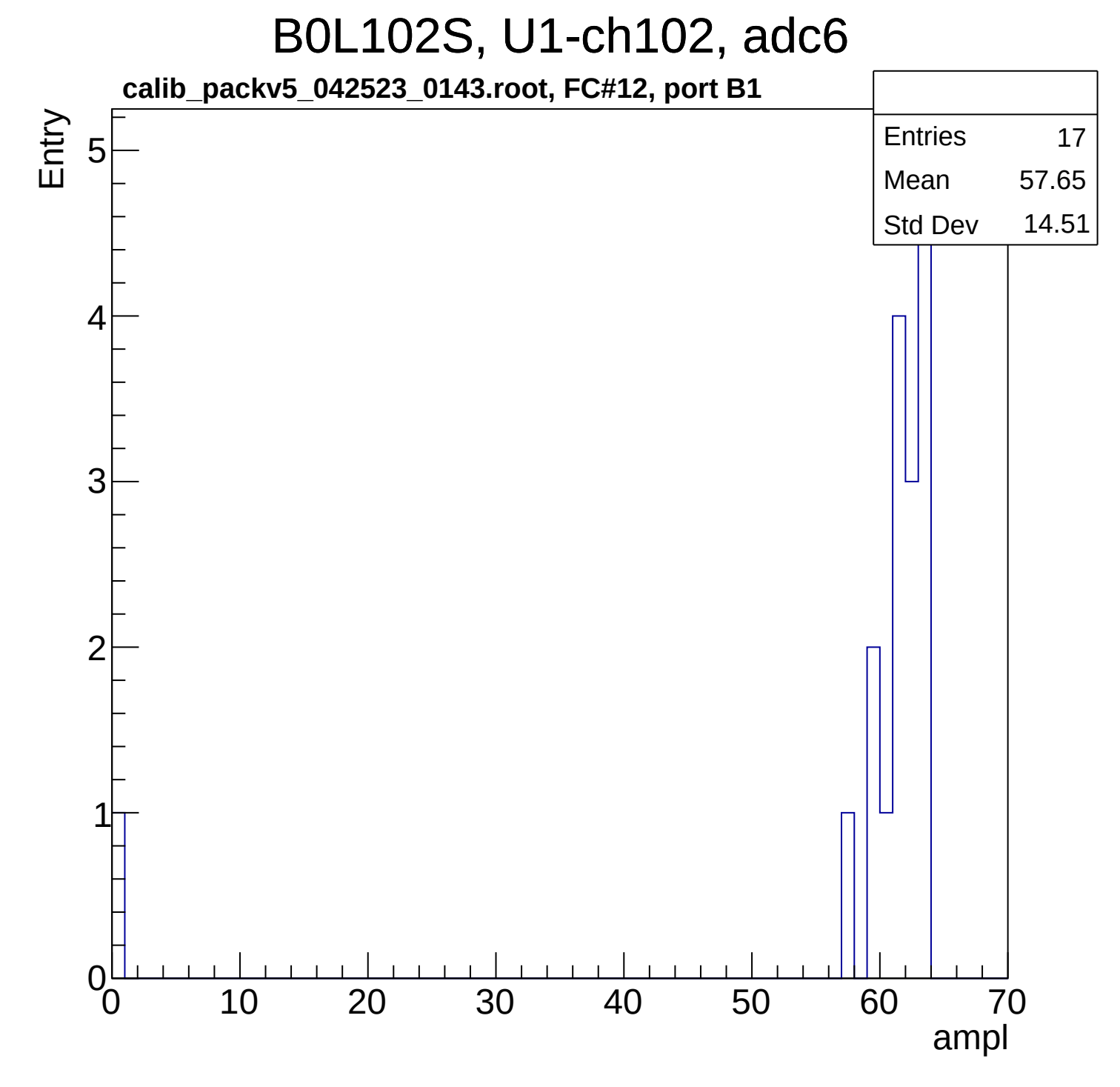

B0L102S, U1-ch102, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 34.34
Std Dev 3.924
Gaus mean : 35.4356
Gaus Width: 5.0918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

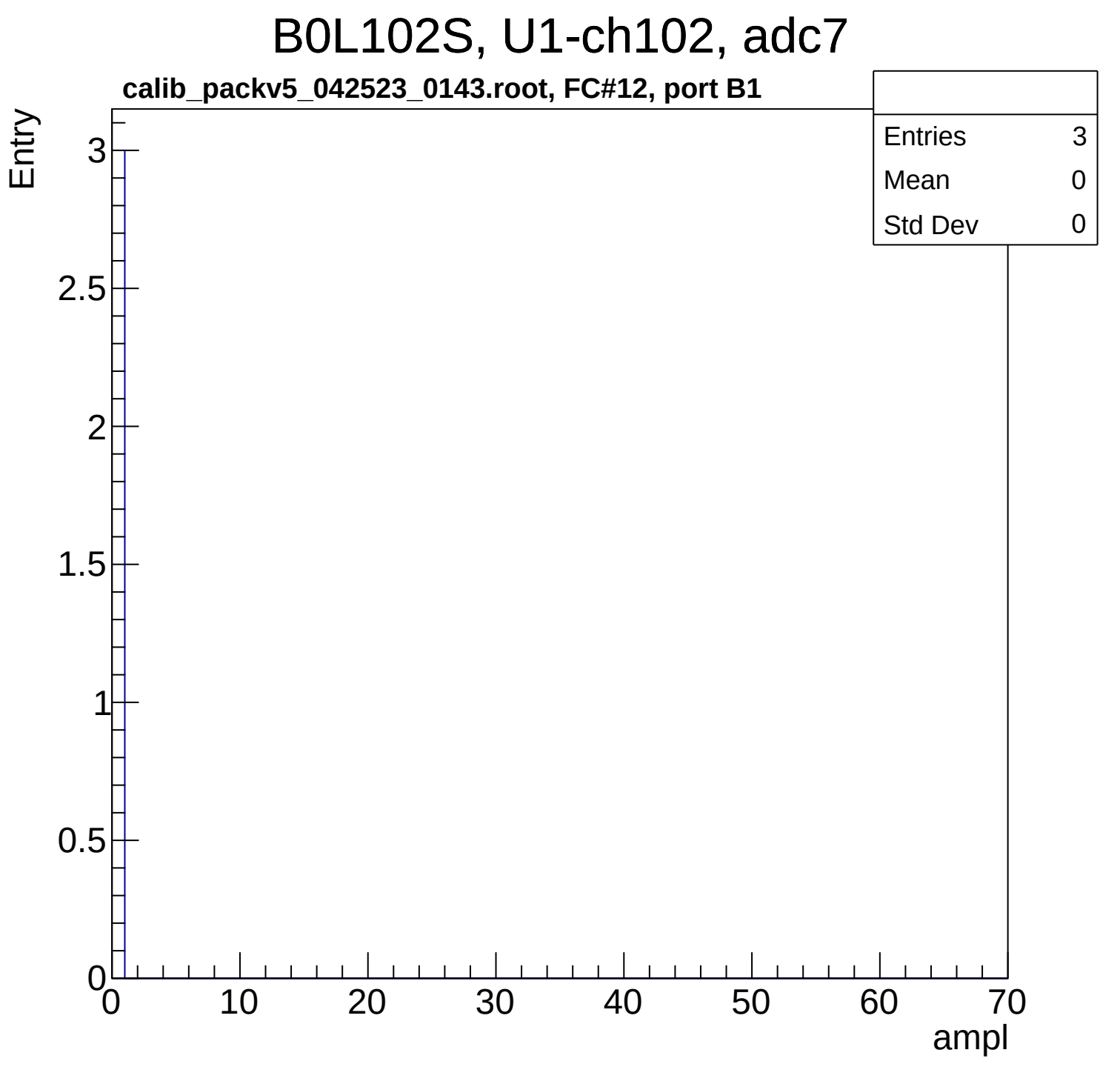

B0L102S, U1-ch102, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 40.69
Std Dev 3.5
Gaus mean : 41.4635
Gaus Width: 3.6844
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

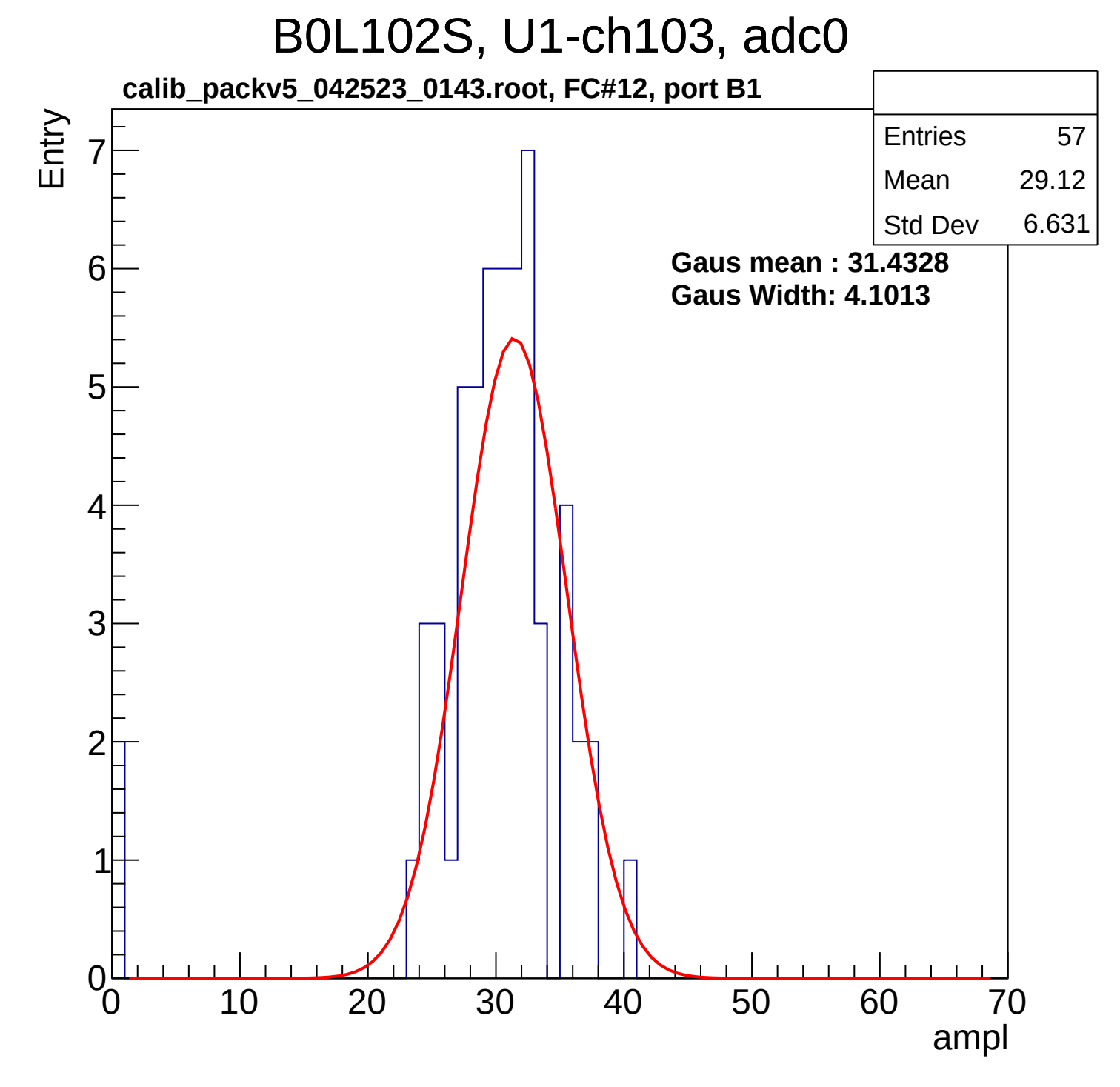

B0L102S, U1-ch102, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 47.48
Std Dev 3.649
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

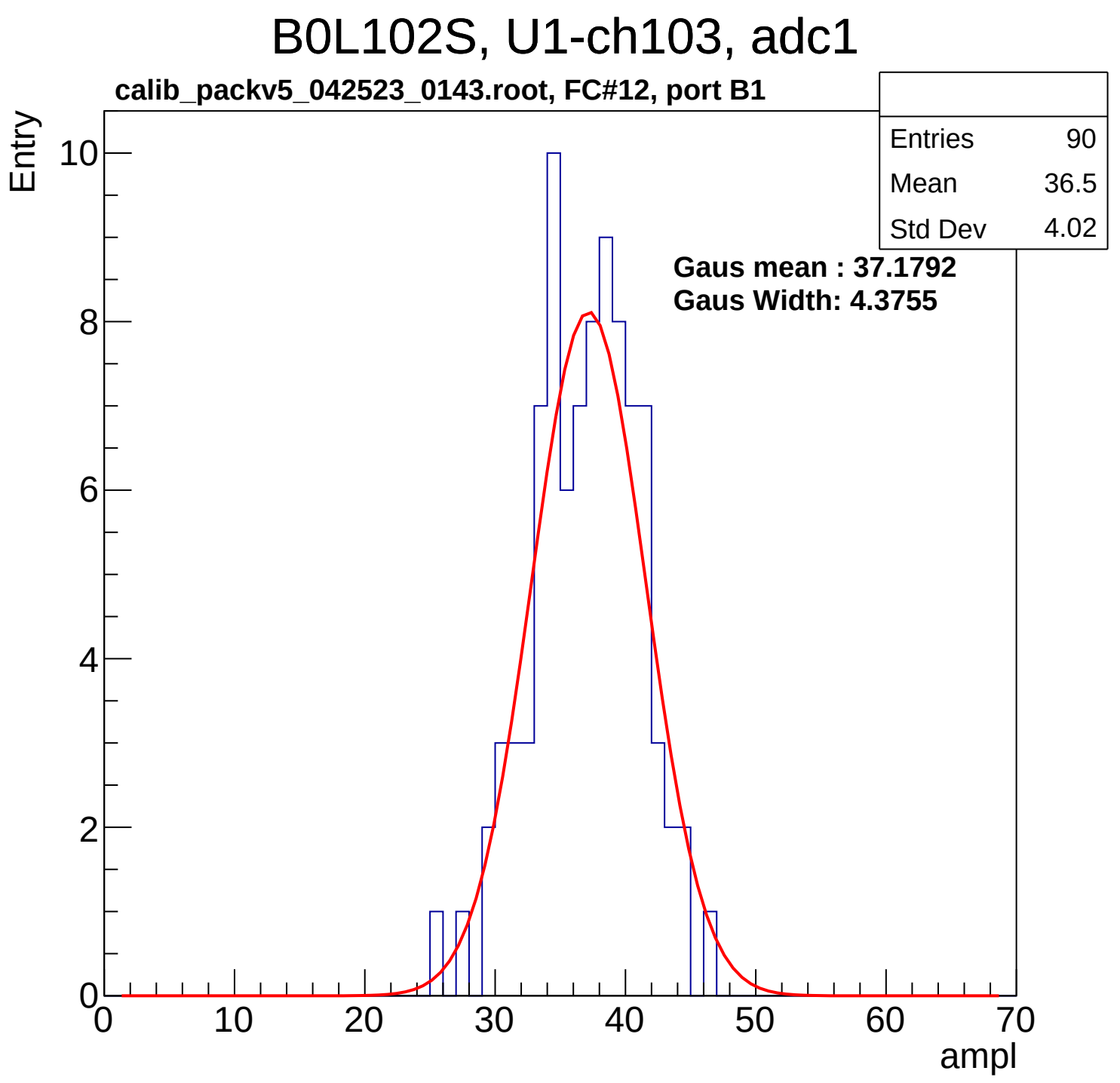

B0L102S, U1-ch102, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.07
Std Dev 3.299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

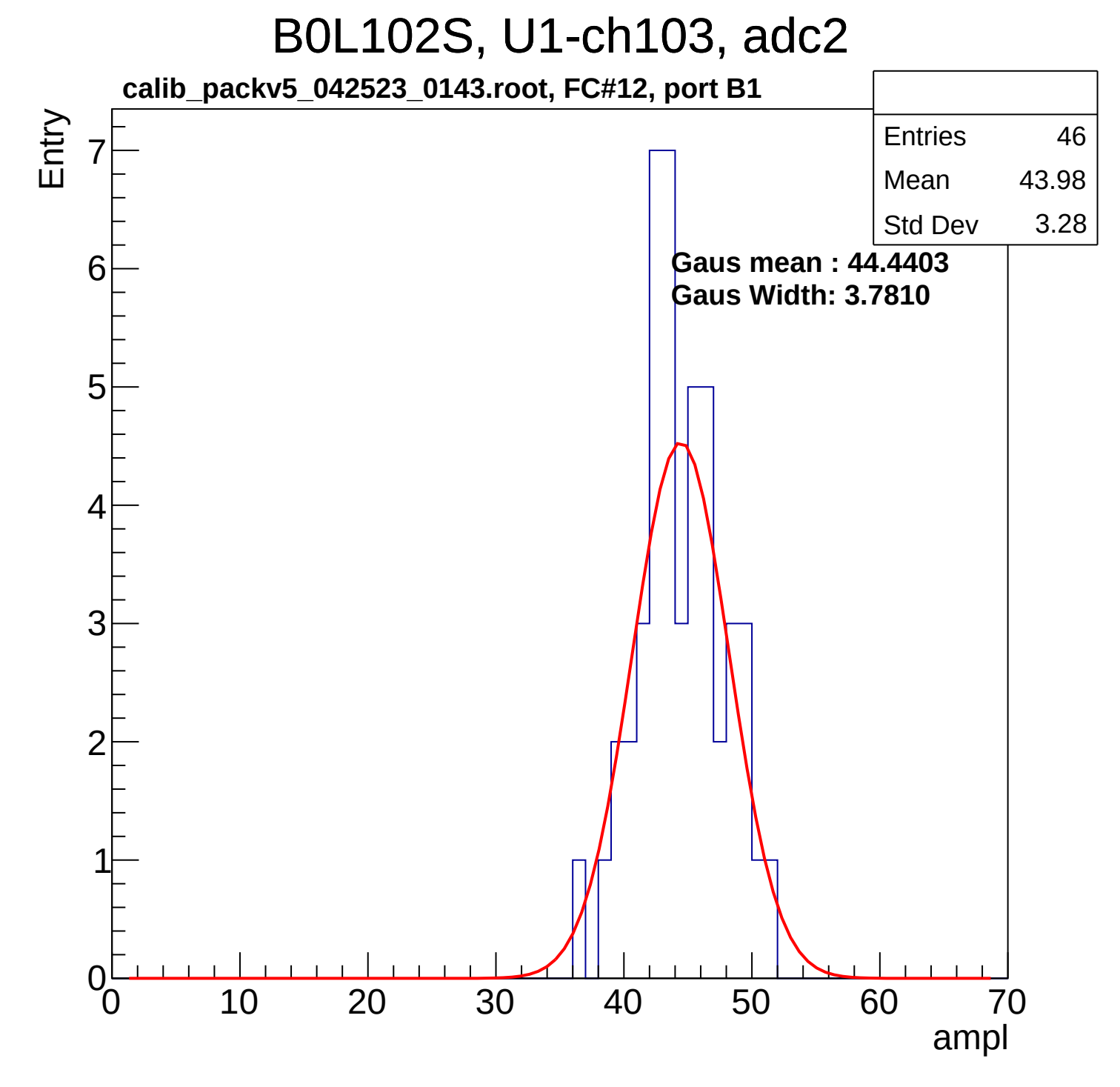

B0L102S, U1-ch102, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 58.75
Std Dev 2.977
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

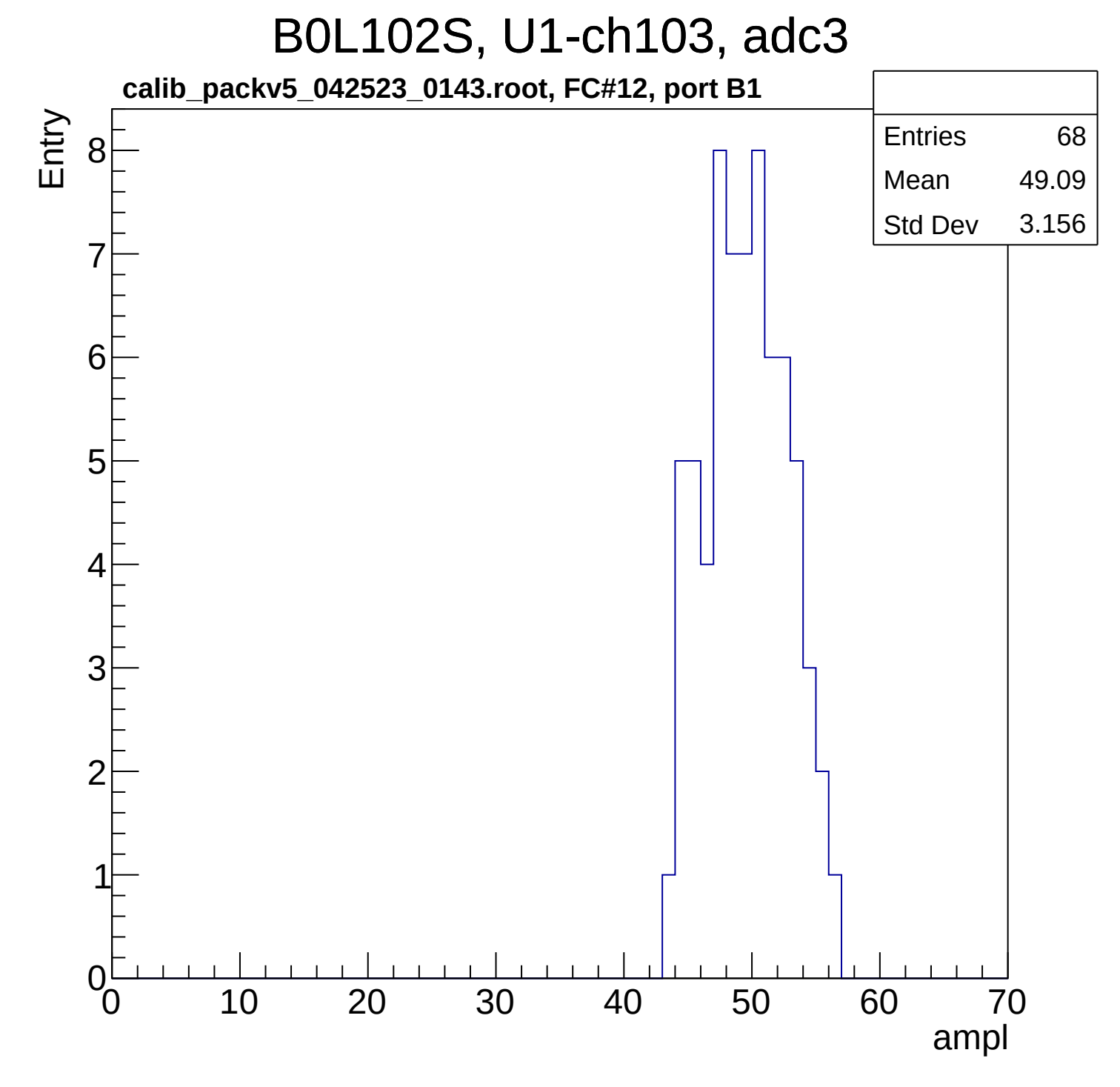

B0L102S, U1-ch102, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 57.65
Std Dev 14.51
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch102, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch103, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 29.12
Std Dev 6.631
Gaus mean : 31.4328
Gaus Width: 4.1013
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch103, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 90
Mean 36.5
Std Dev 4.02
Gaus mean : 37.1792
Gaus Width: 4.3755
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch103, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 43.98
Std Dev 3.28
Gaus mean : 44.4403
Gaus Width: 3.7810
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch103, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.09
Std Dev 3.156
Entry
ampl
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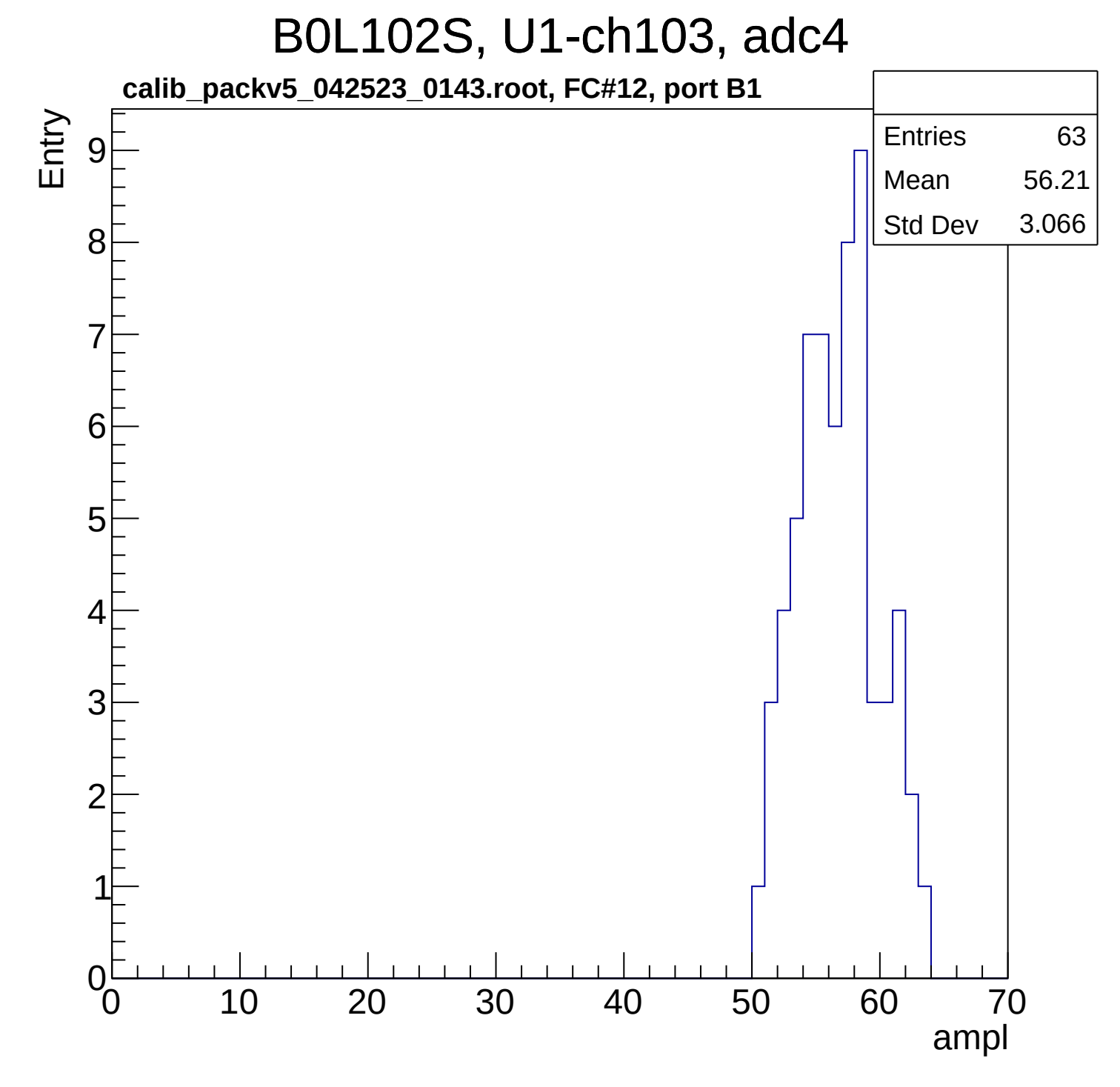
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch103, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 56.21
Std Dev 3.066
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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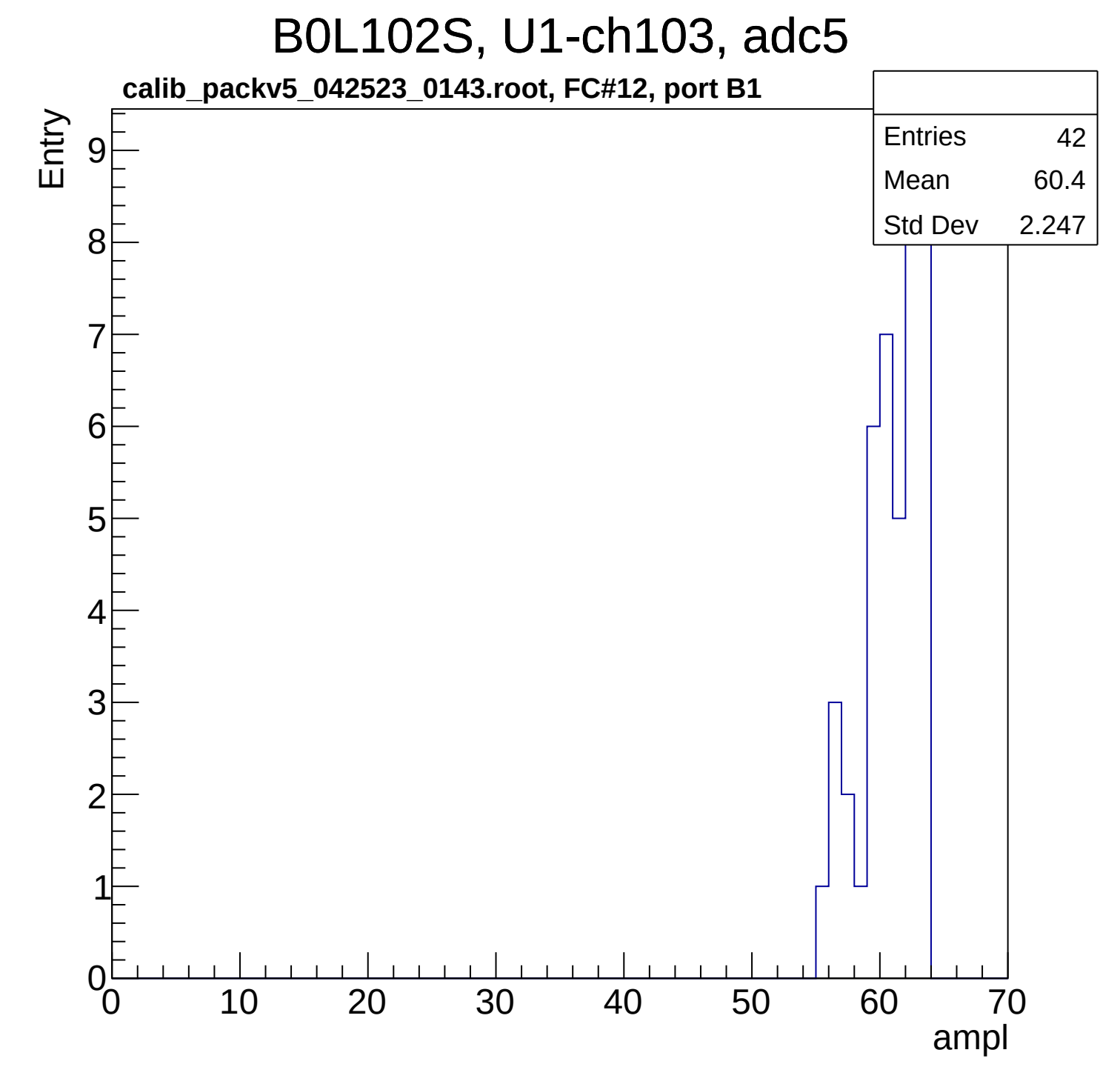
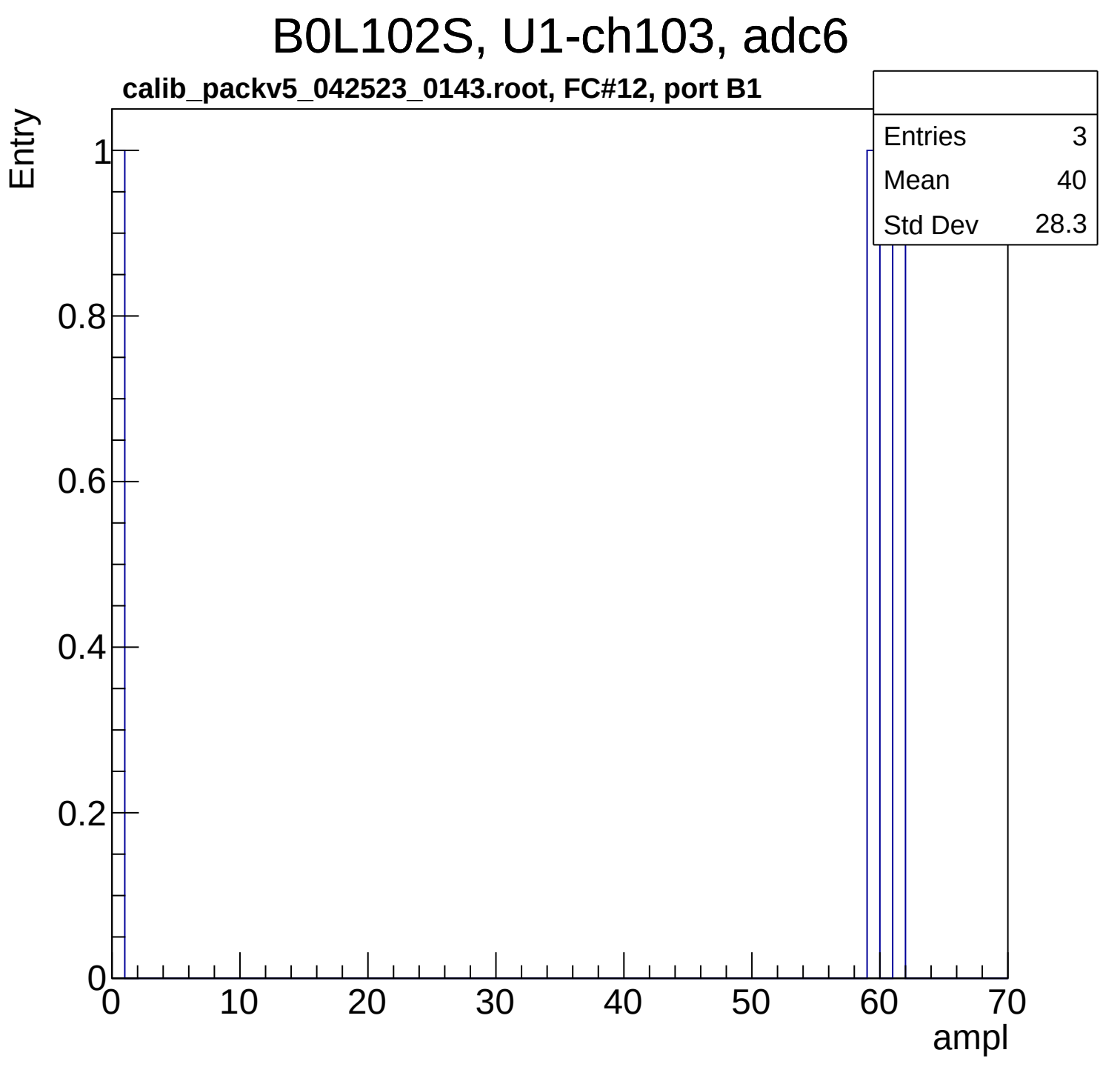
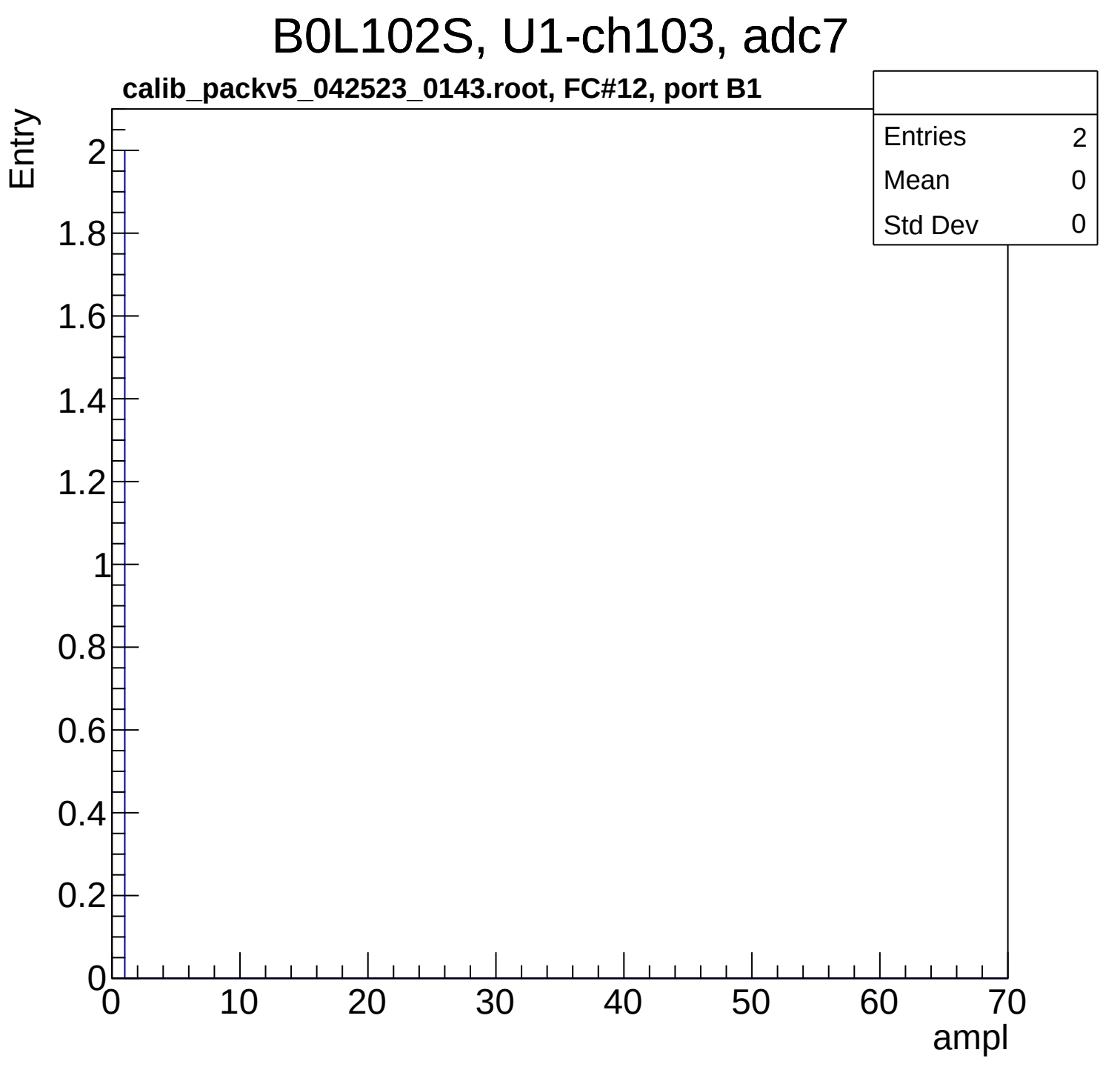
40
50
60
70


B0L102S, U1-ch103, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 60.4
Std Dev 2.247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch103, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 40
Std Dev 28.3
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch103, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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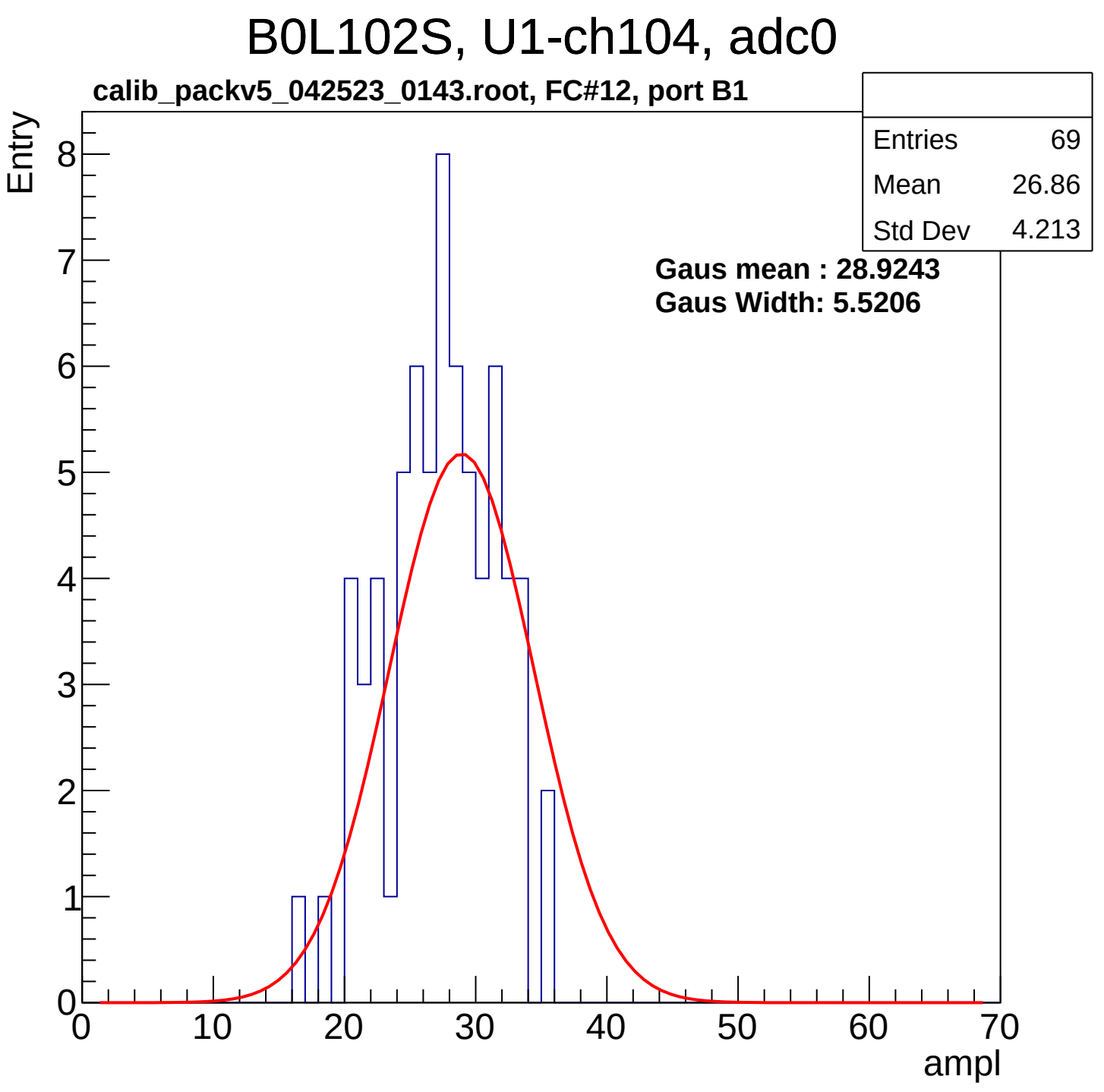
50
60
70
ampl


B0L102S, U1-ch104, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 26.86
Std Dev 4.213
Gaus mean : 28.9243
Gaus Width: 5.5206
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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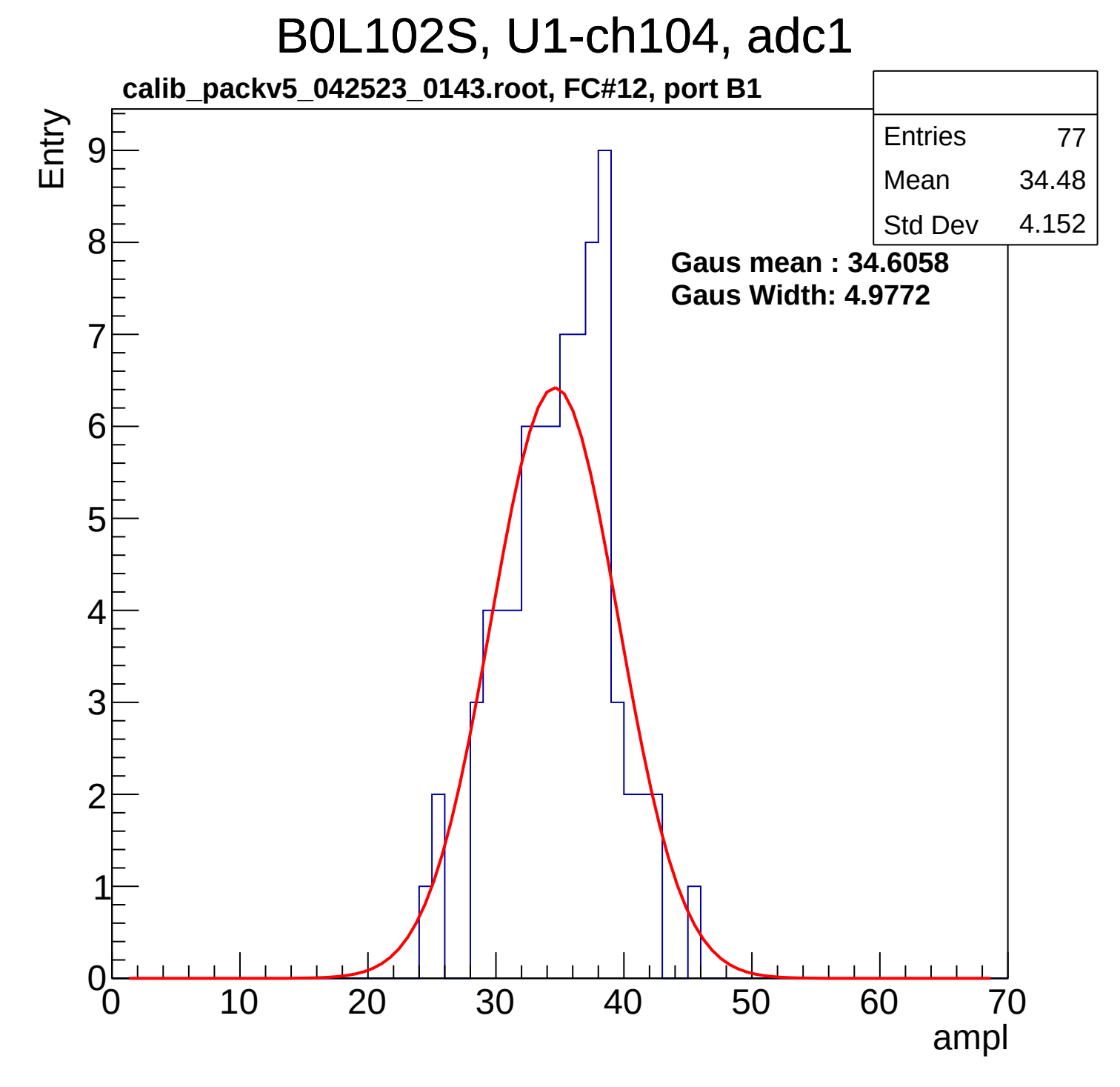
40
50
60
70


B0L102S, U1-ch104, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 34.48
Std Dev 4.152
Gaus mean : 34.6058
Gaus Width: 4.9772
Entry
ampl
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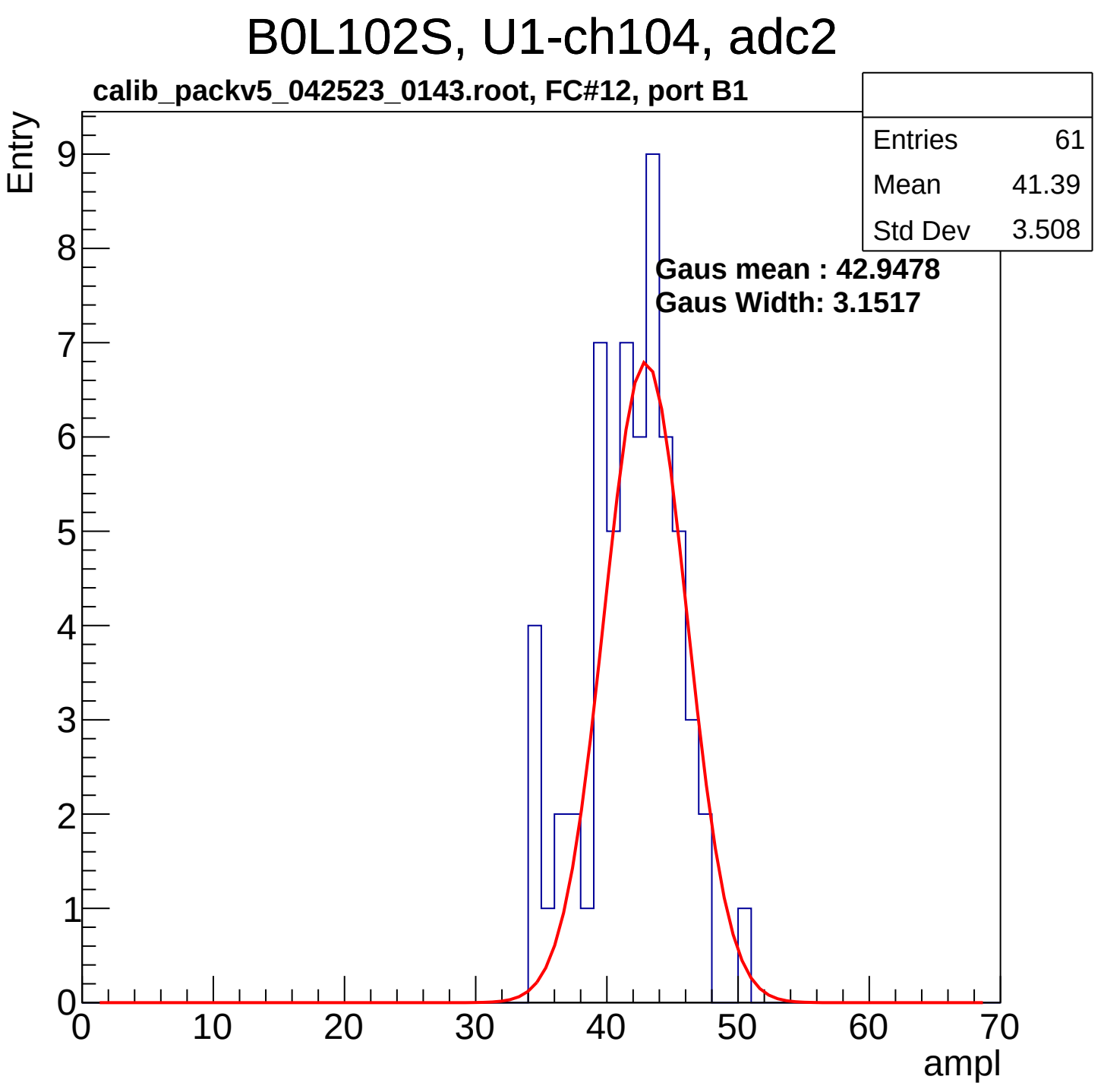
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch104, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 41.39
Std Dev 3.508
Gaus mean : 42.9478
Gaus Width: 3.1517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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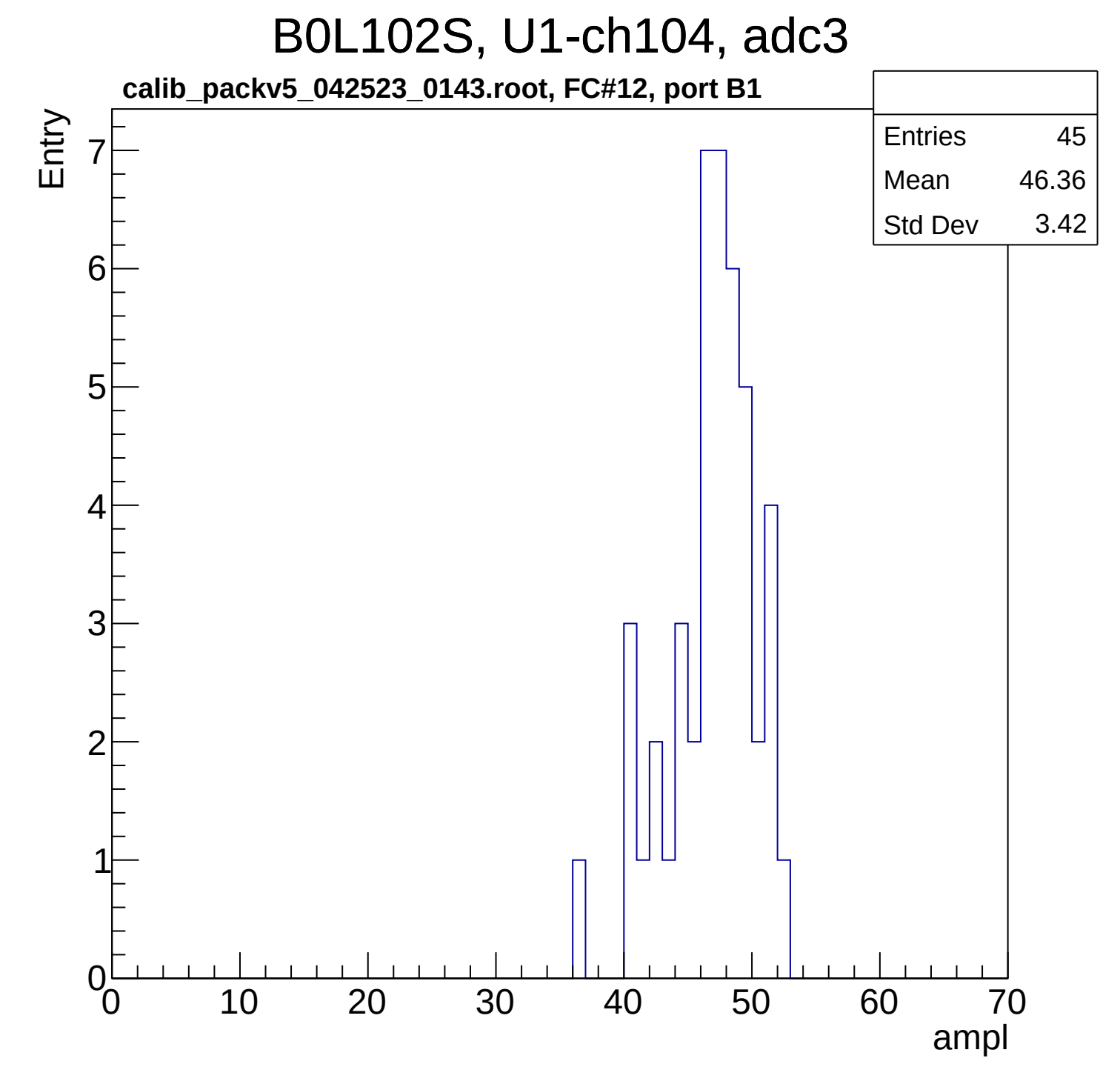
60
70


B0L102S, U1-ch104, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 46.36
Std Dev 3.42
Entry
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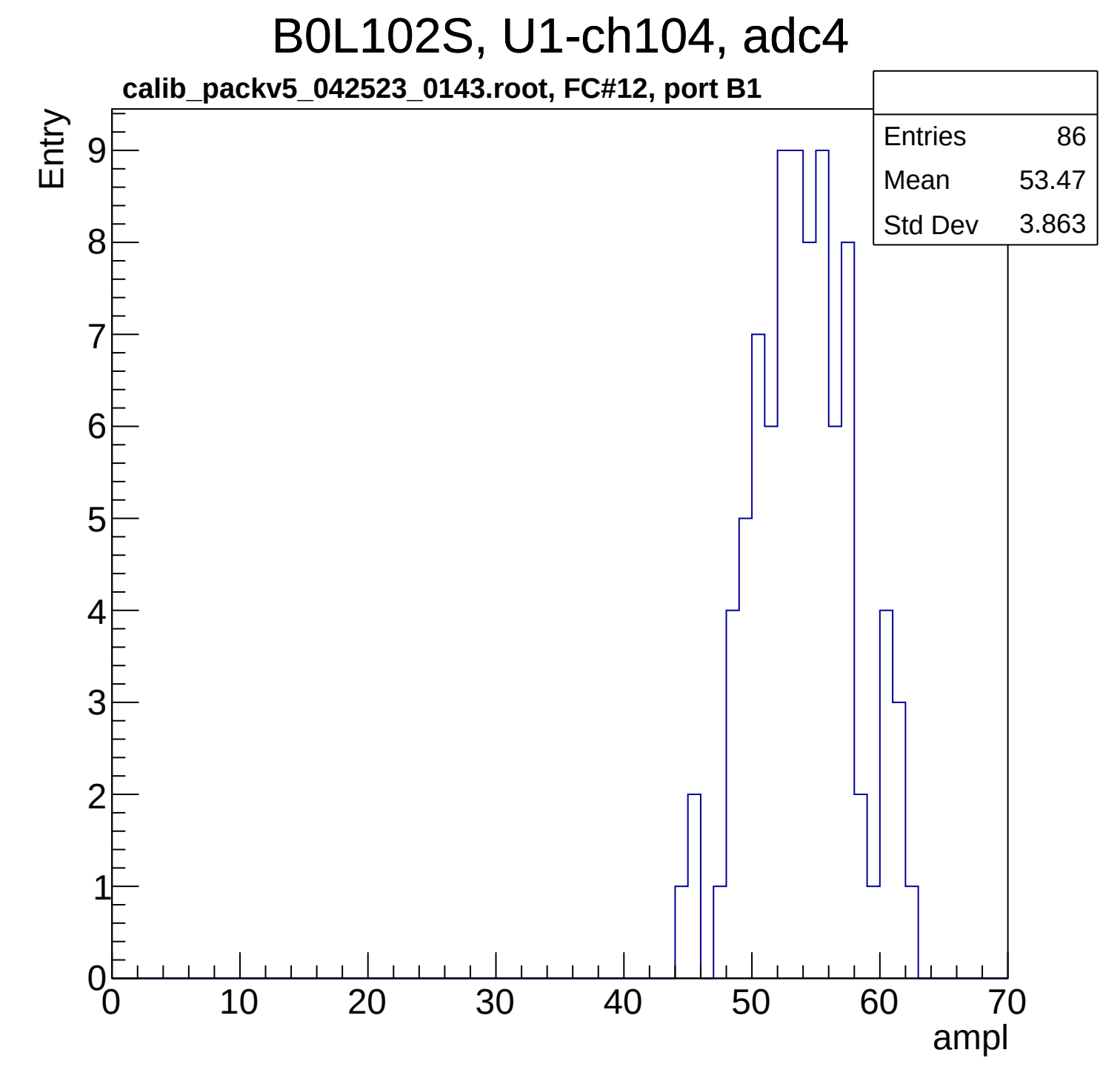
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch104, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 53.47
Std Dev 3.863
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
50
60
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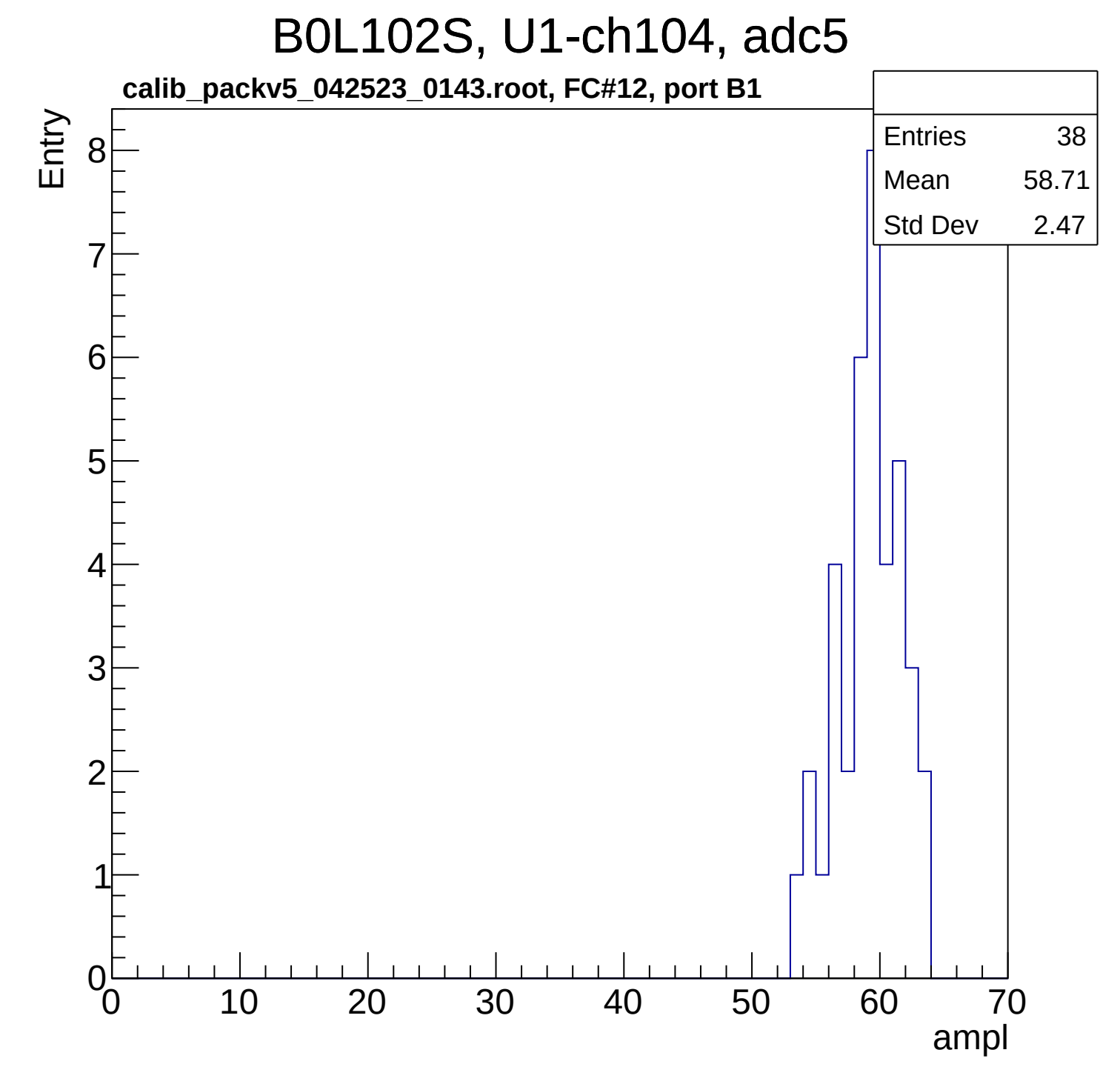
B0L102S, U1-ch104, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.71
Std Dev 2.47
Entry
ampl
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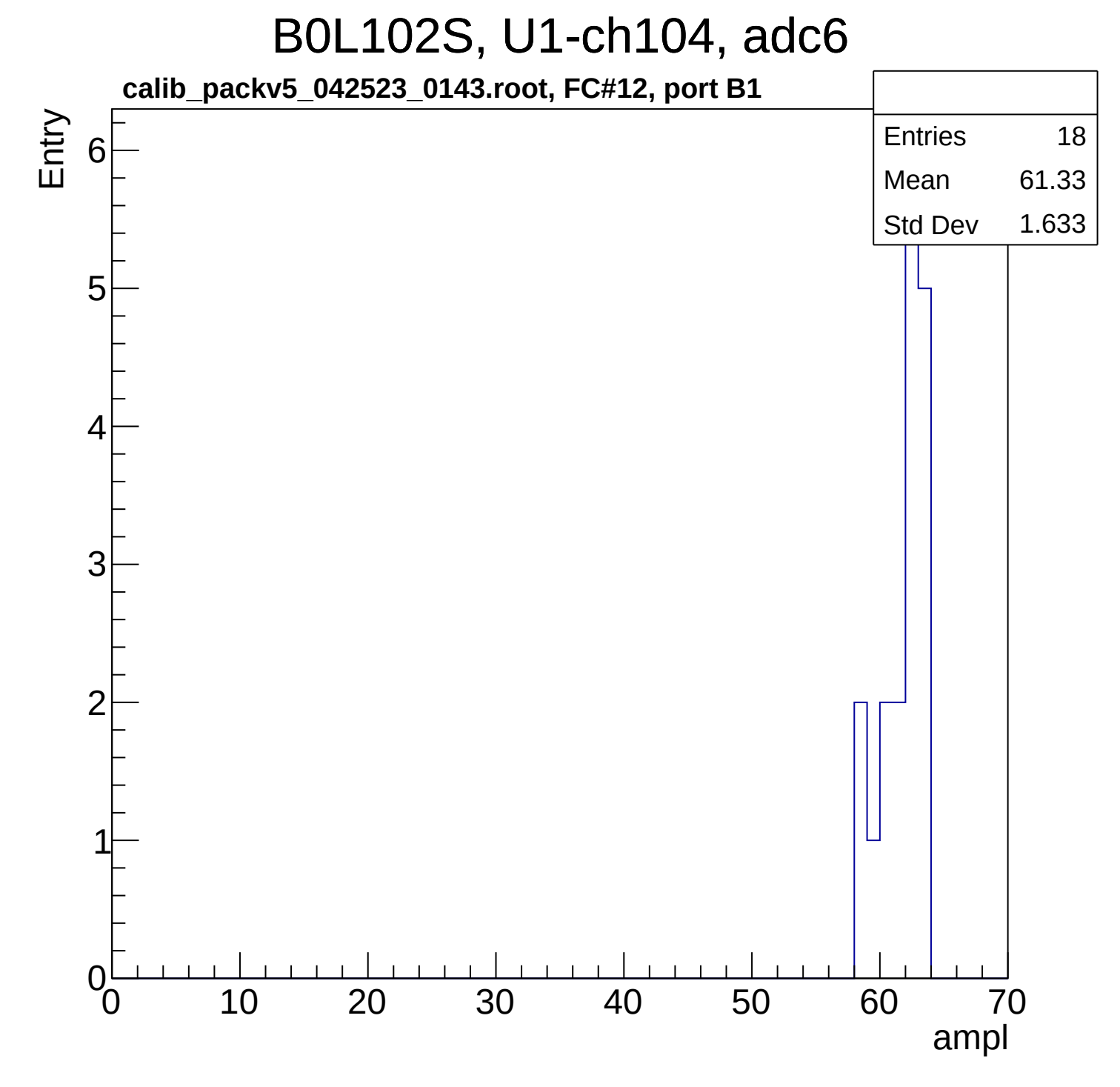
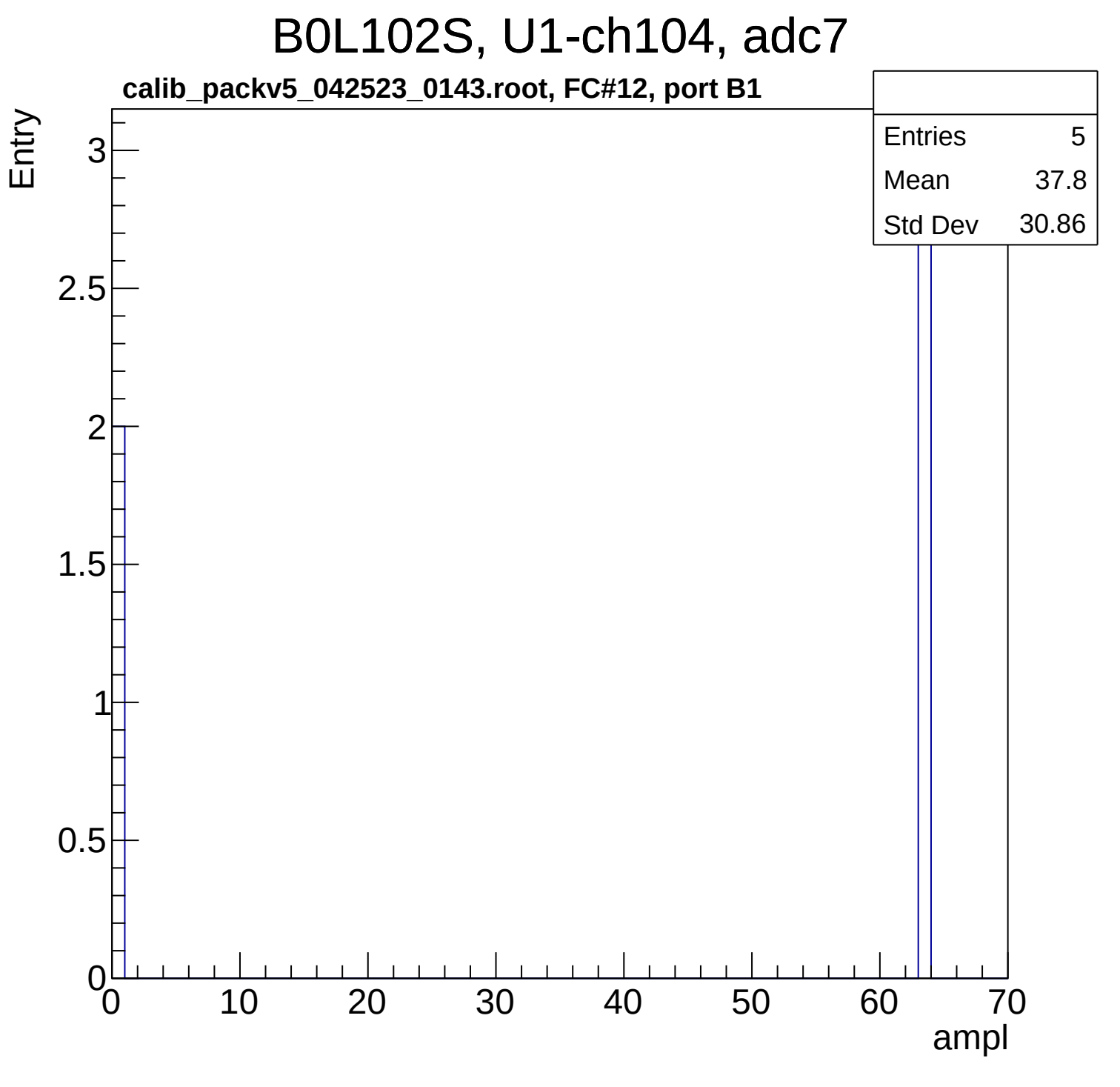
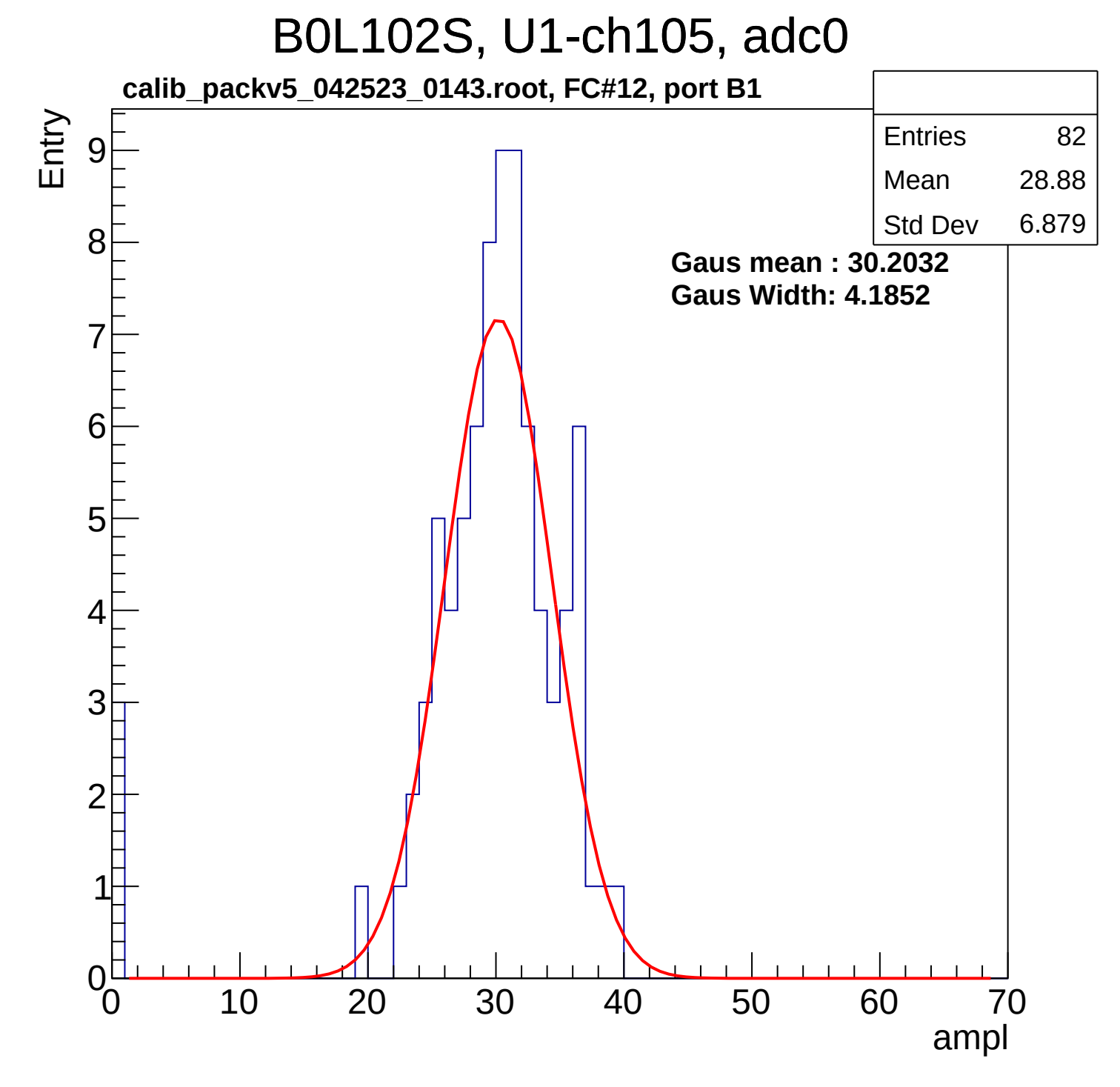
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch104, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 18
Mean 61.33
Std Dev 1.633
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch104, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 37.8
Std Dev 30.86
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch105, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 28.88
Std Dev 6.879
Gaus mean : 30.2032
Gaus Width: 4.1852
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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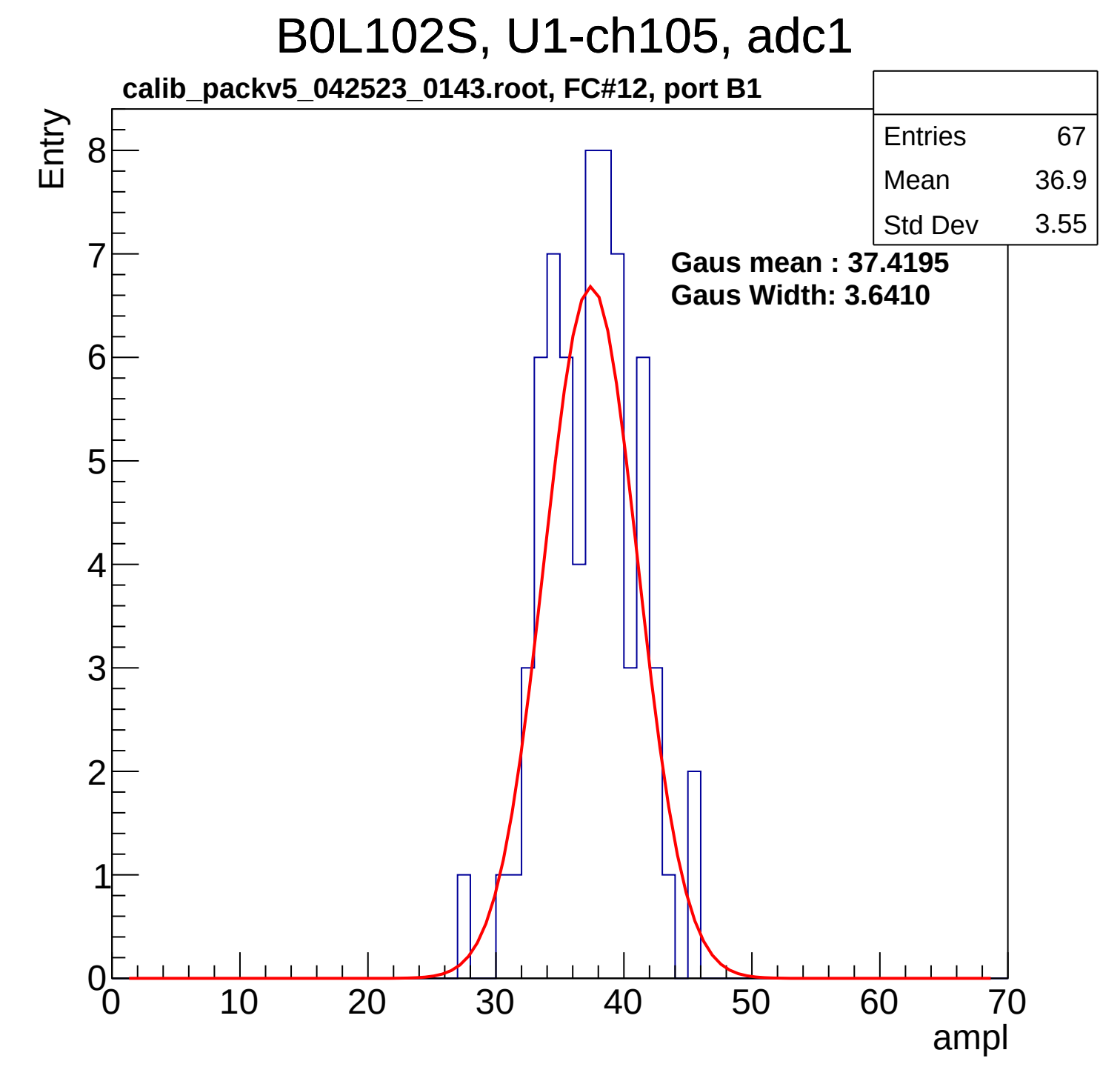
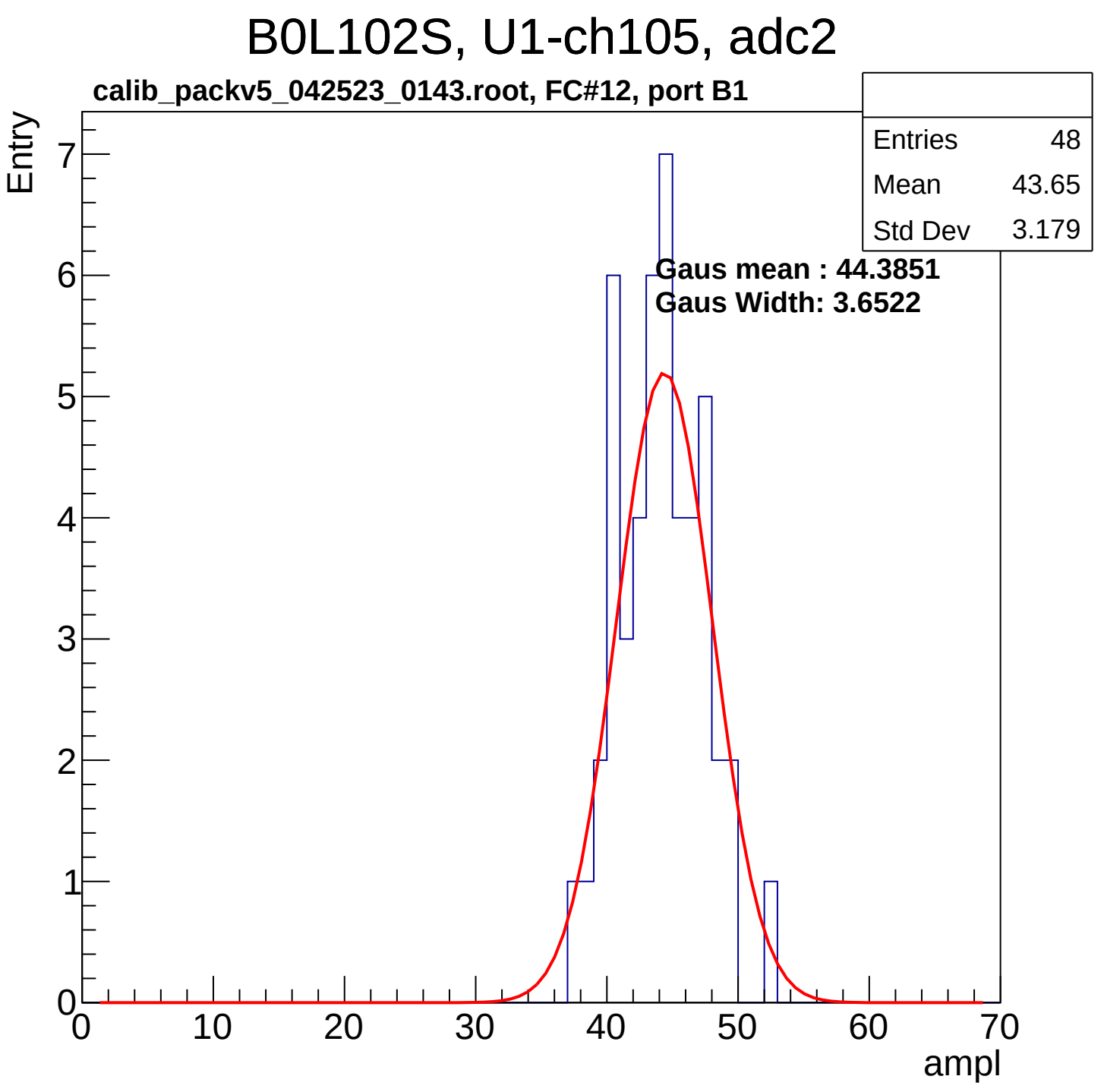
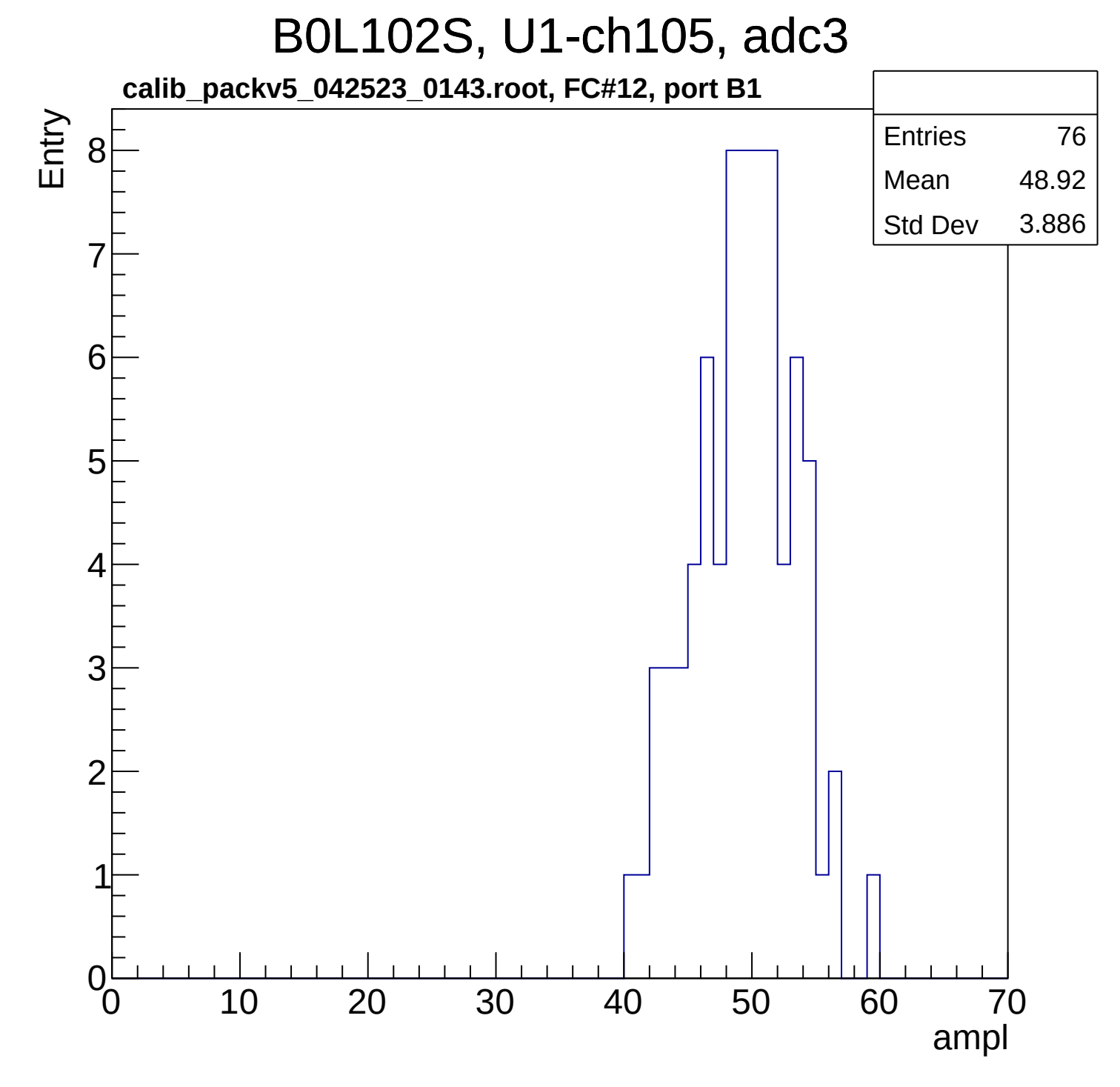

B0L102S, U1-ch105, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 36.9
Std Dev 3.55
Gaus mean : 37.4195
Gaus Width: 3.6410
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch105, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 43.65
Std Dev 3.179
Gaus mean : 44.3851
Gaus Width: 3.6522
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch105, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 48.92
Std Dev 3.886
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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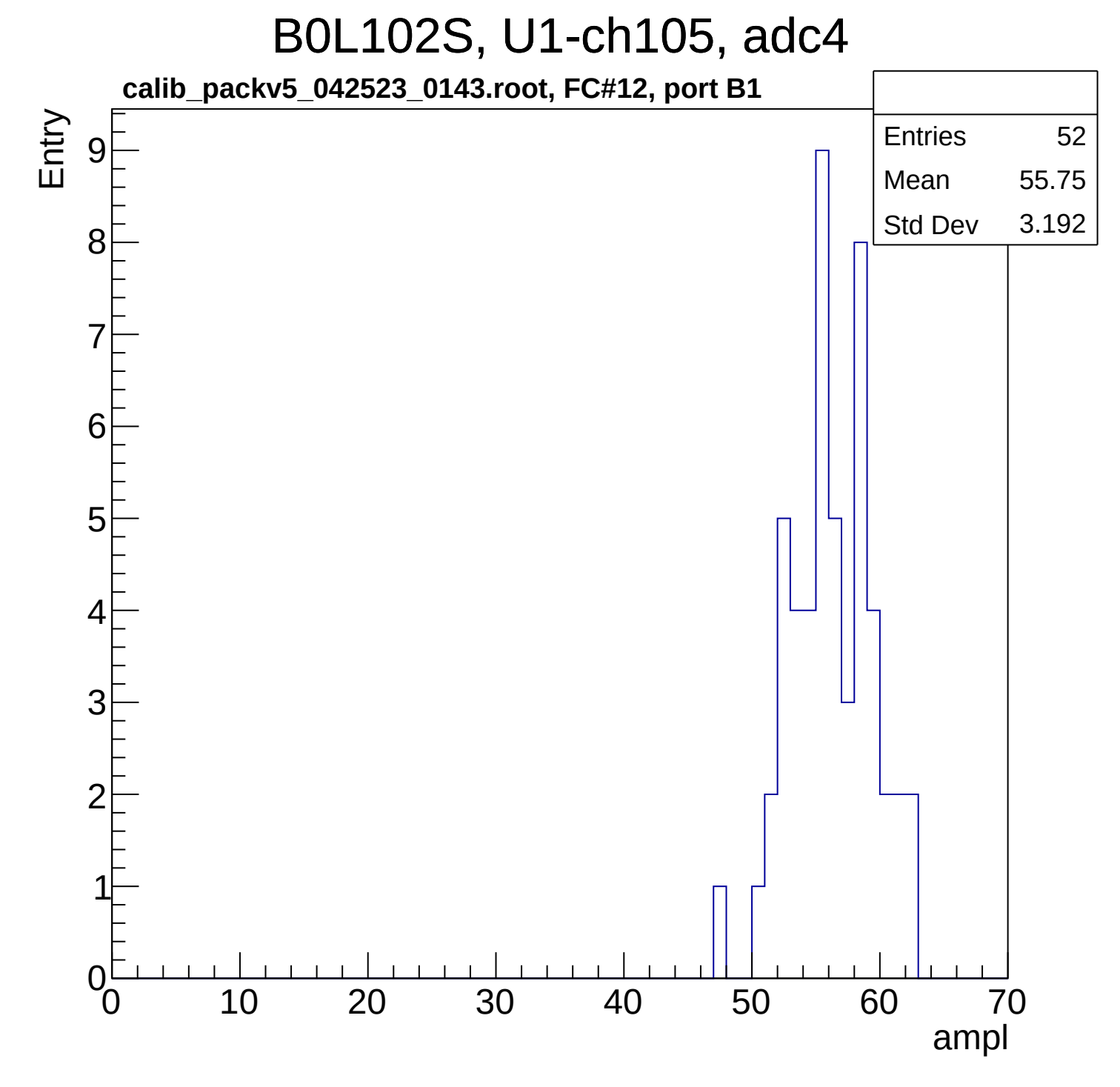
60
70


B0L102S, U1-ch105, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 55.75
Std Dev 3.192
Entry
ampl
0
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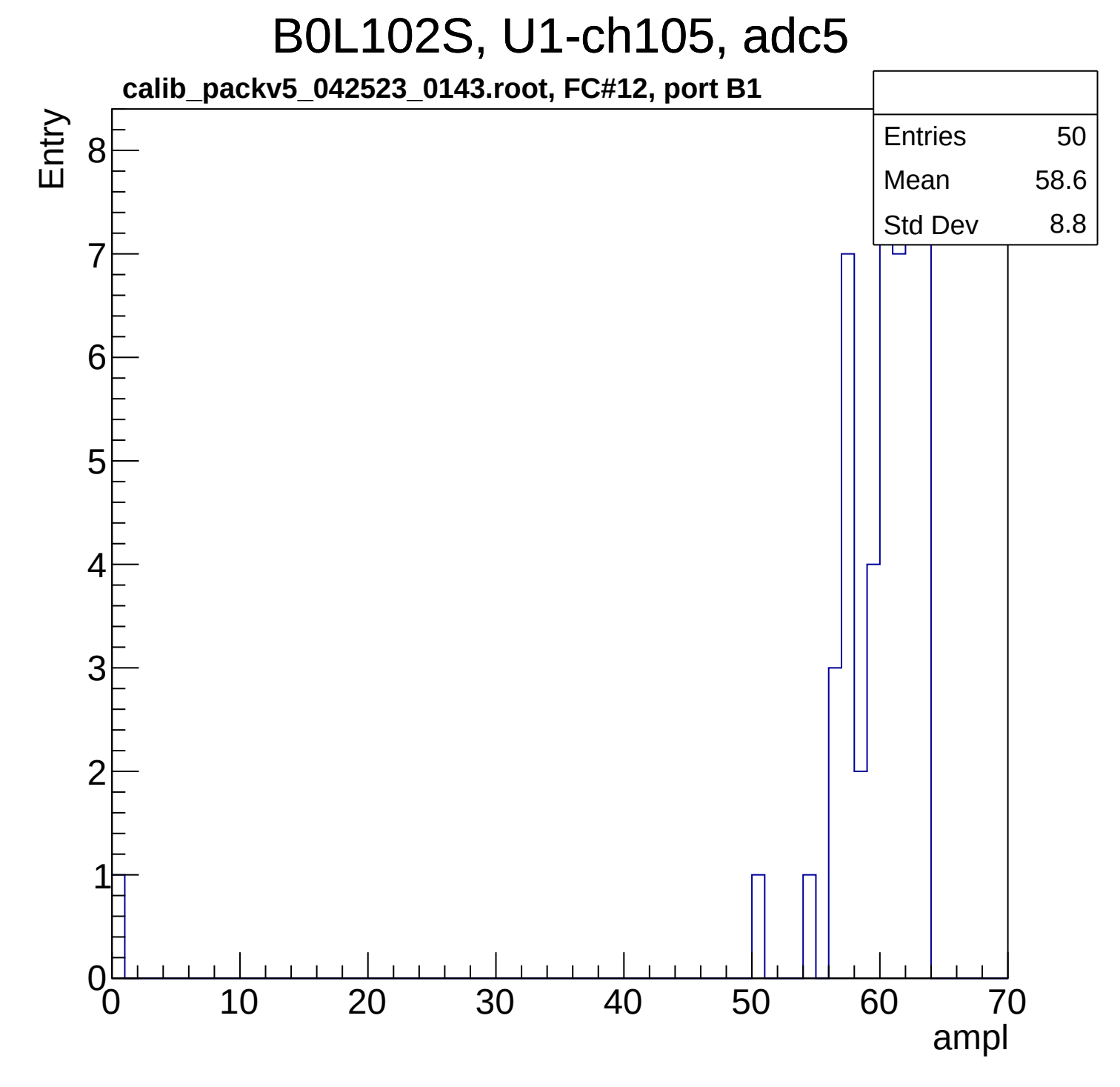
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch105, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.6
Std Dev 8.8
Entry
ampl
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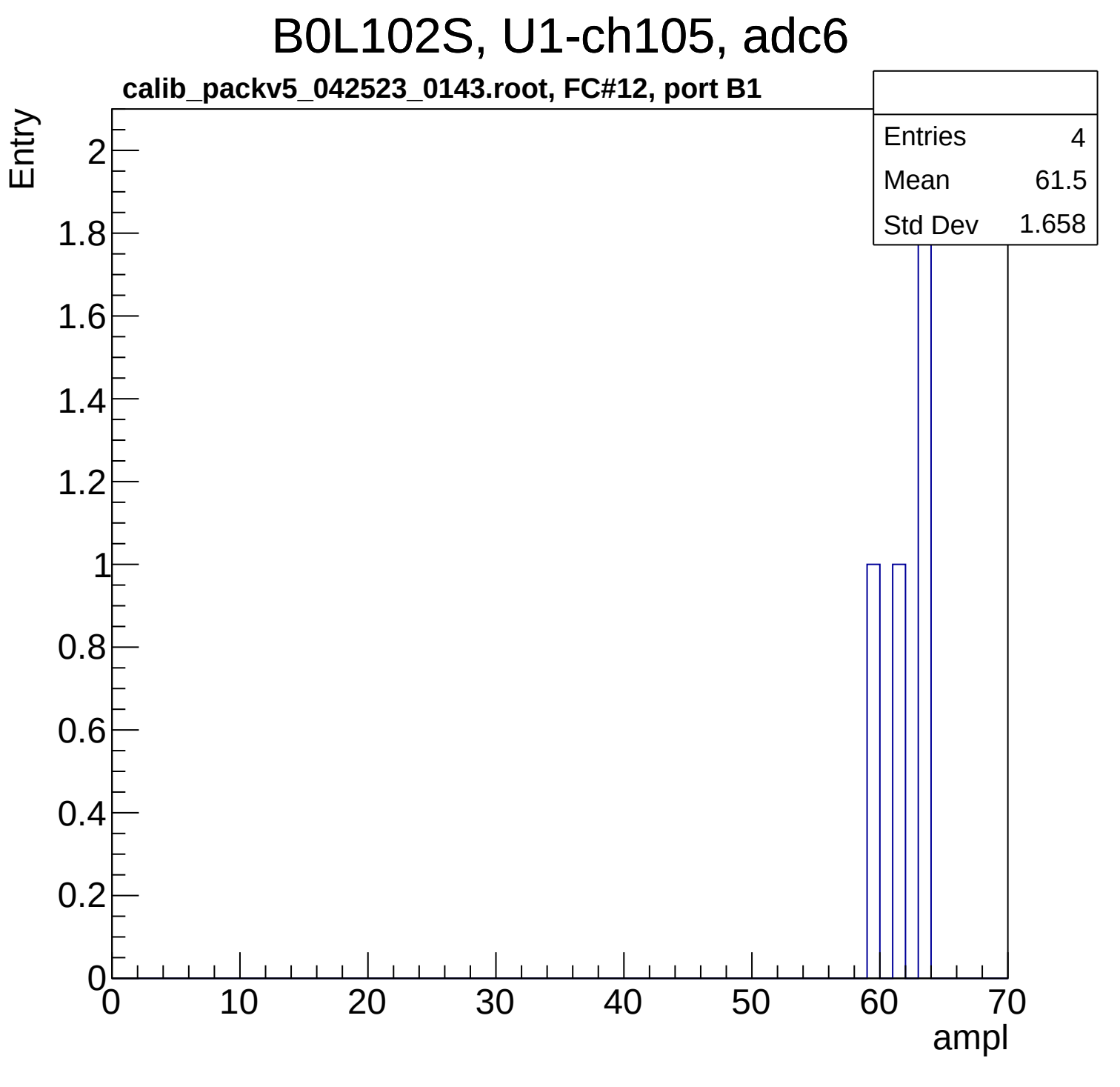
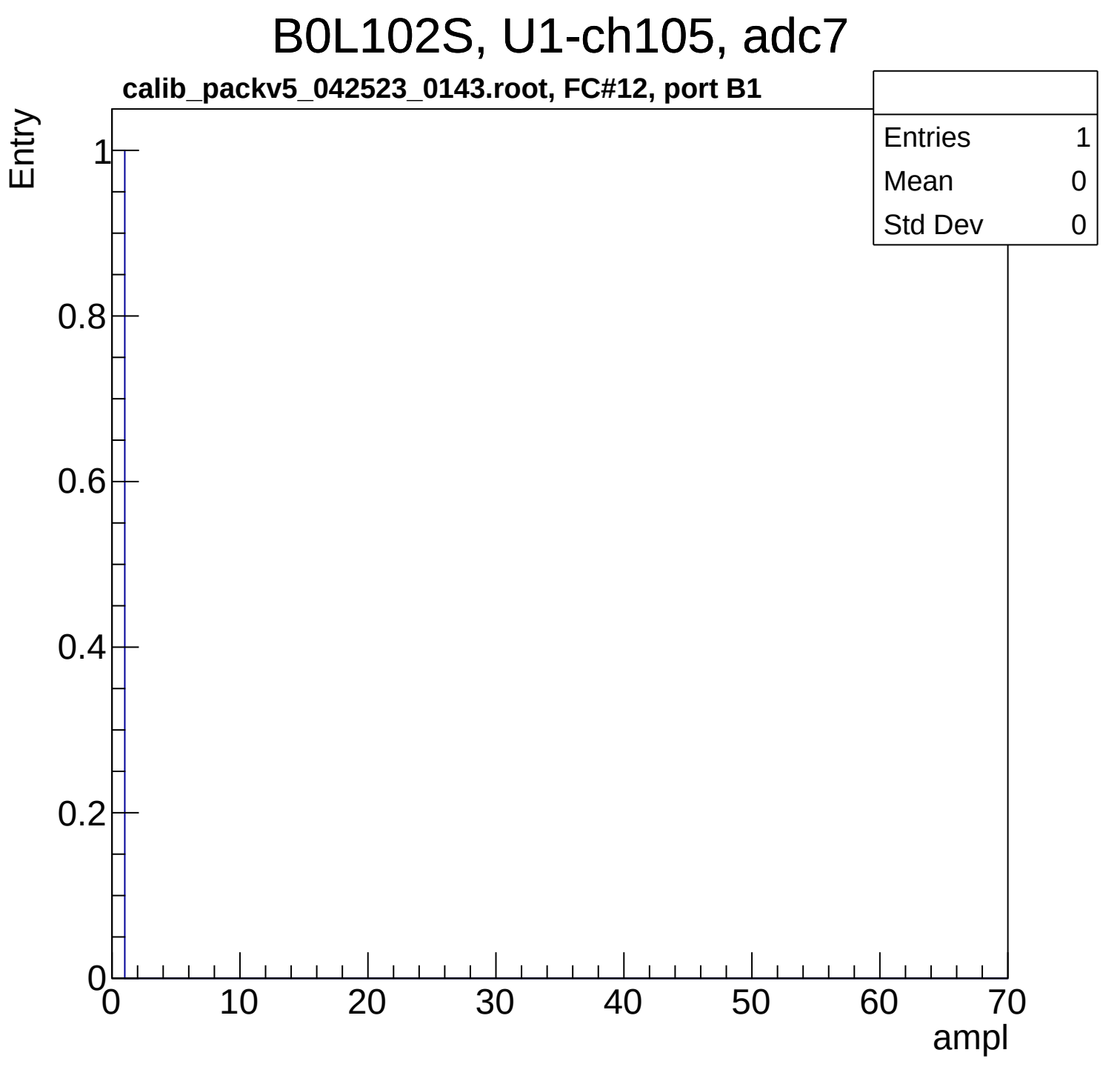
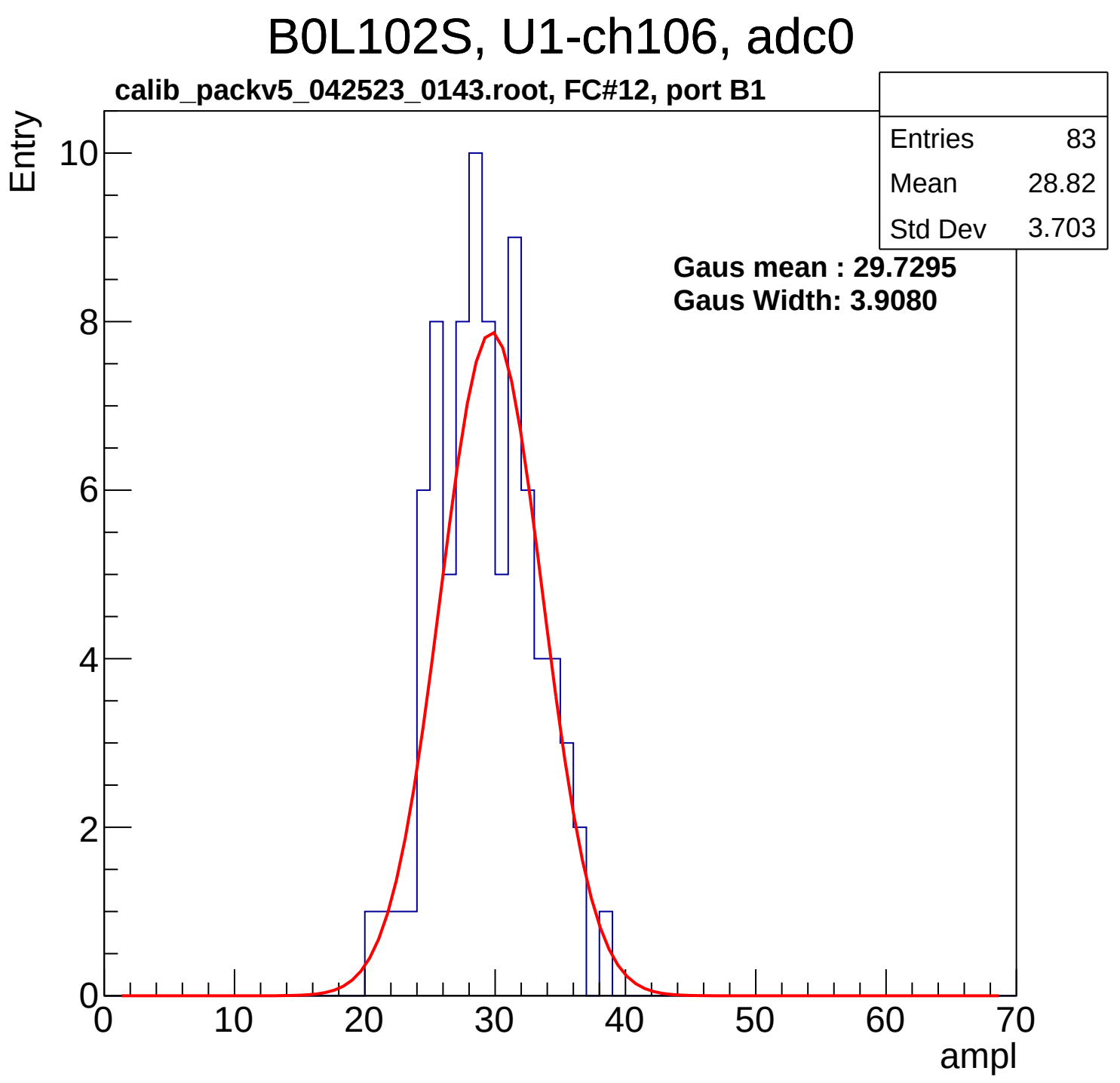
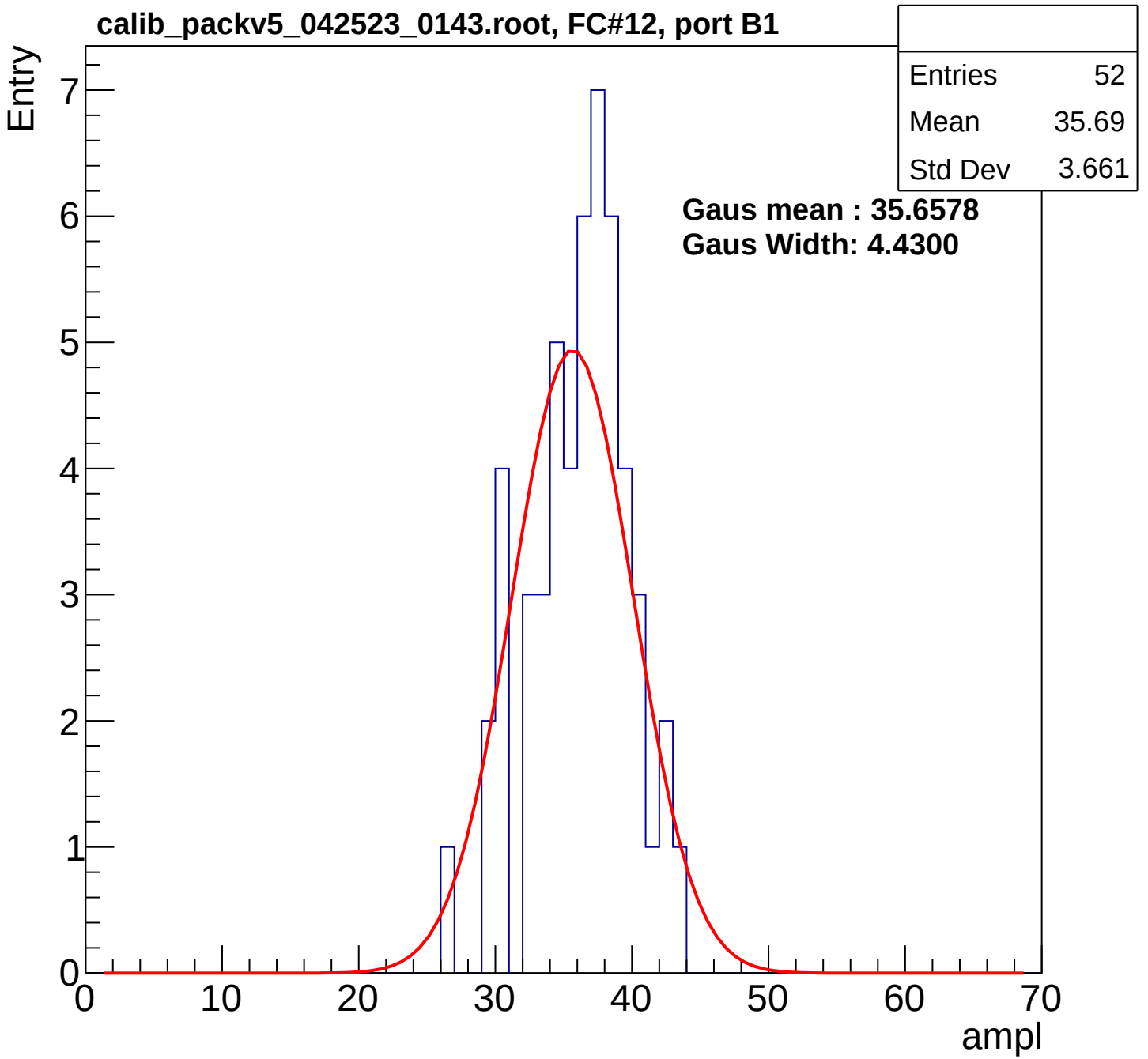
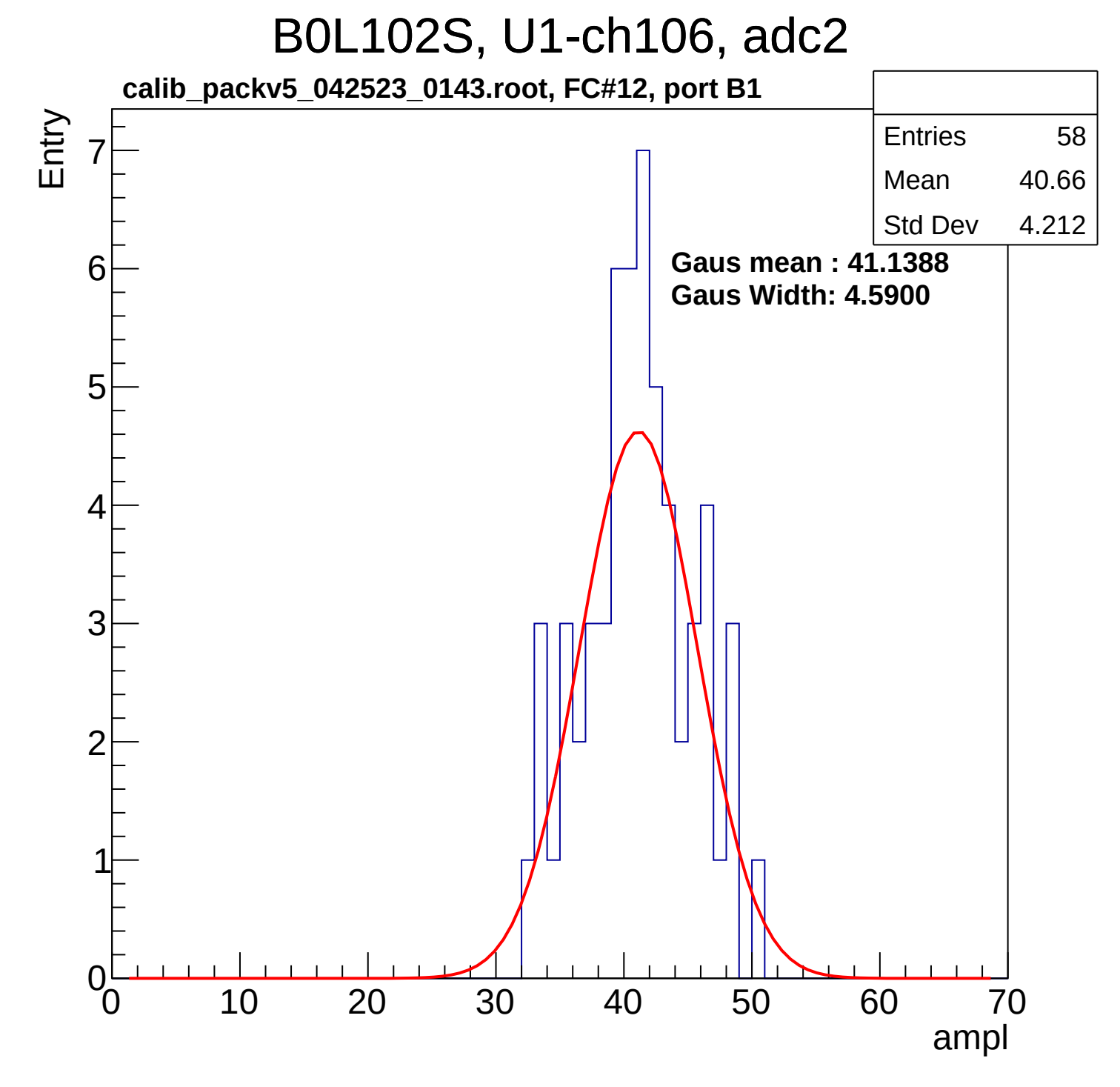
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

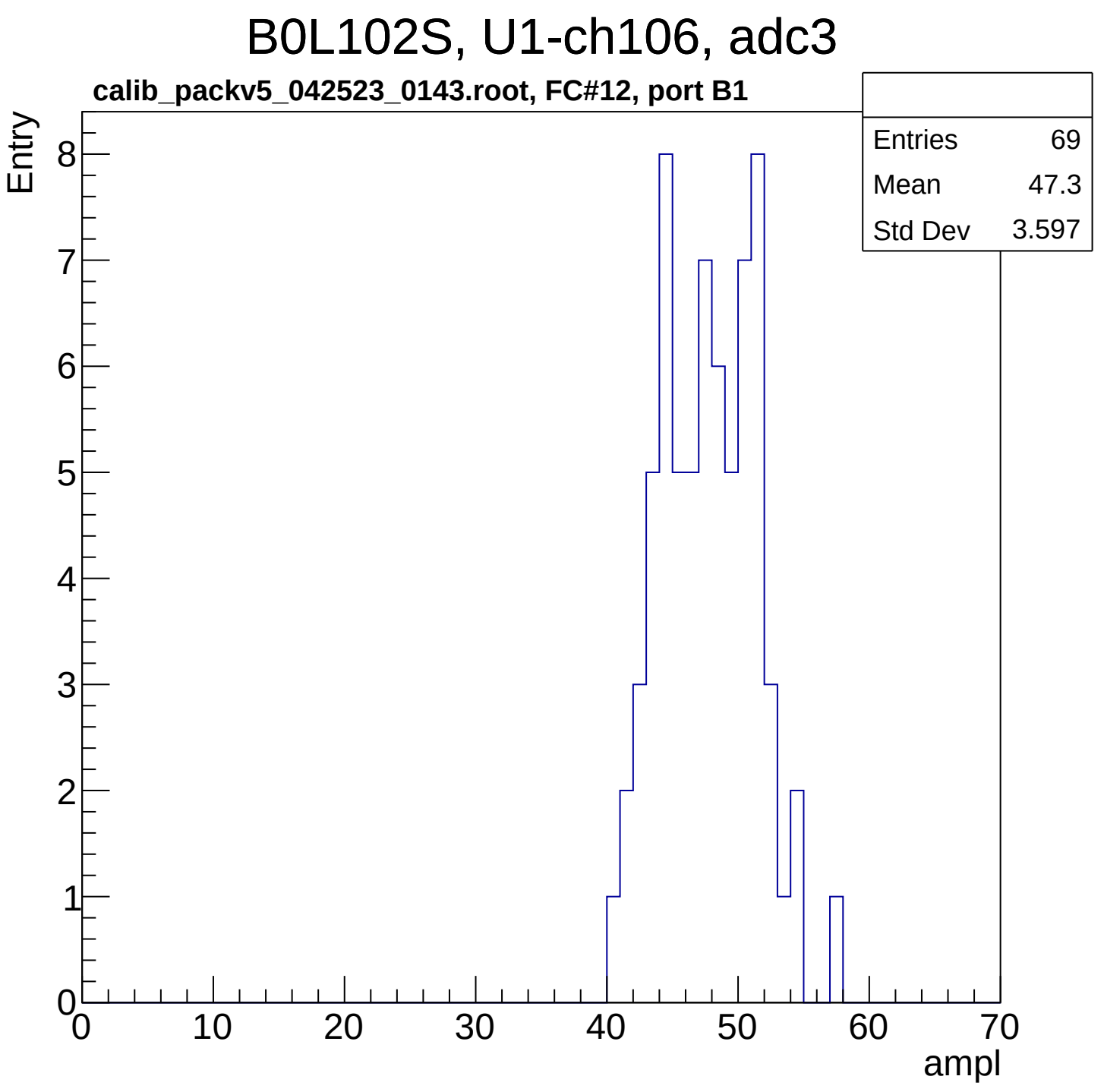

B0L102S, U1-ch105, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

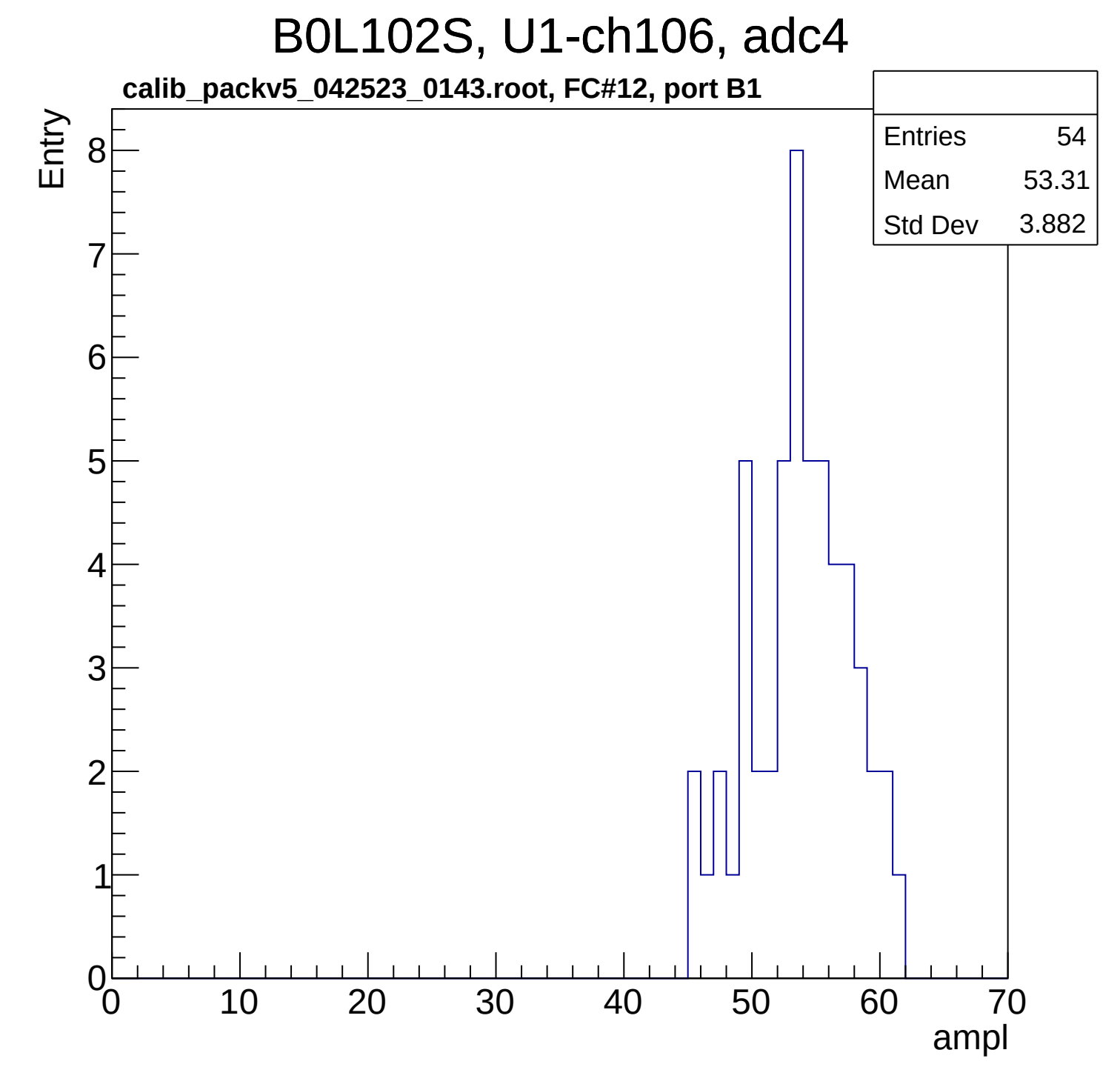

B0L102S, U1-ch105, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

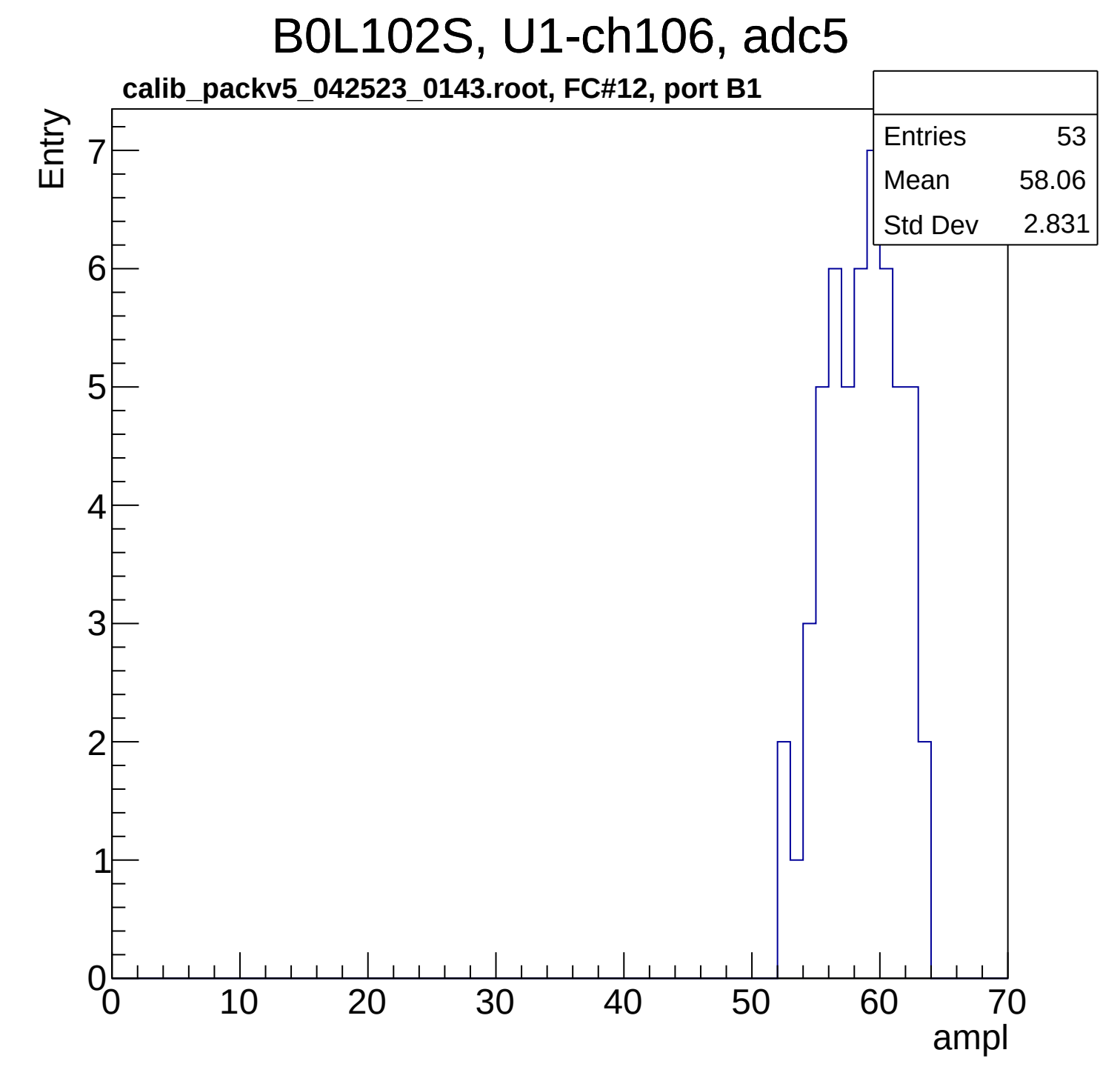

B0L102S, U1-ch106, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 28.82
Std Dev 3.703
Gaus mean : 29.7295
Gaus Width: 3.9080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

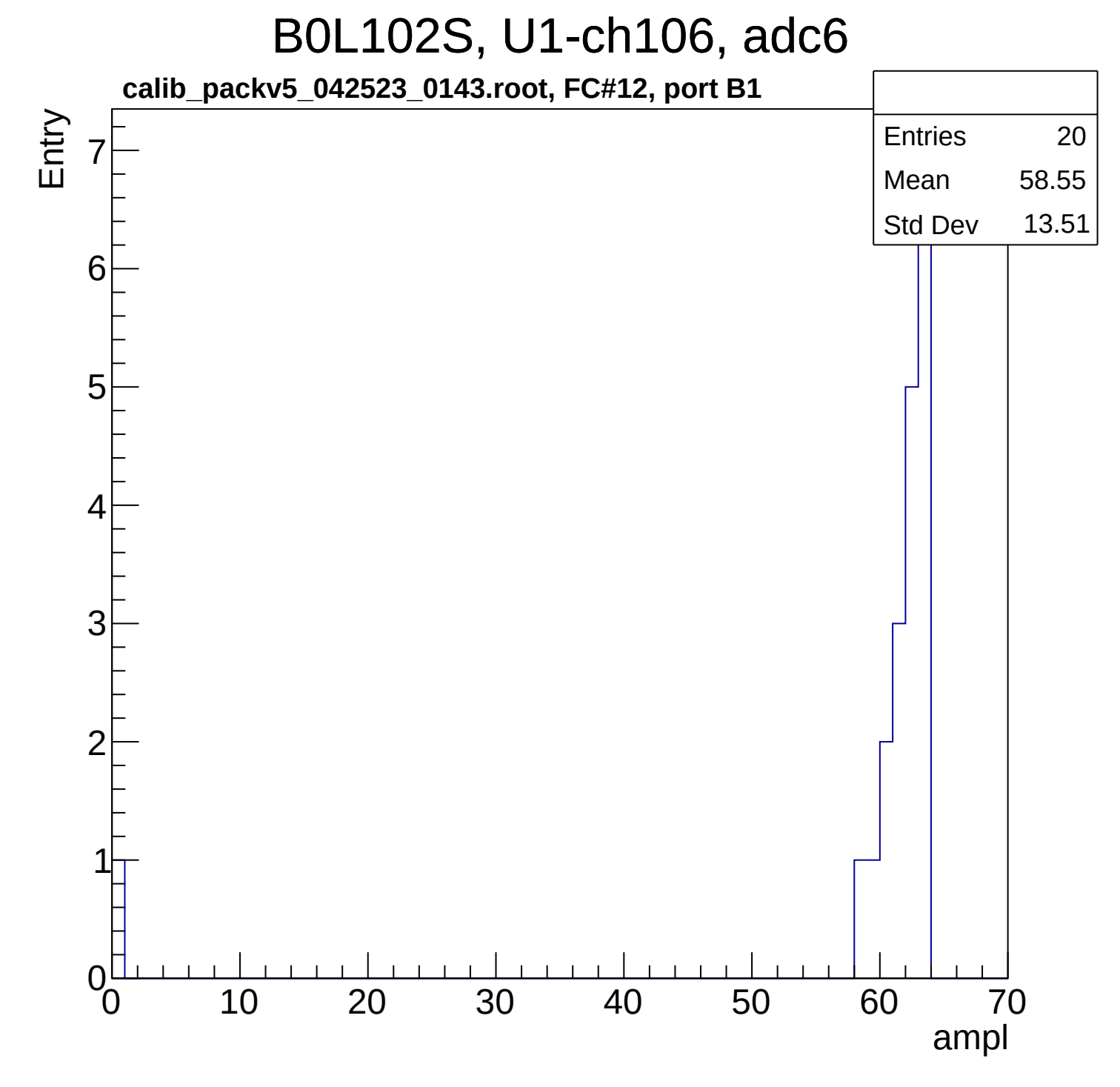

B0L102S, U1-ch106, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 35.69
Std Dev 3.661
Gaus mean : 35.6578
Gaus Width: 4.4300
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

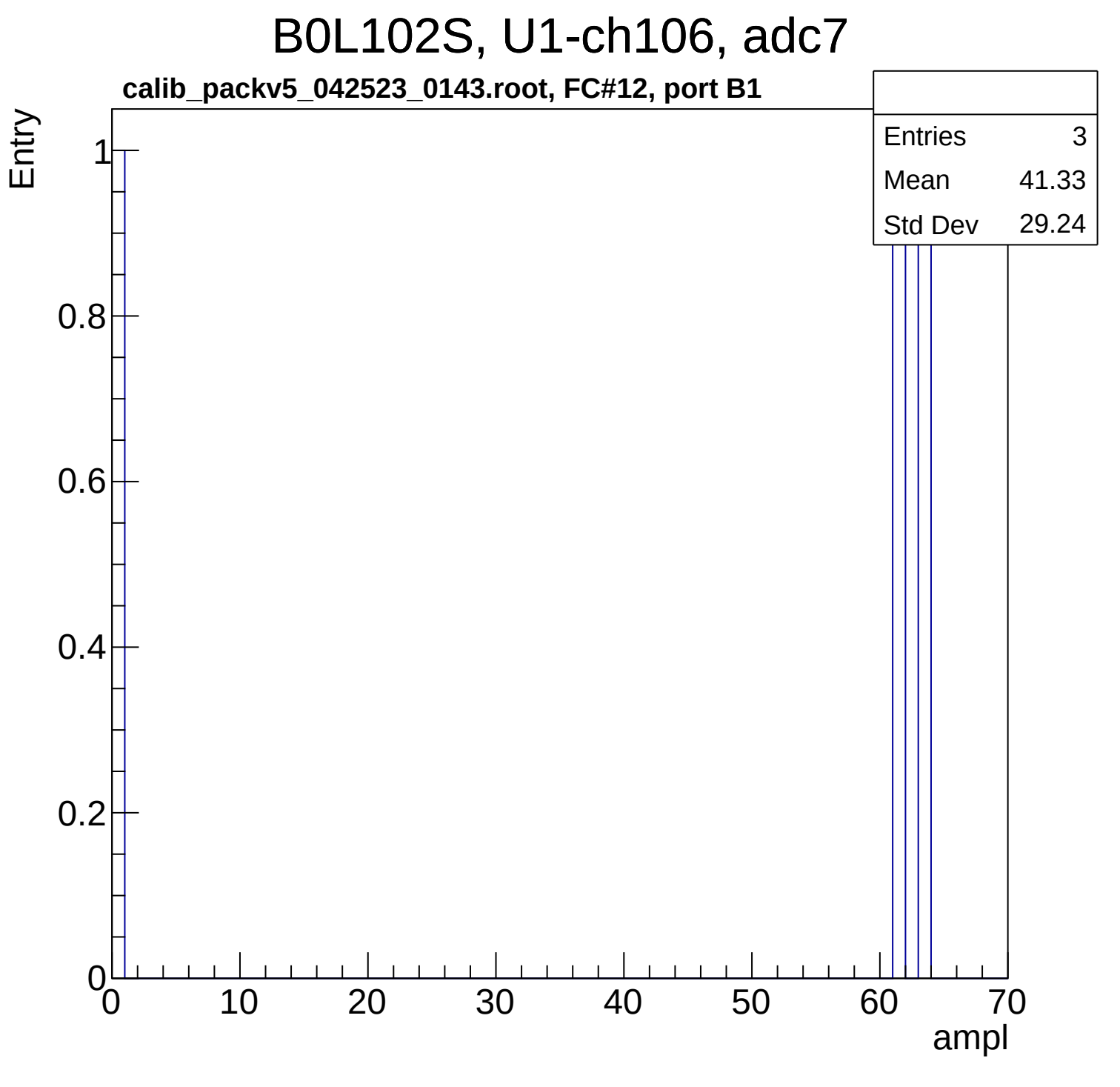

B0L102S, U1-ch106, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 40.66
Std Dev 4.212
Gaus mean : 41.1388
Gaus Width: 4.5900
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

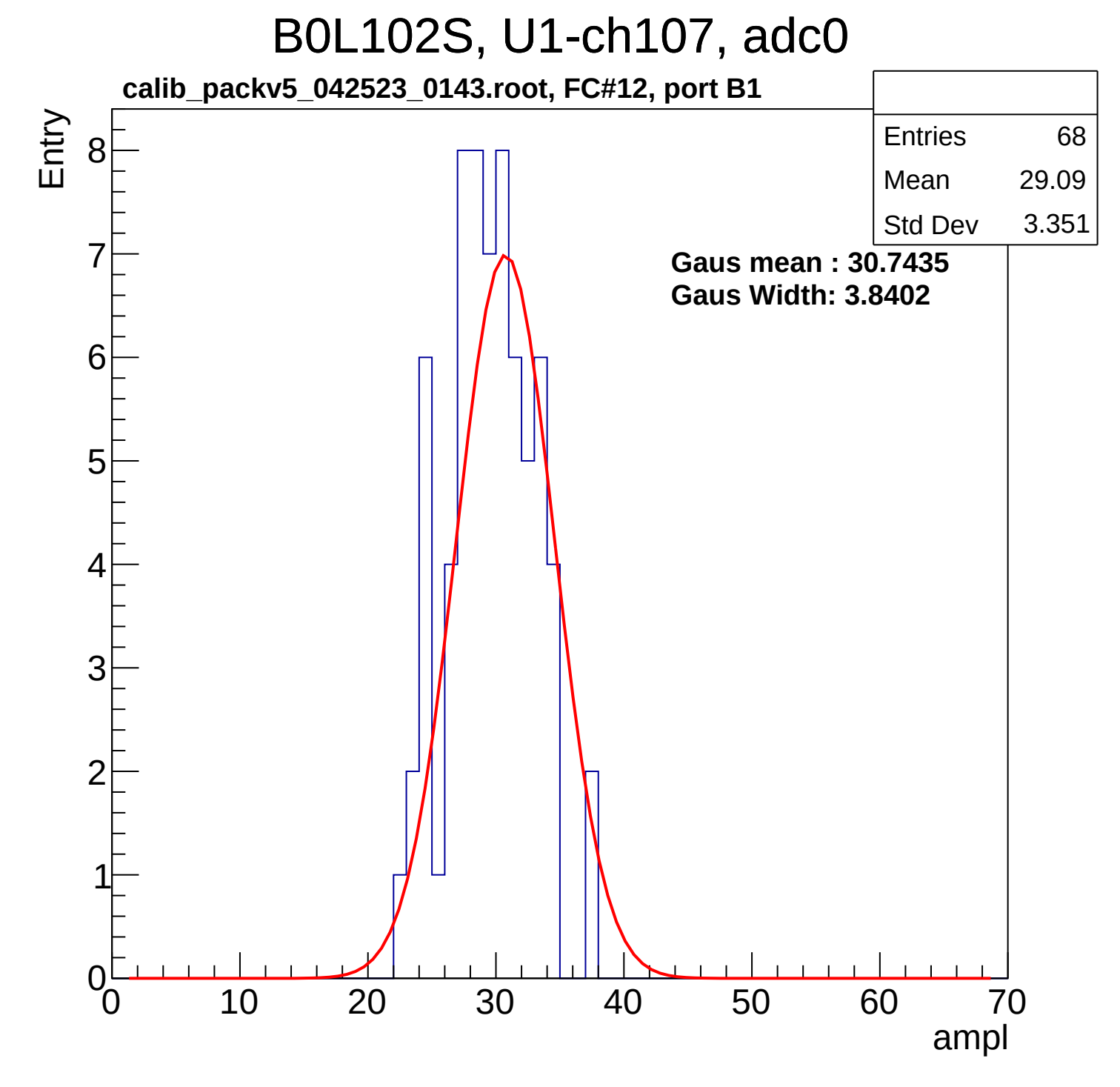

B0L102S, U1-ch106, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 47.3
Std Dev 3.597
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

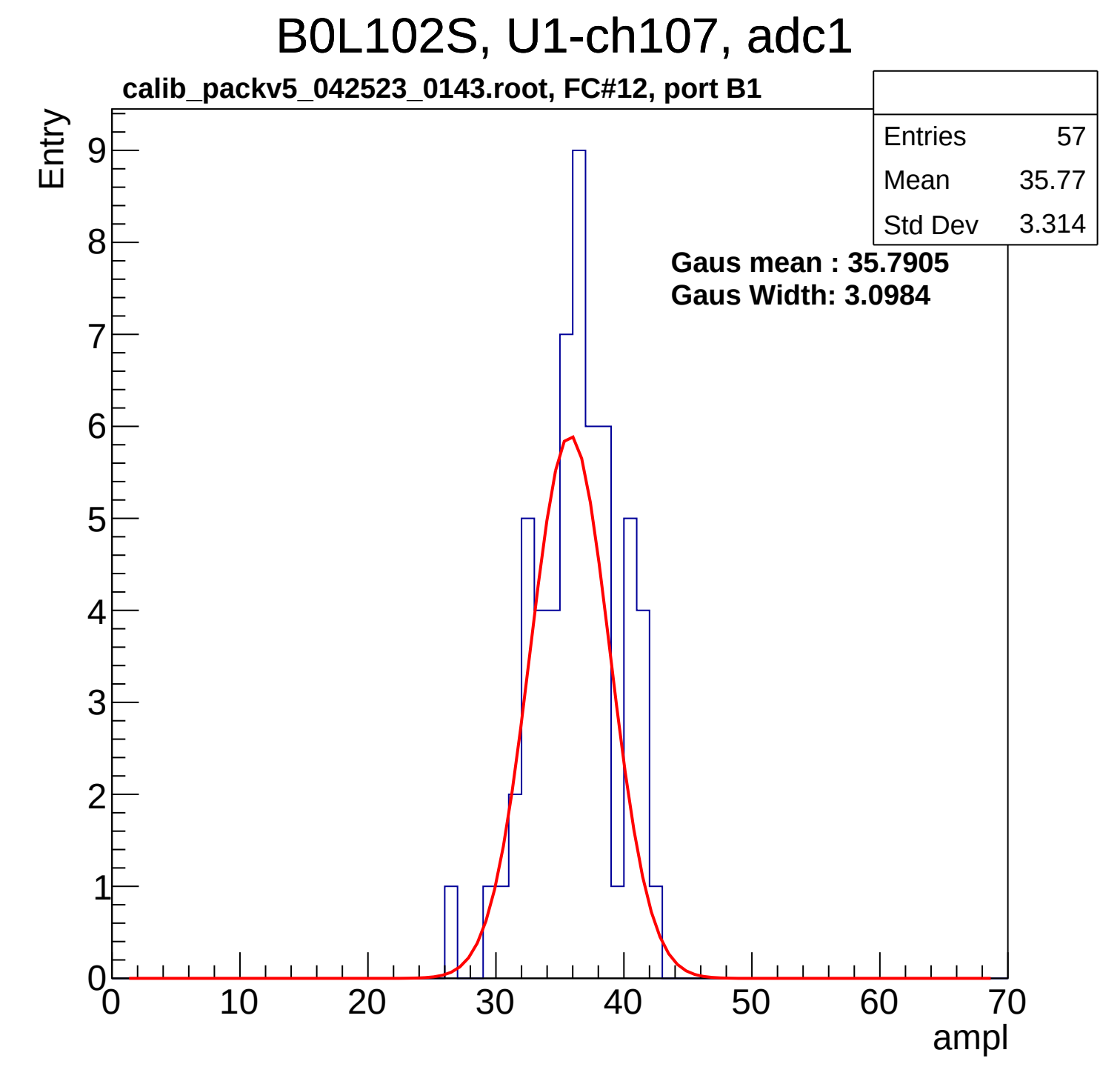

B0L102S, U1-ch106, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 53.31
Std Dev 3.882
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

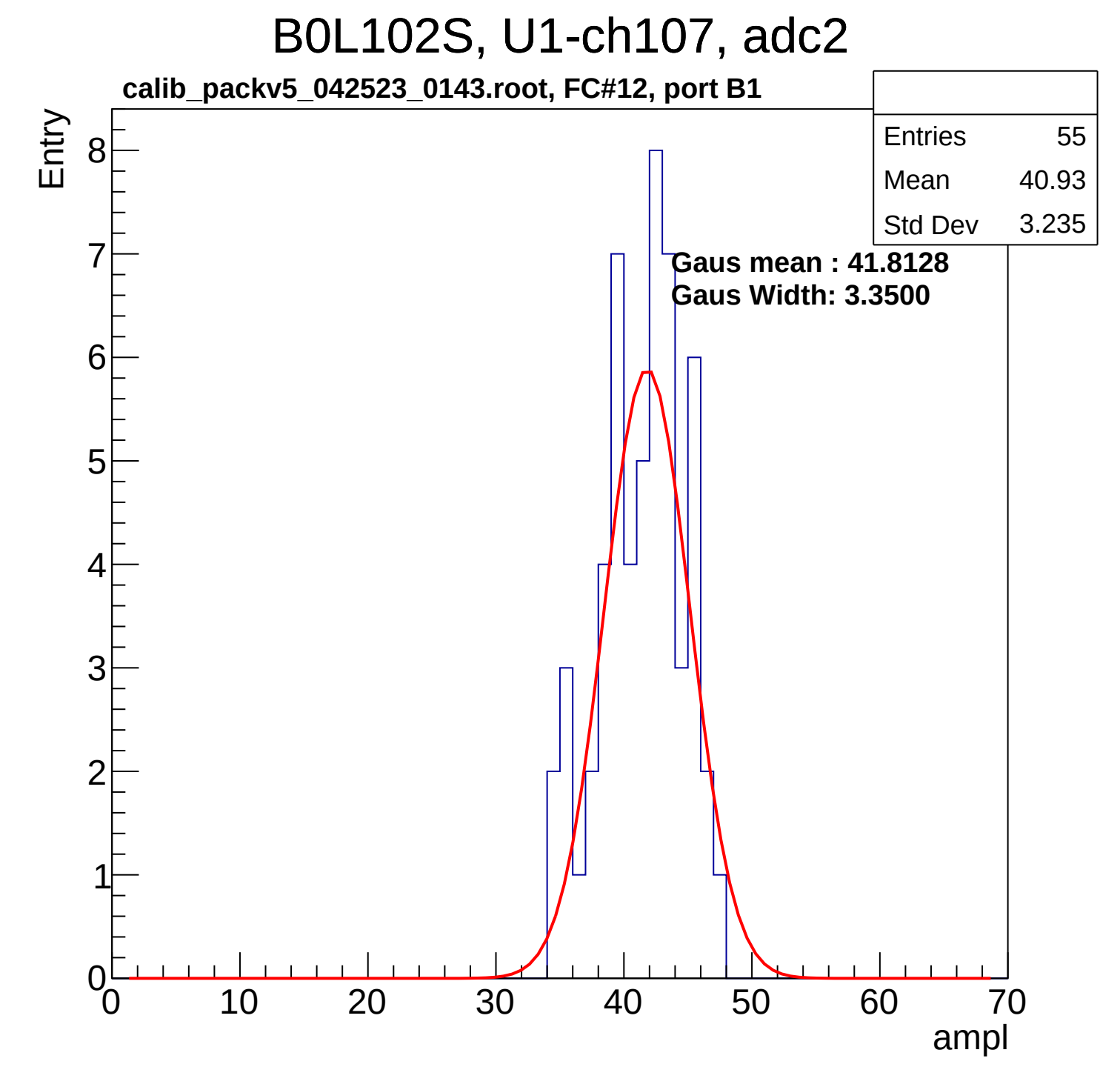

B0L102S, U1-ch106, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 58.06
Std Dev 2.831
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

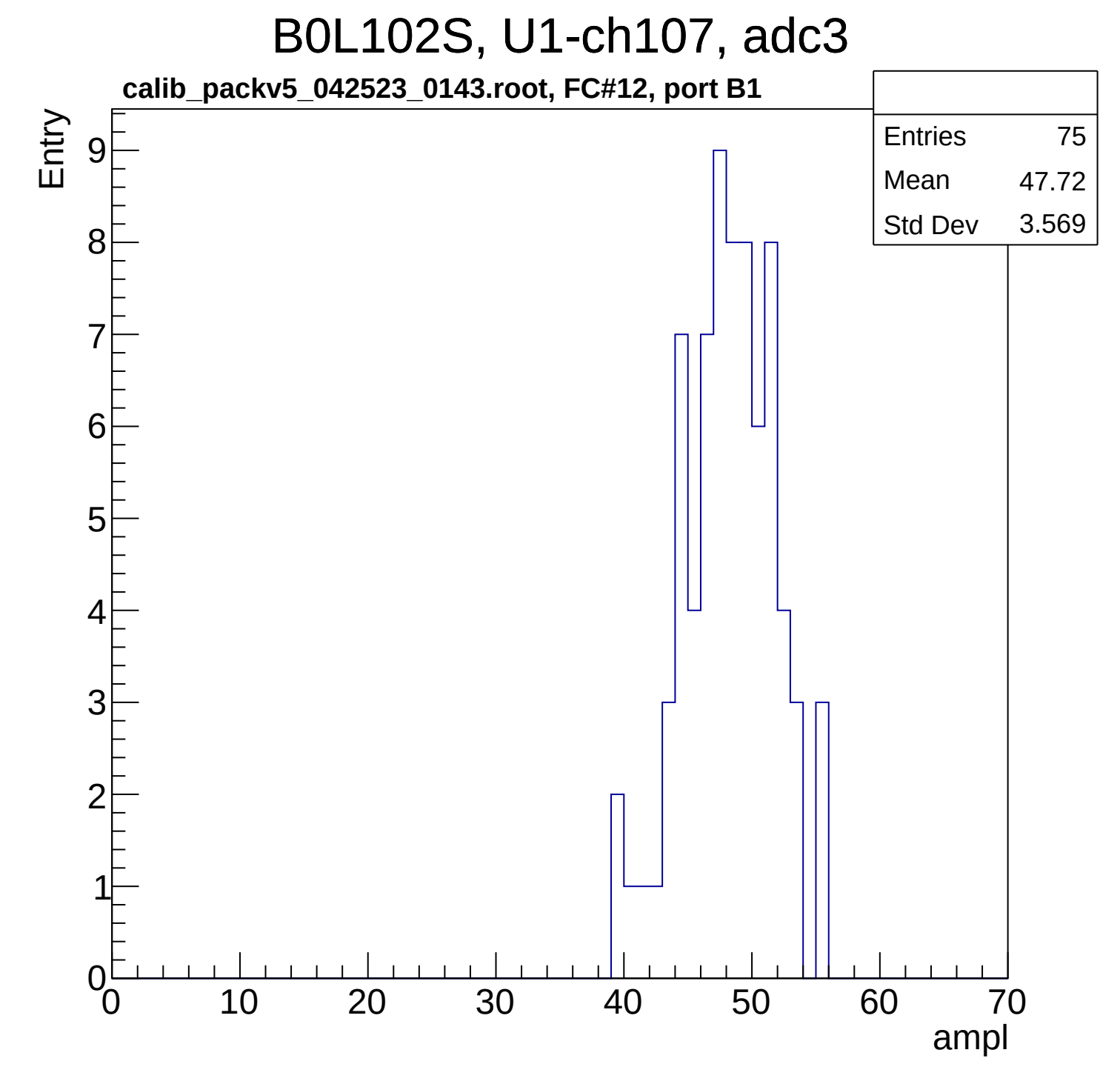

B0L102S, U1-ch106, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 20
Mean 58.55
Std Dev 13.51
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

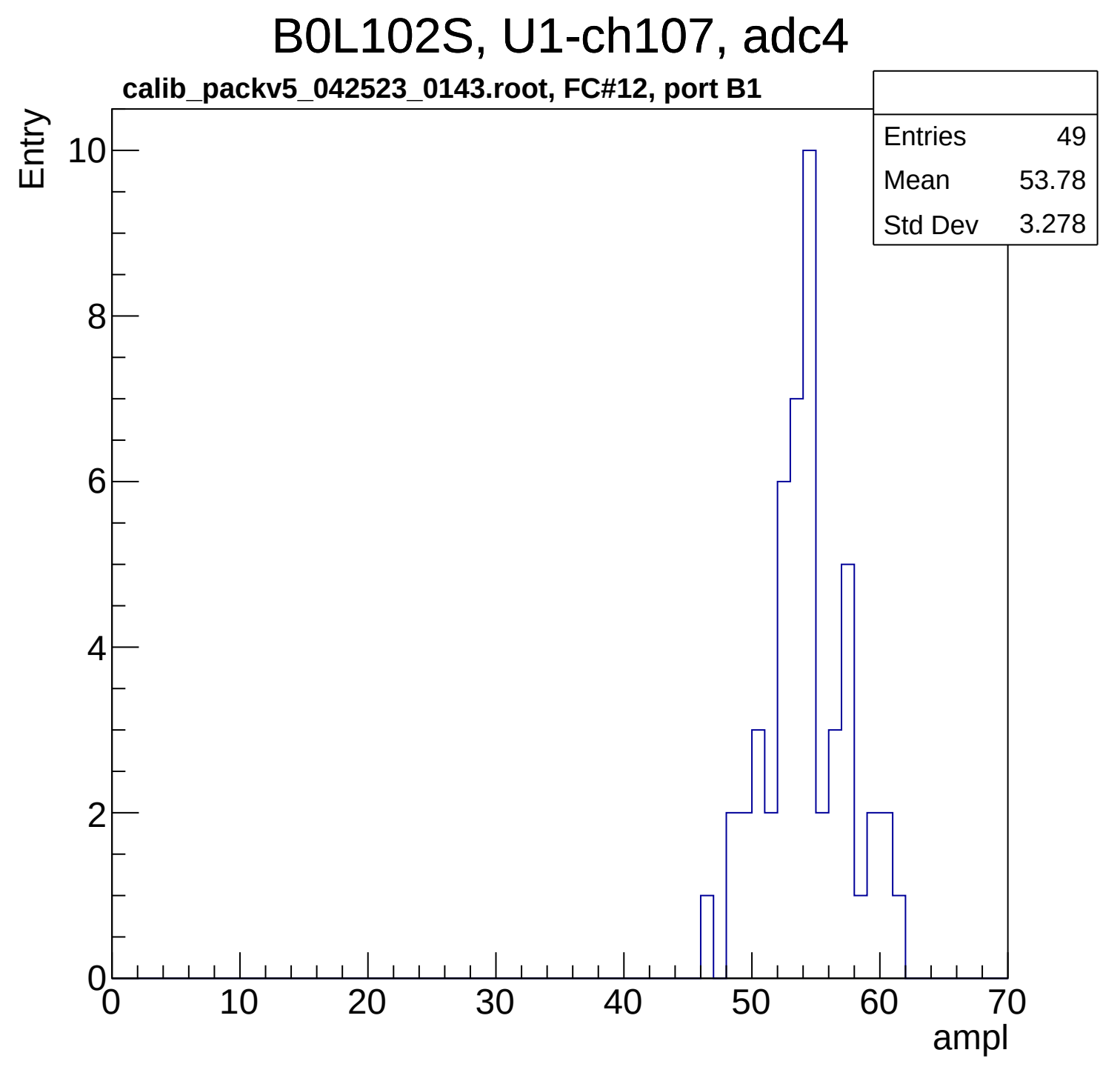

B0L102S, U1-ch106, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

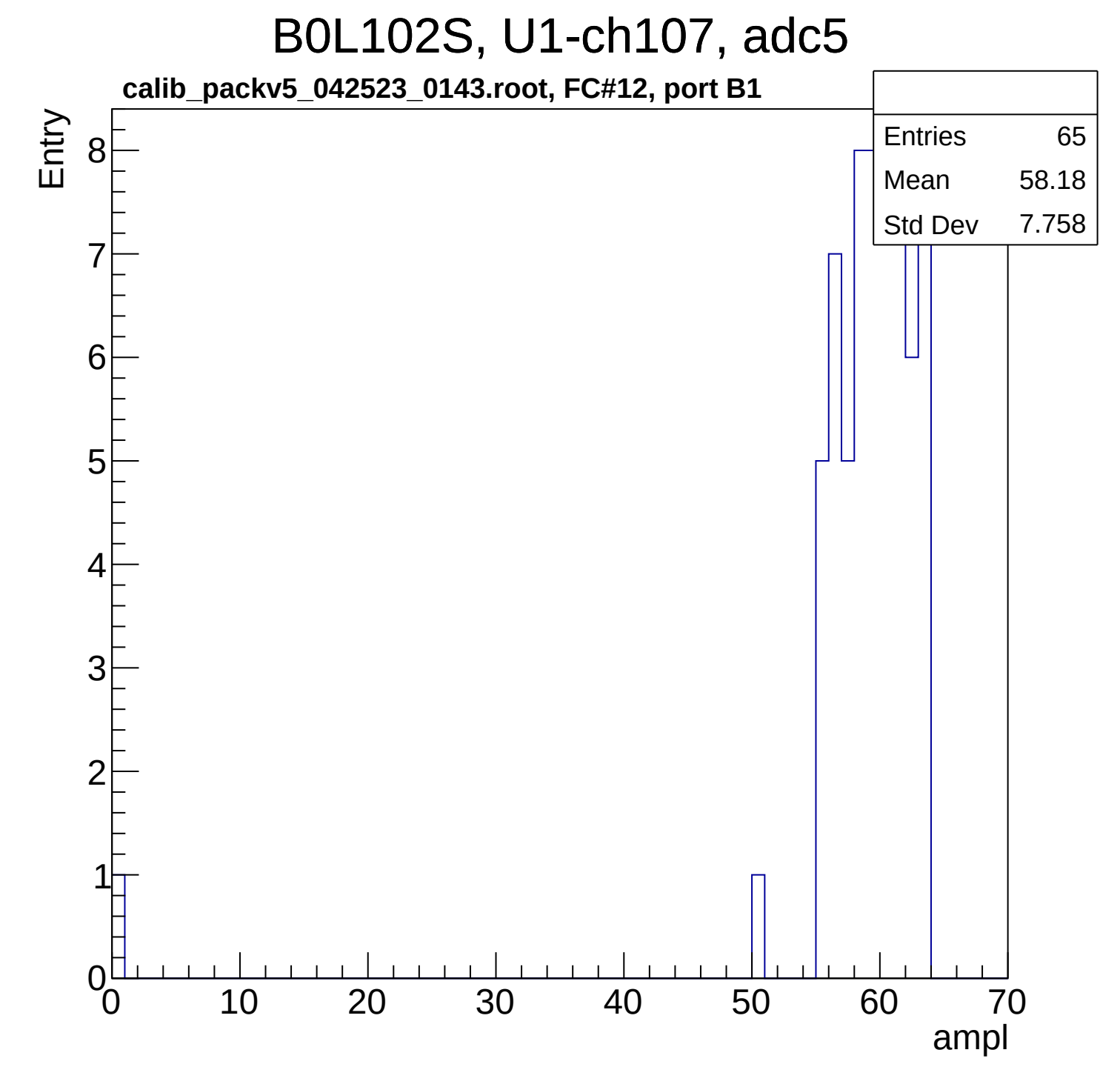

B0L102S, U1-ch107, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 29.09
Std Dev 3.351
Gaus mean : 30.7435
Gaus Width: 3.8402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

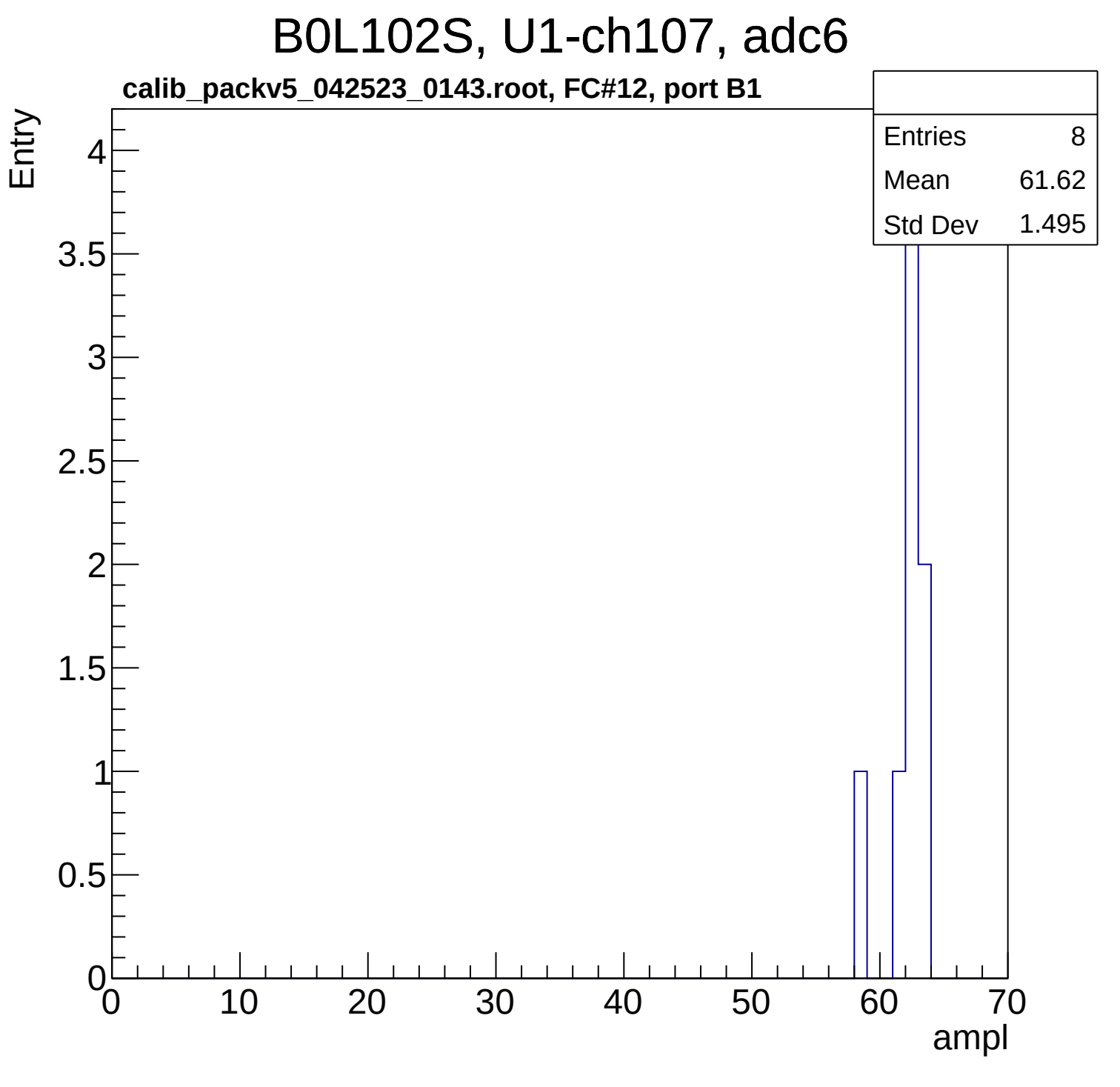

B0L102S, U1-ch107, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 35.77
Std Dev 3.314
Gaus mean : 35.7905
Gaus Width: 3.0984
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch107, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 40.93
Std Dev 3.235
Gaus mean : 41.8128
Gaus Width: 3.3500
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch107, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 47.72
Std Dev 3.569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch107, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 53.78
Std Dev 3.278
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch107, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 58.18
Std Dev 7.758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch107, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.62
Std Dev 1.495
Entry
ampl
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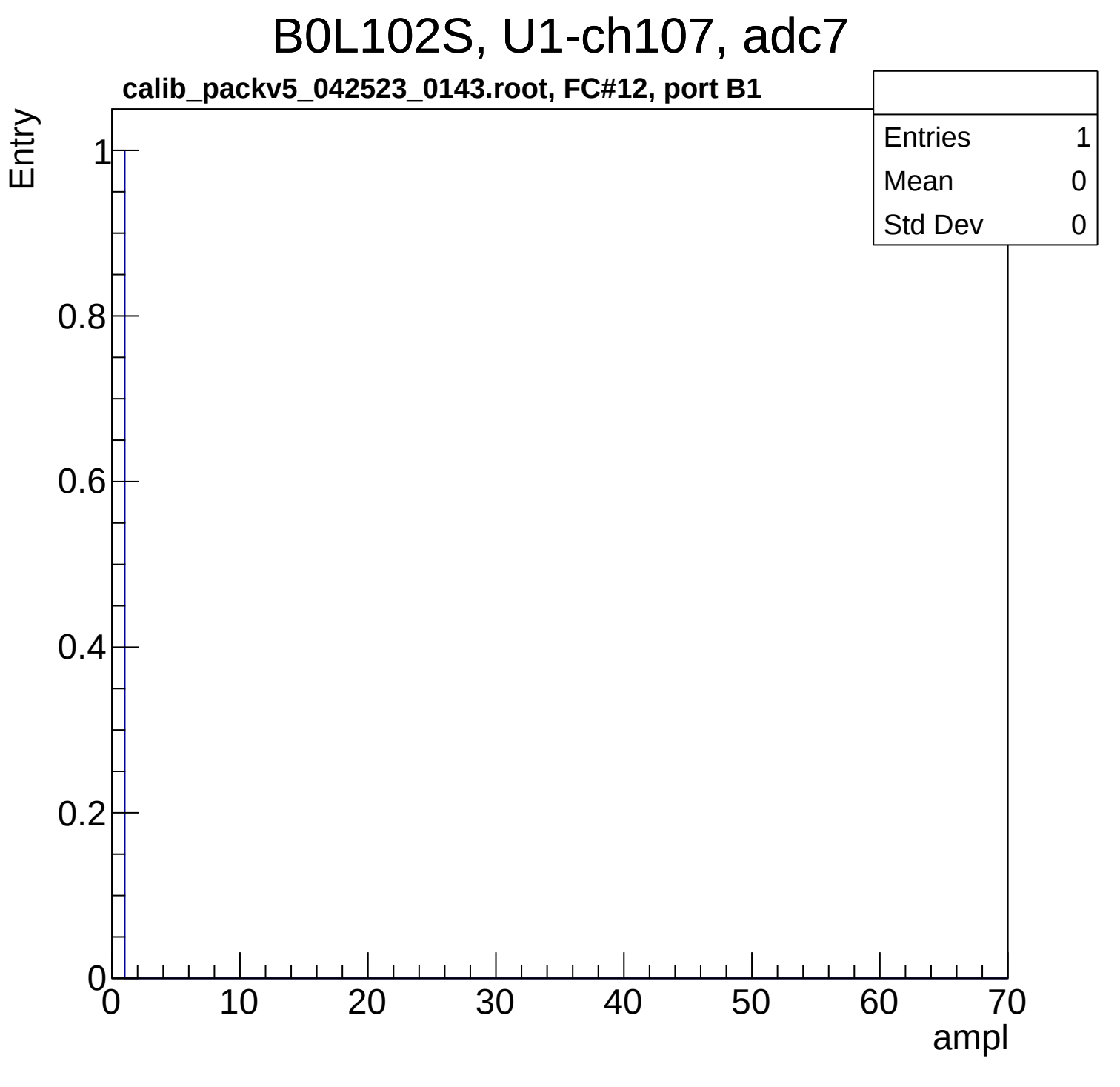
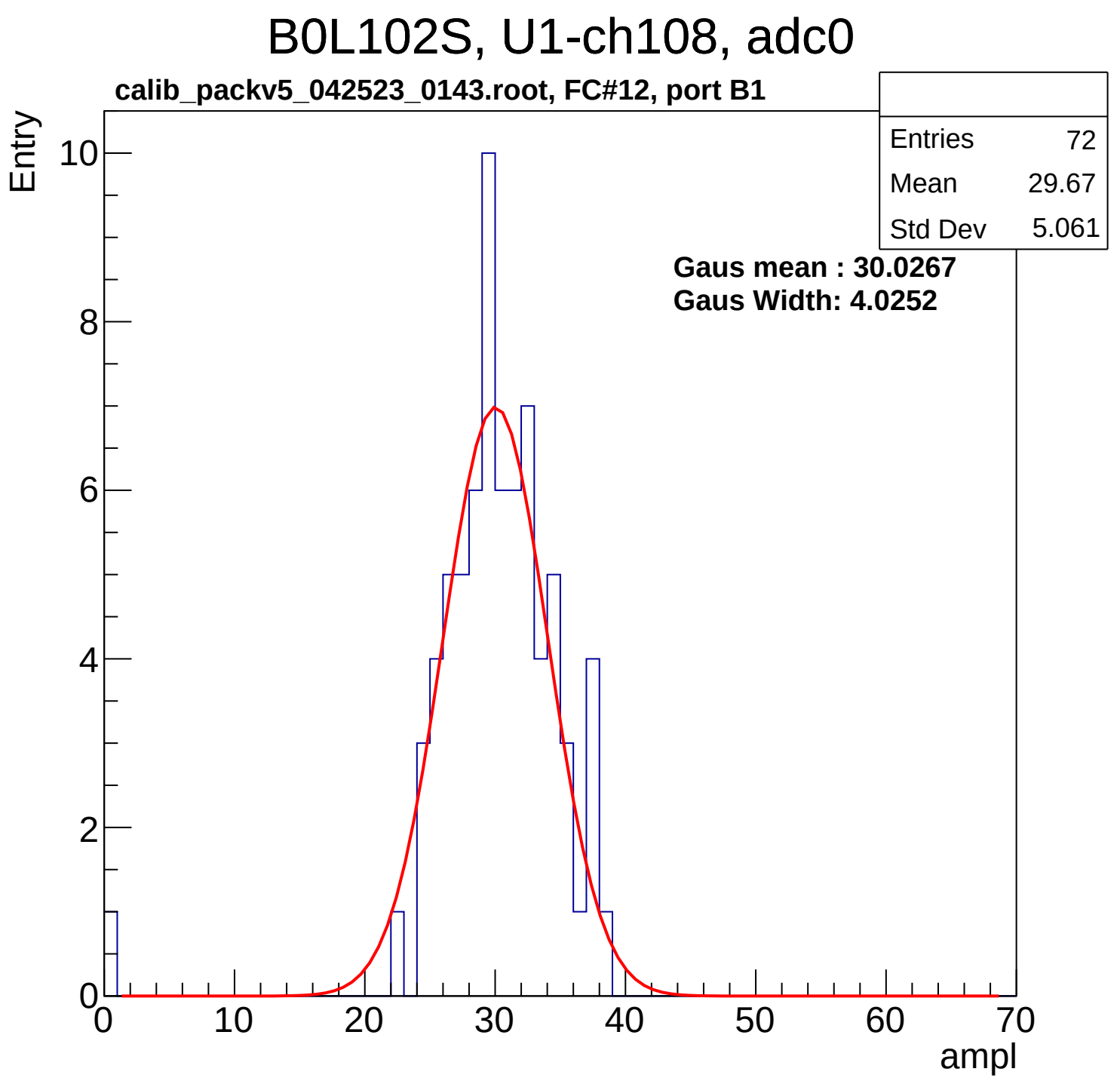
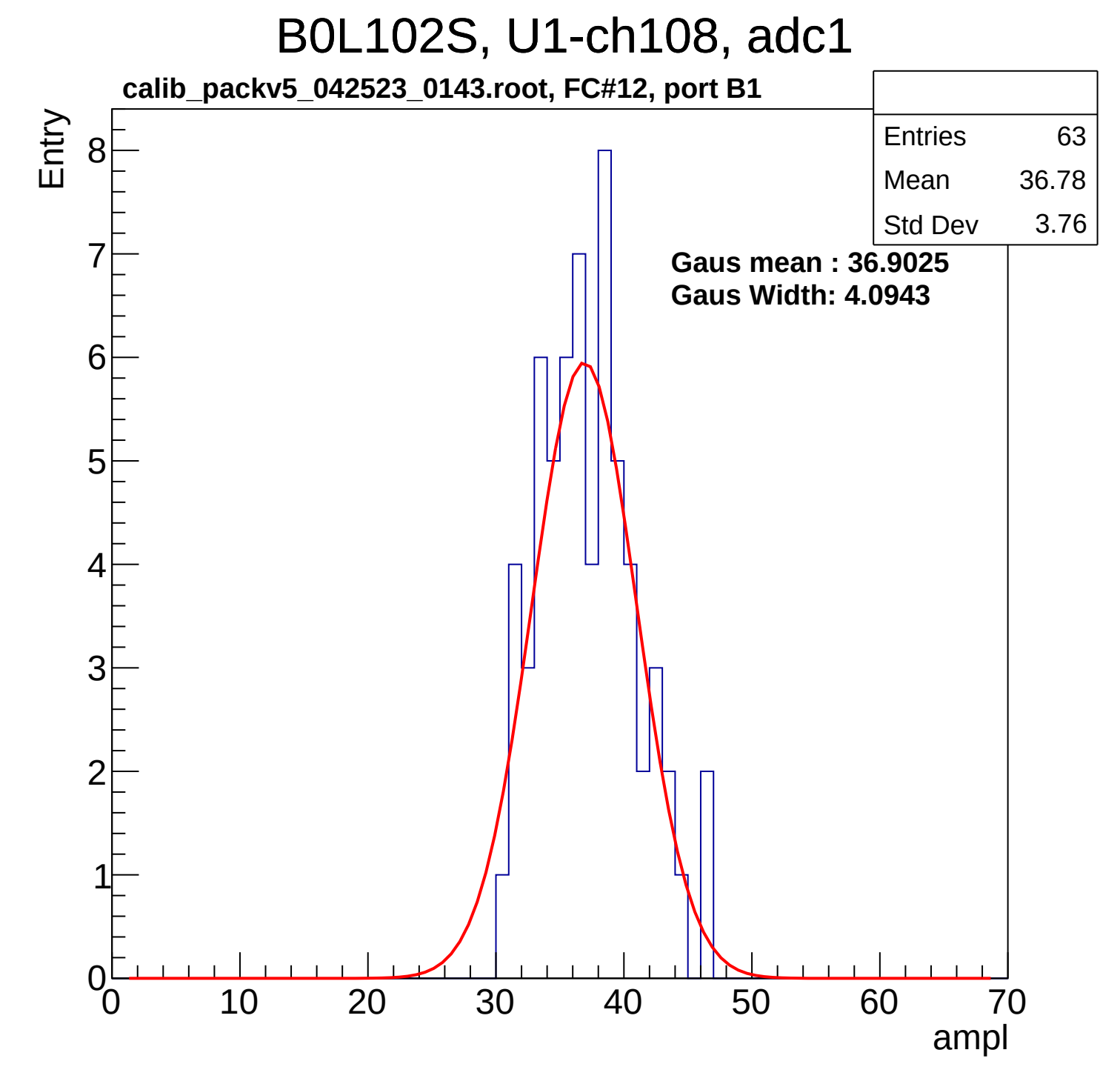
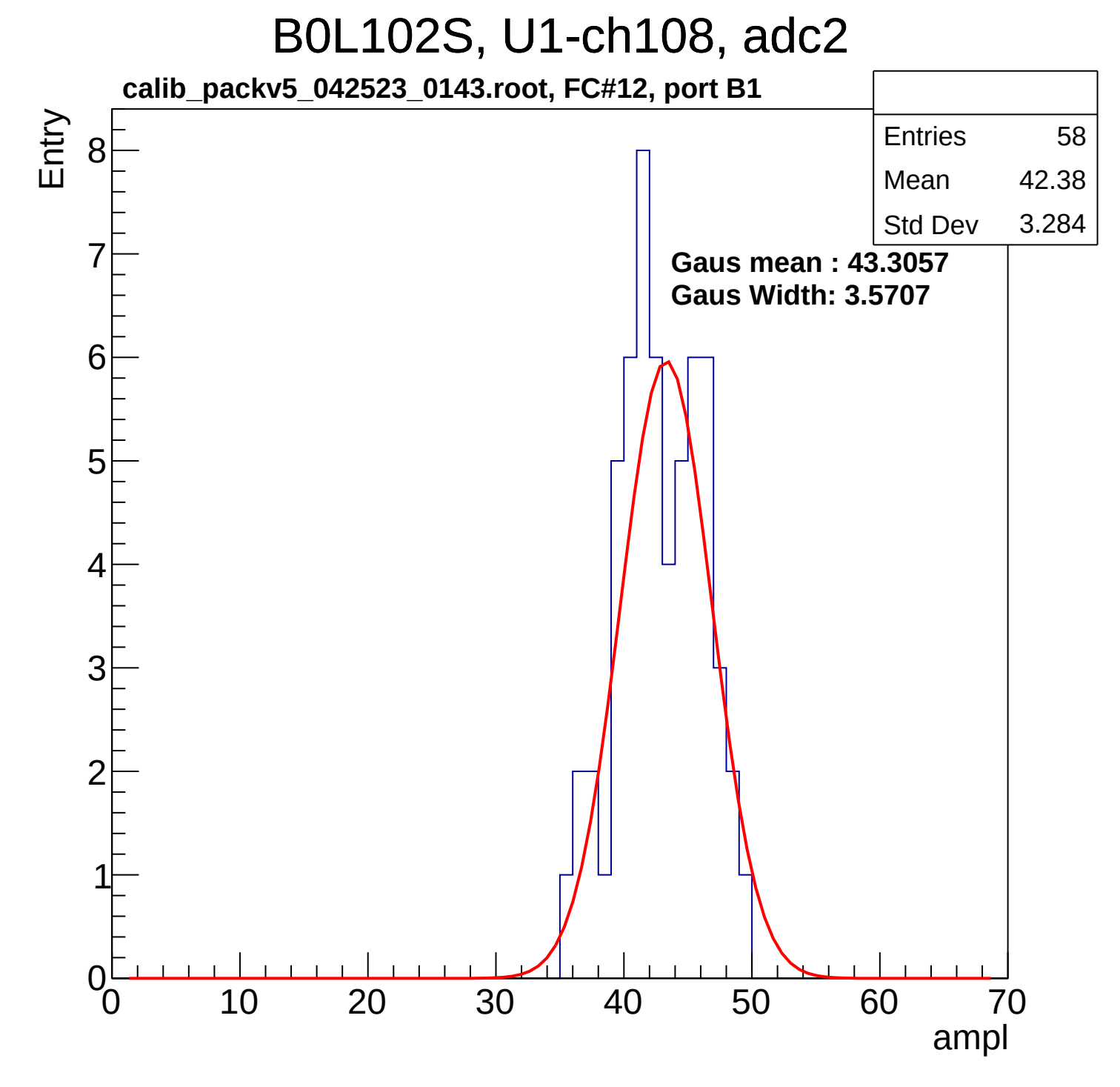
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch107, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch108, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 29.67
Std Dev 5.061
Gaus mean : 30.0267
Gaus Width: 4.0252
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch108, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 36.78
Std Dev 3.76
Gaus mean : 36.9025
Gaus Width: 4.0943
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch108, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 42.38
Std Dev 3.284
Gaus mean : 43.3057
Gaus Width: 3.5707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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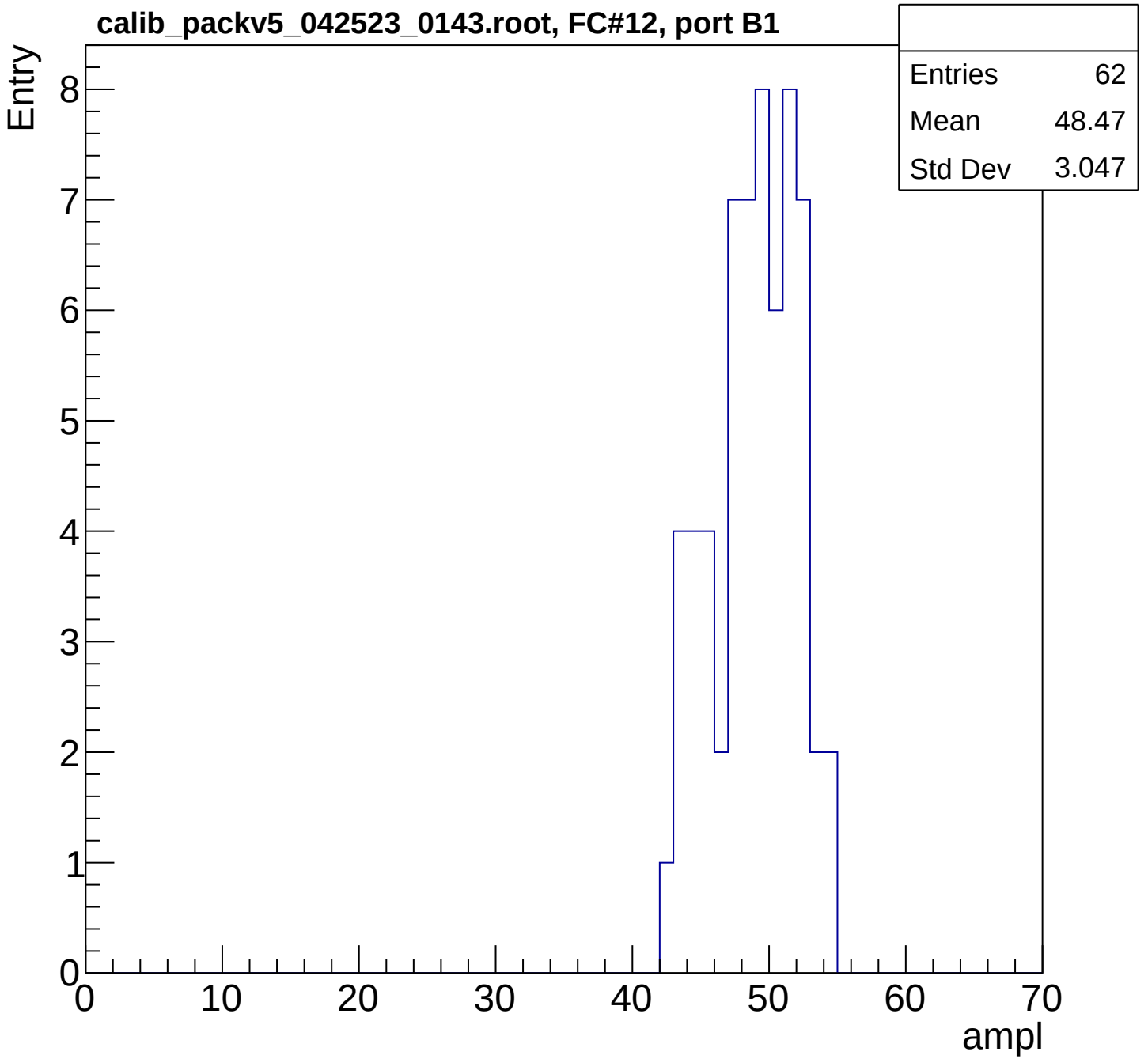
B0L102S, U1-ch108, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 48.47
Std Dev 3.047
Entry
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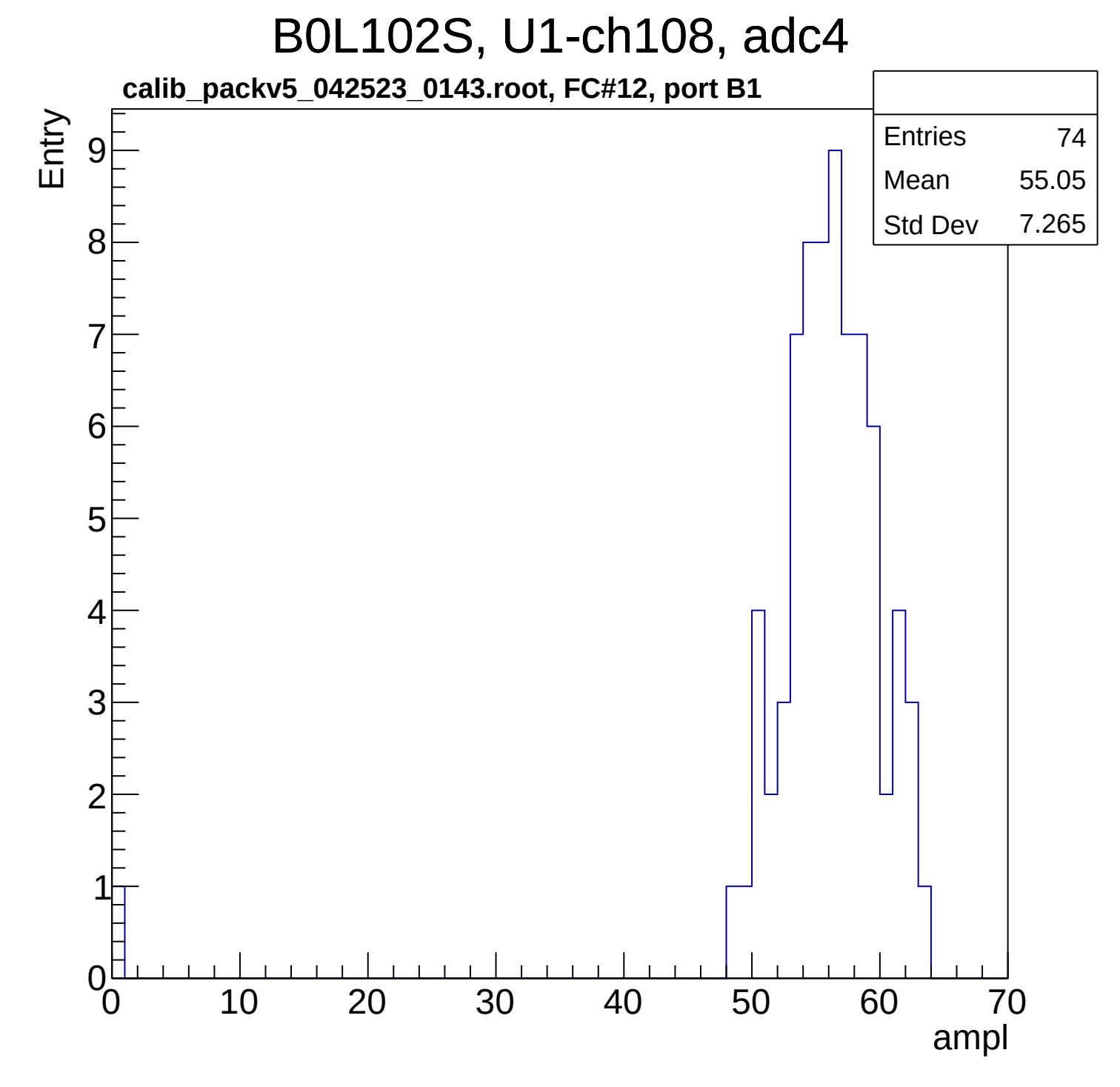
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch108, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 55.05
Std Dev 7.265
Entry
ampl
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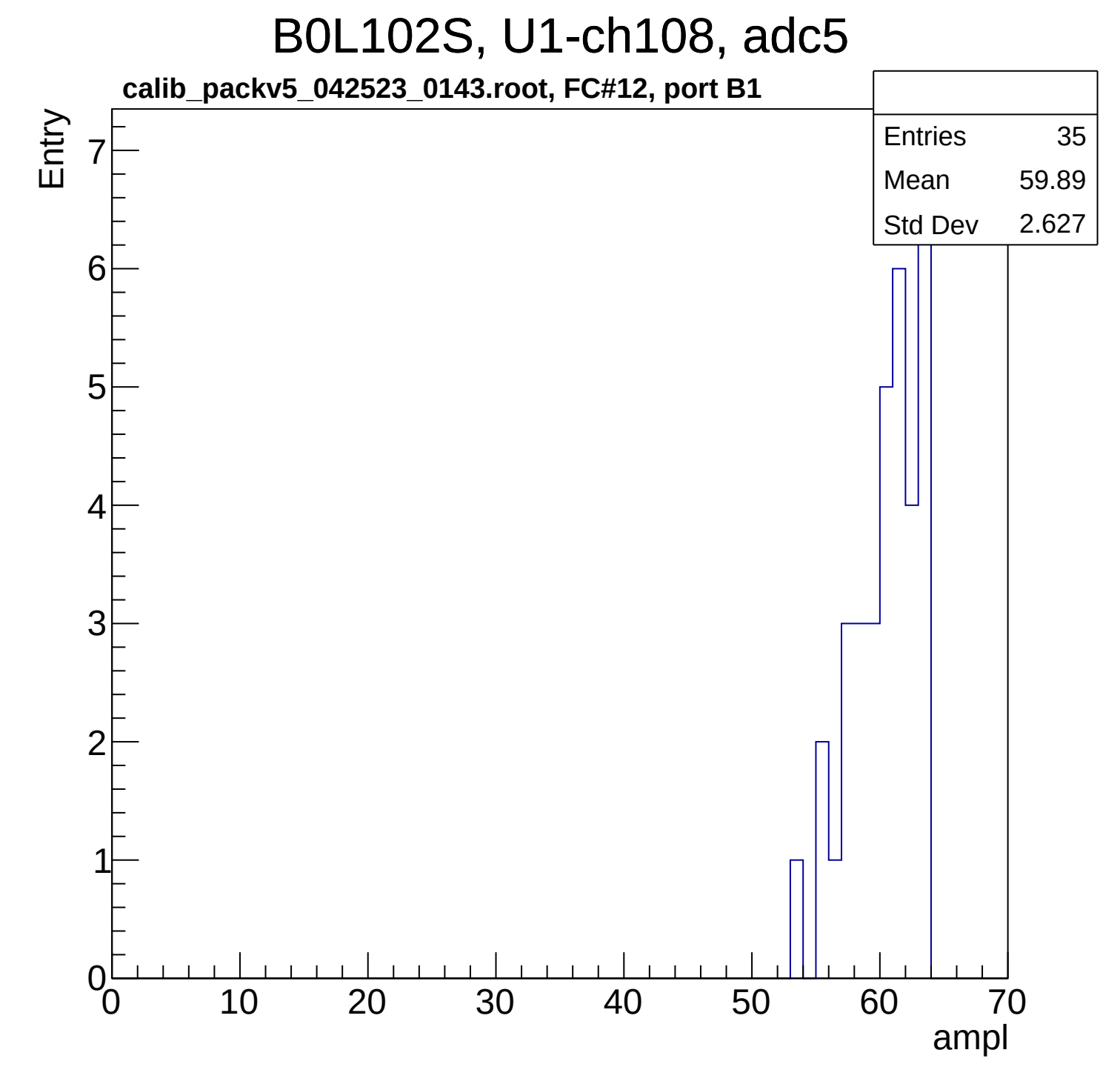
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch108, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 59.89
Std Dev 2.627
Entry
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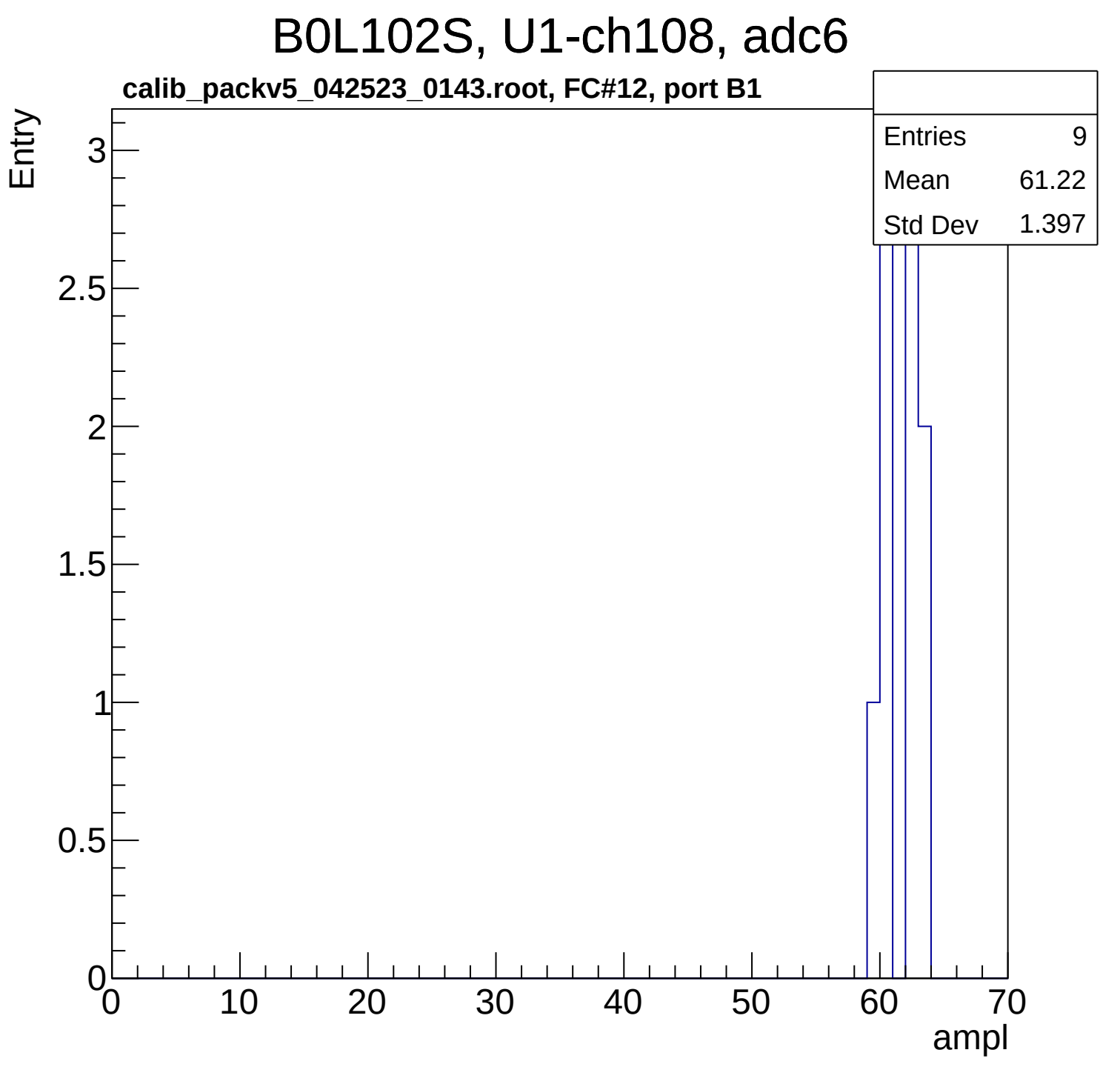
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch108, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.22
Std Dev 1.397
Entry
ampl
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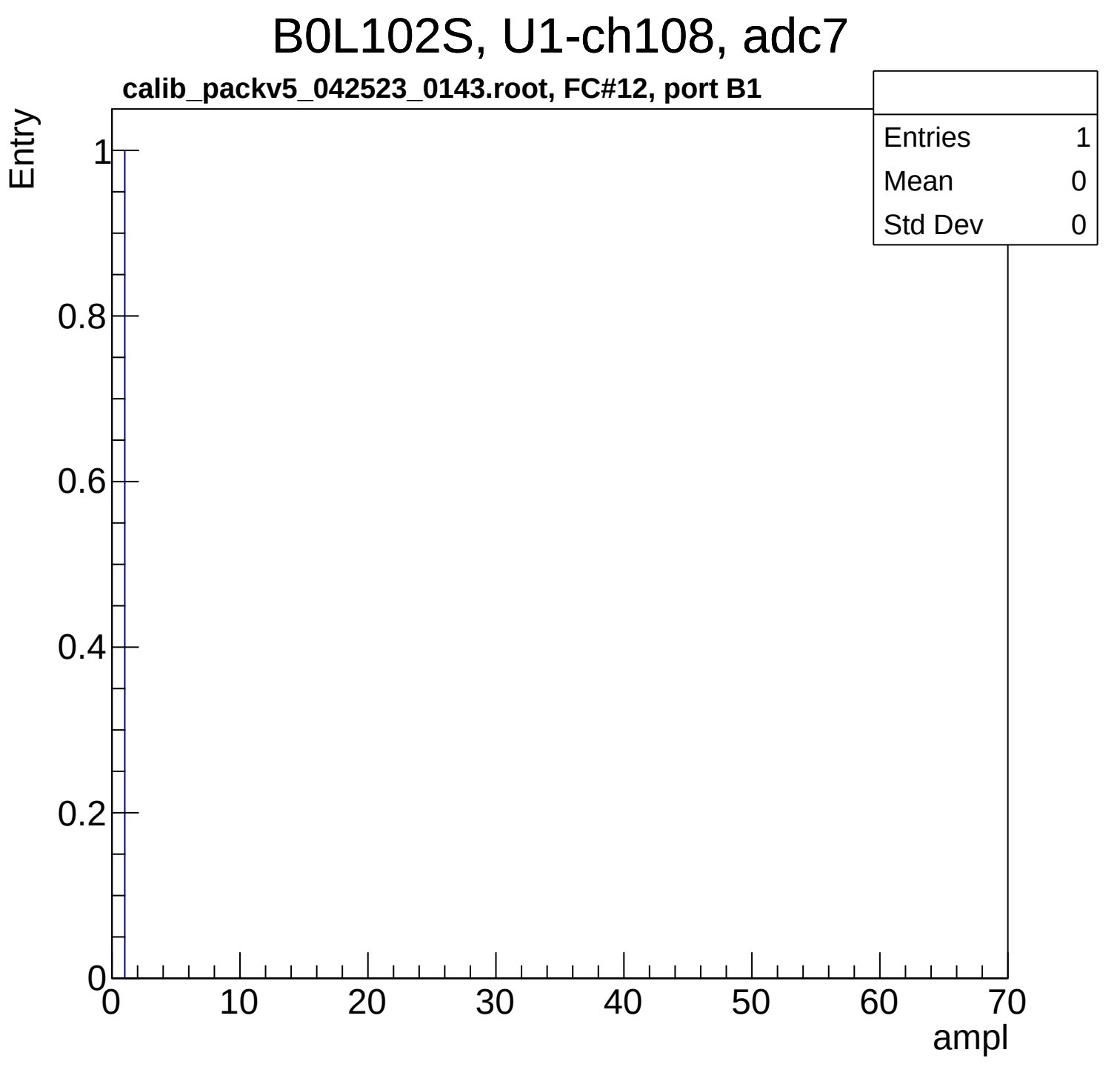
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch108, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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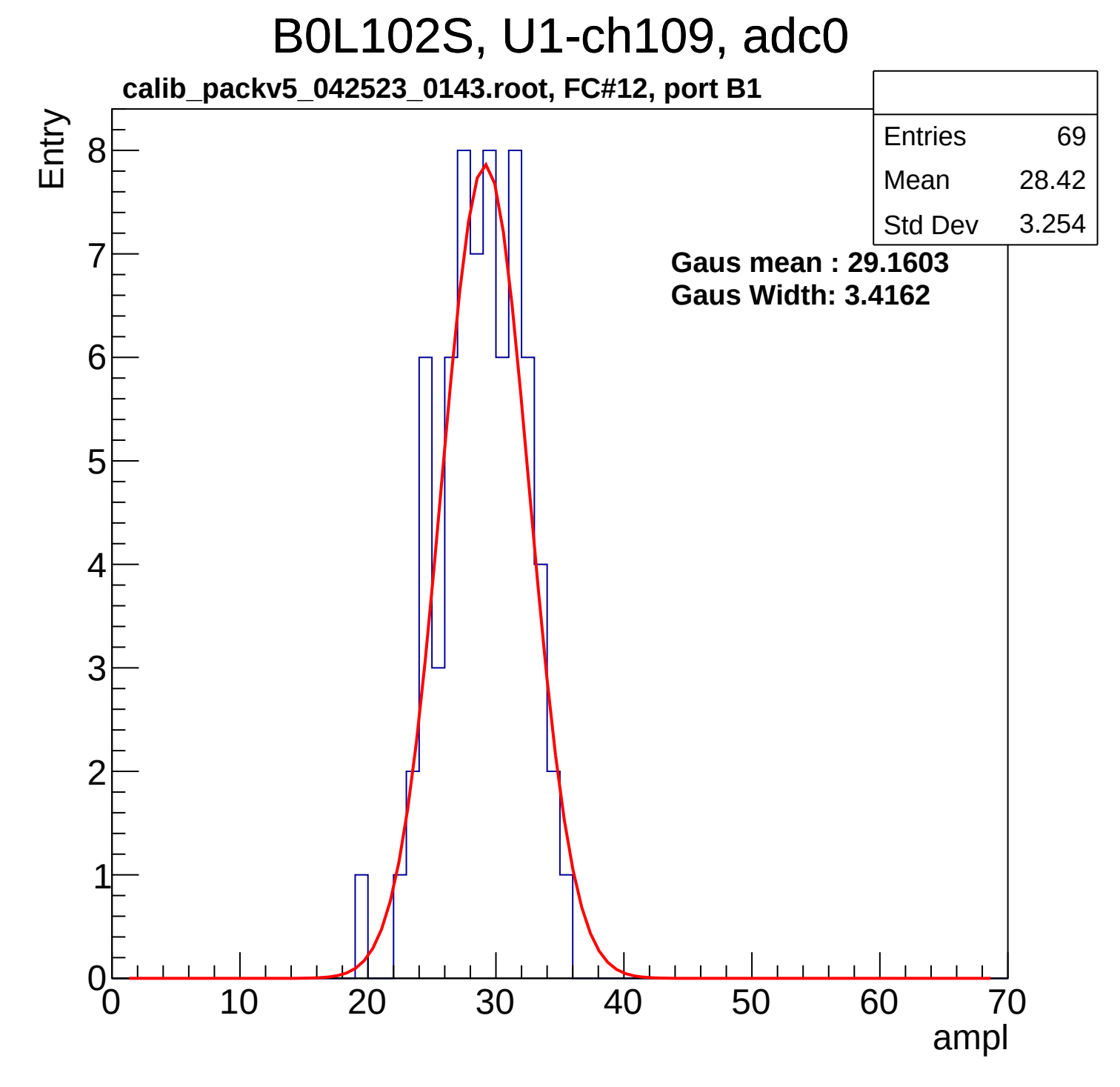
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch109, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 28.42
Std Dev 3.254
Gaus mean : 29.1603
Gaus Width: 3.4162
Entry
ampl
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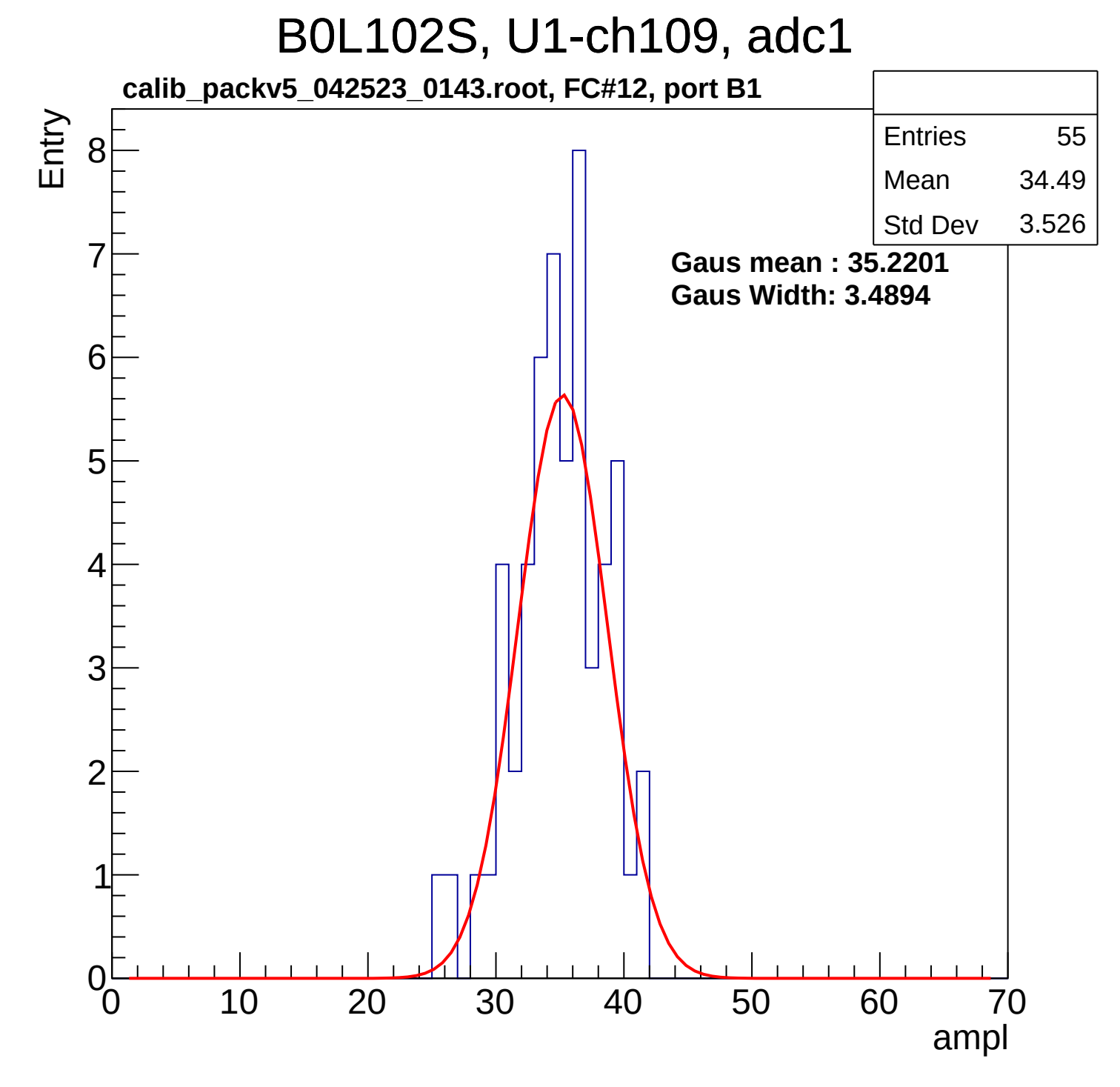
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch109, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 34.49
Std Dev 3.526
Gaus mean : 35.2201
Gaus Width: 3.4894
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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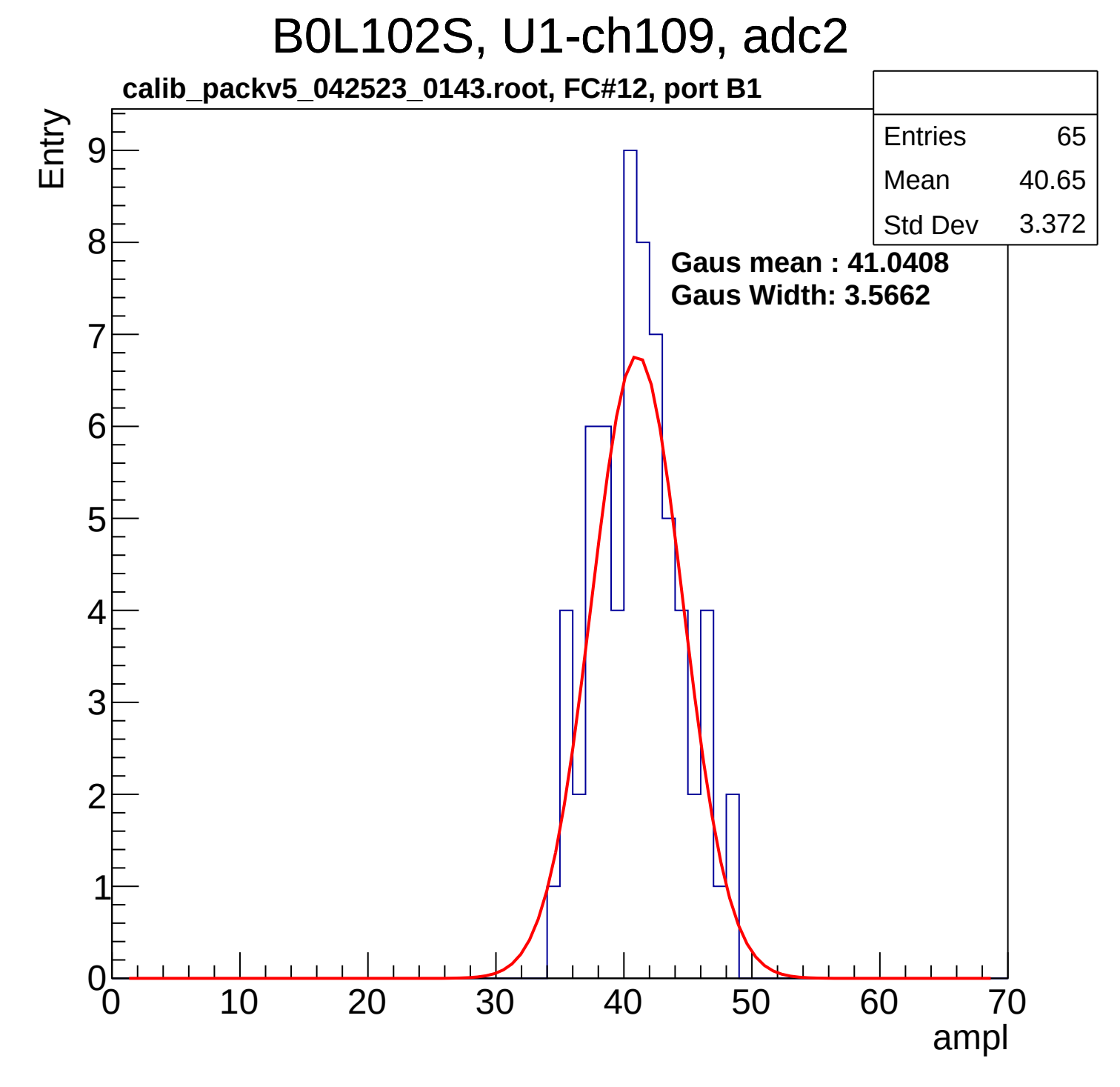
B0L102S, U1-ch109, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 40.65
Std Dev 3.372
Gaus mean : 41.0408
Gaus Width: 3.5662
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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60
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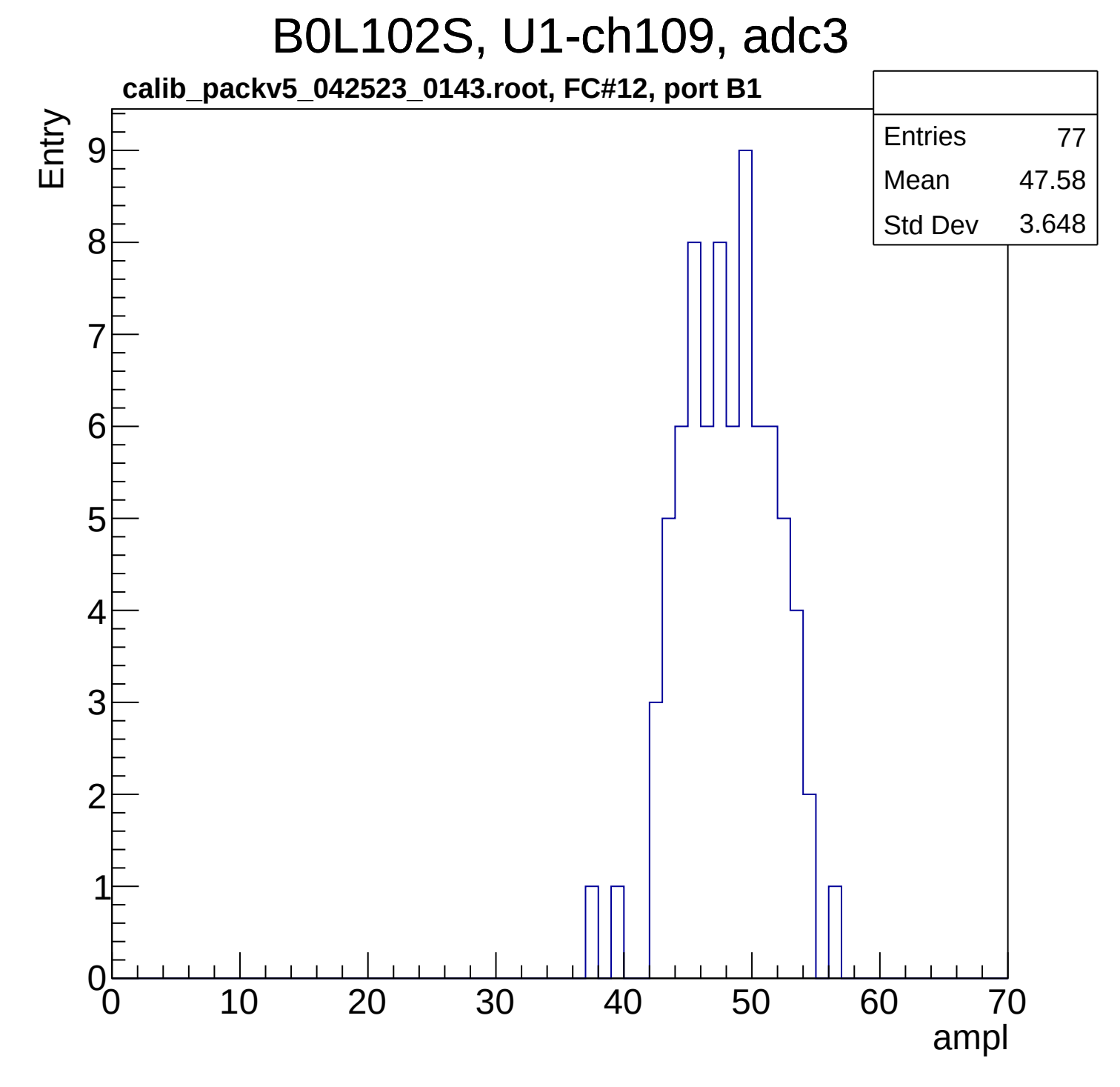
B0L102S, U1-ch109, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 47.58
Std Dev 3.648
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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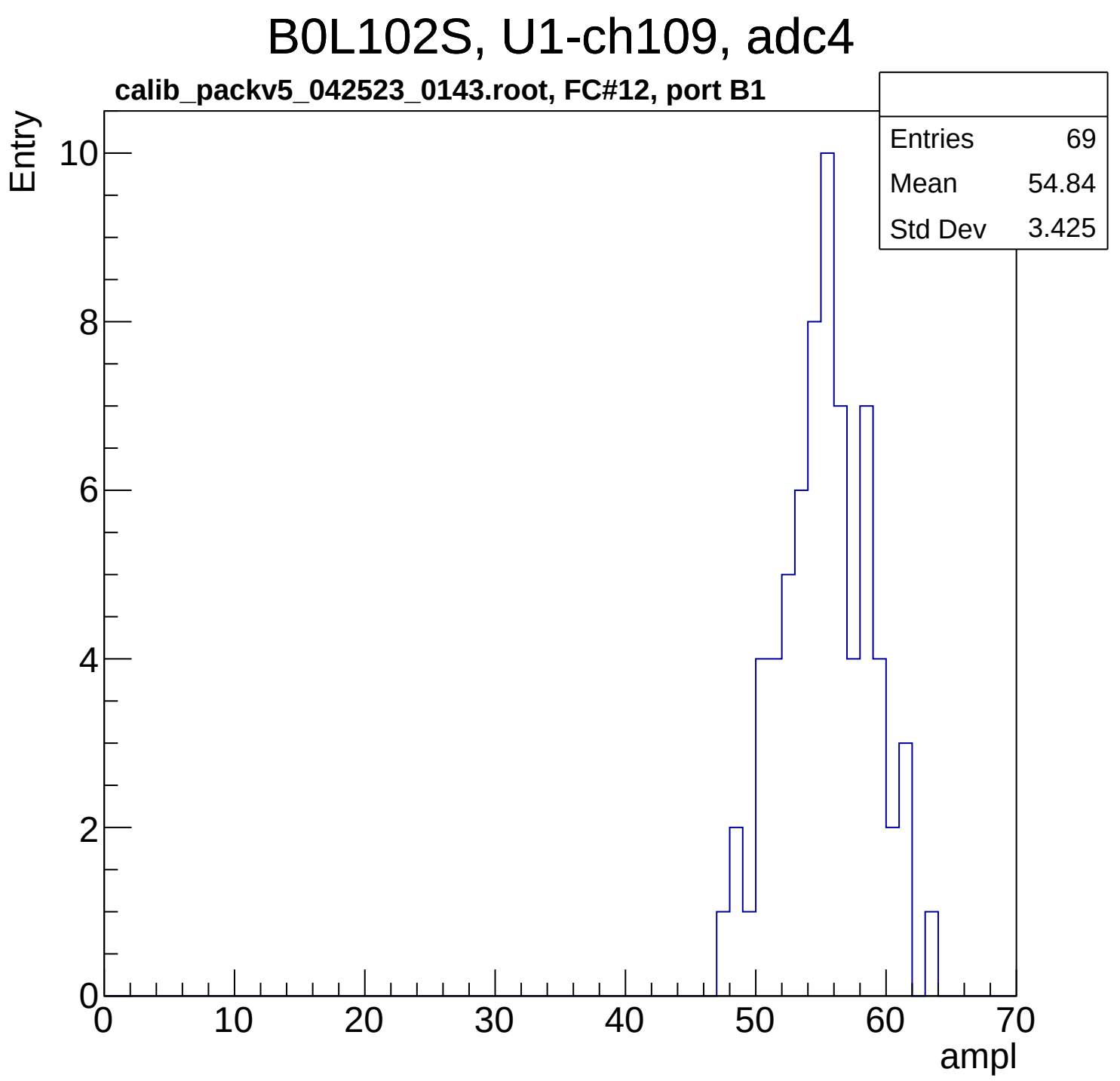
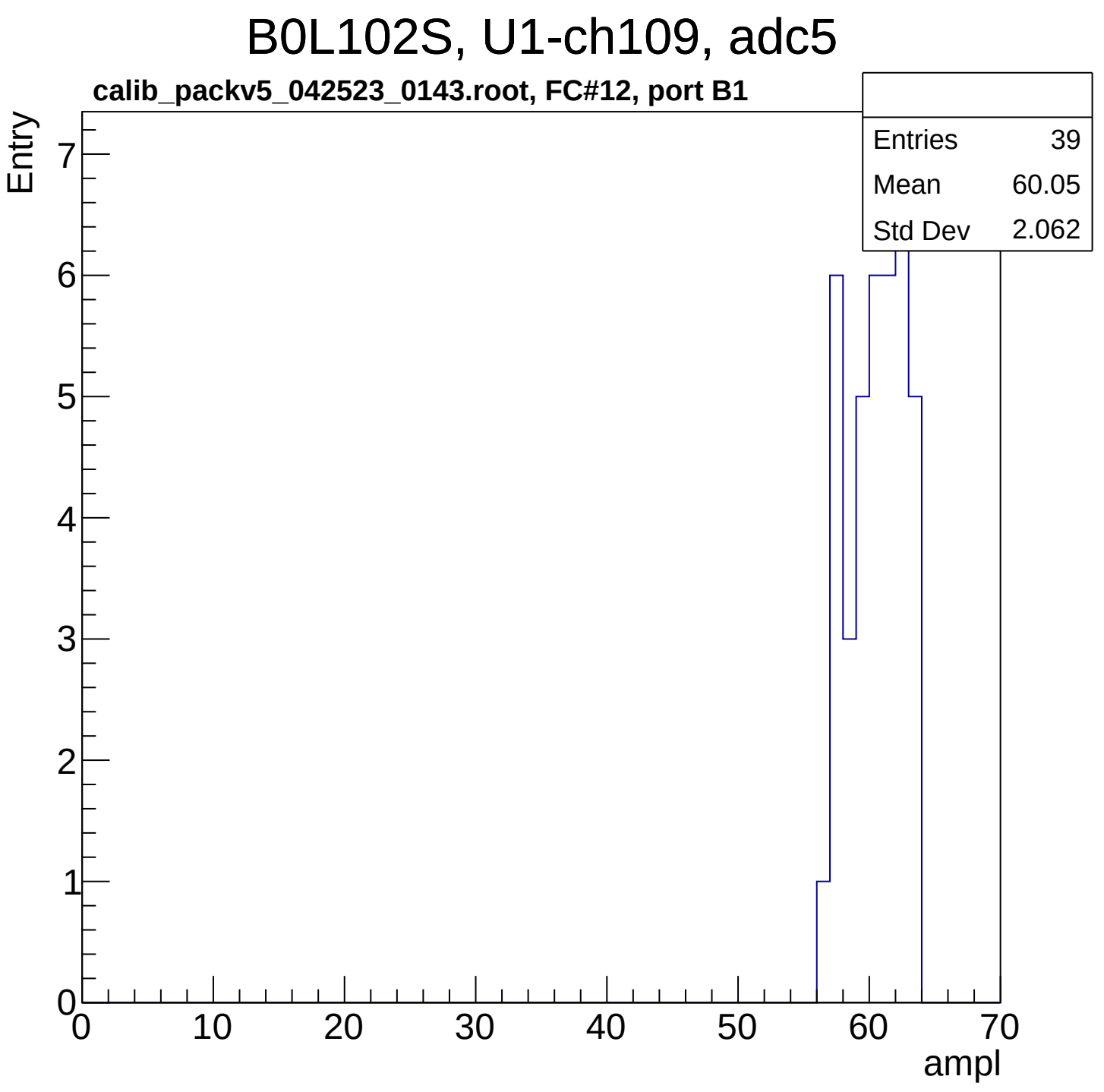

B0L102S, U1-ch109, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 54.84
Std Dev 3.425
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch109, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 60.05
Std Dev 2.062
Entry
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1
2
3
4
5
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7
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10
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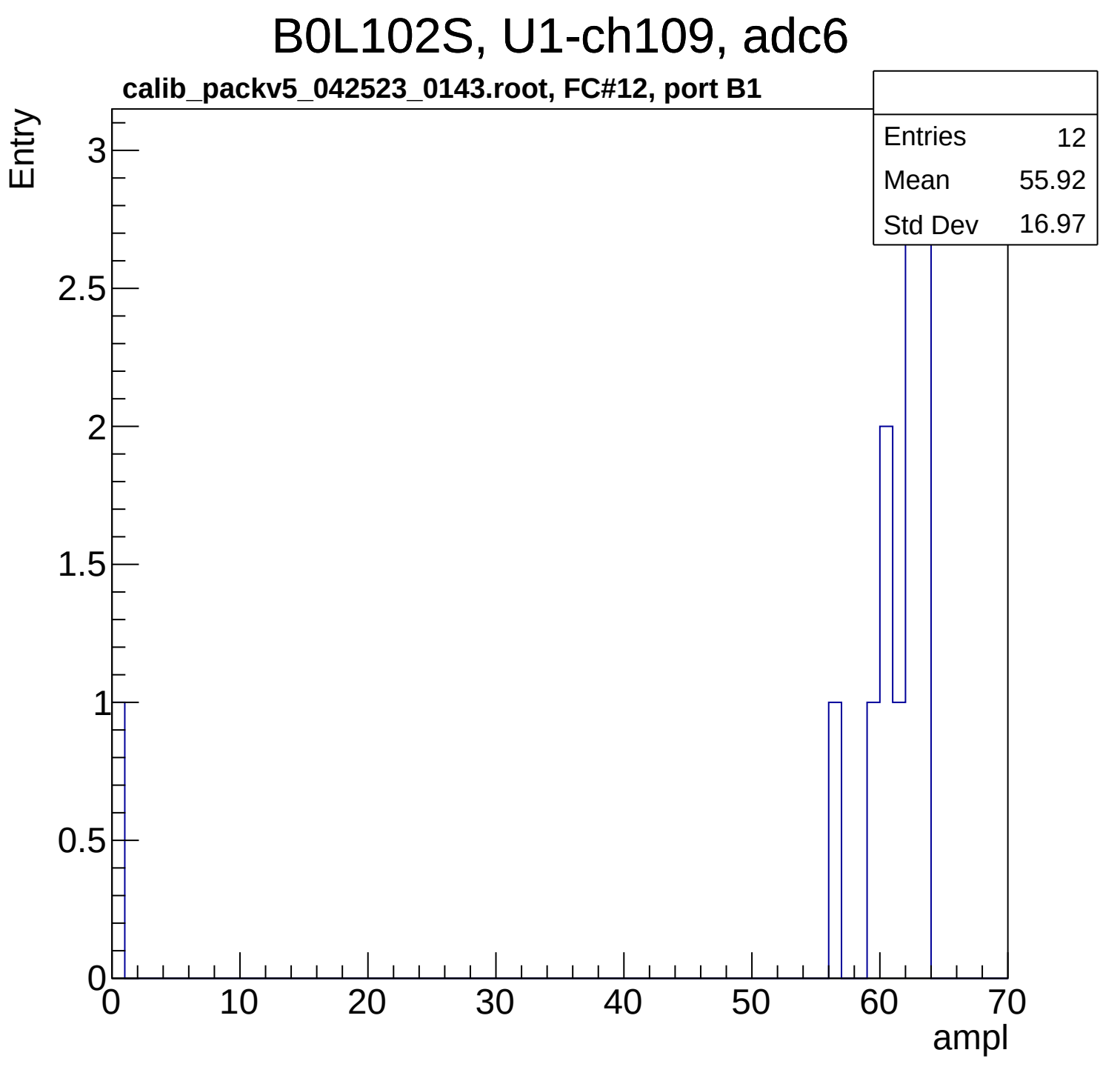
50
60
70
ampl


B0L102S, U1-ch109, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 55.92
Std Dev 16.97
Entry
ampl
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0.5
1
1.5
2
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0
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40
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60
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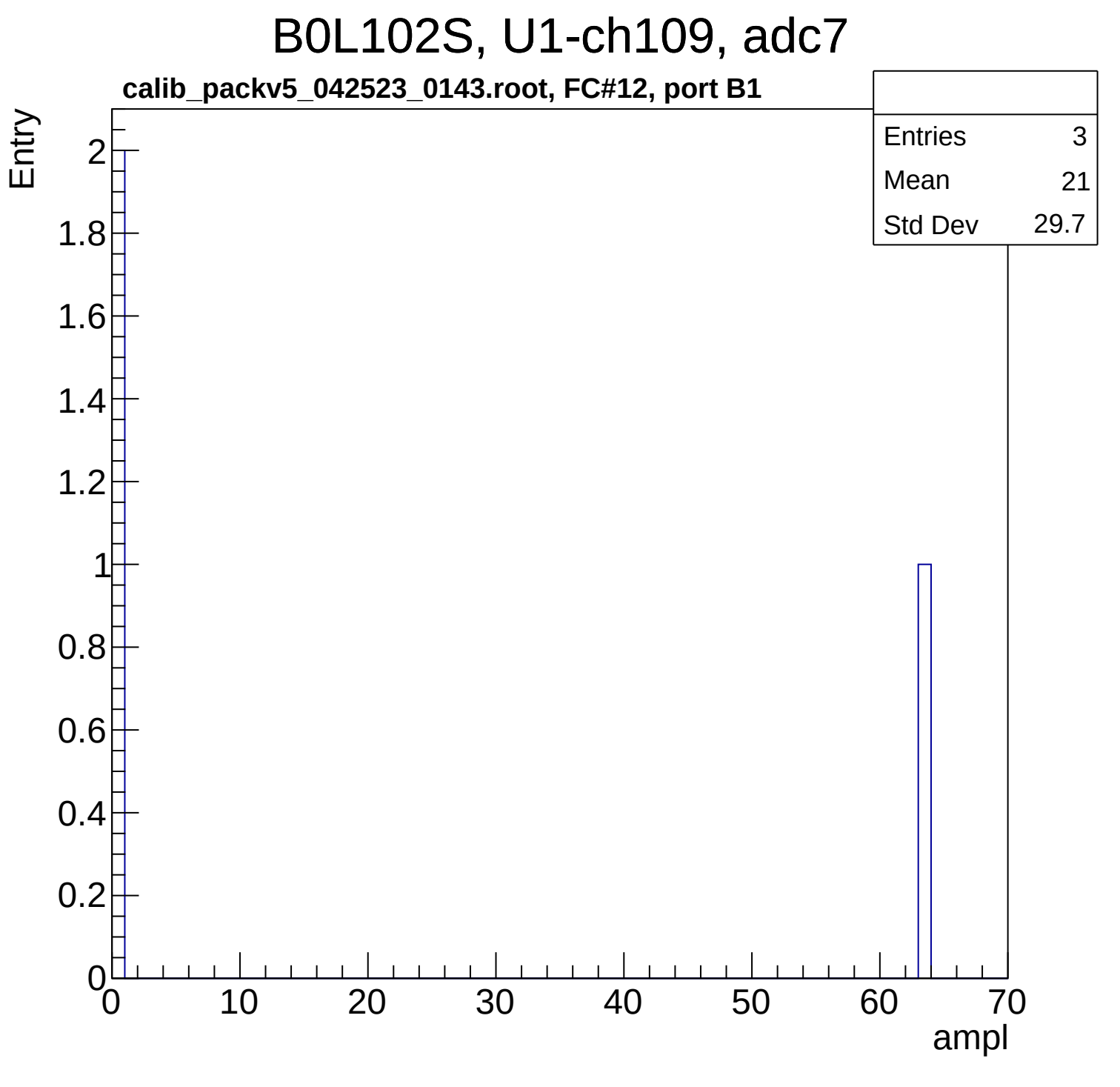
B0L102S, U1-ch109, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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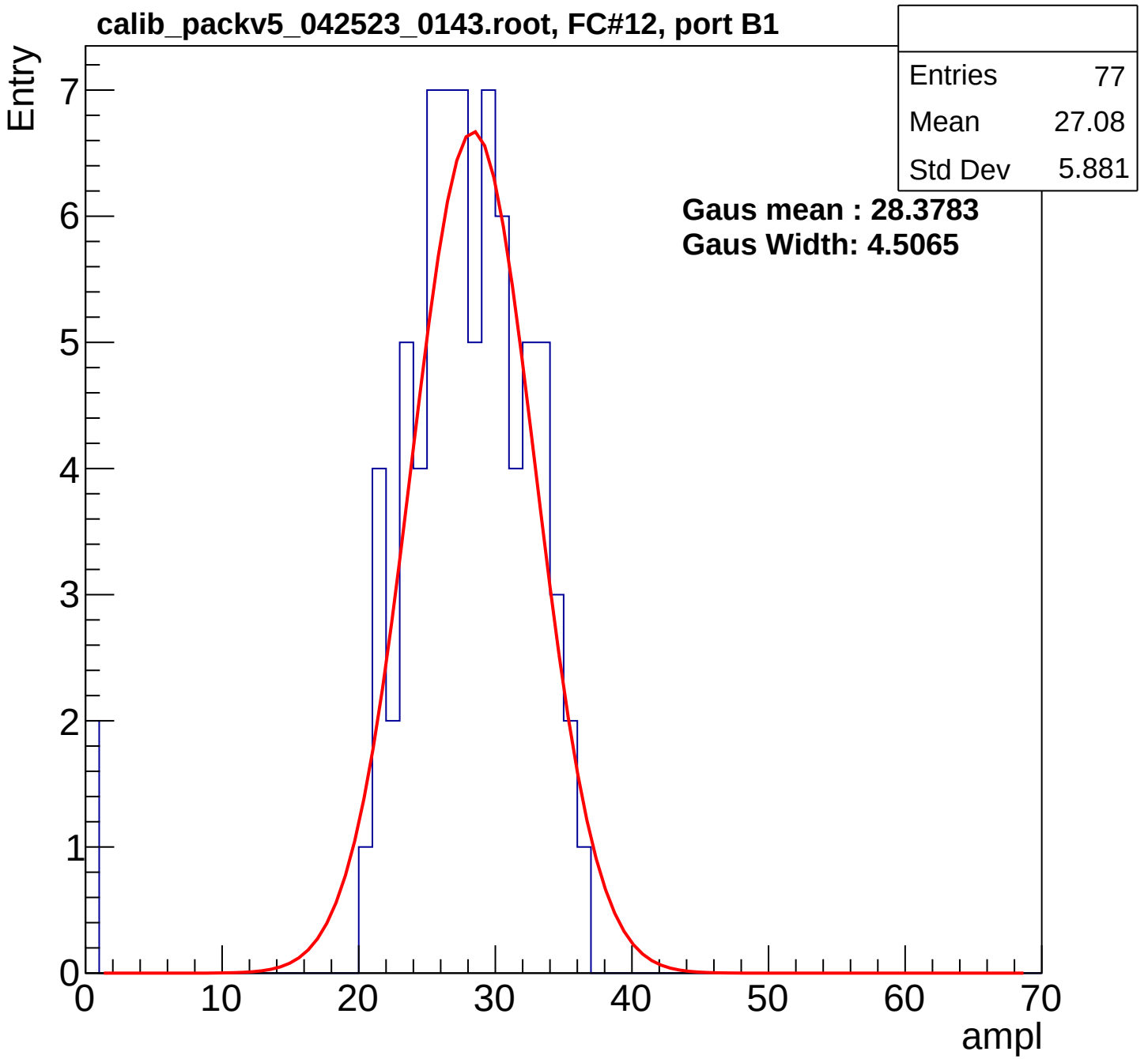
B0L102S, U1-ch110, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 27.08
Std Dev 5.881
Gaus mean : 28.3783
Gaus Width: 4.5065
Entry
ampl
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1
2
3
4
5
6
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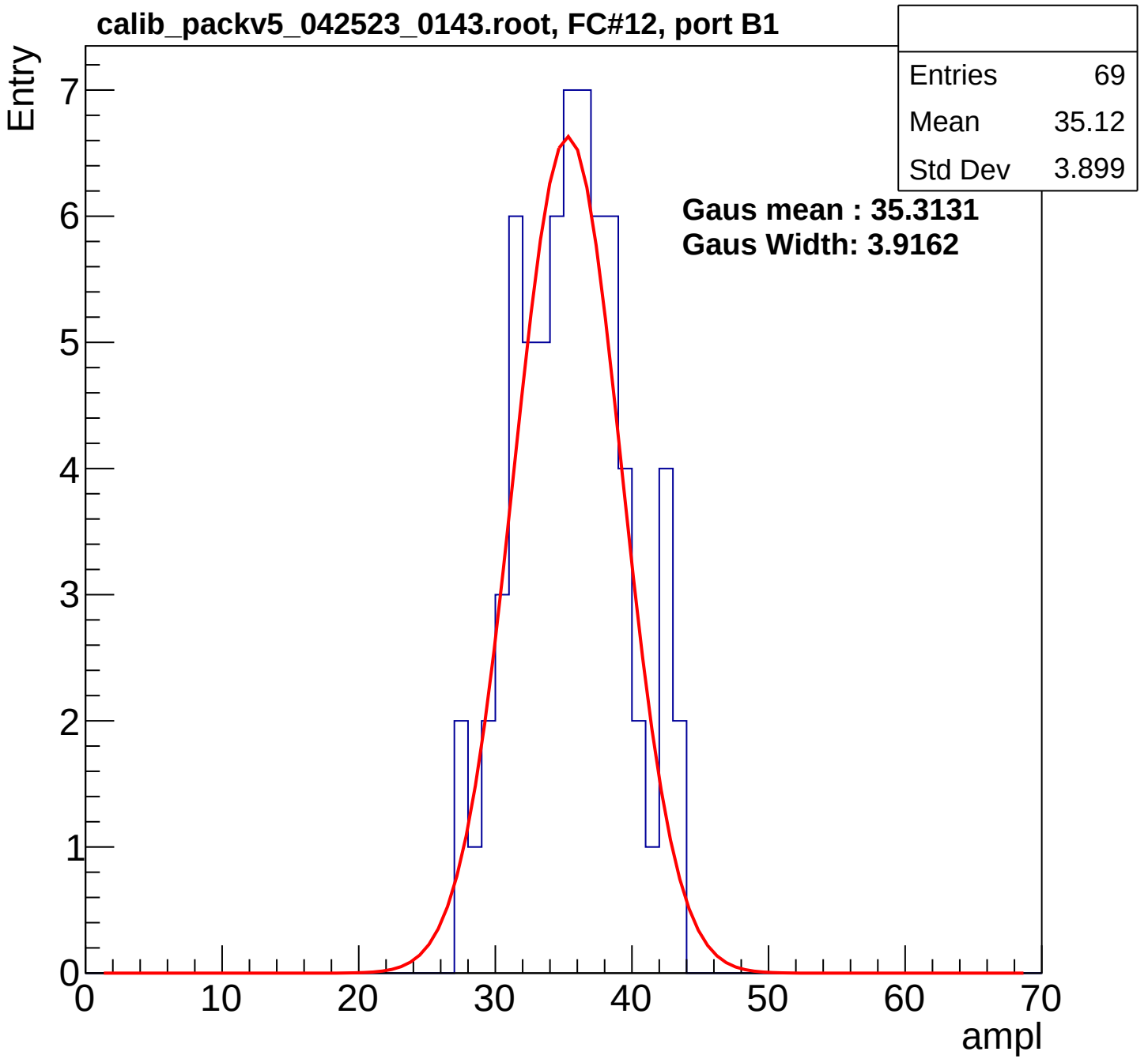
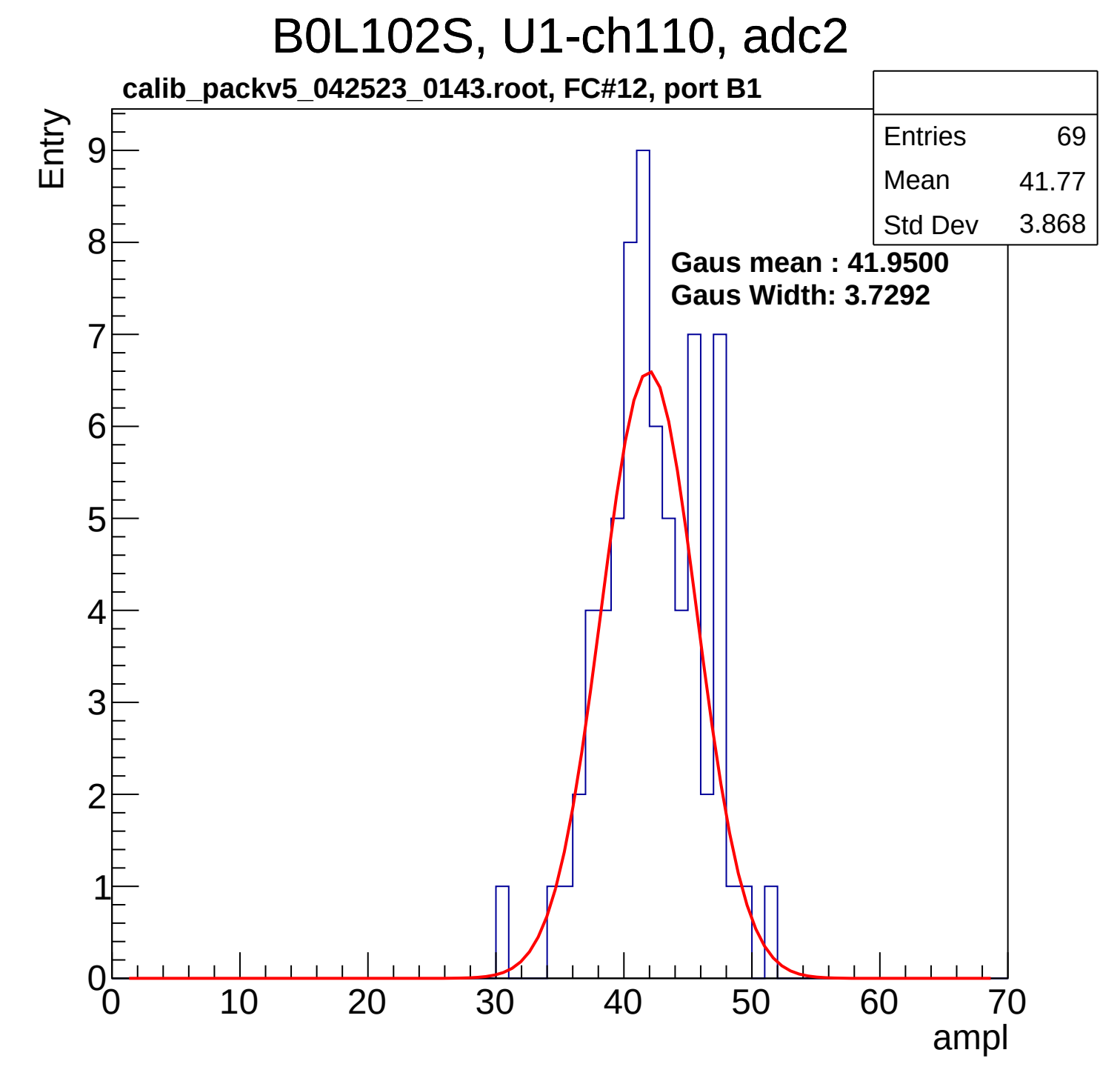
0
10
20
30
40
50
60
70


B0L102S, U1-ch110, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 35.12
Std Dev 3.899
Gaus mean : 35.3131
Gaus Width: 3.9162
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch110, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 41.77
Std Dev 3.868
Gaus mean : 41.9500
Gaus Width: 3.7292
Entry
ampl
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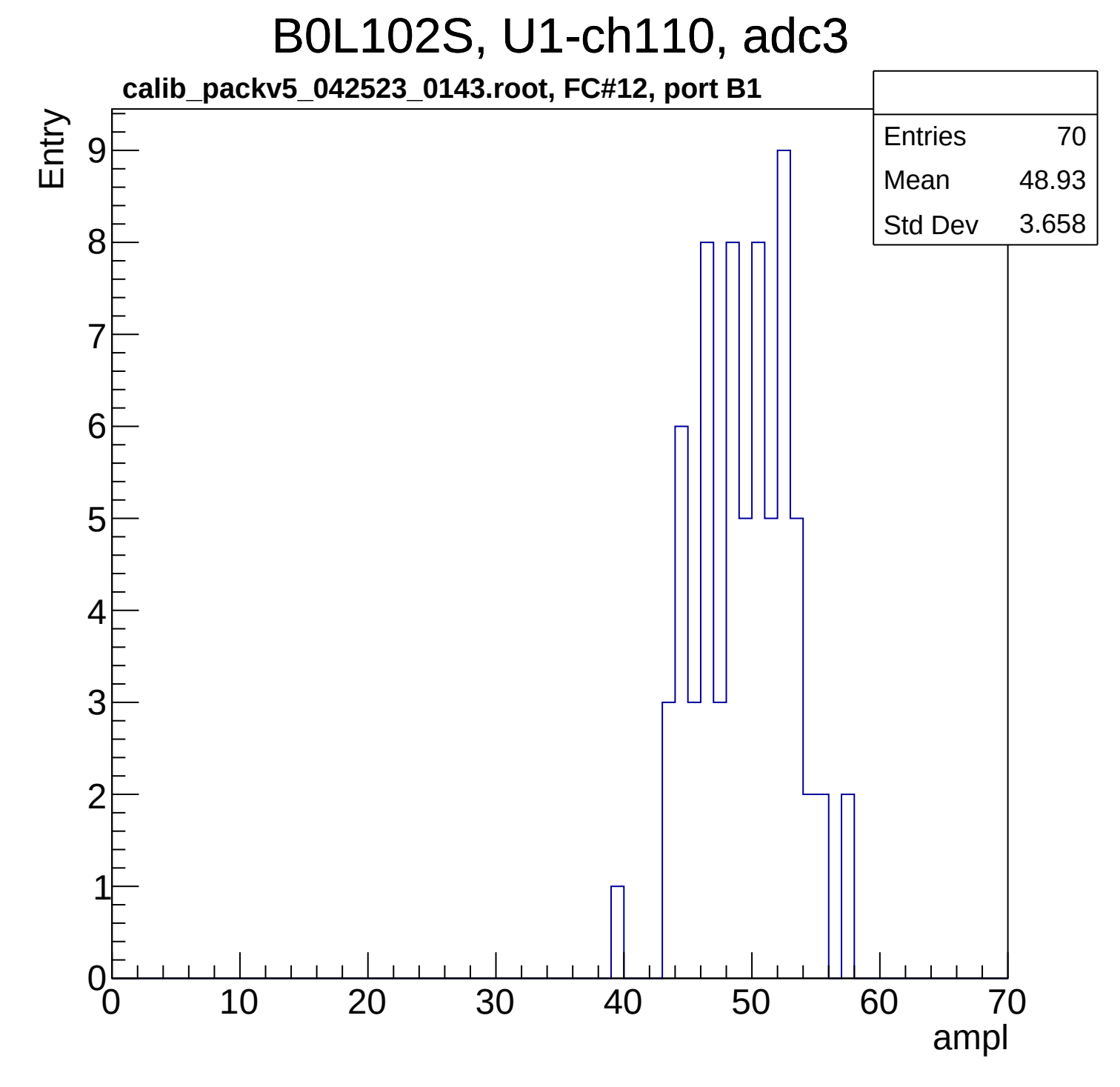
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch110, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 48.93
Std Dev 3.658
Entry
ampl
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2
3
4
5
6
7
8
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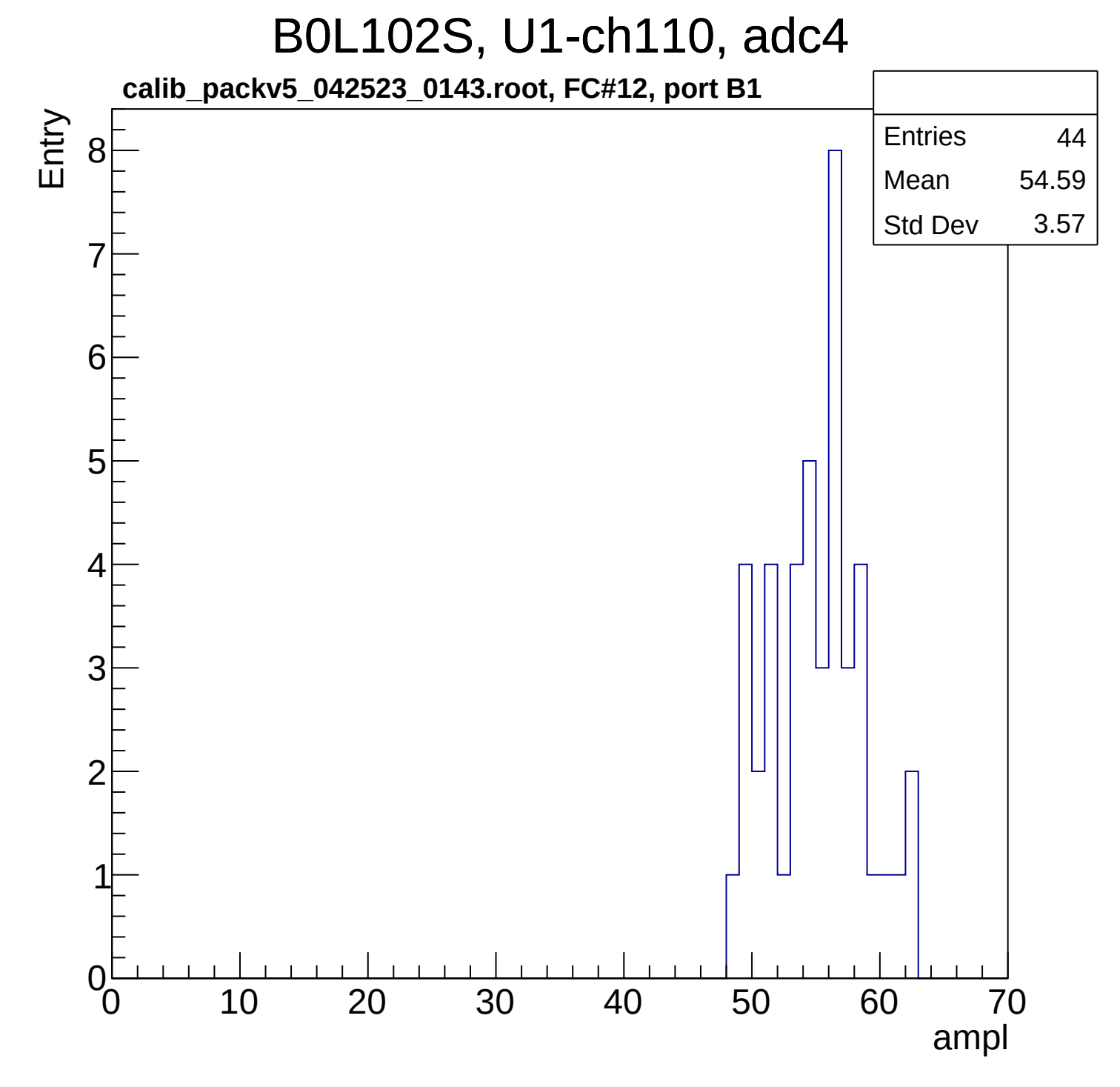
B0L102S, U1-ch110, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 54.59
Std Dev 3.57
Entry
ampl
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2
3
4
5
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0
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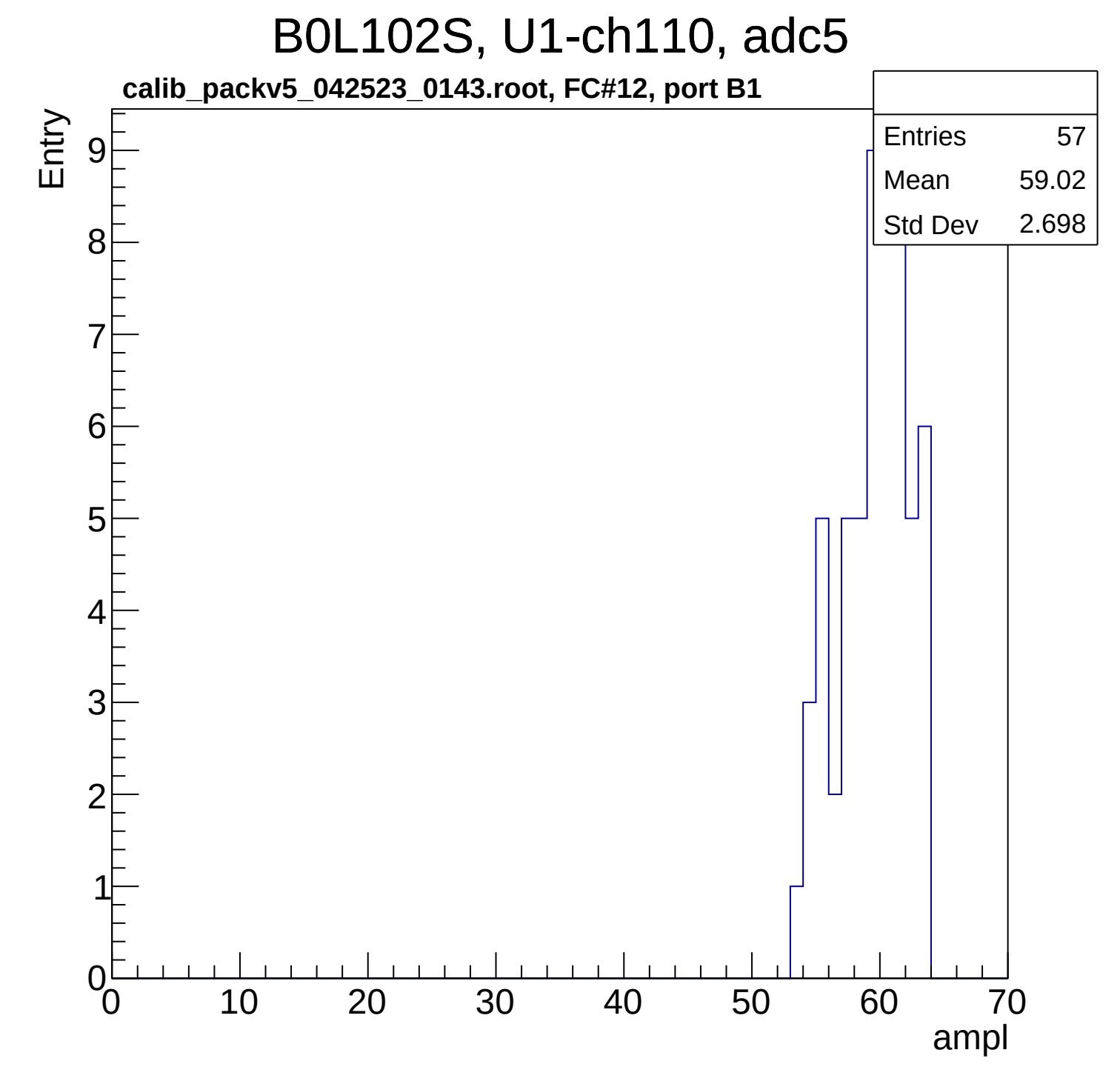
B0L102S, U1-ch110, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 59.02
Std Dev 2.698
Entry
ampl
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2
3
4
5
6
7
8
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0
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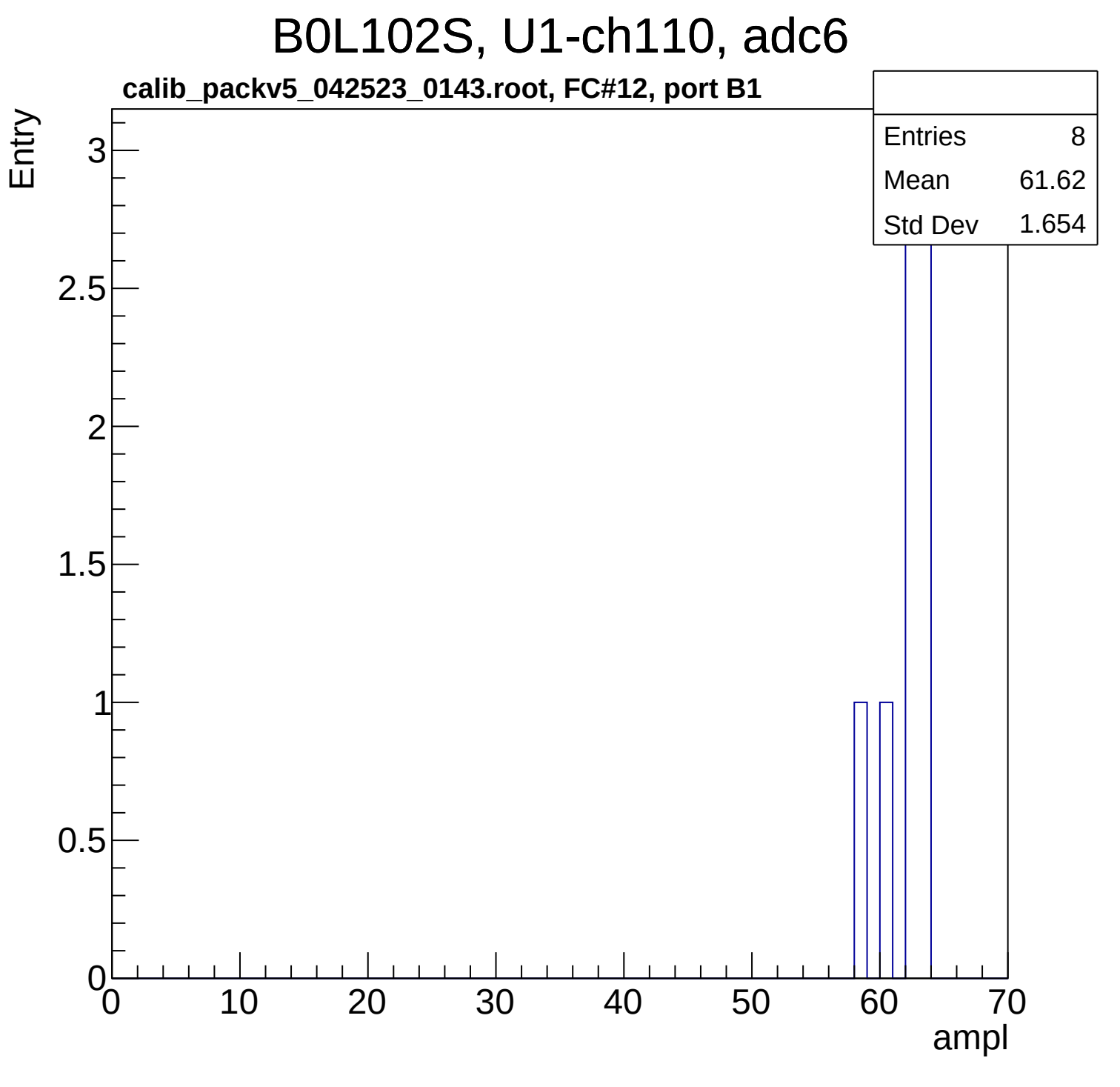
B0L102S, U1-ch110, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.62
Std Dev 1.654
Entry
ampl
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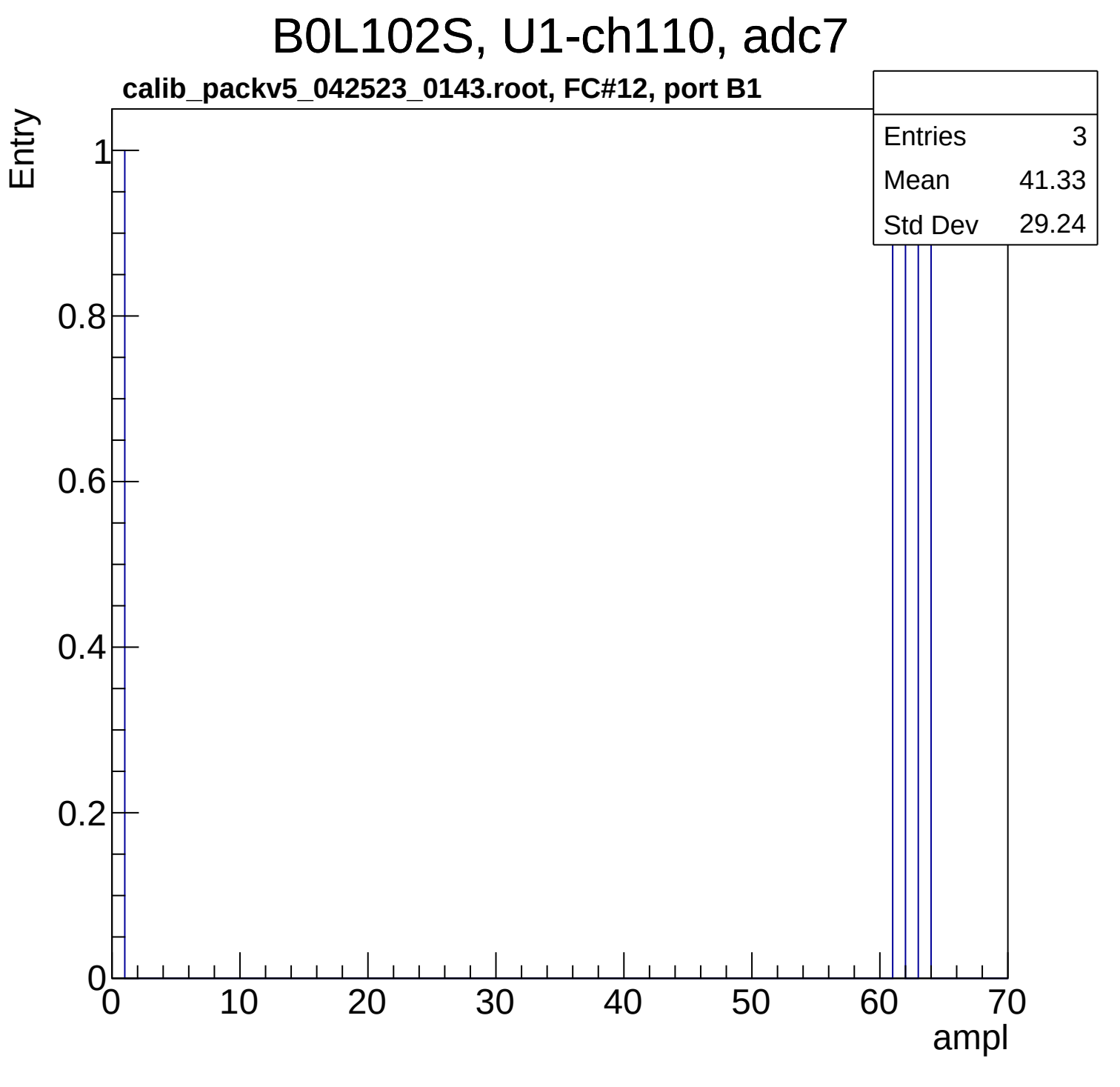
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch110, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 41.33
Std Dev 29.24
Entry
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0.6
0.4
0.2
0
0
10
20
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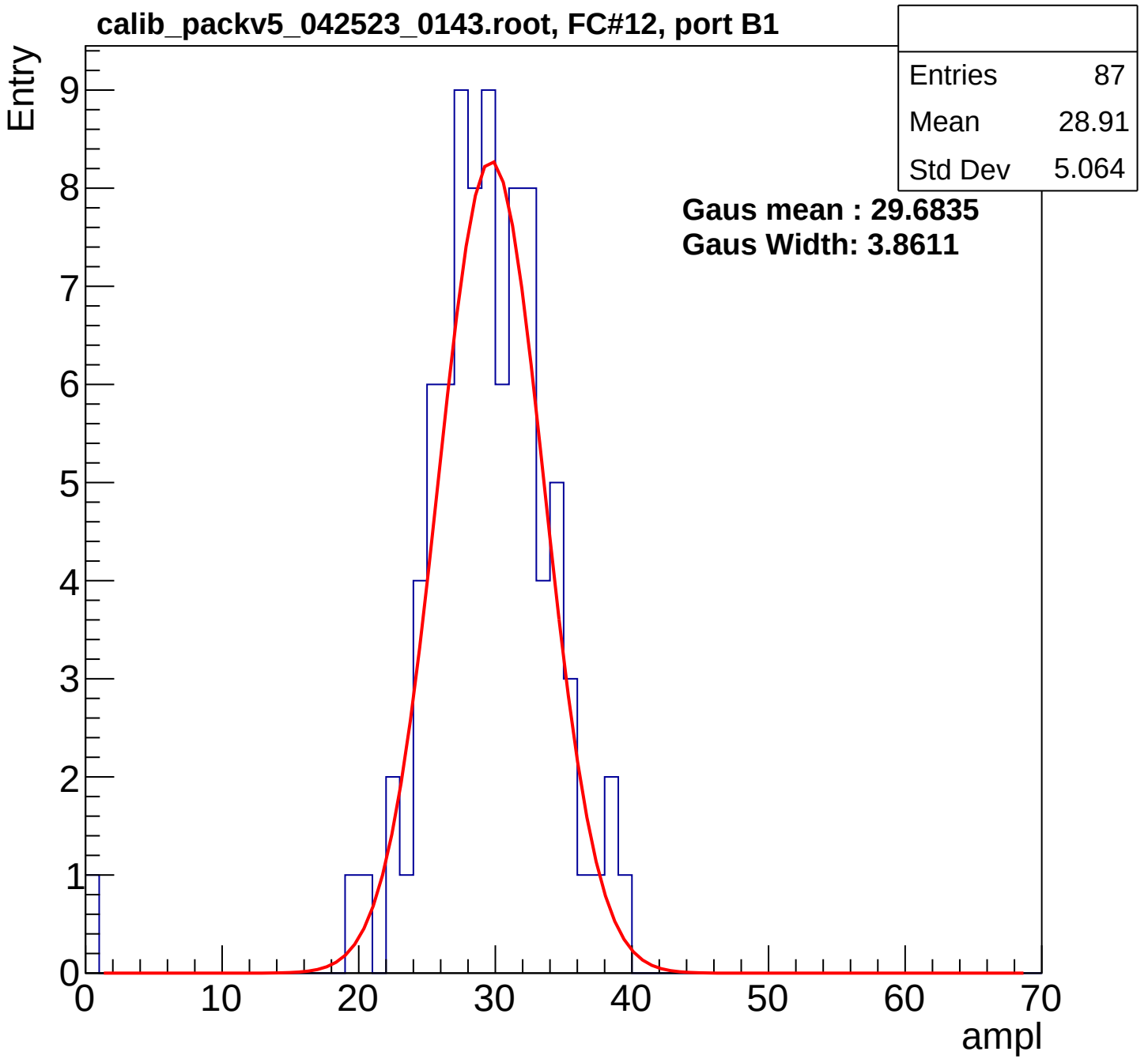
50
60
70
ampl


B0L102S, U1-ch111, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 28.91
Std Dev 5.064
Gaus mean : 29.6835
Gaus Width: 3.8611
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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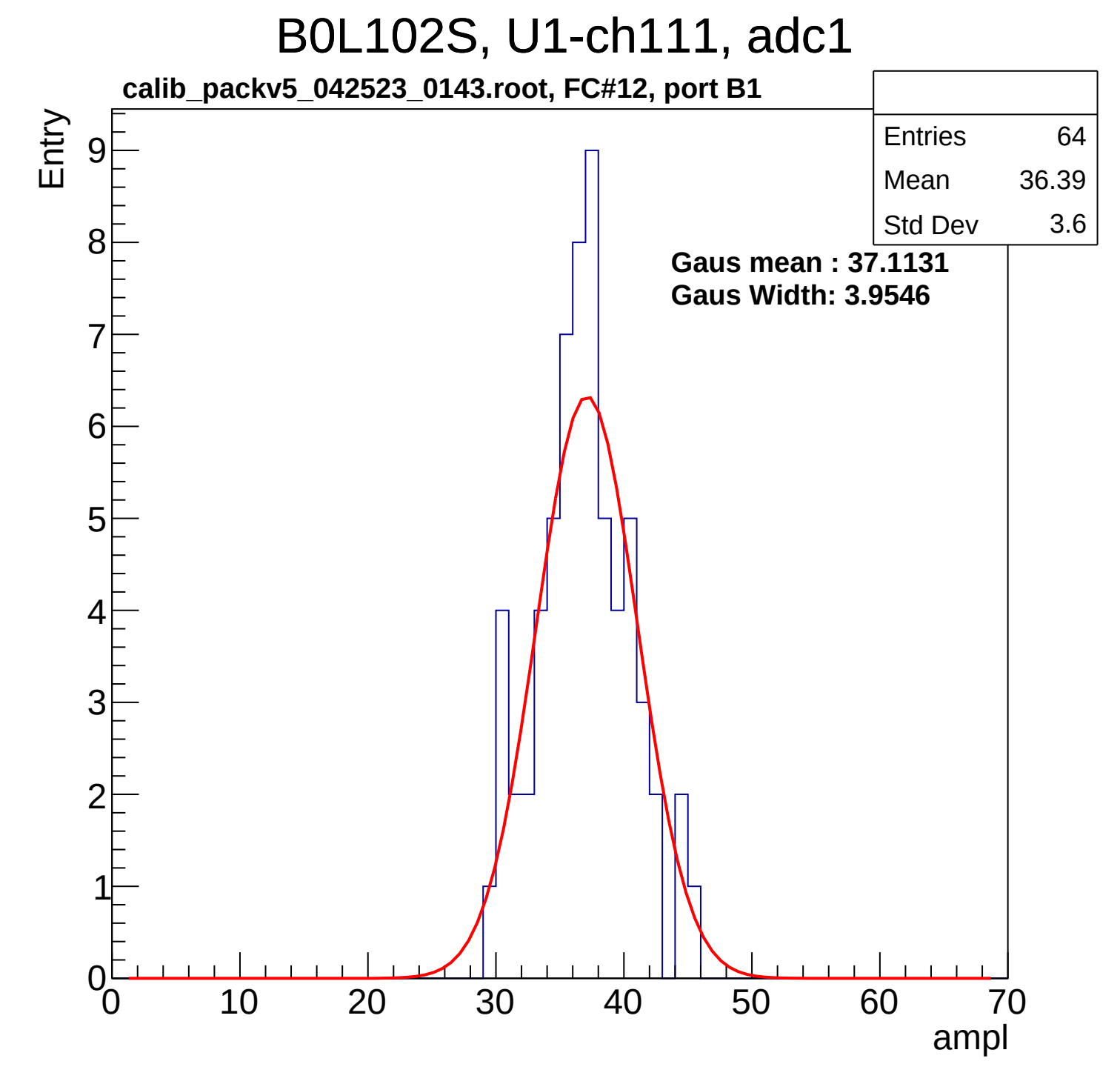
B0L102S, U1-ch111, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.39
Std Dev 3.6
Gaus mean : 37.1131
Gaus Width: 3.9546
Entry
ampl
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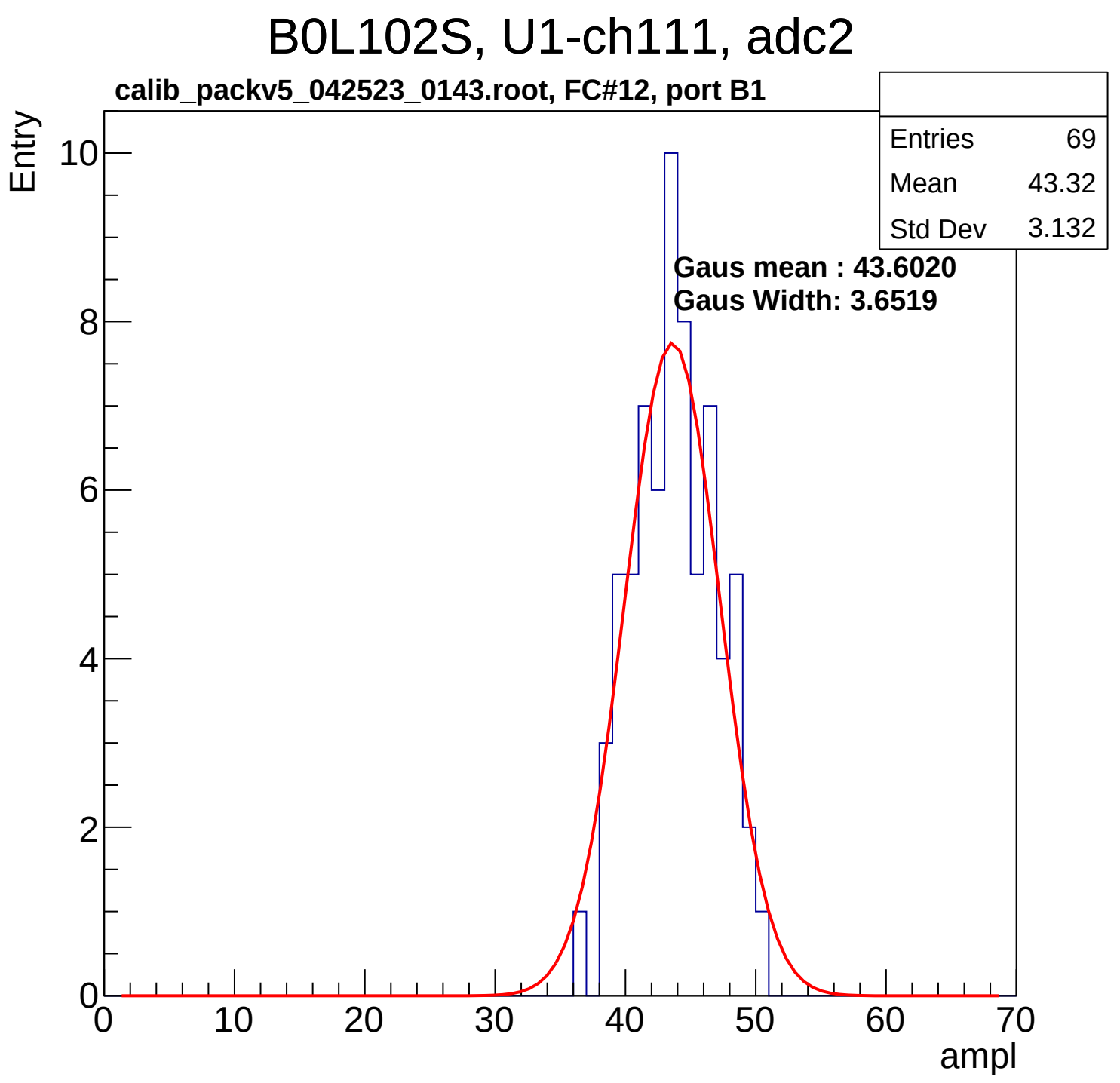
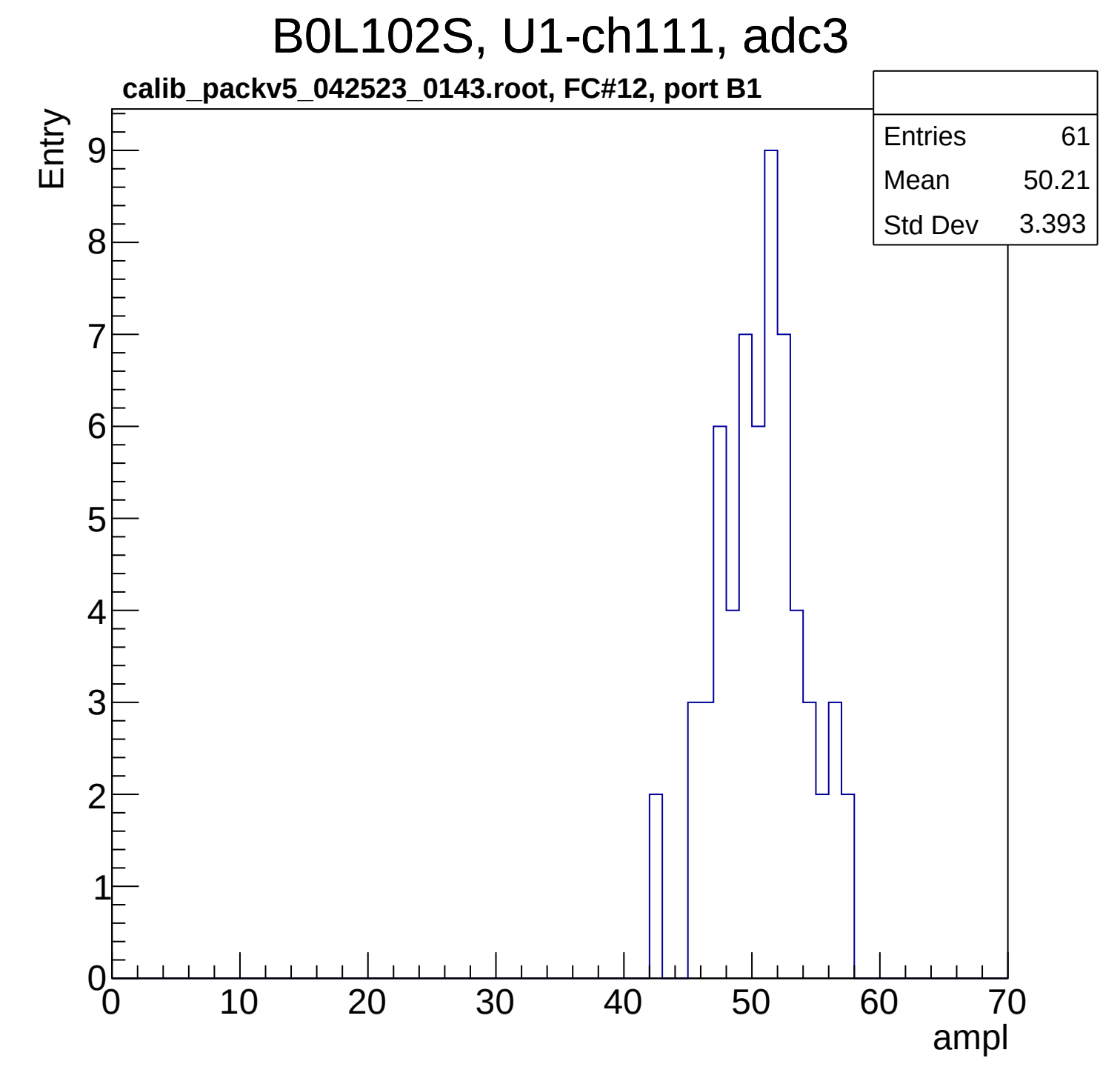
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch111, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 43.32
Std Dev 3.132
Gaus mean : 43.6020
Gaus Width: 3.6519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch111, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 50.21
Std Dev 3.393
Entry
ampl
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2
3
4
5
6
7
8
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0
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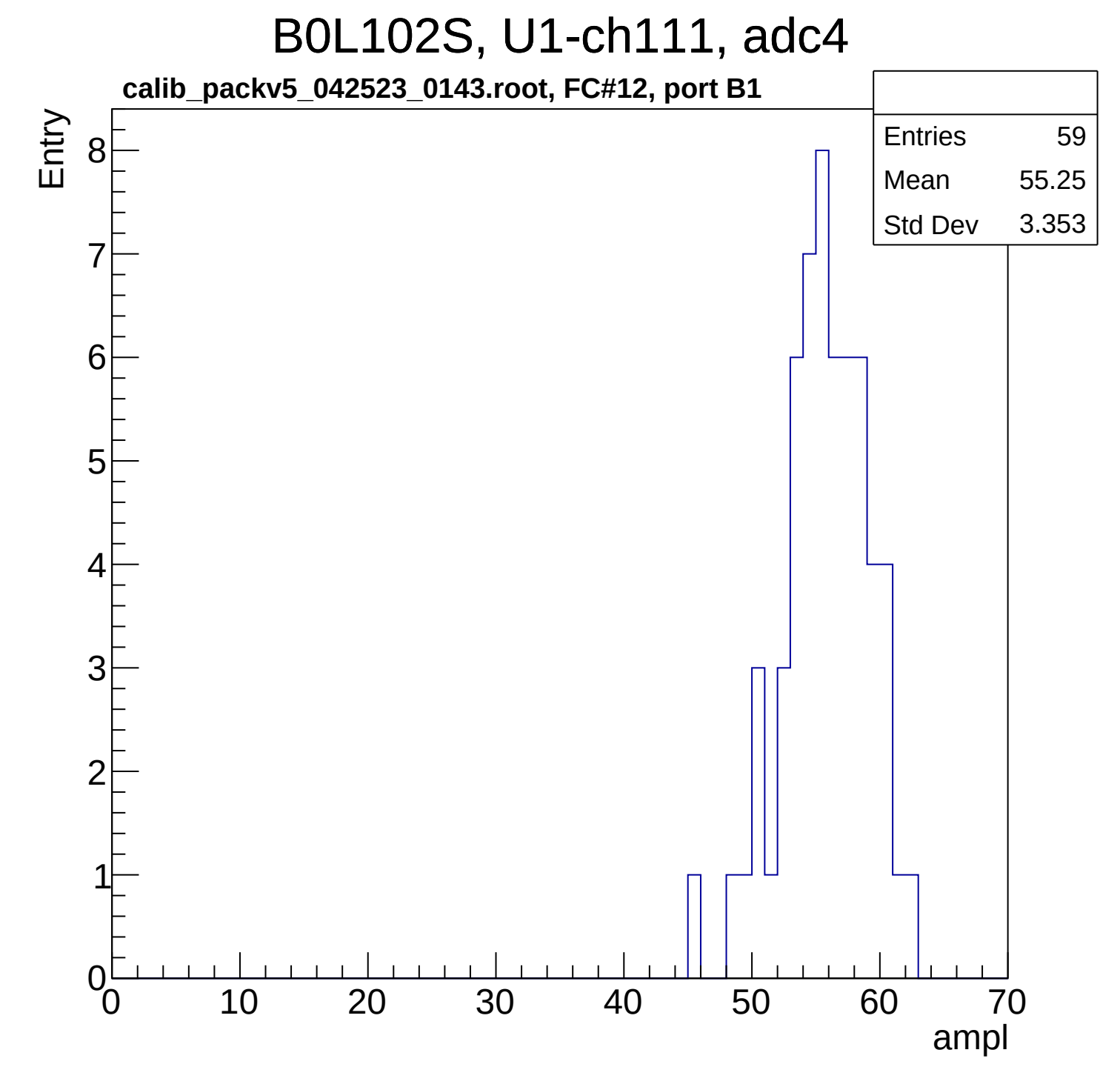
B0L102S, U1-ch111, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.25
Std Dev 3.353
Entry
ampl
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1
2
3
4
5
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0
10
20
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60
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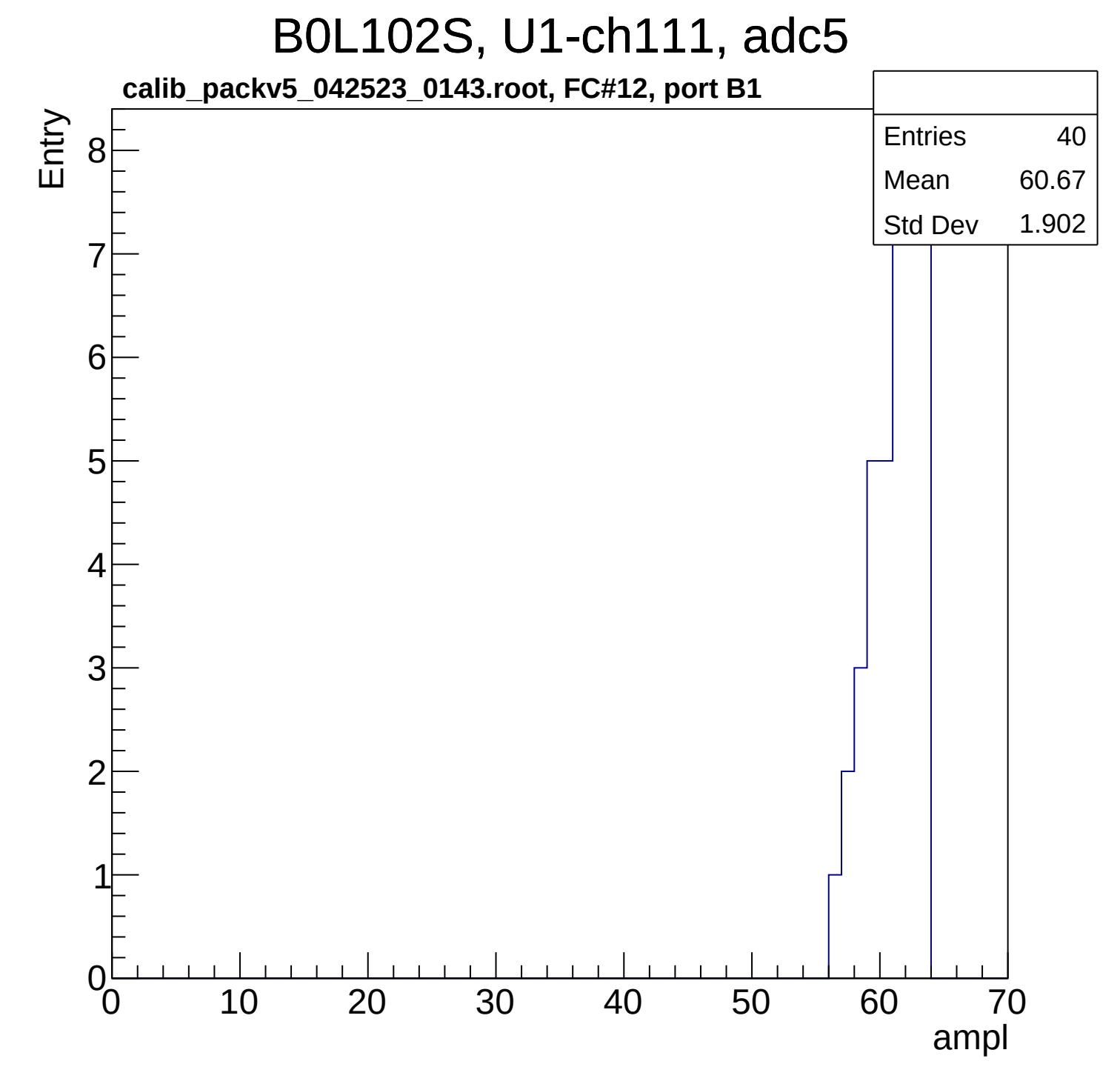
B0L102S, U1-ch111, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 60.67
Std Dev 1.902
Entry
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2
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7
8
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10
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50
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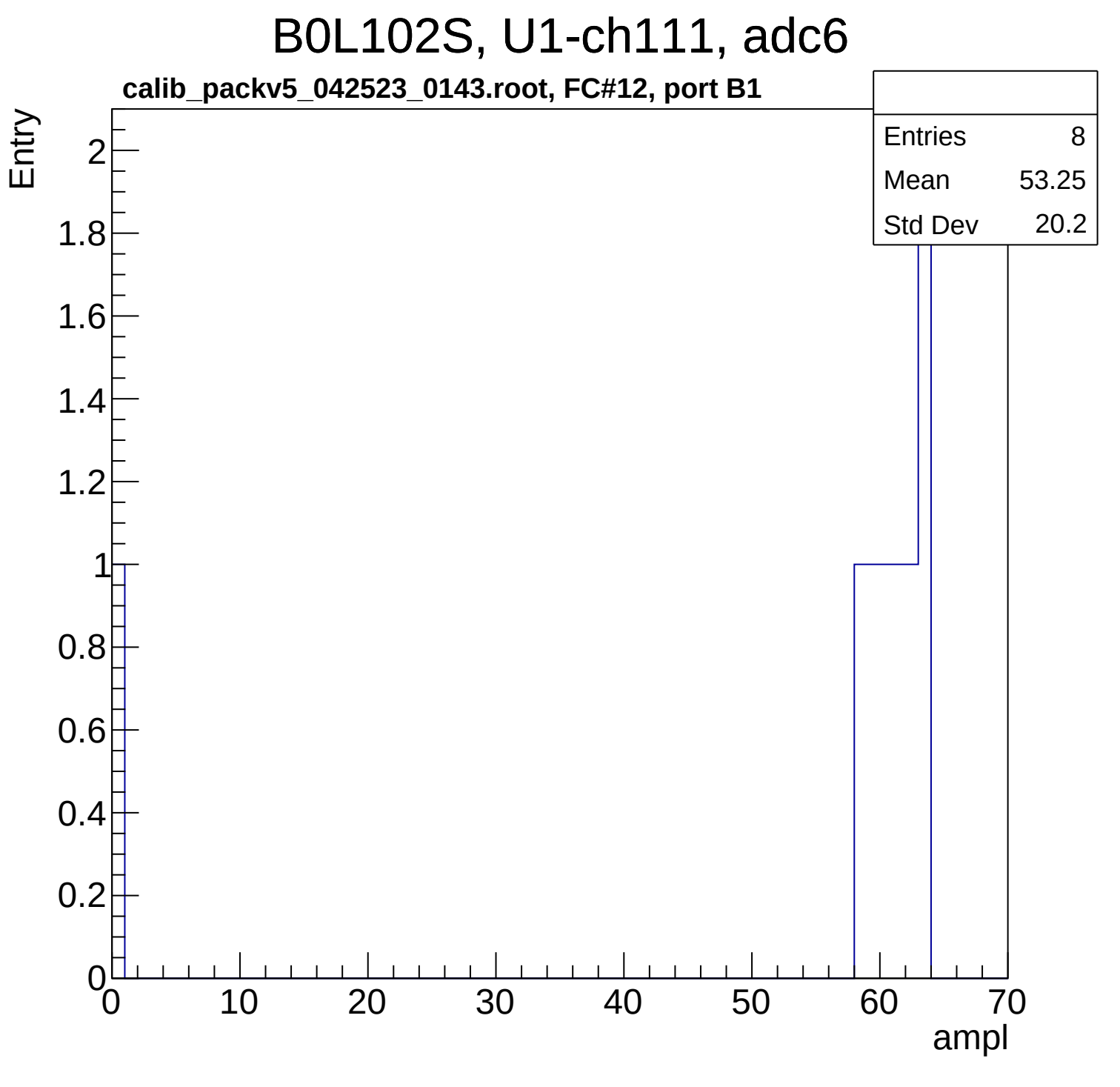
70
ampl


B0L102S, U1-ch111, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 53.25
Std Dev 20.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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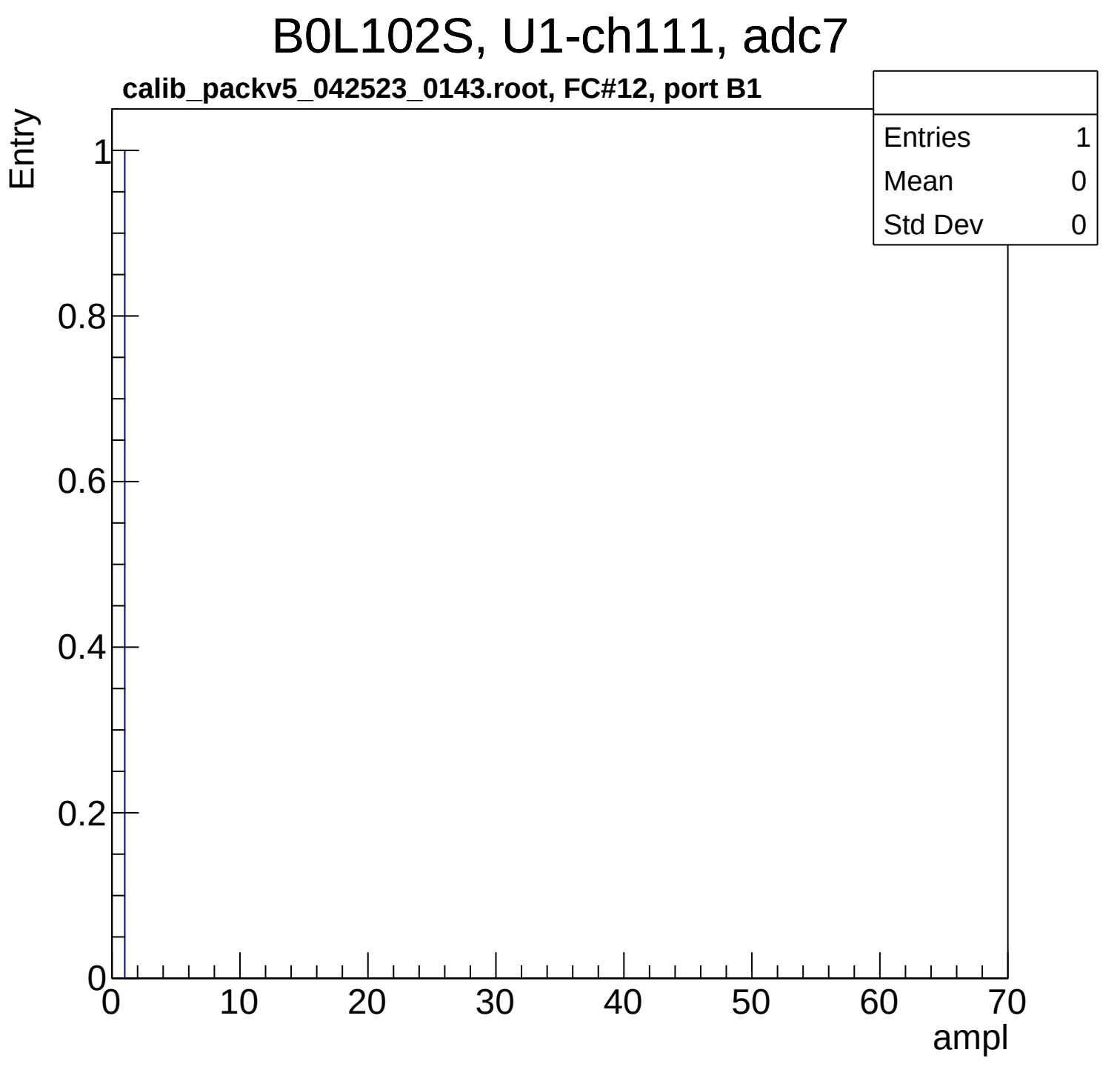
60
70


B0L102S, U1-ch111, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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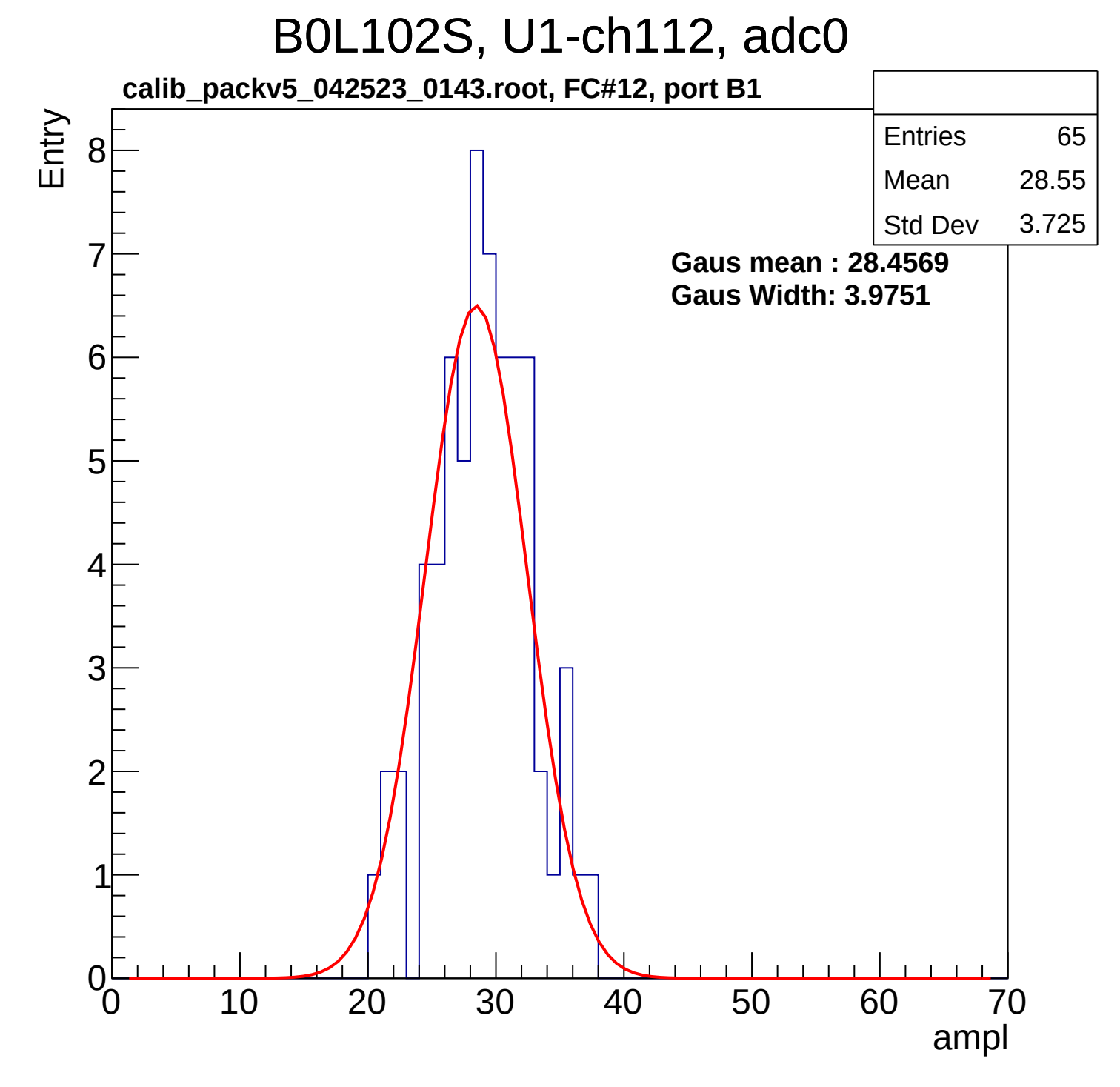
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch112, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 28.55
Std Dev 3.725
Gaus mean : 28.4569
Gaus Width: 3.9751
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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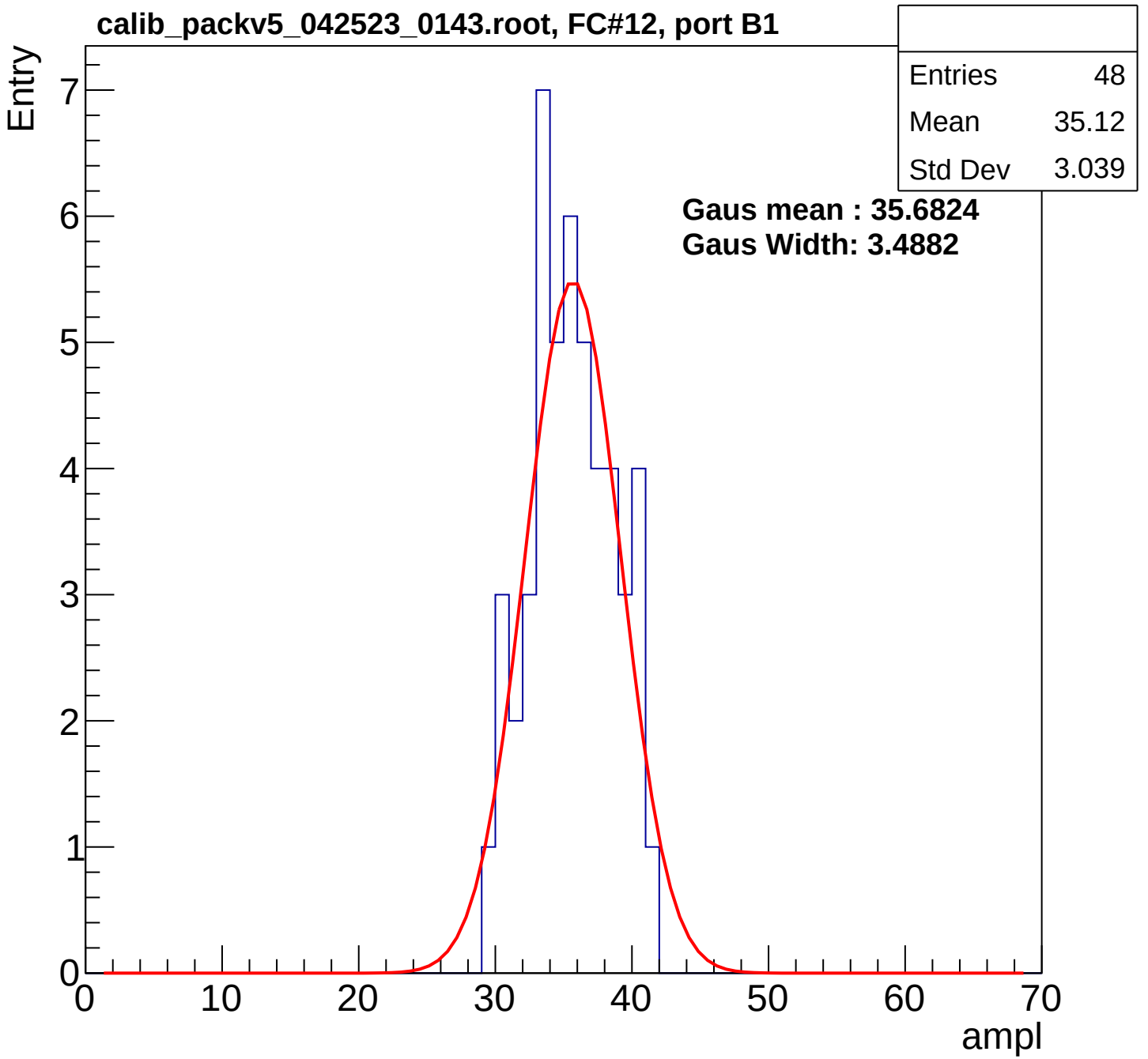
B0L102S, U1-ch112, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 35.12
Std Dev 3.039
Gaus mean : 35.6824
Gaus Width: 3.4882
Entry
ampl
0
1
2
3
4
5
6
7
0
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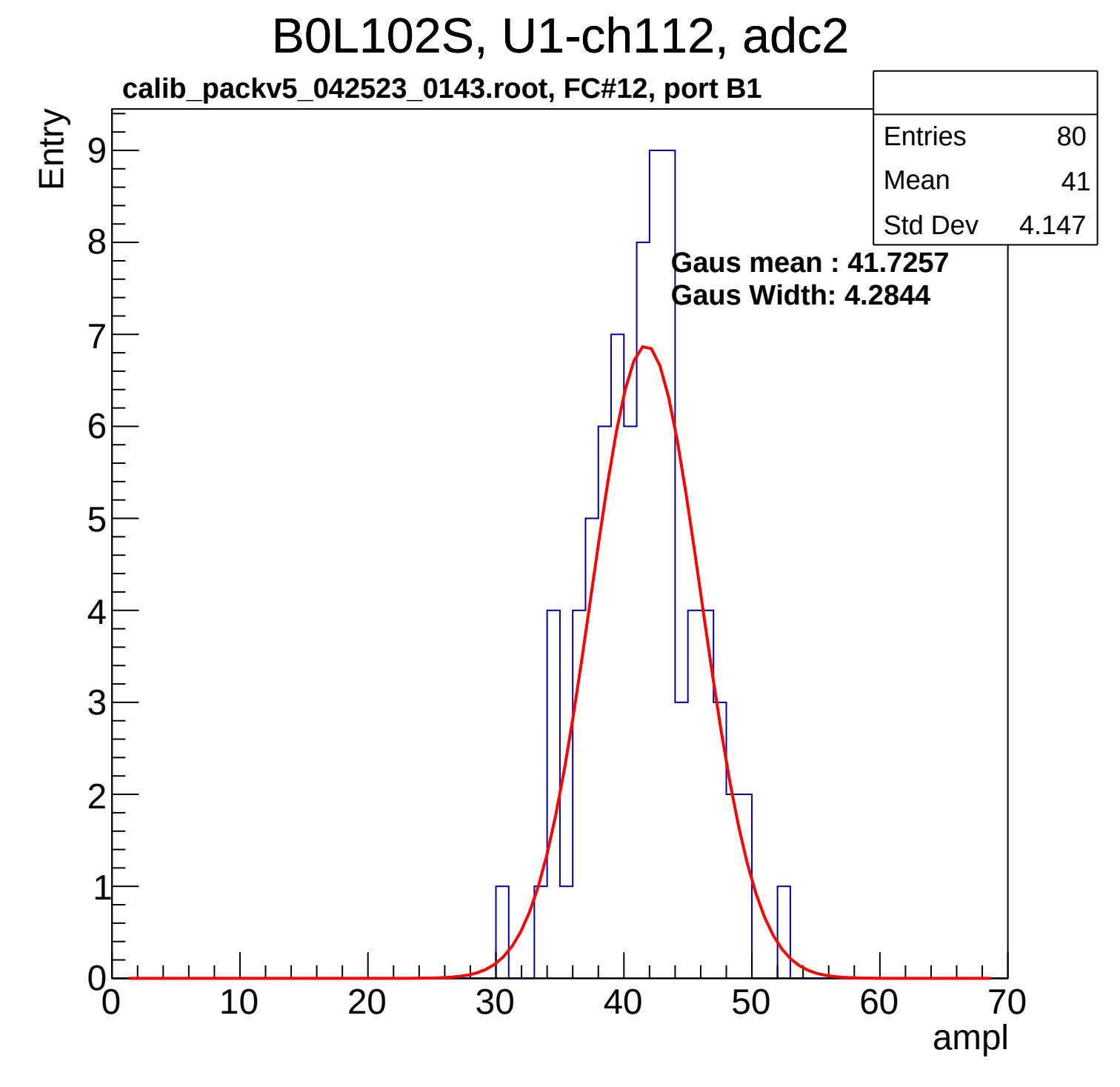
40
50
60
70


B0L102S, U1-ch112, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 41
Std Dev 4.147
Gaus mean : 41.7257
Gaus Width: 4.2844
Entry
ampl
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2
3
4
5
6
7
8
9
0
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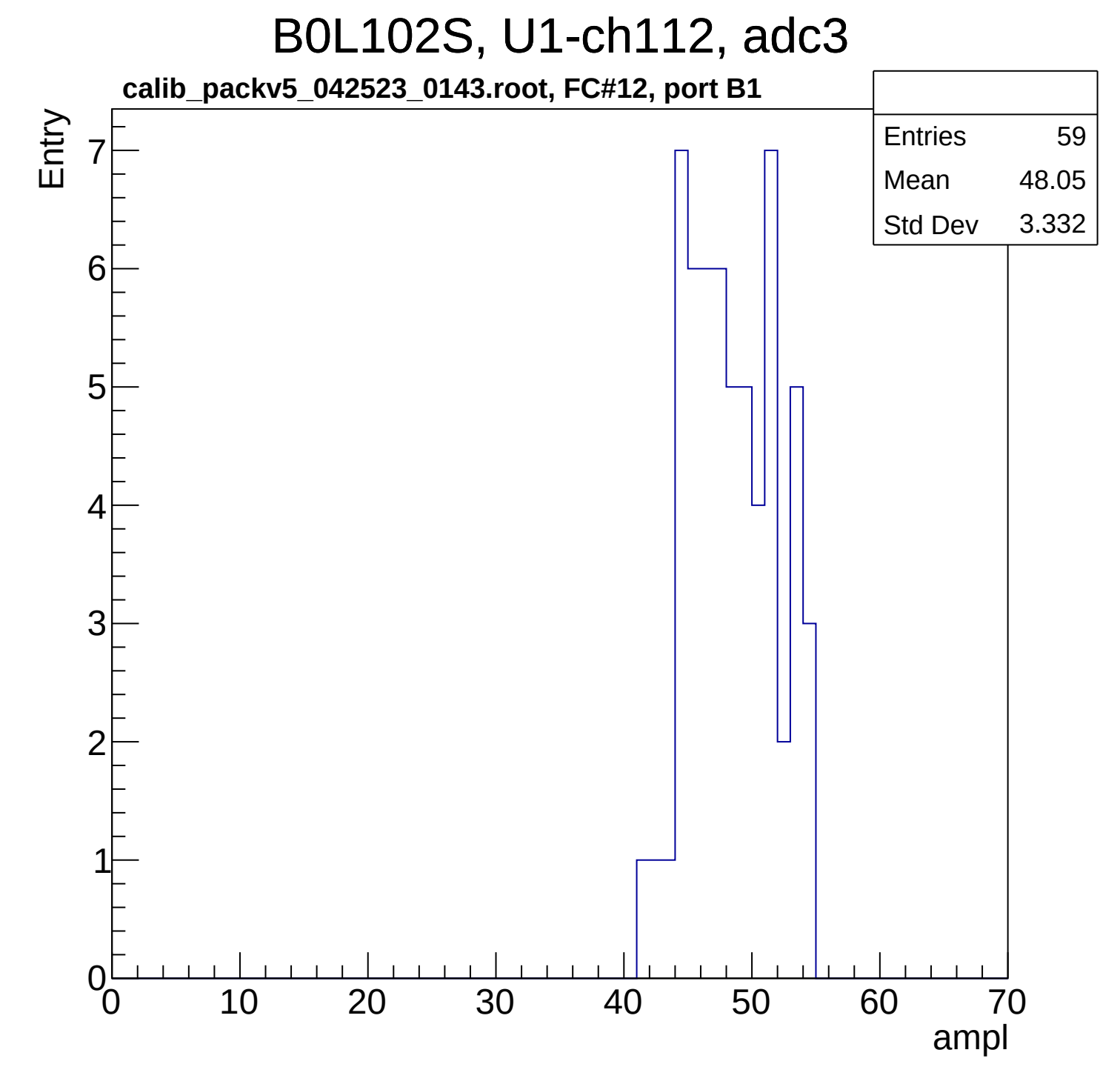
40
50
60
70


B0L102S, U1-ch112, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.05
Std Dev 3.332
Entry
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1
2
3
4
5
6
7
0
10
20
30
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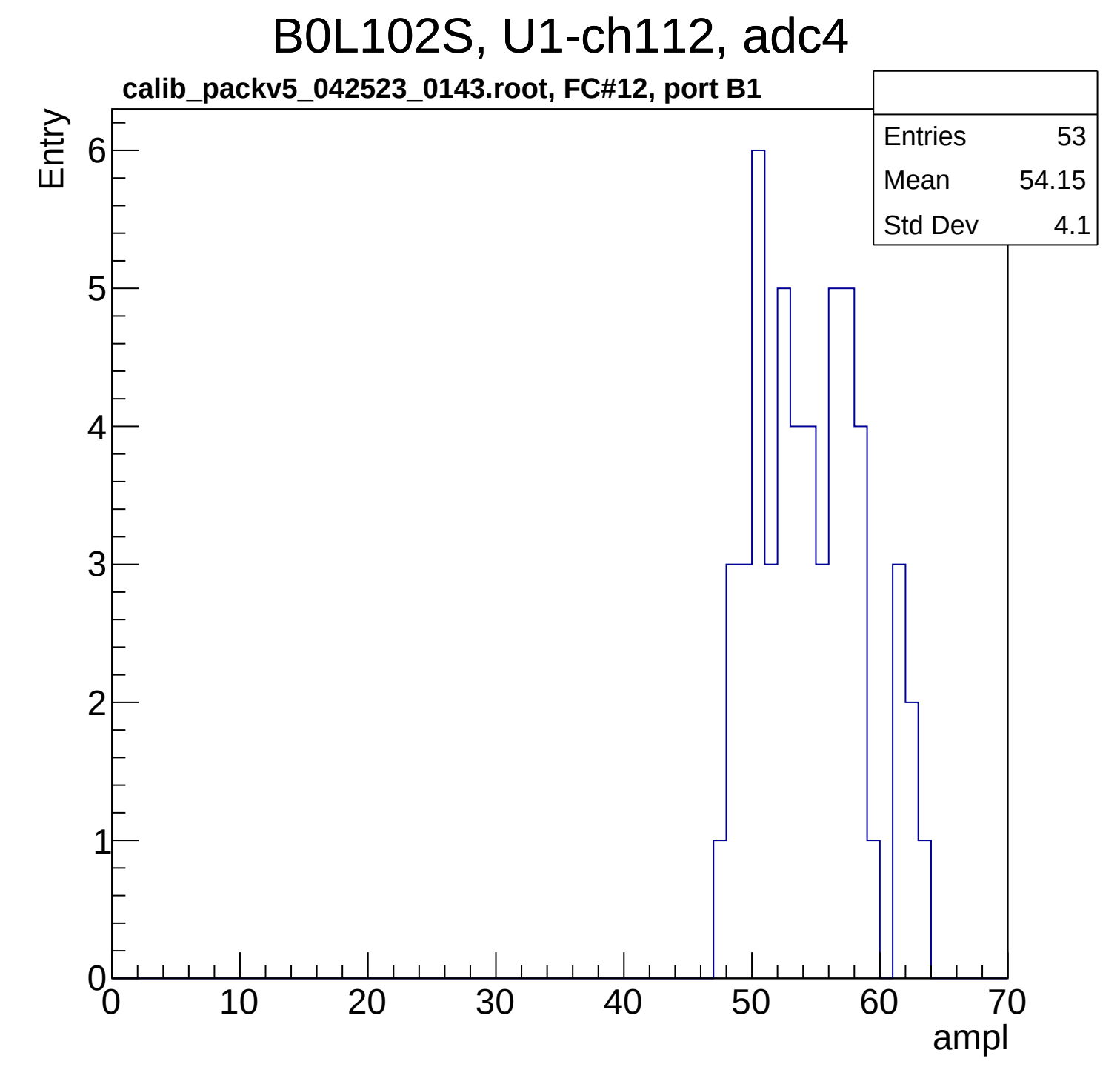
50
60
70
ampl


B0L102S, U1-ch112, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 54.15
Std Dev 4.1
Entry
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2
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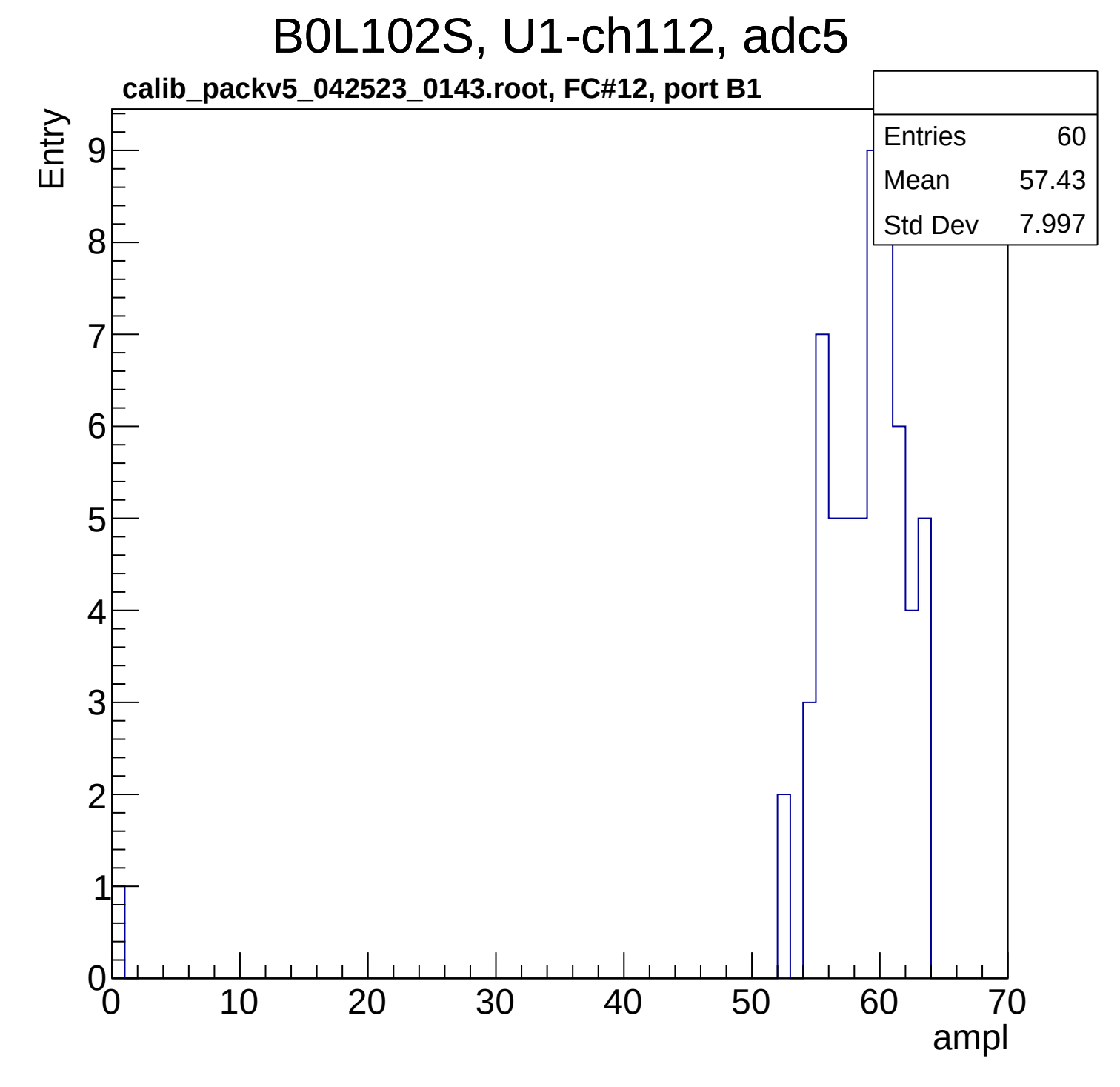
50
60
70
ampl


B0L102S, U1-ch112, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 57.43
Std Dev 7.997
Entry
ampl
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3
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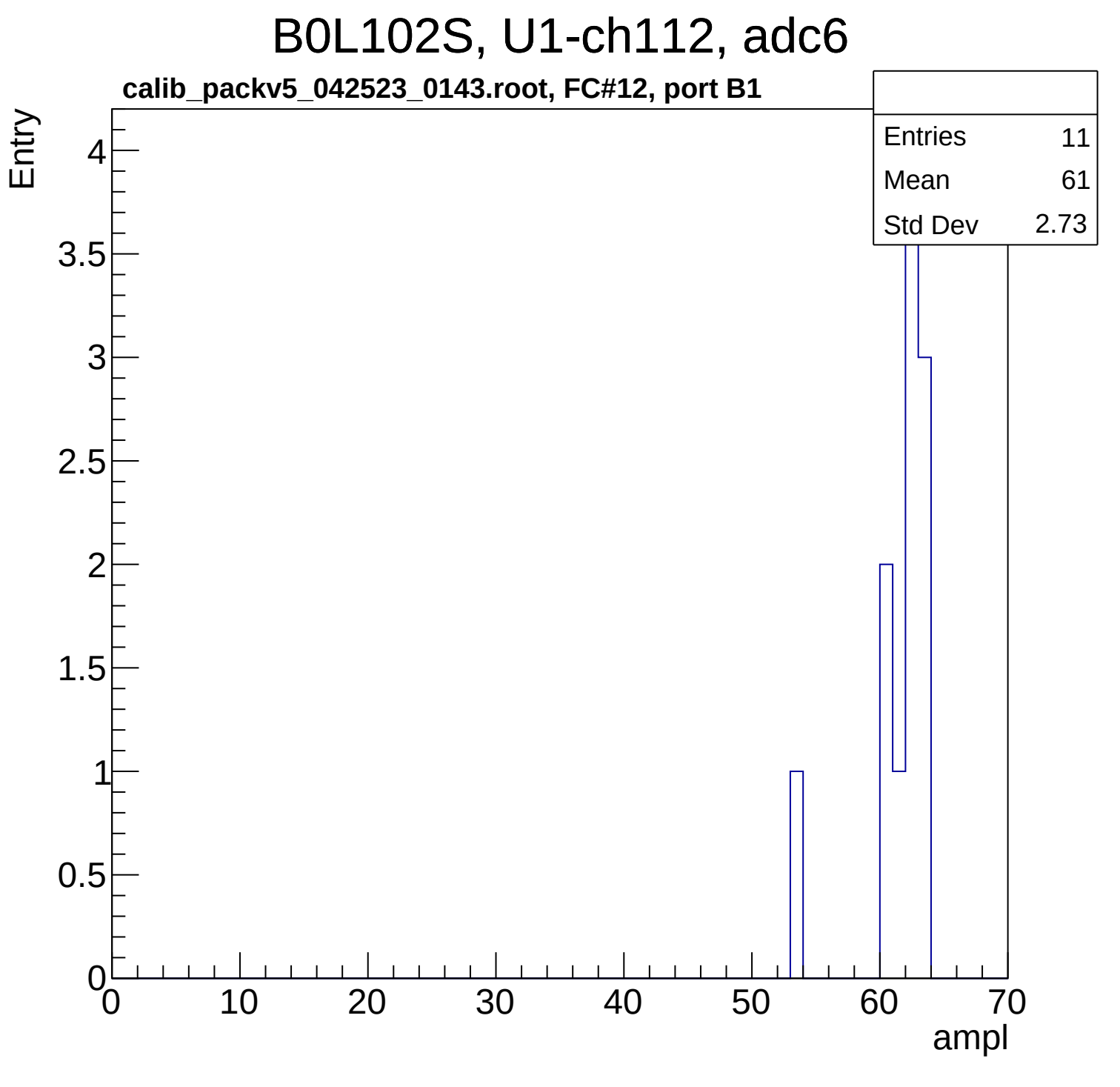
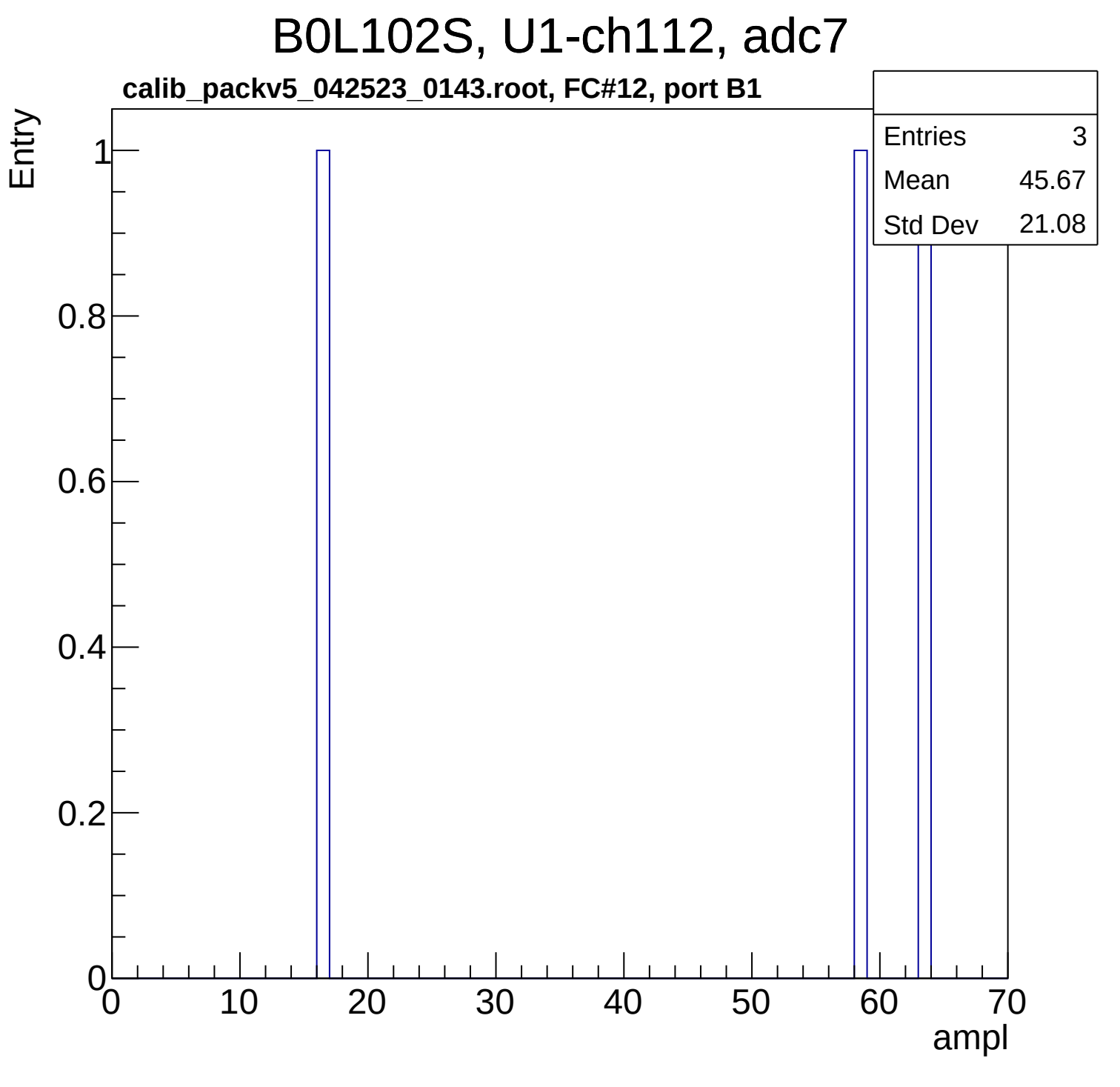

B0L102S, U1-ch112, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61
Std Dev 2.73
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U1-ch112, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 45.67
Std Dev 21.08
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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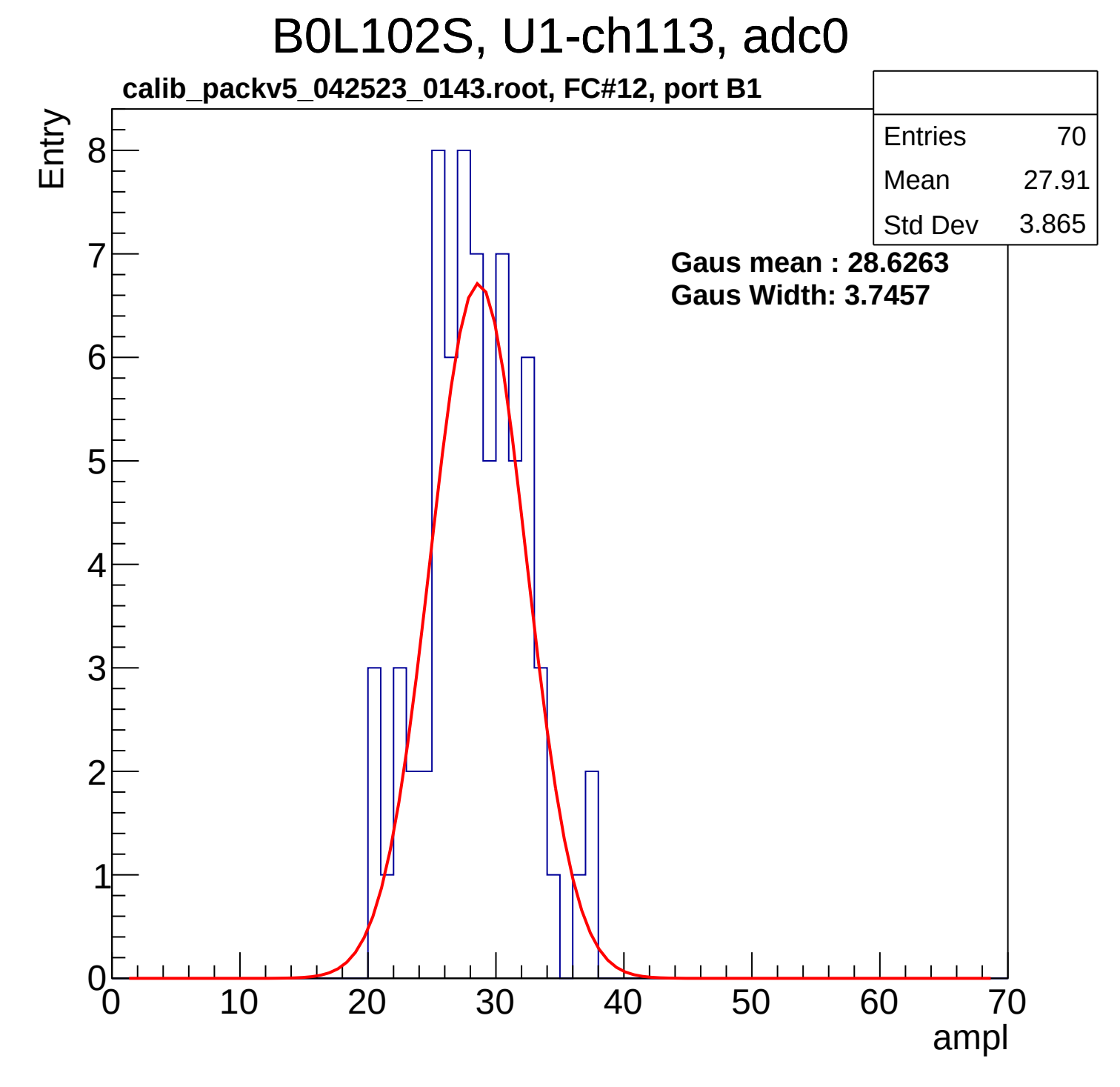
B0L102S, U1-ch113, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 27.91
Std Dev 3.865
Gaus mean : 28.6263
Gaus Width: 3.7457
Entry
ampl
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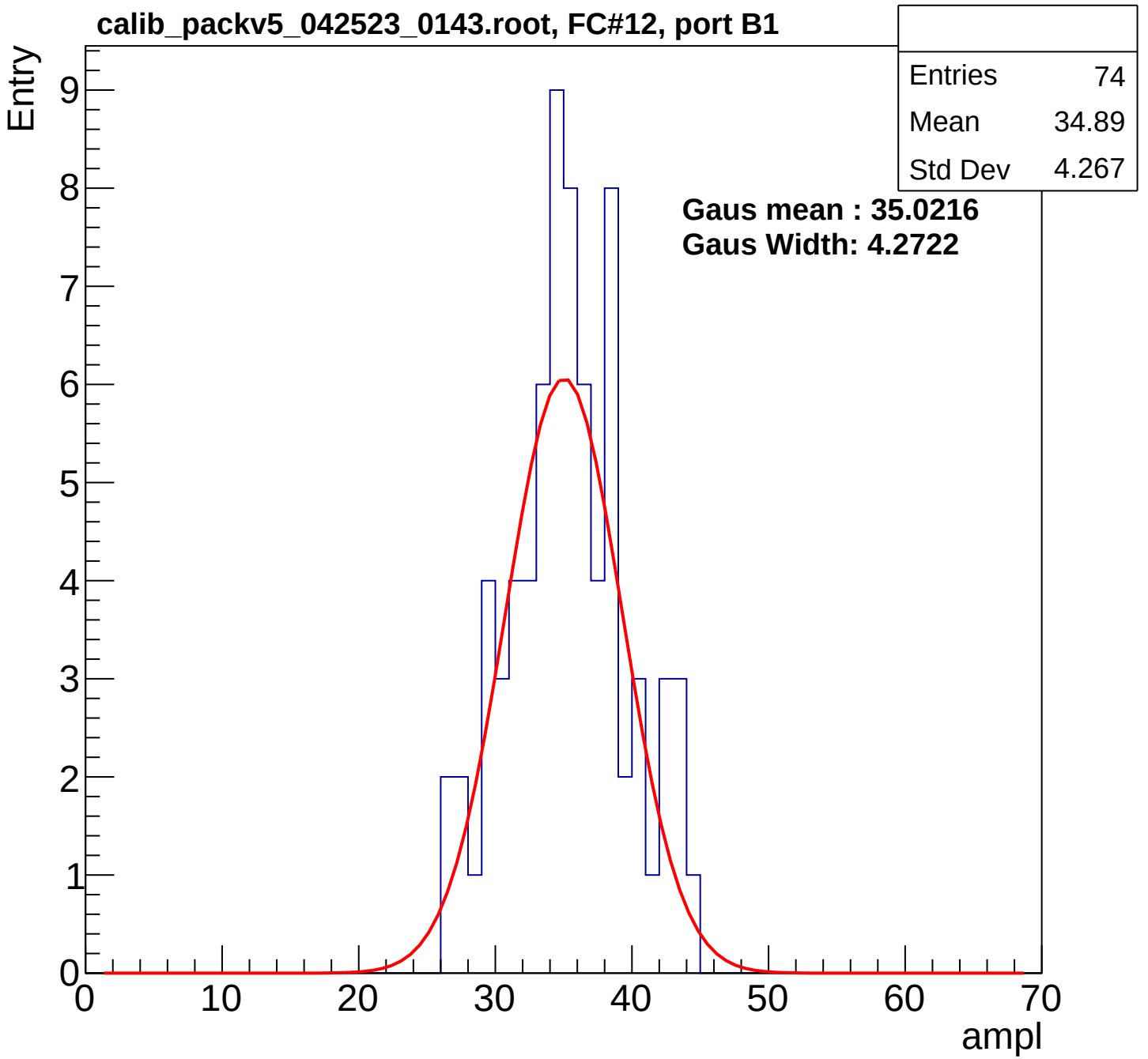
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch113, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 34.89
Std Dev 4.267
Gaus mean : 35.0216
Gaus Width: 4.2722
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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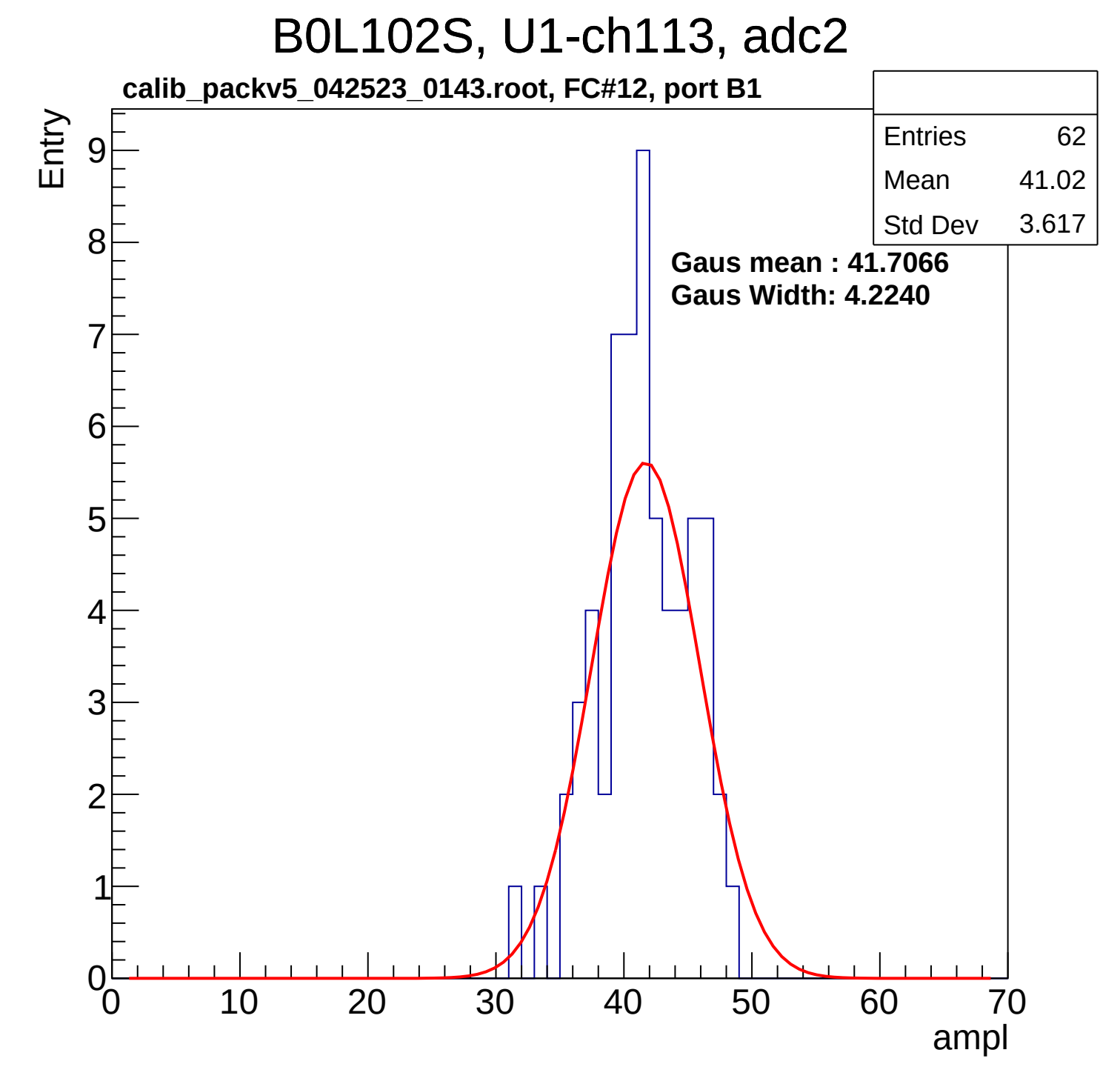
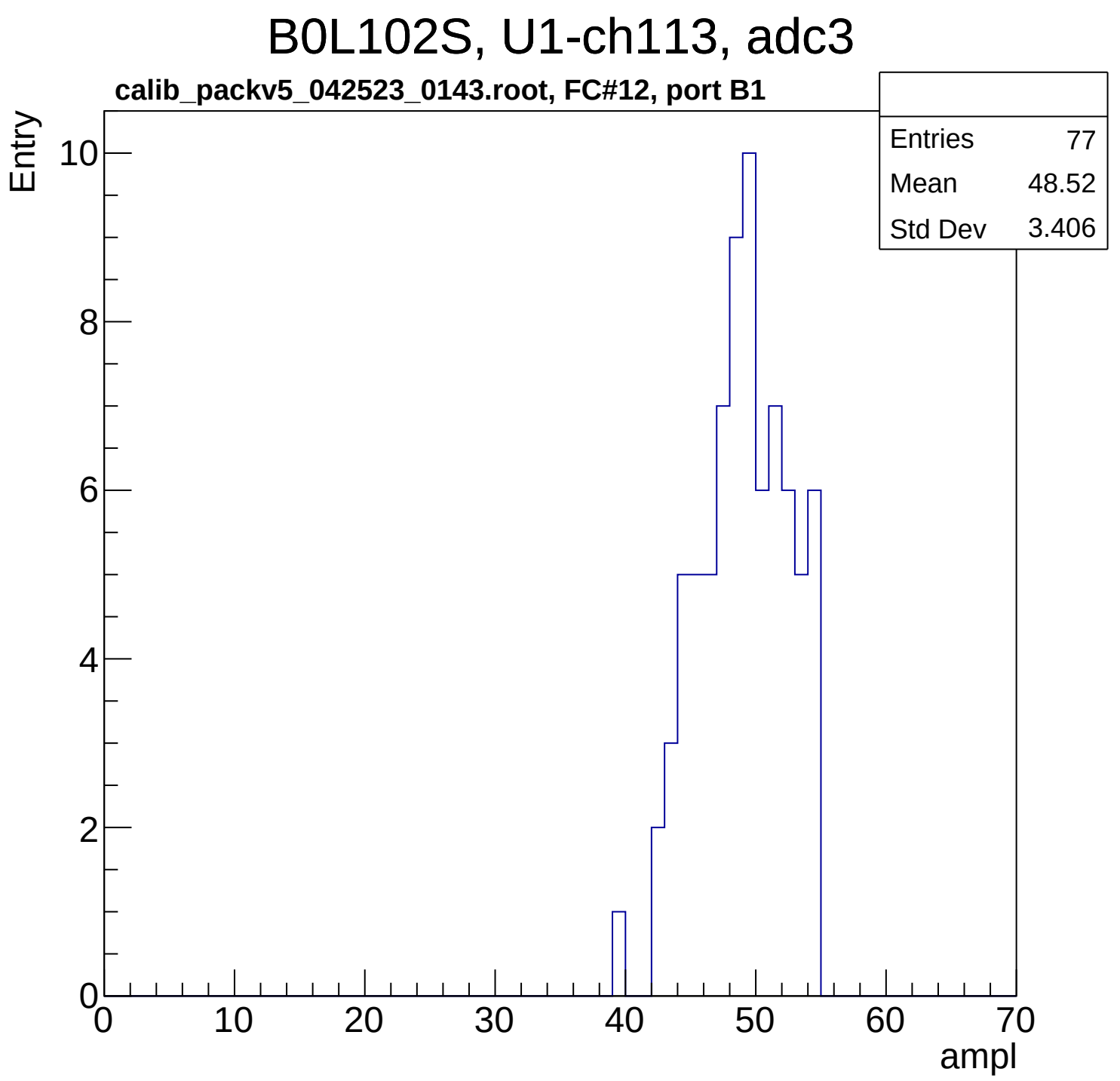

B0L102S, U1-ch113, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 41.02
Std Dev 3.617
Gaus mean : 41.7066
Gaus Width: 4.2240
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch113, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 48.52
Std Dev 3.406
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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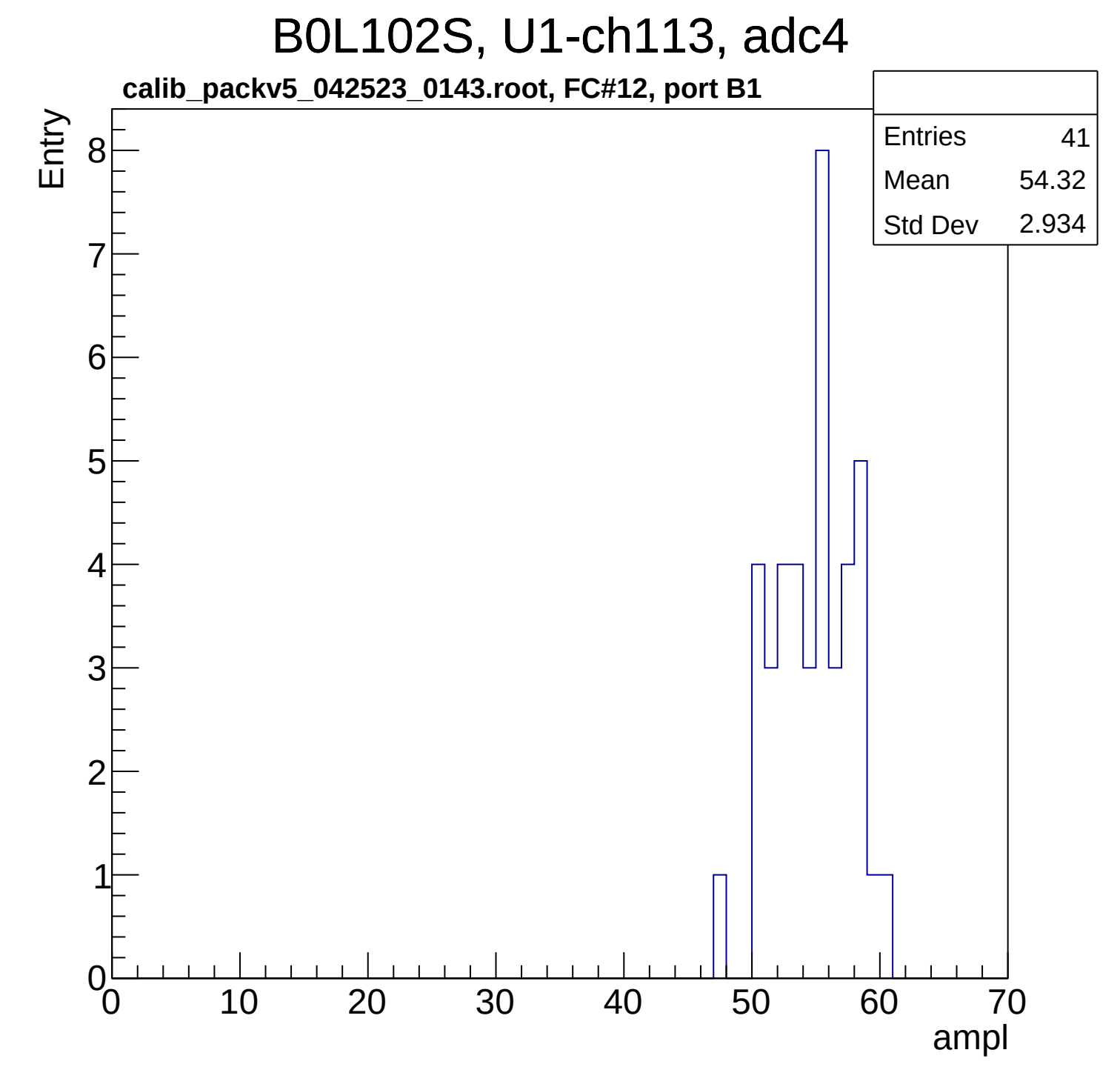
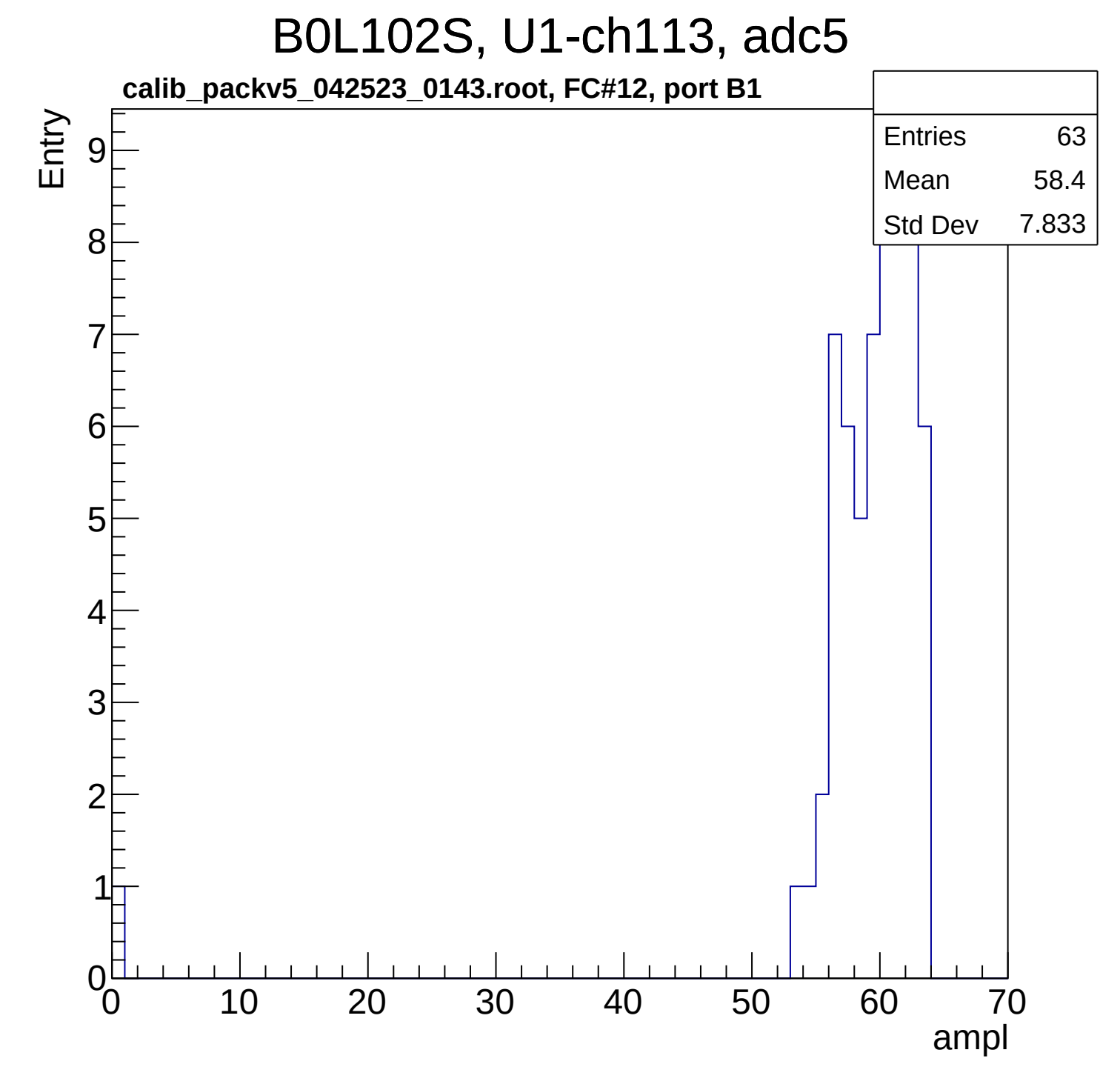

B0L102S, U1-ch113, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 54.32
Std Dev 2.934
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch113, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 58.4
Std Dev 7.833
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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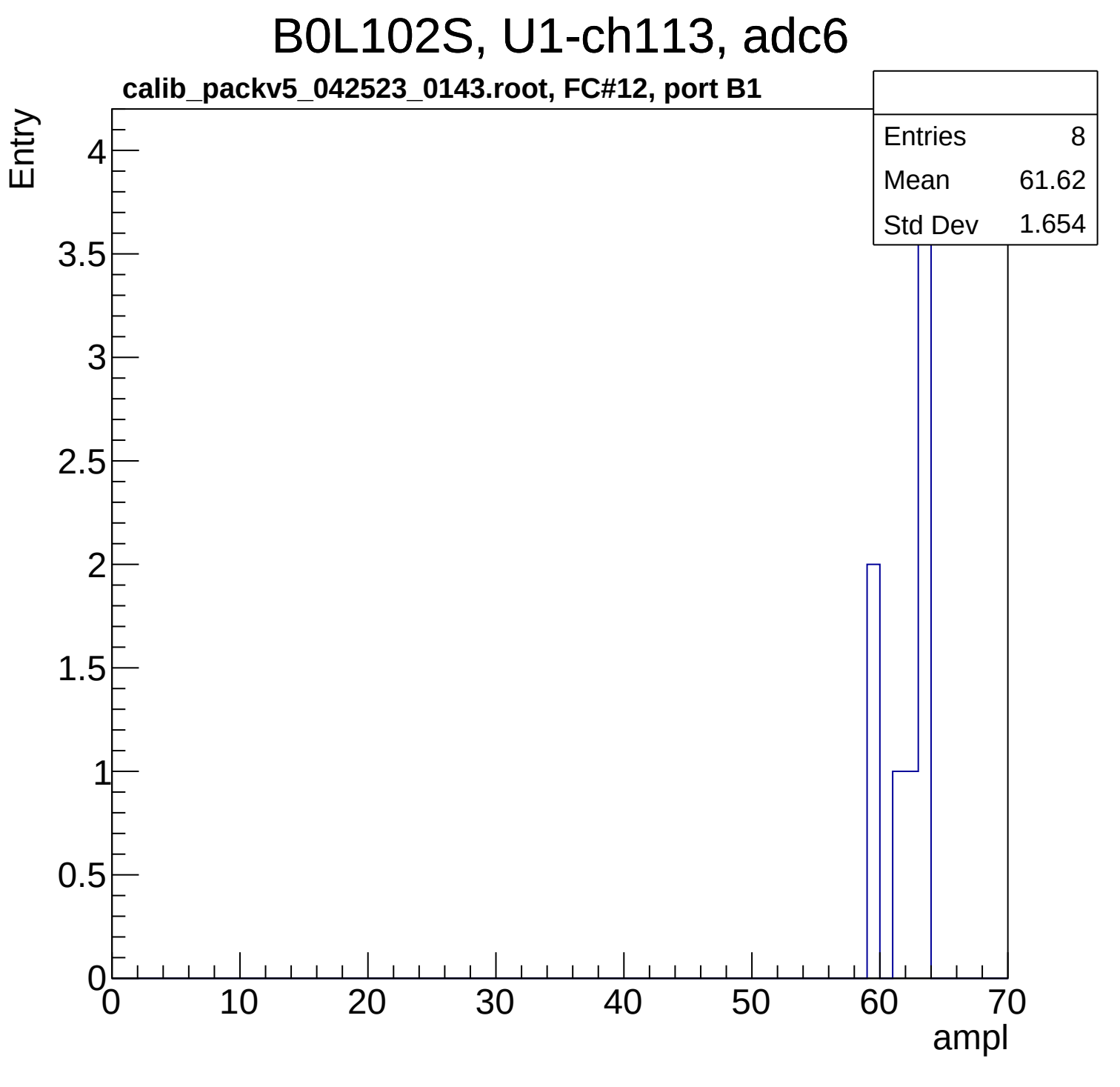
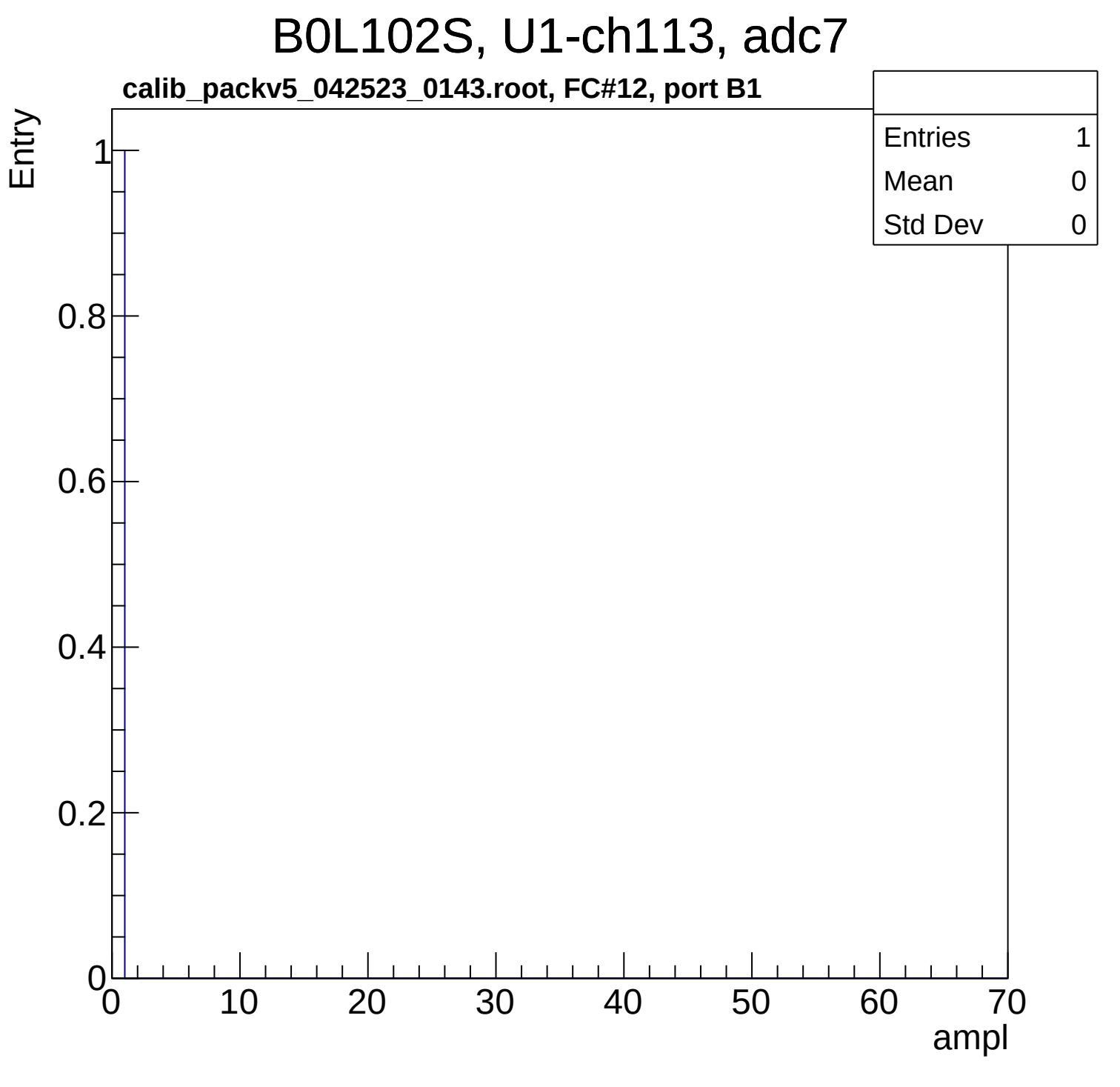
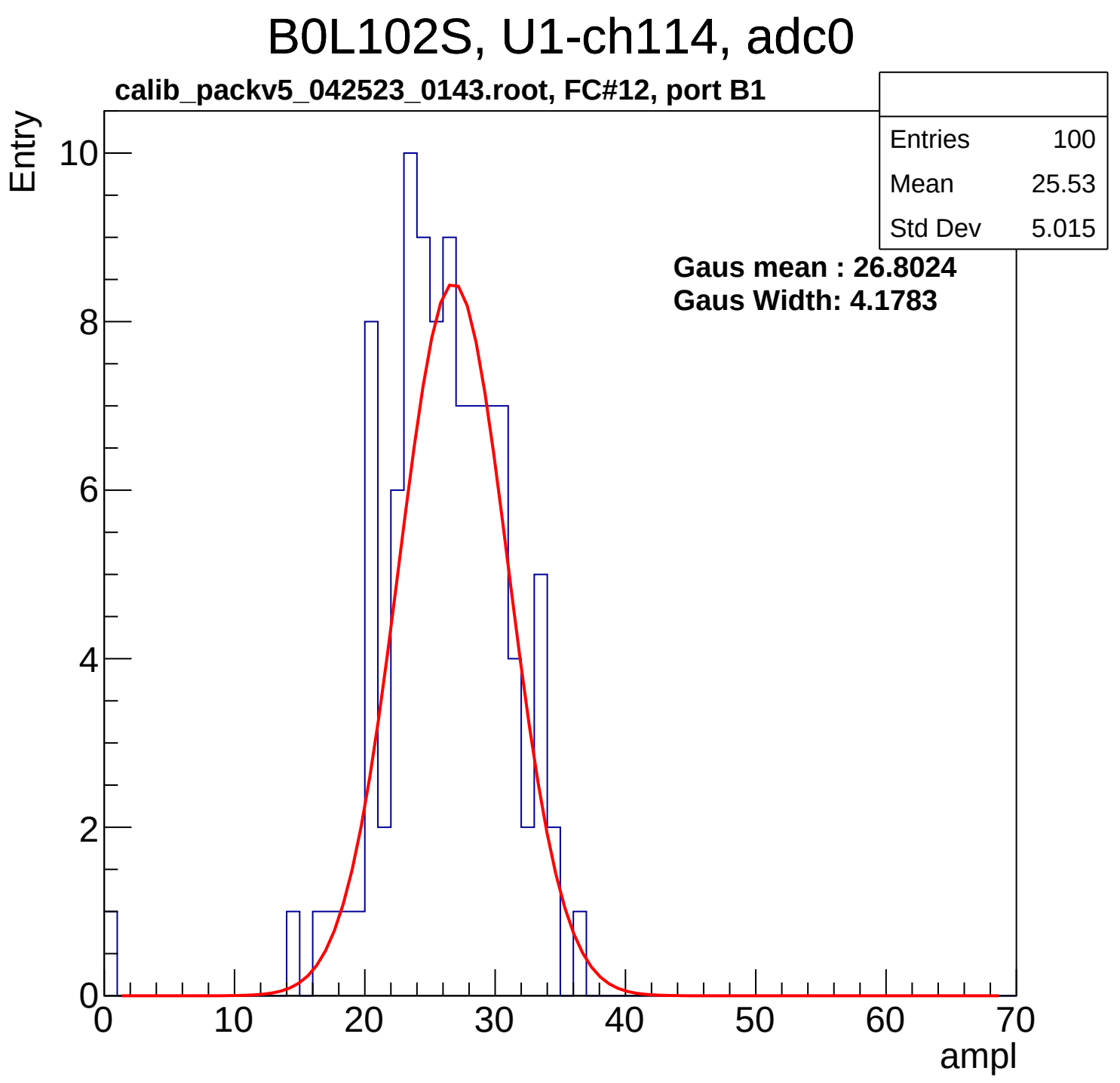
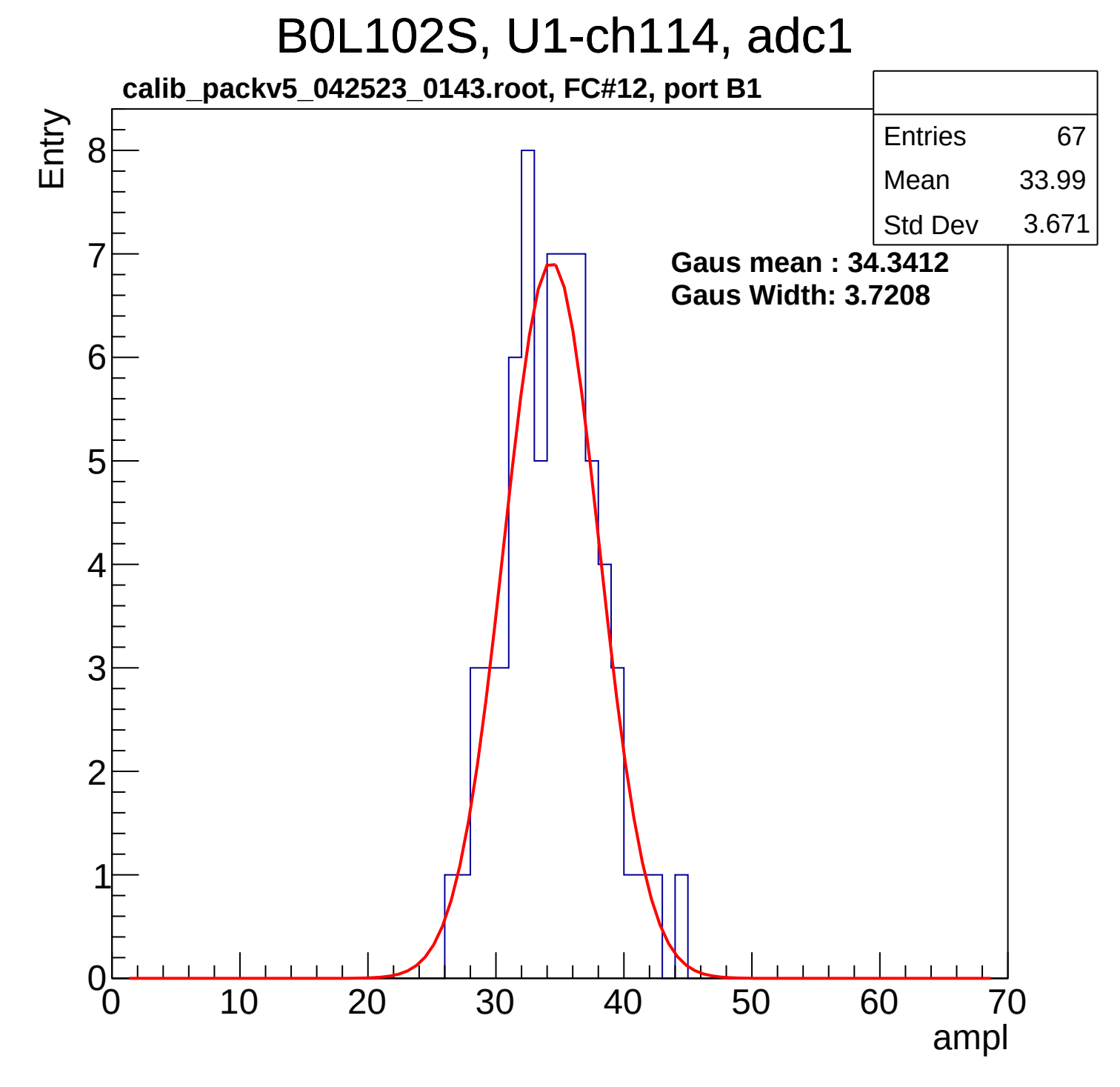
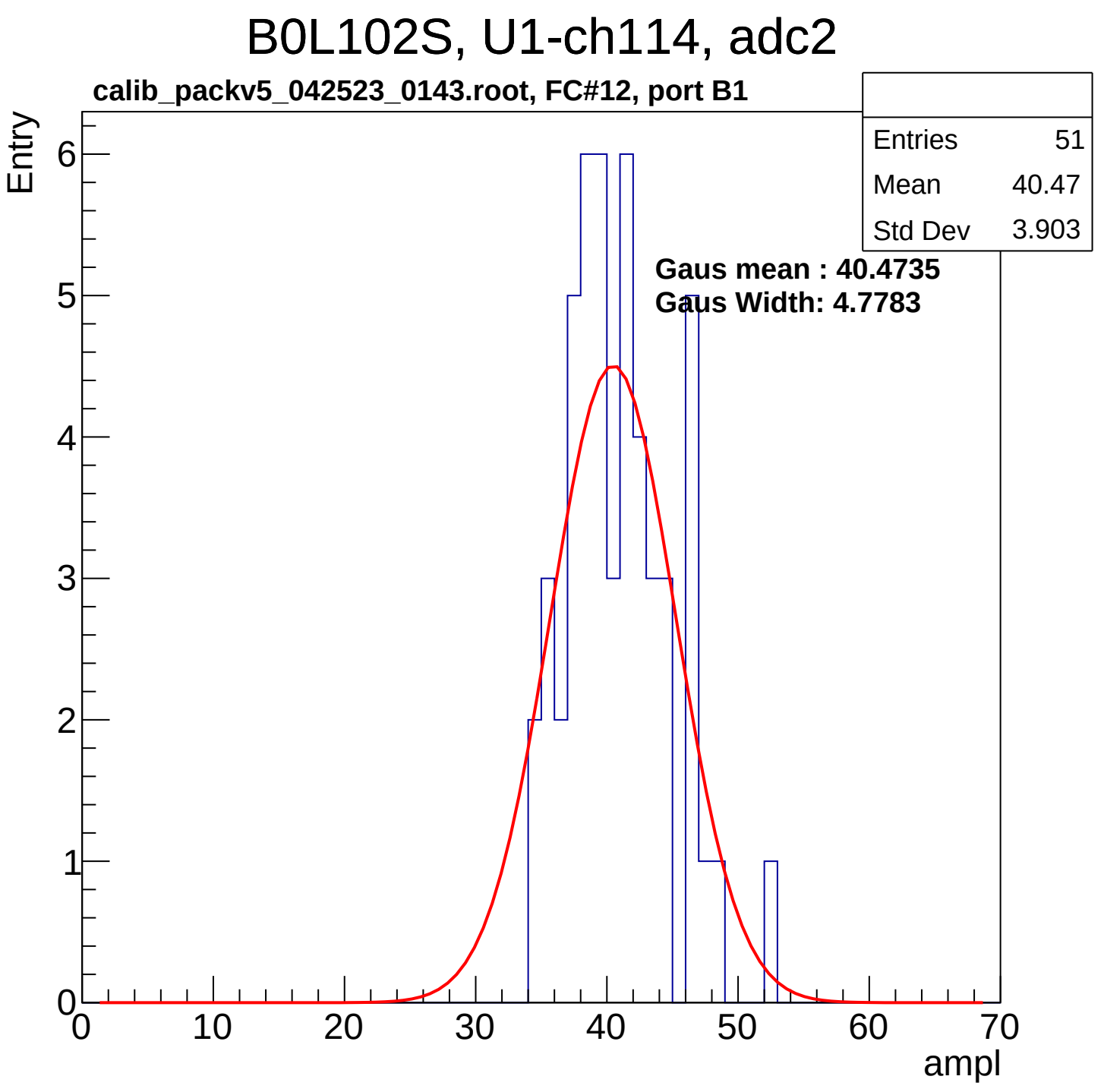
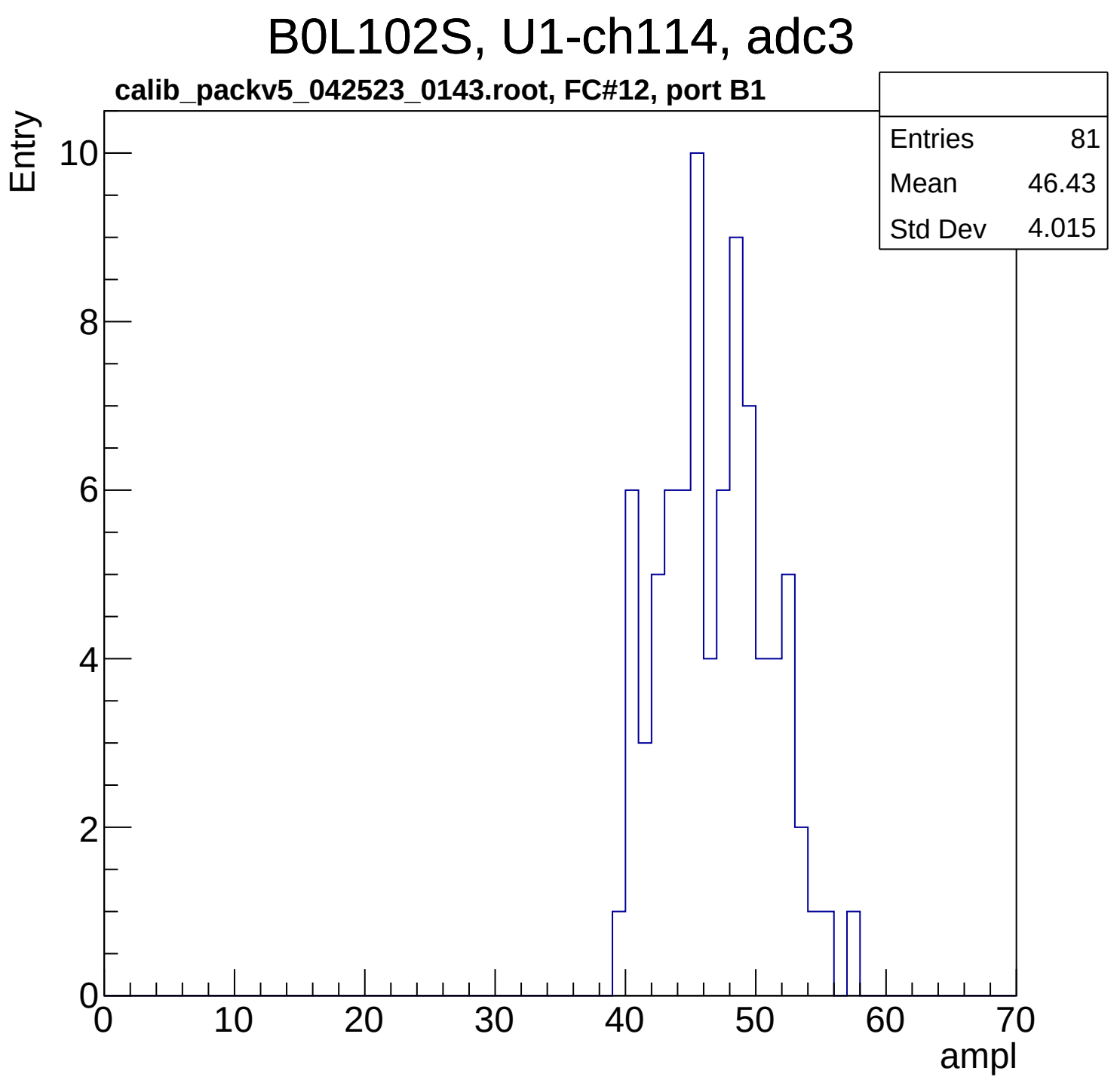

B0L102S, U1-ch113, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.62
Std Dev 1.654
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

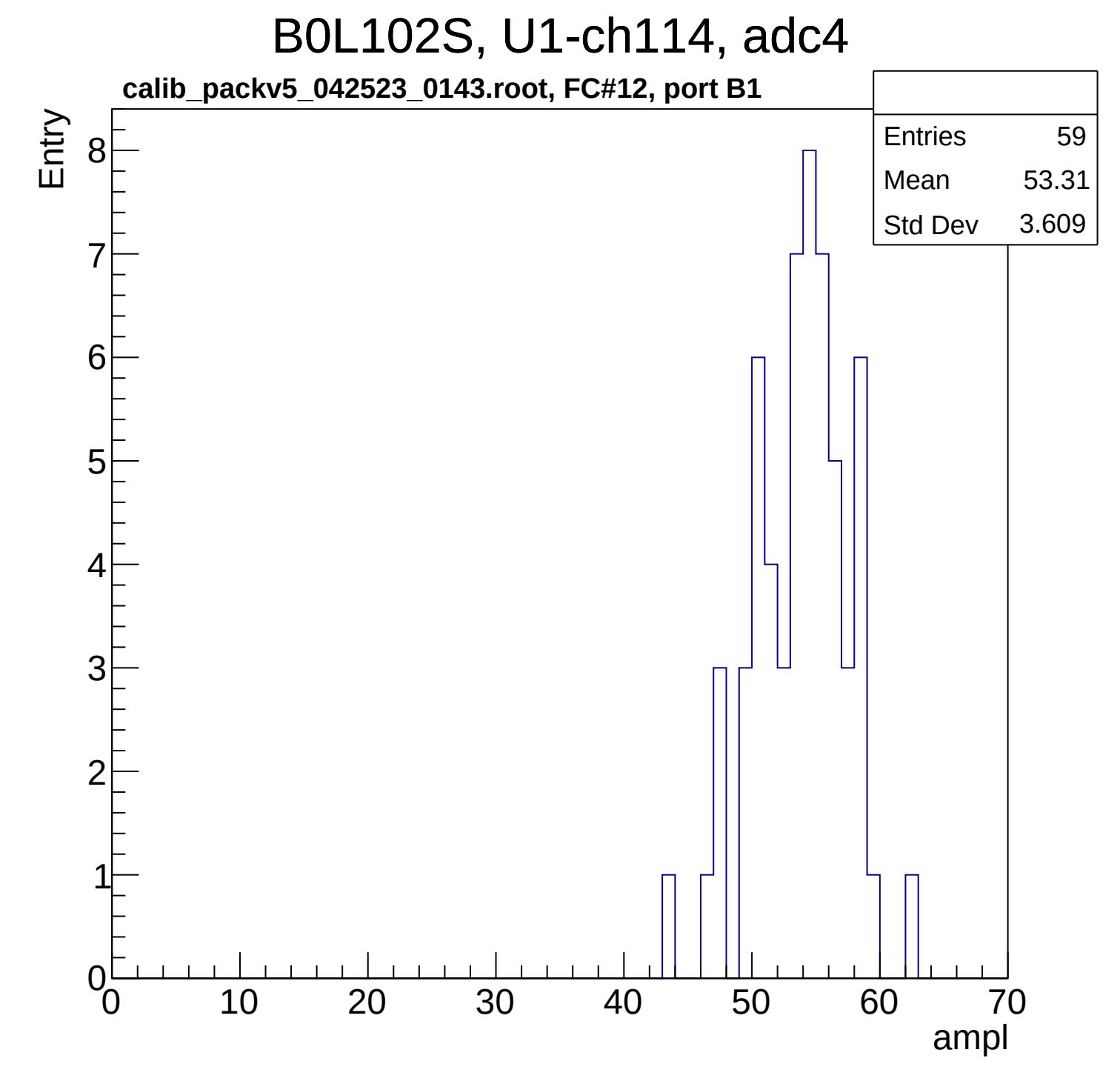

B0L102S, U1-ch113, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

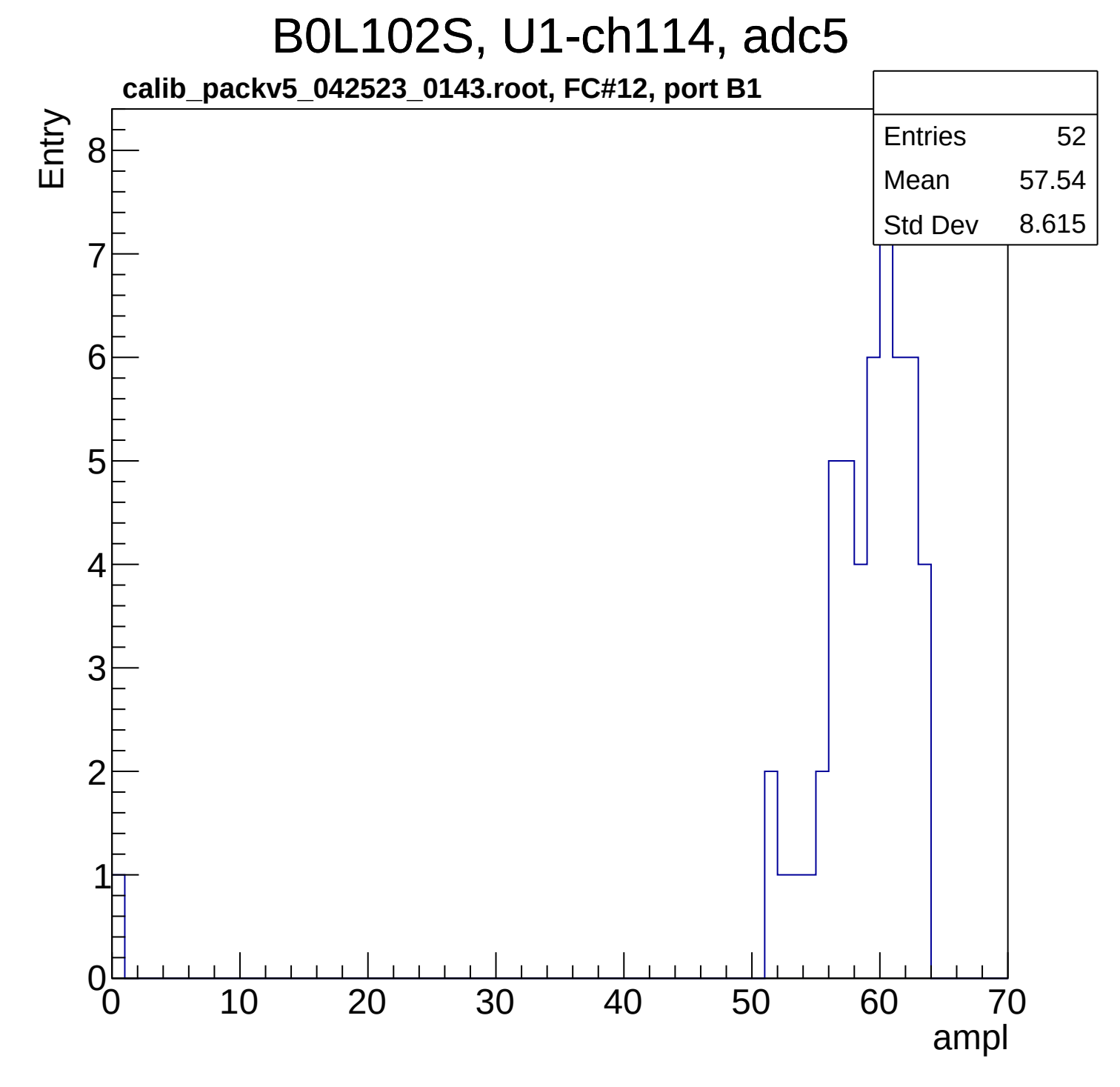

B0L102S, U1-ch114, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 100
Mean 25.53
Std Dev 5.015
Gaus mean : 26.8024
Gaus Width: 4.1783
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

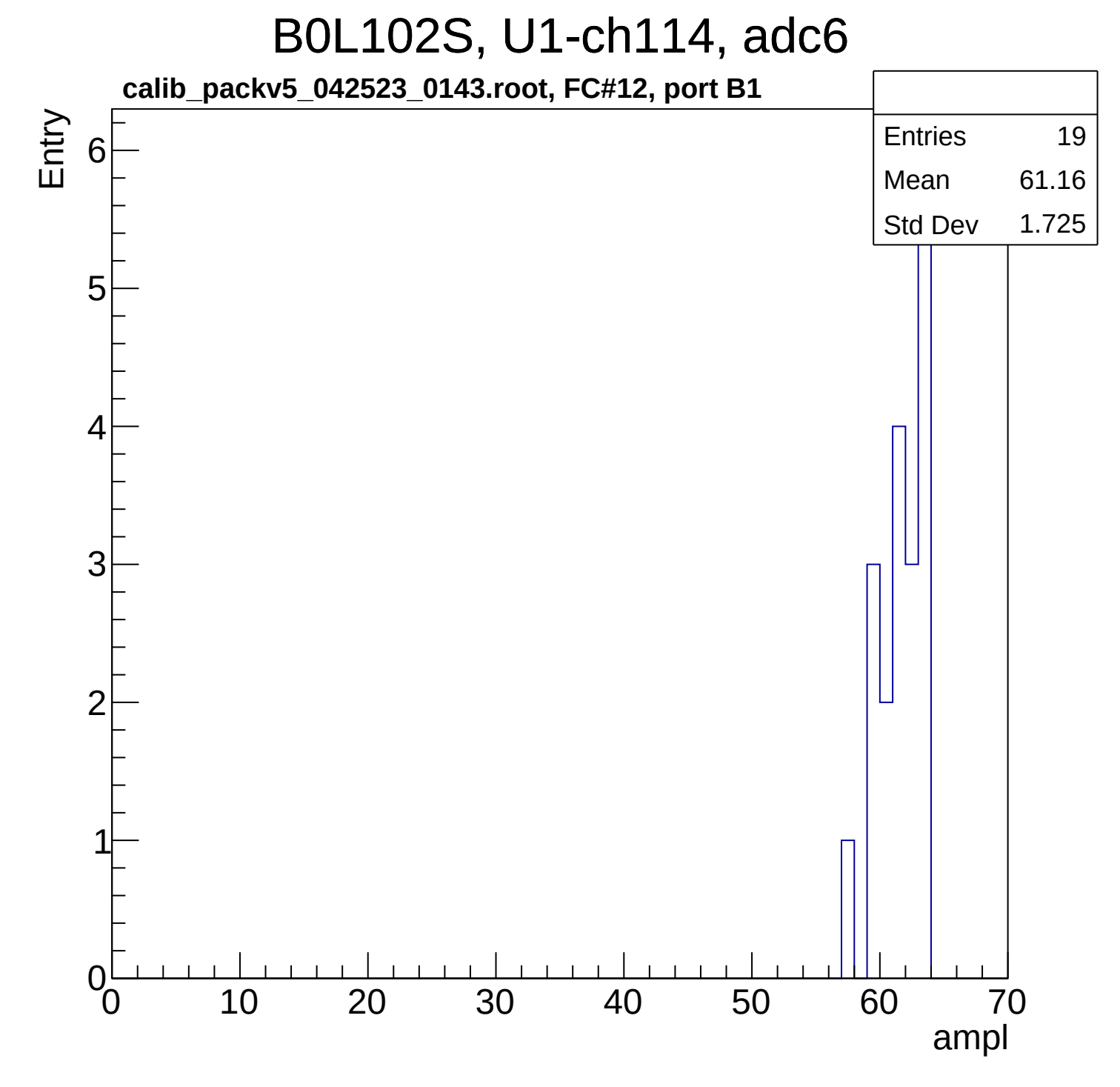

B0L102S, U1-ch114, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 33.99
Std Dev 3.671
Gaus mean : 34.3412
Gaus Width: 3.7208
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

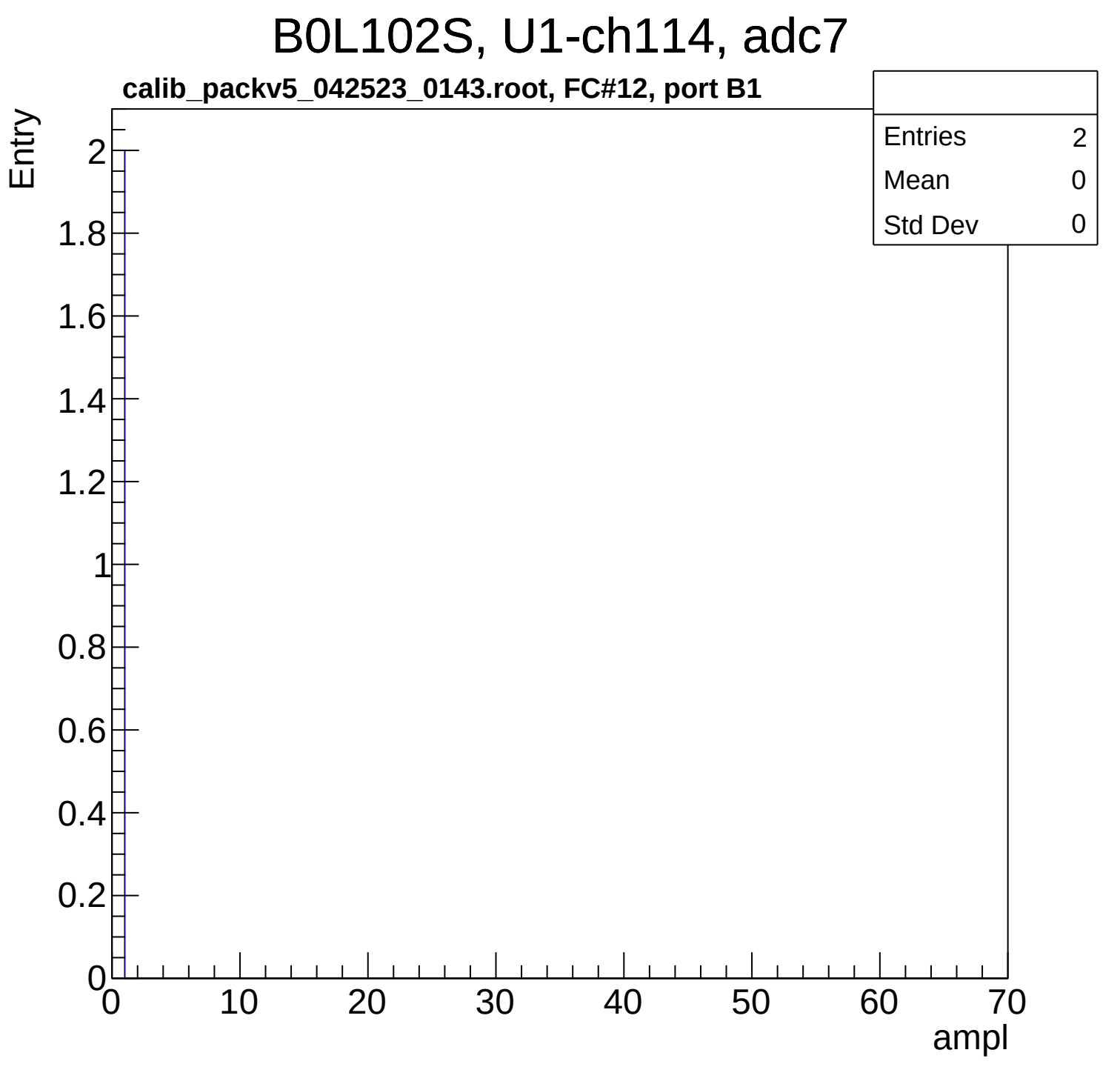

B0L102S, U1-ch114, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 40.47
Std Dev 3.903
Gaus mean : 40.4735
Gaus Width: 4.7783
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

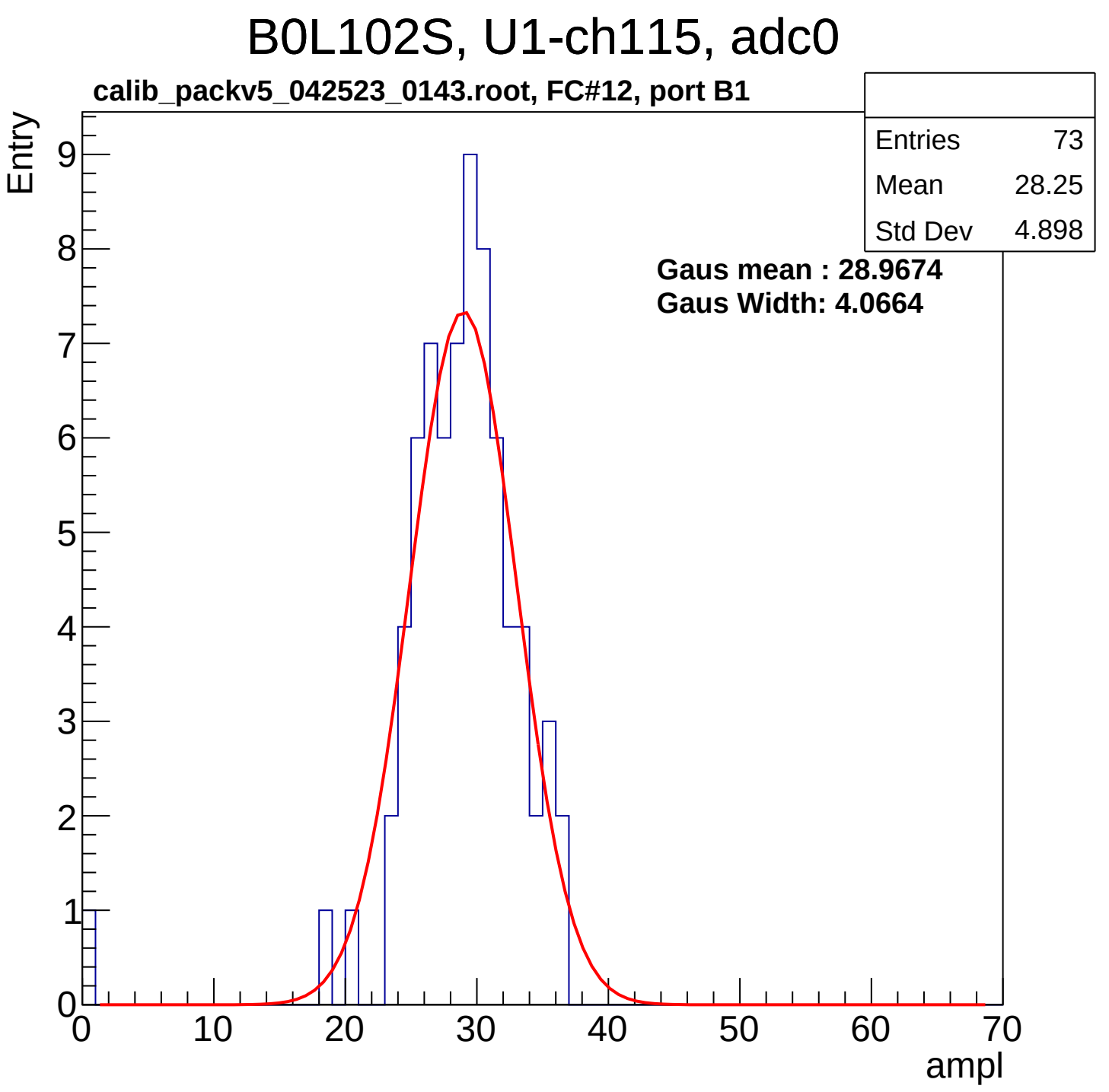

B0L102S, U1-ch114, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 46.43
Std Dev 4.015
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

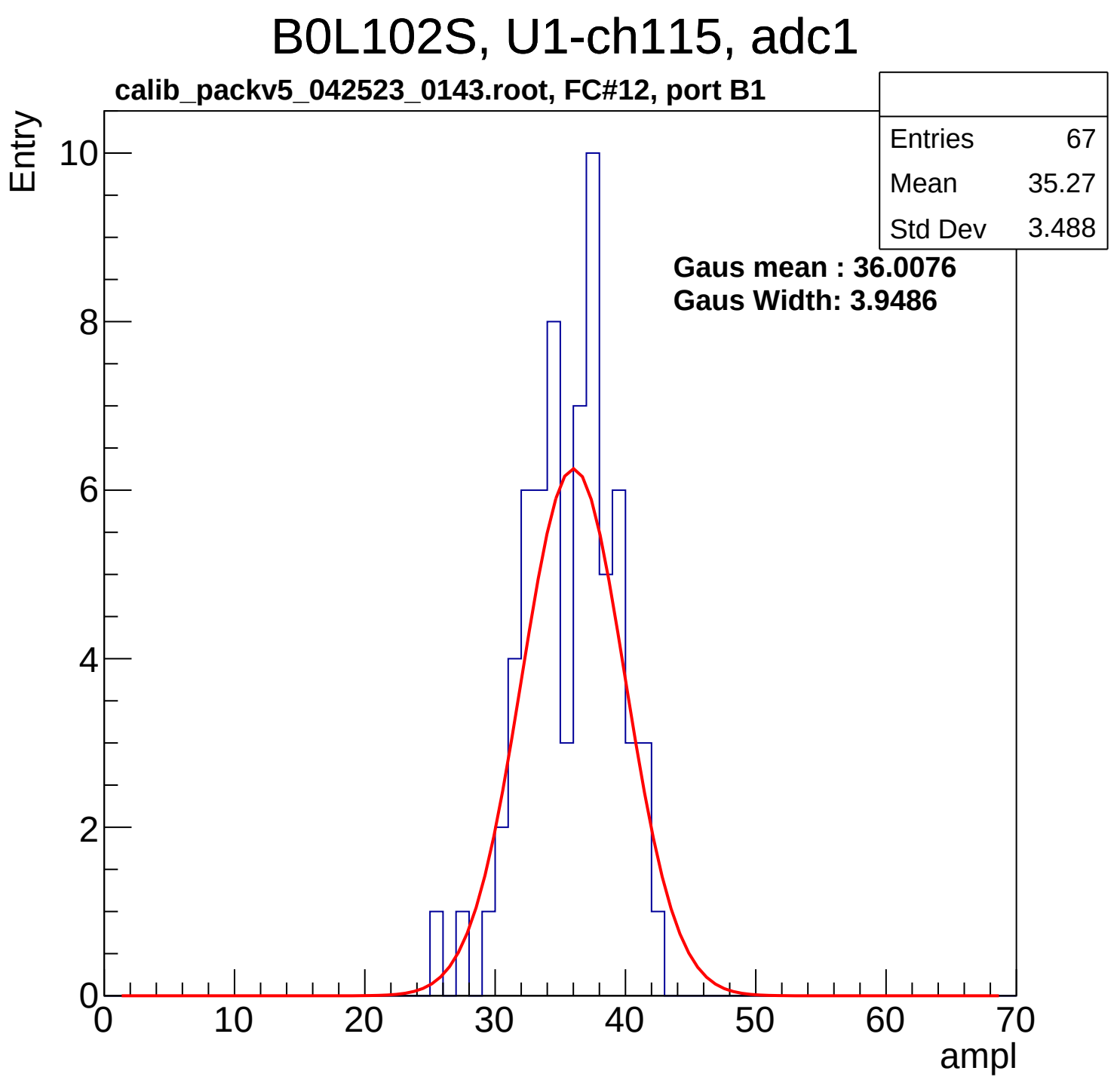

B0L102S, U1-ch114, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 53.31
Std Dev 3.609
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

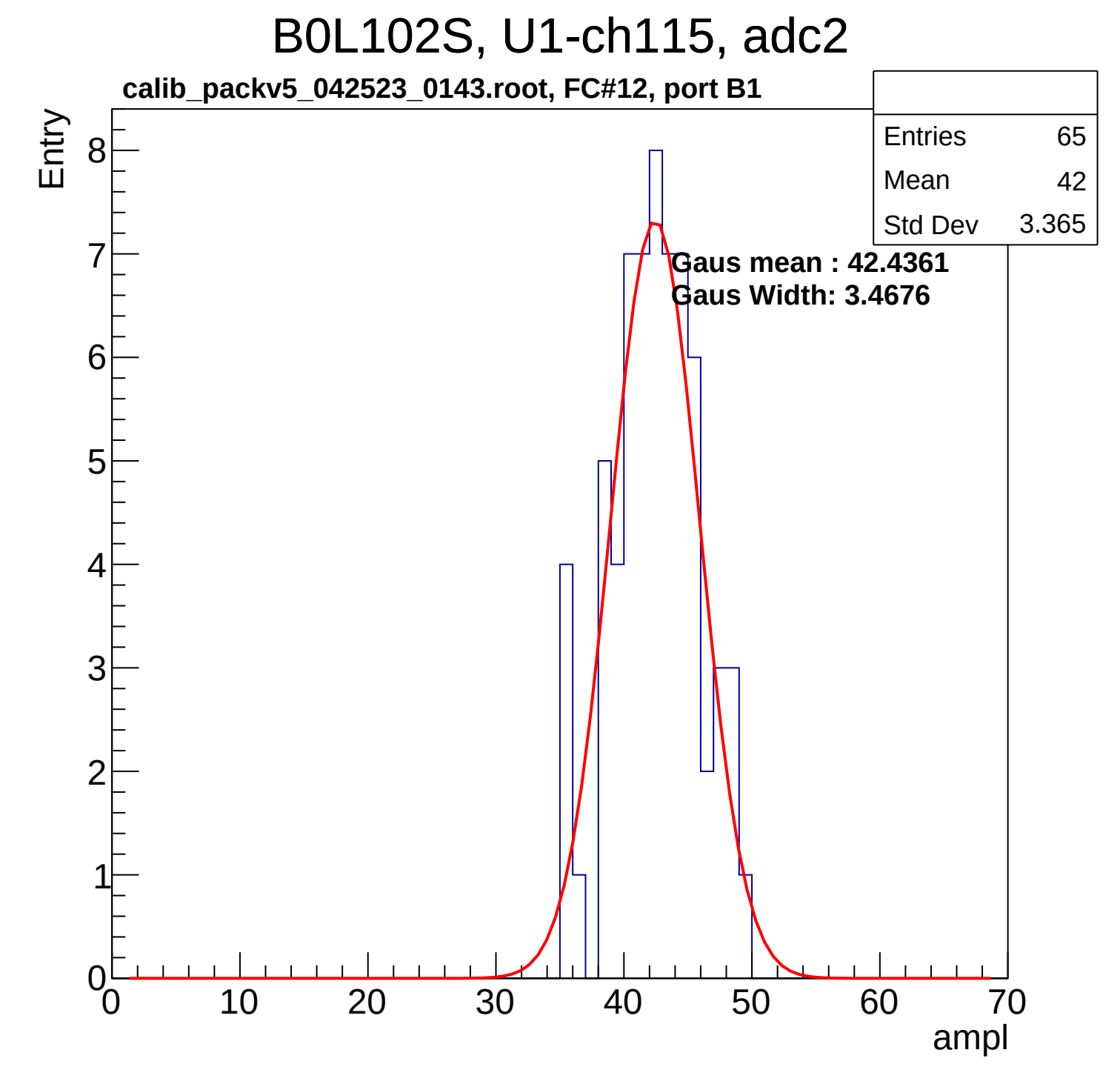

B0L102S, U1-ch114, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 57.54
Std Dev 8.615
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch114, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 19
Mean 61.16
Std Dev 1.725
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch114, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch115, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 28.25
Std Dev 4.898
Gaus mean : 28.9674
Gaus Width: 4.0664
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch115, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.27
Std Dev 3.488
Gaus mean : 36.0076
Gaus Width: 3.9486
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch115, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 42
Std Dev 3.365
Gaus mean : 42.4361
Gaus Width: 3.4676
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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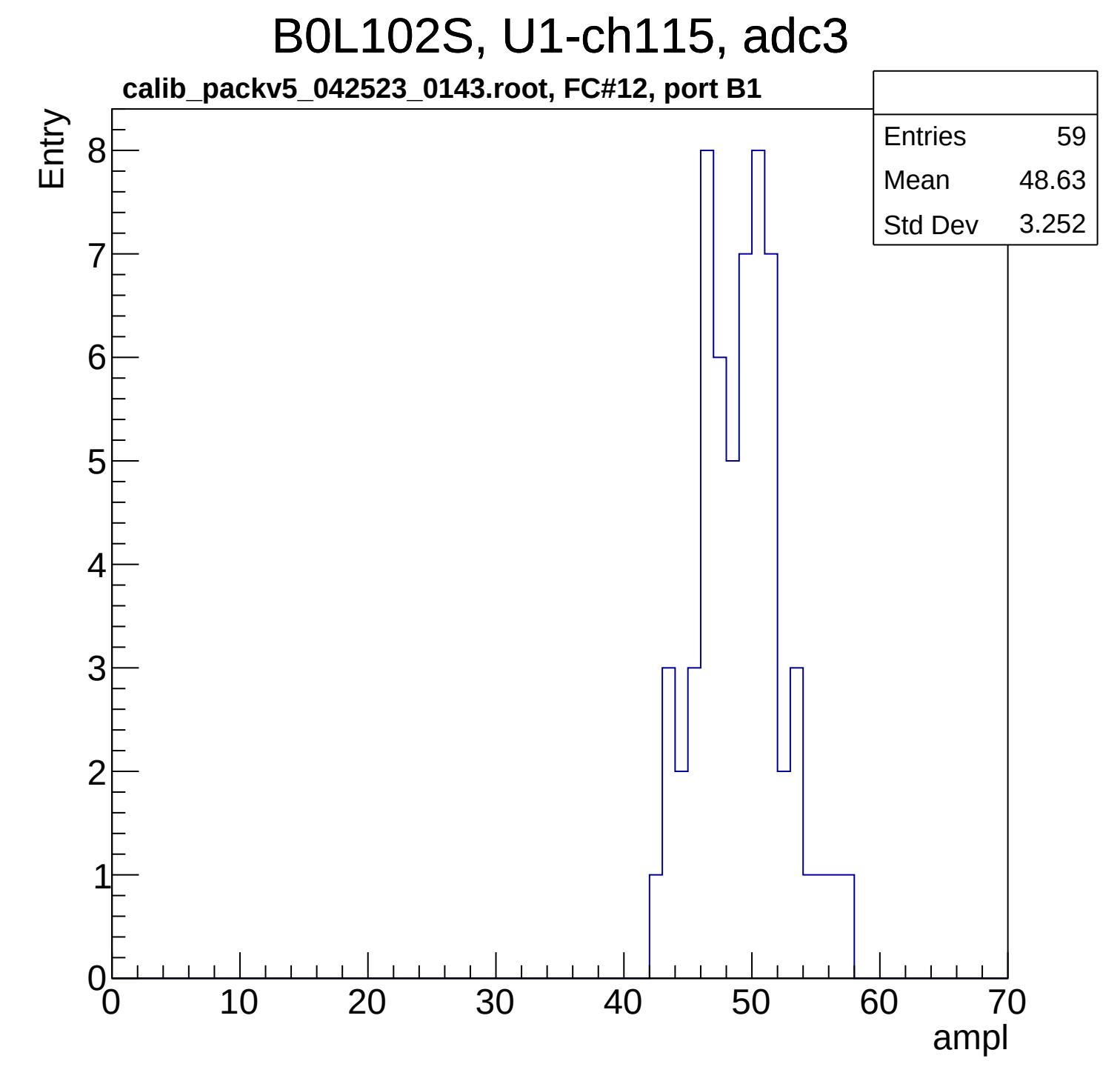
B0L102S, U1-ch115, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.63
Std Dev 3.252
Entry
ampl
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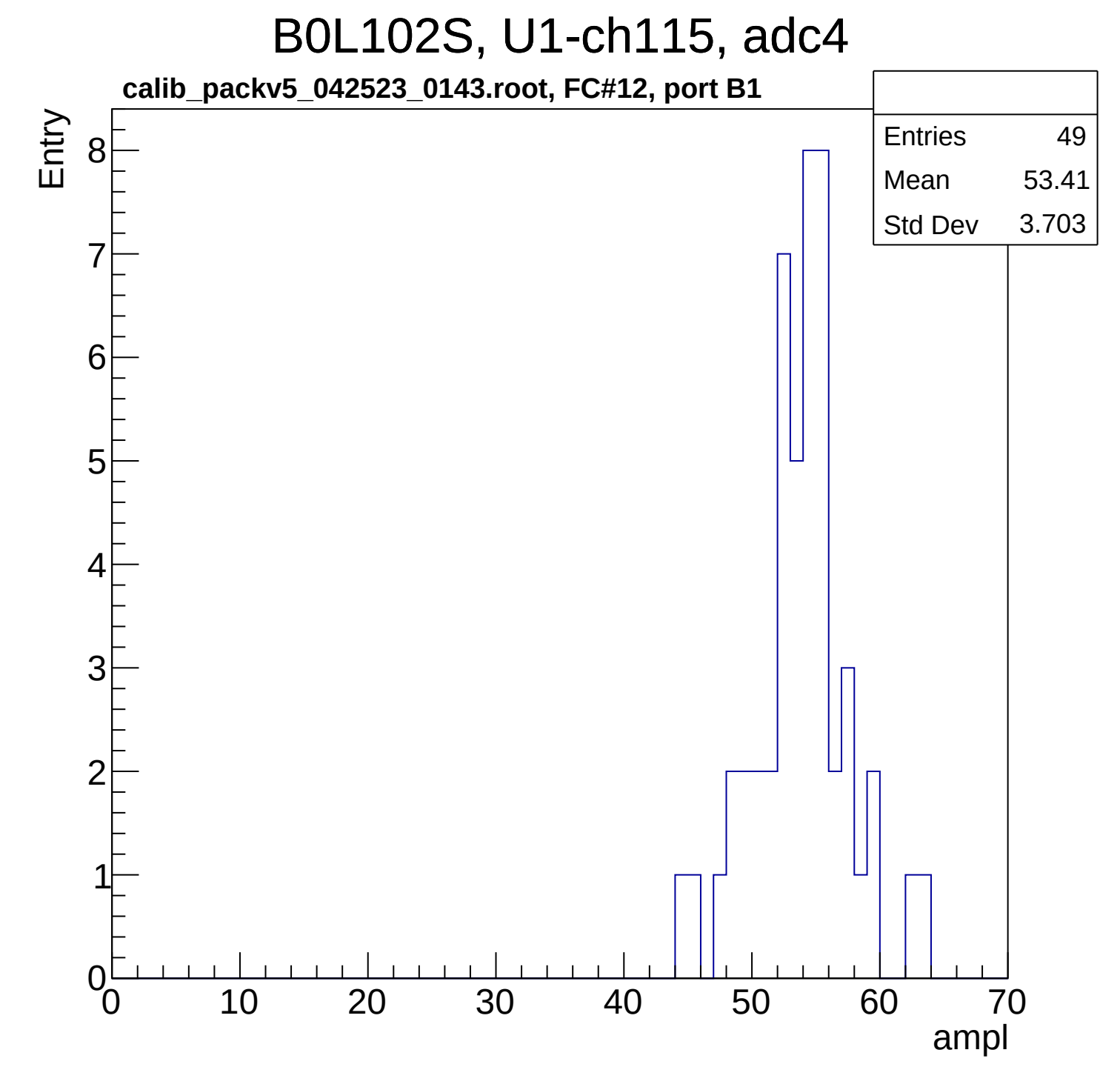
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch115, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 53.41
Std Dev 3.703
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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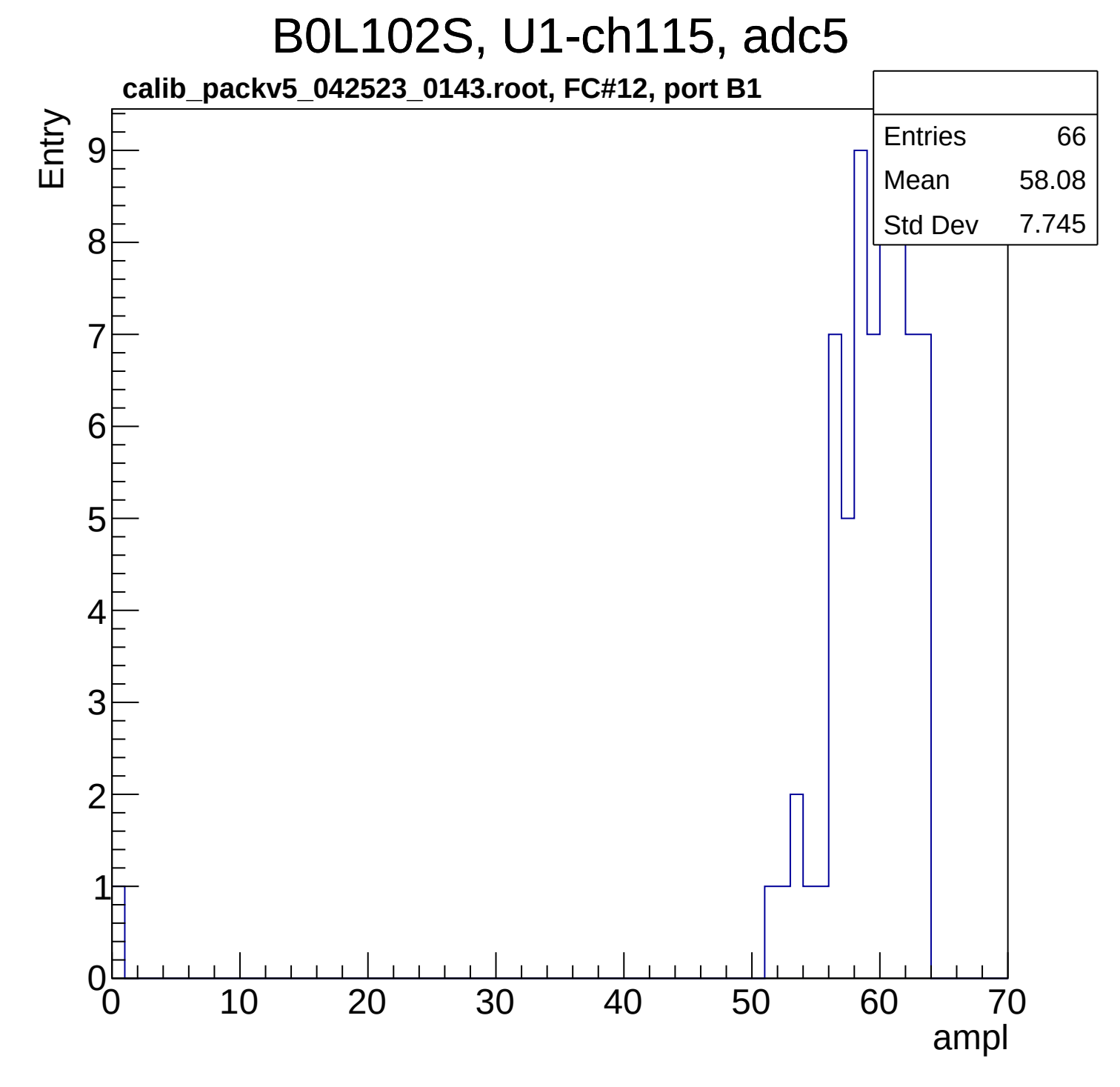
B0L102S, U1-ch115, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 58.08
Std Dev 7.745
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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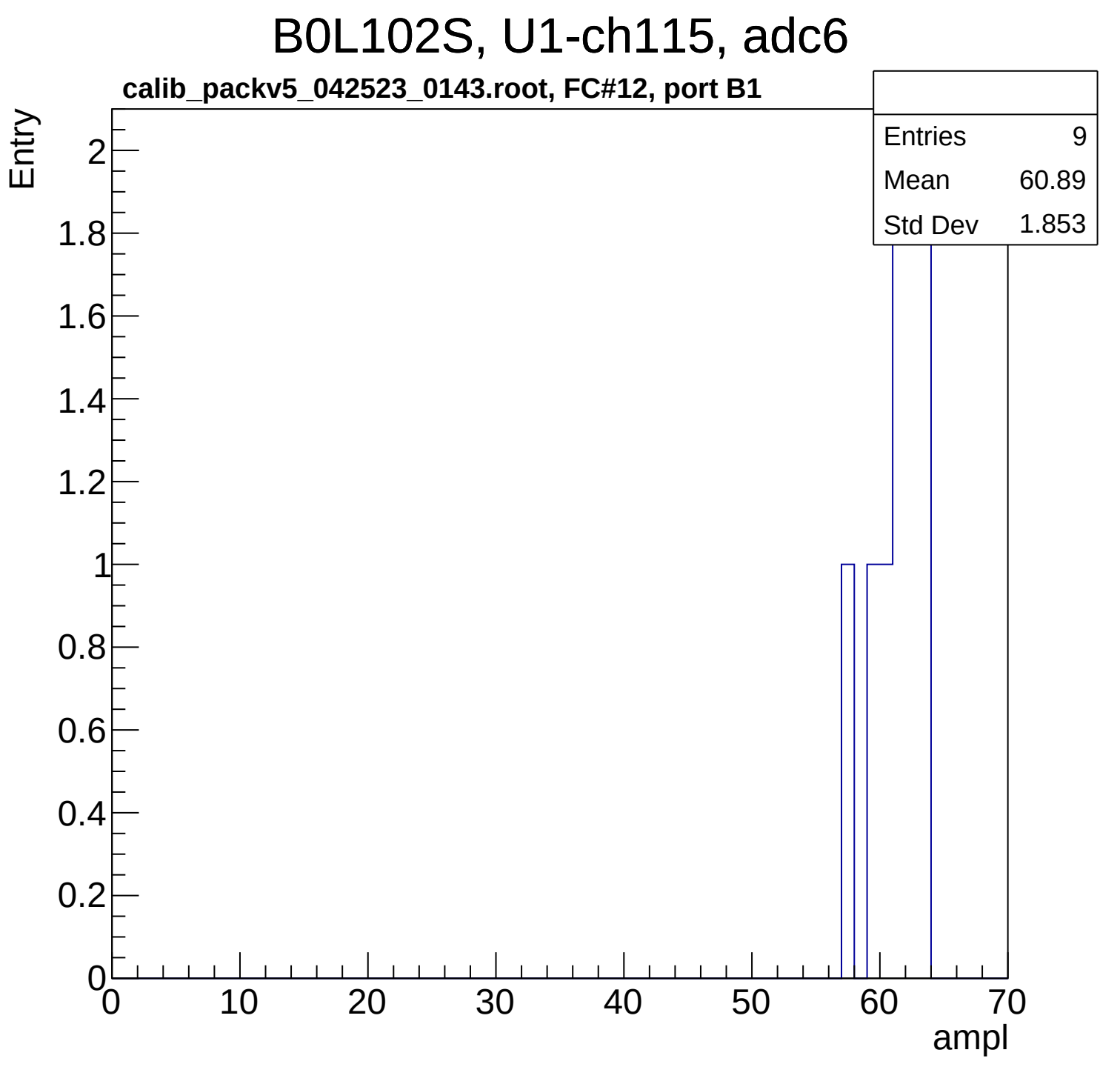
40
50
60
70


B0L102S, U1-ch115, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 60.89
Std Dev 1.853
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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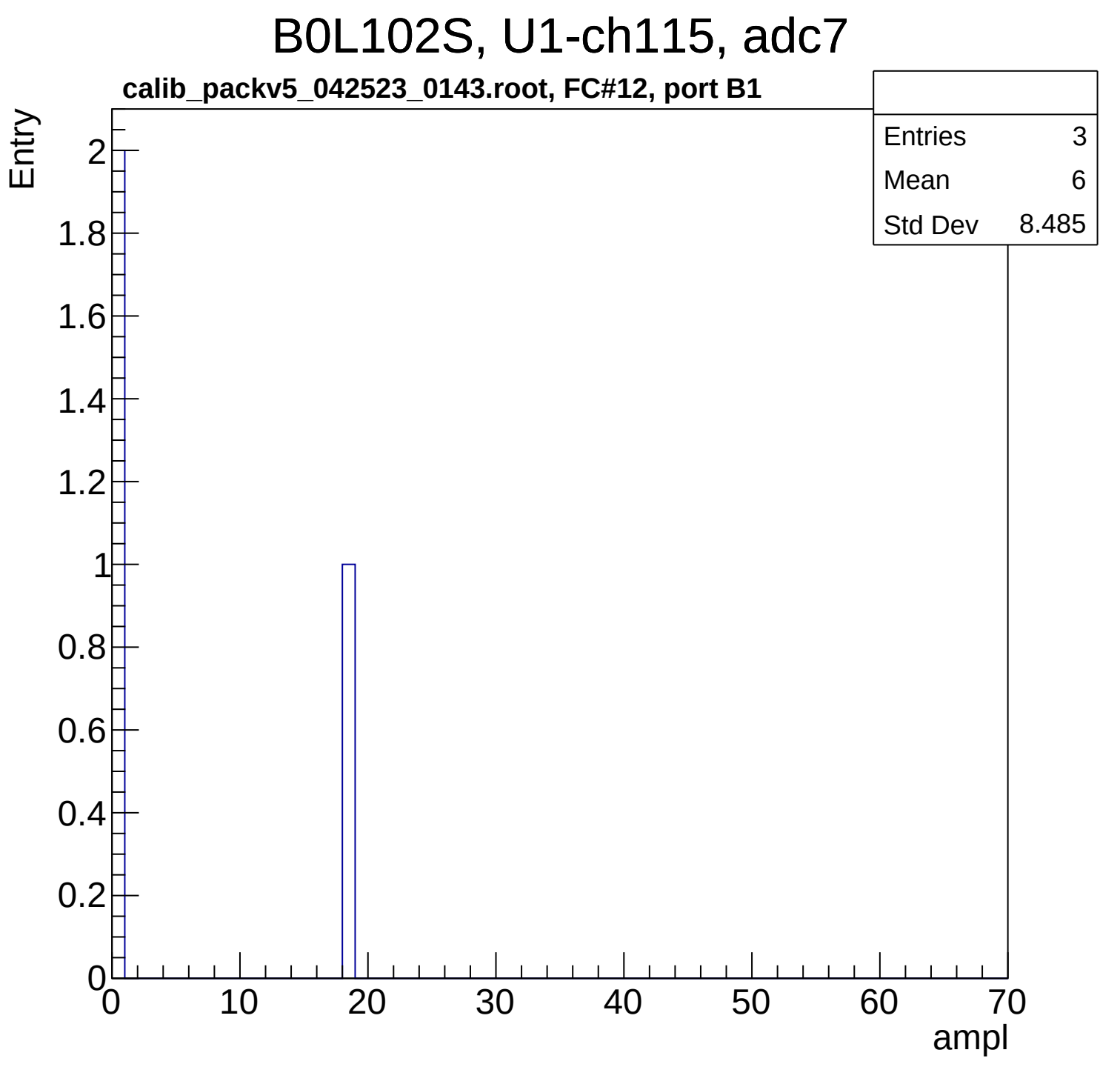
B0L102S, U1-ch115, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 6
Std Dev 8.485
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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30
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50
60
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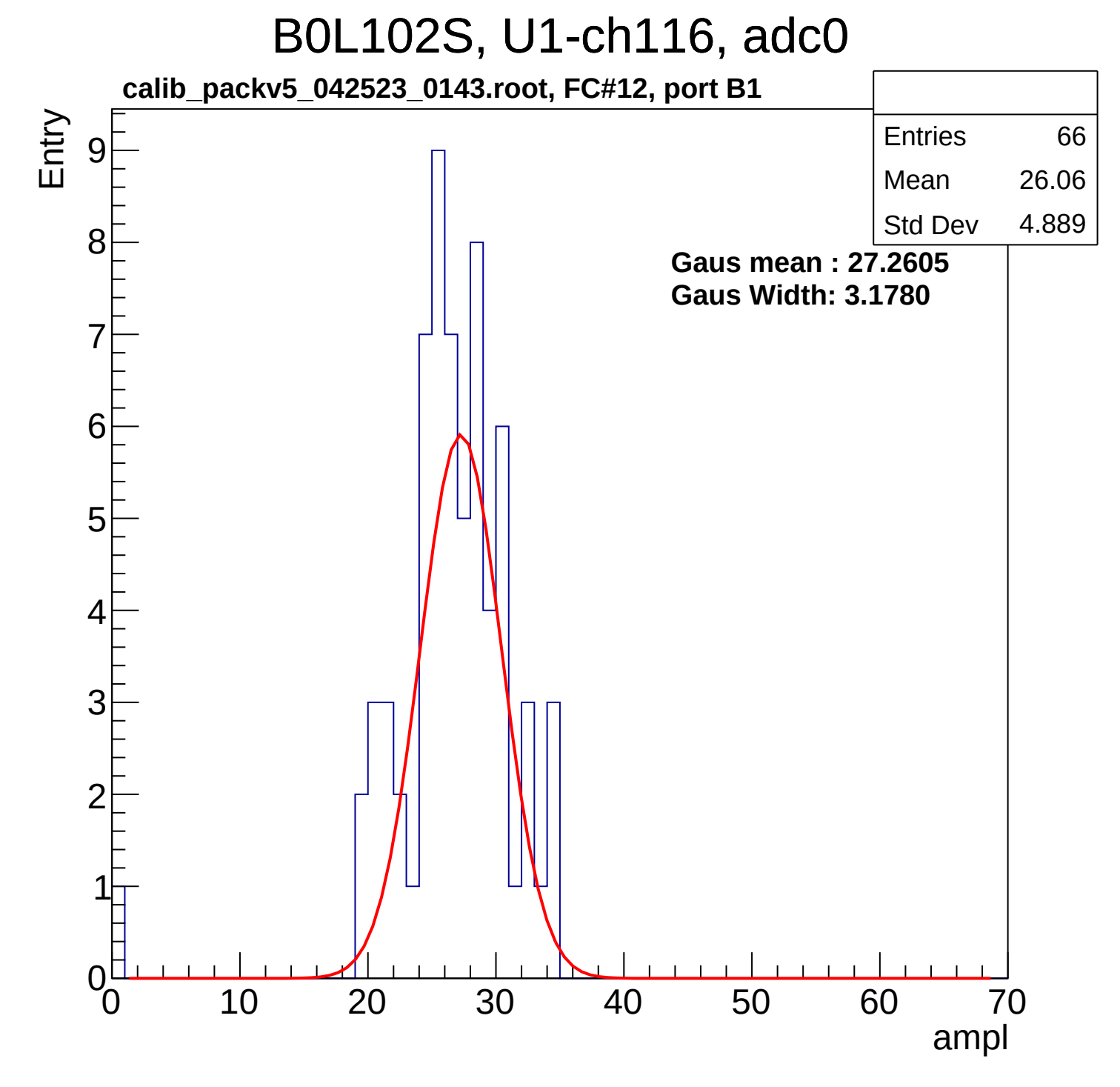
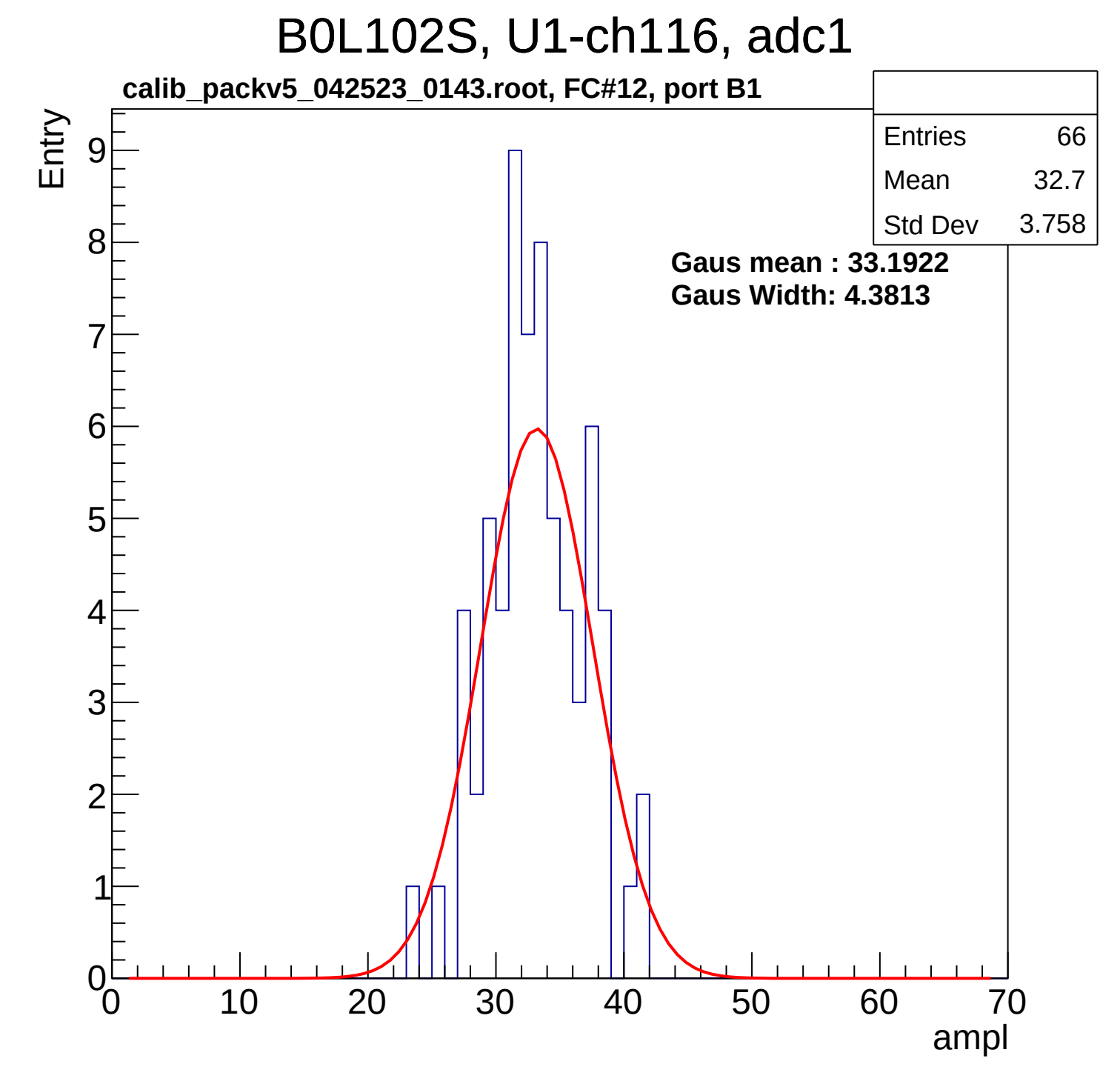
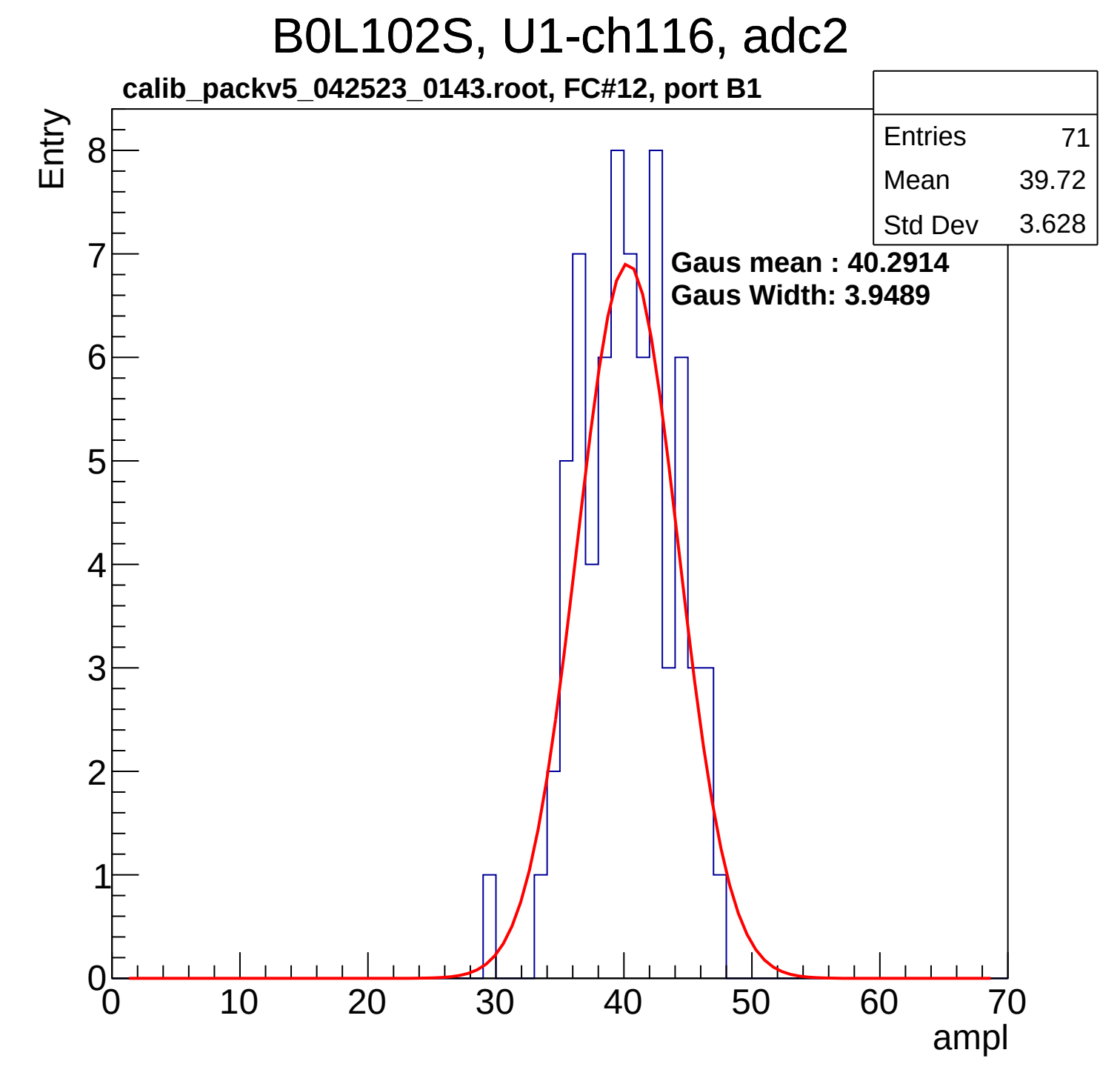

B0L102S, U1-ch116, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 26.06
Std Dev 4.889
Gaus mean : 27.2605
Gaus Width: 3.1780
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch116, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 32.7
Std Dev 3.758
Gaus mean : 33.1922
Gaus Width: 4.3813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch116, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 39.72
Std Dev 3.628
Gaus mean : 40.2914
Gaus Width: 3.9489
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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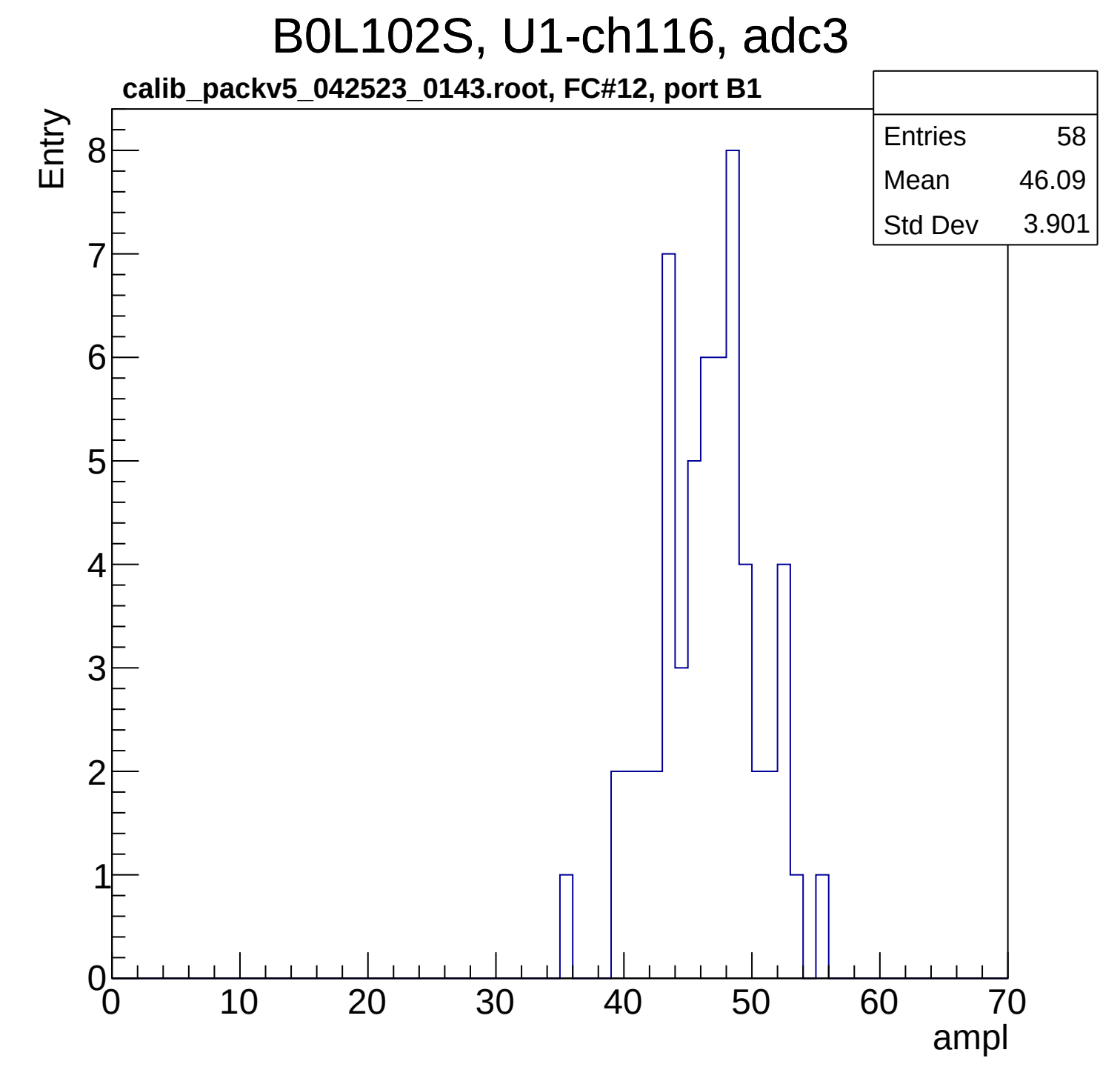
B0L102S, U1-ch116, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 46.09
Std Dev 3.901
Entry
ampl
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3
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0
10
20
30
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60
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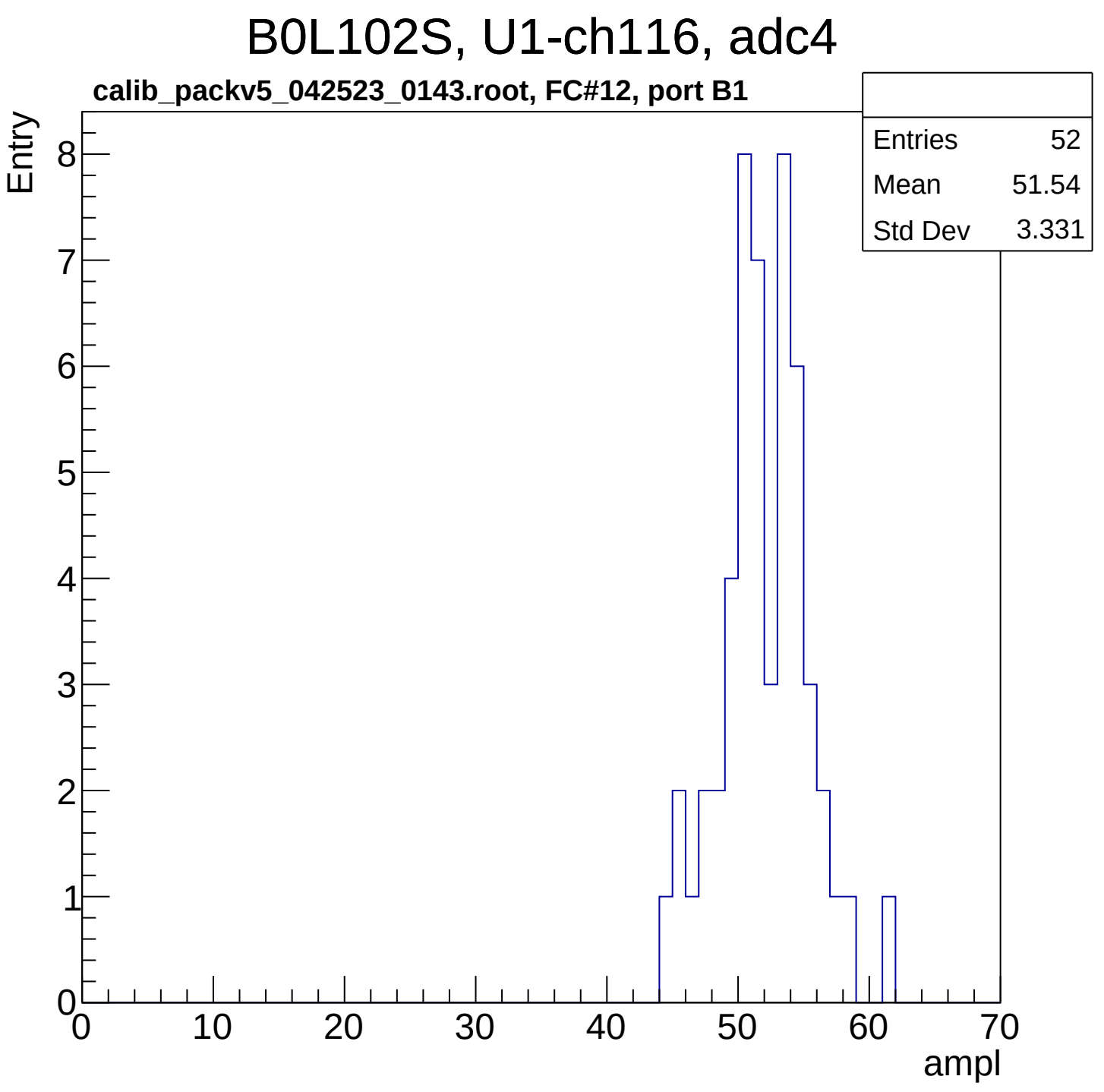
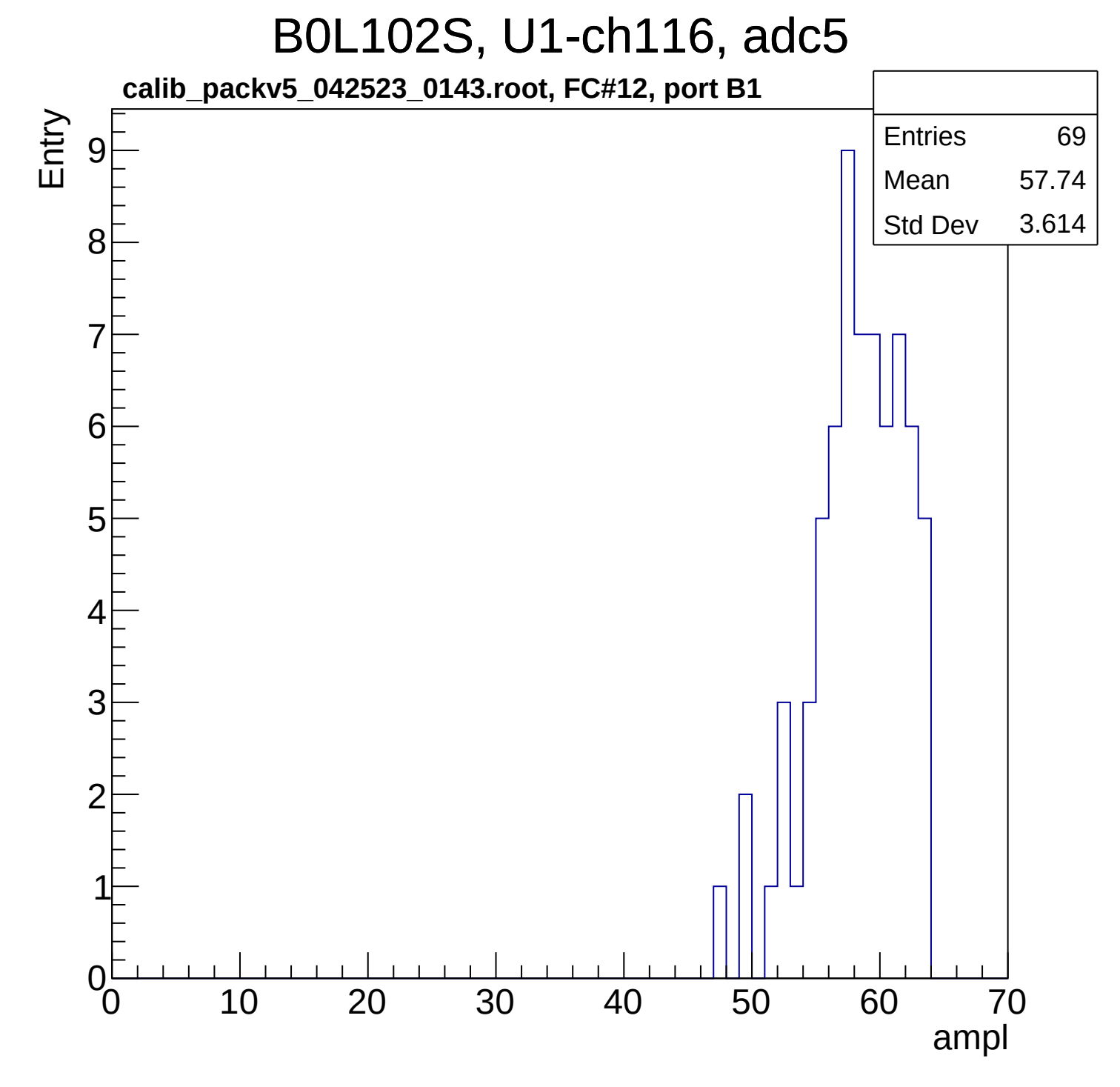

B0L102S, U1-ch116, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 51.54
Std Dev 3.331
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch116, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 57.74
Std Dev 3.614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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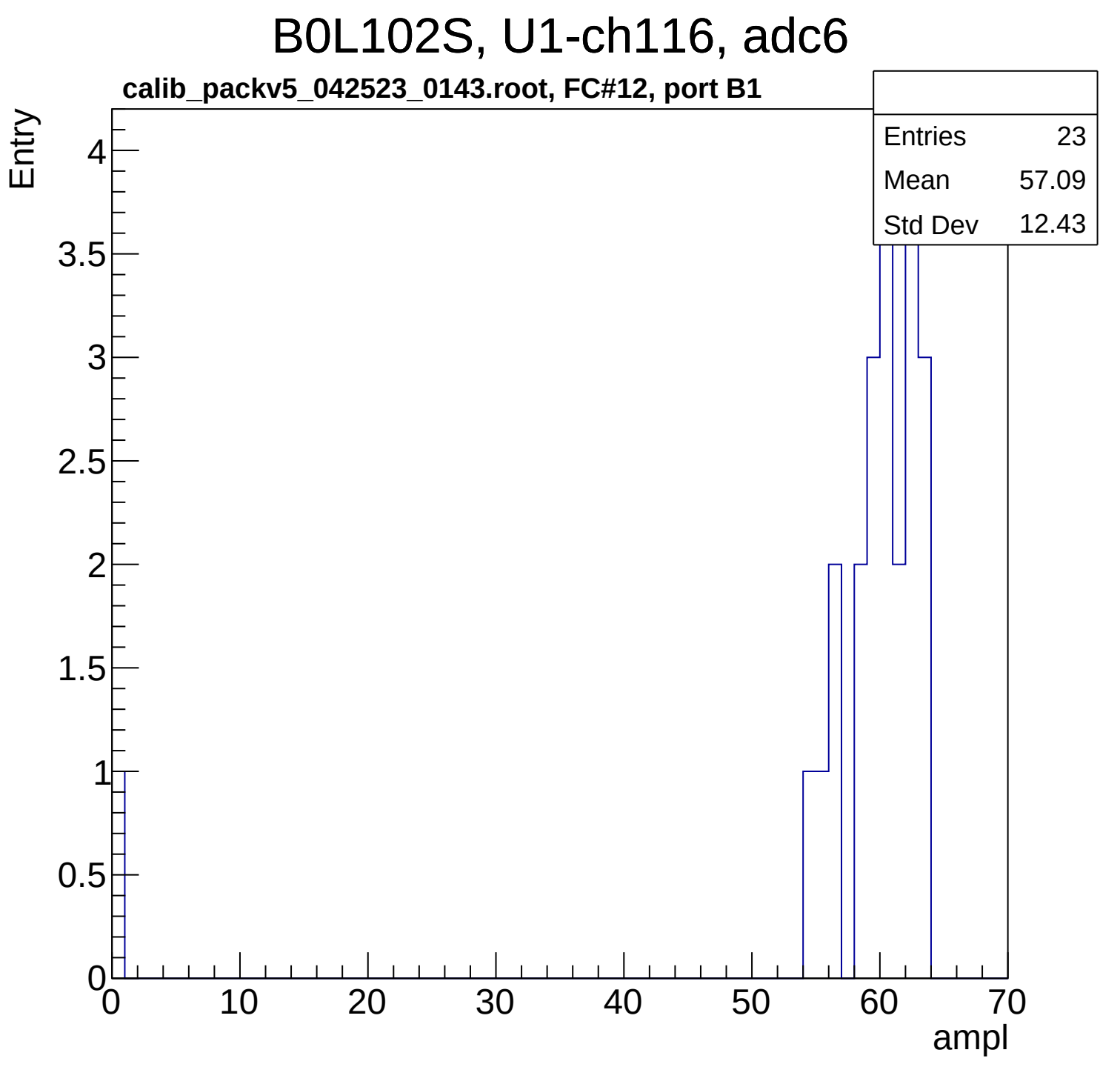
B0L102S, U1-ch116, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 23
Mean 57.09
Std Dev 12.43
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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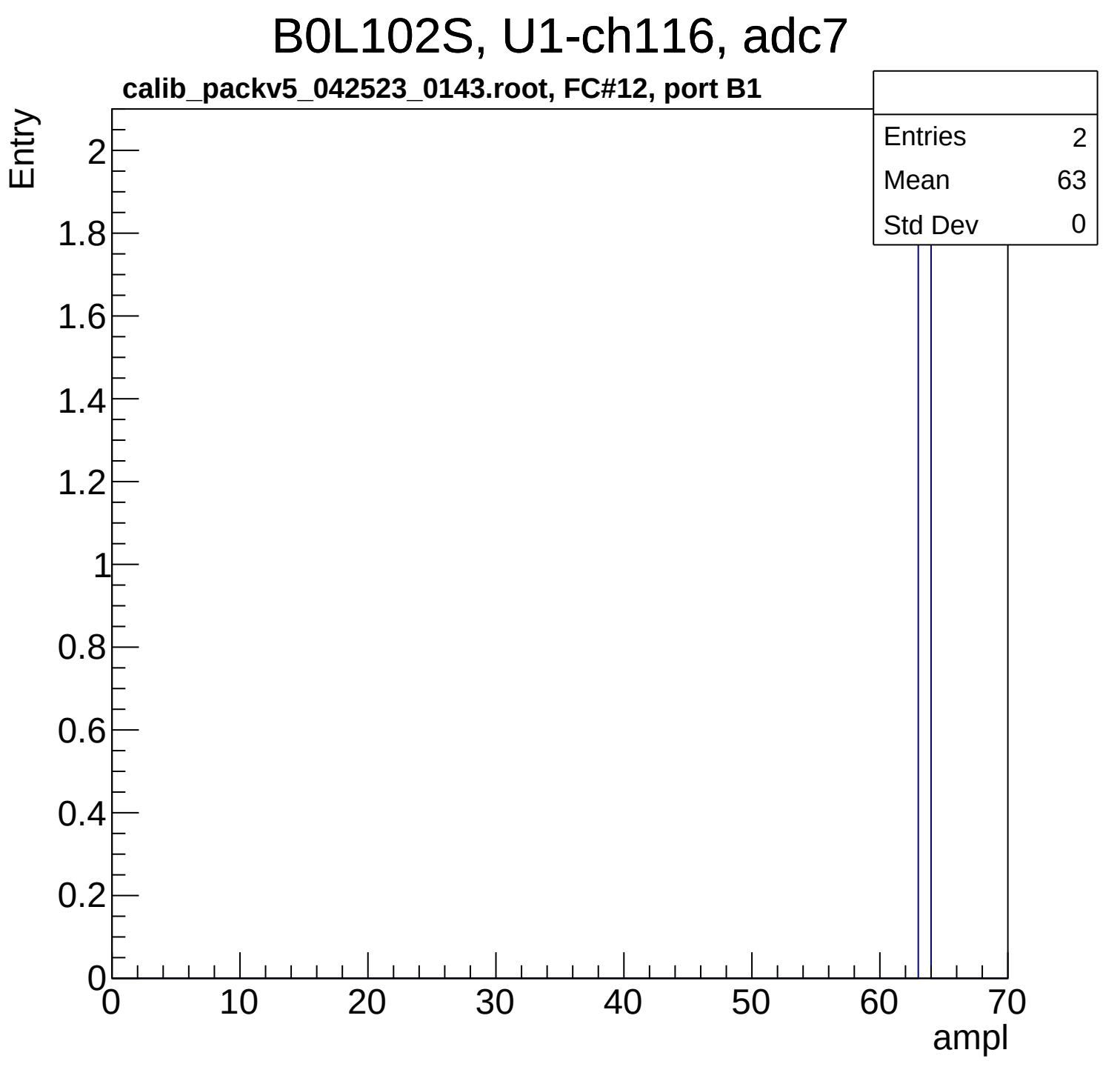
40
50
60
70


B0L102S, U1-ch116, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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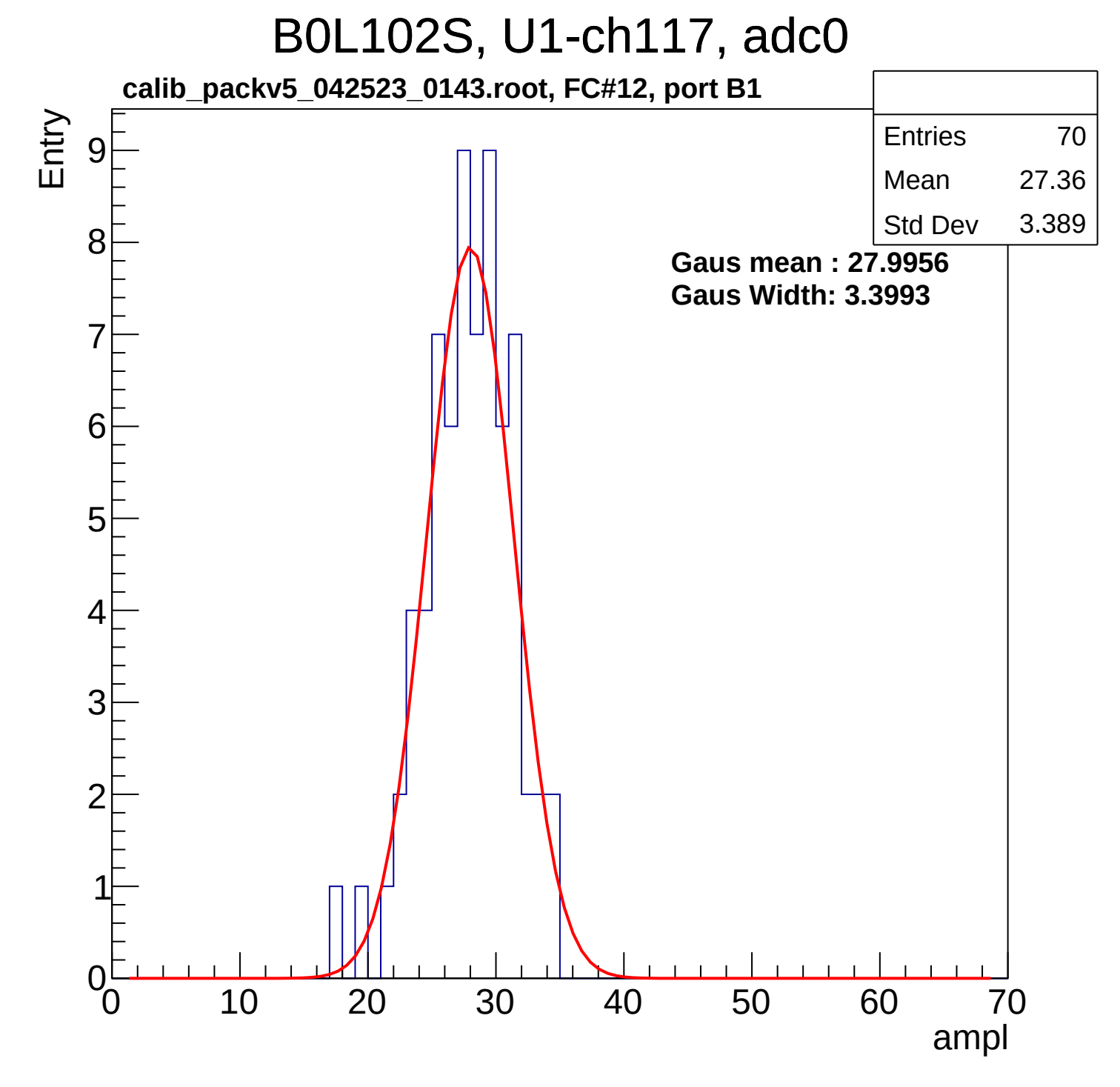
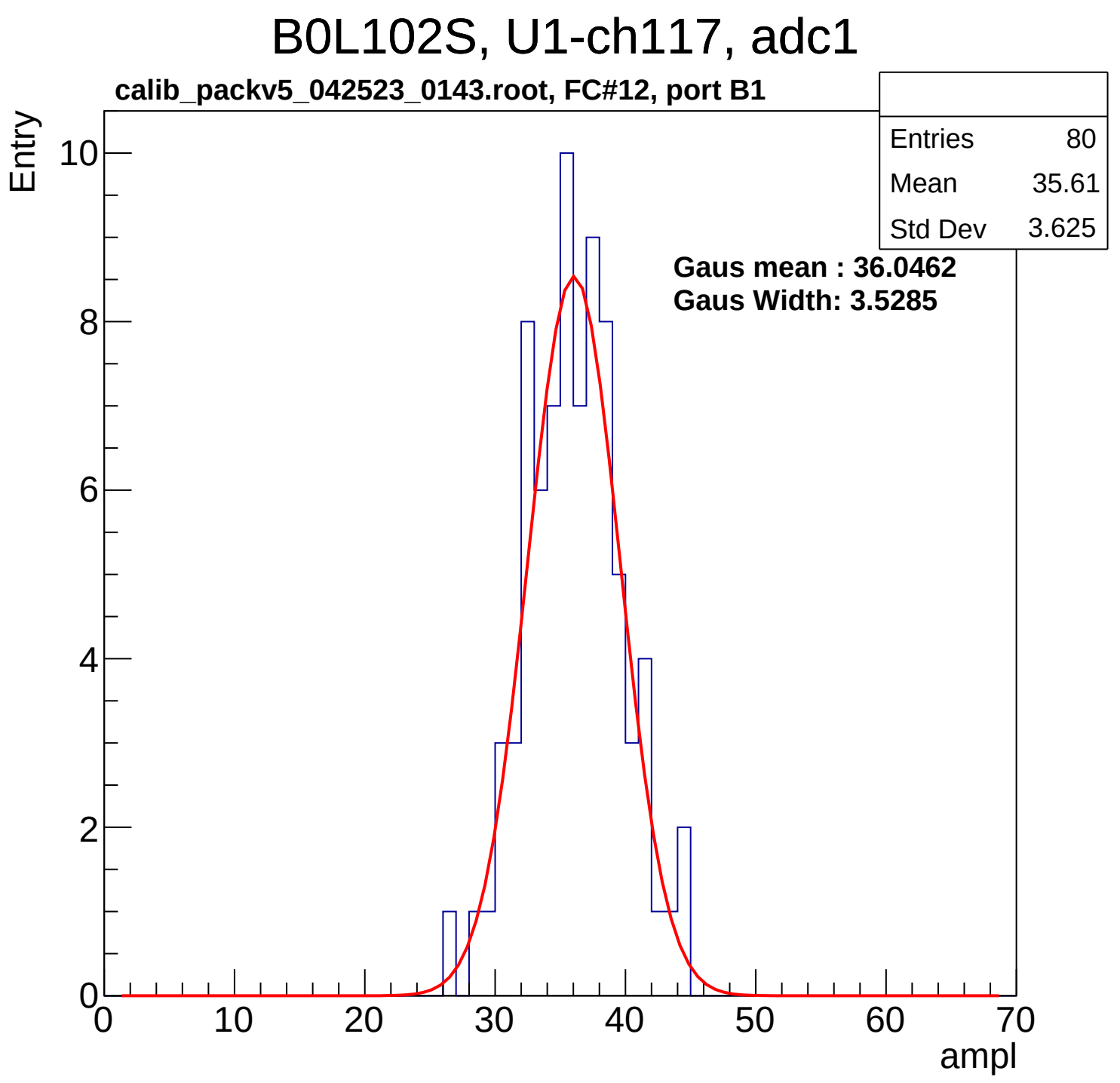
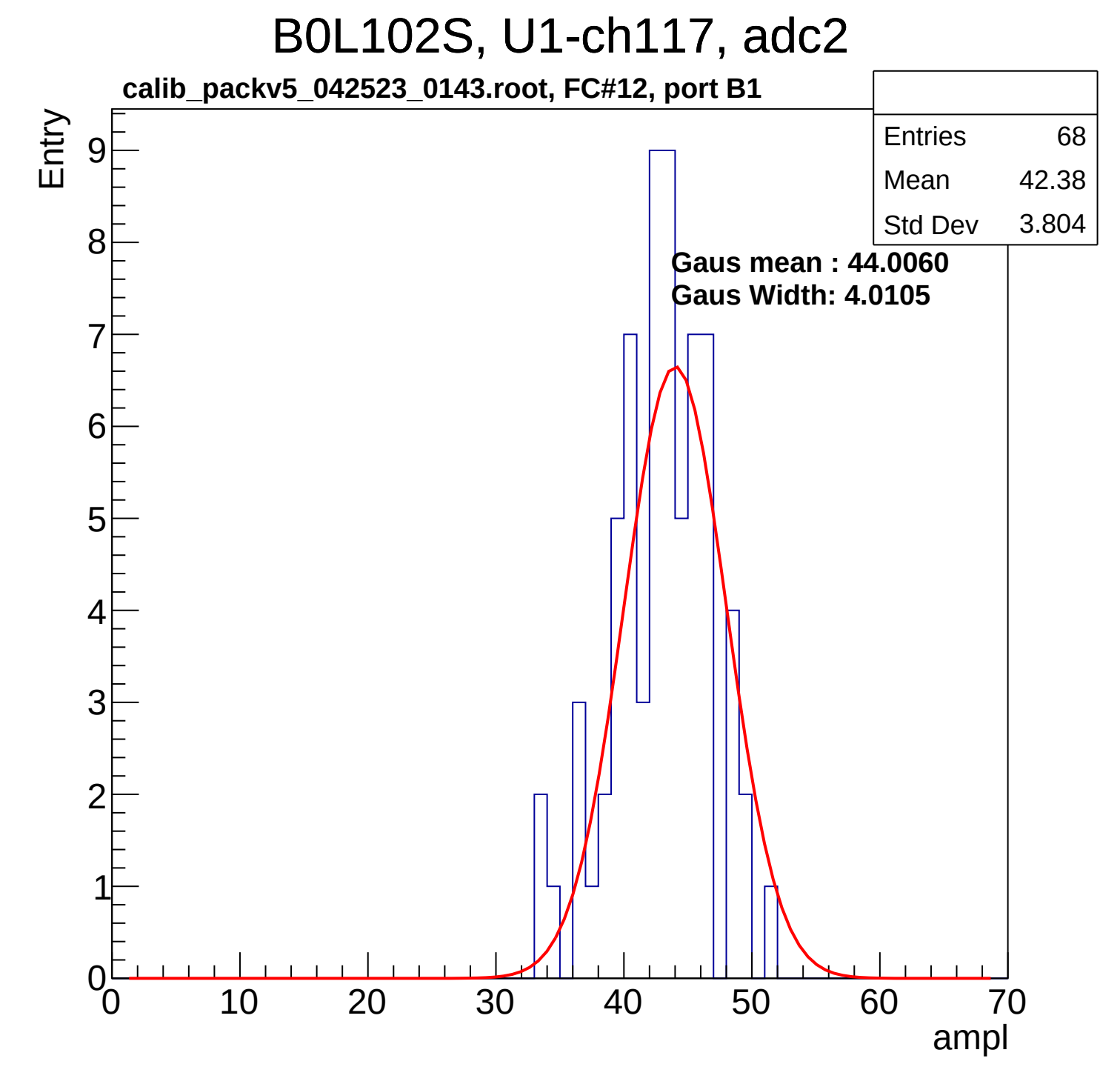
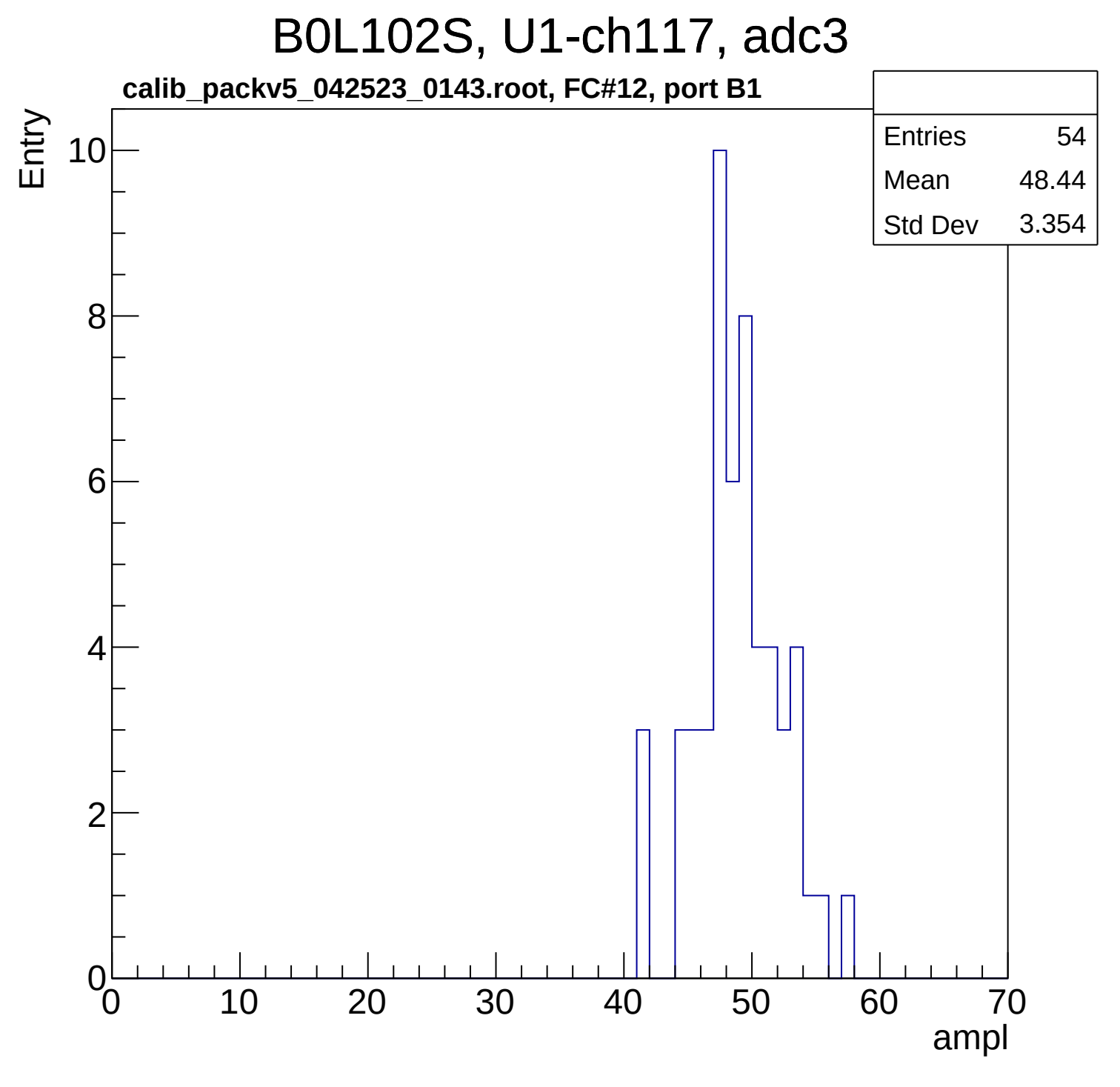
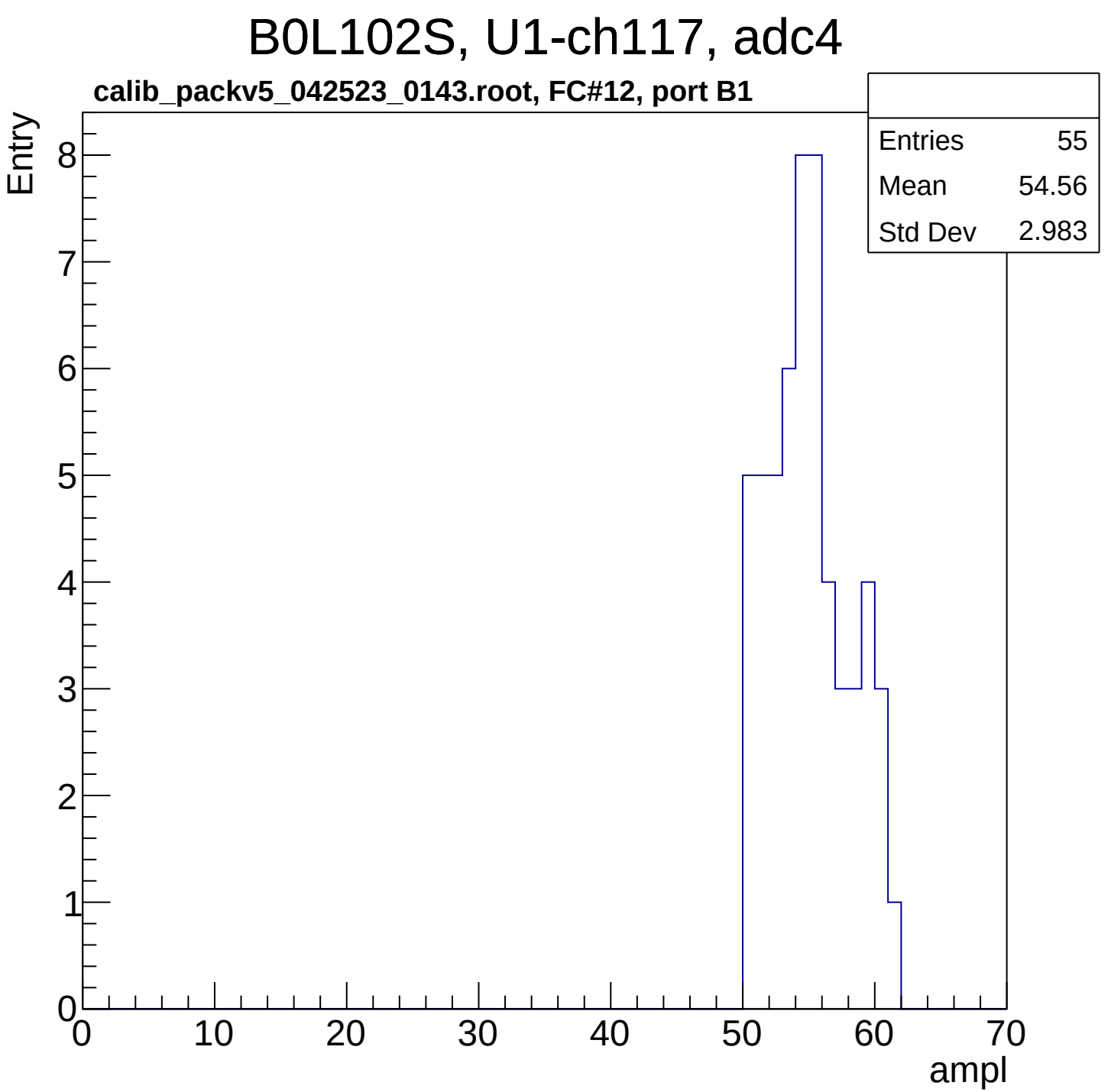
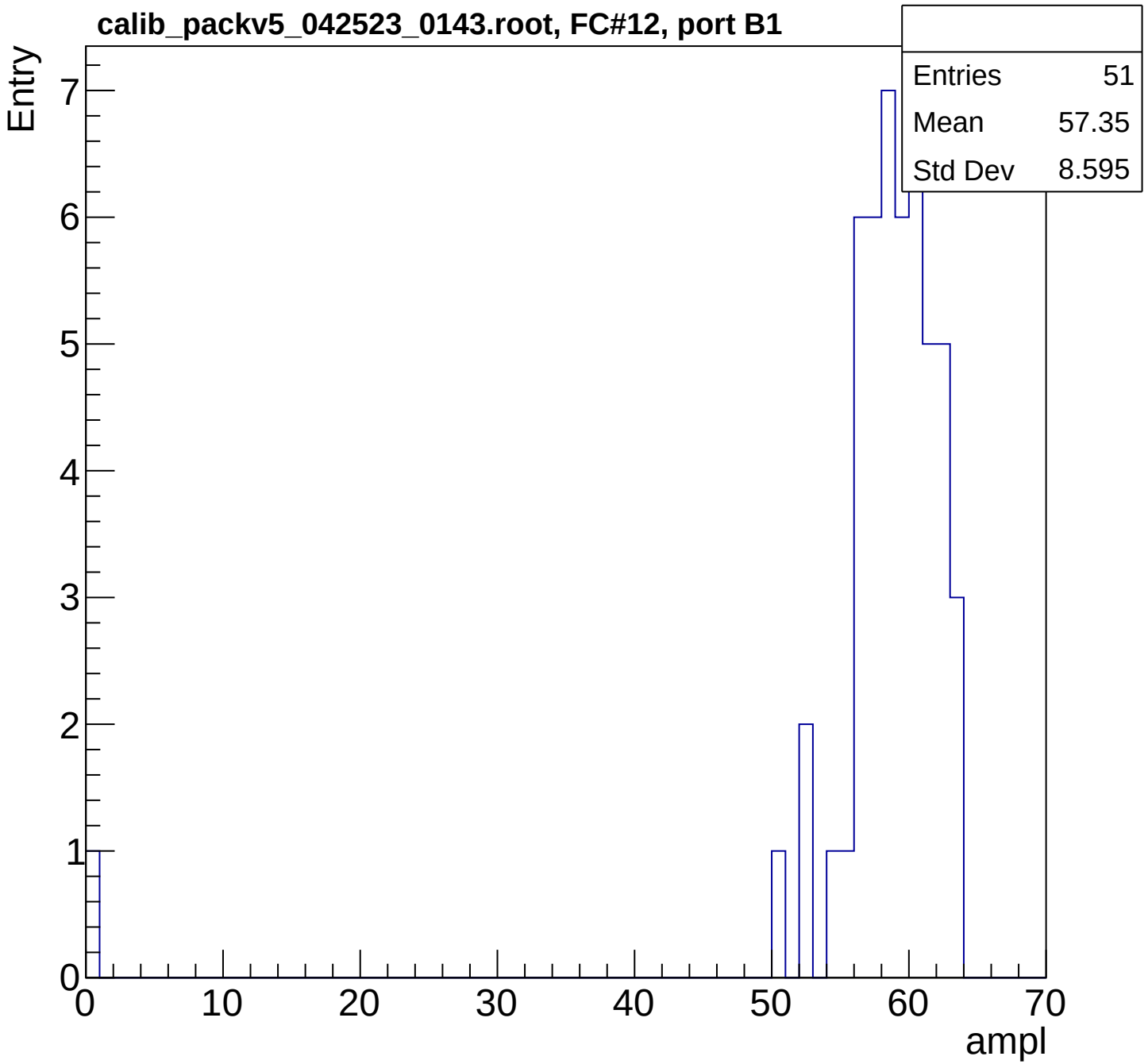

B0L102S, U1-ch117, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 27.36
Std Dev 3.389
Gaus mean : 27.9956
Gaus Width: 3.3993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

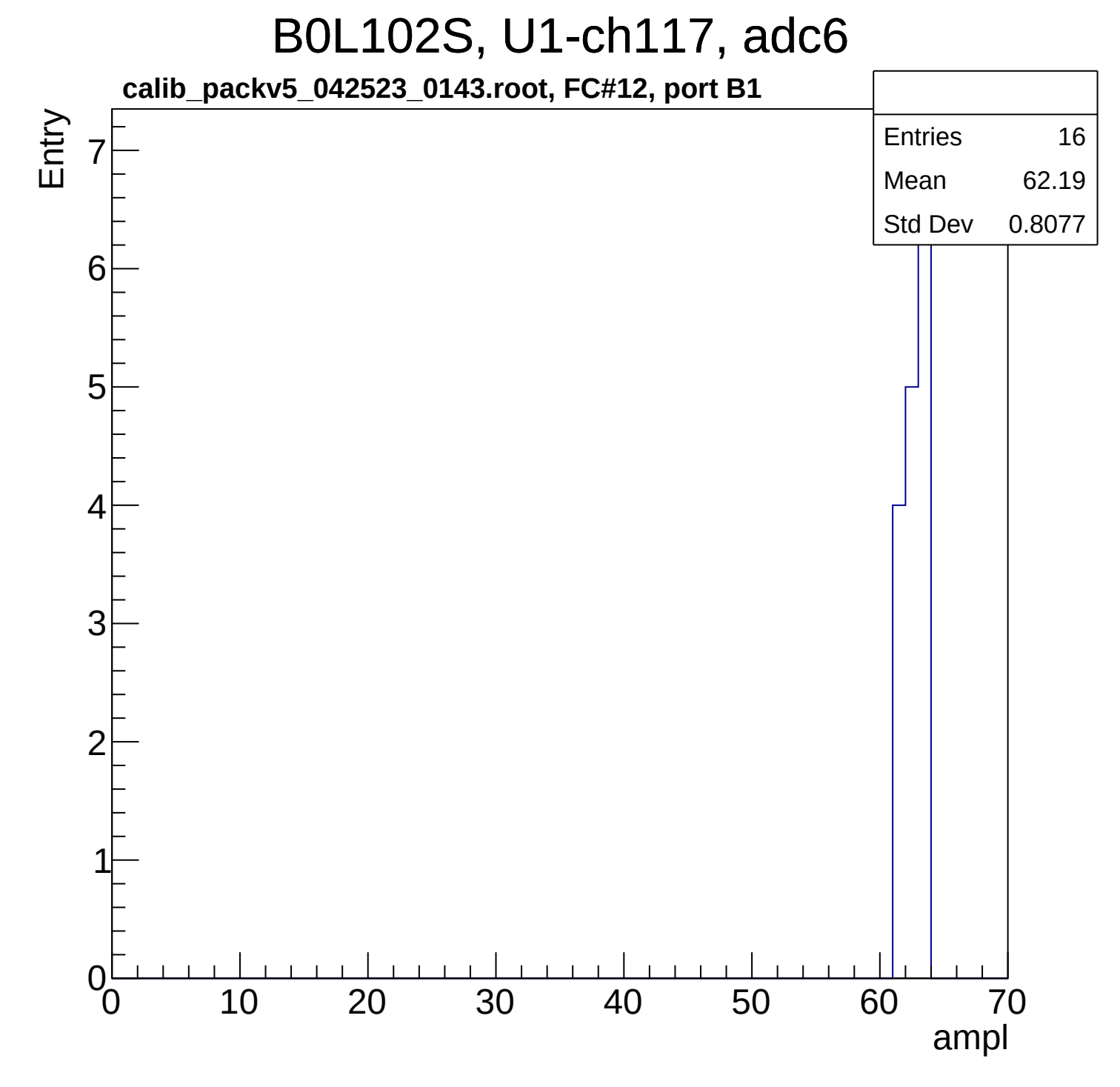

B0L102S, U1-ch117, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 35.61
Std Dev 3.625
Gaus mean : 36.0462
Gaus Width: 3.5285
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

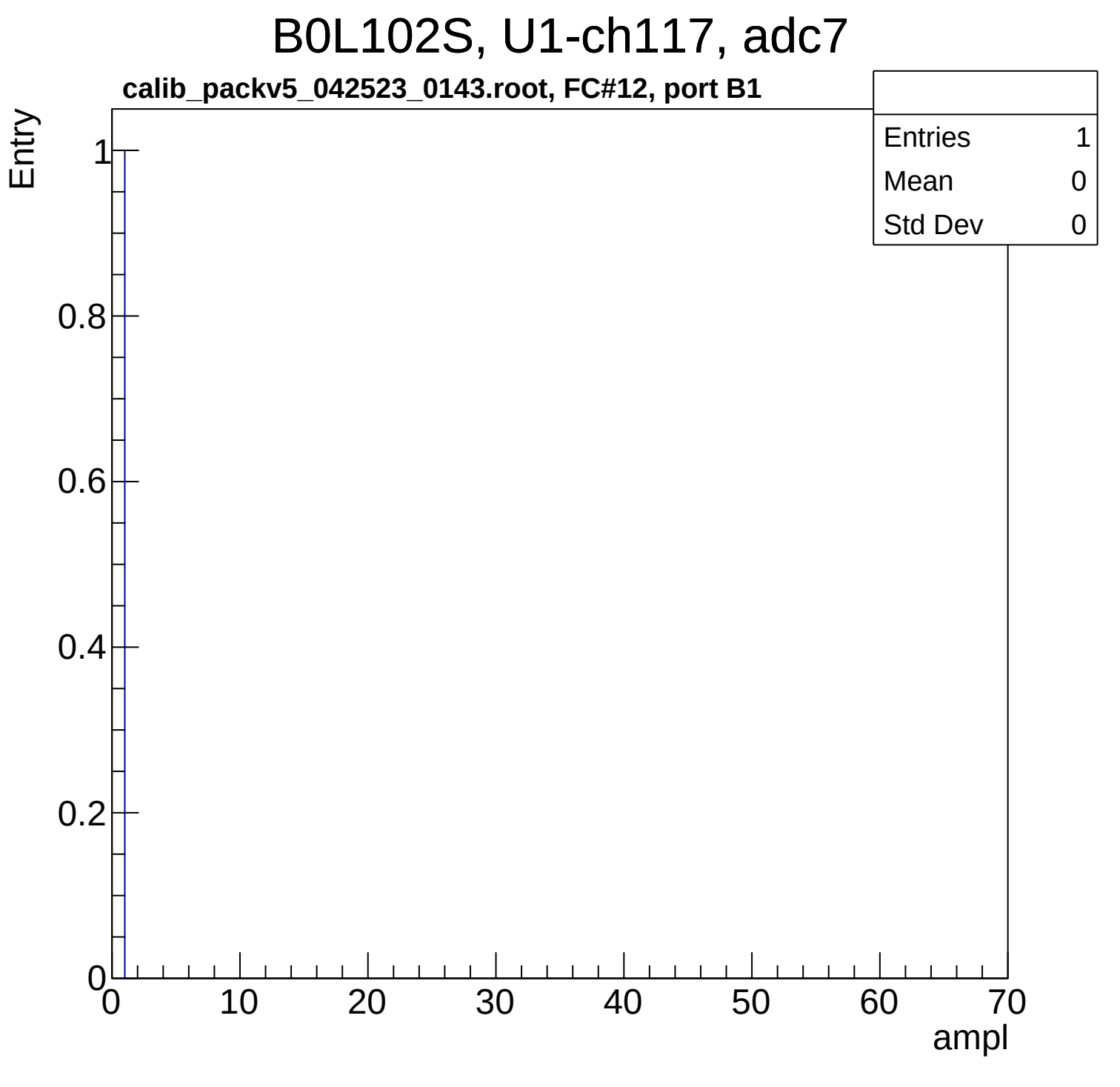

B0L102S, U1-ch117, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 42.38
Std Dev 3.804
Gaus mean : 44.0060
Gaus Width: 4.0105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

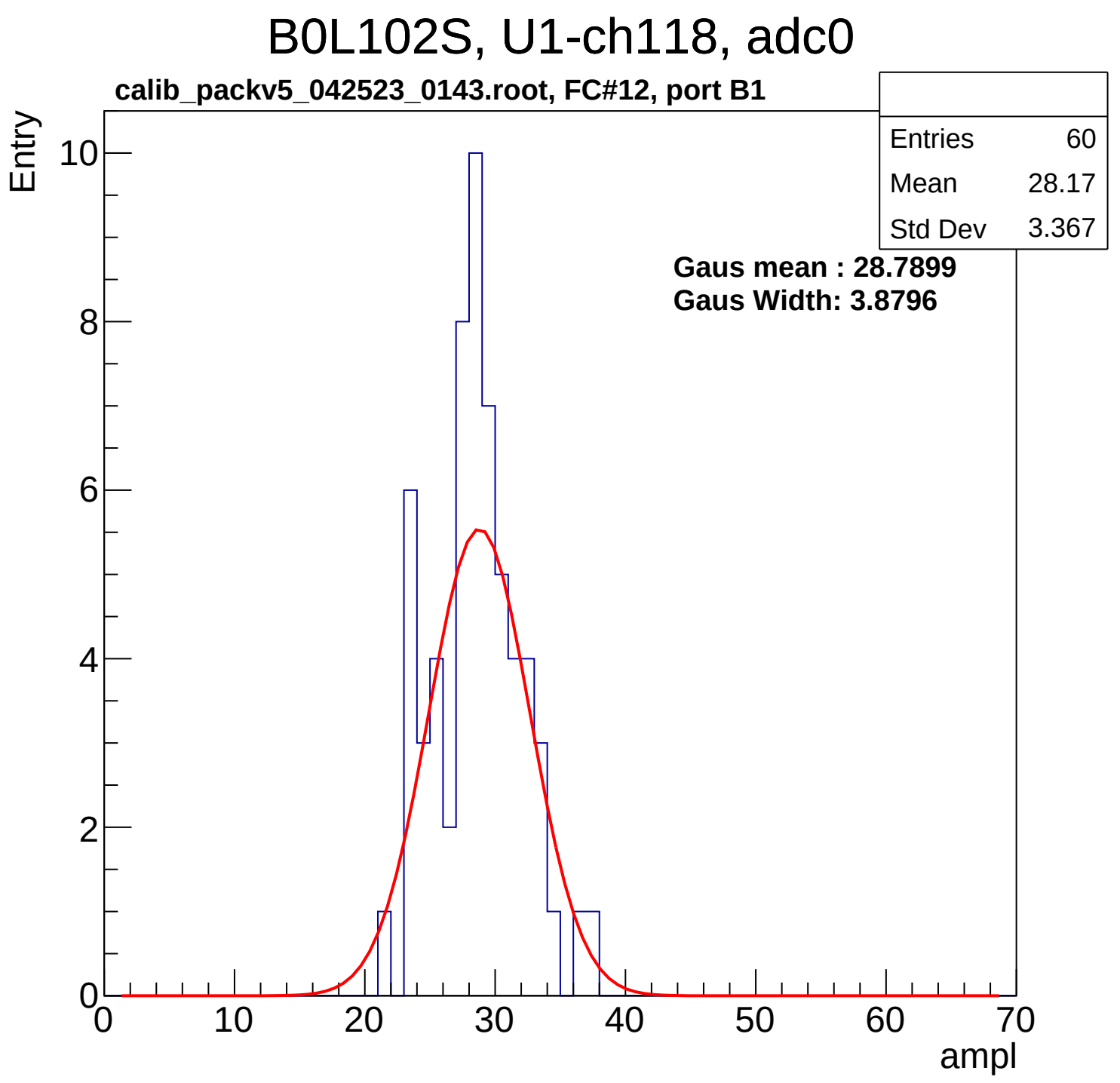

B0L102S, U1-ch117, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 48.44
Std Dev 3.354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

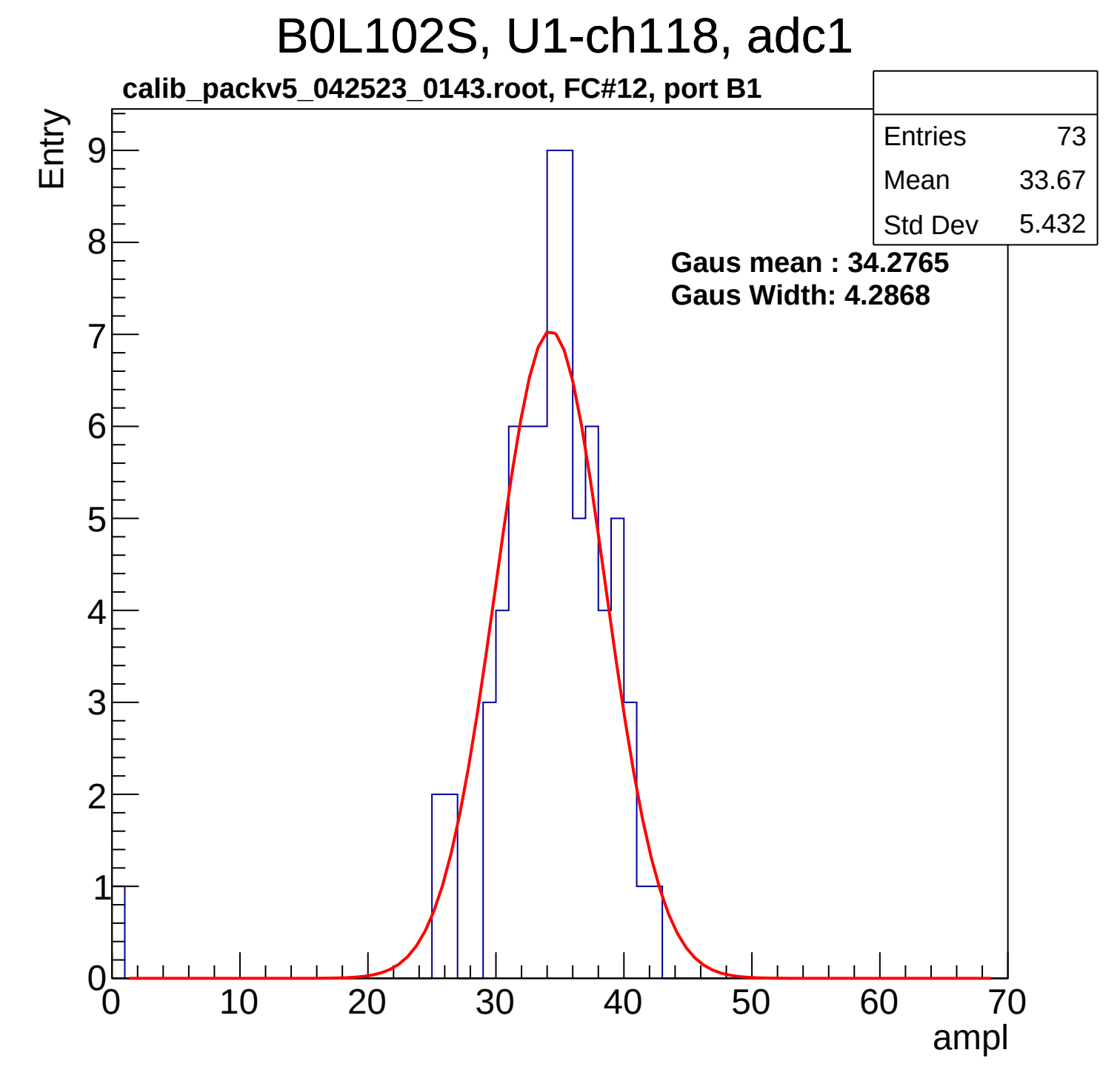

B0L102S, U1-ch117, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 54.56
Std Dev 2.983
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

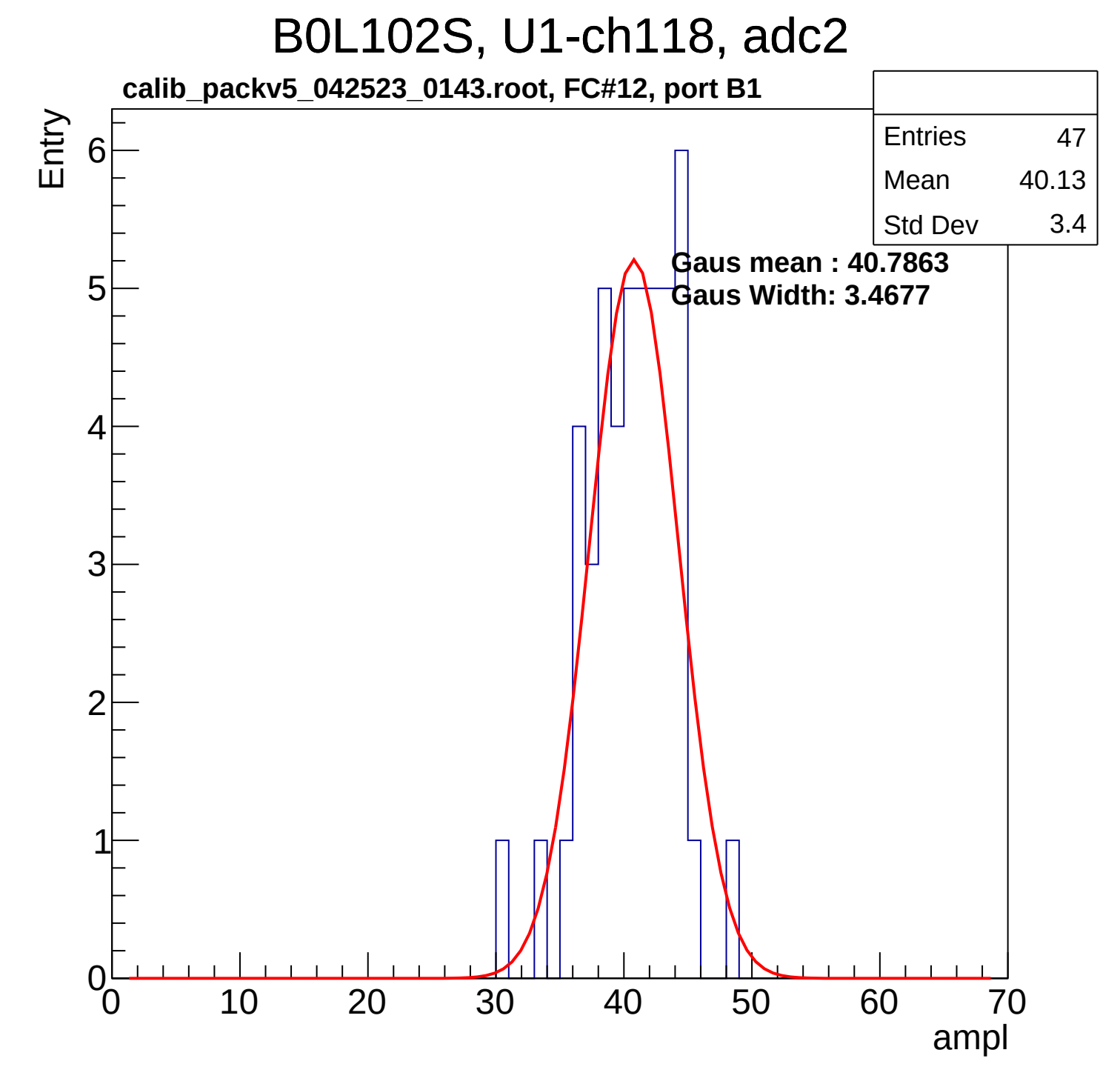

B0L102S, U1-ch117, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 57.35
Std Dev 8.595
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

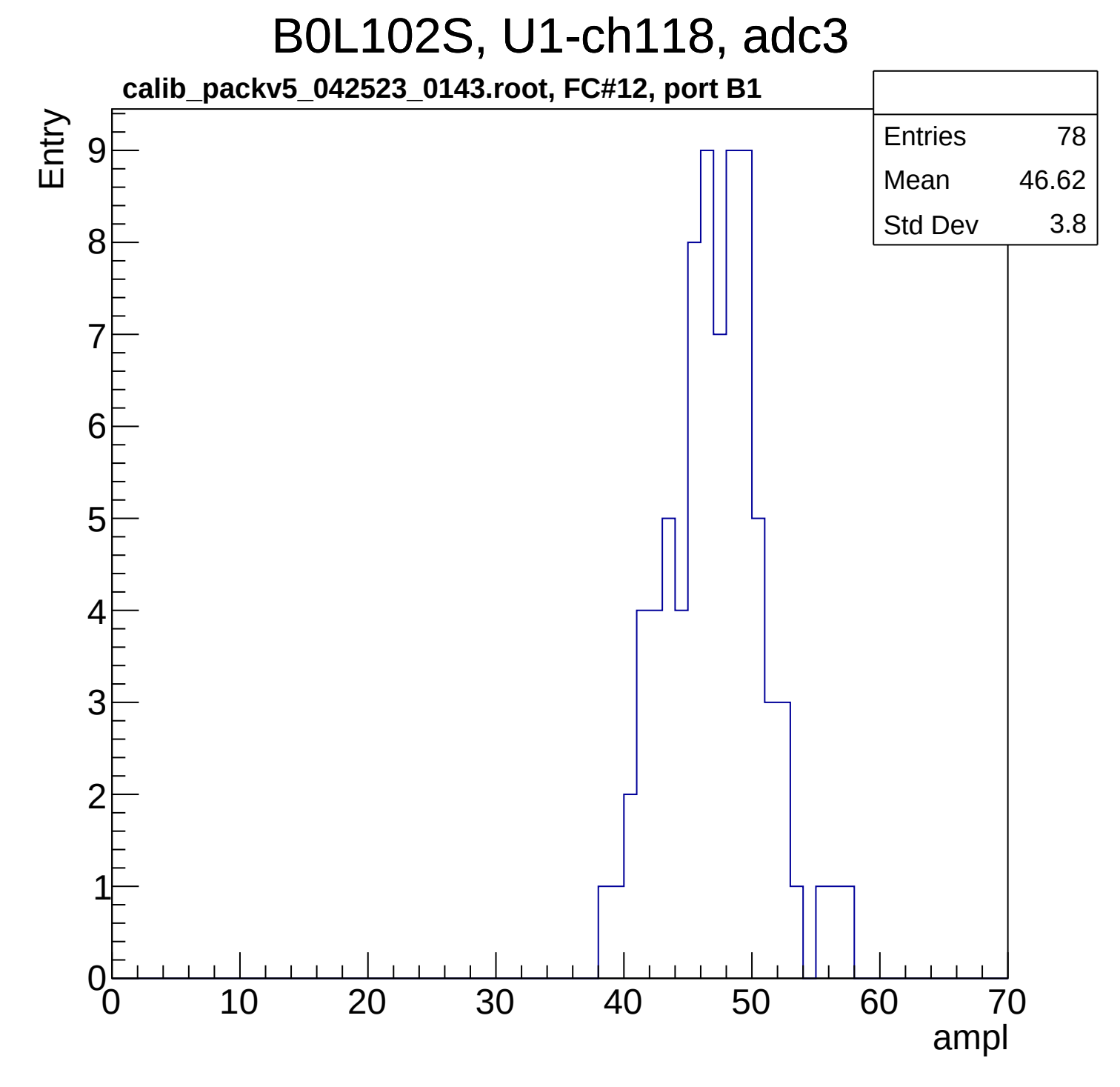

B0L102S, U1-ch117, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 62.19
Std Dev 0.8077
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

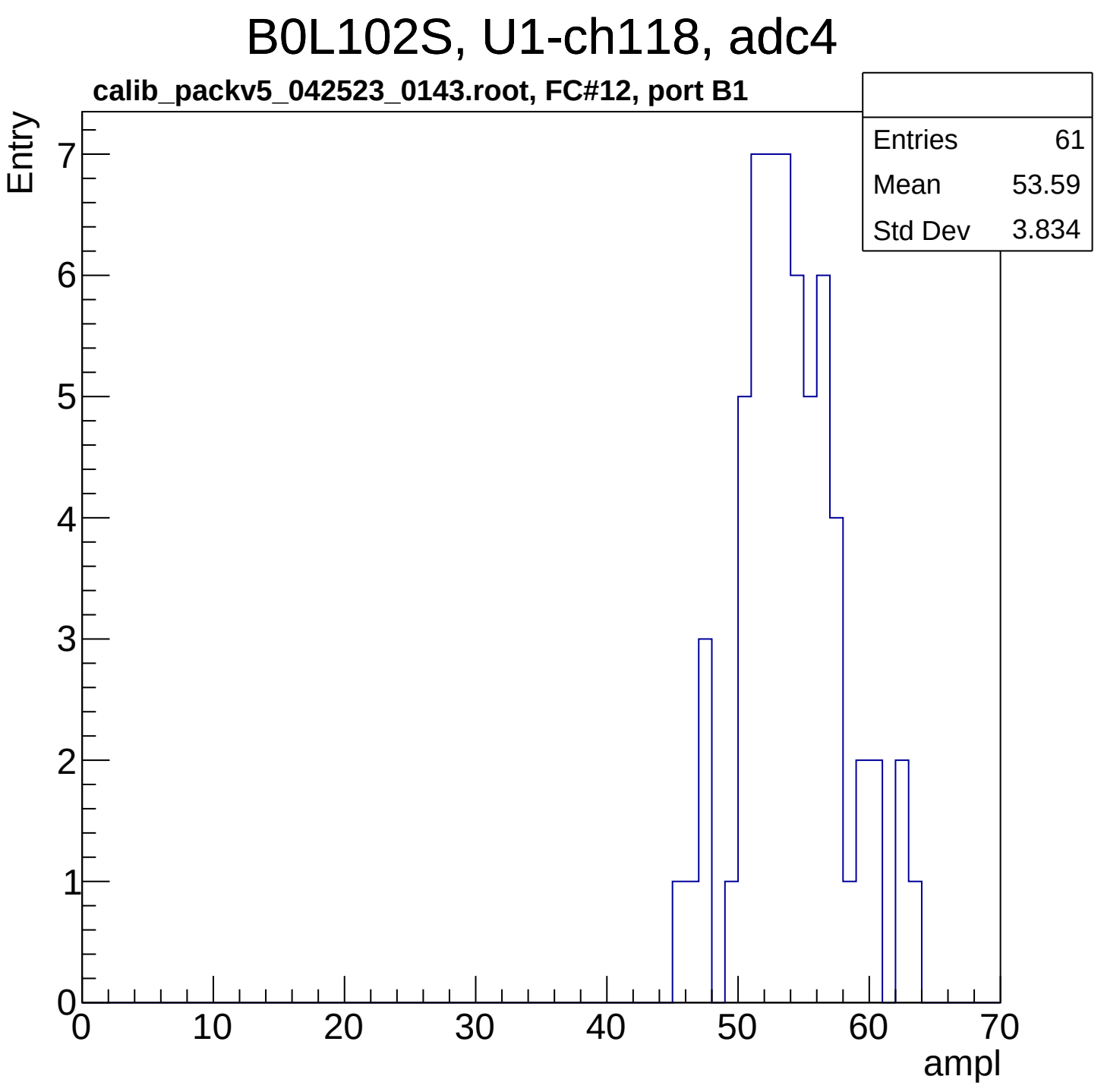

B0L102S, U1-ch117, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

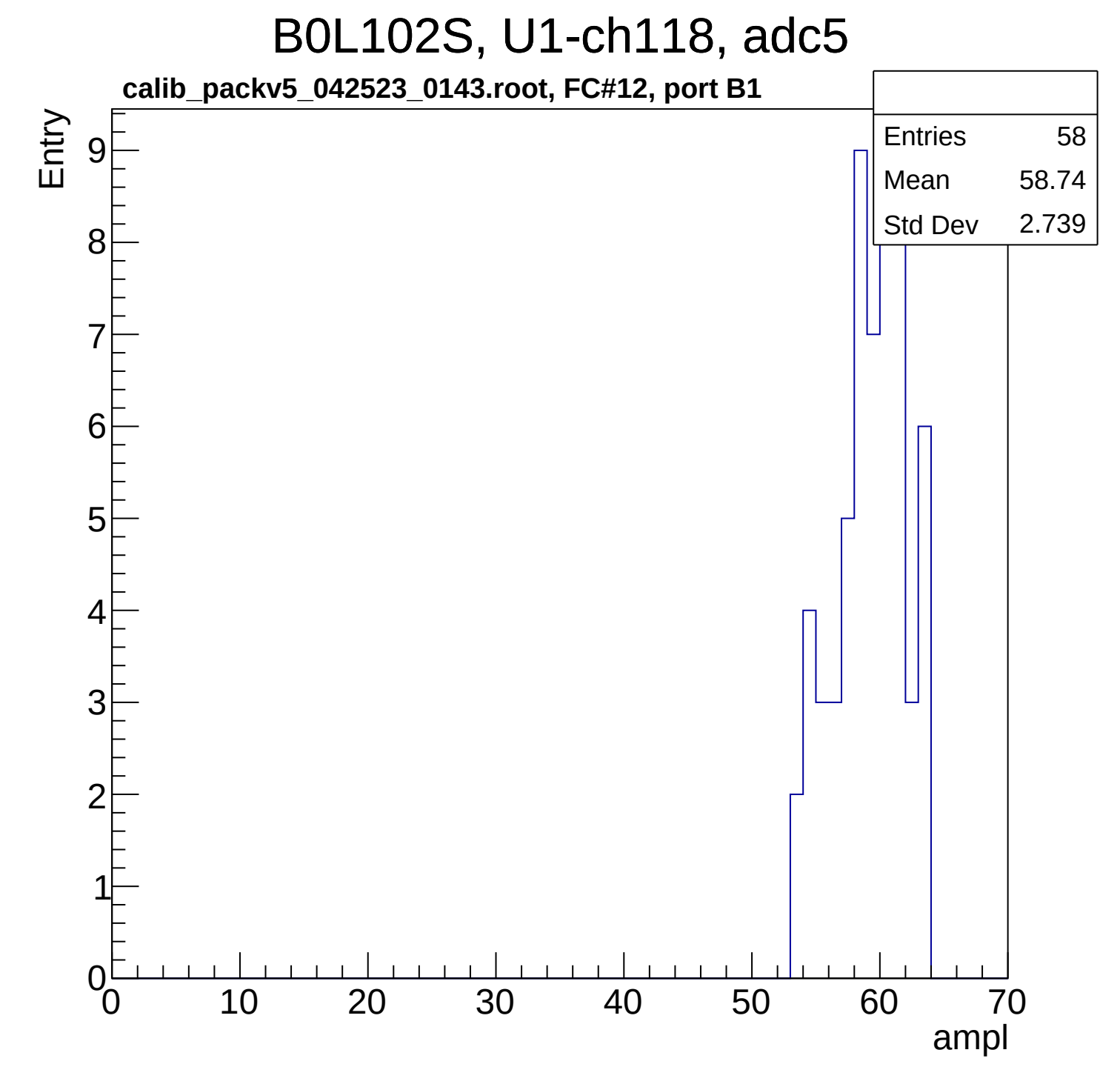

B0L102S, U1-ch118, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 28.17
Std Dev 3.367
Gaus mean : 28.7899
Gaus Width: 3.8796
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch118, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 33.67
Std Dev 5.432
Gaus mean : 34.2765
Gaus Width: 4.2868
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch118, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 40.13
Std Dev 3.4
Gaus mean : 40.7863
Gaus Width: 3.4677
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U1-ch118, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 46.62
Std Dev 3.8
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch118, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 53.59
Std Dev 3.834
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch118, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 58.74
Std Dev 2.739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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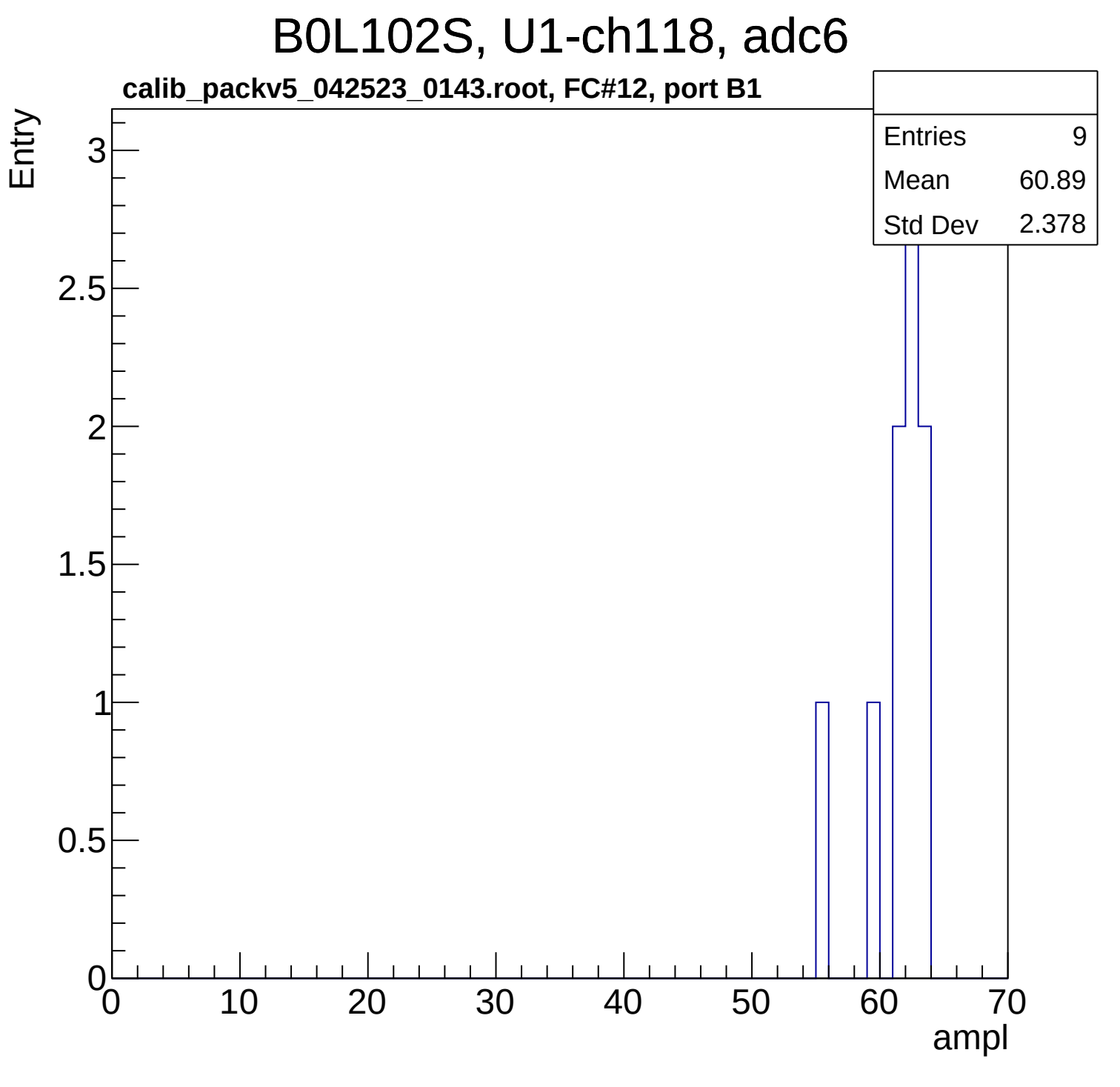
B0L102S, U1-ch118, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 60.89
Std Dev 2.378
Entry
ampl
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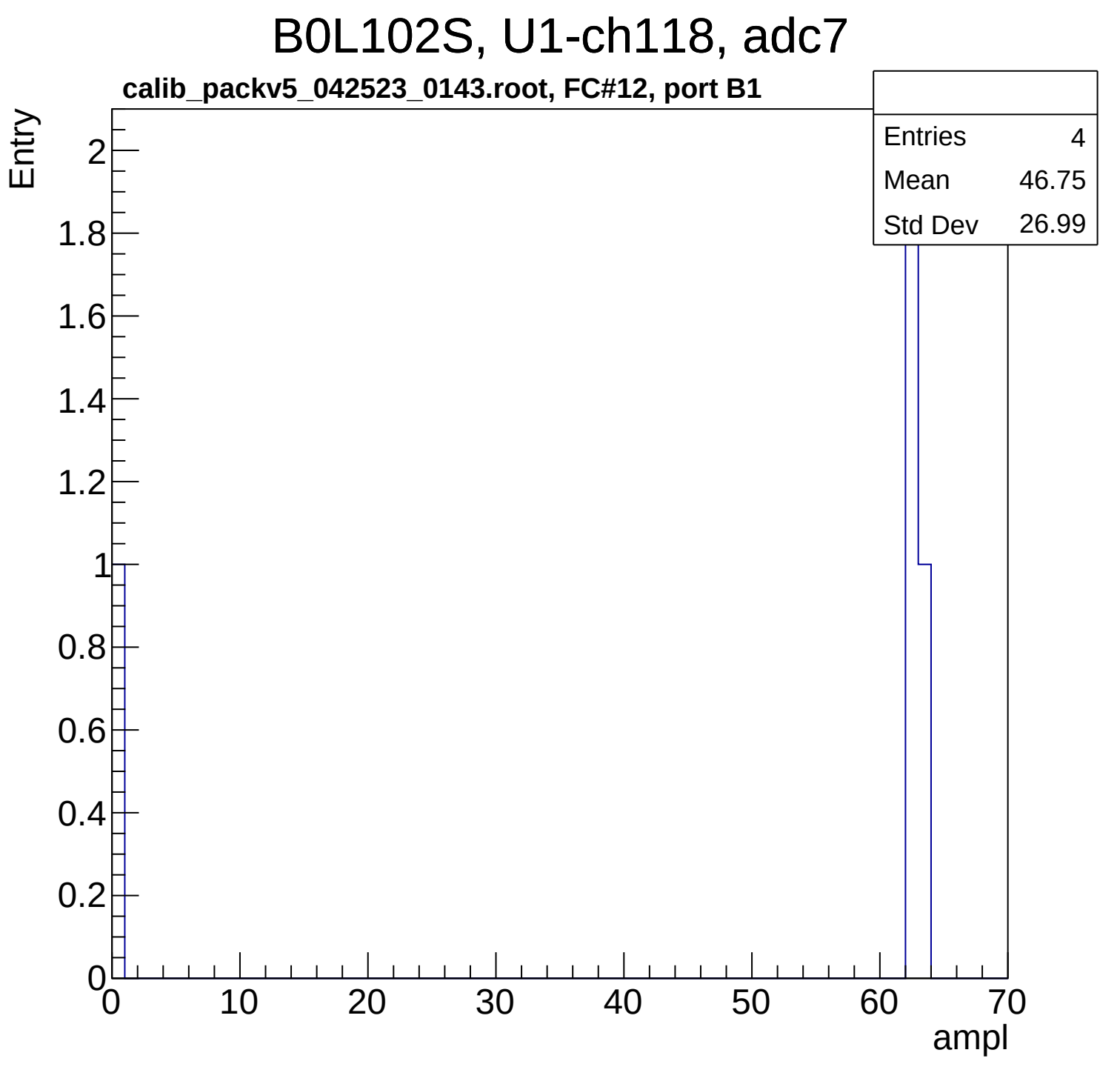
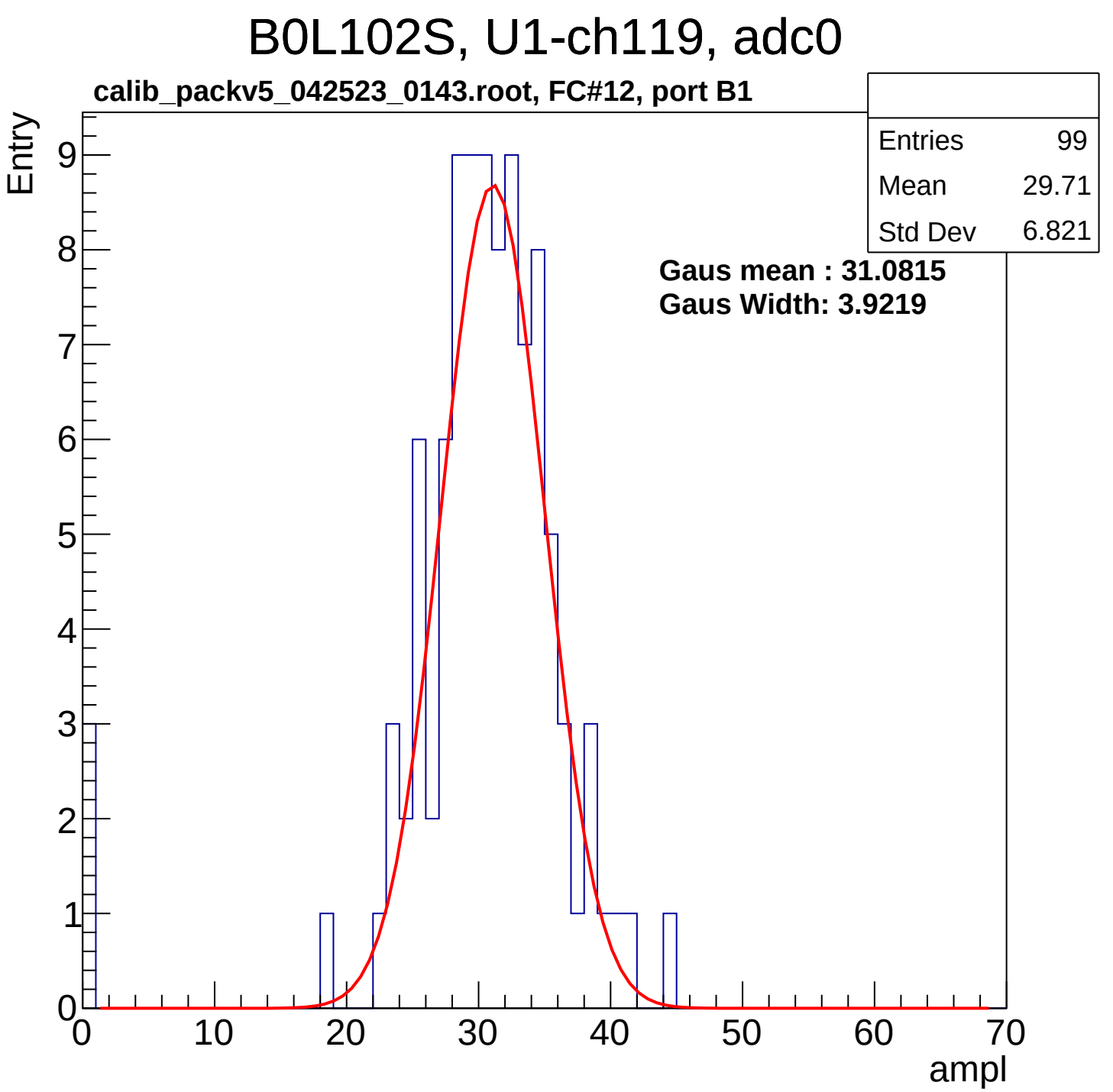
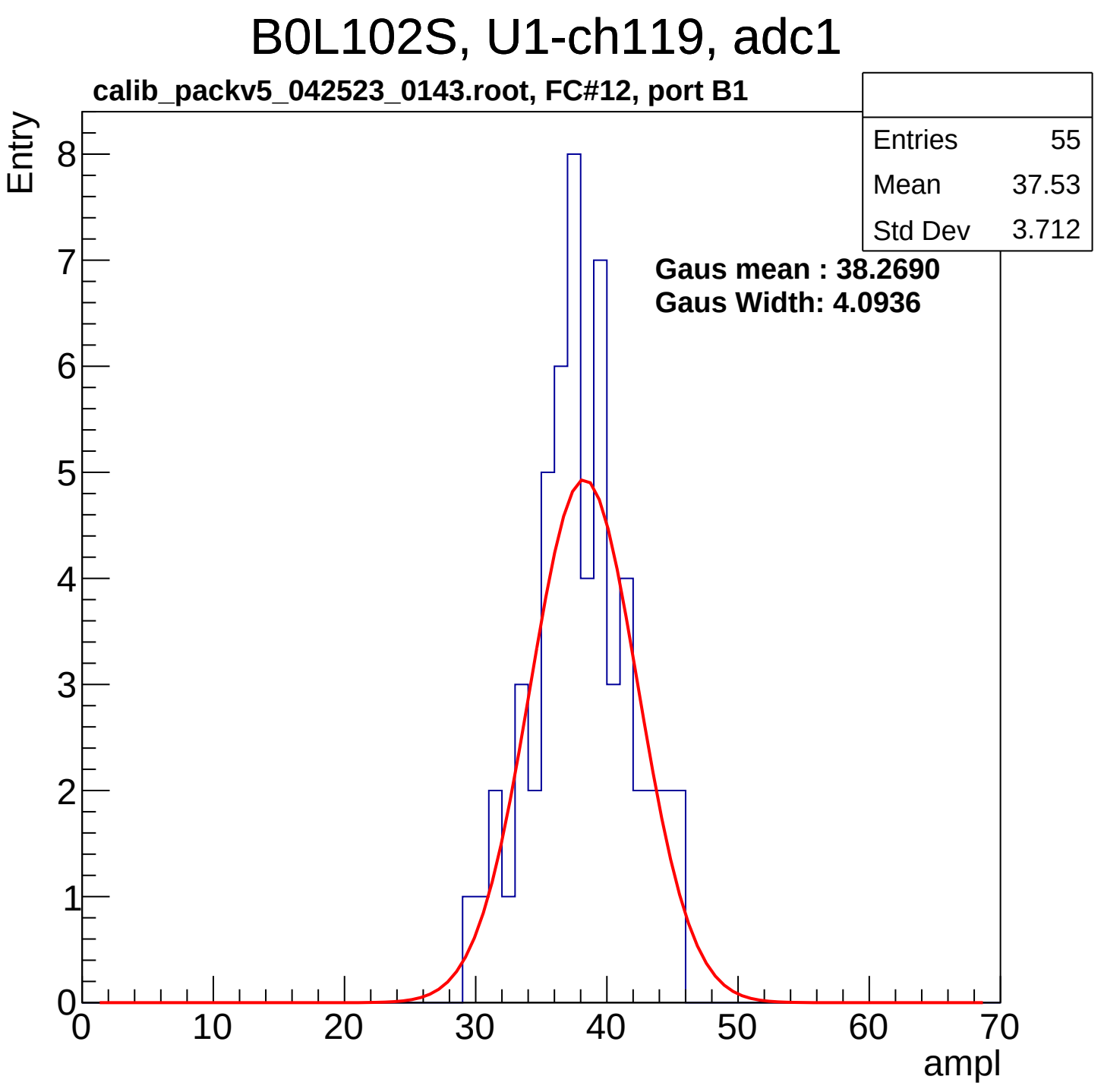
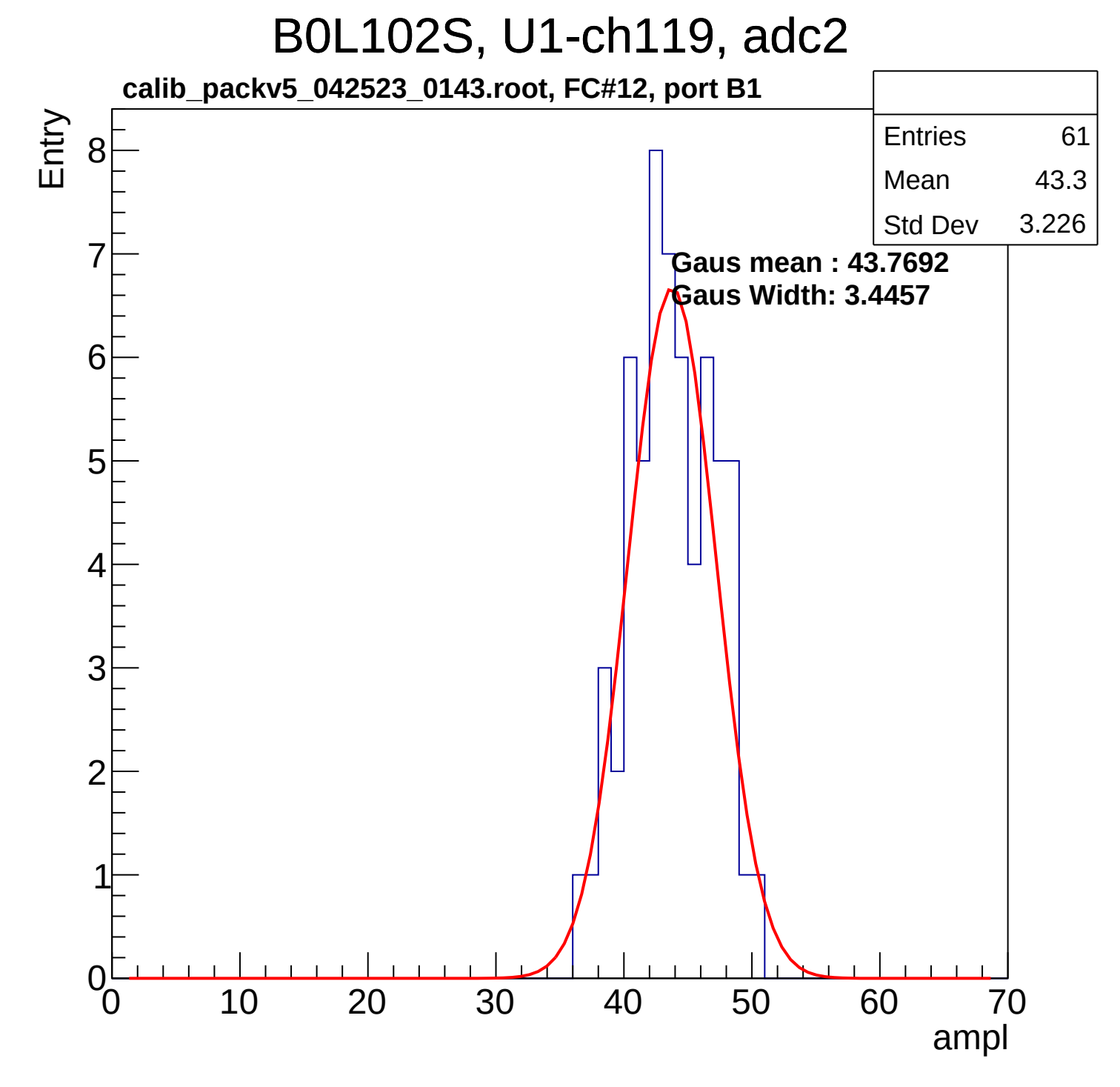
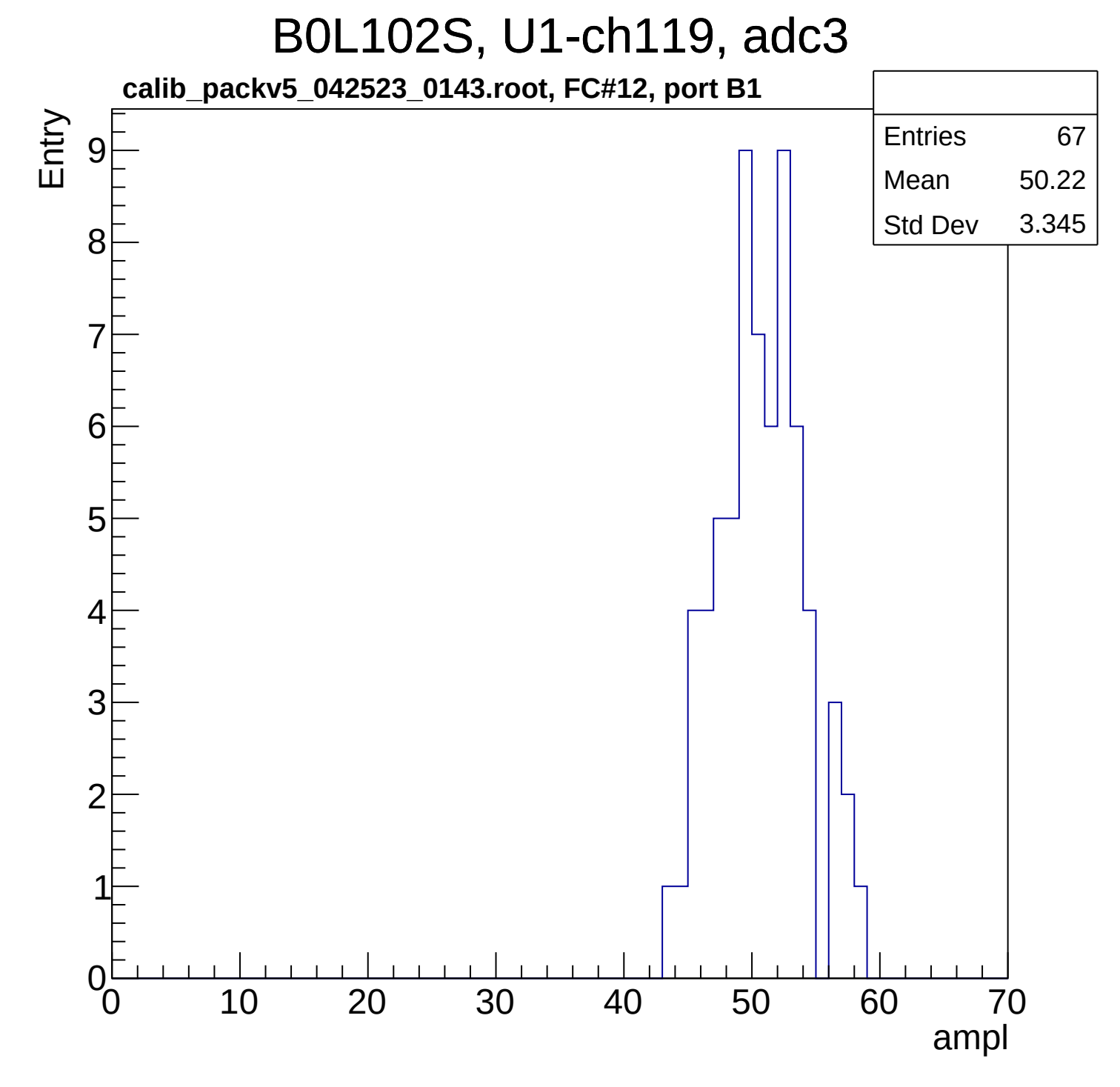
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch118, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch119, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 99
Mean 29.71
Std Dev 6.821
Gaus mean : 31.0815
Gaus Width: 3.9219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch119, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 37.53
Std Dev 3.712
Gaus mean : 38.2690
Gaus Width: 4.0936
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch119, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 43.3
Std Dev 3.226
Gaus mean : 43.7692
Gaus Width: 3.4457
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch119, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.22
Std Dev 3.345
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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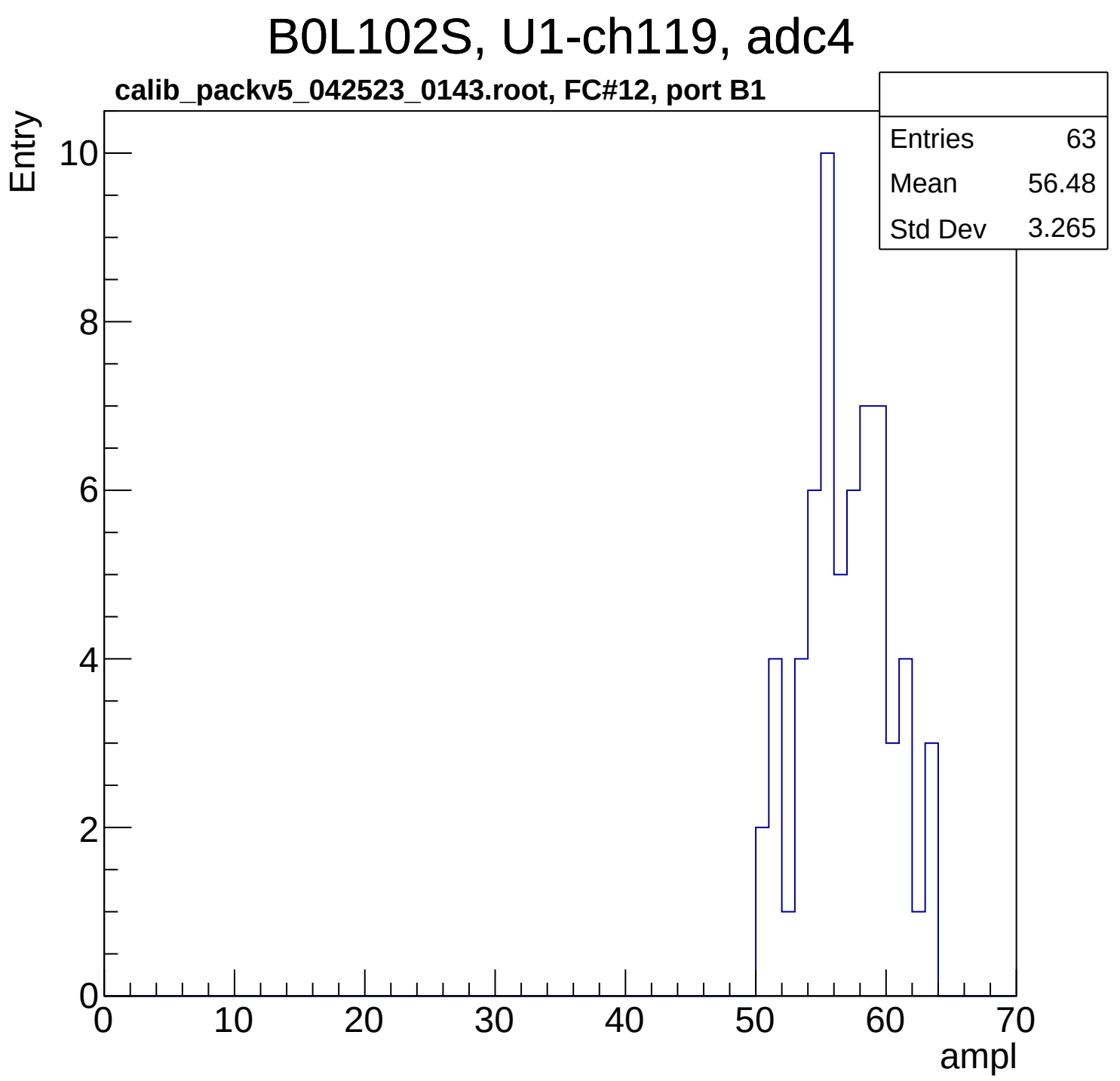
70


B0L102S, U1-ch119, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 56.48
Std Dev 3.265
Entry
0
2
4
6
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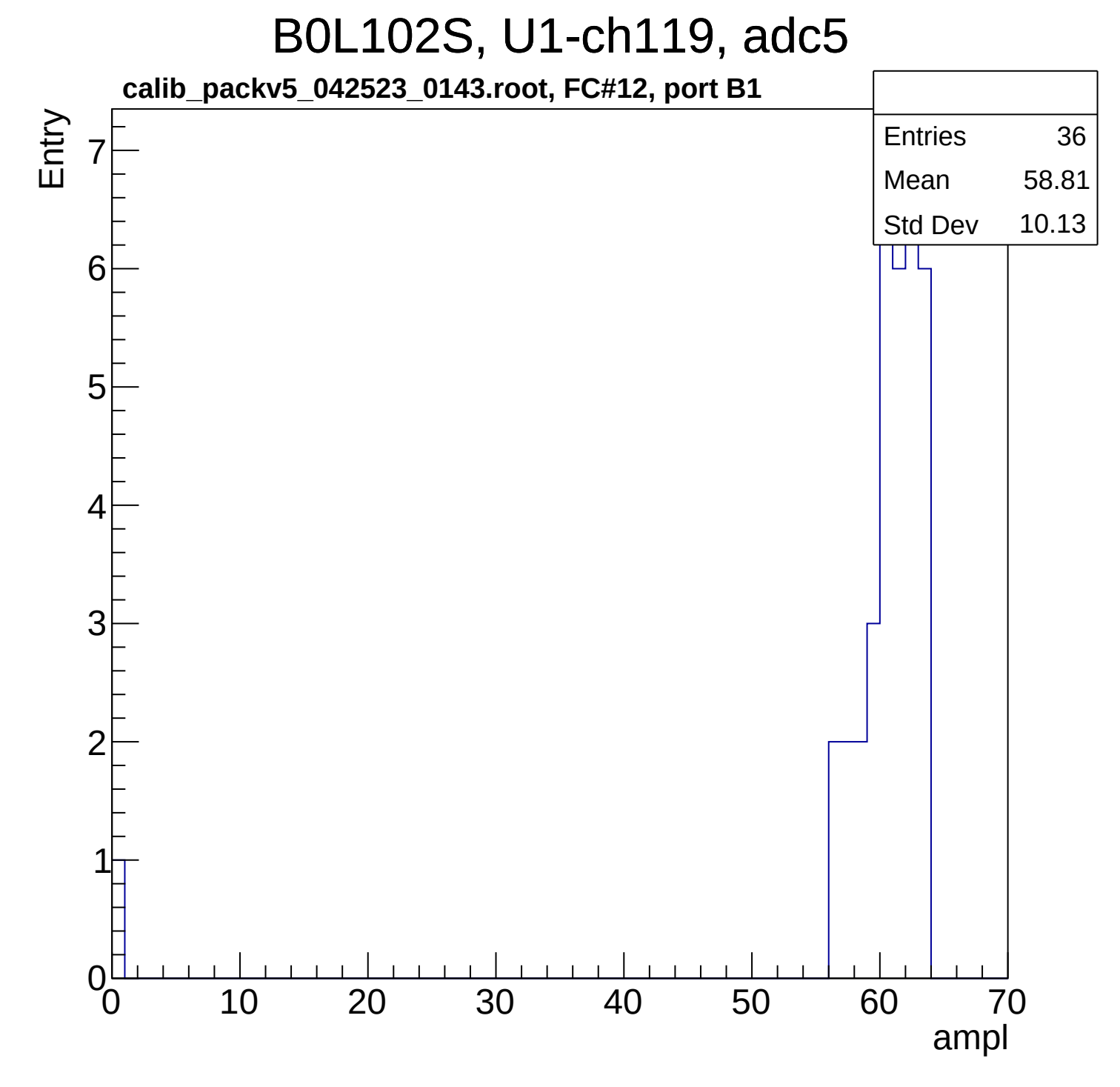
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch119, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.81
Std Dev 10.13
Entry
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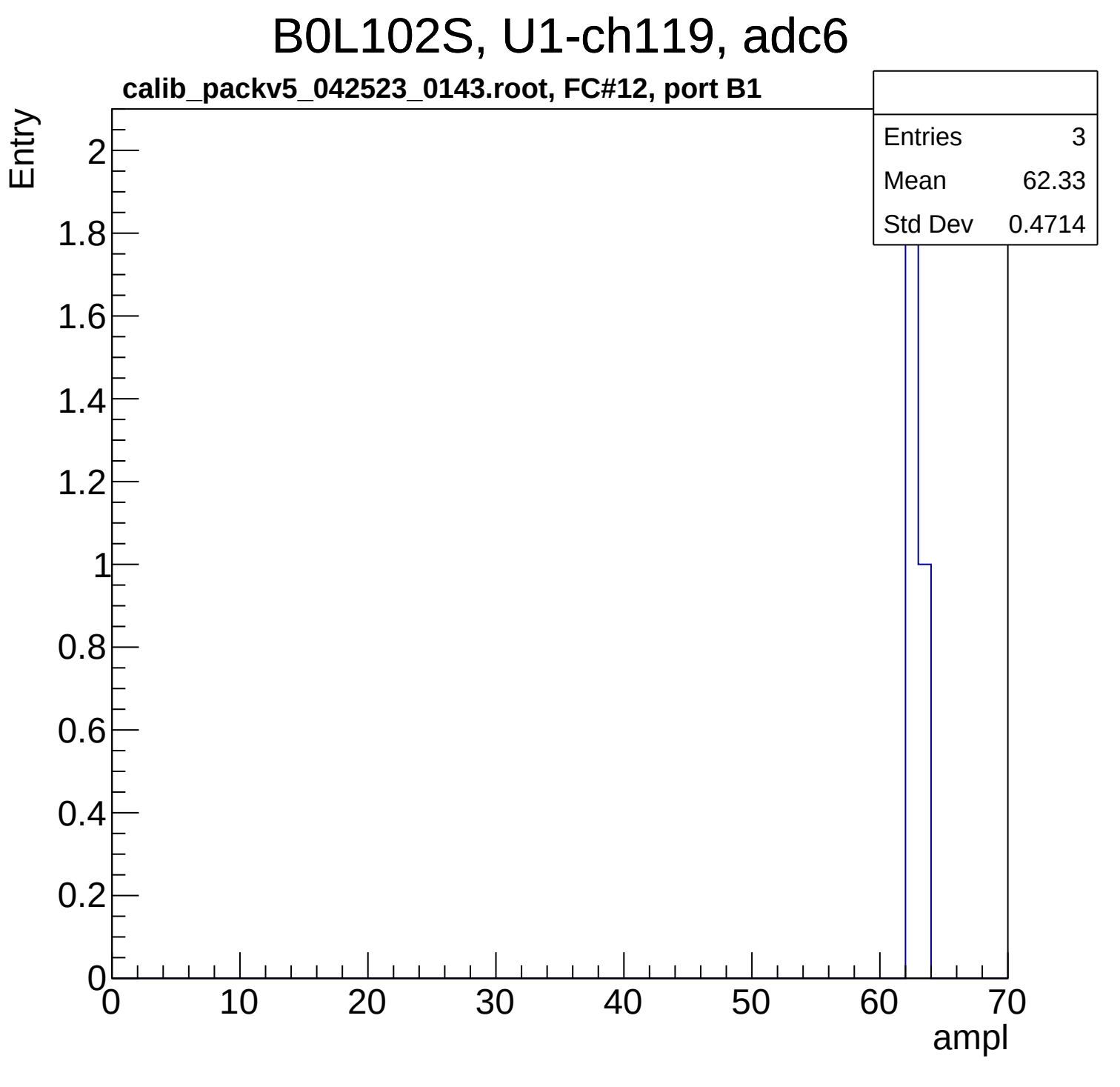
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch119, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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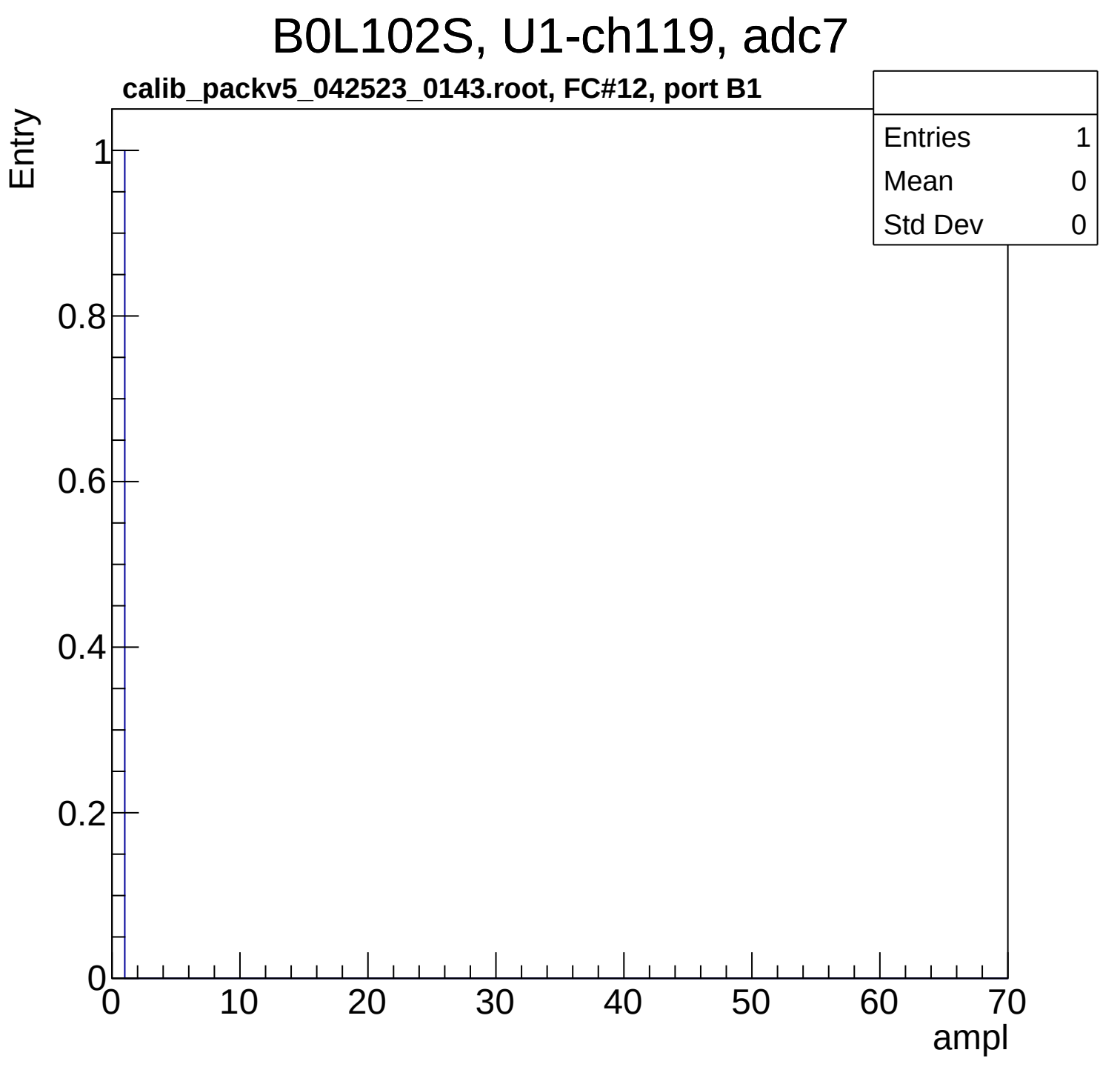
30
40
50
60
70


B0L102S, U1-ch119, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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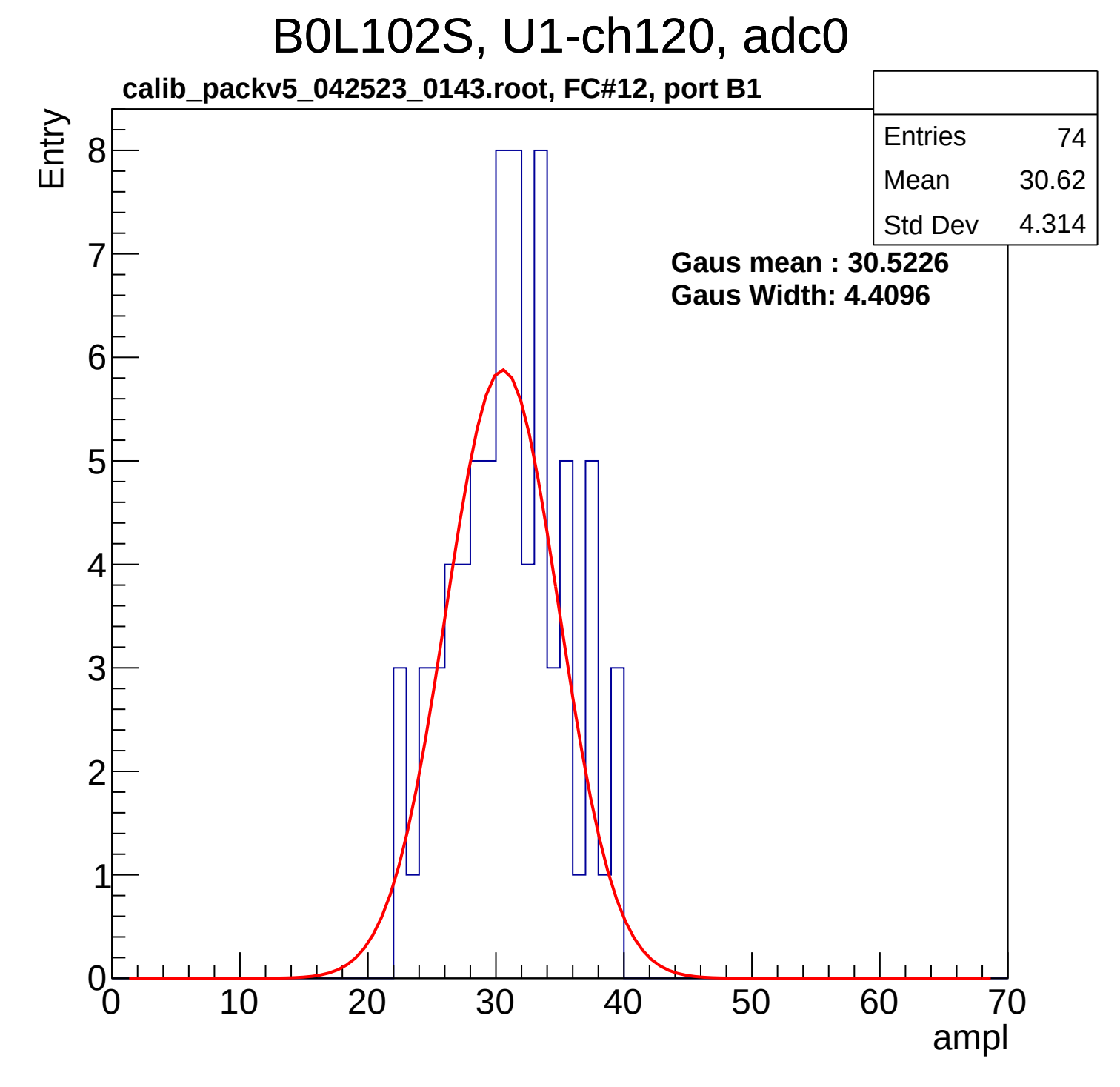
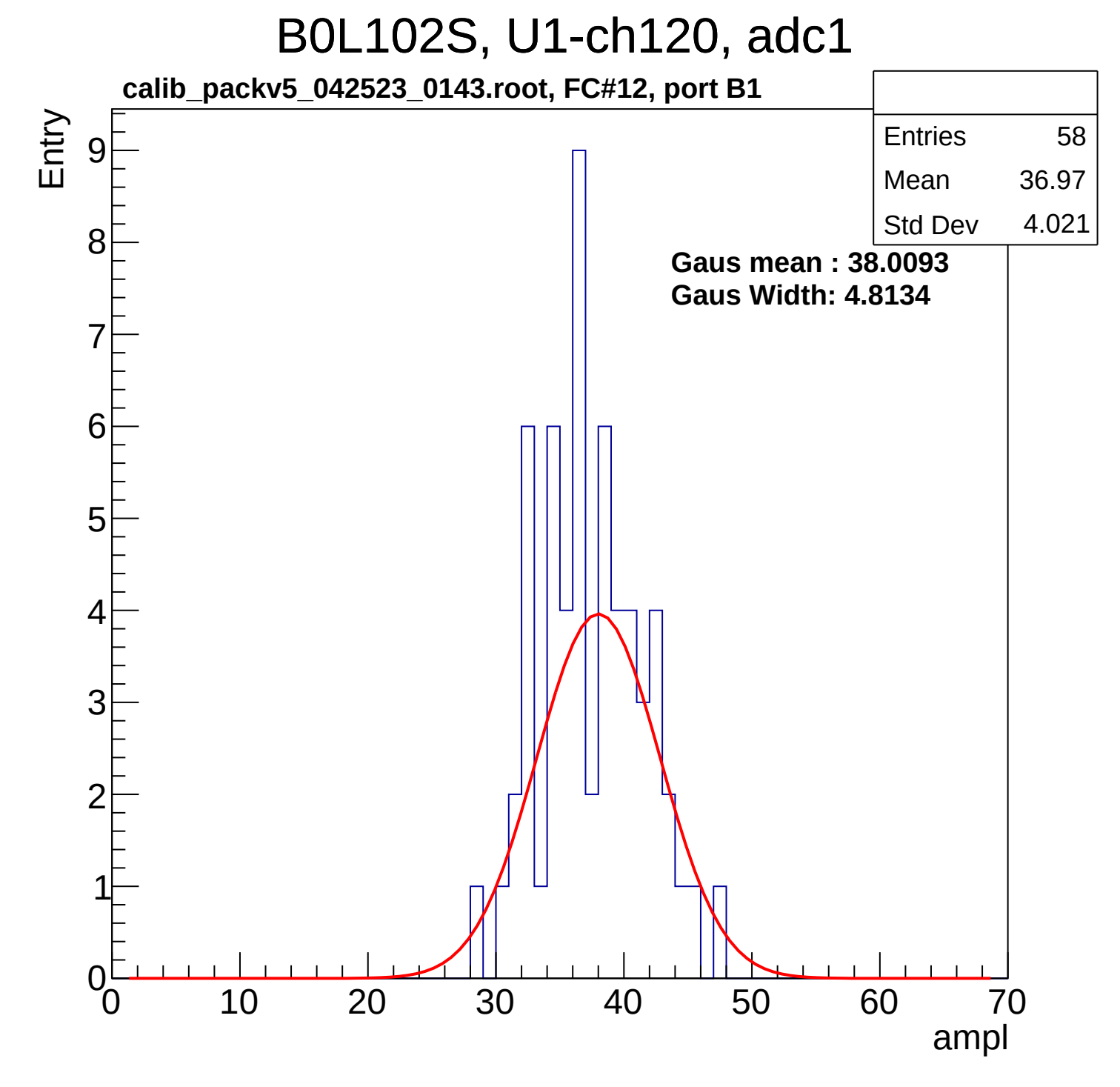
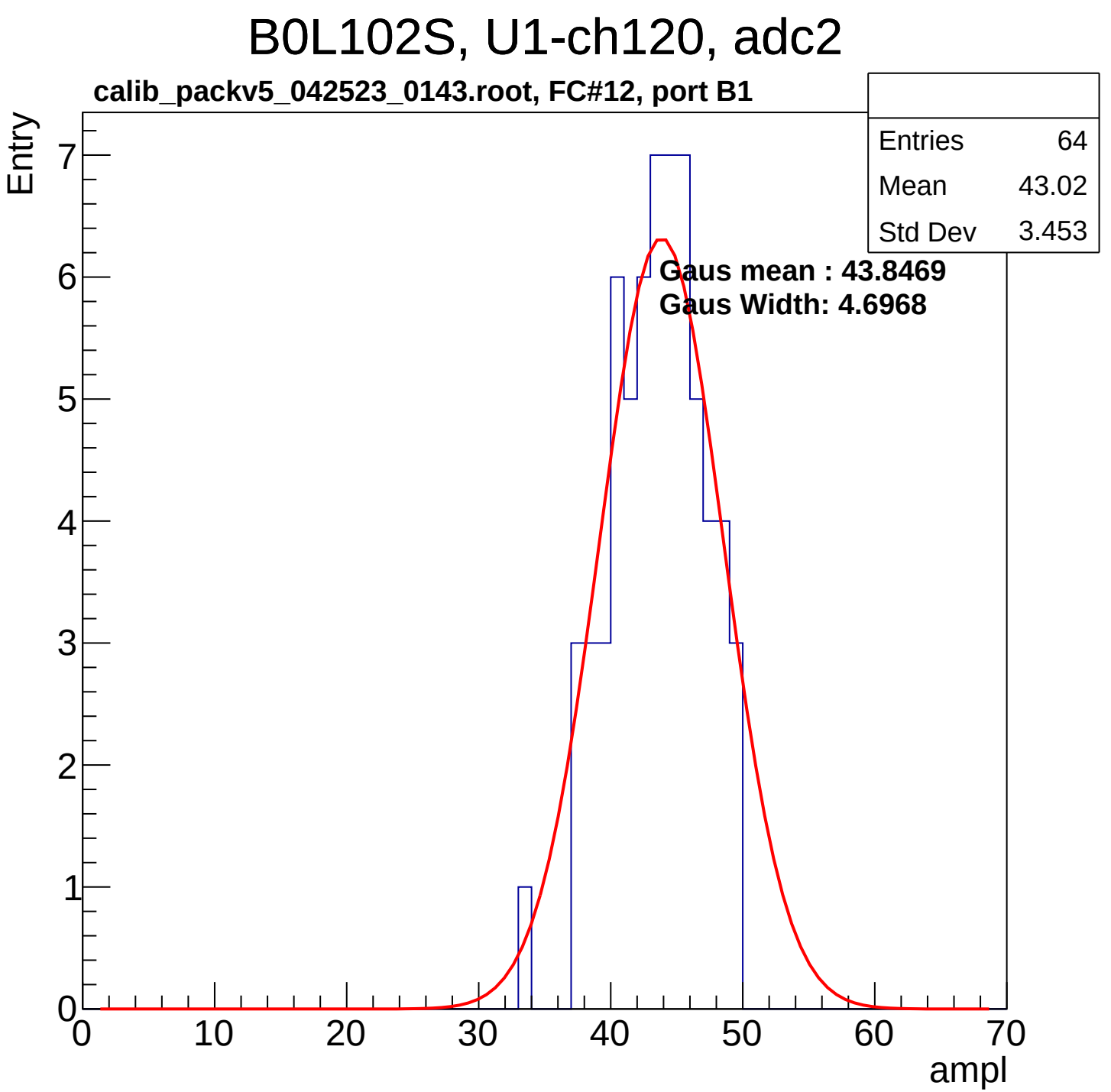
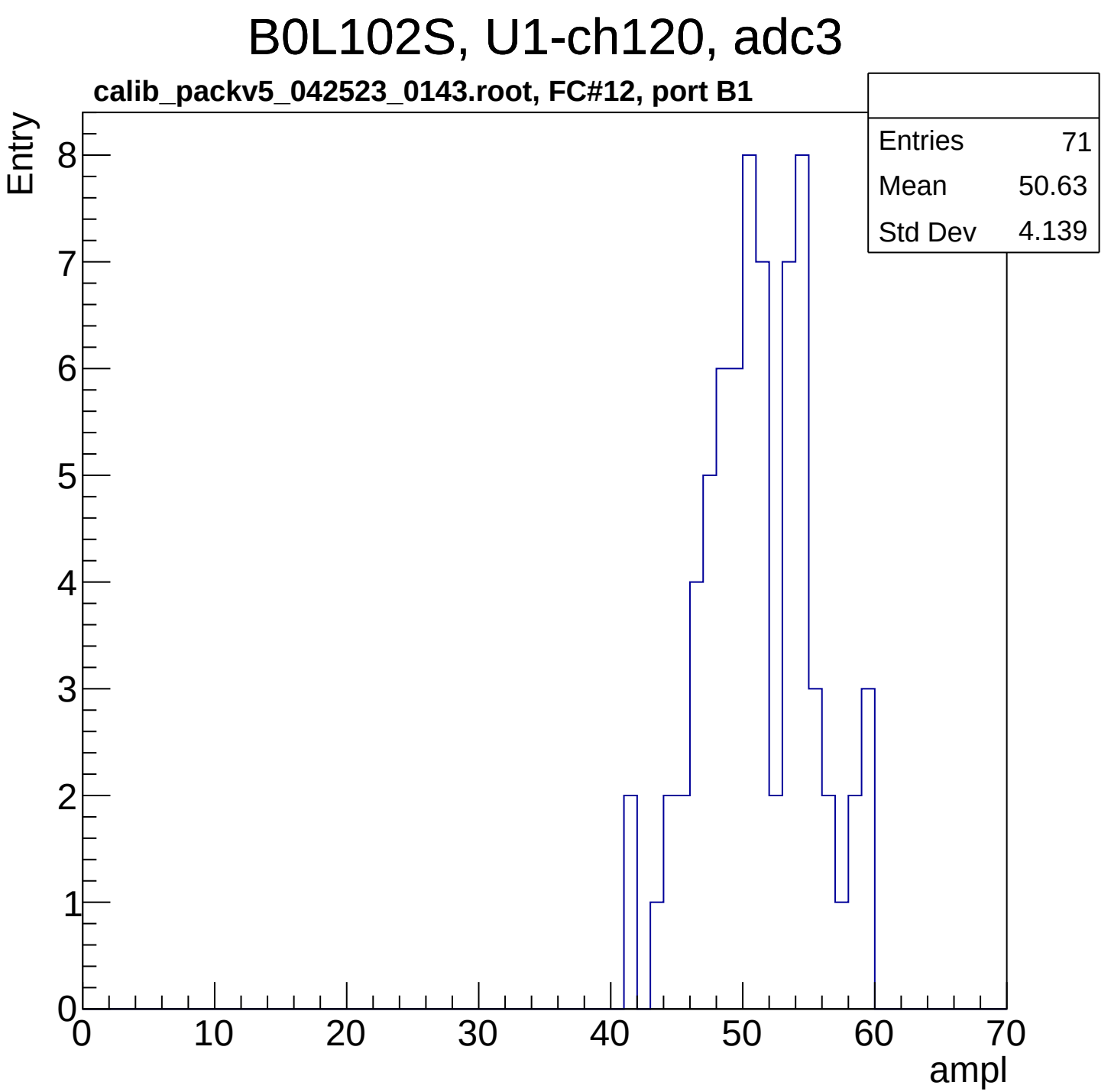
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch120, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 30.62
Std Dev 4.314
Gaus mean : 30.5226
Gaus Width: 4.4096
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch120, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 36.97
Std Dev 4.021
Gaus mean : 38.0093
Gaus Width: 4.8134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch120, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.02
Std Dev 3.453
Gaus mean : 43.8469
Gaus Width: 4.6968
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch120, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 50.63
Std Dev 4.139
Entry
ampl
0
1
2
3
4
5
6
7
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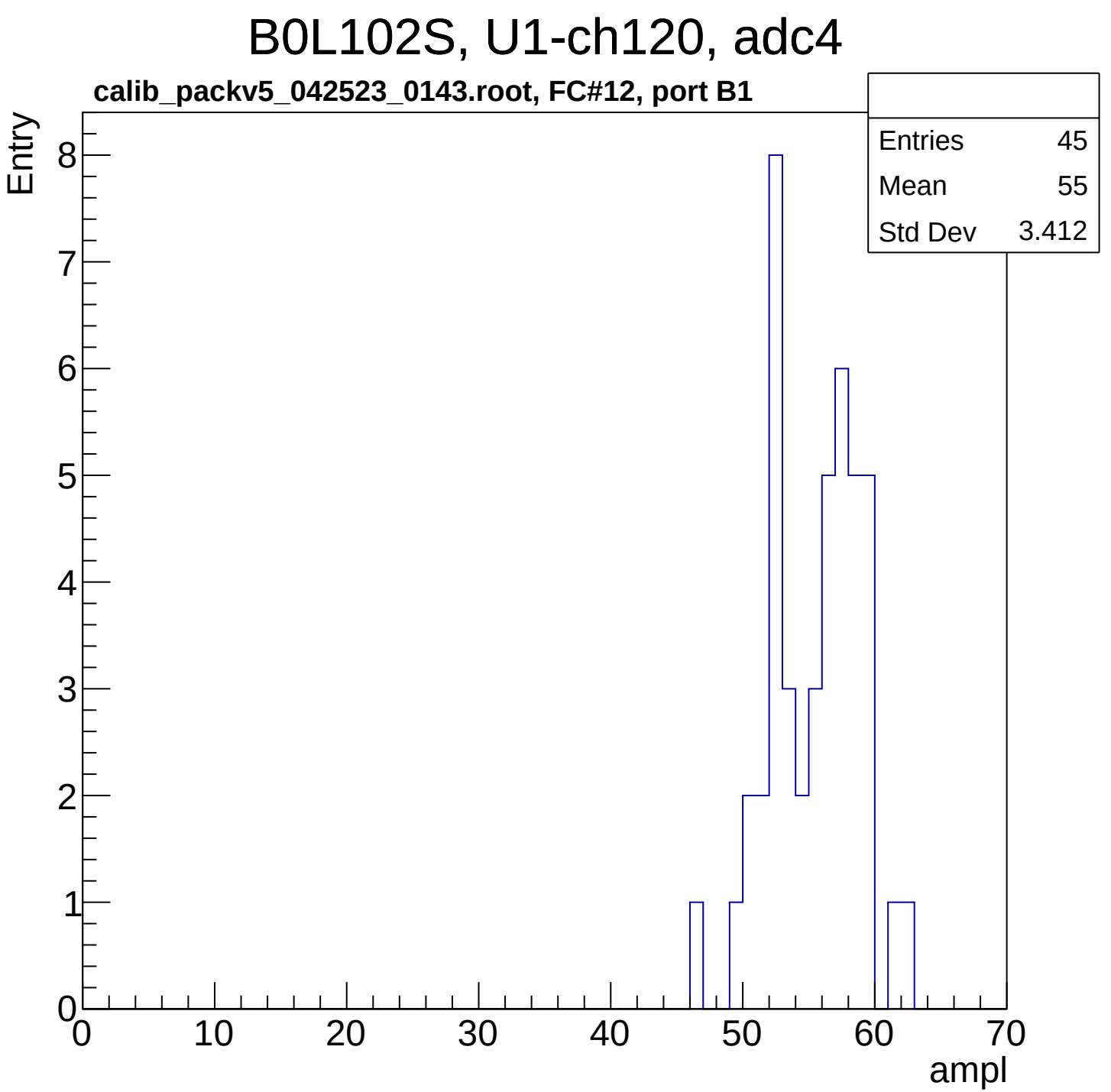
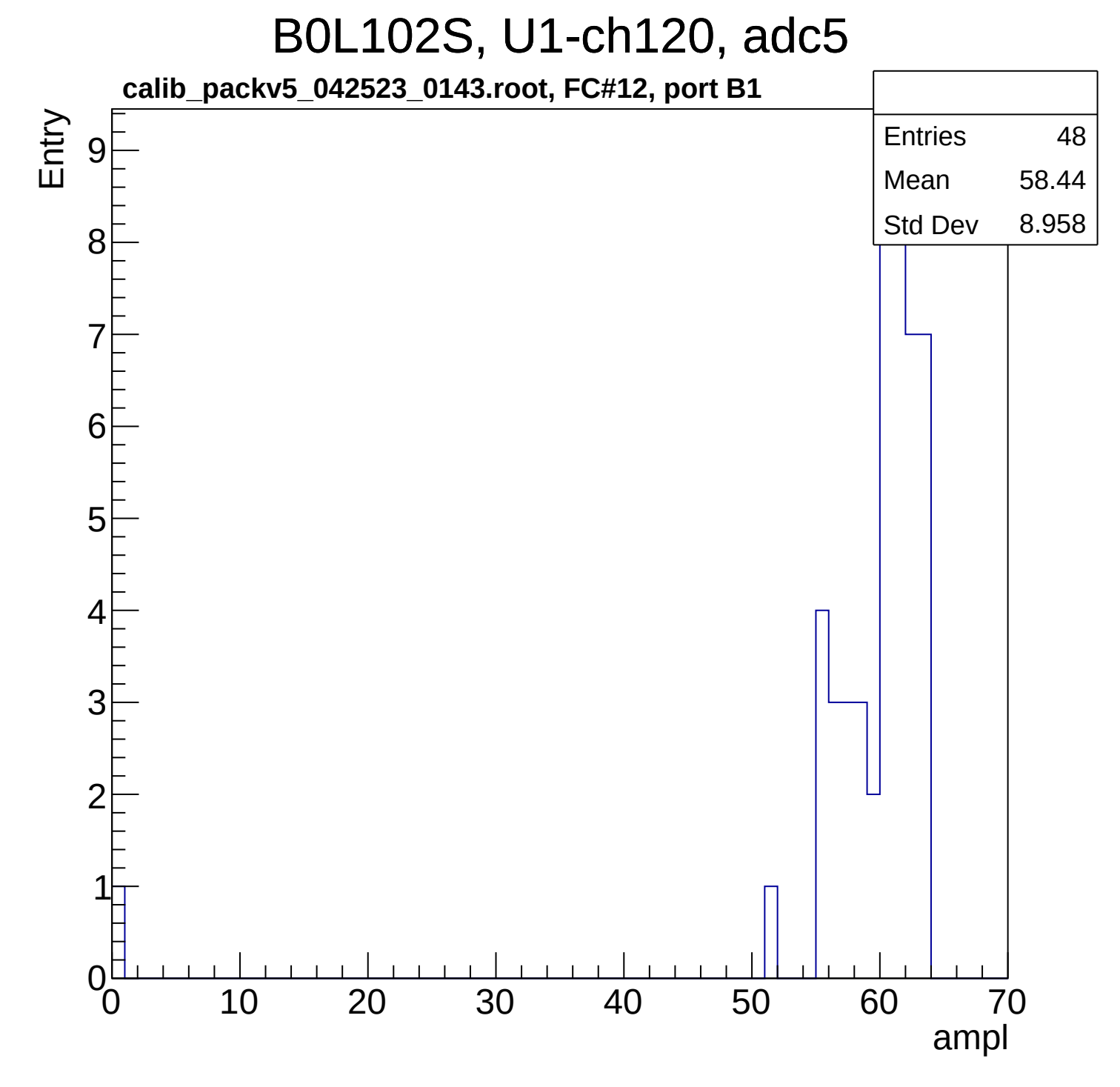
0
10
20
30
40
50
60
70


B0L102S, U1-ch120, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 55
Std Dev 3.412
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch120, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.44
Std Dev 8.958
Entry
ampl
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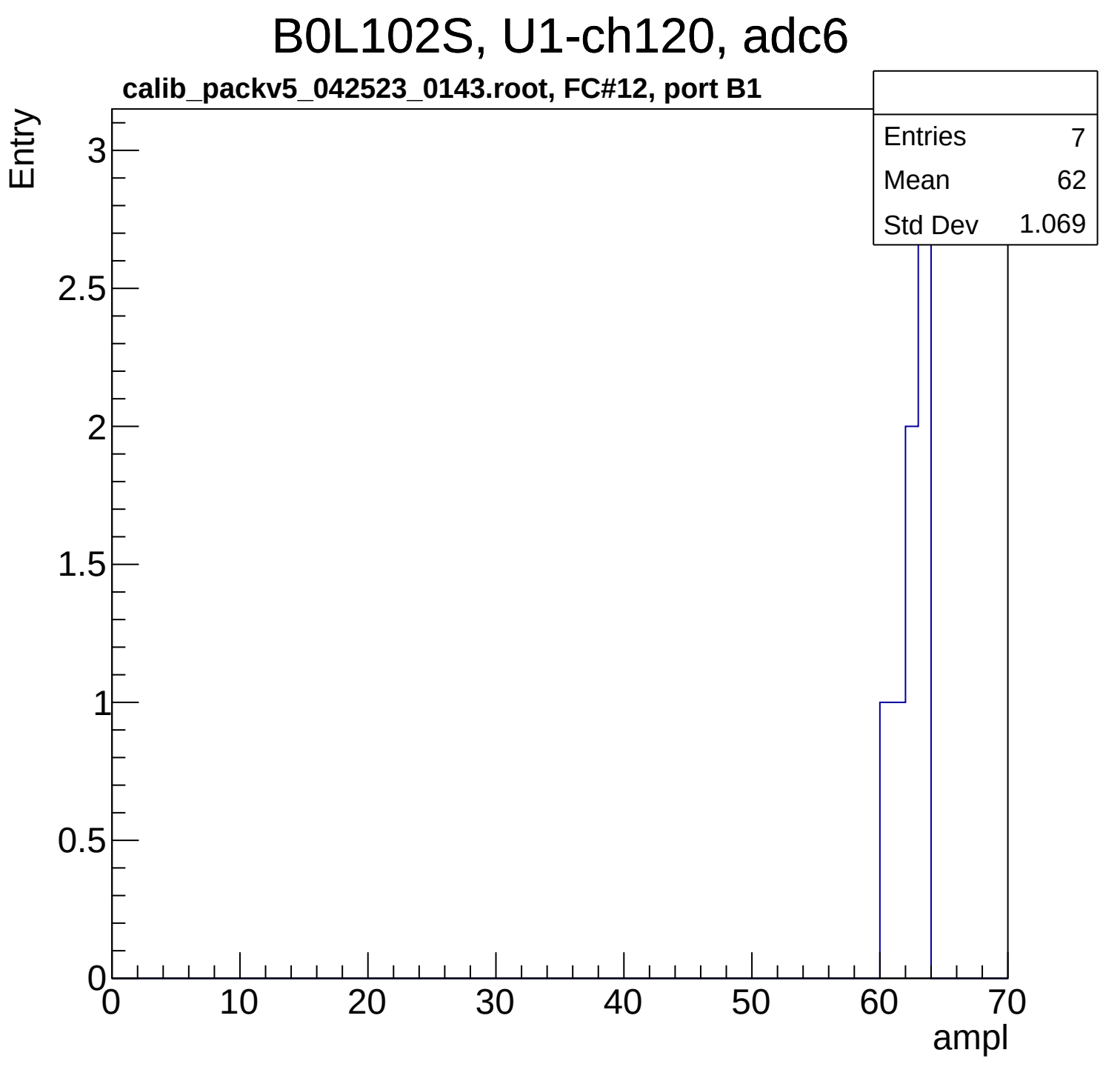
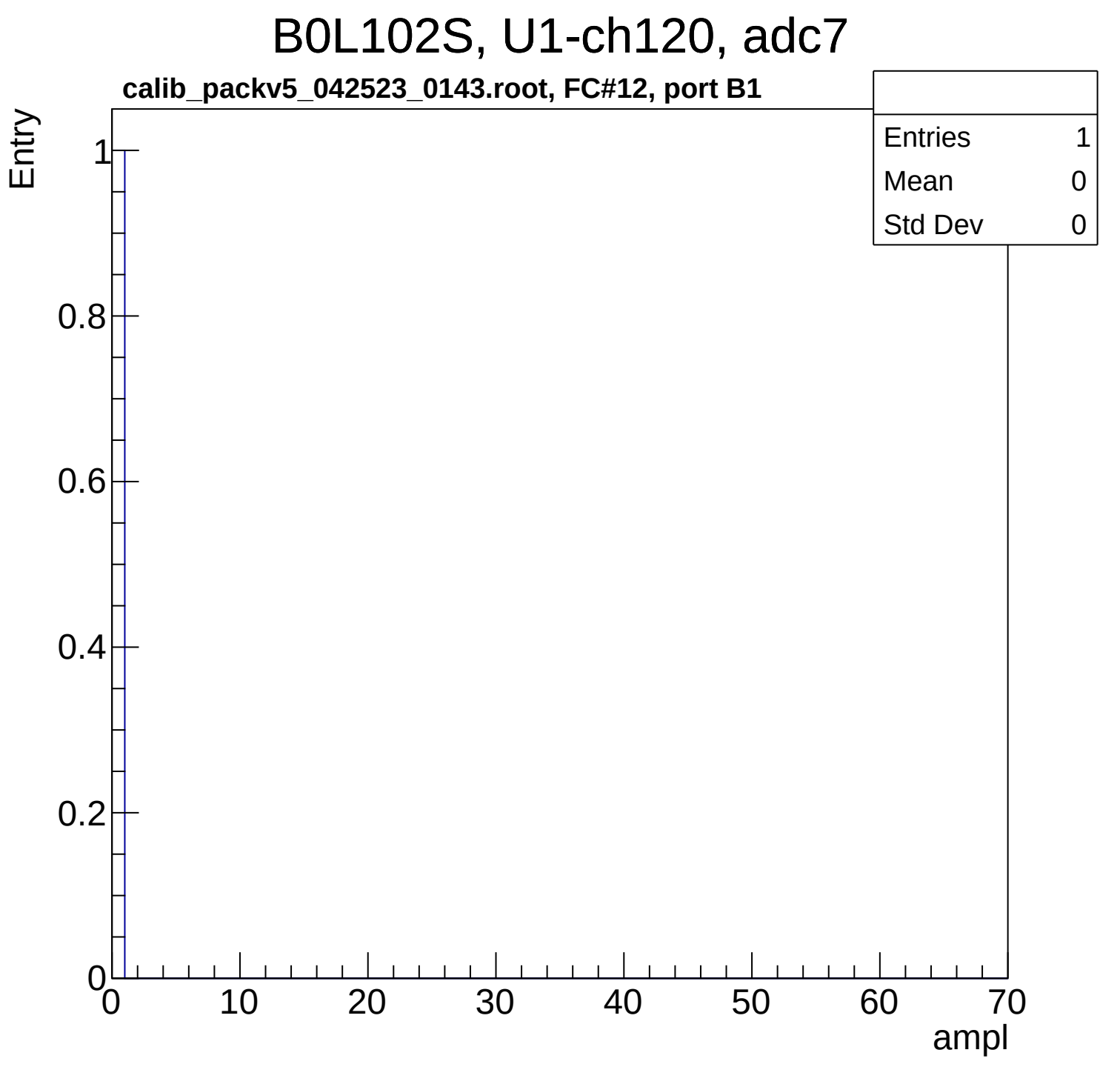
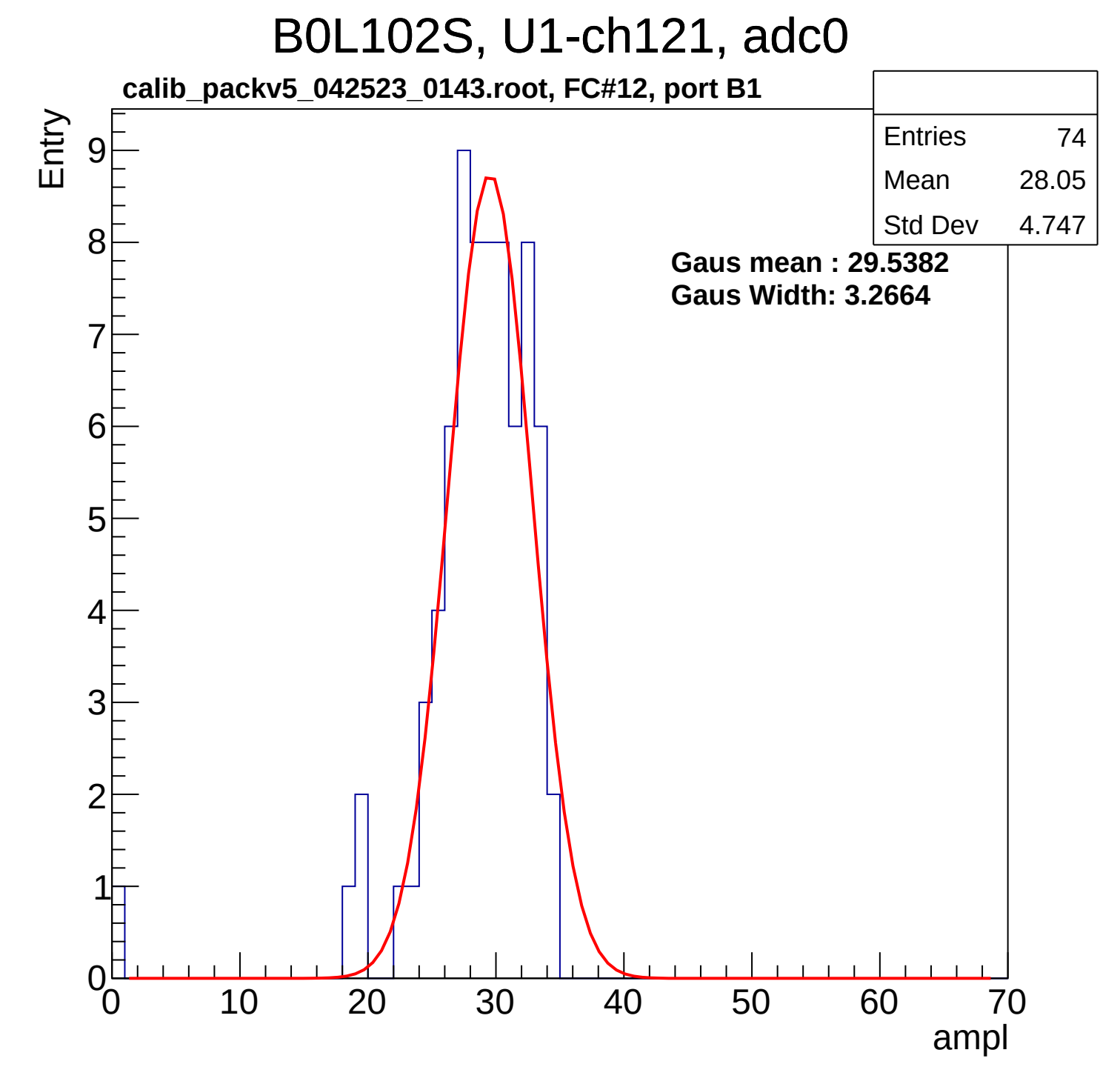
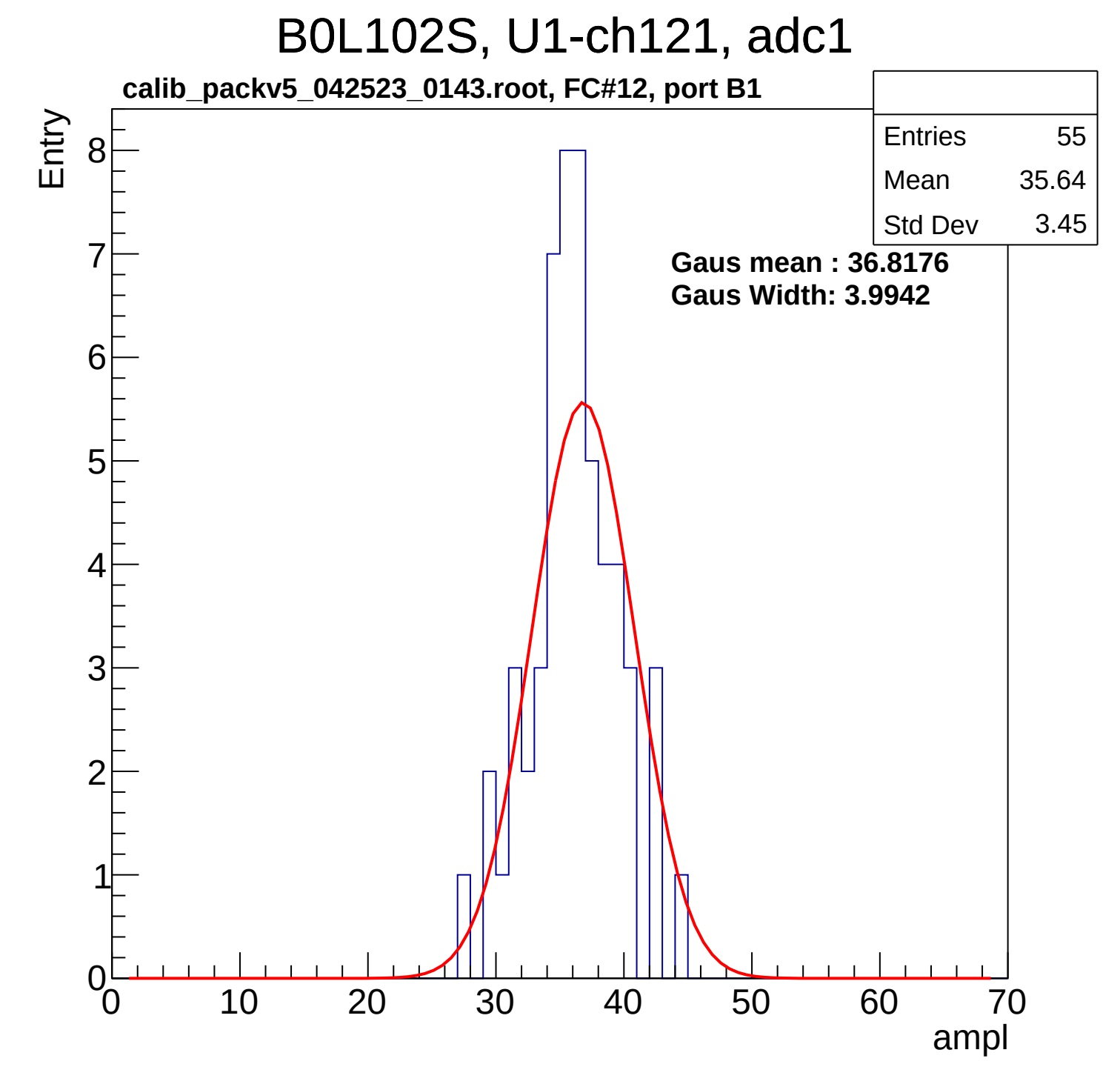
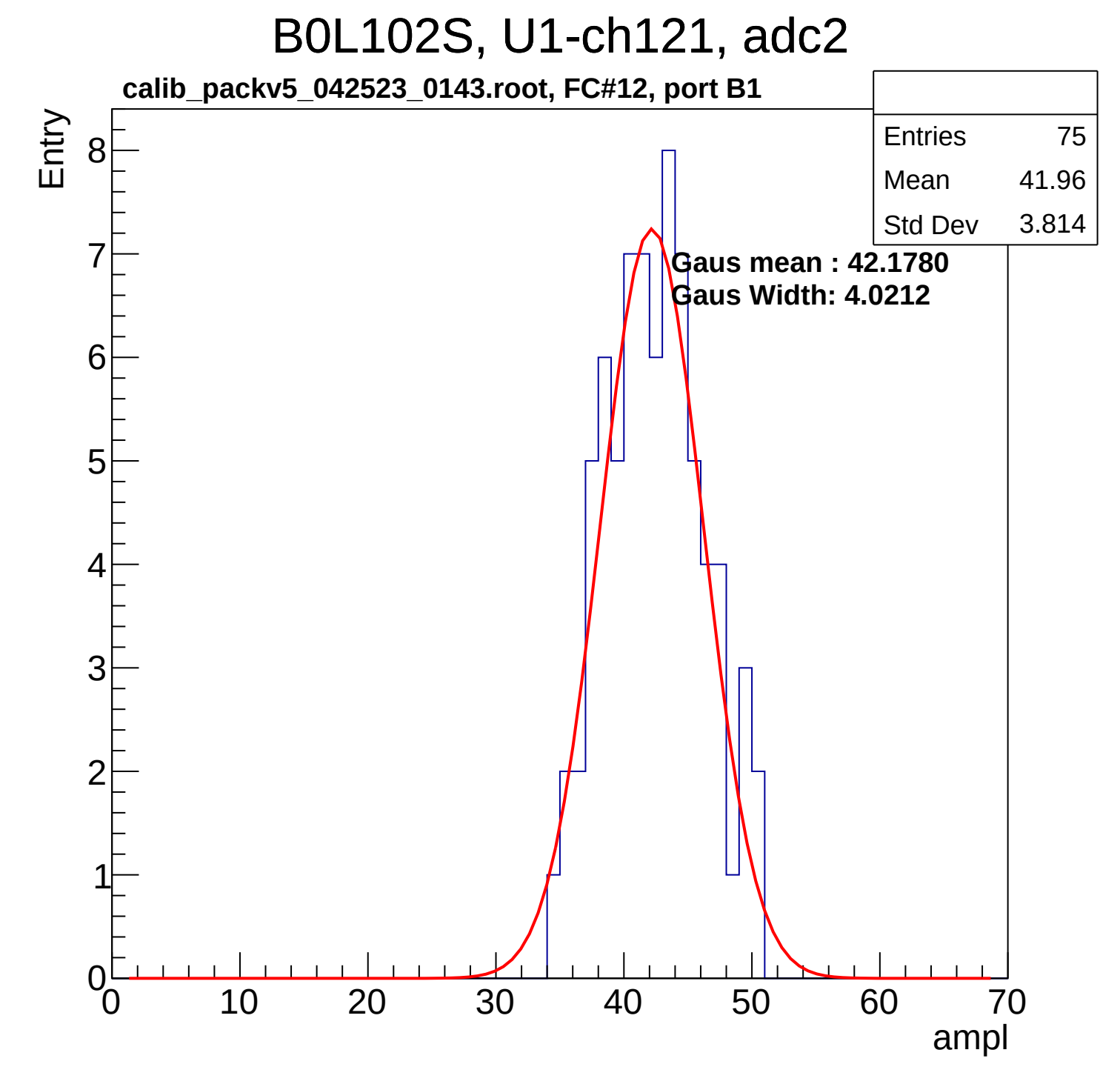
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

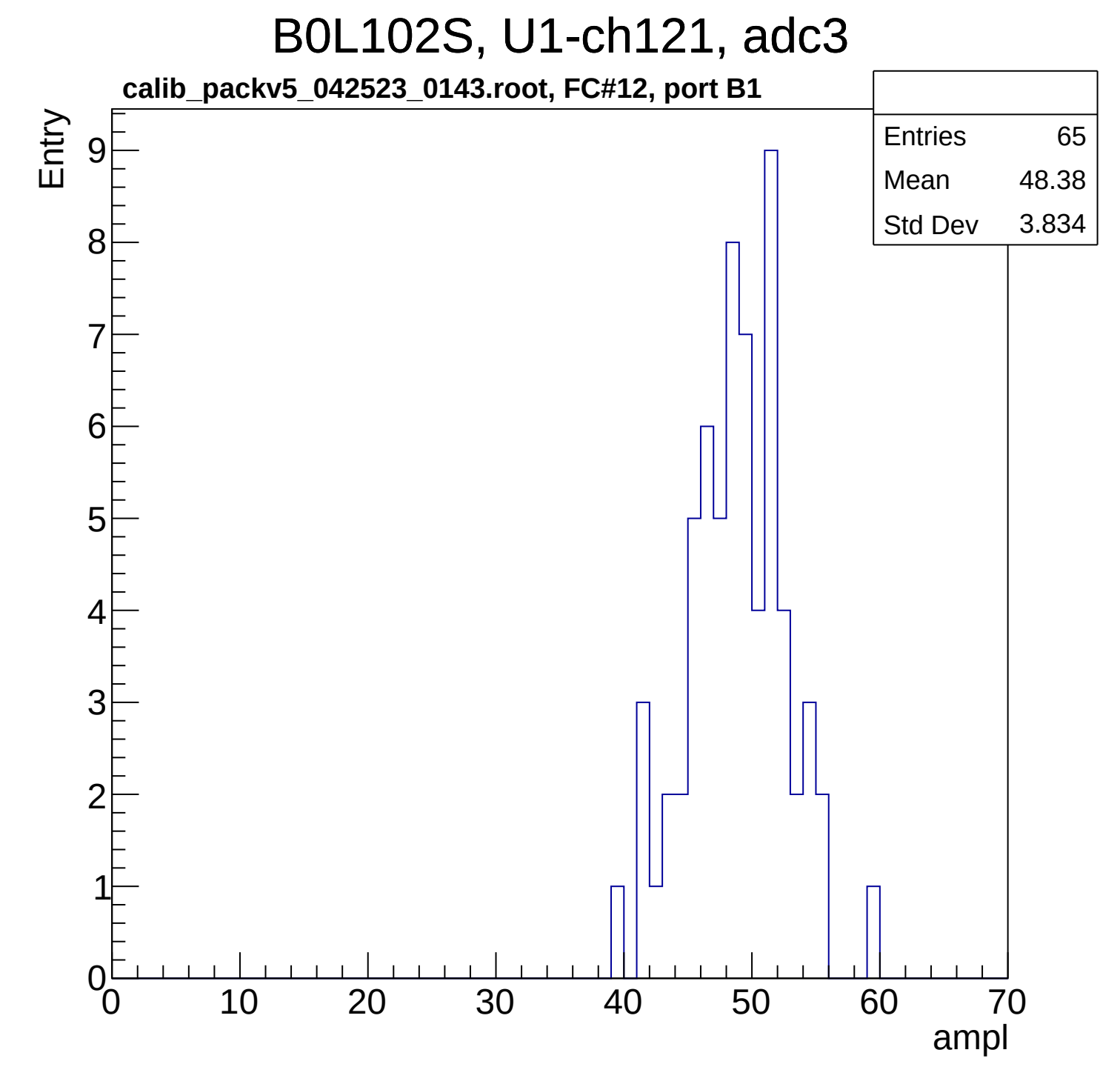

B0L102S, U1-ch120, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62
Std Dev 1.069
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

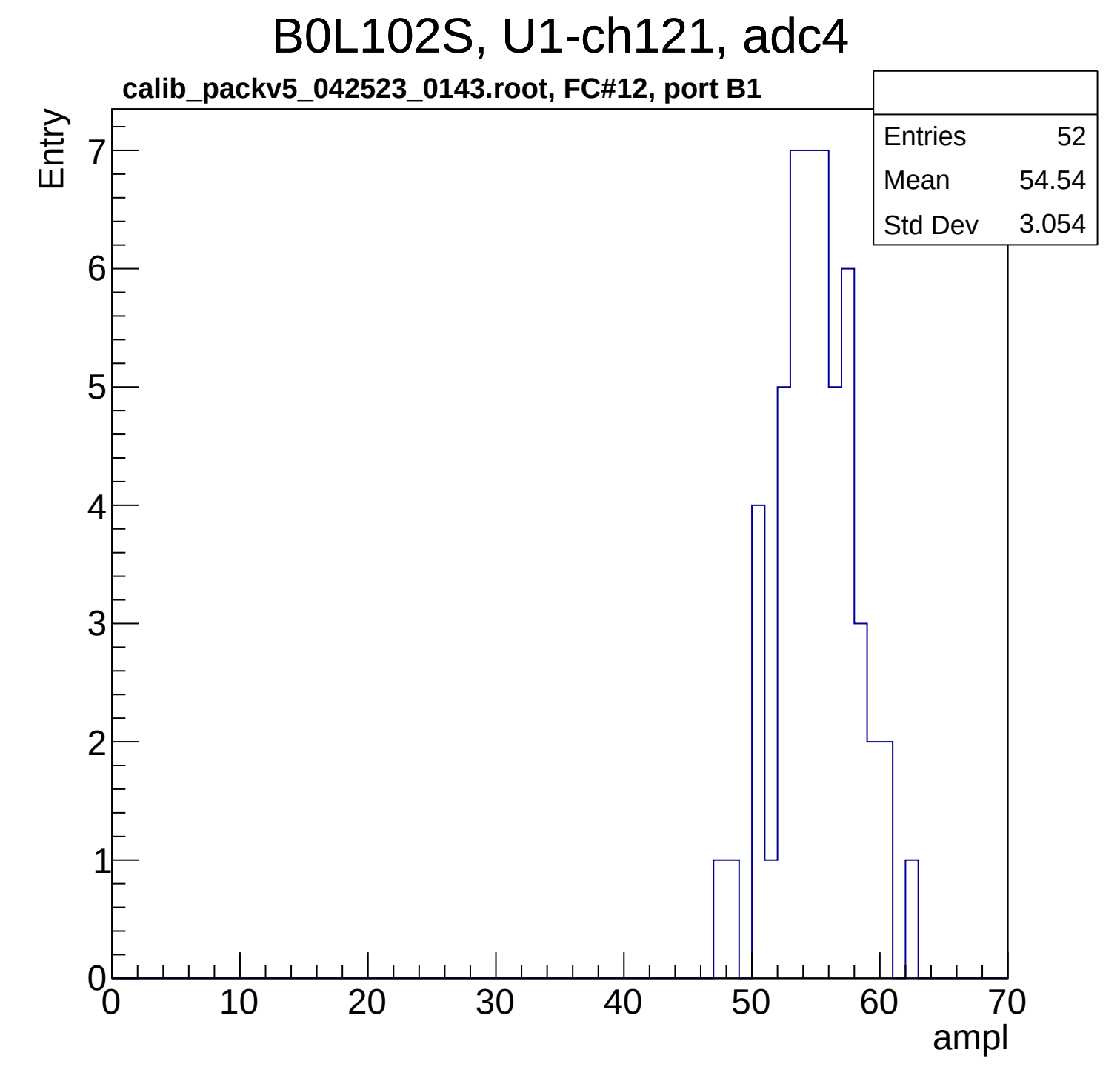

B0L102S, U1-ch120, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

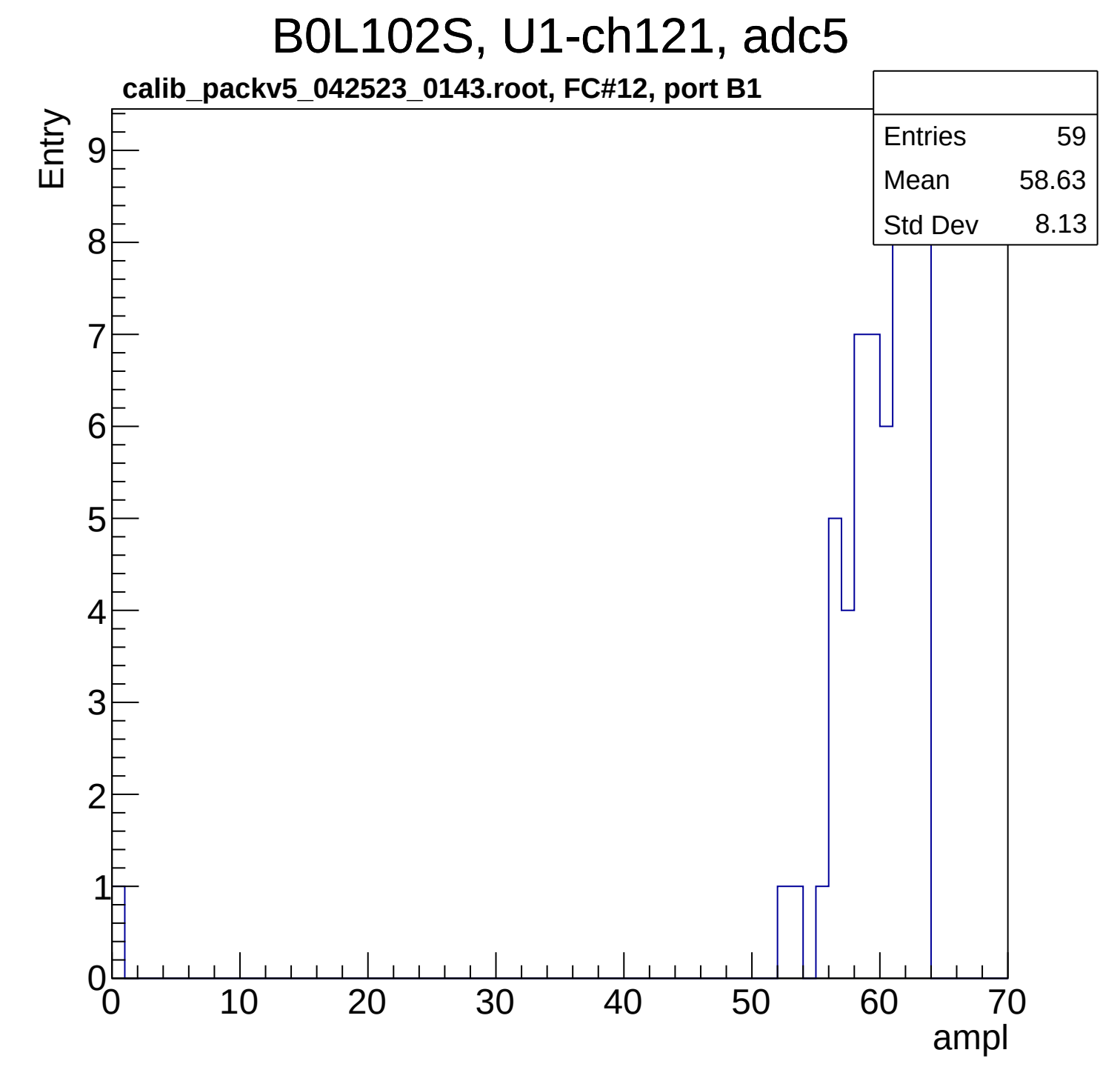

B0L102S, U1-ch121, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 28.05
Std Dev 4.747
Gaus mean : 29.5382
Gaus Width: 3.2664
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

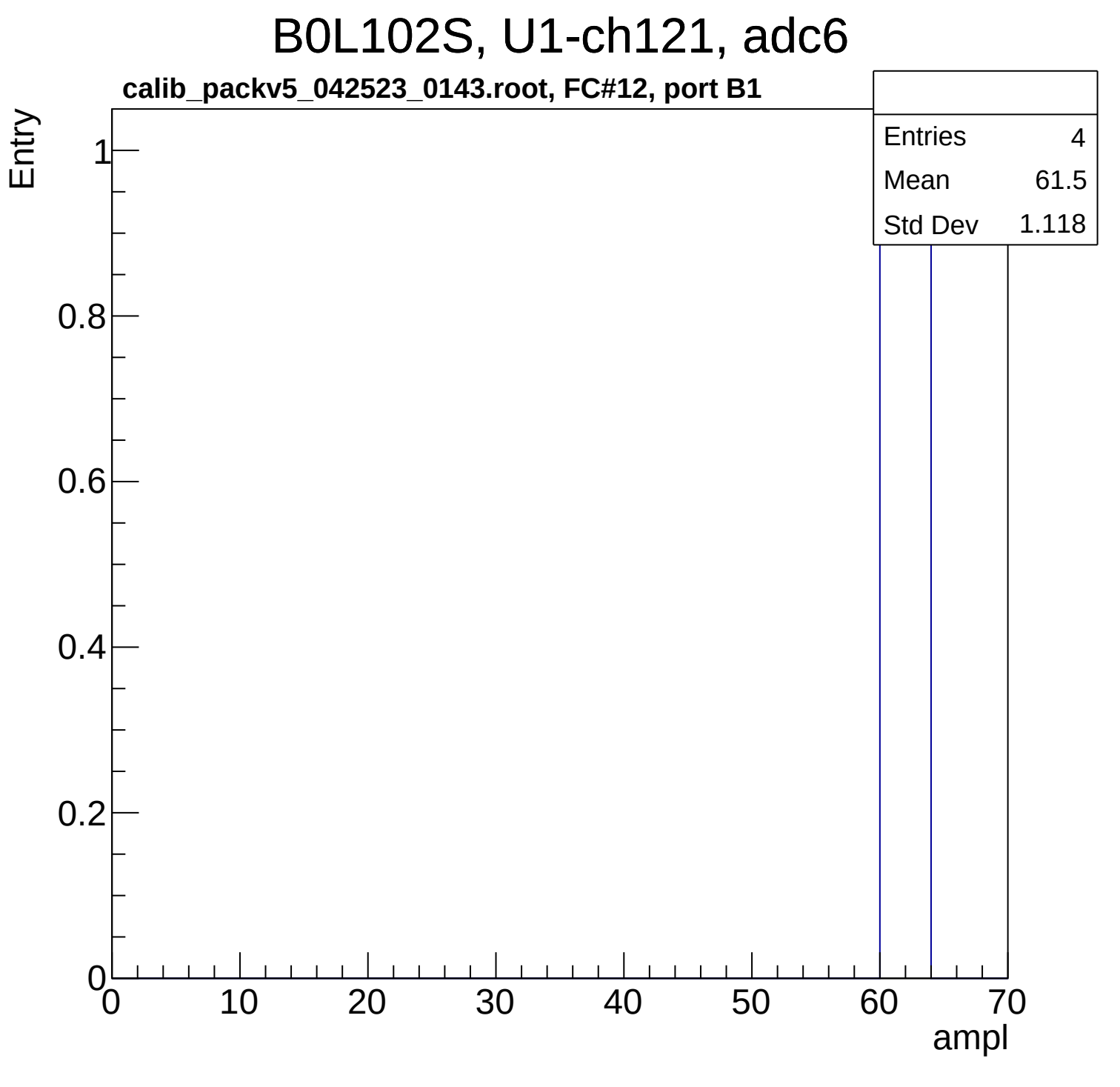

B0L102S, U1-ch121, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 35.64
Std Dev 3.45
Gaus mean : 36.8176
Gaus Width: 3.9942
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

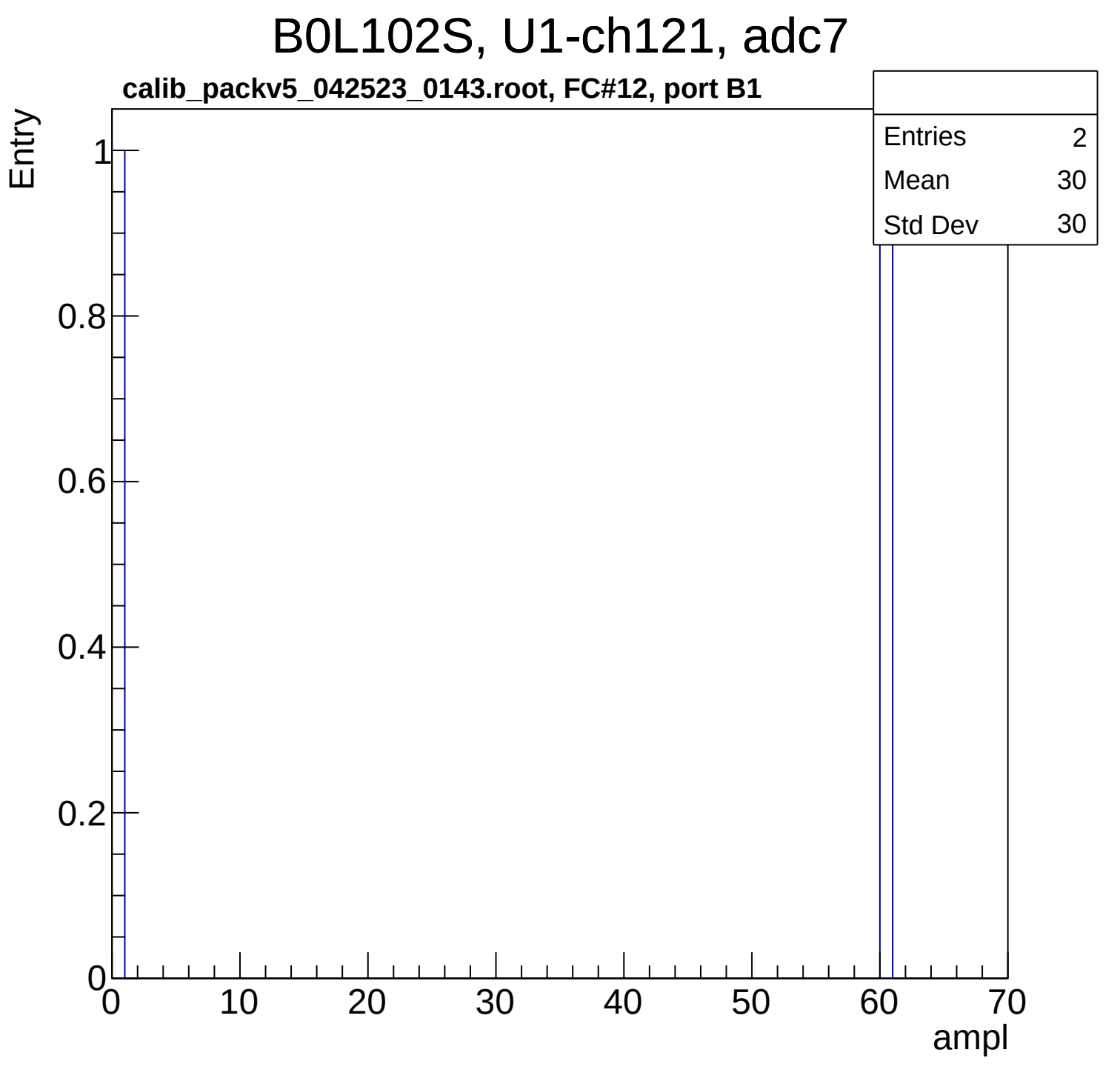

B0L102S, U1-ch121, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 41.96
Std Dev 3.814
Gaus mean : 42.1780
Gaus Width: 4.0212
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

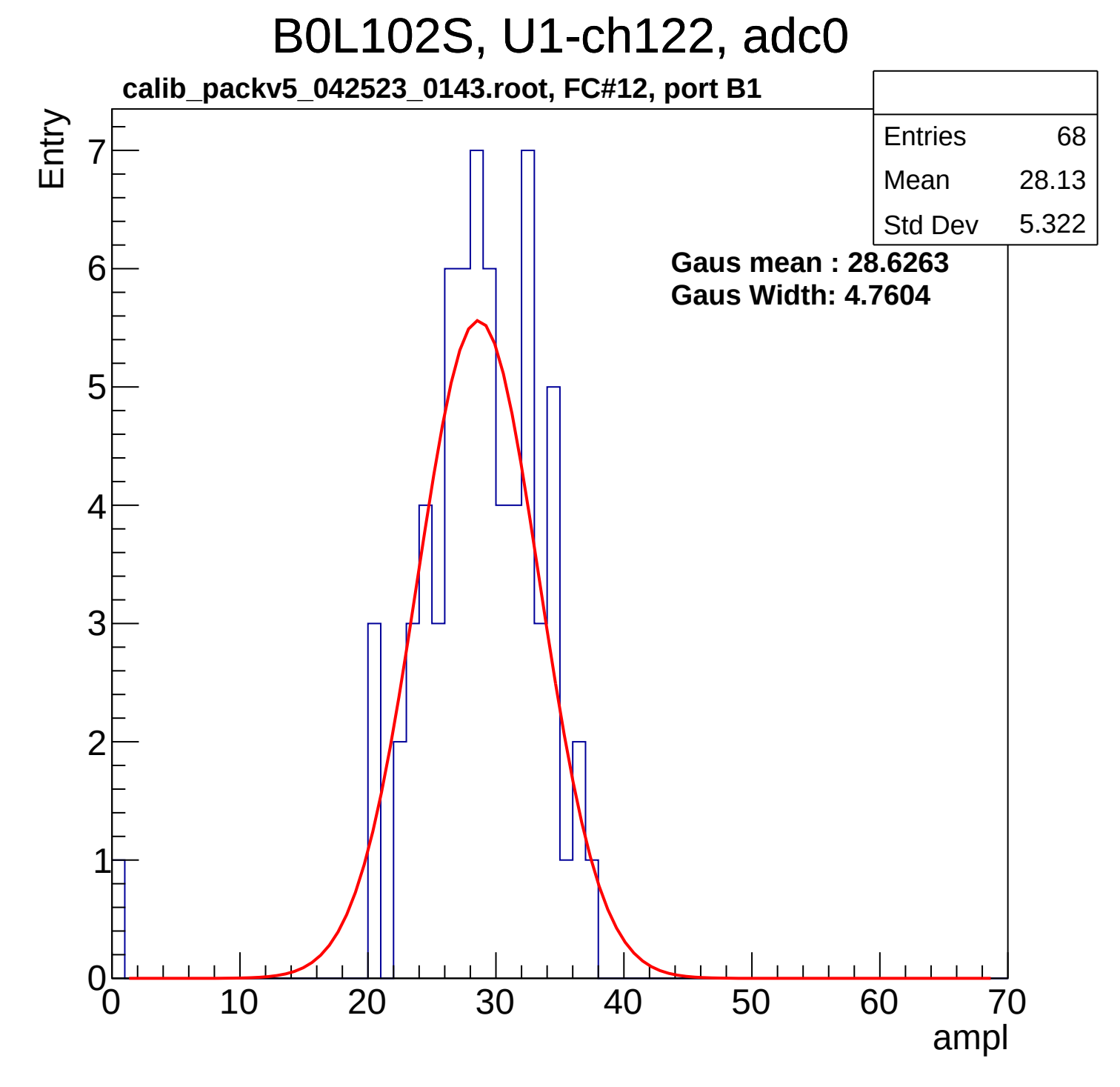

B0L102S, U1-ch121, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.38
Std Dev 3.834
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

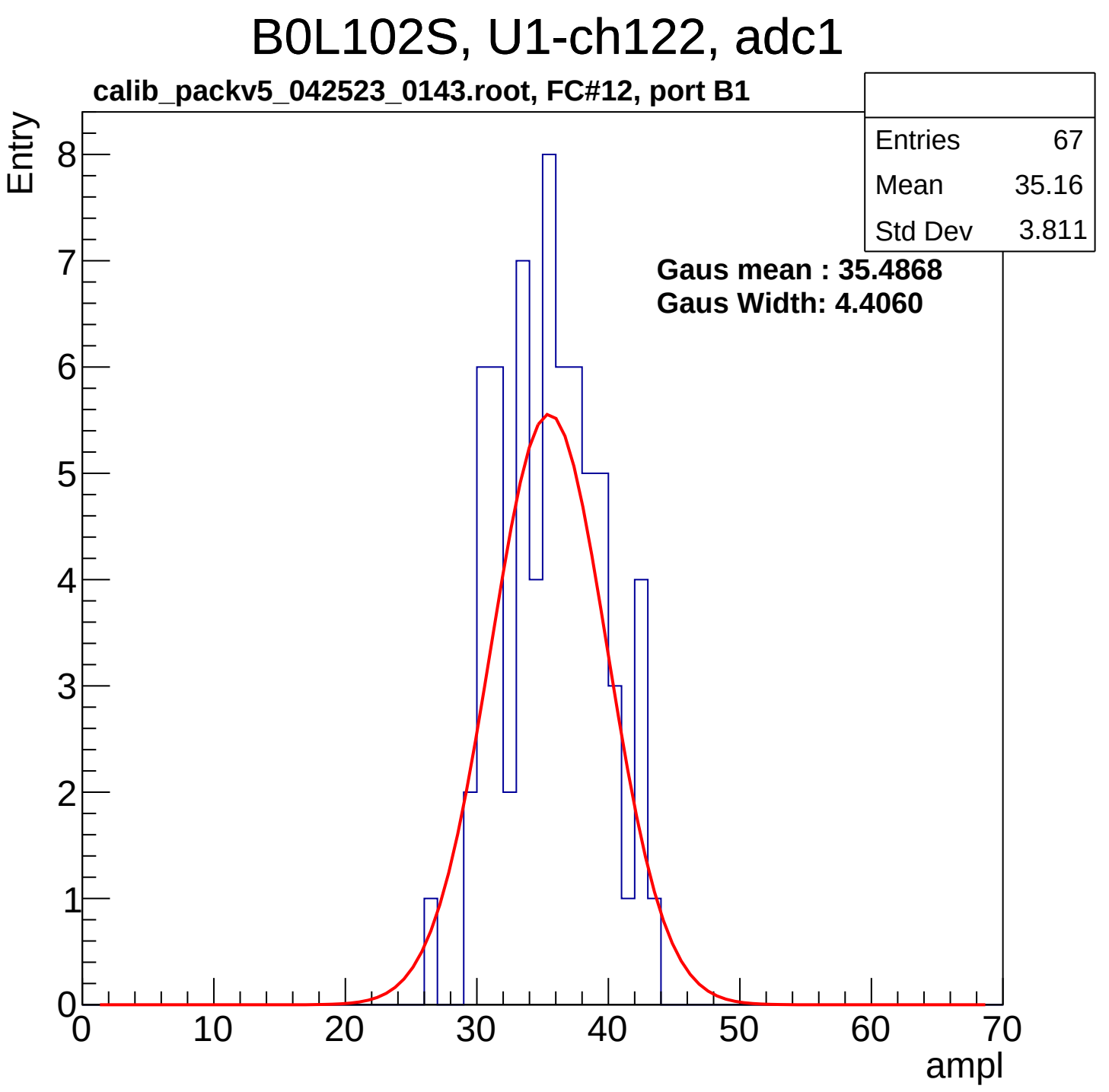

B0L102S, U1-ch121, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 54.54
Std Dev 3.054
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

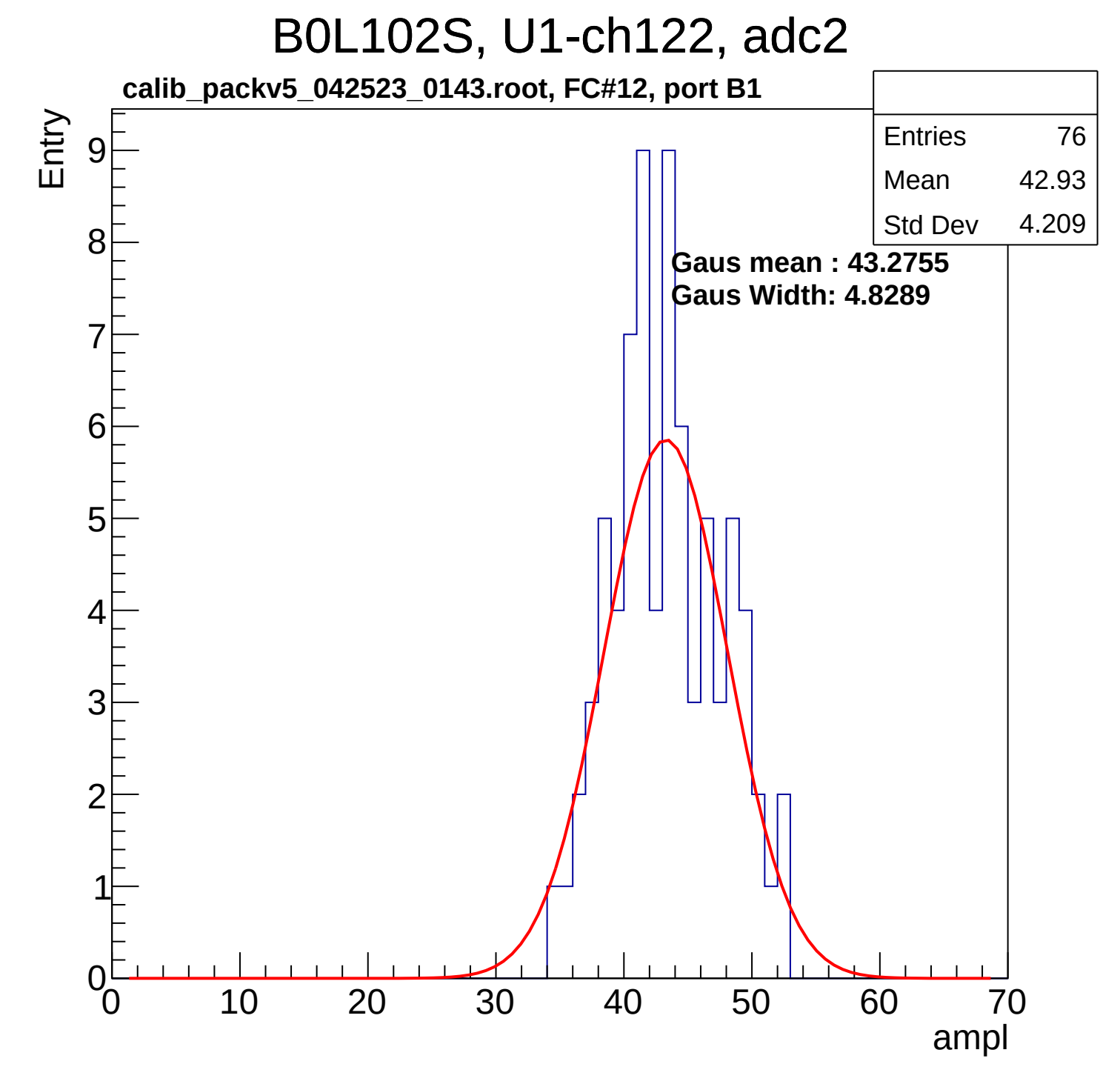

B0L102S, U1-ch121, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 58.63
Std Dev 8.13
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

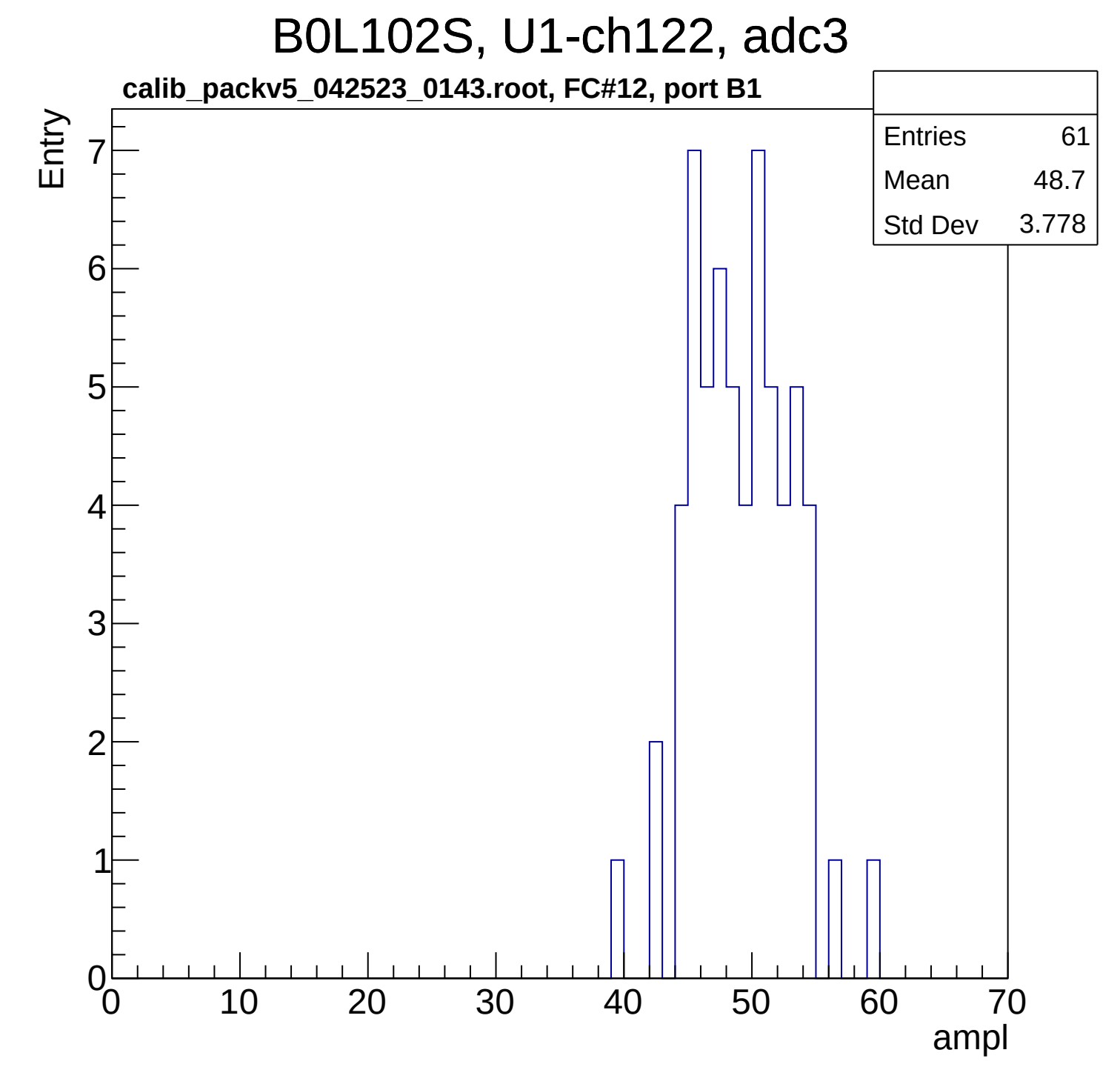

B0L102S, U1-ch121, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

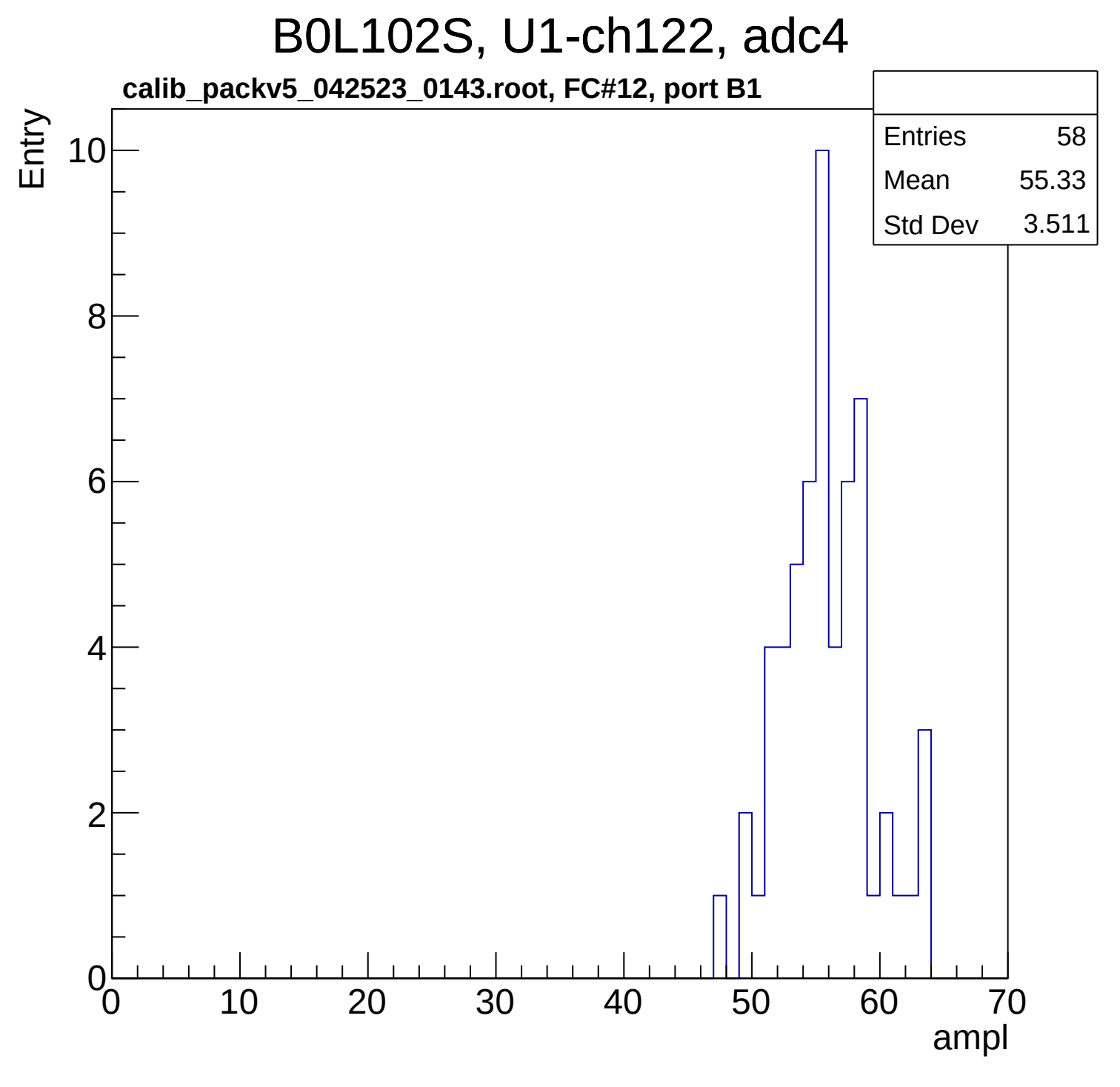

B0L102S, U1-ch121, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 30
Std Dev 30
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

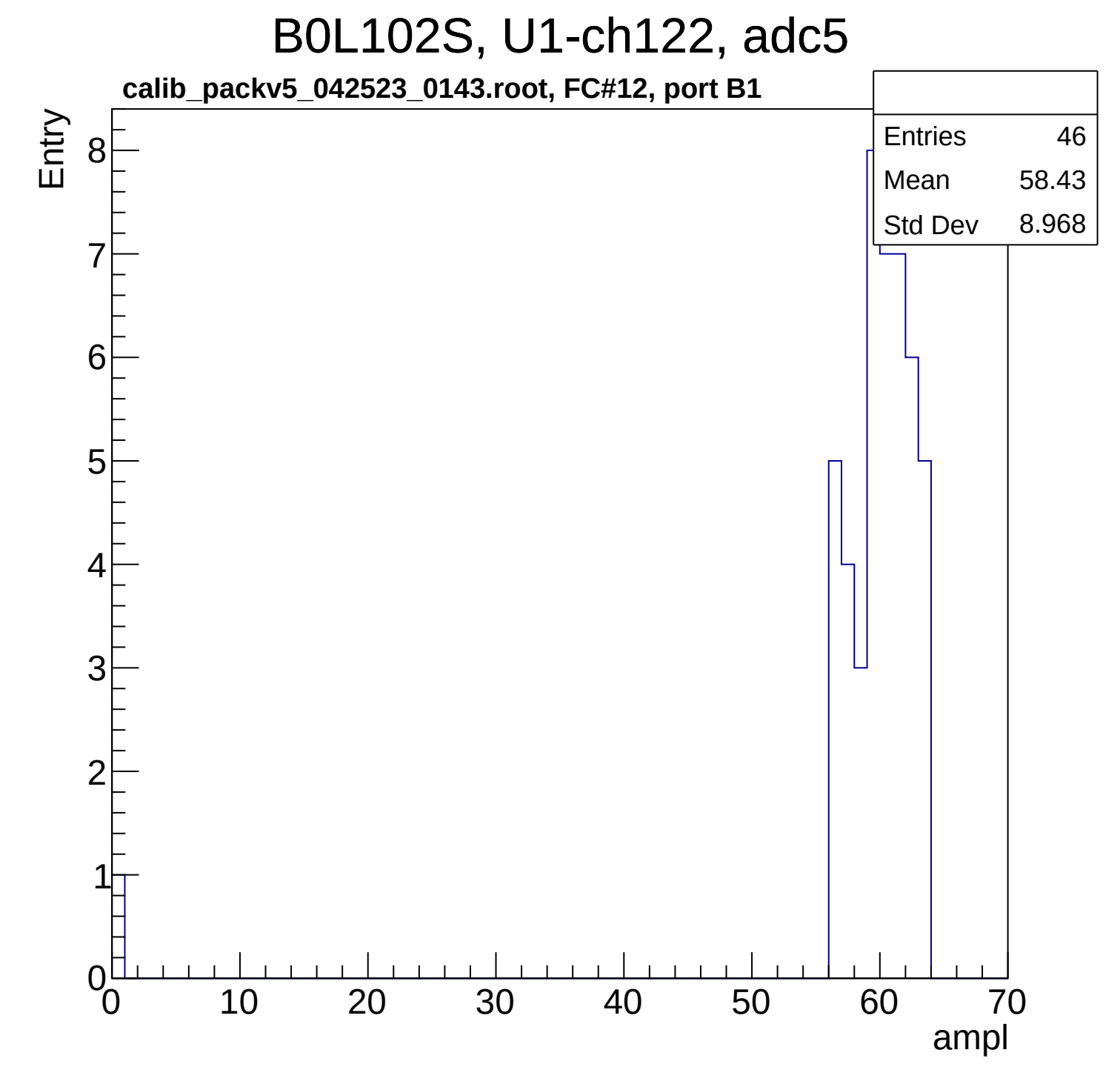

B0L102S, U1-ch122, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 28.13
Std Dev 5.322
Gaus mean : 28.6263
Gaus Width: 4.7604
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

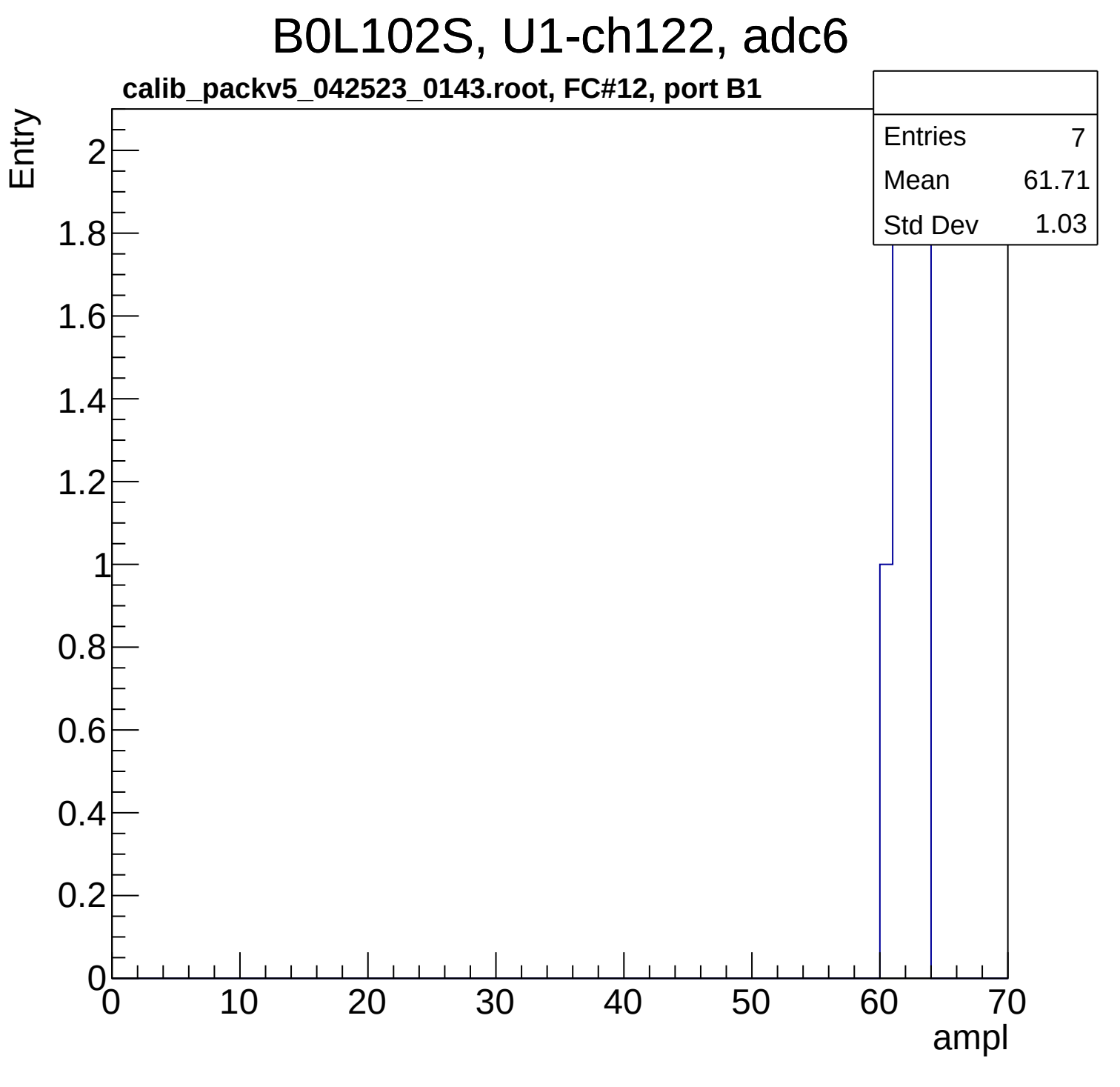

B0L102S, U1-ch122, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.16
Std Dev 3.811
Gaus mean : 35.4868
Gaus Width: 4.4060
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

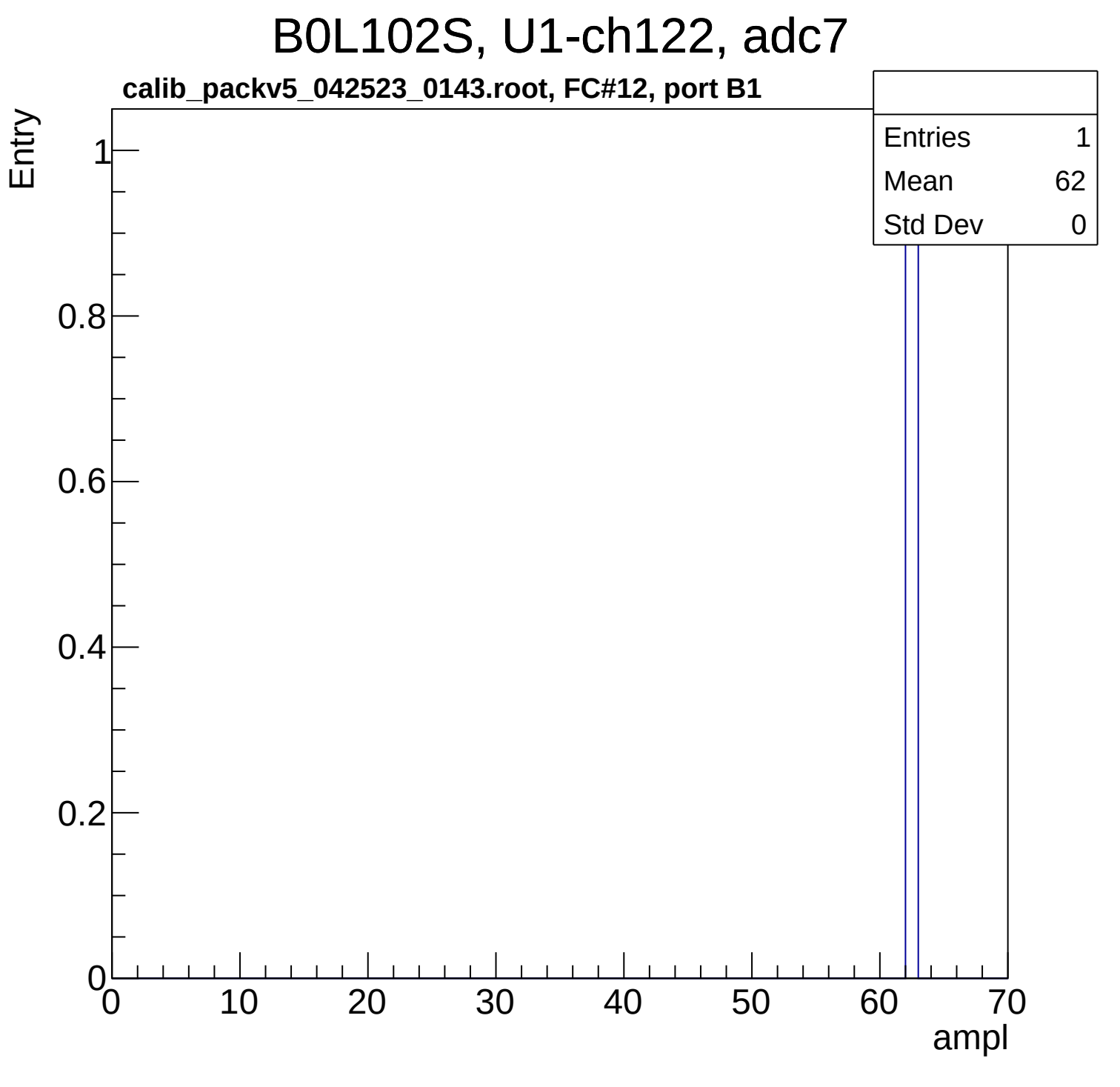

B0L102S, U1-ch122, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 42.93
Std Dev 4.209
Gaus mean : 43.2755
Gaus Width: 4.8289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

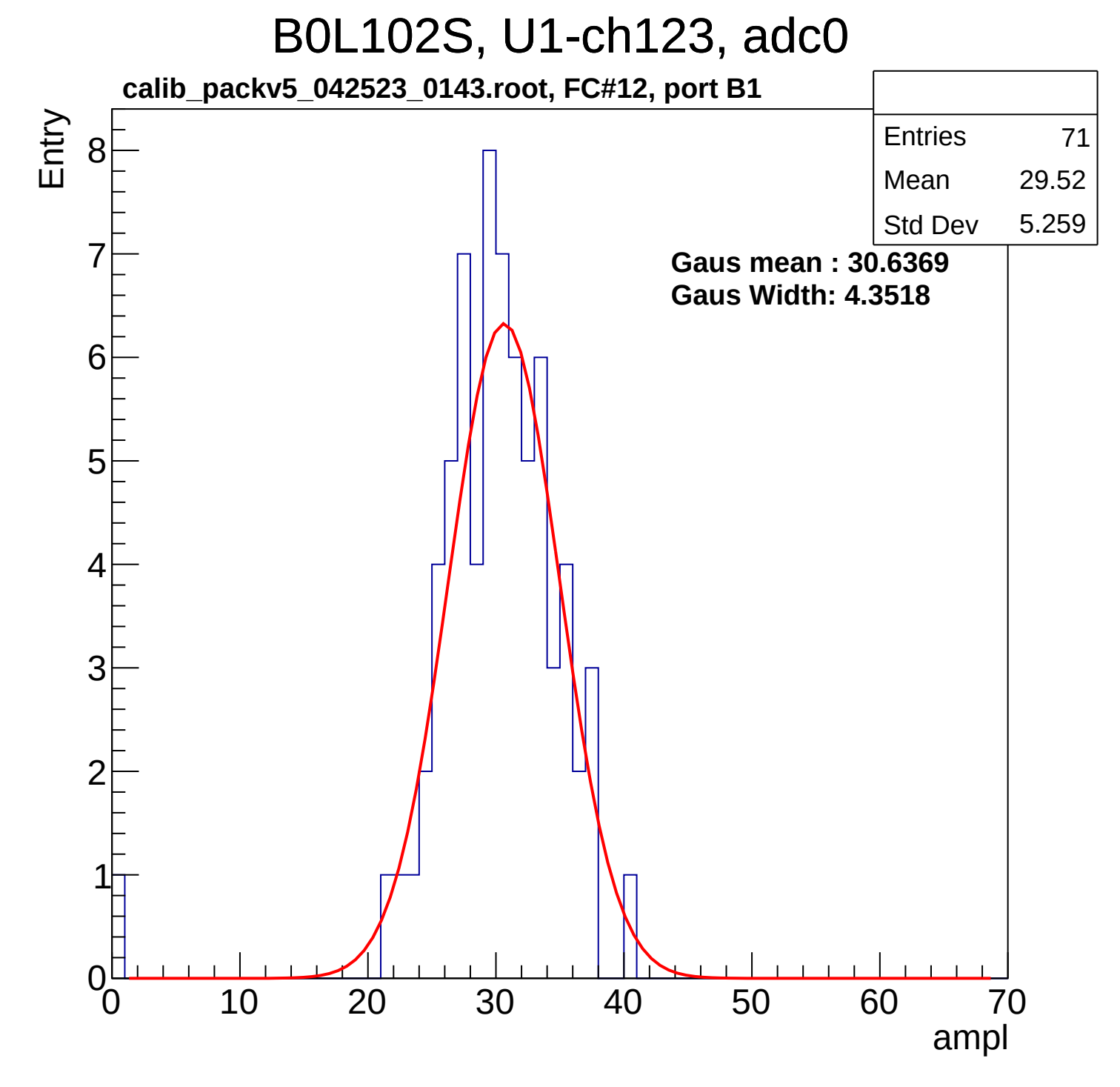

B0L102S, U1-ch122, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 48.7
Std Dev 3.778
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

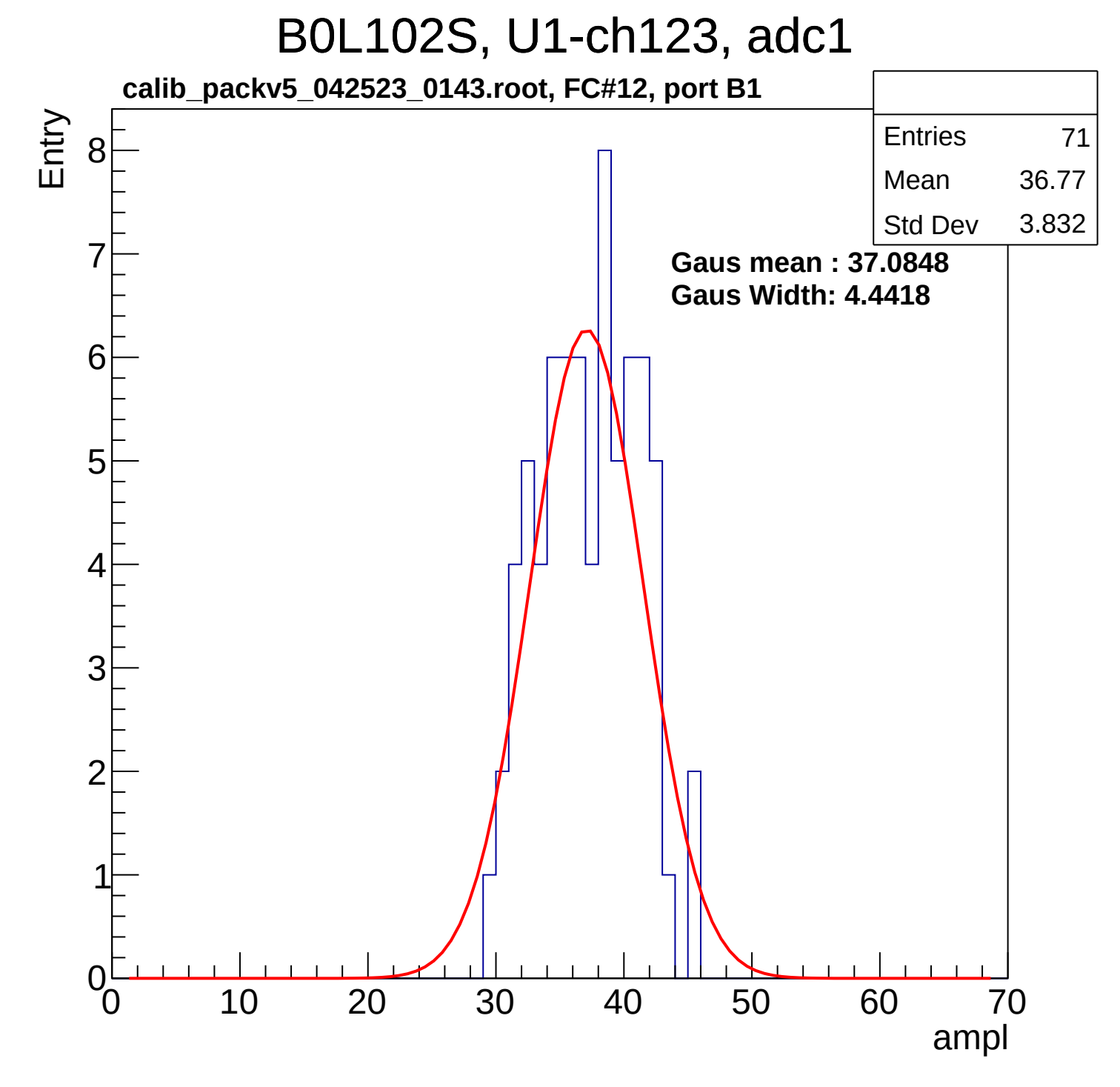

B0L102S, U1-ch122, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 55.33
Std Dev 3.511
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

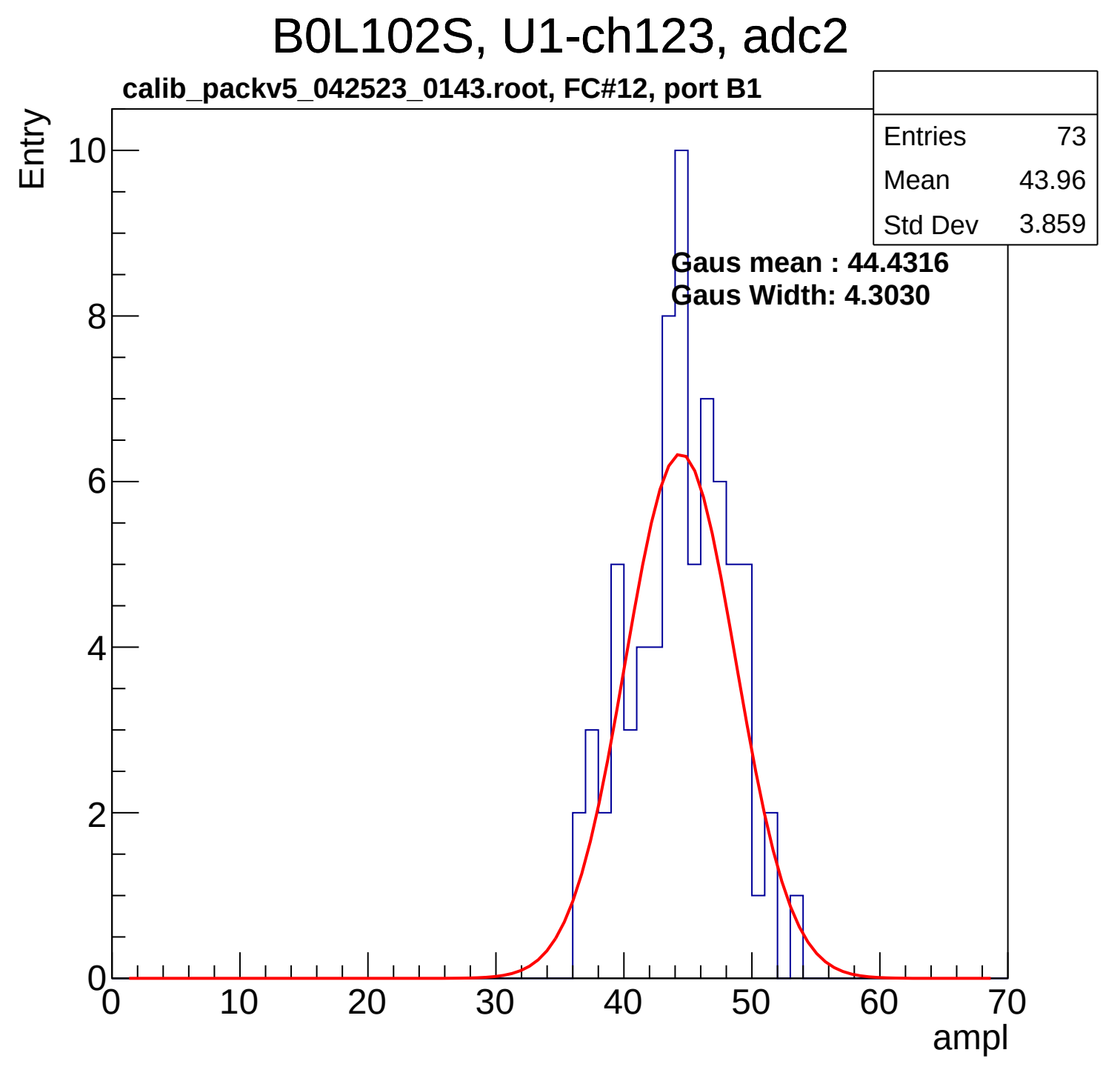

B0L102S, U1-ch122, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.43
Std Dev 8.968
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

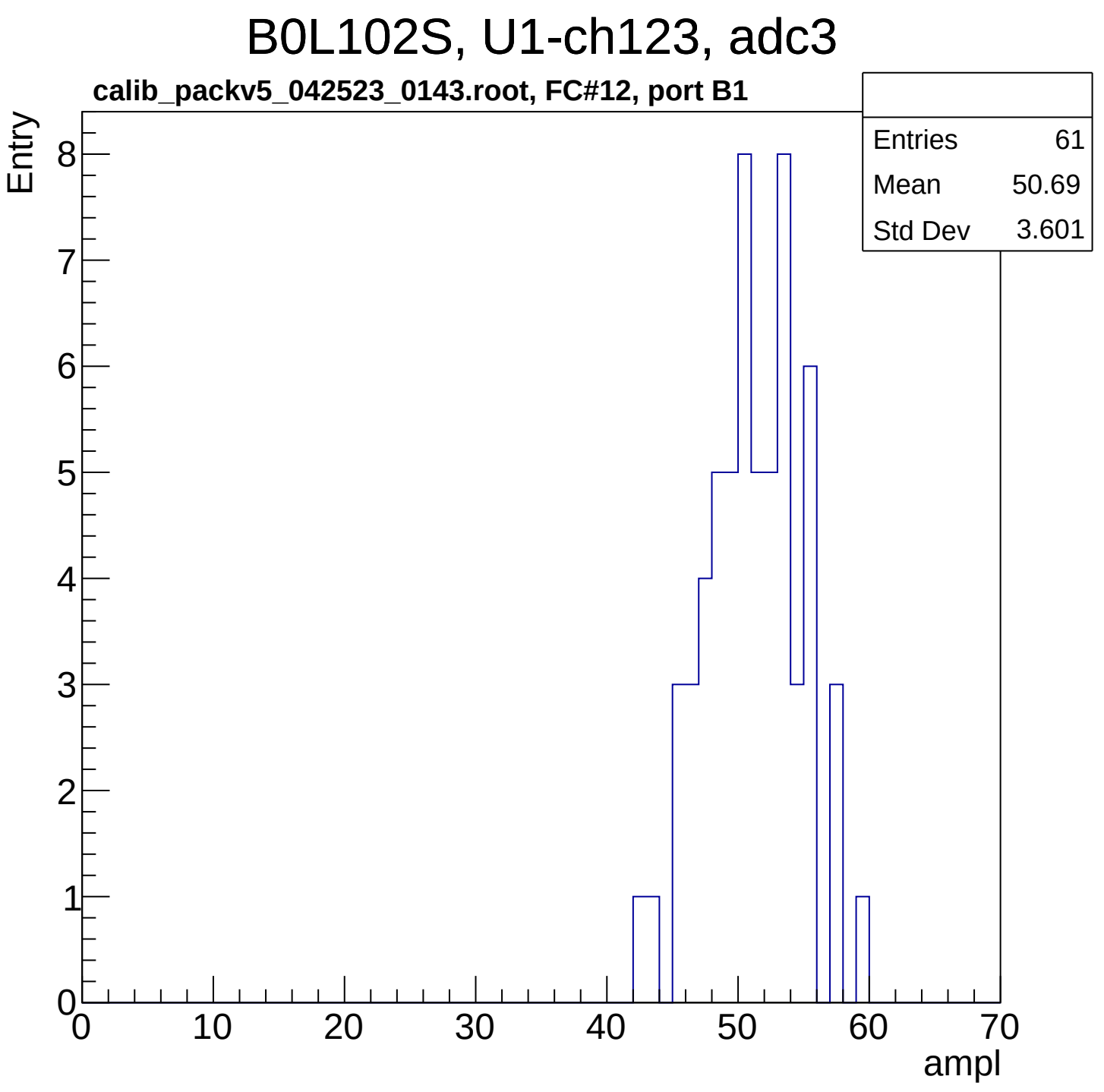

B0L102S, U1-ch122, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.71
Std Dev 1.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch122, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch123, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 29.52
Std Dev 5.259
Gaus mean : 30.6369
Gaus Width: 4.3518
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch123, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 36.77
Std Dev 3.832
Gaus mean : 37.0848
Gaus Width: 4.4418
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch123, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 43.96
Std Dev 3.859
Gaus mean : 44.4316
Gaus Width: 4.3030
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch123, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 50.69
Std Dev 3.601
Entry
0
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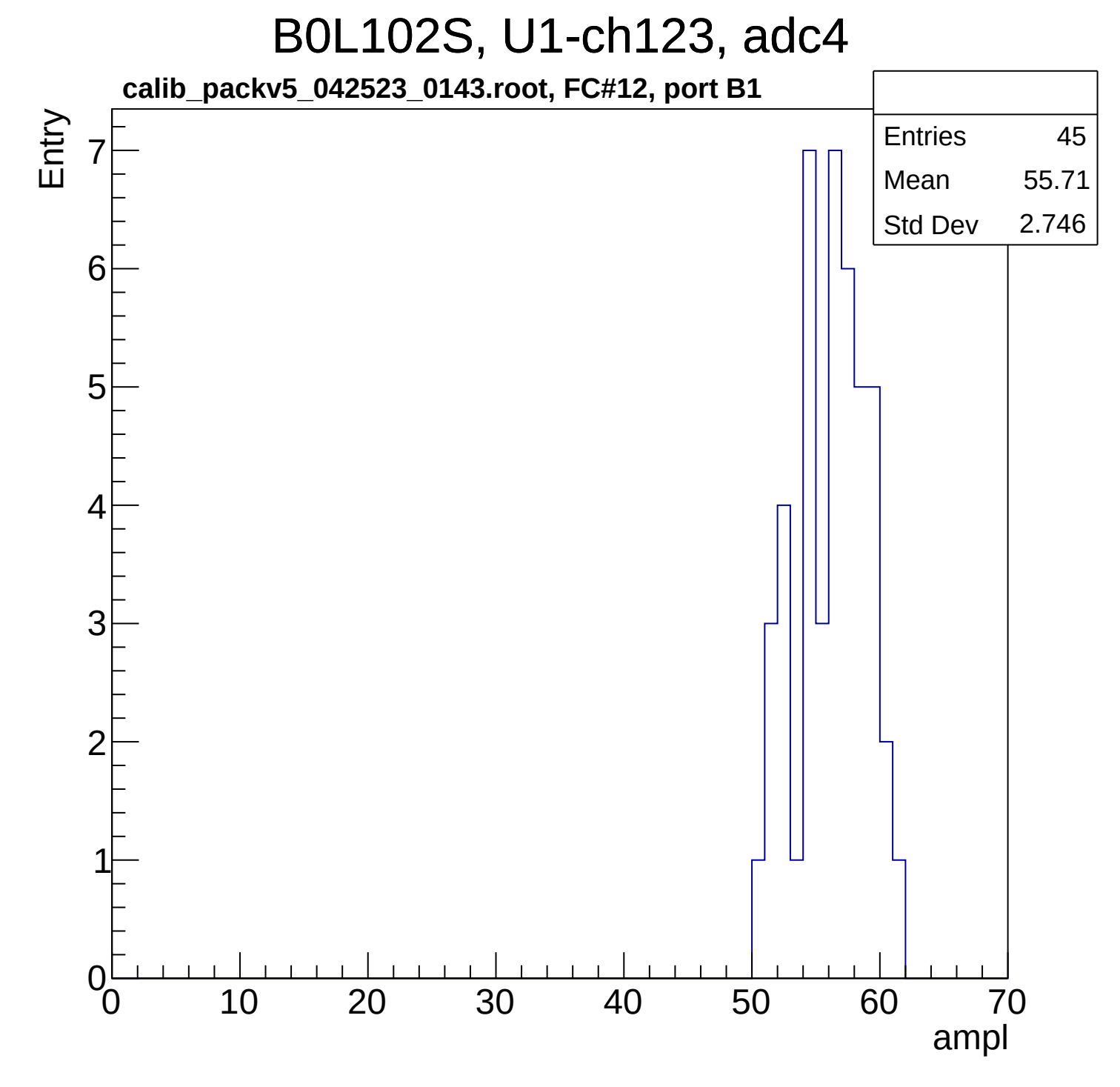
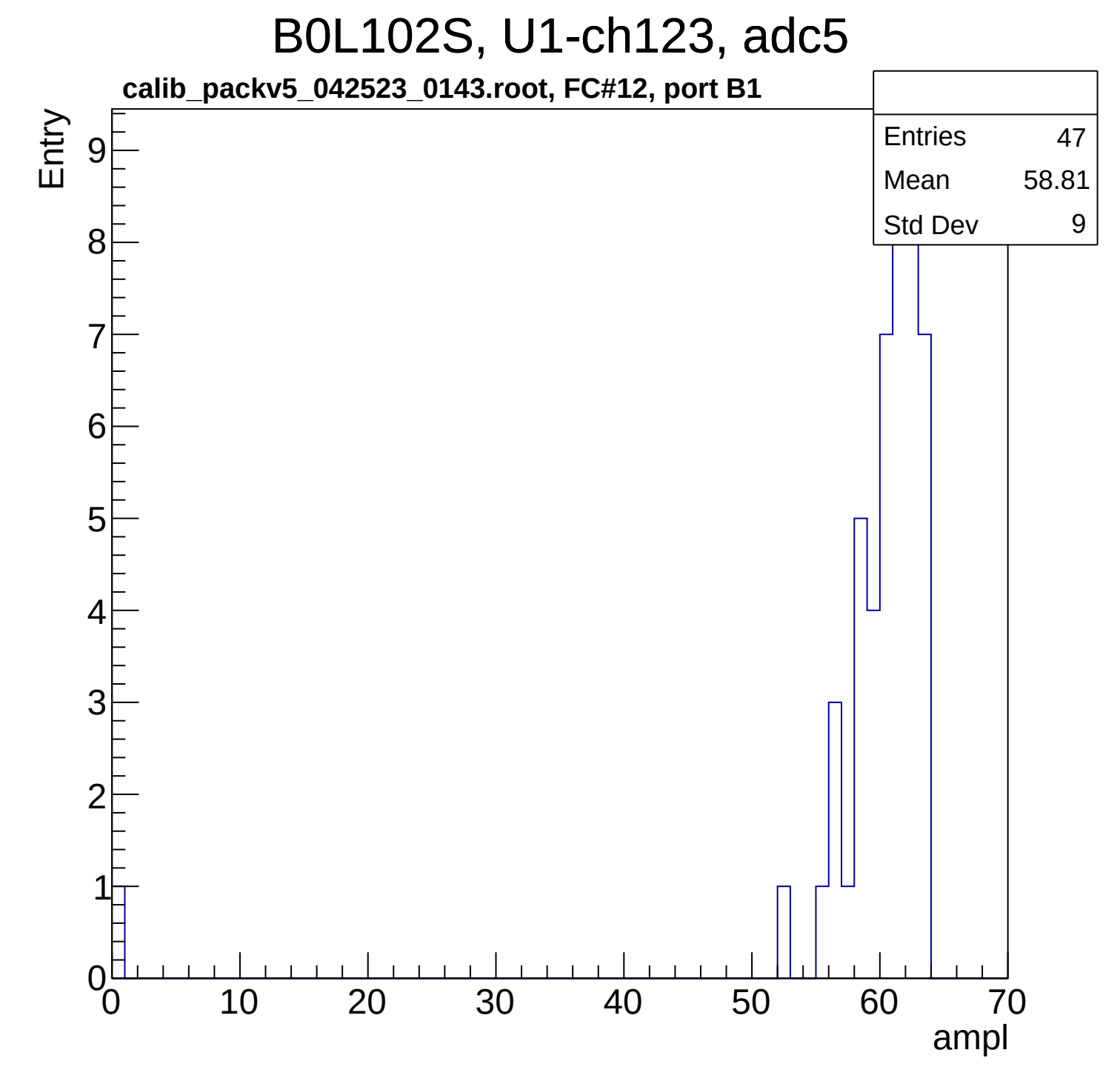
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch123, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 55.71
Std Dev 2.746
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch123, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.81
Std Dev 9
Entry
ampl
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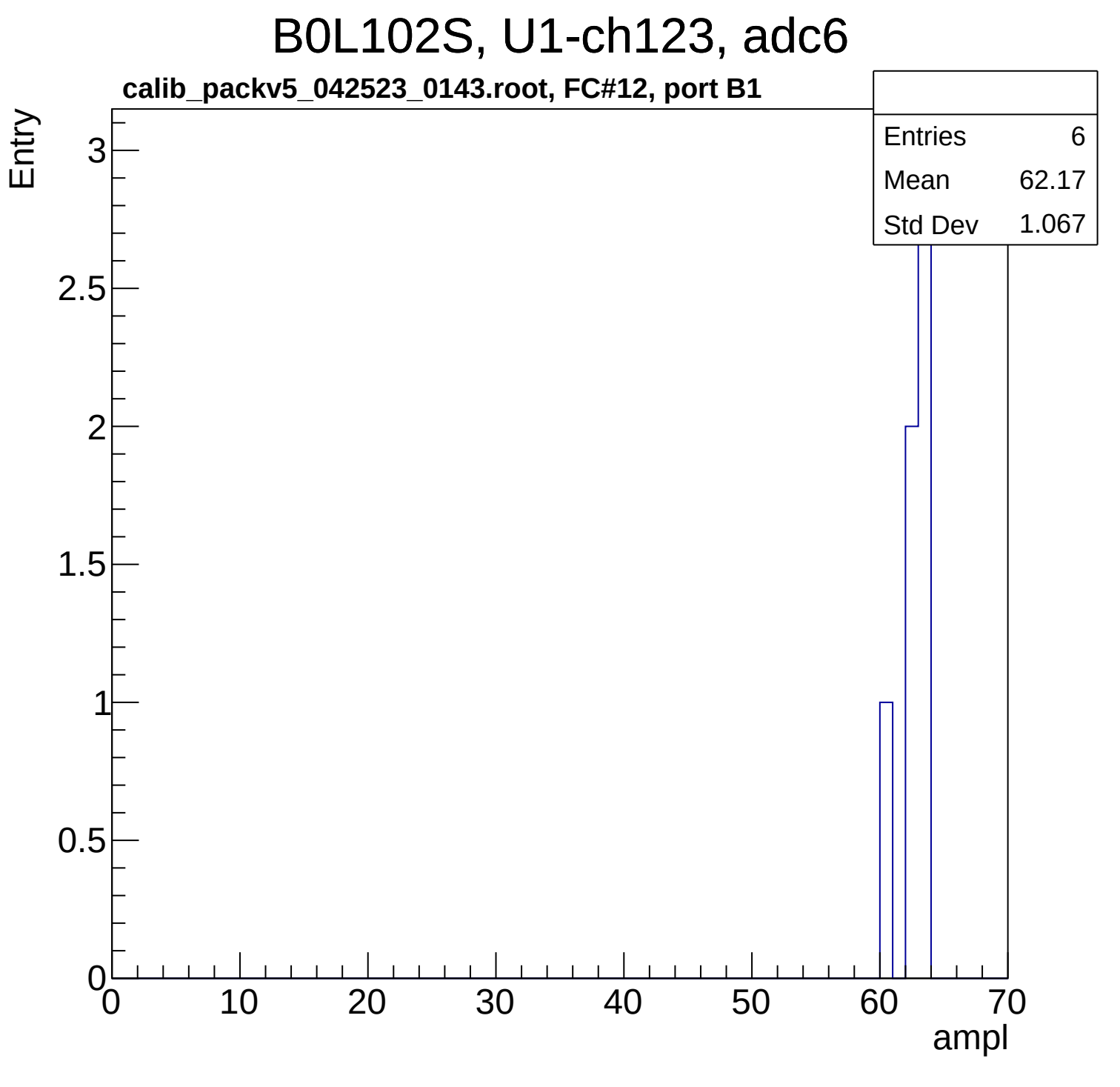
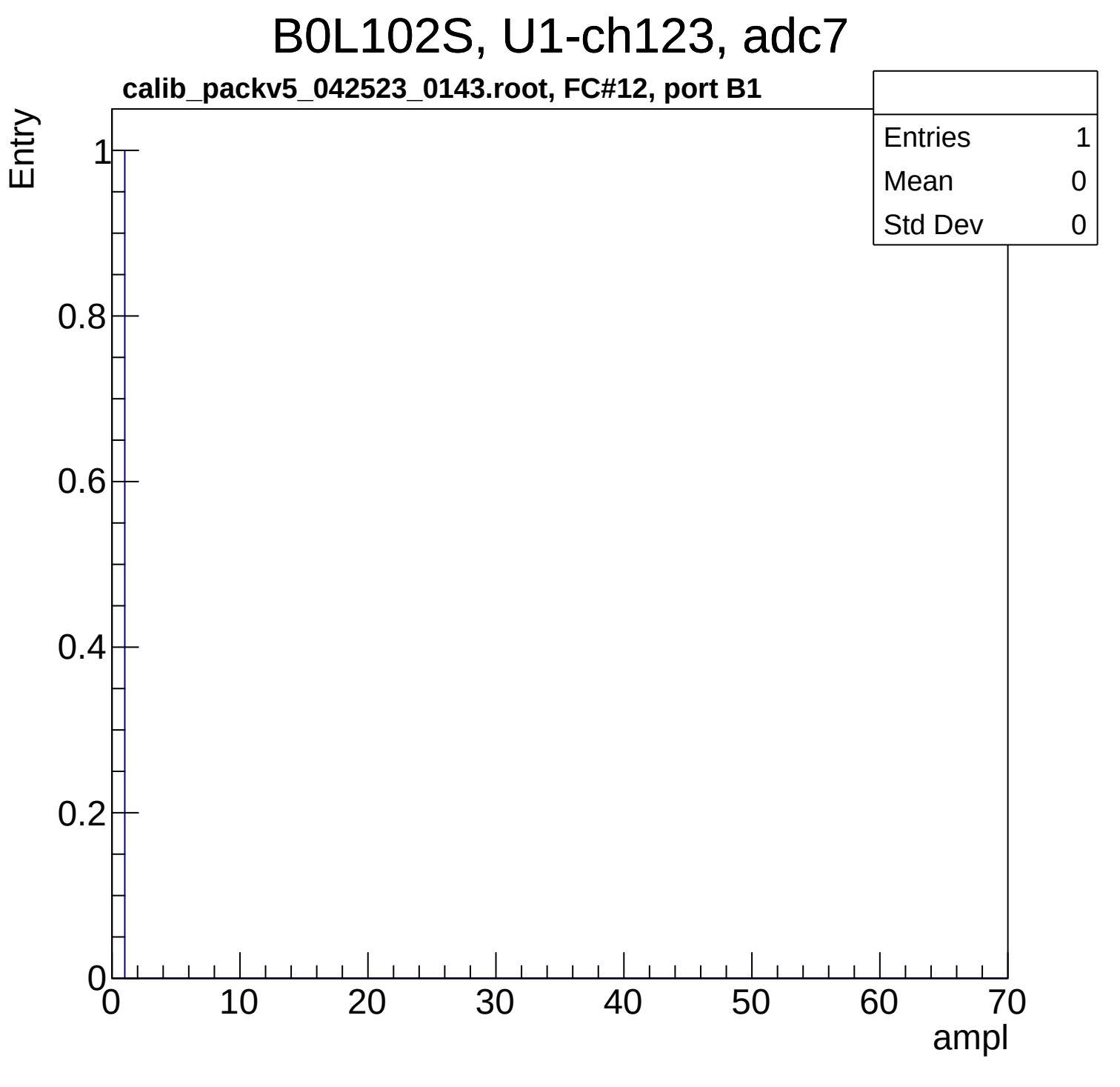
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch123, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch123, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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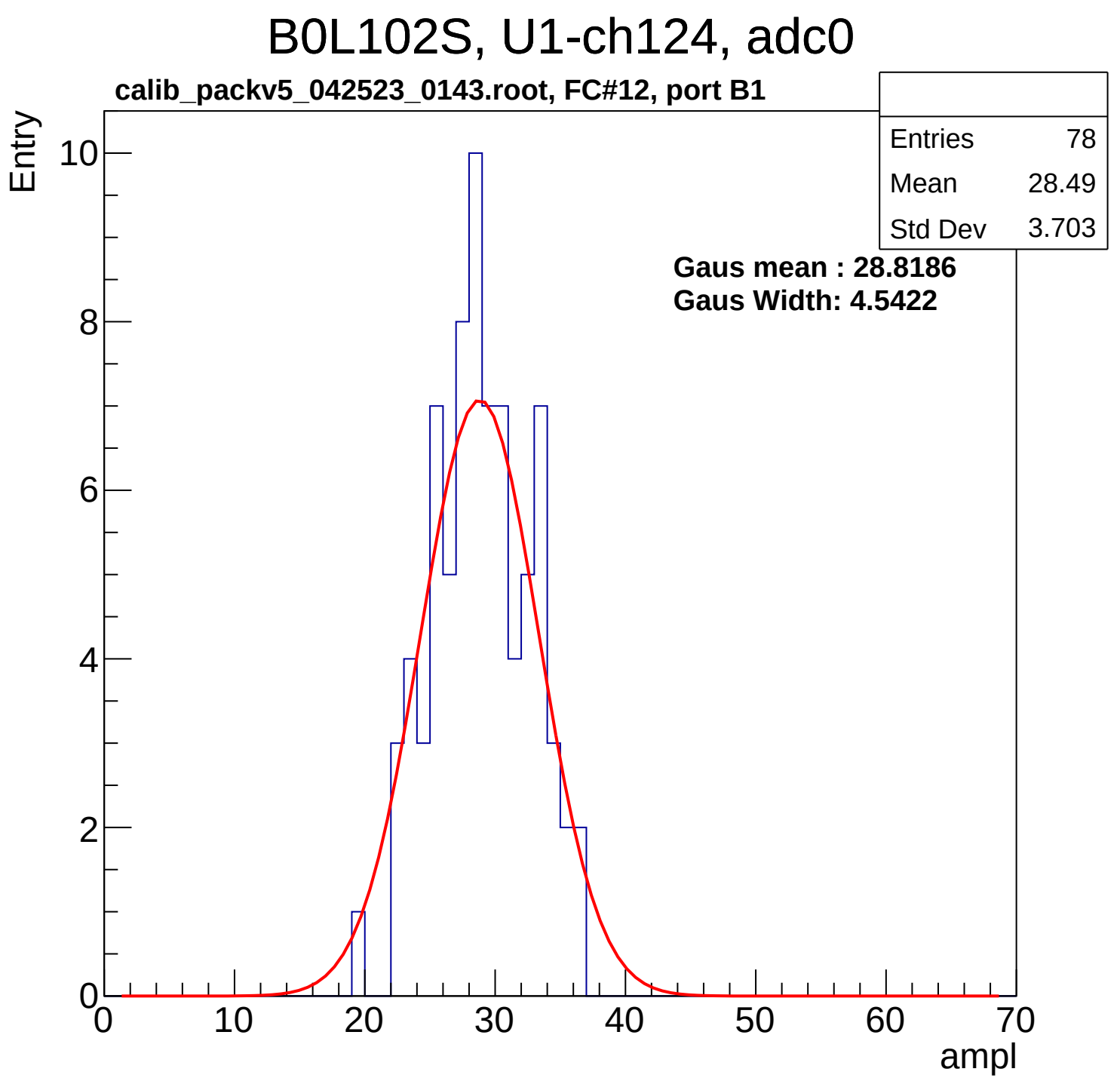
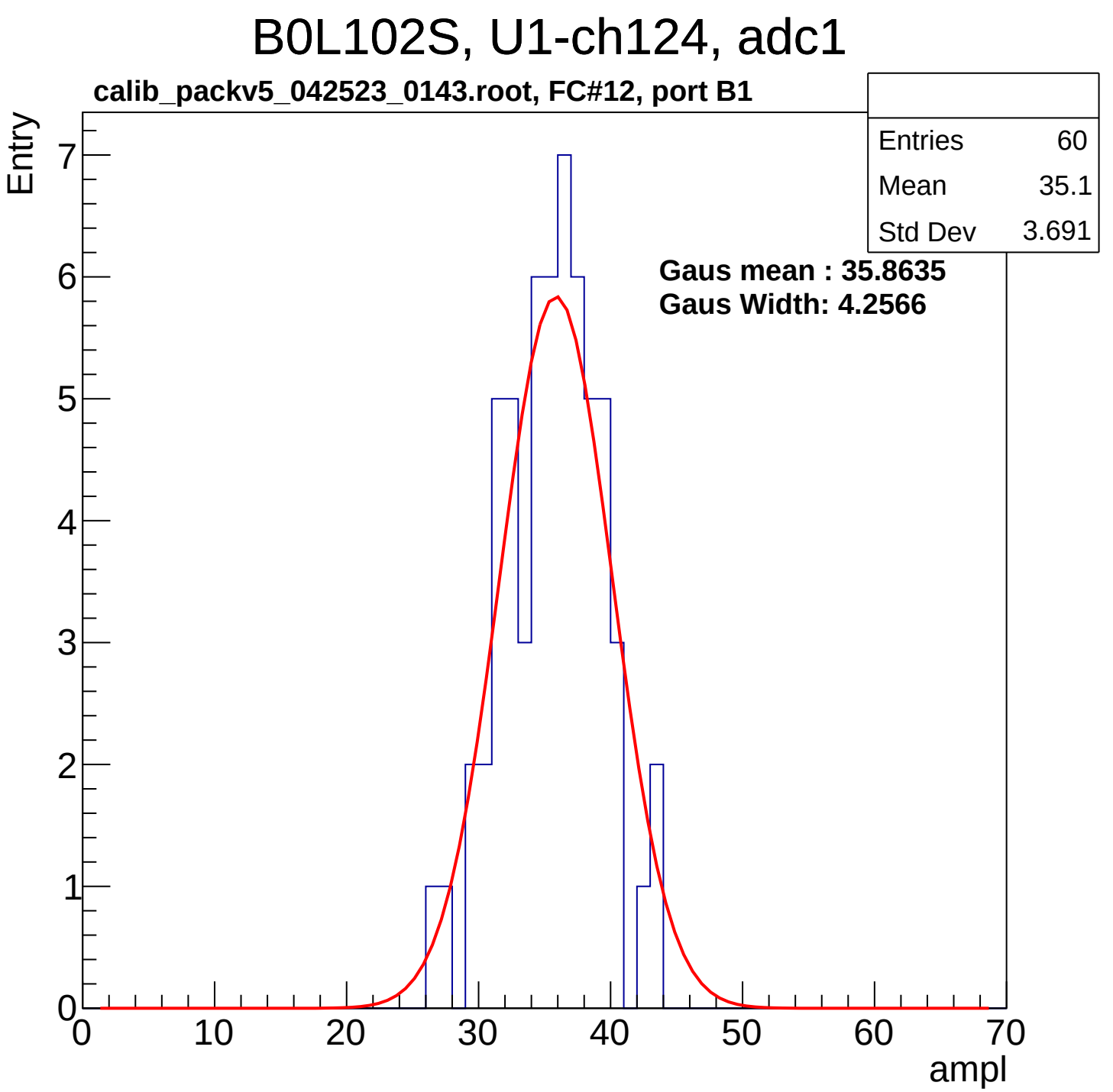
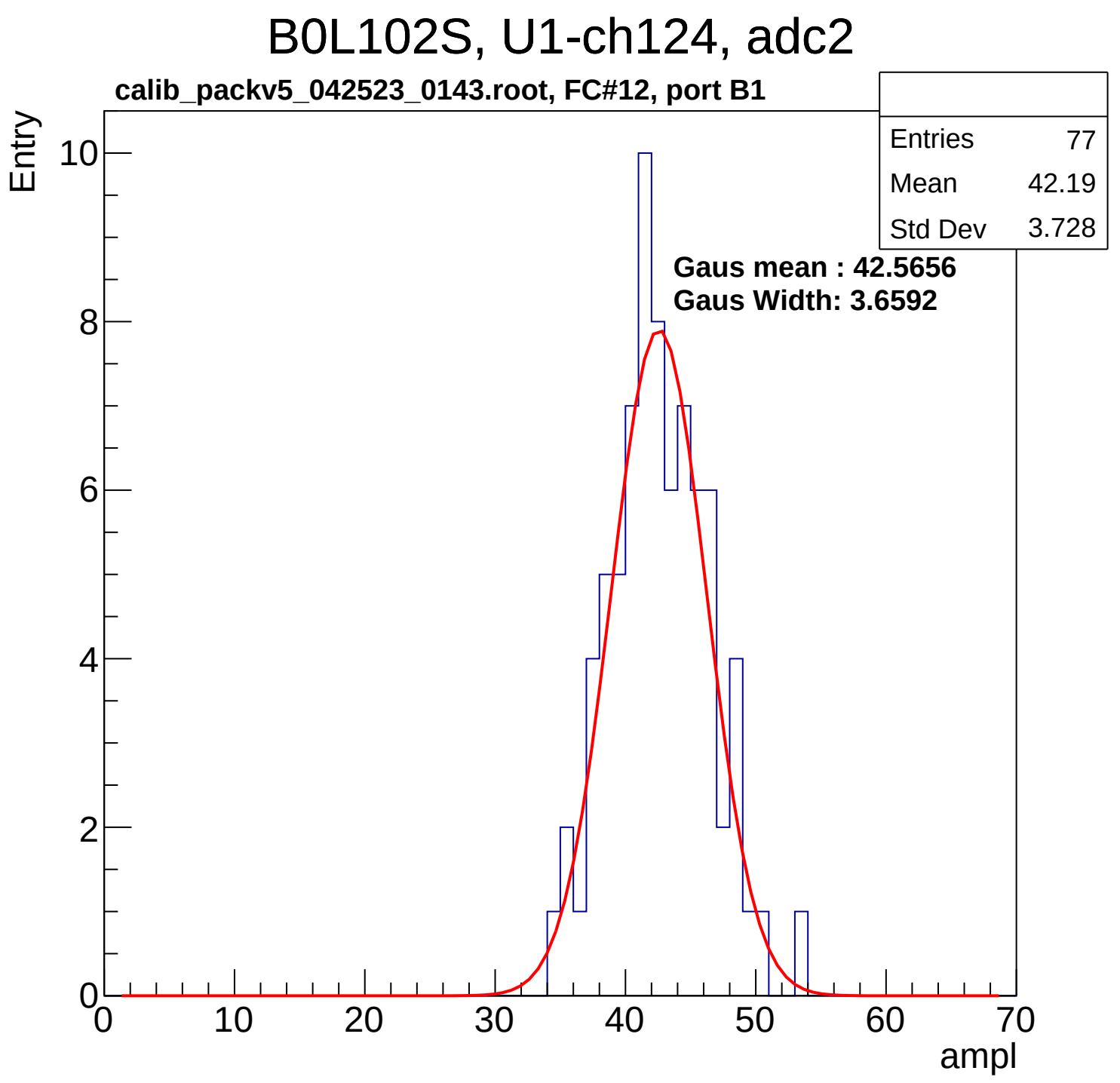
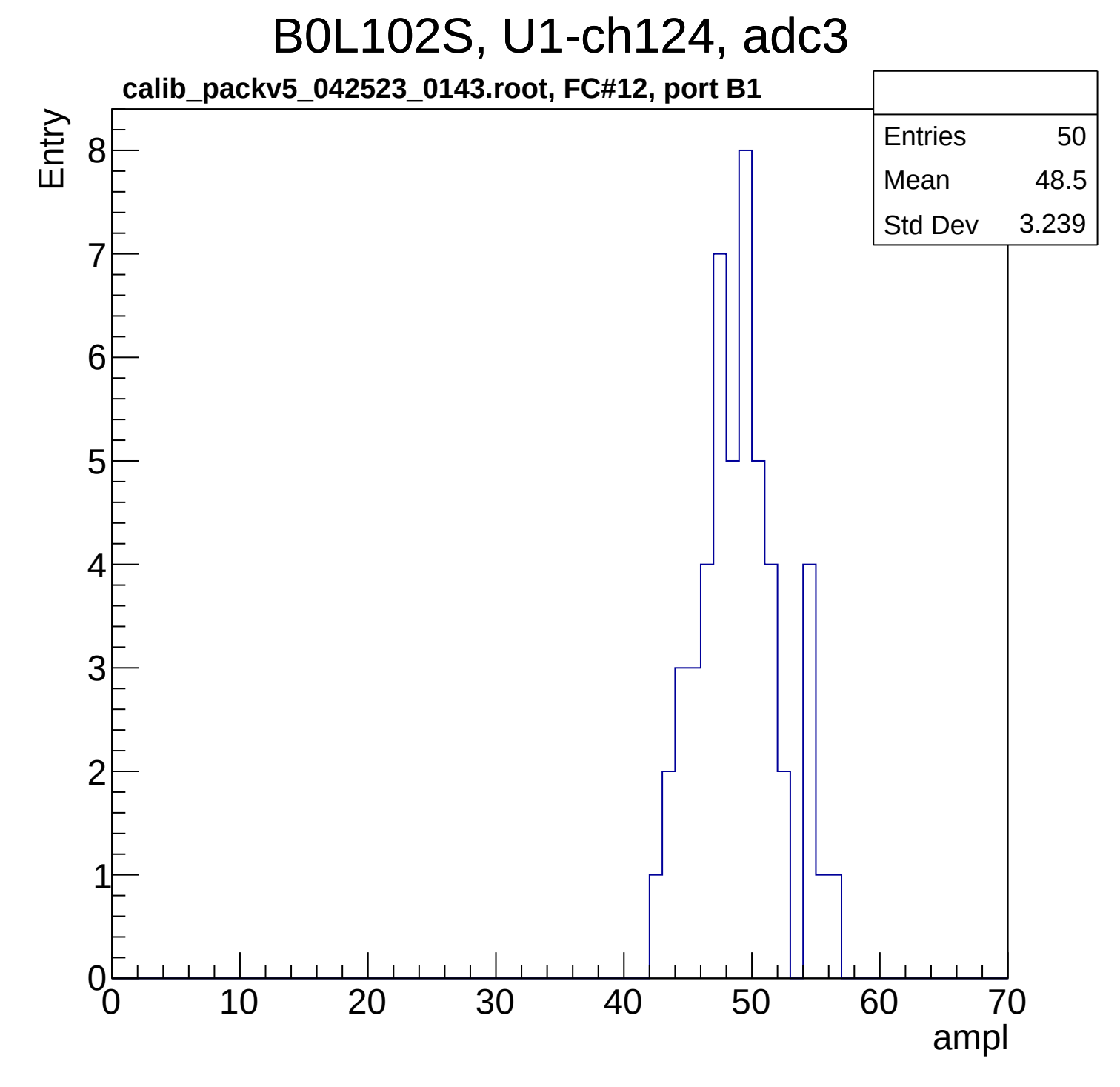
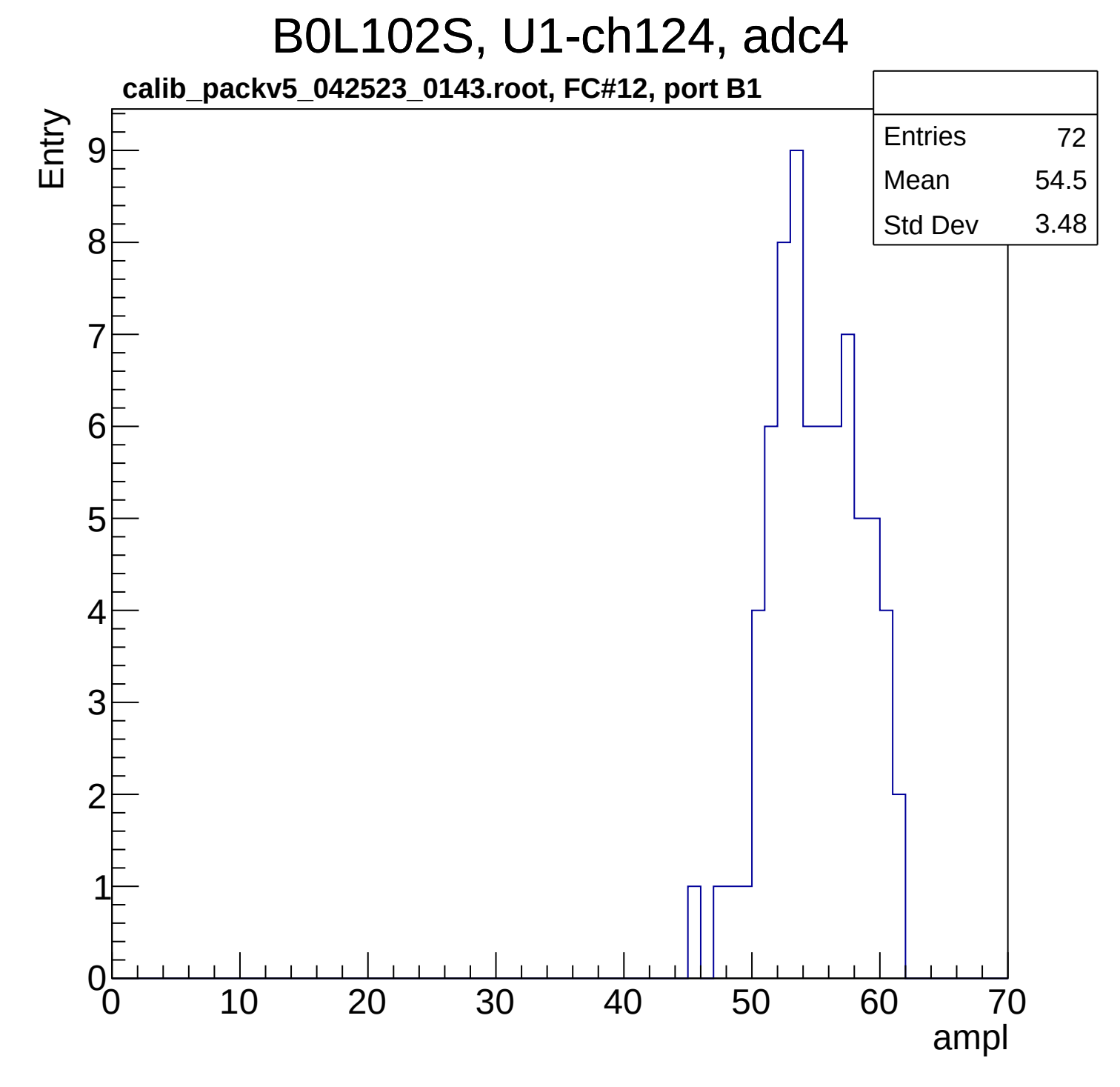
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch124, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 28.49
Std Dev 3.703
Gaus mean : 28.8186
Gaus Width: 4.5422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch124, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 35.1
Std Dev 3.691
Gaus mean : 35.8635
Gaus Width: 4.2566
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch124, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 42.19
Std Dev 3.728
Gaus mean : 42.5656
Gaus Width: 3.6592
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch124, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 48.5
Std Dev 3.239
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch124, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 54.5
Std Dev 3.48
Entry
ampl
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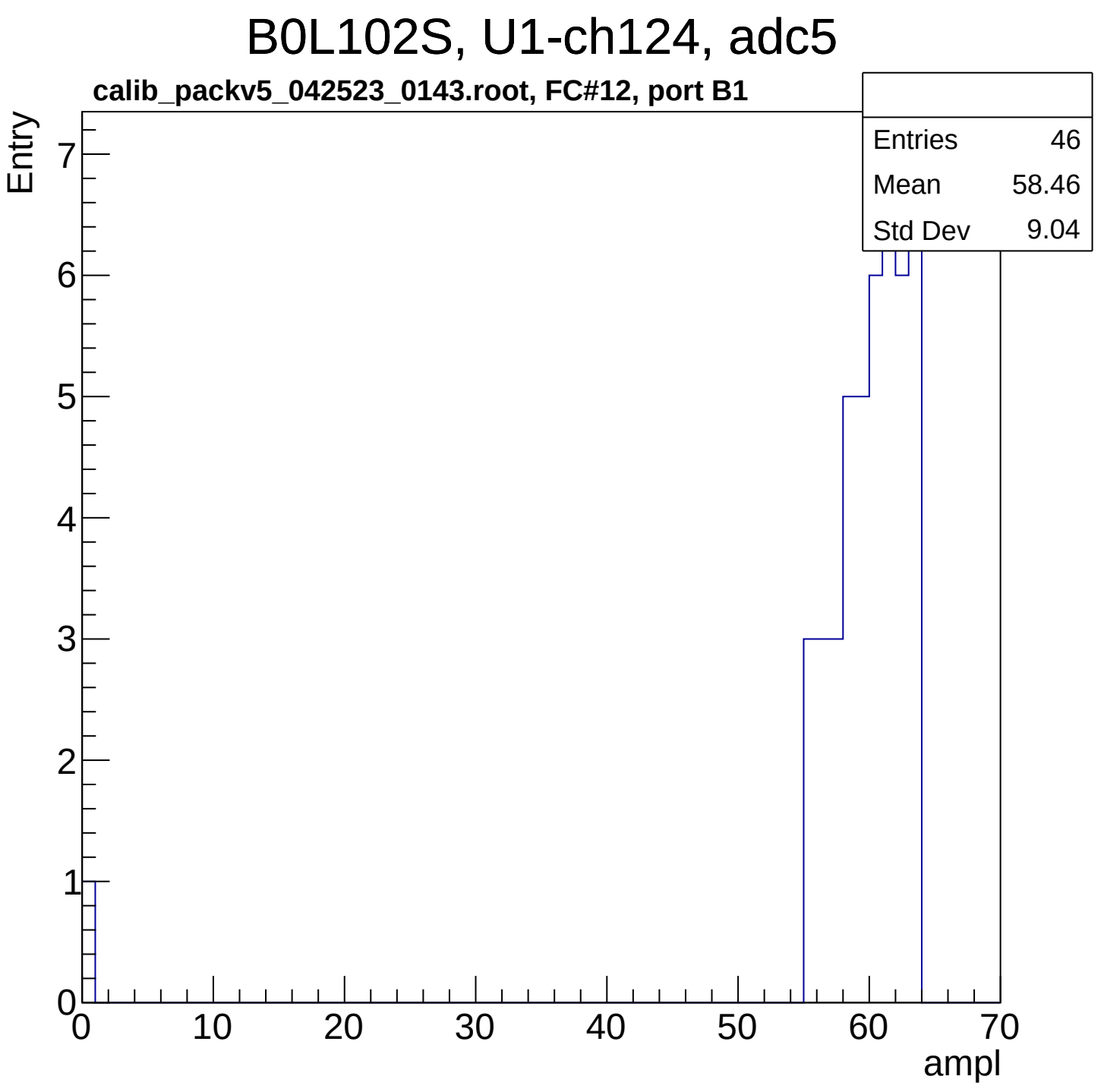
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch124, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.46
Std Dev 9.04
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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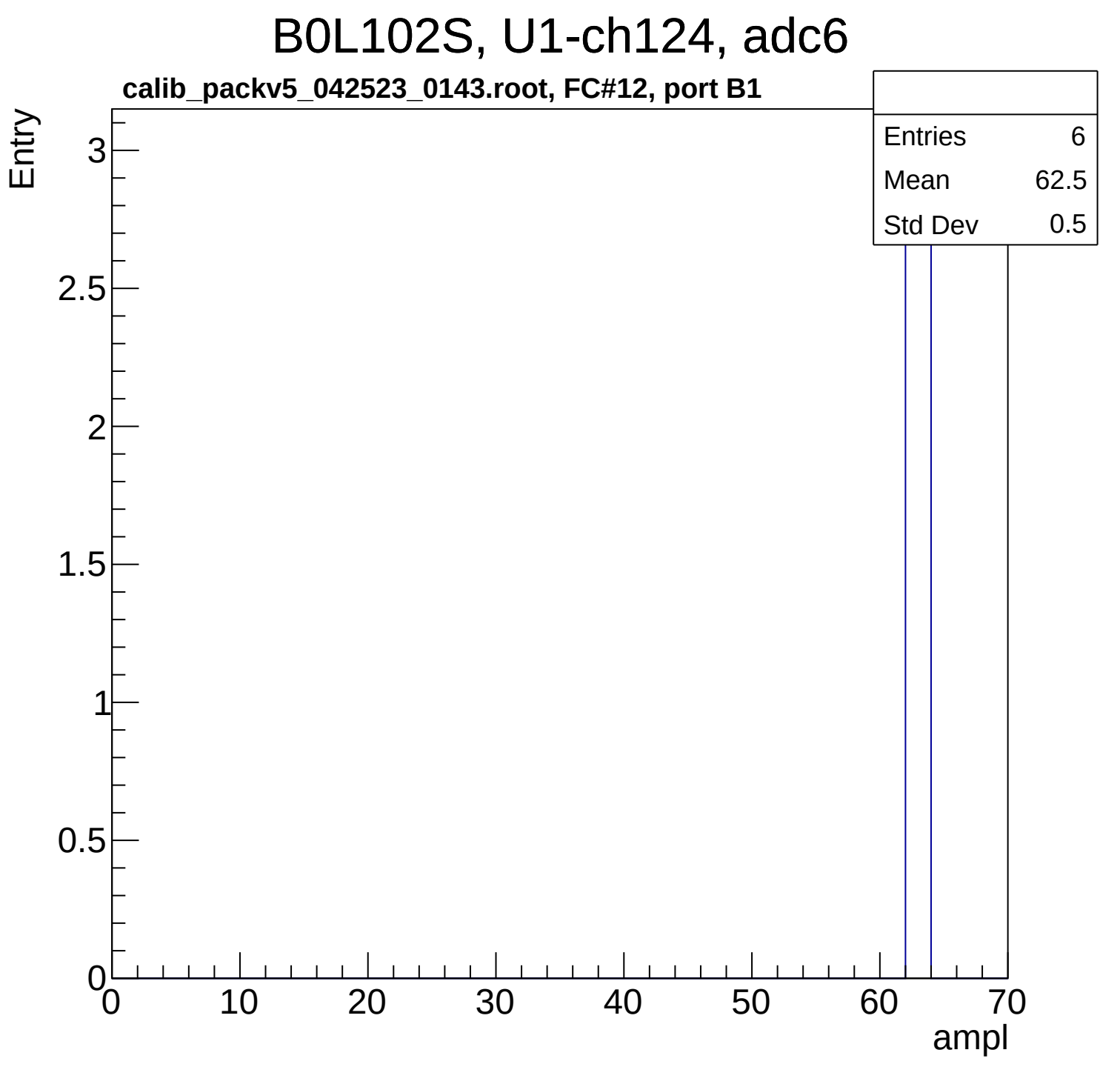
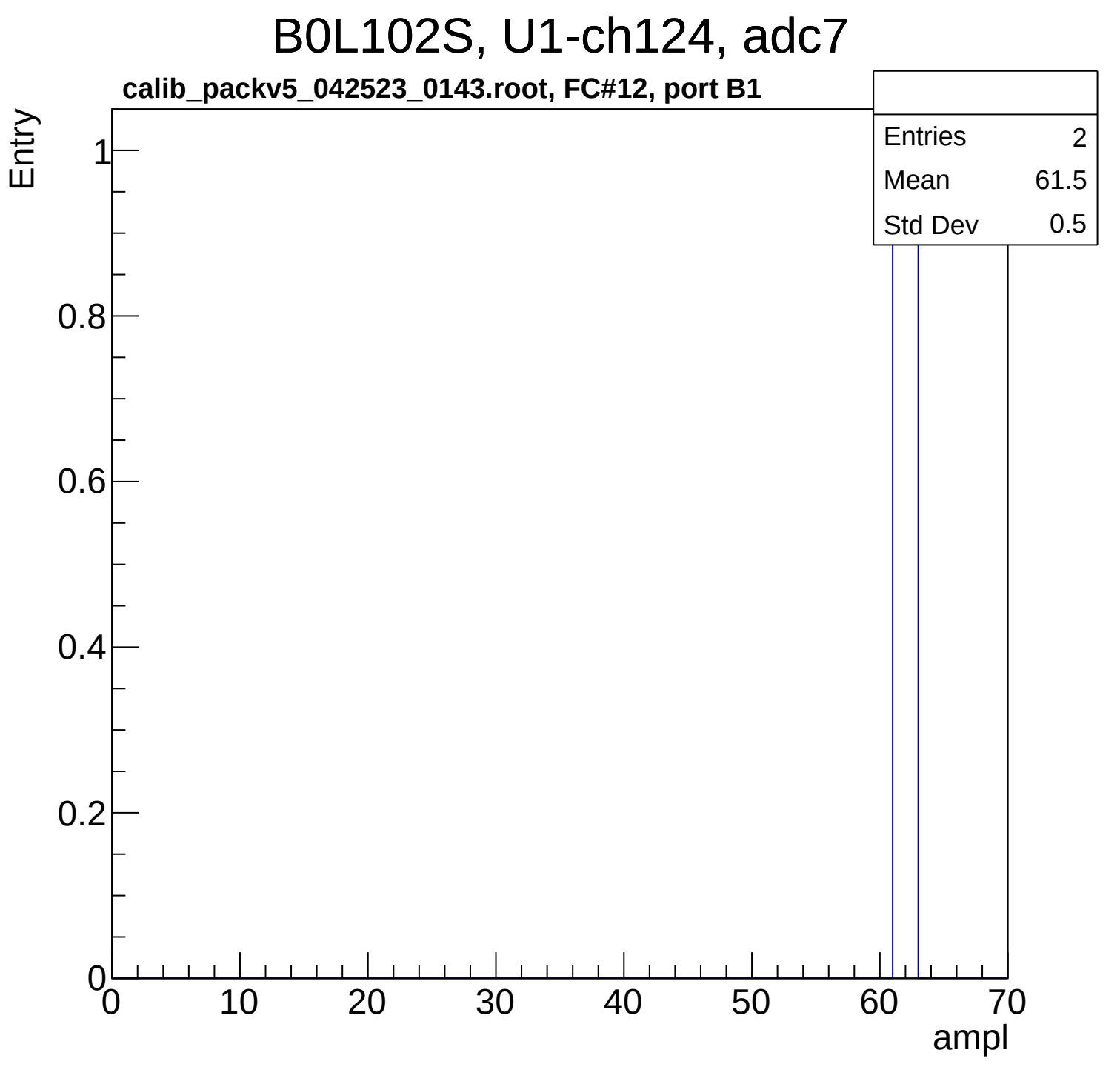

B0L102S, U1-ch124, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U1-ch124, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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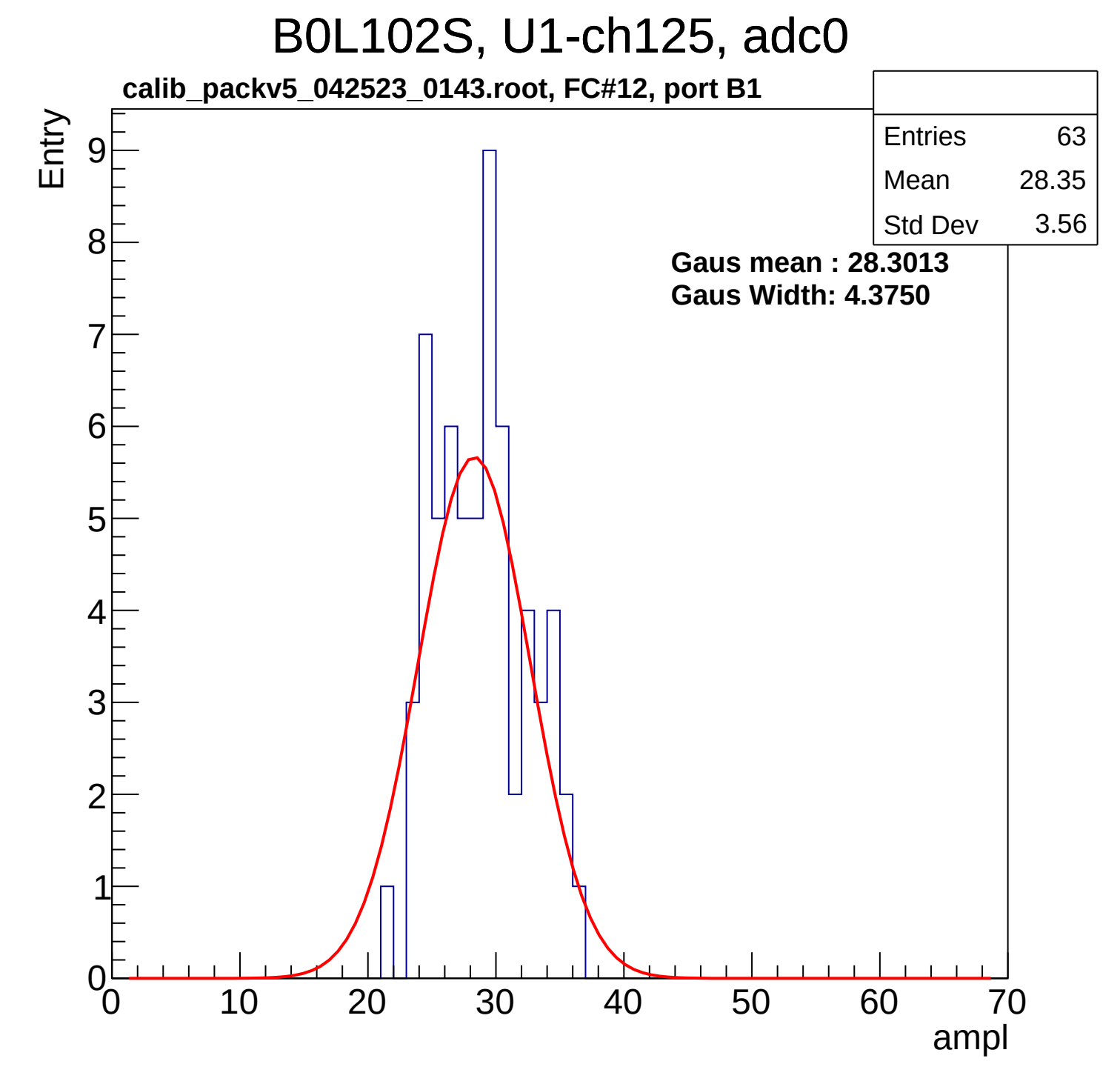
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch125, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.35
Std Dev 3.56
Gaus mean : 28.3013
Gaus Width: 4.3750
Entry
ampl
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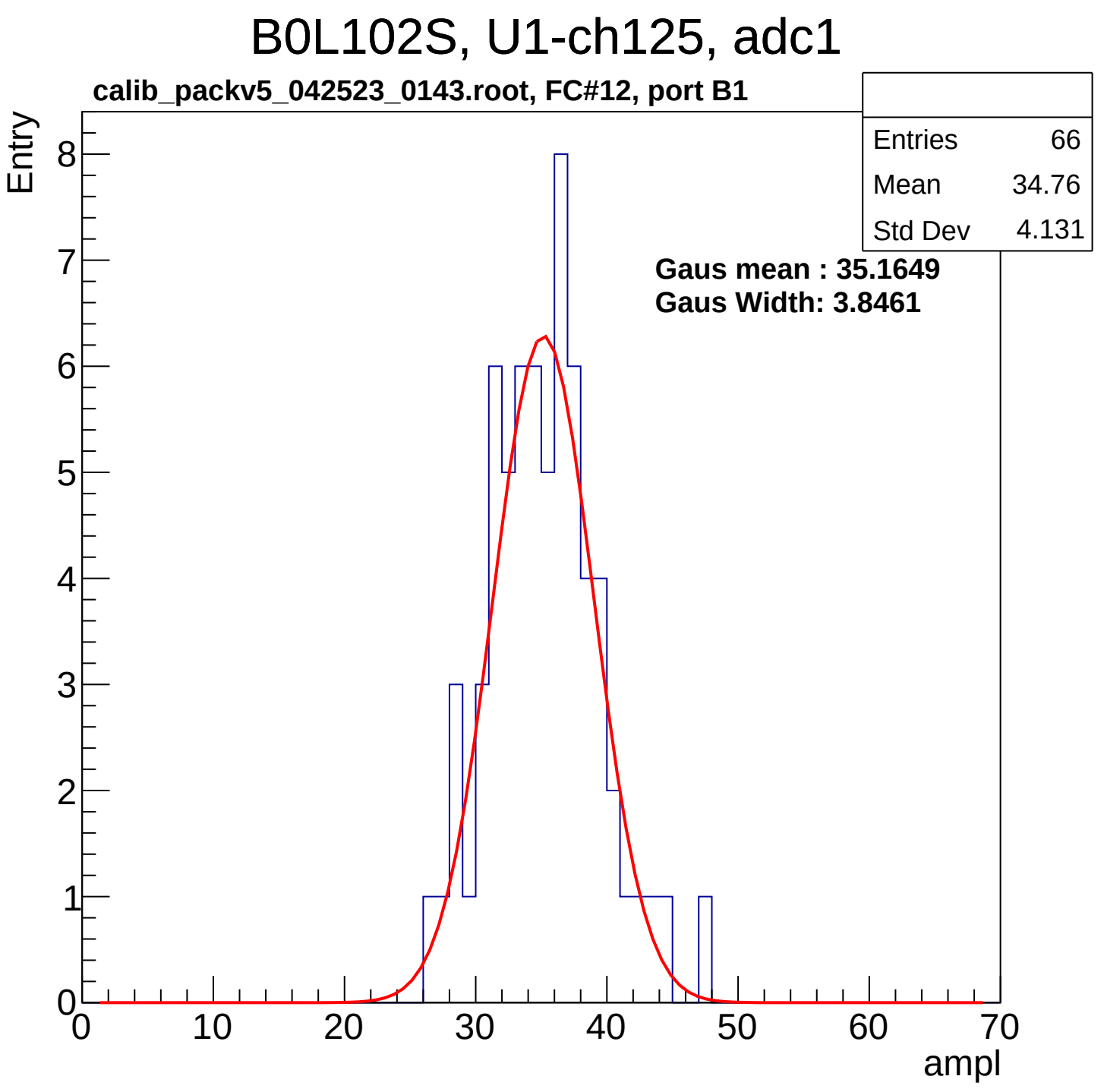
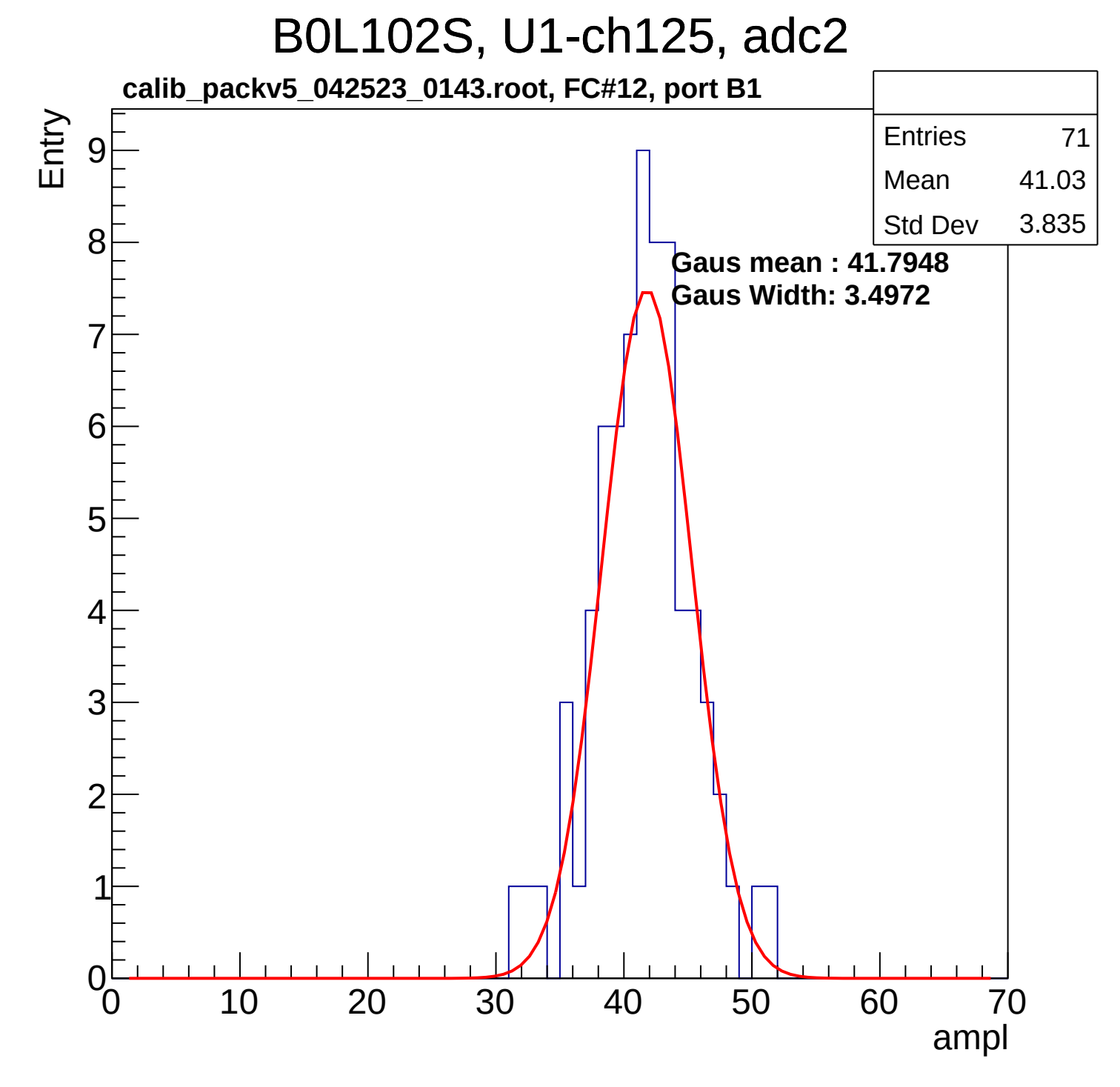
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch125, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 34.76
Std Dev 4.131
Gaus mean : 35.1649
Gaus Width: 3.8461
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch125, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 41.03
Std Dev 3.835
Gaus mean : 41.7948
Gaus Width: 3.4972
Entry
ampl
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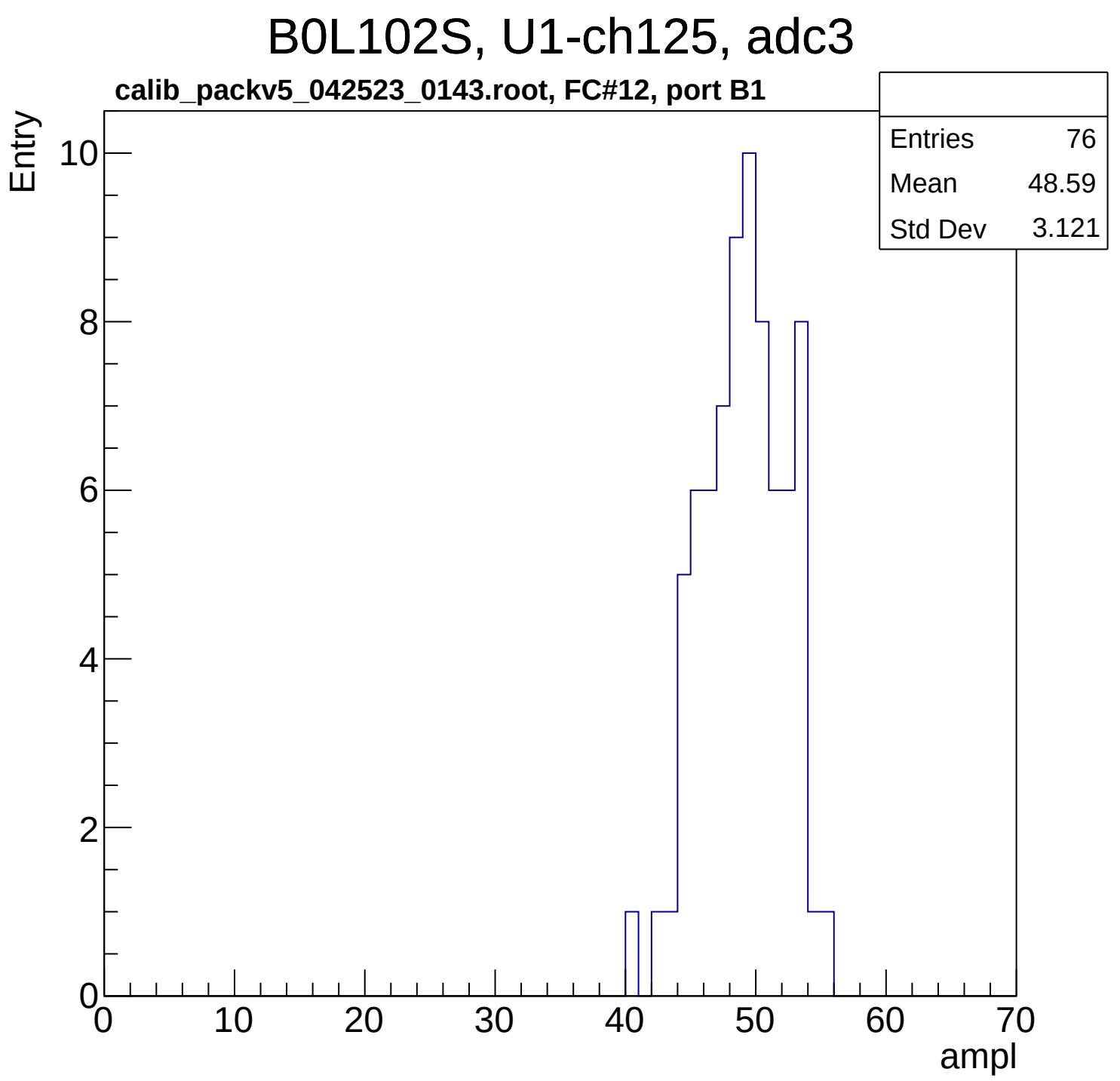
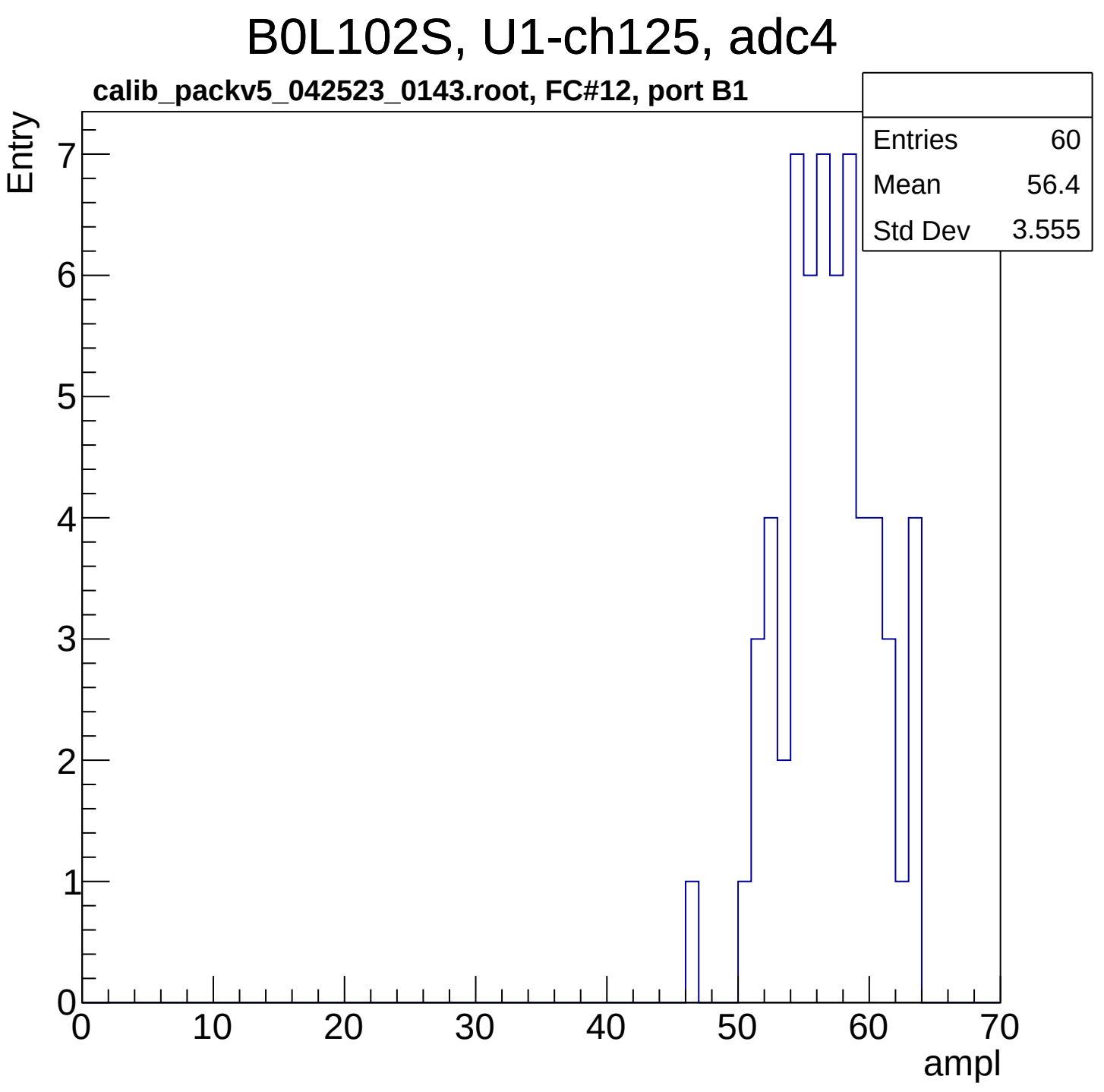
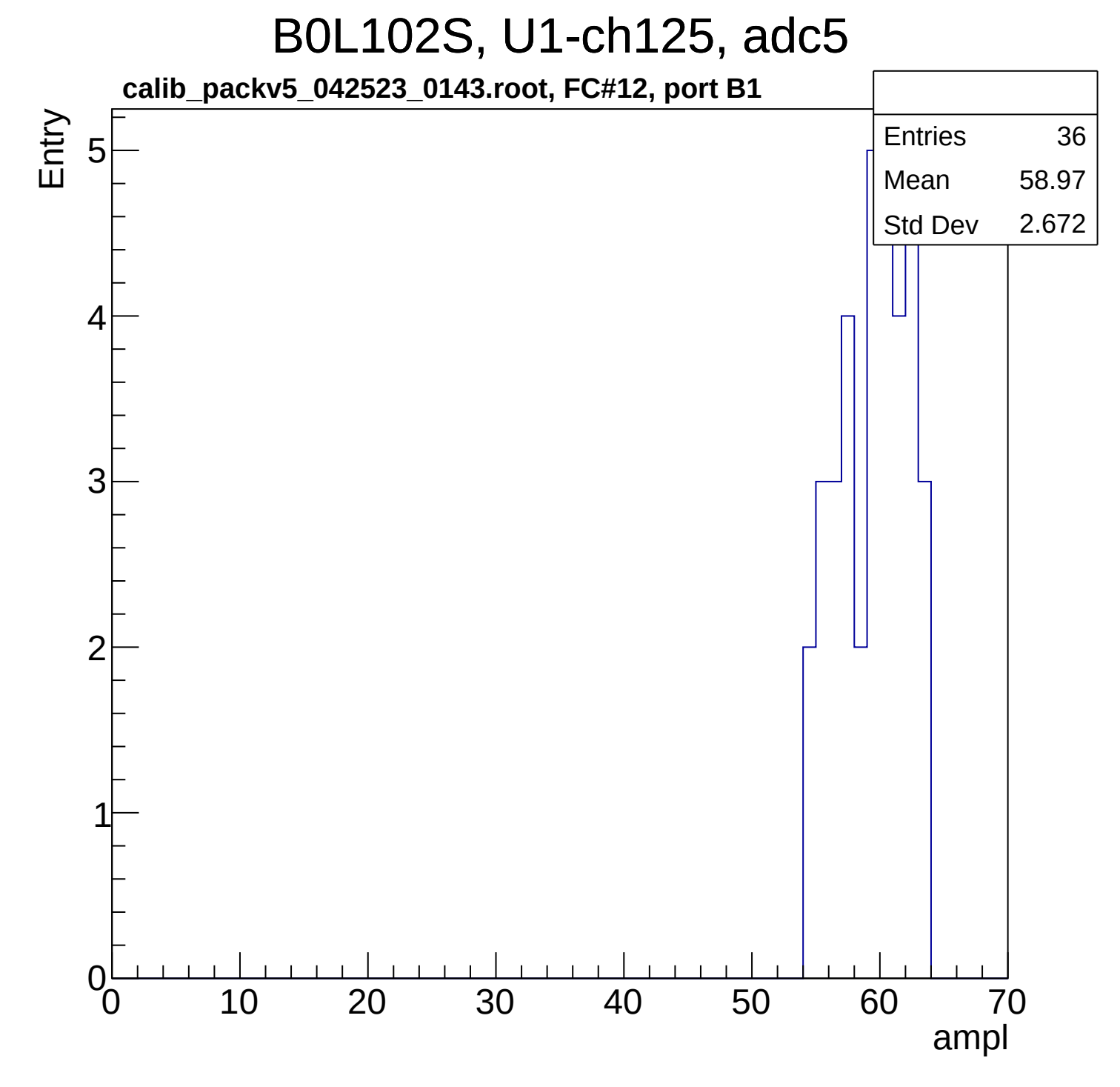
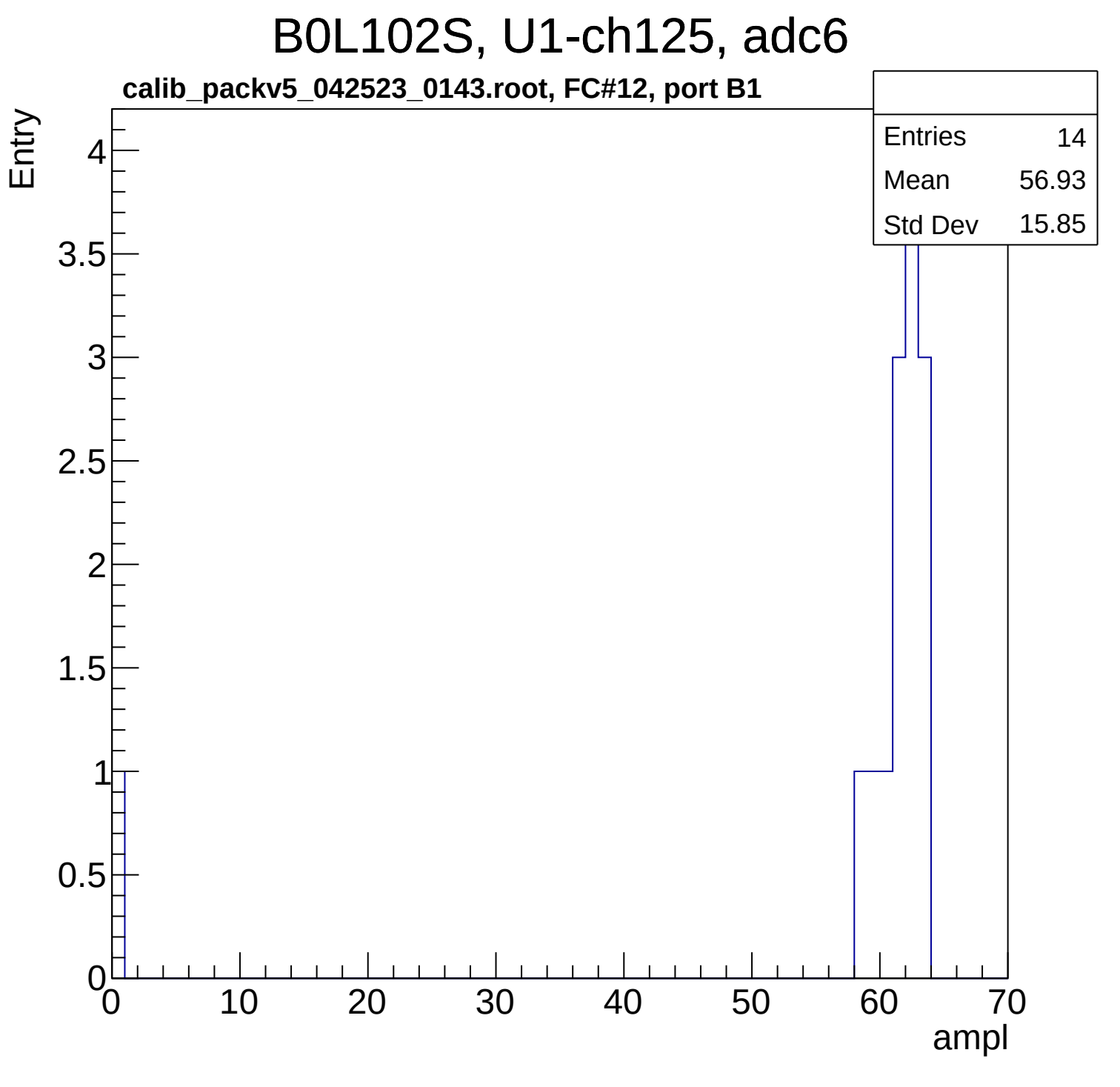
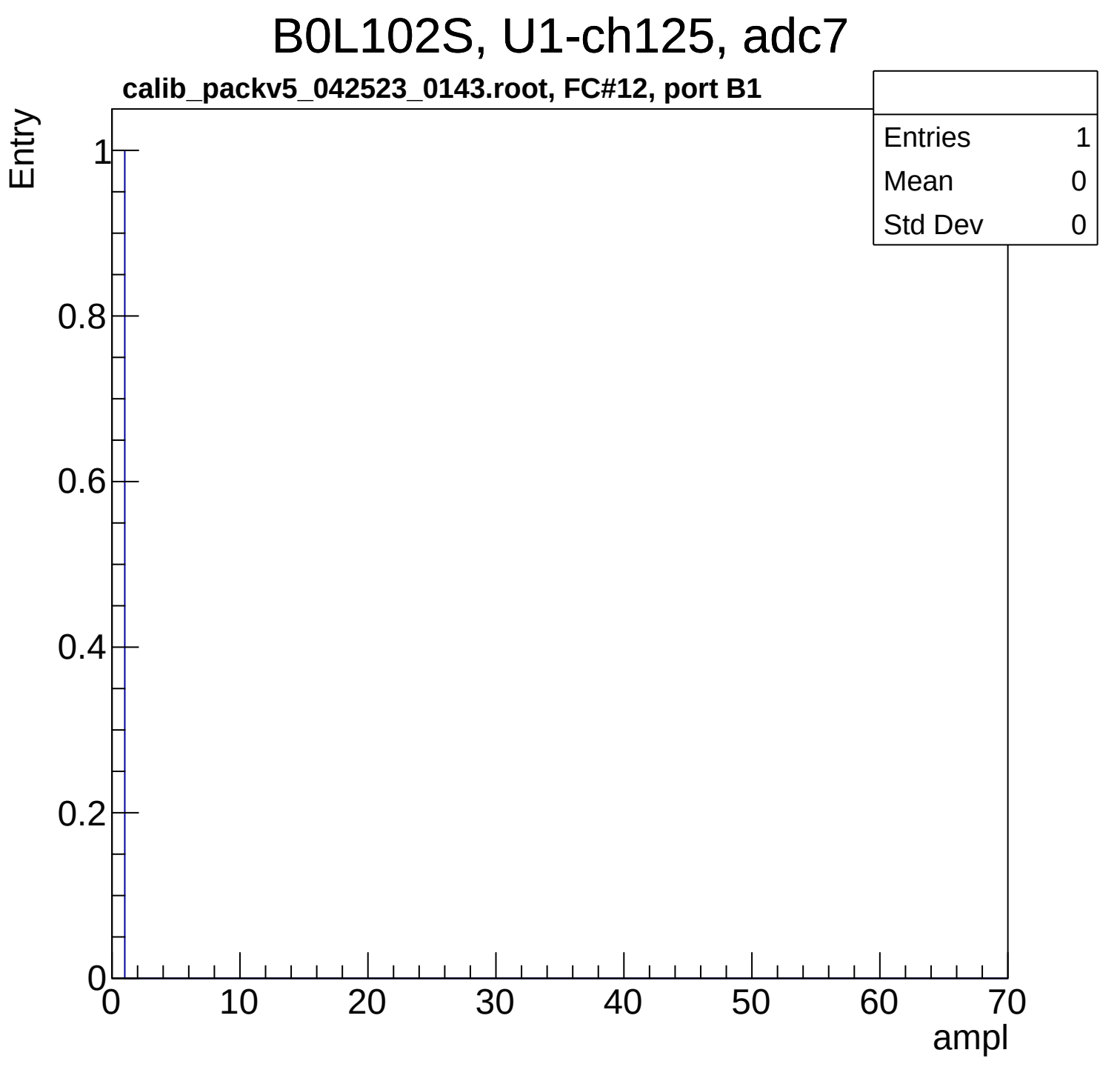
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

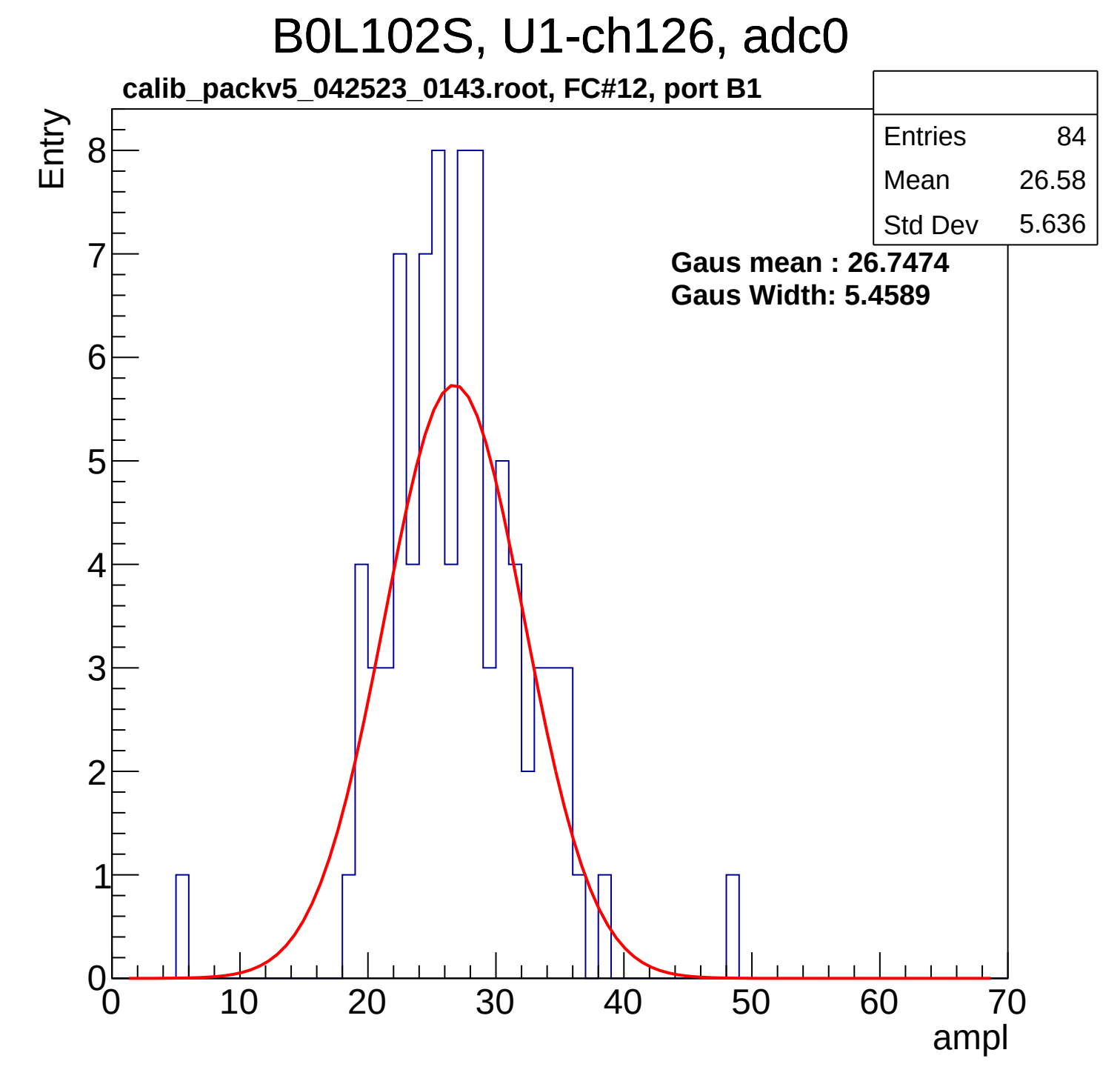

B0L102S, U1-ch125, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 48.59
Std Dev 3.121
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

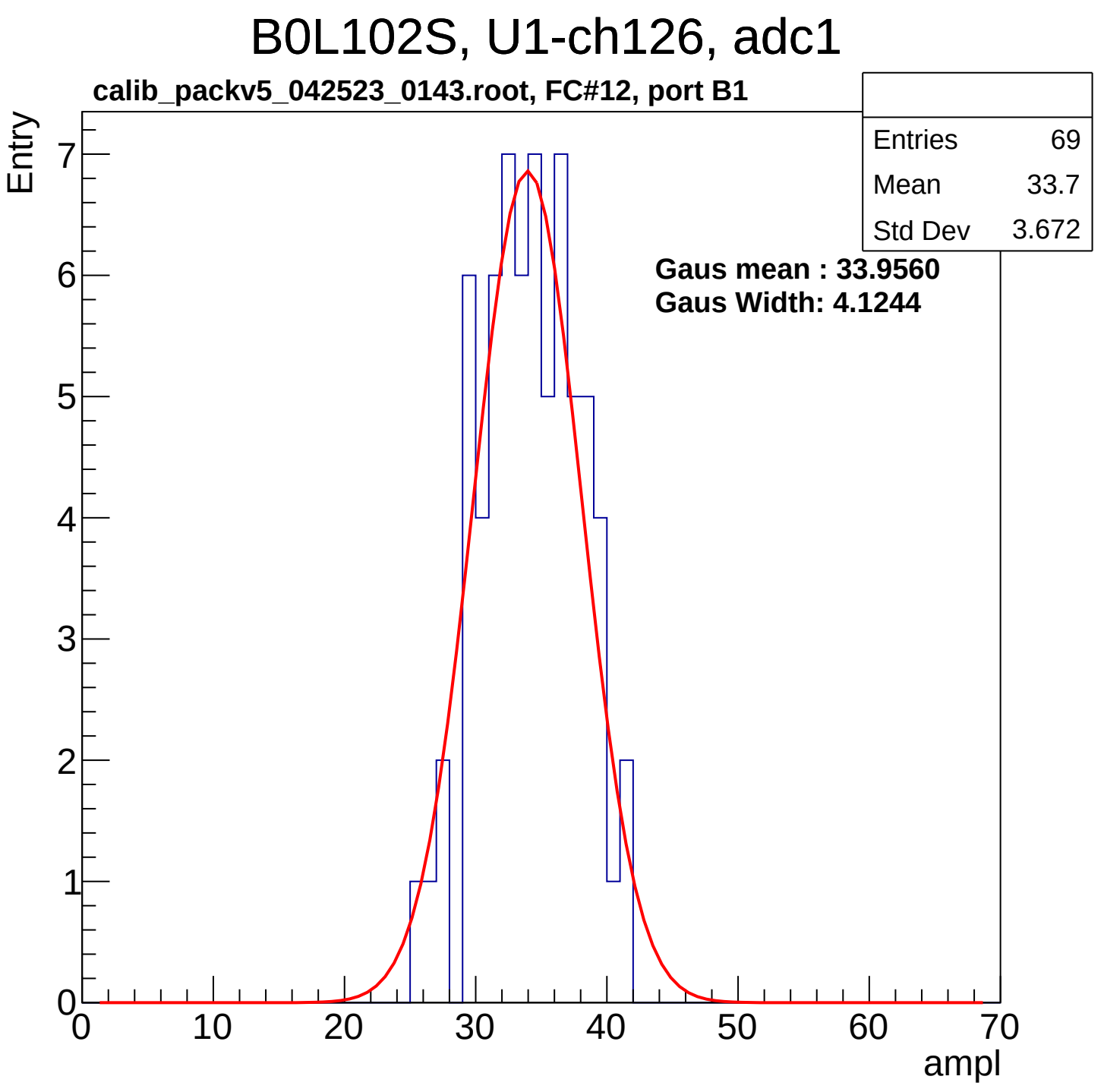

B0L102S, U1-ch125, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.4
Std Dev 3.555
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

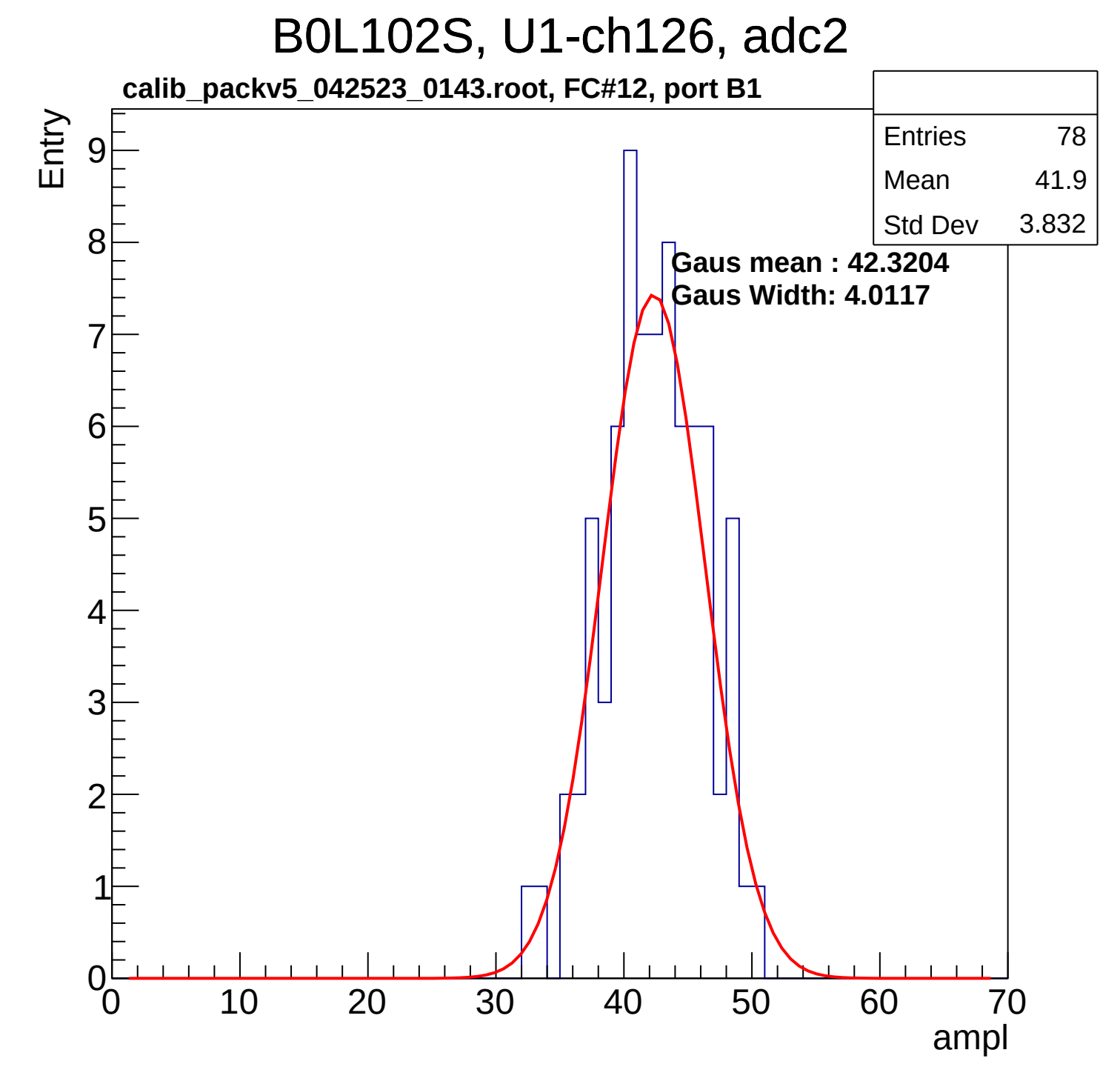

B0L102S, U1-ch125, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.97
Std Dev 2.672
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

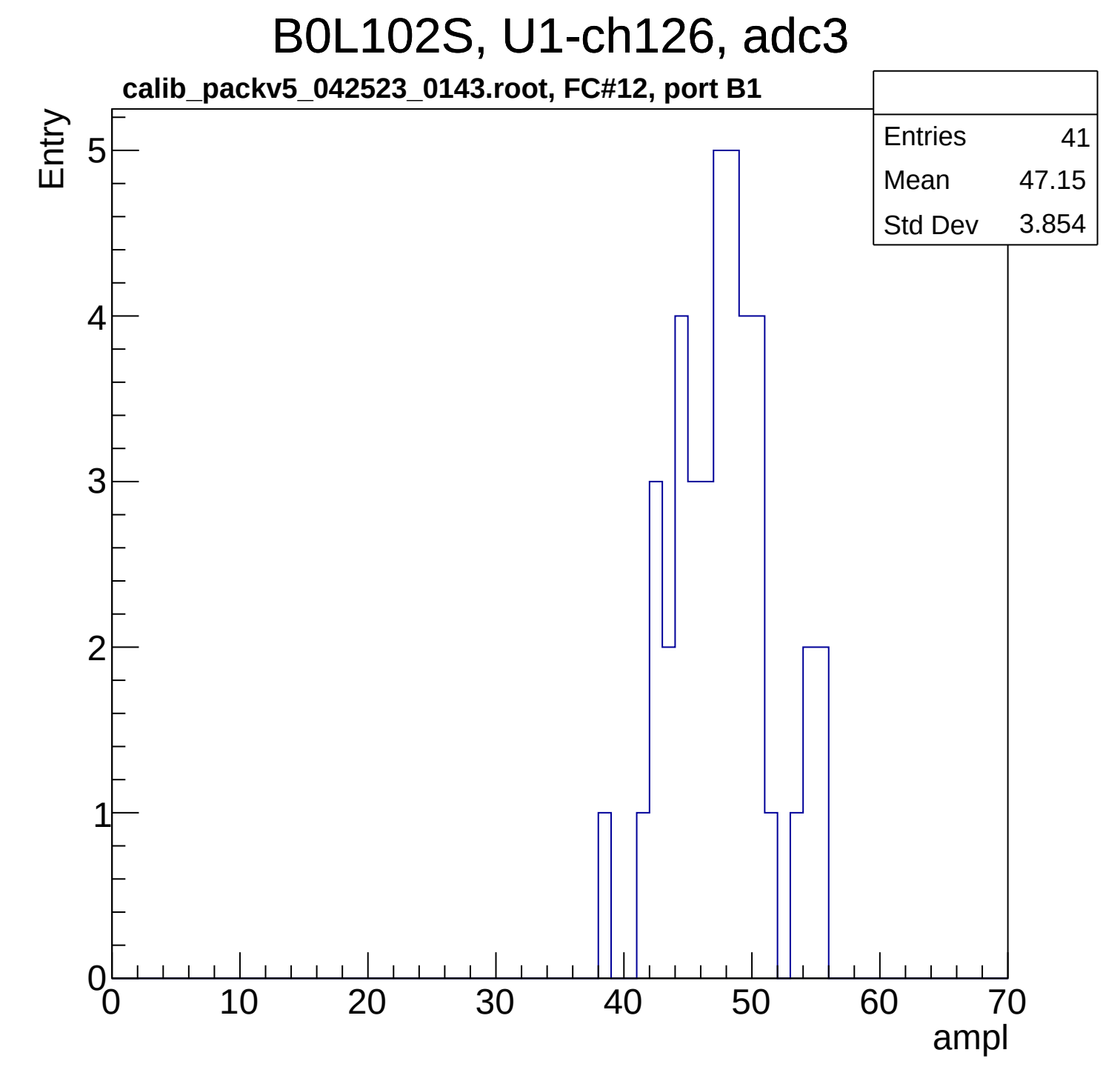

B0L102S, U1-ch125, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 56.93
Std Dev 15.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

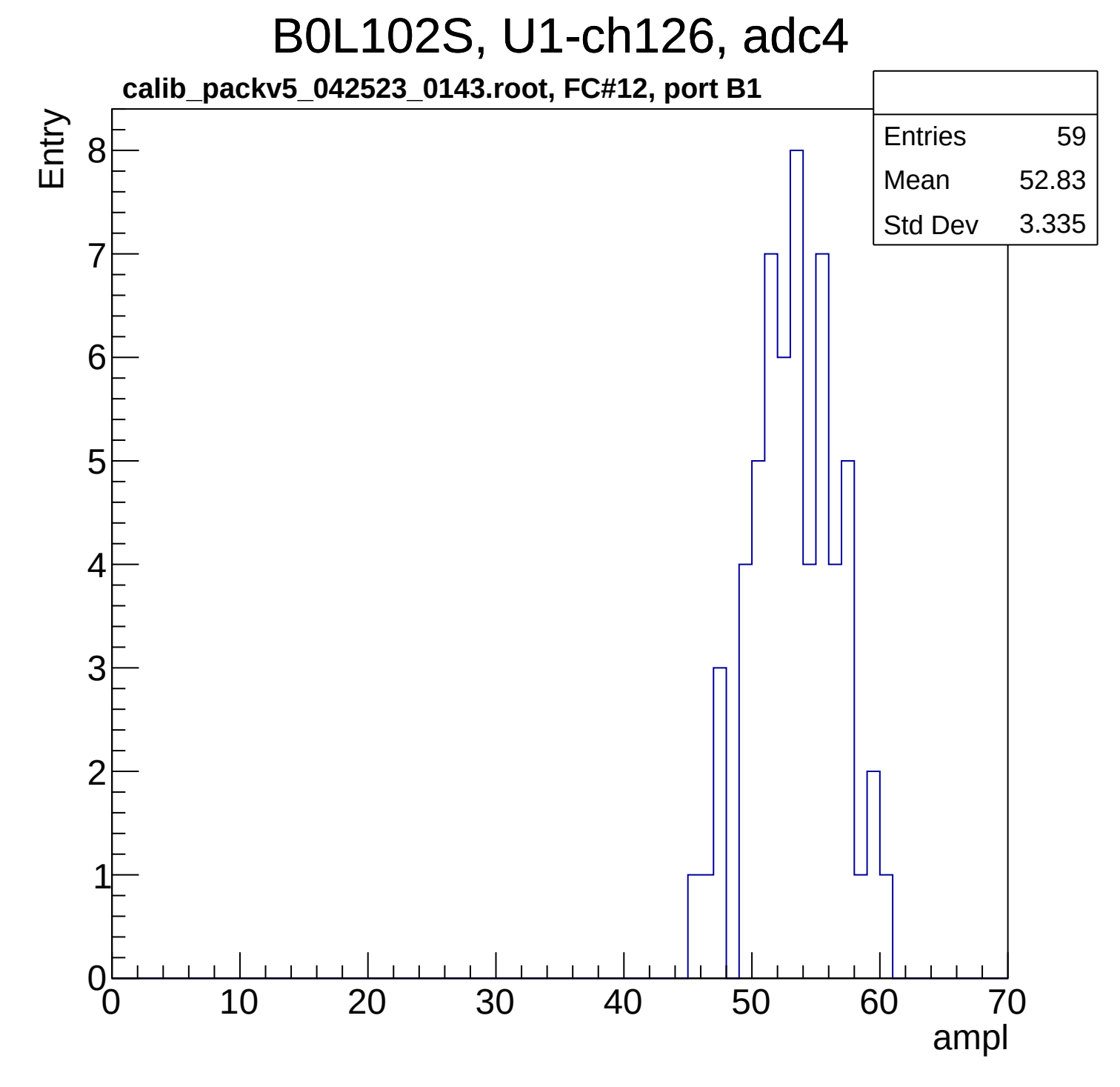

B0L102S, U1-ch125, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch126, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 26.58
Std Dev 5.636
Gaus mean : 26.7474
Gaus Width: 5.4589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch126, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 33.7
Std Dev 3.672
Gaus mean : 33.9560
Gaus Width: 4.1244
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch126, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 41.9
Std Dev 3.832
Gaus mean : 42.3204
Gaus Width: 4.0117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U1-ch126, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 47.15
Std Dev 3.854
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch126, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 52.83
Std Dev 3.335
Entry
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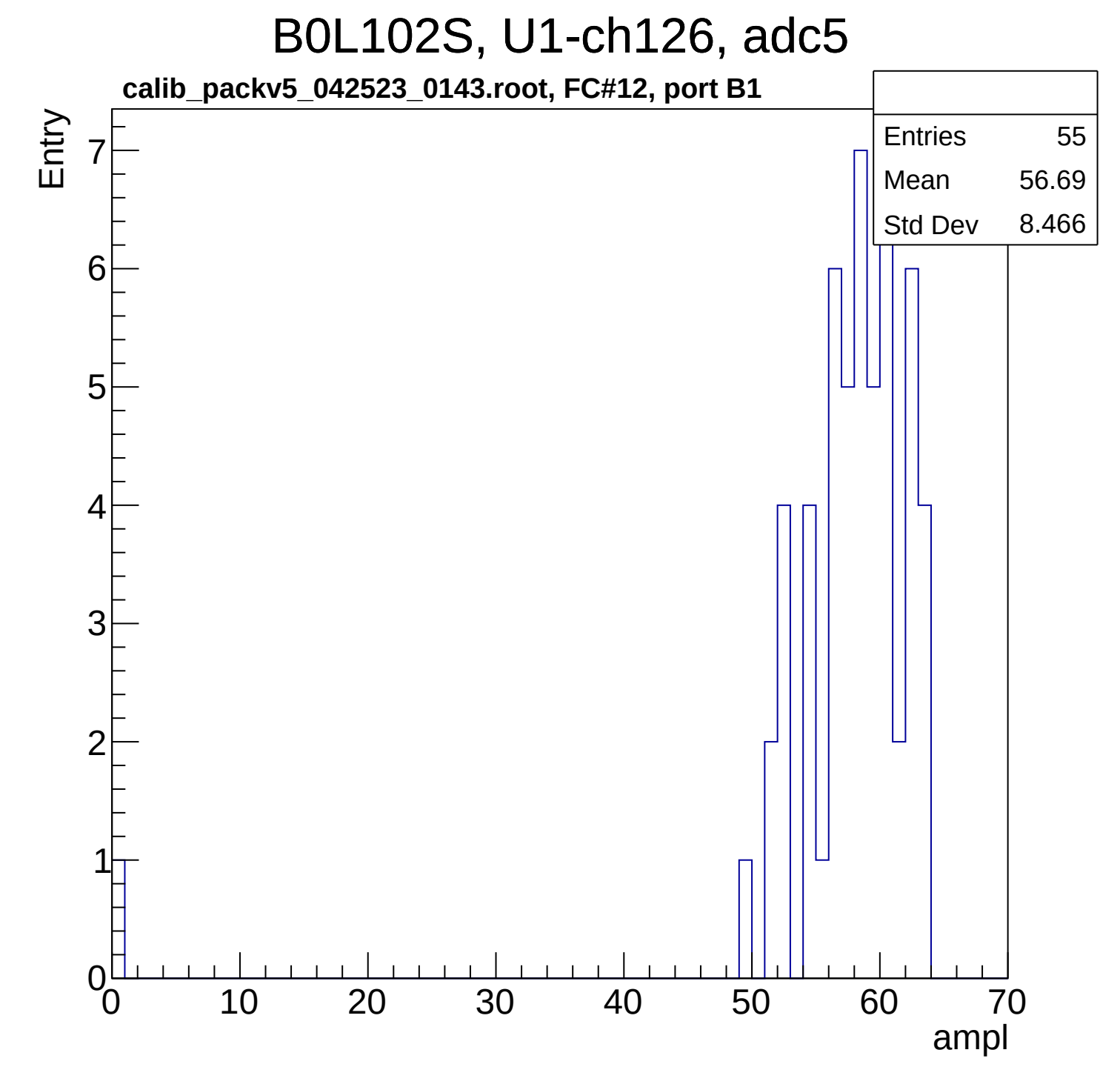
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch126, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.69
Std Dev 8.466
Entry
ampl
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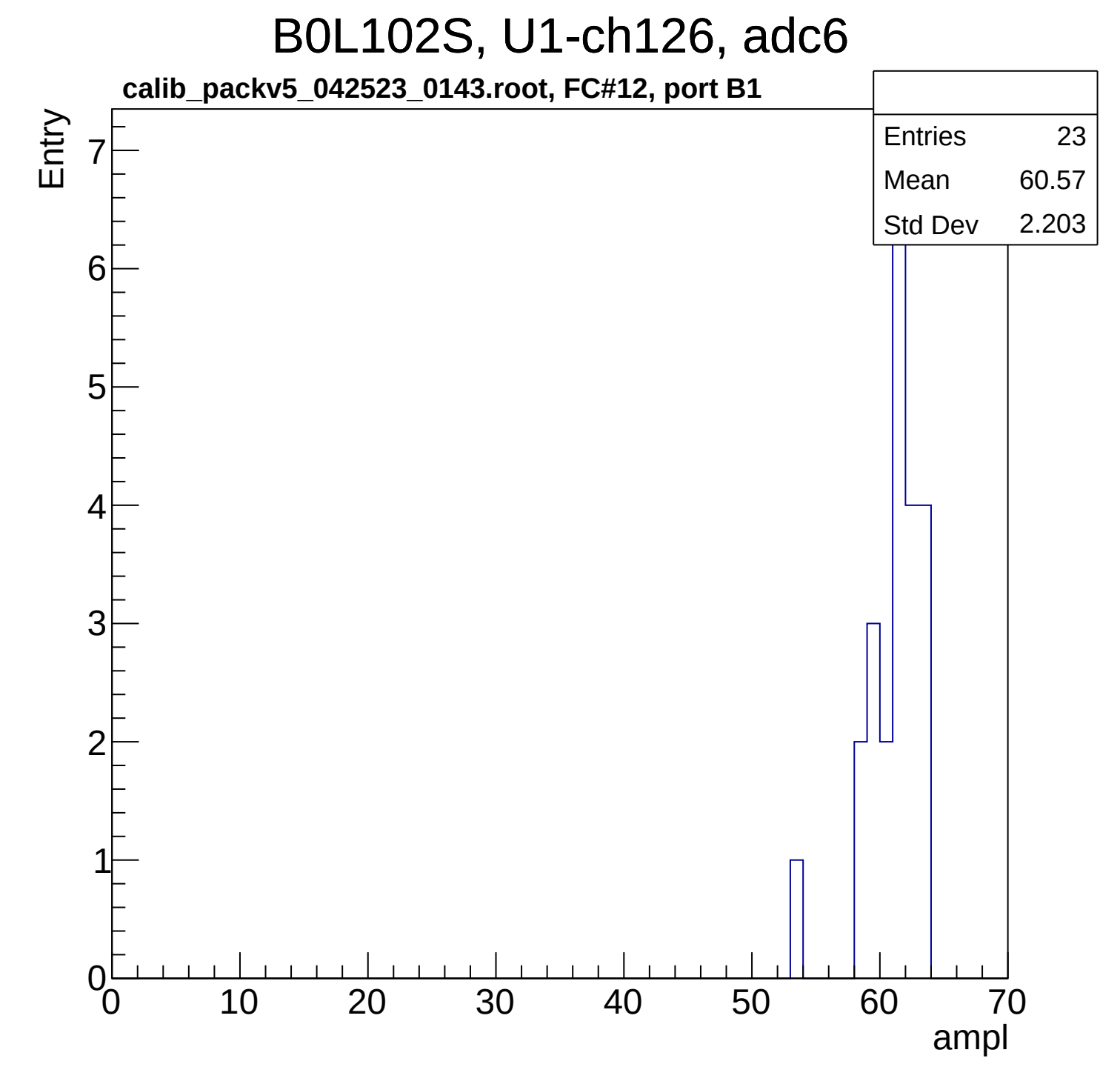
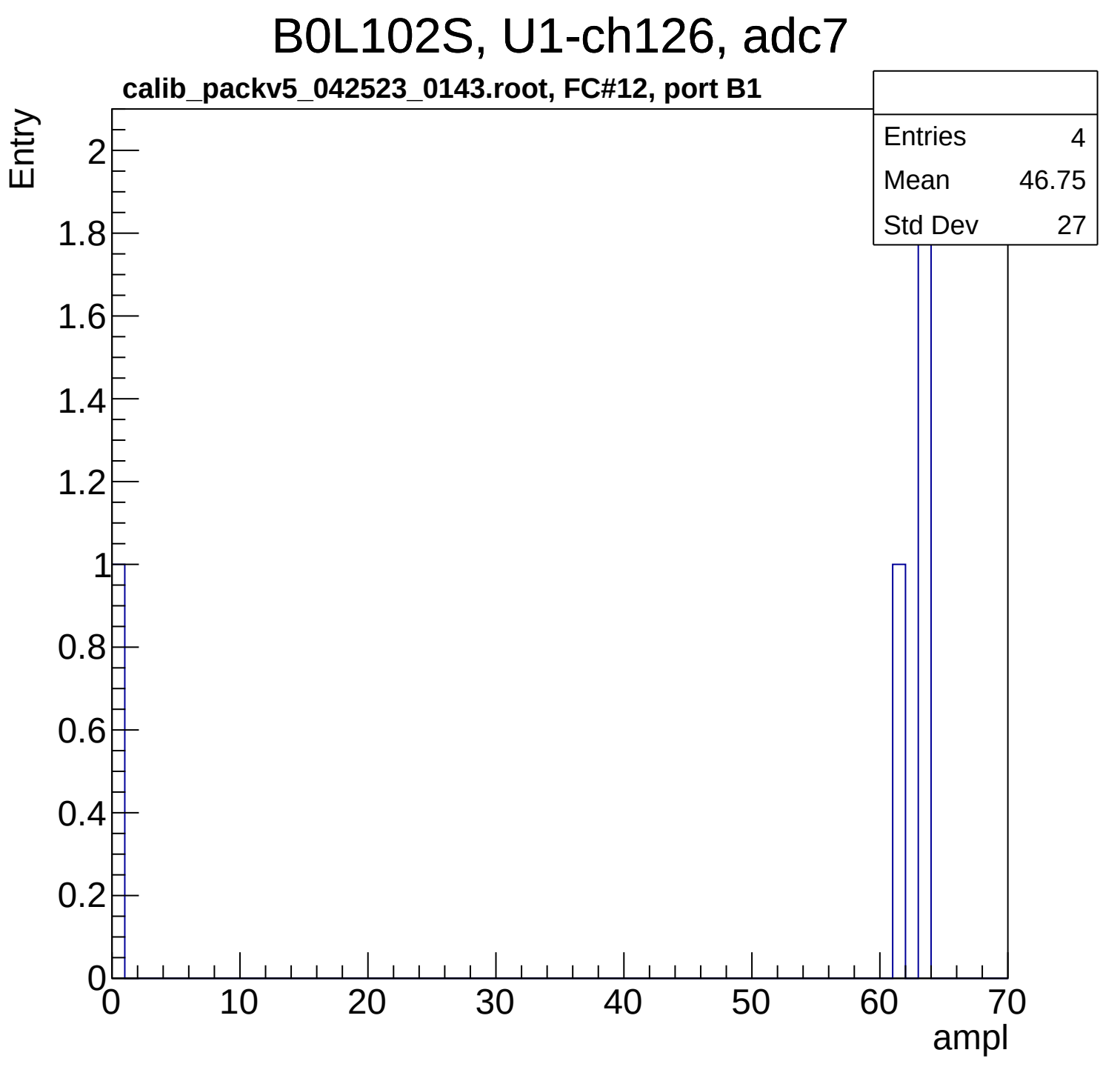
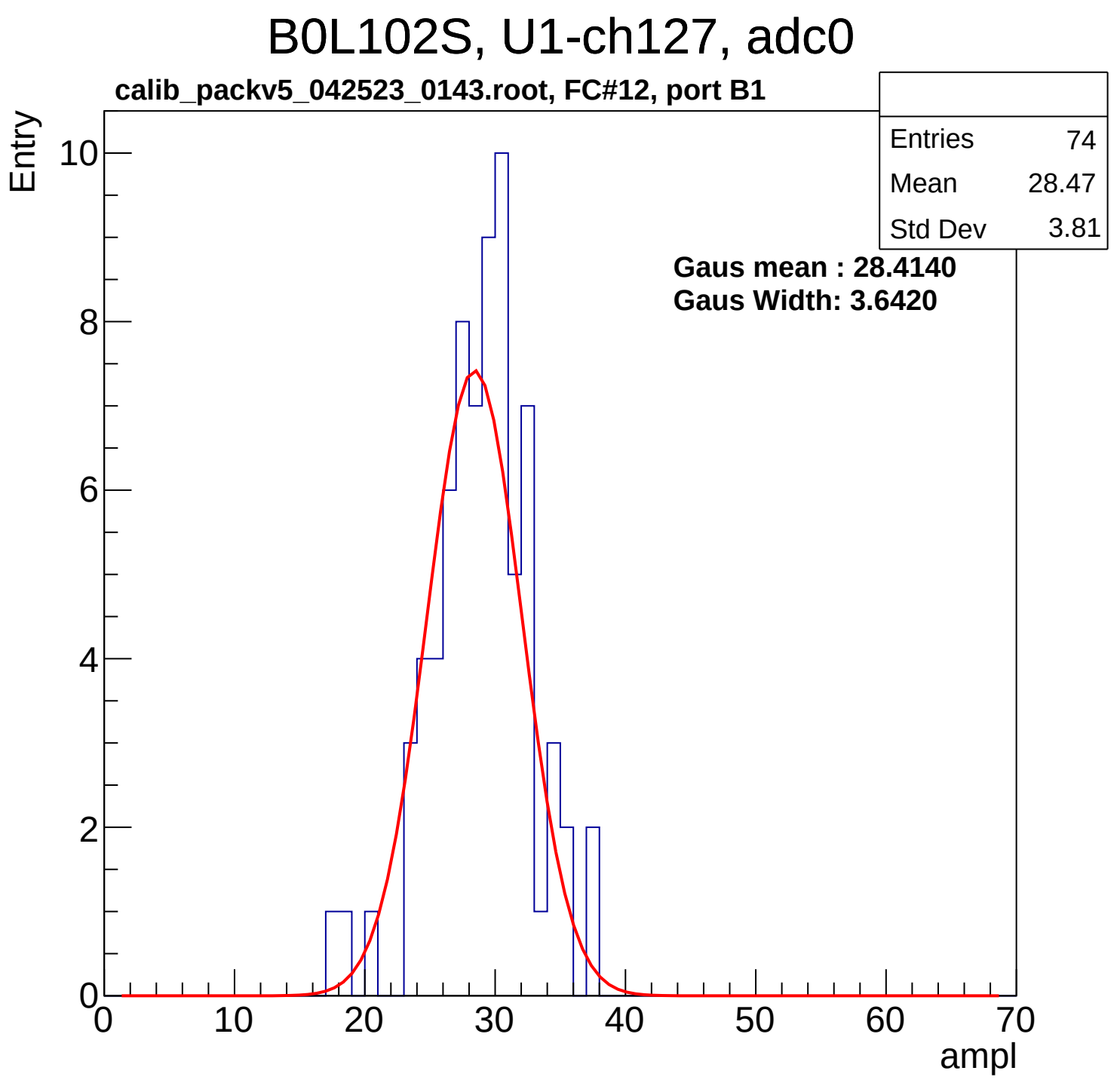
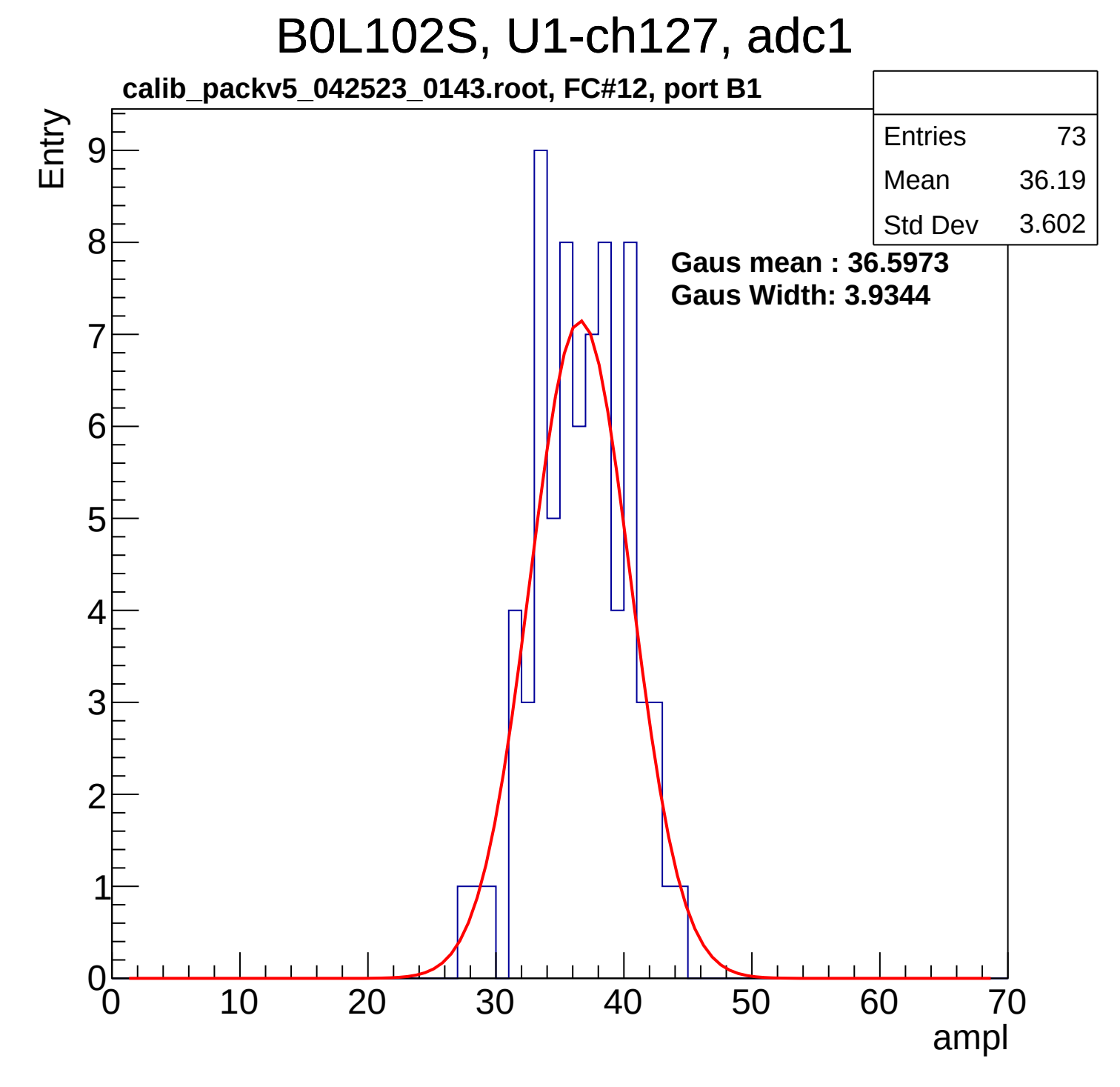
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch126, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 23
Mean 60.57
Std Dev 2.203
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U1-ch126, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U1-ch127, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 28.47
Std Dev 3.81
Gaus mean : 28.4140
Gaus Width: 3.6420
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U1-ch127, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 36.19
Std Dev 3.602
Gaus mean : 36.5973
Gaus Width: 3.9344
Entry
ampl
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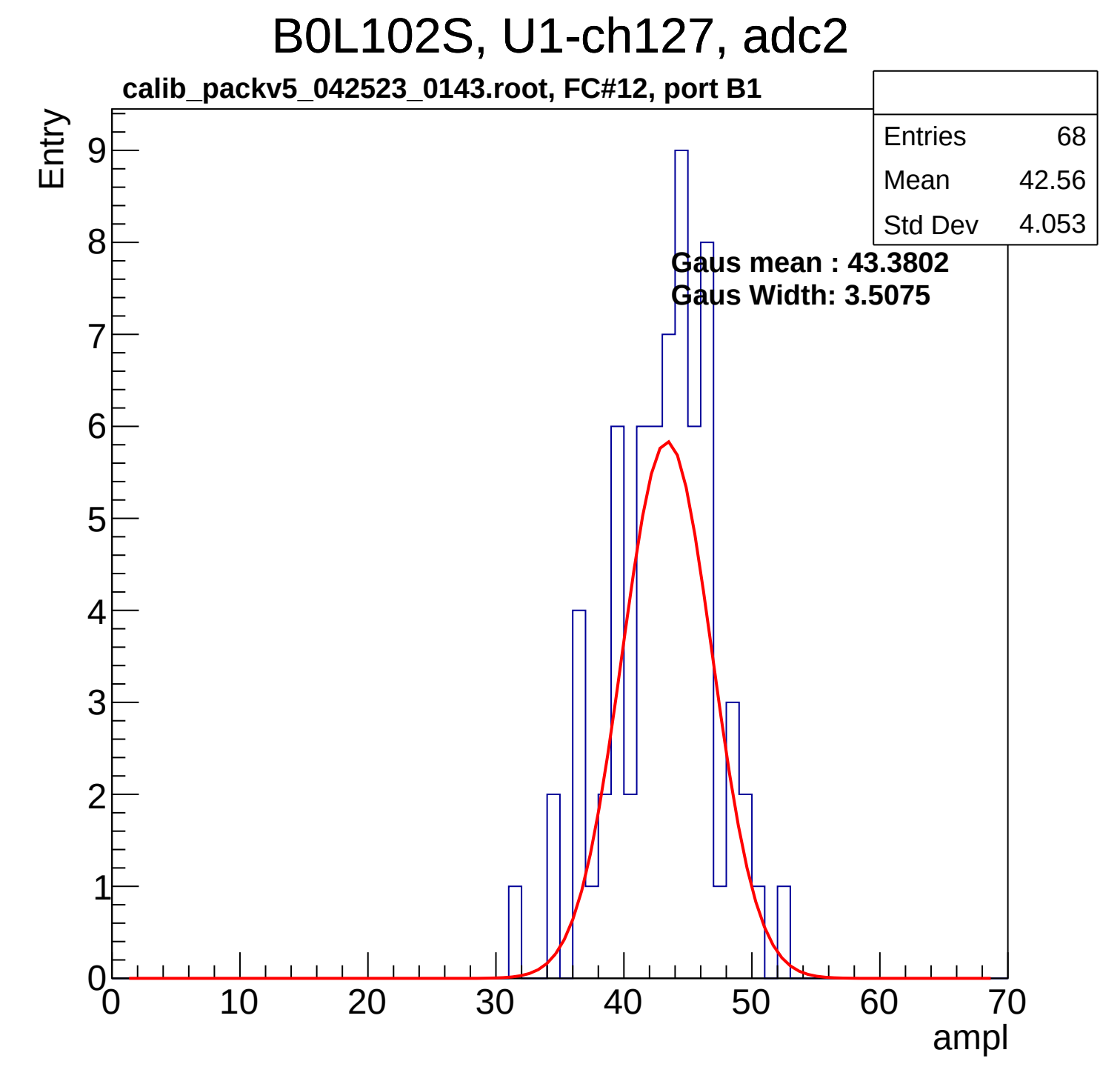
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U1-ch127, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 42.56
Std Dev 4.053
Gaus mean : 43.3802
Gaus Width: 3.5075
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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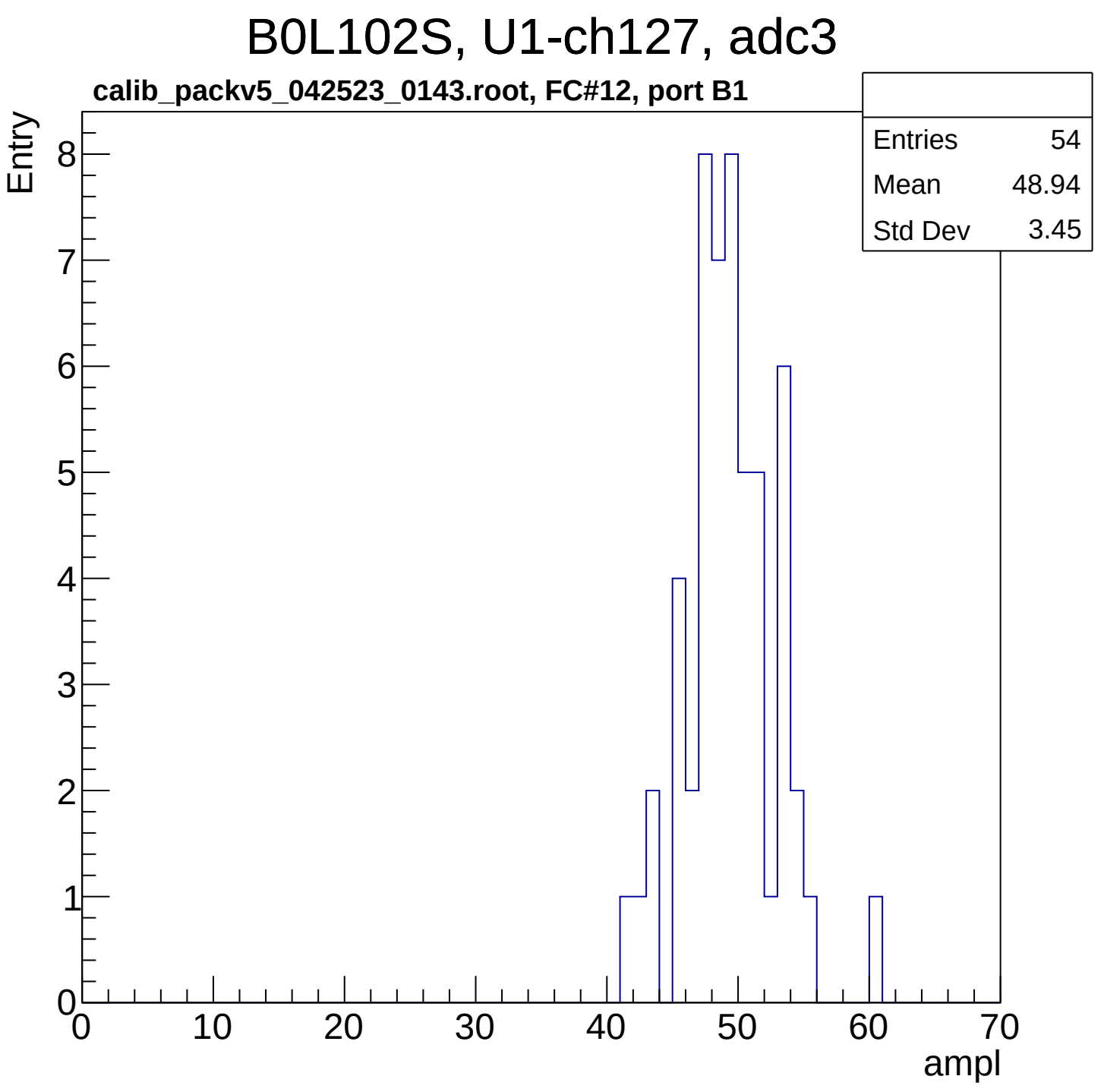
60
70


B0L102S, U1-ch127, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 48.94
Std Dev 3.45
Entry
ampl
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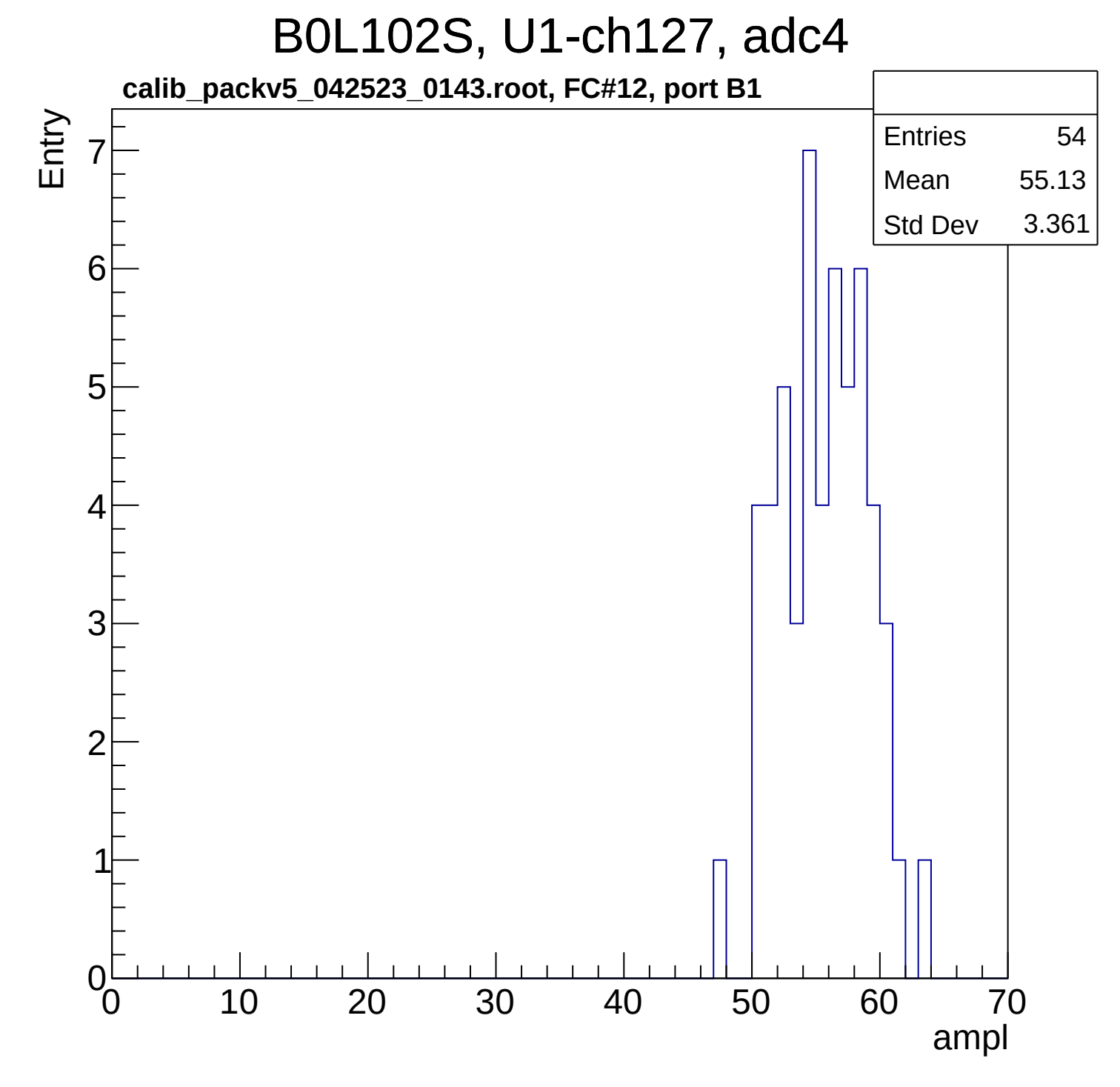
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U1-ch127, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 55.13
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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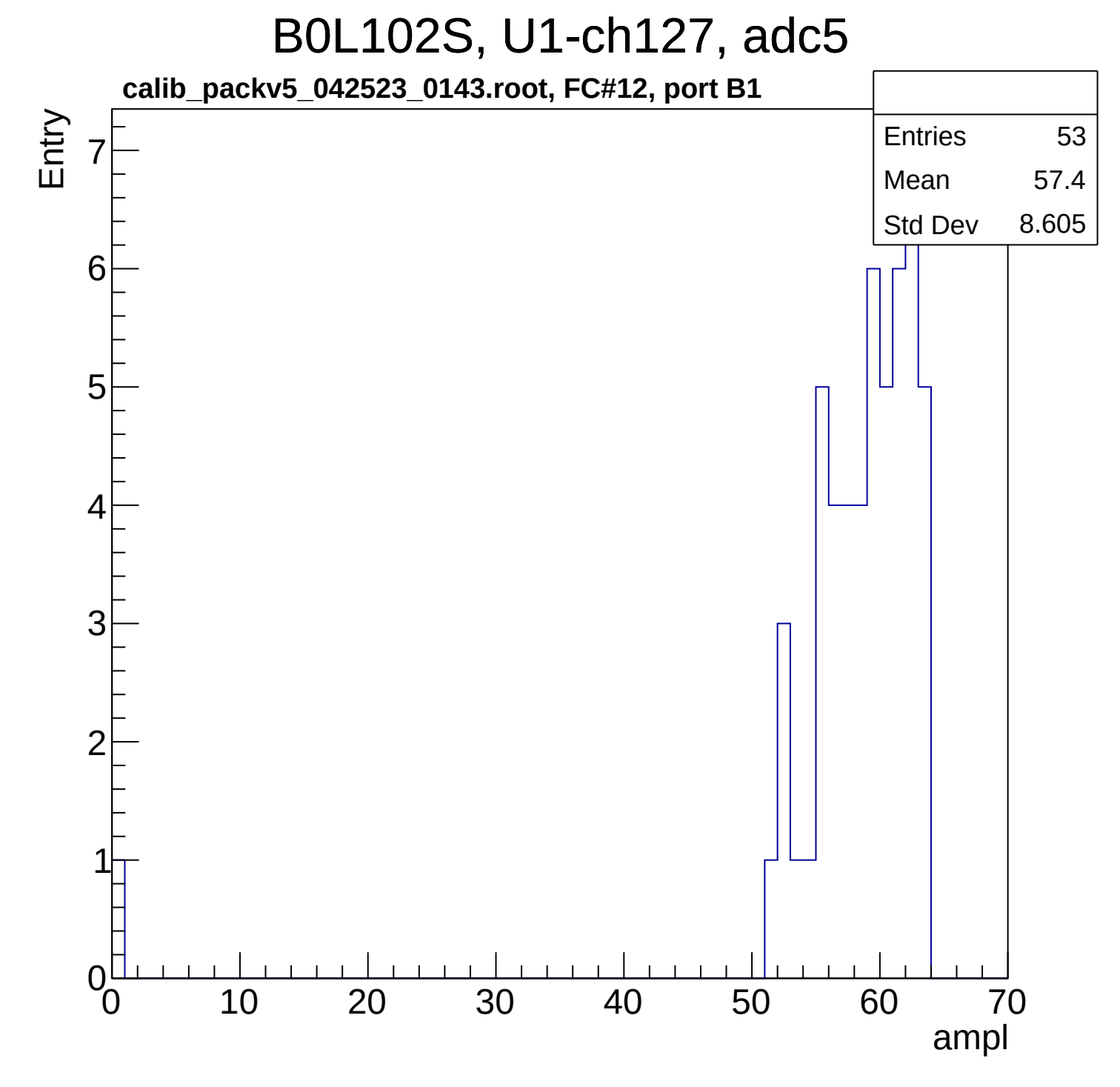
50
60
70


B0L102S, U1-ch127, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 57.4
Std Dev 8.605
Entry
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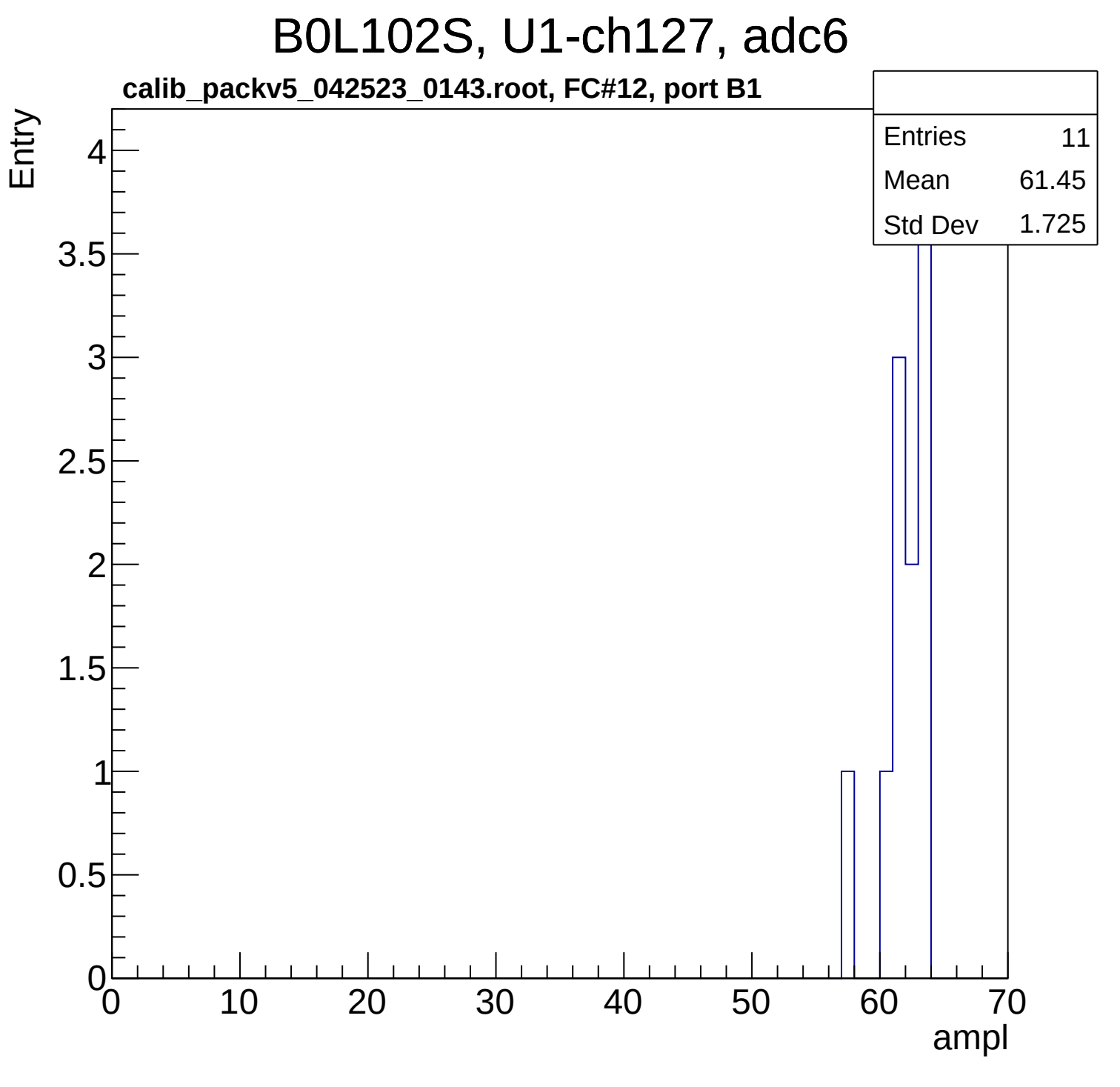
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U1-ch127, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61.45
Std Dev 1.725
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
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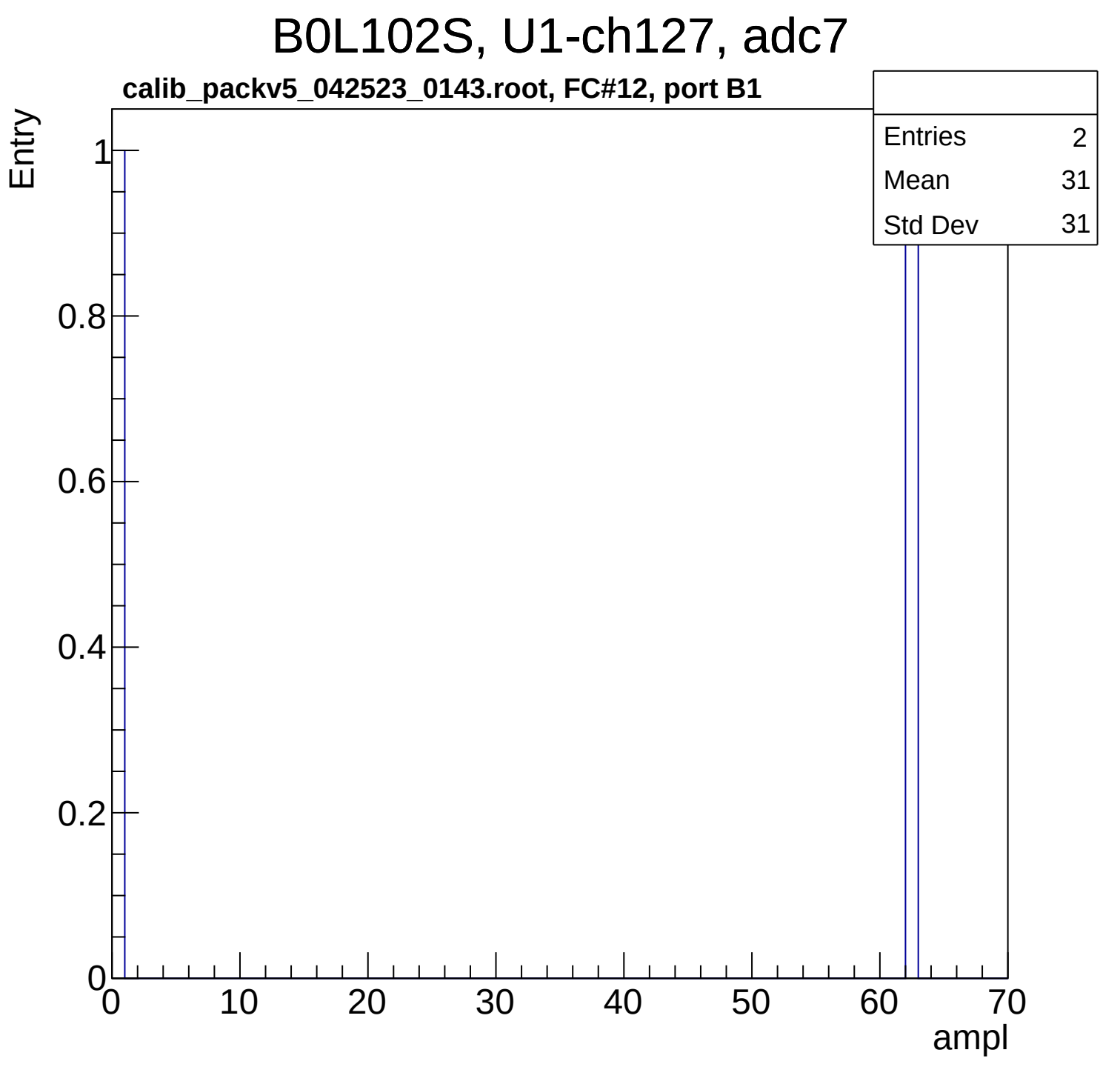
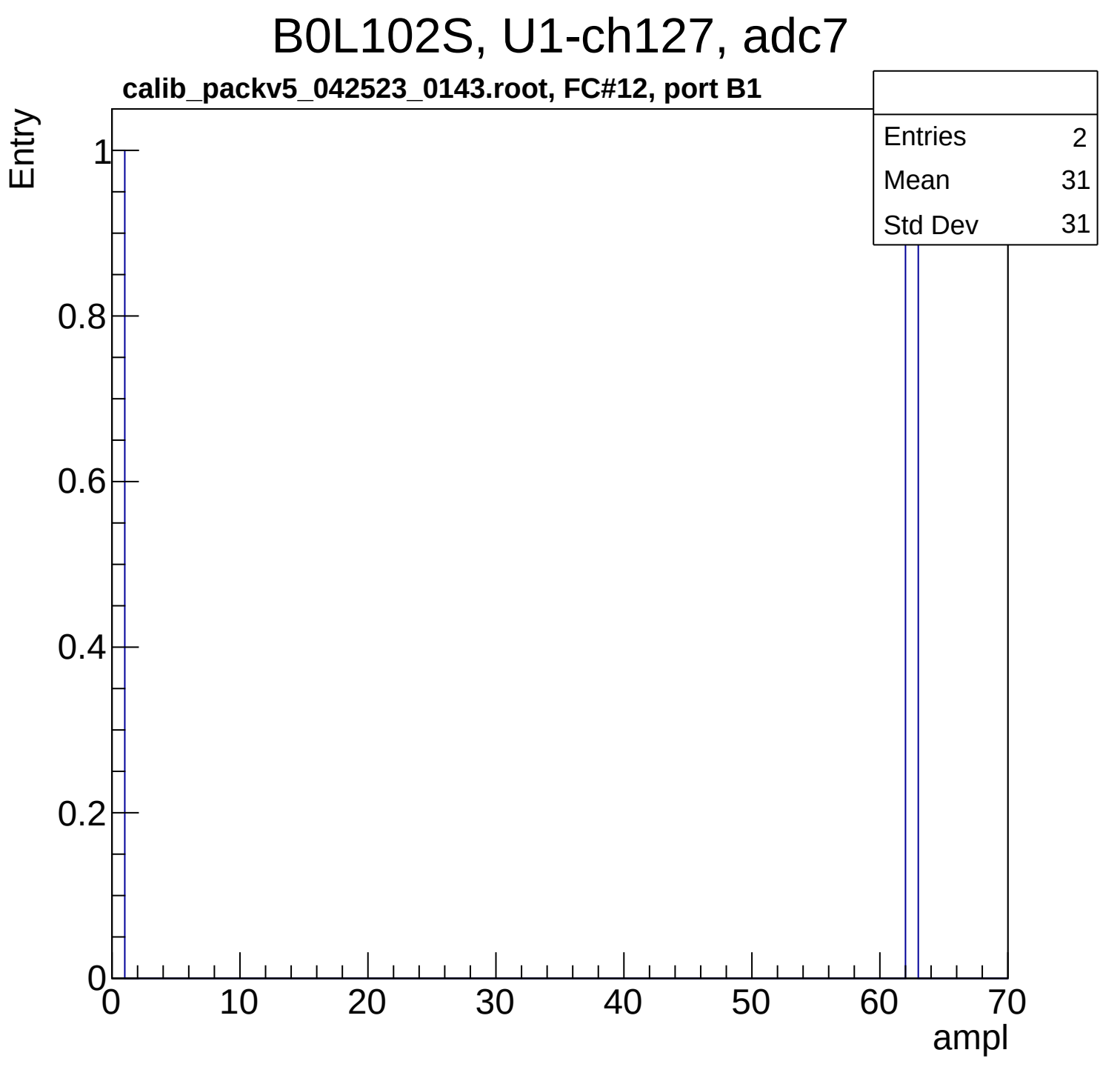
40
50
60
70


B0L102S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl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