

B1L000S, U13-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.5730

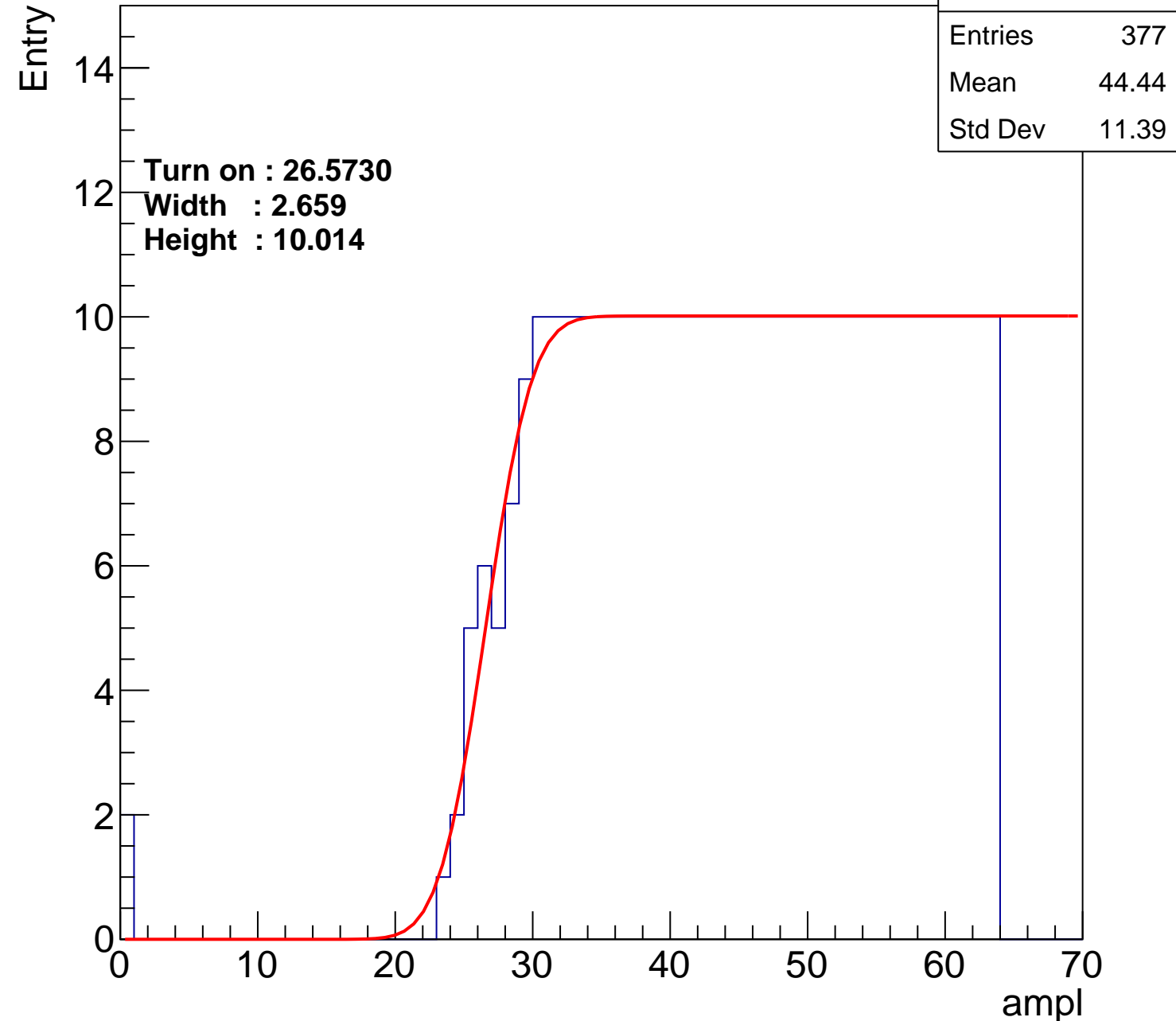
Width : 2.659

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.37
Std Dev	11.46

Turn on : 26.7613

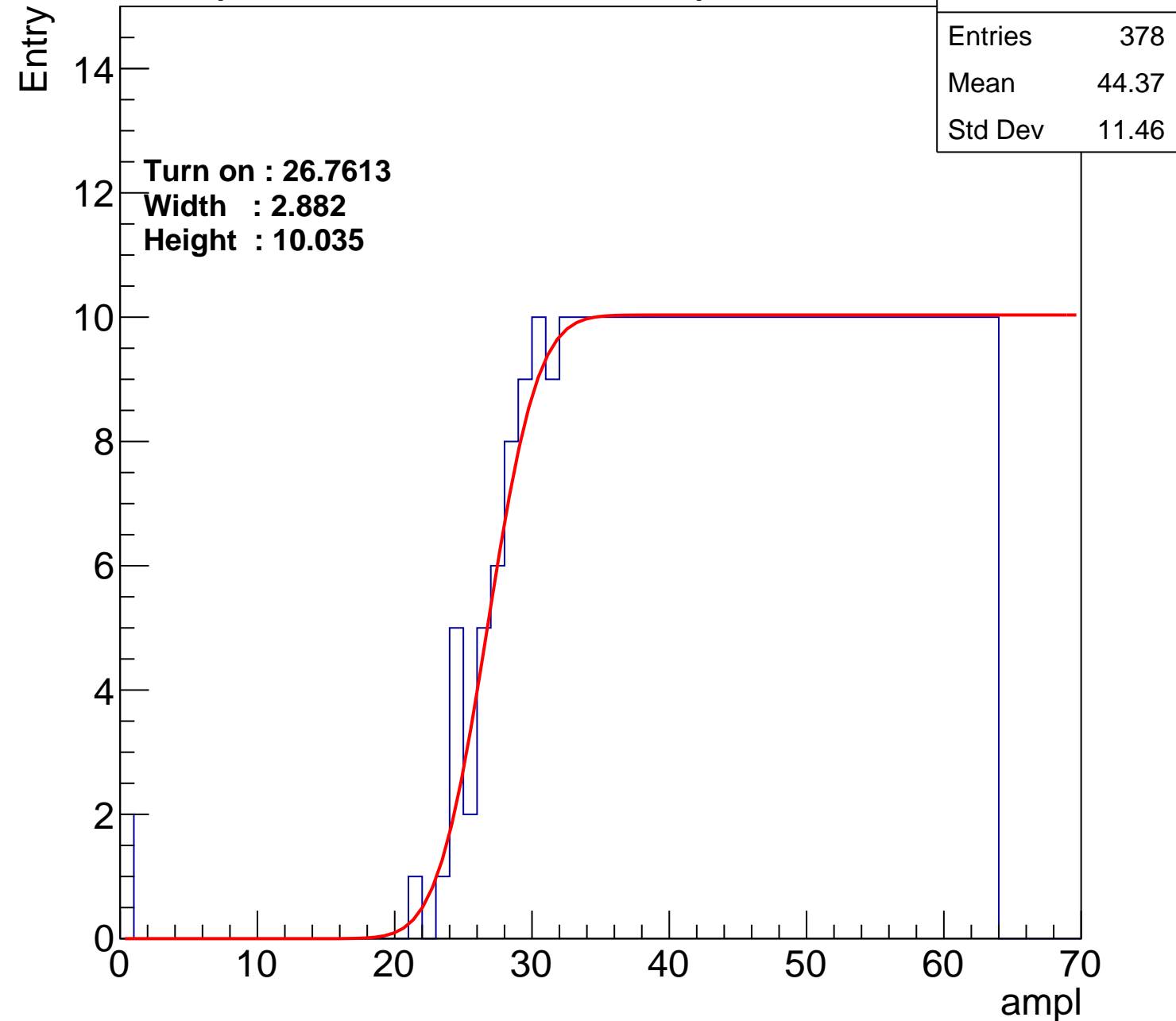
Width : 2.882

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch2

calib_packv5_042523_0143.root, FC#3, port B3

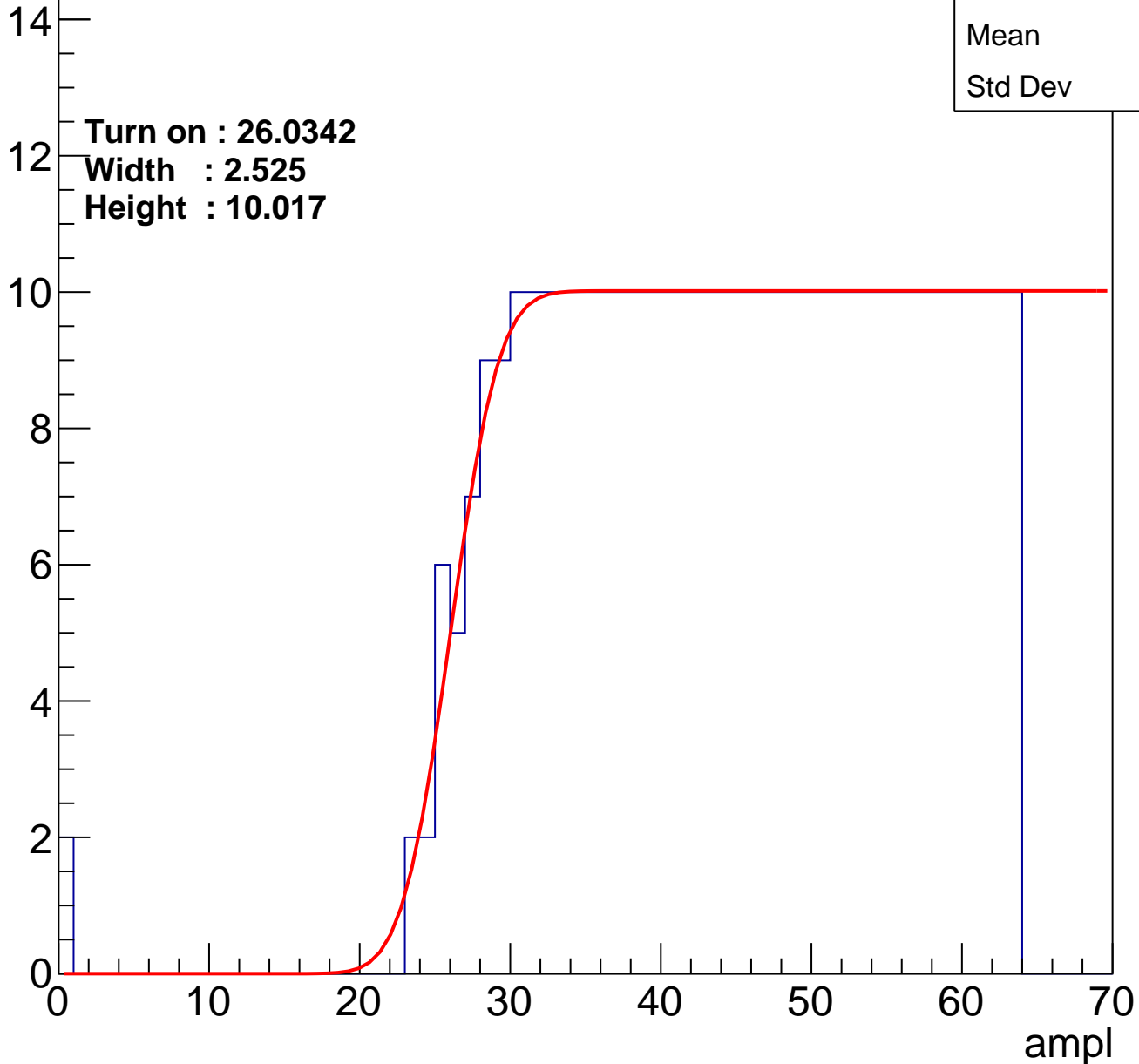
Entries	382
Mean	44.2
Std Dev	11.5

Turn on : 26.0342

Width : 2.525

Height : 10.017

Entry



B1L000S, U13-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	359
Mean	45.4
Std Dev	10.71

Turn on : 28.2222

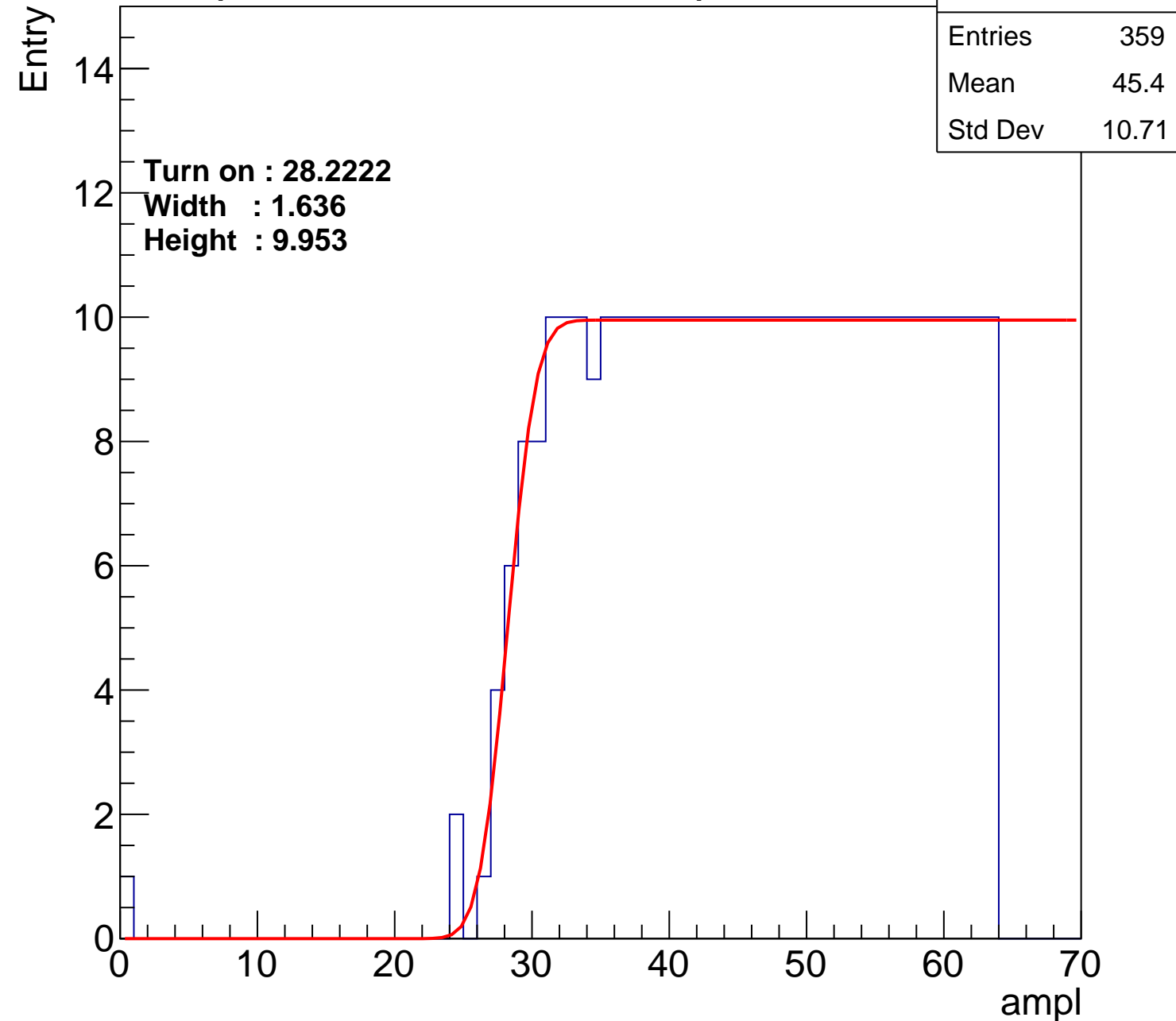
Width : 1.636

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.63
Std Dev	11.78

Turn on : 24.5766

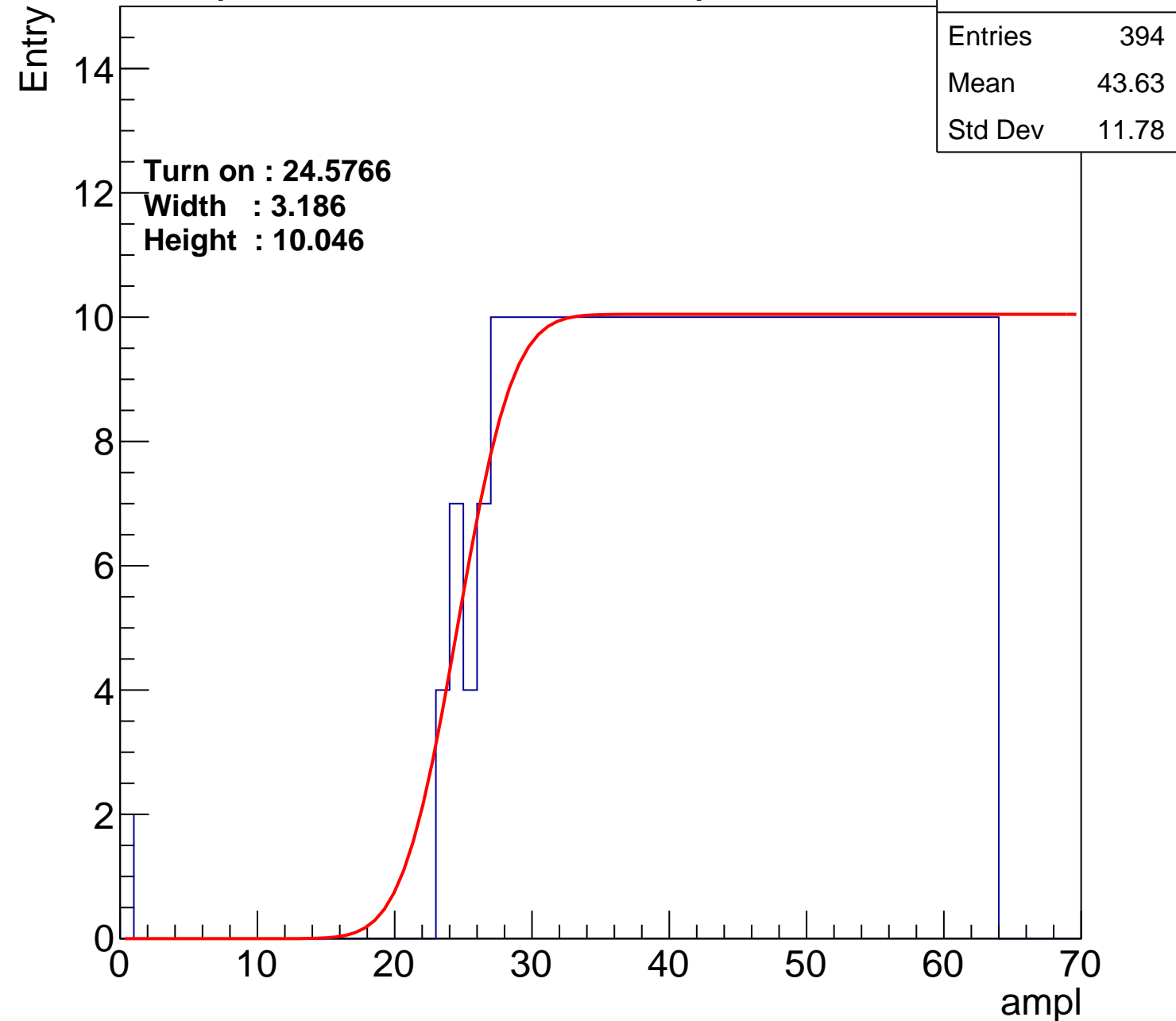
Width : 3.186

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.19
Std Dev	10.84

Turn on : 27.9638

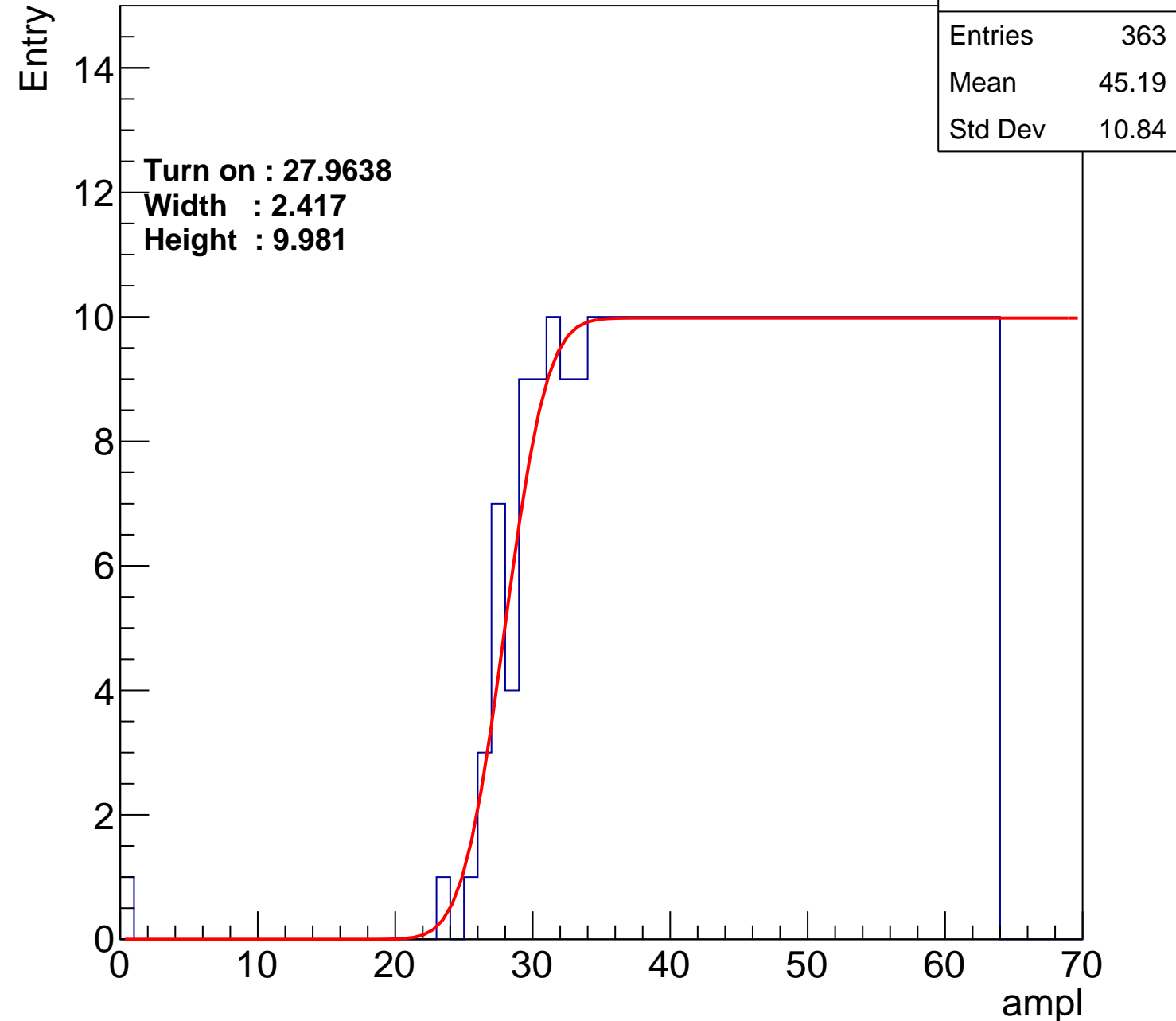
Width : 2.417

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.76
Std Dev	11.25

Turn on : 27.3041

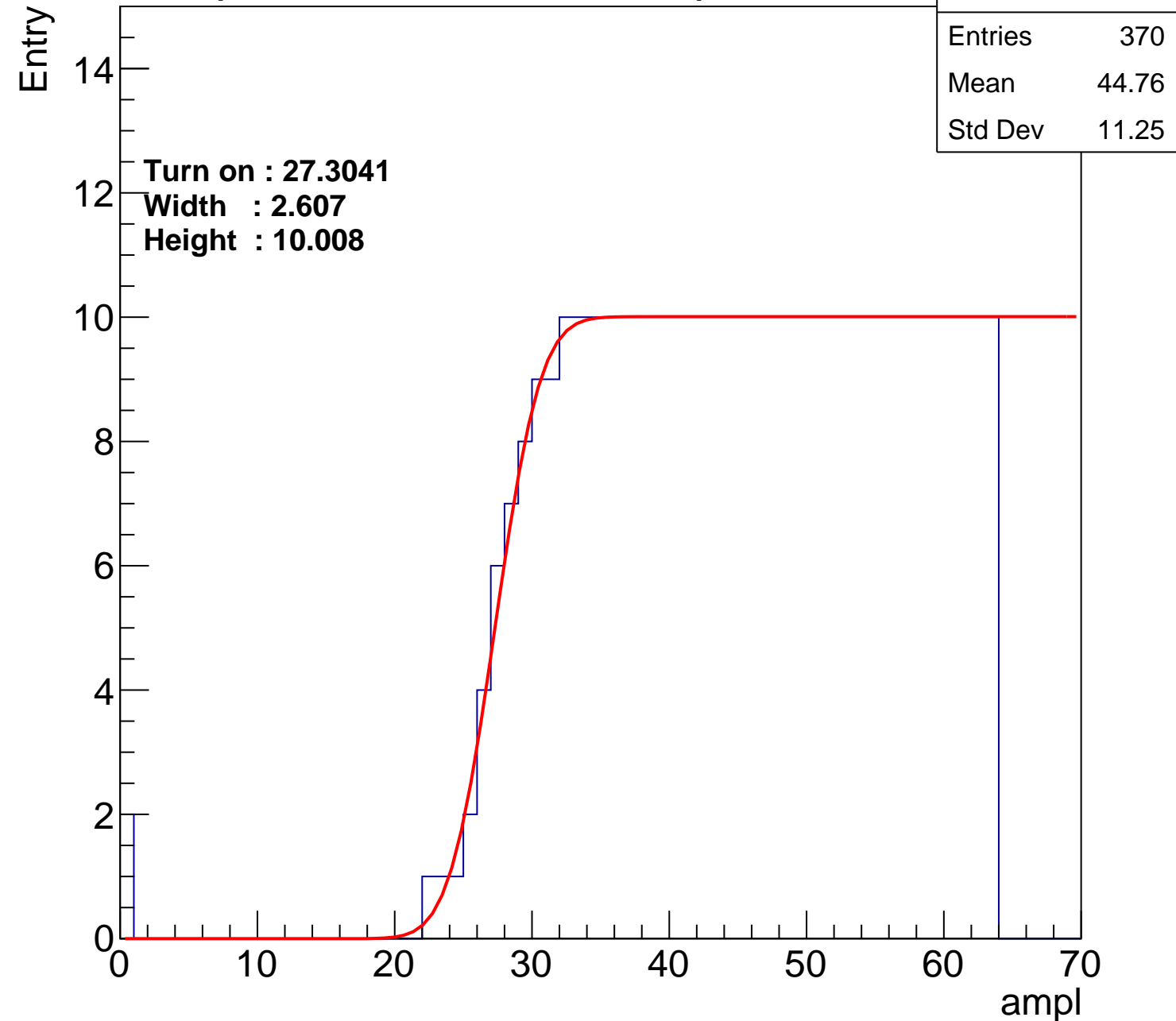
Width : 2.607

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.1341

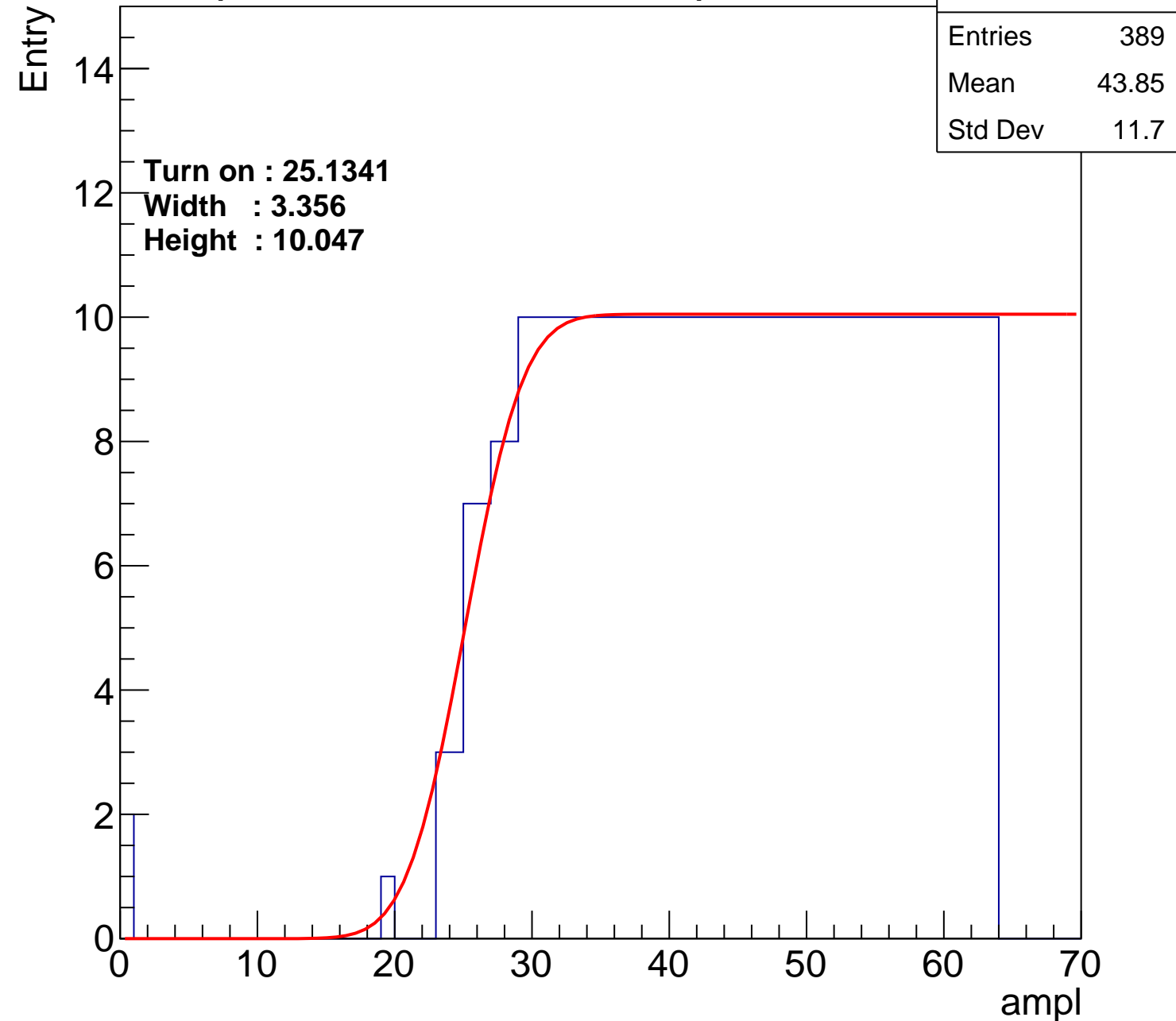
Width : 3.356

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 25.8897

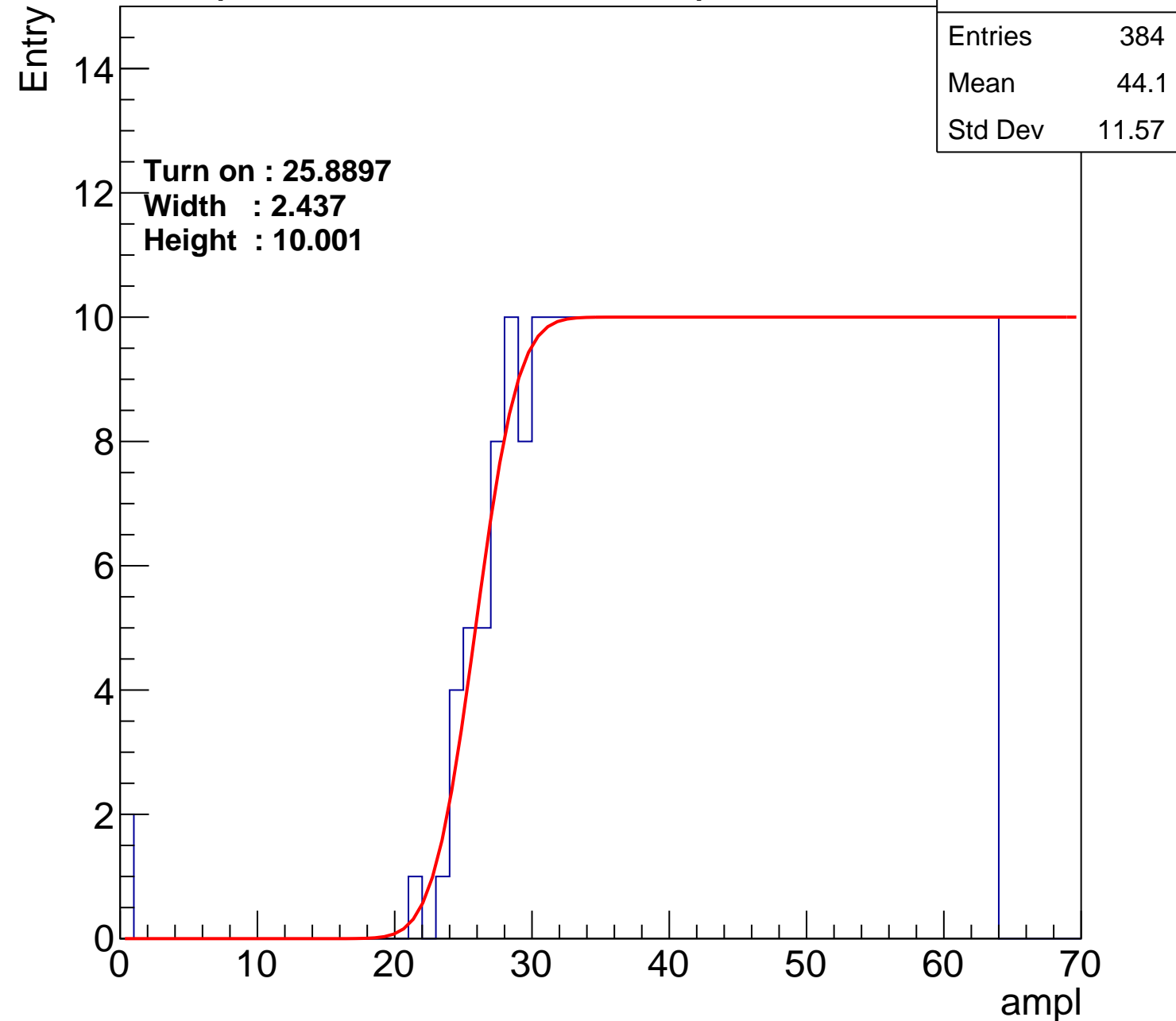
Width : 2.437

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.37
Std Dev	11.4

Turn on : 26.2966

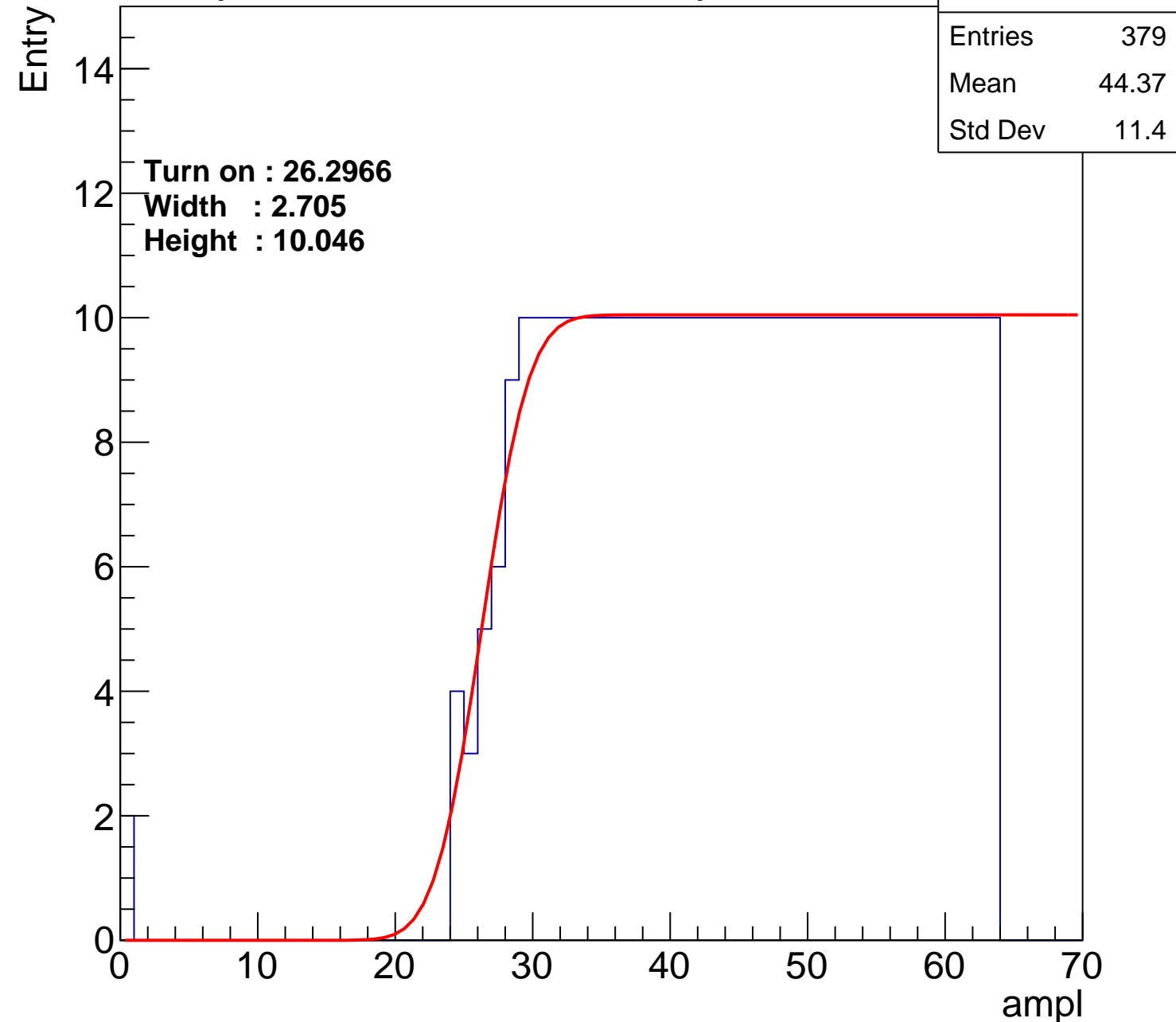
Width : 2.705

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.5719

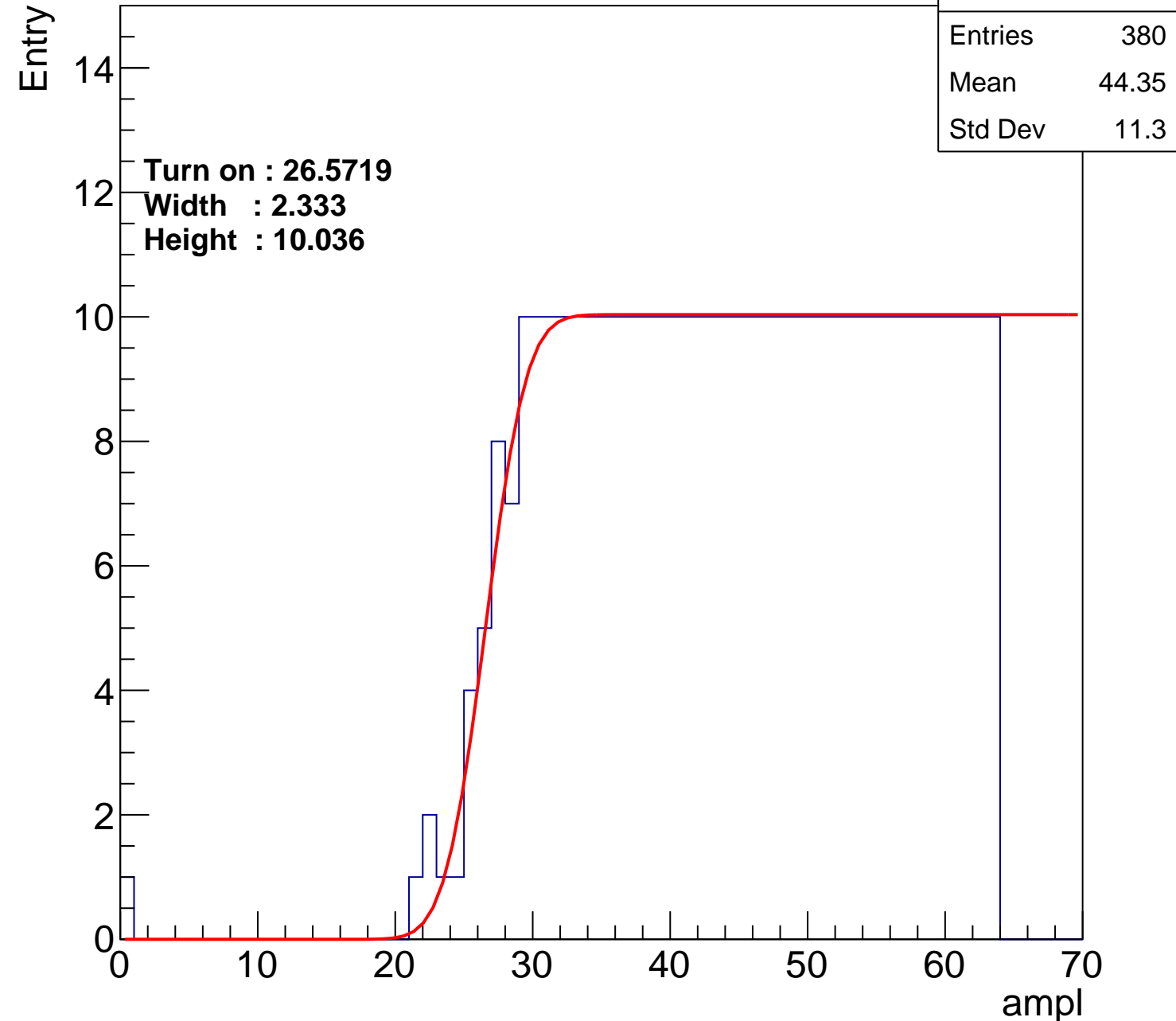
Width : 2.333

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch11

calib_packv5_042523_0143.root, FC#3, port B3

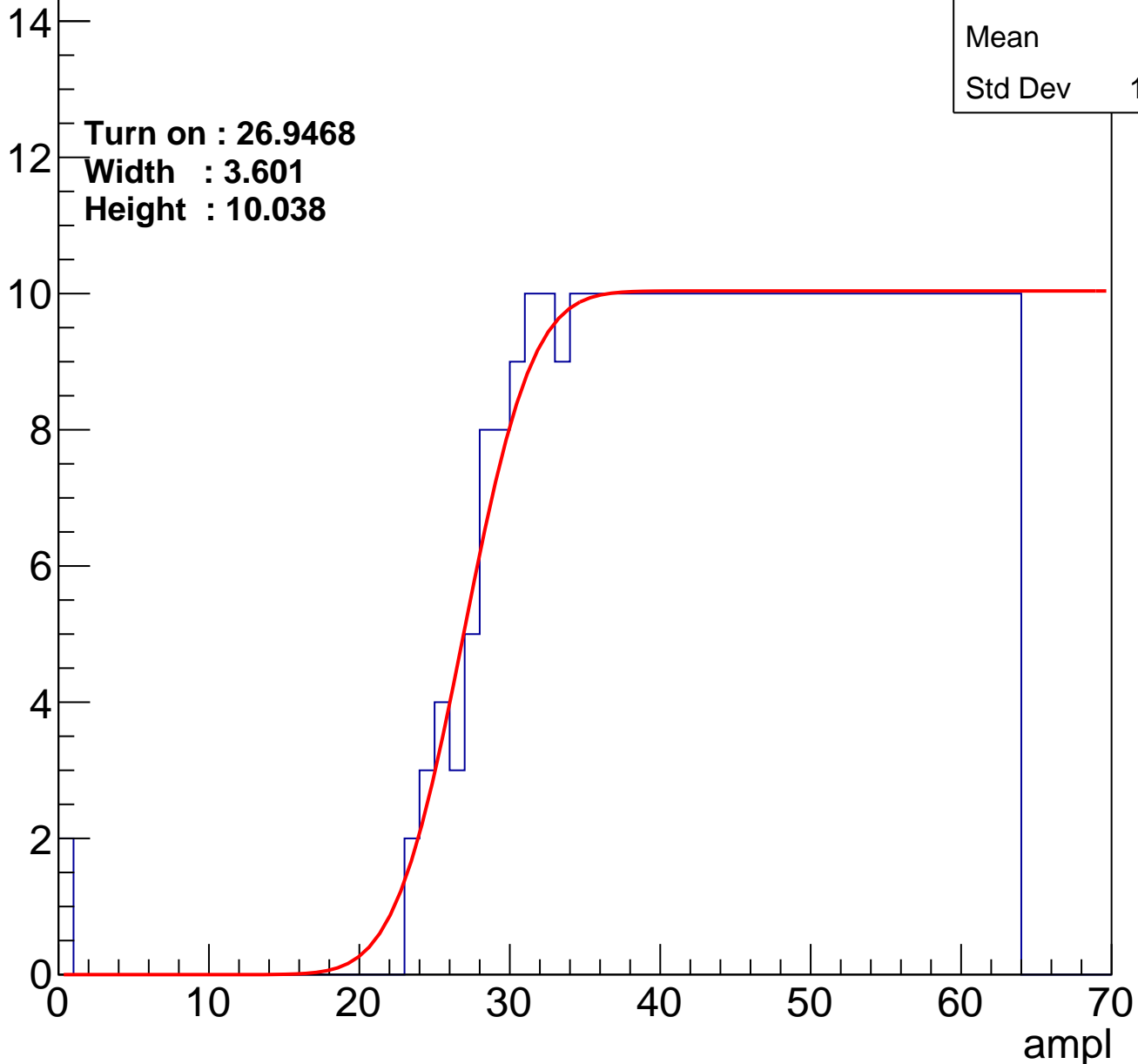
Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 26.9468

Width : 3.601

Height : 10.038

Entry



B1L000S, U13-ch12

calib_packv5_042523_0143.root, FC#3, port B3

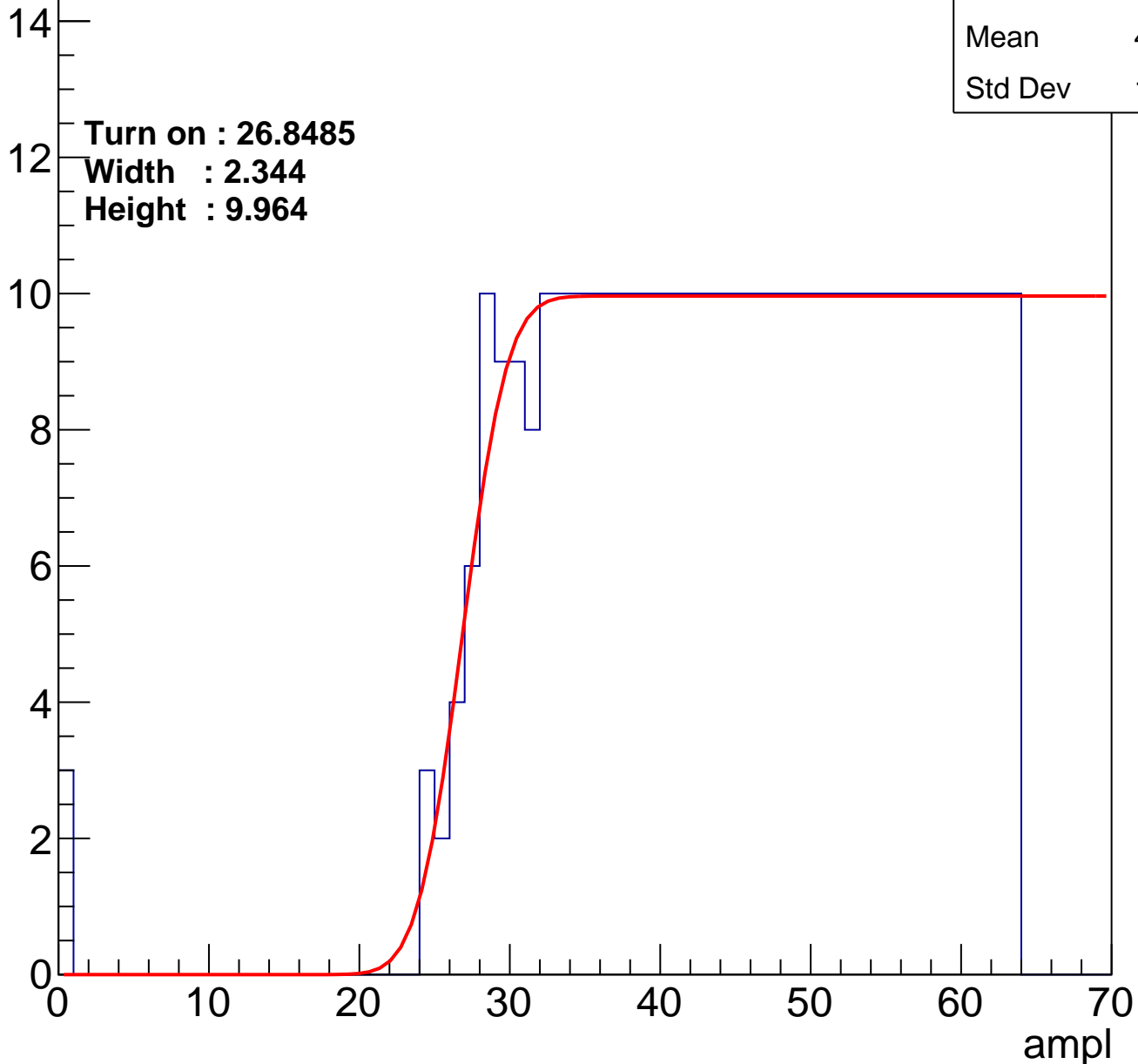
Entries	374
Mean	44.51
Std Dev	11.51

Turn on : 26.8485

Width : 2.344

Height : 9.964

Entry



B1L000S, U13-ch13

calib_packv5_042523_0143.root, FC#3, port B3

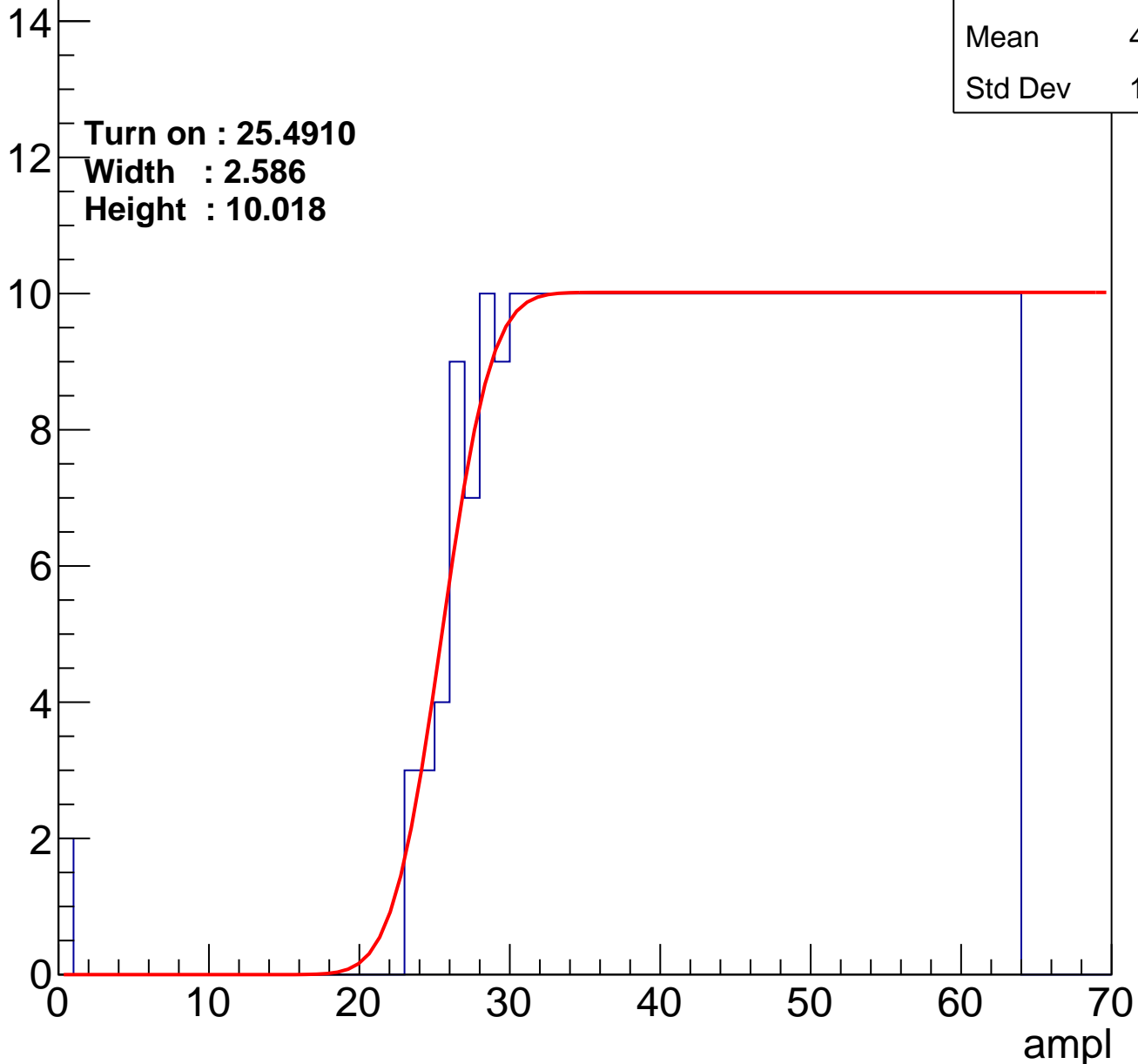
Entries	387
Mean	43.97
Std Dev	11.62

Turn on : 25.4910

Width : 2.586

Height : 10.018

Entry



B1L000S, U13-ch14

calib_packv5_042523_0143.root, FC#3, port B3

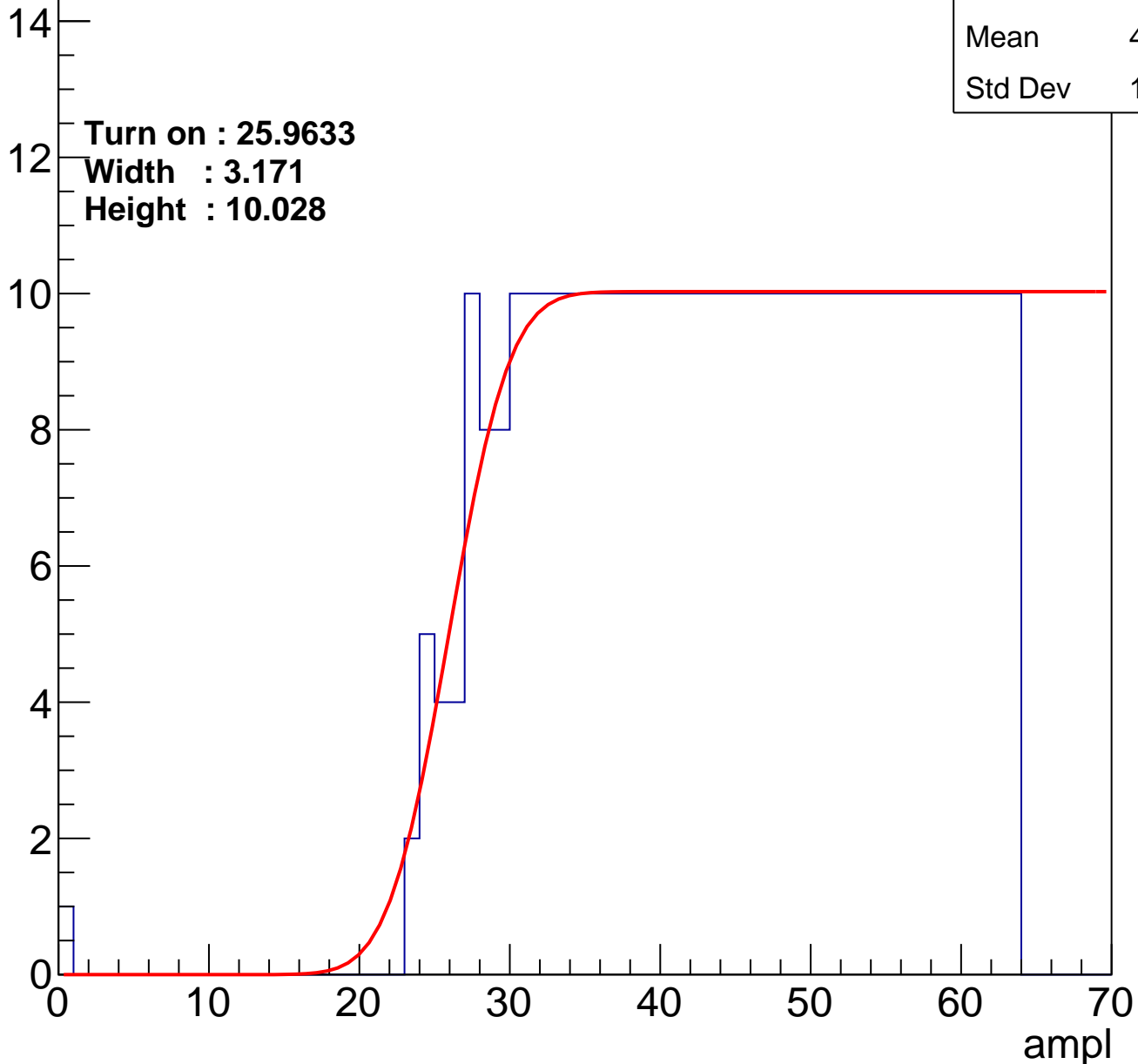
Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 25.9633

Width : 3.171

Height : 10.028

Entry



B1L000S, U13-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.57
Std Dev	11.33

Turn on : 26.9355

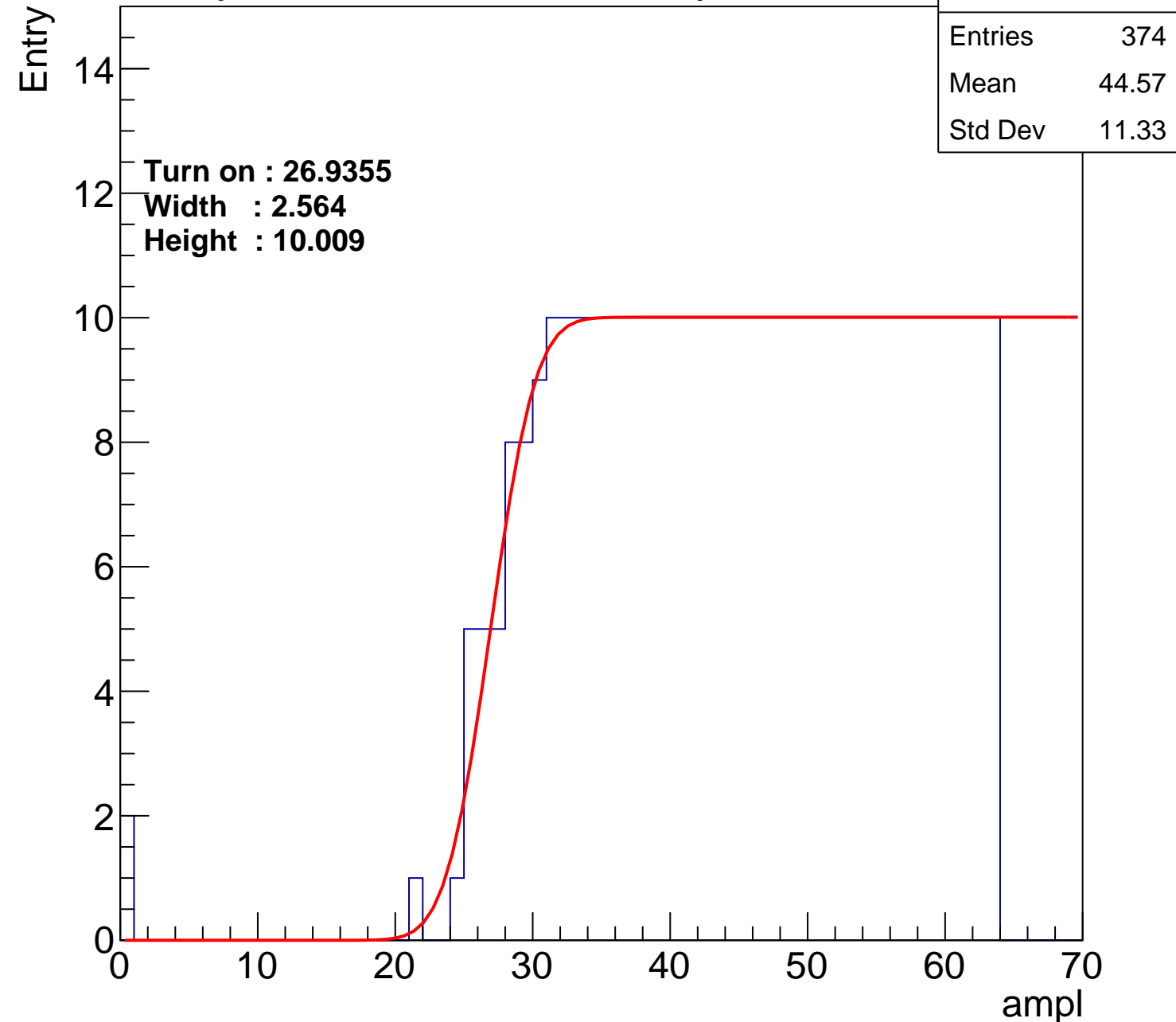
Width : 2.564

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.69
Std Dev	11.63

Turn on : 27.3254

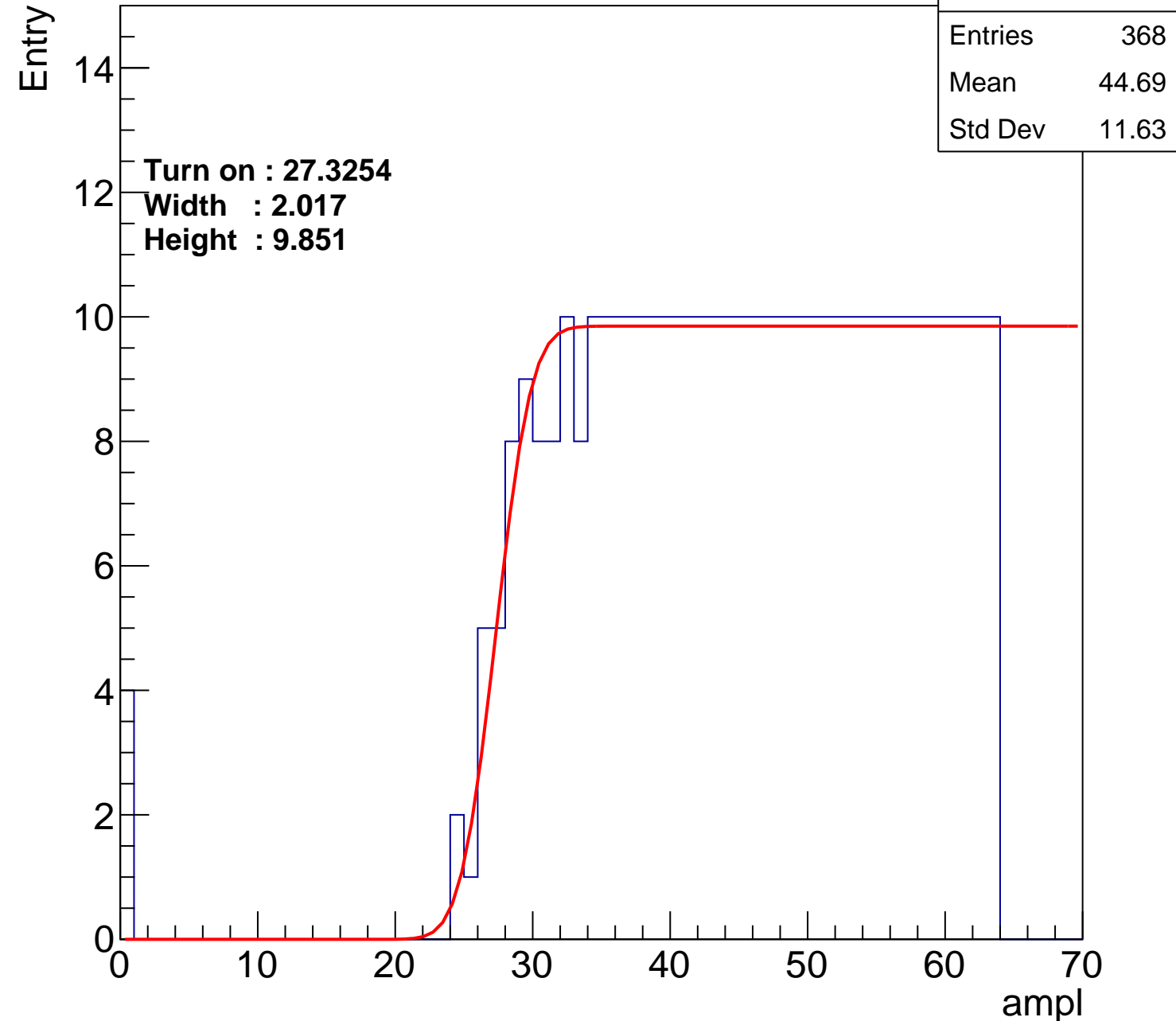
Width : 2.017

Height : 9.851

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch17

calib_packv5_042523_0143.root, FC#3, port B3

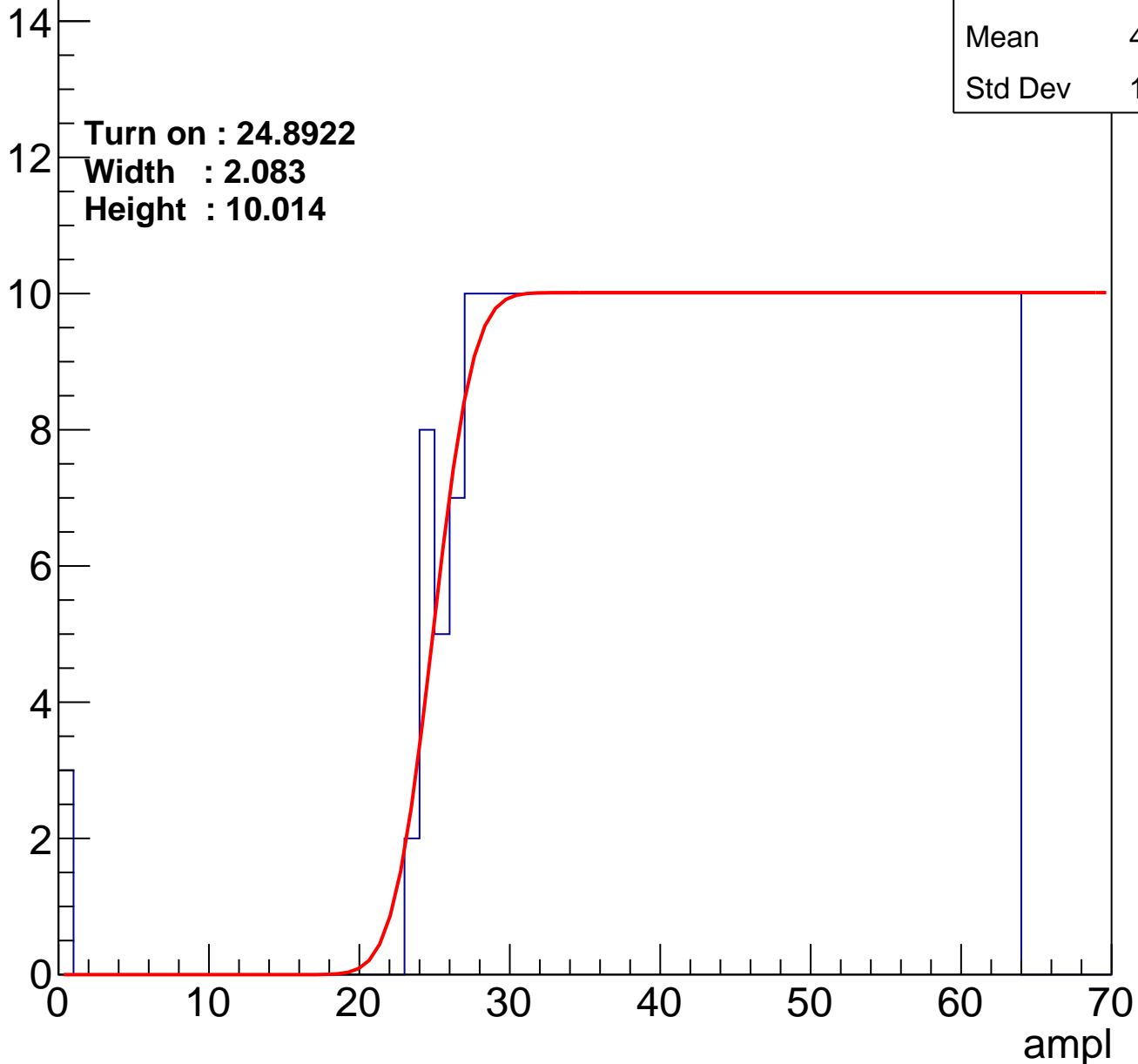
Entries	395
Mean	43.53
Std Dev	11.95

Turn on : 24.8922

Width : 2.083

Height : 10.014

Entry



B1L000S, U13-ch18

calib_packv5_042523_0143.root, FC#3, port B3

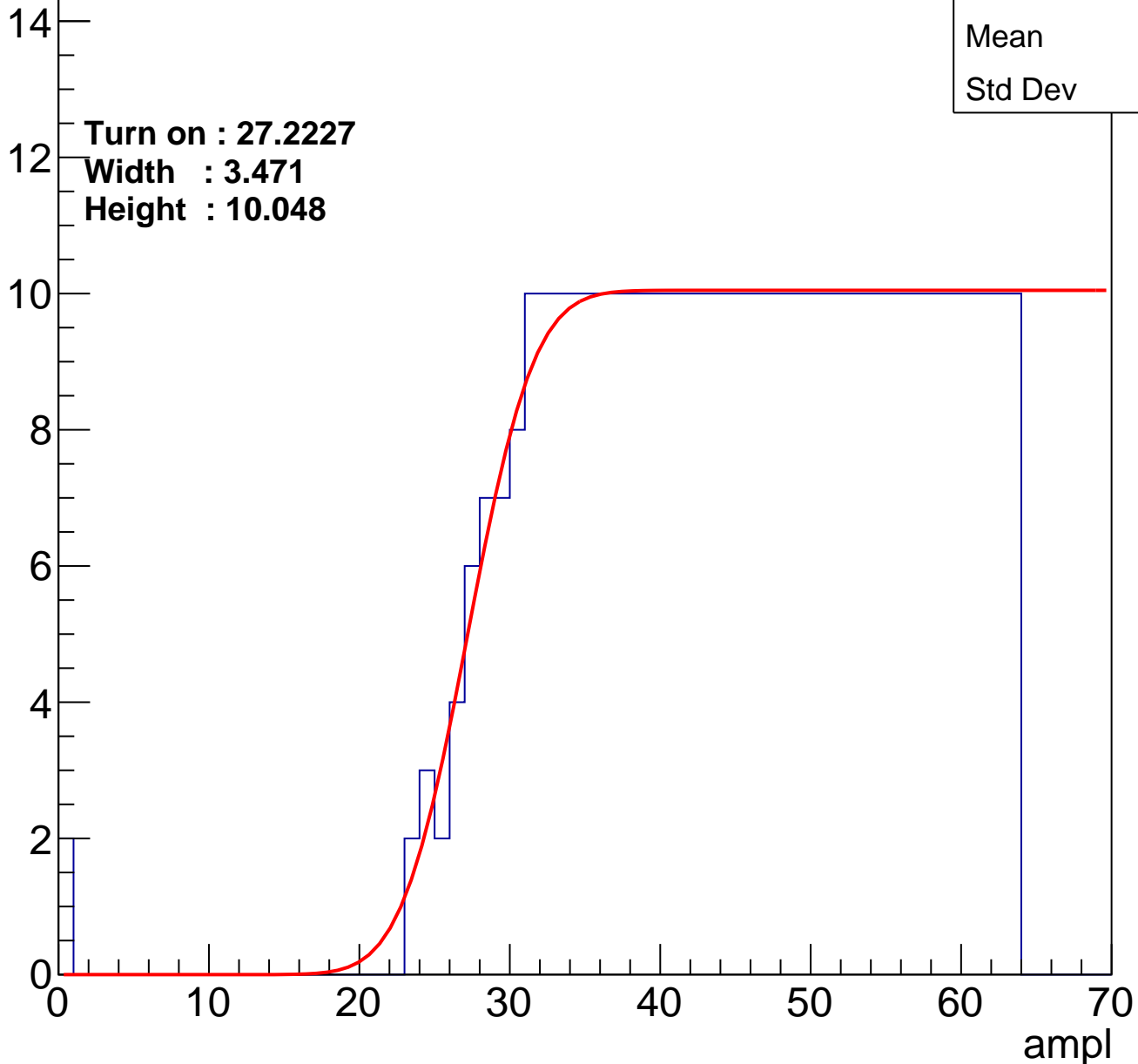
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.2227

Width : 3.471

Height : 10.048

Entry



B1L000S, U13-ch19

calib_packv5_042523_0143.root, FC#3, port B3

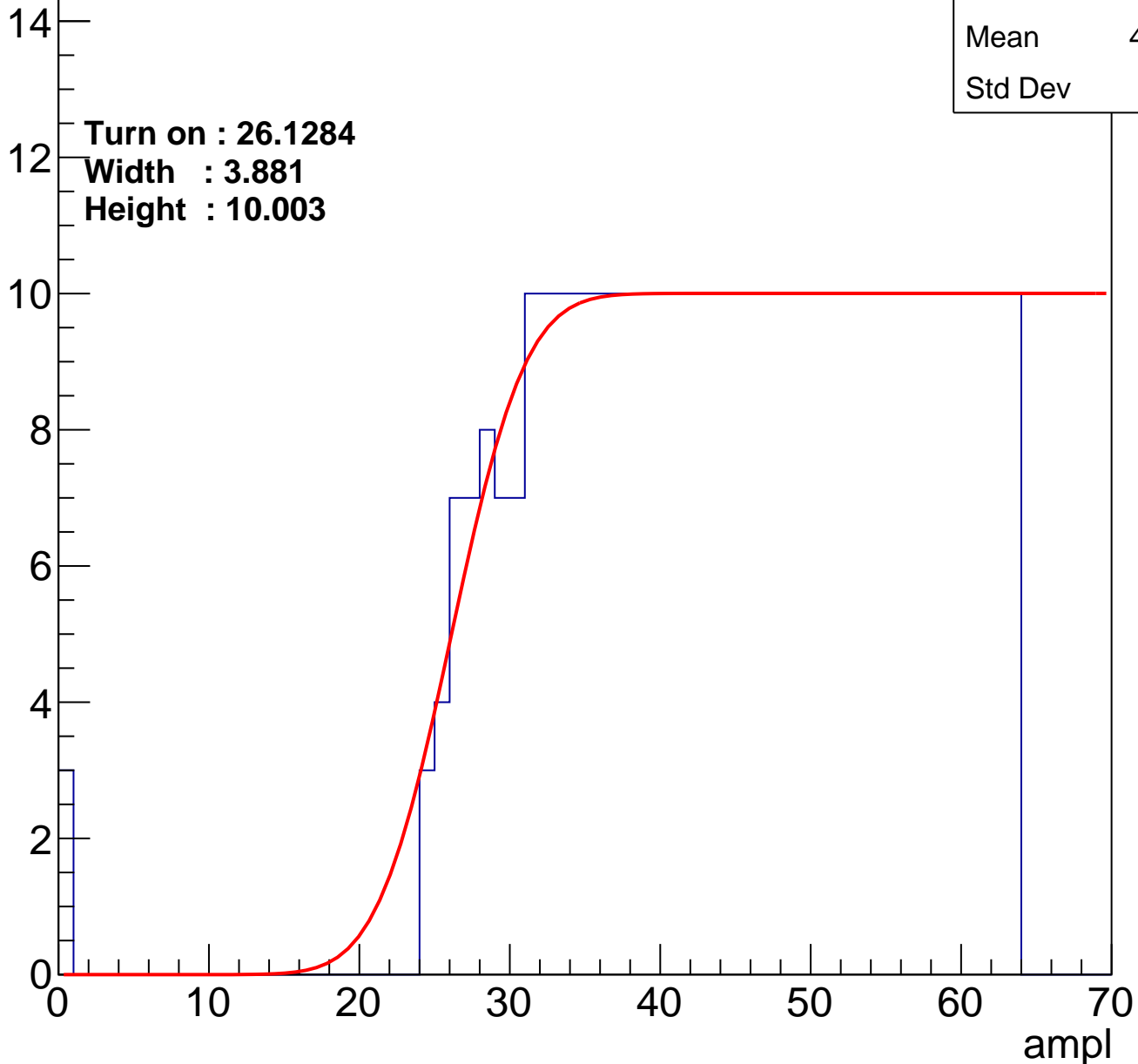
Entries	376
Mean	44.39
Std Dev	11.6

Turn on : 26.1284

Width : 3.881

Height : 10.003

Entry



B1L000S, U13-ch20

calib_packv5_042523_0143.root, FC#3, port B3

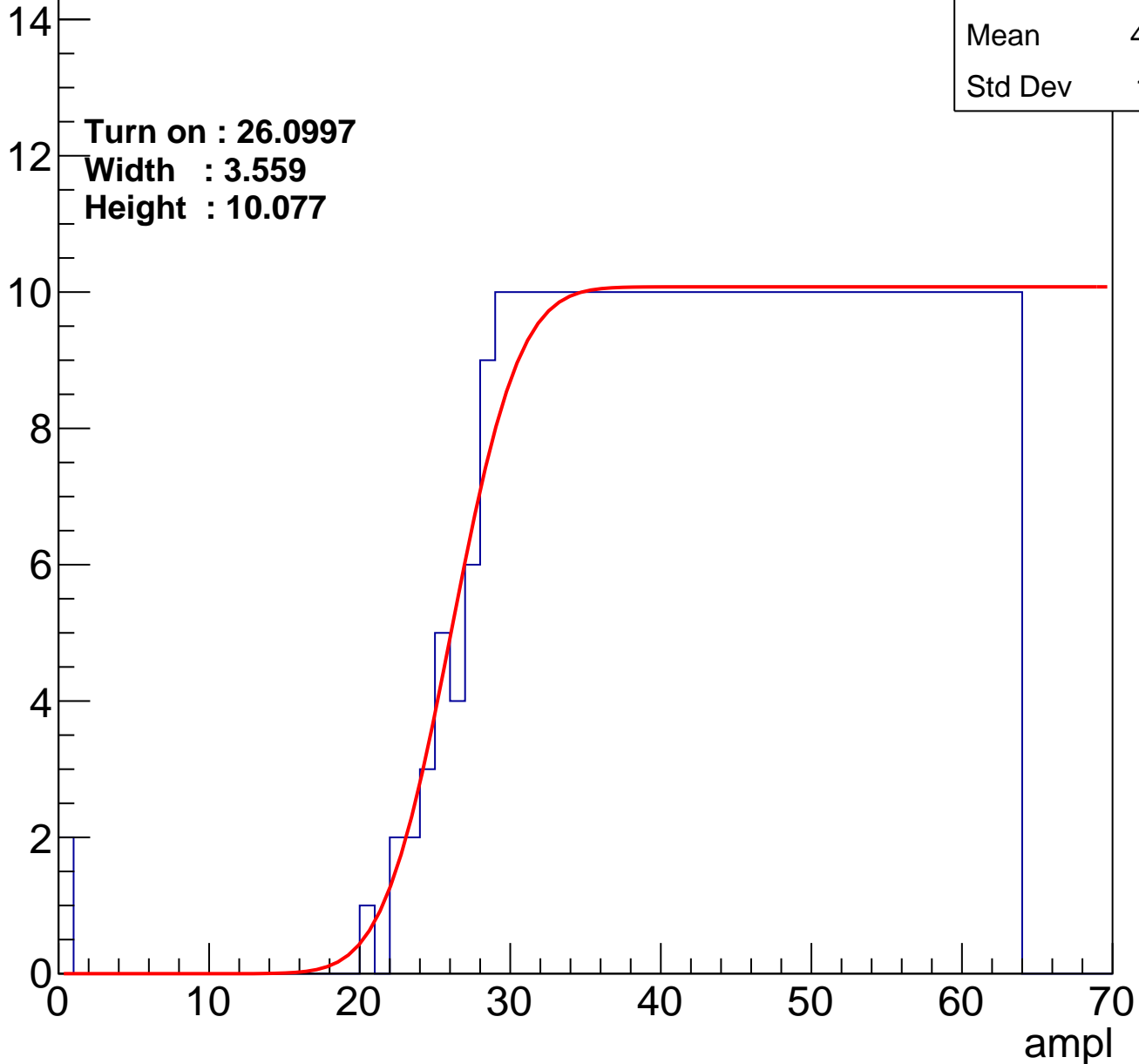
Entries	384
Mean	44.08
Std Dev	11.61

Turn on : 26.0997

Width : 3.559

Height : 10.077

Entry



B1L000S, U13-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.2
Std Dev	11.51

Turn on : 26.0635

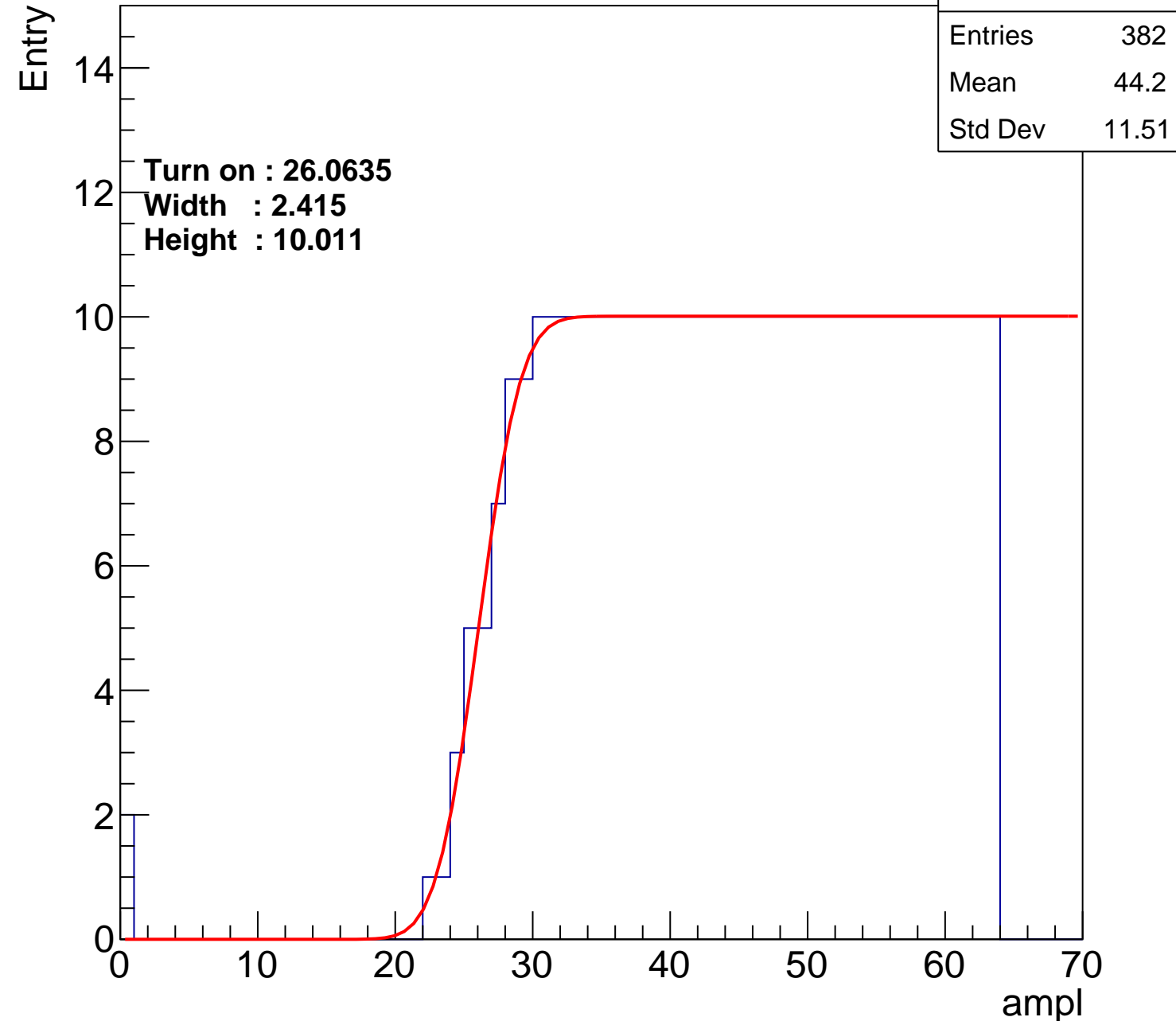
Width : 2.415

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch22

calib_packv5_042523_0143.root, FC#3, port B3

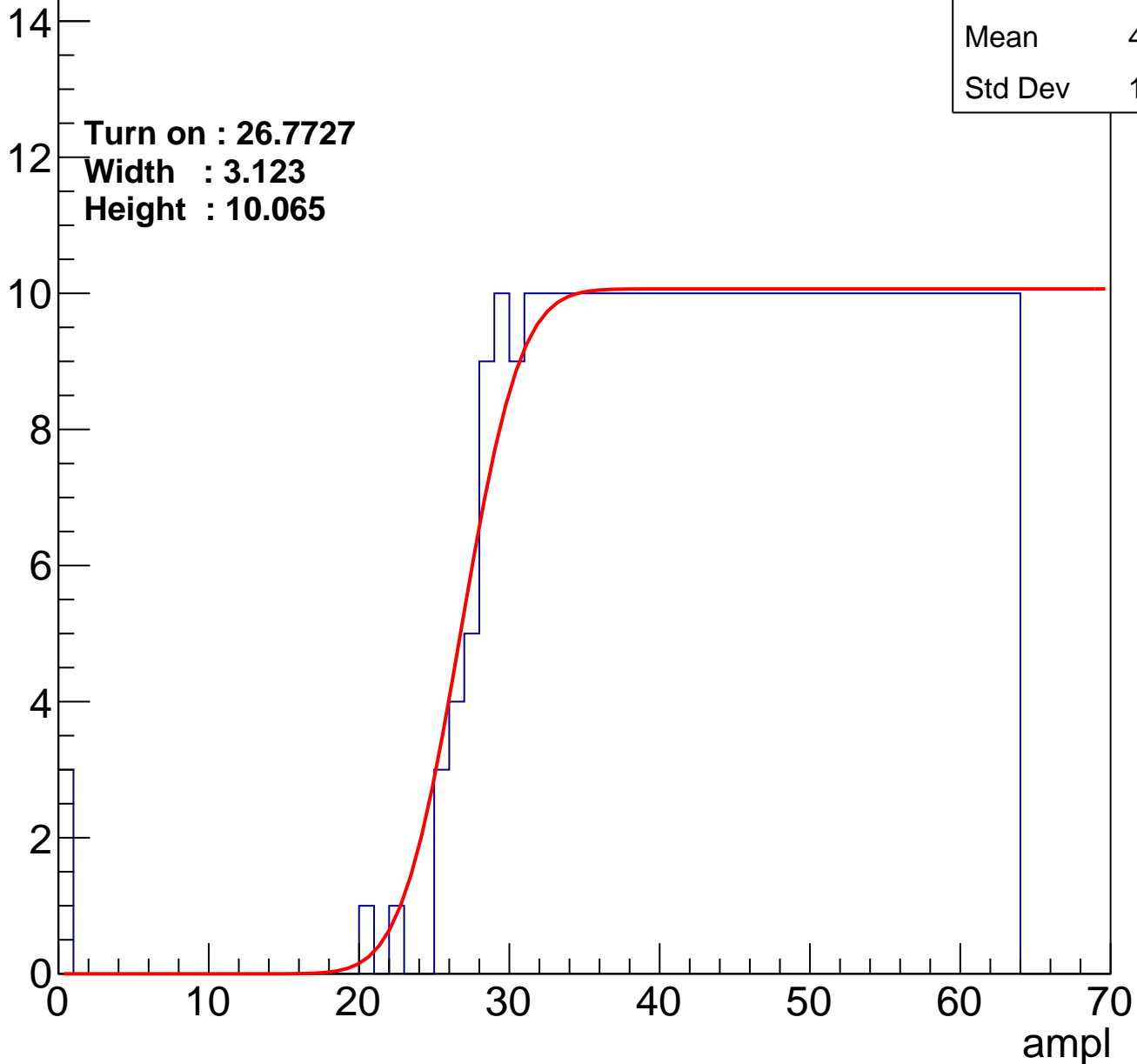
Entries	375
Mean	44.47
Std Dev	11.53

Turn on : 26.7727

Width : 3.123

Height : 10.065

Entry



B1L000S, U13-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.59
Std Dev	11.31

Turn on : 27.0164

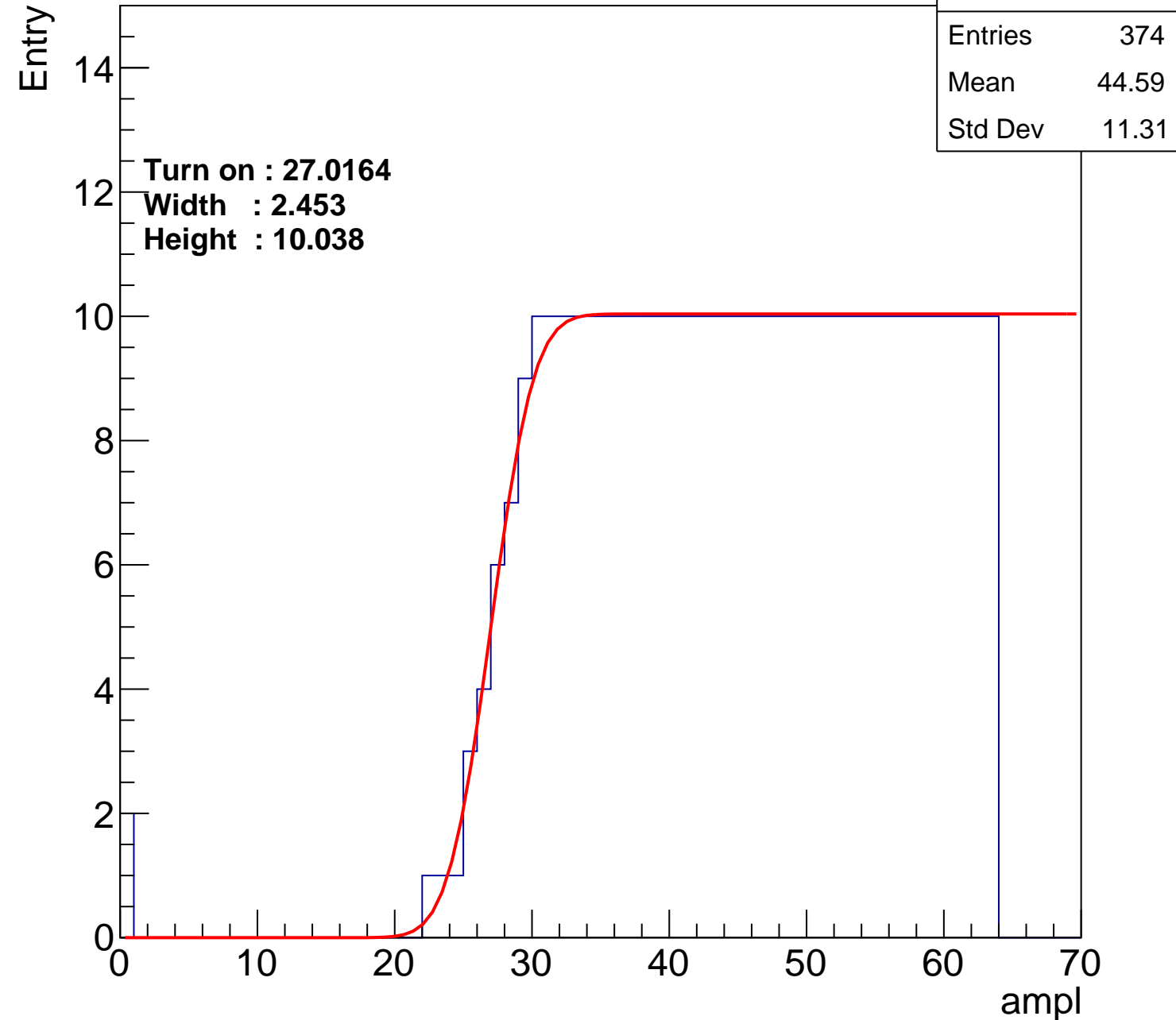
Width : 2.453

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.6008

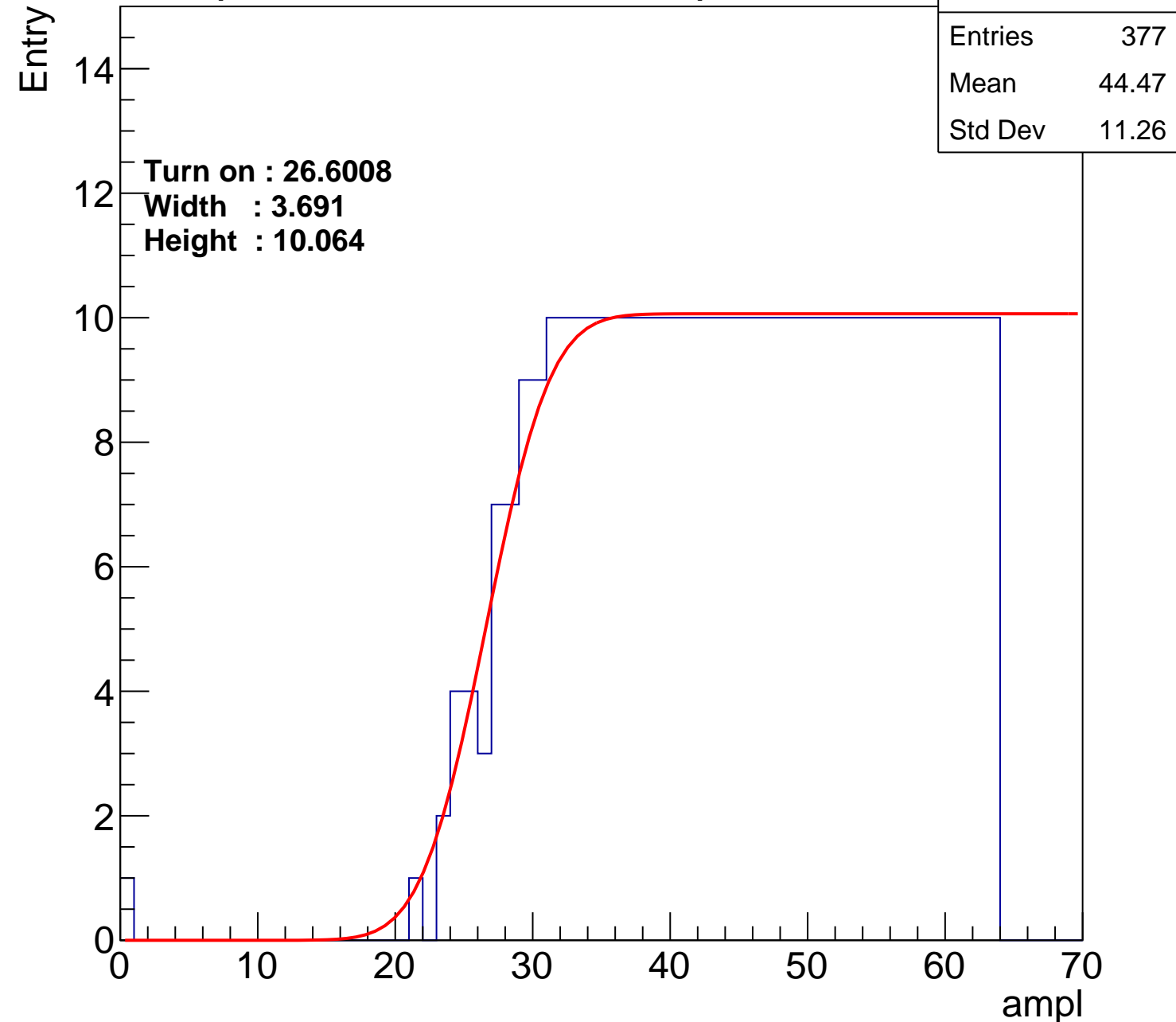
Width : 3.691

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch25

calib_packv5_042523_0143.root, FC#3, port B3

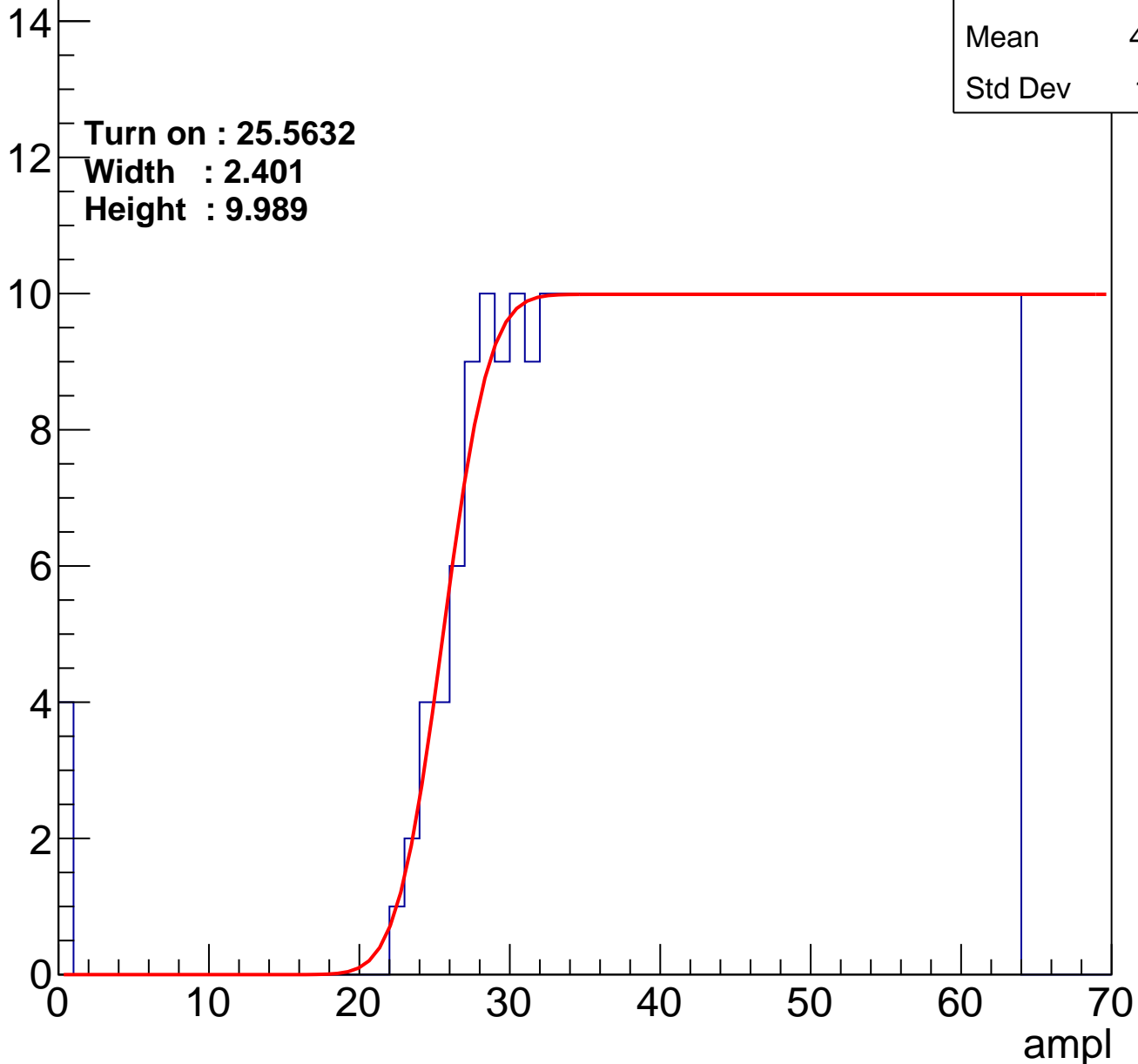
Entries	388
Mean	43.77
Std Dev	12.01

Turn on : 25.5632

Width : 2.401

Height : 9.989

Entry



B1L000S, U13-ch26

calib_packv5_042523_0143.root, FC#3, port B3

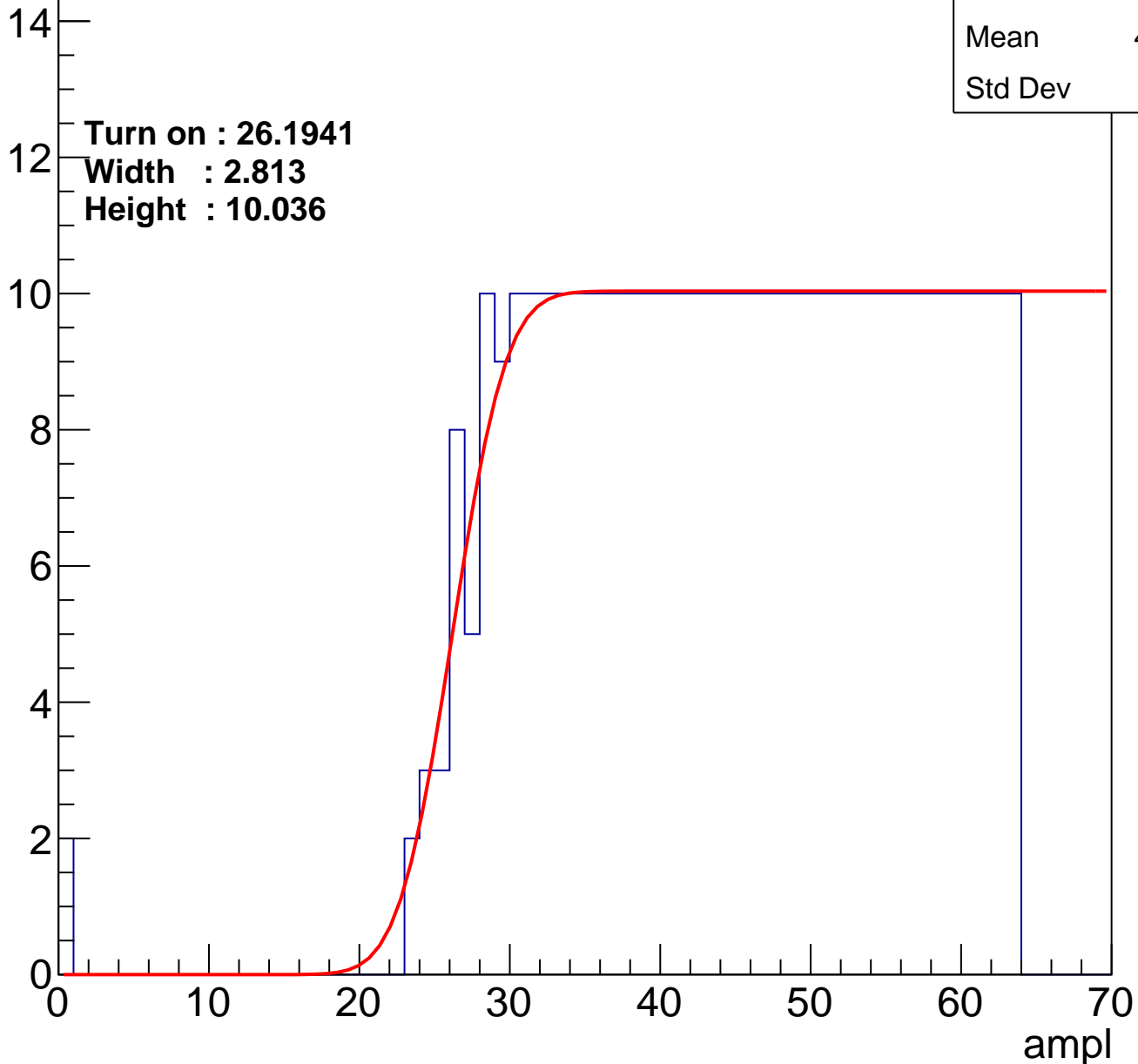
Entries	382
Mean	44.21
Std Dev	11.5

Turn on : 26.1941

Width : 2.813

Height : 10.036

Entry



B1L000S, U13-ch27

calib_packv5_042523_0143.root, FC#3, port B3

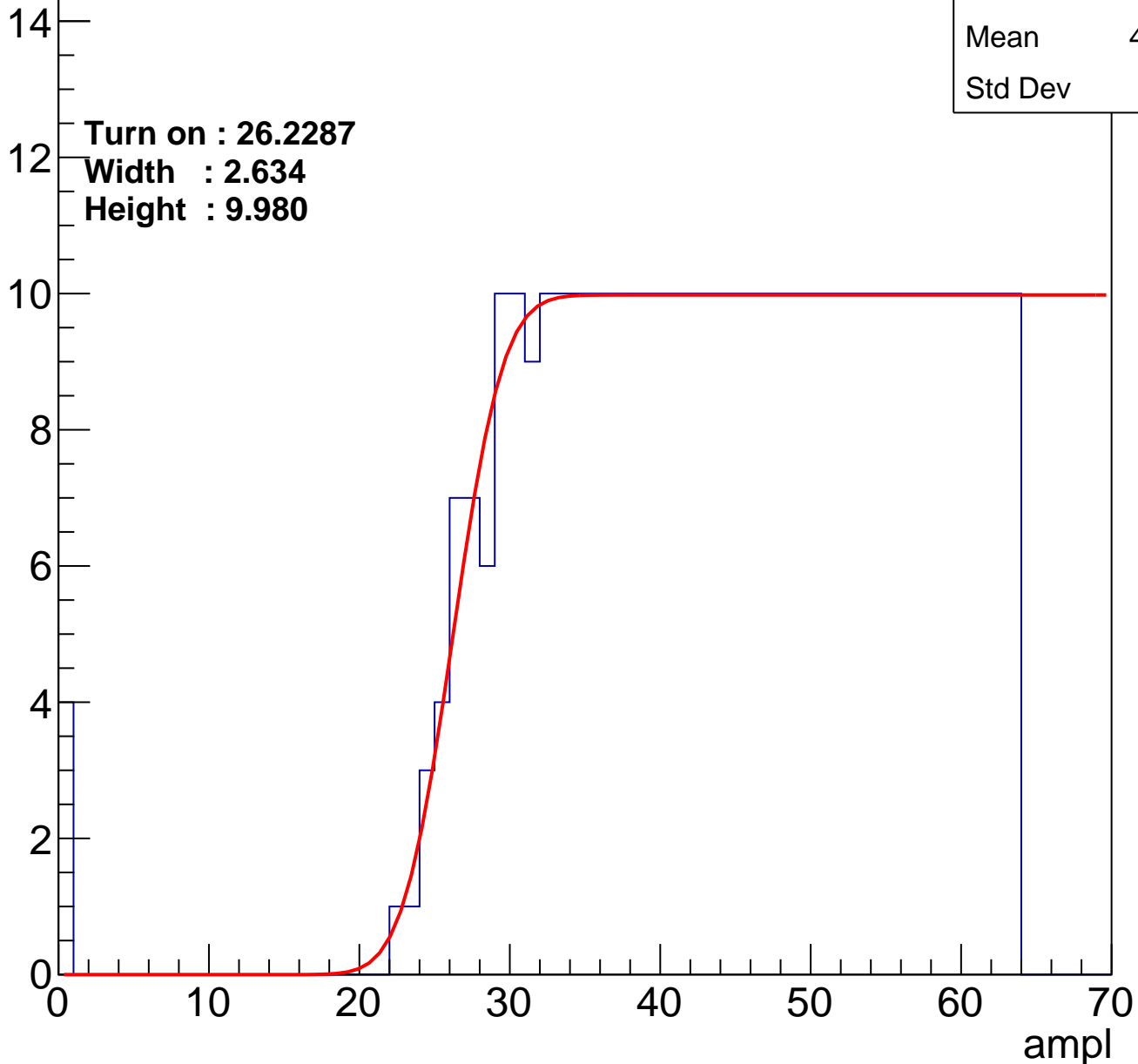
Entries	382
Mean	44.04
Std Dev	11.9

Turn on : 26.2287

Width : 2.634

Height : 9.980

Entry



B1L000S, U13-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.48
Std Dev	12.03

Turn on : 25.2799

Width : 3.379

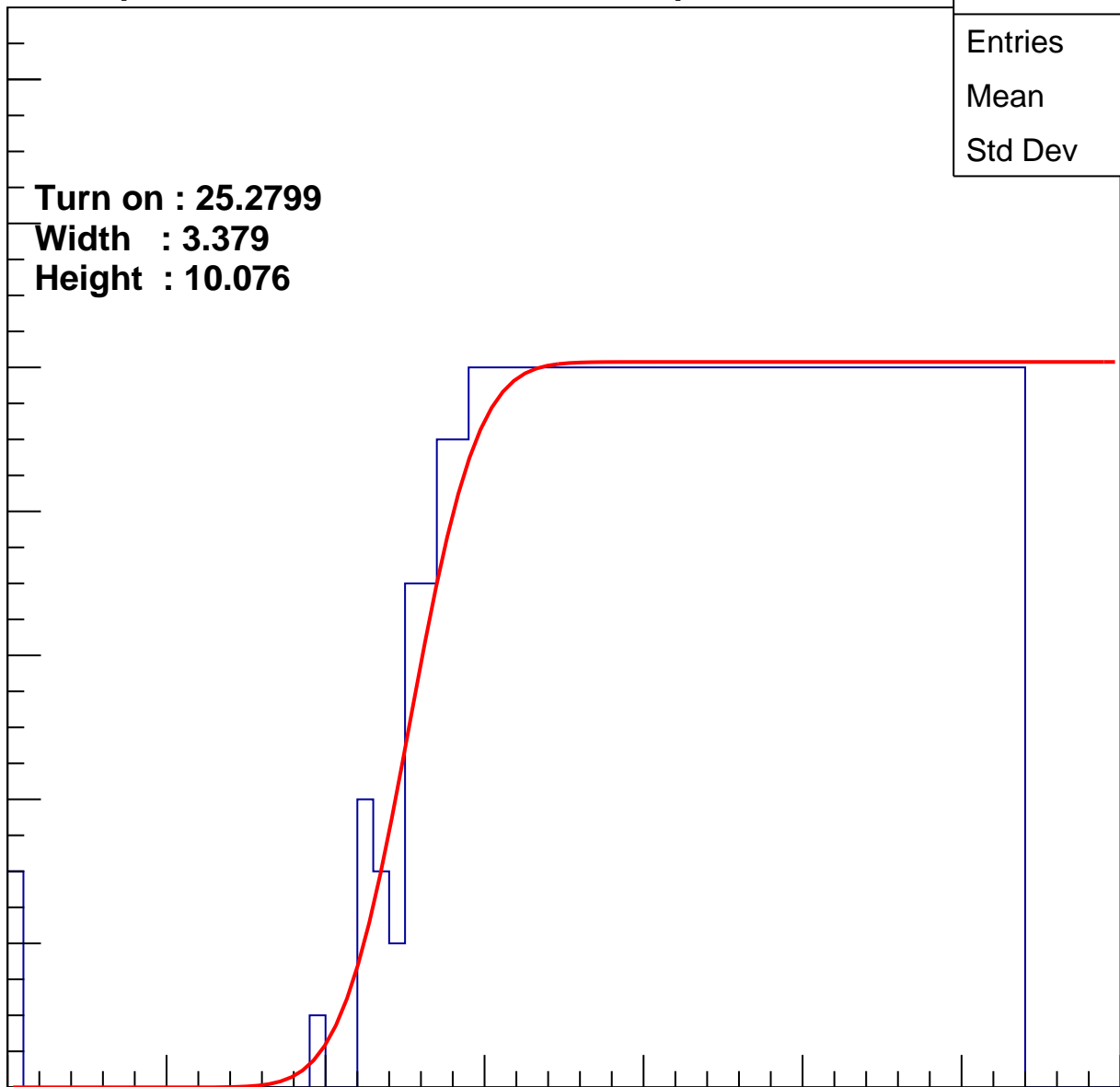
Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U13-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.02
Std Dev	11.47

Turn on : 25.7400

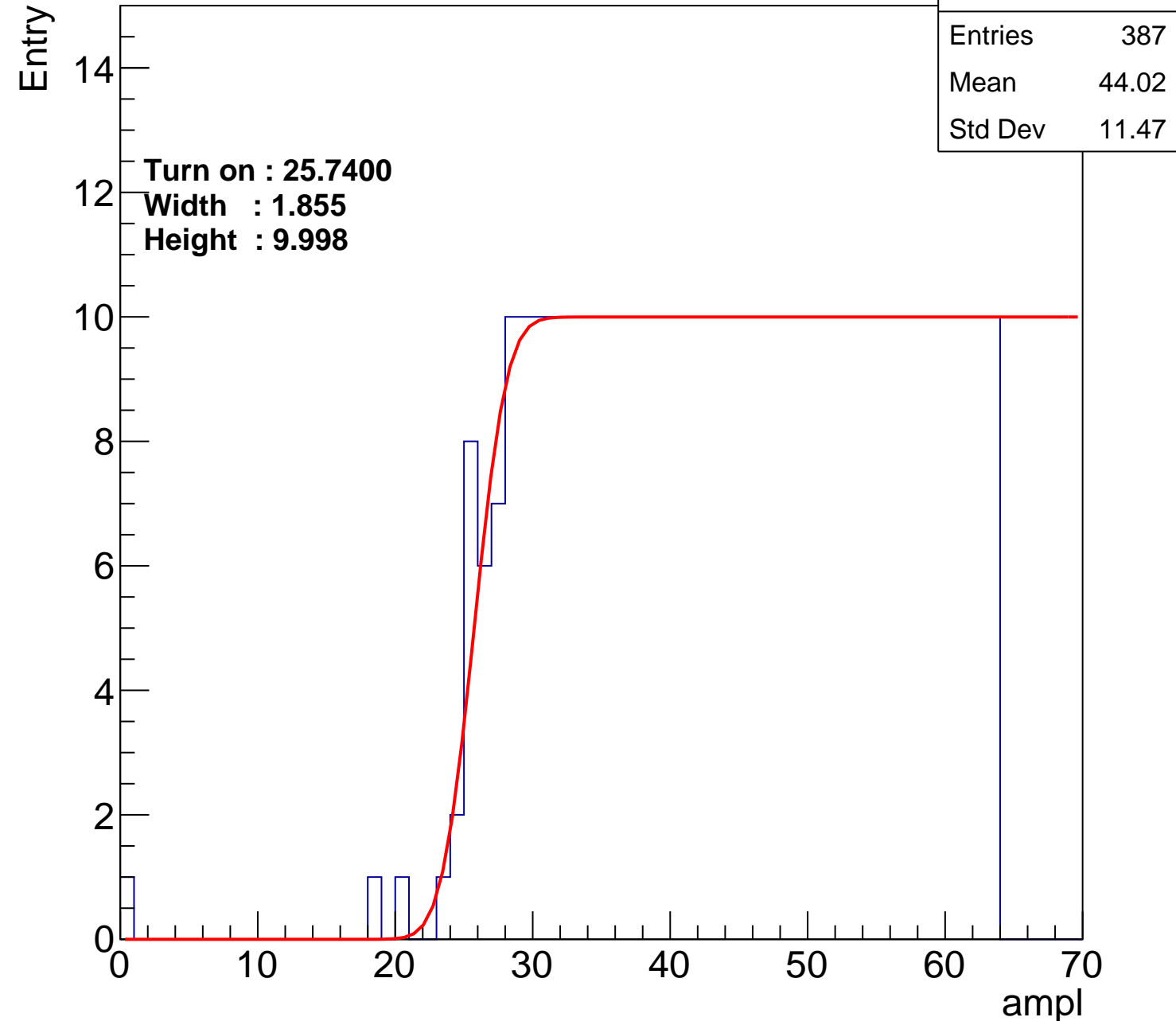
Width : 1.855

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

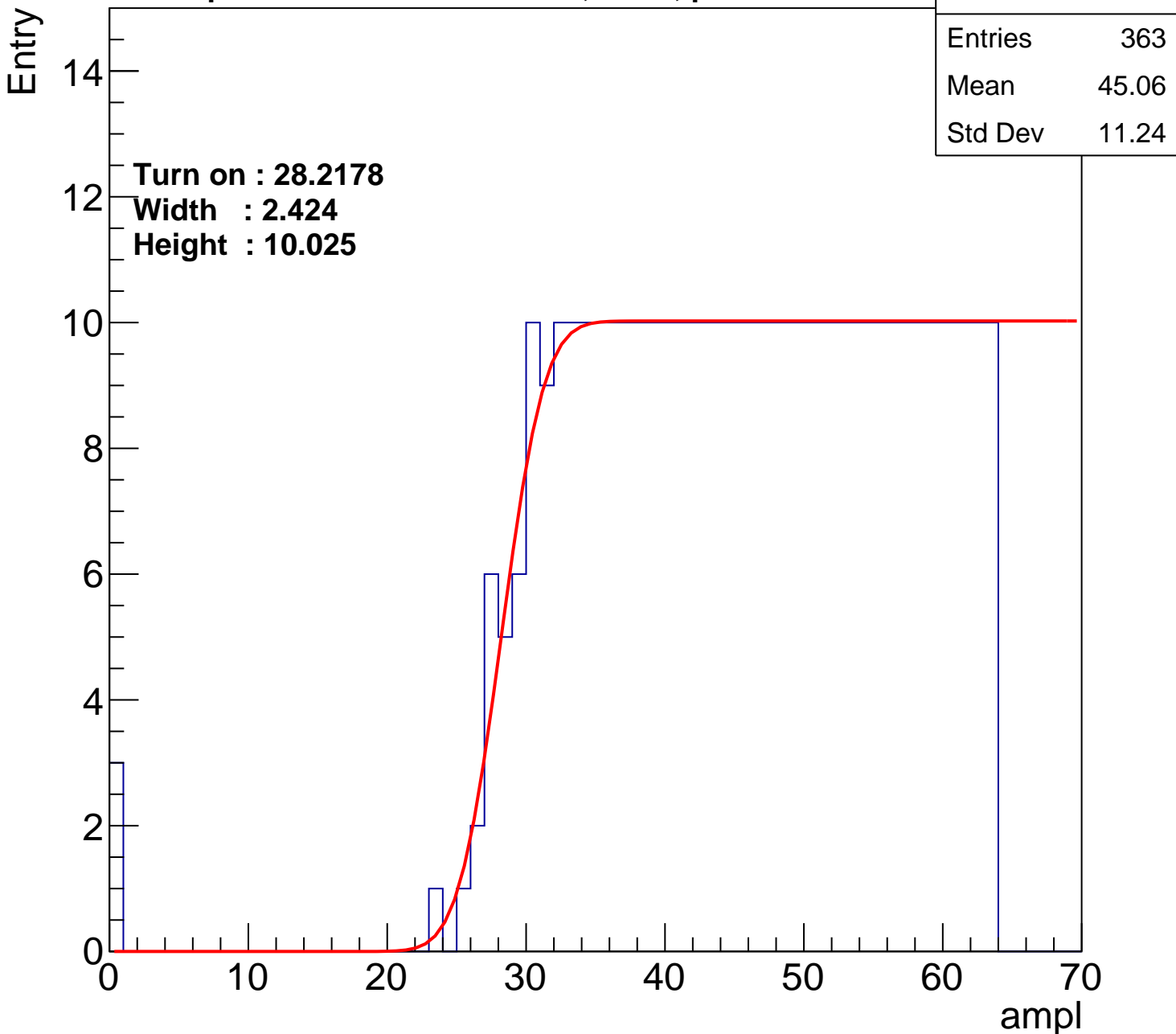
Entries	363
Mean	45.06
Std Dev	11.24

Mean	45.06
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Std Dev	11.24
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Width : 2.424

Height : 10.025



B1L000S, U13-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 26.2797

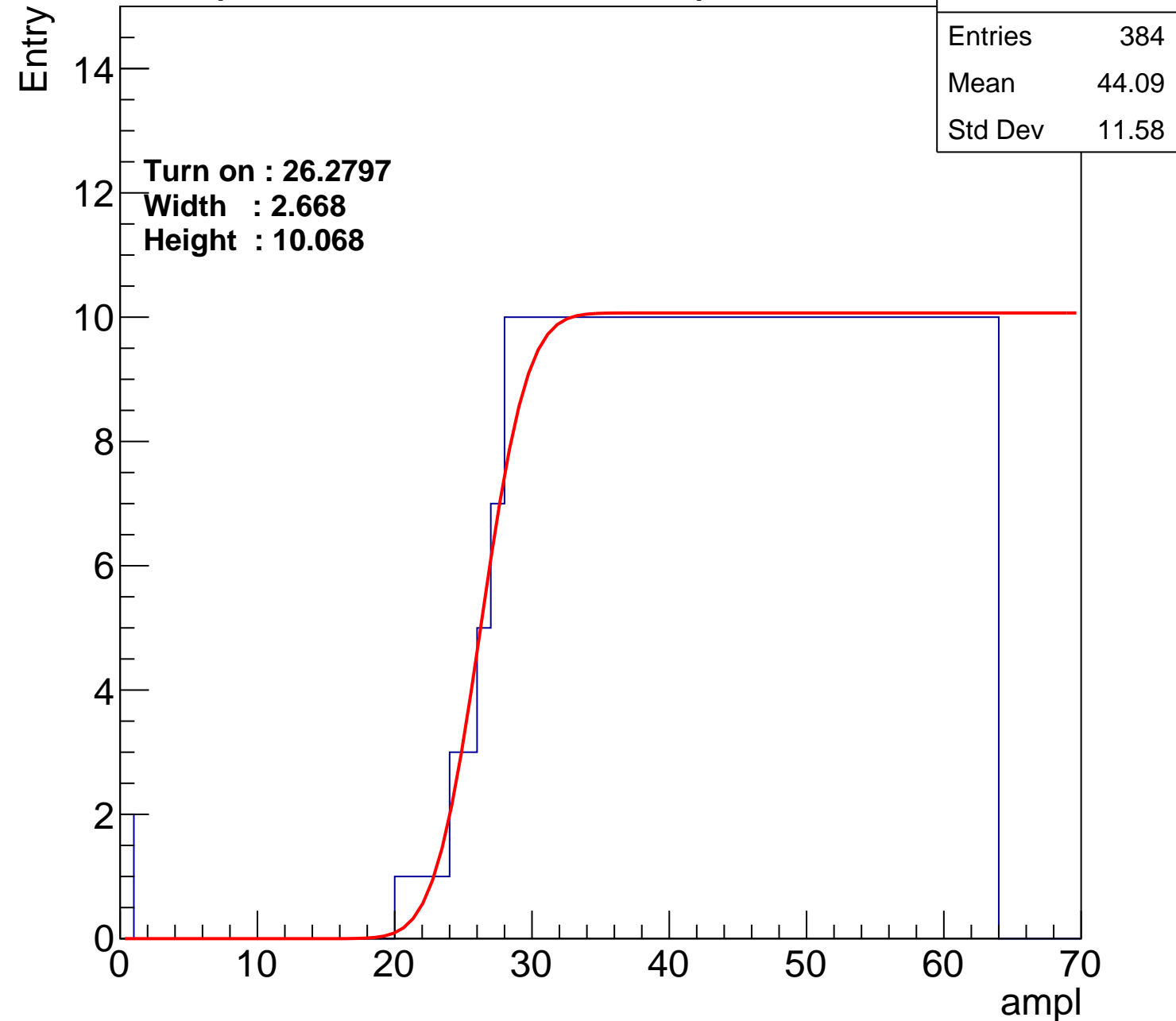
Width : 2.668

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.24
Std Dev	11.49

Turn on : 26.1360

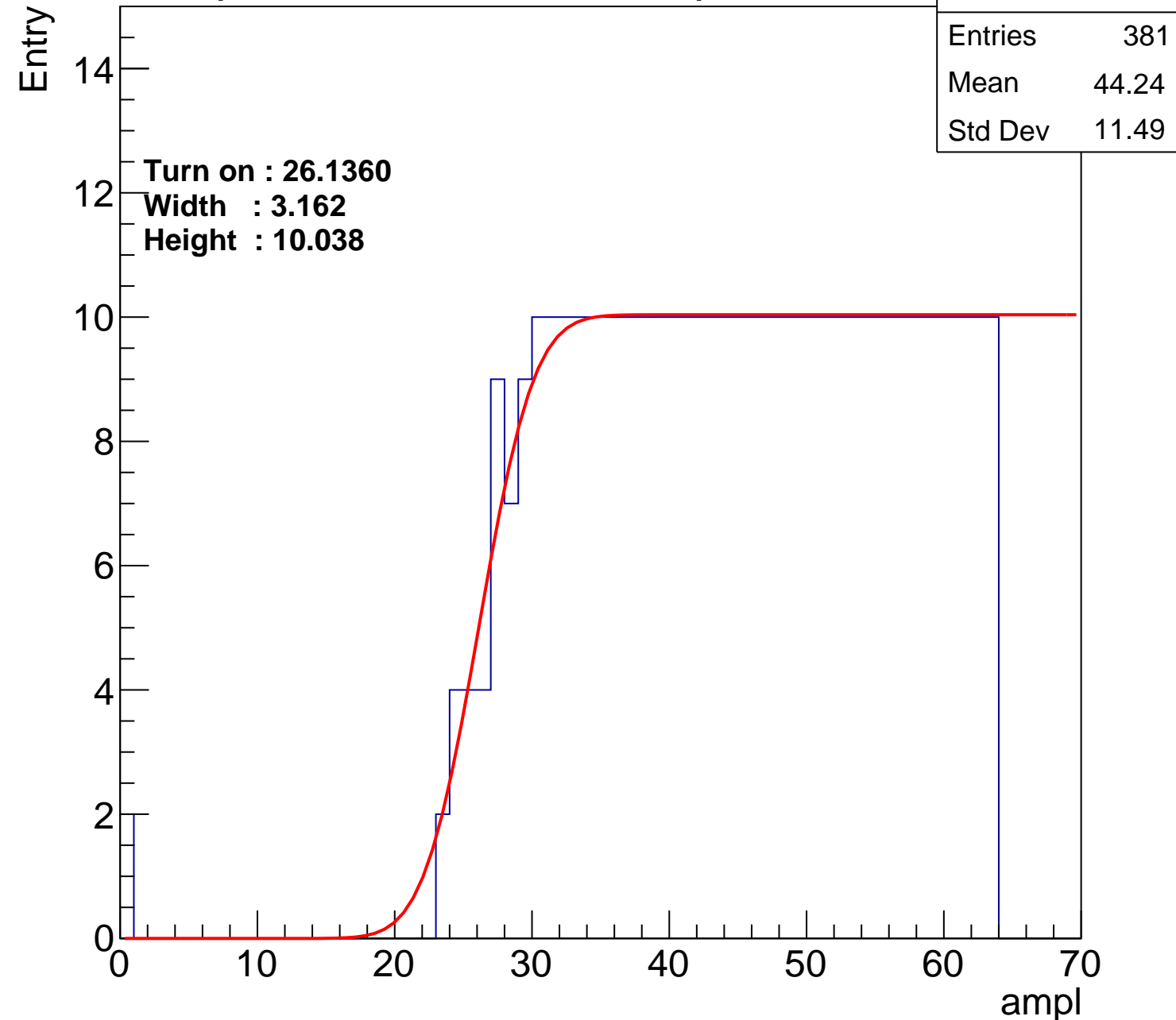
Width : 3.162

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.82
Std Dev	11.21

Turn on : 27.4965

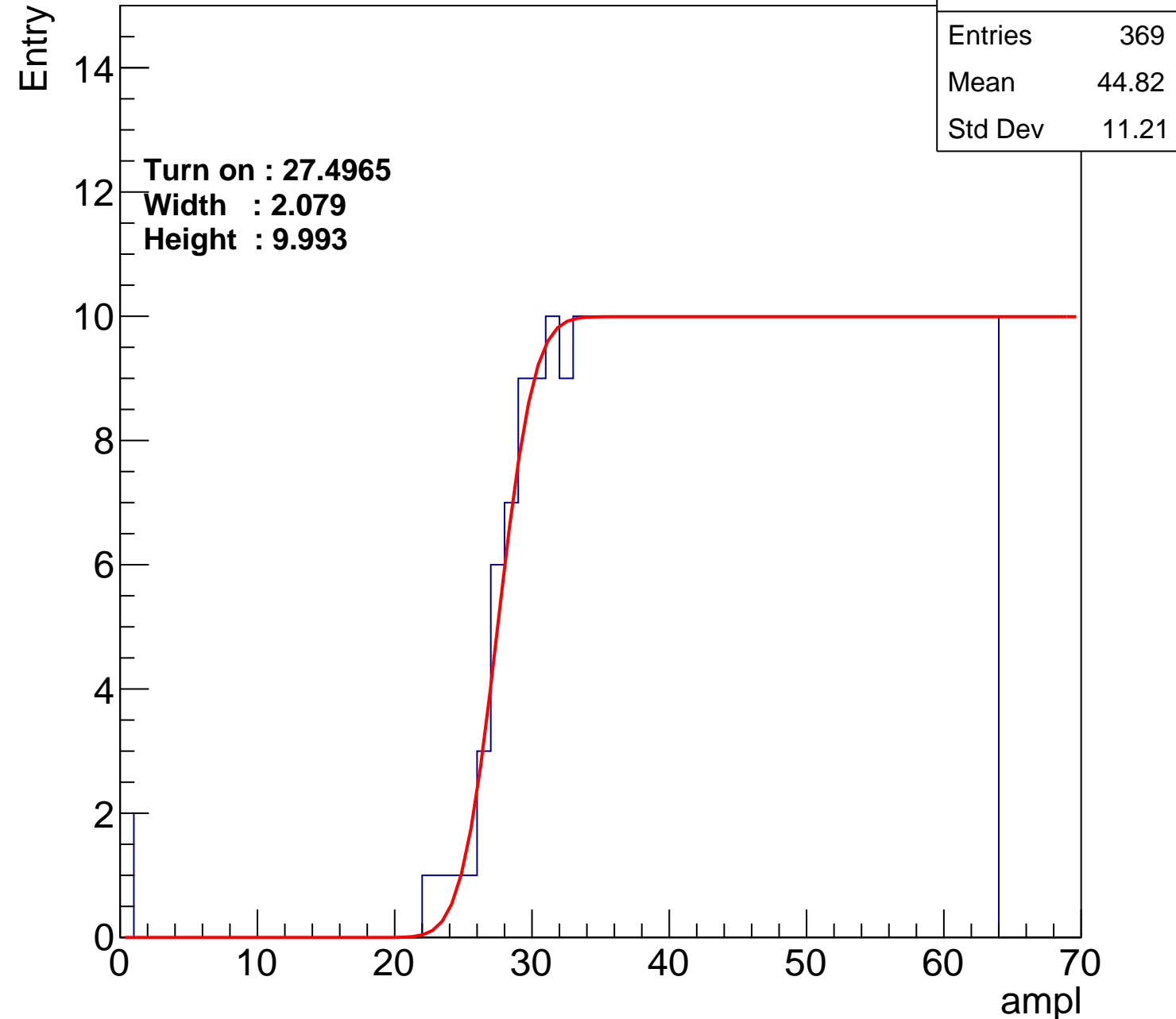
Width : 2.079

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.1337

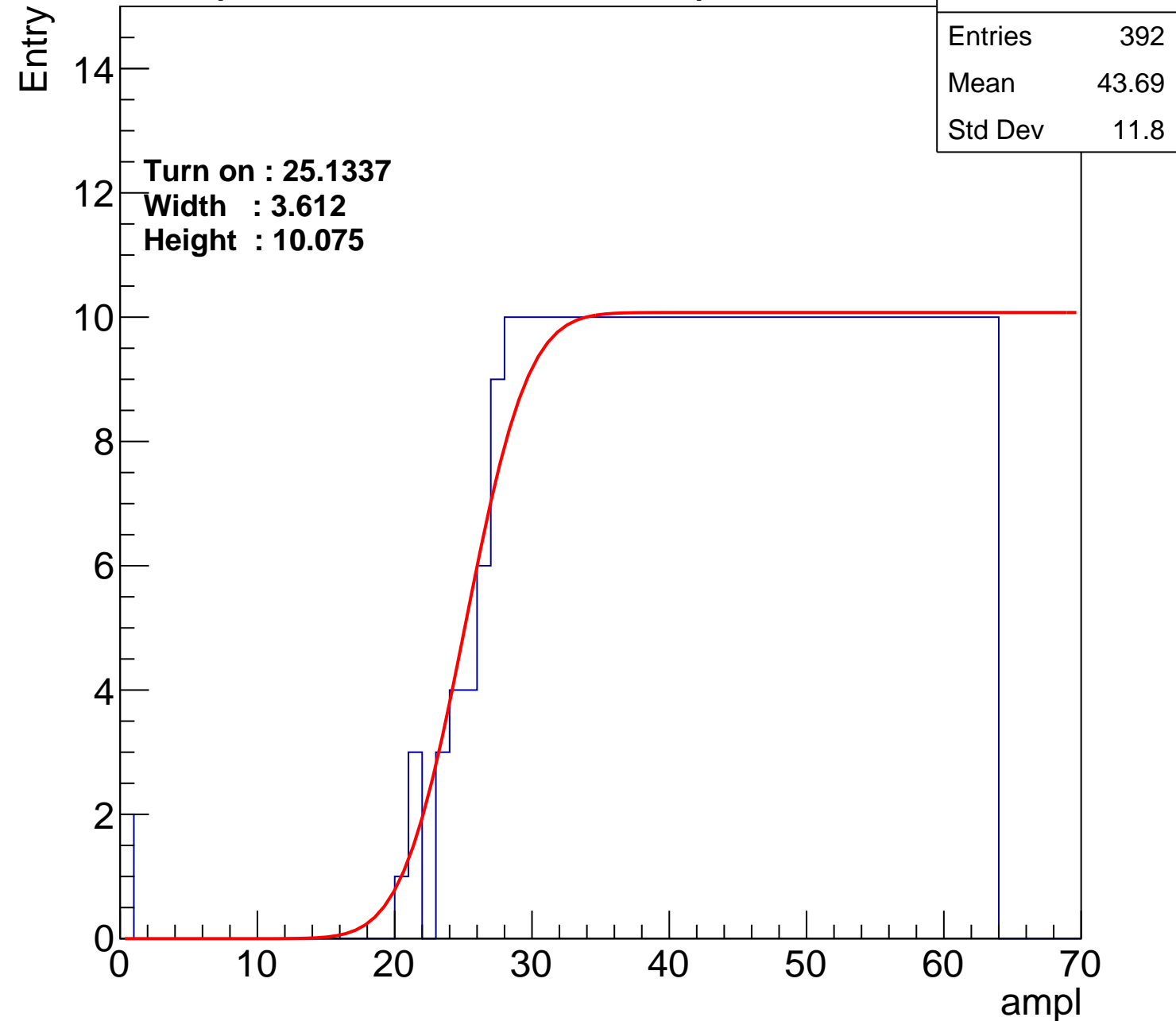
Width : 3.612

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.98
Std Dev	11.12

Turn on : 27.9624

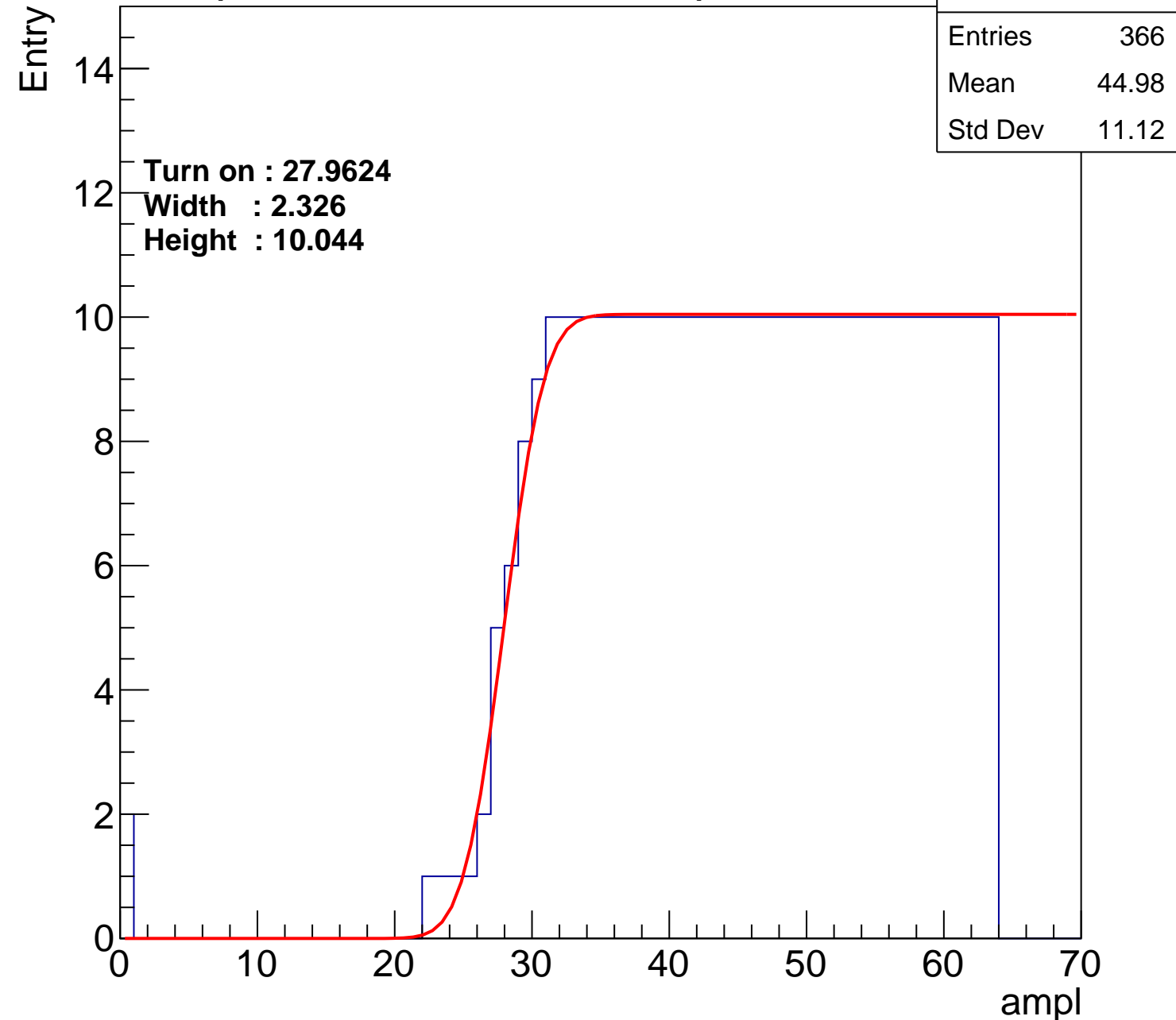
Width : 2.326

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch36

calib_packv5_042523_0143.root, FC#3, port B3

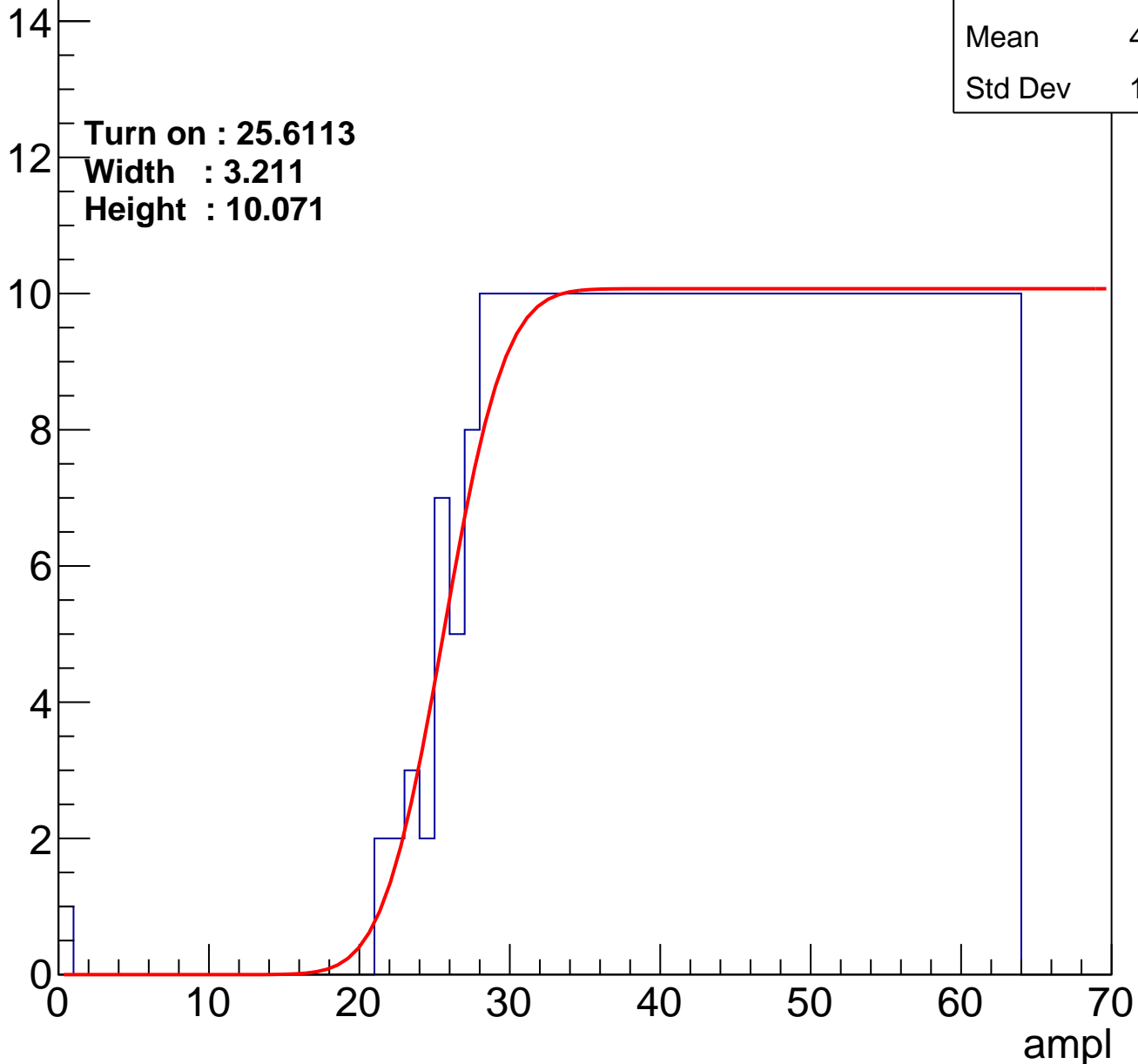
Entries	390
Mean	43.86
Std Dev	11.57

Turn on : 25.6113

Width : 3.211

Height : 10.071

Entry



B1L000S, U13-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.55
Std Dev	11.87

Turn on : 24.8624

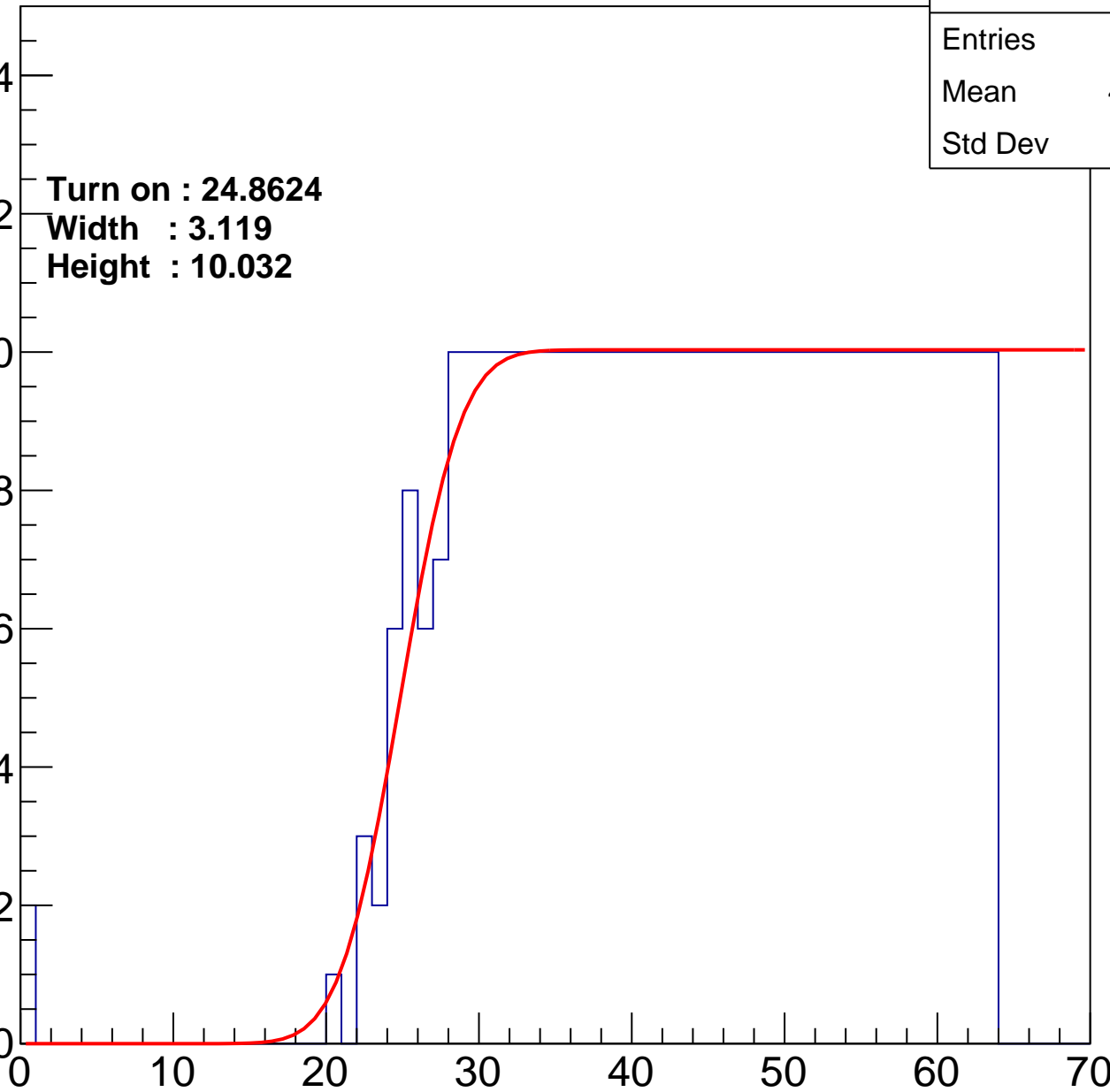
Width : 3.119

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.7002

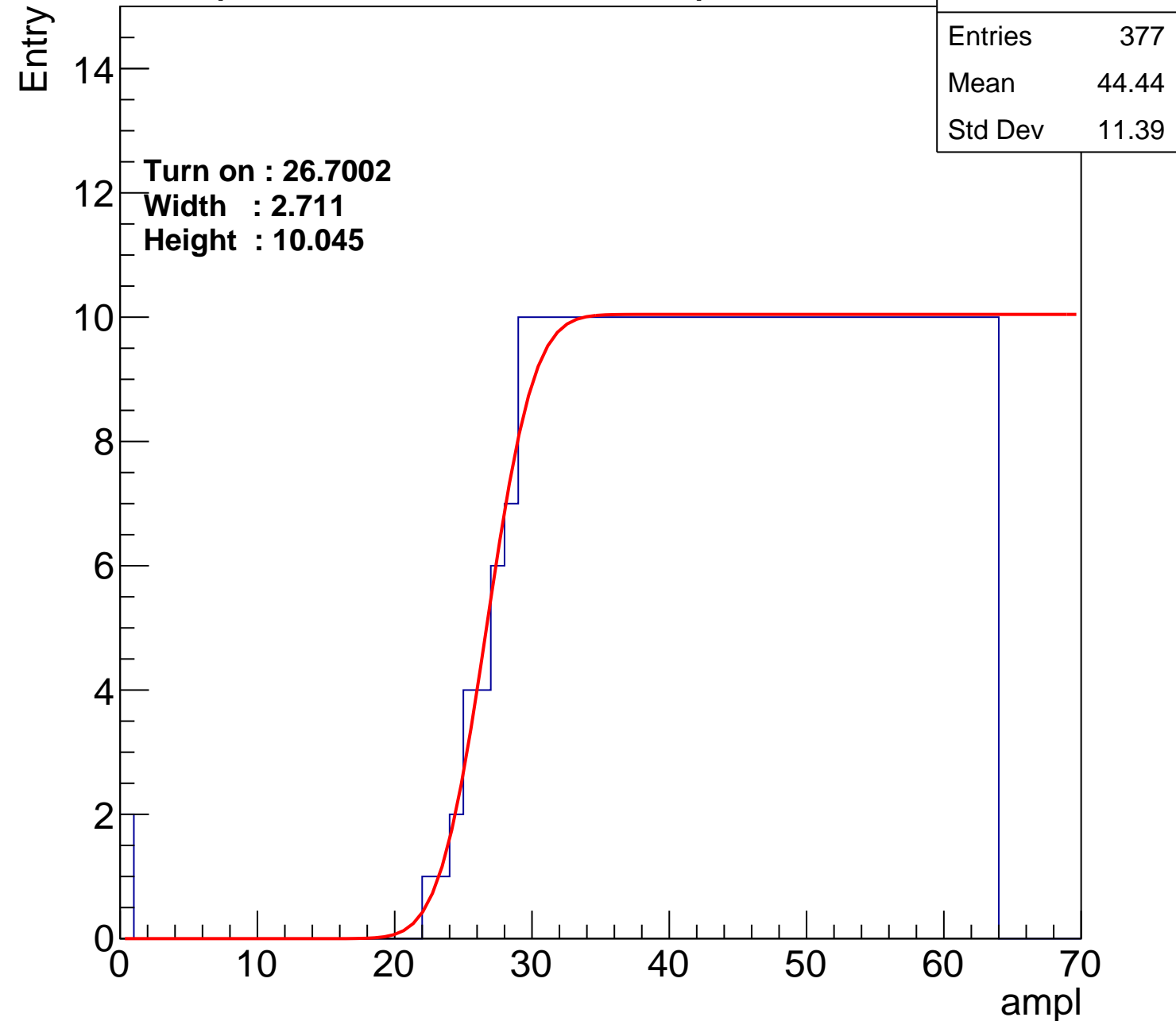
Width : 2.711

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch39

calib_packv5_042523_0143.root, FC#3, port B3

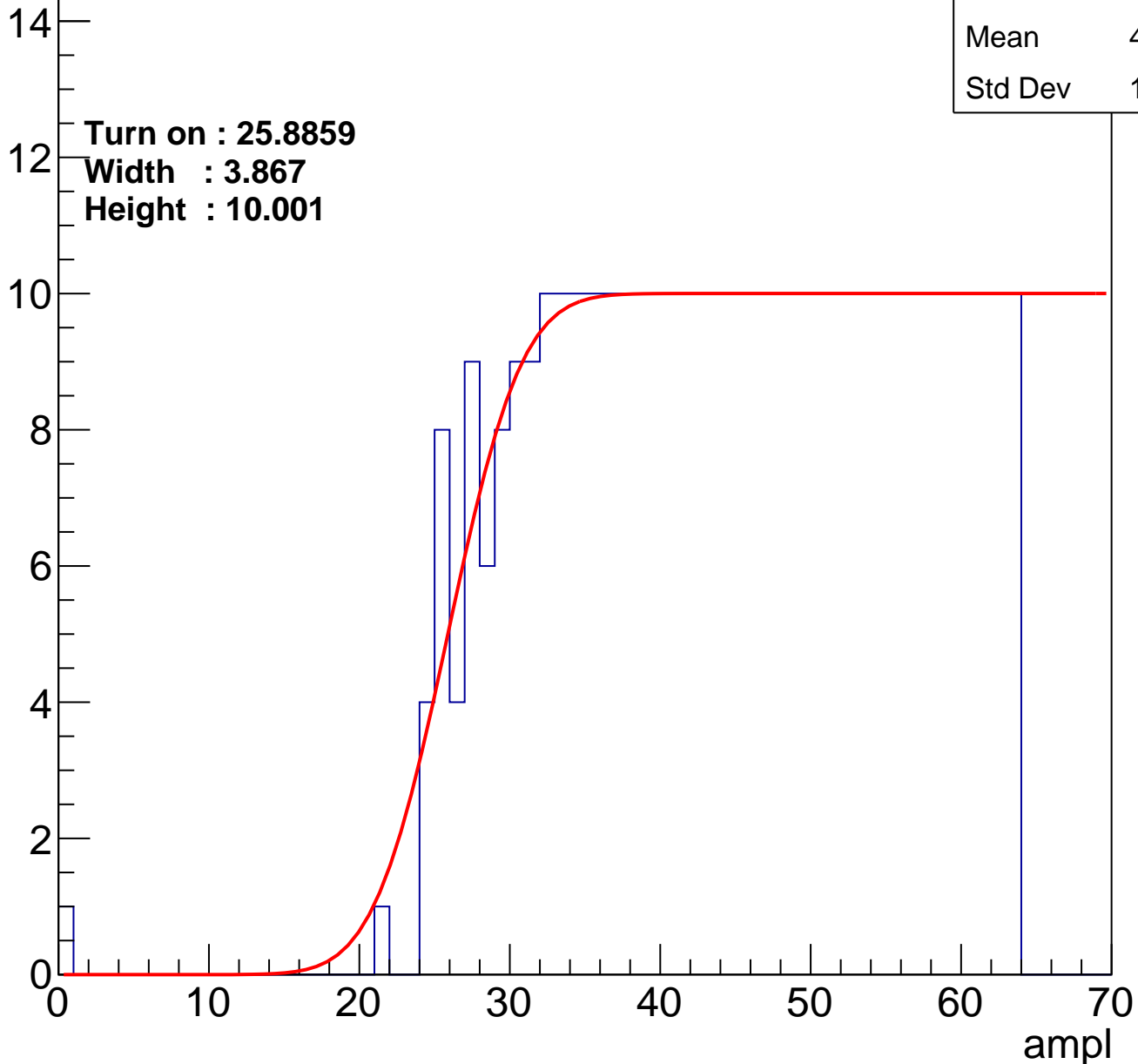
Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 25.8859

Width : 3.867

Height : 10.001

Entry



B1L000S, U13-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.55
Std Dev	11.24

Turn on : 26.9917

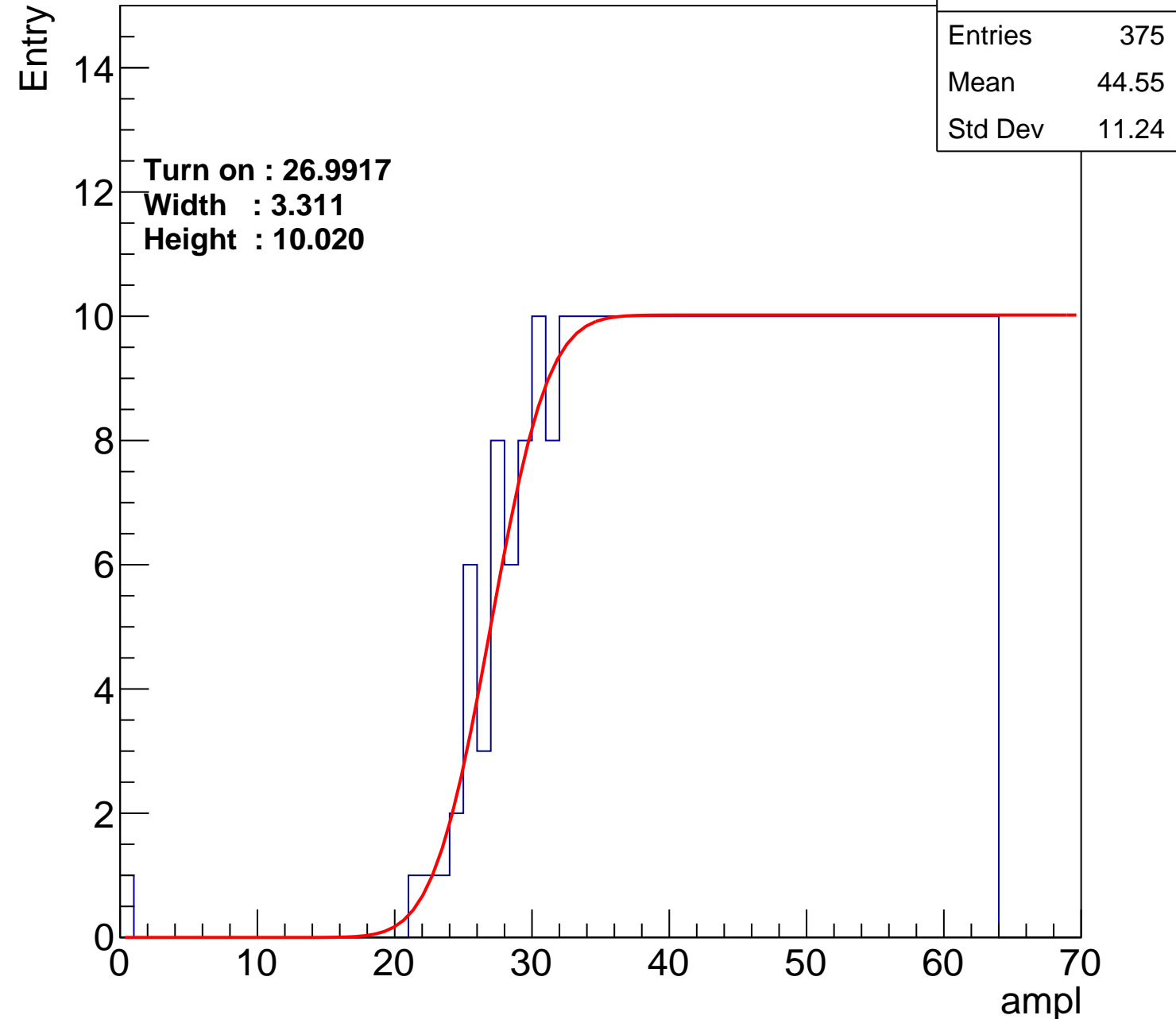
Width : 3.311

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch41

calib_packv5_042523_0143.root, FC#3, port B3

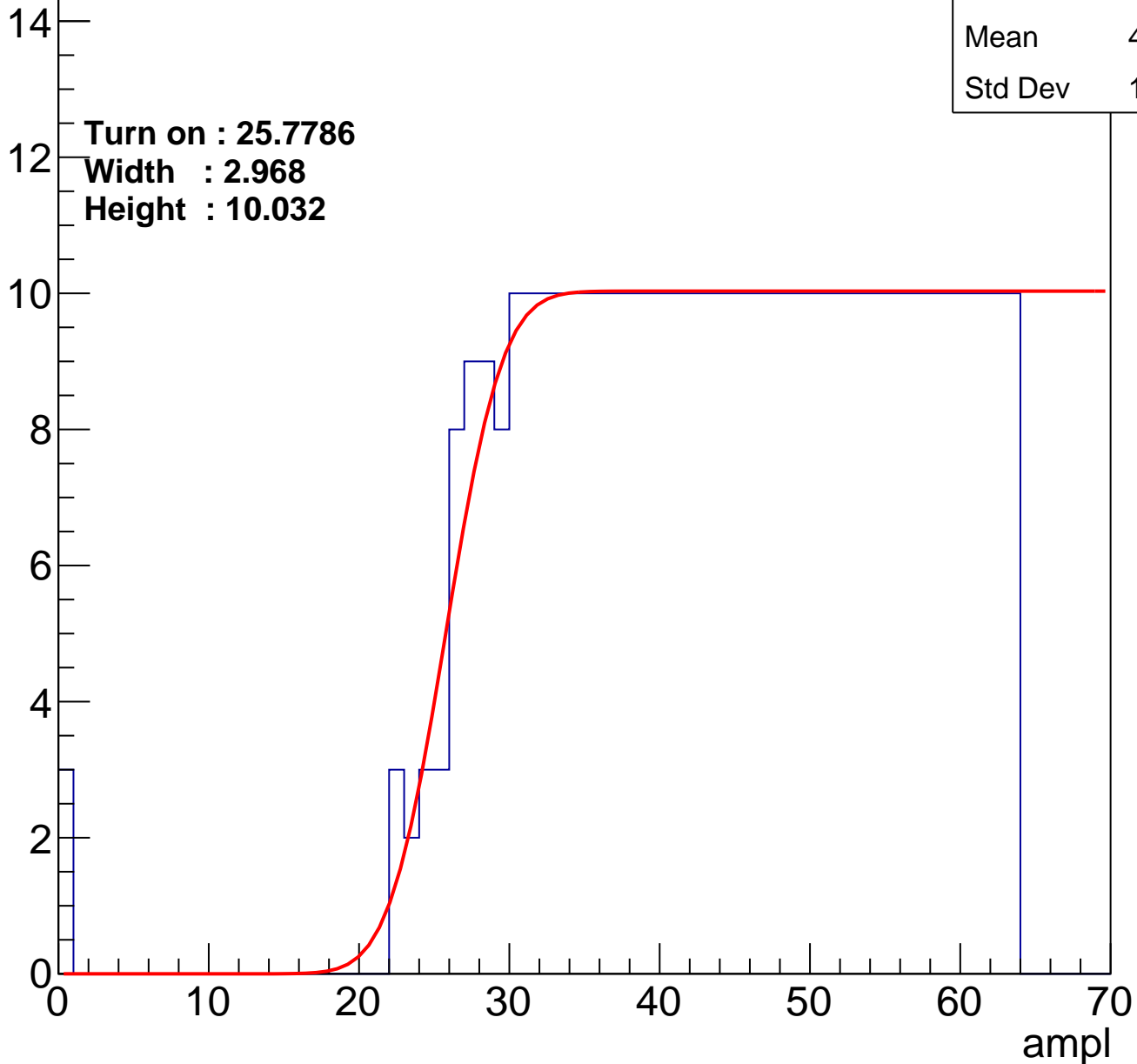
Entries	388
Mean	43.82
Std Dev	11.86

Turn on : 25.7786

Width : 2.968

Height : 10.032

Entry



B1L000S, U13-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.91
Std Dev	11.18

Turn on : 27.9081

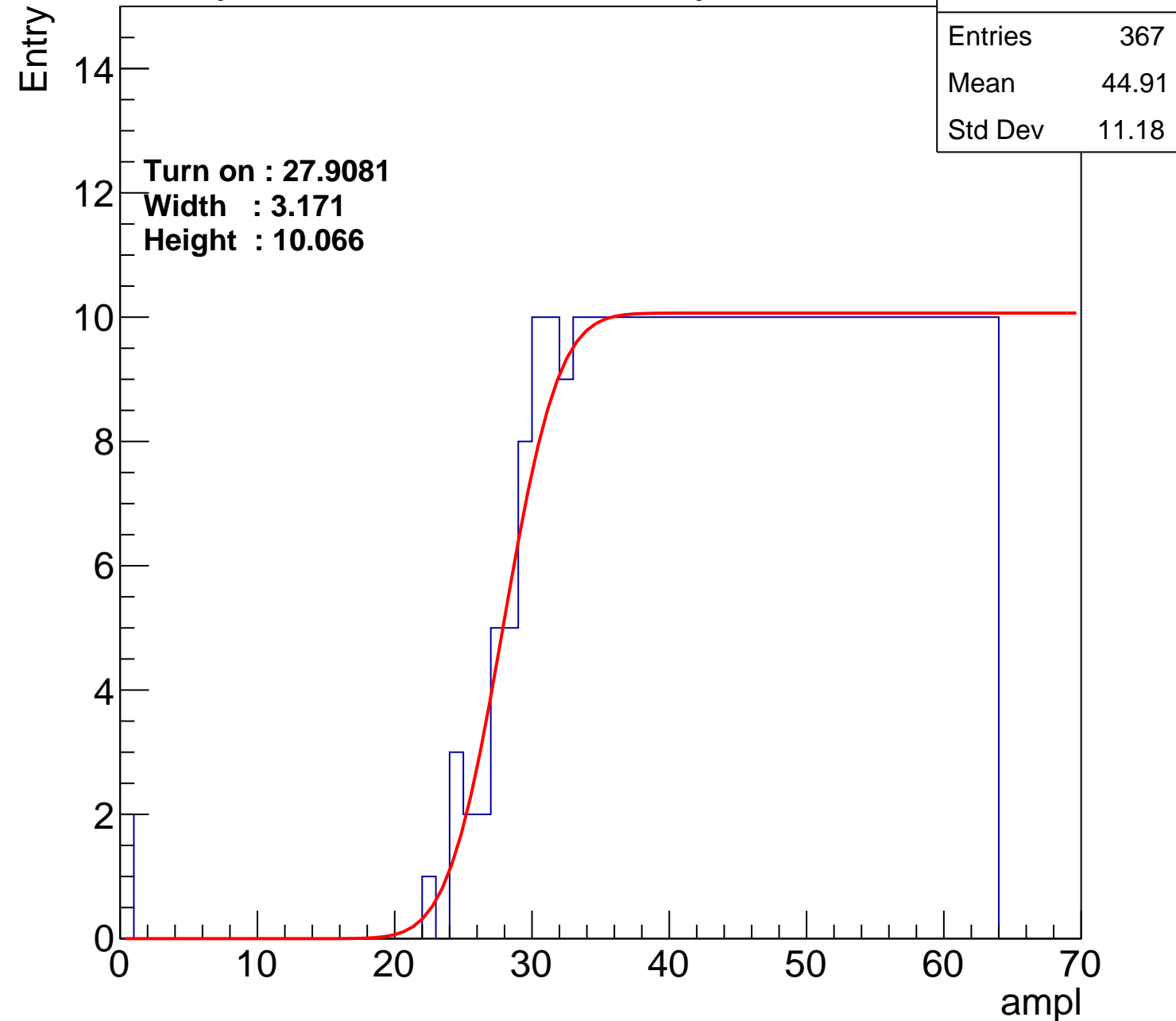
Width : 3.171

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.85
Std Dev	11.17

Turn on : 27.5852

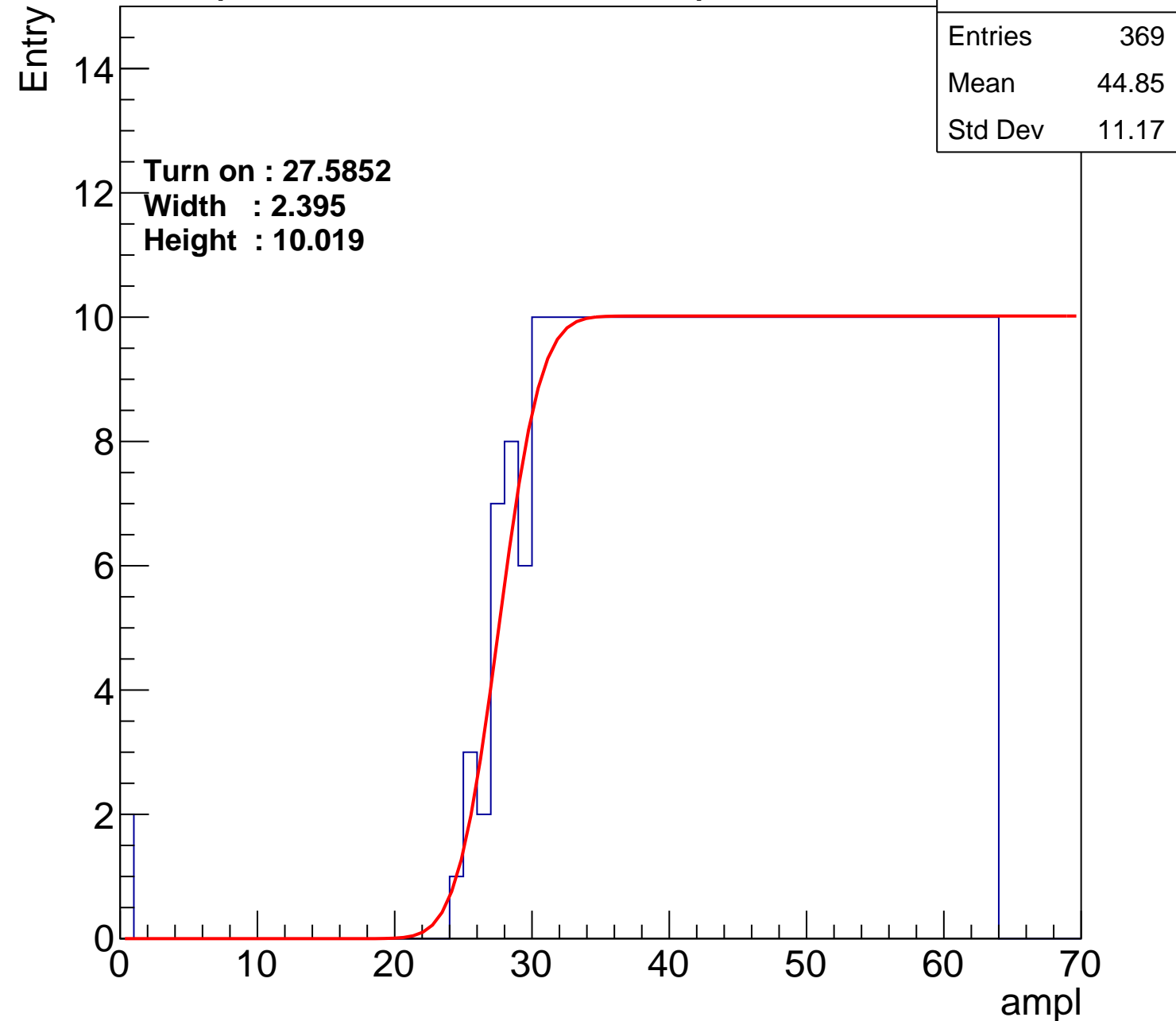
Width : 2.395

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch44

calib_packv5_042523_0143.root, FC#3, port B3

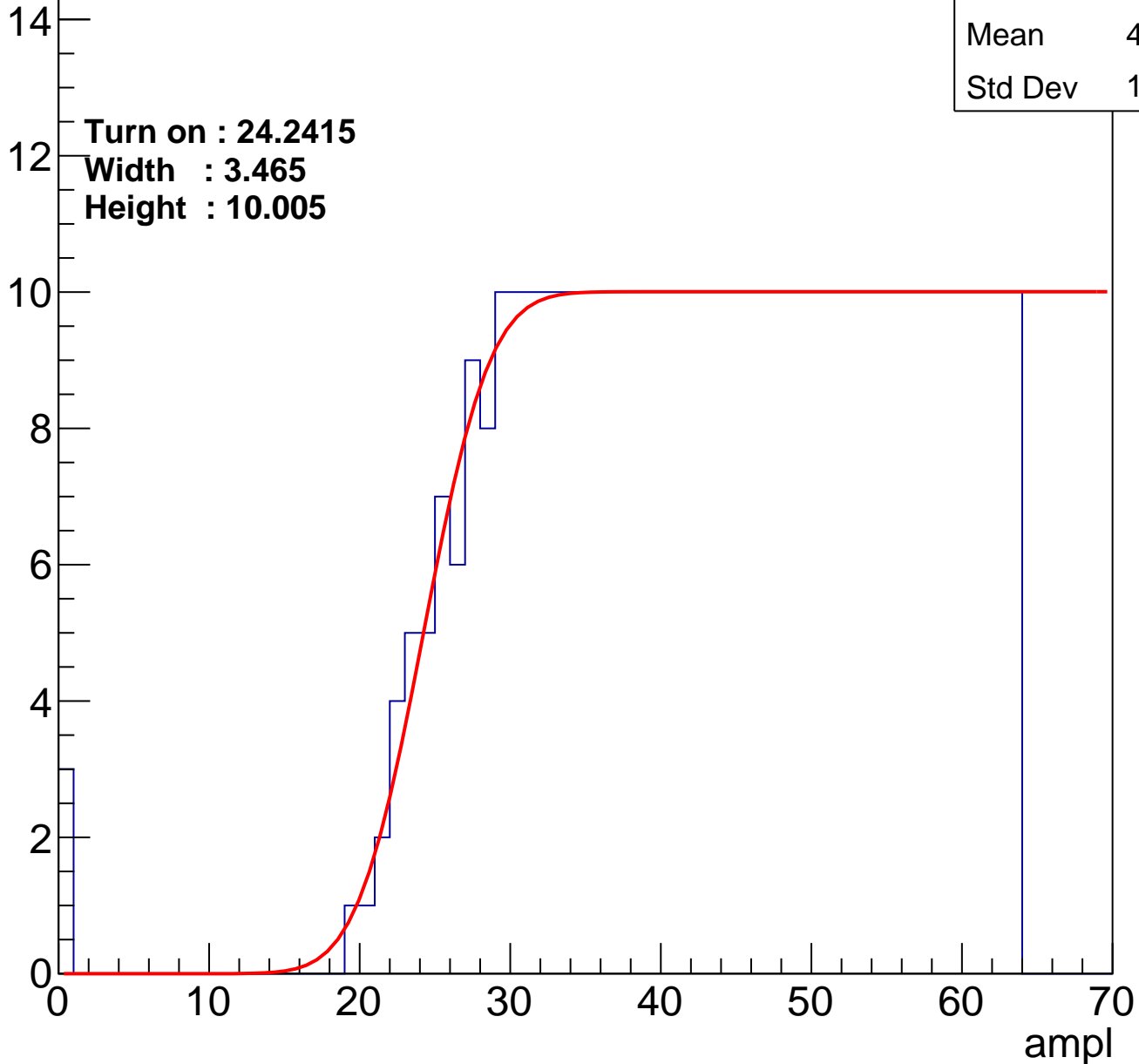
Entries	401
Mean	43.15
Std Dev	12.25

Turn on : 24.2415

Width : 3.465

Height : 10.005

Entry



B1L000S, U13-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.66
Std Dev	12.08

Turn on : 25.6135

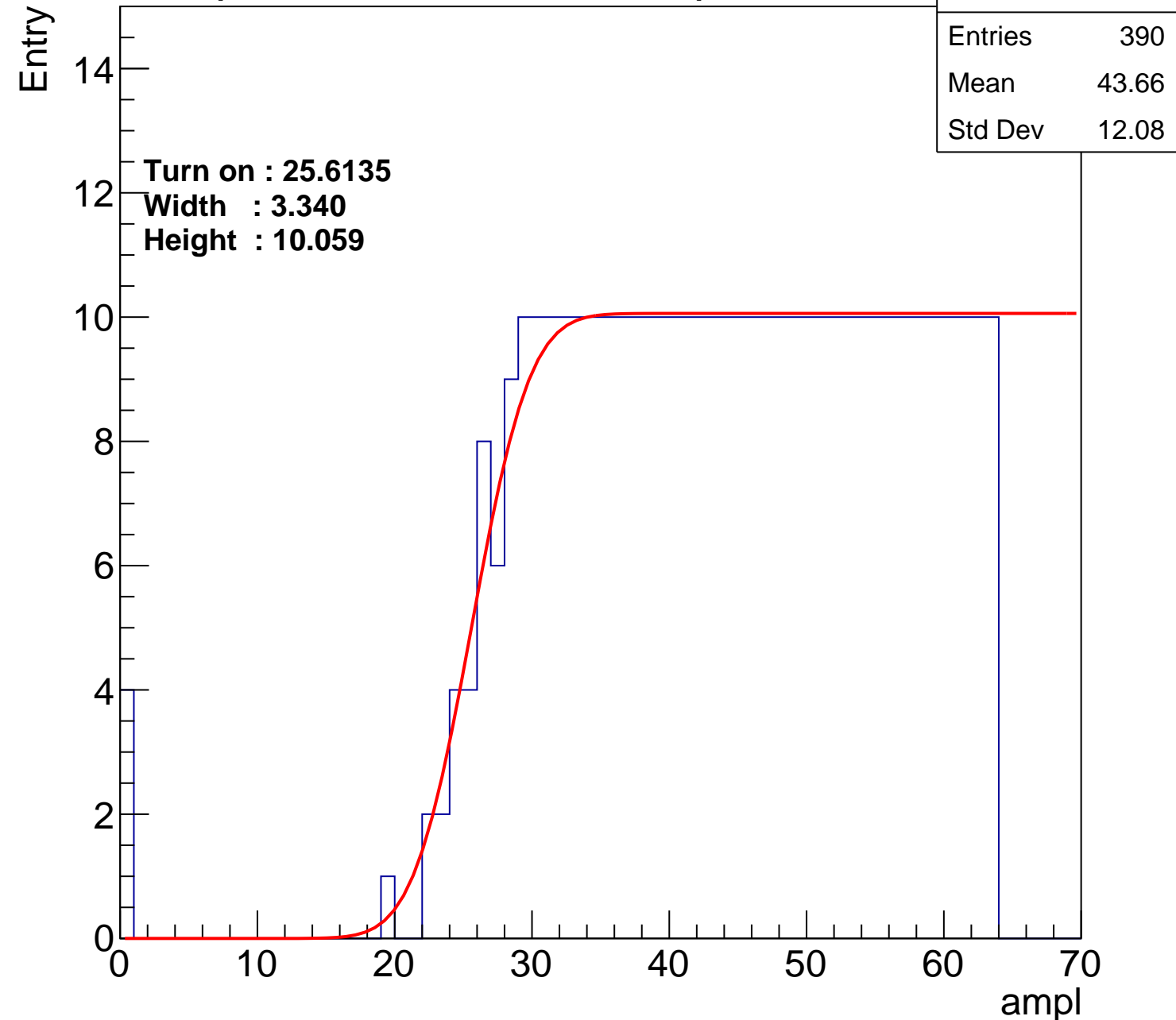
Width : 3.340

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch46

calib_packv5_042523_0143.root, FC#3, port B3

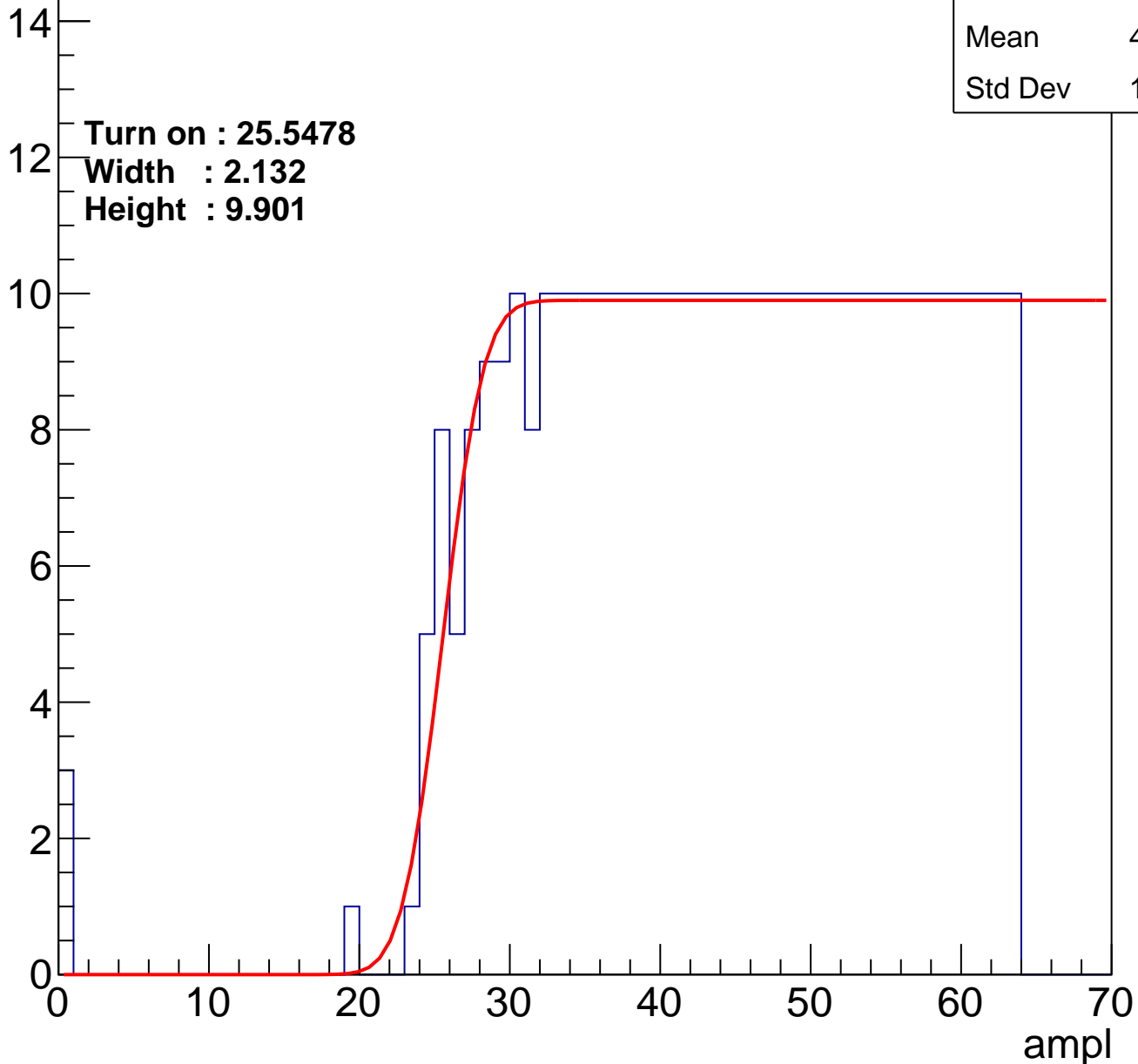
Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 25.5478

Width : 2.132

Height : 9.901

Entry



B1L000S, U13-ch47

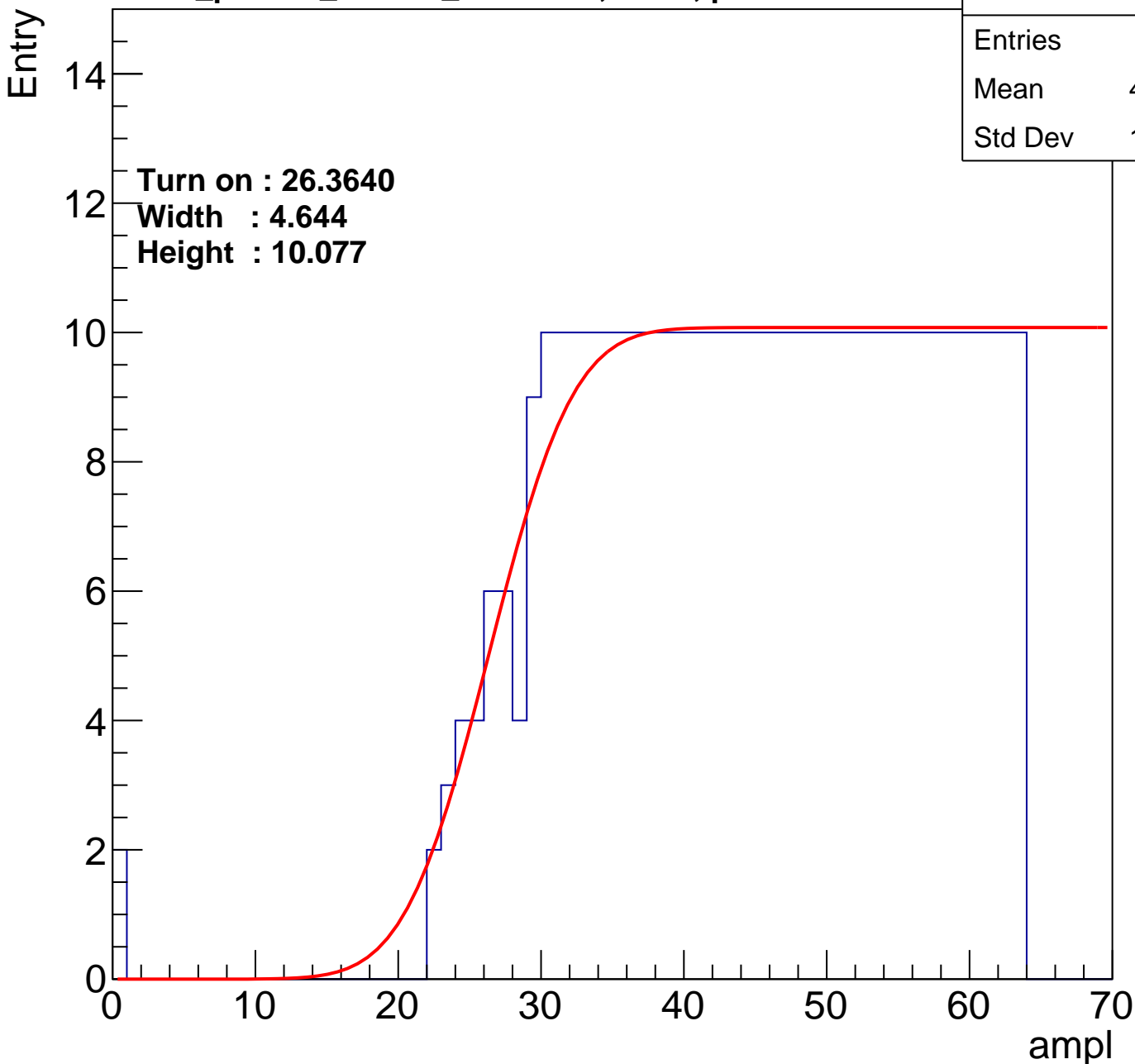
calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.24
Std Dev	11.56

Turn on : 26.3640

Width : 4.644

Height : 10.077



B1L000S, U13-ch48

calib_packv5_042523_0143.root, FC#3, port B3

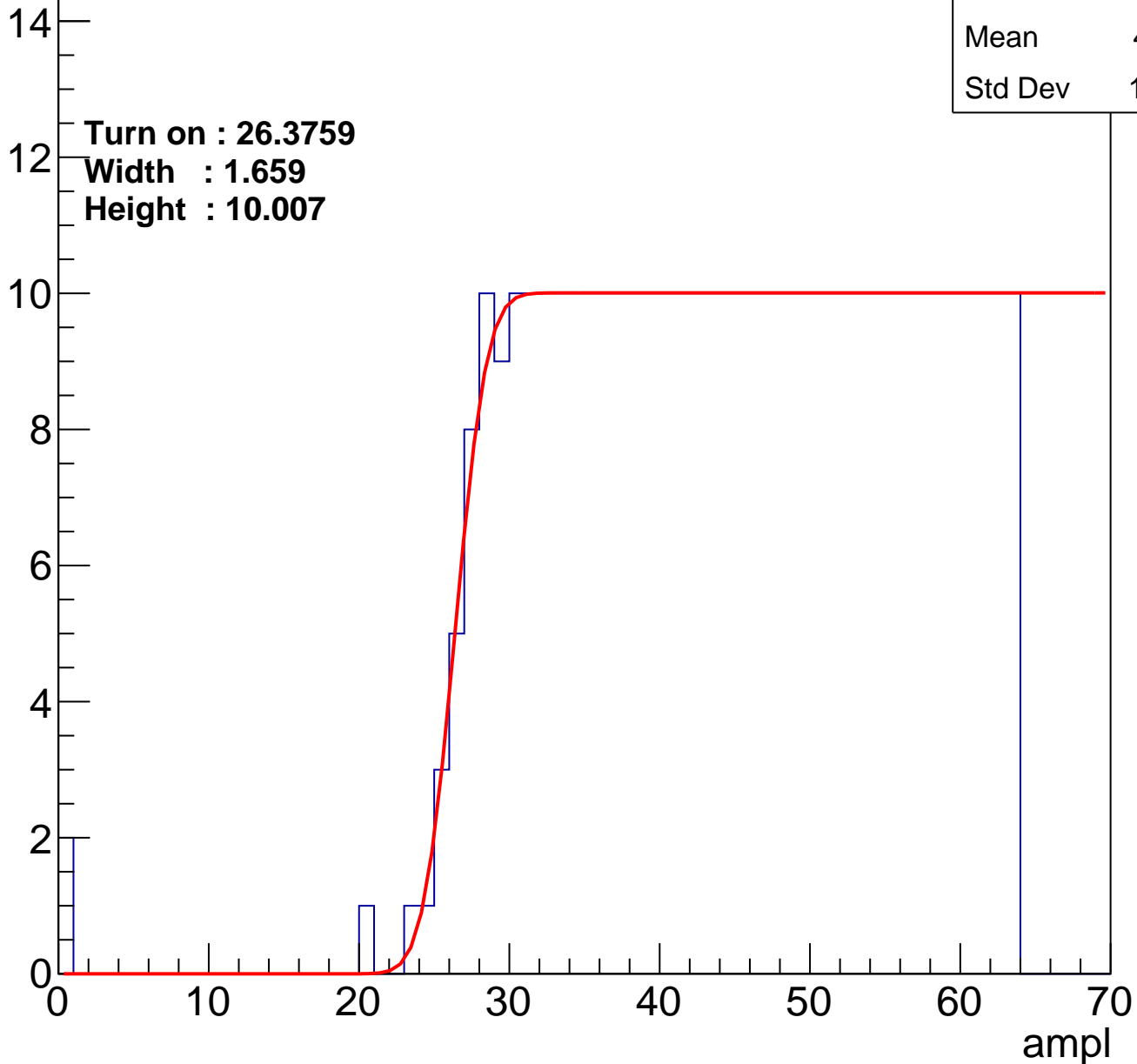
Entries	380
Mean	44.31
Std Dev	11.44

Turn on : 26.3759

Width : 1.659

Height : 10.007

Entry



B1L000S, U13-ch49

calib_packv5_042523_0143.root, FC#3, port B3

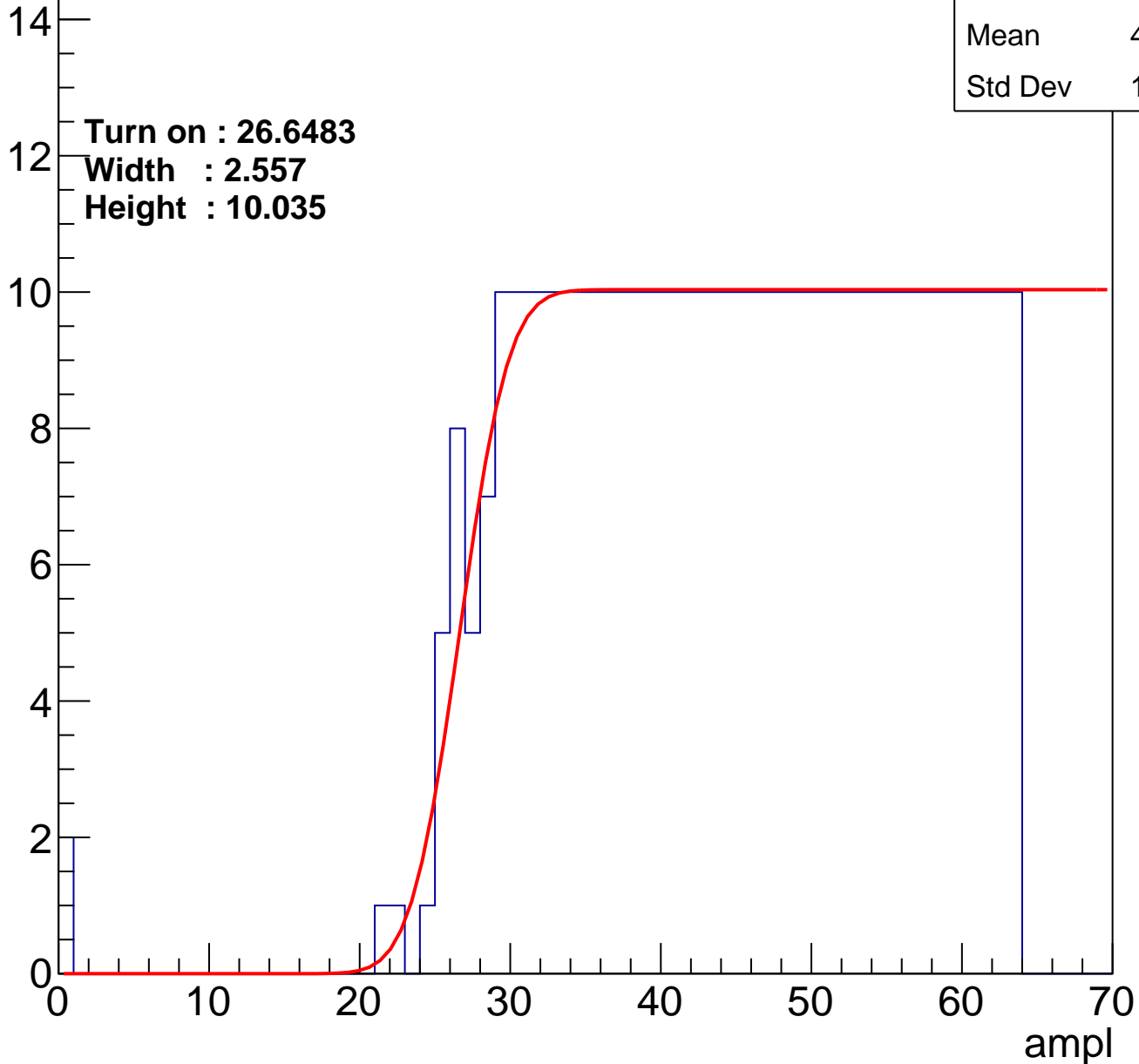
Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.6483

Width : 2.557

Height : 10.035

Entry



B1L000S, U13-ch50

calib_packv5_042523_0143.root, FC#3, port B3

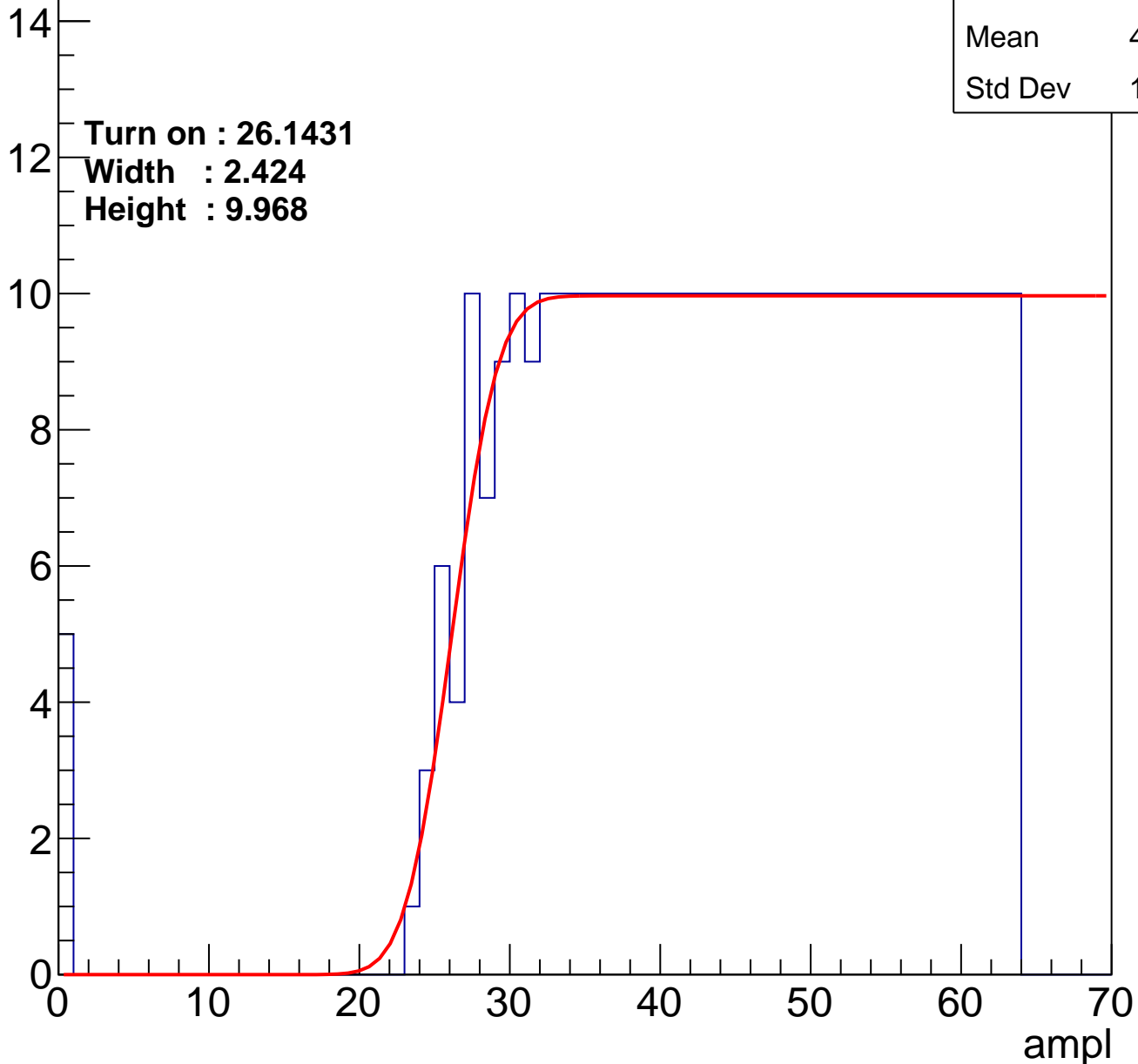
Entries	384
Mean	43.89
Std Dev	12.09

Turn on : 26.1431

Width : 2.424

Height : 9.968

Entry



B1L000S, U13-ch51

calib_packv5_042523_0143.root, FC#3, port B3

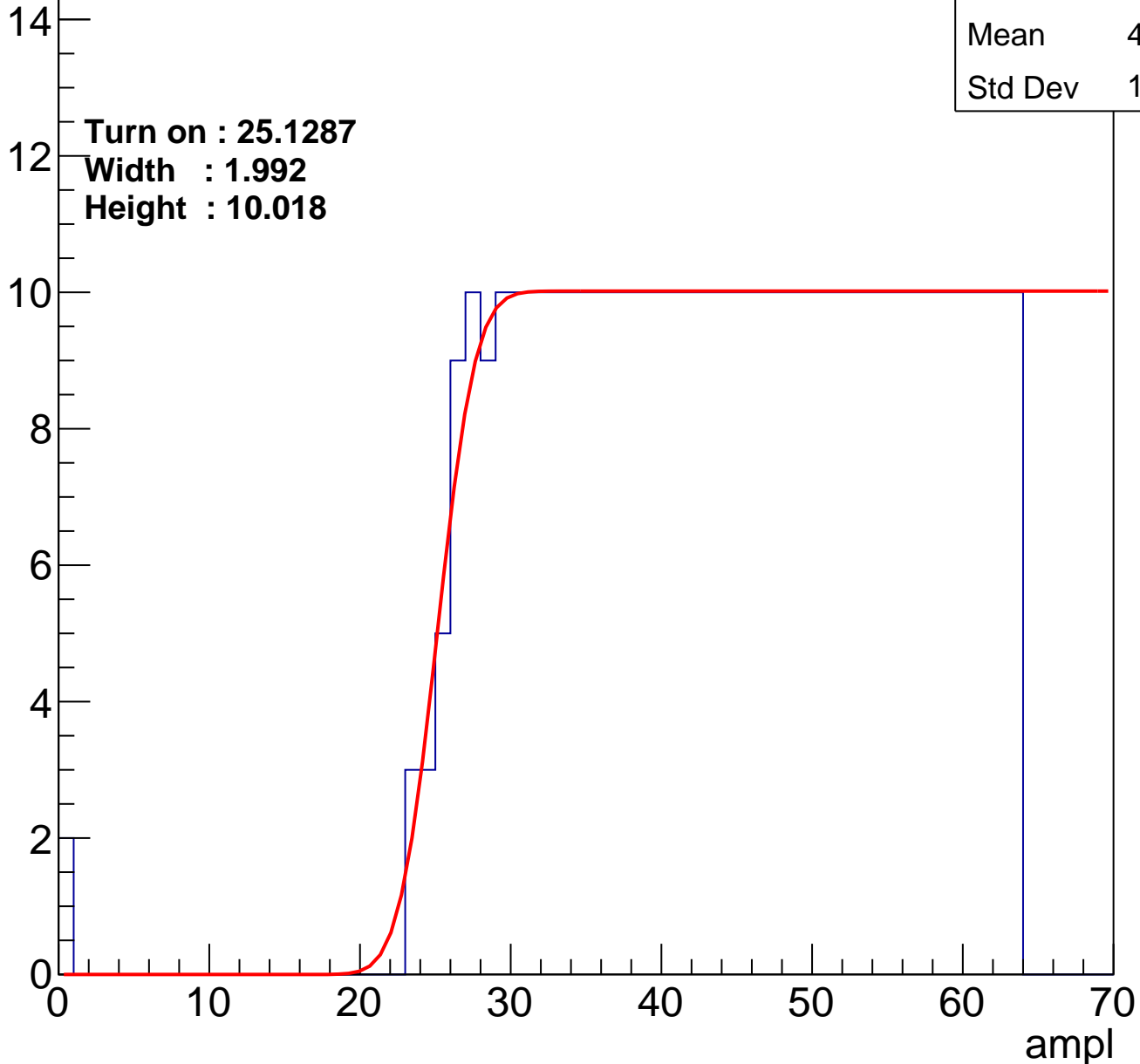
Entries	391
Mean	43.79
Std Dev	11.69

Turn on : 25.1287

Width : 1.992

Height : 10.018

Entry



B1L000S, U13-ch52

calib_packv5_042523_0143.root, FC#3, port B3

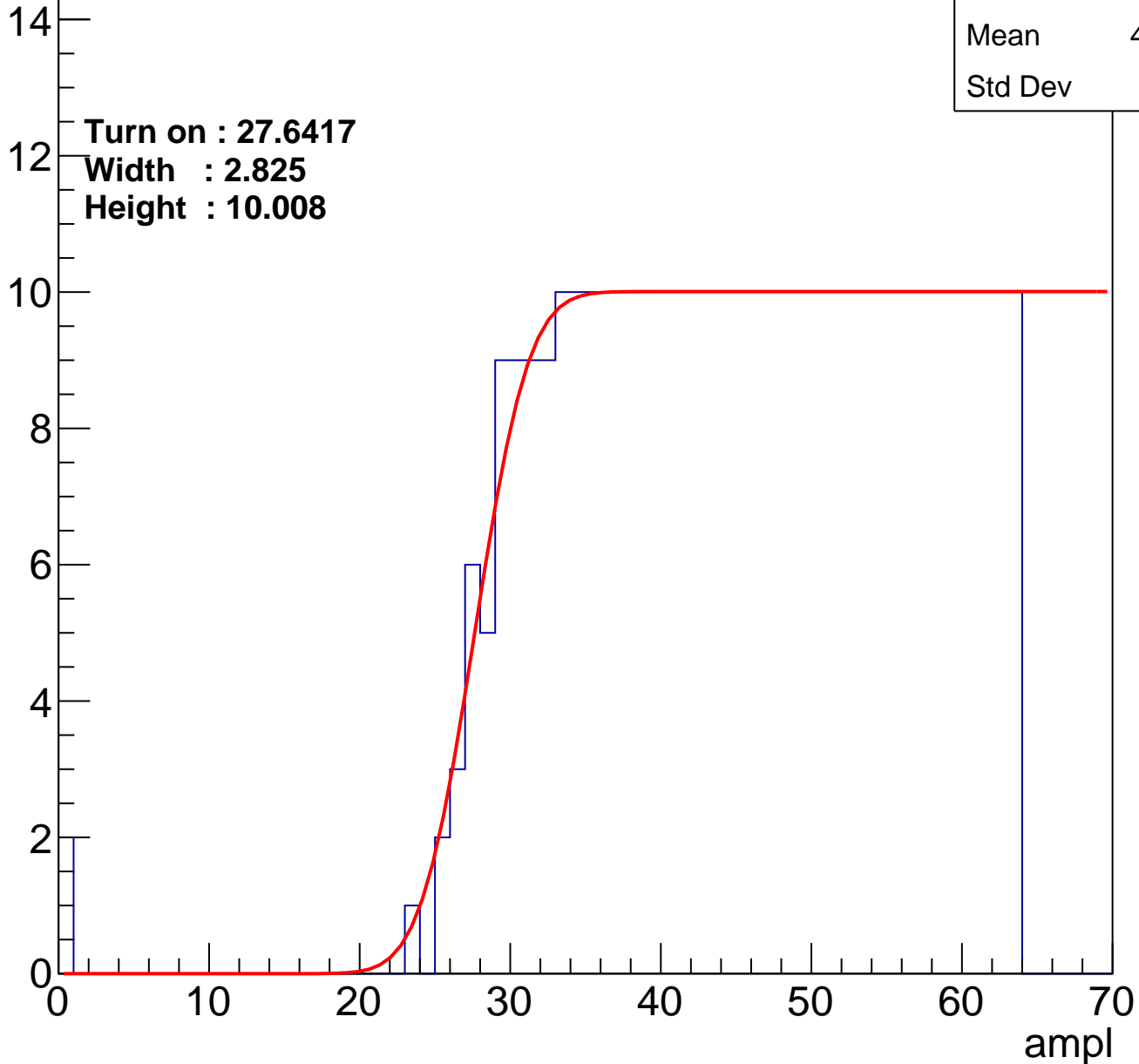
Entries	365
Mean	45.02
Std Dev	11.1

Turn on : 27.6417

Width : 2.825

Height : 10.008

Entry



B1L000S, U13-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.59
Std Dev	11.18

Turn on : 27.2913

Width : 2.877

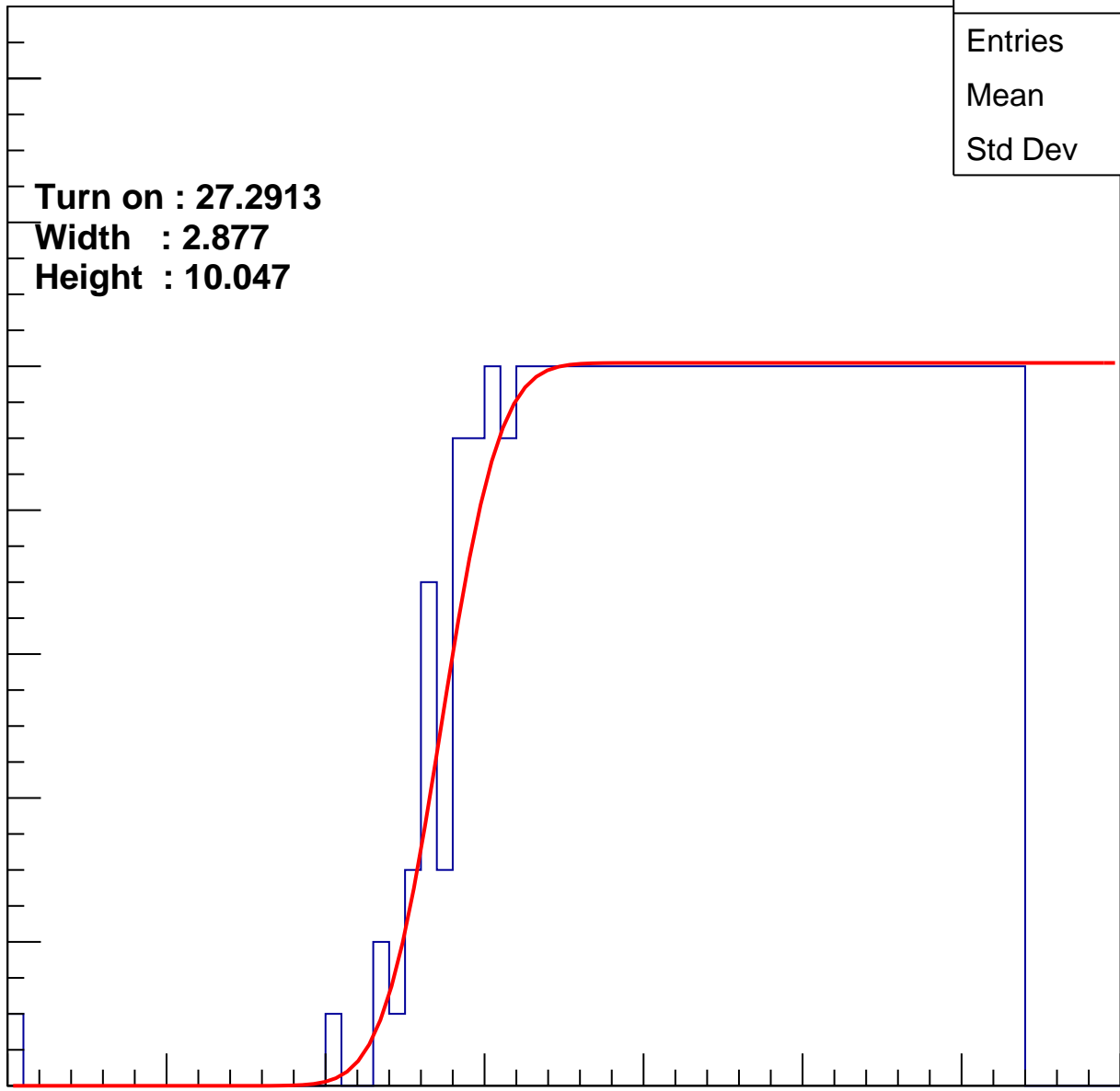
Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U13-ch54

calib_packv5_042523_0143.root, FC#3, port B3

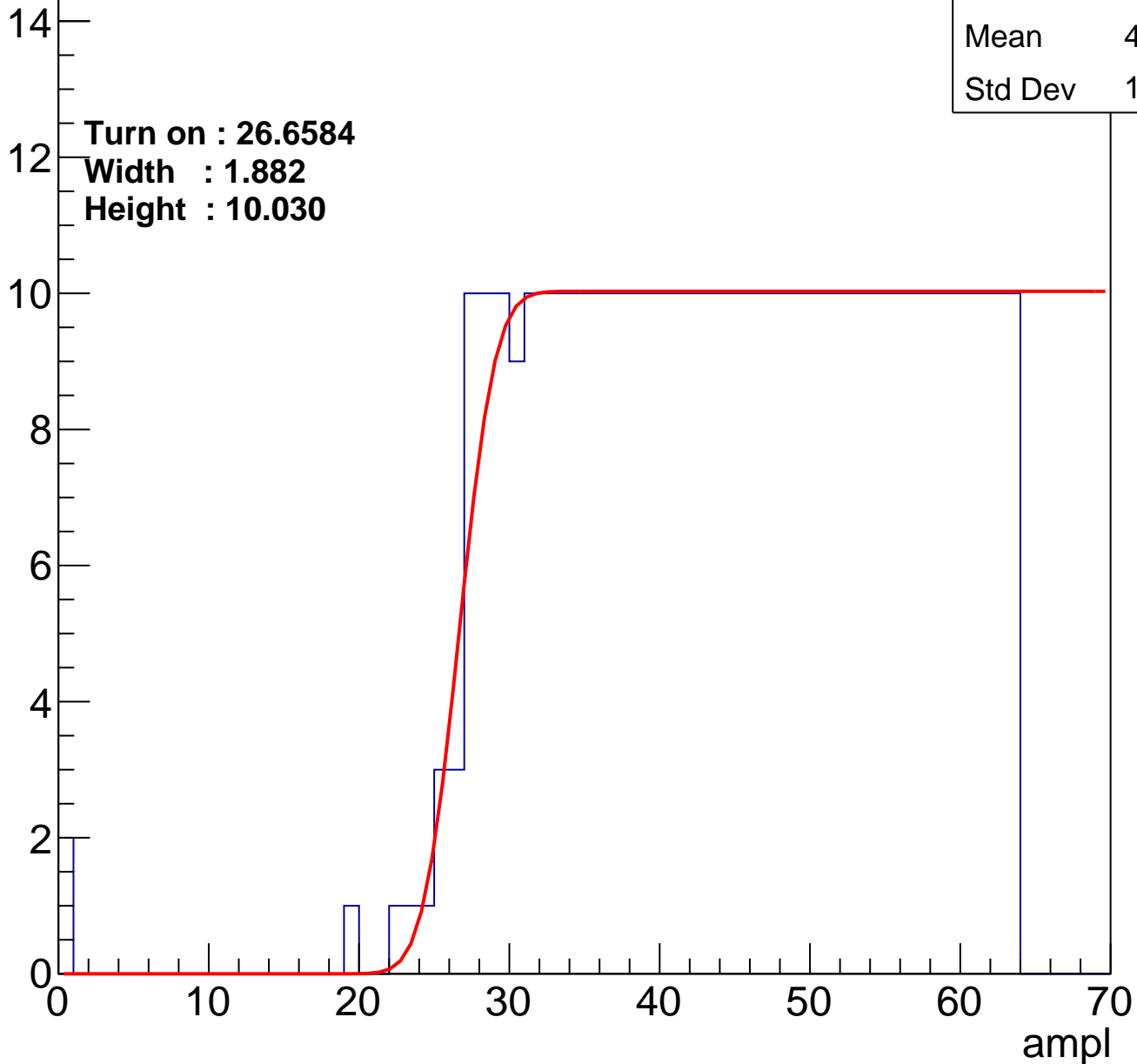
Entries	381
Mean	44.25
Std Dev	11.48

Turn on : 26.6584

Width : 1.882

Height : 10.030

Entry



B1L000S, U13-ch55

calib_packv5_042523_0143.root, FC#3, port B3

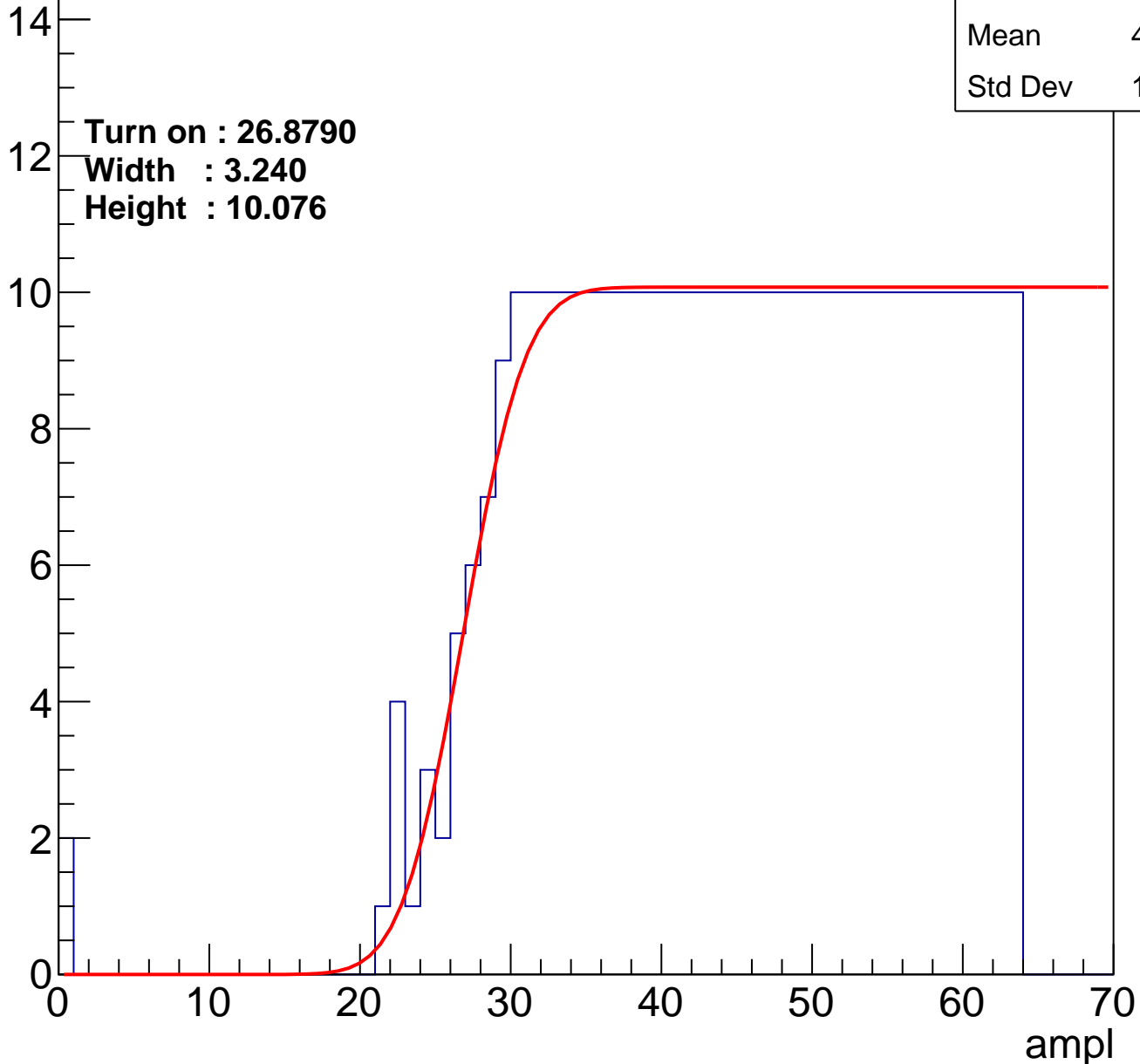
Entries	380
Mean	44.24
Std Dev	11.55

Turn on : 26.8790

Width : 3.240

Height : 10.076

Entry



B1L000S, U13-ch56

calib_packv5_042523_0143.root, FC#3, port B3

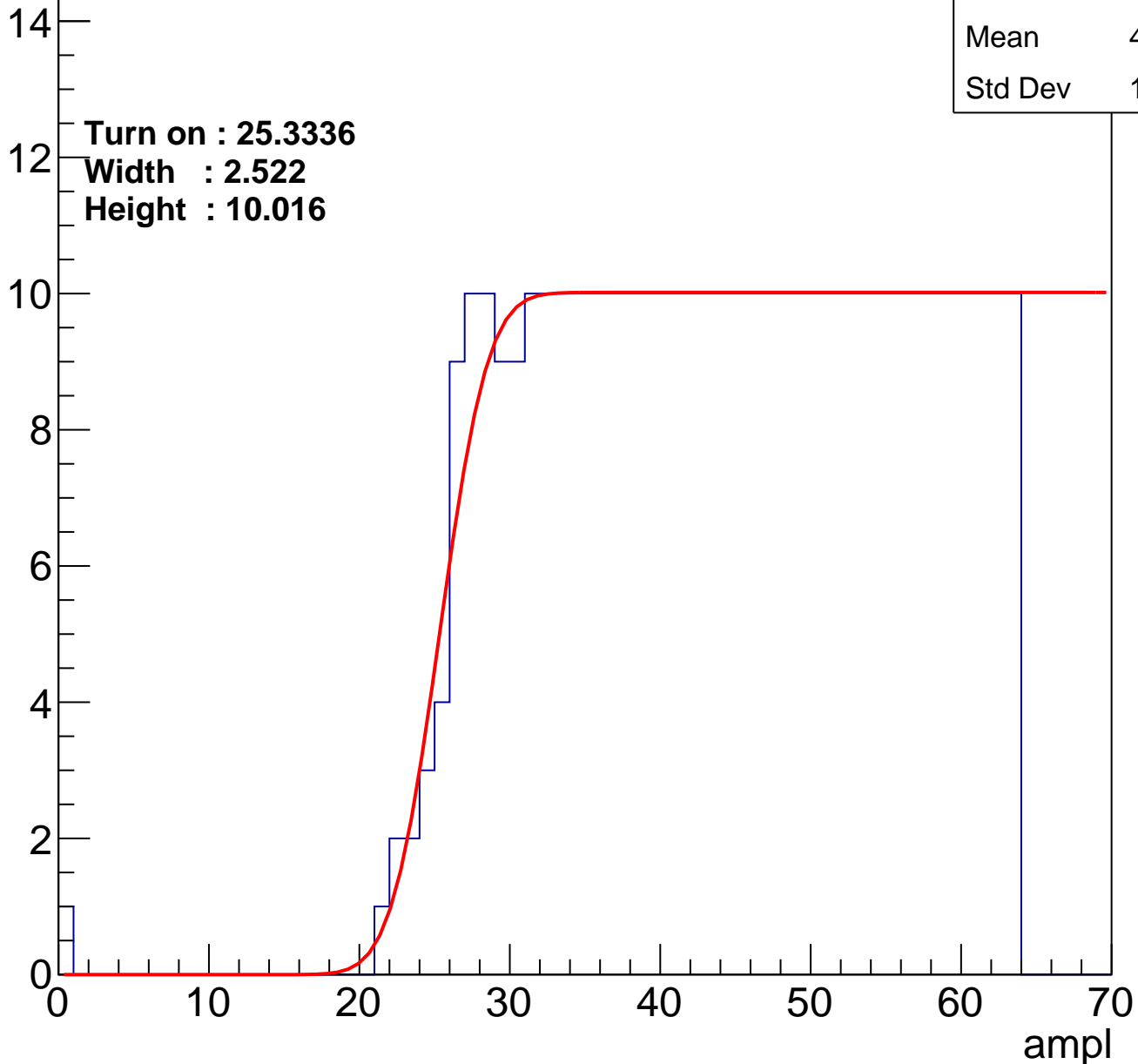
Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 25.3336

Width : 2.522

Height : 10.016

Entry



B1L000S, U13-ch57

calib_packv5_042523_0143.root, FC#3, port B3

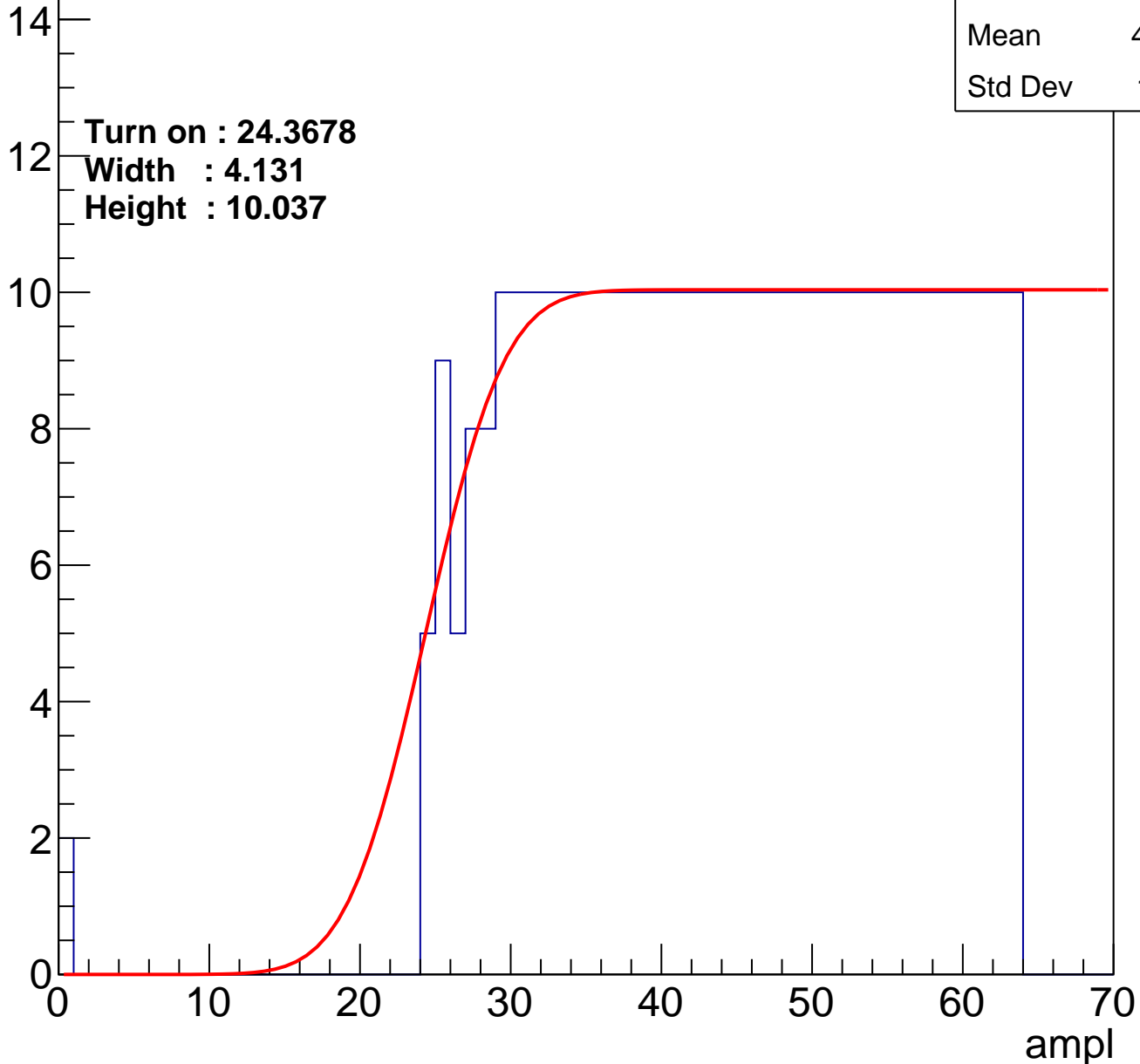
Entries	387
Mean	43.97
Std Dev	11.61

Turn on : 24.3678

Width : 4.131

Height : 10.037

Entry



B1L000S, U13-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.0723

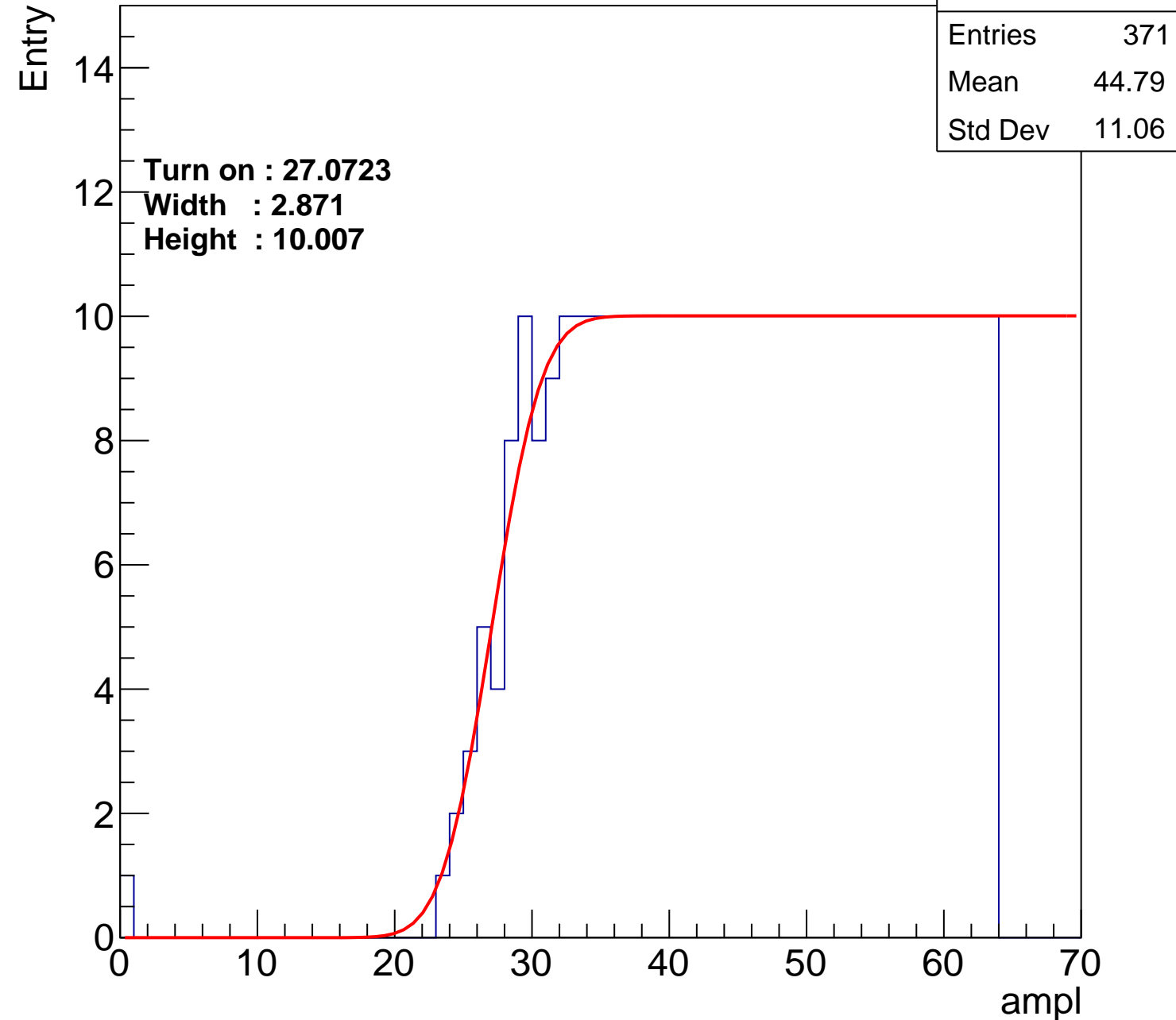
Width : 2.871

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.2
Std Dev	11.82

Turn on : 26.7131

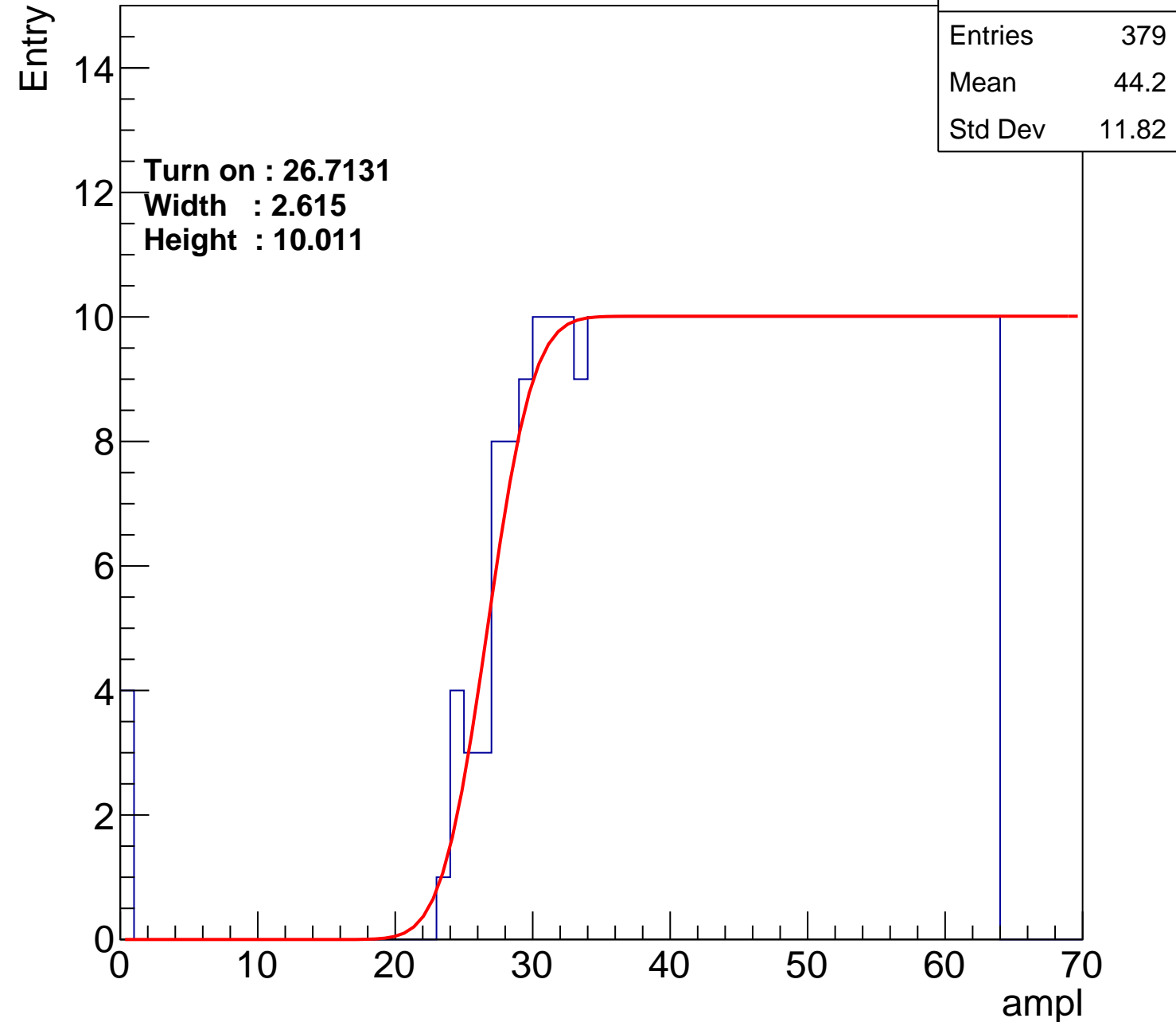
Width : 2.615

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch60

calib_packv5_042523_0143.root, FC#3, port B3

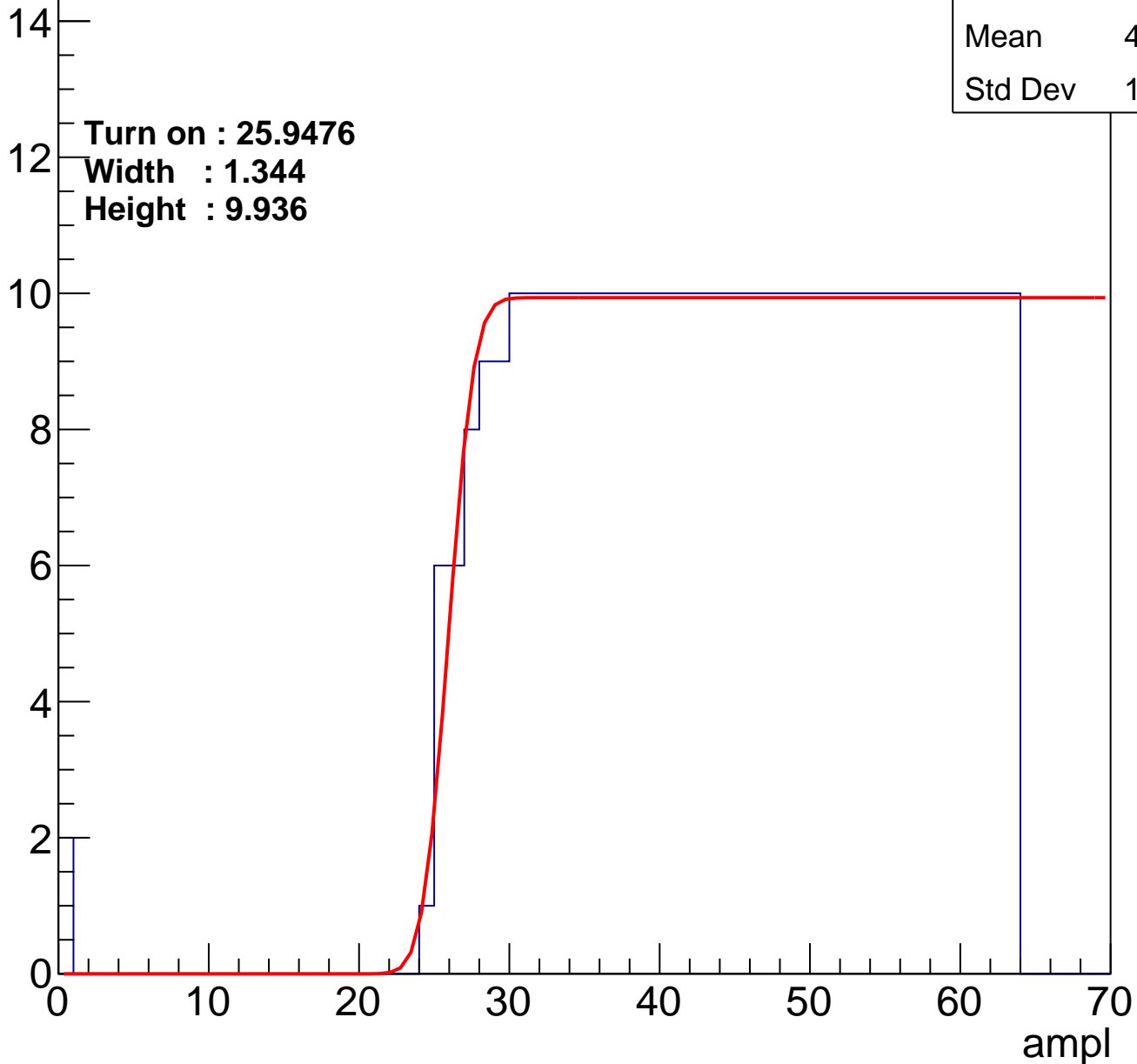
Entries	381
Mean	44.28
Std Dev	11.44

Turn on : 25.9476

Width : 1.344

Height : 9.936

Entry



B1L000S, U13-ch61

calib_packv5_042523_0143.root, FC#3, port B3

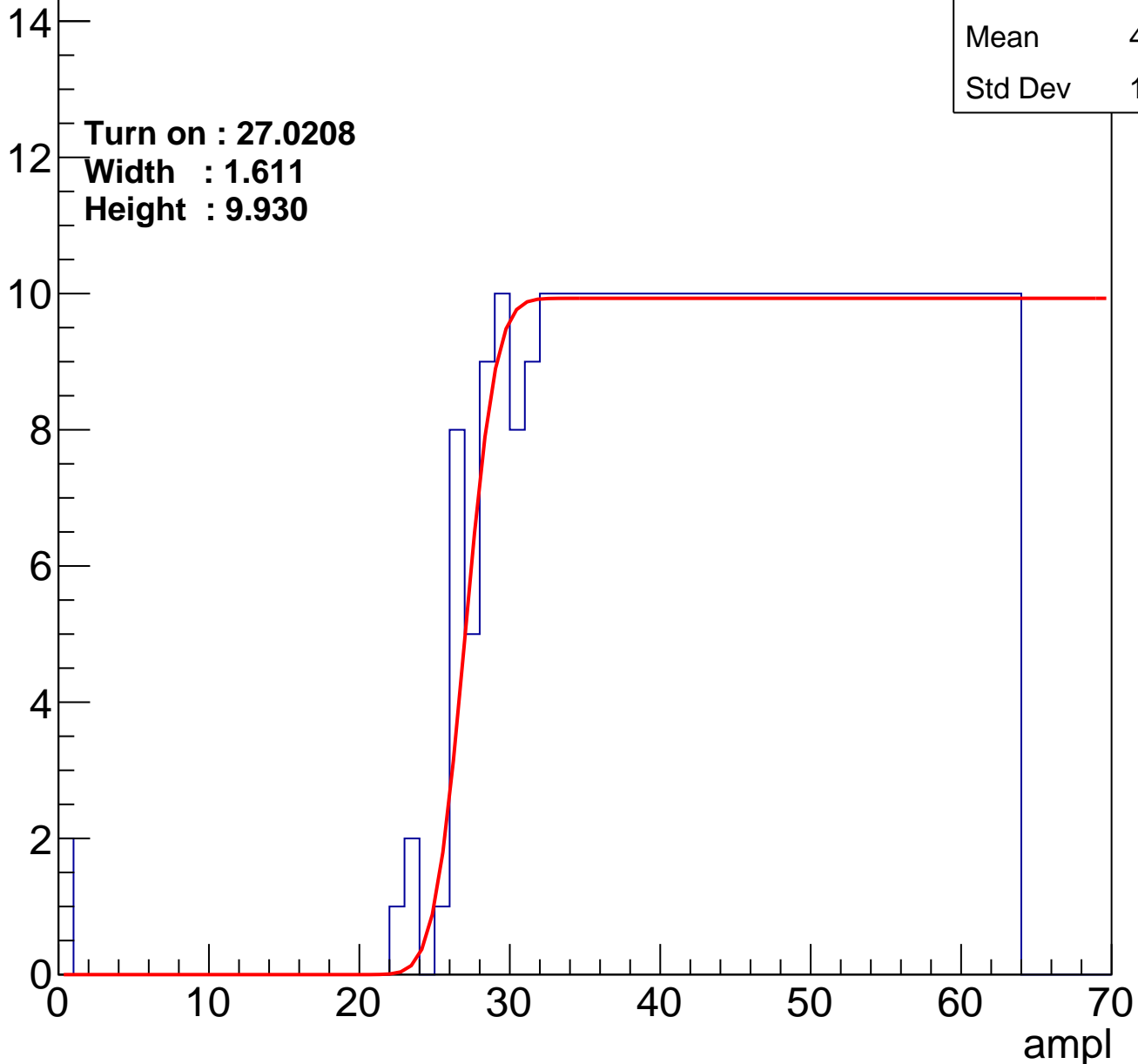
Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 27.0208

Width : 1.611

Height : 9.930

Entry



B1L000S, U13-ch62

calib_packv5_042523_0143.root, FC#3, port B3

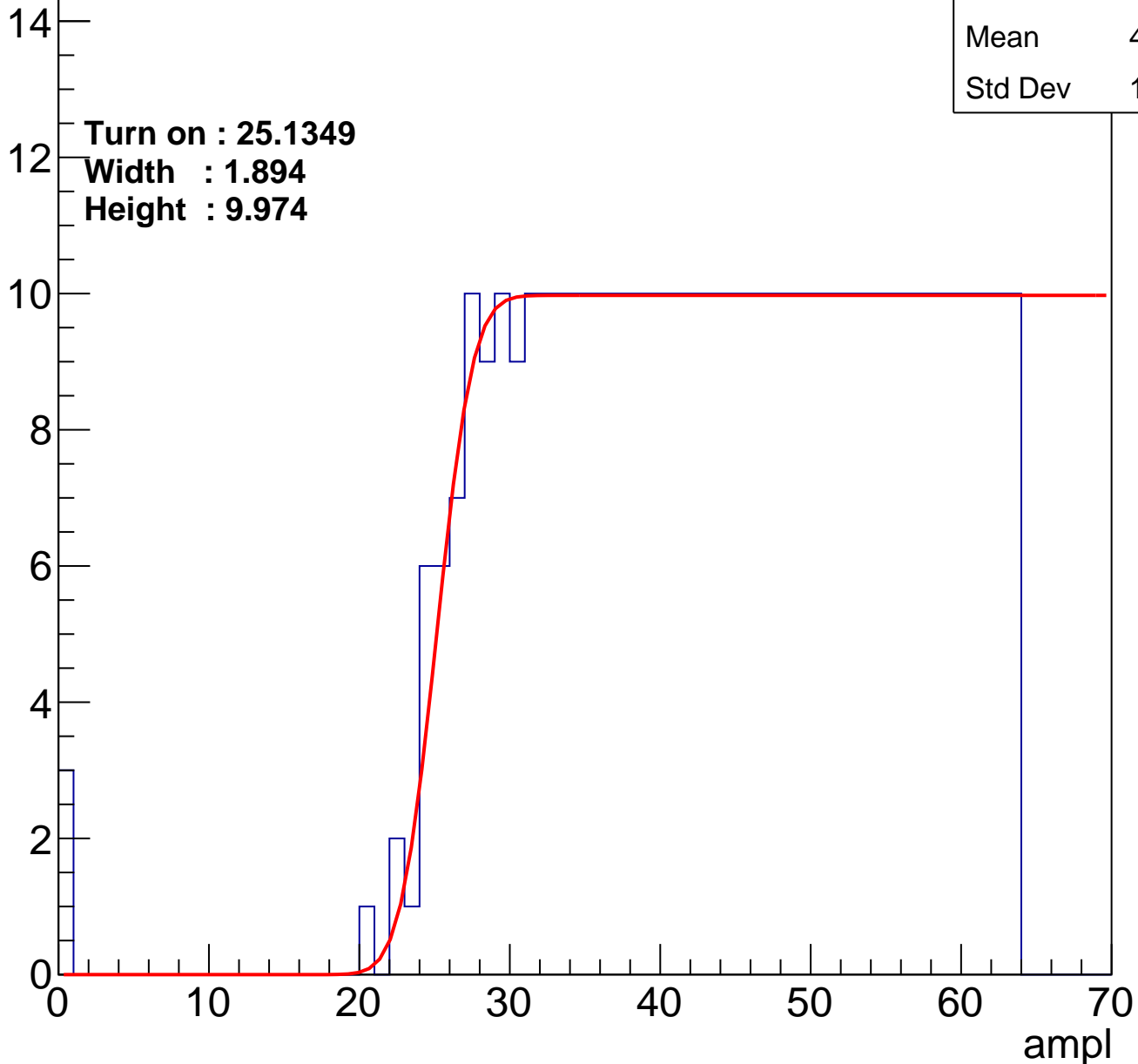
Entries	394
Mean	43.54
Std Dev	11.99

Turn on : 25.1349

Width : 1.894

Height : 9.974

Entry



B1L000S, U13-ch63

calib_packv5_042523_0143.root, FC#3, port B3

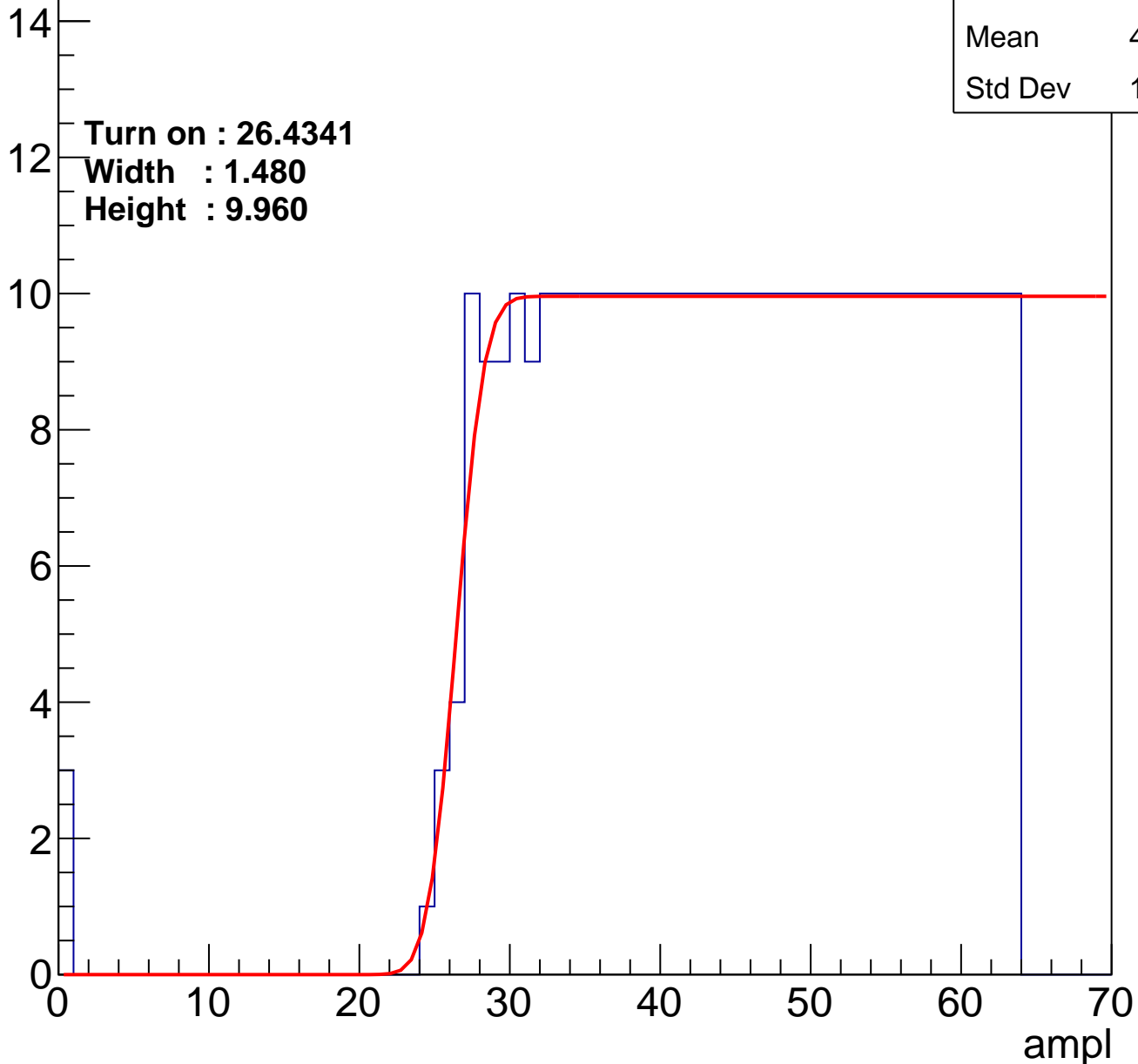
Entries	378
Mean	44.35
Std Dev	11.55

Turn on : 26.4341

Width : 1.480

Height : 9.960

Entry



B1L000S, U13-ch64

calib_packv5_042523_0143.root, FC#3, port B3

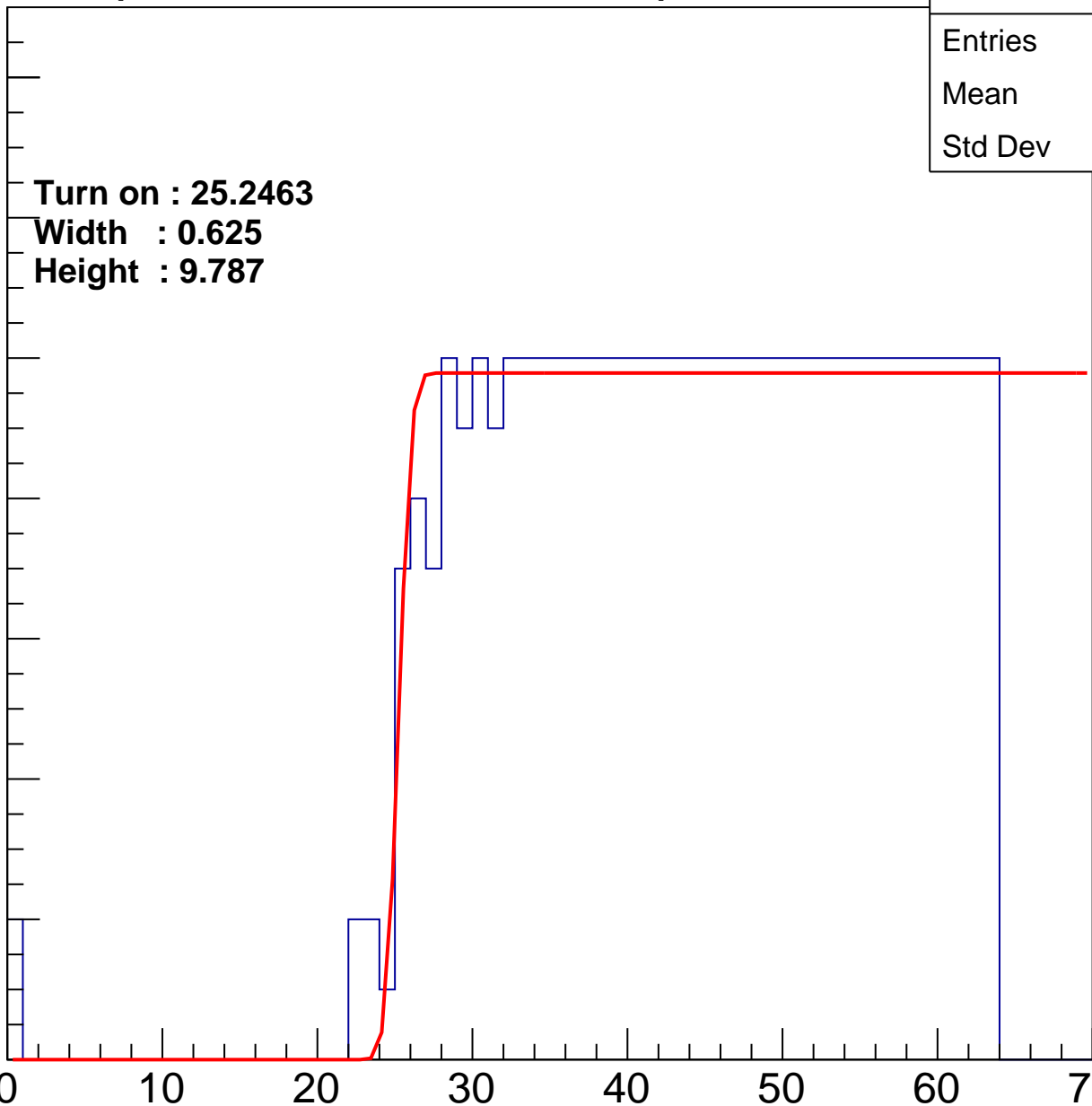
Entry

14
12
10
8
6
4
2
0

Turn on : 25.2463
Width : 0.625
Height : 9.787

Entries	387
Mean	43.94
Std Dev	11.65

ampl



B1L000S, U13-ch65

calib_packv5_042523_0143.root, FC#3, port B3

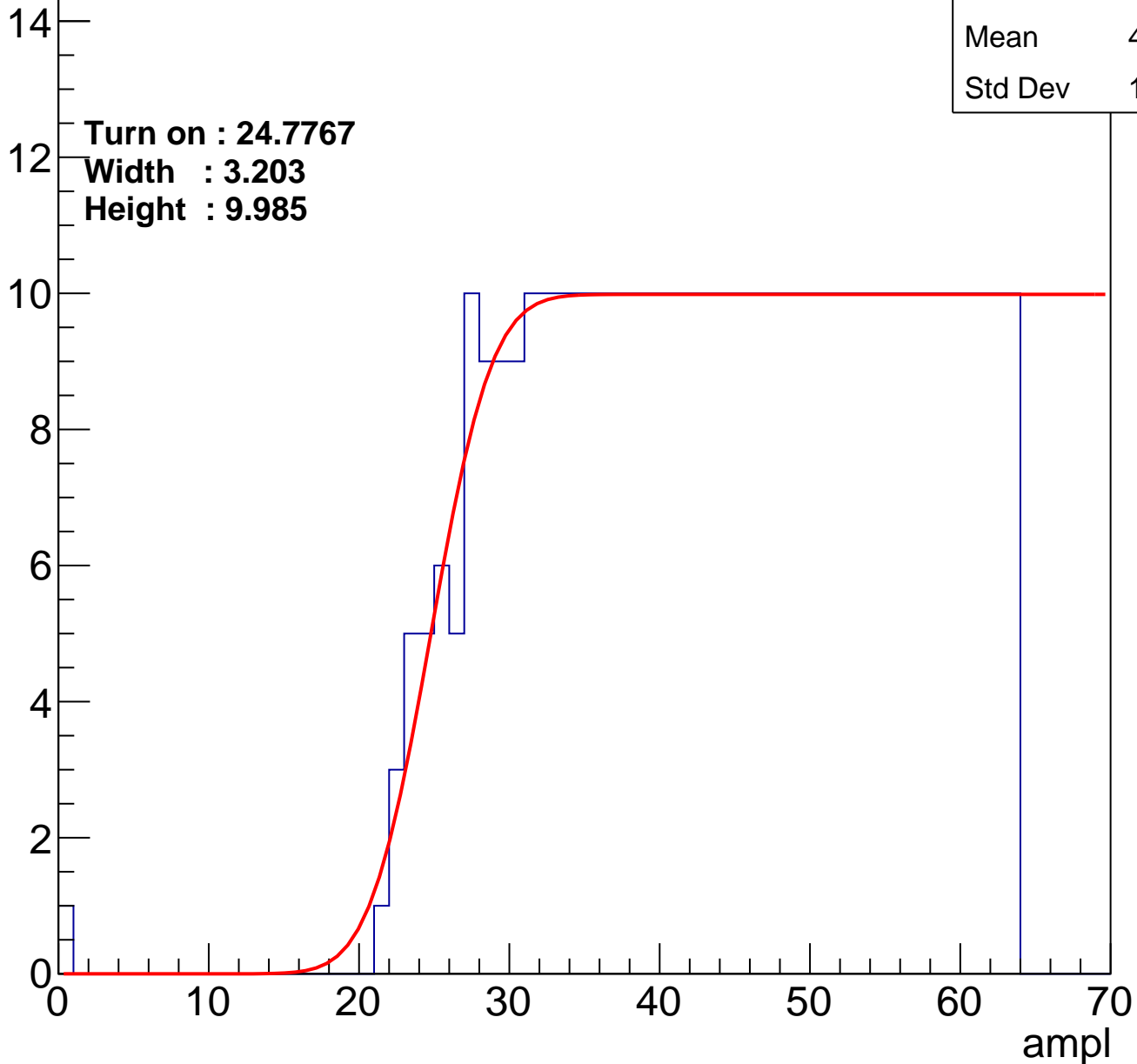
Entries	393
Mean	43.68
Std Dev	11.69

Turn on : 24.7767

Width : 3.203

Height : 9.985

Entry



B1L000S, U13-ch66

calib_packv5_042523_0143.root, FC#3, port B3

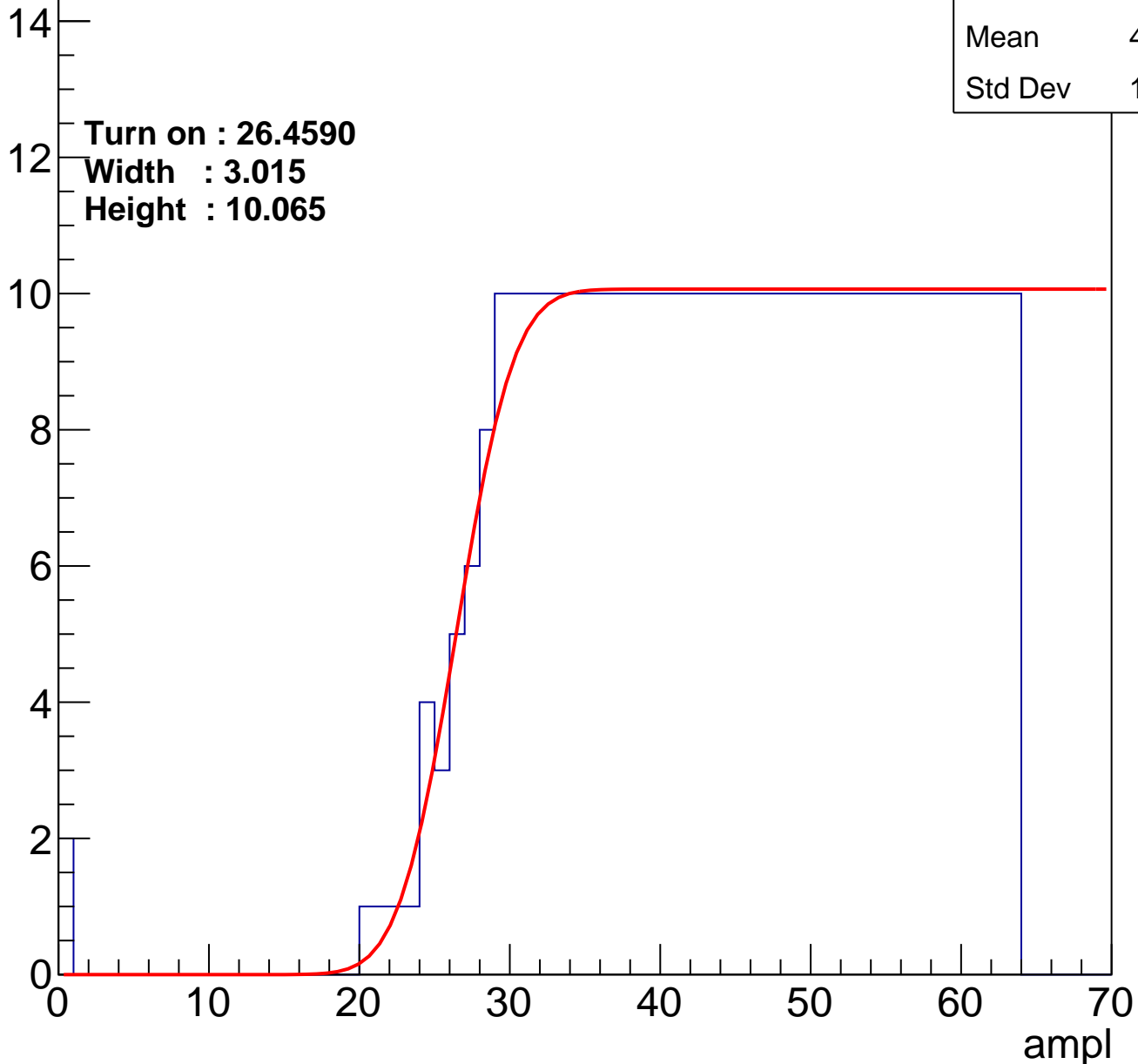
Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.4590

Width : 3.015

Height : 10.065

Entry



B1L000S, U13-ch67

calib_packv5_042523_0143.root, FC#3, port B3

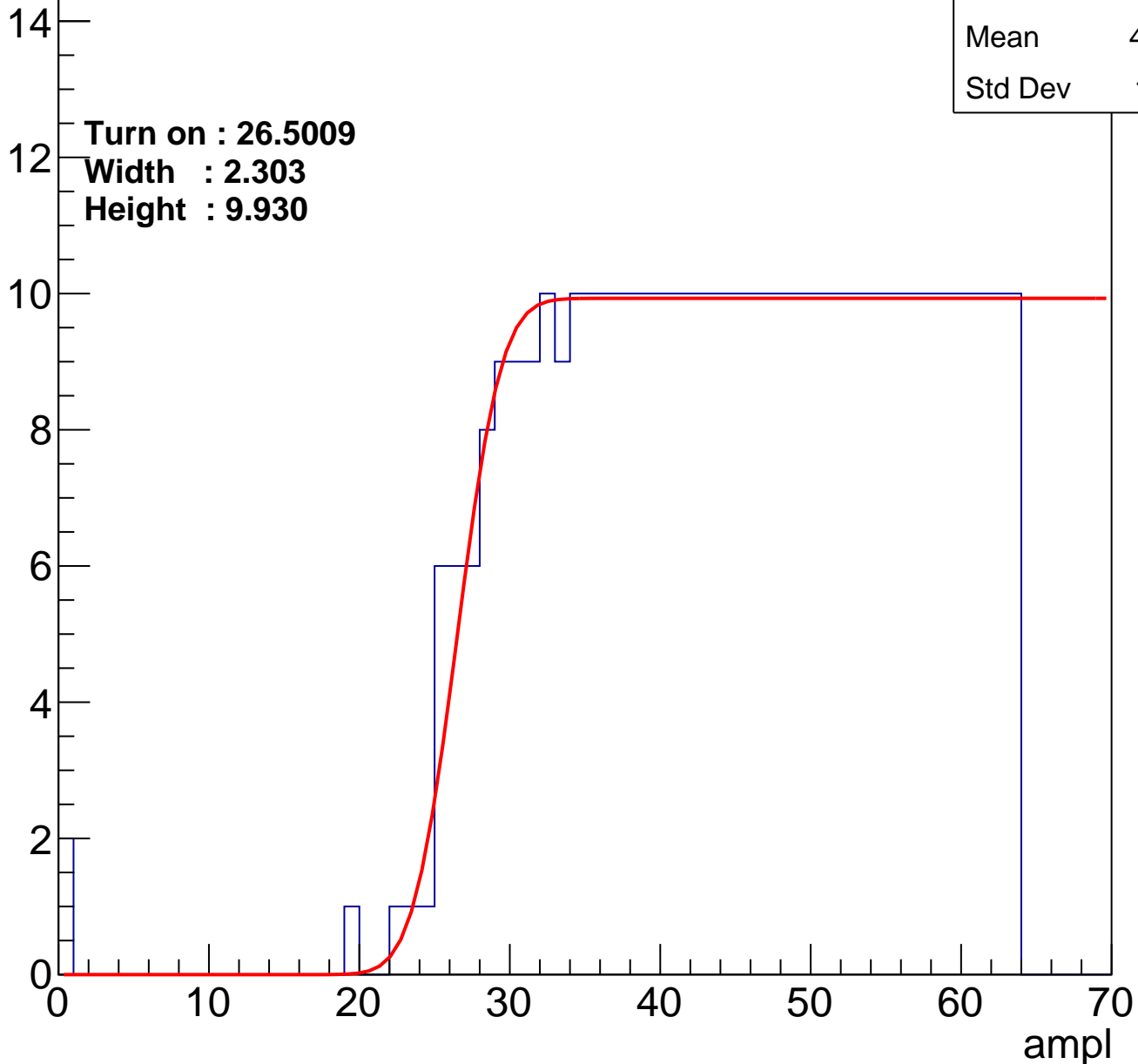
Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 26.5009

Width : 2.303

Height : 9.930

Entry



B1L000S, U13-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.2130

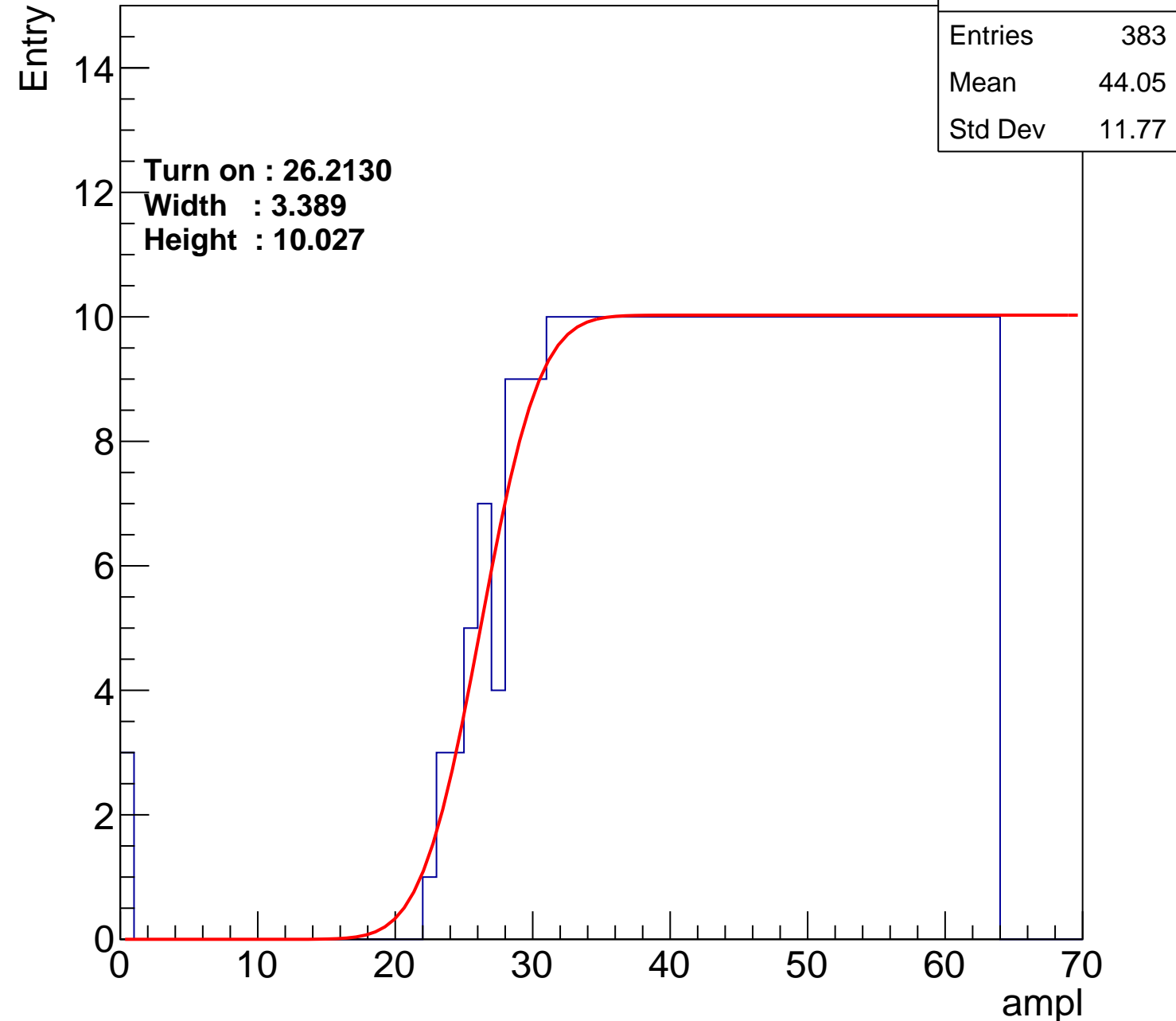
Width : 3.389

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 24.8948
Width : 2.006
Height : 10.021



B1L000S, U13-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.8
Std Dev	12.02

Turn on : 26.1528

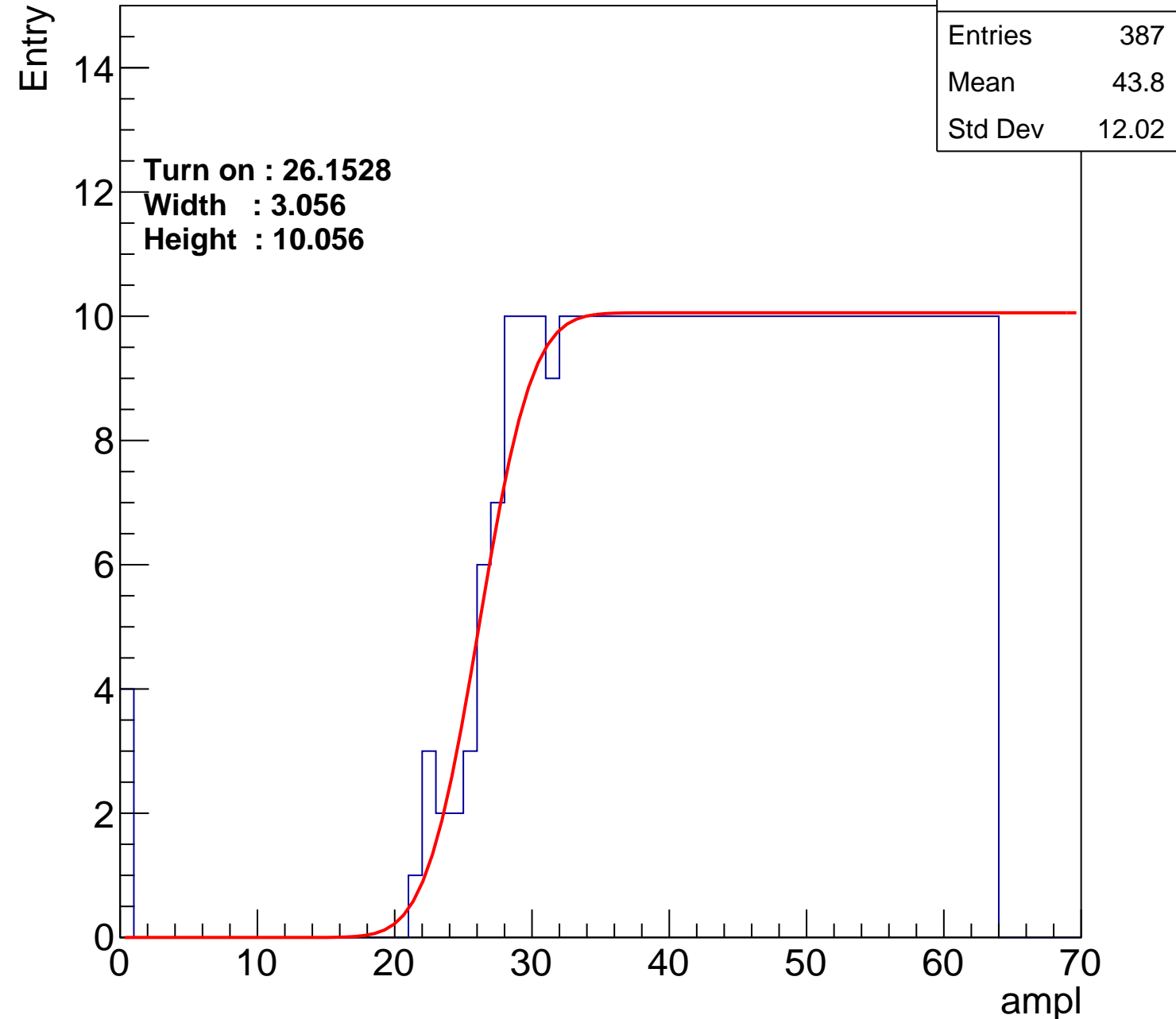
Width : 3.056

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 26.8903

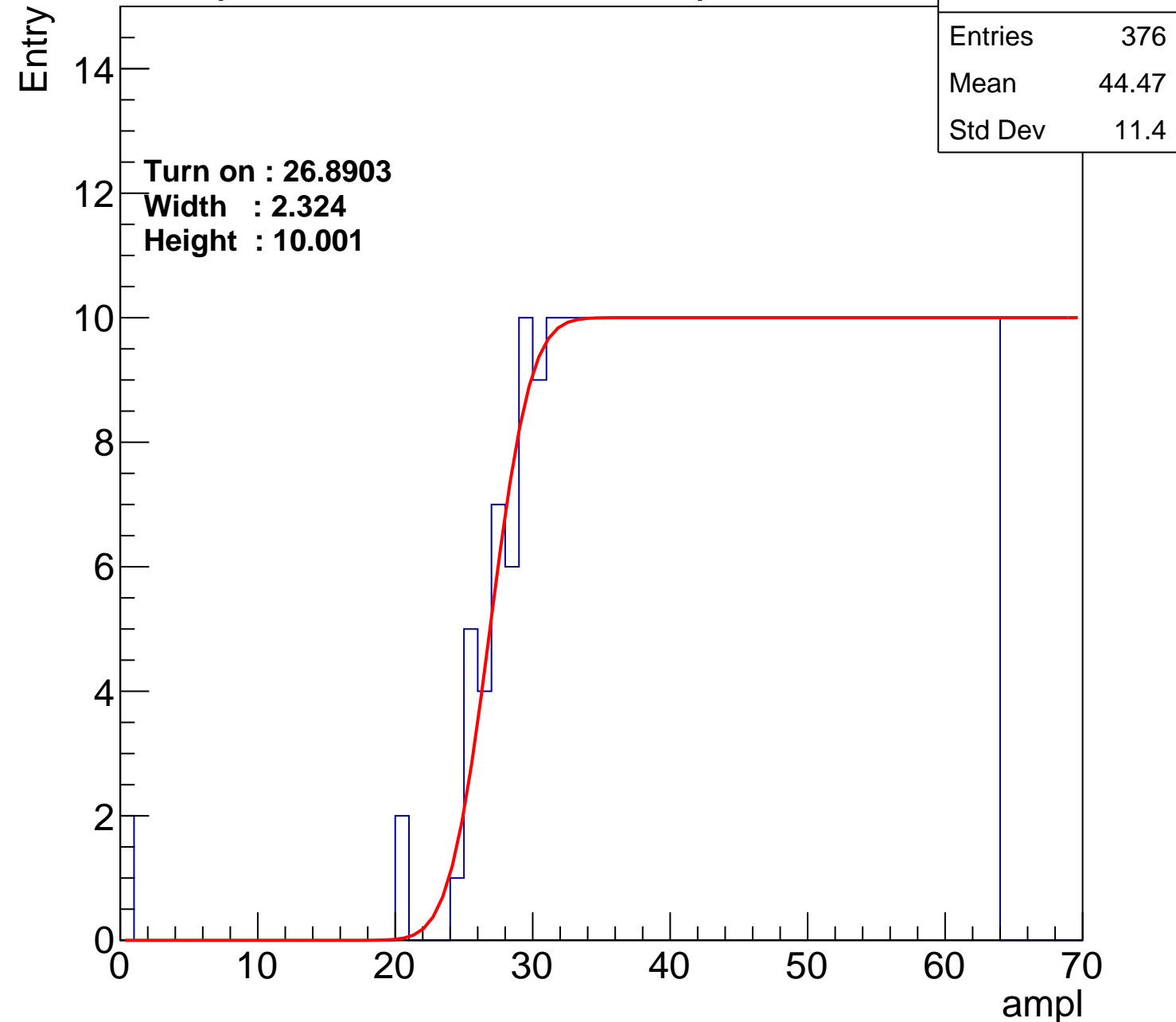
Width : 2.324

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.39
Std Dev	11.77

Turn on : 26.7866

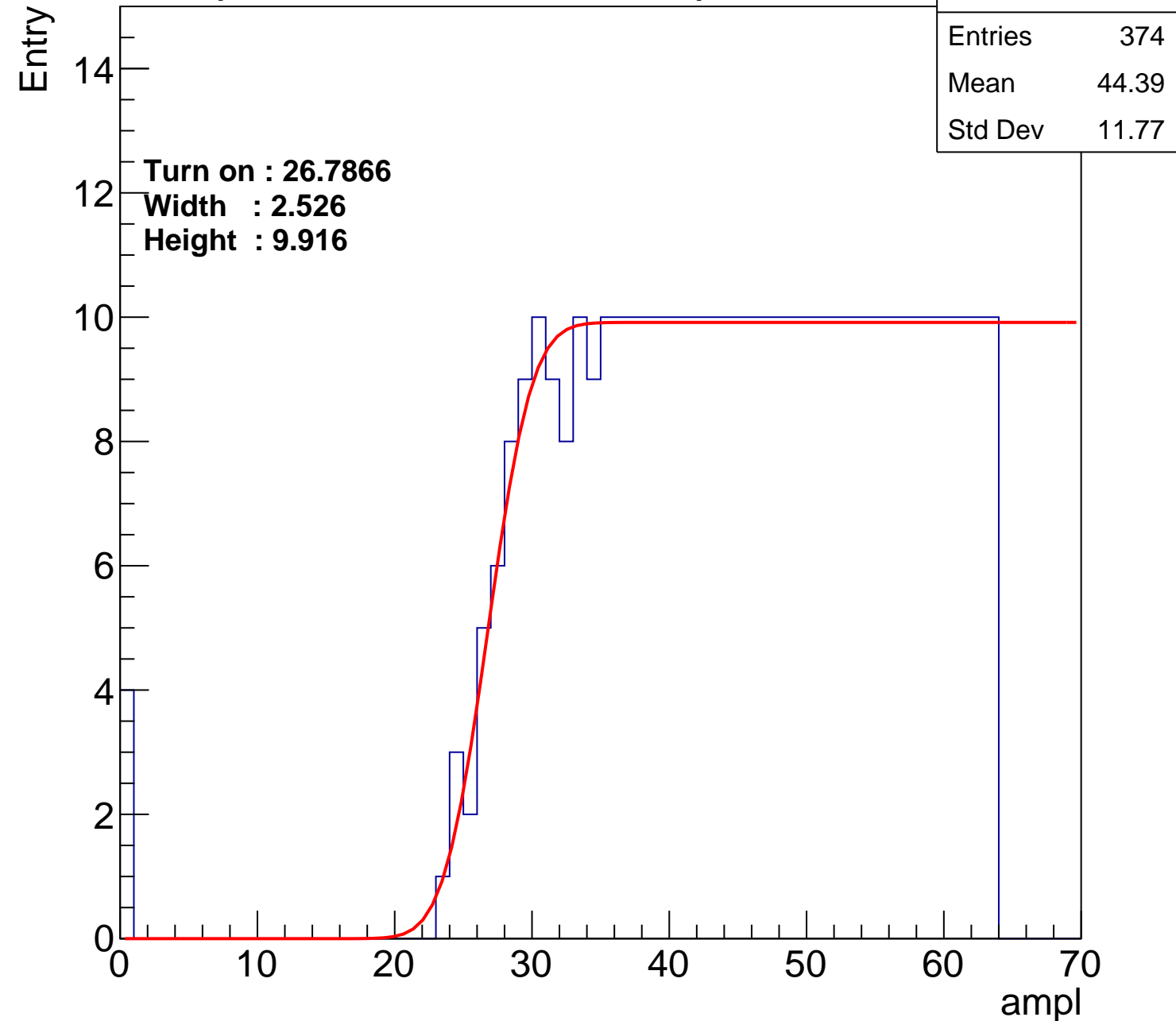
Width : 2.526

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch73

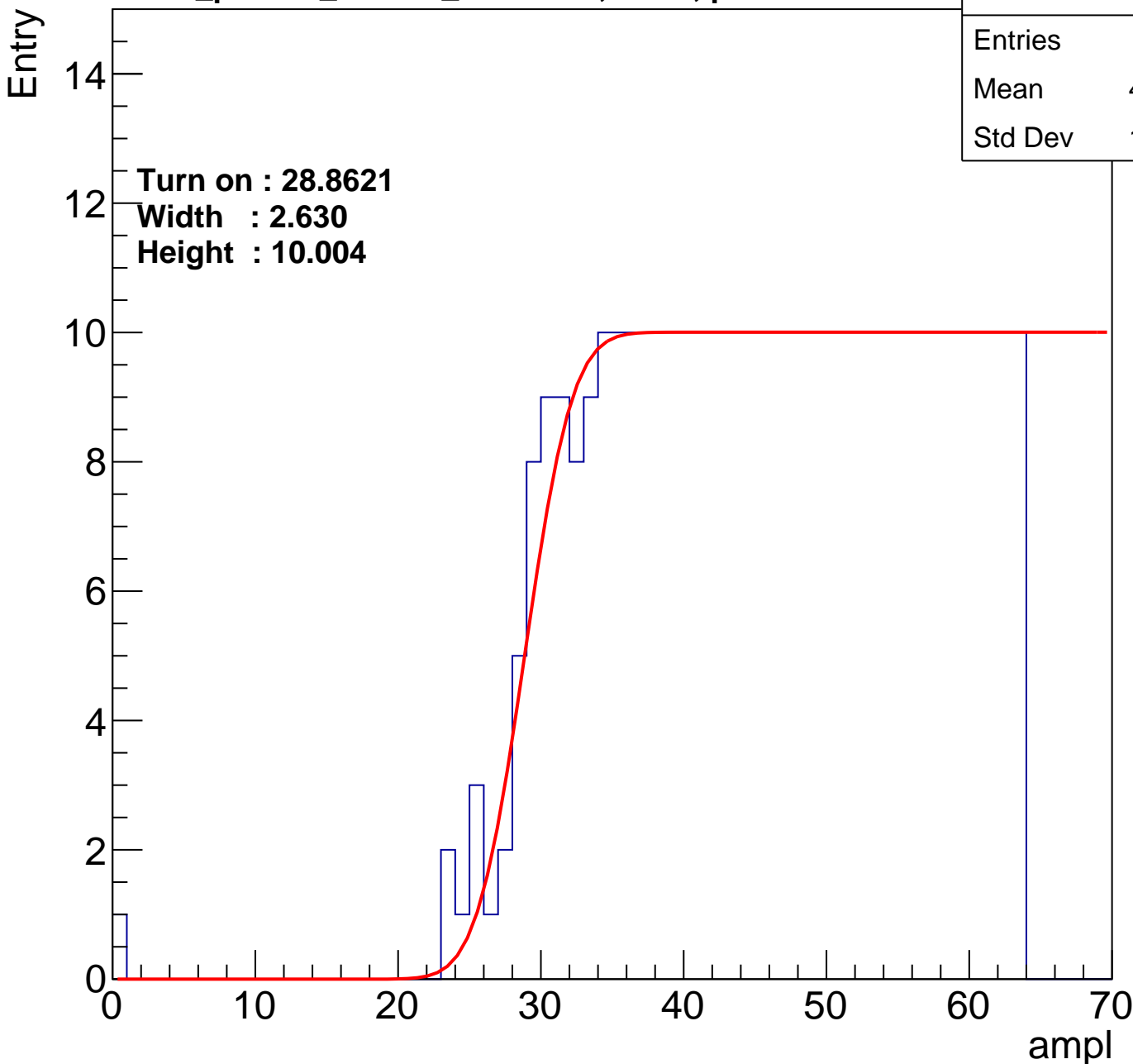
calib_packv5_042523_0143.root, FC#3, port B3

Entries	358
Mean	45.39
Std Dev	10.79

Turn on : 28.8621

Width : 2.630

Height : 10.004



B1L000S, U13-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.63
Std Dev	11.35

Turn on : 27.5304

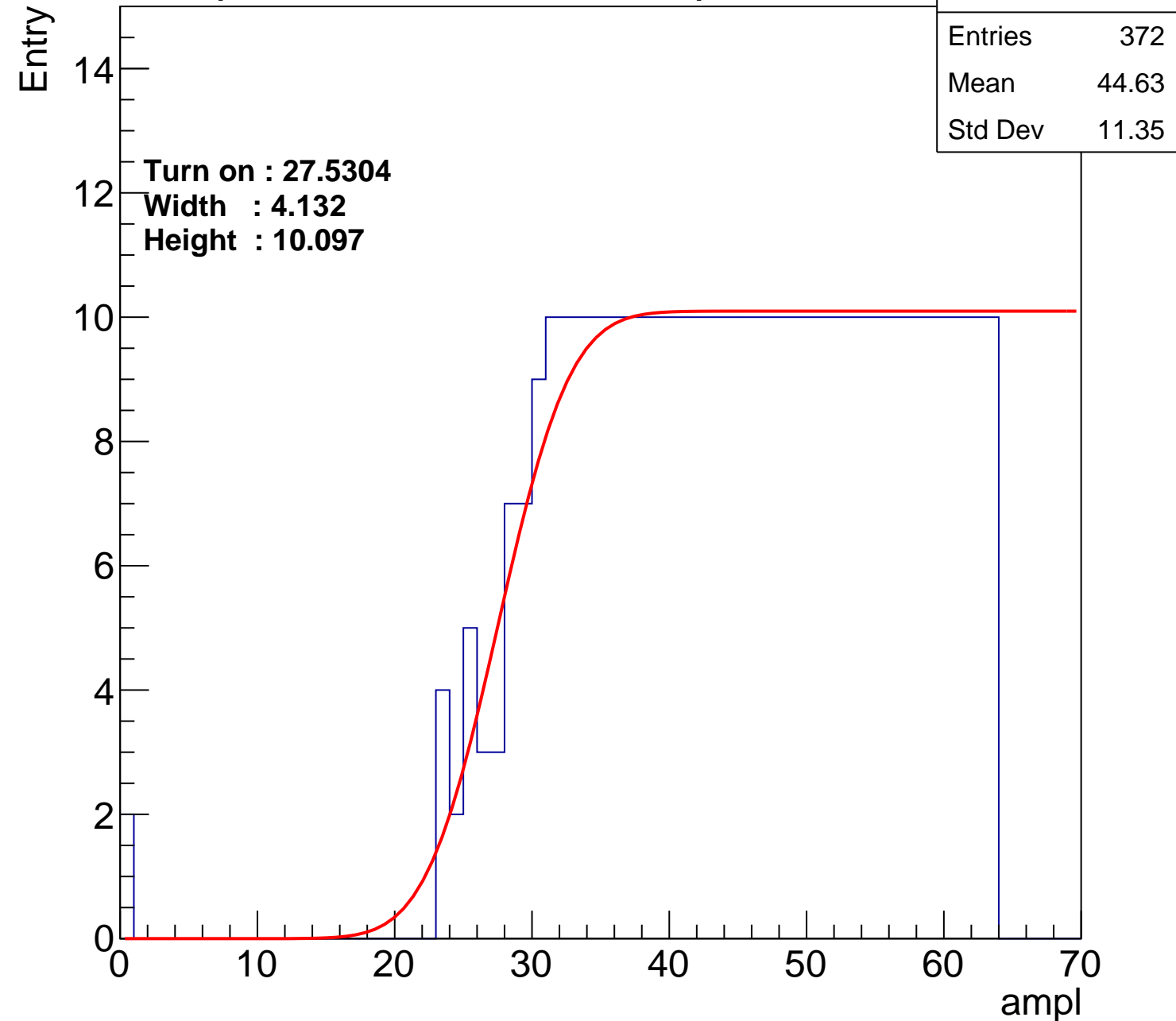
Width : 4.132

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.72
Std Dev	11.25

Turn on : 26.8925

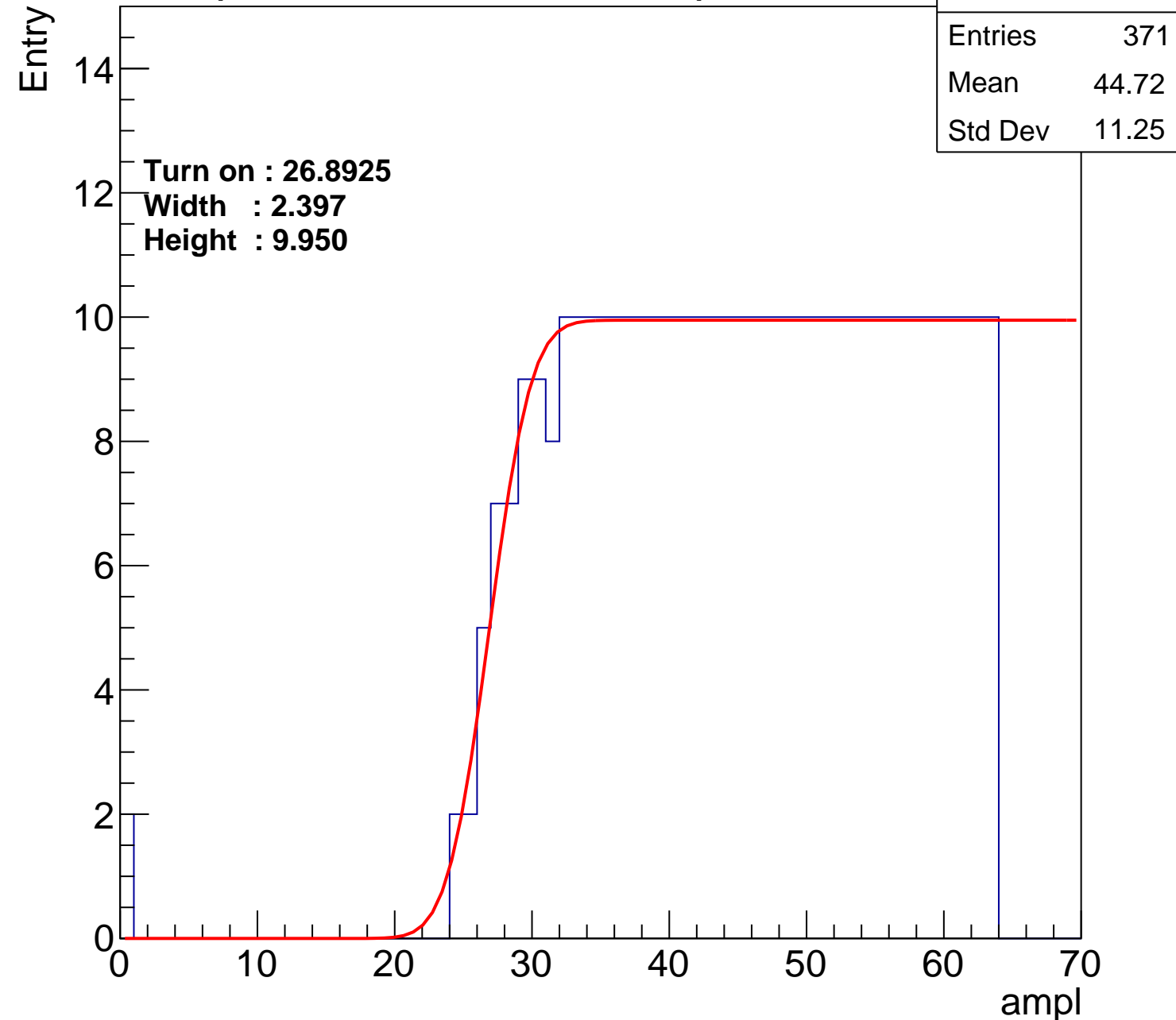
Width : 2.397

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch76

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.31
Std Dev	11.3

Turn on : 26.4731

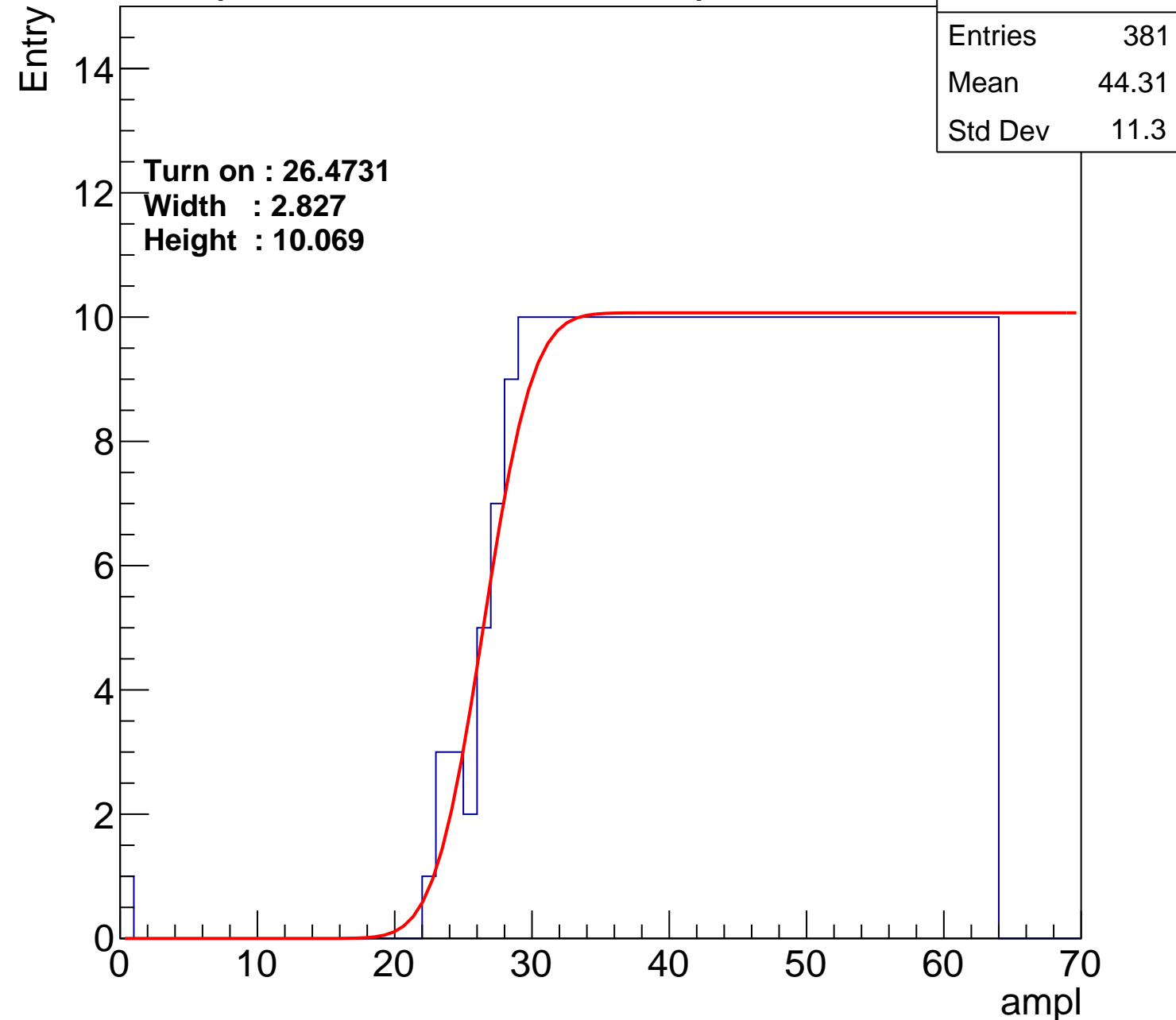
Width : 2.827

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch77

calib_packv5_042523_0143.root, FC#3, port B3

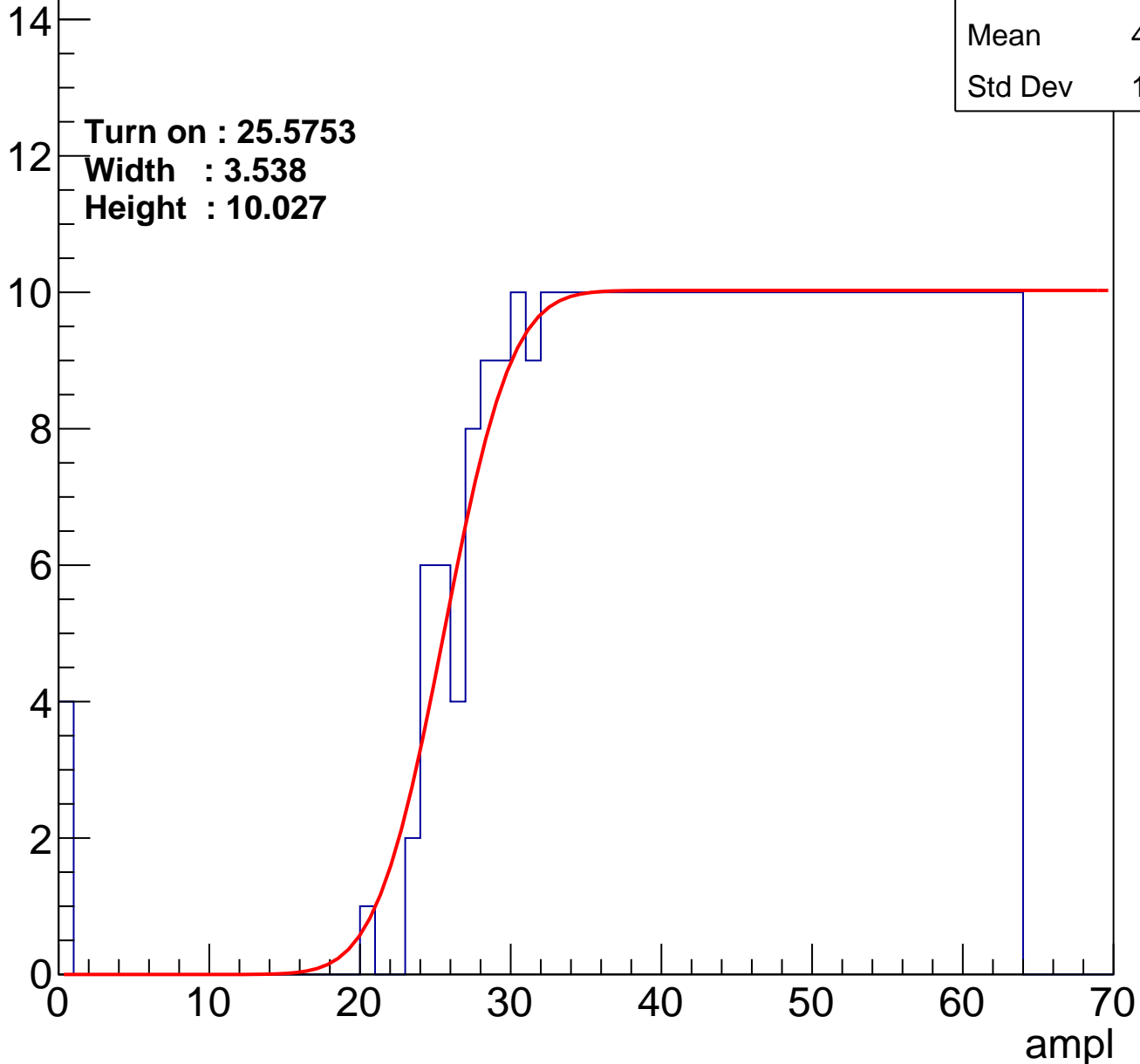
Entries	388
Mean	43.74
Std Dev	12.05

Turn on : 25.5753

Width : 3.538

Height : 10.027

Entry



B1L000S, U13-ch78

calib_packv5_042523_0143.root, FC#3, port B3

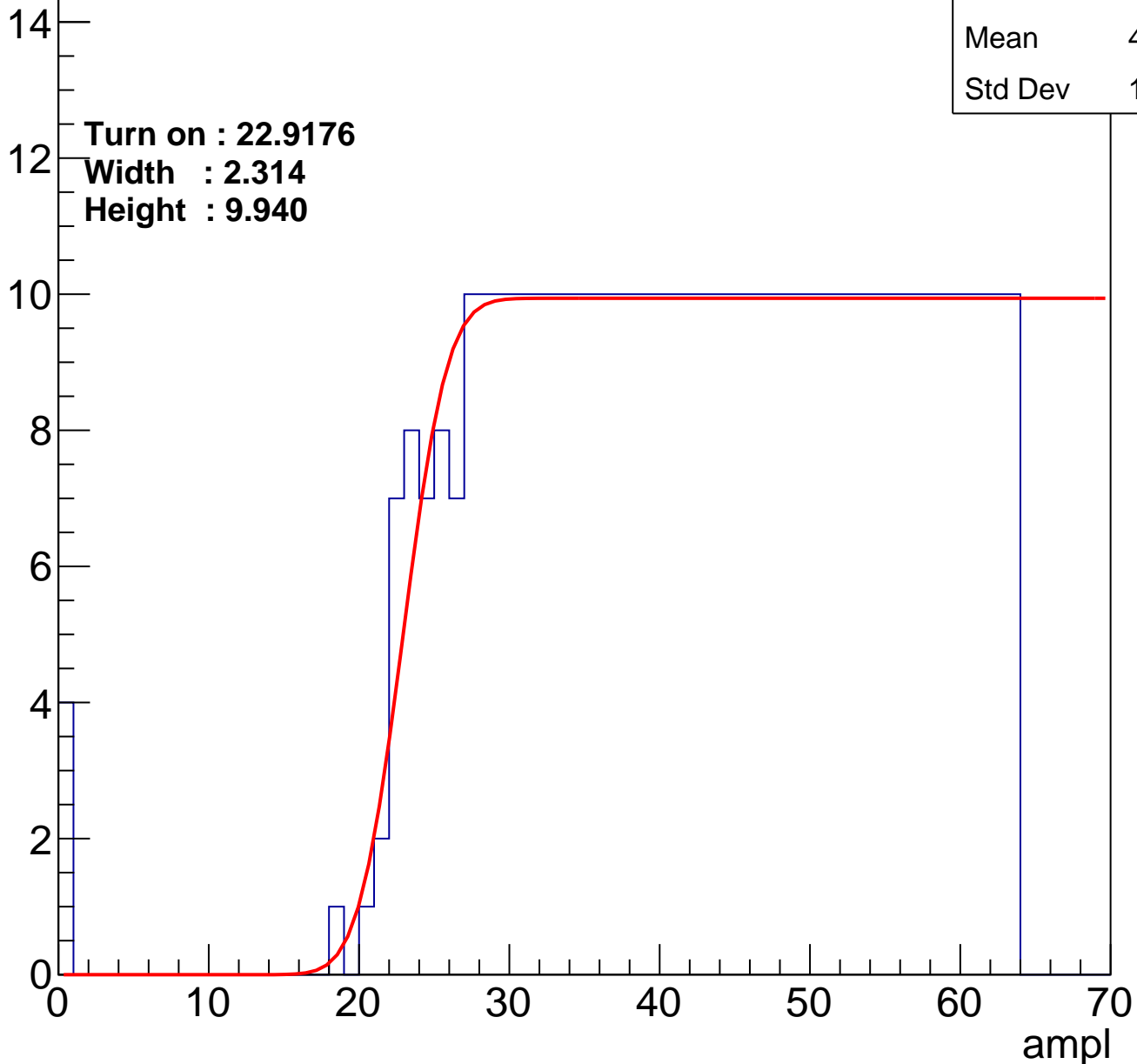
Entries	415
Mean	42.45
Std Dev	12.66

Turn on : 22.9176

Width : 2.314

Height : 9.940

Entry



B1L000S, U13-ch79

calib_packv5_042523_0143.root, FC#3, port B3

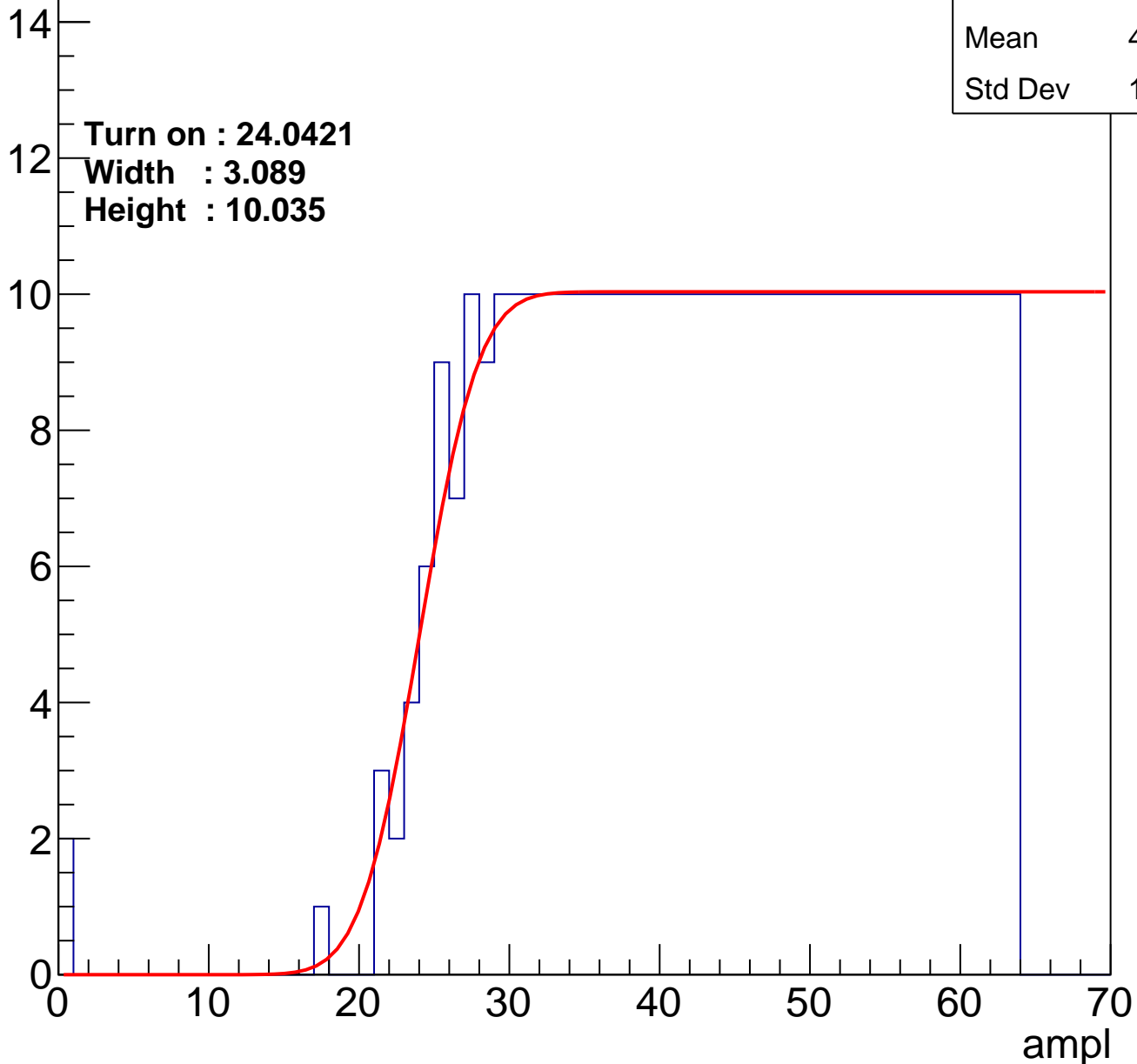
Entries	403
Mean	43.15
Std Dev	12.08

Turn on : 24.0421

Width : 3.089

Height : 10.035

Entry



B1L000S, U13-ch80

calib_packv5_042523_0143.root, FC#3, port B3

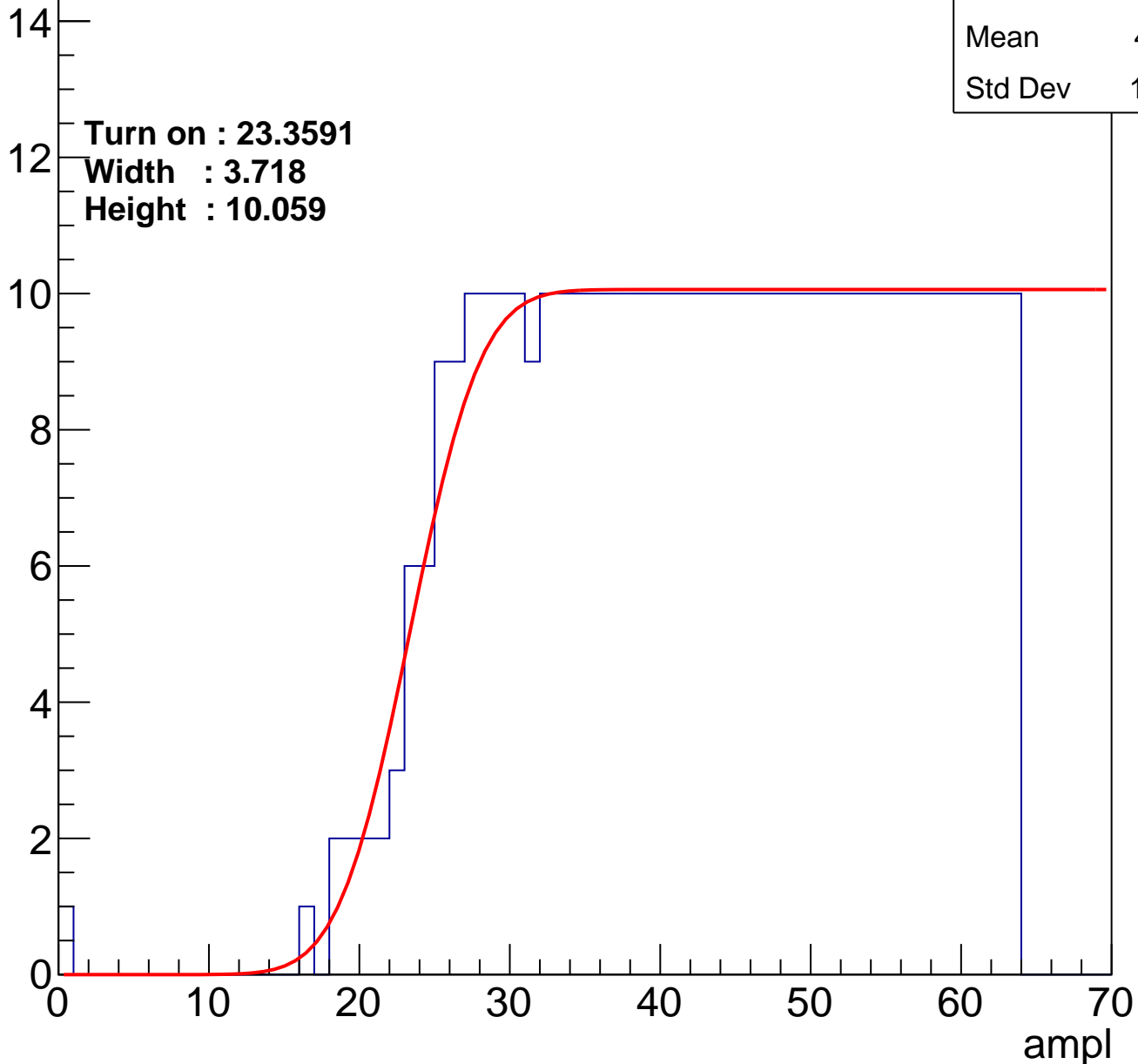
Entries	412
Mean	42.71
Std Dev	12.26

Turn on : 23.3591

Width : 3.718

Height : 10.059

Entry



B1L000S, U13-ch81

calib_packv5_042523_0143.root, FC#3, port B3

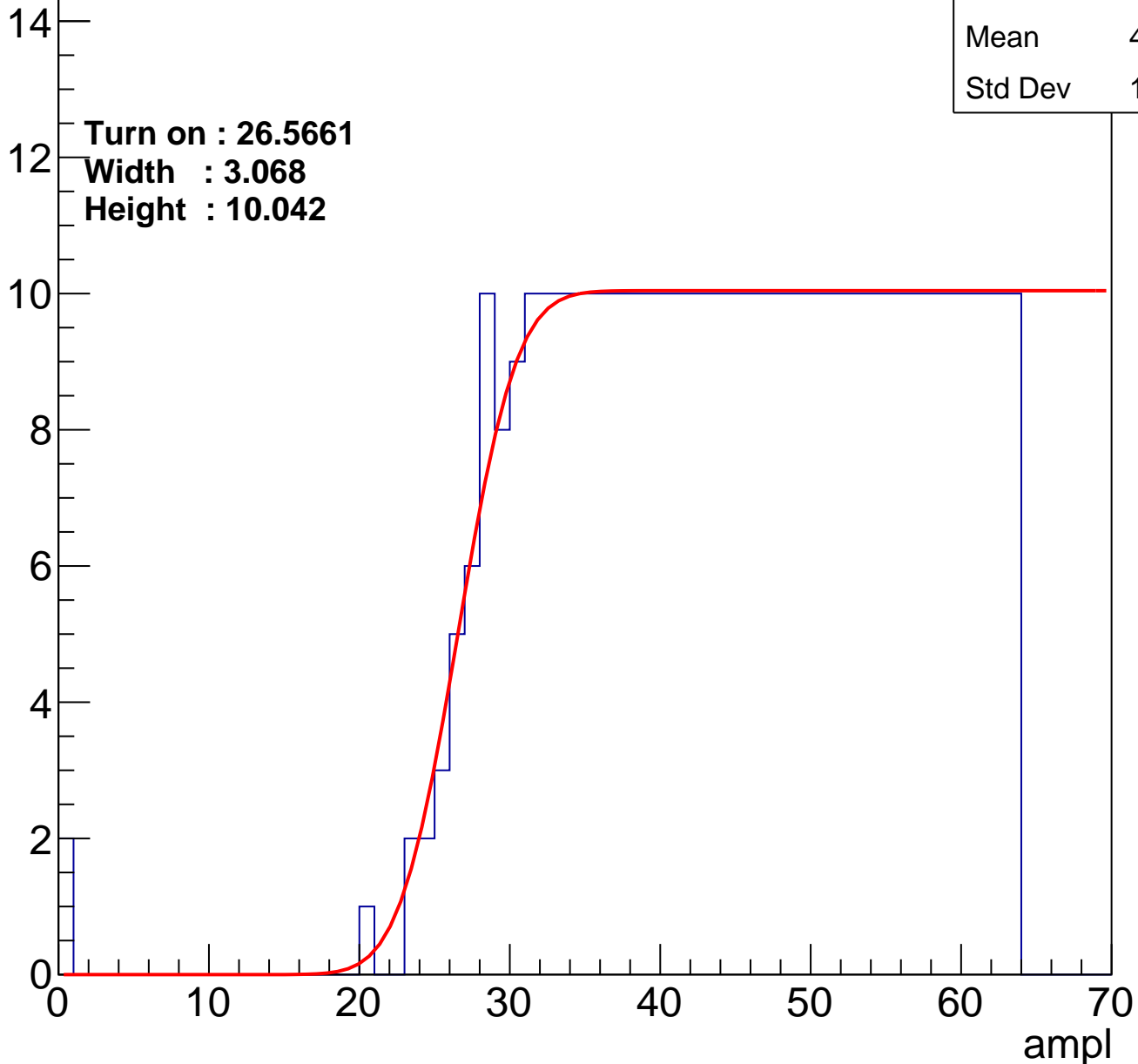
Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.5661

Width : 3.068

Height : 10.042

Entry



B1L000S, U13-ch82

calib_packv5_042523_0143.root, FC#3, port B3

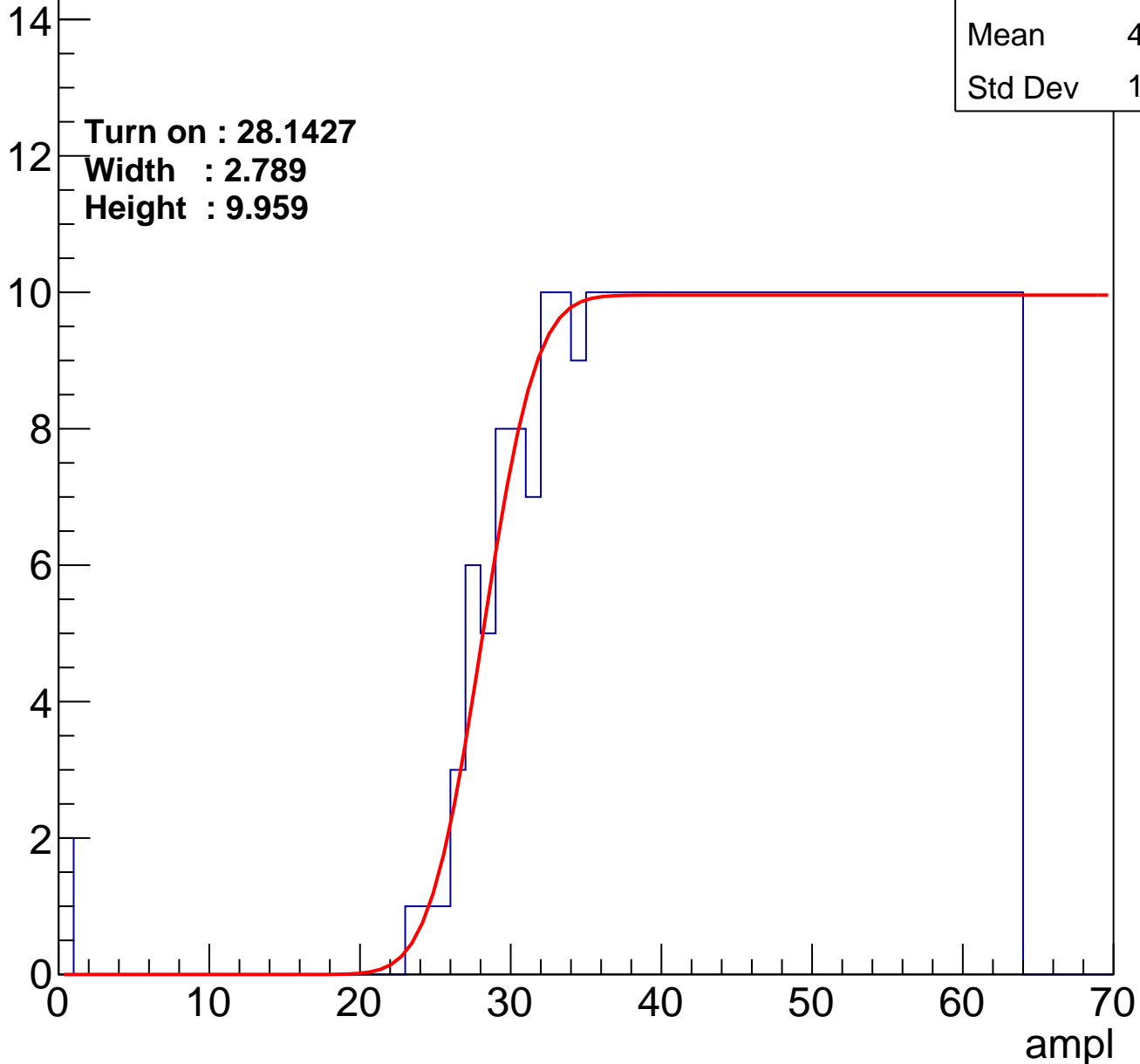
Entries	361
Mean	45.17
Std Dev	11.07

Turn on : 28.1427

Width : 2.789

Height : 9.959

Entry



B1L000S, U13-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.53
Std Dev	11.48

Turn on : 26.7400

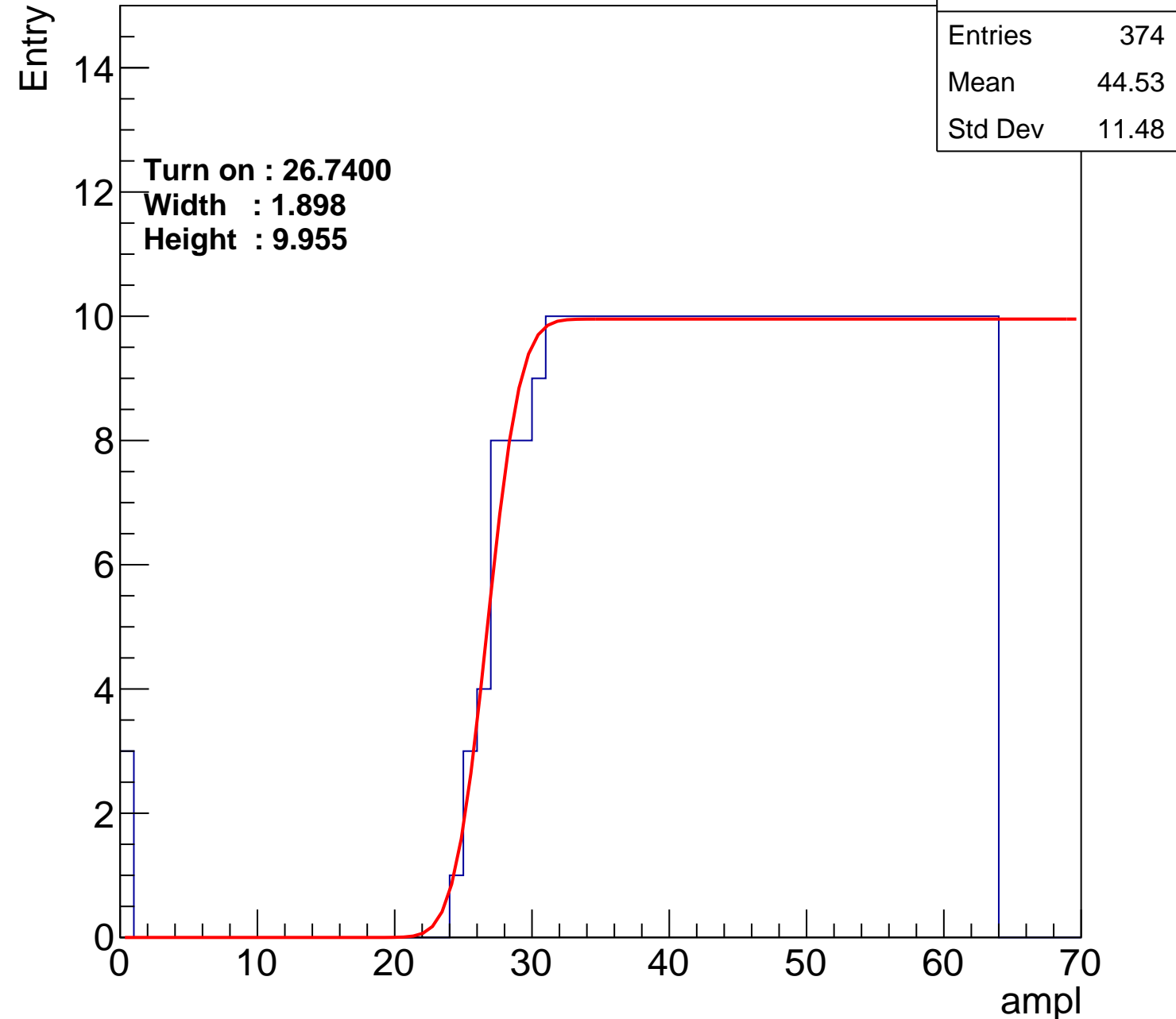
Width : 1.898

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.98
Std Dev	10.96

Turn on : 27.4228

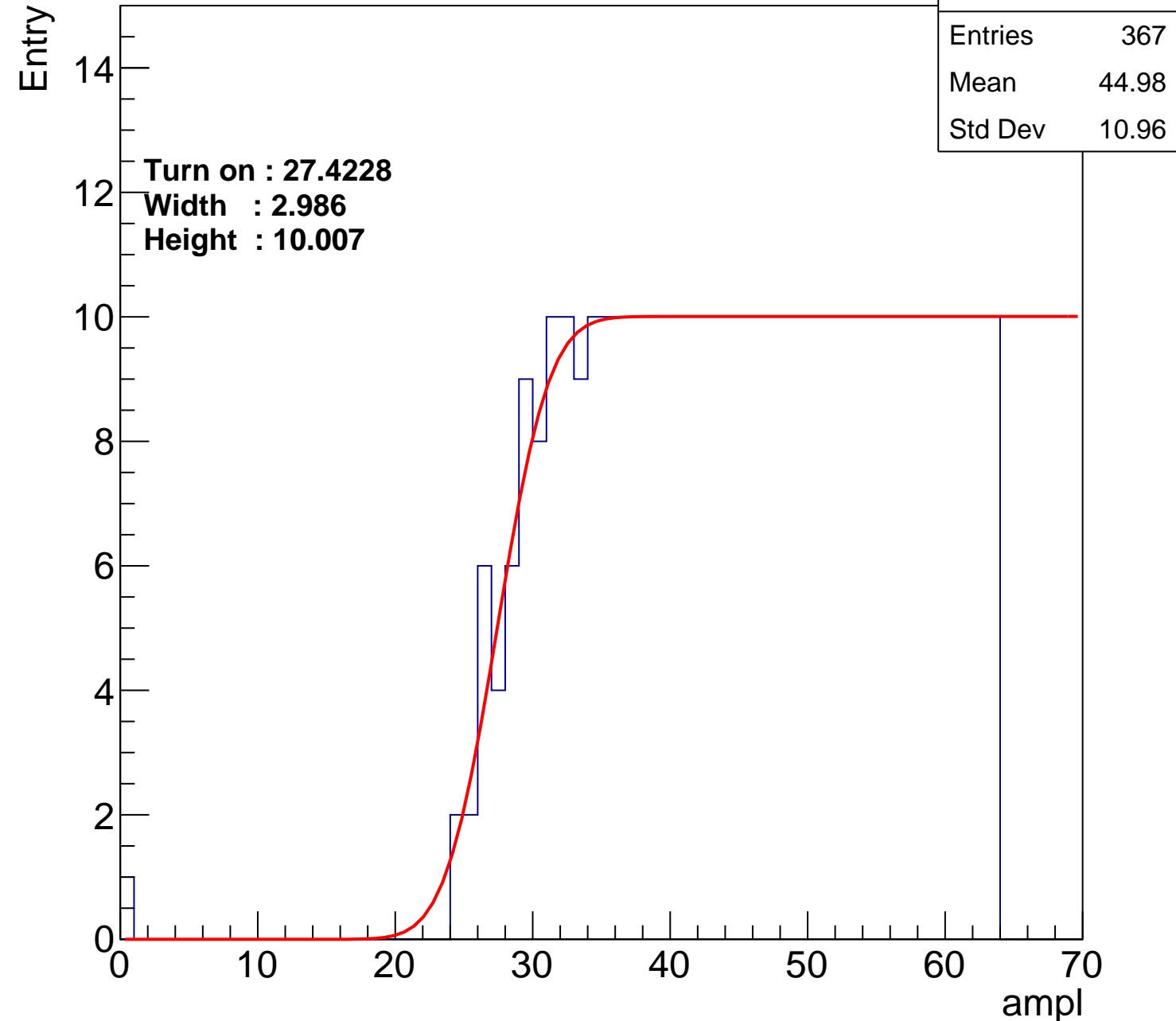
Width : 2.986

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch85

calib_packv5_042523_0143.root, FC#3, port B3

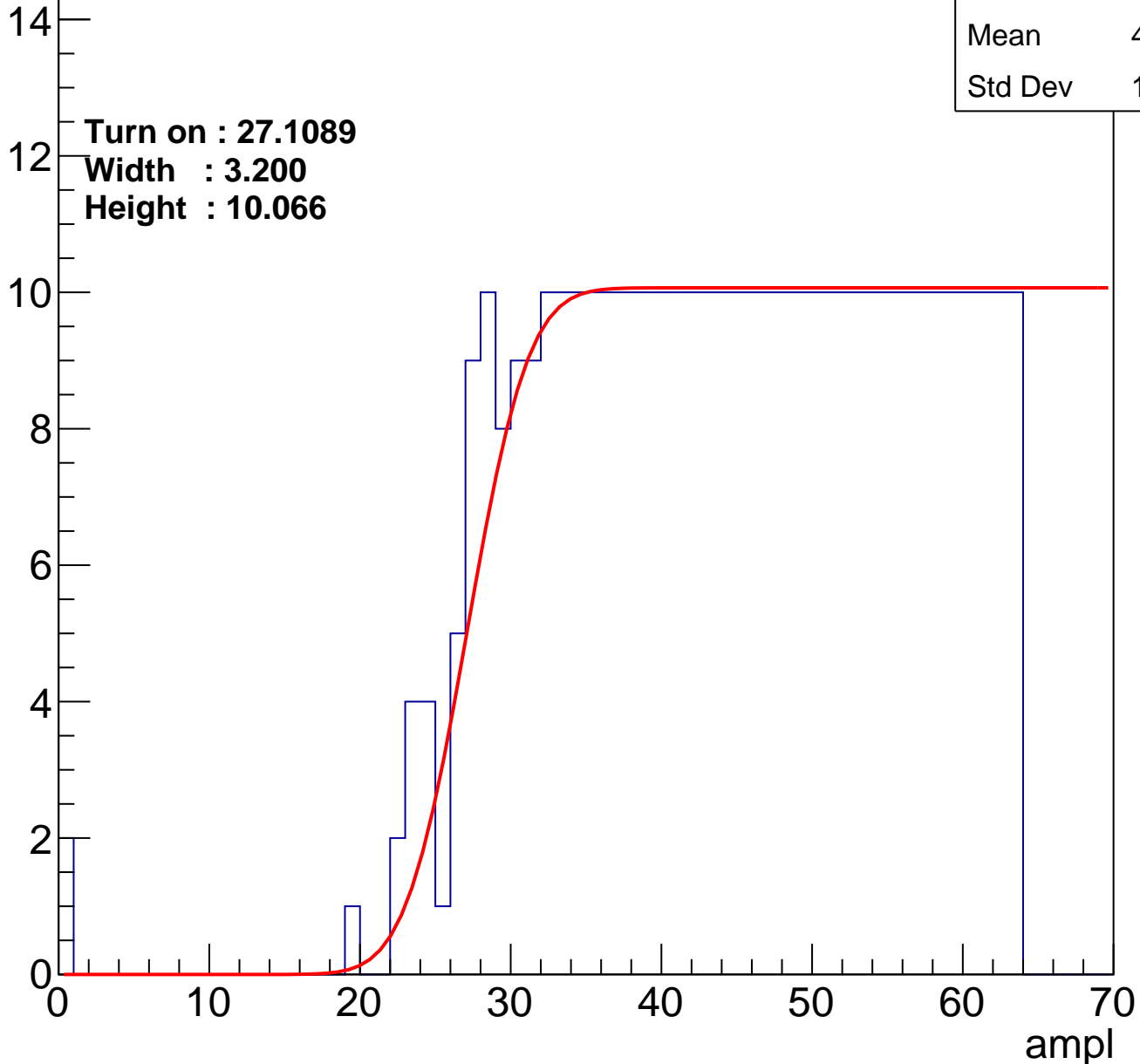
Entries	384
Mean	44.04
Std Dev	11.67

Turn on : 27.1089

Width : 3.200

Height : 10.066

Entry



B1L000S, U13-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	413
Mean	42.56
Std Dev	12.55

Turn on : 23.2871

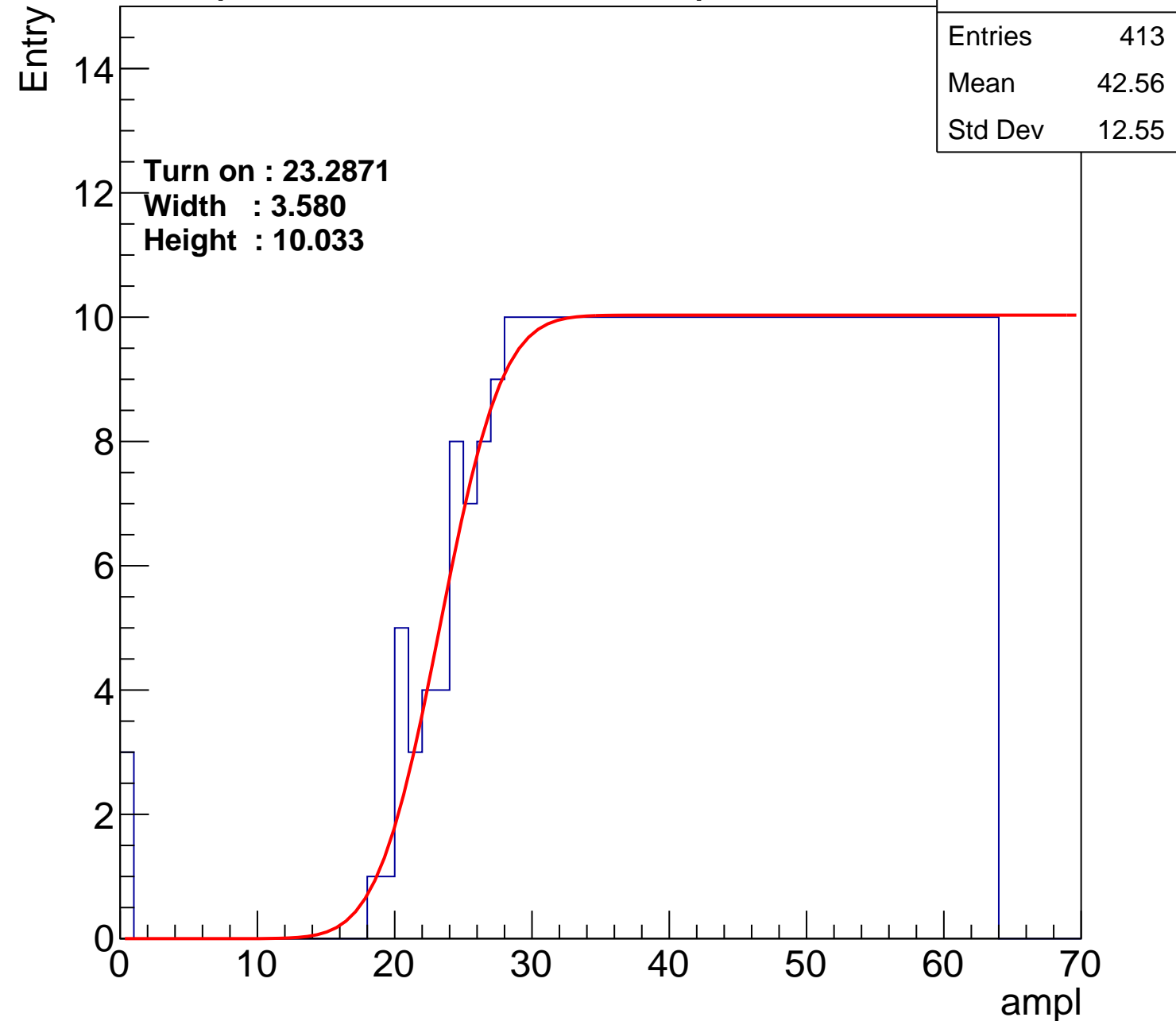
Width : 3.580

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.7420

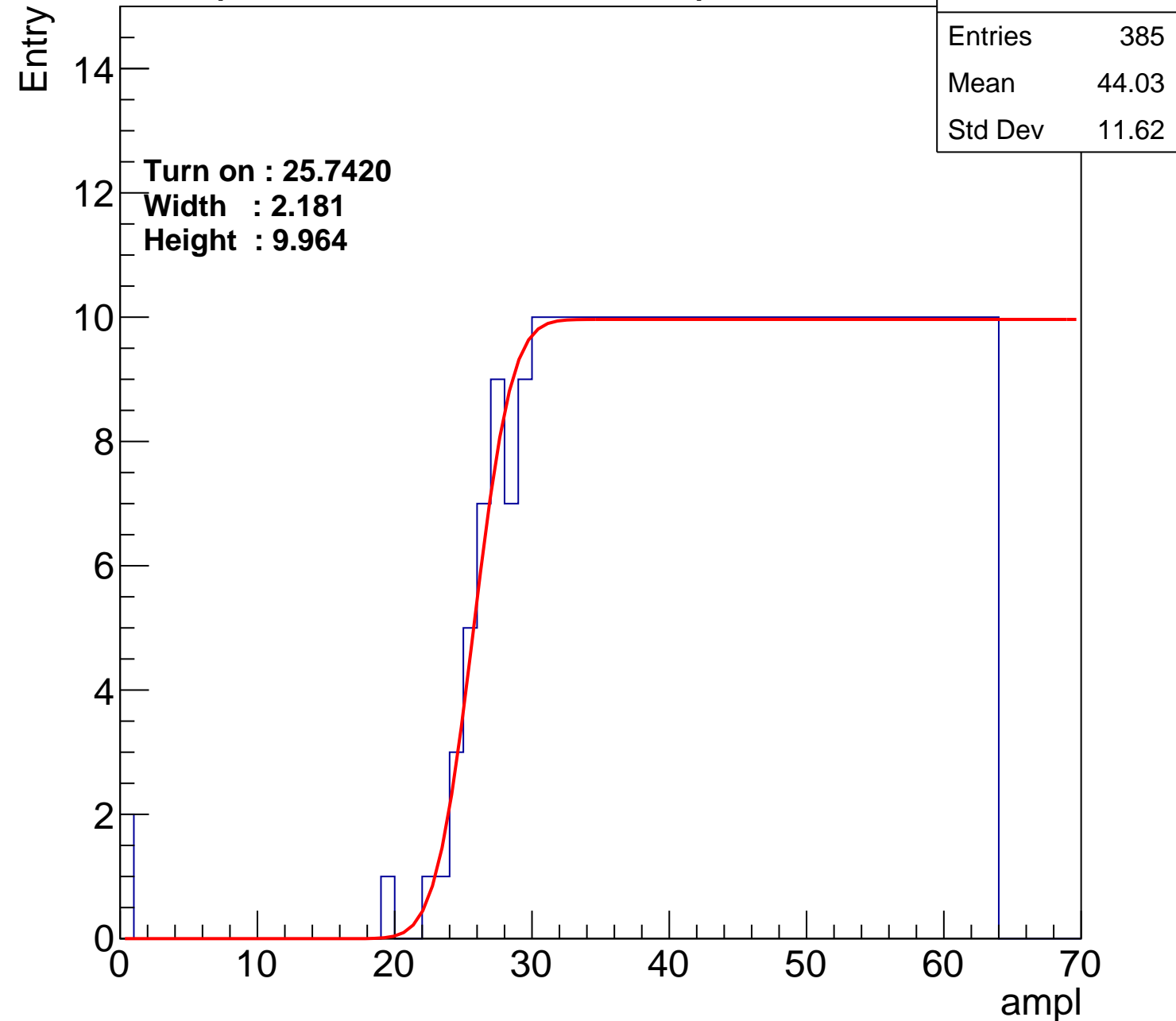
Width : 2.181

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.11
Std Dev	11.59

Turn on : 26.1490

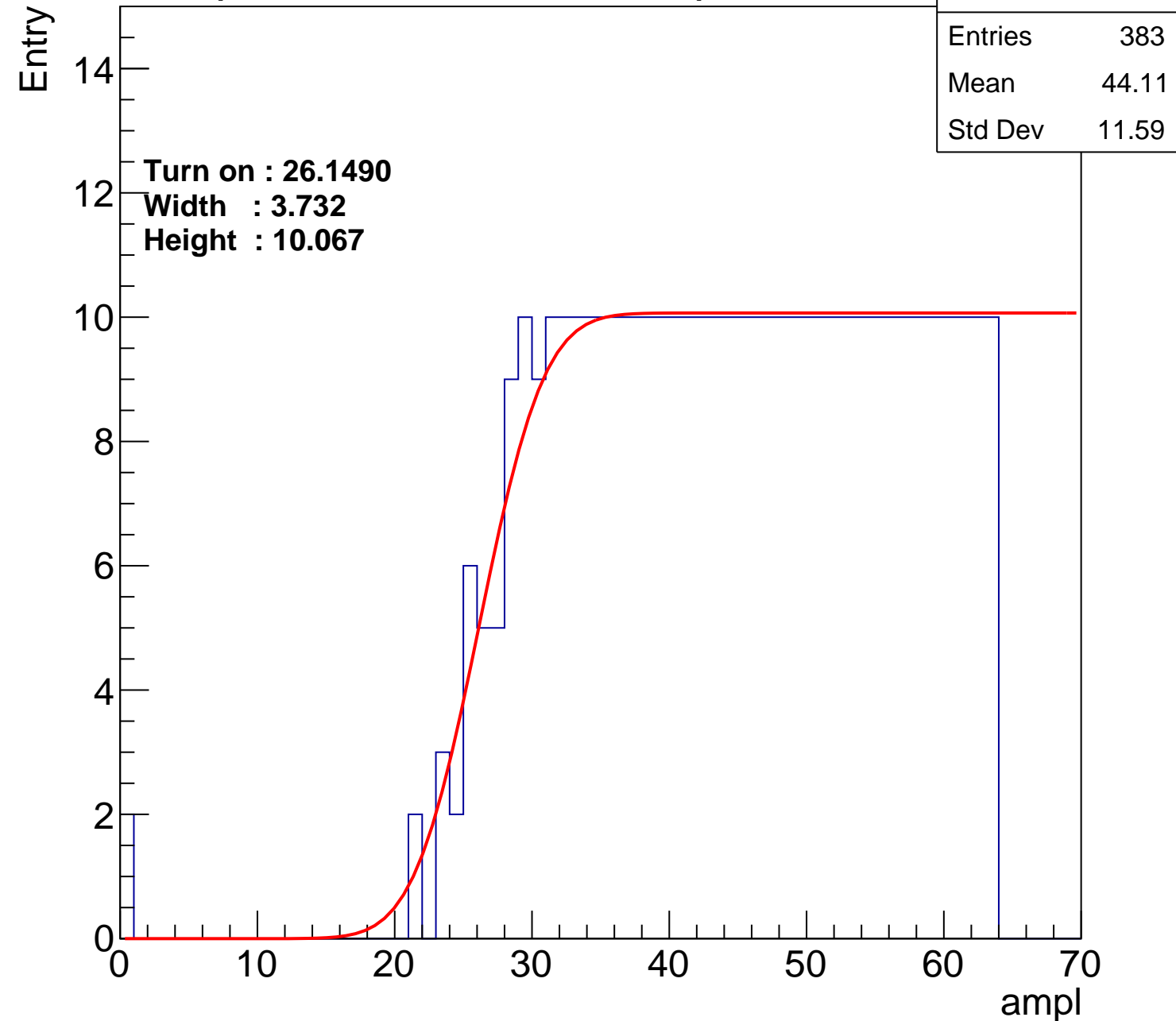
Width : 3.732

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch89

calib_packv5_042523_0143.root, FC#3, port B3

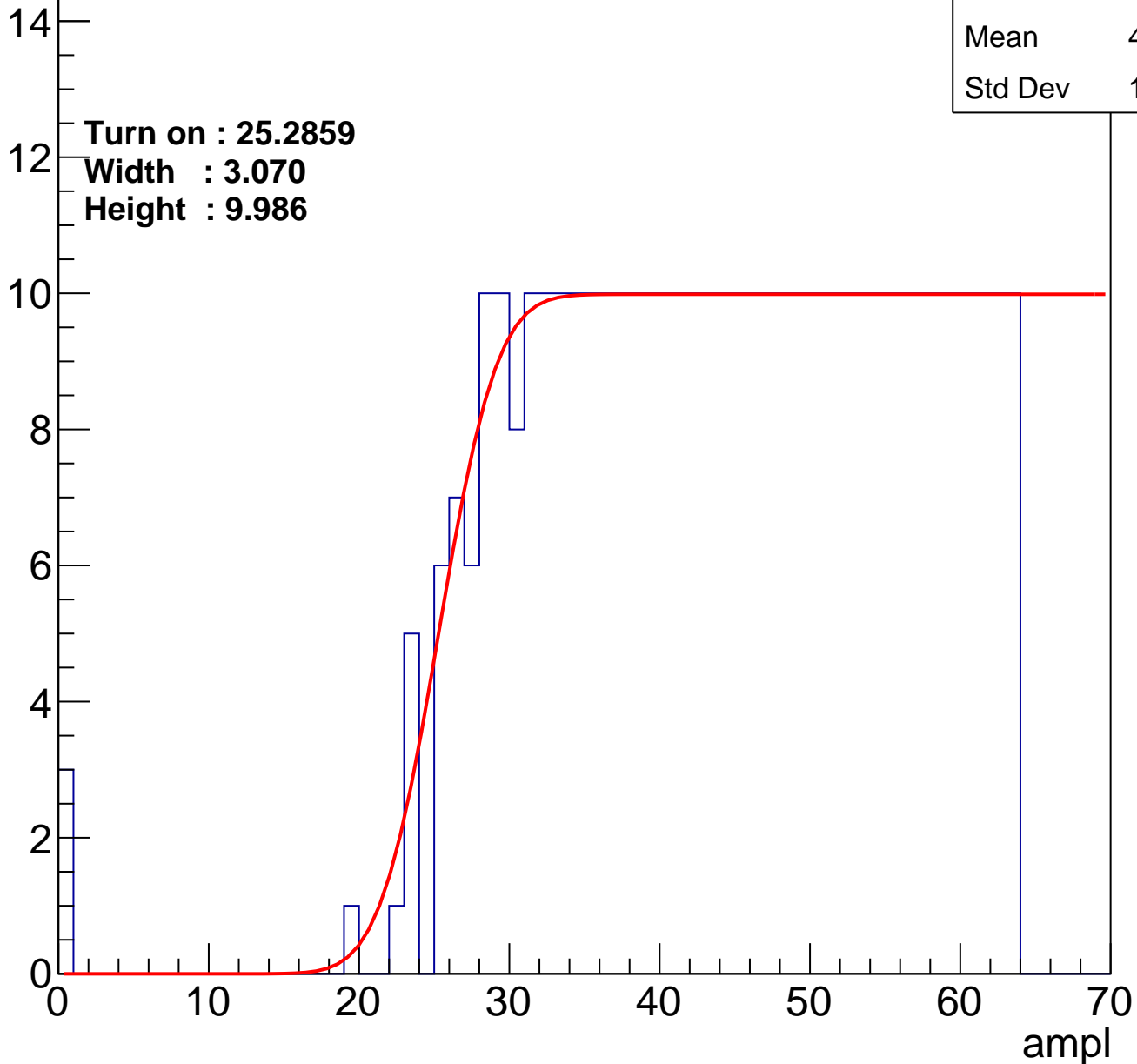
Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 25.2859

Width : 3.070

Height : 9.986

Entry



B1L000S, U13-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.53
Std Dev	11.16

Turn on : 26.4129

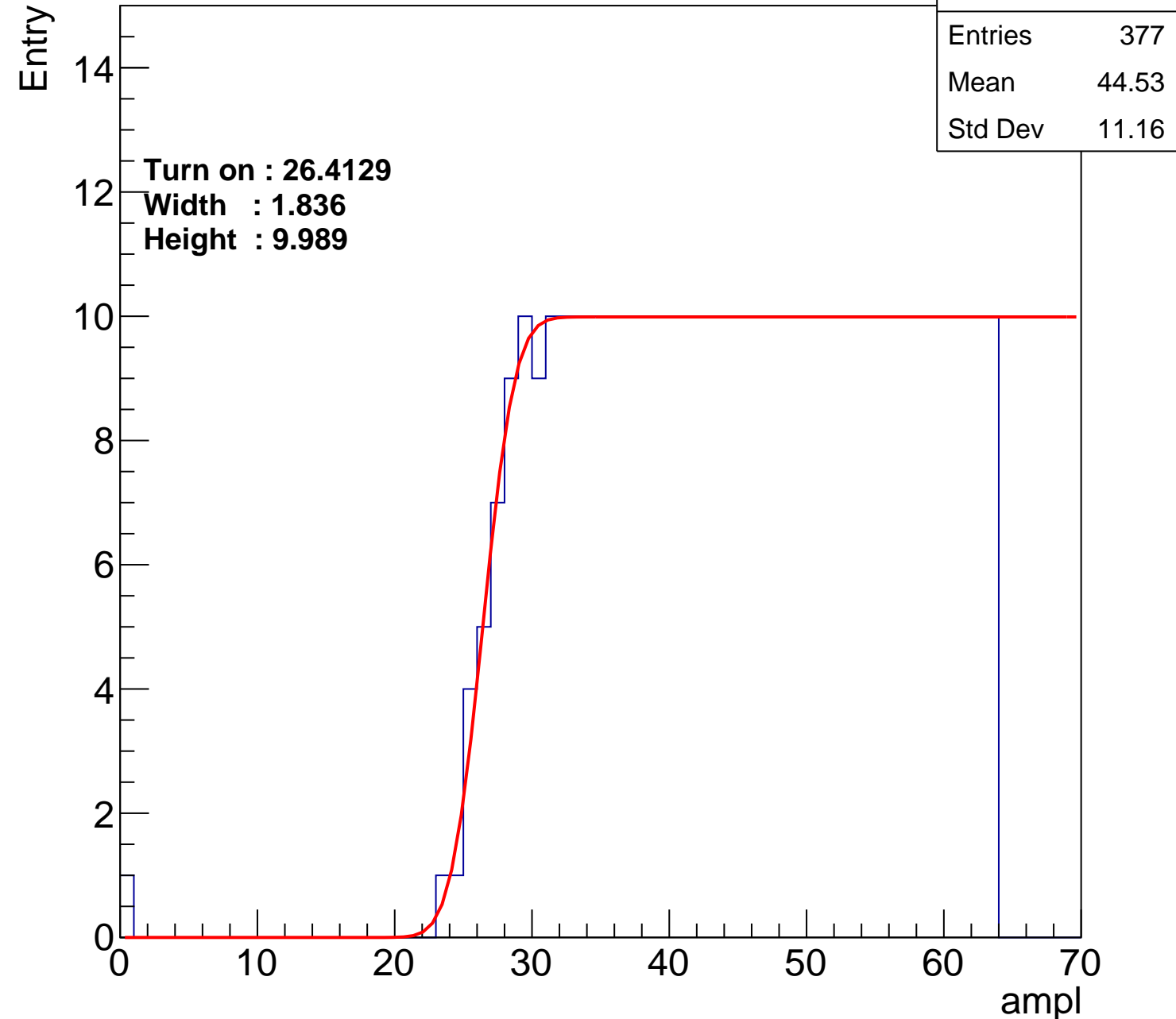
Width : 1.836

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch91

calib_packv5_042523_0143.root, FC#3, port B3

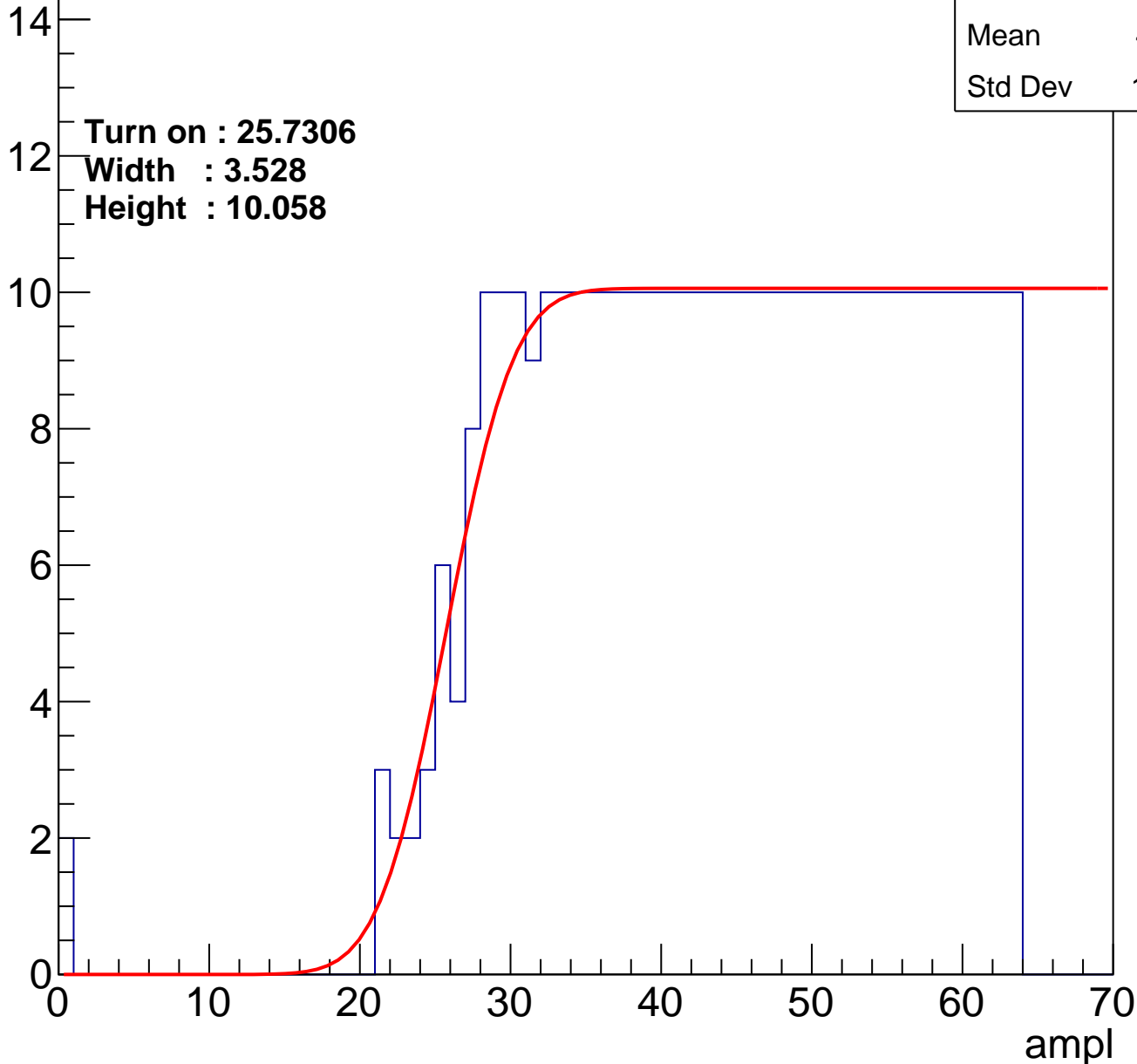
Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 25.7306

Width : 3.528

Height : 10.058

Entry



B1L000S, U13-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.39
Std Dev	11.74

Turn on : 27.0372

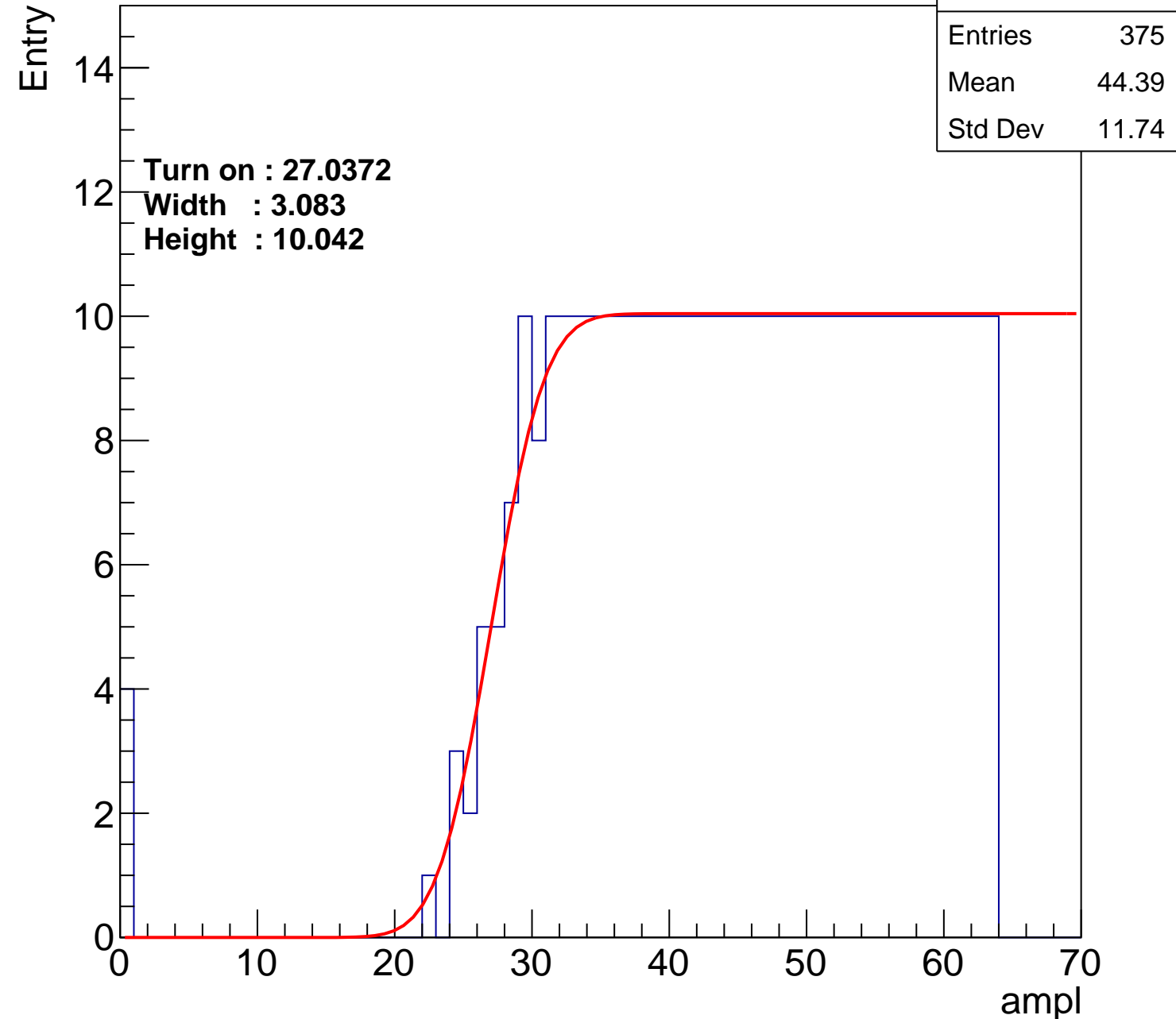
Width : 3.083

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch93

calib_packv5_042523_0143.root, FC#3, port B3

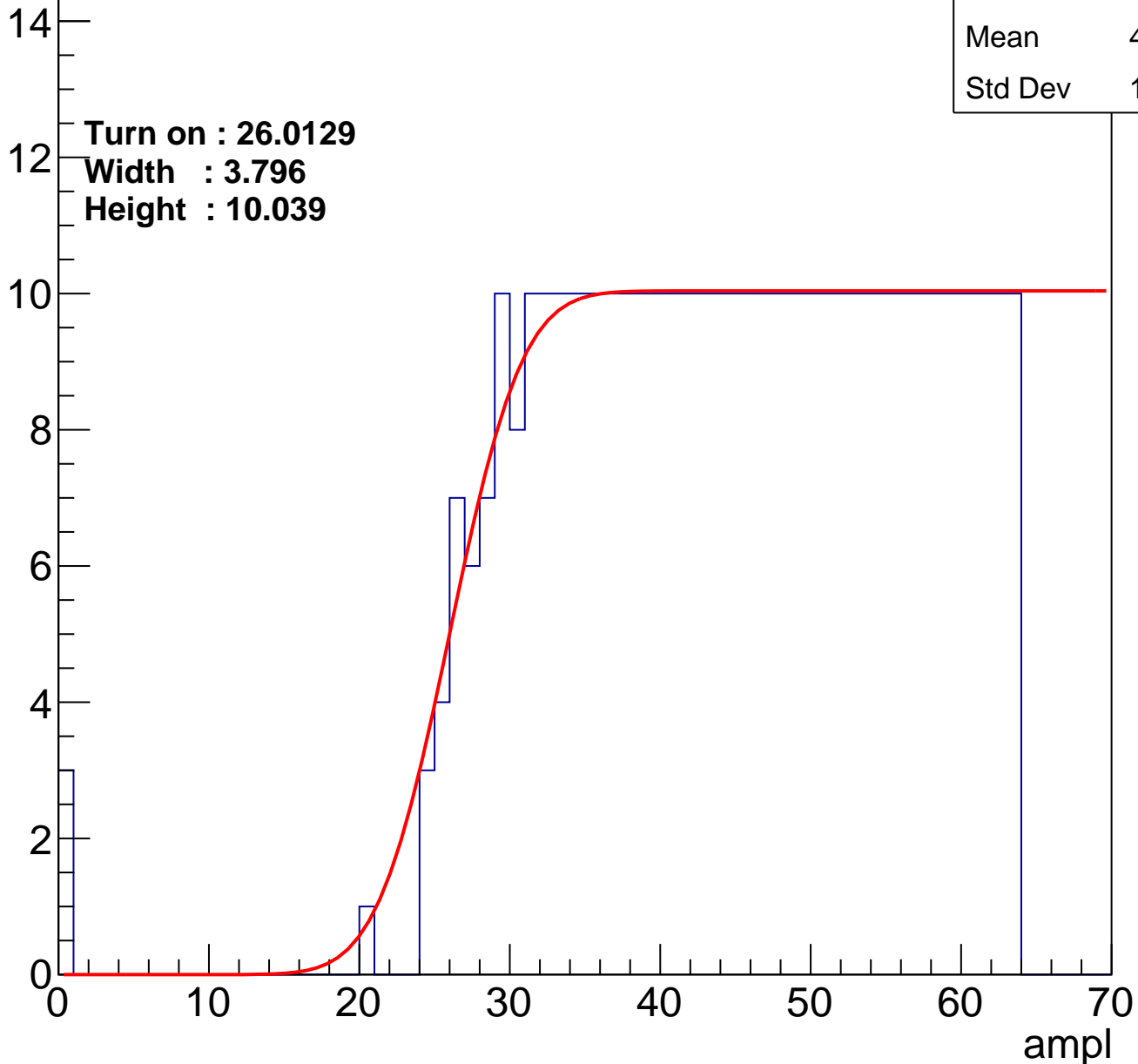
Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.0129

Width : 3.796

Height : 10.039

Entry



B1L000S, U13-ch94

calib_packv5_042523_0143.root, FC#3, port B3

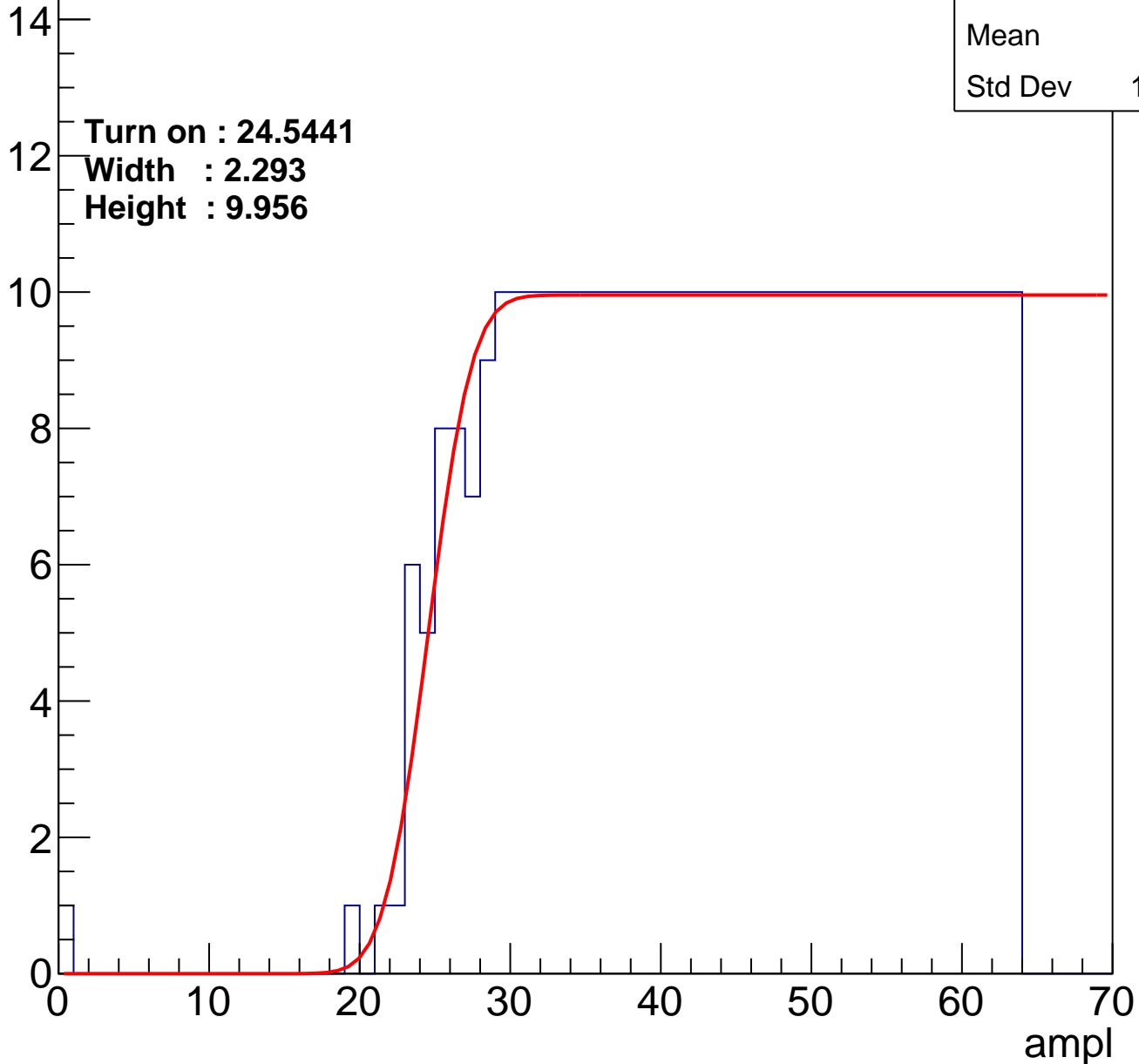
Entries	397
Mean	43.5
Std Dev	11.77

Turn on : 24.5441

Width : 2.293

Height : 9.956

Entry



B1L000S, U13-ch95

calib_packv5_042523_0143.root, FC#3, port B3

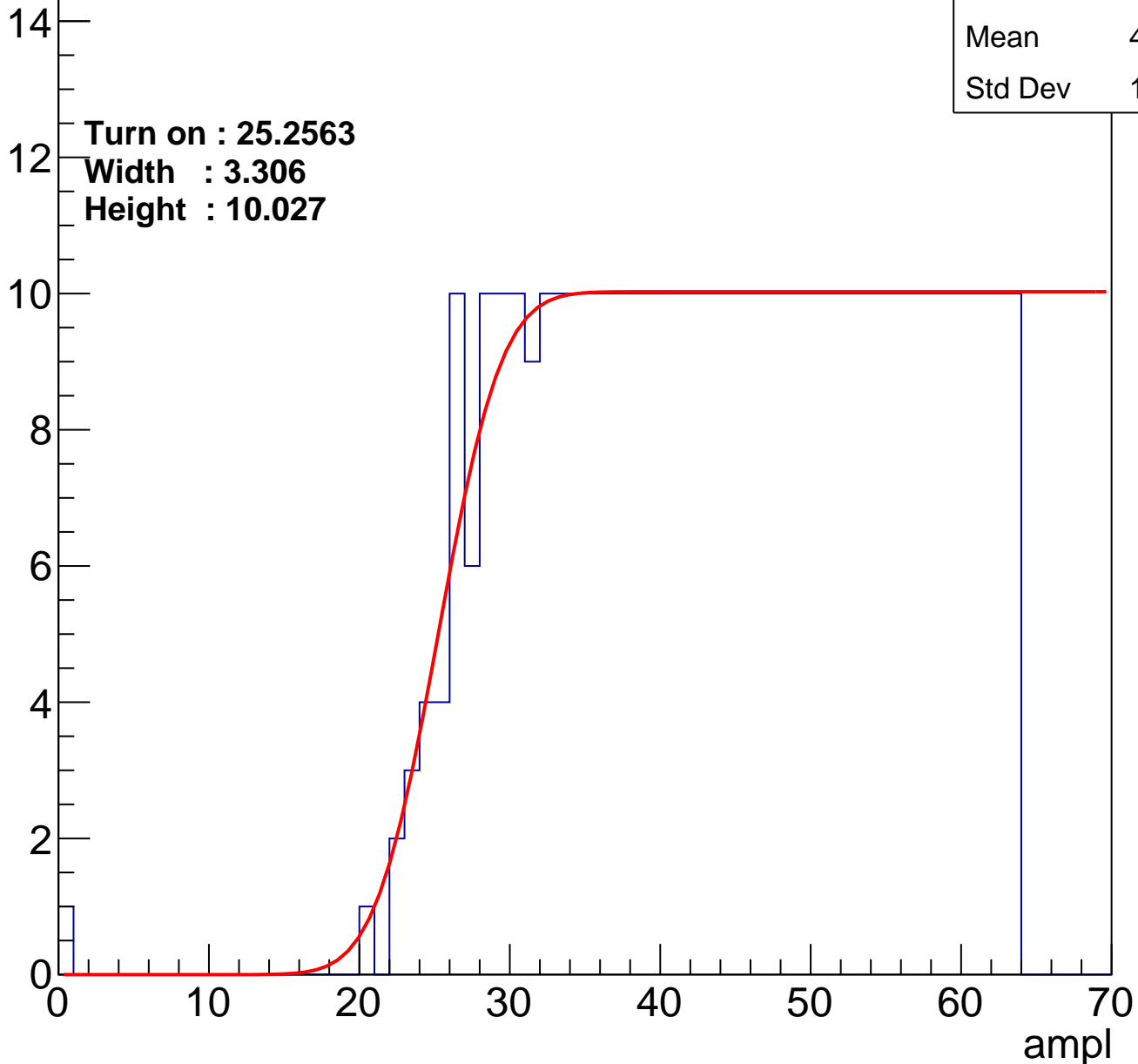
Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.2563

Width : 3.306

Height : 10.027

Entry



B1L000S, U13-ch96

calib_packv5_042523_0143.root, FC#3, port B3

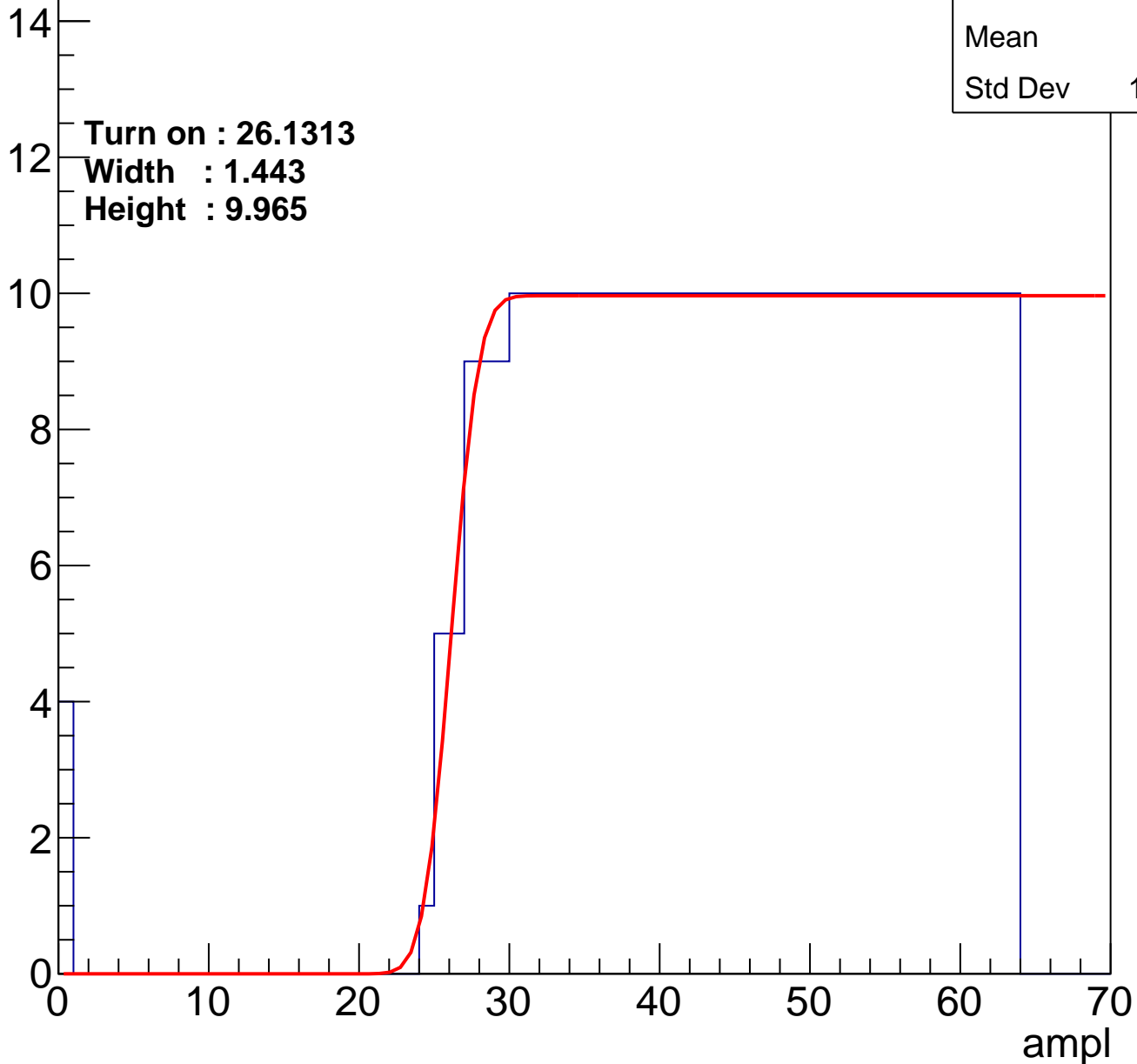
Entries	382
Mean	44.1
Std Dev	11.82

Turn on : 26.1313

Width : 1.443

Height : 9.965

Entry



B1L000S, U13-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.06
Std Dev	11.76

Turn on : 26.2013

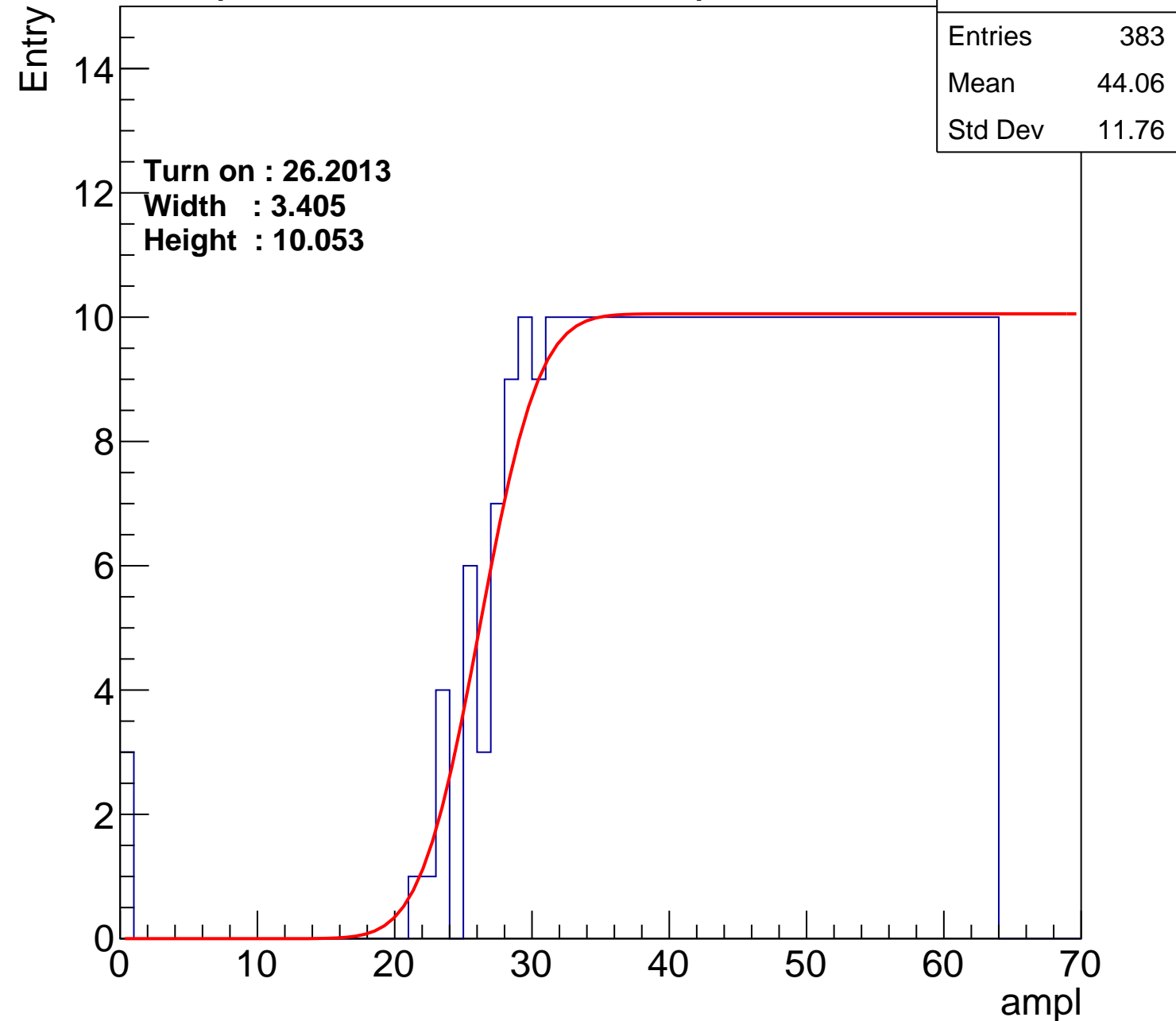
Width : 3.405

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.5834

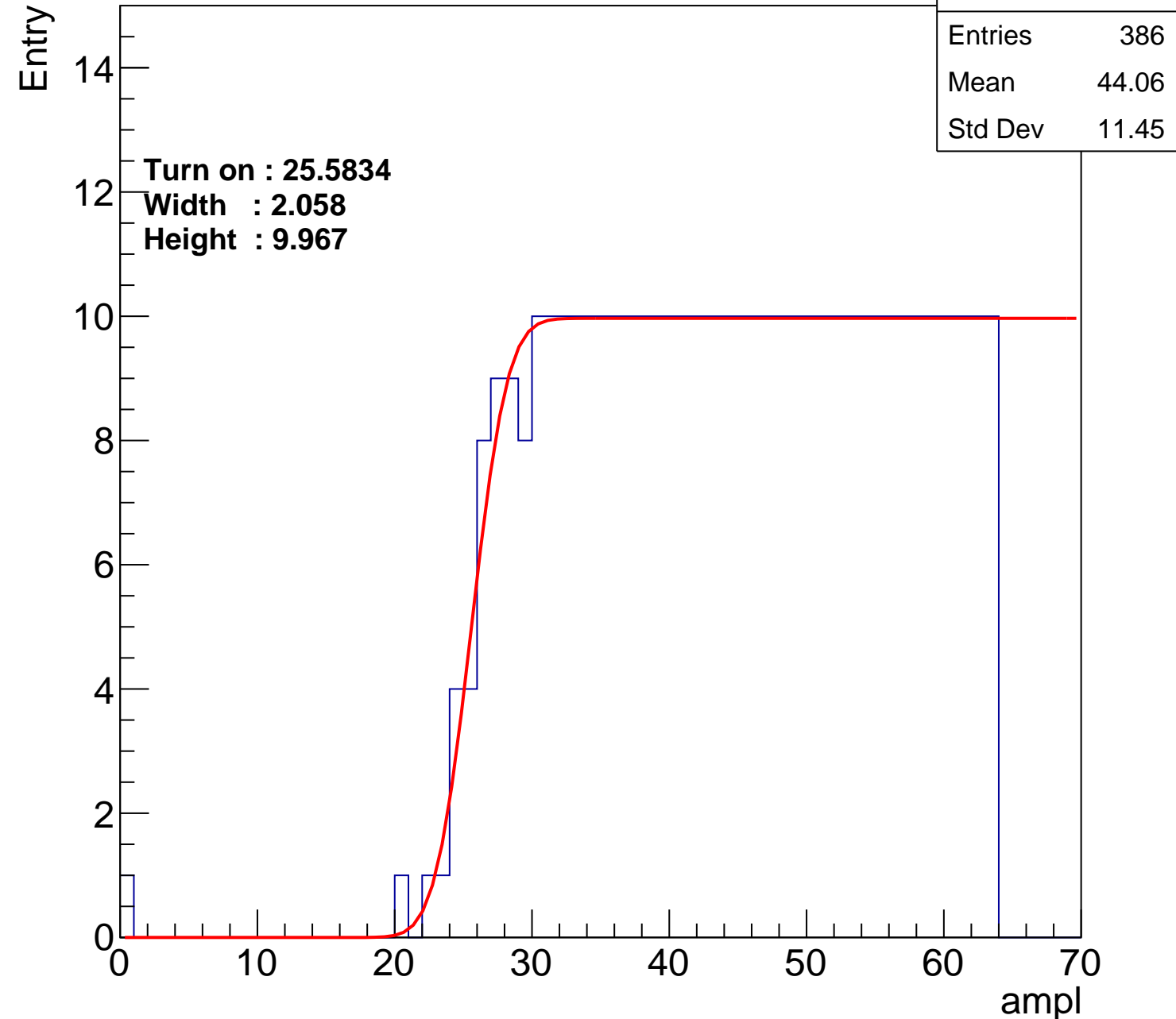
Width : 2.058

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 24.8568

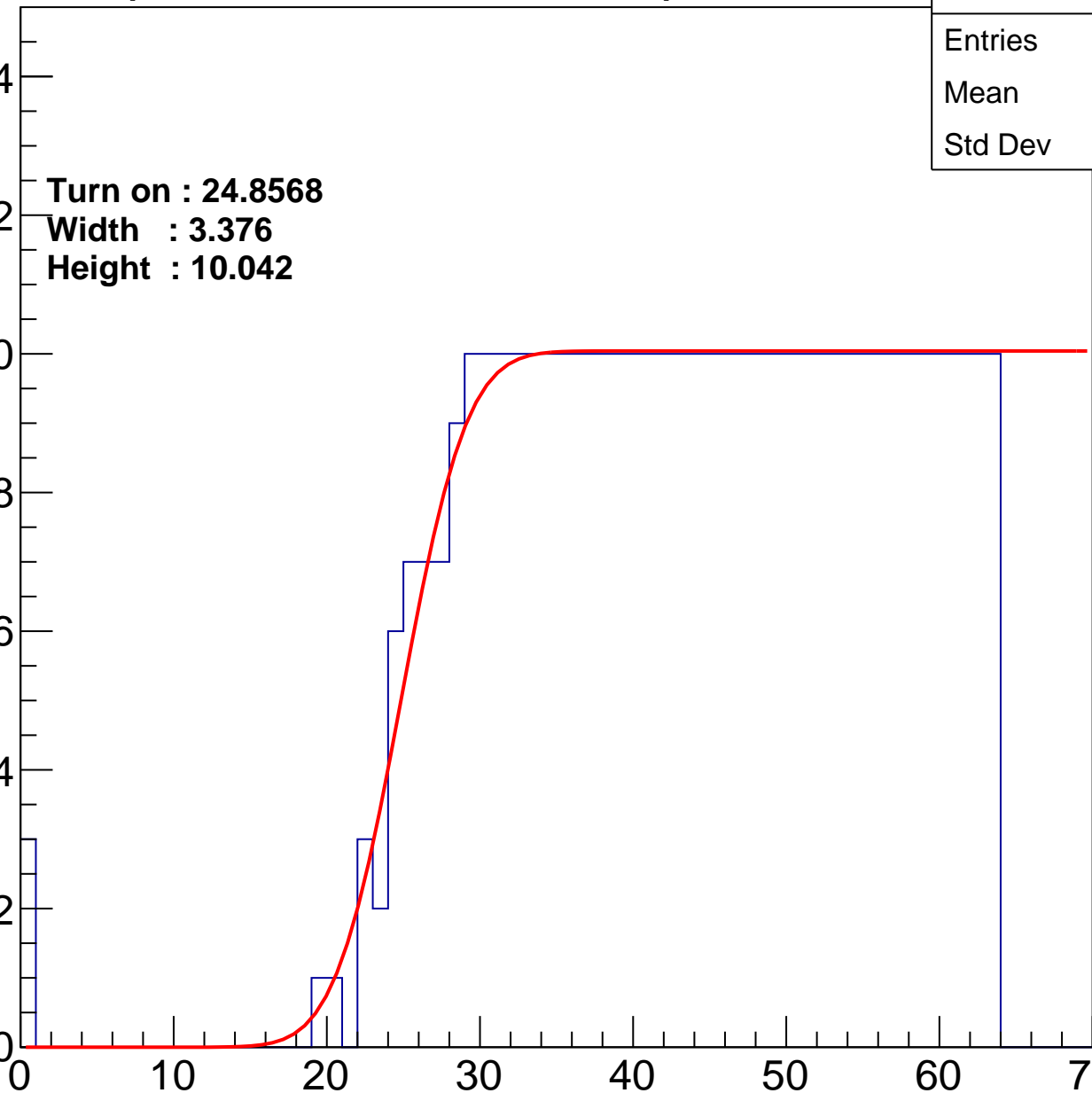
Width : 3.376

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.07
Std Dev	12.25

Turn on : 23.7231

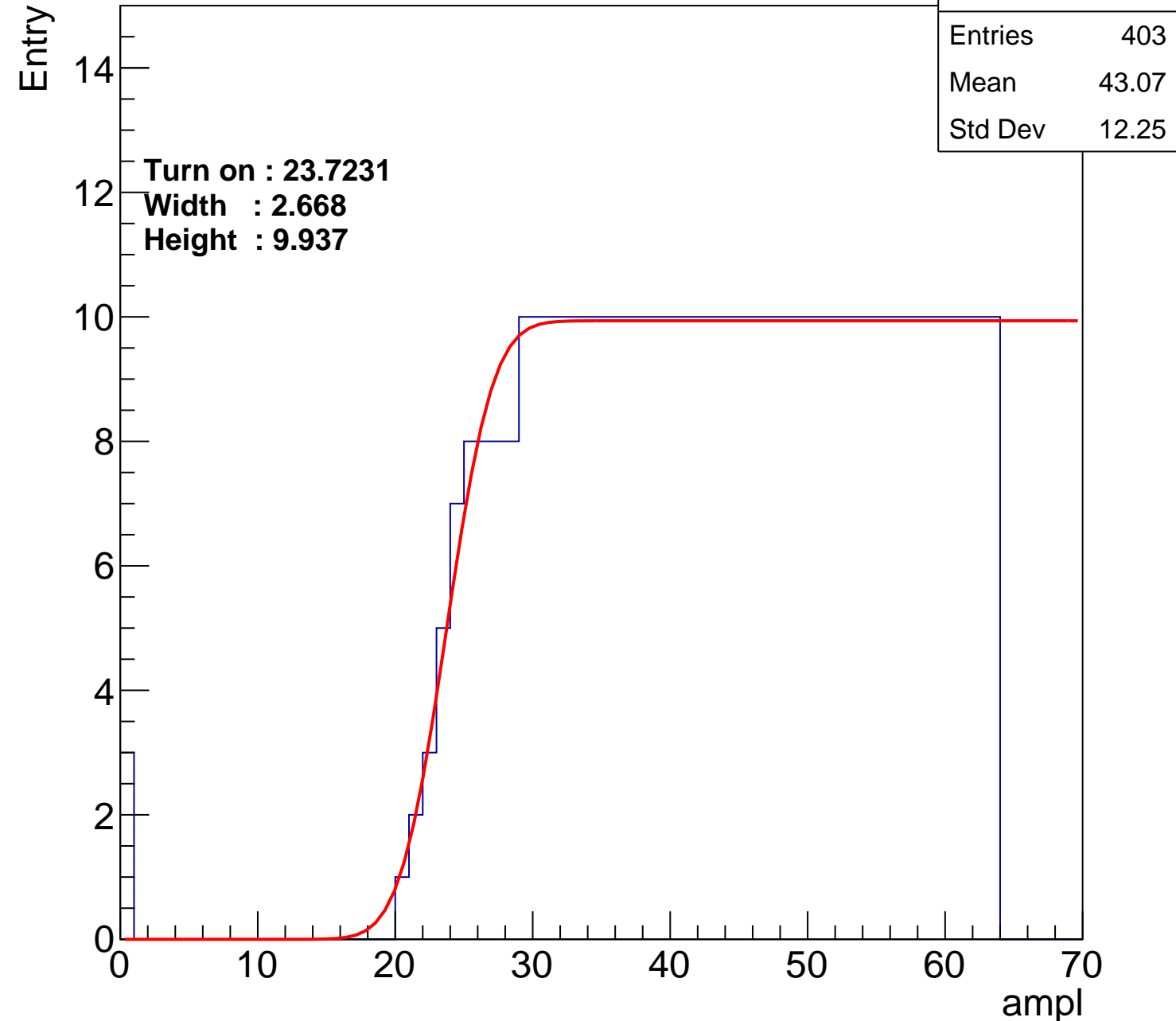
Width : 2.668

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 26.0746

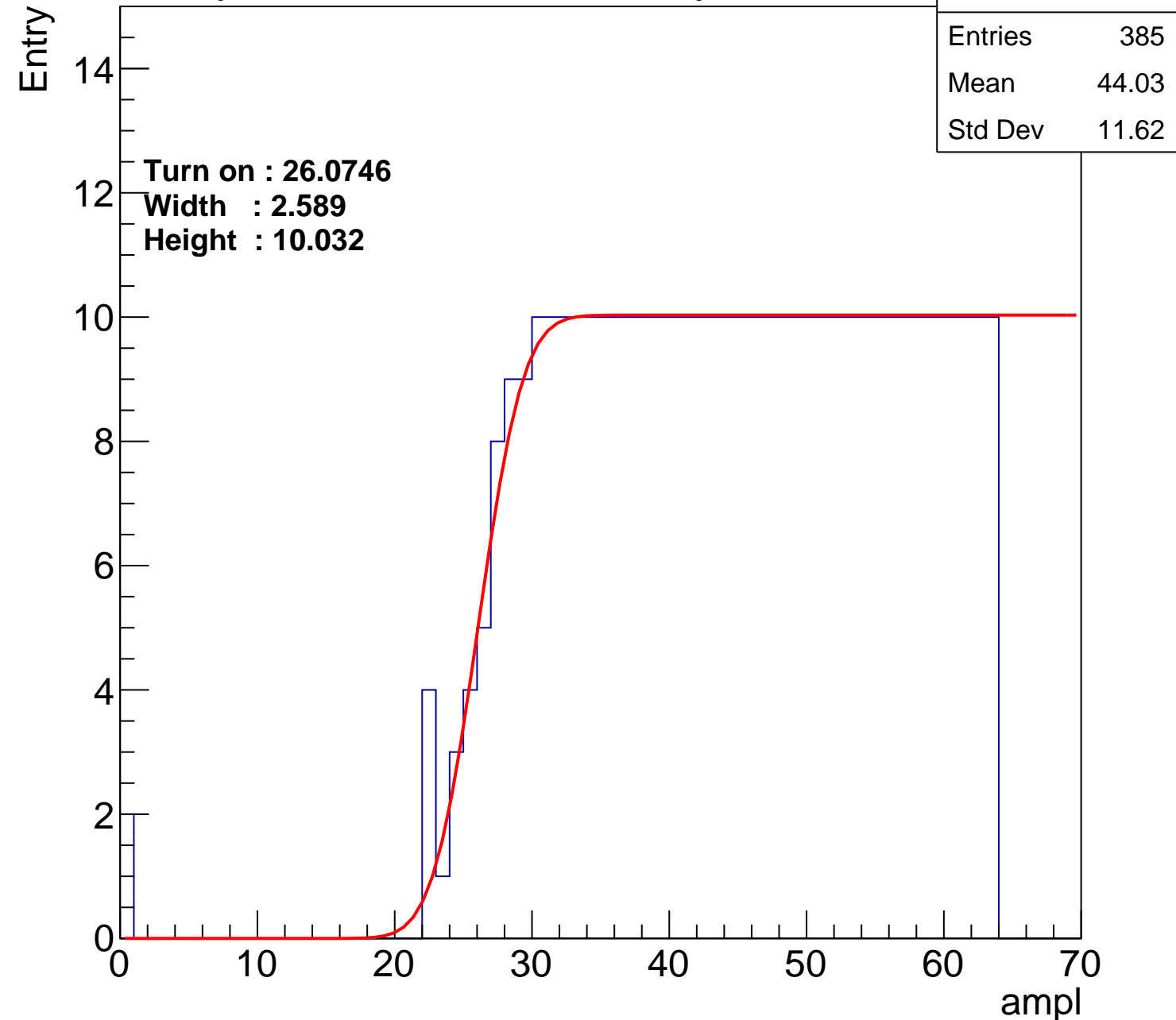
Width : 2.589

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch102

calib_packv5_042523_0143.root, FC#3, port B3

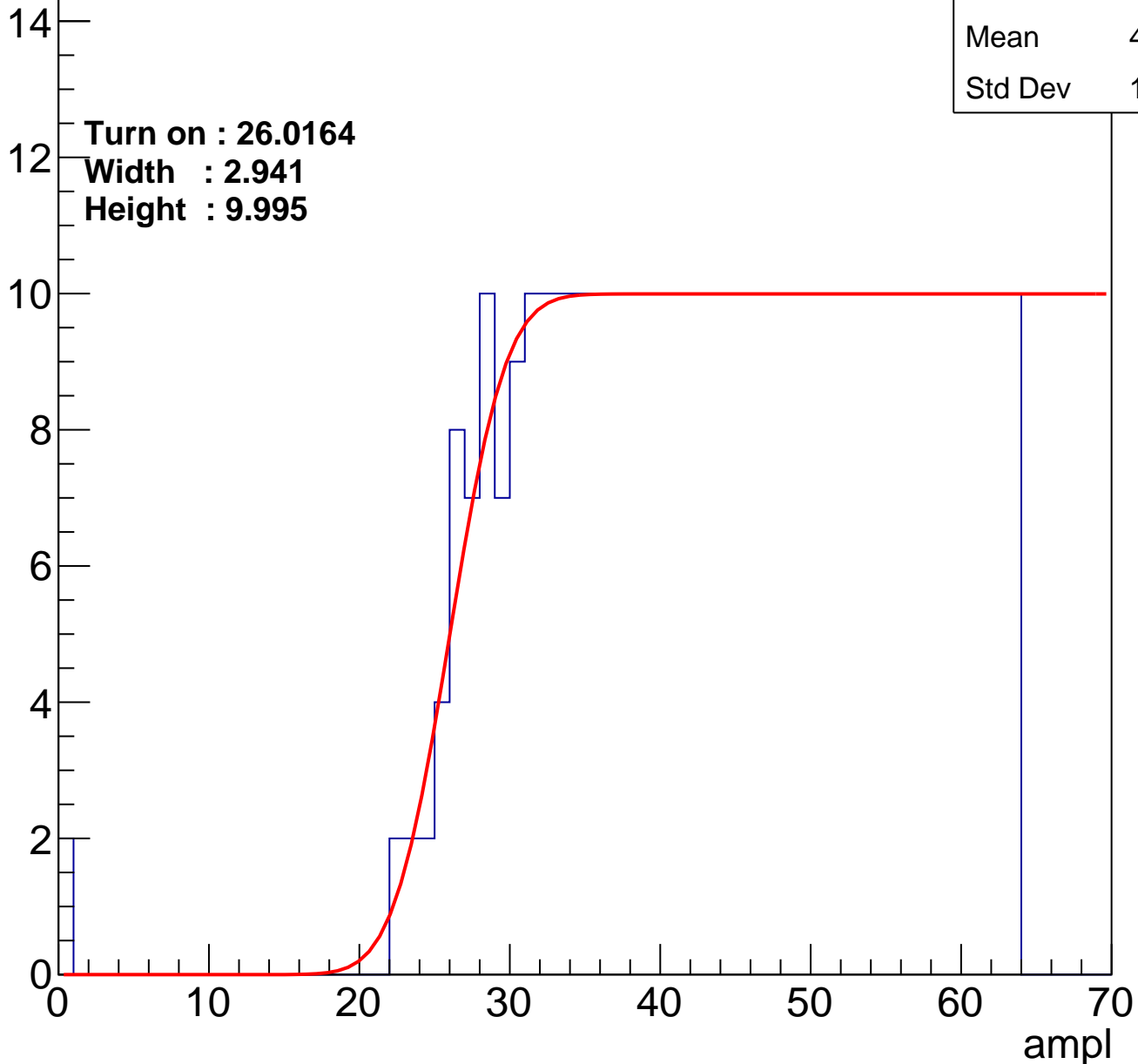
Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.0164

Width : 2.941

Height : 9.995

Entry



B1L000S, U13-ch103

calib_packv5_042523_0143.root, FC#3, port B3

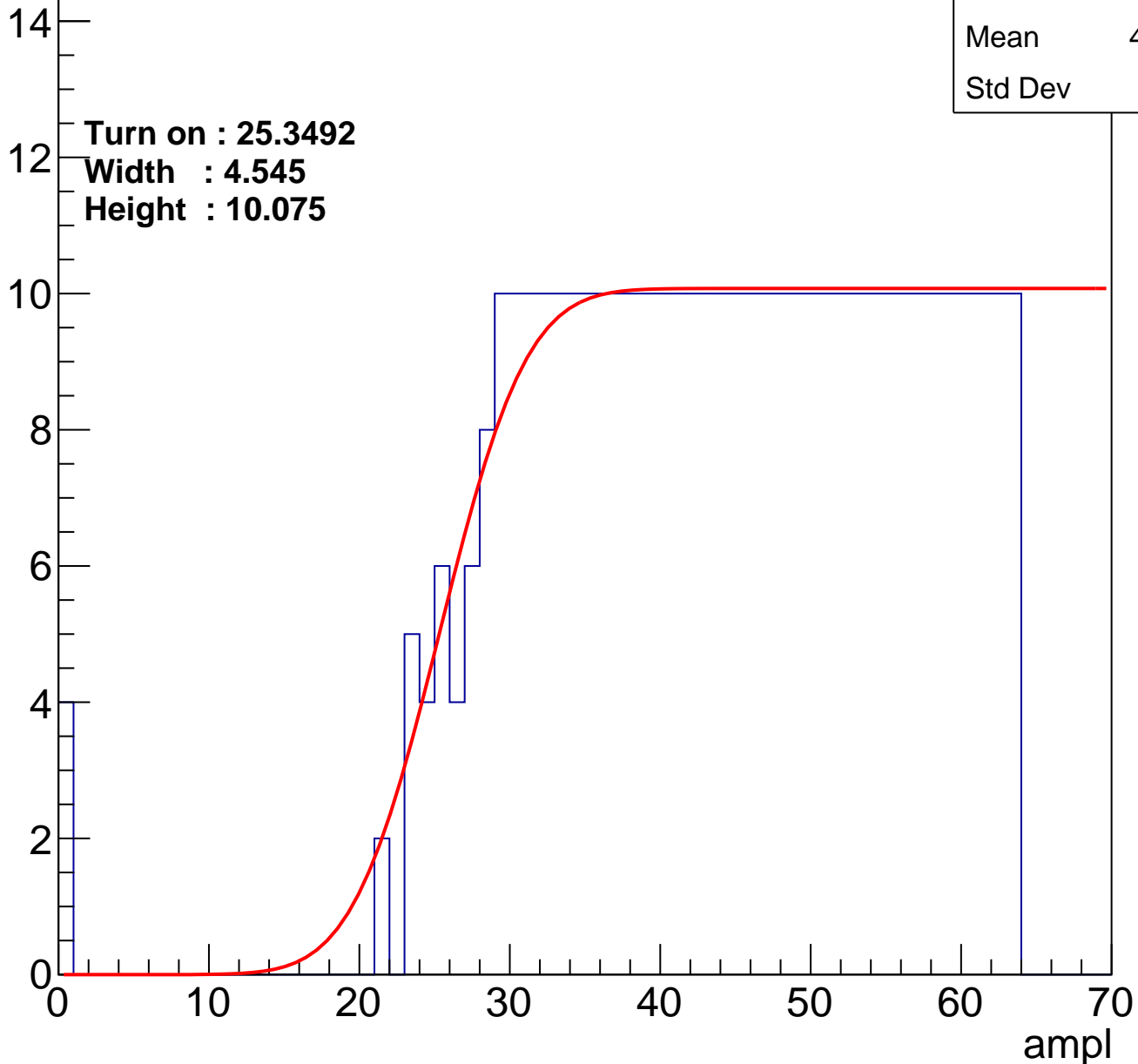
Entries	389
Mean	43.68
Std Dev	12.1

Turn on : 25.3492

Width : 4.545

Height : 10.075

Entry



B1L000S, U13-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.83
Std Dev	11.97

Turn on : 25.7075

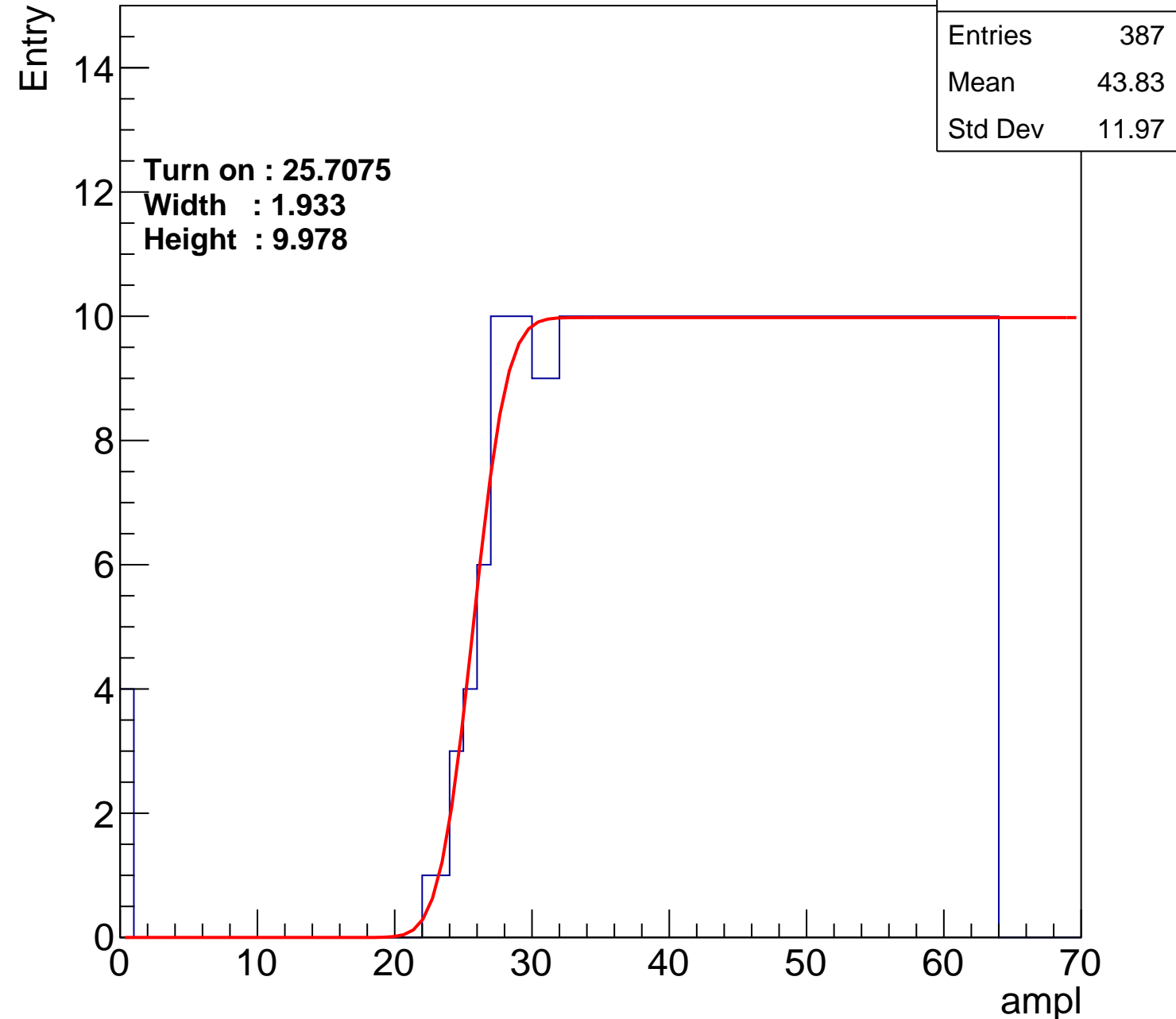
Width : 1.933

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 26.5492

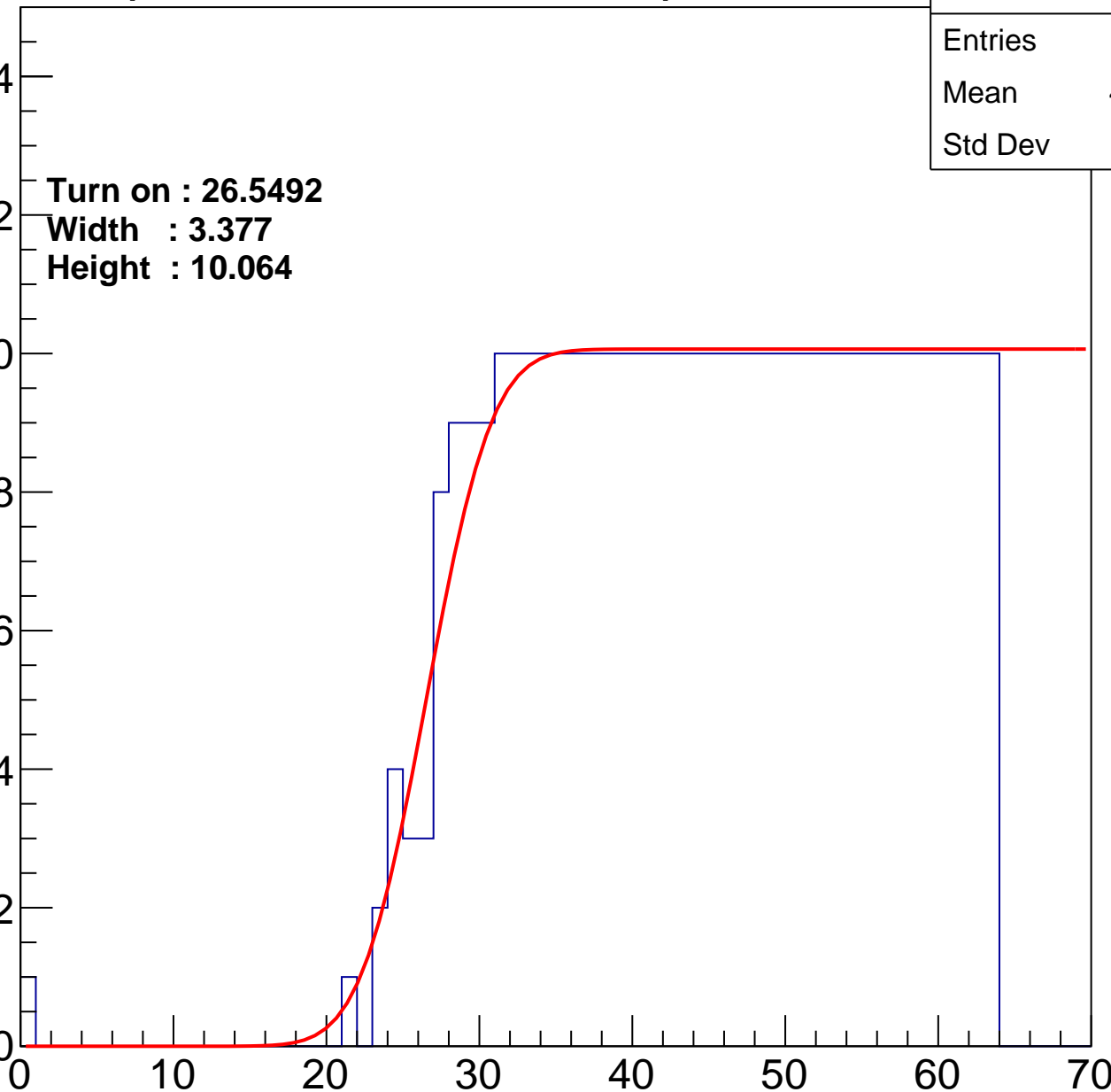
Width : 3.377

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.38
Std Dev	12.21

Turn on : 25.0073

Width : 1.608

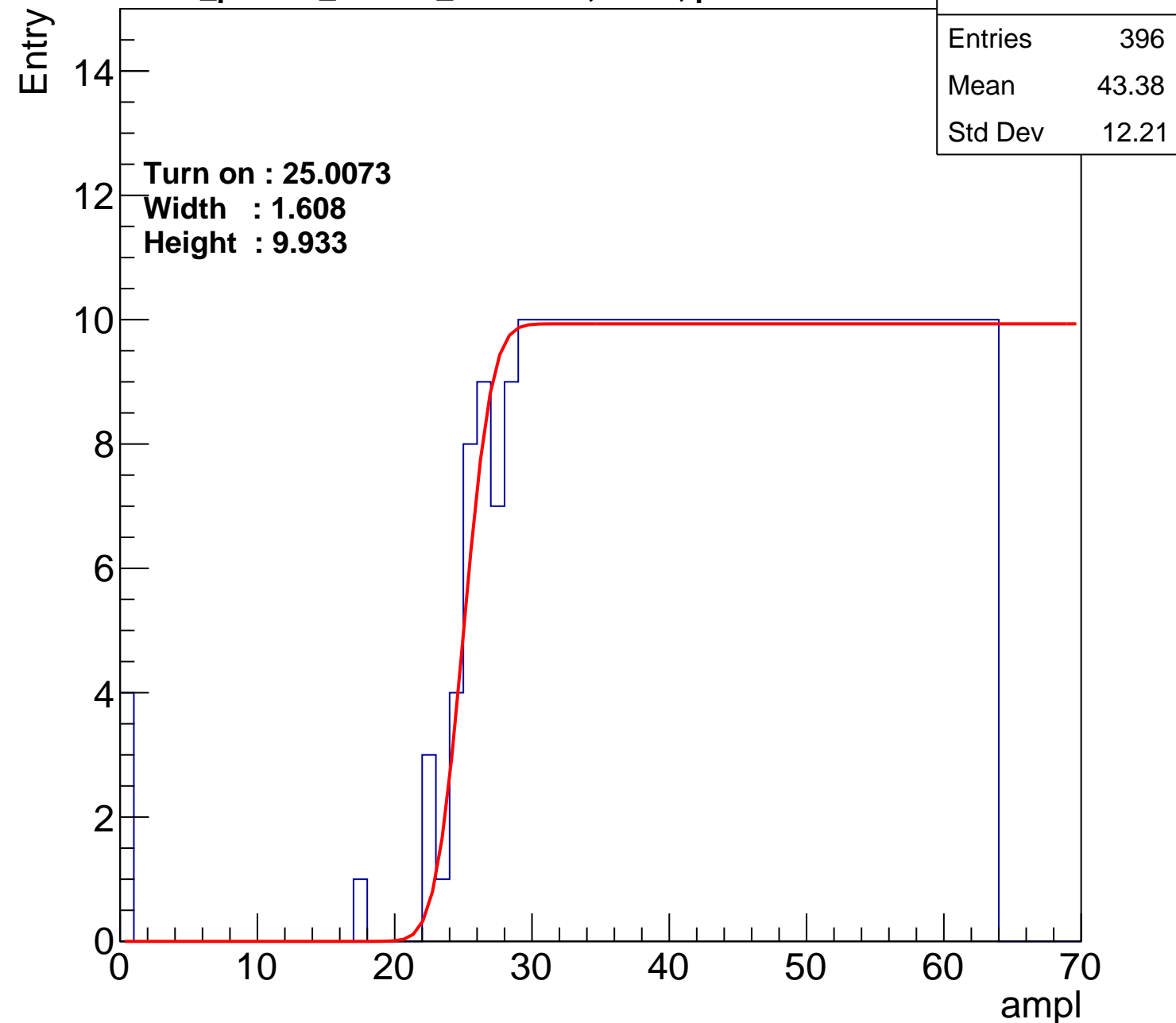
Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U13-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.5317

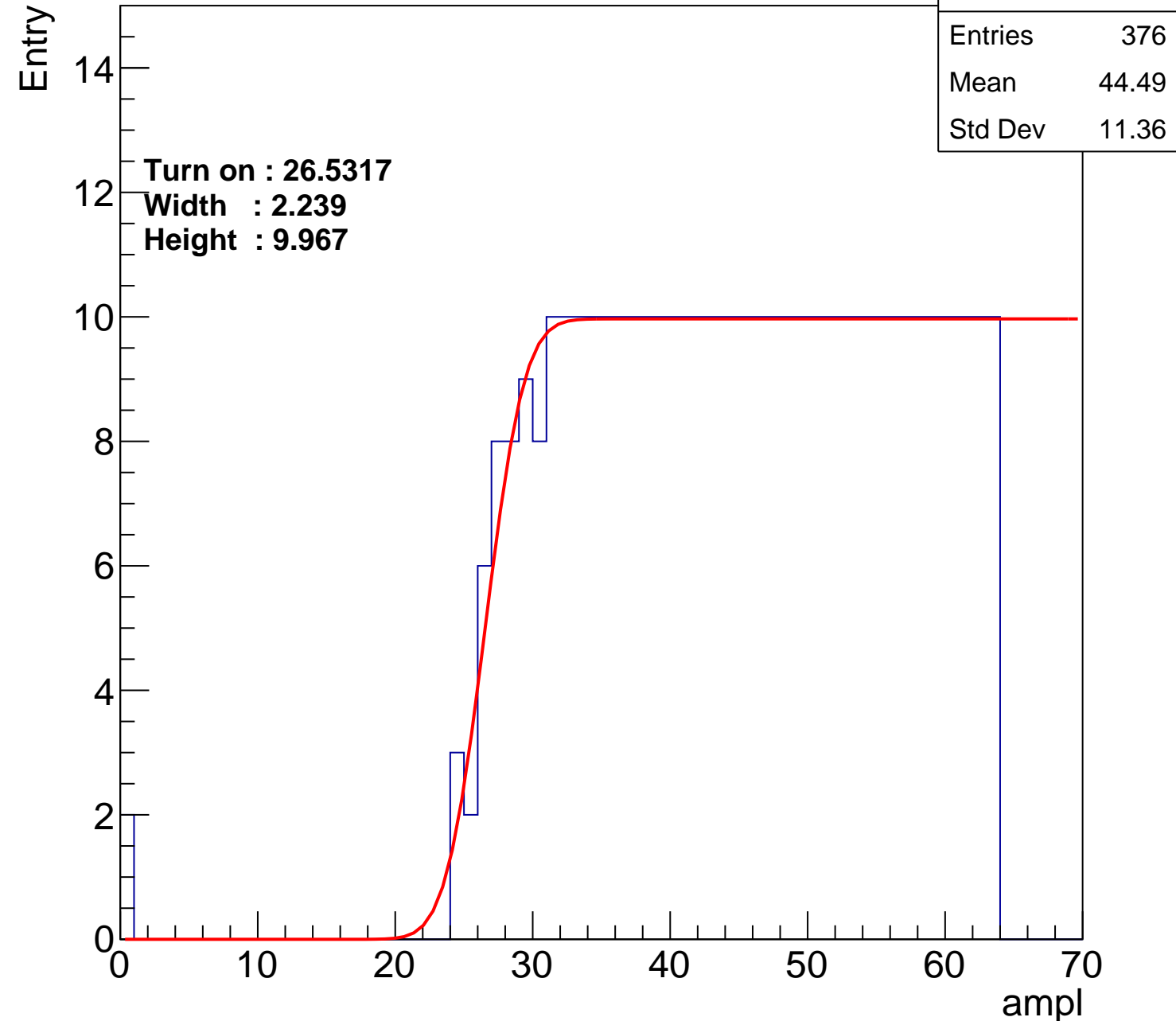
Width : 2.239

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.31
Std Dev	12.37

Turn on : 25.1041

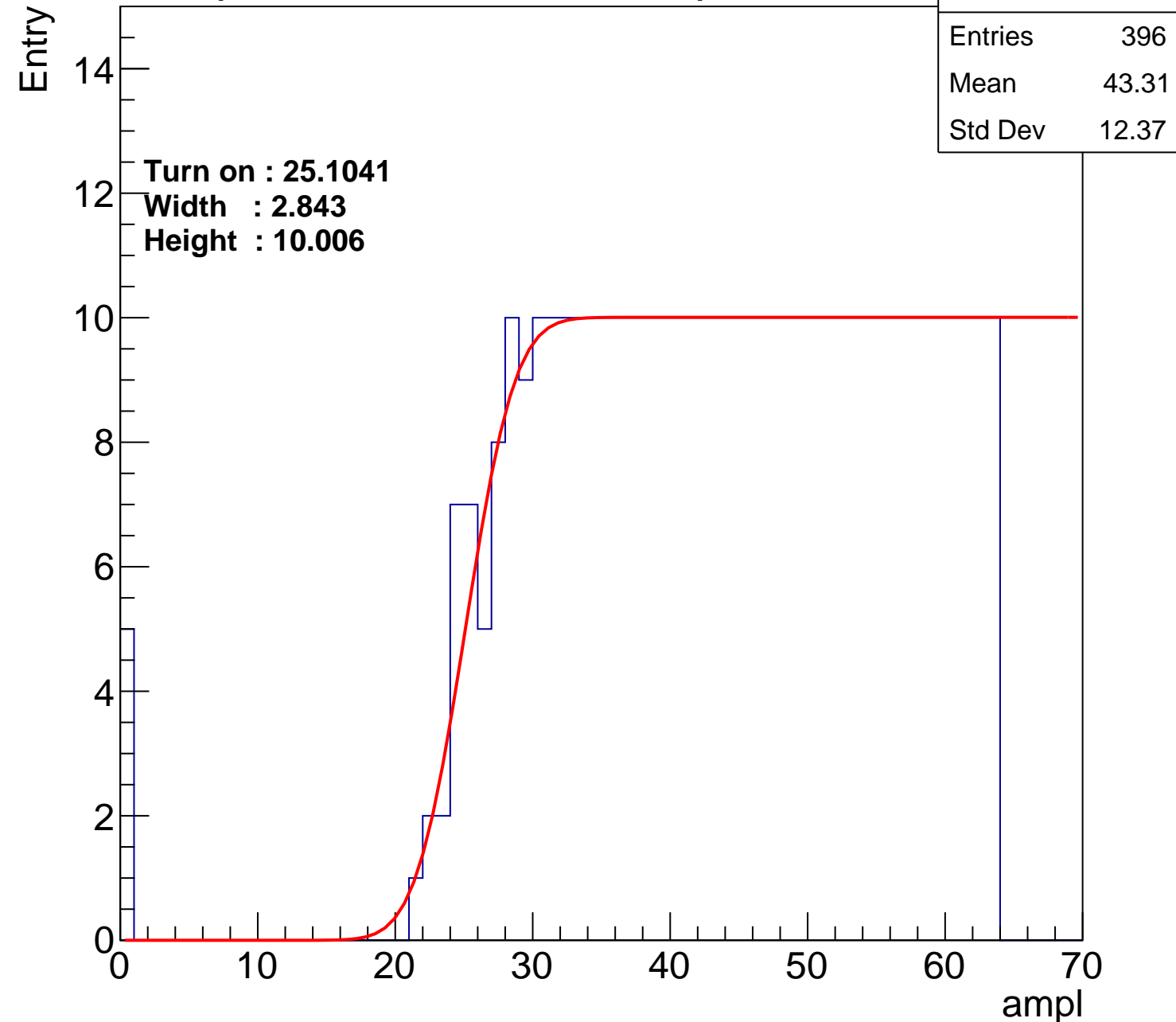
Width : 2.843

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch109

calib_packv5_042523_0143.root, FC#3, port B3

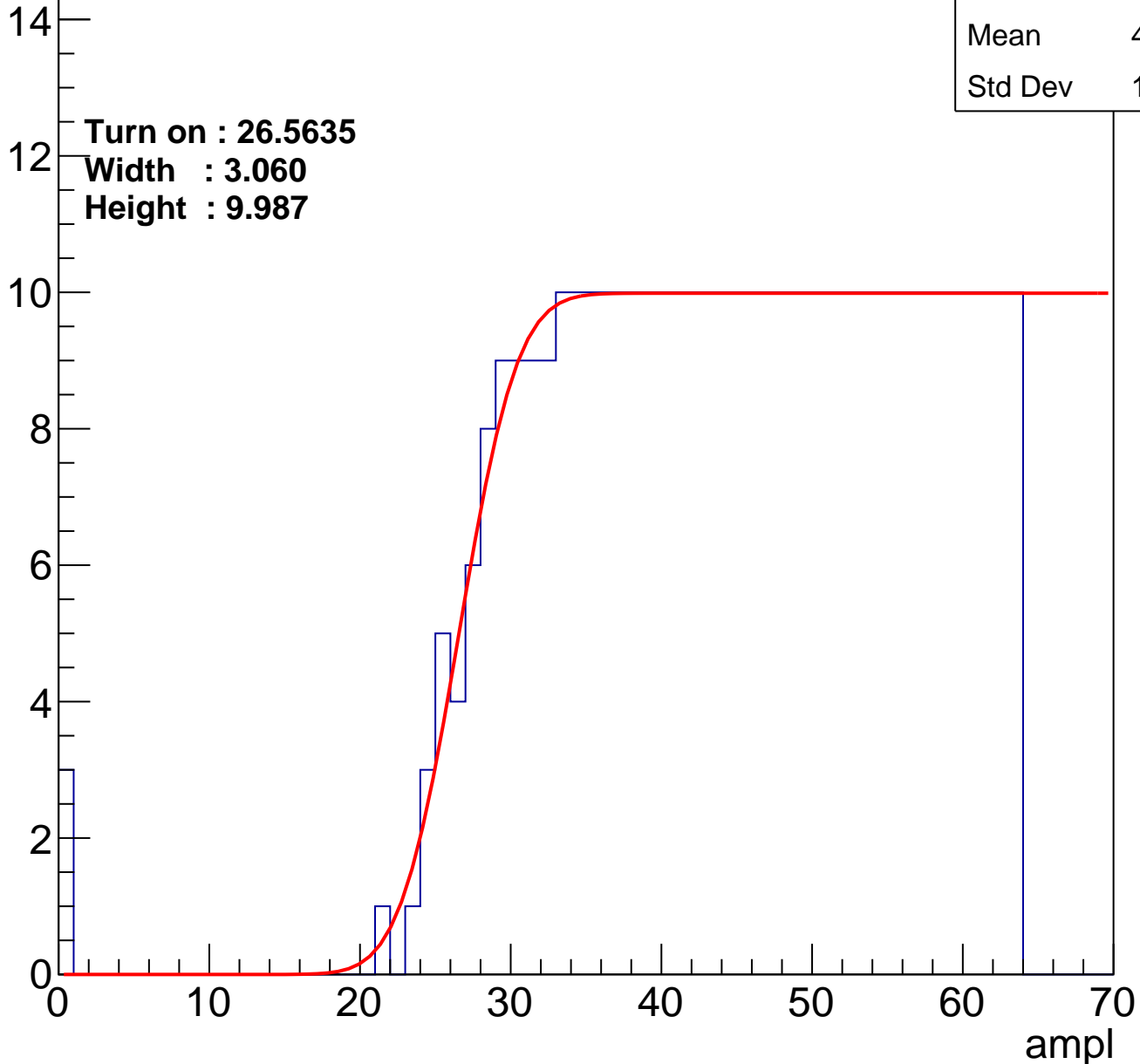
Entries	377
Mean	44.32
Std Dev	11.65

Turn on : 26.5635

Width : 3.060

Height : 9.987

Entry



B1L000S, U13-ch110

calib_packv5_042523_0143.root, FC#3, port B3

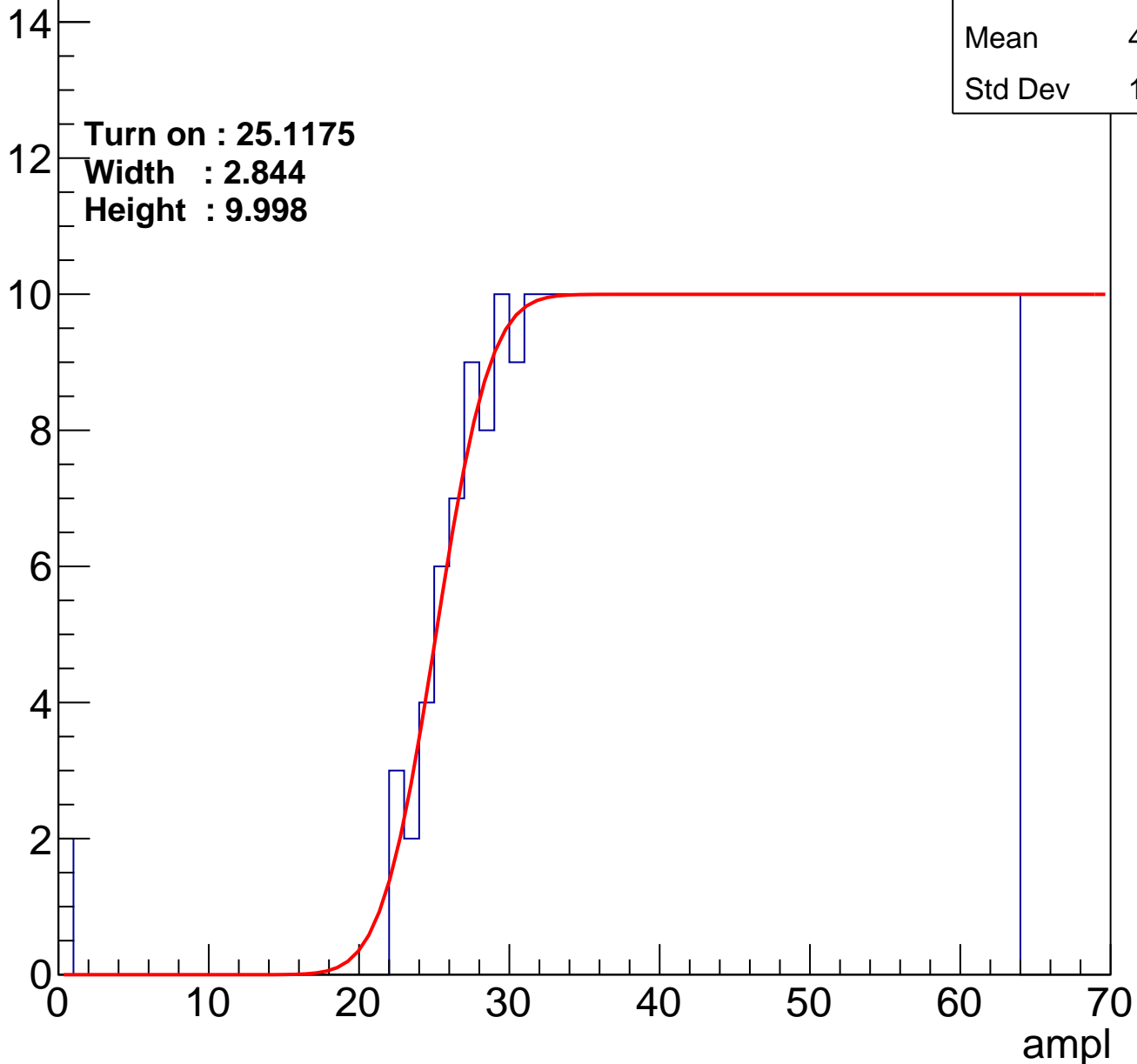
Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.1175

Width : 2.844

Height : 9.998

Entry



B1L000S, U13-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.44
Std Dev	11.7

Turn on : 27.2417

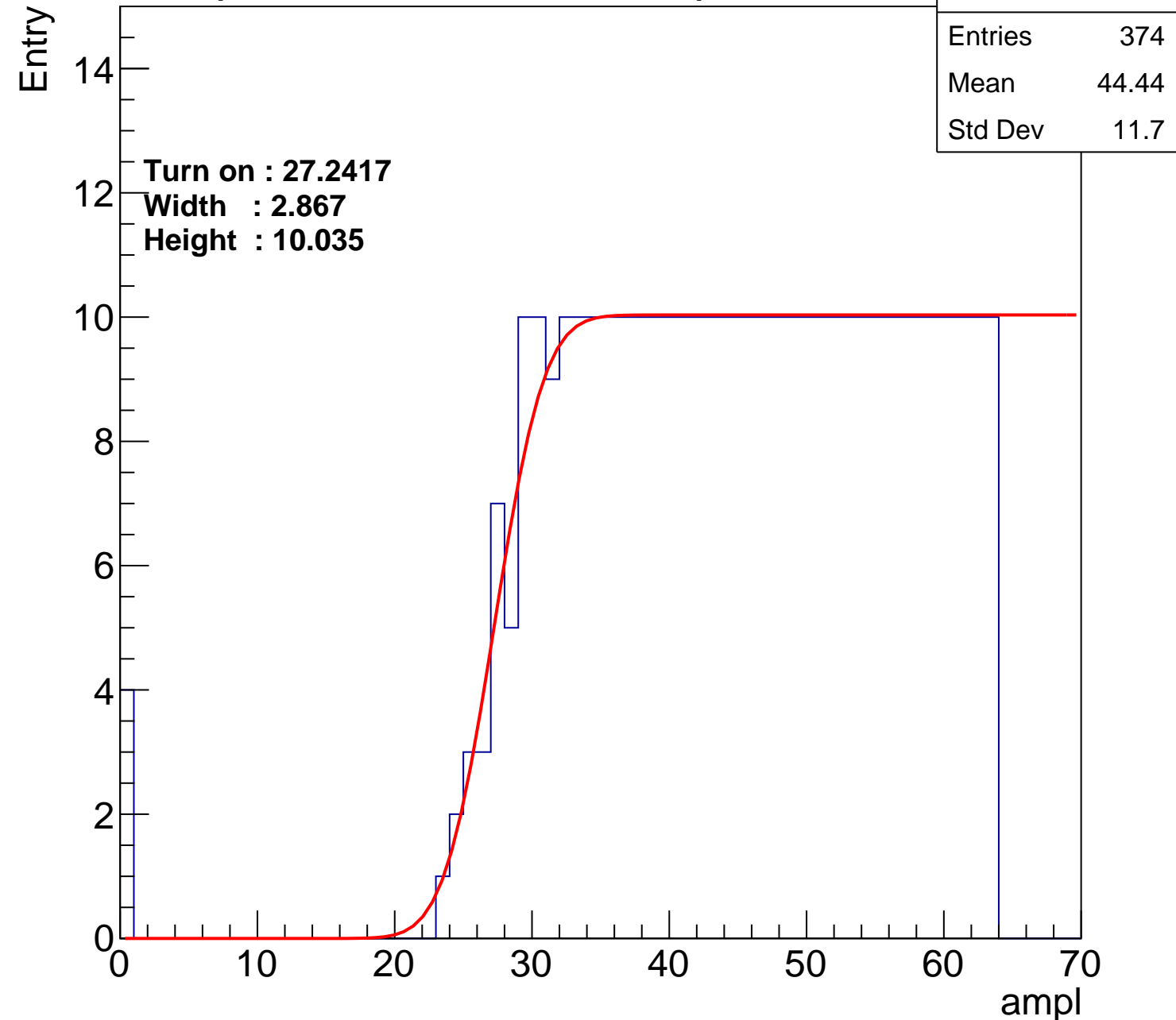
Width : 2.867

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch112

calib_packv5_042523_0143.root, FC#3, port B3

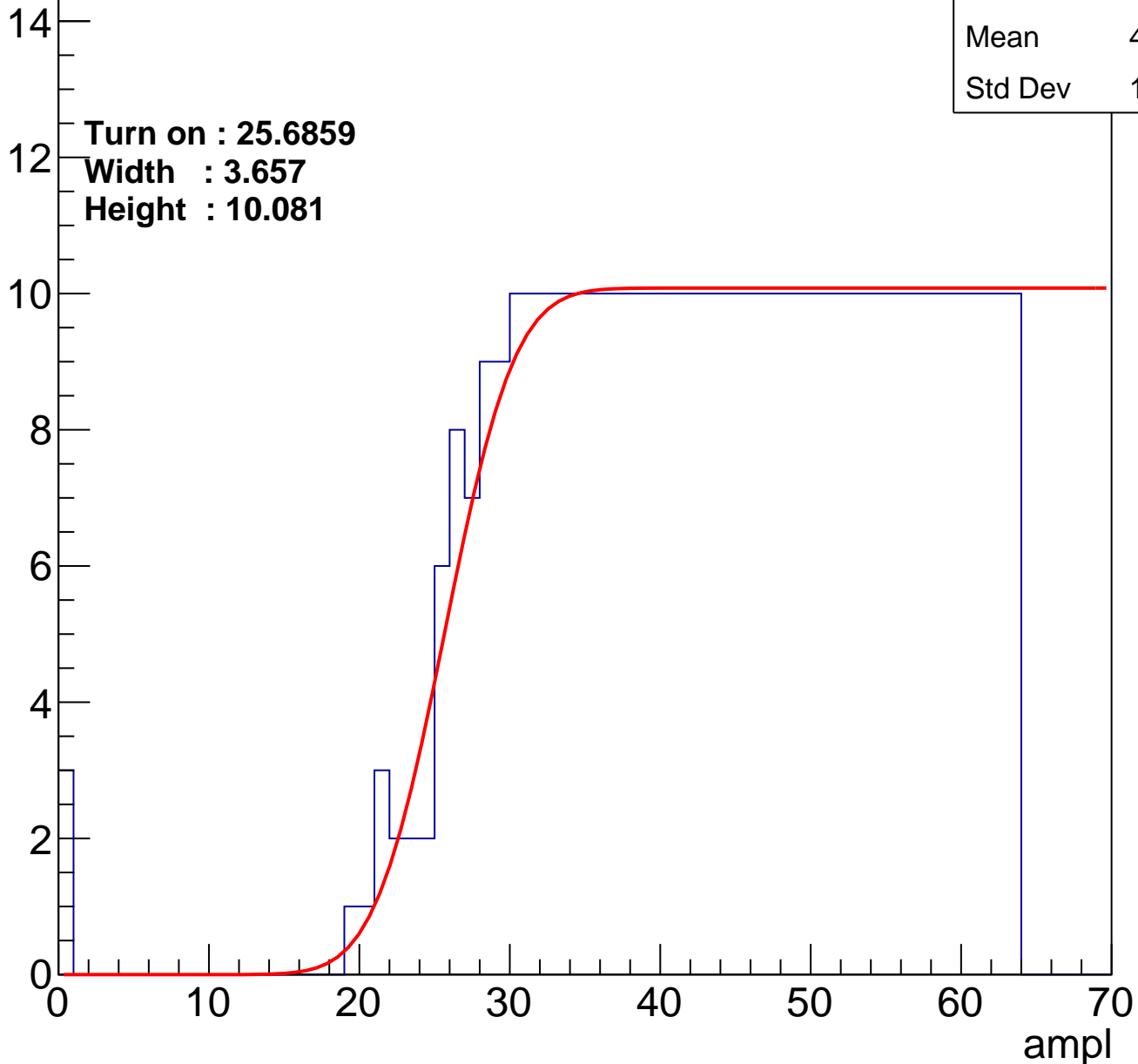
Entries	393
Mean	43.54
Std Dev	12.06

Turn on : 25.6859

Width : 3.657

Height : 10.081

Entry



B1L000S, U13-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.36
Std Dev	12.12

Turn on : 24.6957

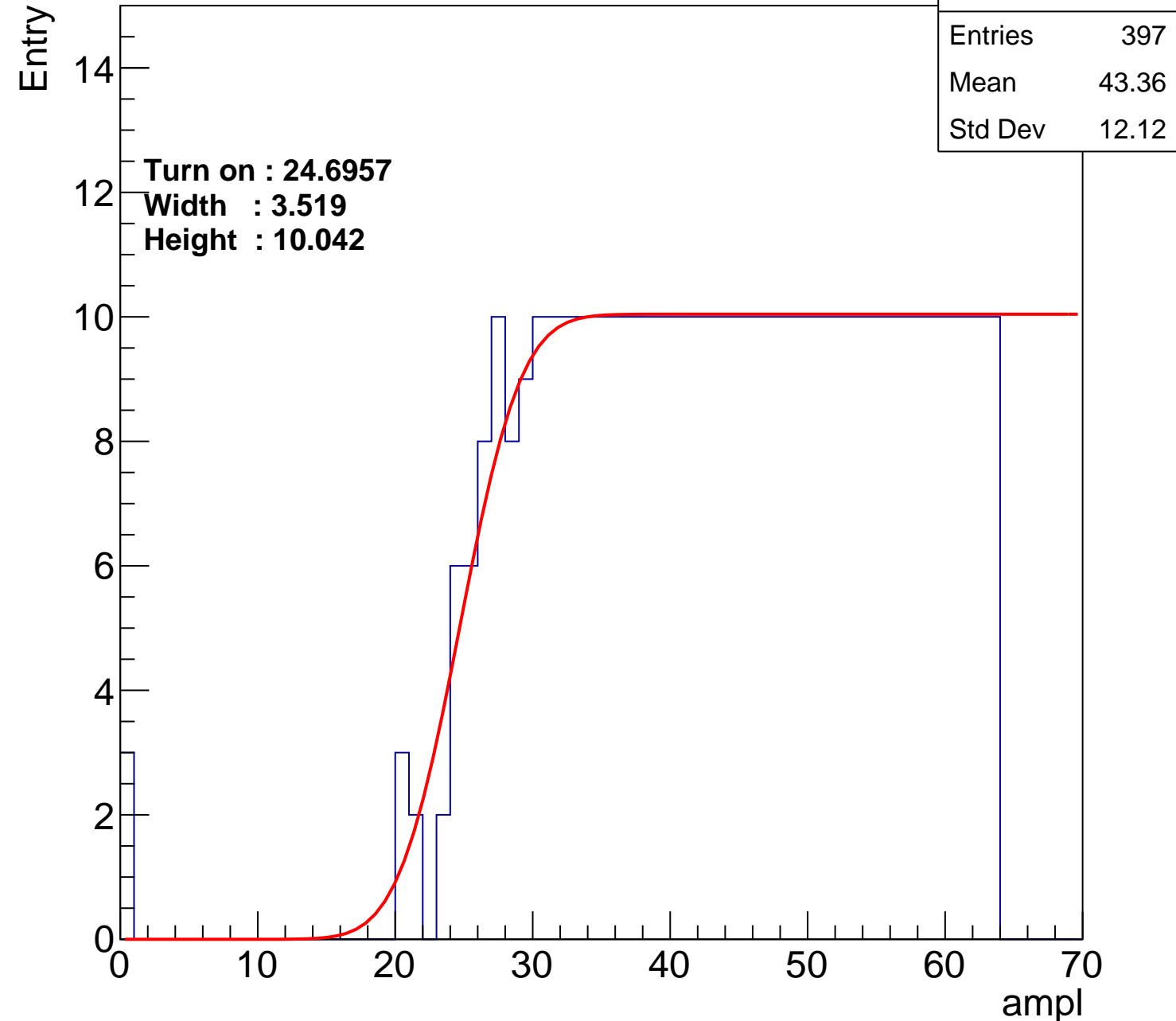
Width : 3.519

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.09
Std Dev	12.05

Turn on : 26.6151

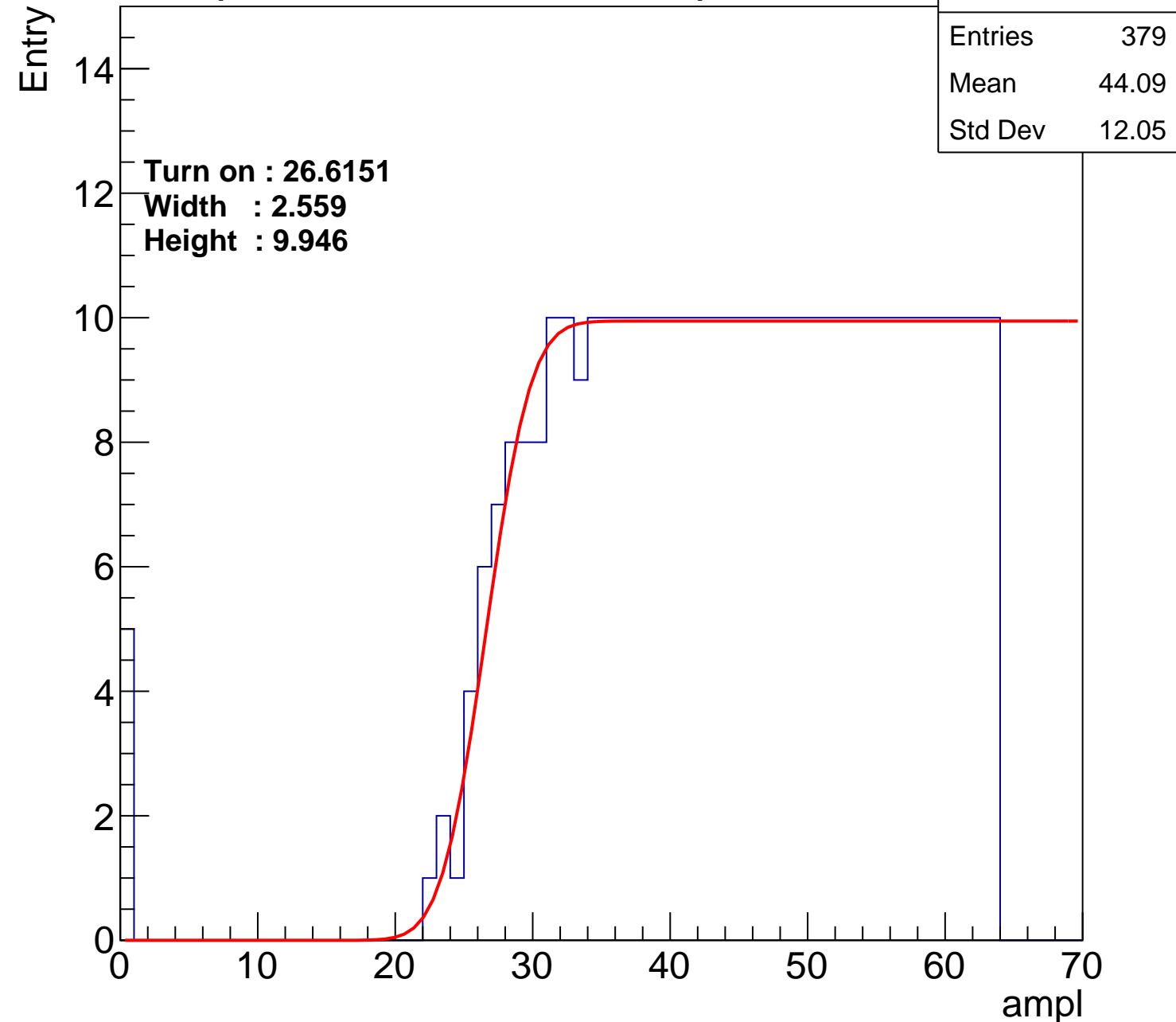
Width : 2.559

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.23
Std Dev	12.18

Turn on : 24.7994

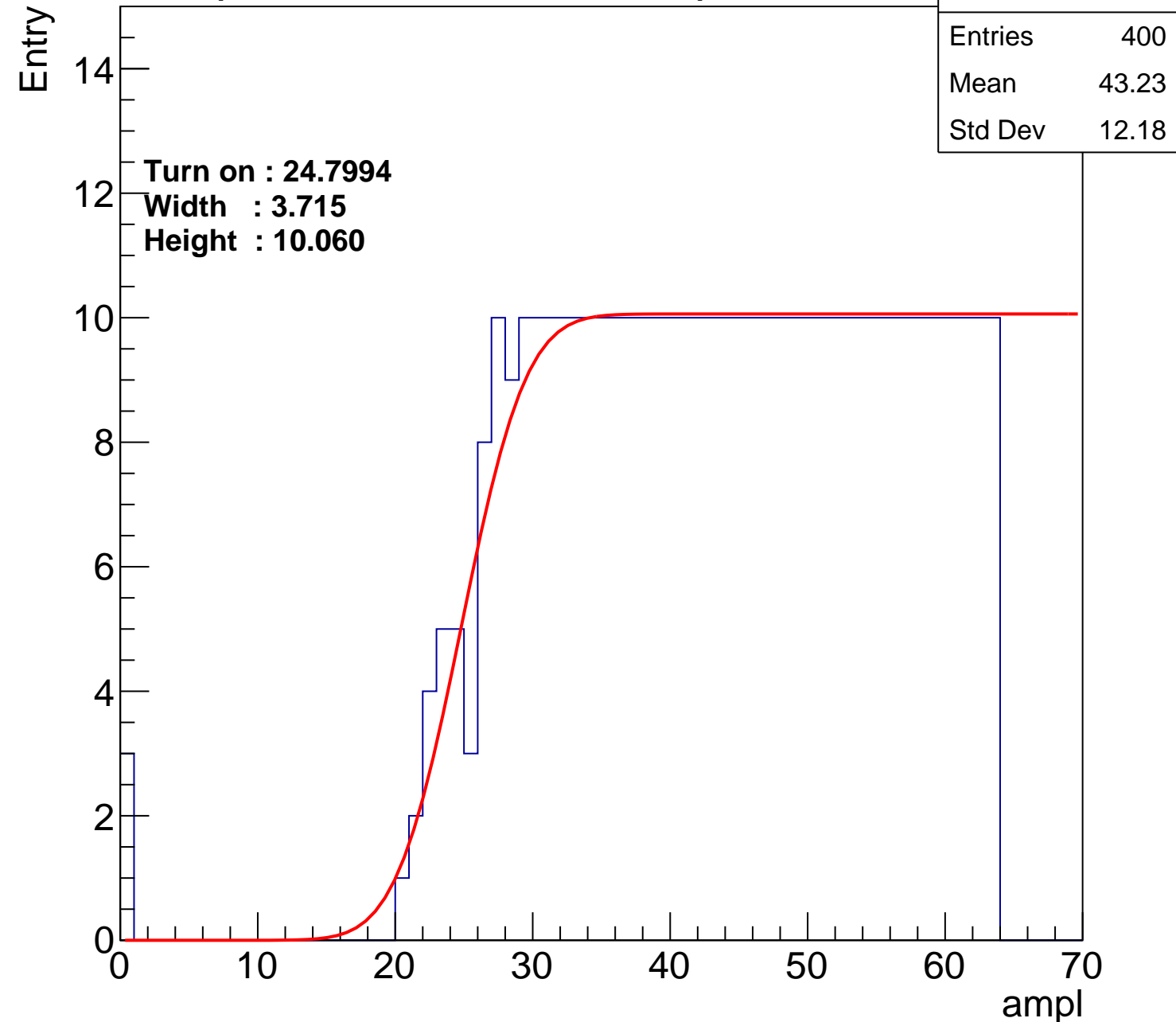
Width : 3.715

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.63
Std Dev	11.71

Turn on : 24.5876

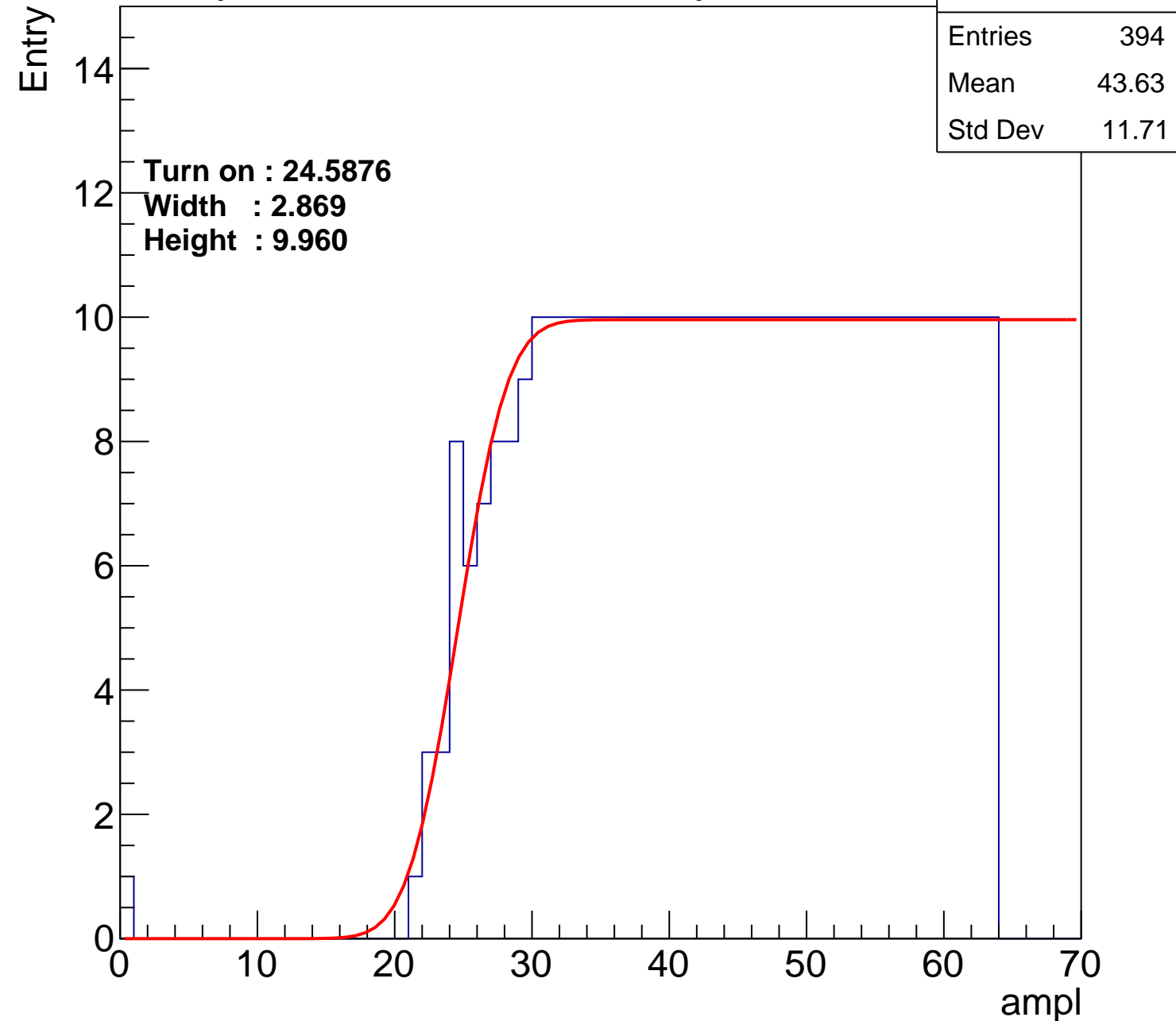
Width : 2.869

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.21
Std Dev	12.2

Turn on : 24.7930

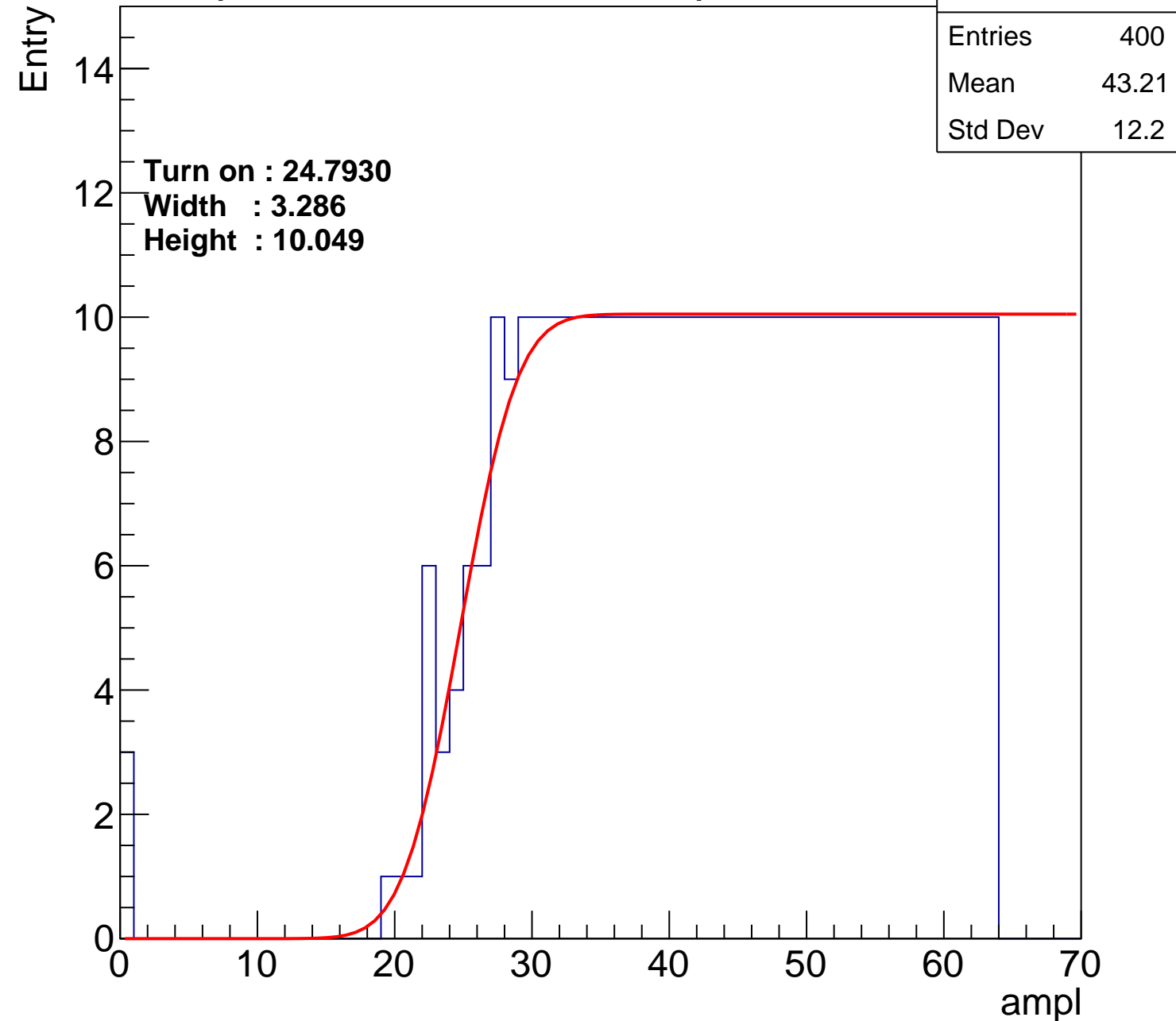
Width : 3.286

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.42
Std Dev	11.61

Turn on : 26.7690

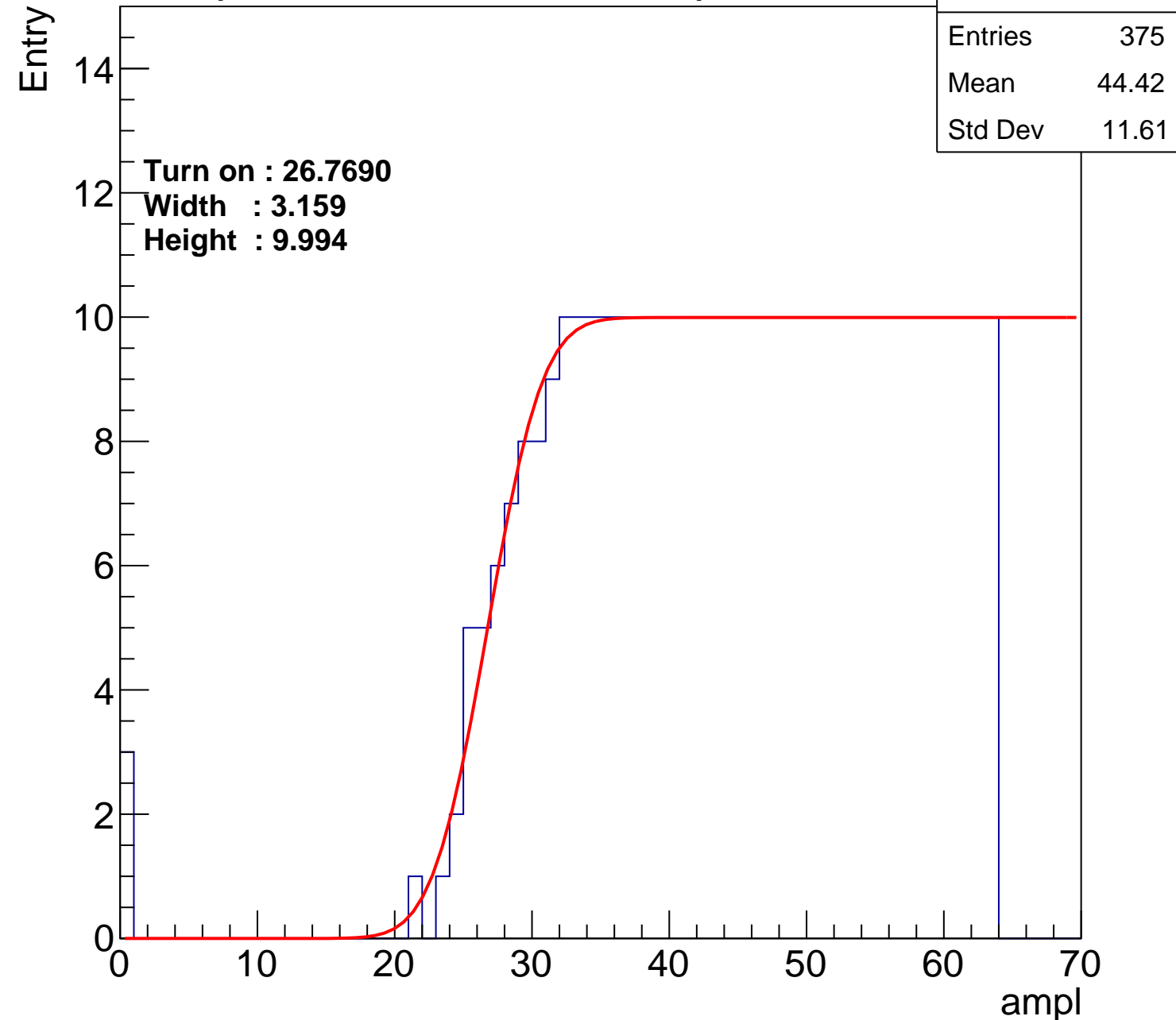
Width : 3.159

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.24
Std Dev	12.2

Turn on : 24.0590

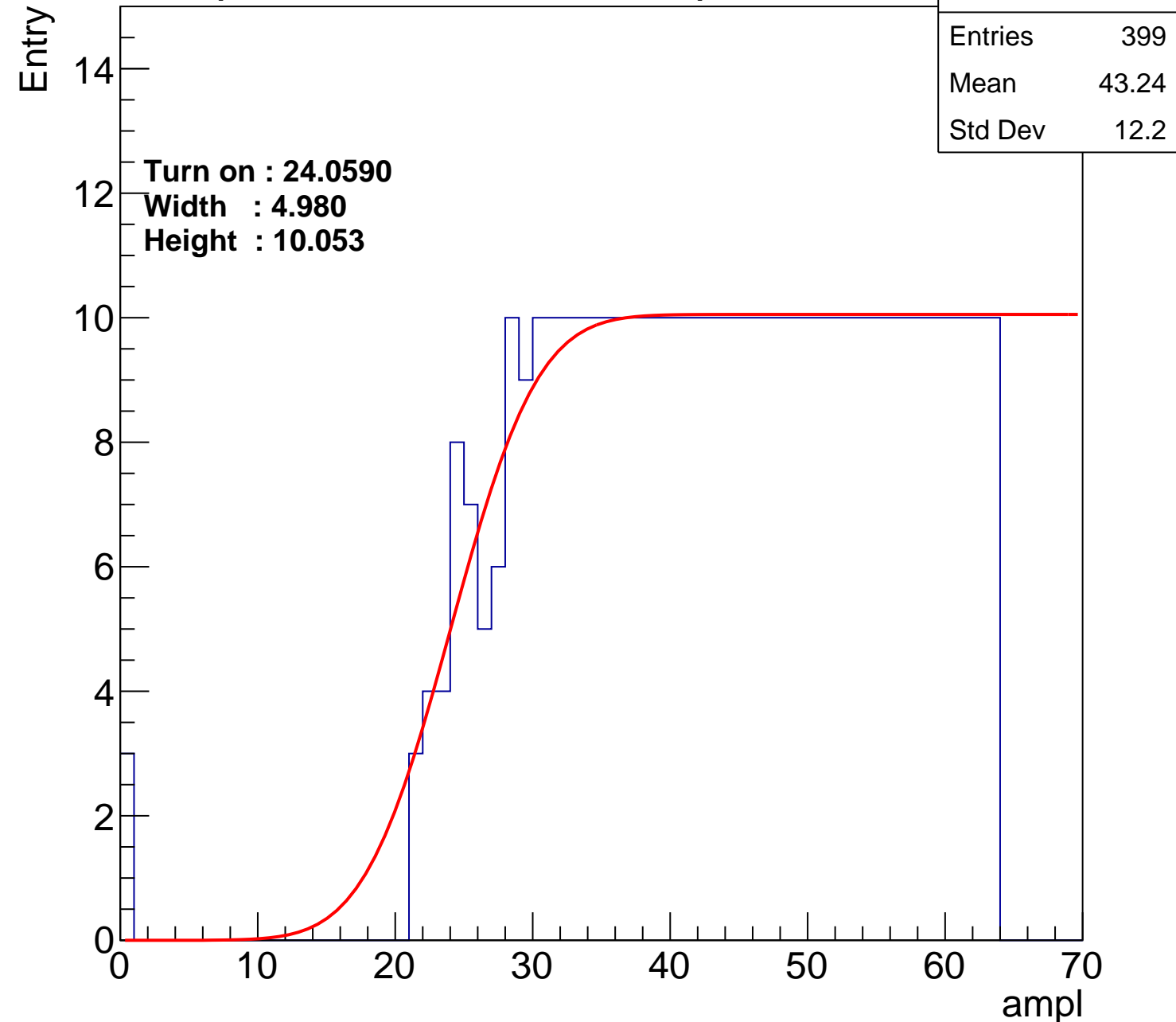
Width : 4.980

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.39
Std Dev	11.41

Turn on : 26.5565

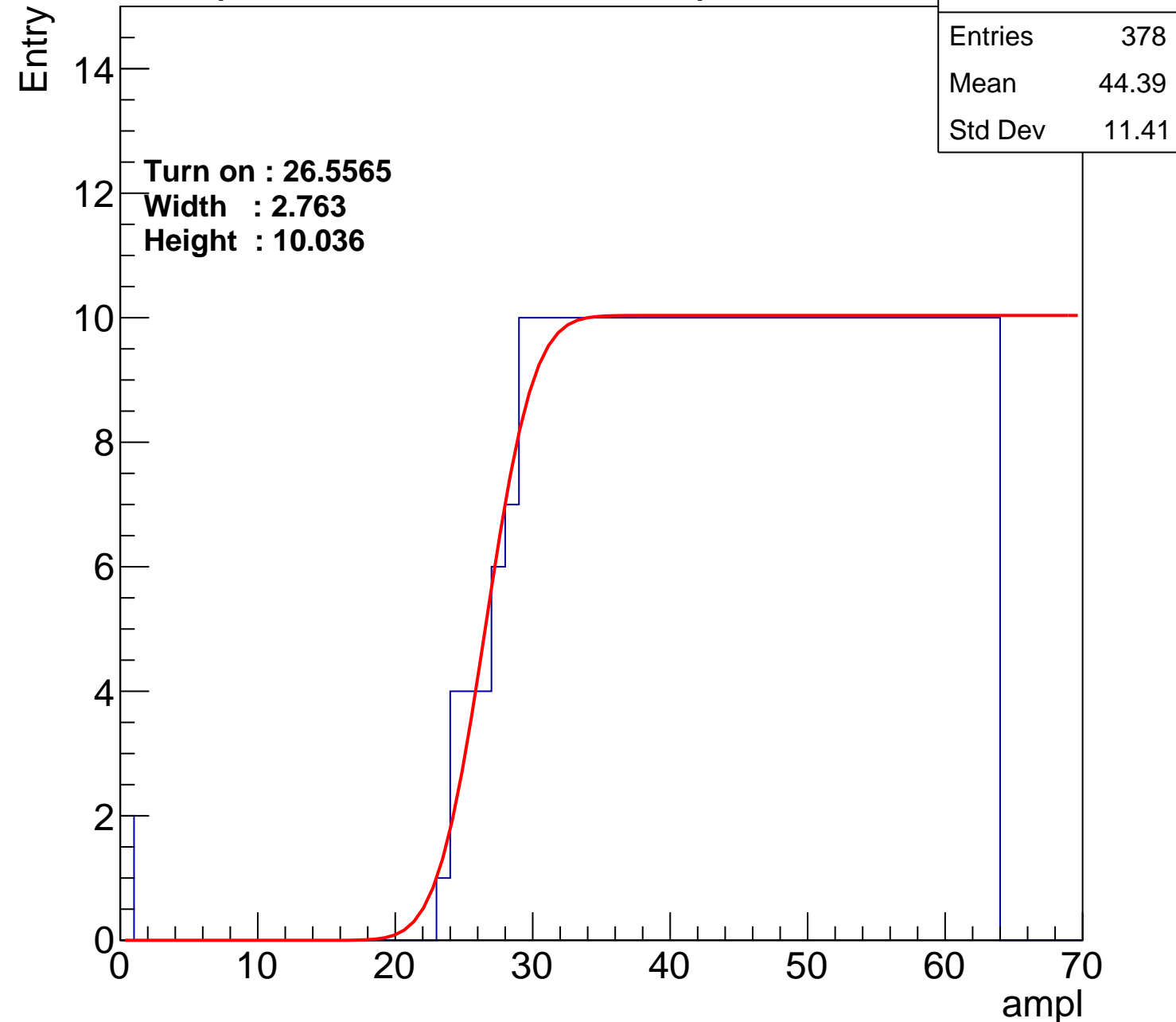
Width : 2.763

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.3726

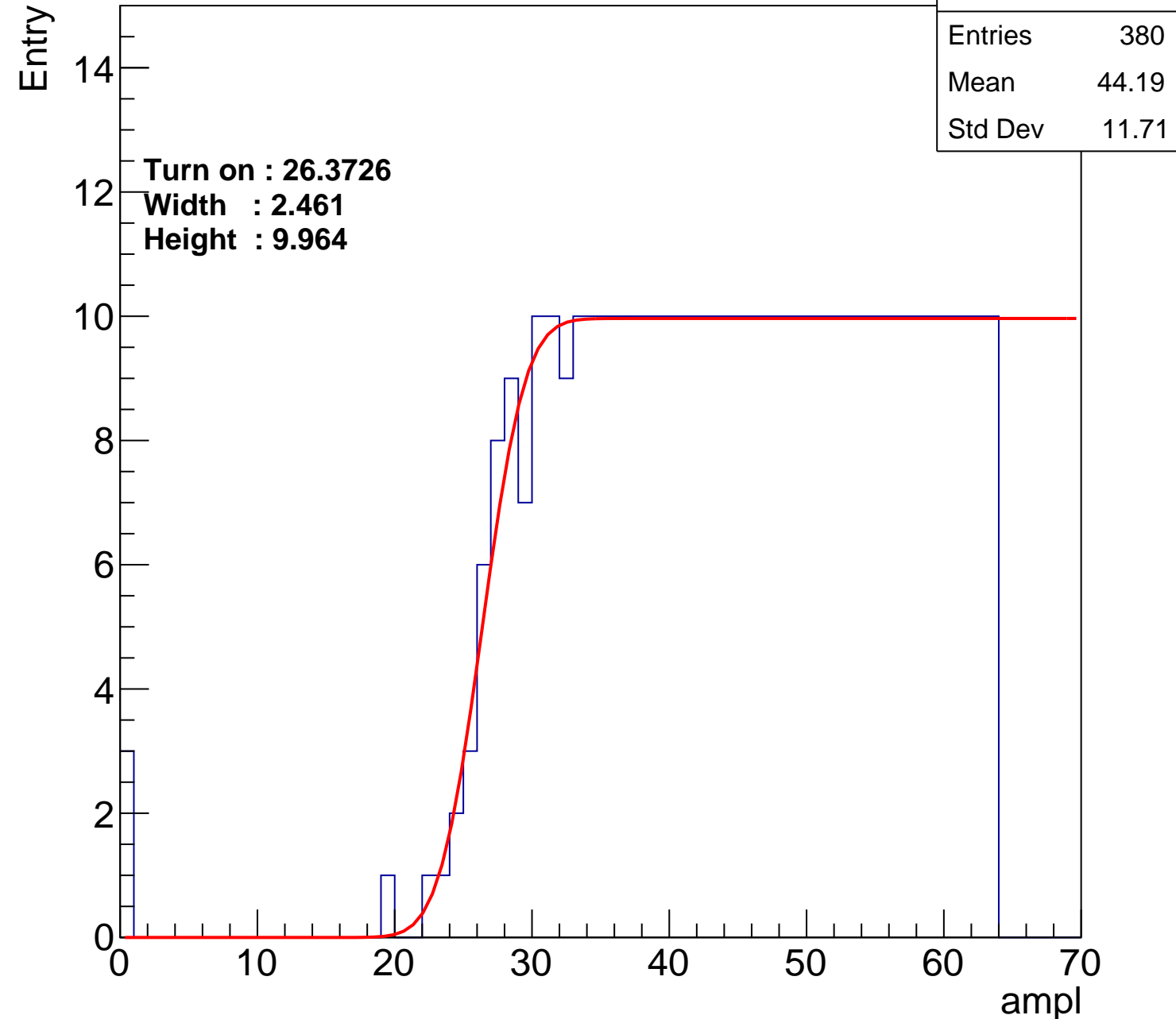
Width : 2.461

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.73
Std Dev	11.66

Turn on : 24.7885

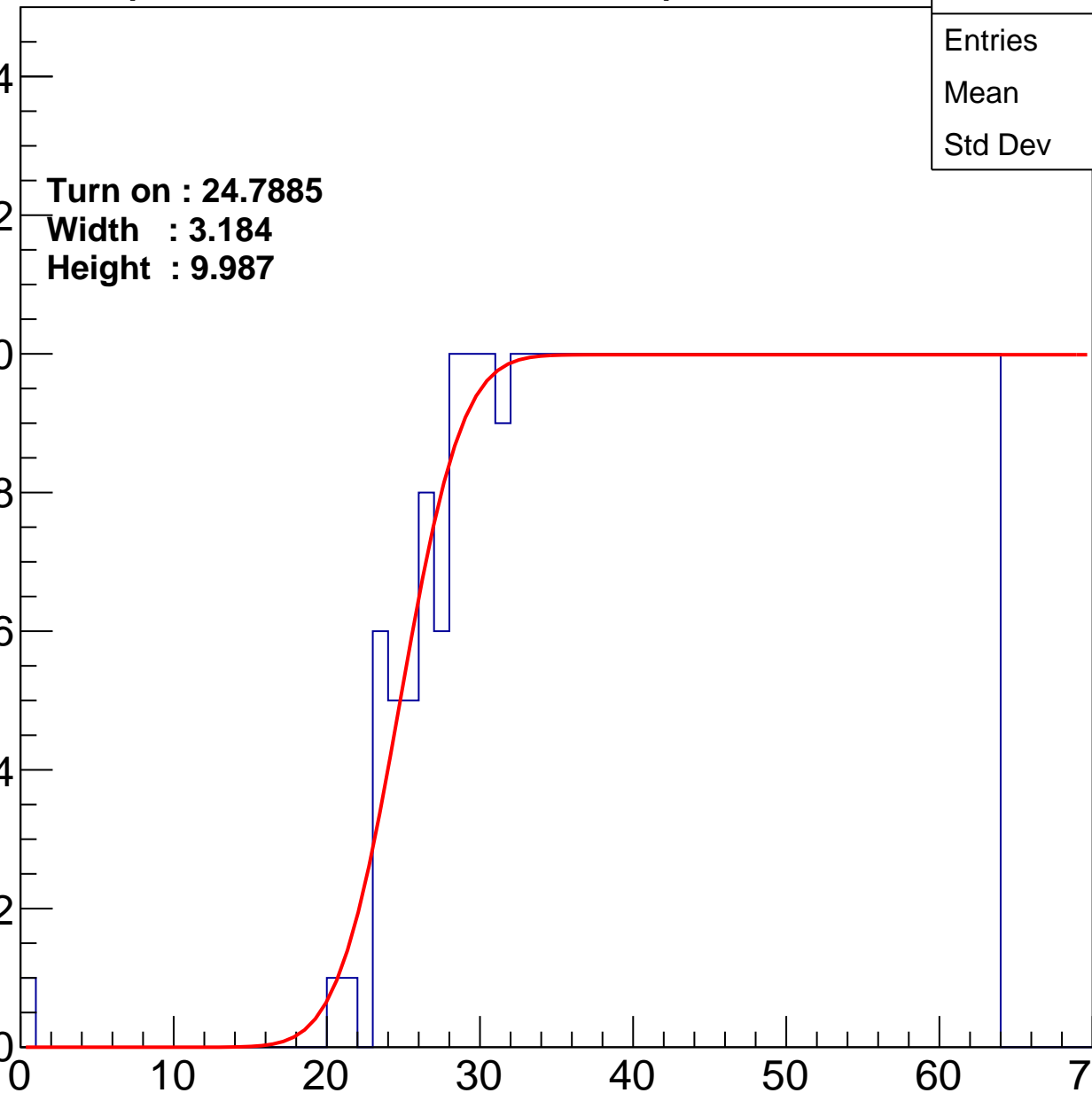
Width : 3.184

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.41
Std Dev	11.82

Turn on : 24.7153

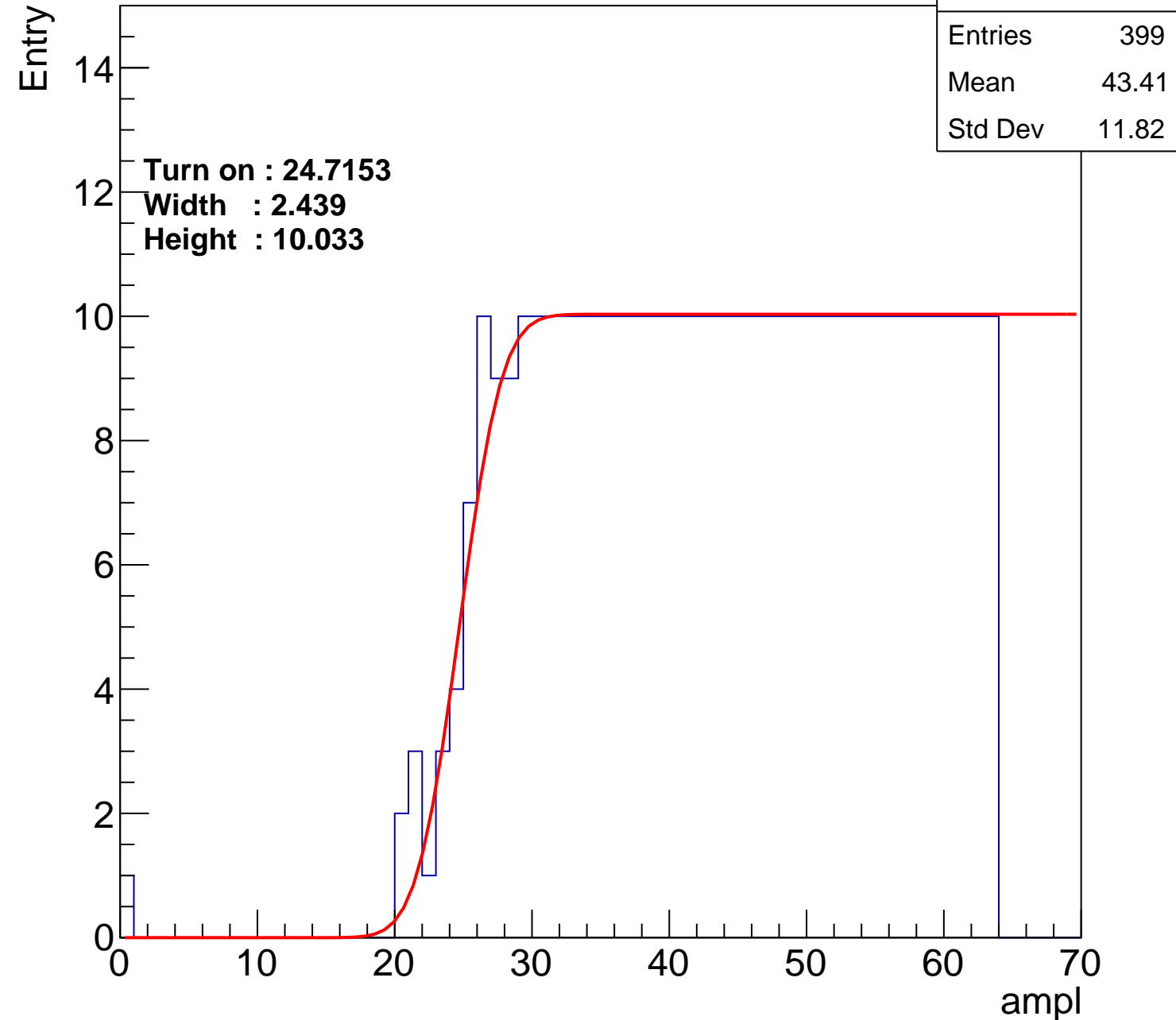
Width : 2.439

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.71
Std Dev	11.94

Turn on : 25.6285

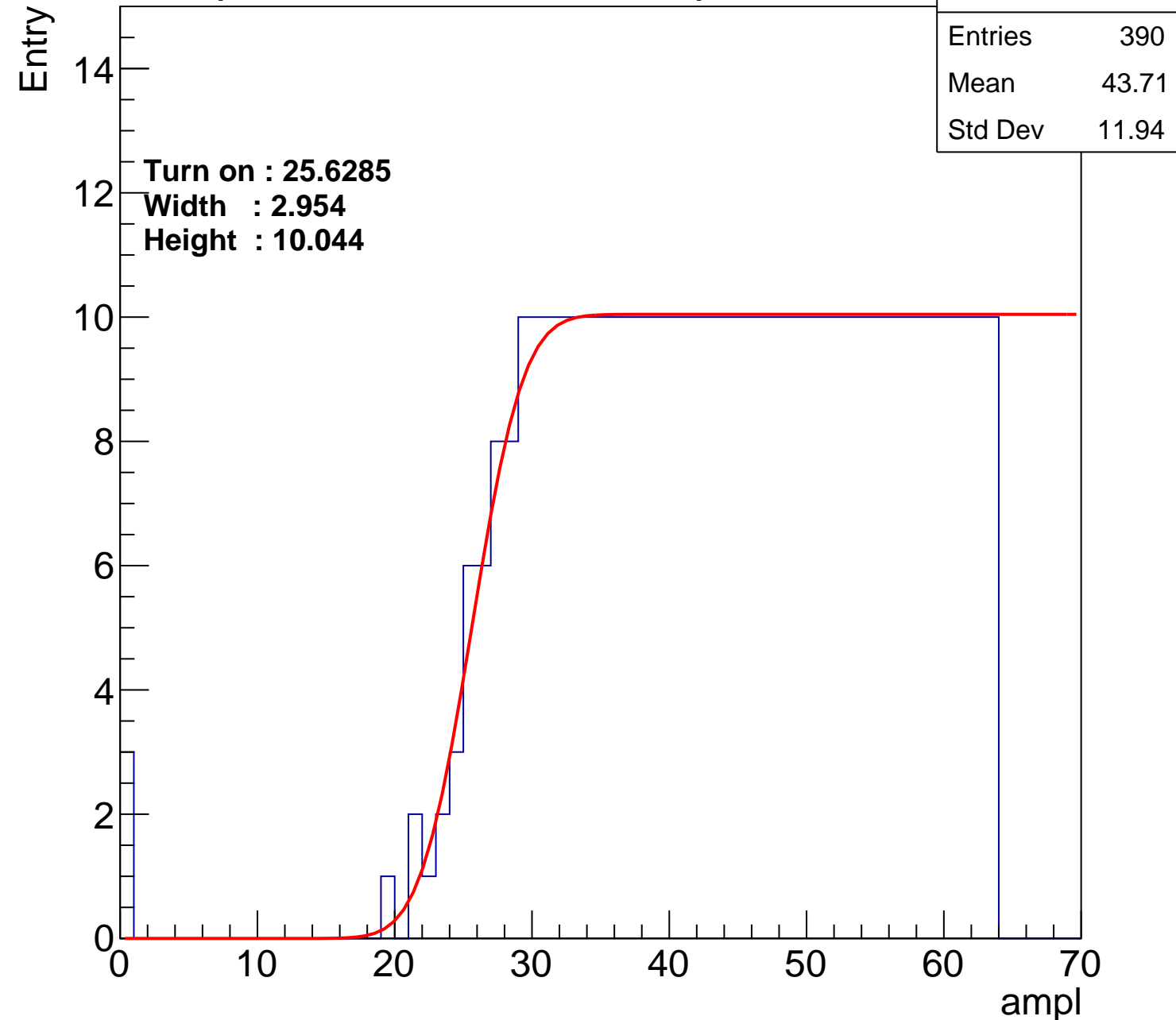
Width : 2.954

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.3113

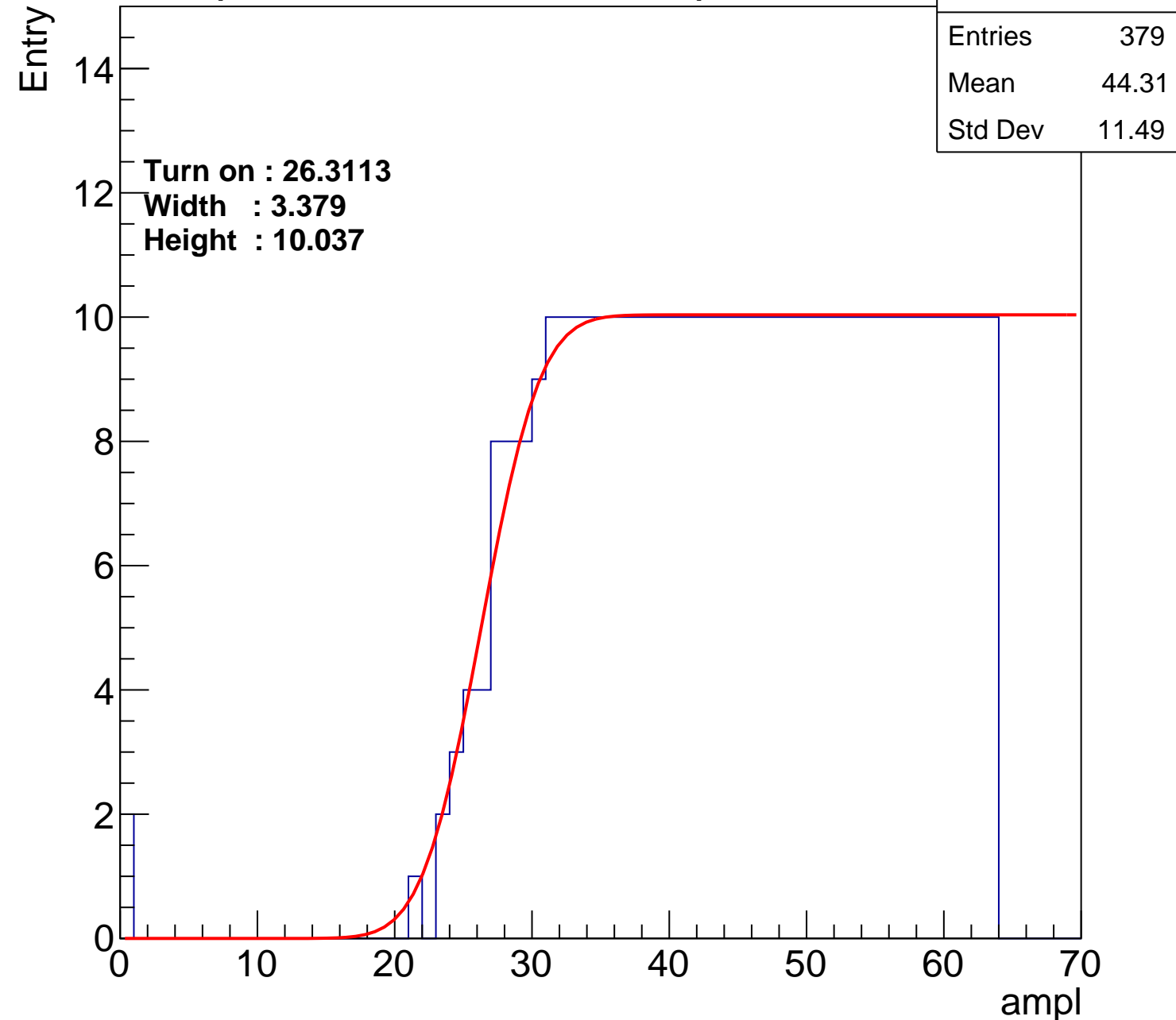
Width : 3.379

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.62
Std Dev	11.74

Turn on : 24.6824

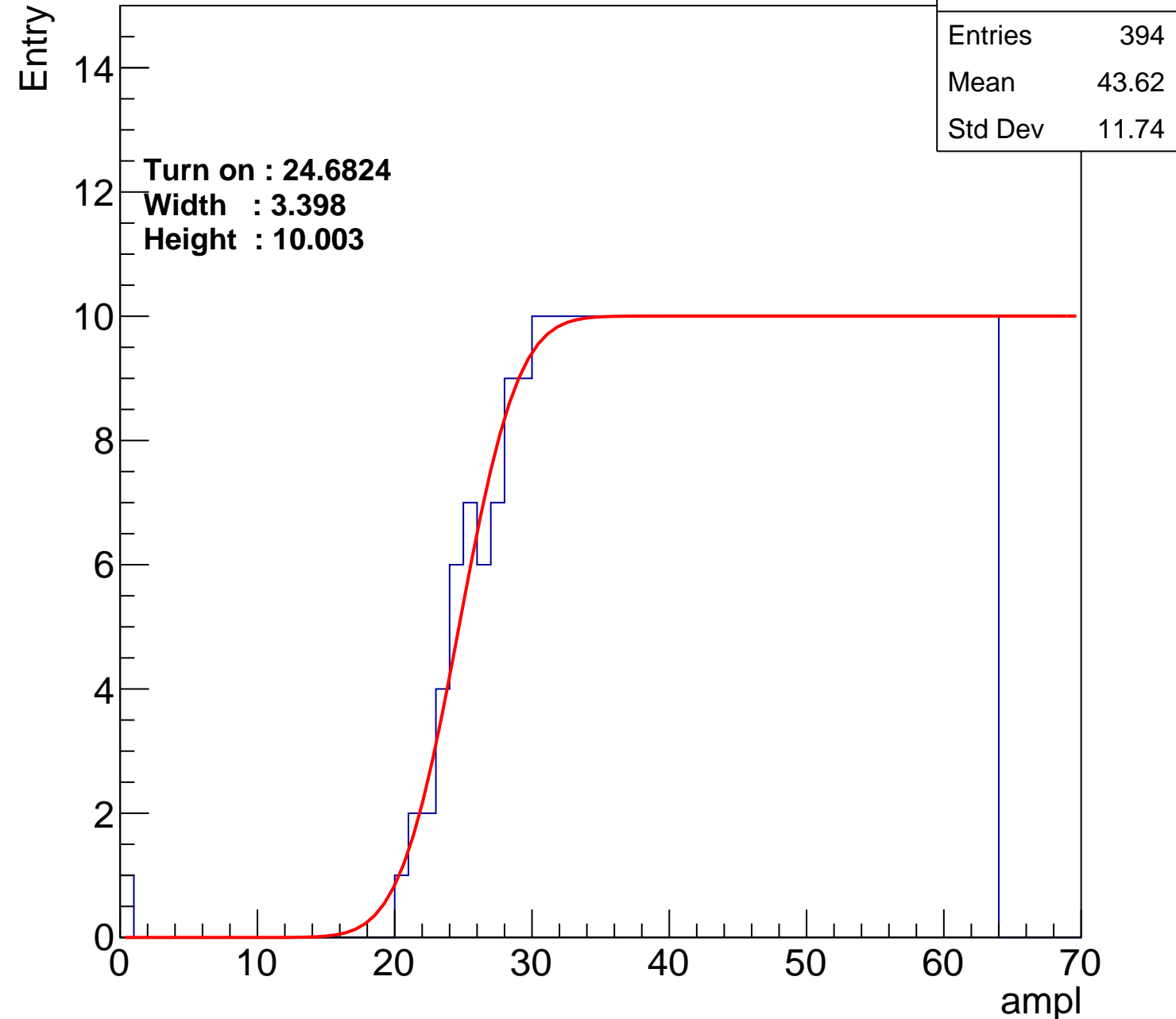
Width : 3.398

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U13-ch127

calib_packv5_042523_0143.root, FC#3, port B3

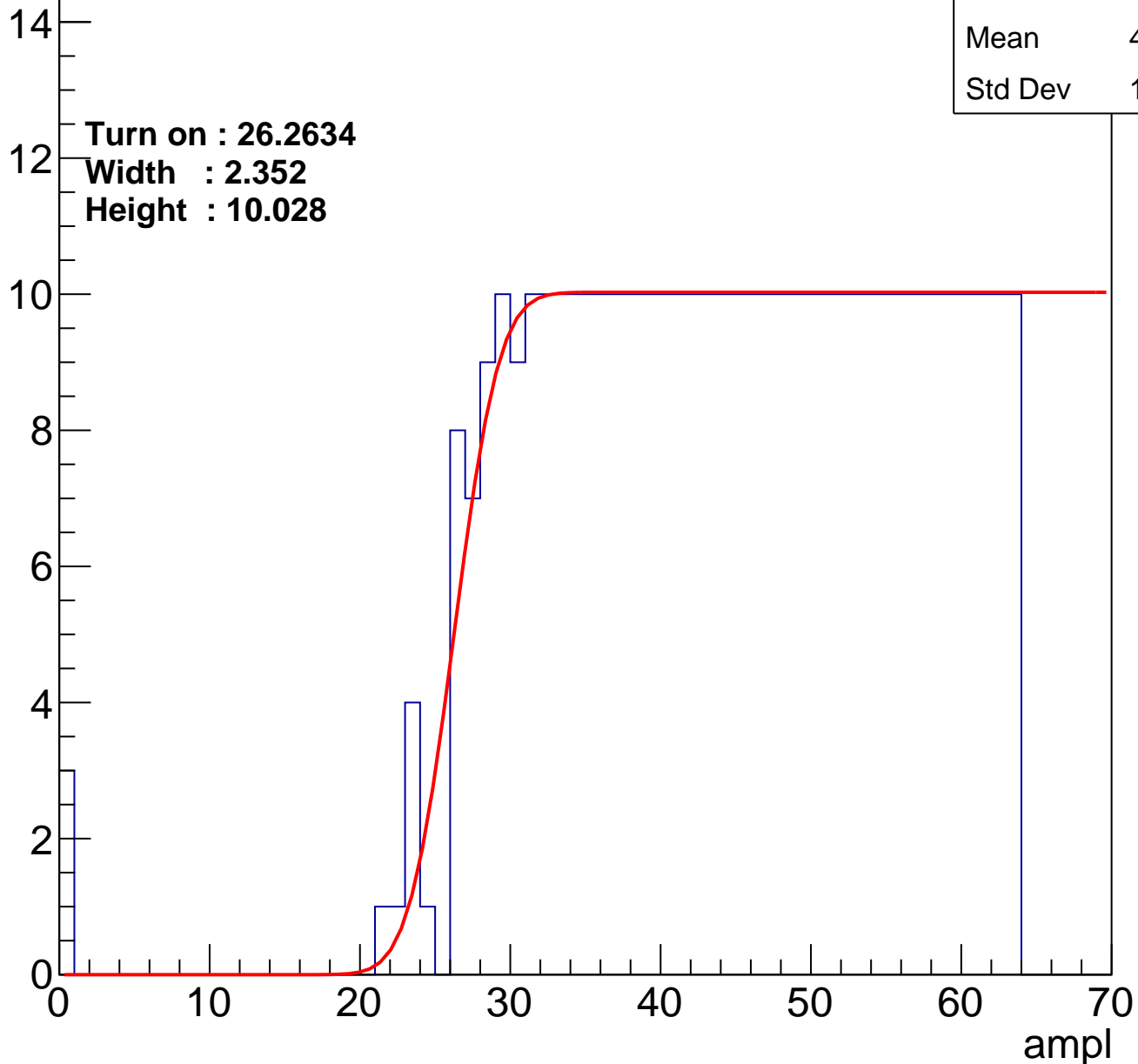
Entries	383
Mean	44.07
Std Dev	11.74

Turn on : 26.2634

Width : 2.352

Height : 10.028

Entry



B1L000S, U13-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.07
Std Dev	11.74

Turn on : 26.2634

Width : 2.352

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl

