

B1L102S, U9-ch0, adc0

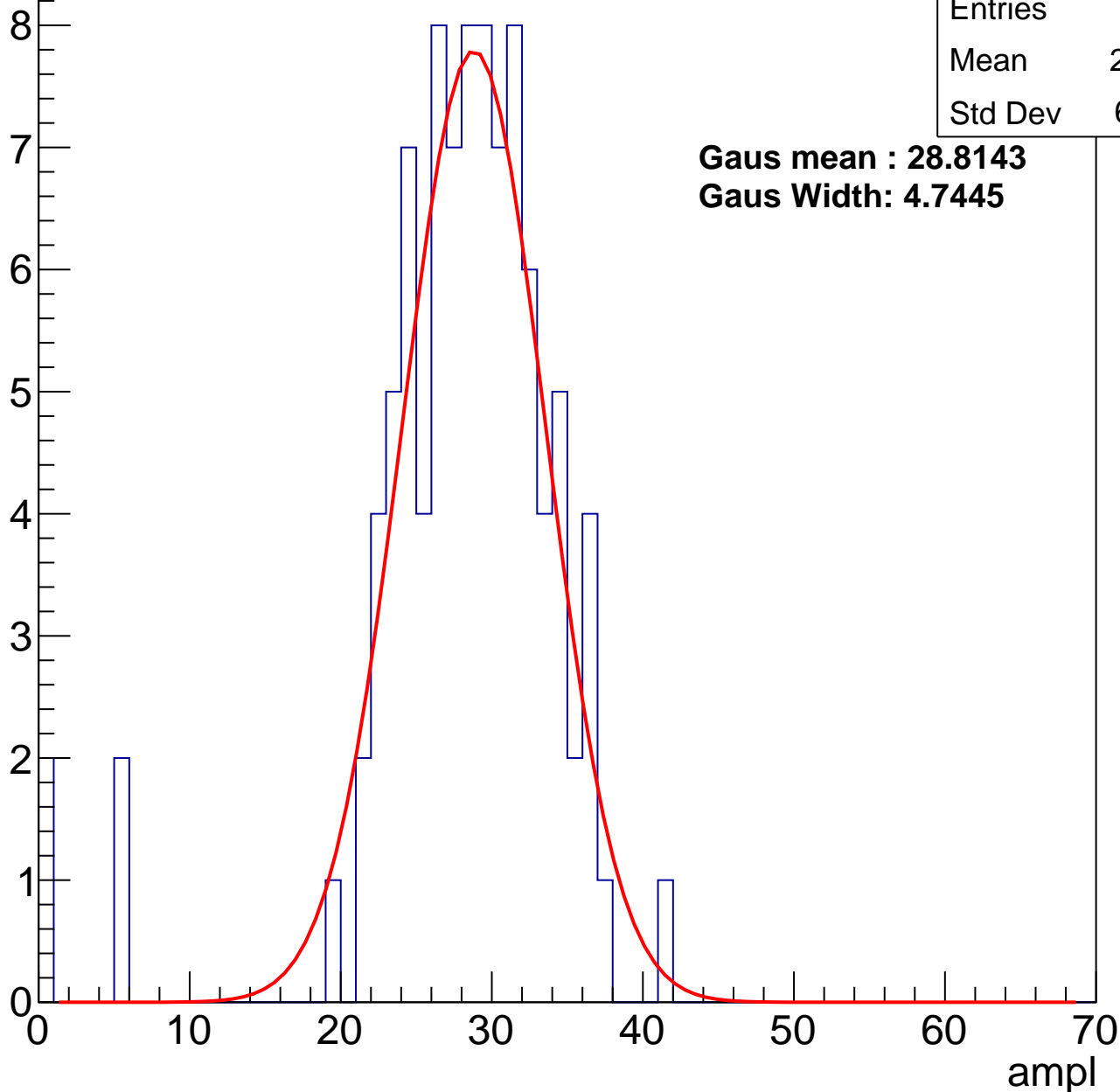
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	96
Mean	27.49
Std Dev	6.711

Gaus mean : 28.8143

Gaus Width: 4.7445



B1L102S, U9-ch0, adc1

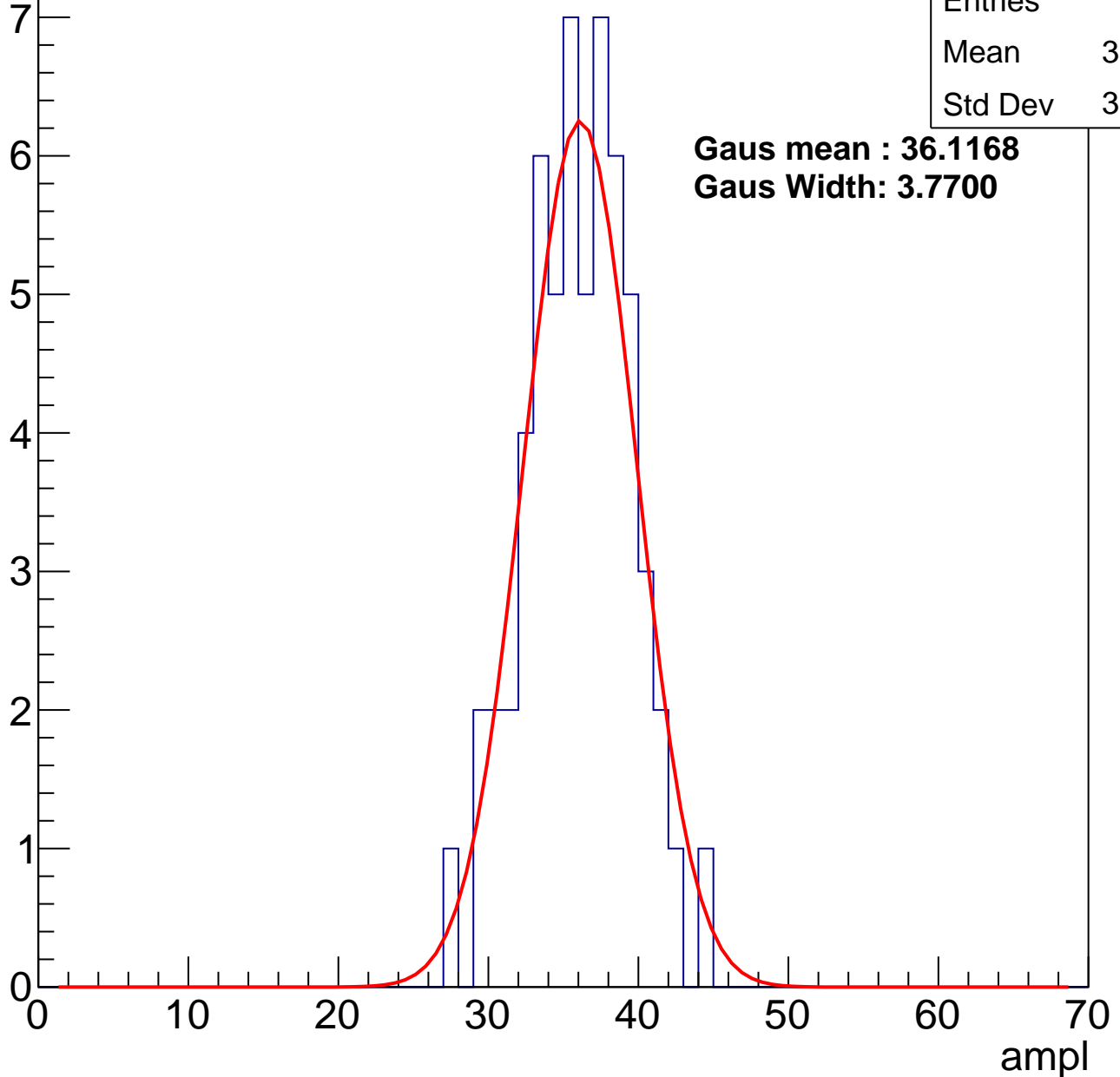
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.56
Std Dev	3.465

Gaus mean : 36.1168

Gaus Width: 3.7700



B1L102S, U9-ch0, adc2

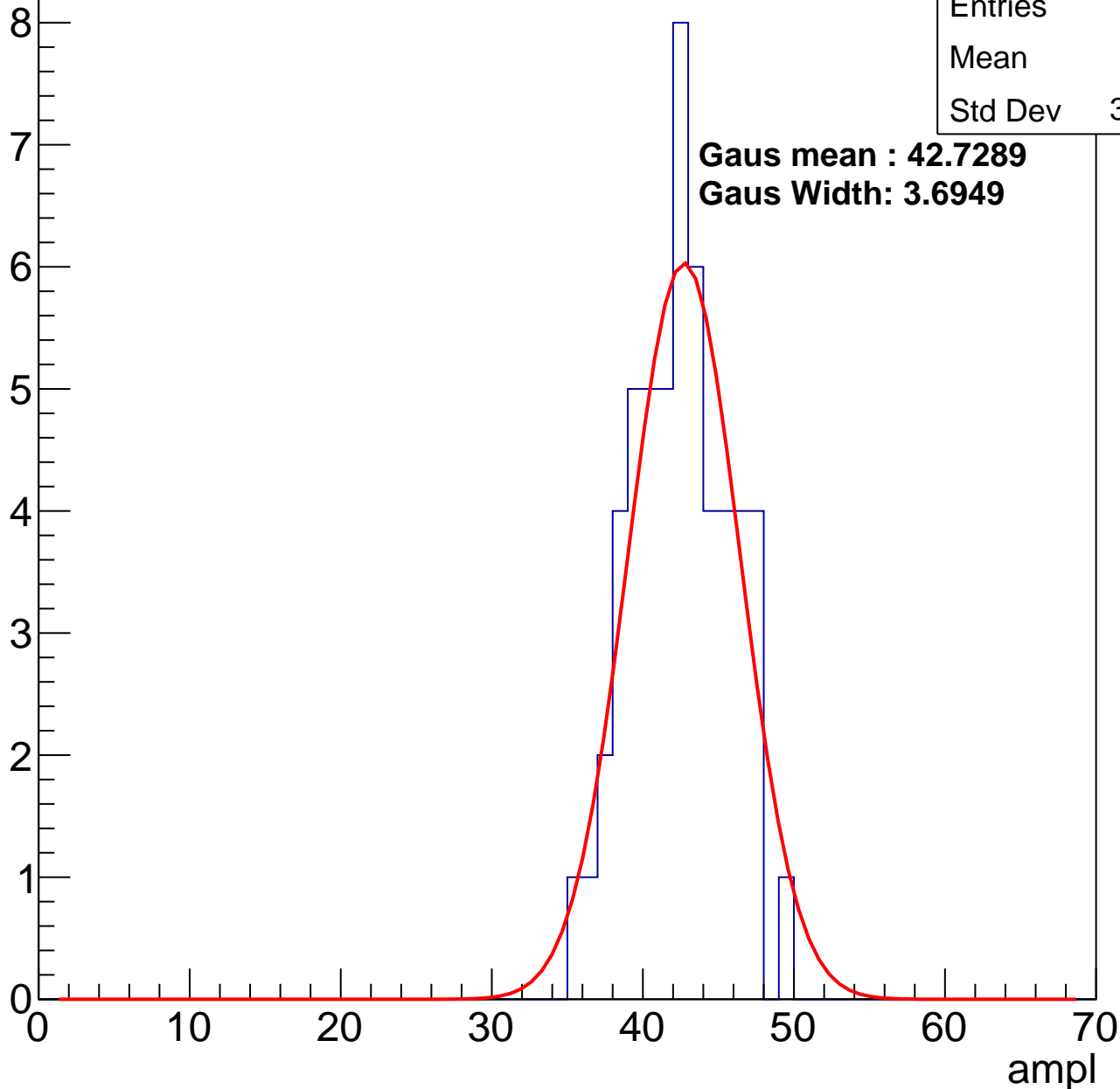
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42
Std Dev	3.162

Gaus mean : 42.7289

Gaus Width: 3.6949



B1L102S, U9-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	95
Mean	49.04
Std Dev	4.073

Entry

10

8

6

4

2

0

0

10

20

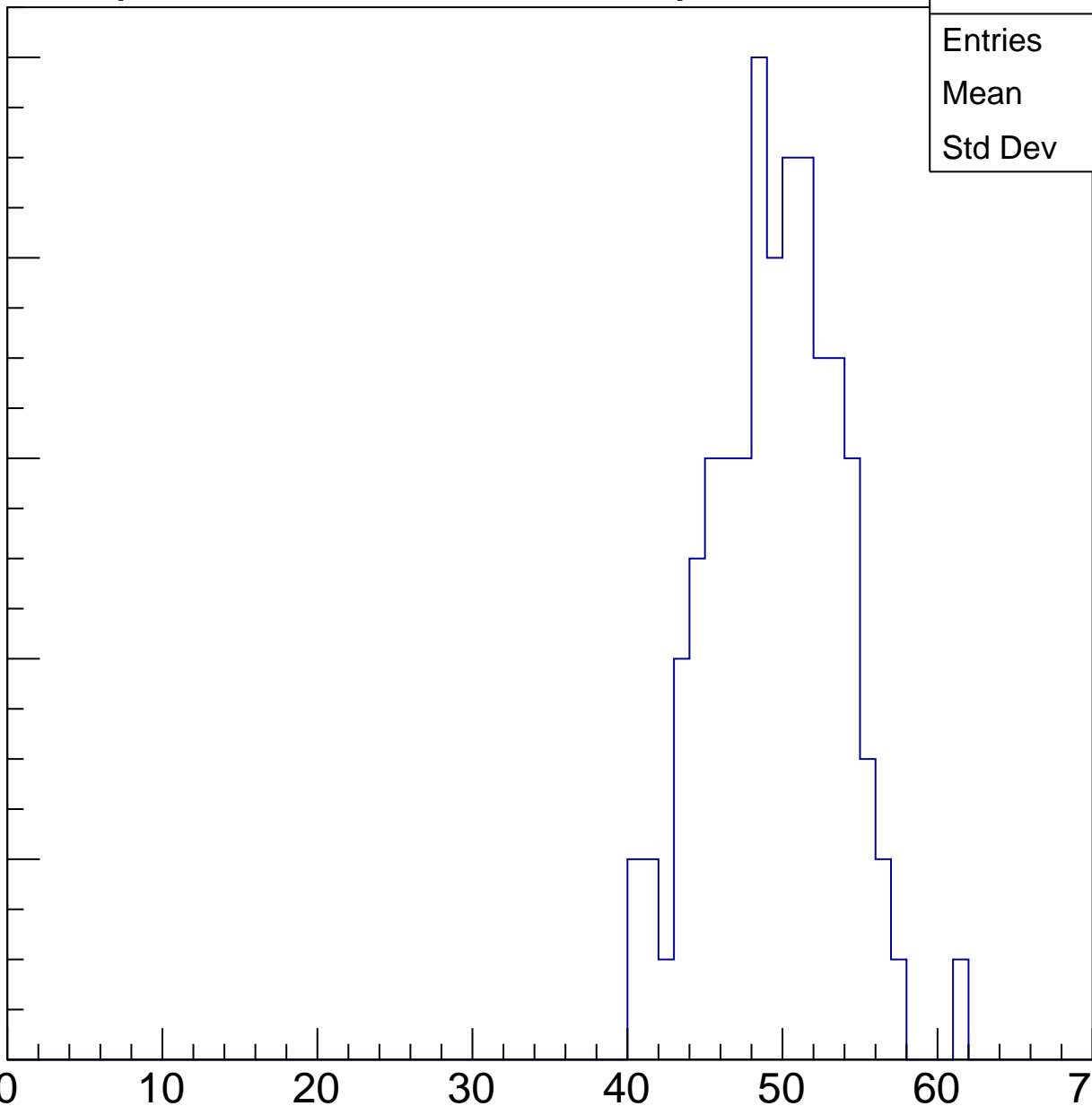
30

40

50

60

ampl

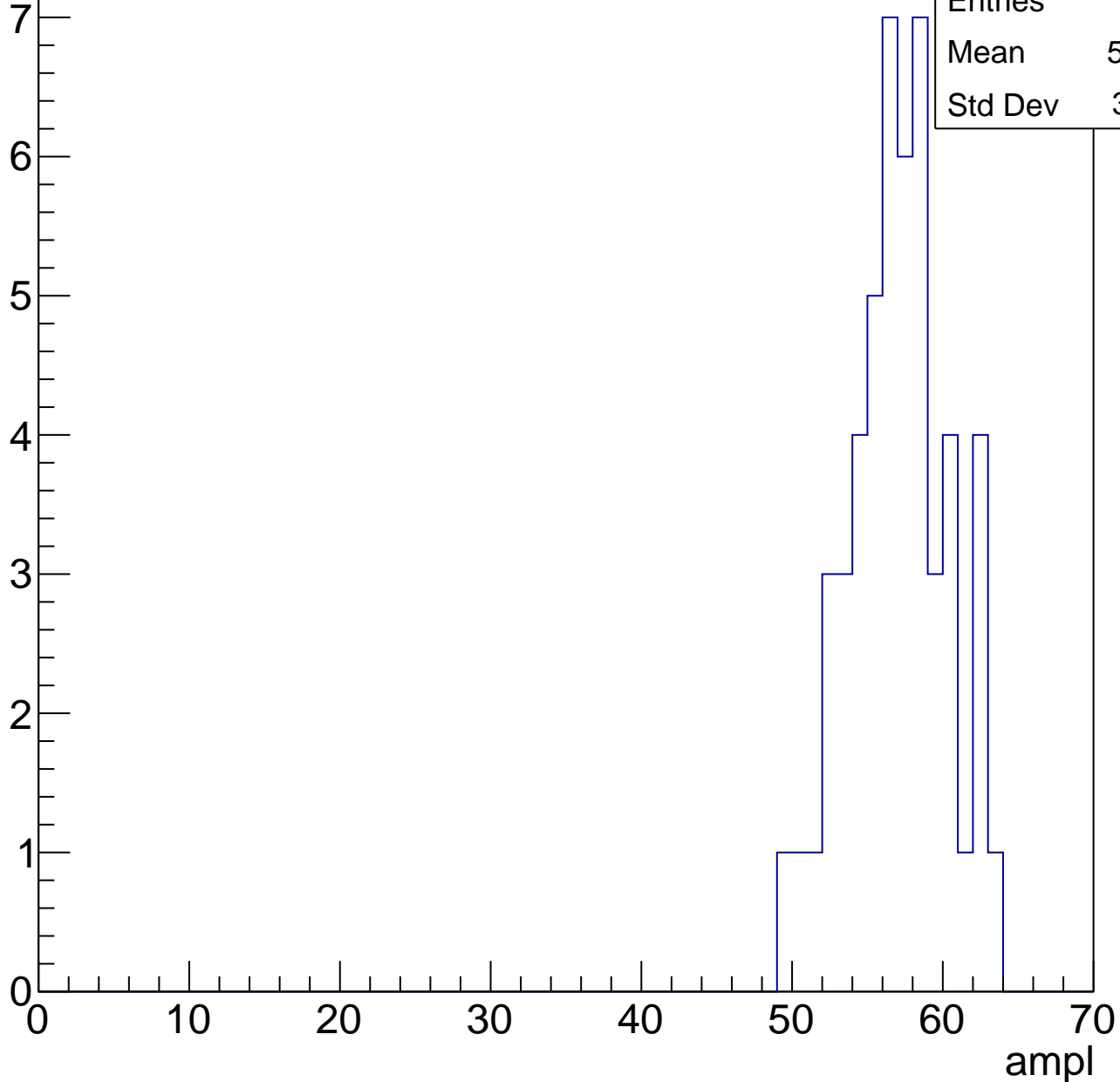


B1L102S, U9-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.57
Std Dev	3.231



B1L102S, U9-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

41

Mean

58.51

Std Dev

9.503

ampl

0

10

20

30

40

50

60

70

B1L102S, U9-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

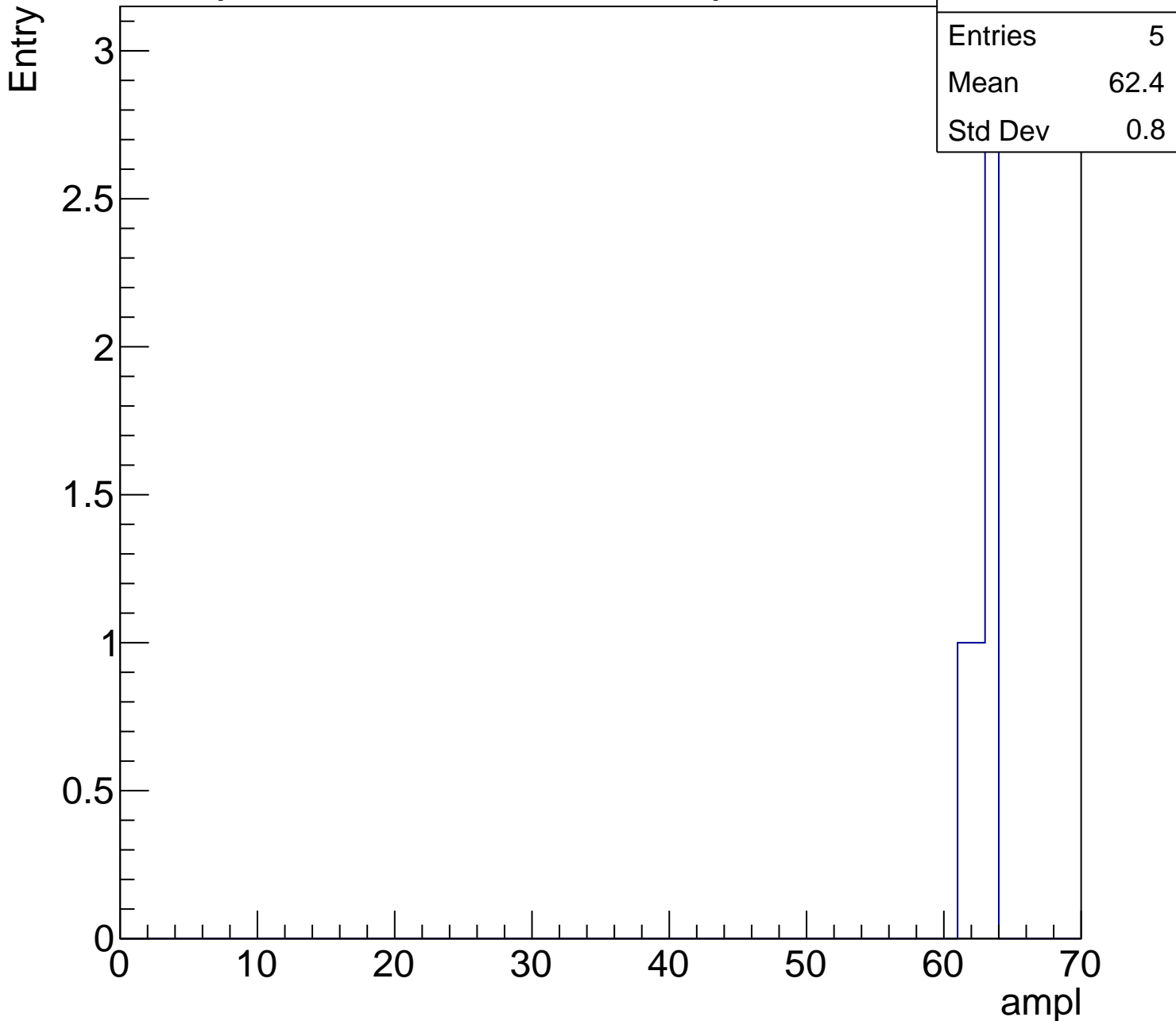
5

Mean

62.4

Std Dev

0.8



B1L102S, U9-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch1, adc0

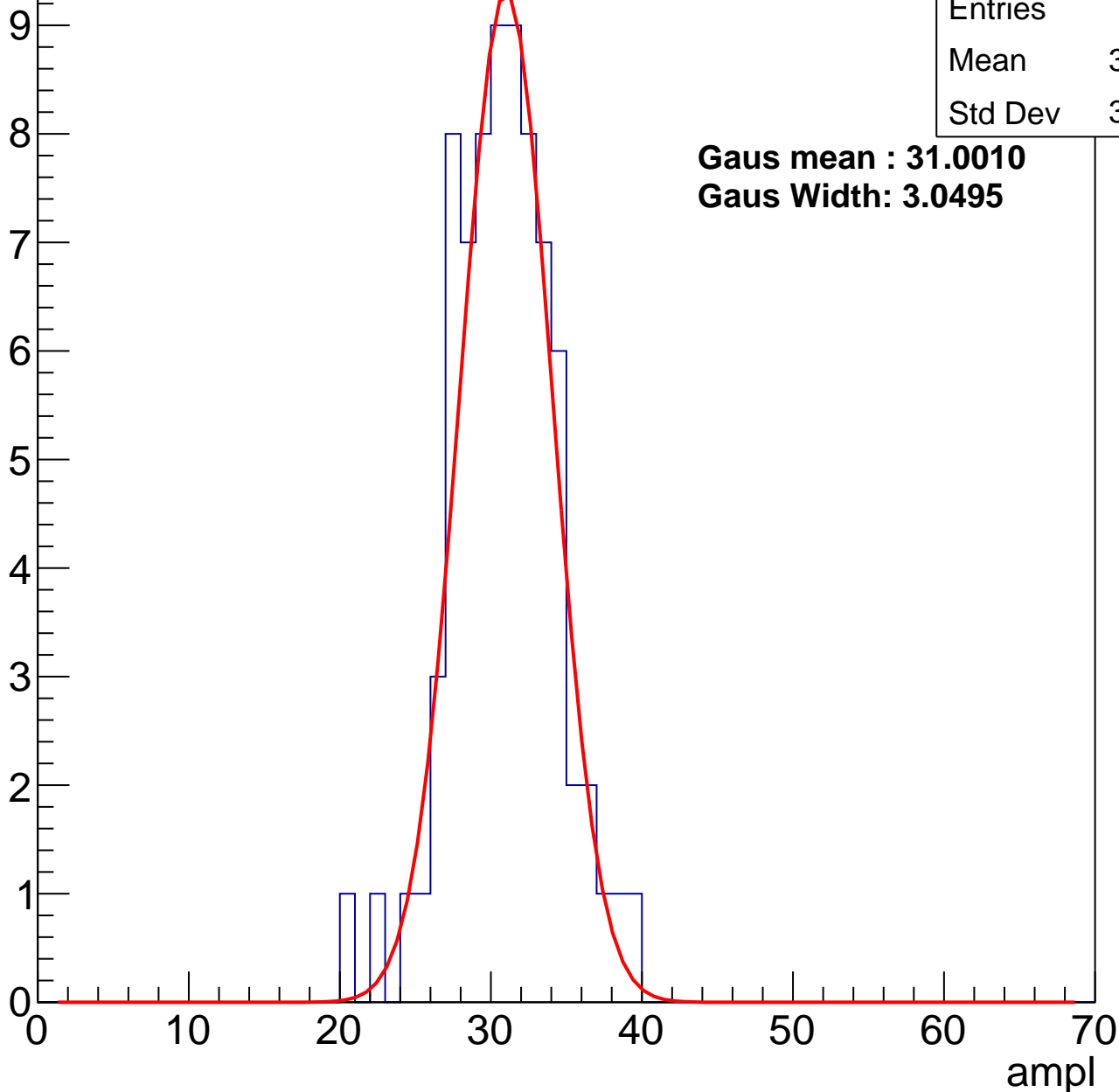
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	30.38
Std Dev	3.414

Gaus mean : 31.0010

Gaus Width: 3.0495



B1L102S, U9-ch1, adc1

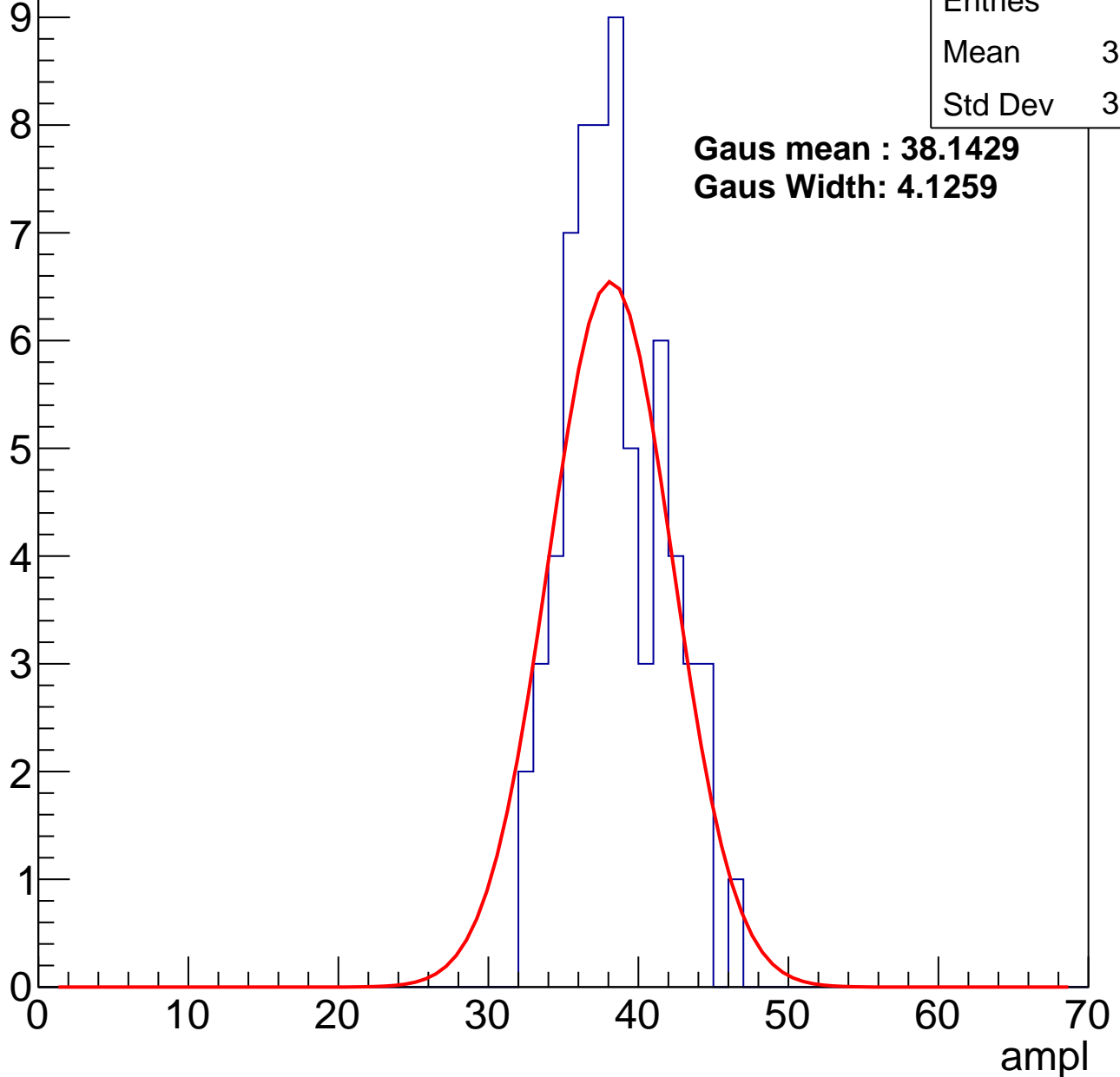
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	37.97
Std Dev	3.247

Gaus mean : 38.1429

Gaus Width: 4.1259



B1L102S, U9-ch1, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	44.39
Std Dev	3.856

Gaus mean : 44.2753

Gaus Width: 3.9579

Entry

10

8

6

4

2

0

0

10

20

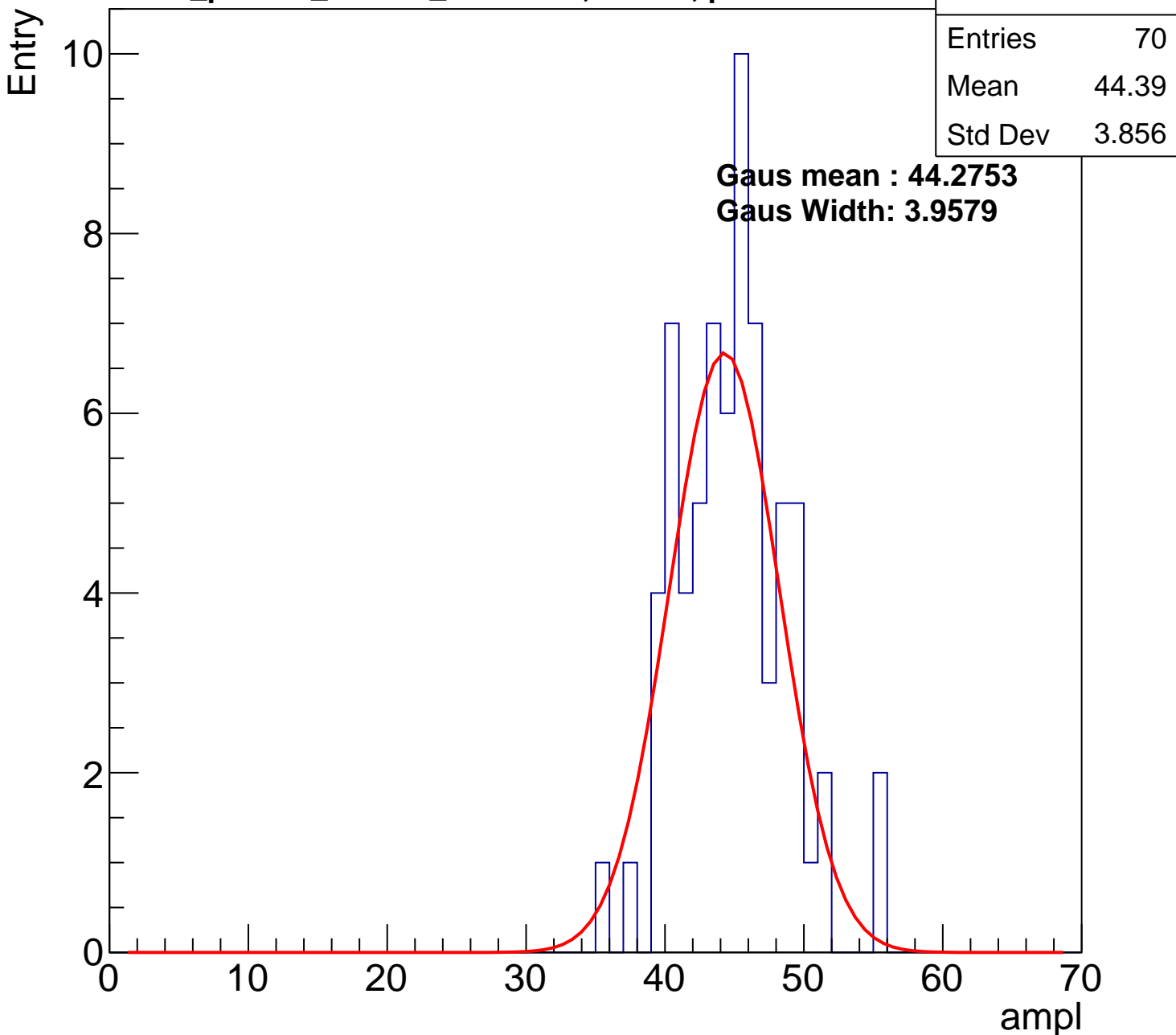
30

40

50

60

ampl

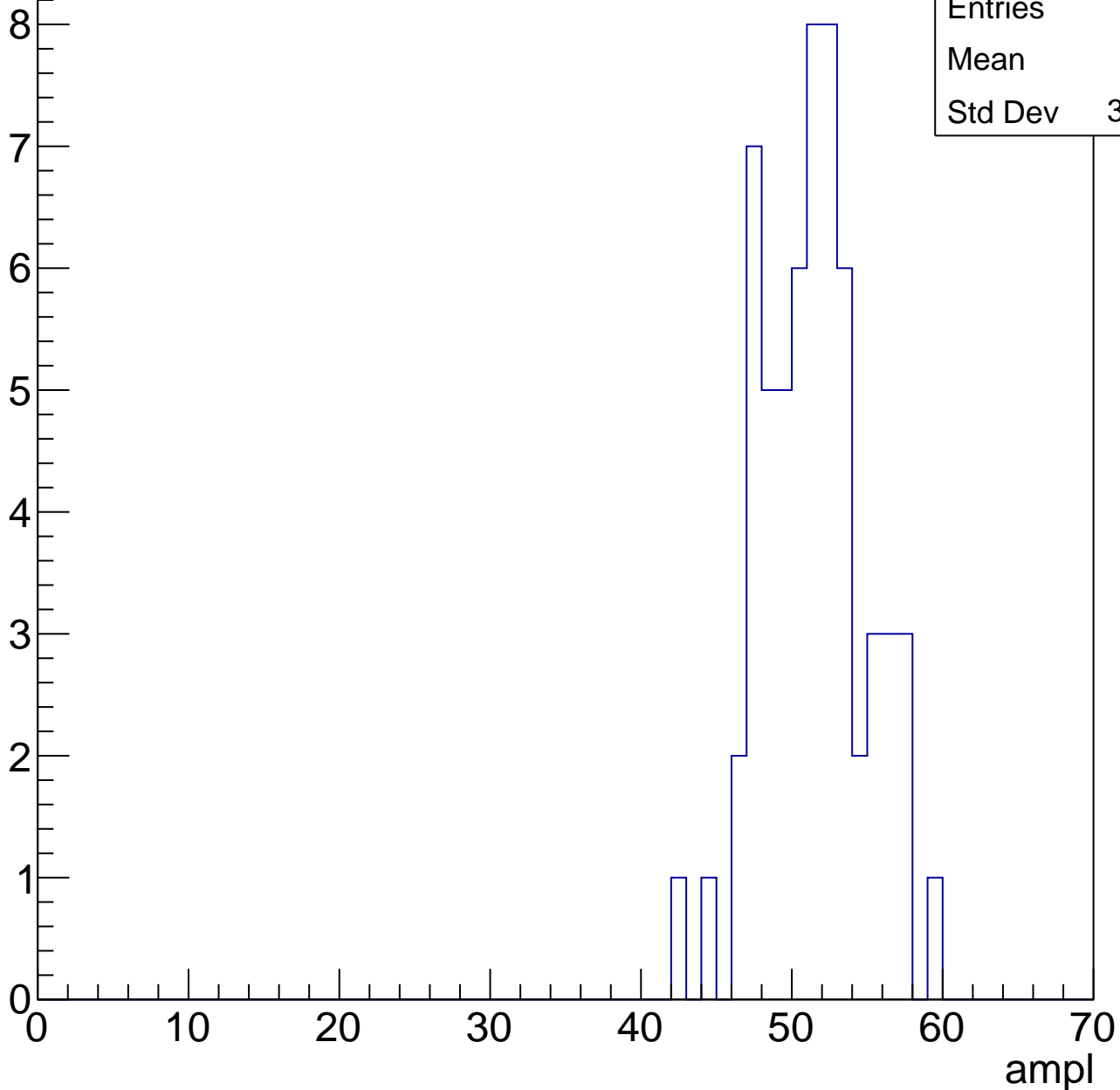


B1L102S, U9-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.9
Std Dev	3.405

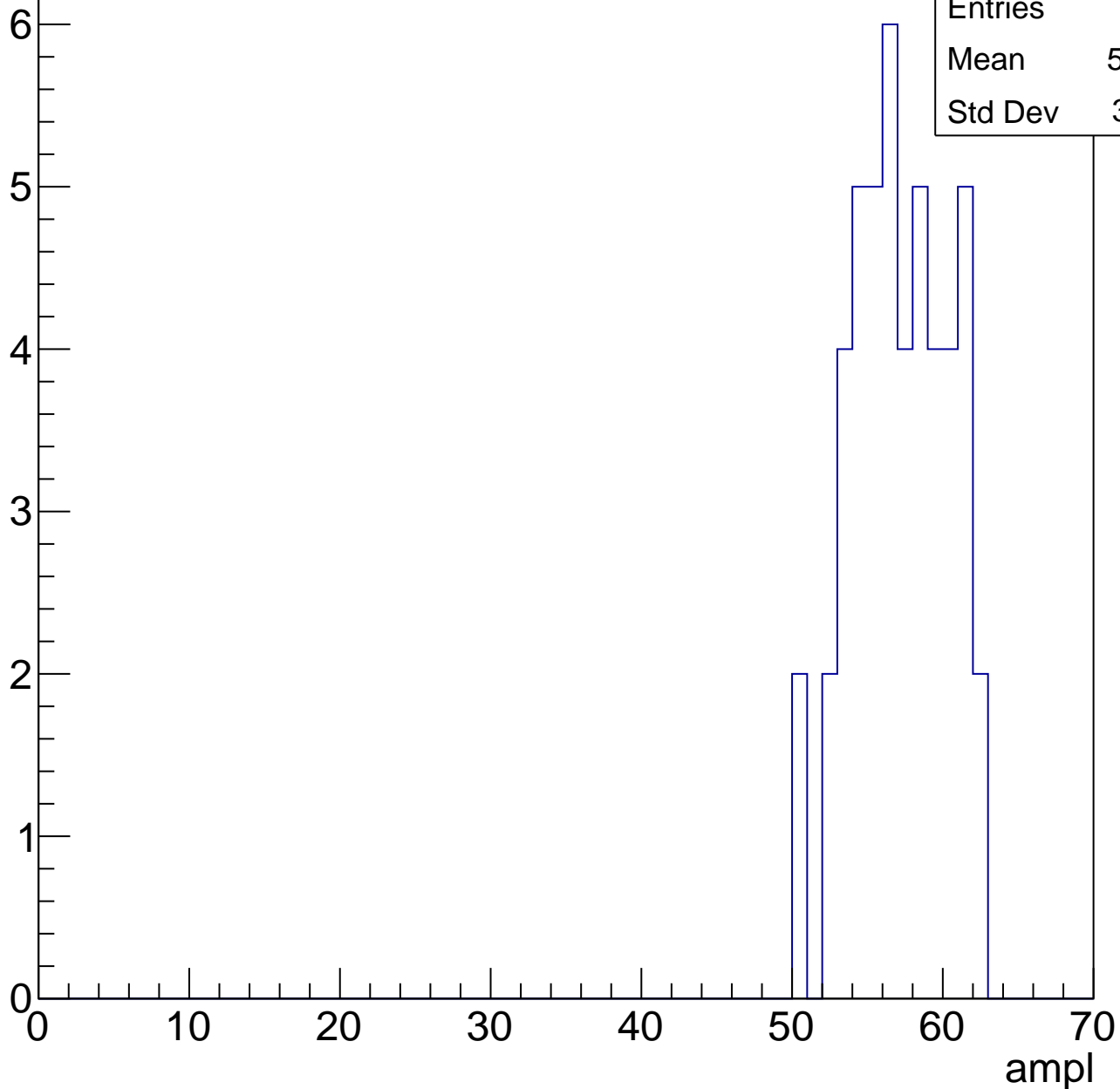


B1L102S, U9-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	56.67
Std Dev	3.111

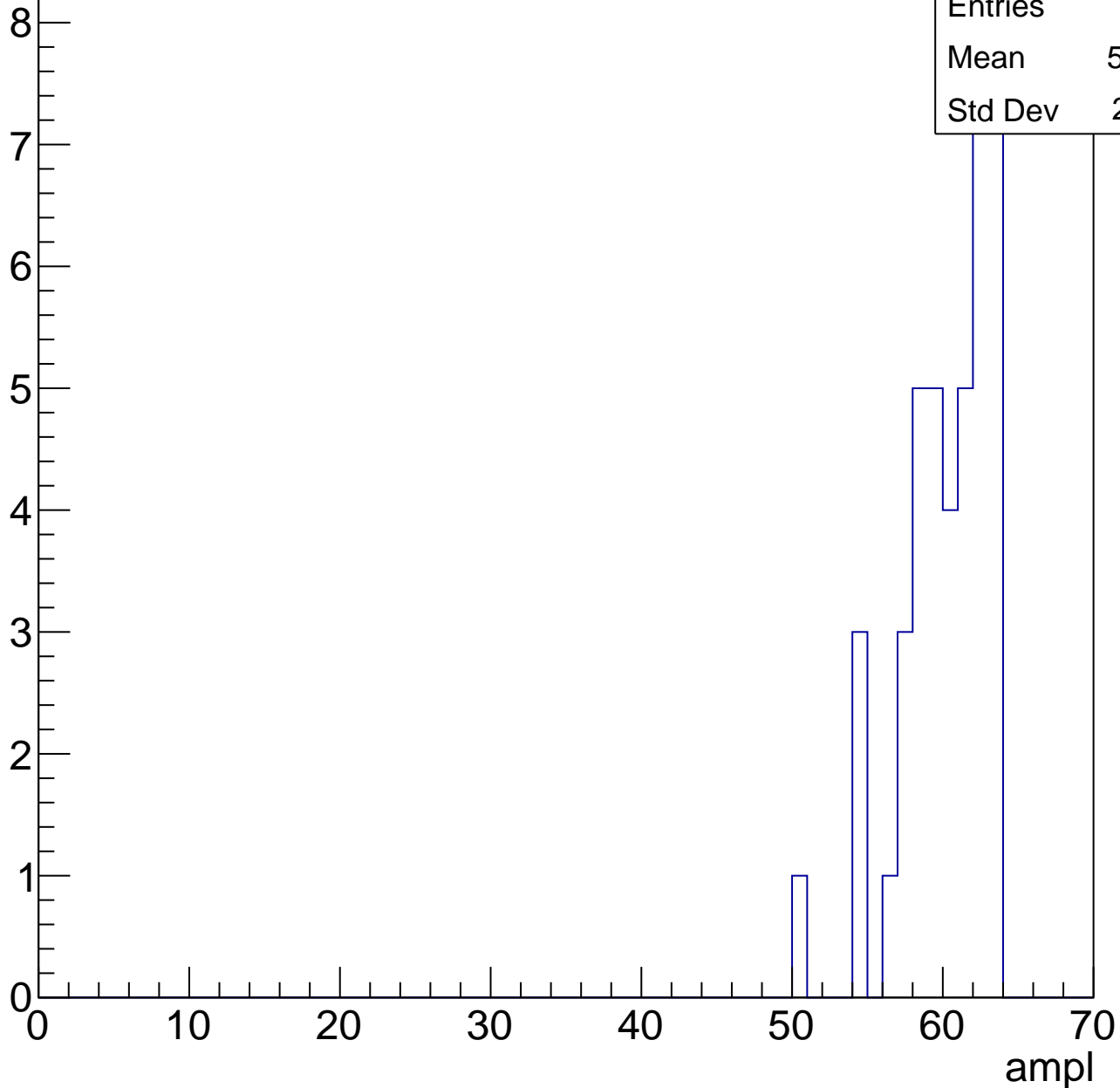


B1L102S, U9-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.74
Std Dev	2.981



B1L102S, U9-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.2
Std Dev	24.64

0 10 20 30 40 50 60 70

ampl

B1L102S, U9-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch2, adc0

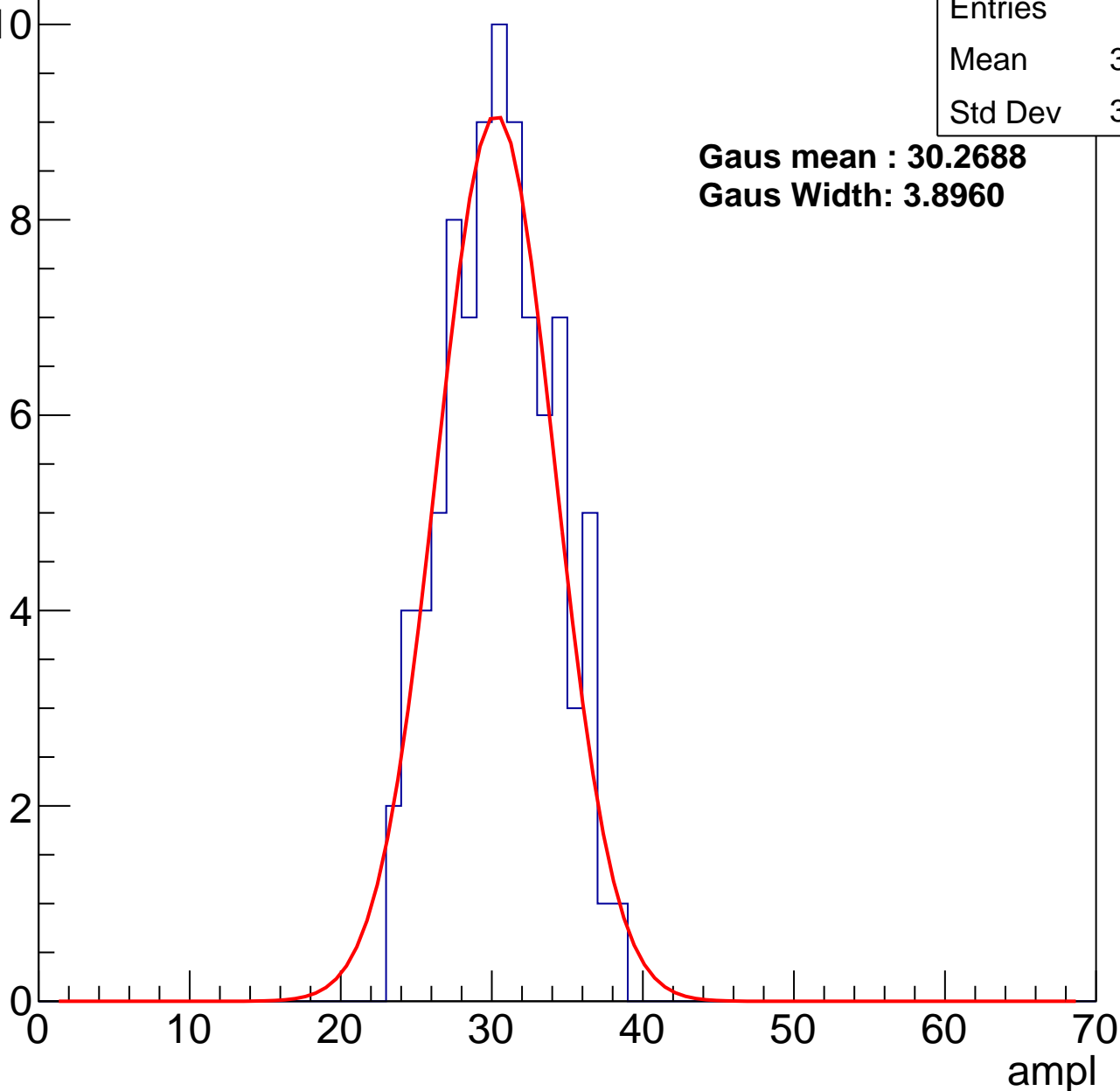
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	30.05
Std Dev	3.538

Gaus mean : 30.2688

Gaus Width: 3.8960



B1L102S, U9-ch2, adc1

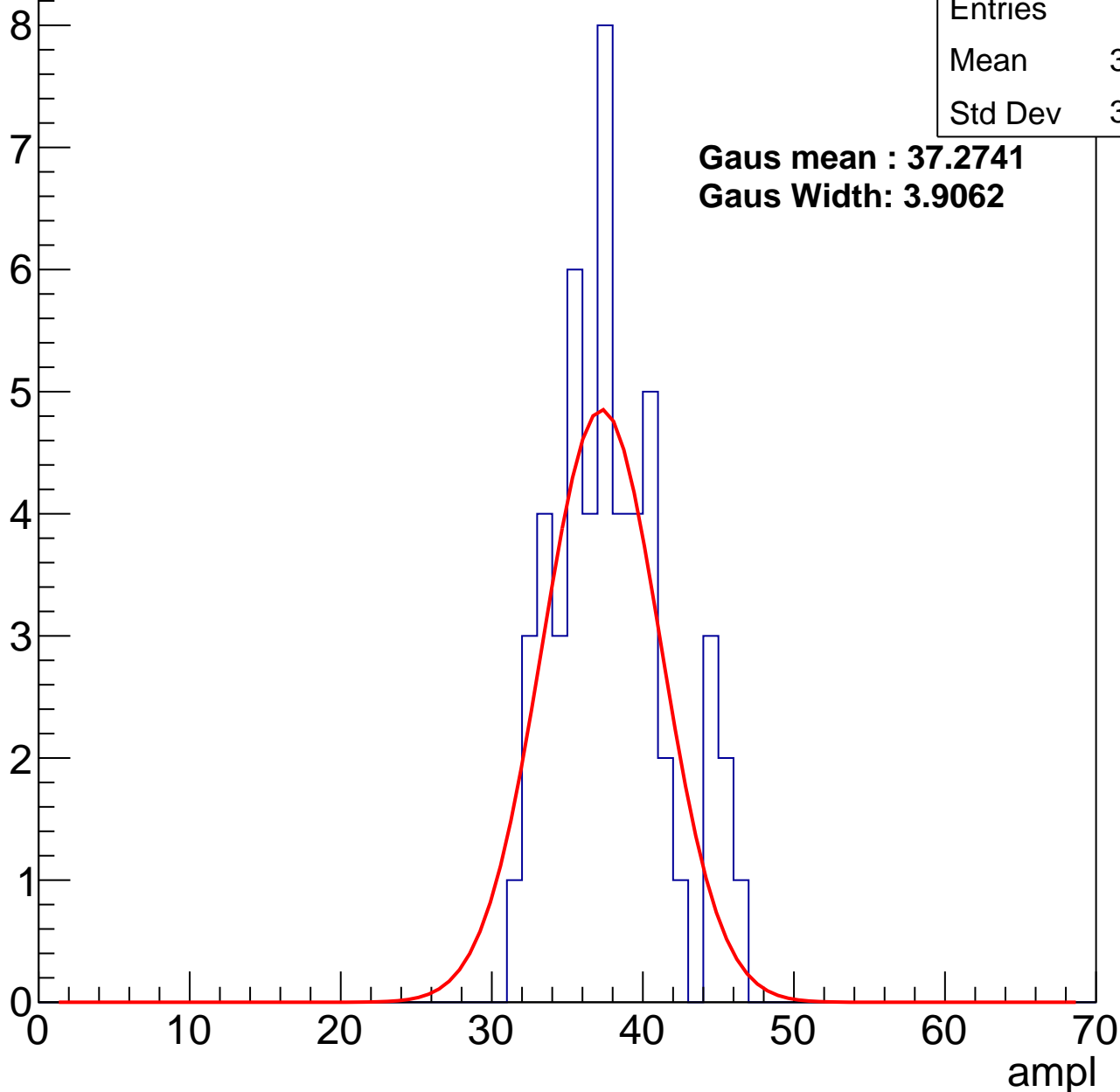
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	37.47
Std Dev	3.696

Gaus mean : 37.2741

Gaus Width: 3.9062



B1L102S, U9-ch2, adc2

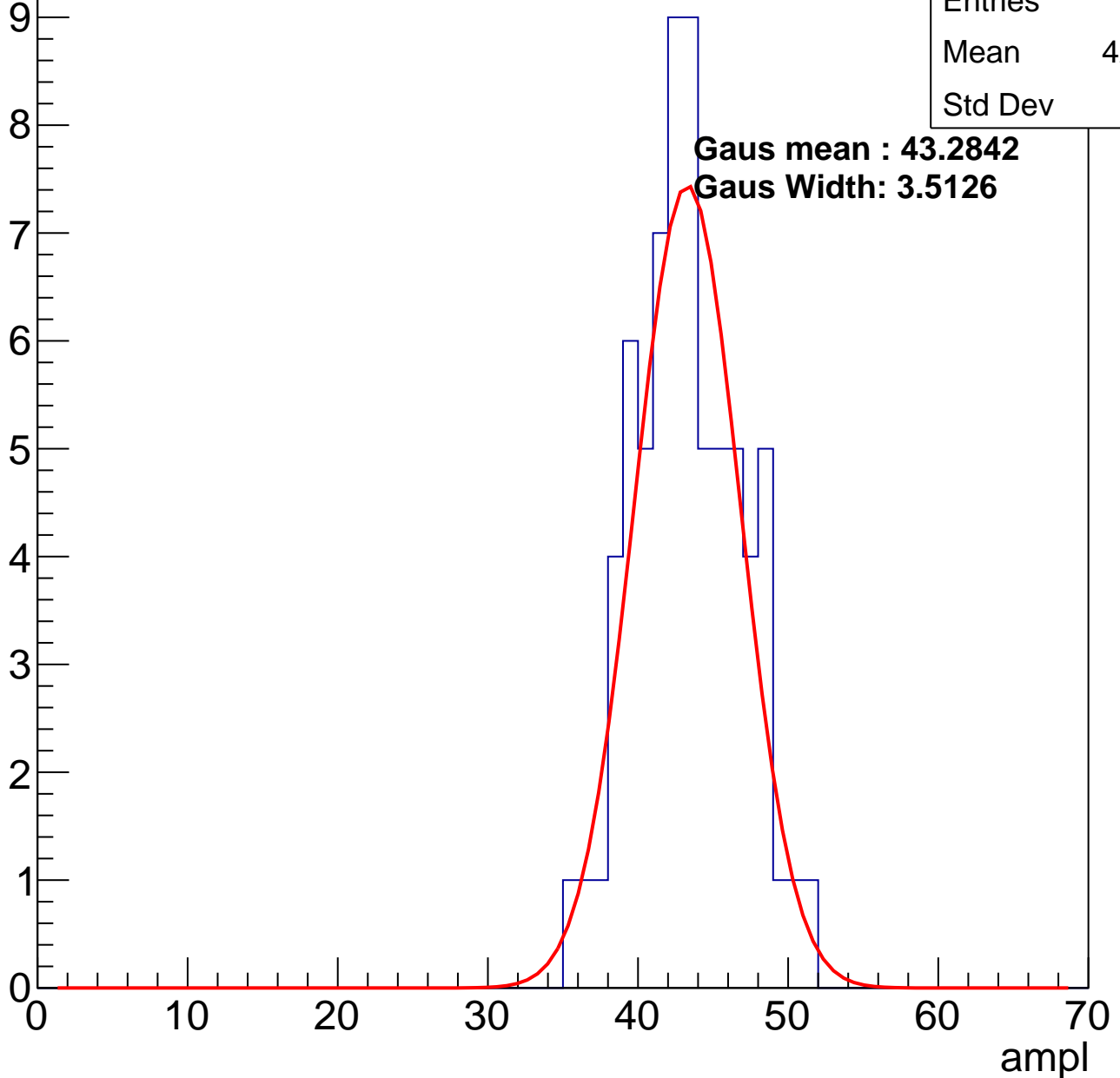
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.84
Std Dev	3.45

Gaus mean : 43.2842

Gaus Width: 3.5126

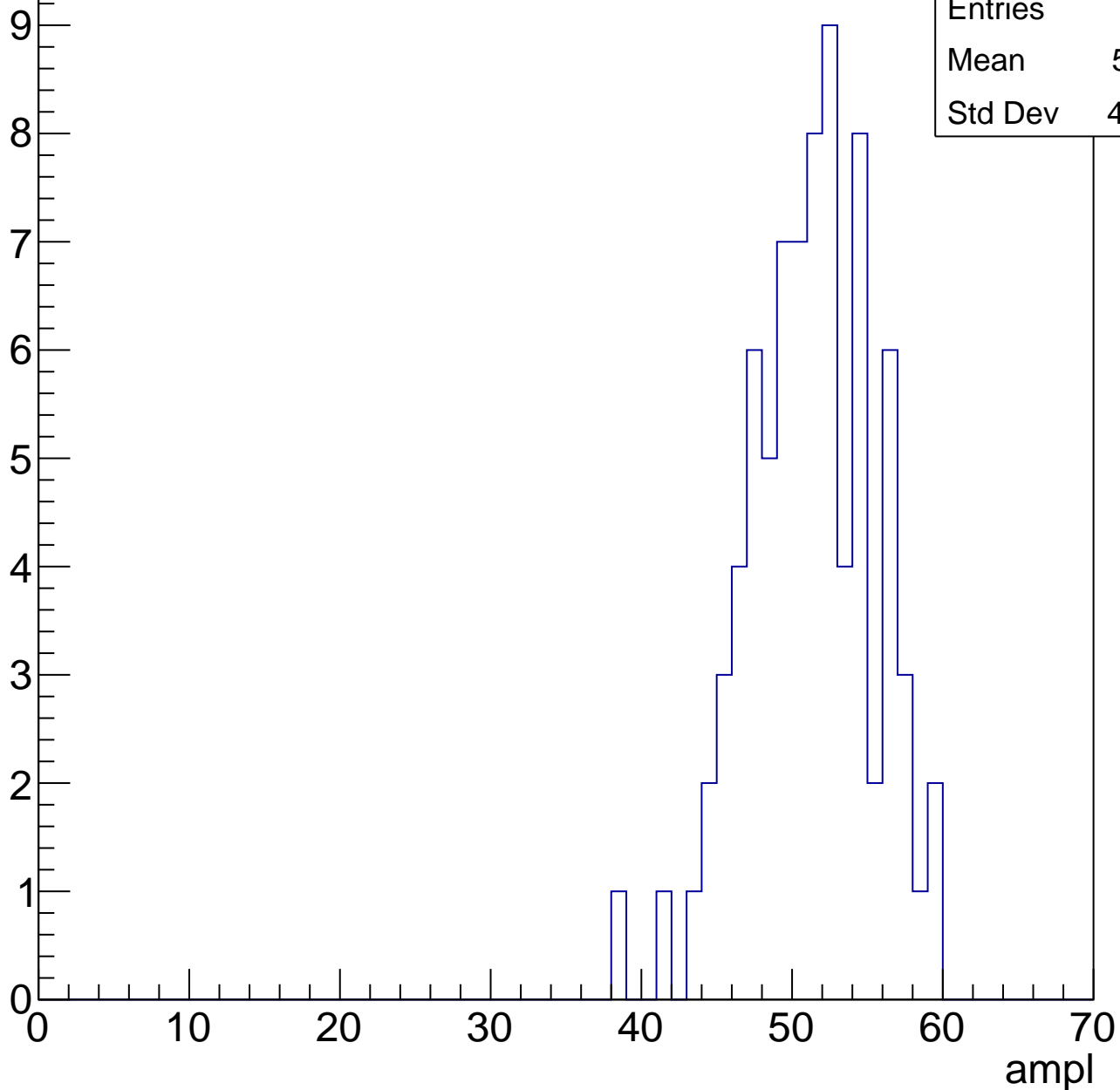


B1L102S, U9-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	50.71
Std Dev	4.142

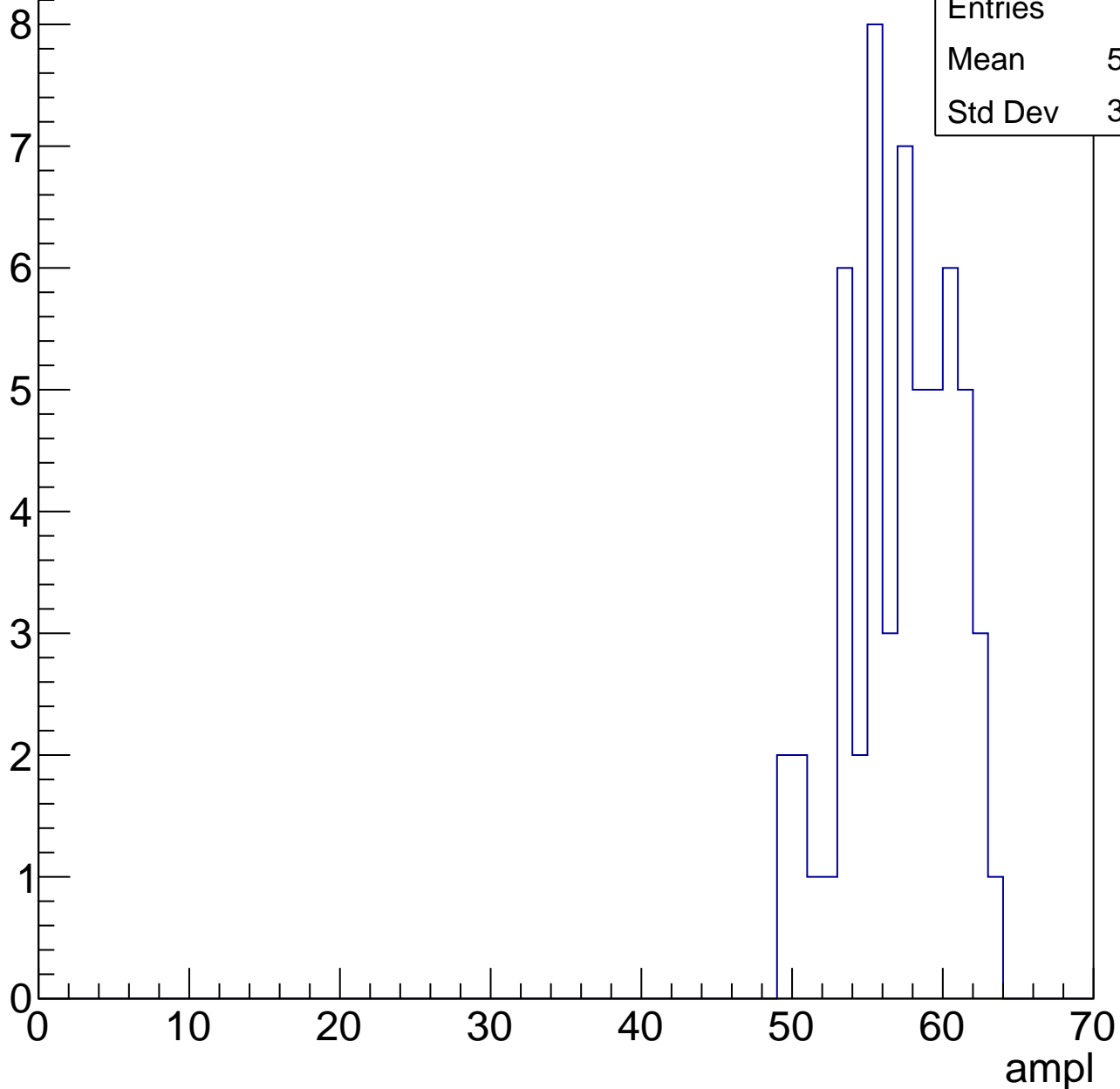


B1L102S, U9-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.72
Std Dev	3.508

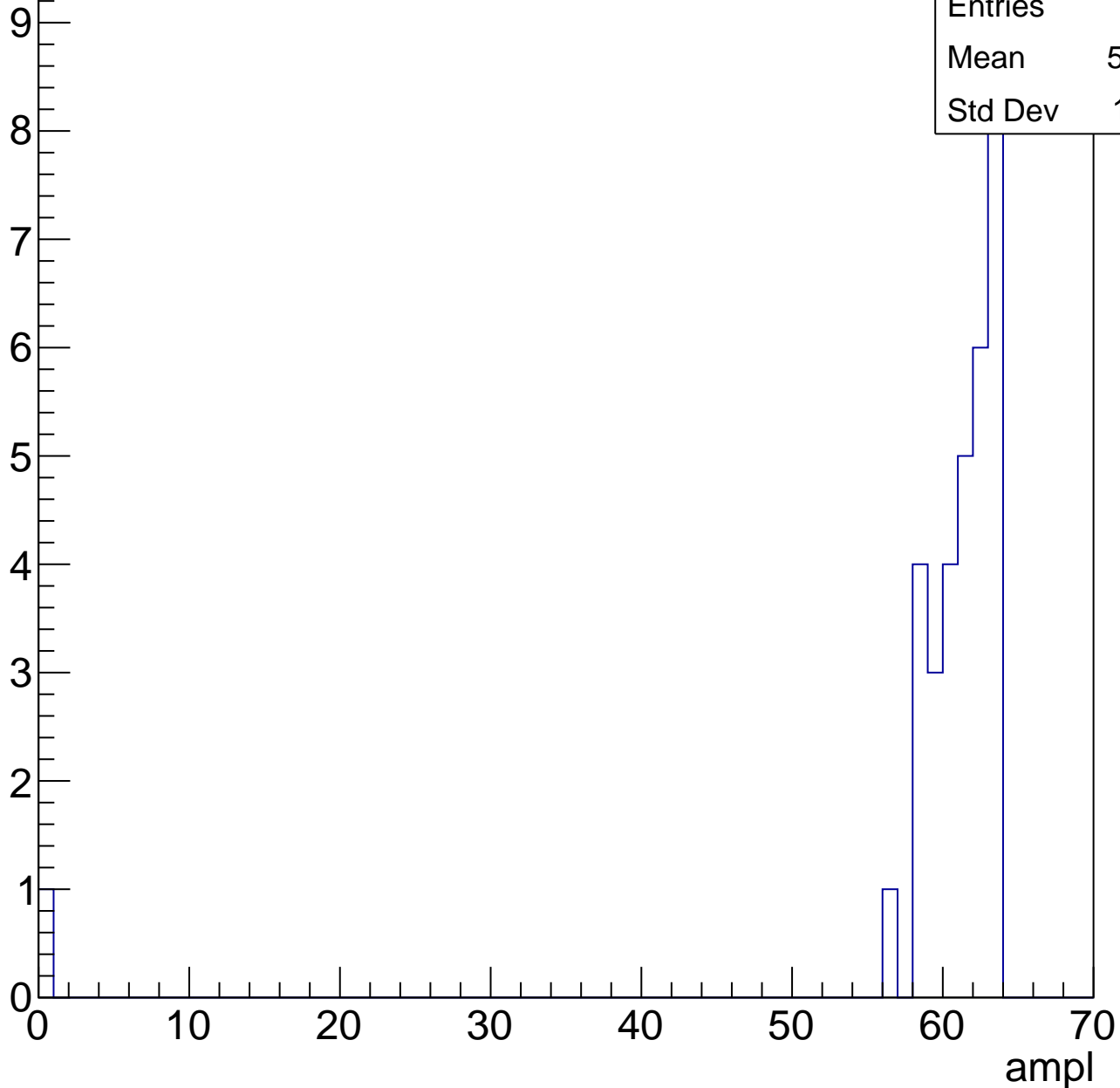


B1L102S, U9-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	59.06
Std Dev	10.61



B1L102S, U9-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

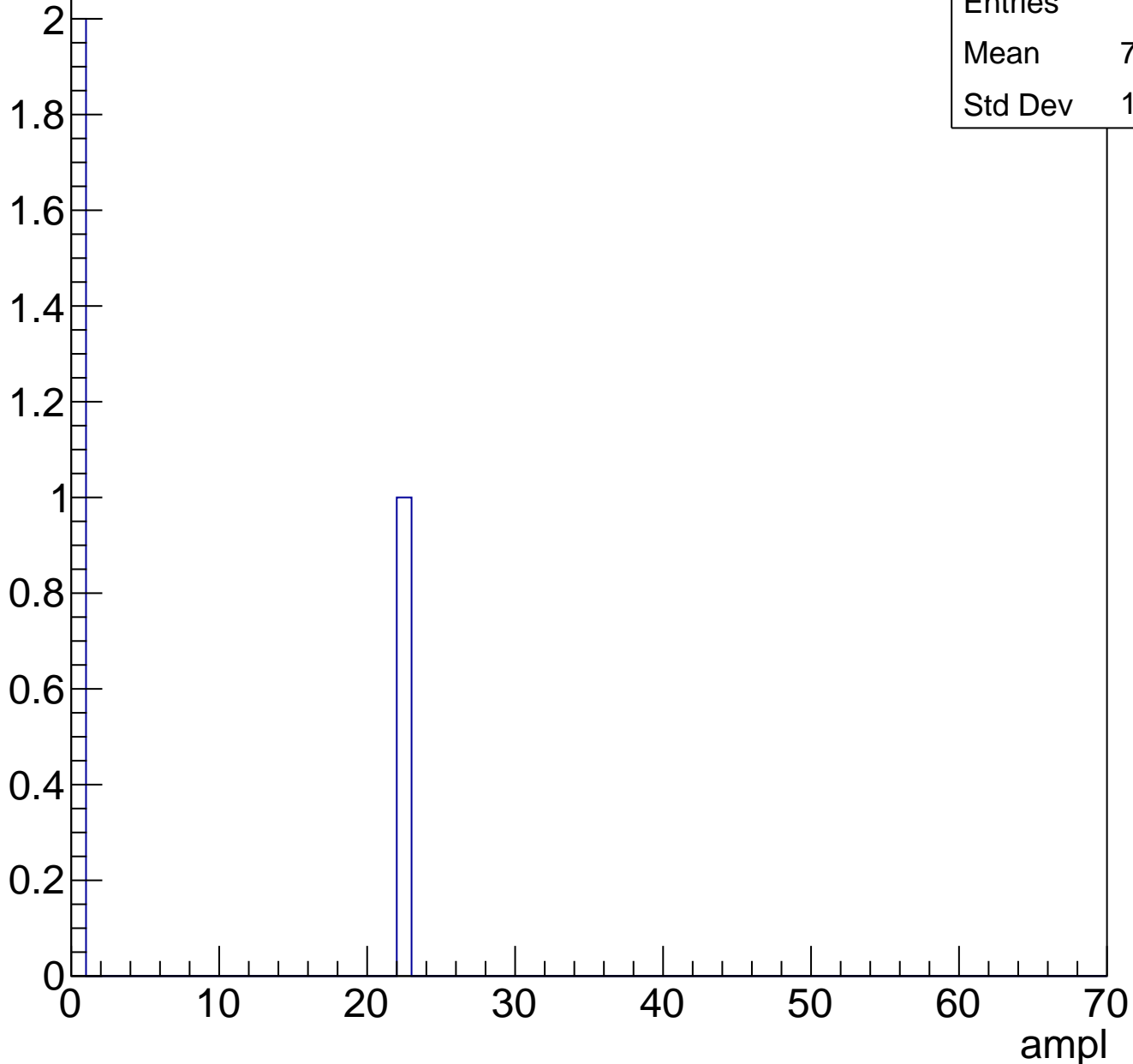
Entry



B1L102S, U9-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L102S, U9-ch3, adc0

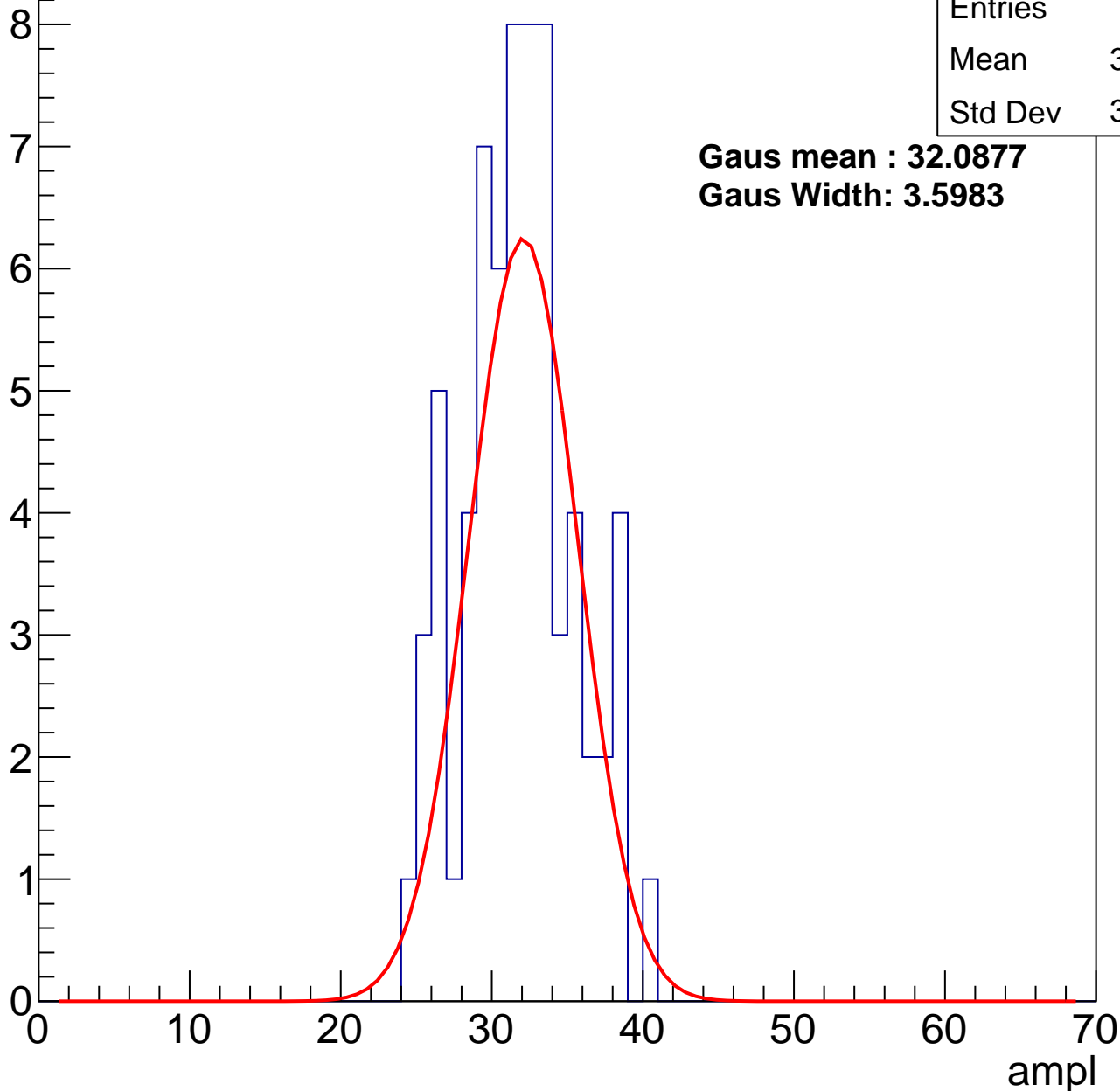
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	31.33
Std Dev	3.646

Gaus mean : 32.0877

Gaus Width: 3.5983



B1L102S, U9-ch3, adc1

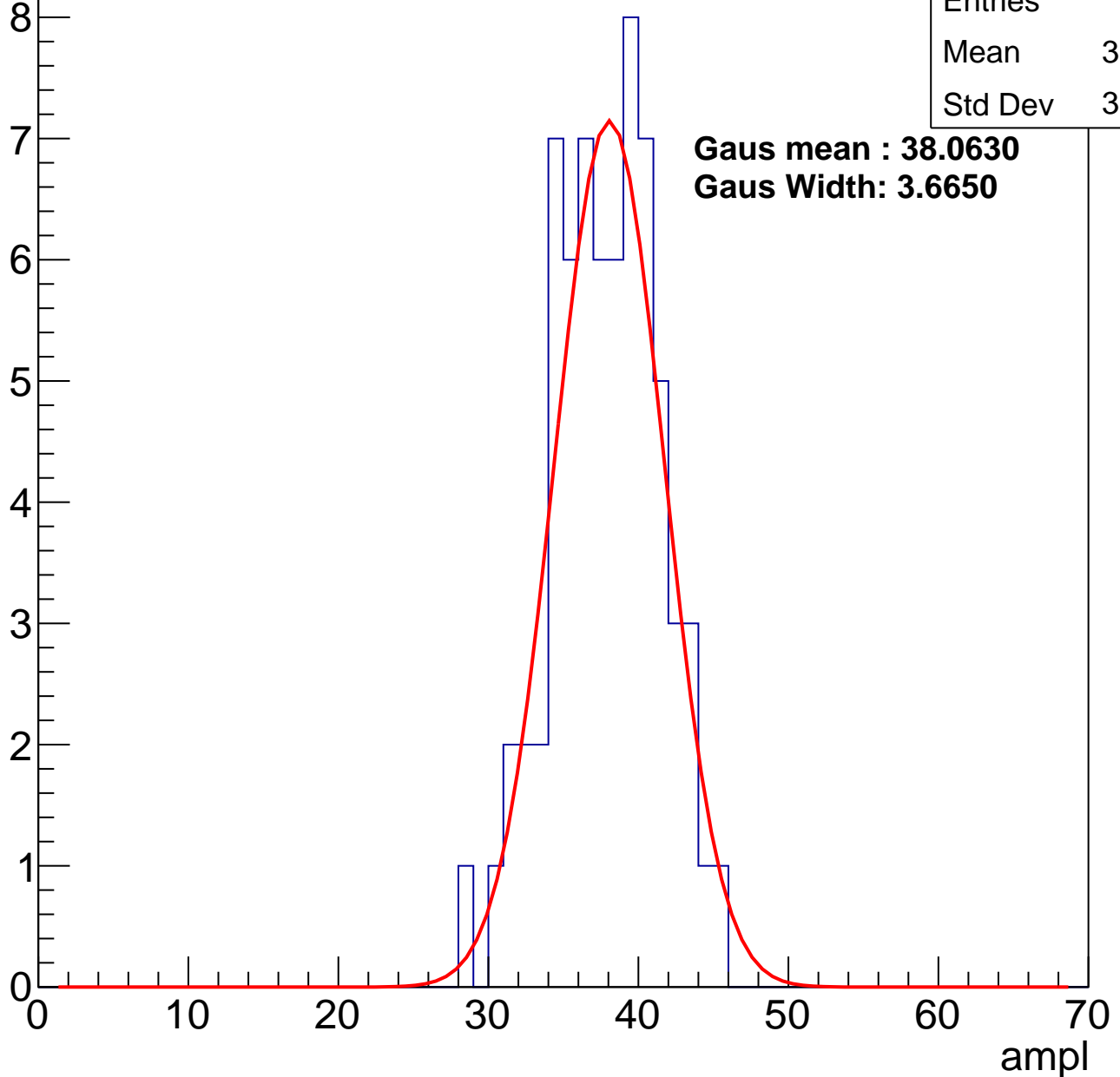
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.37
Std Dev	3.527

Gaus mean : 38.0630

Gaus Width: 3.6650



B1L102S, U9-ch3, adc2

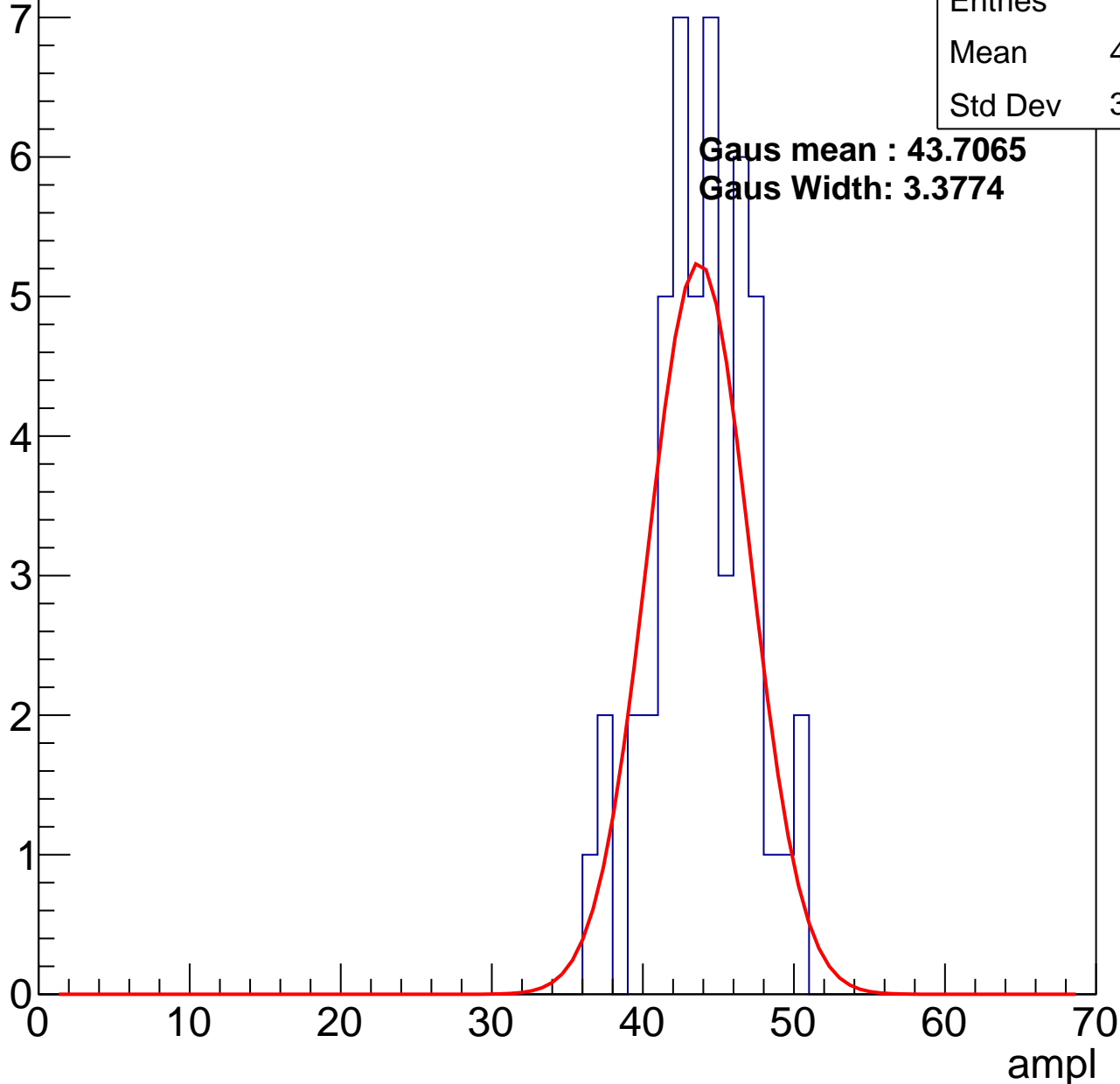
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.53
Std Dev	3.182

Gaus mean : 43.7065

Gaus Width: 3.3774



B1L102S, U9-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

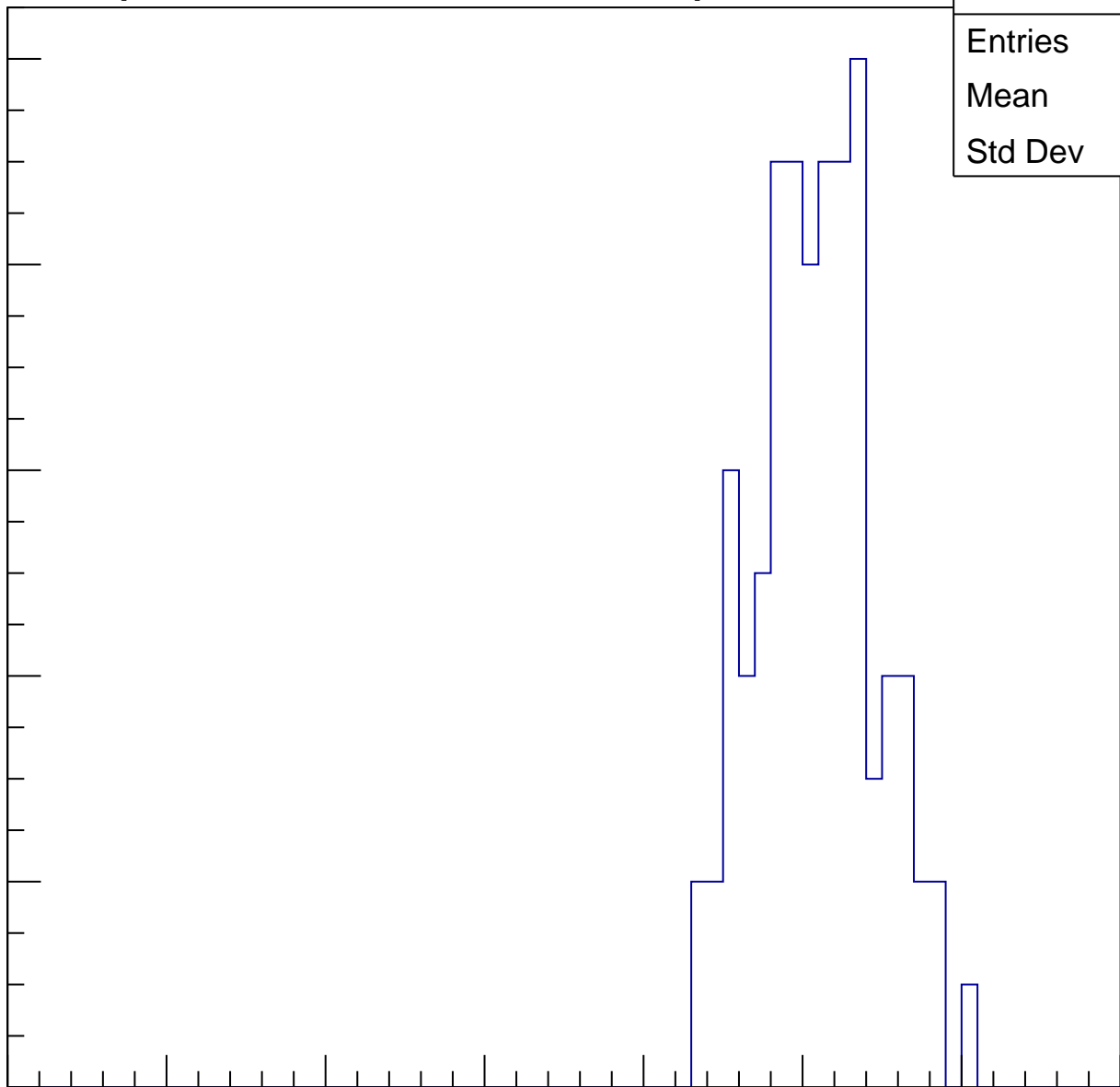
Entries	89
Mean	50.44
Std Dev	3.687

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

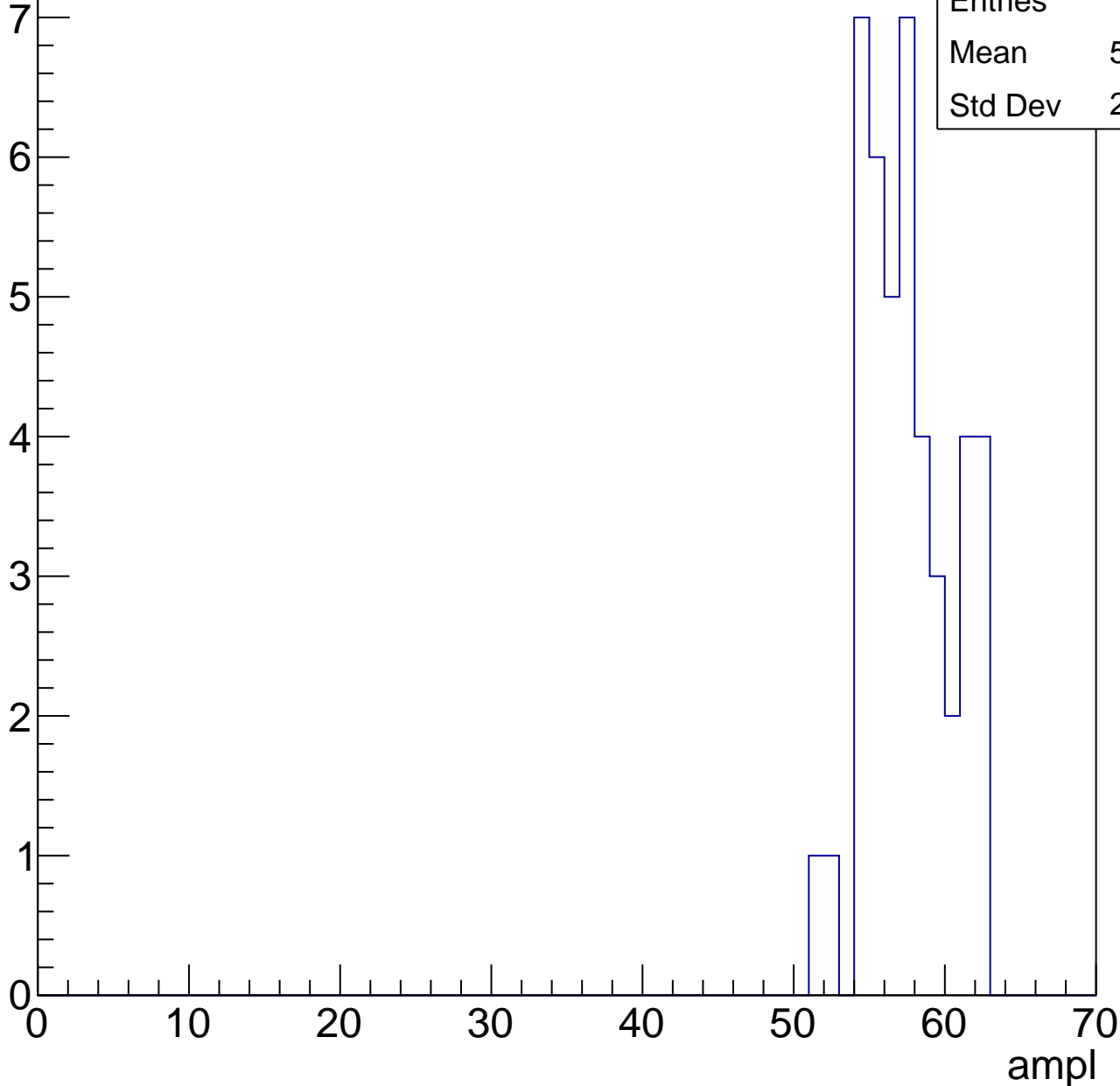


B1L102S, U9-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	57.07
Std Dev	2.824



B1L102S, U9-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	43
Mean	59.21
Std Dev	9.328

Entry

10

8

6

4

2

0

0

10

20

30

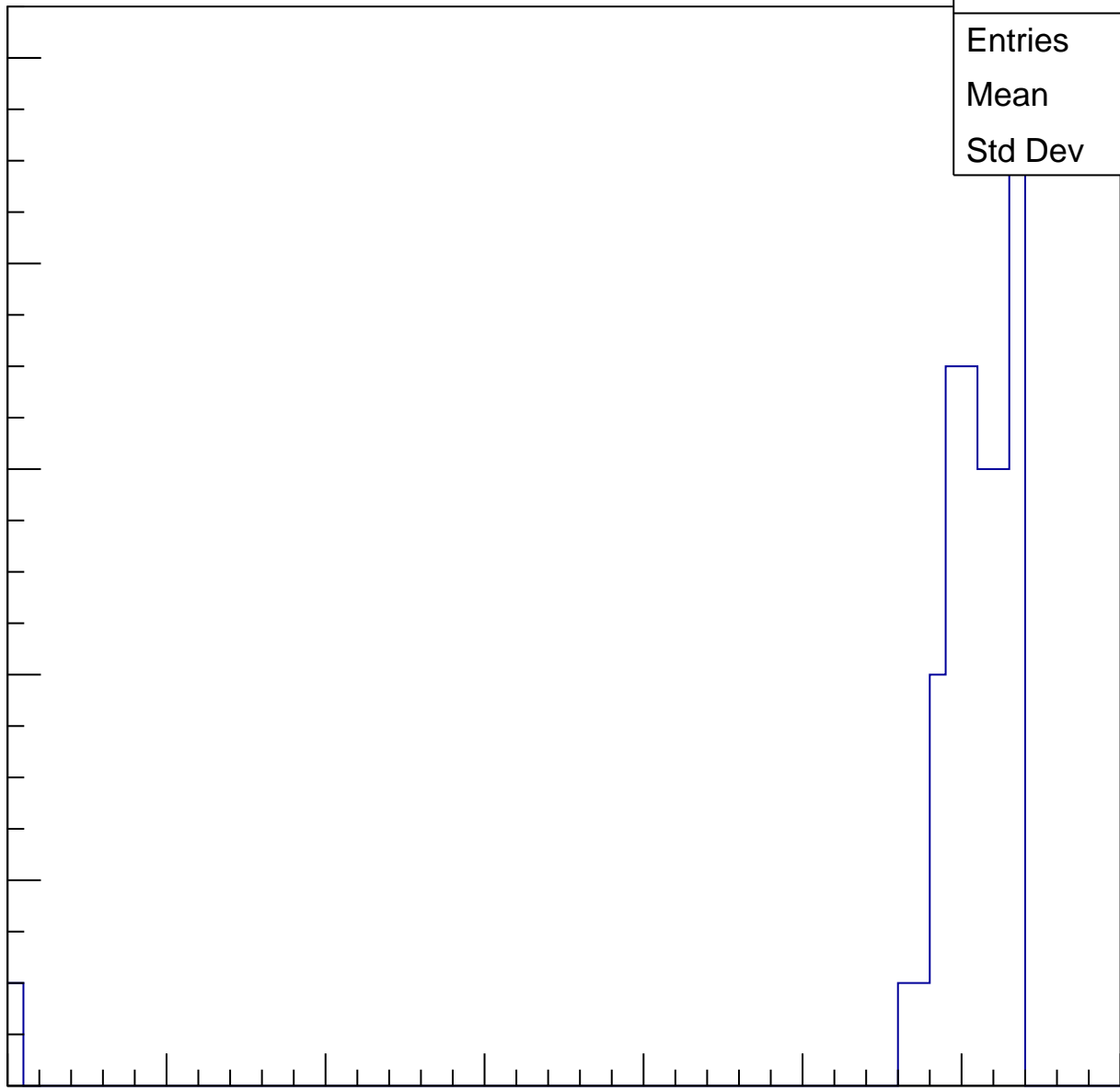
40

50

60

70

ampl



B1L102S, U9-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U9-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch4, adc0

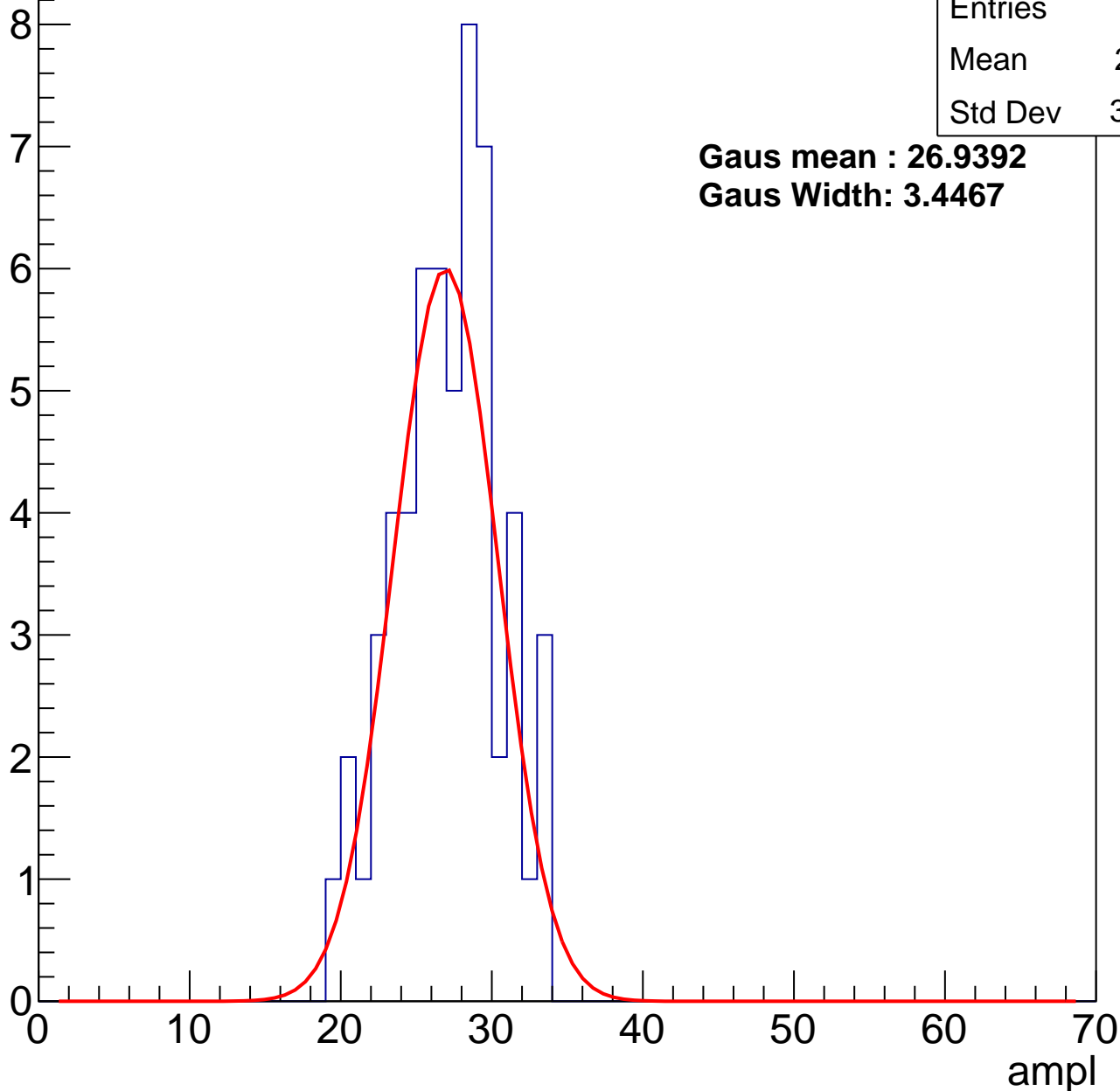
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	26.61
Std Dev	3.365

Gaus mean : 26.9392

Gaus Width: 3.4467



B1L102S, U9-ch4, adc1

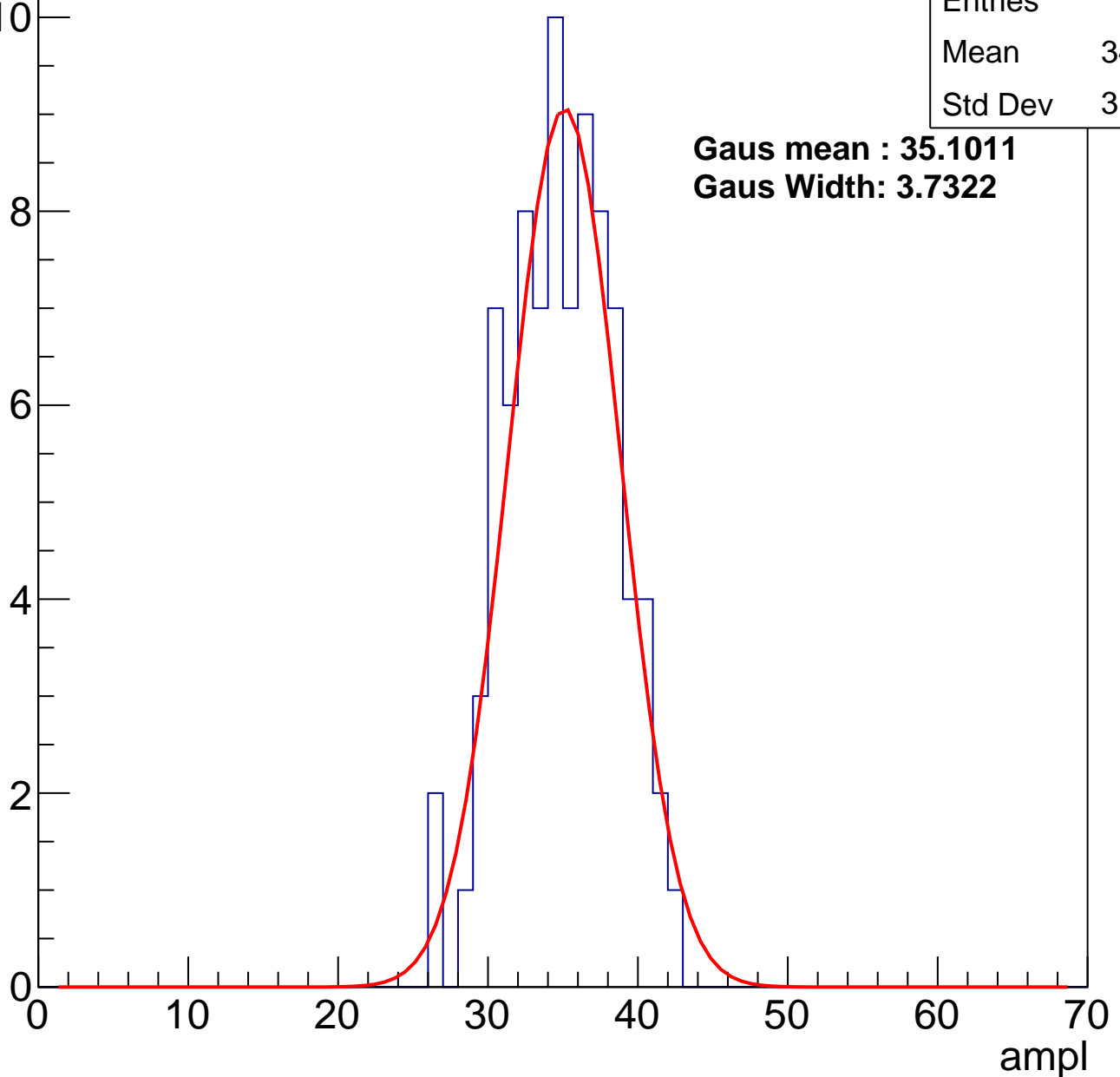
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	34.43
Std Dev	3.513

Gaus mean : 35.1011

Gaus Width: 3.7322



B1L102S, U9-ch4, adc2

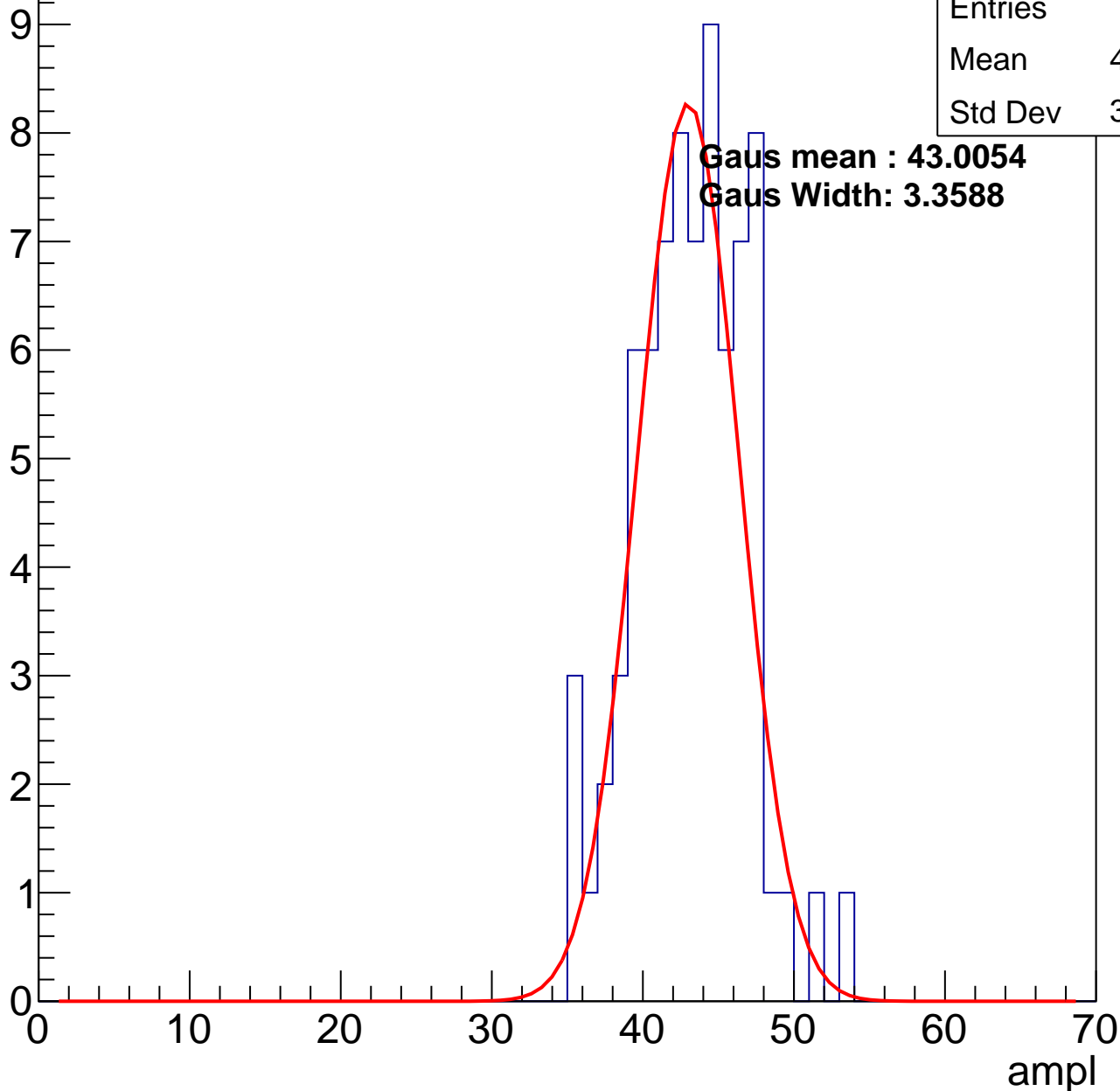
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42.75
Std Dev	3.637

Gaus mean : 43.0054

Gaus Width: 3.3588

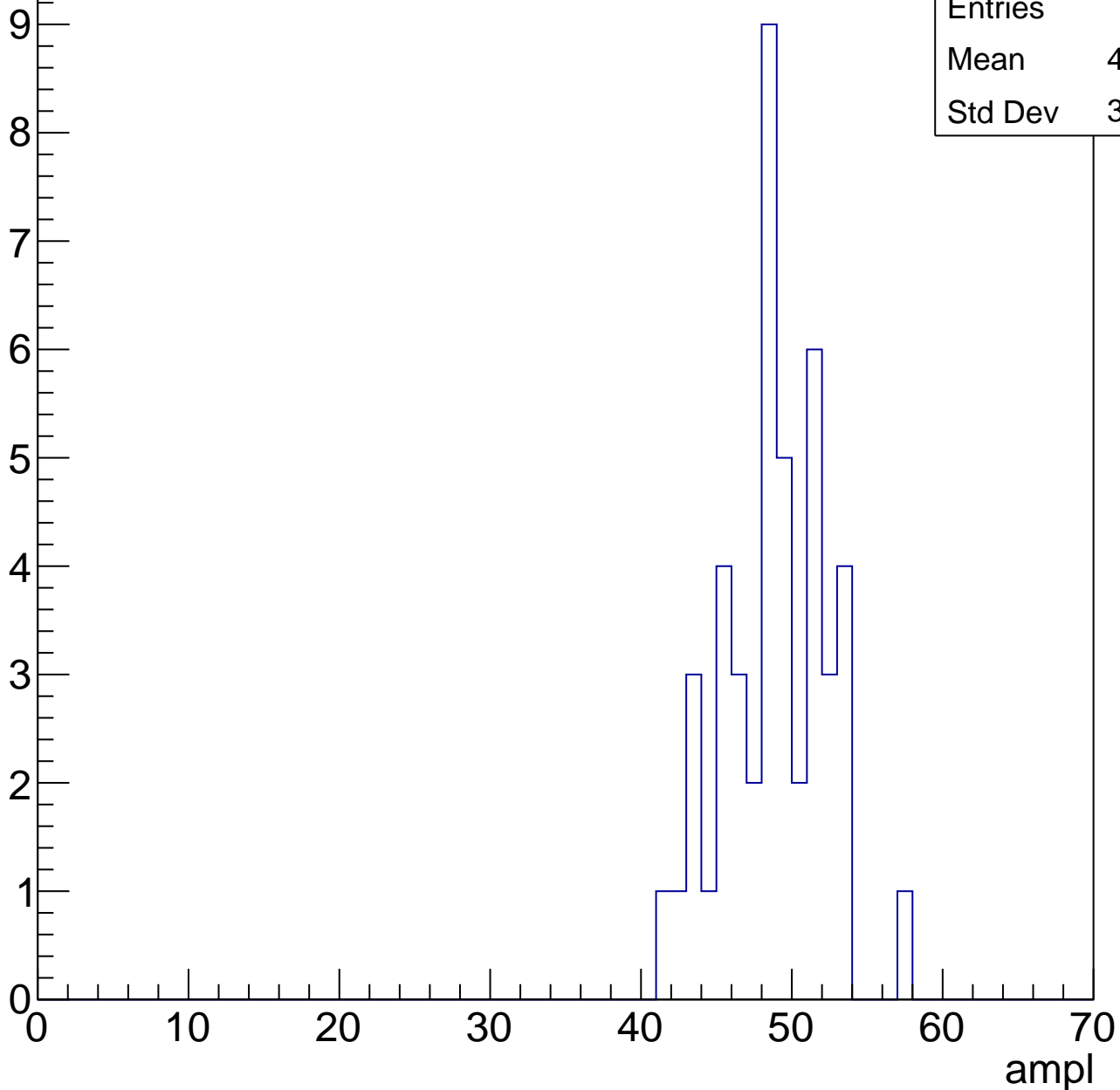


B1L102S, U9-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

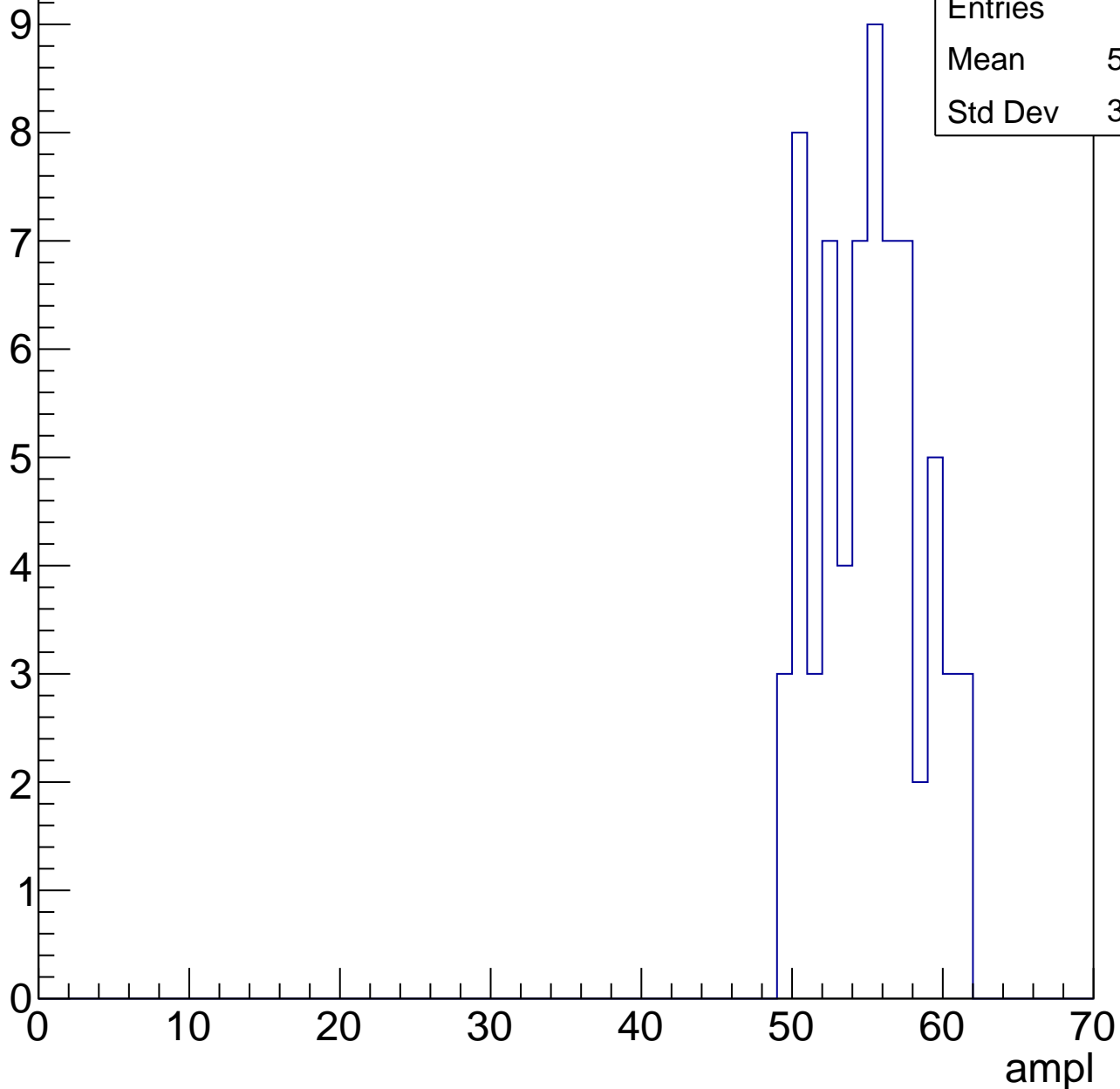
Entries	45
Mean	48.36
Std Dev	3.387



B1L102S, U9-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

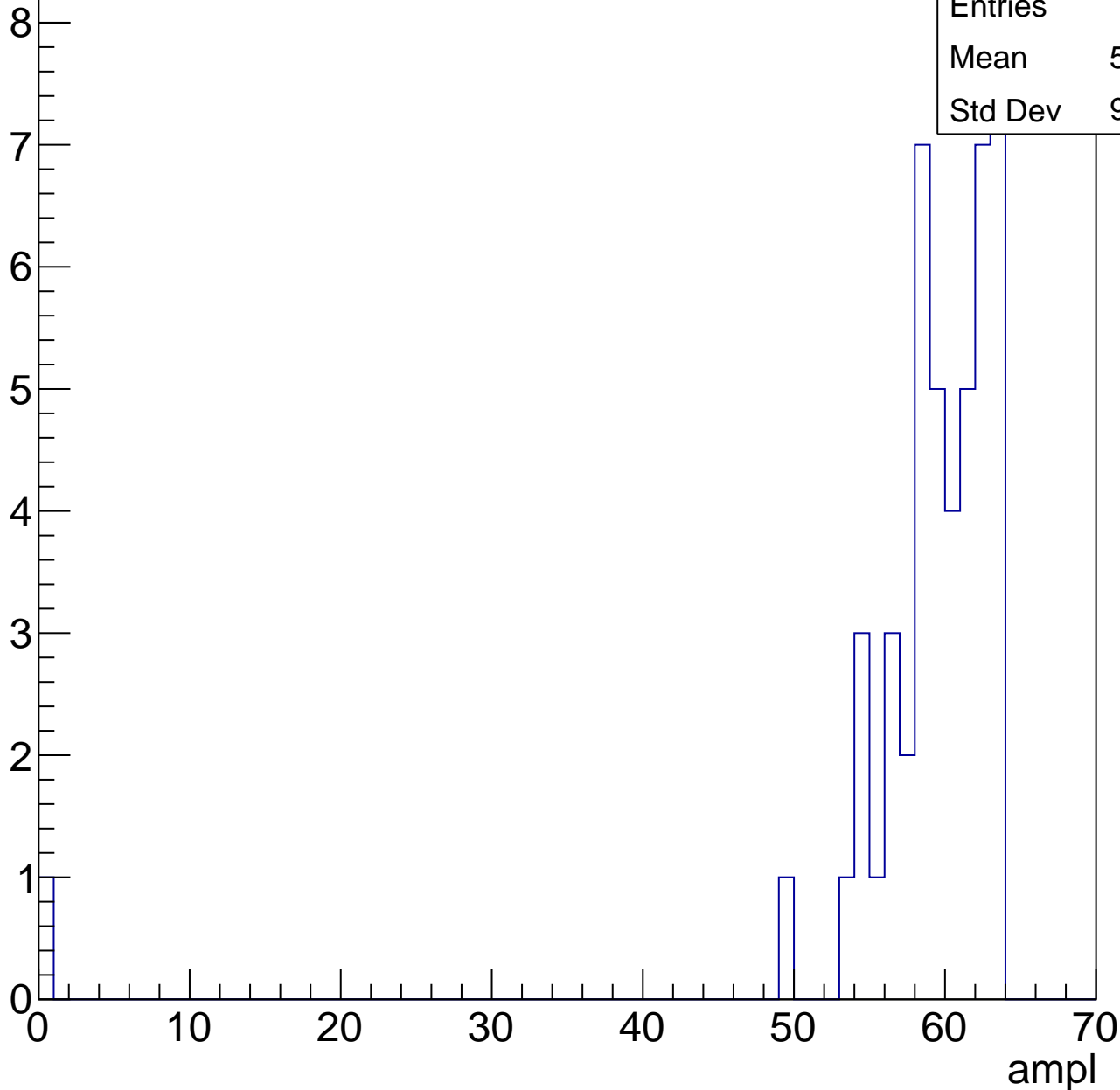


B1L102S, U9-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.02
Std Dev	9.036



B1L102S, U9-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

Mean

61.09

Std Dev

1.443

ampl

0 10 20 30 40 50 60 70

B1L102S, U9-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch5, adc0

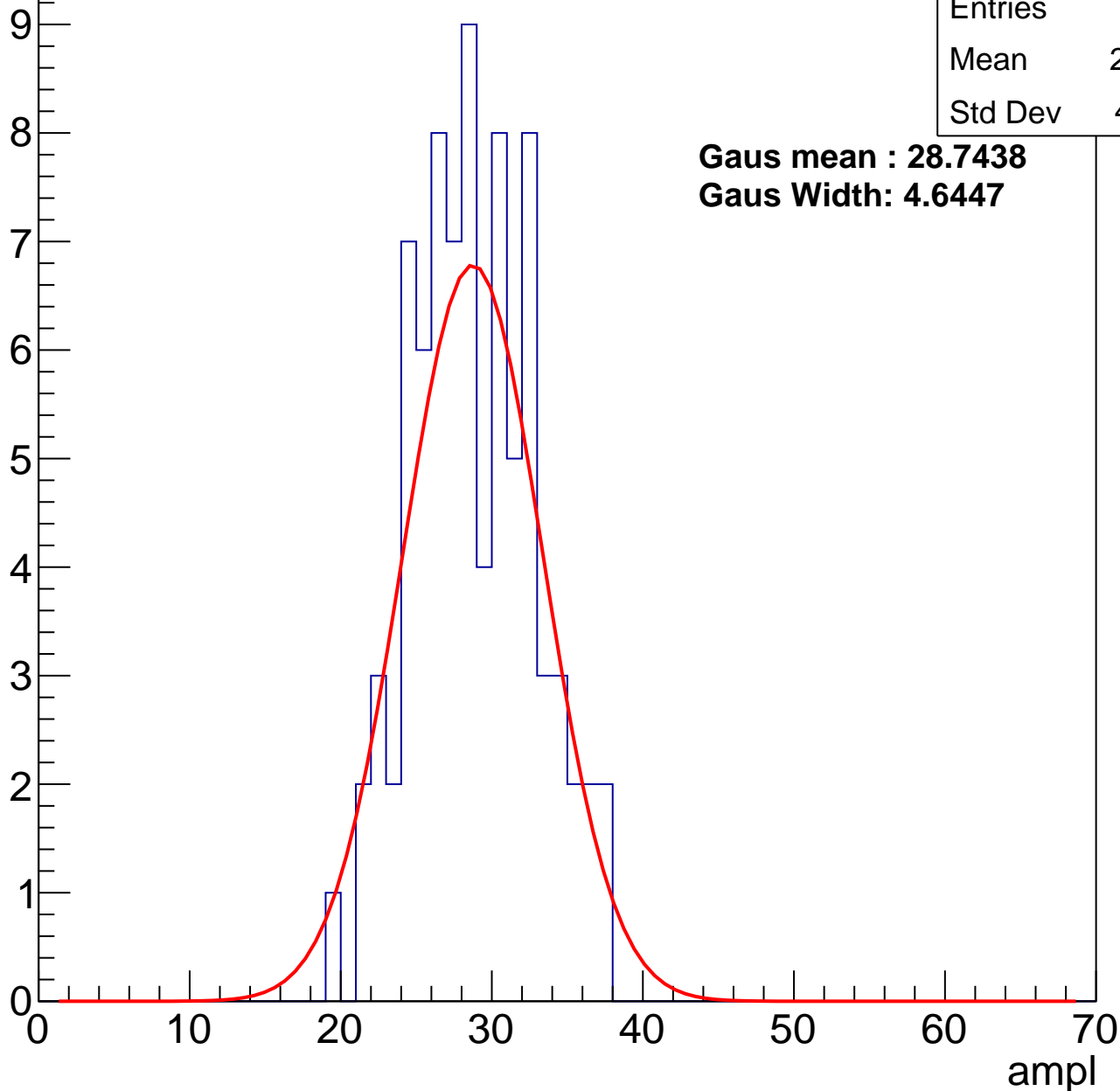
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.34
Std Dev	4.001

Gaus mean : 28.7438

Gaus Width: 4.6447



B1L102S, U9-ch5, adc1

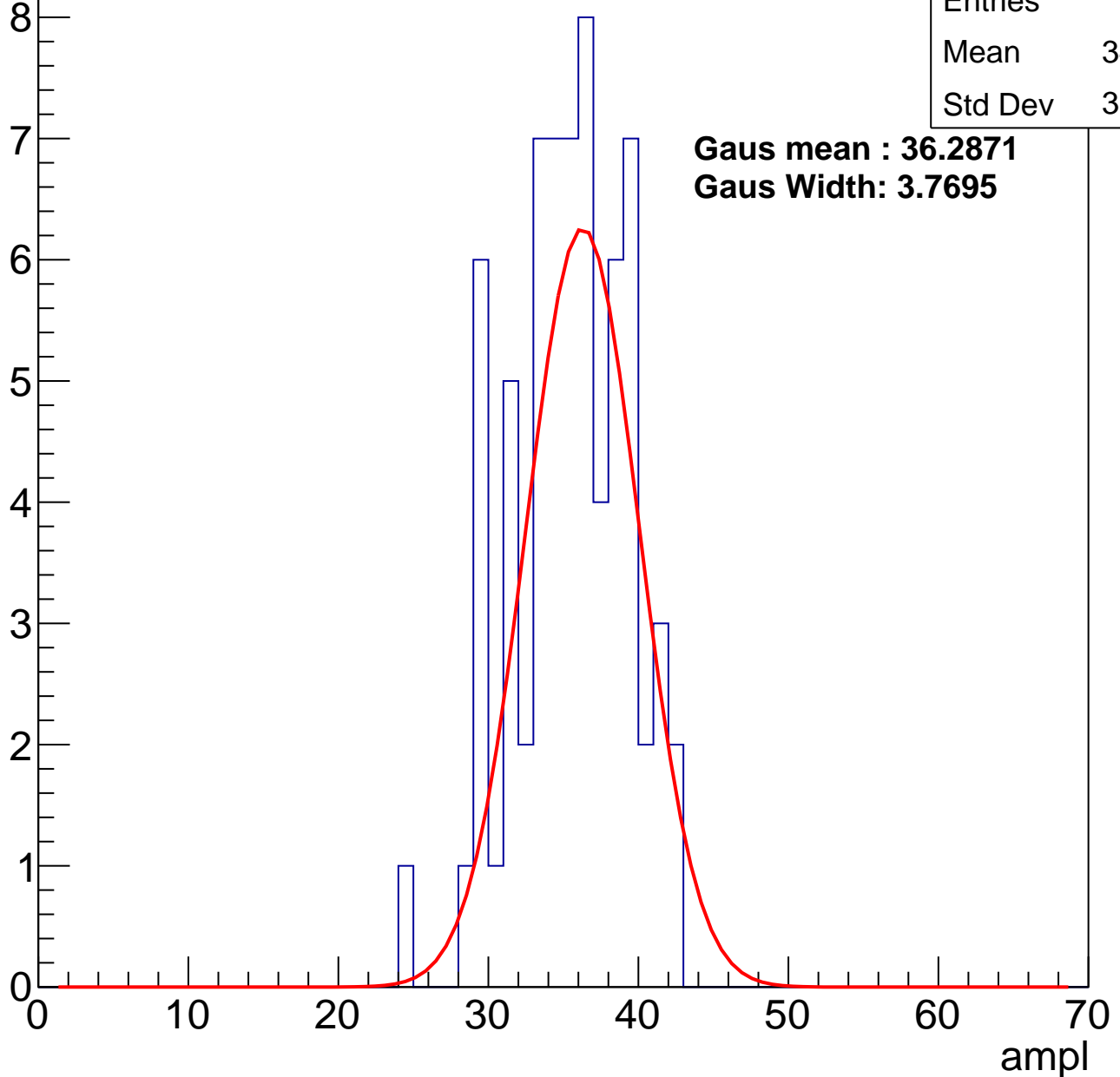
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.97
Std Dev	3.788

Gaus mean : 36.2871

Gaus Width: 3.7695



B1L102S, U9-ch5, adc2

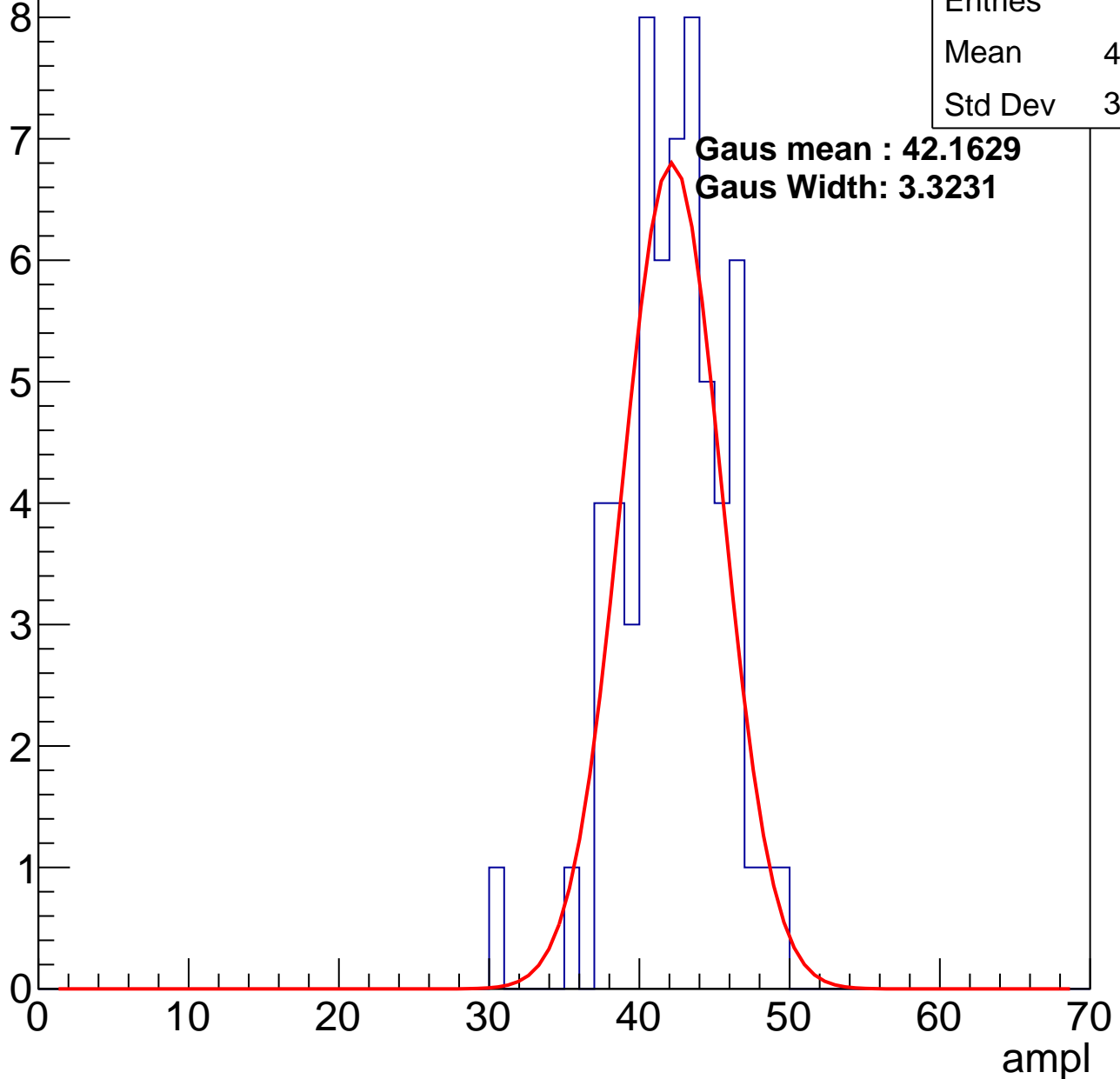
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.77
Std Dev	3.388

Gaus mean : 42.1629

Gaus Width: 3.3231

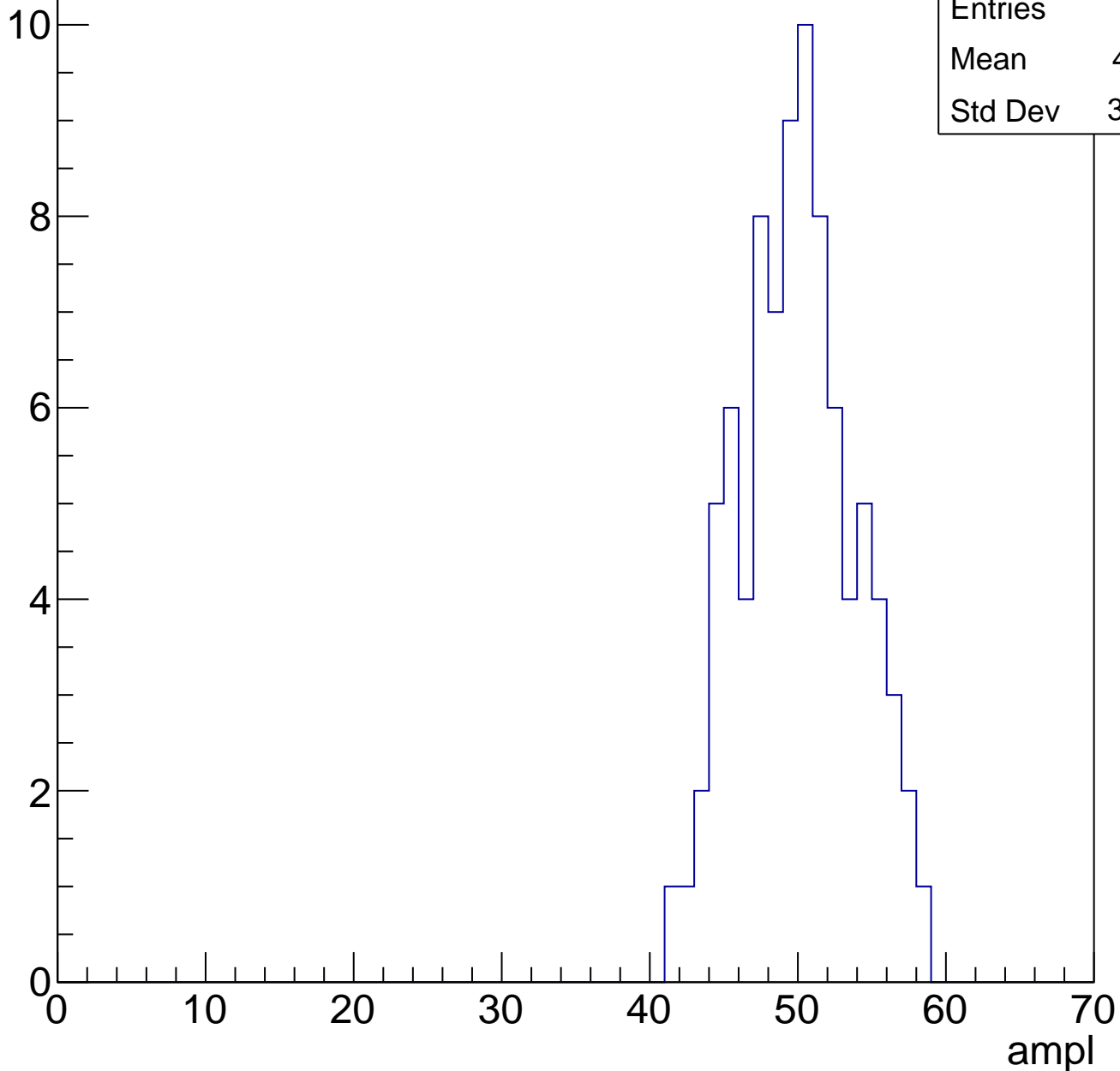


B1L102S, U9-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	49.51
Std Dev	3.803

Entry

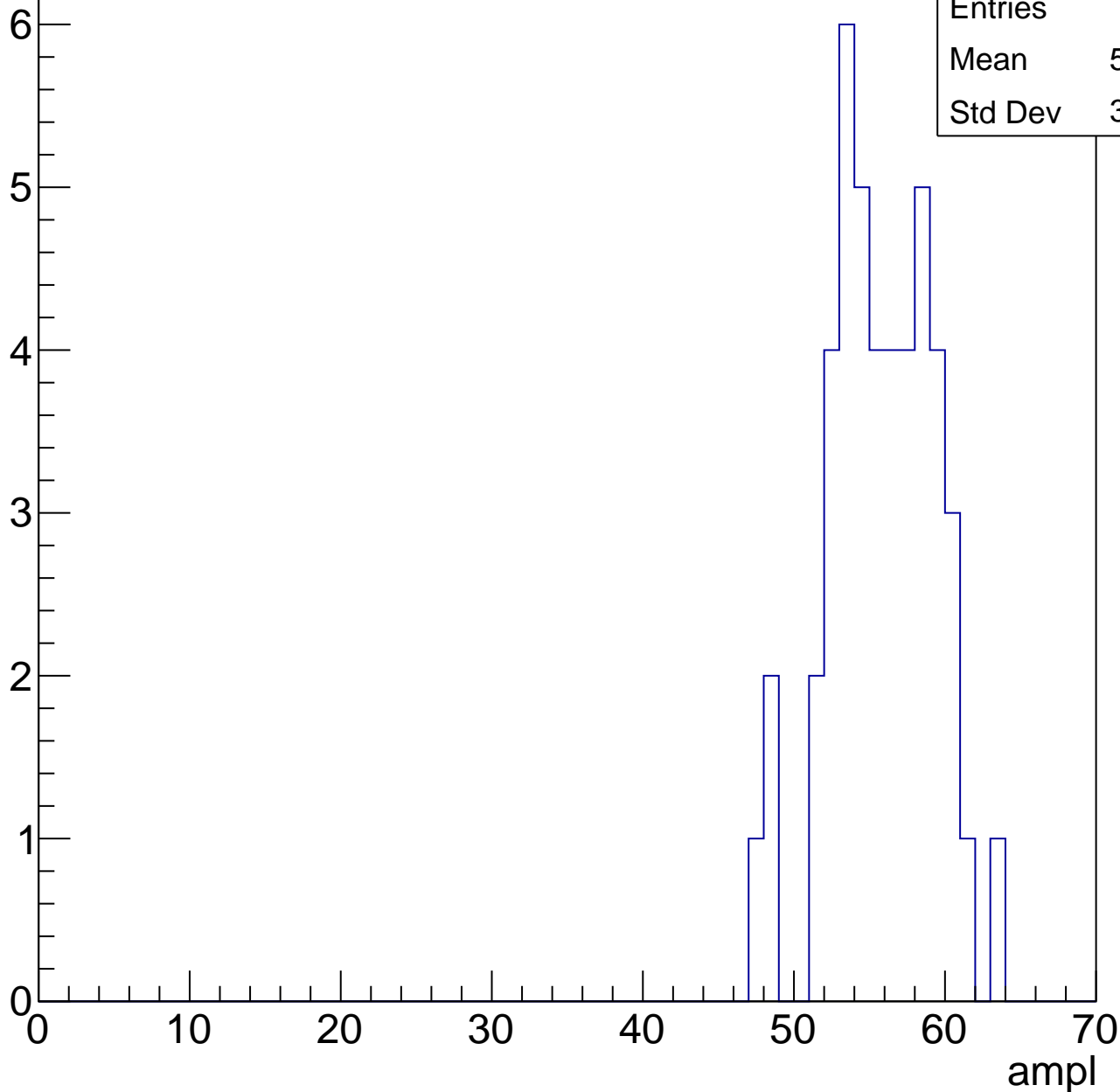


B1L102S, U9-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	55.28
Std Dev	3.487



B1L102S, U9-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

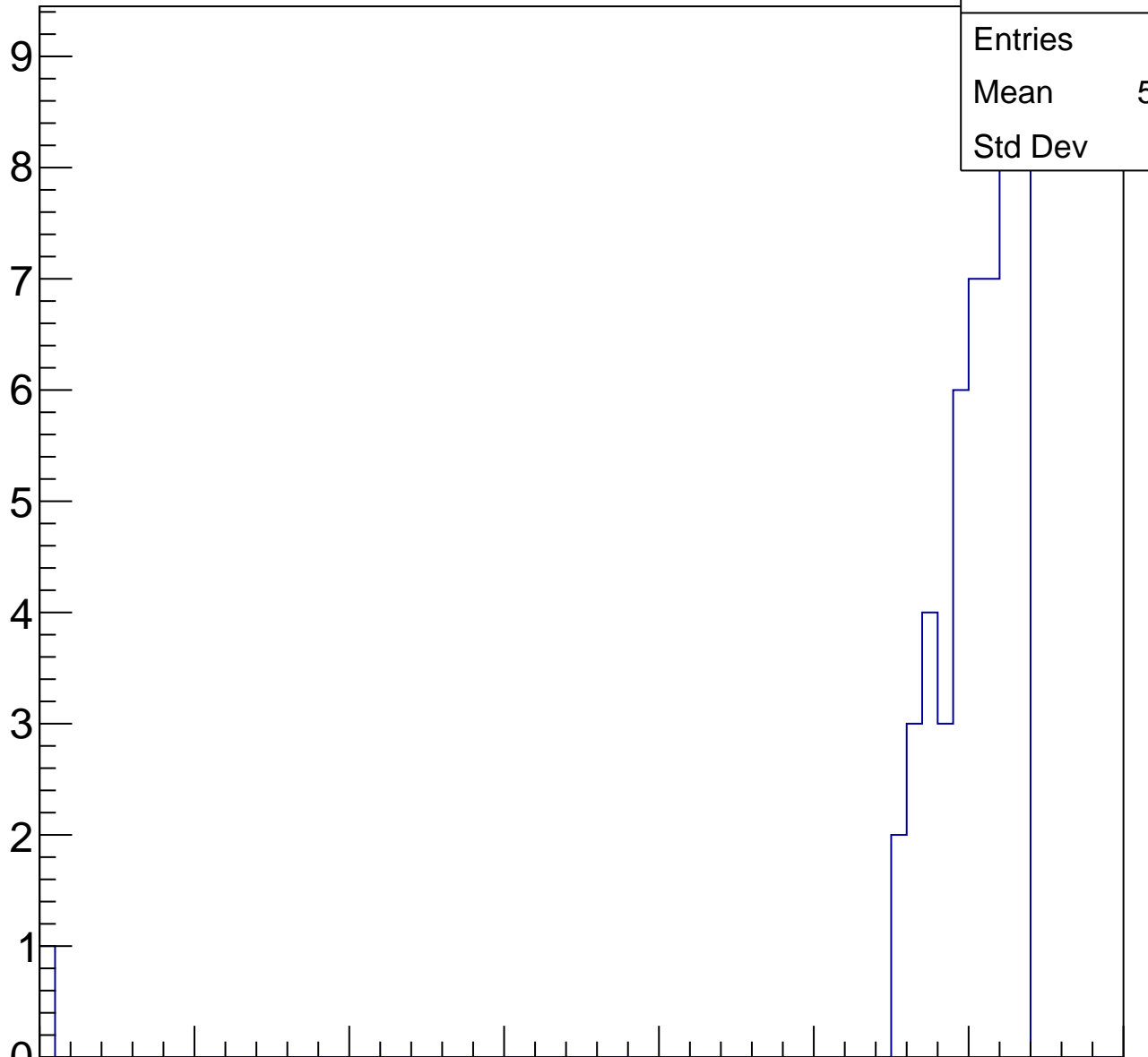
Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.86
Std Dev	8.72

ampl

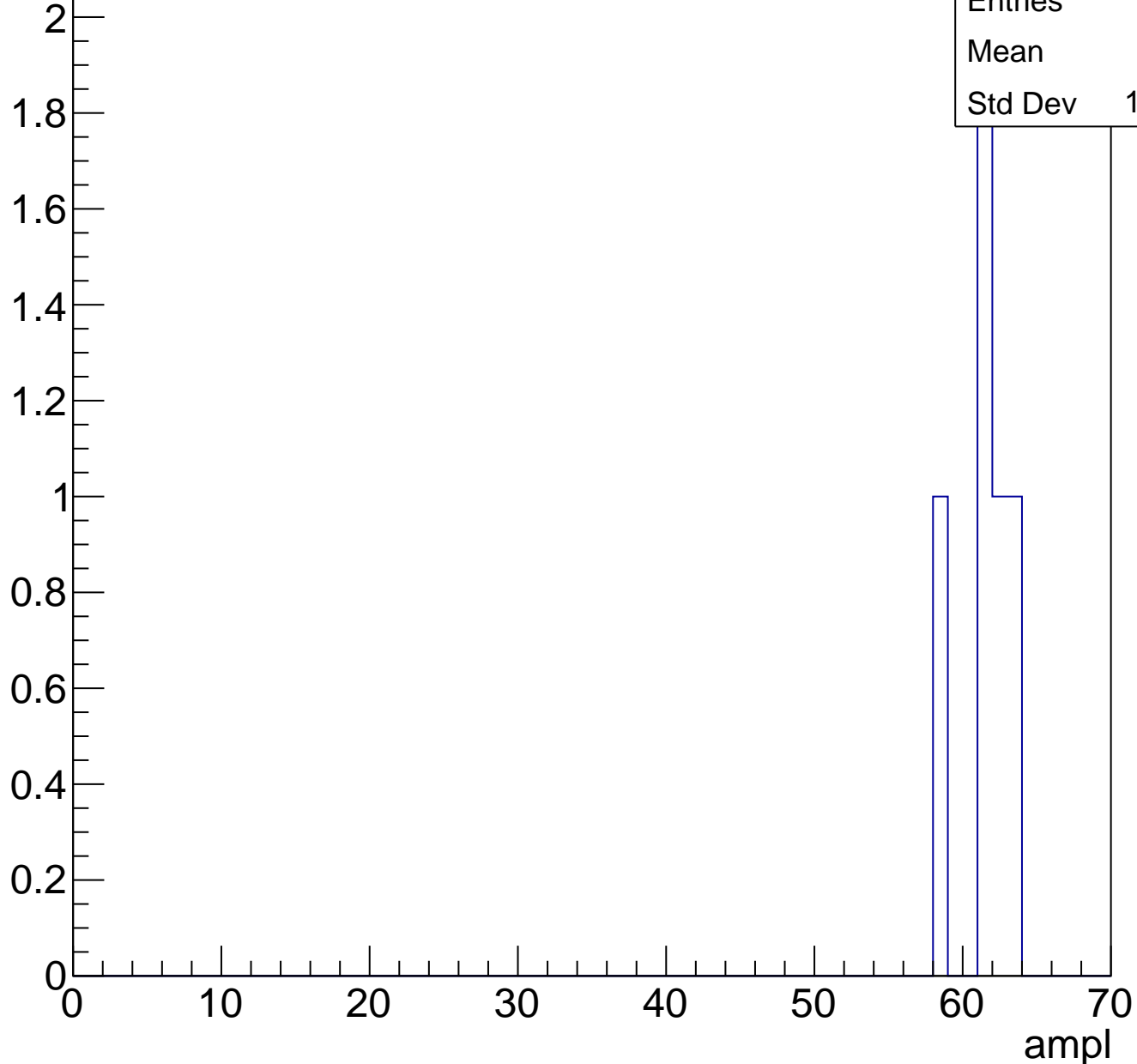
0 10 20 30 40 50 60 70



B1L102S, U9-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch6, adc0

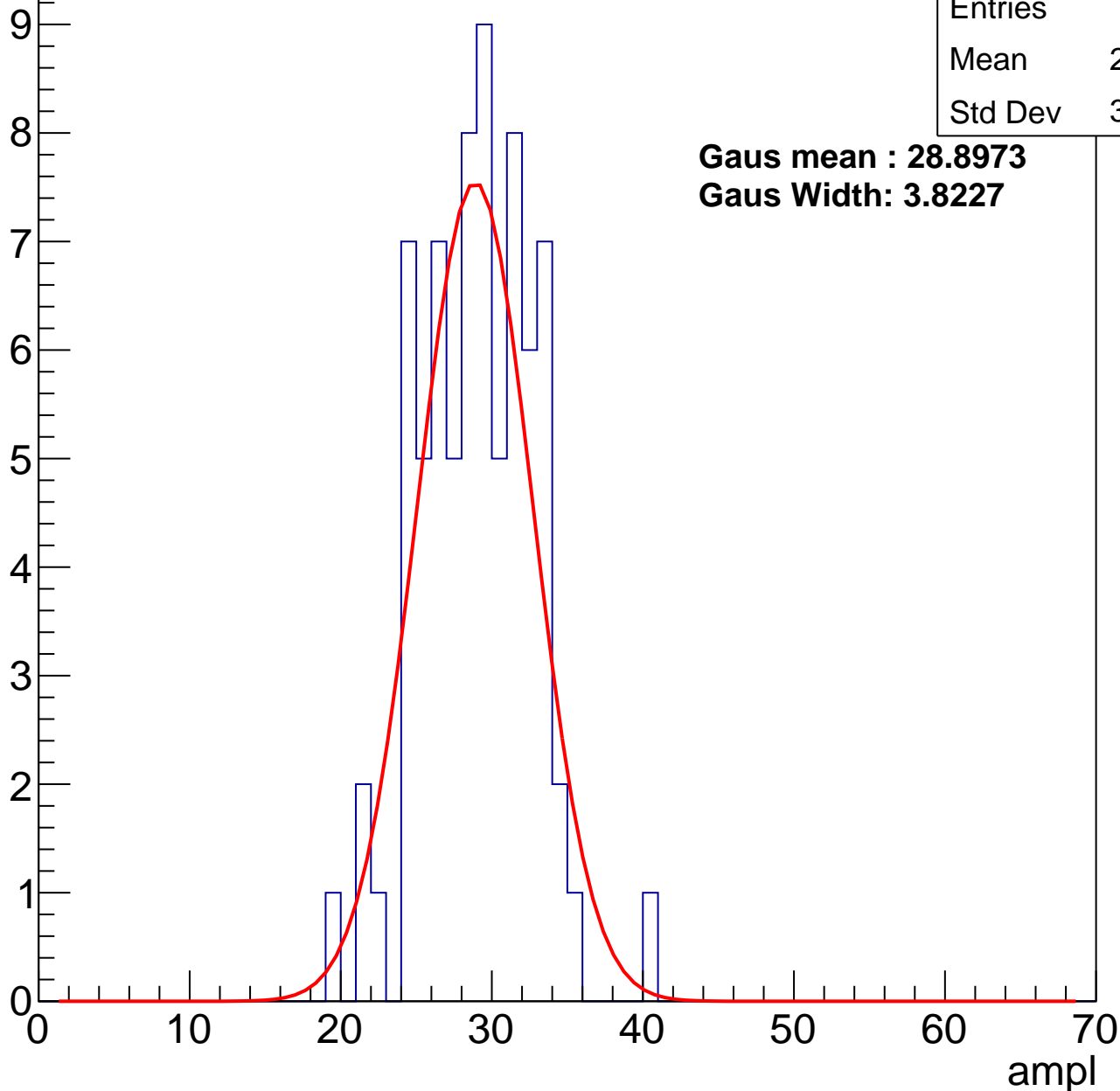
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.56
Std Dev	3.685

Gaus mean : 28.8973

Gaus Width: 3.8227



B1L102S, U9-ch6, adc1

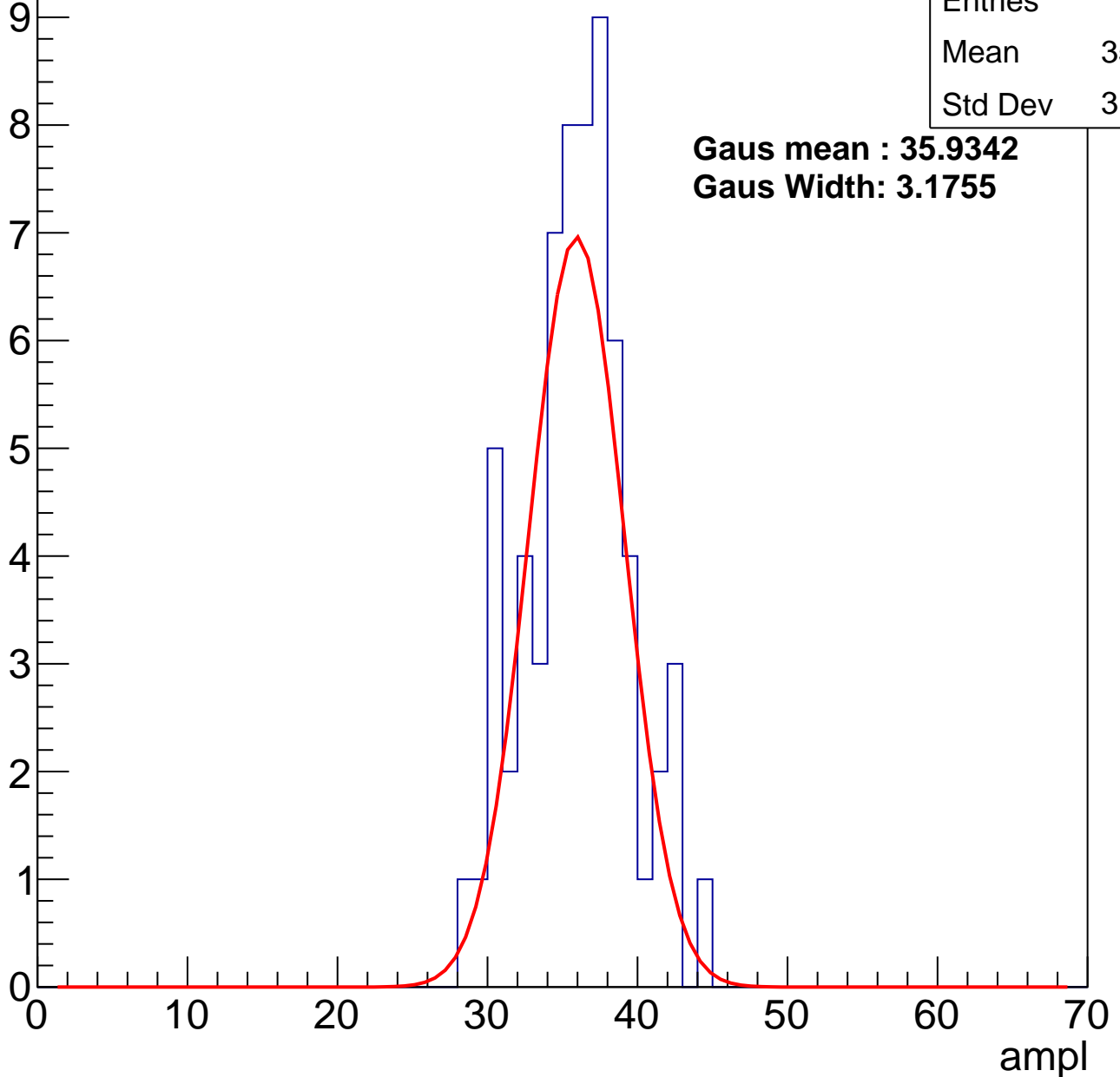
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.55
Std Dev	3.429

Gaus mean : 35.9342

Gaus Width: 3.1755



B1L102S, U9-ch6, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	42.11
Std Dev	3.581

Gaus mean : 42.5663

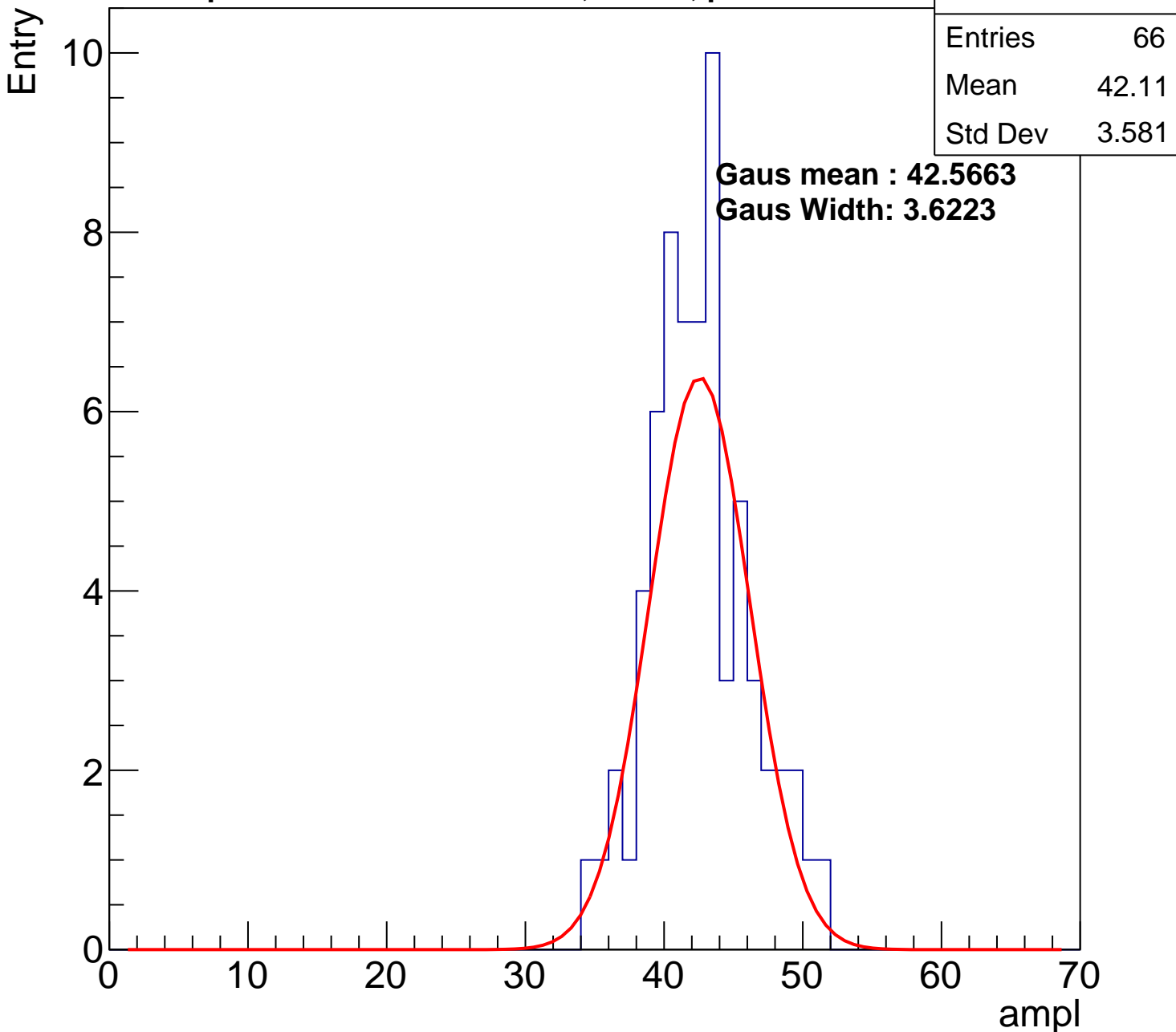
Gaus Width: 3.6223

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

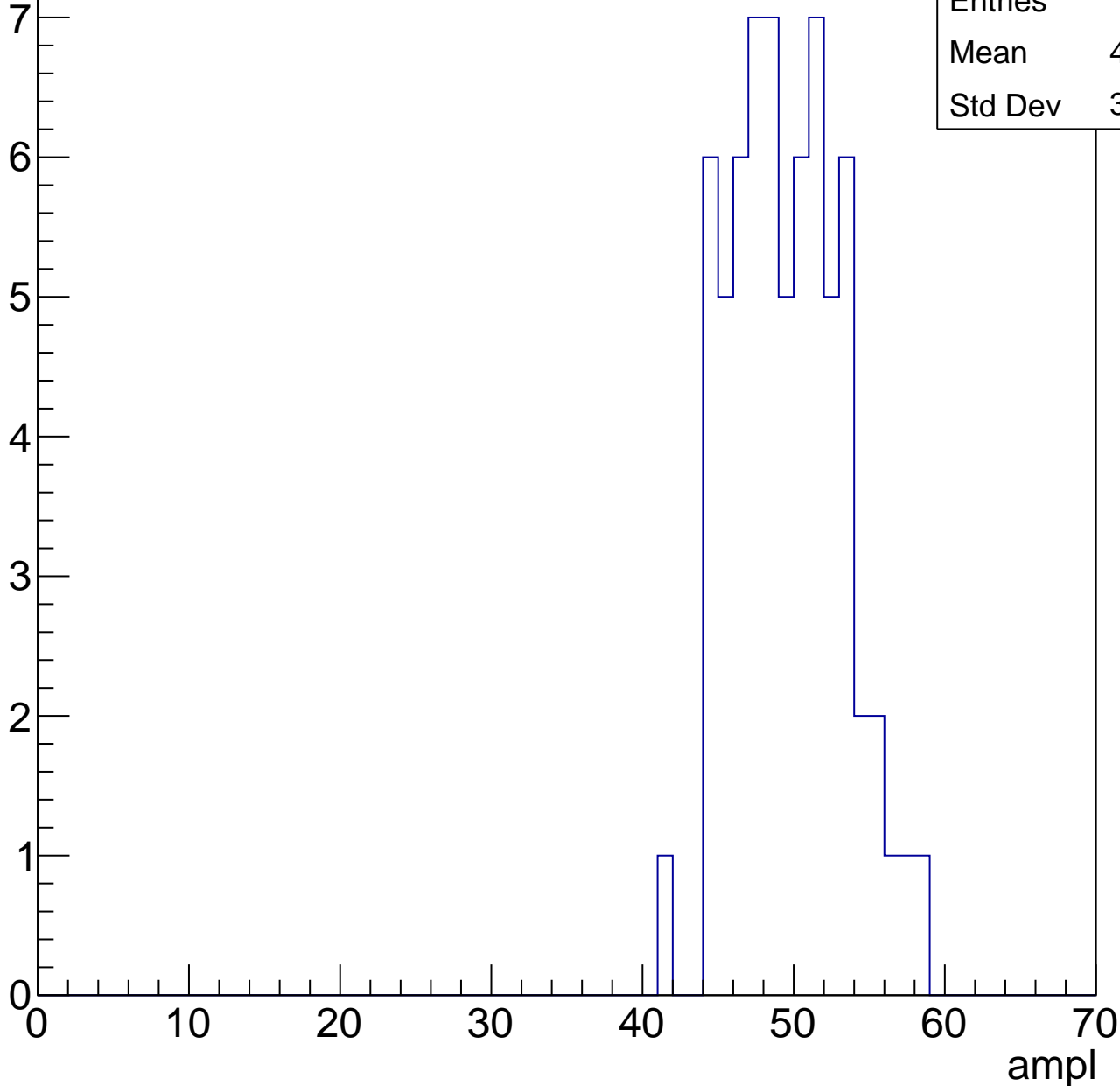


B1L102S, U9-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.12
Std Dev	3.583

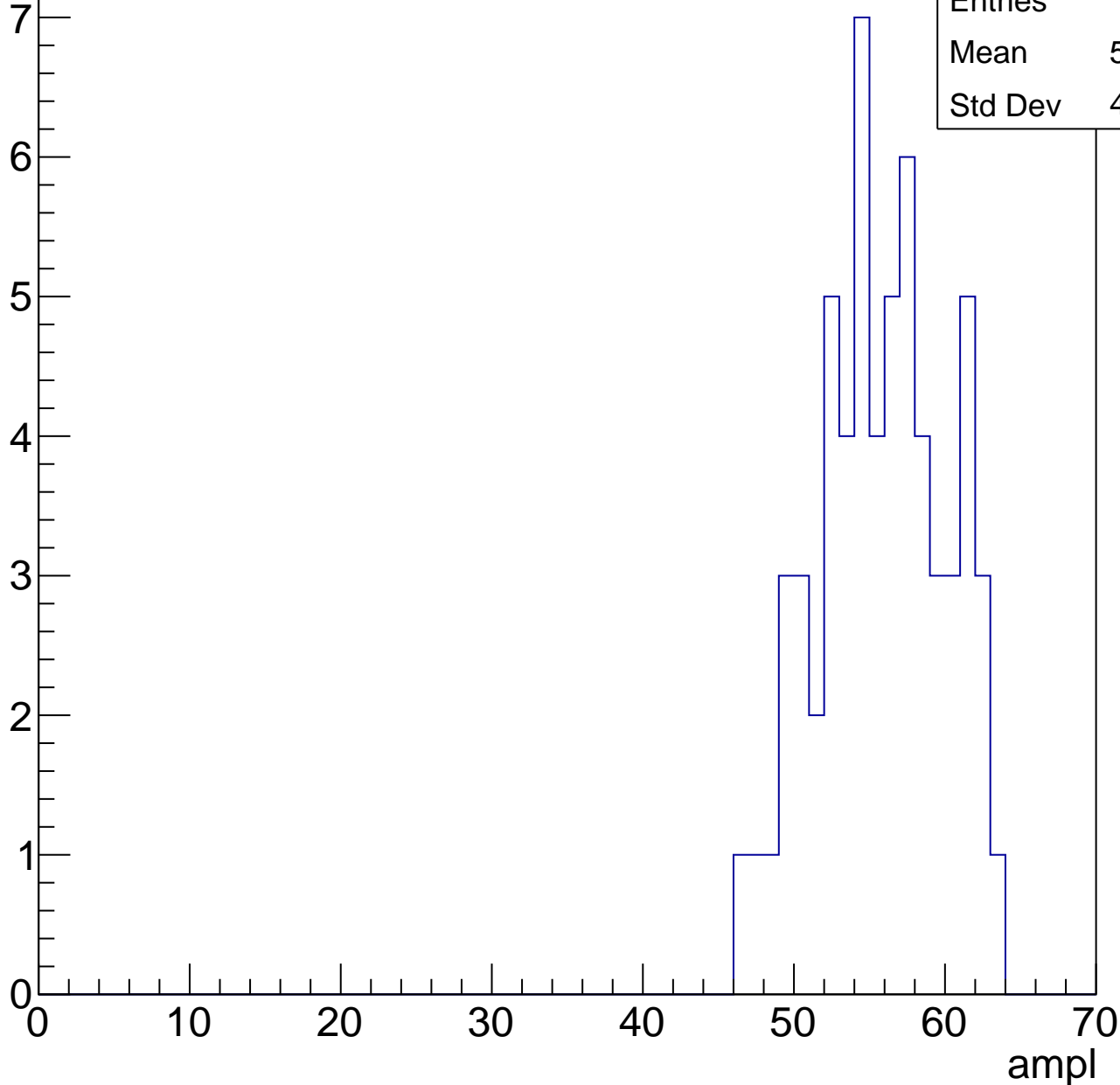


B1L102S, U9-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.33
Std Dev	4.148

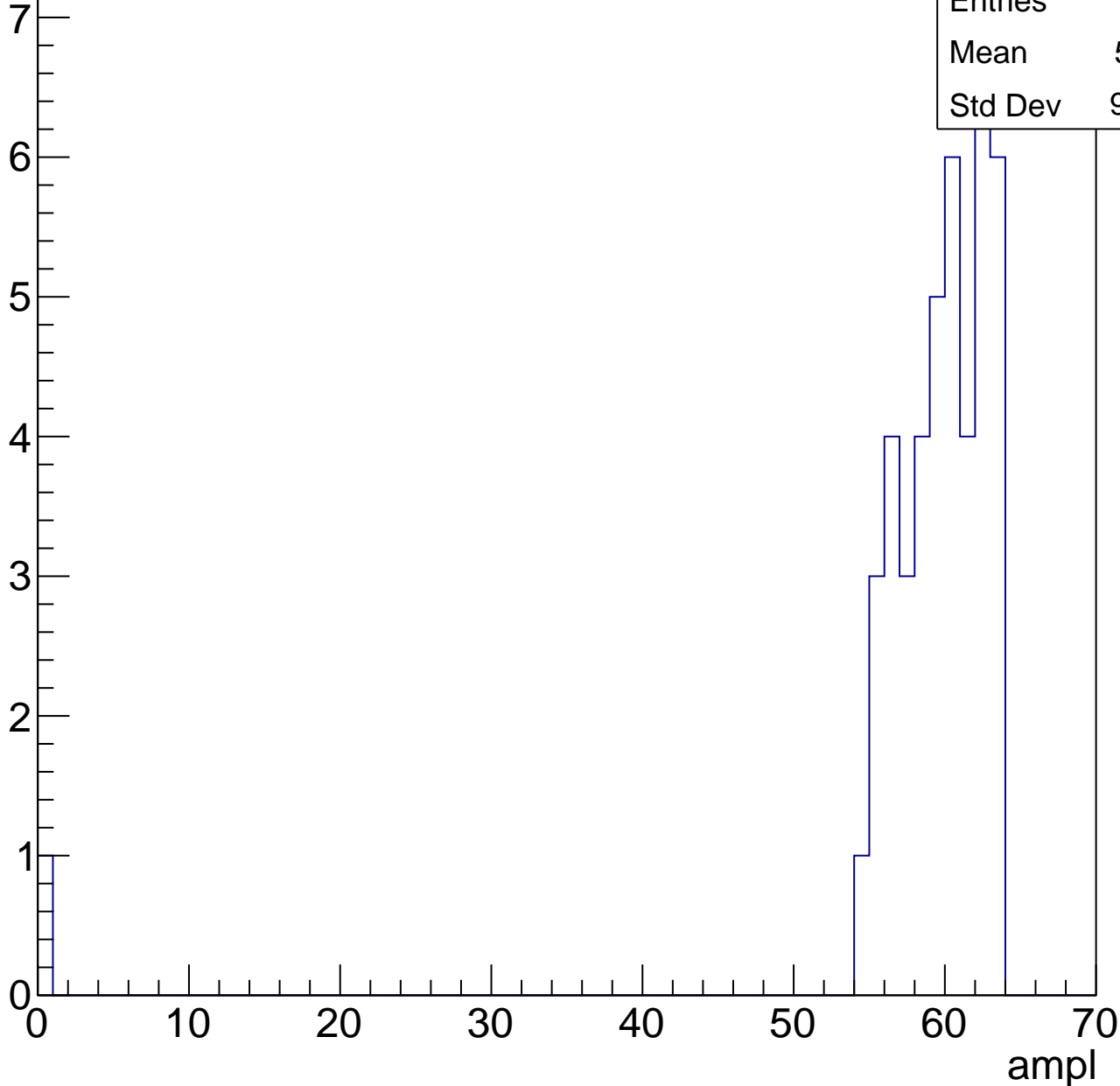


B1L102S, U9-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.11
Std Dev	9.235



B1L102S, U9-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

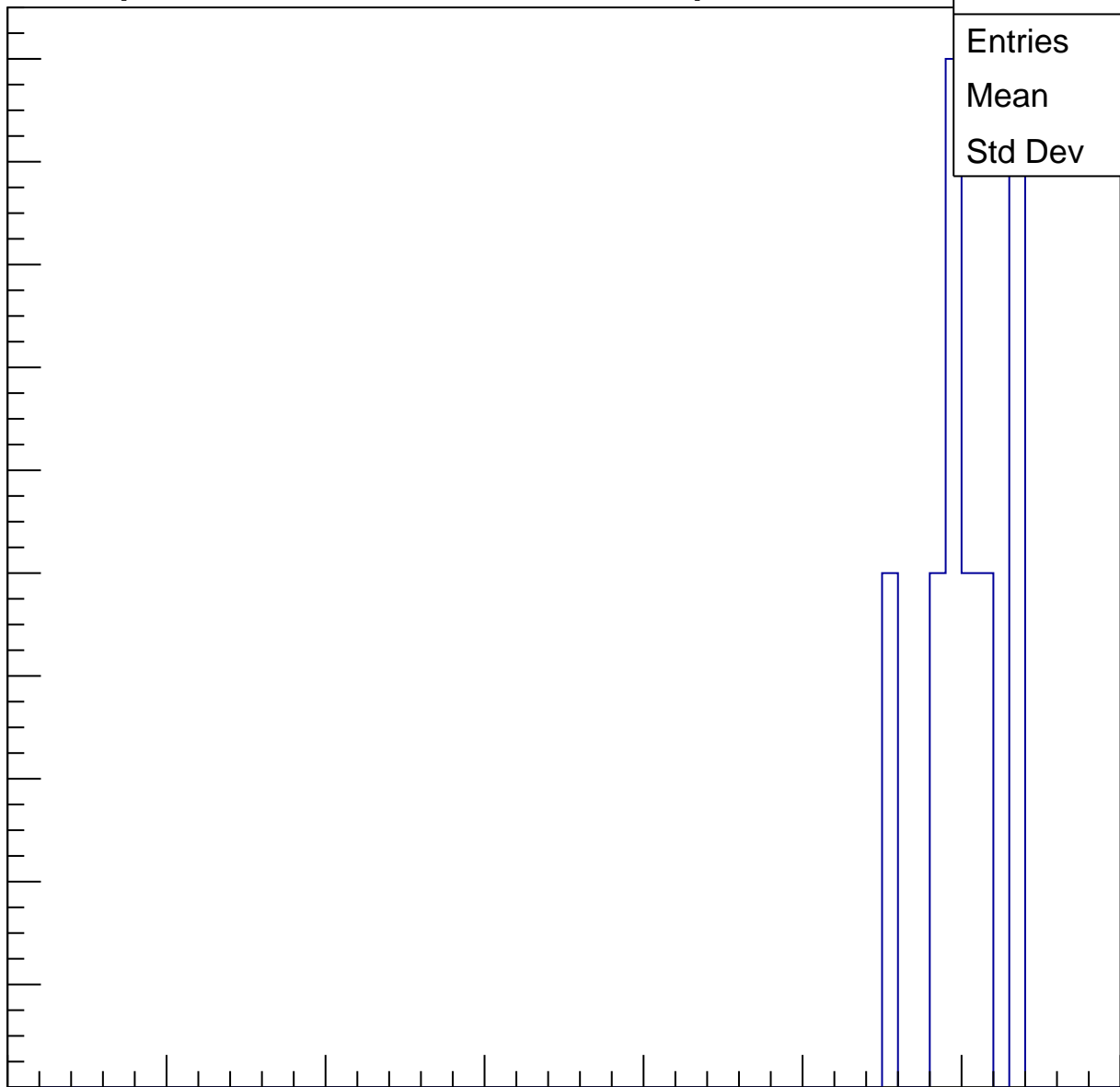
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	59.75
Std Dev	2.487

0 10 20 30 40 50 60 70

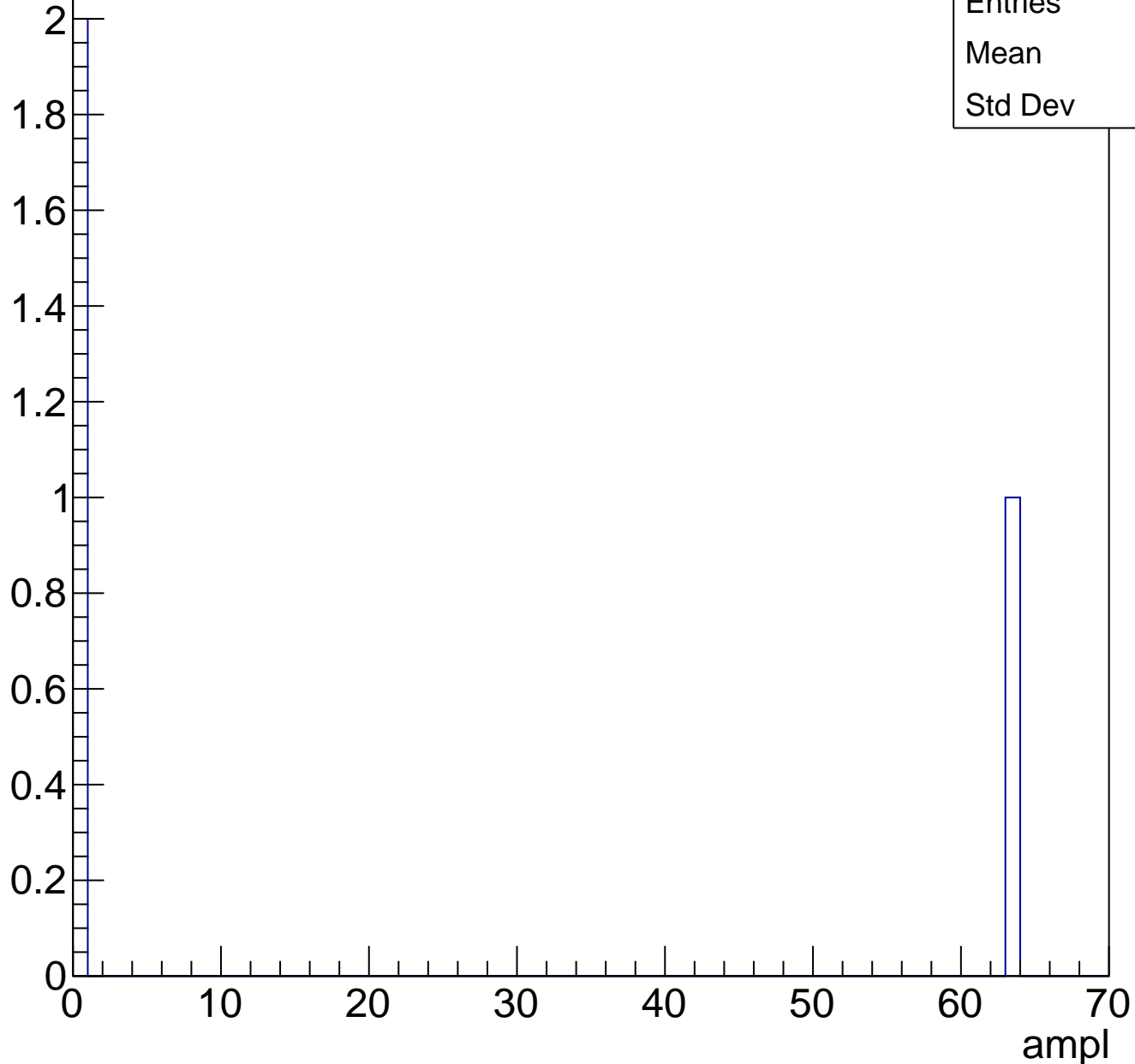
ampl



B1L102S, U9-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U9-ch7, adc0

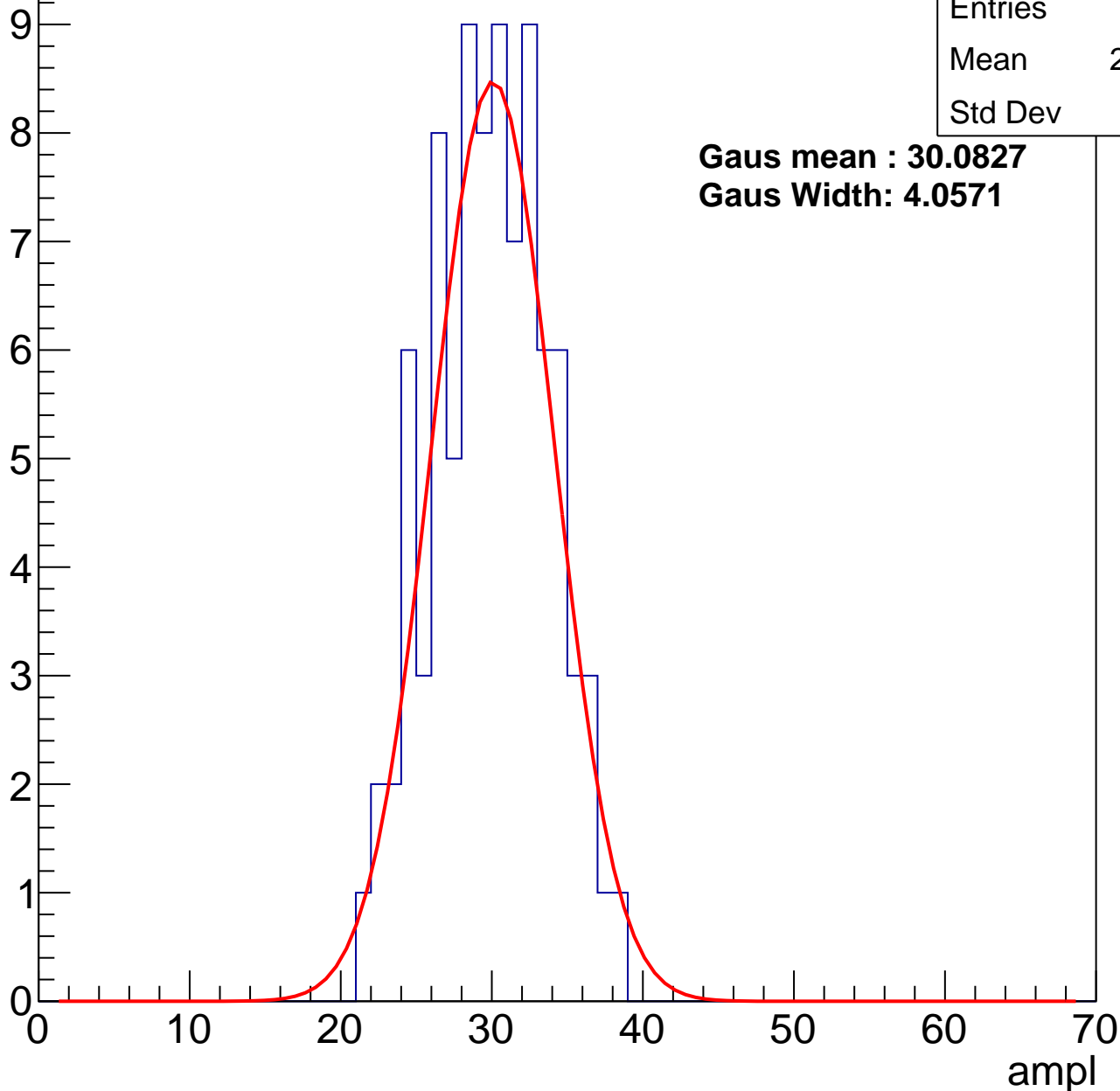
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	29.46
Std Dev	3.79

Gaus mean : 30.0827

Gaus Width: 4.0571



B1L102S, U9-ch7, adc1

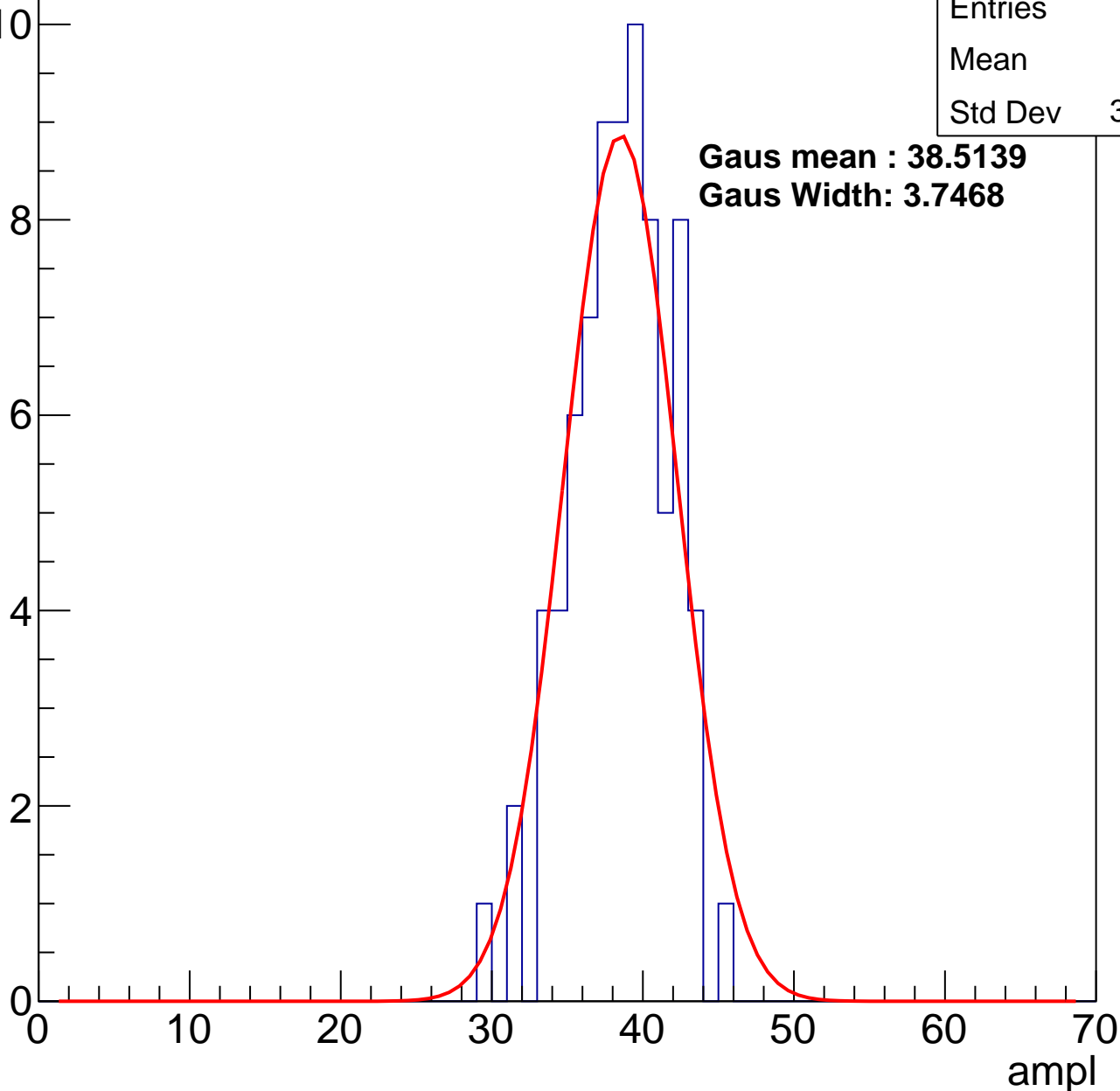
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	38
Std Dev	3.199

Gaus mean : 38.5139

Gaus Width: 3.7468



B1L102S, U9-ch7, adc2

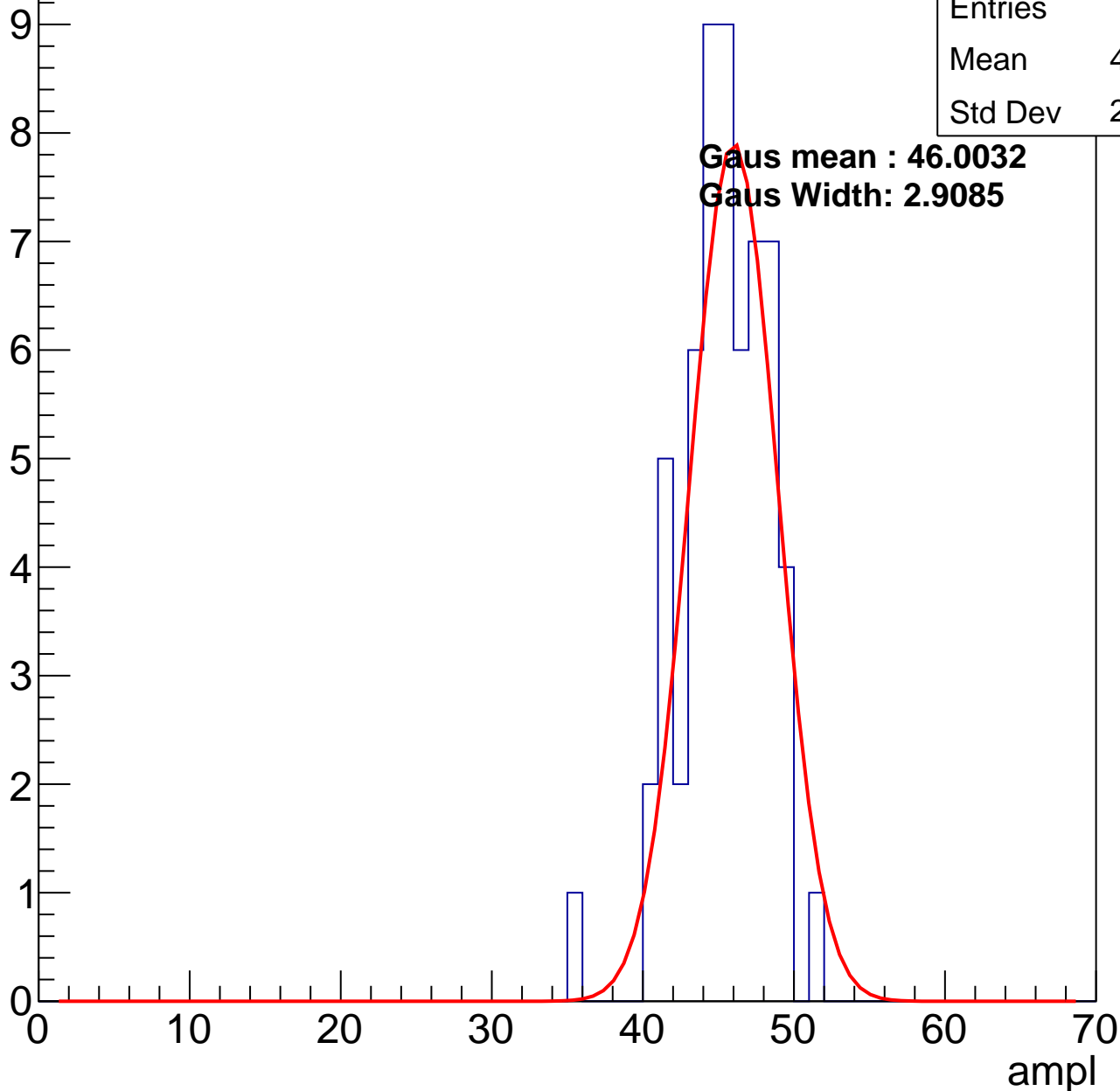
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	44.93
Std Dev	2.846

Gaus mean : 46.0032

Gaus Width: 2.9085



B1L102S, U9-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	52.01
Std Dev	3.317

Entry

10

8

6

4

2

0

0

10

20

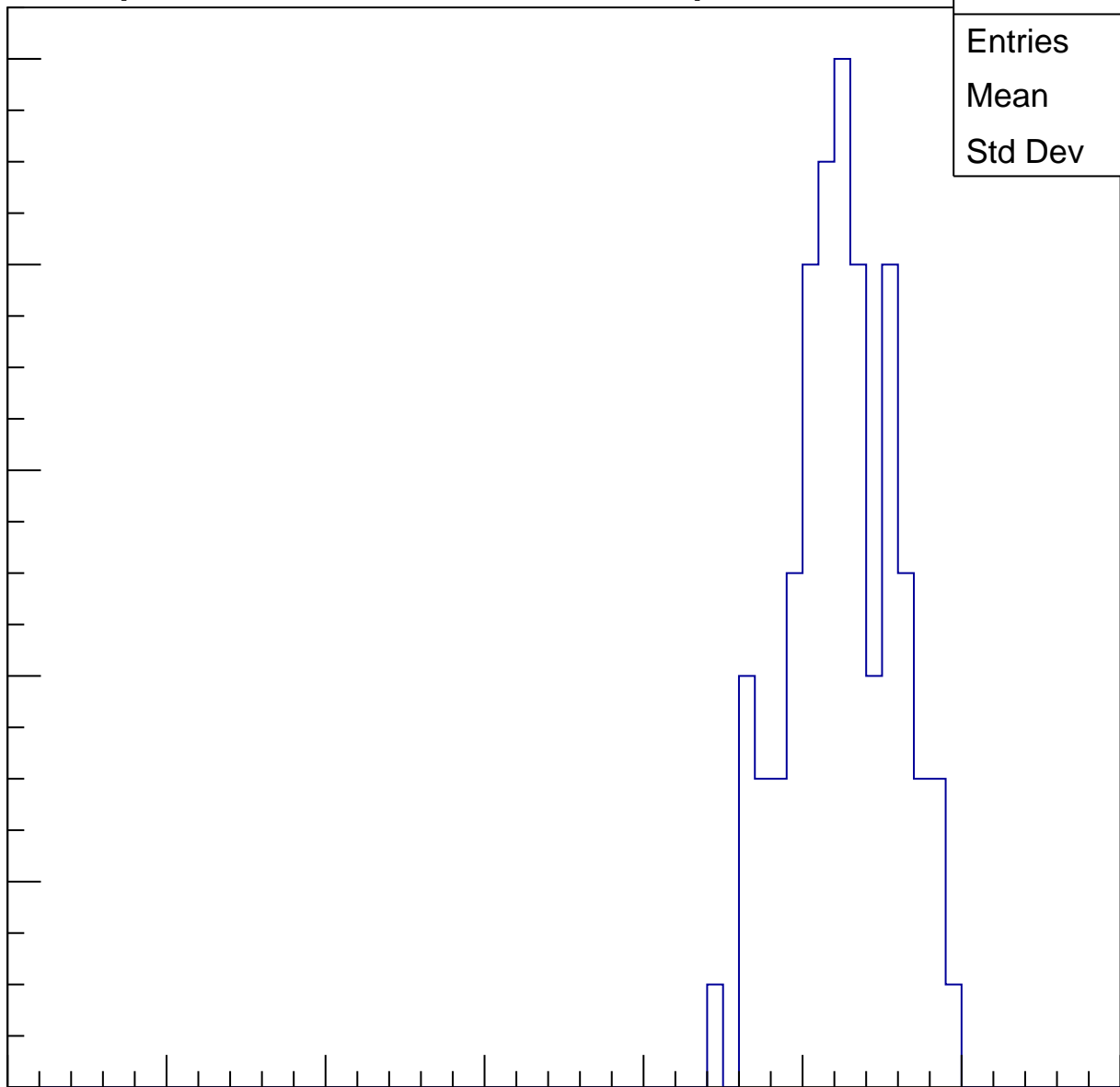
30

40

50

60

ampl

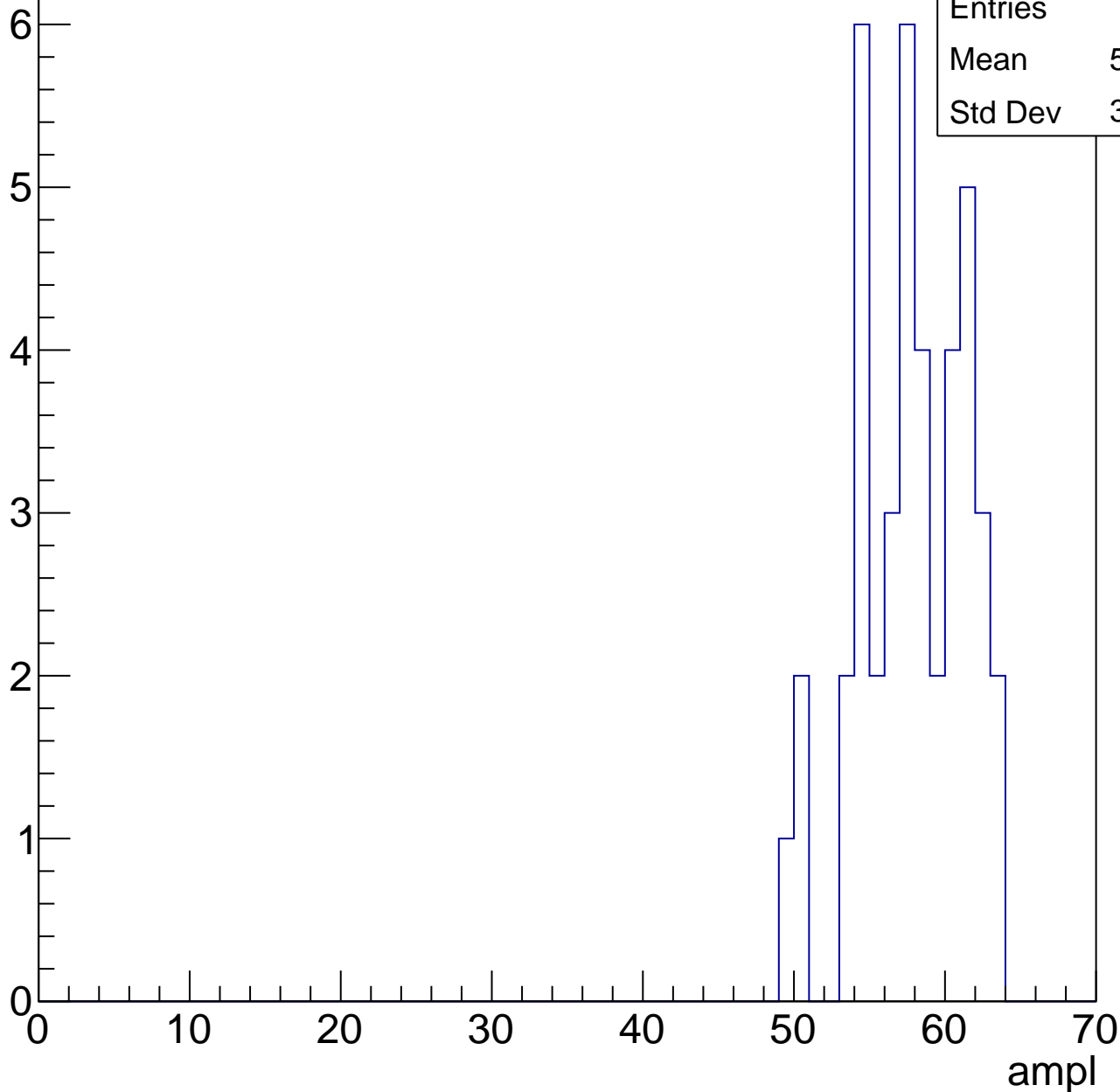


B1L102S, U9-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	57.29
Std Dev	3.554



B1L102S, U9-ch7, adc5

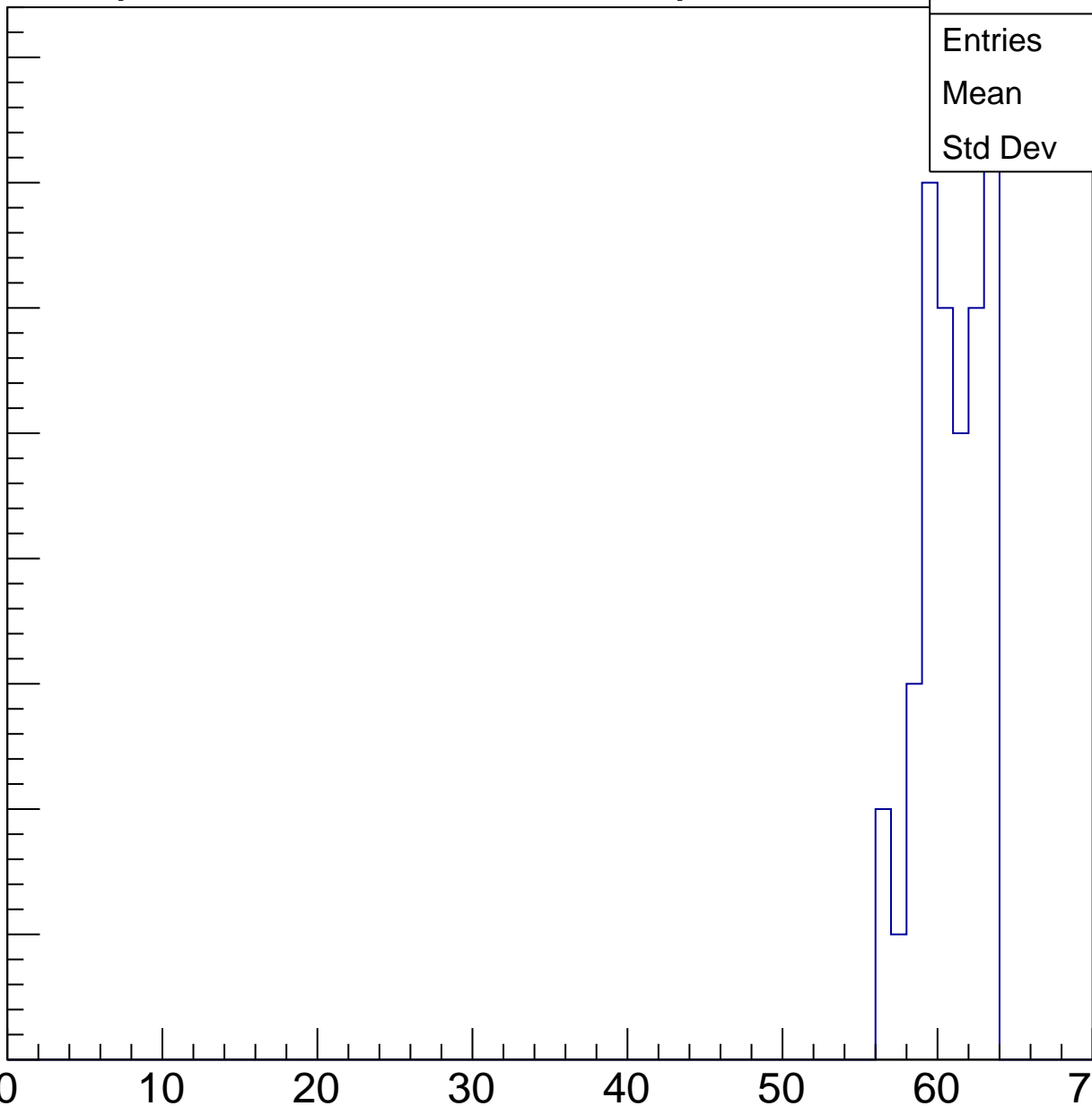
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.45
Std Dev	2.009

ampl



B1L102S, U9-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U9-ch8, adc0

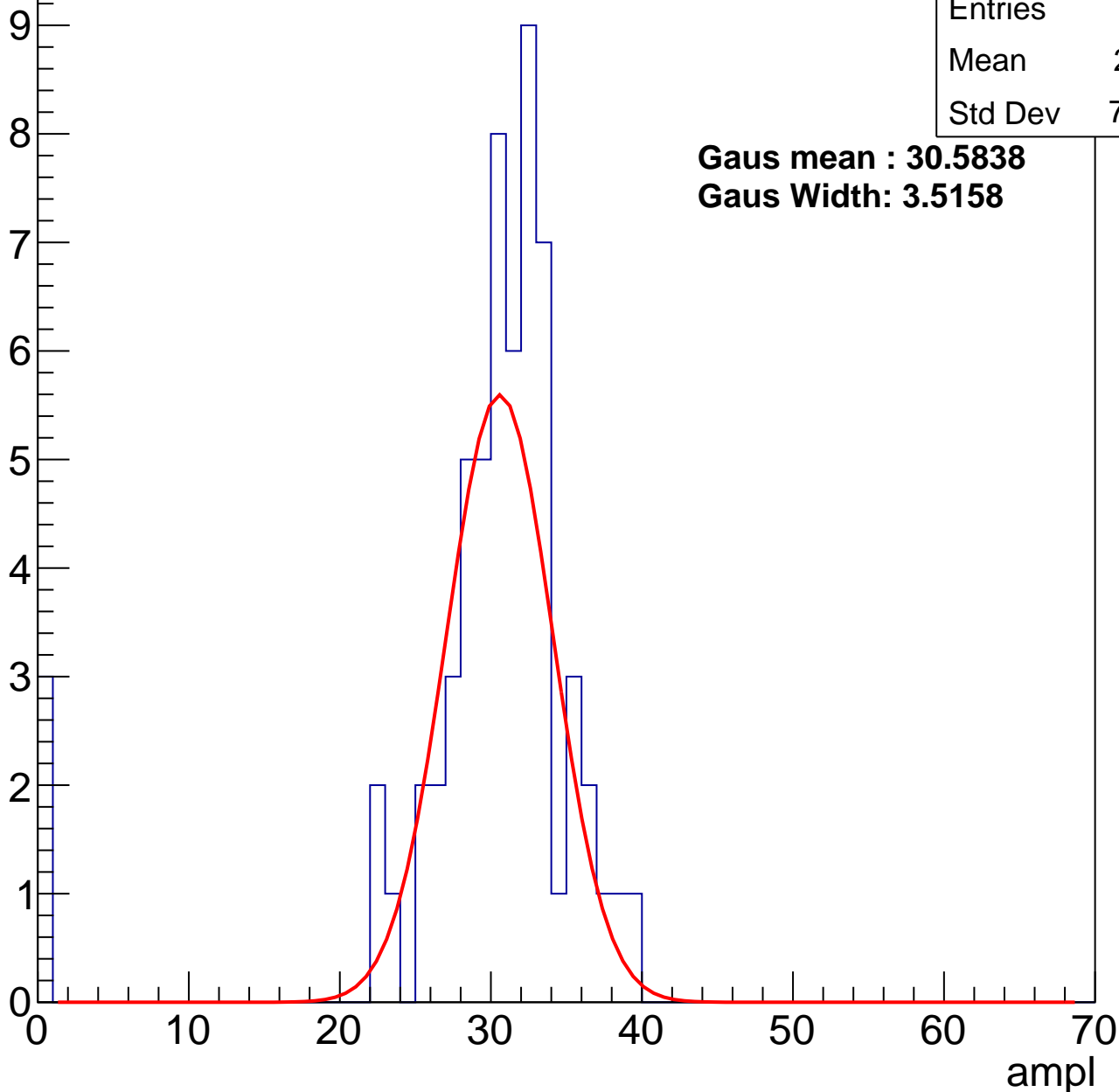
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.11
Std Dev	7.432

Gaus mean : 30.5838

Gaus Width: 3.5158



B1L102S, U9-ch8, adc1

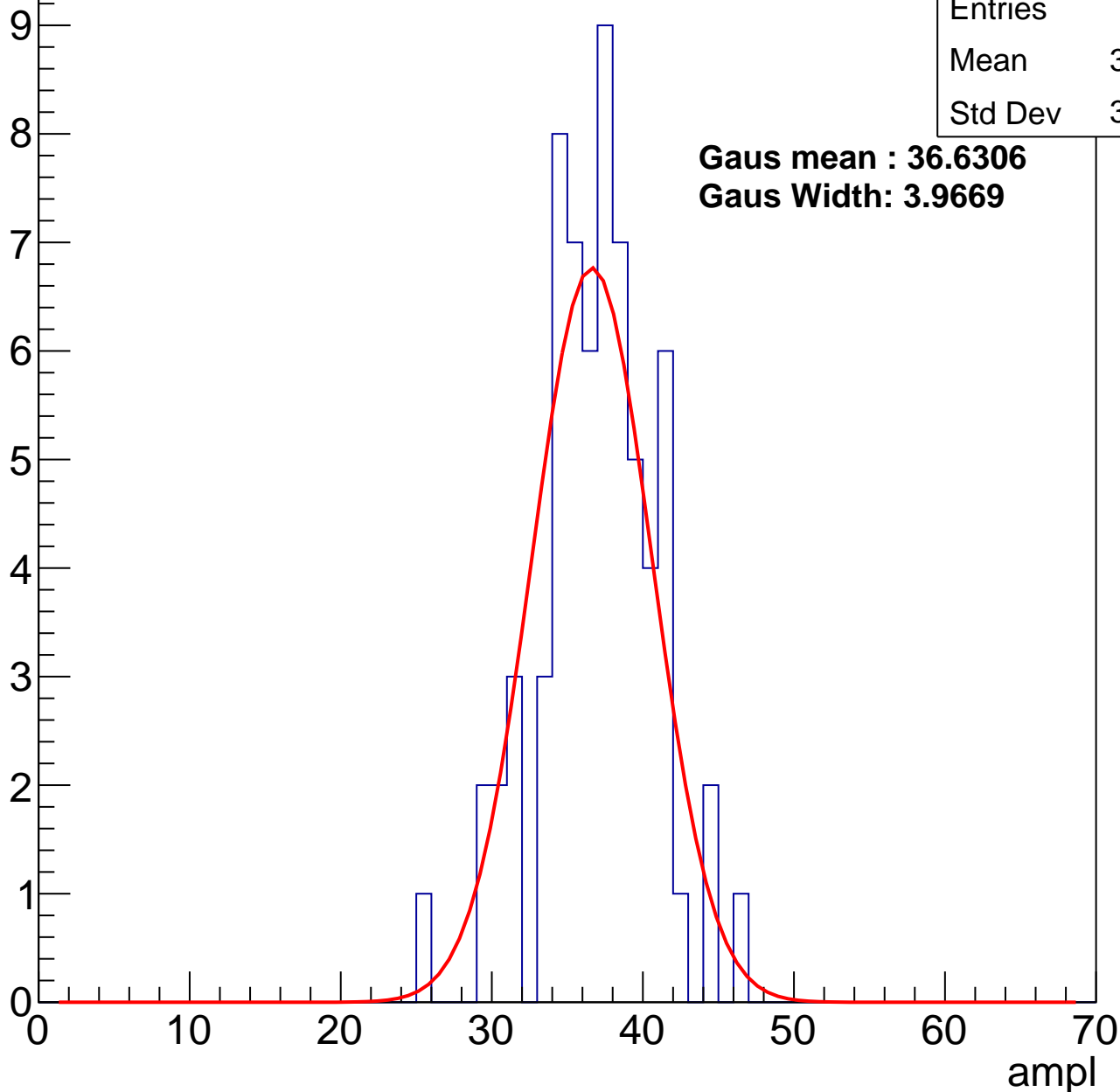
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.48
Std Dev	3.818

Gaus mean : 36.6306

Gaus Width: 3.9669



B1L102S, U9-ch8, adc2

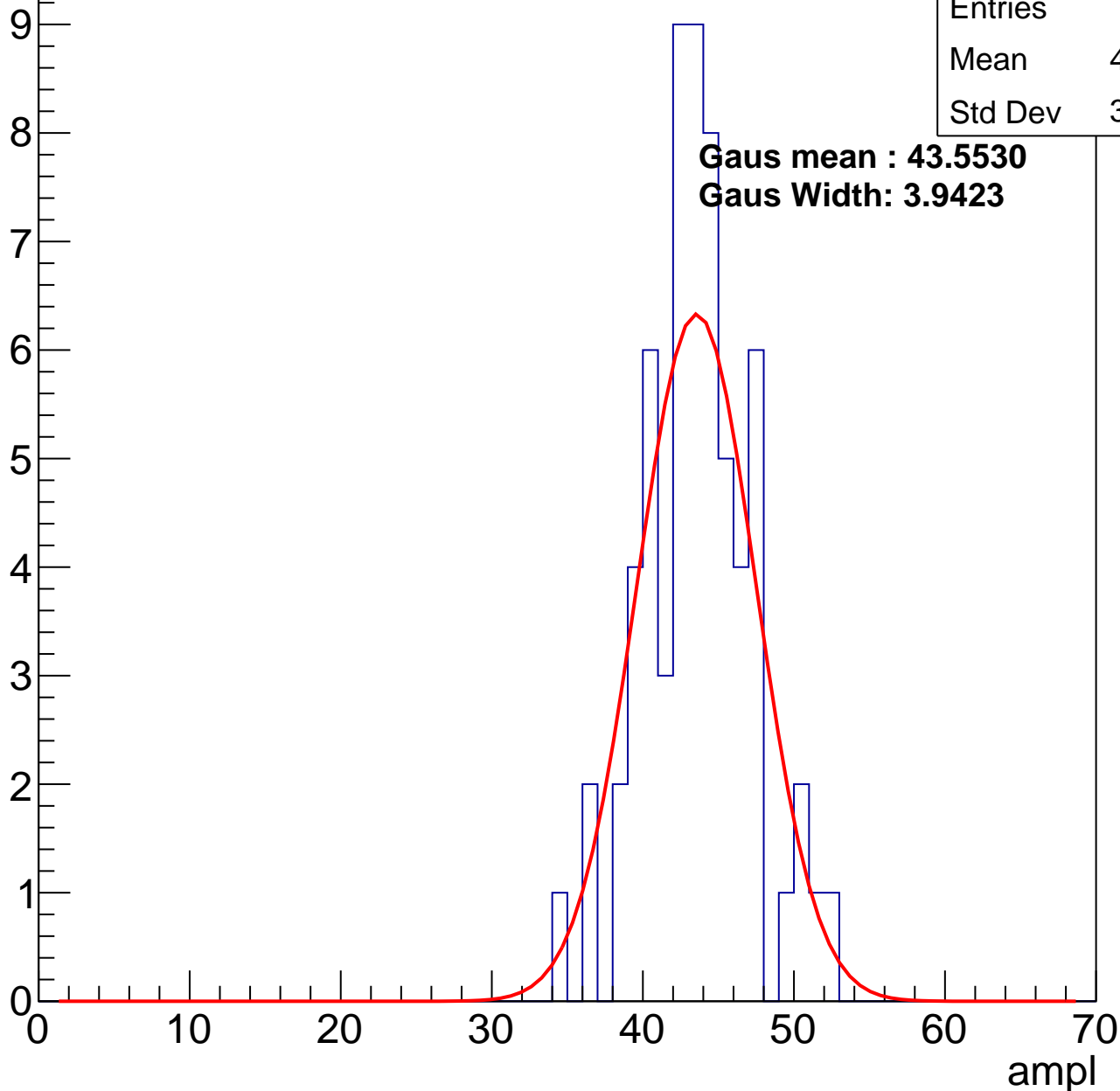
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.14
Std Dev	3.548

Gaus mean : 43.5530

Gaus Width: 3.9423

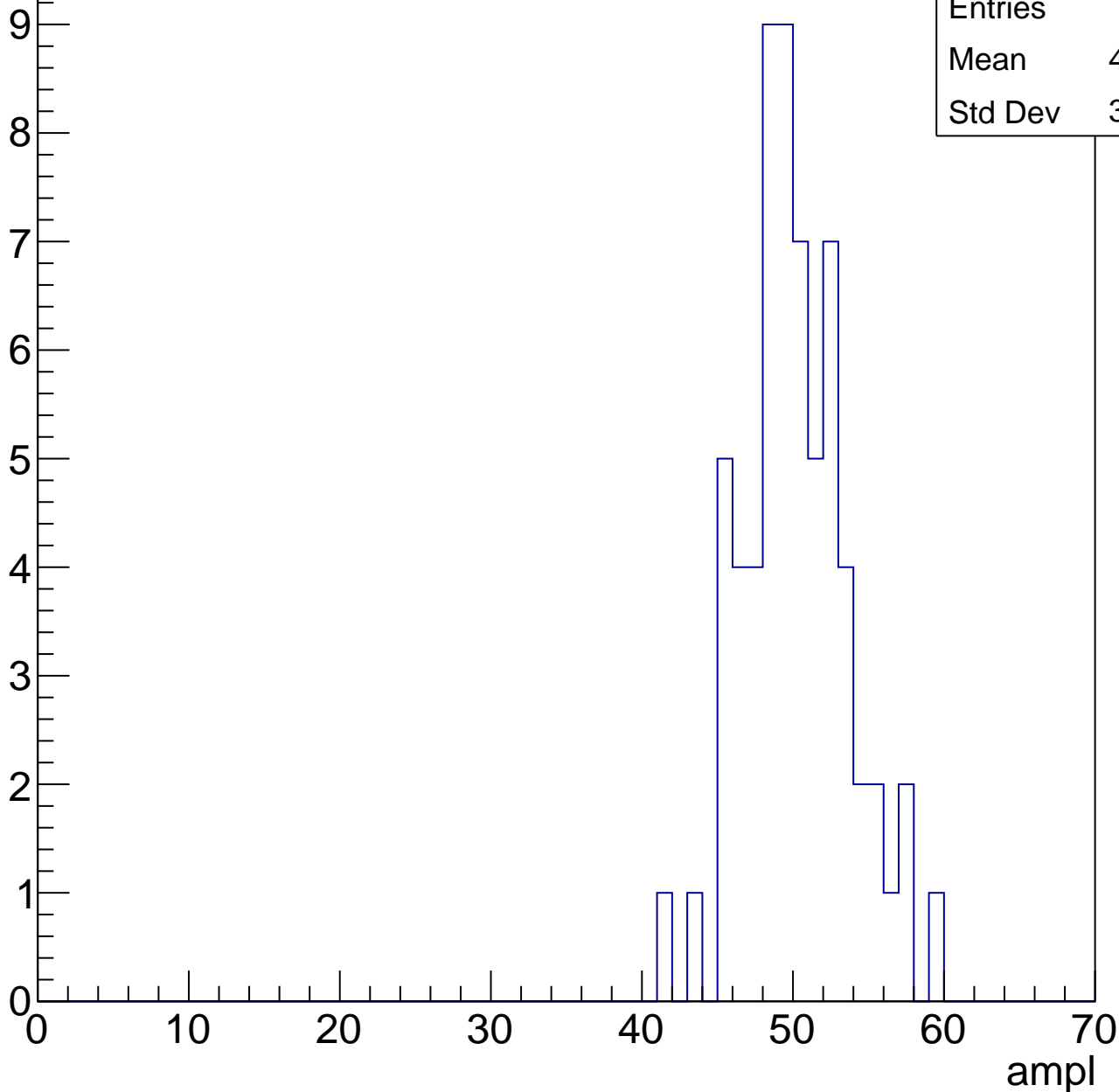


B1L102S, U9-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.72
Std Dev	3.448

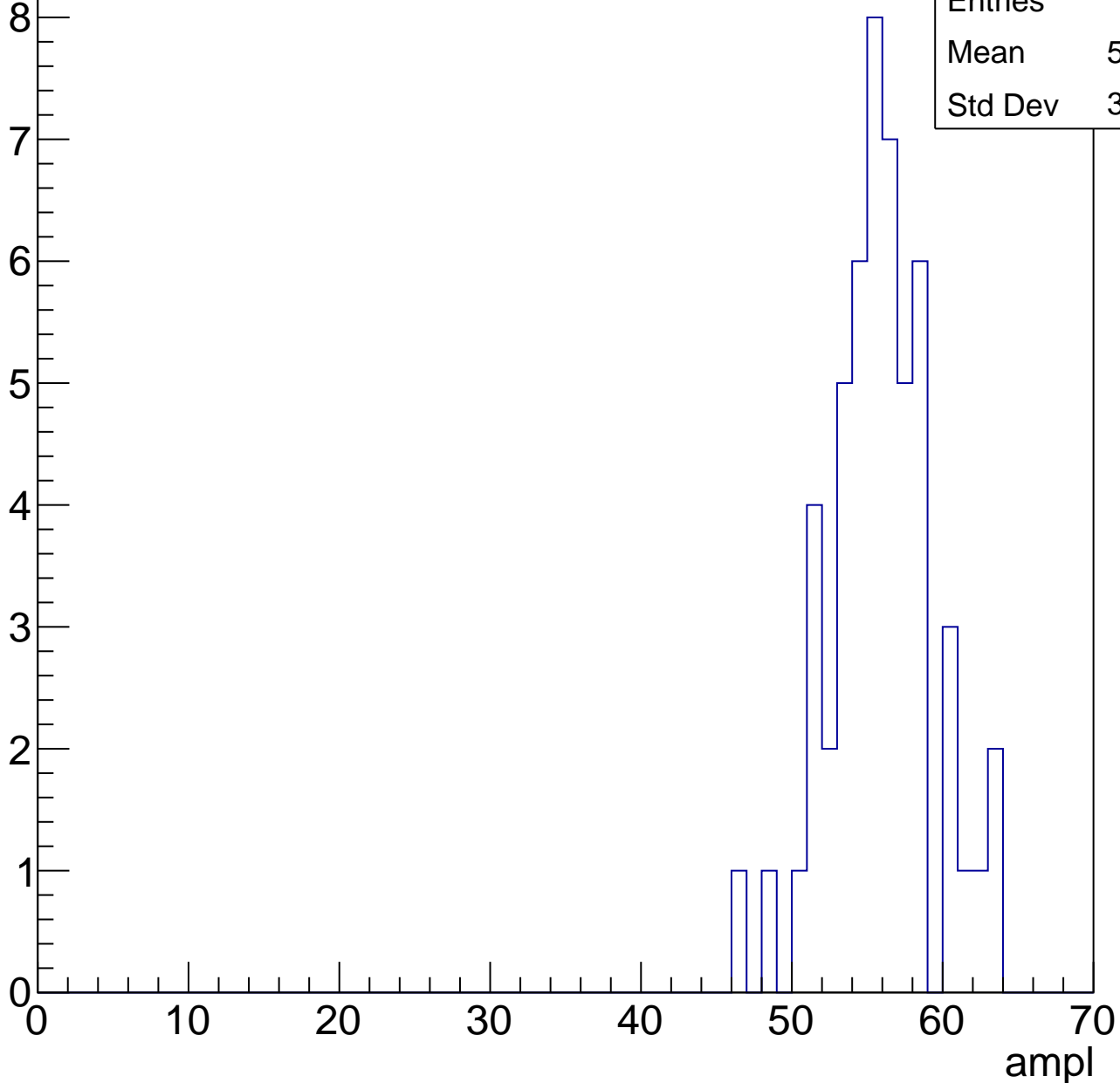


B1L102S, U9-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

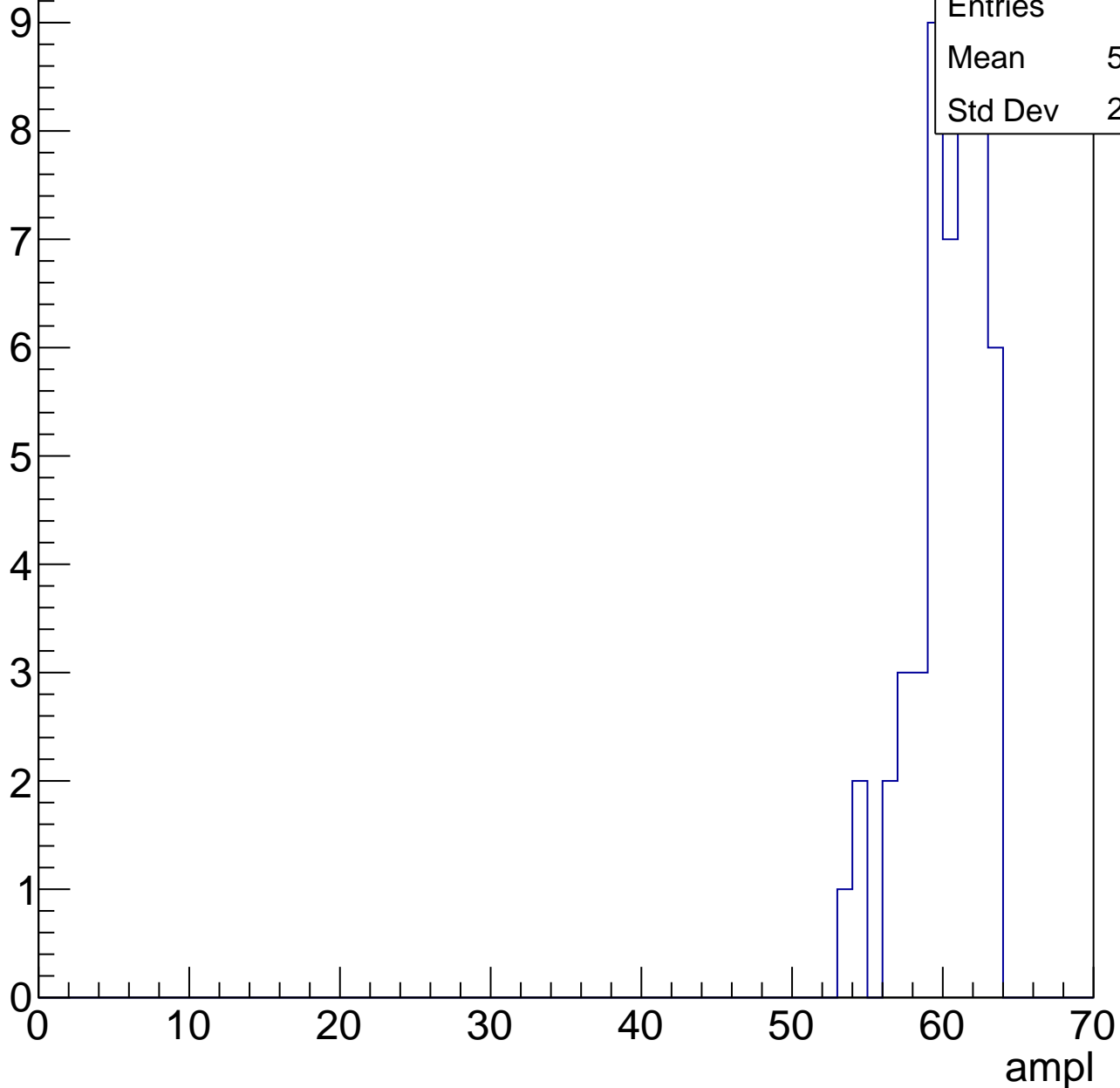
Entries	53
Mean	55.38
Std Dev	3.433



B1L102S, U9-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

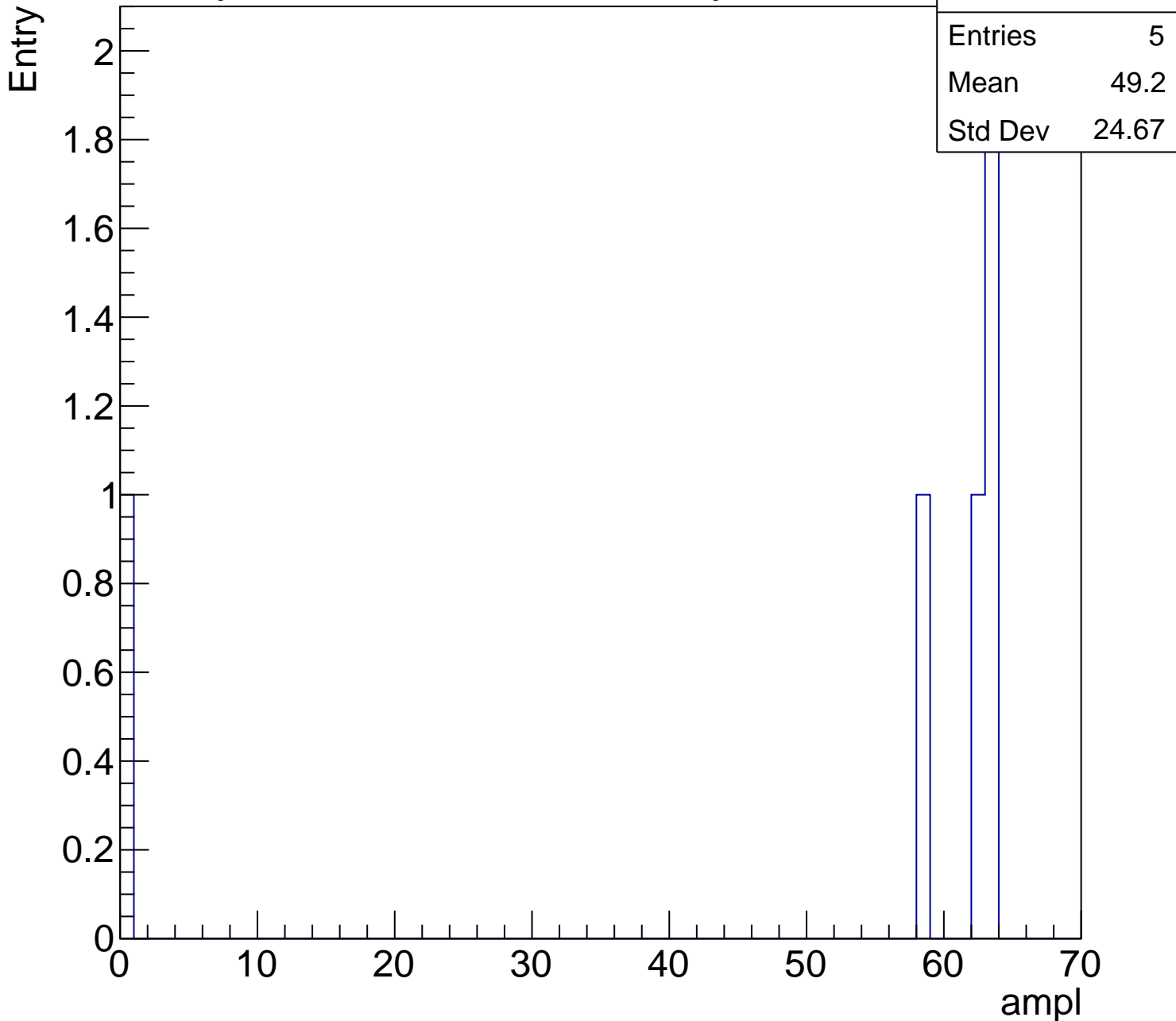
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.2
Std Dev	24.67

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch9, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	29.25
Std Dev	3.184

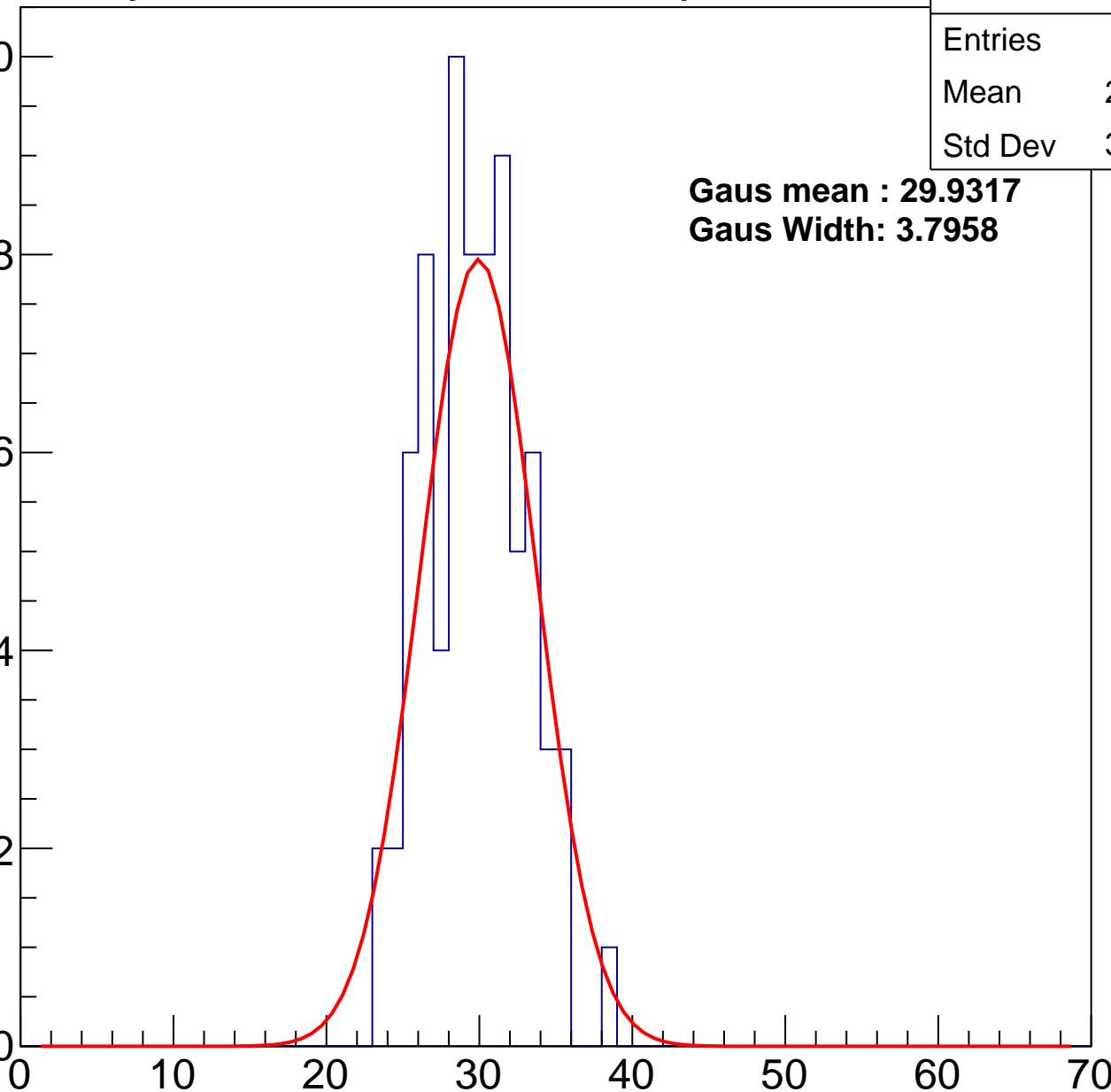
Gaus mean : 29.9317

Gaus Width: 3.7958

Entry

10
8
6
4
2
0

ampl



B1L102S, U9-ch9, adc1

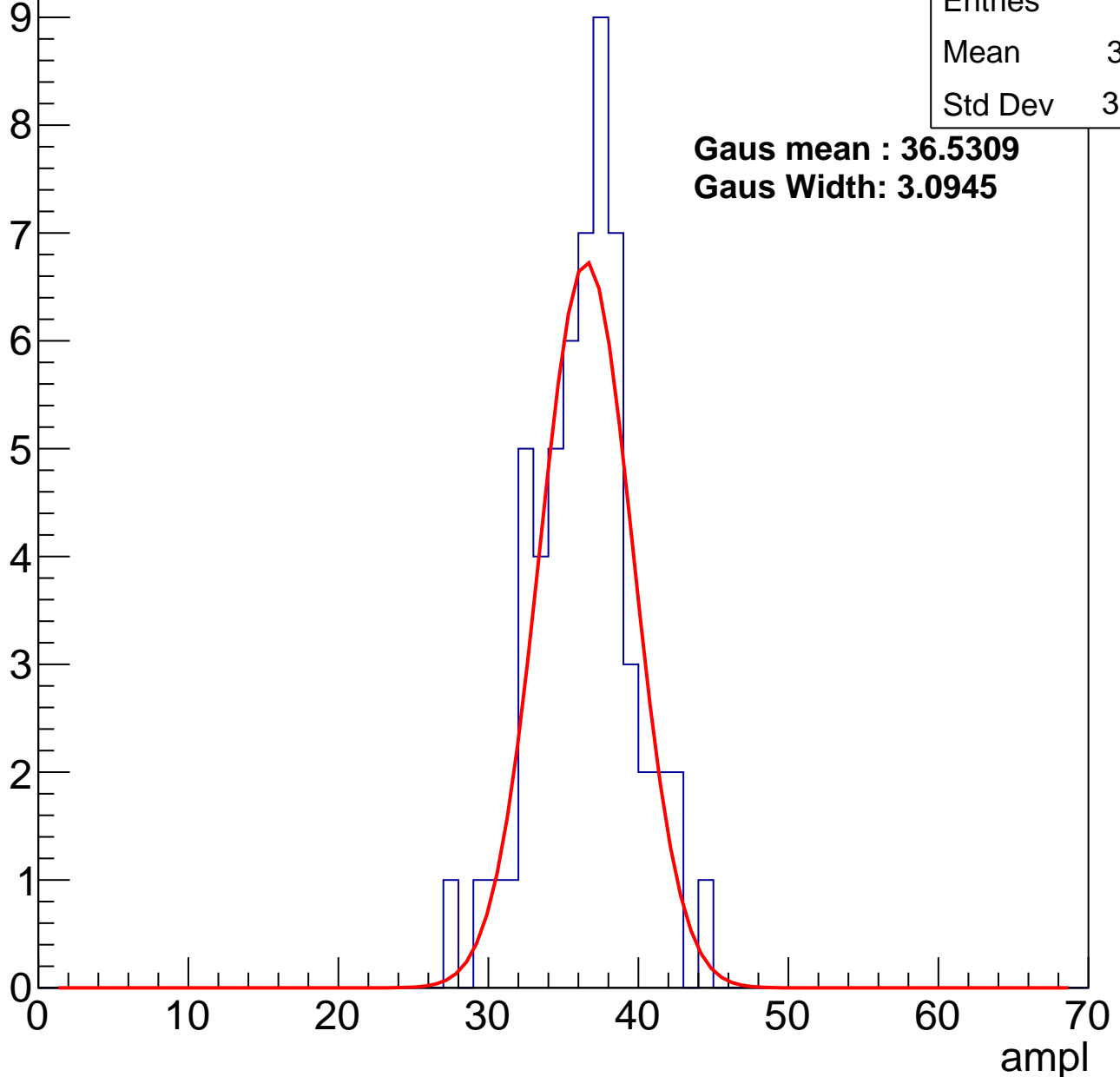
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.91
Std Dev	3.284

Gaus mean : 36.5309

Gaus Width: 3.0945



B1L102S, U9-ch9, adc2

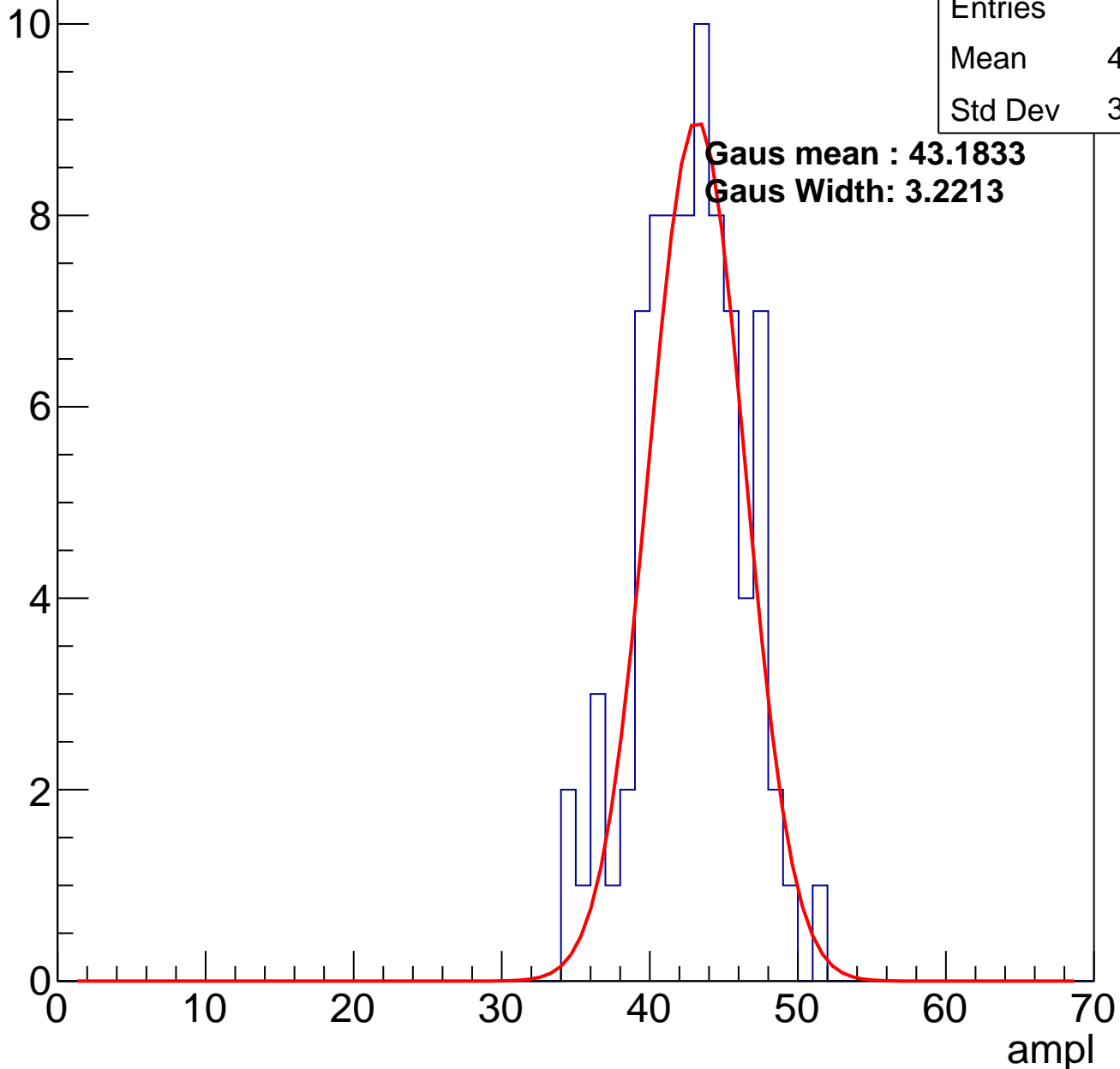
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	42.34
Std Dev	3.503

Gaus mean : 43.1833

Gaus Width: 3.2213

Entry

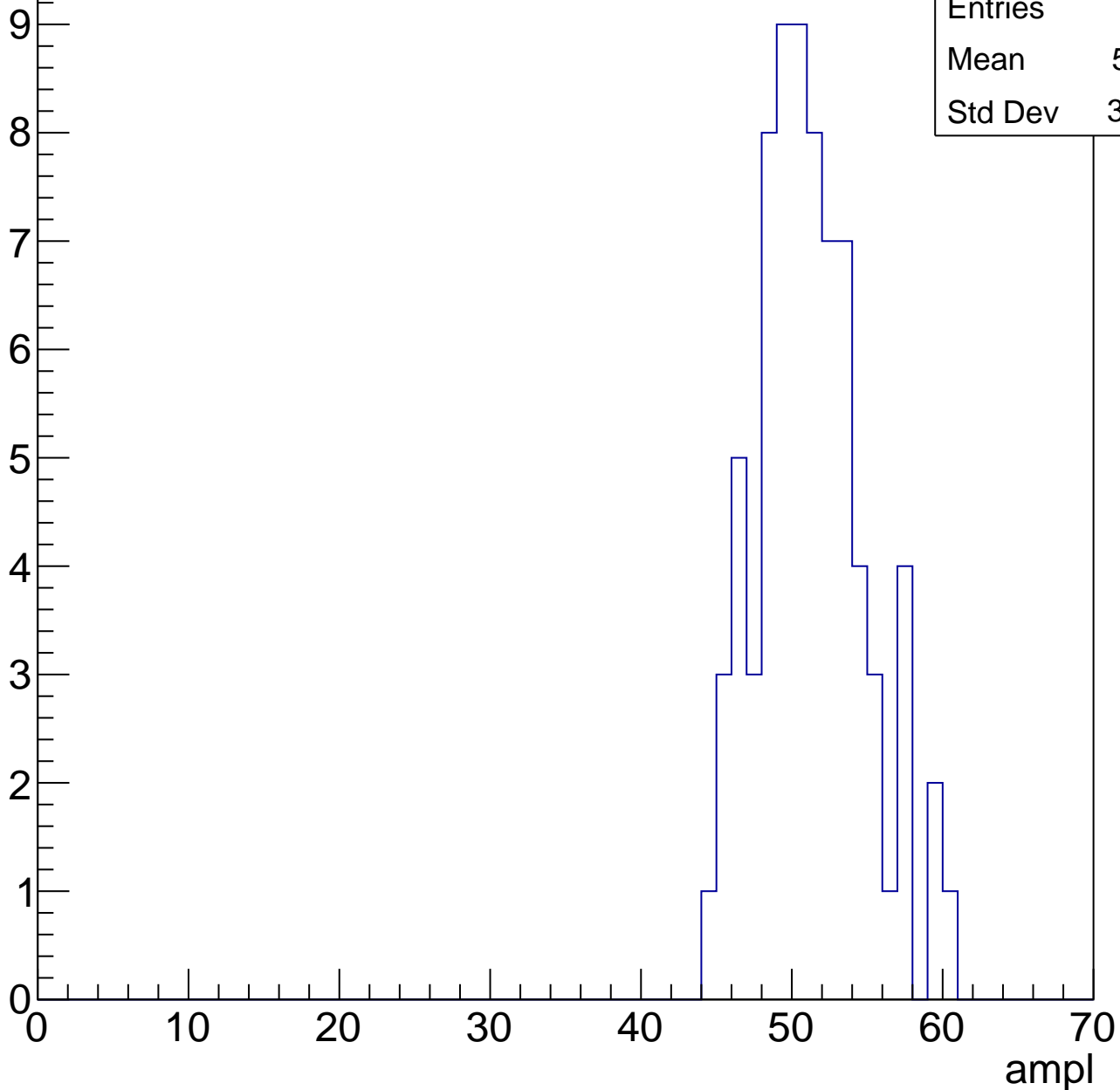


B1L102S, U9-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	50.81
Std Dev	3.543



B1L102S, U9-ch9, adc4

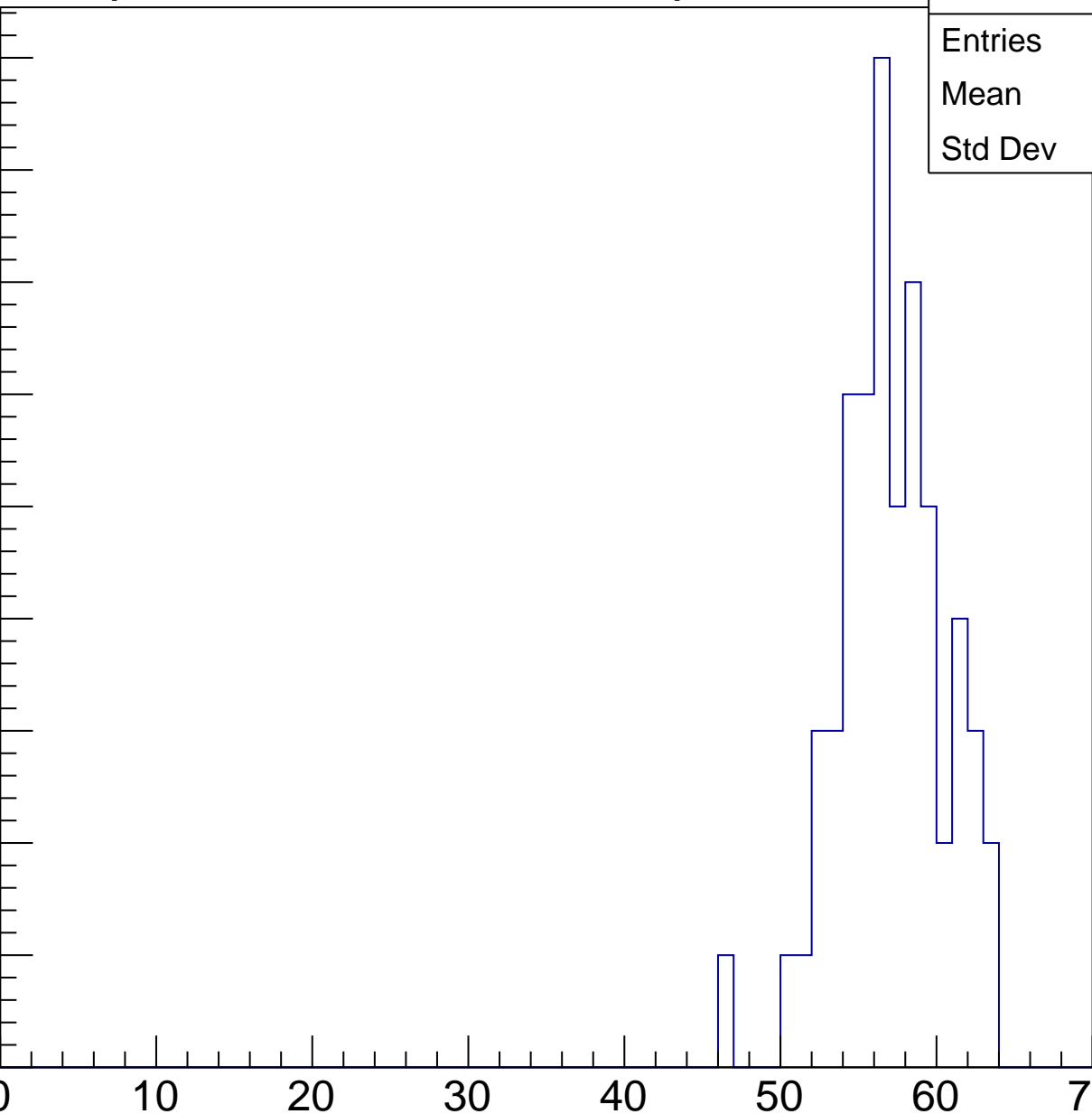
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	56.59
Std Dev	3.379

ampl

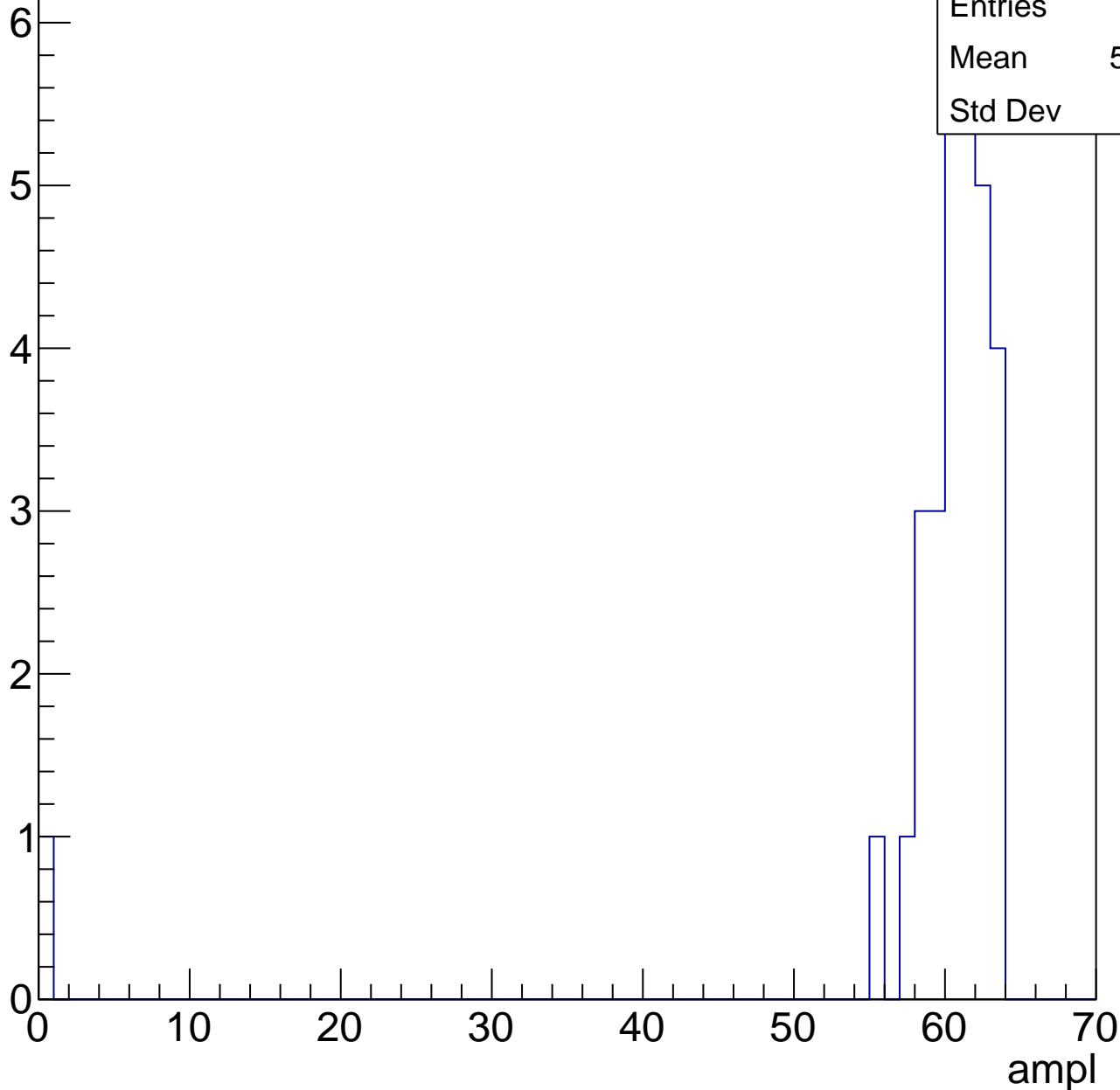


B1L102S, U9-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	58.37
Std Dev	11



B1L102S, U9-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch10, adc0

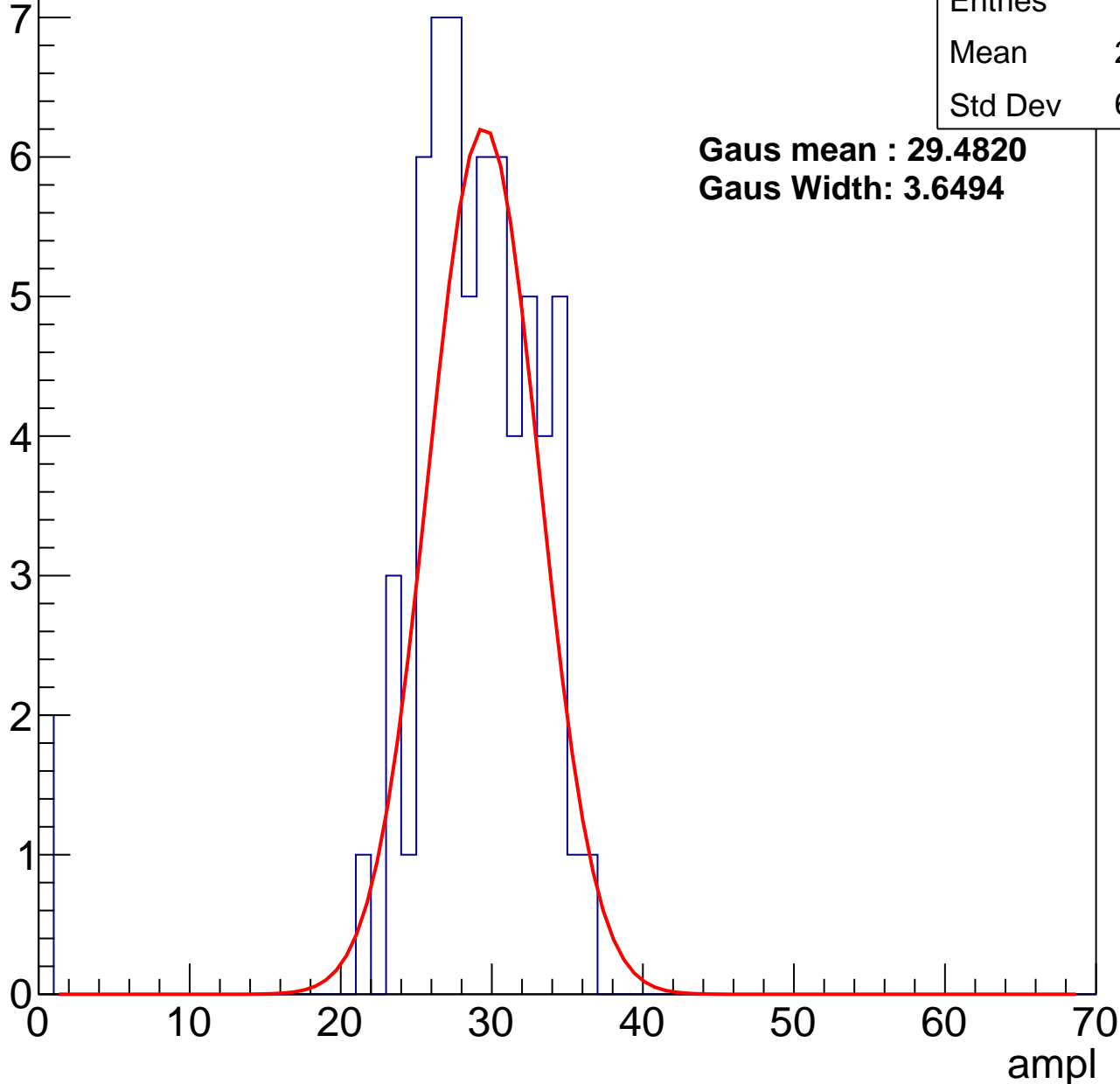
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.91
Std Dev	6.041

Gaus mean : 29.4820

Gaus Width: 3.6494



B1L102S, U9-ch10, adc1

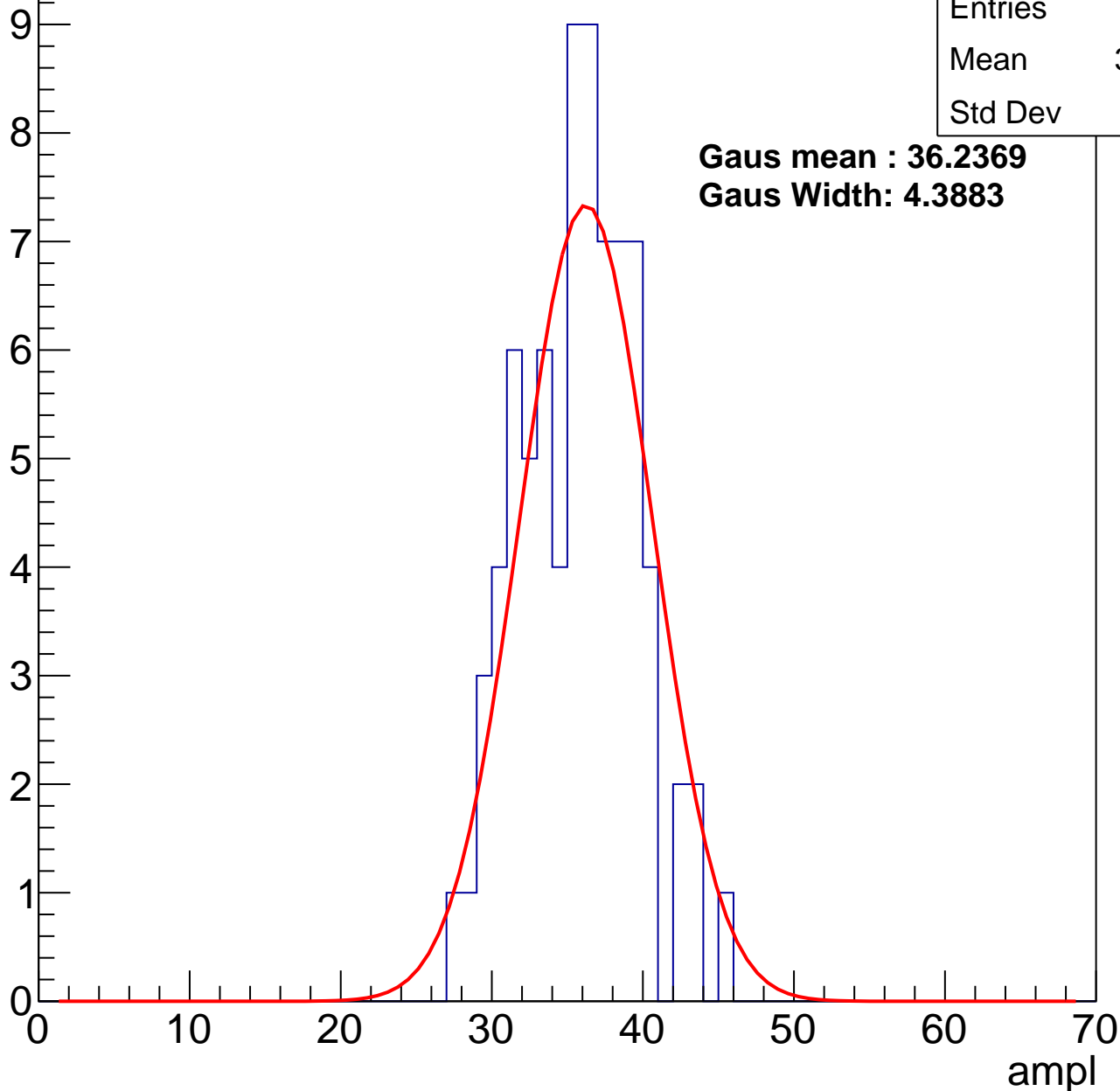
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.31
Std Dev	3.79

Gaus mean : 36.2369

Gaus Width: 4.3883



B1L102S, U9-ch10, adc2

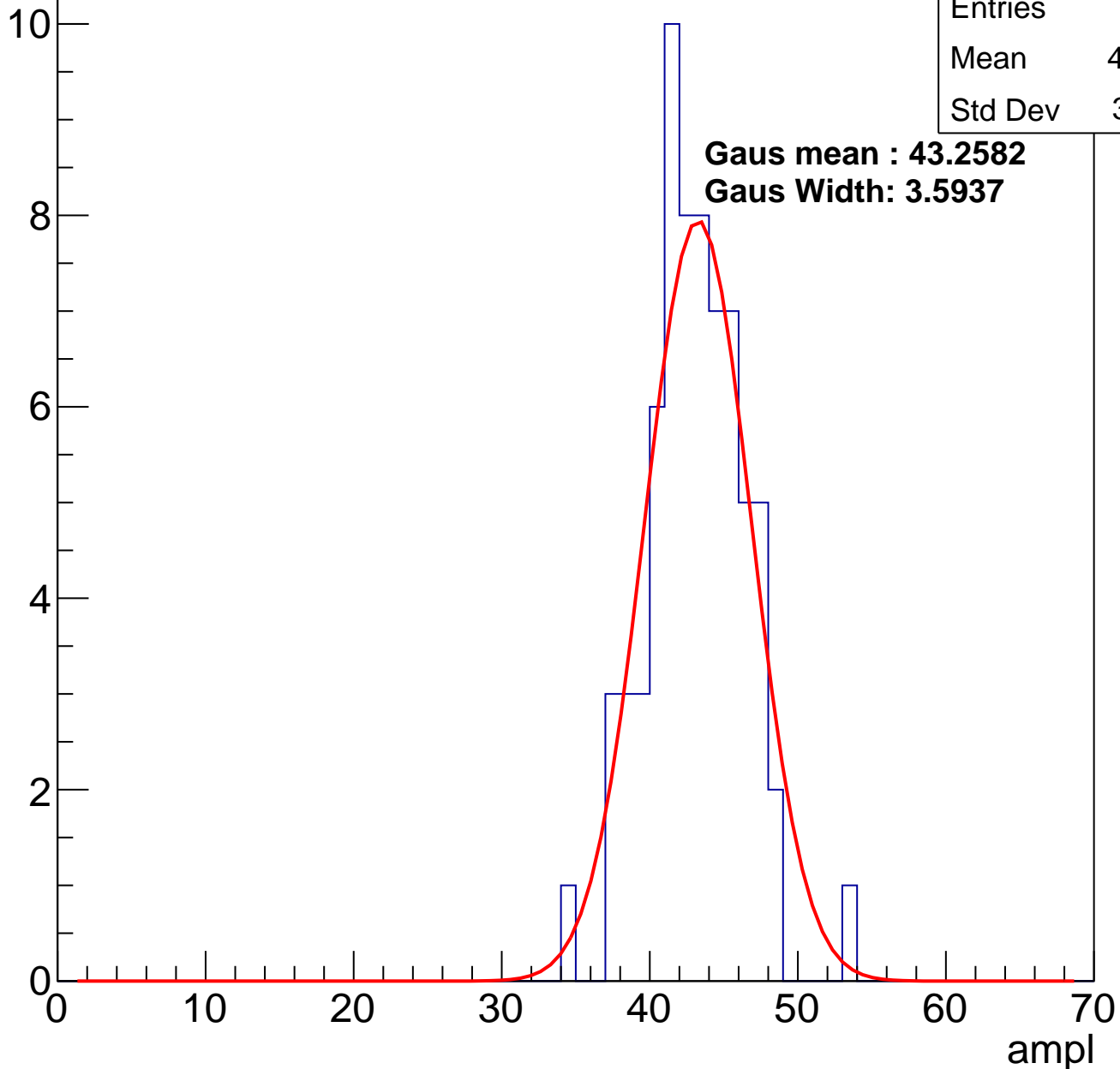
calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	42.65
Std Dev	3.221

Gaus mean : 43.2582

Gaus Width: 3.5937

Entry

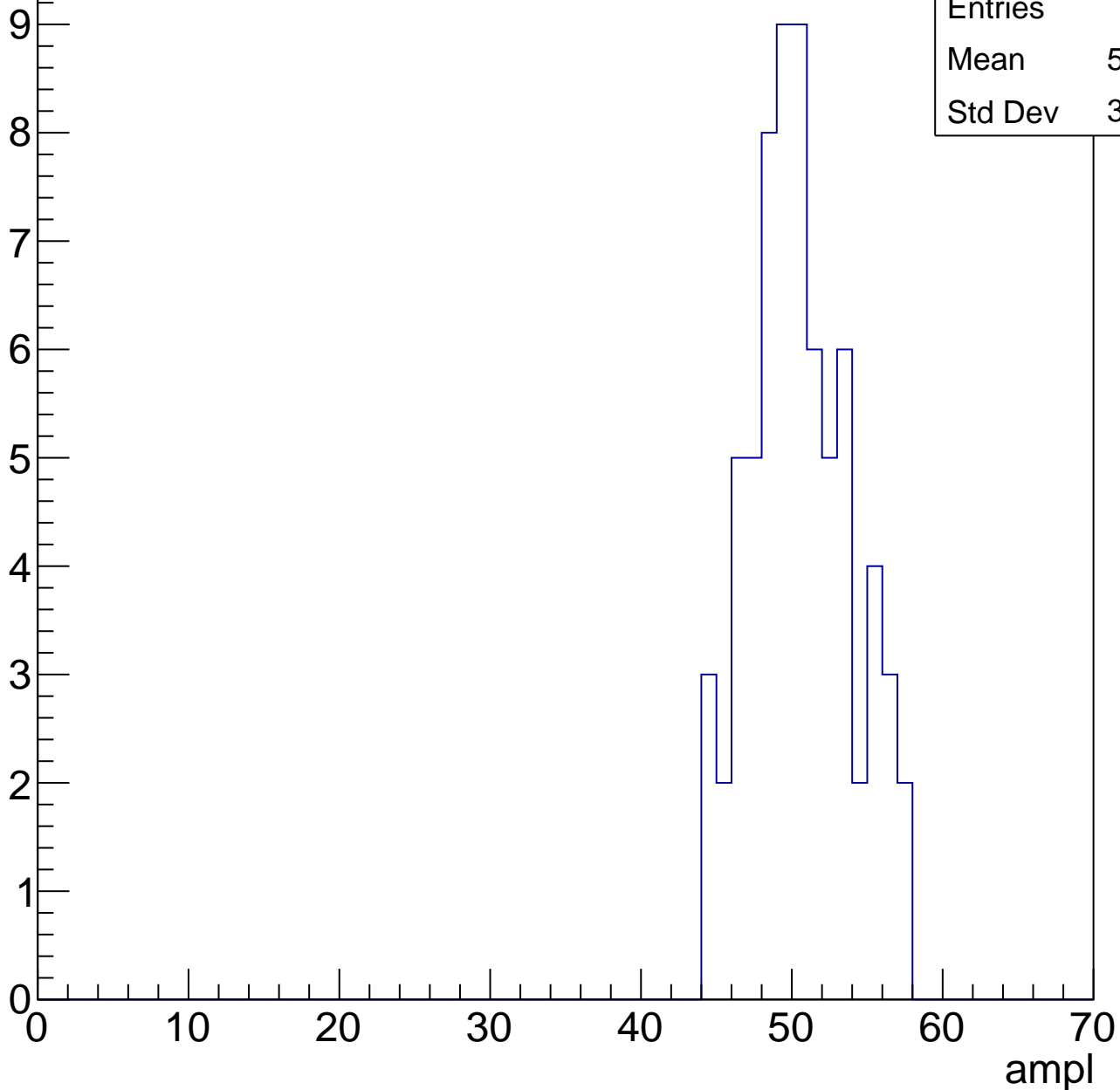


B1L102S, U9-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

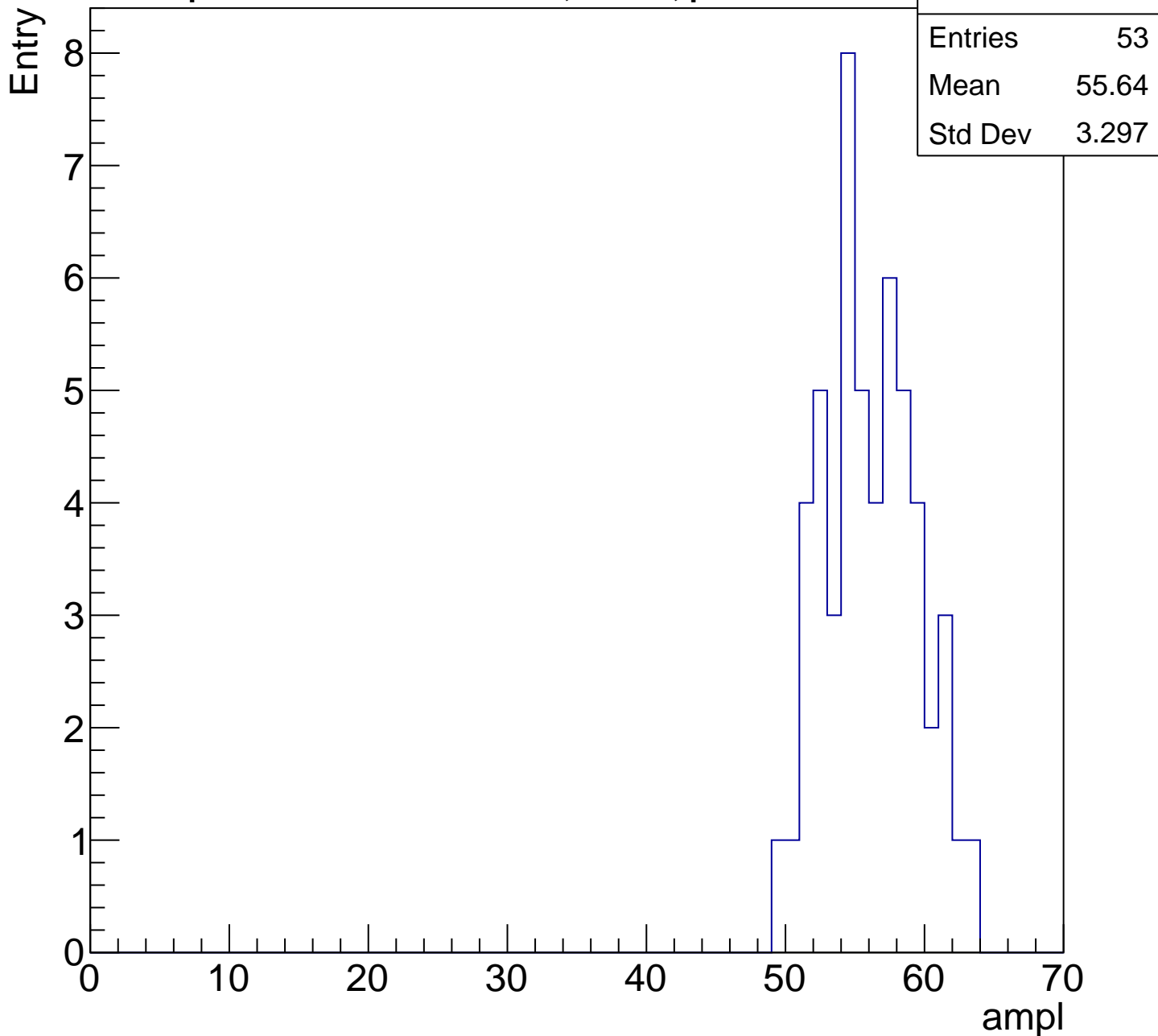
Entry

Entries	69
Mean	50.09
Std Dev	3.278



B1L102S, U9-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U9-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	59.95
Std Dev	2.203

ampl

0

10

20

30

40

50

60

70

B1L102S, U9-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

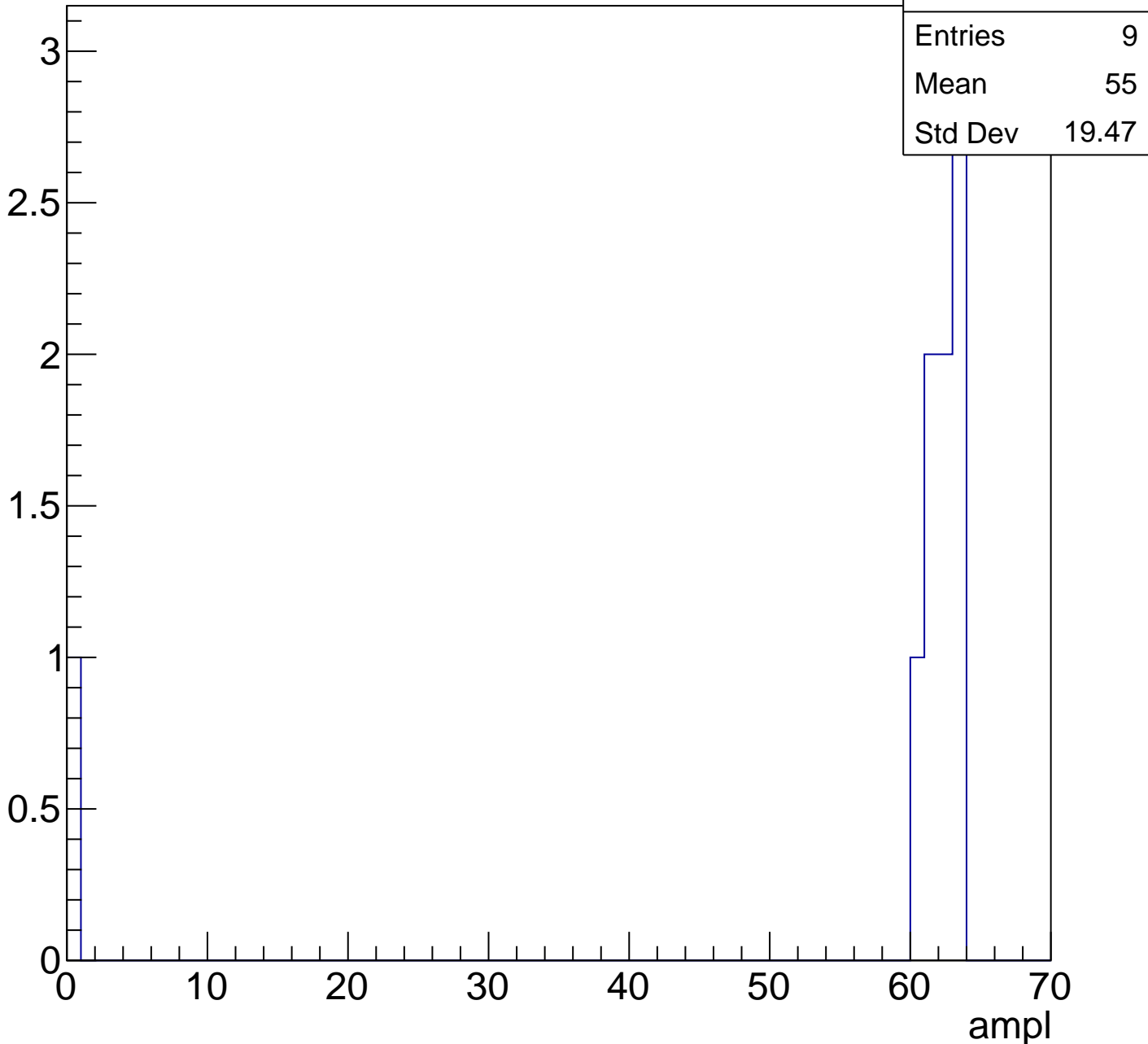
1

0.5

0

ampl

Entries	9
Mean	55
Std Dev	19.47



B1L102S, U9-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch11, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	28.52
Std Dev	4.741

Gaus mean : 29.3988

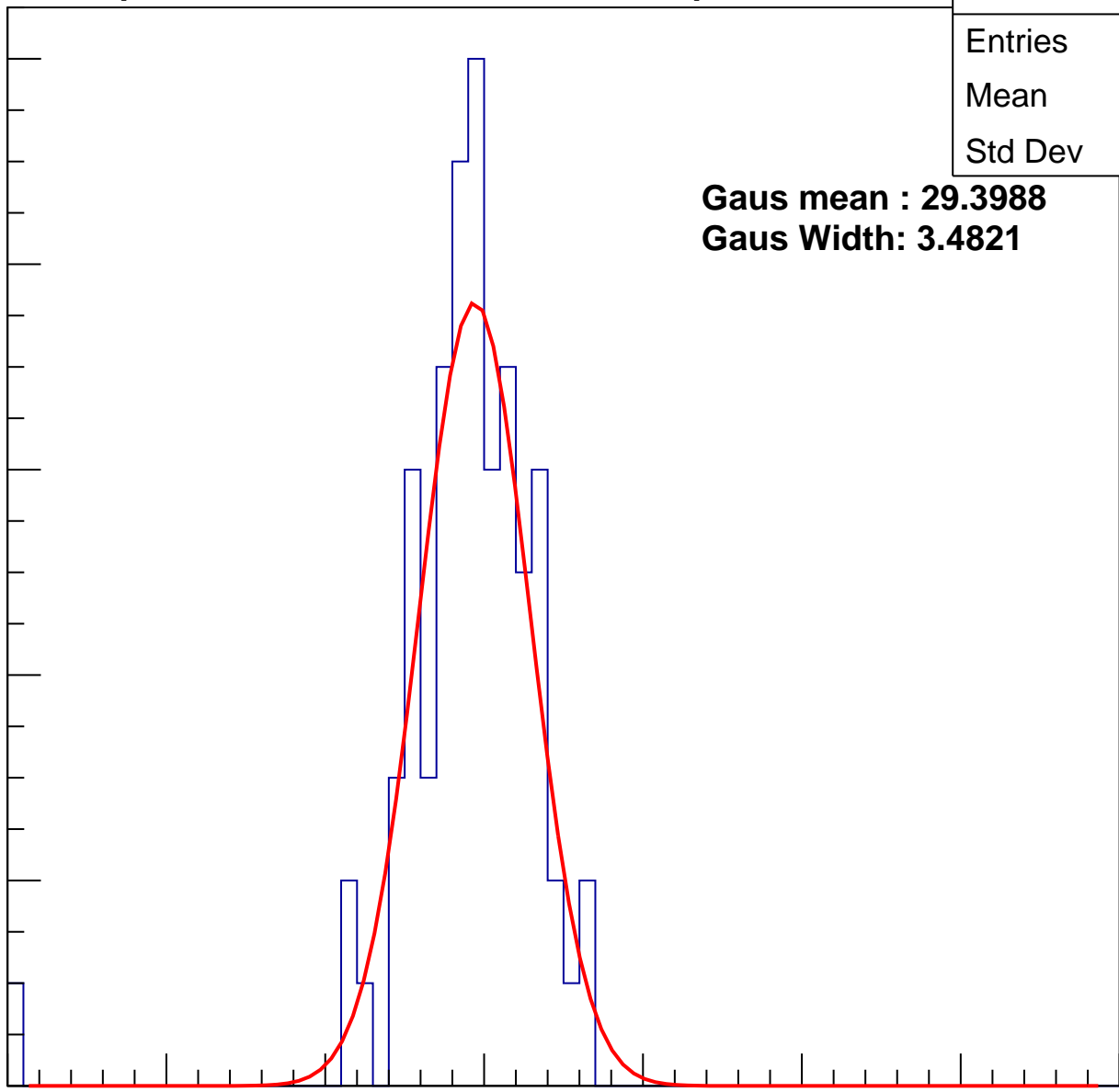
Gaus Width: 3.4821

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch11, adc1

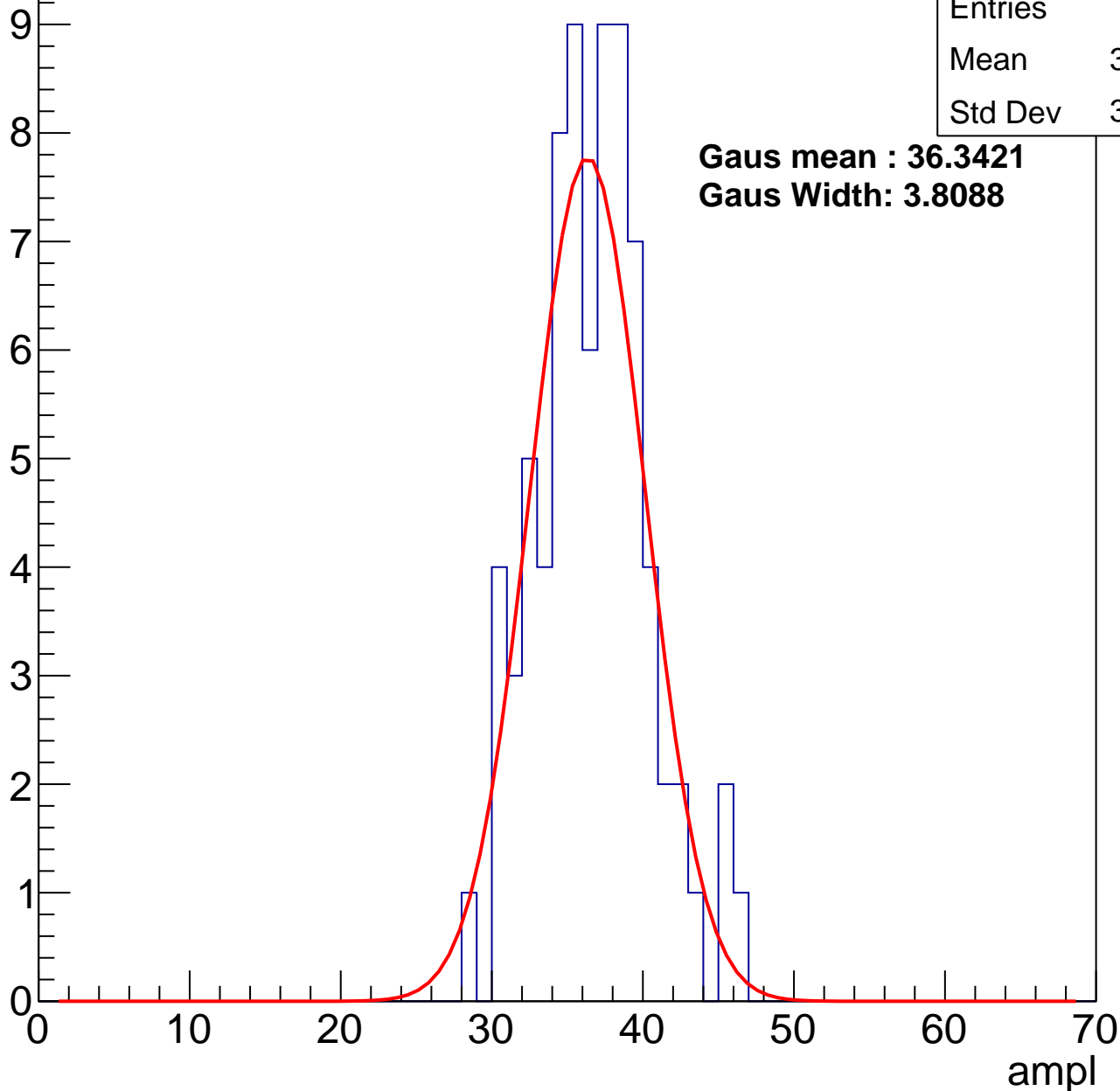
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.22
Std Dev	3.677

Gaus mean : 36.3421

Gaus Width: 3.8088



B1L102S, U9-ch11, adc2

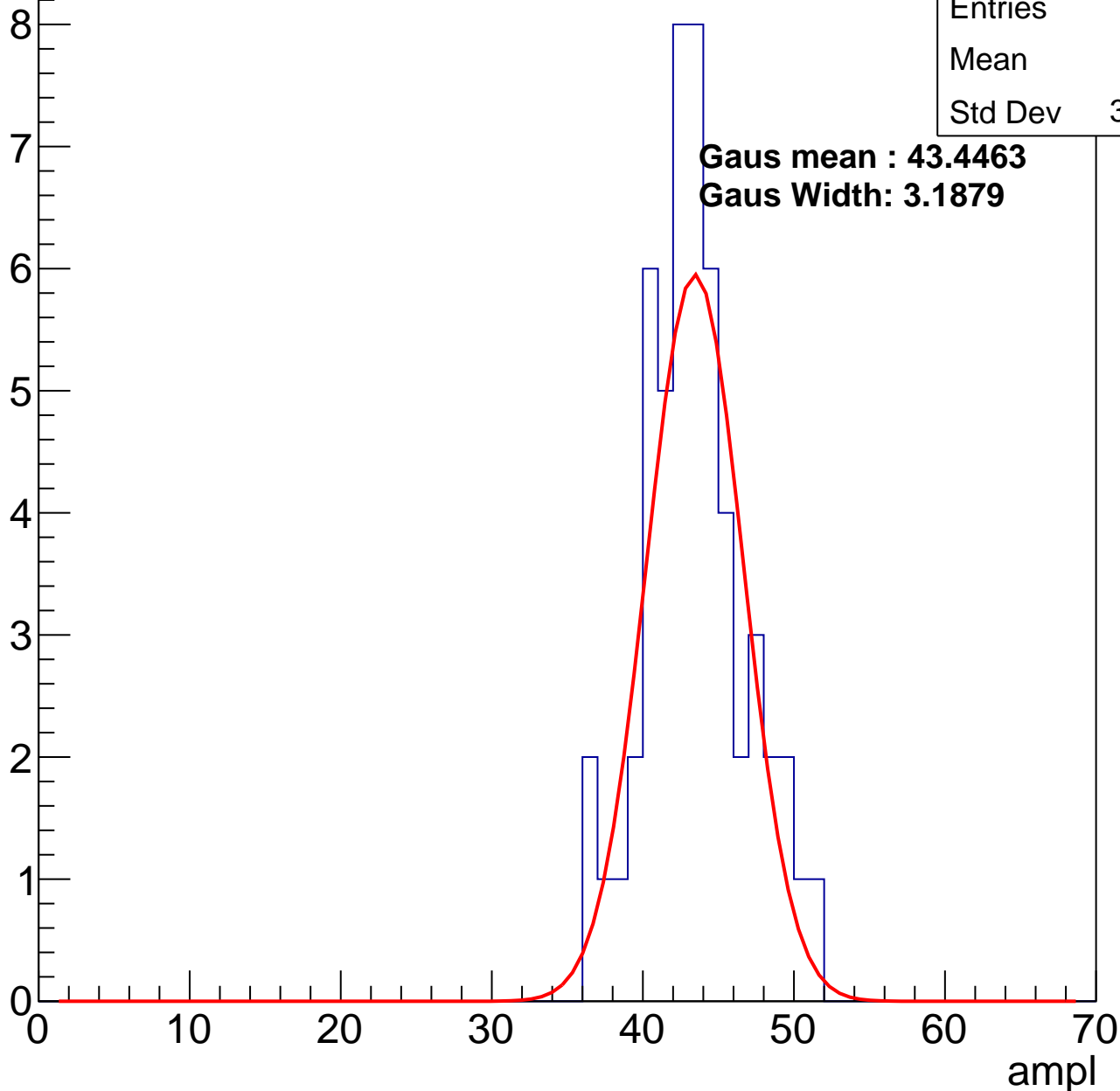
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	43
Std Dev	3.322

Gaus mean : 43.4463

Gaus Width: 3.1879

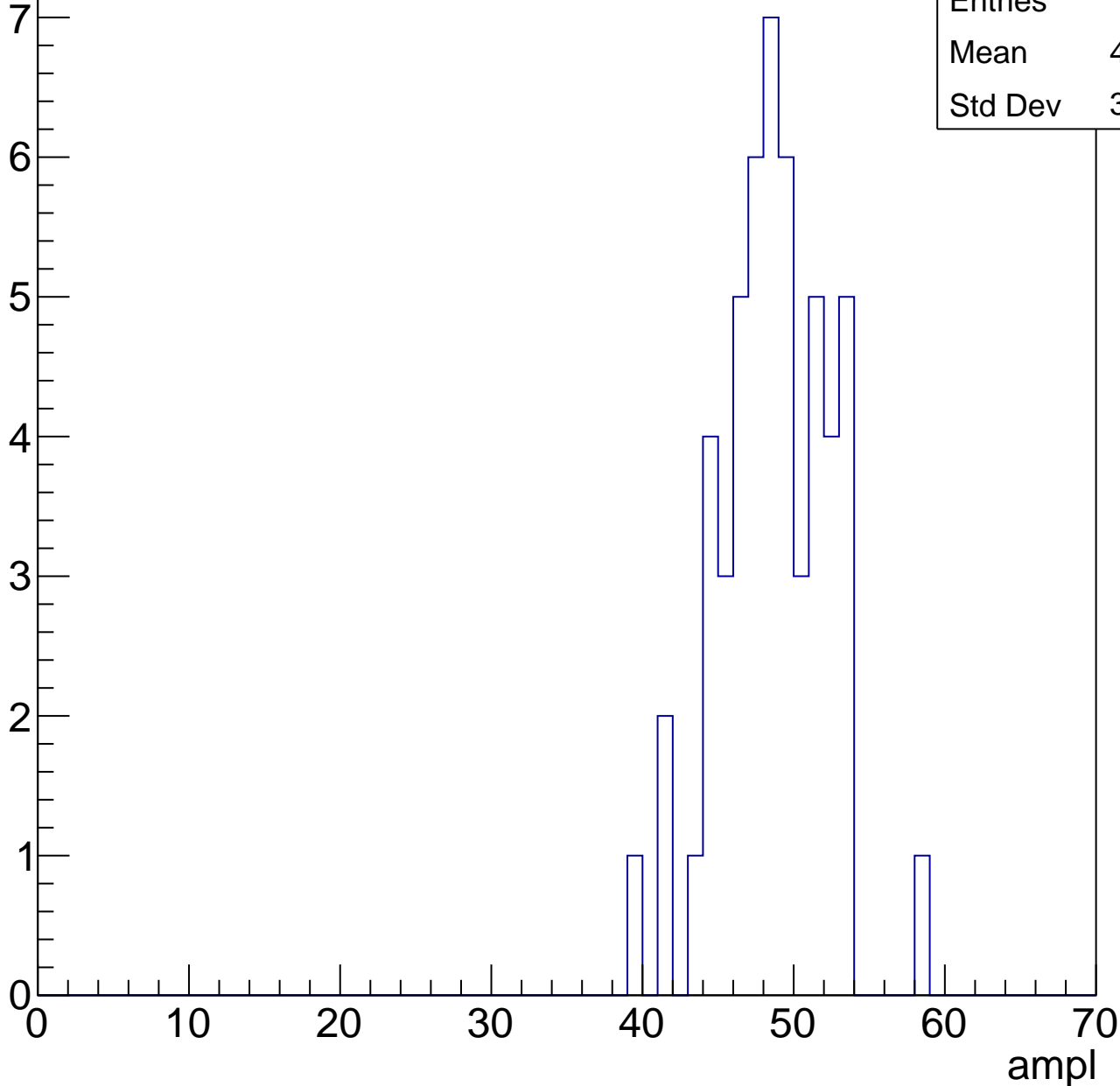


B1L102S, U9-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.17
Std Dev	3.559

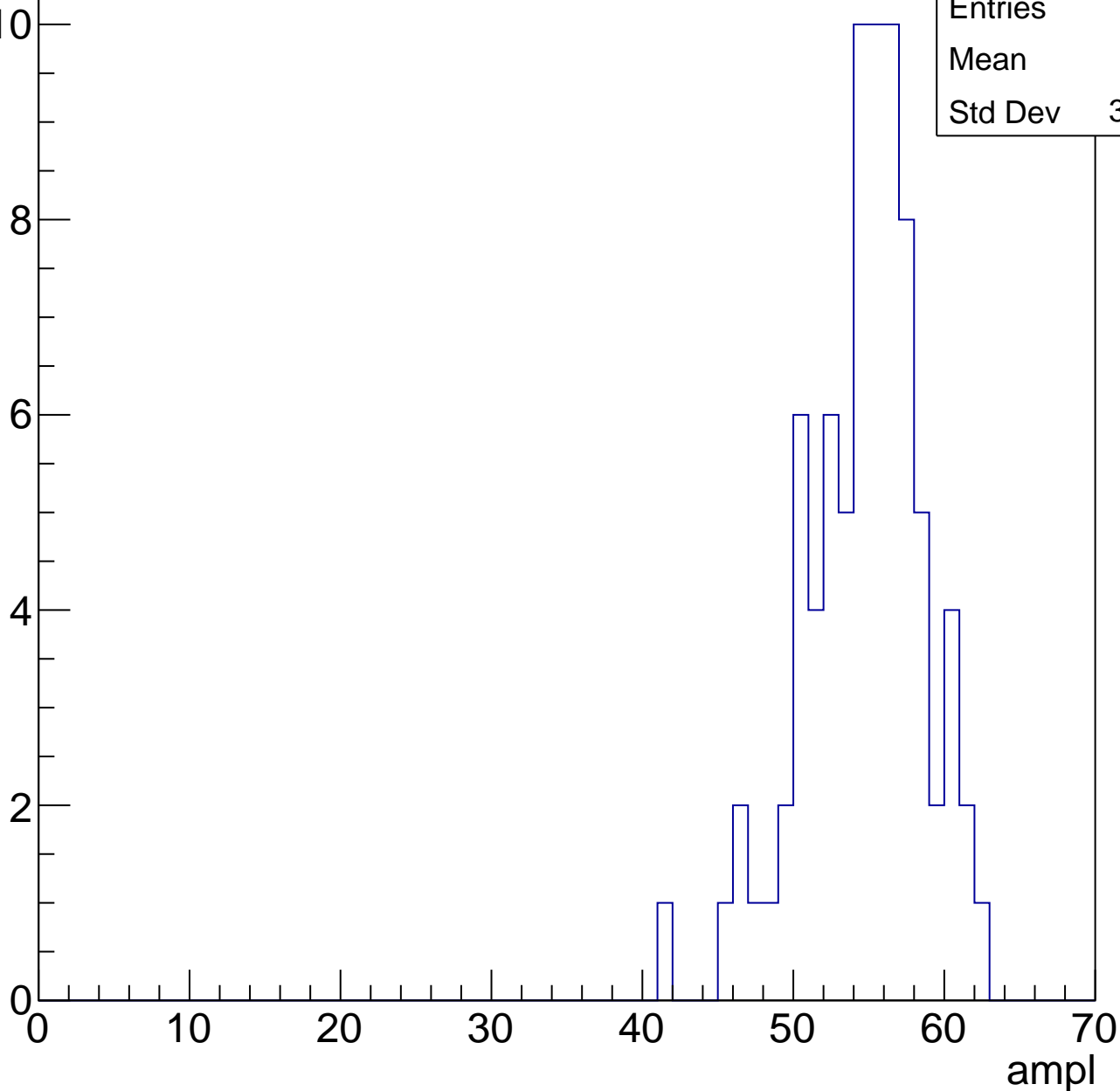


B1L102S, U9-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	54.2
Std Dev	3.895

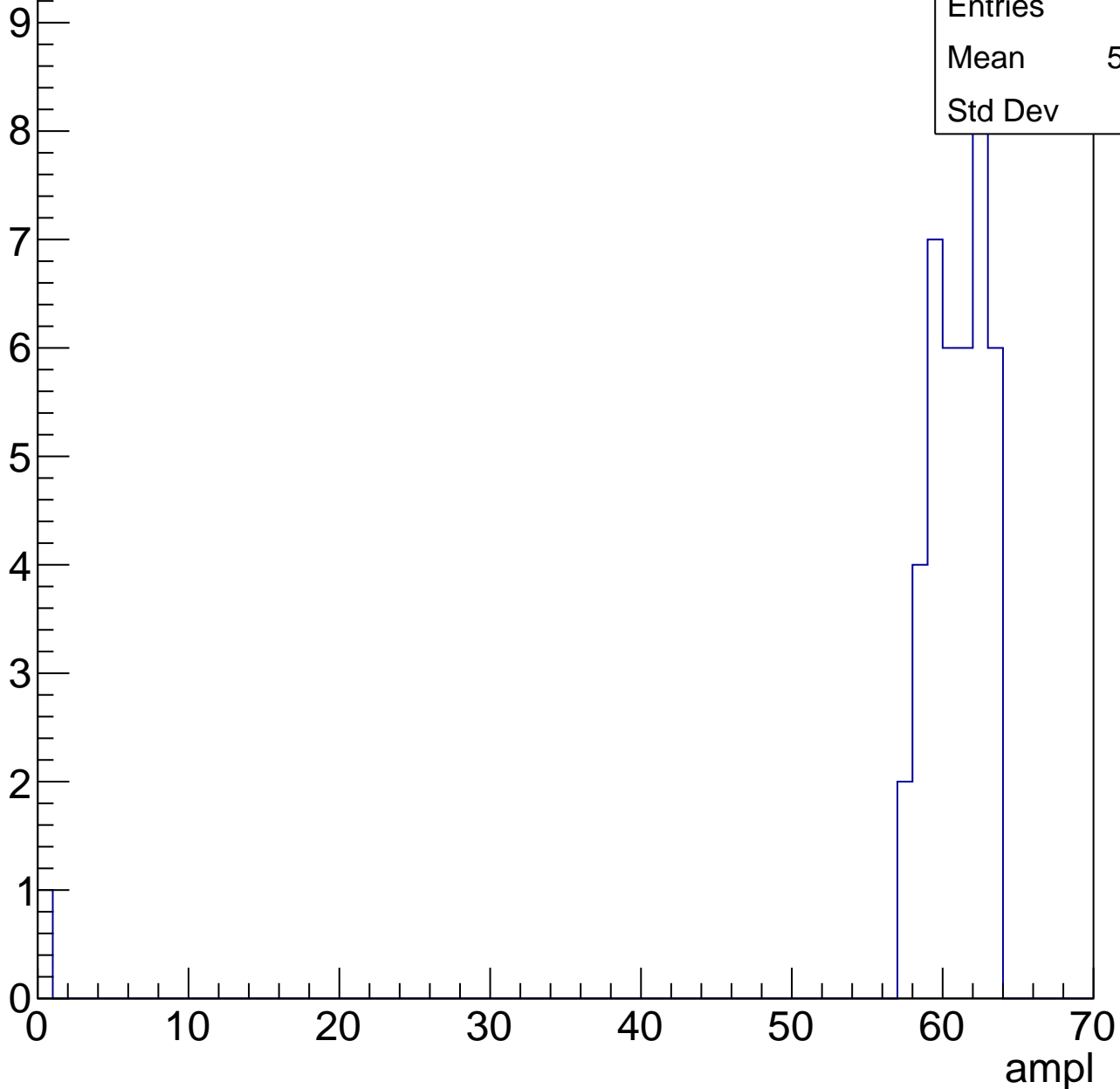


B1L102S, U9-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

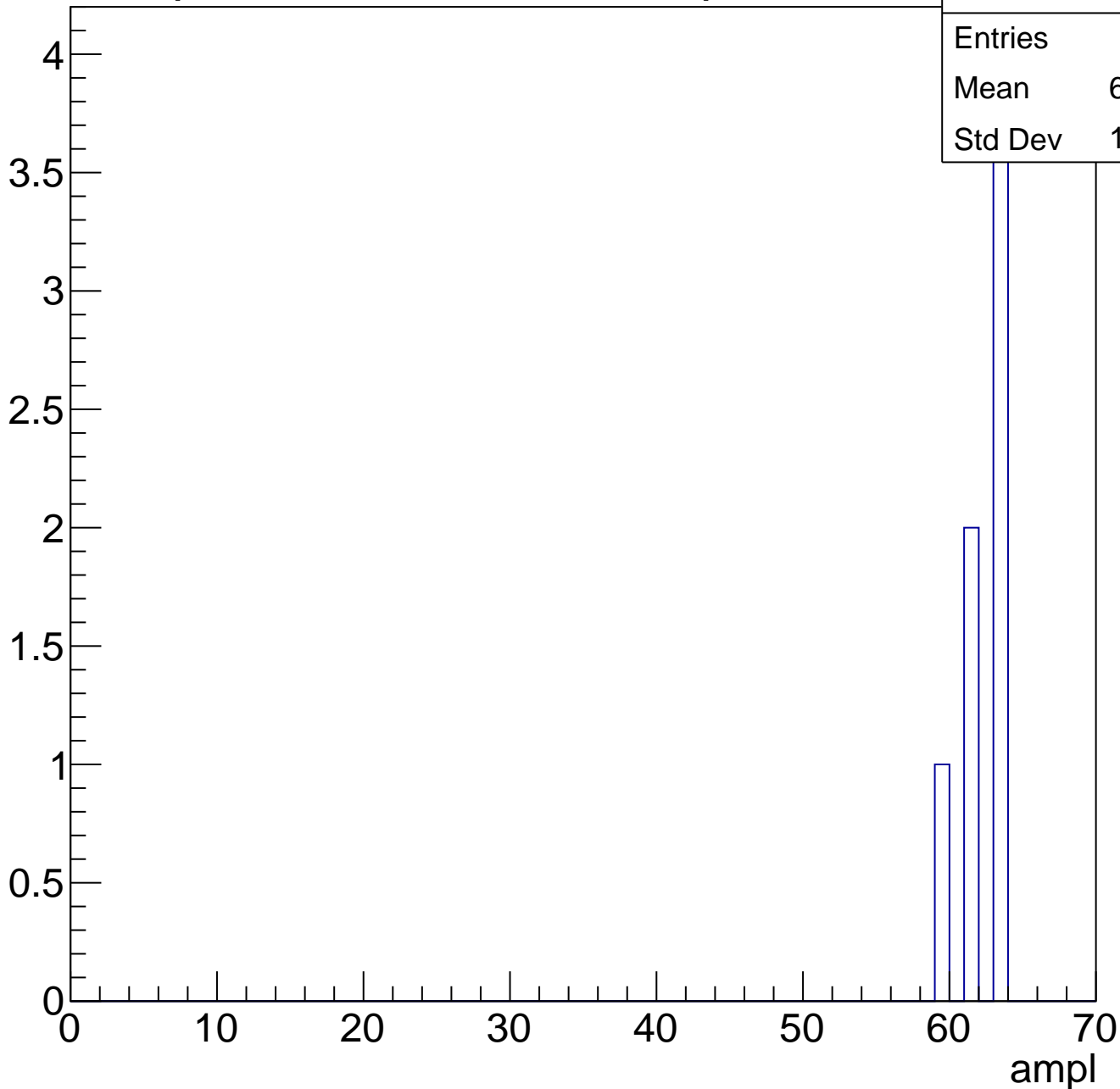
Entries	41
Mean	59.05
Std Dev	9.5



B1L102S, U9-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch12, adc0

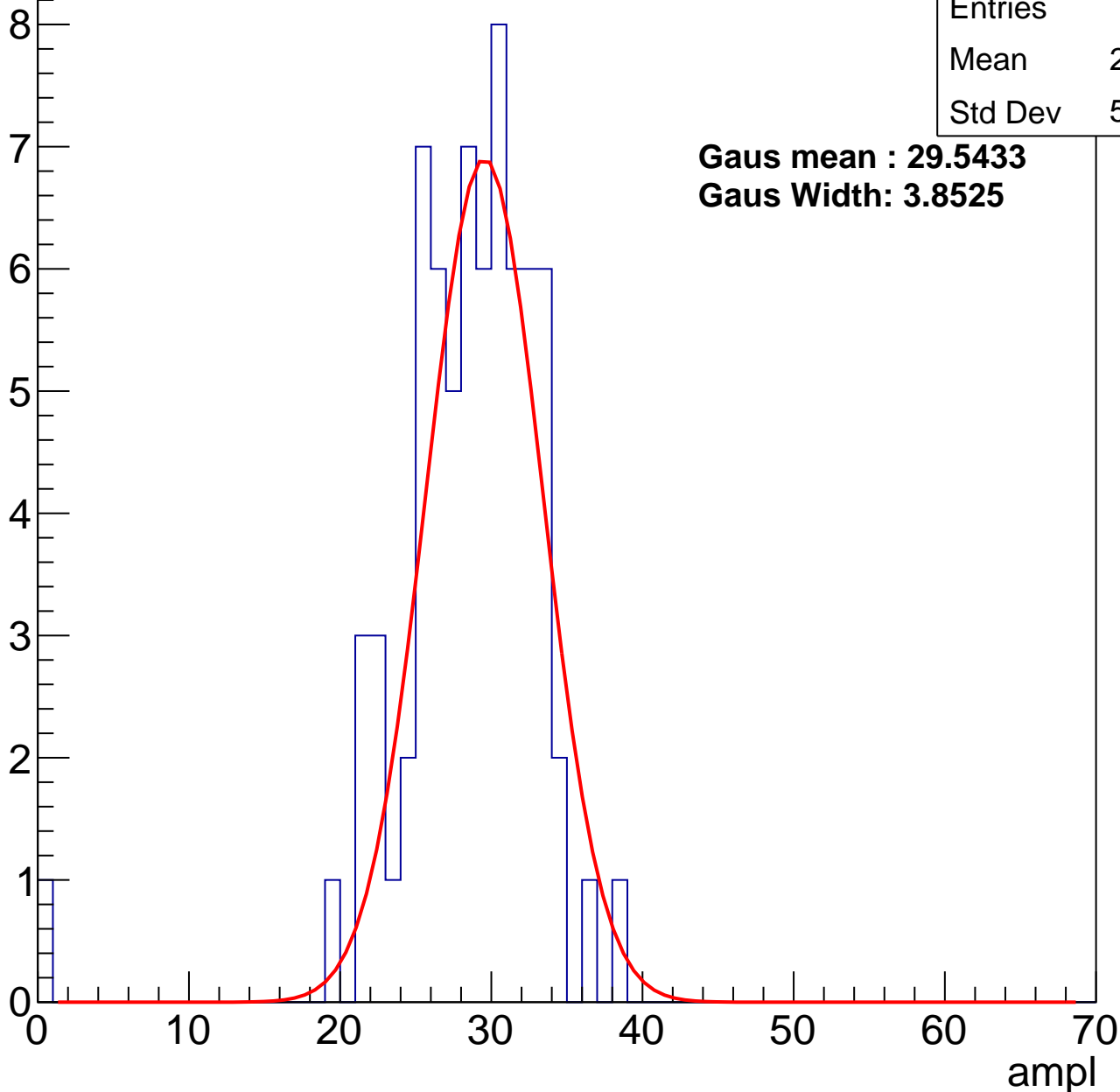
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.96
Std Dev	5.059

Gaus mean : 29.5433

Gaus Width: 3.8525



B1L102S, U9-ch12, adc1

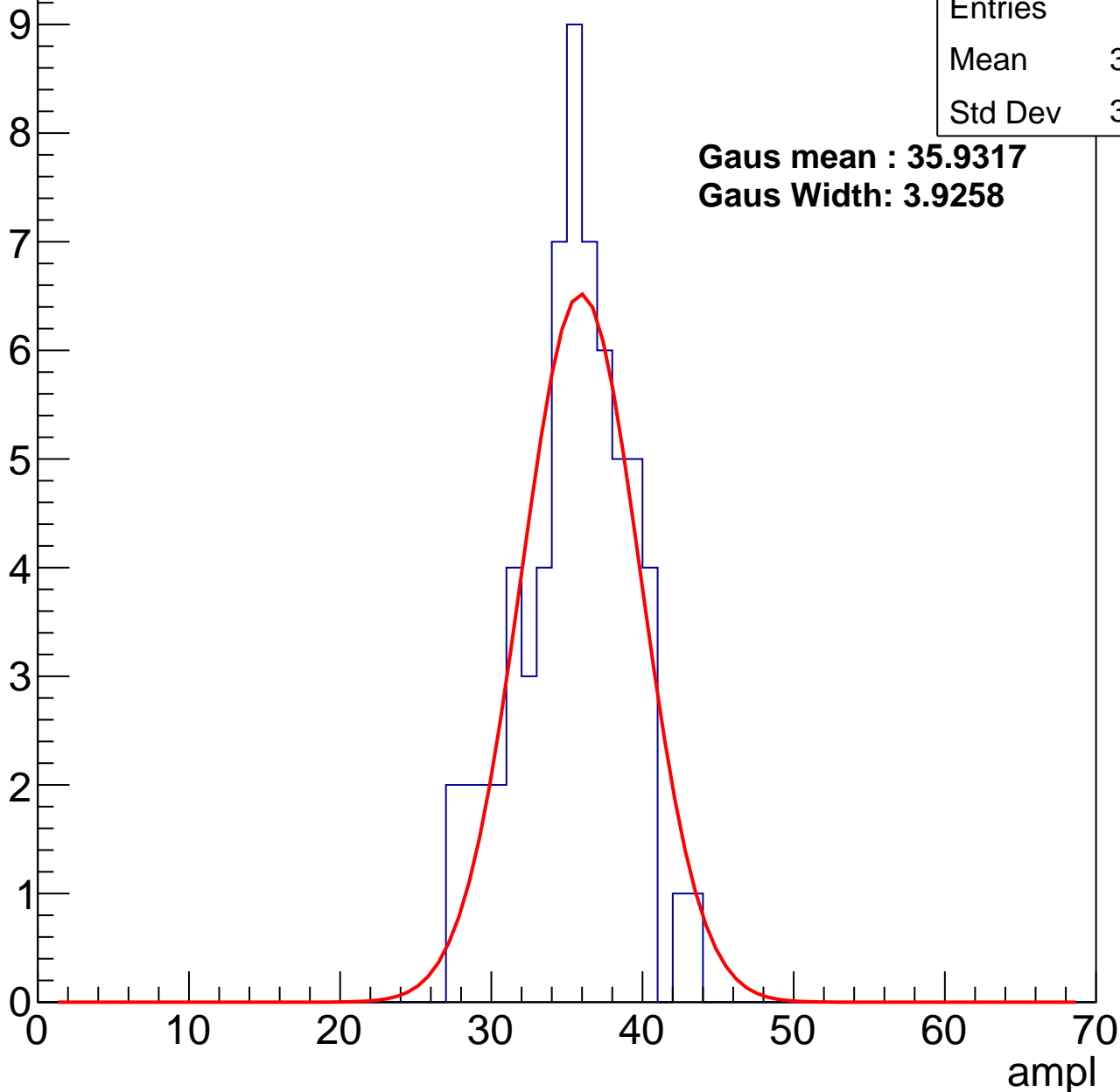
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	34.95
Std Dev	3.603

Gaus mean : 35.9317

Gaus Width: 3.9258



B1L102S, U9-ch12, adc2

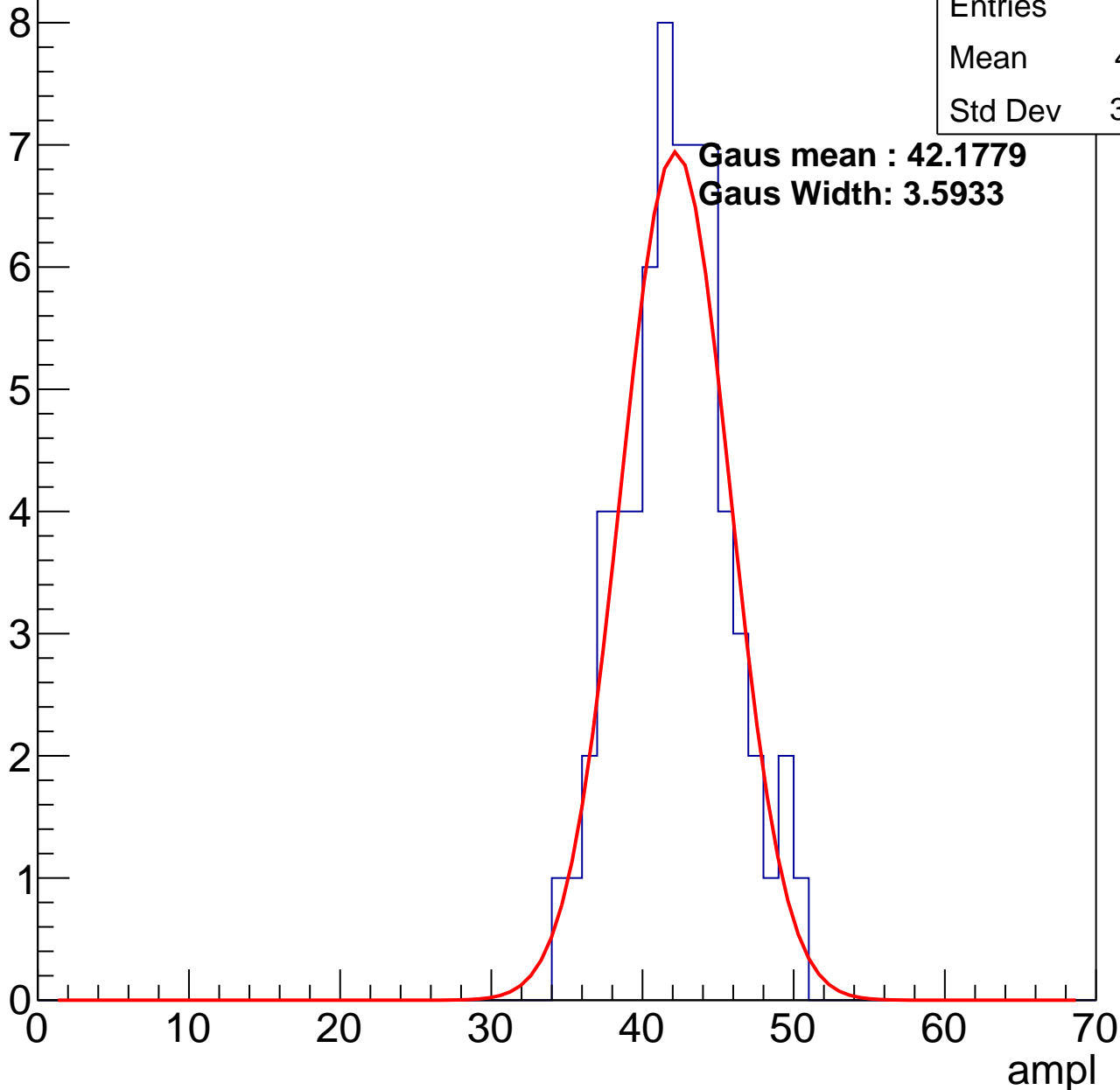
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.81
Std Dev	3.495

Gaus mean : 42.1779

Gaus Width: 3.5933

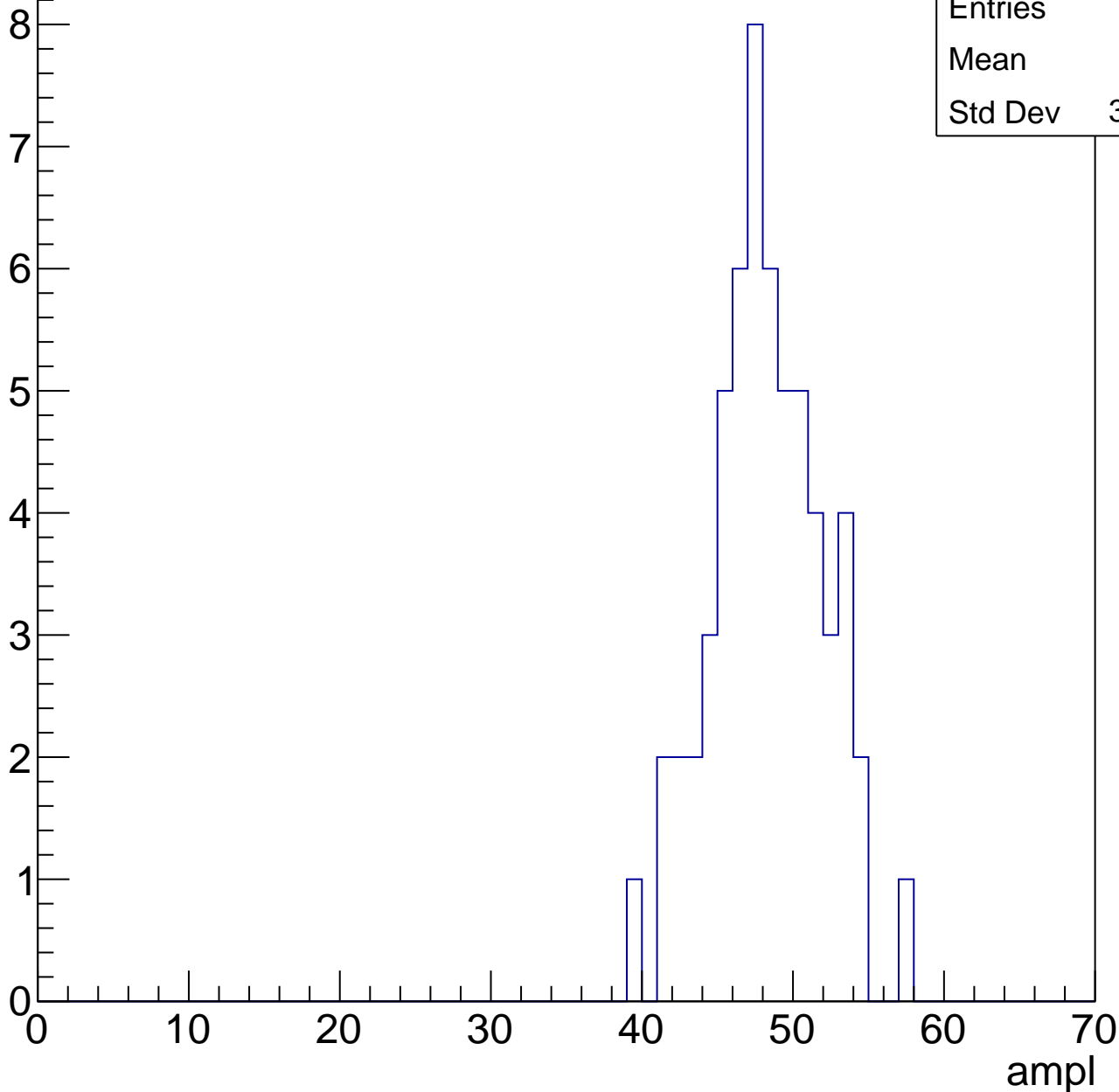


B1L102S, U9-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.8
Std Dev	3.649

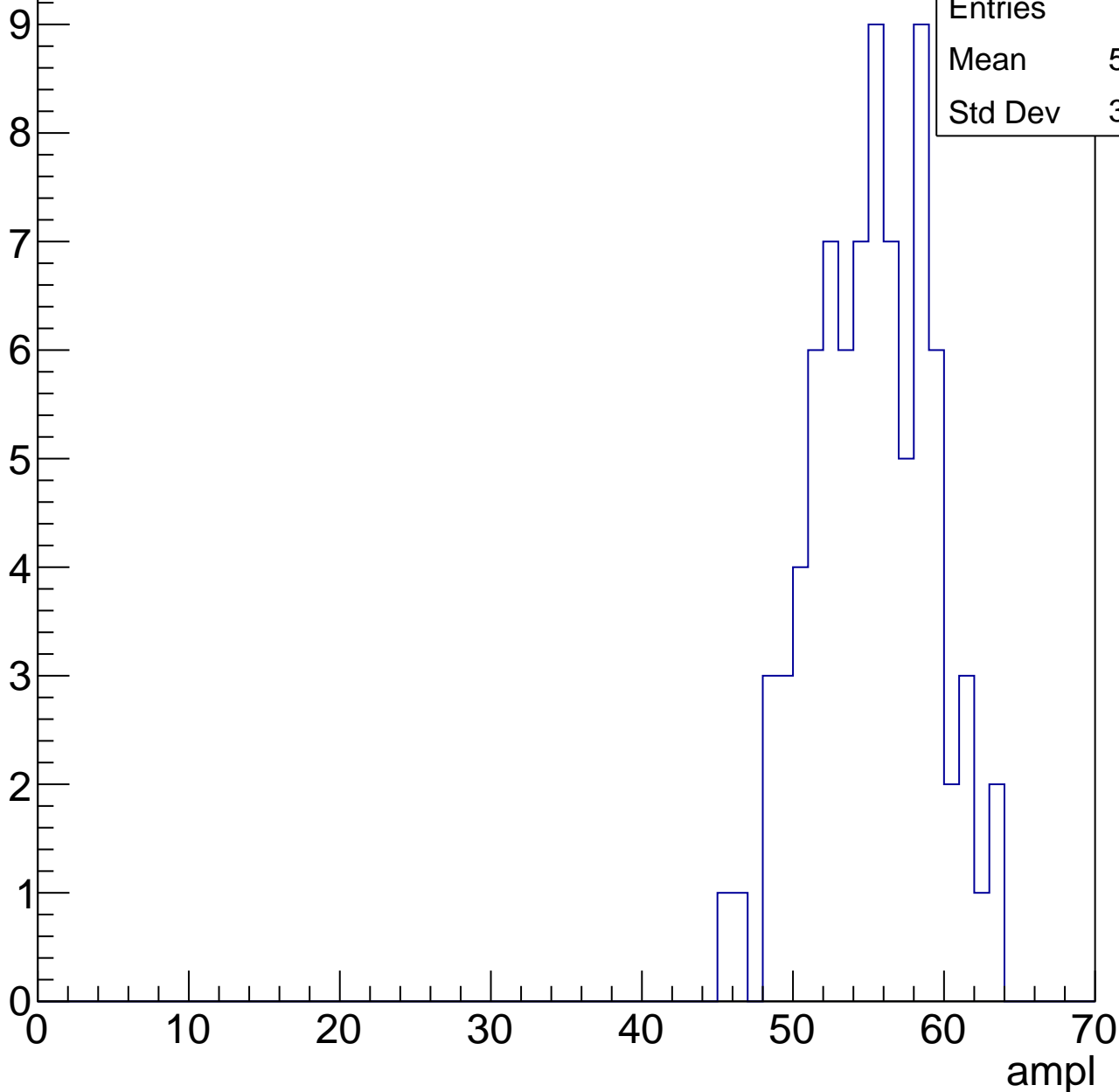


B1L102S, U9-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	54.72
Std Dev	3.918



B1L102S, U9-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

32

Mean

57.31

Std Dev

10.53

ampl

0

10

20

30

40

50

60

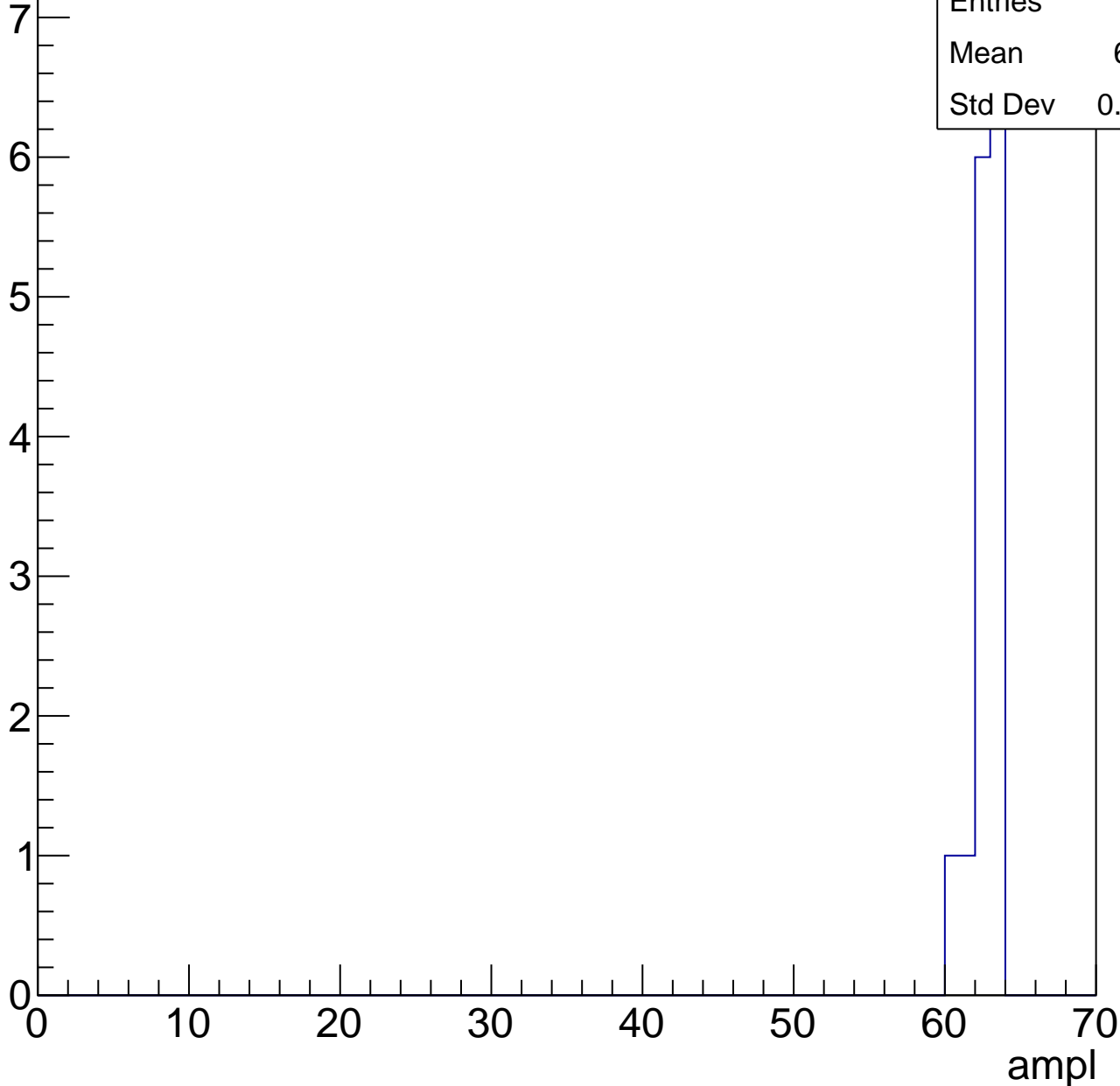
70

B1L102S, U9-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	62.27
Std Dev	0.8537



B1L102S, U9-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch13, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	28.88
Std Dev	4.752

Gaus mean : 29.5450

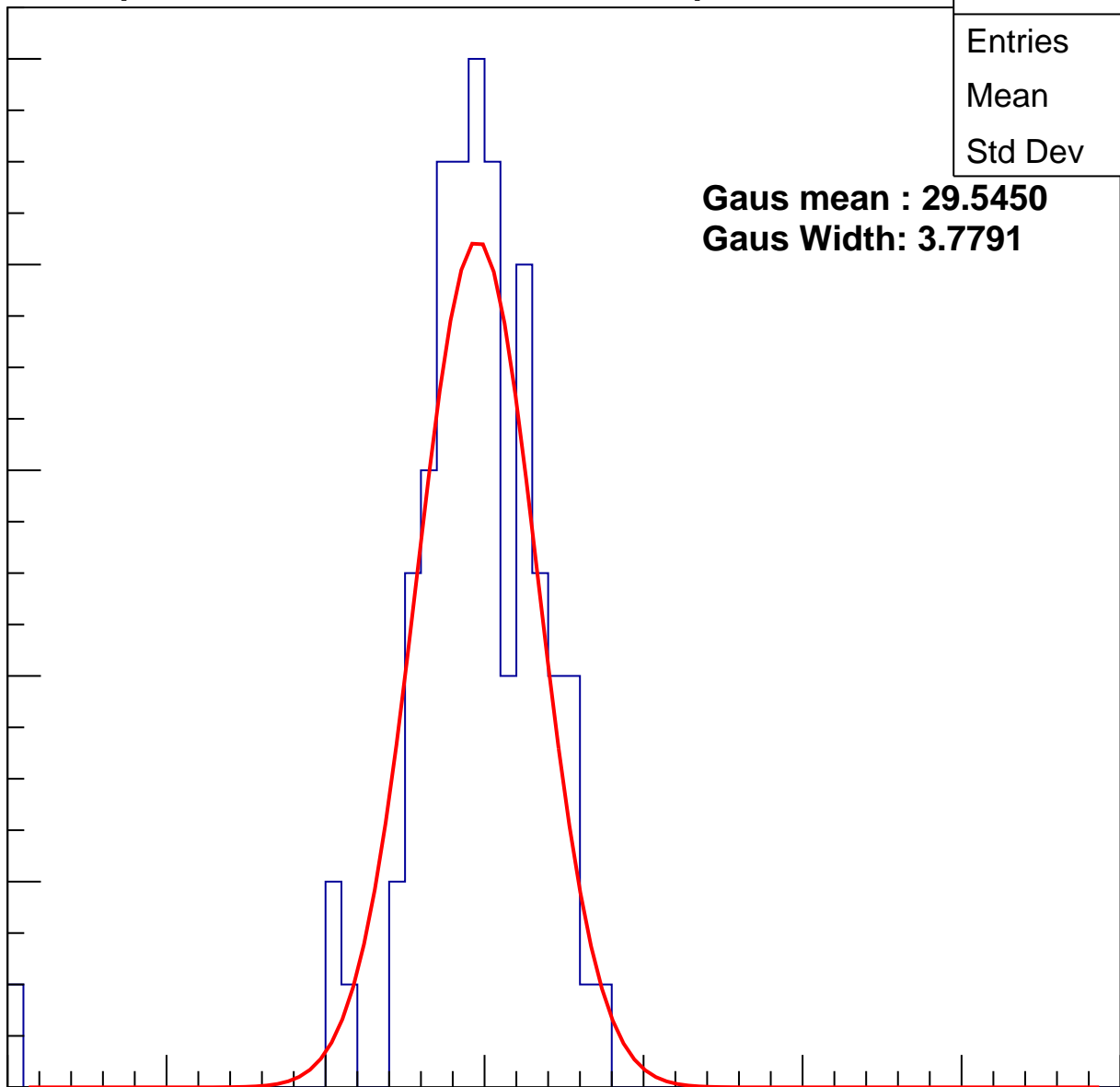
Gaus Width: 3.7791

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch13, adc1

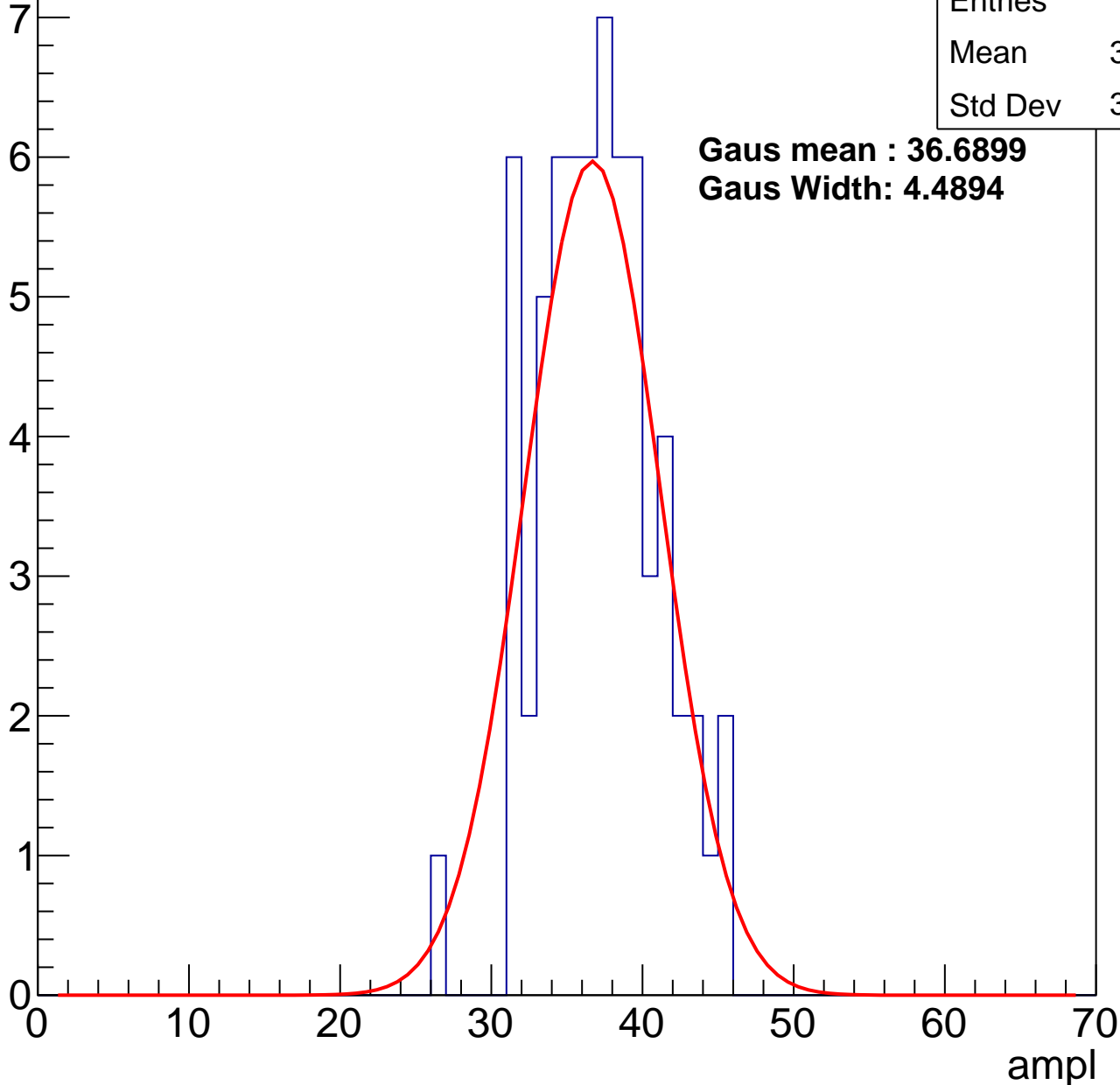
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.62
Std Dev	3.846

Gaus mean : 36.6899

Gaus Width: 4.4894



B1L102S, U9-ch13, adc2

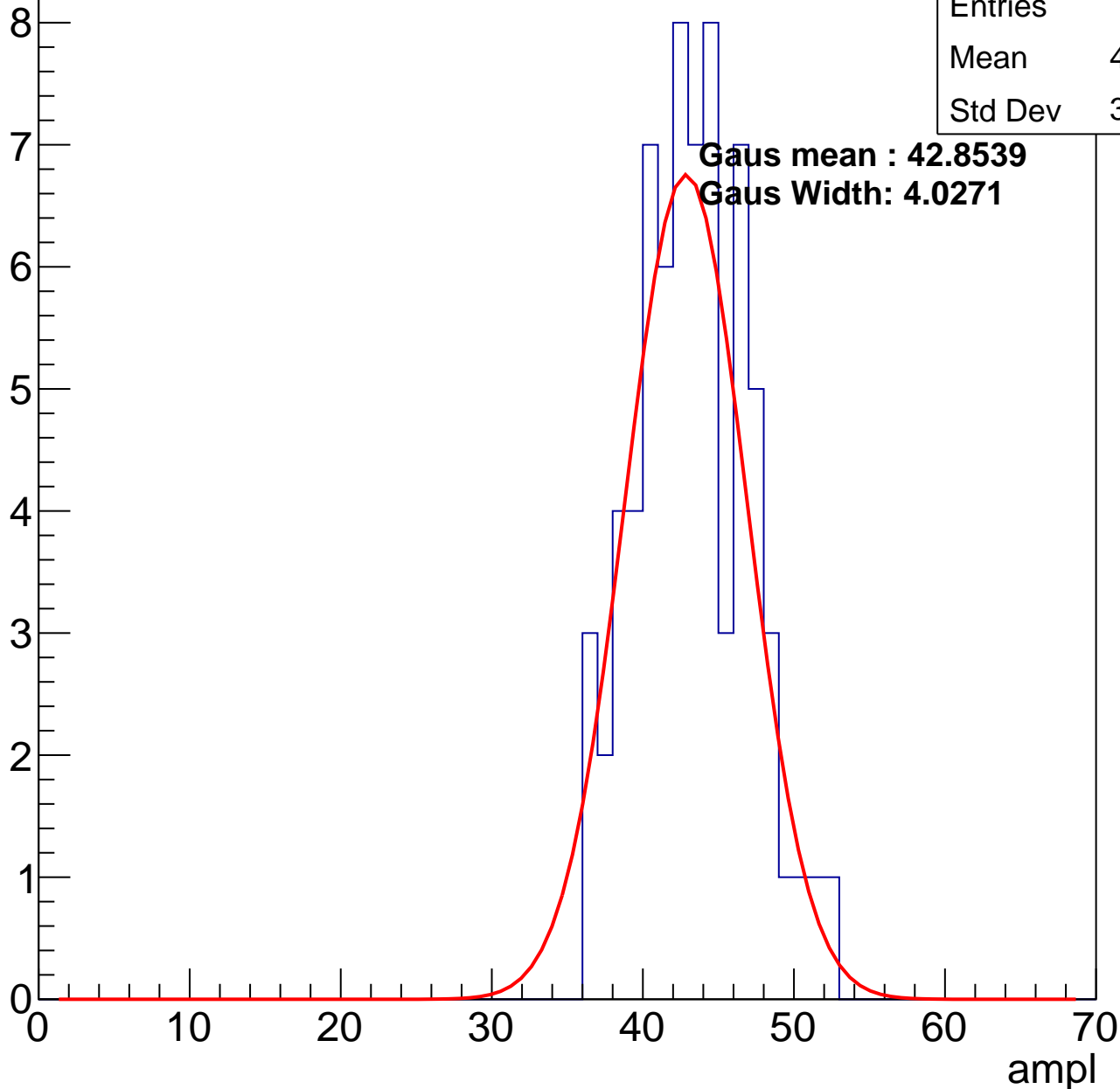
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.86
Std Dev	3.647

Gaus mean : 42.8539

Gaus Width: 4.0271

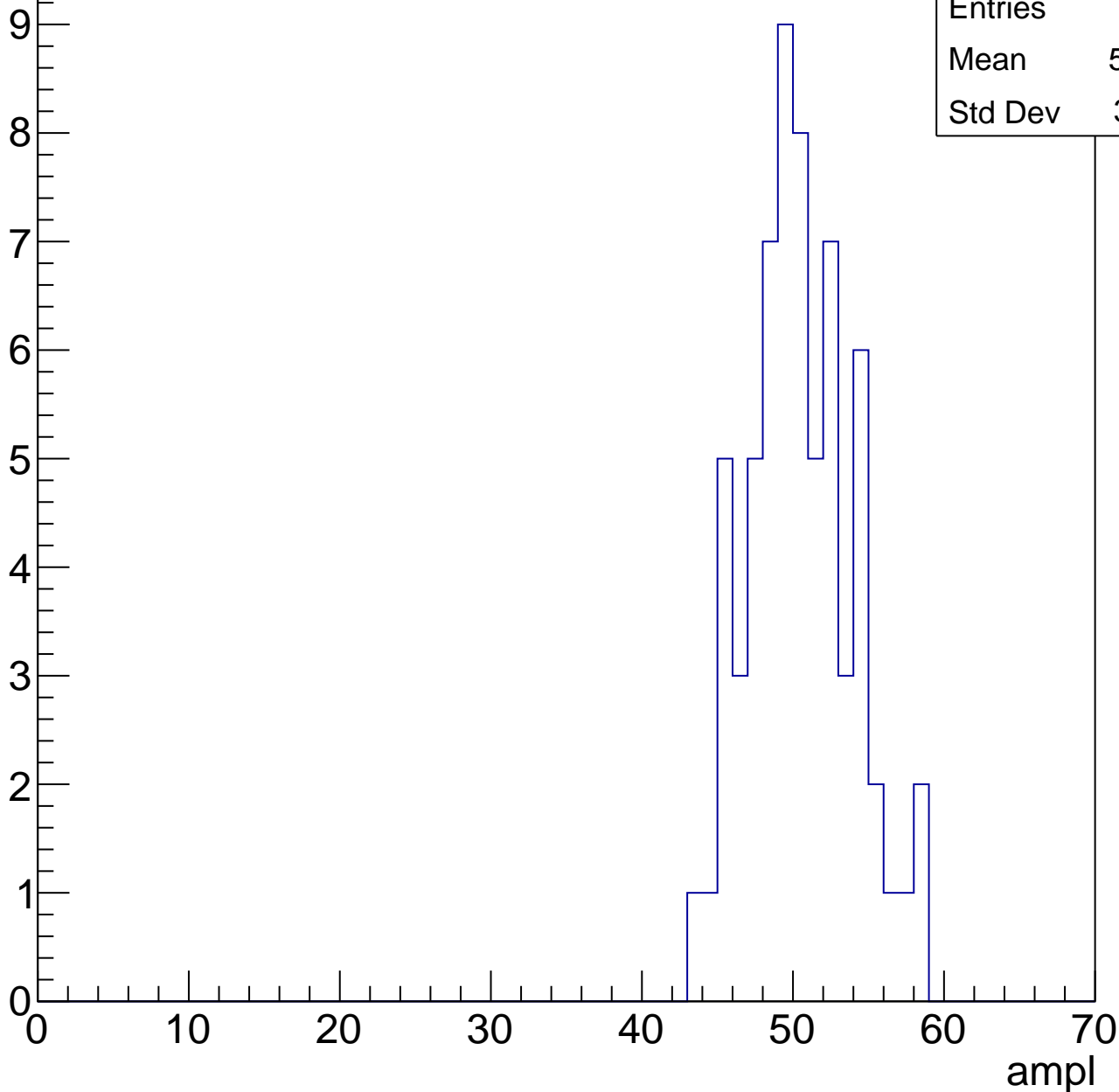


B1L102S, U9-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.05
Std Dev	3.391

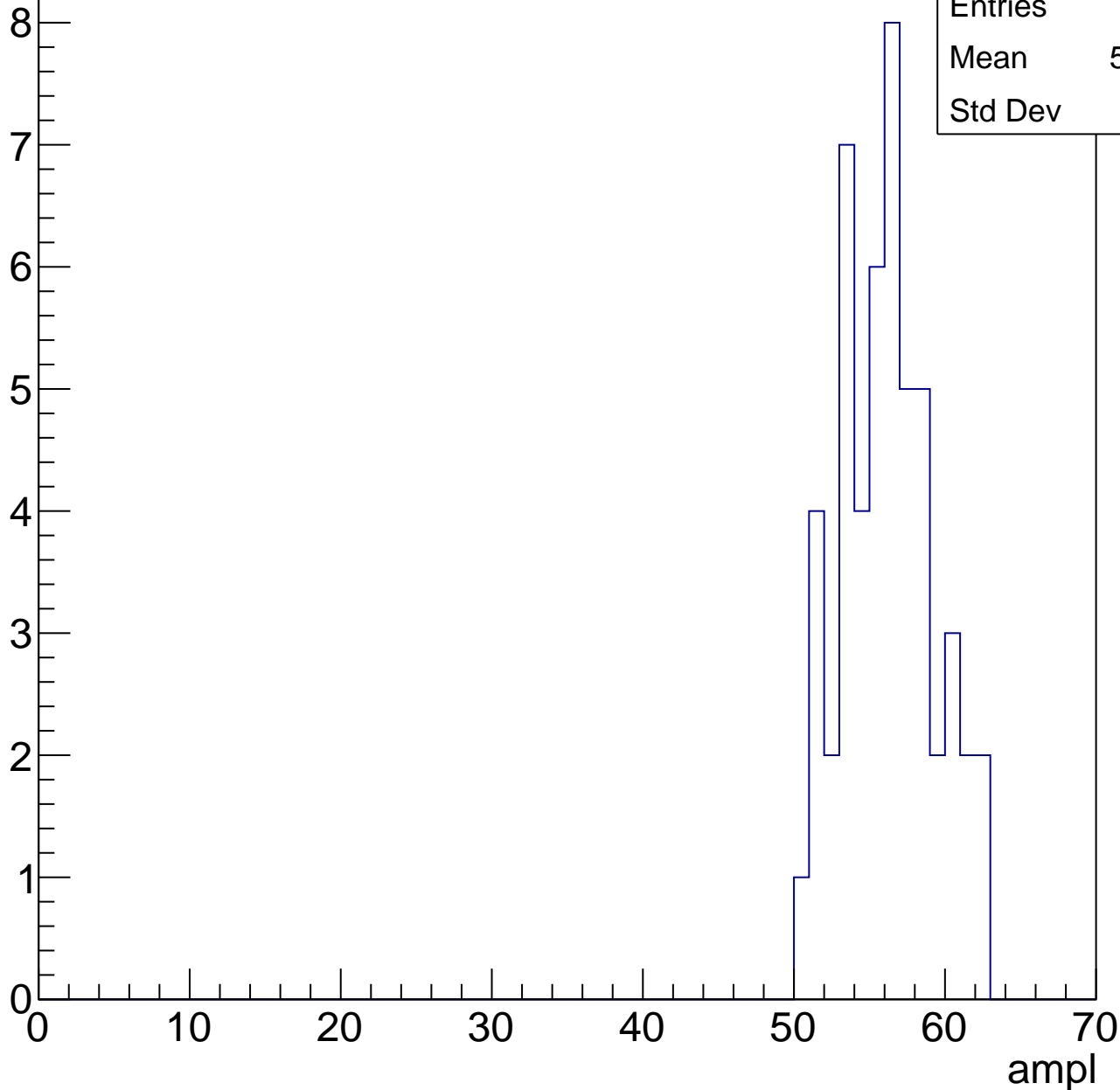


B1L102S, U9-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

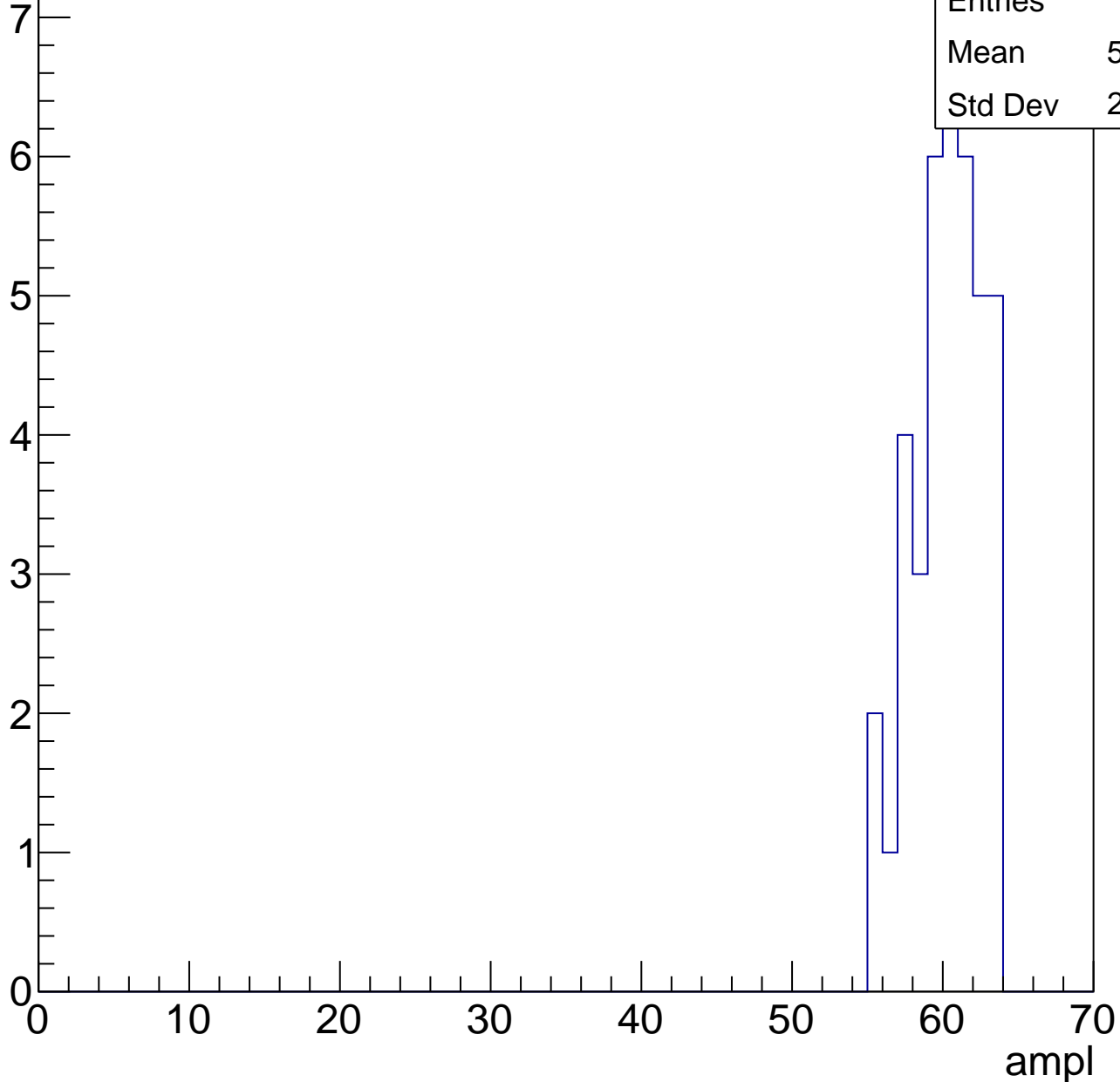
Entries	51
Mean	55.73
Std Dev	3.01



B1L102S, U9-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

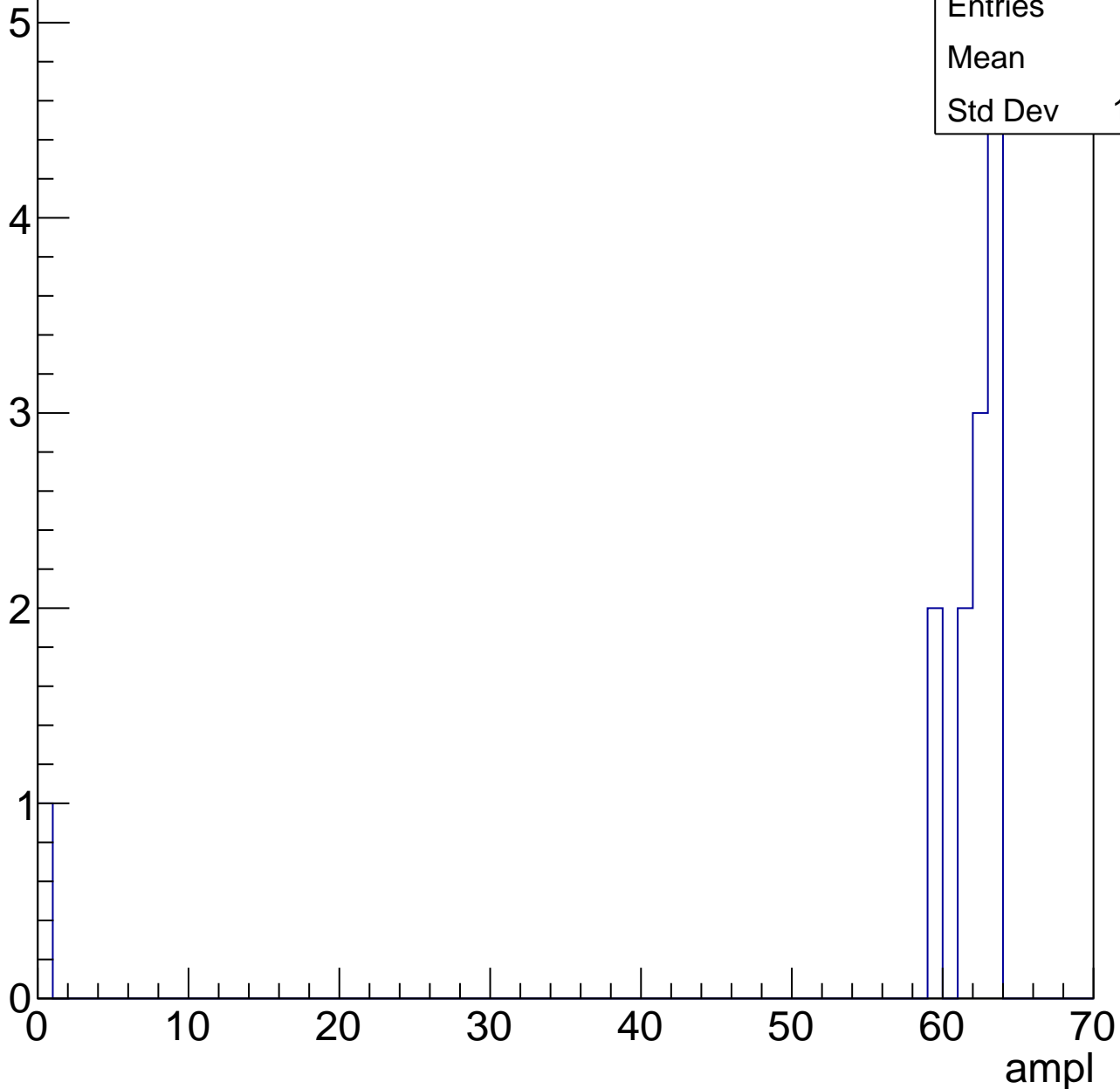


B1L102S, U9-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	57
Std Dev	16.51



B1L102S, U9-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch14, adc0

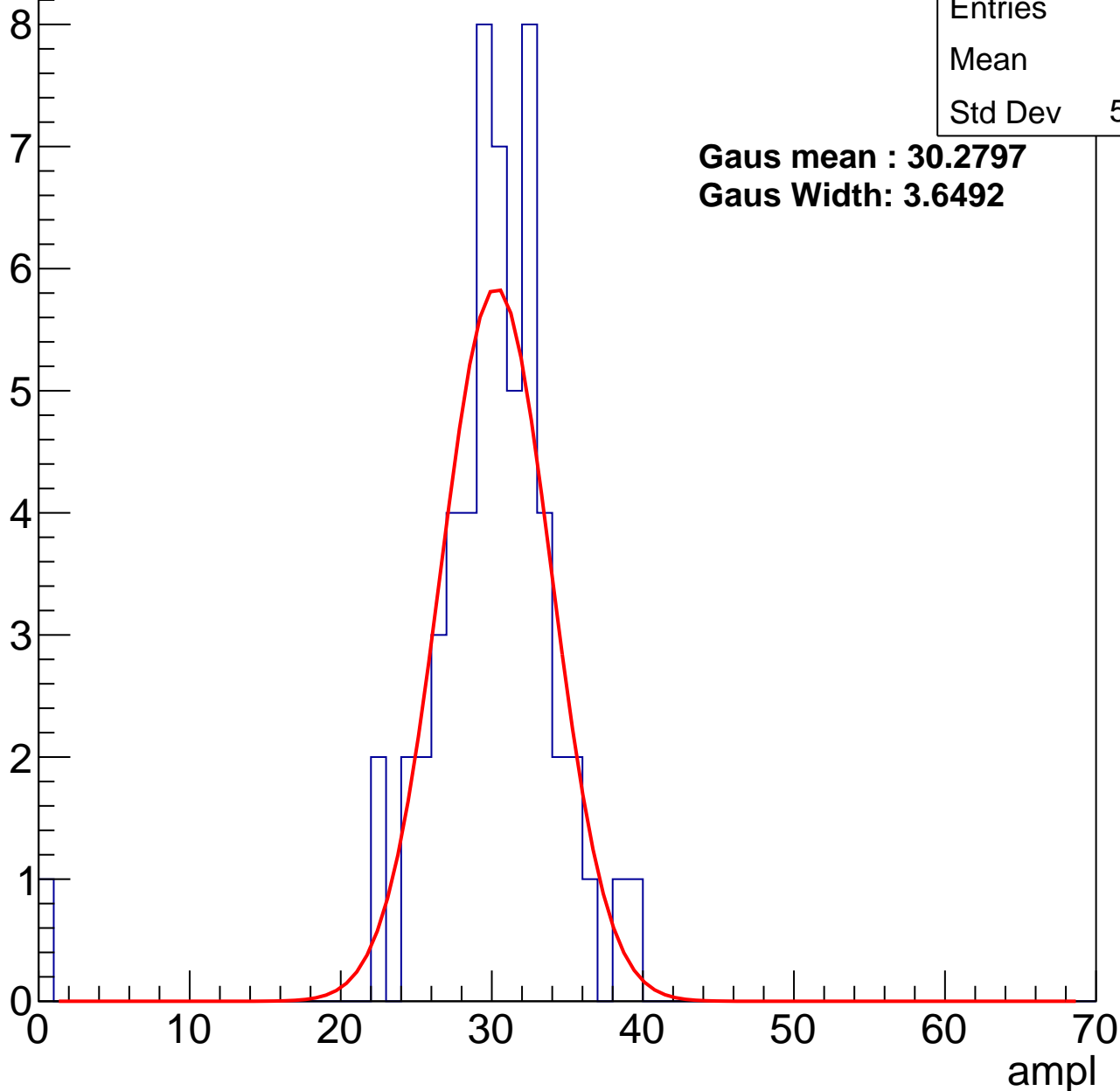
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	29.4
Std Dev	5.254

Gaus mean : 30.2797

Gaus Width: 3.6492



B1L102S, U9-ch14, adc1

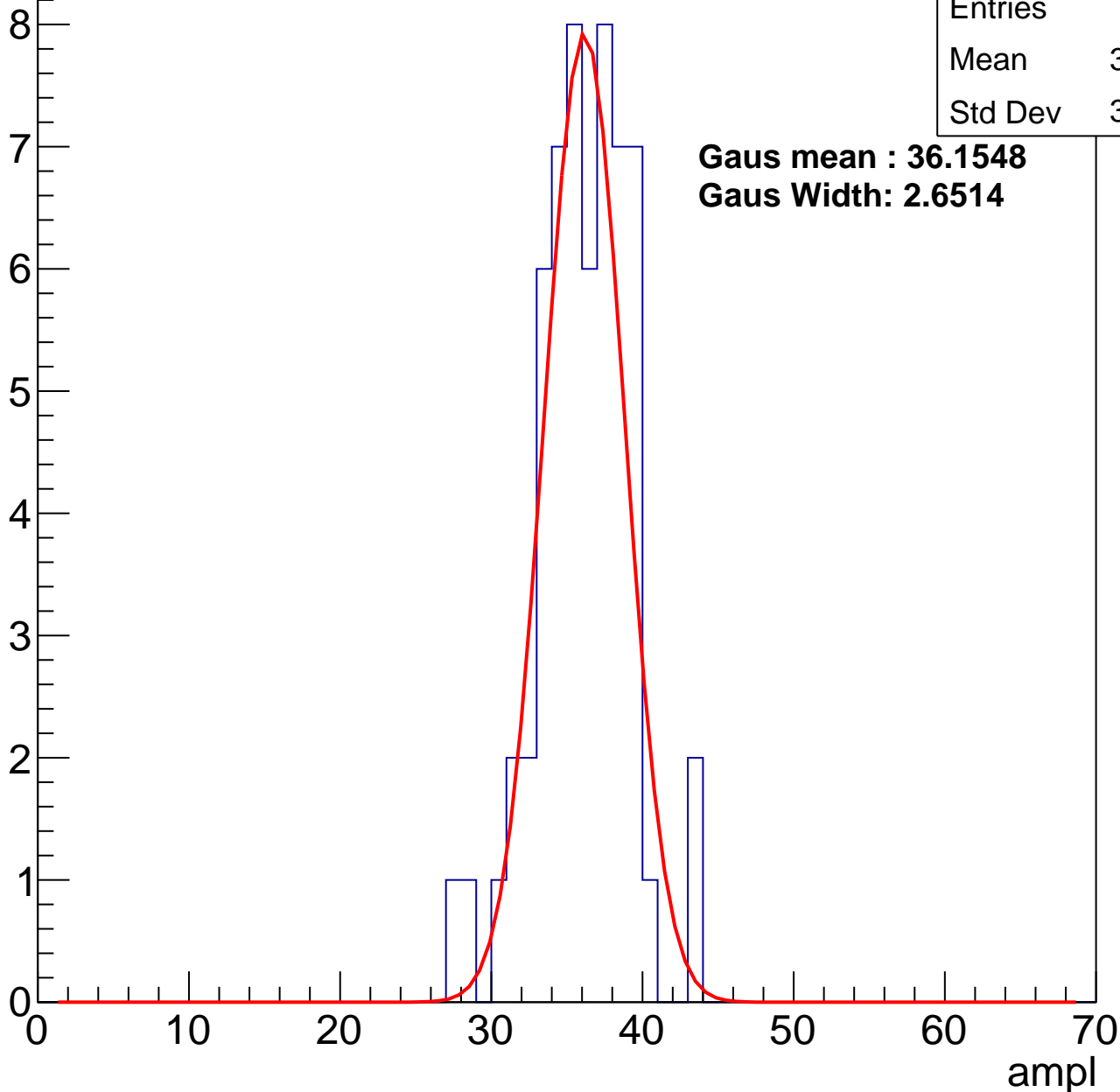
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.66
Std Dev	3.079

Gaus mean : 36.1548

Gaus Width: 2.6514



B1L102S, U9-ch14, adc2

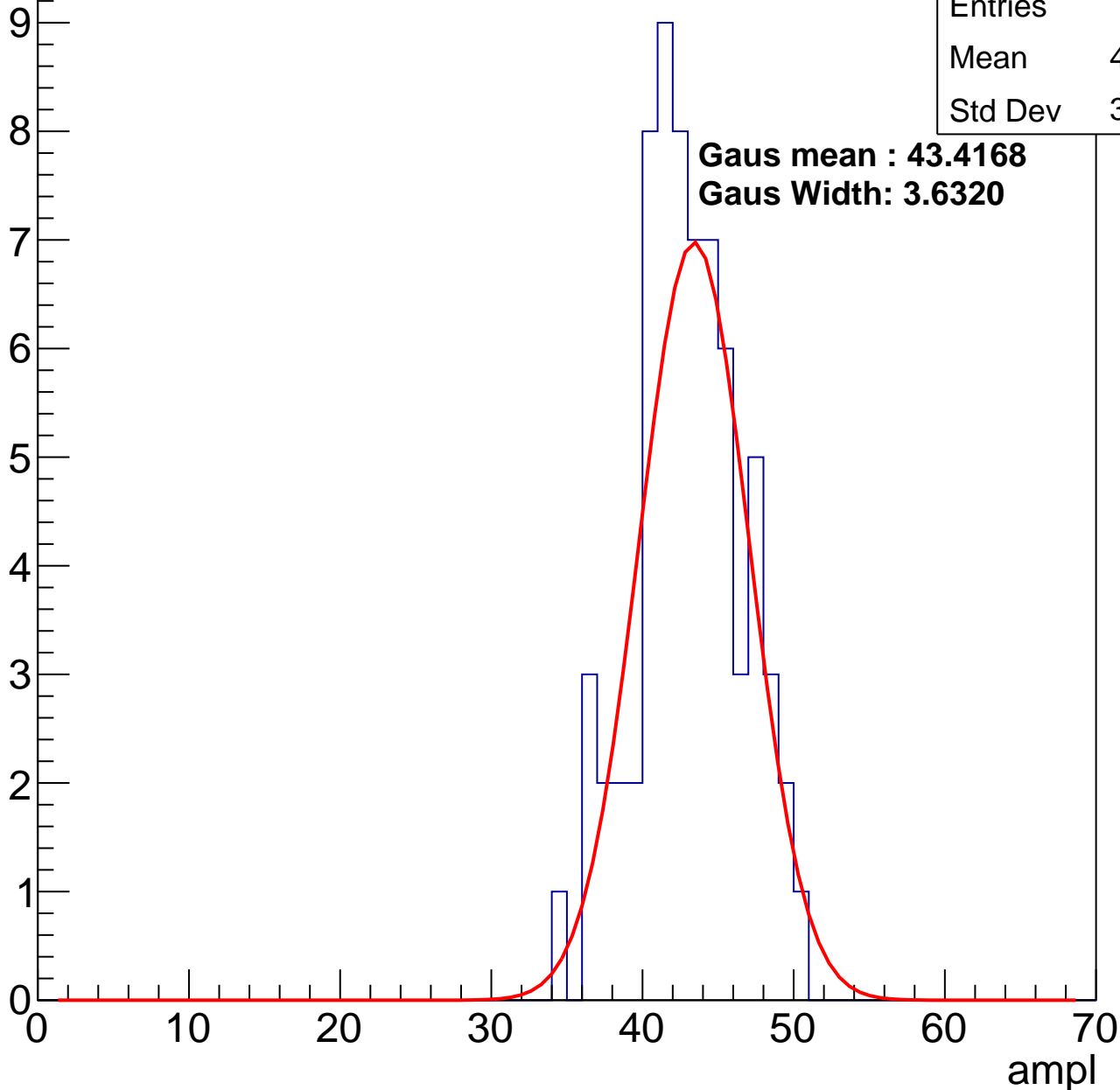
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.59
Std Dev	3.449

Gaus mean : 43.4168

Gaus Width: 3.6320

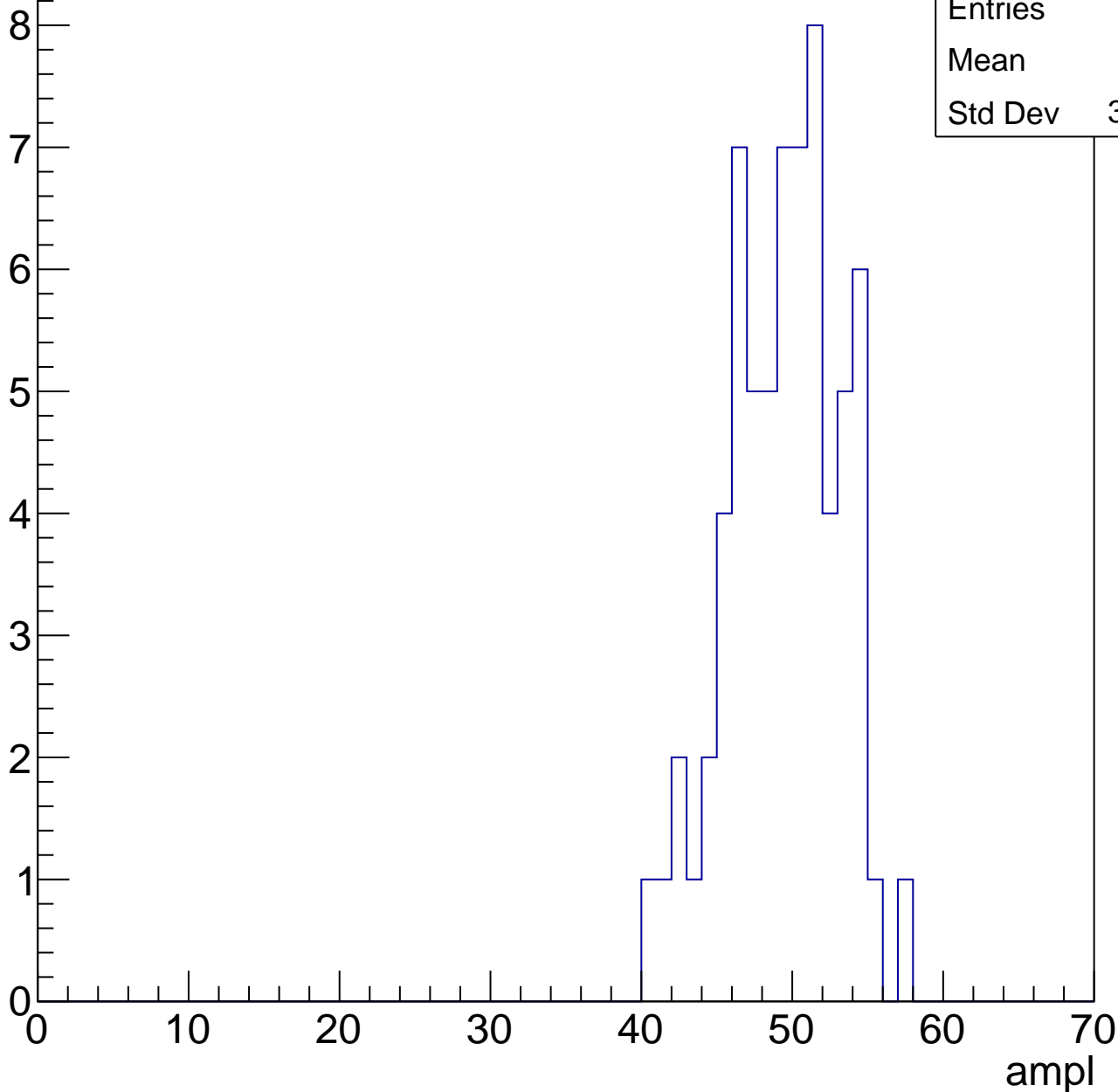


B1L102S, U9-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

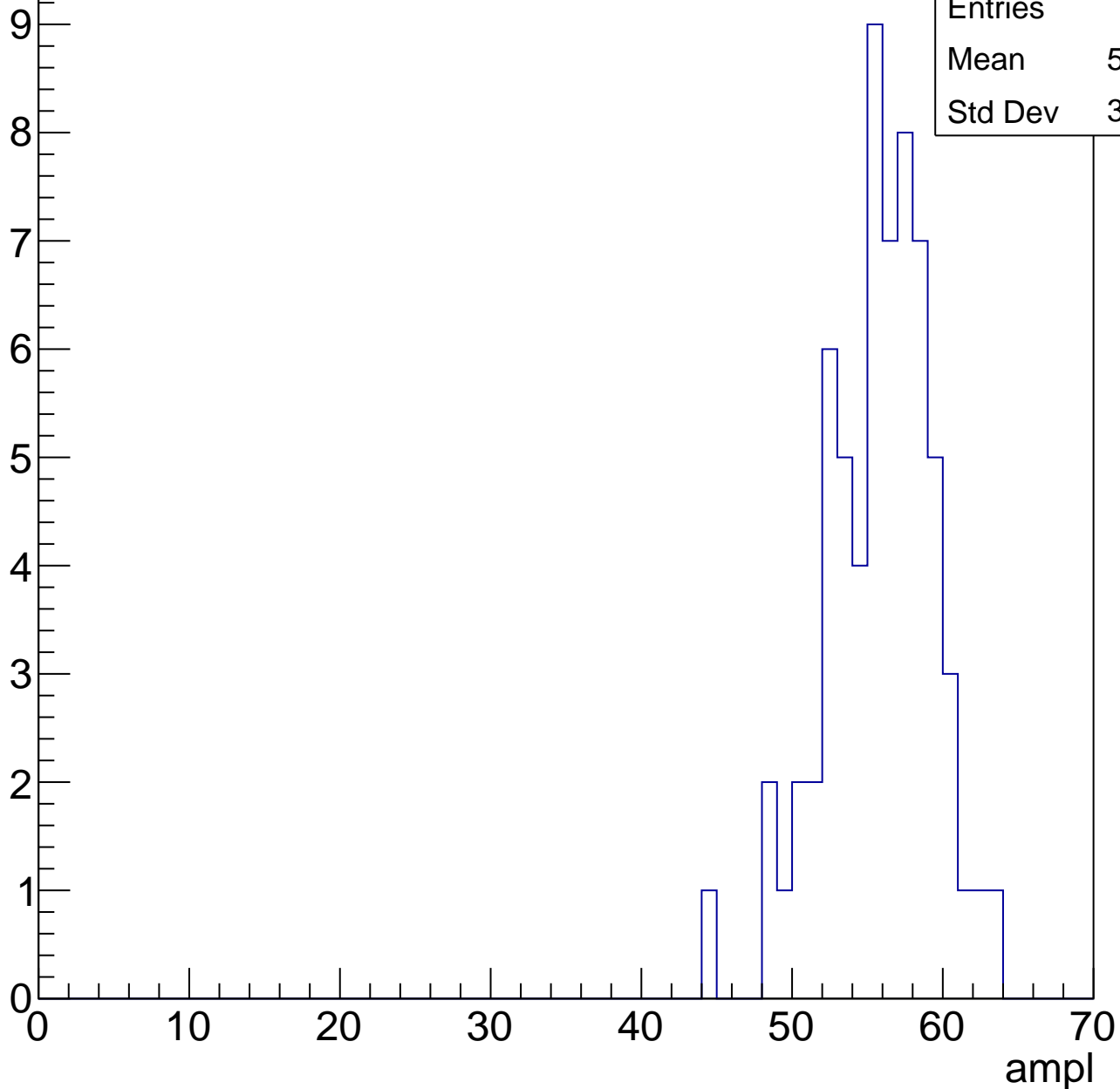
Entries	67
Mean	49
Std Dev	3.636



B1L102S, U9-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



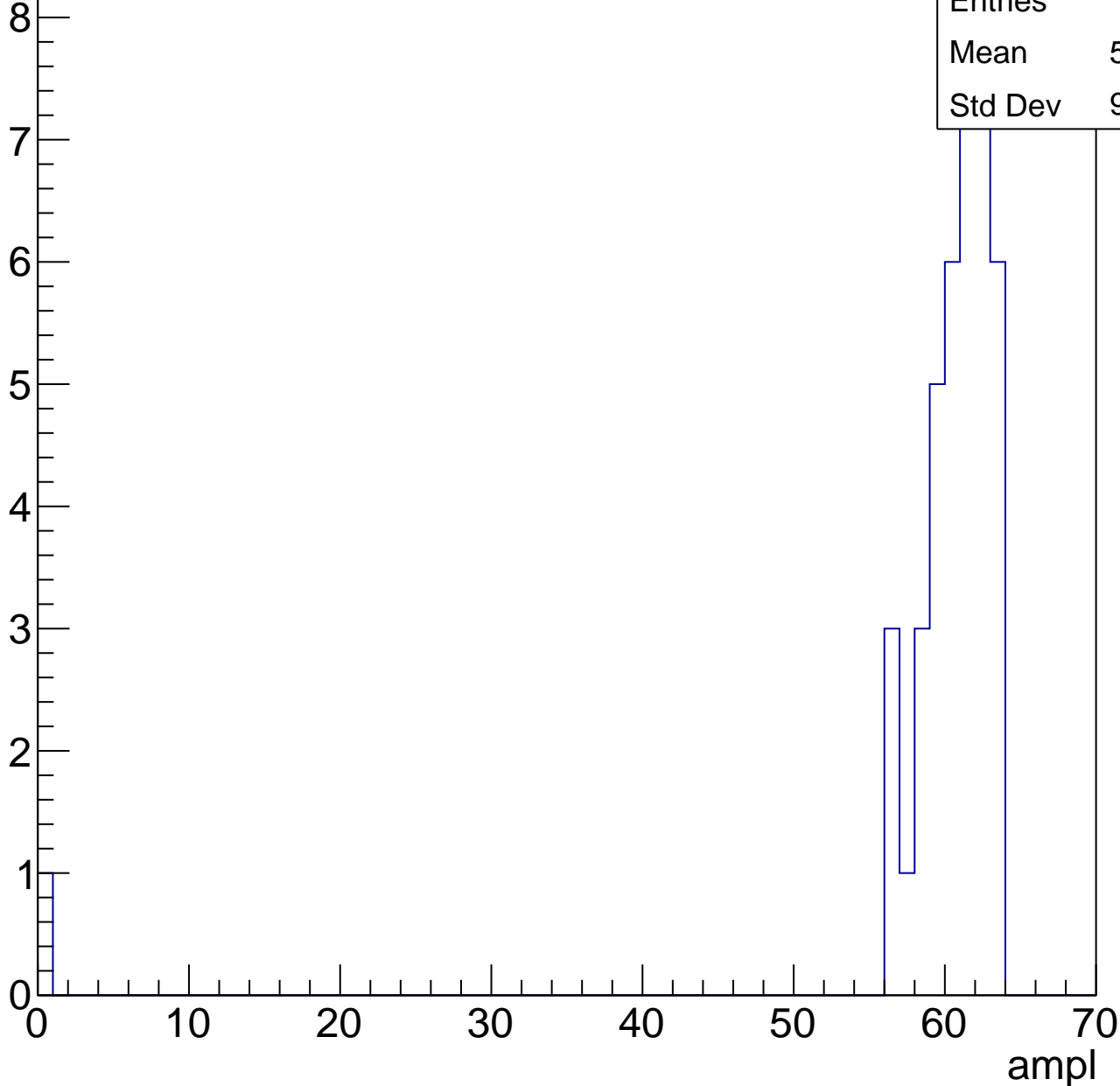
Entries	65
Mean	55.29
Std Dev	3.542

B1L102S, U9-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

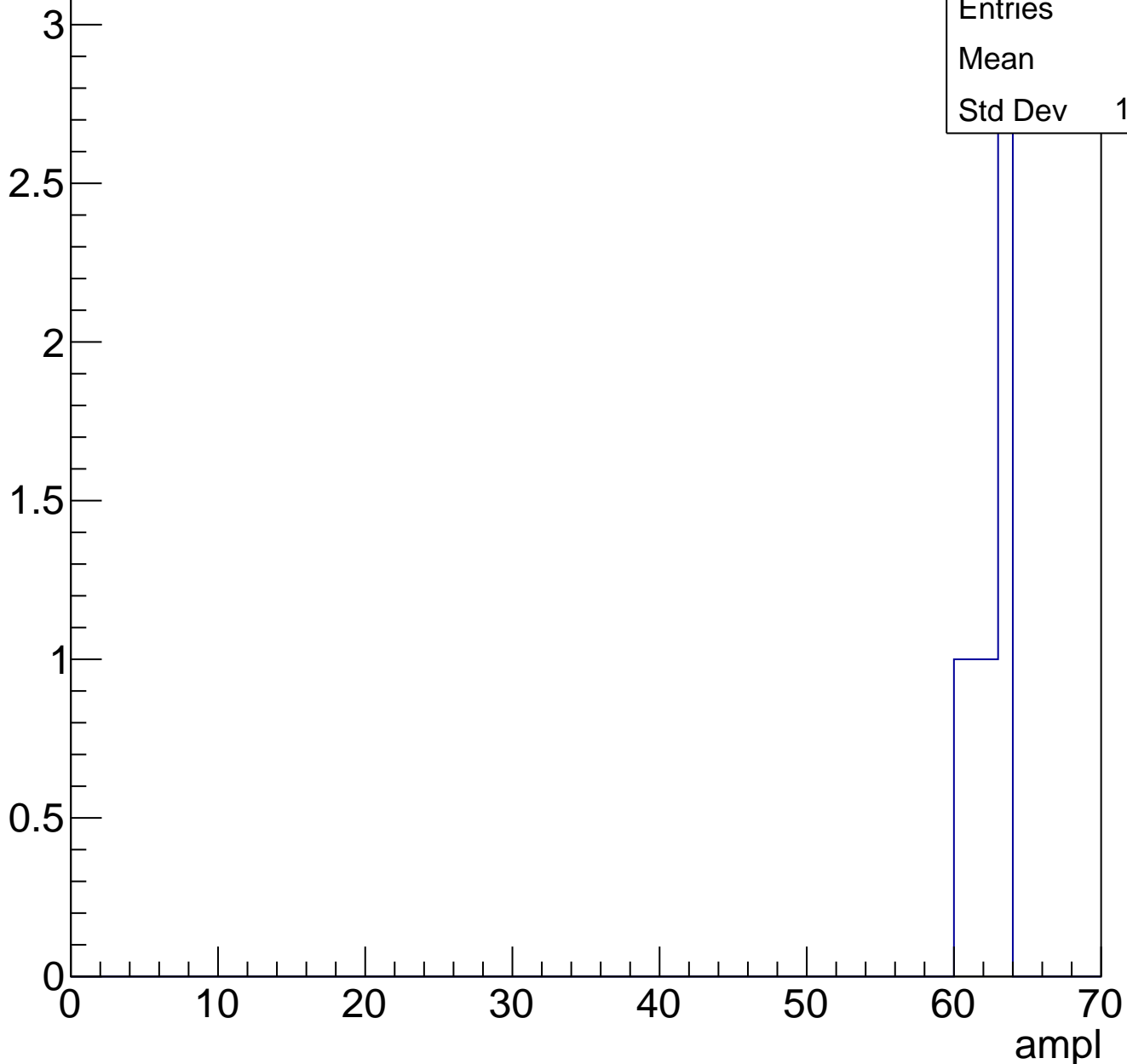
Entries	41
Mean	58.93
Std Dev	9.526



B1L102S, U9-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch15, adc0

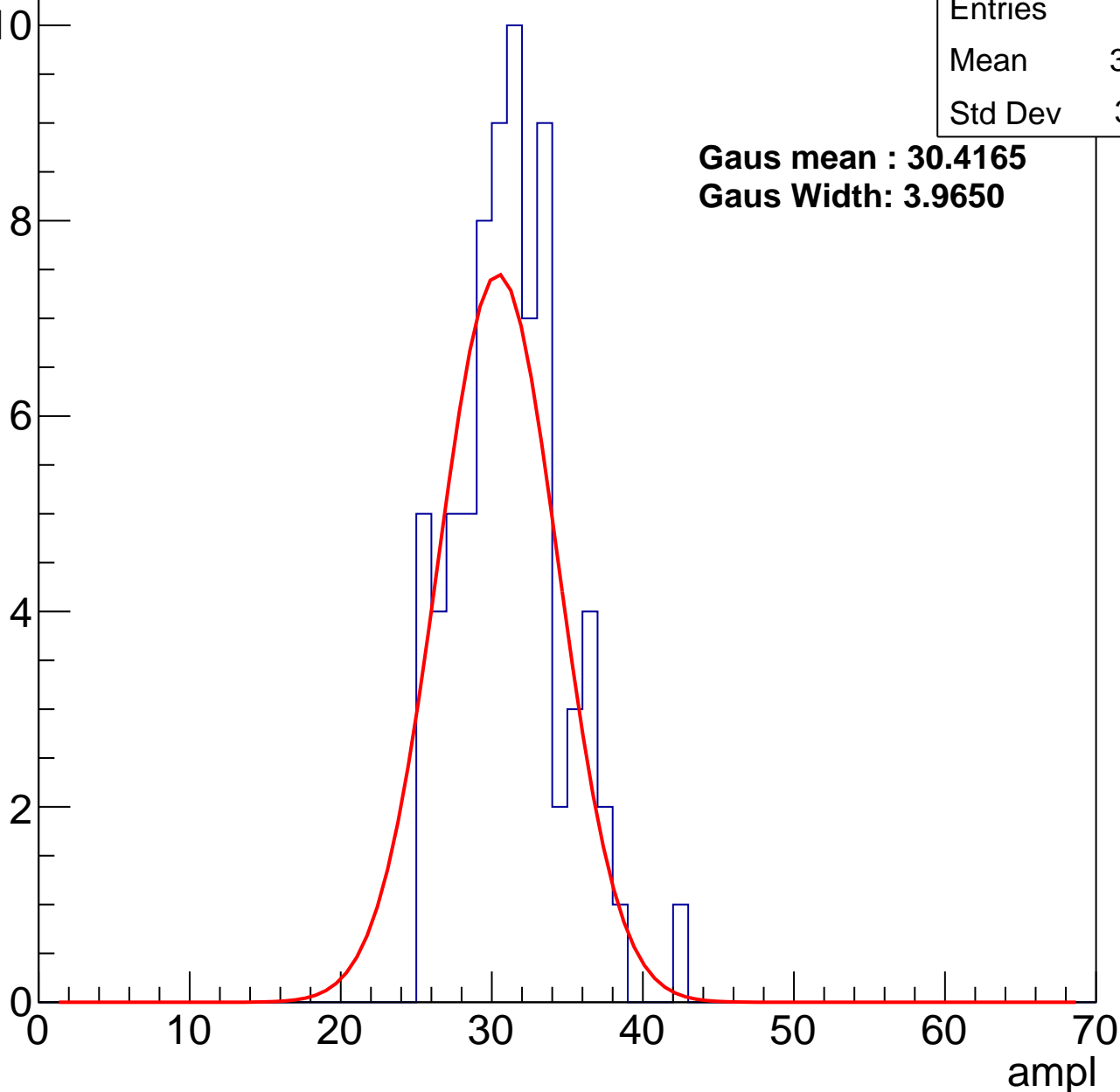
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	30.77
Std Dev	3.451

Gaus mean : 30.4165

Gaus Width: 3.9650



B1L102S, U9-ch15, adc1

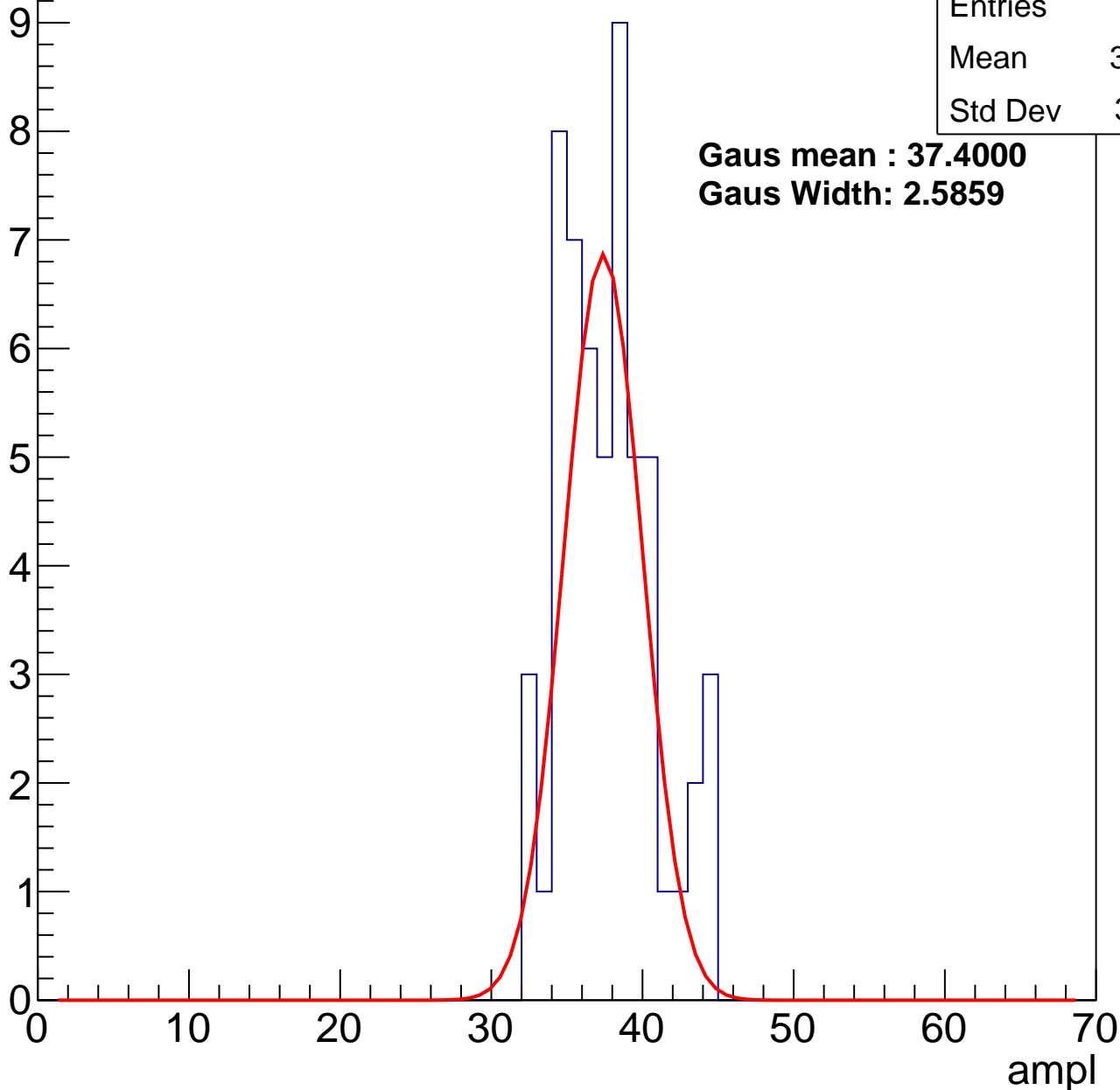
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	37.23
Std Dev	3.071

Gaus mean : 37.4000

Gaus Width: 2.5859



B1L102S, U9-ch15, adc2

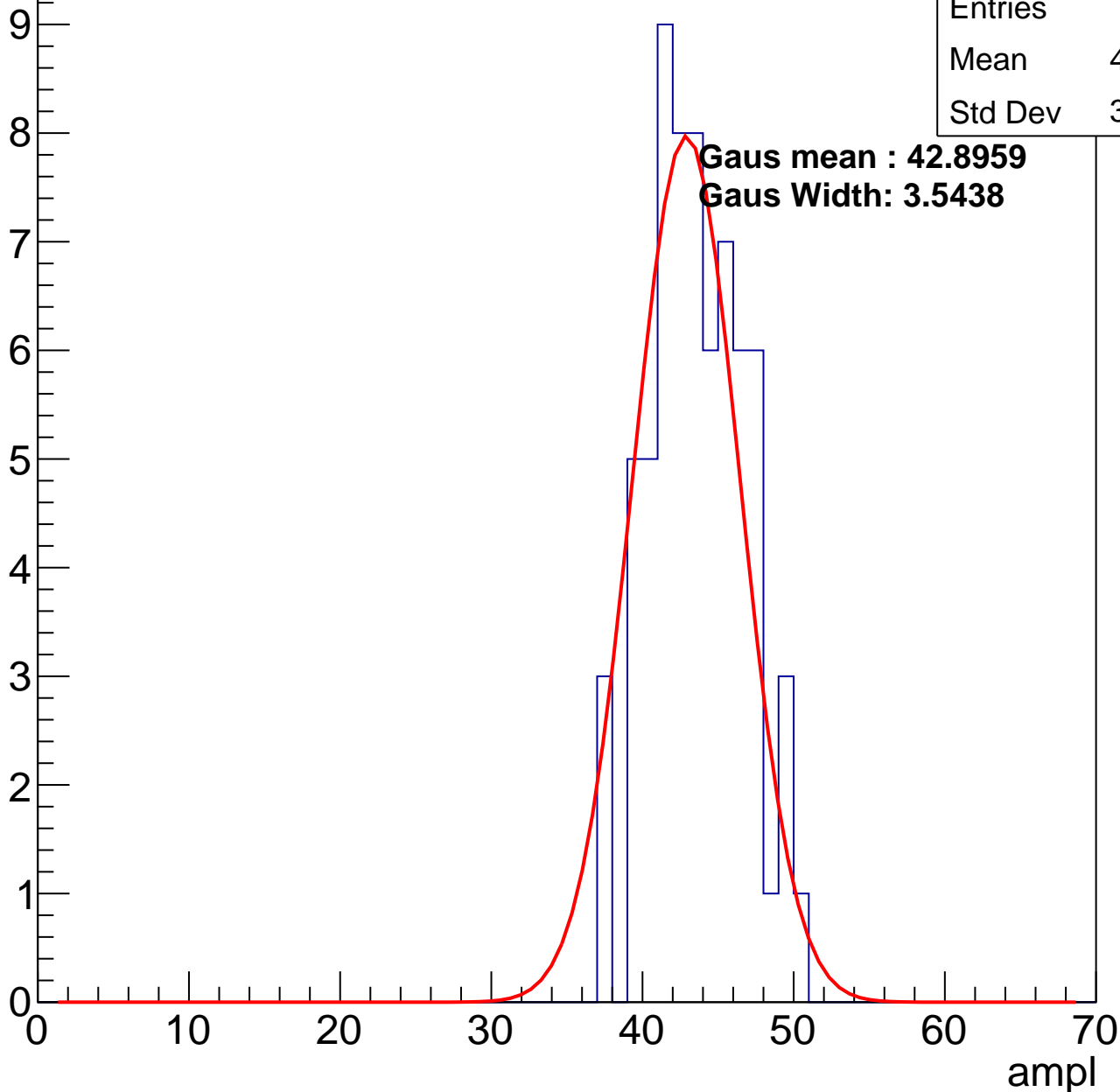
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.19
Std Dev	3.069

Gaus mean : 42.8959

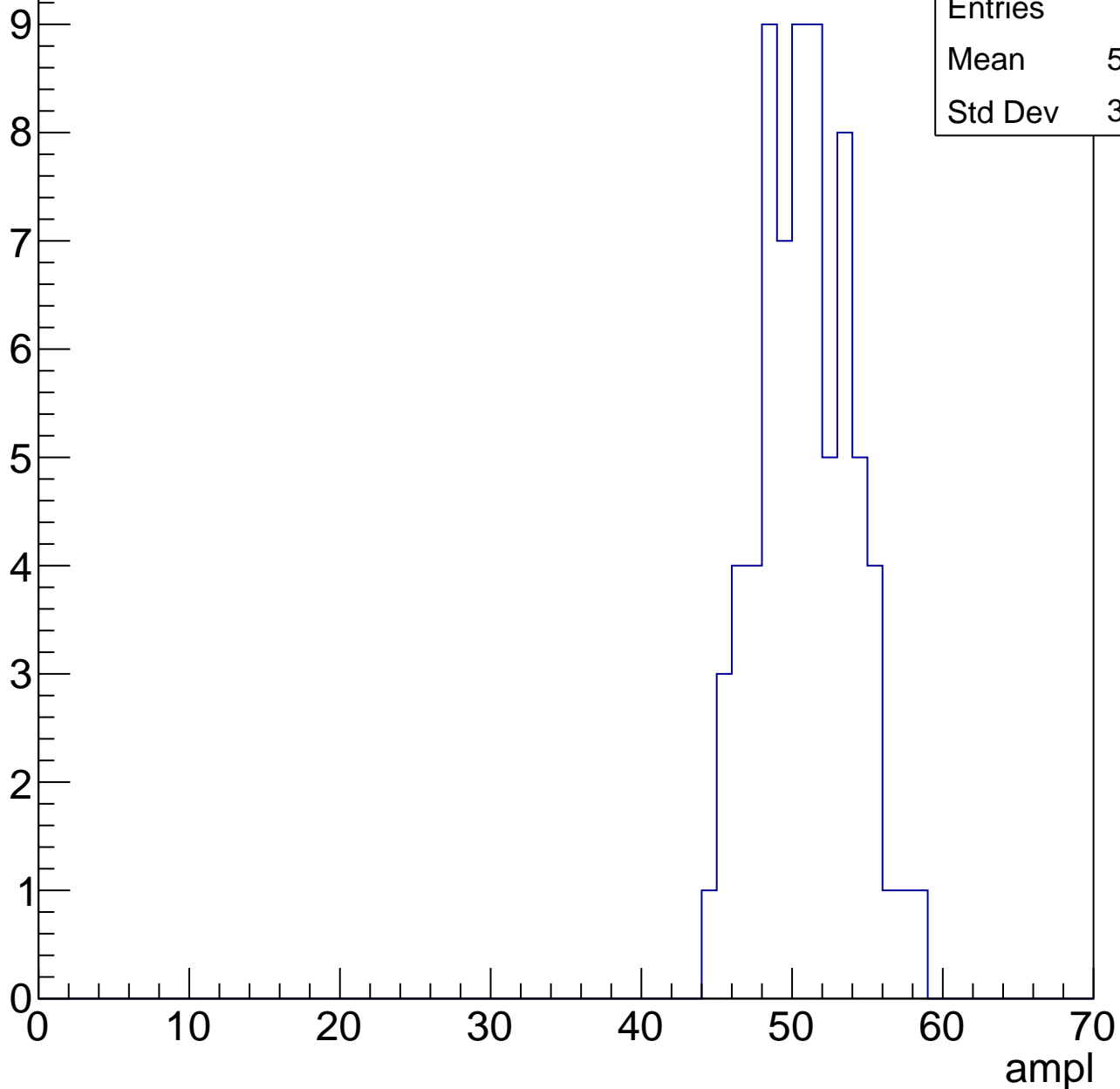
Gaus Width: 3.5438



B1L102S, U9-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

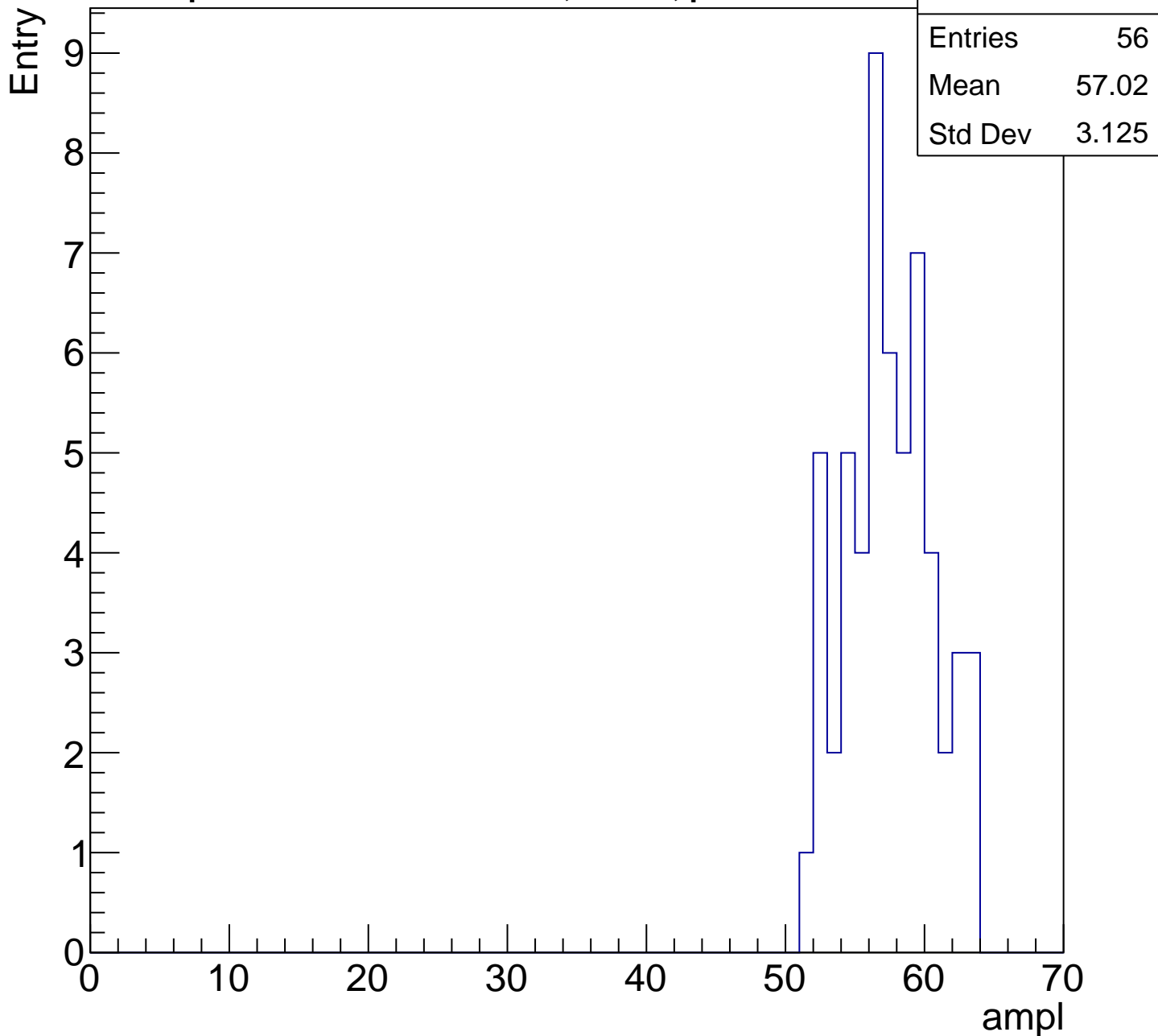
Entry



Entries	71
Mean	50.42
Std Dev	3.075

B1L102S, U9-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

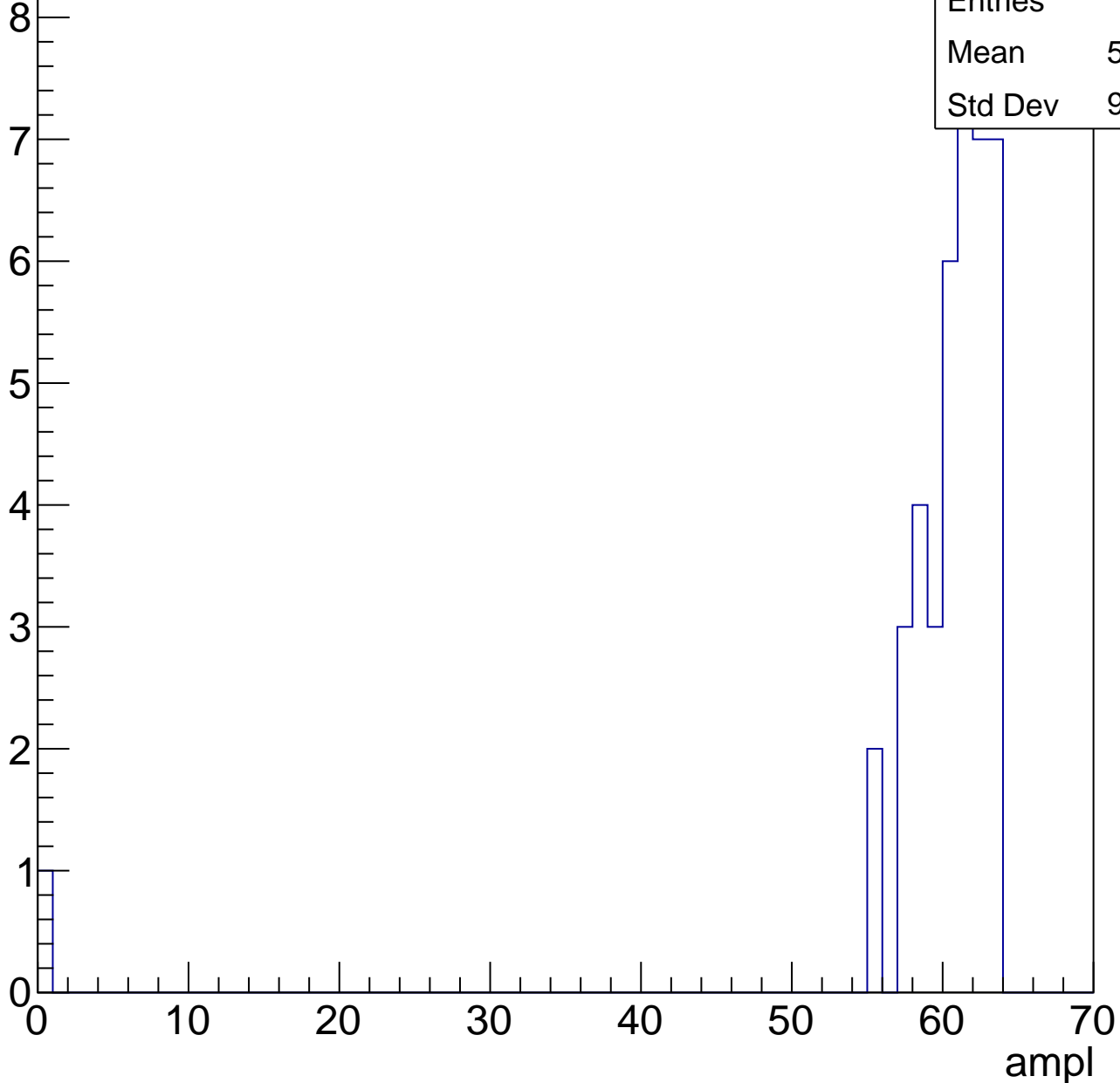


B1L102S, U9-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.85
Std Dev	9.552



B1L102S, U9-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch16, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	89
Mean	27.18
Std Dev	4.81

Gaus mean : 28.1984

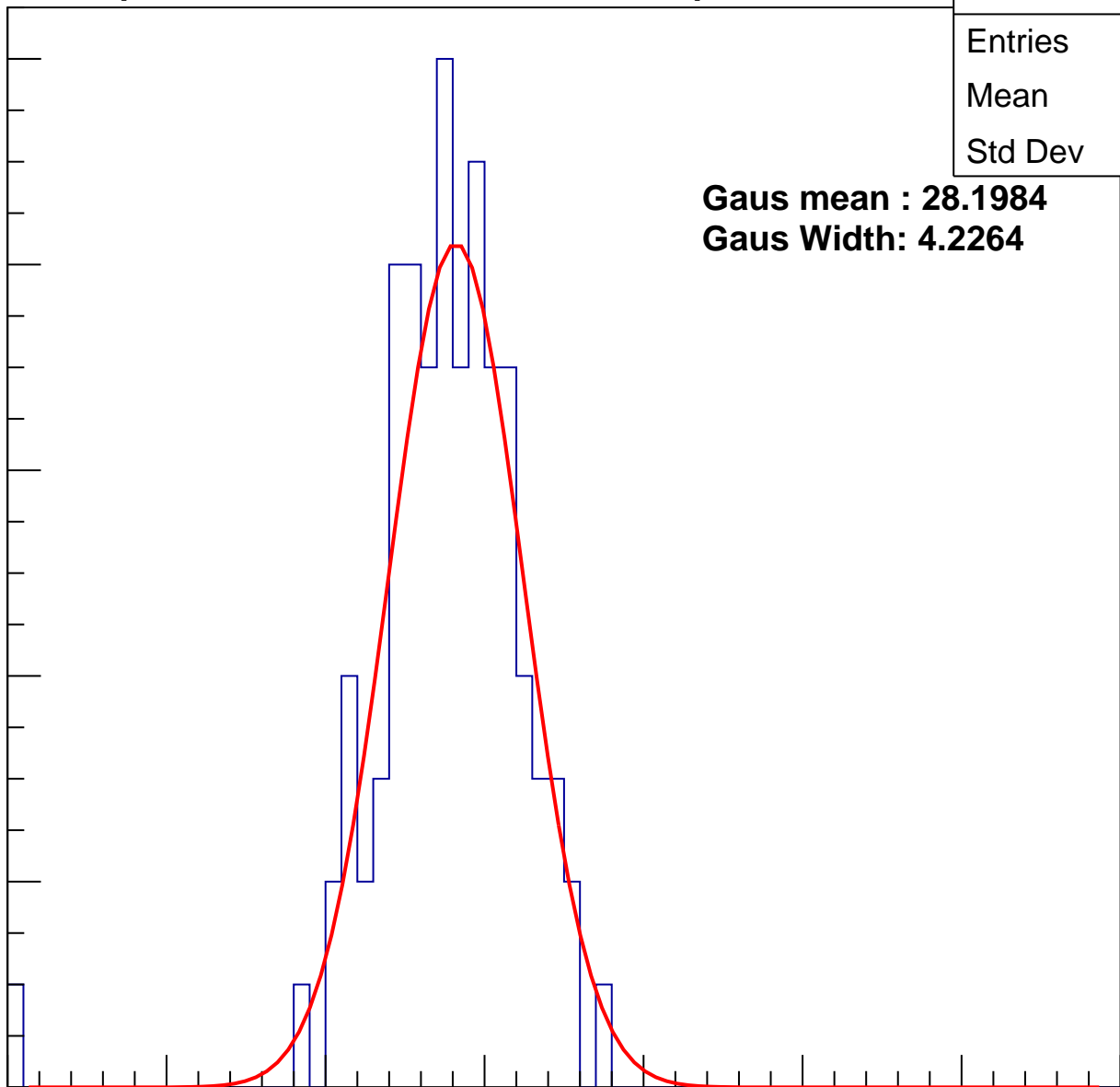
Gaus Width: 4.2264

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch16, adc1

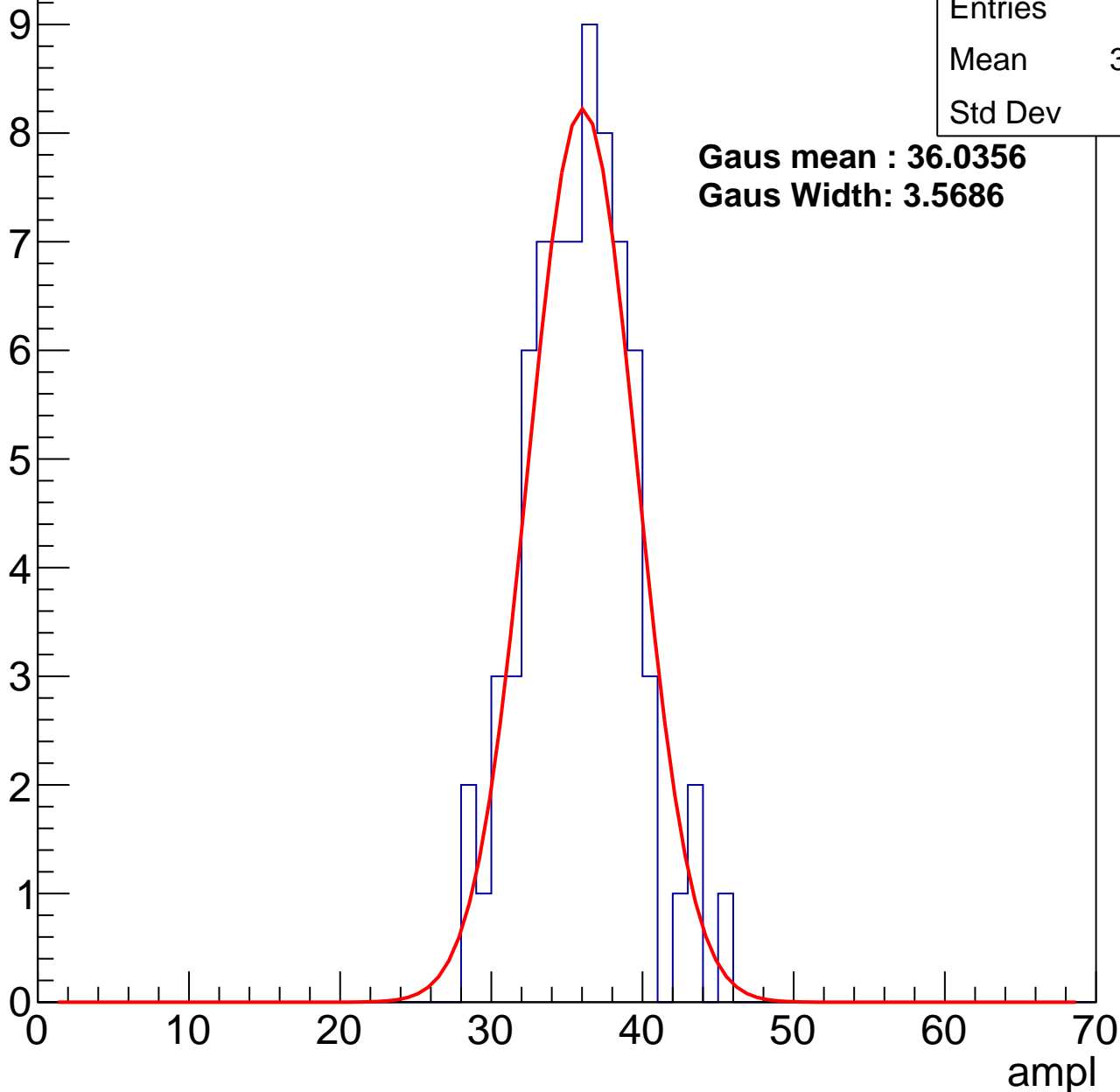
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.44
Std Dev	3.48

Gaus mean : 36.0356

Gaus Width: 3.5686



B1L102S, U9-ch16, adc2

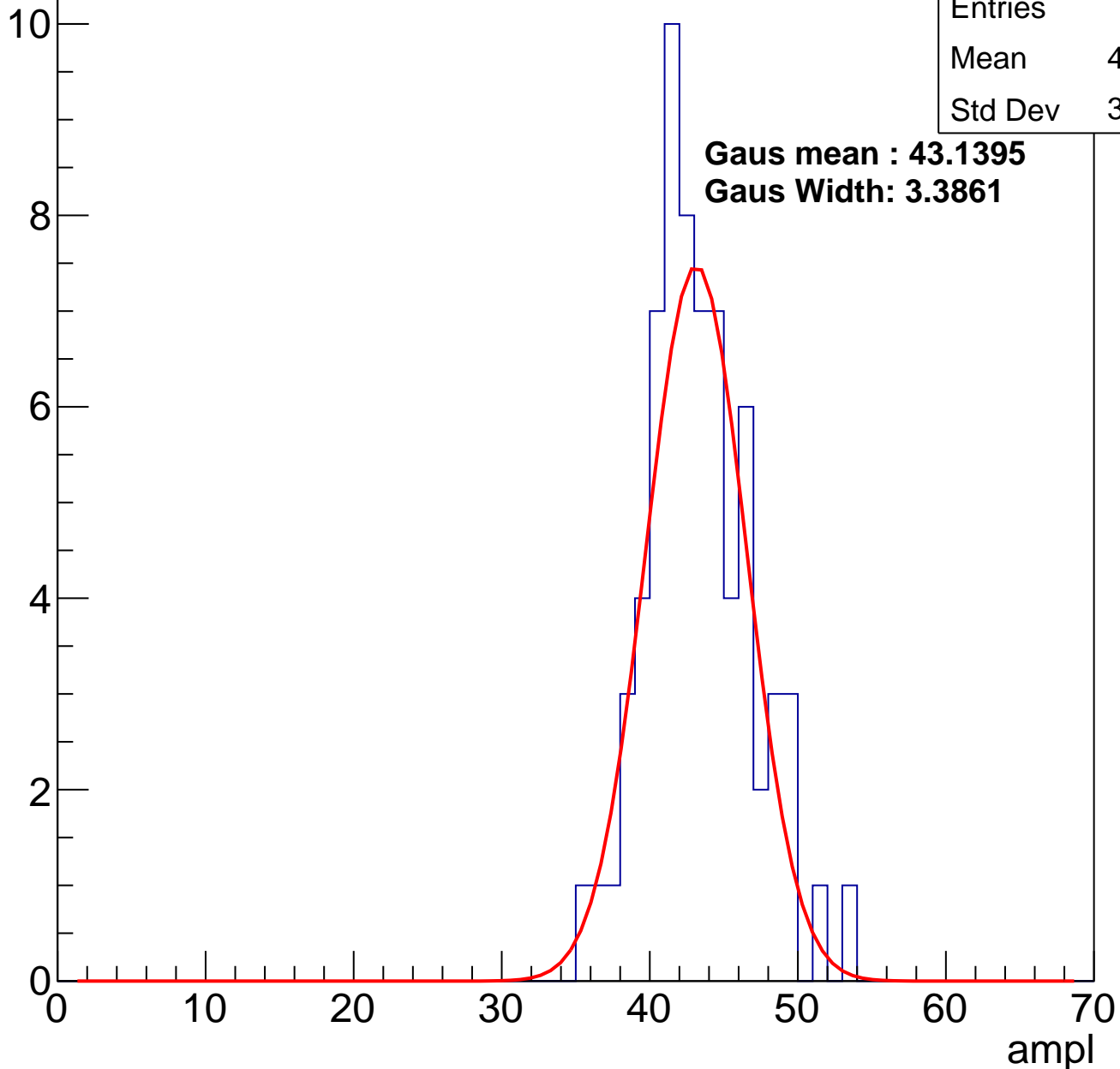
calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	42.87
Std Dev	3.518

Gaus mean : 43.1395

Gaus Width: 3.3861

Entry

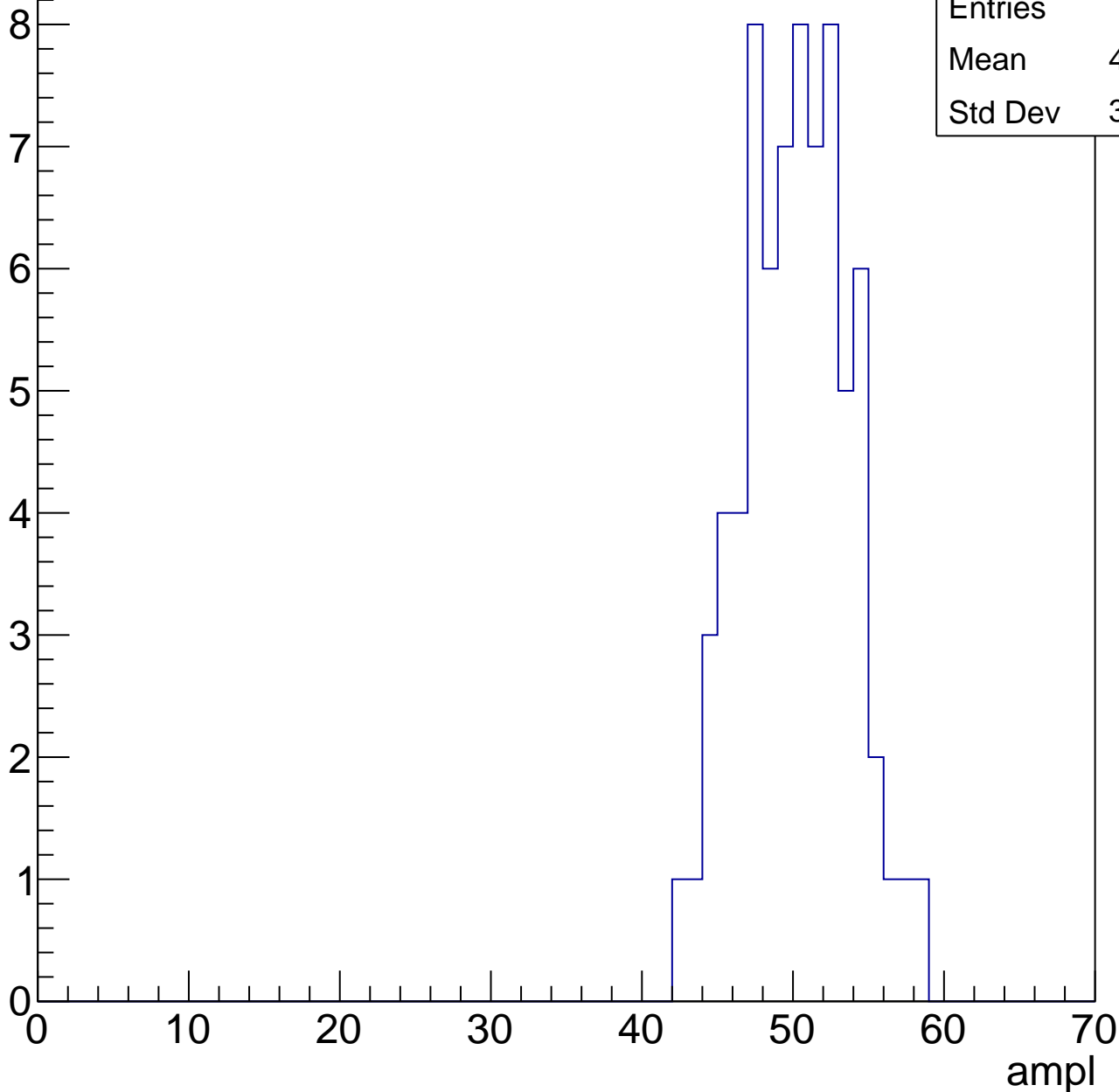


B1L102S, U9-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.74
Std Dev	3.436

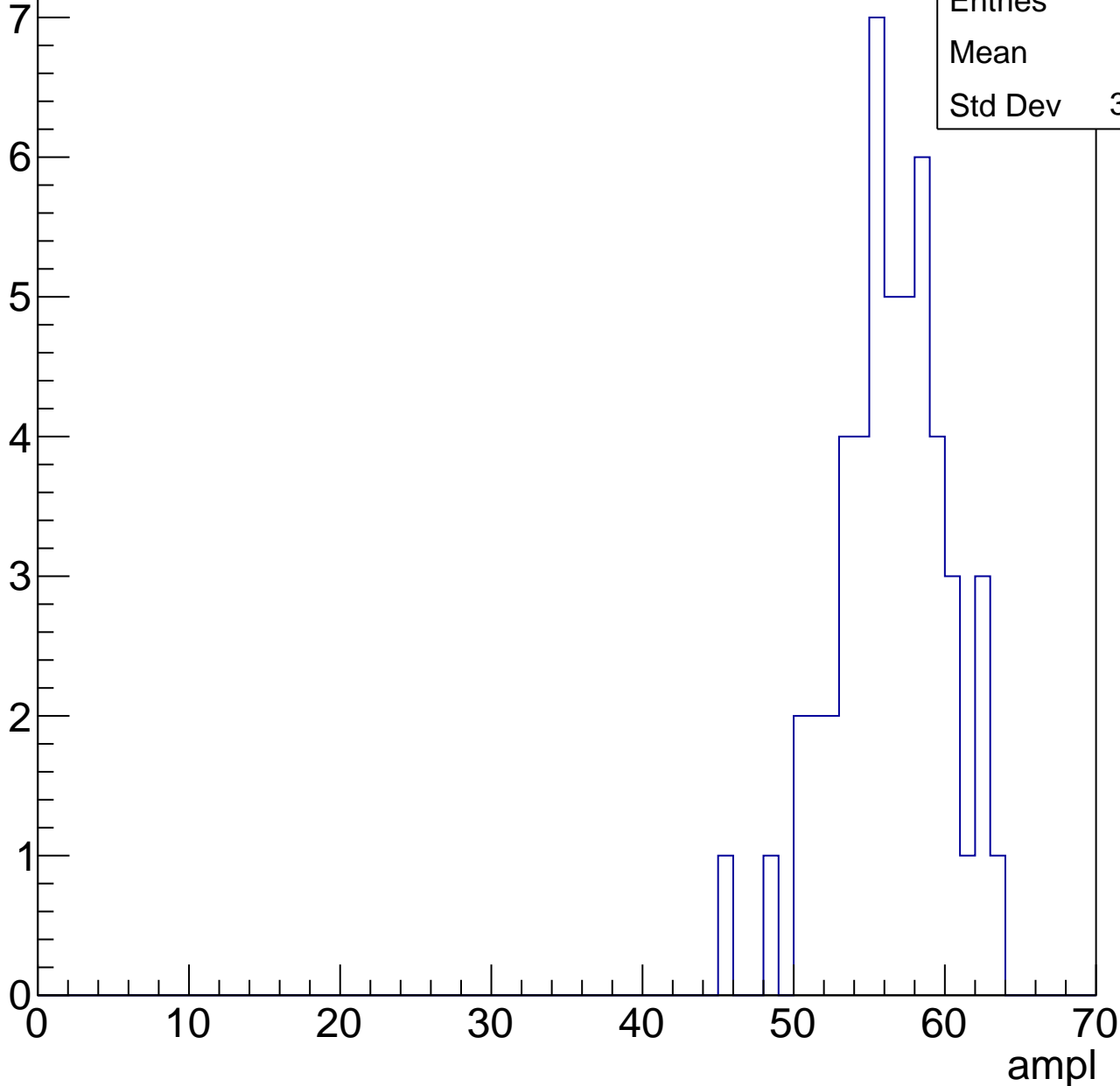


B1L102S, U9-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.9
Std Dev	3.685

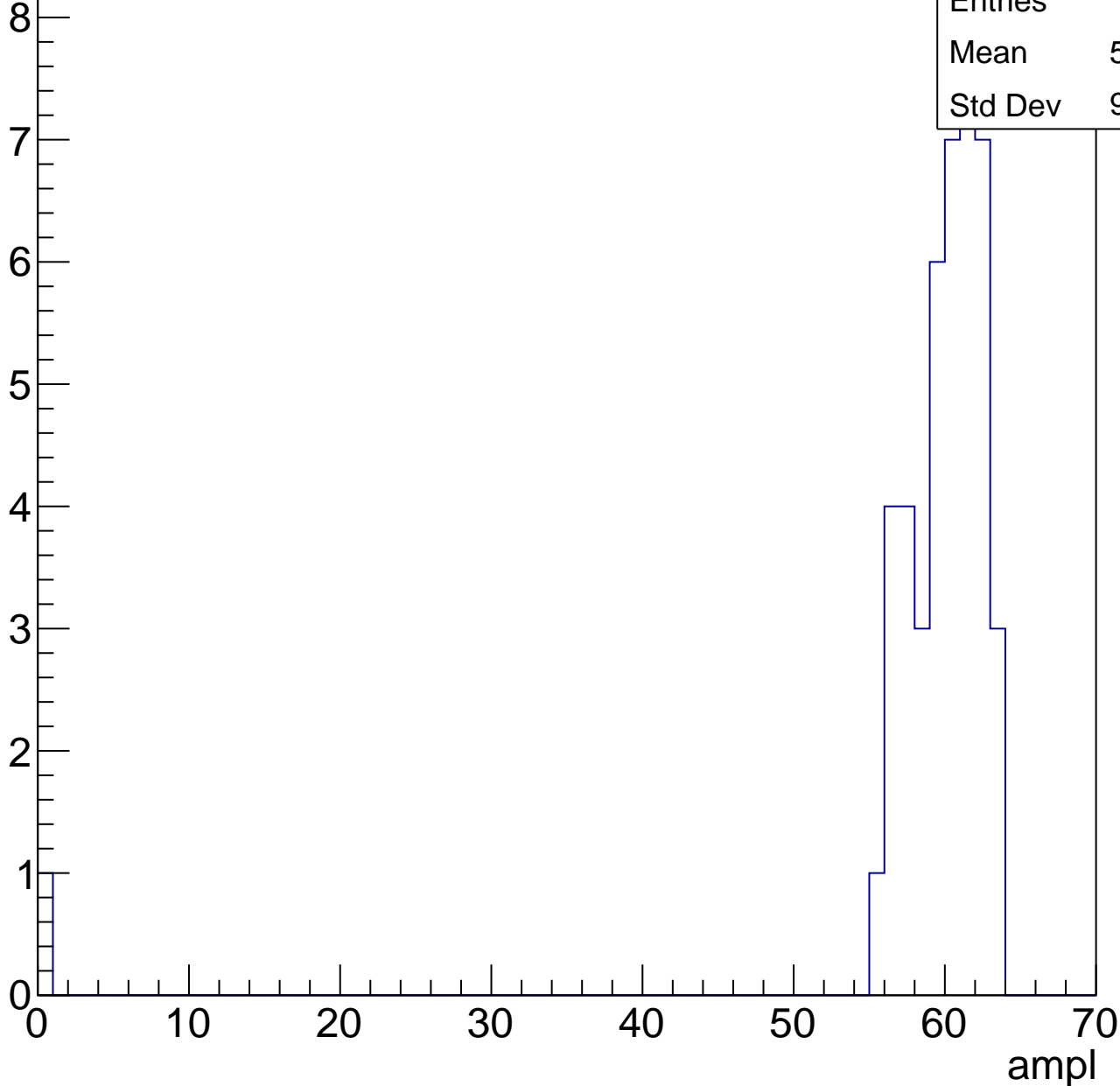


B1L102S, U9-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.32
Std Dev	9.147

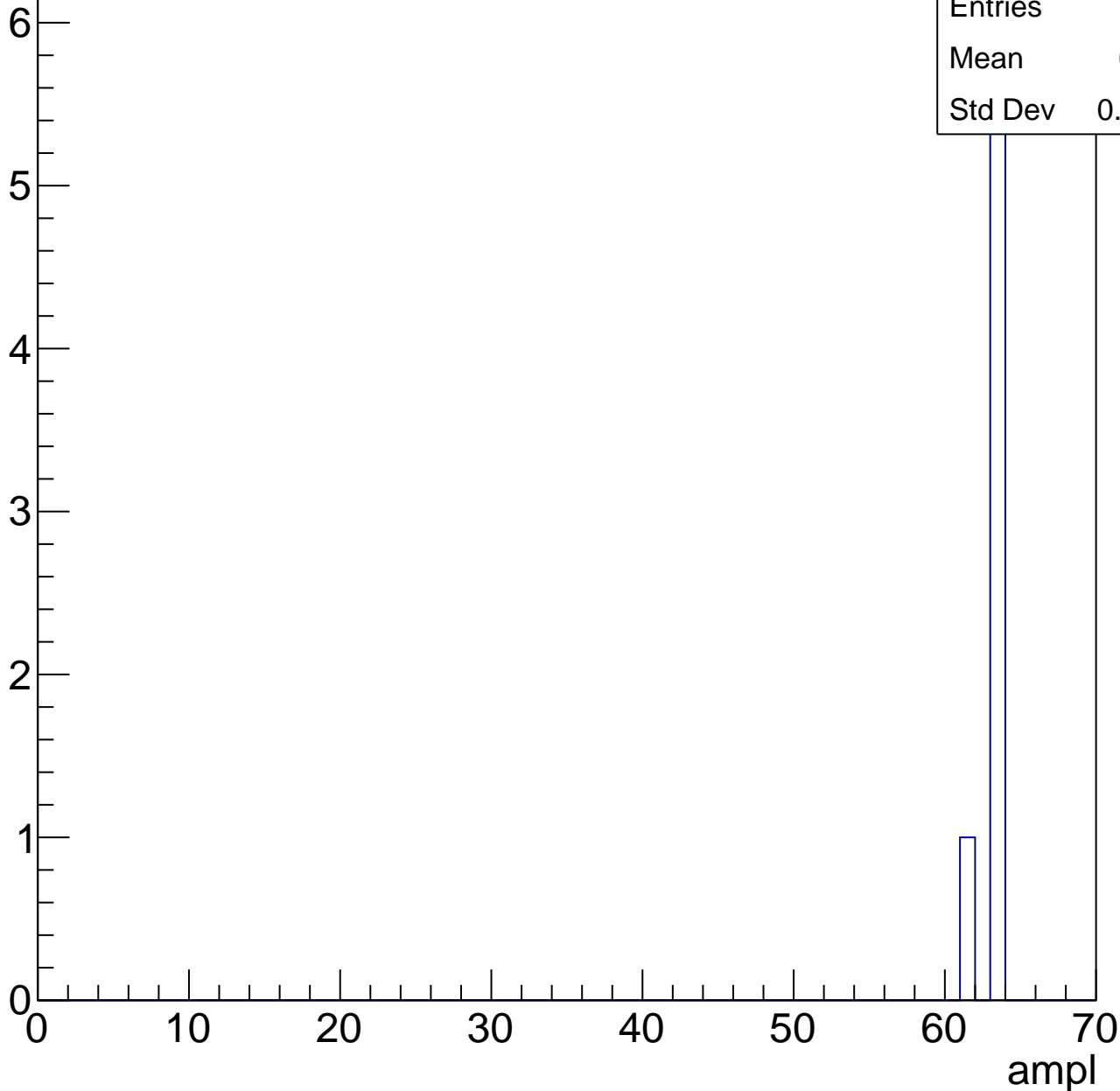


B1L102S, U9-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.71
Std Dev	0.6999



B1L102S, U9-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch17, adc0

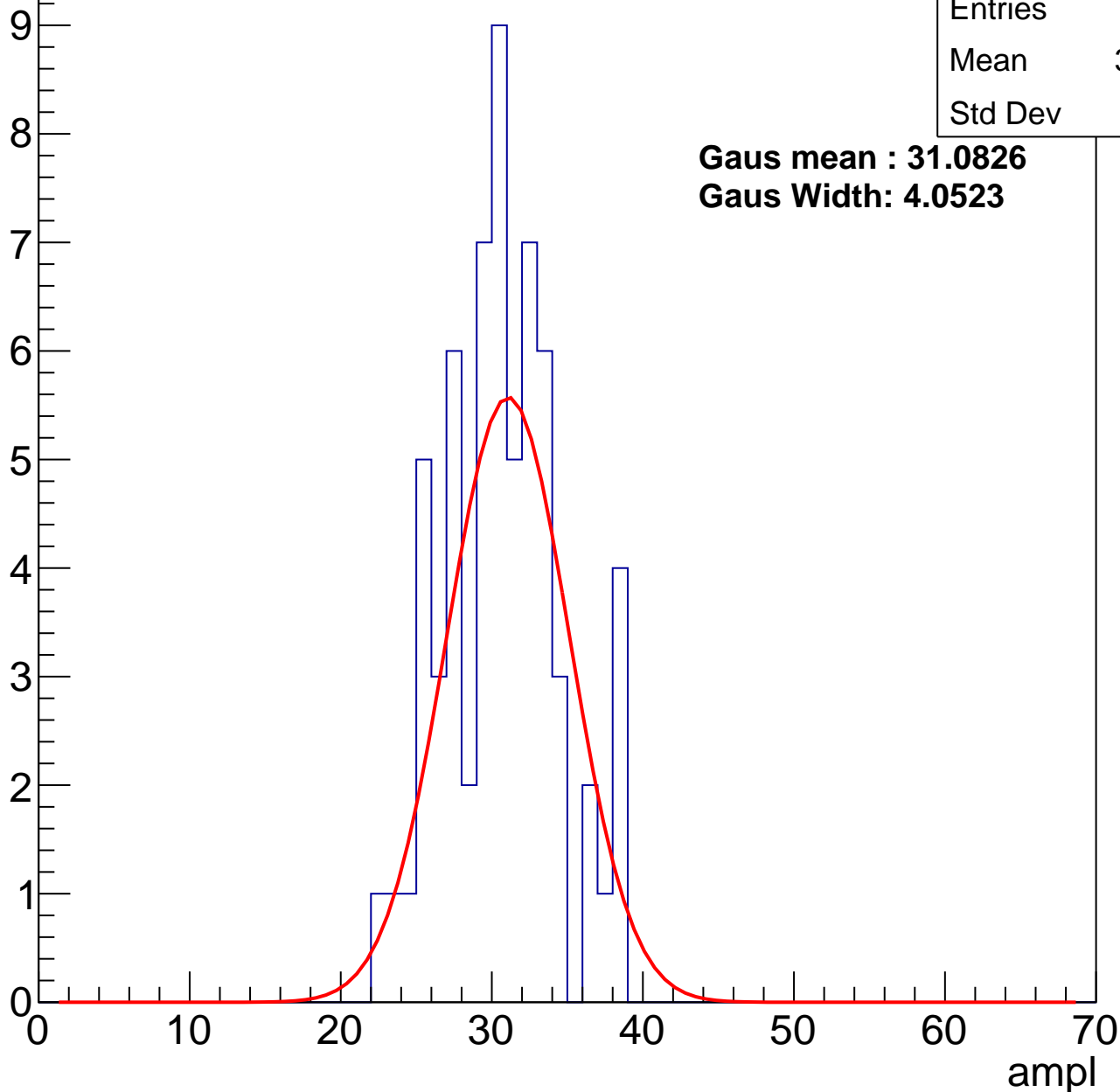
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	30.21
Std Dev	3.78

Gaus mean : 31.0826

Gaus Width: 4.0523



B1L102S, U9-ch17, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	35.79
Std Dev	3.446

Gaus mean : 36.4350

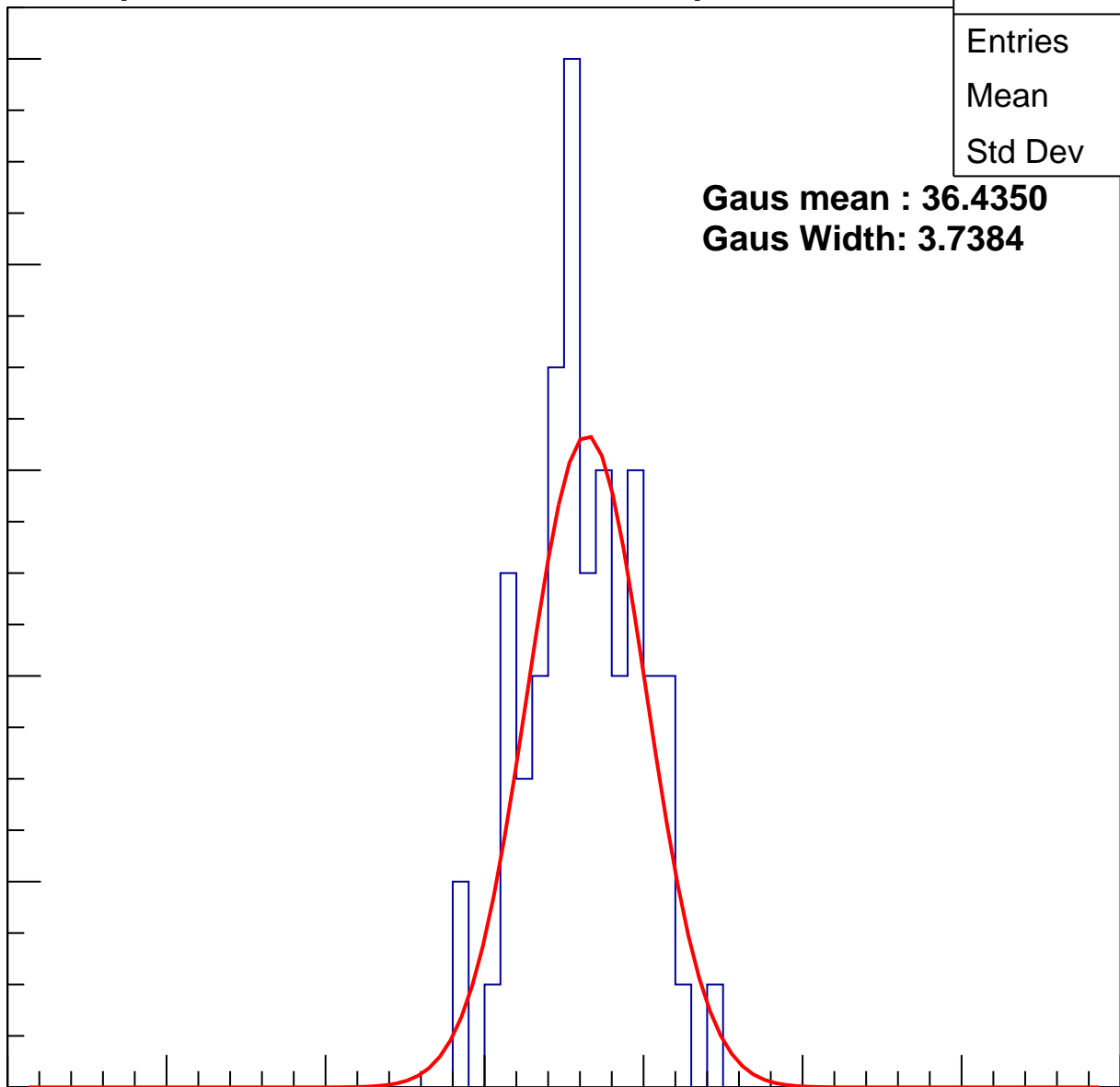
Gaus Width: 3.7384

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch17, adc2

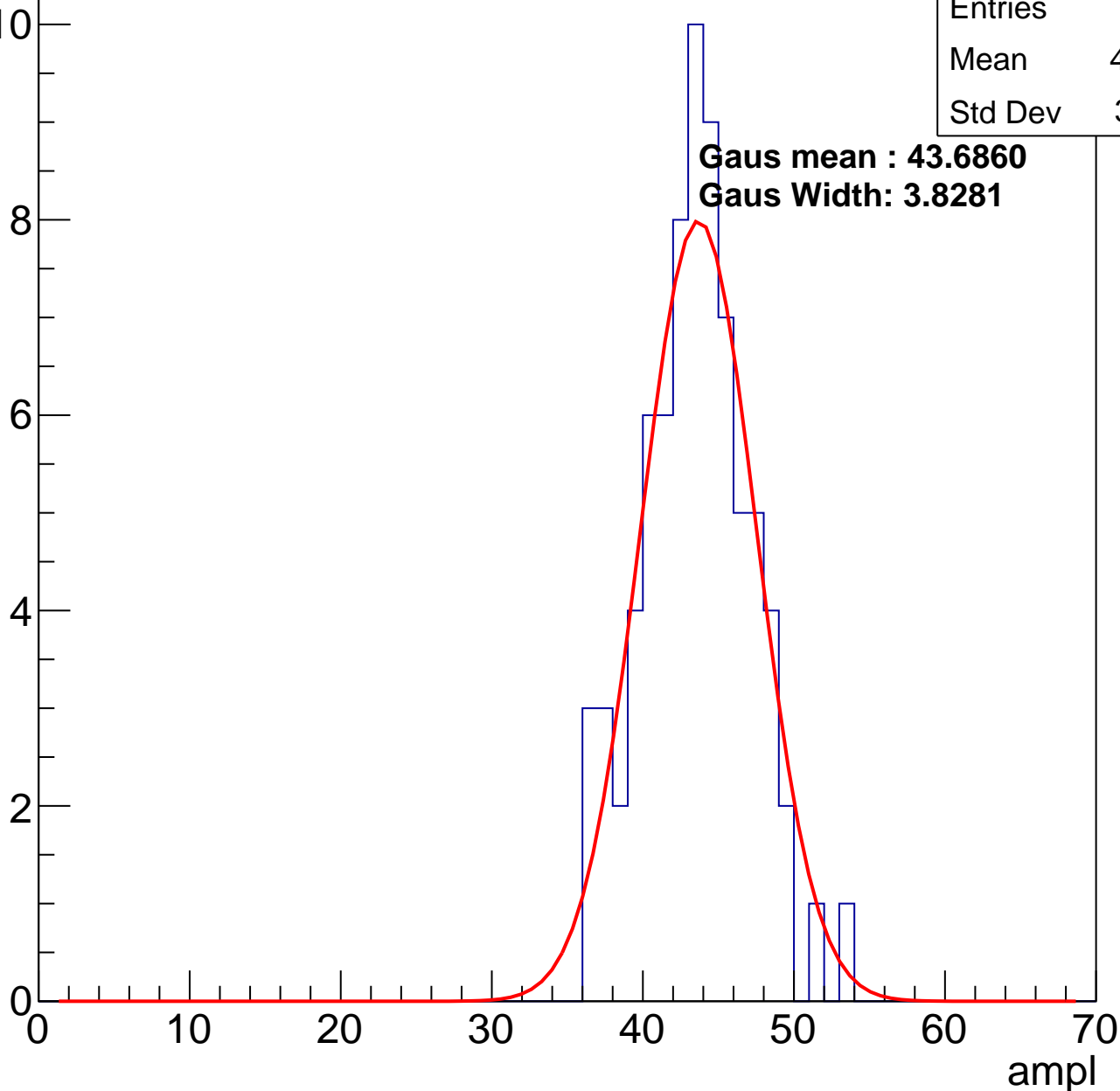
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.07
Std Dev	3.541

Gaus mean : 43.6860

Gaus Width: 3.8281

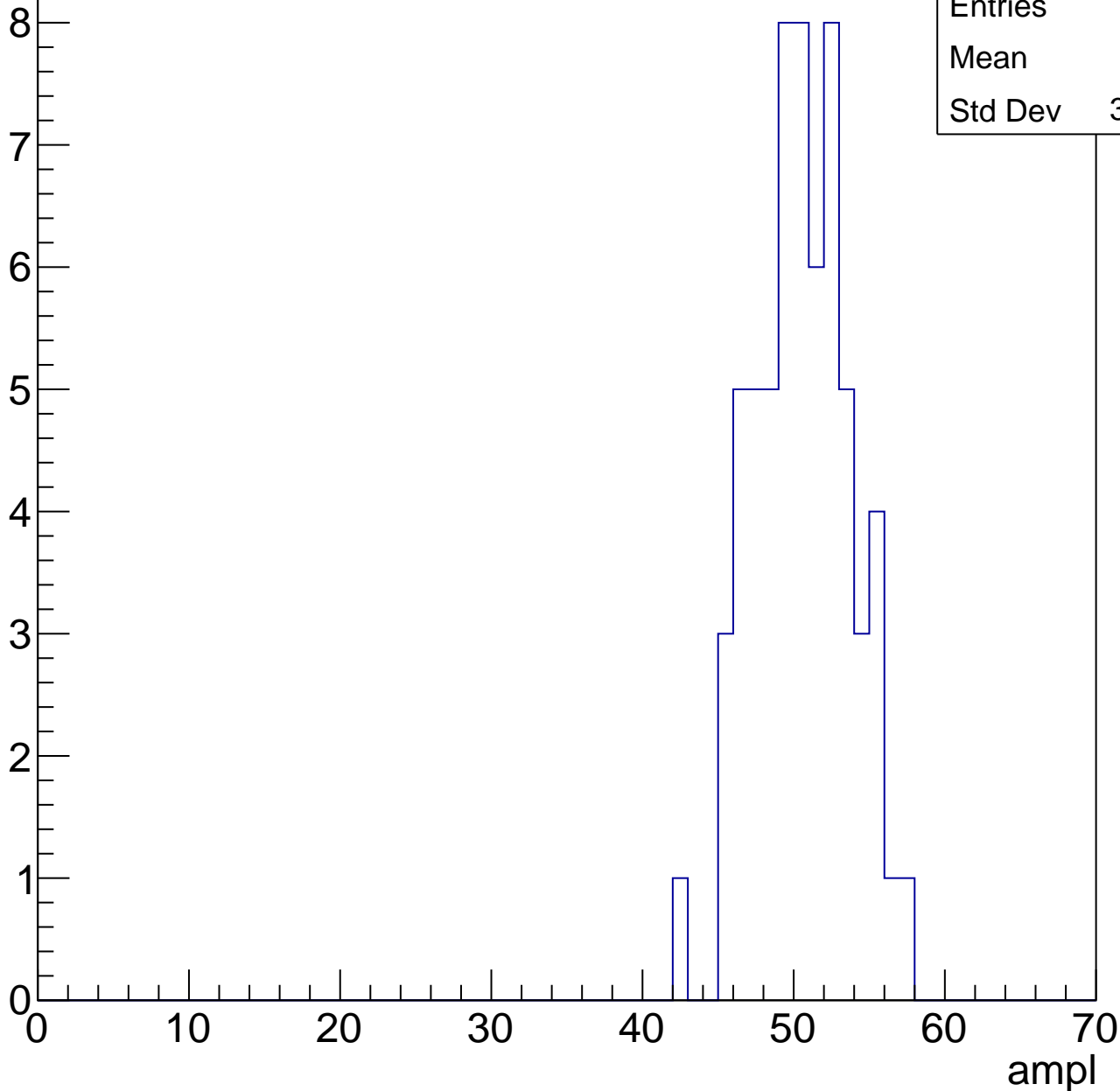


B1L102S, U9-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.1
Std Dev	3.105

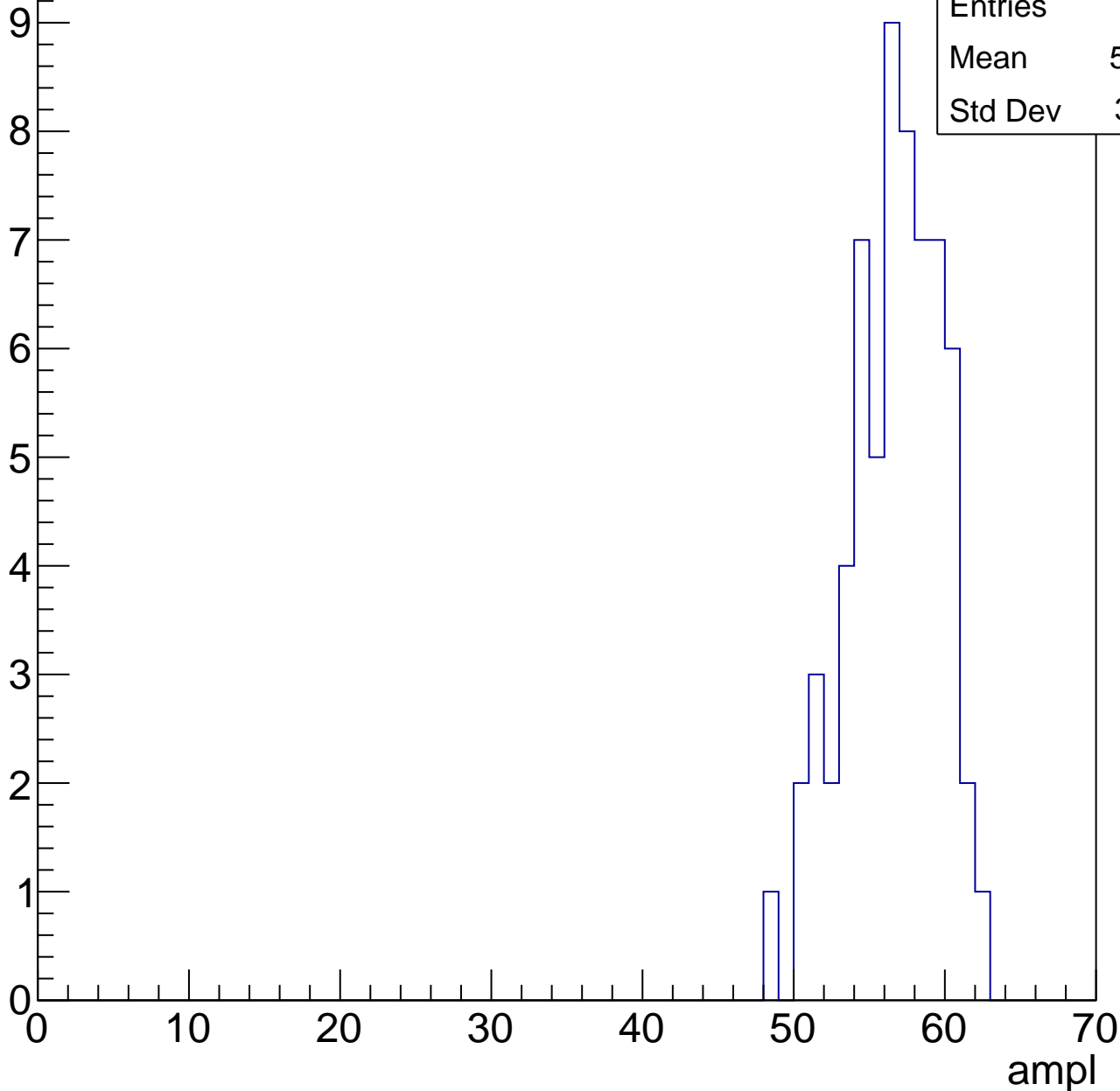


B1L102S, U9-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	56.14
Std Dev	3.041



B1L102S, U9-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	36
Mean	59.31
Std Dev	10.2

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

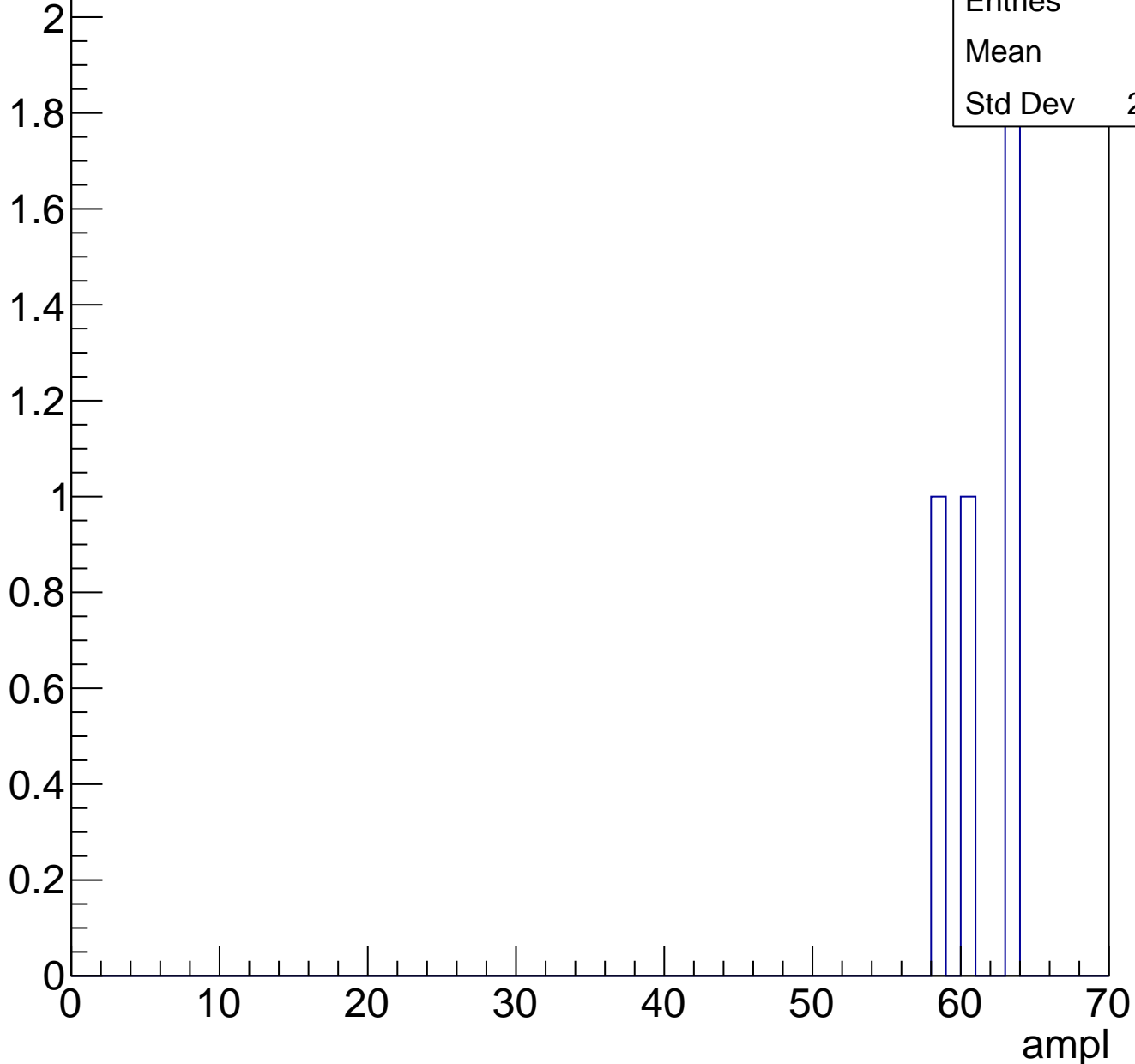
8

9

B1L102S, U9-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch18, adc0

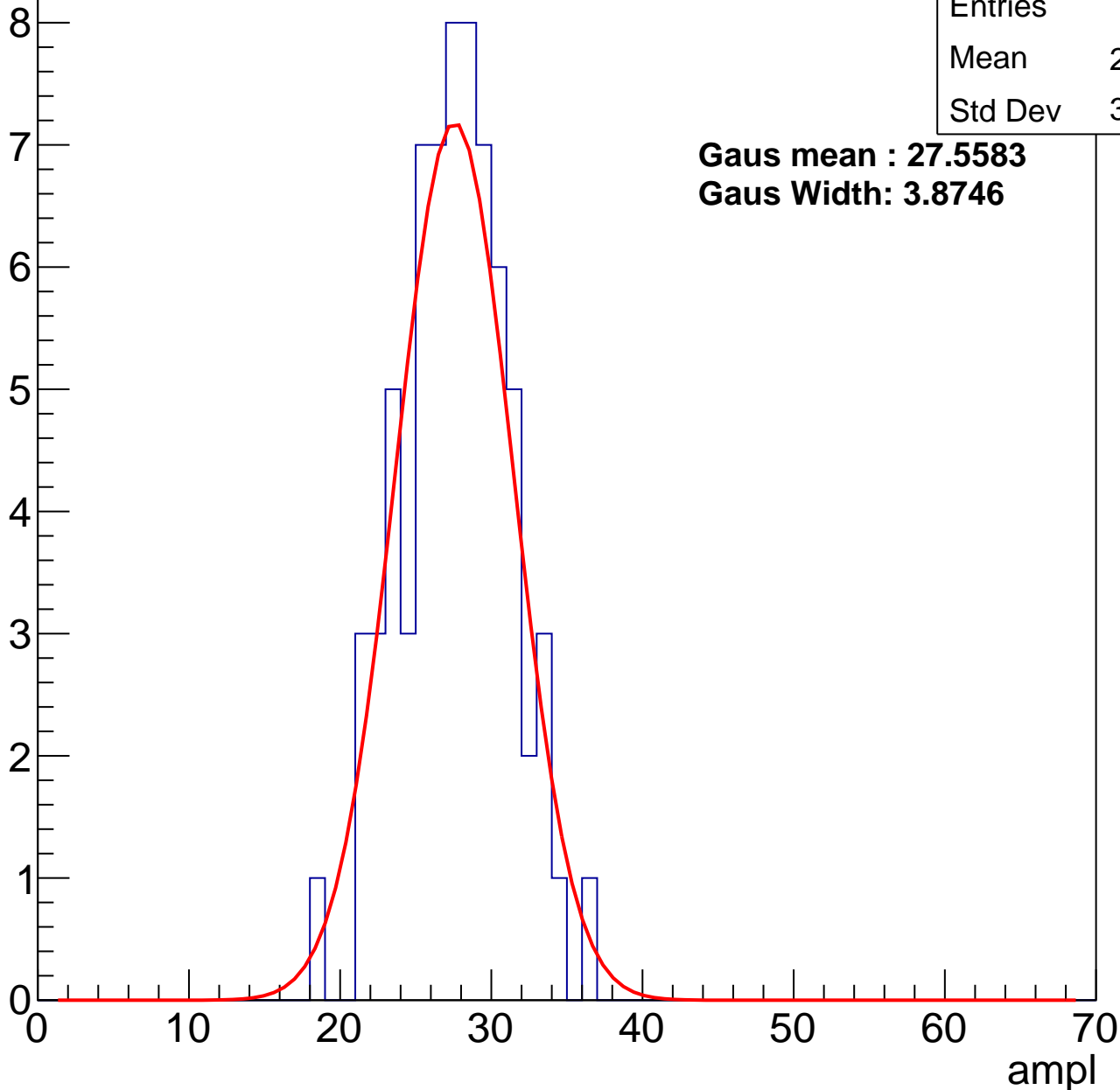
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.17
Std Dev	3.509

Gaus mean : 27.5583

Gaus Width: 3.8746



B1L102S, U9-ch18, adc1

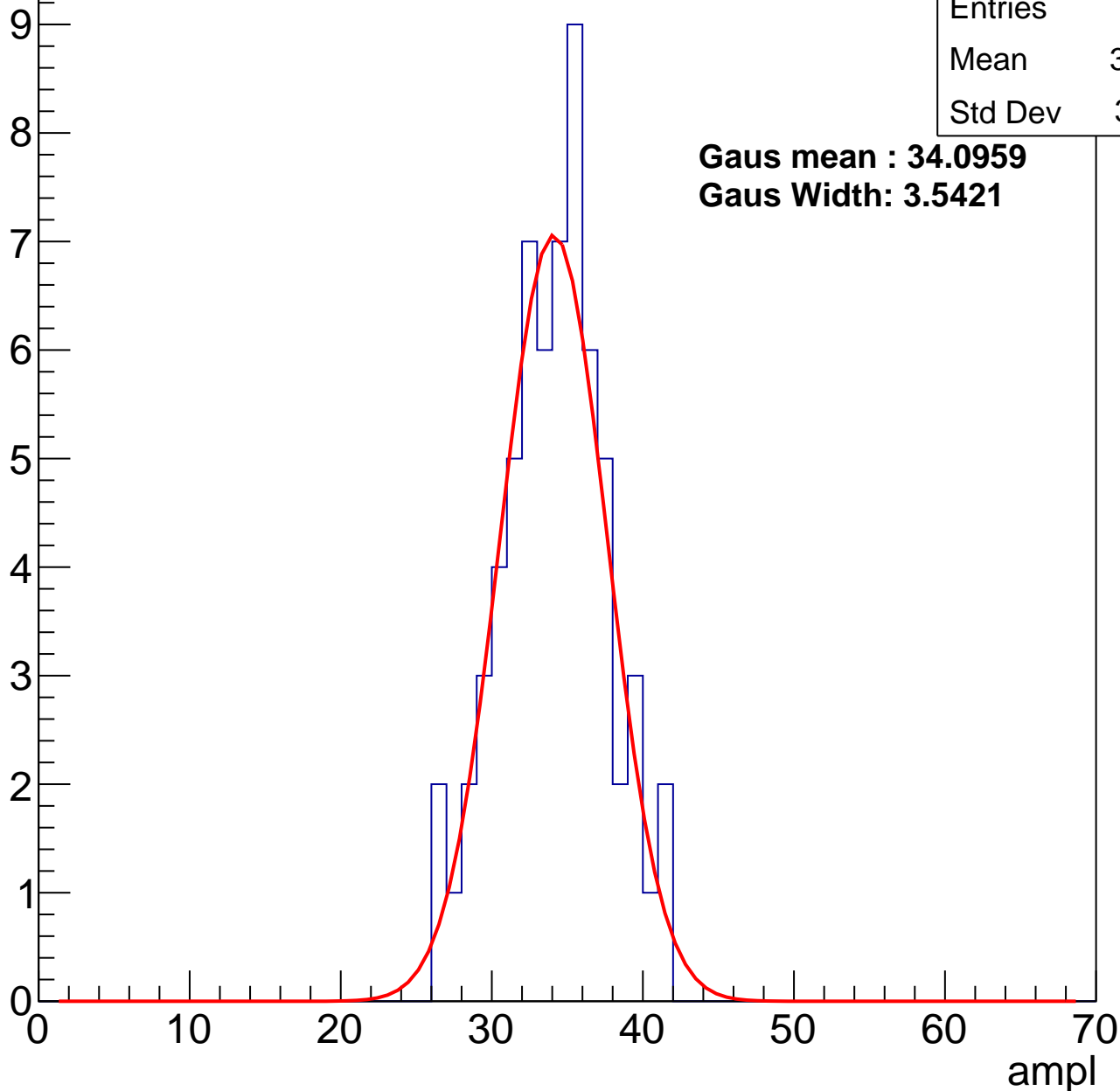
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	33.66
Std Dev	3.461

Gaus mean : 34.0959

Gaus Width: 3.5421



B1L102S, U9-ch18, adc2

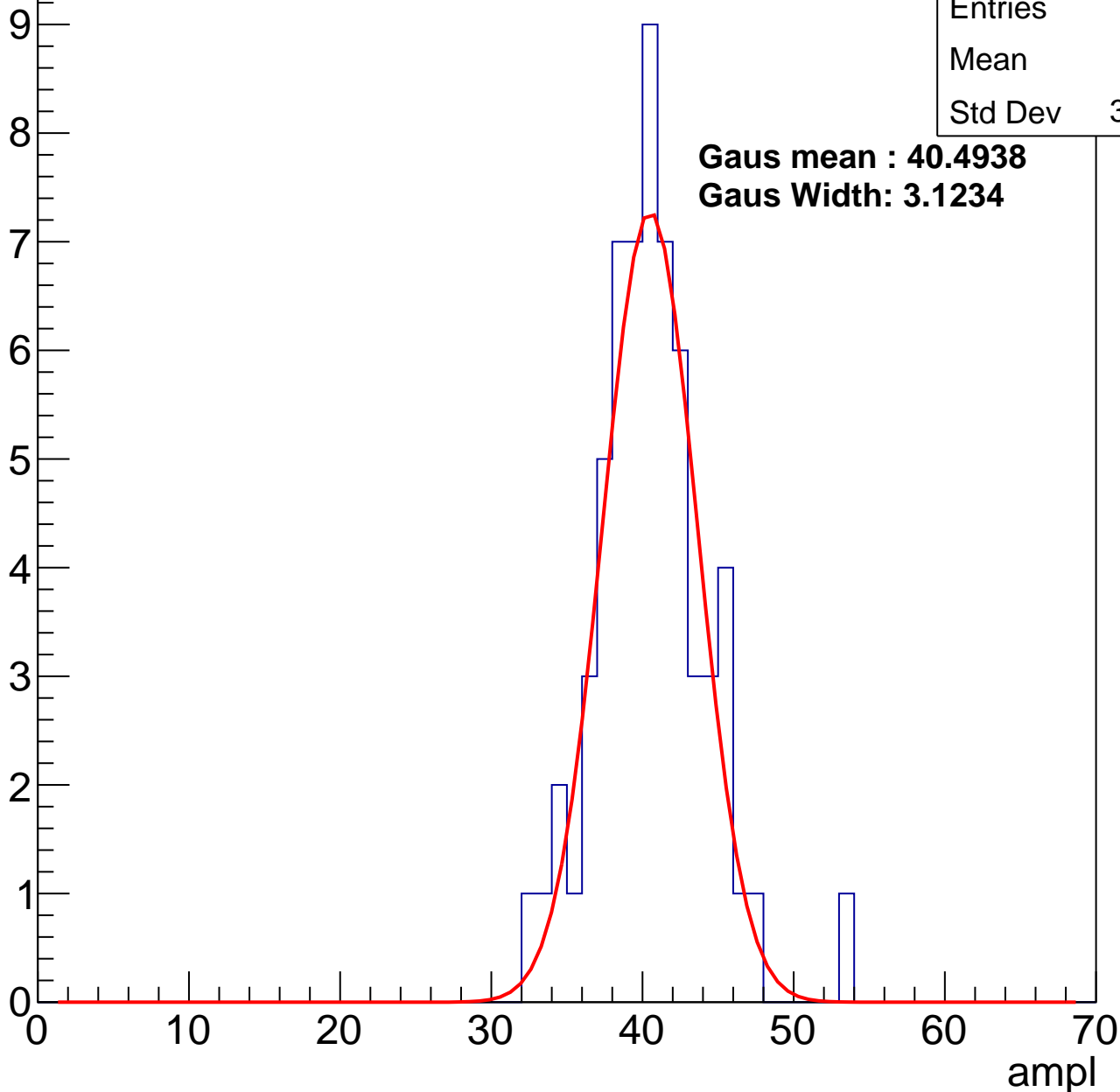
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	40.1
Std Dev	3.586

Gaus mean : 40.4938

Gaus Width: 3.1234

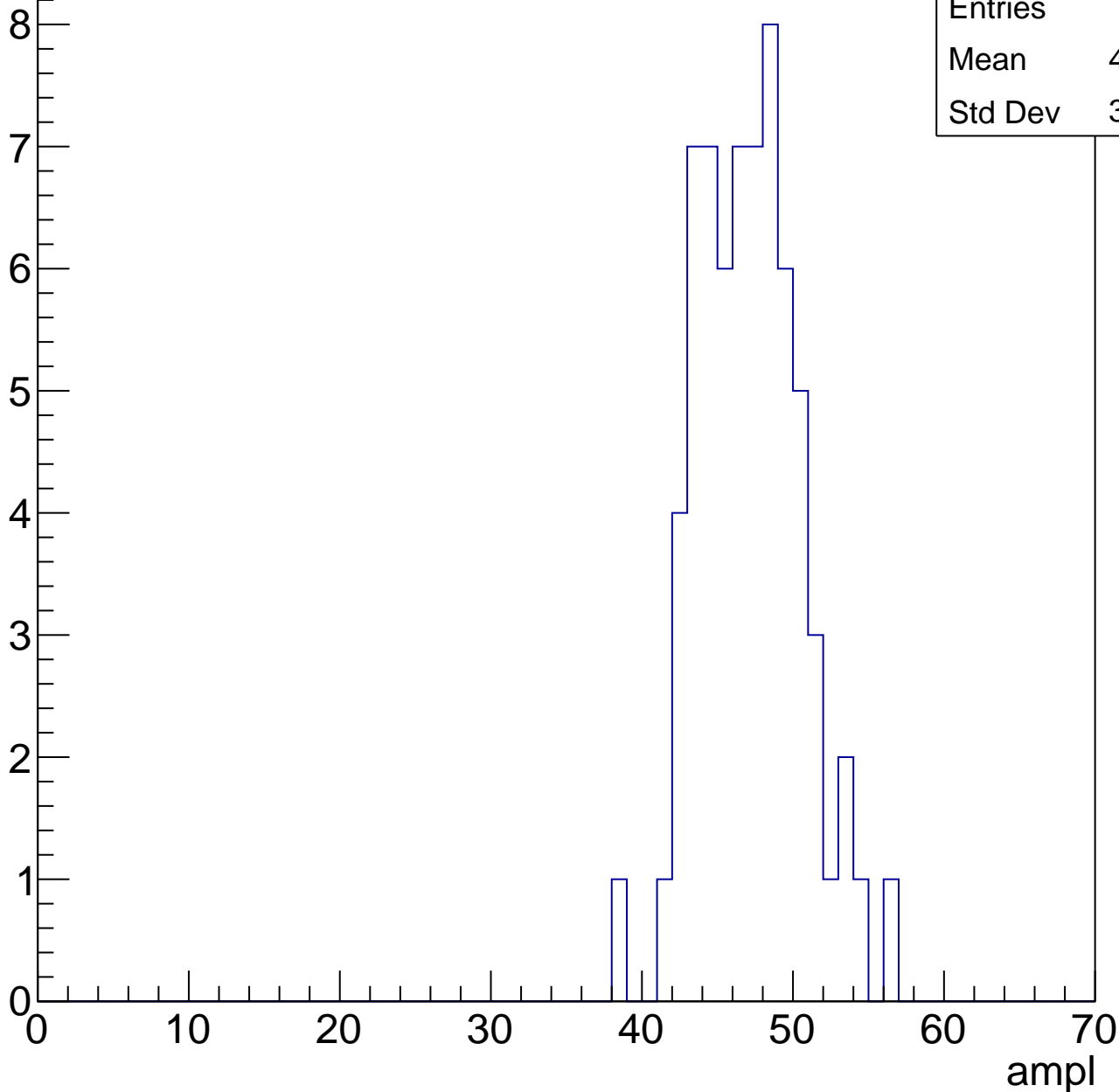


B1L102S, U9-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

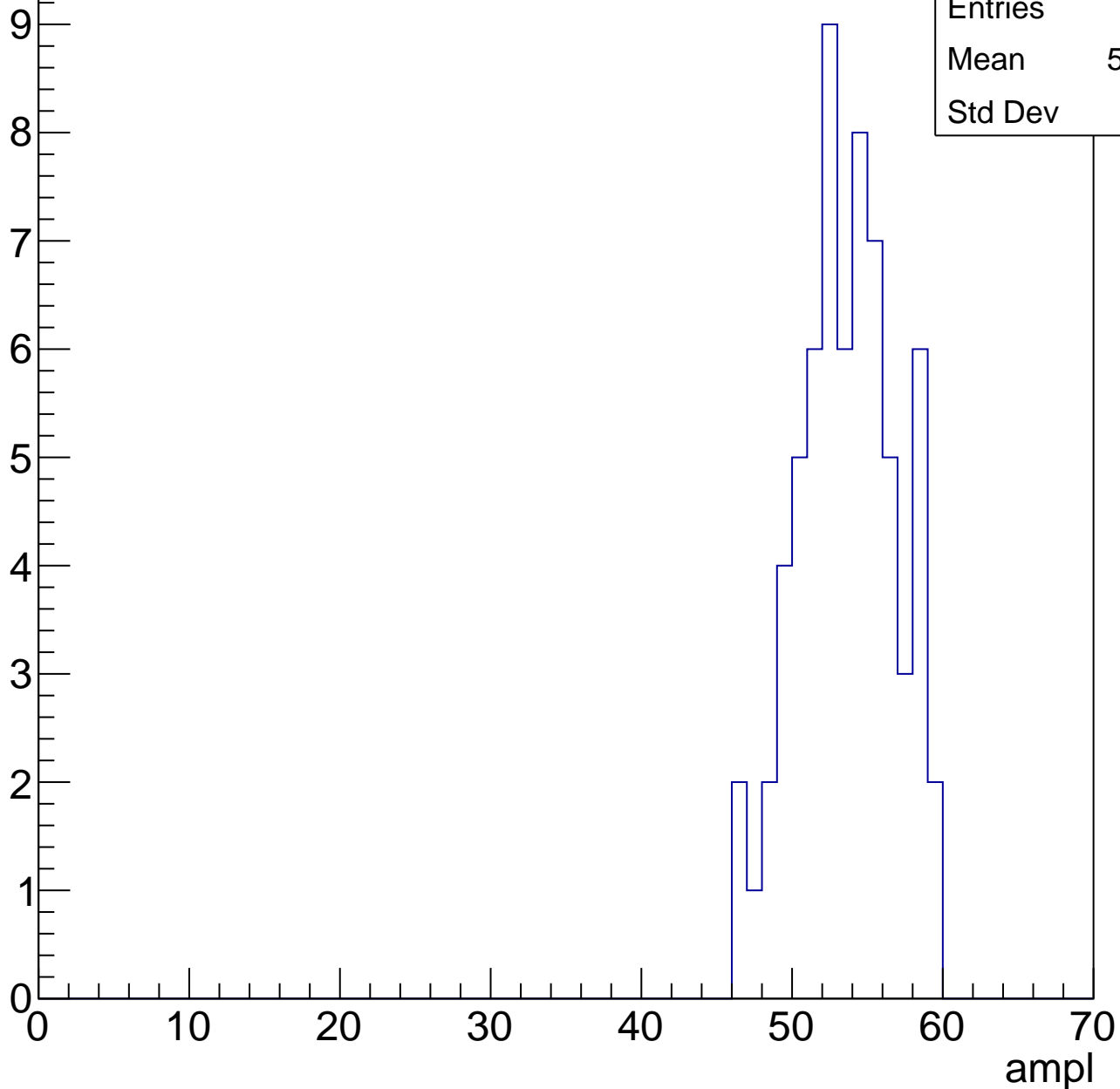
Entries	67
Mean	46.66
Std Dev	3.384



B1L102S, U9-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	66
Mean	53.14
Std Dev	3.2

B1L102S, U9-ch18, adc5

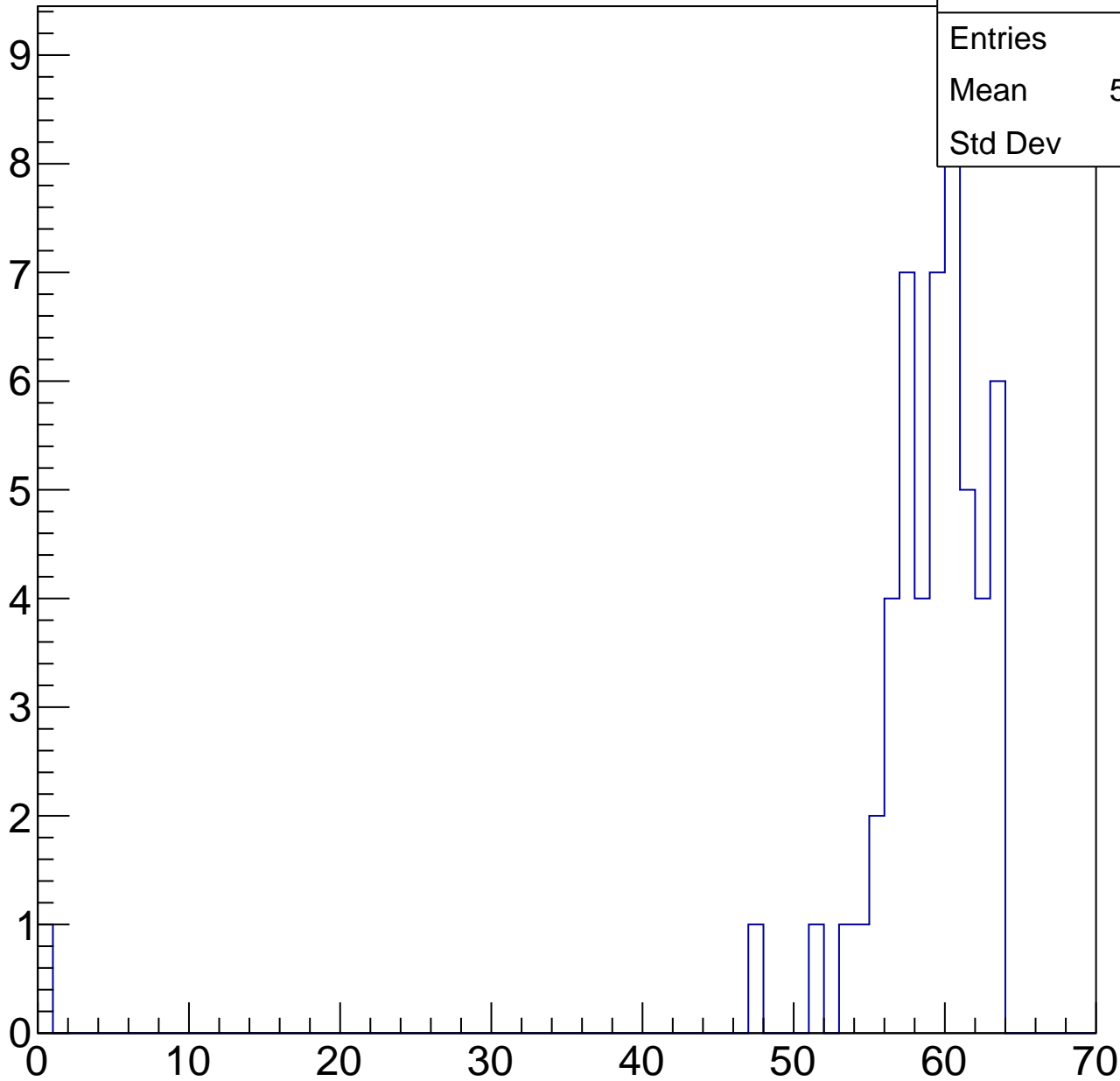
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	57.62
Std Dev	8.59

ampl

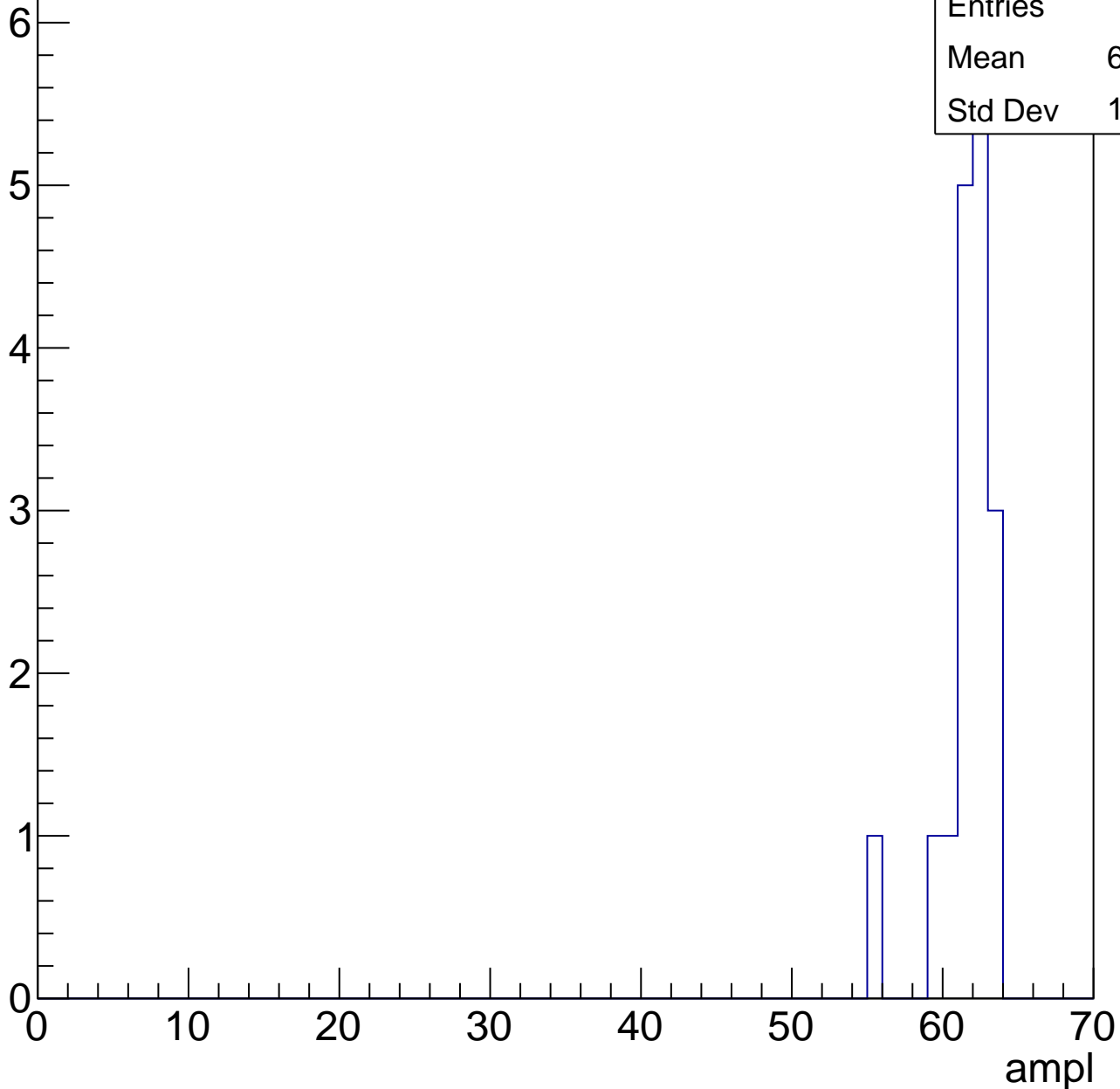


B1L102S, U9-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

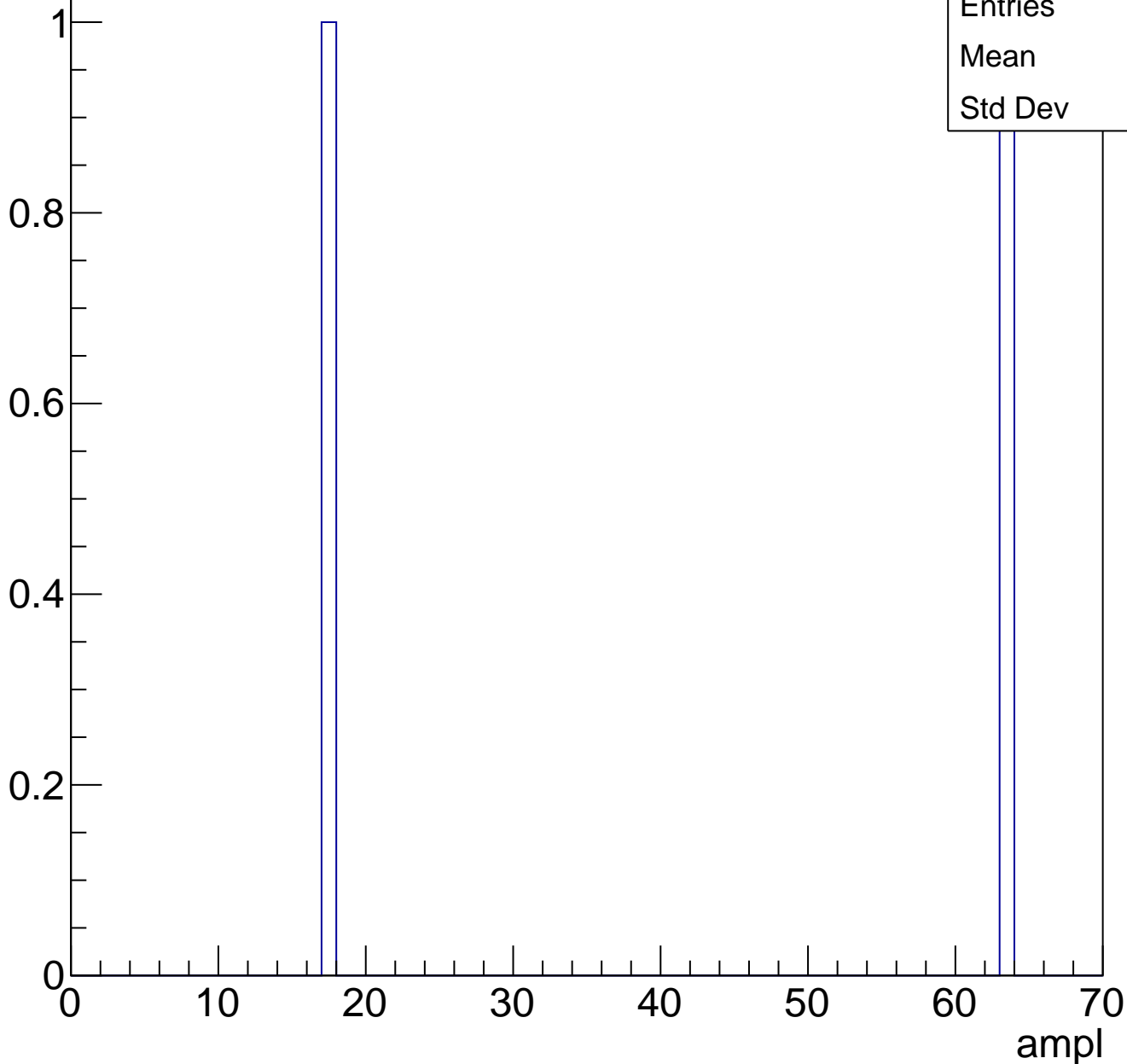
Entries	17
Mean	61.18
Std Dev	1.855



B1L102S, U9-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch19, adc0

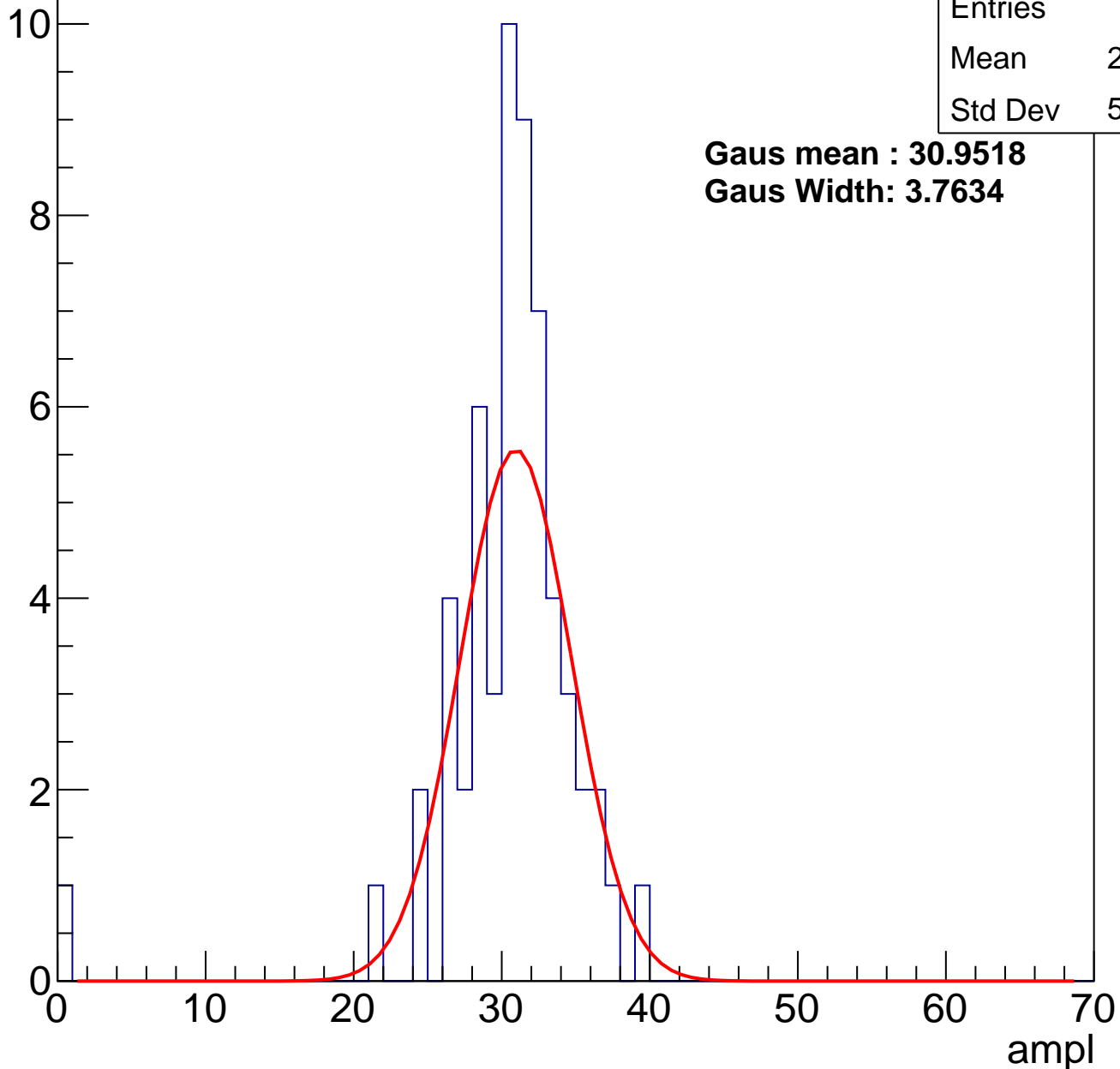
calib_packv5_042523_0143.root, FC#11, port A2

Entries	58
Mean	29.95
Std Dev	5.144

Gaus mean : 30.9518

Gaus Width: 3.7634

Entry



B1L102S, U9-ch19, adc1

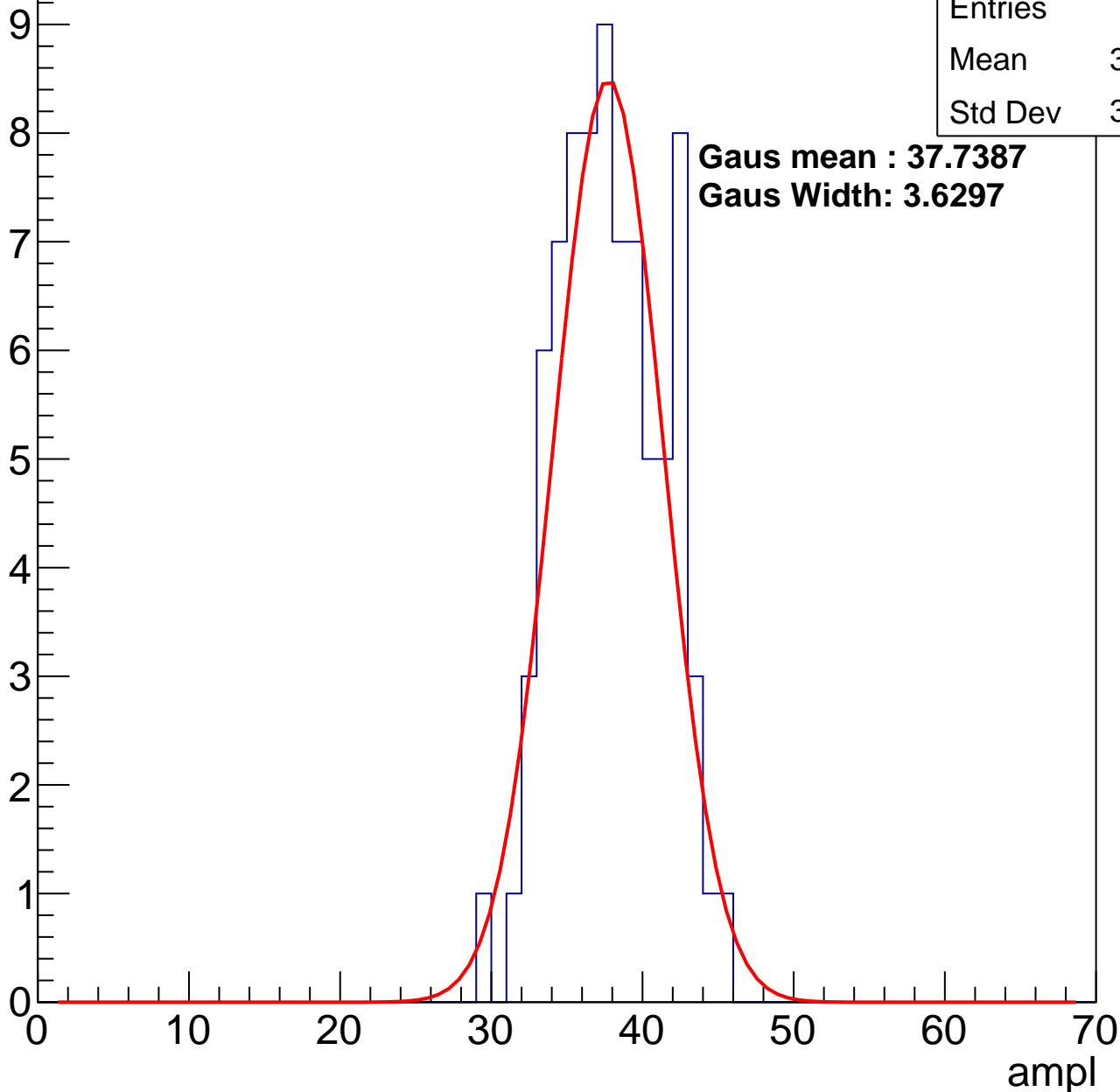
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	37.39
Std Dev	3.433

Gaus mean : 37.7387

Gaus Width: 3.6297



B1L102S, U9-ch19, adc2

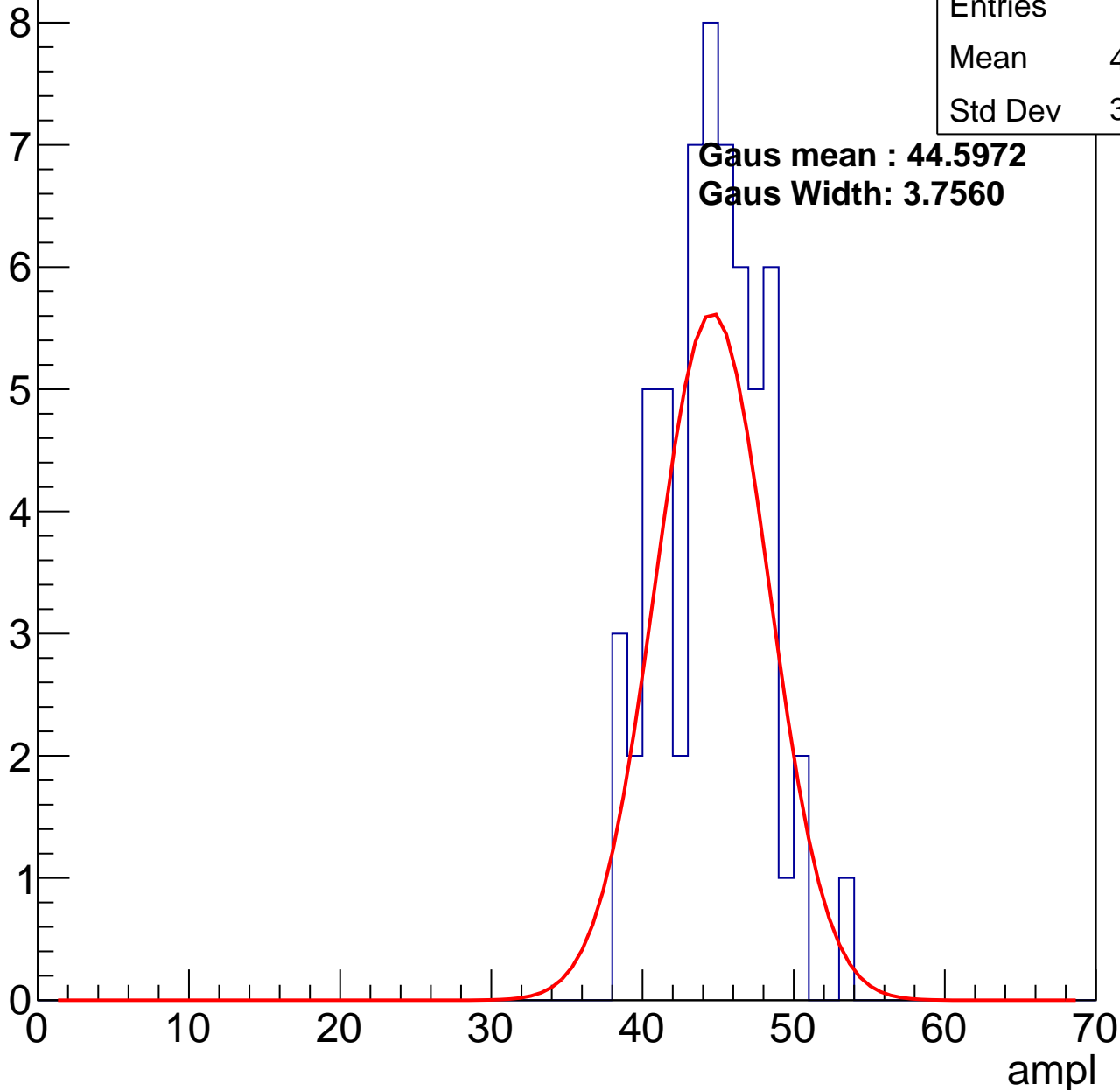
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	44.17
Std Dev	3.282

Gaus mean : 44.5972

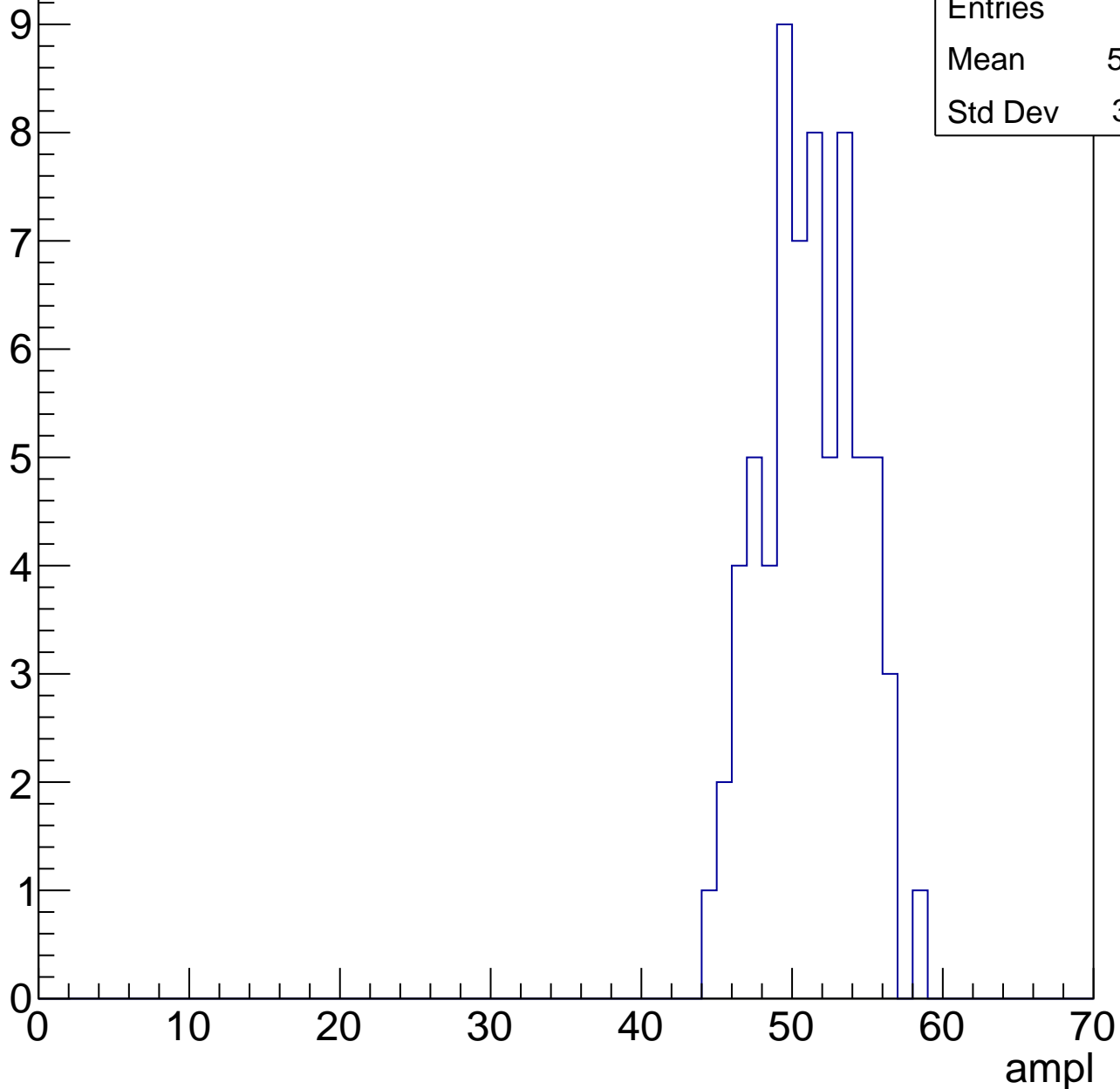
Gaus Width: 3.7560



B1L102S, U9-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

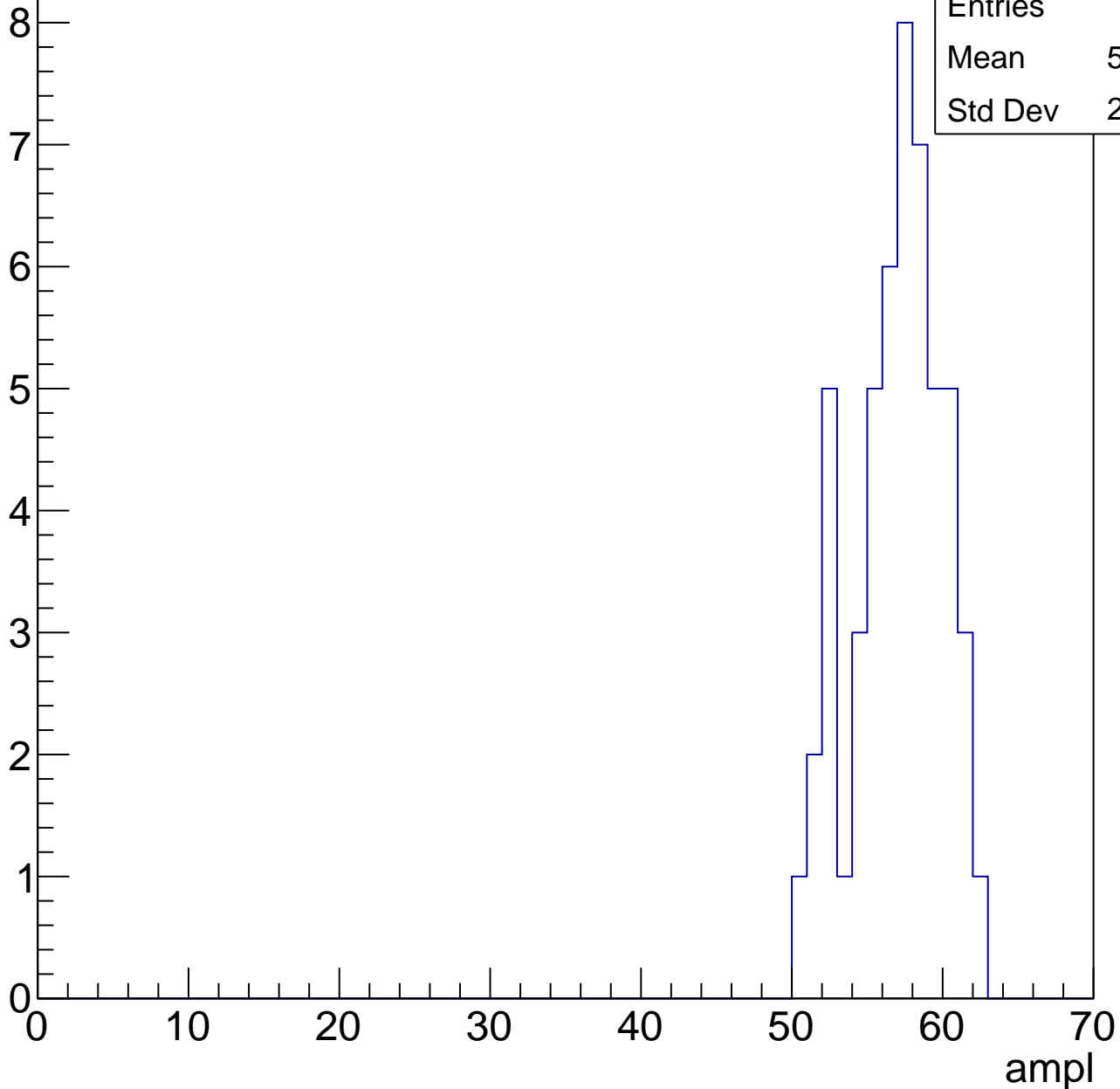


B1L102S, U9-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	56.54
Std Dev	2.938

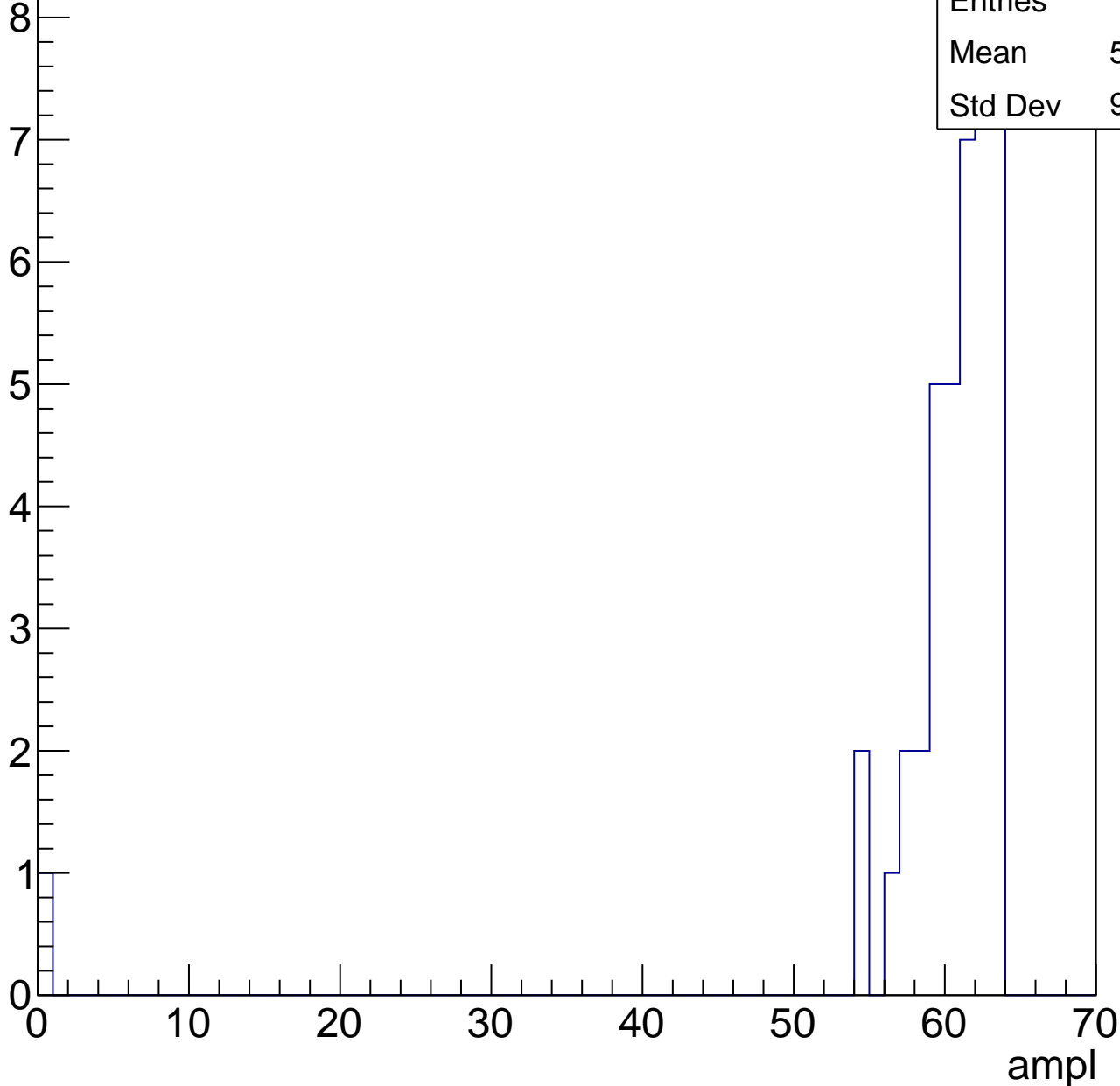


B1L102S, U9-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.93
Std Dev	9.605



B1L102S, U9-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U9-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch20, adc0

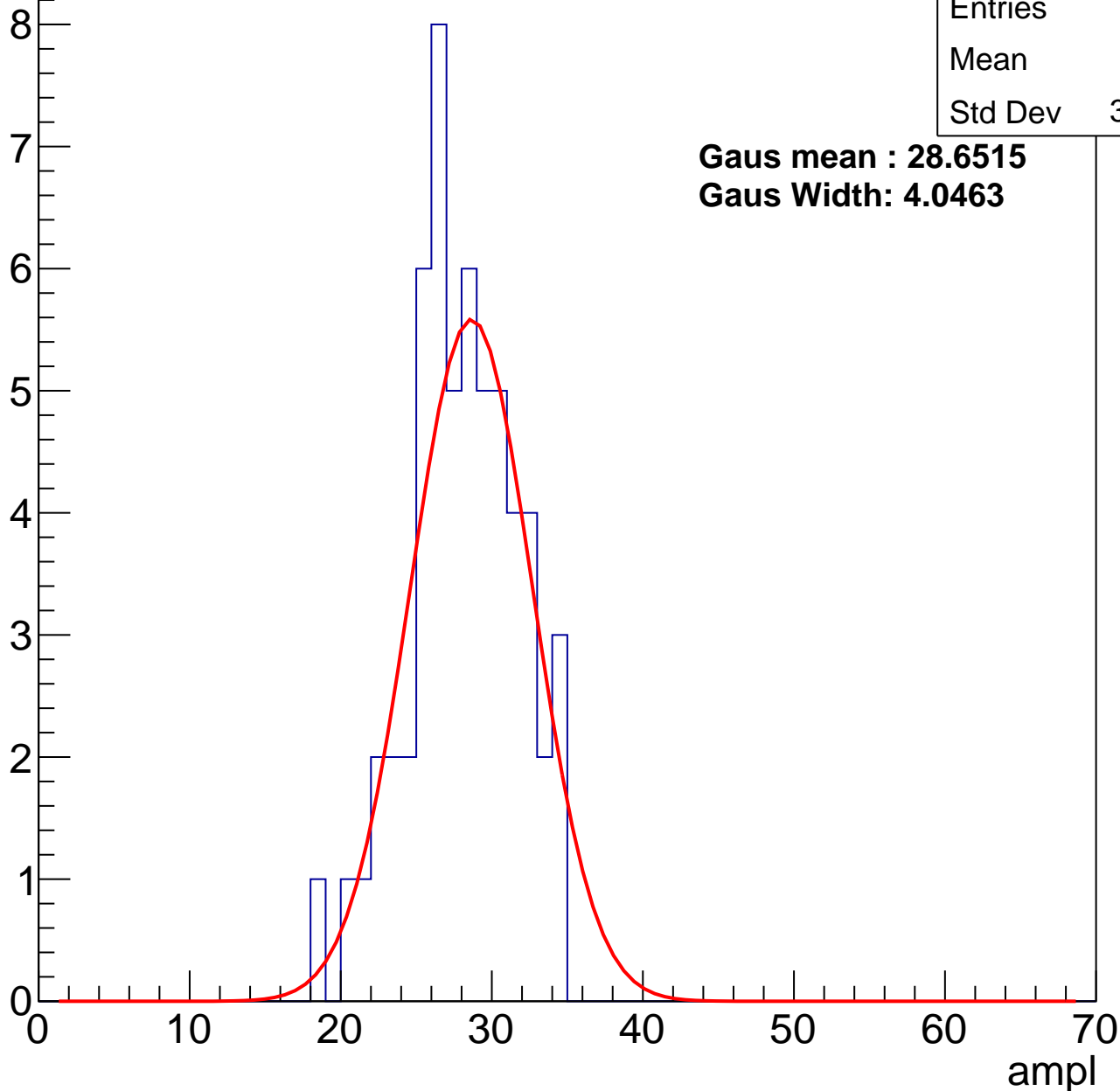
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.6
Std Dev	3.583

Gaus mean : 28.6515

Gaus Width: 4.0463



B1L102S, U9-ch20, adc1

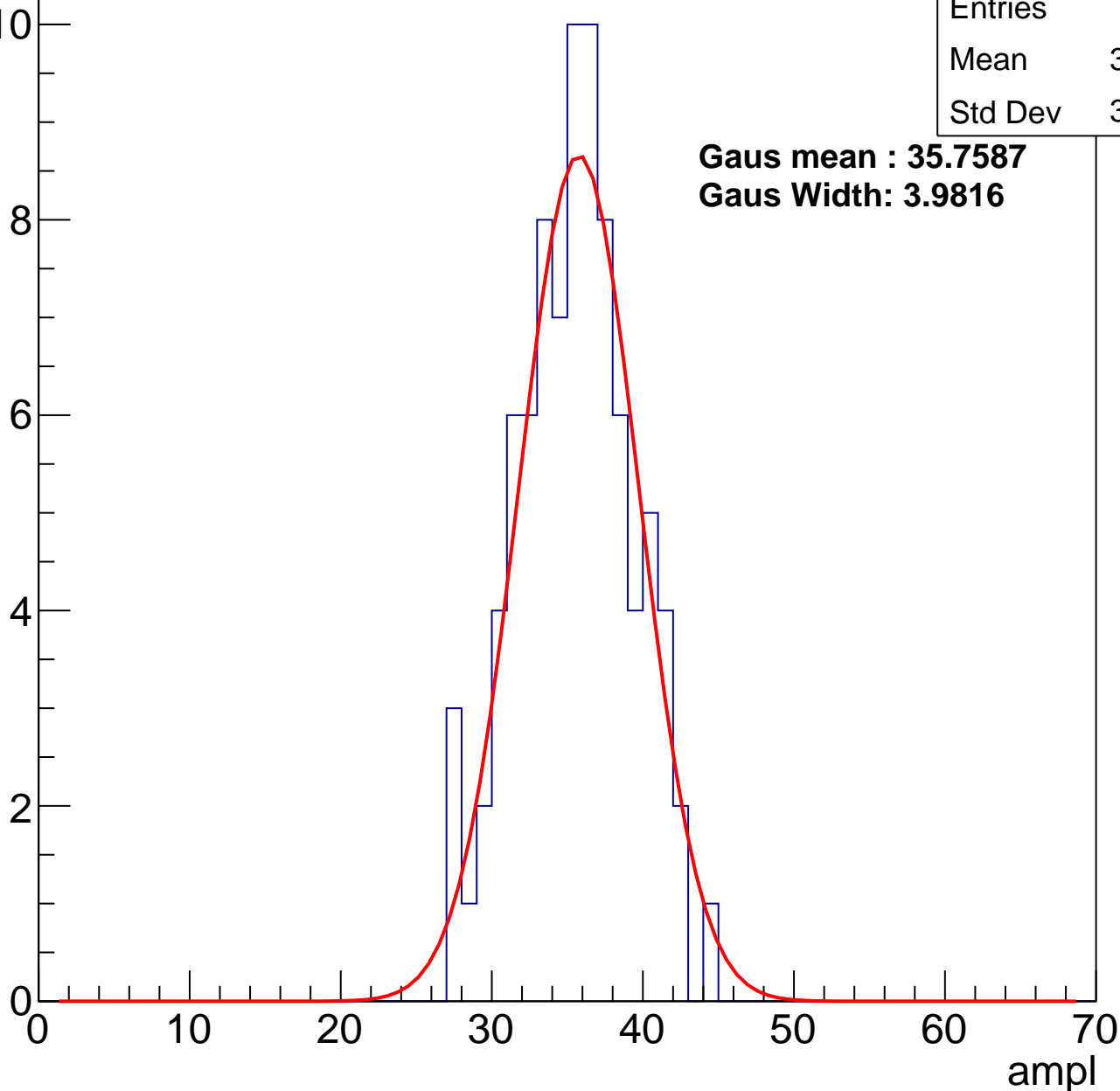
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	35.05
Std Dev	3.729

Gaus mean : 35.7587

Gaus Width: 3.9816



B1L102S, U9-ch20, adc2

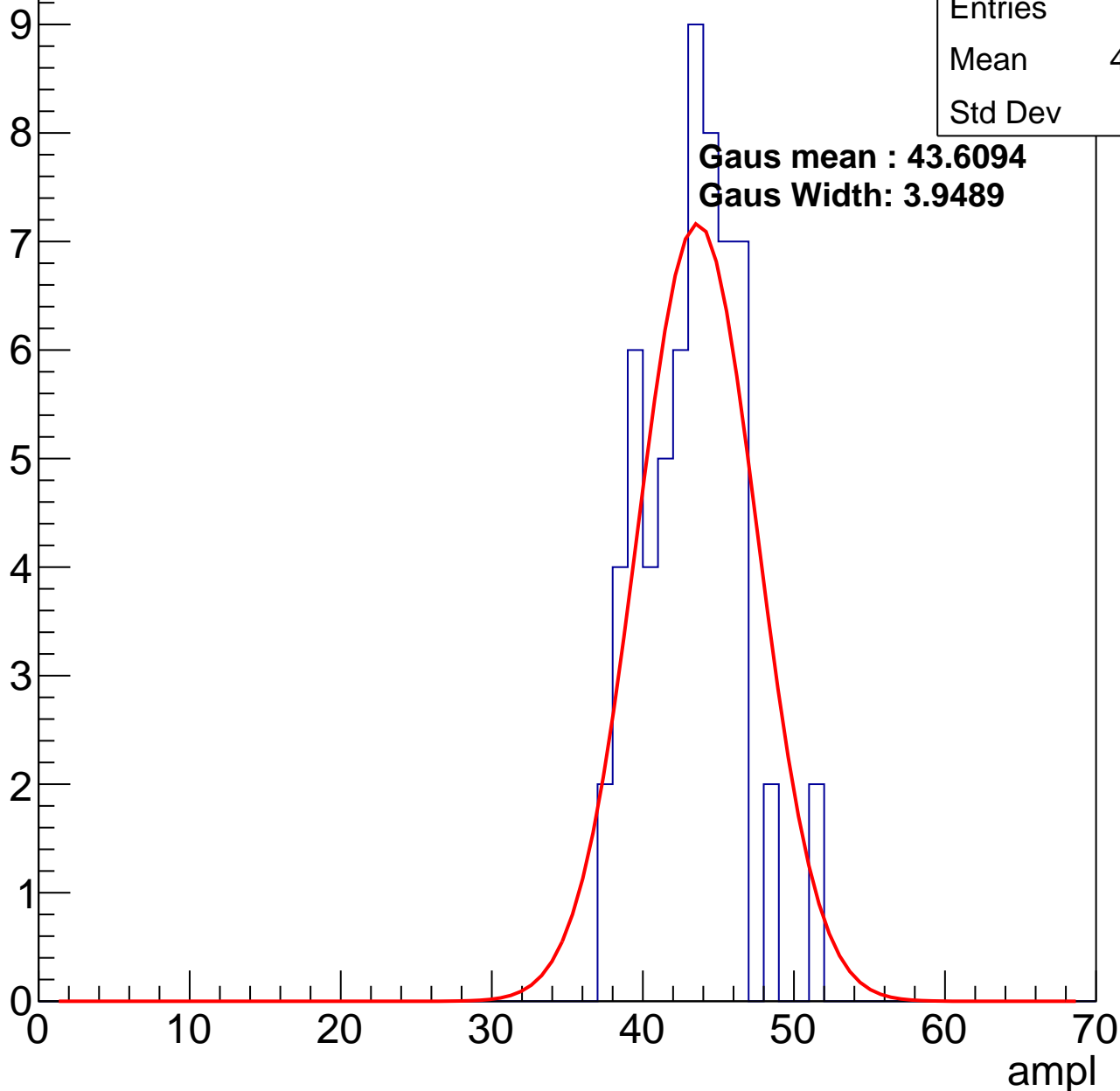
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.76
Std Dev	3.12

Gaus mean : 43.6094

Gaus Width: 3.9489

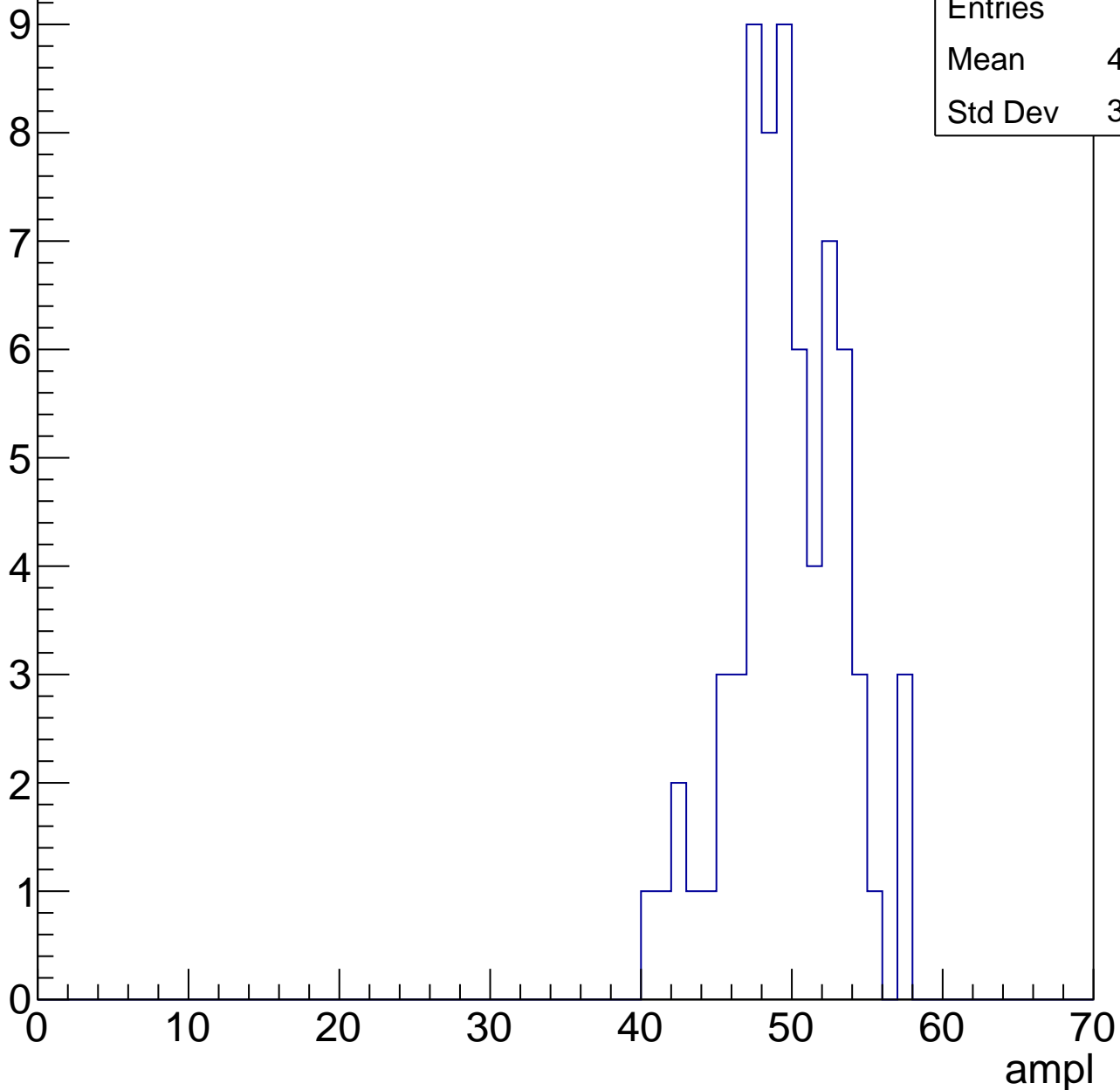


B1L102S, U9-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

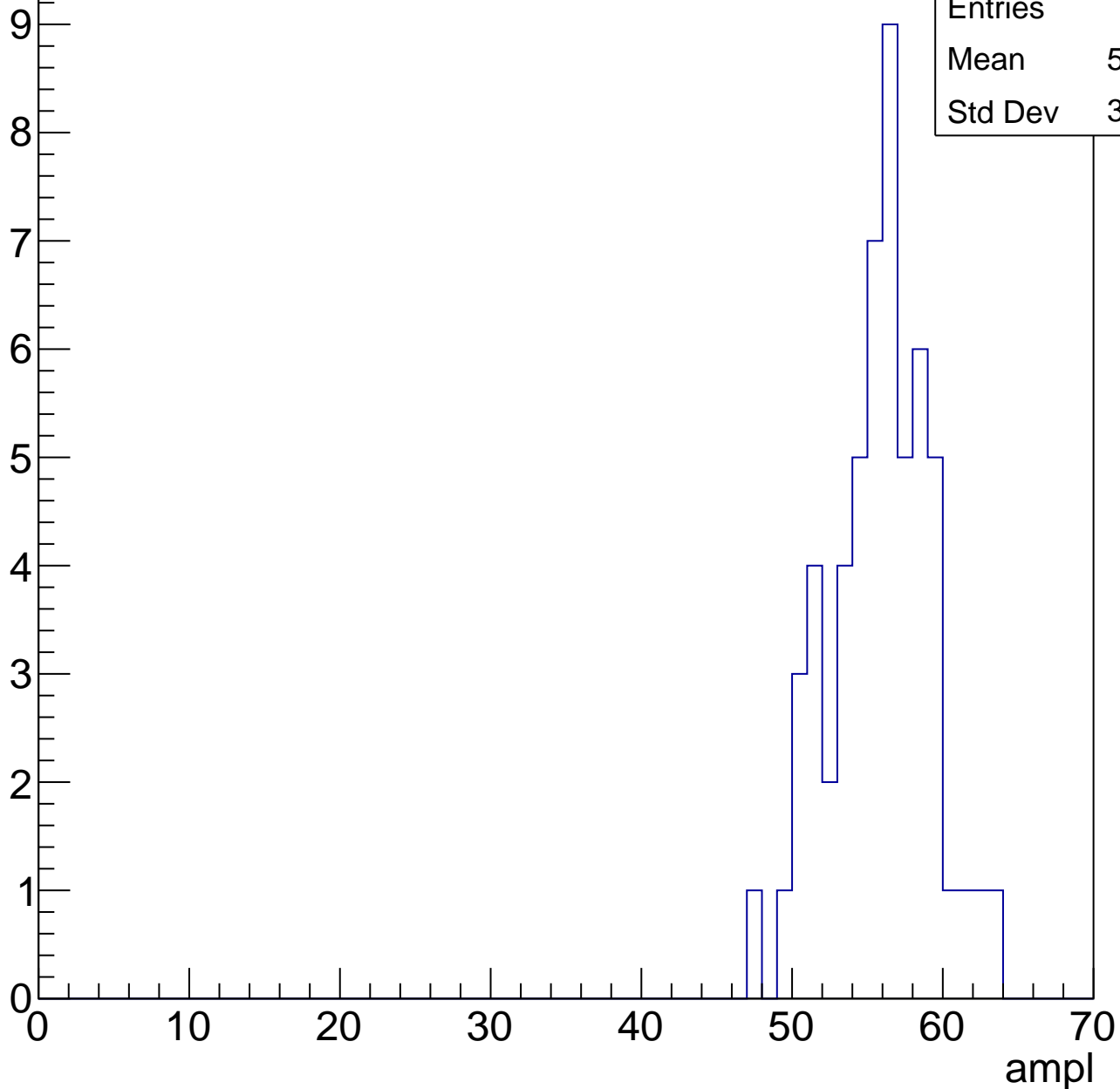
Entries	68
Mean	49.22
Std Dev	3.645



B1L102S, U9-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

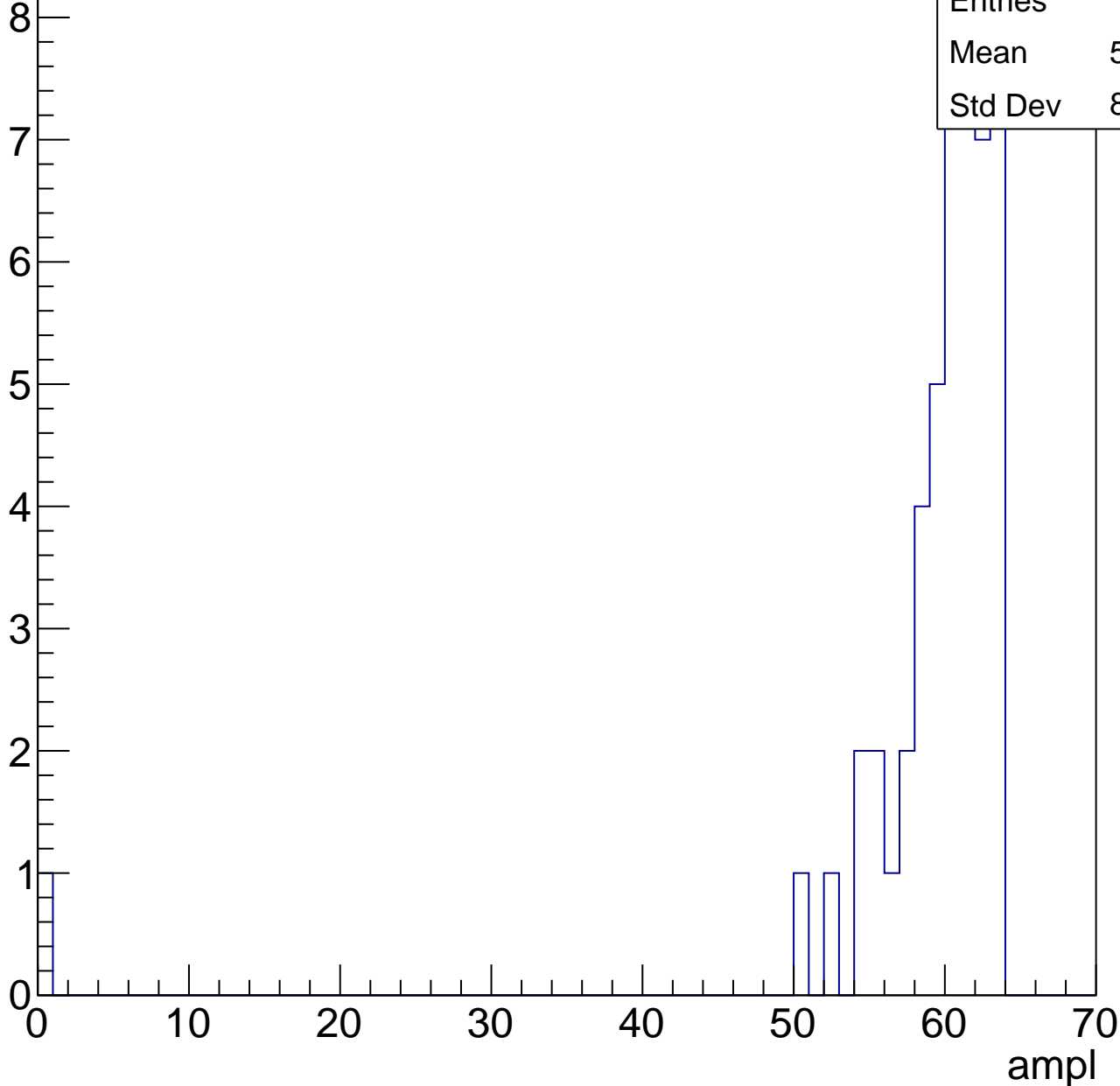


B1L102S, U9-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.46
Std Dev	8.866



B1L102S, U9-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch21, adc0

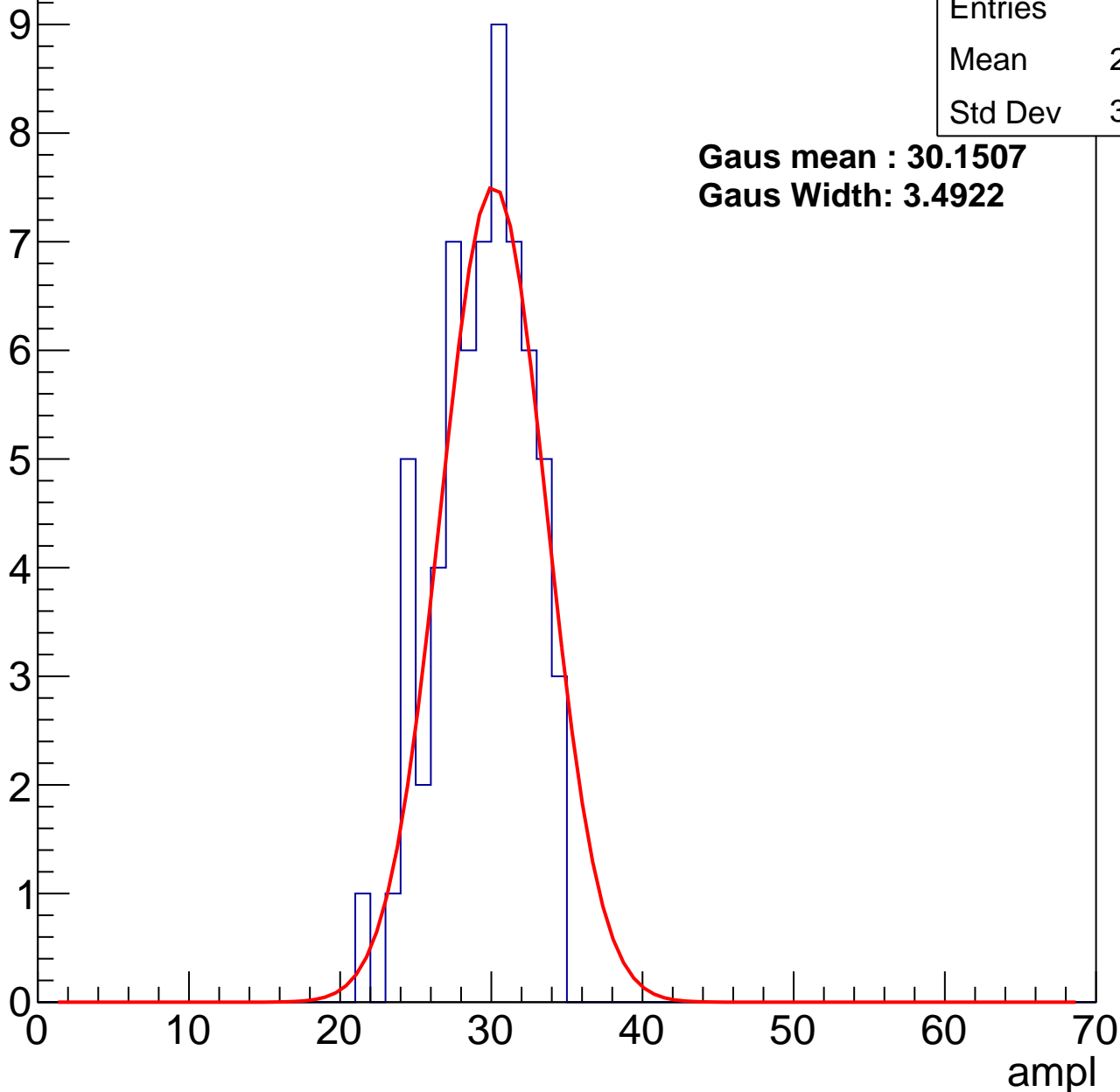
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.95
Std Dev	3.015

Gaus mean : 30.1507

Gaus Width: 3.4922



B1L102S, U9-ch21, adc1

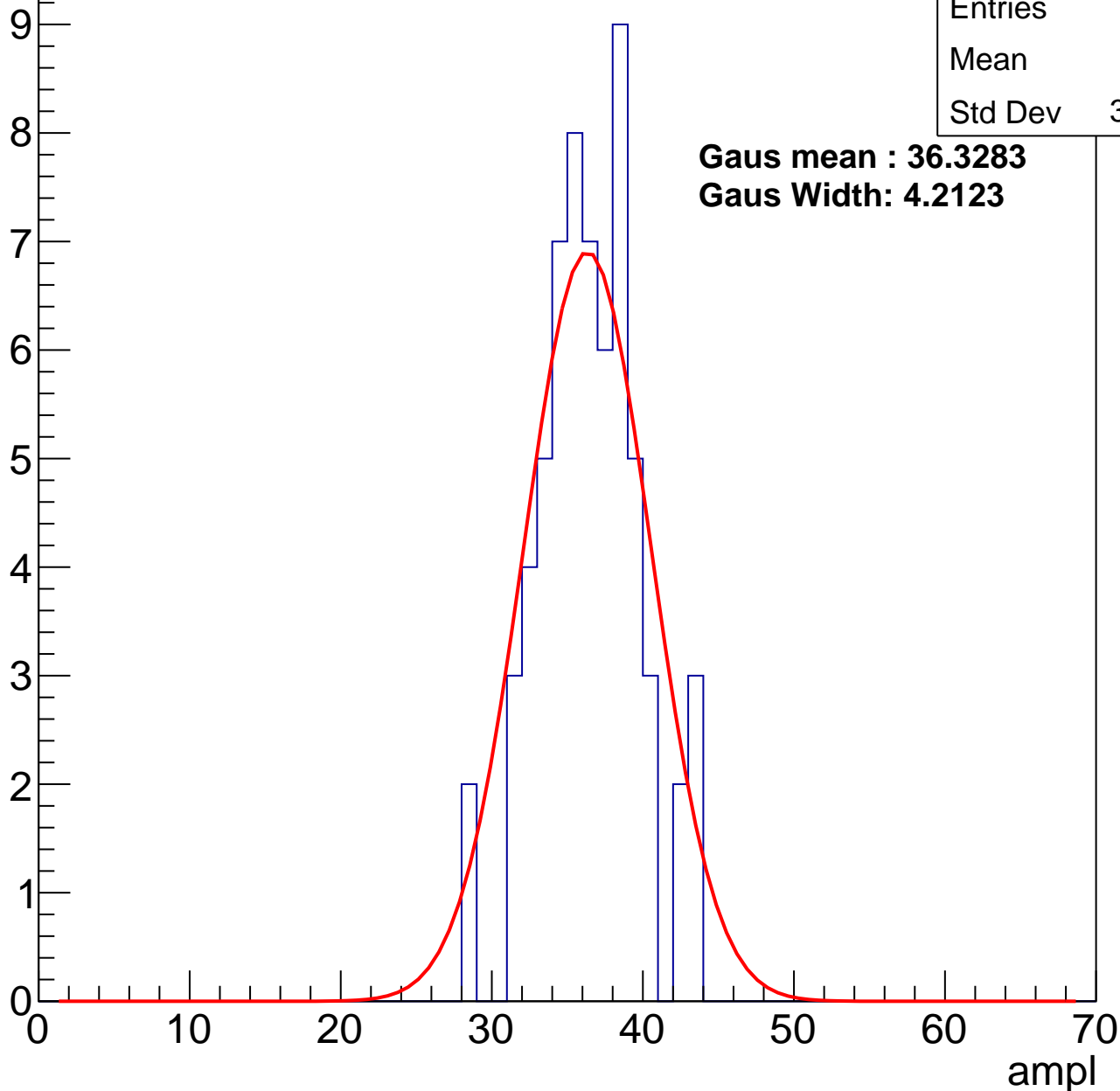
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36
Std Dev	3.312

Gaus mean : 36.3283

Gaus Width: 4.2123



B1L102S, U9-ch21, adc2

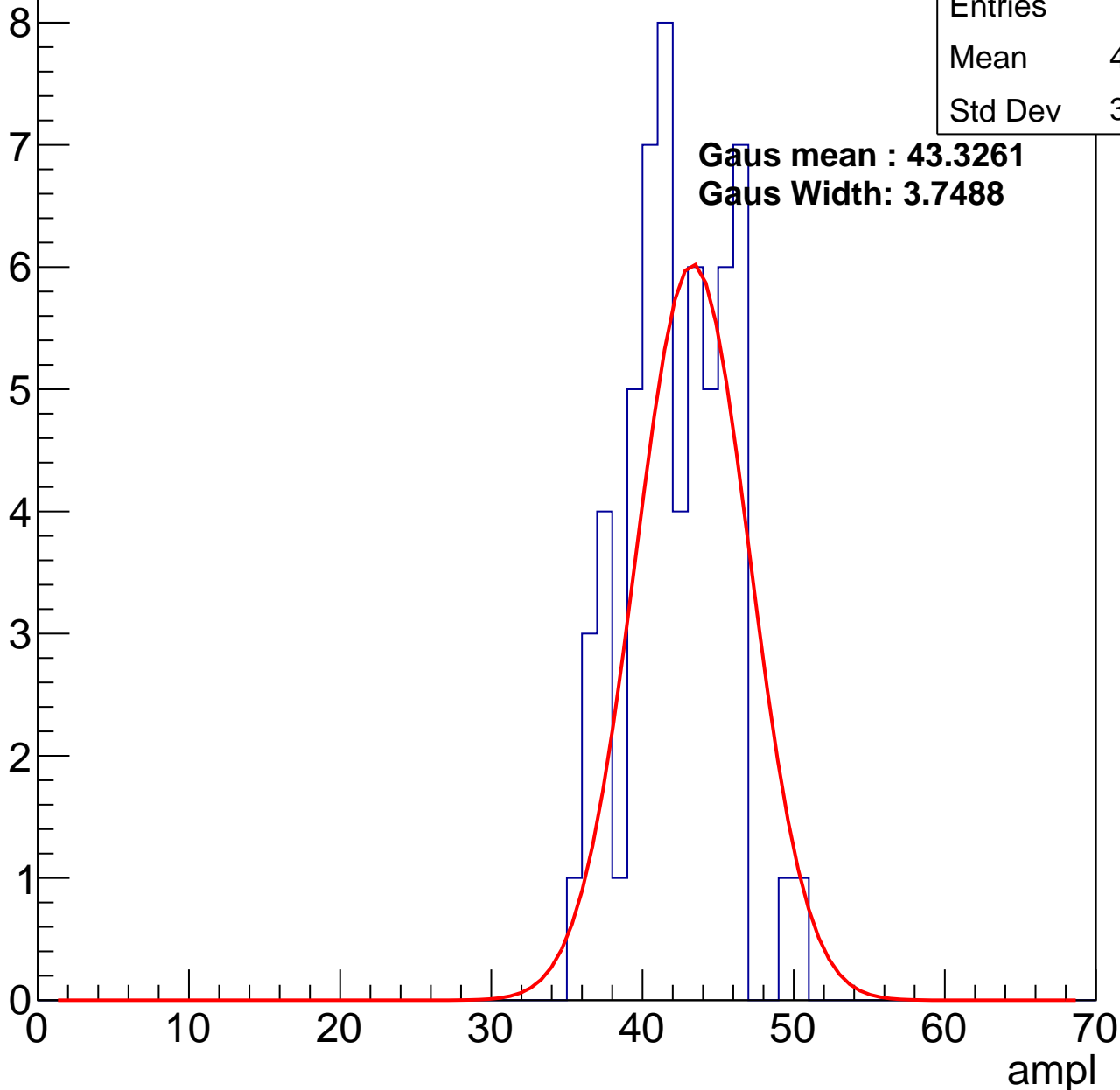
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.85
Std Dev	3.339

Gaus mean : 43.3261

Gaus Width: 3.7488

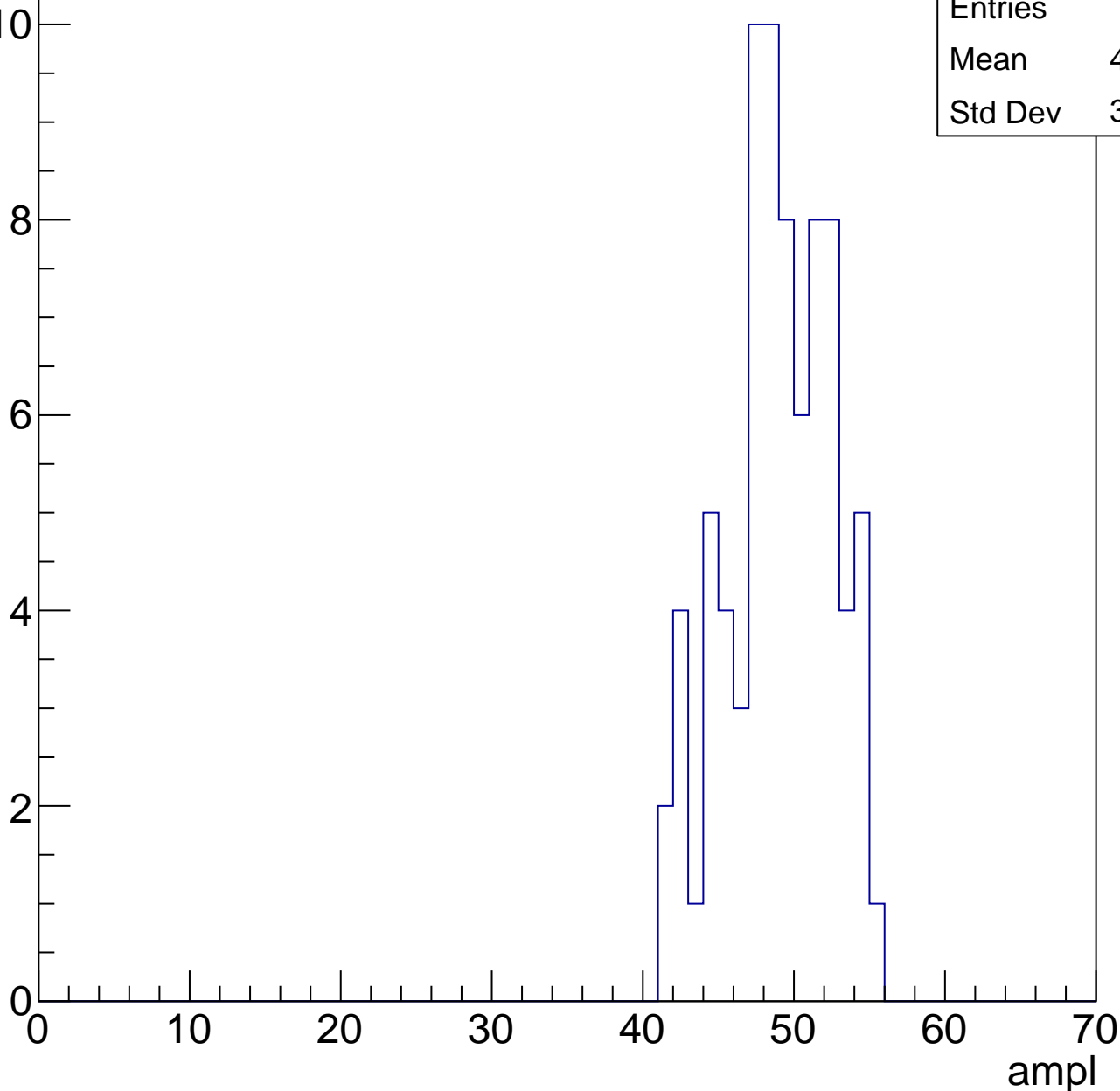


B1L102S, U9-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	48.53
Std Dev	3.456

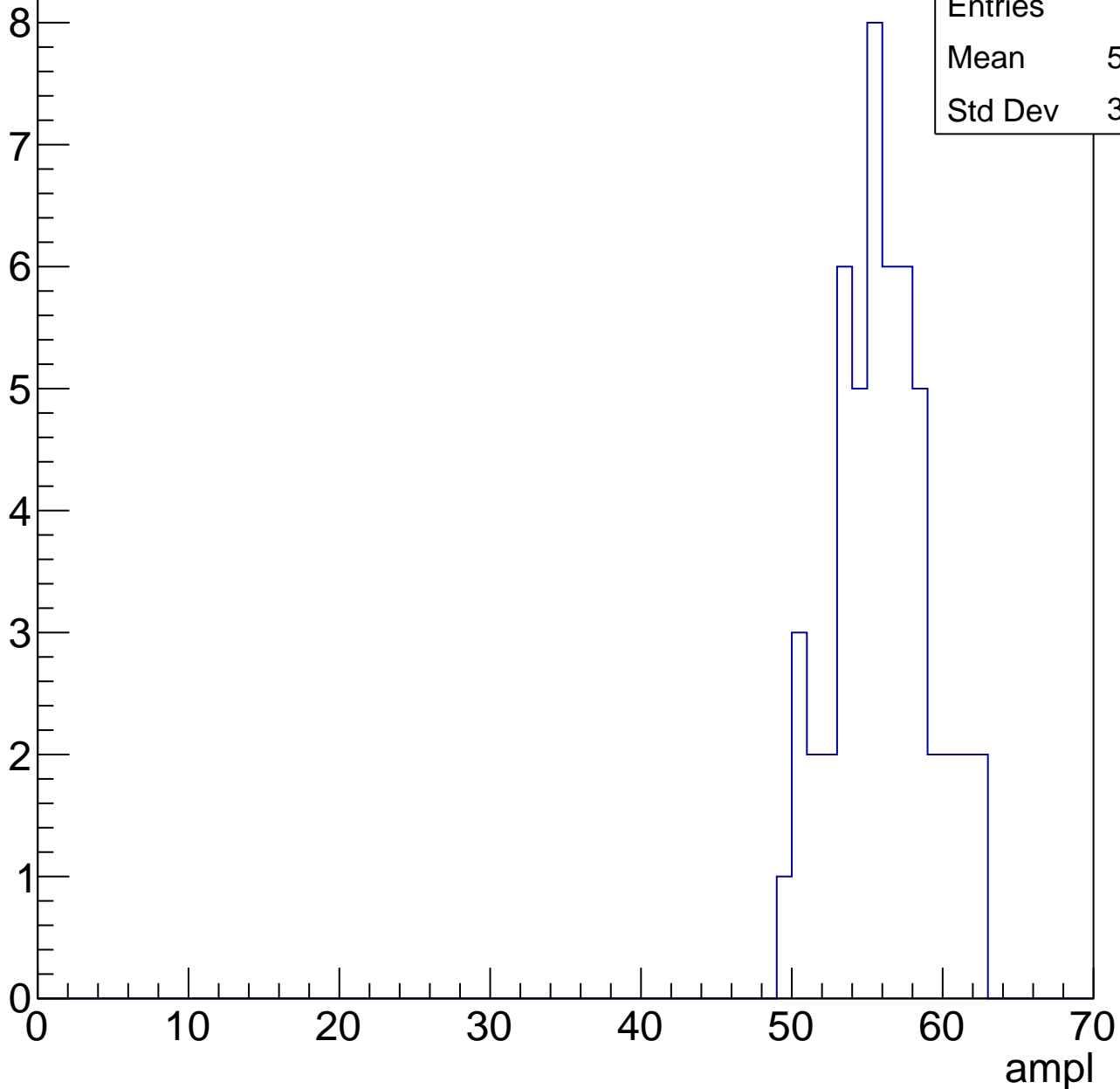


B1L102S, U9-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.48
Std Dev	3.116



B1L102S, U9-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

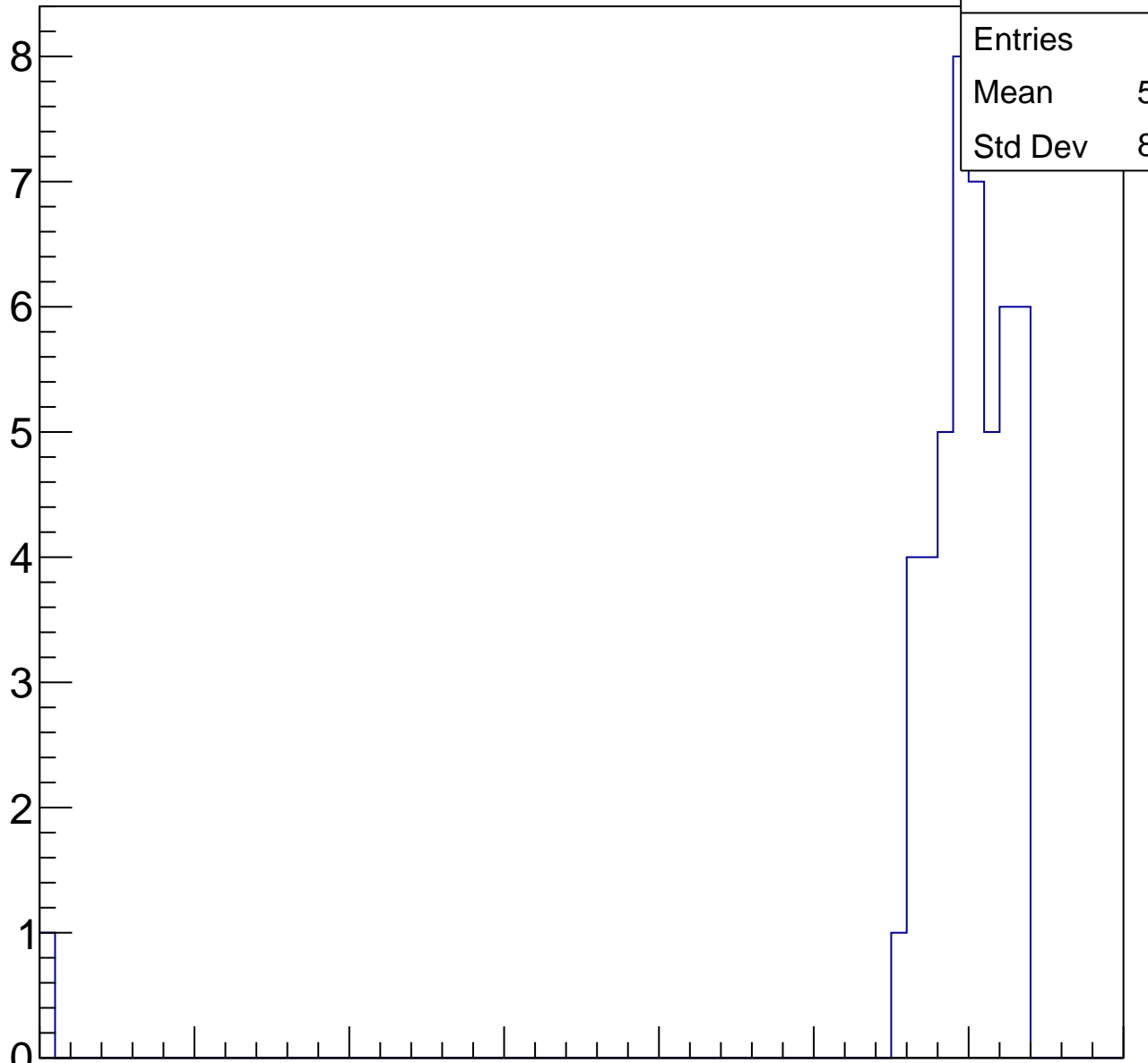
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.38
Std Dev	8.888

ampl

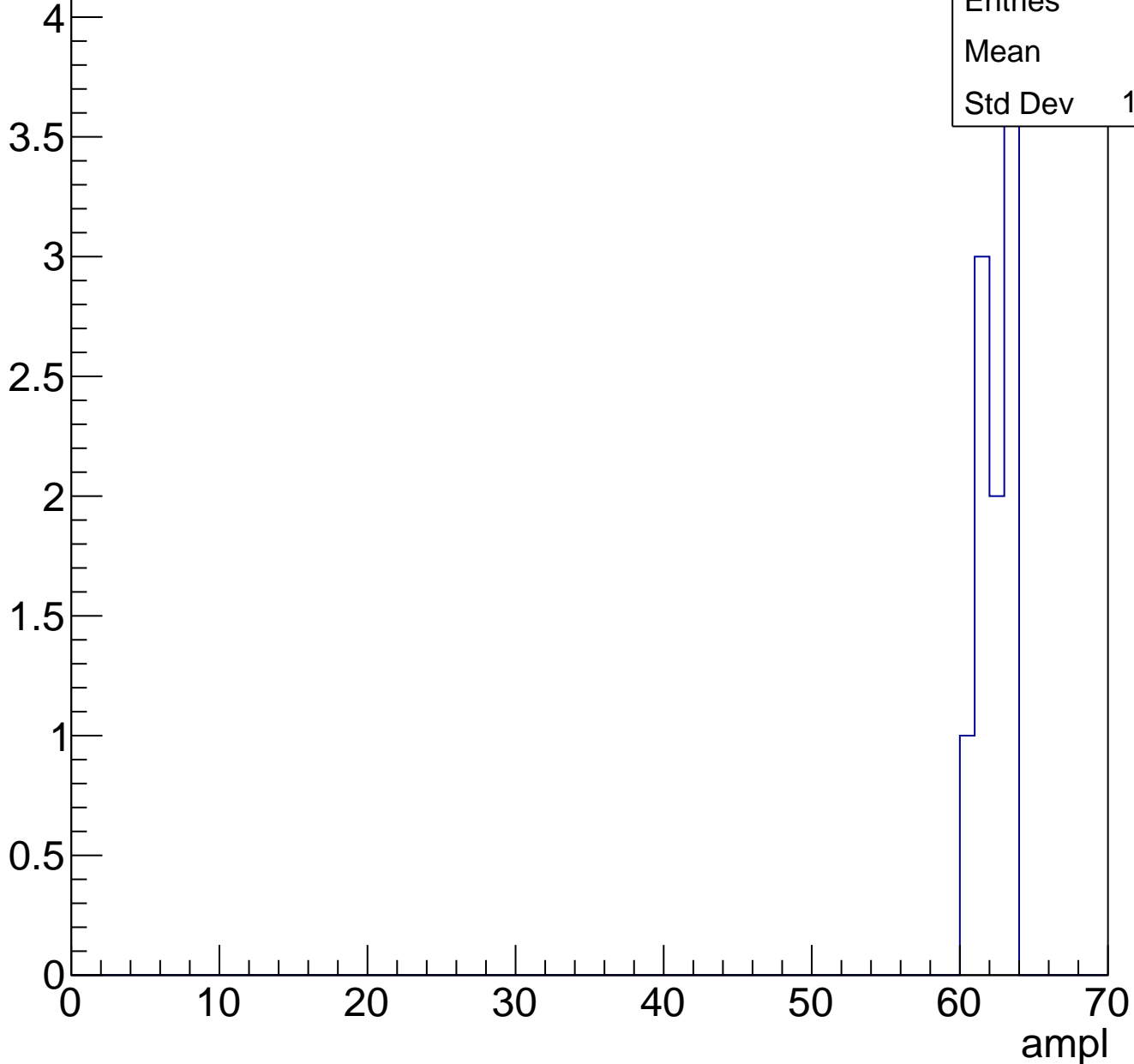
0 10 20 30 40 50 60 70



B1L102S, U9-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	61.9
Std Dev	1.044

B1L102S, U9-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch22, adc0

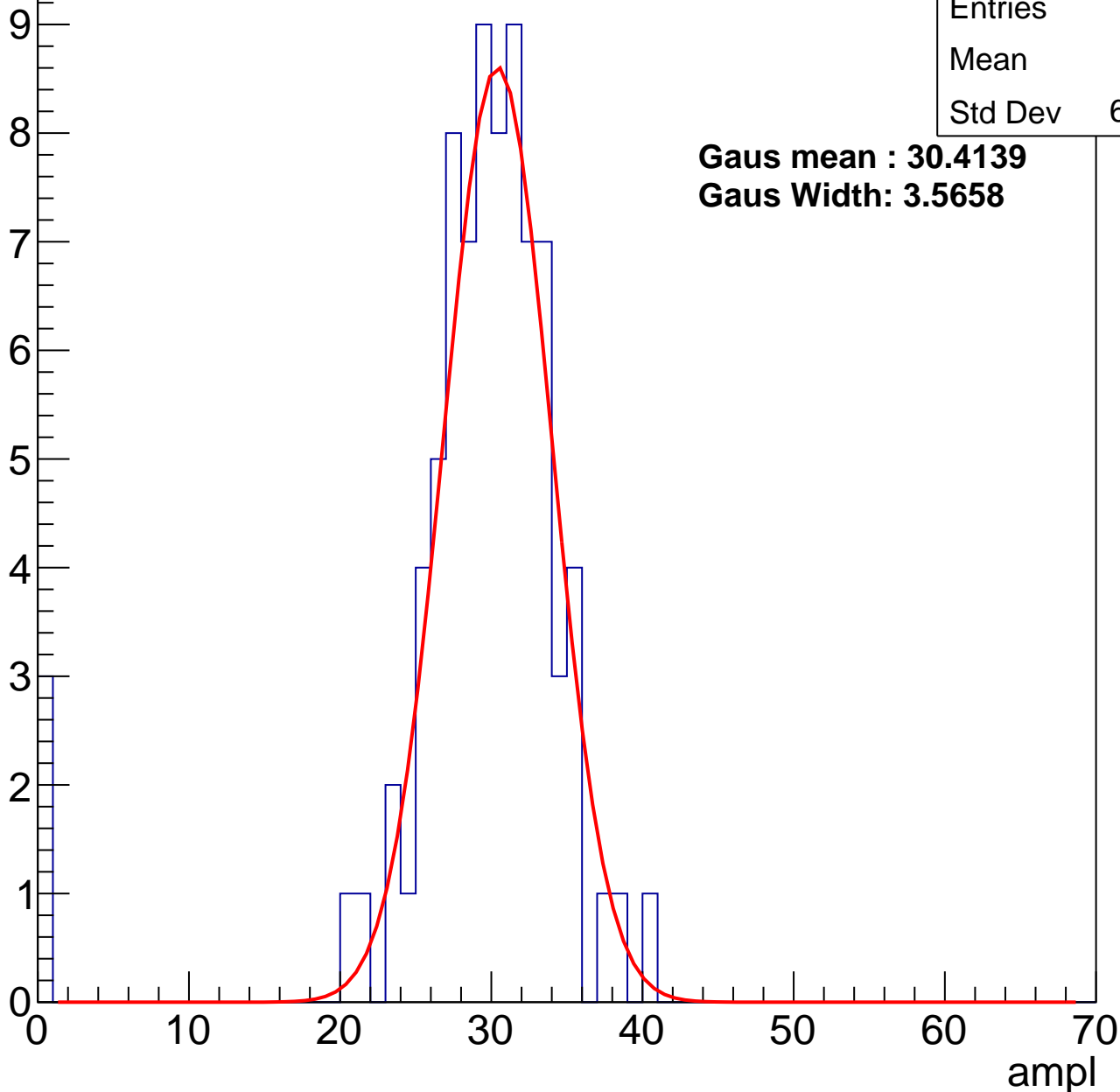
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.6
Std Dev	6.629

Gaus mean : 30.4139

Gaus Width: 3.5658



B1L102S, U9-ch22, adc1

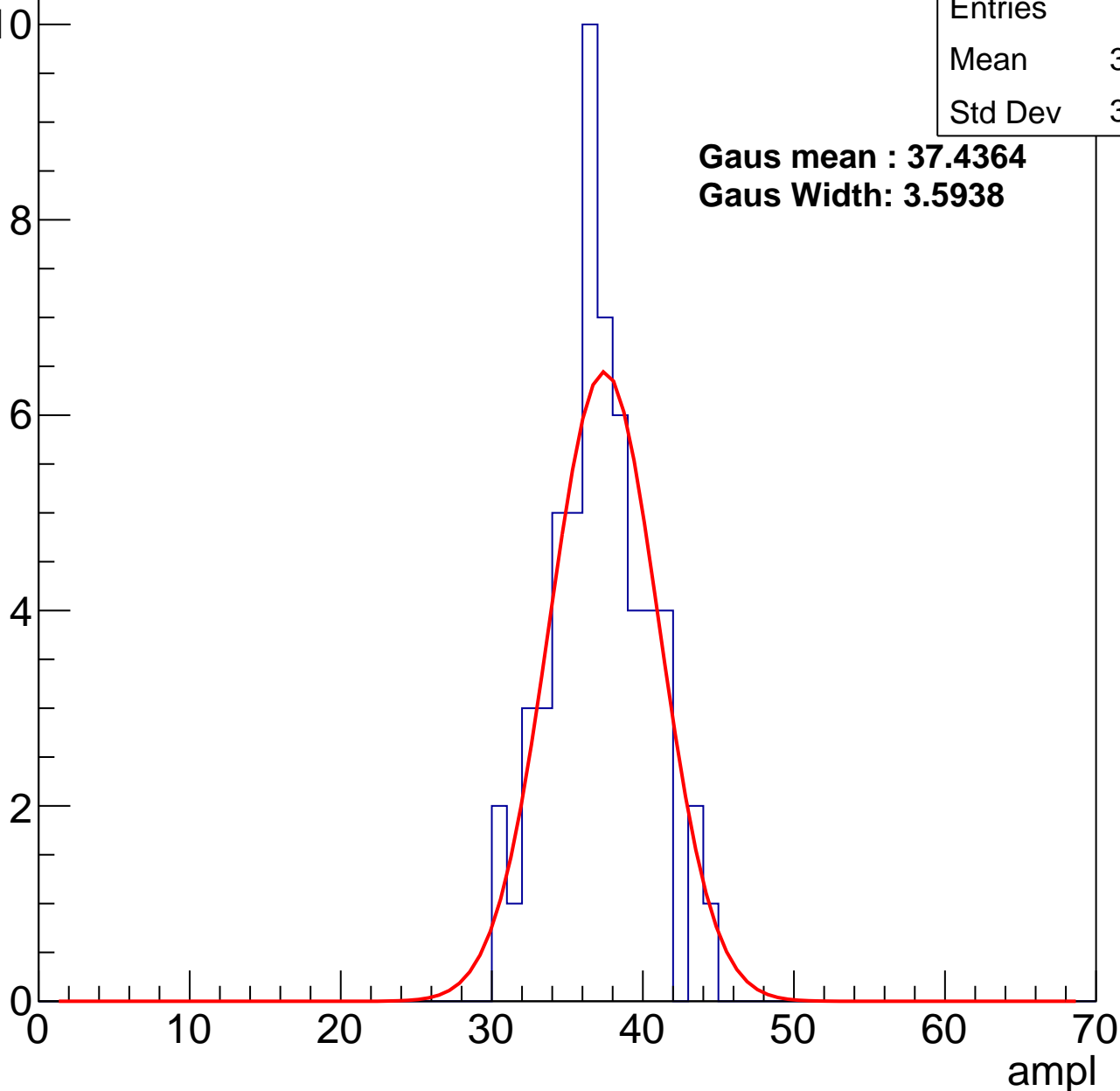
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	36.63
Std Dev	3.155

Gaus mean : 37.4364

Gaus Width: 3.5938



B1L102S, U9-ch22, adc2

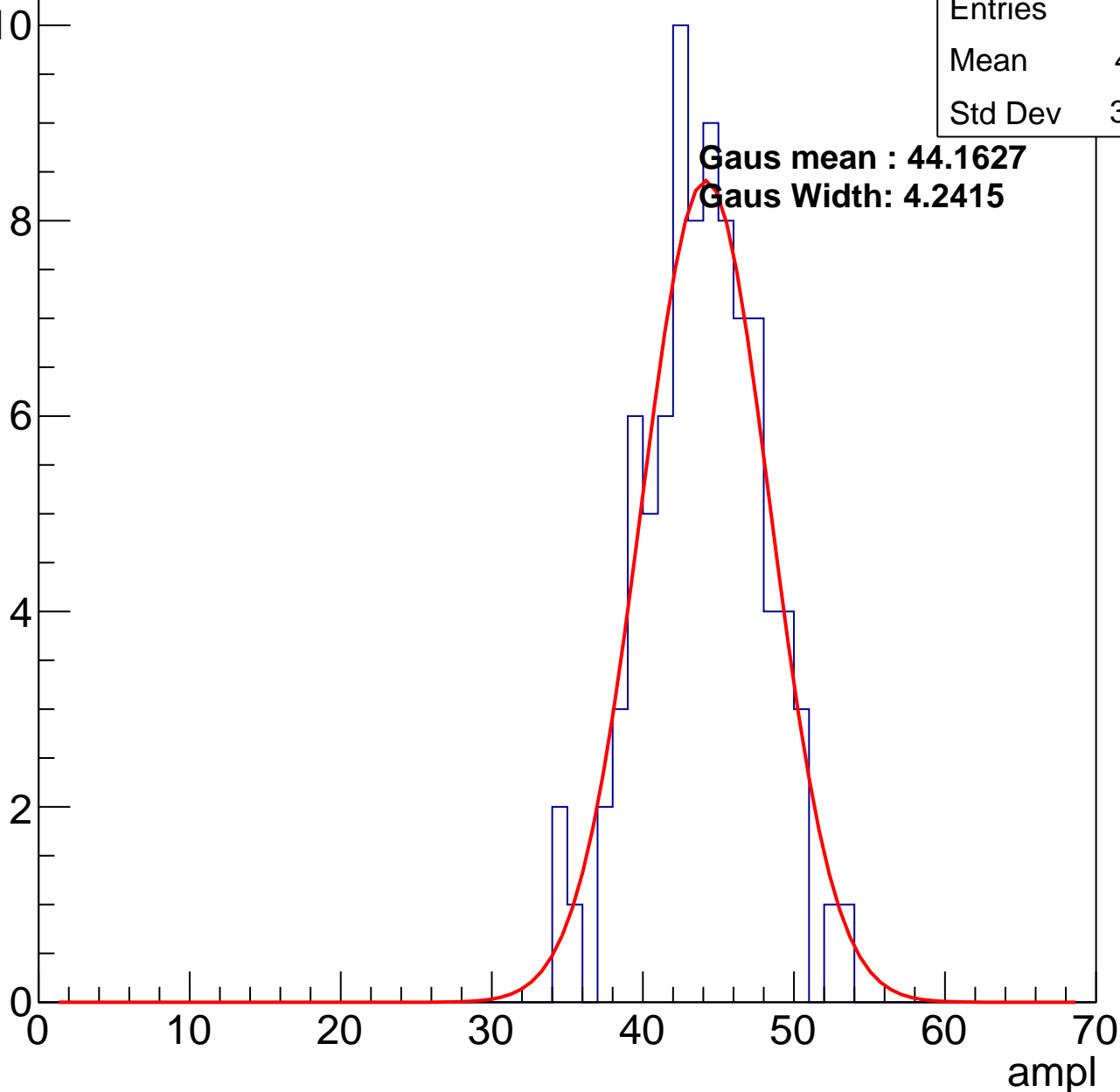
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	43.51
Std Dev	3.886

Gaus mean : 44.1627

Gaus Width: 4.2415

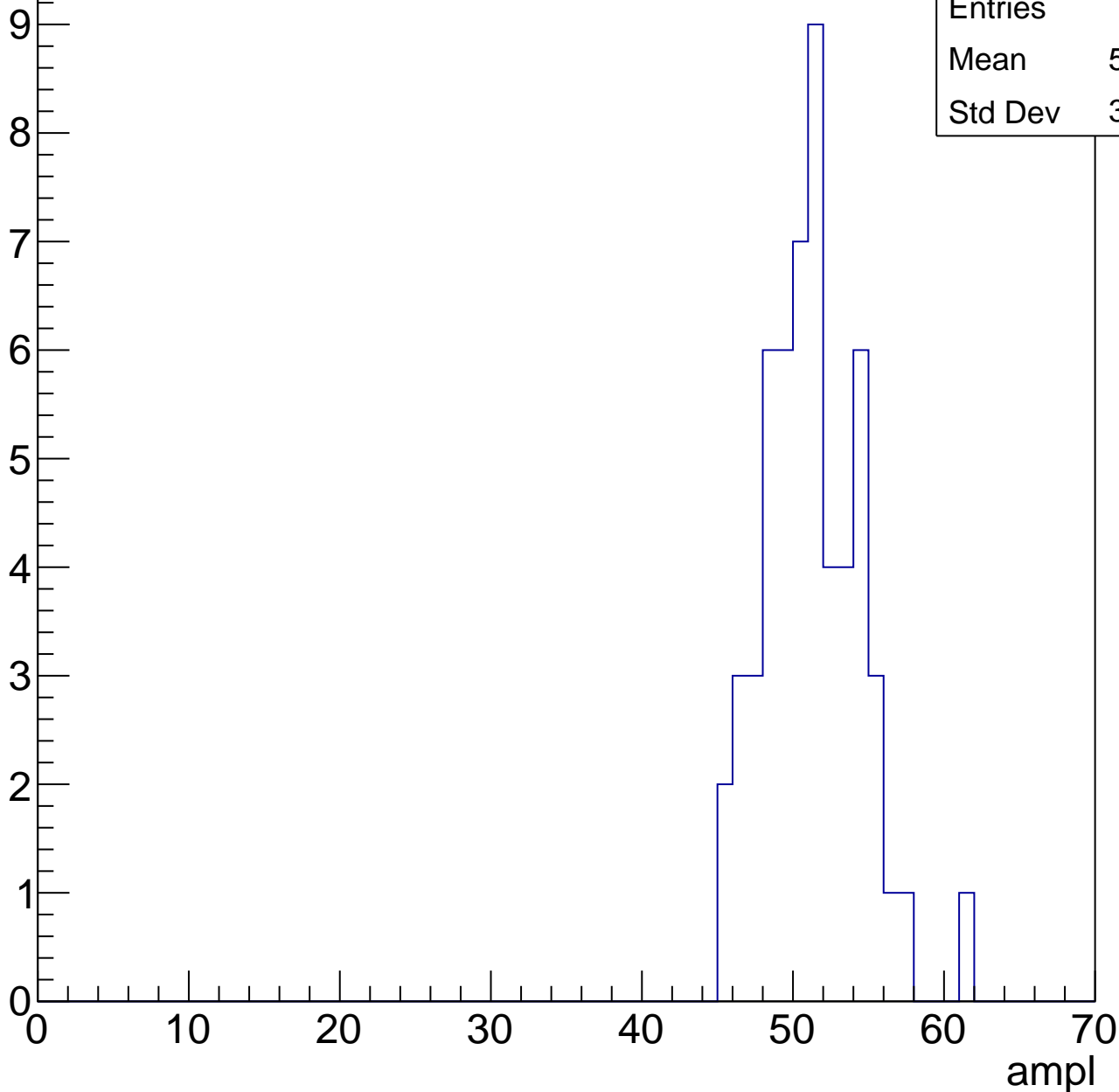


B1L102S, U9-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

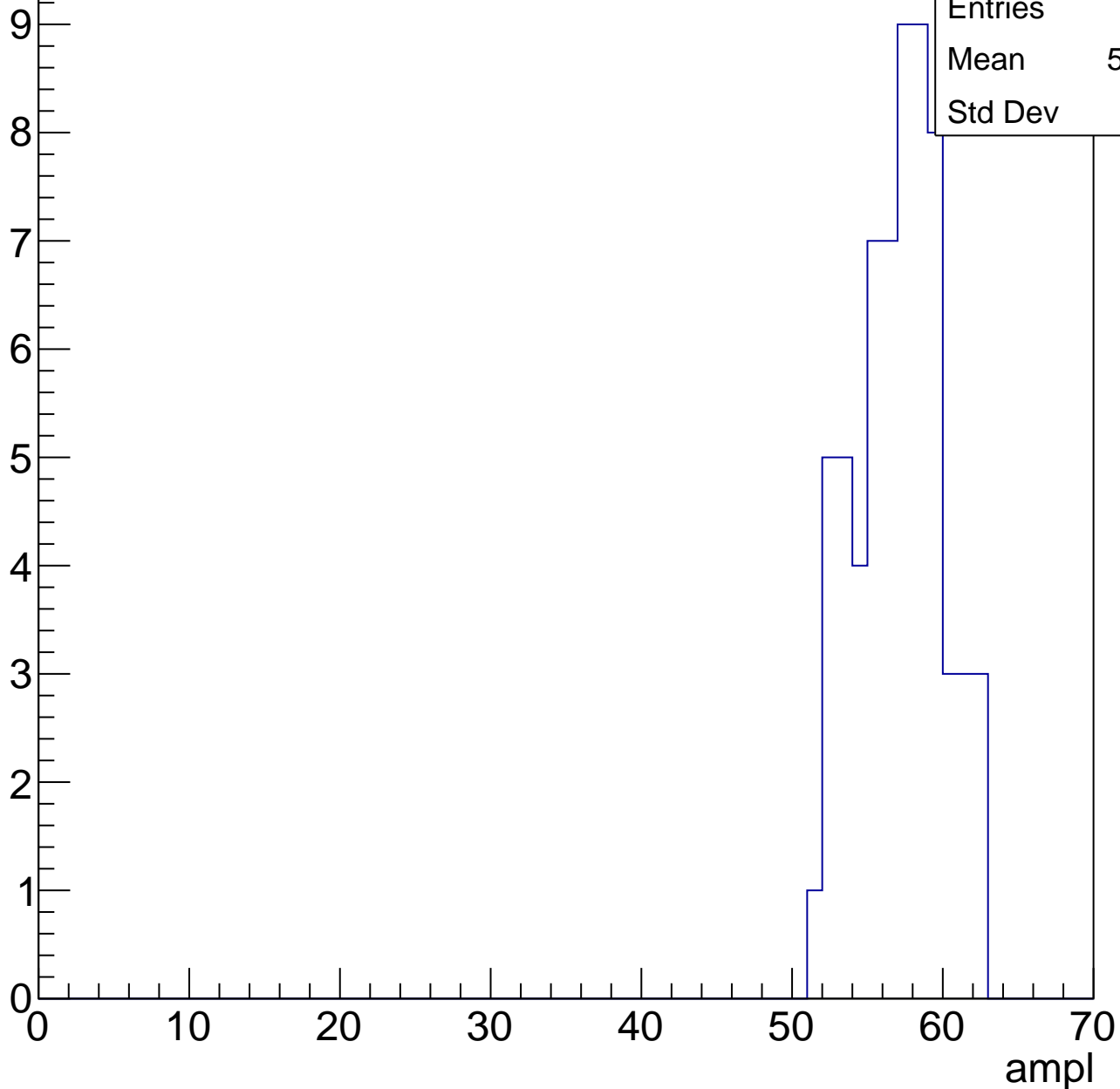
Entries	56
Mean	50.77
Std Dev	3.162



B1L102S, U9-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



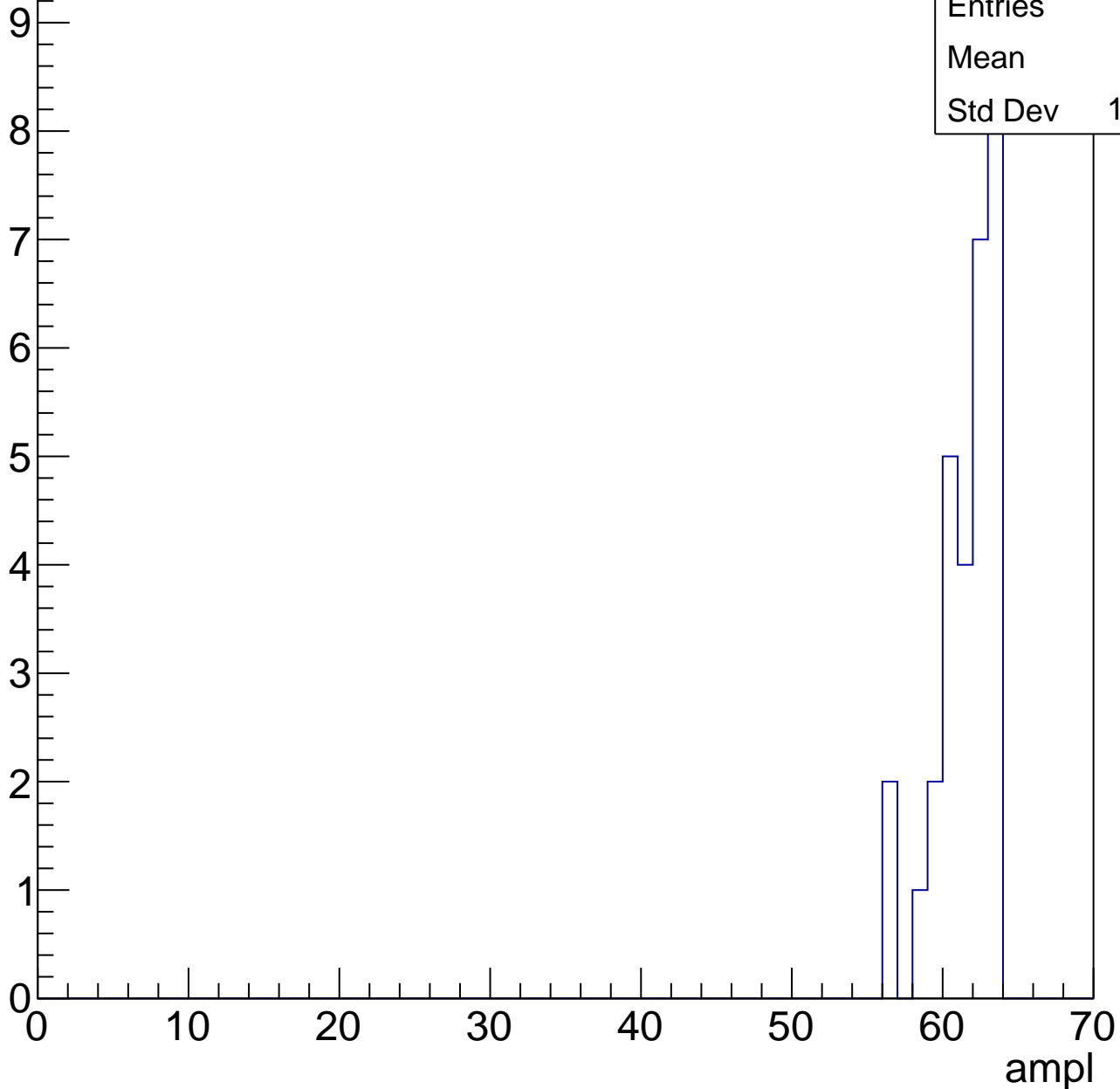
Entries	64
Mean	56.64
Std Dev	2.78

B1L102S, U9-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	61.1
Std Dev	1.955



B1L102S, U9-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

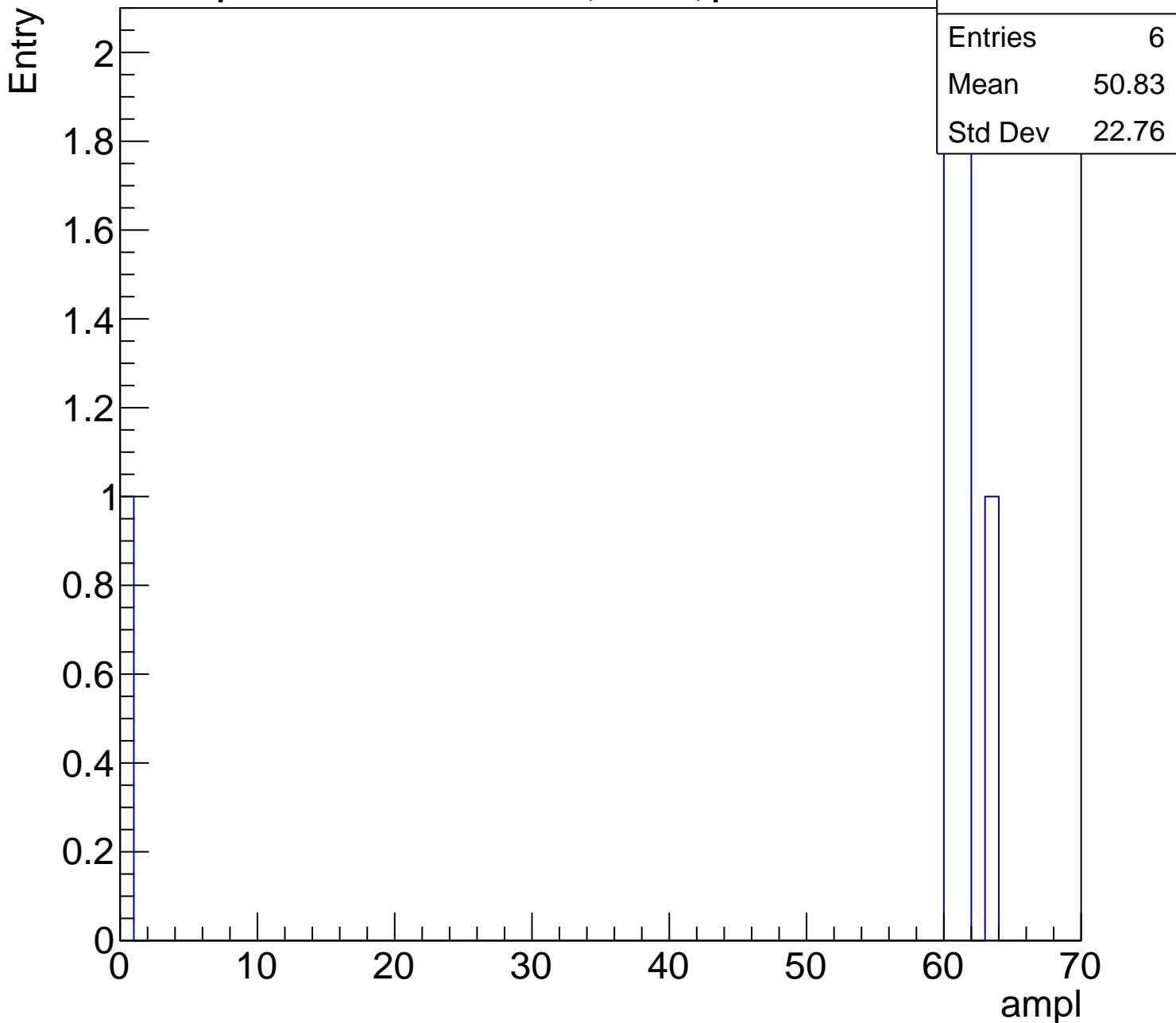
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.83
Std Dev	22.76

0 10 20 30 40 50 60 70

ampl



B1L102S, U9-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch23, adc0

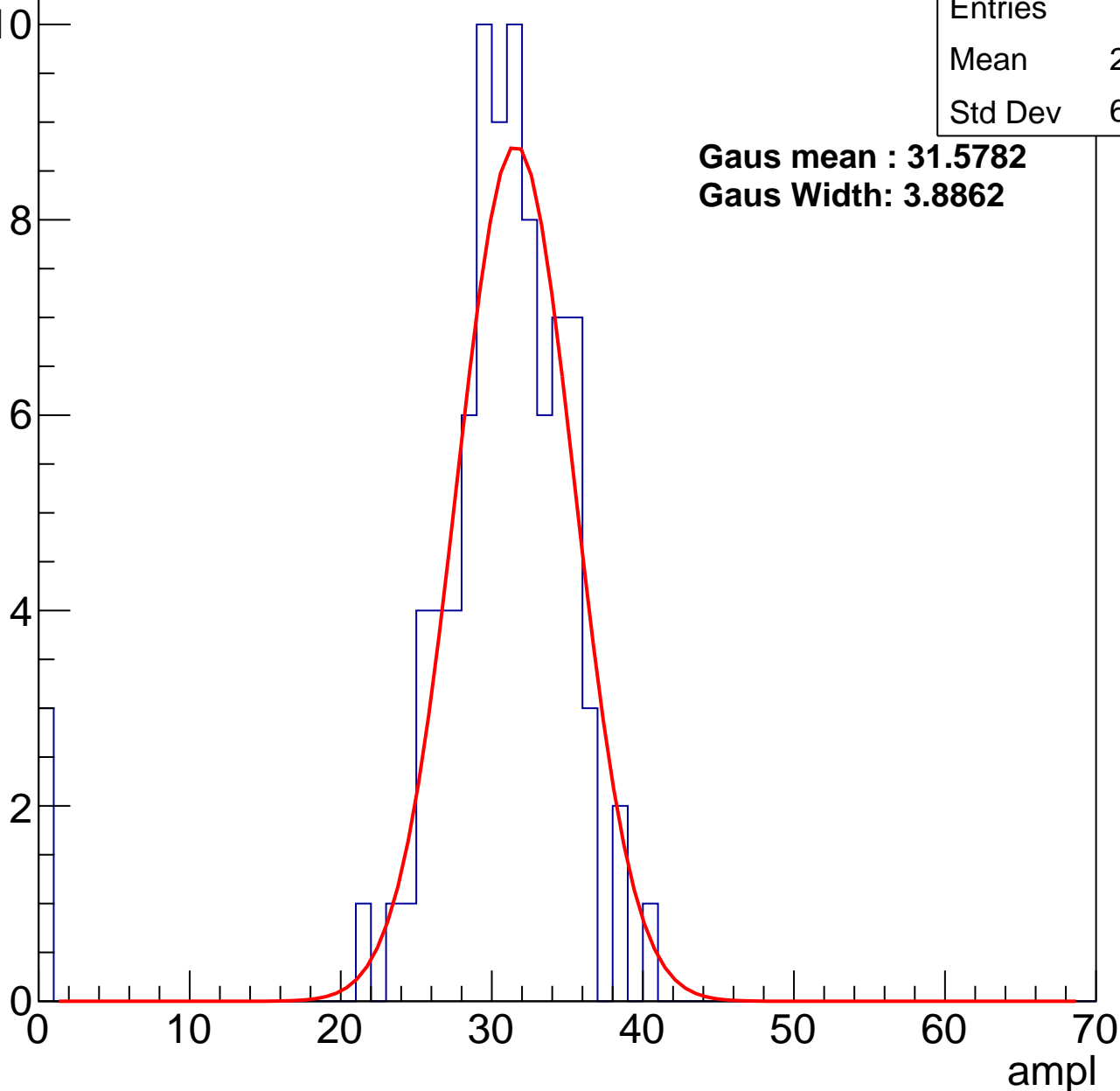
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	29.64
Std Dev	6.616

Gaus mean : 31.5782

Gaus Width: 3.8862



B1L102S, U9-ch23, adc1

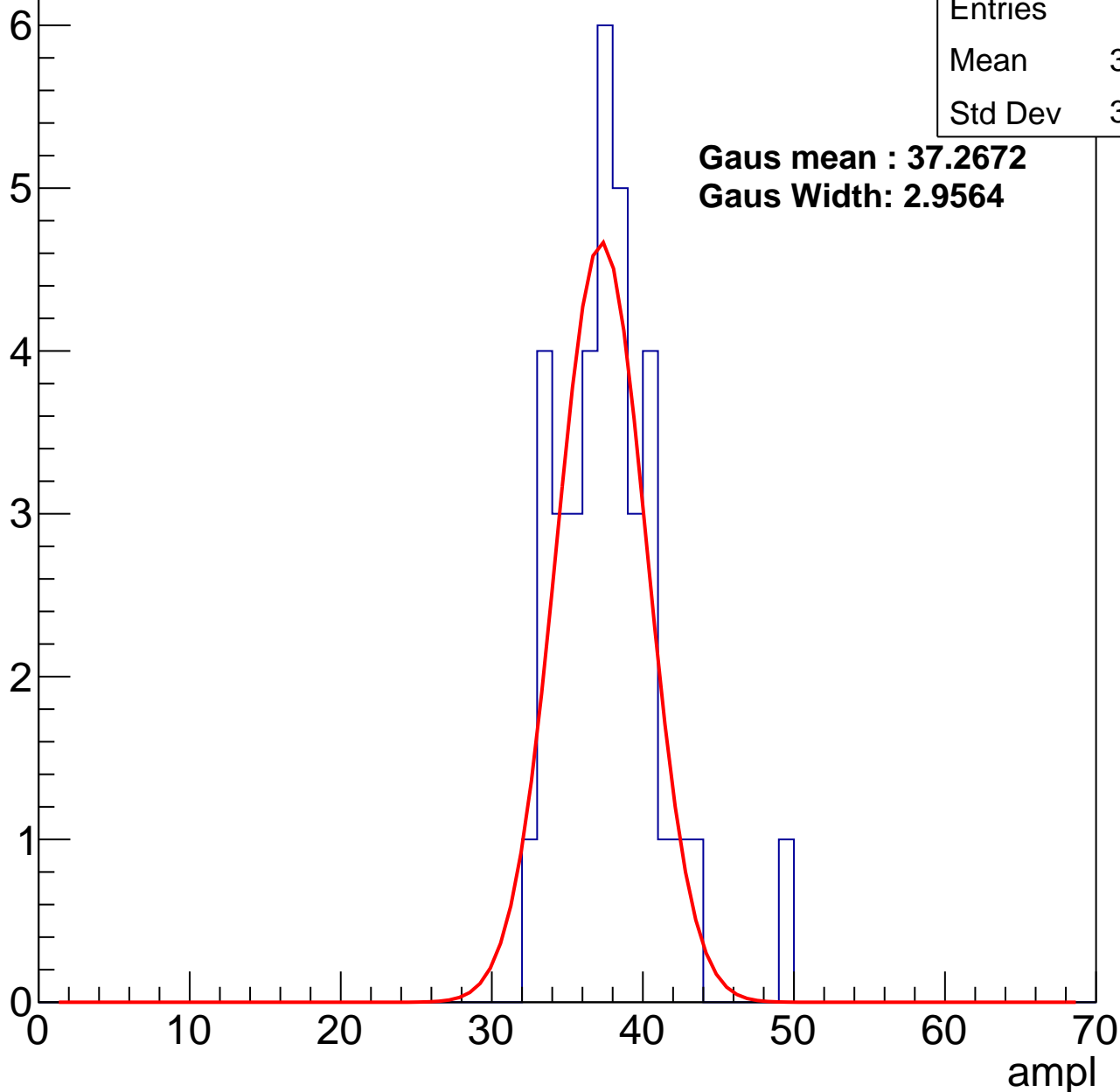
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	37.27
Std Dev	3.302

Gaus mean : 37.2672

Gaus Width: 2.9564



B1L102S, U9-ch23, adc2

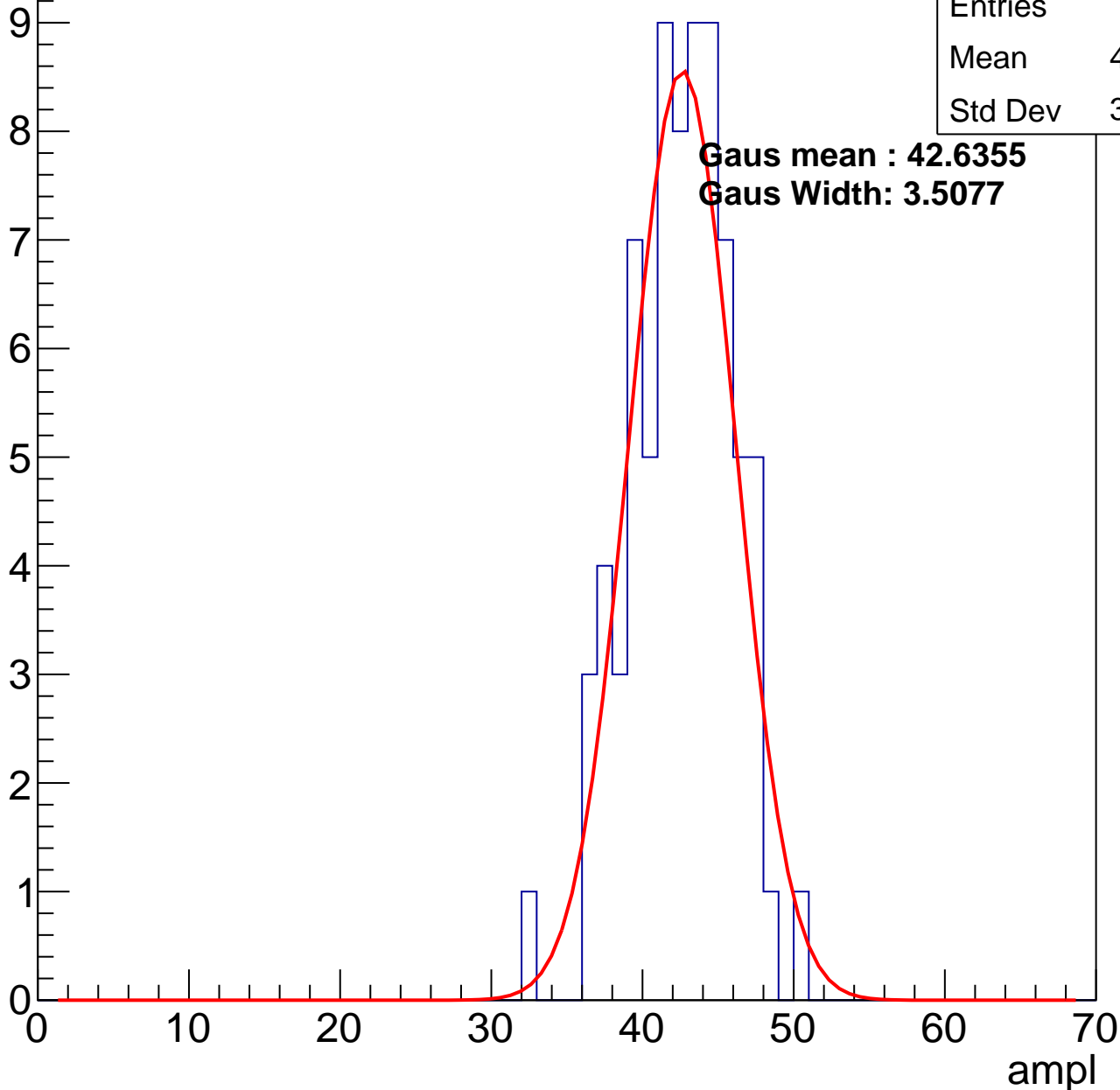
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42.09
Std Dev	3.343

Gaus mean : 42.6355

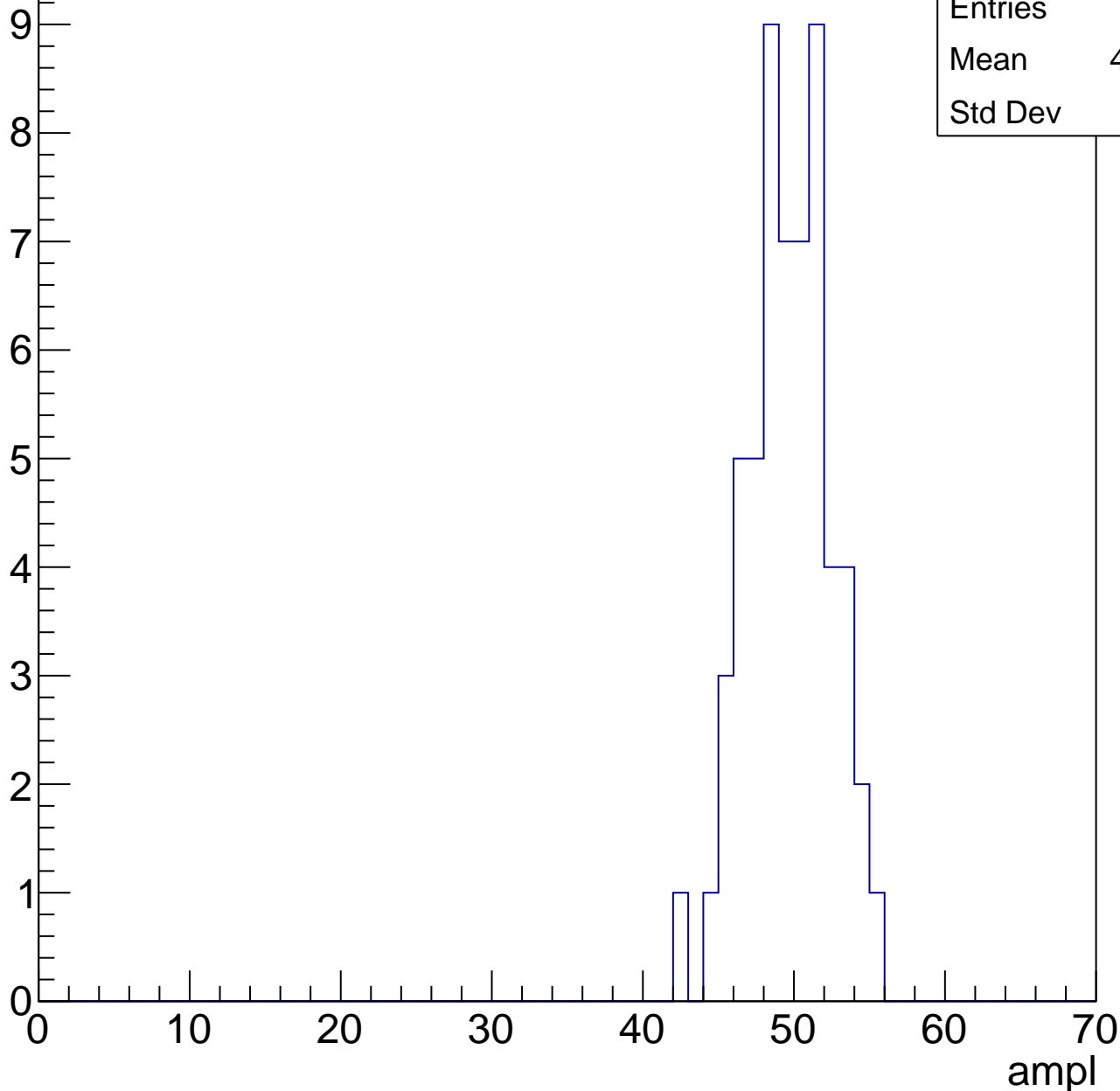
Gaus Width: 3.5077



B1L102S, U9-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

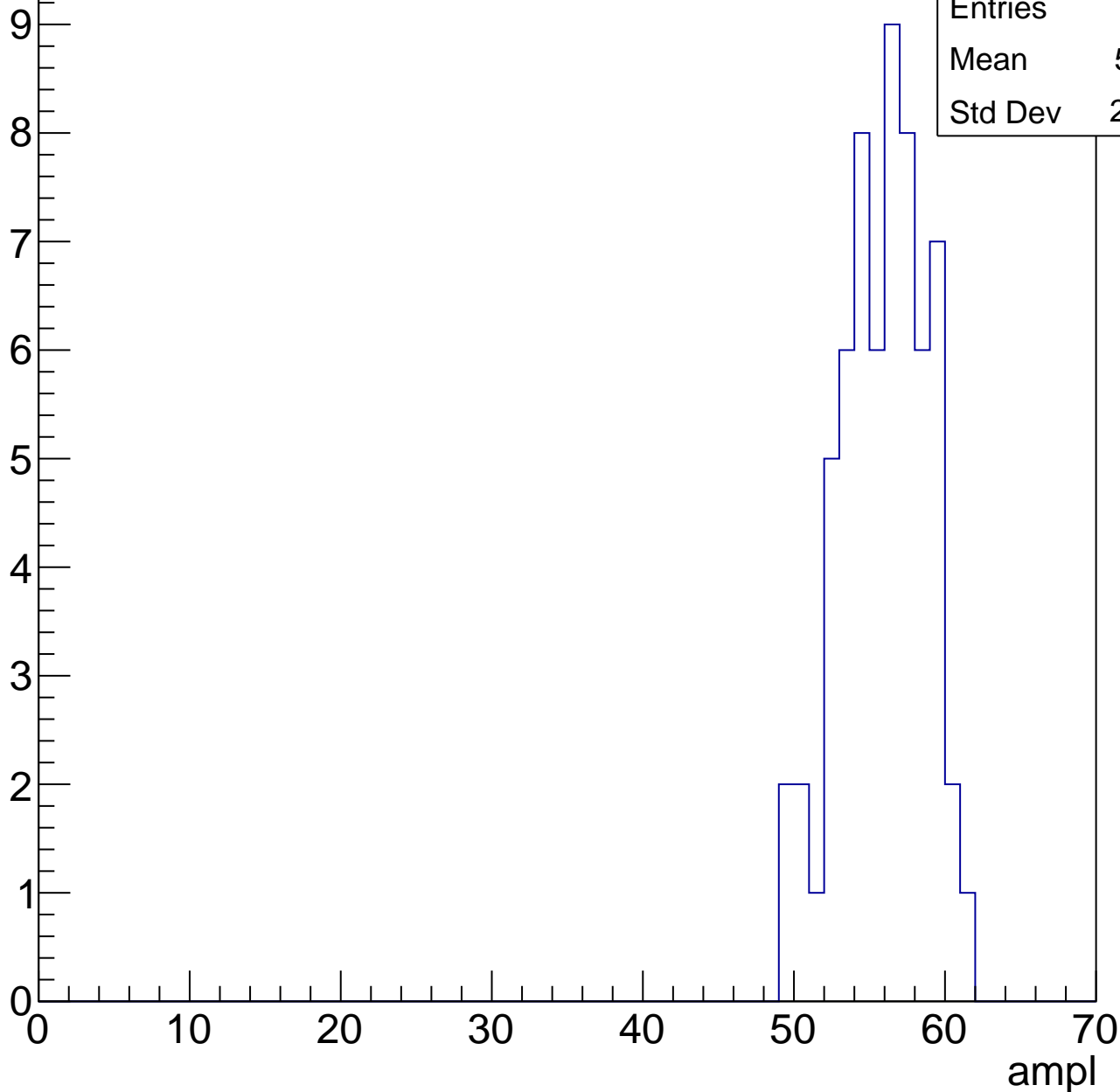


Entries	58
Mean	49.19
Std Dev	2.7

B1L102S, U9-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



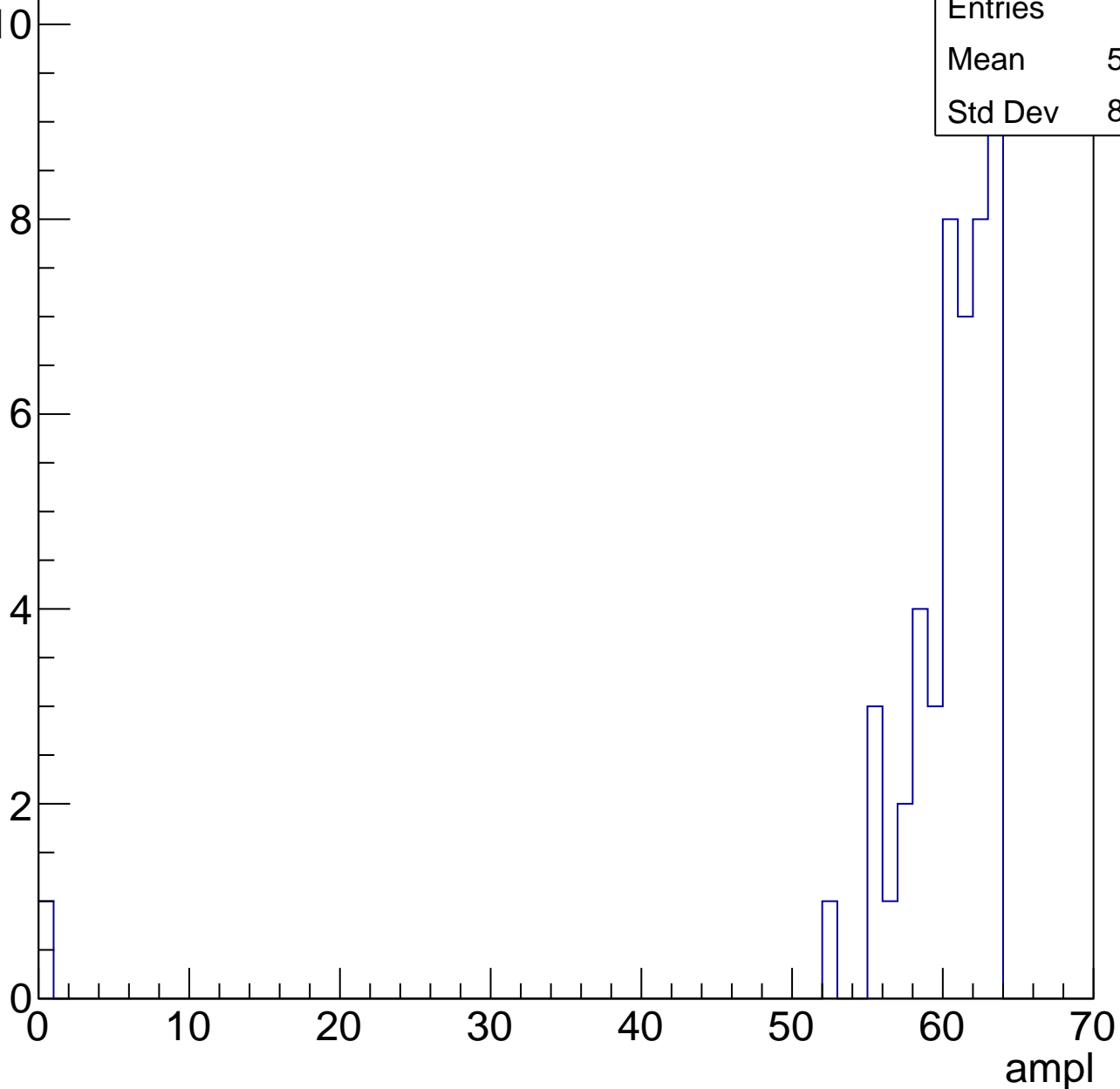
Entries	63
Mean	55.41
Std Dev	2.809

B1L102S, U9-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

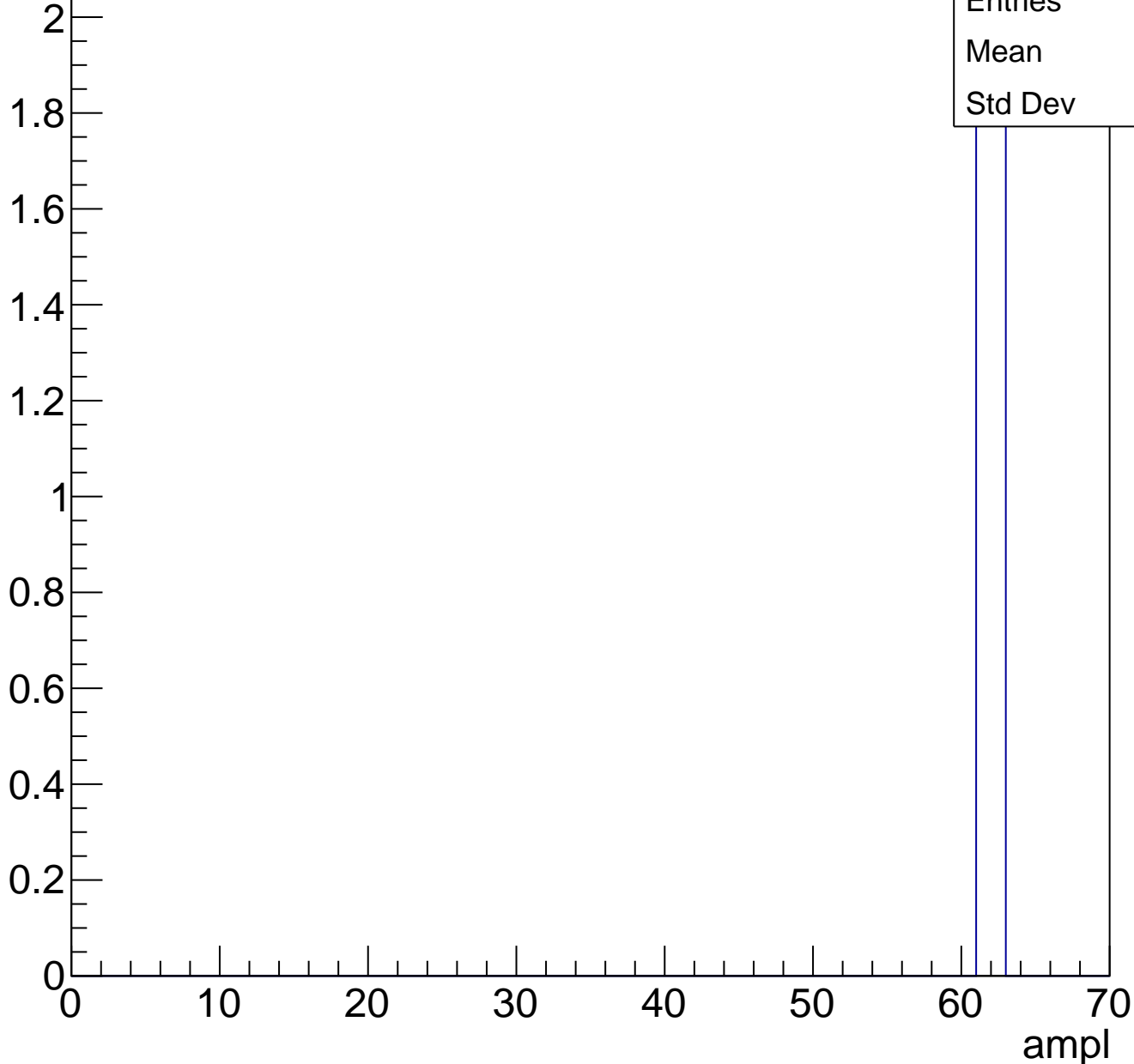
Entries	48
Mean	58.94
Std Dev	8.975



B1L102S, U9-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch24, adc0

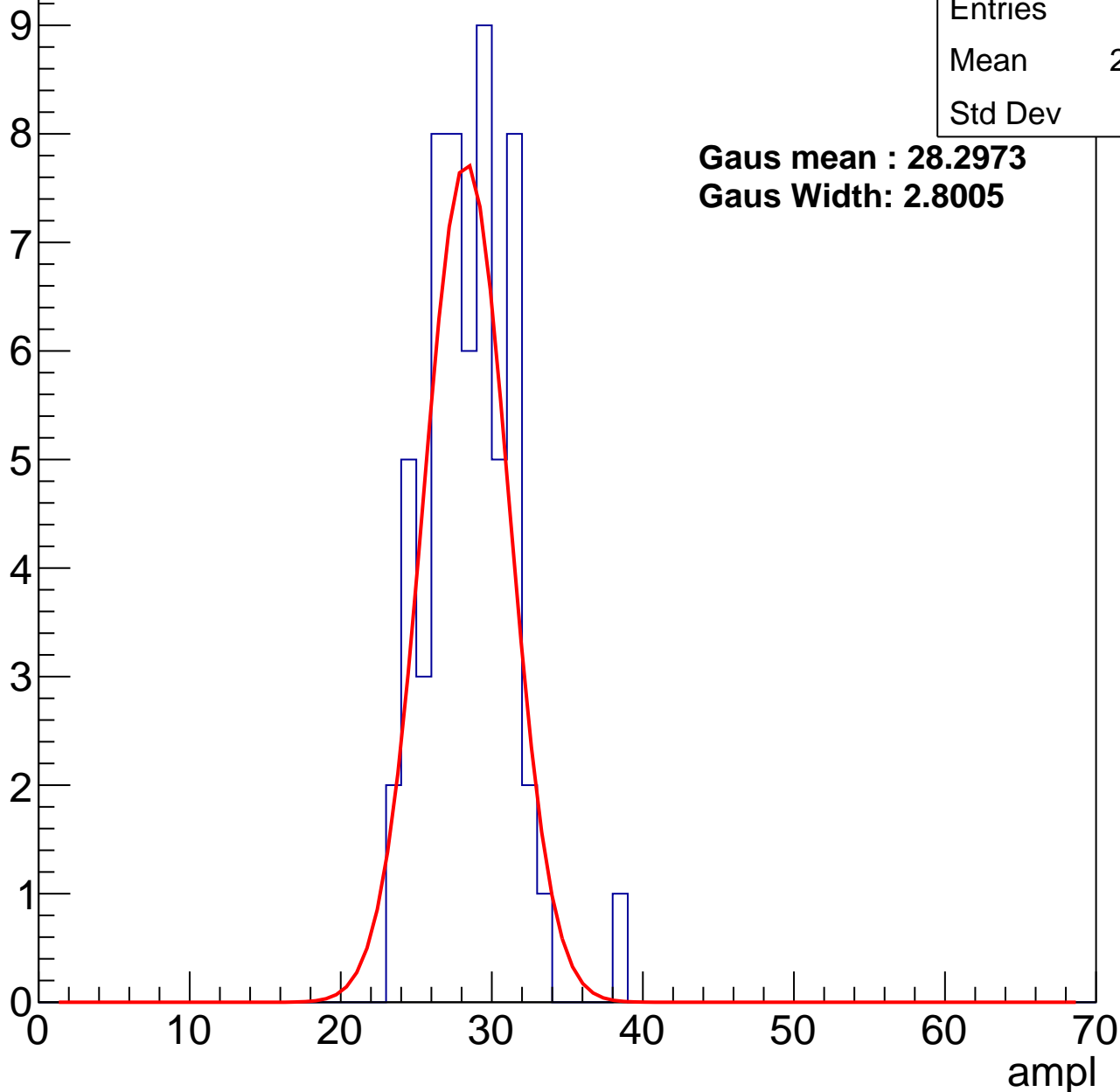
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.05
Std Dev	2.8

Gaus mean : 28.2973

Gaus Width: 2.8005



B1L102S, U9-ch24, adc1

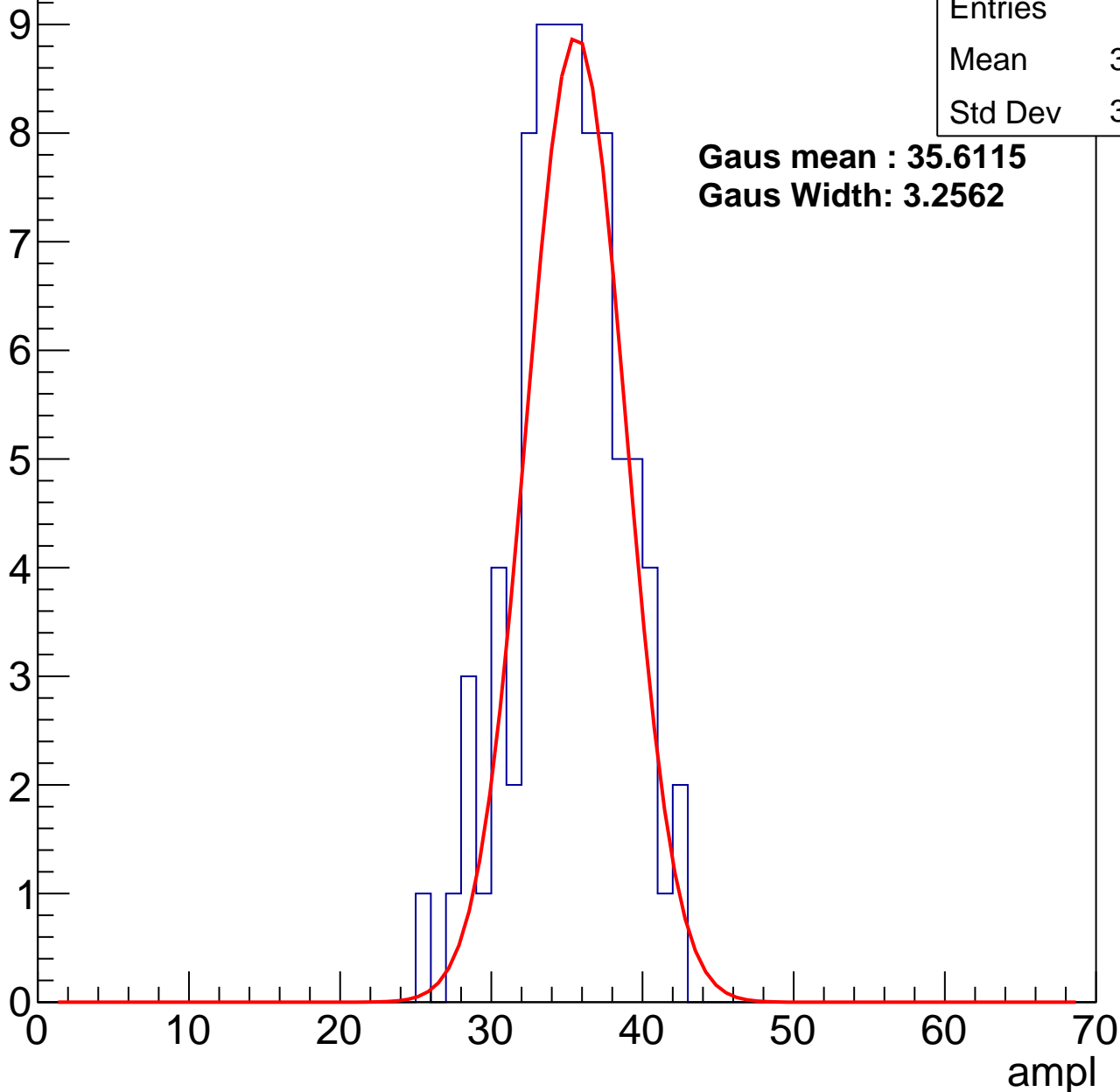
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.69
Std Dev	3.513

Gaus mean : 35.6115

Gaus Width: 3.2562

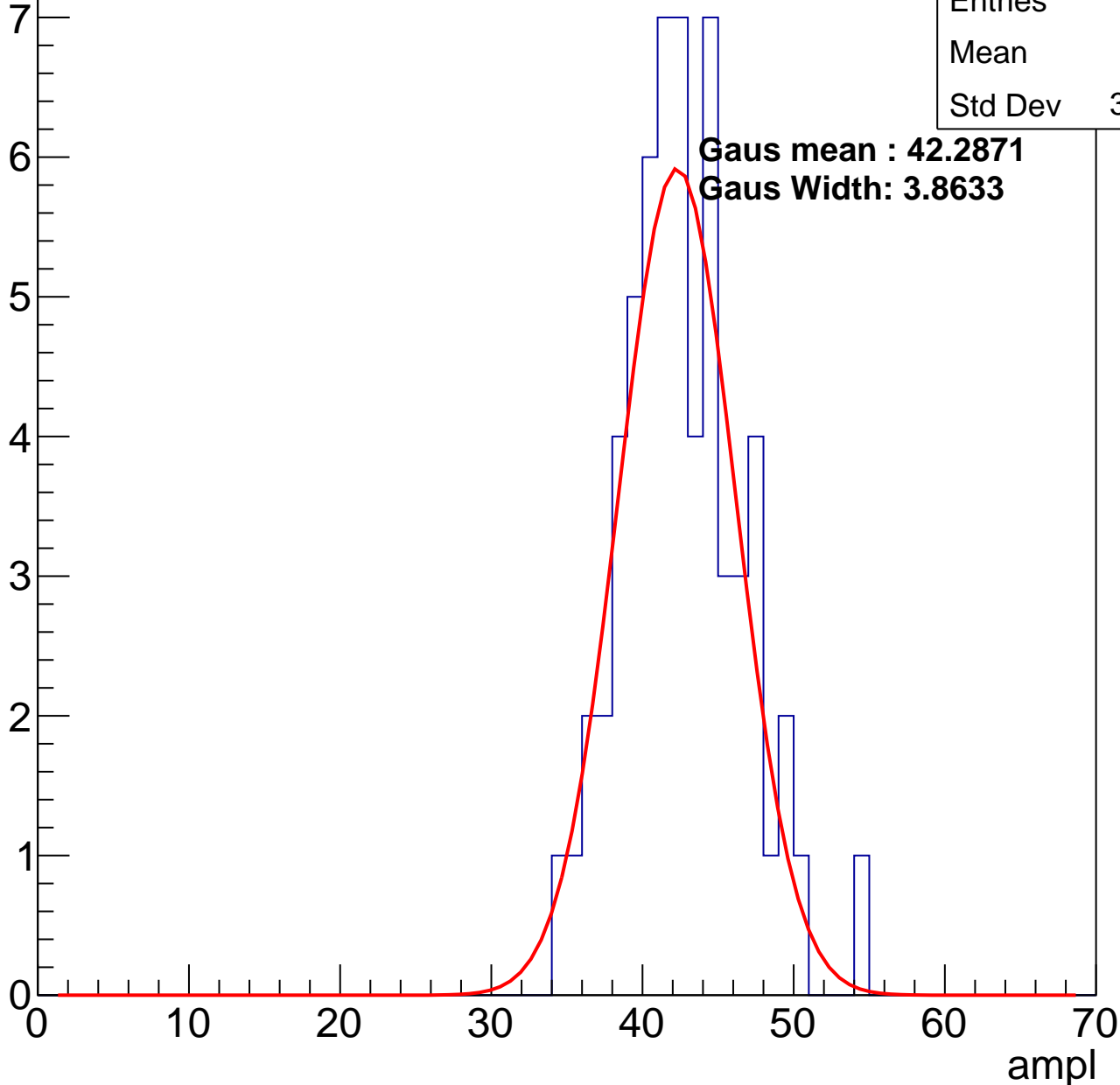


B1L102S, U9-ch24, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.2
Std Dev	3.887

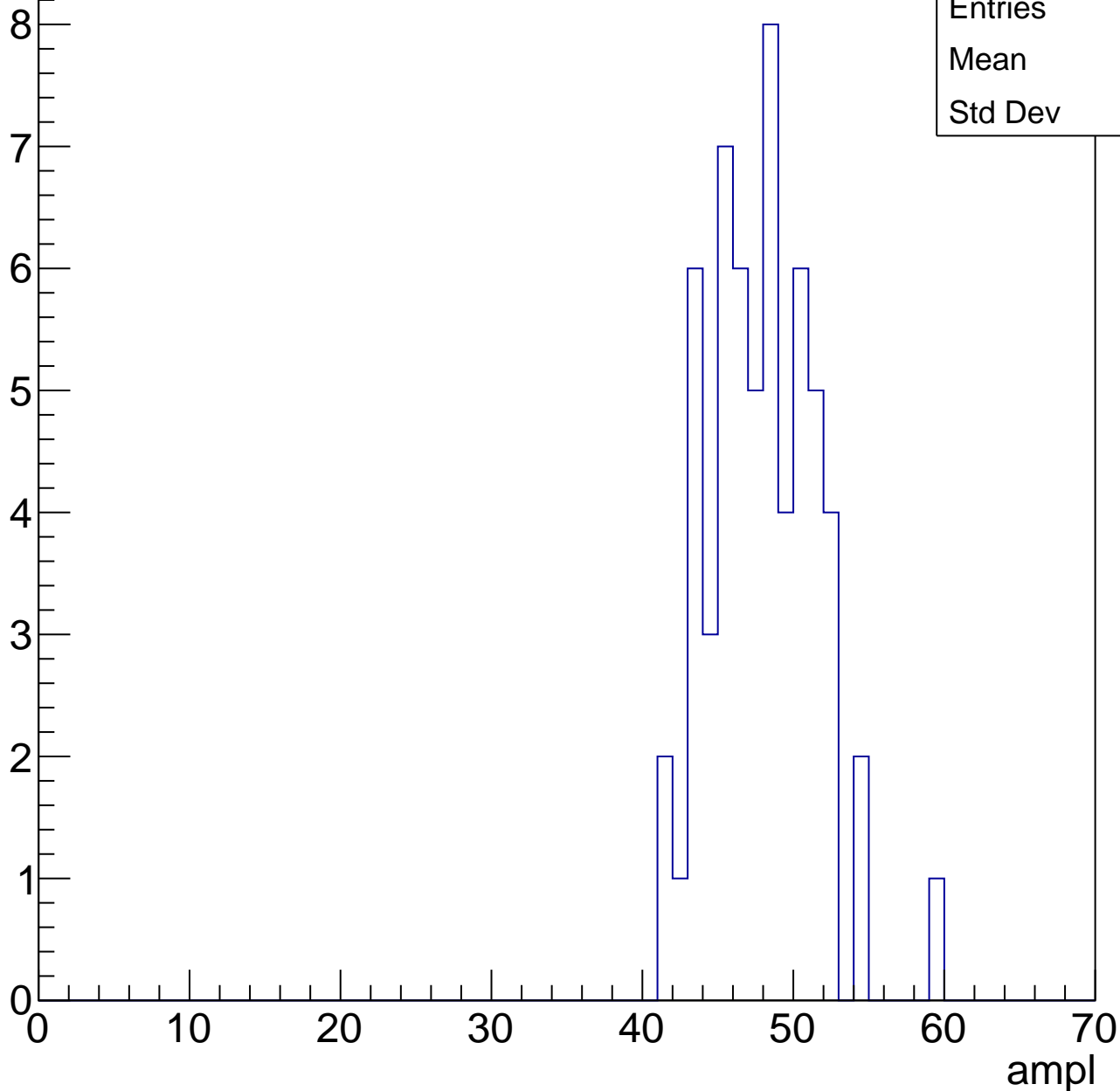


B1L102S, U9-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.5
Std Dev	3.51



B1L102S, U9-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

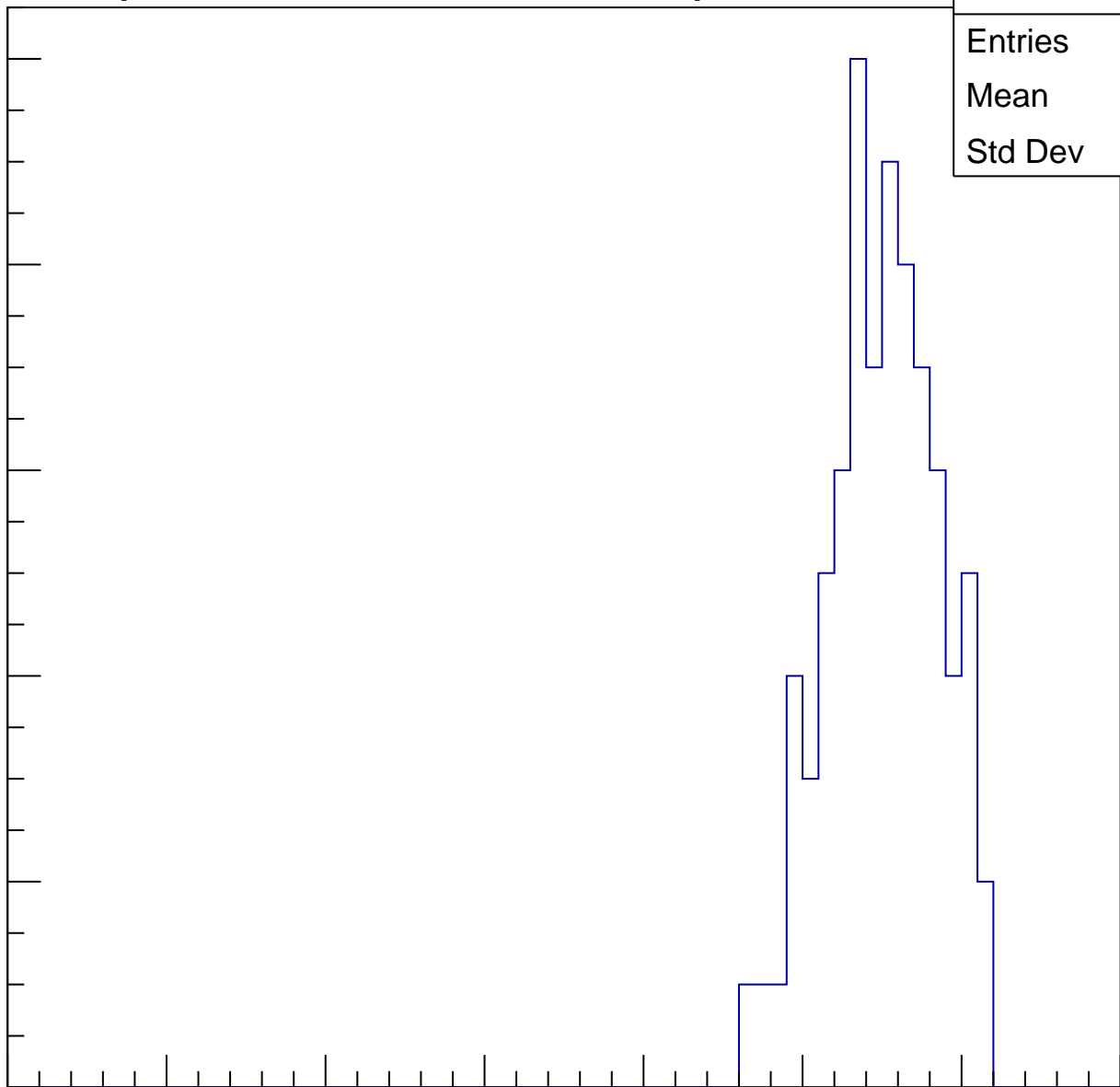
Entries	79
Mean	54.56
Std Dev	3.43

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

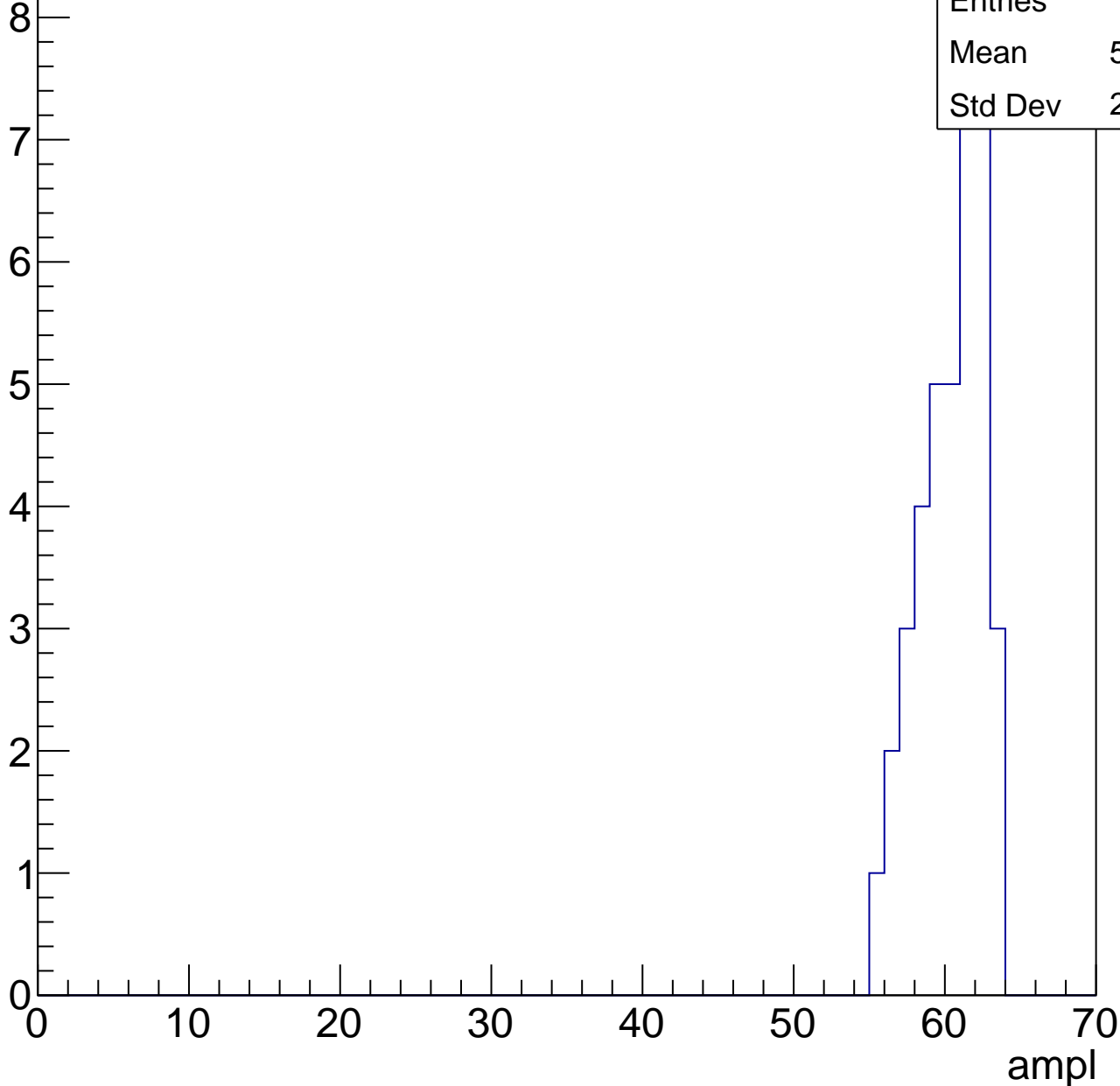


B1L102S, U9-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.95
Std Dev	2.099

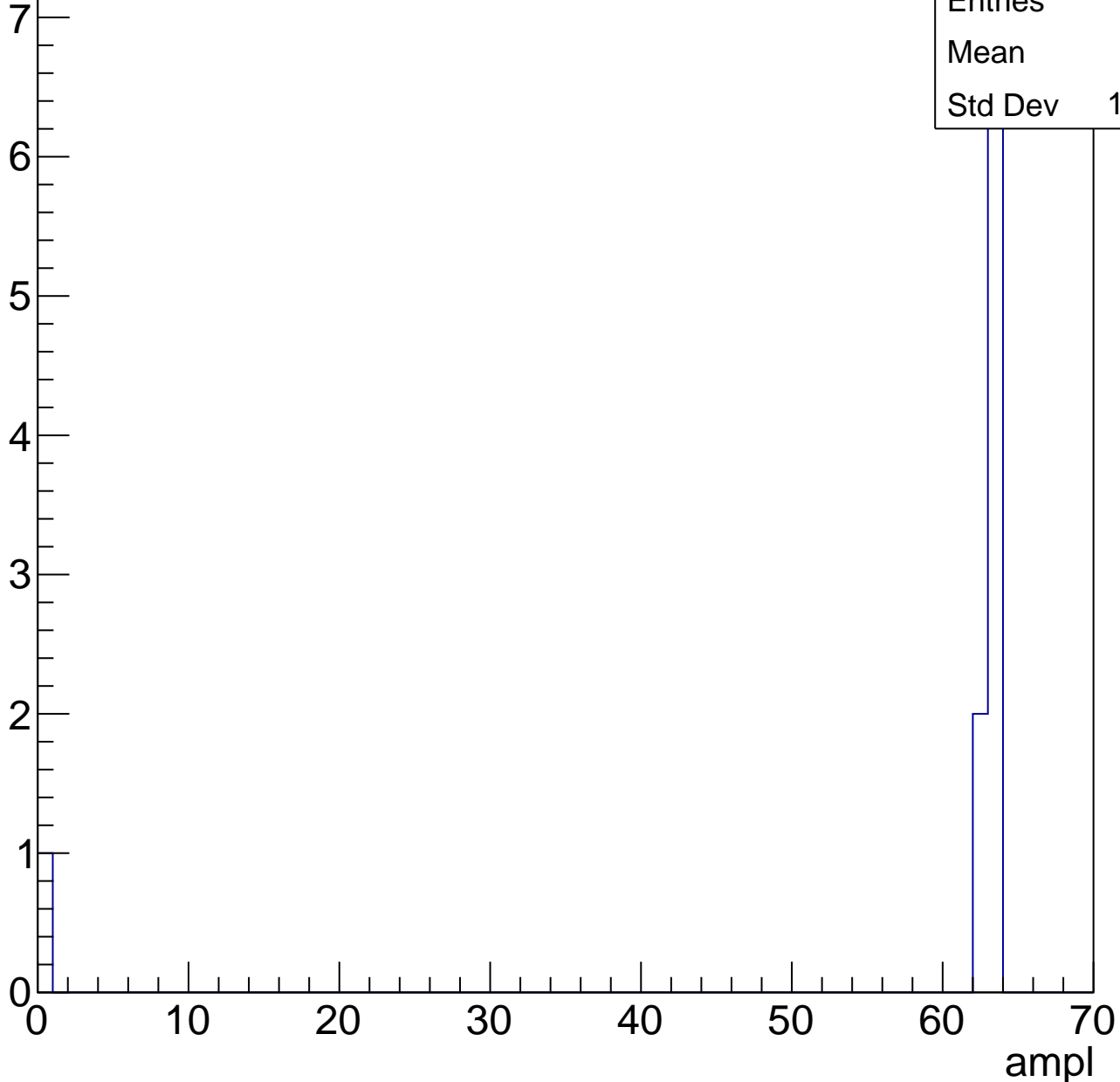


B1L102S, U9-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	56.5
Std Dev	18.84



B1L102S, U9-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch25, adc0

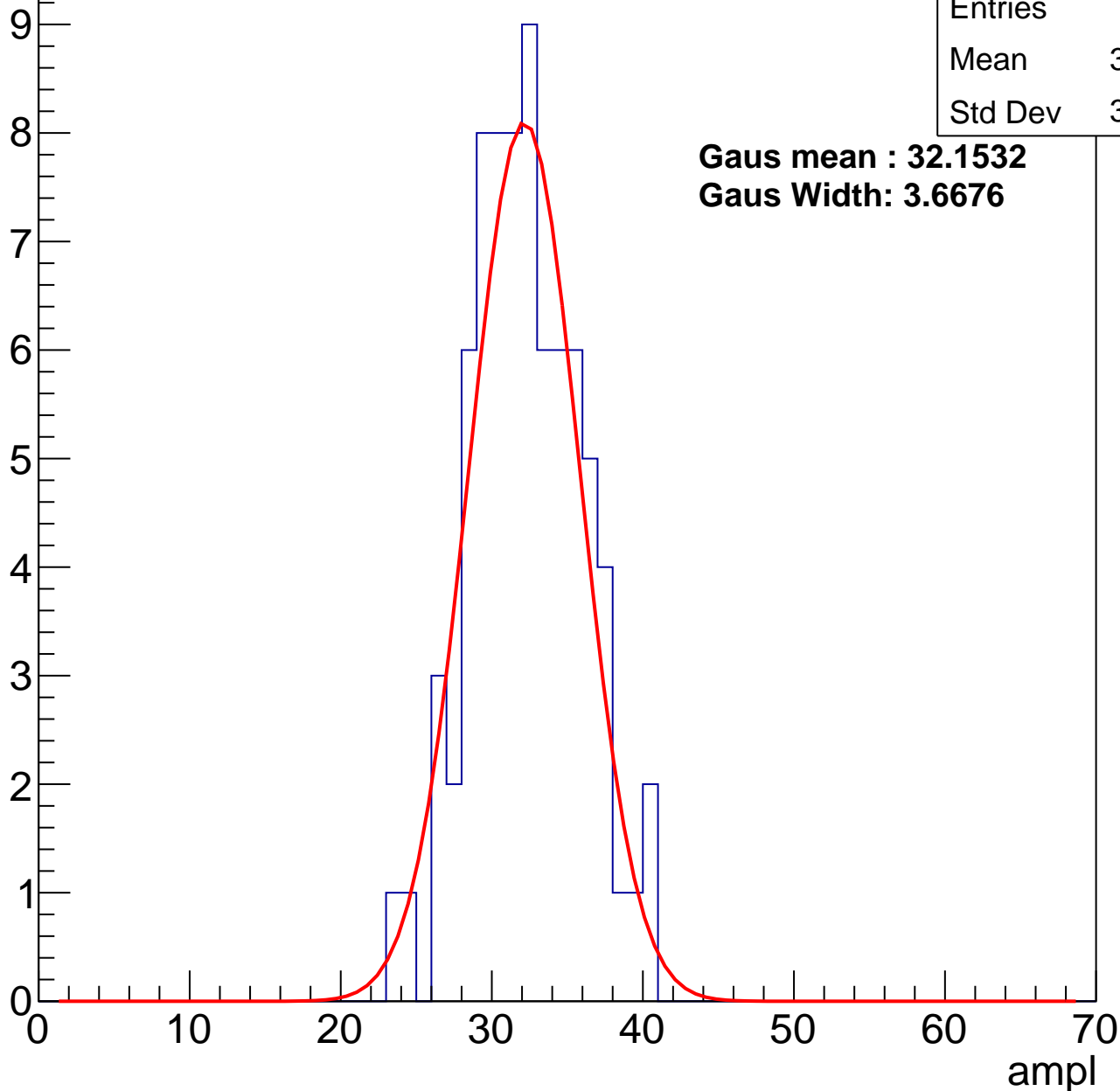
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	31.84
Std Dev	3.579

Gaus mean : 32.1532

Gaus Width: 3.6676



B1L102S, U9-ch25, adc1

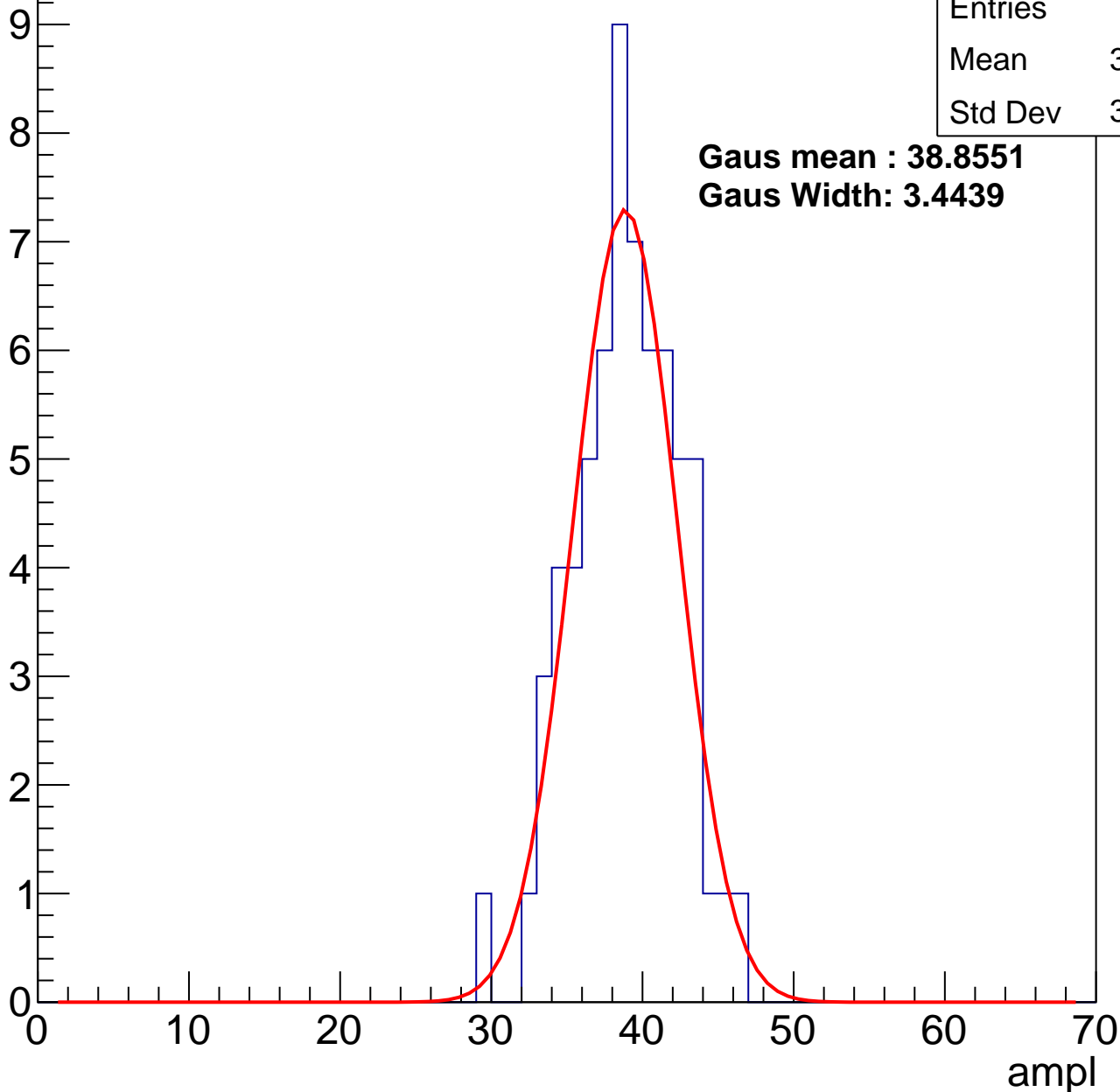
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	38.45
Std Dev	3.383

Gaus mean : 38.8551

Gaus Width: 3.4439



B1L102S, U9-ch25, adc2

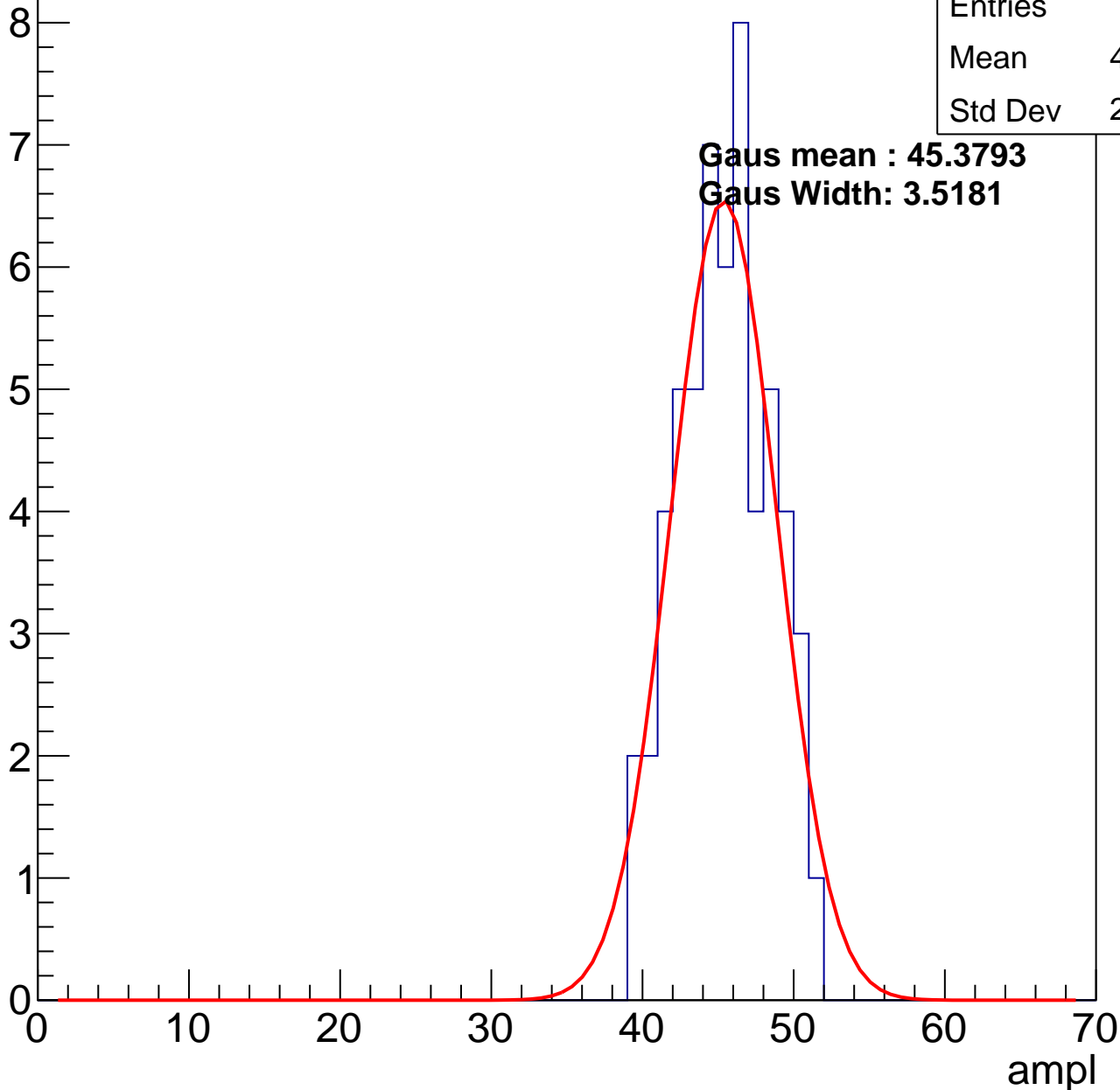
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	44.96
Std Dev	2.994

Gaus mean : 45.3793

Gaus Width: 3.5181

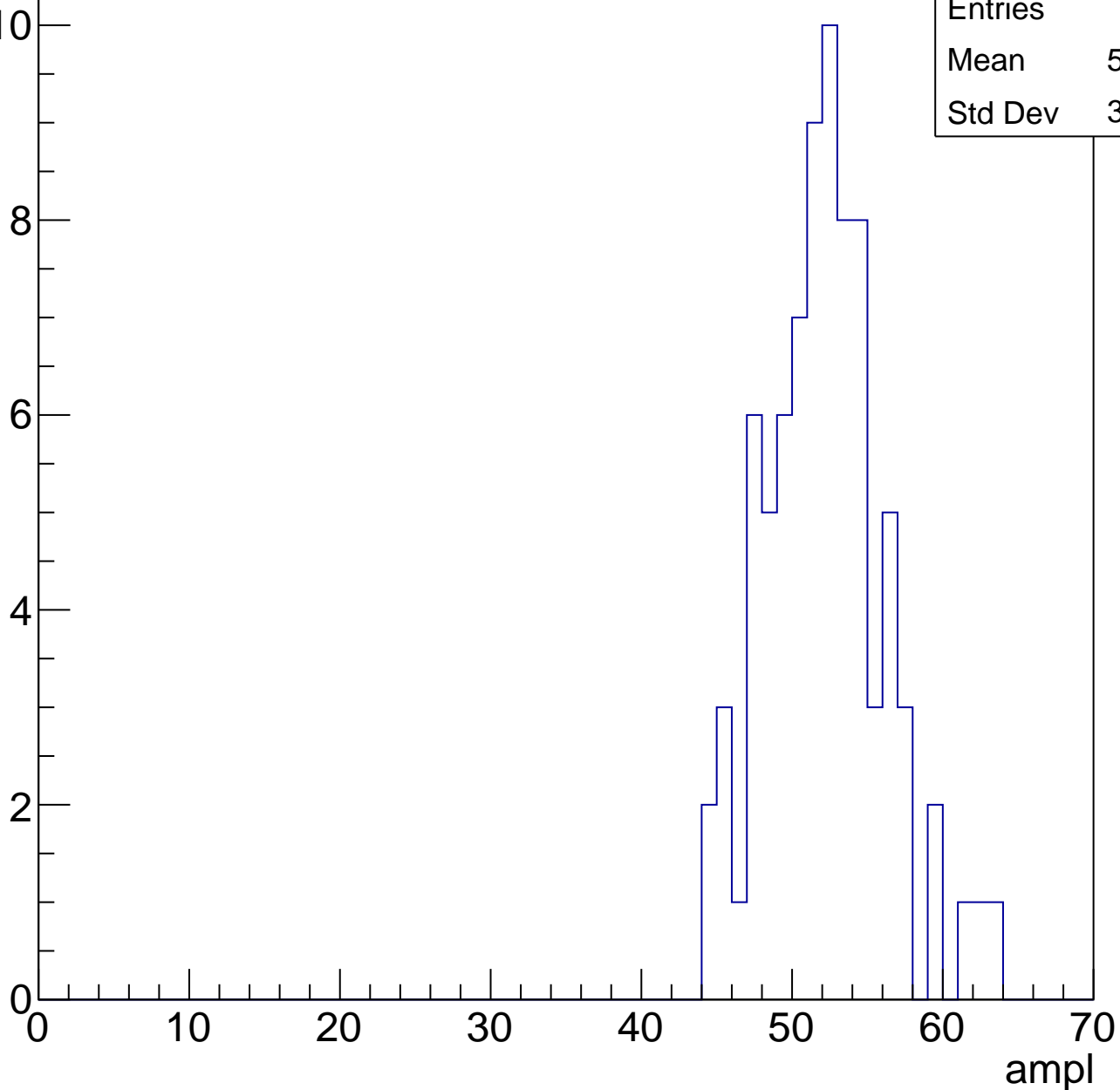


B1L102S, U9-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	51.73
Std Dev	3.925

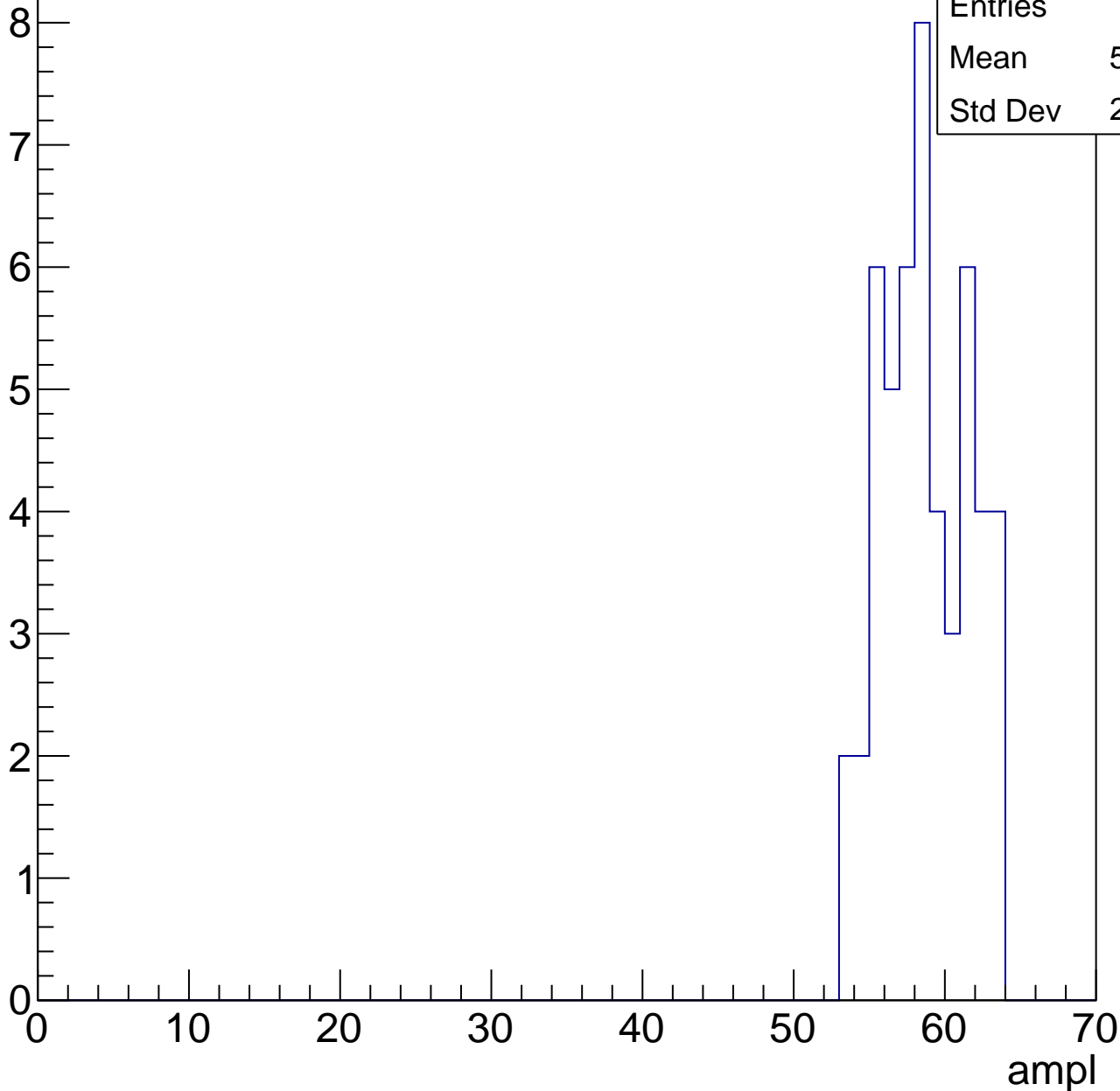


B1L102S, U9-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.24
Std Dev	2.804



B1L102S, U9-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

29

Mean

58.34

Std Dev

11.2

ampl

0

10

20

30

40

50

60

70

B1L102S, U9-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch26, adc0

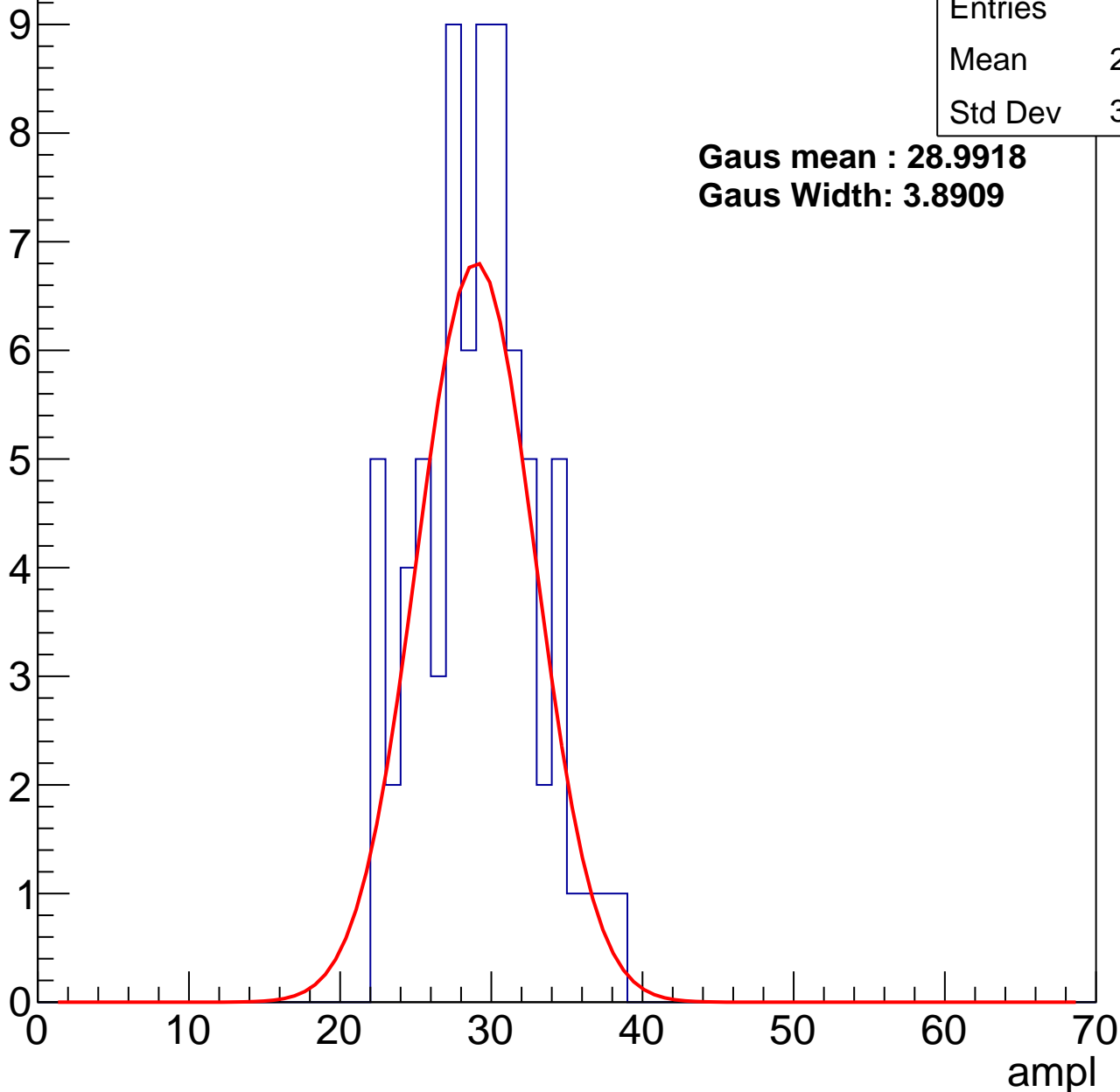
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.72
Std Dev	3.722

Gaus mean : 28.9918

Gaus Width: 3.8909



B1L102S, U9-ch26, adc1

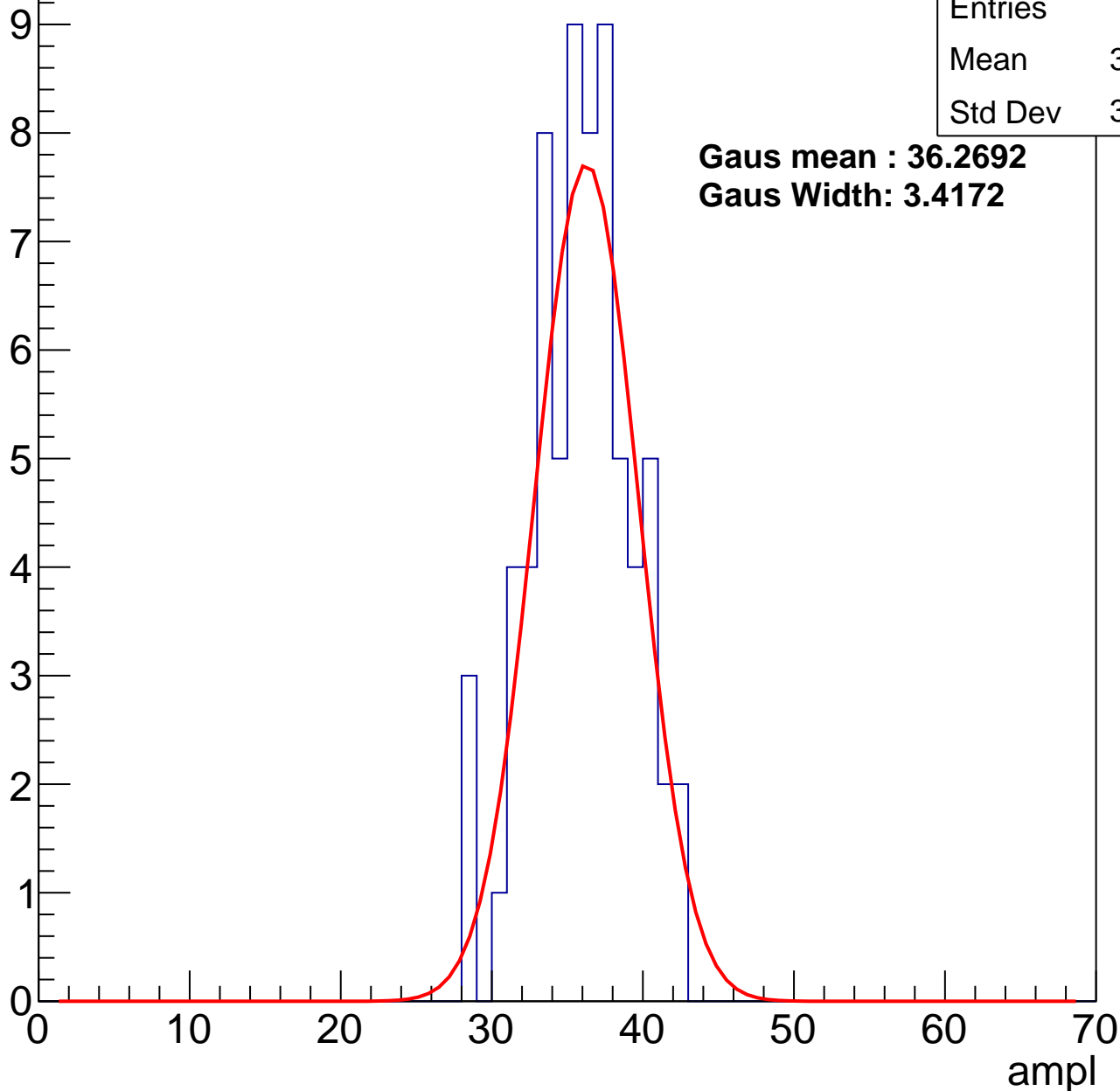
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.48
Std Dev	3.278

Gaus mean : 36.2692

Gaus Width: 3.4172



B1L102S, U9-ch26, adc2

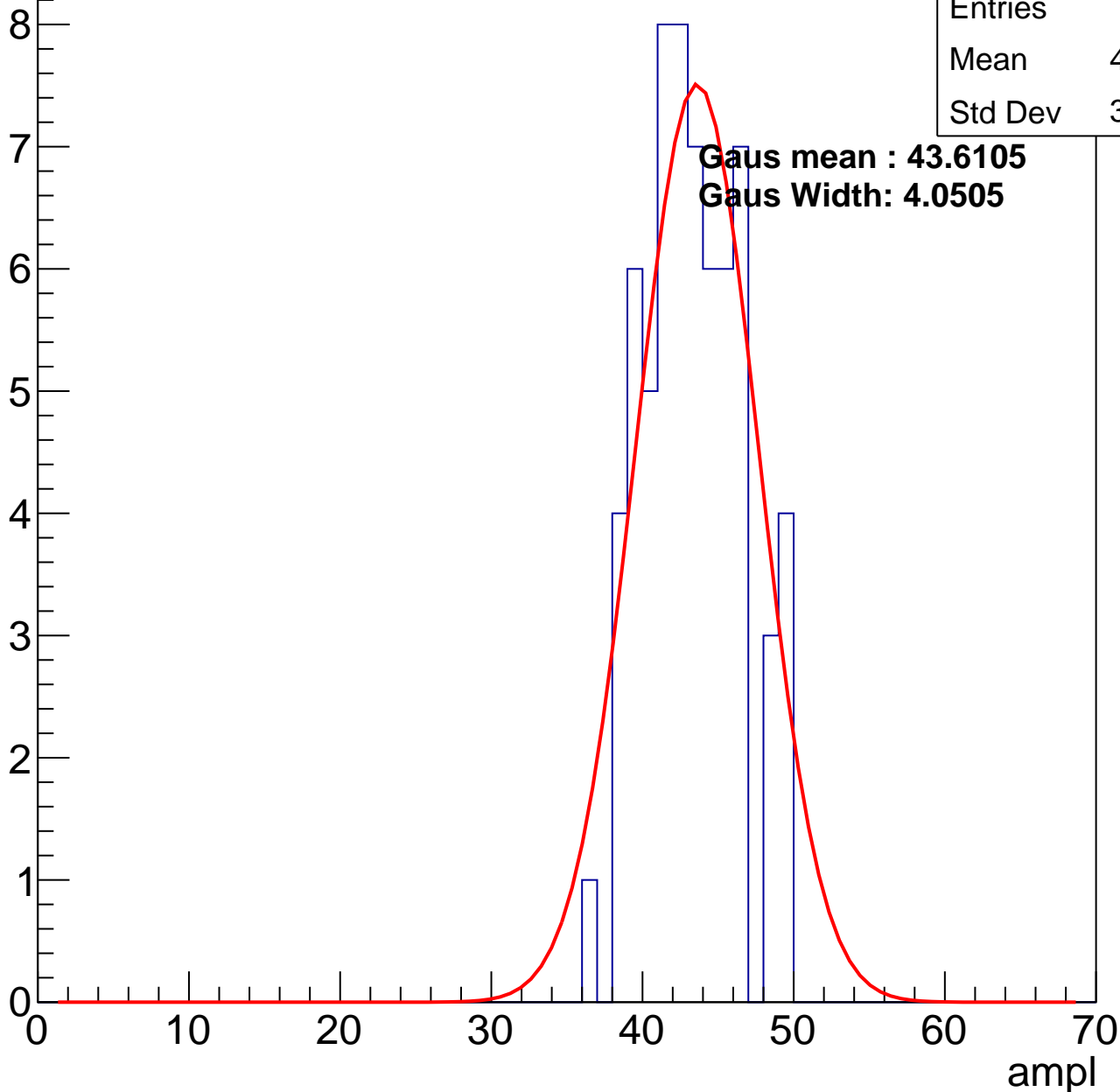
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.82
Std Dev	3.137

Gaus mean : 43.6105

Gaus Width: 4.0505

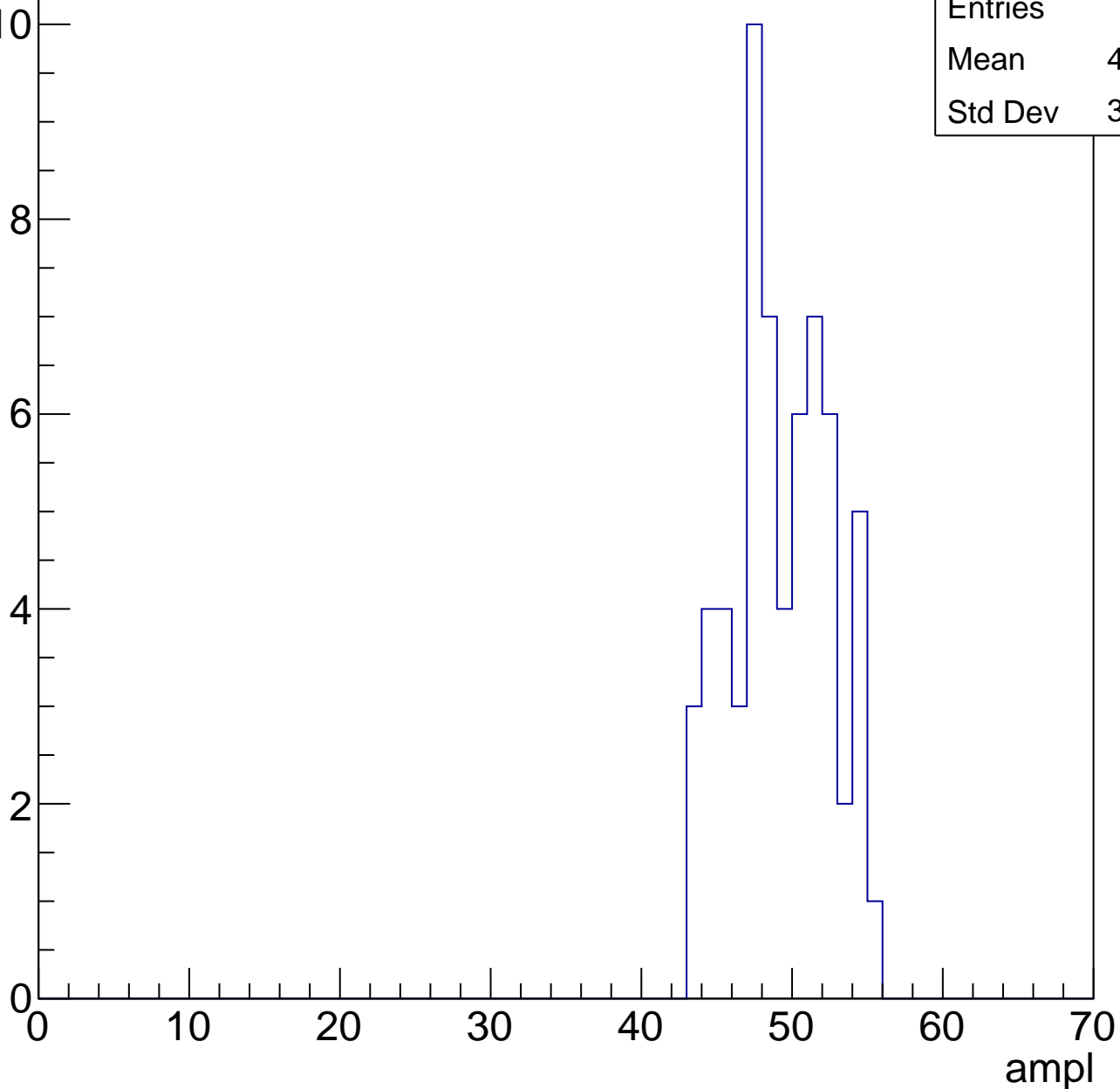


B1L102S, U9-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.79
Std Dev	3.173

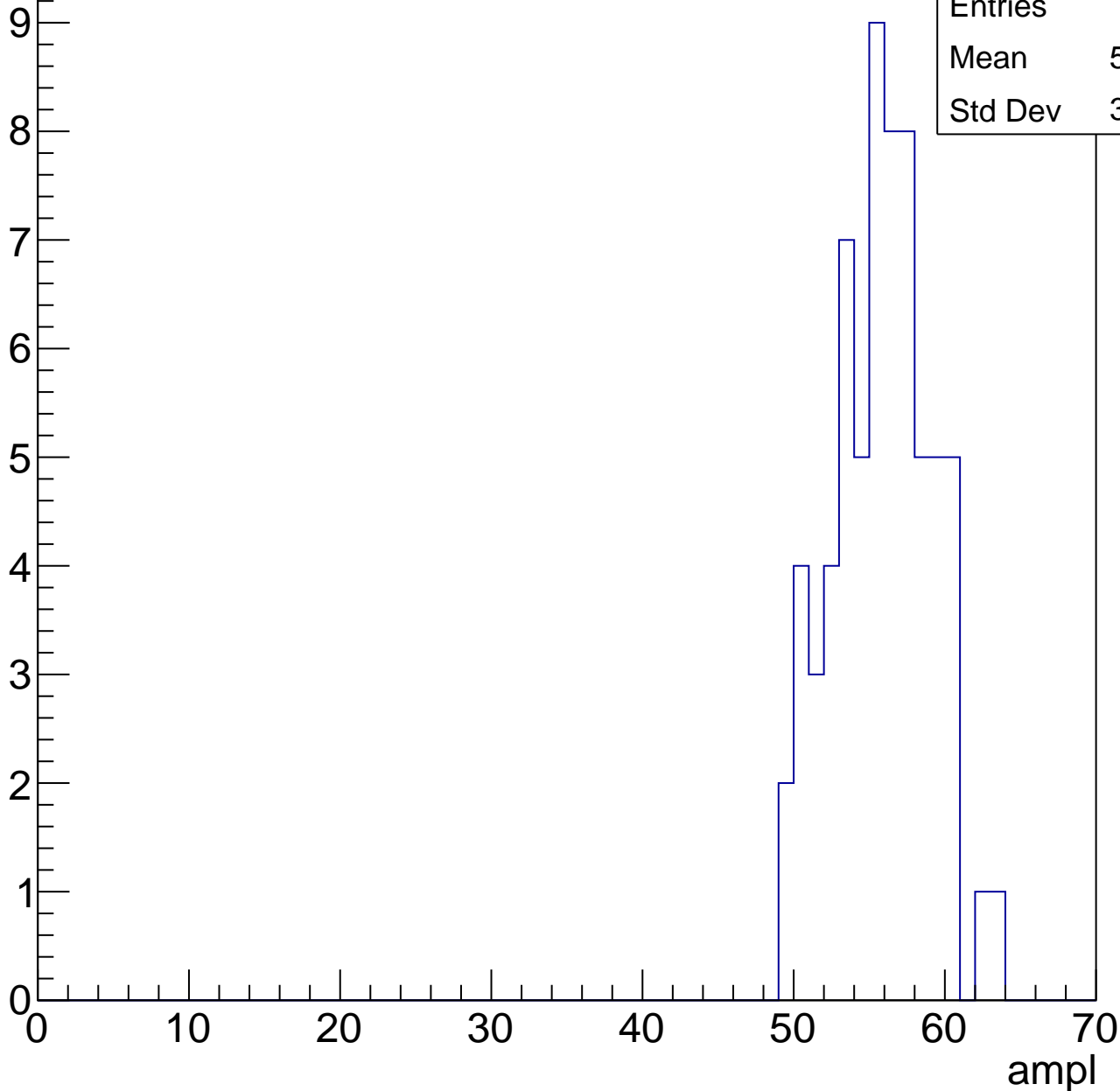


B1L102S, U9-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	55.36
Std Dev	3.194

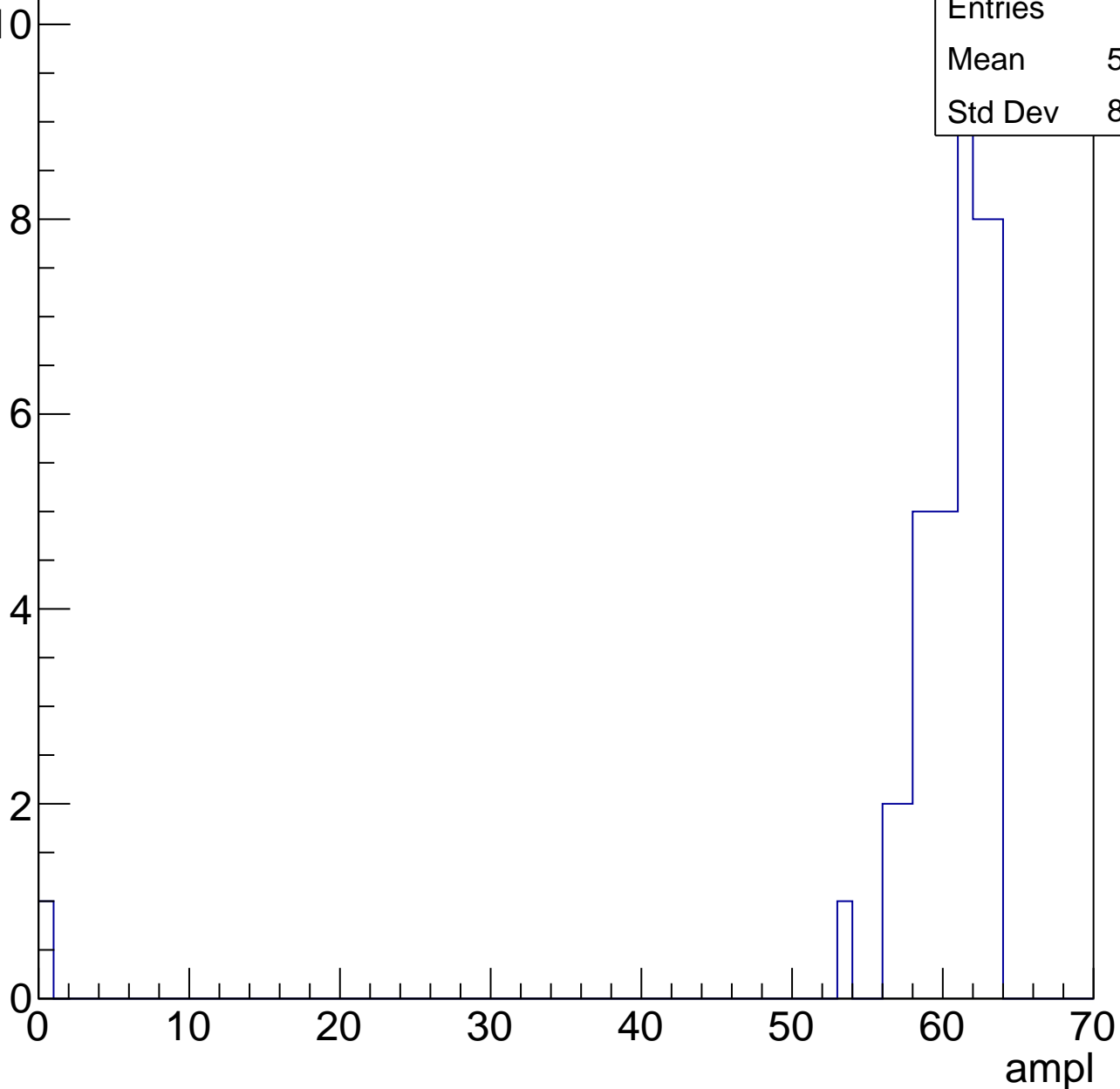


B1L102S, U9-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.02
Std Dev	8.983



B1L102S, U9-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch27, adc0

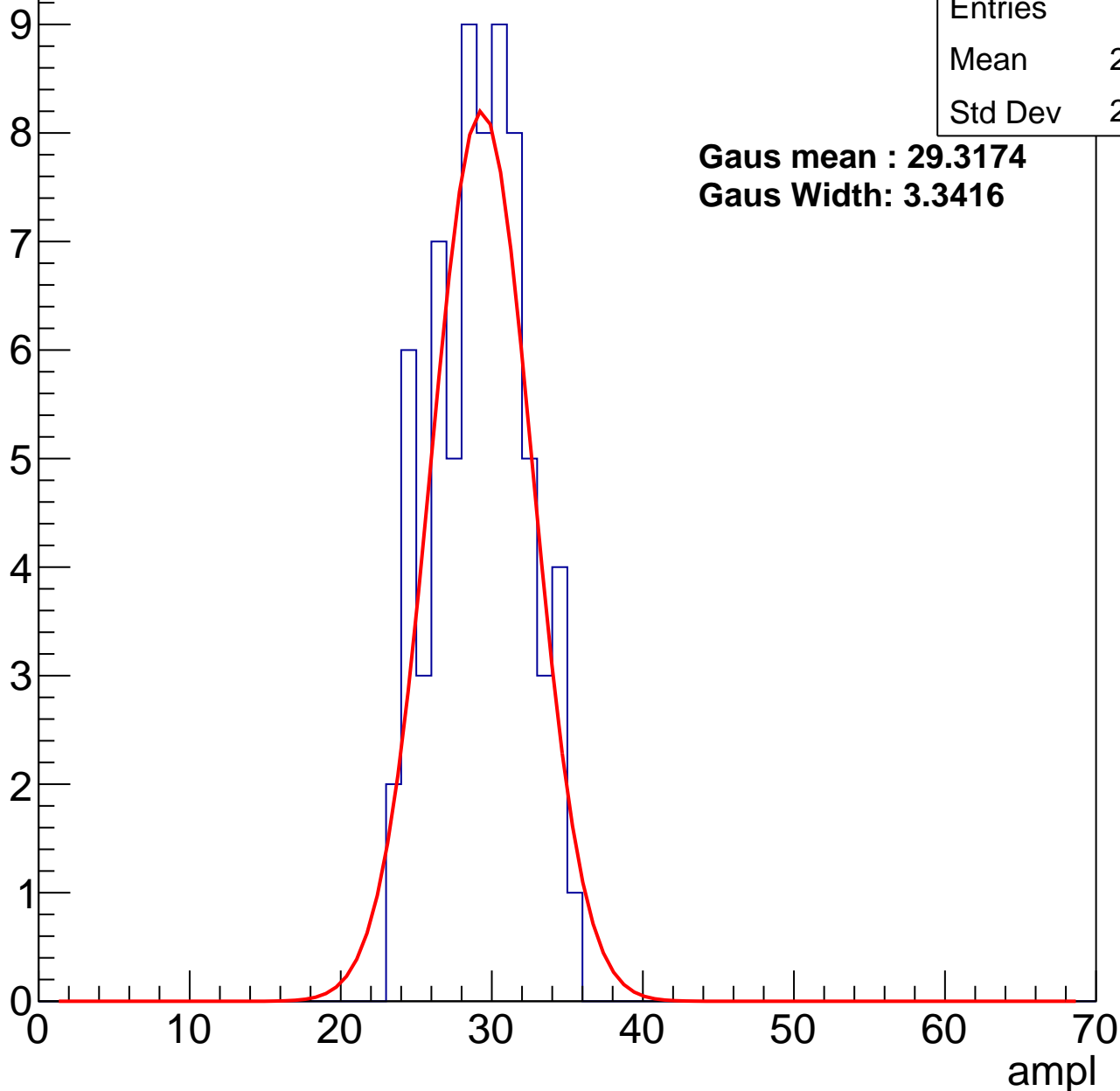
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.77
Std Dev	2.996

Gaus mean : 29.3174

Gaus Width: 3.3416



B1L102S, U9-ch27, adc1

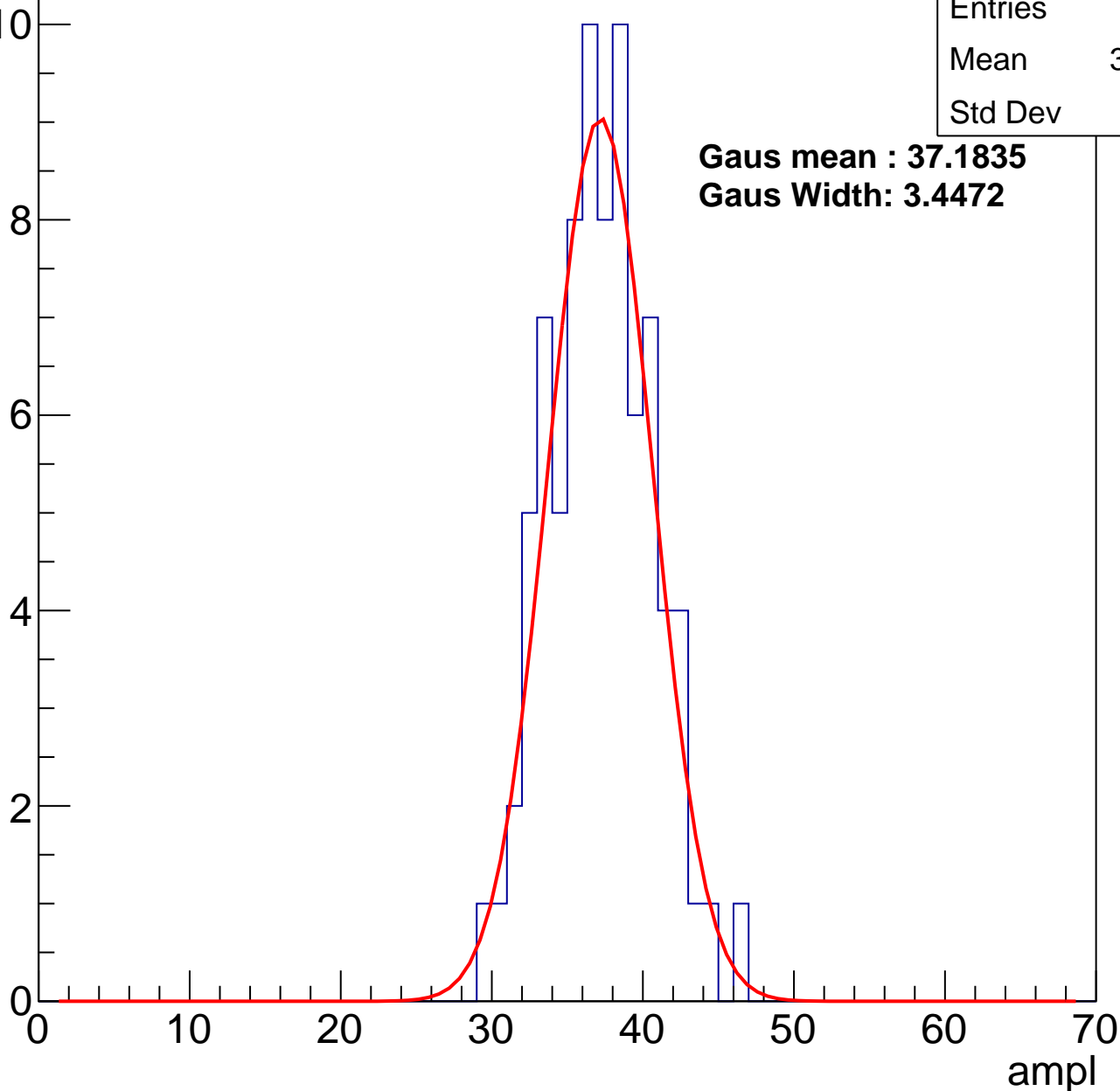
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.75
Std Dev	3.39

Gaus mean : 37.1835

Gaus Width: 3.4472



B1L102S, U9-ch27, adc2

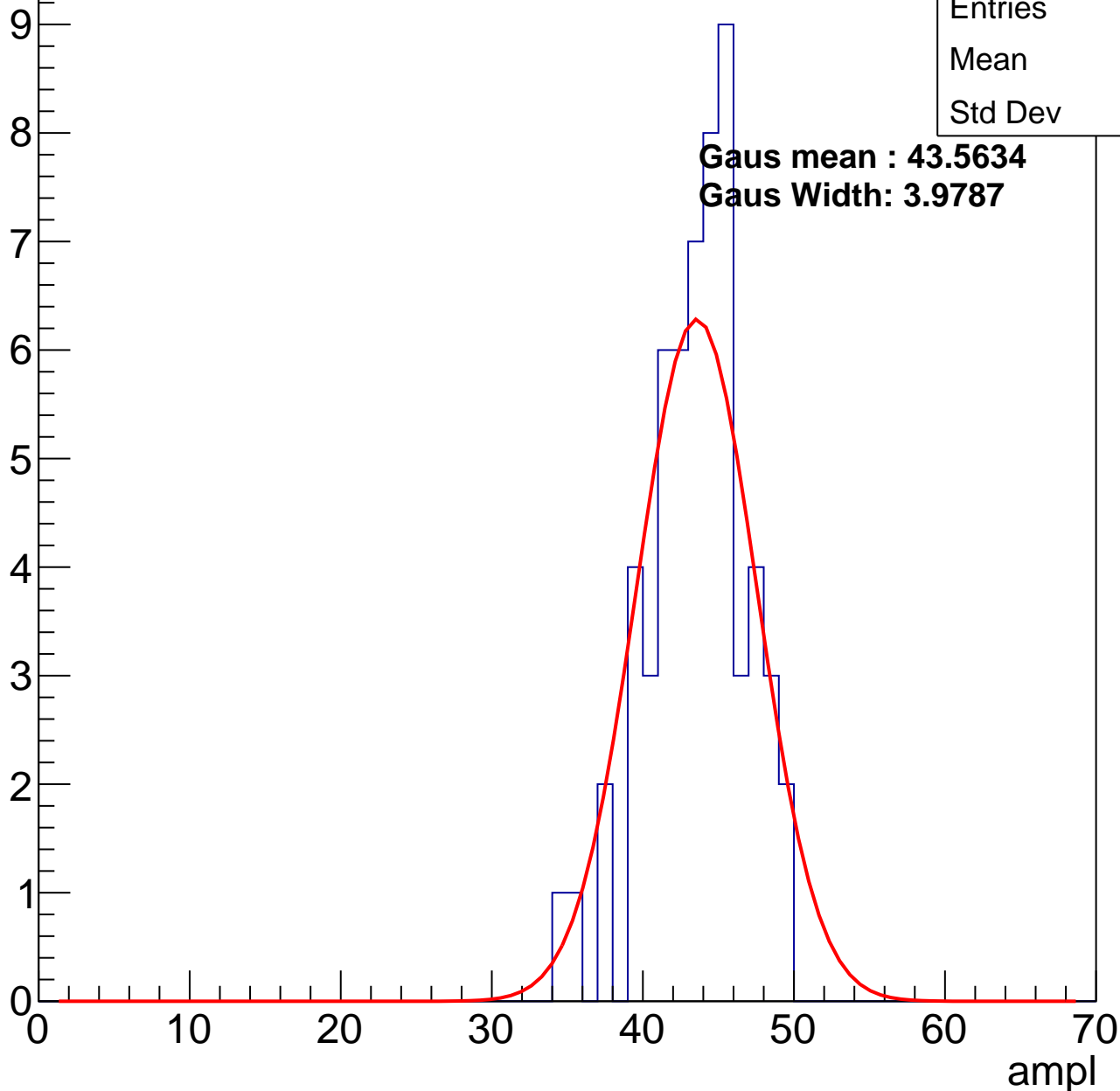
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	43.1
Std Dev	3.24

Gaus mean : 43.5634

Gaus Width: 3.9787

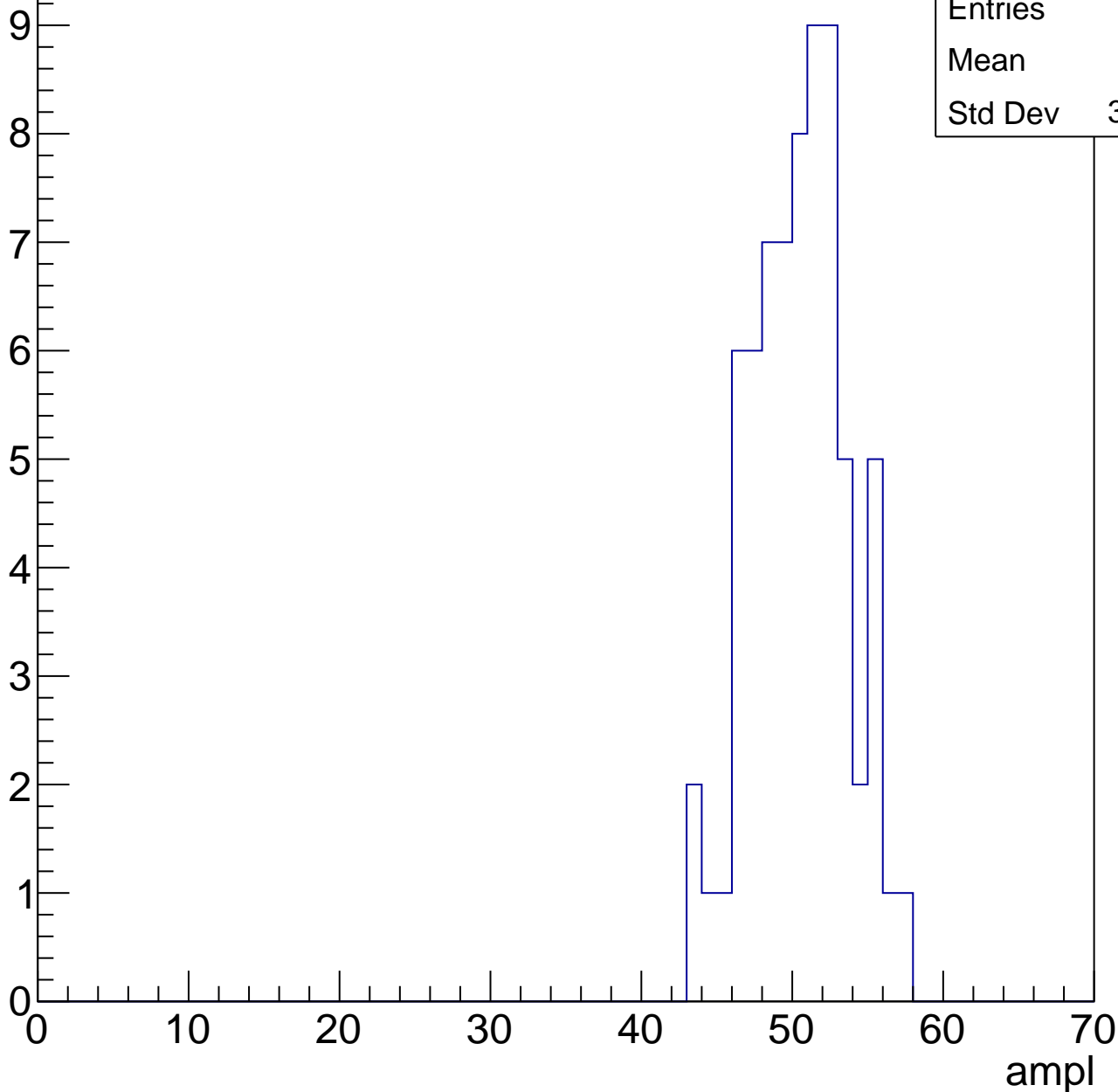


B1L102S, U9-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	50
Std Dev	3.108

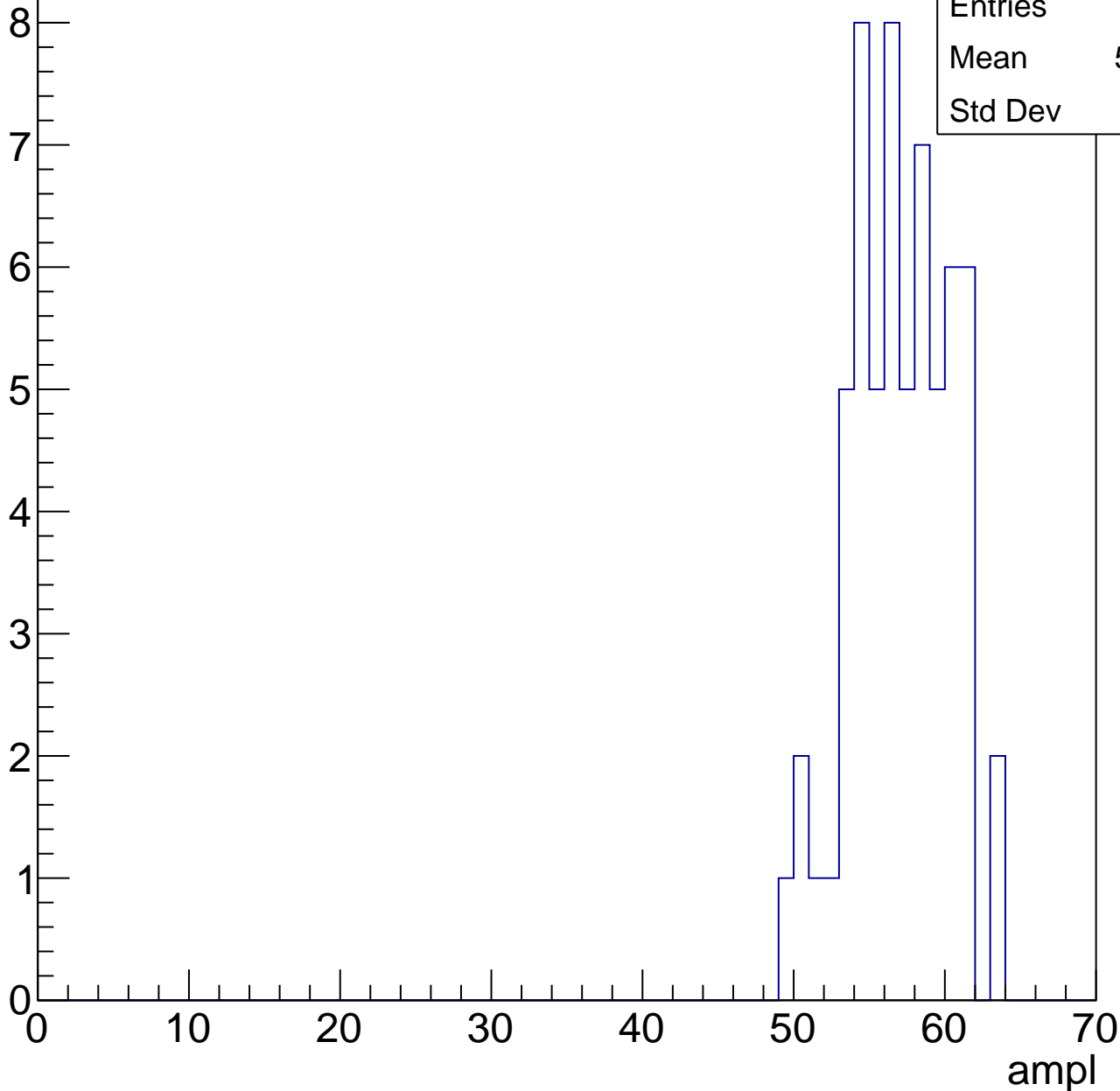


B1L102S, U9-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.61
Std Dev	3.22

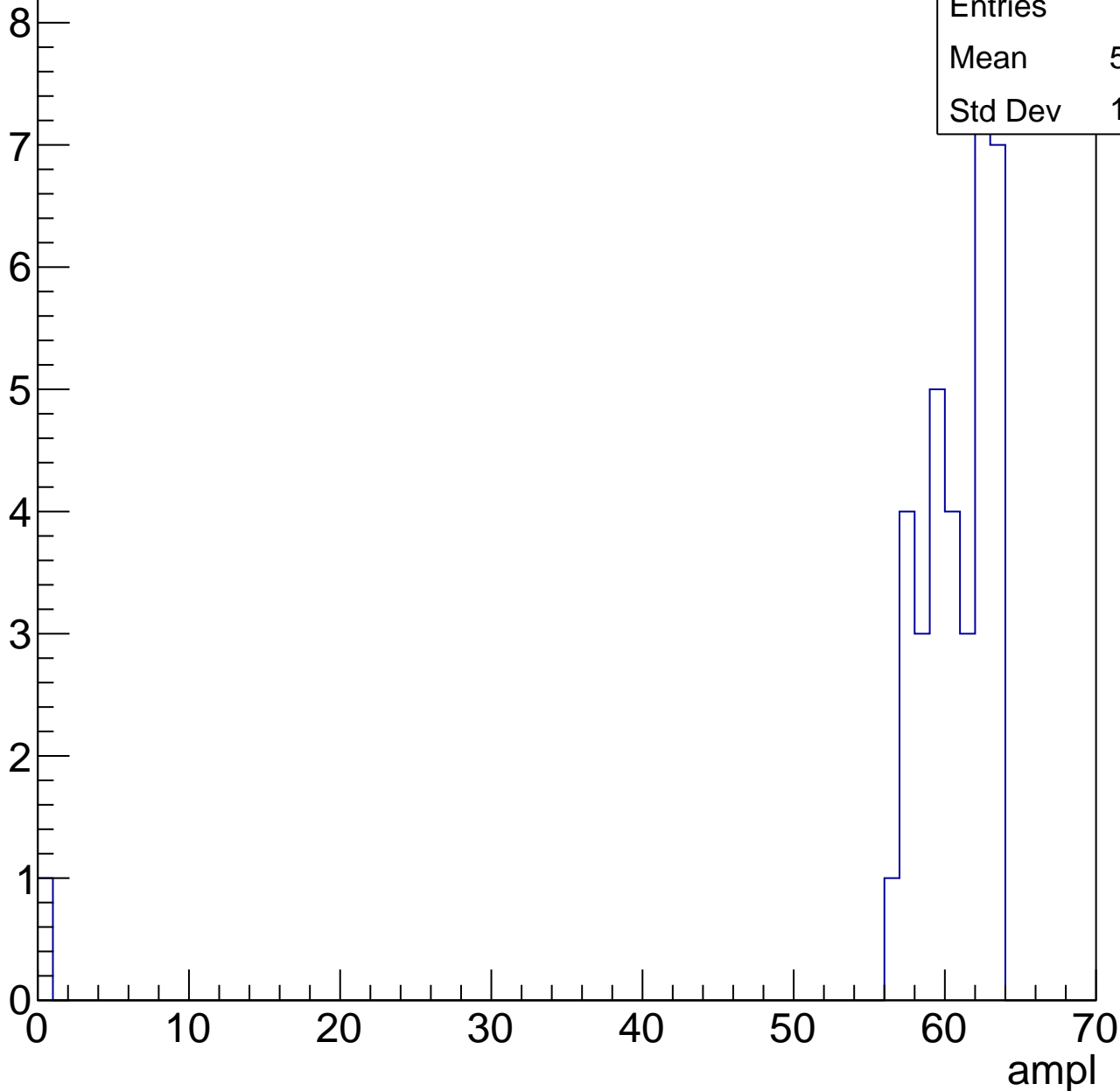


B1L102S, U9-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.69
Std Dev	10.15



B1L102S, U9-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

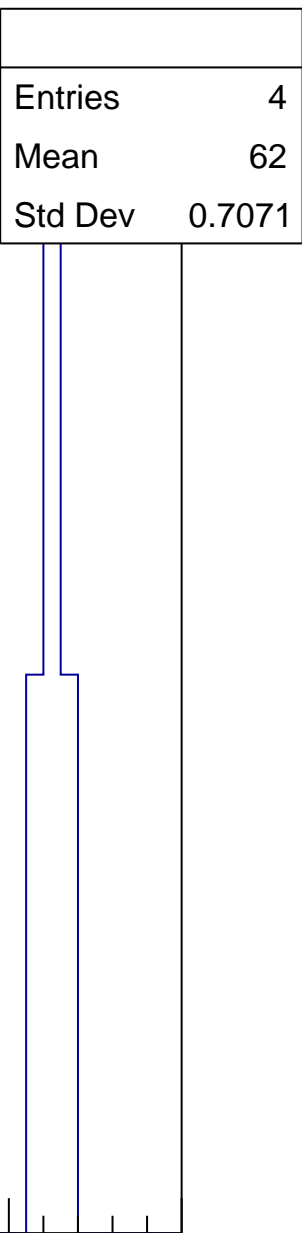
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

0 10 20 30 40 50 60 70

ampl



B1L102S, U9-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch28, adc0

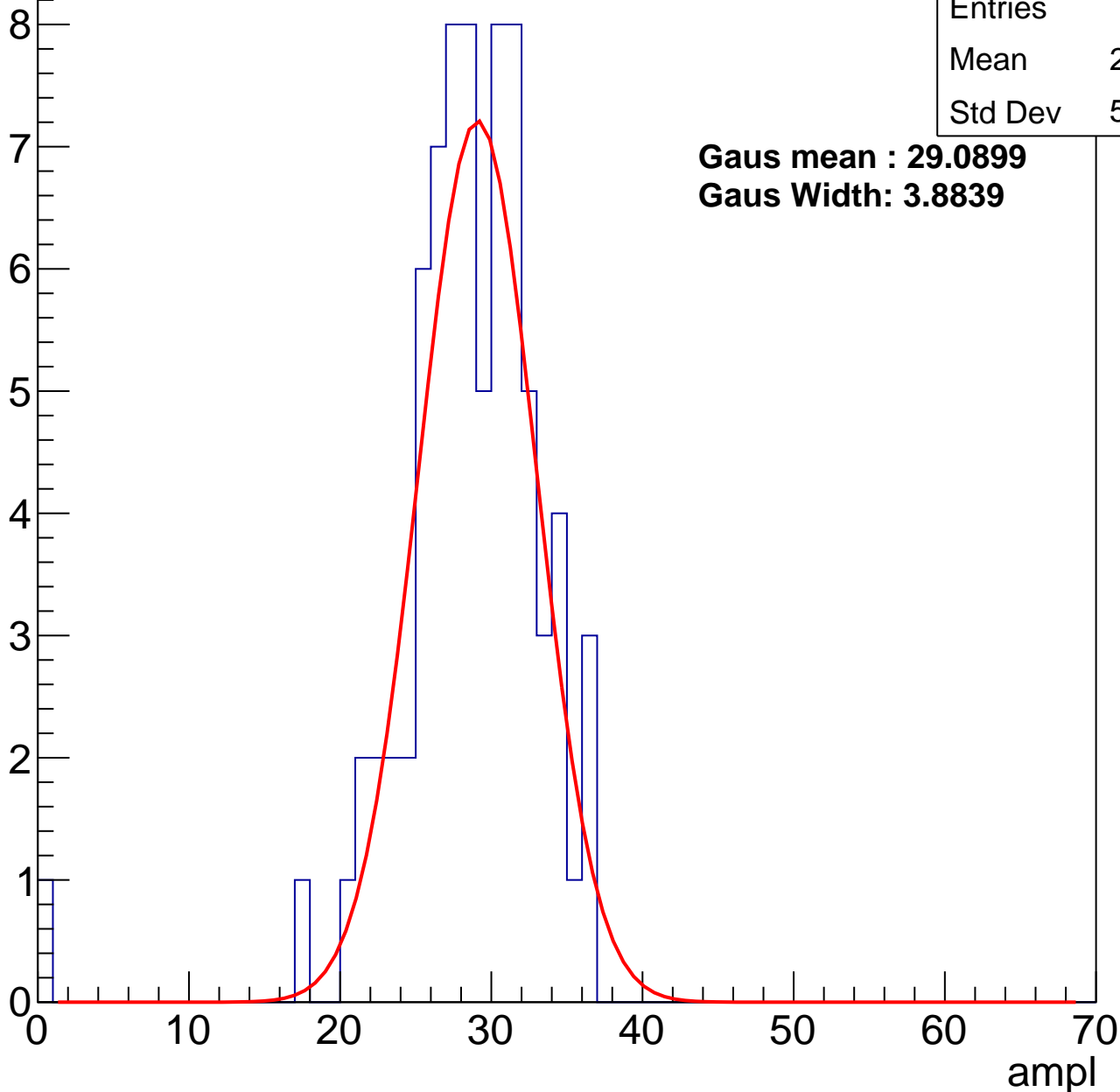
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.05
Std Dev	5.045

Gaus mean : 29.0899

Gaus Width: 3.8839



B1L102S, U9-ch28, adc1

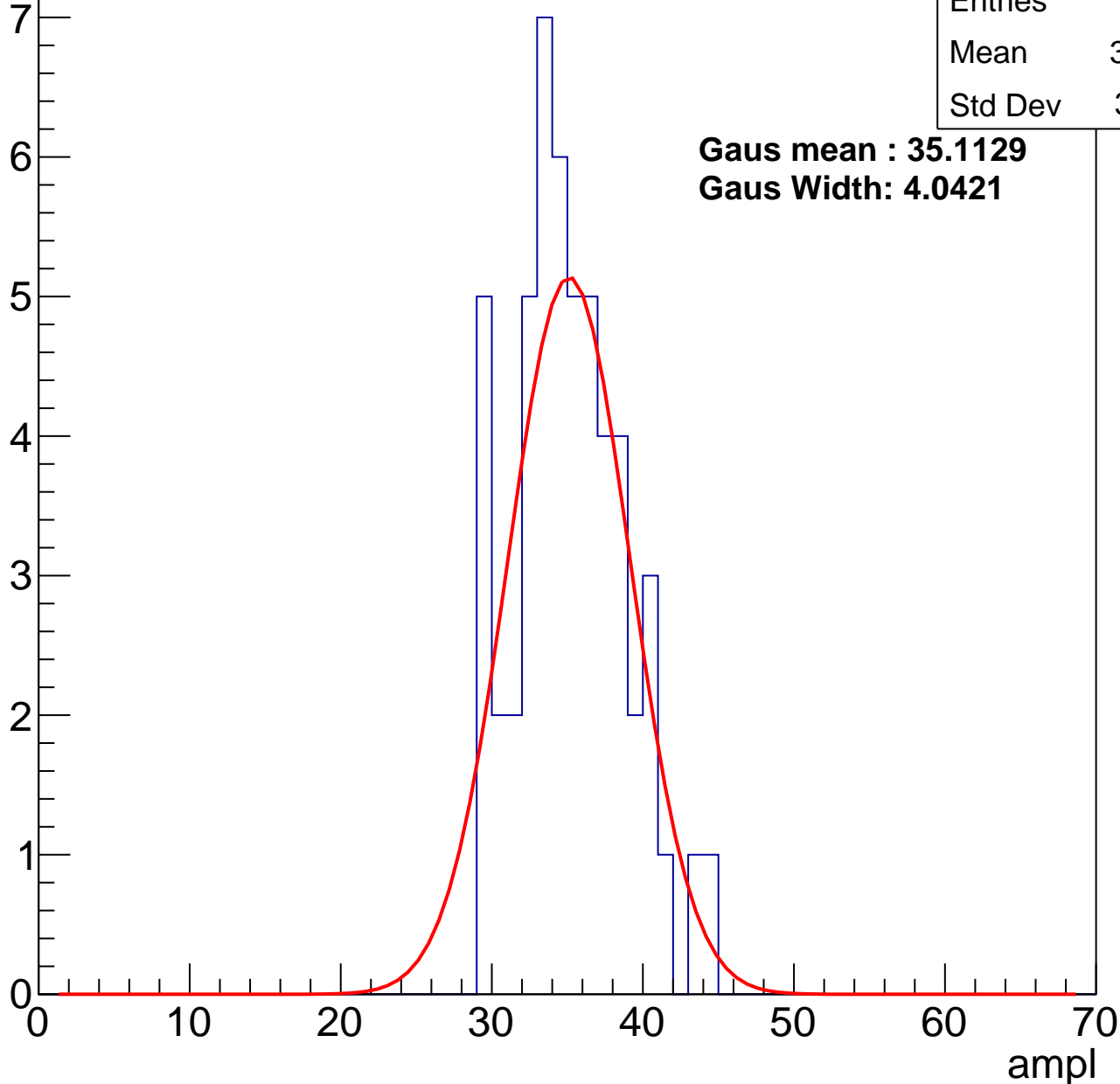
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.77
Std Dev	3.591

Gaus mean : 35.1129

Gaus Width: 4.0421



B1L102S, U9-ch28, adc2

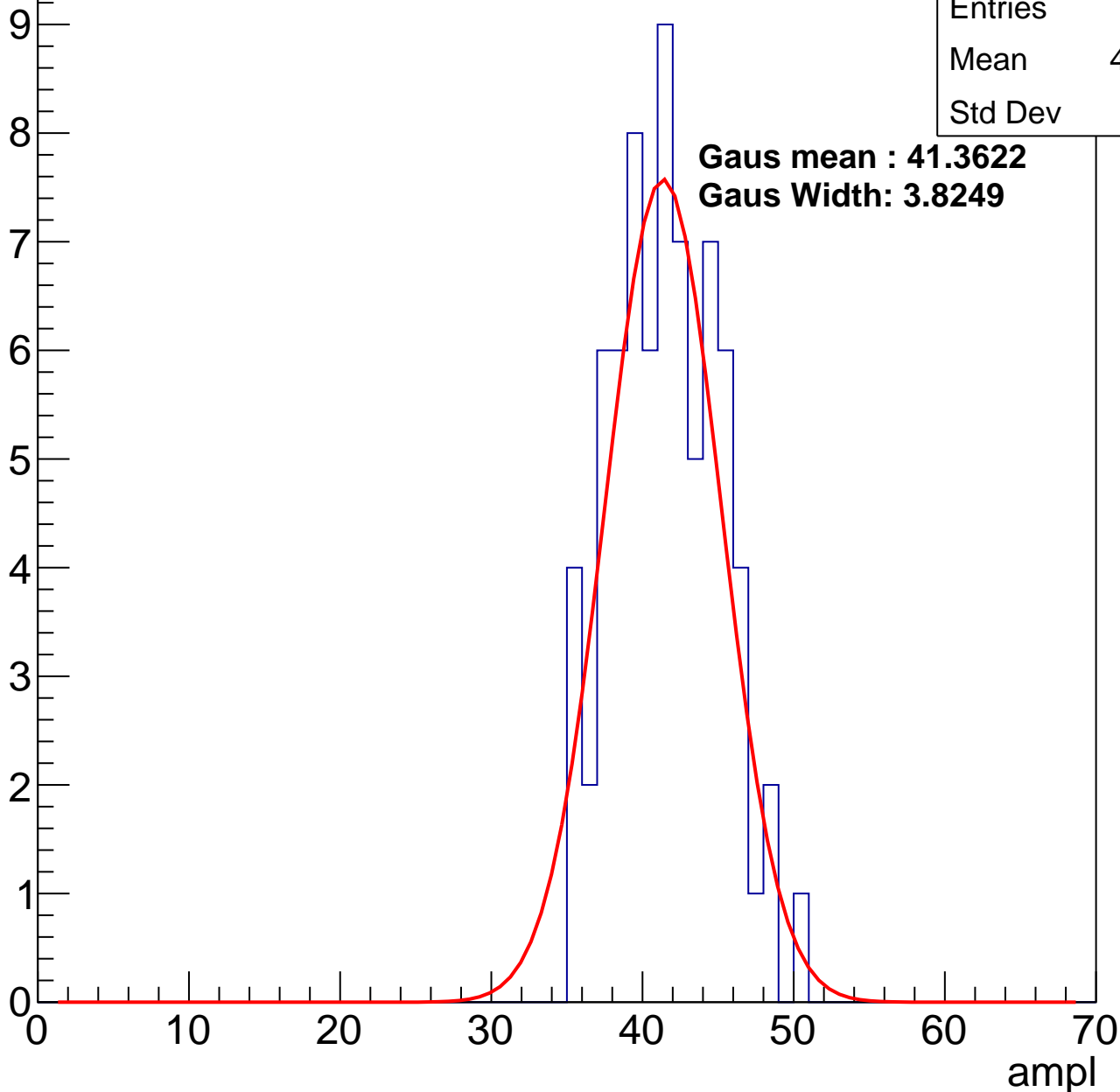
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.18
Std Dev	3.45

Gaus mean : 41.3622

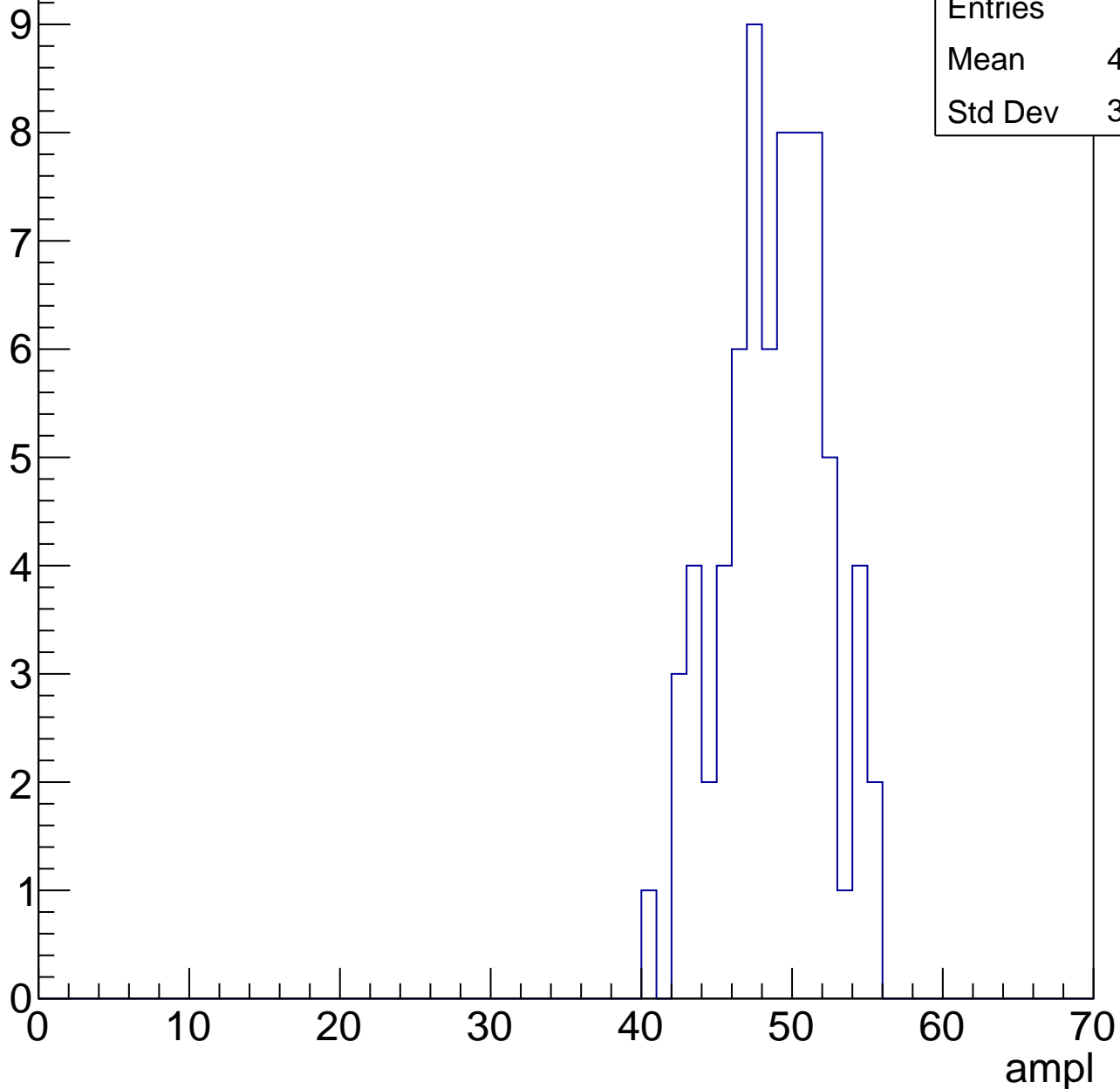
Gaus Width: 3.8249



B1L102S, U9-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

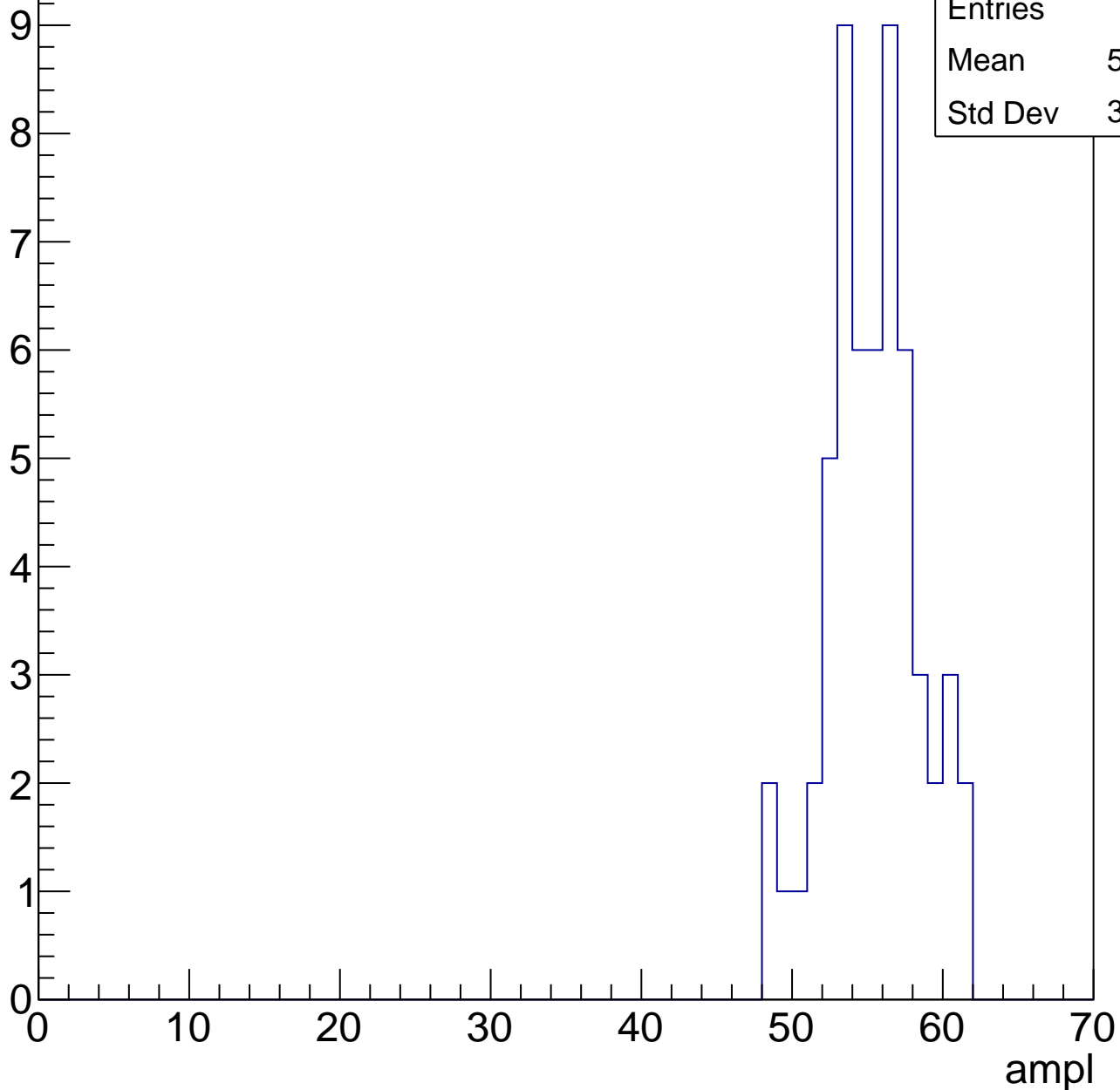


Entries	71
Mean	48.34
Std Dev	3.406

B1L102S, U9-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

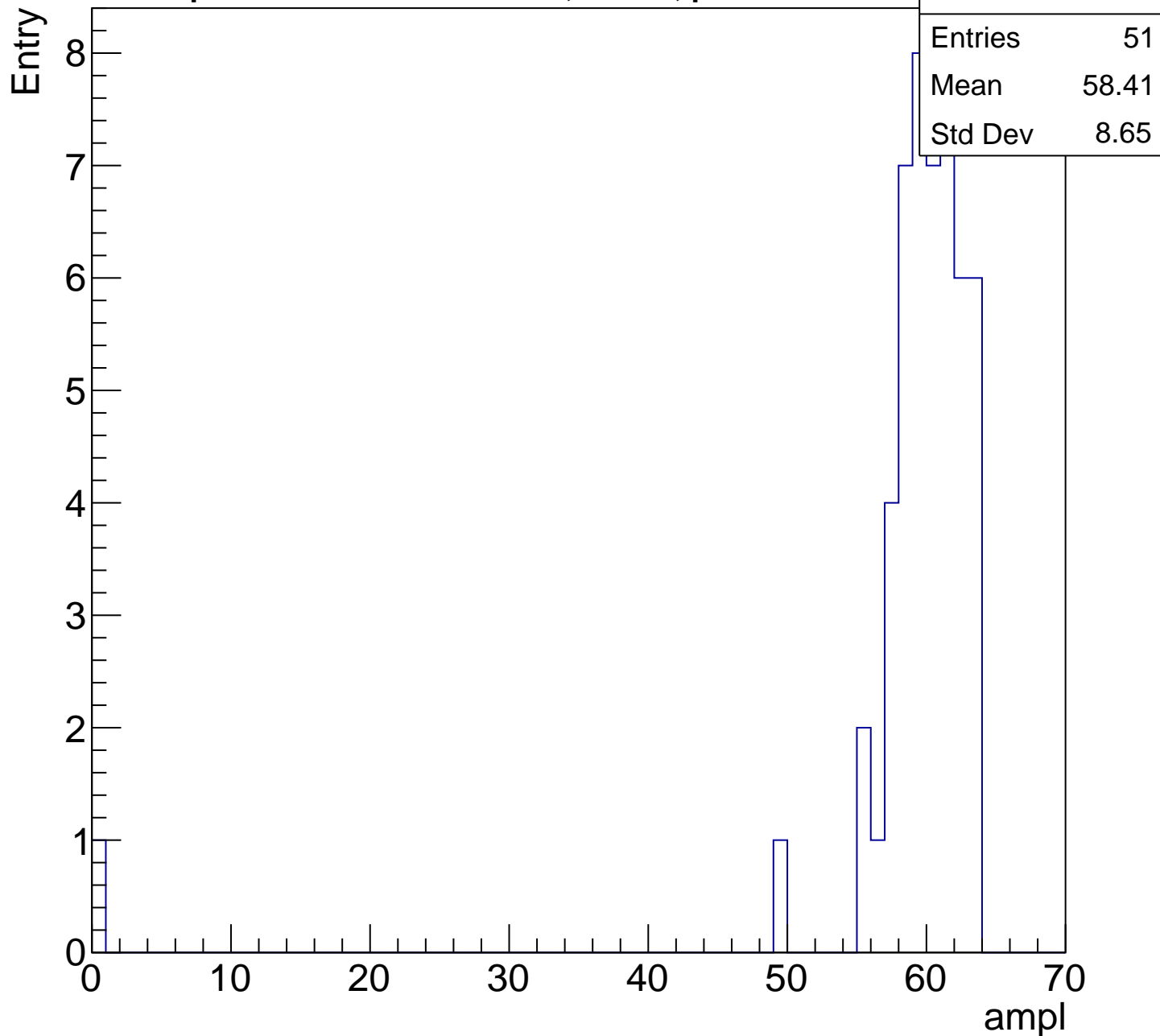
Entry



Entries	57
Mean	54.88
Std Dev	3.009

B1L102S, U9-ch28, adc5

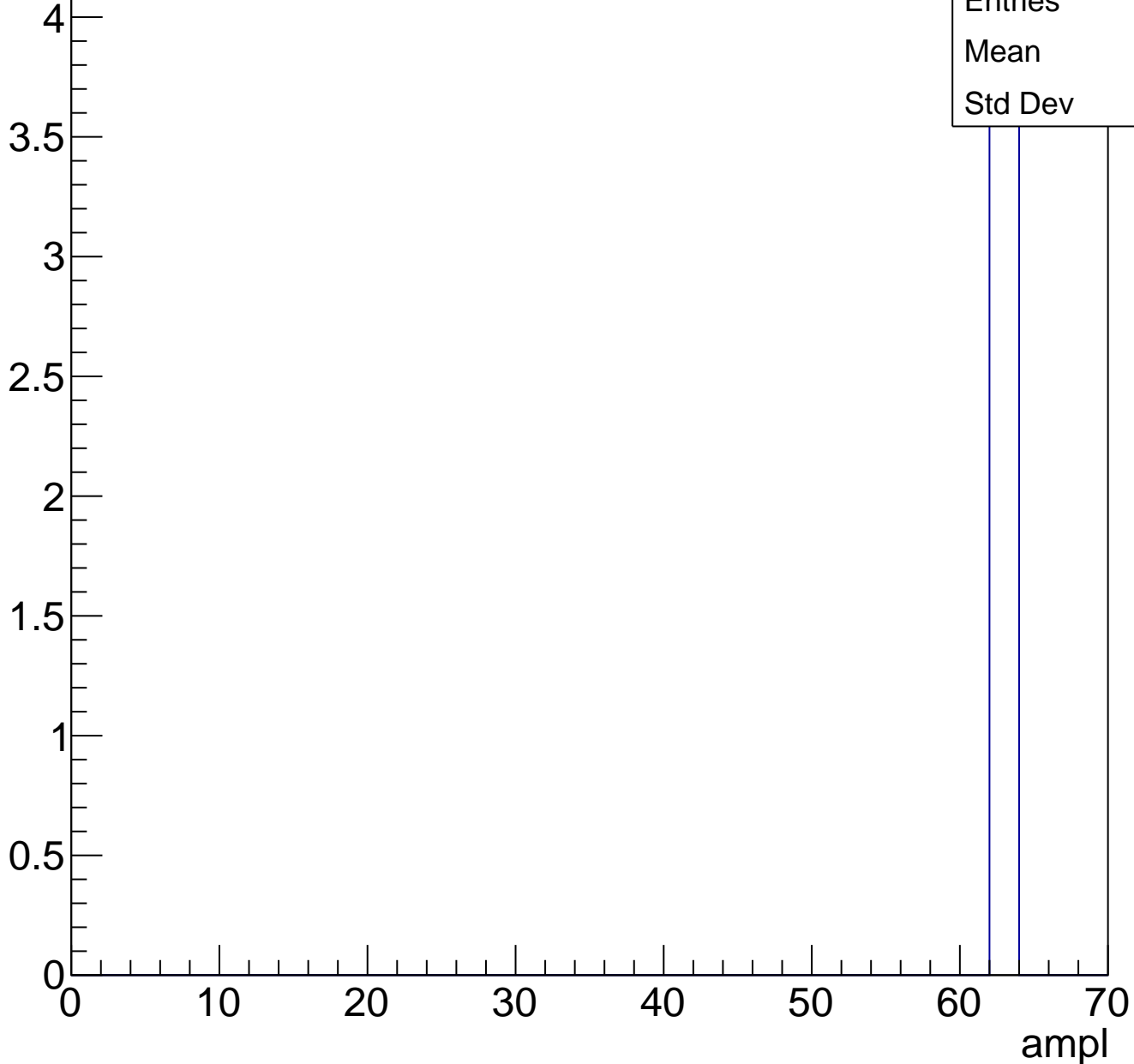
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U9-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch29, adc0

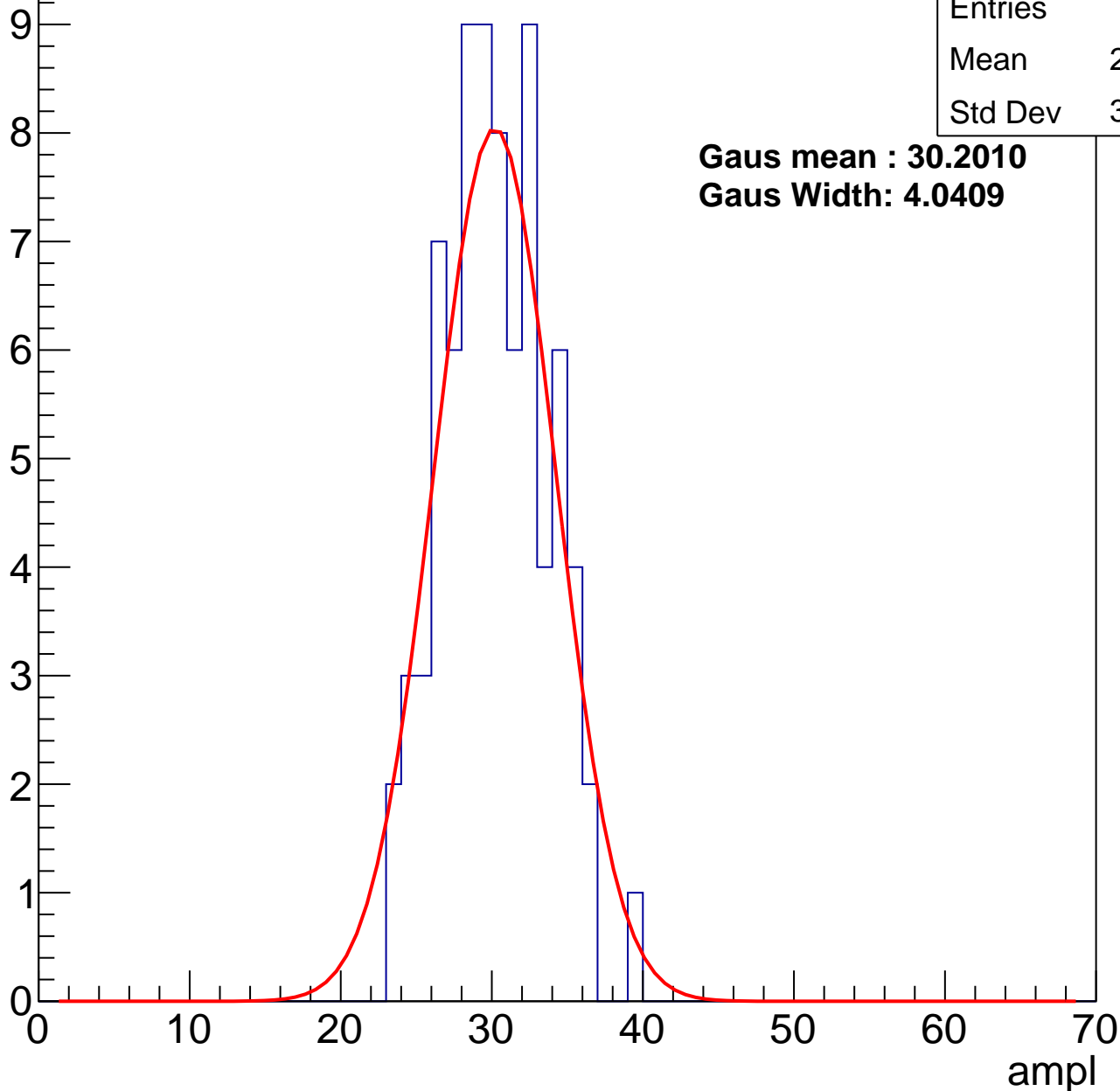
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29.76
Std Dev	3.402

Gaus mean : 30.2010

Gaus Width: 4.0409



B1L102S, U9-ch29, adc1

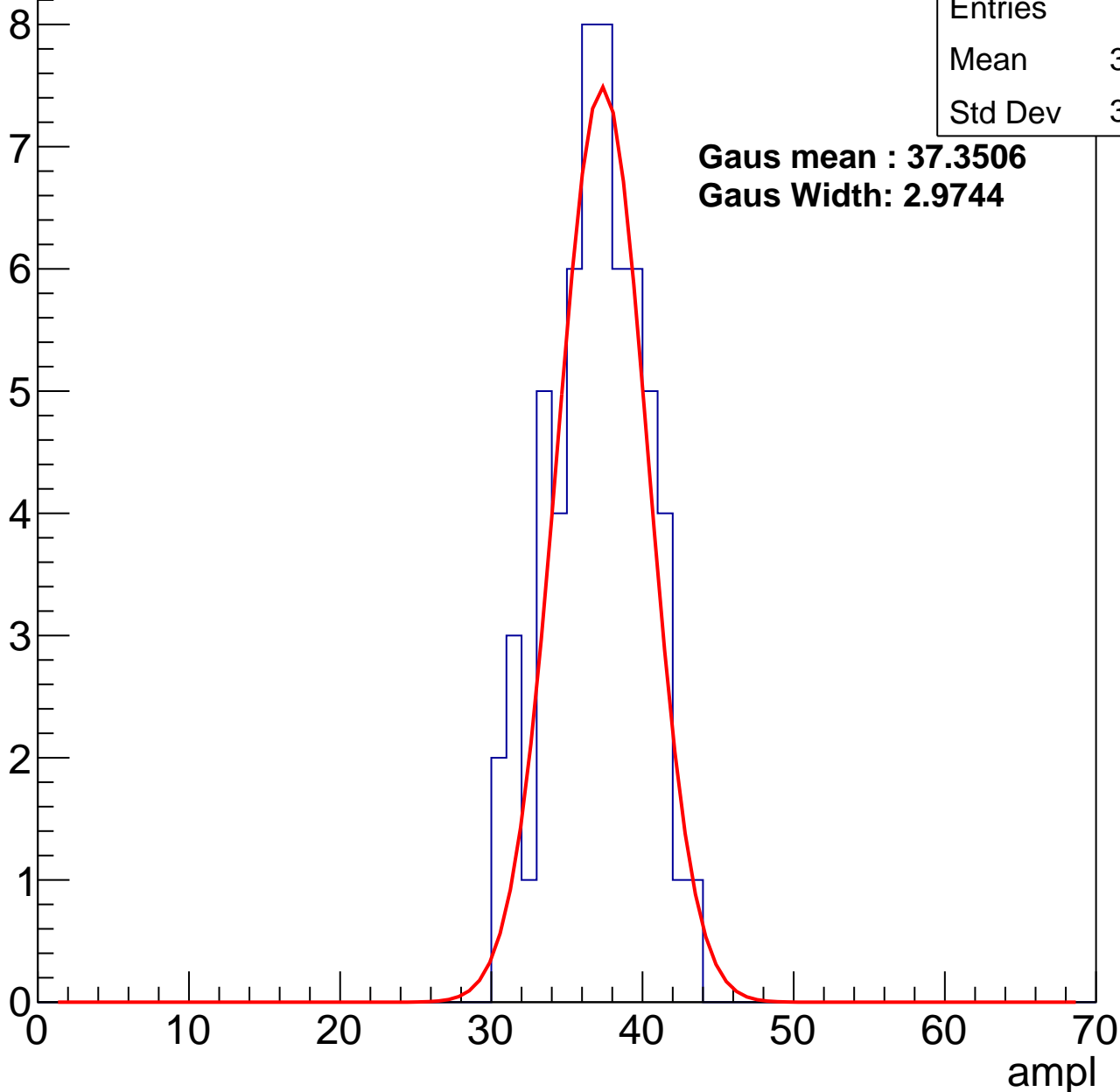
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	36.52
Std Dev	3.069

Gaus mean : 37.3506

Gaus Width: 2.9744



B1L102S, U9-ch29, adc2

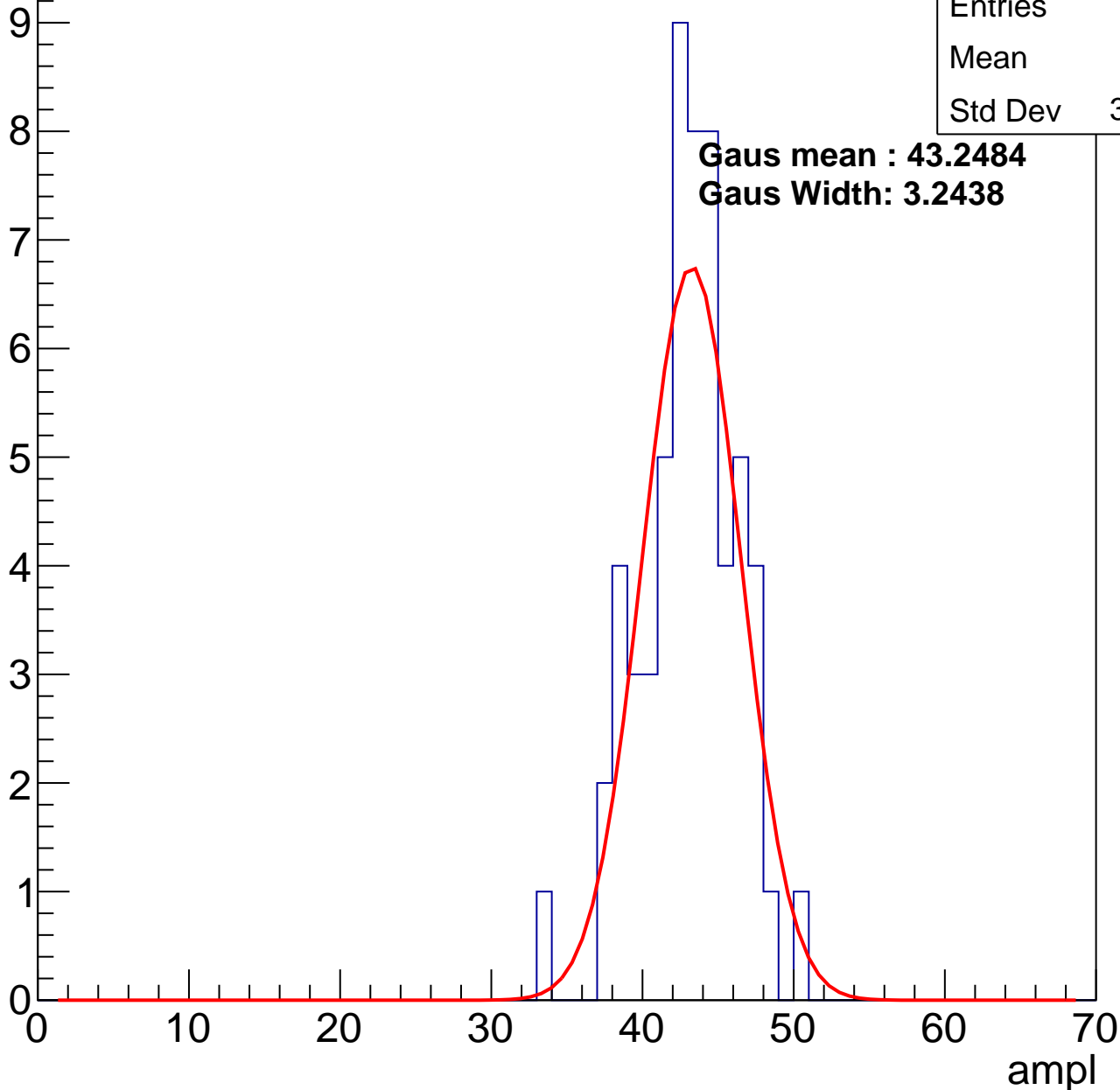
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.6
Std Dev	3.146

Gaus mean : 43.2484

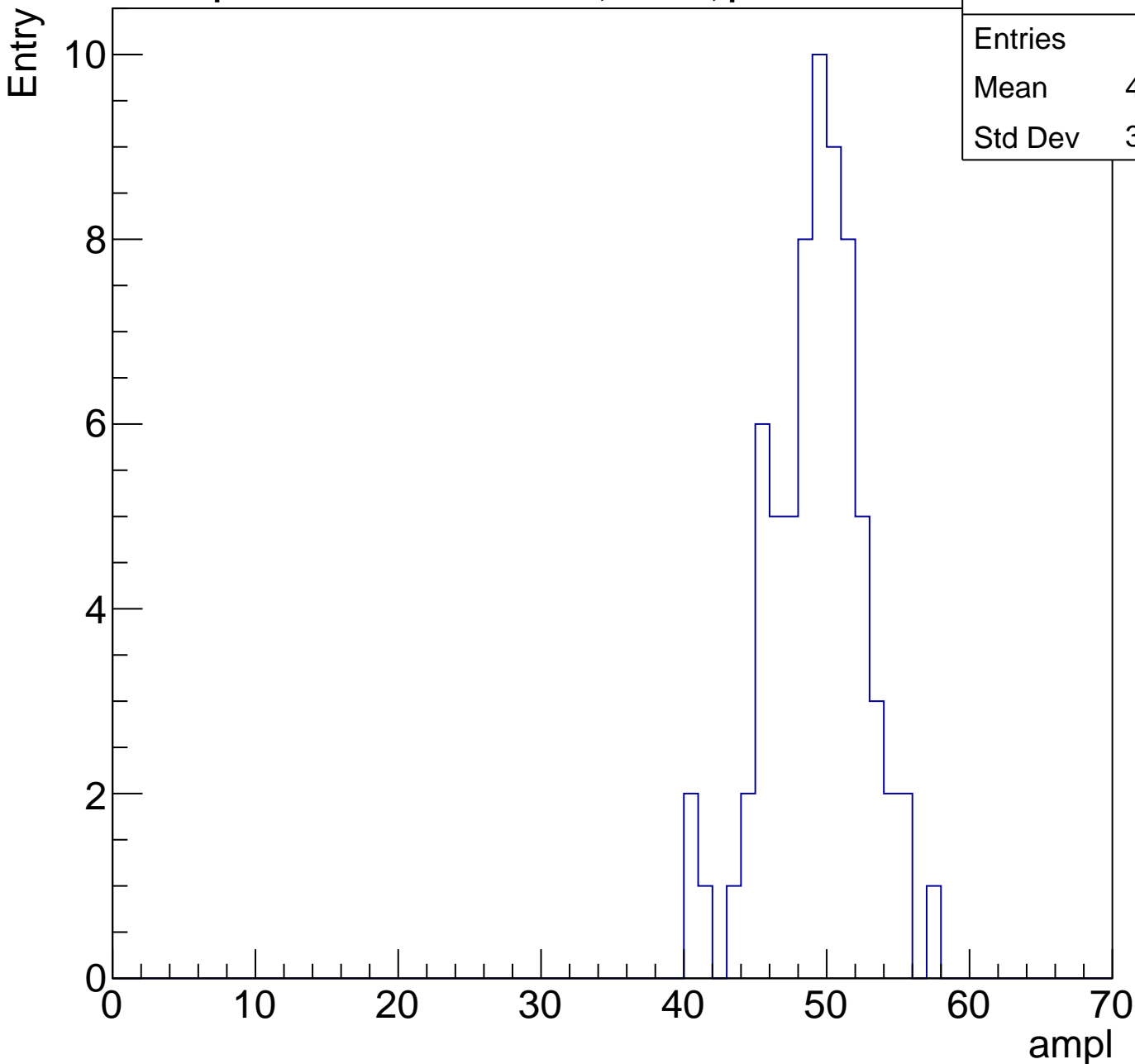
Gaus Width: 3.2438



B1L102S, U9-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	48.76
Std Dev	3.374

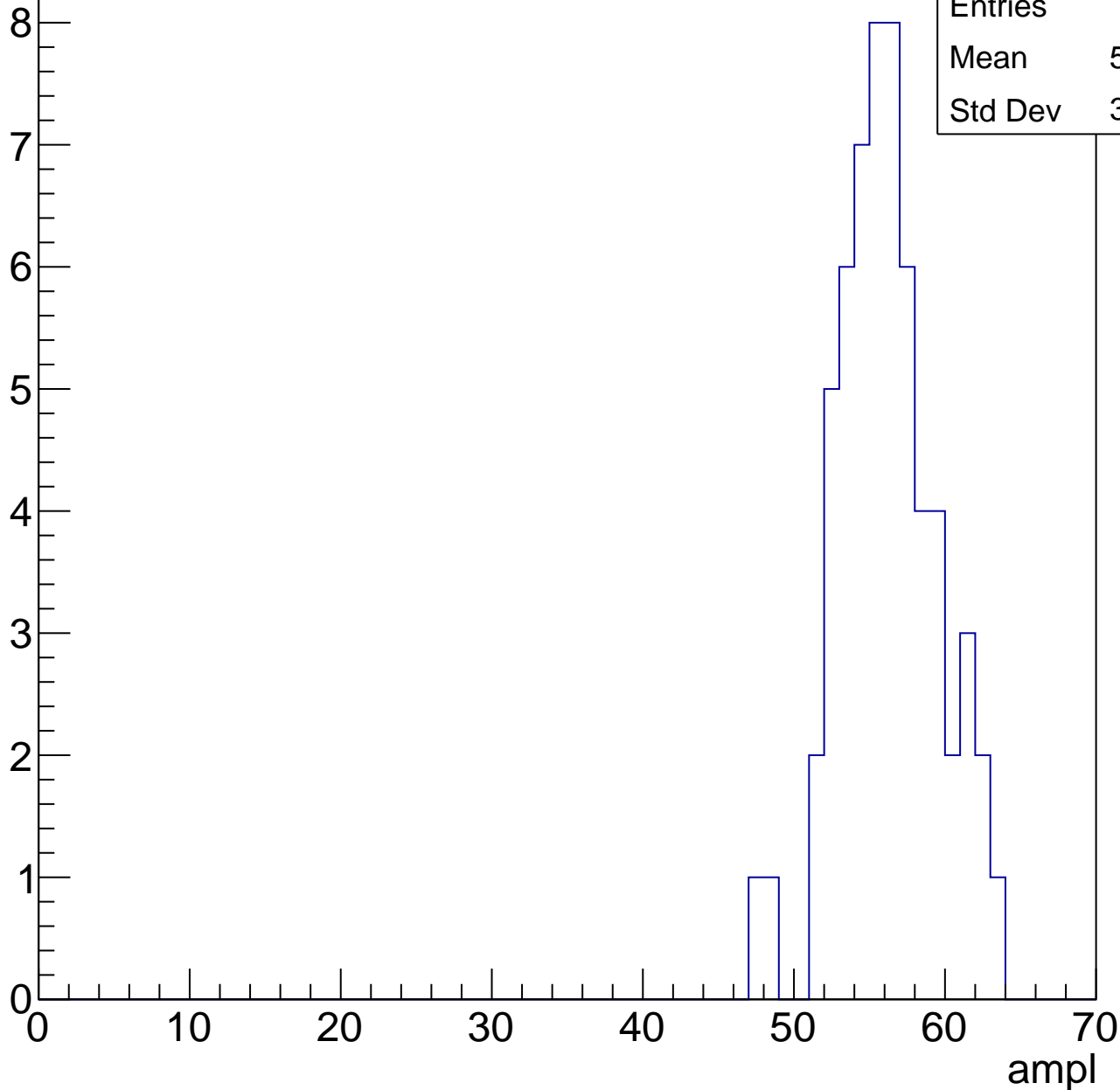


B1L102S, U9-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.68
Std Dev	3.279

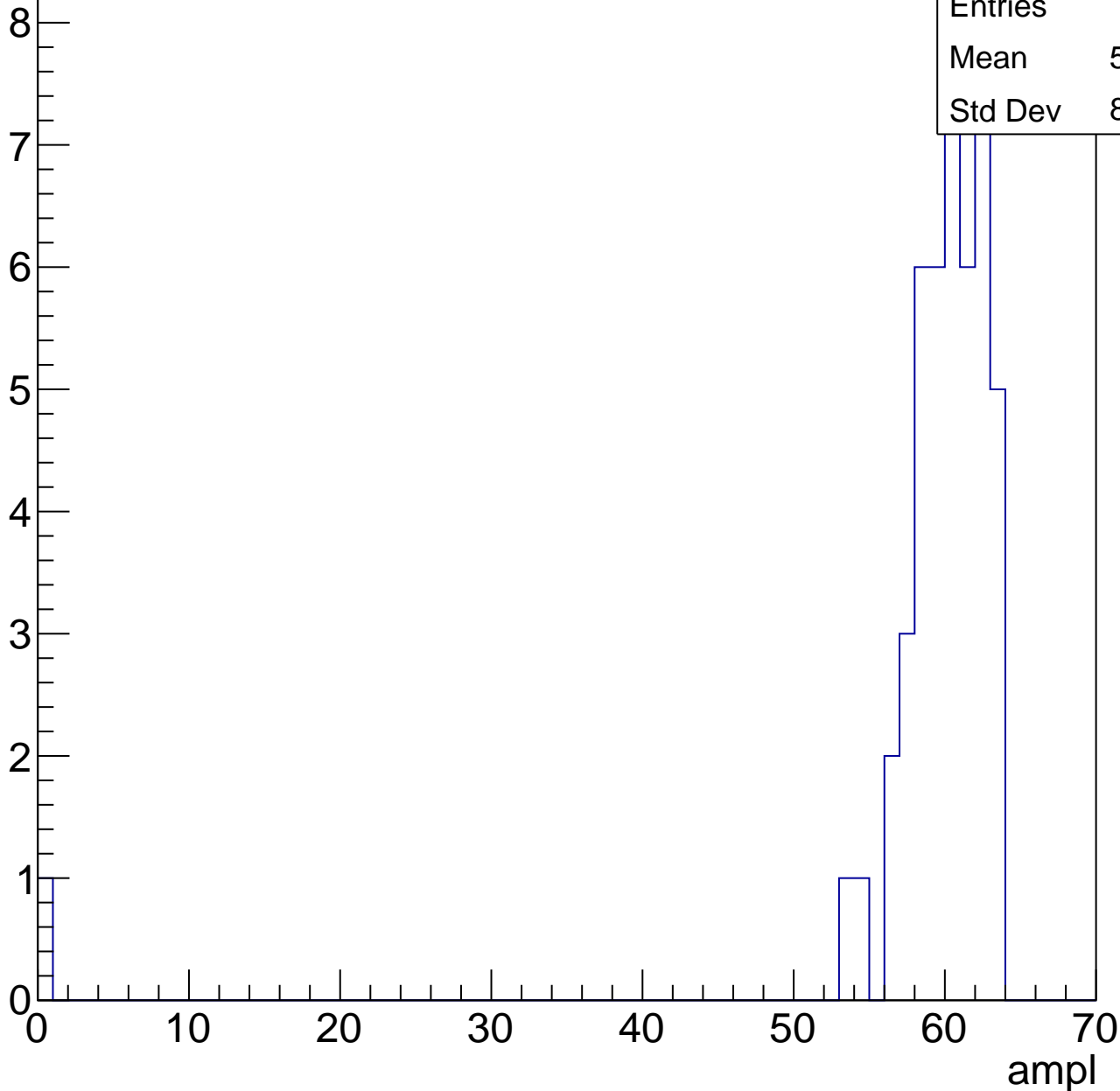


B1L102S, U9-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

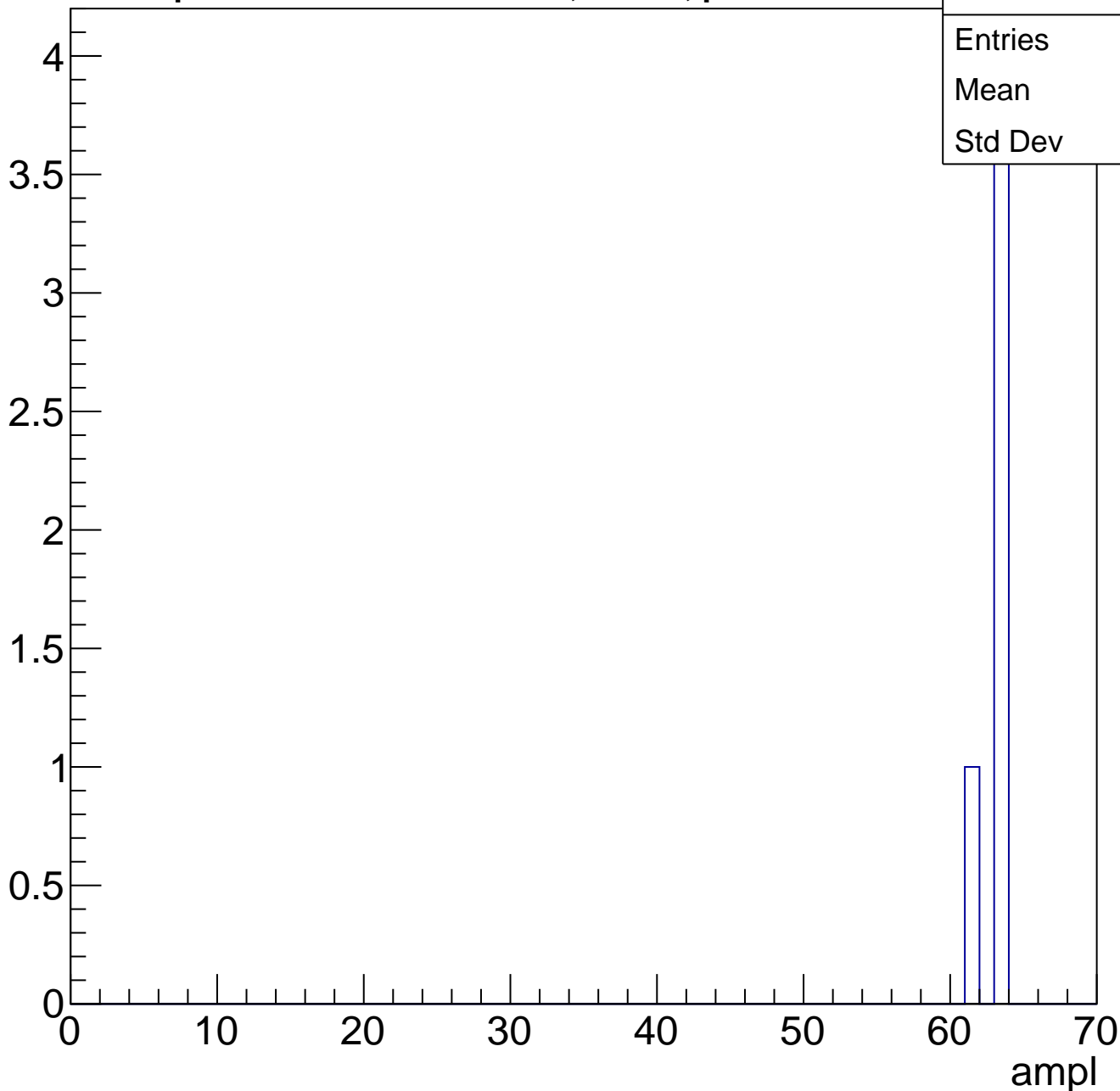
Entries	47
Mean	58.49
Std Dev	8.932



B1L102S, U9-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch30, adc0

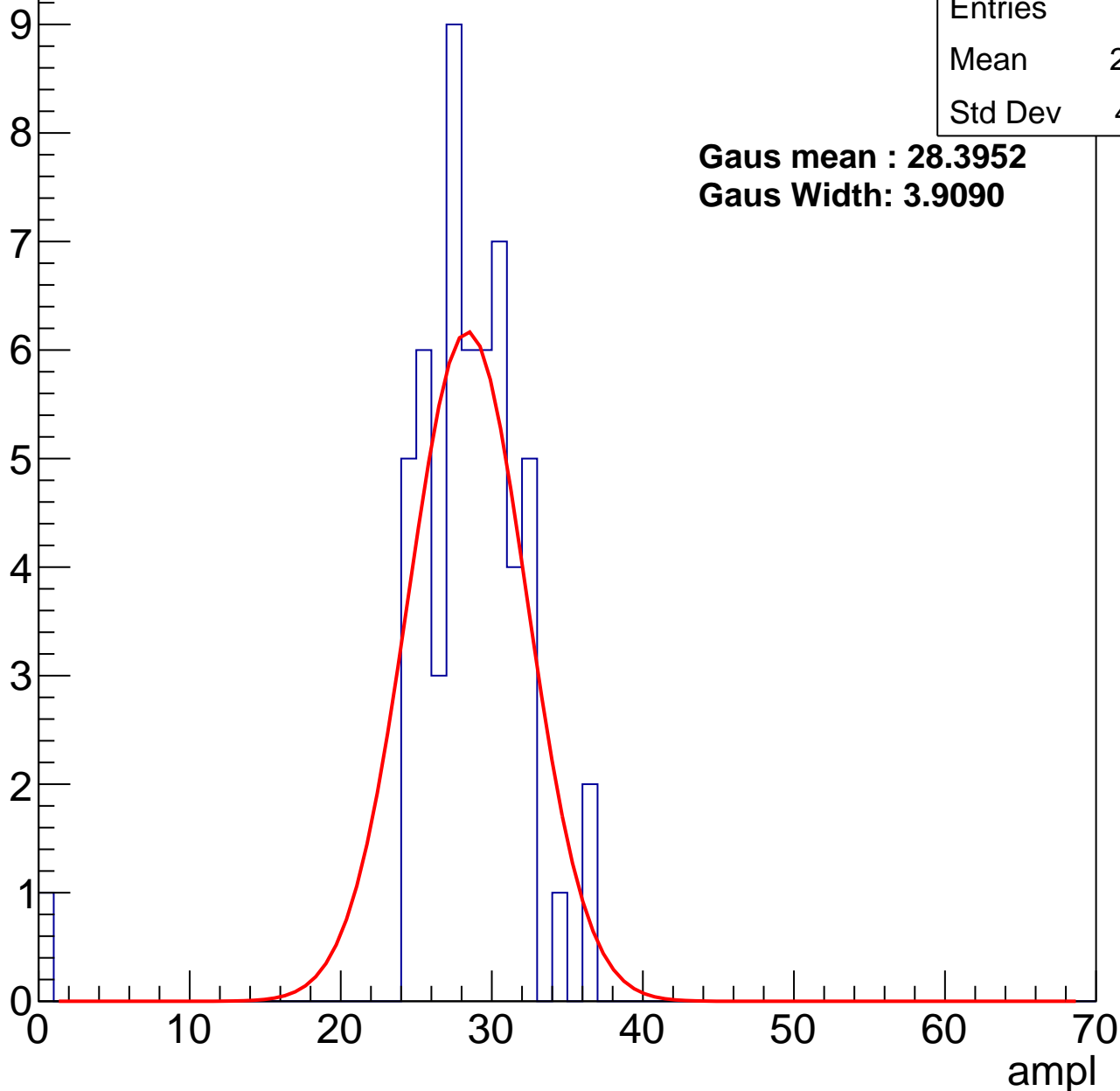
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	27.87
Std Dev	4.771

Gaus mean : 28.3952

Gaus Width: 3.9090



B1L102S, U9-ch30, adc1

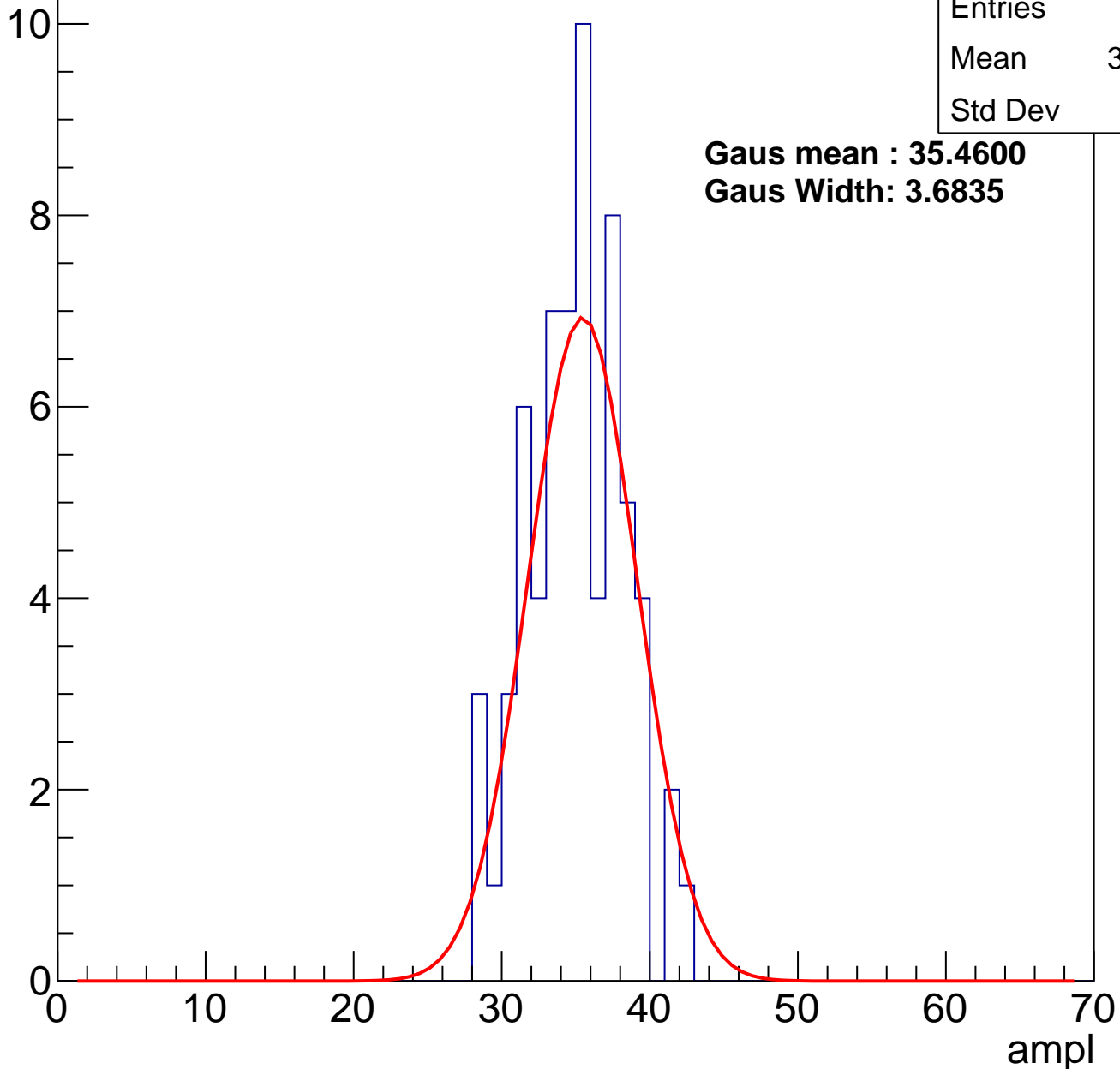
calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	34.55
Std Dev	3.23

Gaus mean : 35.4600

Gaus Width: 3.6835

Entry



B1L102S, U9-ch30, adc2

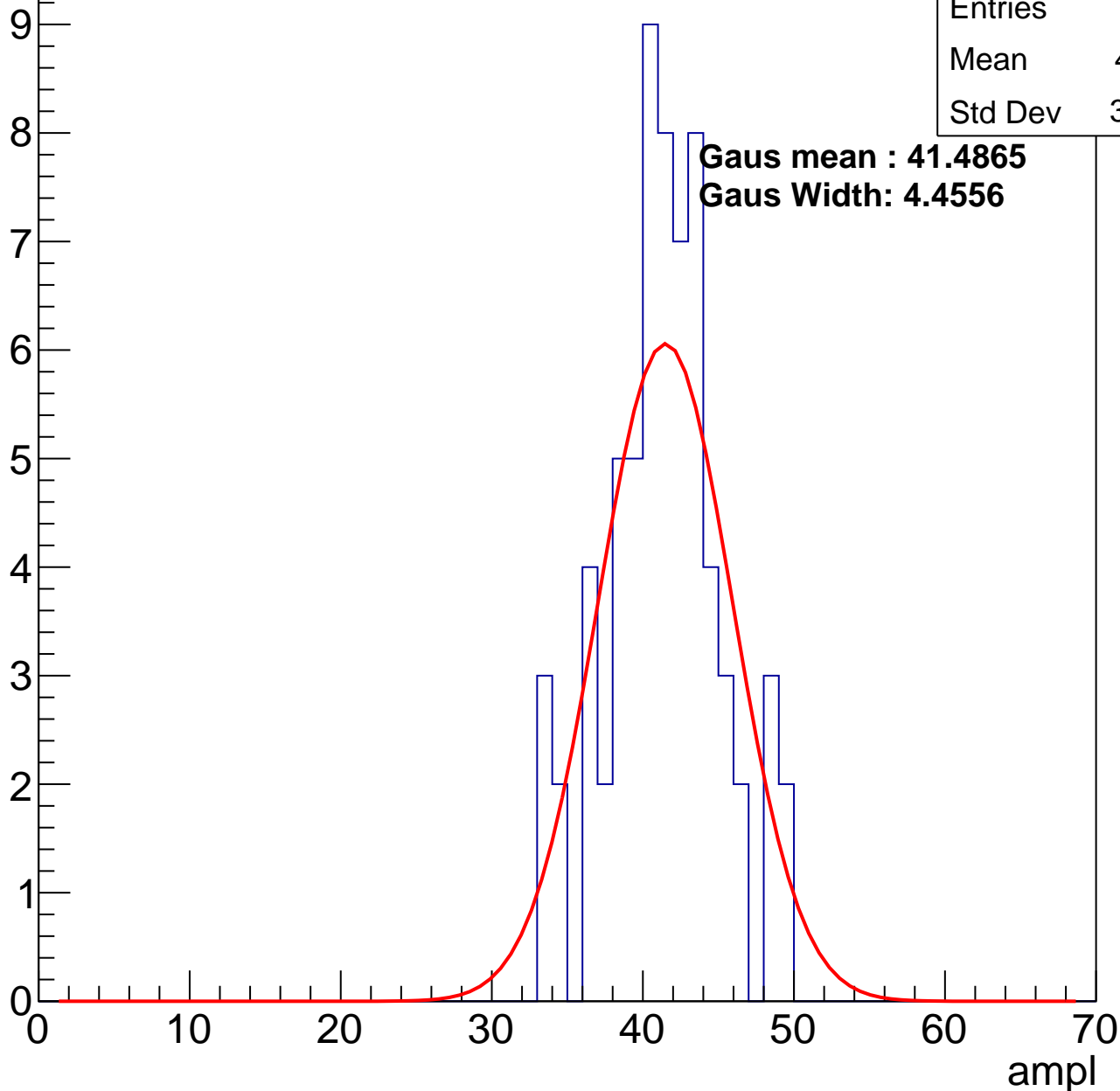
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	40.91
Std Dev	3.753

Gaus mean : 41.4865

Gaus Width: 4.4556

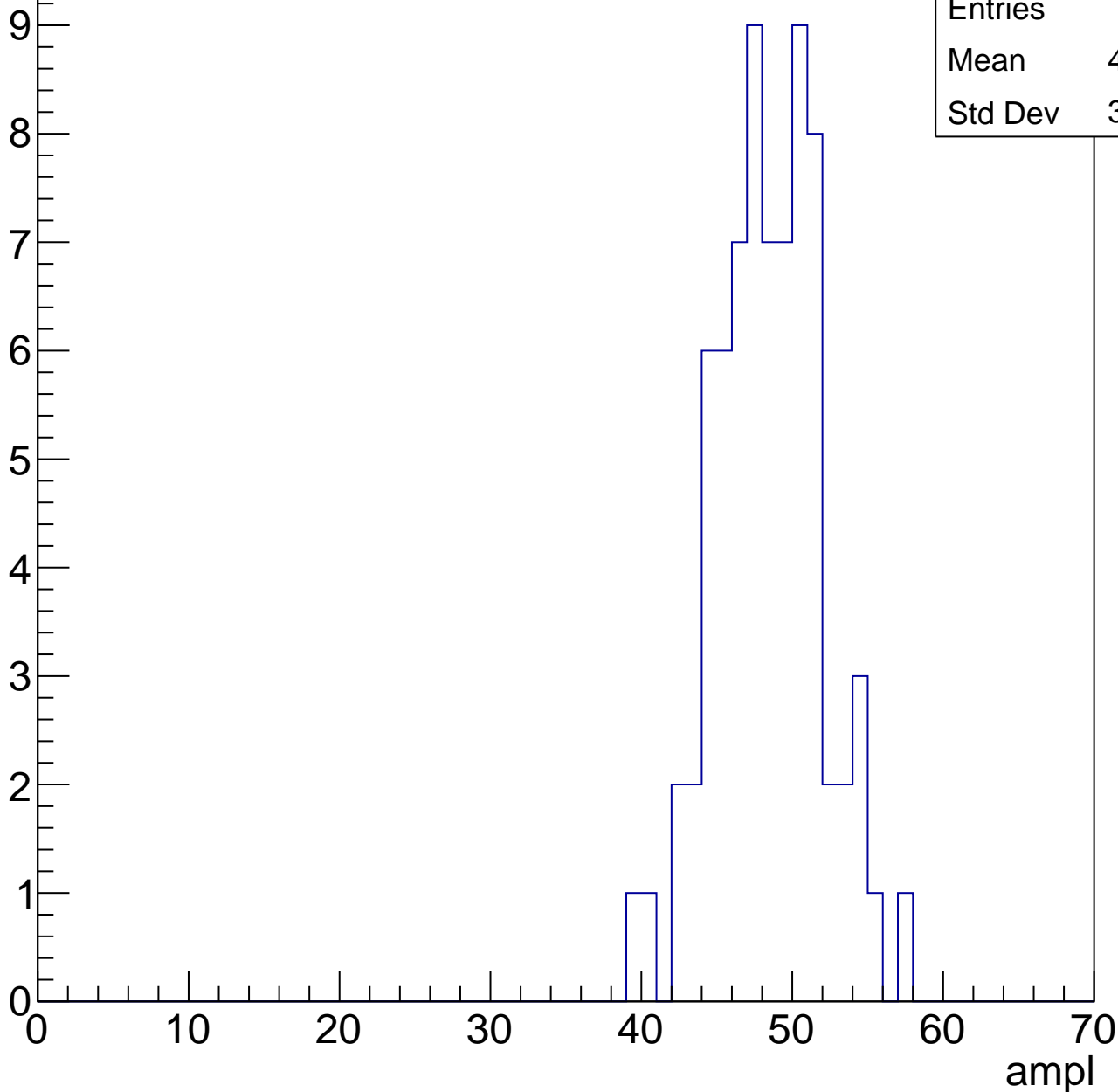


B1L102S, U9-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	47.96
Std Dev	3.466

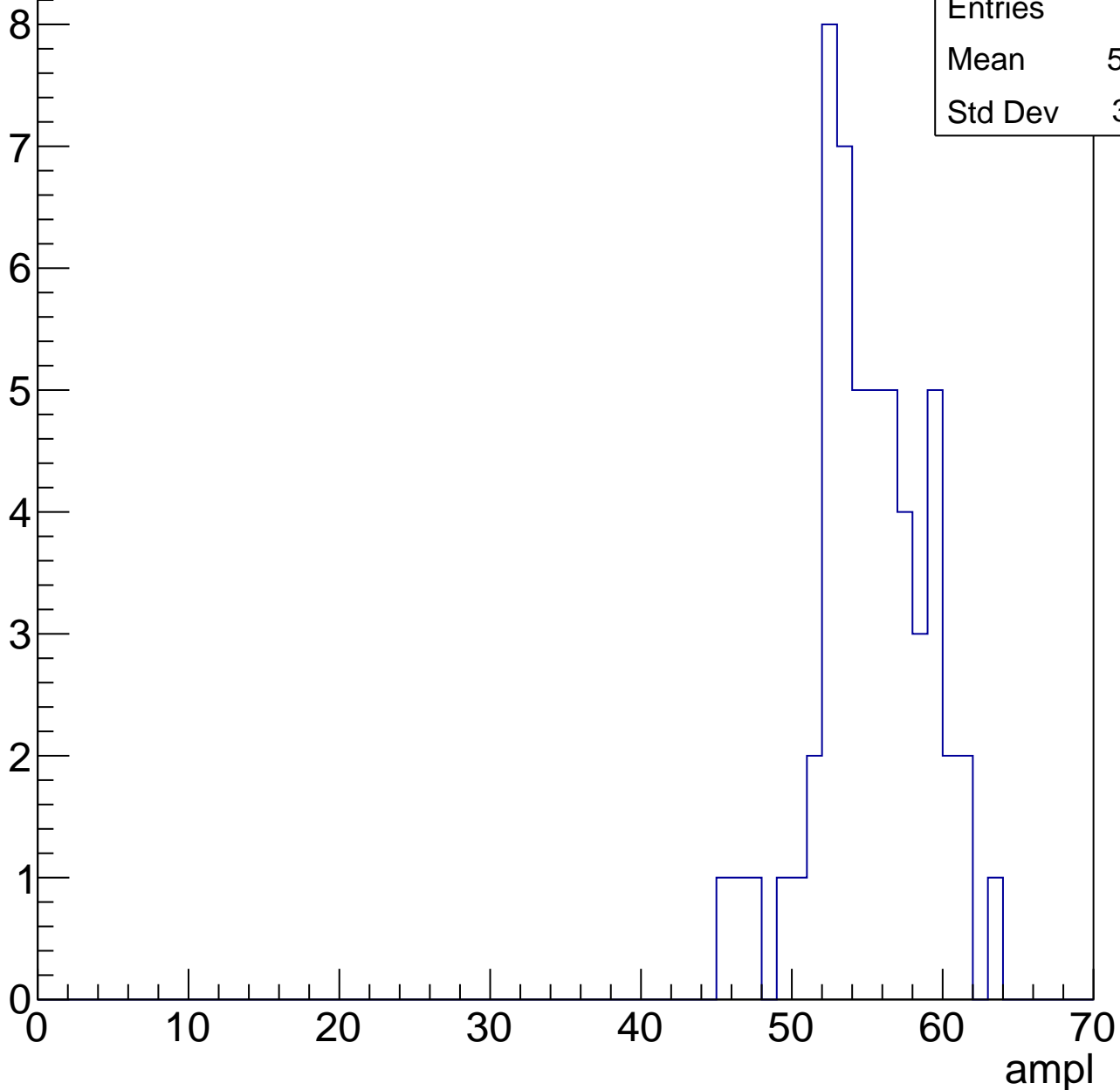


B1L102S, U9-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.69
Std Dev	3.731

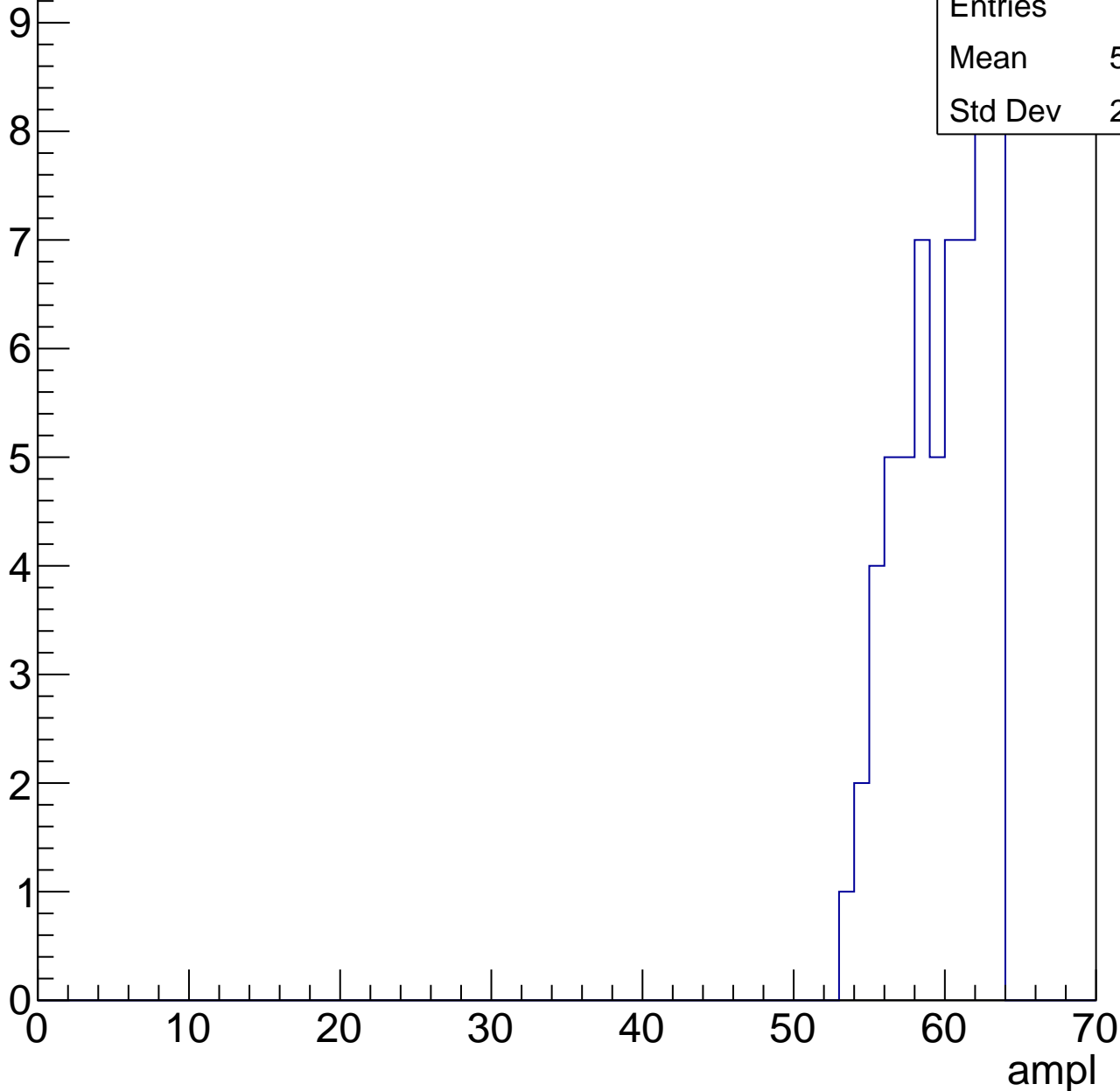


B1L102S, U9-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

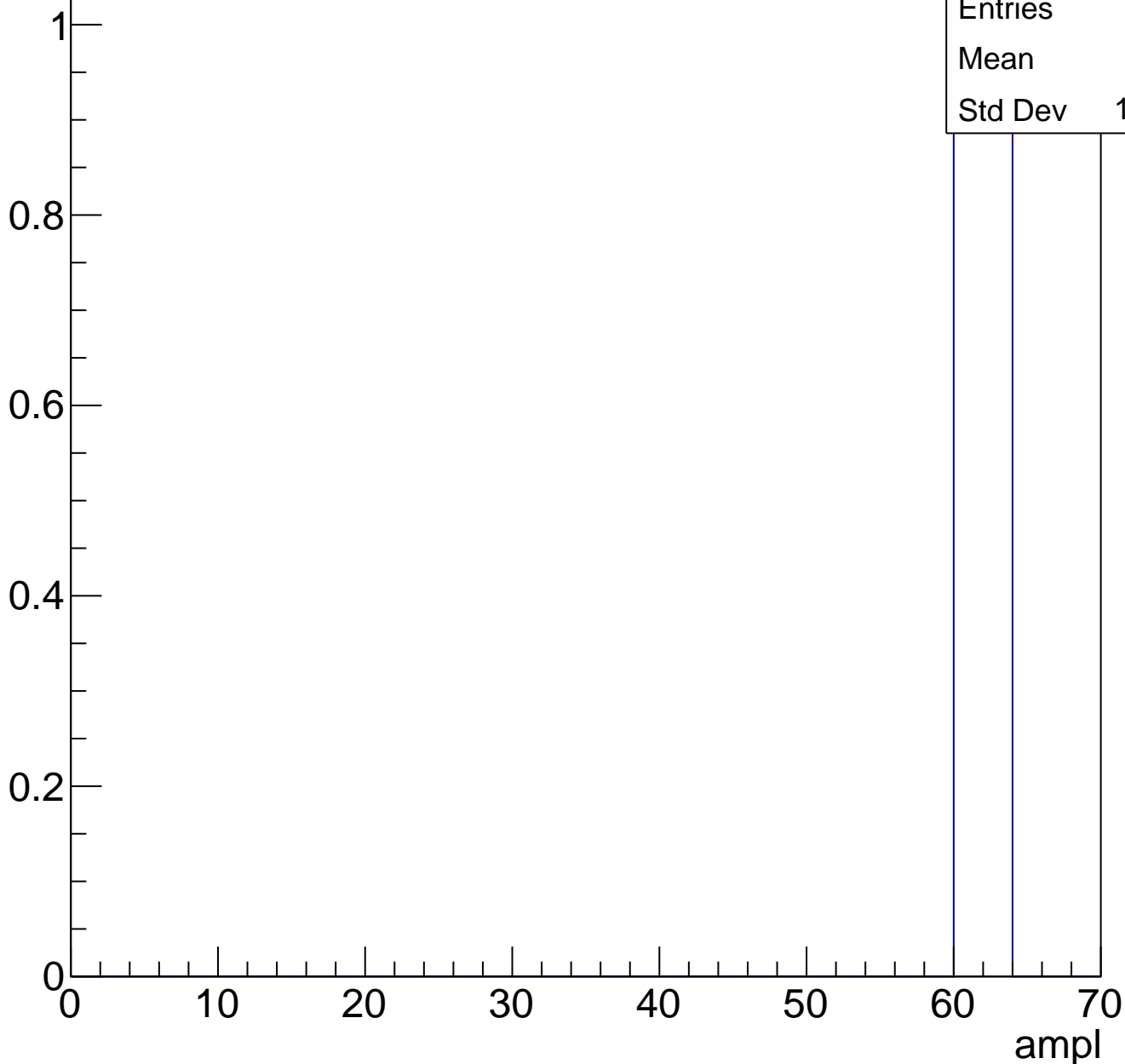
Entries	60
Mean	59.27
Std Dev	2.774



B1L102S, U9-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L102S, U9-ch31, adc0

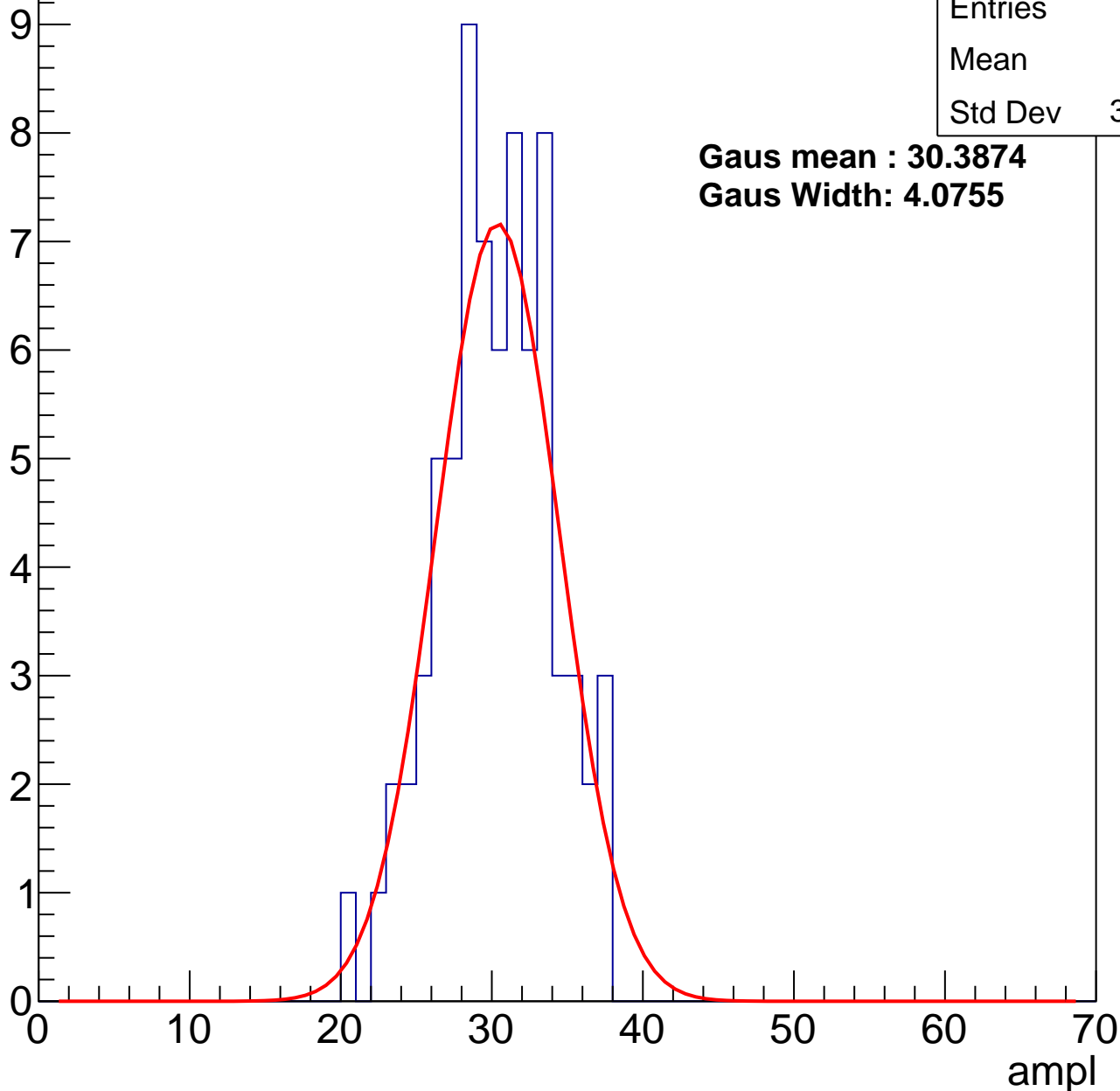
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29.8
Std Dev	3.713

Gaus mean : 30.3874

Gaus Width: 4.0755



B1L102S, U9-ch31, adc1

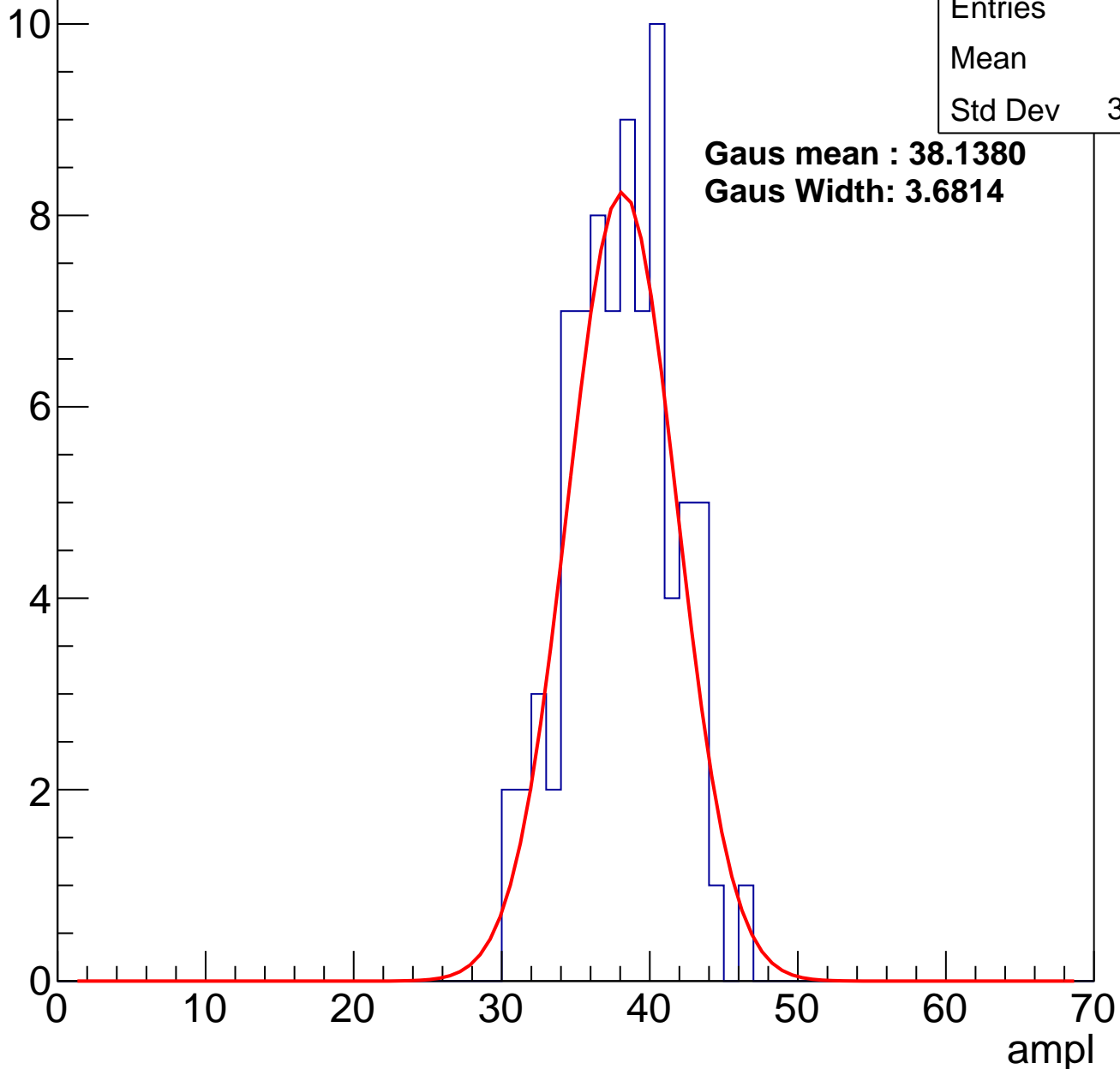
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	37.6
Std Dev	3.488

Gaus mean : 38.1380

Gaus Width: 3.6814

Entry



B1L102S, U9-ch31, adc2

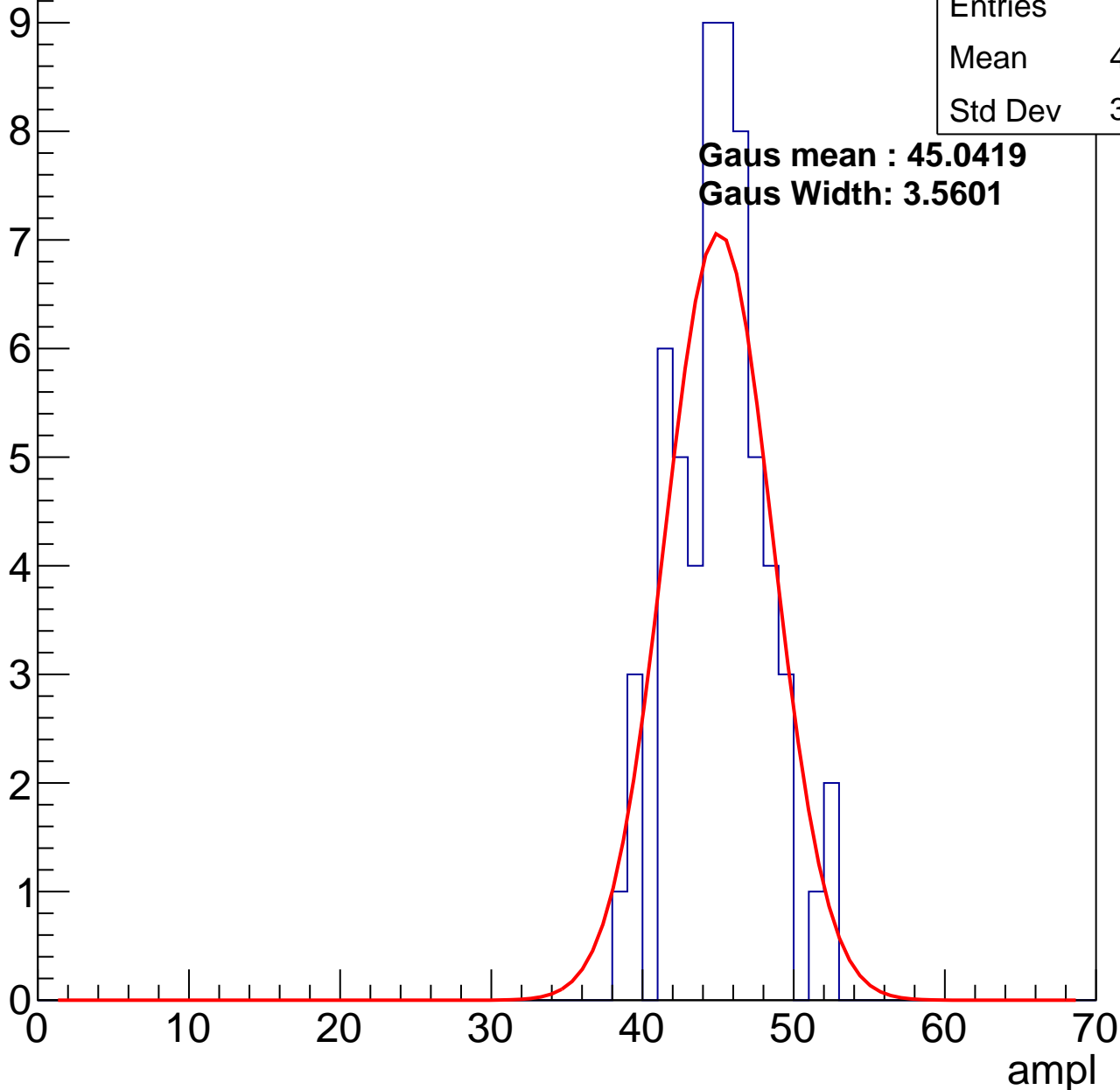
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	44.68
Std Dev	3.063

Gaus mean : 45.0419

Gaus Width: 3.5601

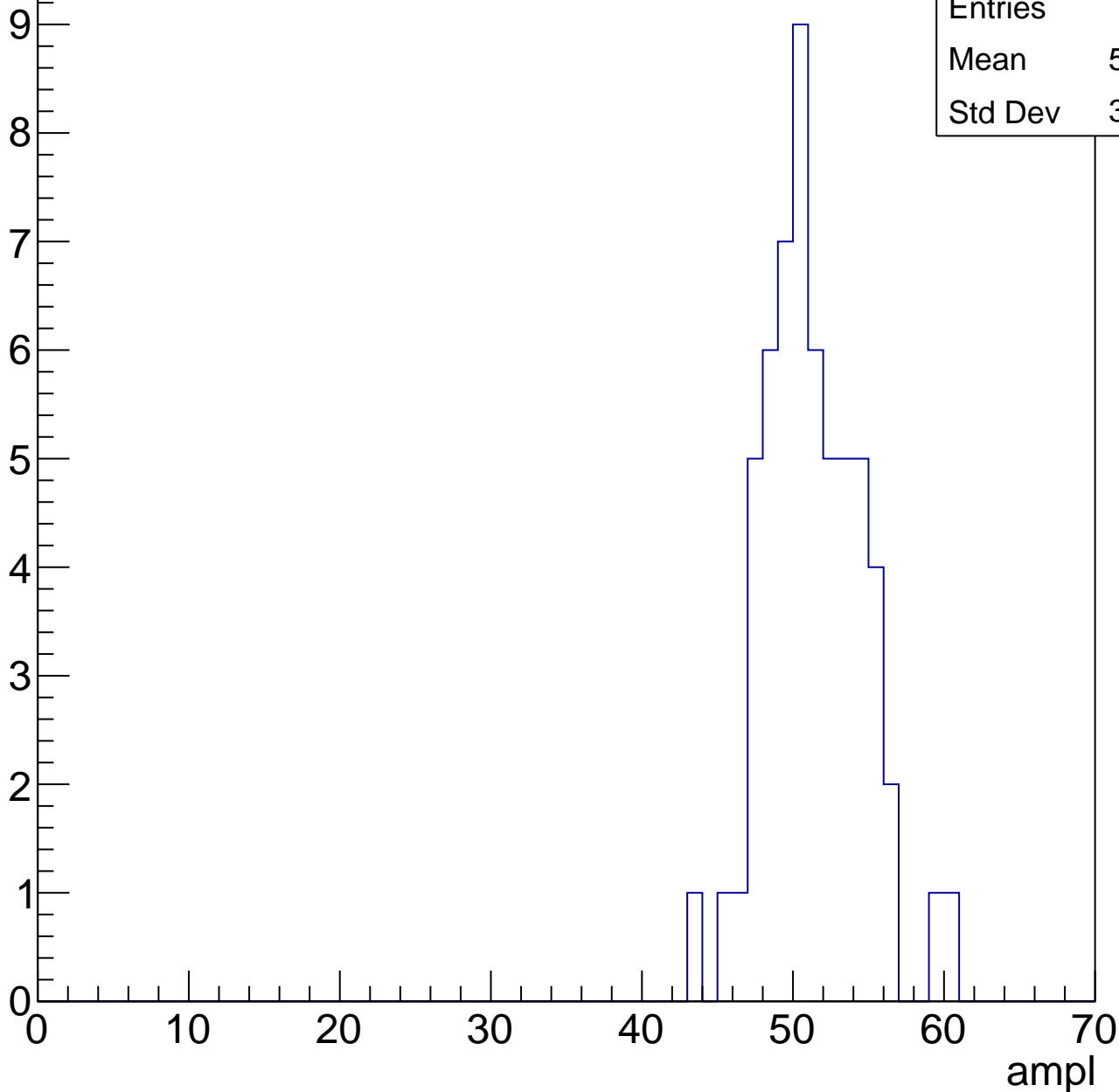


B1L102S, U9-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.88
Std Dev	3.253

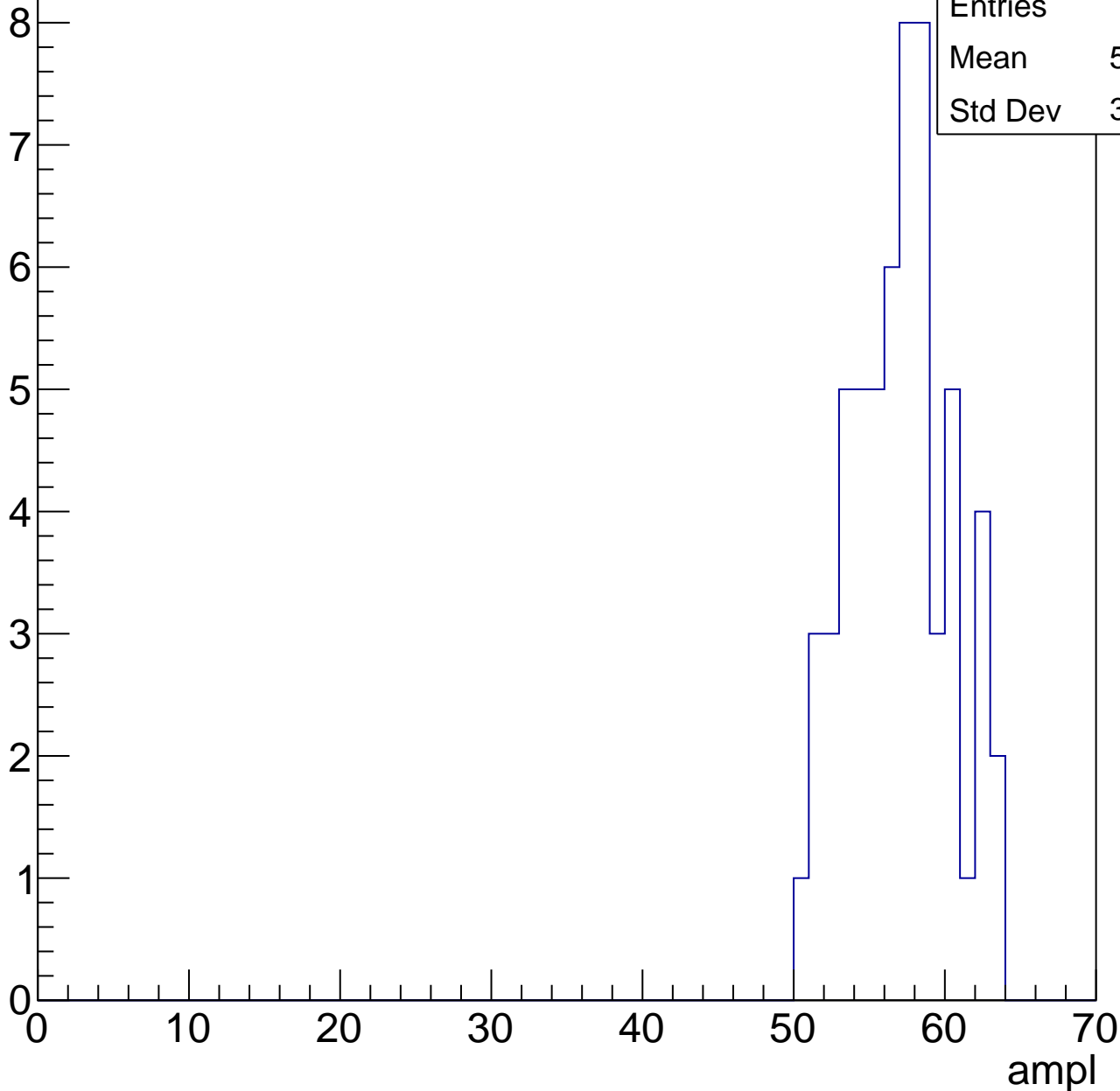


B1L102S, U9-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.56
Std Dev	3.254

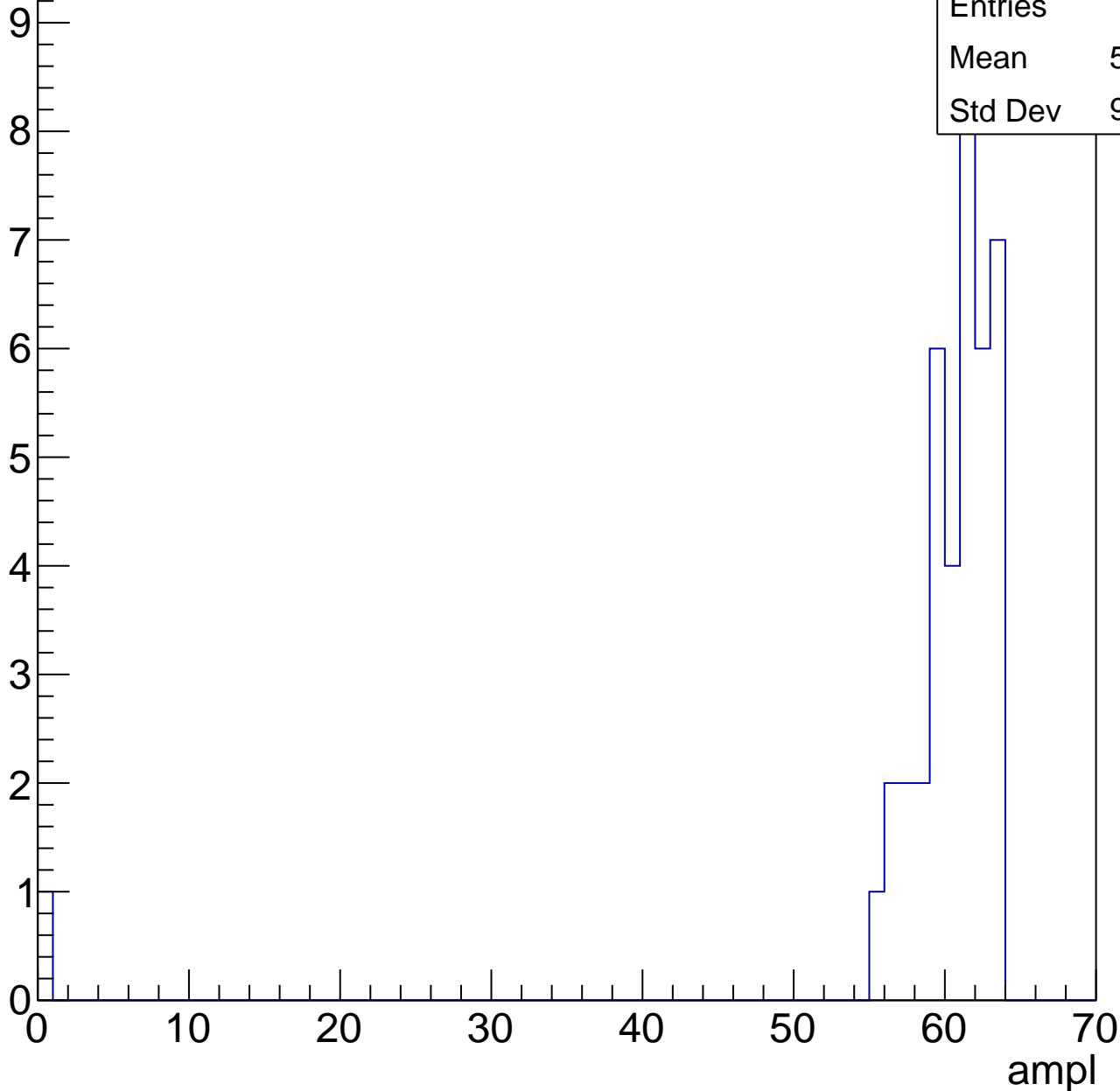


B1L102S, U9-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.83
Std Dev	9.656



B1L102S, U9-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch32, adc0

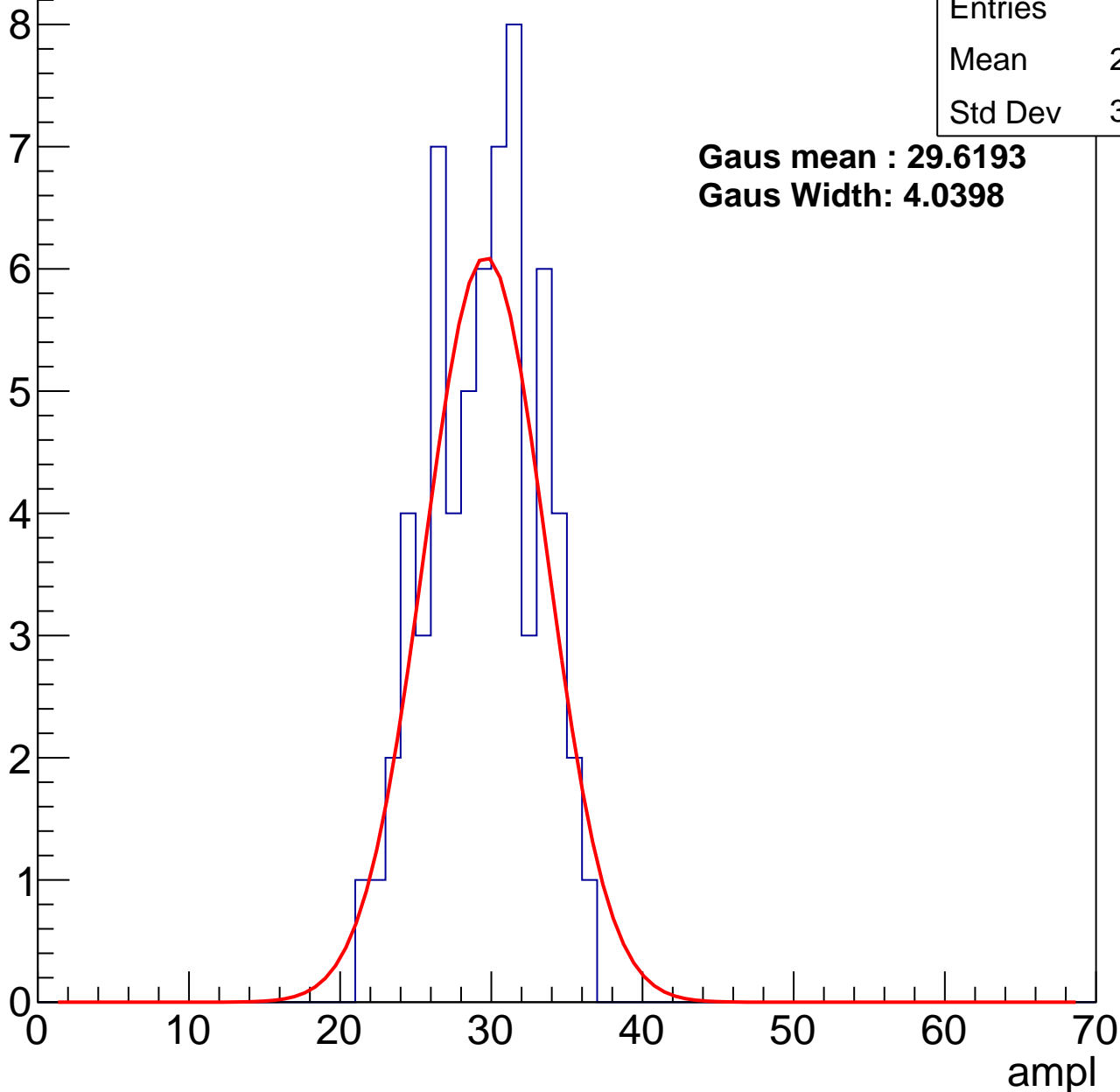
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.03
Std Dev	3.535

Gaus mean : 29.6193

Gaus Width: 4.0398



B1L102S, U9-ch32, adc1

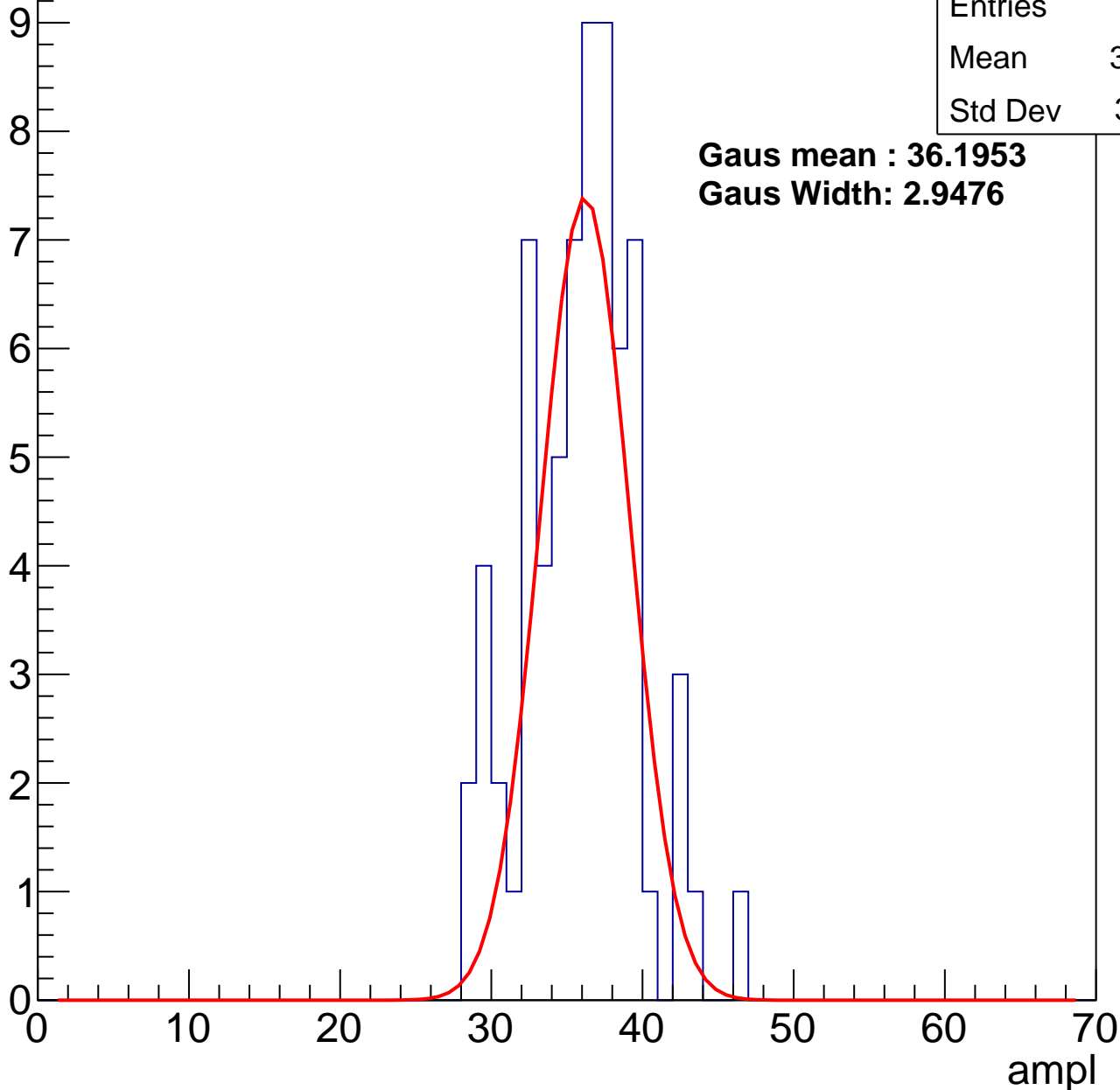
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.46
Std Dev	3.701

Gaus mean : 36.1953

Gaus Width: 2.9476



B1L102S, U9-ch32, adc2

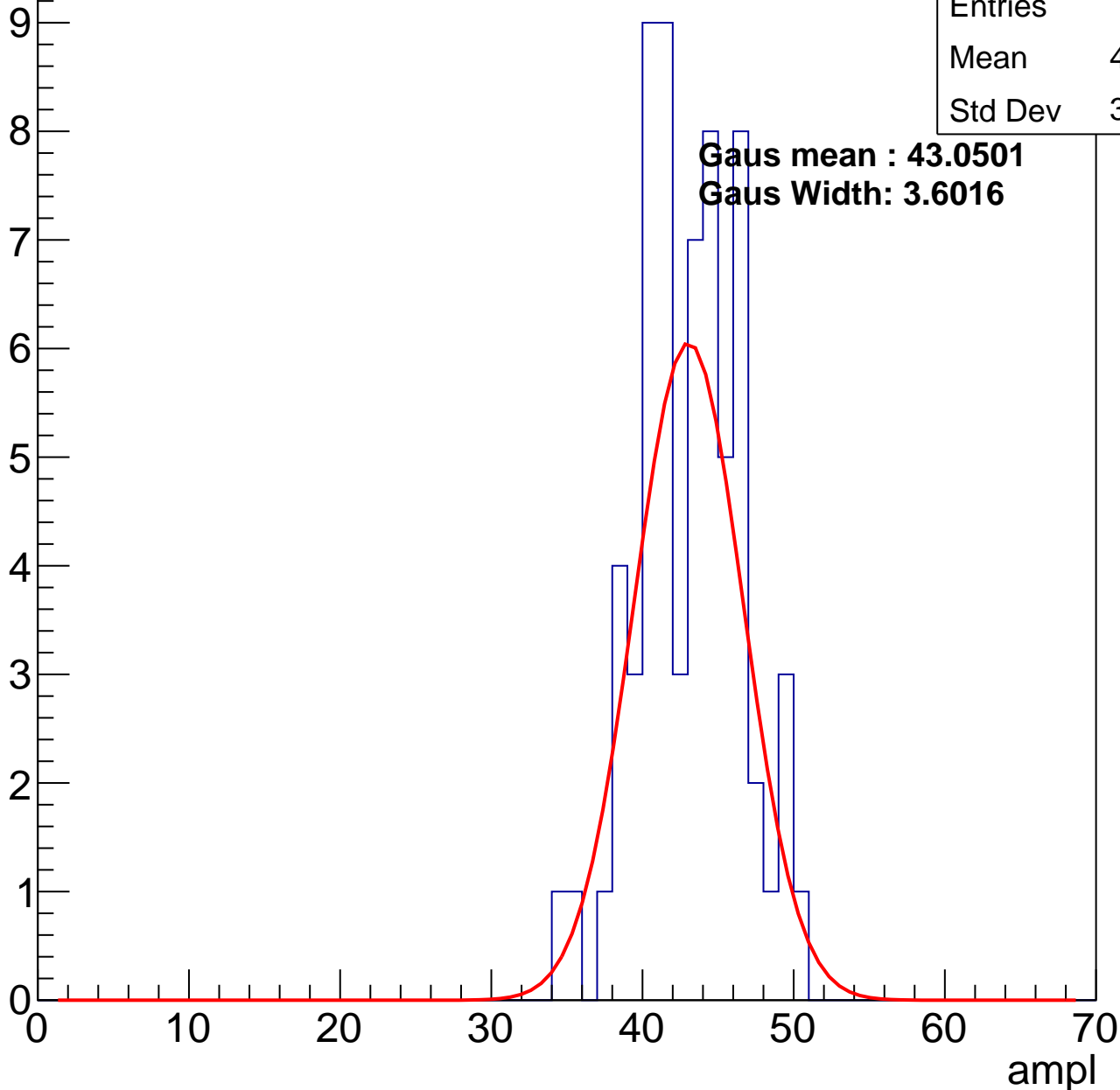
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.65
Std Dev	3.382

Gaus mean : 43.0501

Gaus Width: 3.6016

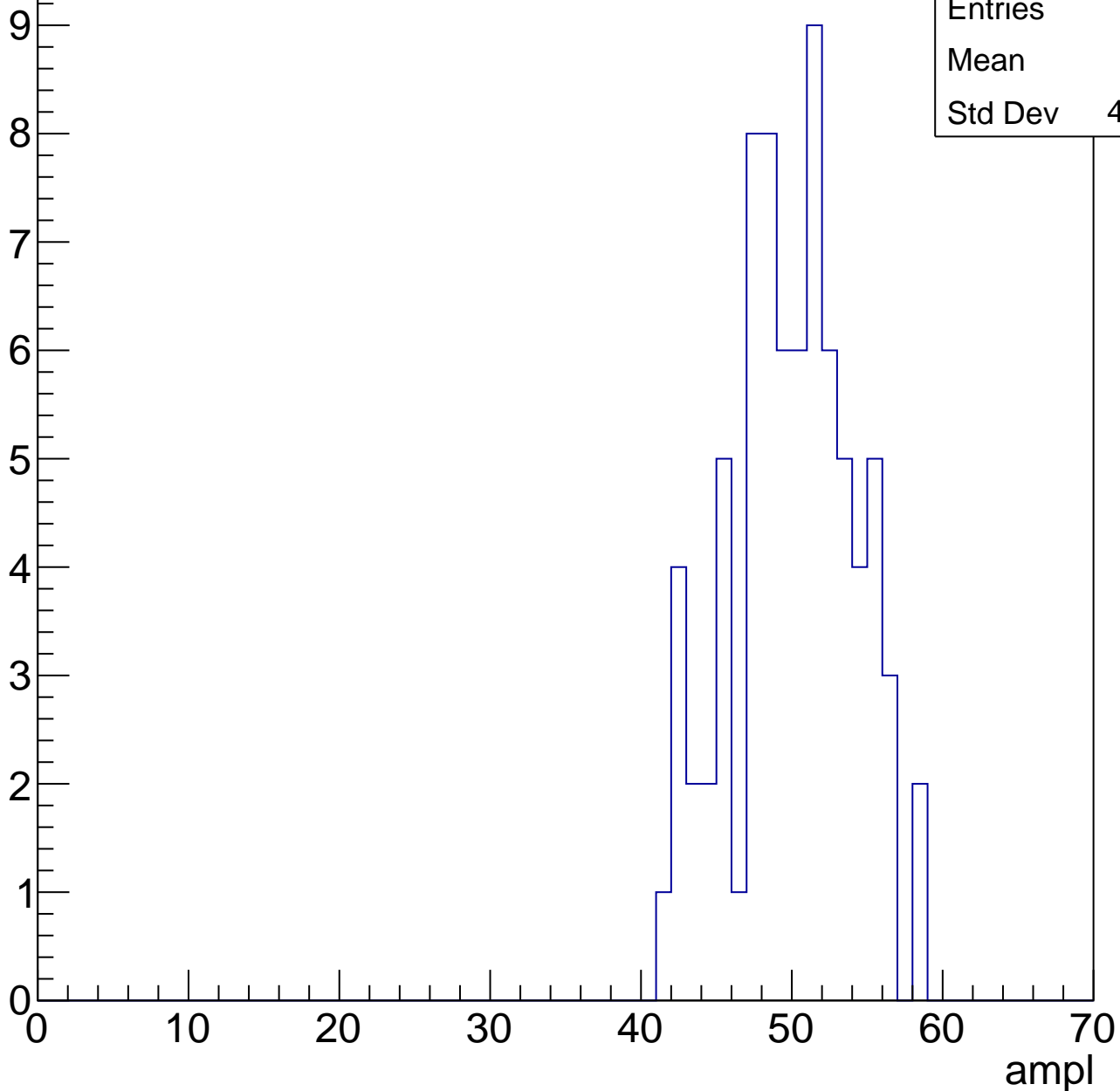


B1L102S, U9-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.6
Std Dev	4.027

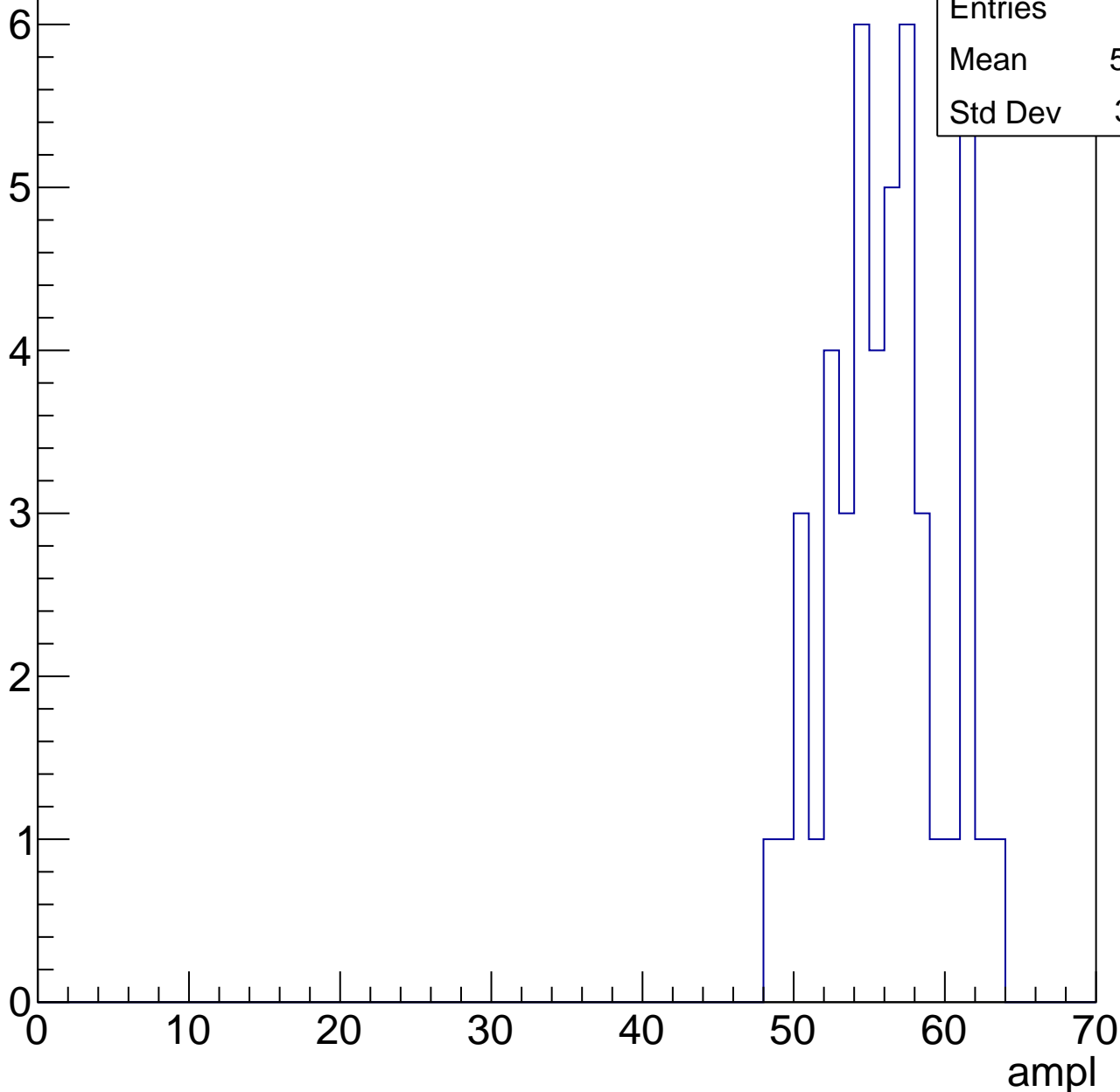


B1L102S, U9-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	55.64
Std Dev	3.681

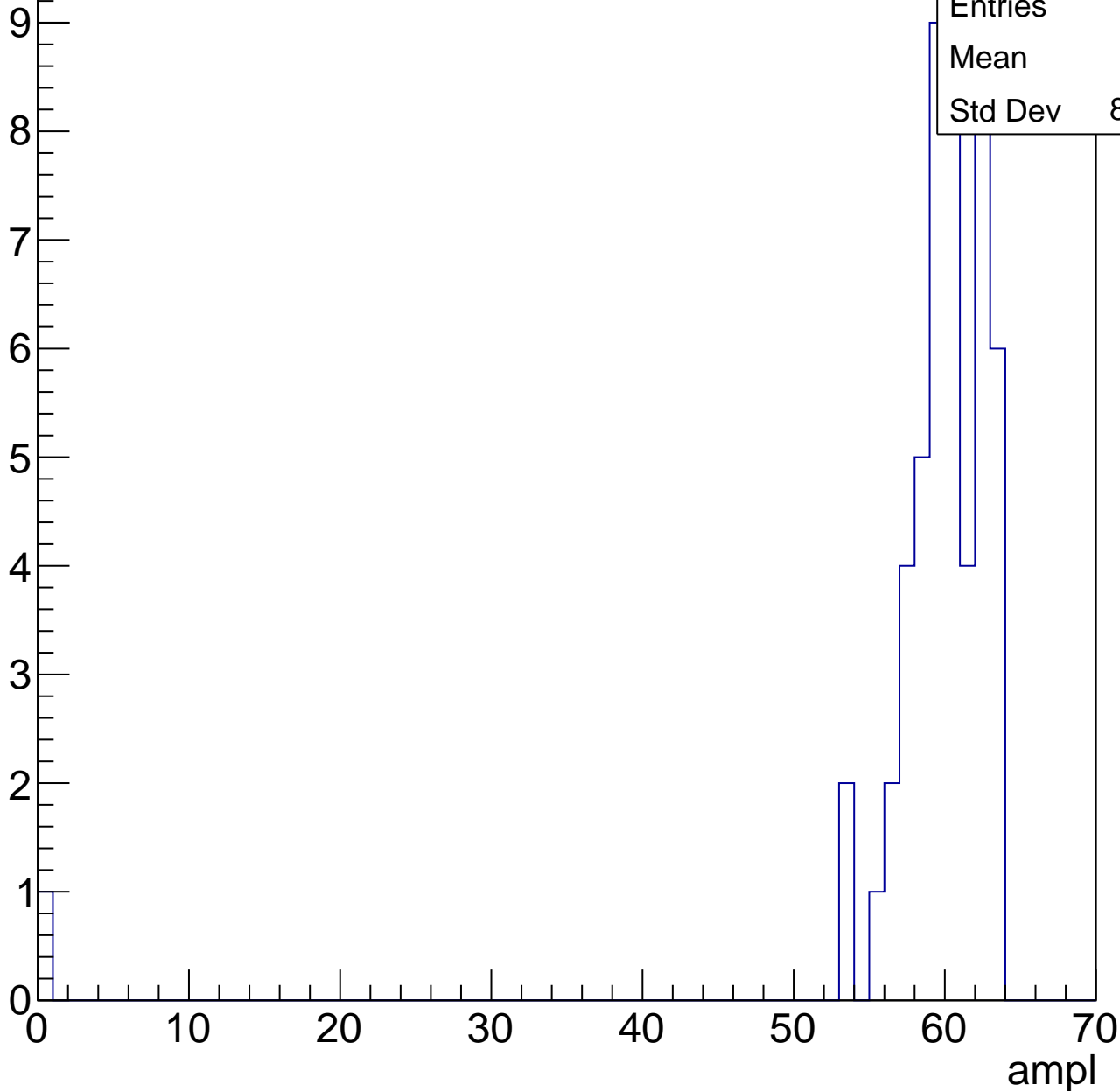


B1L102S, U9-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.5
Std Dev	8.545



B1L102S, U9-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch33, adc0

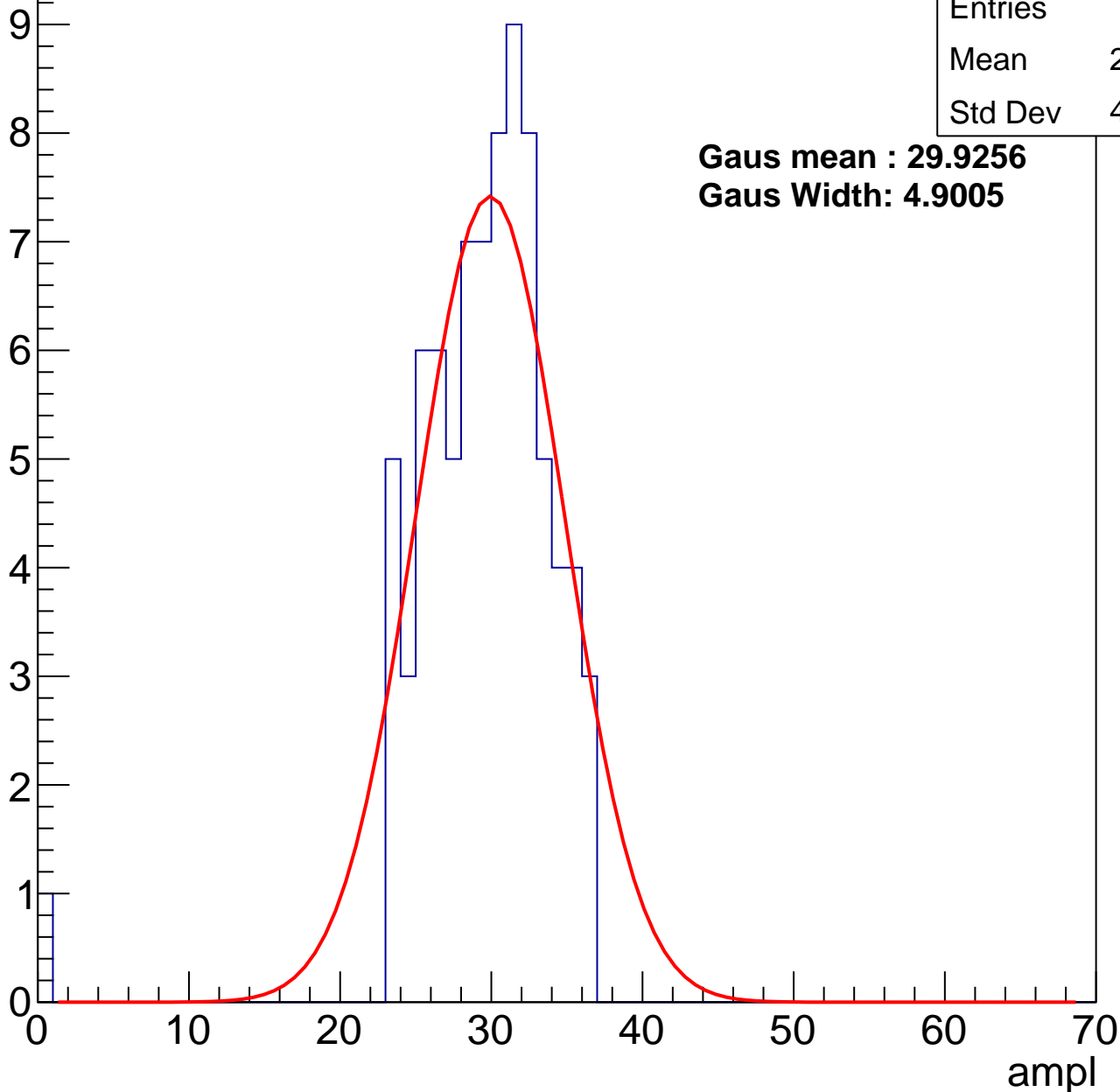
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.02
Std Dev	4.792

Gaus mean : 29.9256

Gaus Width: 4.9005



B1L102S, U9-ch33, adc1

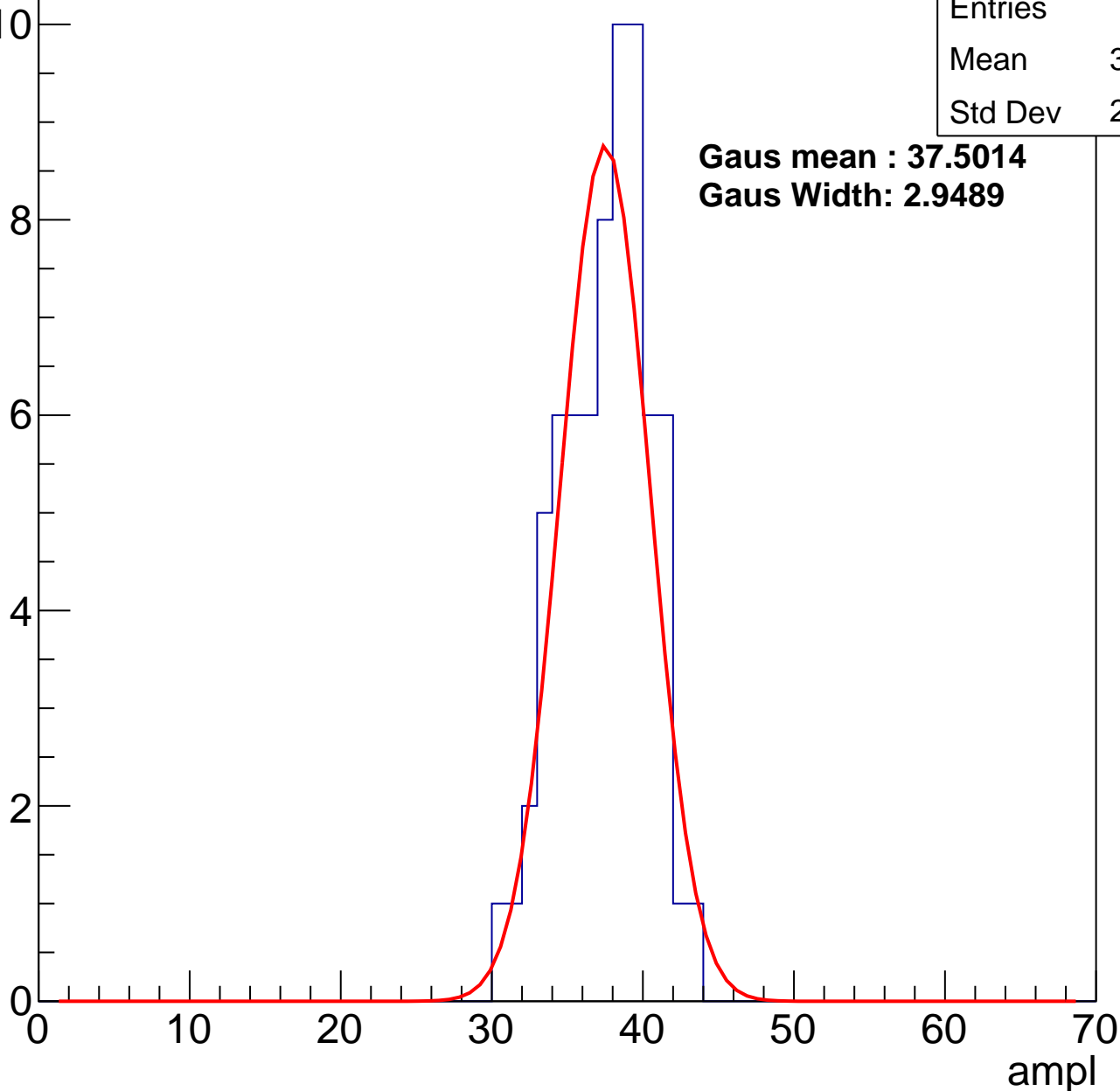
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.06
Std Dev	2.848

Gaus mean : 37.5014

Gaus Width: 2.9489



B1L102S, U9-ch33, adc2

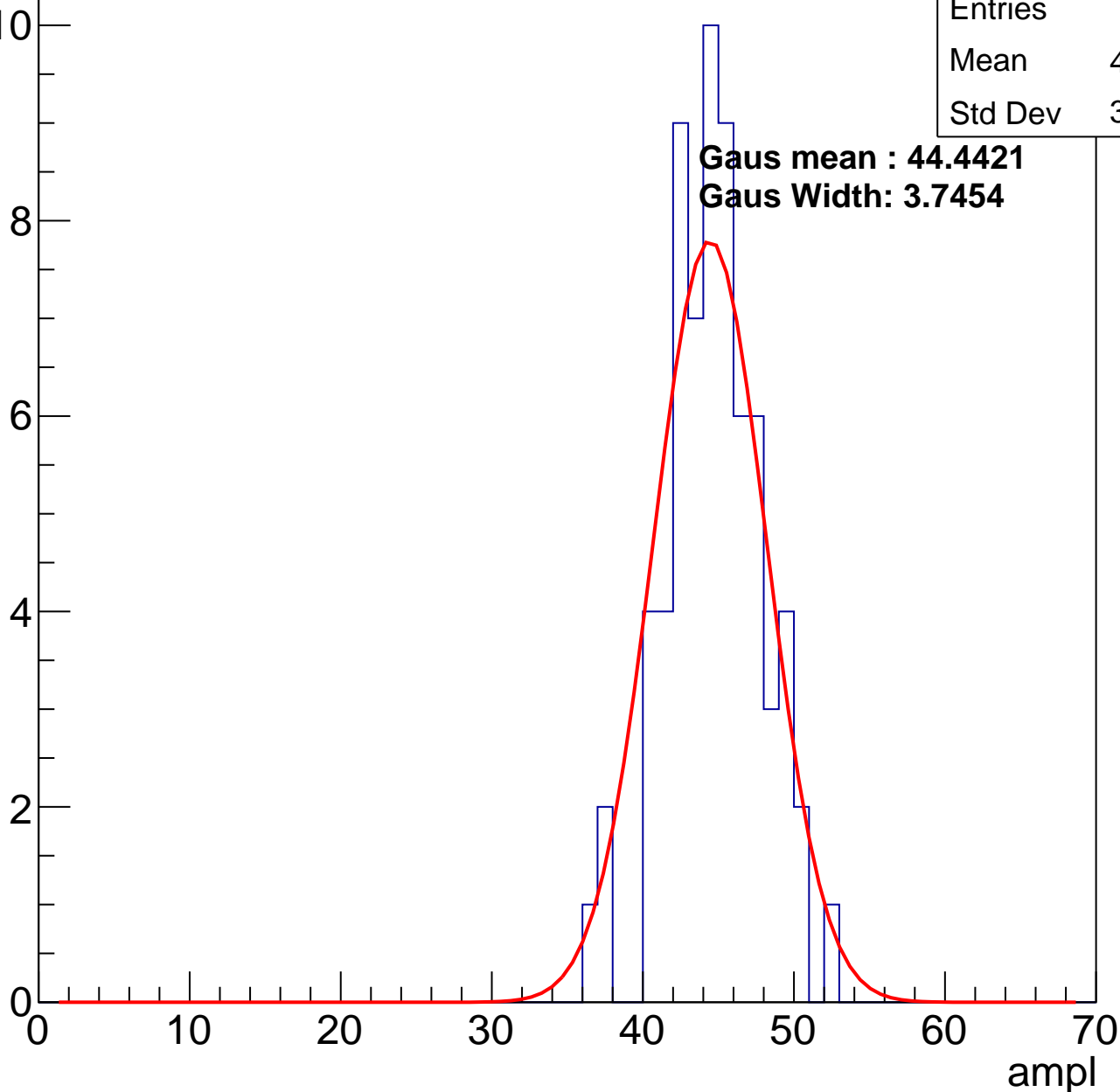
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	44.24
Std Dev	3.144

Gaus mean : 44.4421

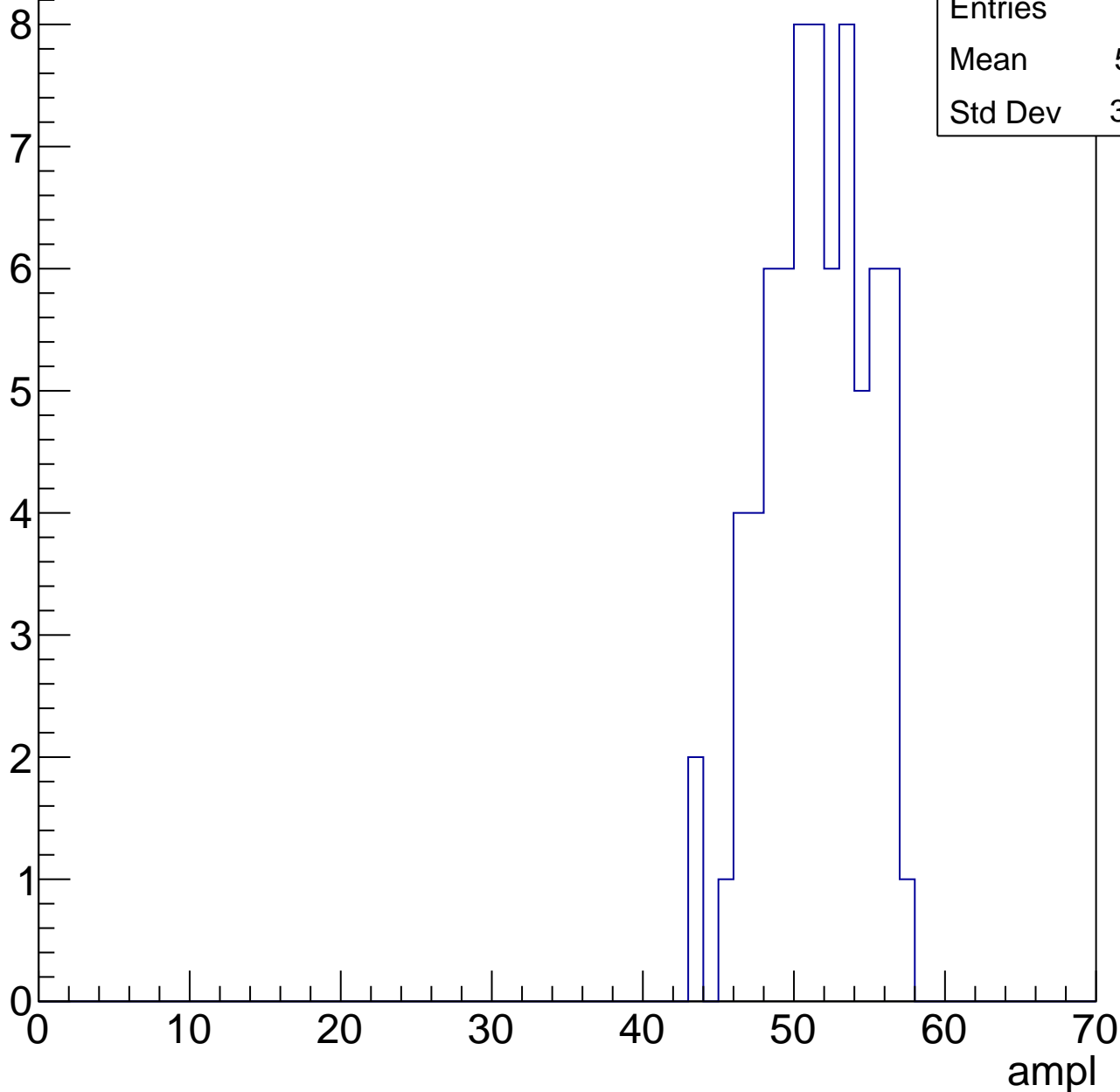
Gaus Width: 3.7454



B1L102S, U9-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

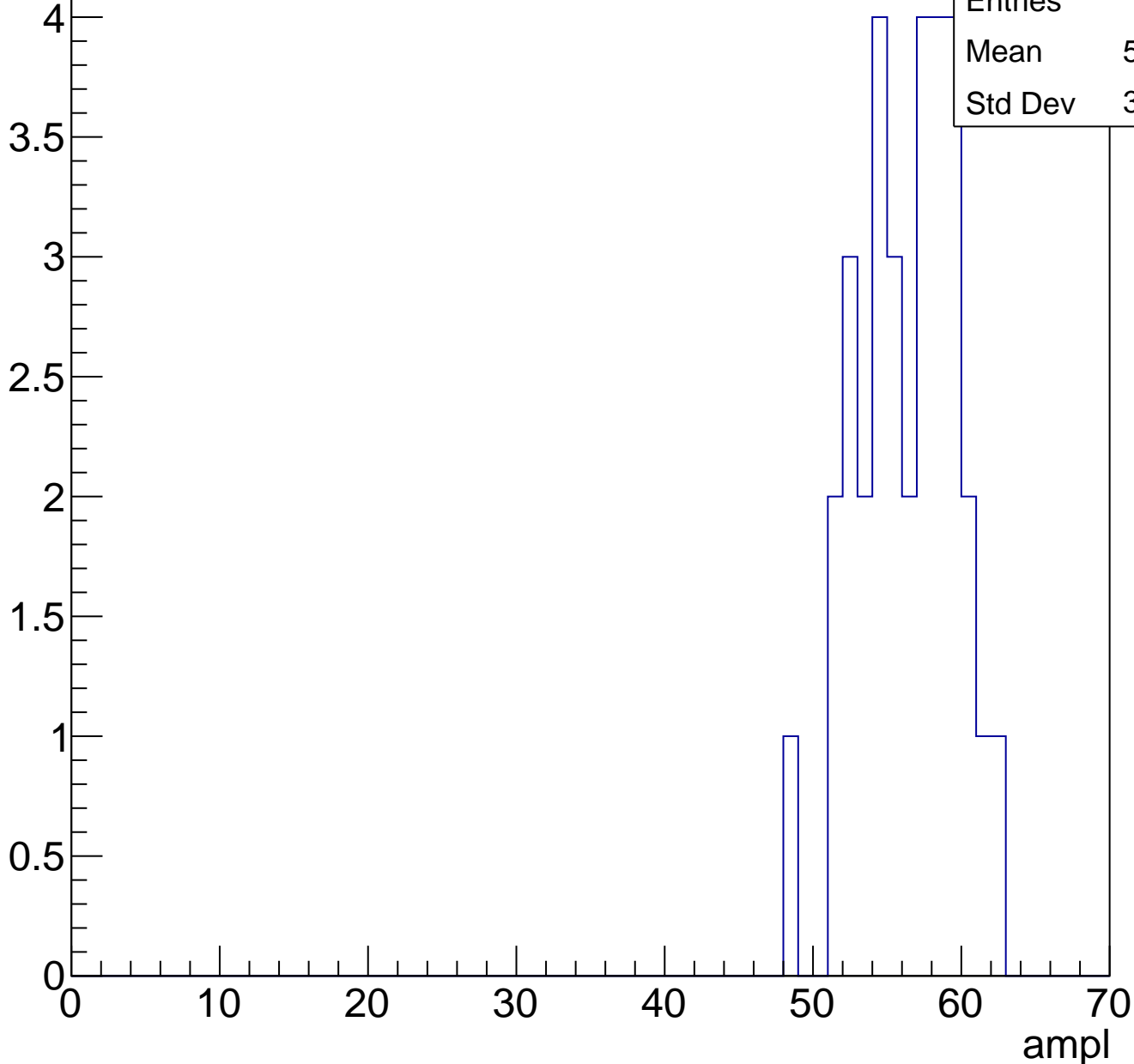


Entries	71
Mean	51.01
Std Dev	3.312

B1L102S, U9-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

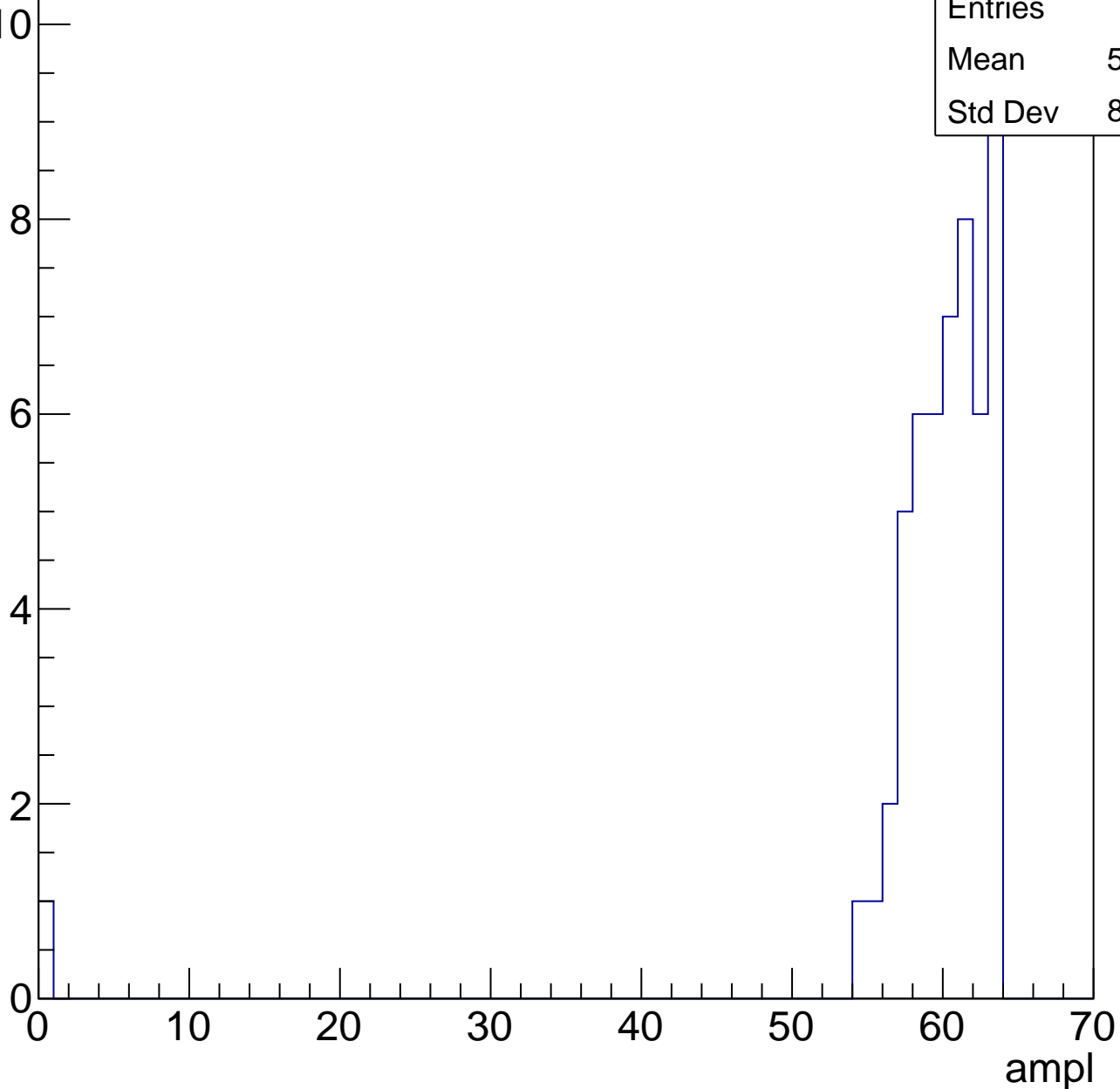


B1L102S, U9-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

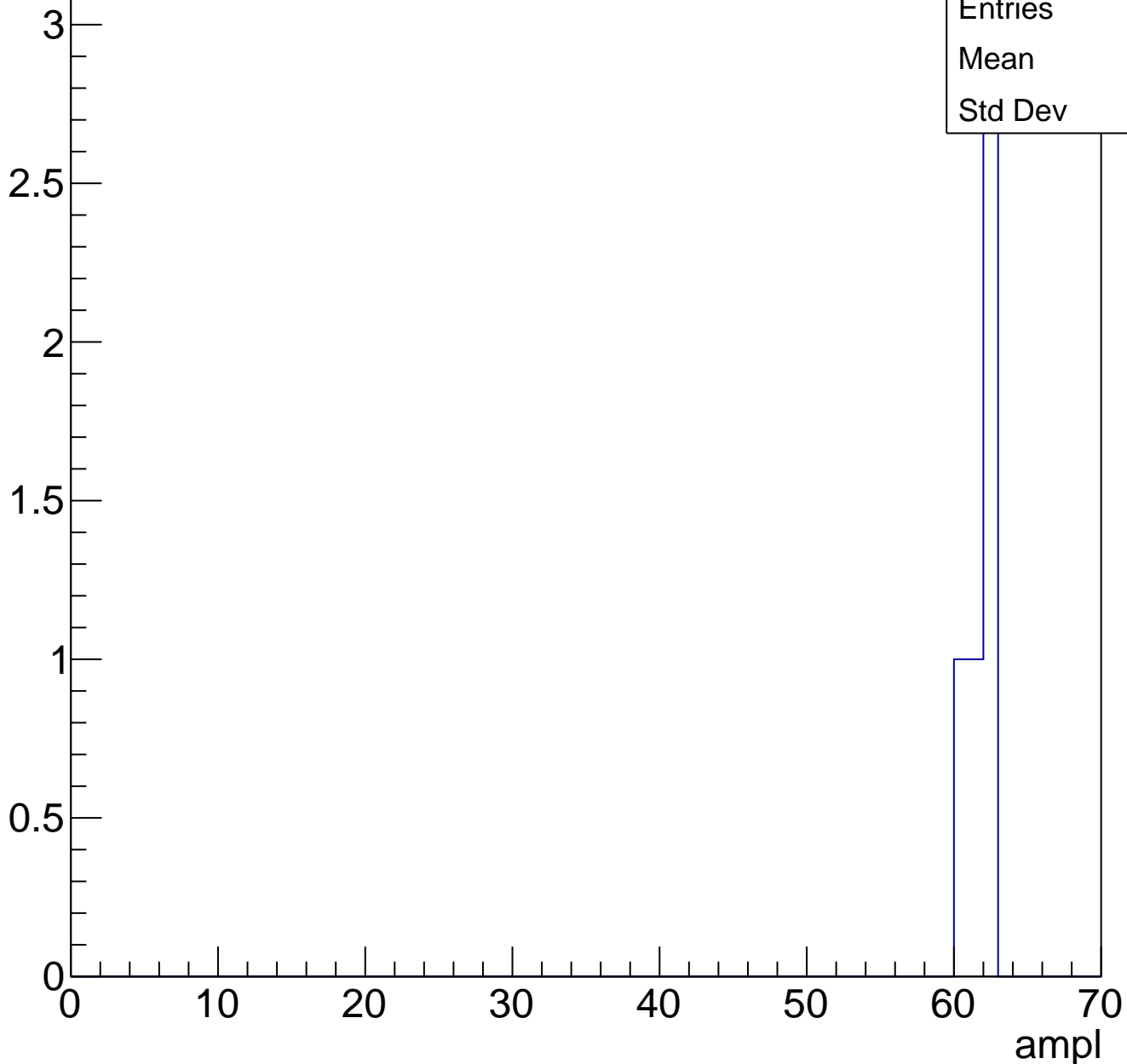
Entries	53
Mean	58.83
Std Dev	8.487



B1L102S, U9-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch34, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	29.91
Std Dev	4.86

Gaus mean : 30.9582

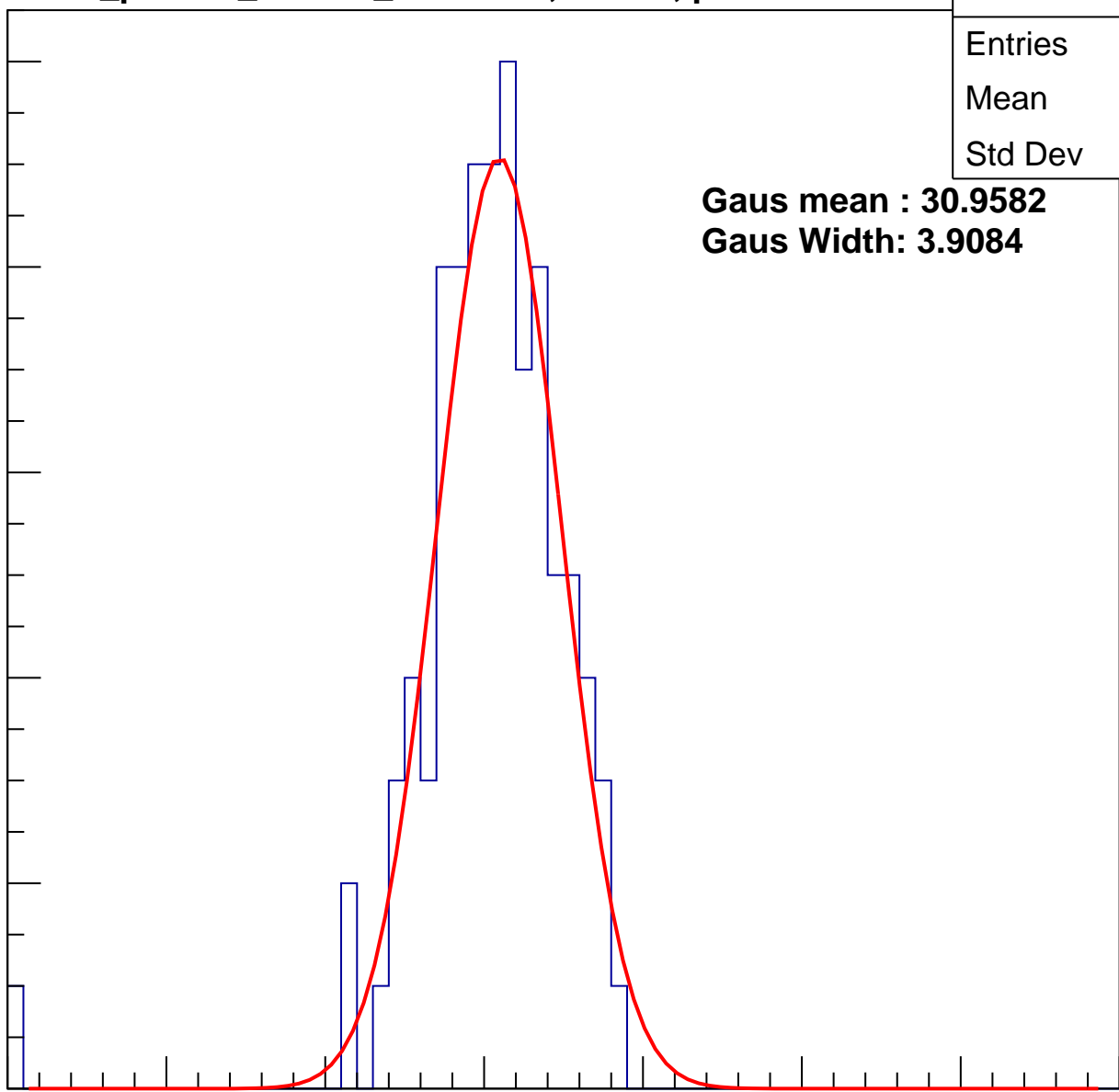
Gaus Width: 3.9084

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch34, adc1

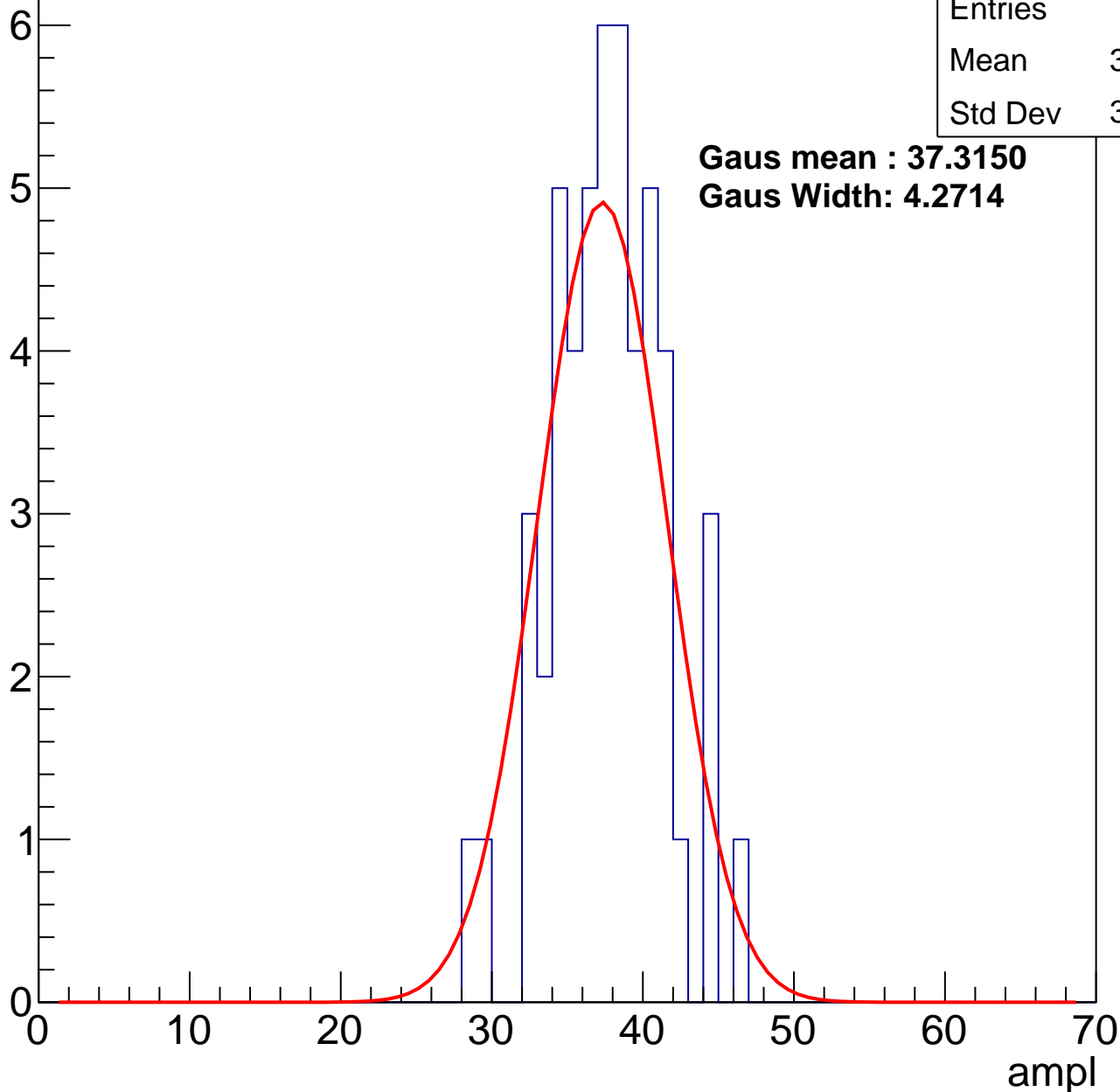
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	37.24
Std Dev	3.703

Gaus mean : 37.3150

Gaus Width: 4.2714



B1L102S, U9-ch34, adc2

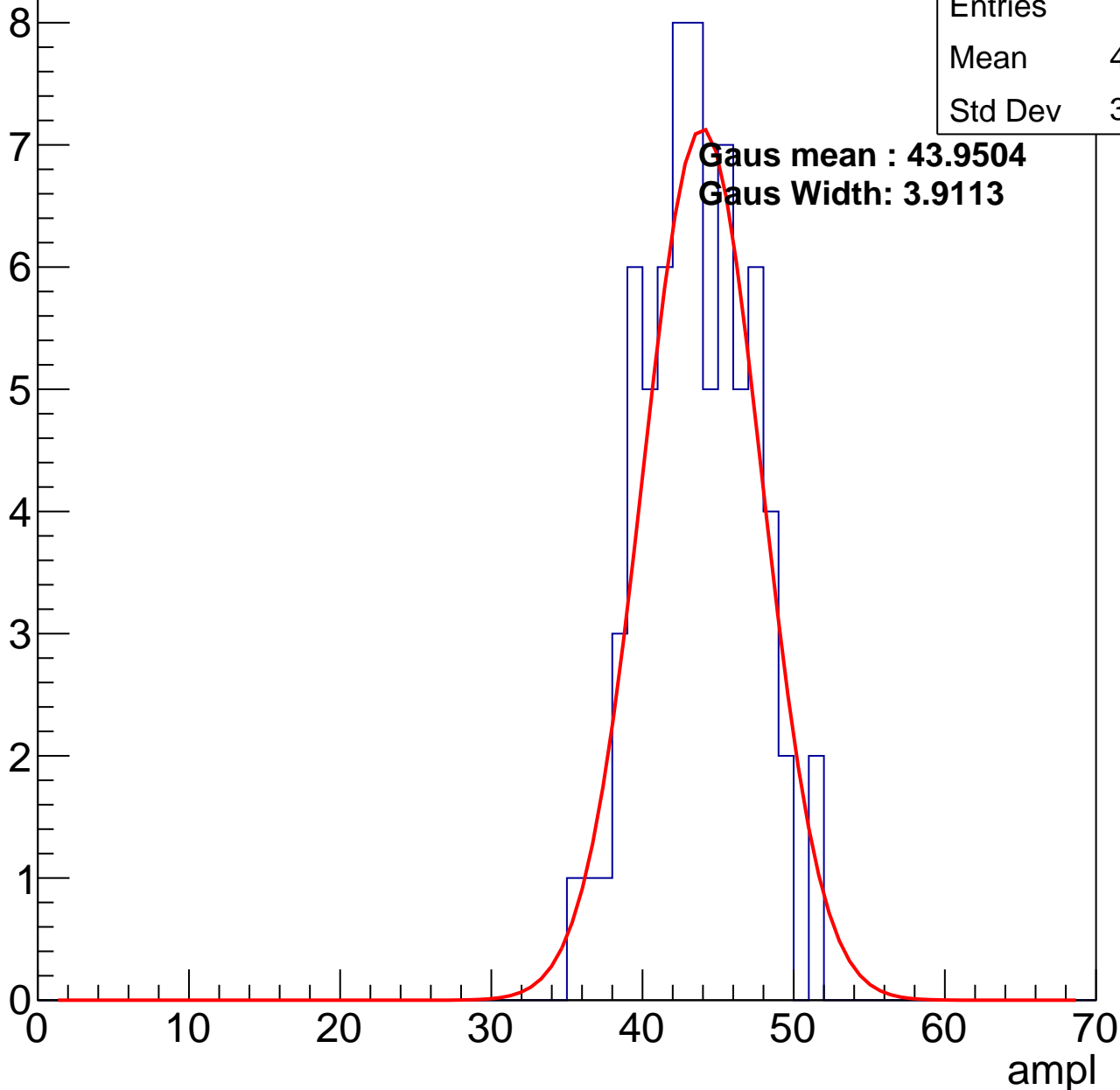
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.16
Std Dev	3.524

Gaus mean : 43.9504

Gaus Width: 3.9113

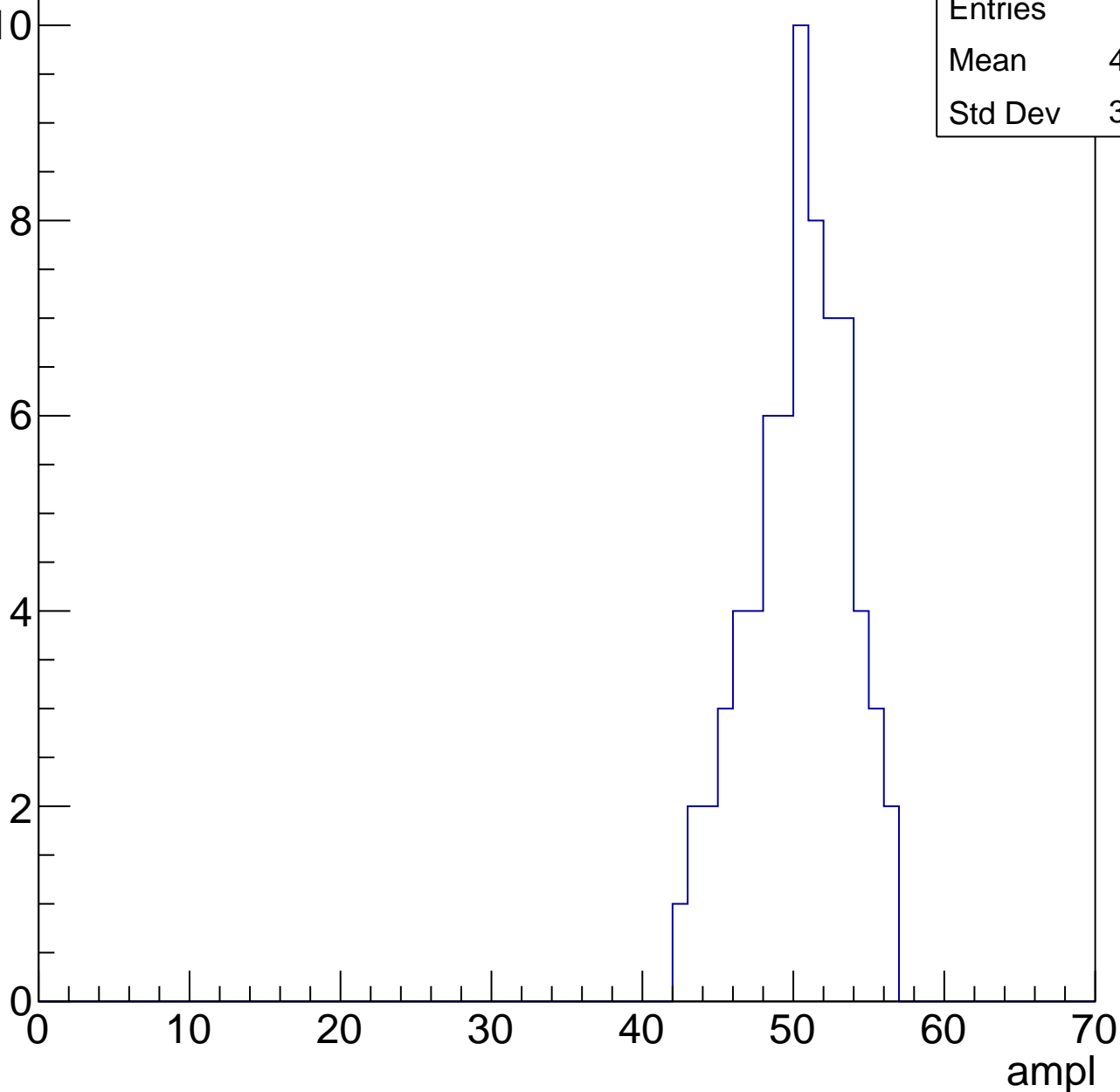


B1L102S, U9-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

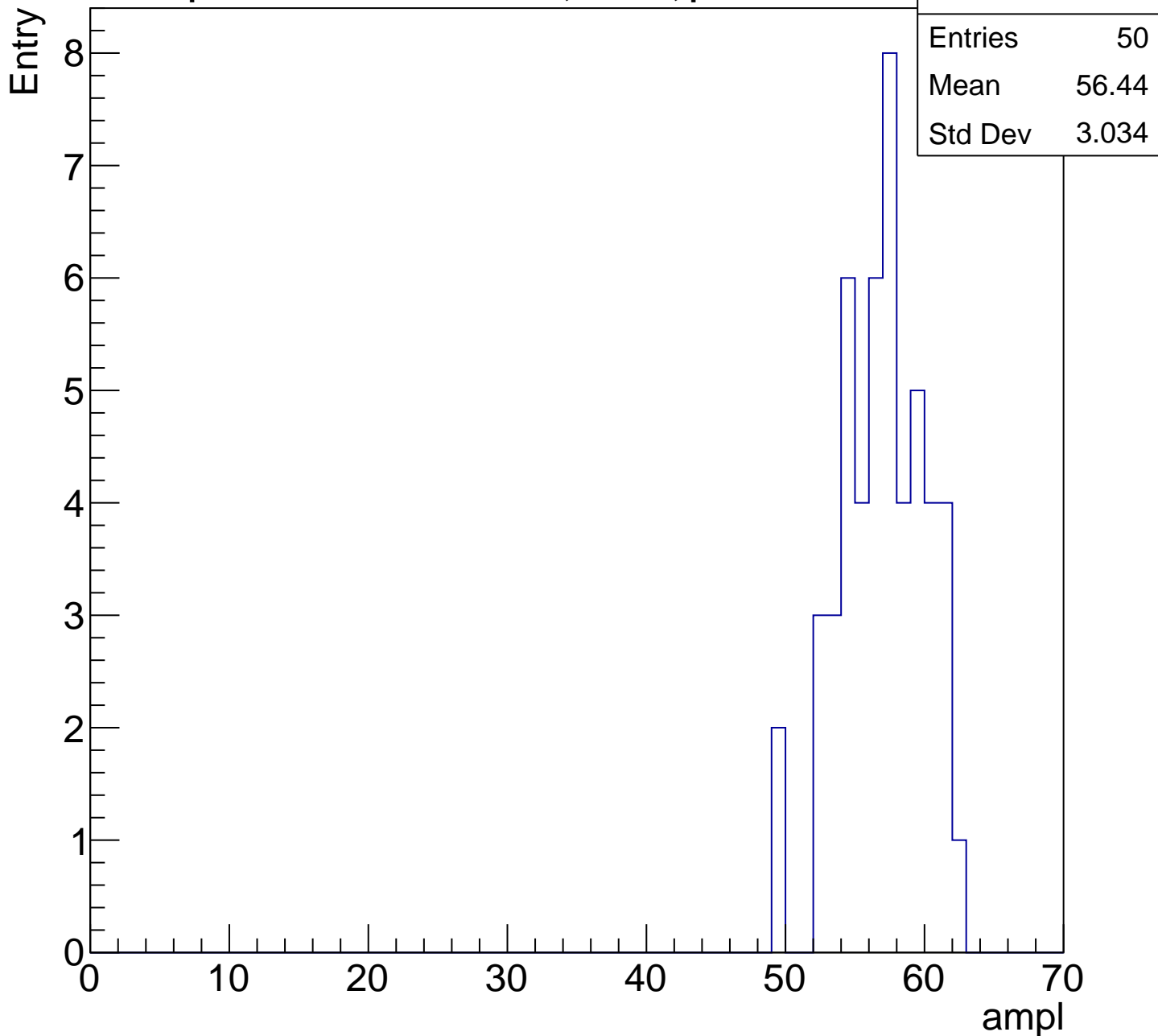
Entry

Entries	69
Mean	49.87
Std Dev	3.292



B1L102S, U9-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

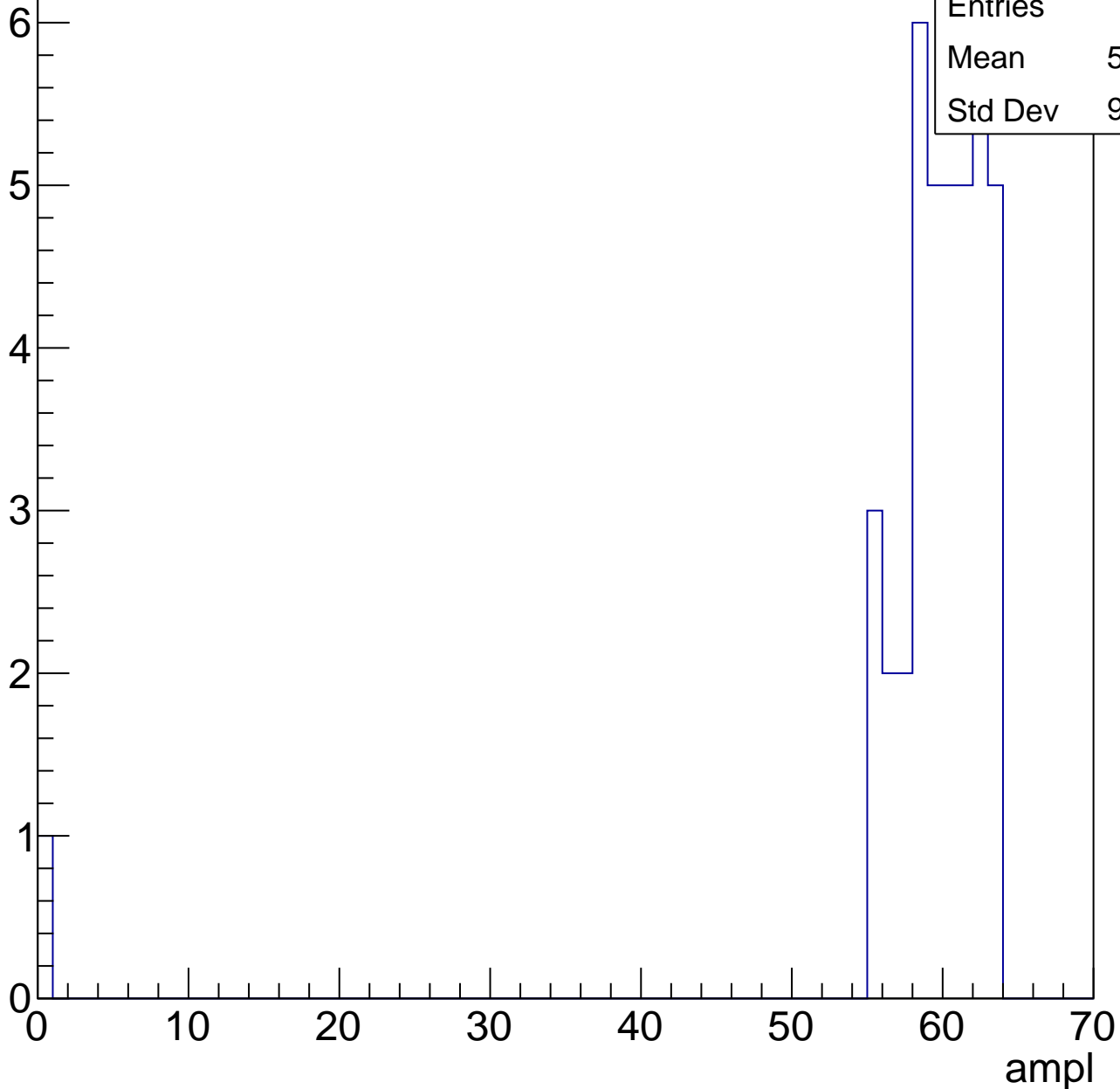


B1L102S, U9-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.15
Std Dev	9.606

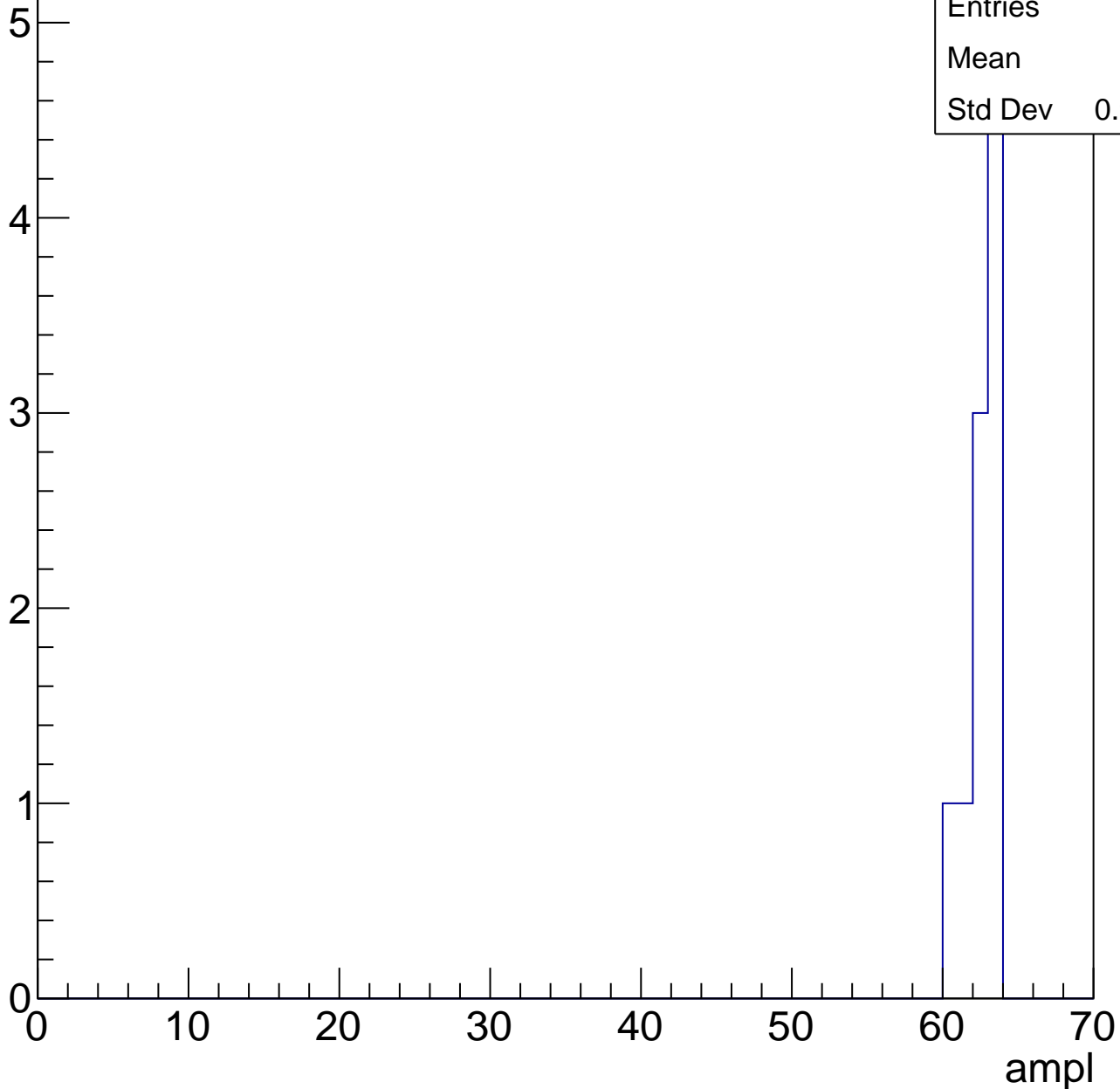


B1L102S, U9-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62.2
Std Dev	0.9798



B1L102S, U9-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch35, adc0

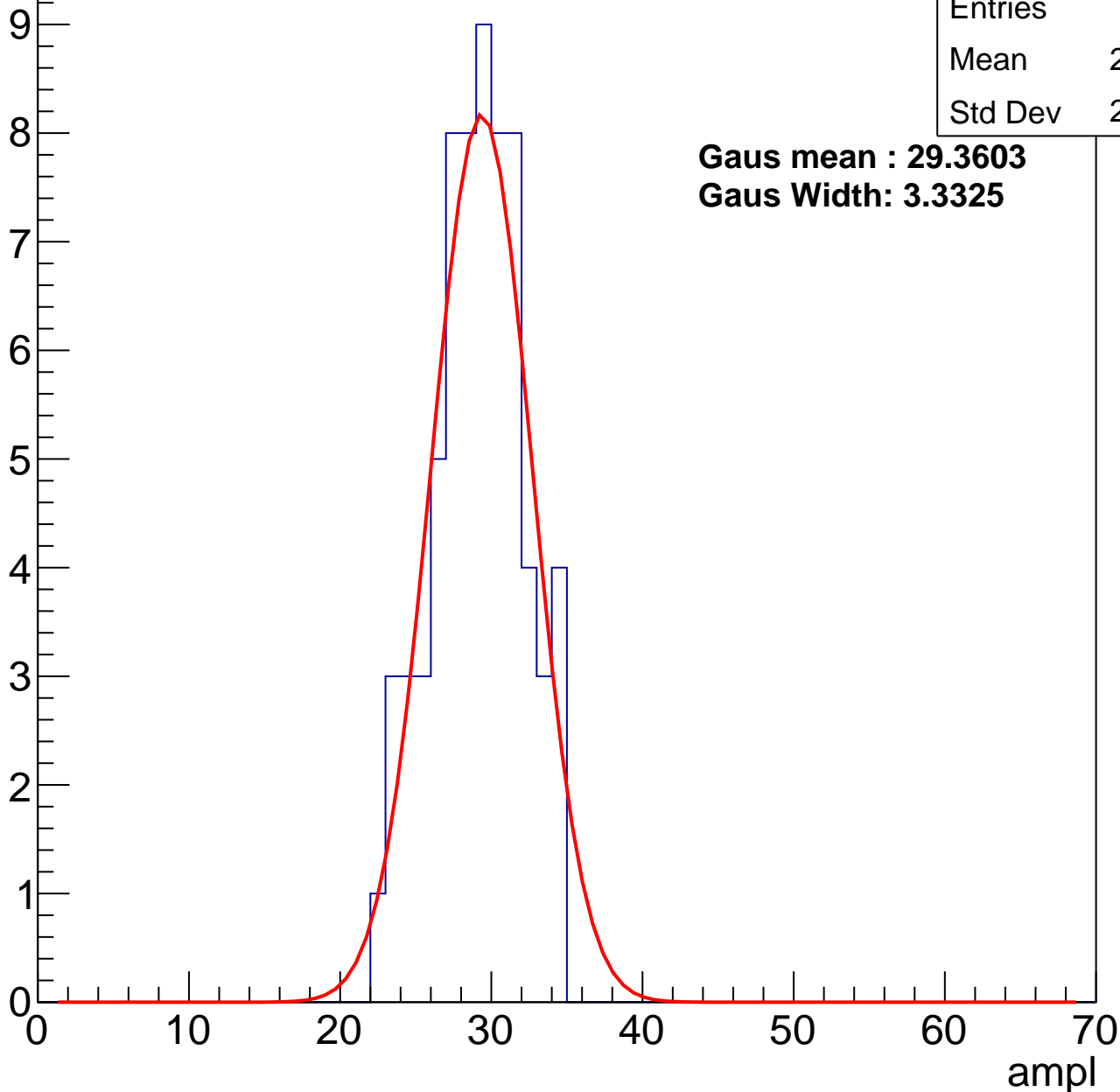
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.66
Std Dev	2.945

Gaus mean : 29.3603

Gaus Width: 3.3325



B1L102S, U9-ch35, adc1

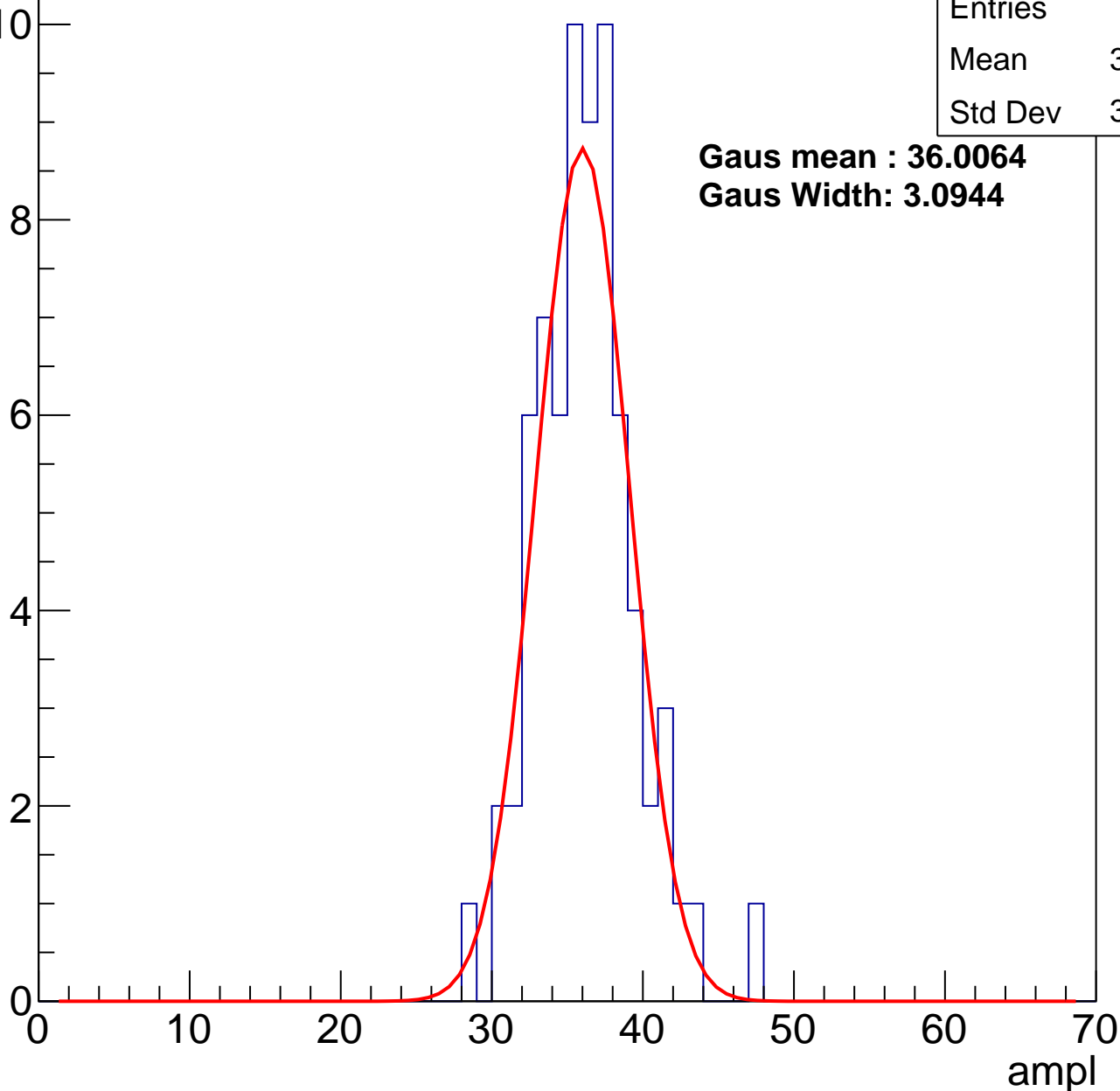
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.77
Std Dev	3.268

Gaus mean : 36.0064

Gaus Width: 3.0944



B1L102S, U9-ch35, adc2

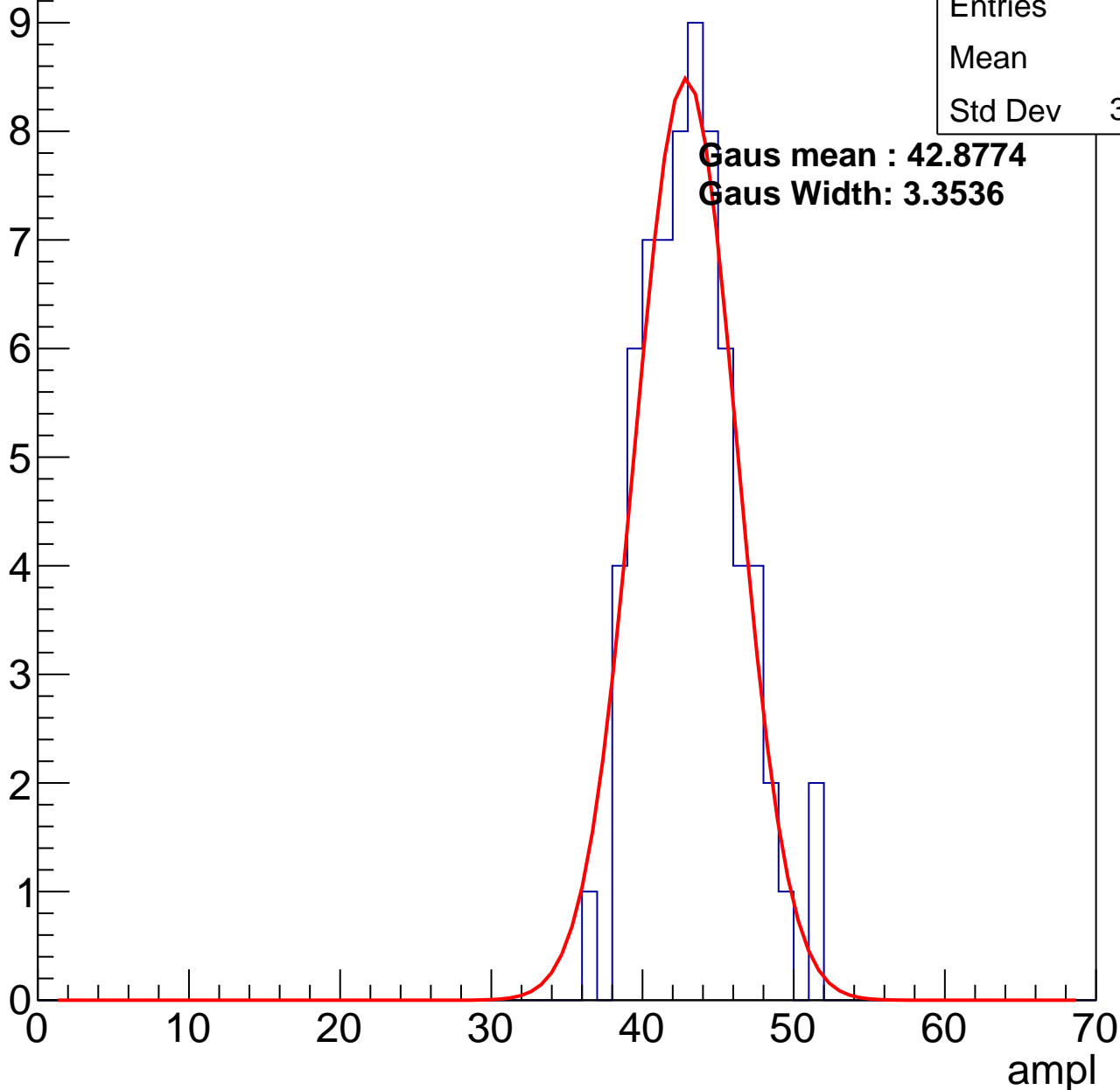
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.8
Std Dev	3.156

Gaus mean : 42.8774

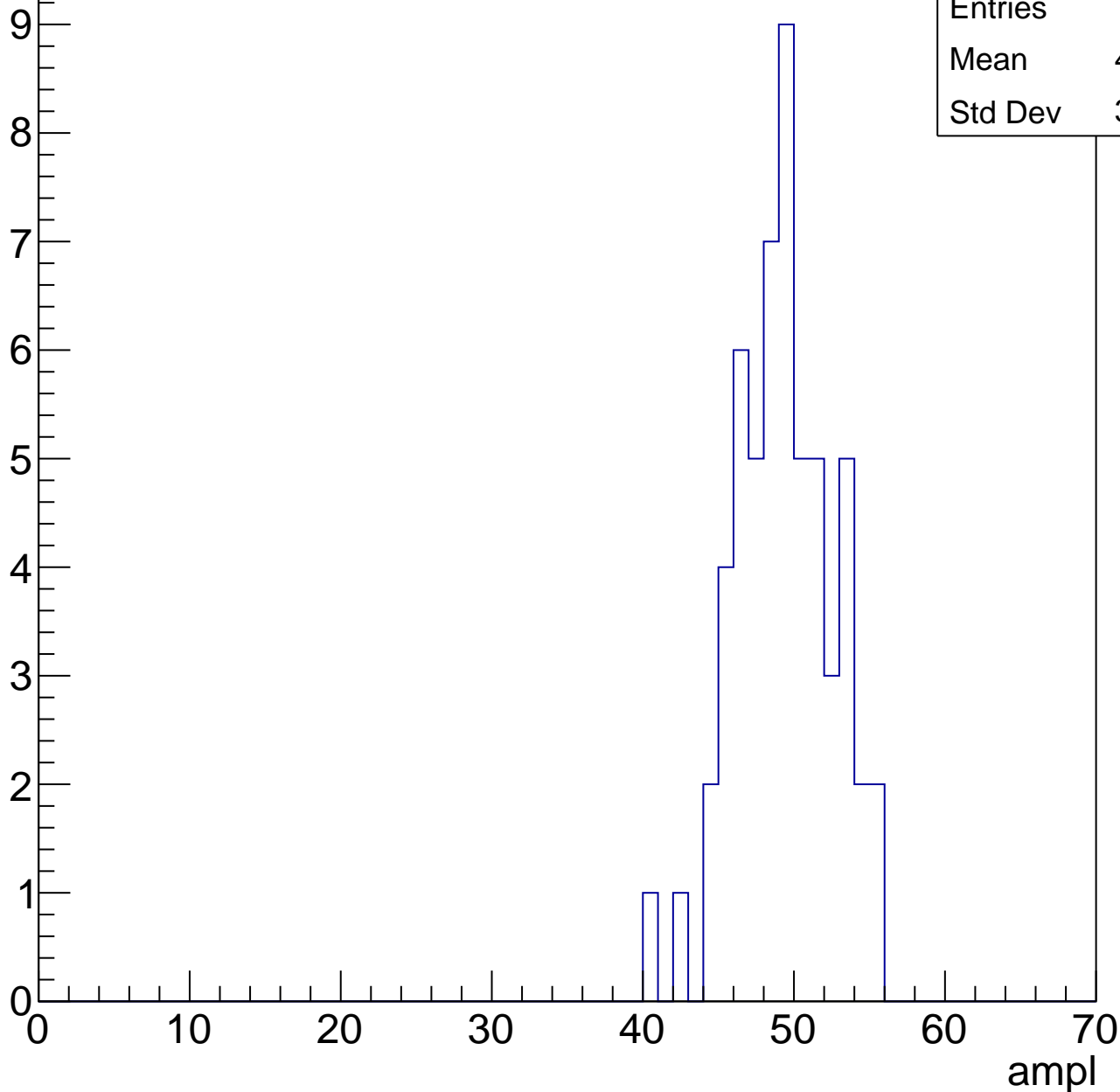
Gaus Width: 3.3536



B1L102S, U9-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

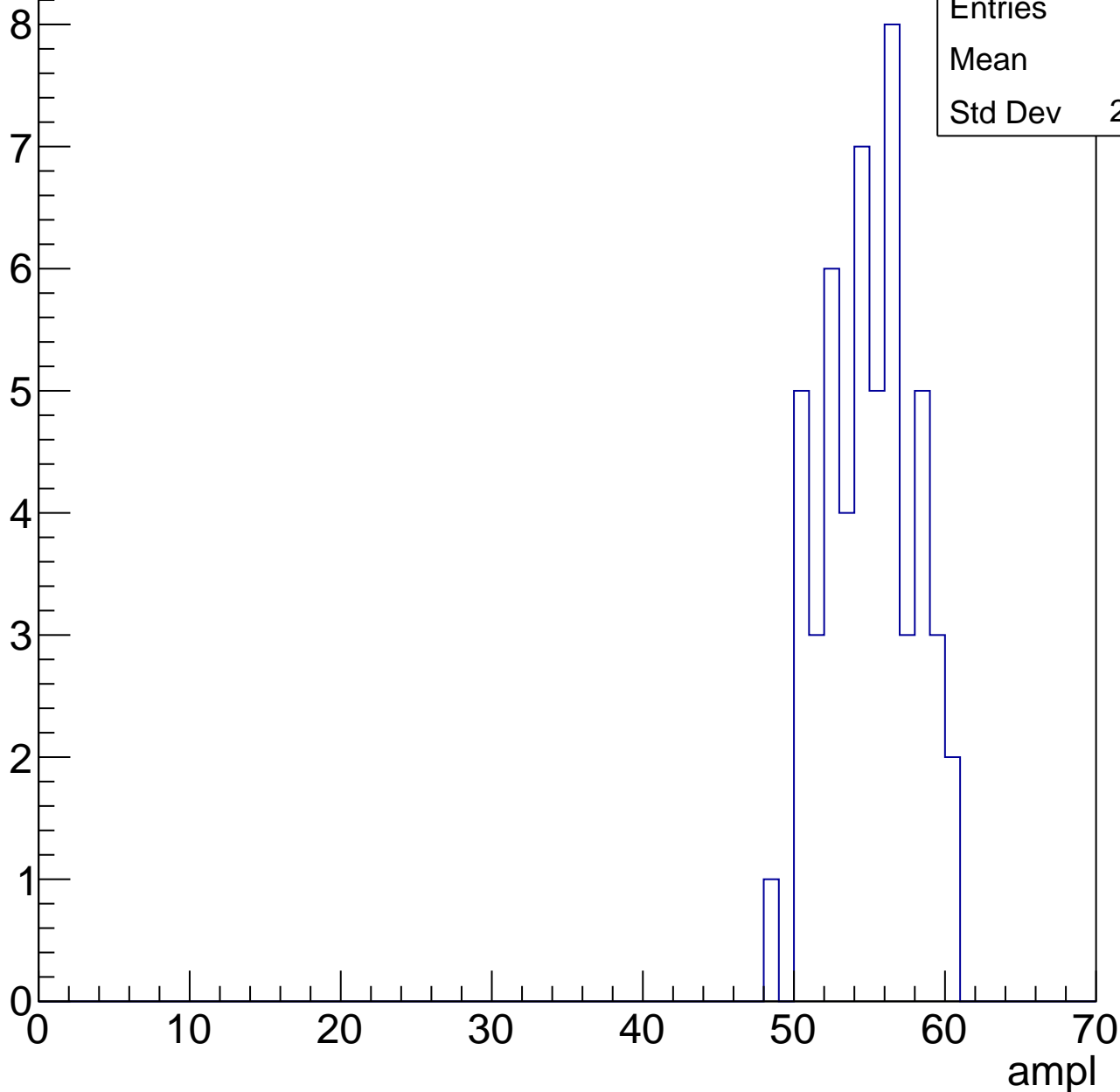


B1L102S, U9-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.5
Std Dev	2.938

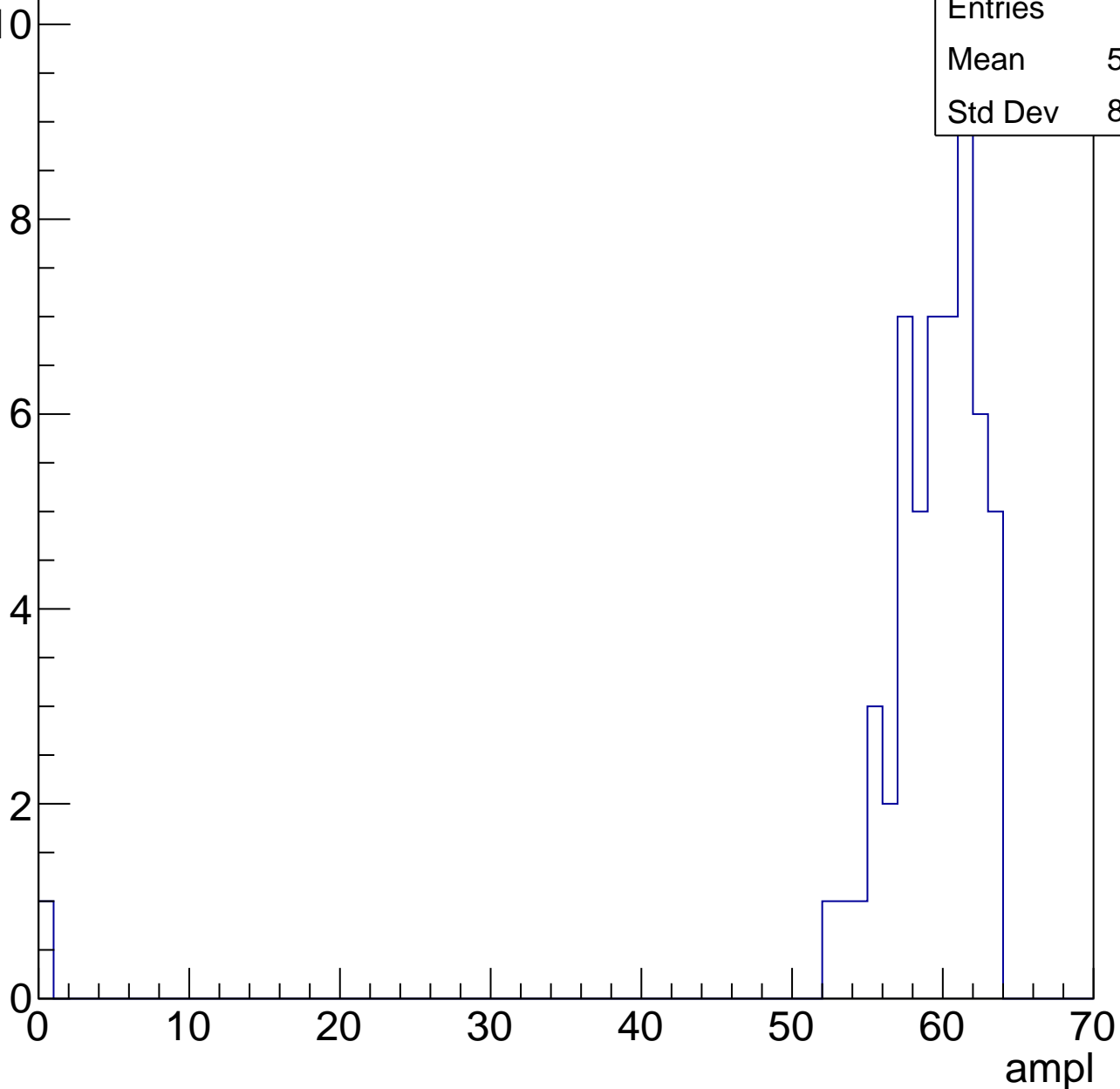


B1L102S, U9-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.12
Std Dev	8.268

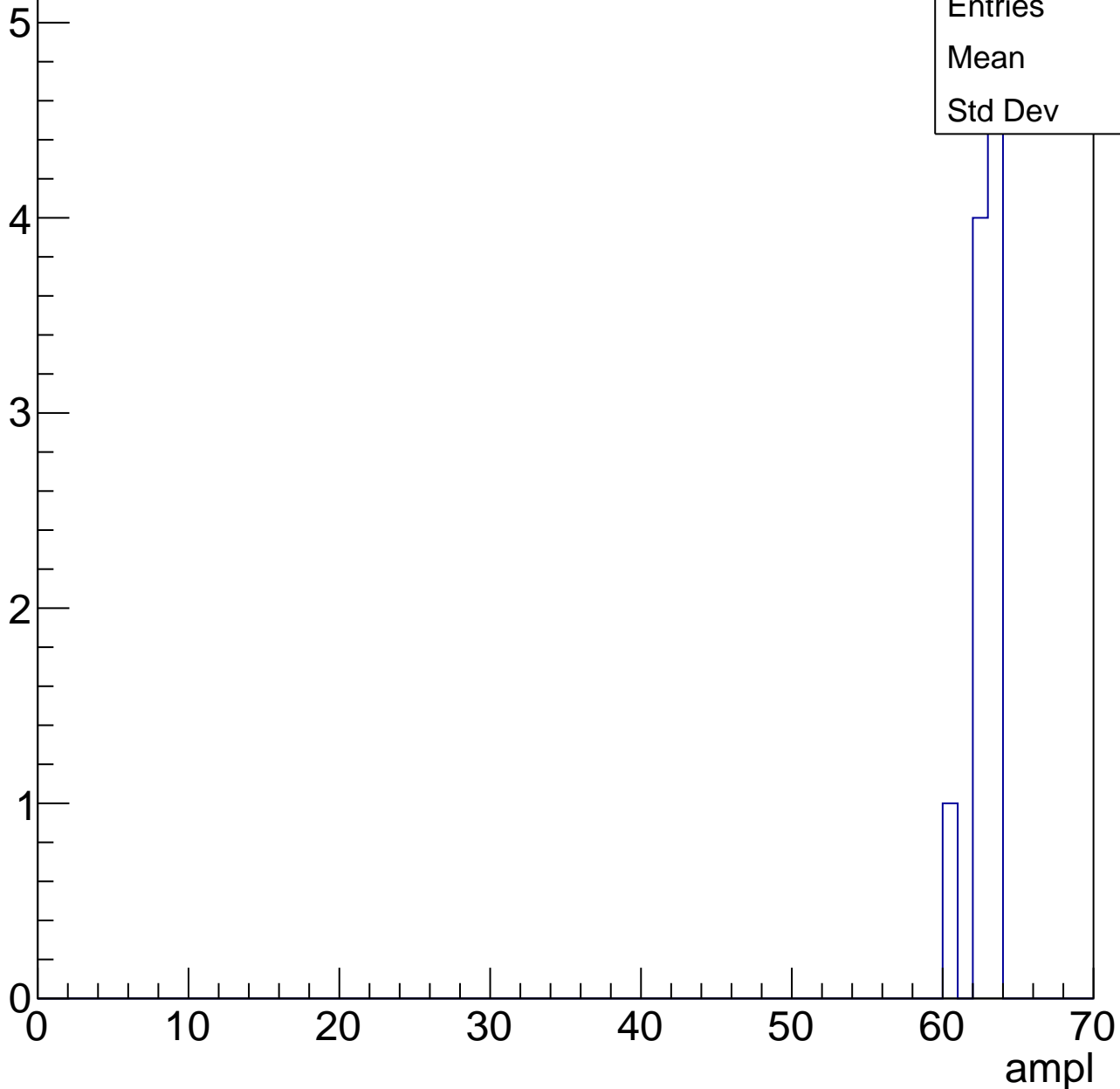


B1L102S, U9-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62.3
Std Dev	0.9



B1L102S, U9-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch36, adc0

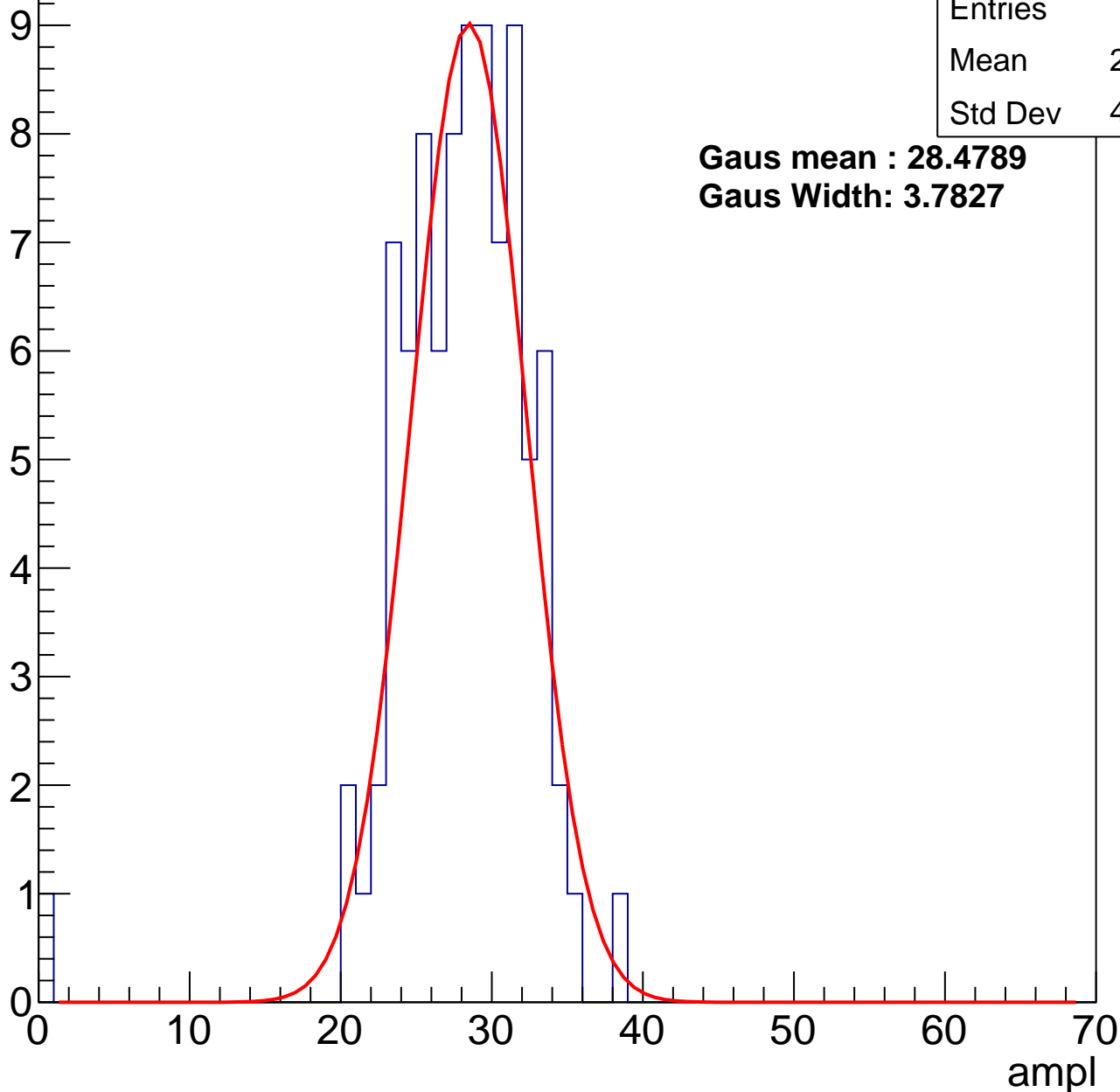
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	27.59
Std Dev	4.669

Gaus mean : 28.4789

Gaus Width: 3.7827



B1L102S, U9-ch36, adc1

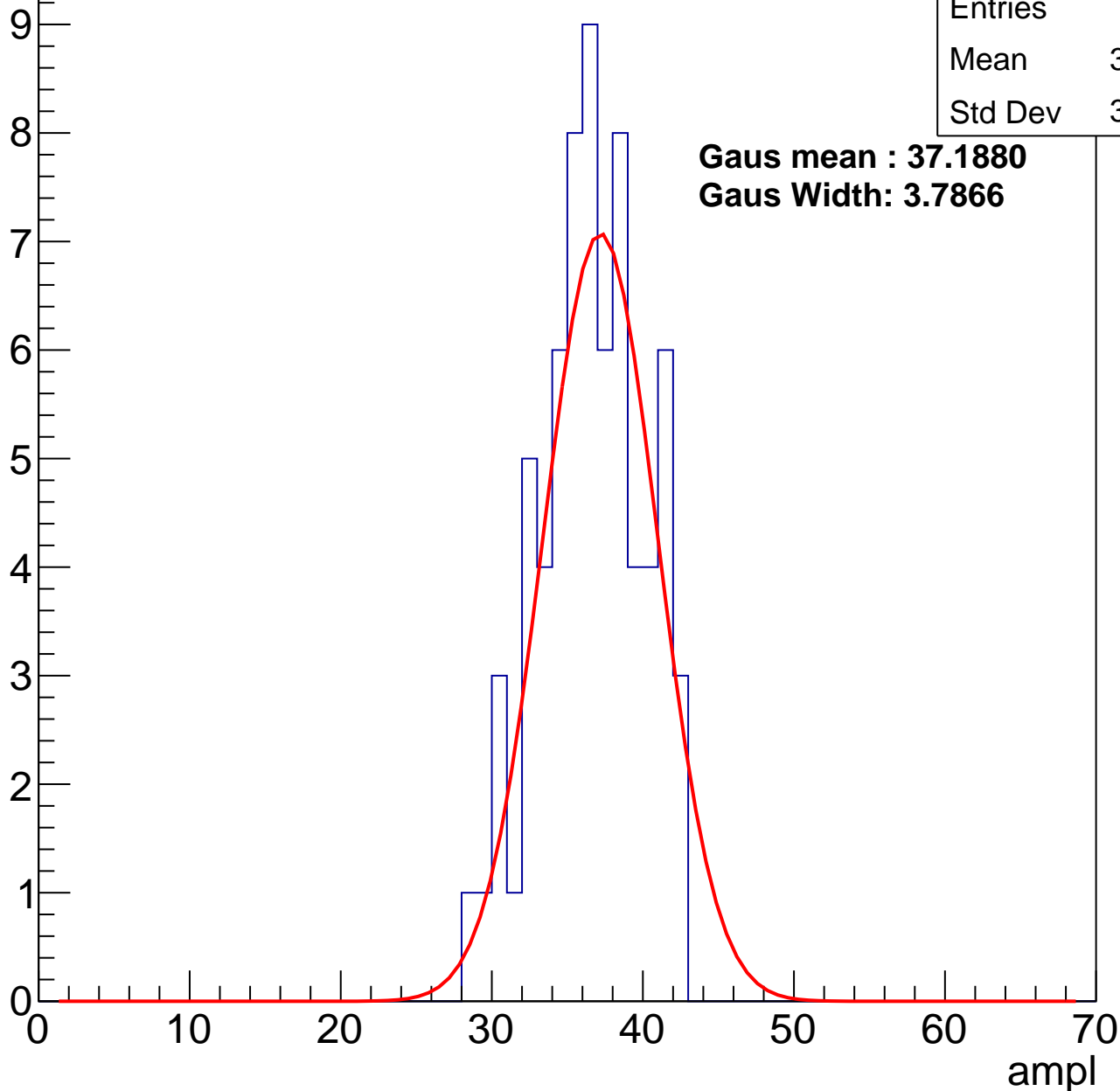
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.12
Std Dev	3.382

Gaus mean : 37.1880

Gaus Width: 3.7866



B1L102S, U9-ch36, adc2

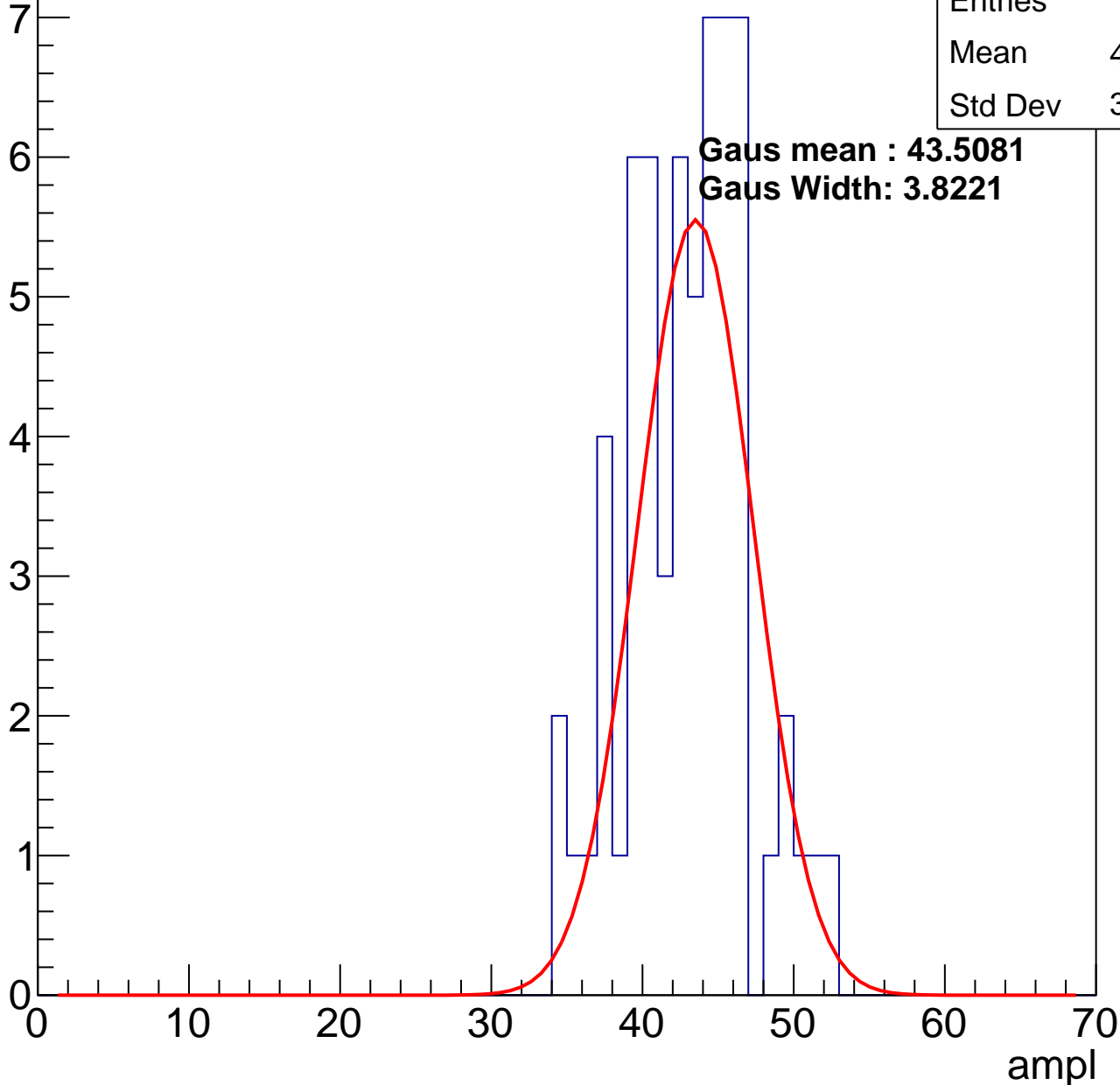
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.47
Std Dev	3.983

Gaus mean : 43.5081

Gaus Width: 3.8221

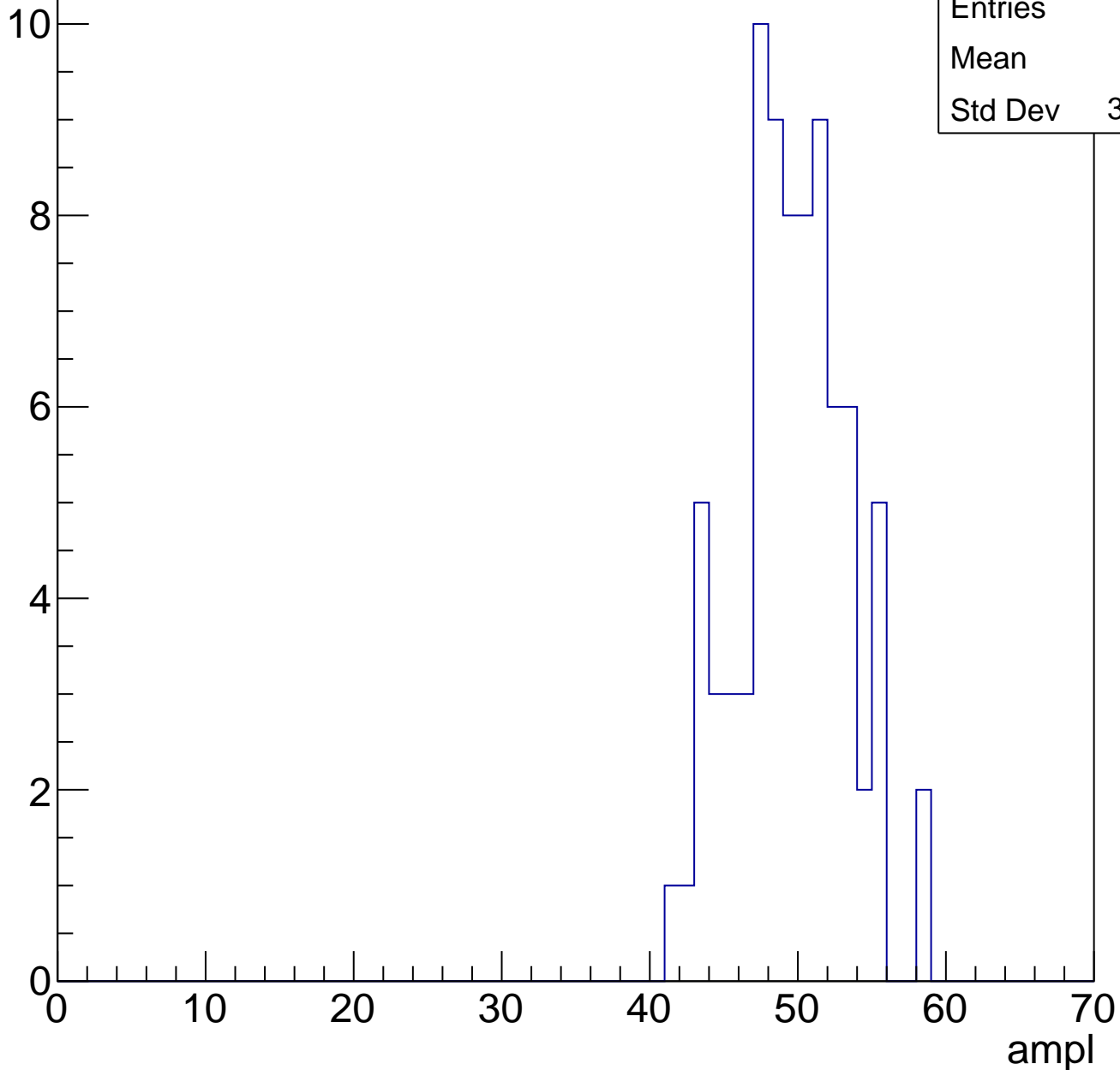


B1L102S, U9-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	49.2
Std Dev	3.643

Entry

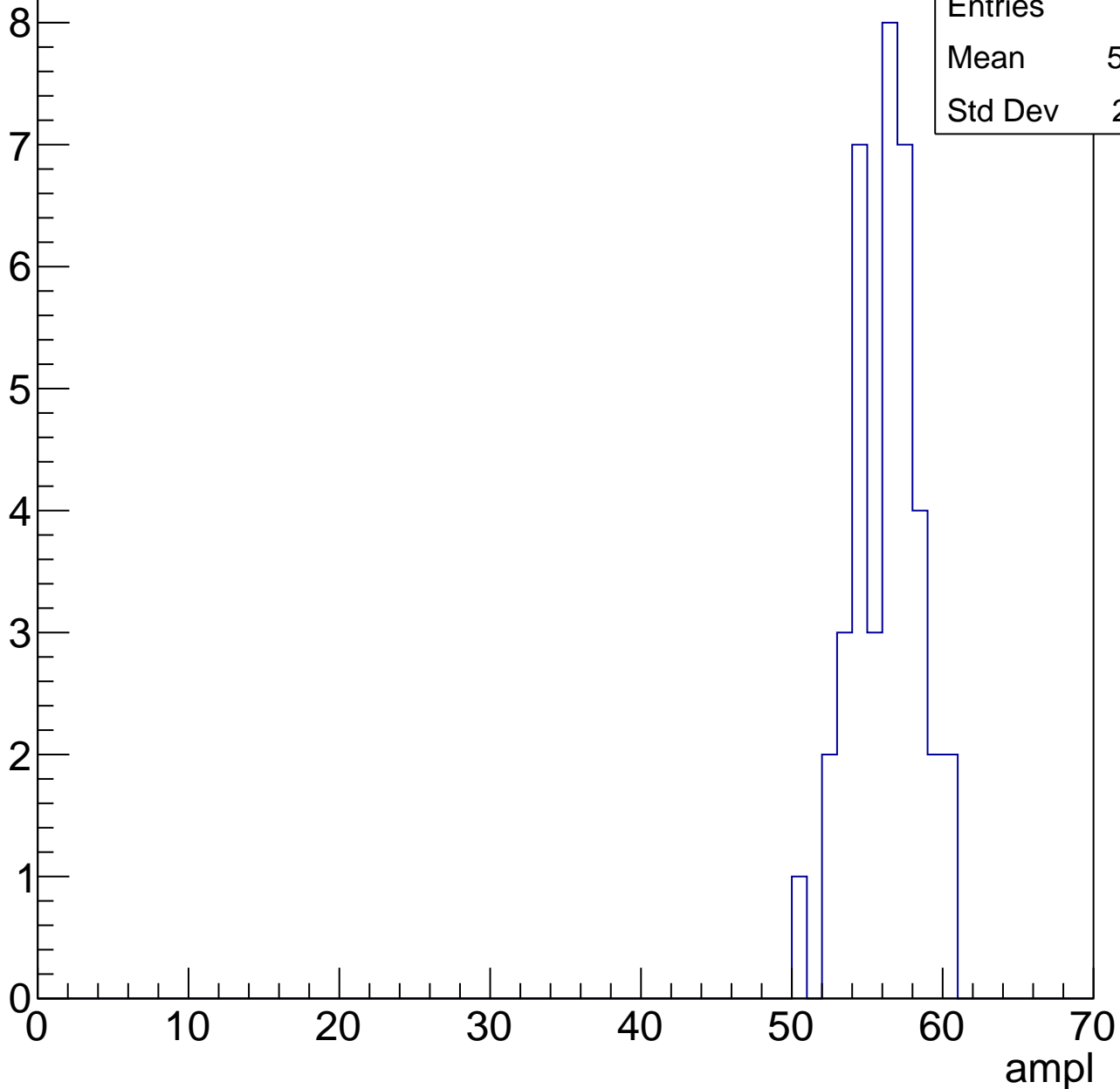


B1L102S, U9-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	55.72
Std Dev	2.241

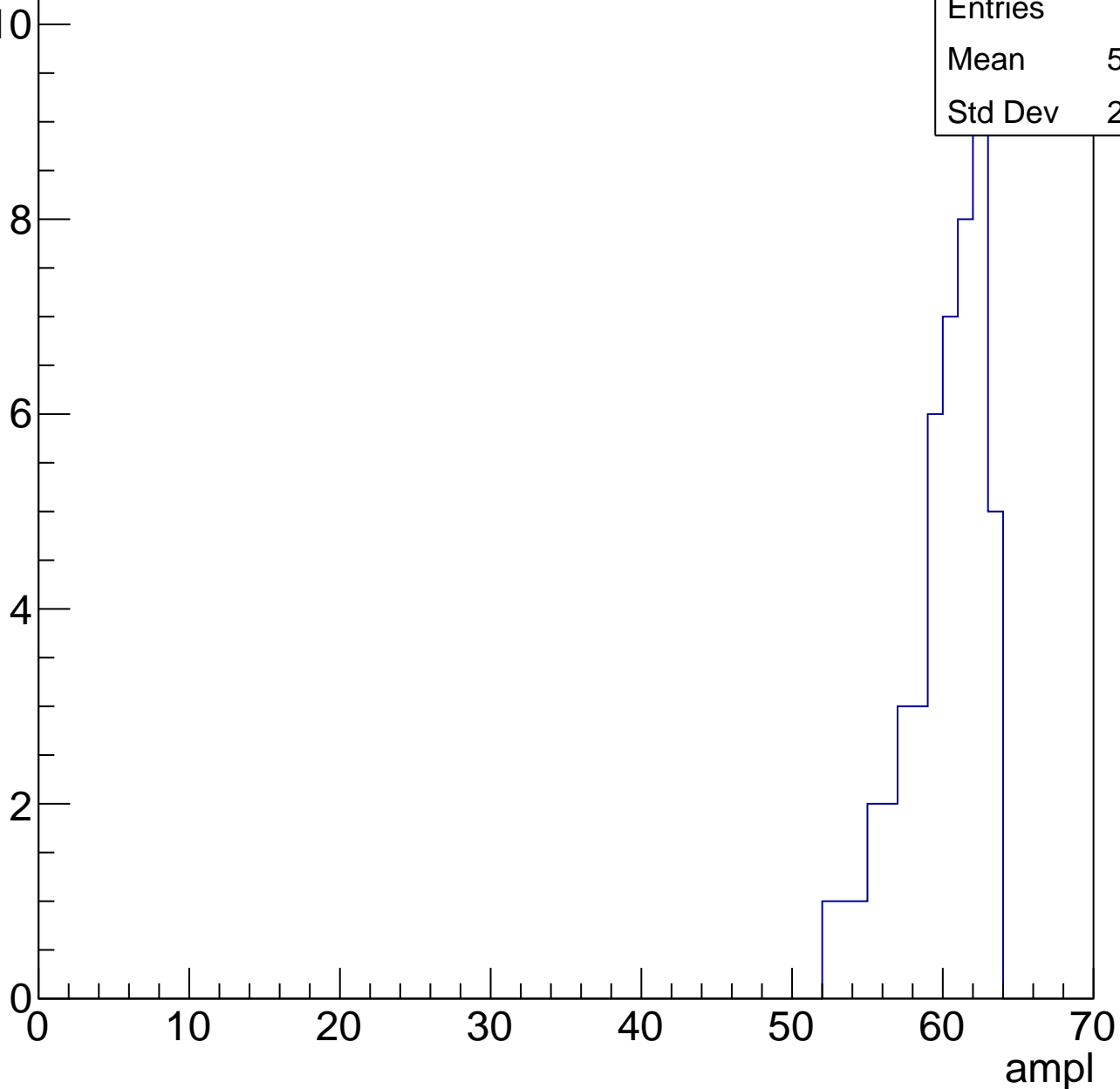


B1L102S, U9-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59.65
Std Dev	2.722

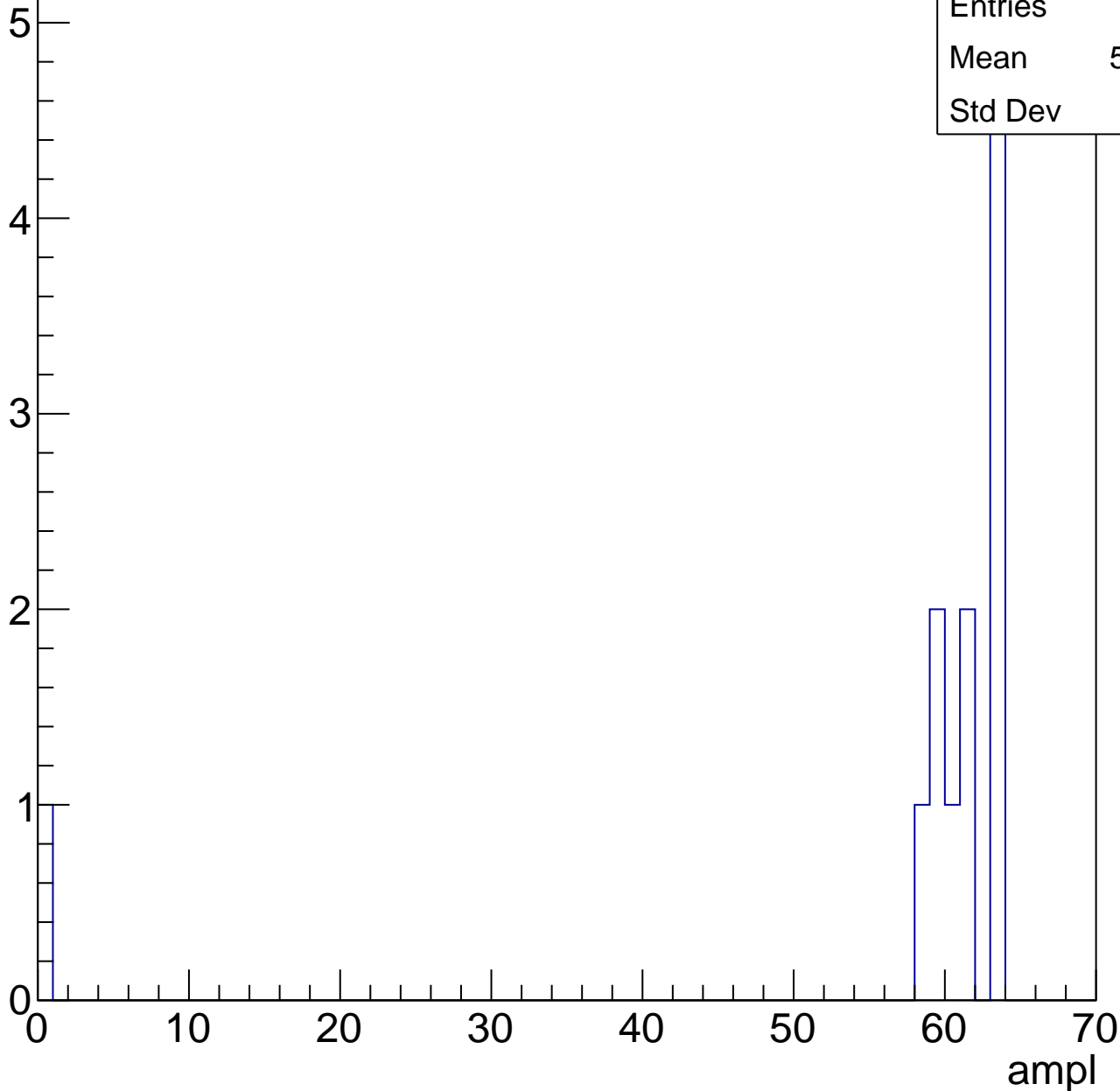


B1L102S, U9-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	56.08
Std Dev	17



B1L102S, U9-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch37, adc0

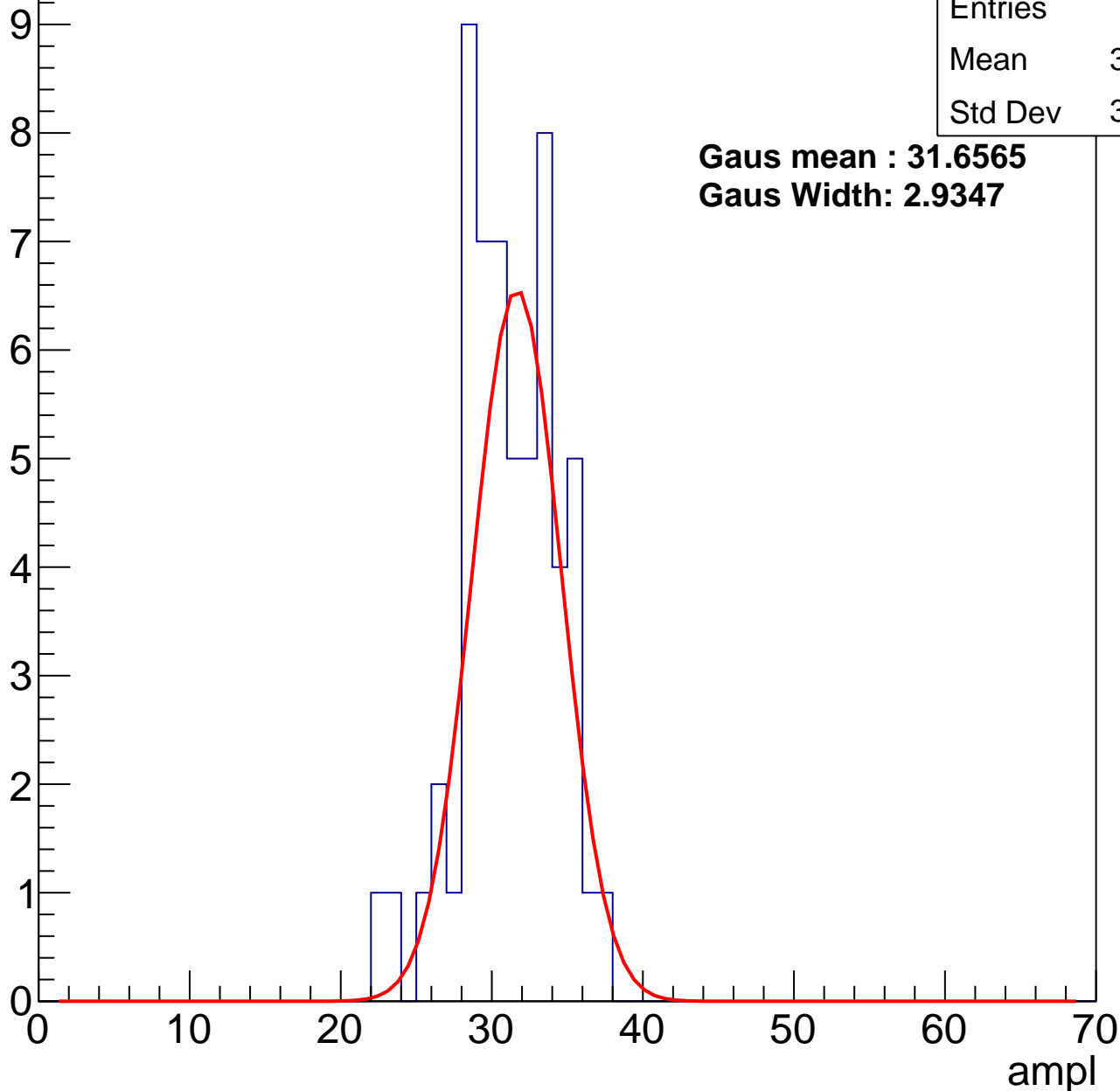
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	30.64
Std Dev	3.133

Gaus mean : 31.6565

Gaus Width: 2.9347



B1L102S, U9-ch37, adc1

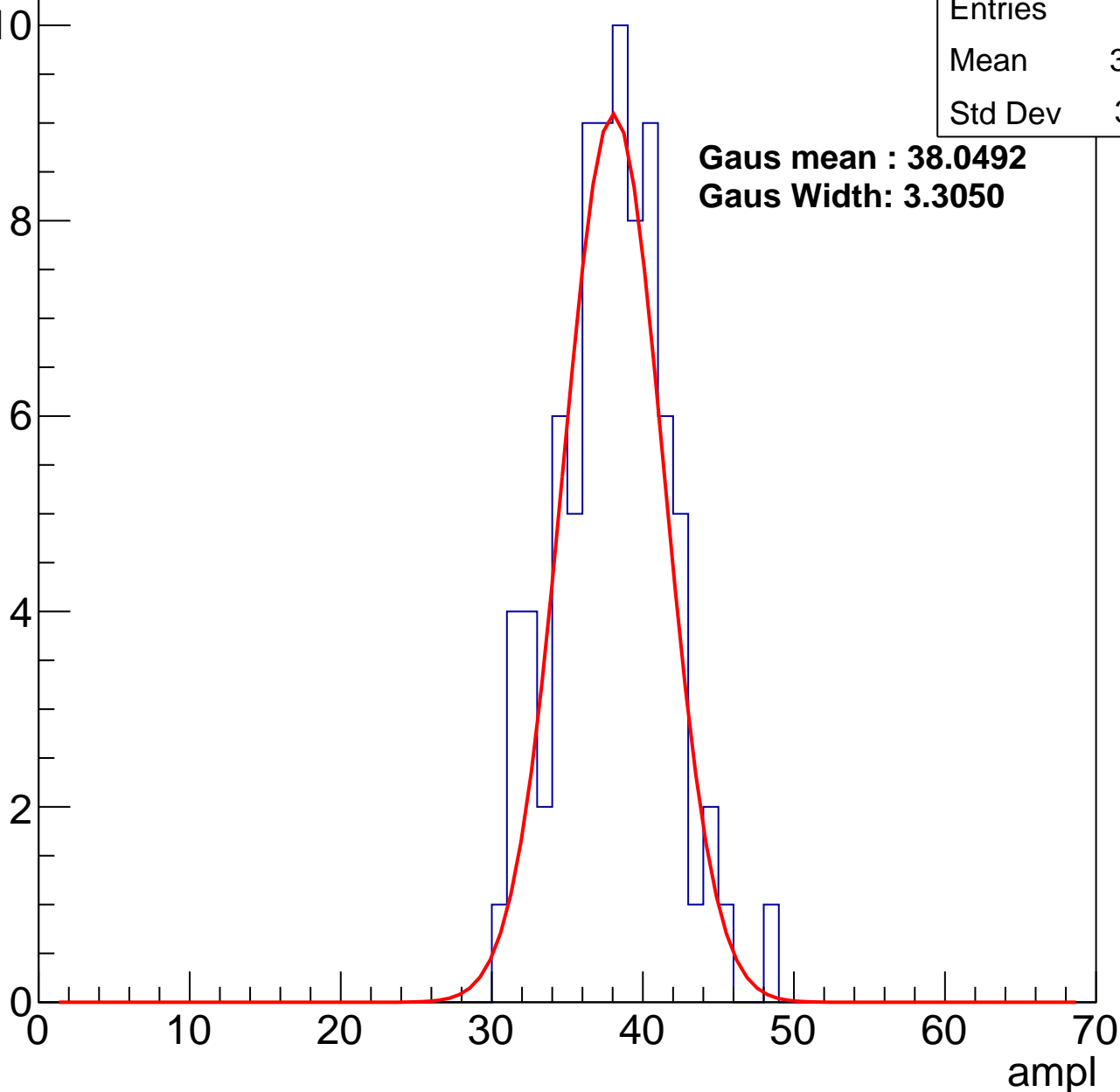
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	37.54
Std Dev	3.551

Gaus mean : 38.0492

Gaus Width: 3.3050



B1L102S, U9-ch37, adc2

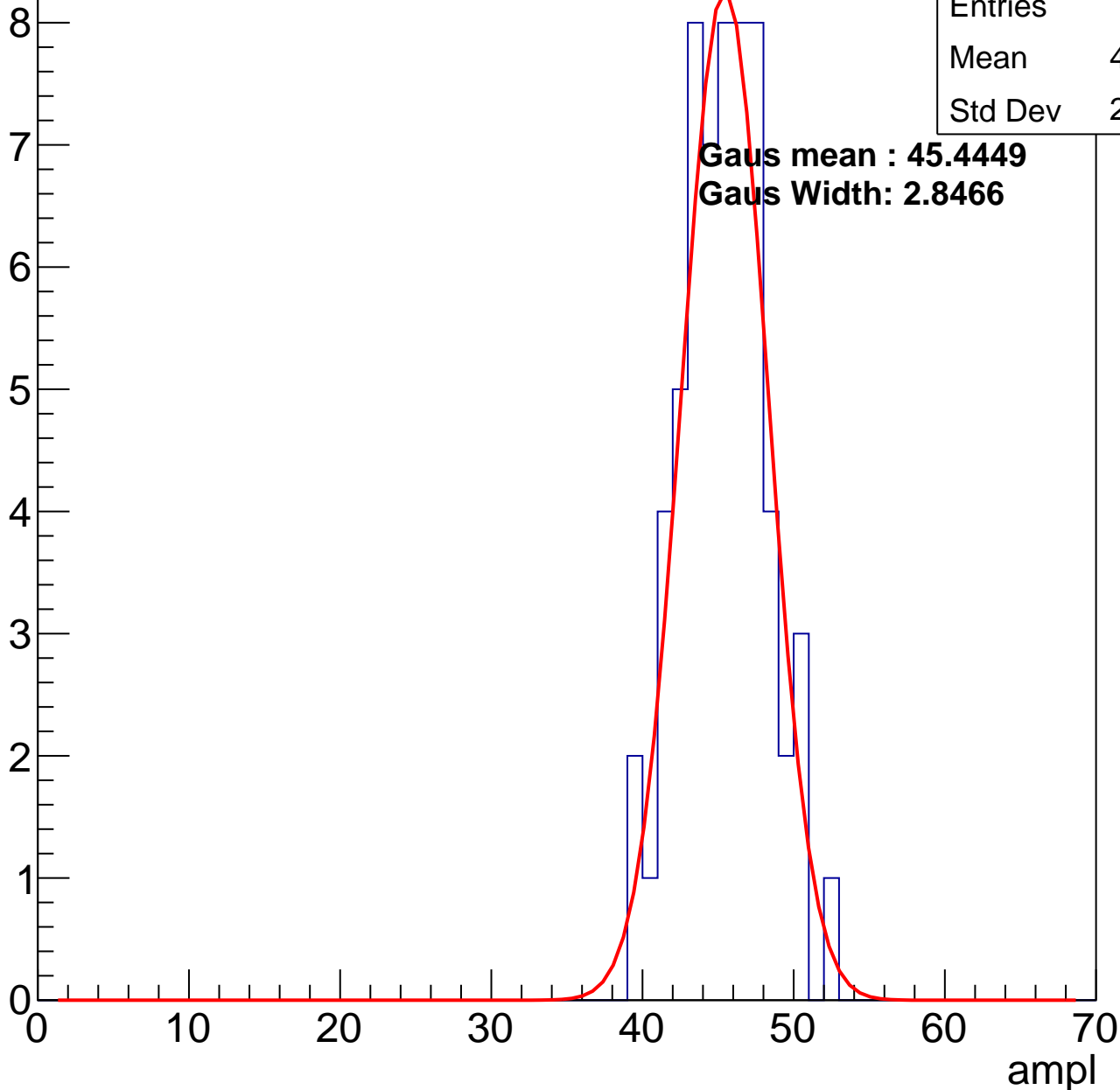
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	44.92
Std Dev	2.795

Gaus mean : 45.4449

Gaus Width: 2.8466

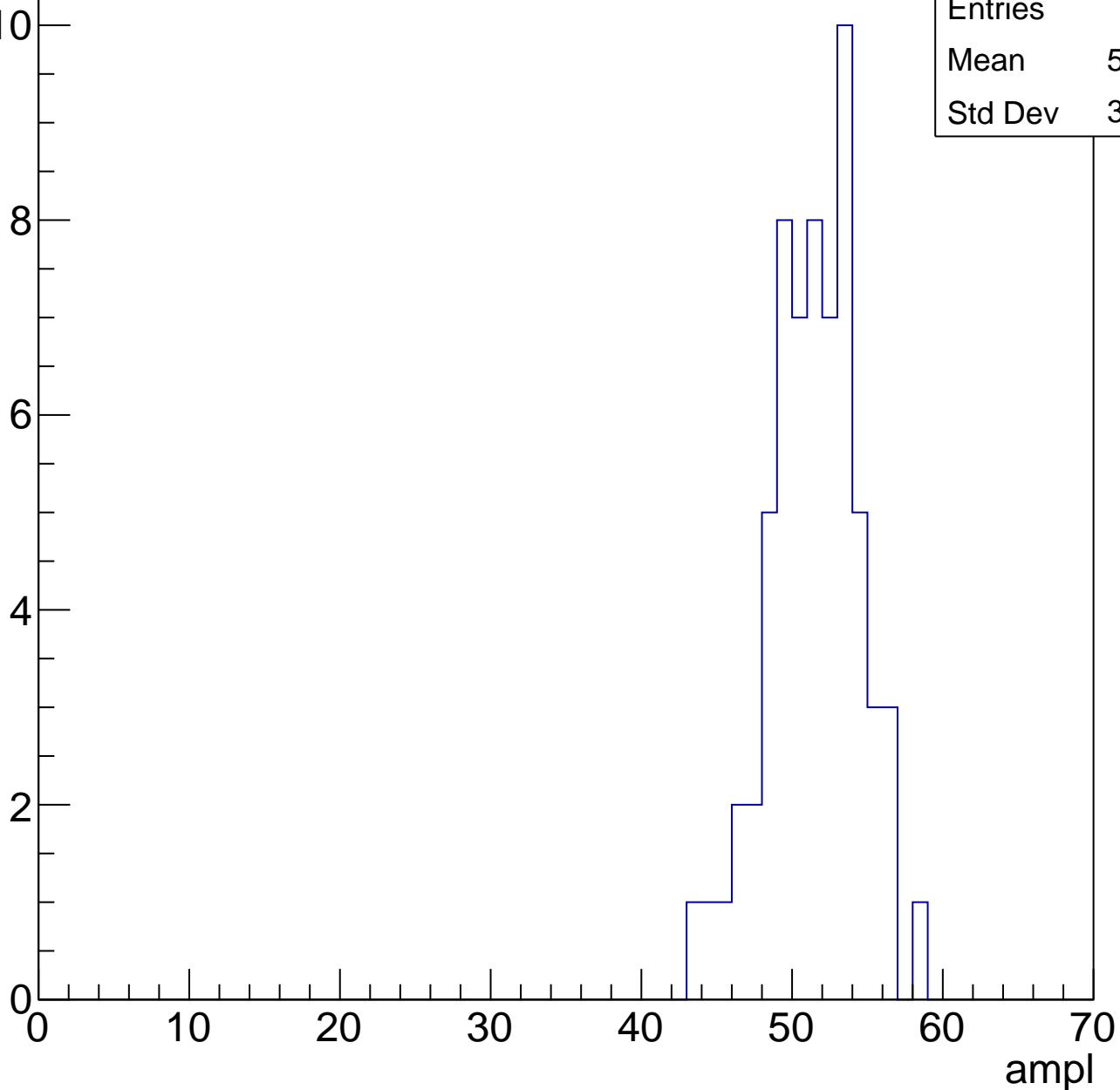


B1L102S, U9-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

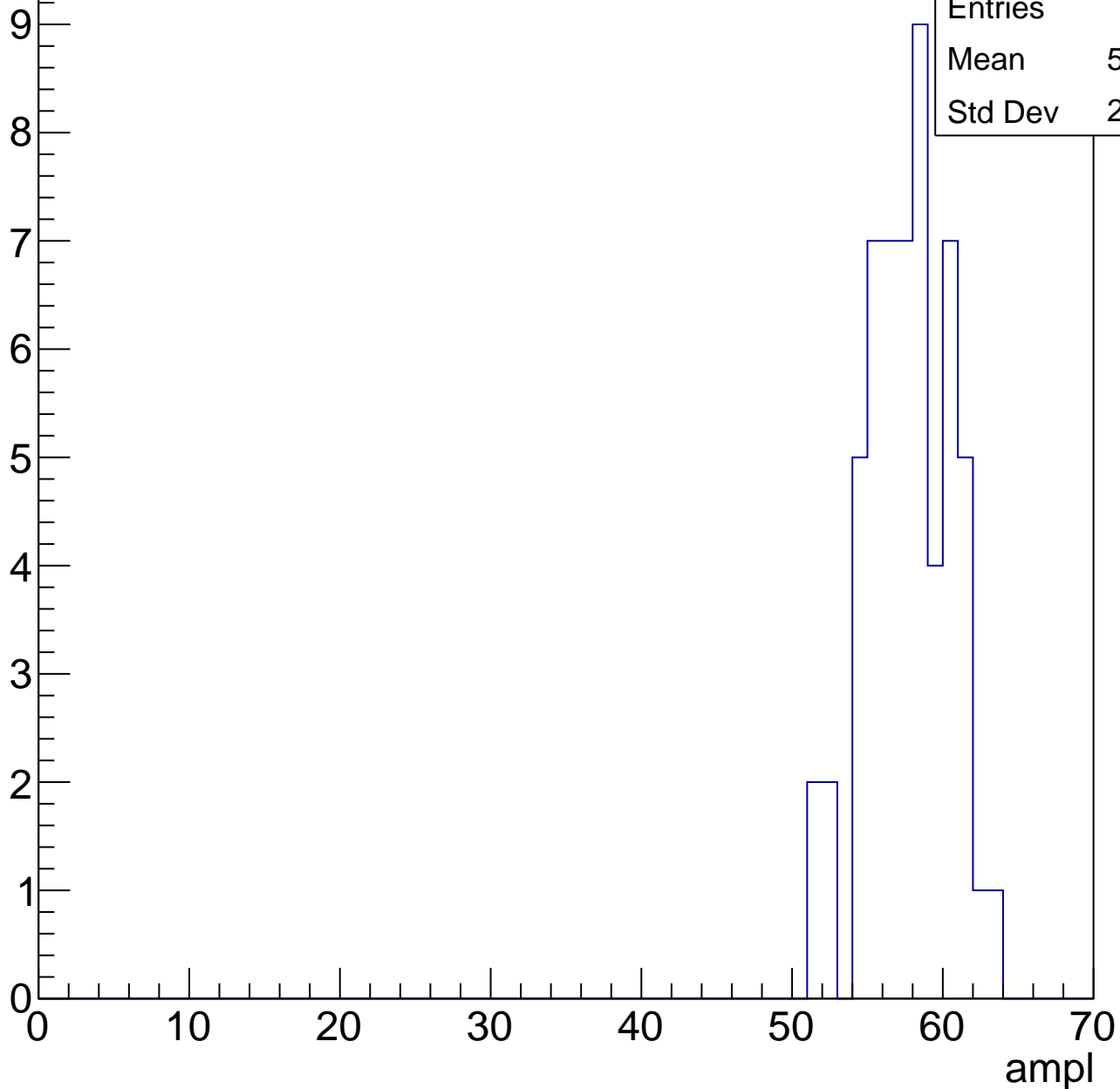
Entries	64
Mean	50.98
Std Dev	3.008



B1L102S, U9-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

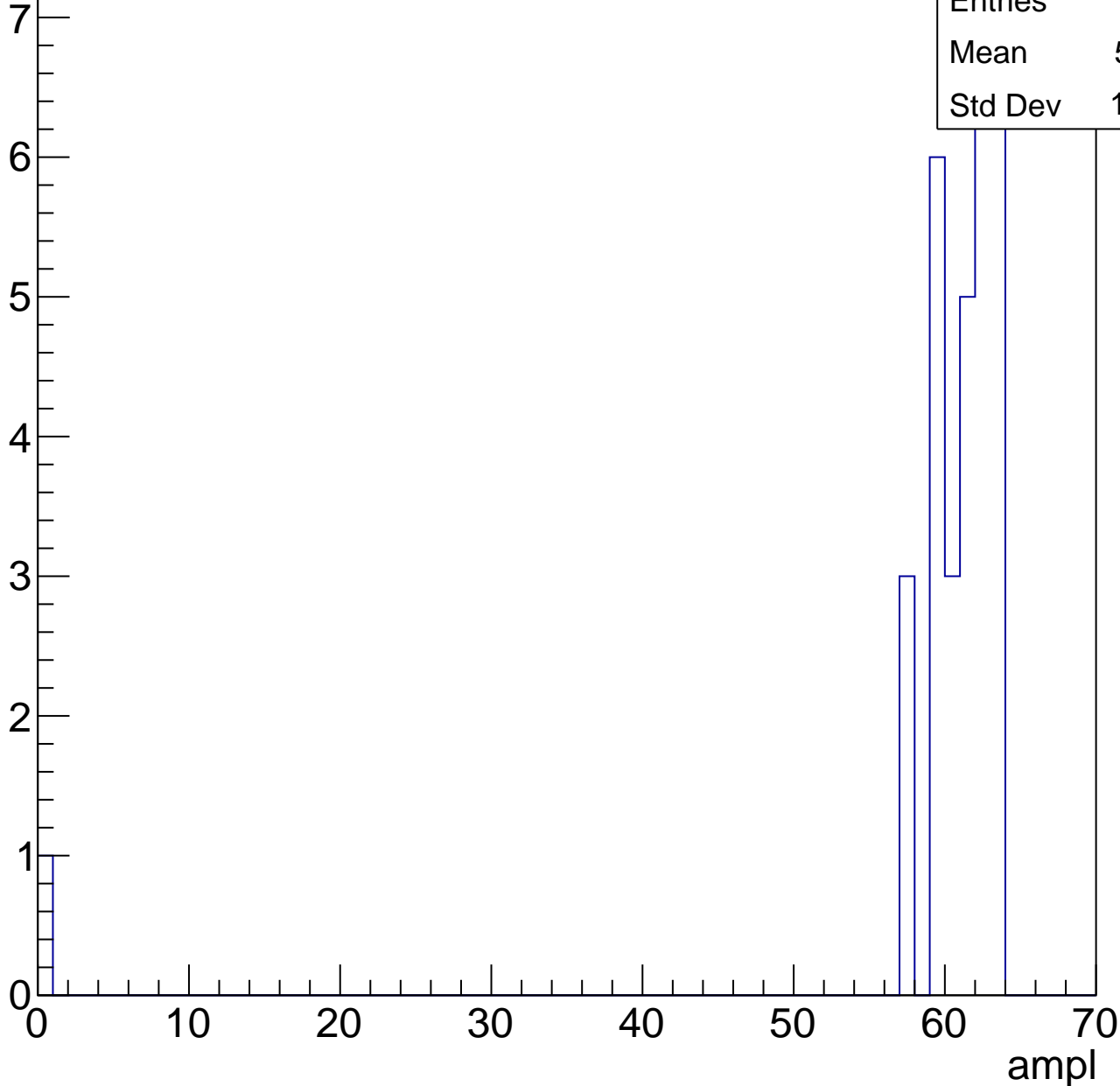


B1L102S, U9-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	58.91
Std Dev	10.74



B1L102S, U9-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch38, adc0

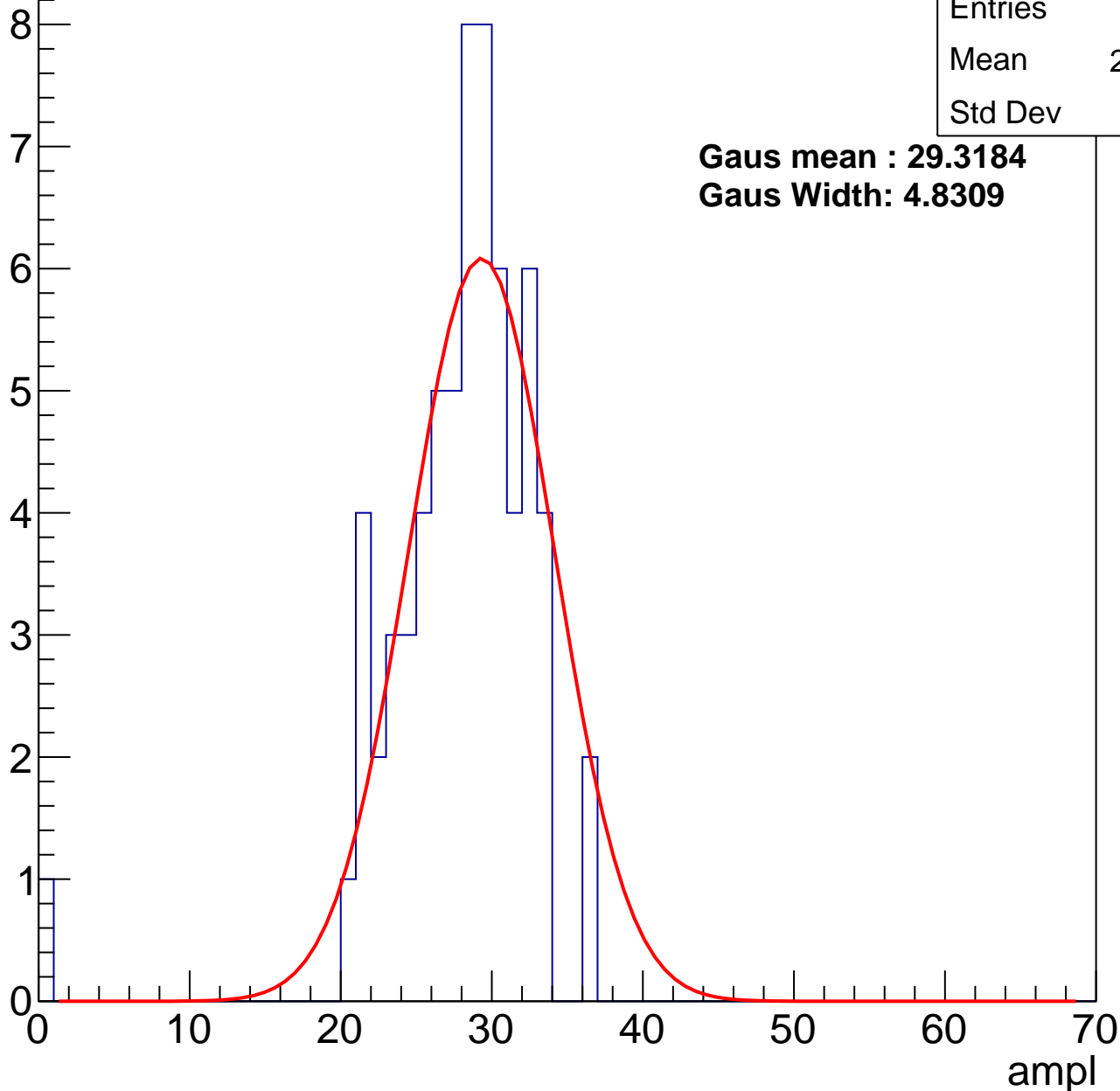
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.42
Std Dev	5.03

Gaus mean : 29.3184

Gaus Width: 4.8309



B1L102S, U9-ch38, adc1

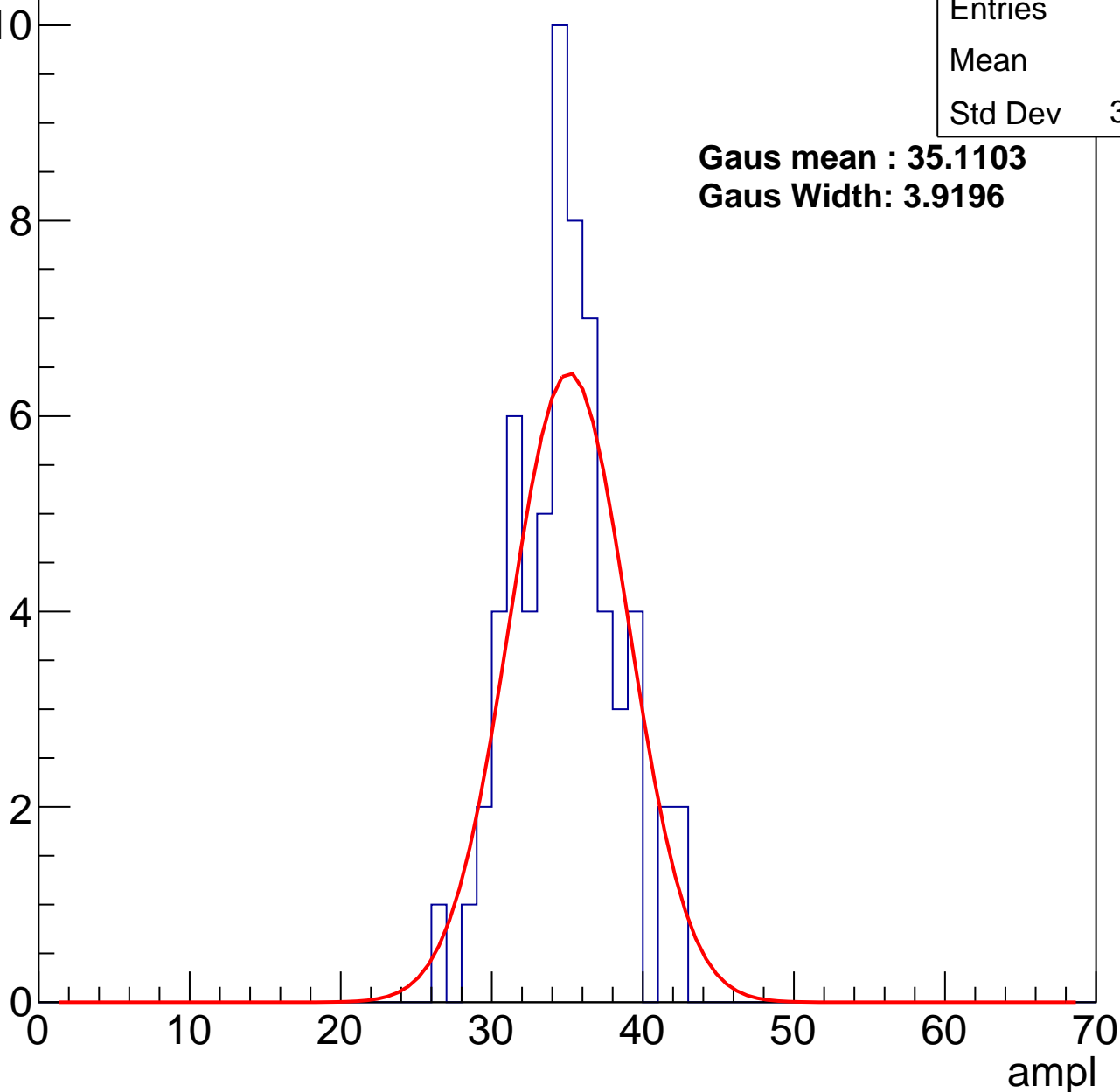
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.4
Std Dev	3.393

Gaus mean : 35.1103

Gaus Width: 3.9196



B1L102S, U9-ch38, adc2

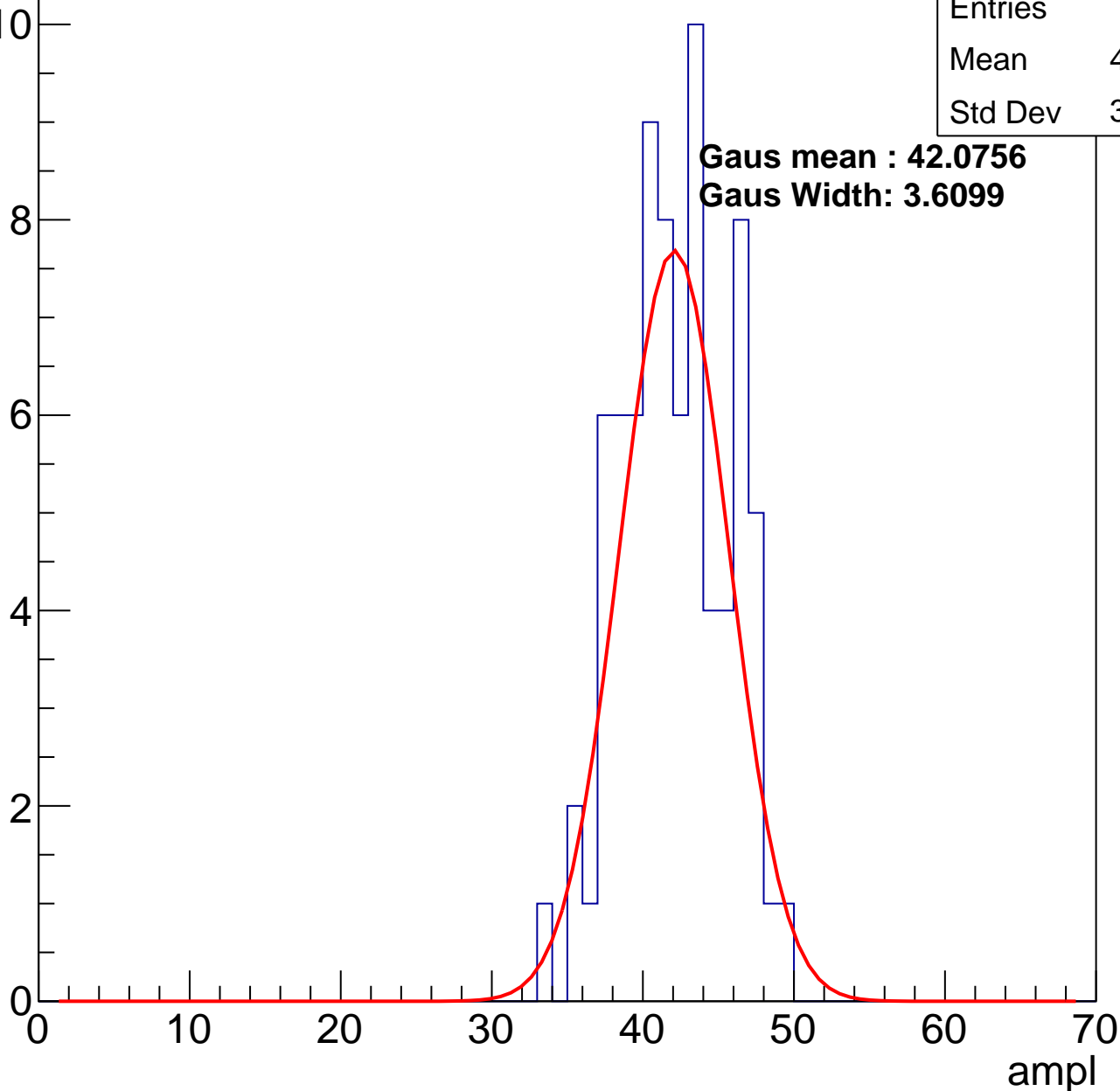
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	41.65
Std Dev	3.489

Gaus mean : 42.0756

Gaus Width: 3.6099

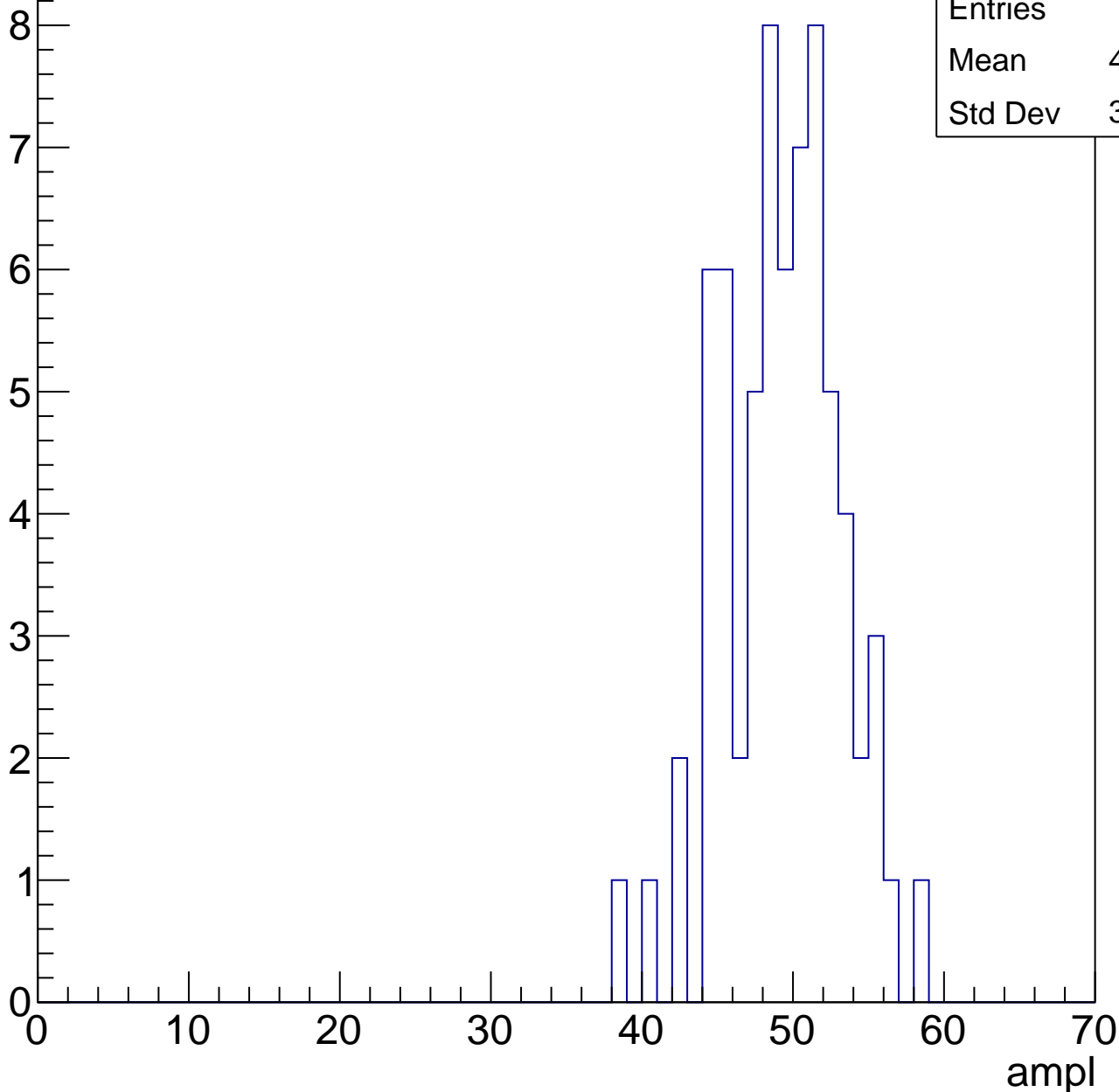


B1L102S, U9-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.79
Std Dev	3.883

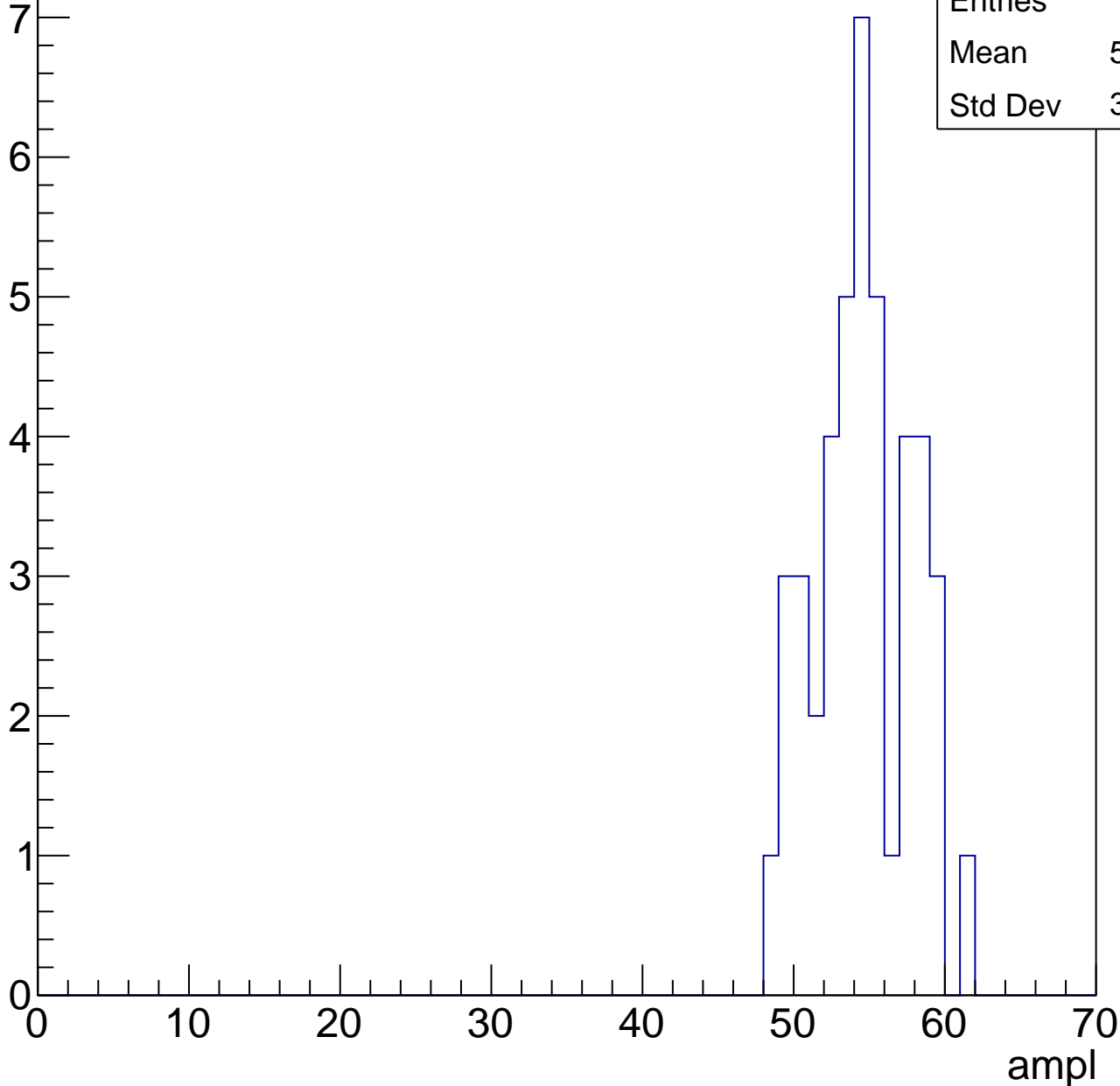


B1L102S, U9-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	54.12
Std Dev	3.164



B1L102S, U9-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

Entries

62

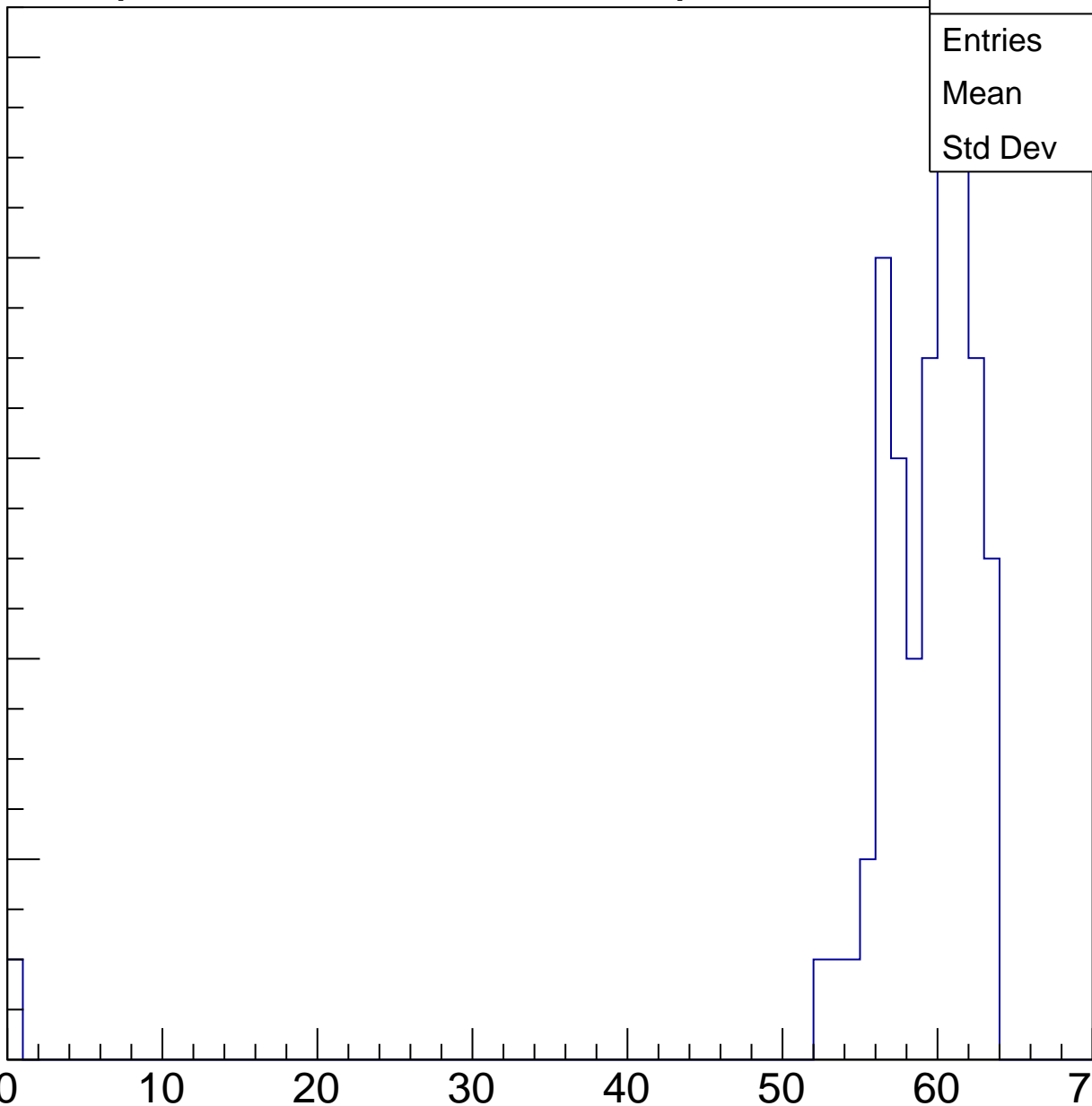
Mean

58.1

Std Dev

7.892

ampl

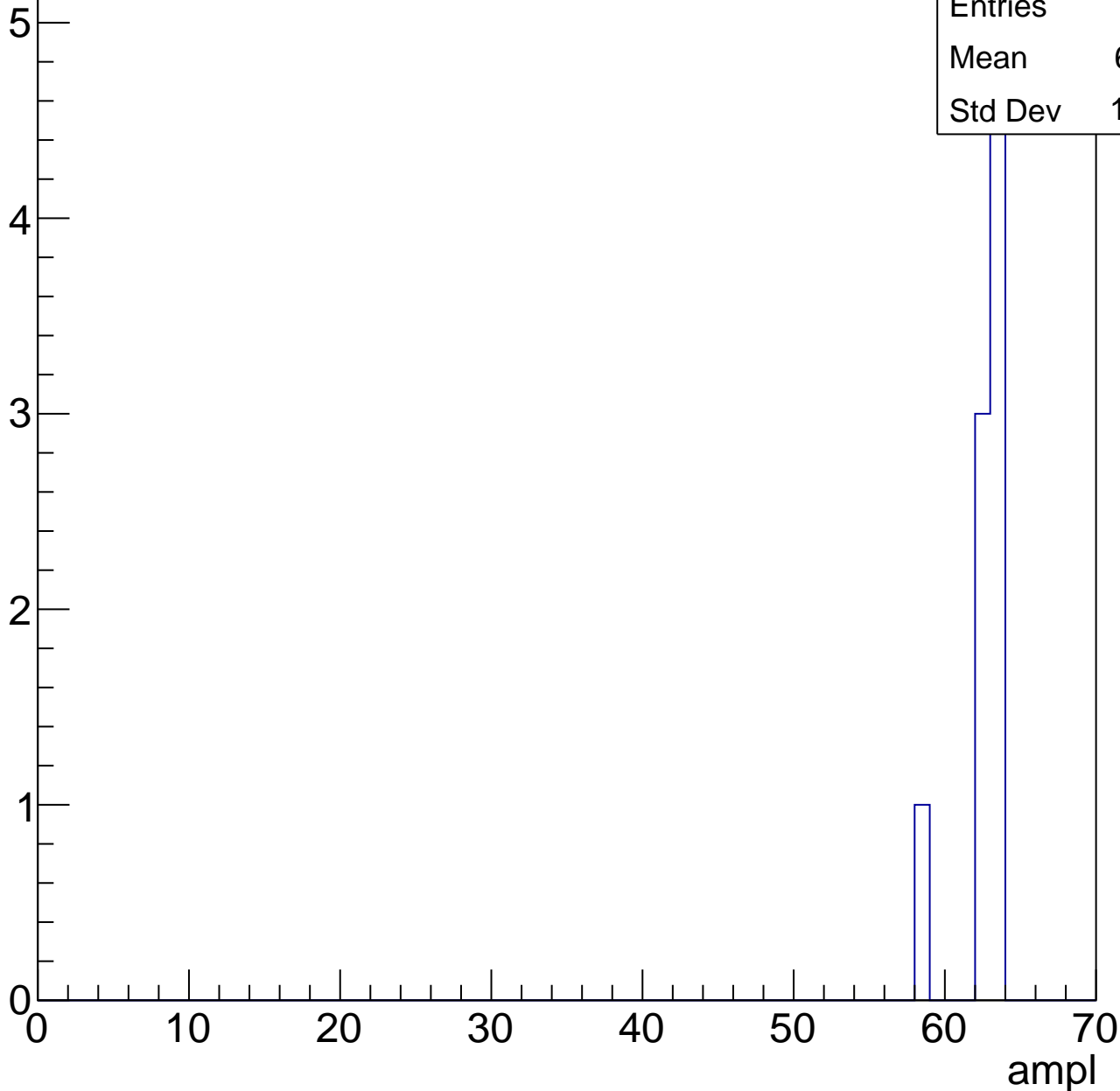


B1L102S, U9-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.11
Std Dev	1.523



B1L102S, U9-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	9.5
Std Dev	9.5

ampl

B1L102S, U9-ch39, adc0

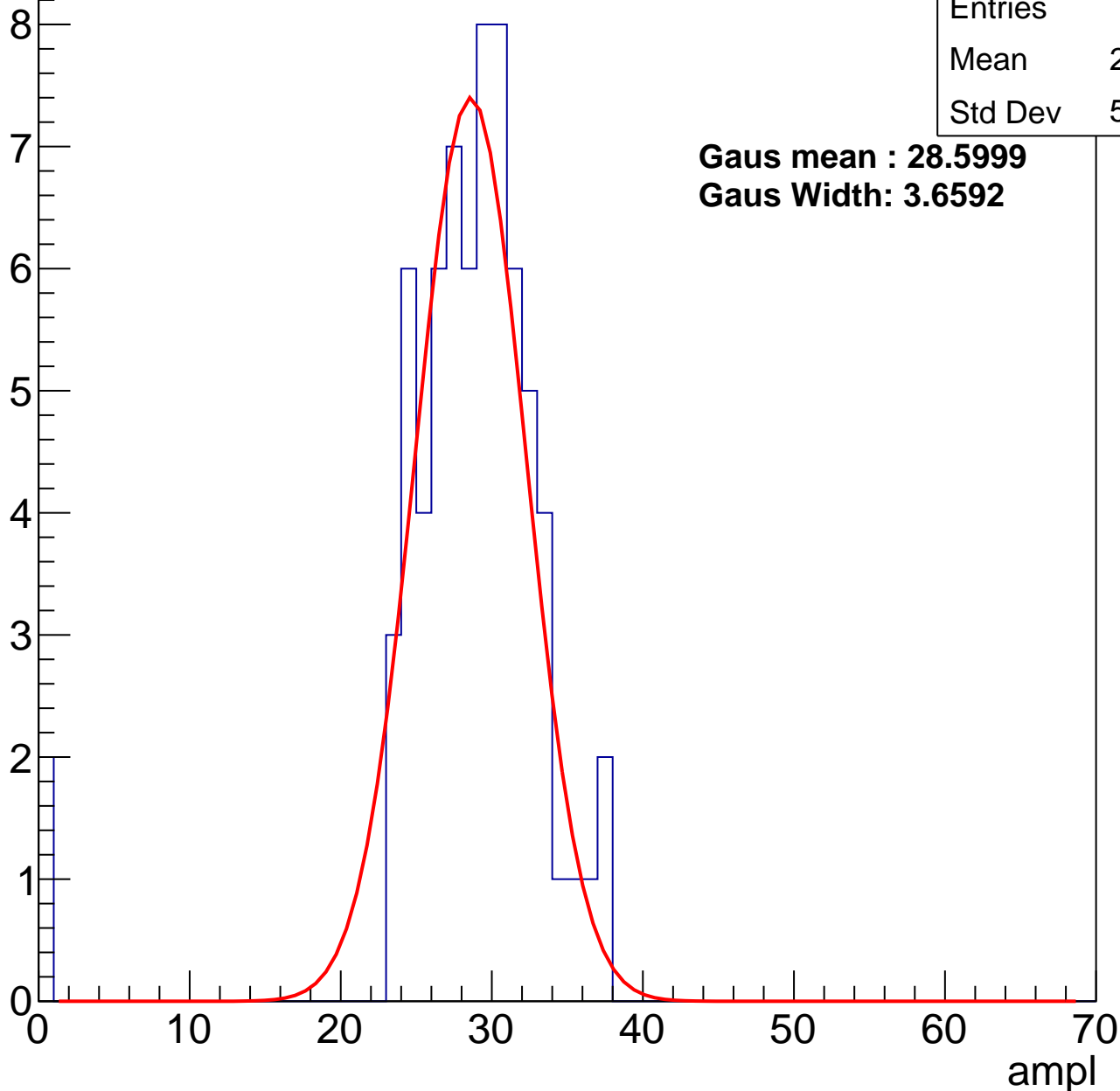
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.93
Std Dev	5.844

Gaus mean : 28.5999

Gaus Width: 3.6592



B1L102S, U9-ch39, adc1

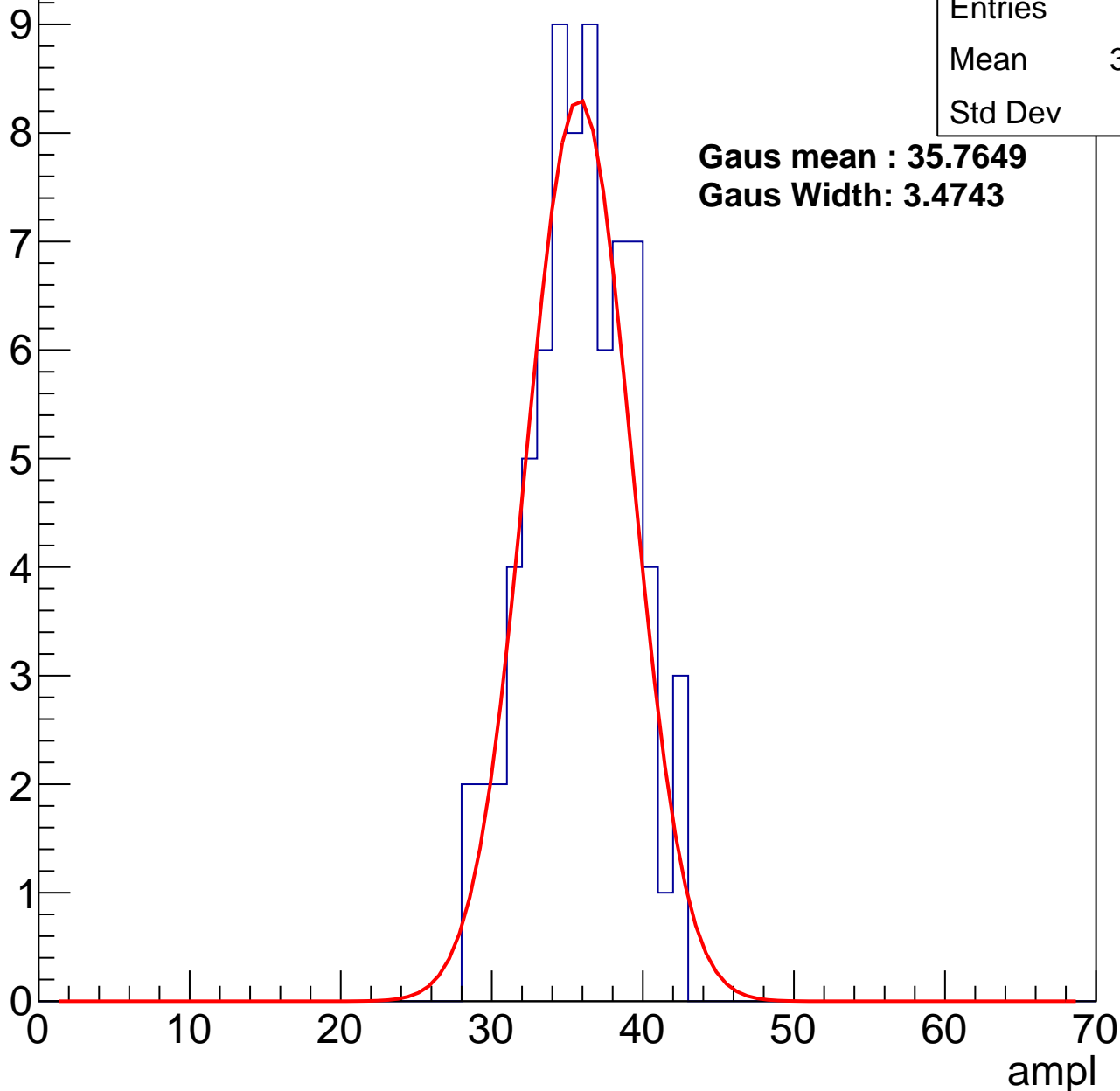
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.39
Std Dev	3.35

Gaus mean : 35.7649

Gaus Width: 3.4743



B1L102S, U9-ch39, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	43.27
Std Dev	3.103

Gaus mean : 44.0868

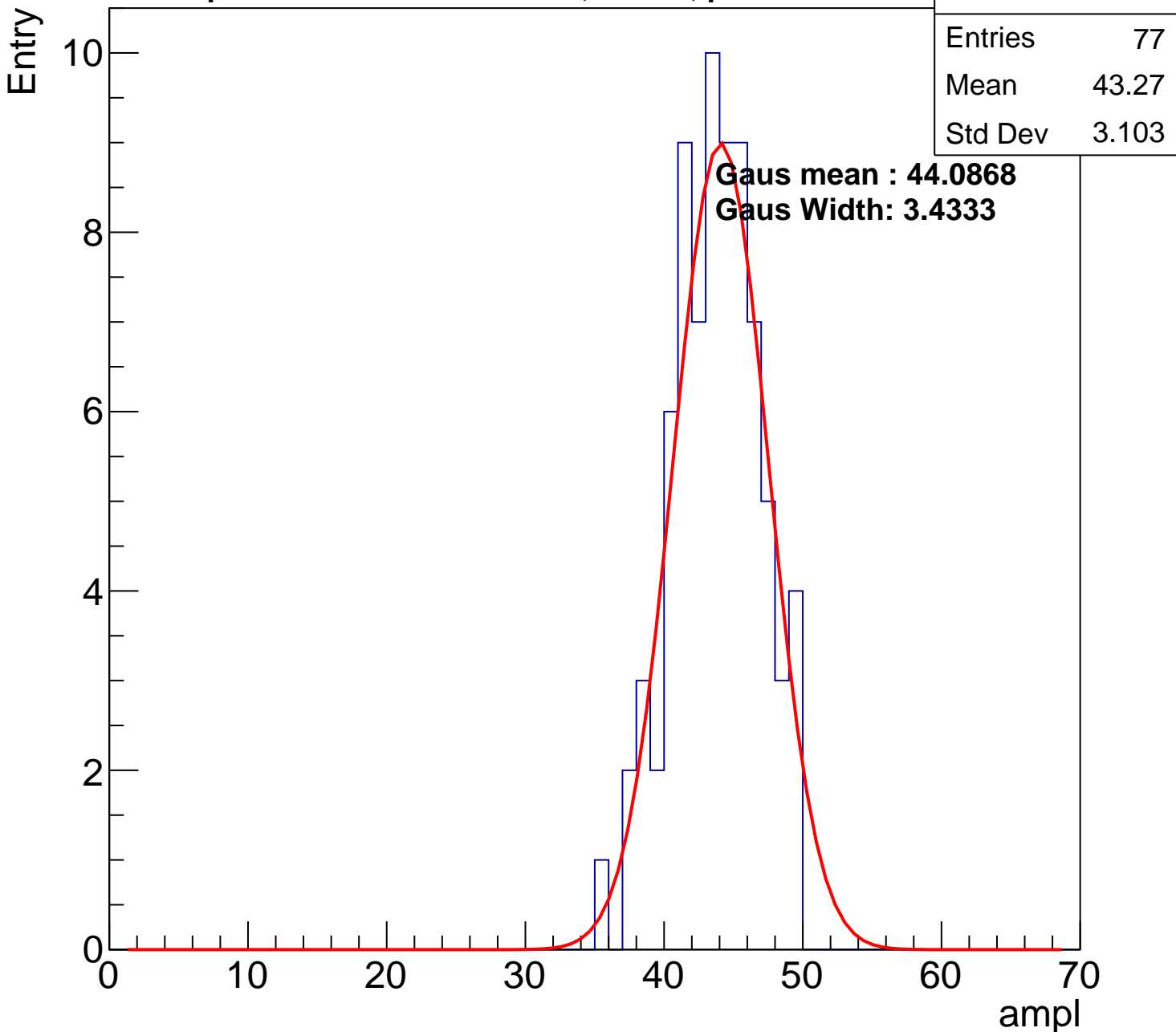
Gaus Width: 3.4333

Entry

10
8
6
4
2
0

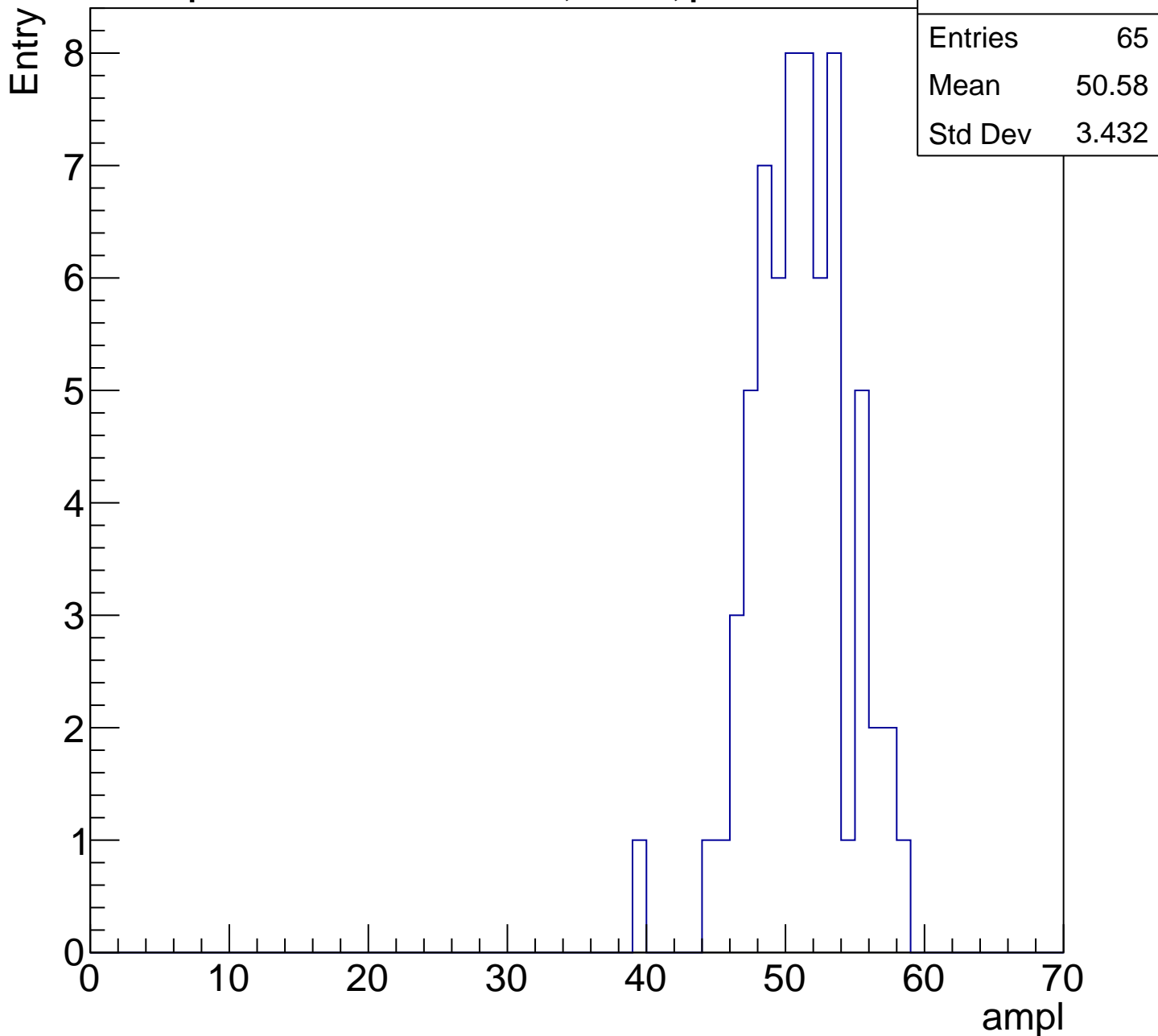
ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

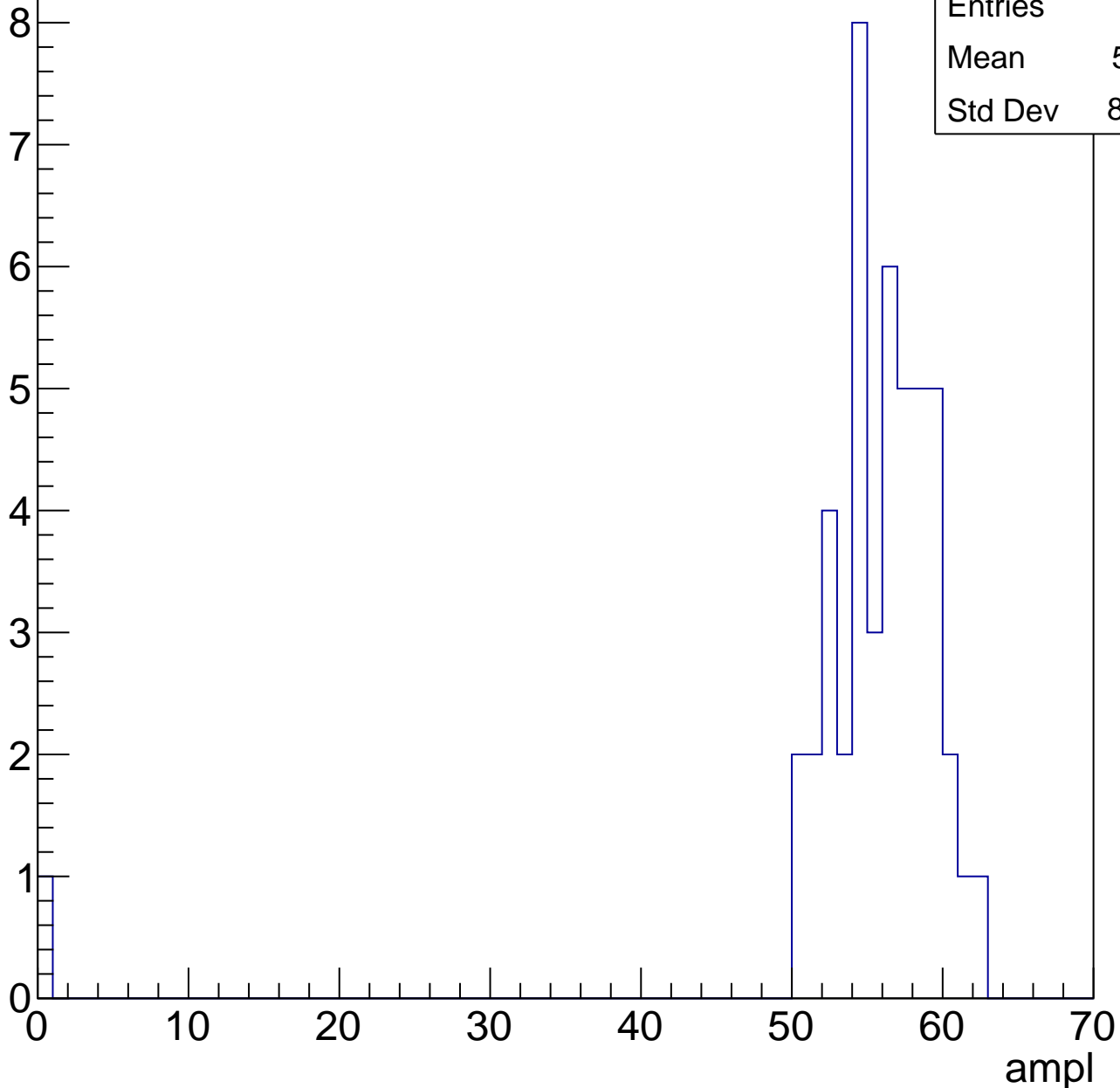


B1L102S, U9-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.51
Std Dev	8.547



B1L102S, U9-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

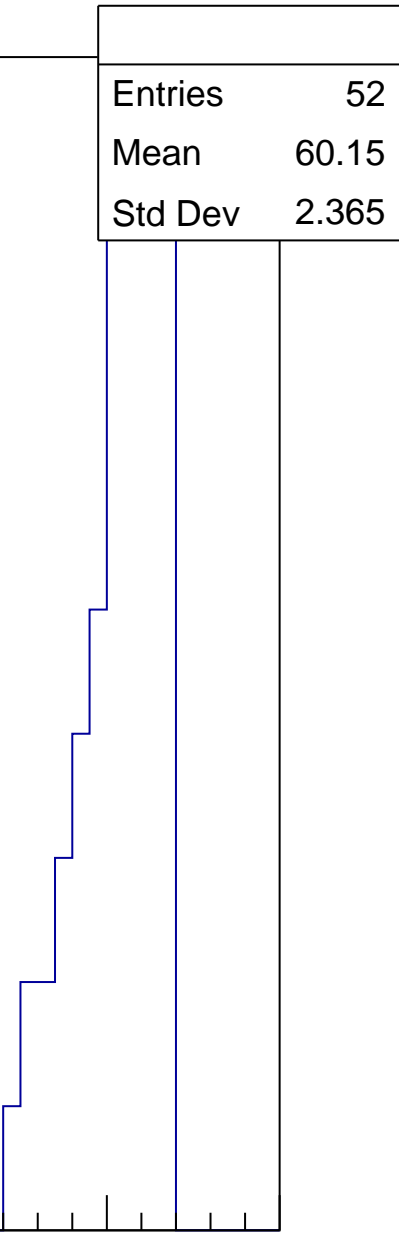
Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	60.15
Std Dev	2.365

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

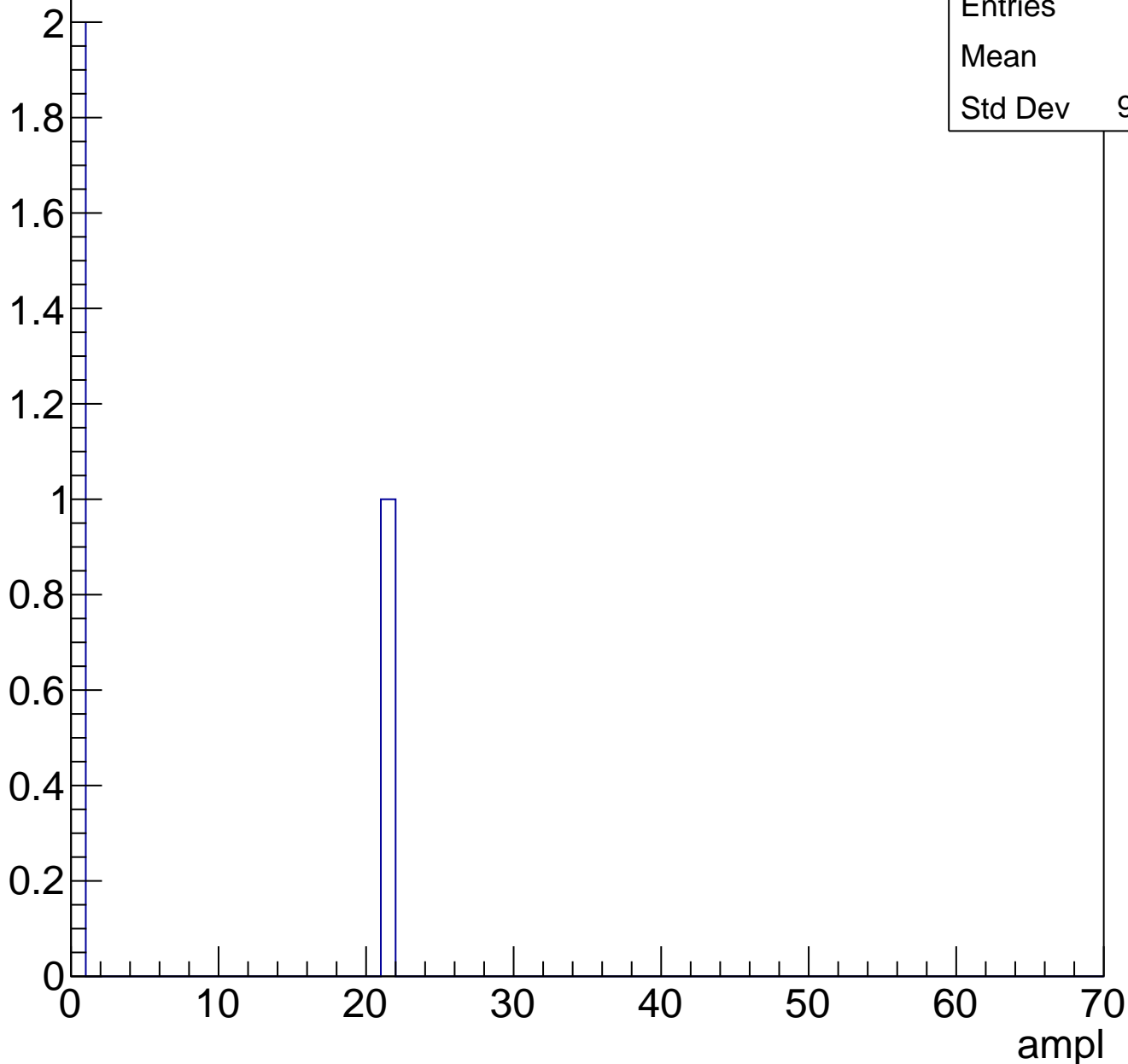
Entry



B1L102S, U9-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch40, adc0

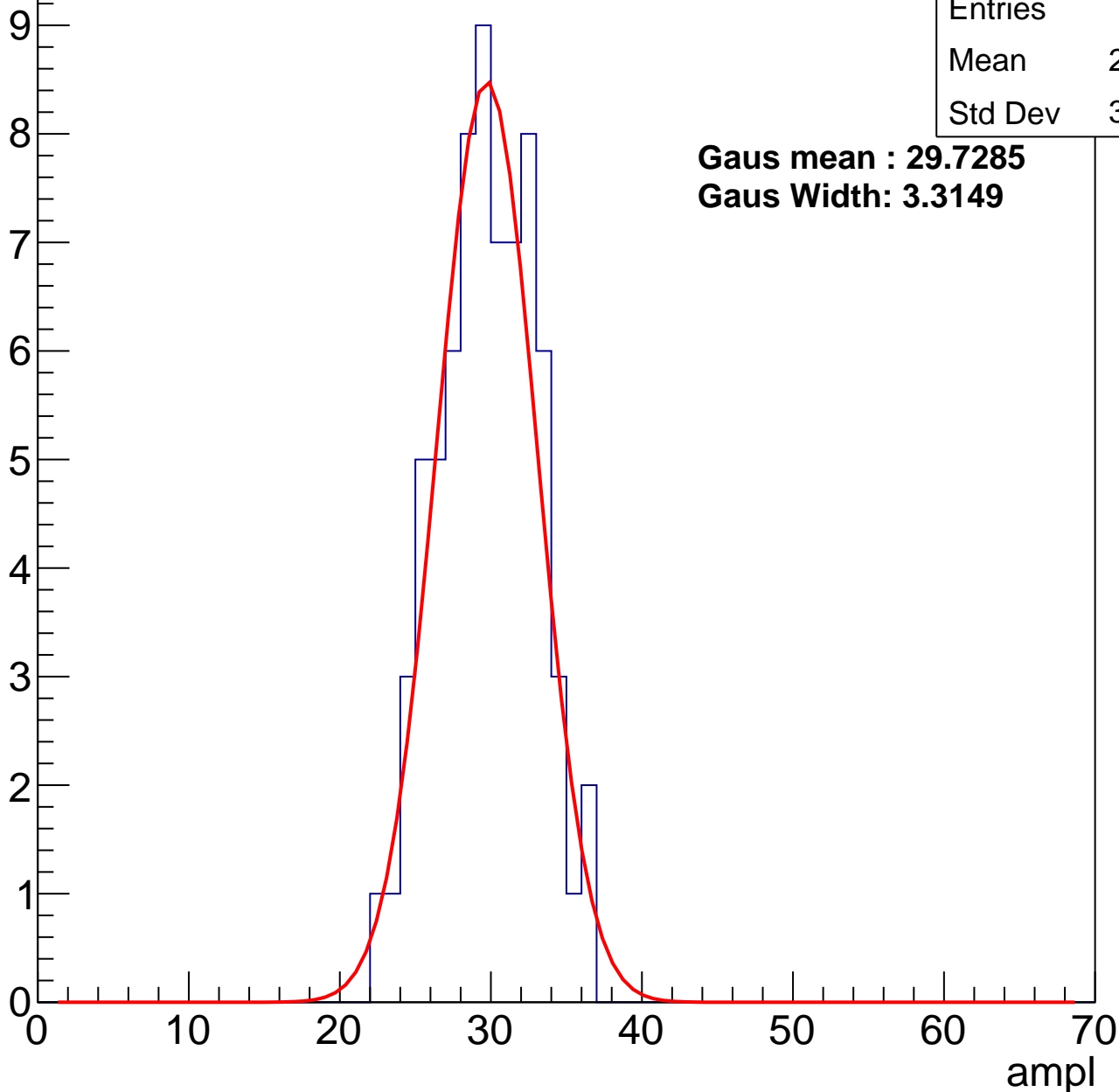
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.29
Std Dev	3.169

Gaus mean : 29.7285

Gaus Width: 3.3149



B1L102S, U9-ch40, adc1

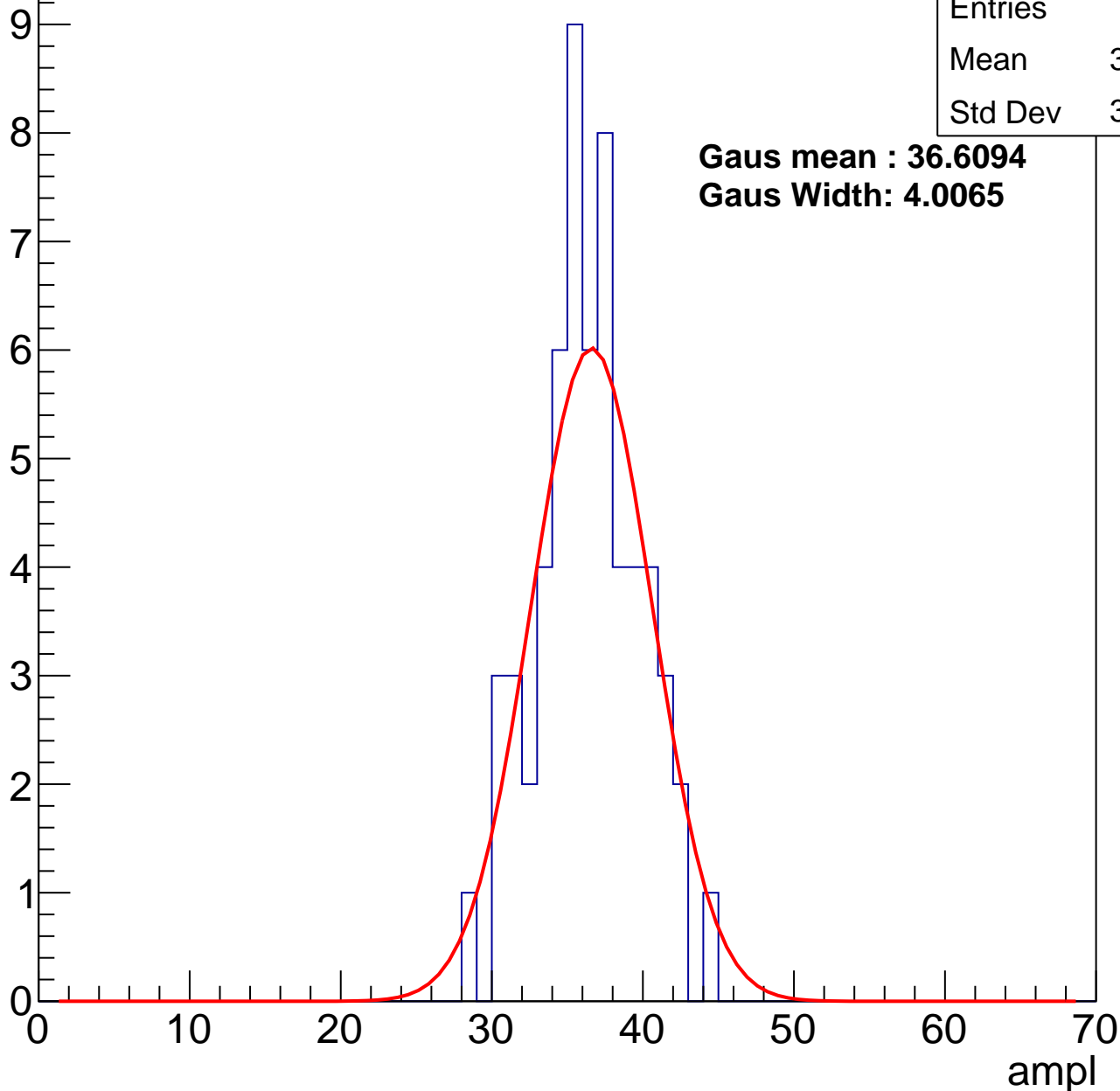
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.95
Std Dev	3.374

Gaus mean : 36.6094

Gaus Width: 4.0065



B1L102S, U9-ch40, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	41.73
Std Dev	3.006

Gaus mean : 42.1751

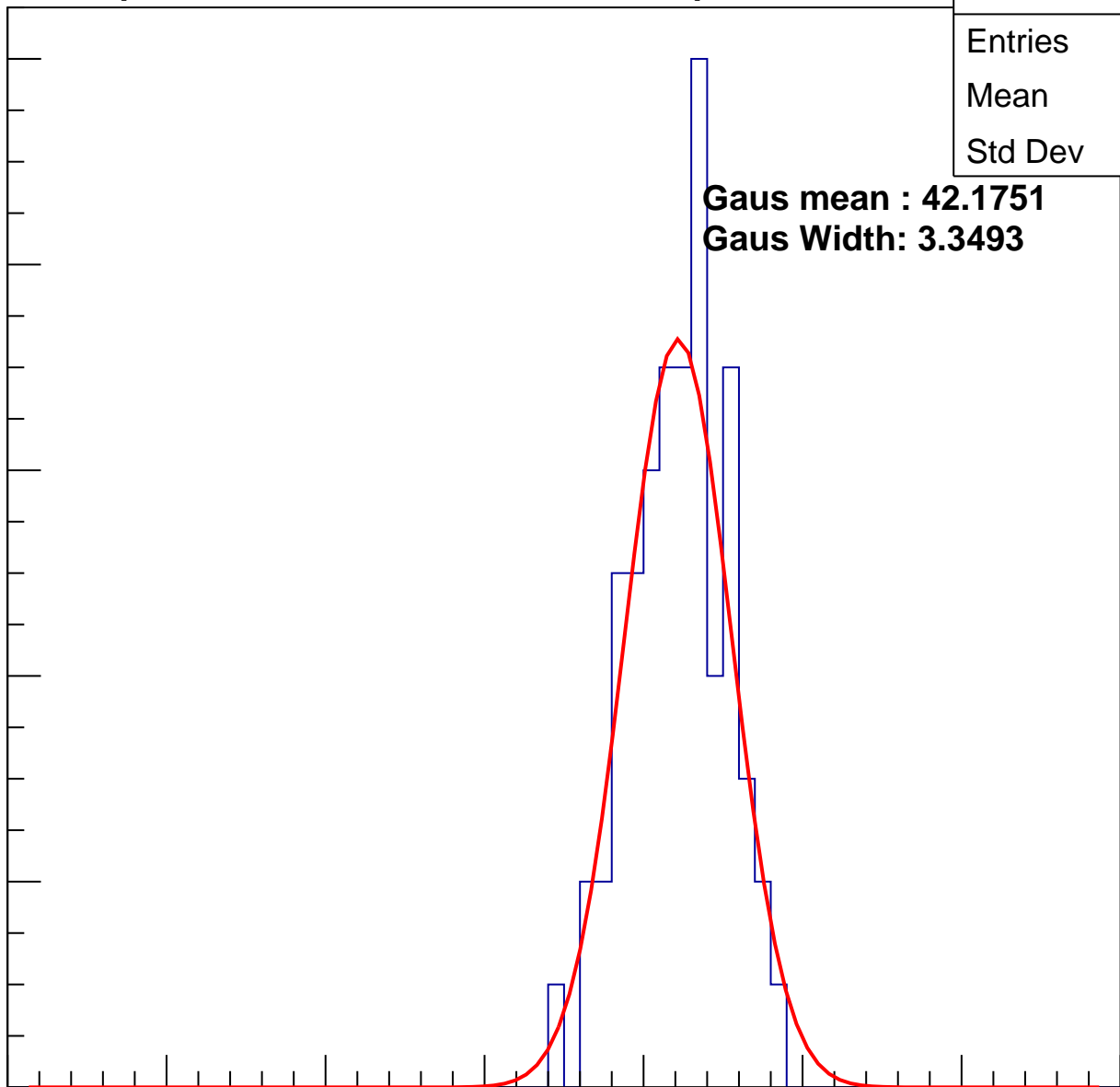
Gaus Width: 3.3493

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

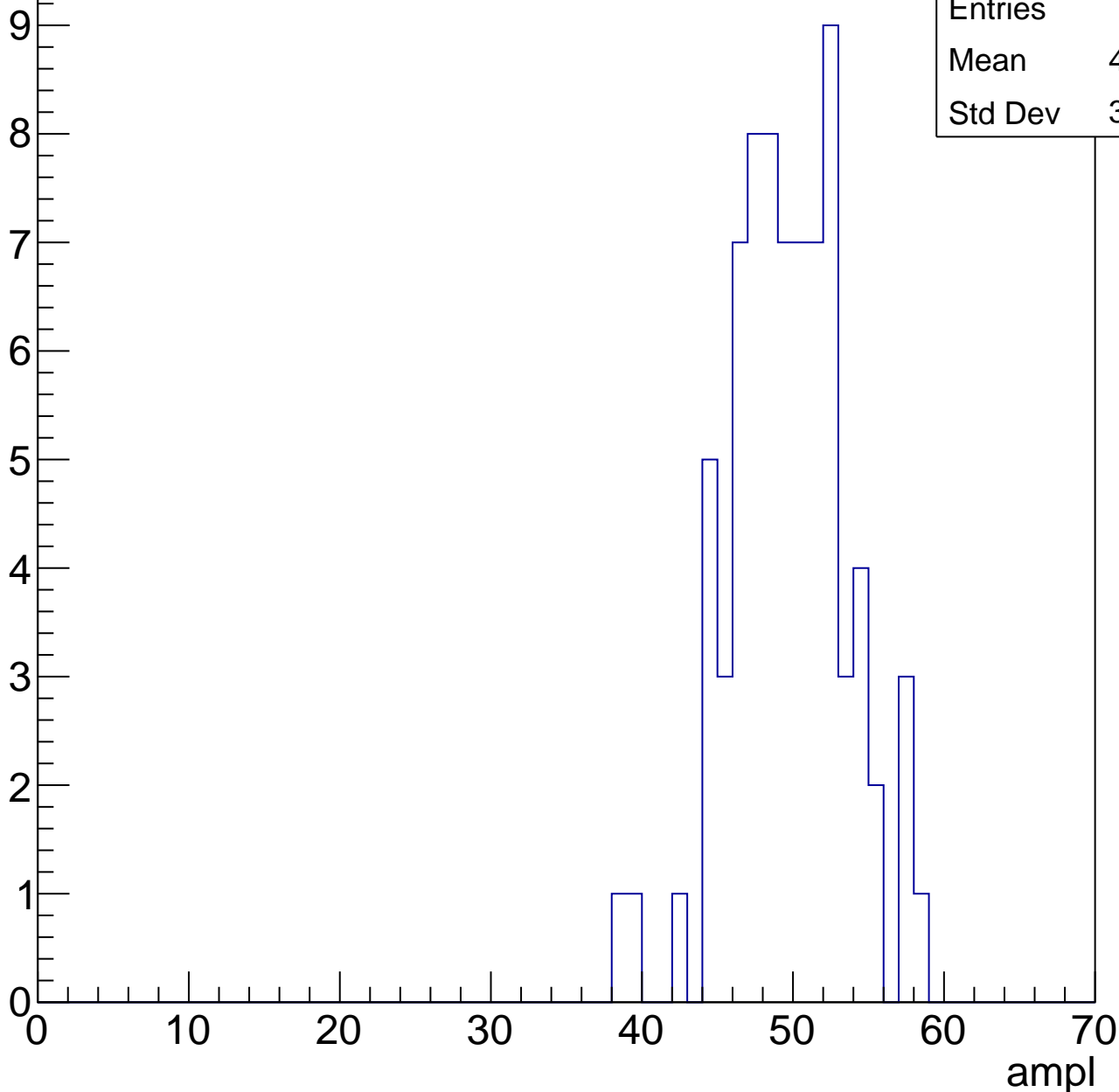


B1L102S, U9-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.19
Std Dev	3.868

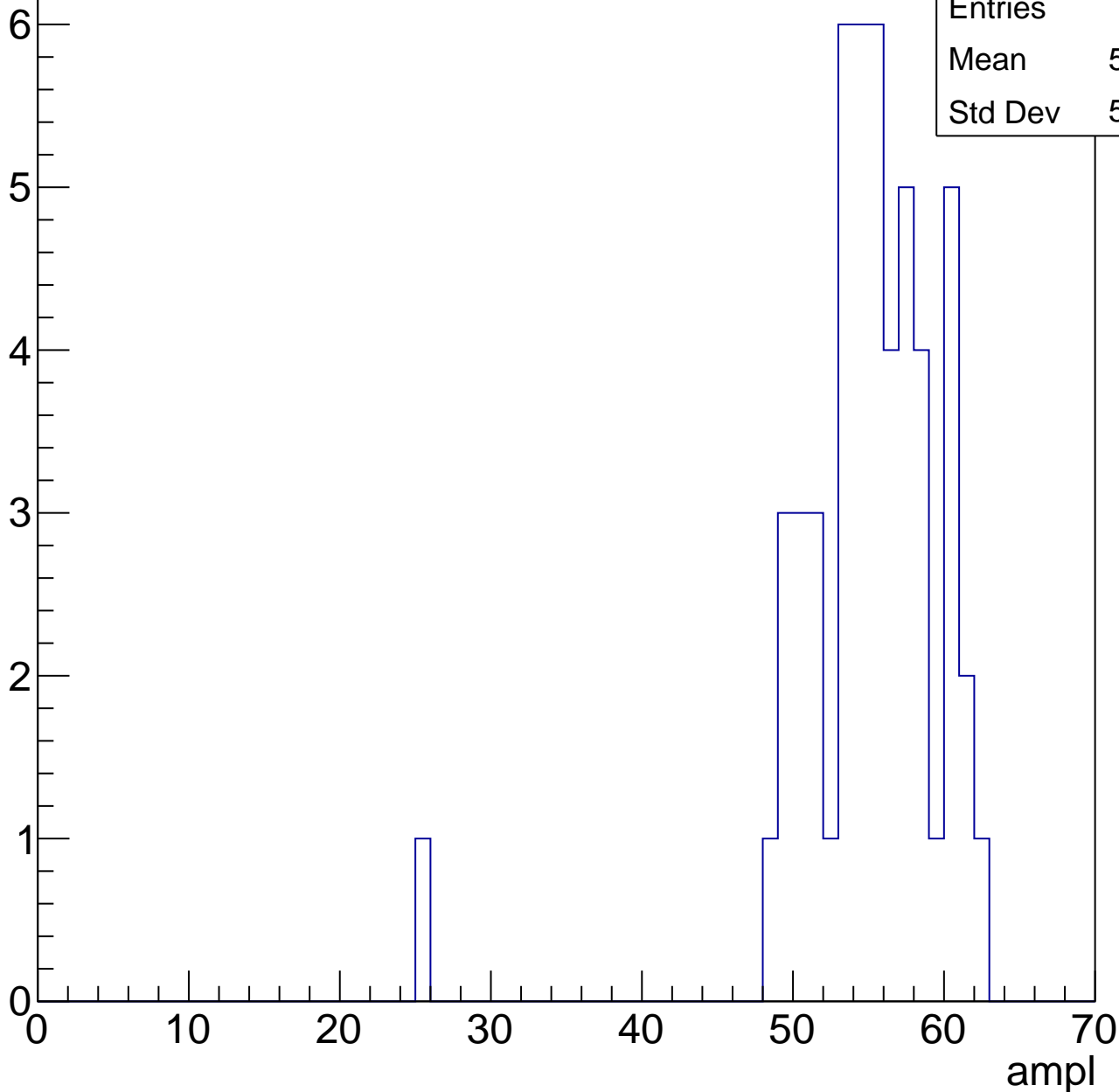


B1L102S, U9-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.44
Std Dev	5.415

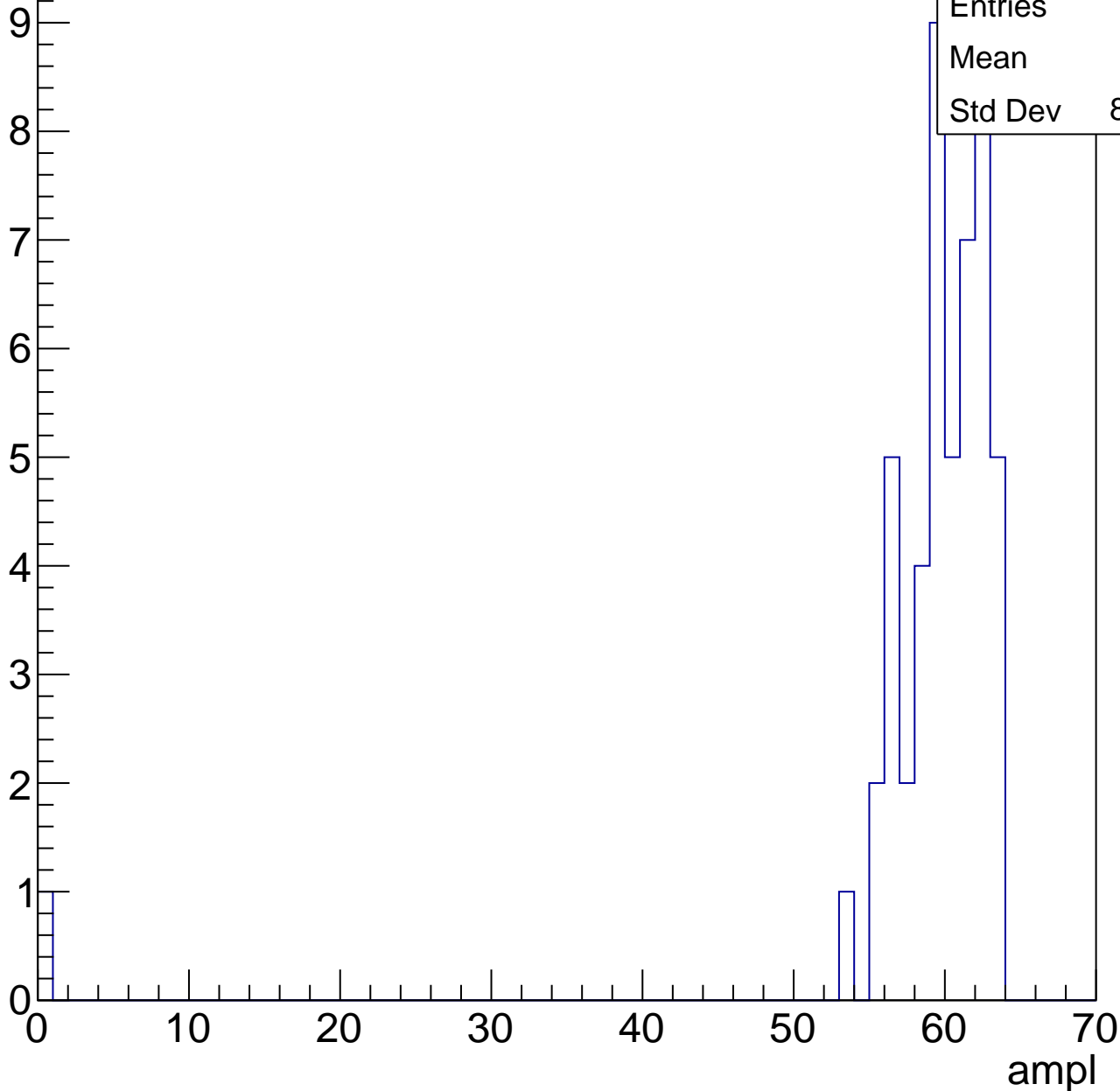


B1L102S, U9-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.4
Std Dev	8.697

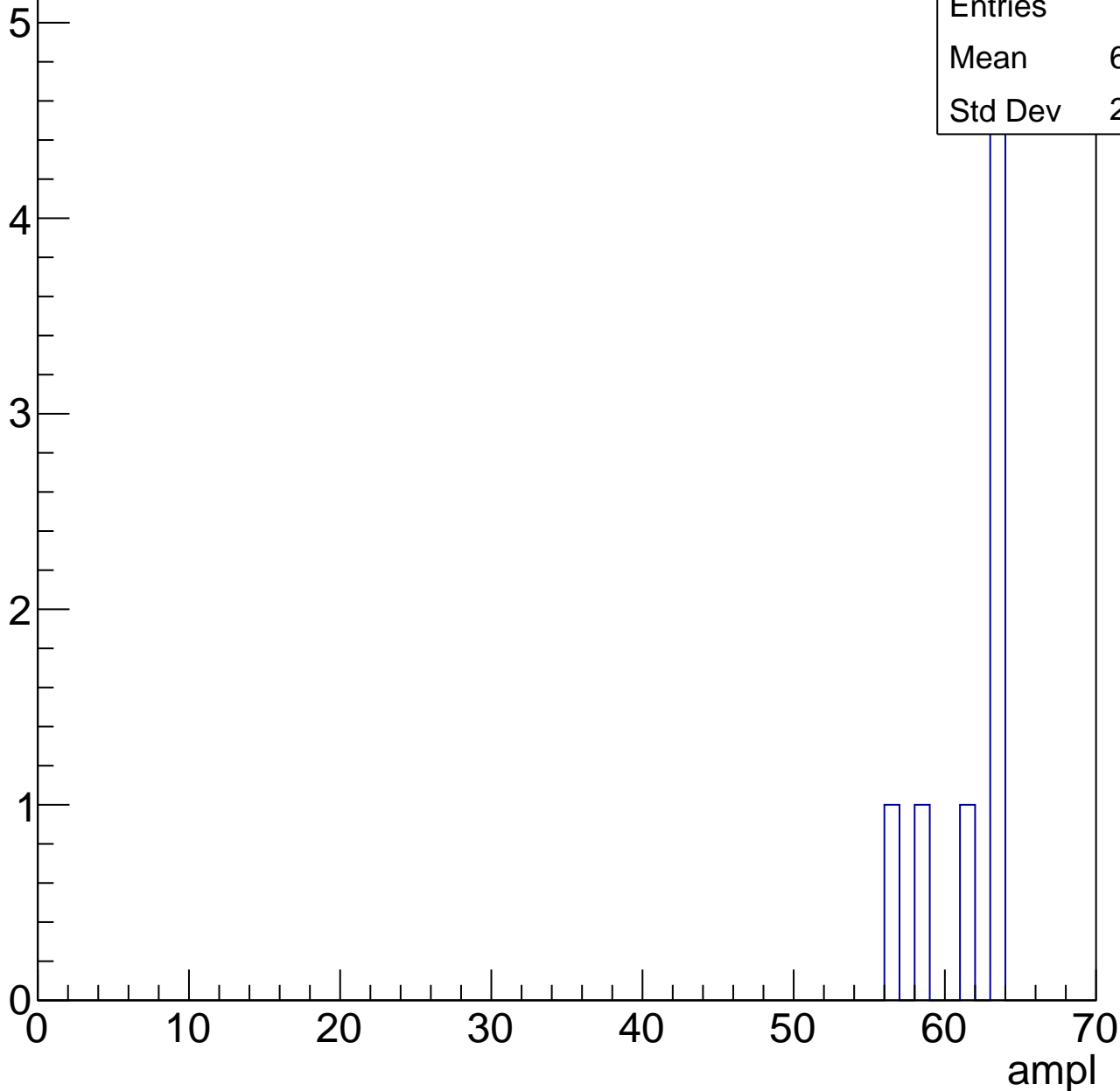


B1L102S, U9-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	61.25
Std Dev	2.586



B1L102S, U9-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L102S, U9-ch41, adc0

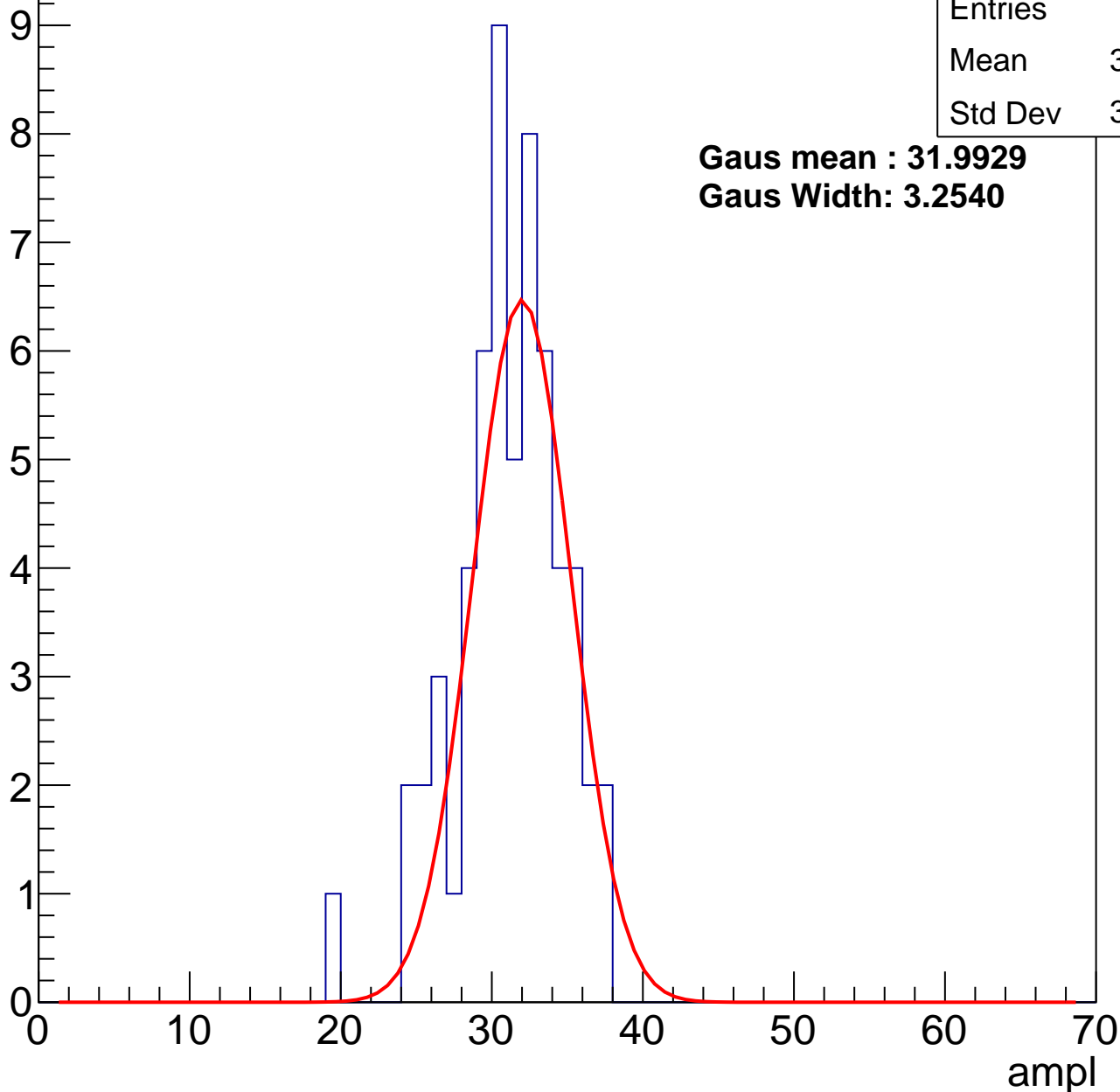
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	30.66
Std Dev	3.496

Gaus mean : 31.9929

Gaus Width: 3.2540



B1L102S, U9-ch41, adc1

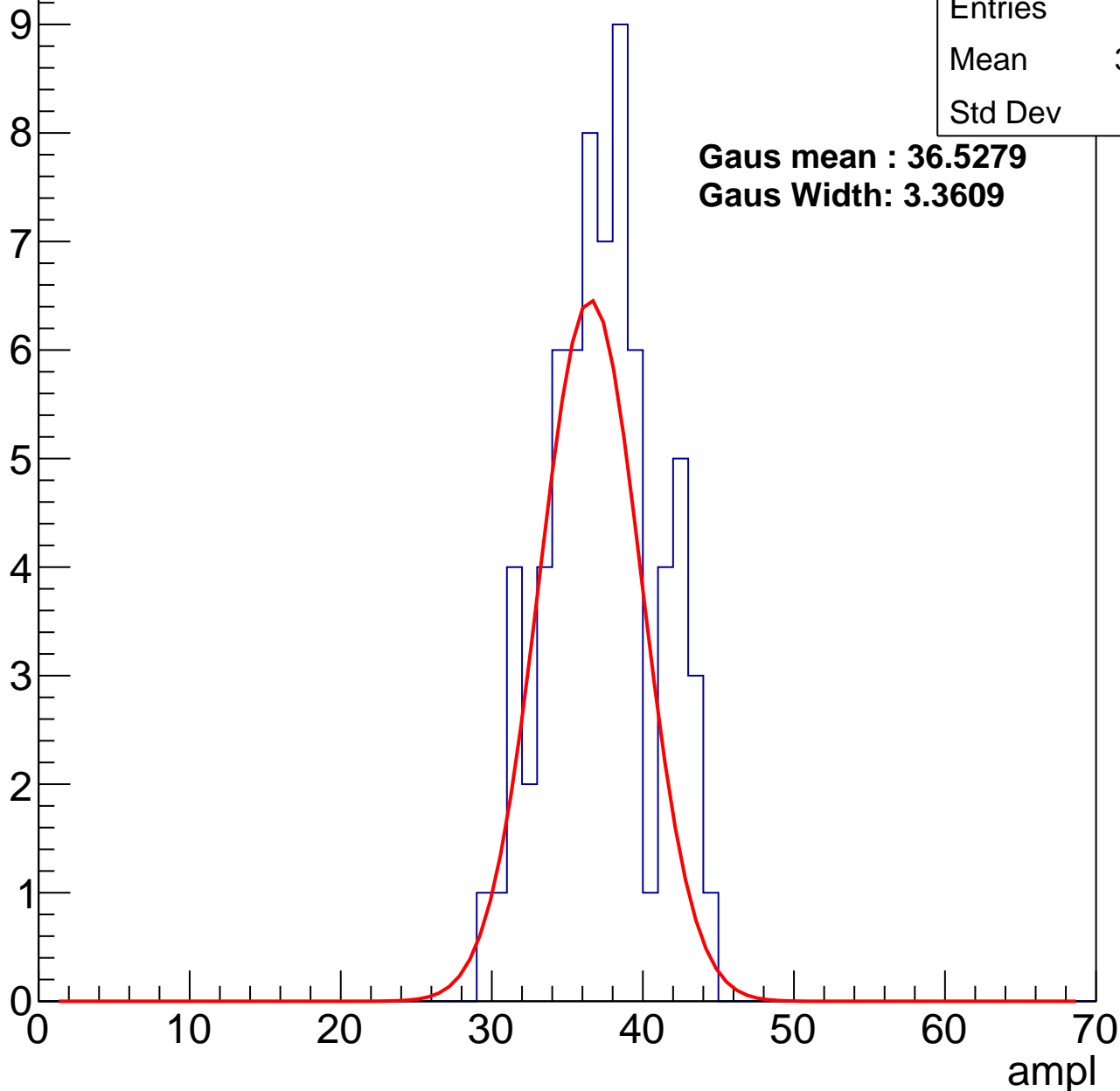
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.81
Std Dev	3.52

Gaus mean : 36.5279

Gaus Width: 3.3609



B1L102S, U9-ch41, adc2

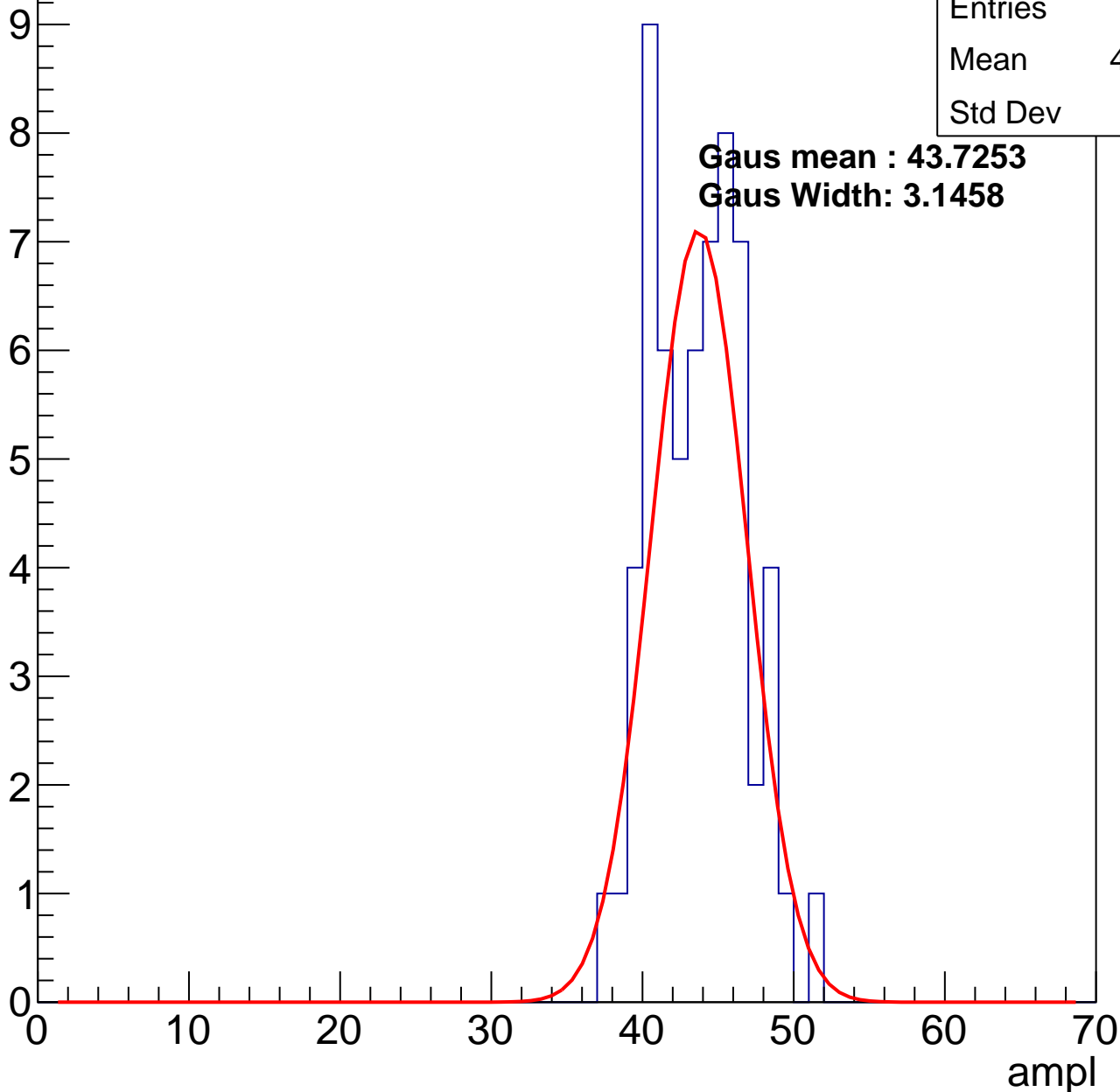
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.24
Std Dev	3.02

Gaus mean : 43.7253

Gaus Width: 3.1458



B1L102S, U9-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

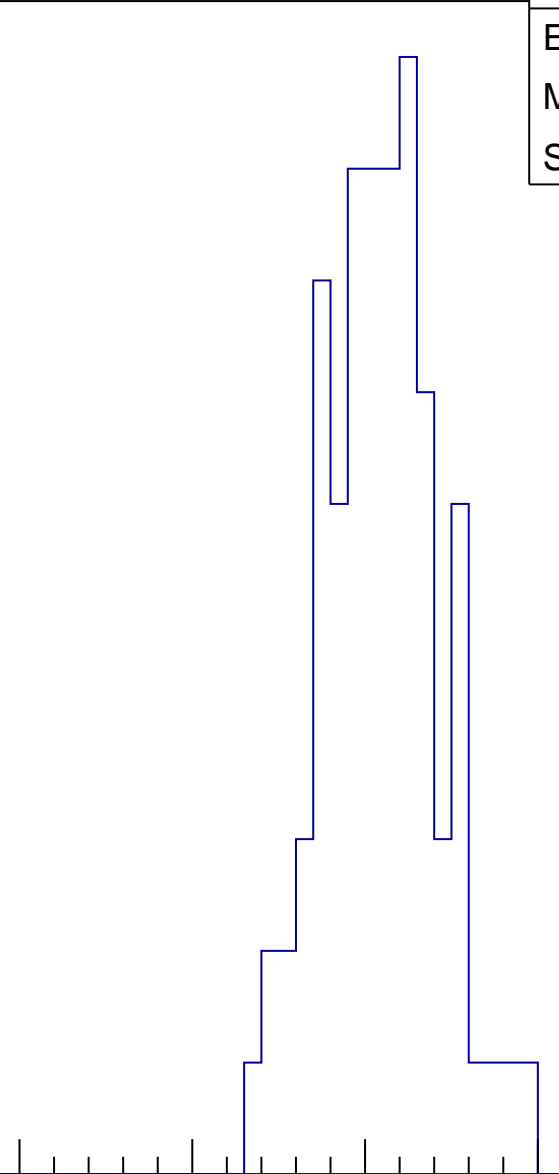
Entries	79
Mean	50.46
Std Dev	3.276

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

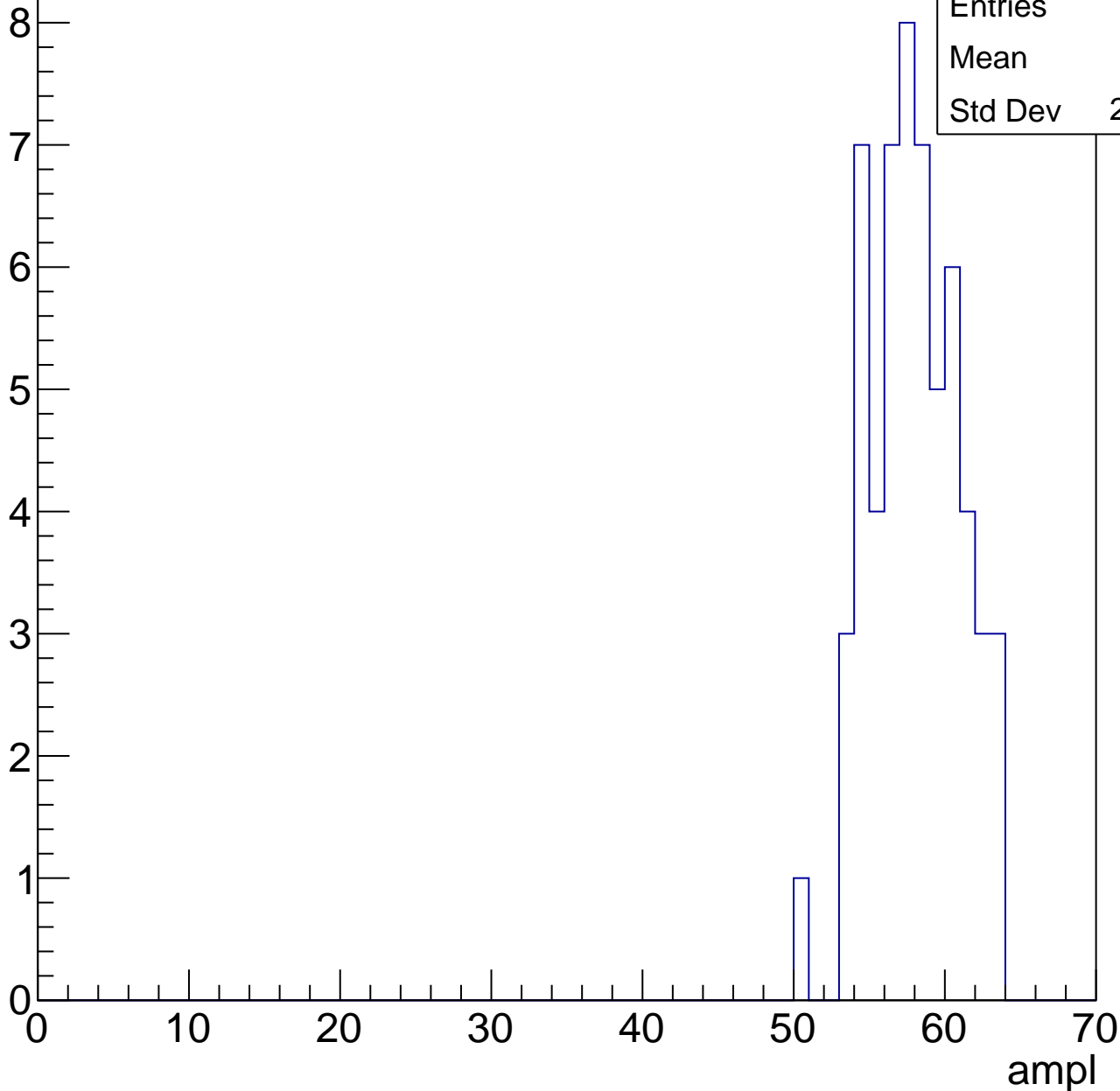


B1L102S, U9-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.5
Std Dev	2.926

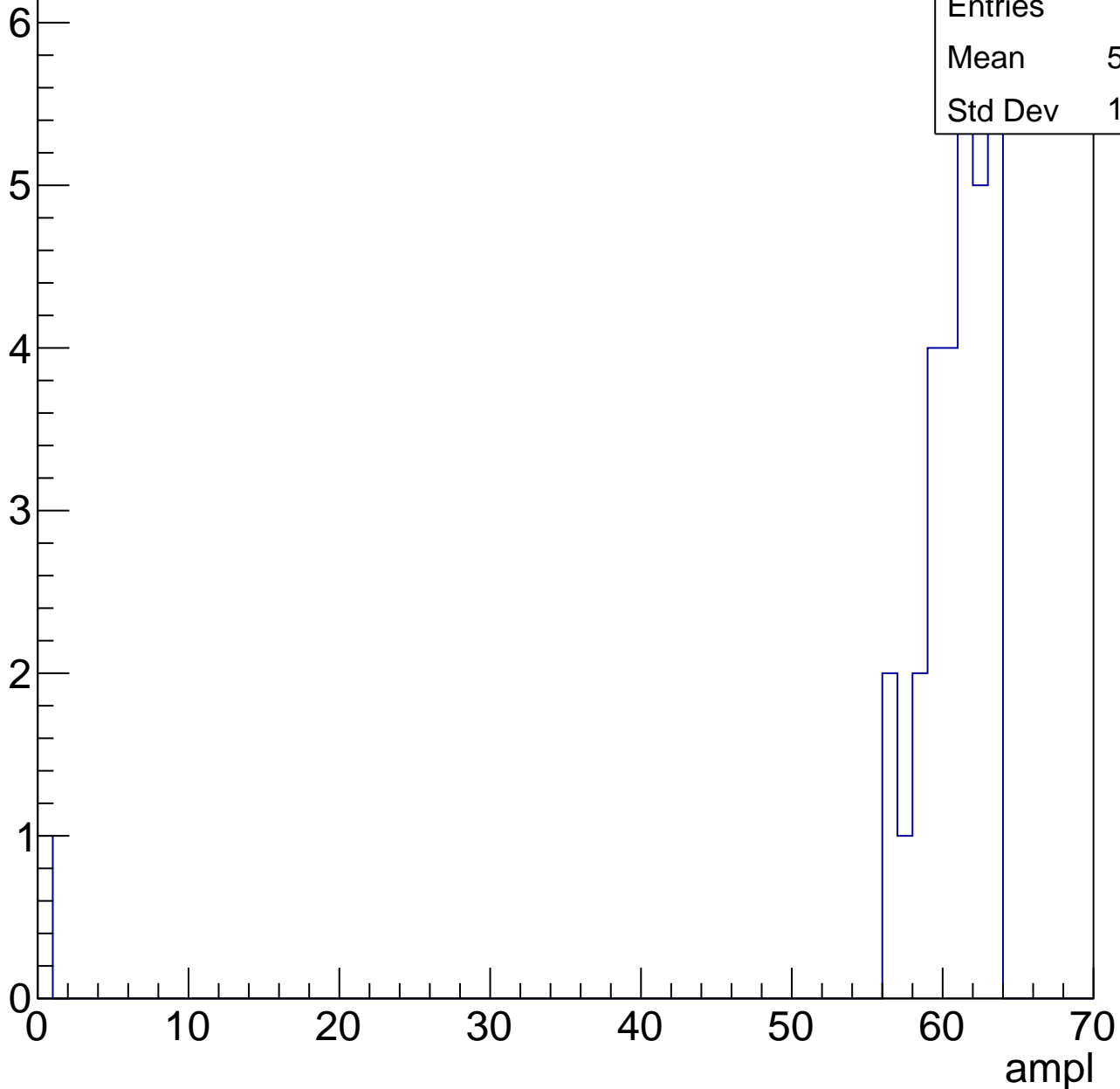


B1L102S, U9-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	58.55
Std Dev	10.88



B1L102S, U9-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch42, adc0

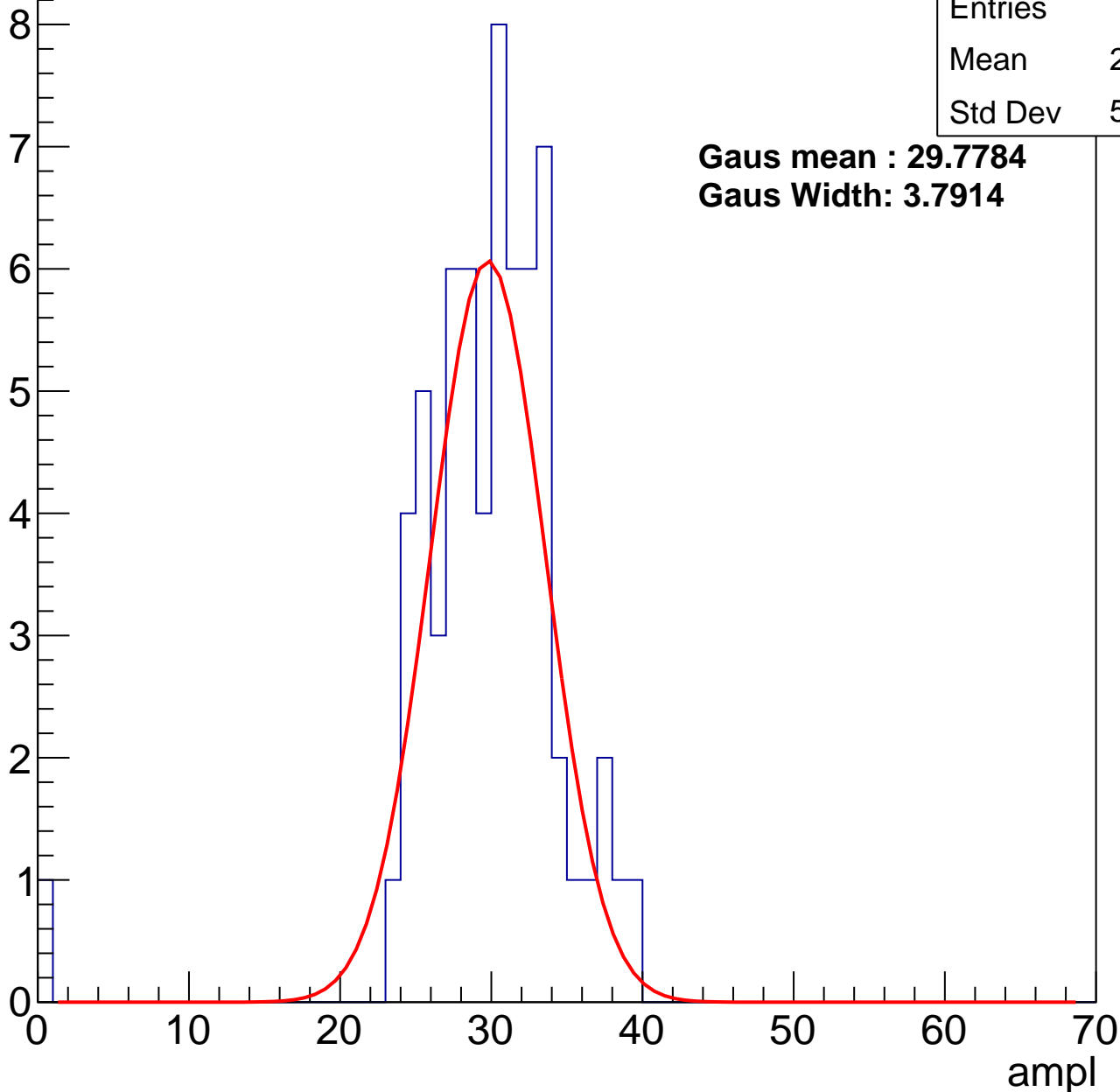
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.34
Std Dev	5.187

Gaus mean : 29.7784

Gaus Width: 3.7914



B1L102S, U9-ch42, adc1

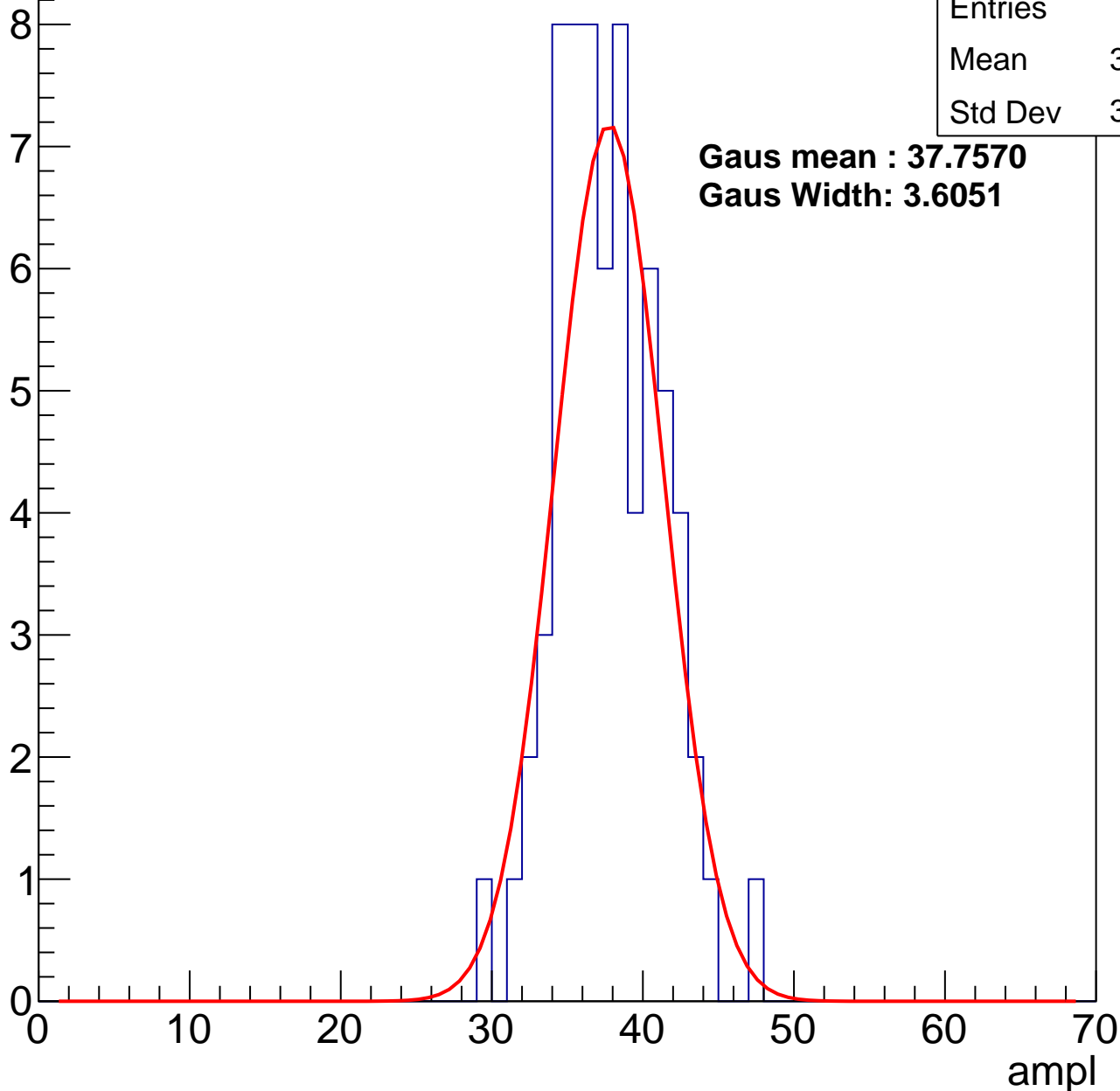
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.28
Std Dev	3.395

Gaus mean : 37.7570

Gaus Width: 3.6051



B1L102S, U9-ch42, adc2

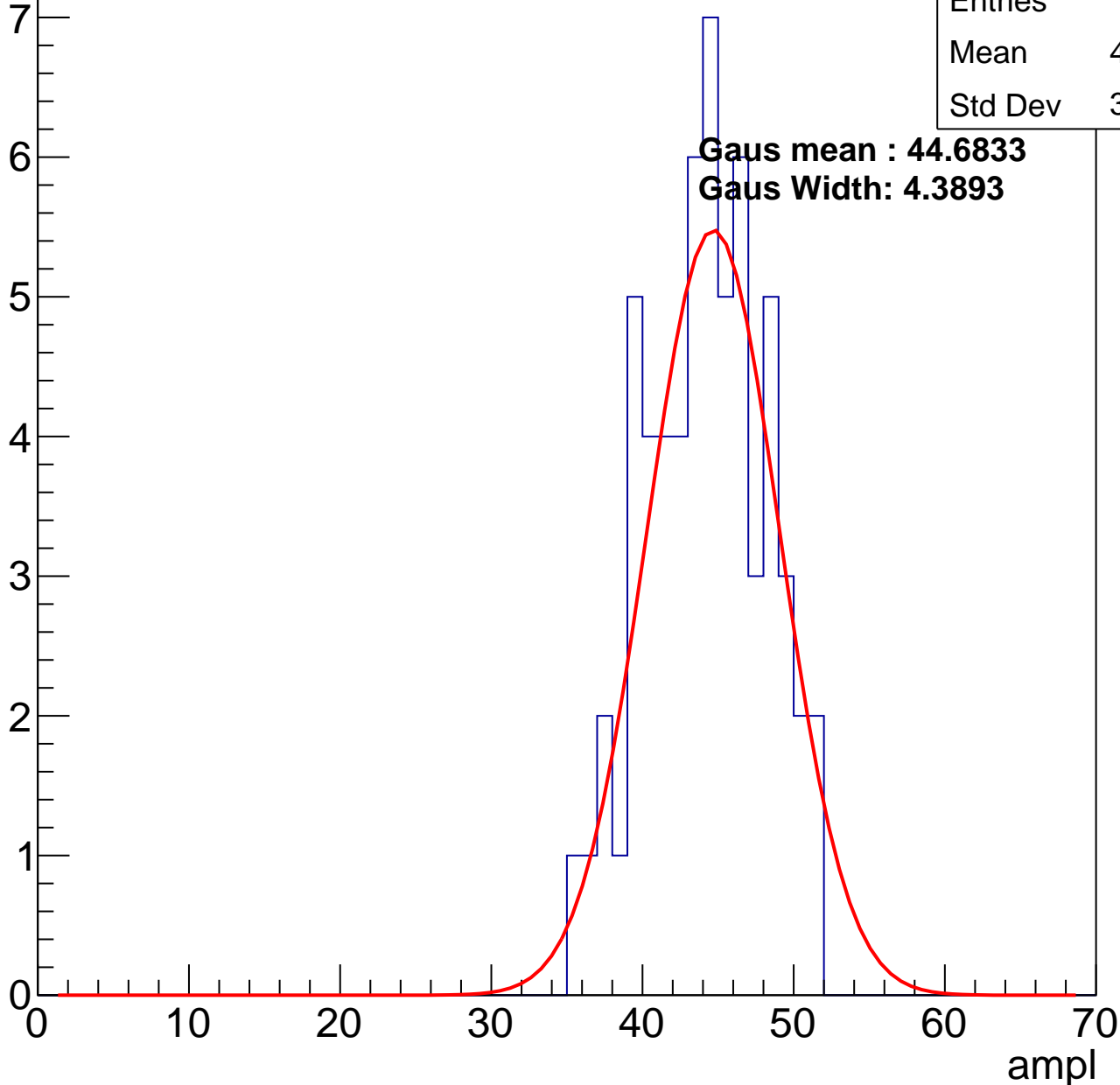
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.72
Std Dev	3.846

Gaus mean : 44.6833

Gaus Width: 4.3893



B1L102S, U9-ch42, adc3

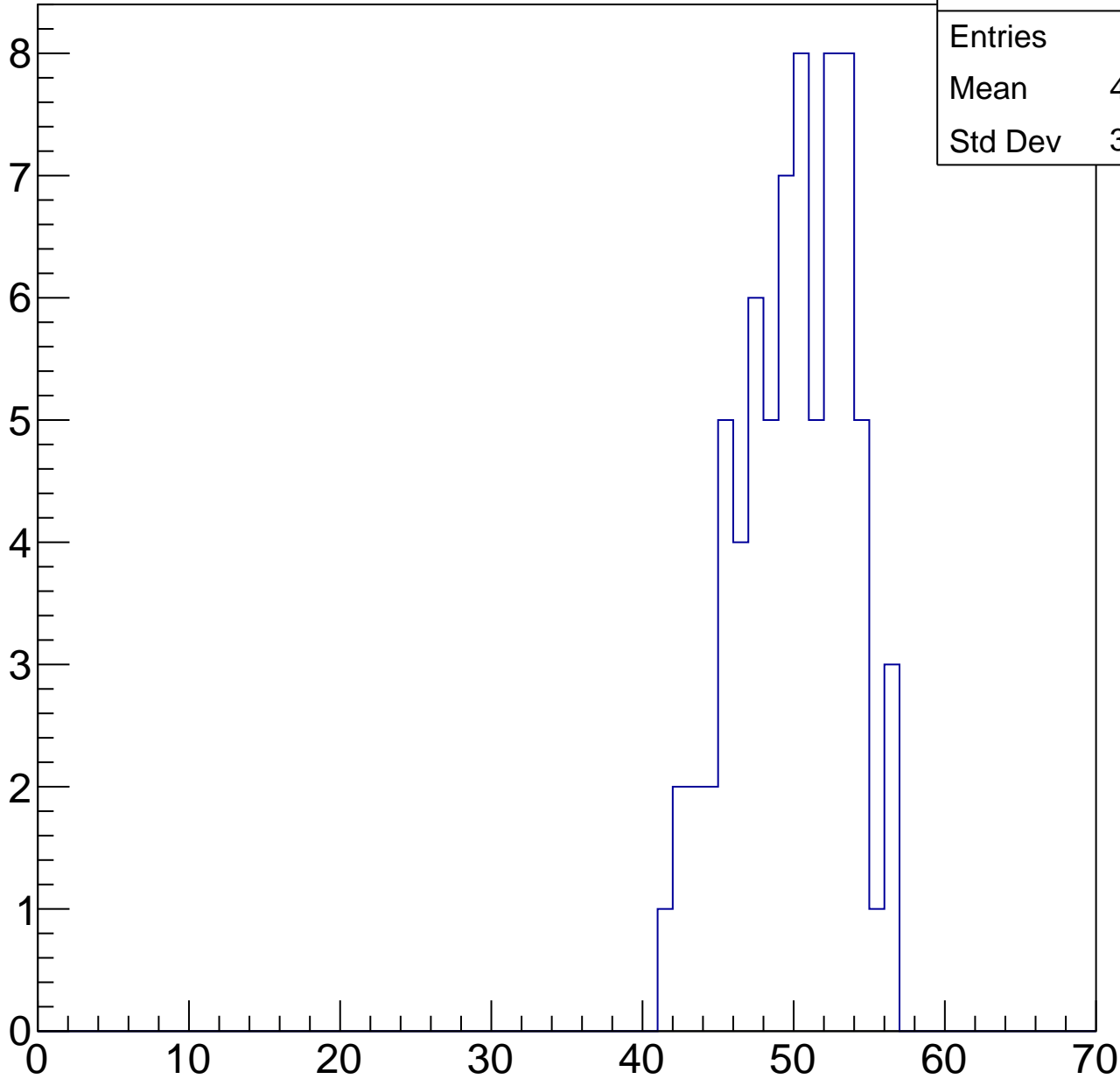
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	72
Mean	49.46
Std Dev	3.632

ampl



B1L102S, U9-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	56.83
Std Dev	2.934

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

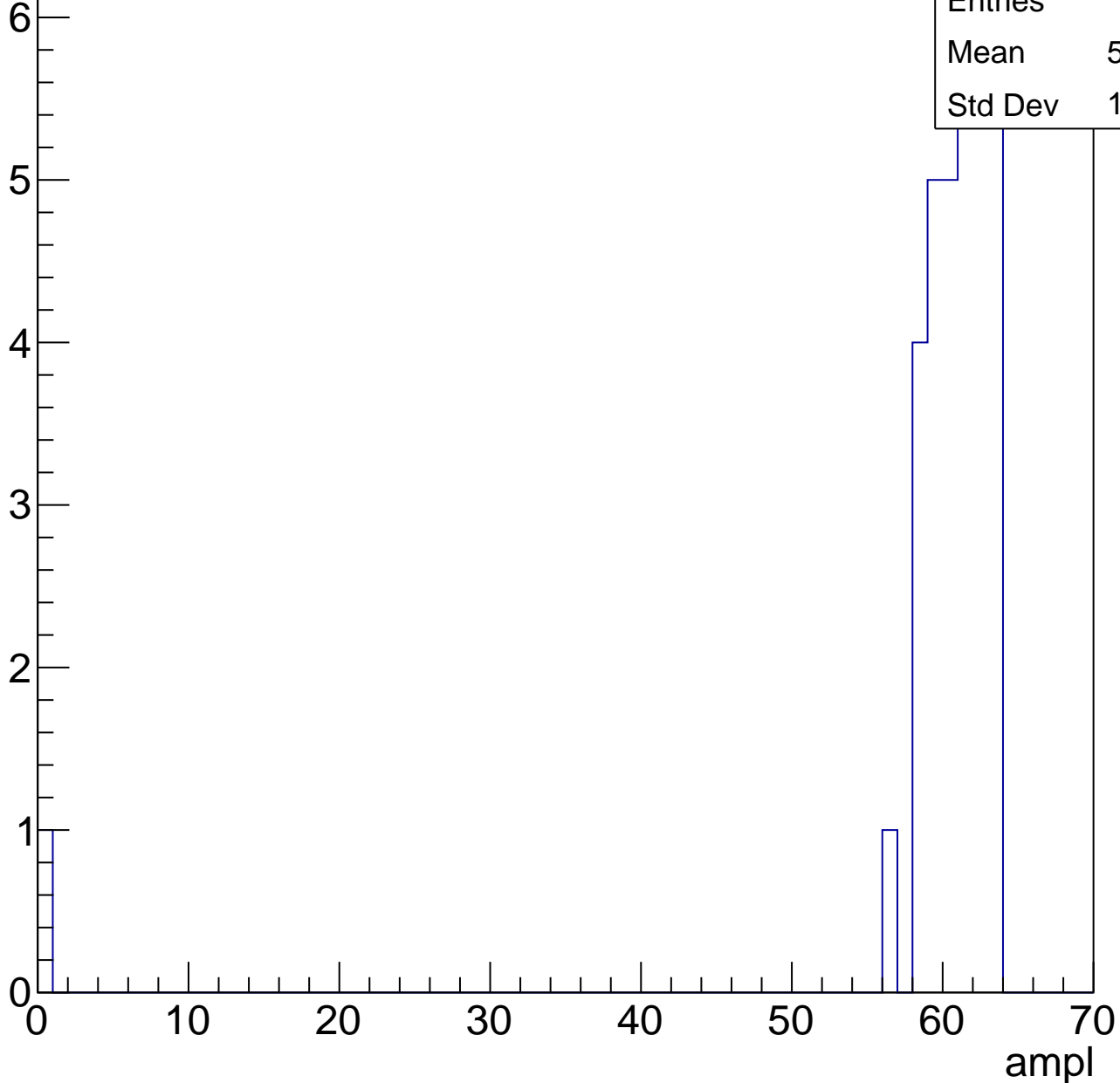


B1L102S, U9-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	58.79
Std Dev	10.39



B1L102S, U9-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U9-ch43, adc0

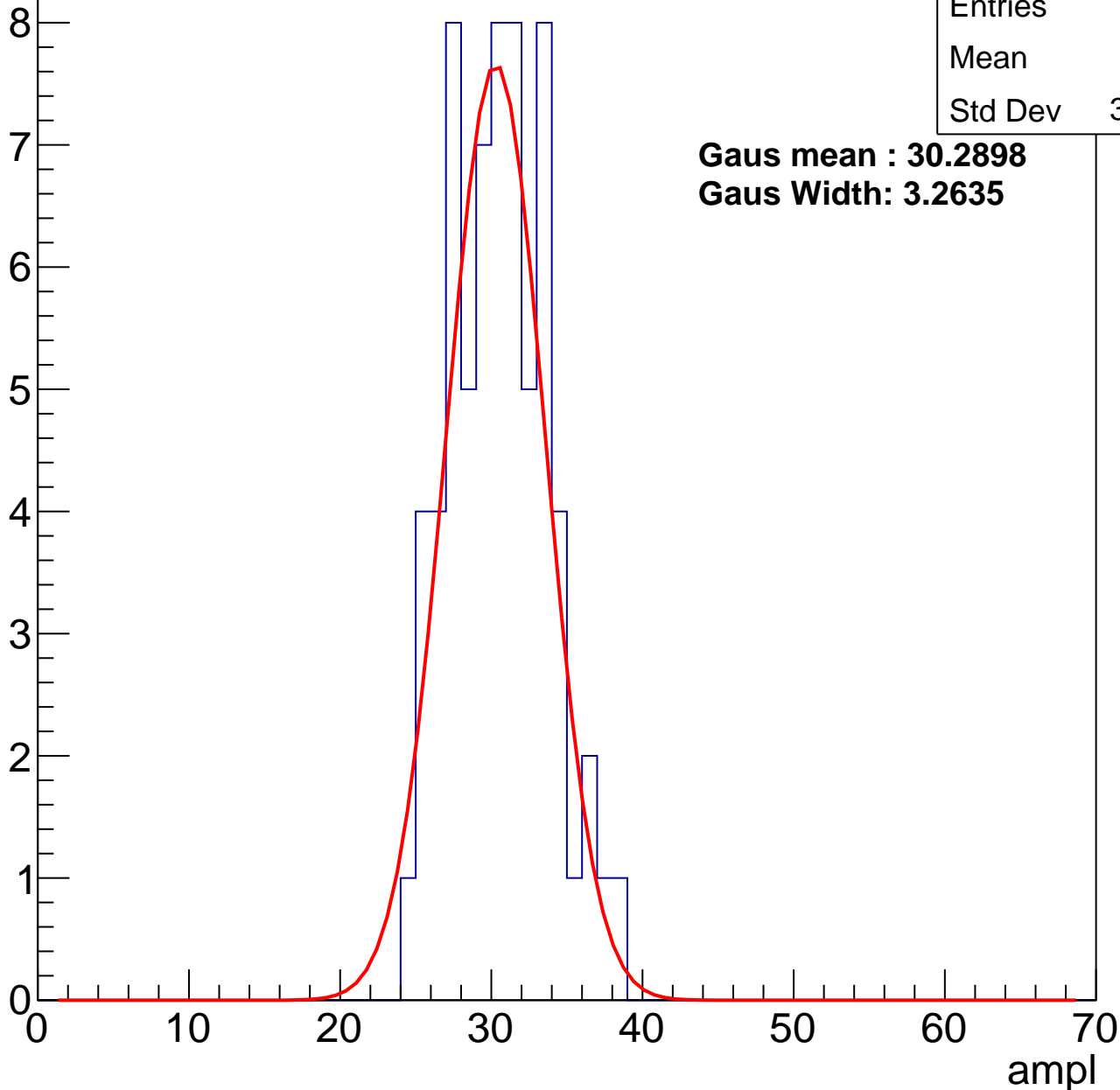
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30.1
Std Dev	3.168

Gaus mean : 30.2898

Gaus Width: 3.2635



B1L102S, U9-ch43, adc1

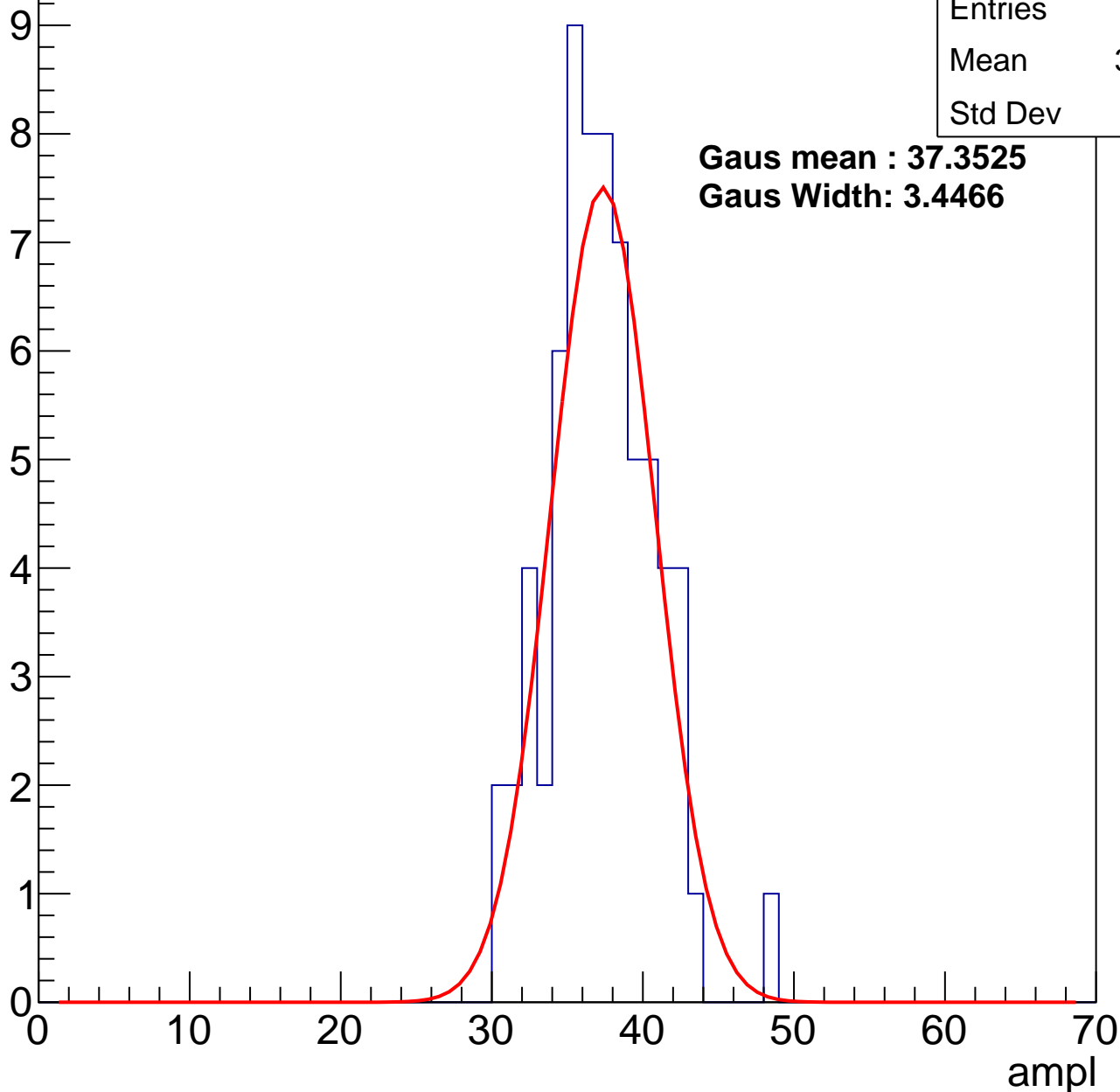
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.81
Std Dev	3.41

Gaus mean : 37.3525

Gaus Width: 3.4466



B1L102S, U9-ch43, adc2

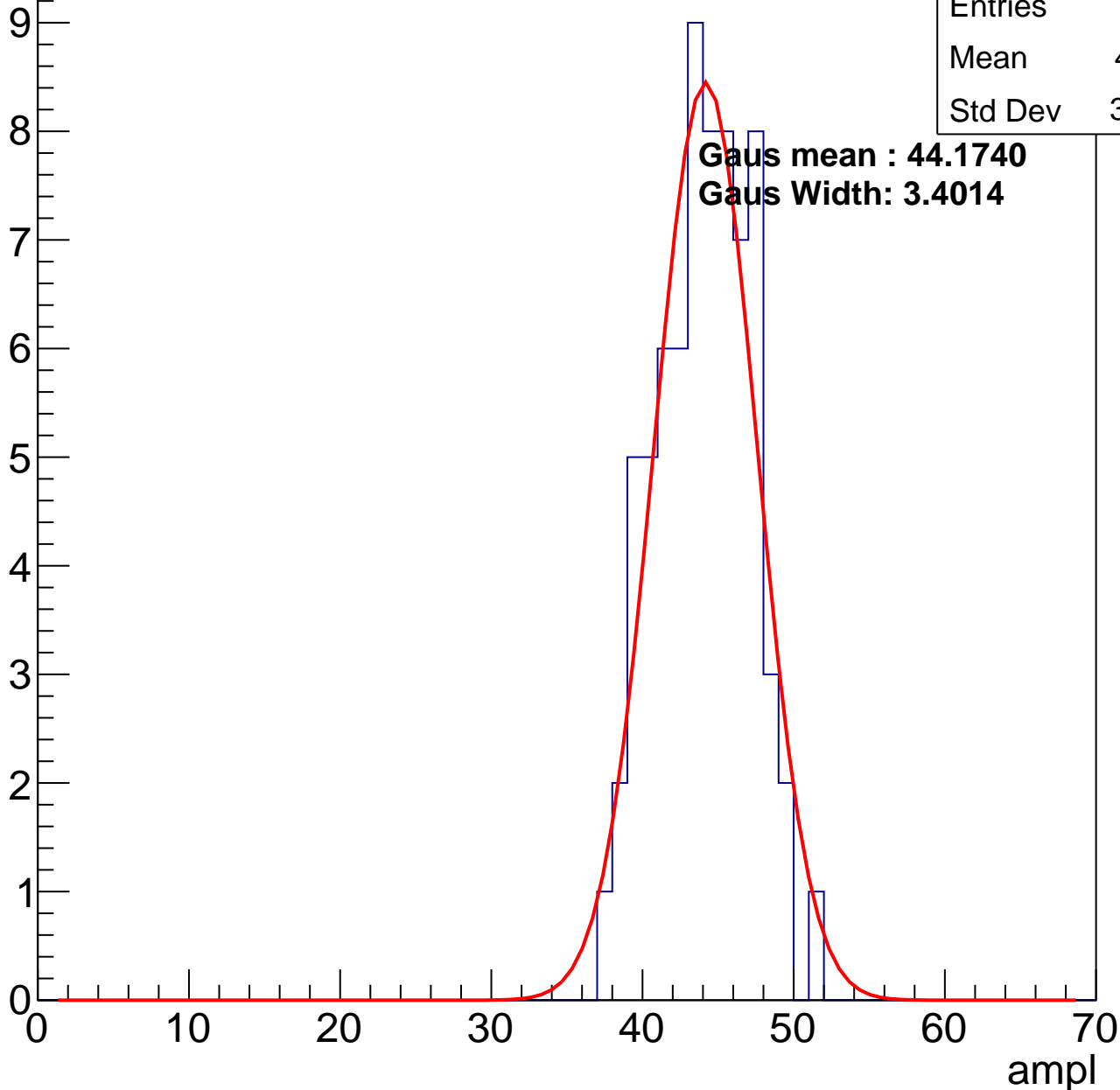
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.61
Std Dev	3.042

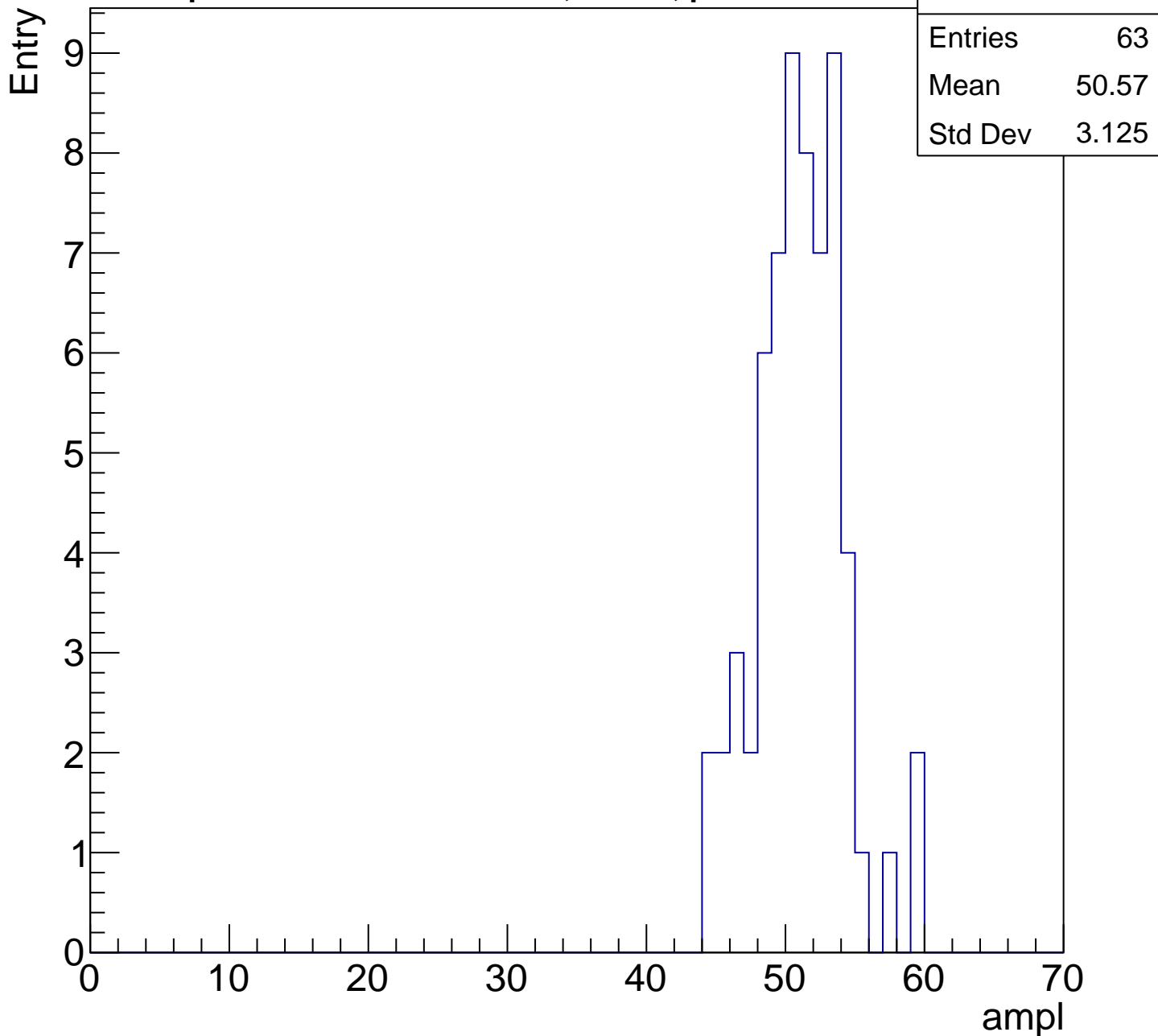
Gaus mean : 44.1740

Gaus Width: 3.4014



B1L102S, U9-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

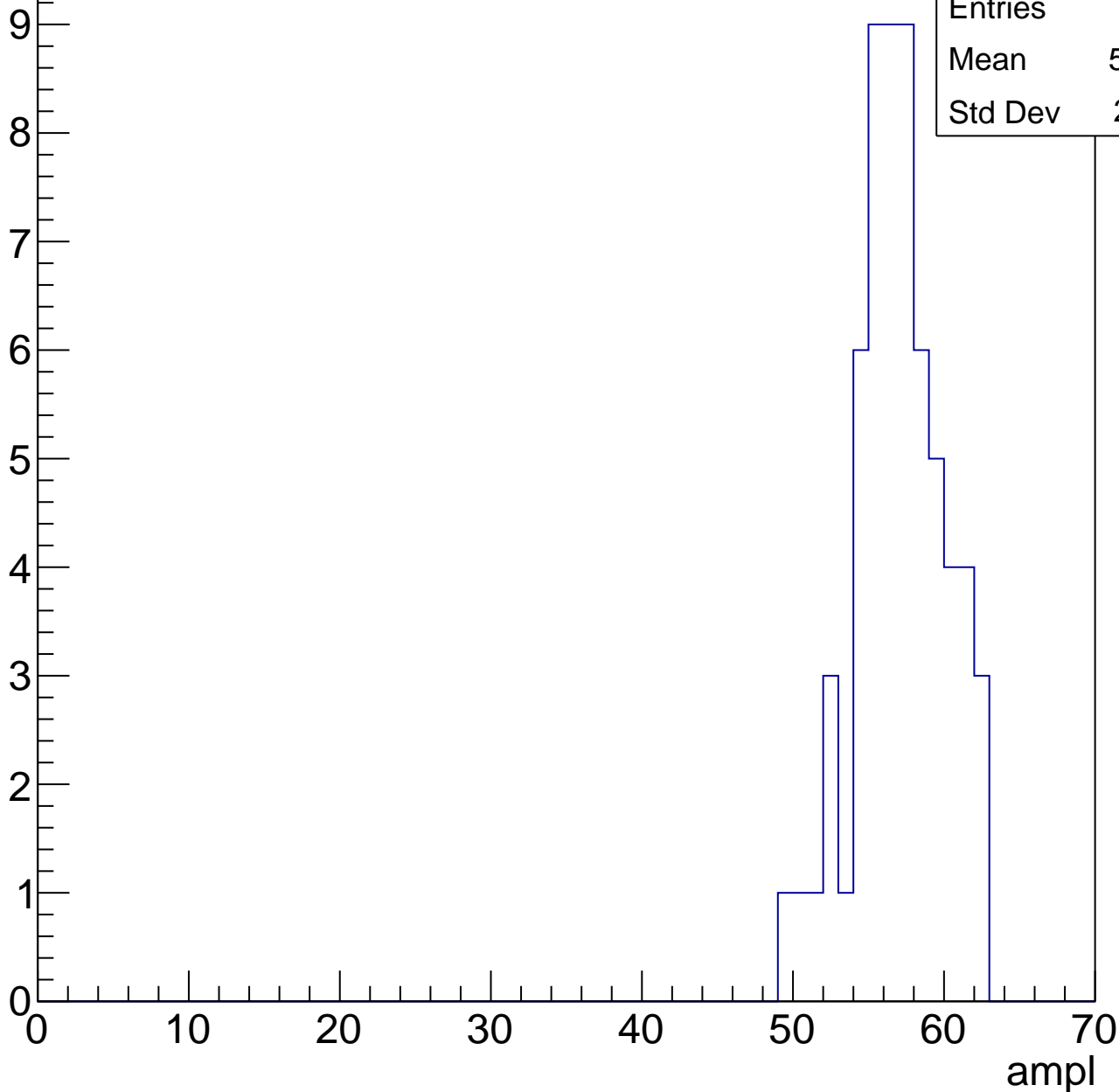


B1L102S, U9-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.58
Std Dev	2.921

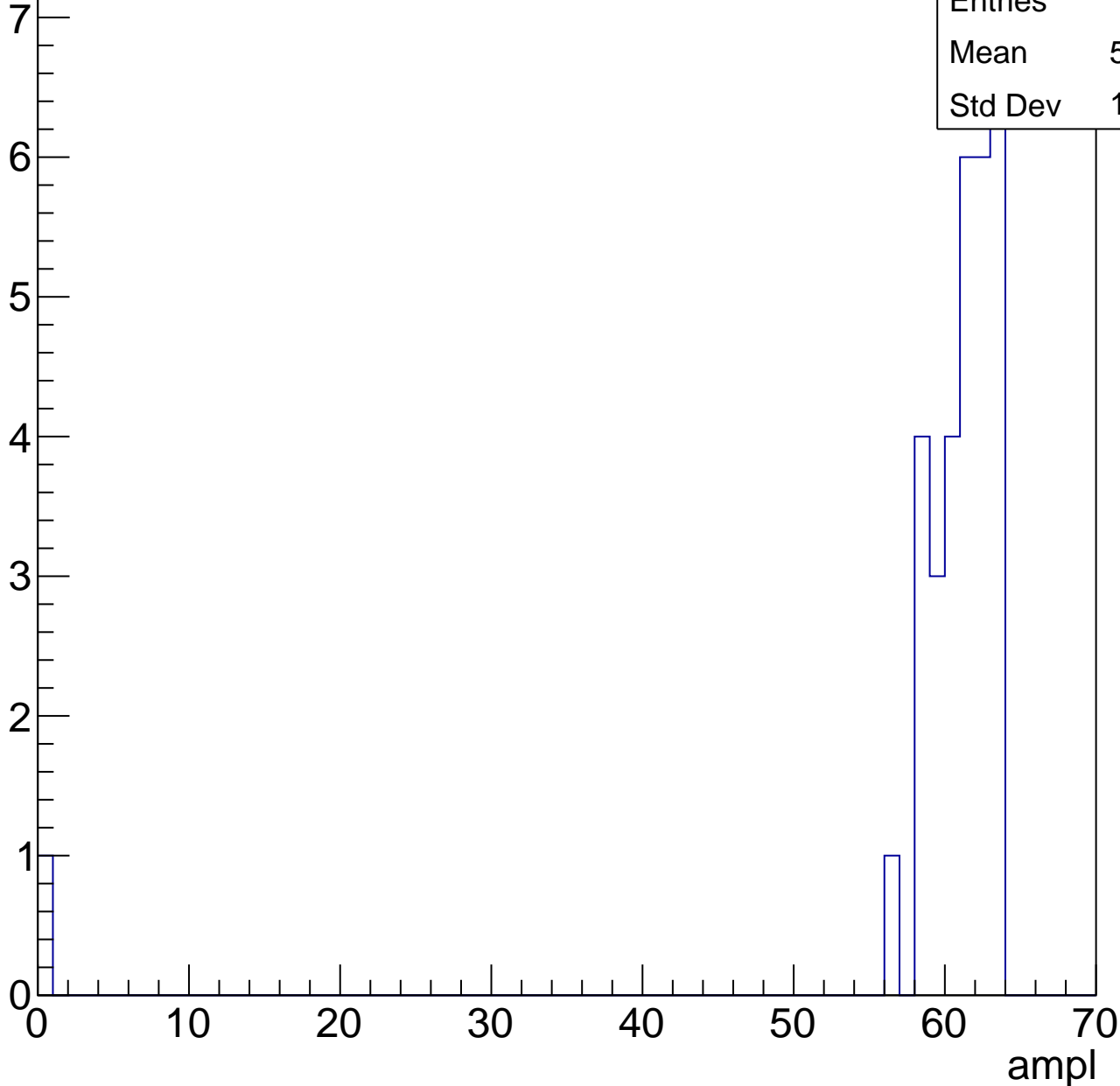


B1L102S, U9-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

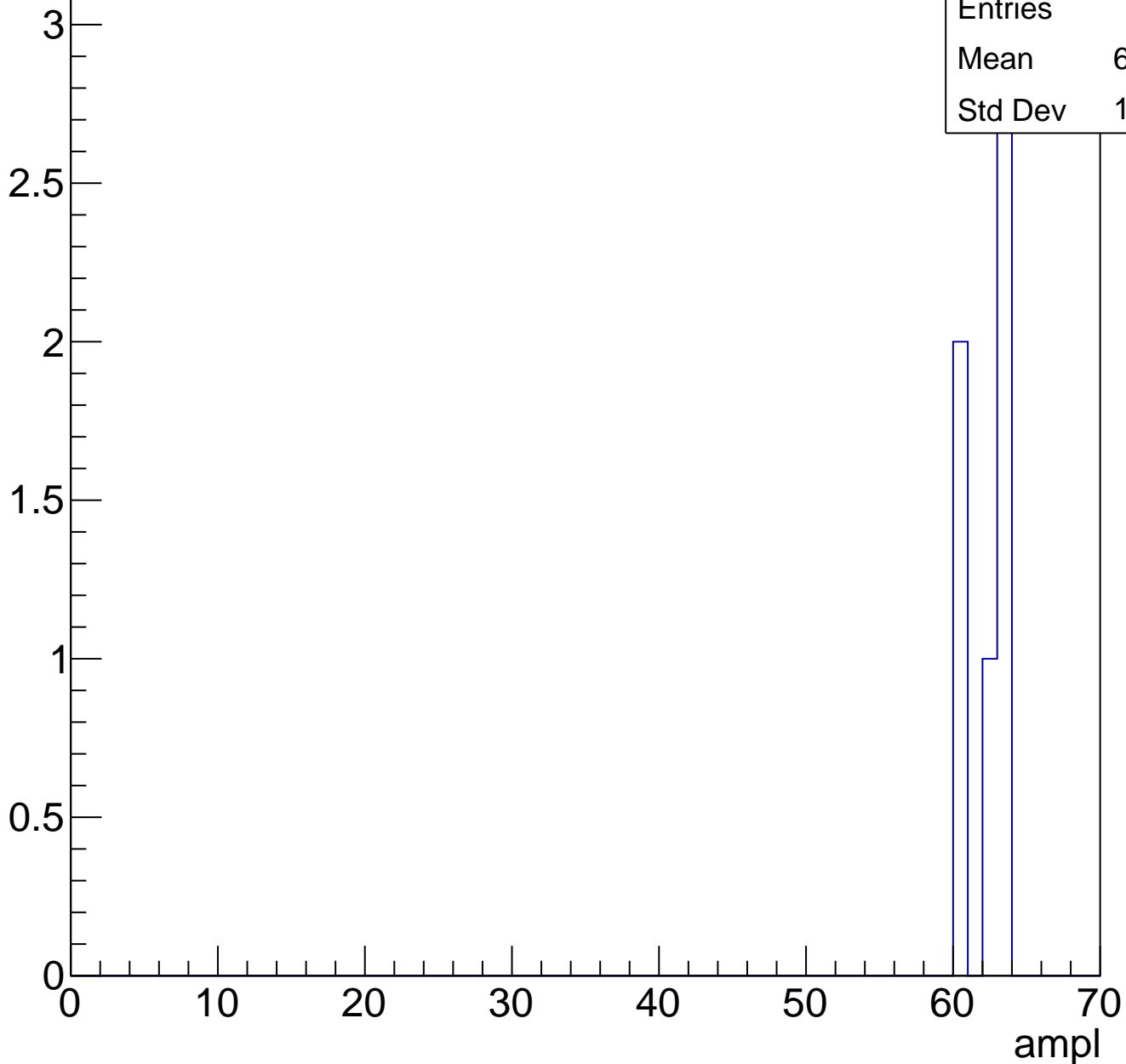
Entries	32
Mean	58.88
Std Dev	10.73



B1L102S, U9-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch44, adc0

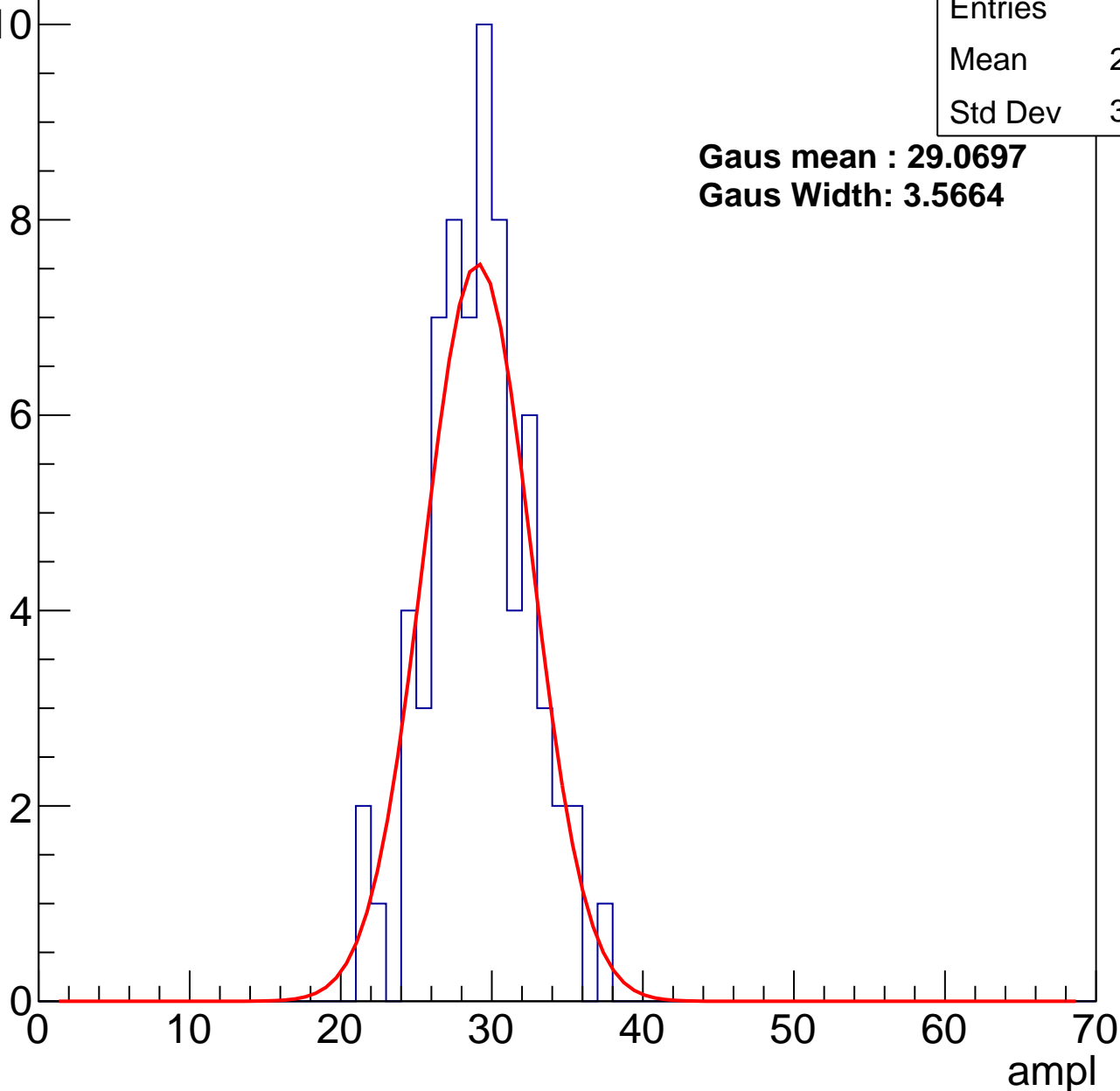
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.66
Std Dev	3.279

Gaus mean : 29.0697

Gaus Width: 3.5664



B1L102S, U9-ch44, adc1

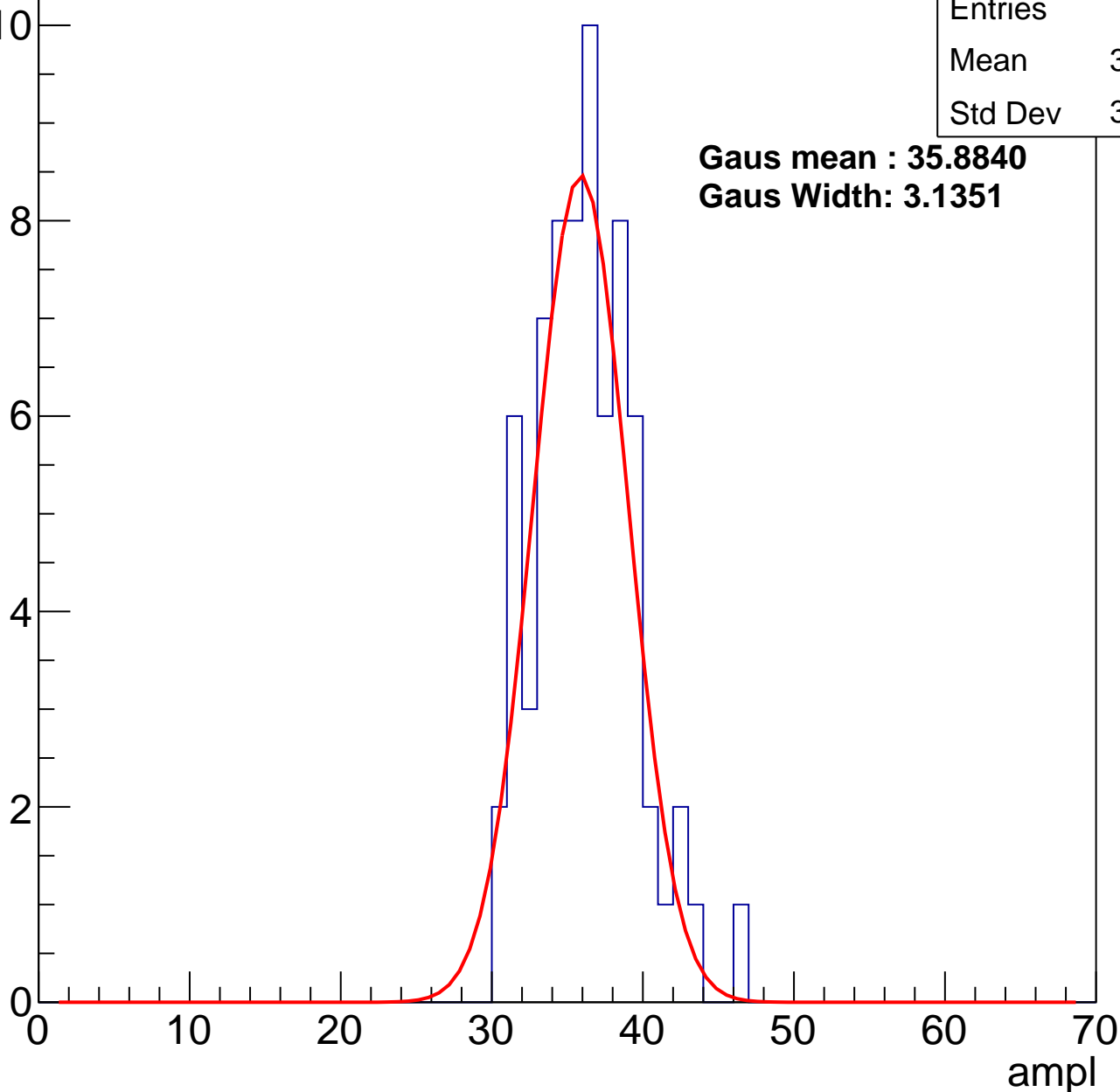
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.76
Std Dev	3.226

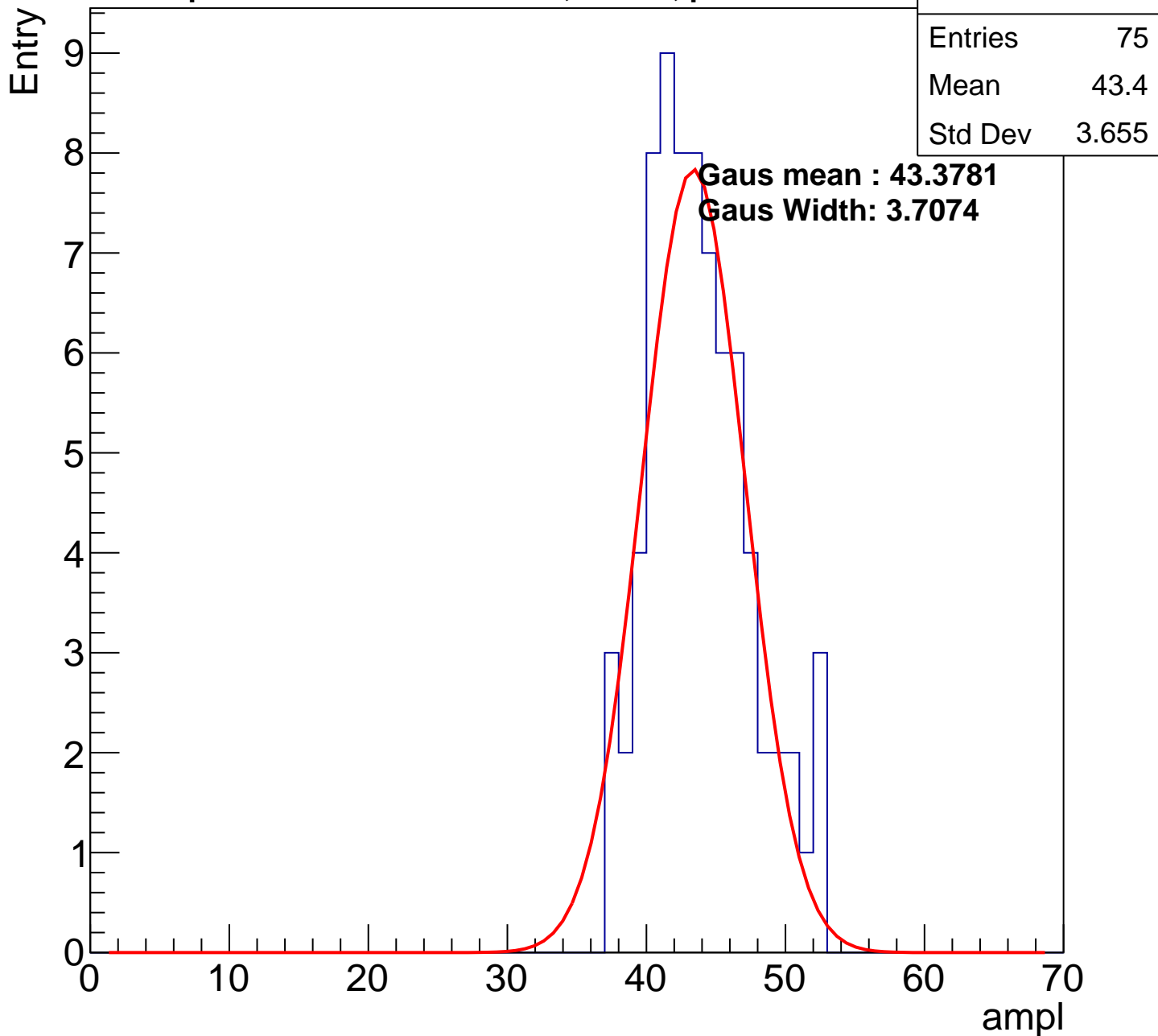
Gaus mean : 35.8840

Gaus Width: 3.1351



B1L102S, U9-ch44, adc2

calib_packv5_042523_0143.root, FC#11, port A2

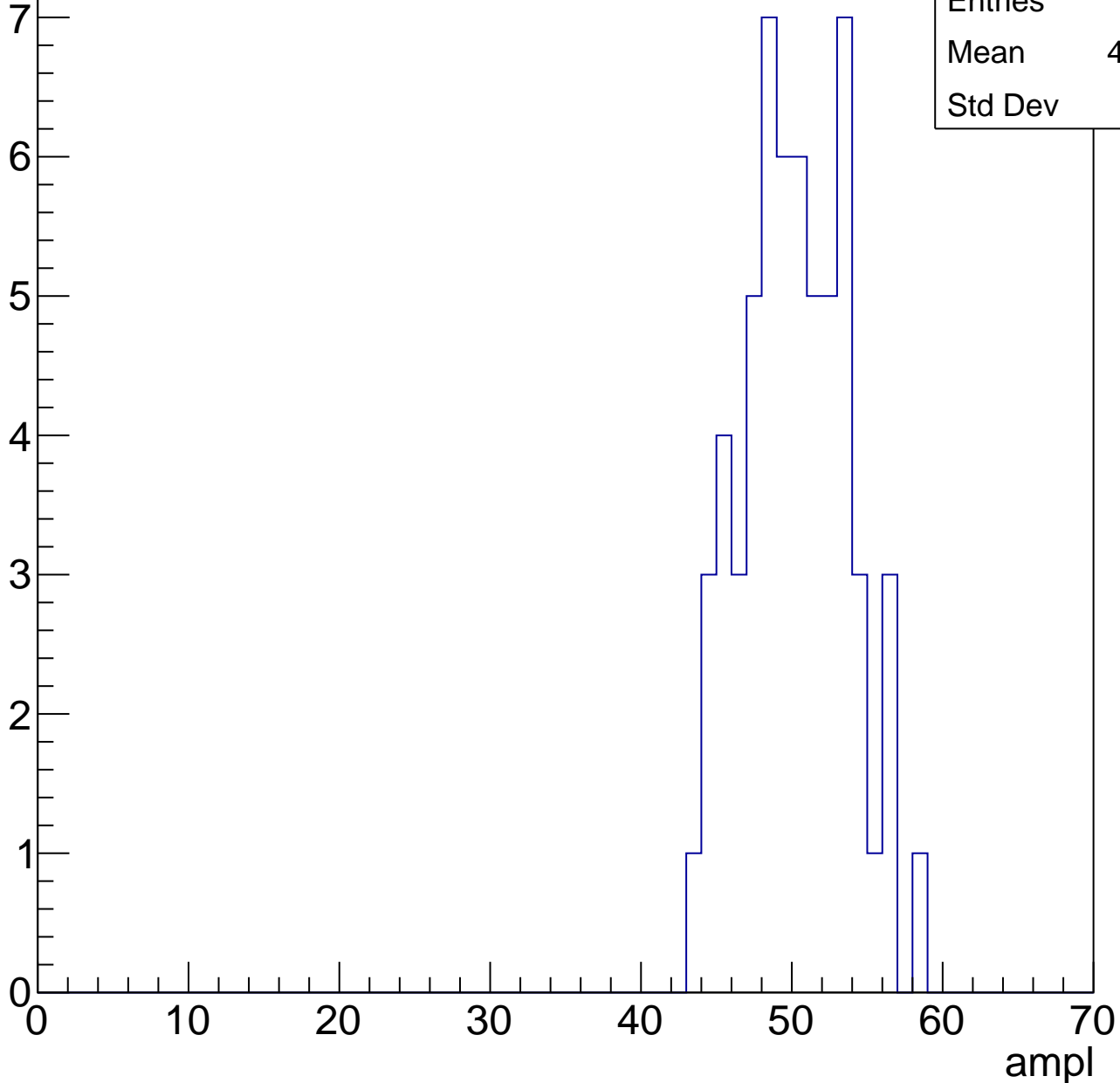


B1L102S, U9-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.78
Std Dev	3.45



B1L102S, U9-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 59

Mean 55.61

Std Dev 3.669

4

3

2

1

0

0

1

2

3

4

5

6

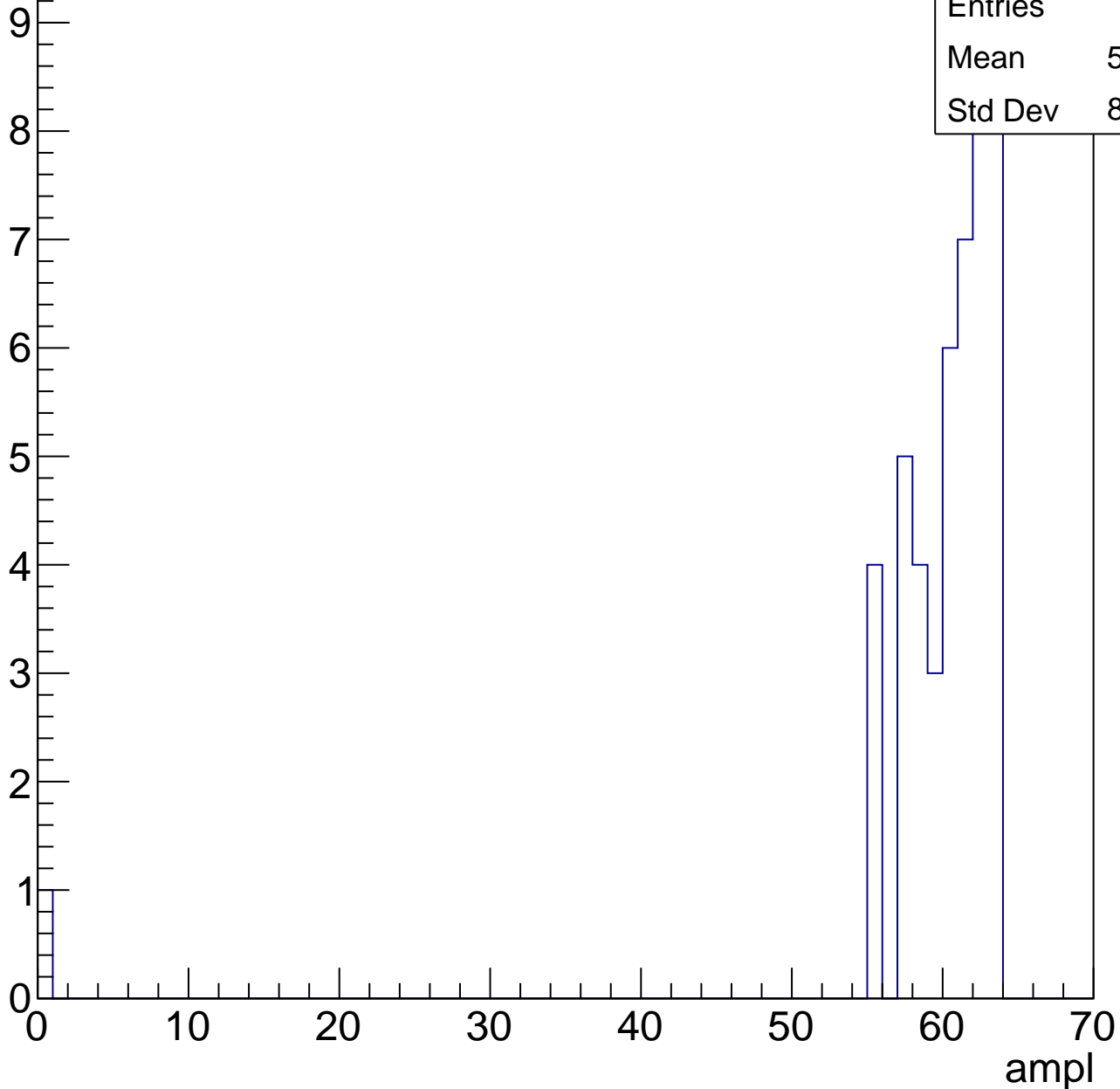
7

B1L102S, U9-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.88
Std Dev	8.929



B1L102S, U9-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch45, adc0

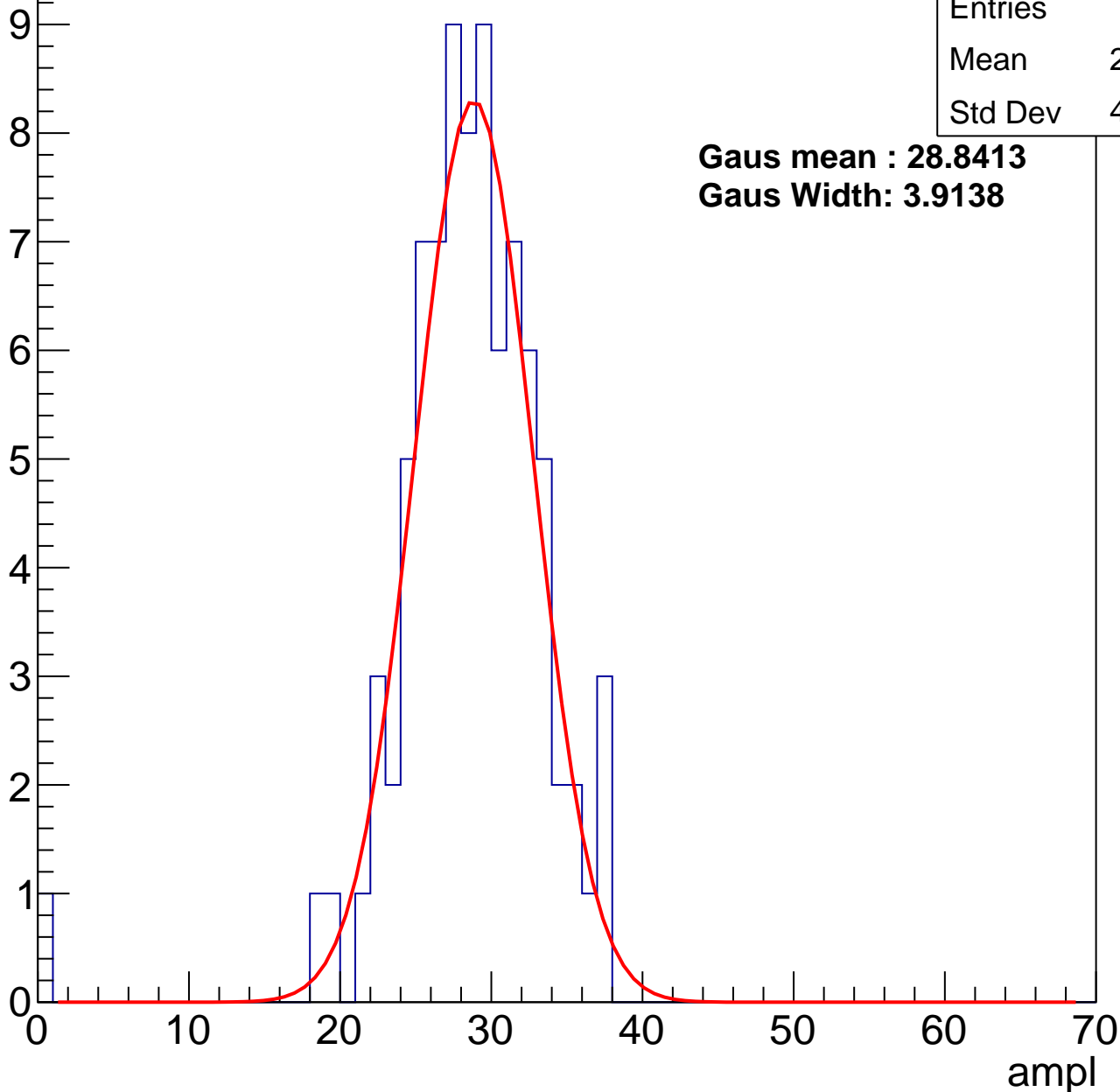
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	28.07
Std Dev	4.995

Gaus mean : 28.8413

Gaus Width: 3.9138



B1L102S, U9-ch45, adc1

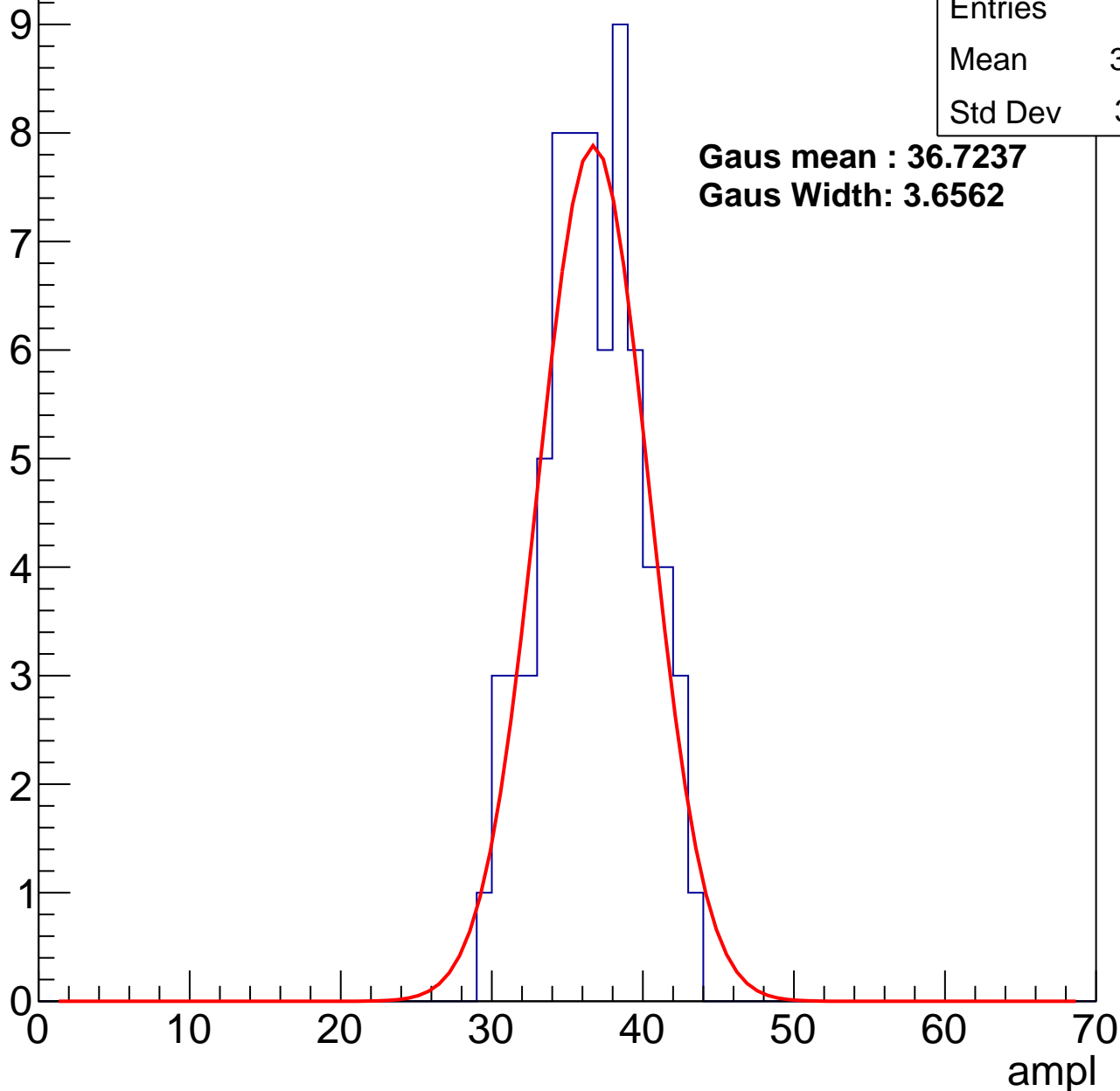
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.17
Std Dev	3.291

Gaus mean : 36.7237

Gaus Width: 3.6562



B1L102S, U9-ch45, adc2

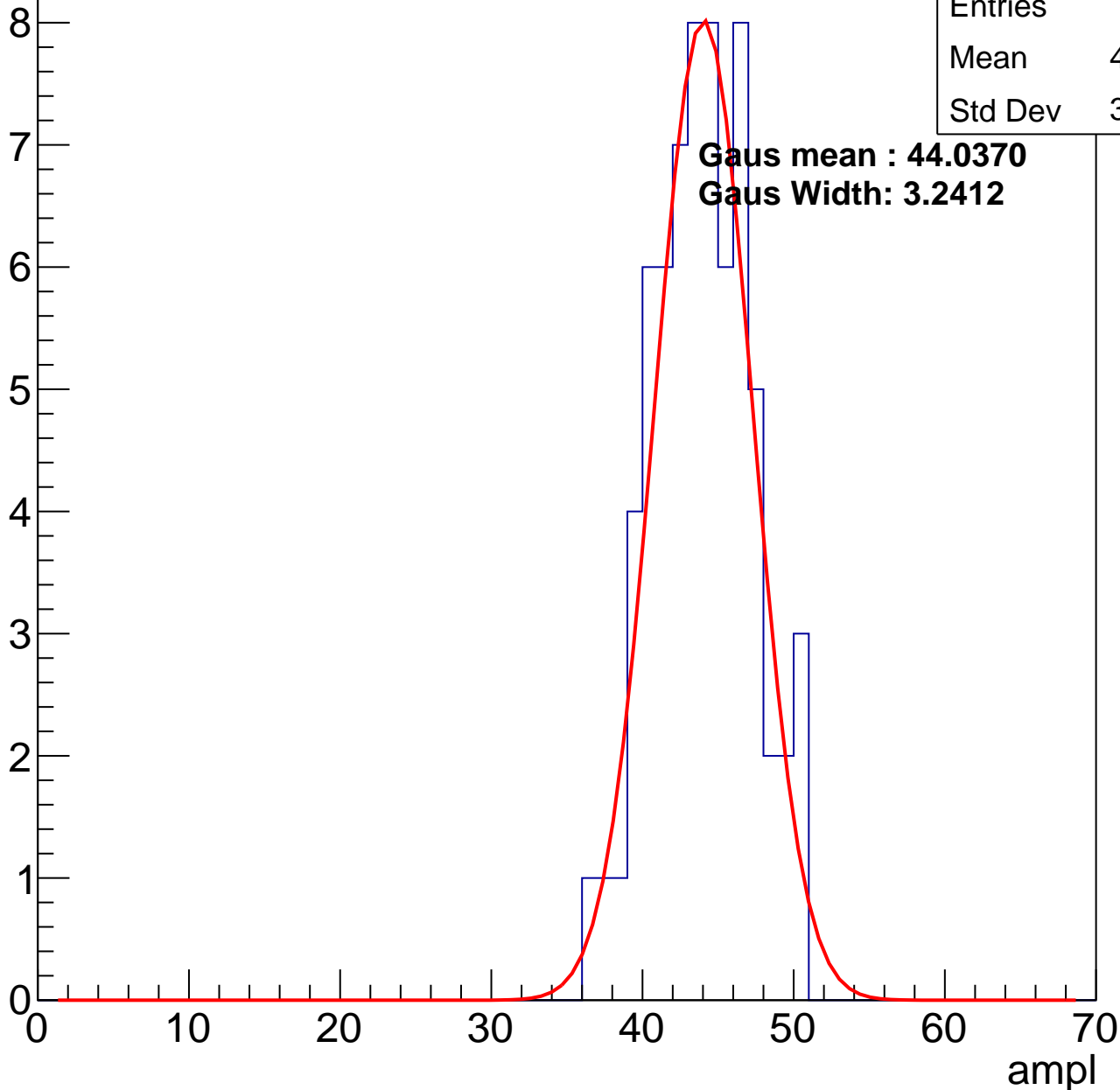
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.53
Std Dev	3.192

Gaus mean : 44.0370

Gaus Width: 3.2412

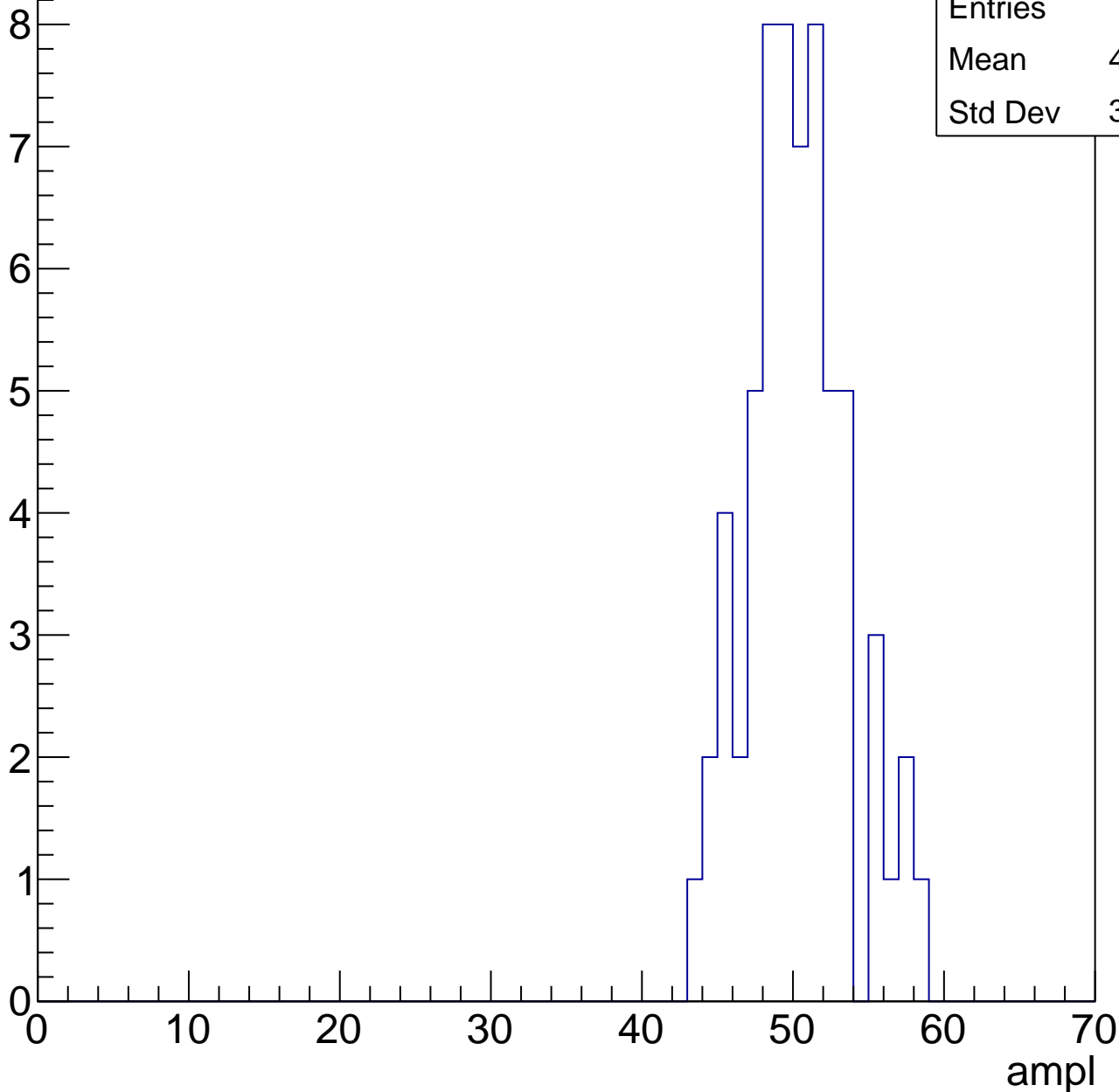


B1L102S, U9-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

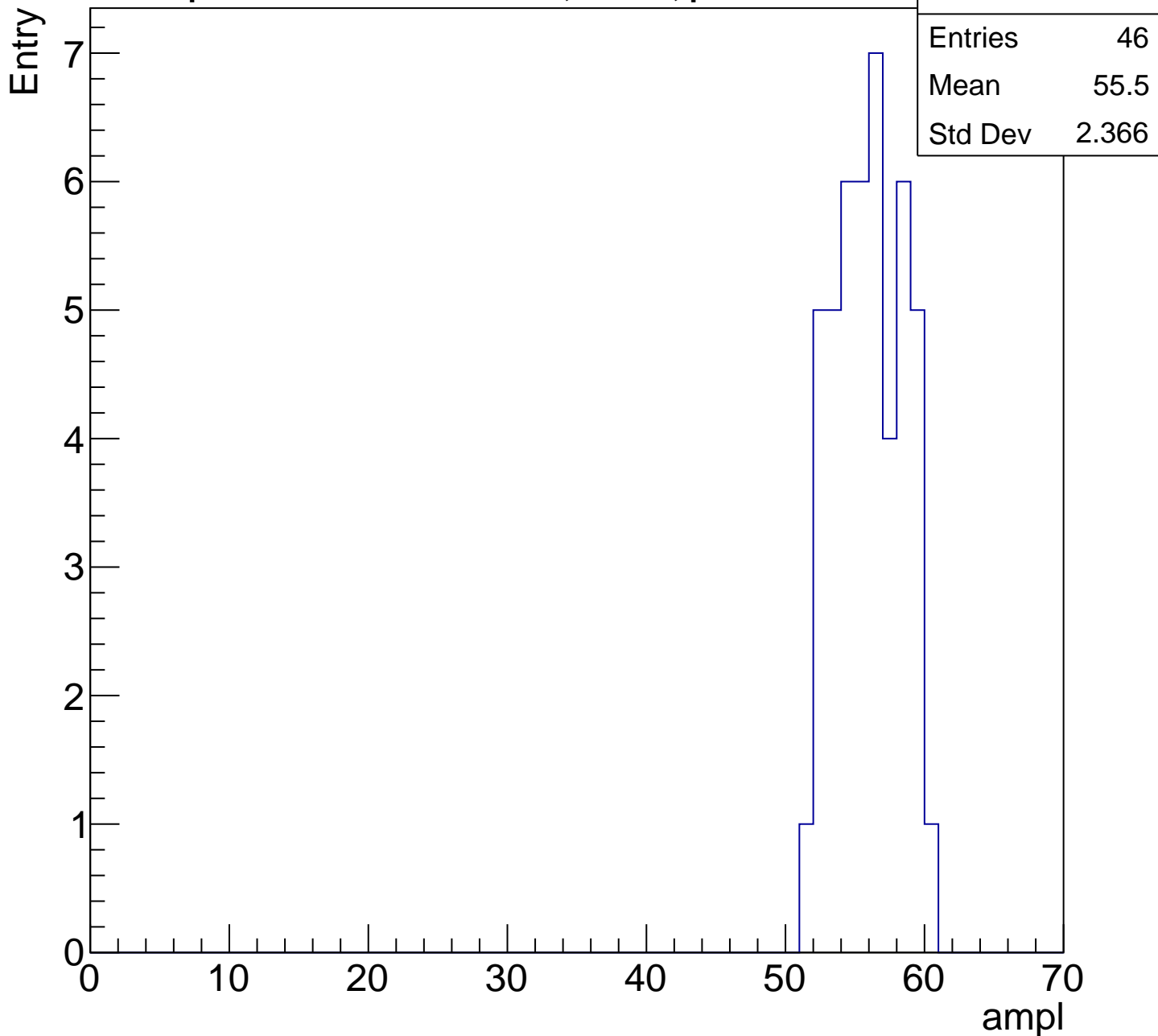
Entry

Entries	62
Mean	49.84
Std Dev	3.318



B1L102S, U9-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

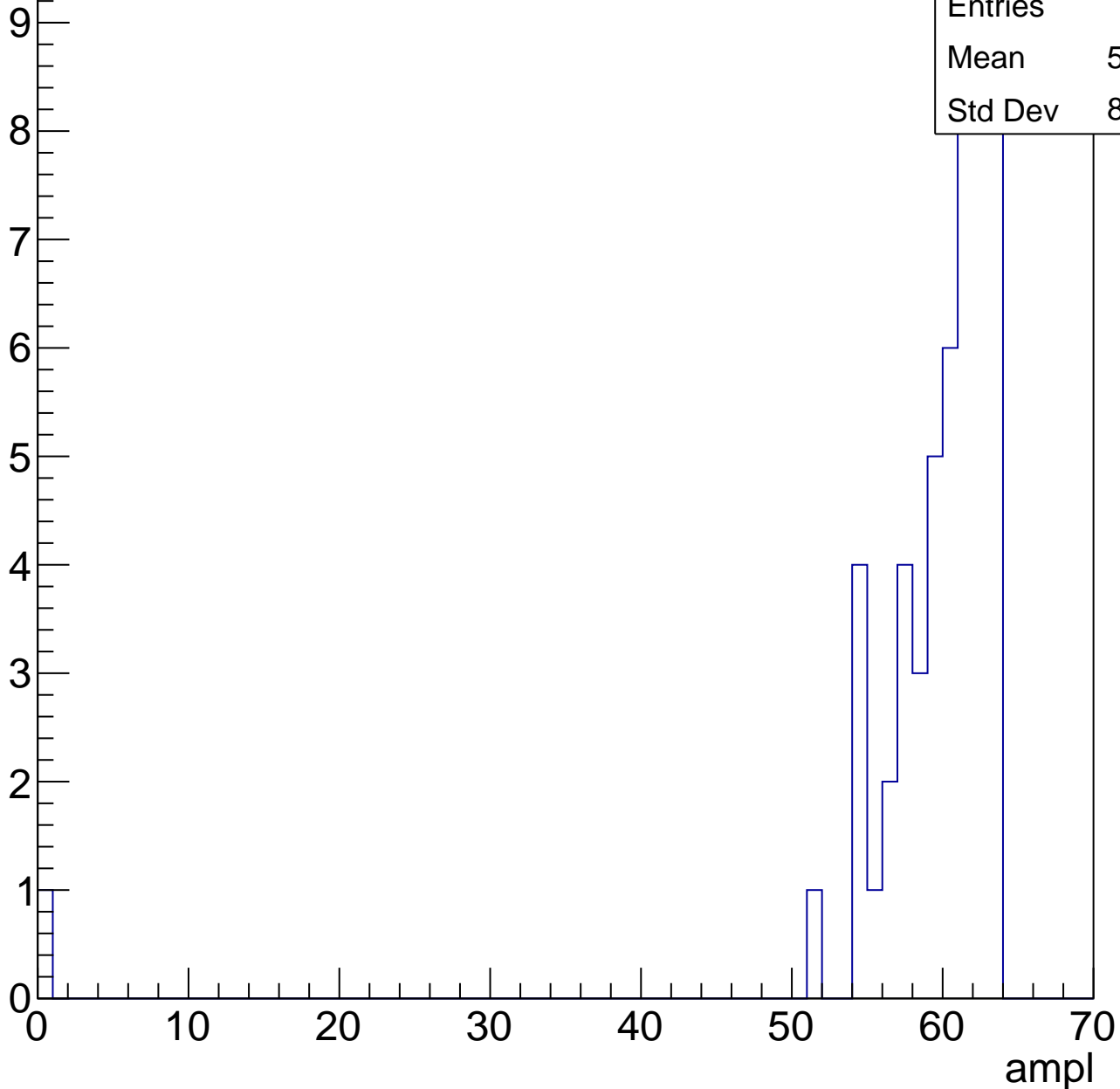


B1L102S, U9-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.48
Std Dev	8.695



B1L102S, U9-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

8

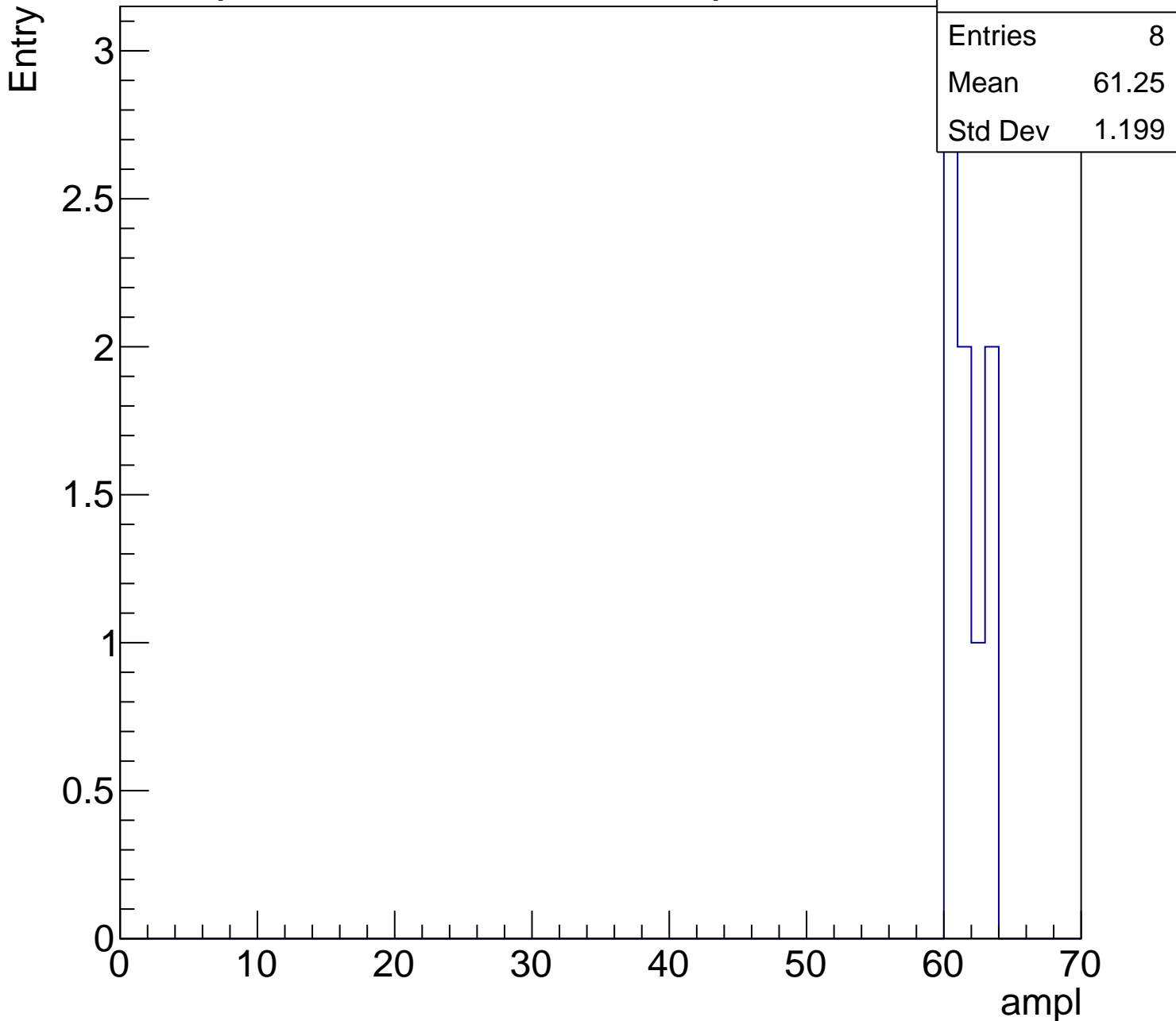
Mean

61.25

Std Dev

1.199

ampl



B1L102S, U9-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch46, adc0

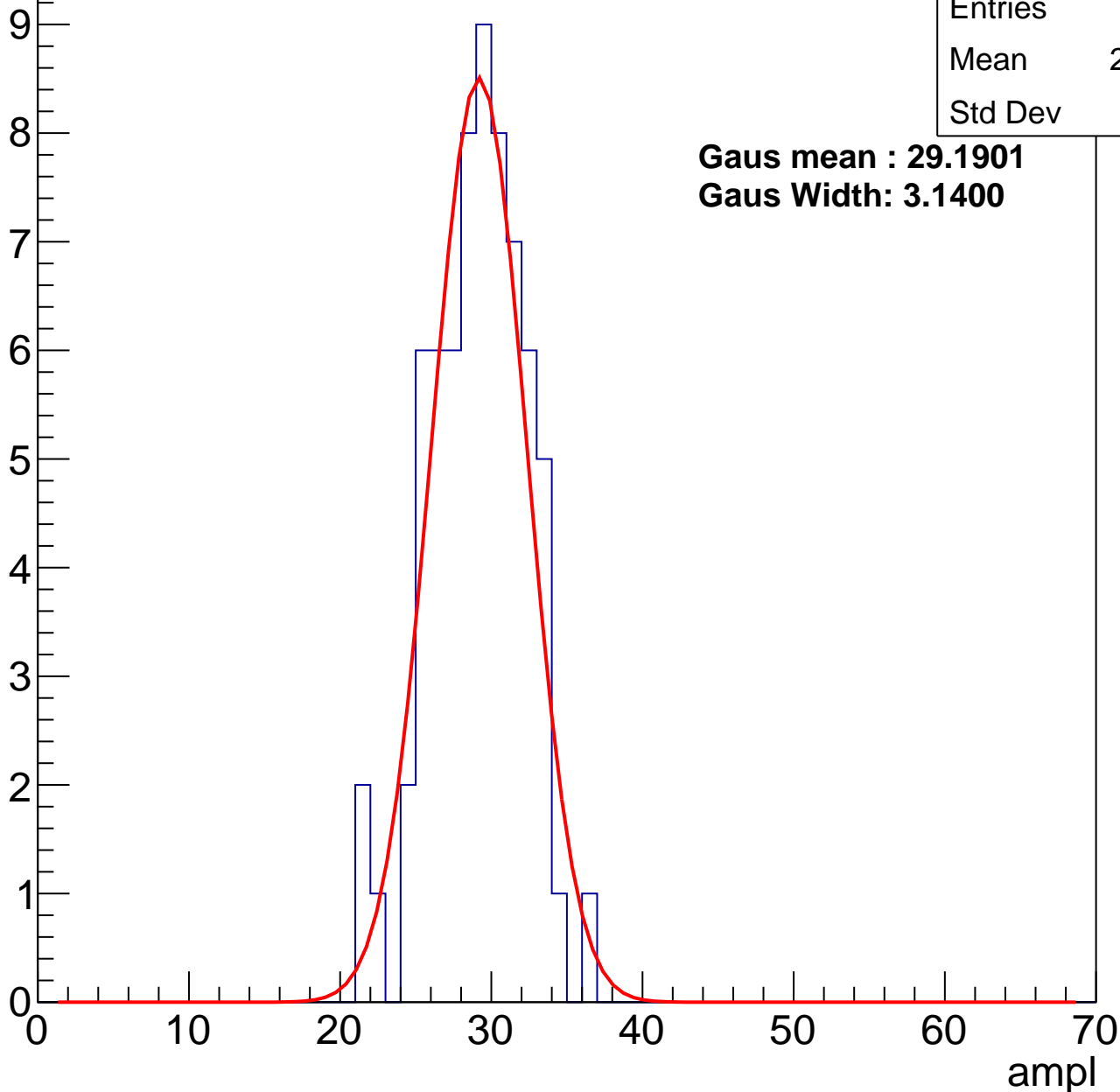
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.66
Std Dev	3.08

Gaus mean : 29.1901

Gaus Width: 3.1400



B1L102S, U9-ch46, adc1

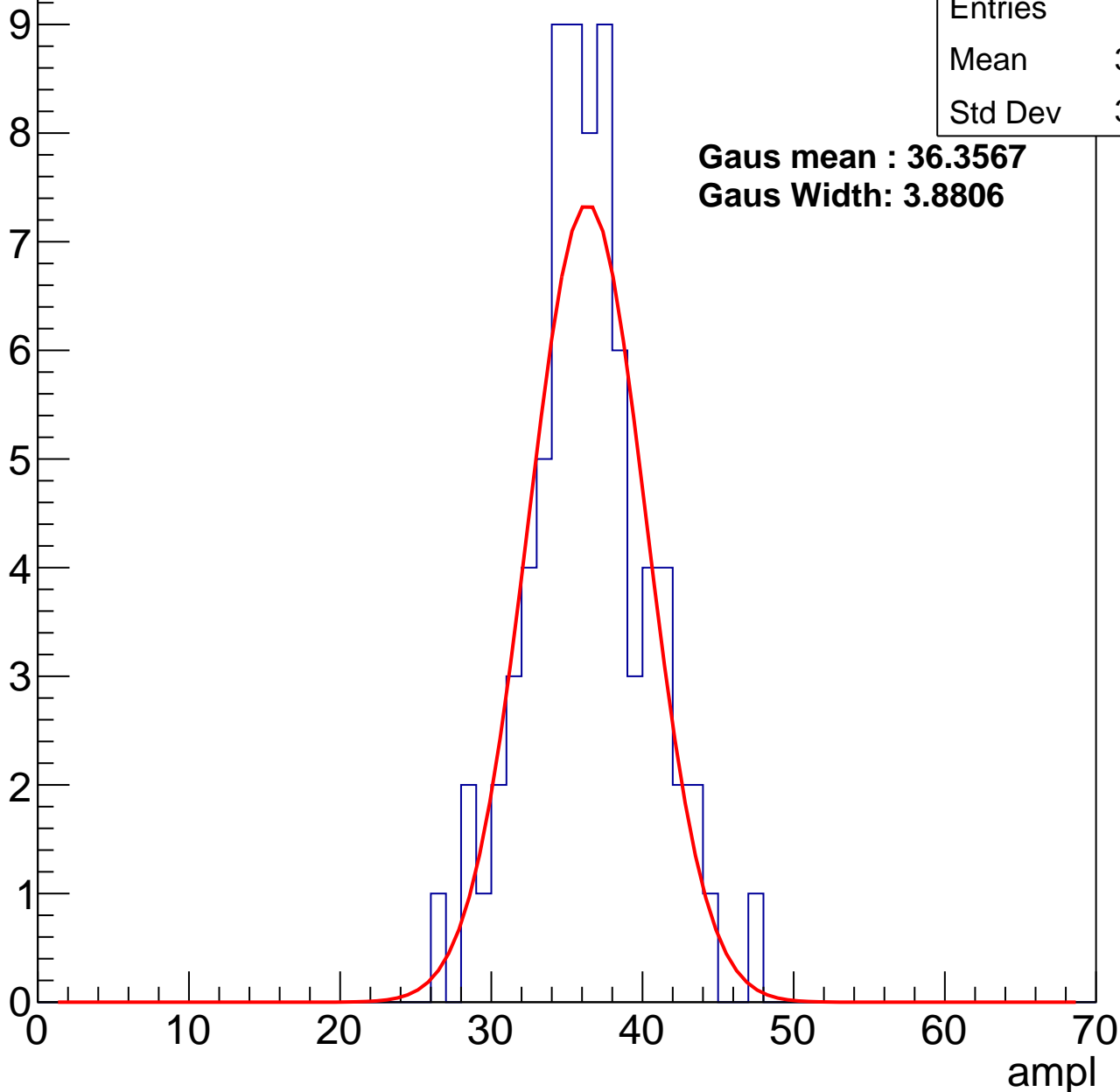
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.91
Std Dev	3.901

Gaus mean : 36.3567

Gaus Width: 3.8806



B1L102S, U9-ch46, adc2

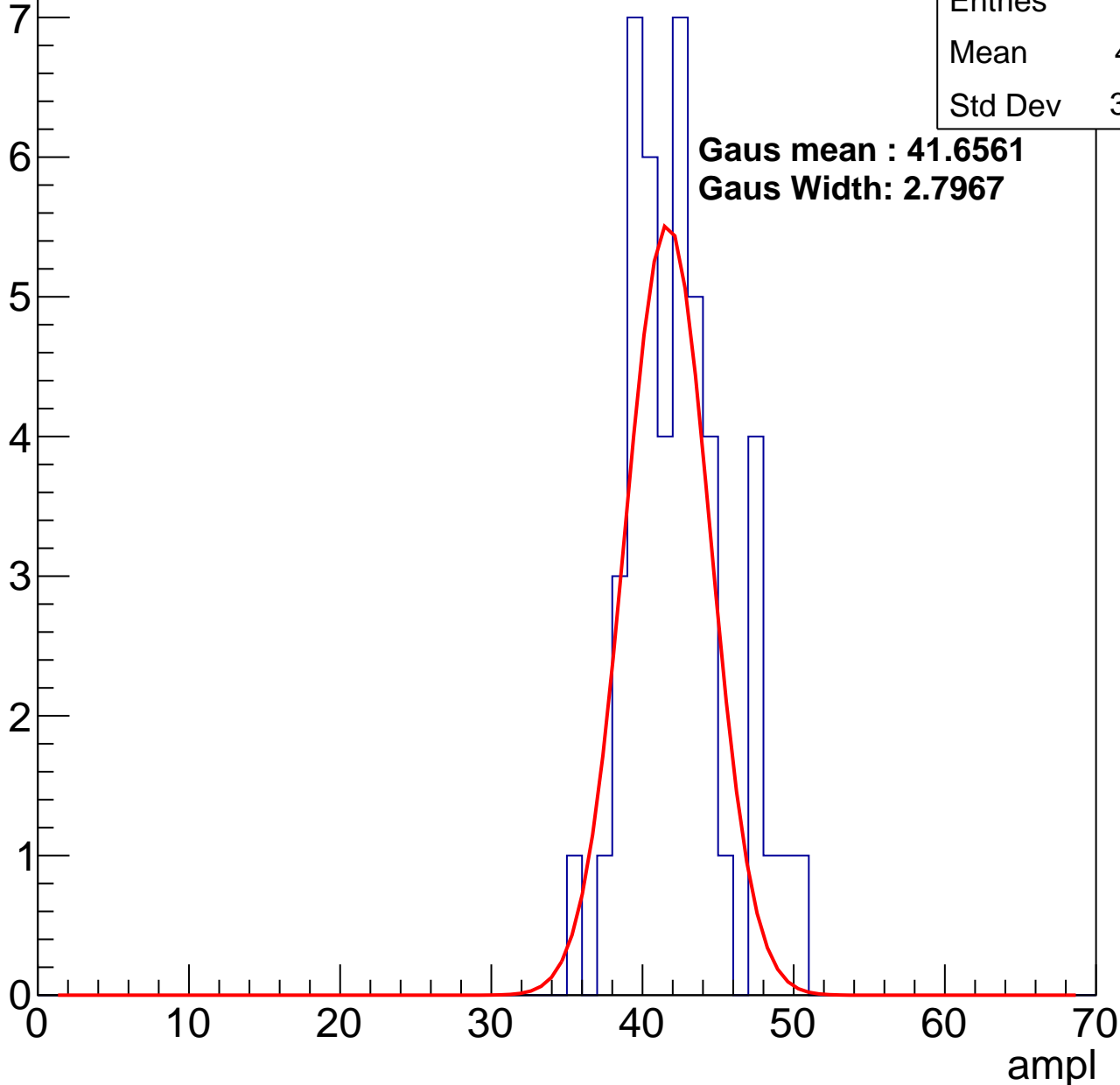
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	41.91
Std Dev	3.269

Gaus mean : 41.6561

Gaus Width: 2.7967



B1L102S, U9-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

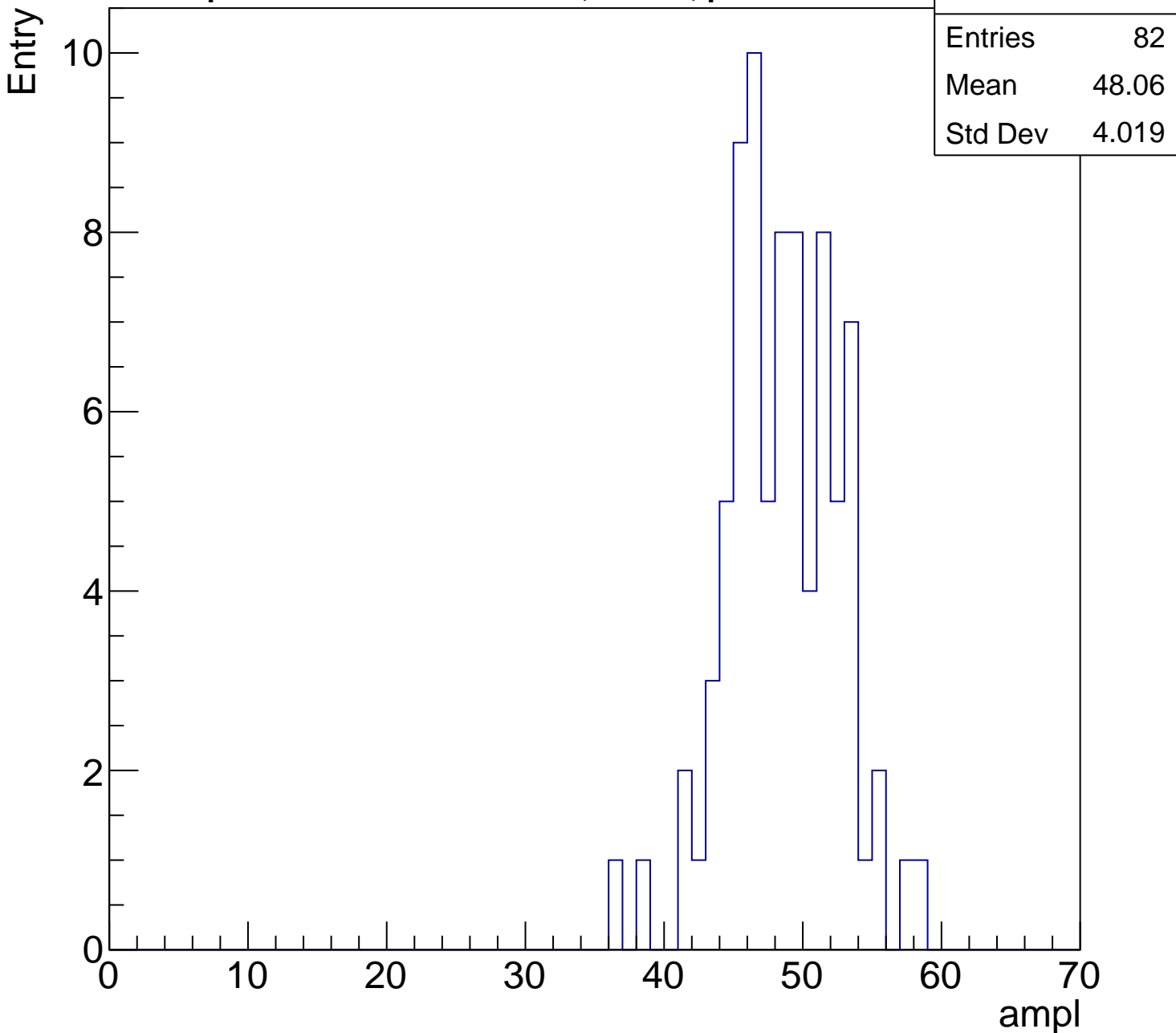
Entries	82
Mean	48.06
Std Dev	4.019

Entry

10
8
6
4
2
0

ampl

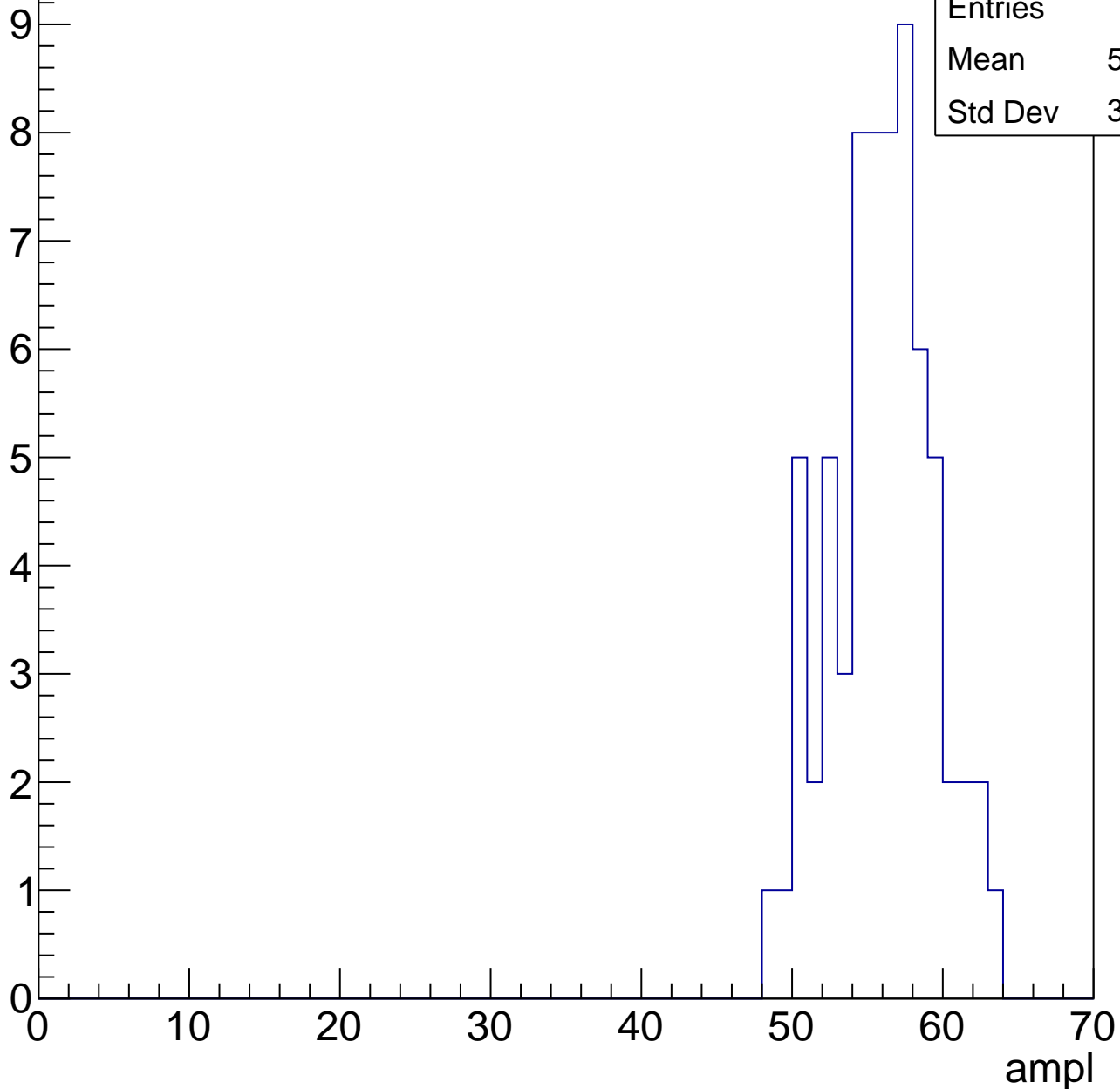
0 10 20 30 40 50 60 70



B1L102S, U9-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



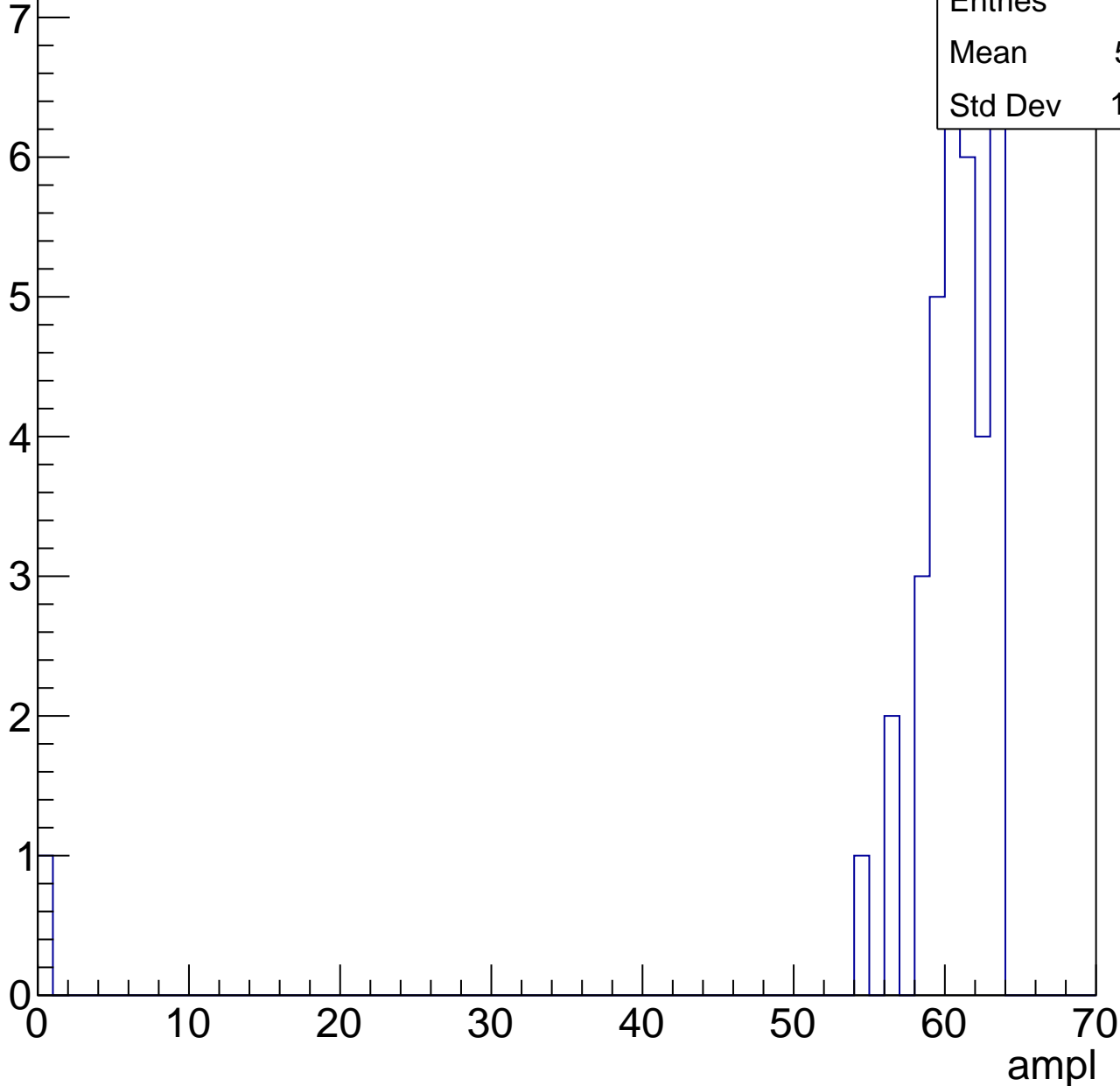
Entries	68
Mean	55.49
Std Dev	3.328

B1L102S, U9-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

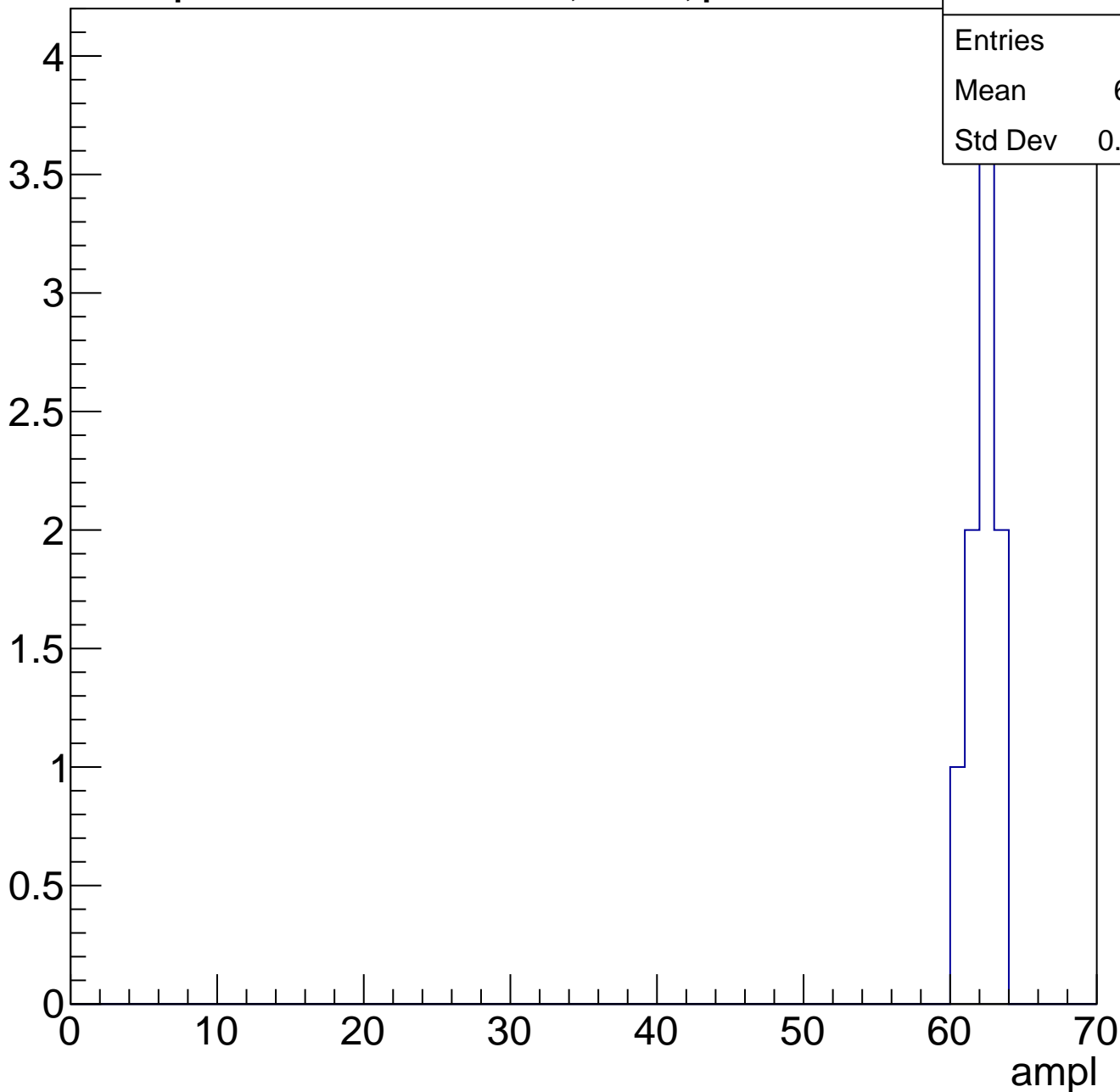
Entries	36
Mean	58.61
Std Dev	10.14



B1L102S, U9-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

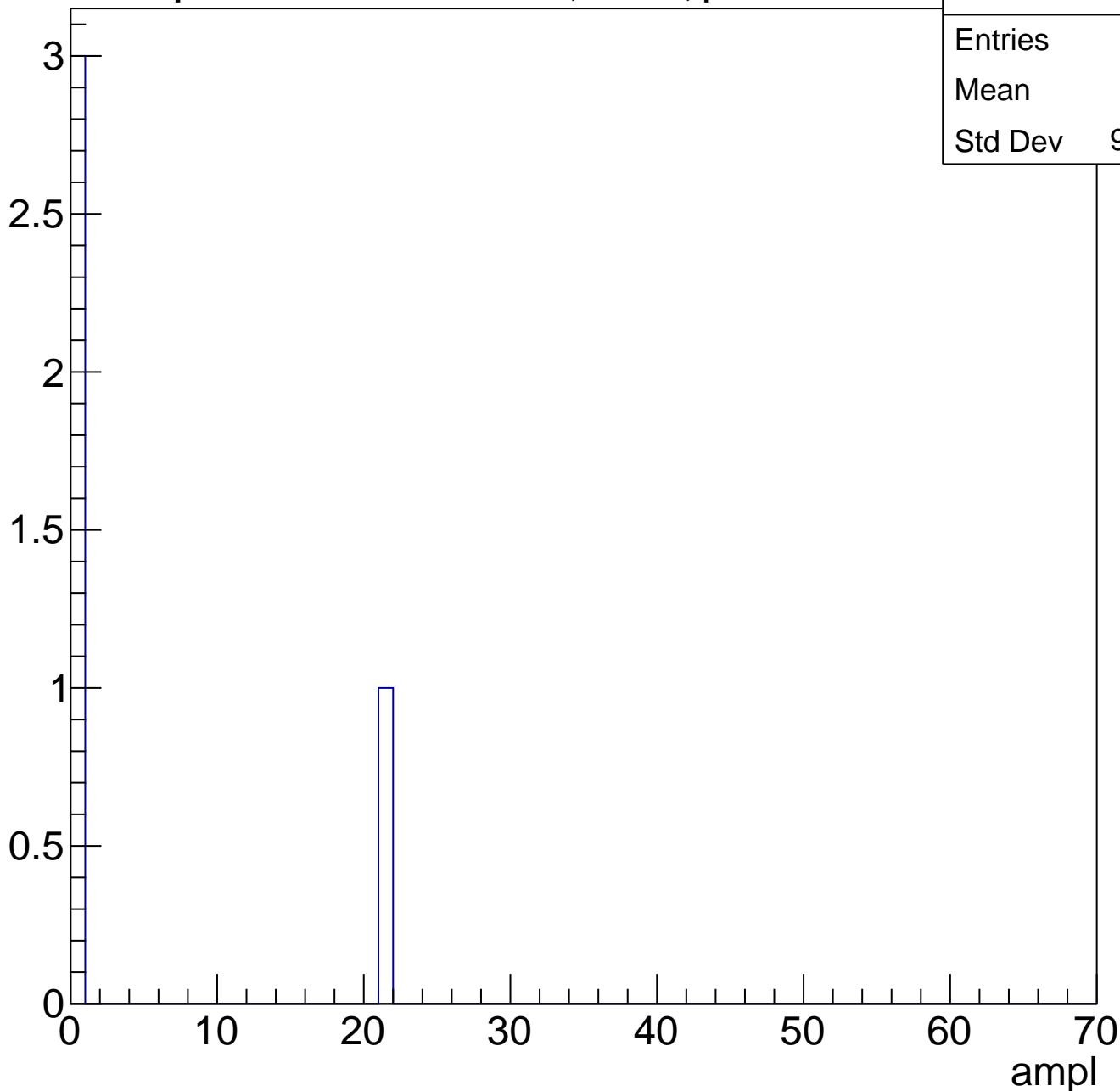
Entry



B1L102S, U9-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	5.25
Std Dev	9.093

B1L102S, U9-ch47, adc0

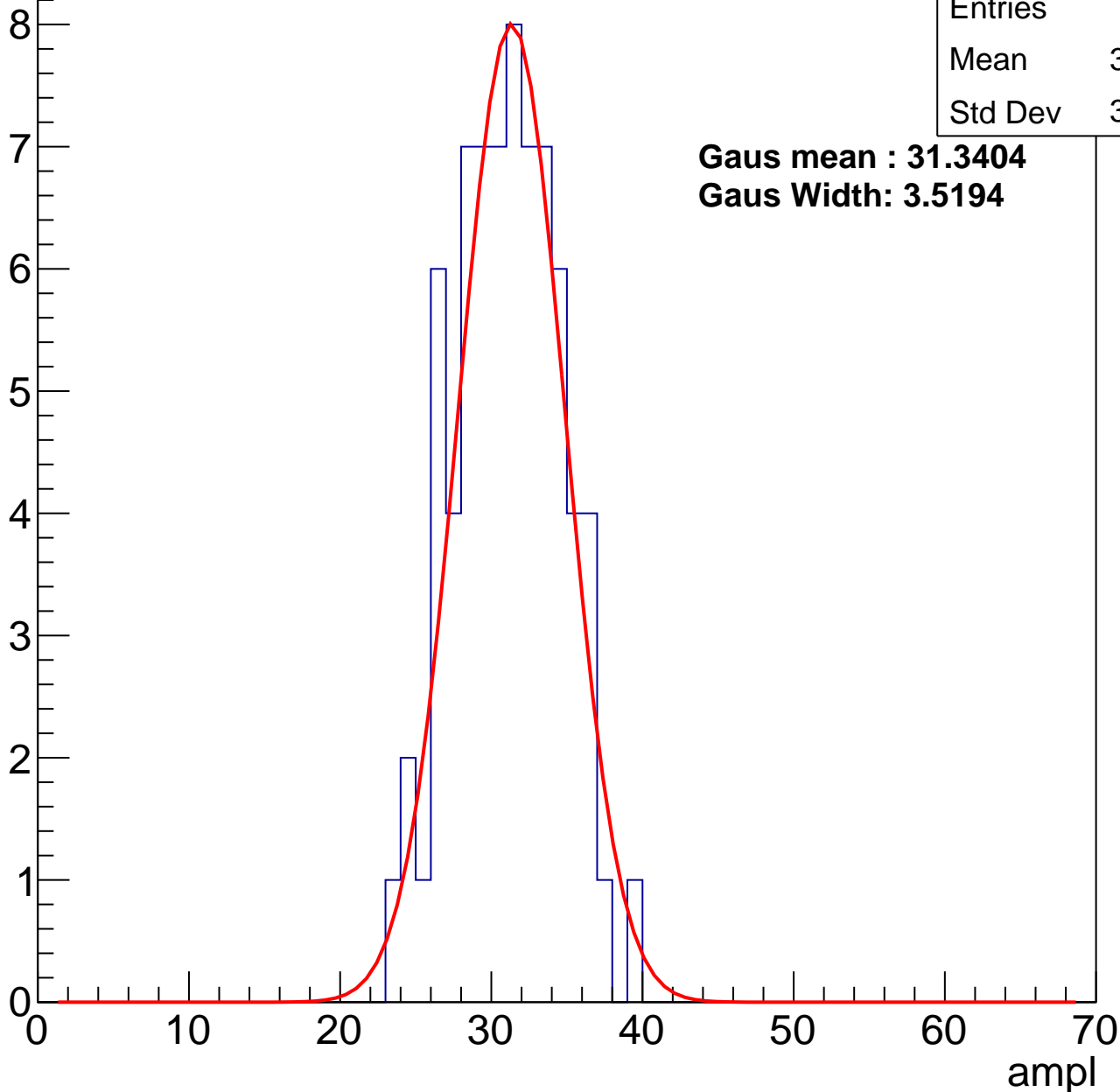
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	30.63
Std Dev	3.418

Gaus mean : 31.3404

Gaus Width: 3.5194



B1L102S, U9-ch47, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	38.48
Std Dev	3.85

Gaus mean : 39.0641

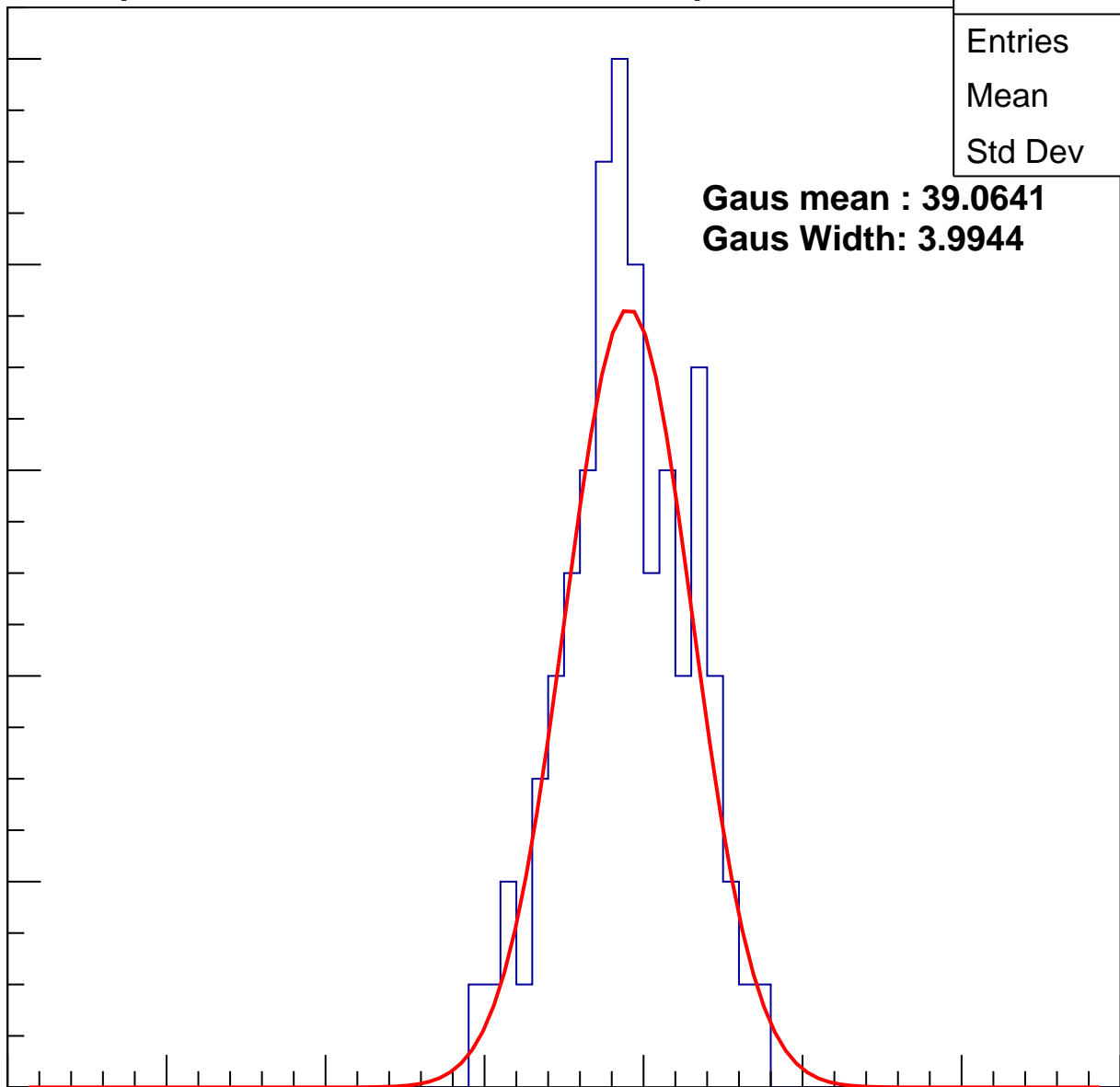
Gaus Width: 3.9944

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch47, adc2

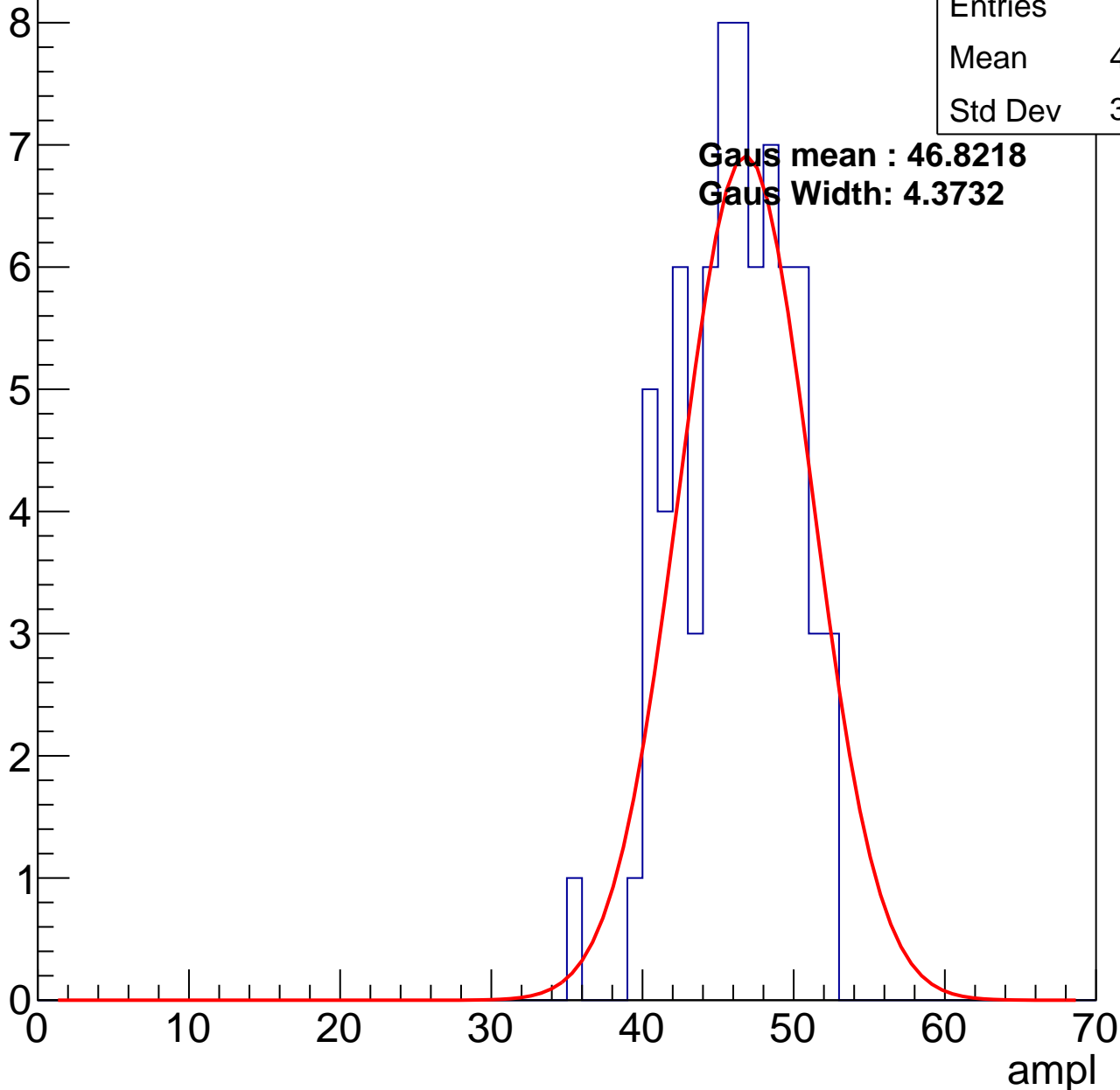
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	45.64
Std Dev	3.632

Gaus mean : 46.8218

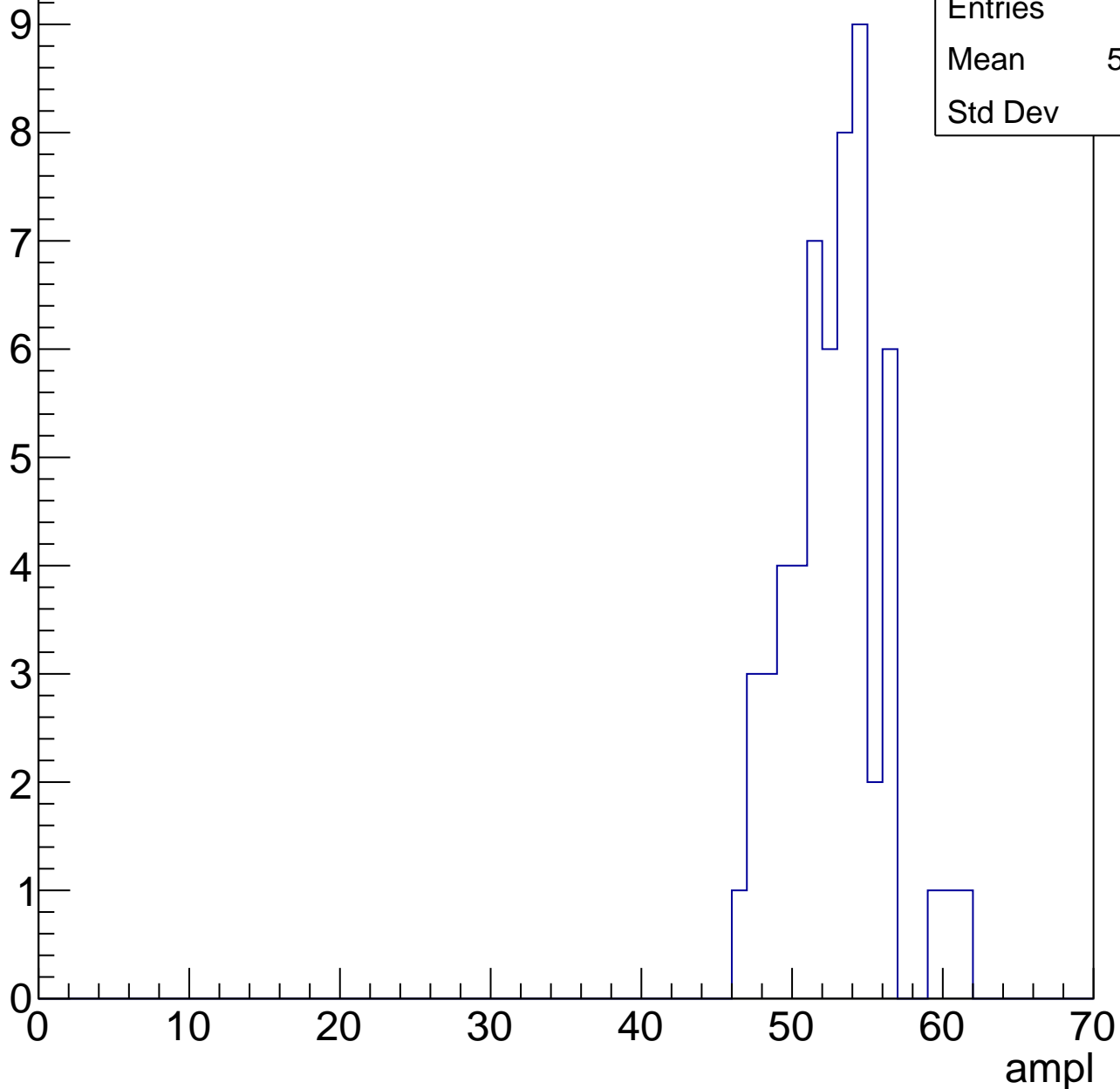
Gaus Width: 4.3732



B1L102S, U9-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

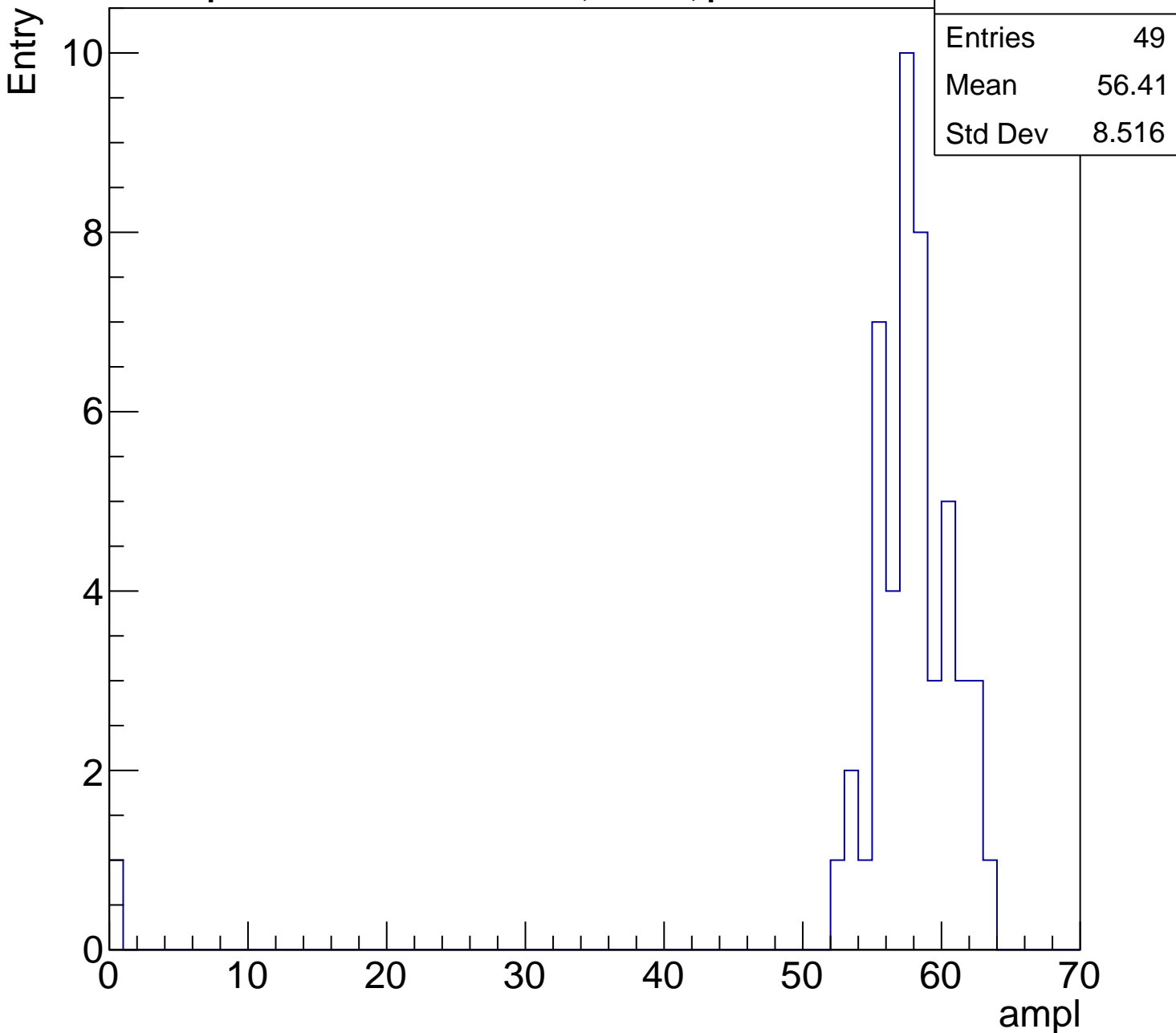
Entries	49
Mean	56.41
Std Dev	8.516

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

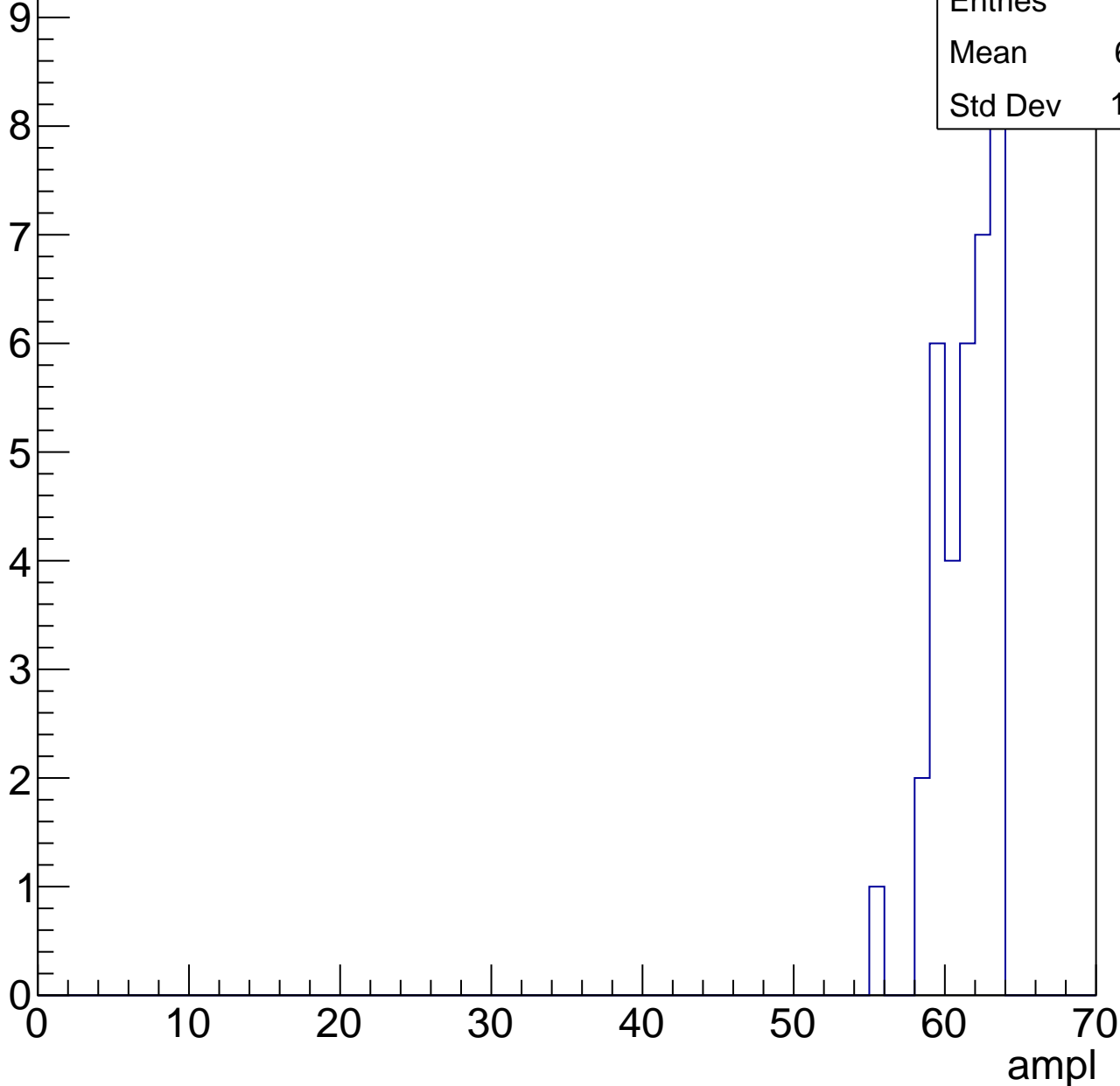


B1L102S, U9-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	60.91
Std Dev	1.888



B1L102S, U9-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U9-ch48, adc0

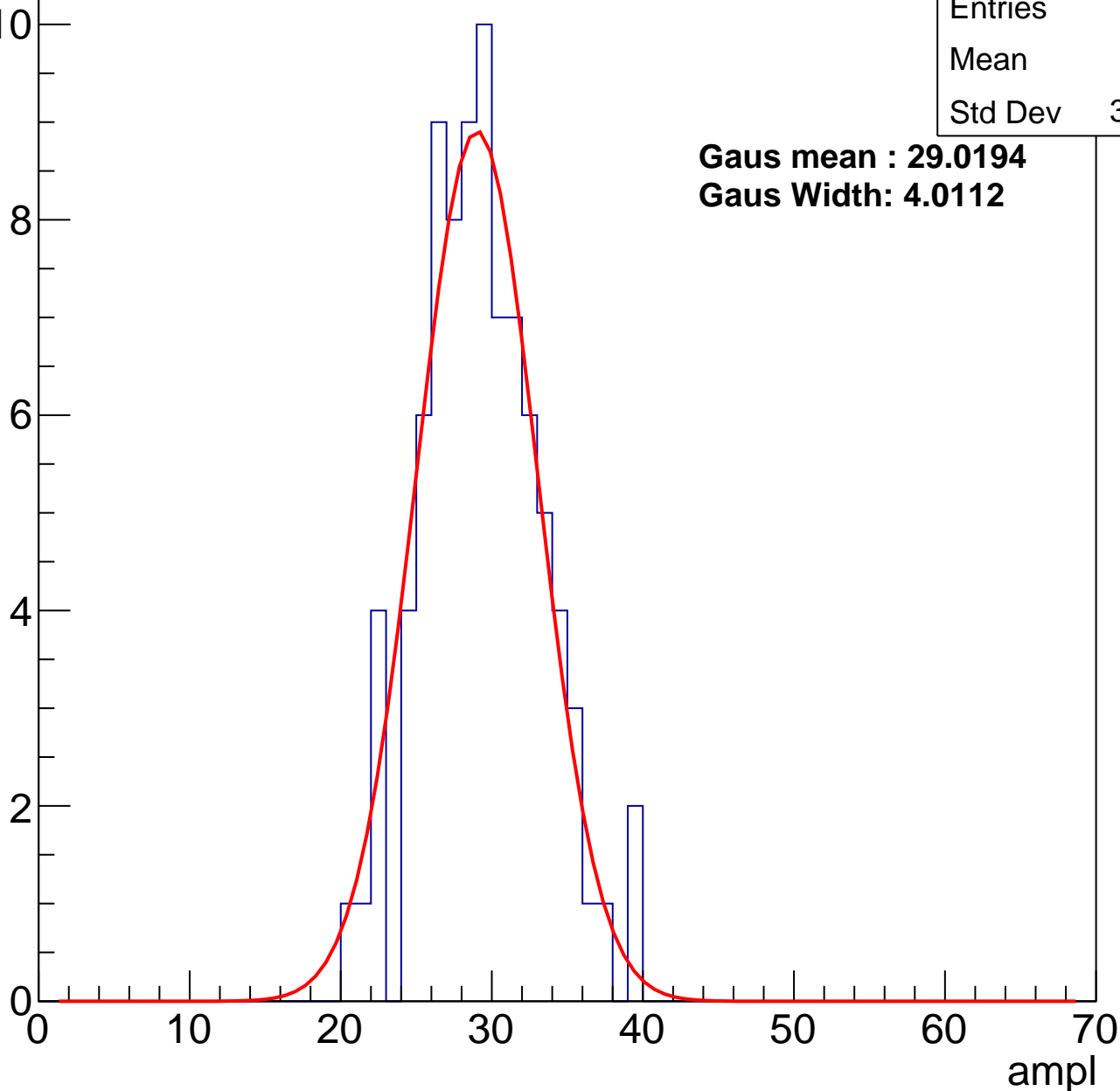
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	28.9
Std Dev	3.914

Gaus mean : 29.0194

Gaus Width: 4.0112



B1L102S, U9-ch48, adc1

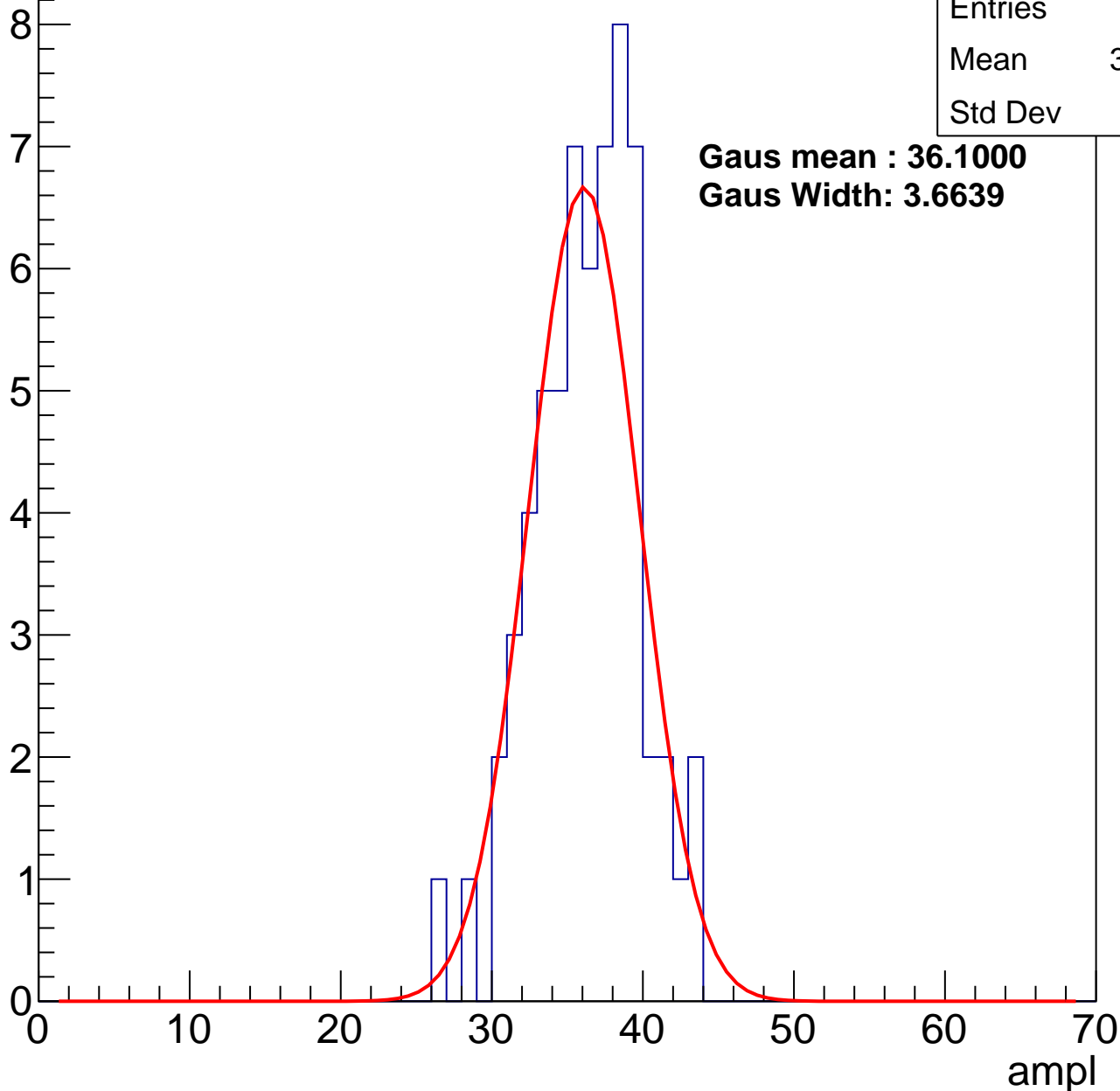
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.83
Std Dev	3.48

Gaus mean : 36.1000

Gaus Width: 3.6639



B1L102S, U9-ch48, adc2

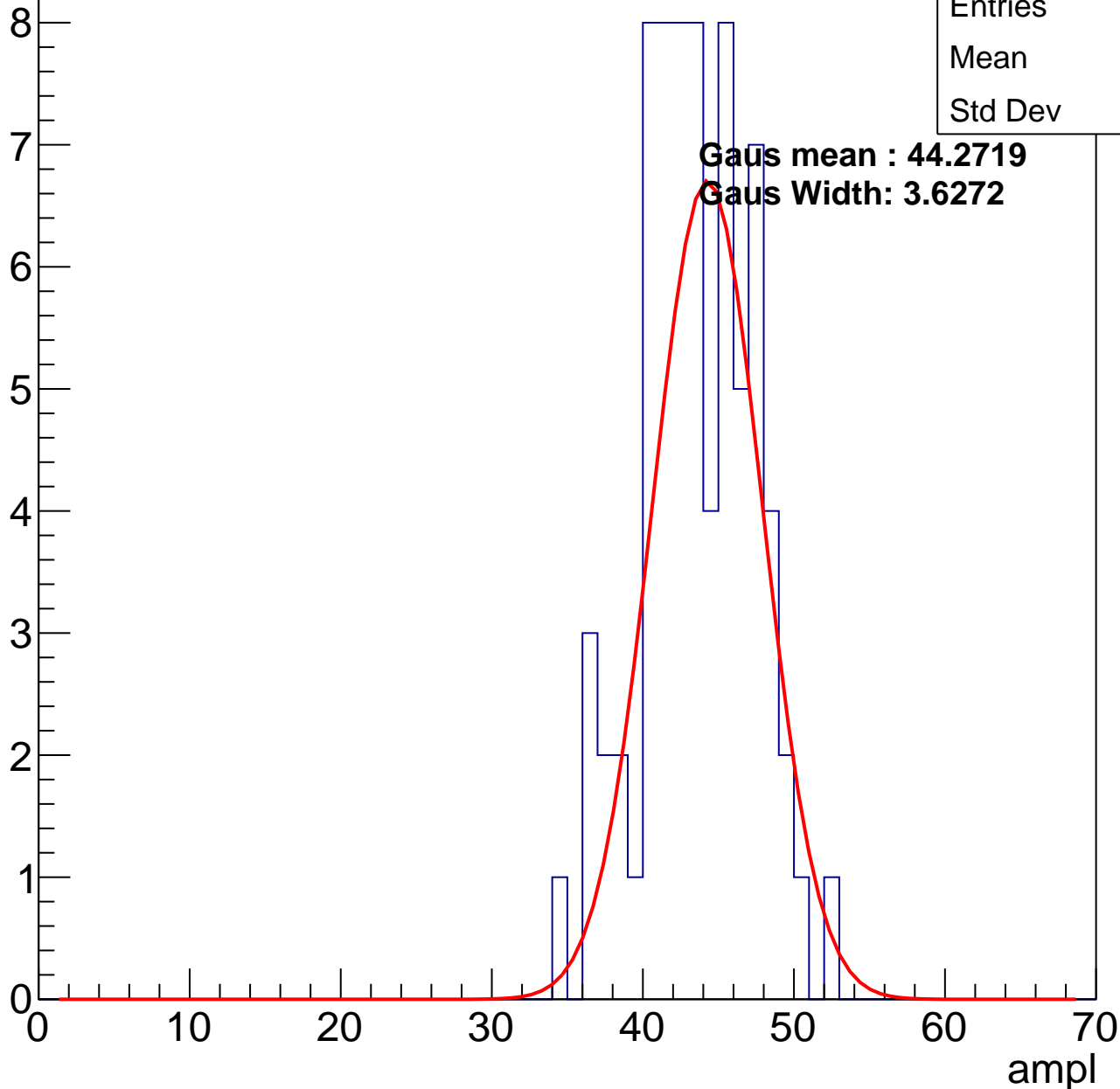
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.1
Std Dev	3.65

Gaus mean : 44.2719

Gaus Width: 3.6272

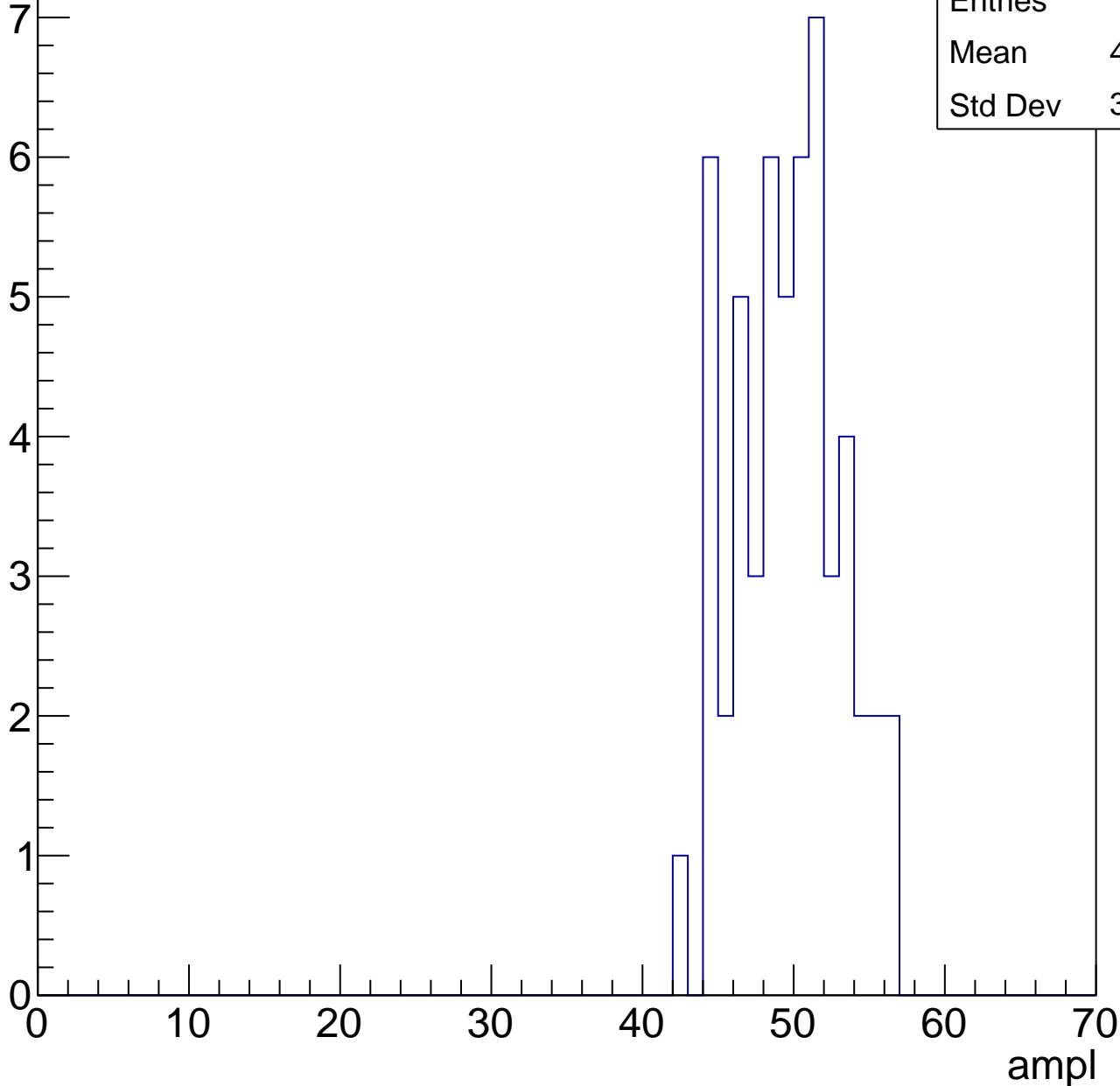


B1L102S, U9-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

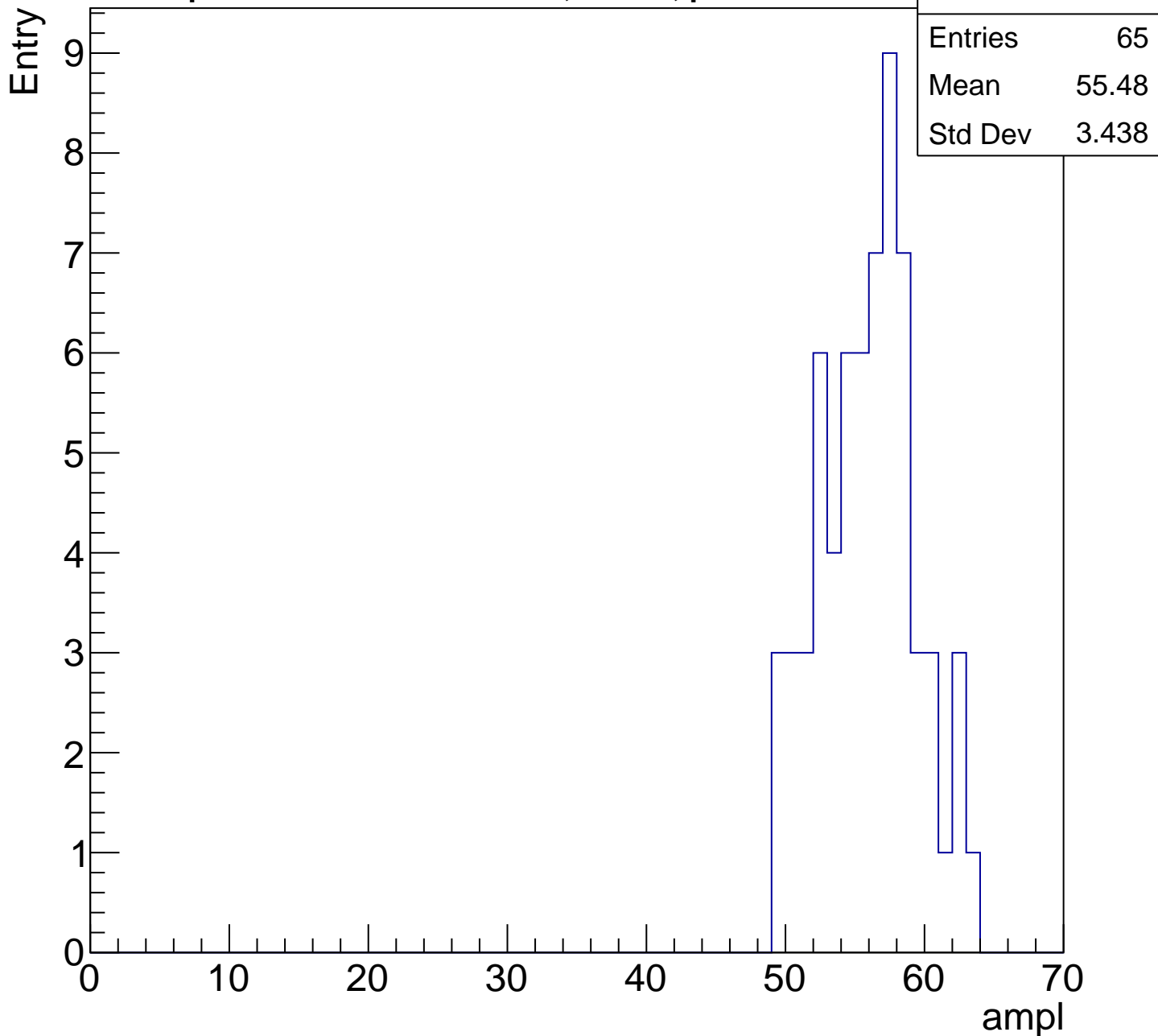
Entry

Entries	54
Mean	49.17
Std Dev	3.436



B1L102S, U9-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U9-ch48, adc5

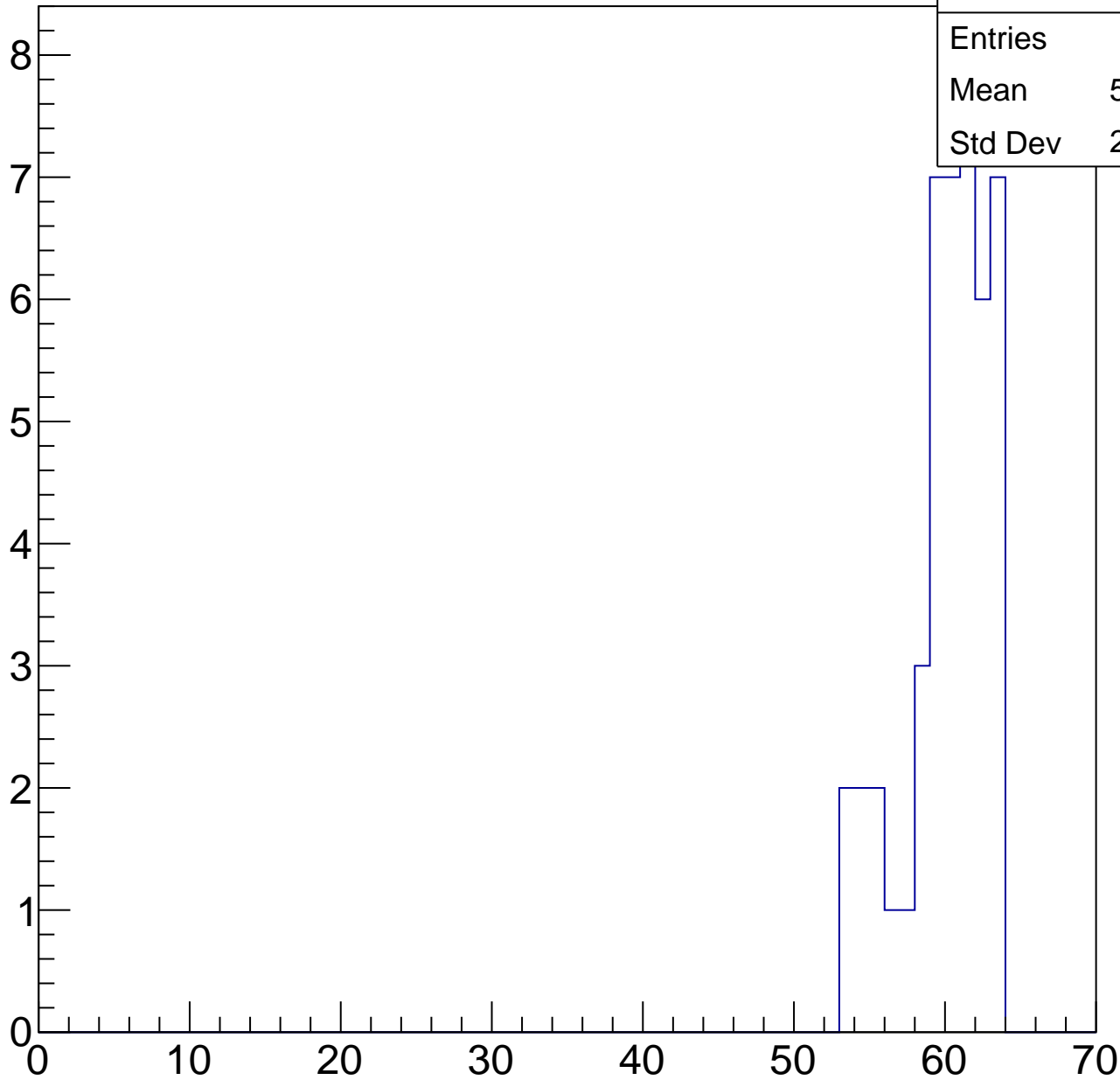
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.67
Std Dev	2.775

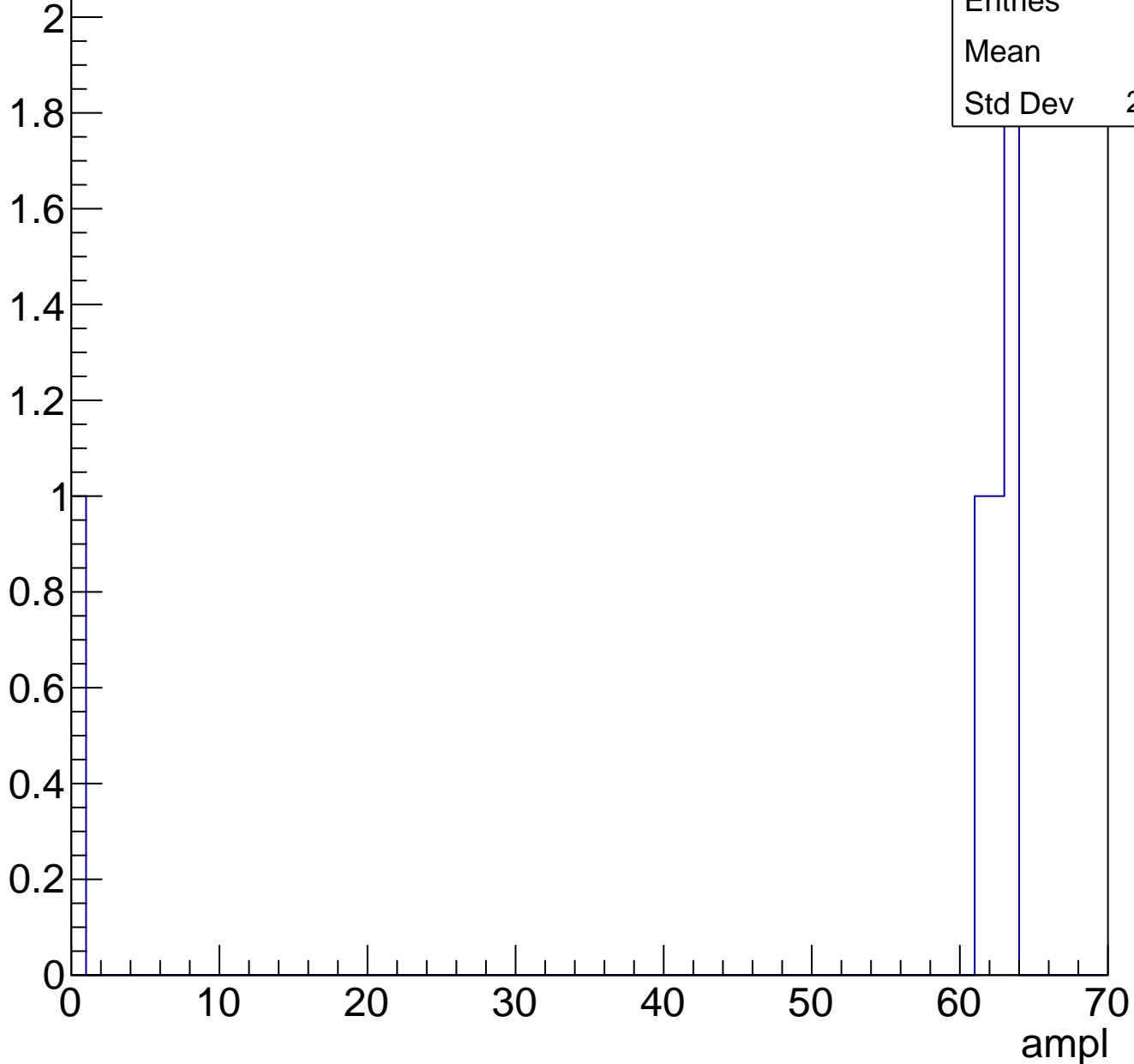
ampl



B1L102S, U9-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch49, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	27.5
Std Dev	3.394

Gaus mean : 28.2479

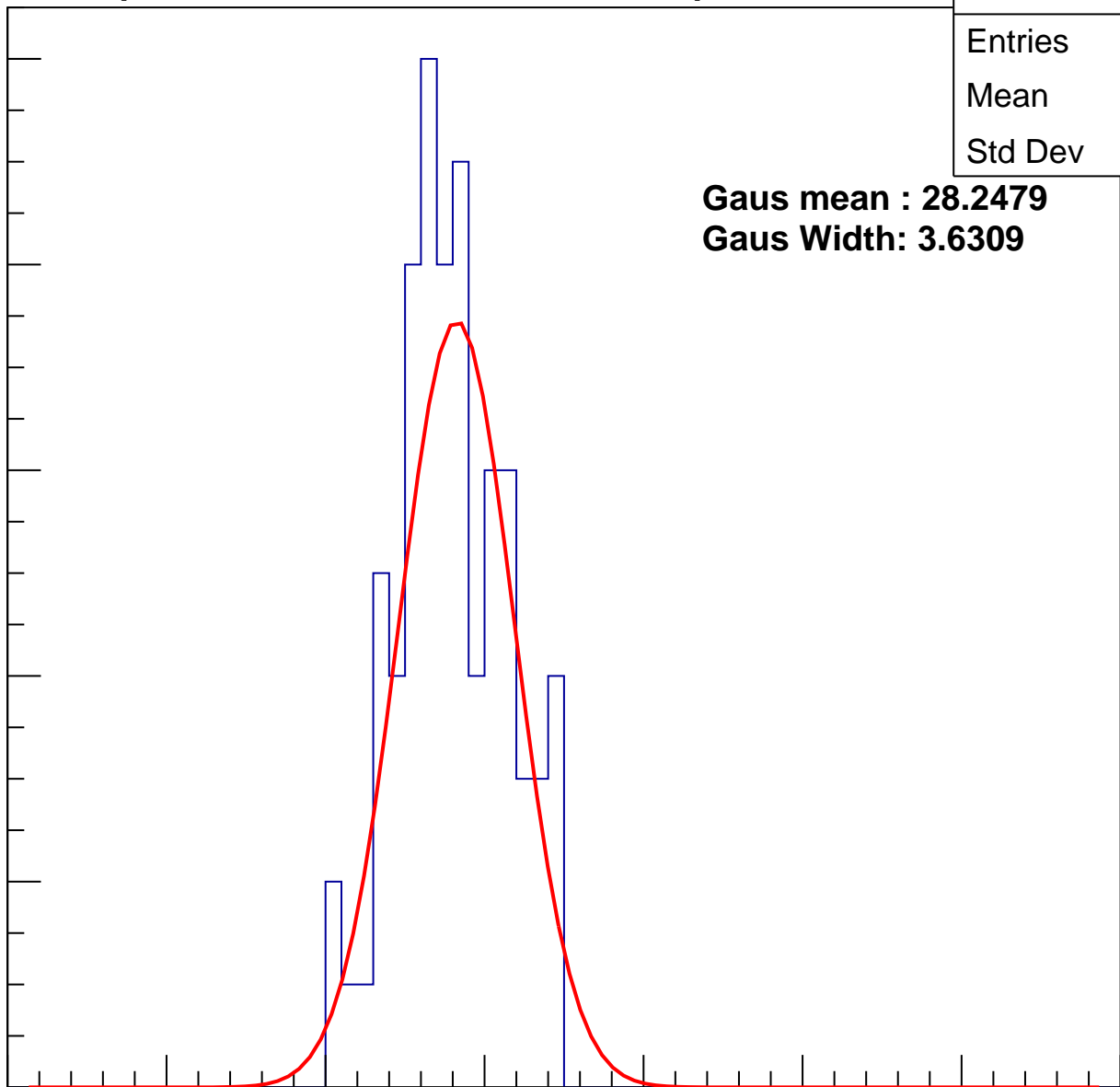
Gaus Width: 3.6309

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch49, adc1

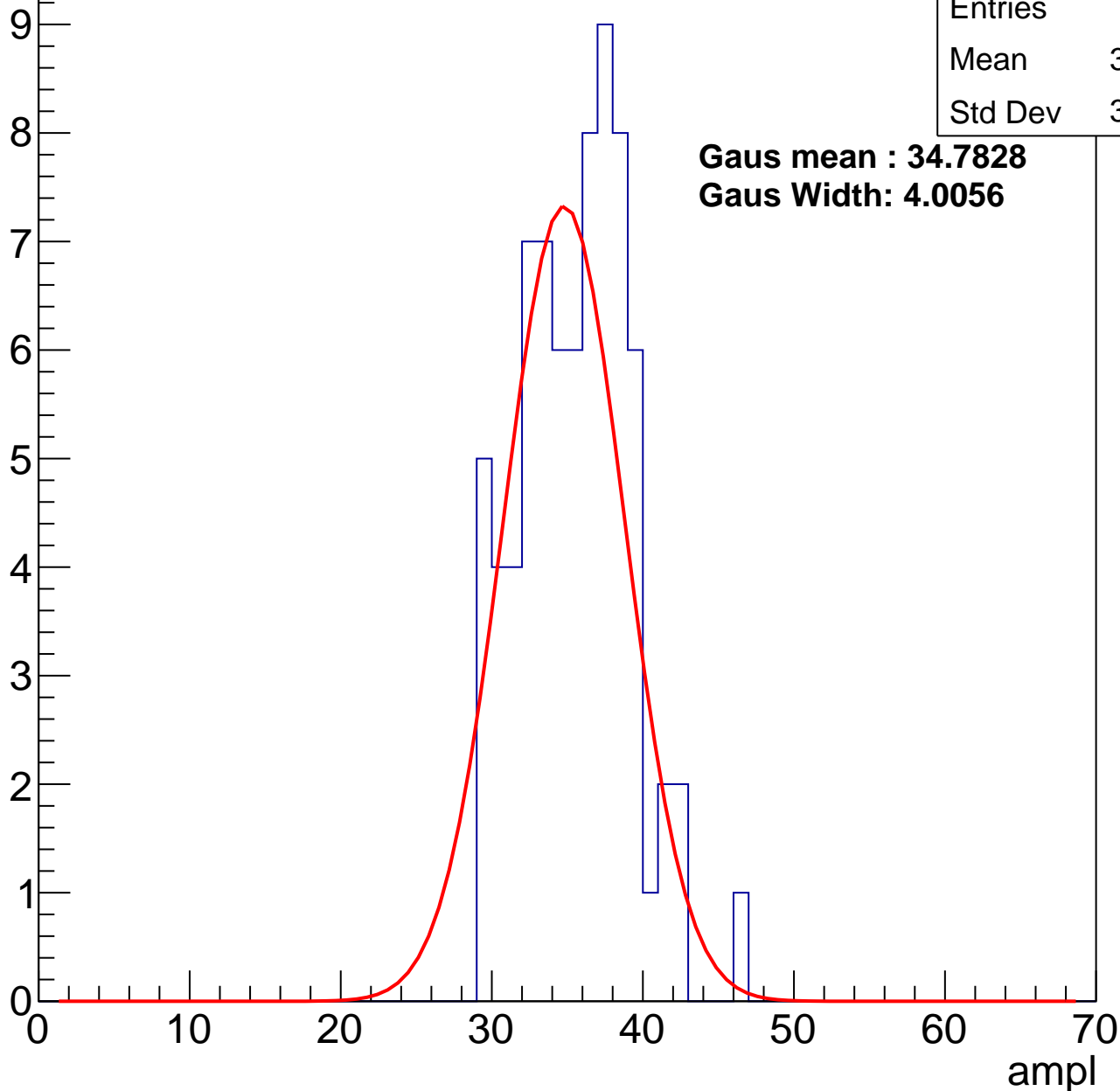
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.12
Std Dev	3.569

Gaus mean : 34.7828

Gaus Width: 4.0056



B1L102S, U9-ch49, adc2

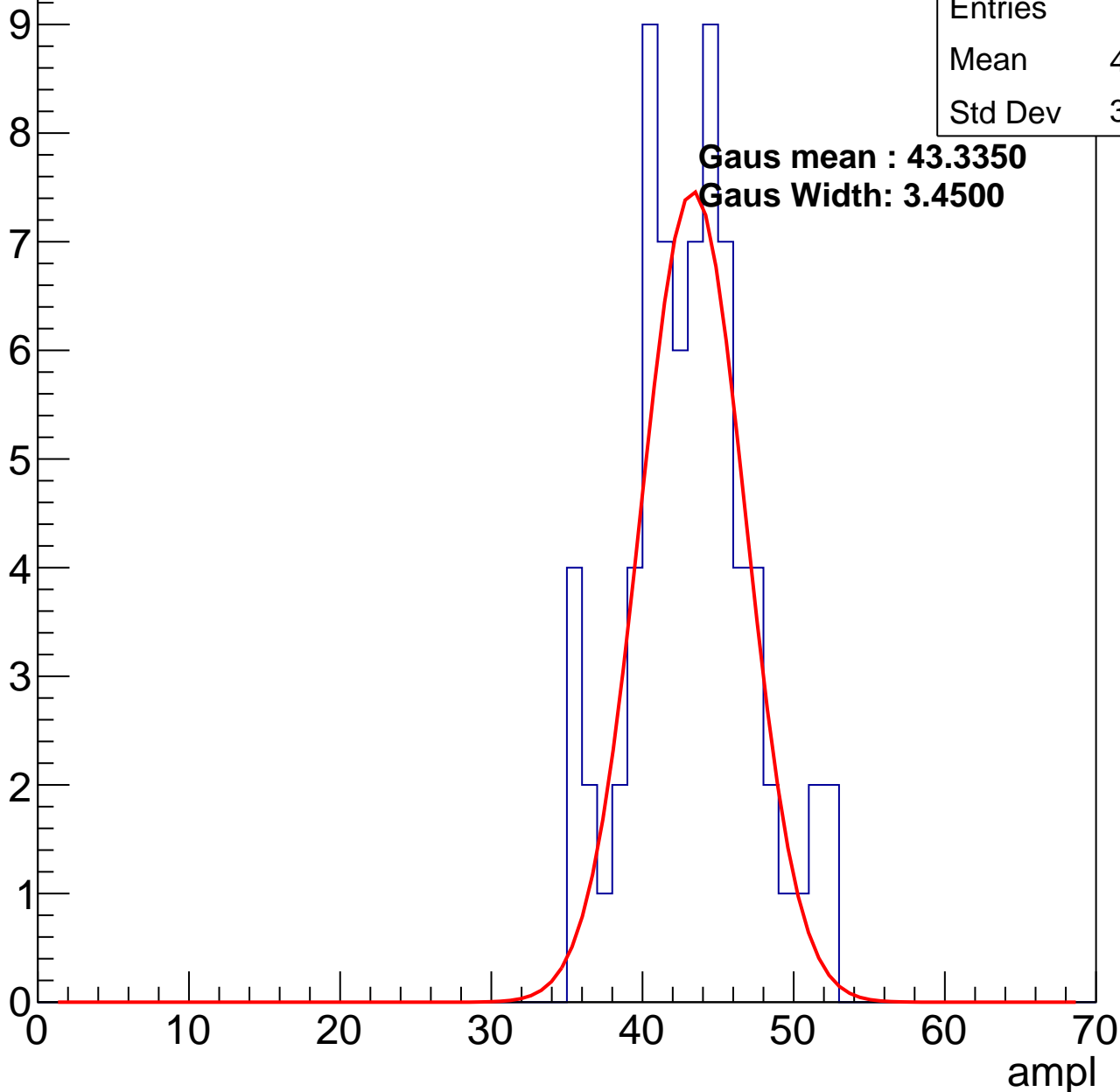
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.77
Std Dev	3.988

Gaus mean : 43.3350

Gaus Width: 3.4500

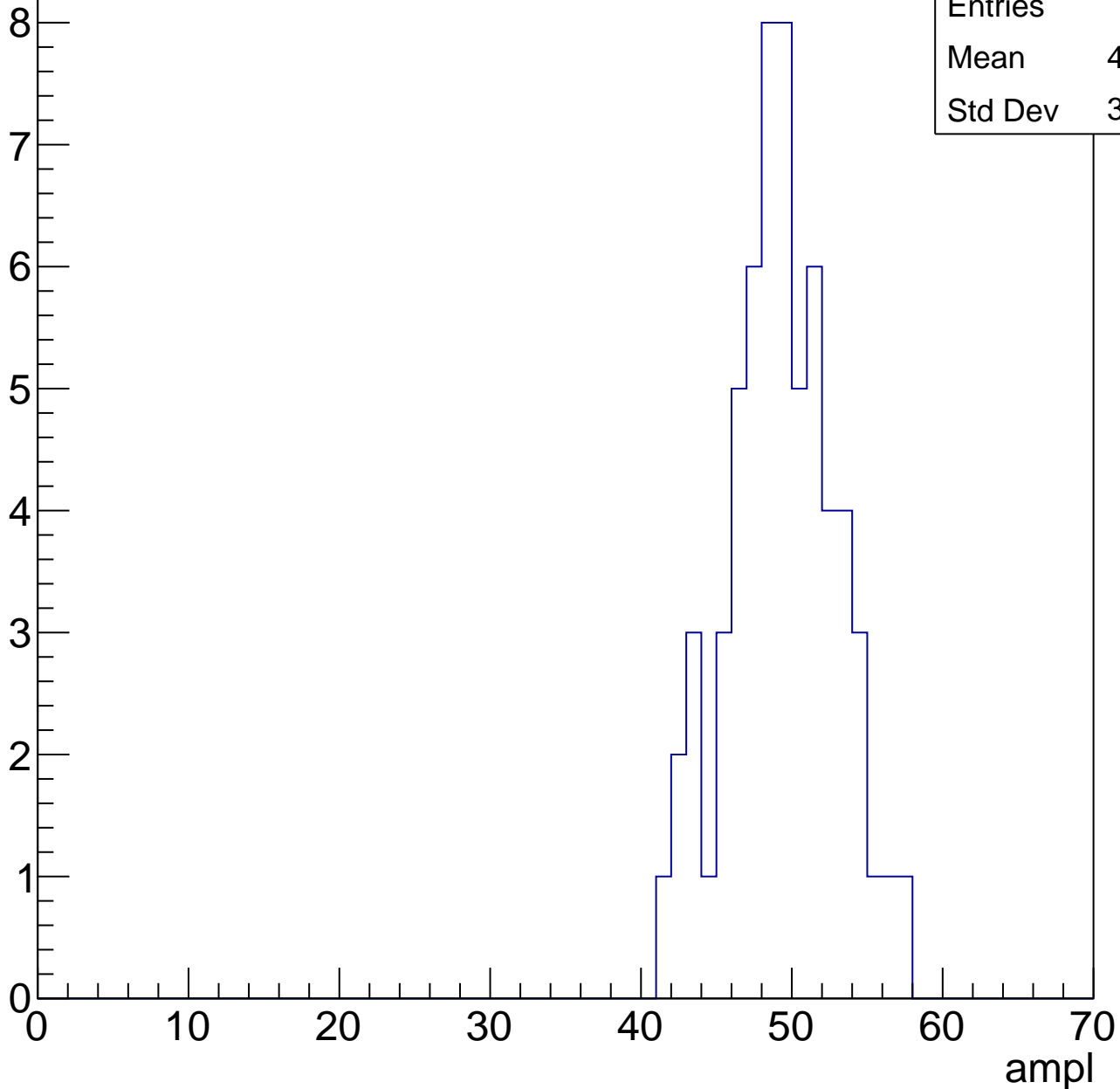


B1L102S, U9-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.82
Std Dev	3.527

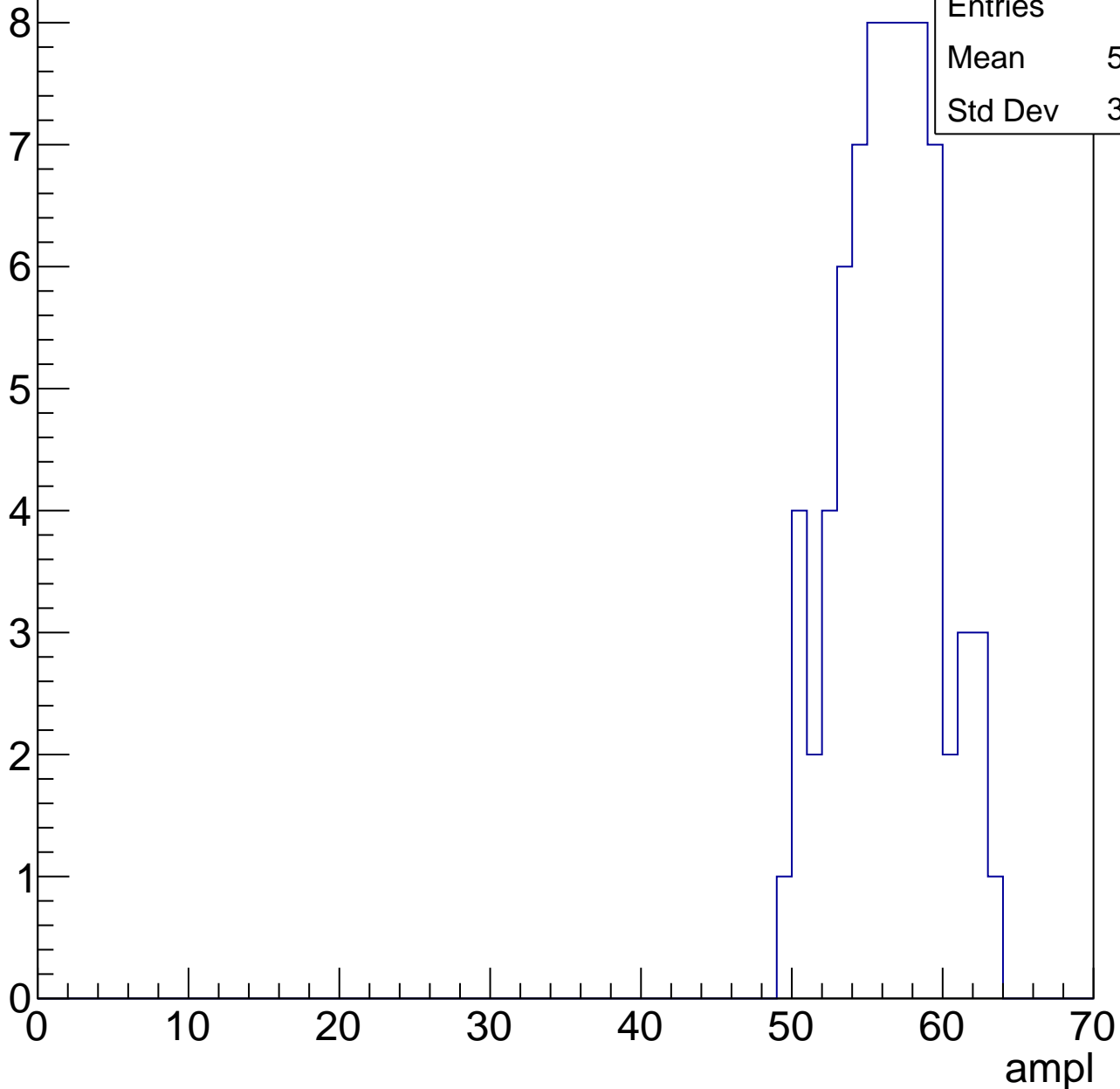


B1L102S, U9-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	55.94
Std Dev	3.257



B1L102S, U9-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	35
Mean	60.66
Std Dev	2.028

10

20

30

40

50

60

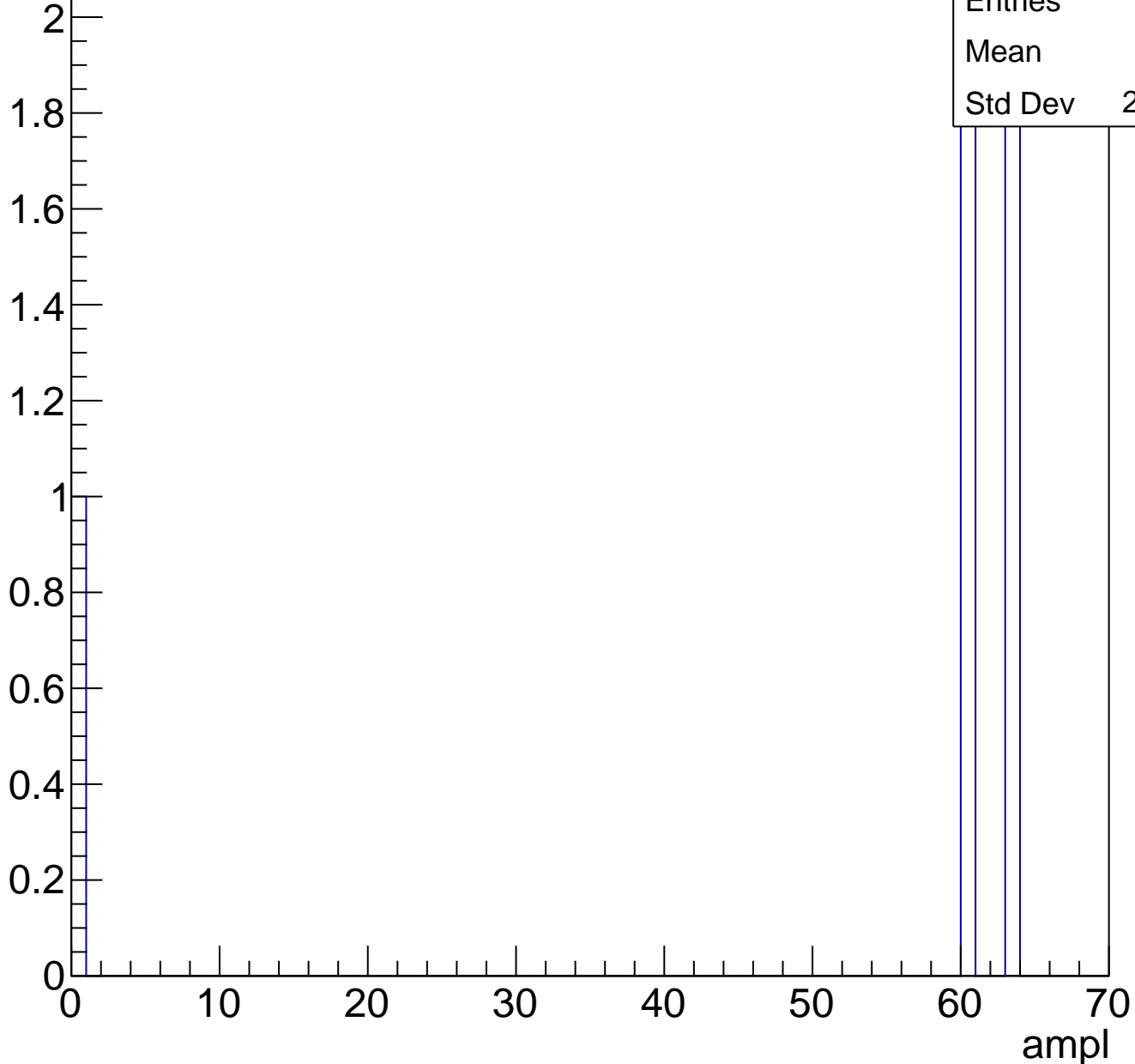
70

ampl

B1L102S, U9-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch50, adc0

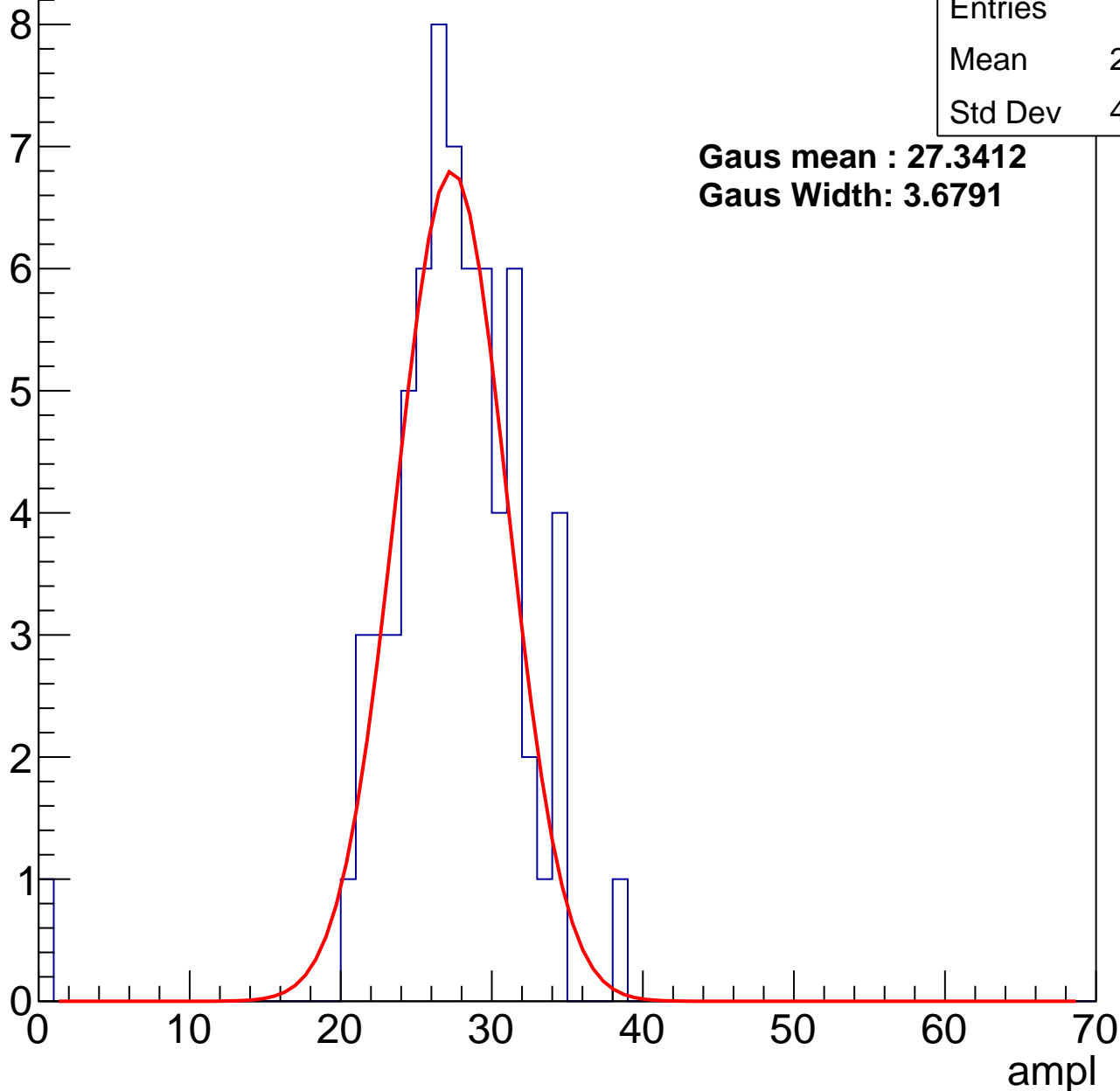
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	26.93
Std Dev	4.963

Gaus mean : 27.3412

Gaus Width: 3.6791



B1L102S, U9-ch50, adc1

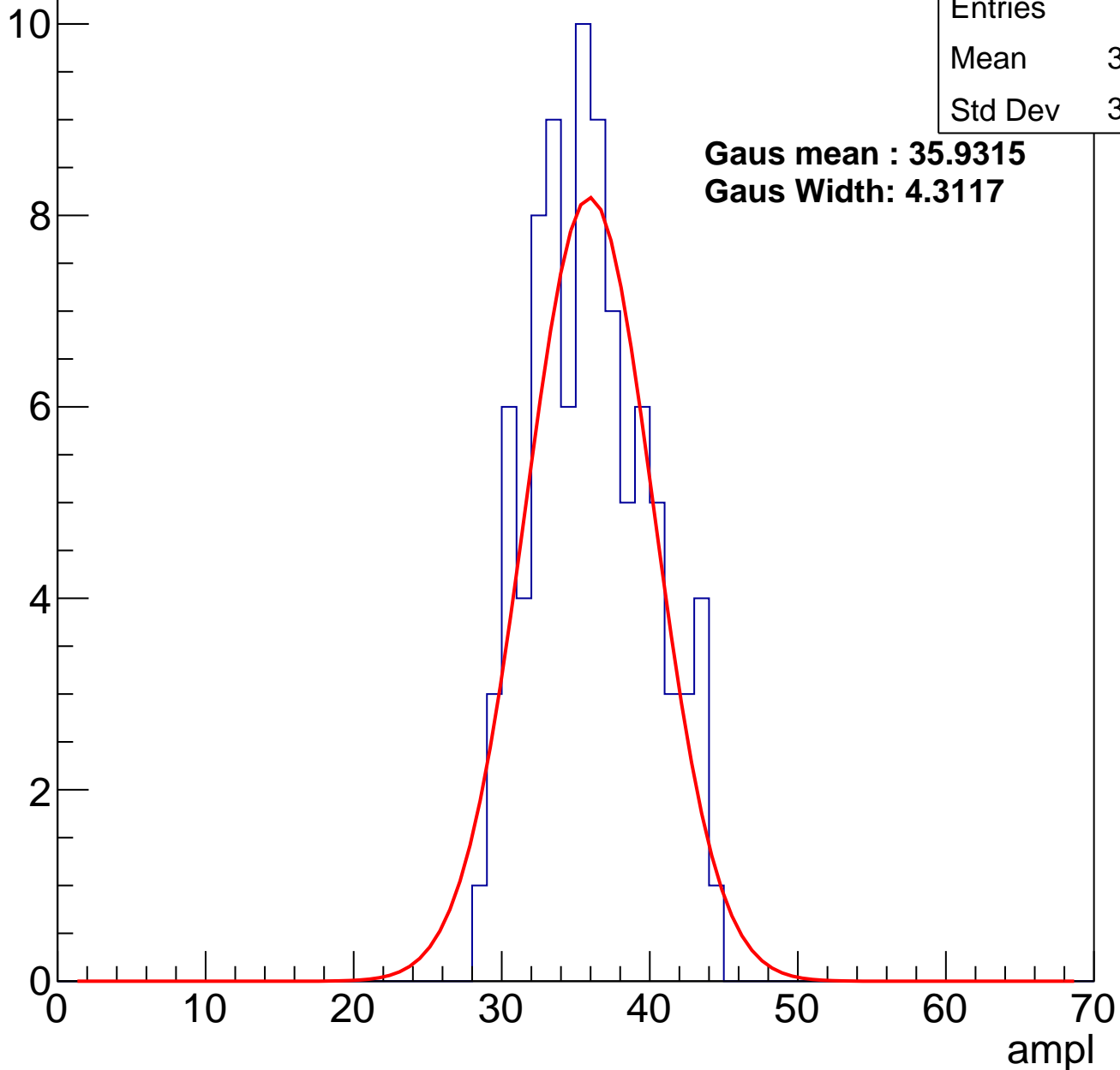
calib_packv5_042523_0143.root, FC#11, port A2

Entries	90
Mean	35.53
Std Dev	3.879

Gaus mean : 35.9315

Gaus Width: 4.3117

Entry



B1L102S, U9-ch50, adc2

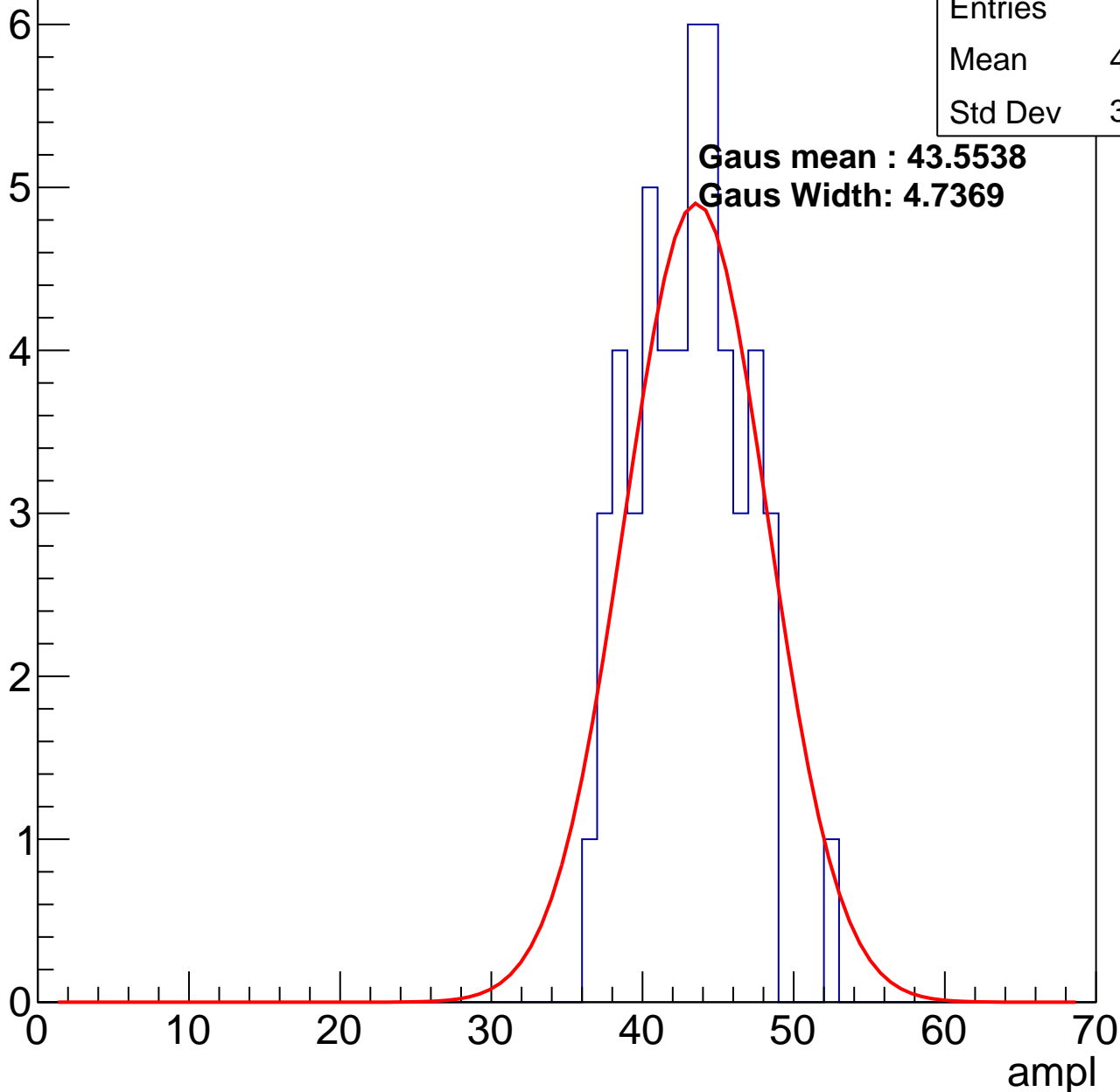
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.59
Std Dev	3.516

Gaus mean : 43.5538

Gaus Width: 4.7369

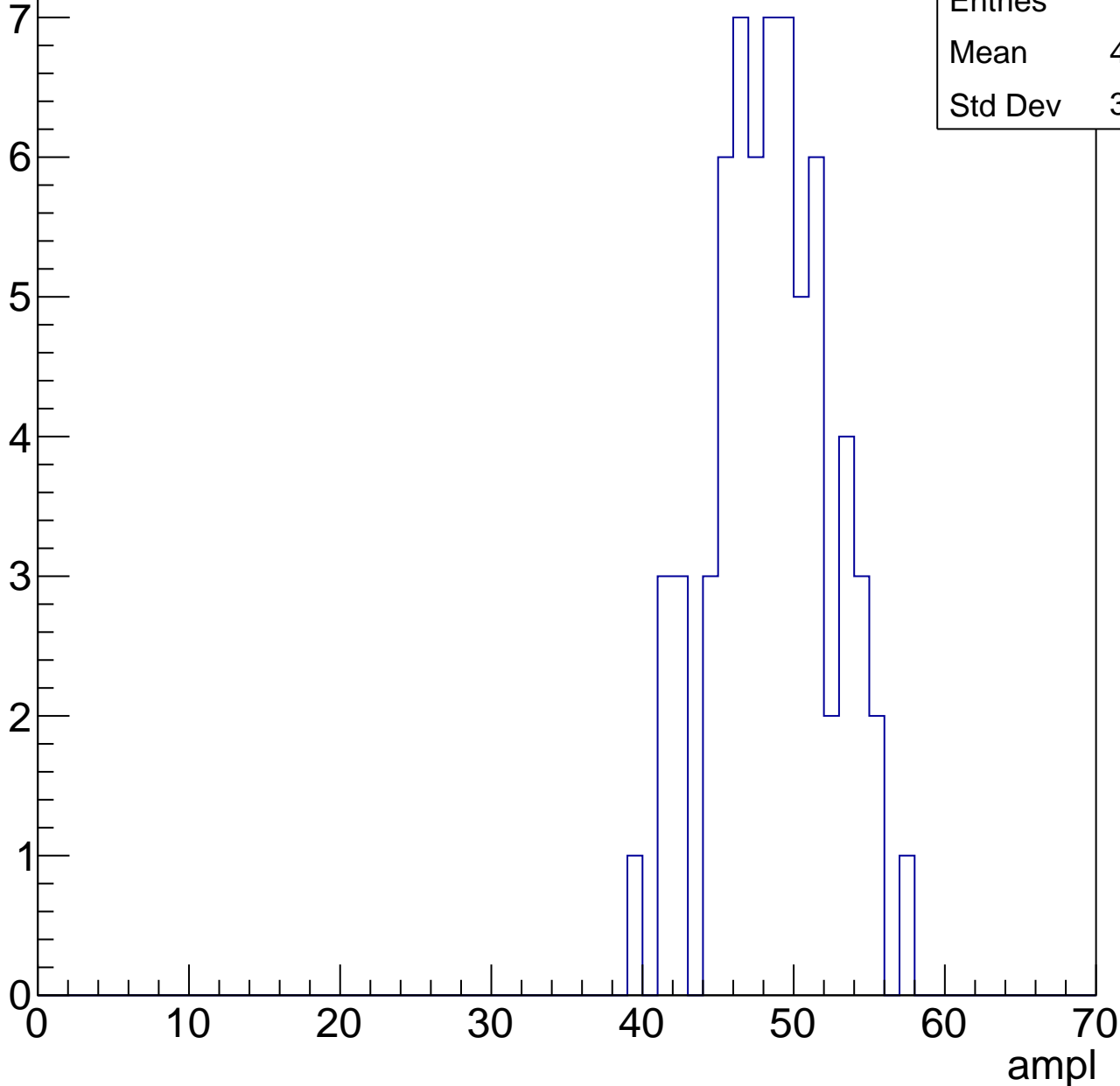


B1L102S, U9-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.09
Std Dev	3.837

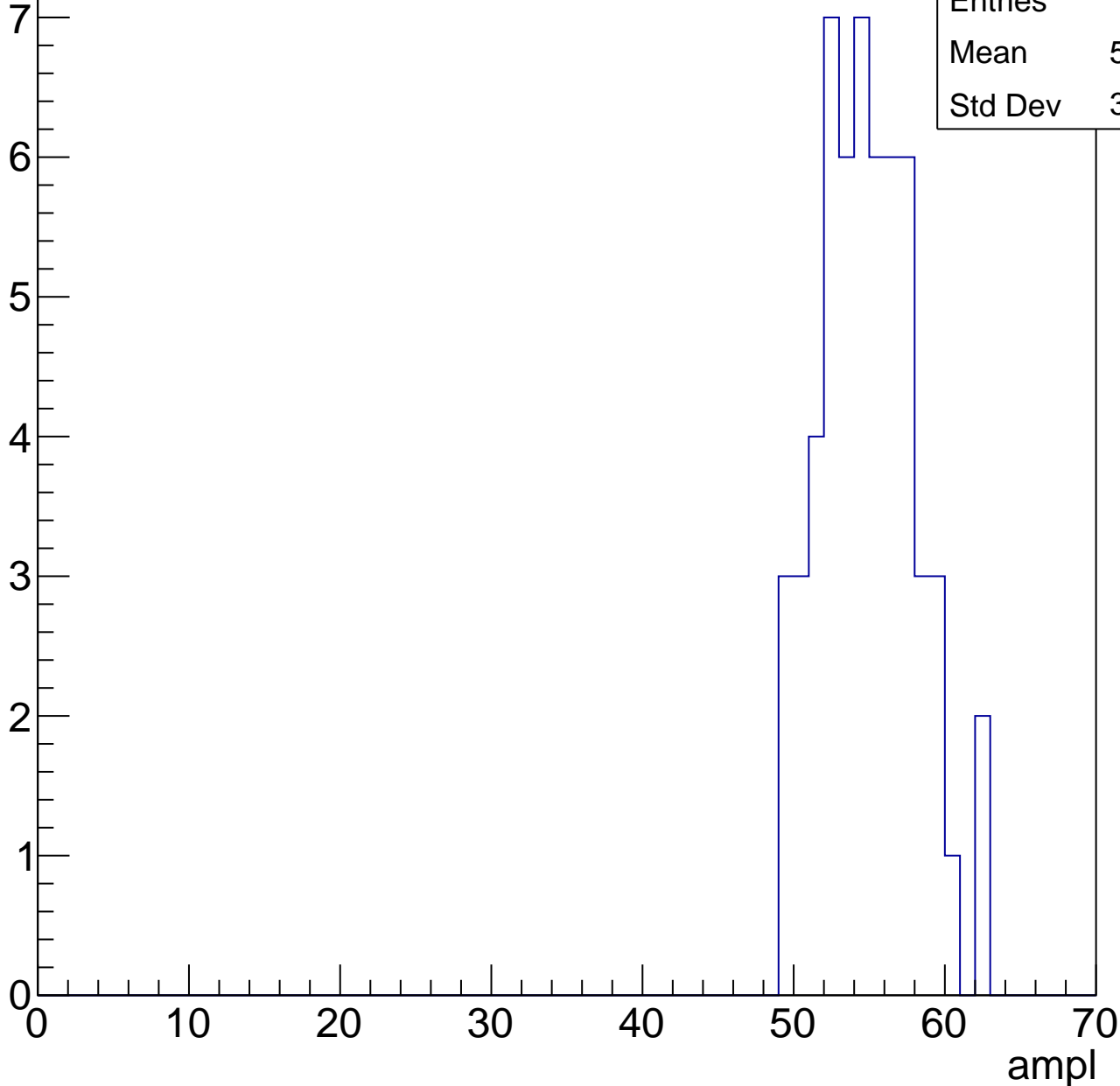


B1L102S, U9-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.46
Std Dev	3.112

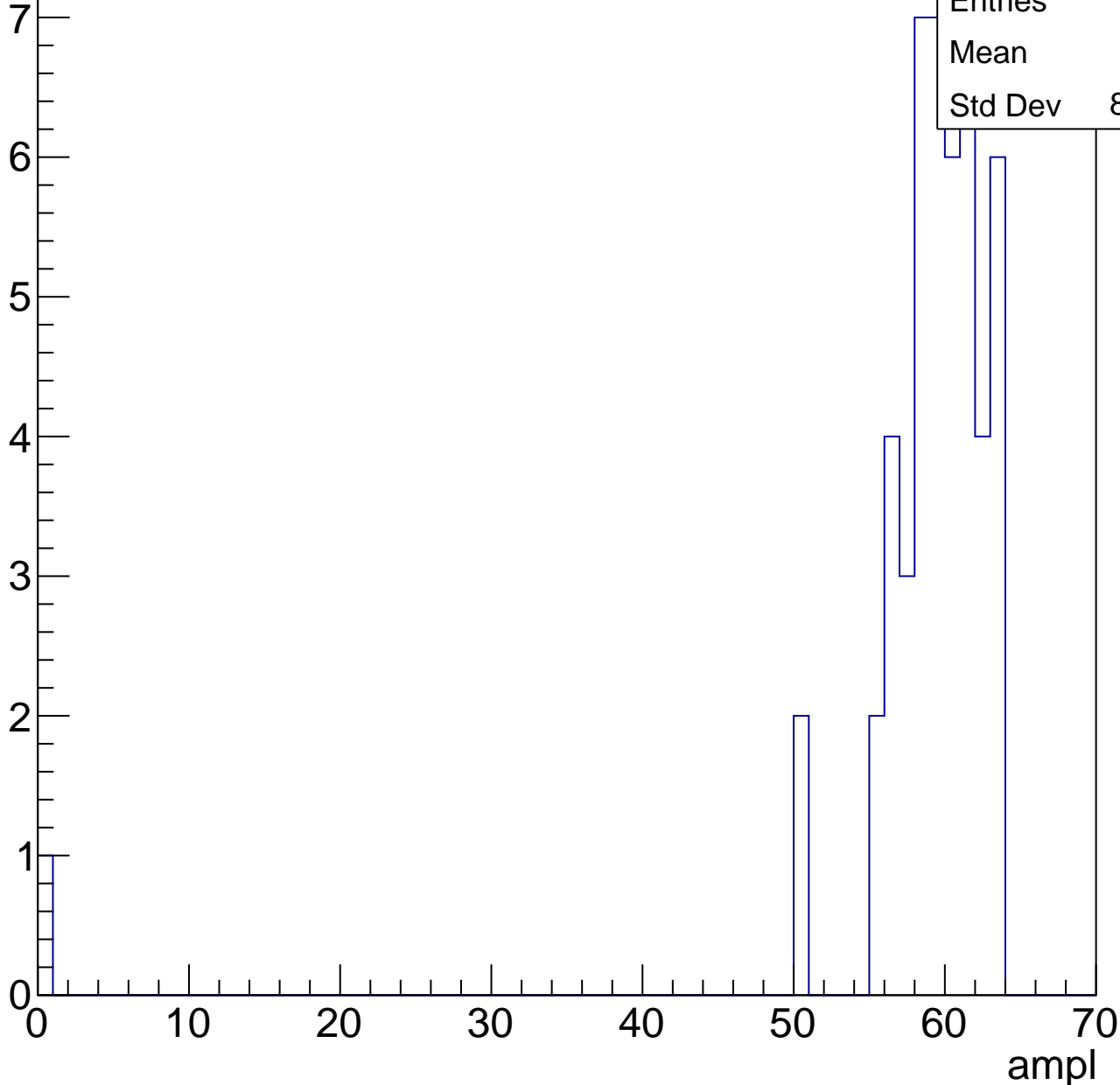


B1L102S, U9-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.9
Std Dev	8.848



B1L102S, U9-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

0 10 20 30 40 50 60 70

ampl

Entries	14
Mean	61.64
Std Dev	1.109

B1L102S, U9-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch51, adc0

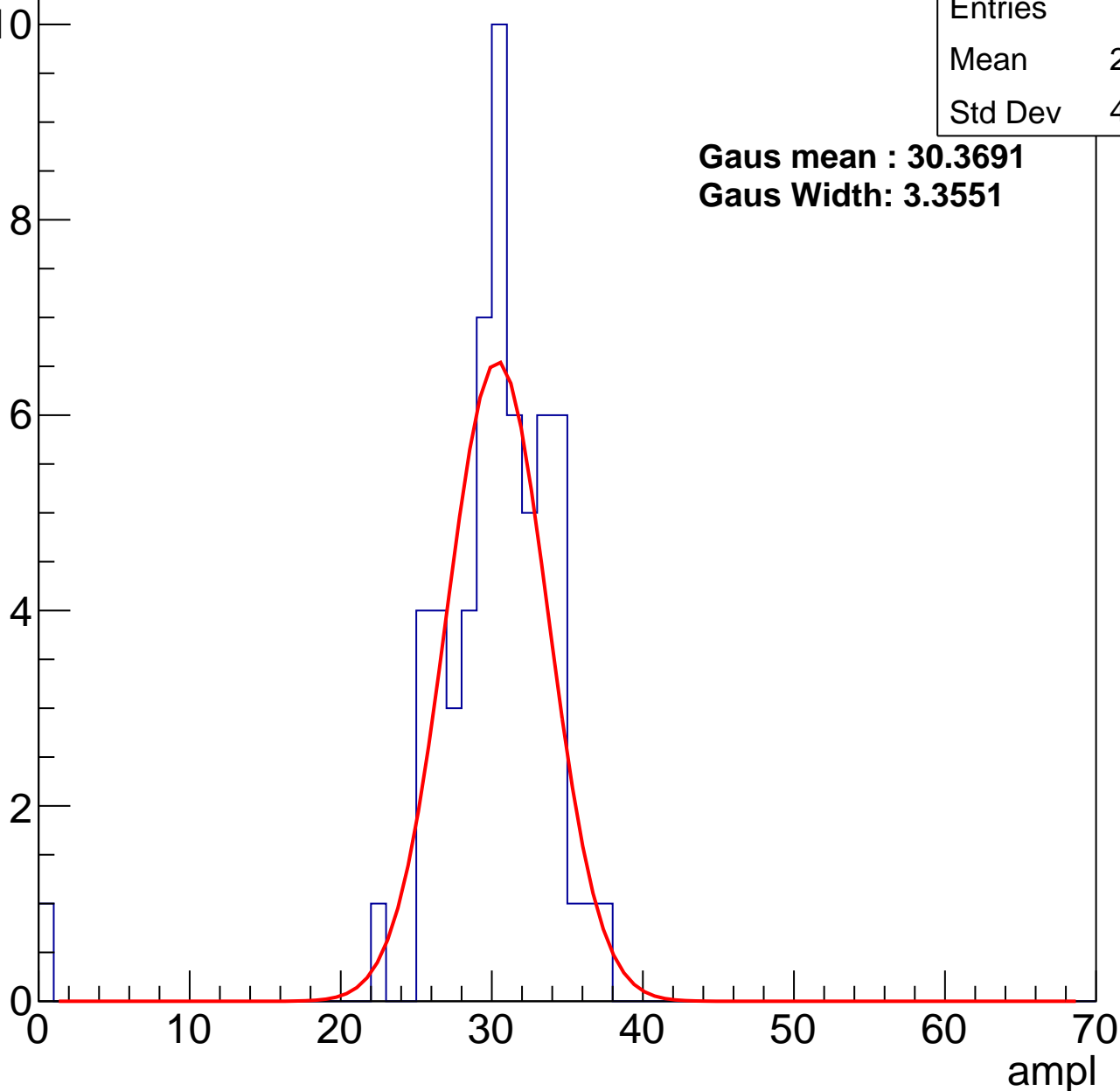
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.63
Std Dev	4.926

Gaus mean : 30.3691

Gaus Width: 3.3551



B1L102S, U9-ch51, adc1

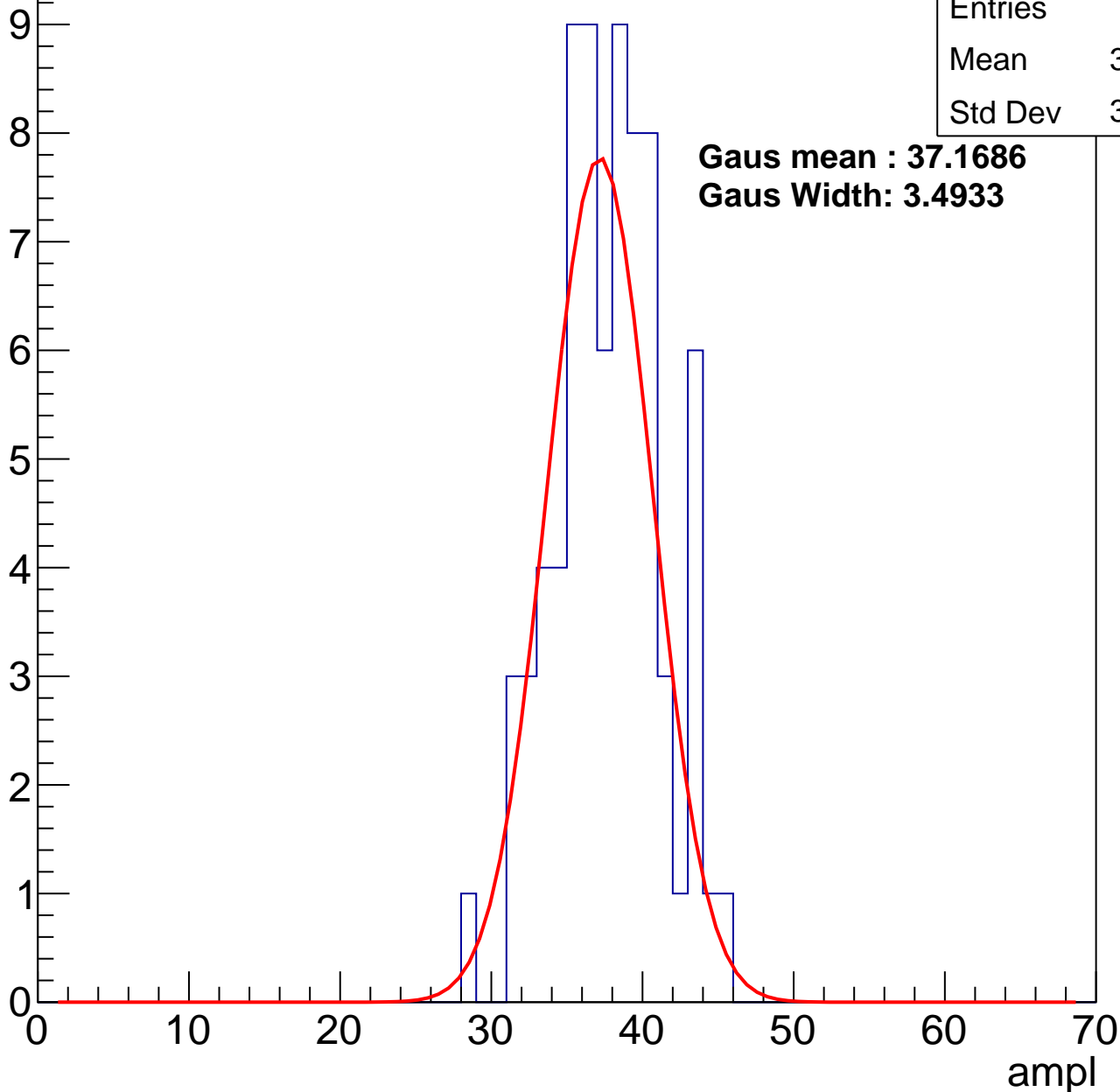
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	37.26
Std Dev	3.477

Gaus mean : 37.1686

Gaus Width: 3.4933



B1L102S, U9-ch51, adc2

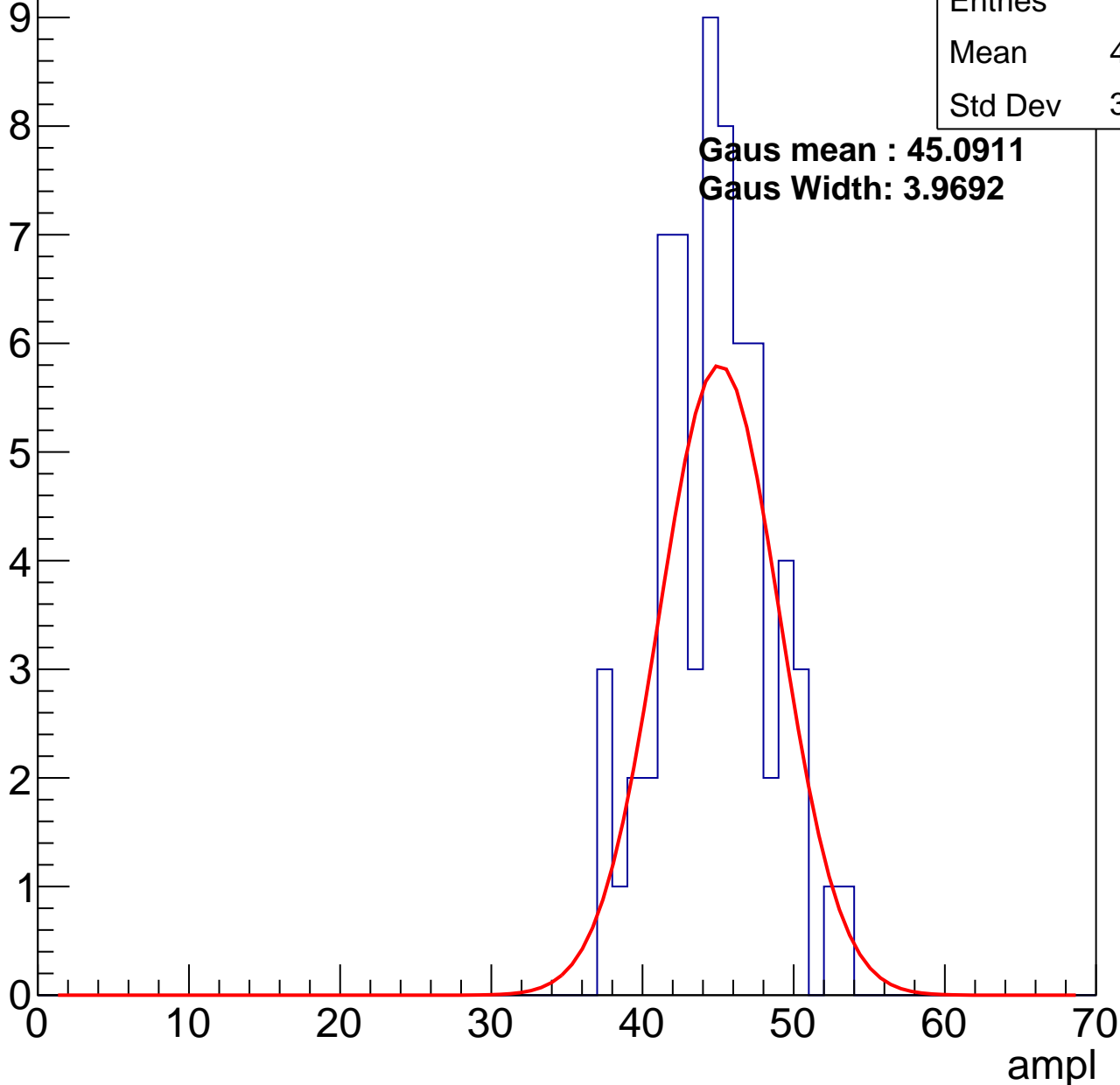
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.28
Std Dev	3.563

Gaus mean : 45.0911

Gaus Width: 3.9692

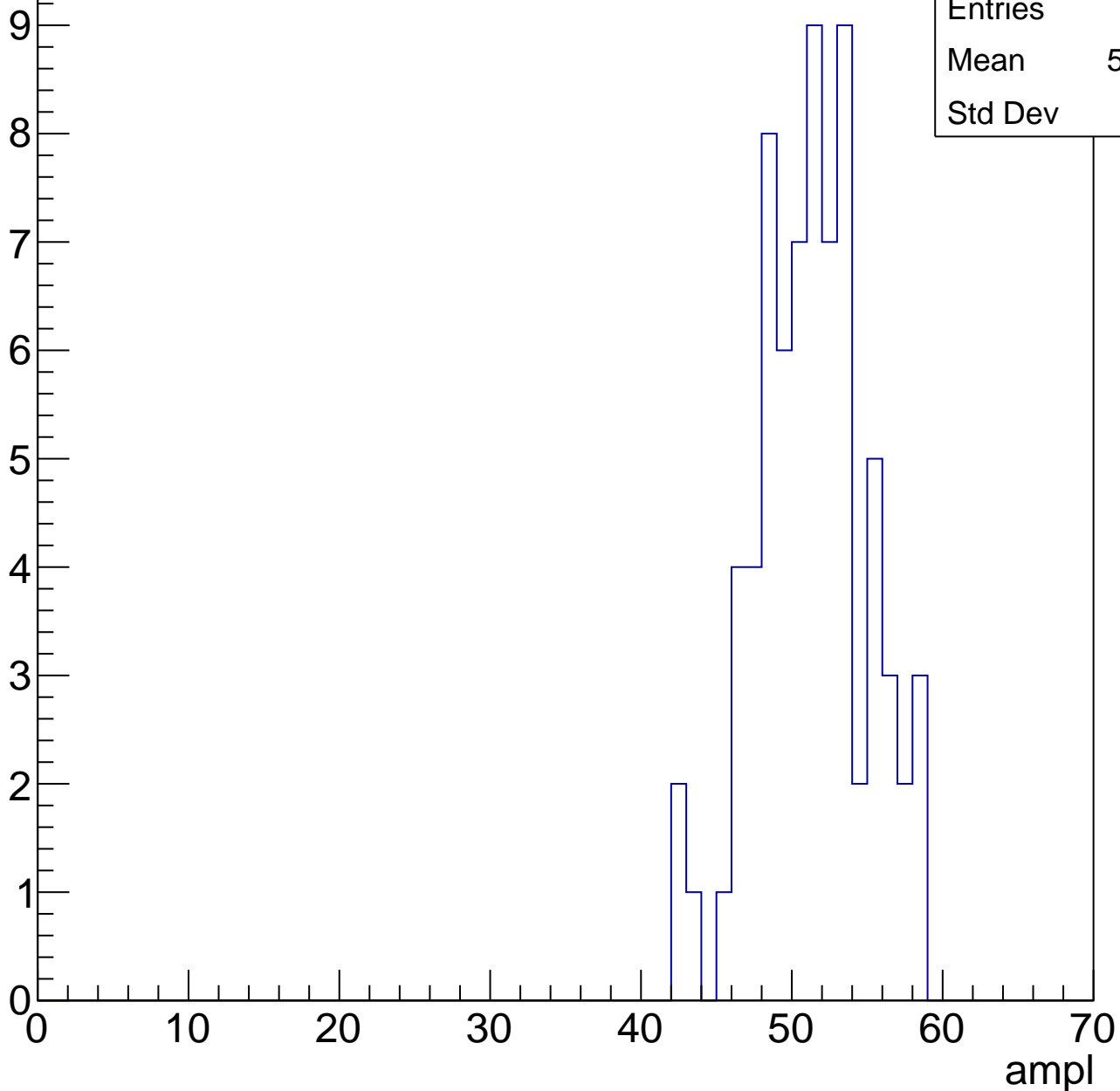


B1L102S, U9-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	50.84
Std Dev	3.63

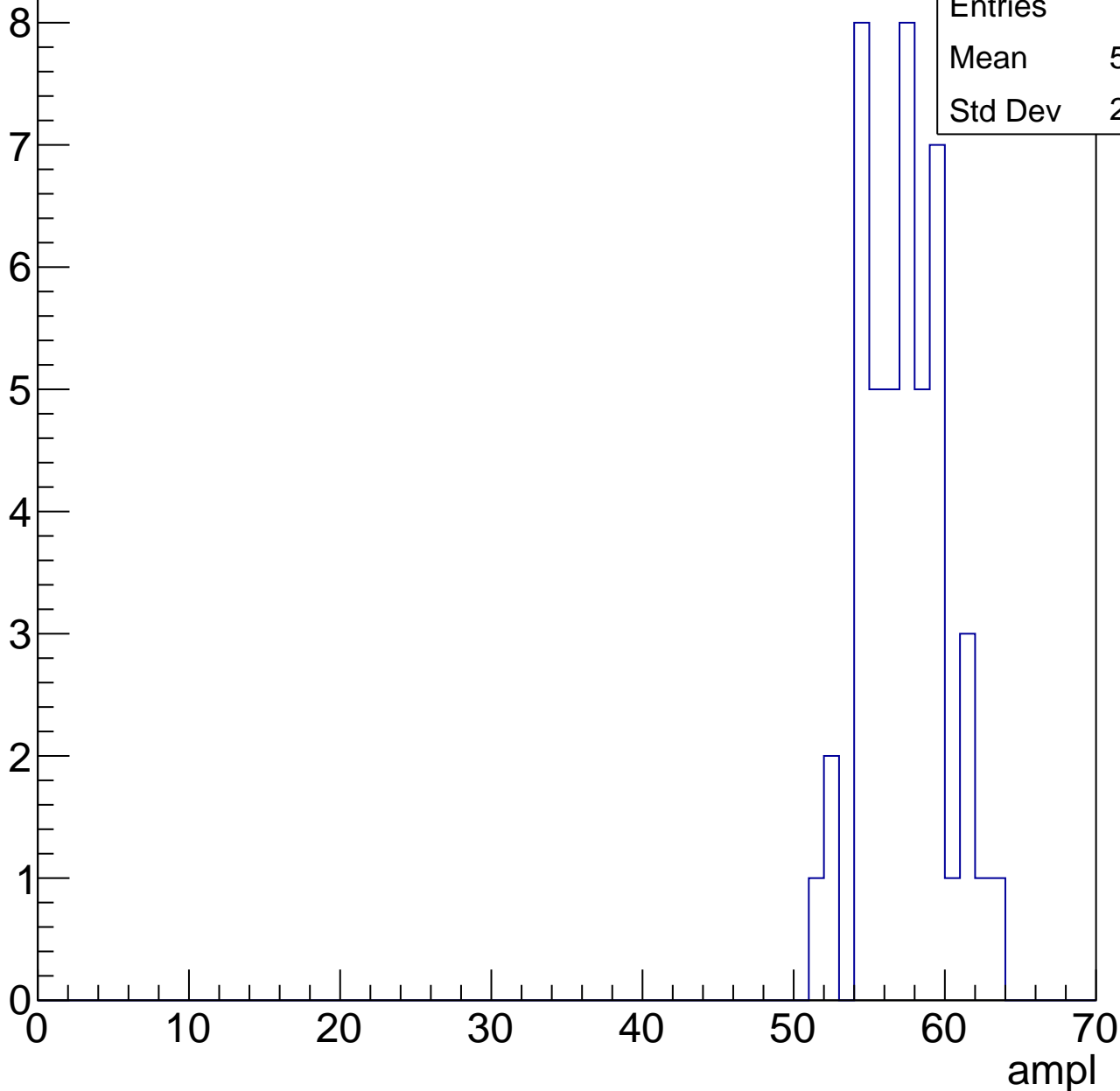


B1L102S, U9-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	56.79
Std Dev	2.657



B1L102S, U9-ch51, adc5

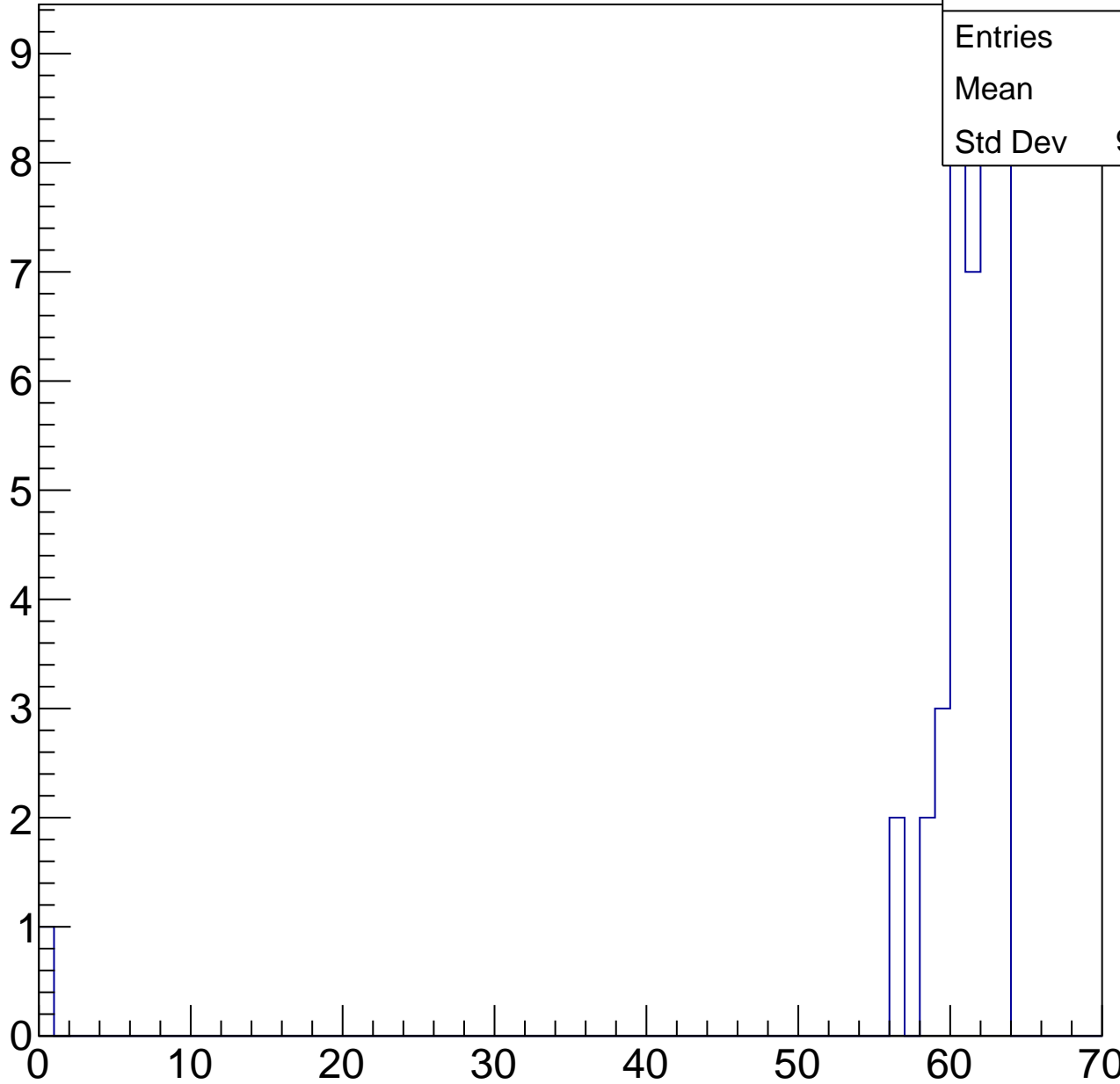
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.3
Std Dev	9.662

ampl



B1L102S, U9-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch52, adc0

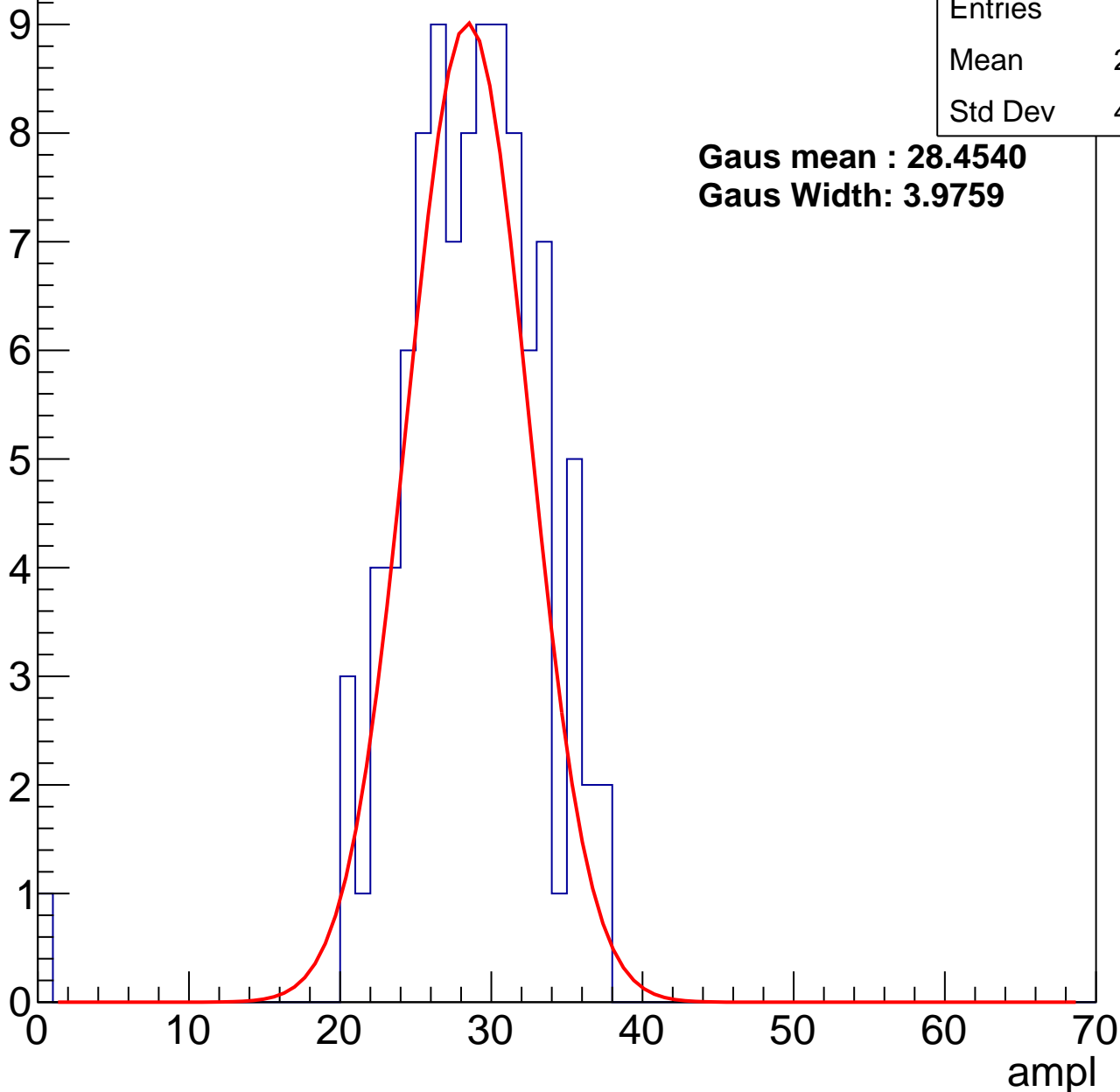
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	100
Mean	28.09
Std Dev	4.938

Gaus mean : 28.4540

Gaus Width: 3.9759



B1L102S, U9-ch52, adc1

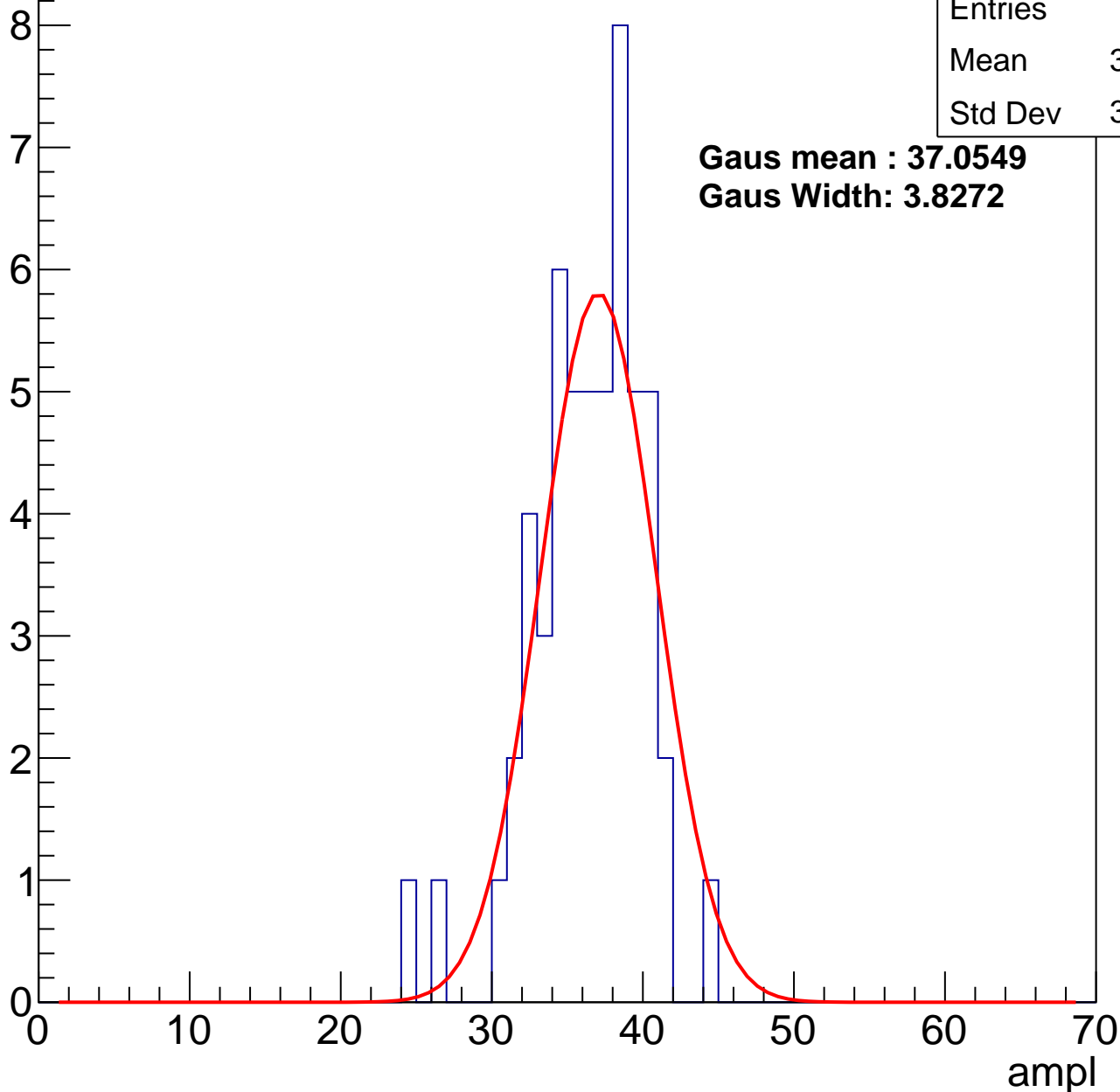
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.89
Std Dev	3.665

Gaus mean : 37.0549

Gaus Width: 3.8272



B1L102S, U9-ch52, adc2

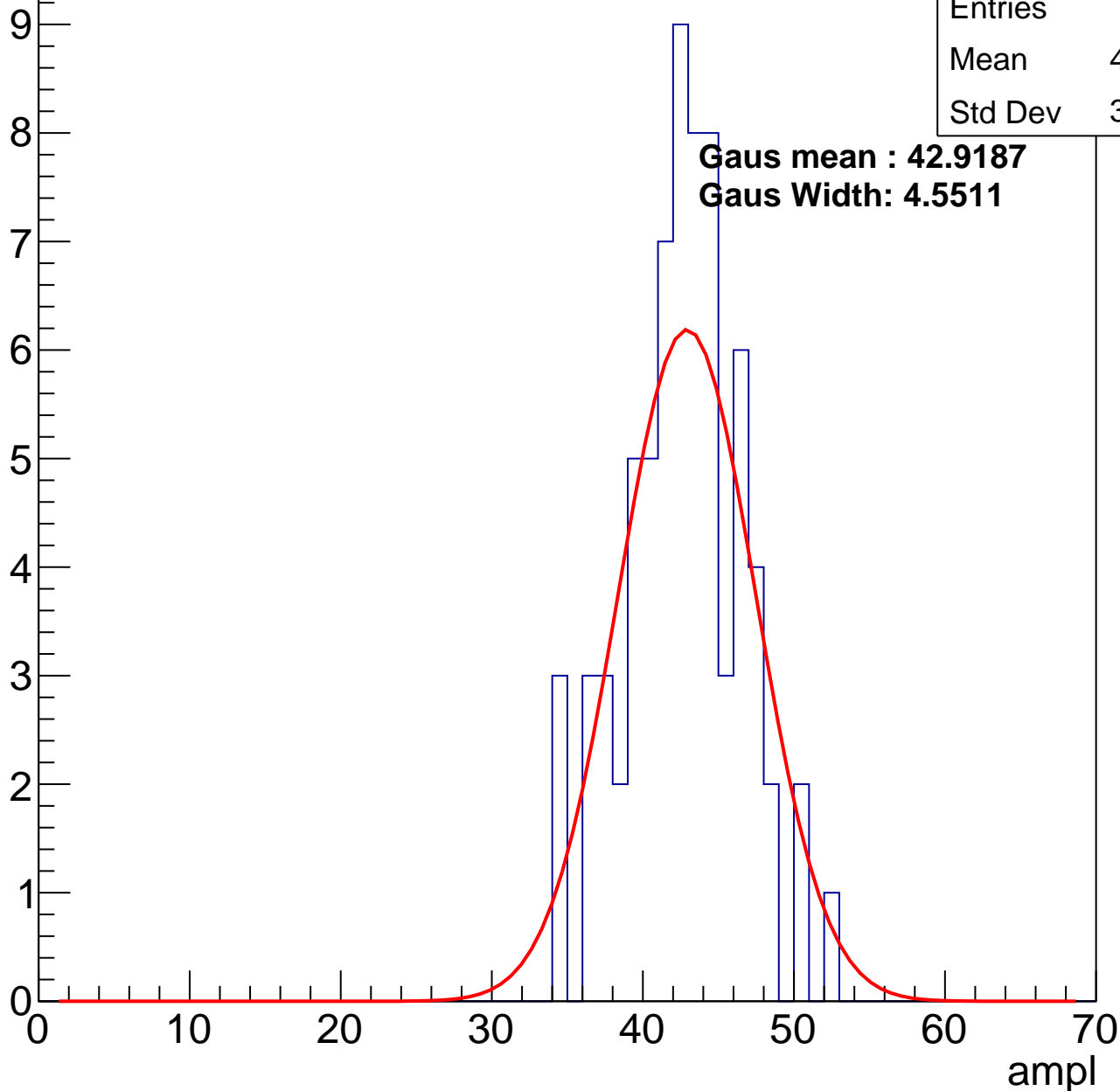
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.25
Std Dev	3.823

Gaus mean : 42.9187

Gaus Width: 4.5511

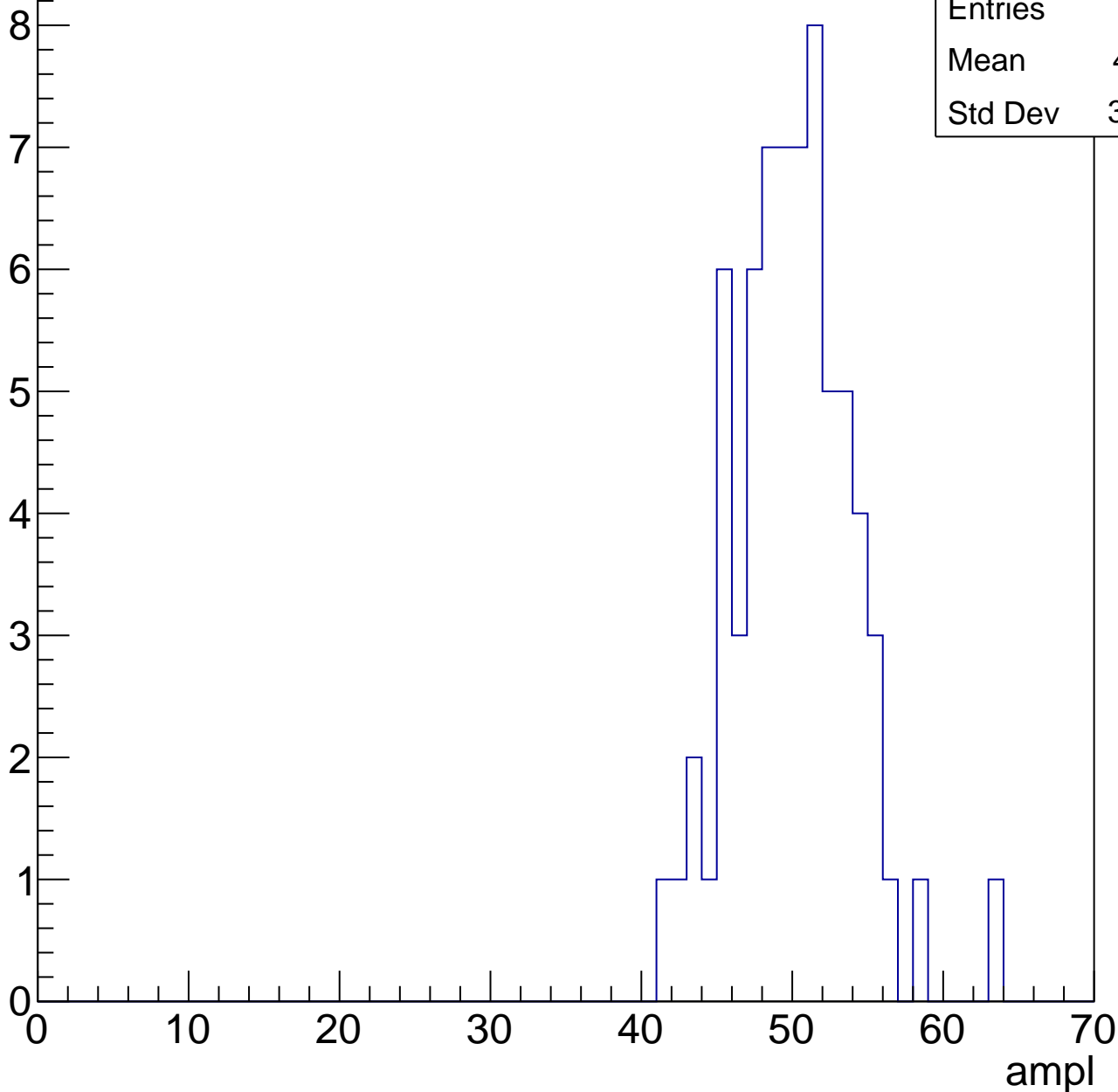


B1L102S, U9-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.61
Std Dev	3.876

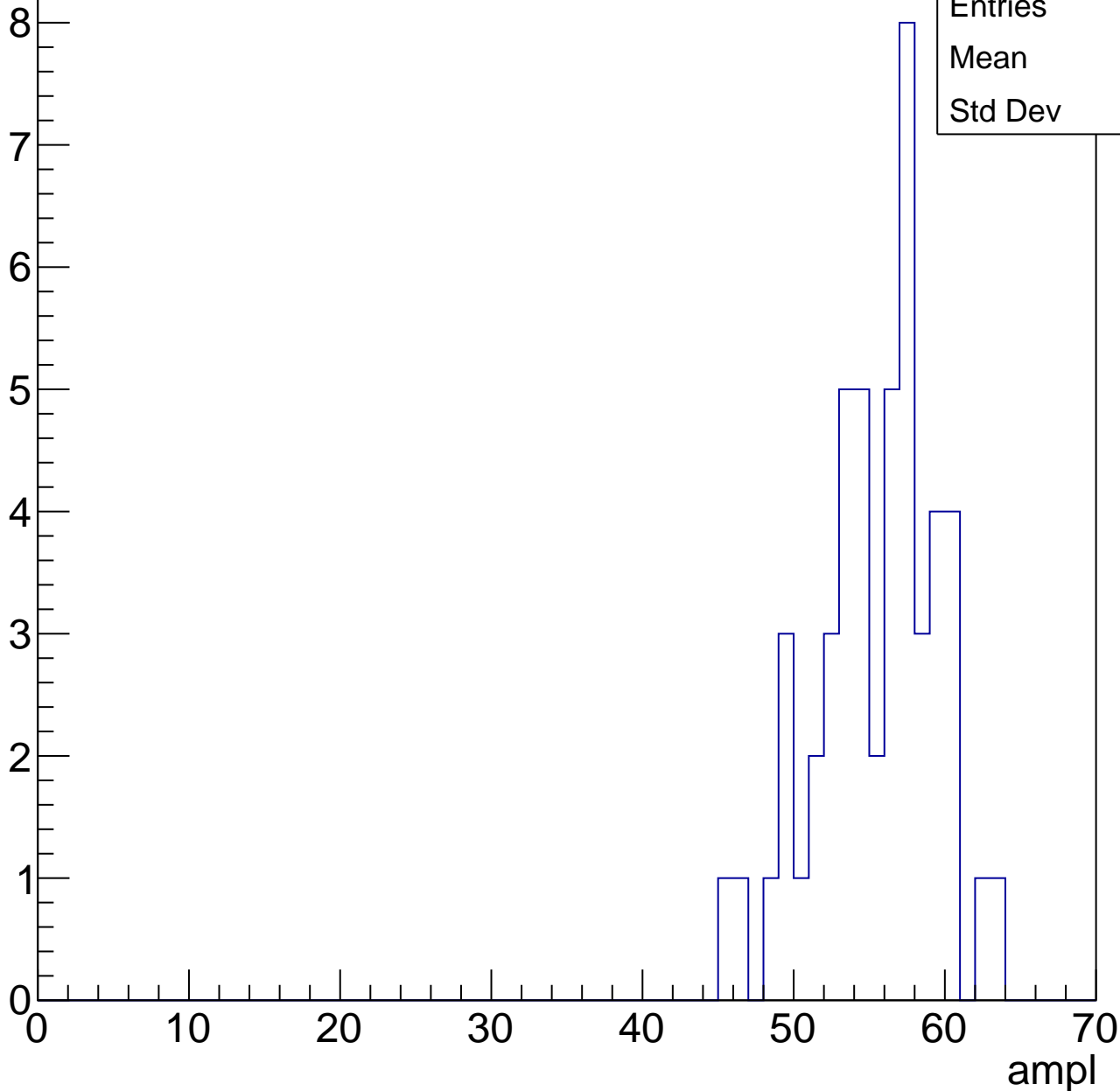


B1L102S, U9-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55
Std Dev	3.97

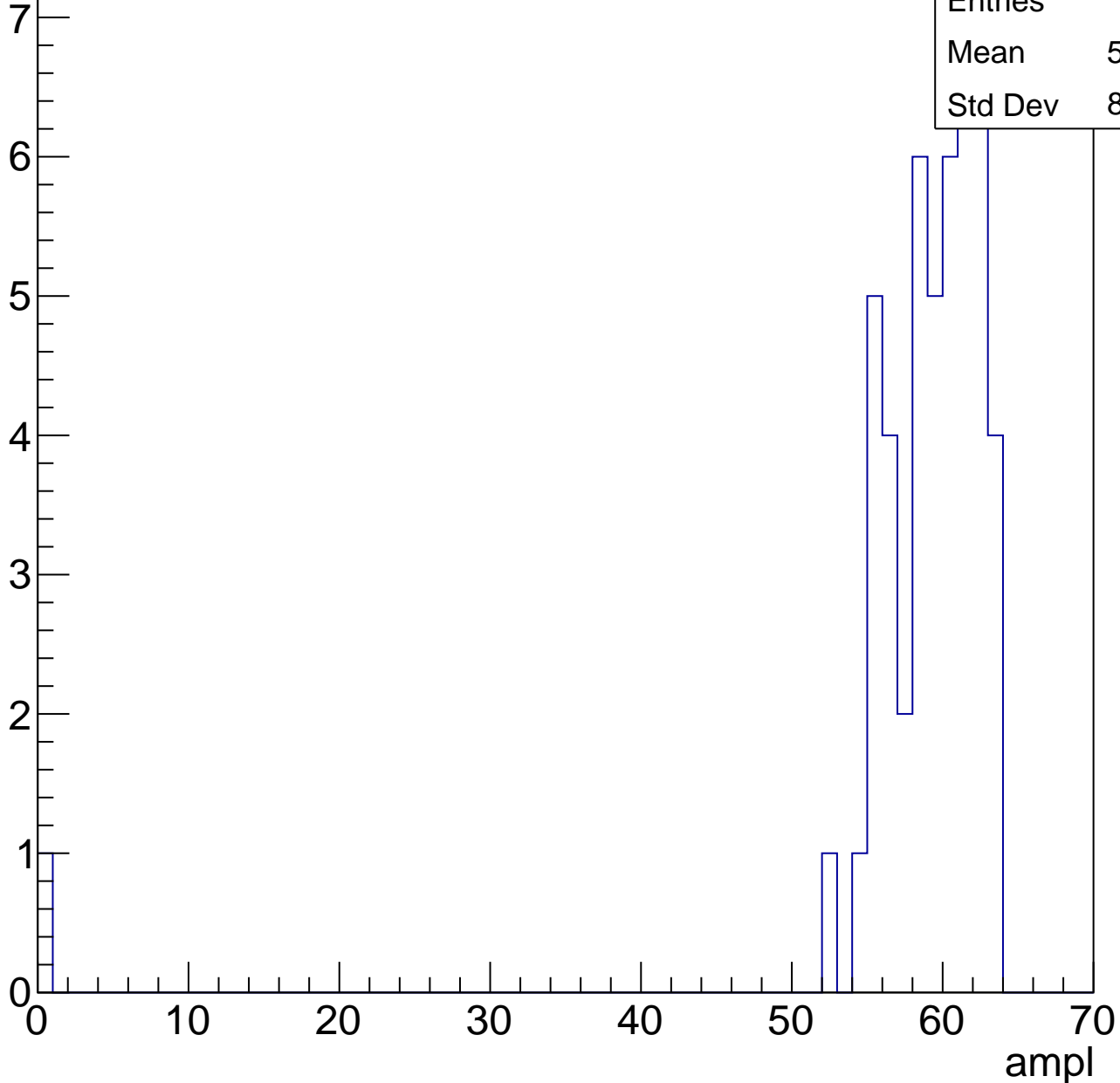


B1L102S, U9-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

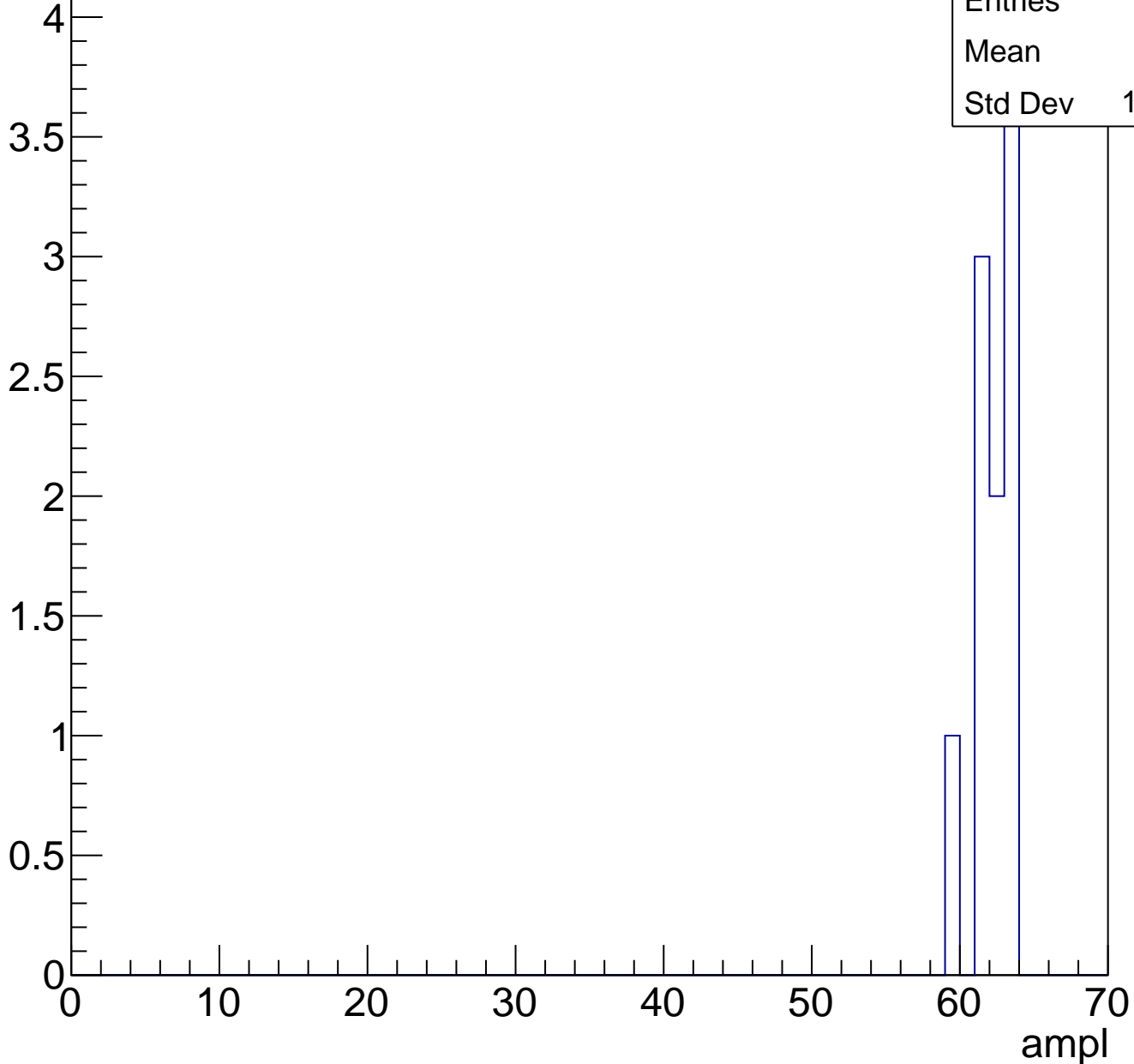
Entries	49
Mean	57.86
Std Dev	8.785



B1L102S, U9-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch53, adc0

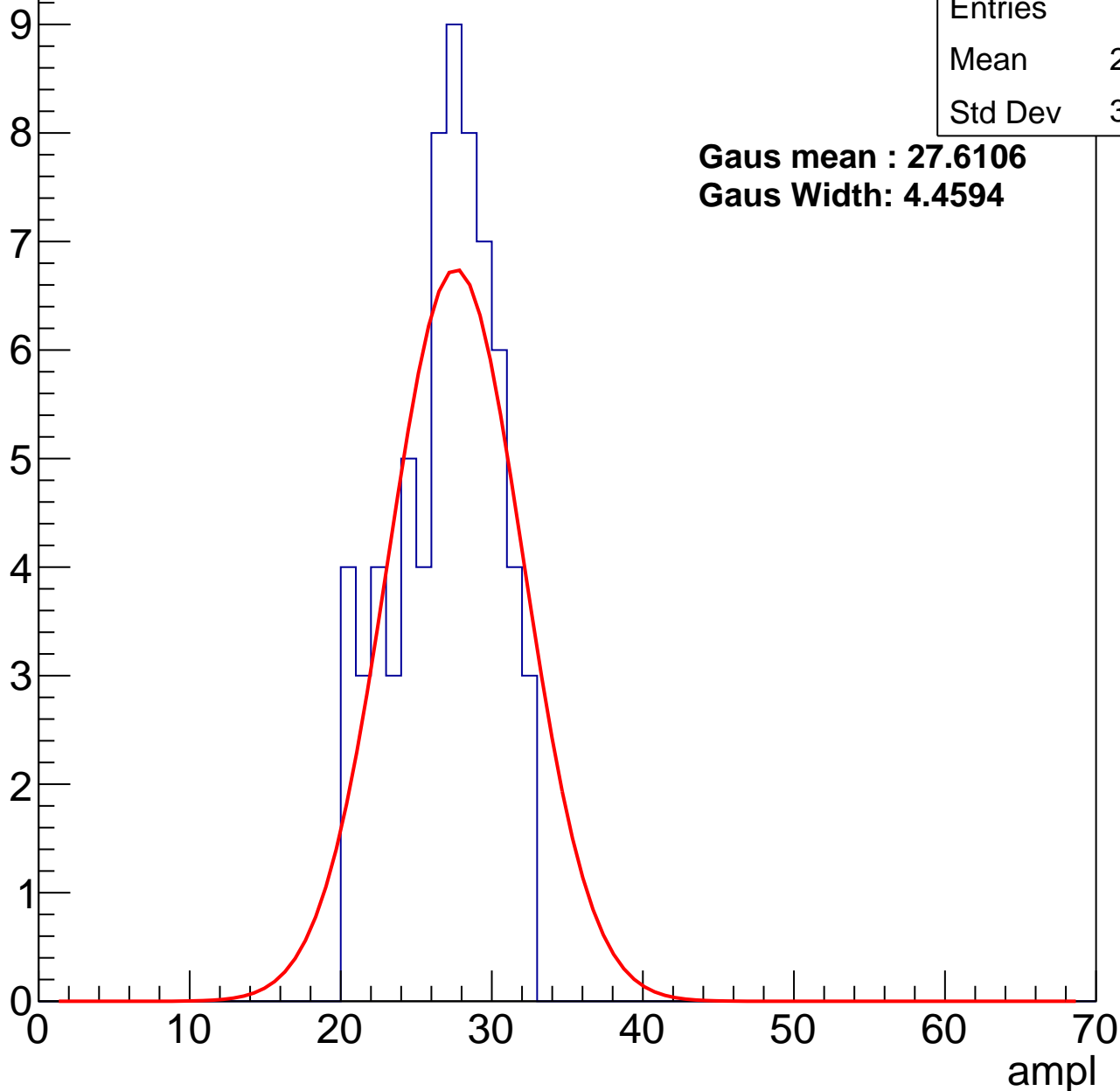
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	26.44
Std Dev	3.274

Gaus mean : 27.6106

Gaus Width: 4.4594



B1L102S, U9-ch53, adc1

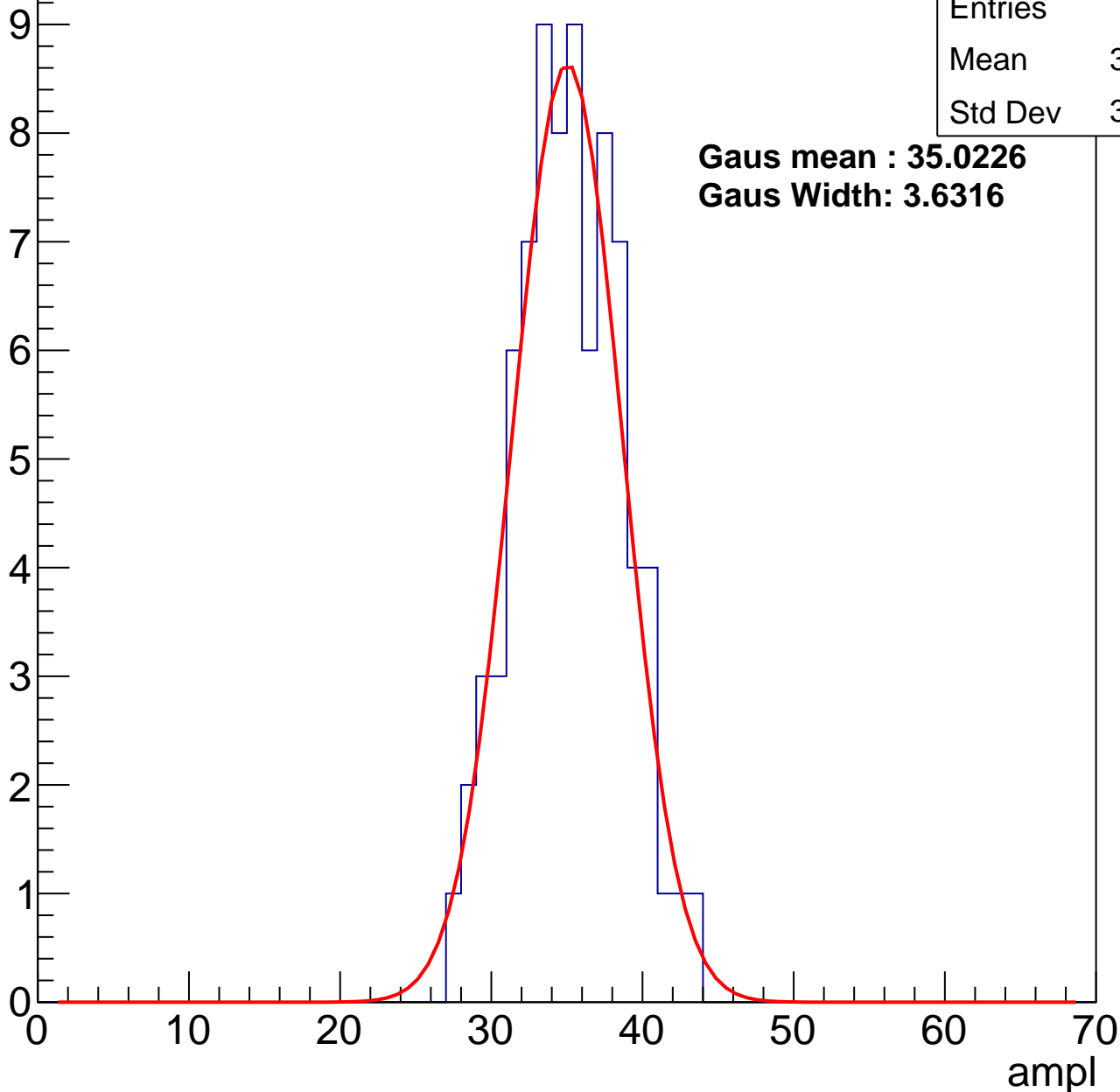
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.67
Std Dev	3.452

Gaus mean : 35.0226

Gaus Width: 3.6316



B1L102S, U9-ch53, adc2

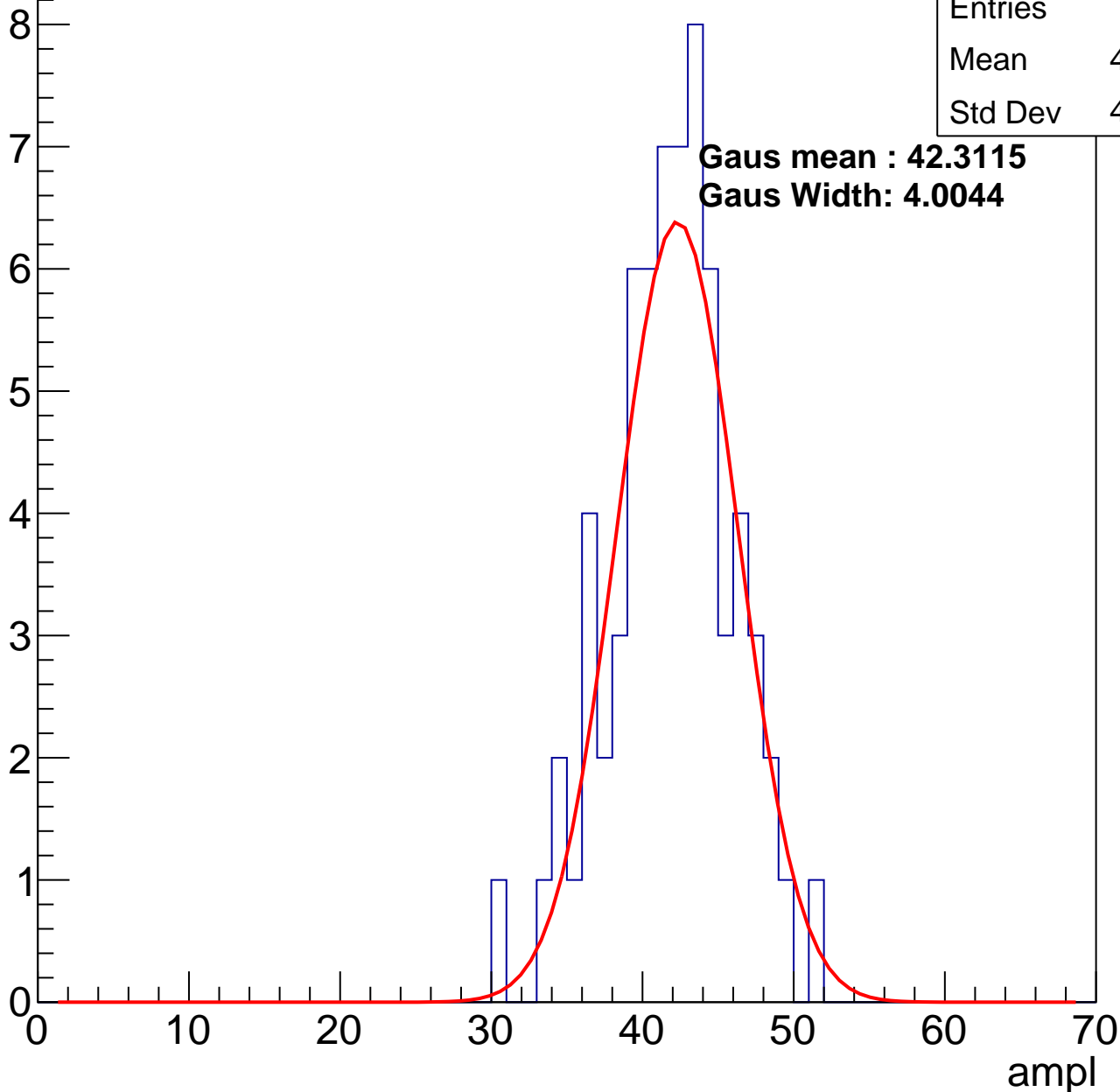
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.43
Std Dev	4.034

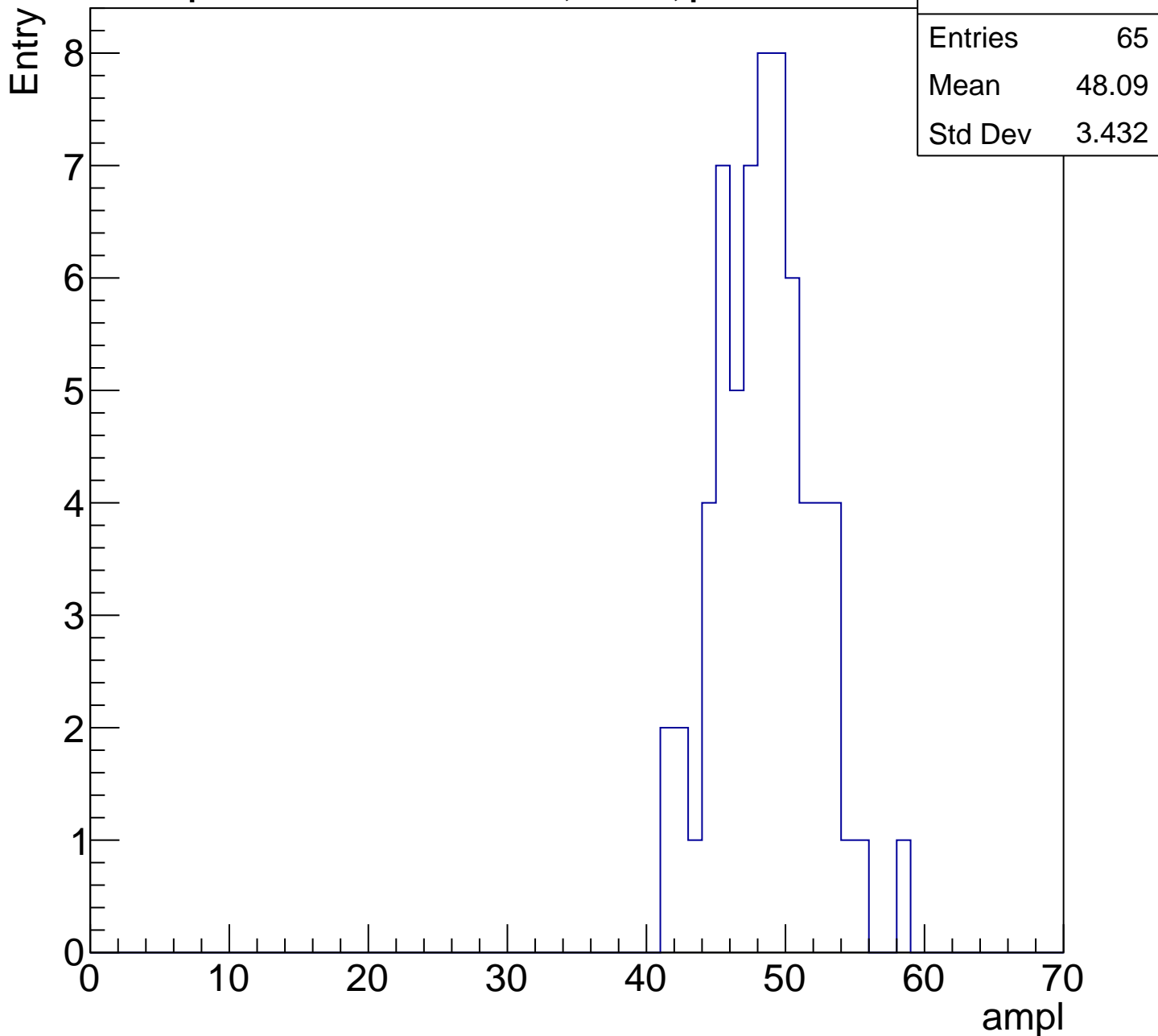
Gaus mean : 42.3115

Gaus Width: 4.0044



B1L102S, U9-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

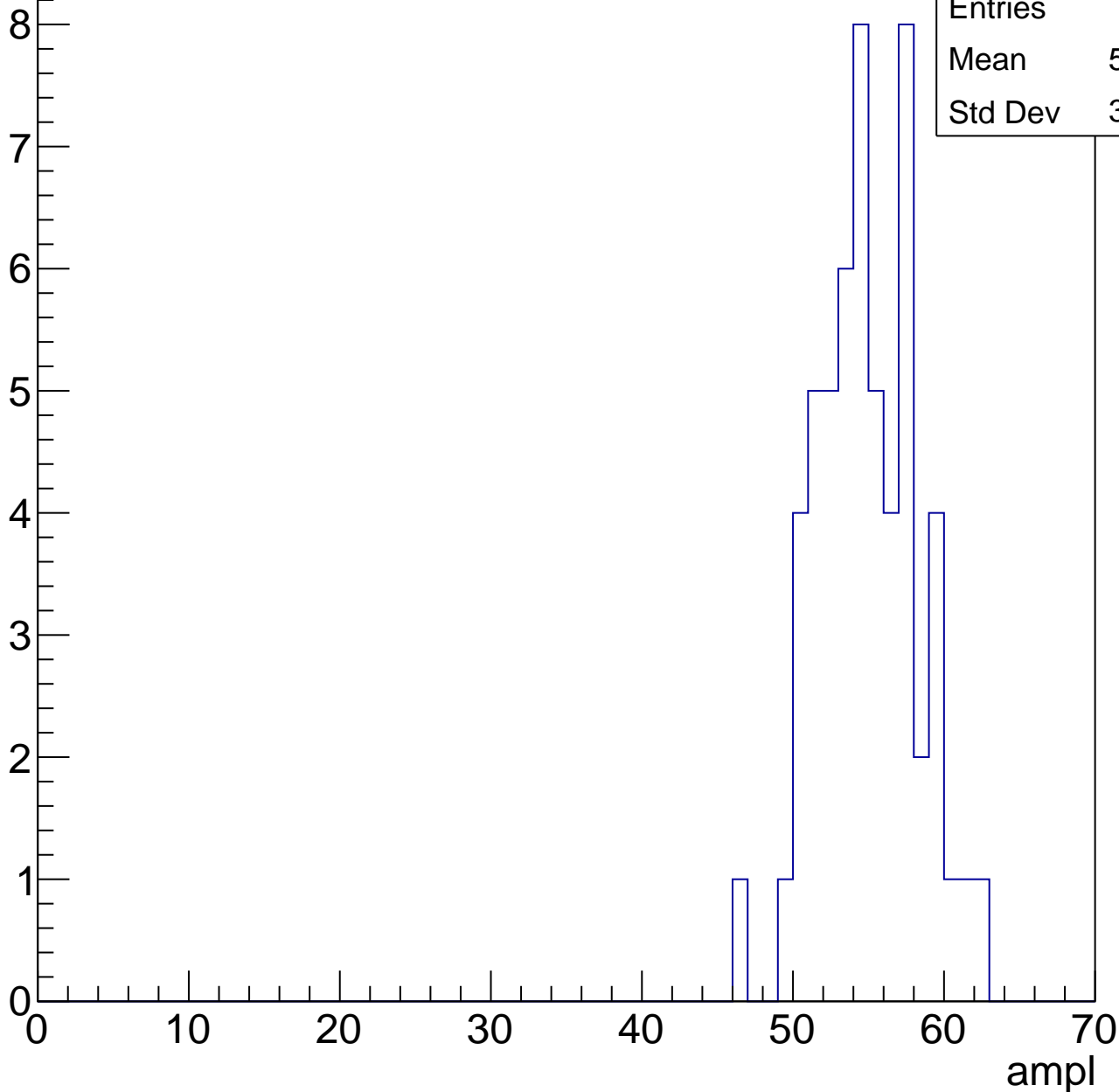


B1L102S, U9-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.46
Std Dev	3.235

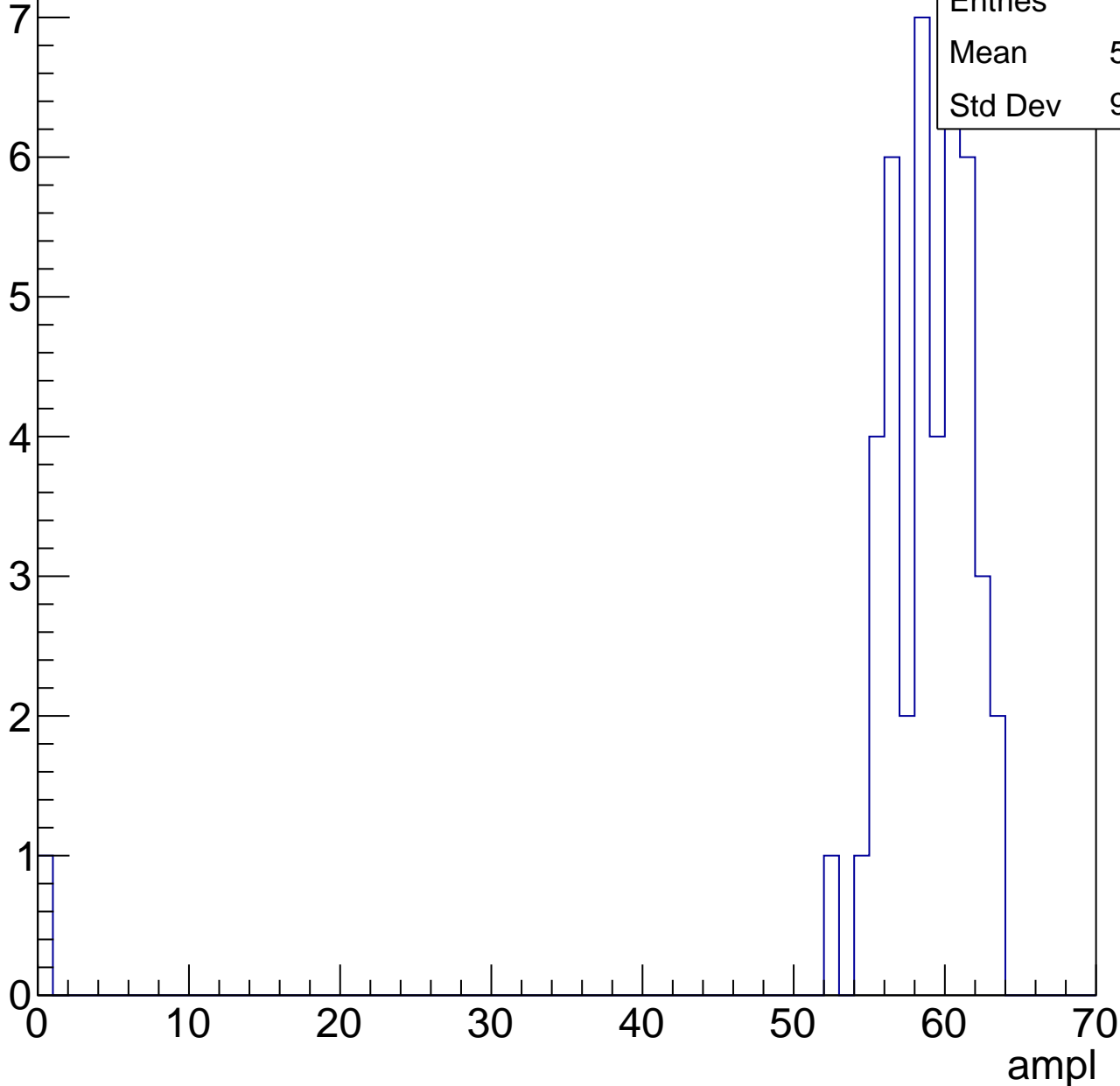


B1L102S, U9-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	57.18
Std Dev	9.086

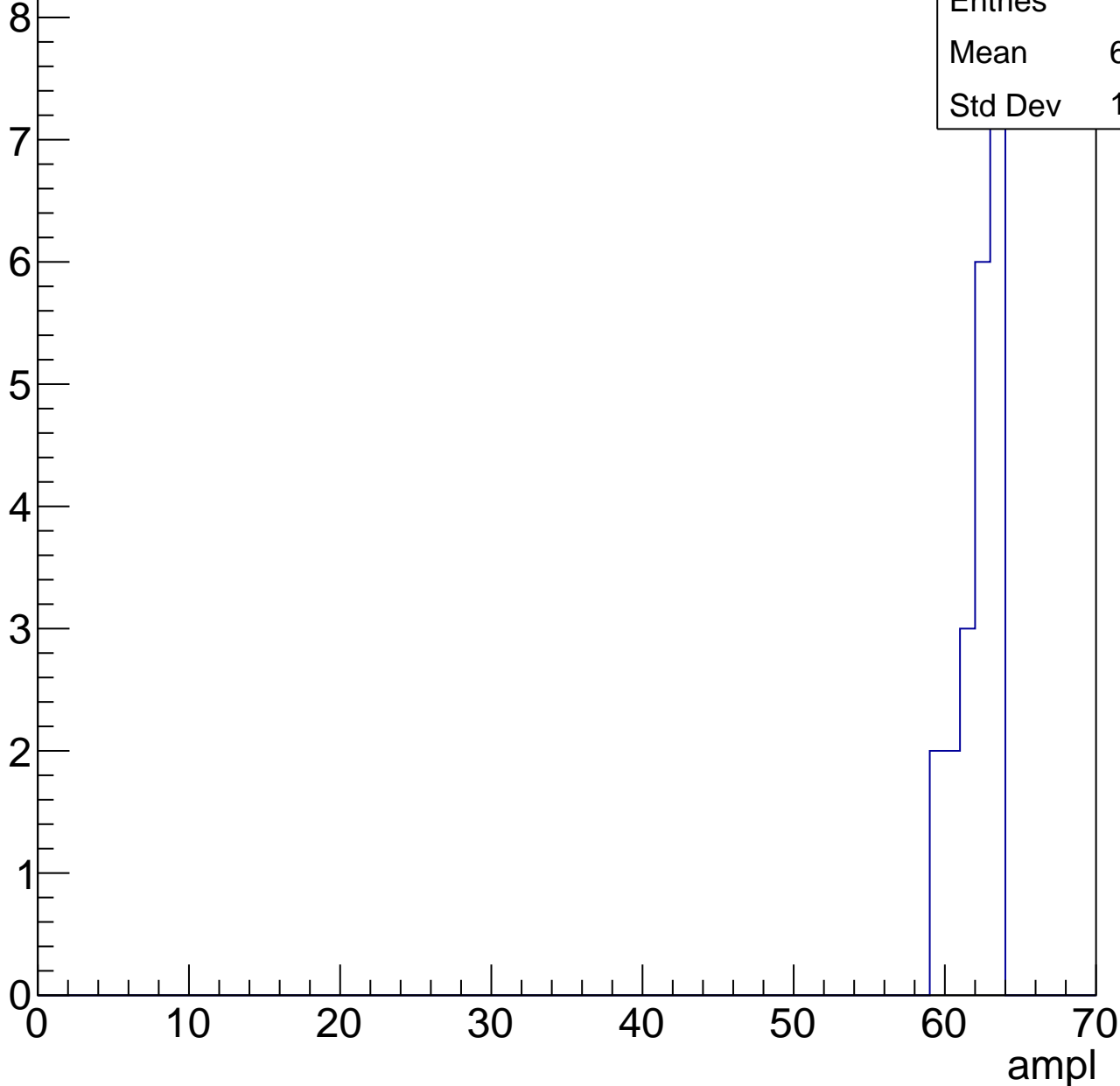


B1L102S, U9-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	61.76
Std Dev	1.306



B1L102S, U9-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch54, adc0

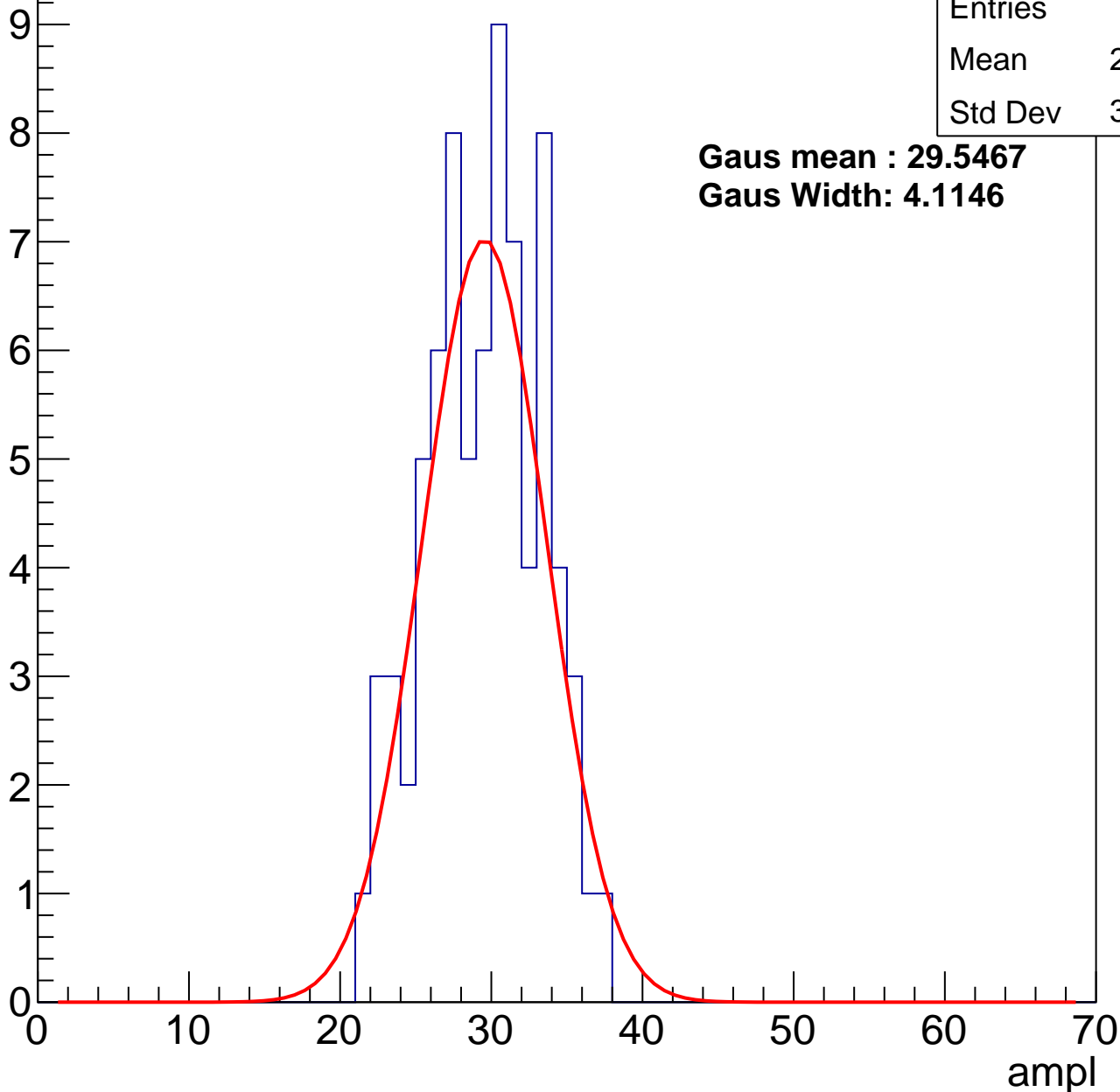
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.05
Std Dev	3.738

Gaus mean : 29.5467

Gaus Width: 4.1146



B1L102S, U9-ch54, adc1

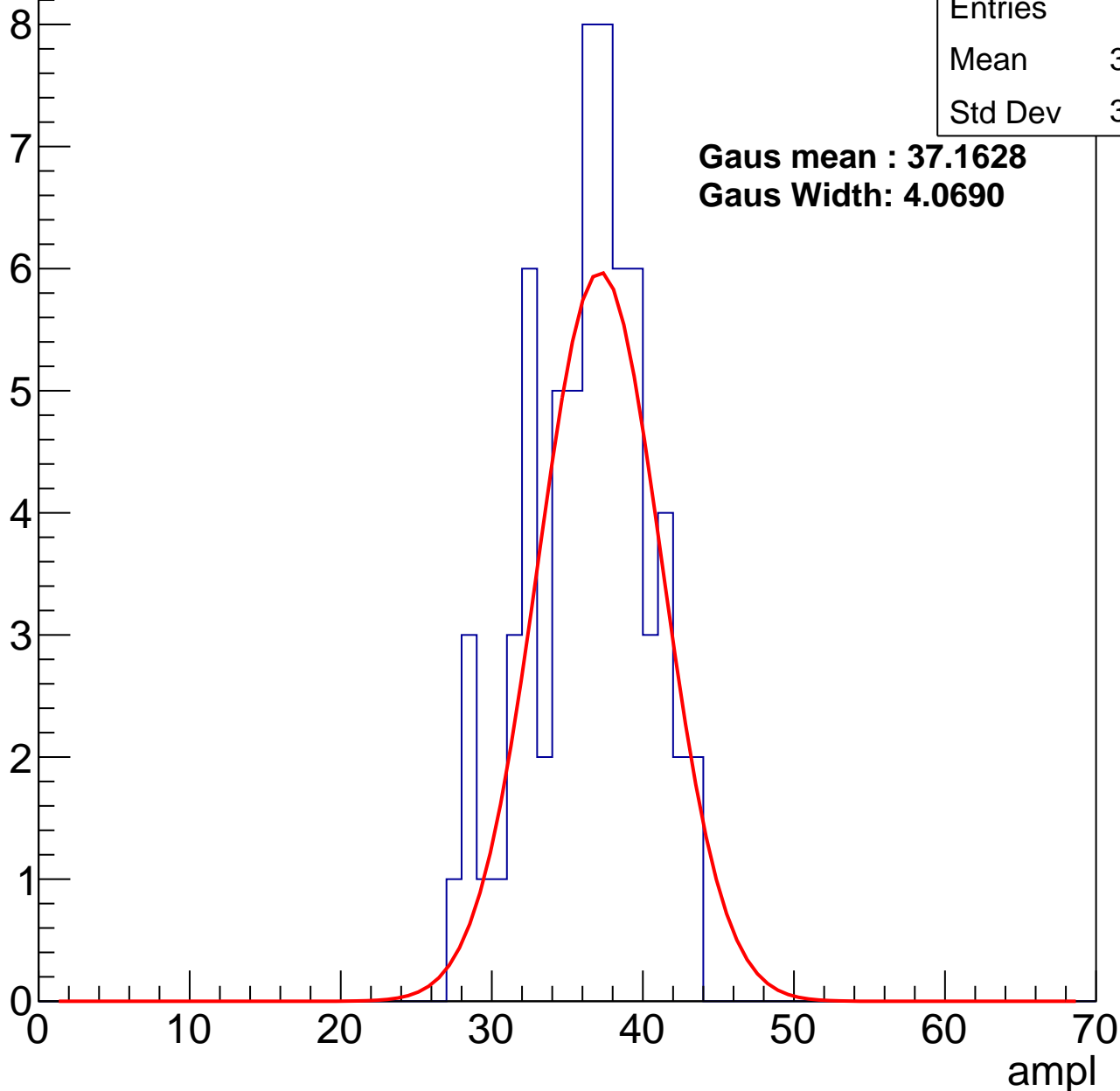
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.85
Std Dev	3.843

Gaus mean : 37.1628

Gaus Width: 4.0690



B1L102S, U9-ch54, adc2

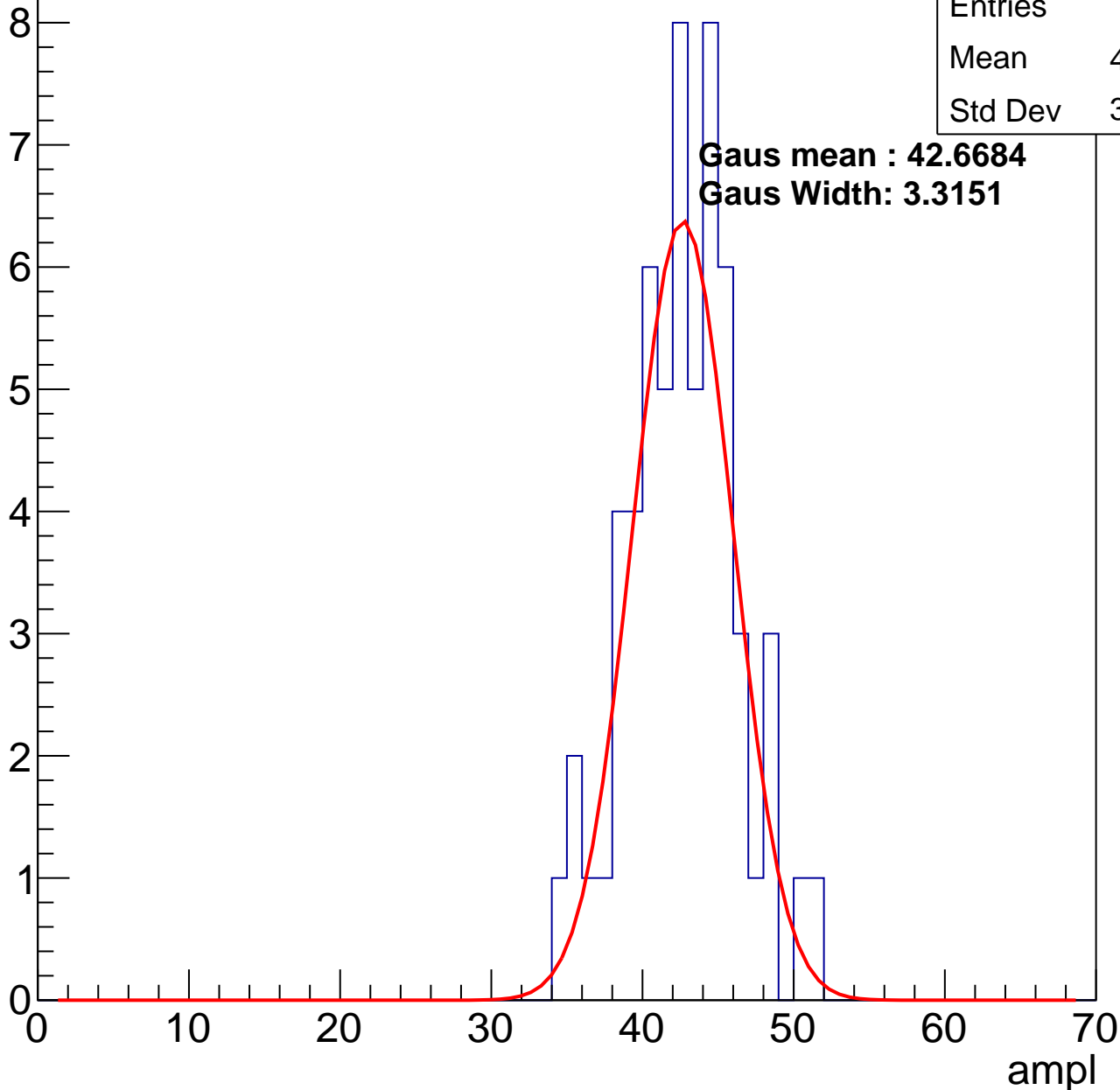
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.22
Std Dev	3.573

Gaus mean : 42.6684

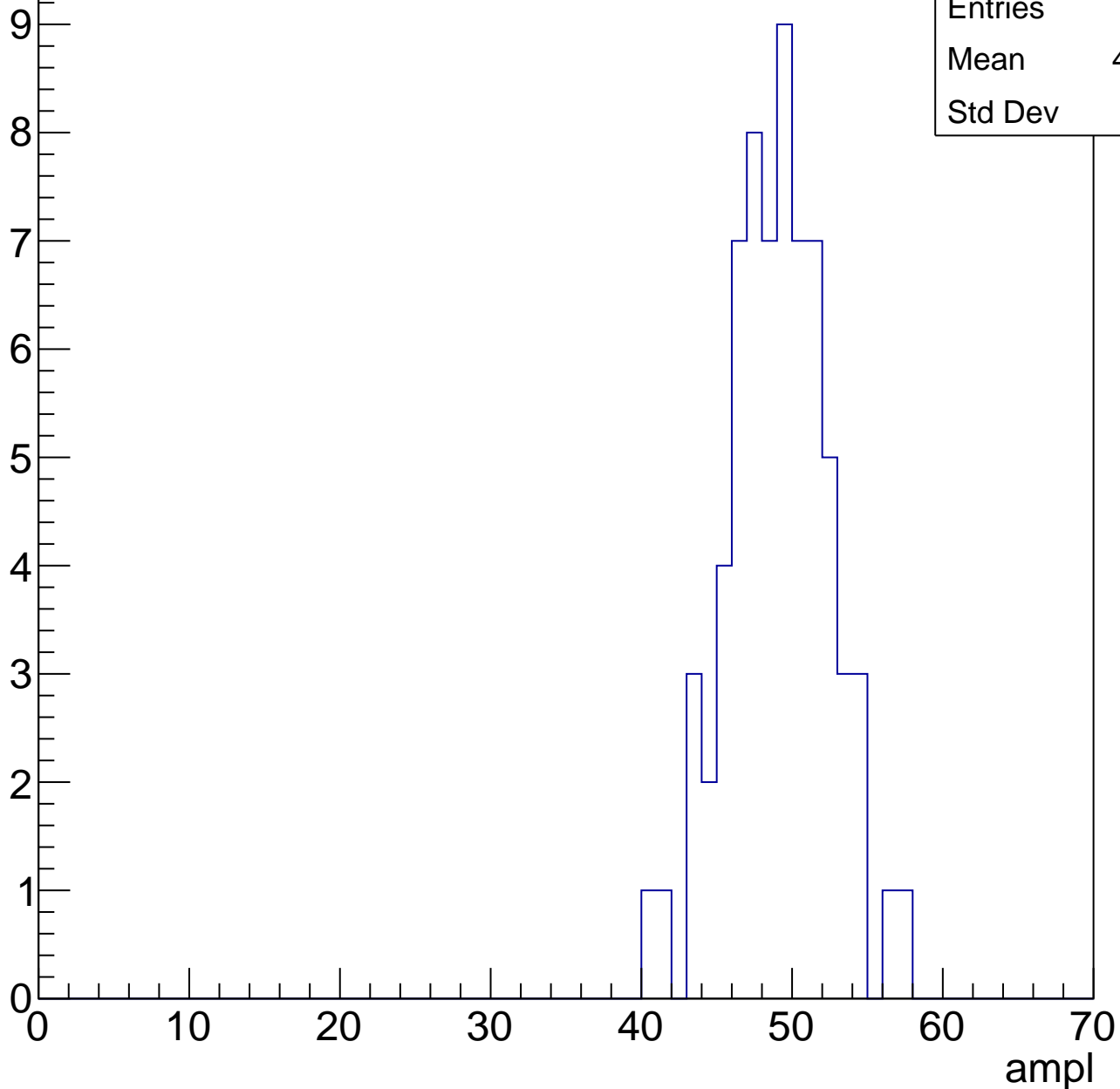
Gaus Width: 3.3151



B1L102S, U9-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



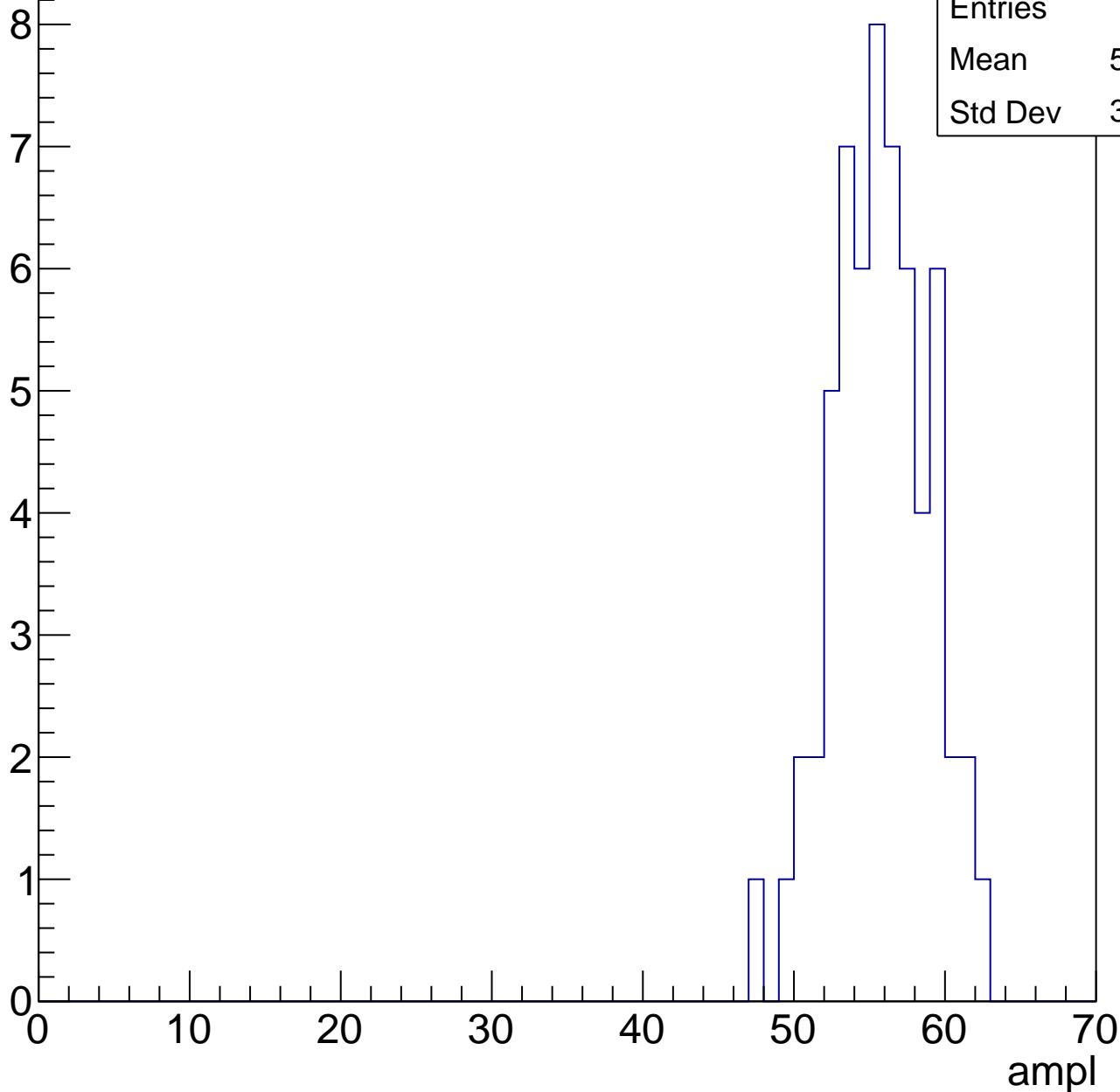
Entries	69
Mean	48.61
Std Dev	3.35

B1L102S, U9-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.28
Std Dev	3.136



B1L102S, U9-ch54, adc5

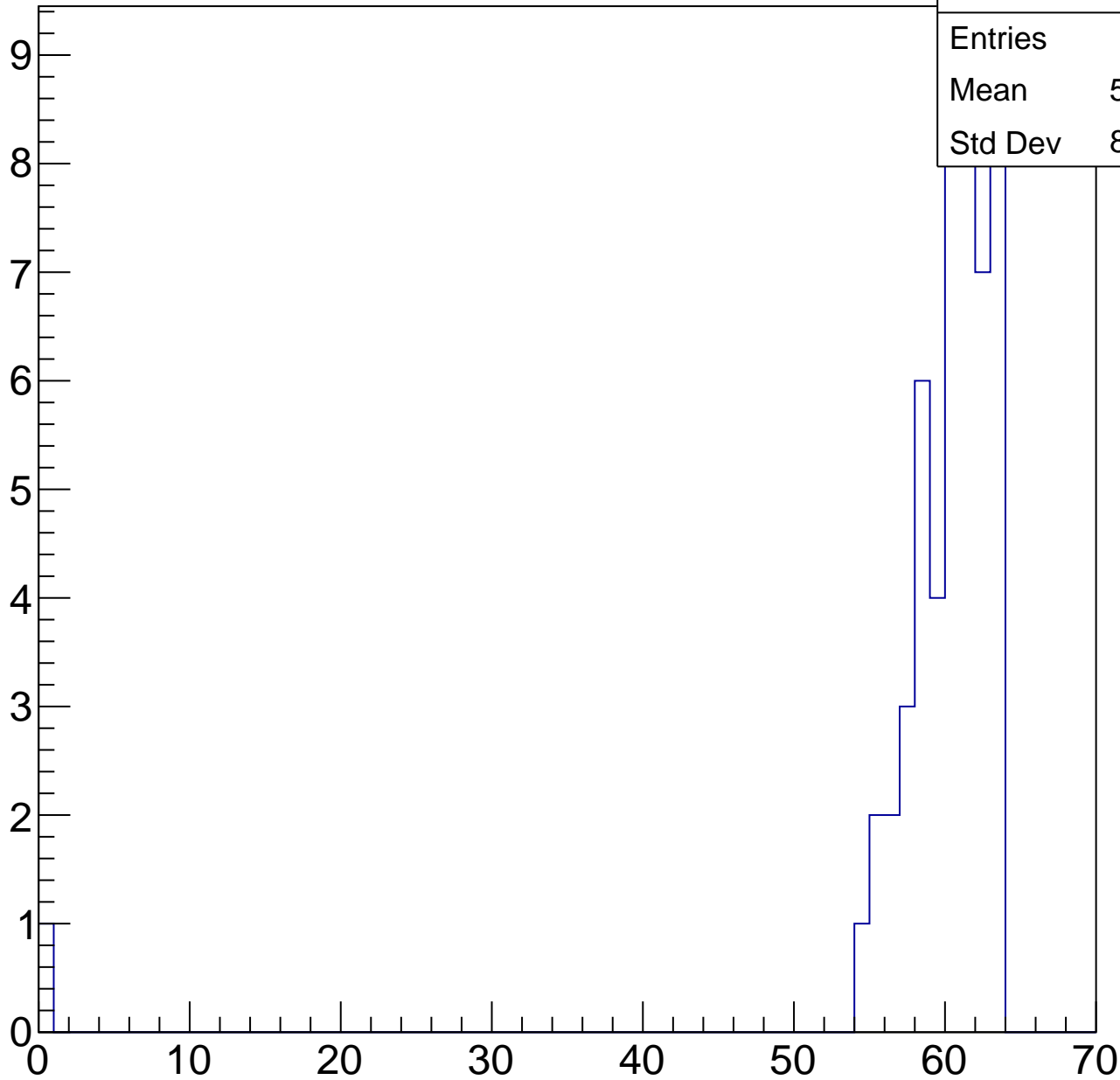
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.82
Std Dev	8.654

ampl



B1L102S, U9-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L102S, U9-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L102S, U9-ch55, adc0

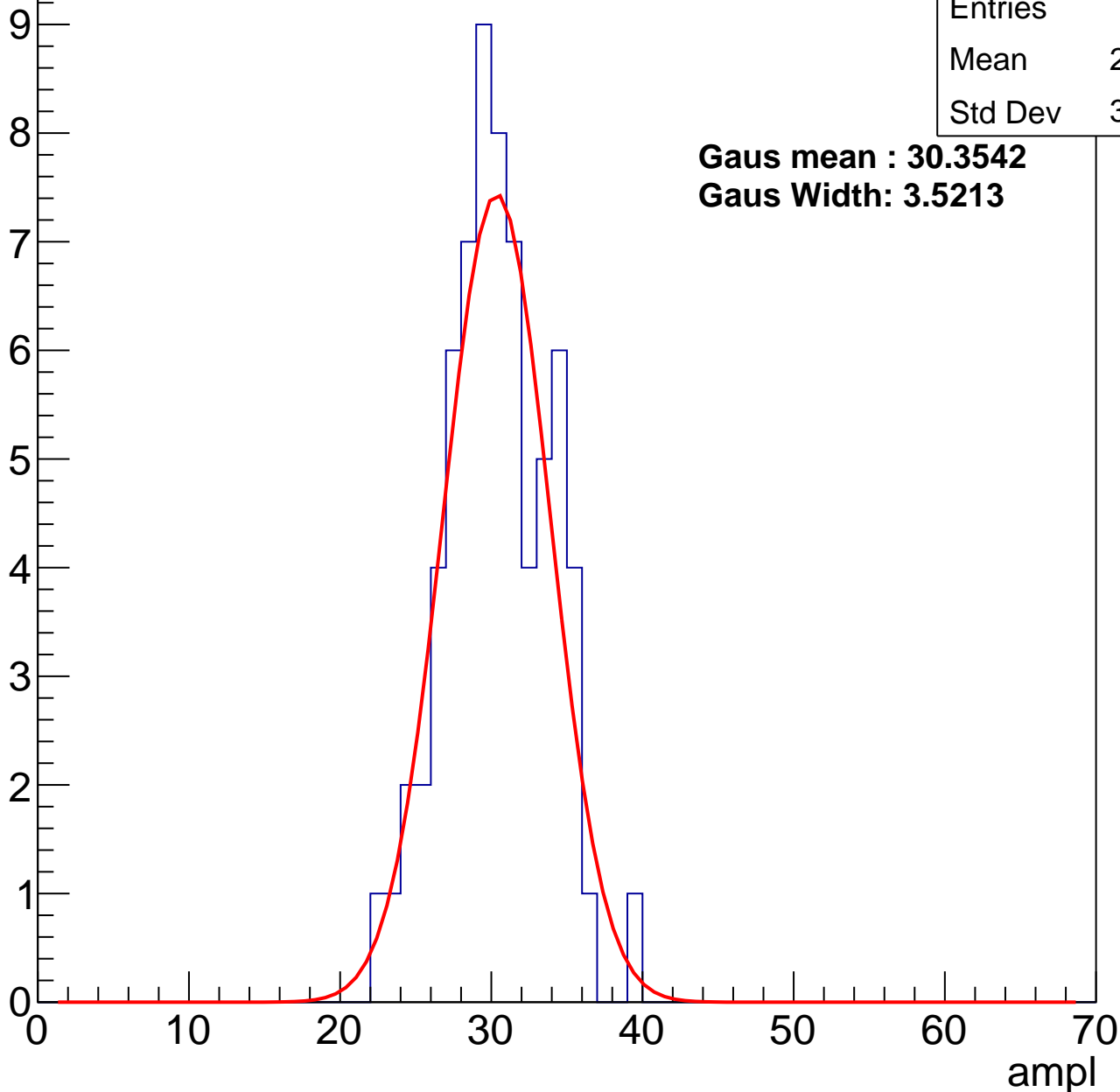
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.93
Std Dev	3.366

Gaus mean : 30.3542

Gaus Width: 3.5213



B1L102S, U9-ch55, adc1

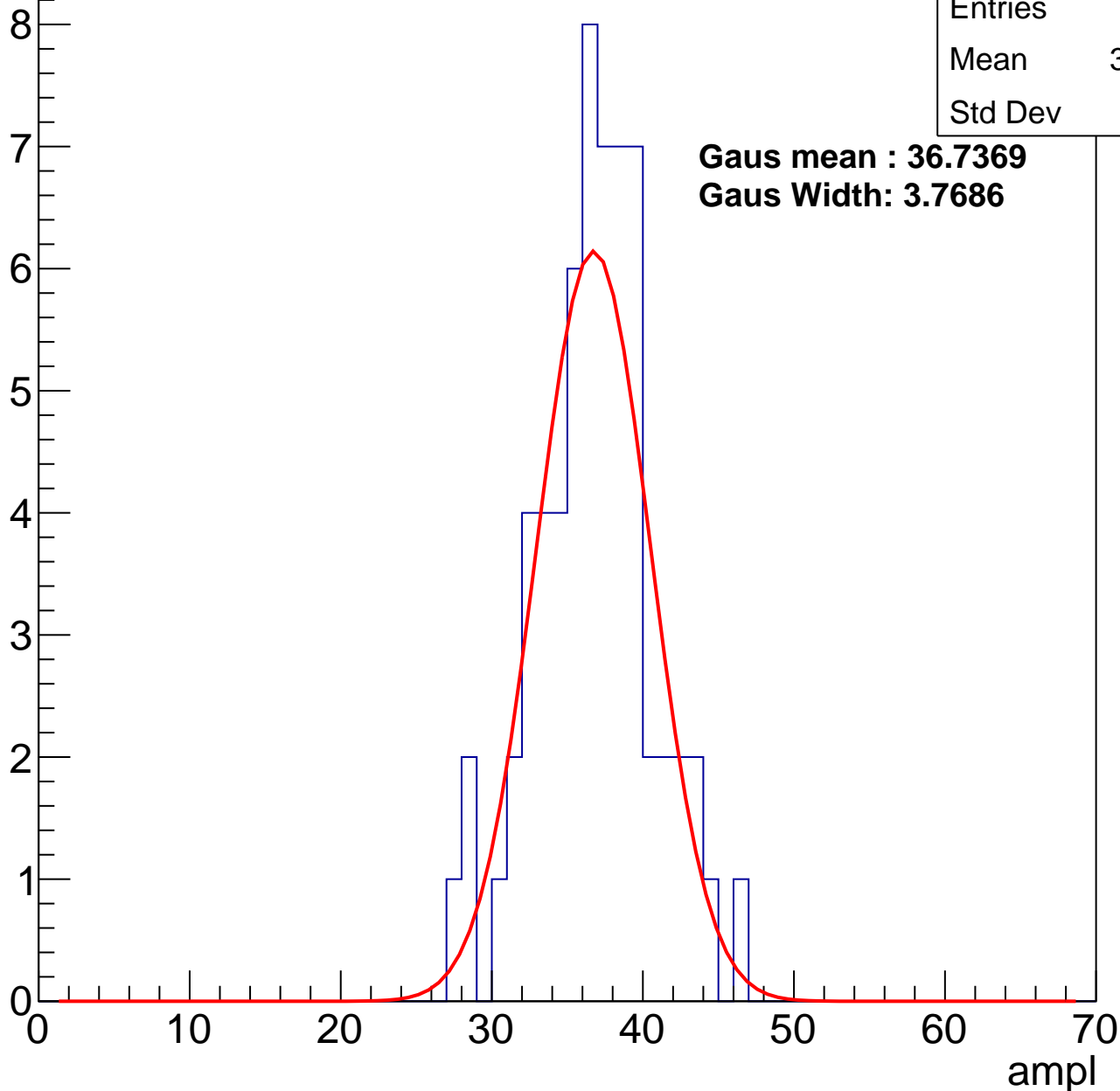
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.33
Std Dev	3.83

Gaus mean : 36.7369

Gaus Width: 3.7686

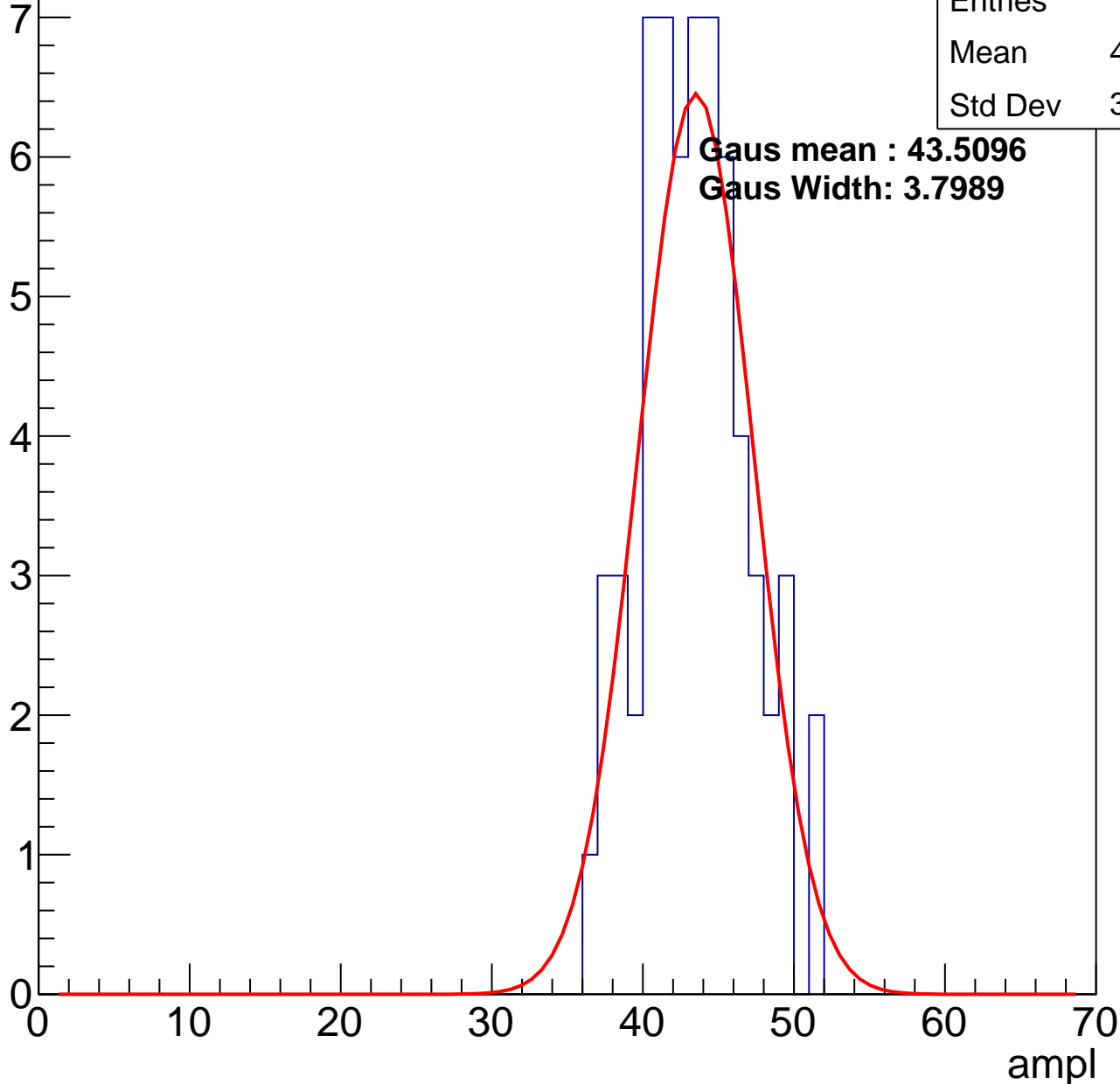


B1L102S, U9-ch55, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

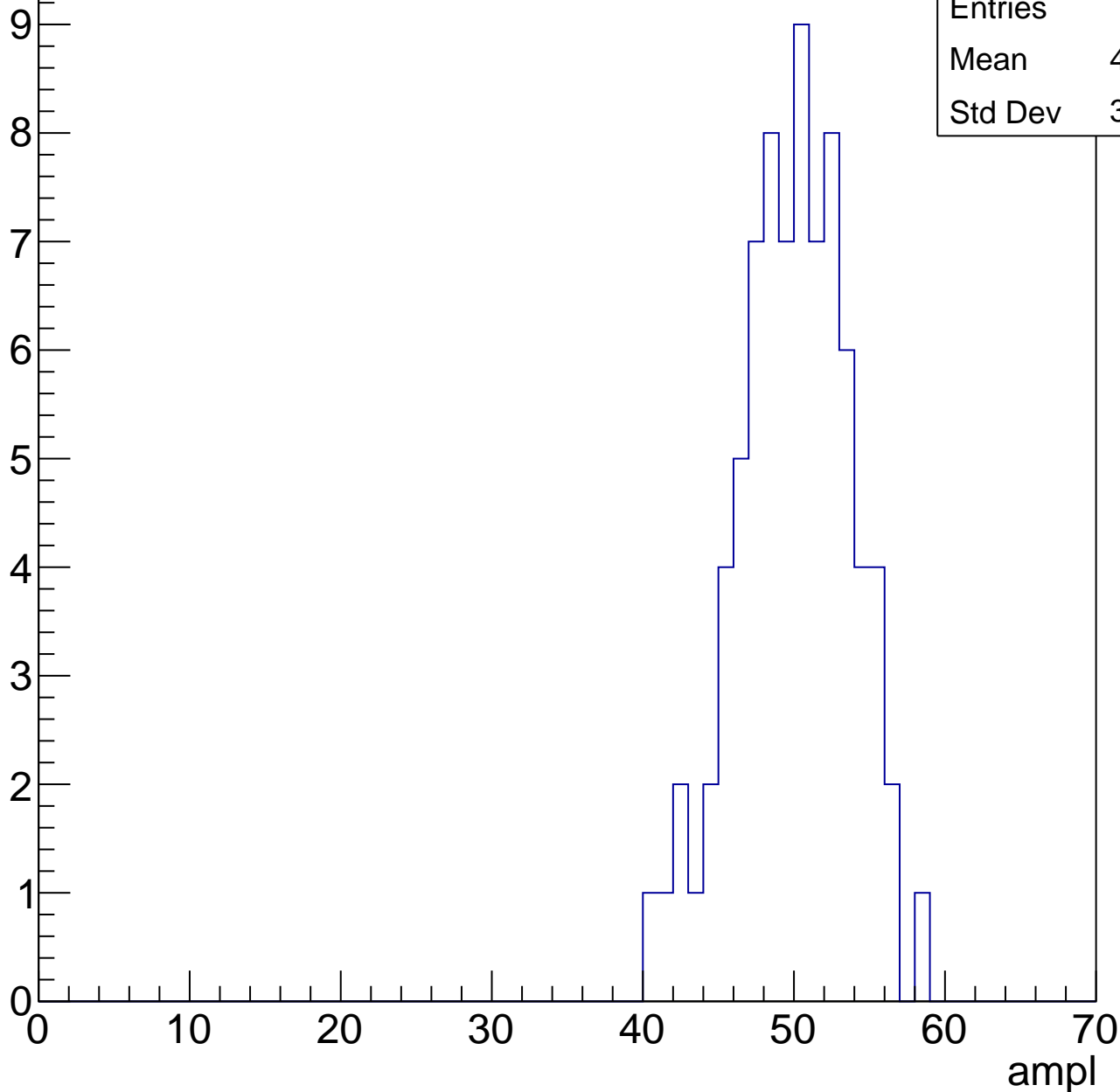
Entries	63
Mean	42.97
Std Dev	3.478



B1L102S, U9-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

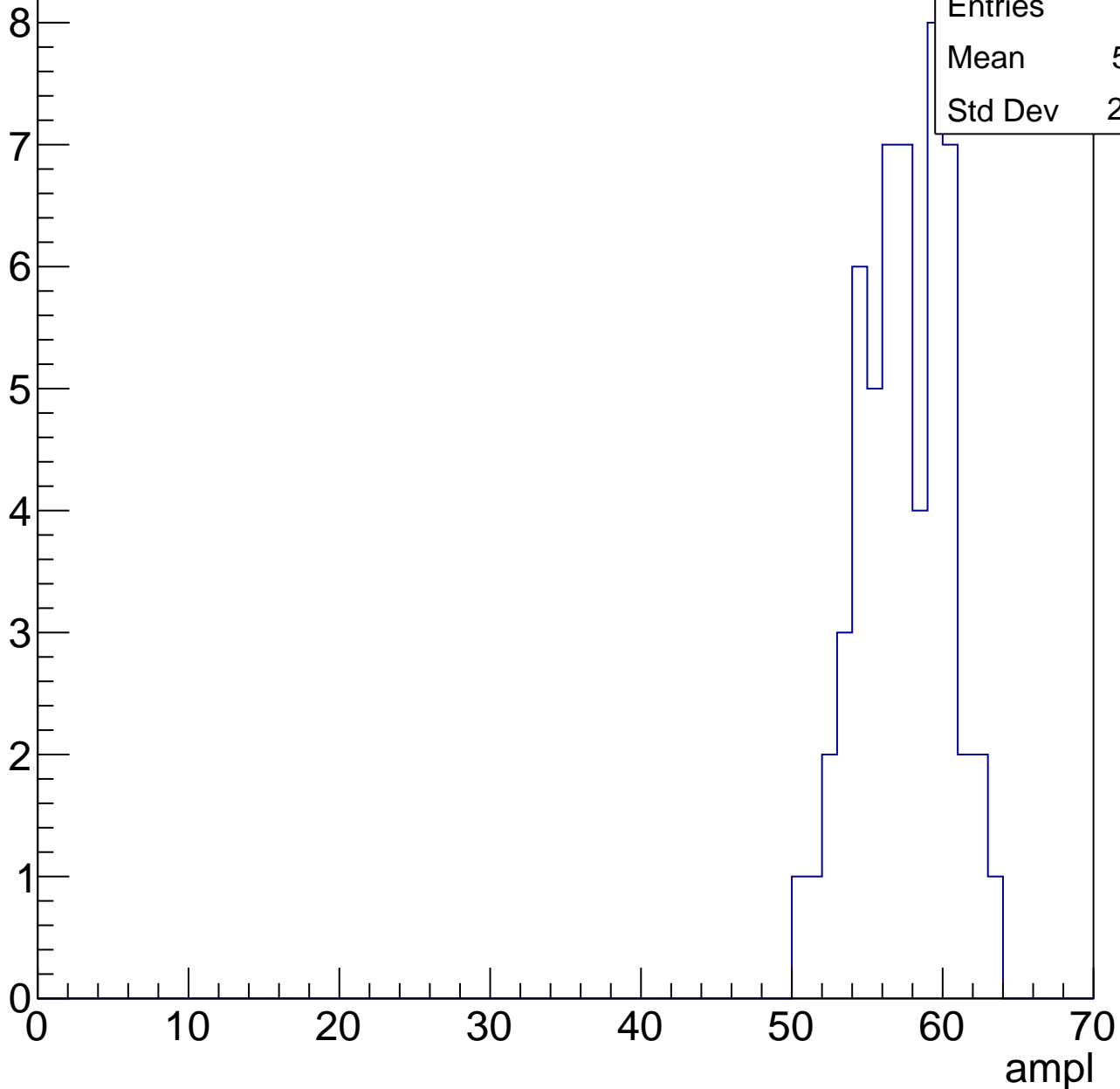


B1L102S, U9-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	56.91
Std Dev	2.929

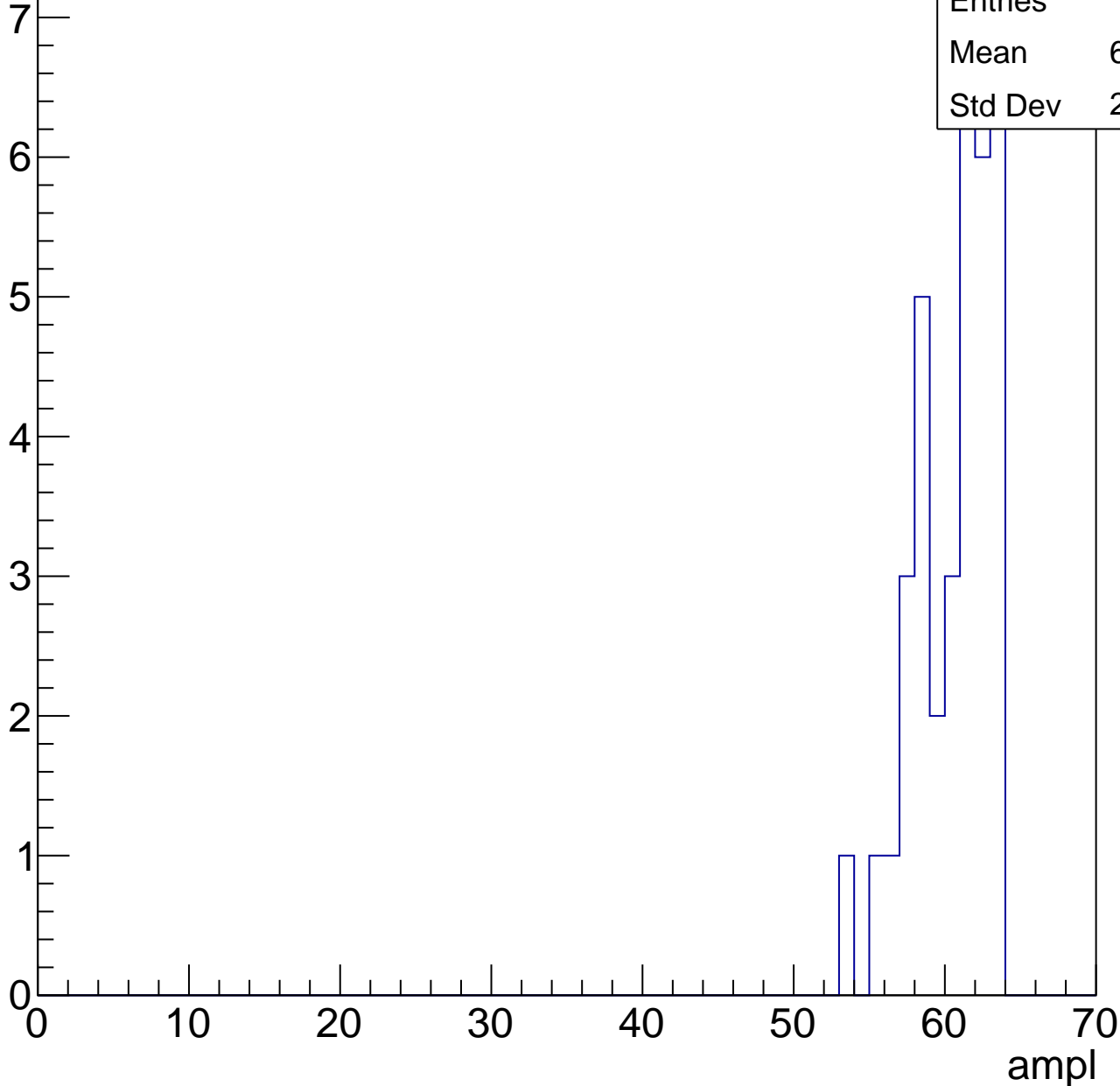


B1L102S, U9-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	60.08
Std Dev	2.543



B1L102S, U9-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch56, adc0

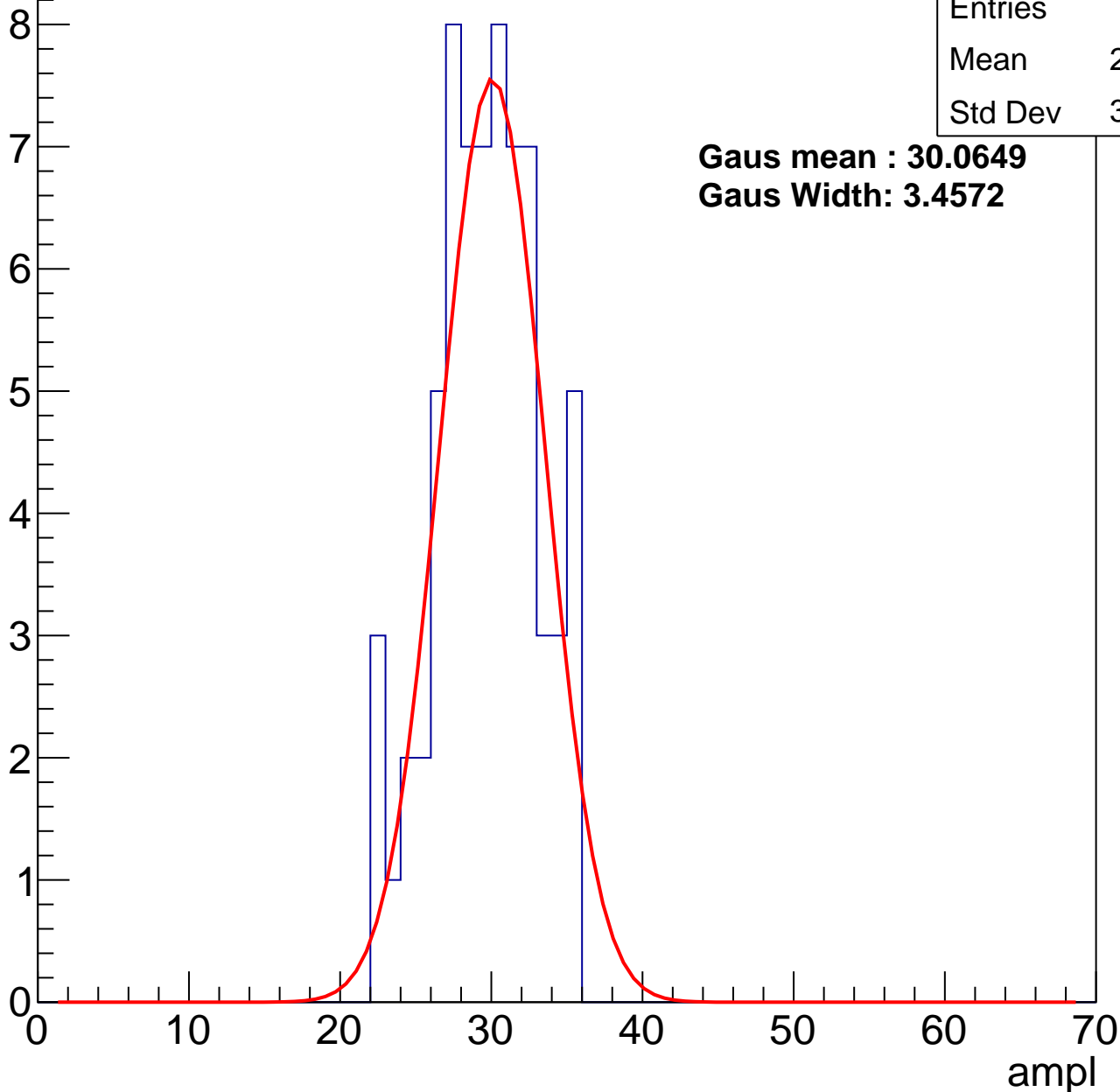
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.25
Std Dev	3.314

Gaus mean : 30.0649

Gaus Width: 3.4572



B1L102S, U9-ch56, adc1

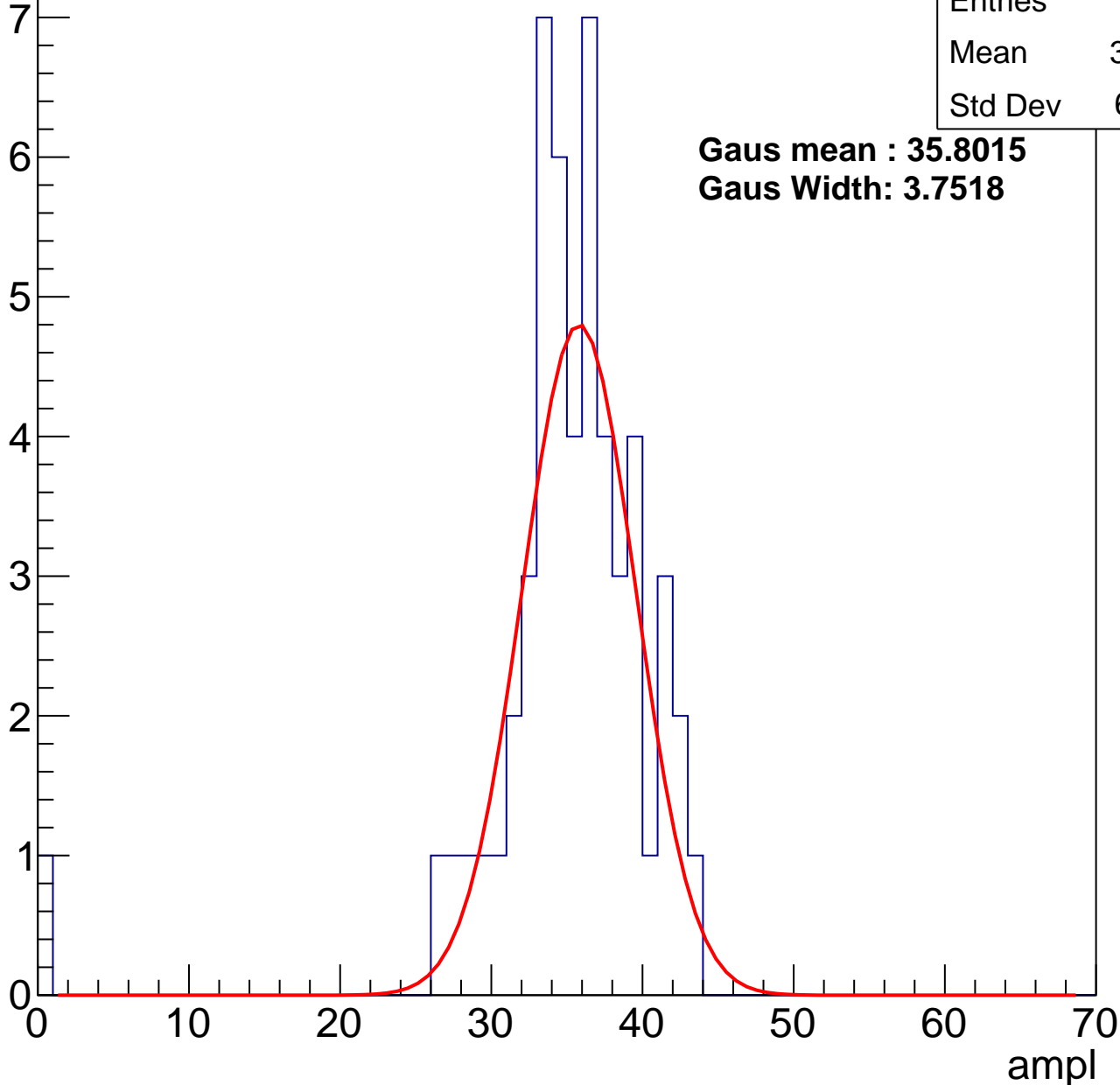
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.58
Std Dev	6.101

Gaus mean : 35.8015

Gaus Width: 3.7518



B1L102S, U9-ch56, adc2

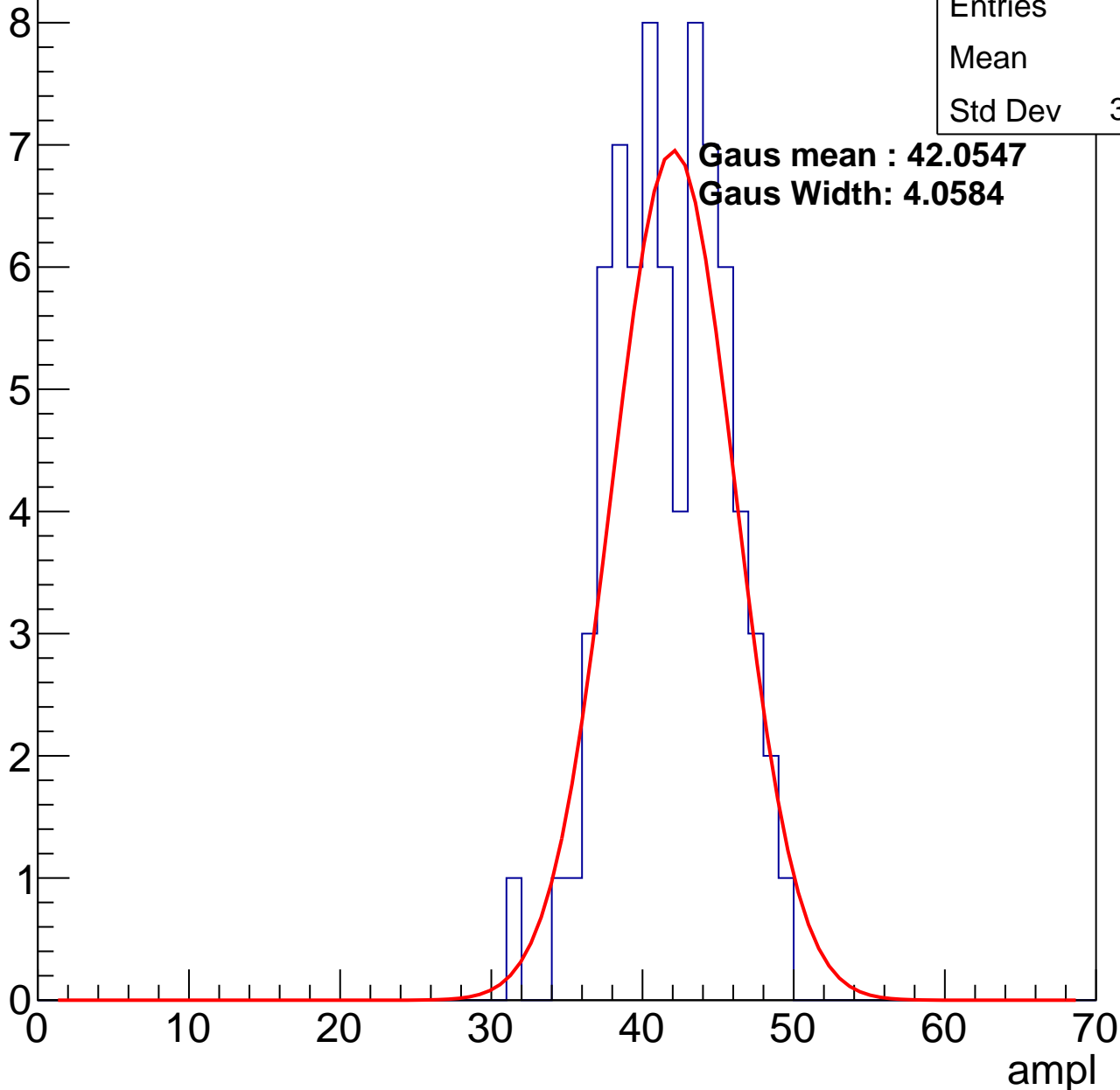
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.3
Std Dev	3.693

Gaus mean : 42.0547

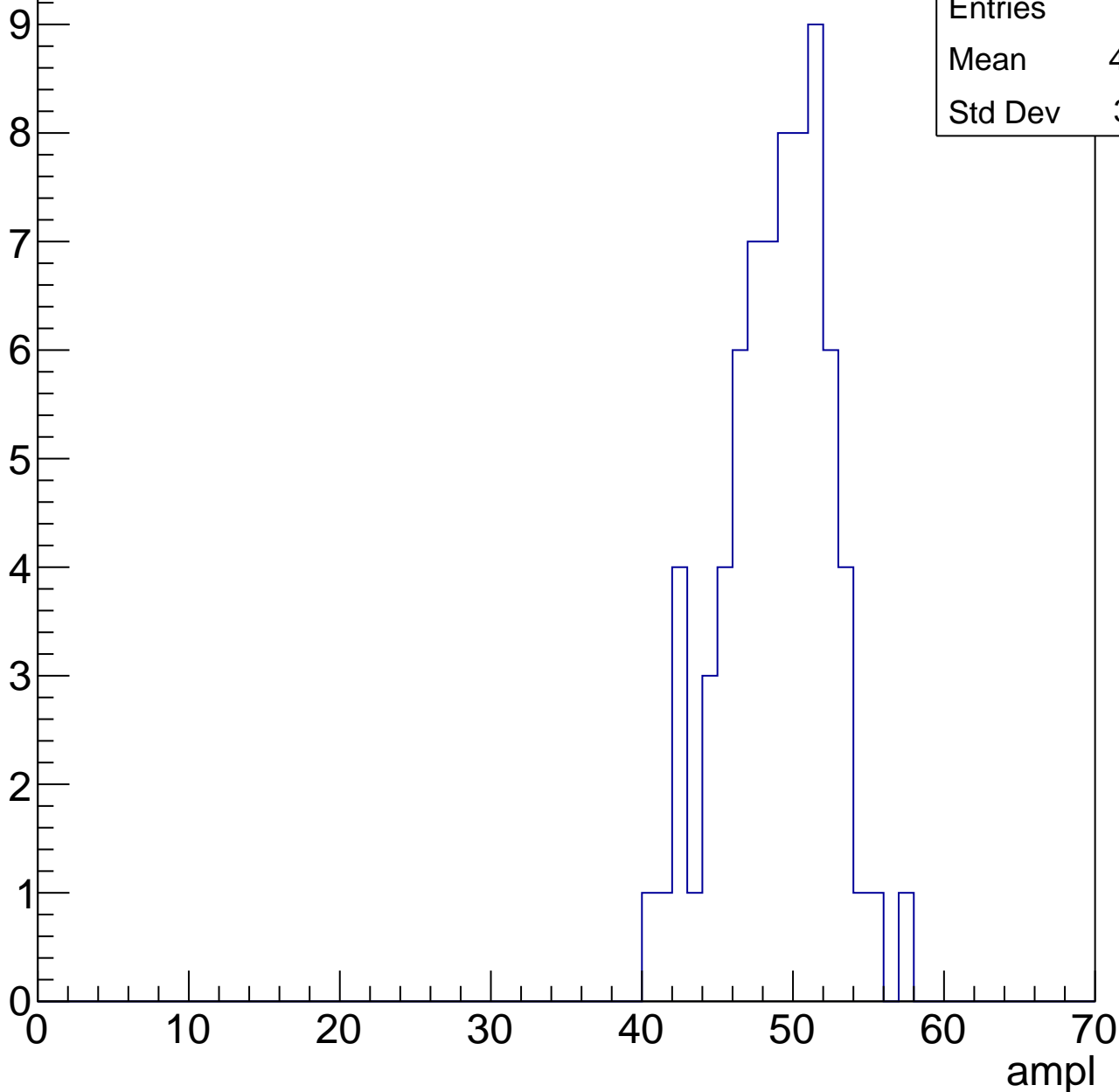
Gaus Width: 4.0584



B1L102S, U9-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

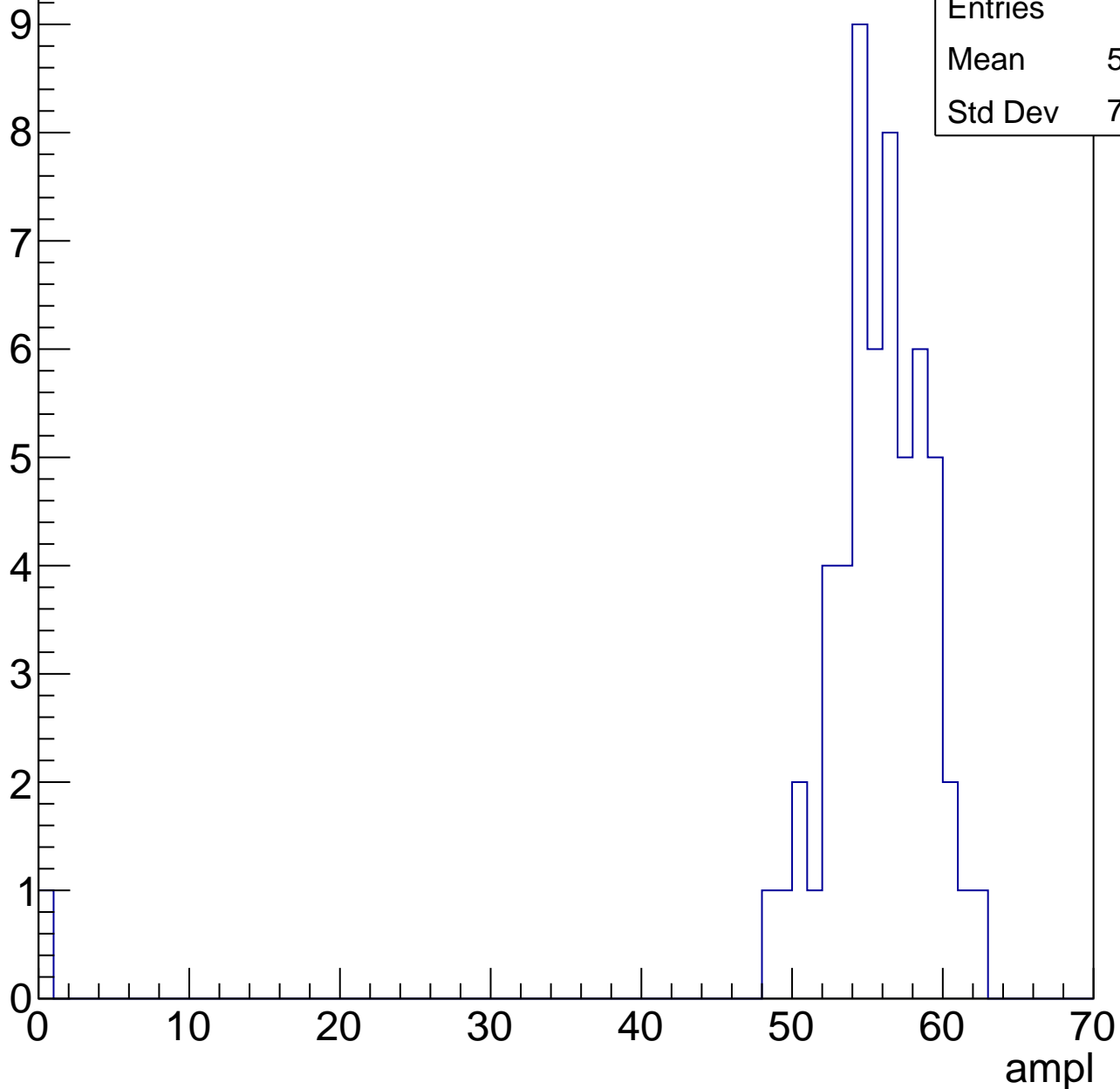
Entry



B1L102S, U9-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch56, adc5

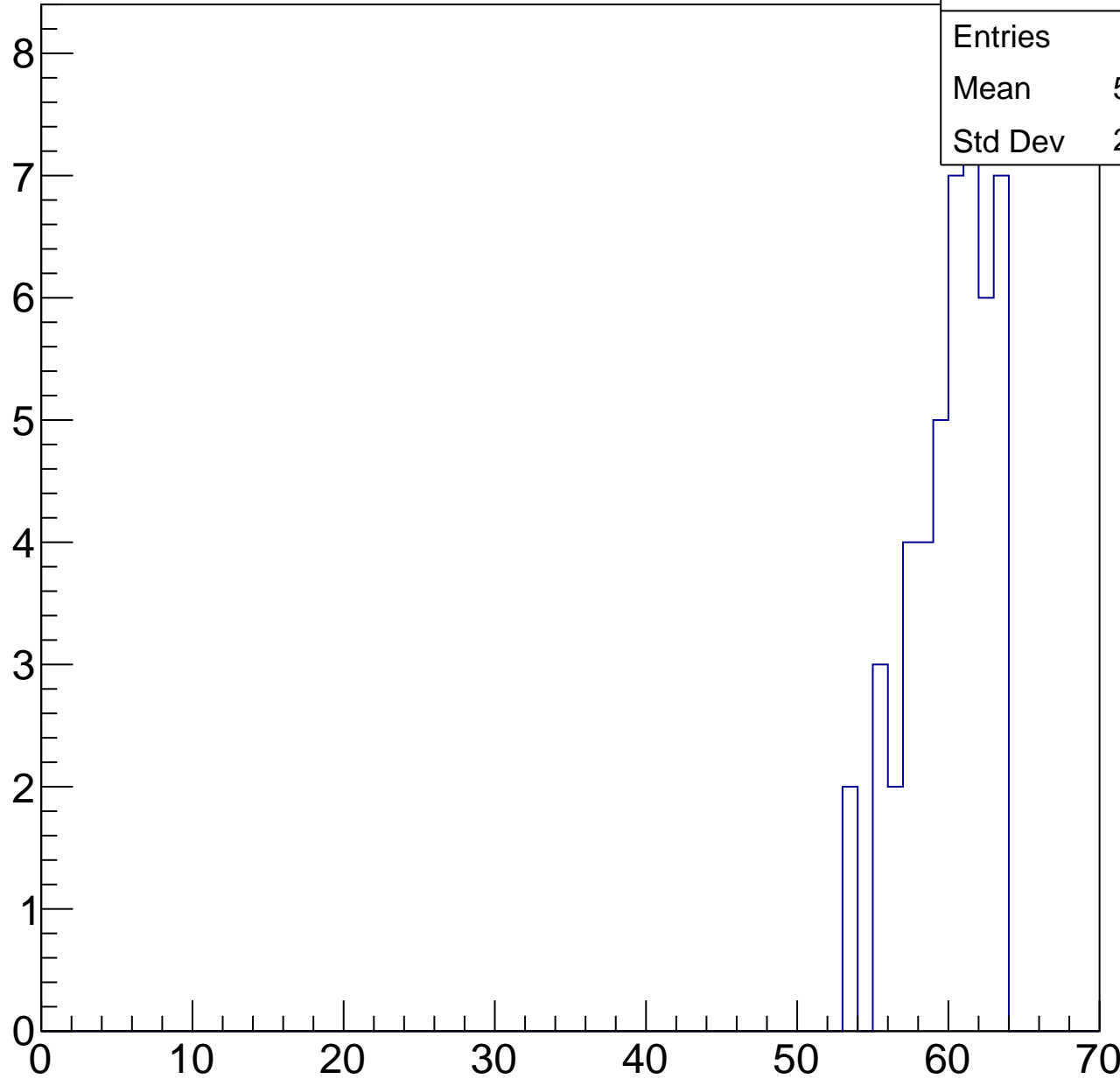
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.56
Std Dev	2.692

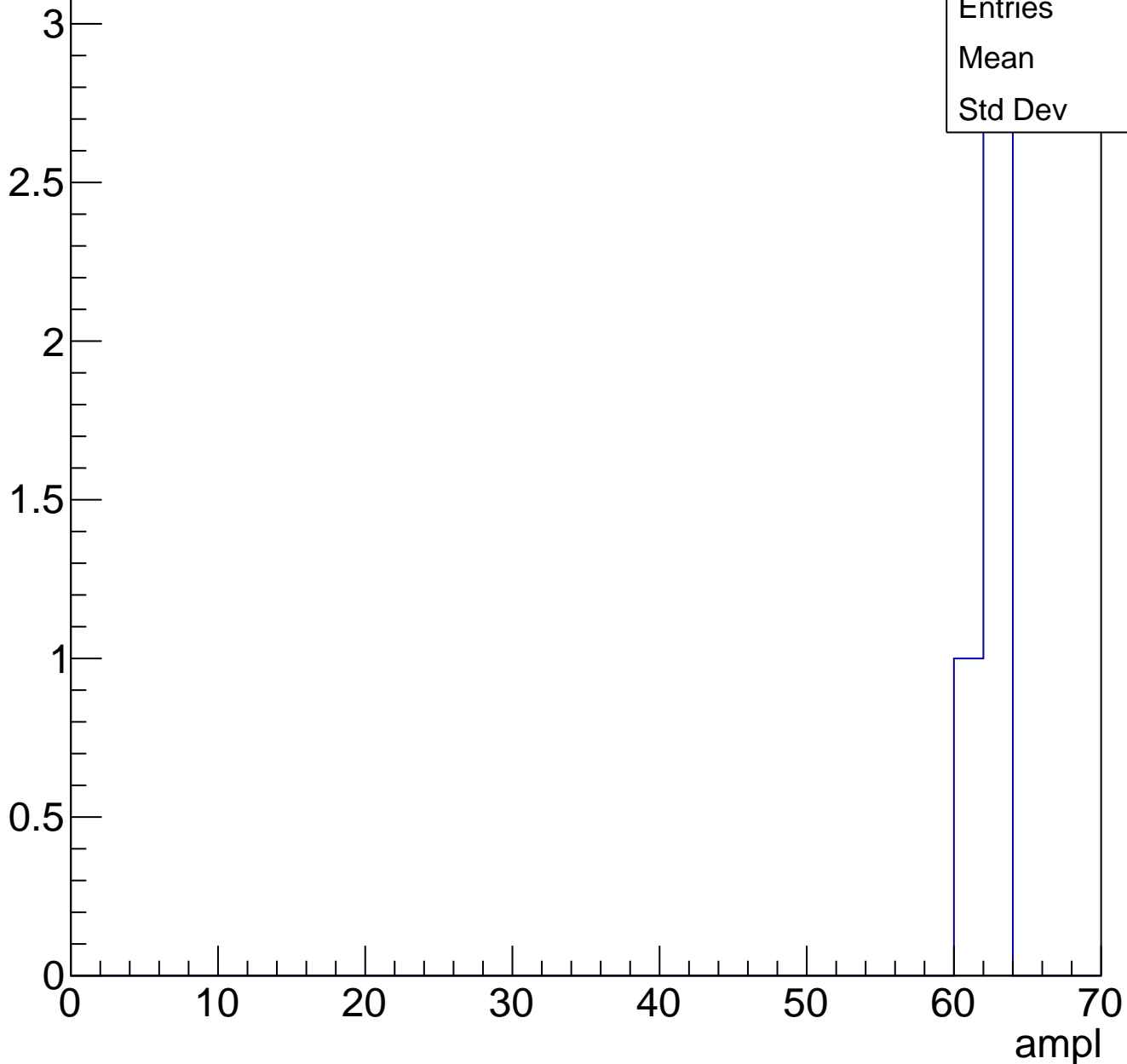
ampl



B1L102S, U9-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch57, adc0

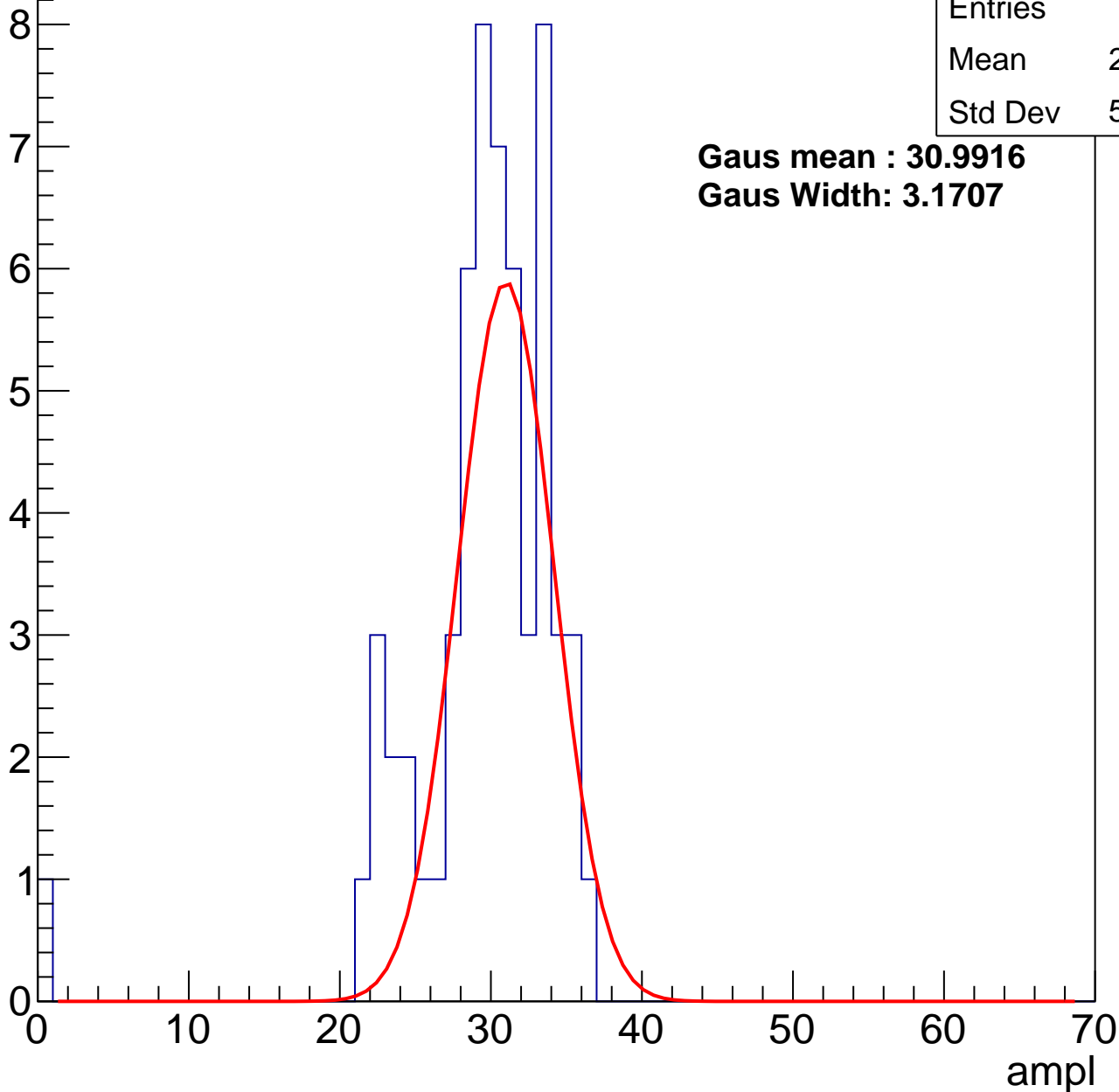
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.02
Std Dev	5.264

Gaus mean : 30.9916

Gaus Width: 3.1707



B1L102S, U9-ch57, adc1

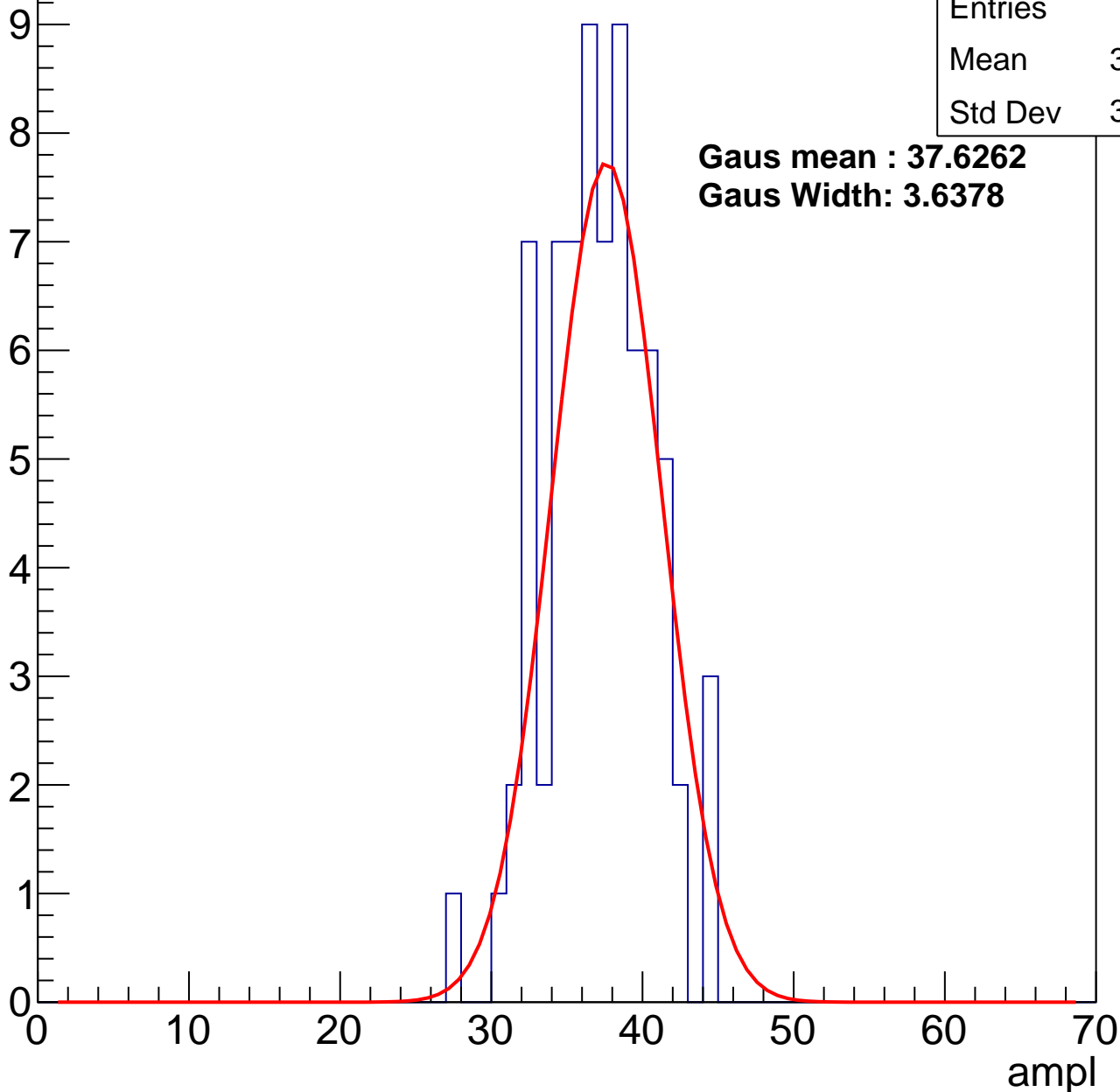
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.65
Std Dev	3.454

Gaus mean : 37.6262

Gaus Width: 3.6378



B1L102S, U9-ch57, adc2

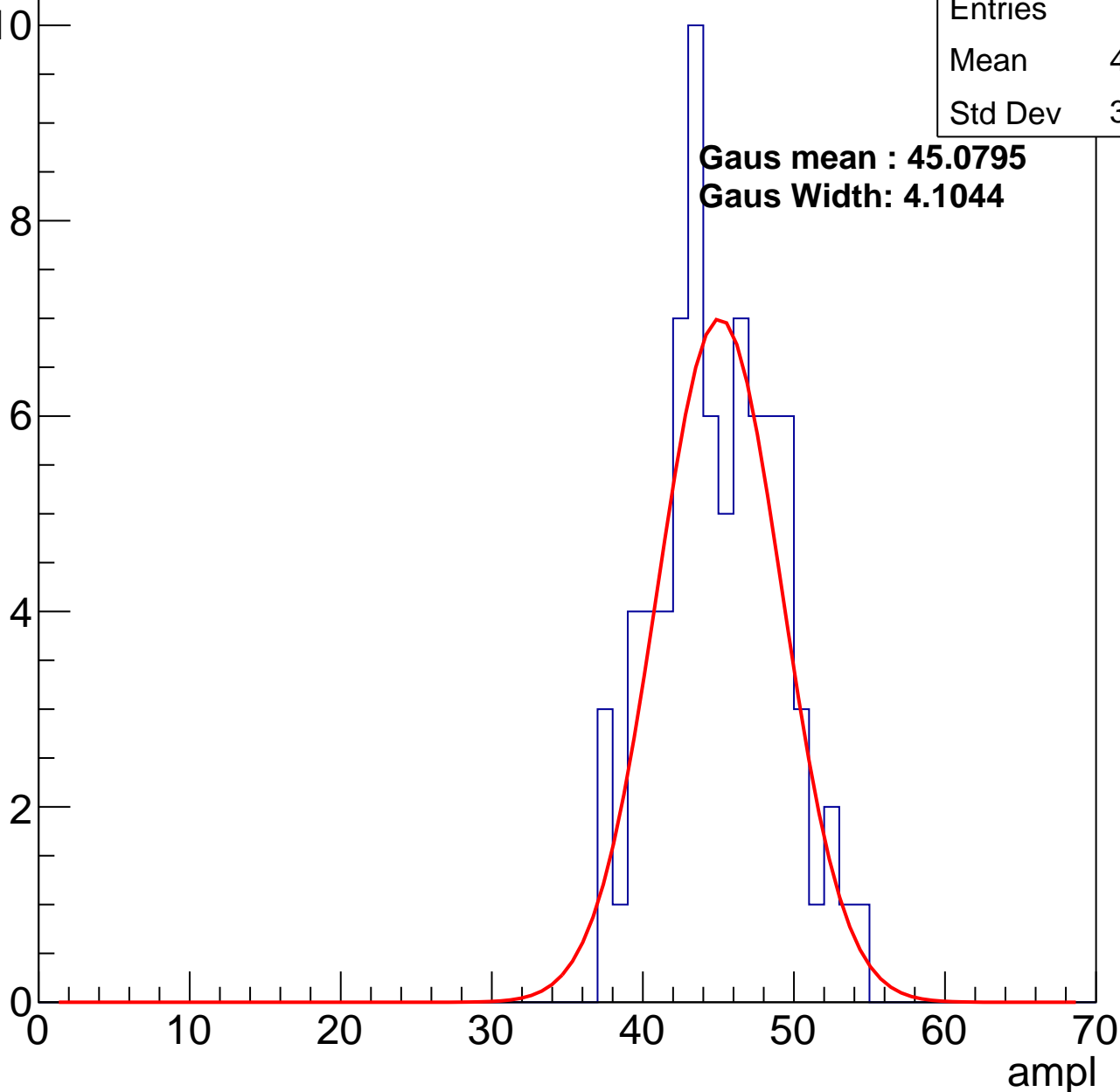
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	44.68
Std Dev	3.923

Gaus mean : 45.0795

Gaus Width: 4.1044

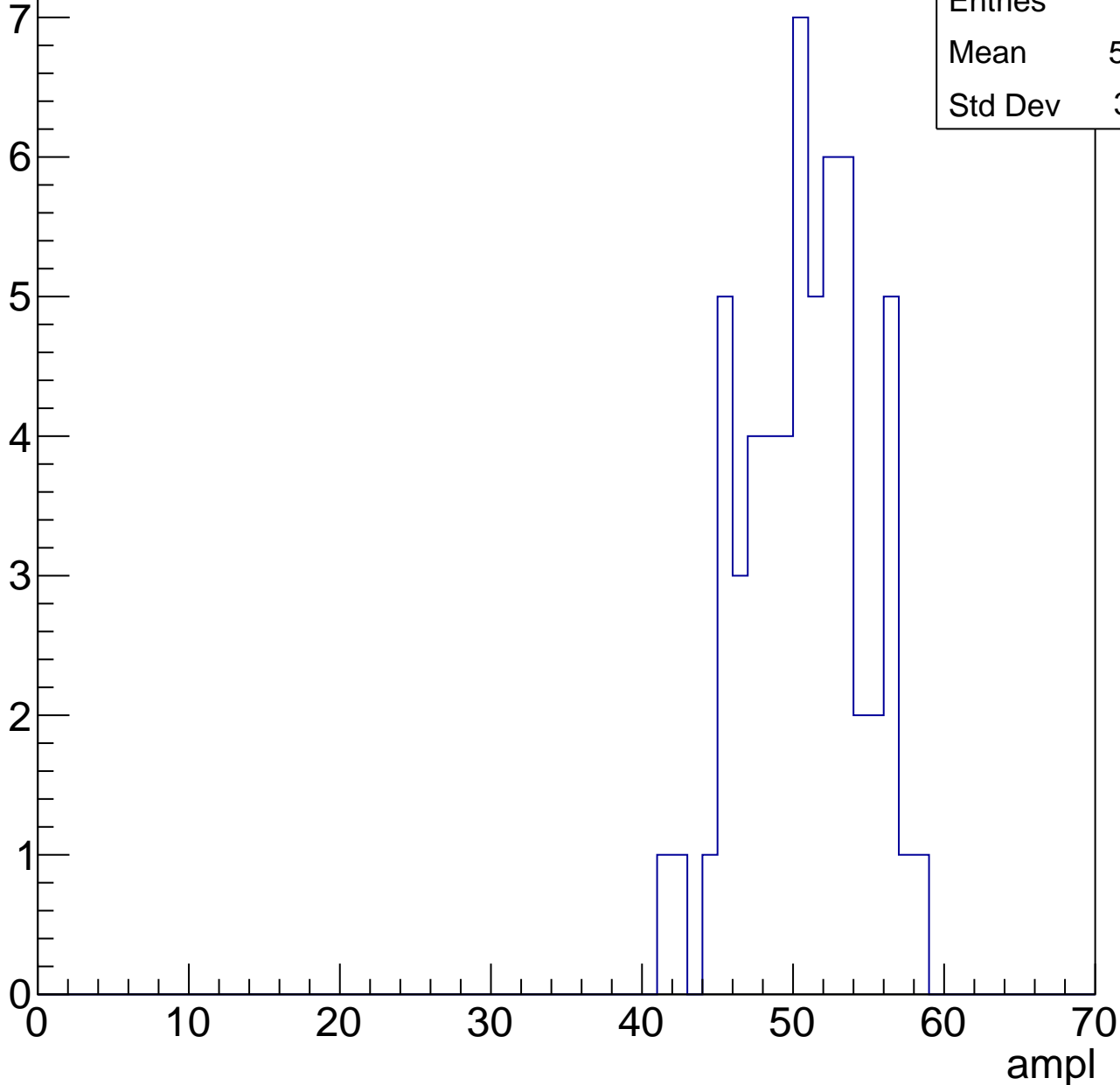


B1L102S, U9-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.24
Std Dev	3.861

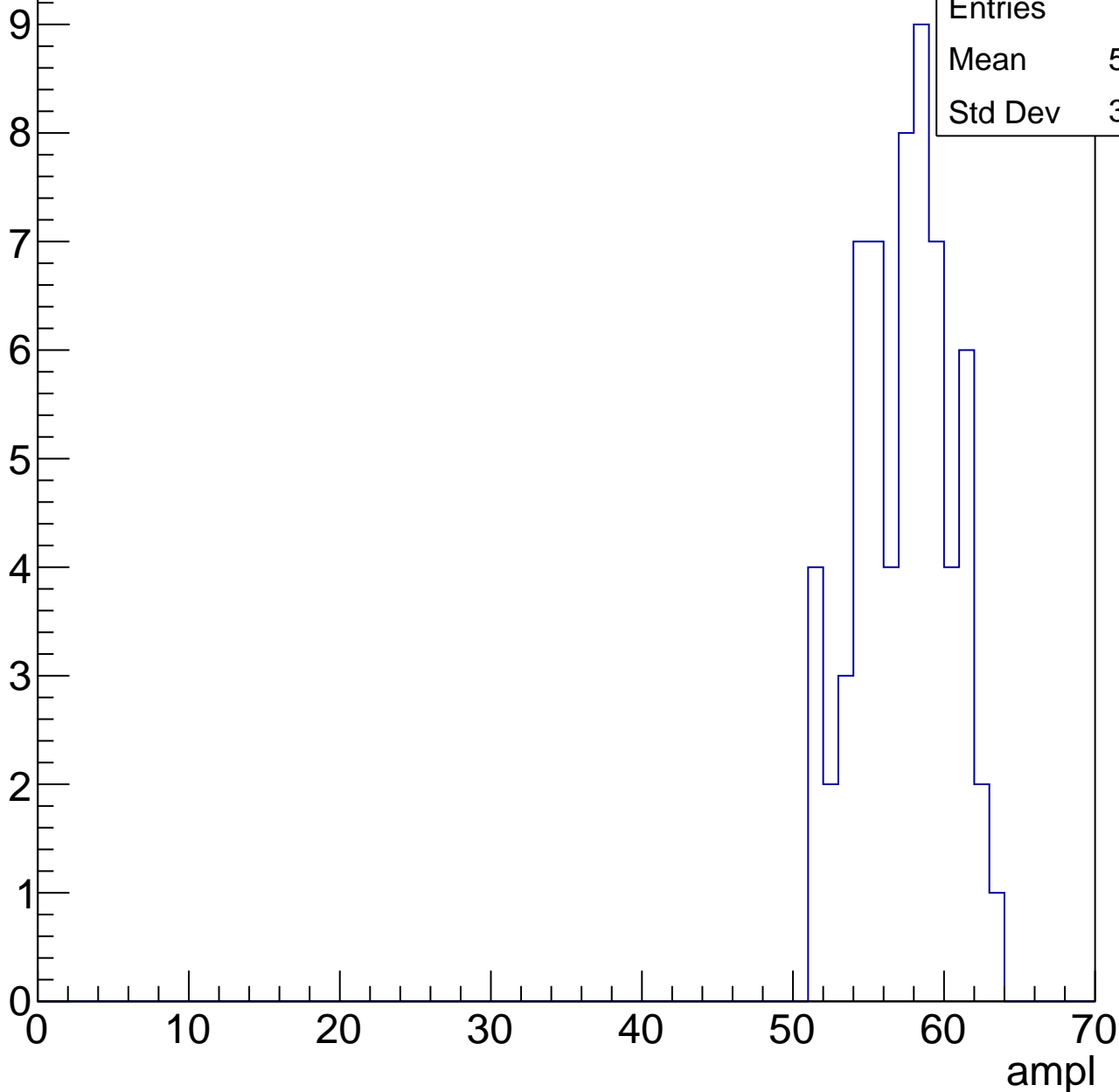


B1L102S, U9-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	56.84
Std Dev	3.037

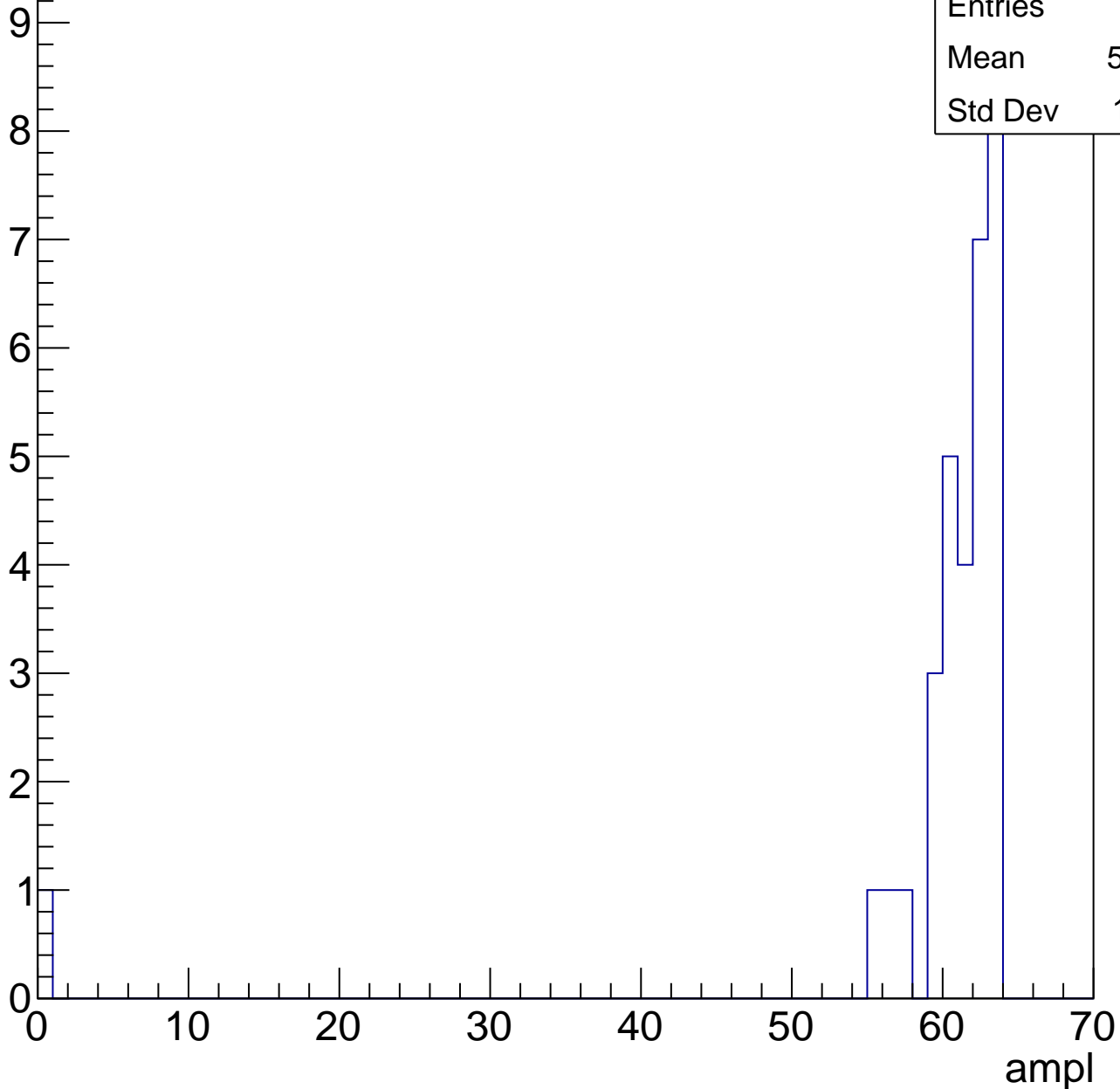


B1L102S, U9-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	59.06
Std Dev	10.81



B1L102S, U9-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch58, adc0

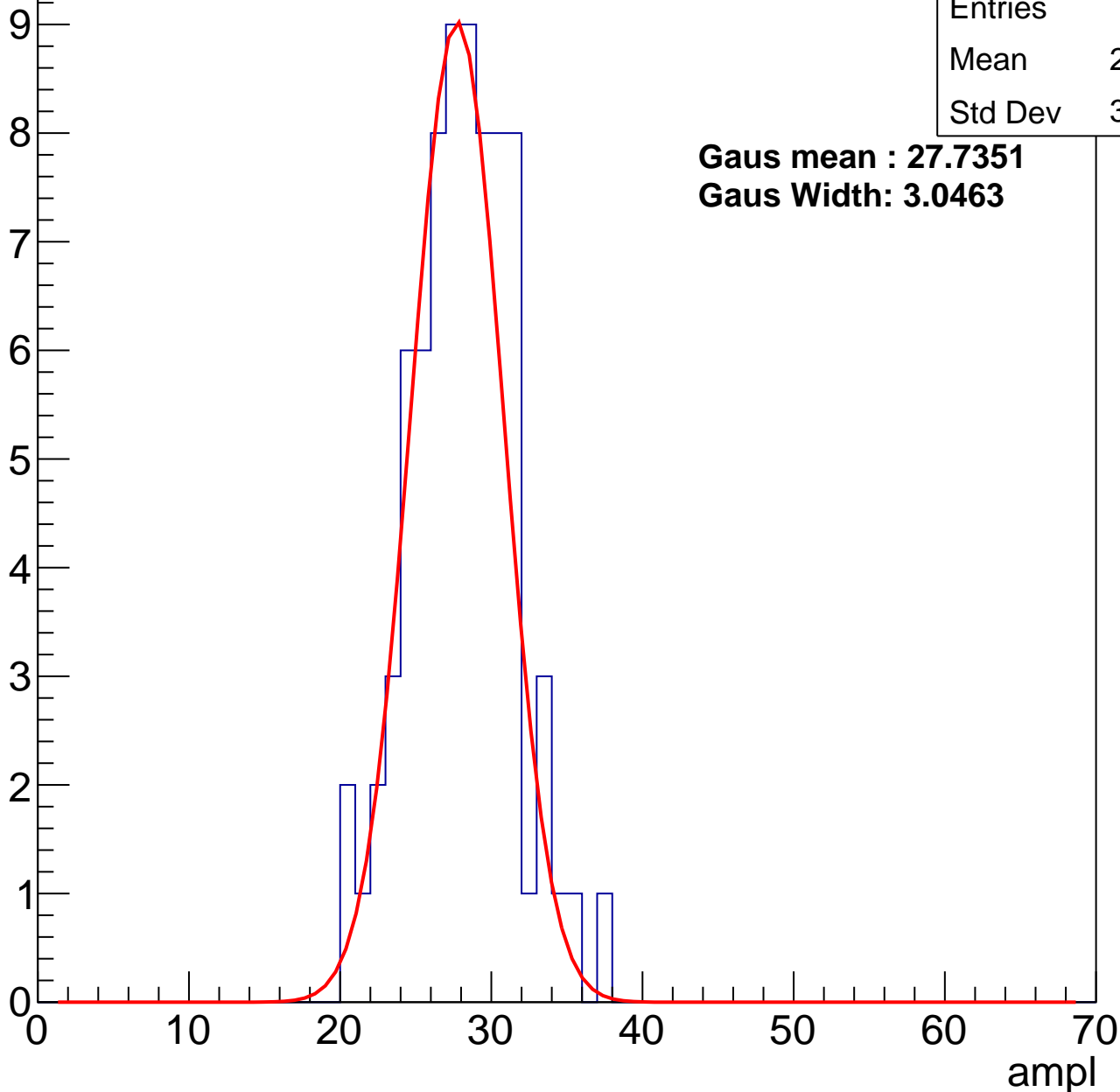
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	27.64
Std Dev	3.369

Gaus mean : 27.7351

Gaus Width: 3.0463



B1L102S, U9-ch58, adc1

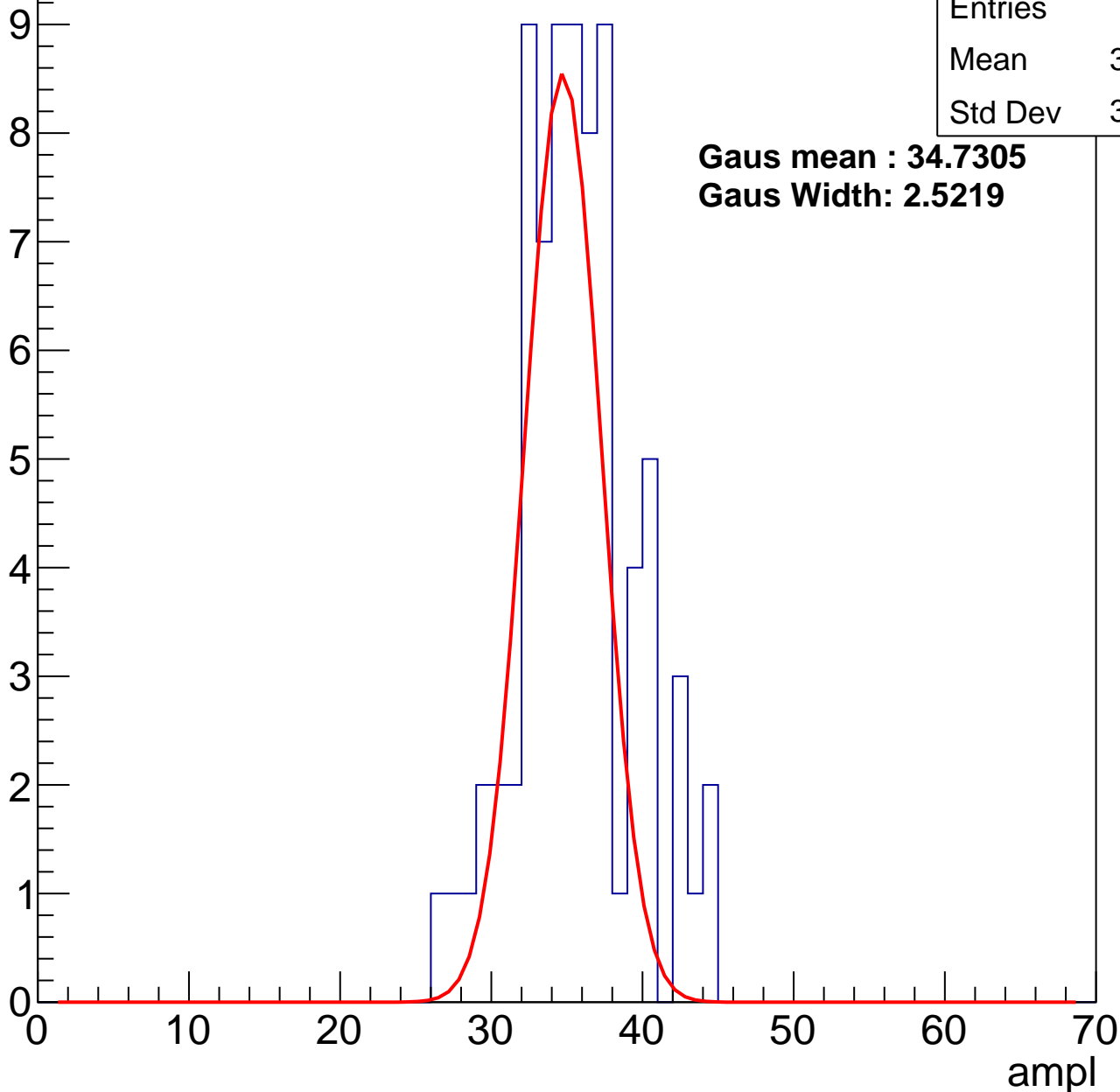
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.17
Std Dev	3.768

Gaus mean : 34.7305

Gaus Width: 2.5219



B1L102S, U9-ch58, adc2

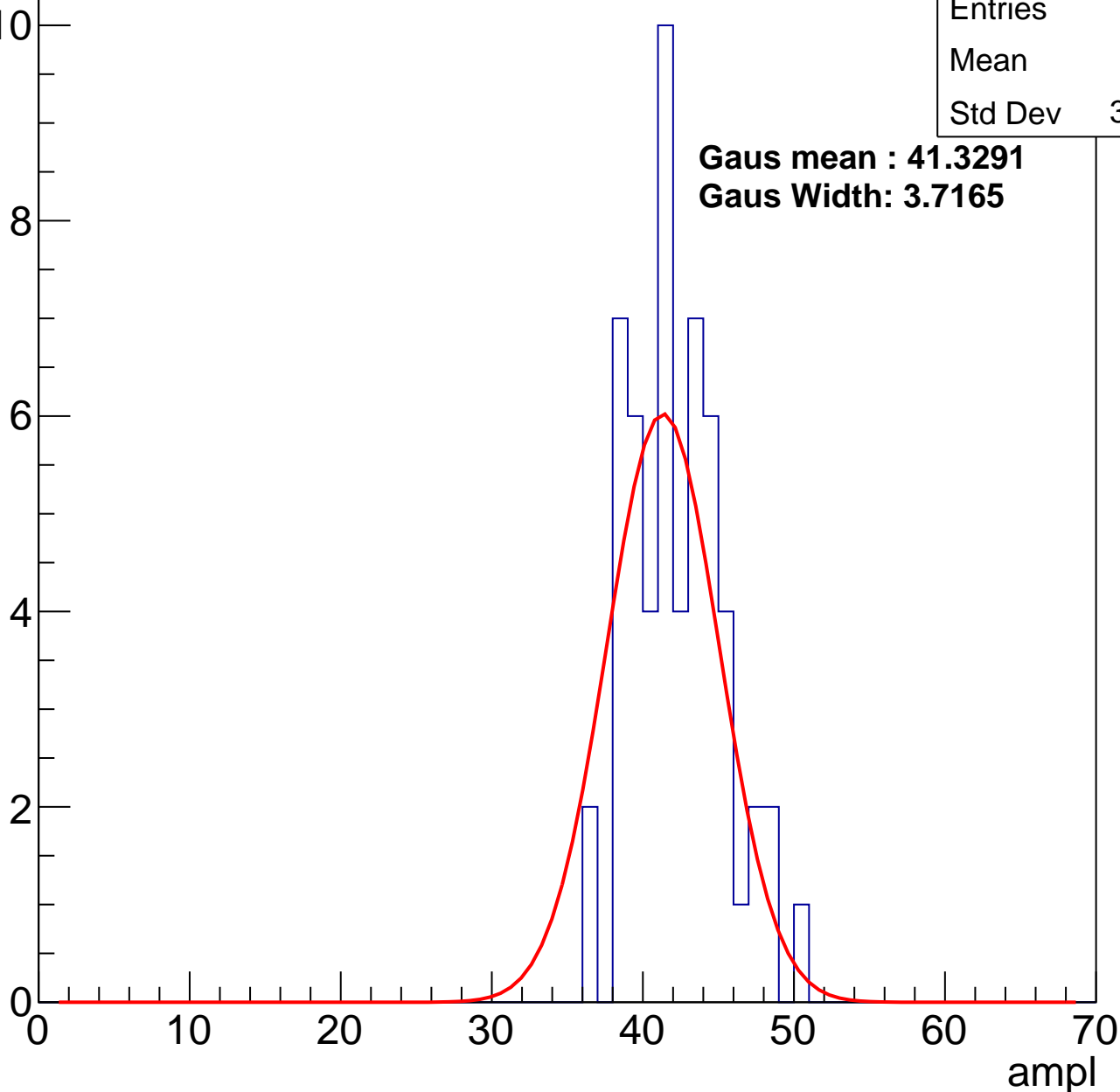
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.8
Std Dev	3.079

Gaus mean : 41.3291

Gaus Width: 3.7165

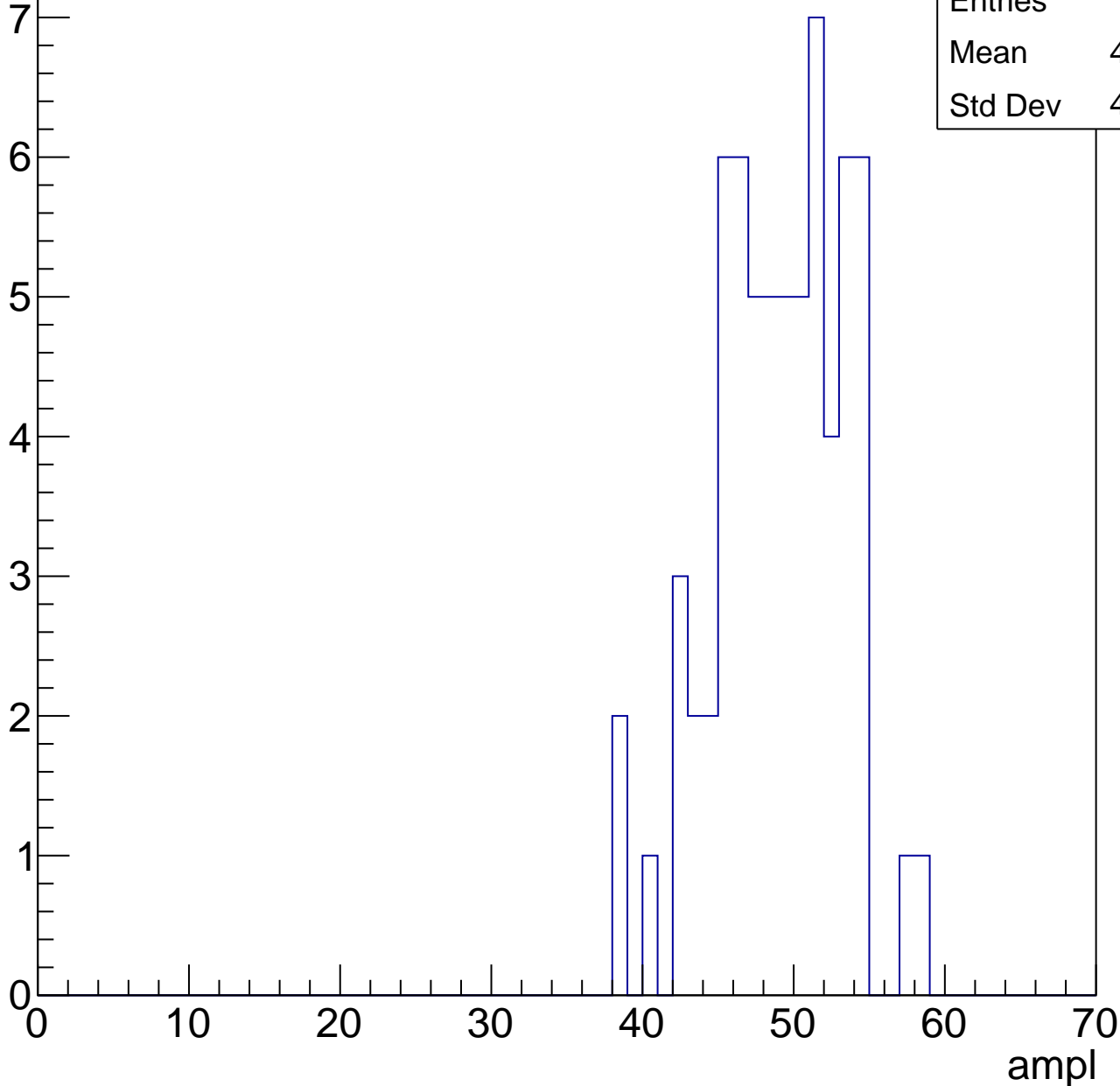


B1L102S, U9-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.57
Std Dev	4.257

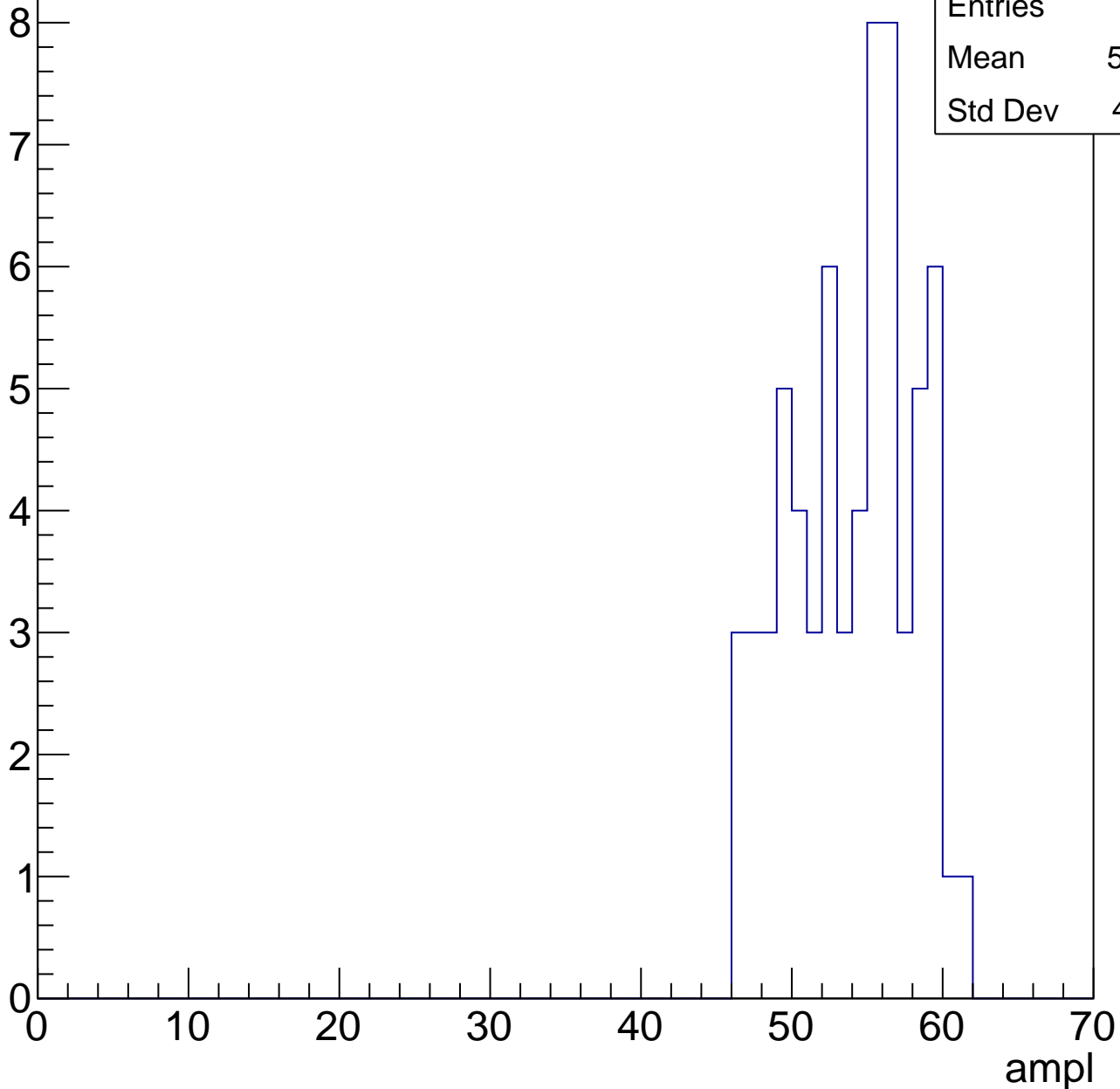


B1L102S, U9-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	53.52
Std Dev	4.001

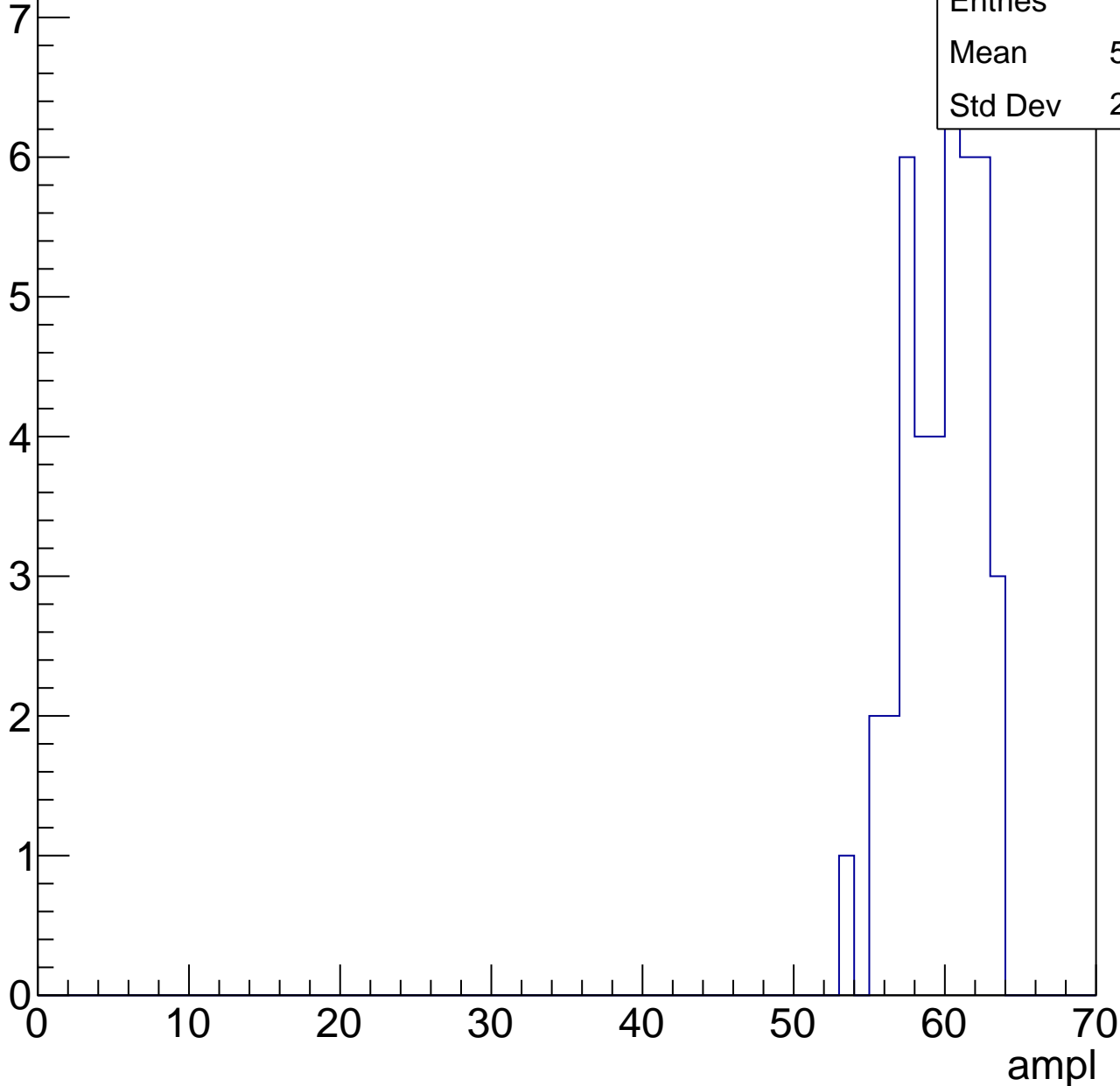


B1L102S, U9-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	59.32
Std Dev	2.434

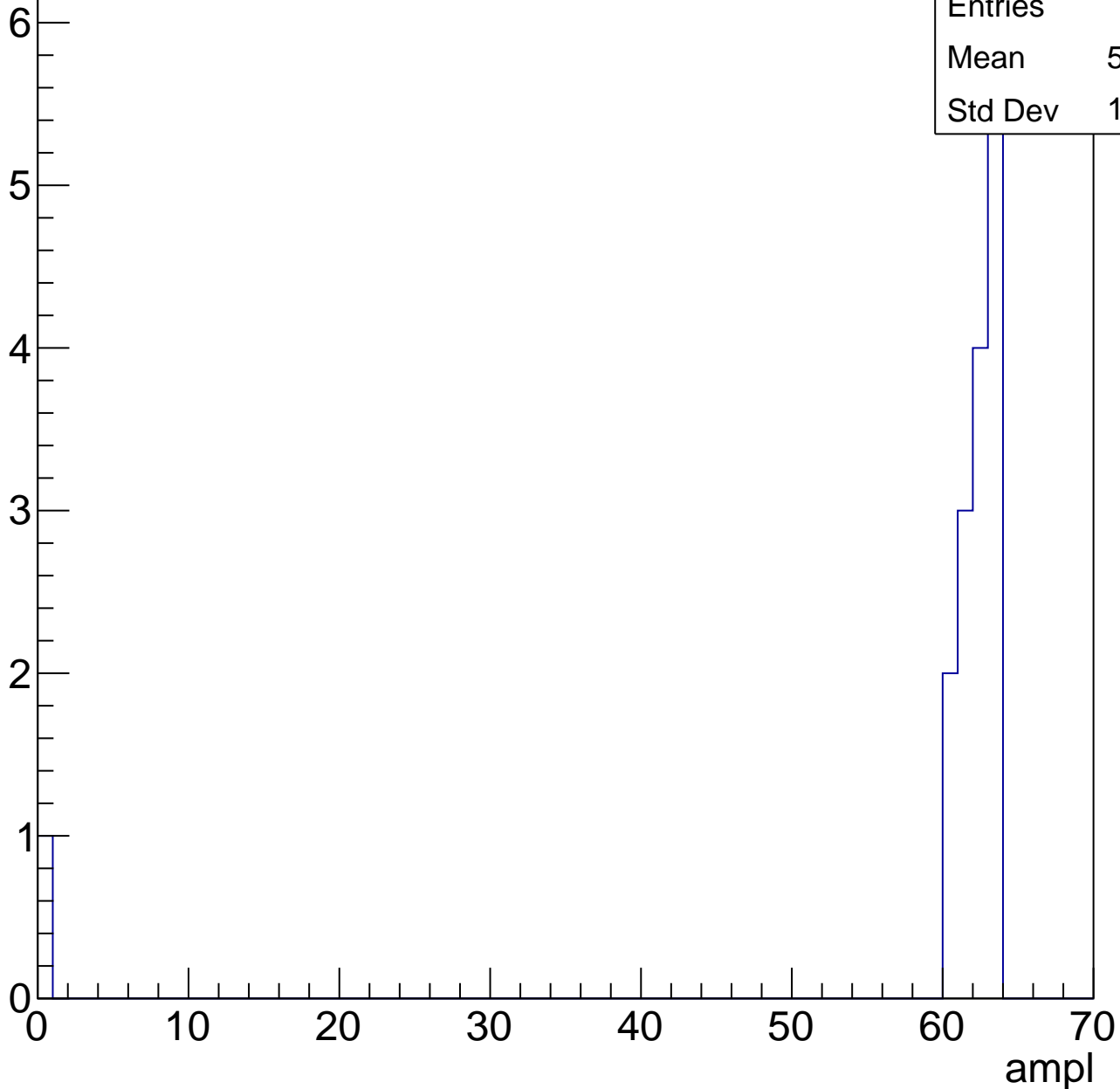


B1L102S, U9-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	58.06
Std Dev	15.03



B1L102S, U9-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch59, adc0

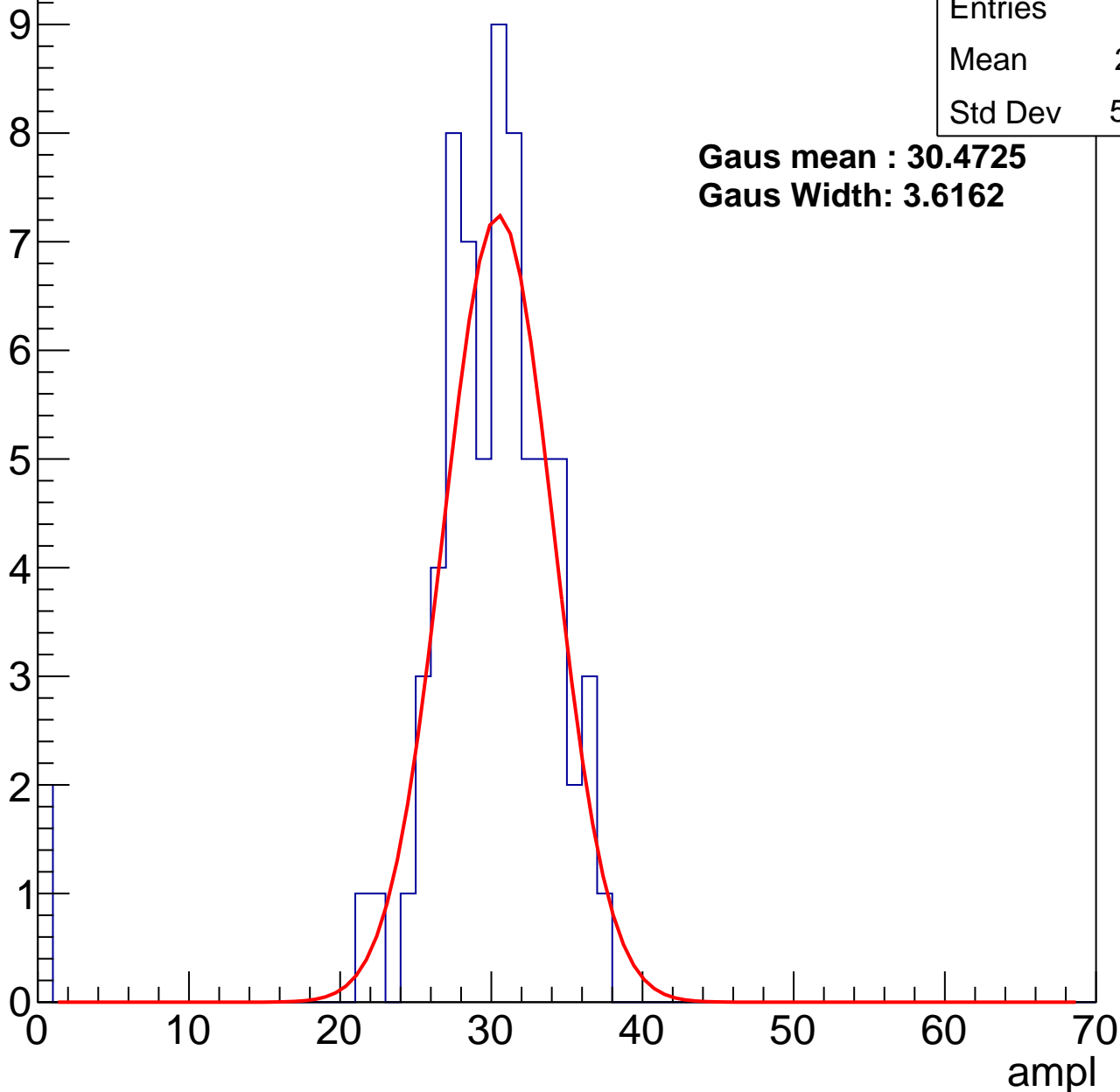
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.01
Std Dev	5.996

Gaus mean : 30.4725

Gaus Width: 3.6162



B1L102S, U9-ch59, adc1

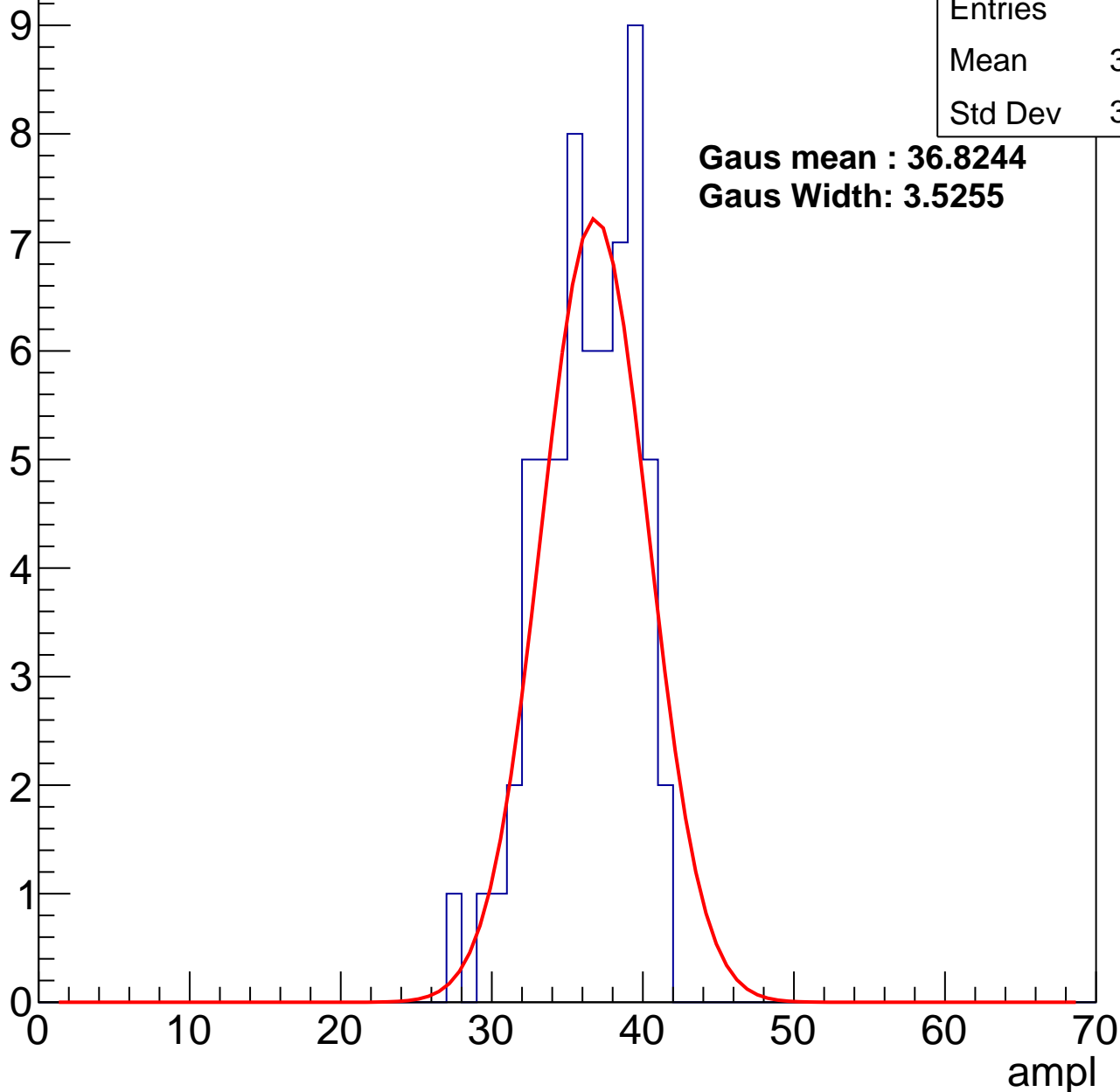
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.87
Std Dev	3.119

Gaus mean : 36.8244

Gaus Width: 3.5255



B1L102S, U9-ch59, adc2

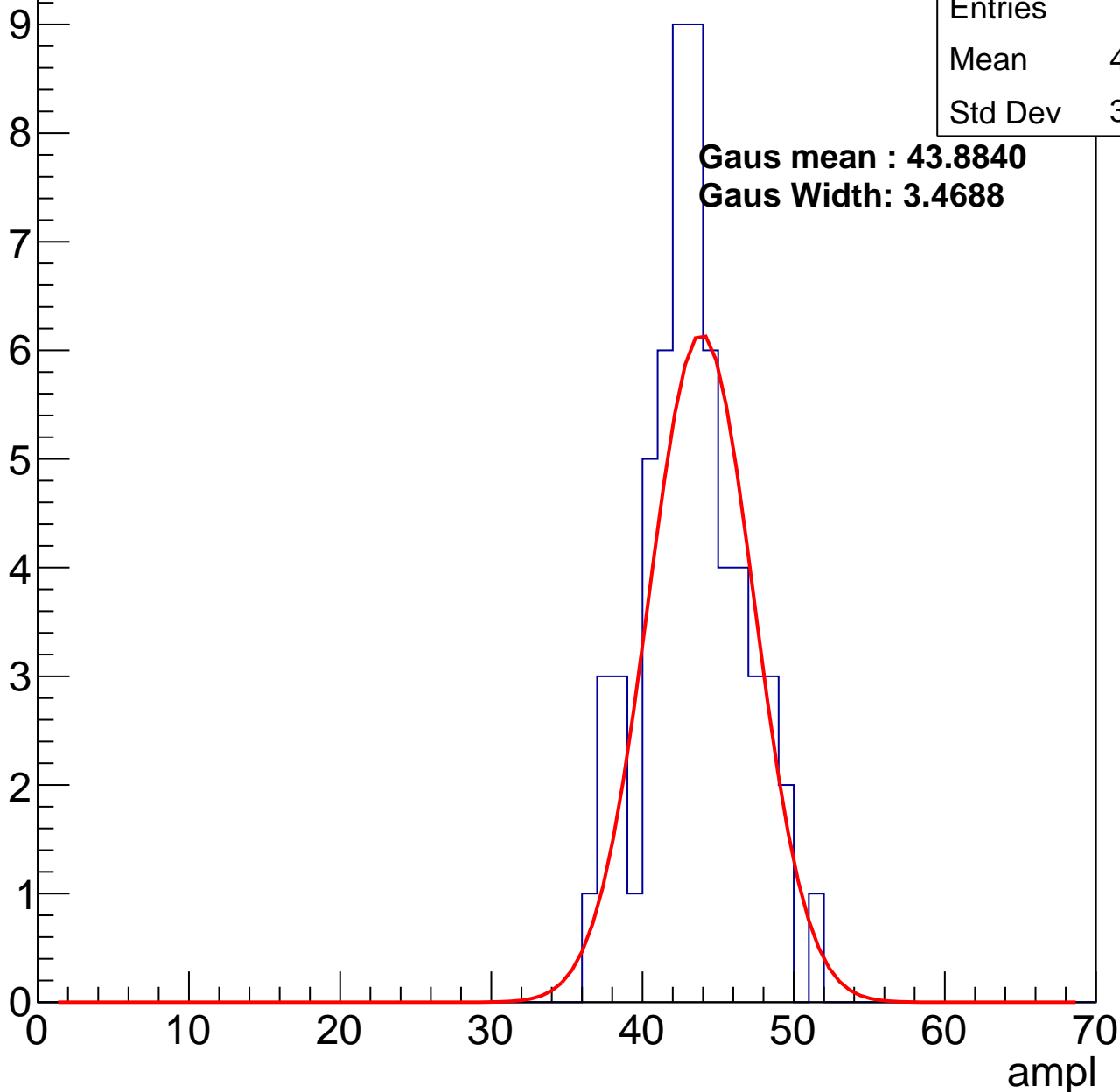
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.88
Std Dev	3.272

Gaus mean : 43.8840

Gaus Width: 3.4688

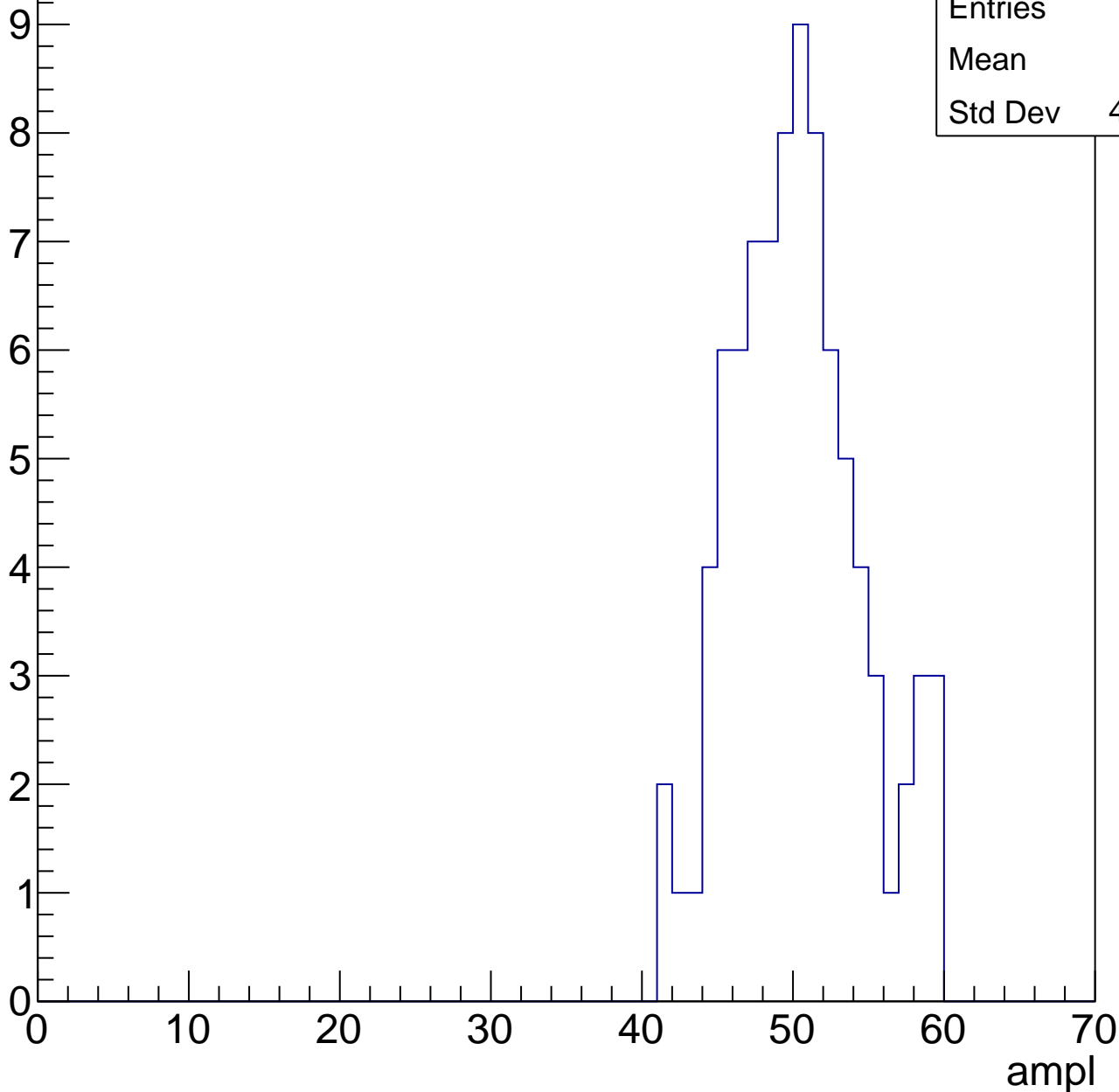


B1L102S, U9-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	49.8
Std Dev	4.239

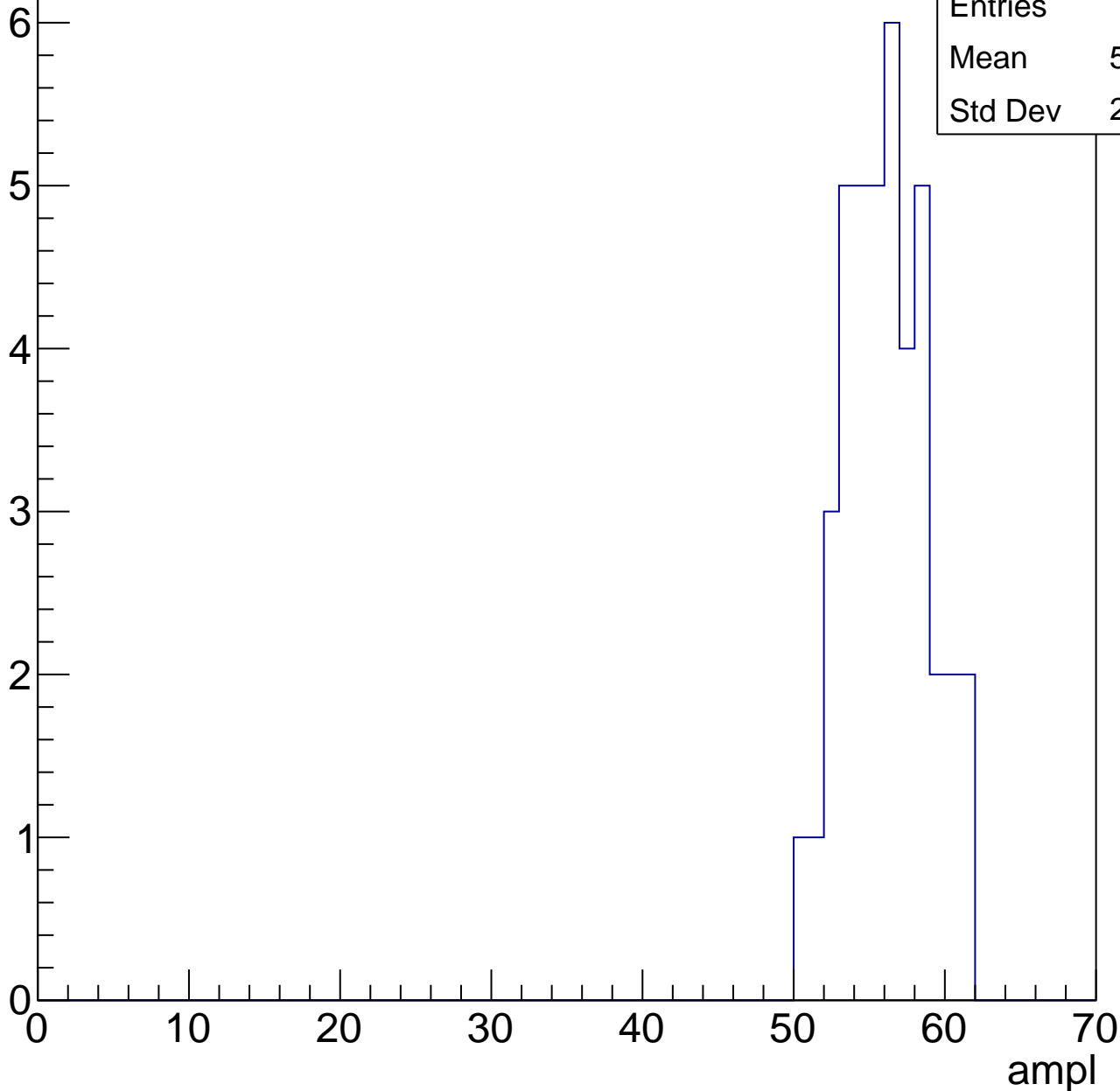


B1L102S, U9-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

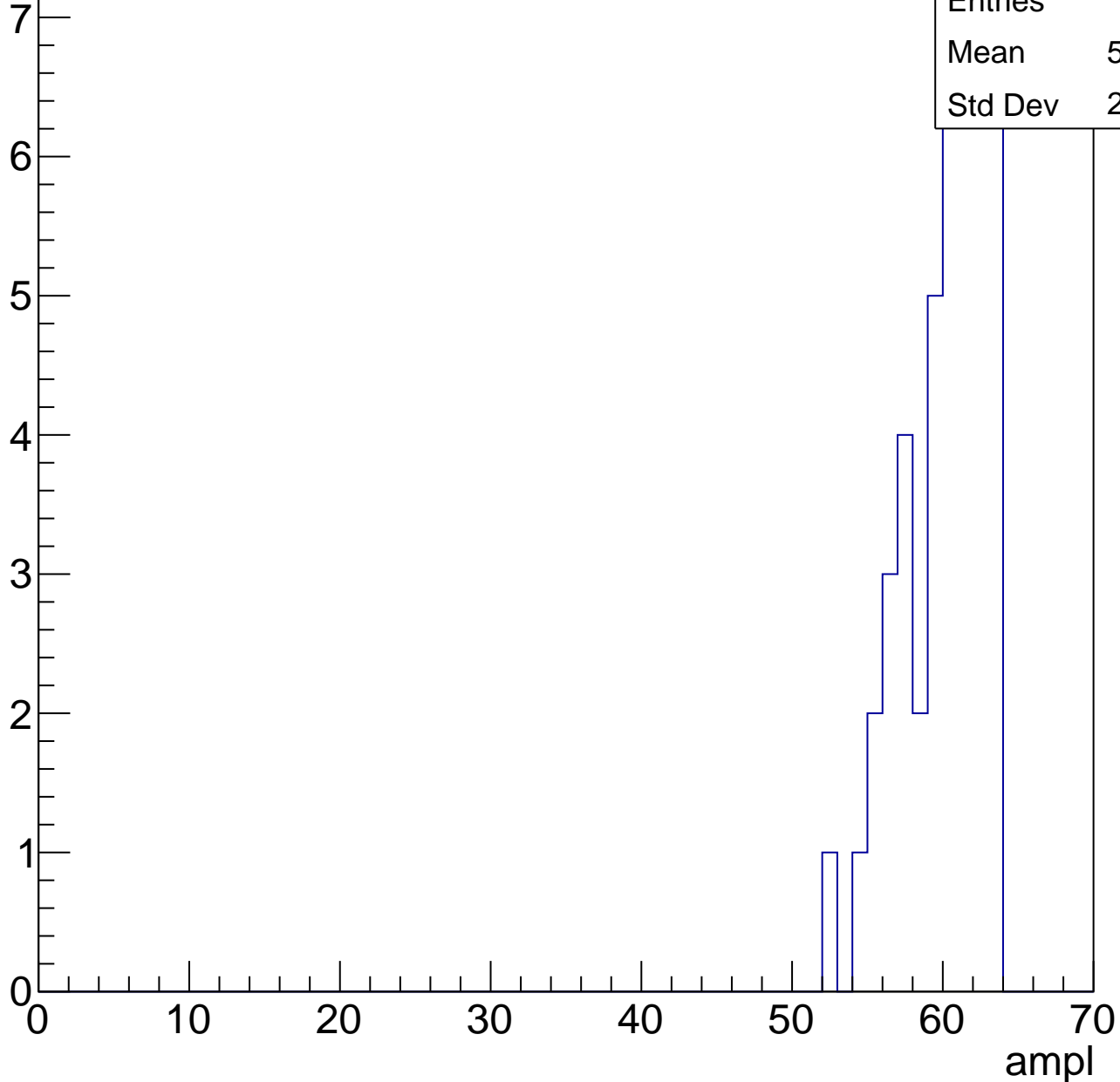
Entries	41
Mean	55.63
Std Dev	2.694



B1L102S, U9-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

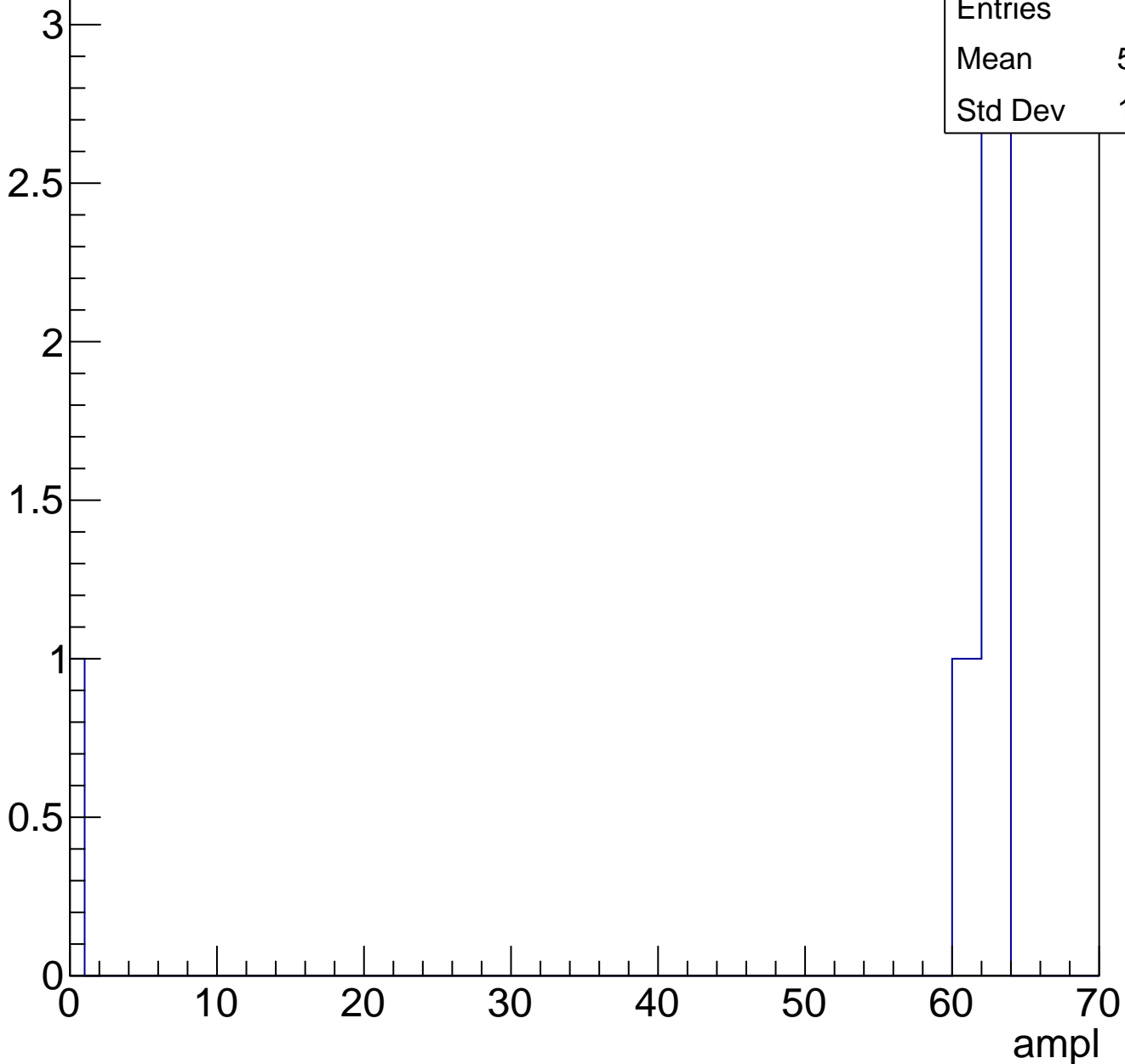
Entry



B1L102S, U9-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch60, adc0

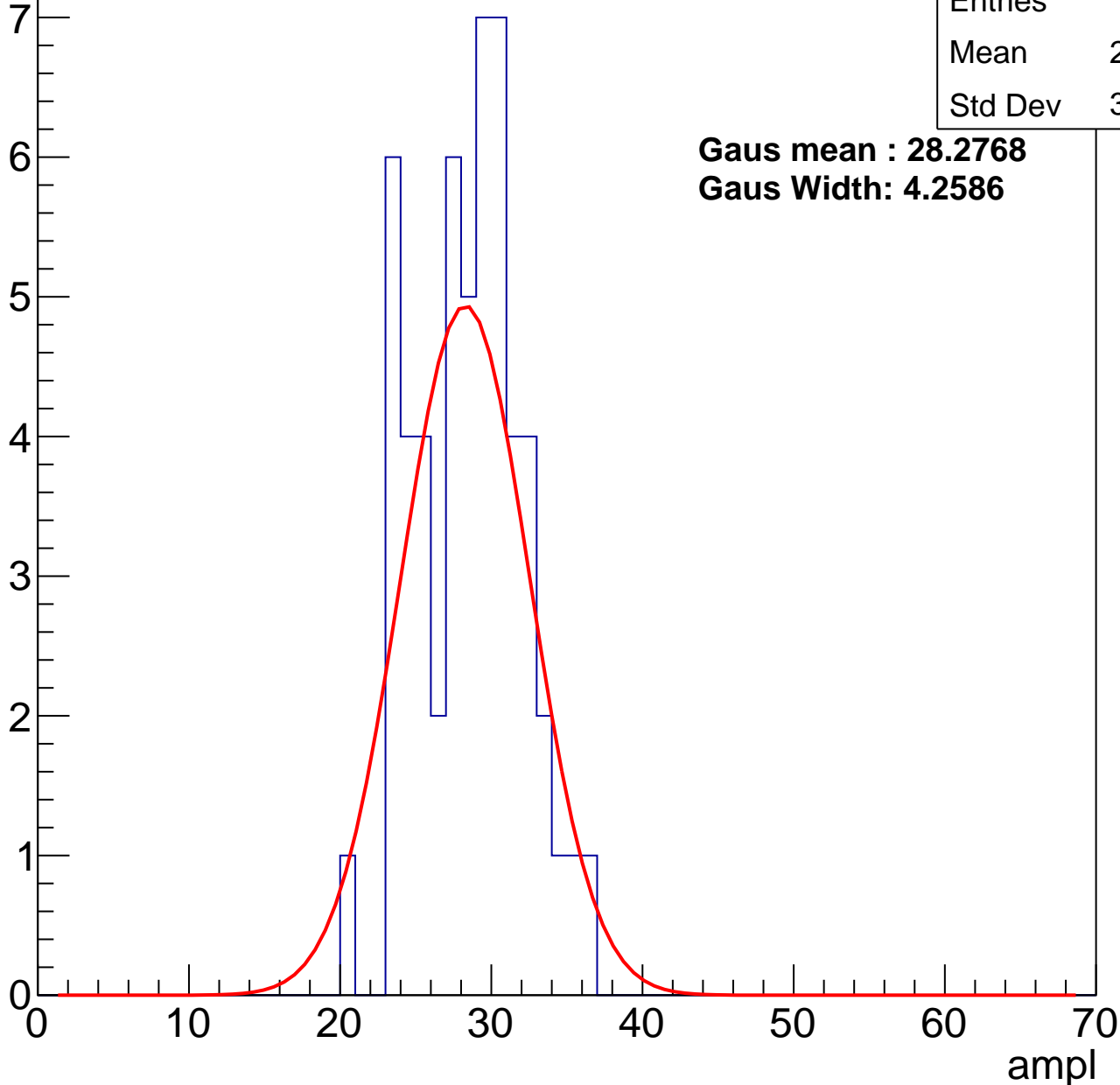
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.07
Std Dev	3.469

Gaus mean : 28.2768

Gaus Width: 4.2586



B1L102S, U9-ch60, adc1

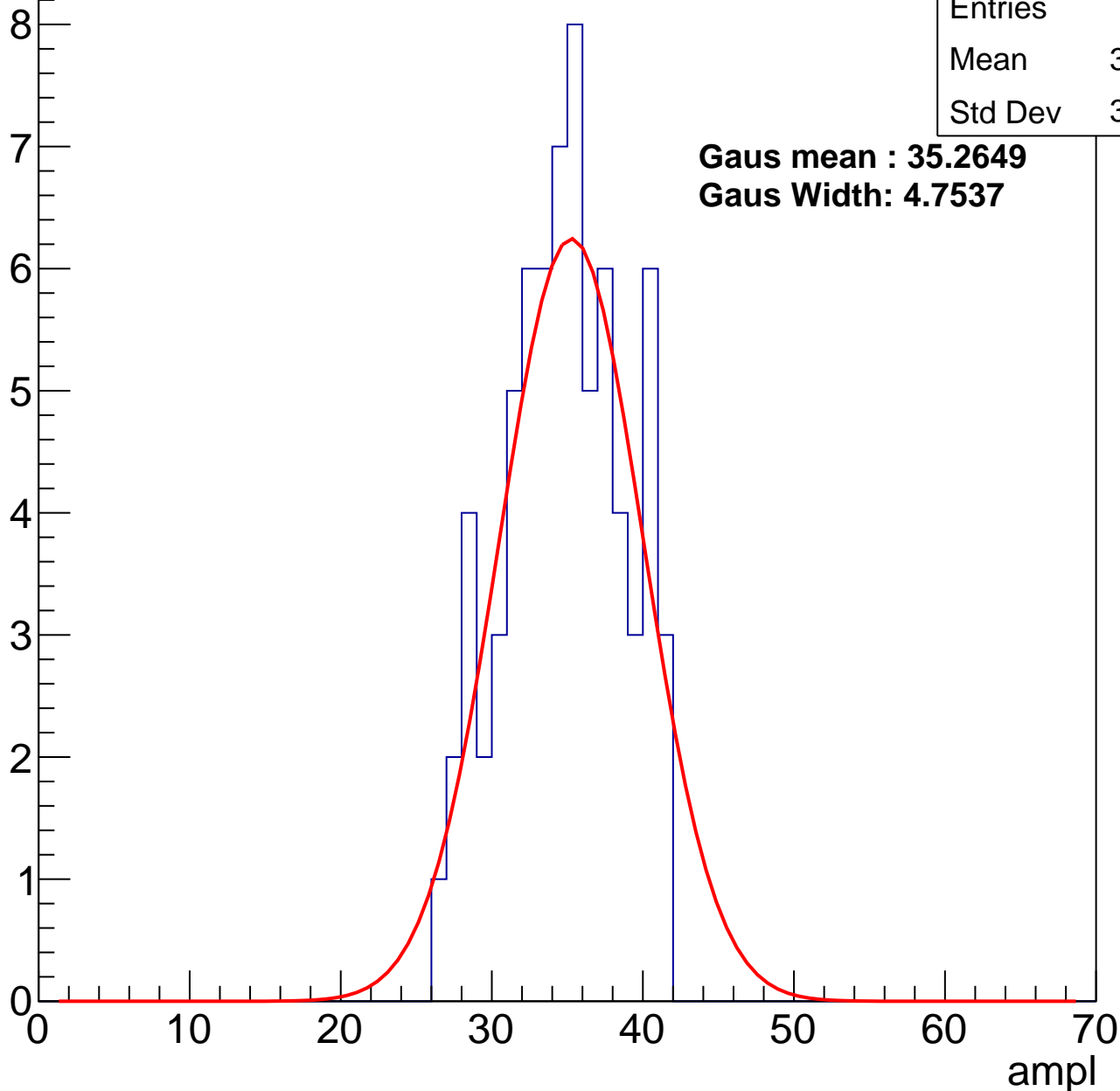
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.32
Std Dev	3.856

Gaus mean : 35.2649

Gaus Width: 4.7537



B1L102S, U9-ch60, adc2

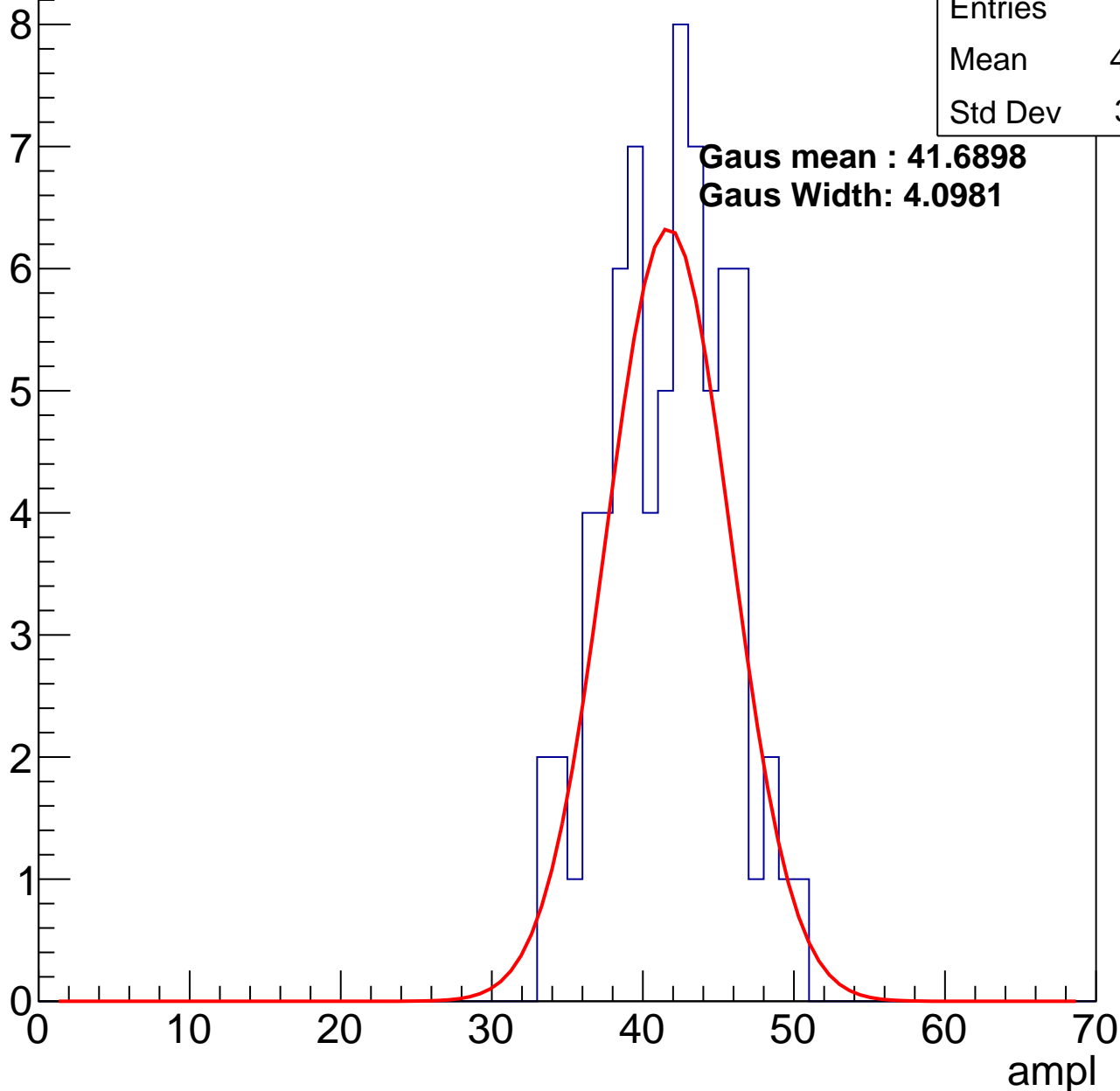
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.28
Std Dev	3.931

Gaus mean : 41.6898

Gaus Width: 4.0981

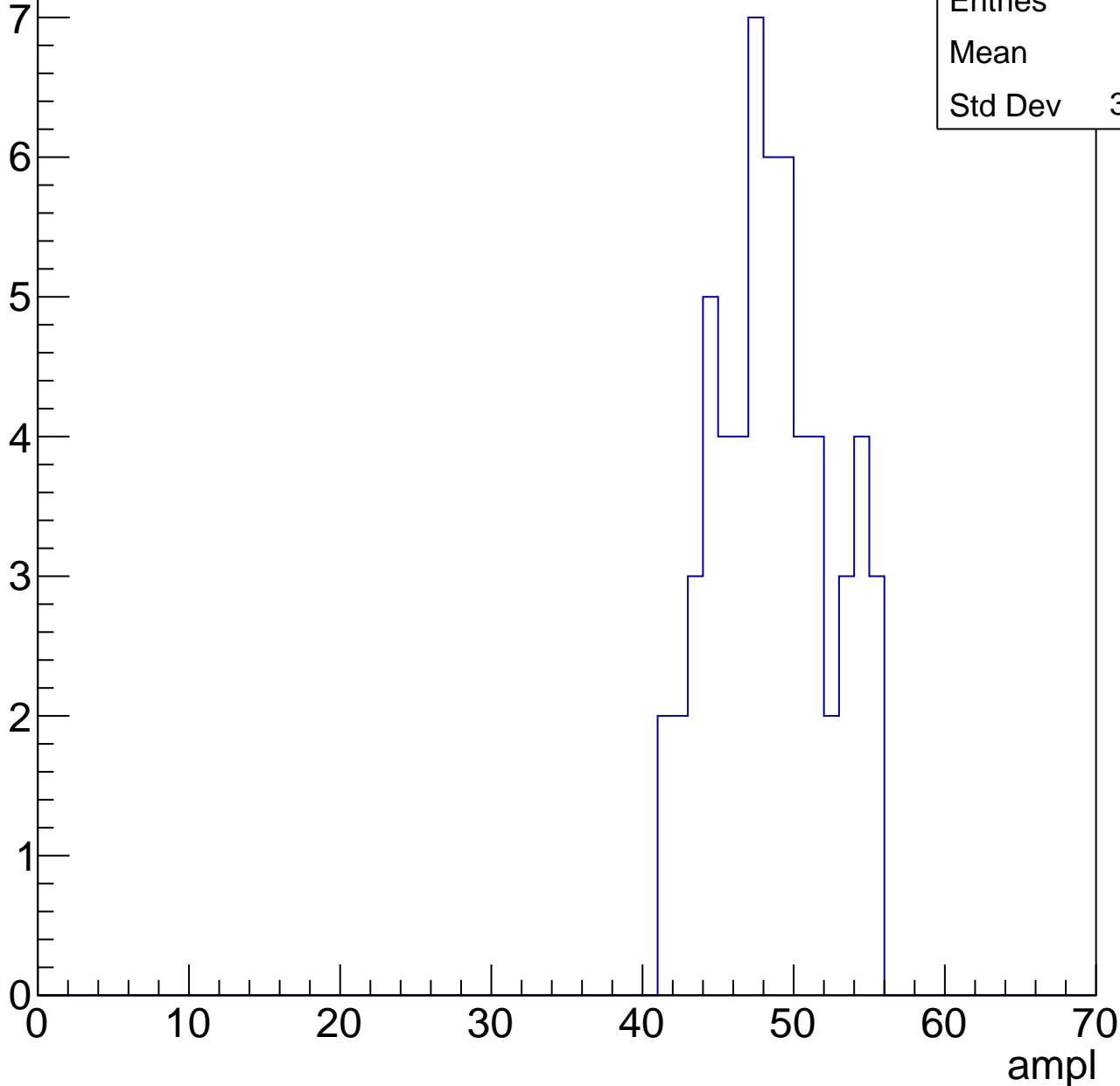


B1L102S, U9-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.1
Std Dev	3.772

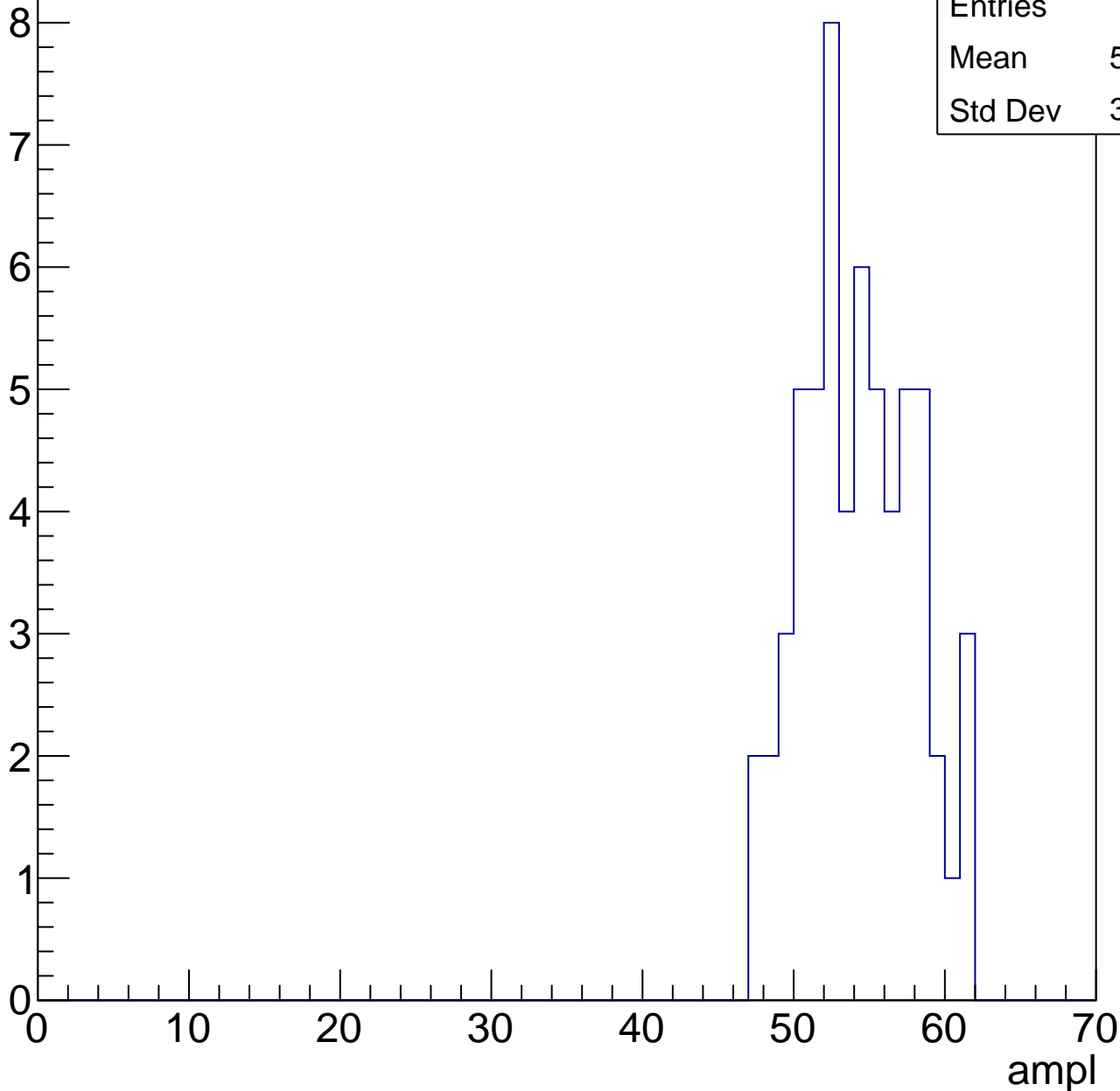


B1L102S, U9-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.82
Std Dev	3.612

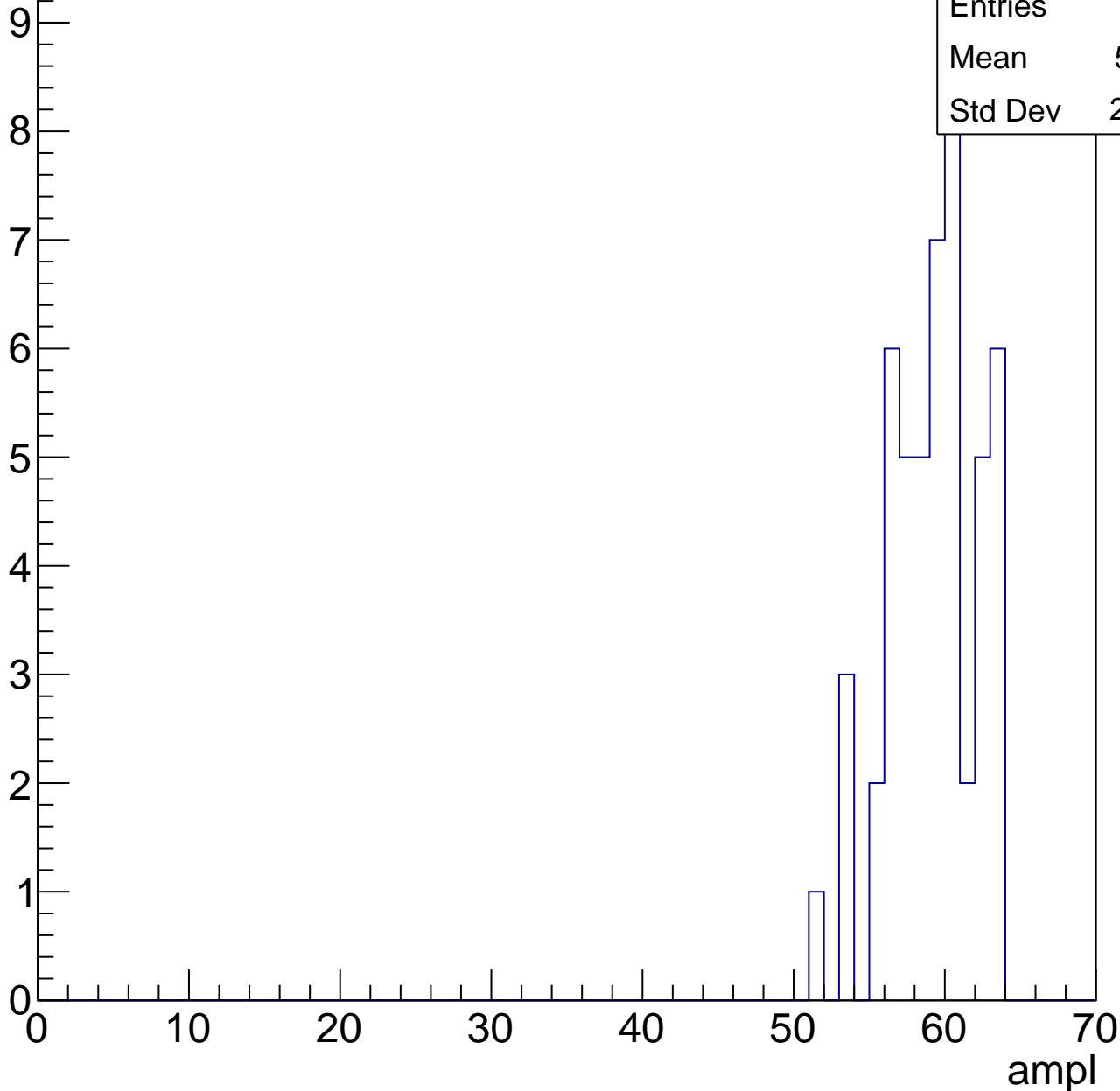


B1L102S, U9-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.71
Std Dev	2.926

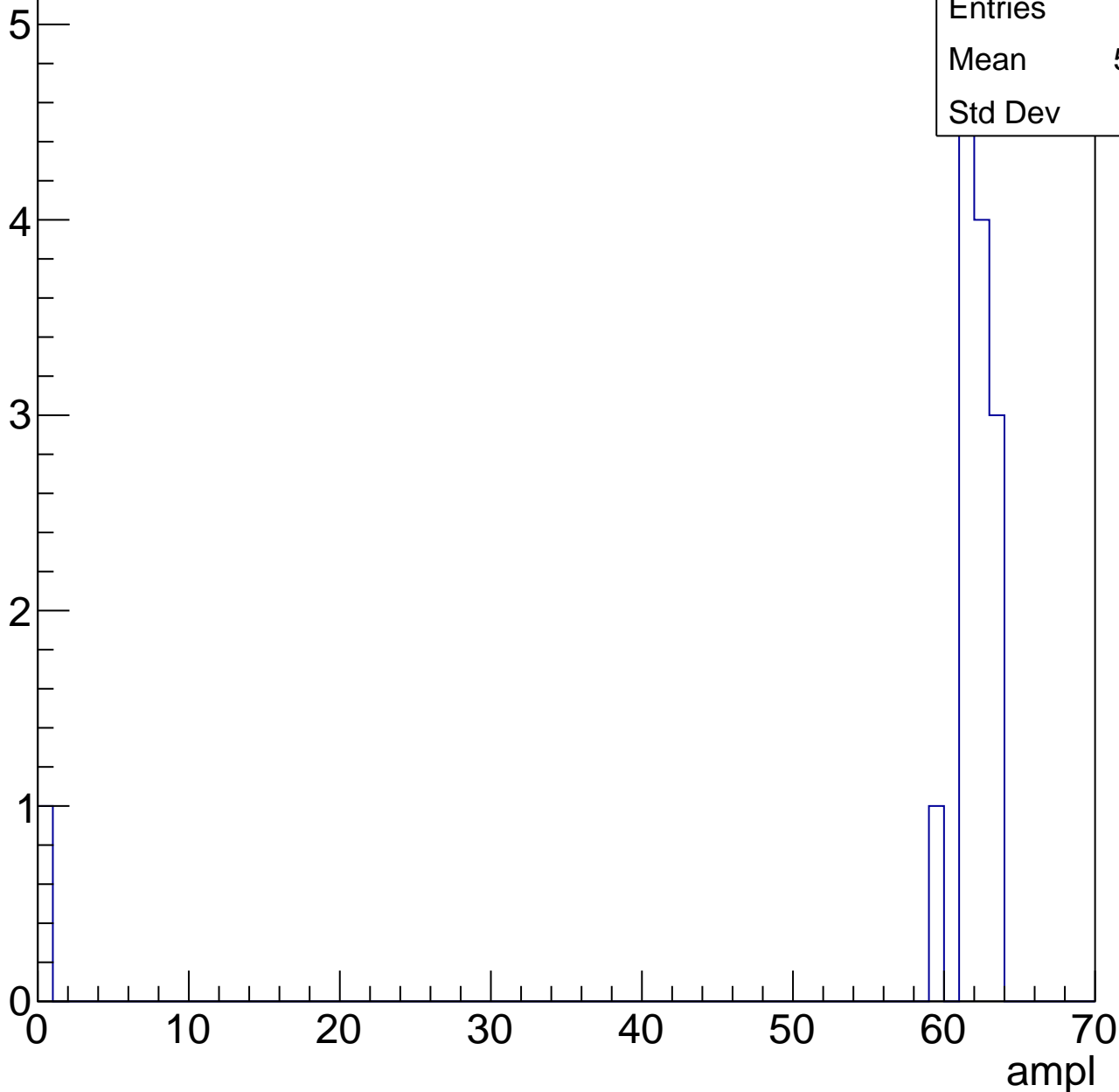


B1L102S, U9-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.21
Std Dev	15.9



B1L102S, U9-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L102S, U9-ch61, adc0

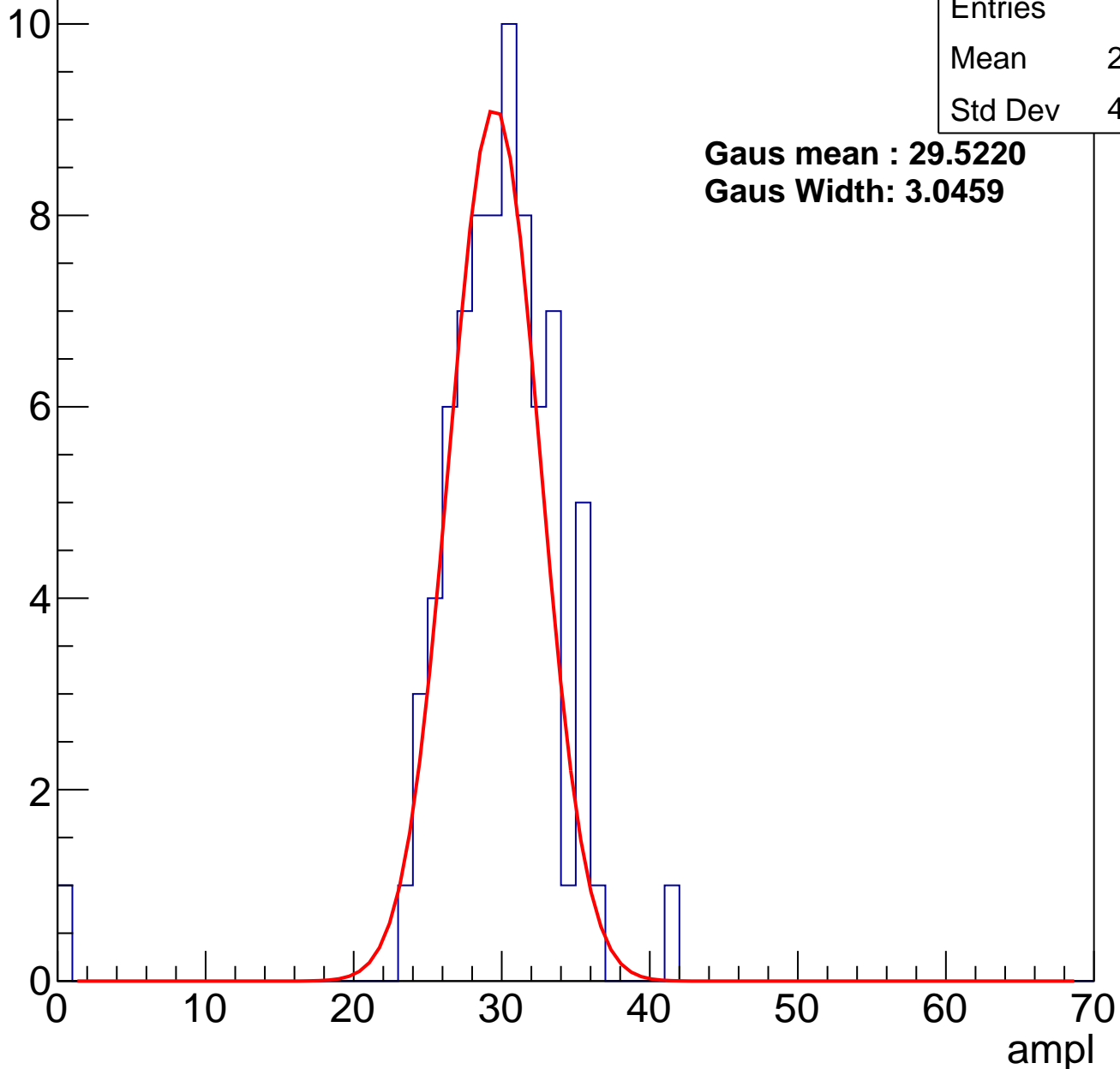
calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	29.26
Std Dev	4.708

Gaus mean : 29.5220

Gaus Width: 3.0459

Entry



B1L102S, U9-ch61, adc1

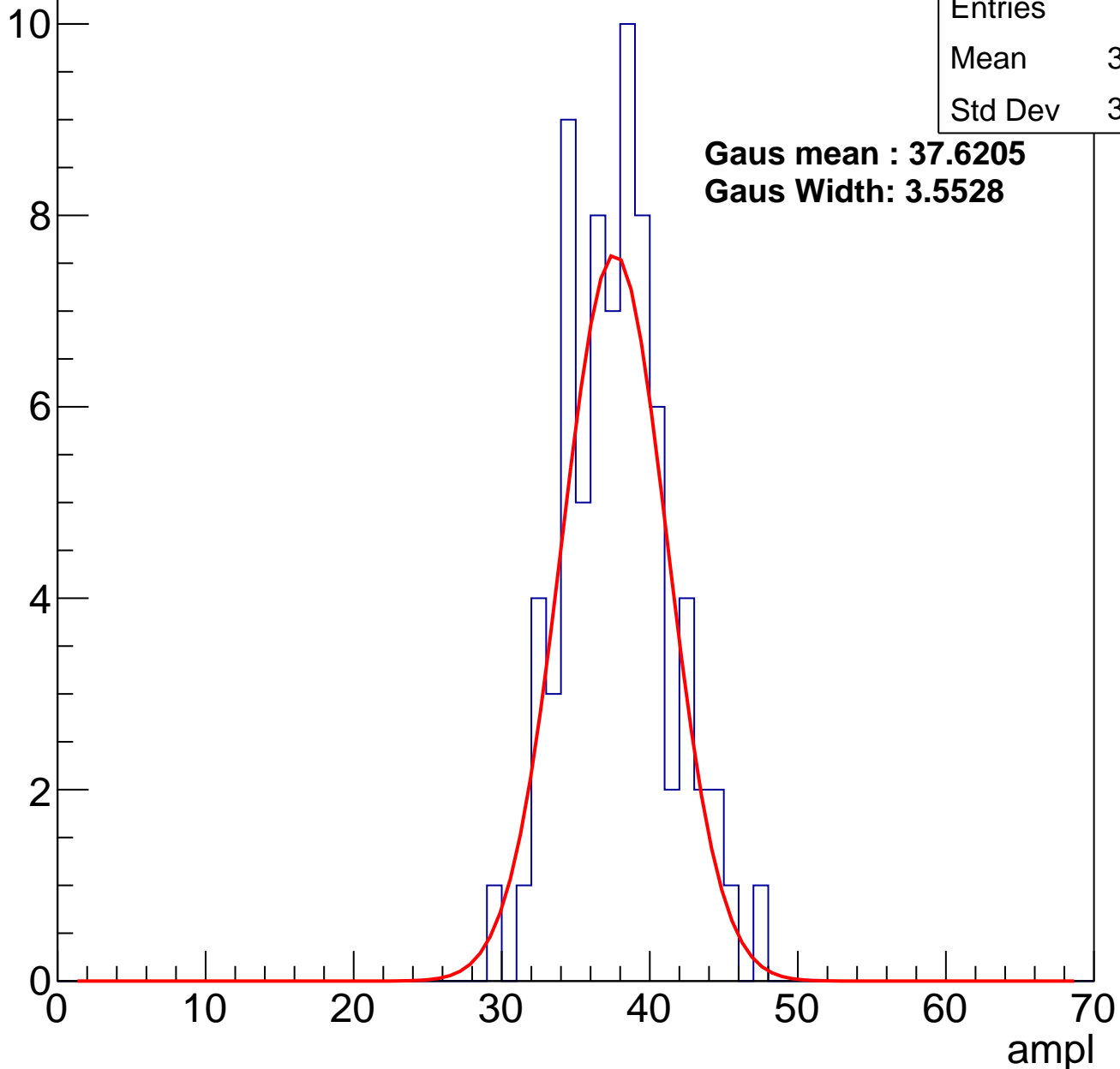
calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	37.34
Std Dev	3.508

Gaus mean : 37.6205

Gaus Width: 3.5528

Entry



B1L102S, U9-ch61, adc2

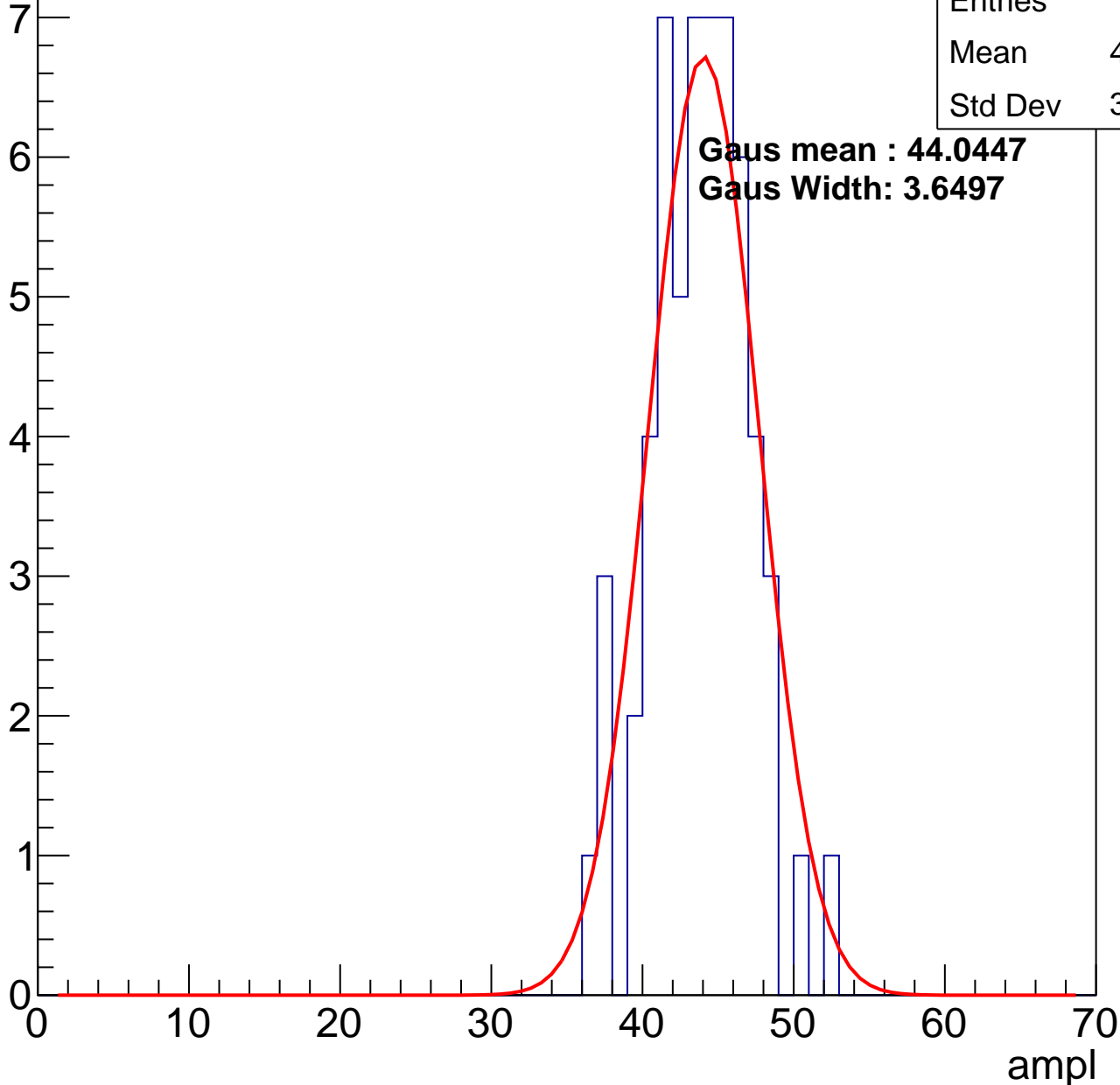
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.38
Std Dev	3.237

Gaus mean : 44.0447

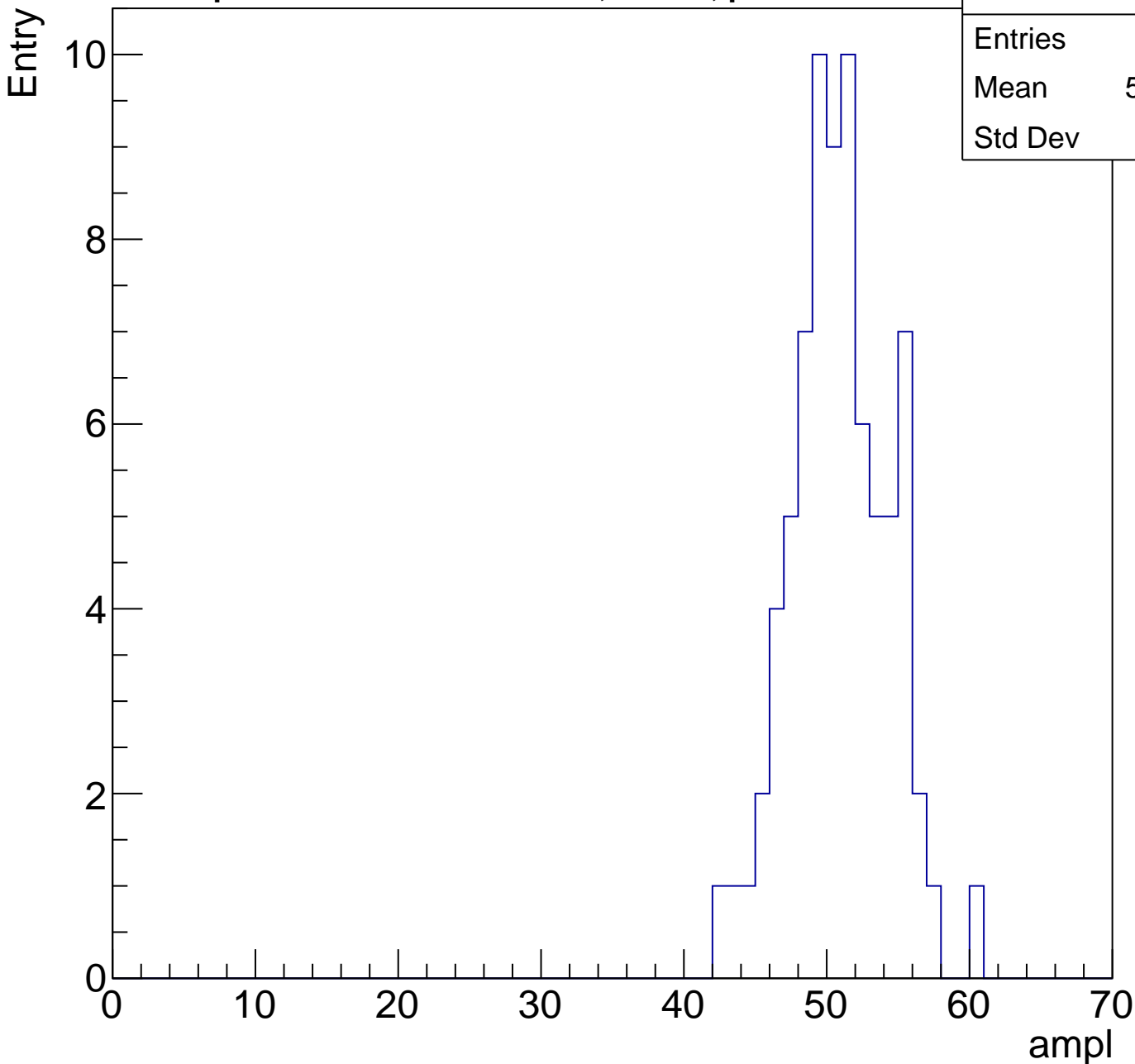
Gaus Width: 3.6497



B1L102S, U9-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	50.45
Std Dev	3.39



B1L102S, U9-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	58
Mean	57.24
Std Dev	2.744

Entry

10

8

6

4

2

0

0

10

20

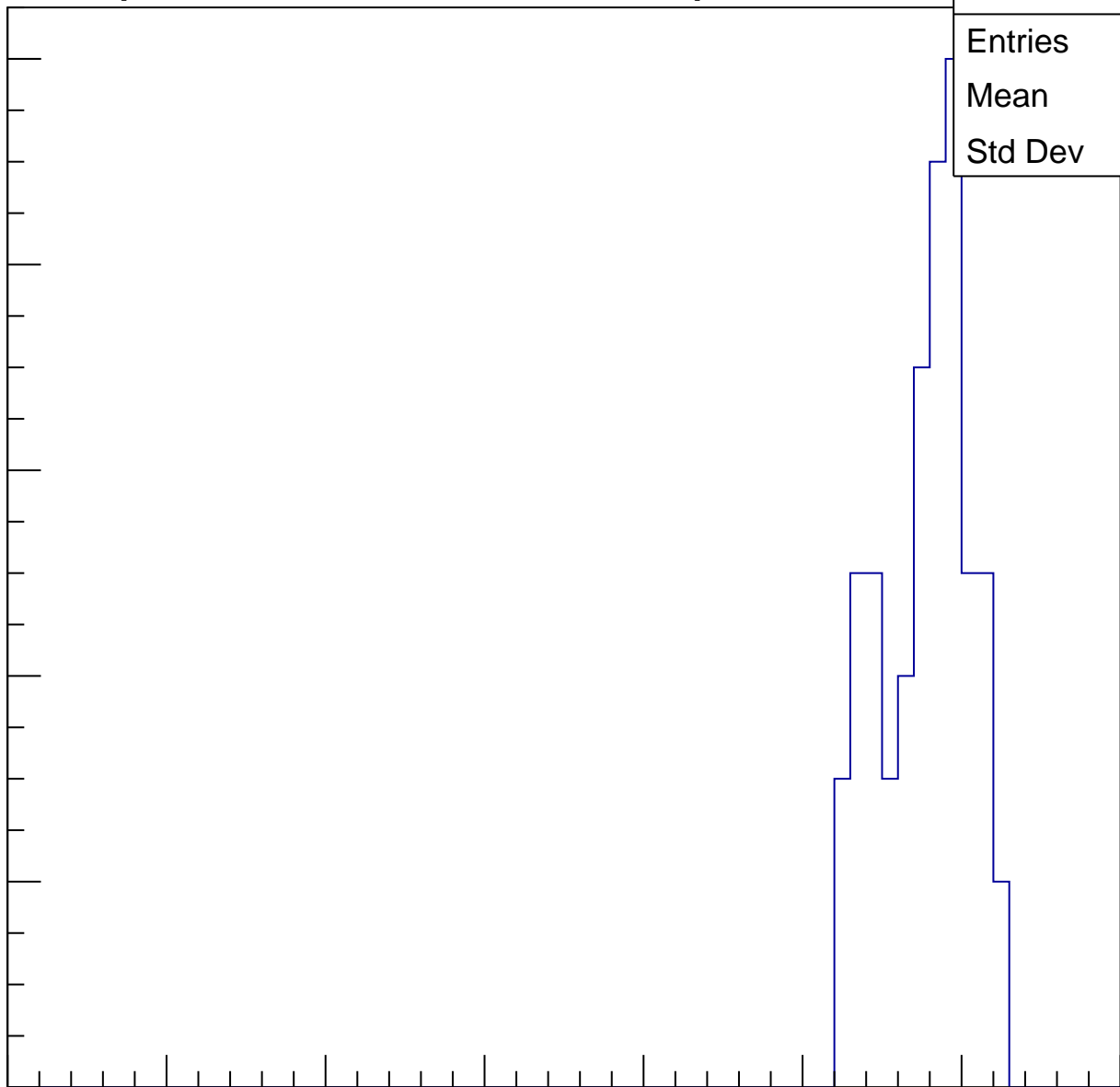
30

40

50

60

ampl

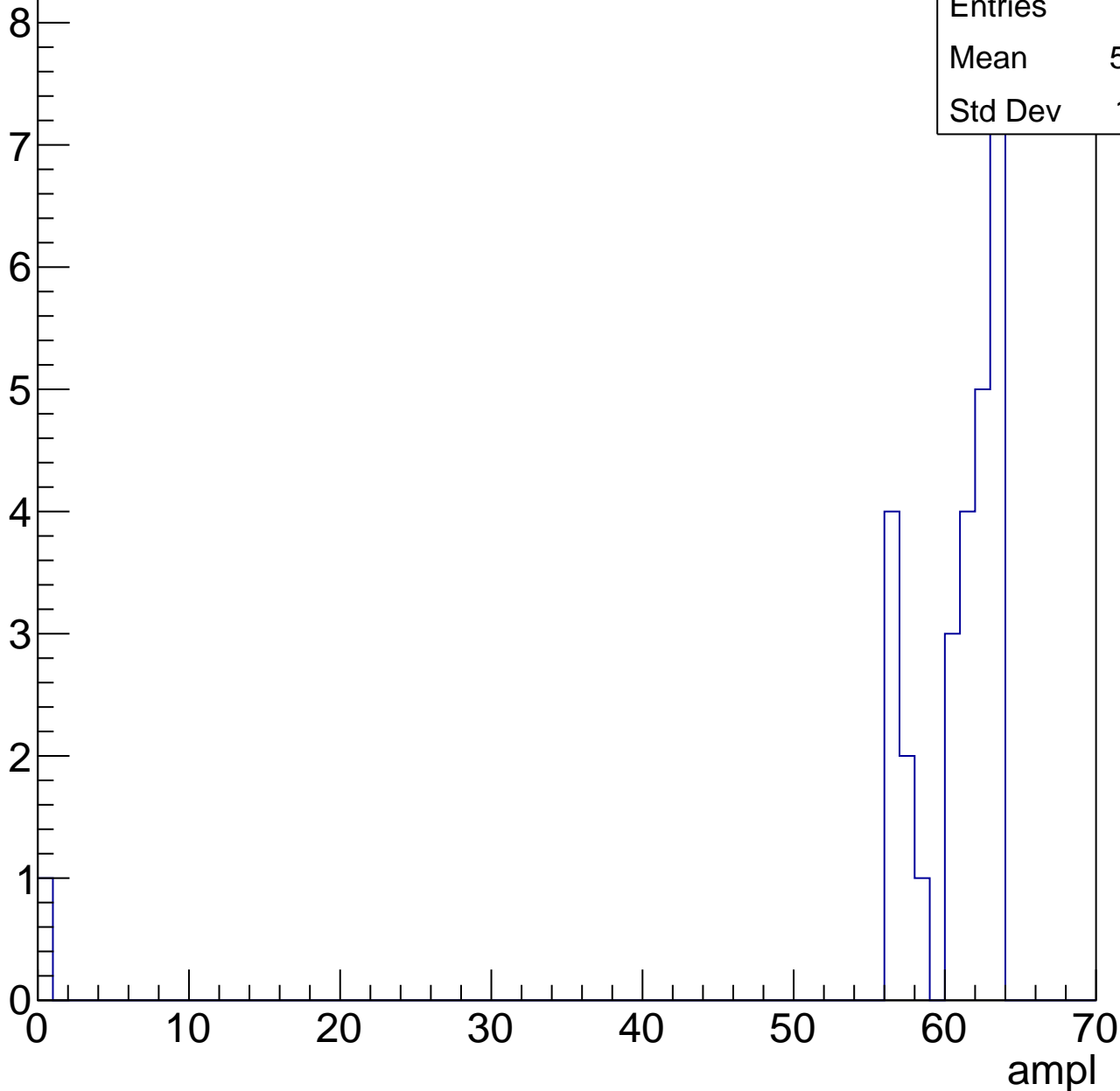


B1L102S, U9-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	58.36
Std Dev	11.51



B1L102S, U9-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

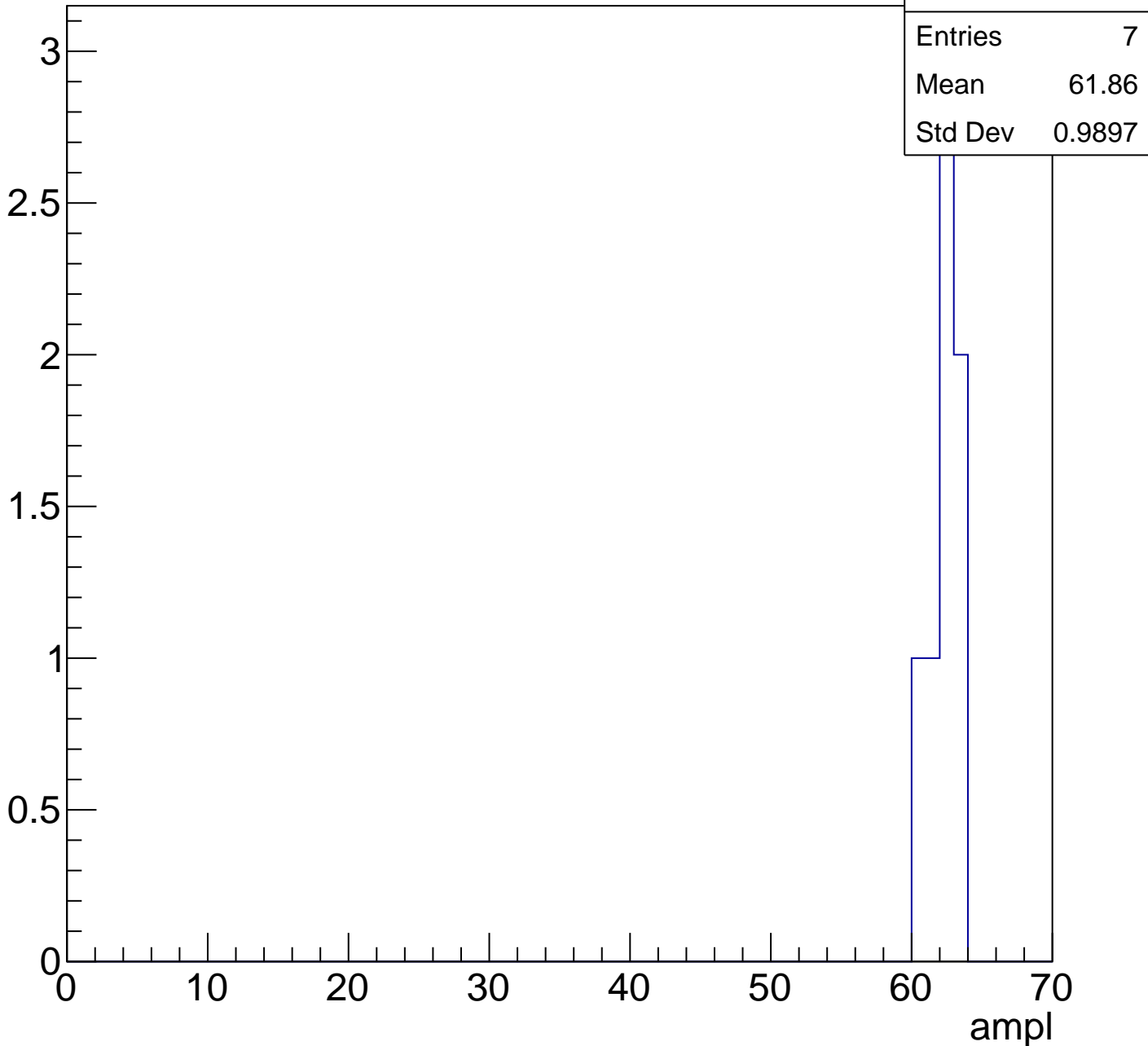
7

Mean

61.86

Std Dev

0.9897



B1L102S, U9-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch62, adc0

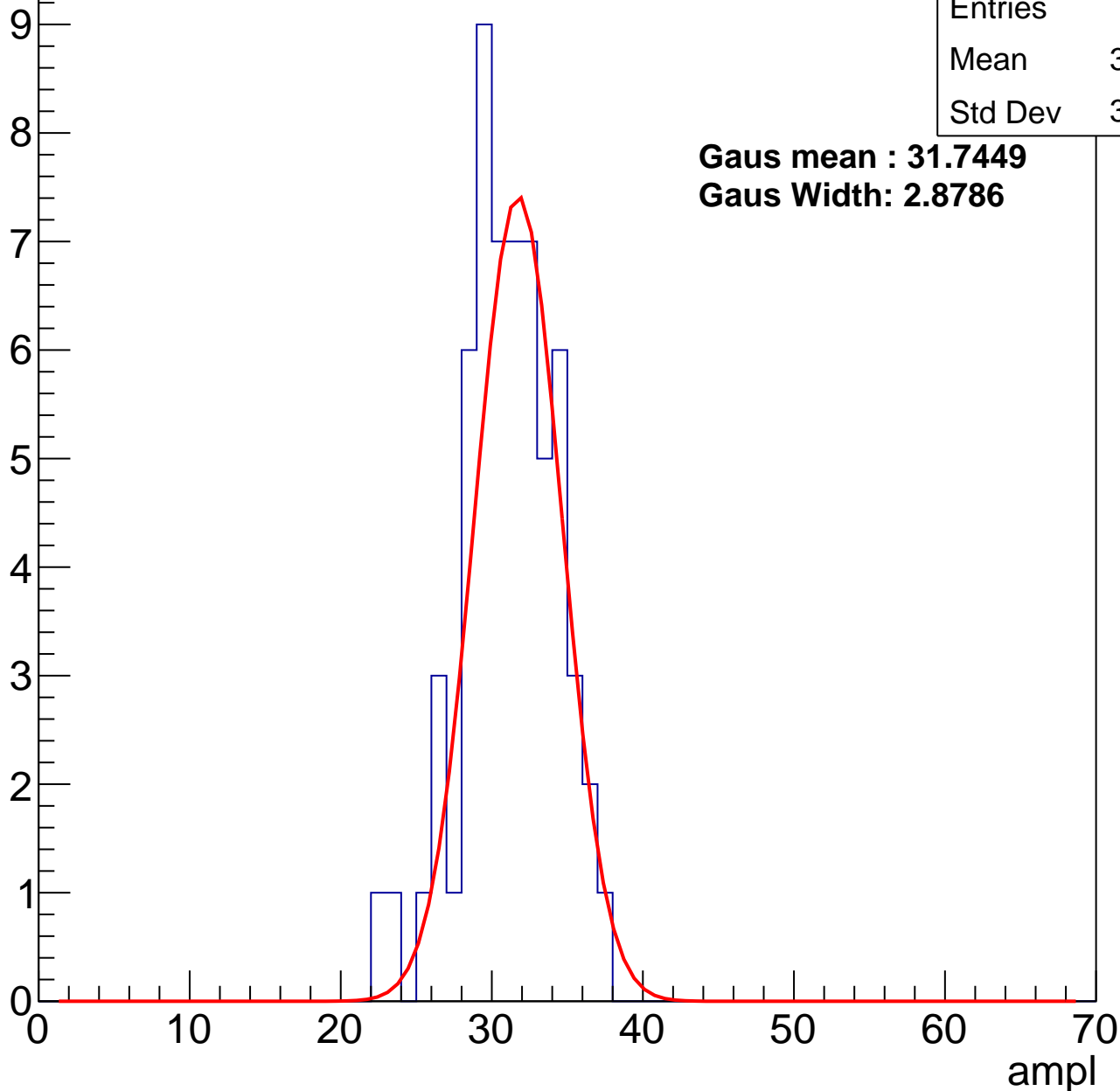
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	30.63
Std Dev	3.098

Gaus mean : 31.7449

Gaus Width: 2.8786



B1L102S, U9-ch62, adc1

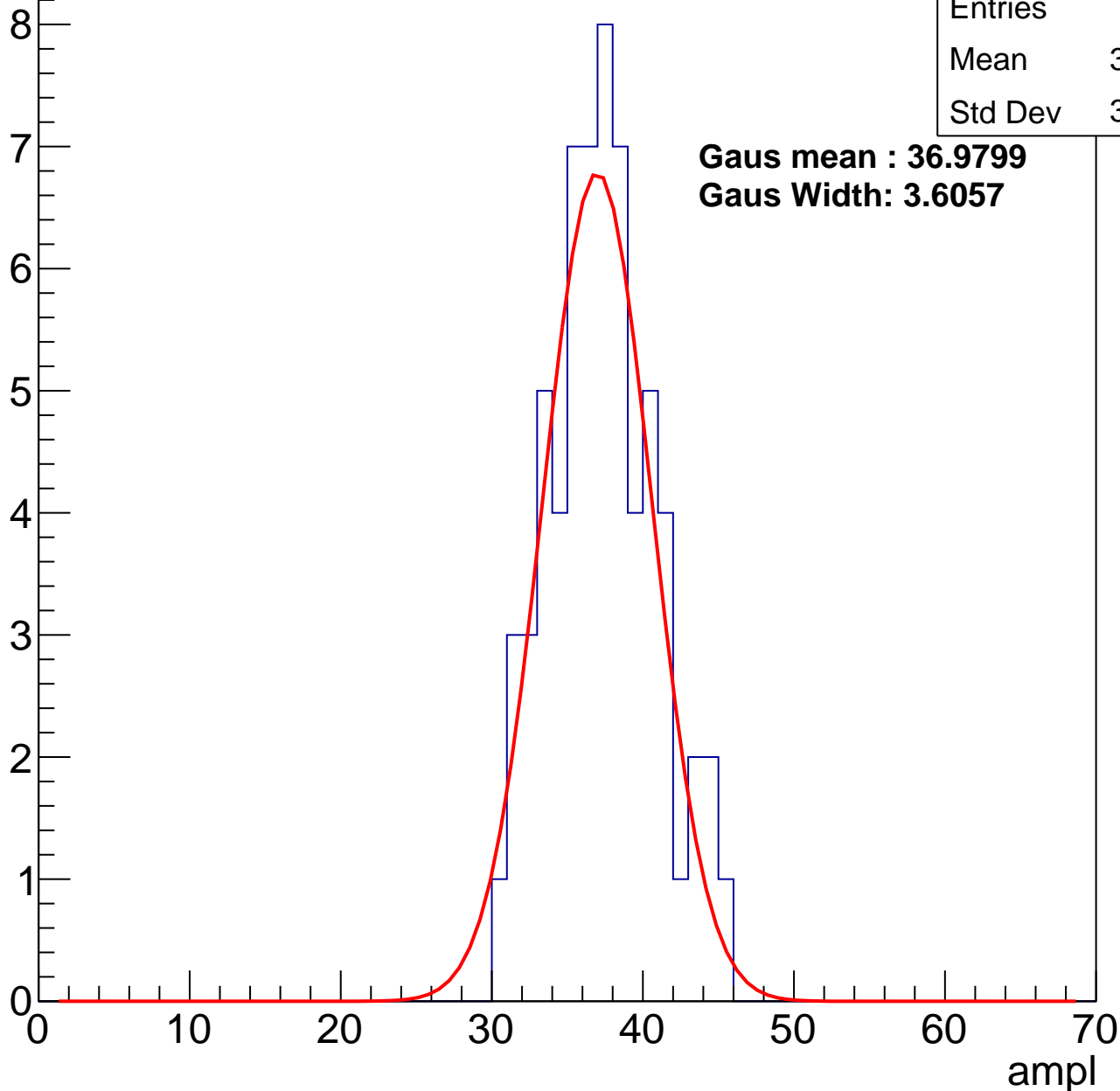
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.88
Std Dev	3.475

Gaus mean : 36.9799

Gaus Width: 3.6057



B1L102S, U9-ch62, adc2

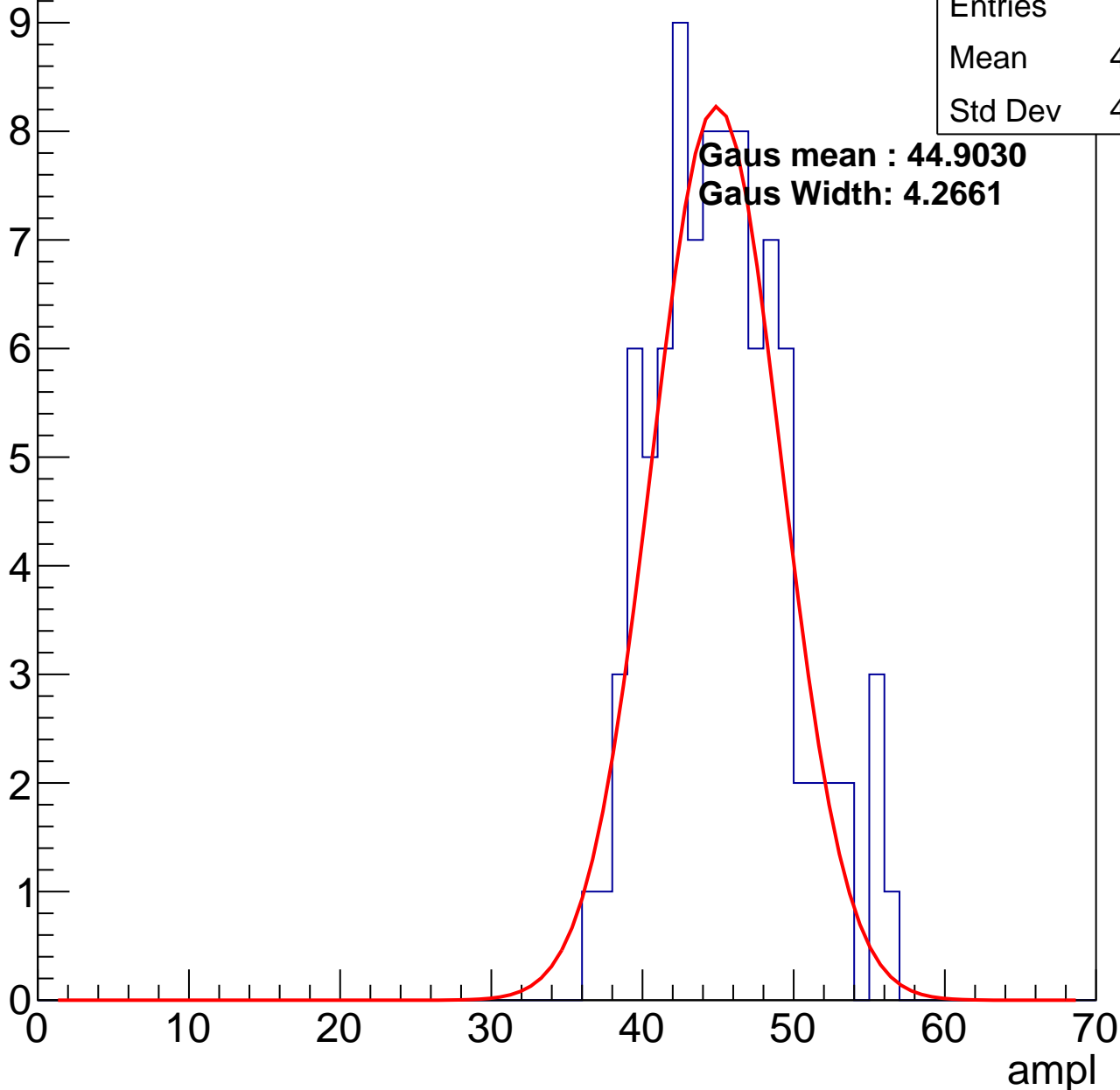
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	44.85
Std Dev	4.404

Gaus mean : 44.9030

Gaus Width: 4.2661

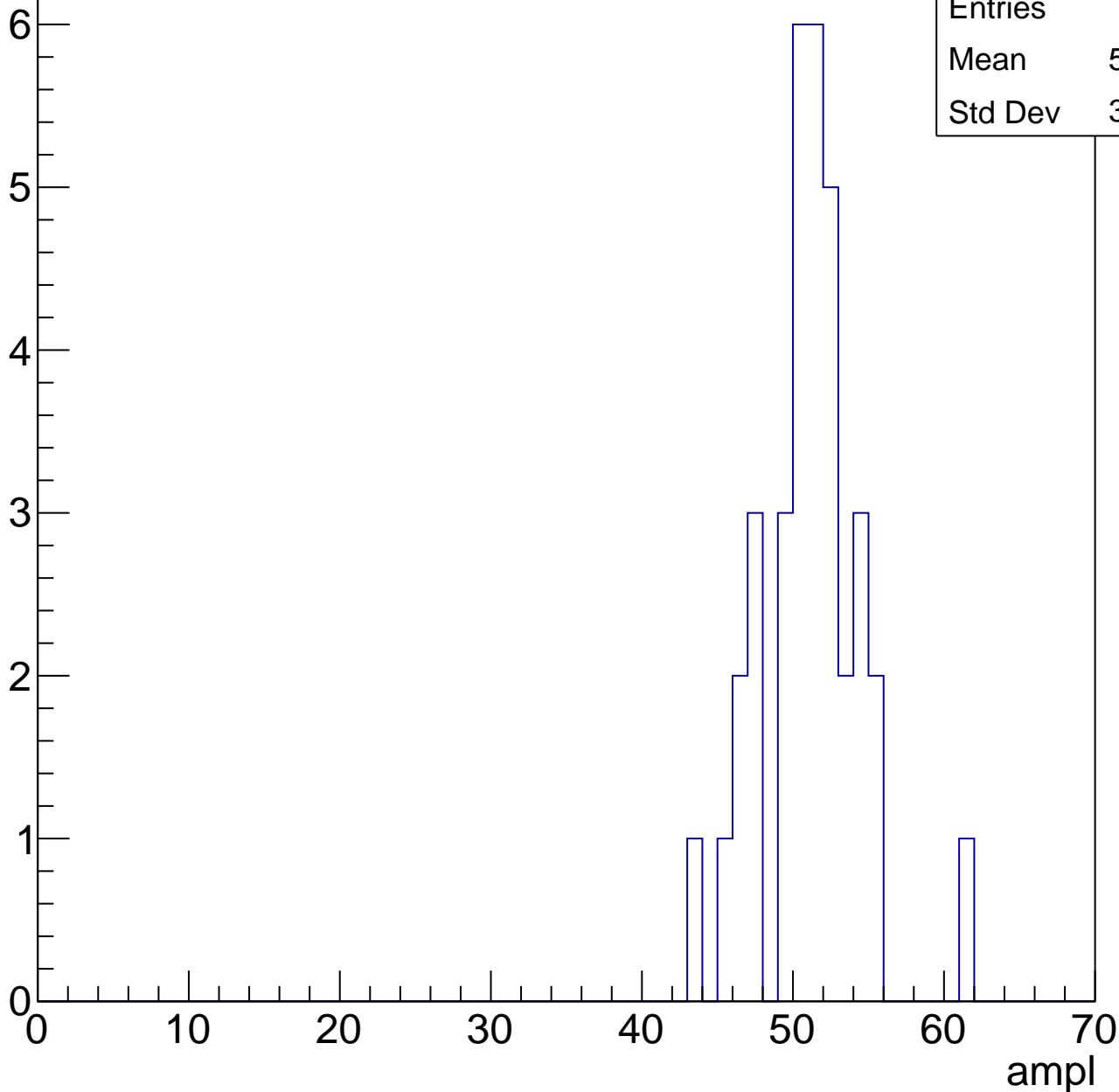


B1L102S, U9-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

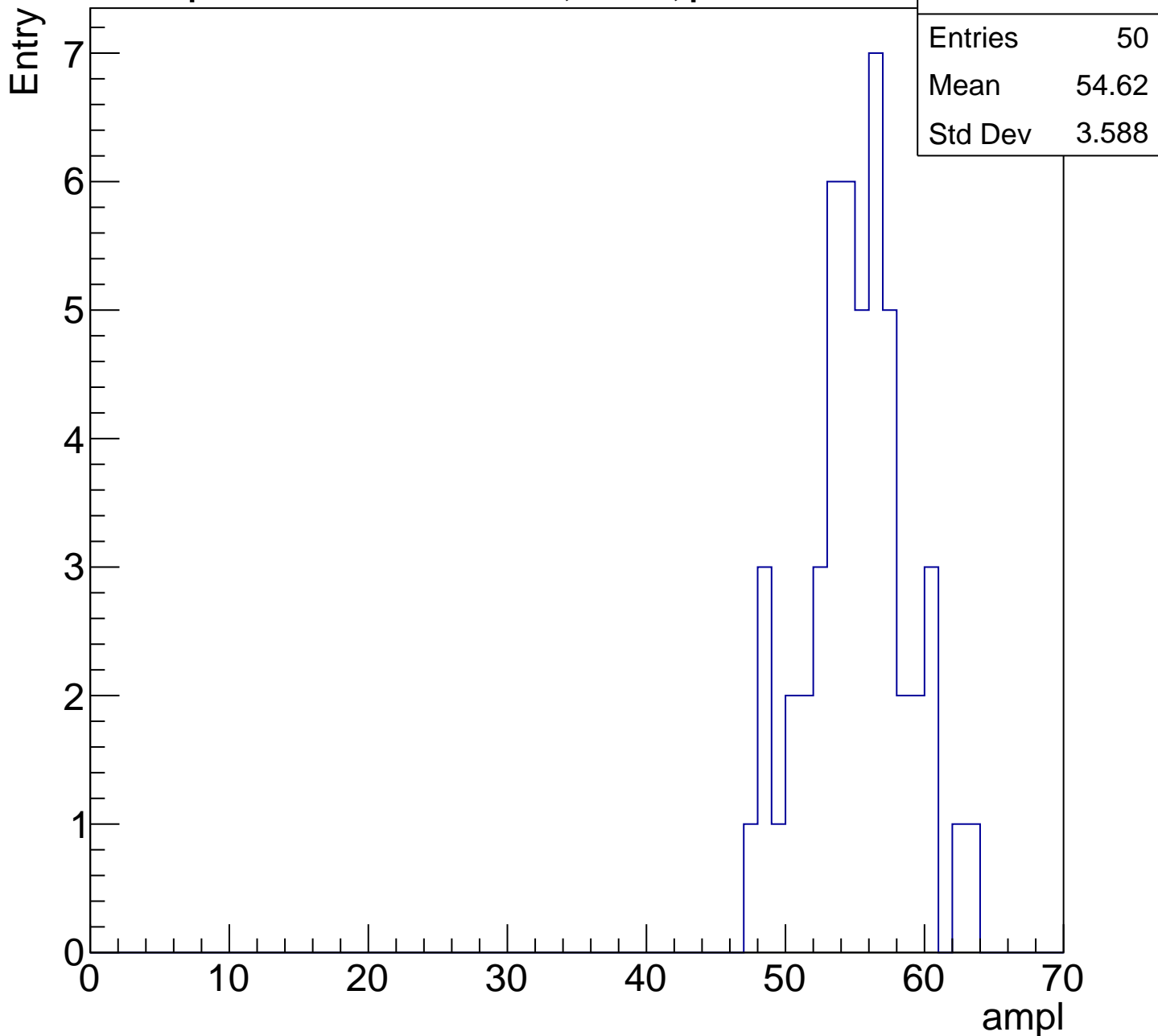
Entry

Entries	35
Mean	50.66
Std Dev	3.312



B1L102S, U9-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U9-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.68
Std Dev	8.427

ampl

0

10

20

30

40

50

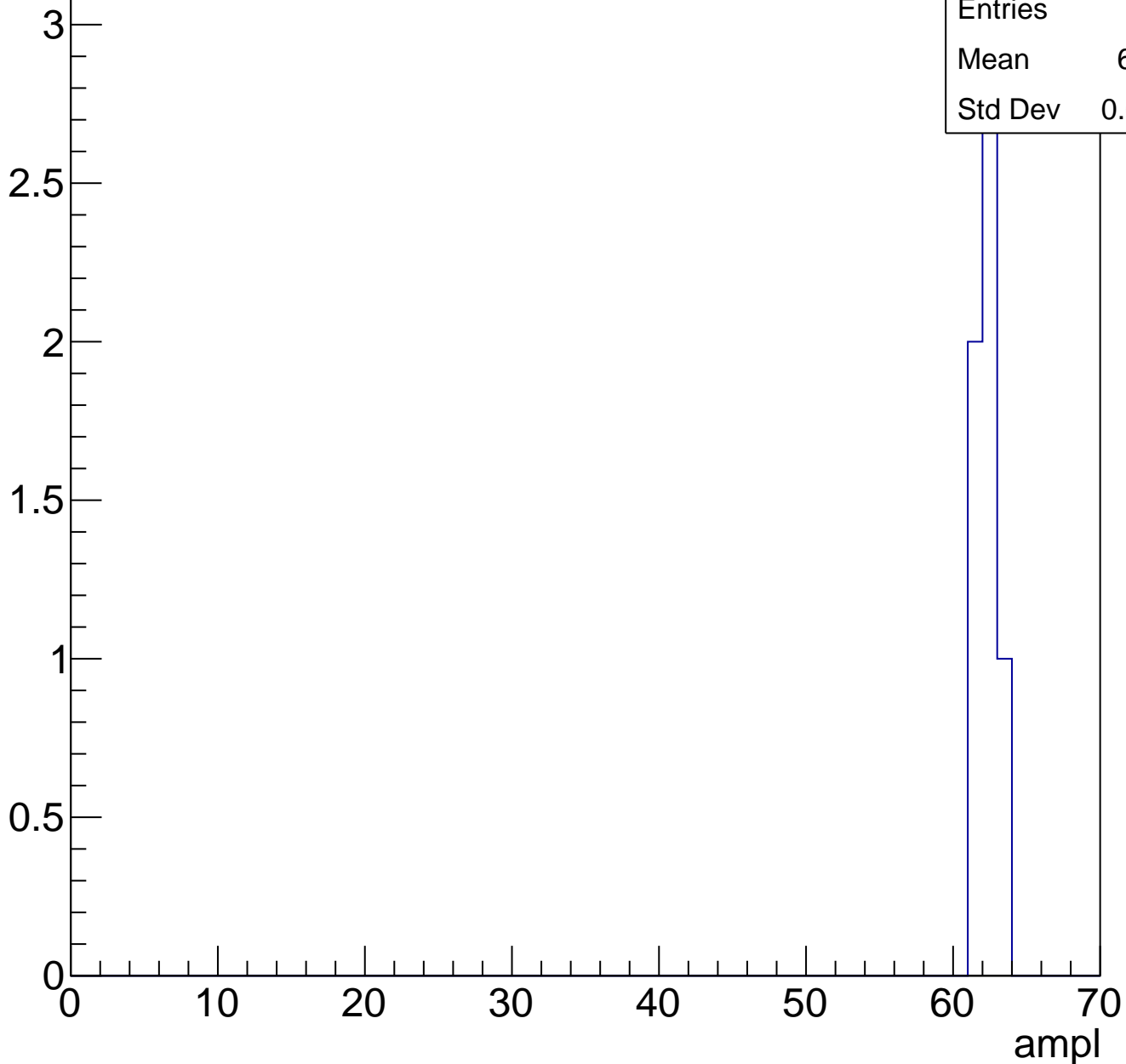
60

70

B1L102S, U9-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch63, adc0

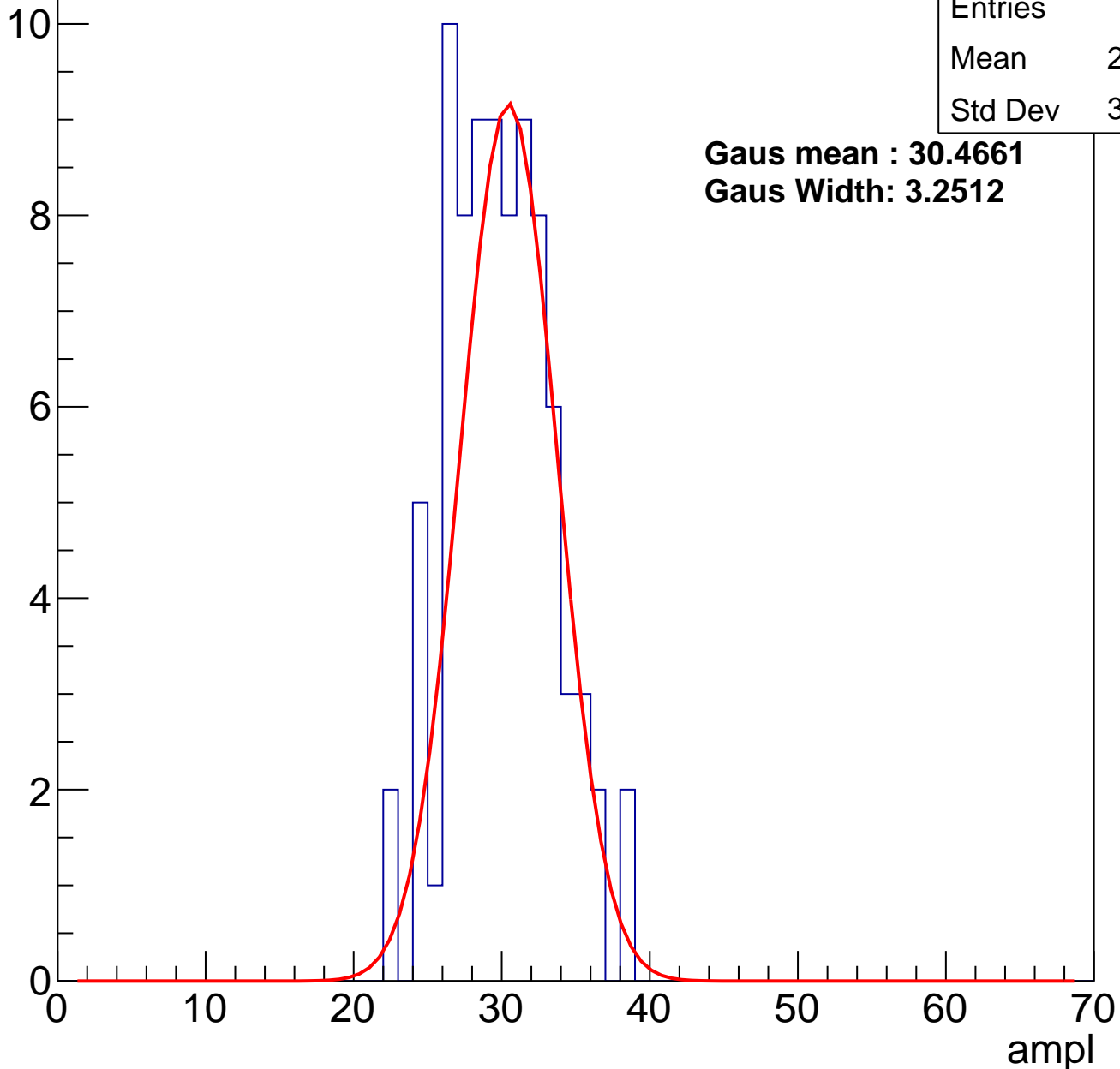
calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	29.48
Std Dev	3.439

Gaus mean : 30.4661

Gaus Width: 3.2512

Entry



B1L102S, U9-ch63, adc1

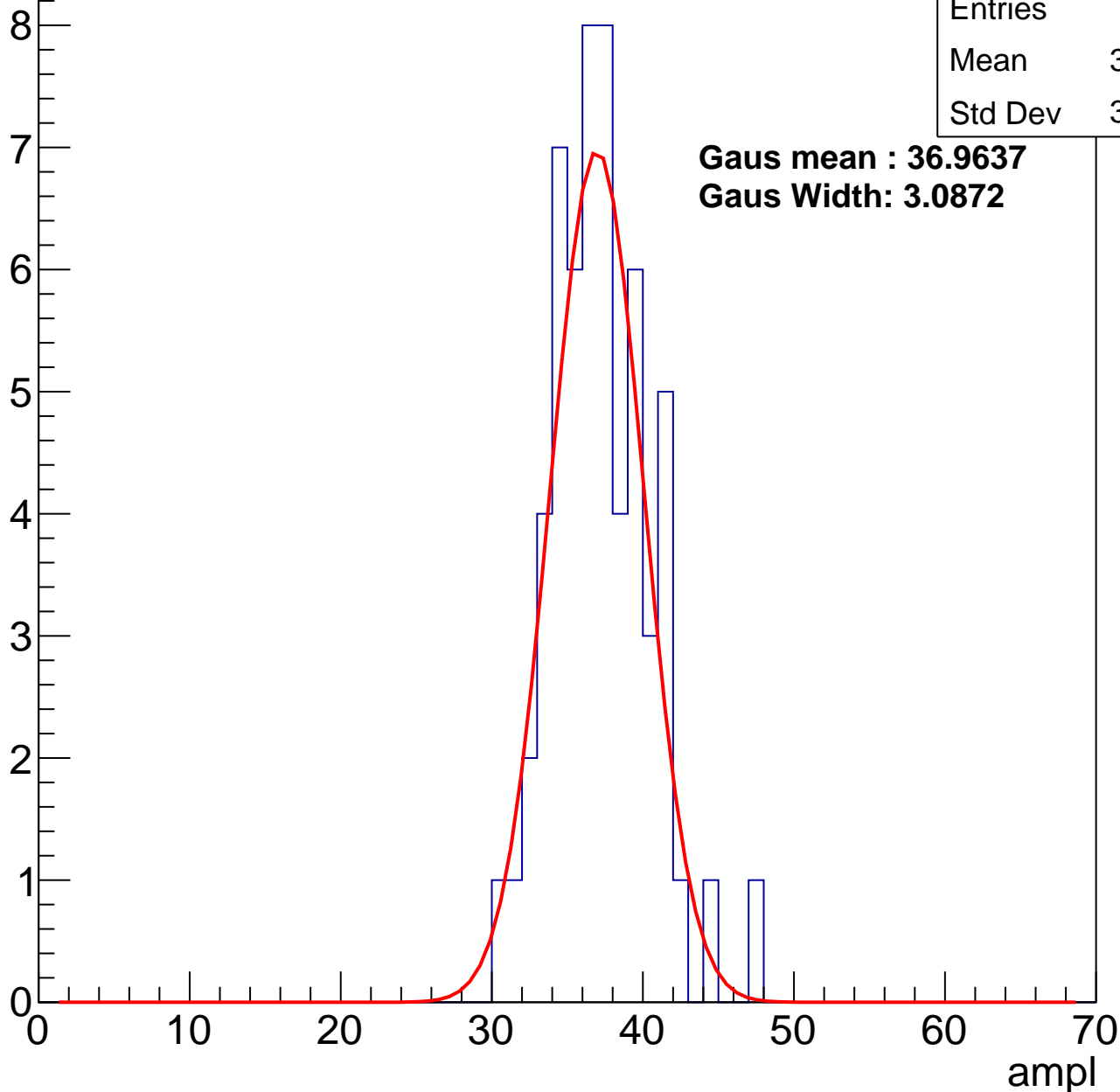
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.78
Std Dev	3.217

Gaus mean : 36.9637

Gaus Width: 3.0872



B1L102S, U9-ch63, adc2

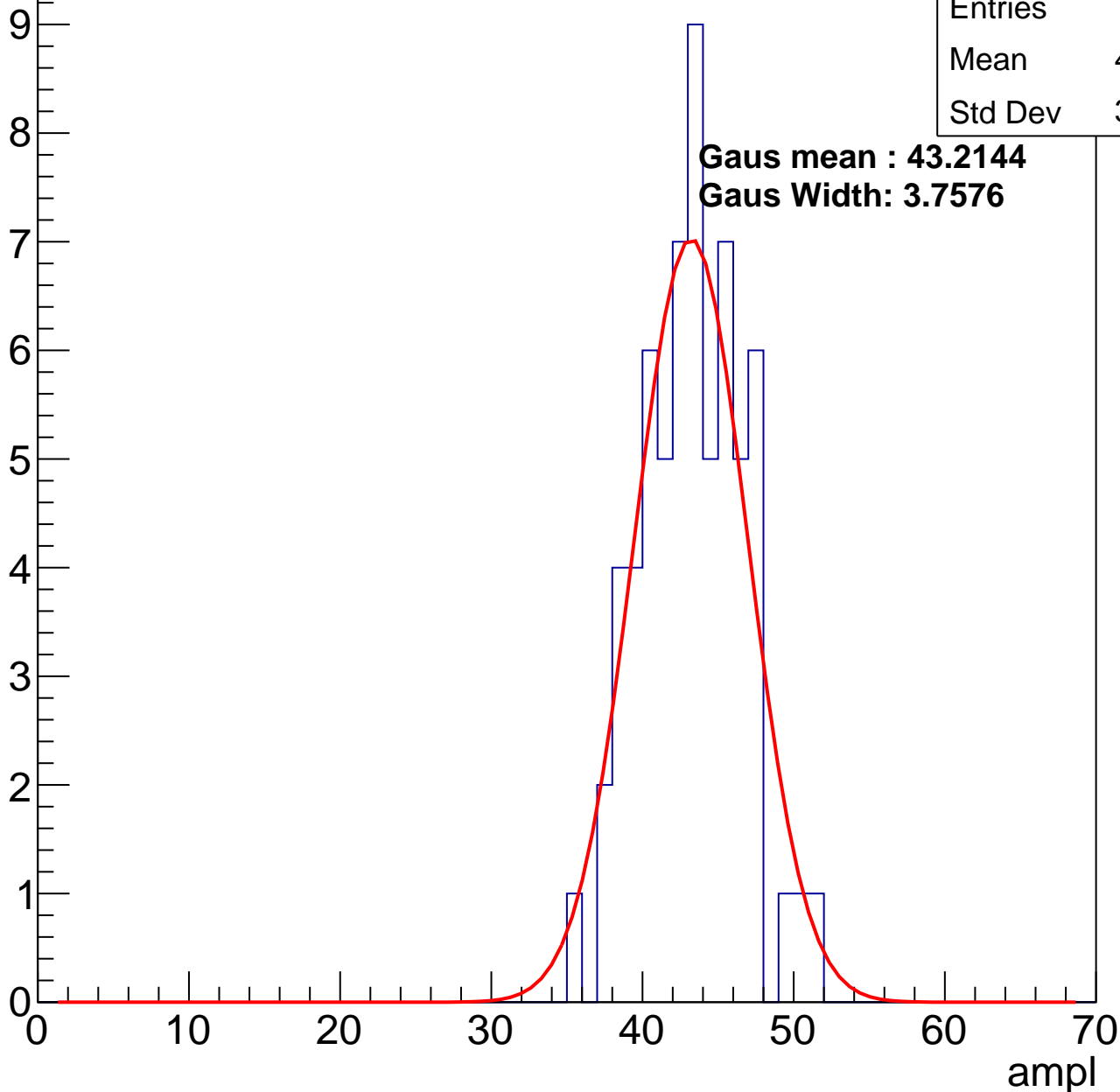
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.81
Std Dev	3.311

Gaus mean : 43.2144

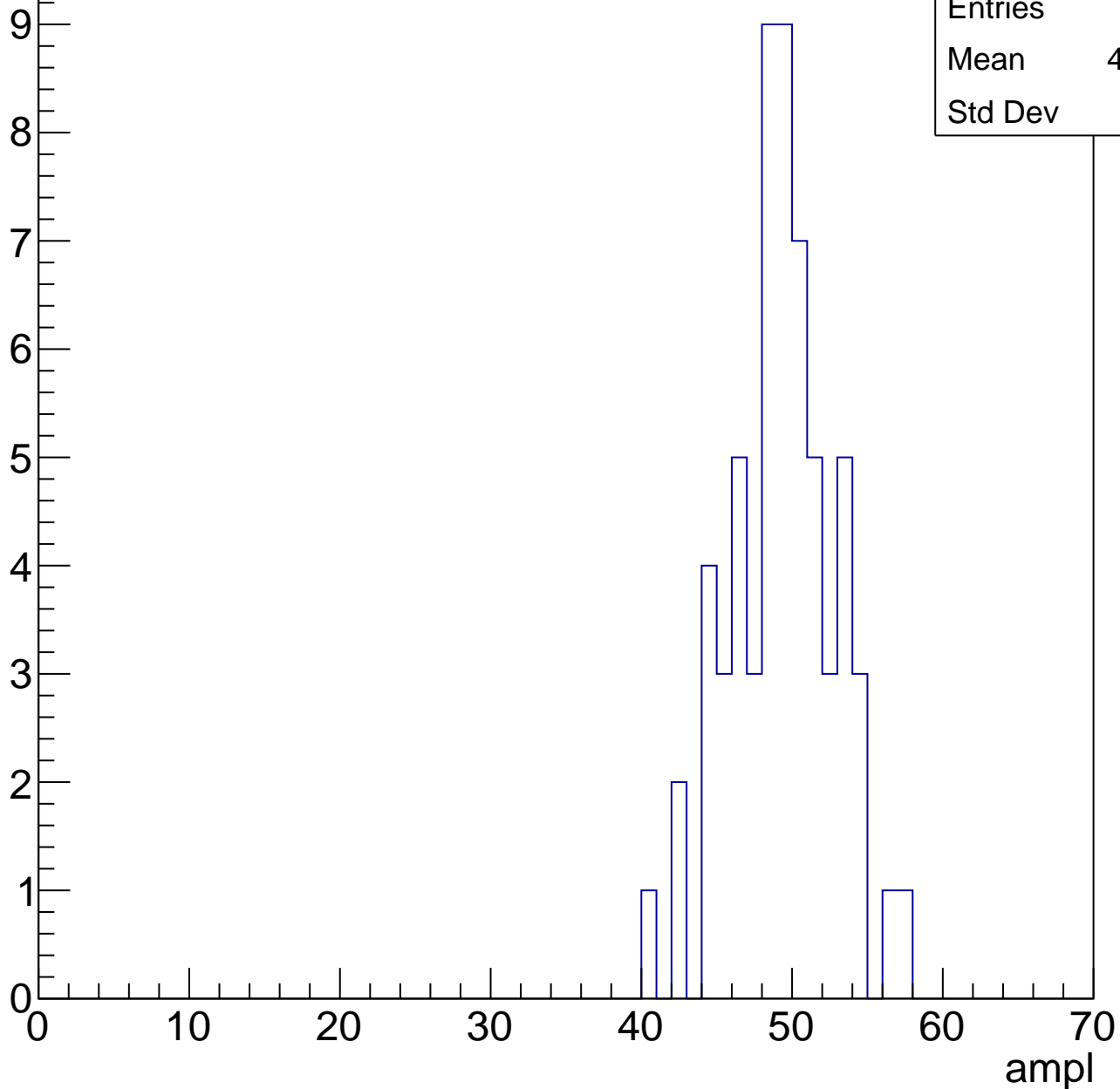
Gaus Width: 3.7576



B1L102S, U9-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

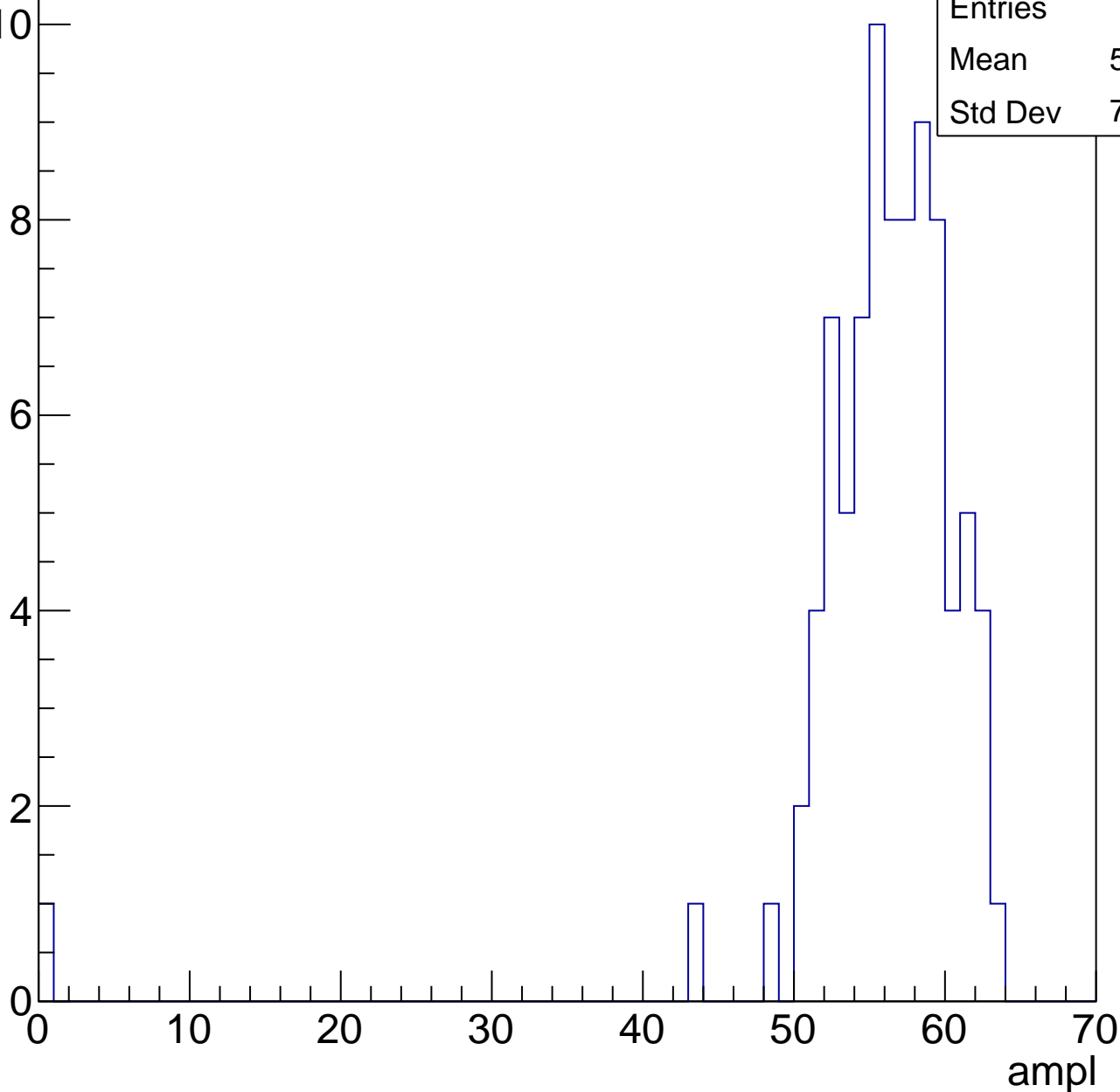


B1L102S, U9-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	55.36
Std Dev	7.029

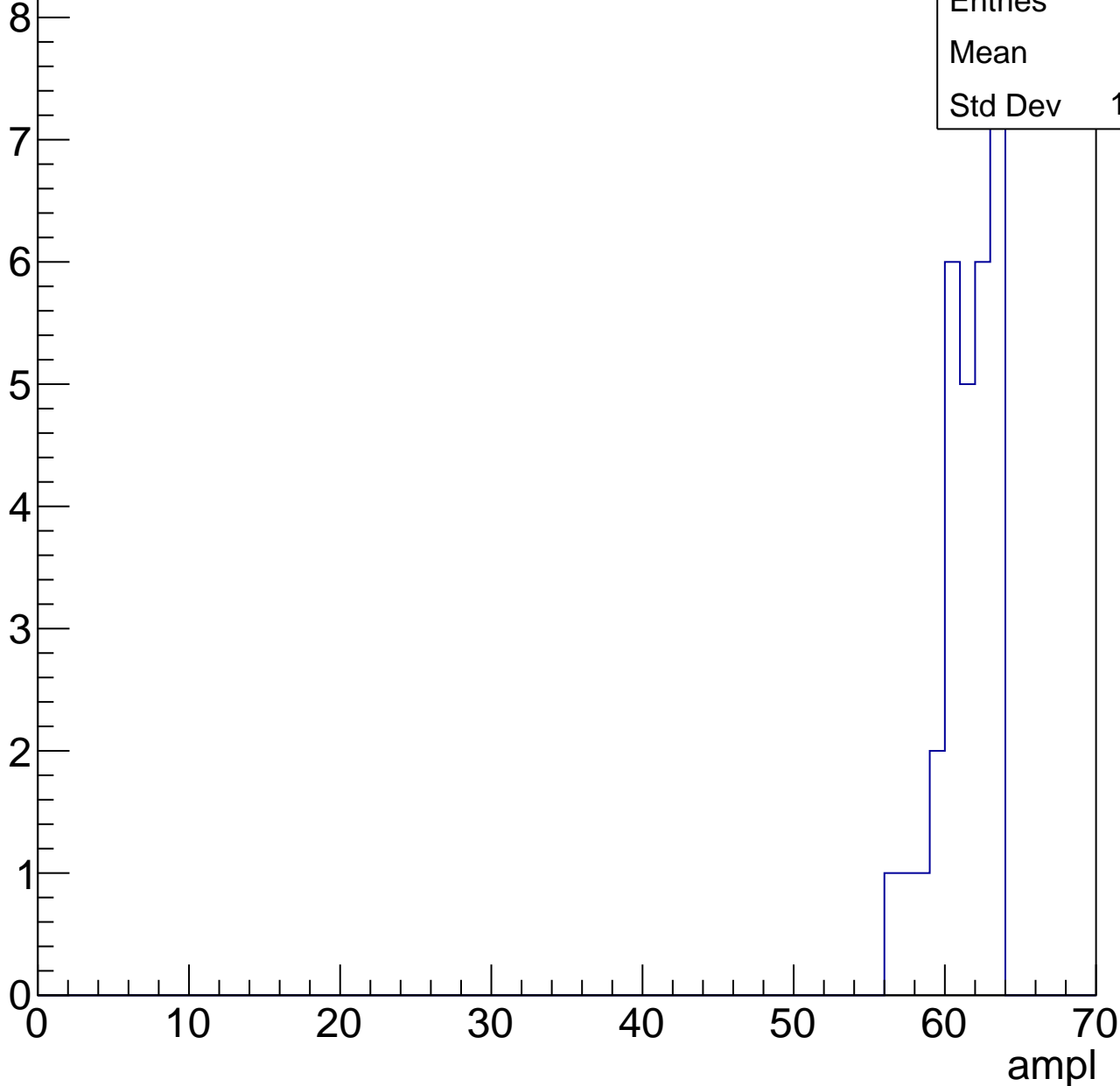


B1L102S, U9-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	61
Std Dev	1.844



B1L102S, U9-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch64, adc0

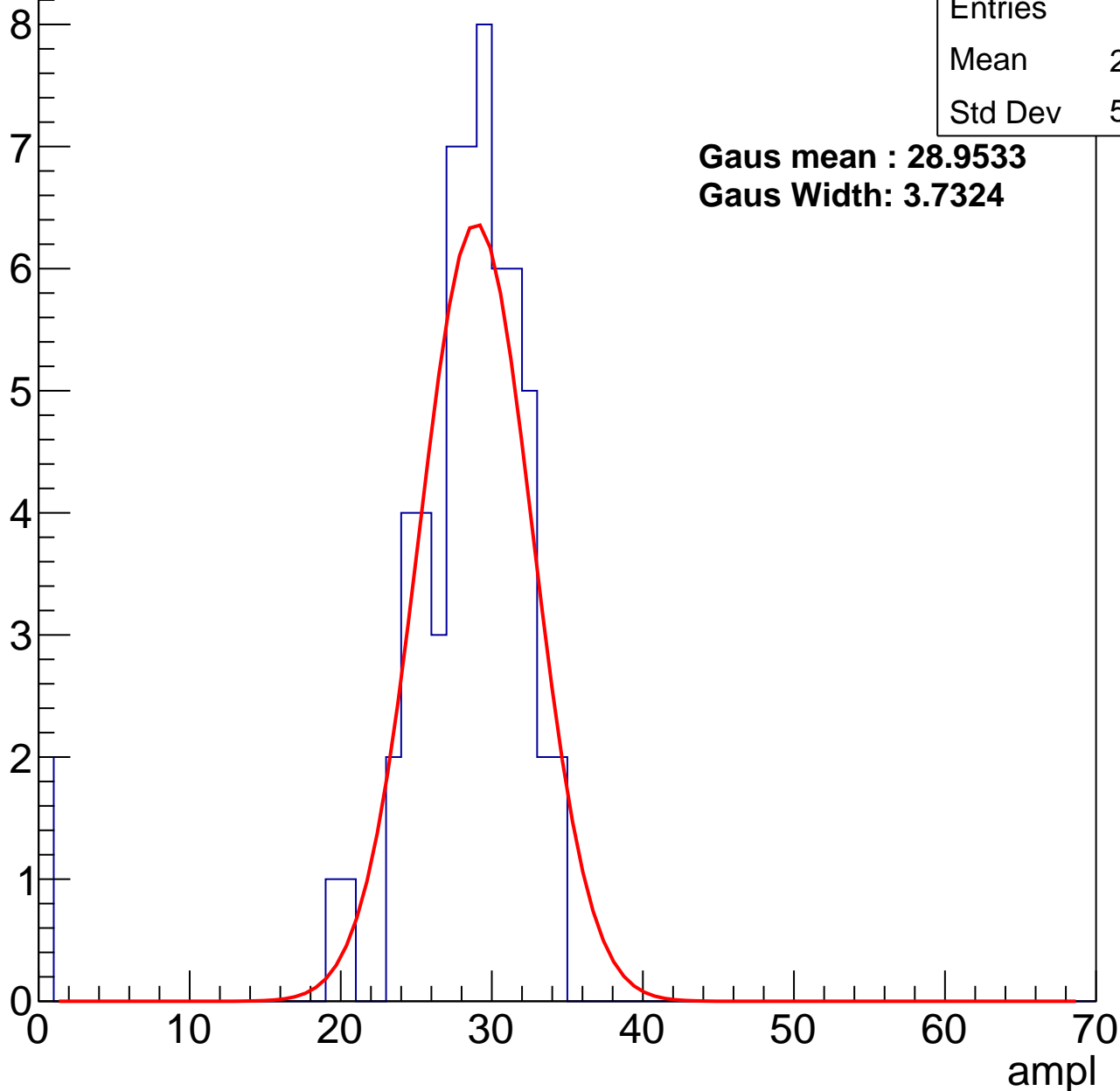
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	27.27
Std Dev	5.969

Gaus mean : 28.9533

Gaus Width: 3.7324



B1L102S, U9-ch64, adc1

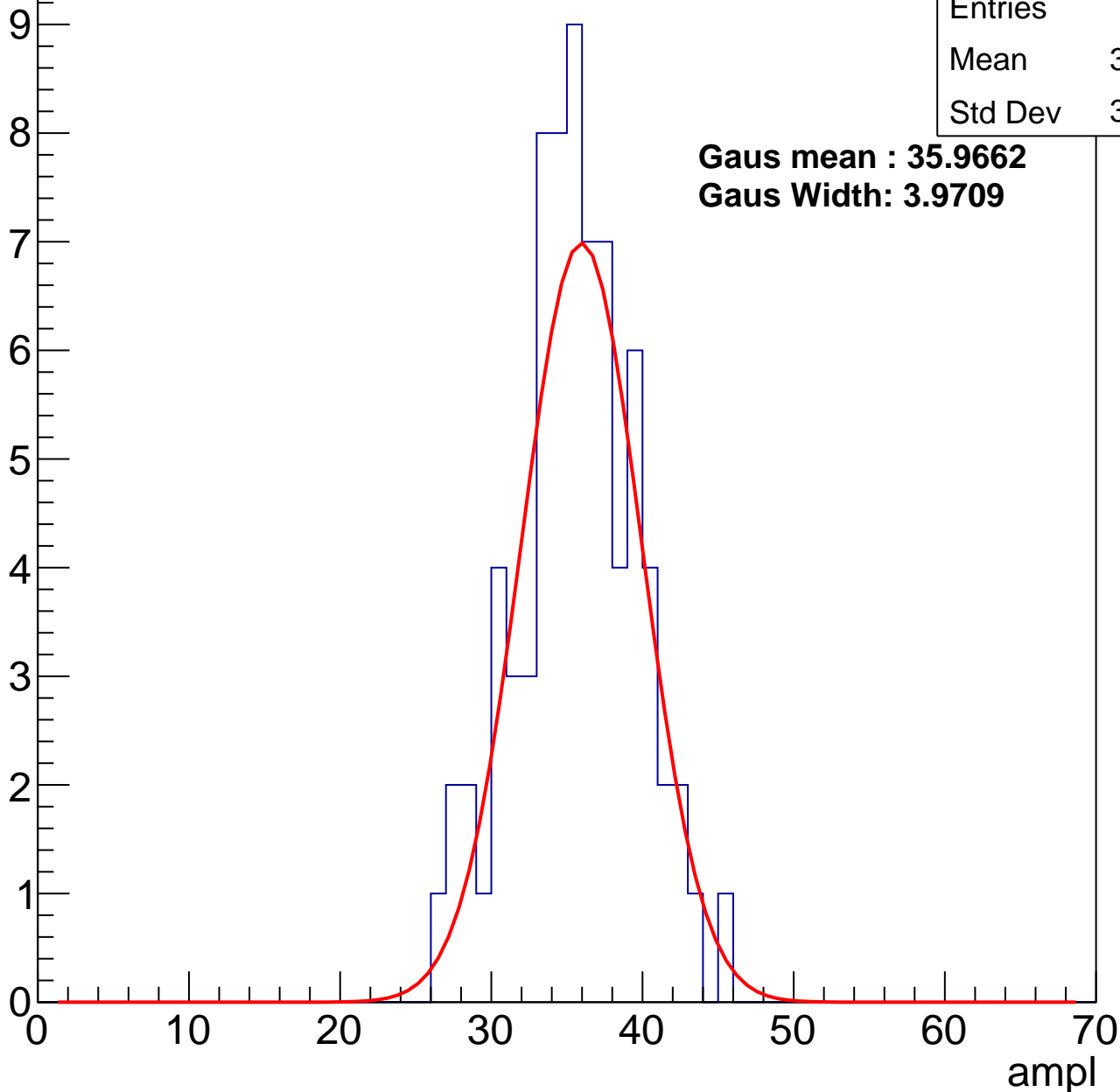
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.15
Std Dev	3.928

Gaus mean : 35.9662

Gaus Width: 3.9709



B1L102S, U9-ch64, adc2

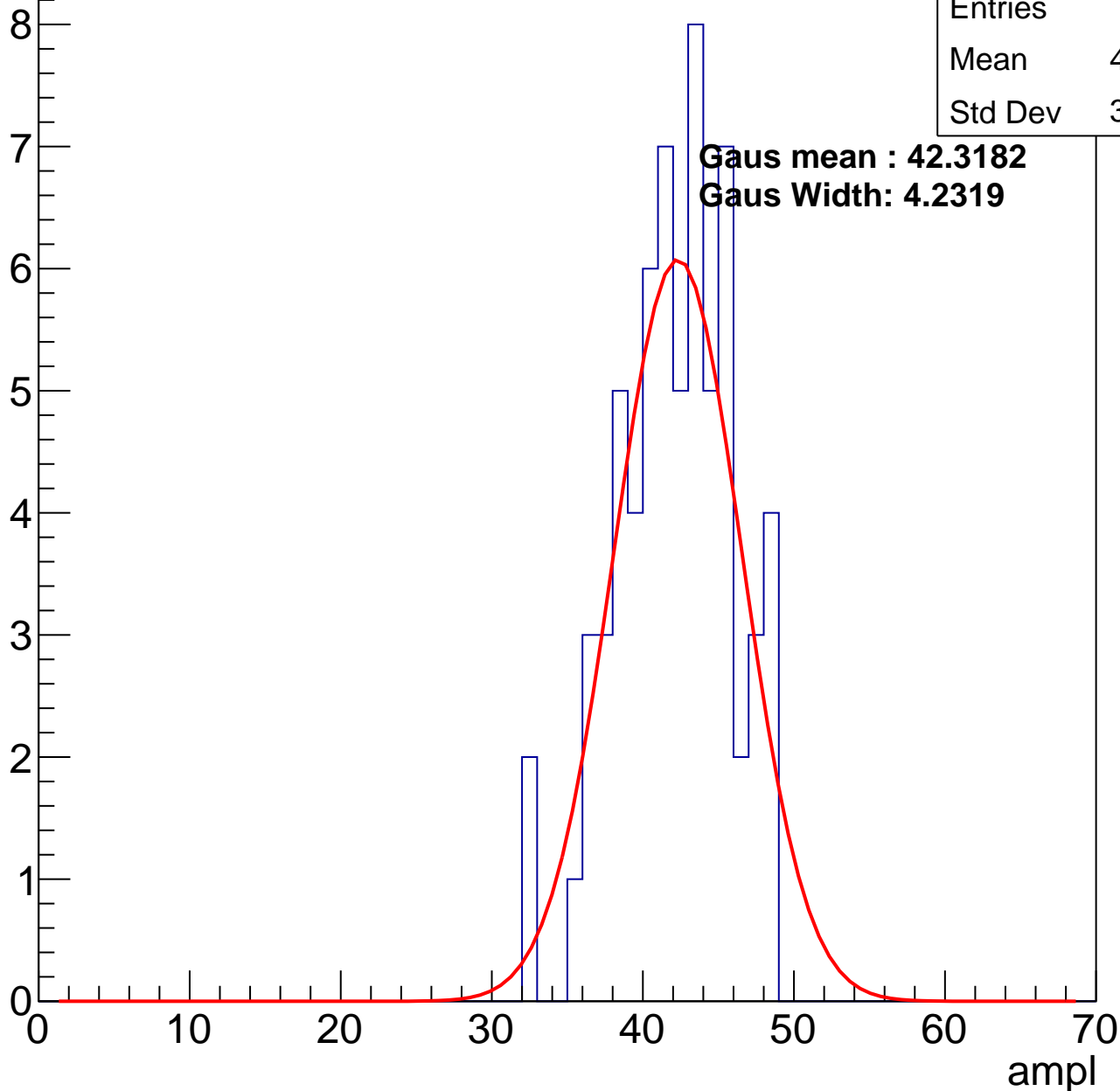
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.62
Std Dev	3.745

Gaus mean : 42.3182

Gaus Width: 4.2319



B1L102S, U9-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

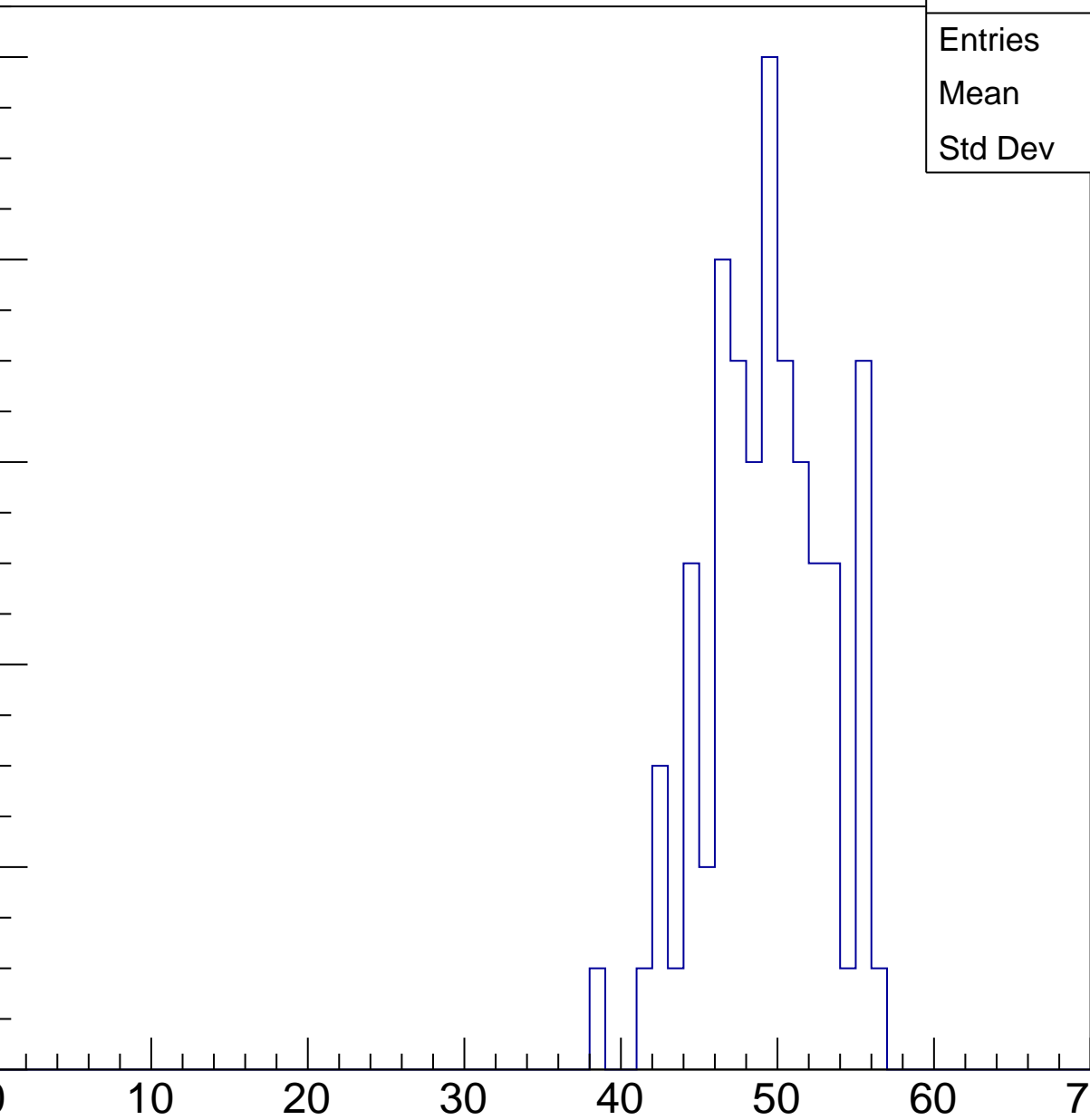
Entries	76
Mean	48.8
Std Dev	3.822

Entry

10
8
6
4
2
0

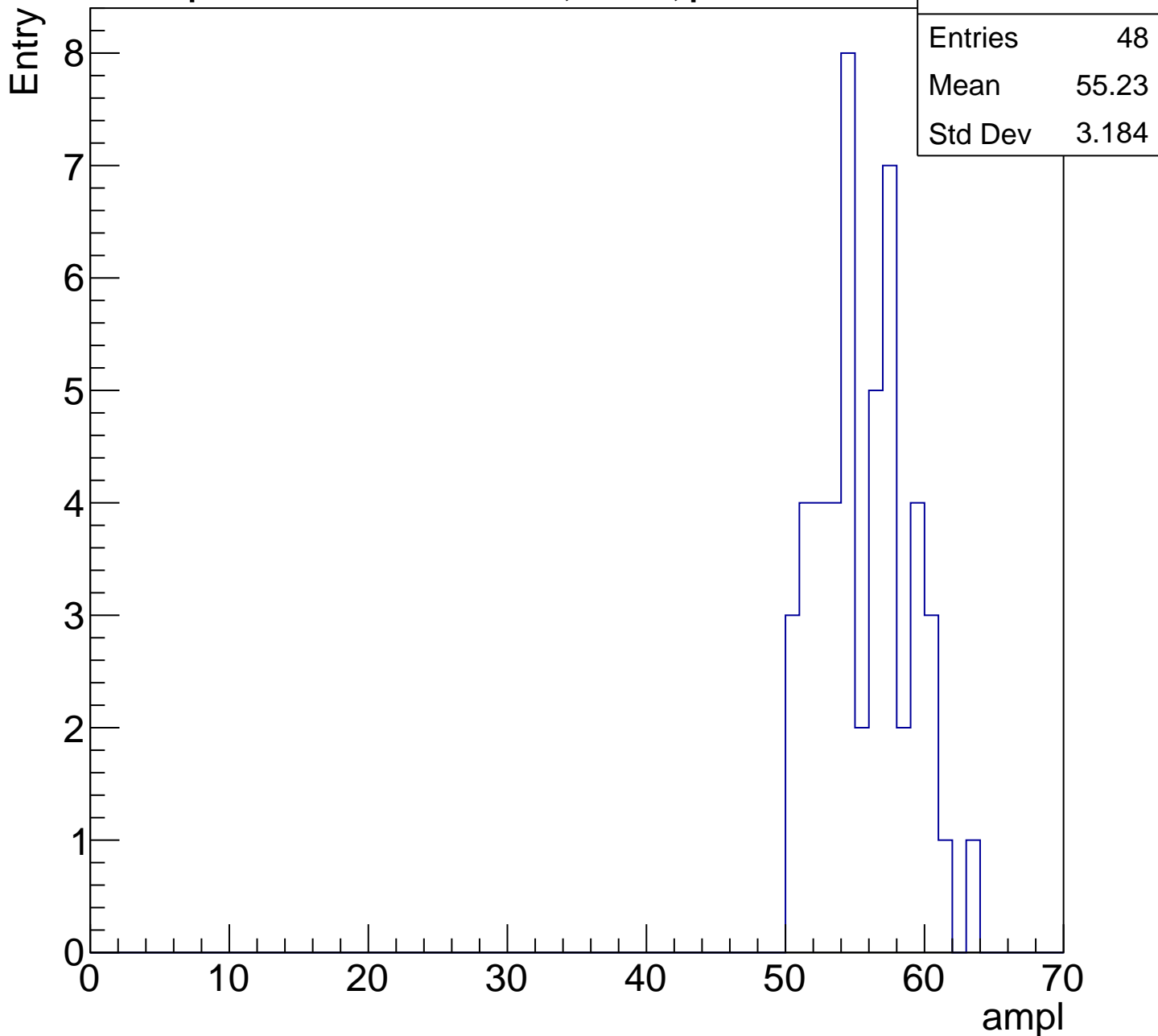
0 10 20 30 40 50 60 70

ampl



B1L102S, U9-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

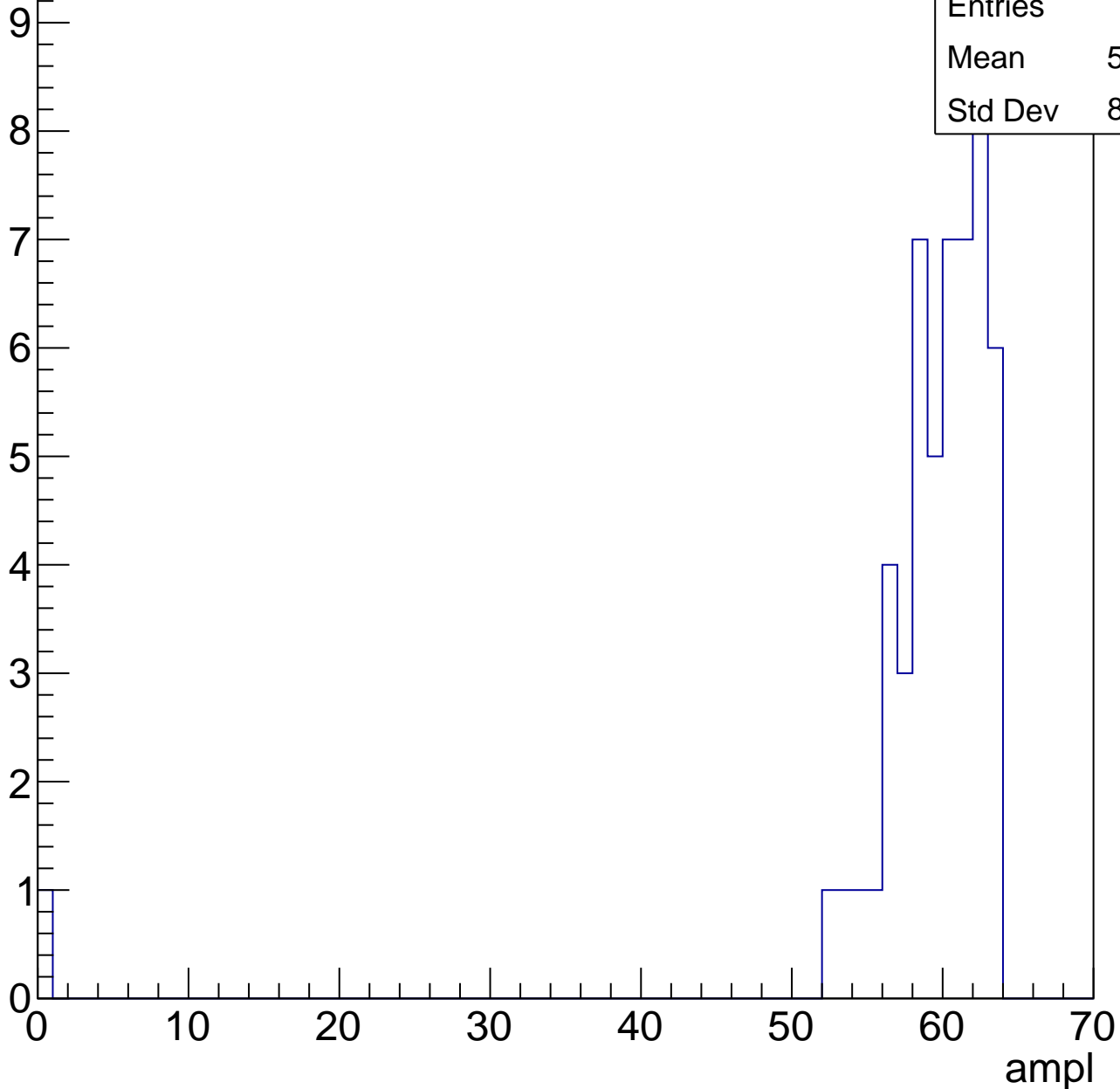


B1L102S, U9-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

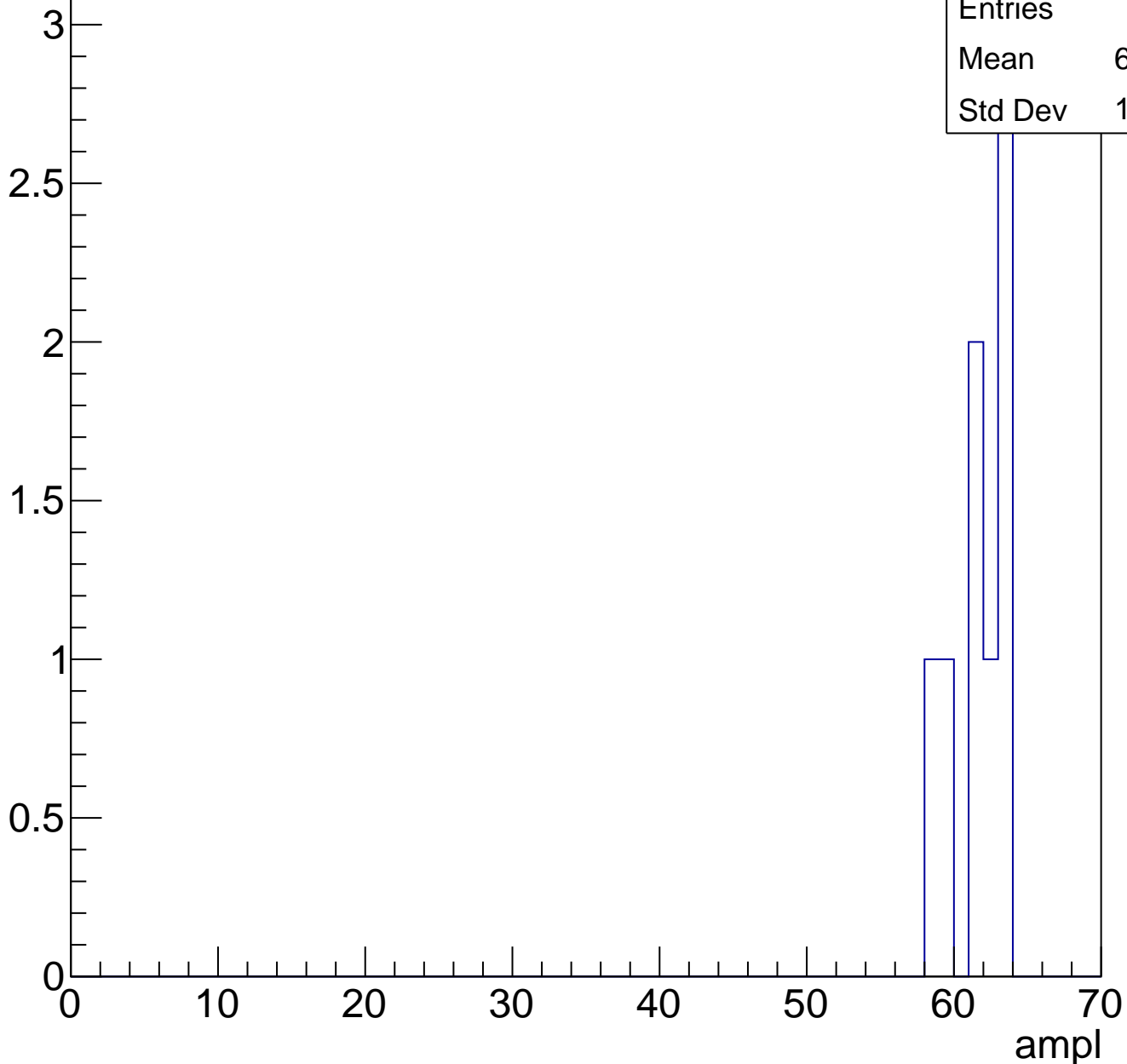
Entries	53
Mean	58.36
Std Dev	8.525



B1L102S, U9-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch65, adc0

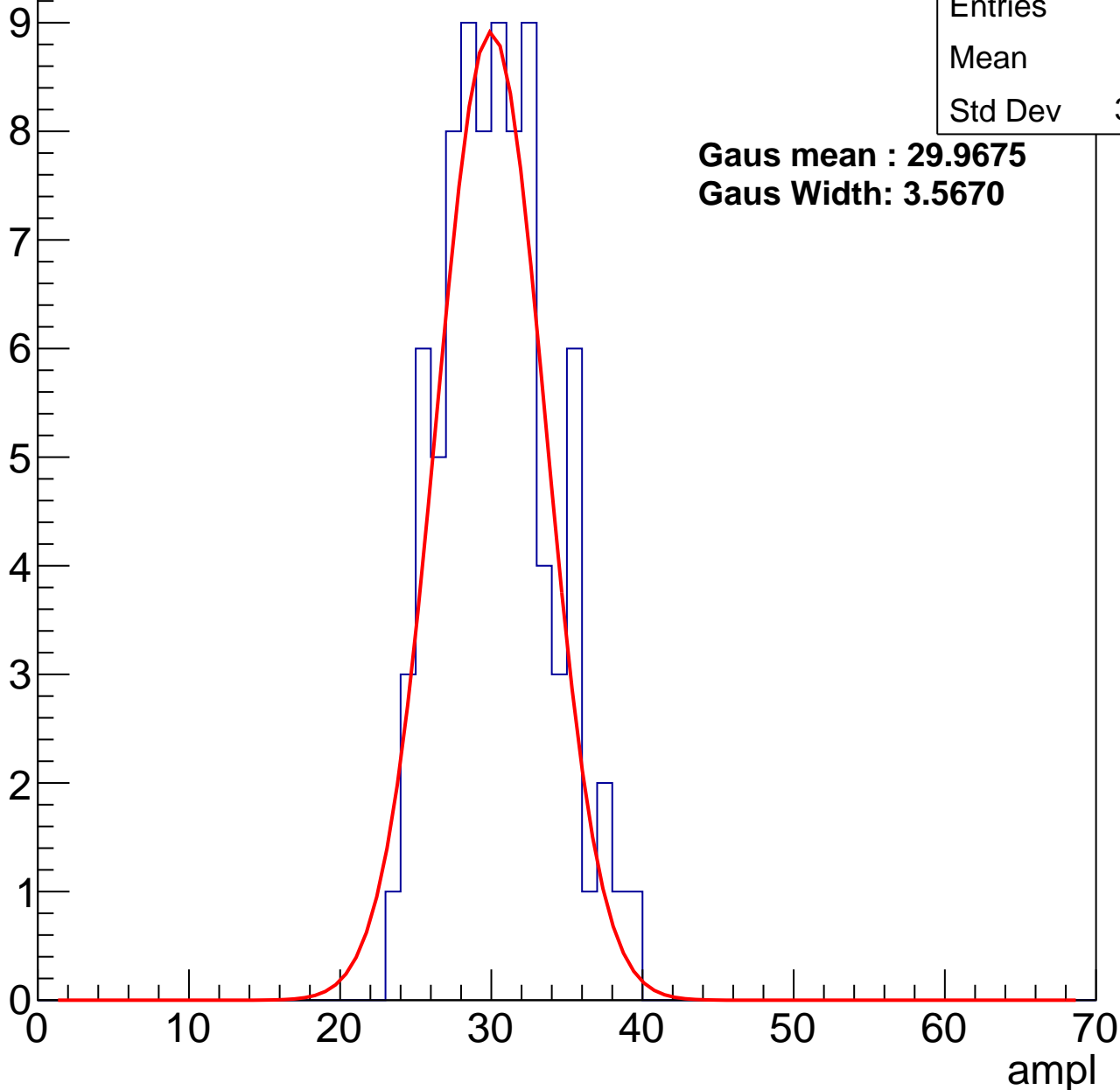
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	29.9
Std Dev	3.551

Gaus mean : 29.9675

Gaus Width: 3.5670



B1L102S, U9-ch65, adc1

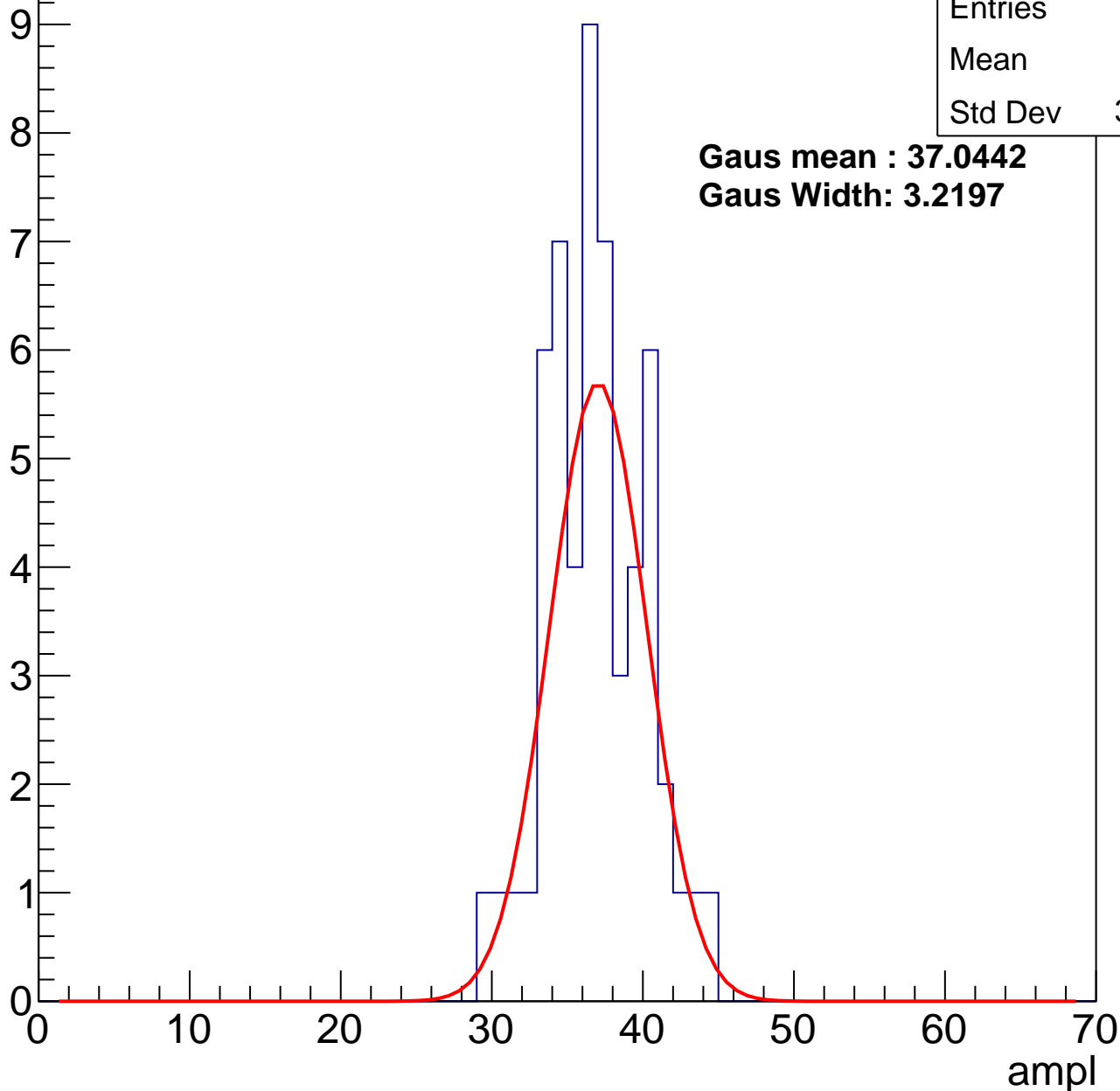
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	36.4
Std Dev	3.171

Gaus mean : 37.0442

Gaus Width: 3.2197



B1L102S, U9-ch65, adc2

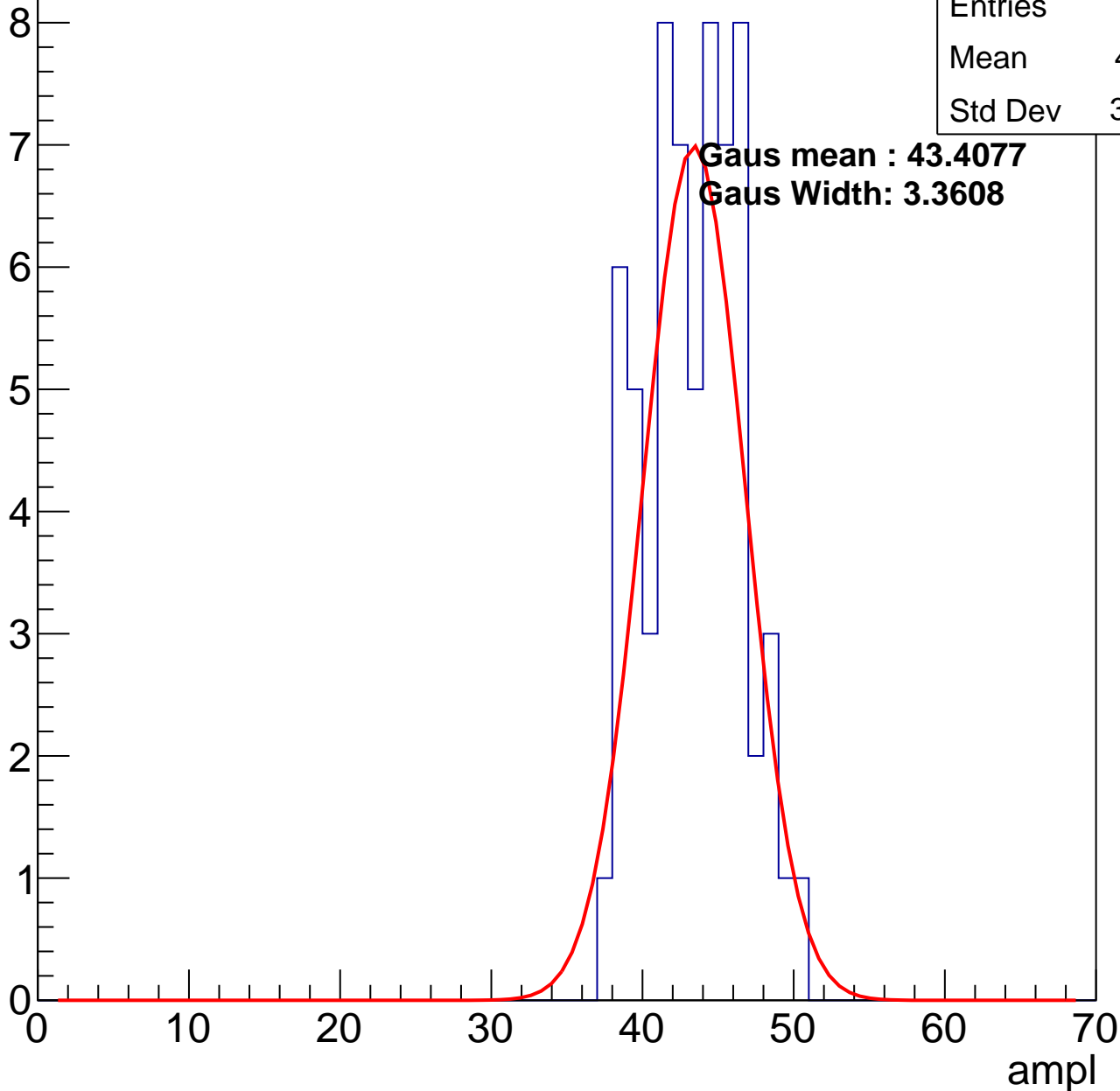
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.91
Std Dev	3.117

Gaus mean : 43.4077

Gaus Width: 3.3608

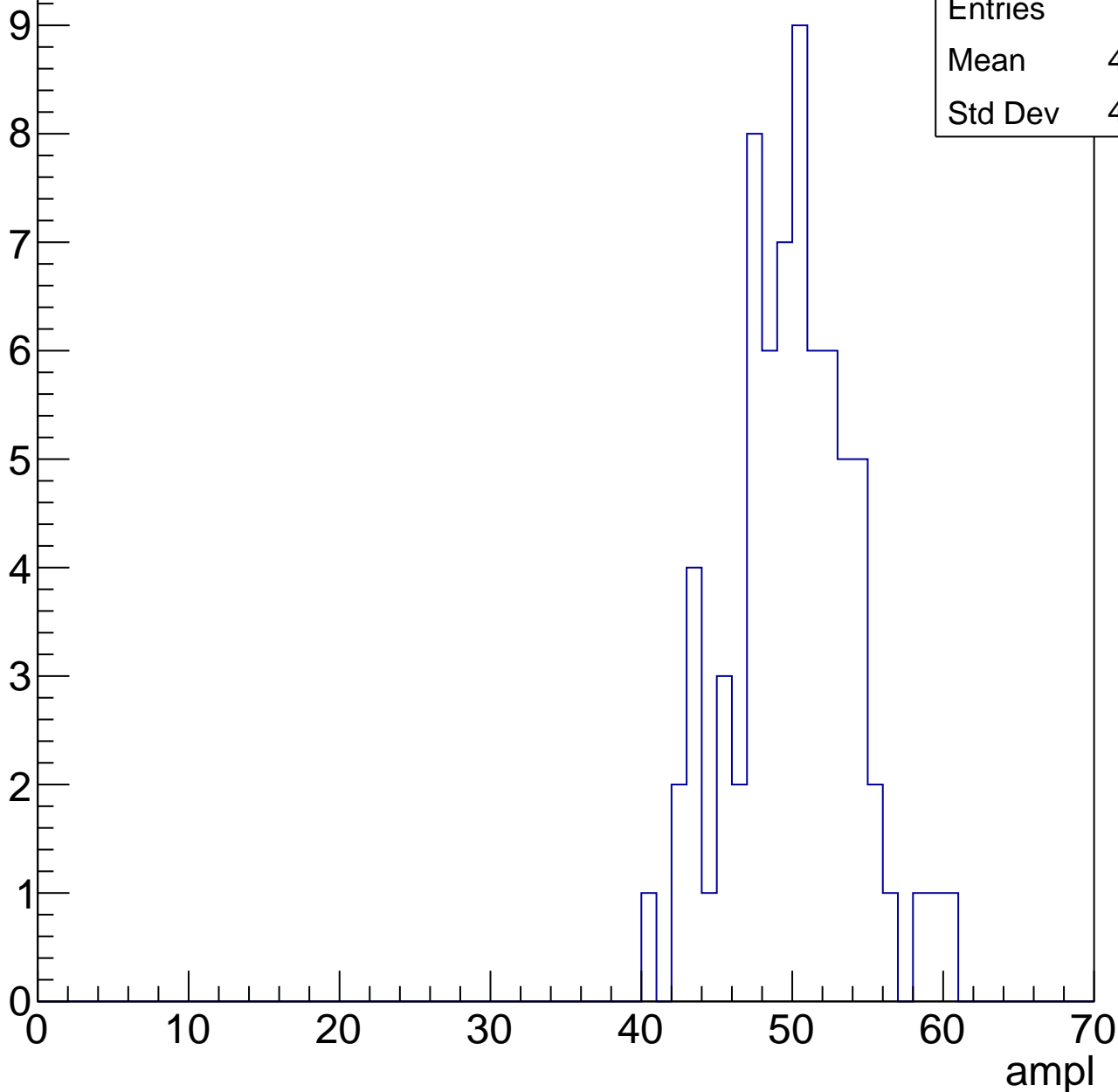


B1L102S, U9-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.58
Std Dev	4.013

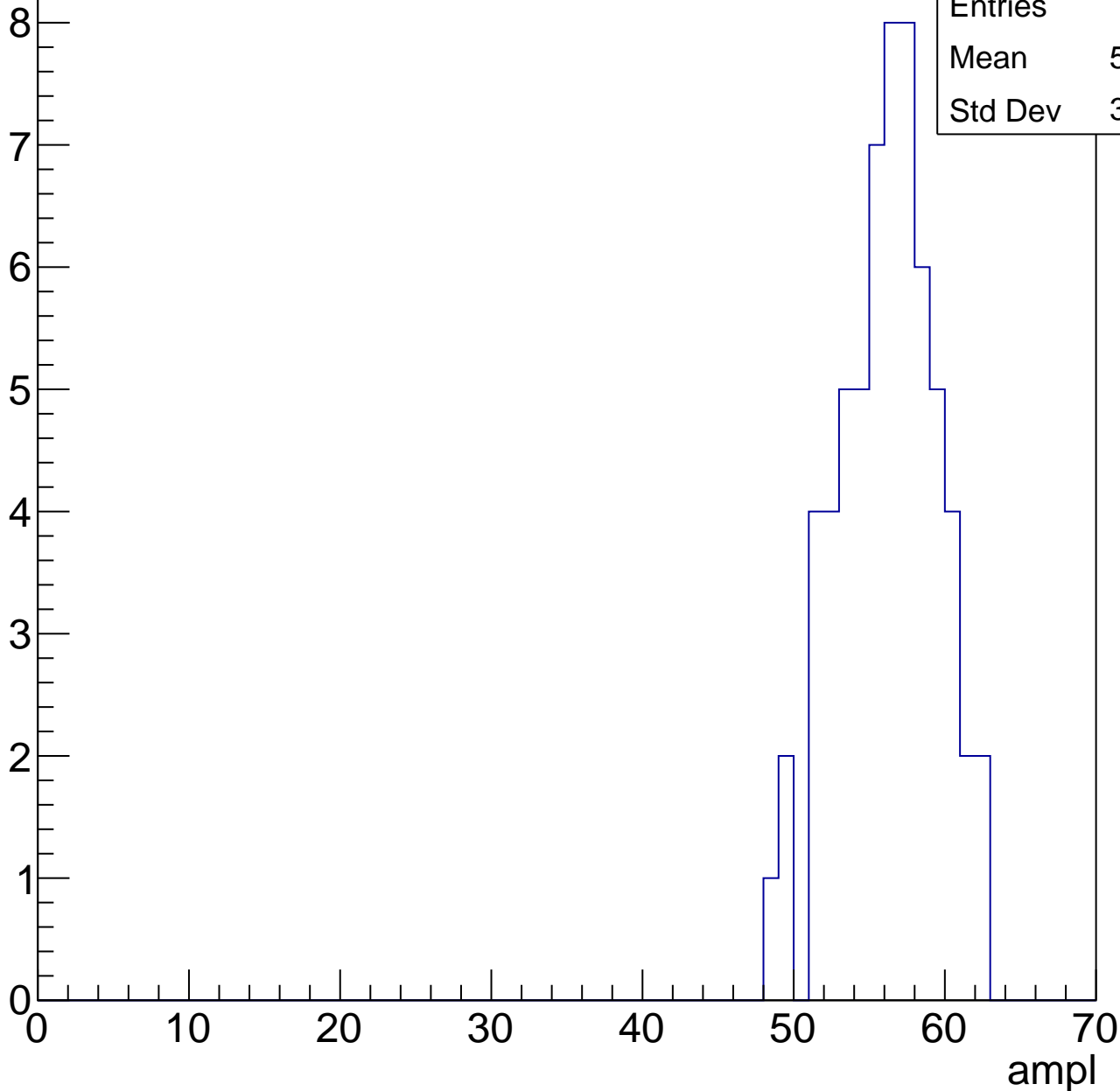


B1L102S, U9-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.73
Std Dev	3.228

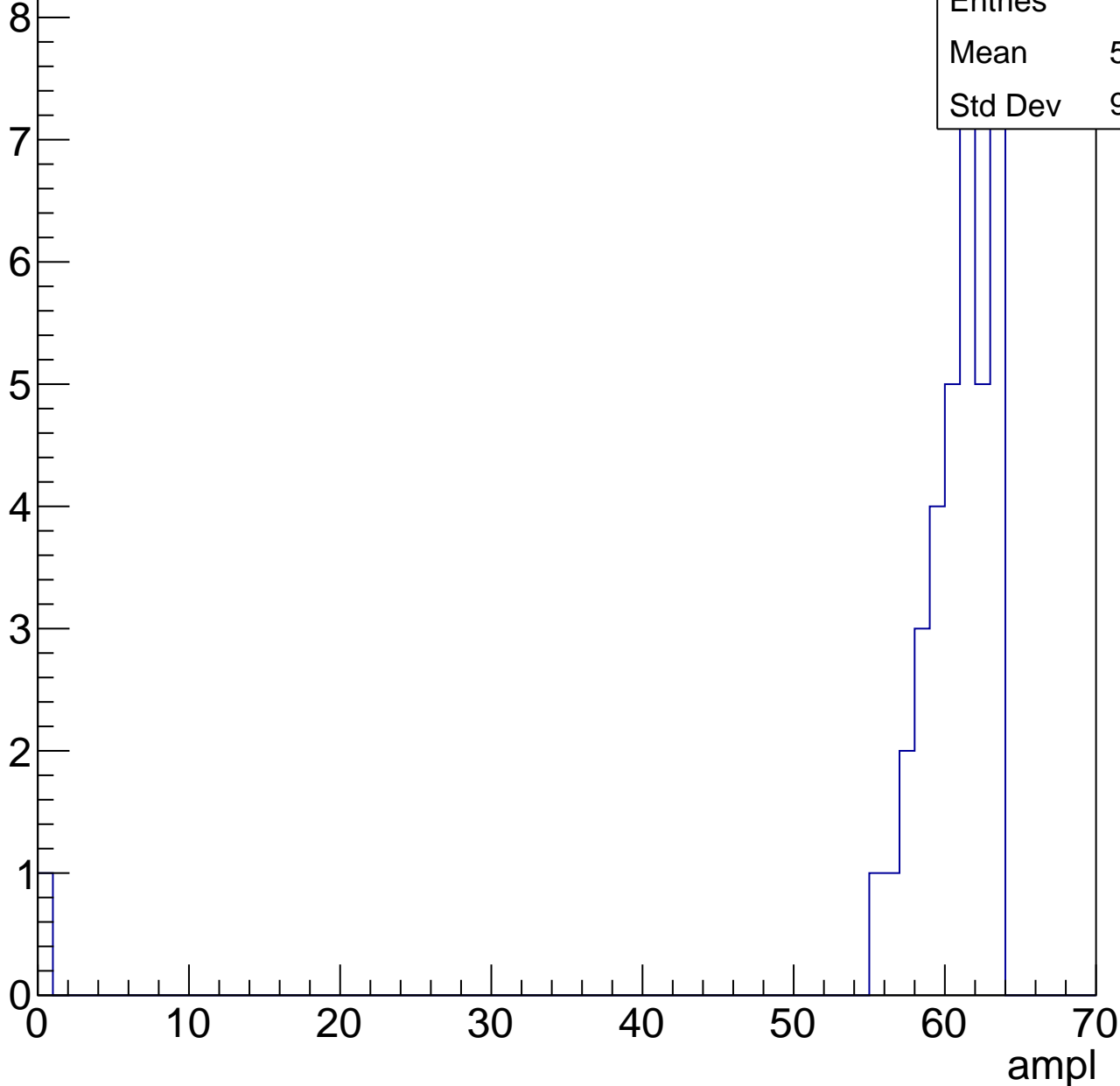


B1L102S, U9-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

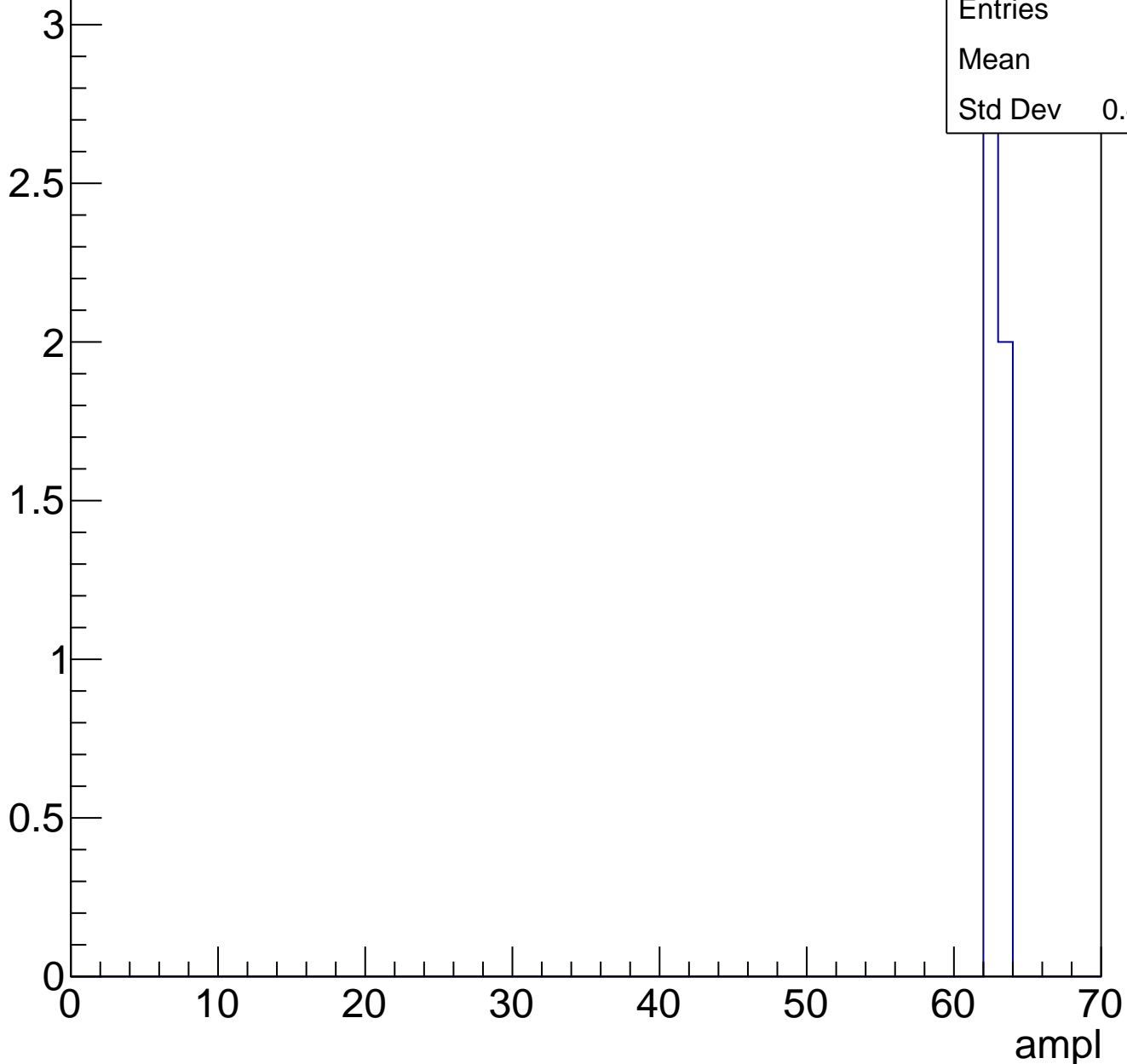
Entries	38
Mean	58.87
Std Dev	9.903



B1L102S, U9-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch66, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	27.6
Std Dev	4.652

Gaus mean : 28.6279

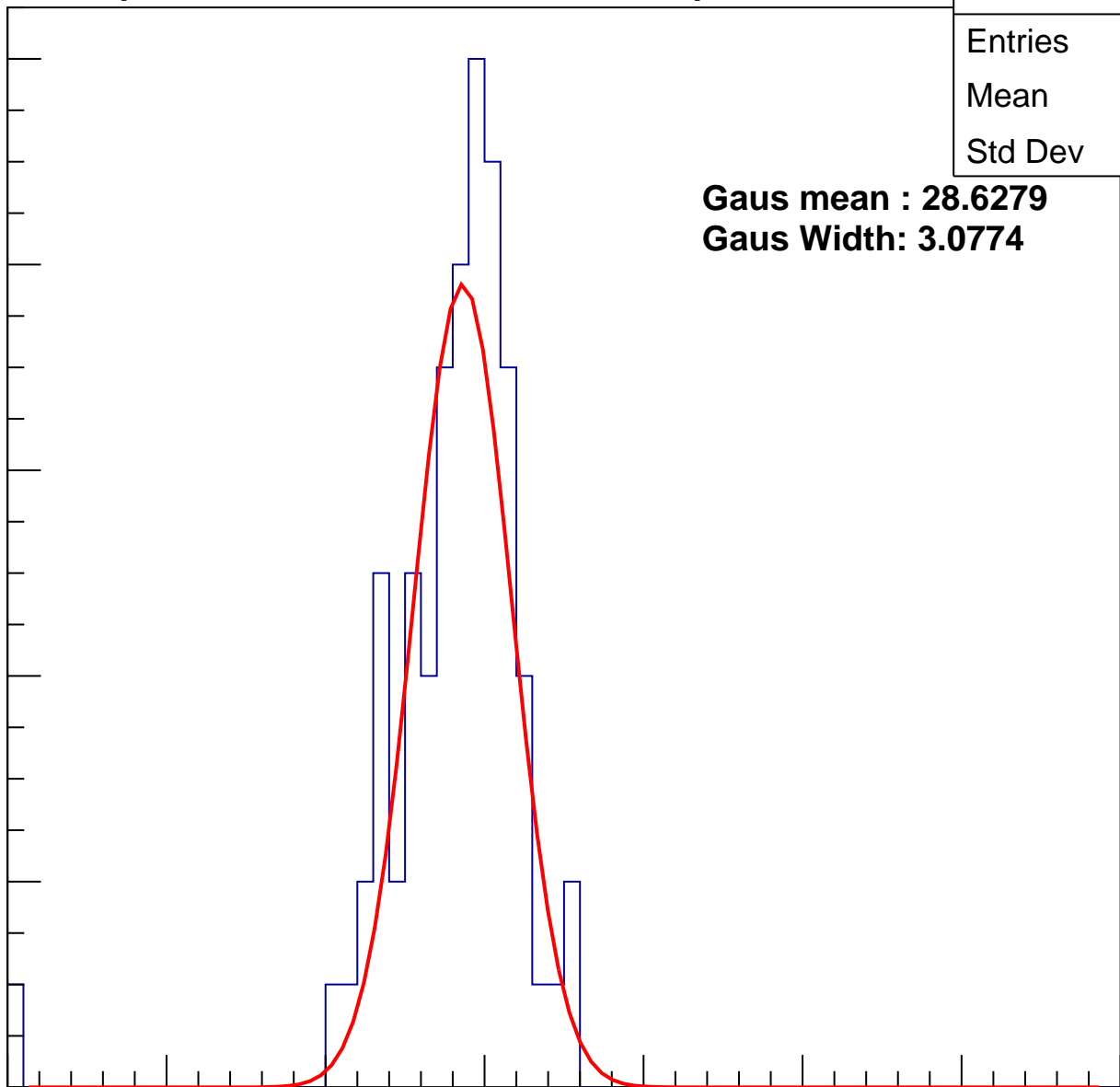
Gaus Width: 3.0774

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch66, adc1

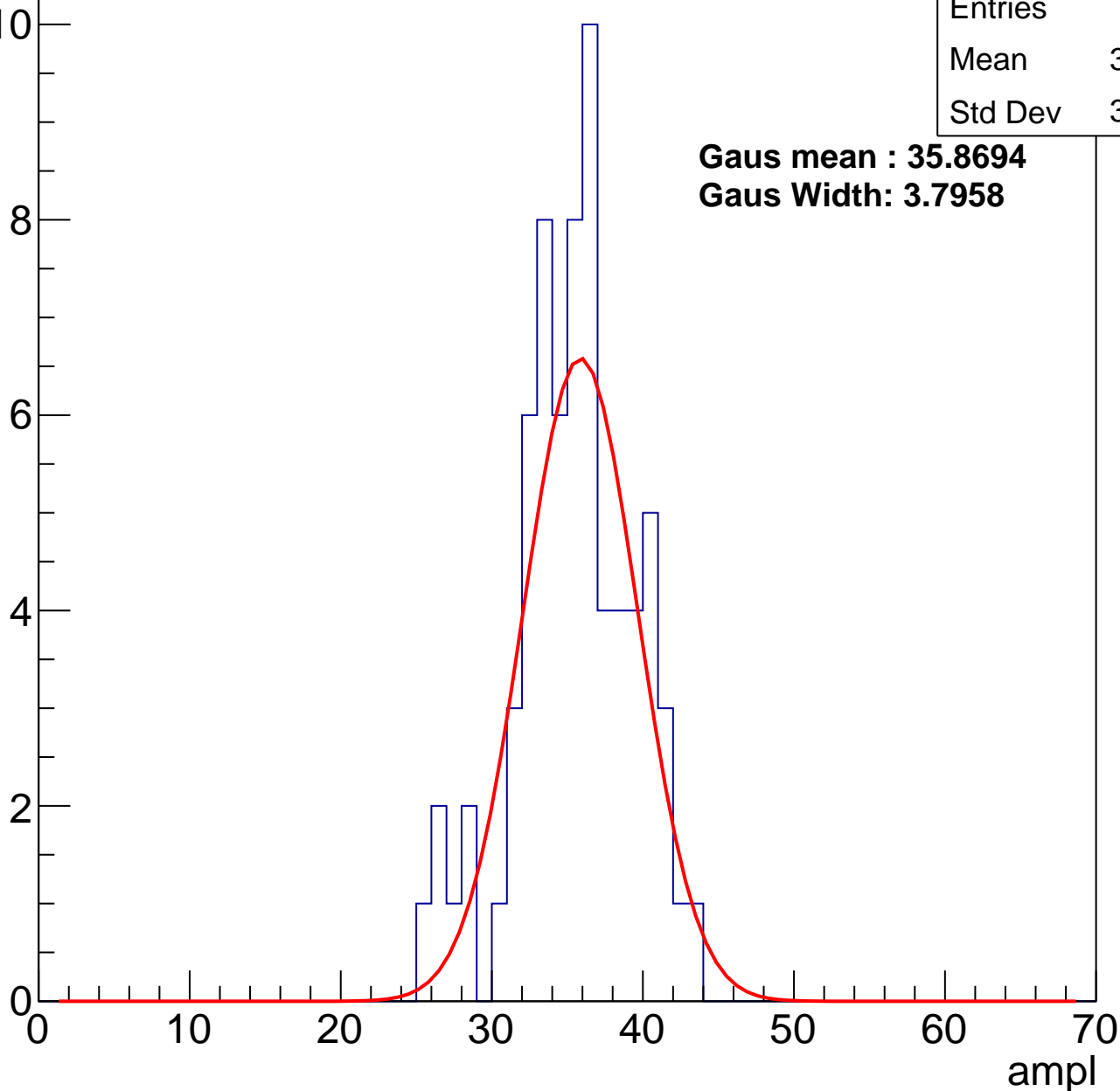
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.96
Std Dev	3.893

Gaus mean : 35.8694

Gaus Width: 3.7958



B1L102S, U9-ch66, adc2

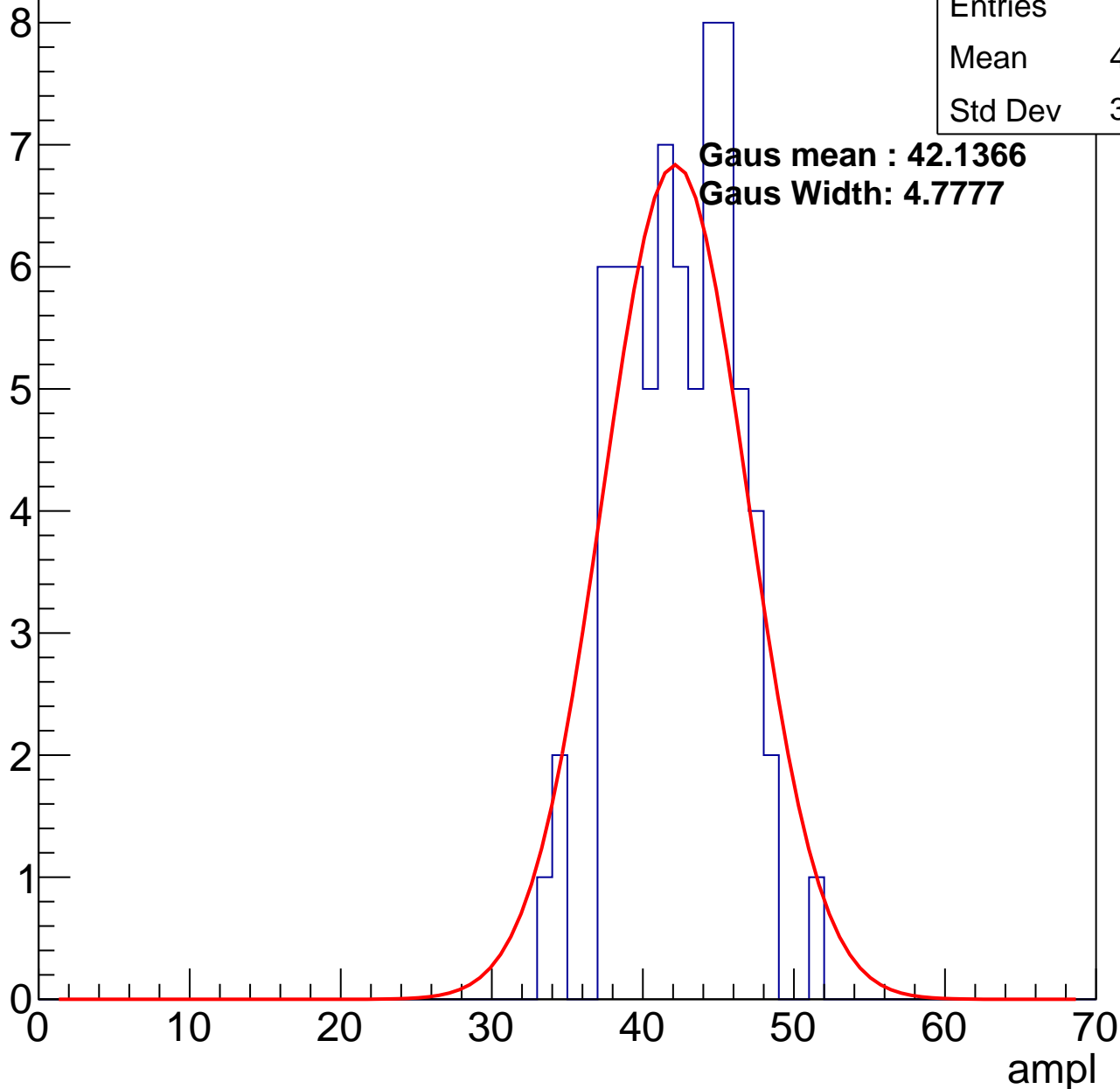
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.89
Std Dev	3.684

Gaus mean : 42.1366

Gaus Width: 4.7777

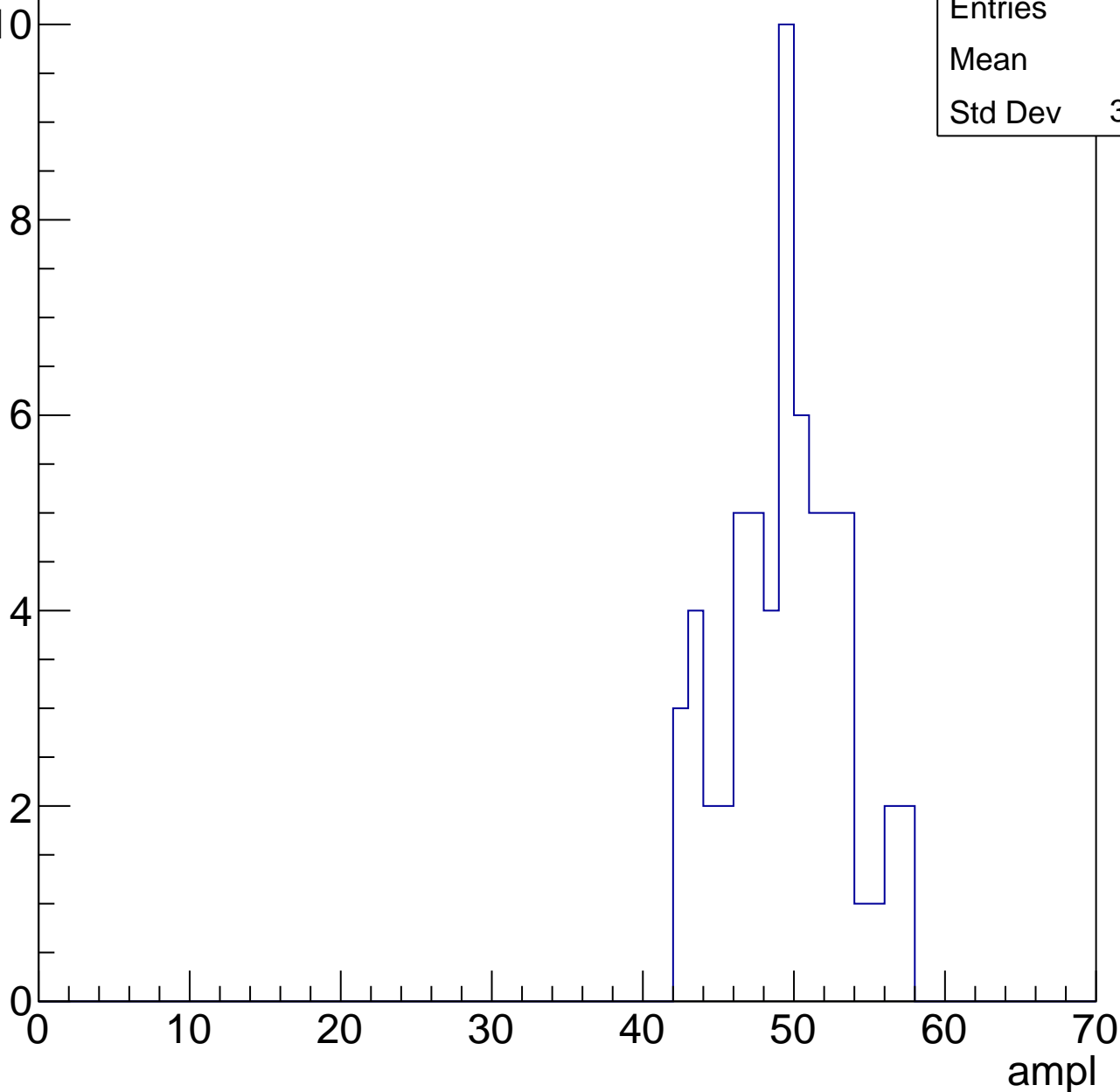


B1L102S, U9-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49
Std Dev	3.767

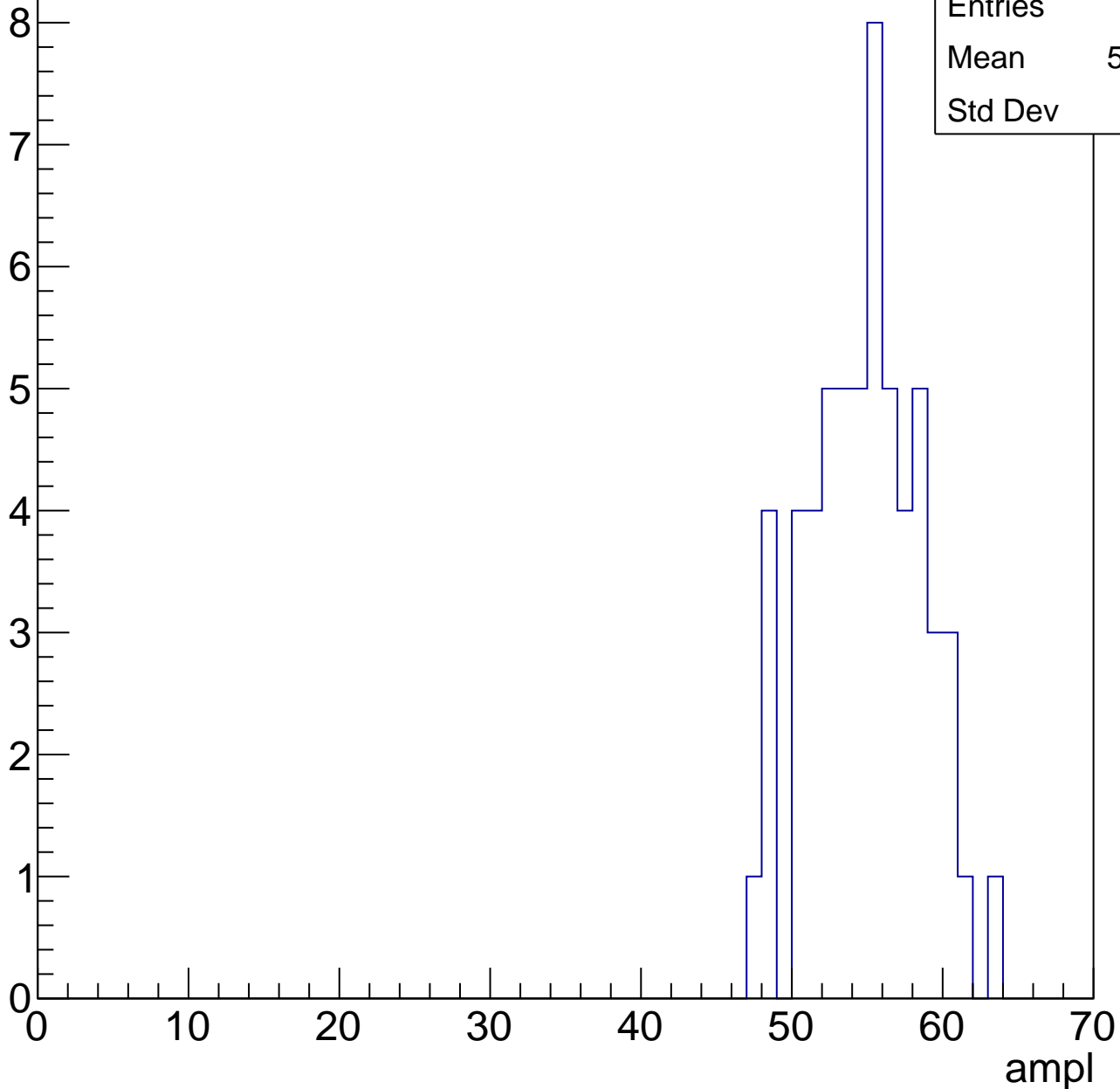


B1L102S, U9-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.43
Std Dev	3.63

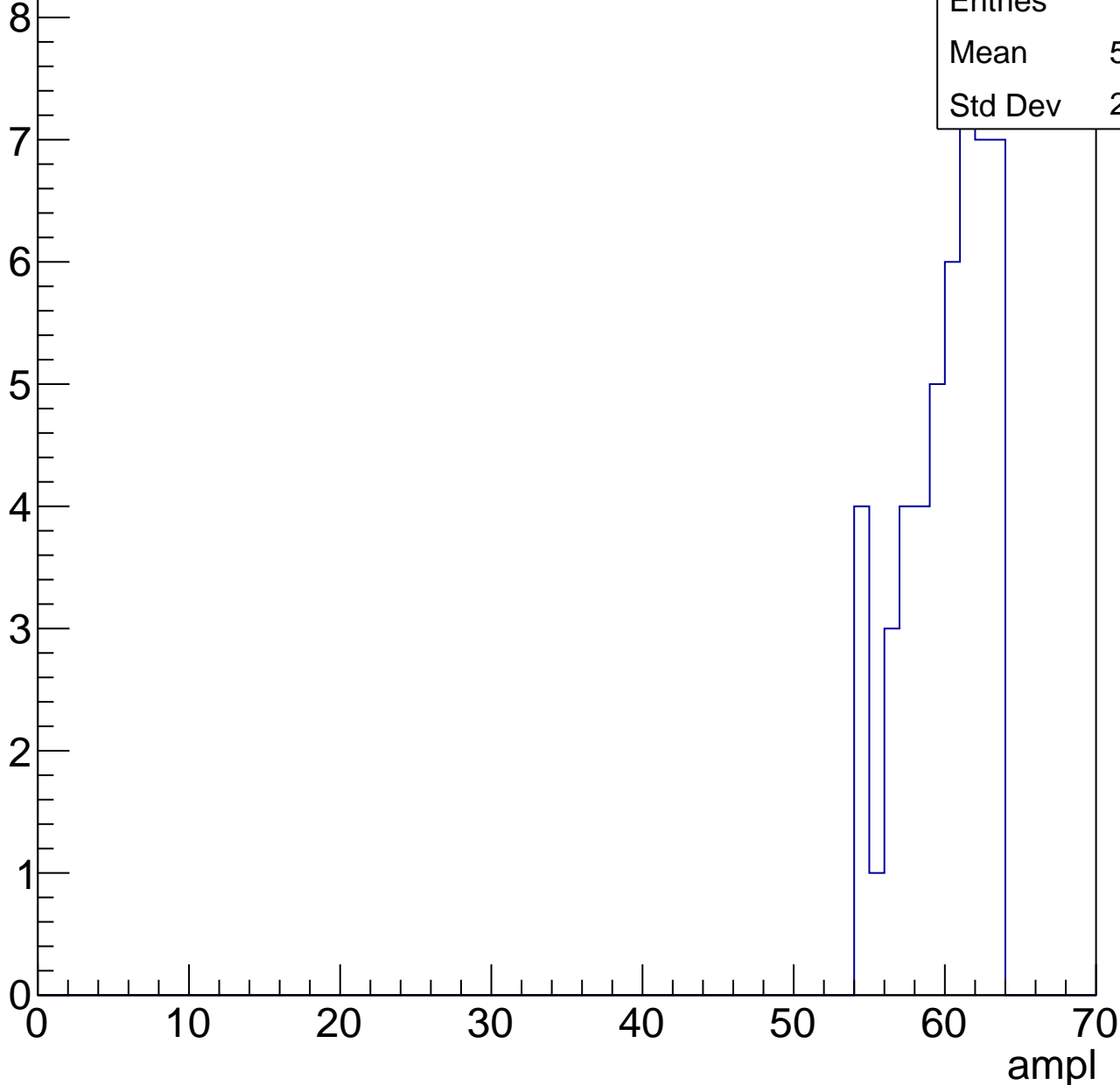


B1L102S, U9-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

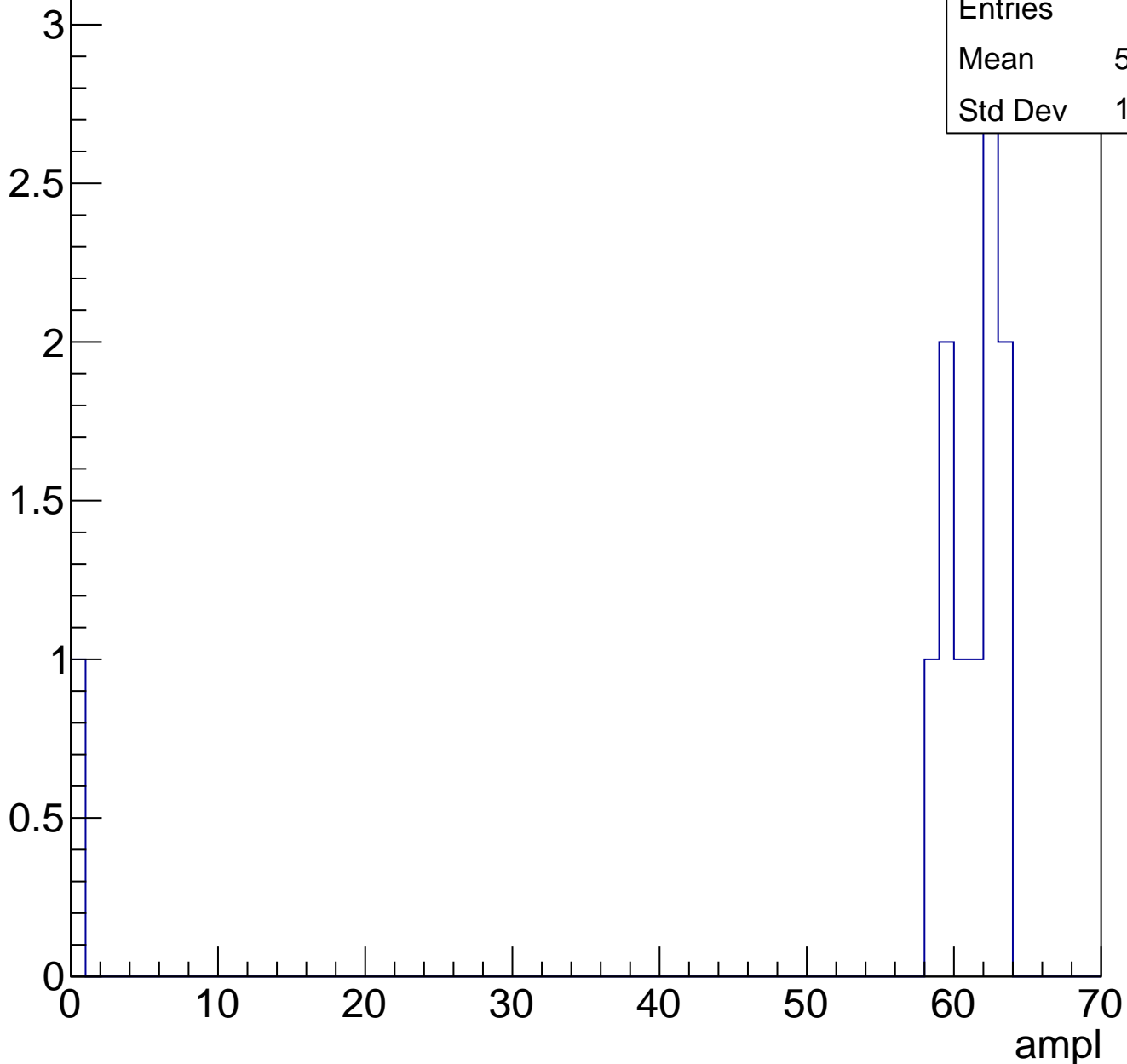
Entries	49
Mean	59.53
Std Dev	2.719



B1L102S, U9-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch67, adc0

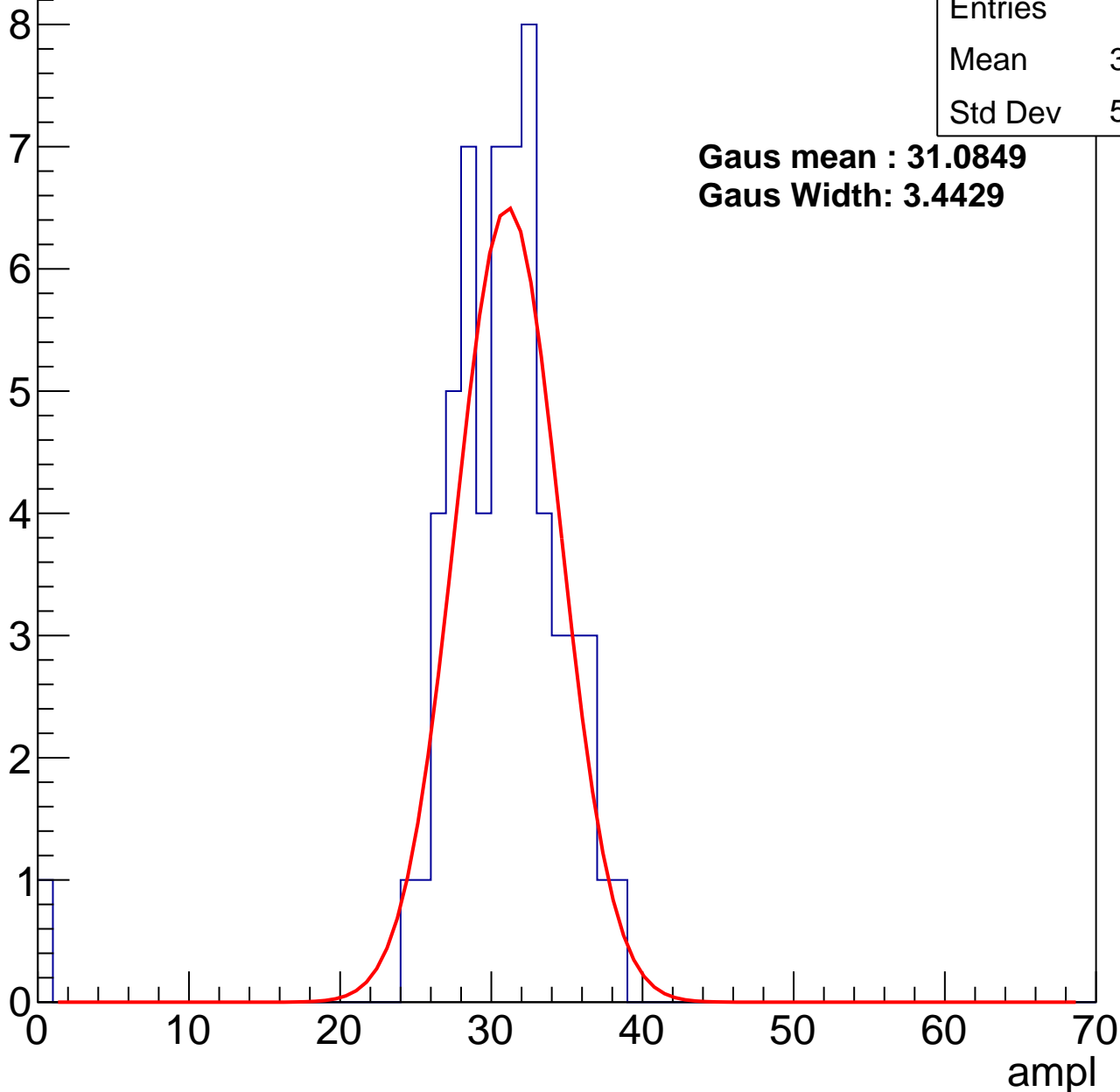
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	30.08
Std Dev	5.034

Gaus mean : 31.0849

Gaus Width: 3.4429



B1L102S, U9-ch67, adc1

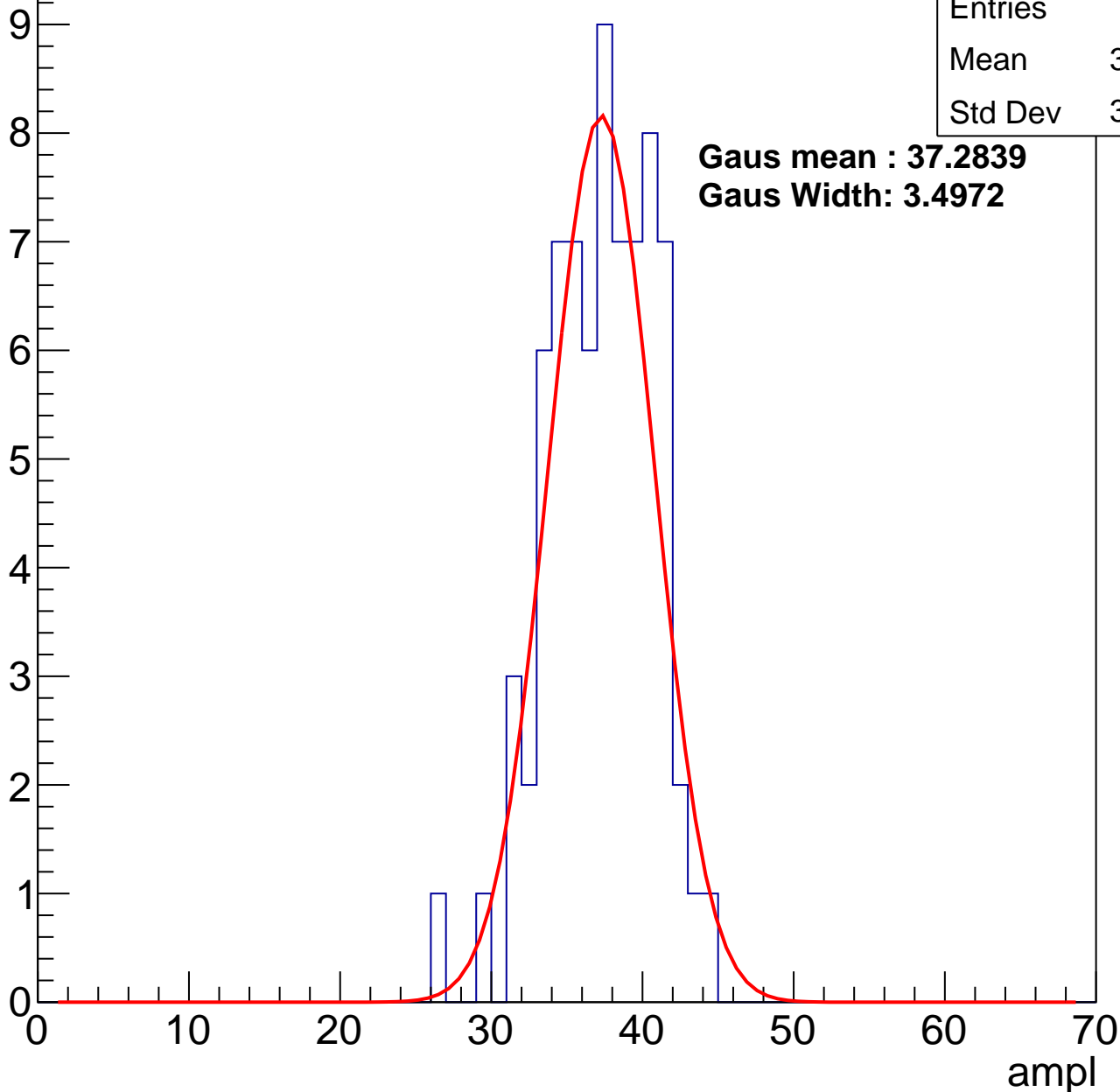
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.79
Std Dev	3.438

Gaus mean : 37.2839

Gaus Width: 3.4972



B1L102S, U9-ch67, adc2

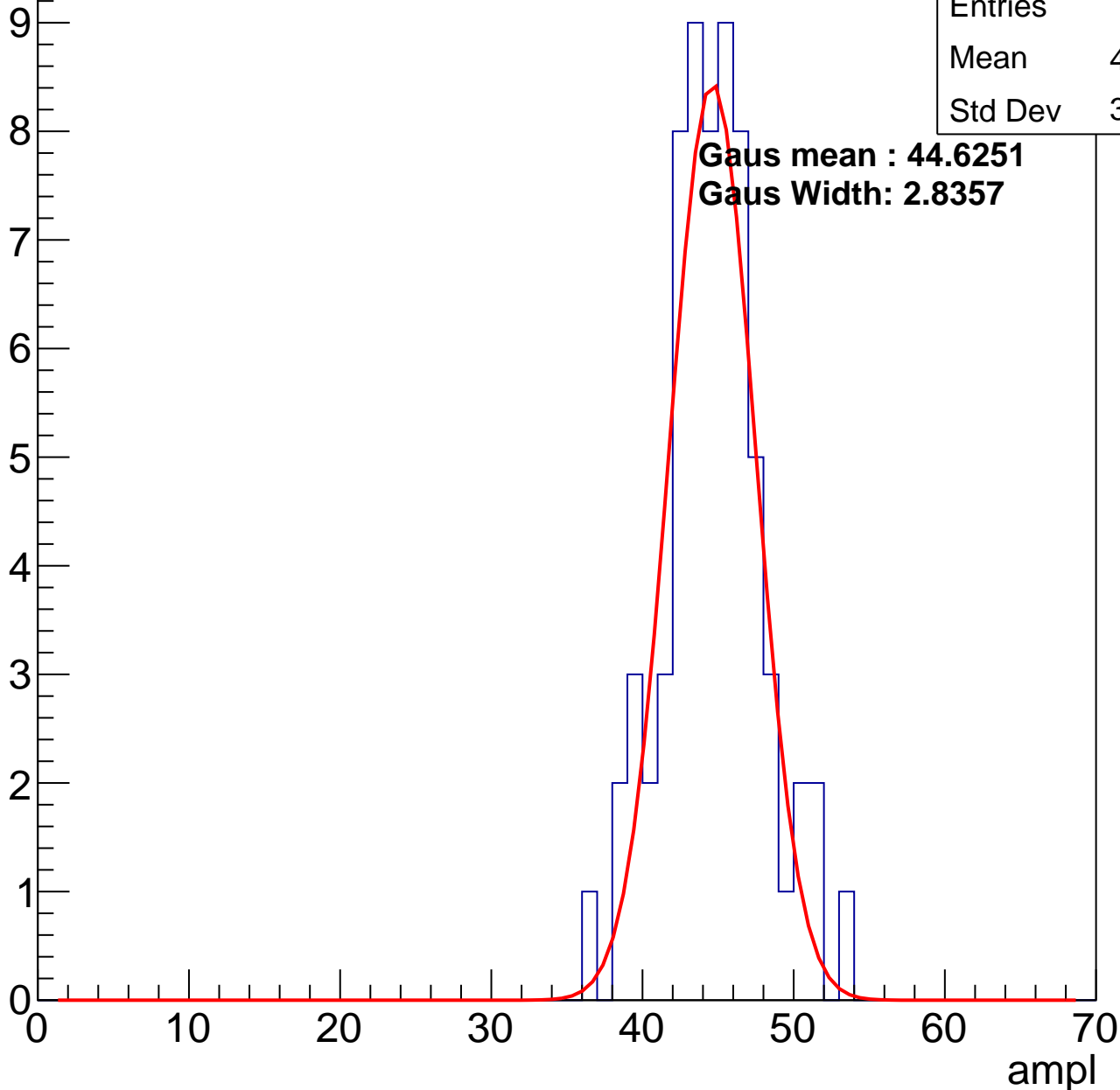
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.22
Std Dev	3.273

Gaus mean : 44.6251

Gaus Width: 2.8357

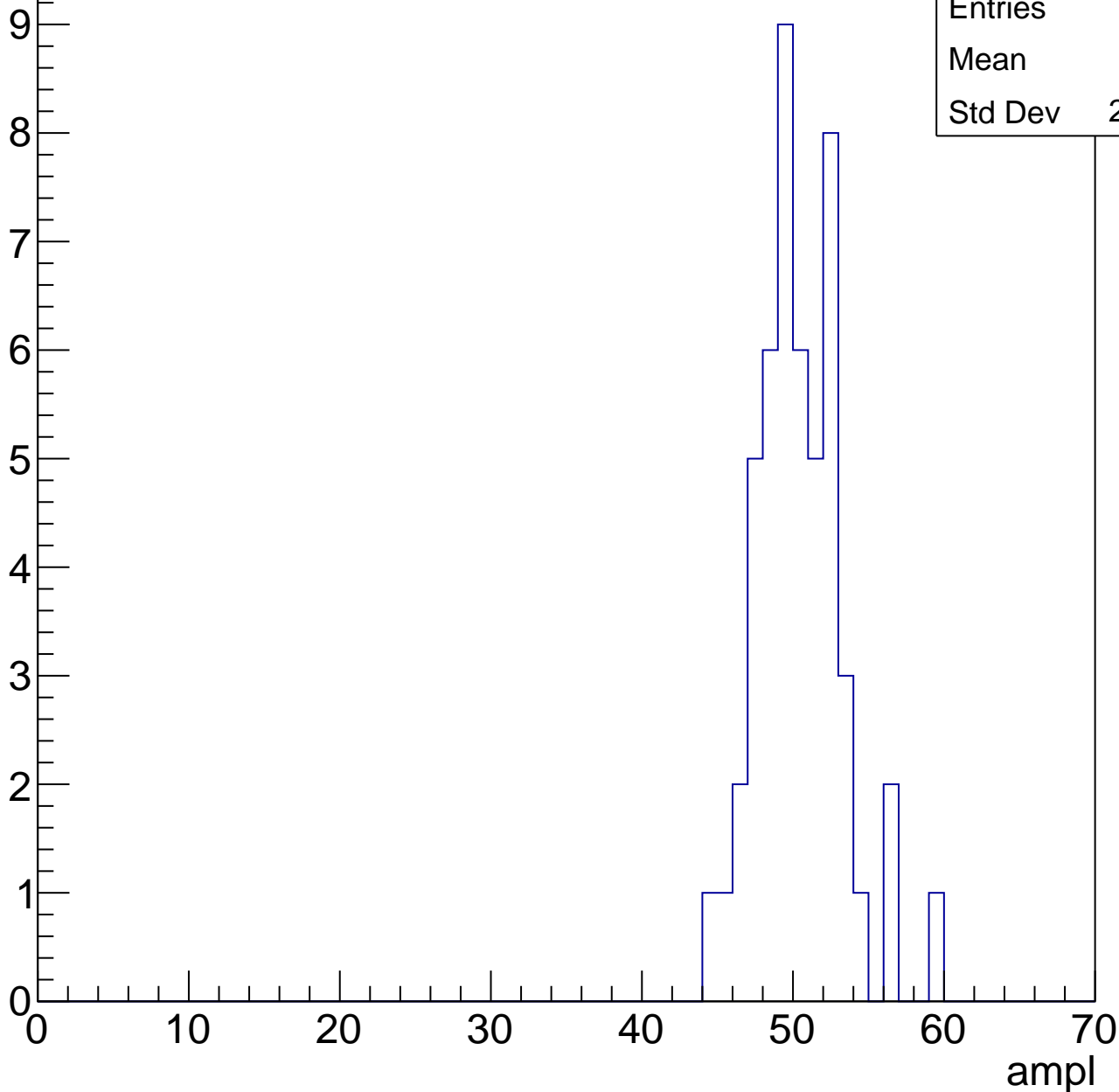


B1L102S, U9-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	50
Std Dev	2.843



B1L102S, U9-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

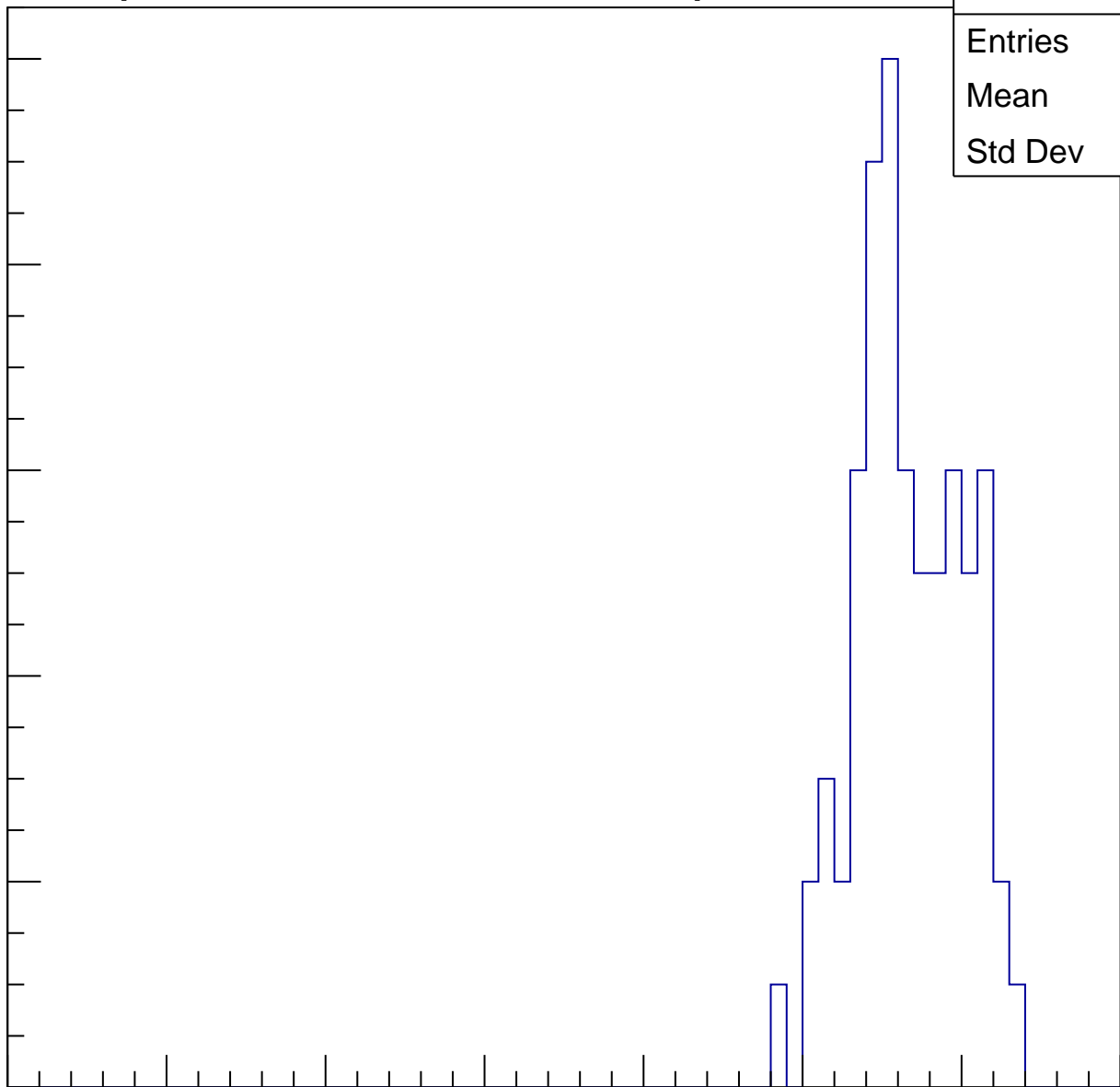
Entries	69
Mean	56.19
Std Dev	3.342

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

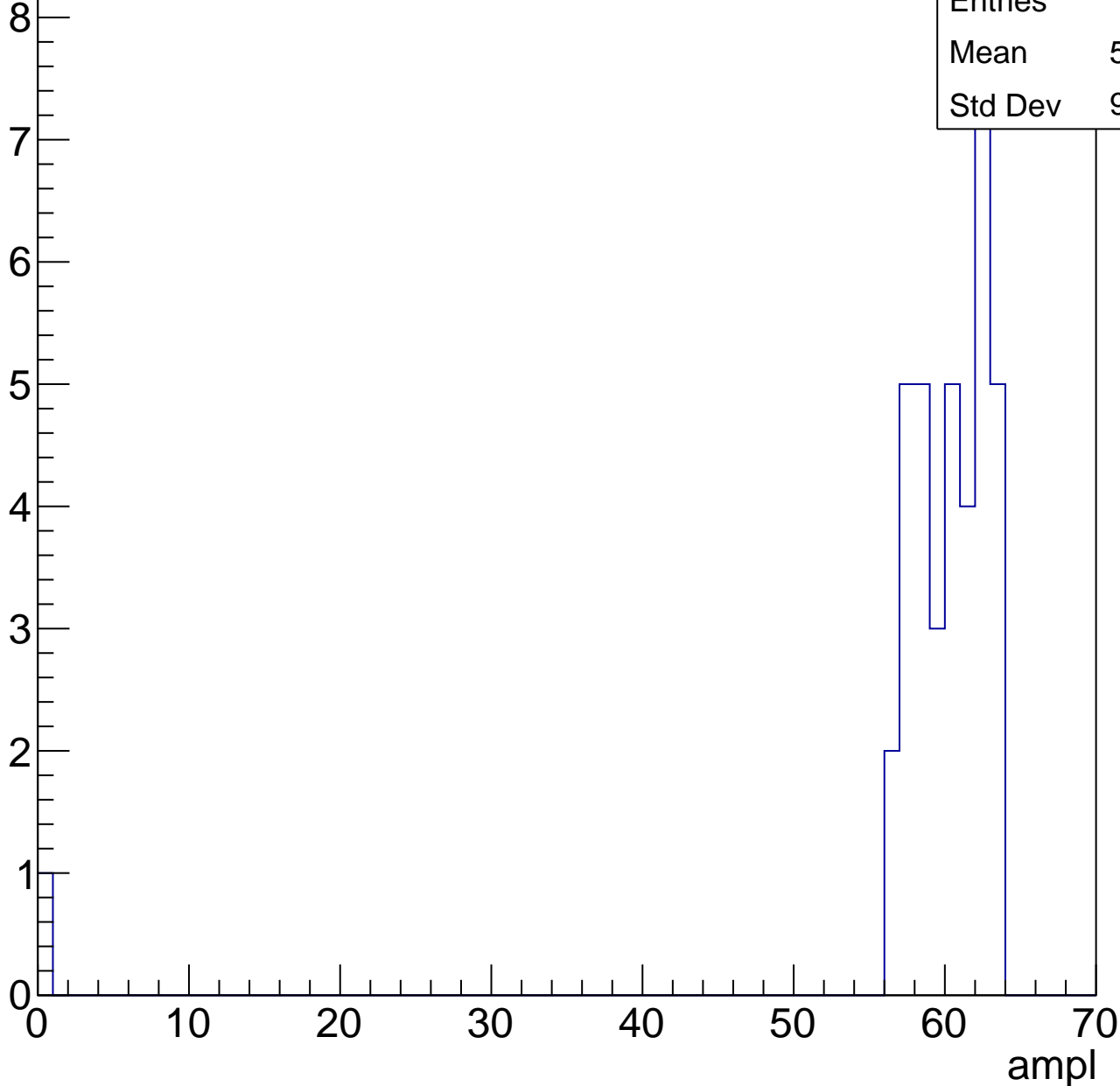


B1L102S, U9-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

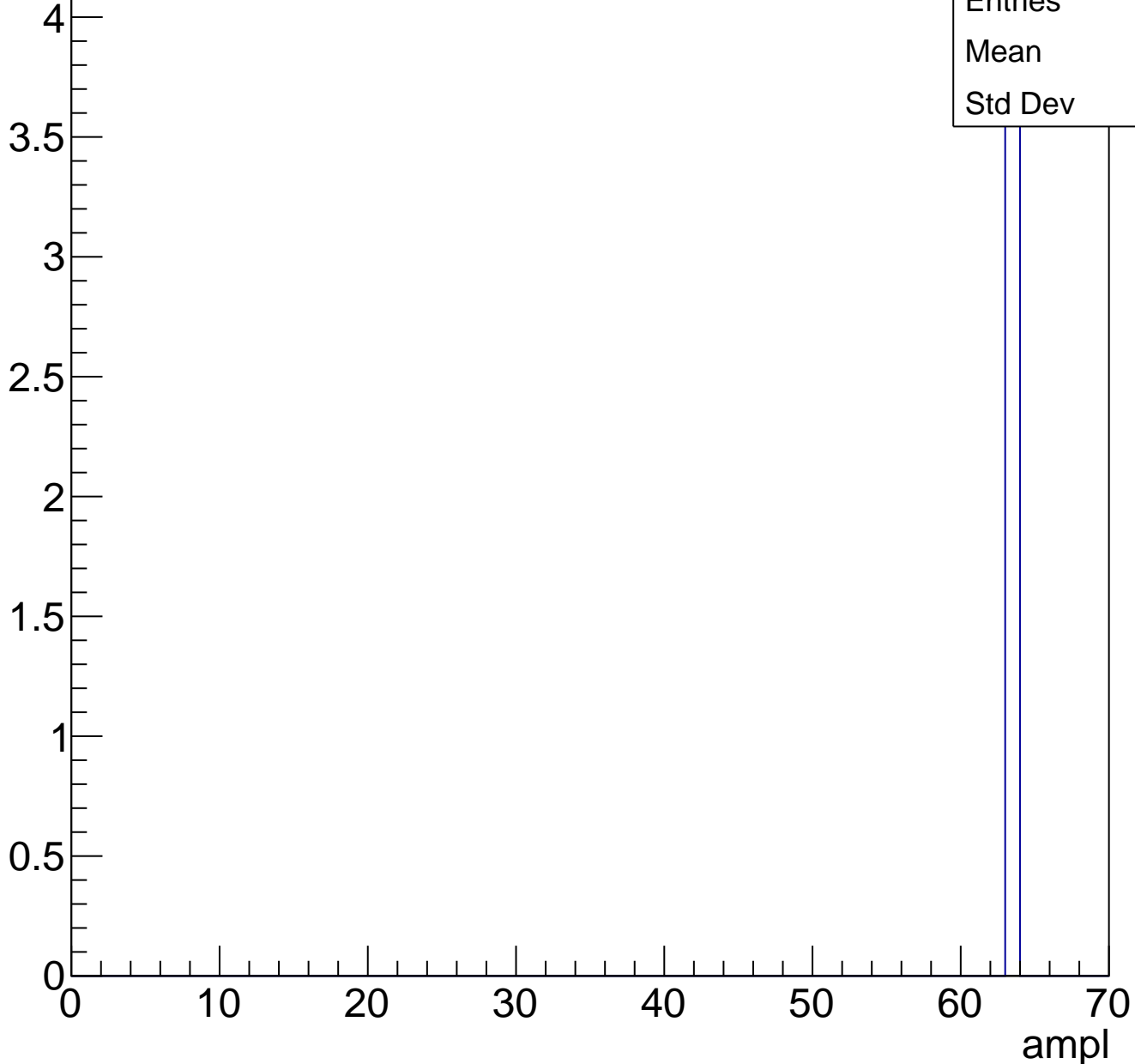
Entries	38
Mean	58.39
Std Dev	9.845



B1L102S, U9-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch68, adc0

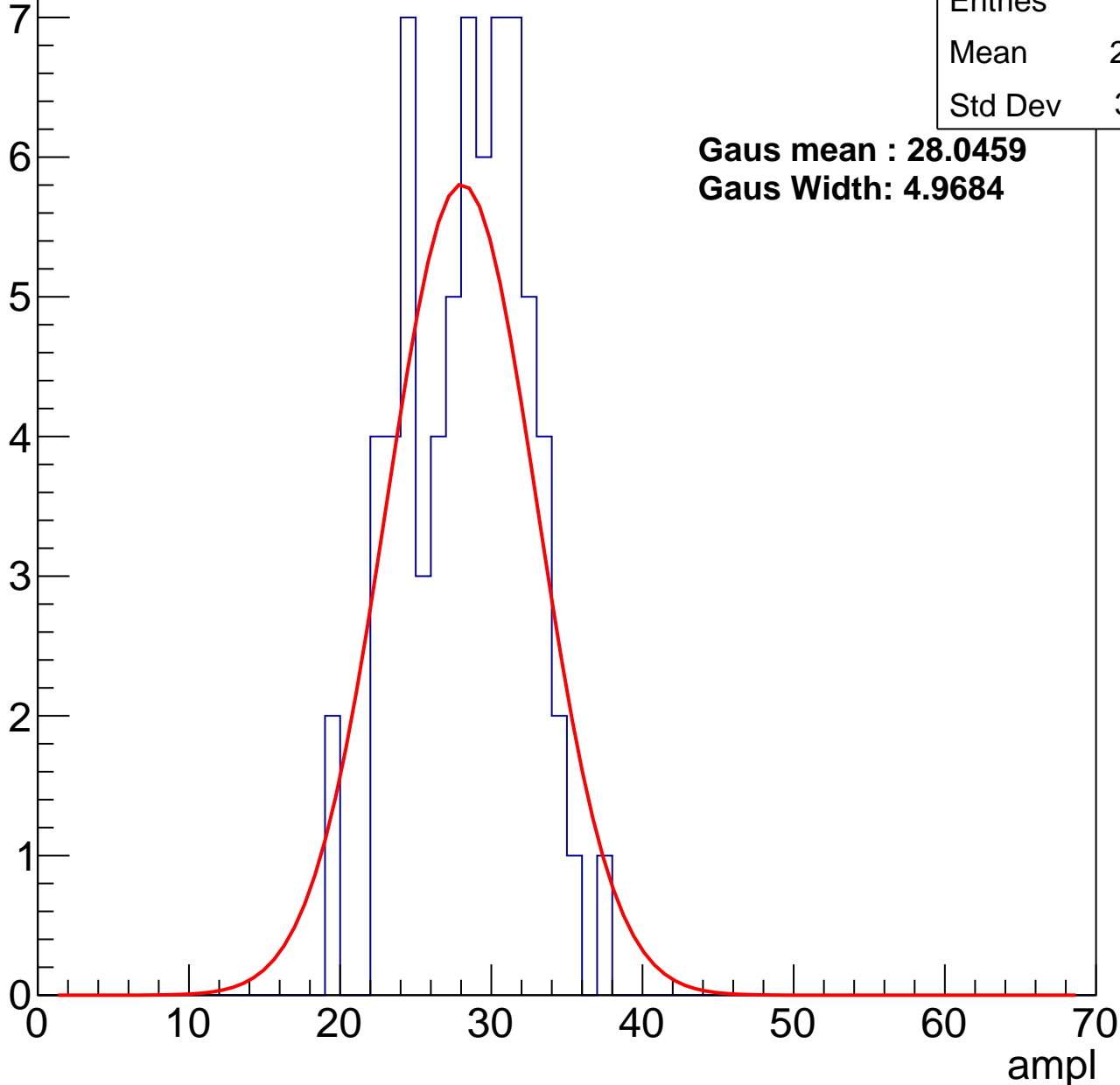
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.96
Std Dev	3.891

Gaus mean : 28.0459

Gaus Width: 4.9684



B1L102S, U9-ch68, adc1

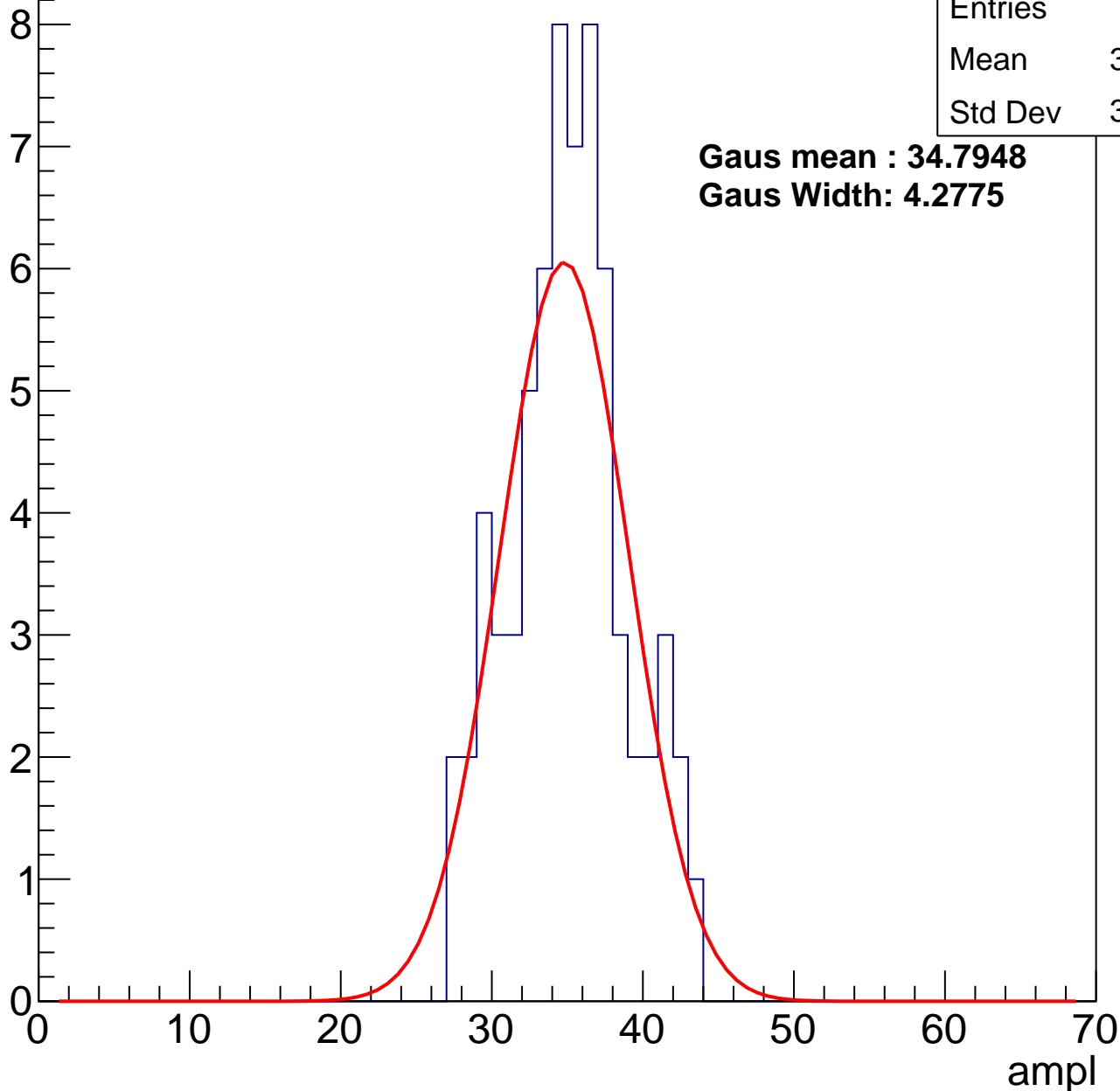
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.57
Std Dev	3.802

Gaus mean : 34.7948

Gaus Width: 4.2775



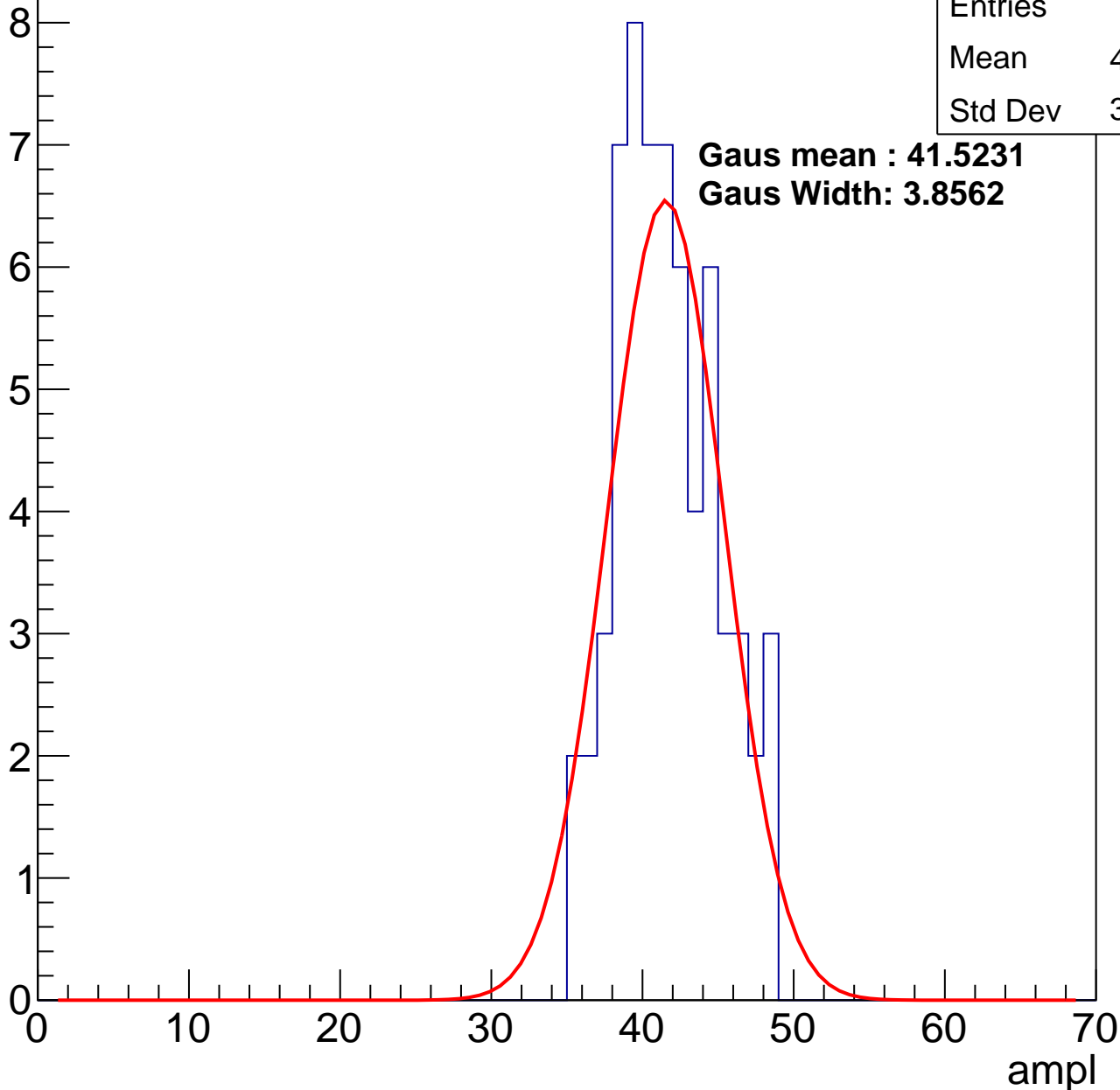
B1L102S, U9-ch68, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.22
Std Dev	3.302

Gaus mean : 41.5231
Gaus Width: 3.8562

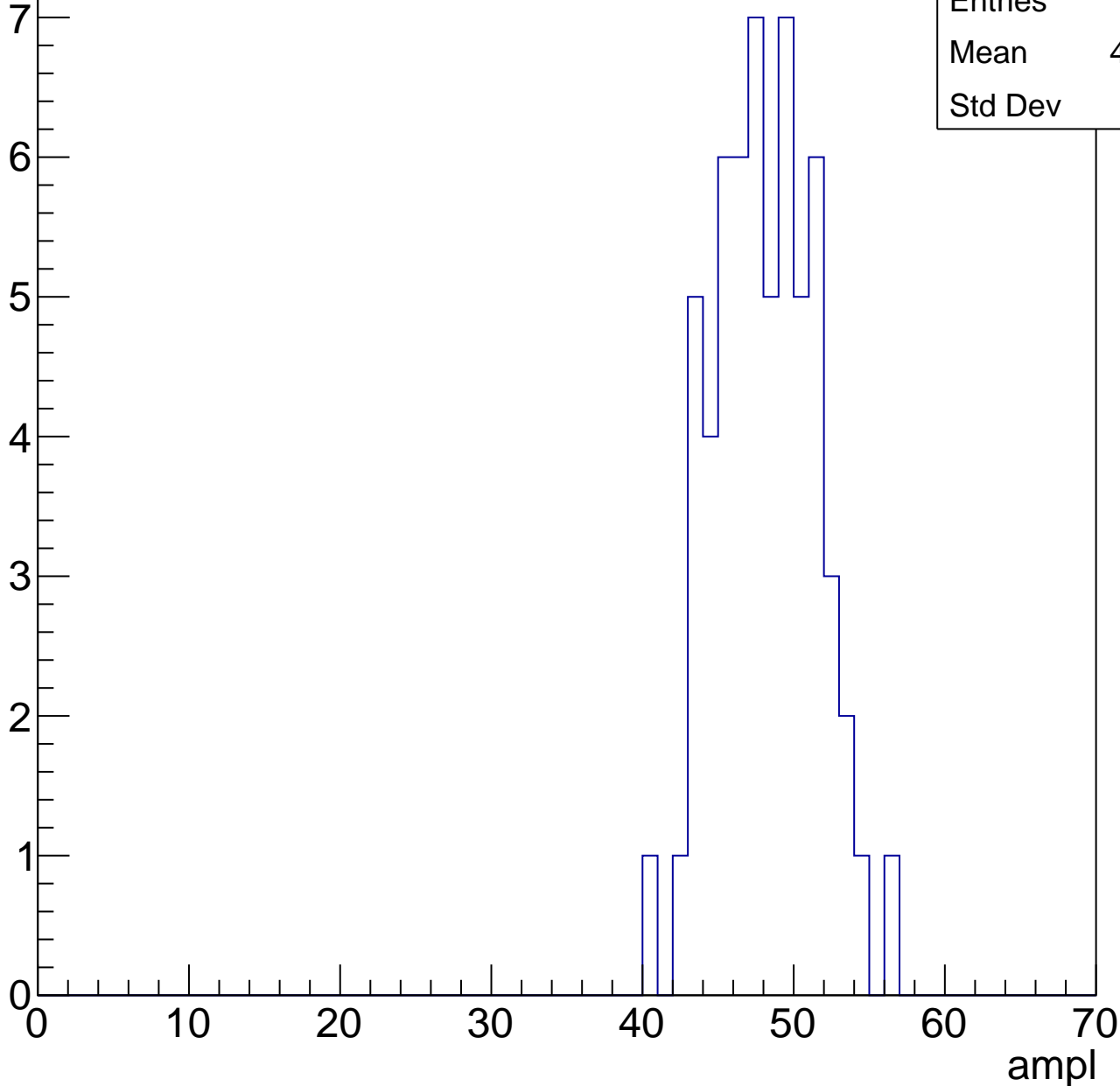


B1L102S, U9-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.65
Std Dev	3.28

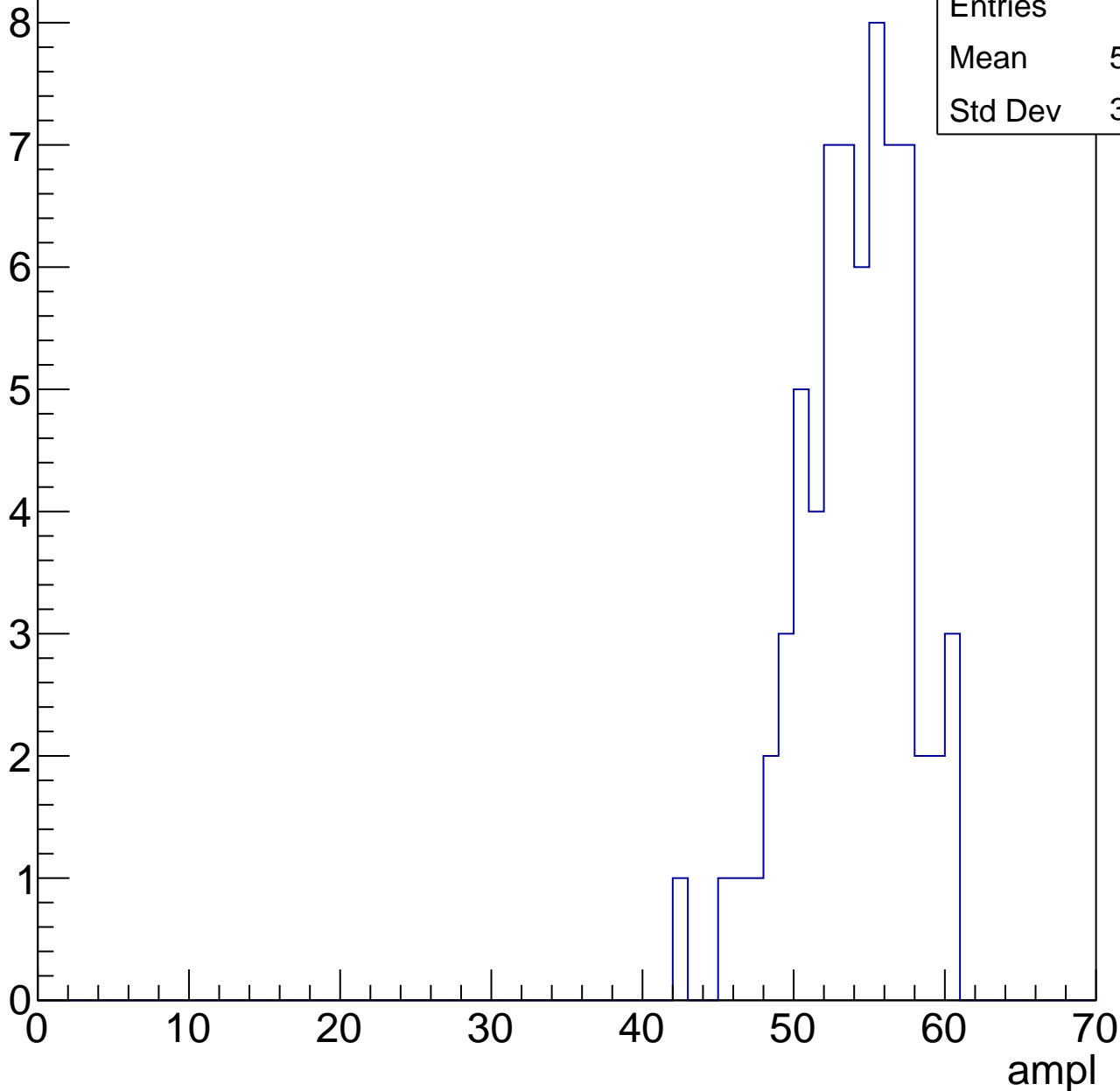


B1L102S, U9-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	53.45
Std Dev	3.674



B1L102S, U9-ch68, adc5

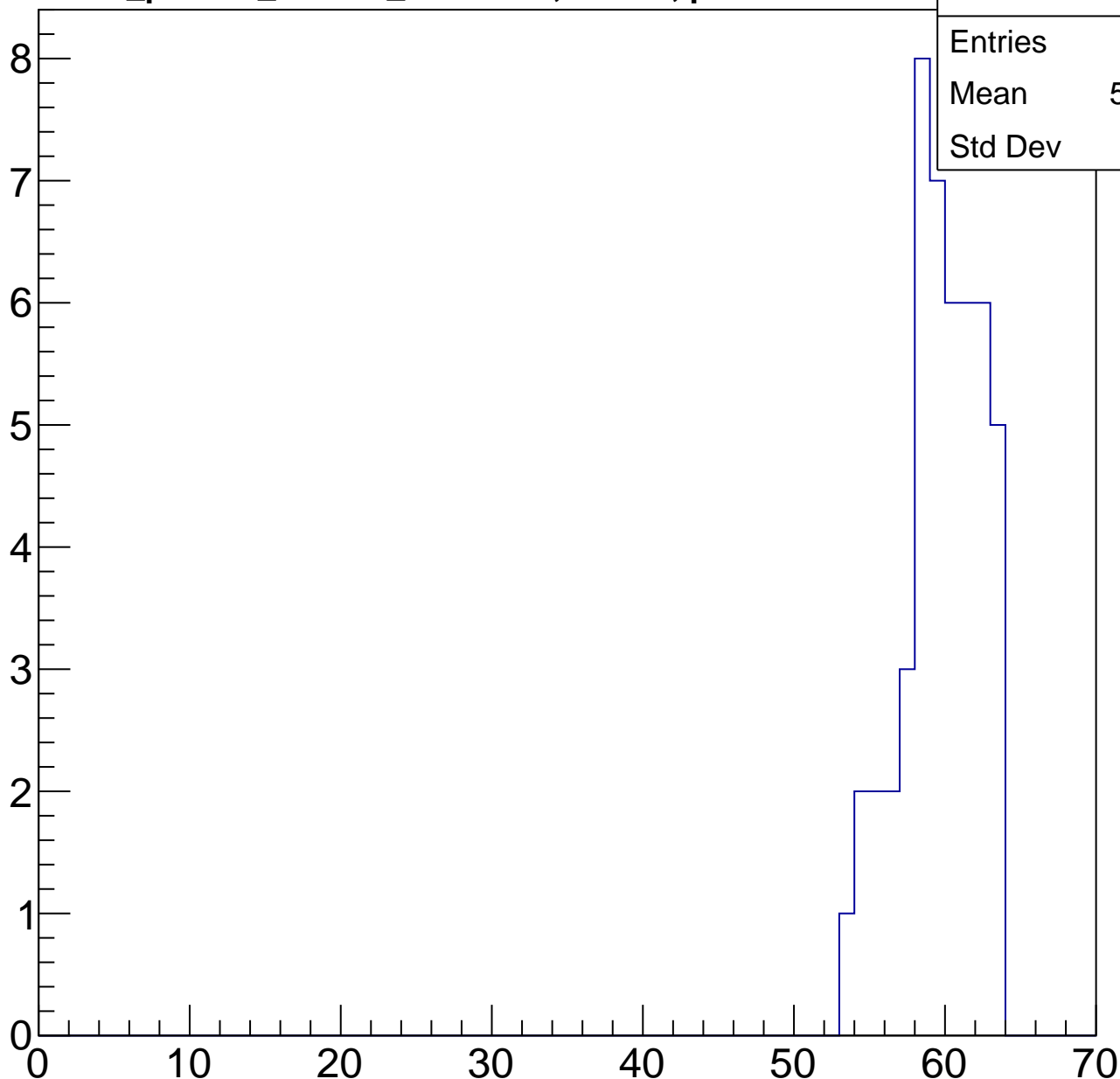
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.25
Std Dev	2.57

ampl

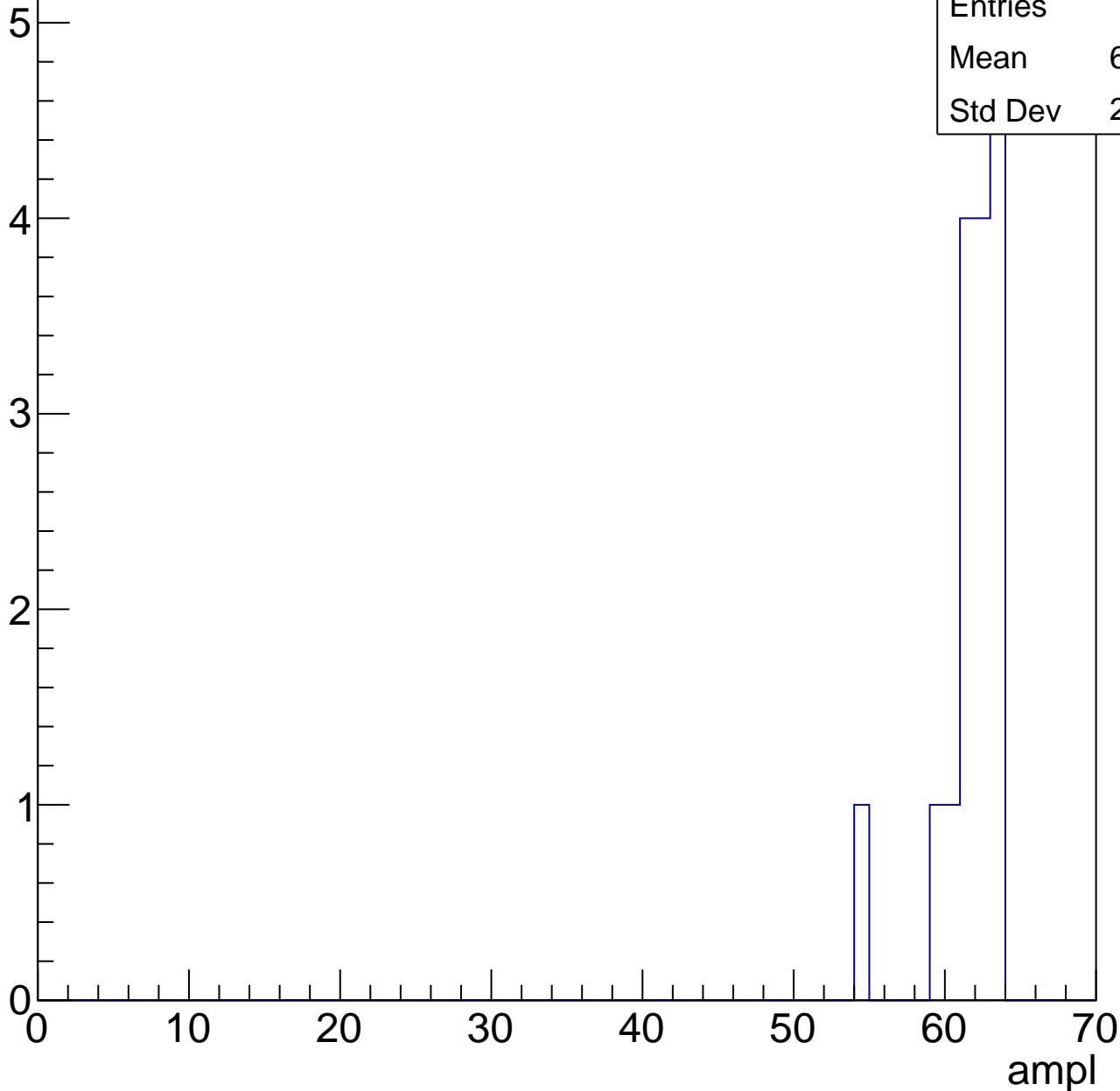


B1L102S, U9-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

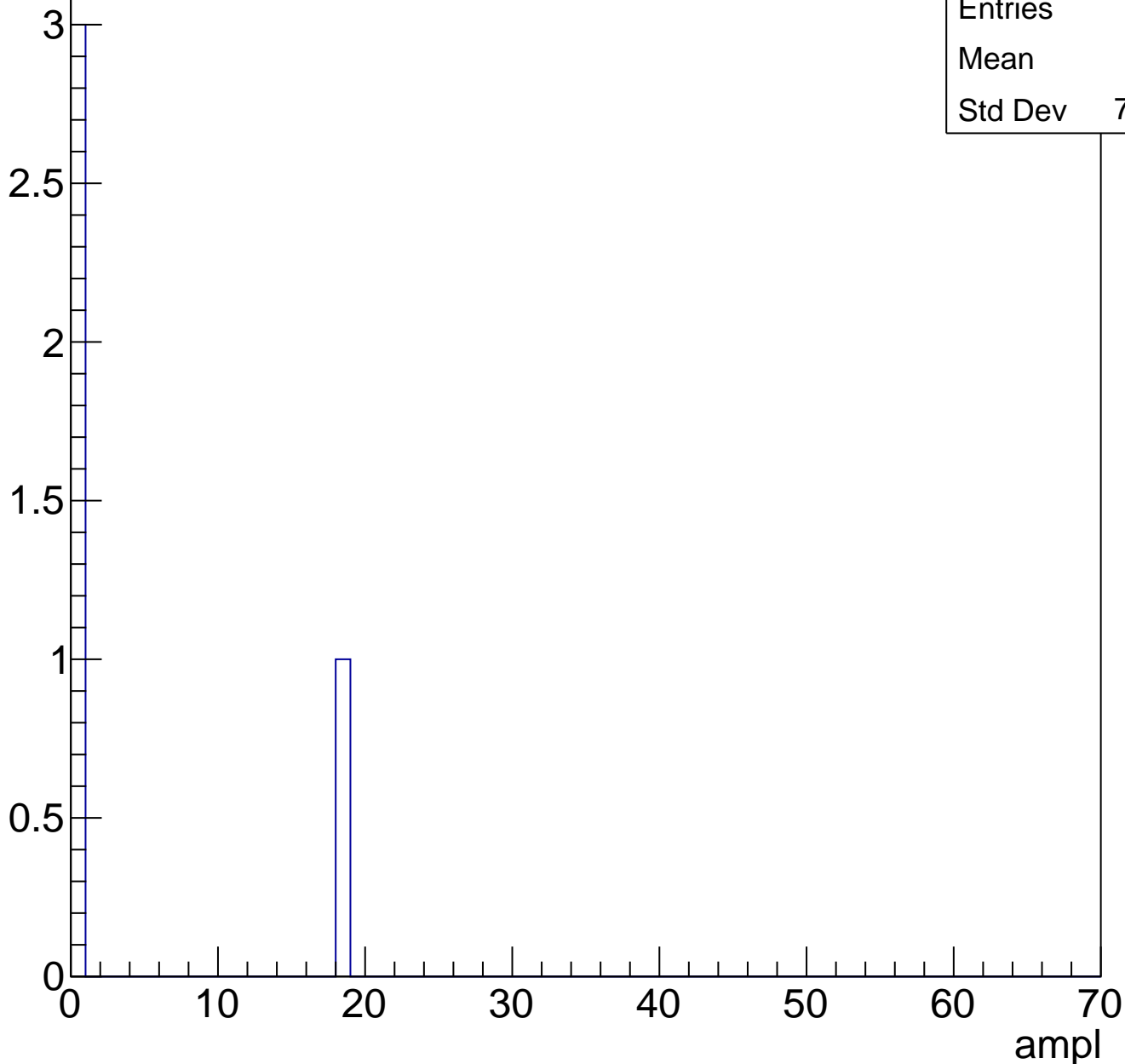
Entries	16
Mean	61.25
Std Dev	2.194



B1L102S, U9-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch69, adc0

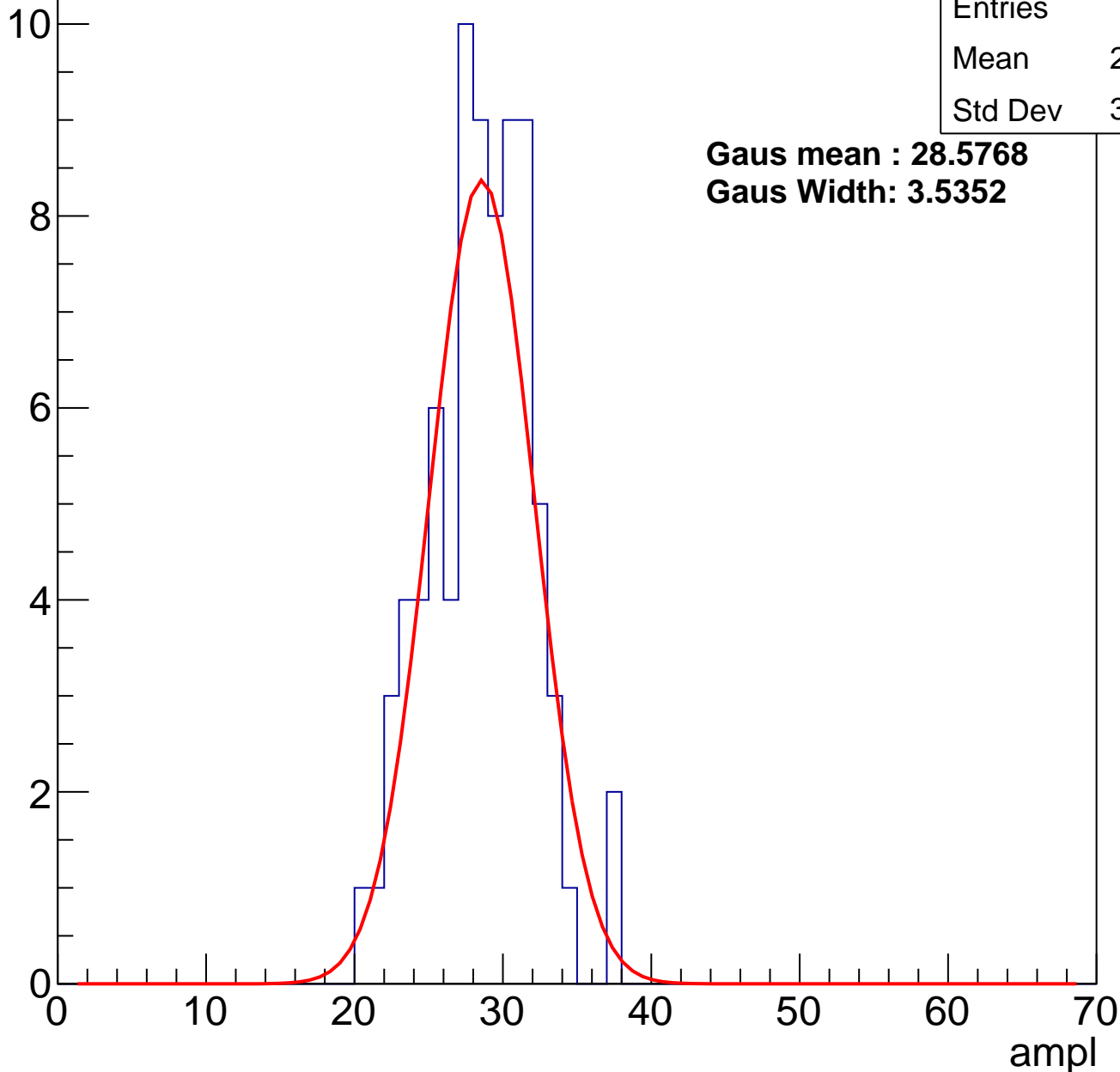
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	28.09
Std Dev	3.454

Gaus mean : 28.5768

Gaus Width: 3.5352

Entry



B1L102S, U9-ch69, adc1

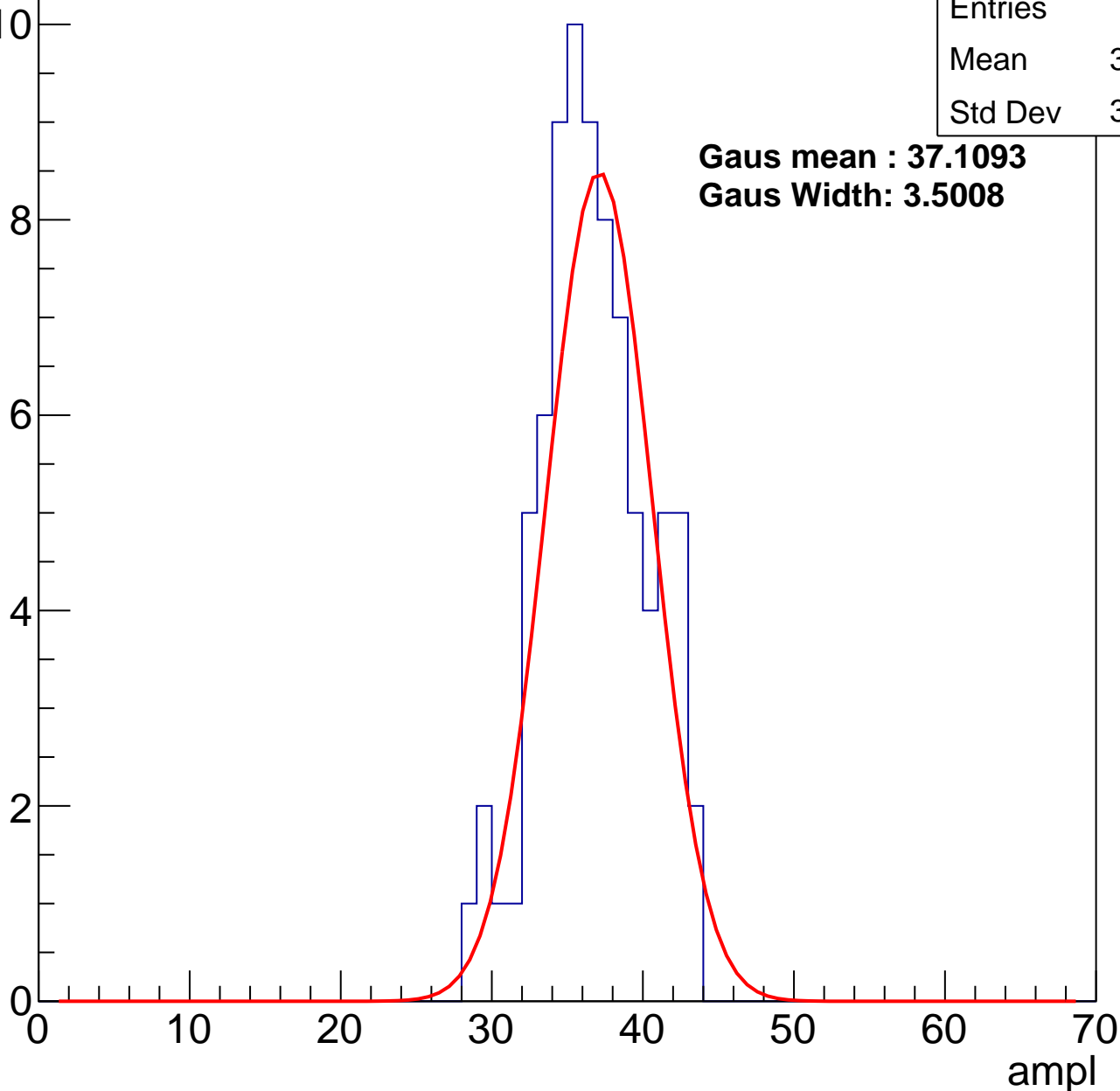
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.29
Std Dev	3.436

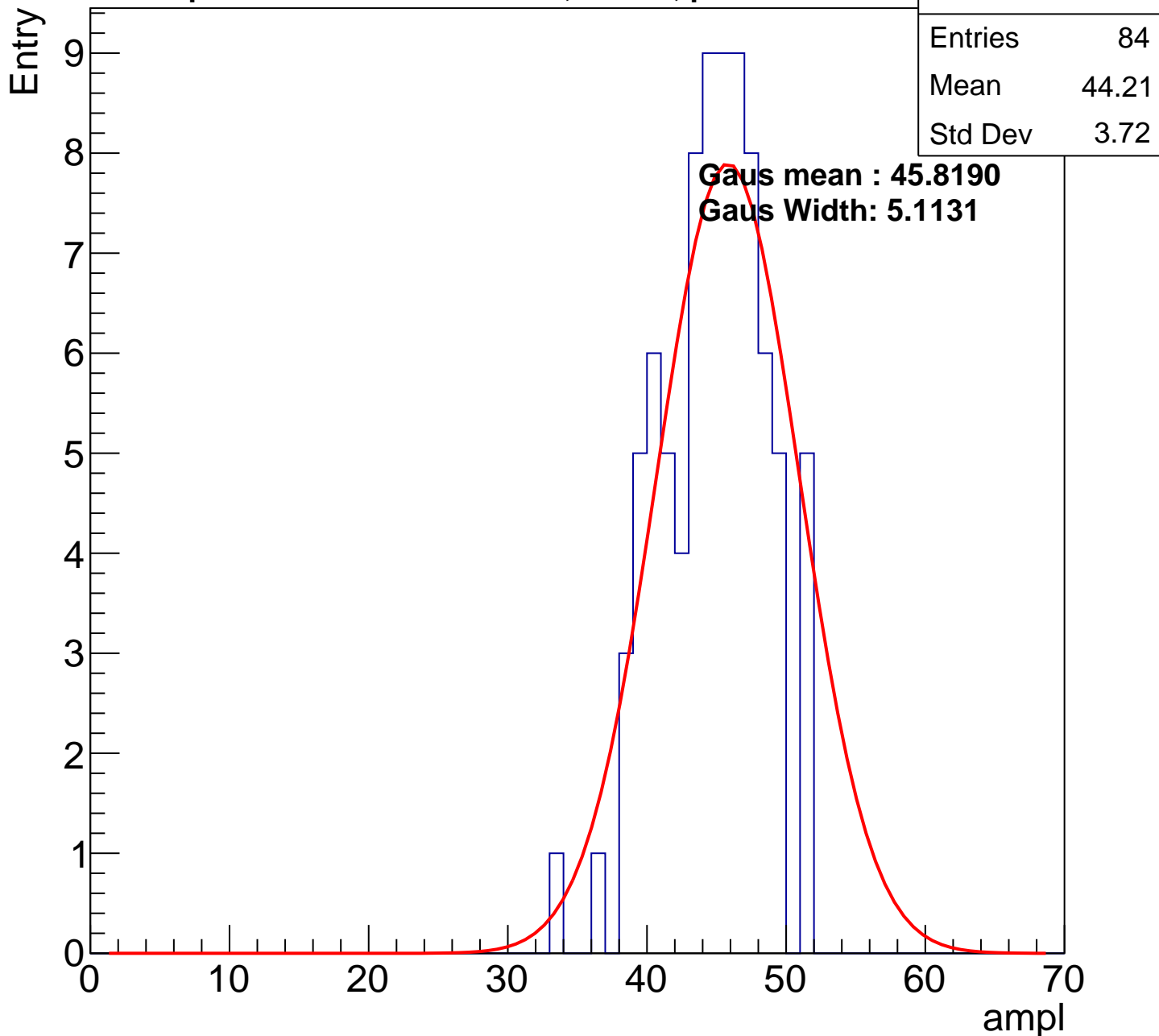
Gaus mean : 37.1093

Gaus Width: 3.5008



B1L102S, U9-ch69, adc2

calib_packv5_042523_0143.root, FC#11, port A2

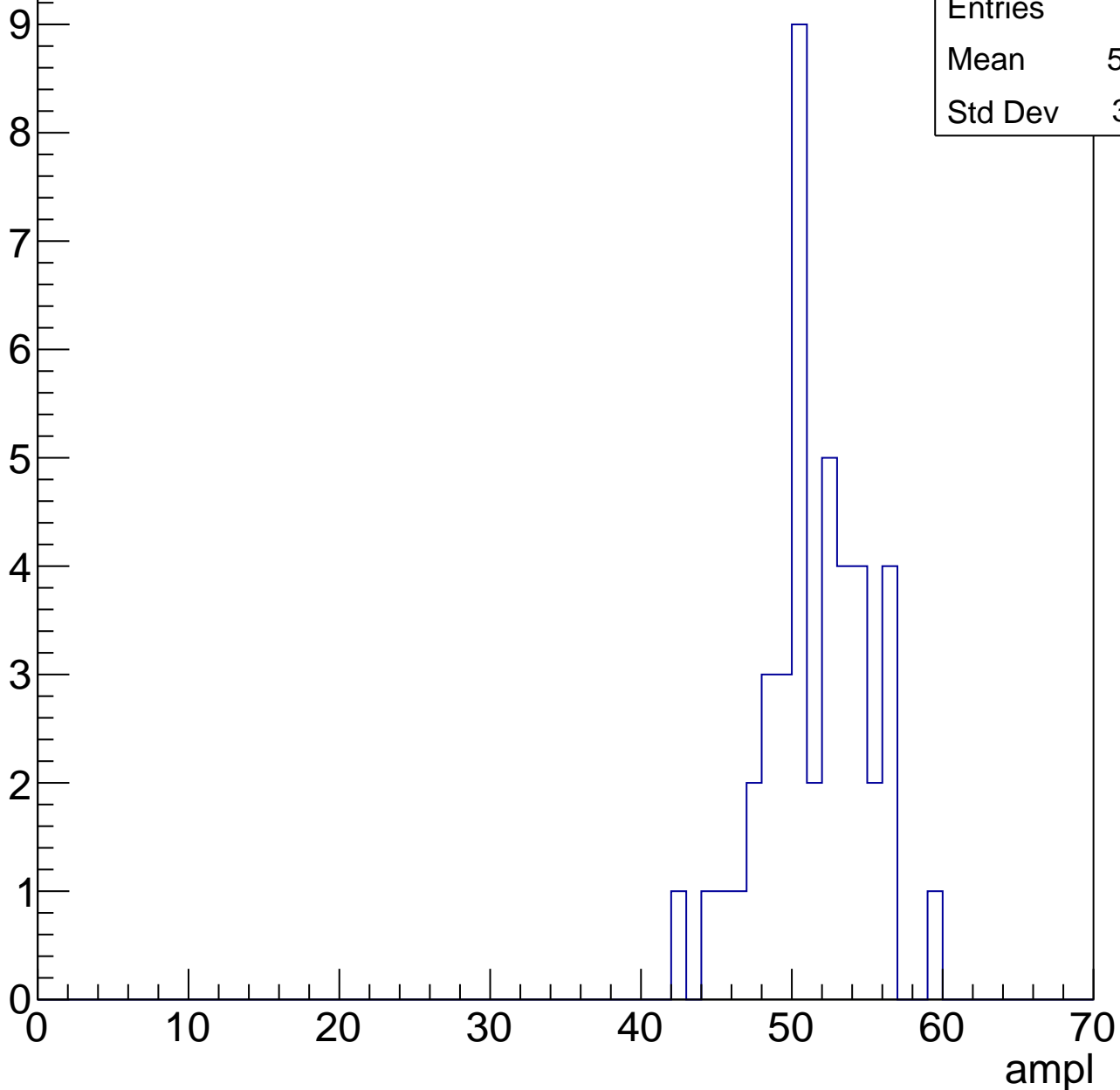


B1L102S, U9-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	51.05
Std Dev	3.491

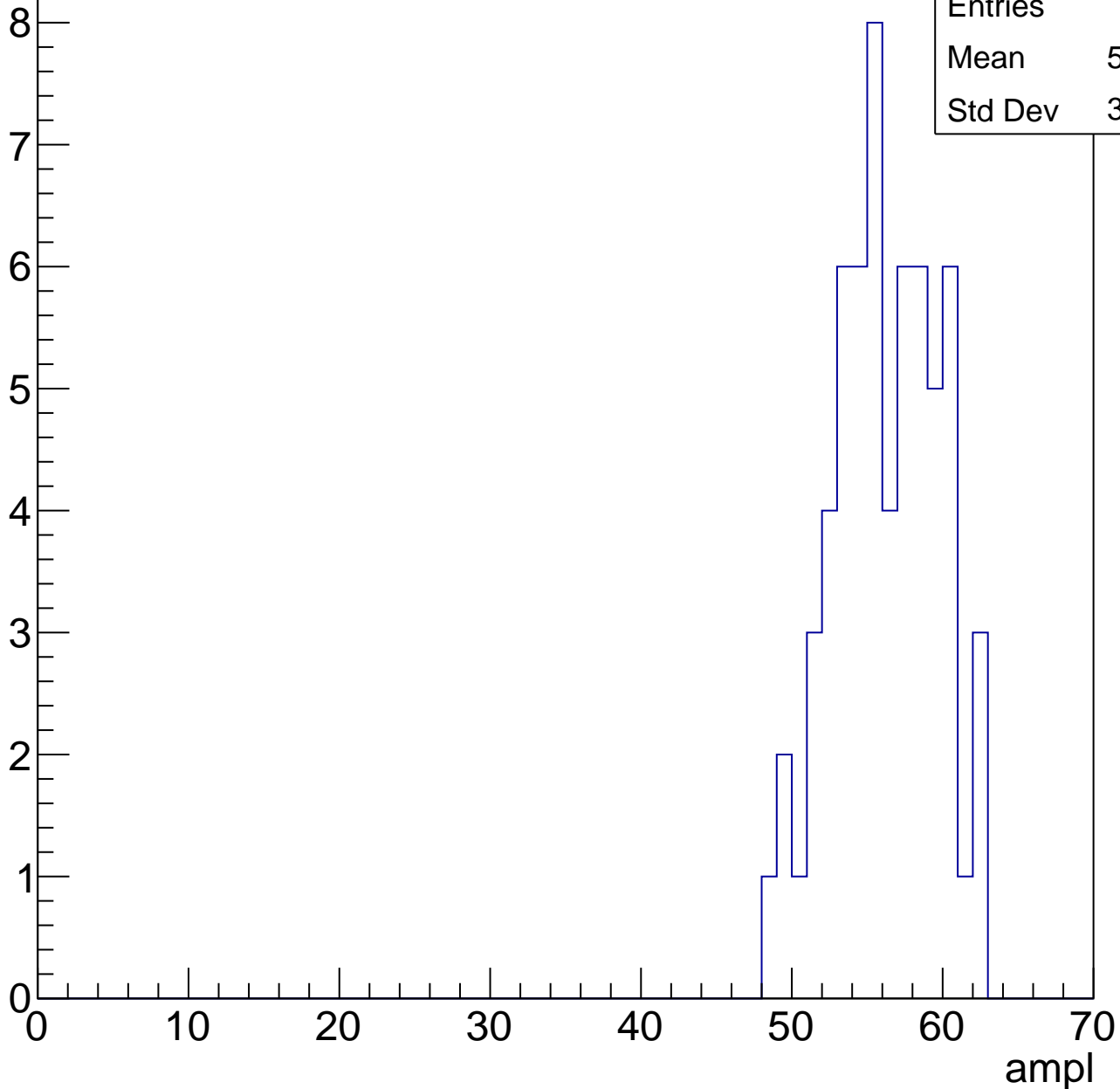


B1L102S, U9-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.73
Std Dev	3.413

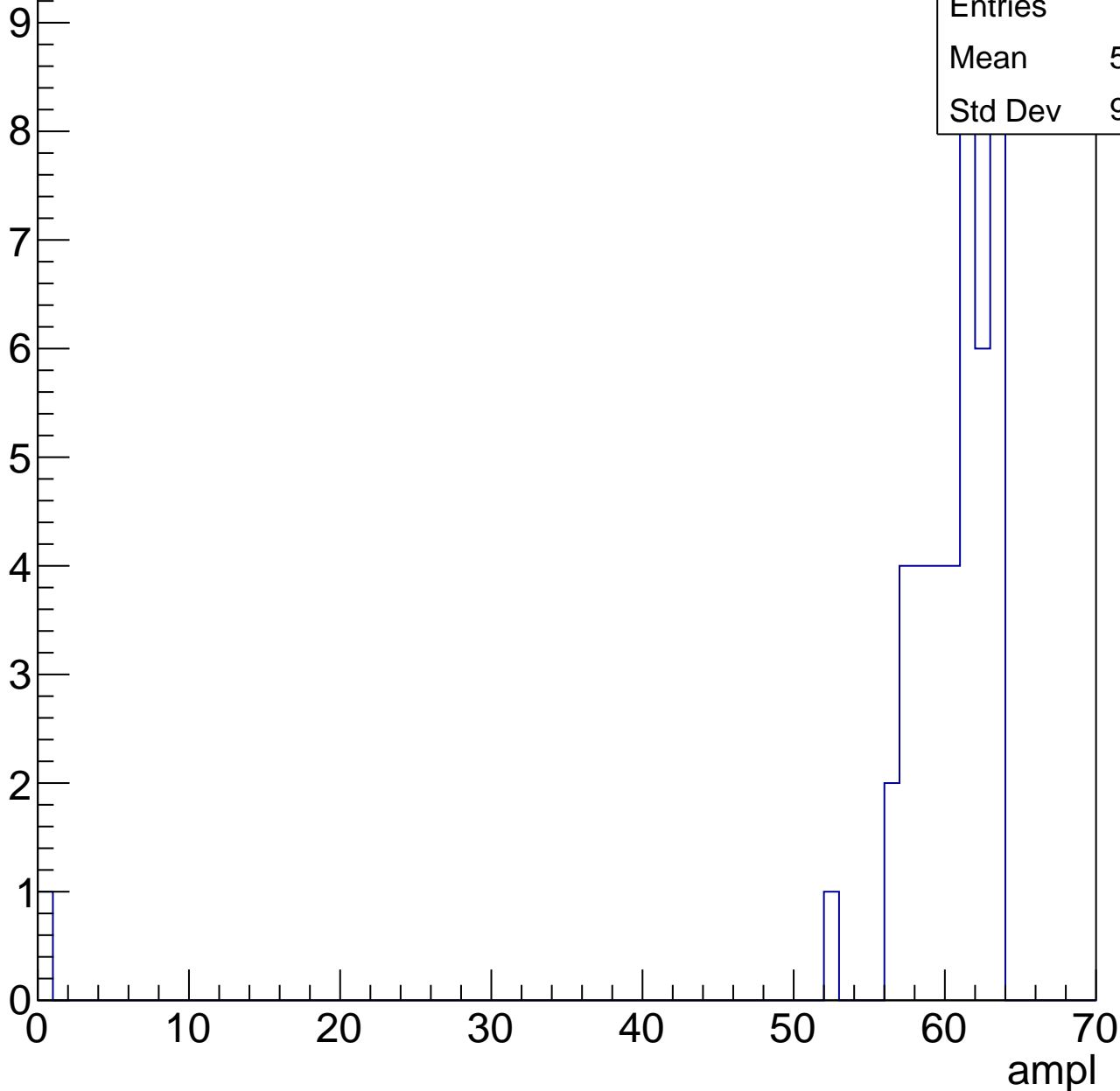


B1L102S, U9-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

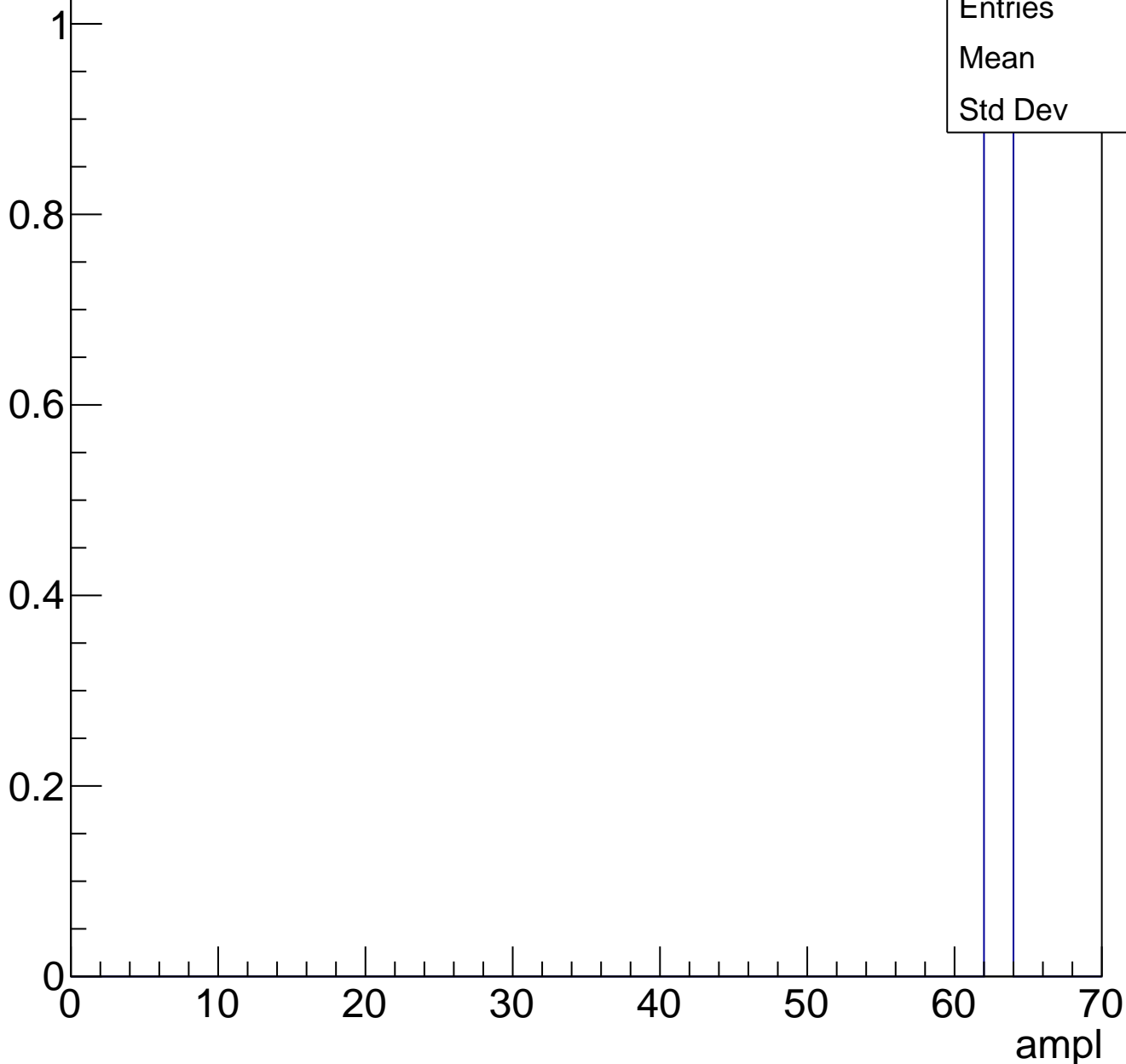
Entries	44
Mean	58.82
Std Dev	9.299



B1L102S, U9-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch70, adc0

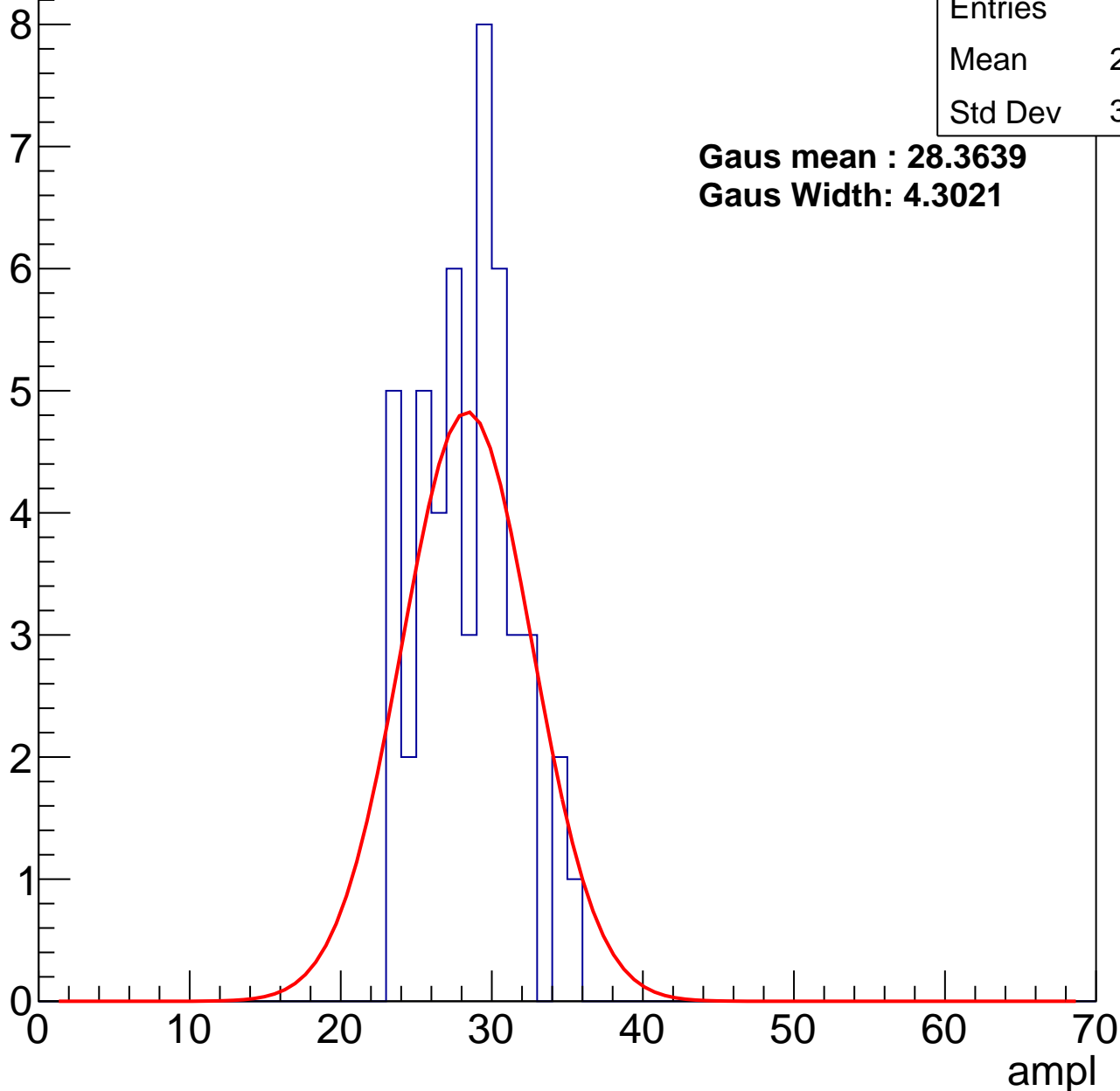
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	27.96
Std Dev	3.068

Gaus mean : 28.3639

Gaus Width: 4.3021



B1L102S, U9-ch70, adc1

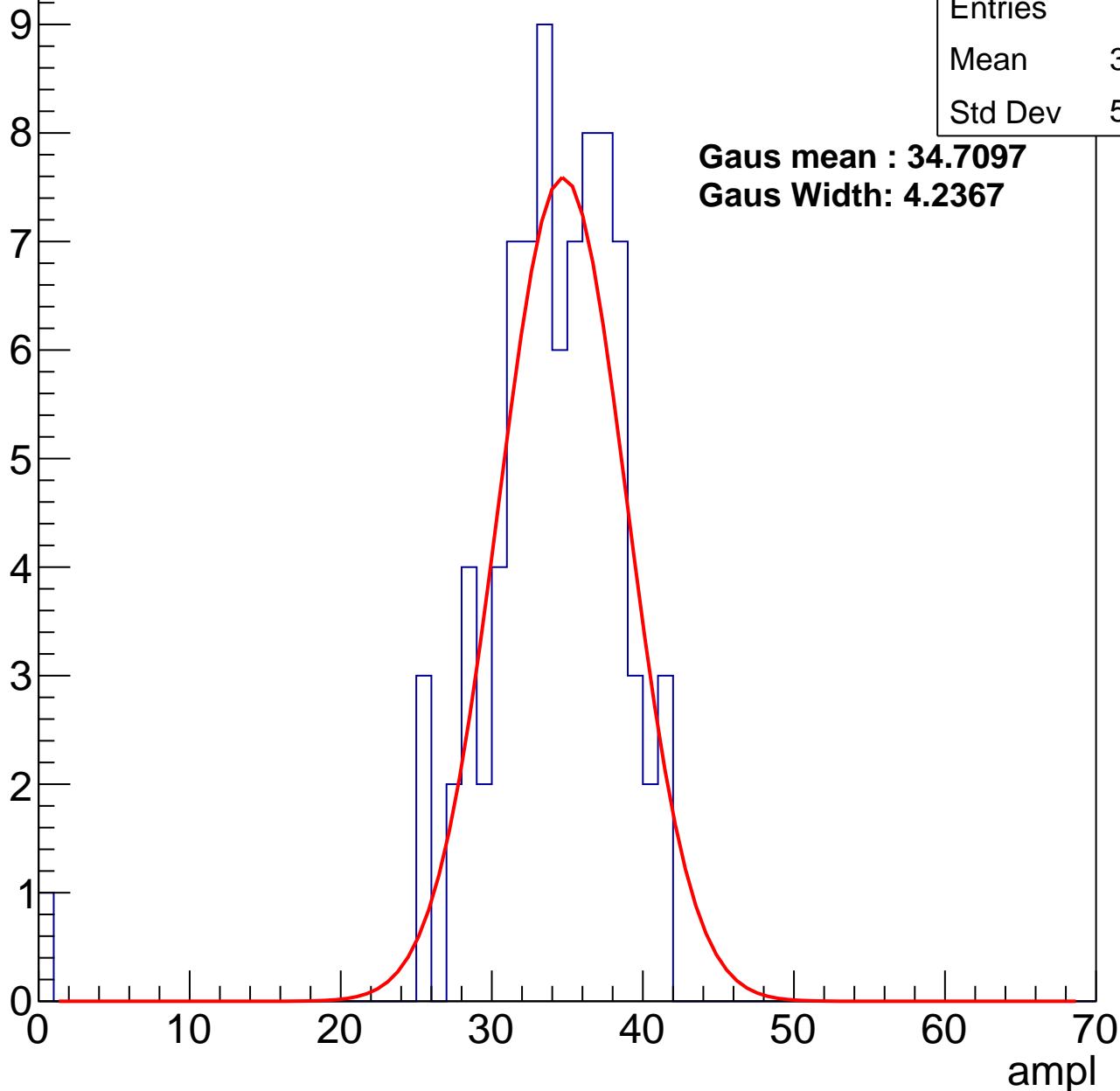
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	33.45
Std Dev	5.317

Gaus mean : 34.7097

Gaus Width: 4.2367



B1L102S, U9-ch70, adc2

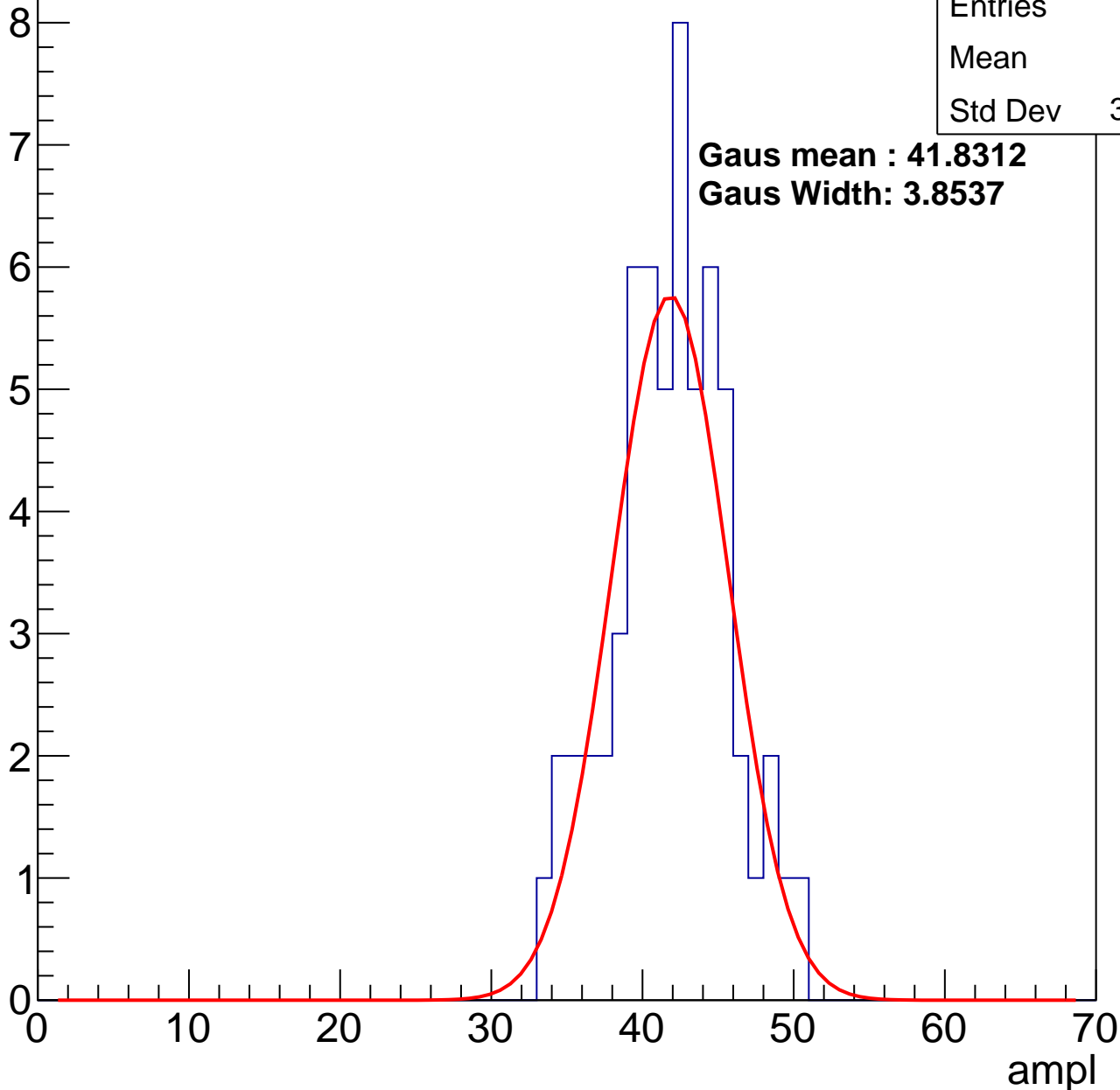
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.4
Std Dev	3.774

Gaus mean : 41.8312

Gaus Width: 3.8537

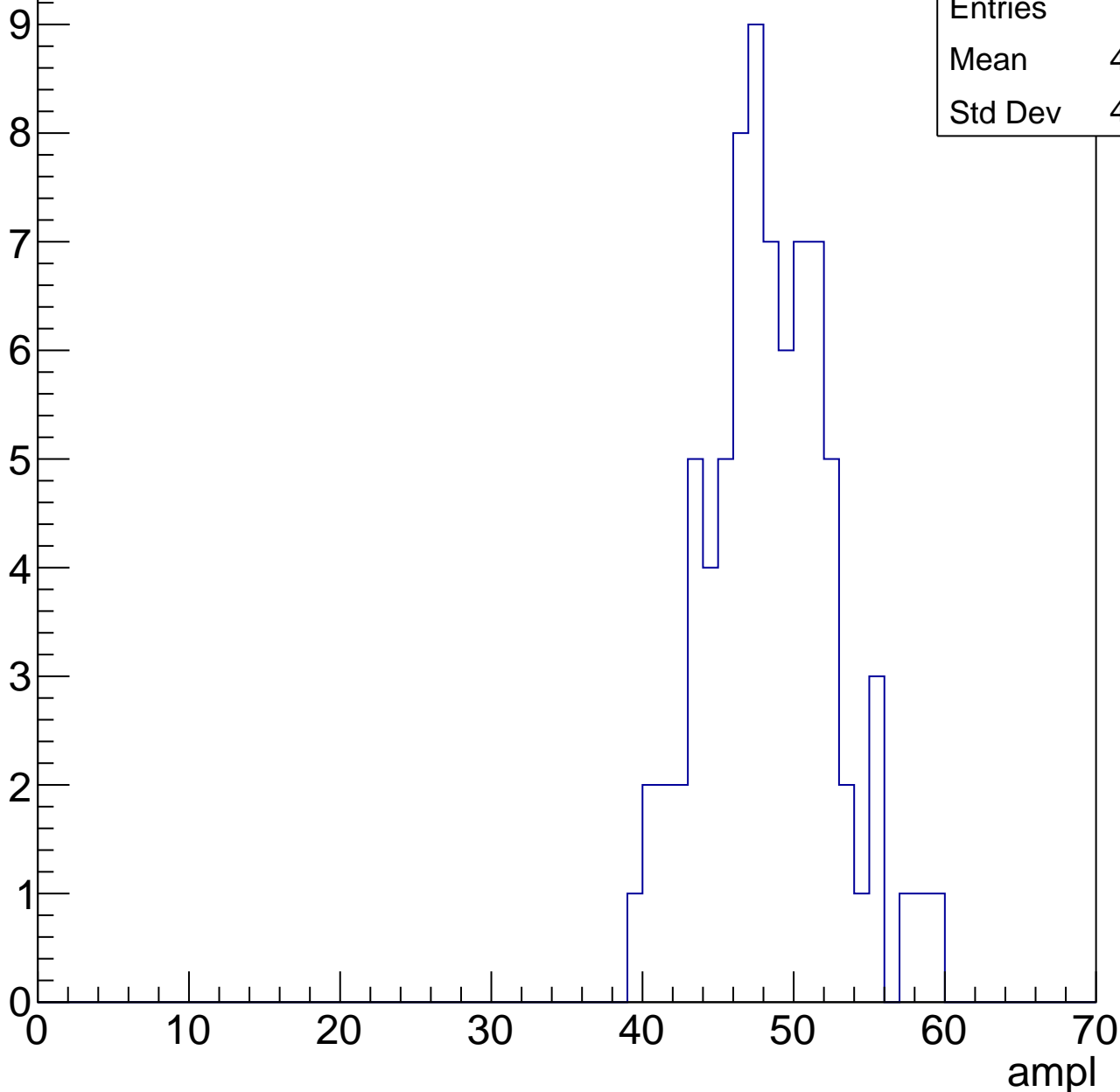


B1L102S, U9-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	47.95
Std Dev	4.158

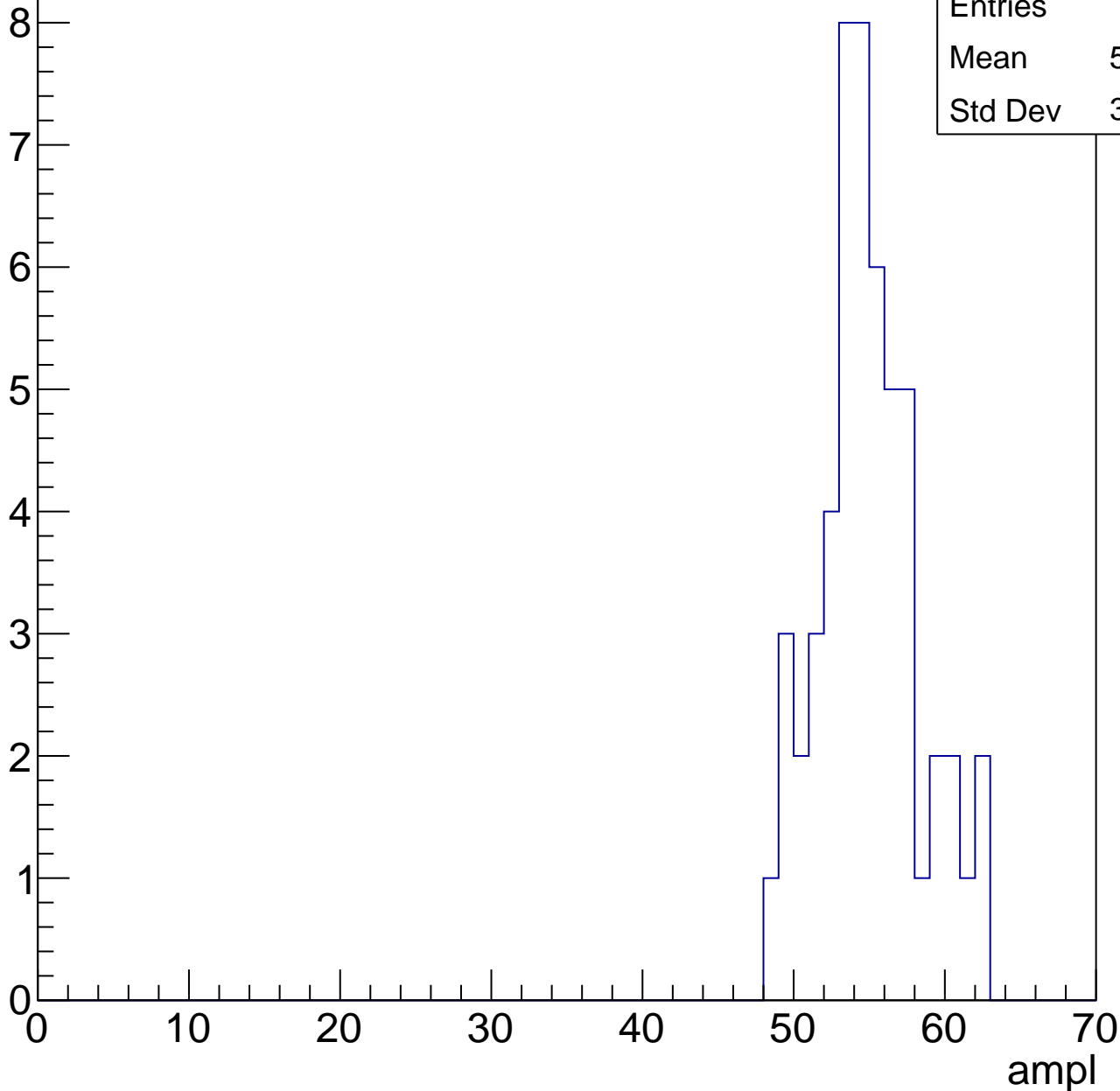


B1L102S, U9-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.49
Std Dev	3.272



B1L102S, U9-ch70, adc5

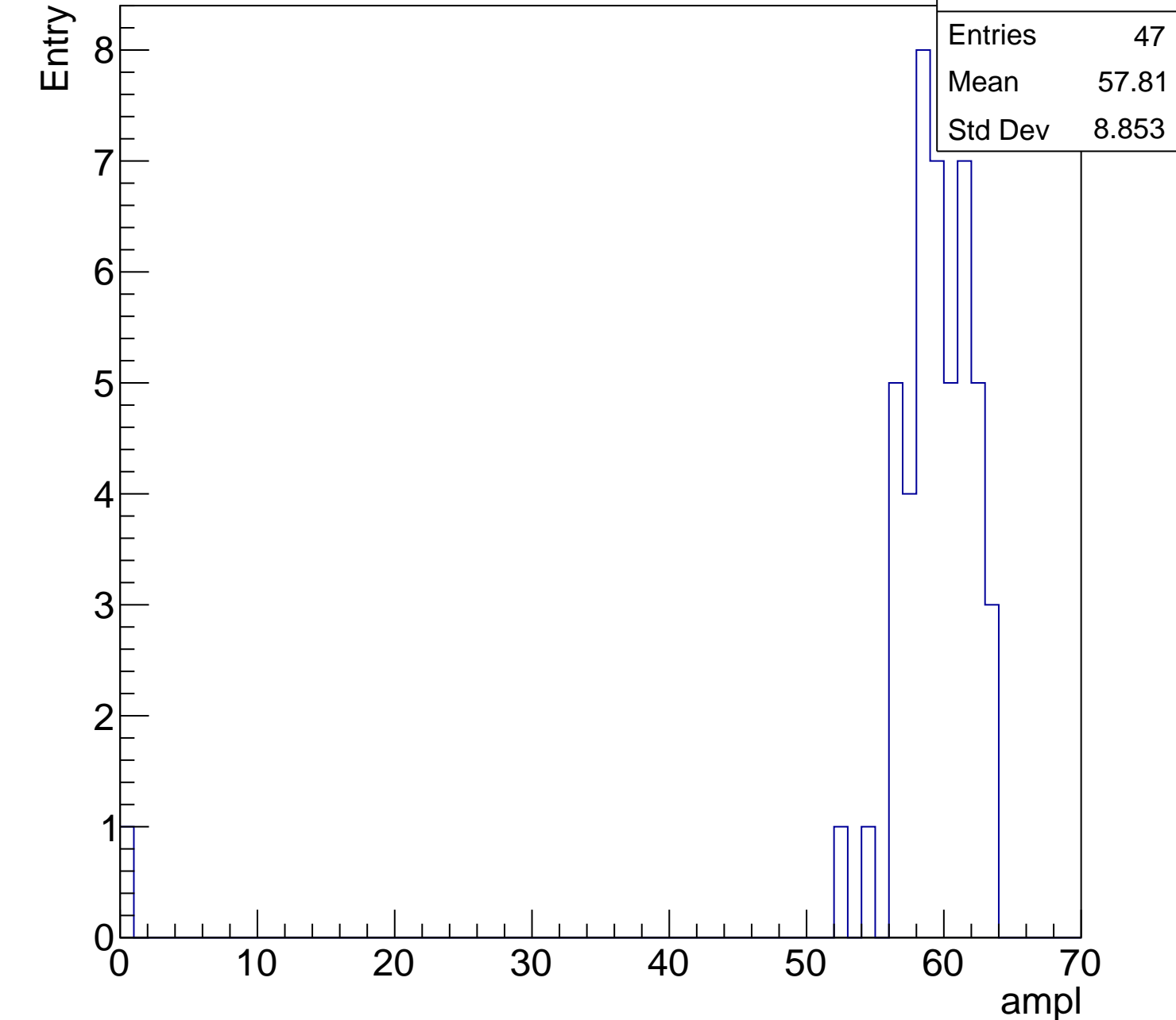
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	57.81
Std Dev	8.853

ampl

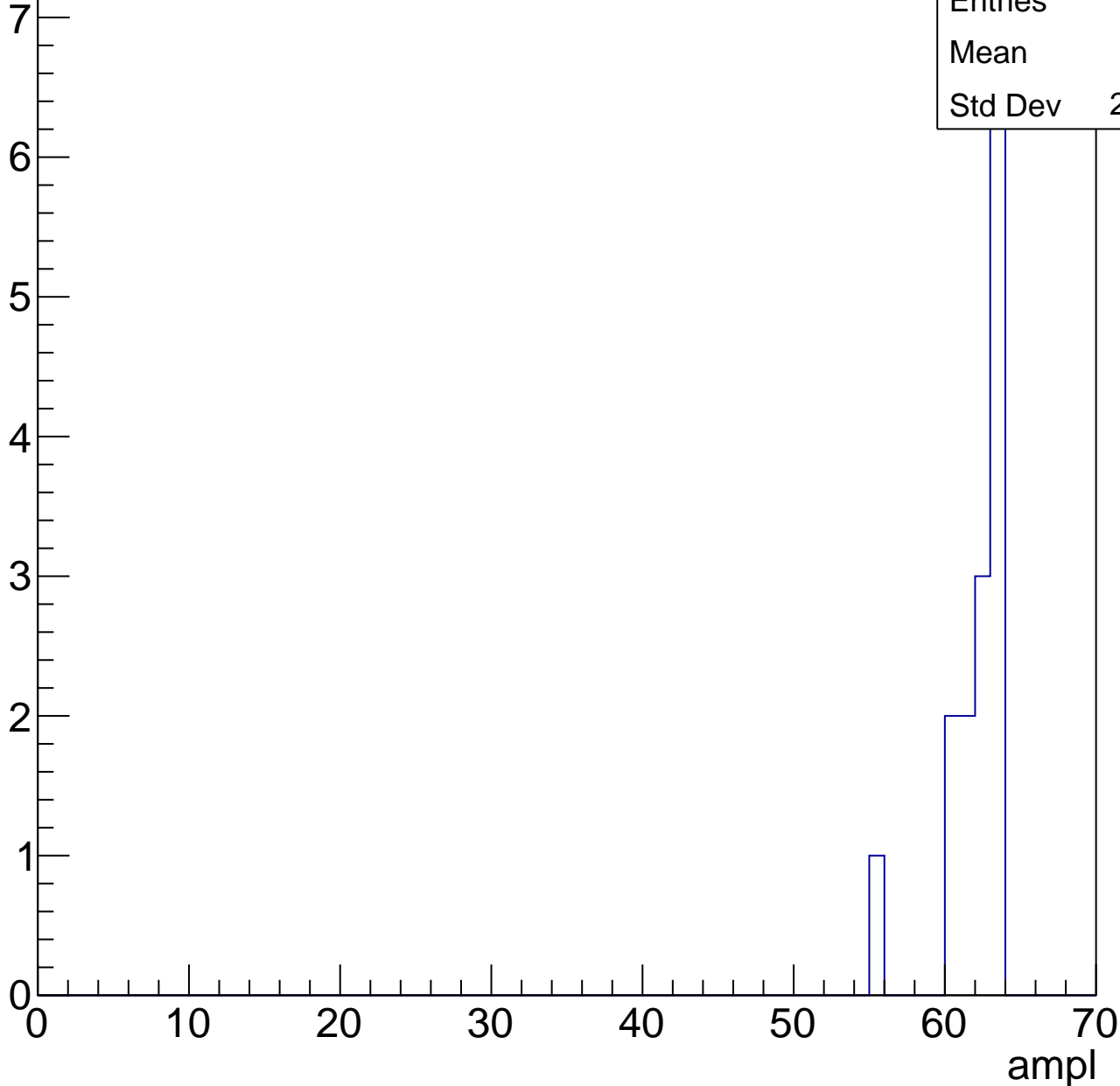


B1L102S, U9-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.6
Std Dev	2.059



B1L102S, U9-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch71, adc0

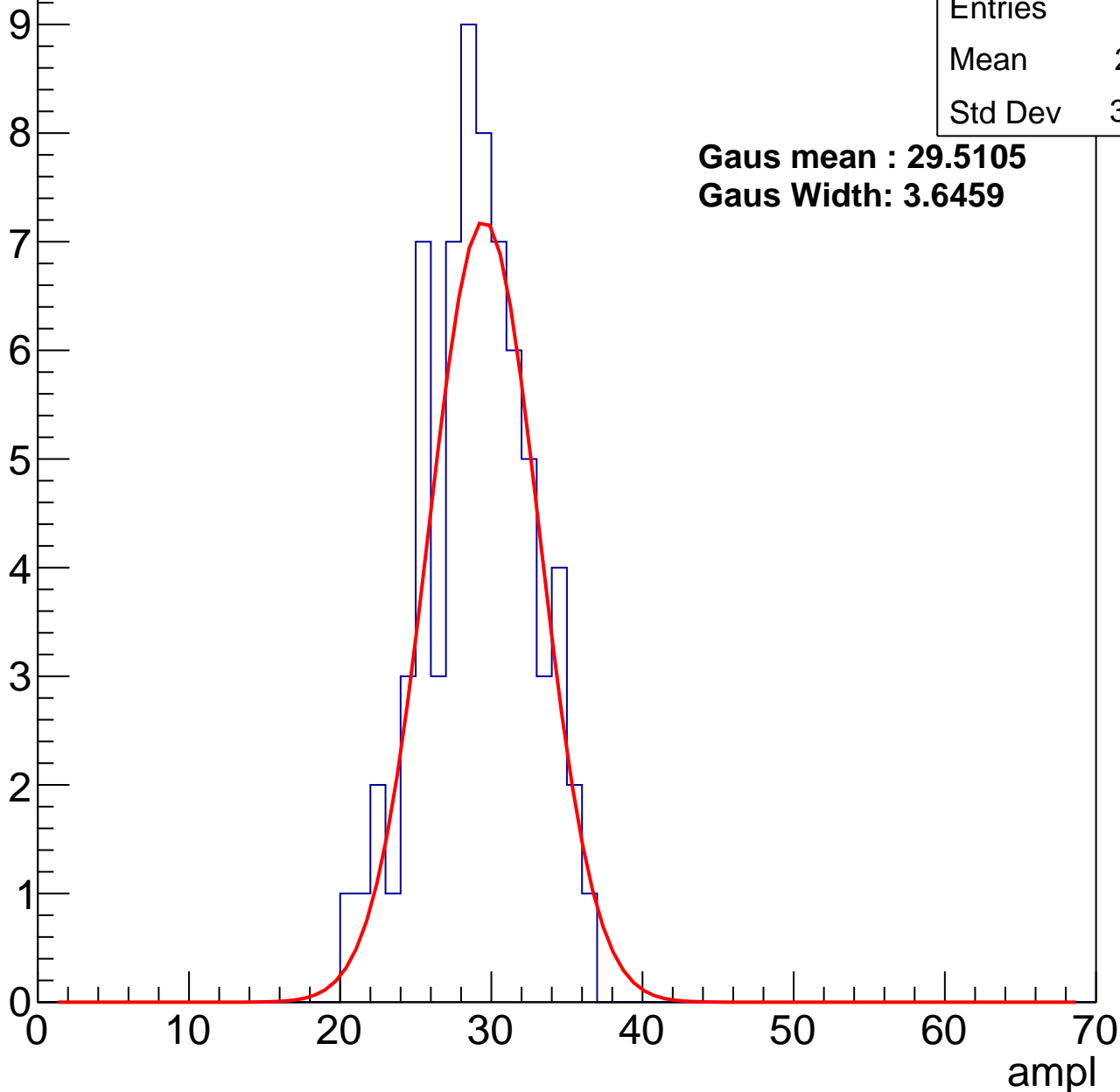
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.61
Std Dev	3.514

Gaus mean : 29.5105

Gaus Width: 3.6459



B1L102S, U9-ch71, adc1

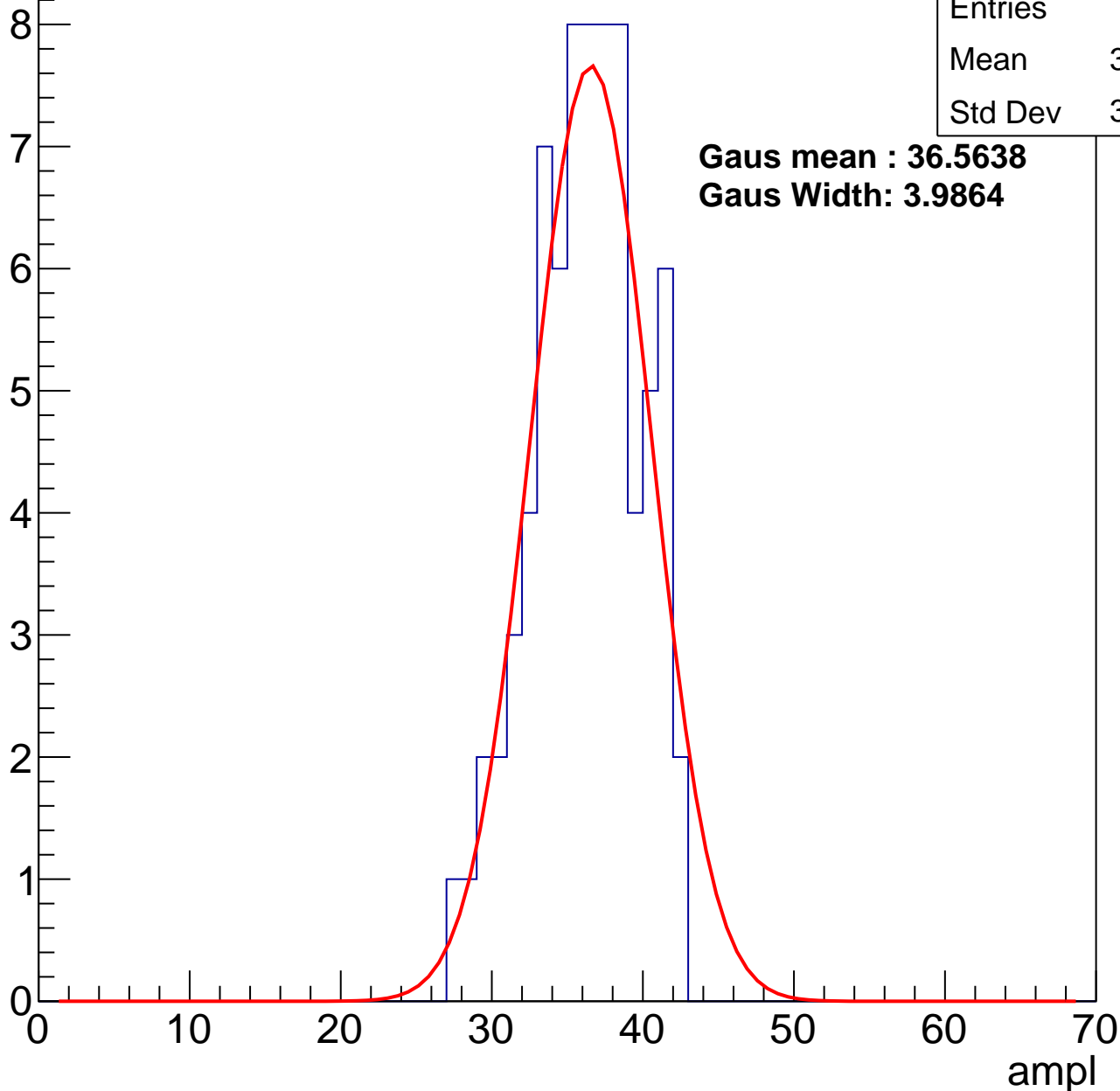
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.77
Std Dev	3.508

Gaus mean : 36.5638

Gaus Width: 3.9864



B1L102S, U9-ch71, adc2

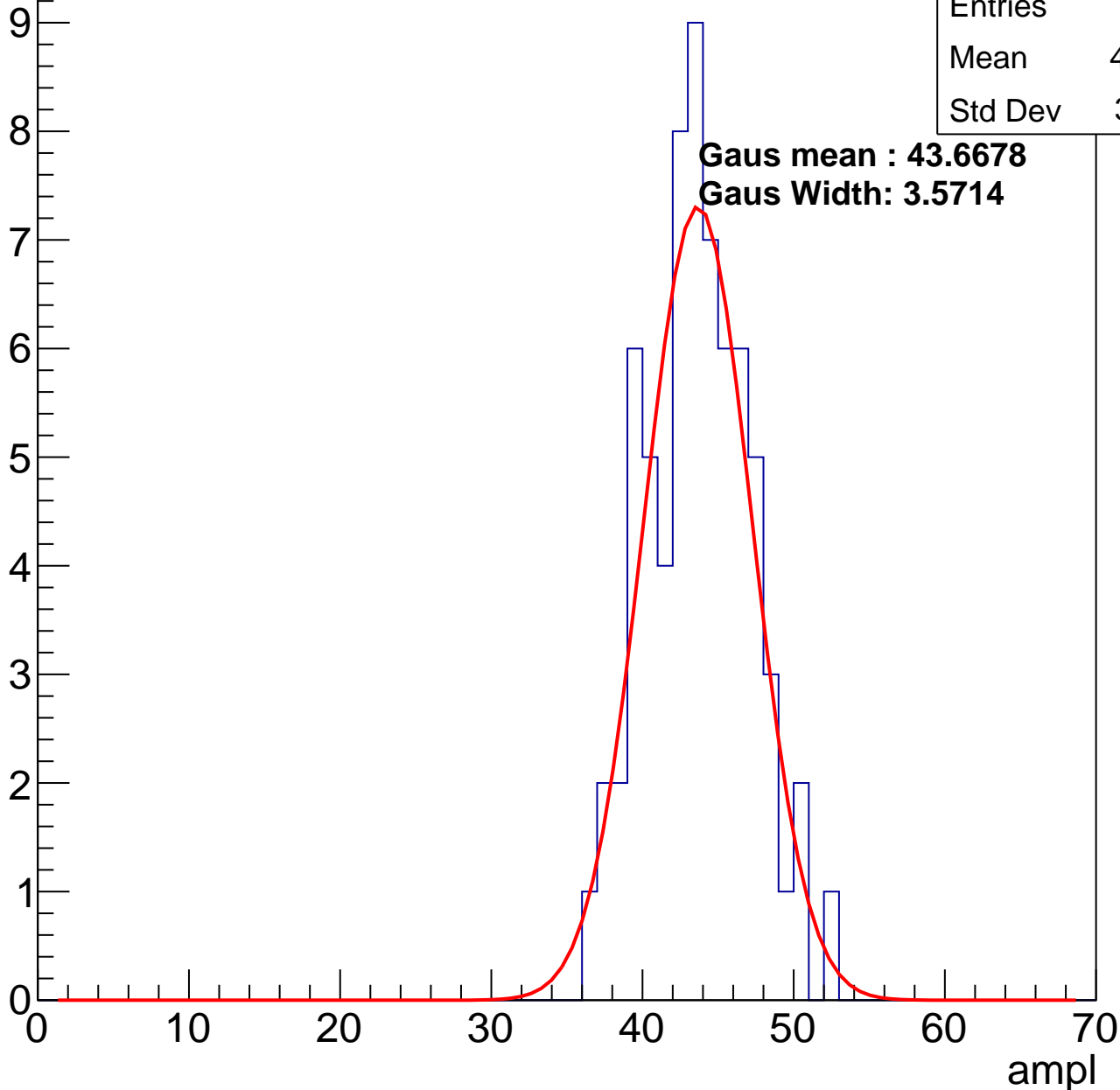
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.25
Std Dev	3.401

Gaus mean : 43.6678

Gaus Width: 3.5714

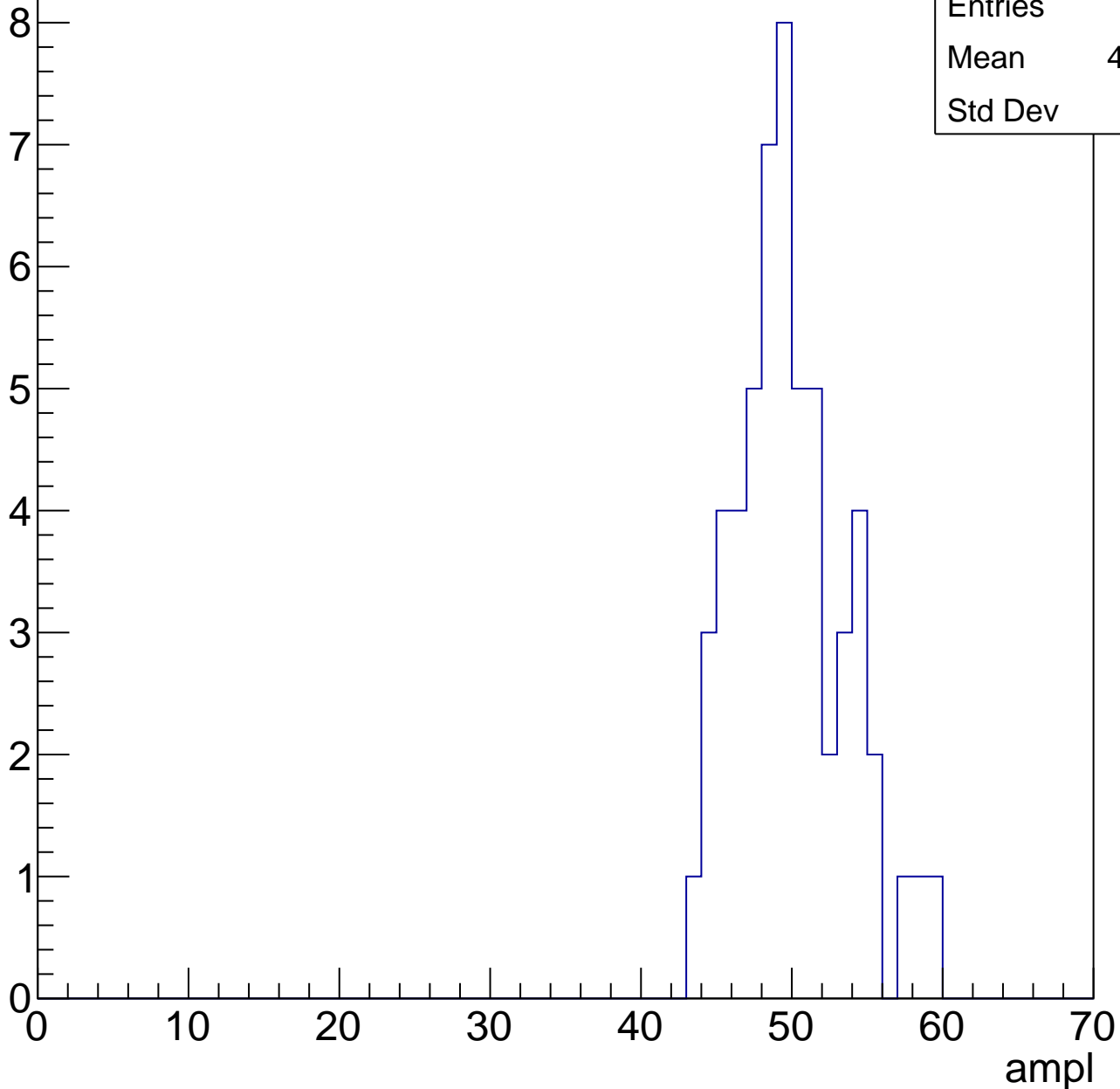


B1L102S, U9-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

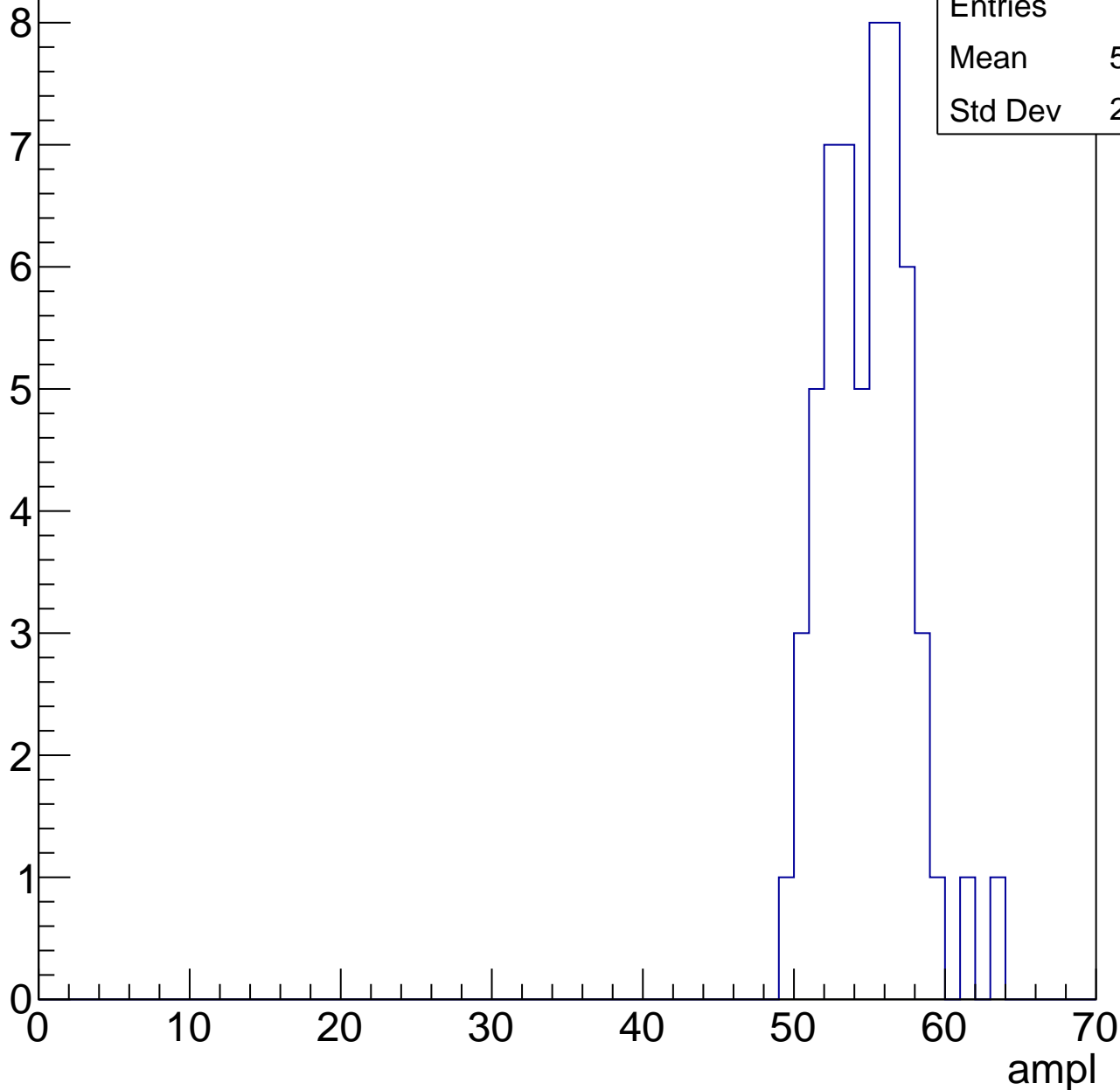
Entries	56
Mean	49.46
Std Dev	3.62



B1L102S, U9-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch71, adc5

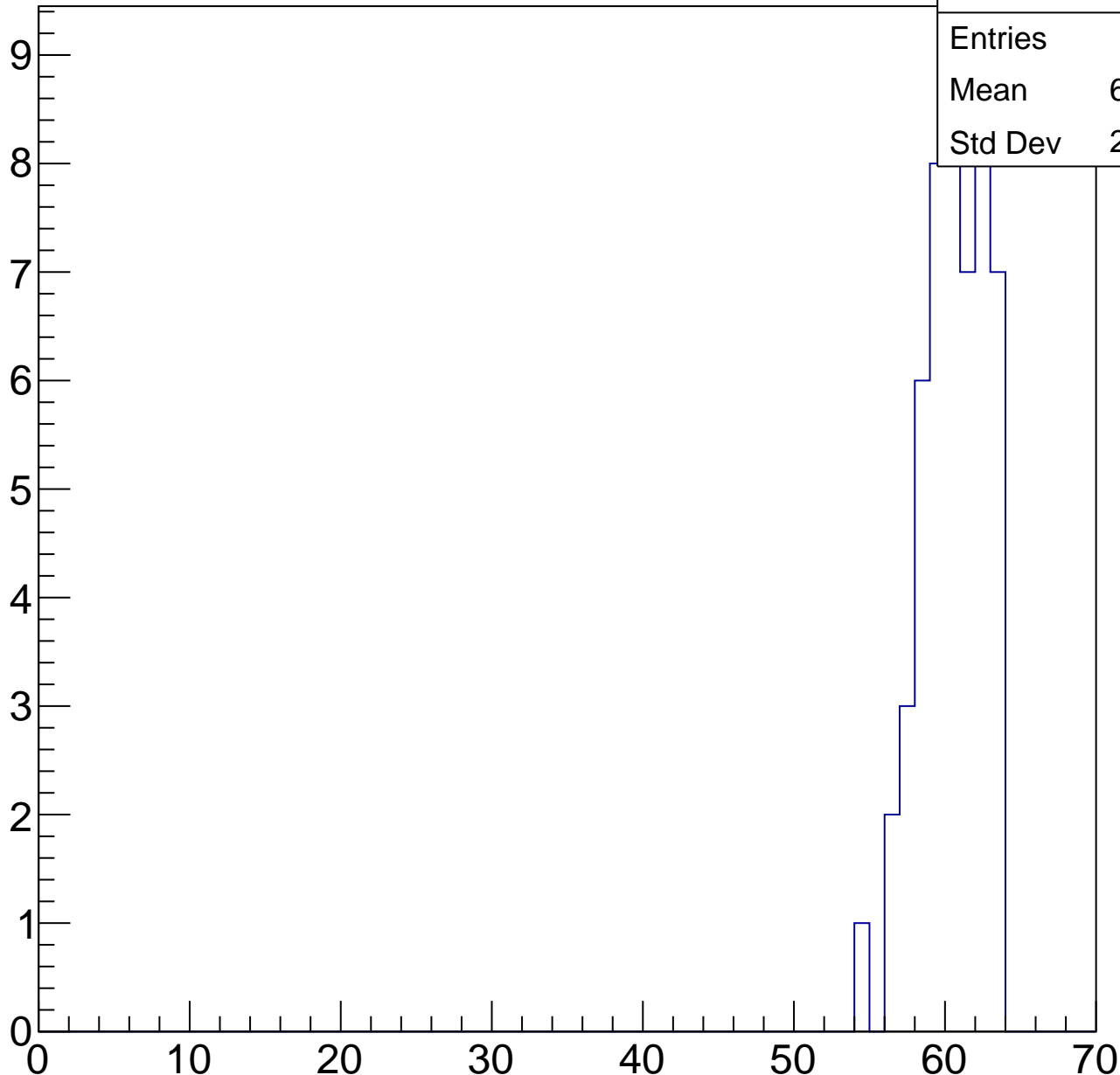
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	60.02
Std Dev	2.119

ampl



B1L102S, U9-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	54
Std Dev	20.43

ampl

B1L102S, U9-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch72, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	28.38
Std Dev	4.05

Gaus mean : 28.7601

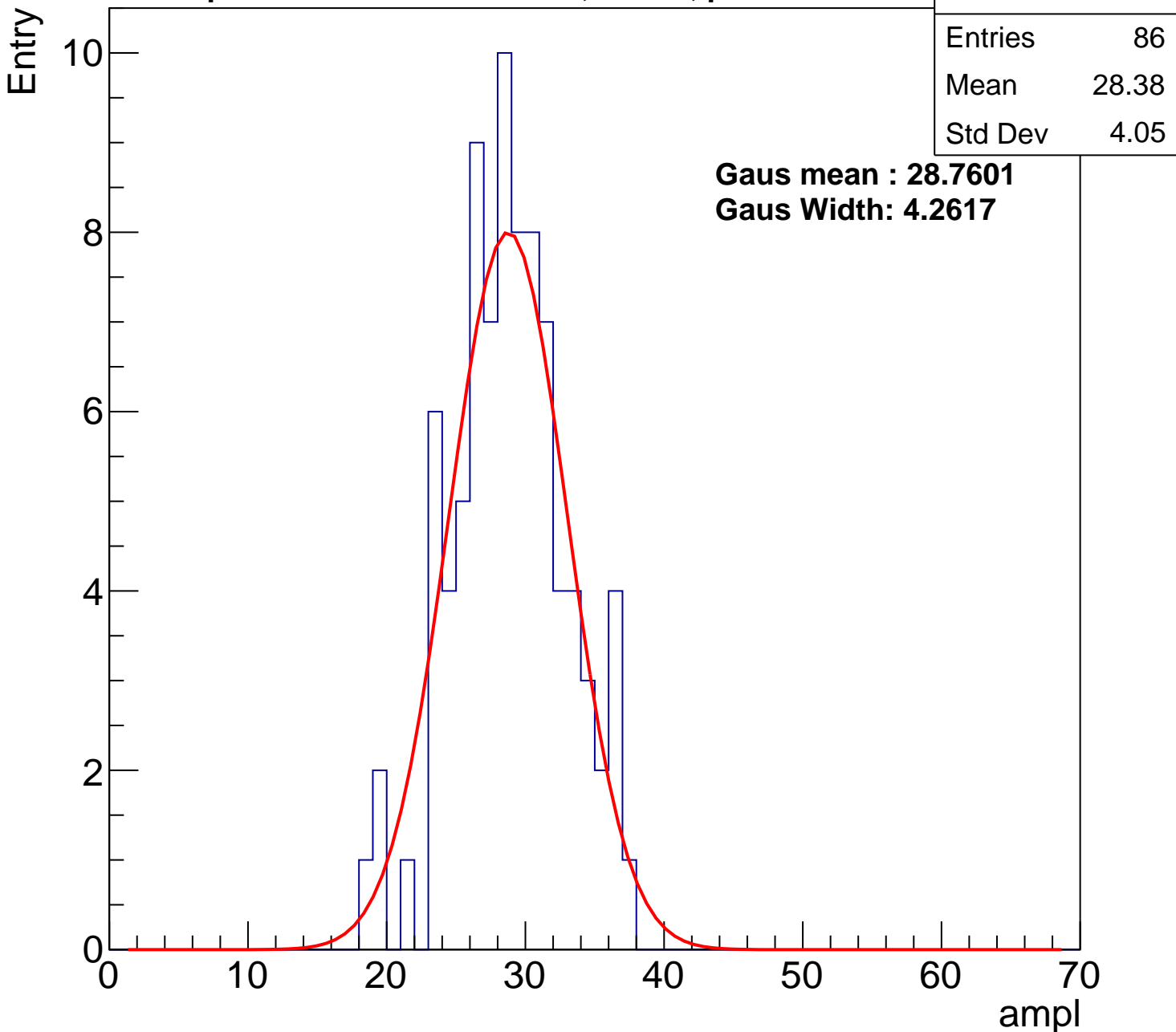
Gaus Width: 4.2617

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch72, adc1

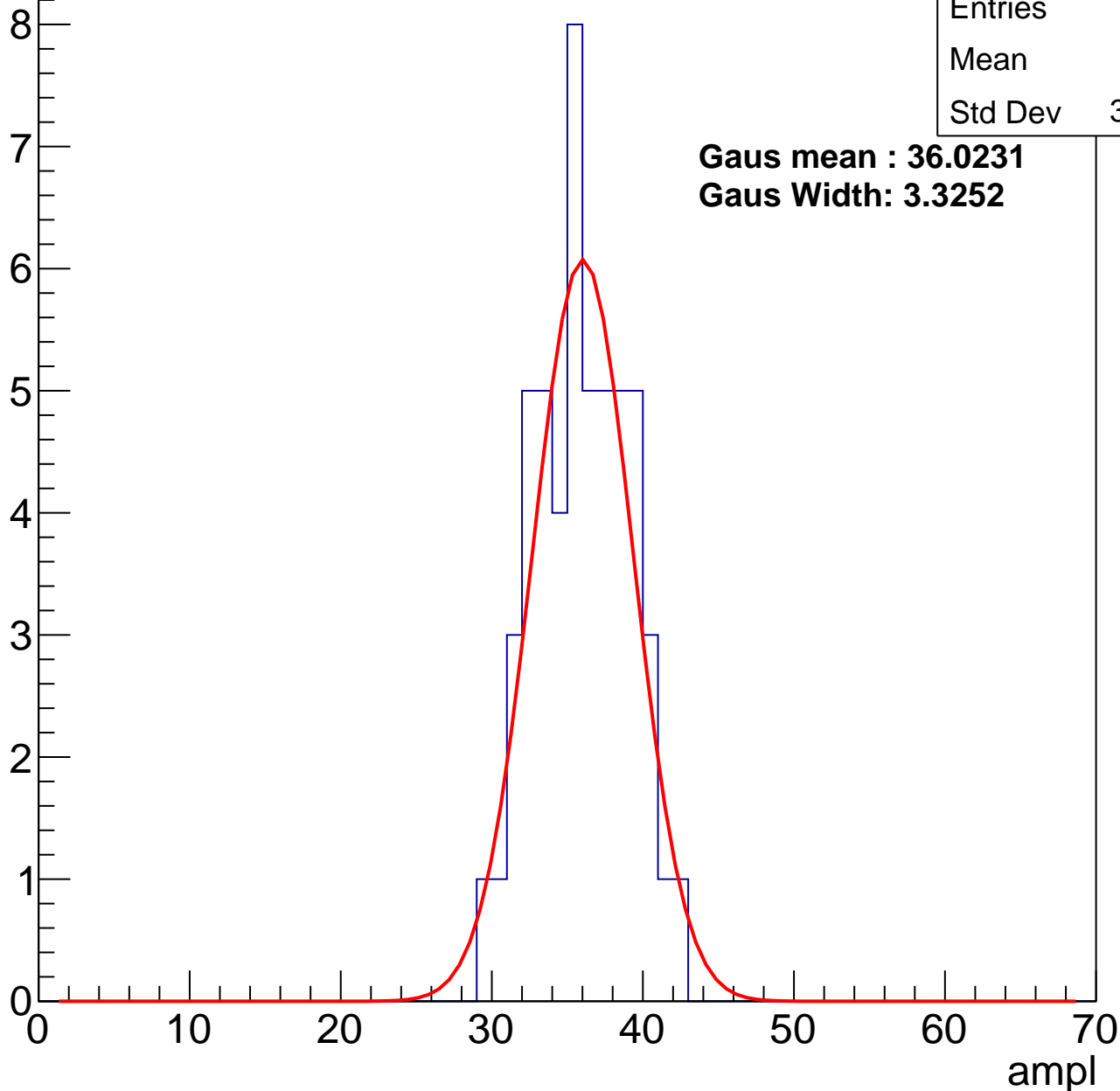
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	35.5
Std Dev	3.022

Gaus mean : 36.0231

Gaus Width: 3.3252



B1L102S, U9-ch72, adc2

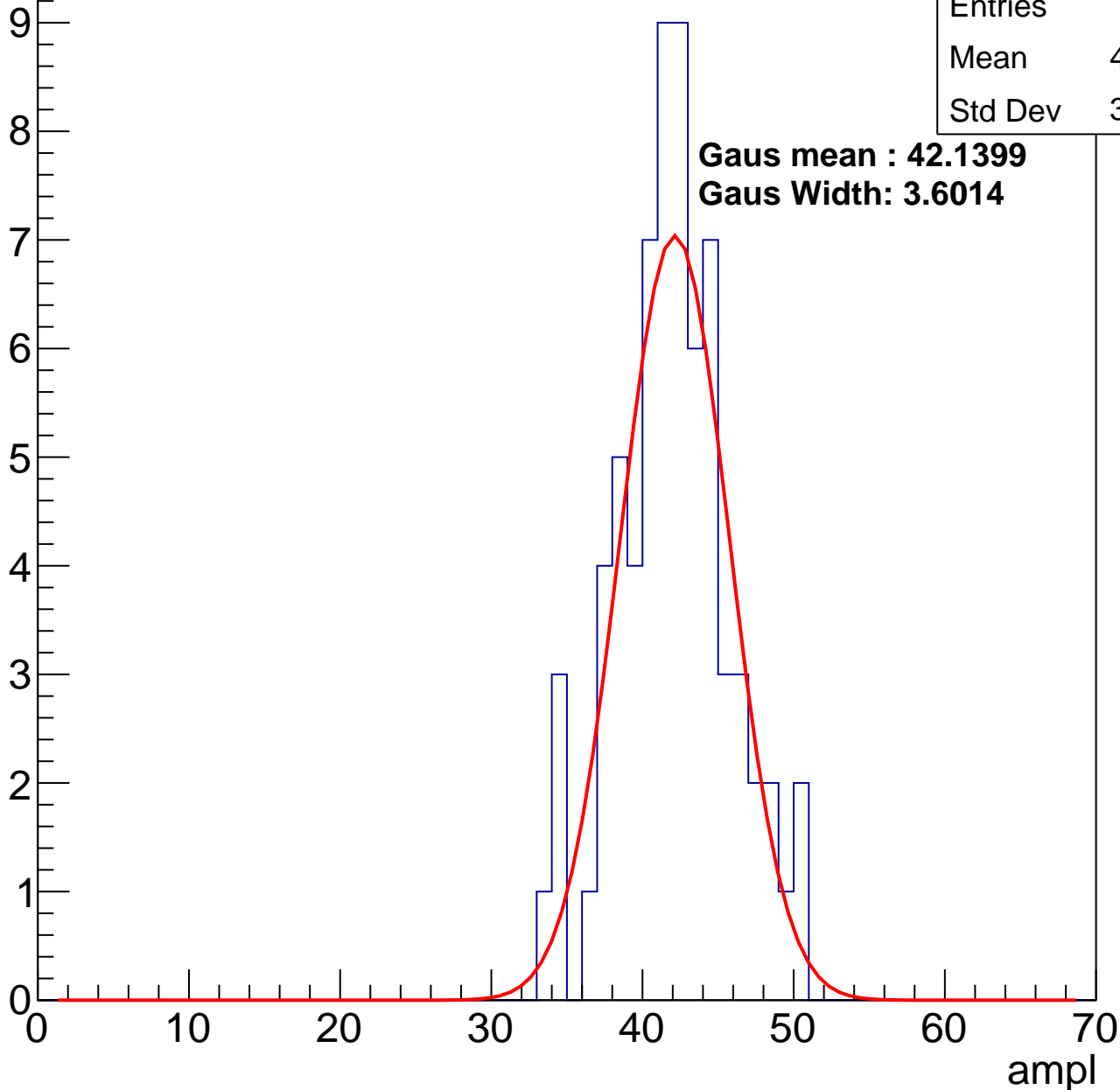
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.59
Std Dev	3.727

Gaus mean : 42.1399

Gaus Width: 3.6014

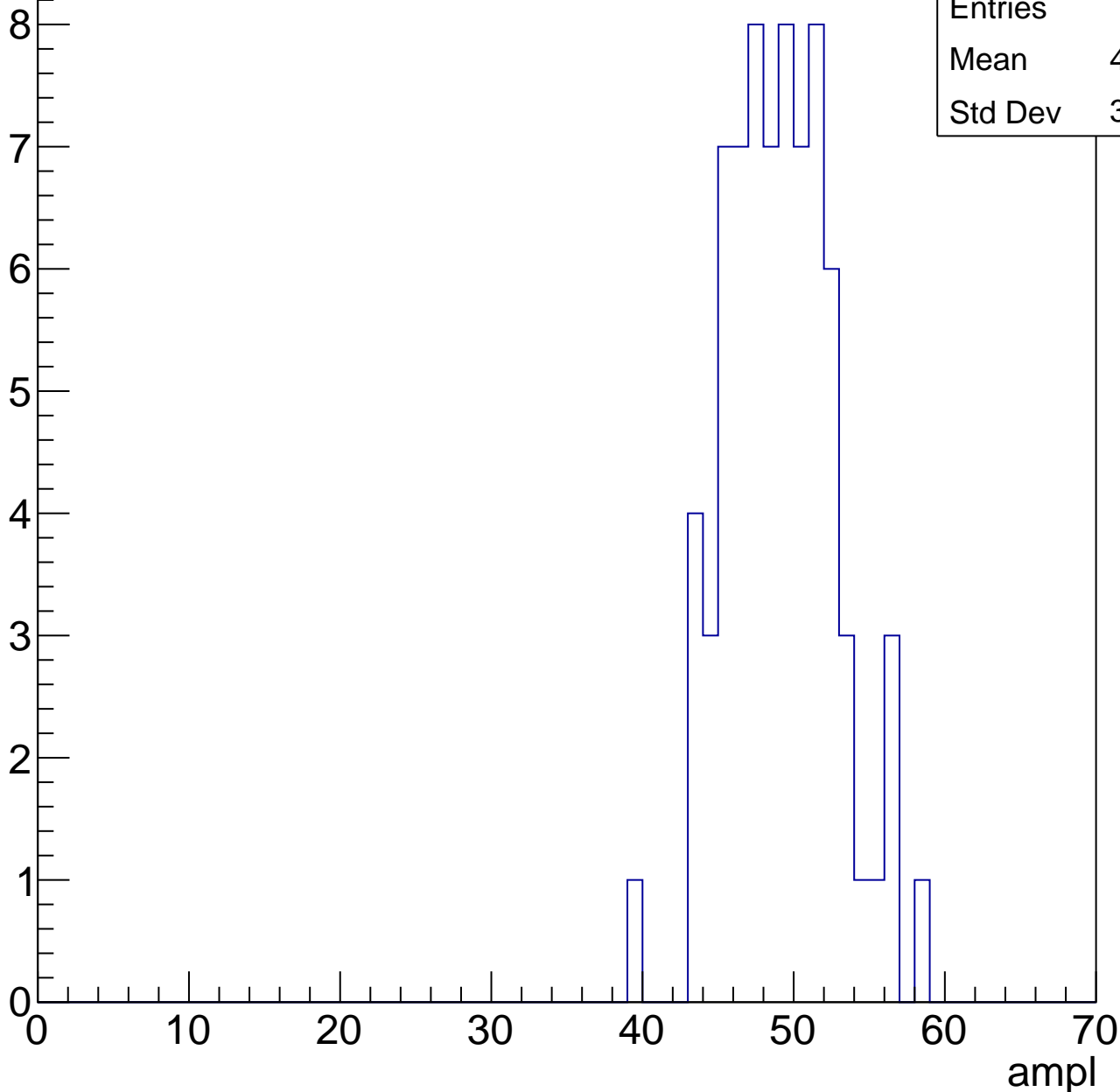


B1L102S, U9-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

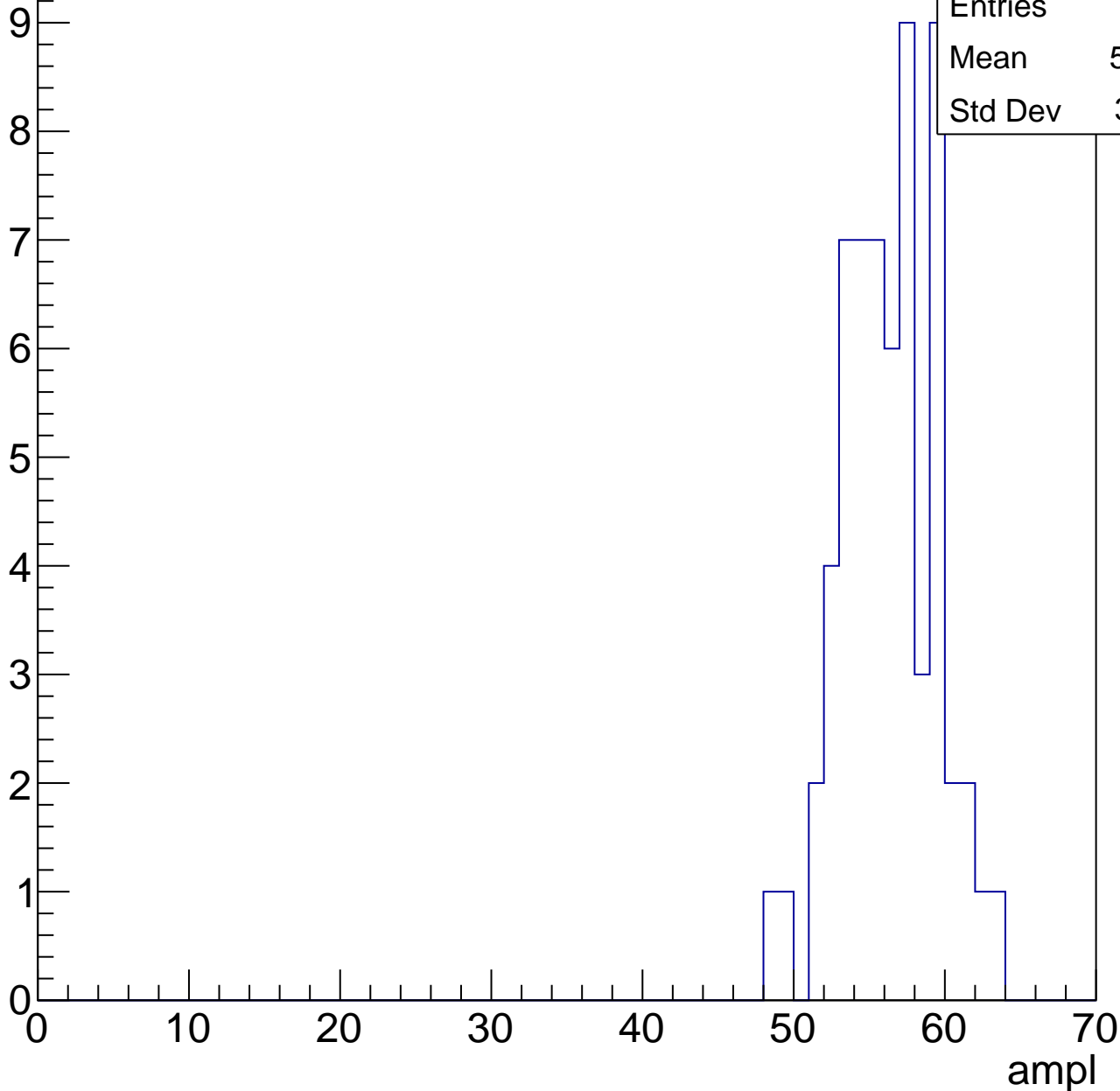
Entries	75
Mean	48.64
Std Dev	3.565



B1L102S, U9-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

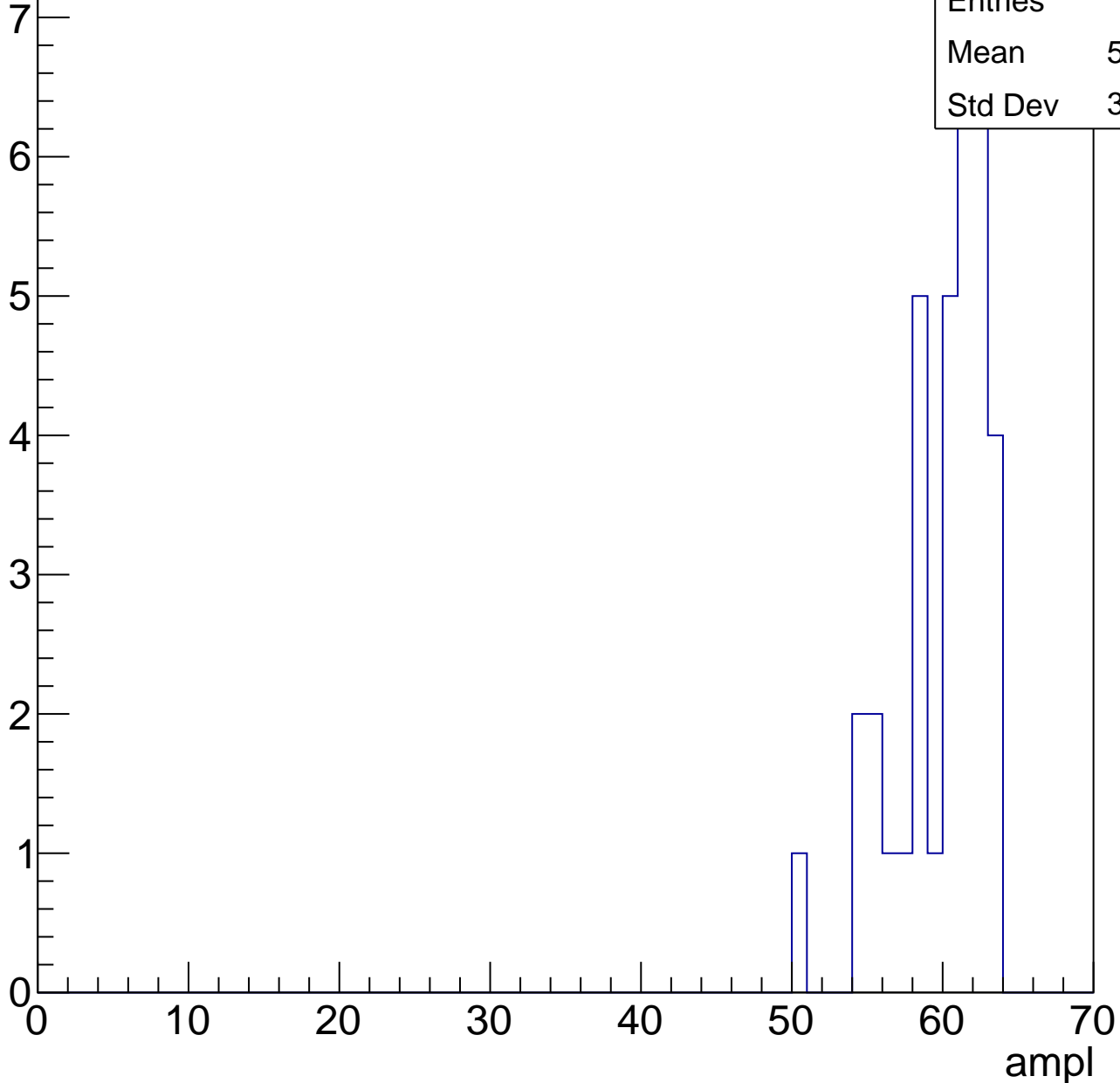


B1L102S, U9-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	59.53
Std Dev	3.032

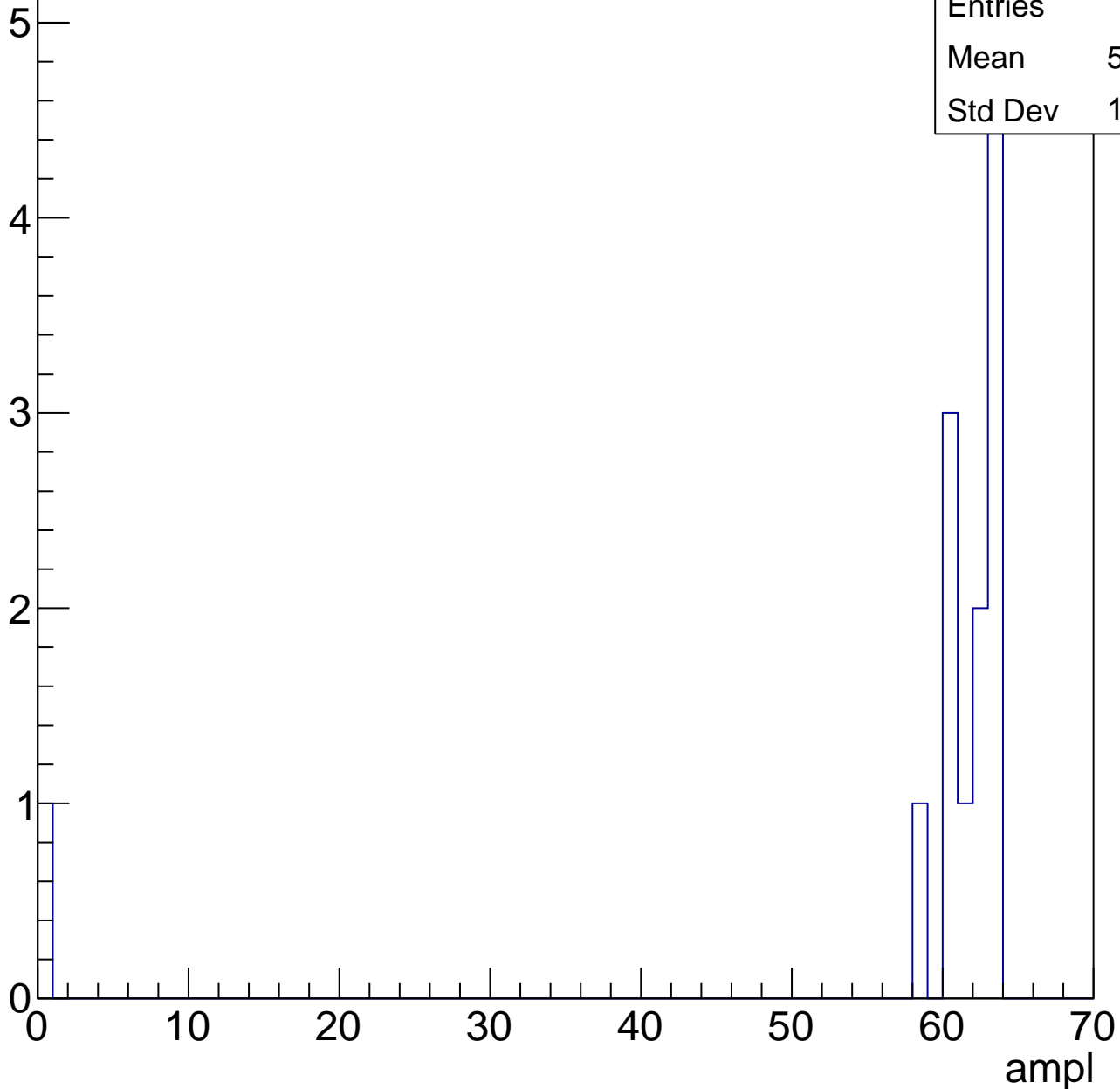


B1L102S, U9-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.77
Std Dev	16.46



B1L102S, U9-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch73, adc0

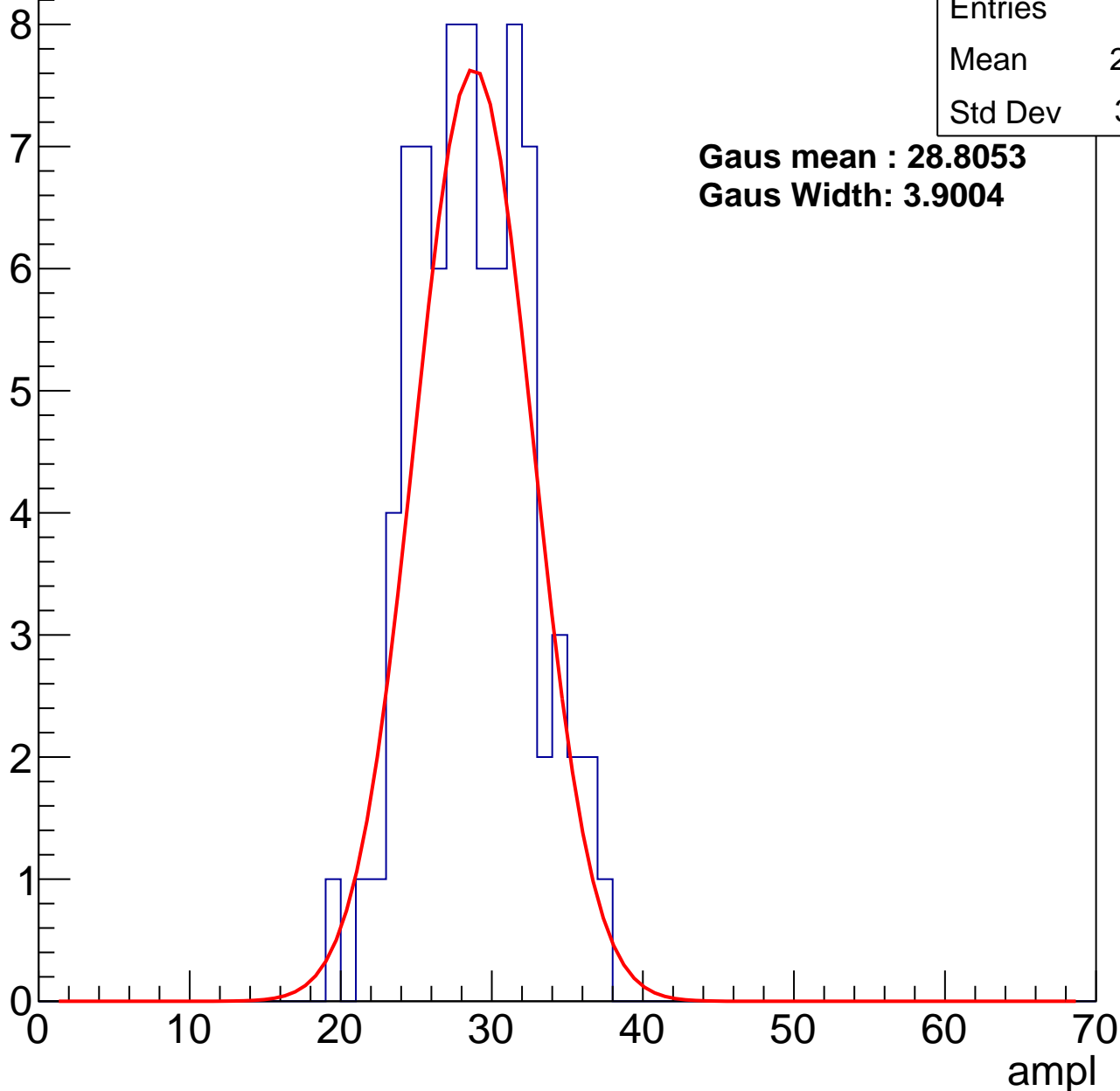
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.32
Std Dev	3.781

Gaus mean : 28.8053

Gaus Width: 3.9004



B1L102S, U9-ch73, adc1

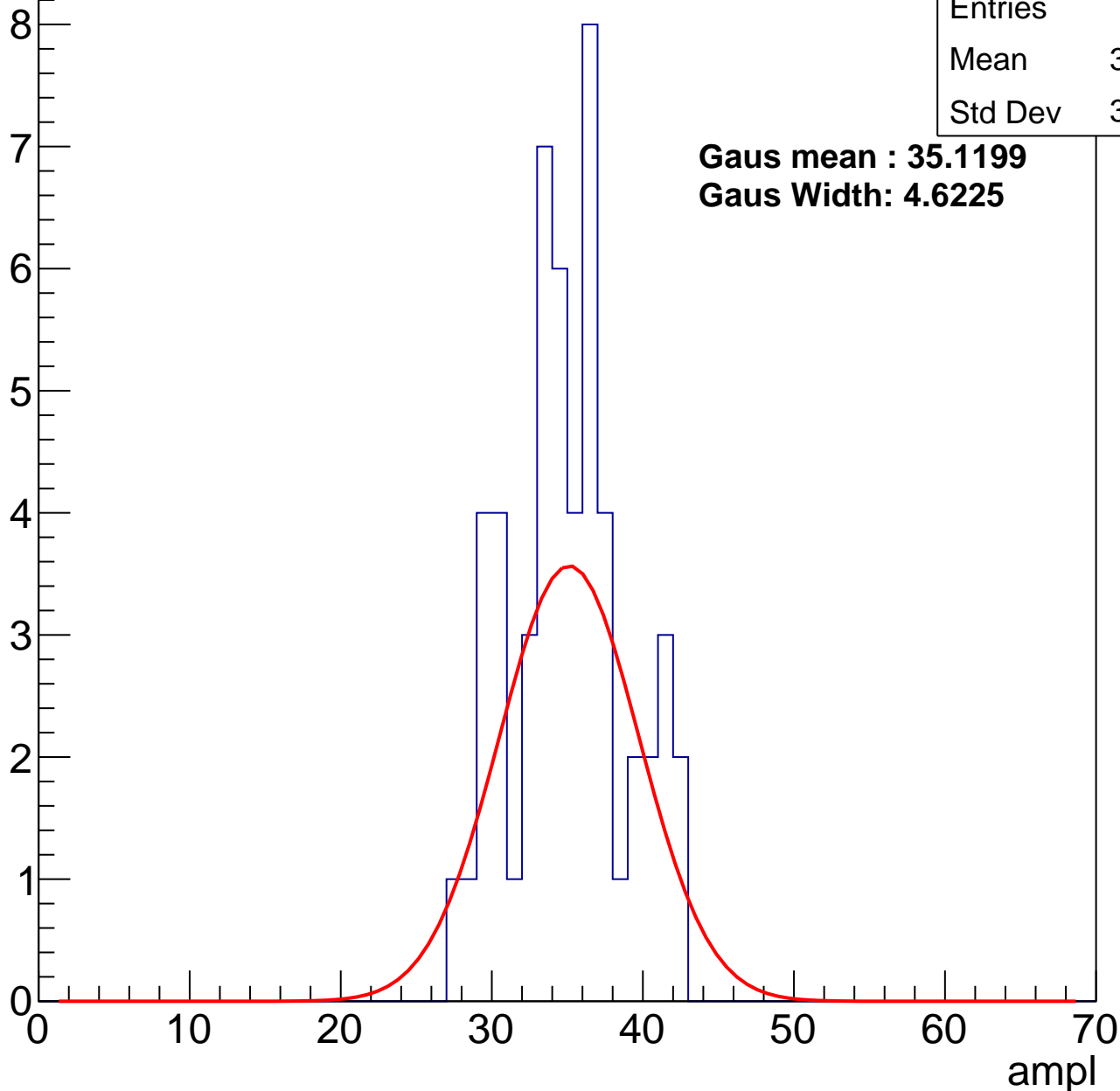
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.57
Std Dev	3.749

Gaus mean : 35.1199

Gaus Width: 4.6225



B1L102S, U9-ch73, adc2

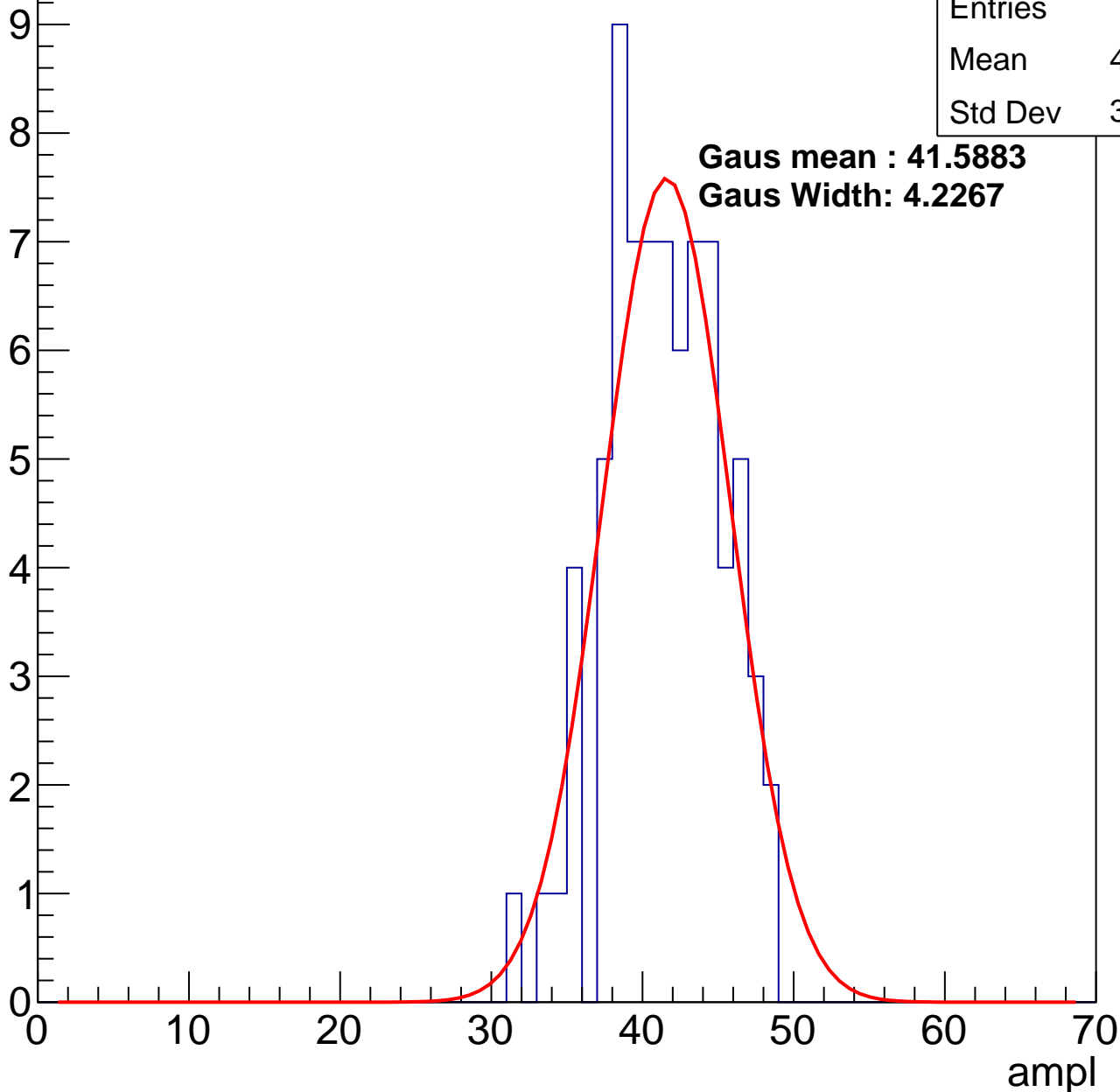
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	40.96
Std Dev	3.708

Gaus mean : 41.5883

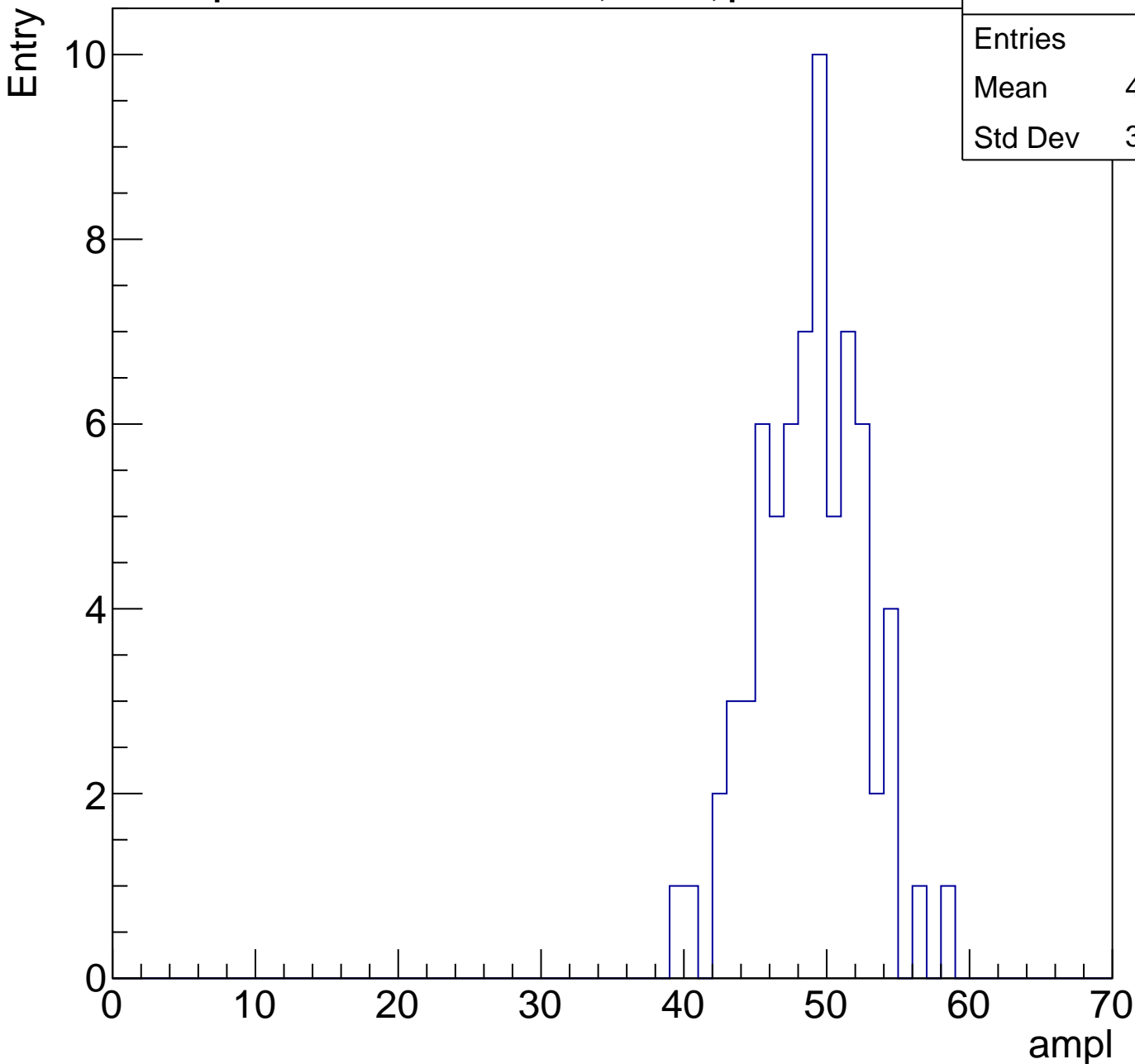
Gaus Width: 4.2267



B1L102S, U9-ch73, adc3

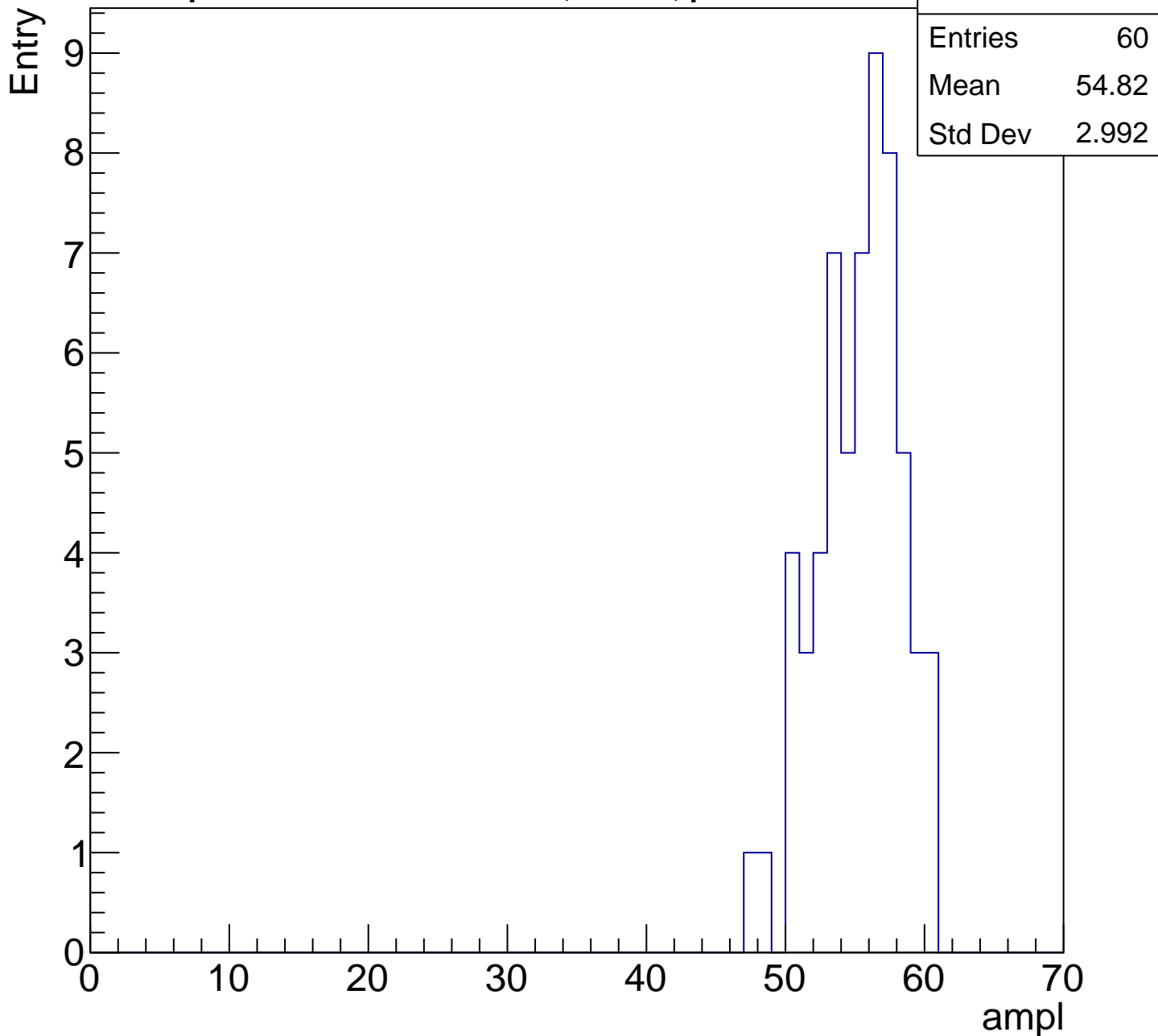
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	48.39
Std Dev	3.697



B1L102S, U9-ch73, adc4

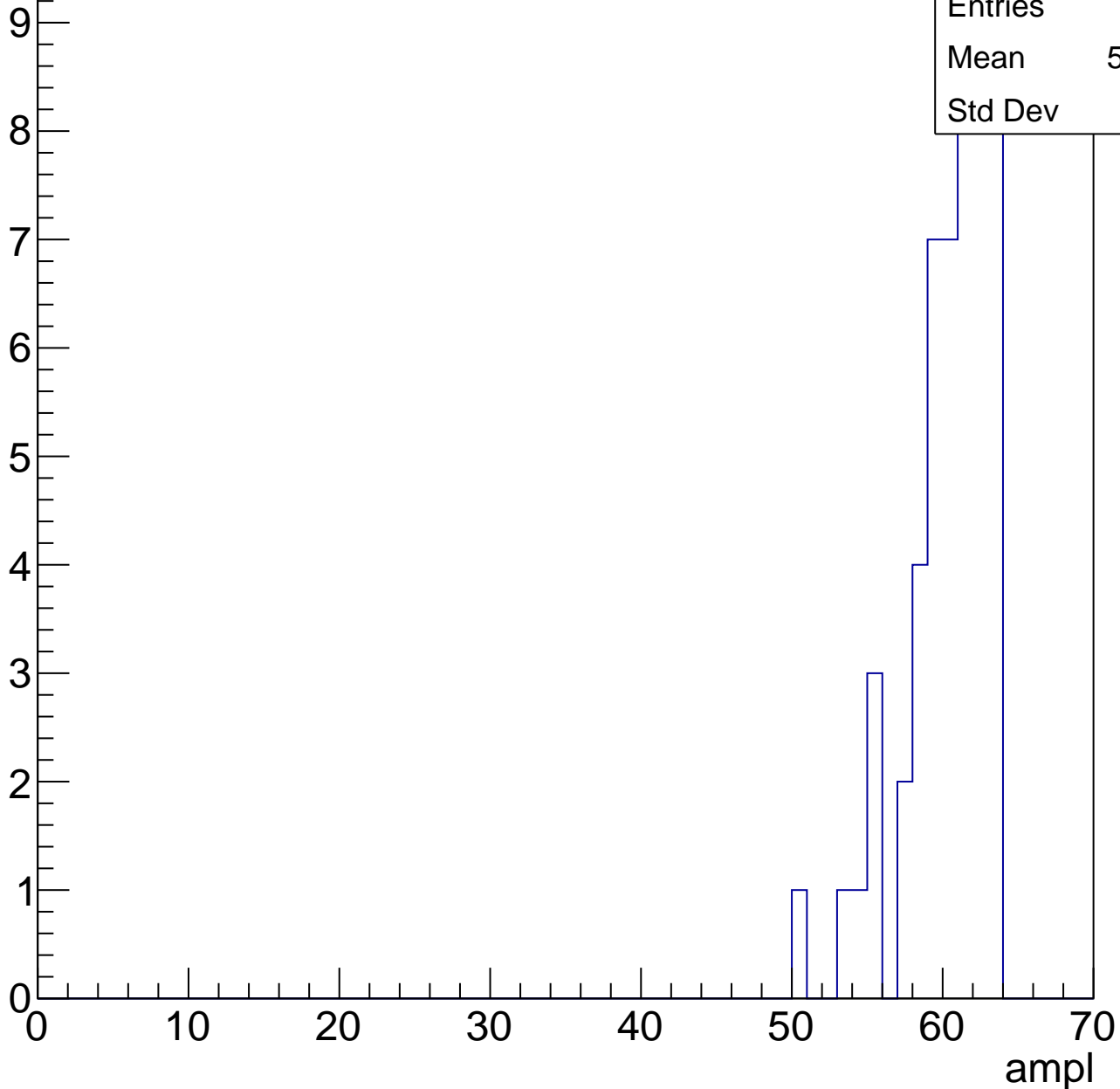
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U9-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

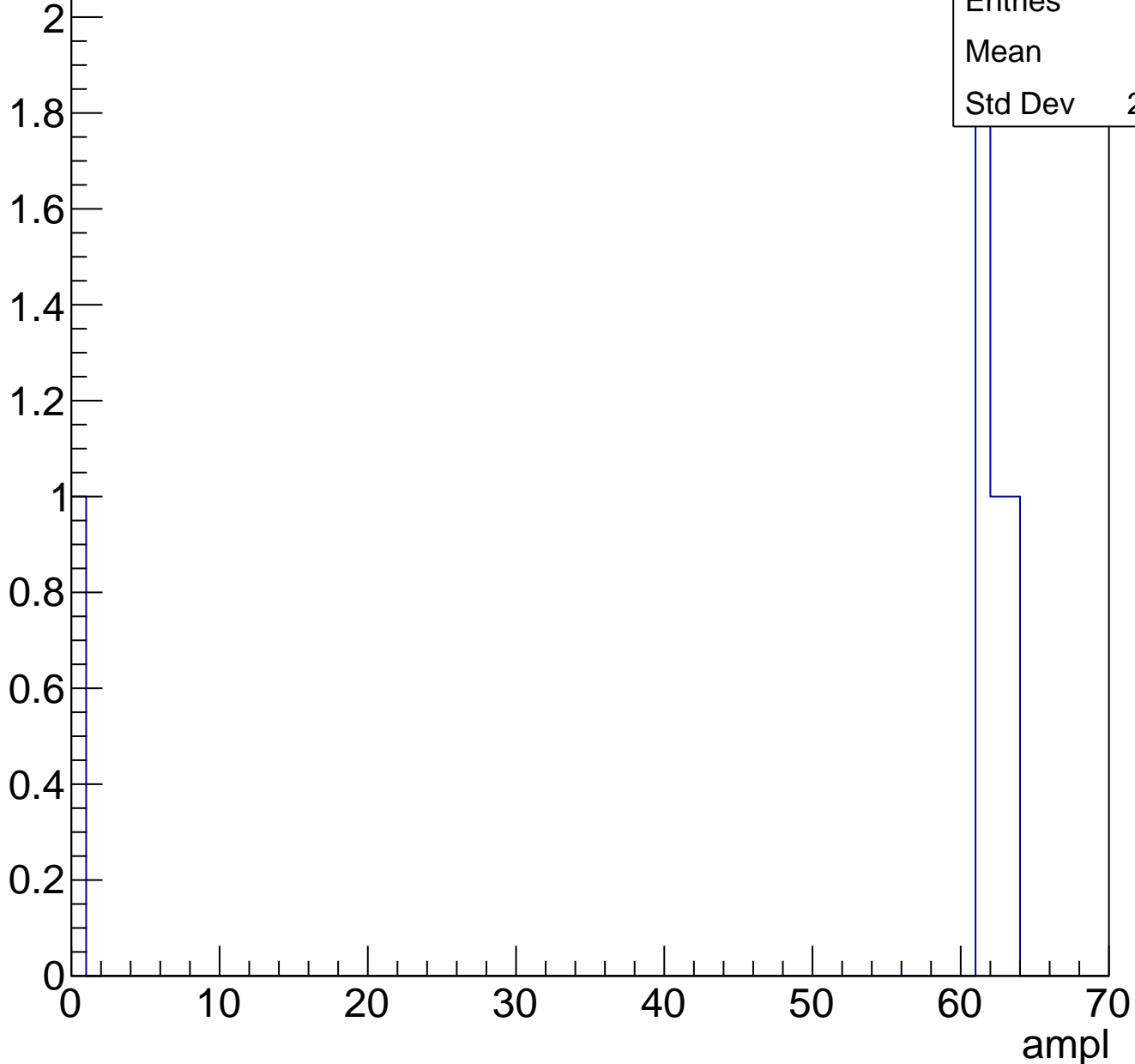
Entry



B1L102S, U9-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

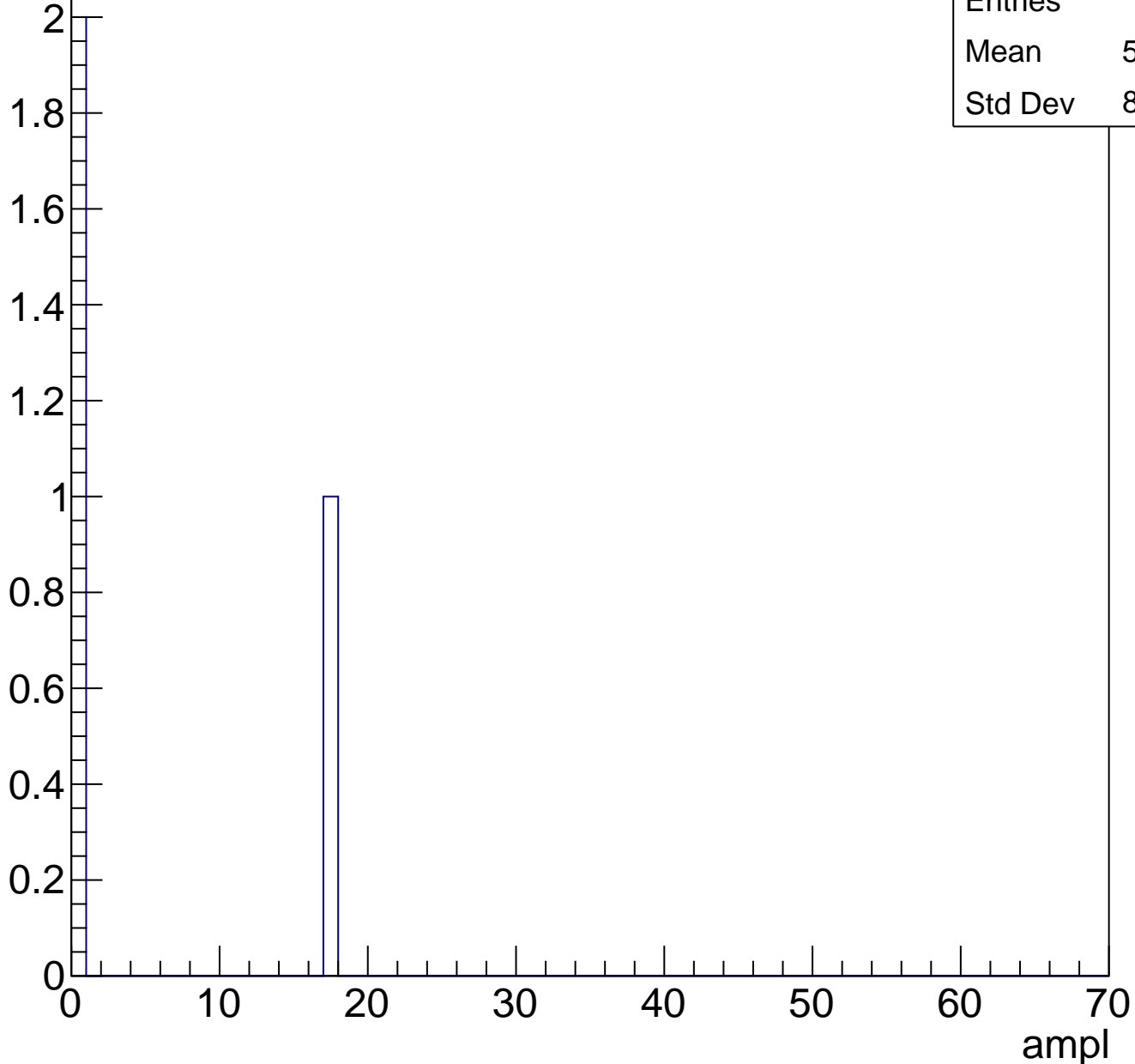


Entries	5
Mean	49.4
Std Dev	24.71

B1L102S, U9-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	5.667
Std Dev	8.014

B1L102S, U9-ch74, adc0

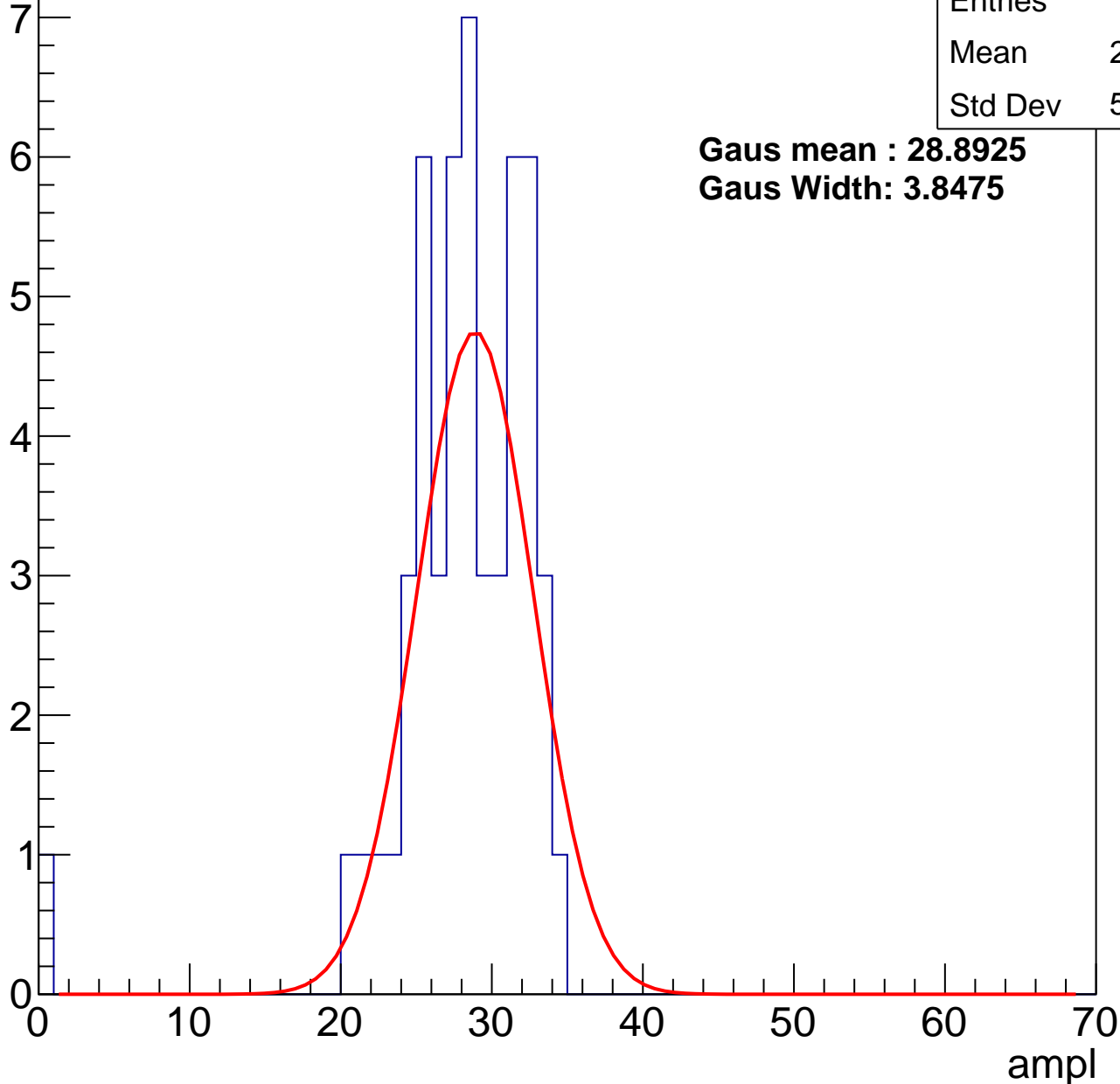
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	27.54
Std Dev	5.082

Gaus mean : 28.8925

Gaus Width: 3.8475



B1L102S, U9-ch74, adc1

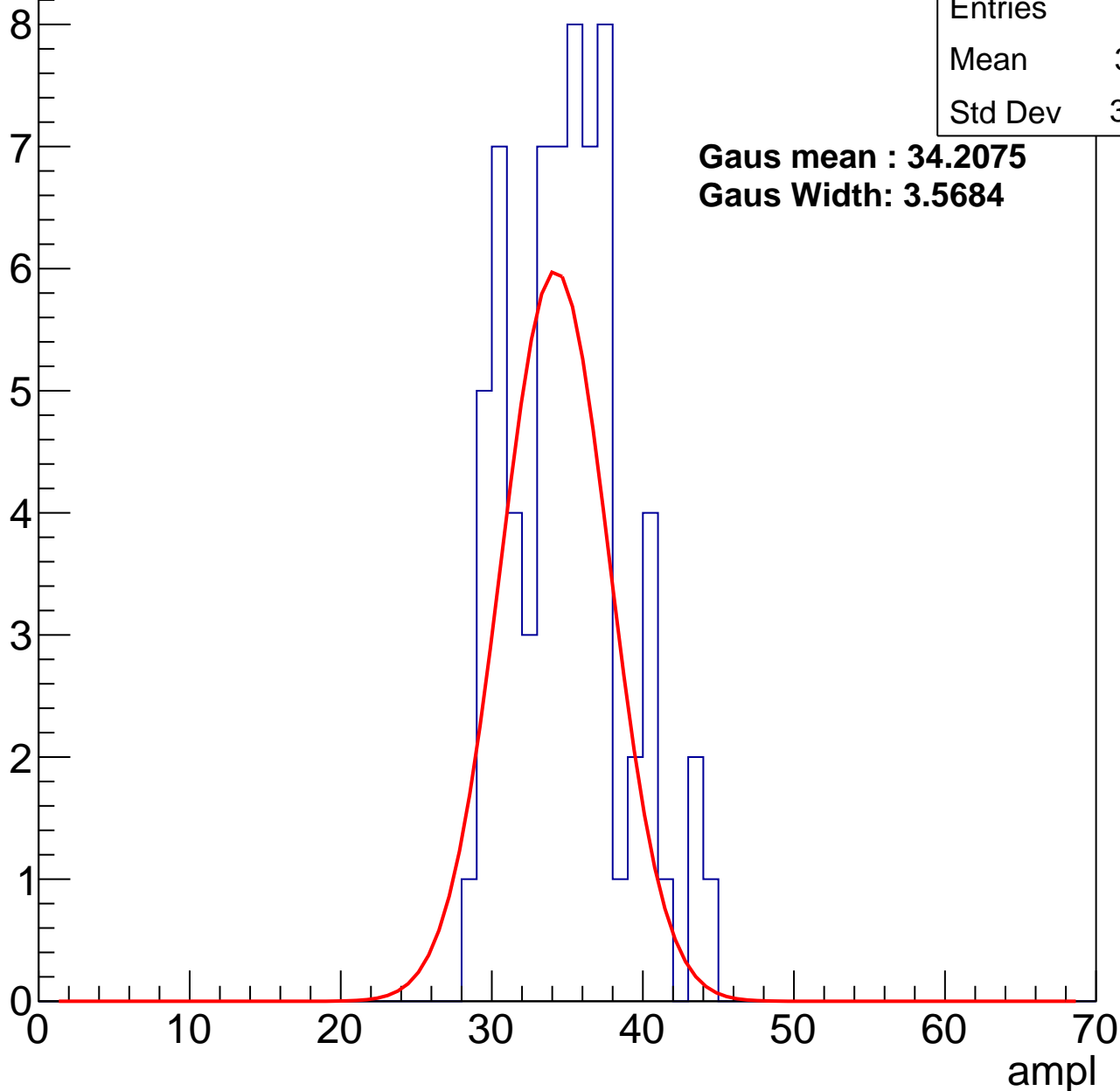
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.51
Std Dev	3.716

Gaus mean : 34.2075

Gaus Width: 3.5684



B1L102S, U9-ch74, adc2

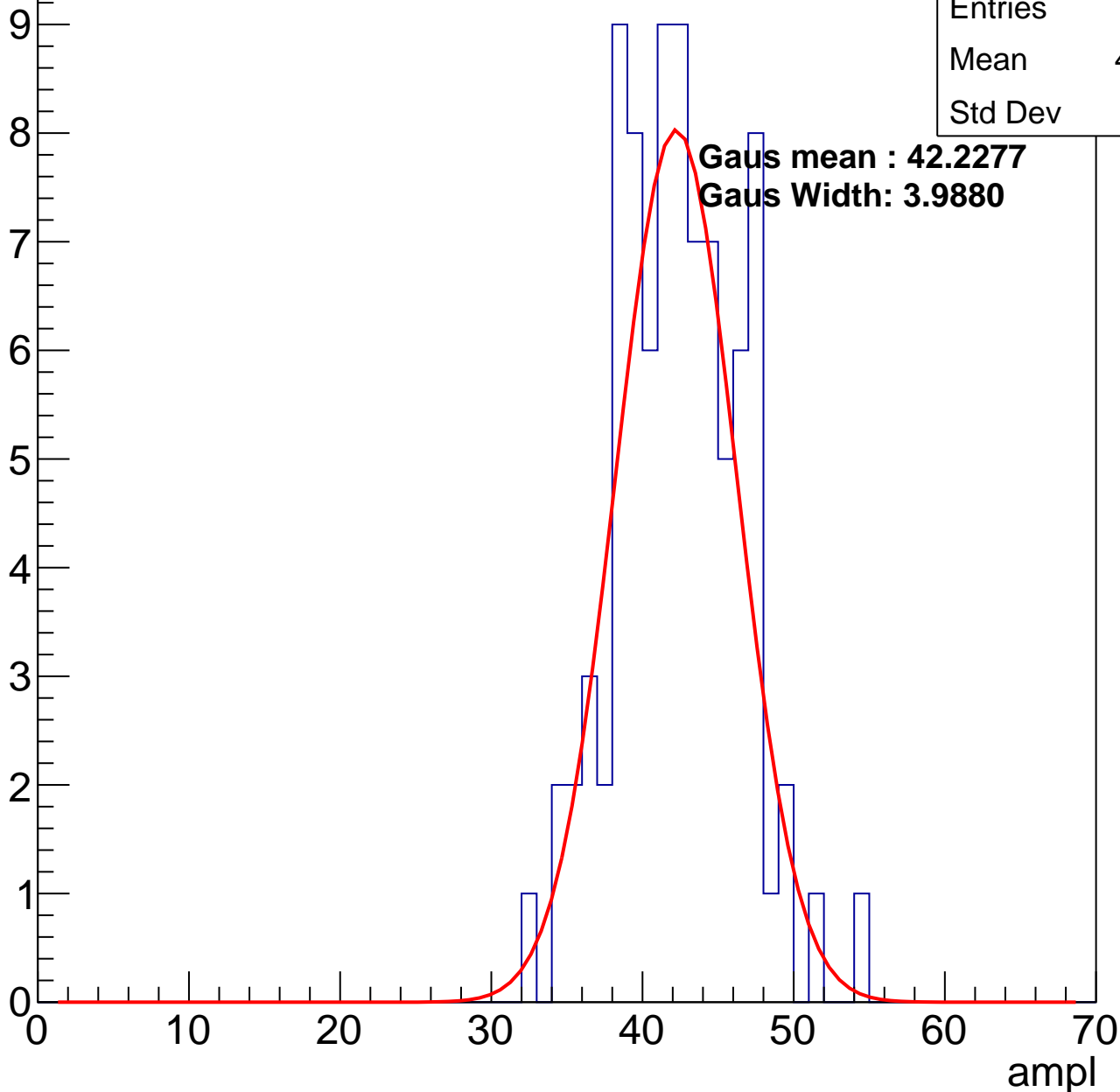
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	41.91
Std Dev	4.06

Gaus mean : 42.2277

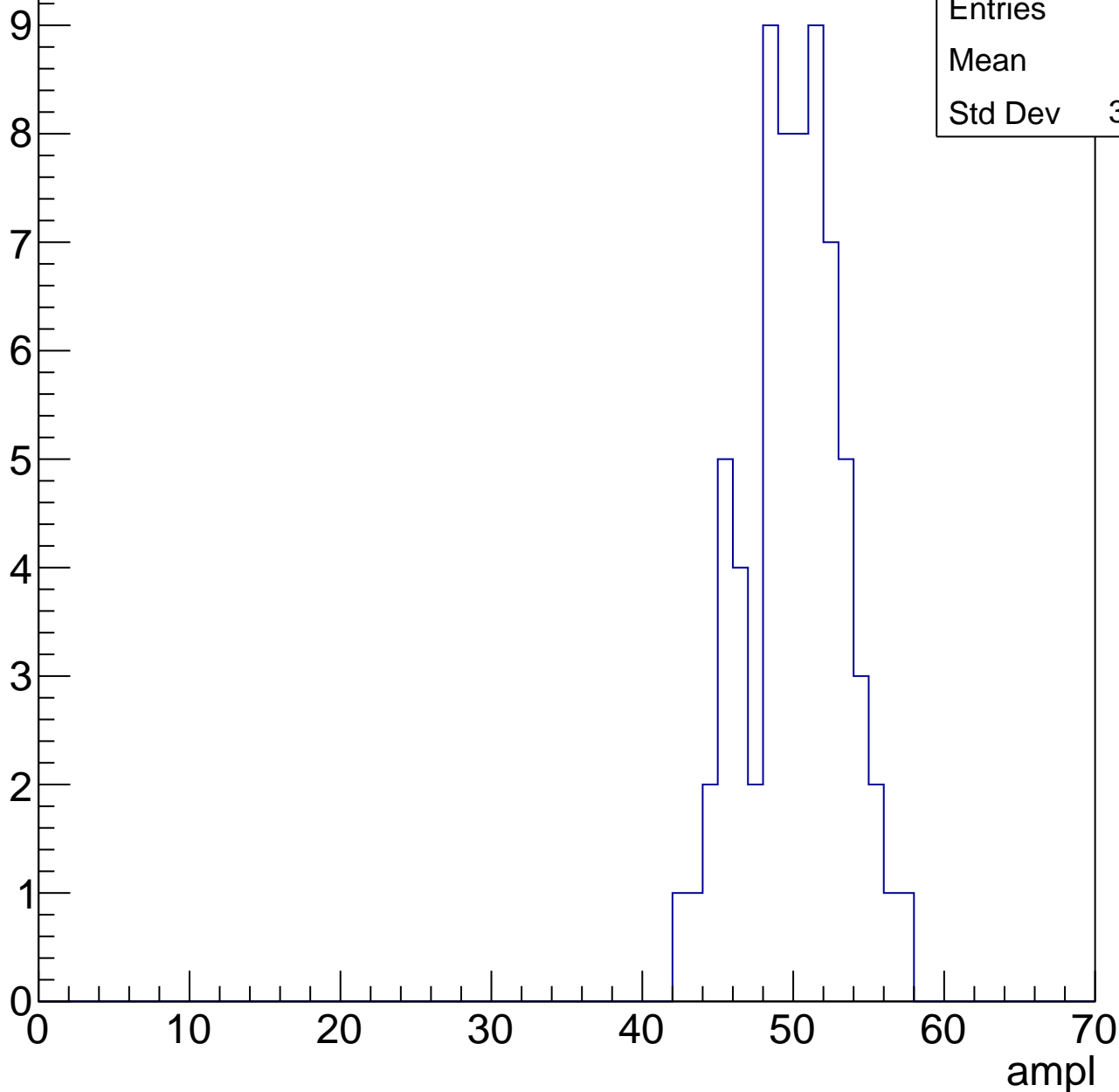
Gaus Width: 3.9880



B1L102S, U9-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



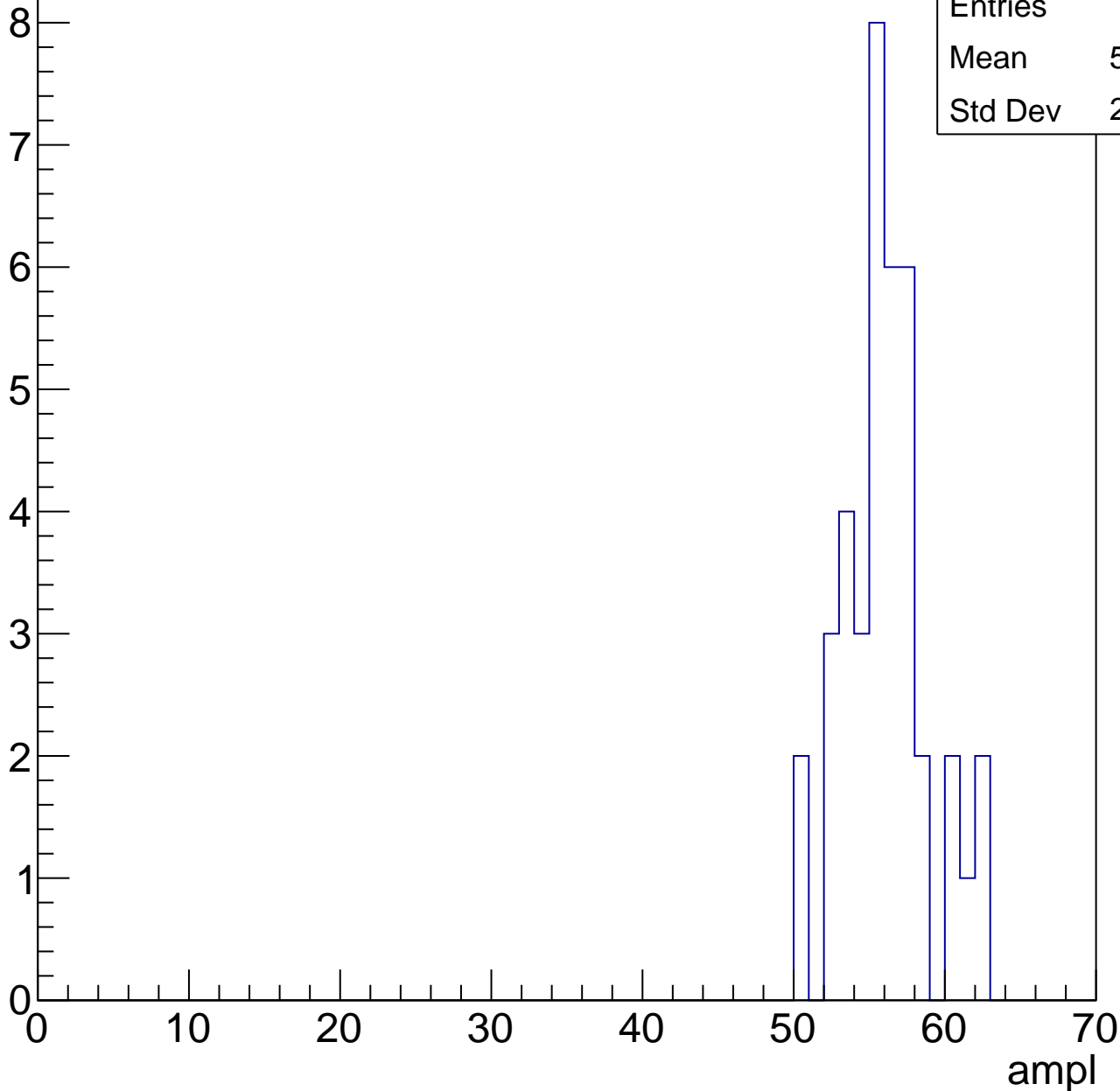
Entries	68
Mean	49.6
Std Dev	3.186

B1L102S, U9-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	55.62
Std Dev	2.834

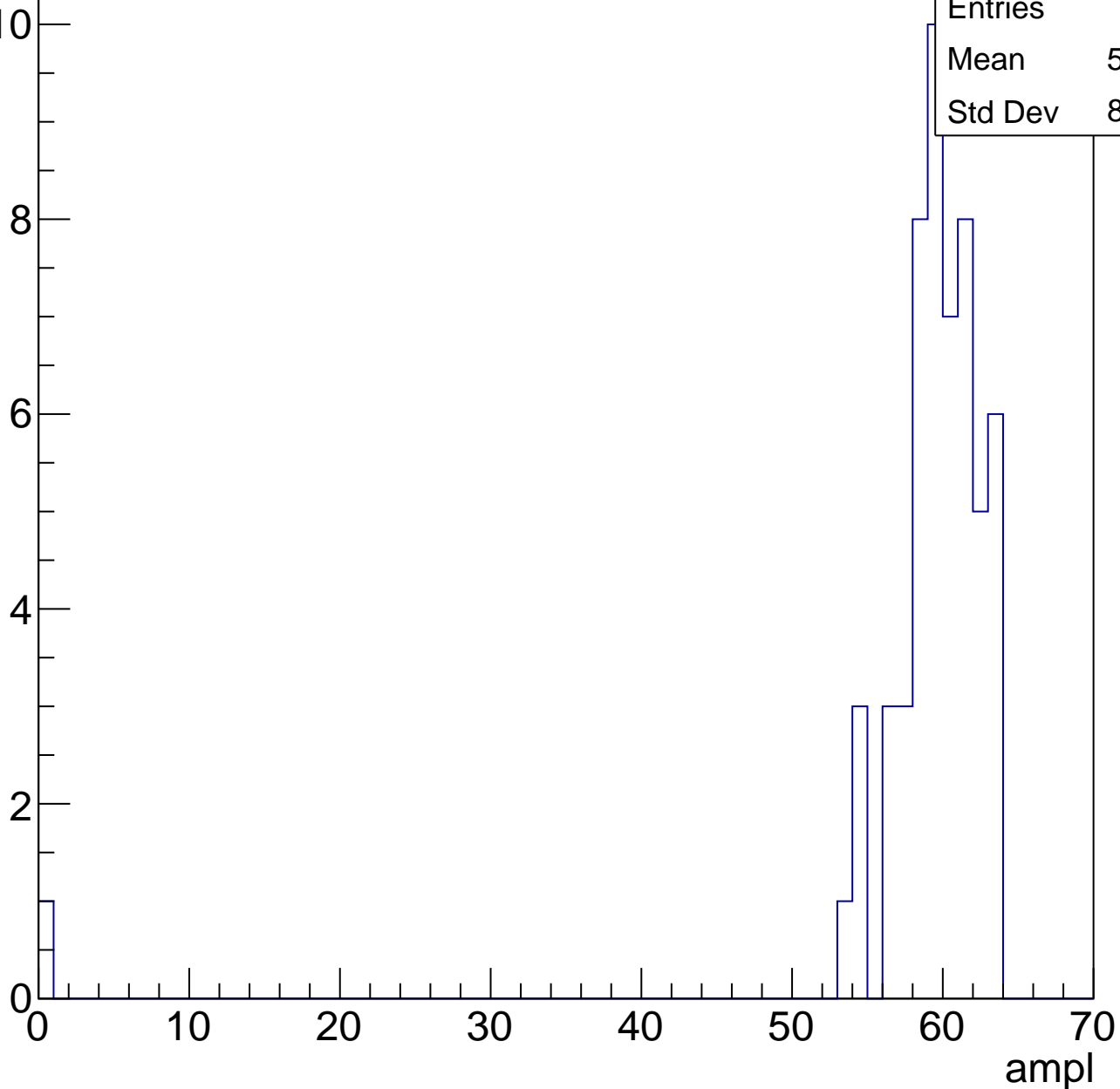


B1L102S, U9-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

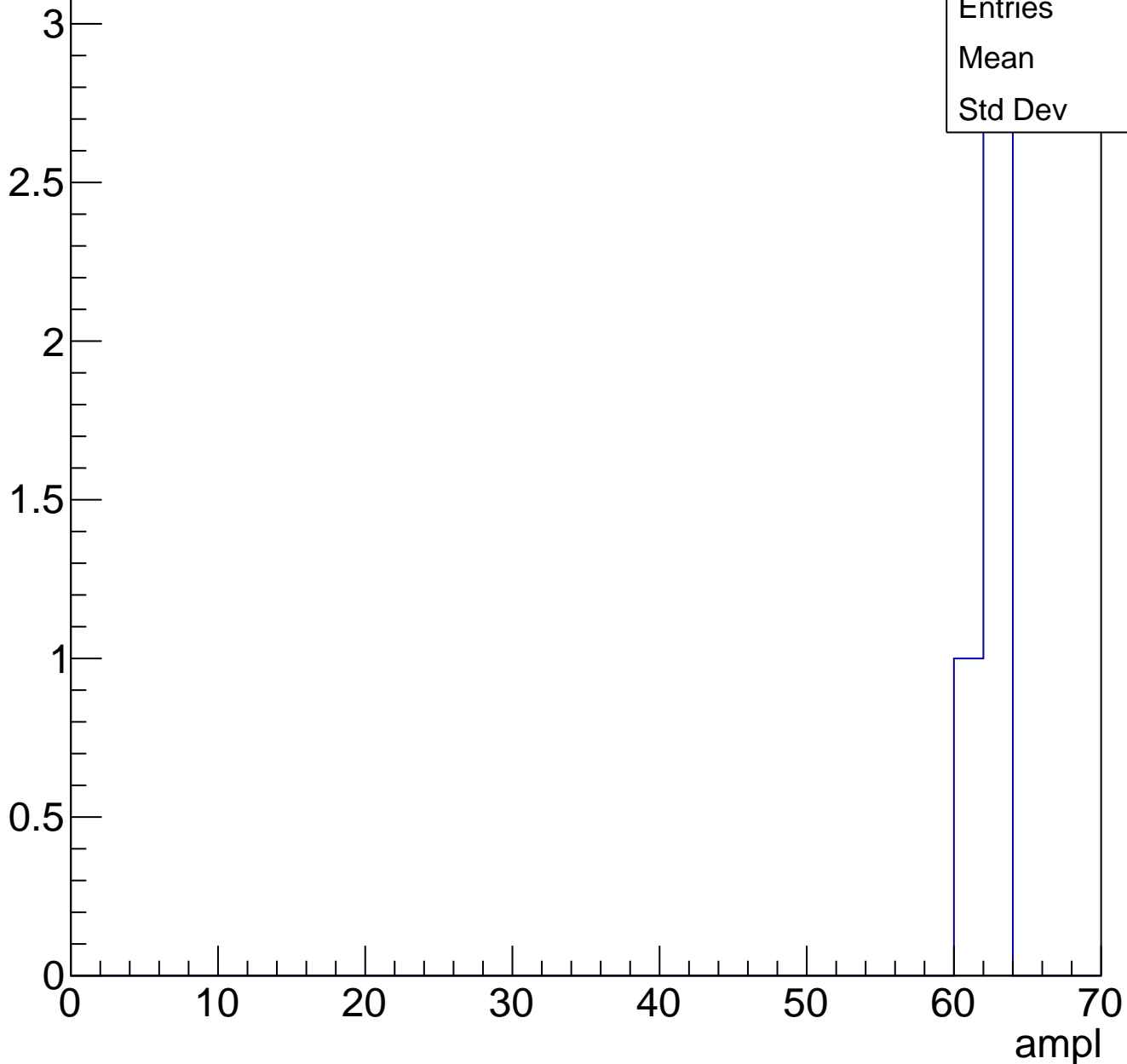
Entries	55
Mean	58.25
Std Dev	8.299



B1L102S, U9-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	8
Mean	62
Std Dev	1

B1L102S, U9-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U9-ch75, adc0

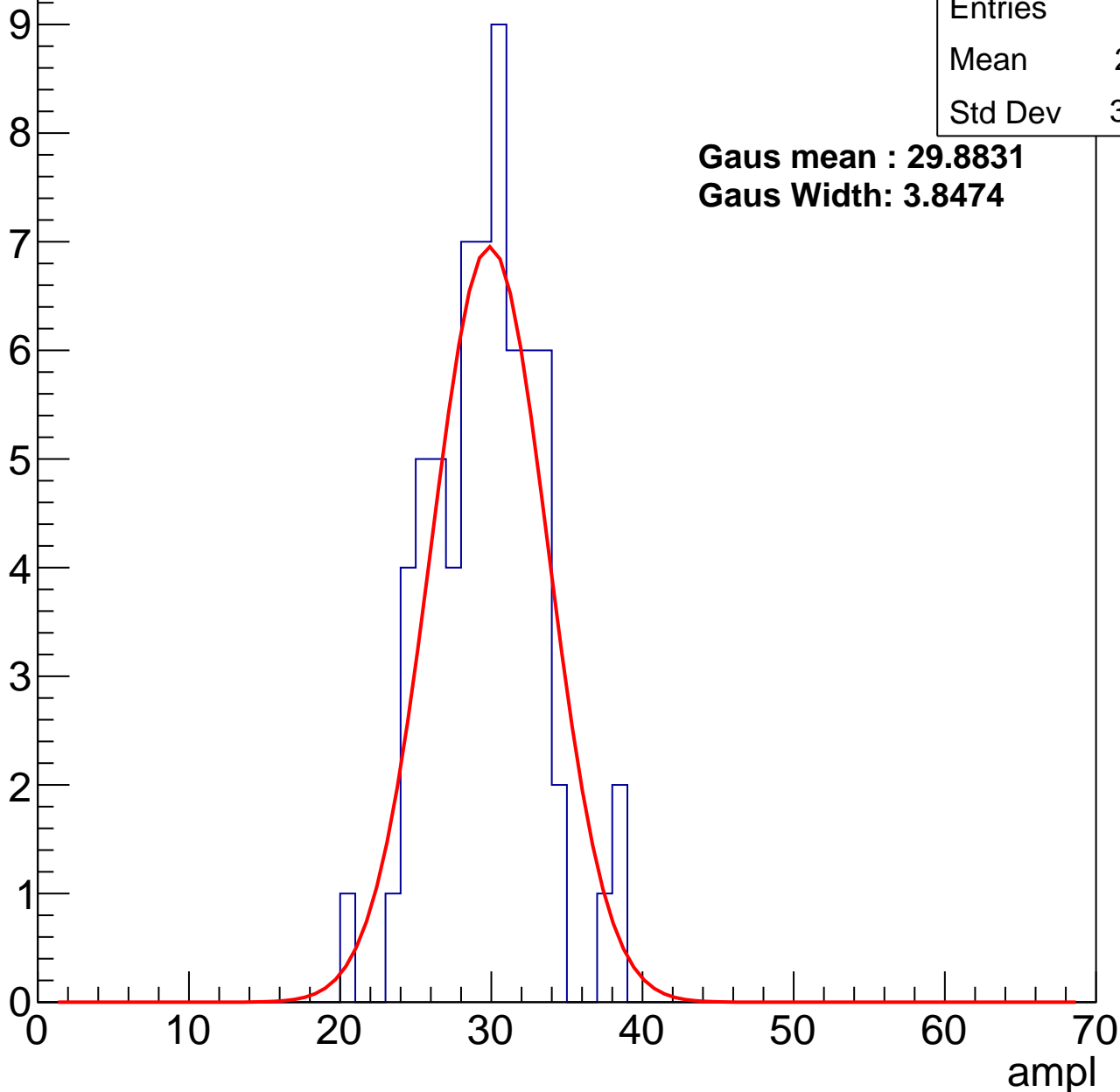
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.21
Std Dev	3.527

Gaus mean : 29.8831

Gaus Width: 3.8474



B1L102S, U9-ch75, adc1

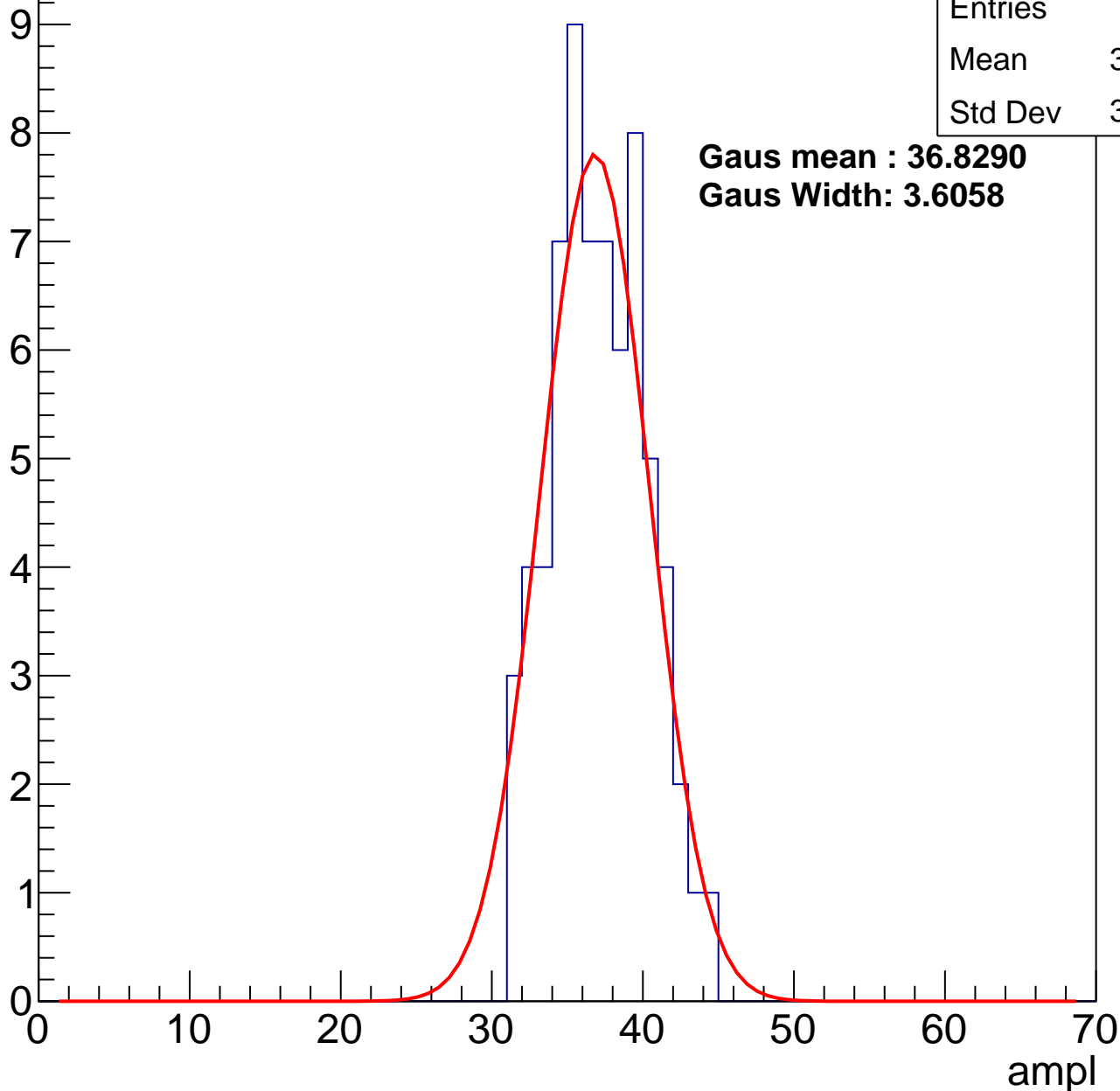
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.65
Std Dev	3.095

Gaus mean : 36.8290

Gaus Width: 3.6058



B1L102S, U9-ch75, adc2

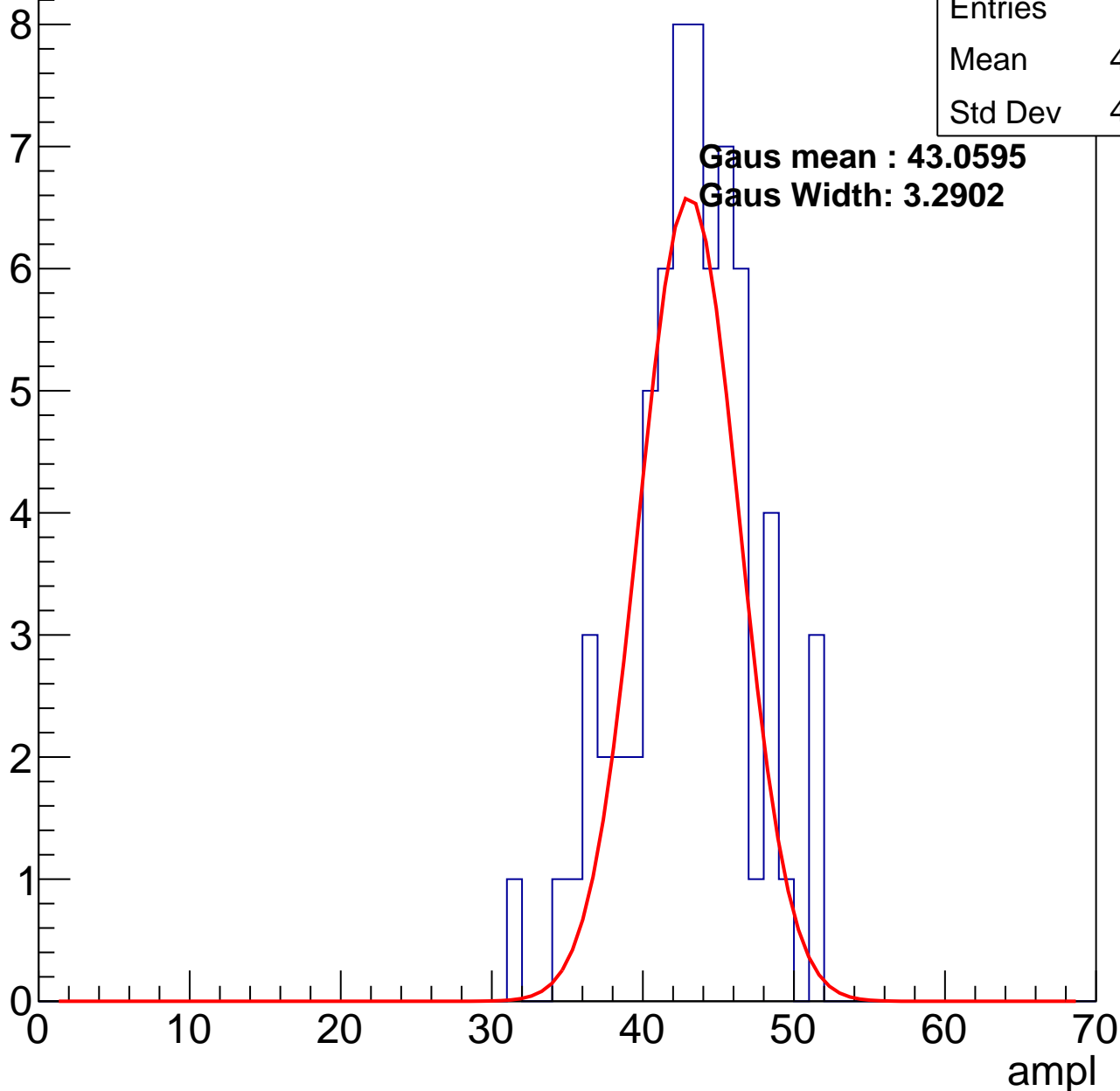
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.66
Std Dev	4.035

Gaus mean : 43.0595

Gaus Width: 3.2902

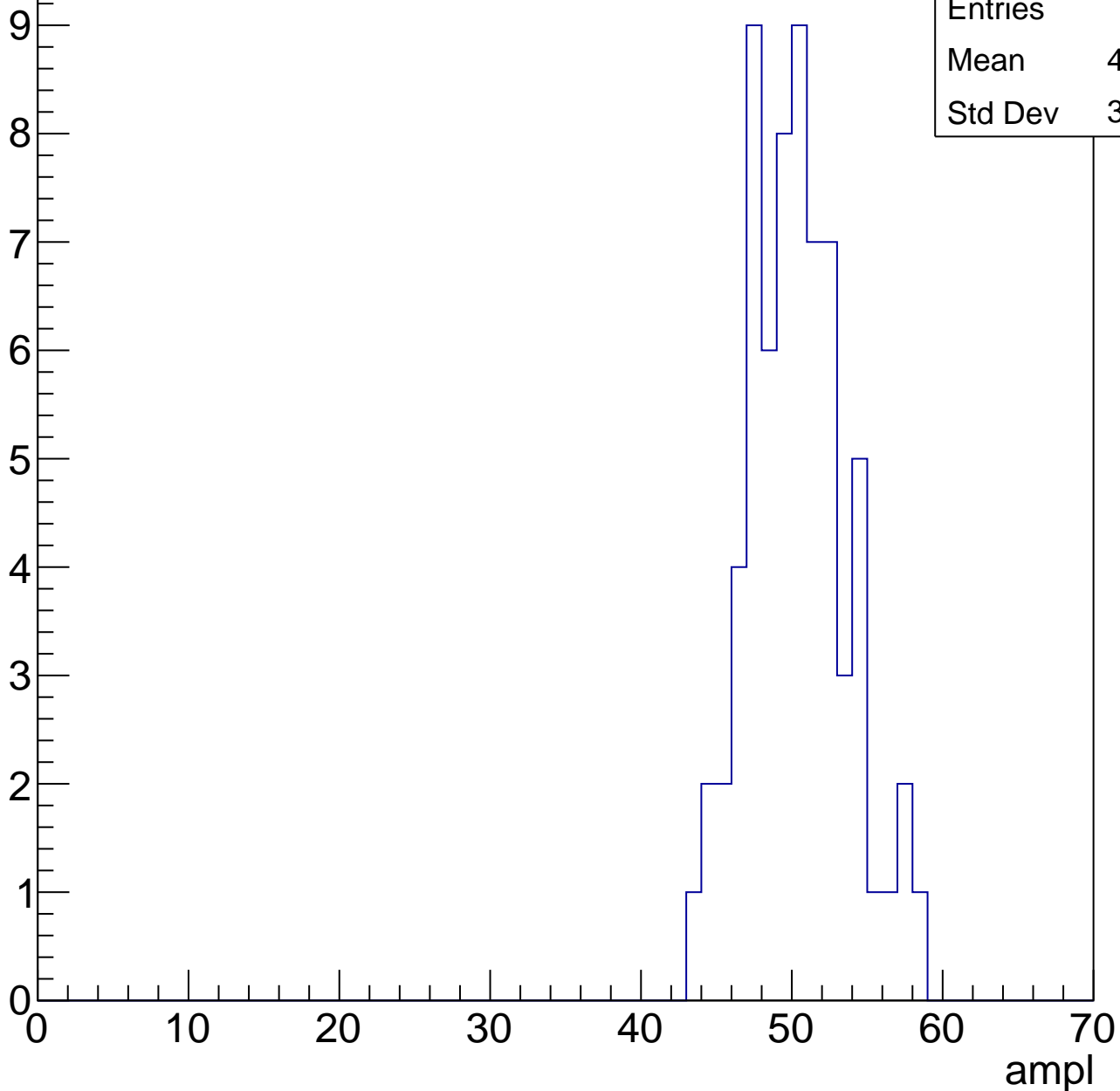


B1L102S, U9-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

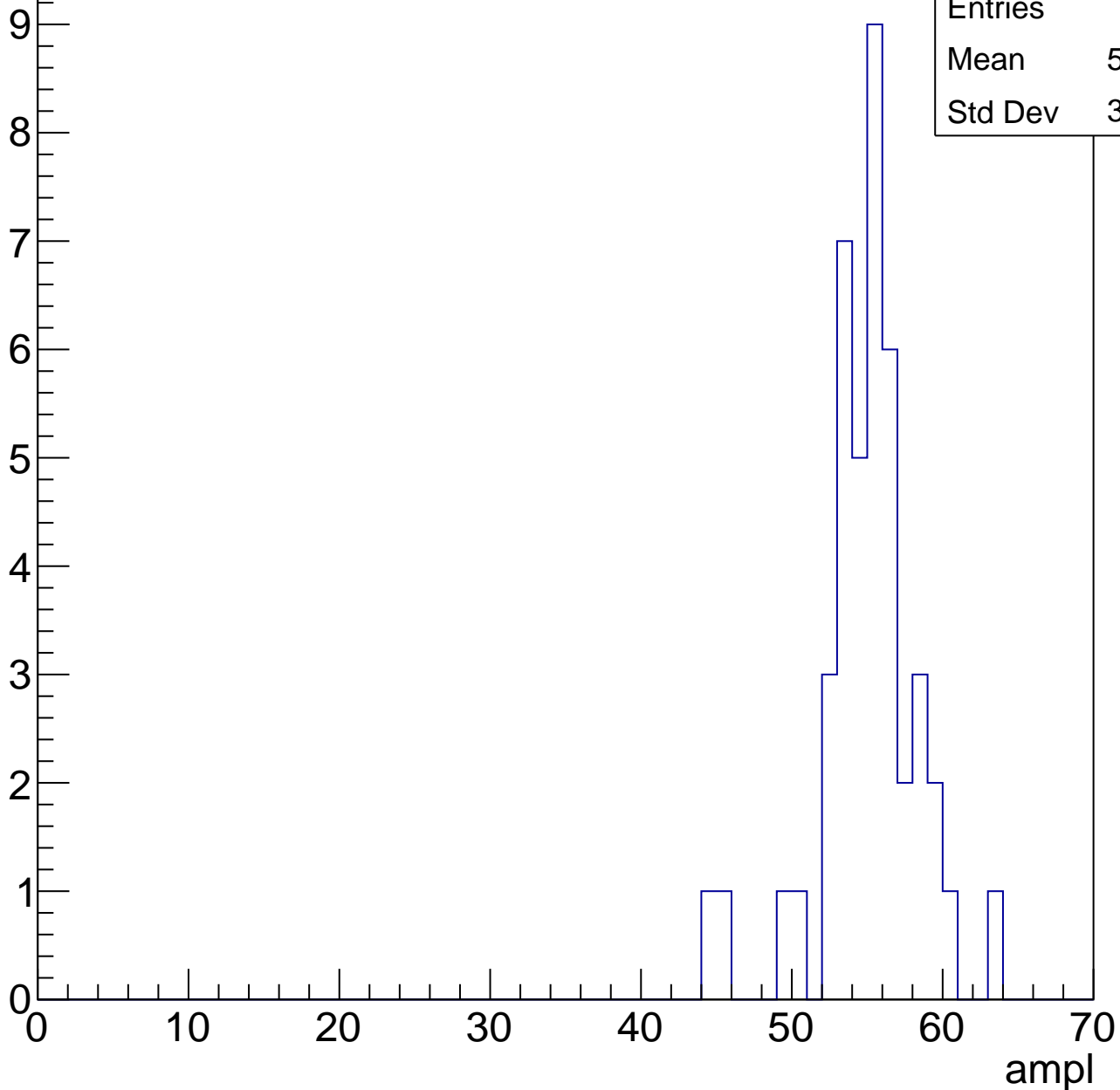
Entries	68
Mean	49.87
Std Dev	3.235



B1L102S, U9-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

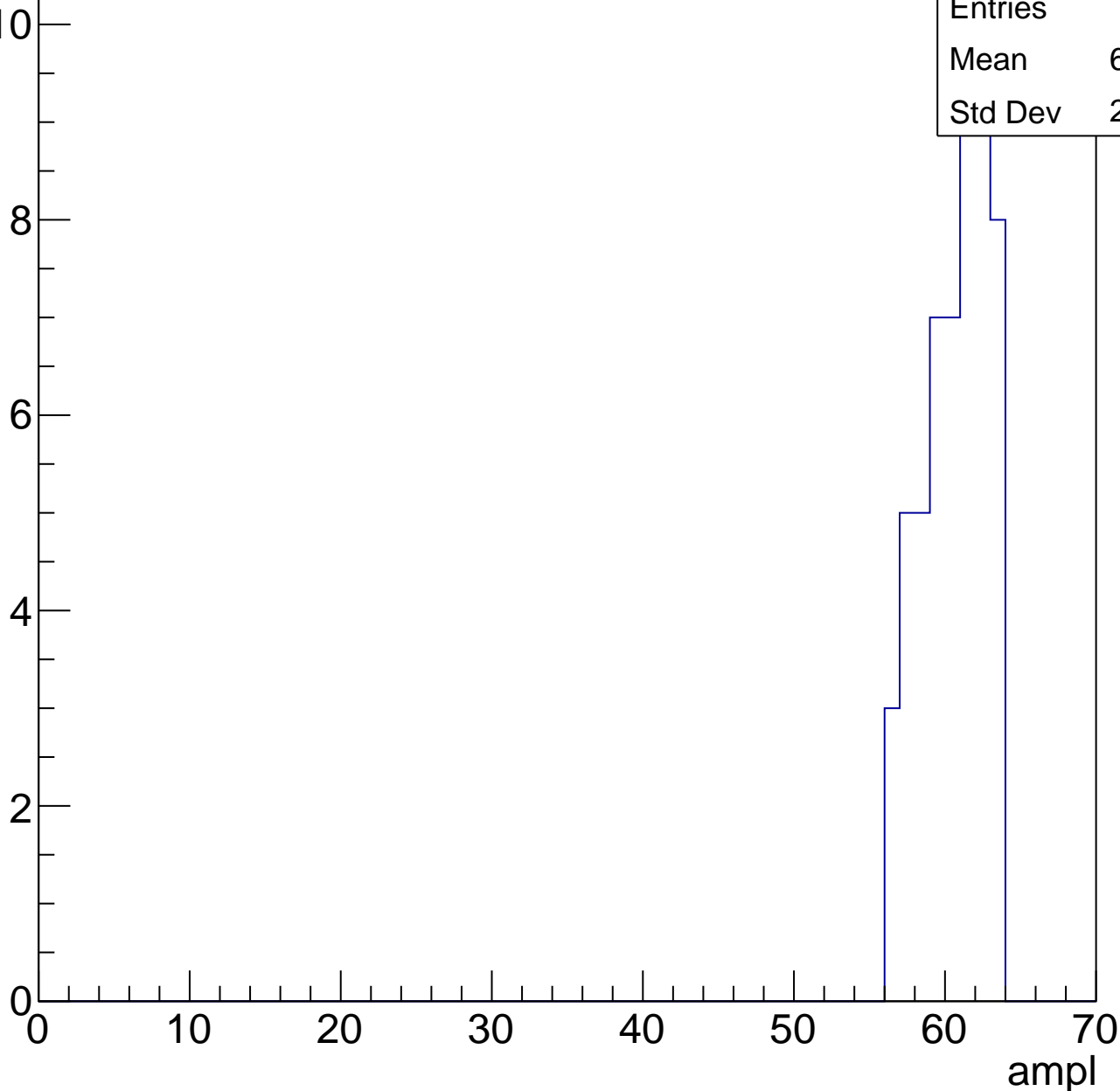


B1L102S, U9-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	60.17
Std Dev	2.106



B1L102S, U9-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	52.25
Std Dev	19.82

ampl

0 10 20 30 40 50 60 70

B1L102S, U9-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch76, adc0

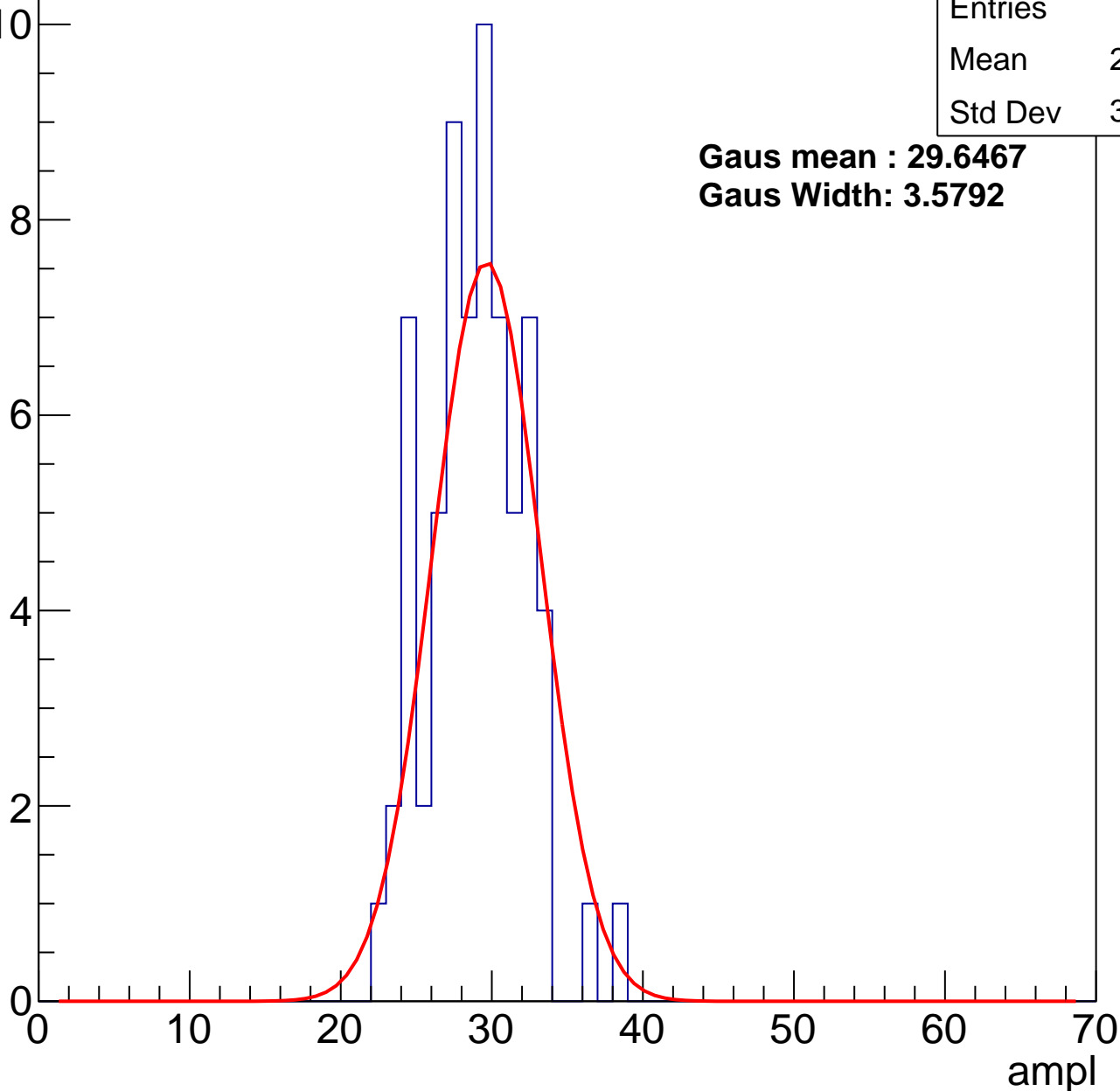
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.53
Std Dev	3.174

Gaus mean : 29.6467

Gaus Width: 3.5792



B1L102S, U9-ch76, adc1

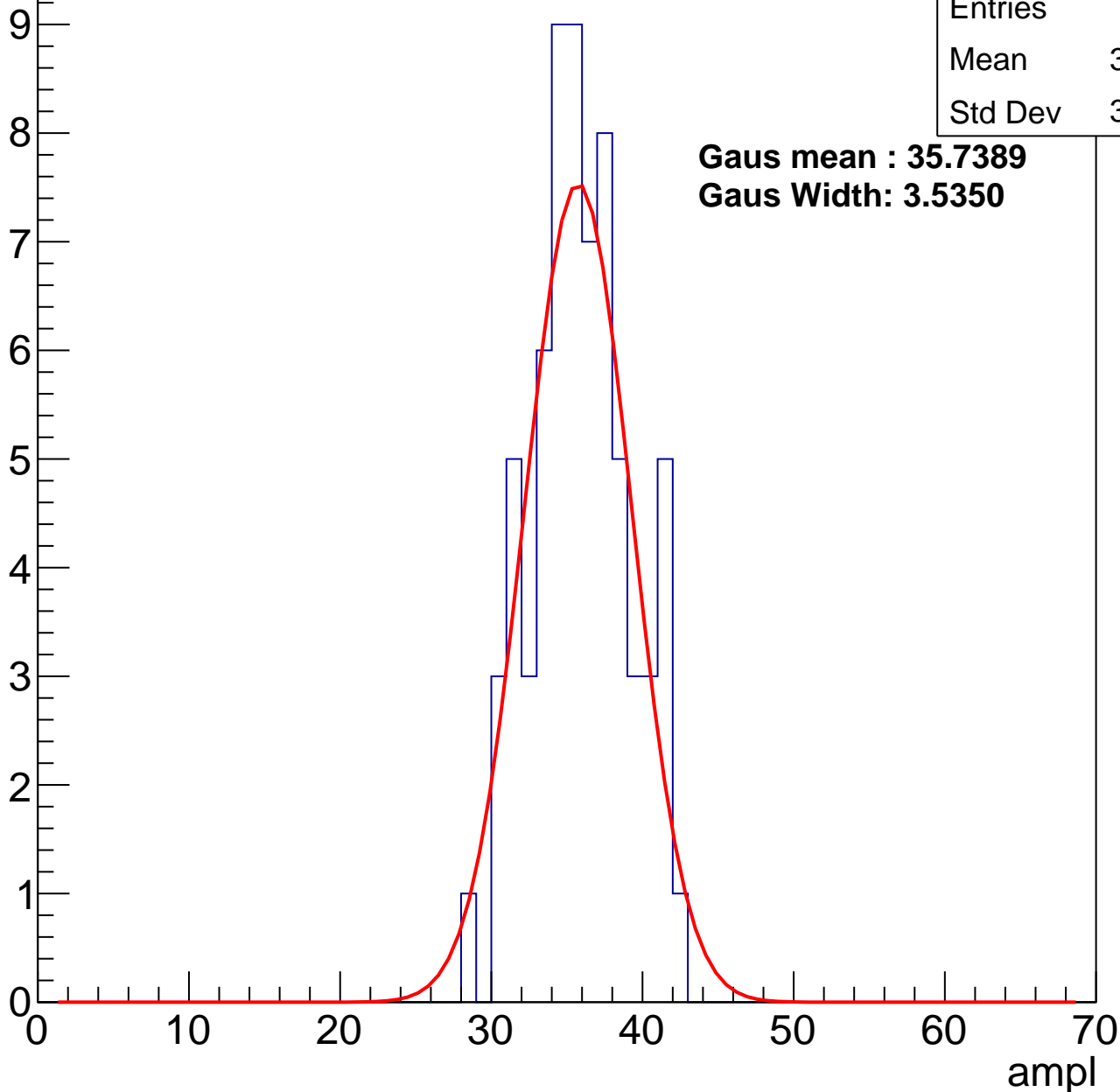
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.44
Std Dev	3.178

Gaus mean : 35.7389

Gaus Width: 3.5350



B1L102S, U9-ch76, adc2

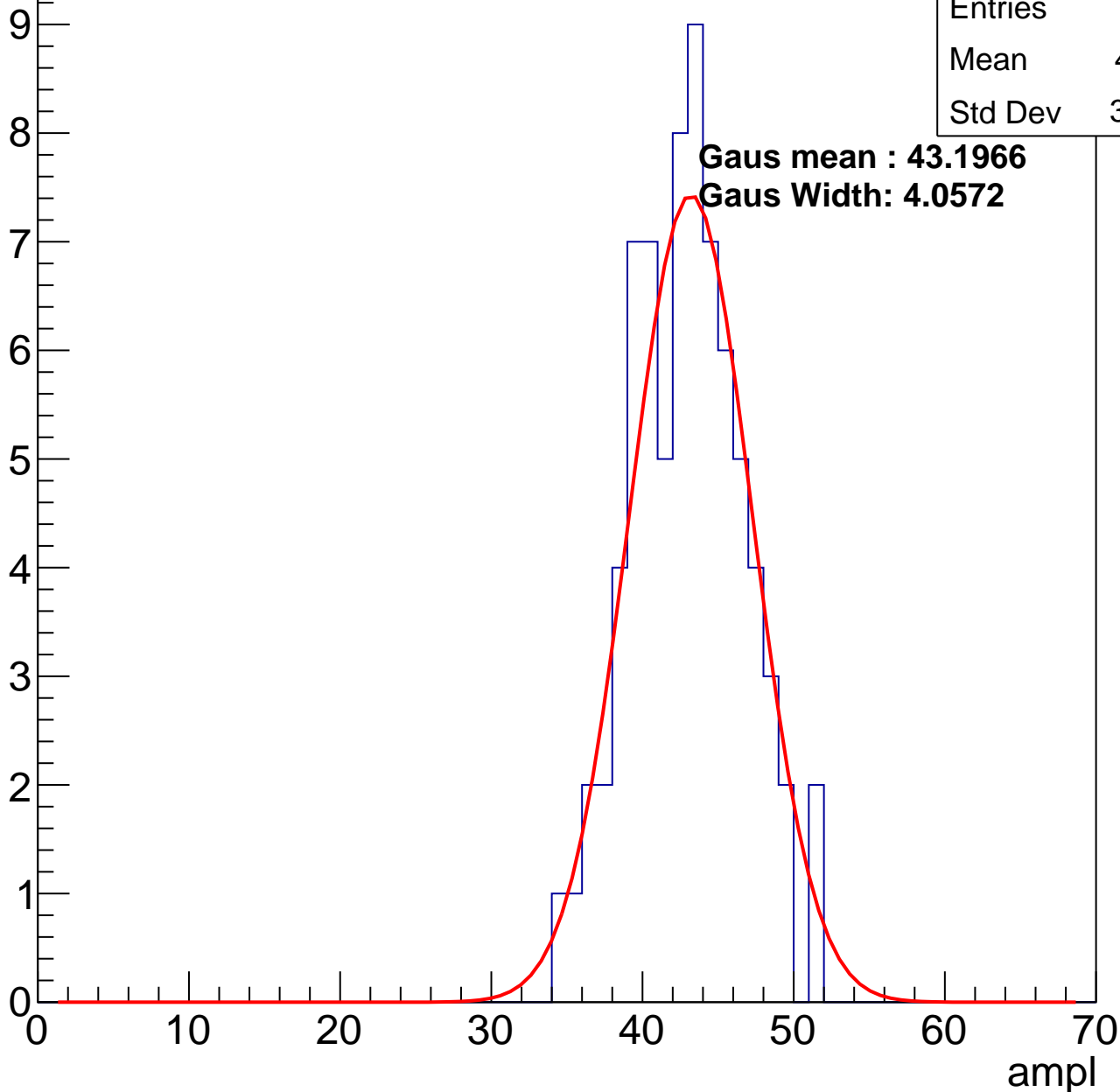
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	42.51
Std Dev	3.678

Gaus mean : 43.1966

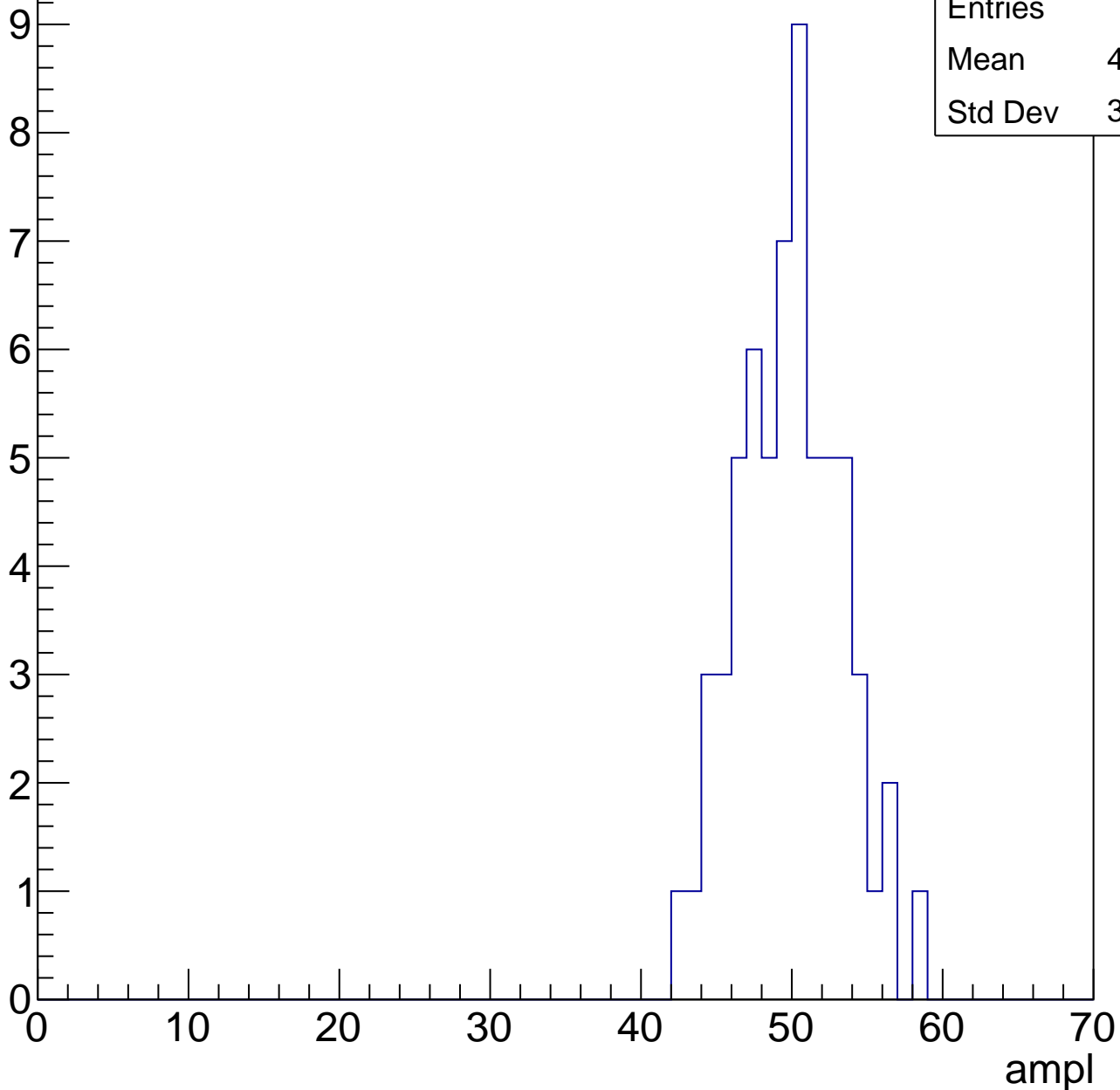
Gaus Width: 4.0572



B1L102S, U9-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

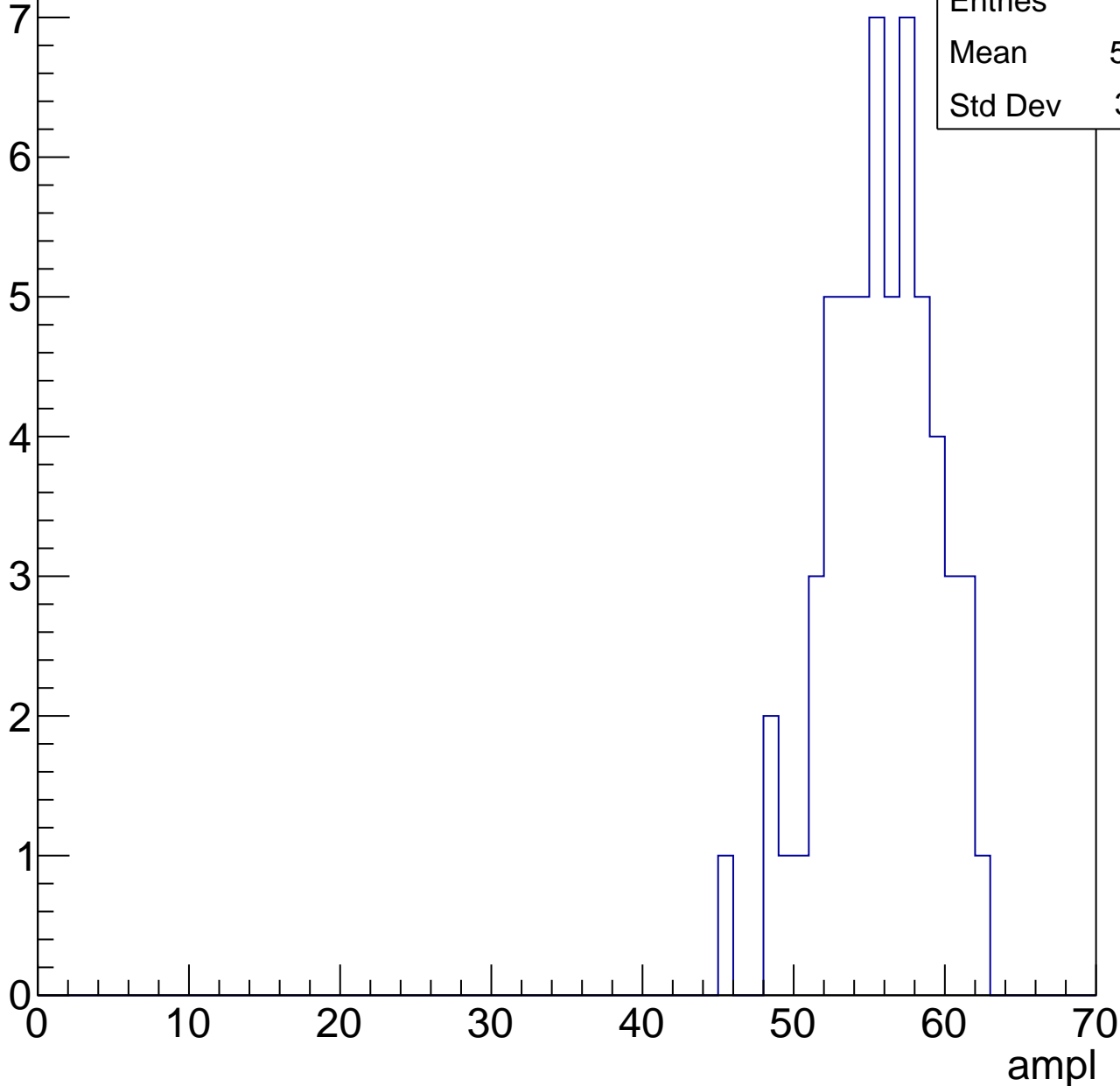


B1L102S, U9-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.22
Std Dev	3.591



B1L102S, U9-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

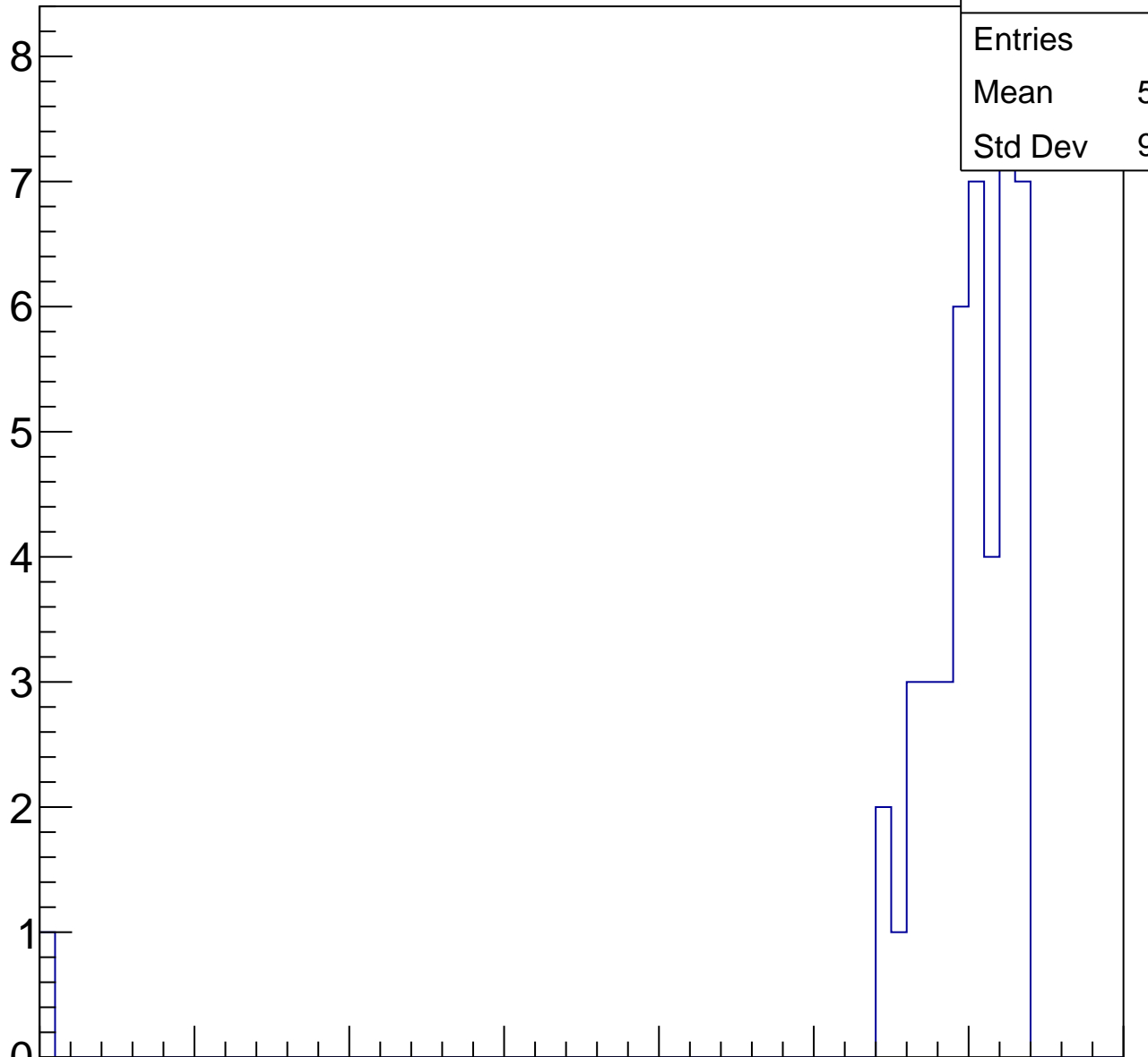
Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.47
Std Dev	9.169

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	60.5
Std Dev	2.55

ampl

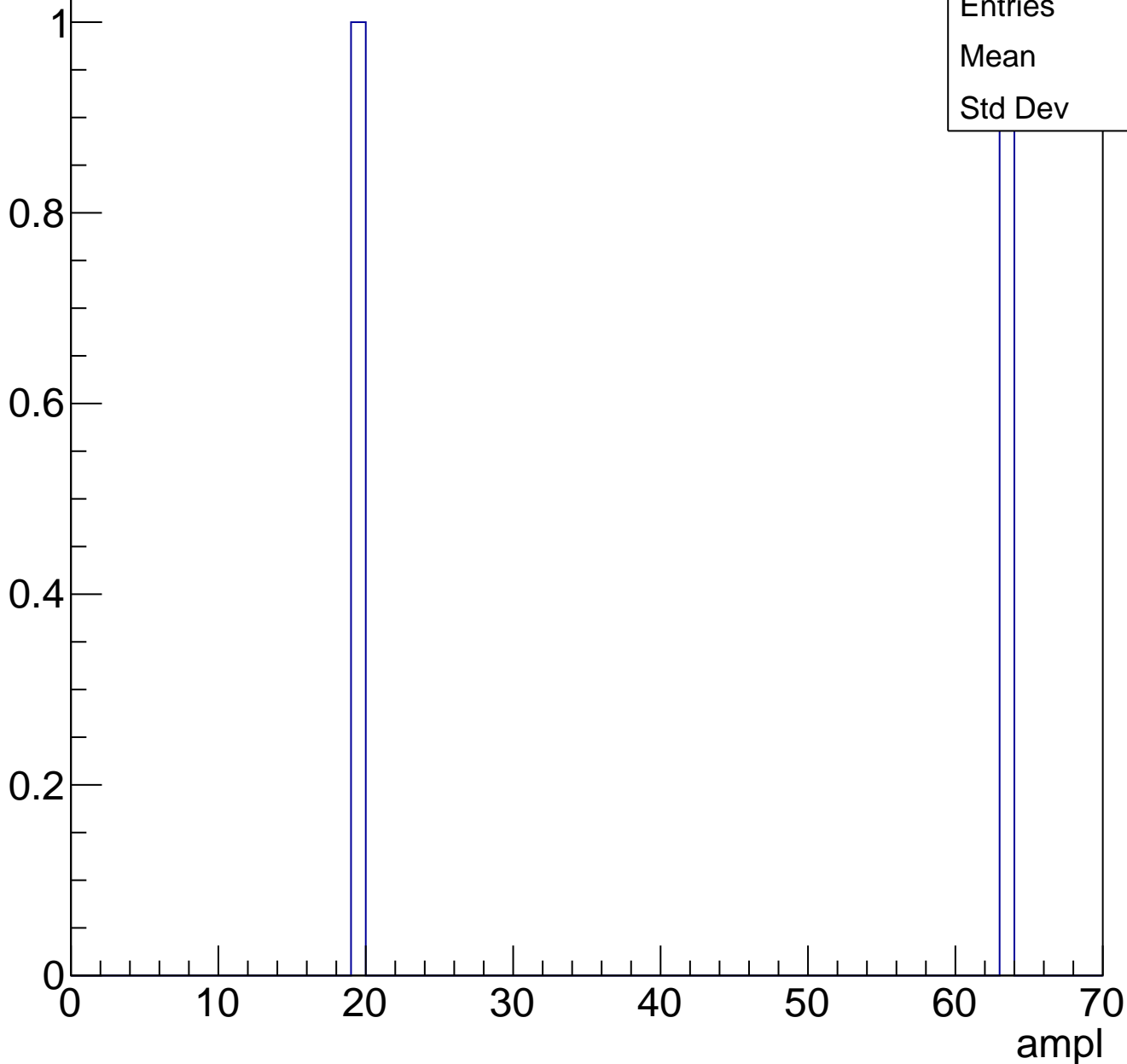
0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B1L102S, U9-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch77, adc0

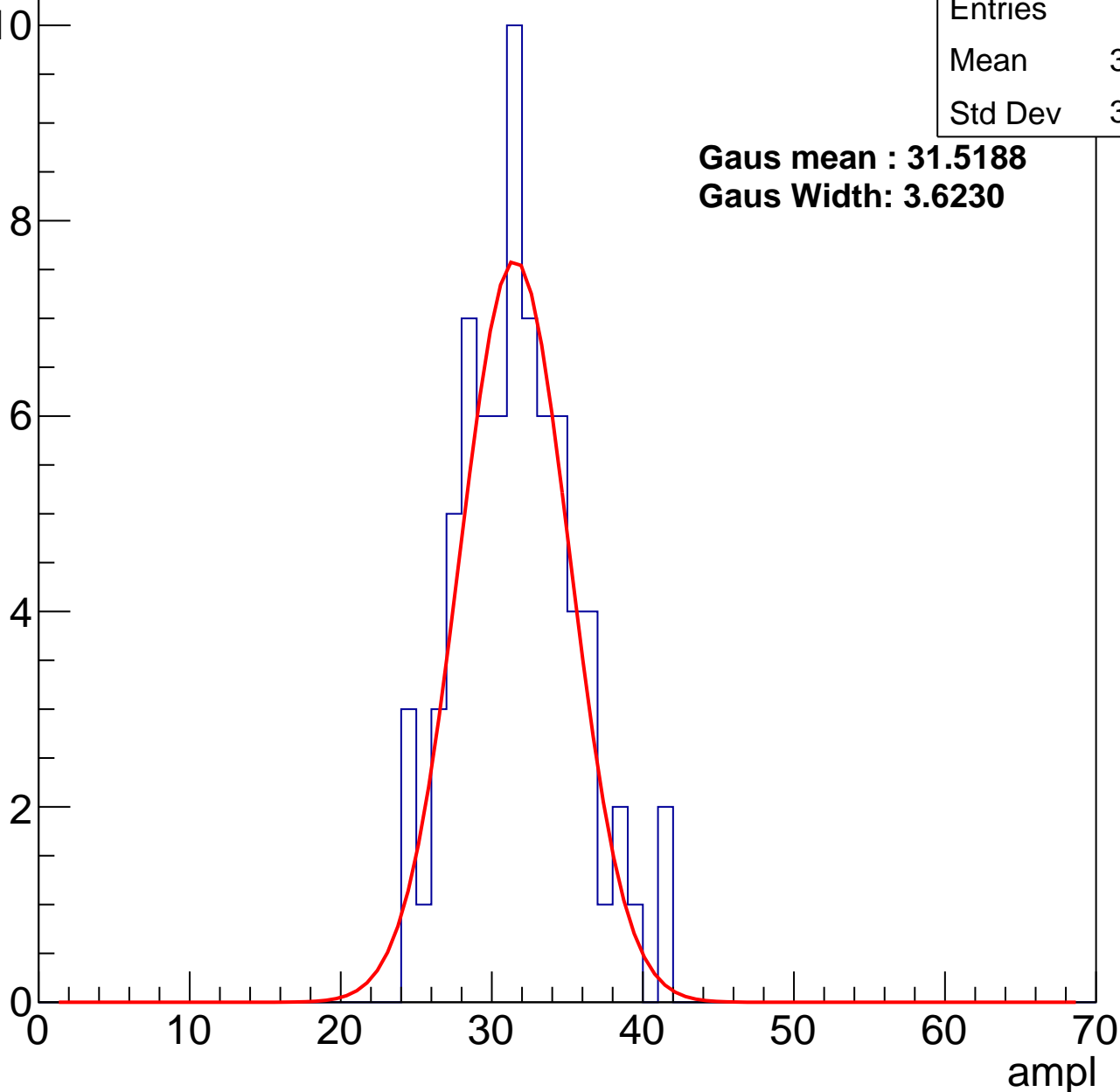
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	31.27
Std Dev	3.807

Gaus mean : 31.5188

Gaus Width: 3.6230



B1L102S, U9-ch77, adc1

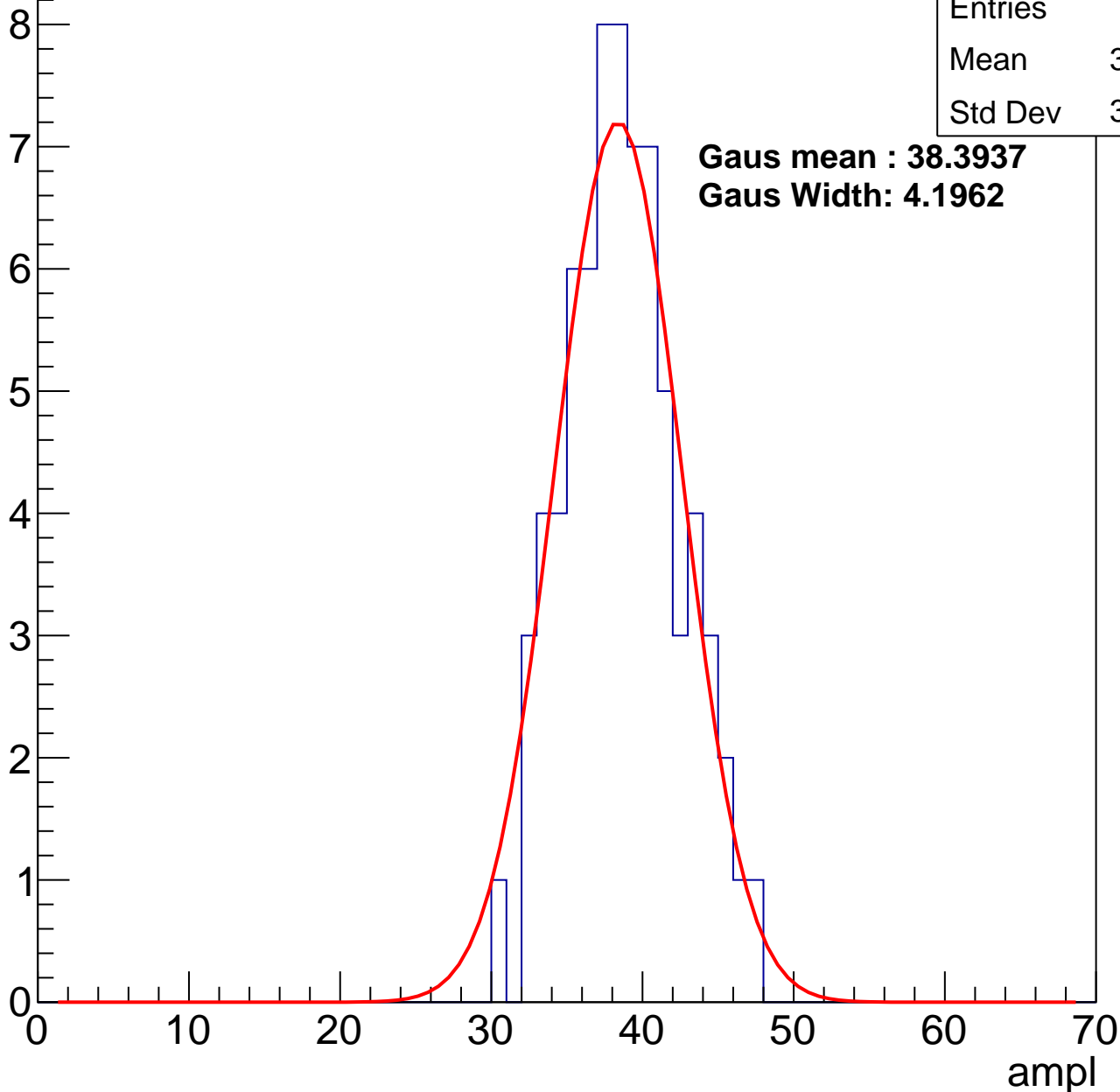
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	38.23
Std Dev	3.696

Gaus mean : 38.3937

Gaus Width: 4.1962



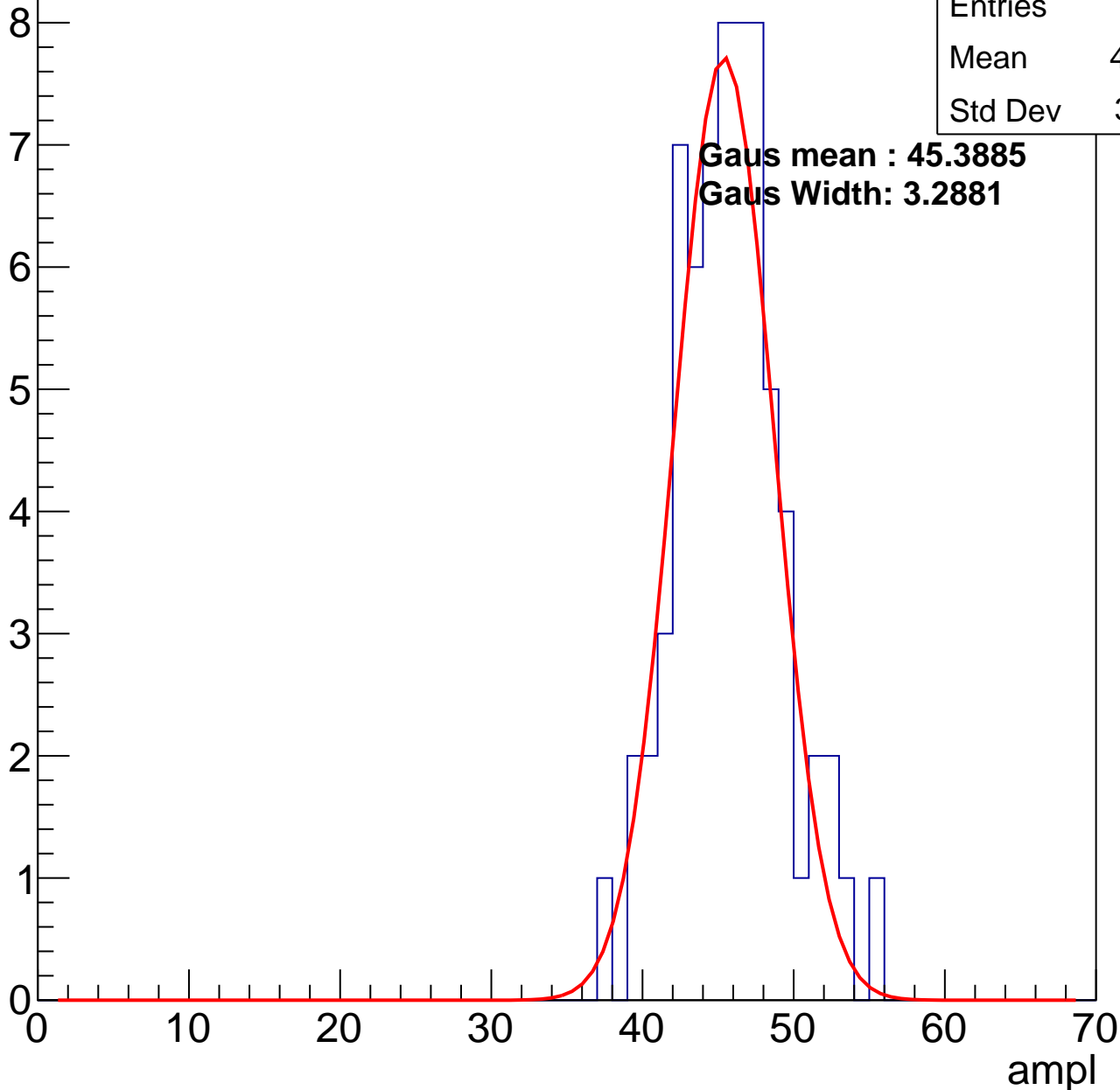
B1L102S, U9-ch77, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	45.32
Std Dev	3.491

Gaus mean : 45.3885
Gaus Width: 3.2881

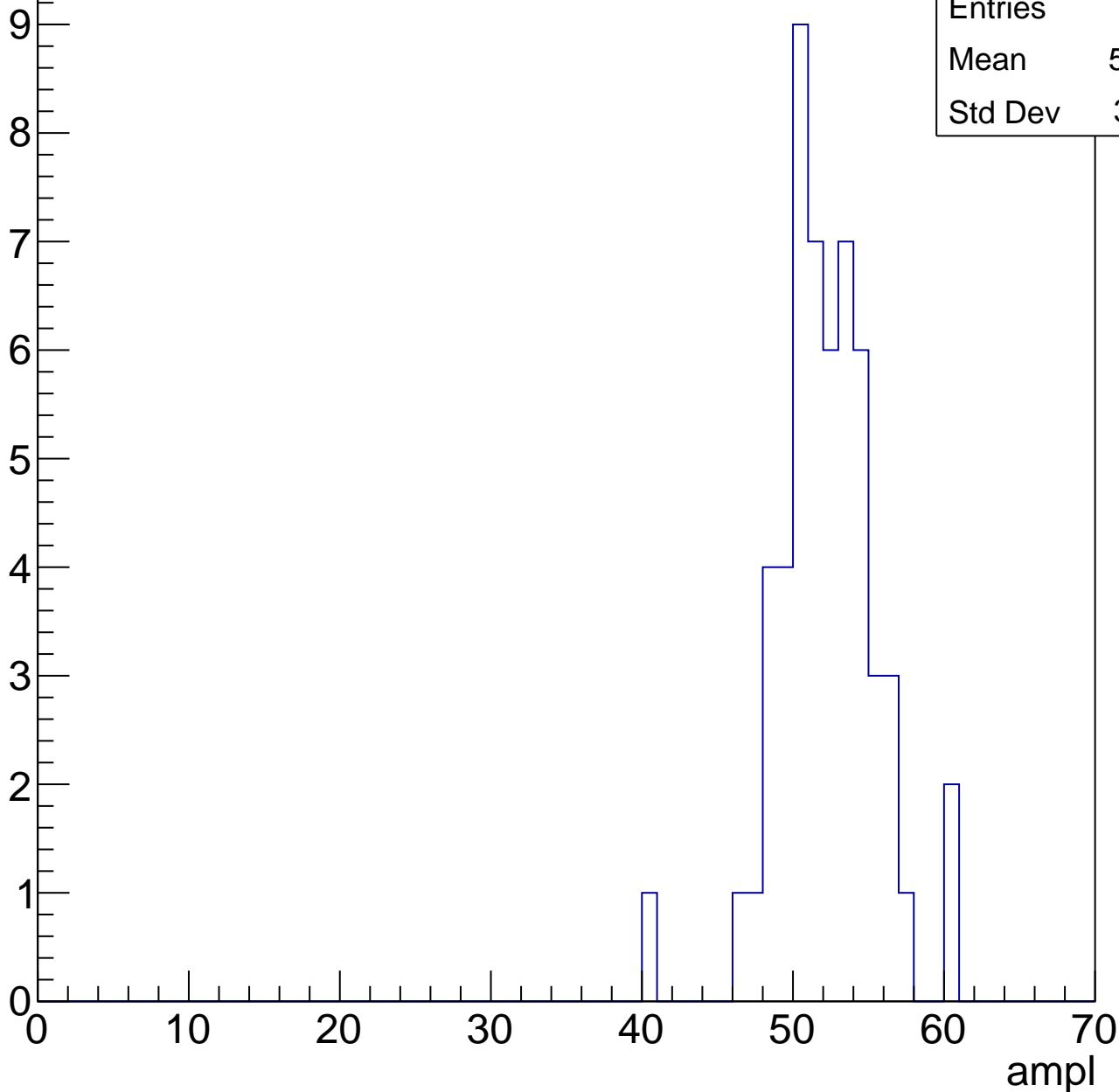


B1L102S, U9-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	51.73
Std Dev	3.311

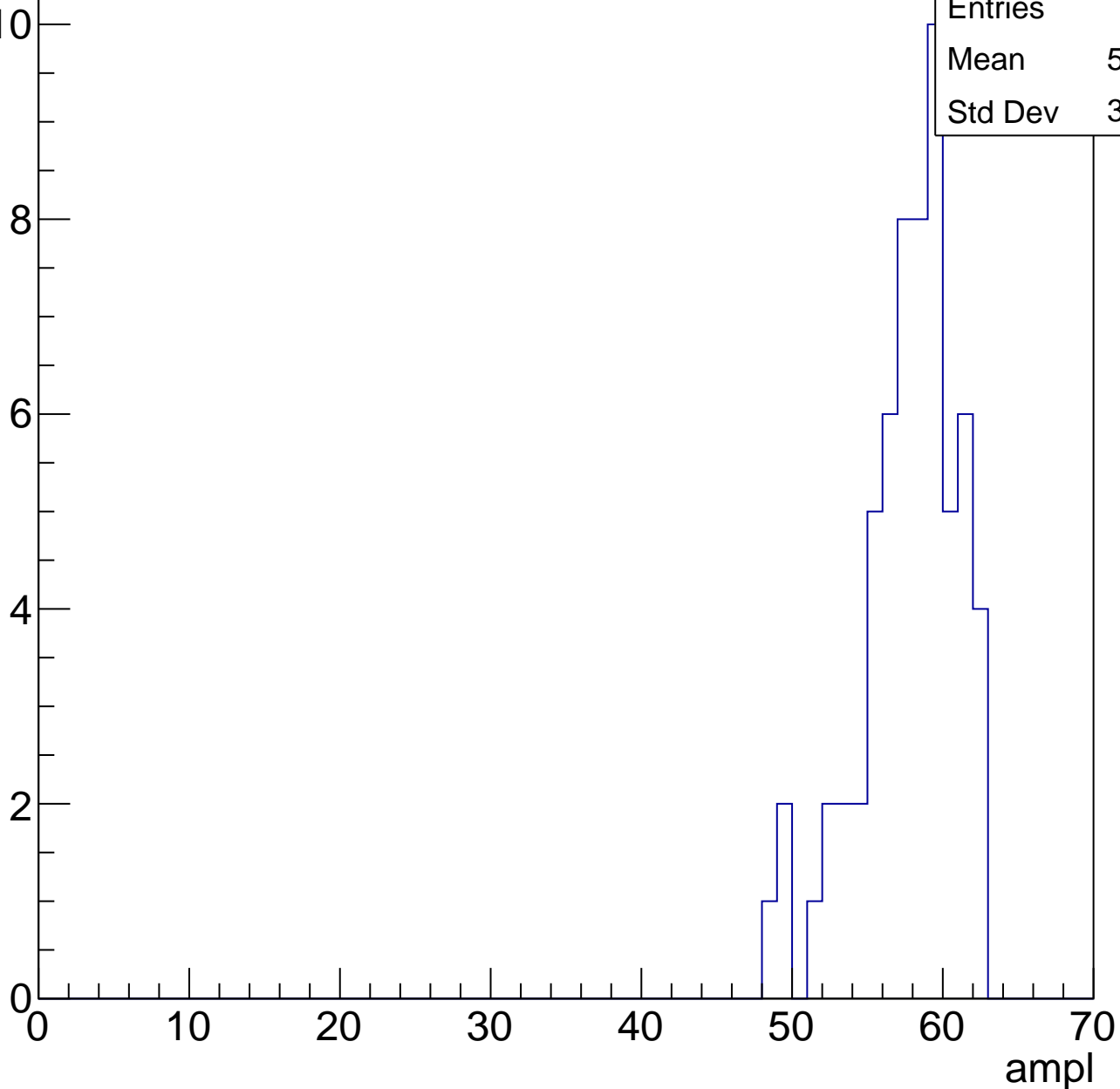


B1L102S, U9-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	57.26
Std Dev	3.252

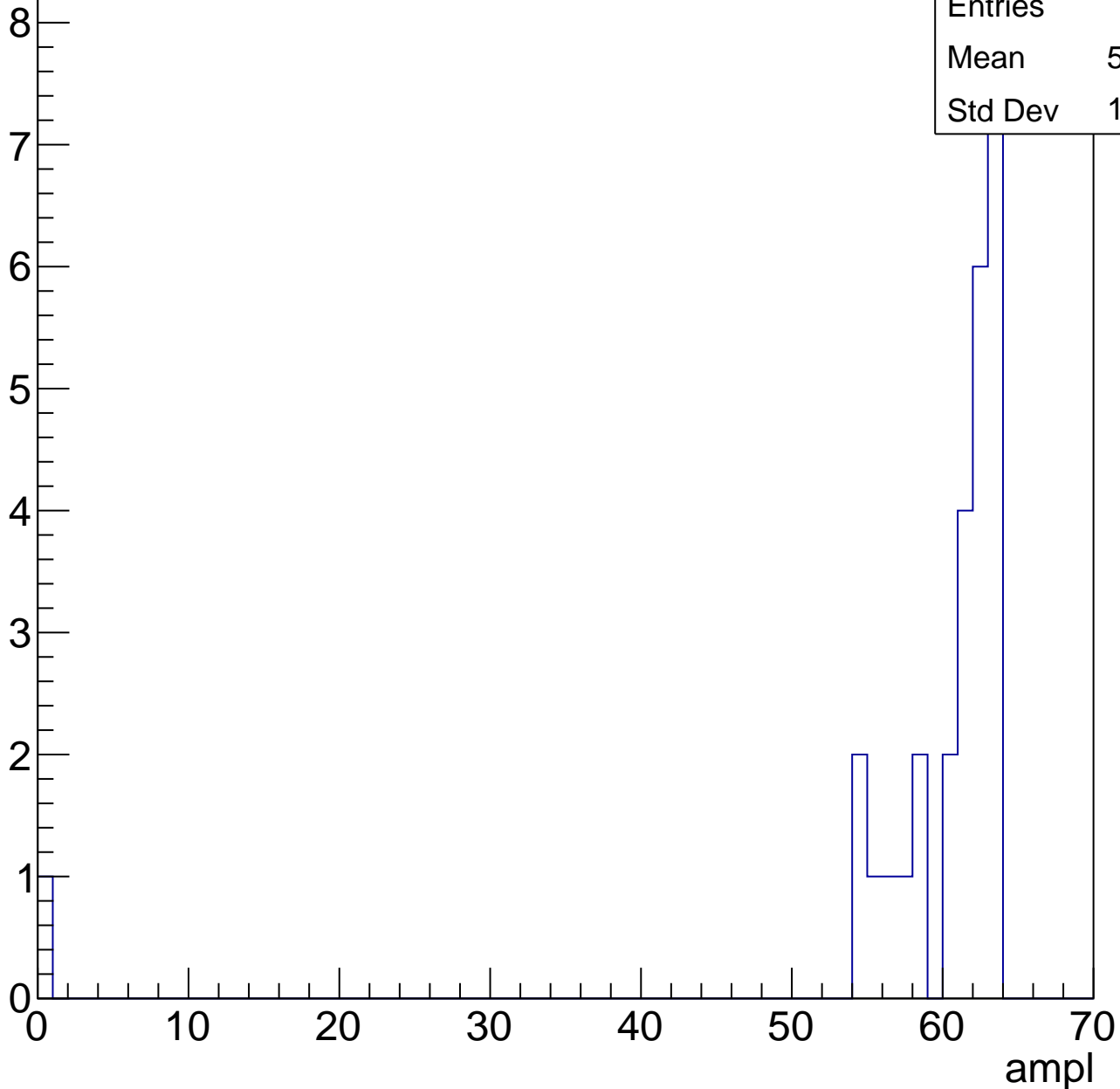


B1L102S, U9-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	58.29
Std Dev	11.57



B1L102S, U9-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch78, adc0

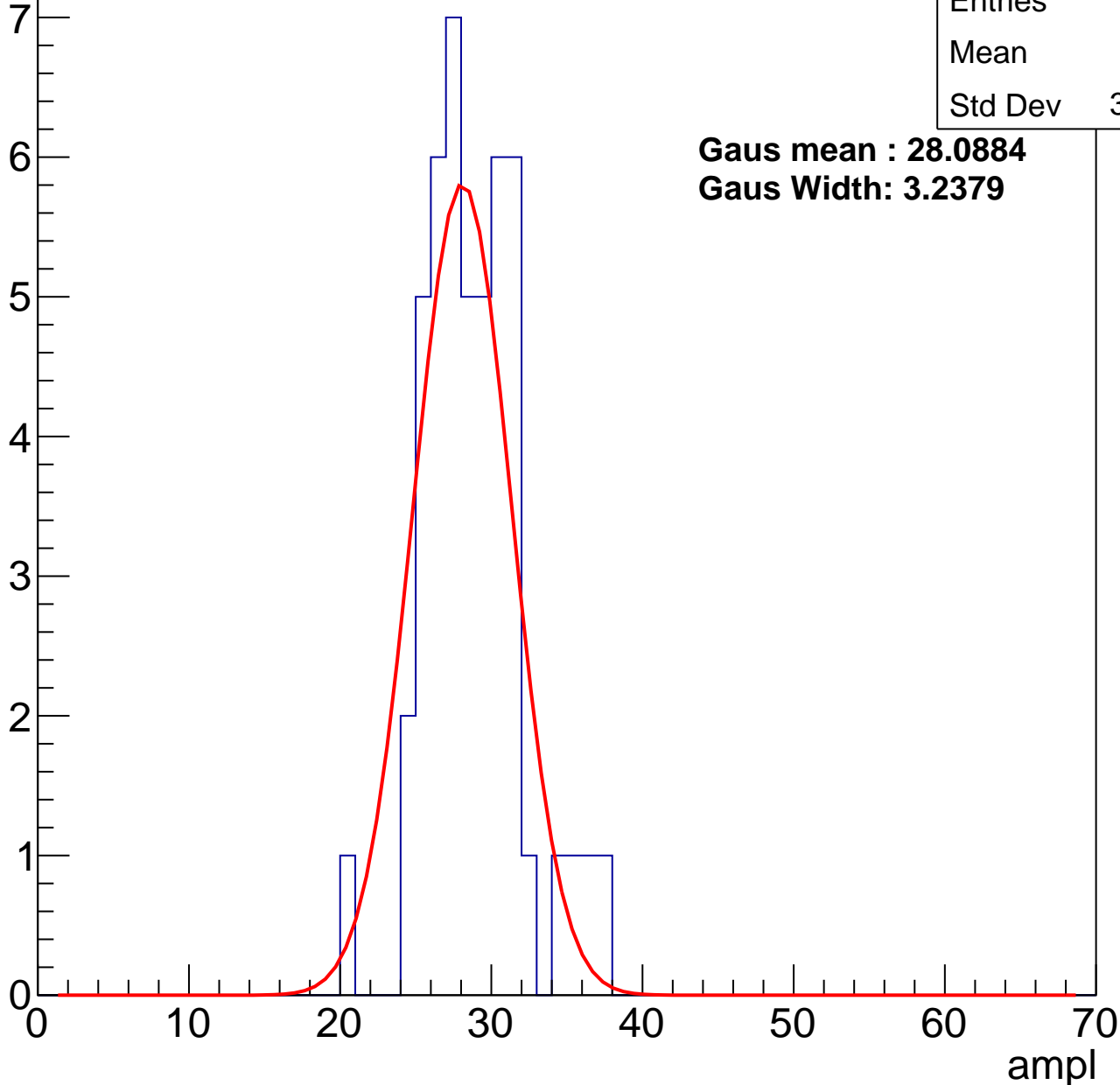
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	28.4
Std Dev	3.206

Gaus mean : 28.0884

Gaus Width: 3.2379



B1L102S, U9-ch78, adc1

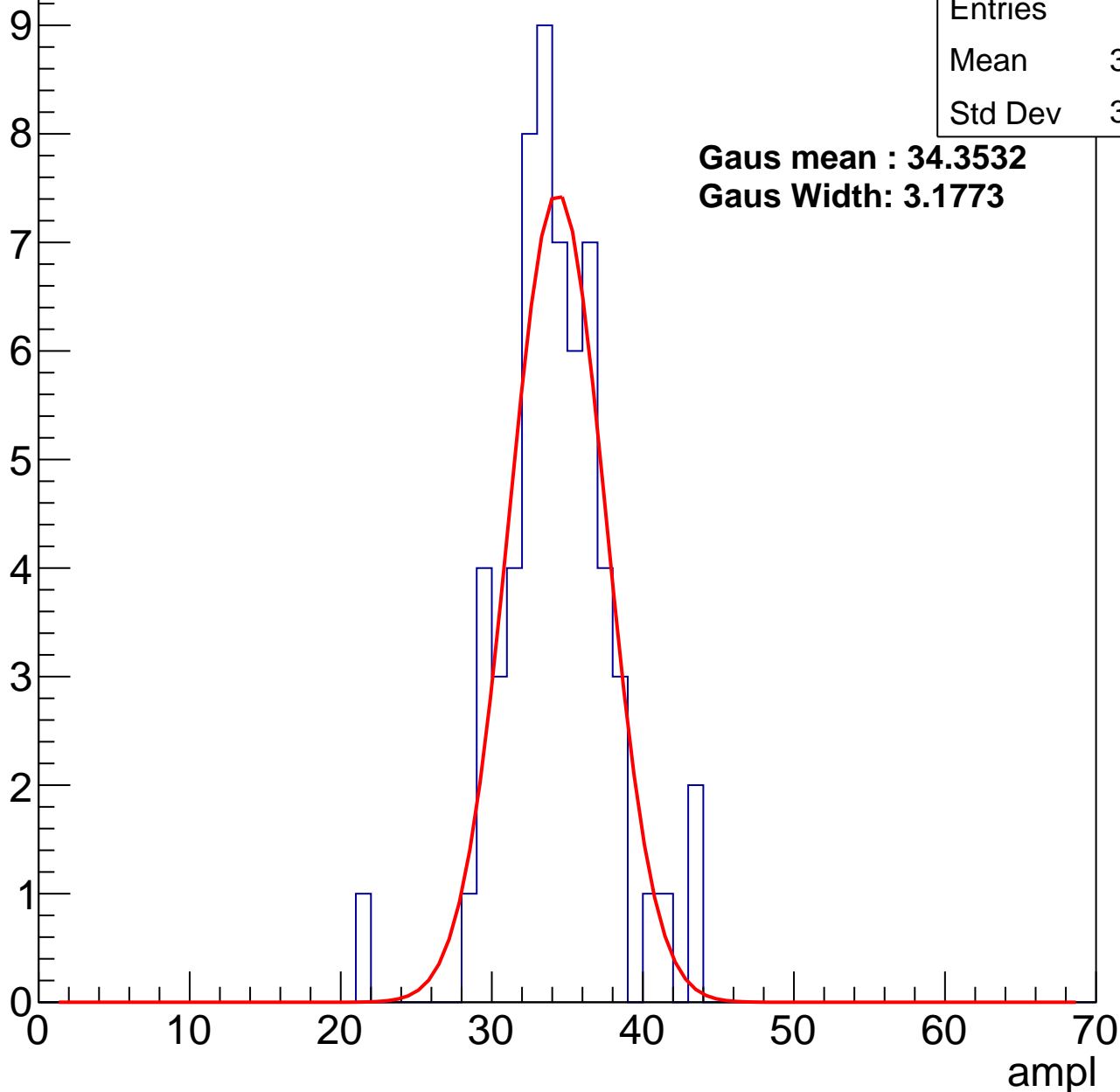
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	33.79
Std Dev	3.604

Gaus mean : 34.3532

Gaus Width: 3.1773



B1L102S, U9-ch78, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	40.84
Std Dev	4.34

Gaus mean : 41.0004

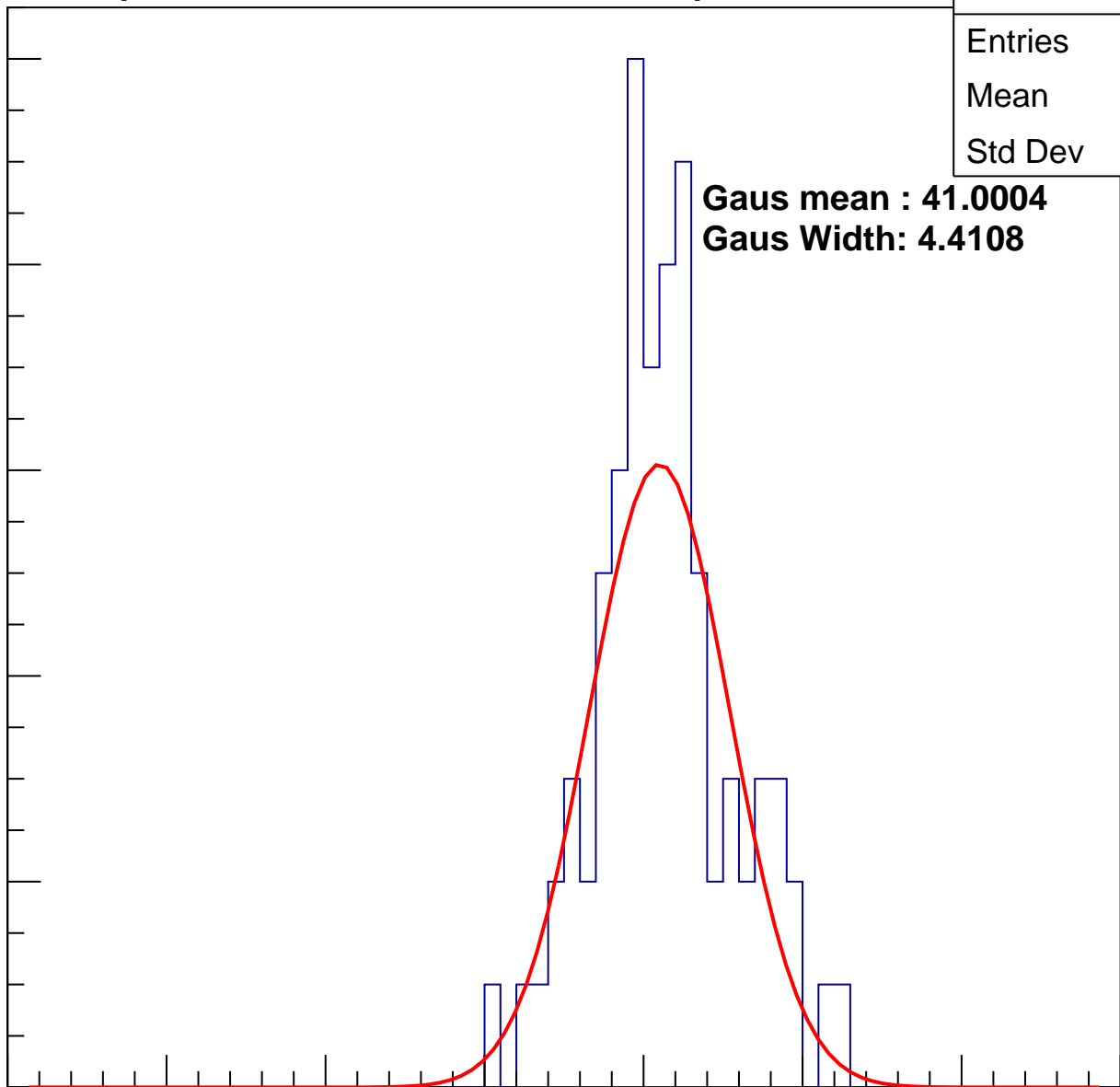
Gaus Width: 4.4108

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

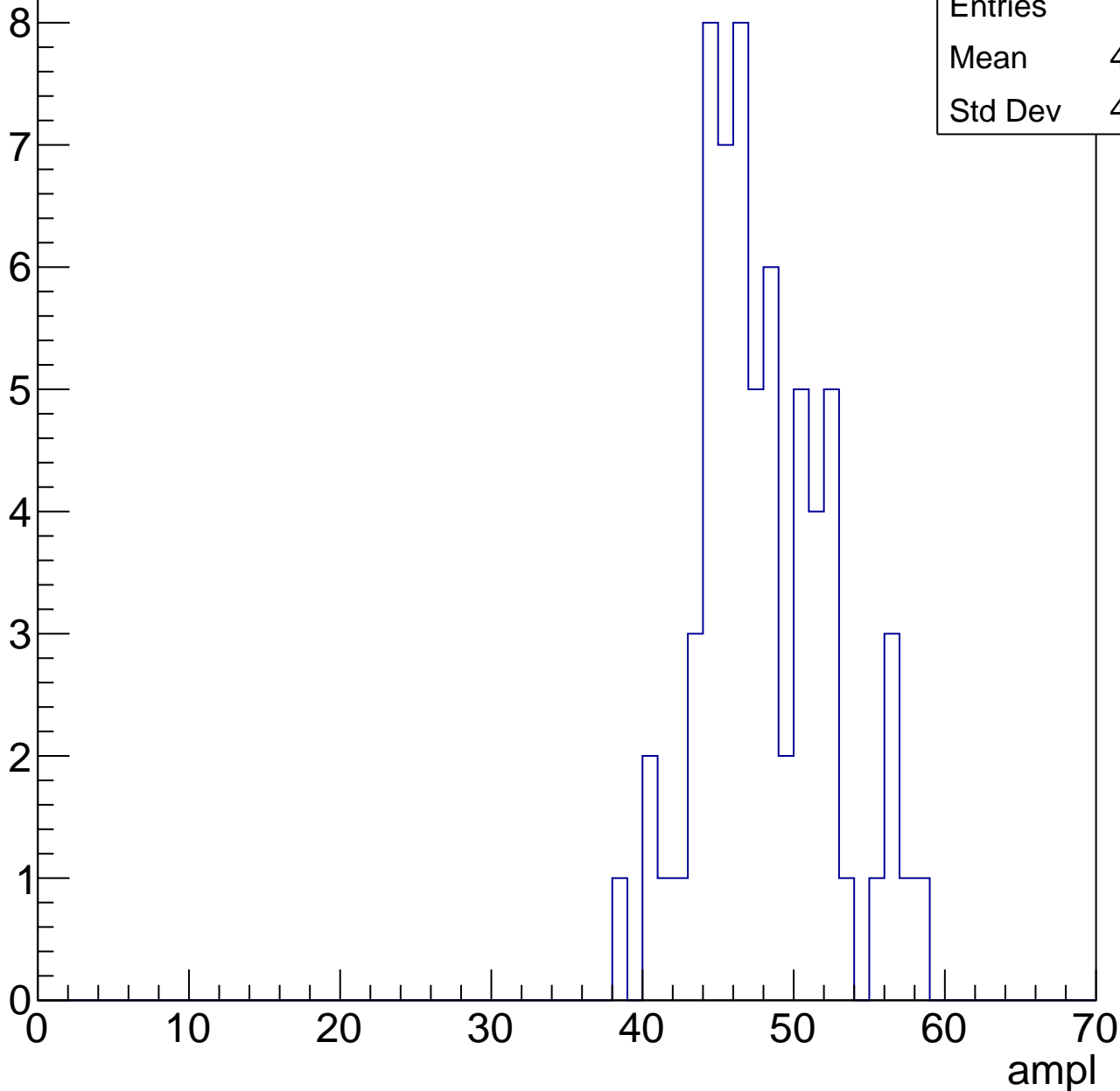


B1L102S, U9-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.55
Std Dev	4.275



B1L102S, U9-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	59
Mean	53.75
Std Dev	3.457

0

1

2

3

4

5

6

7

10

20

30

40

50

60

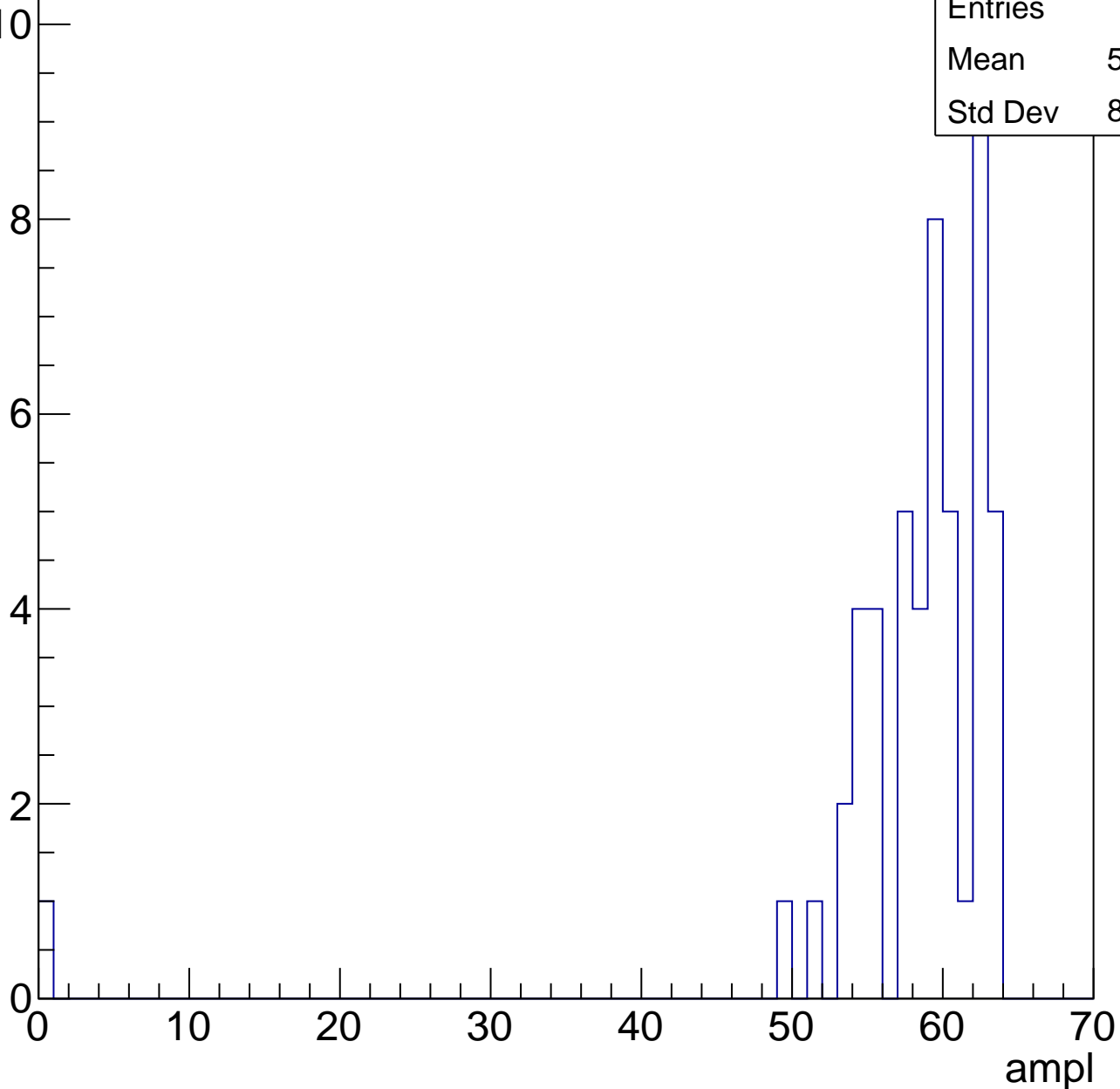
ampl

B1L102S, U9-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.39
Std Dev	8.803

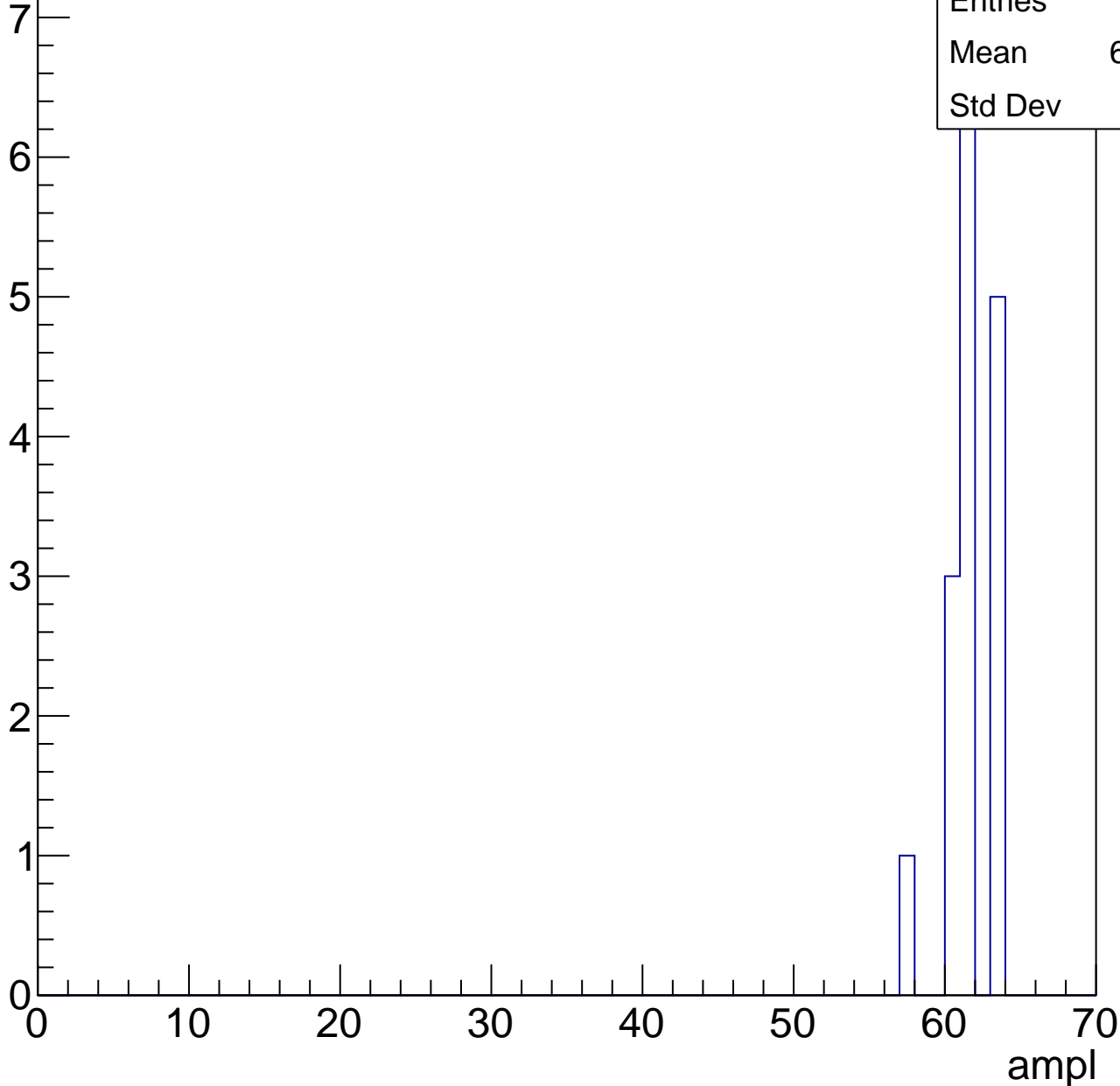


B1L102S, U9-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.19
Std Dev	1.55



B1L102S, U9-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch79, adc0

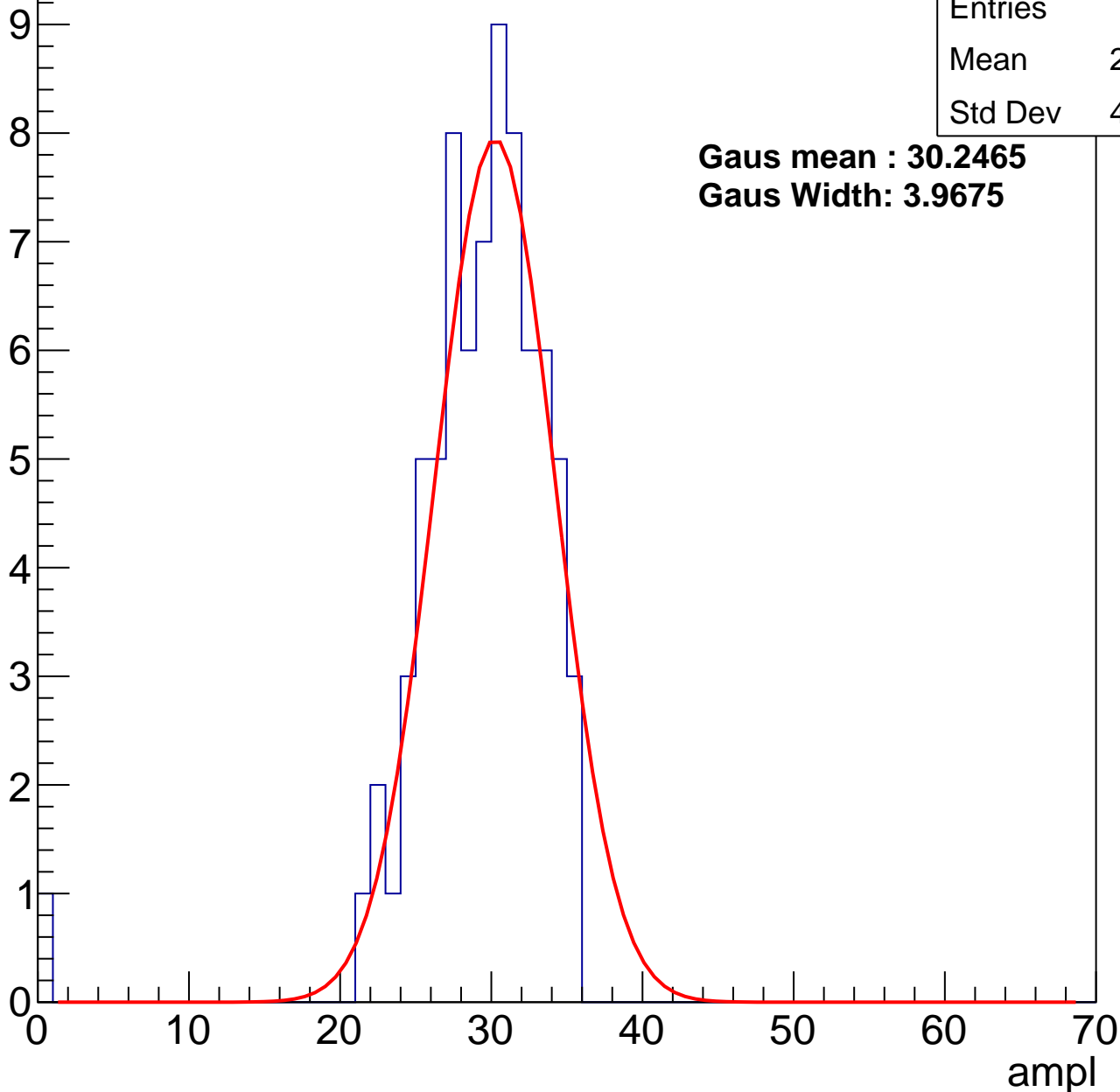
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.75
Std Dev	4.724

Gaus mean : 30.2465

Gaus Width: 3.9675



B1L102S, U9-ch79, adc1

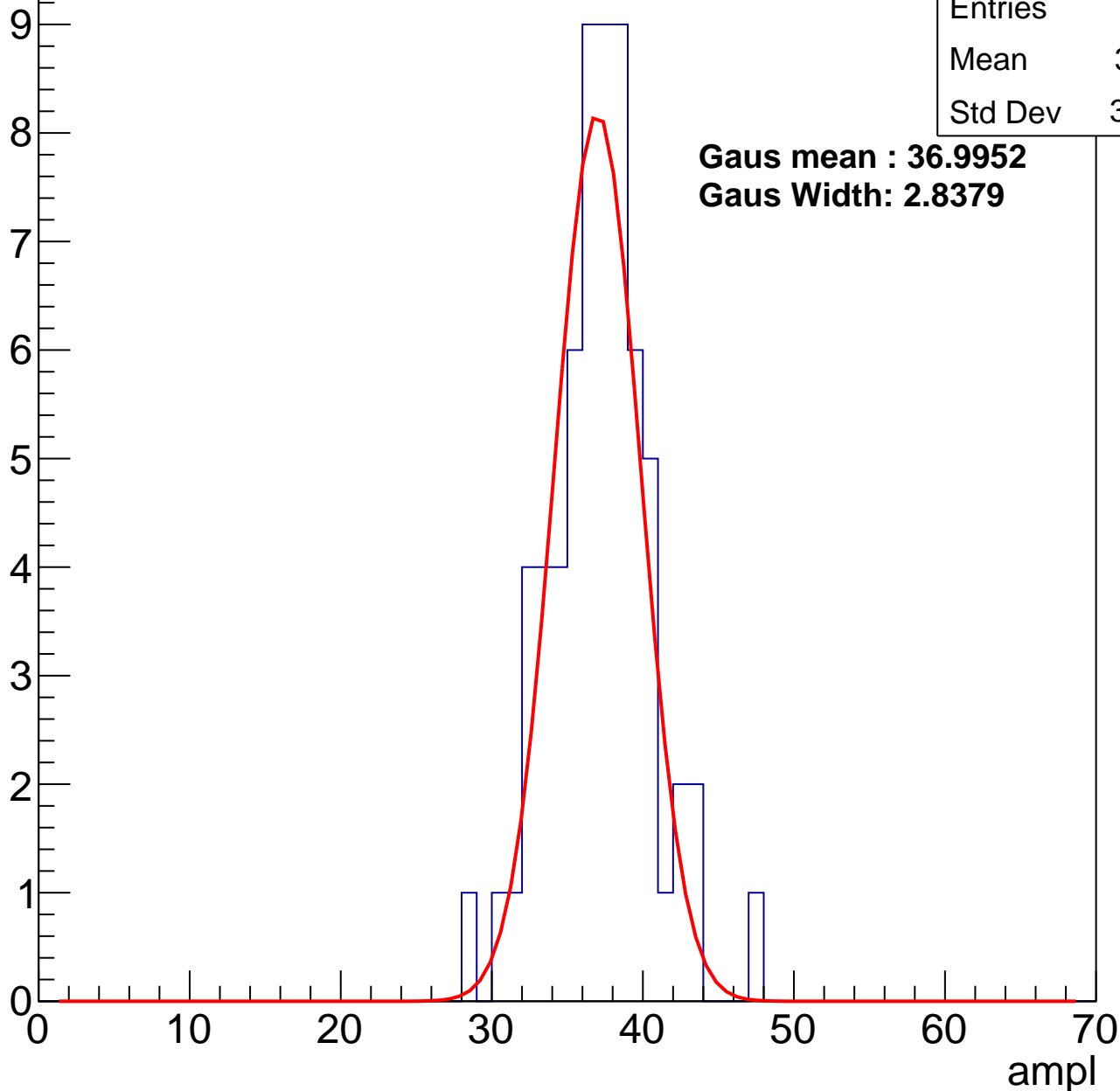
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.71
Std Dev	3.304

Gaus mean : 36.9952

Gaus Width: 2.8379



B1L102S, U9-ch79, adc2

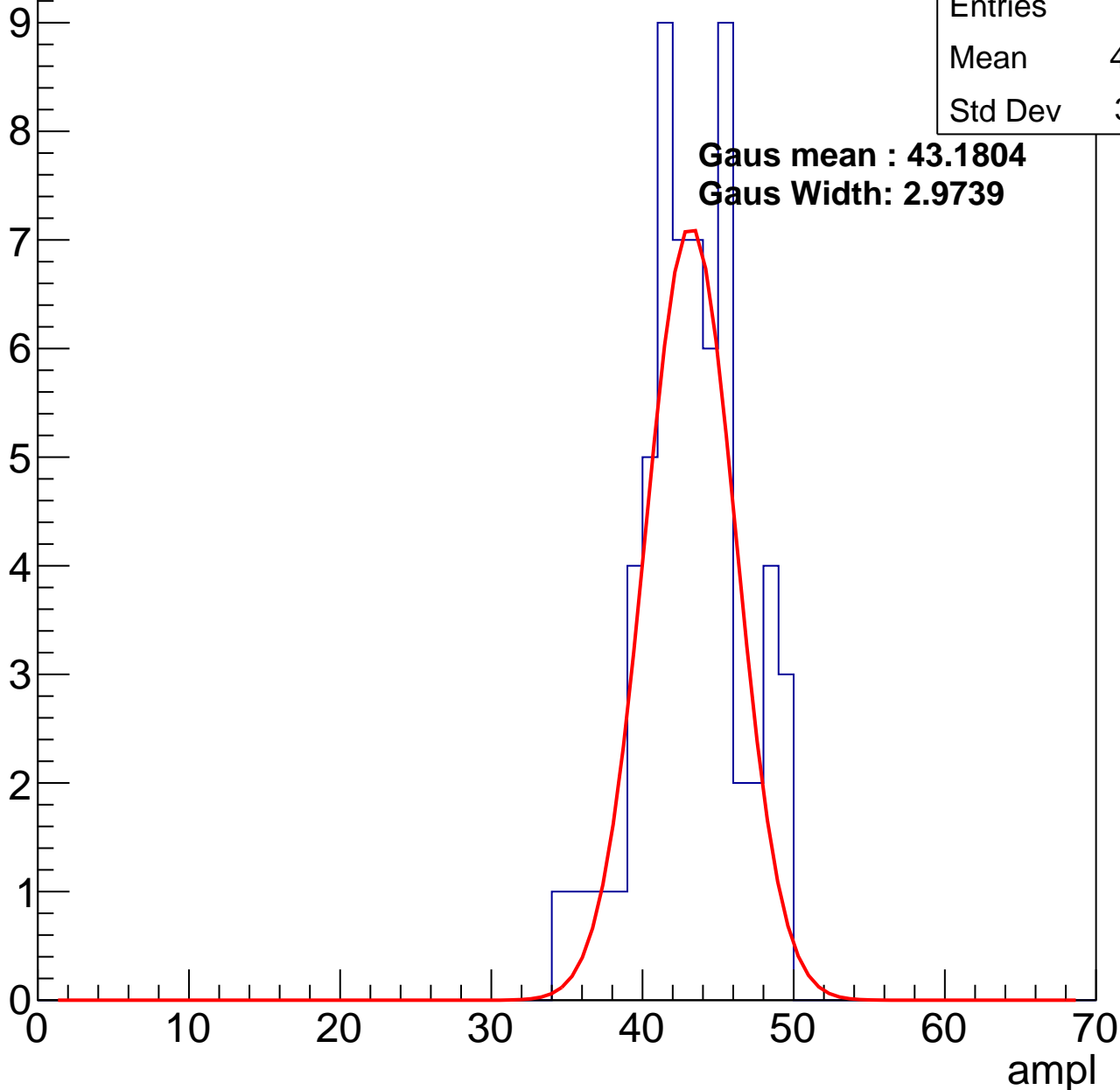
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.76
Std Dev	3.341

Gaus mean : 43.1804

Gaus Width: 2.9739

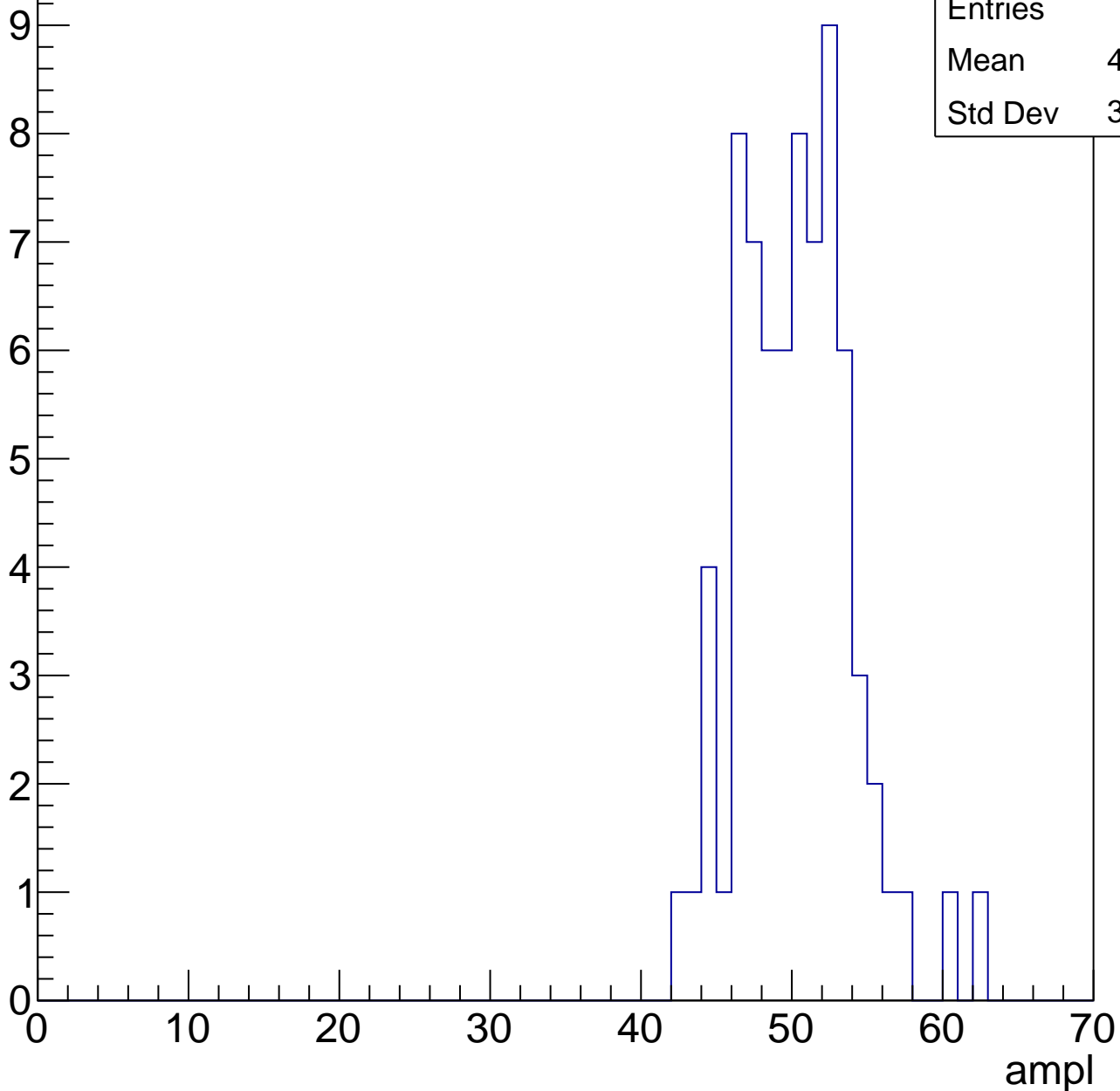


B1L102S, U9-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

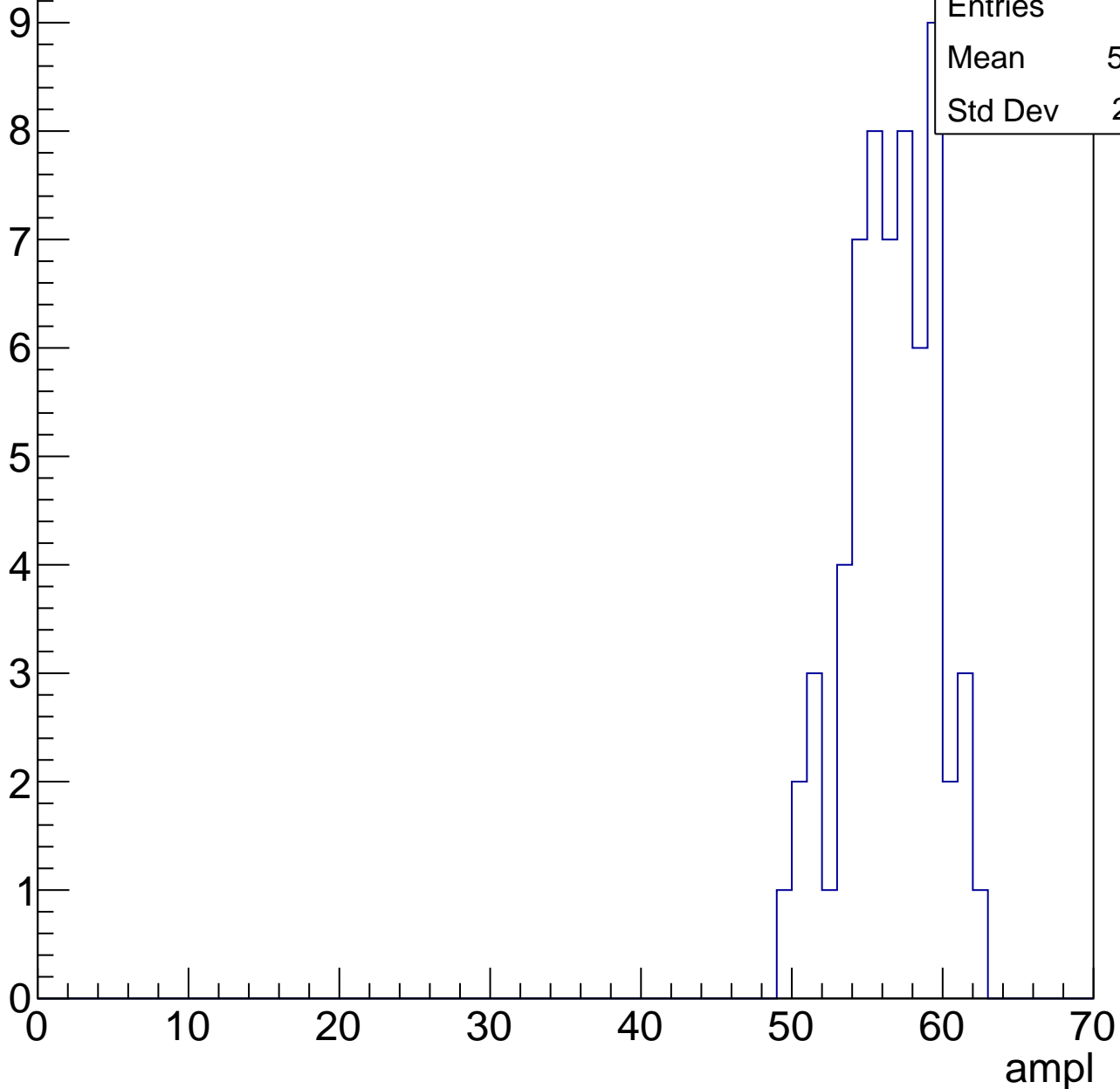
Entries	73
Mean	49.79
Std Dev	3.745



B1L102S, U9-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

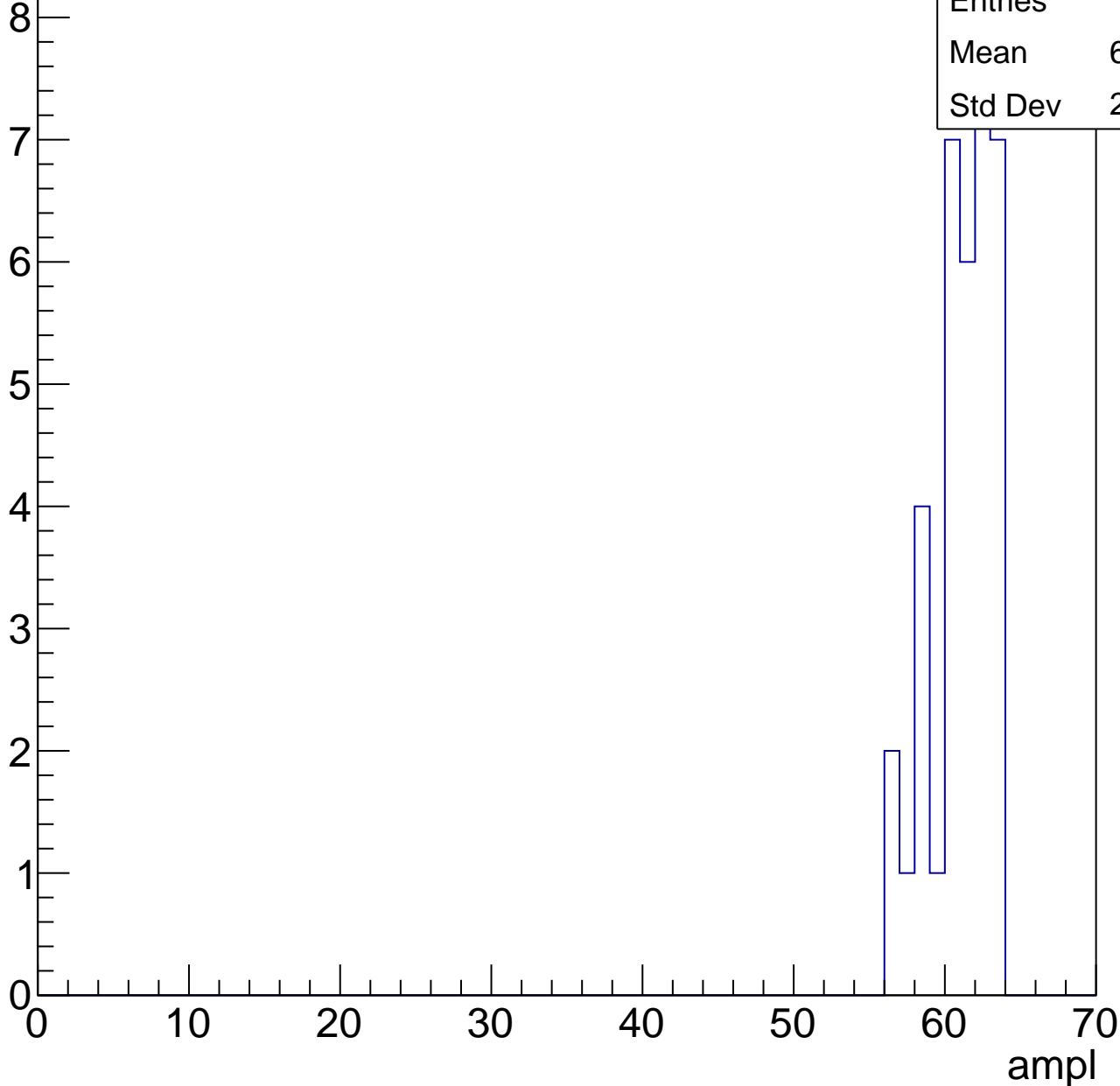


B1L102S, U9-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

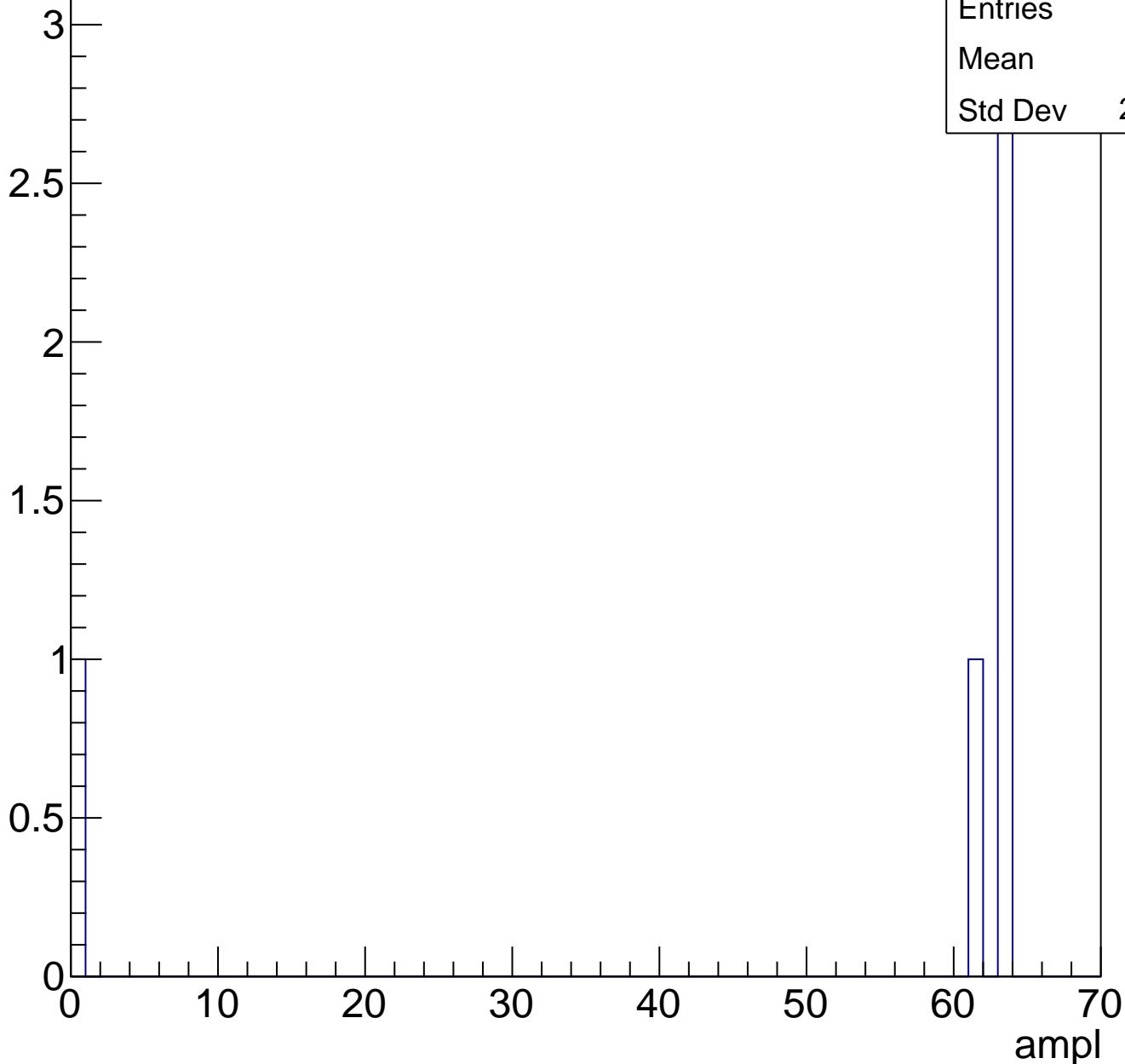
Entries	36
Mean	60.64
Std Dev	2.002



B1L102S, U9-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch80, adc0

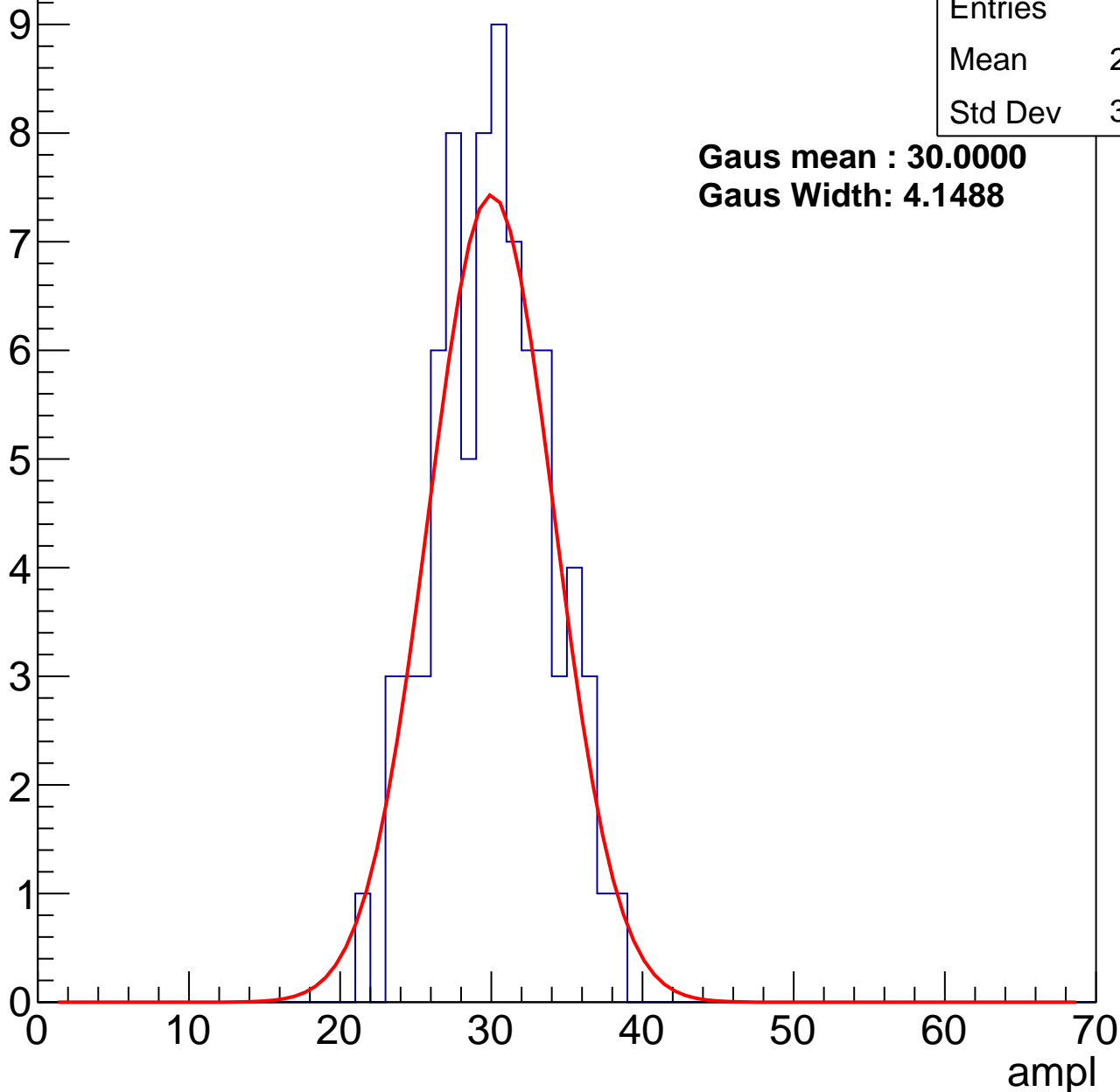
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.65
Std Dev	3.695

Gaus mean : 30.0000

Gaus Width: 4.1488



B1L102S, U9-ch80, adc1

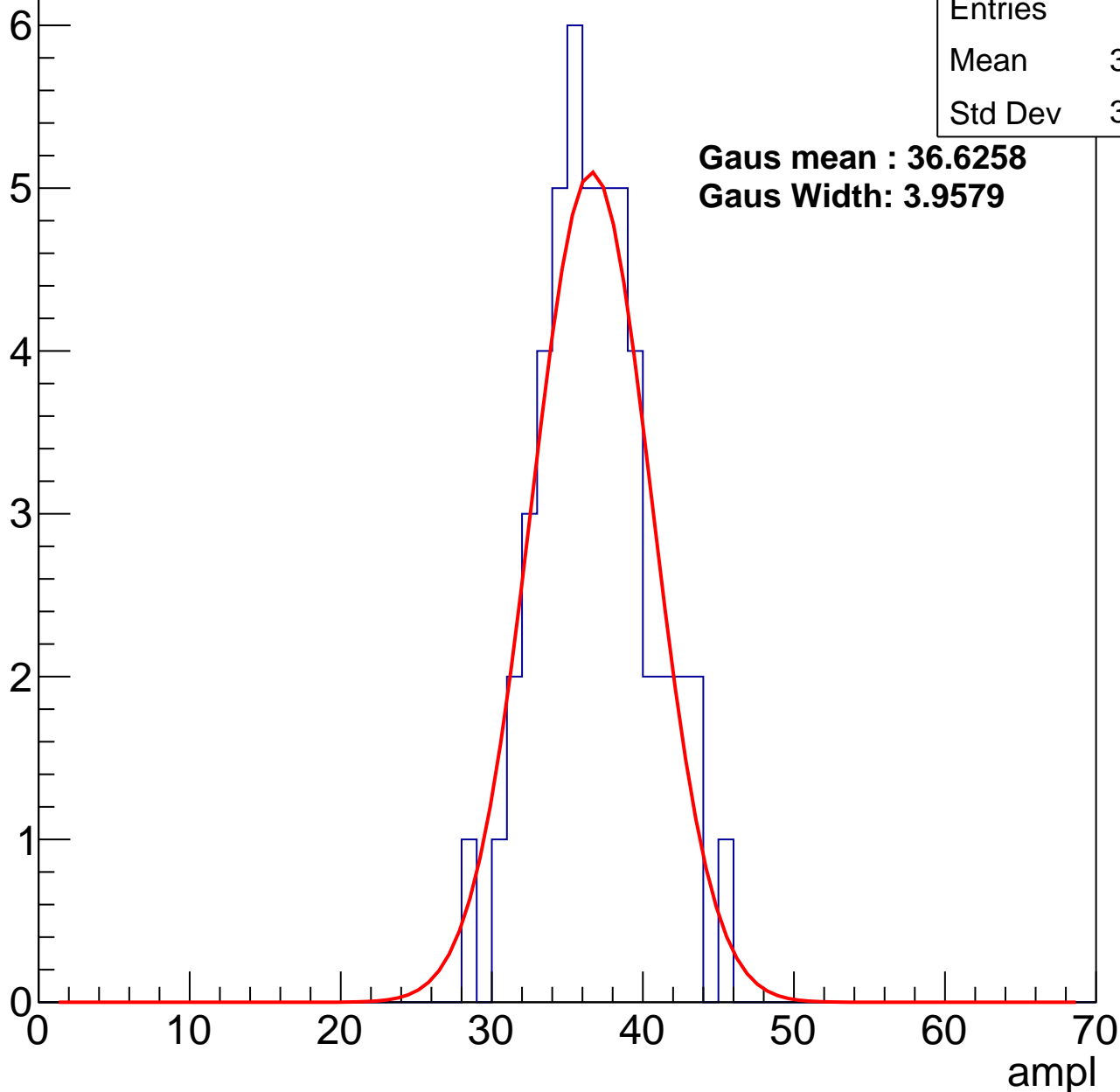
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	36.32
Std Dev	3.602

Gaus mean : 36.6258

Gaus Width: 3.9579



B1L102S, U9-ch80, adc2

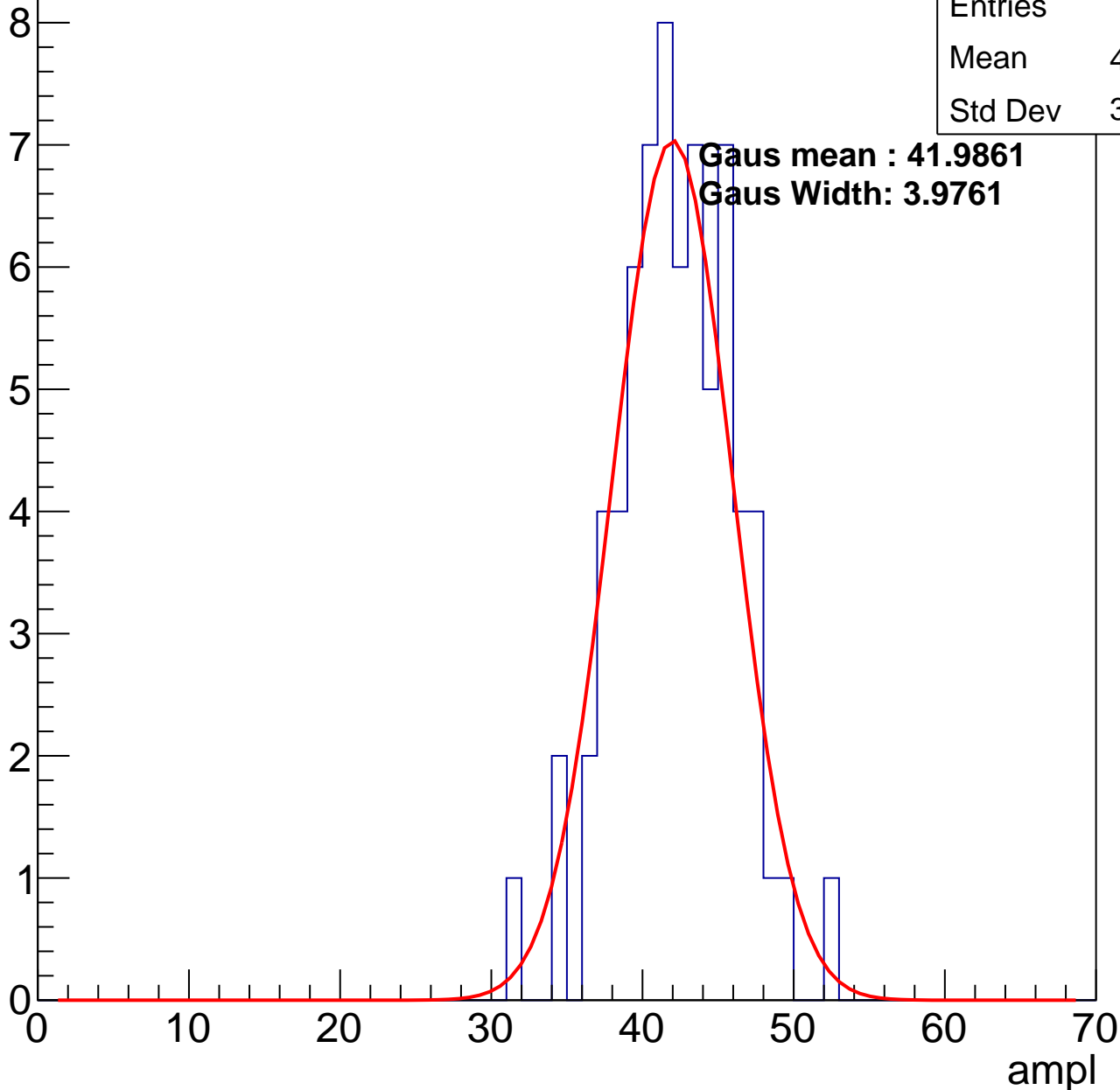
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.74
Std Dev	3.797

Gaus mean : 41.9861

Gaus Width: 3.9761

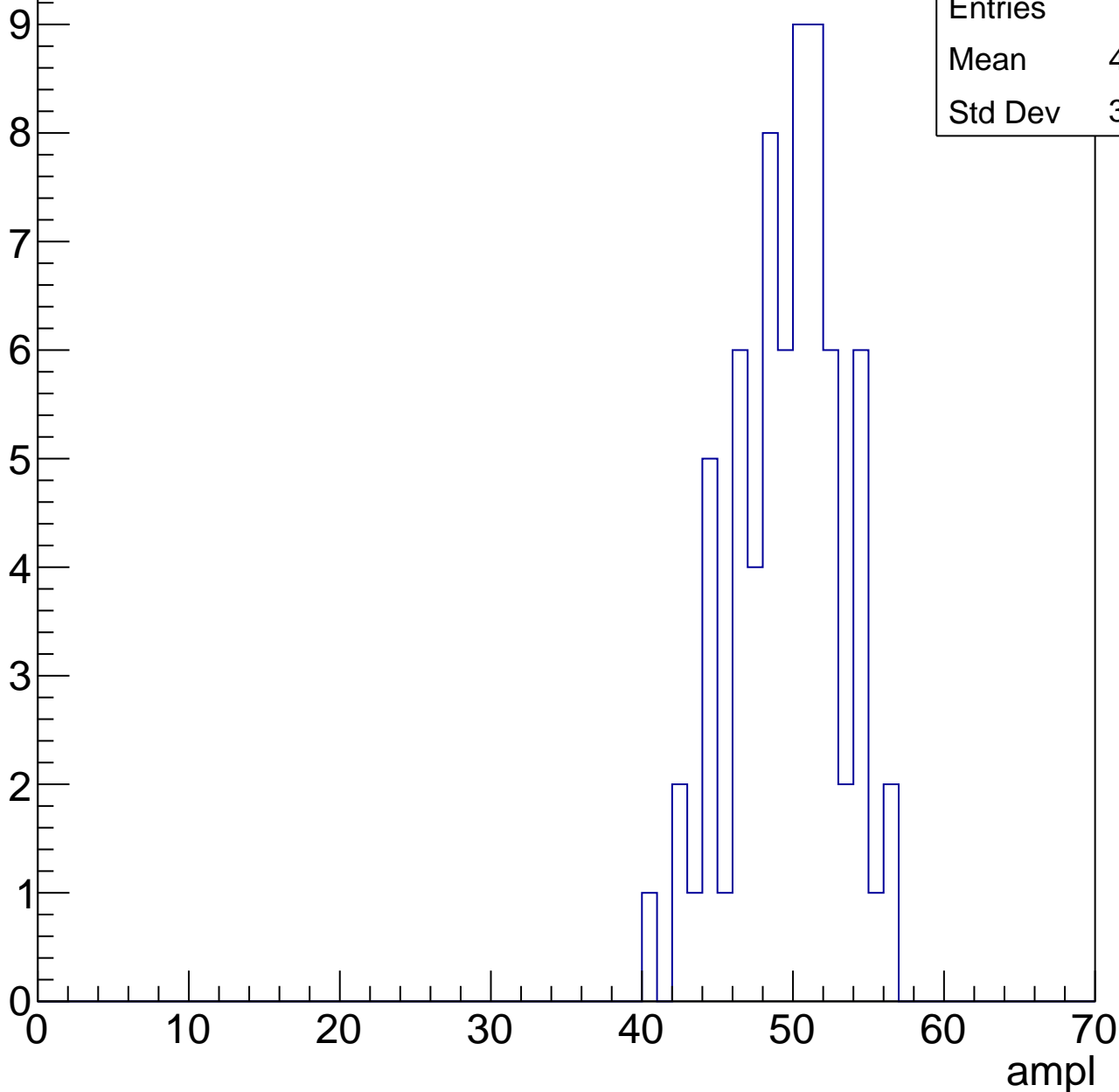


B1L102S, U9-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

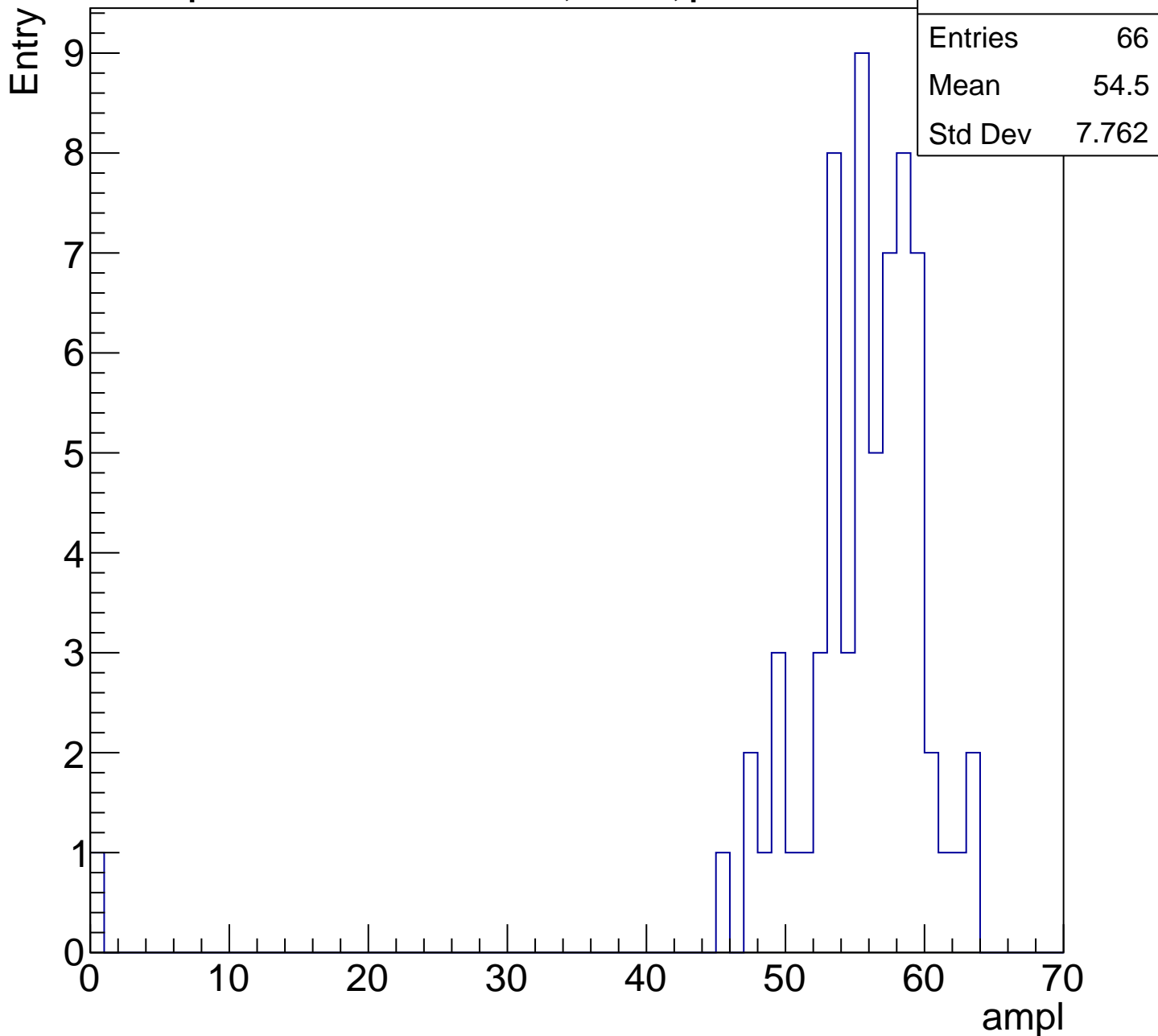
Entry

Entries	69
Mean	49.16
Std Dev	3.504



B1L102S, U9-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

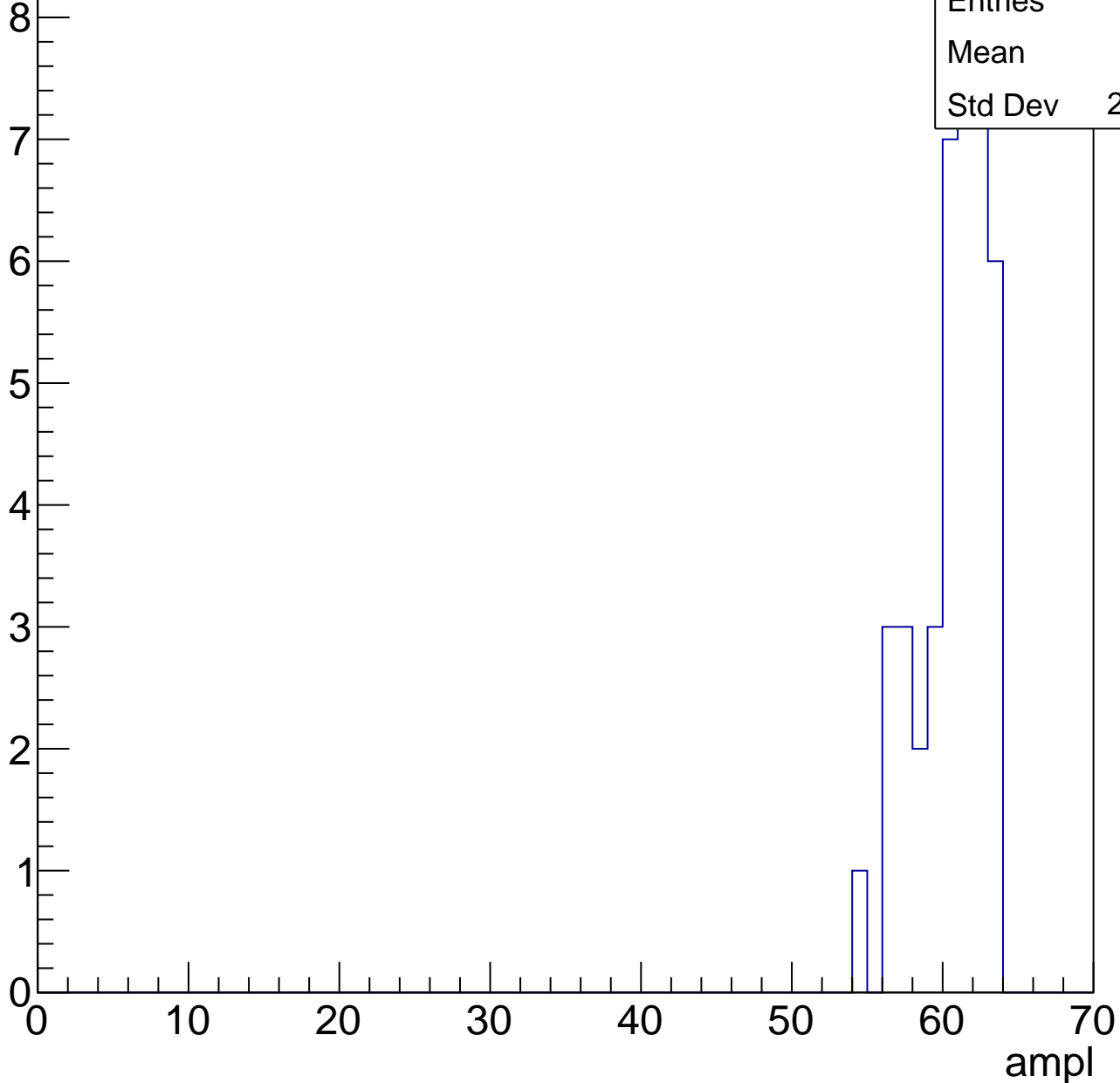


B1L102S, U9-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	60.2
Std Dev	2.287



B1L102S, U9-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

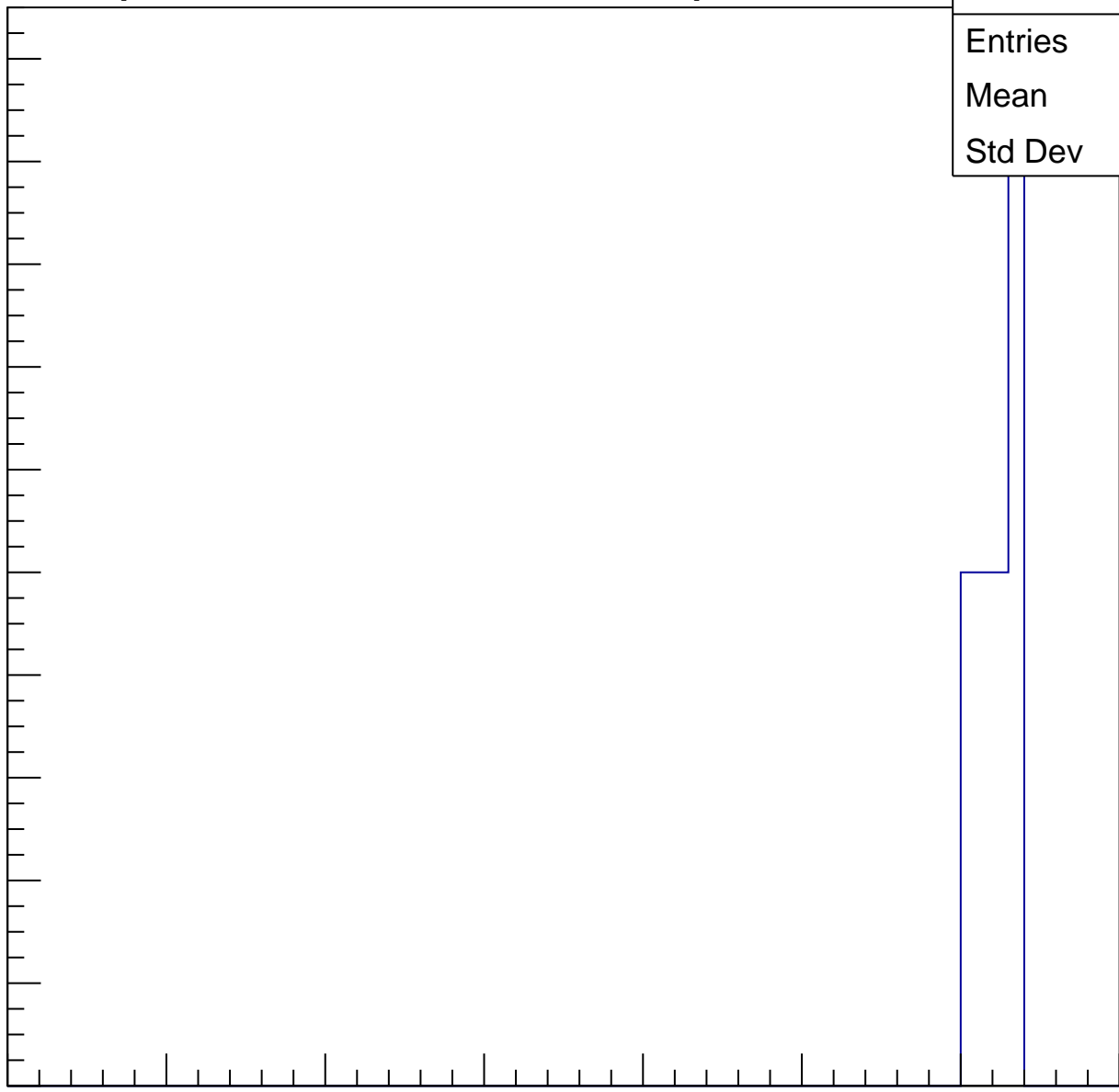
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch81, adc0

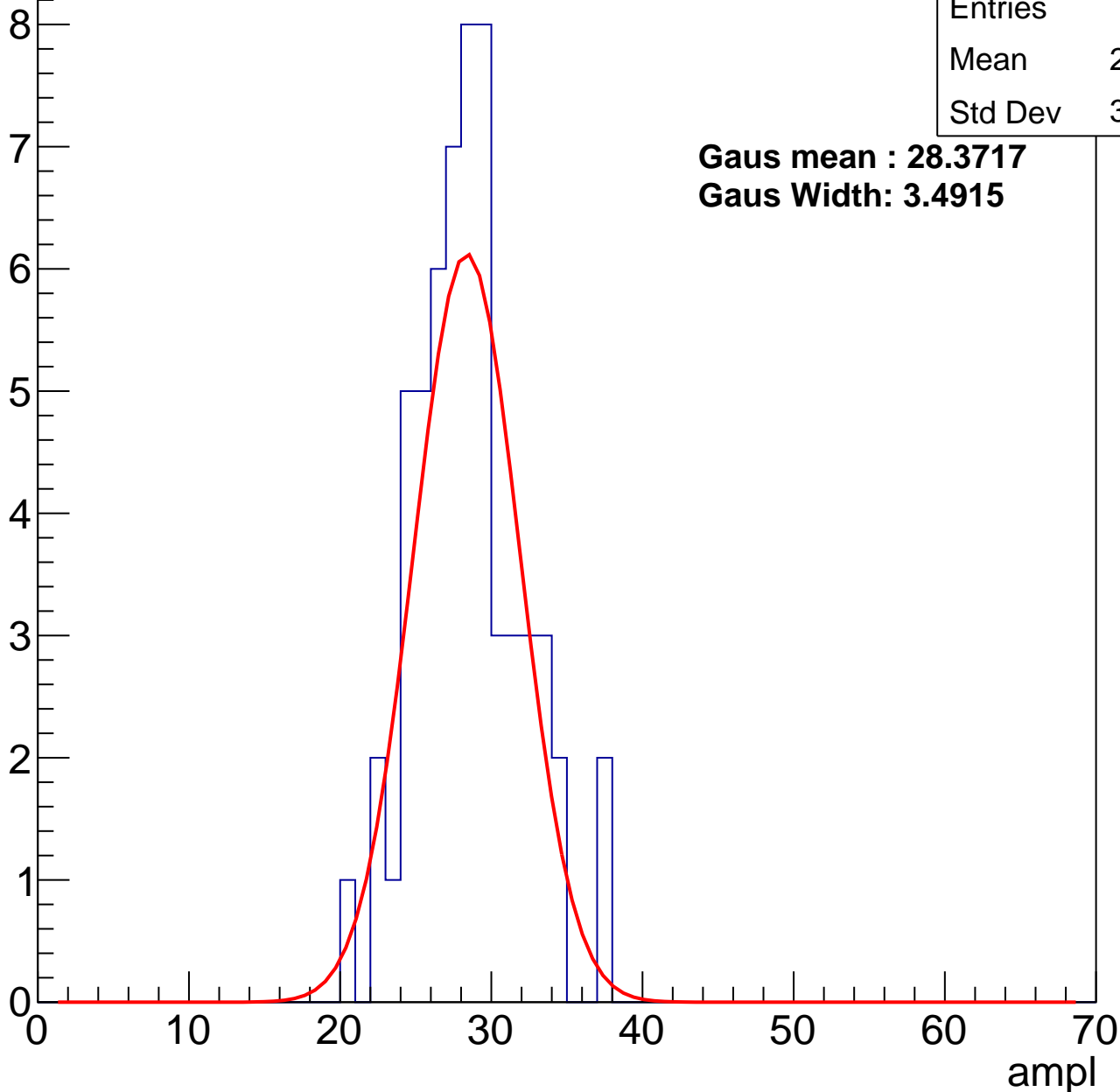
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.02
Std Dev	3.496

Gaus mean : 28.3717

Gaus Width: 3.4915



B1L102S, U9-ch81, adc1

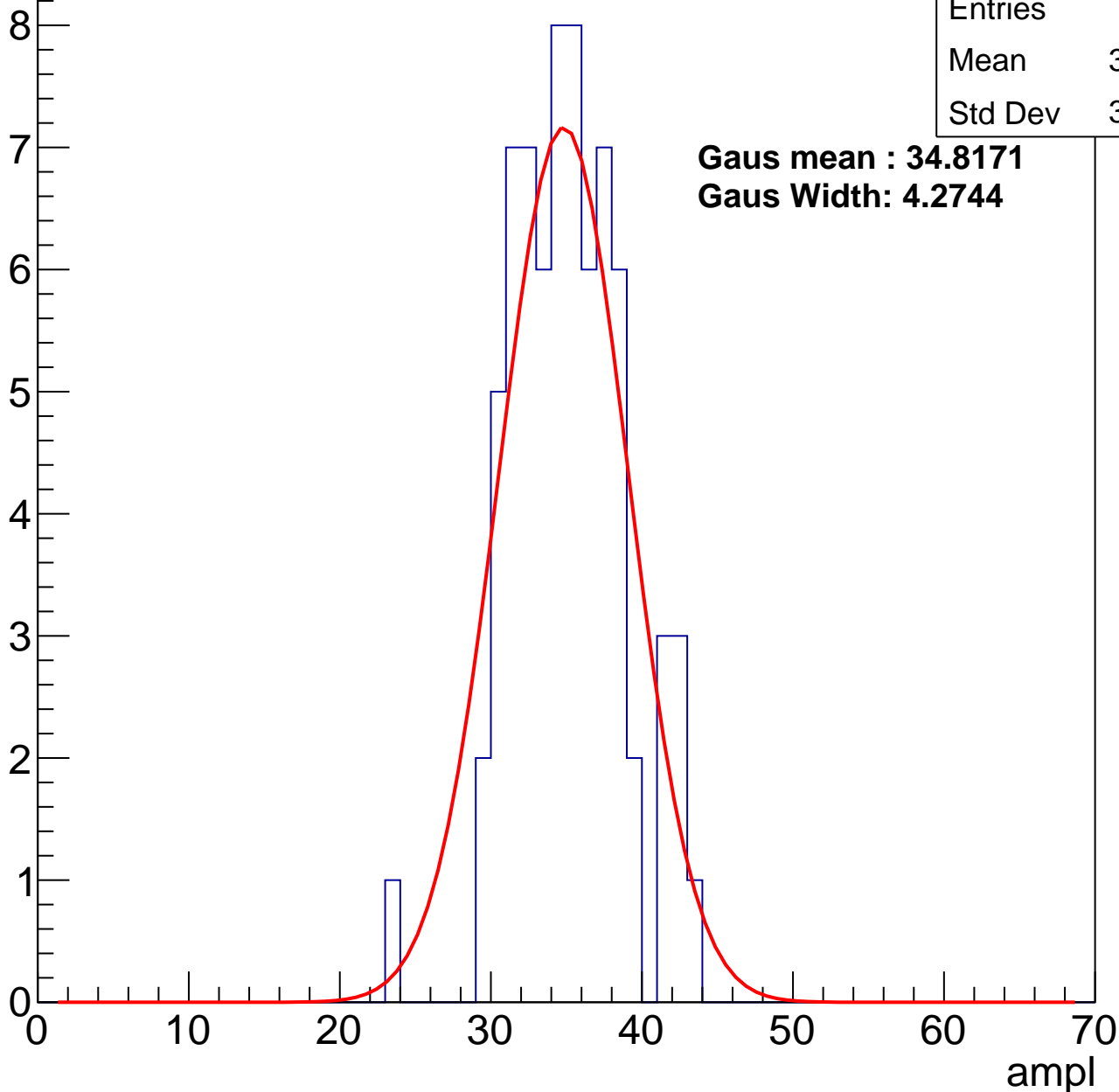
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.65
Std Dev	3.686

Gaus mean : 34.8171

Gaus Width: 4.2744



B1L102S, U9-ch81, adc2

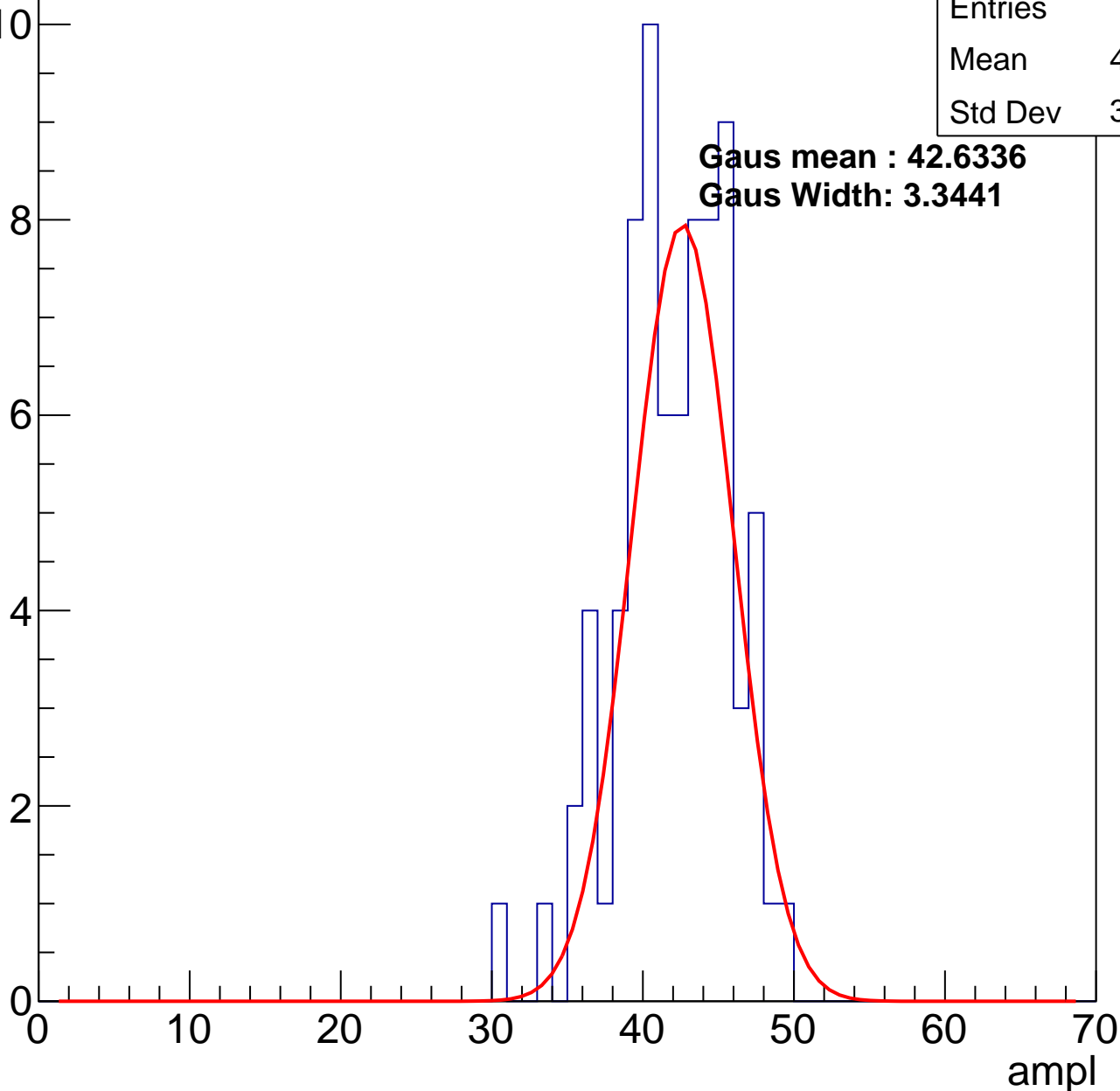
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	41.63
Std Dev	3.655

Gaus mean : 42.6336

Gaus Width: 3.3441

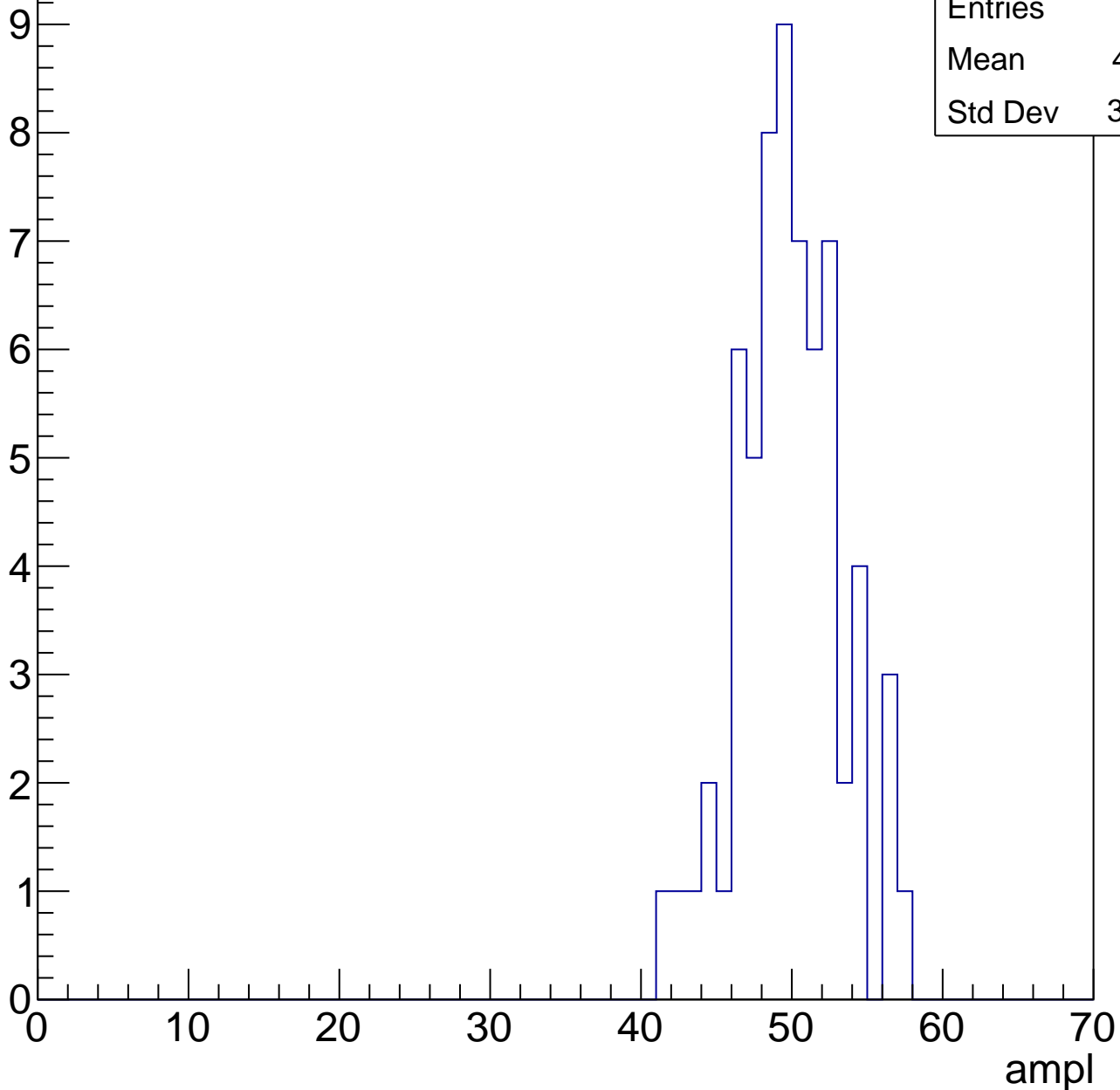


B1L102S, U9-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.41
Std Dev	3.362

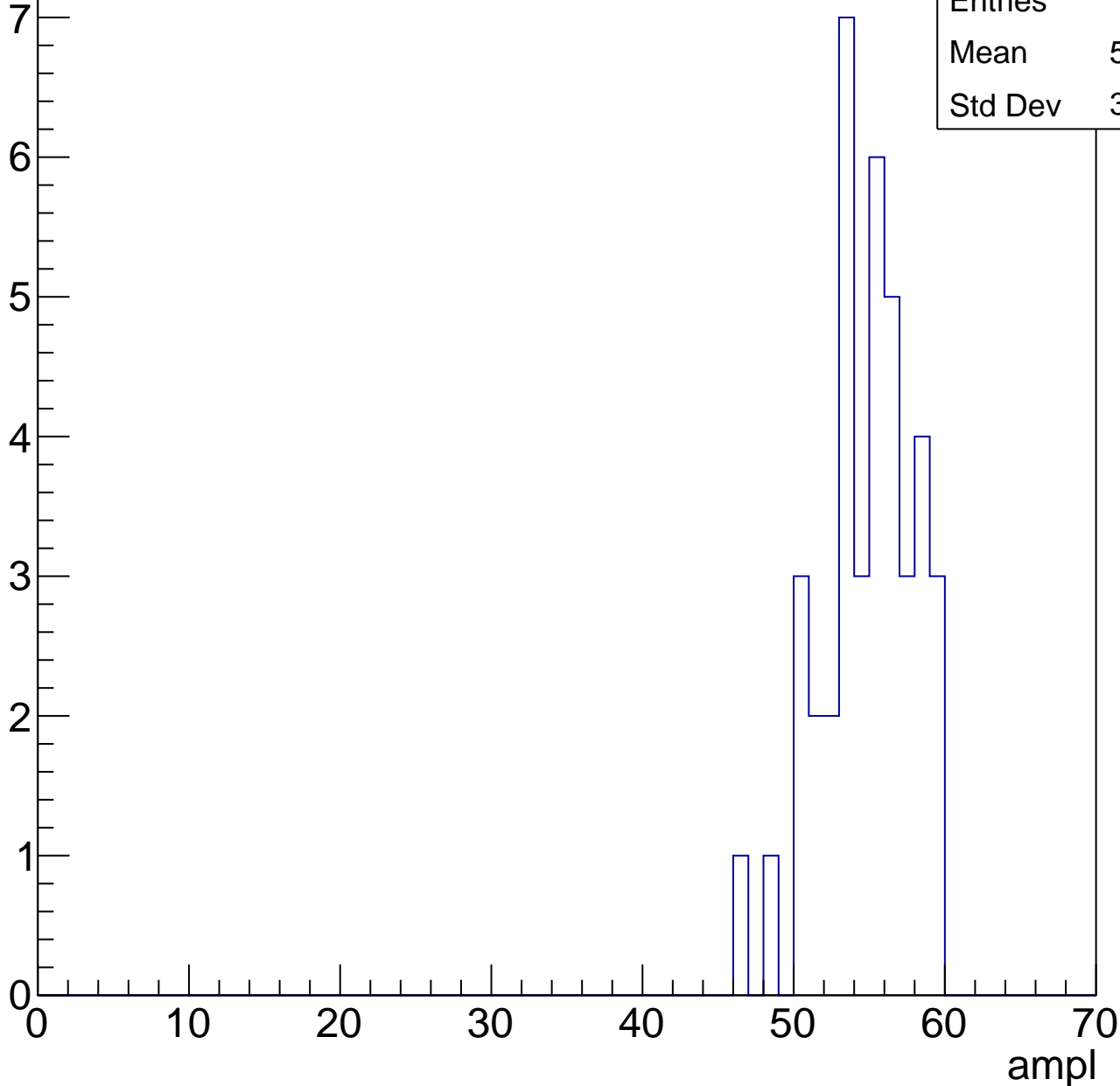


B1L102S, U9-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

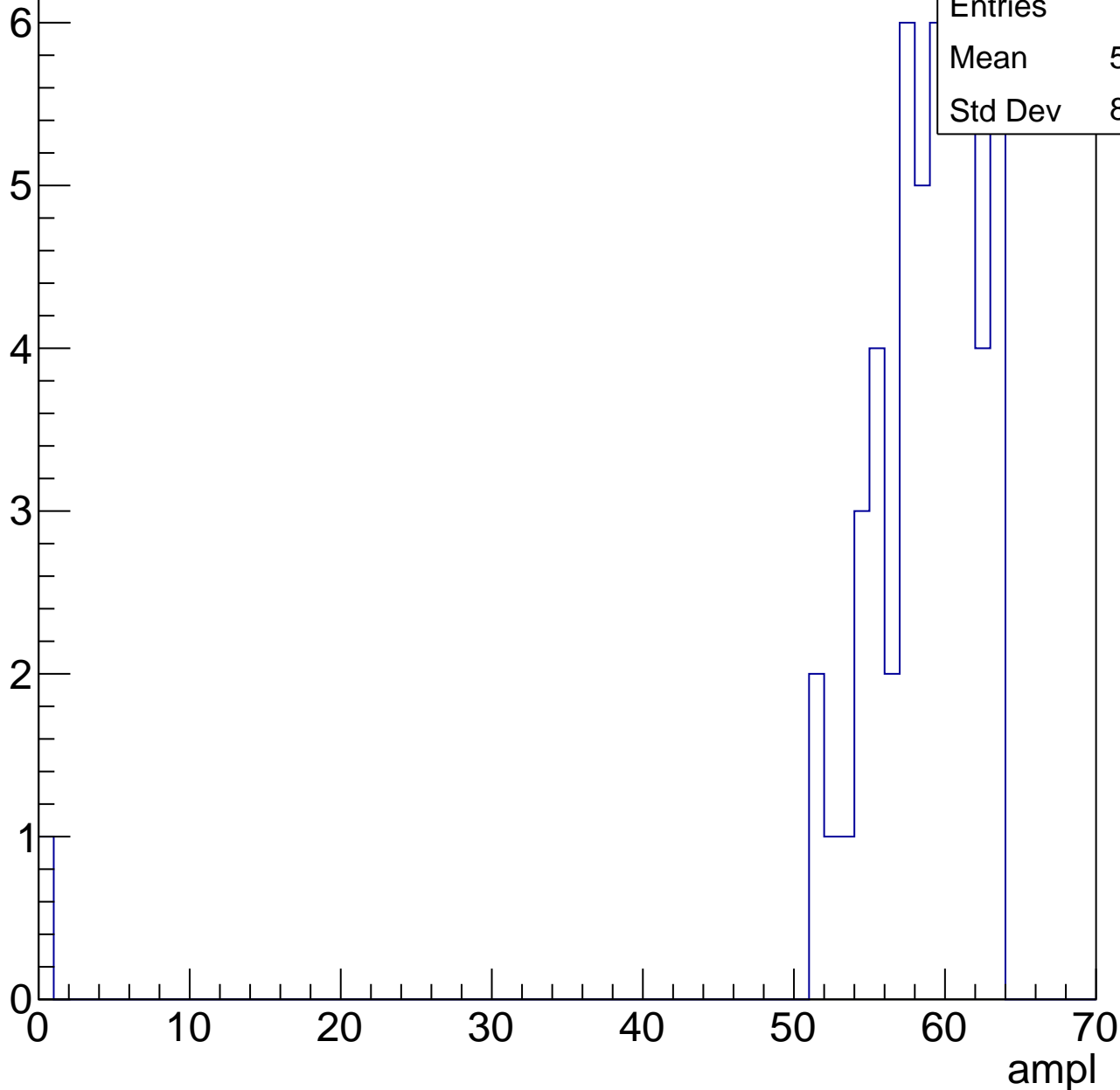
Entries	40
Mean	54.33
Std Dev	3.036



B1L102S, U9-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

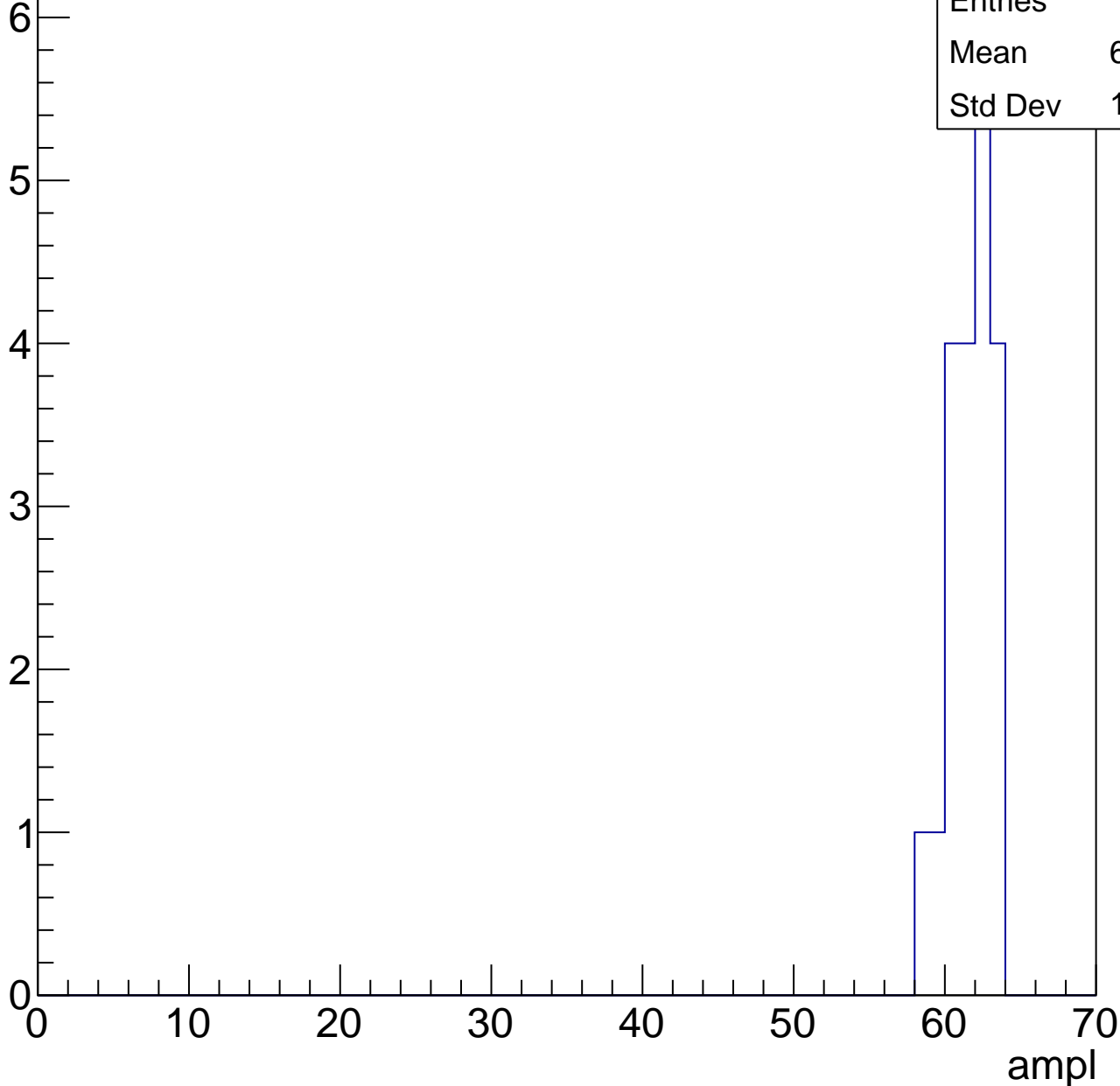


B1L102S, U9-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	61.25
Std Dev	1.374



B1L102S, U9-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch82, adc0

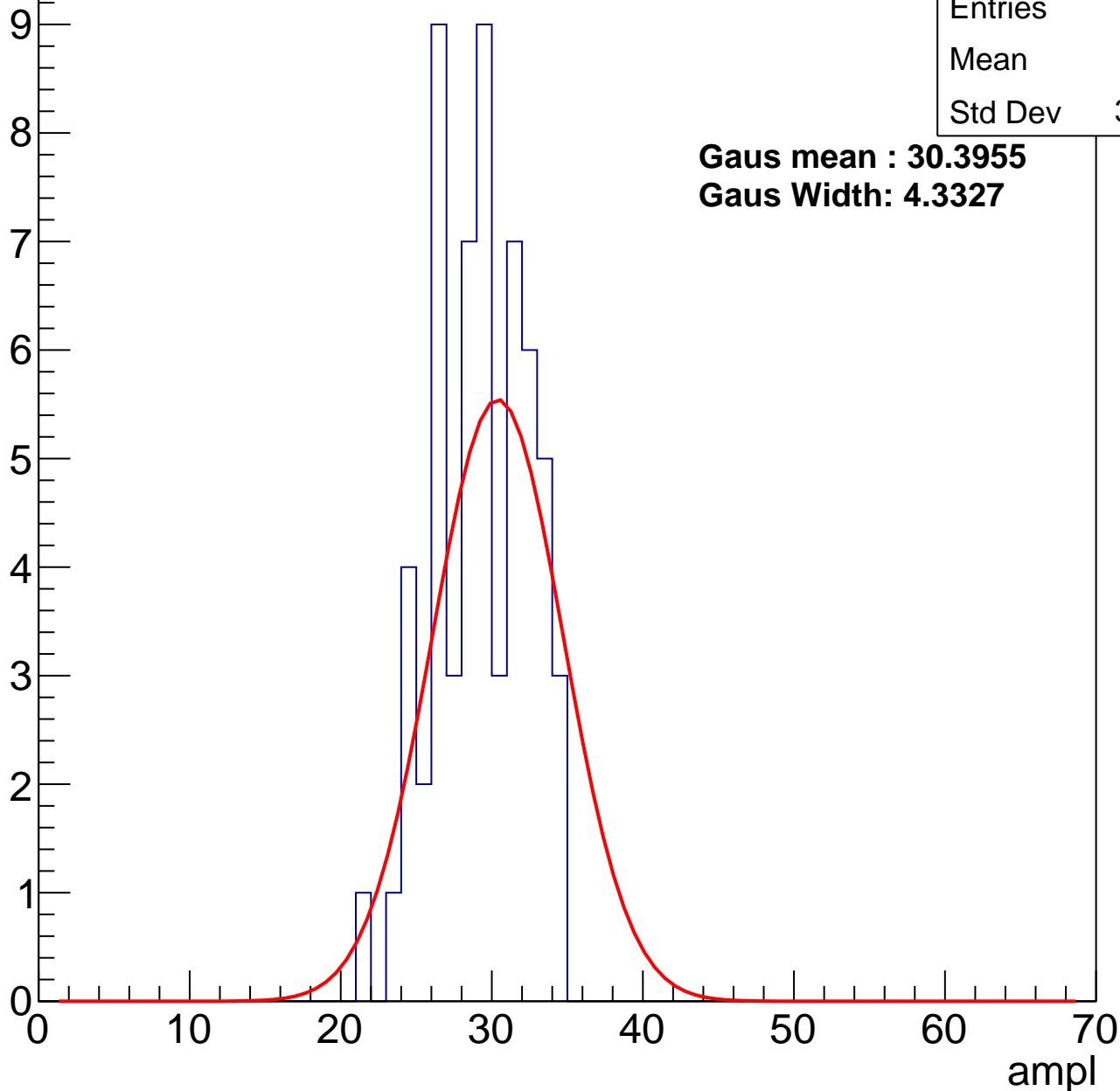
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.8
Std Dev	3.081

Gaus mean : 30.3955

Gaus Width: 4.3327



B1L102S, U9-ch82, adc1

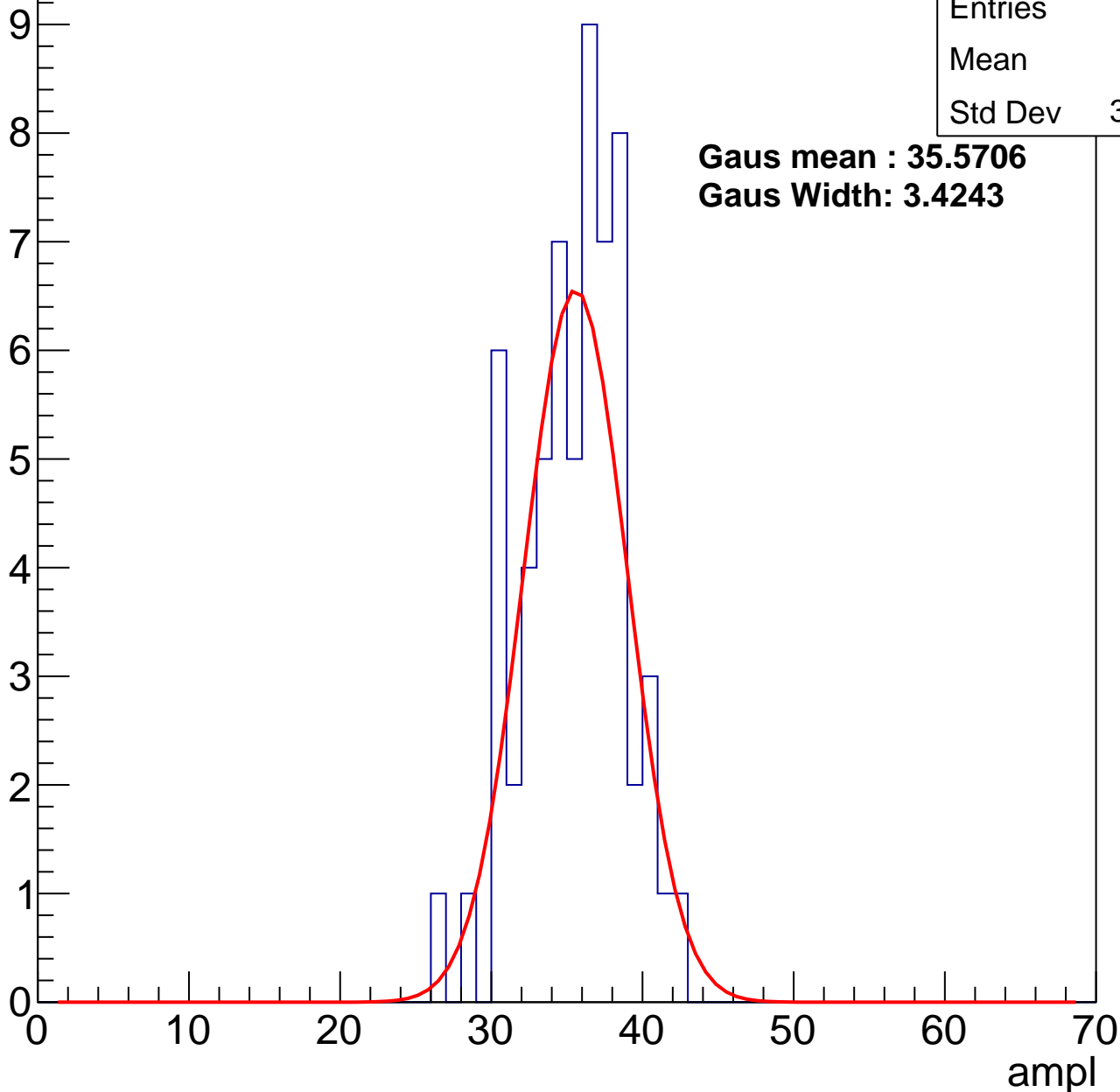
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35
Std Dev	3.302

Gaus mean : 35.5706

Gaus Width: 3.4243



B1L102S, U9-ch82, adc2

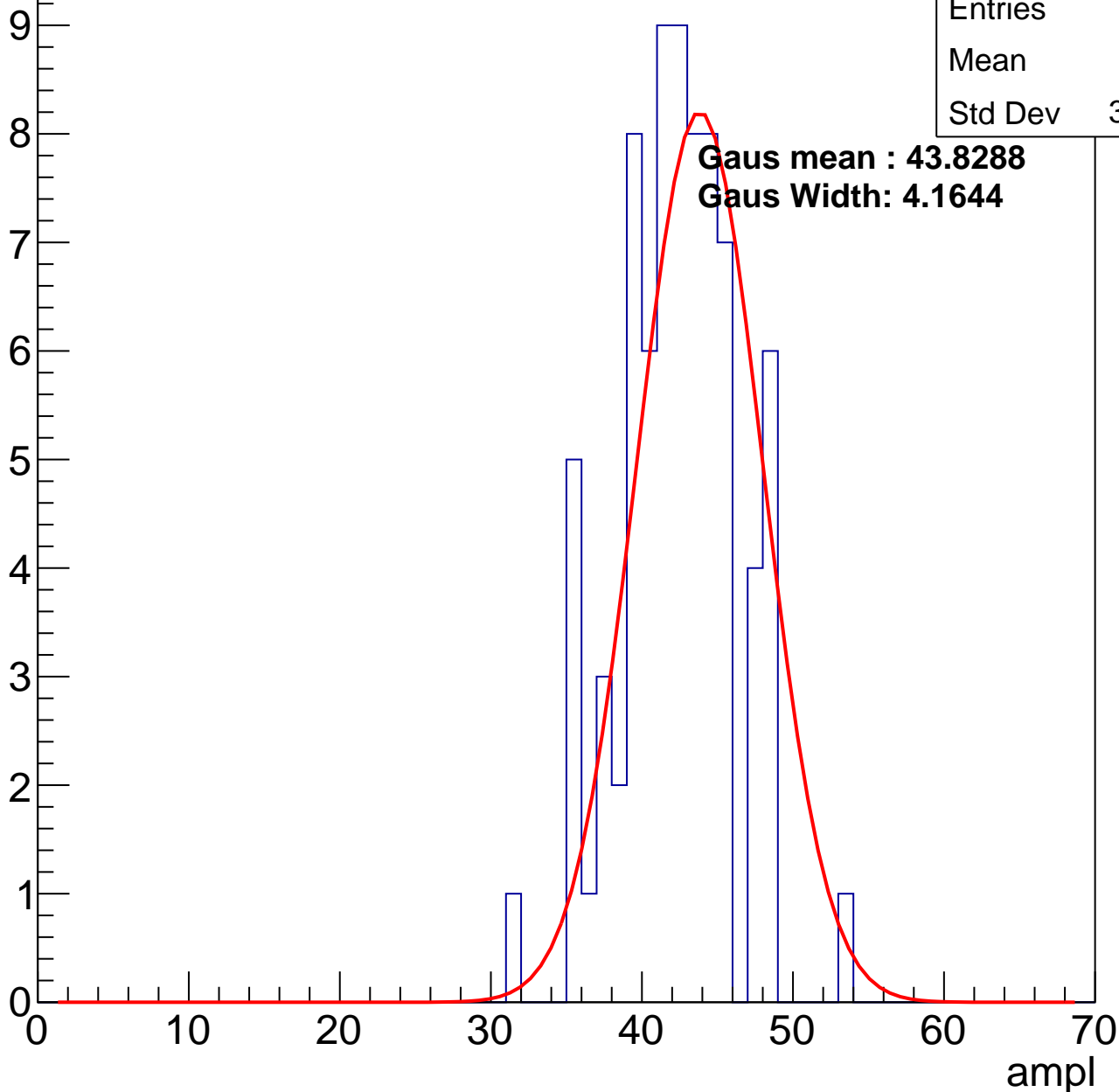
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	41.9
Std Dev	3.845

Gaus mean : 43.8288

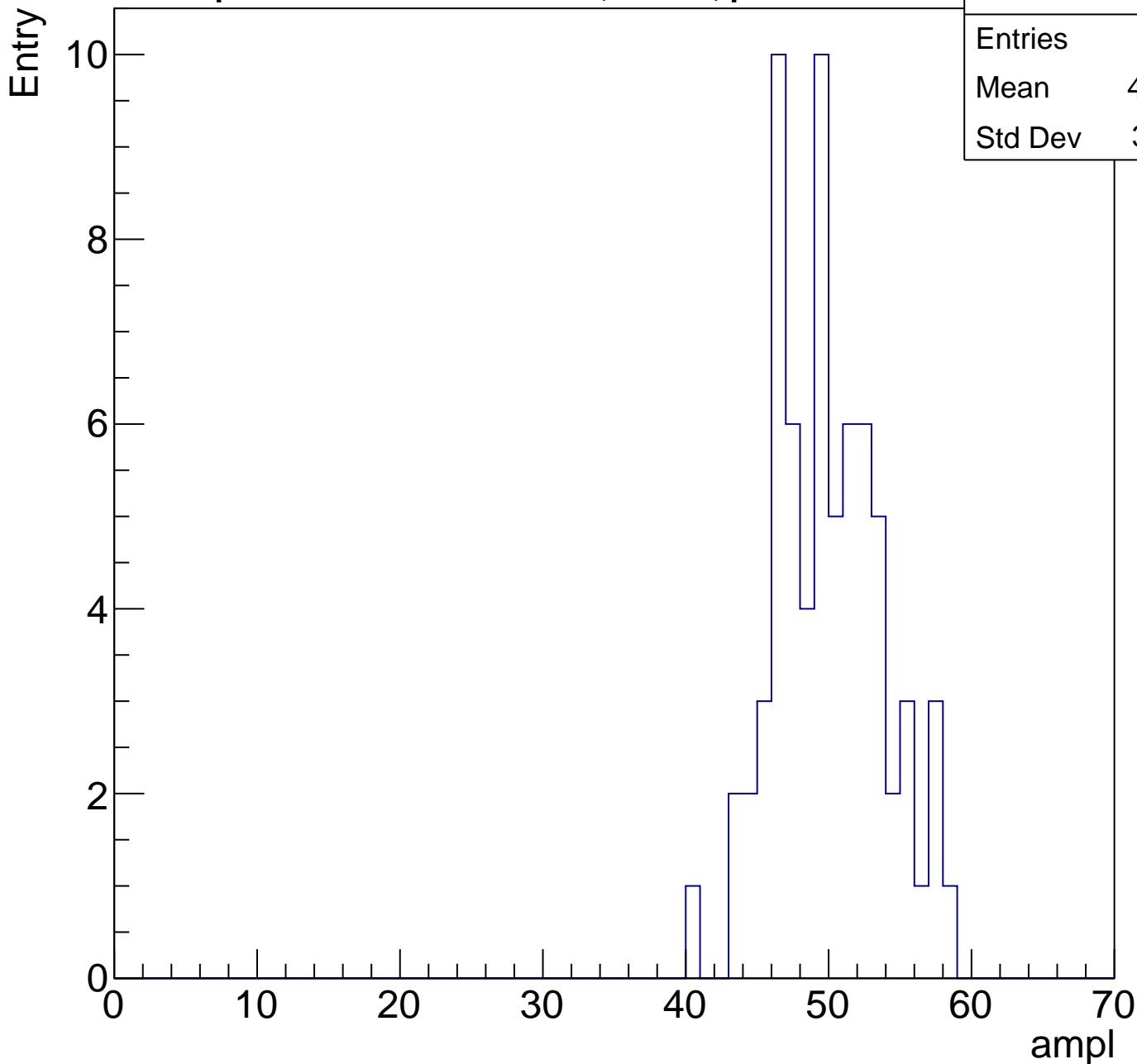
Gaus Width: 4.1644



B1L102S, U9-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	49.49
Std Dev	3.771

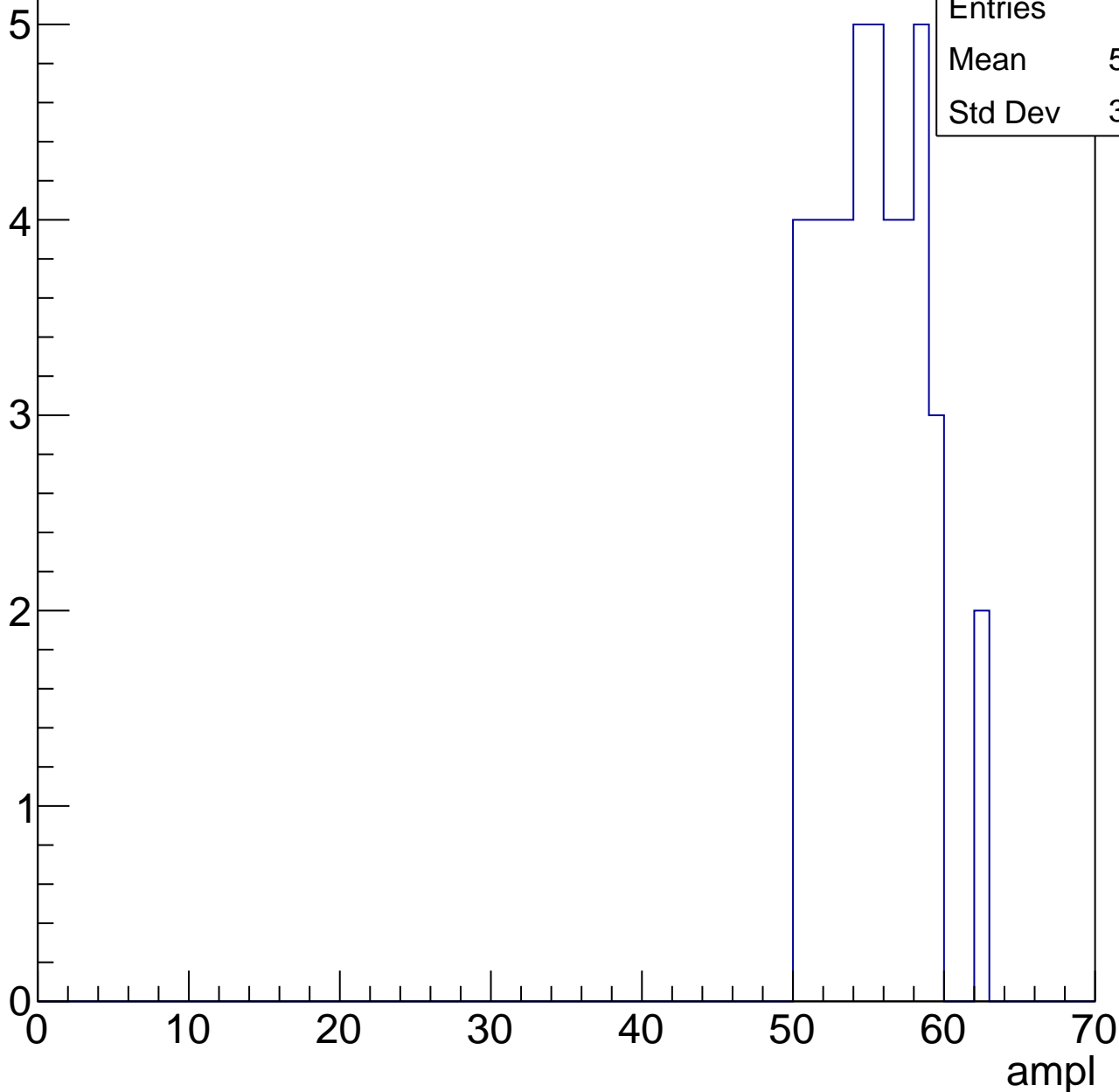


B1L102S, U9-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	54.82
Std Dev	3.128

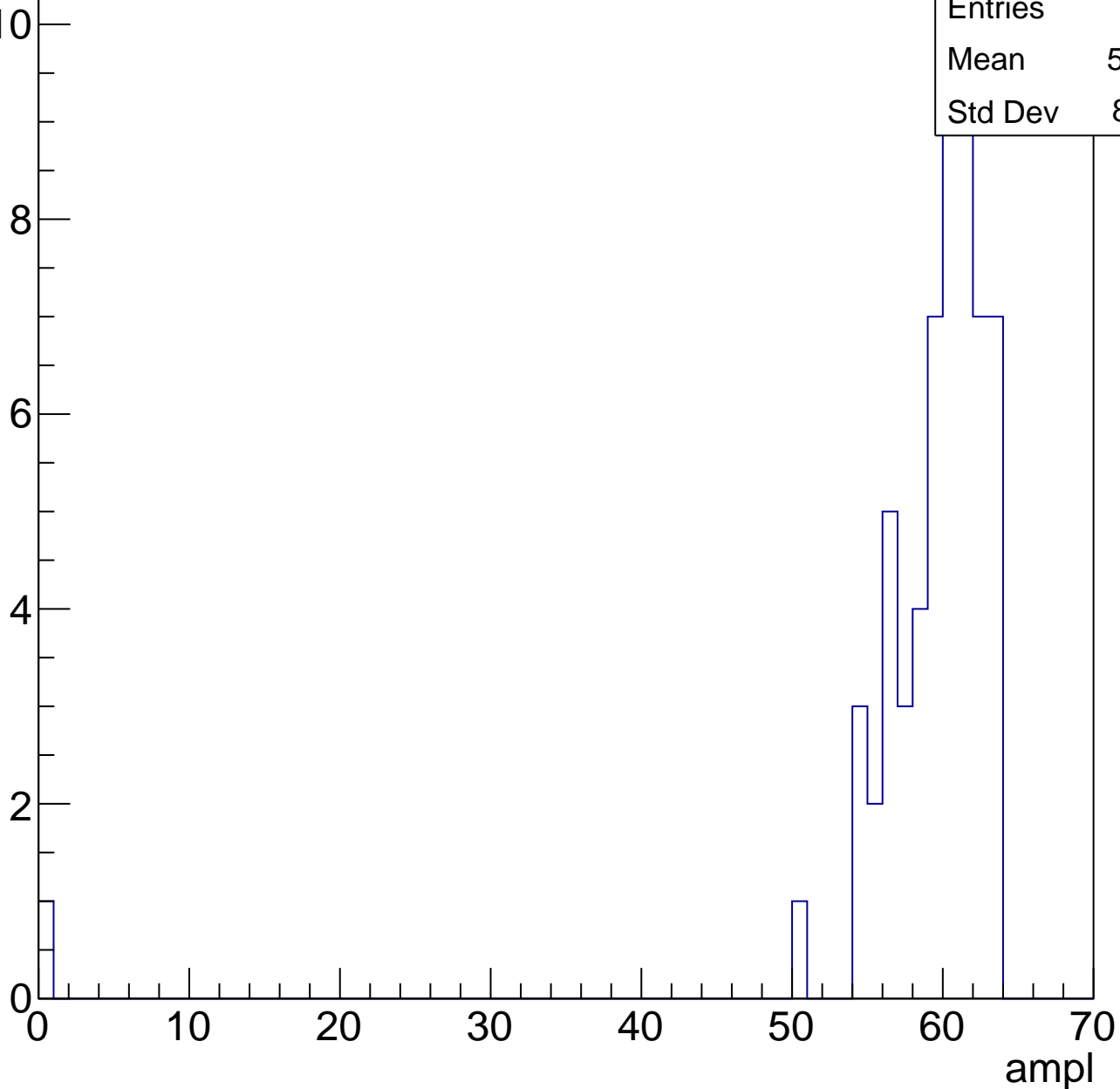


B1L102S, U9-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	58.36
Std Dev	8.161



B1L102S, U9-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch83, adc0

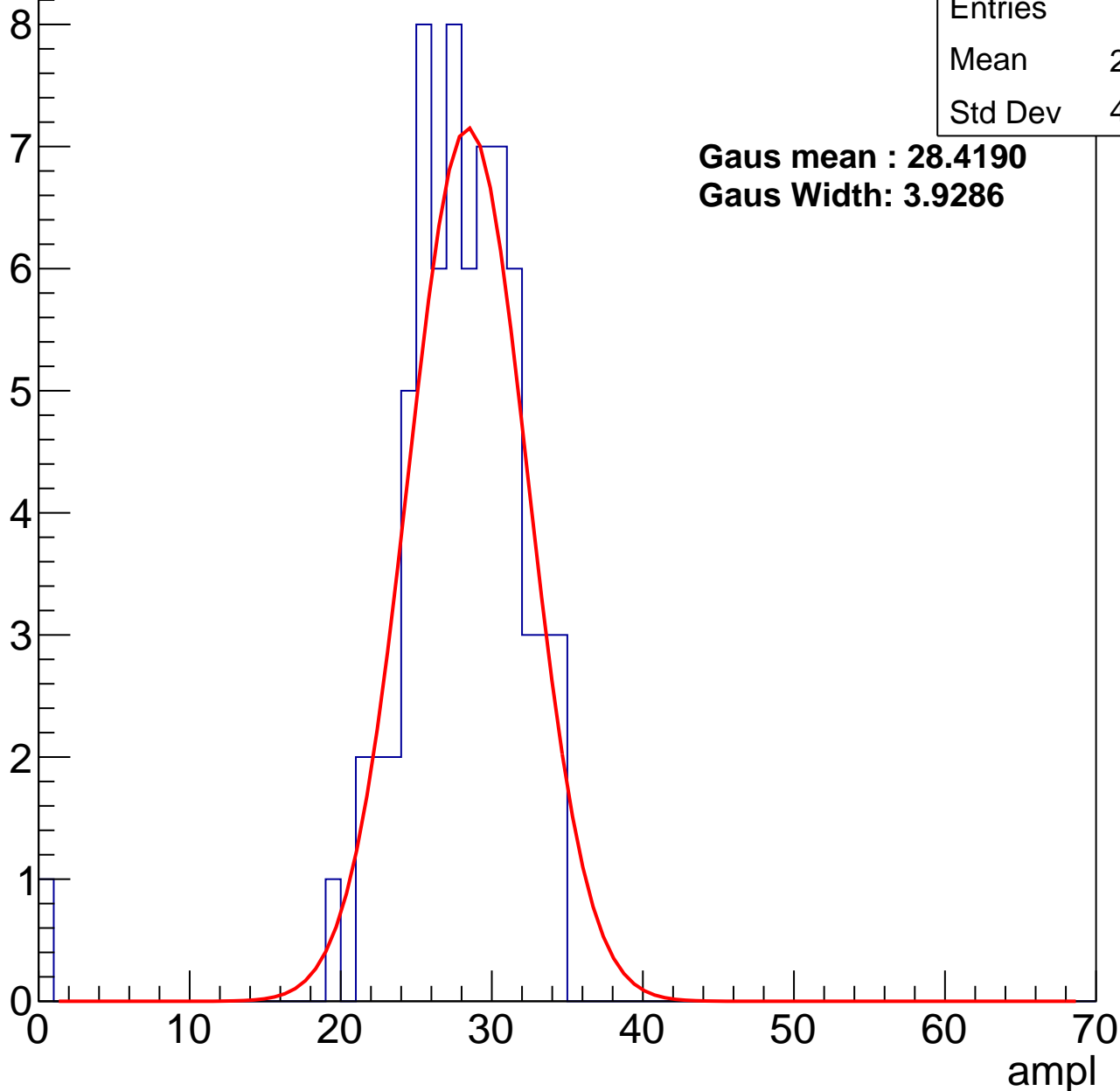
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.24
Std Dev	4.704

Gaus mean : 28.4190

Gaus Width: 3.9286



B1L102S, U9-ch83, adc1

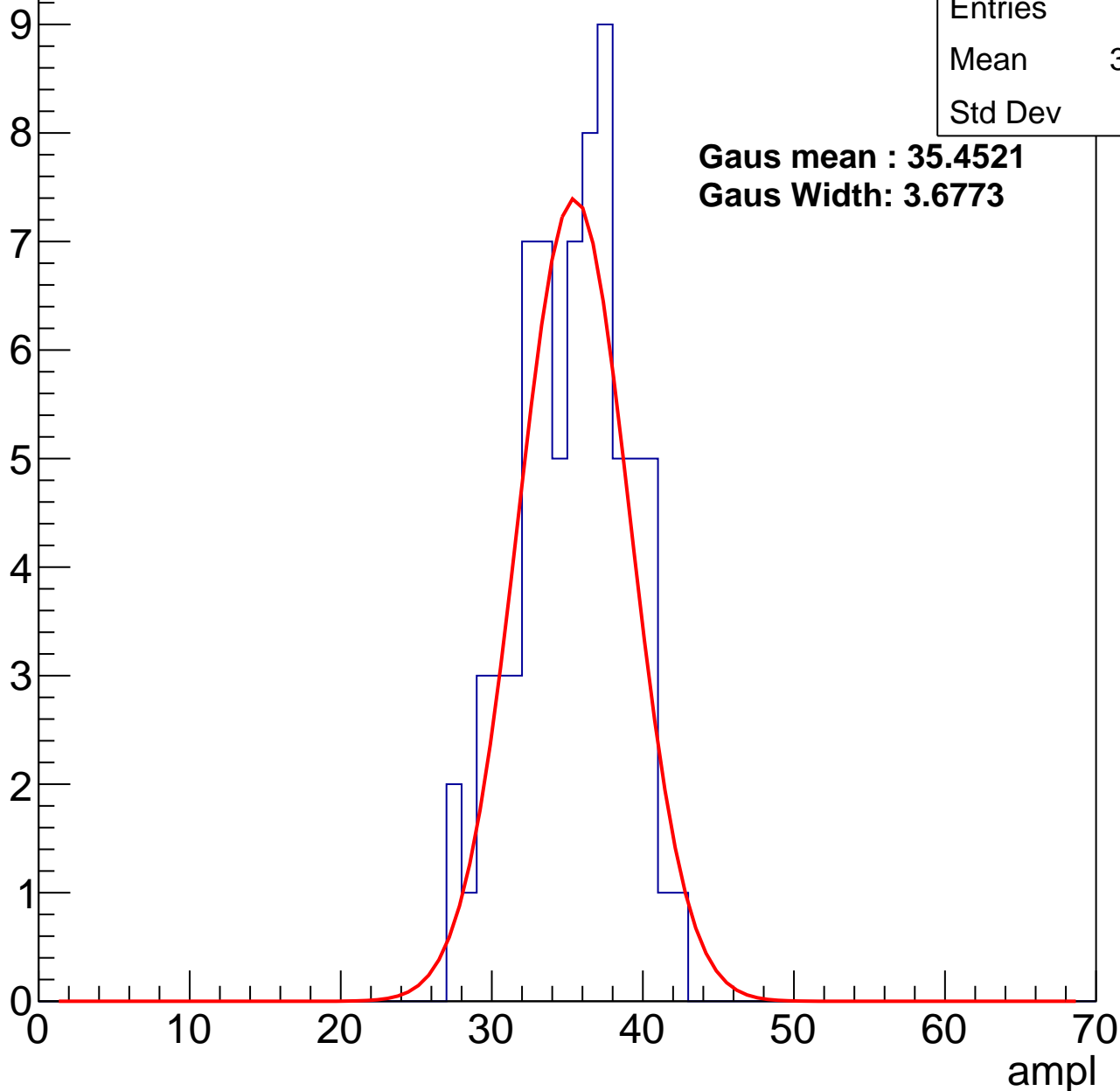
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.88
Std Dev	3.5

Gaus mean : 35.4521

Gaus Width: 3.6773



B1L102S, U9-ch83, adc2

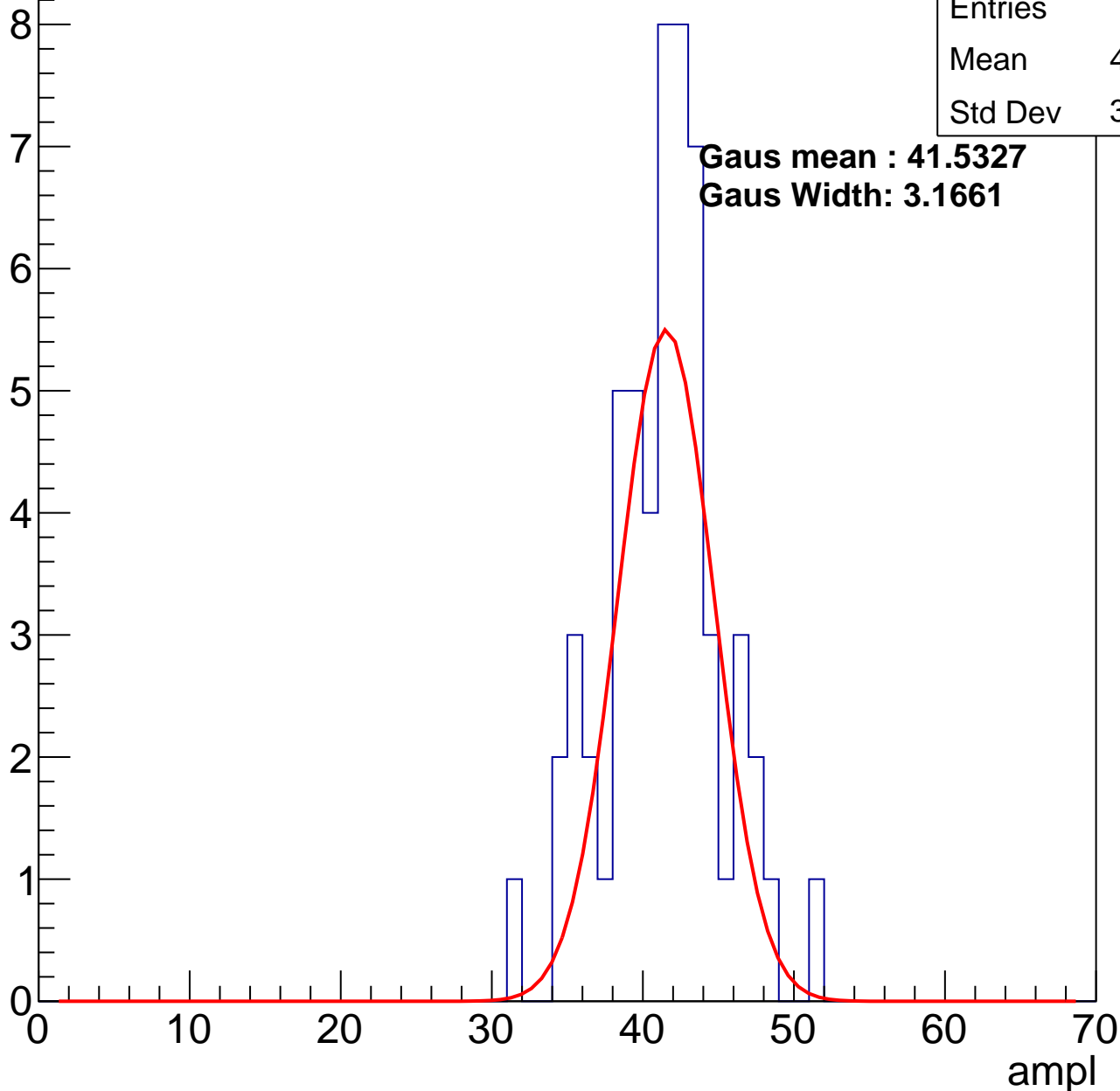
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	40.89
Std Dev	3.782

Gaus mean : 41.5327

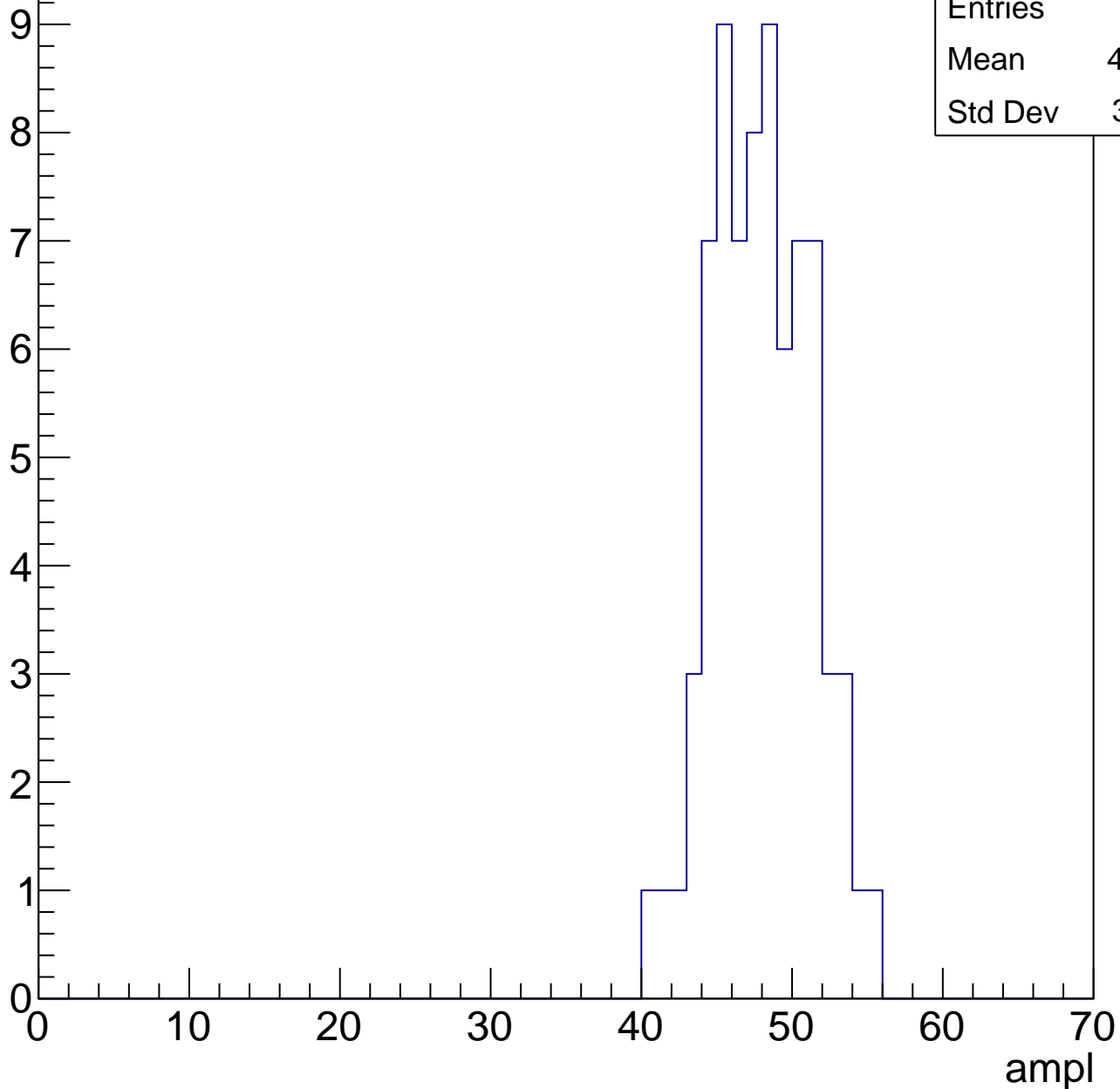
Gaus Width: 3.1661



B1L102S, U9-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

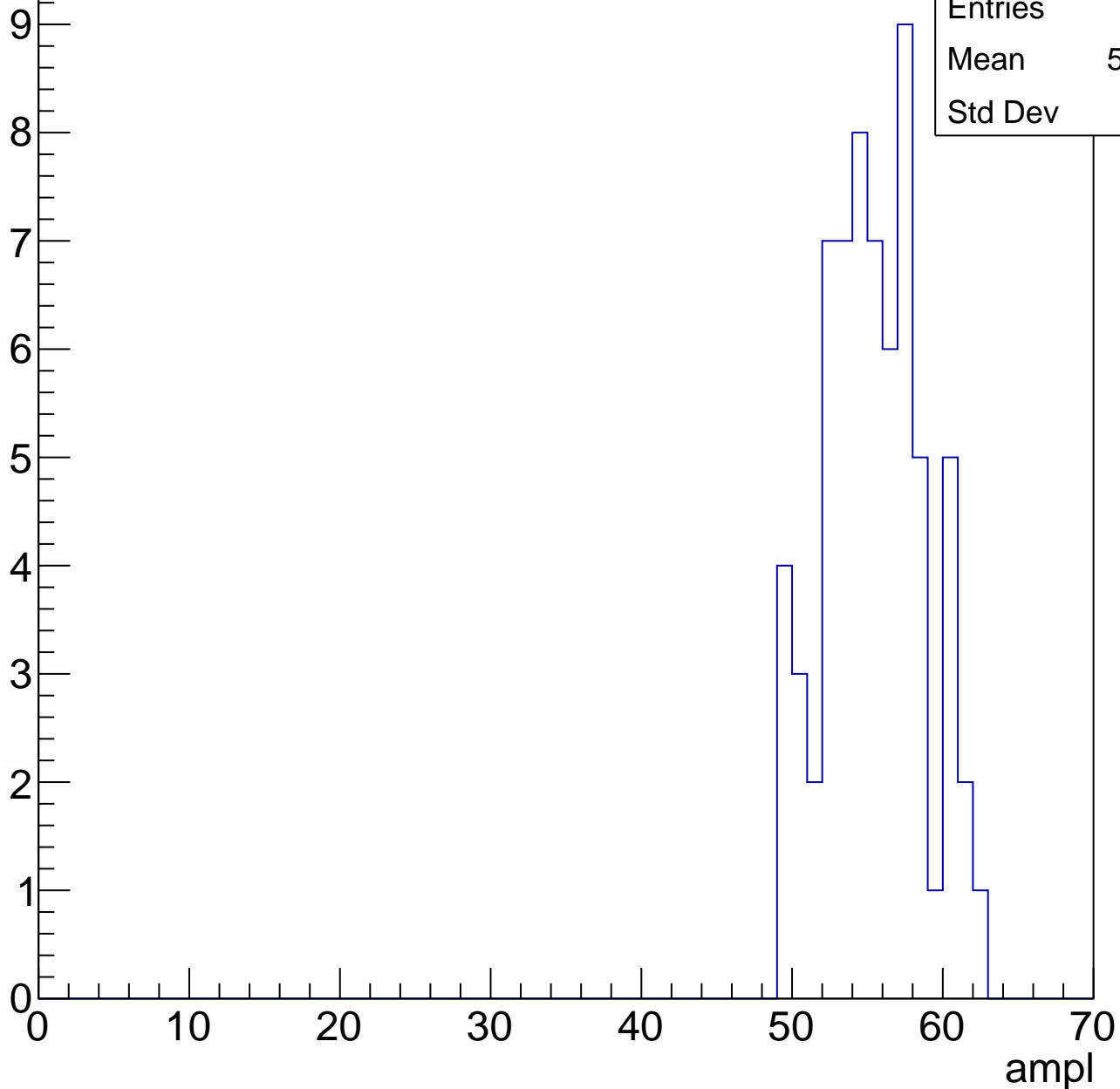


Entries	74
Mean	47.57
Std Dev	3.171

B1L102S, U9-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

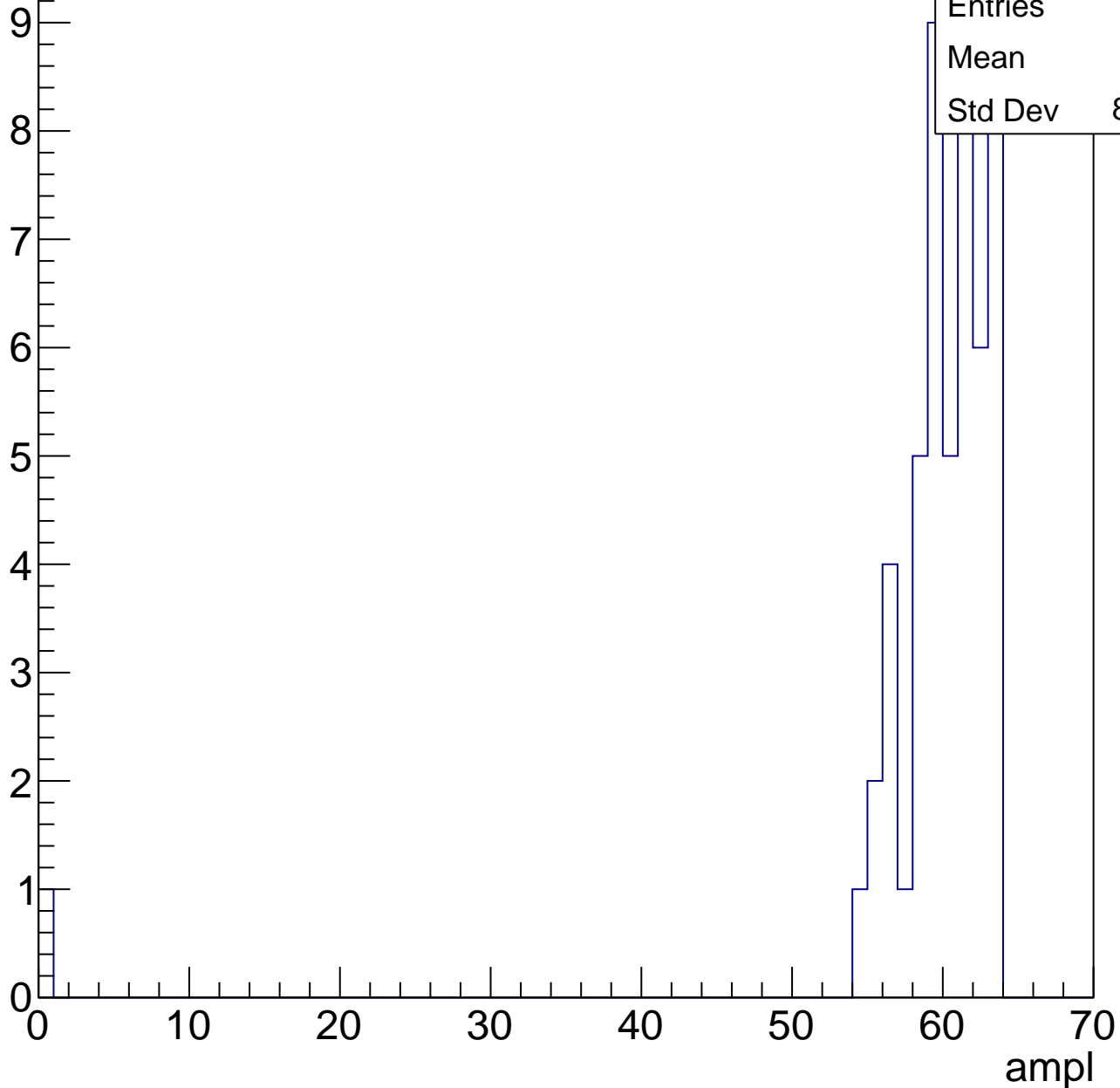
Entry



B1L102S, U9-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch84, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	27.44
Std Dev	3.908

Gaus mean : 27.9286

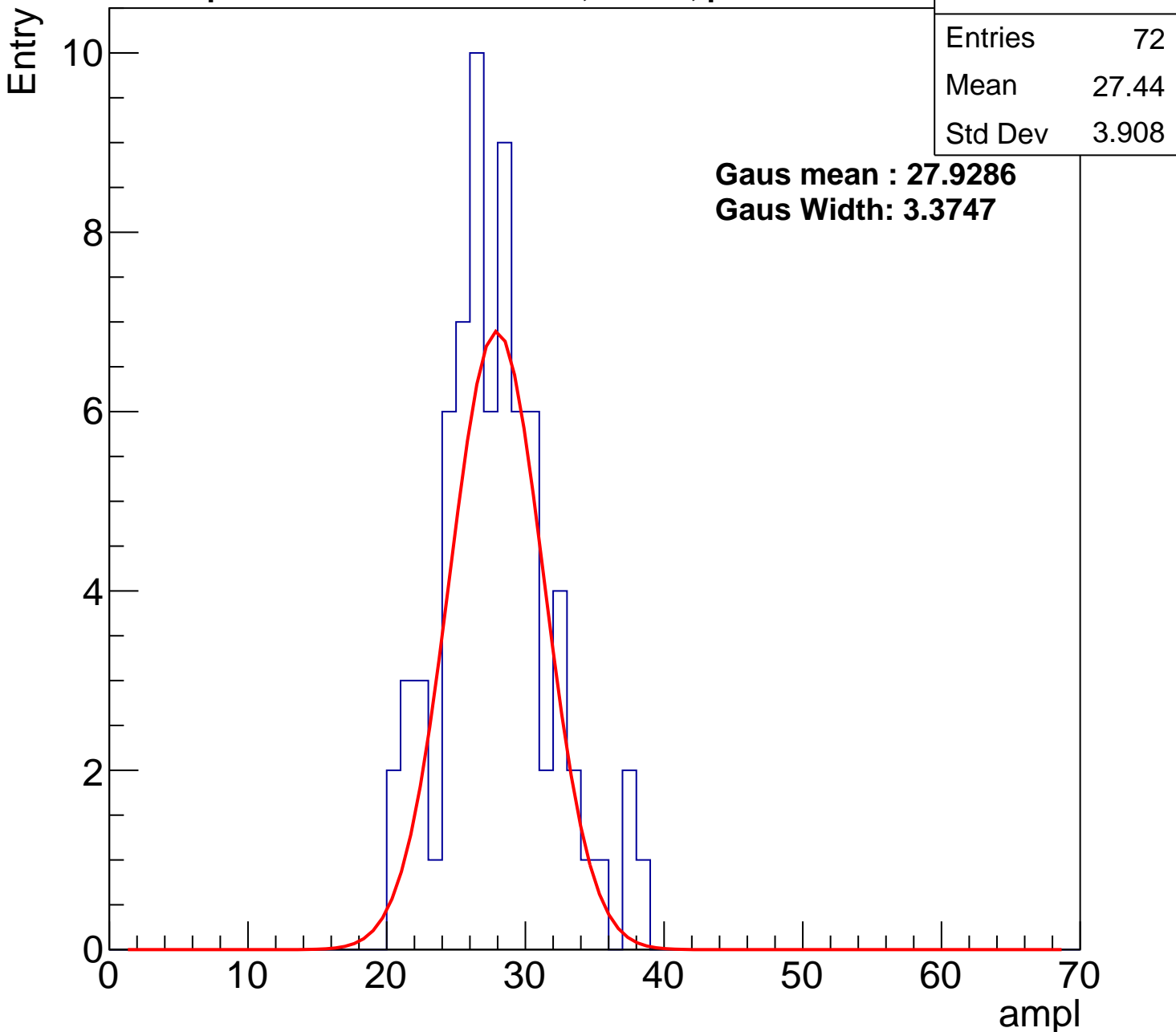
Gaus Width: 3.3747

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch84, adc1

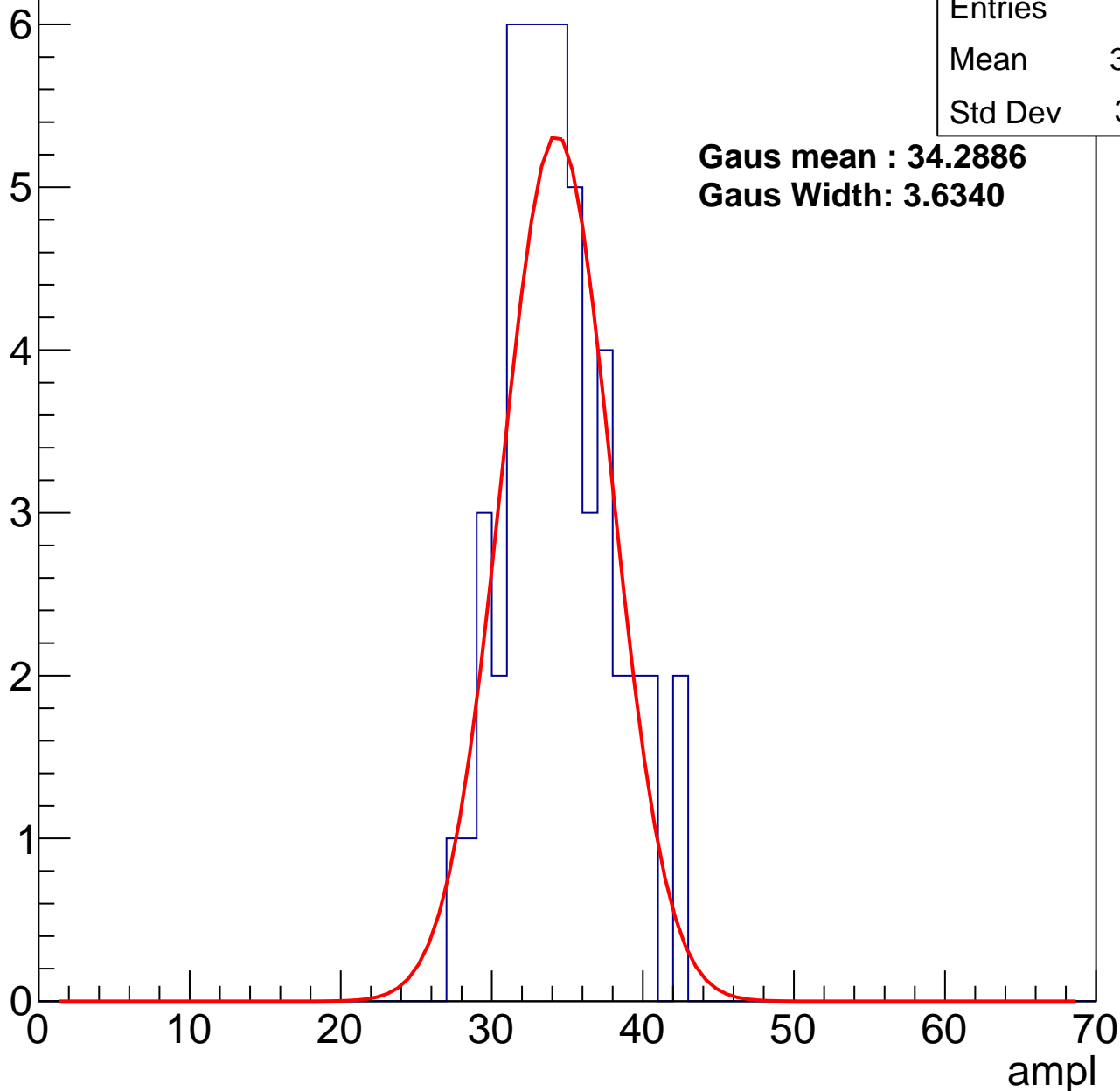
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	33.94
Std Dev	3.461

Gaus mean : 34.2886

Gaus Width: 3.6340



B1L102S, U9-ch84, adc2

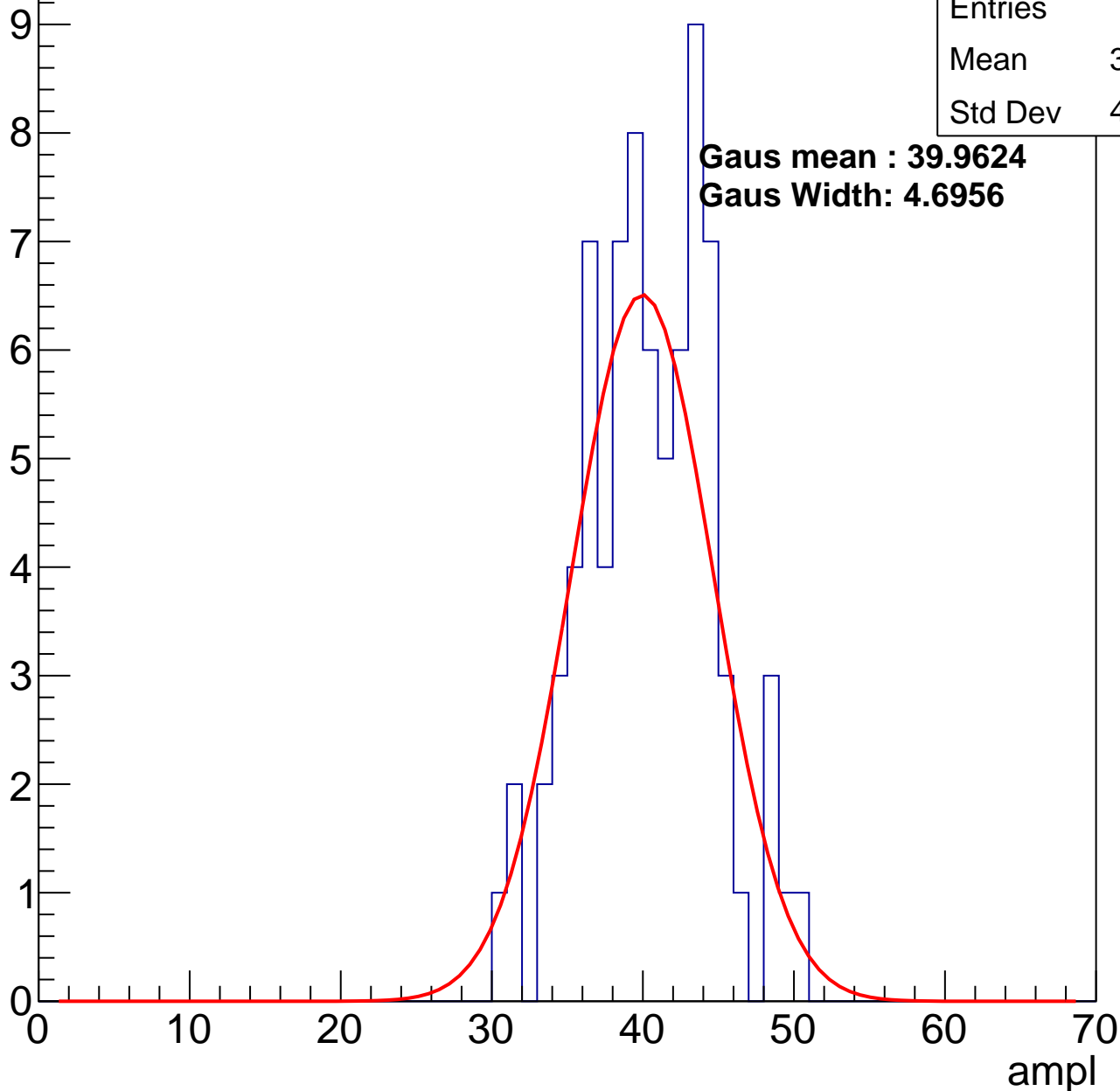
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	39.92
Std Dev	4.268

Gaus mean : 39.9624

Gaus Width: 4.6956

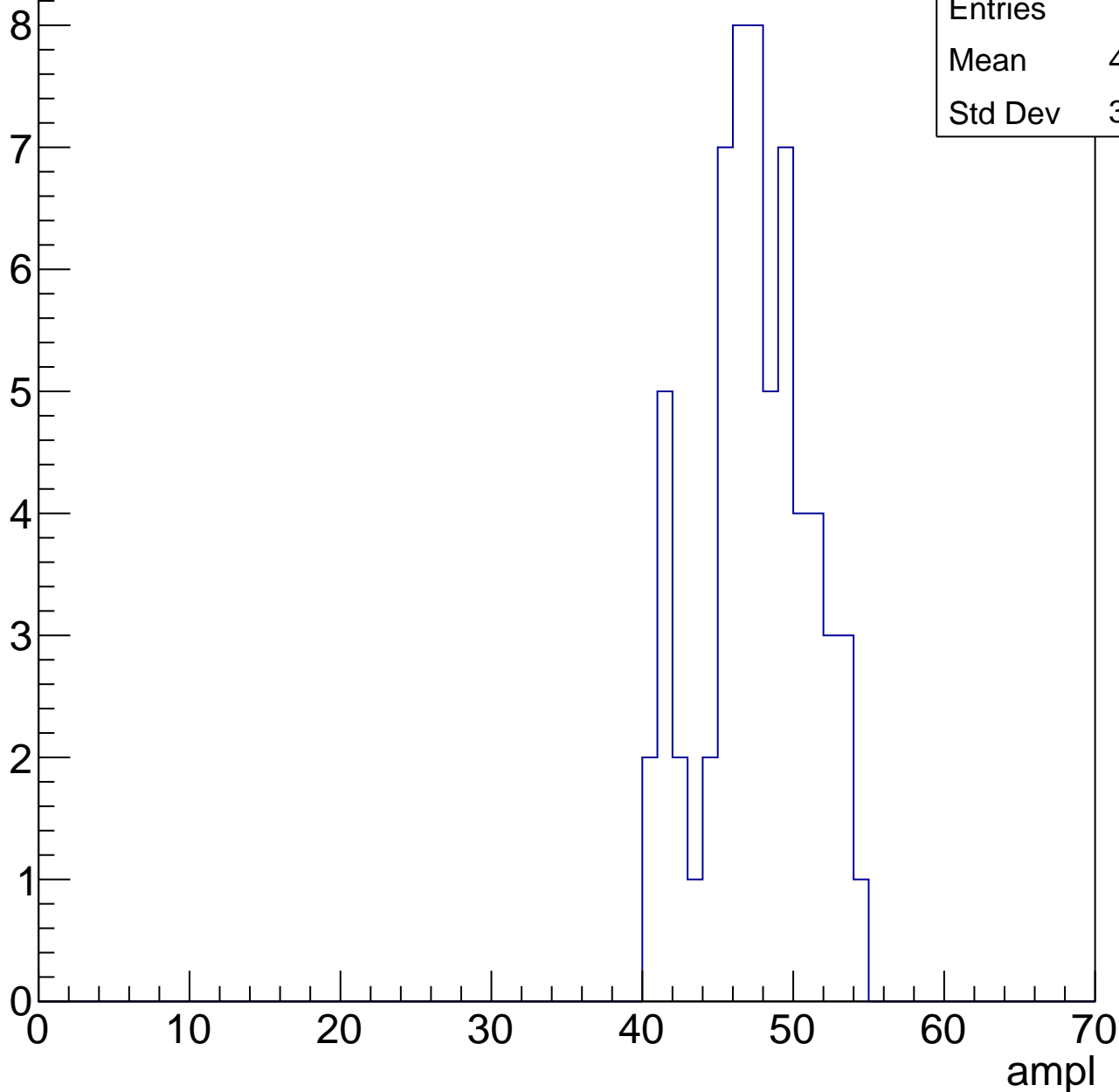


B1L102S, U9-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	47.02
Std Dev	3.508

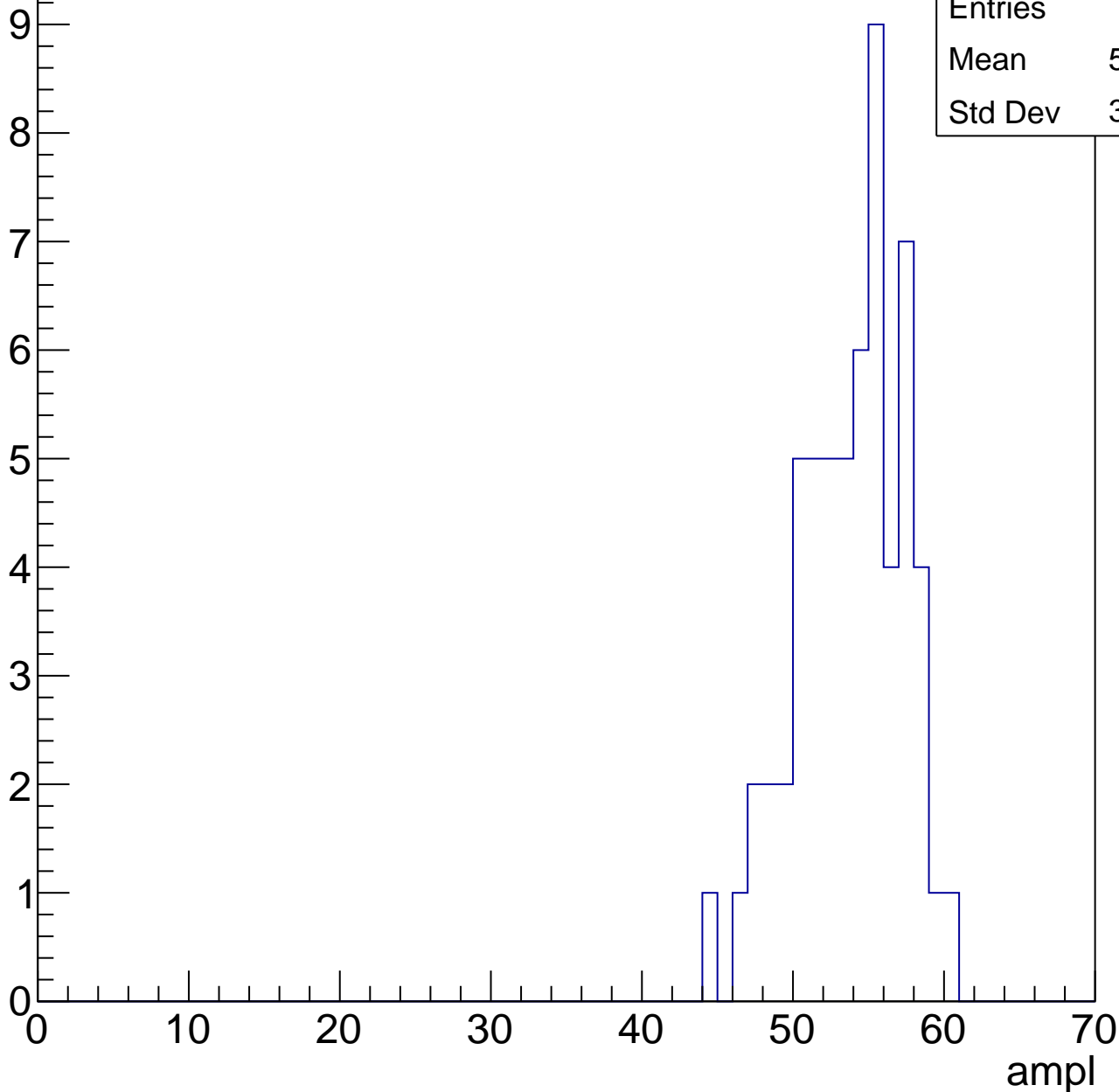


B1L102S, U9-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

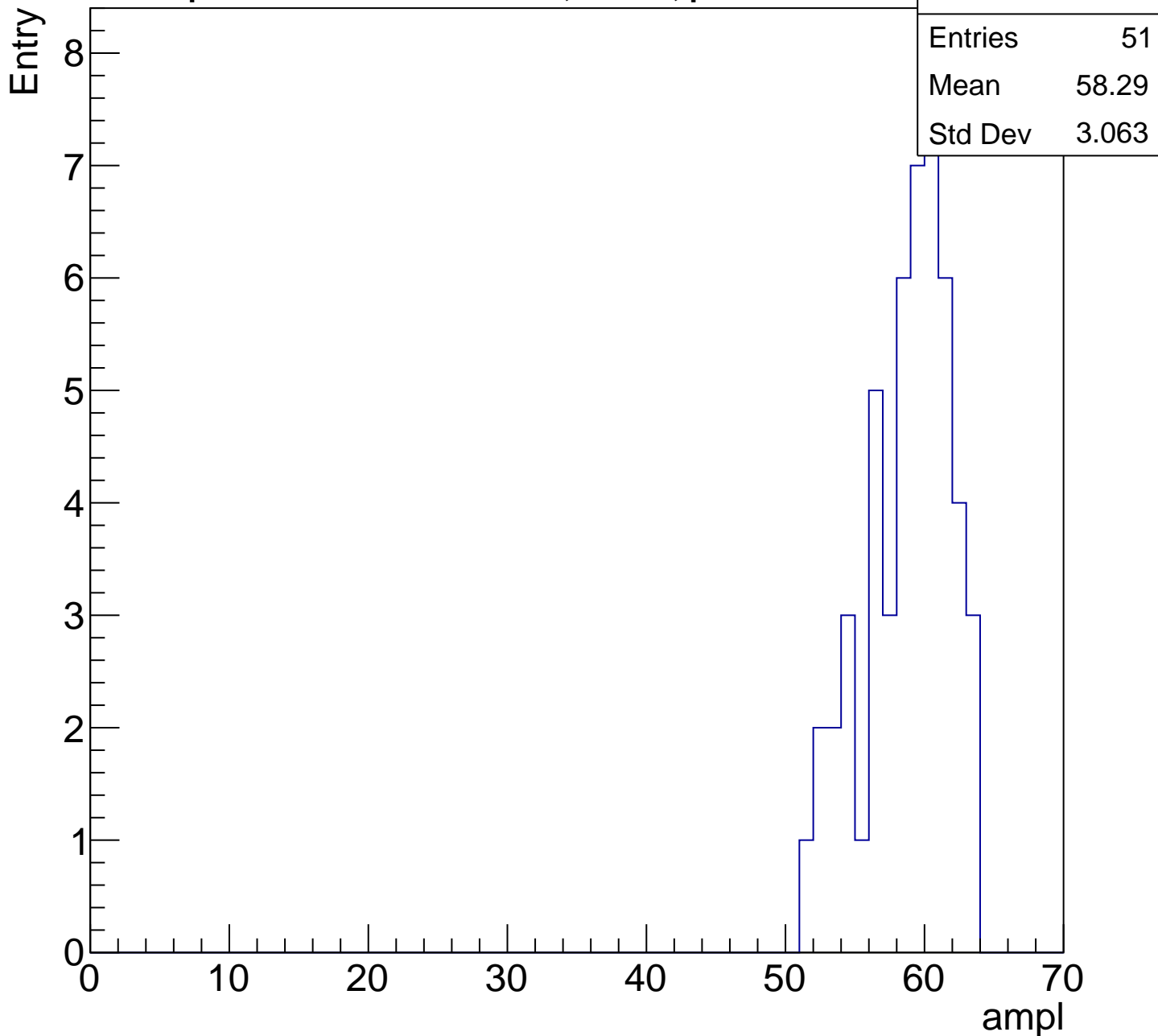
Entry

Entries	60
Mean	53.35
Std Dev	3.468



B1L102S, U9-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

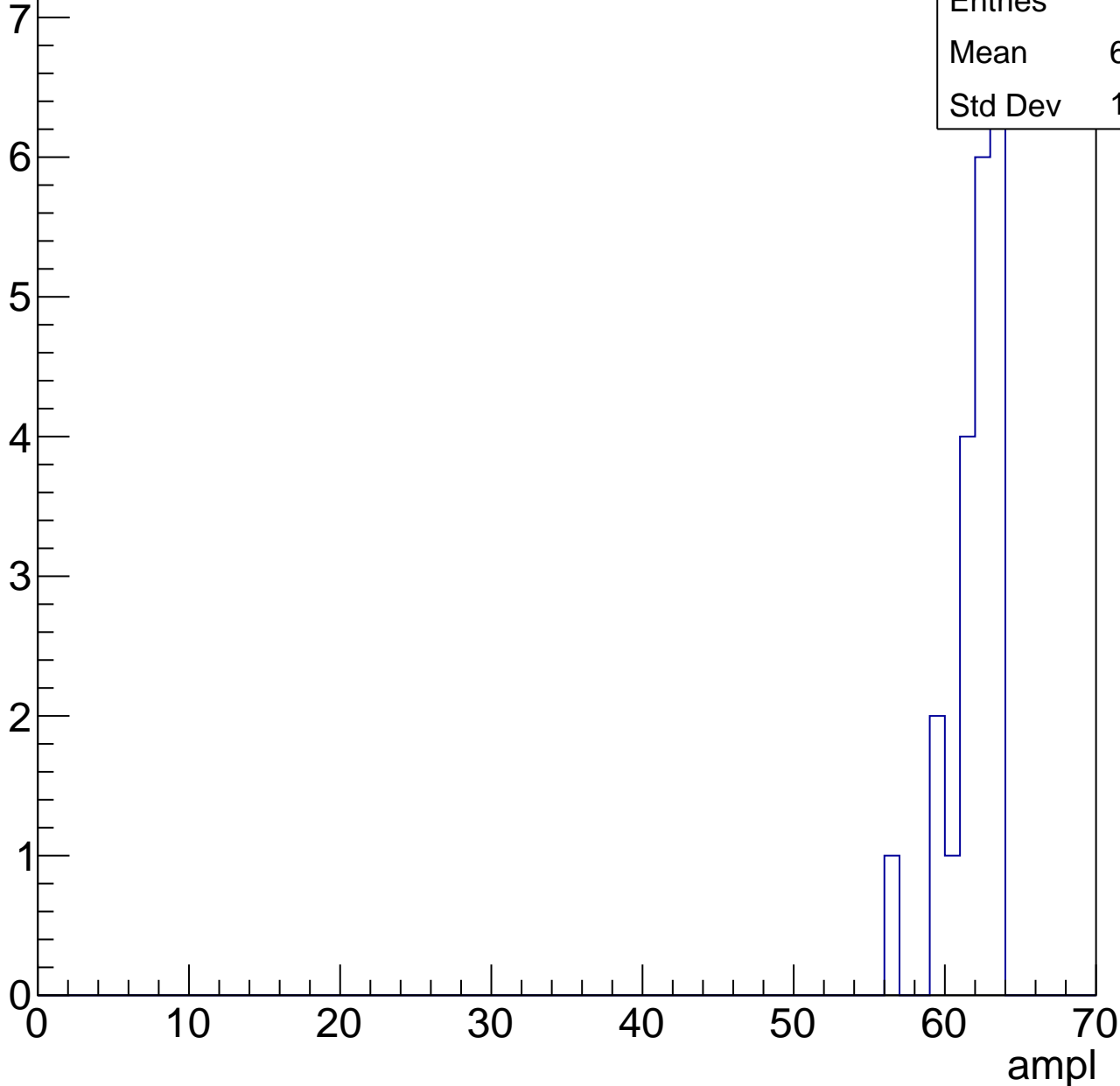


B1L102S, U9-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	61.48
Std Dev	1.735



B1L102S, U9-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch85, adc0

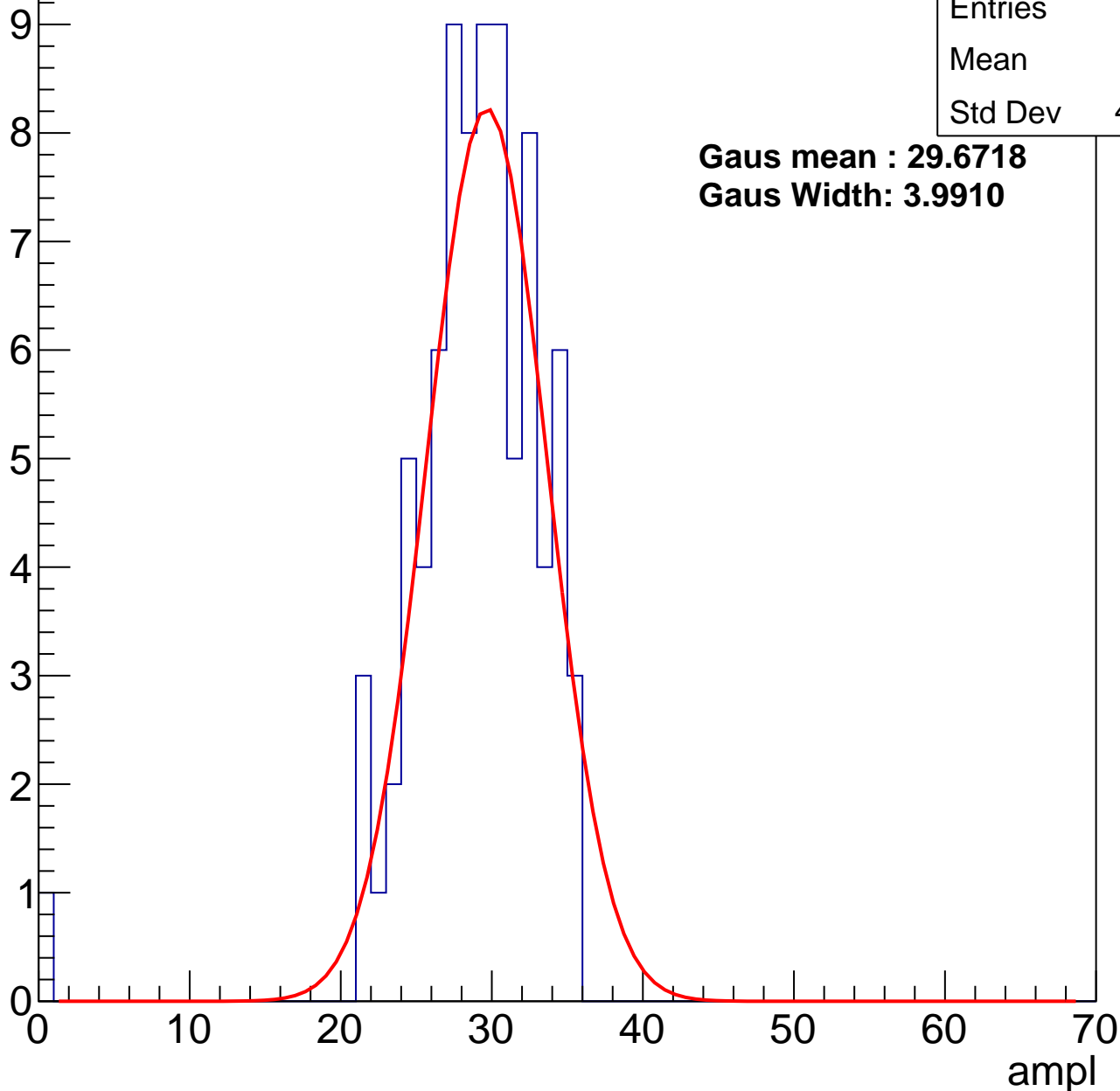
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.4
Std Dev	4.701

Gaus mean : 29.6718

Gaus Width: 3.9910



B1L102S, U9-ch85, adc1

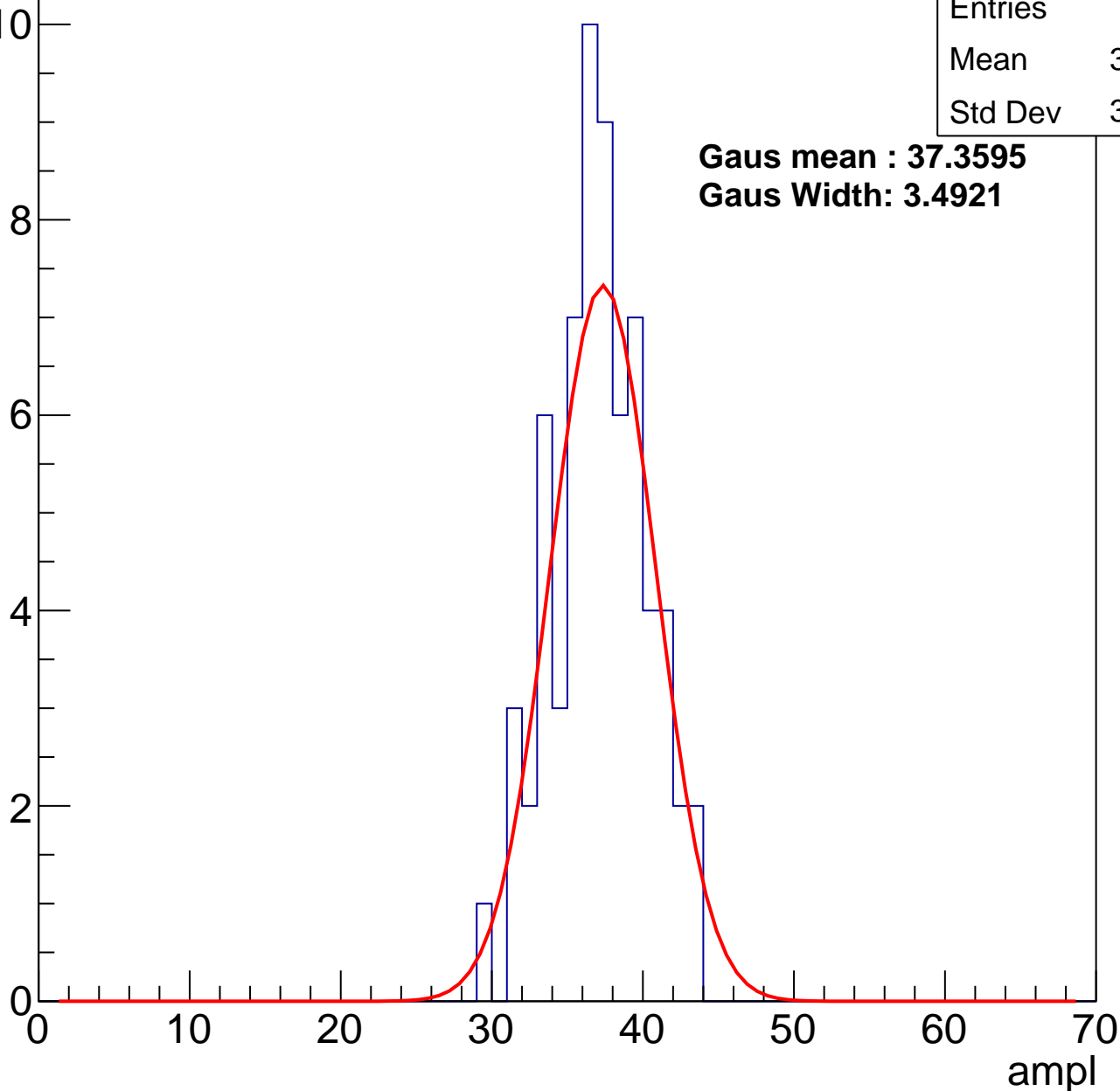
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.65
Std Dev	3.092

Gaus mean : 37.3595

Gaus Width: 3.4921



B1L102S, U9-ch85, adc2

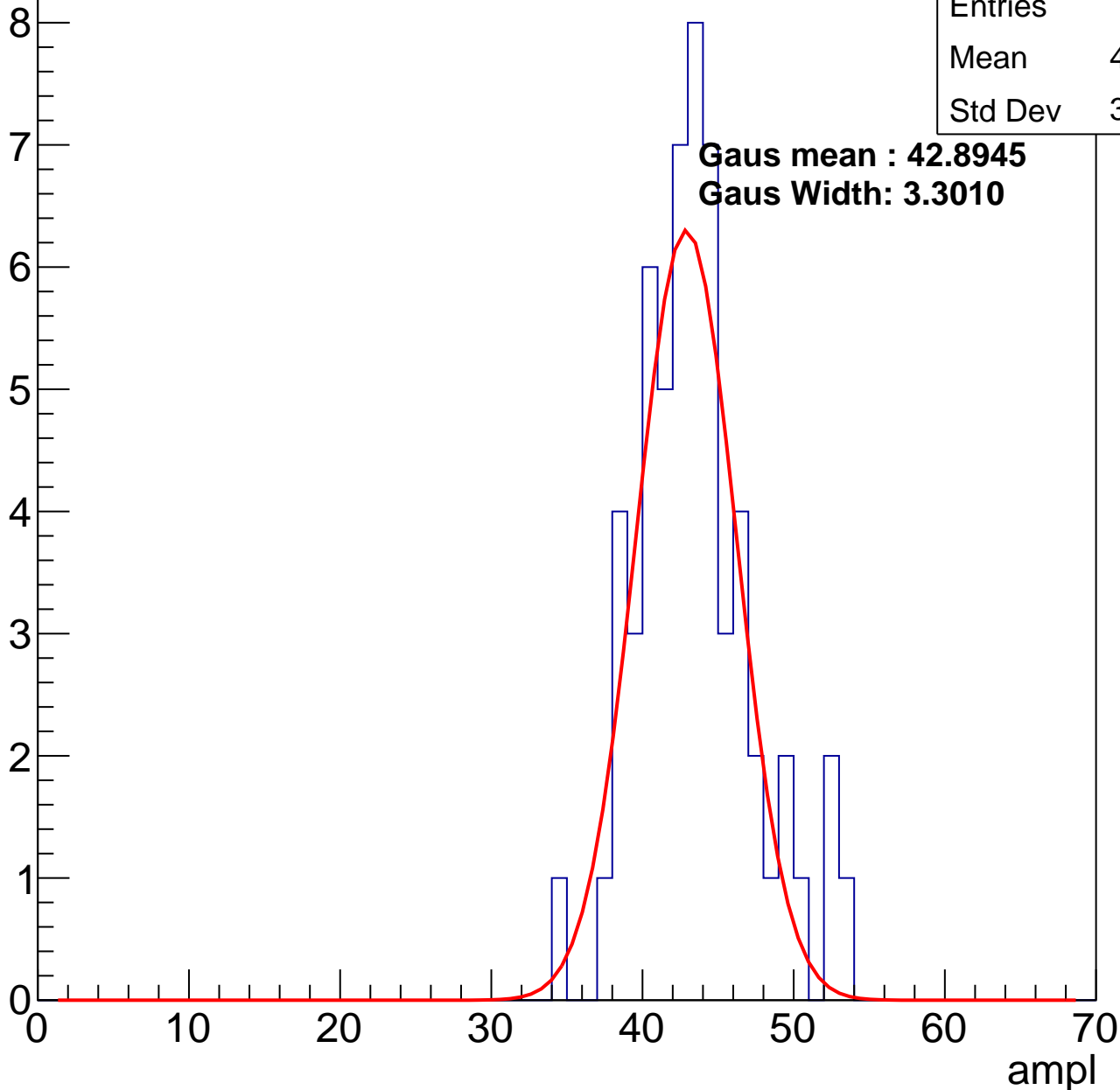
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.05
Std Dev	3.808

Gaus mean : 42.8945

Gaus Width: 3.3010

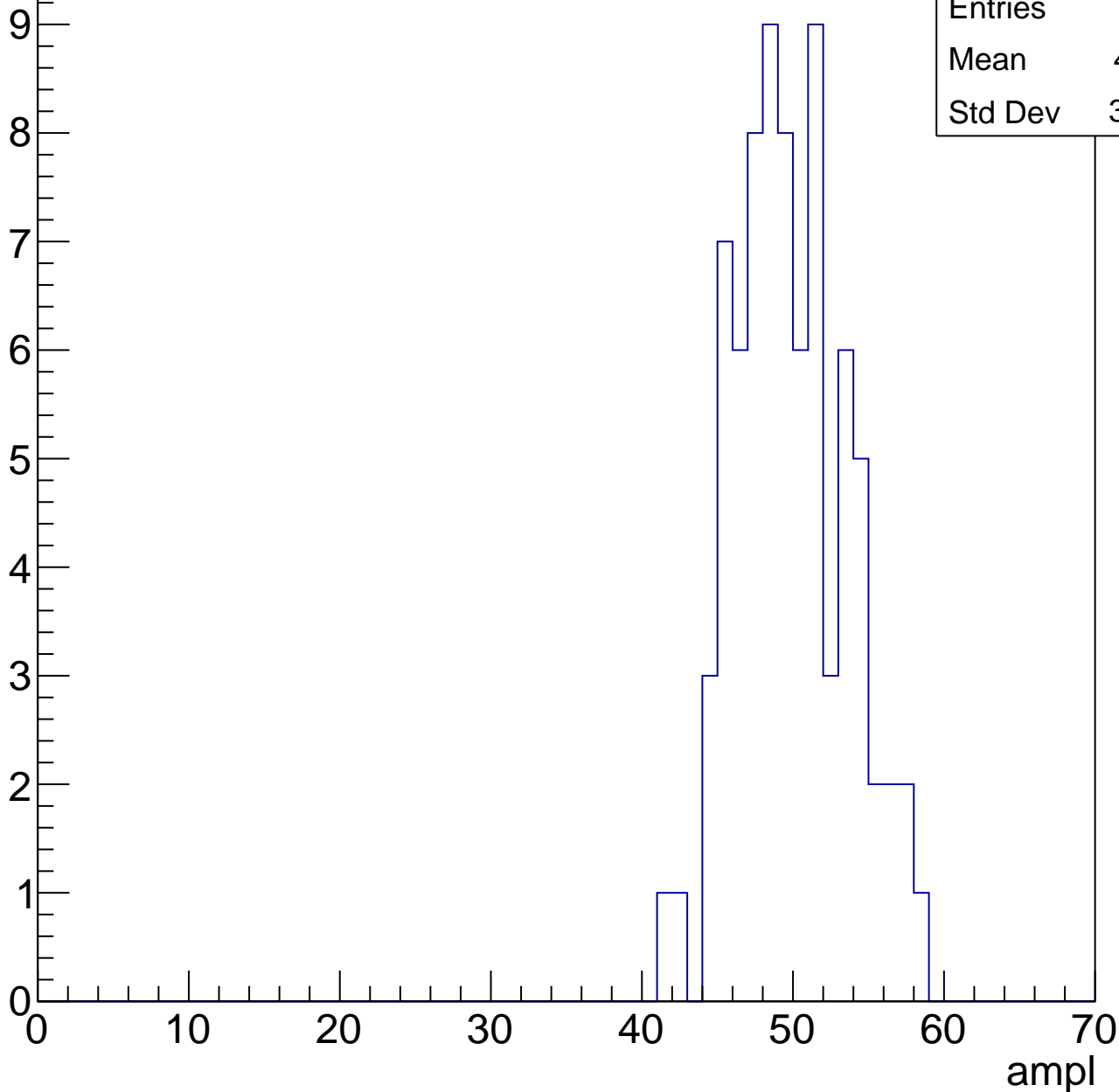


B1L102S, U9-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	49.41
Std Dev	3.648

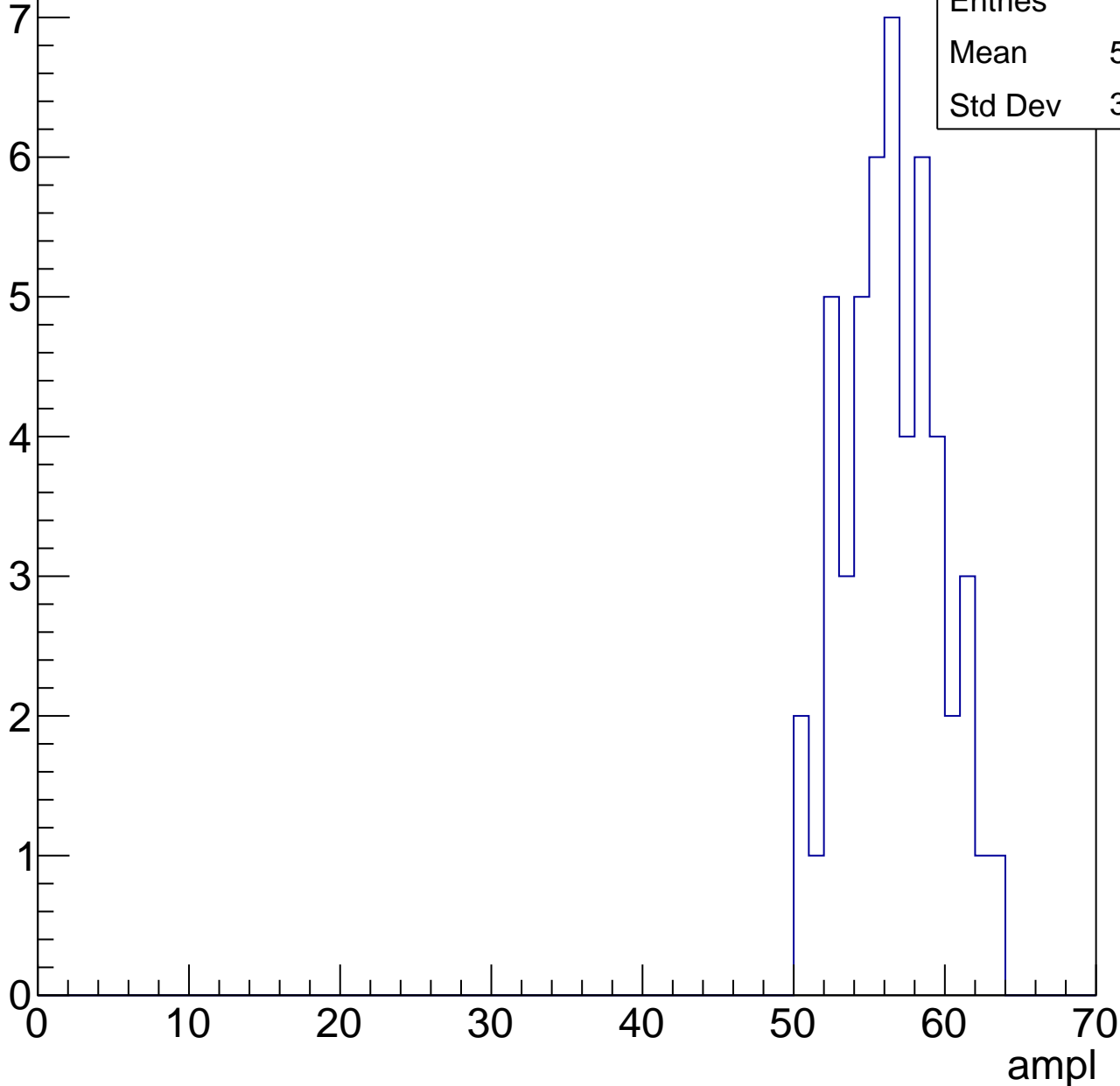


B1L102S, U9-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

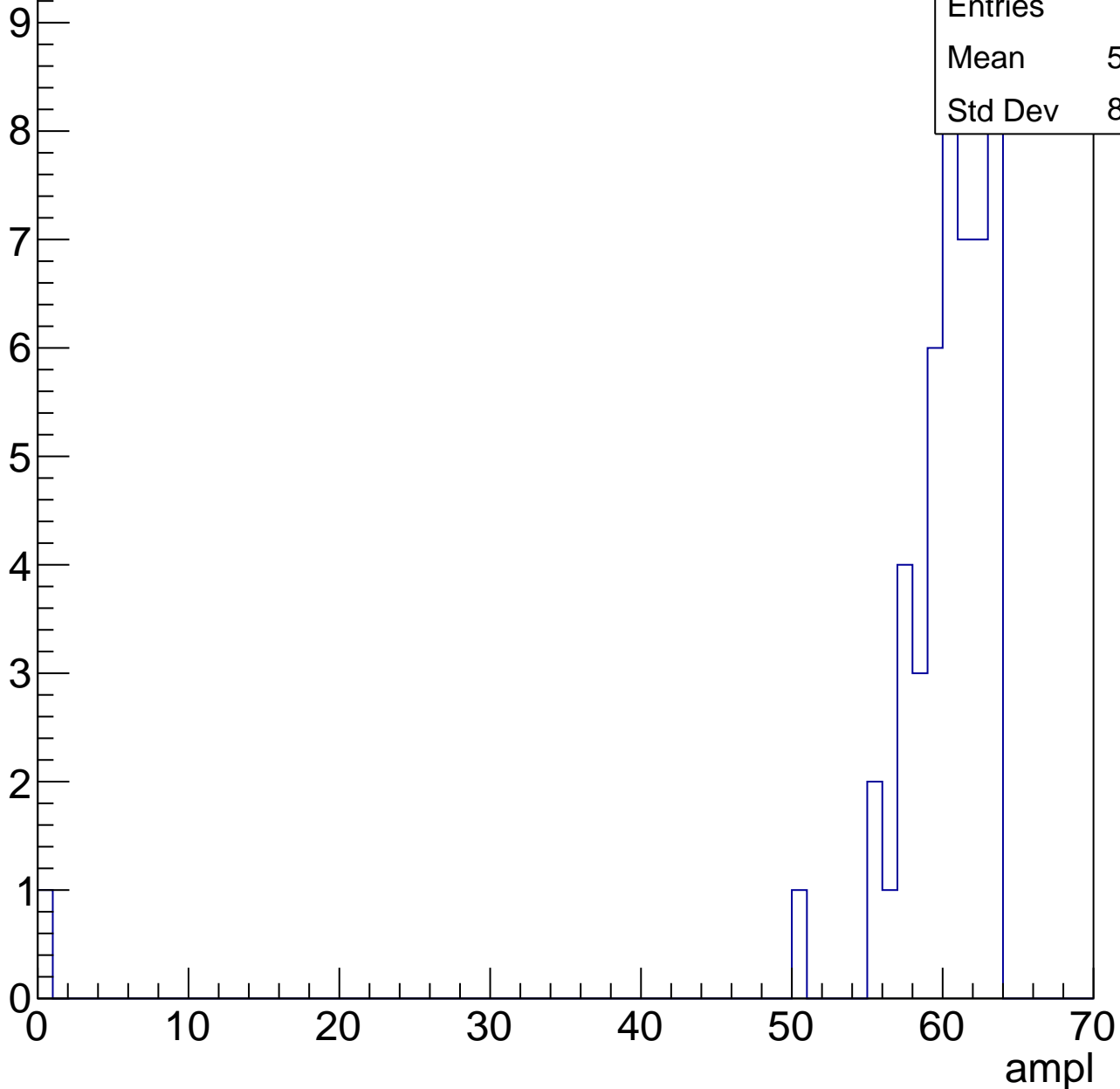
Entries	50
Mean	56.04
Std Dev	3.117



B1L102S, U9-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B1L102S, U9-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch86, adc0

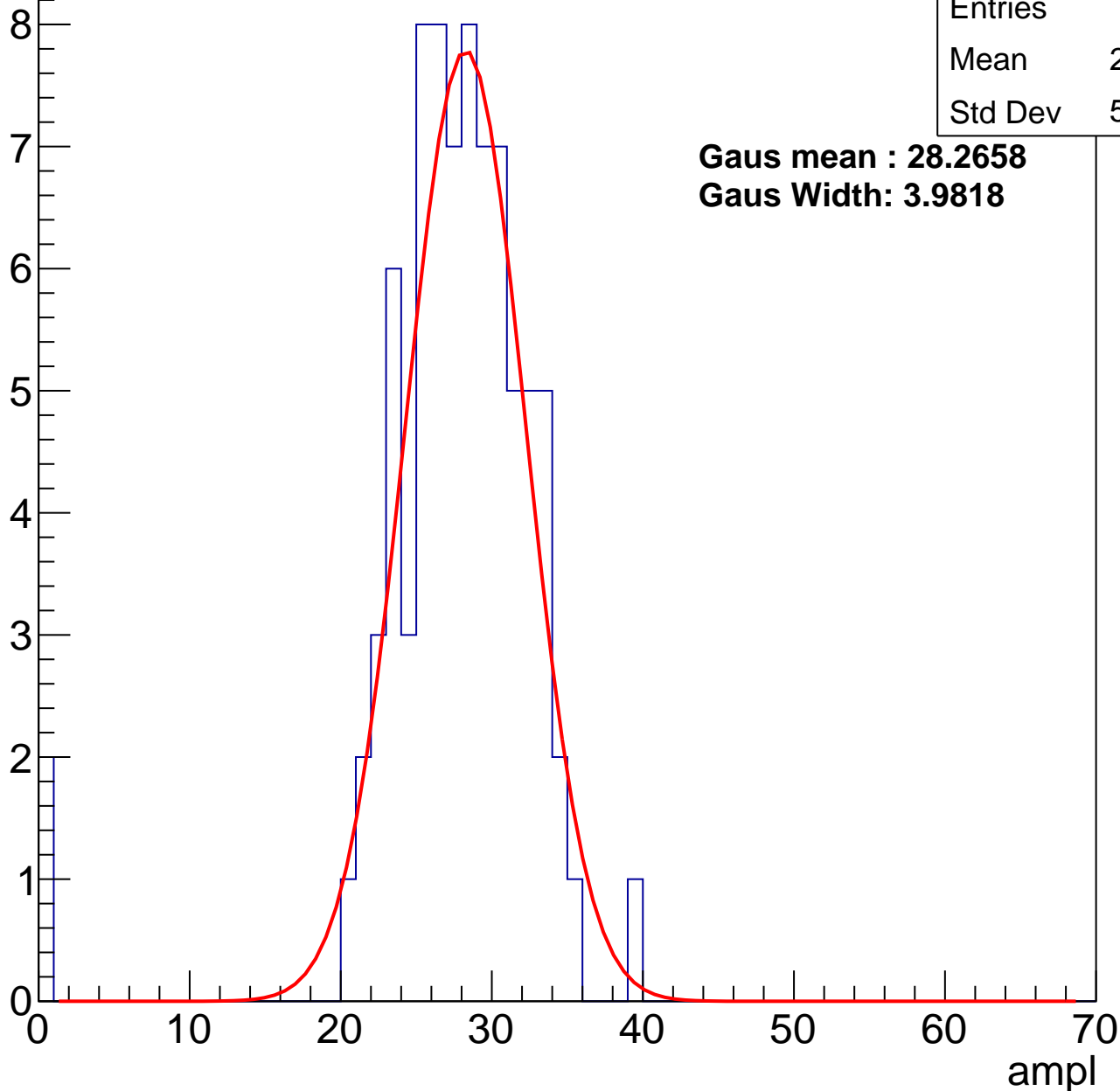
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.09
Std Dev	5.673

Gaus mean : 28.2658

Gaus Width: 3.9818



B1L102S, U9-ch86, adc1

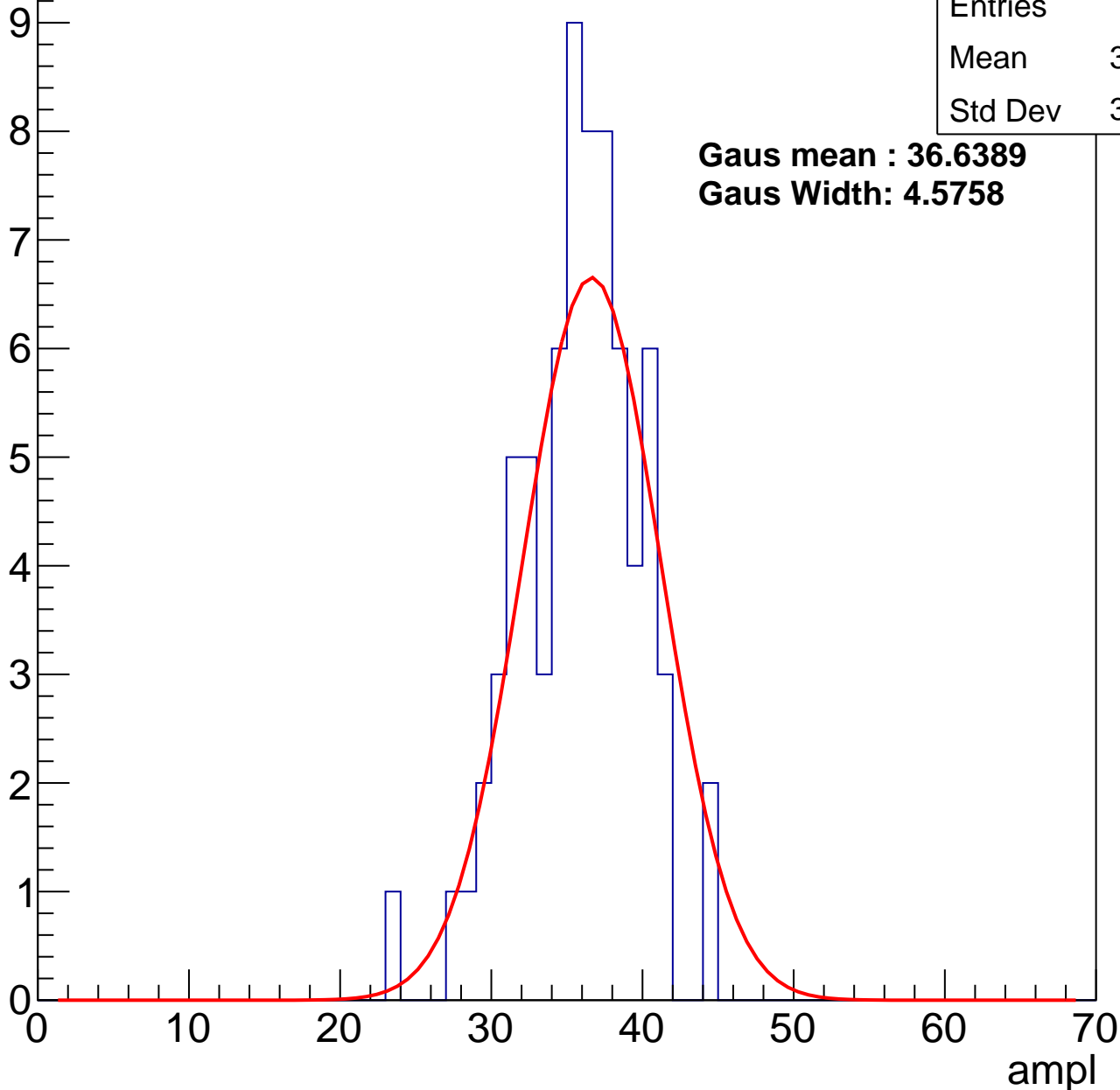
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.32
Std Dev	3.913

Gaus mean : 36.6389

Gaus Width: 4.5758



B1L102S, U9-ch86, adc2

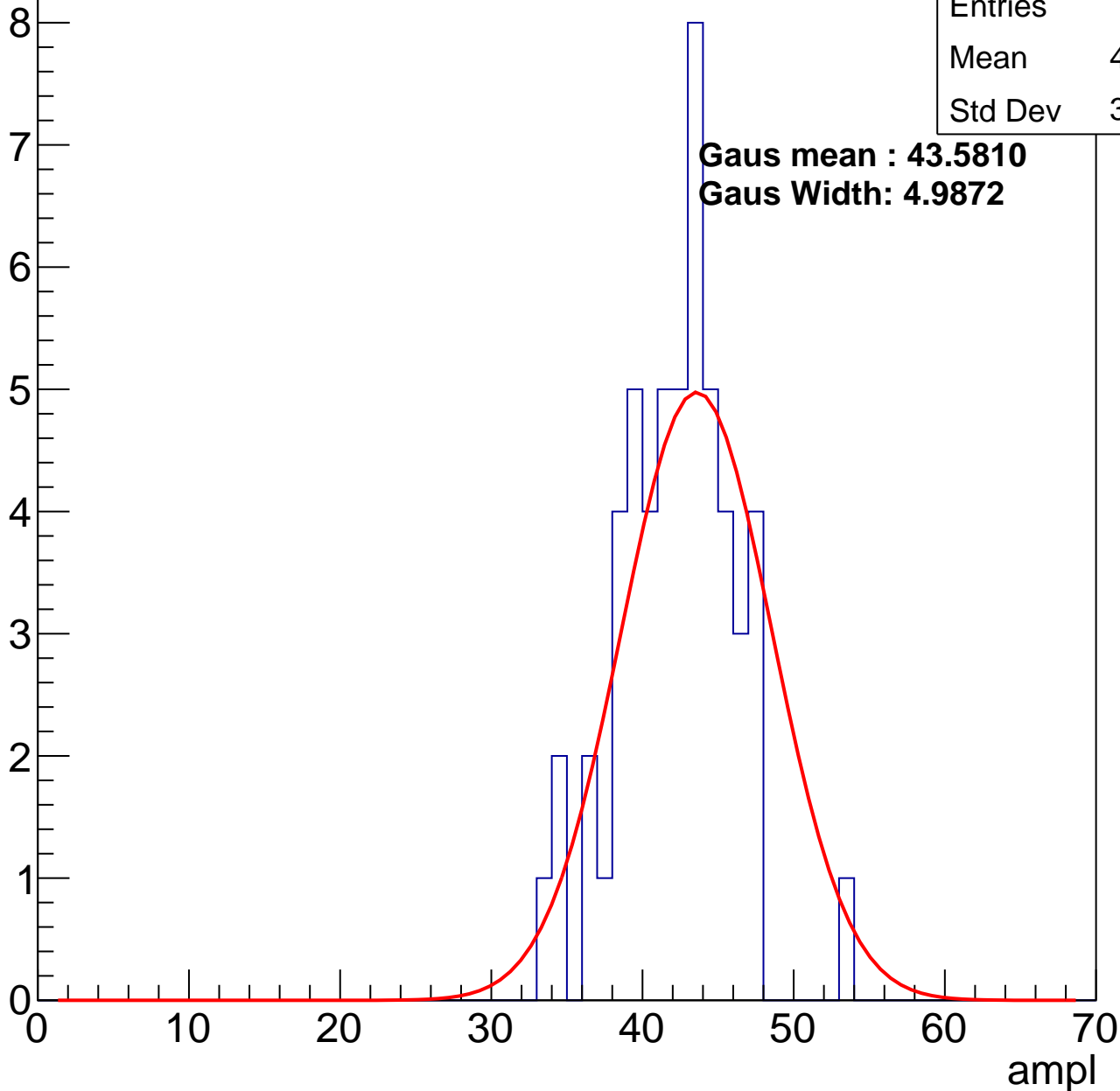
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.76
Std Dev	3.766

Gaus mean : 43.5810

Gaus Width: 4.9872

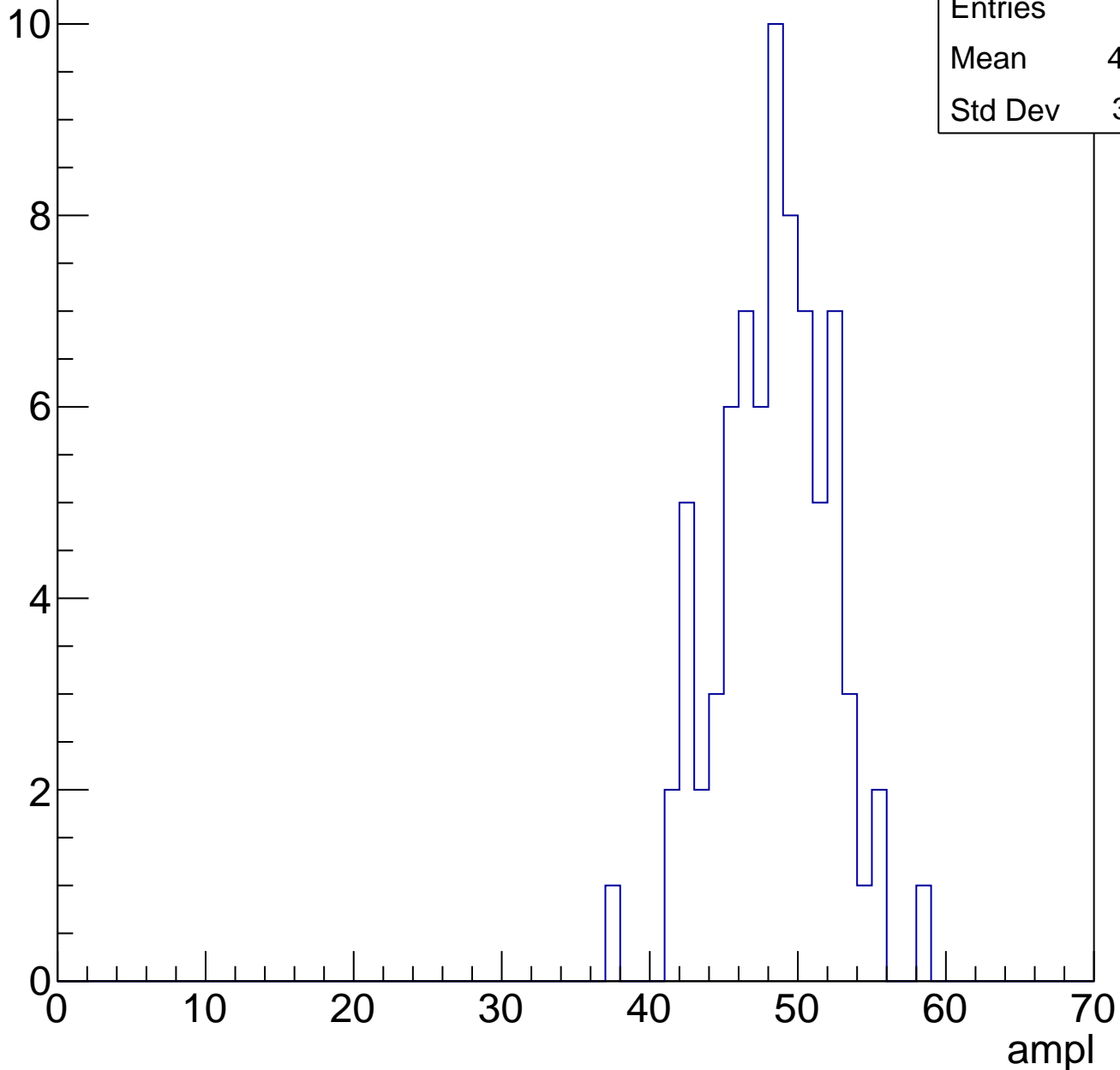


B1L102S, U9-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	47.93
Std Dev	3.781

Entry

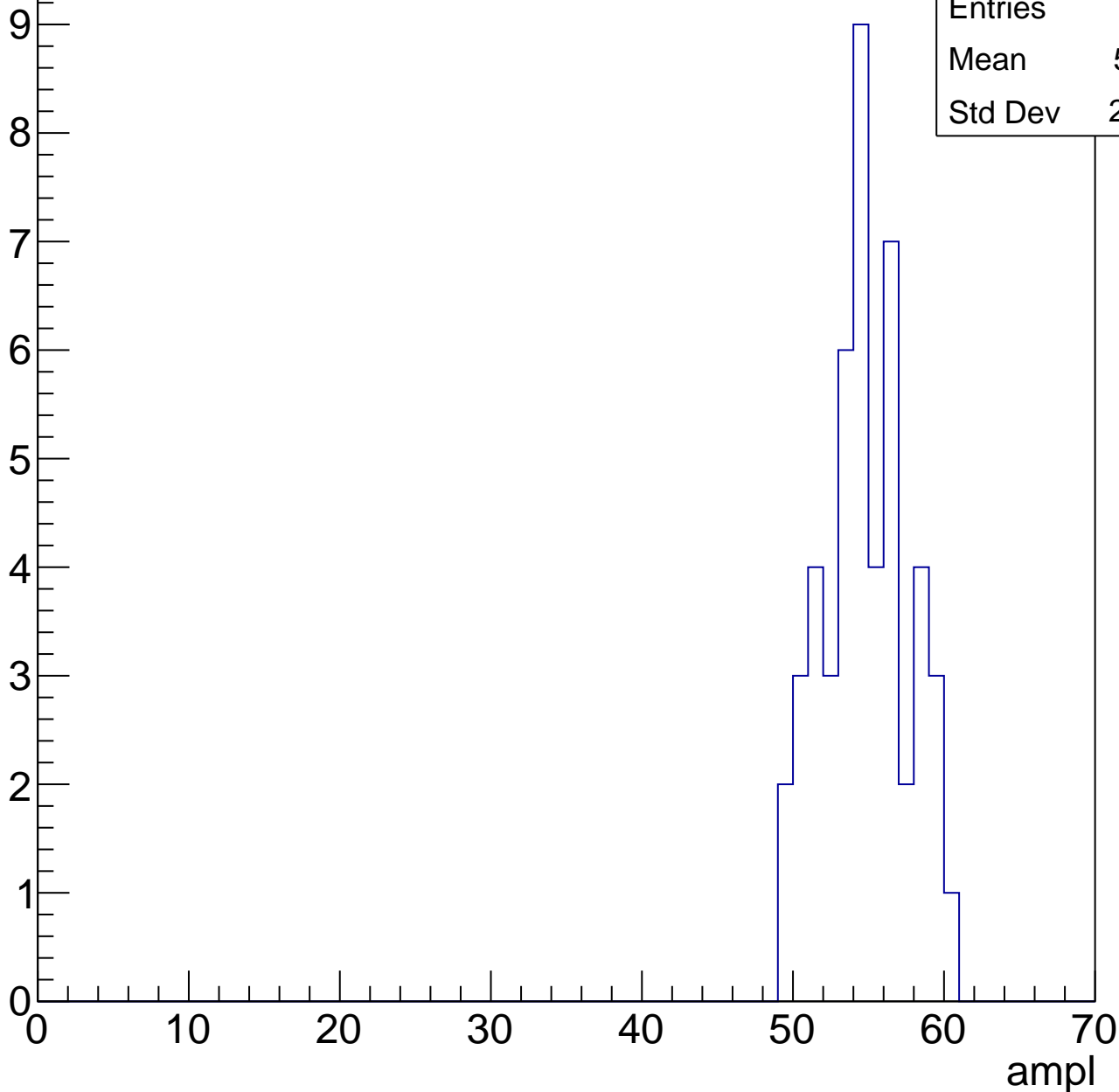


B1L102S, U9-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.31
Std Dev	2.785

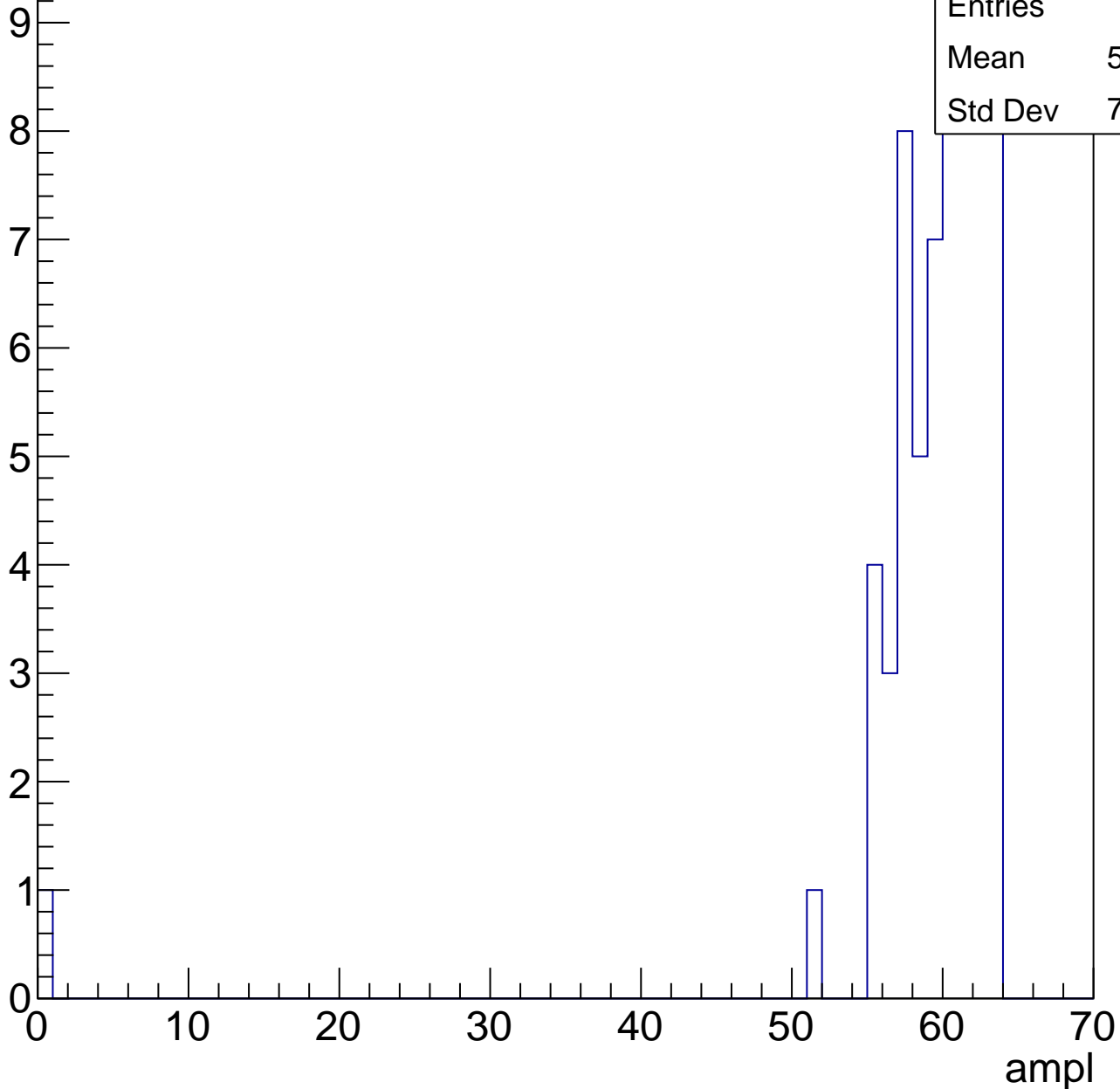


B1L102S, U9-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	58.54
Std Dev	7.878



B1L102S, U9-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L102S, U9-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch87, adc0

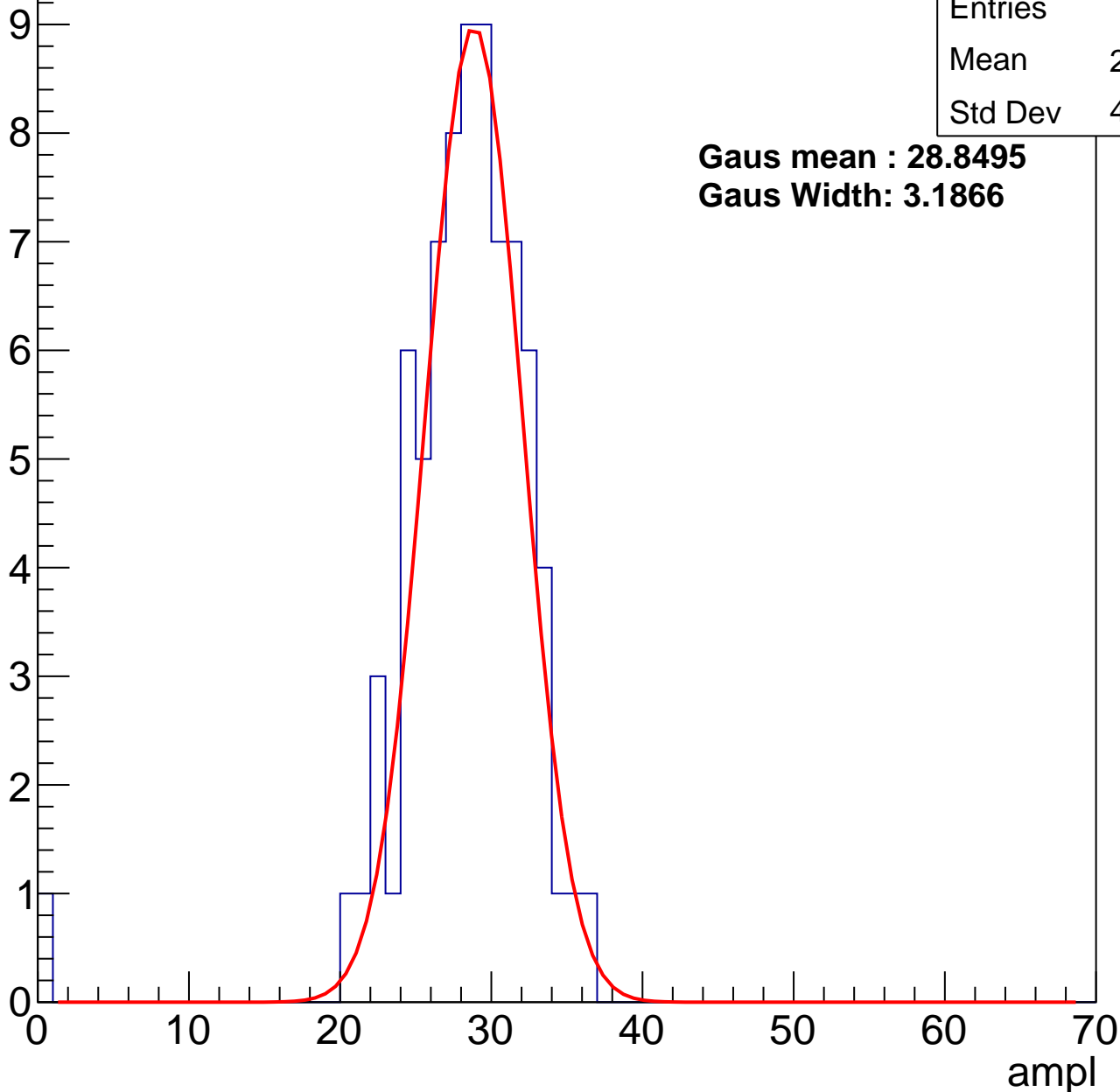
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	27.77
Std Dev	4.596

Gaus mean : 28.8495

Gaus Width: 3.1866



B1L102S, U9-ch87, adc1

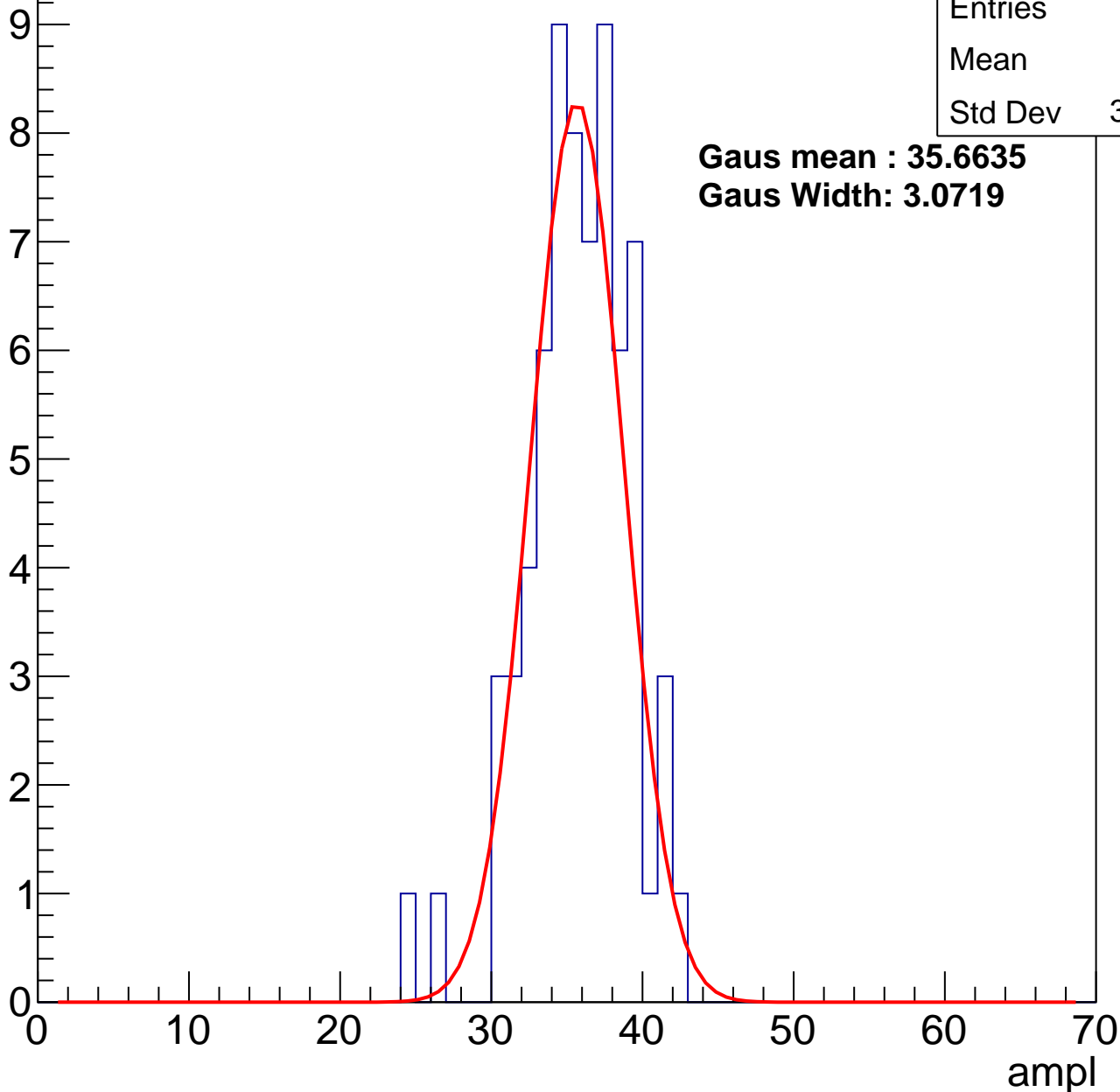
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.3
Std Dev	3.359

Gaus mean : 35.6635

Gaus Width: 3.0719



B1L102S, U9-ch87, adc2

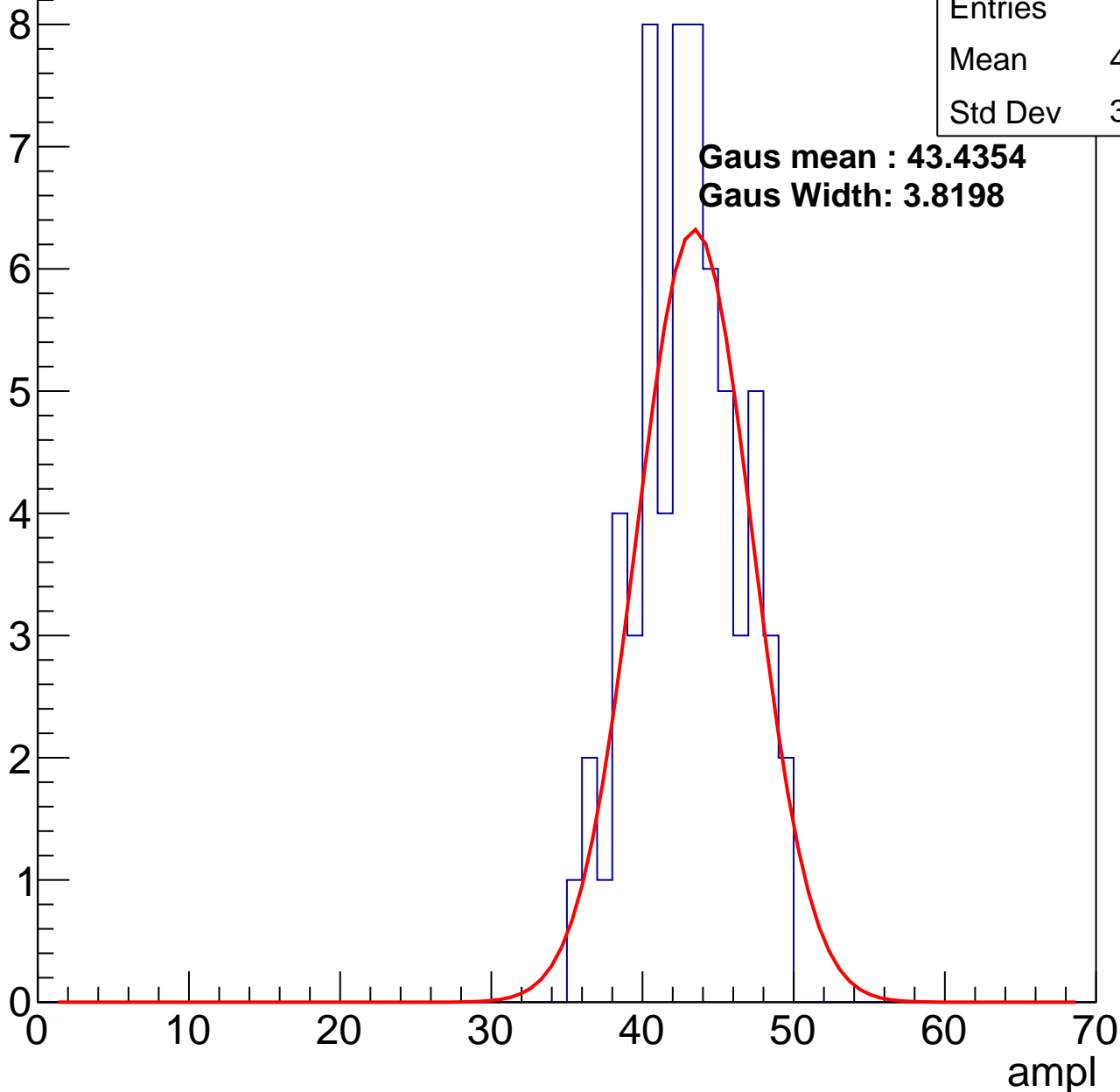
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.56
Std Dev	3.356

Gaus mean : 43.4354

Gaus Width: 3.8198

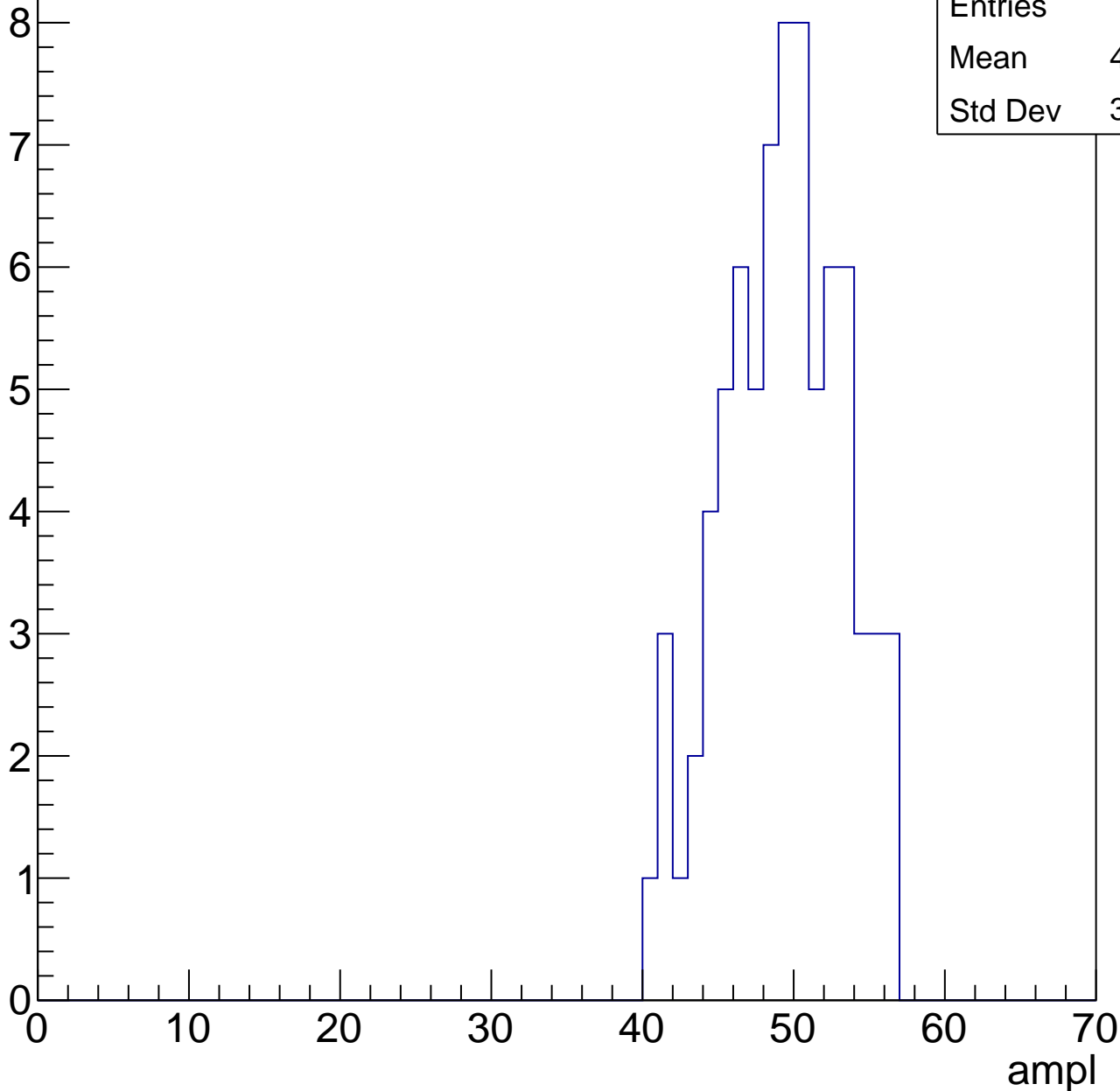


B1L102S, U9-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	48.83
Std Dev	3.908

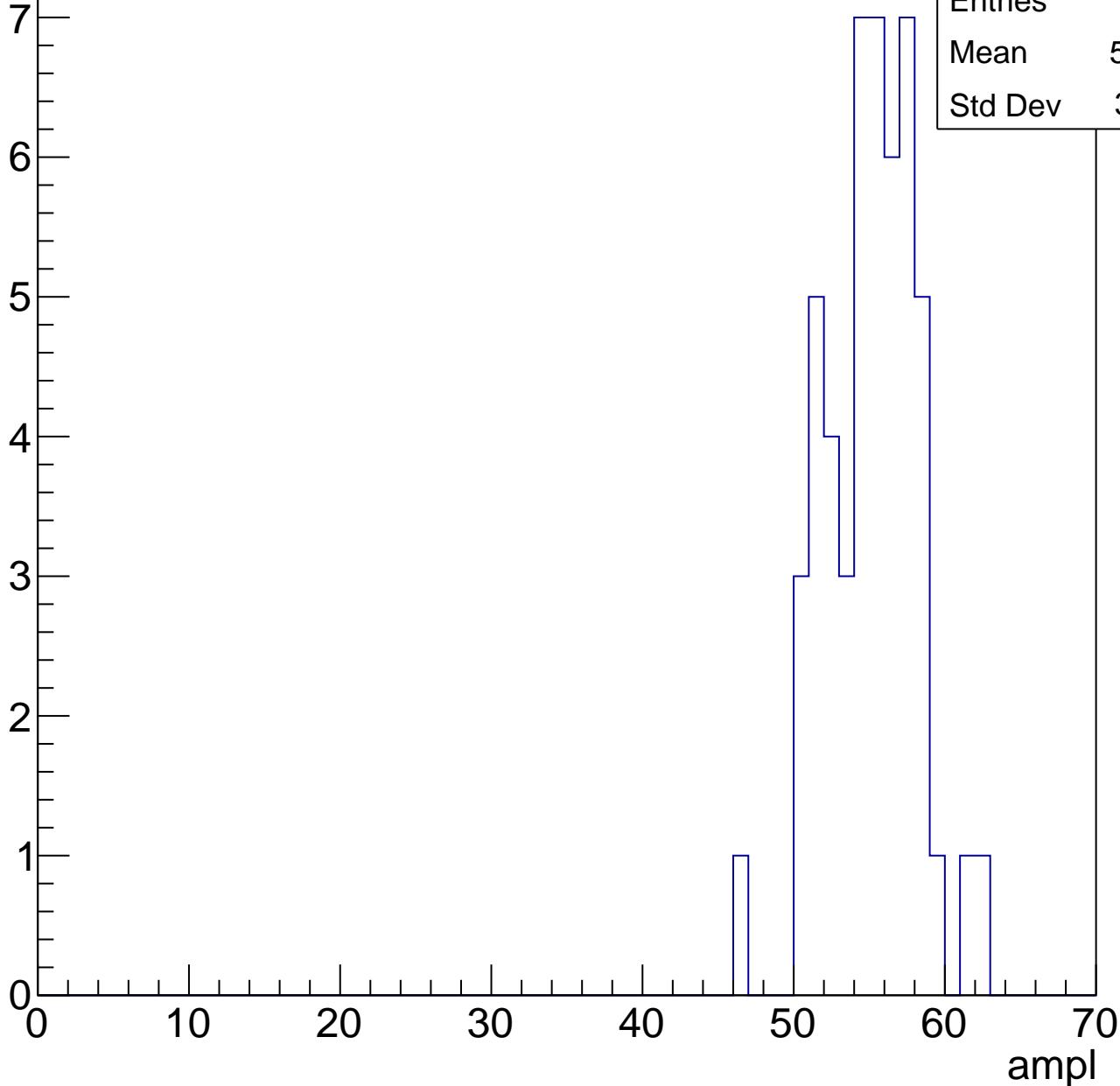


B1L102S, U9-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.67
Std Dev	3.021

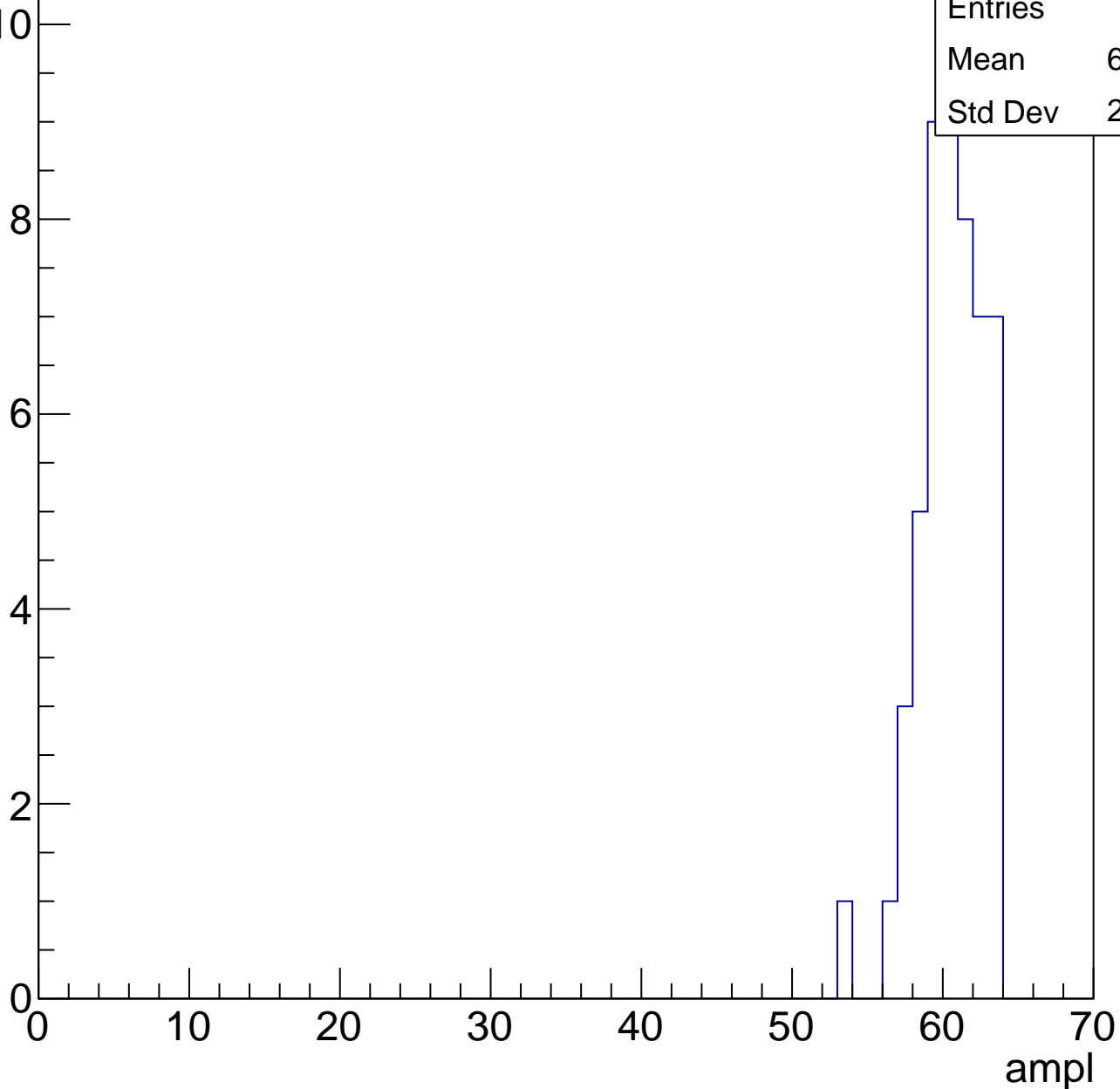


B1L102S, U9-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

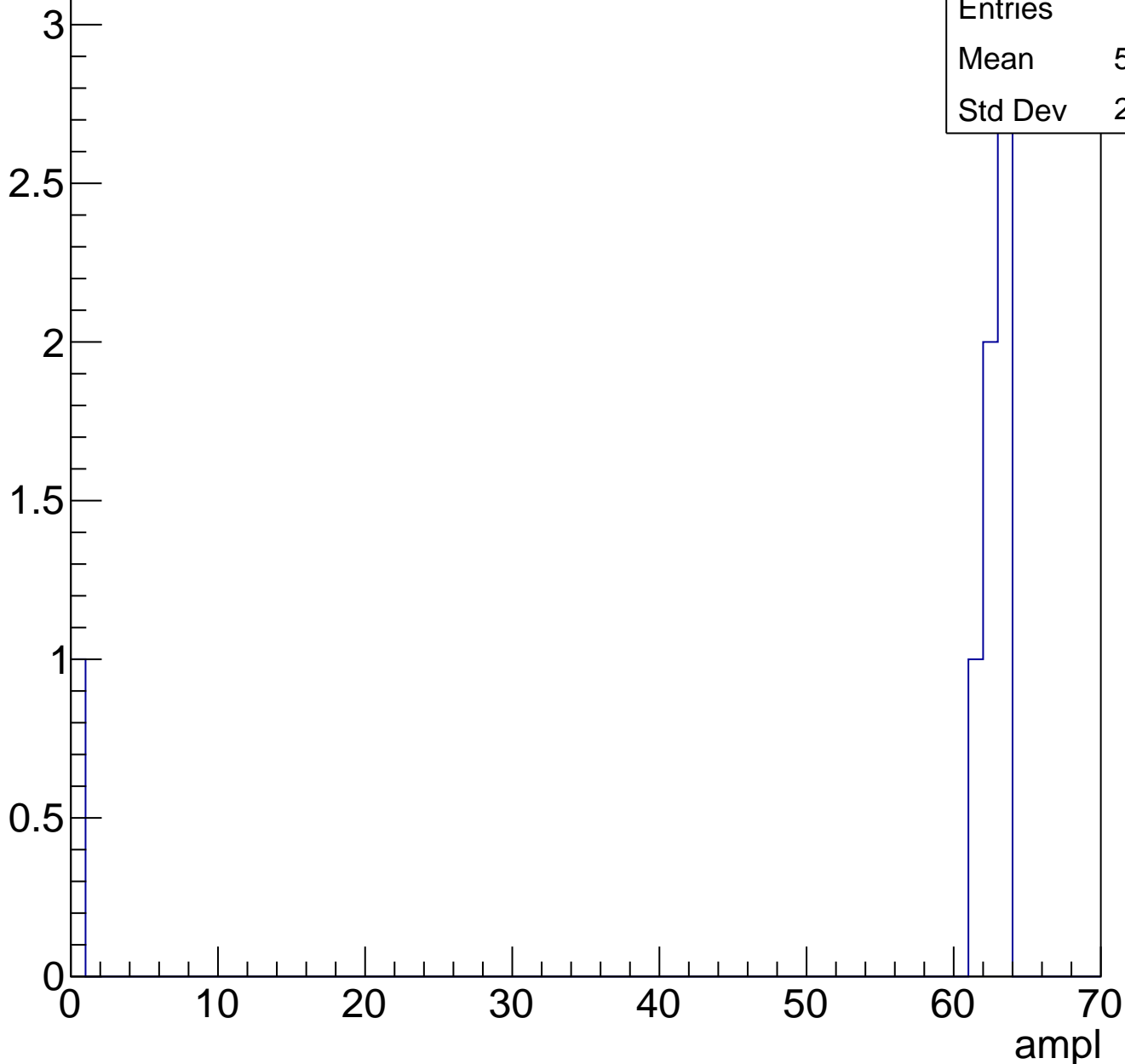
Entries	51
Mean	60.08
Std Dev	2.075



B1L102S, U9-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch88, adc0

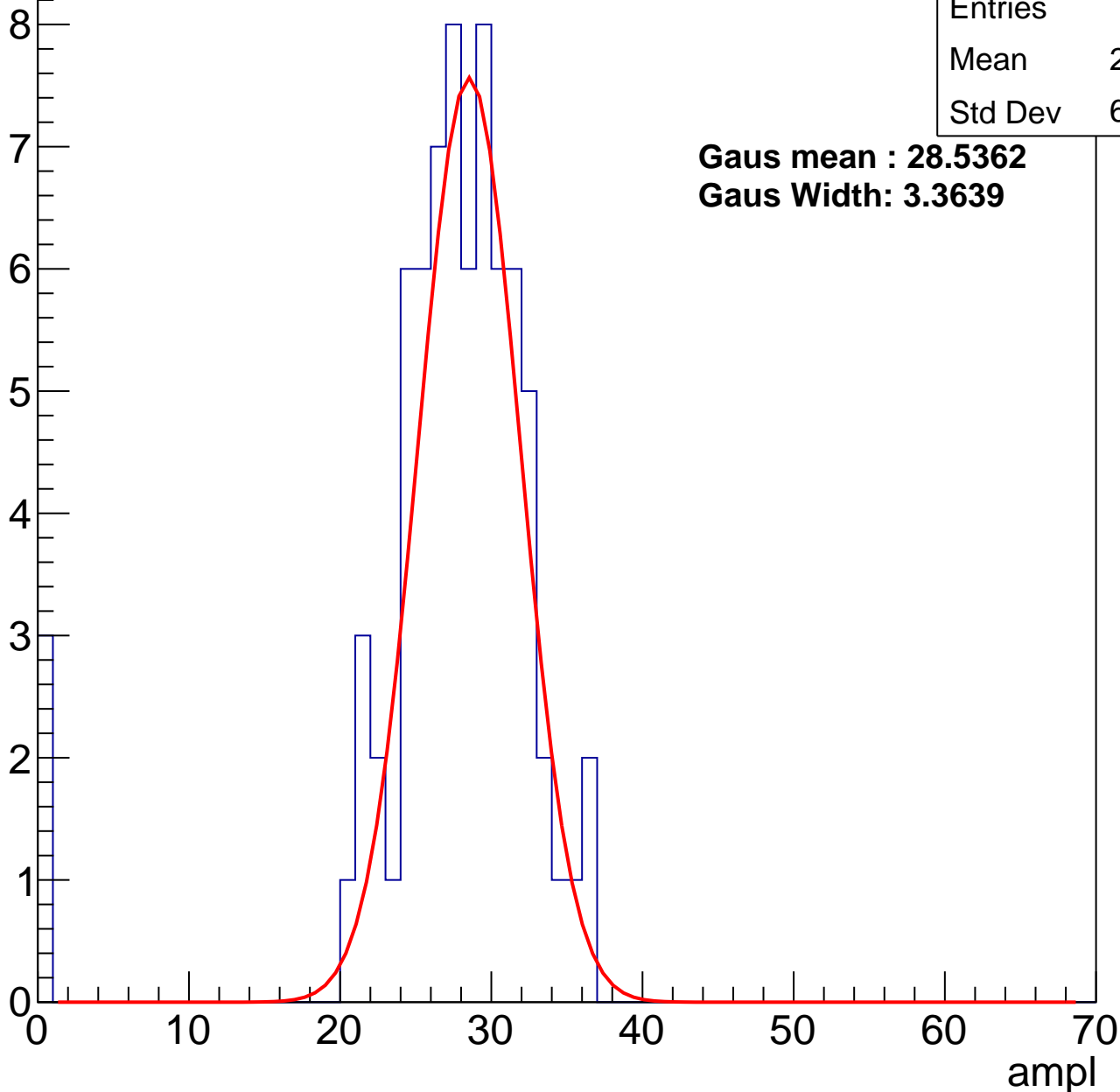
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	26.69
Std Dev	6.522

Gaus mean : 28.5362

Gaus Width: 3.3639



B1L102S, U9-ch88, adc1

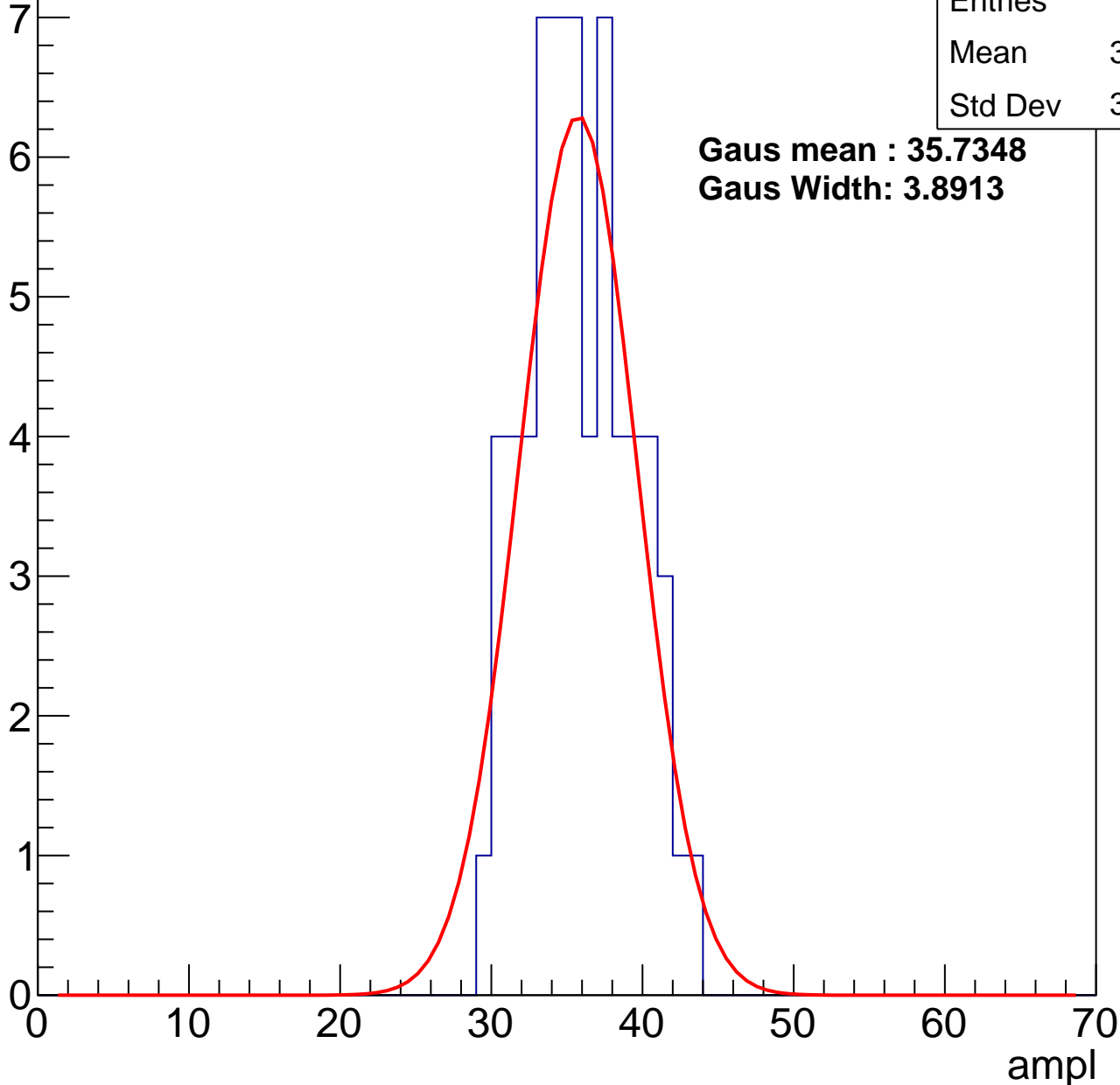
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.39
Std Dev	3.395

Gaus mean : 35.7348

Gaus Width: 3.8913

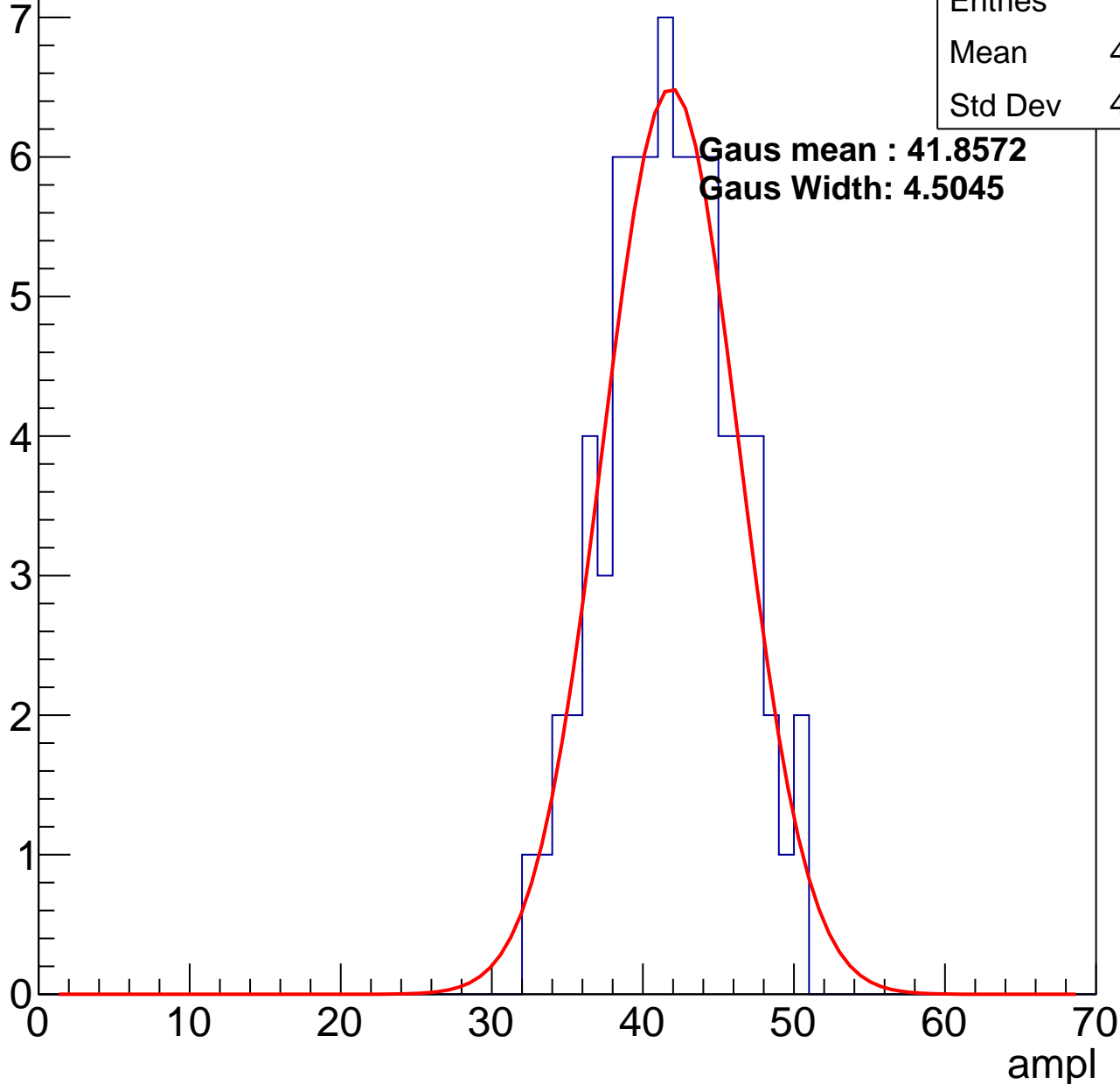


B1L102S, U9-ch88, adc2

calib_packv5_042523_0143.root, FC#11, port A2

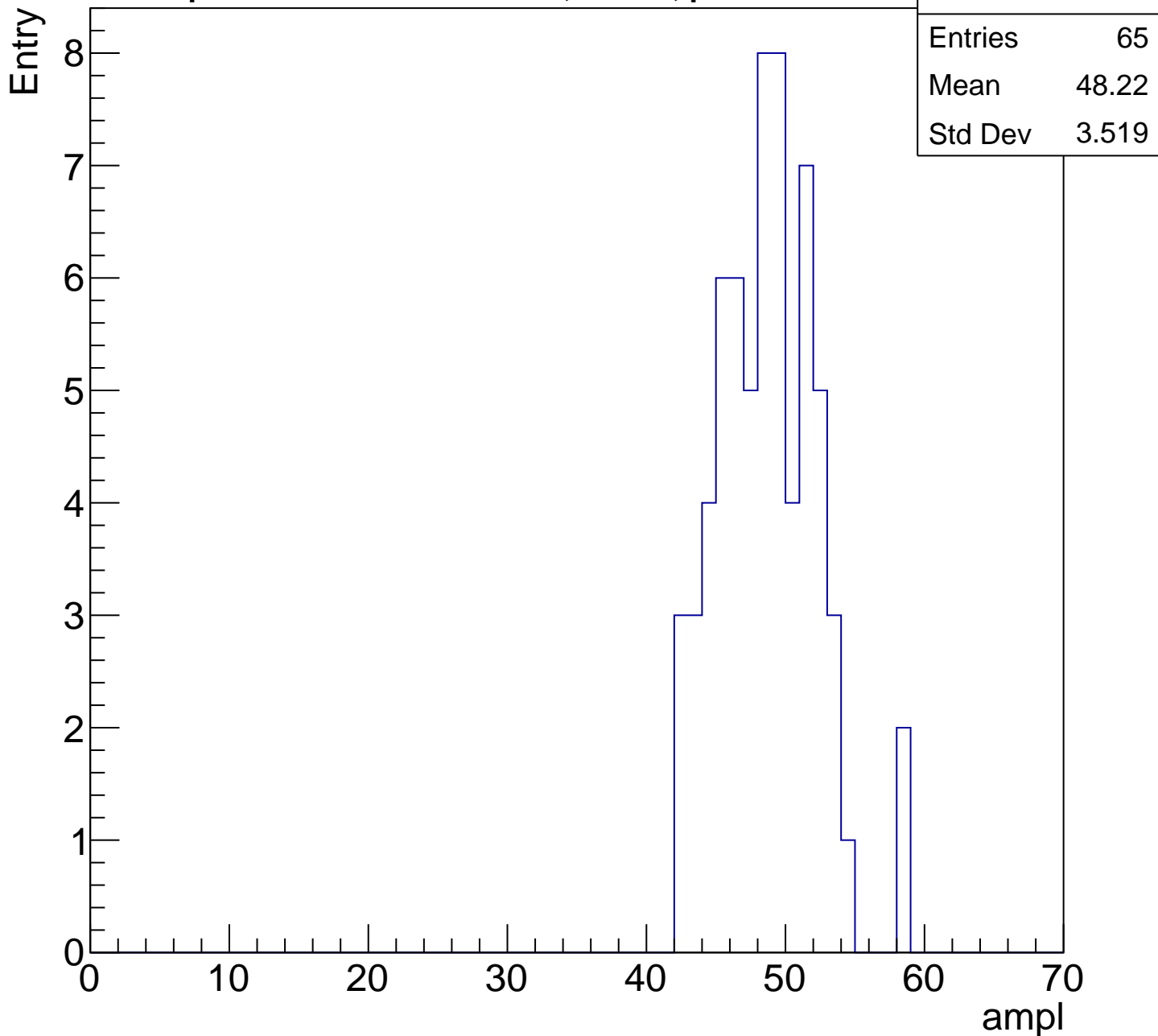
Entry

Entries	73
Mean	41.34
Std Dev	4.145



B1L102S, U9-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

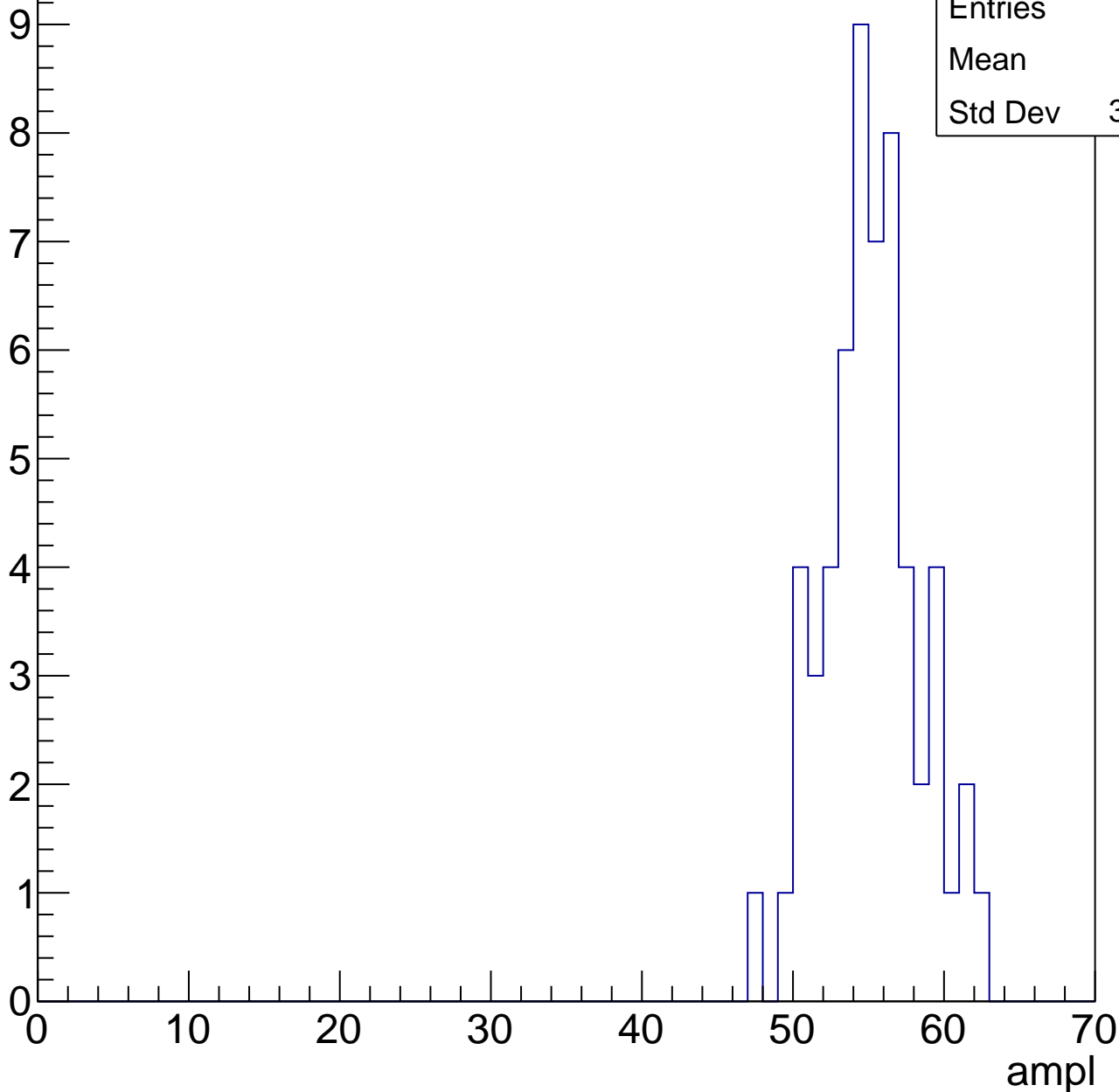


B1L102S, U9-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.7
Std Dev	3.145

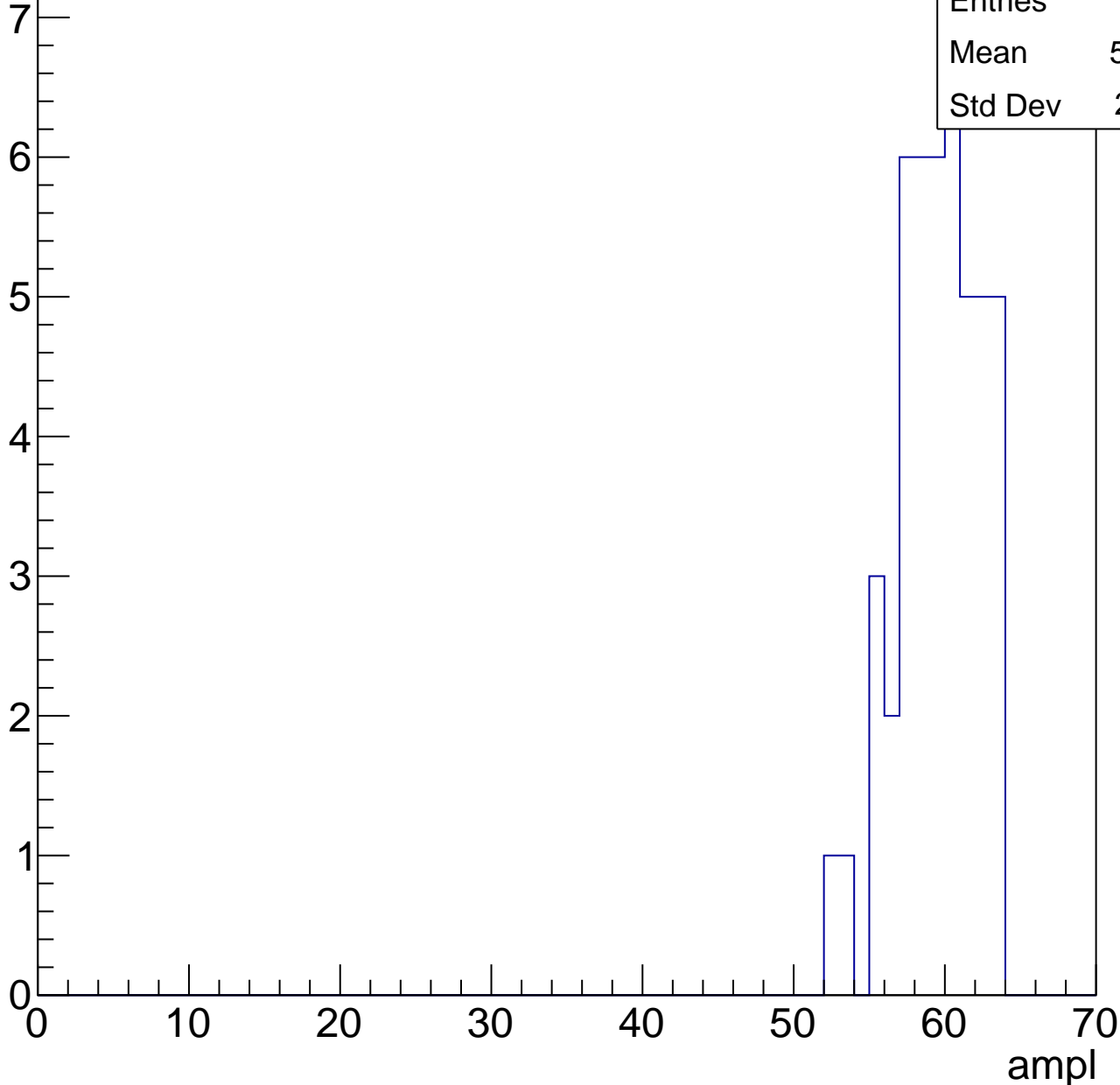


B1L102S, U9-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.06
Std Dev	2.661

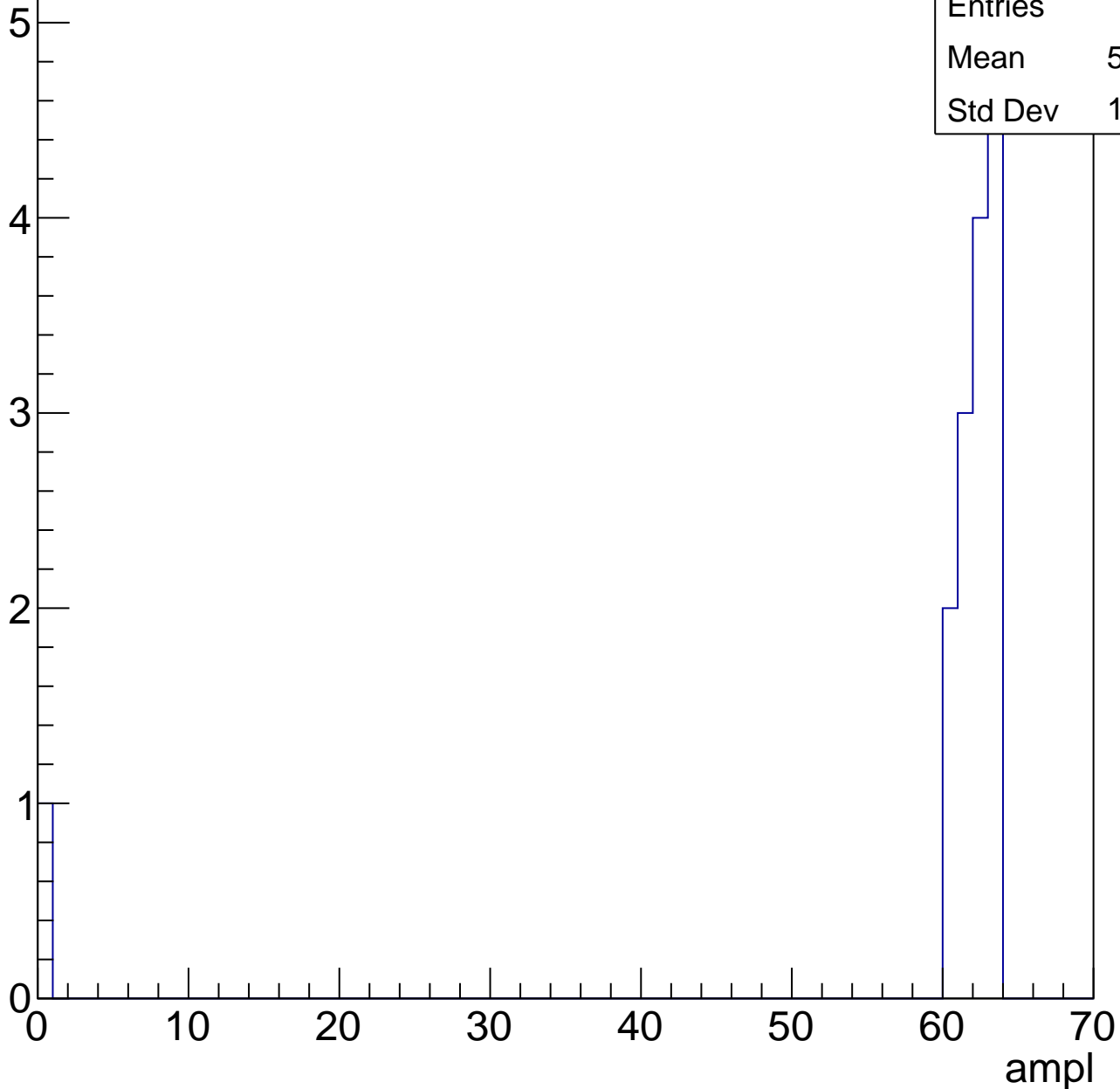


B1L102S, U9-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.73
Std Dev	15.46



B1L102S, U9-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch89, adc0

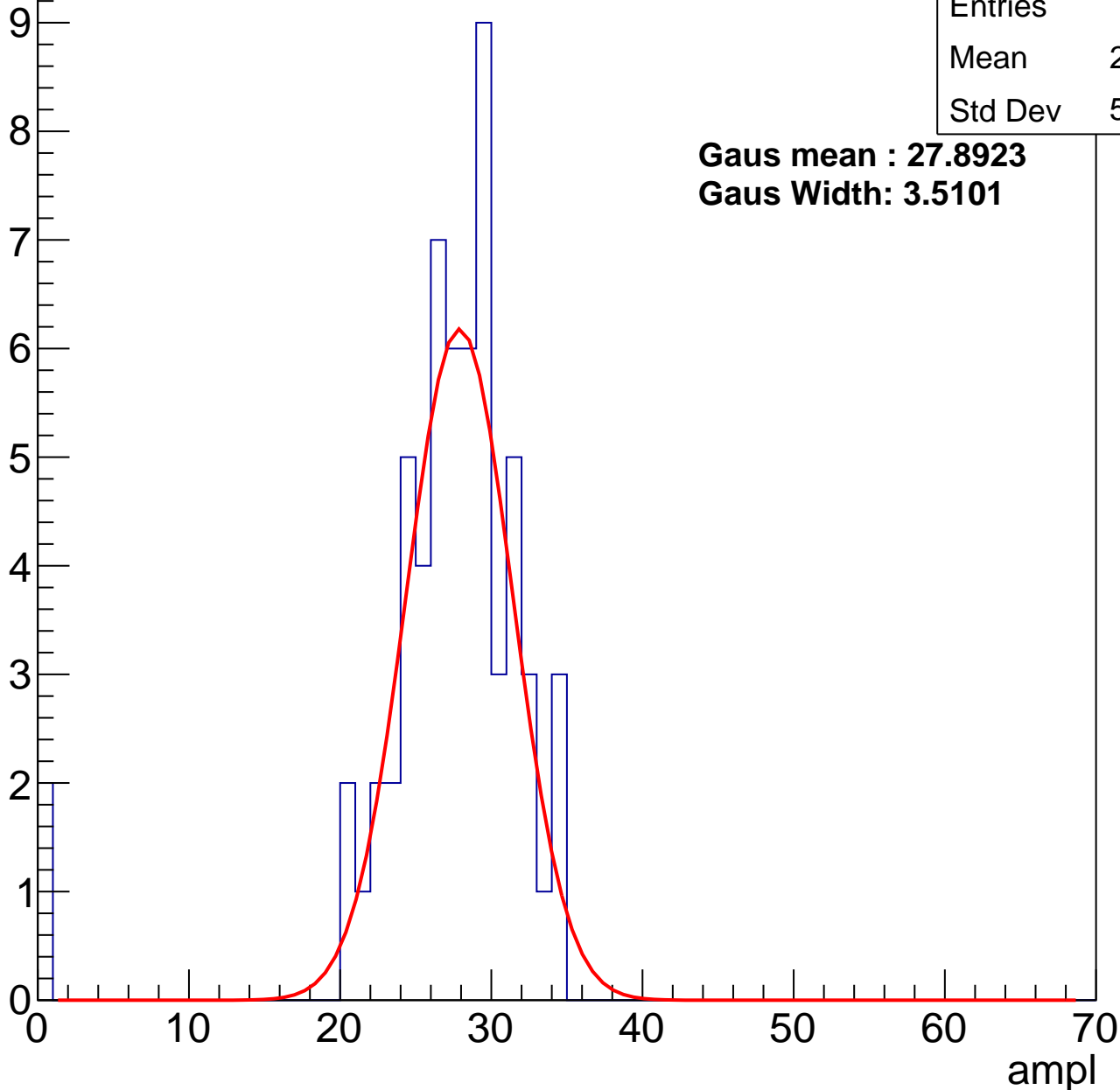
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	26.56
Std Dev	5.919

Gaus mean : 27.8923

Gaus Width: 3.5101



B1L102S, U9-ch89, adc1

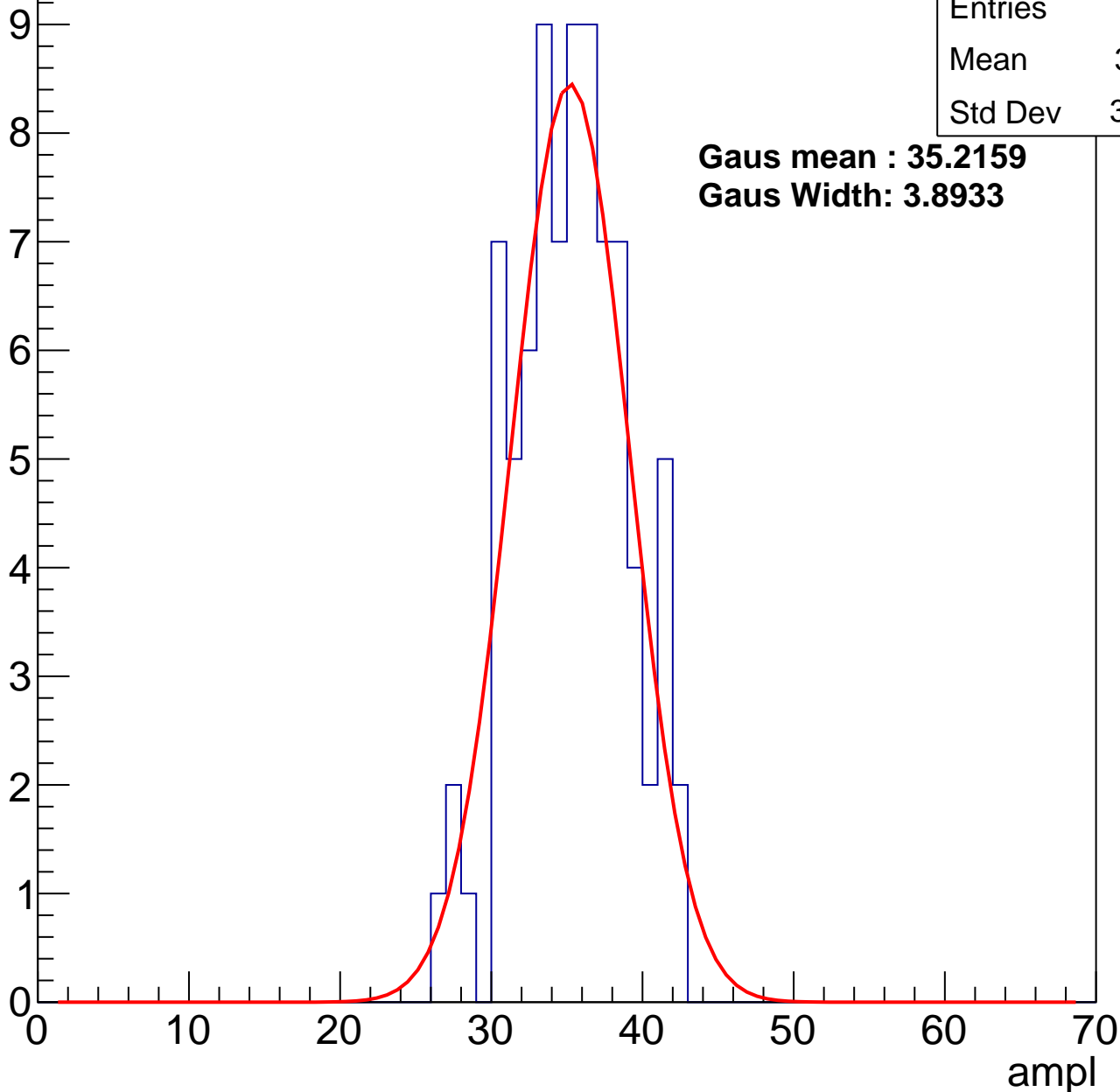
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	34.81
Std Dev	3.639

Gaus mean : 35.2159

Gaus Width: 3.8933



B1L102S, U9-ch89, adc2

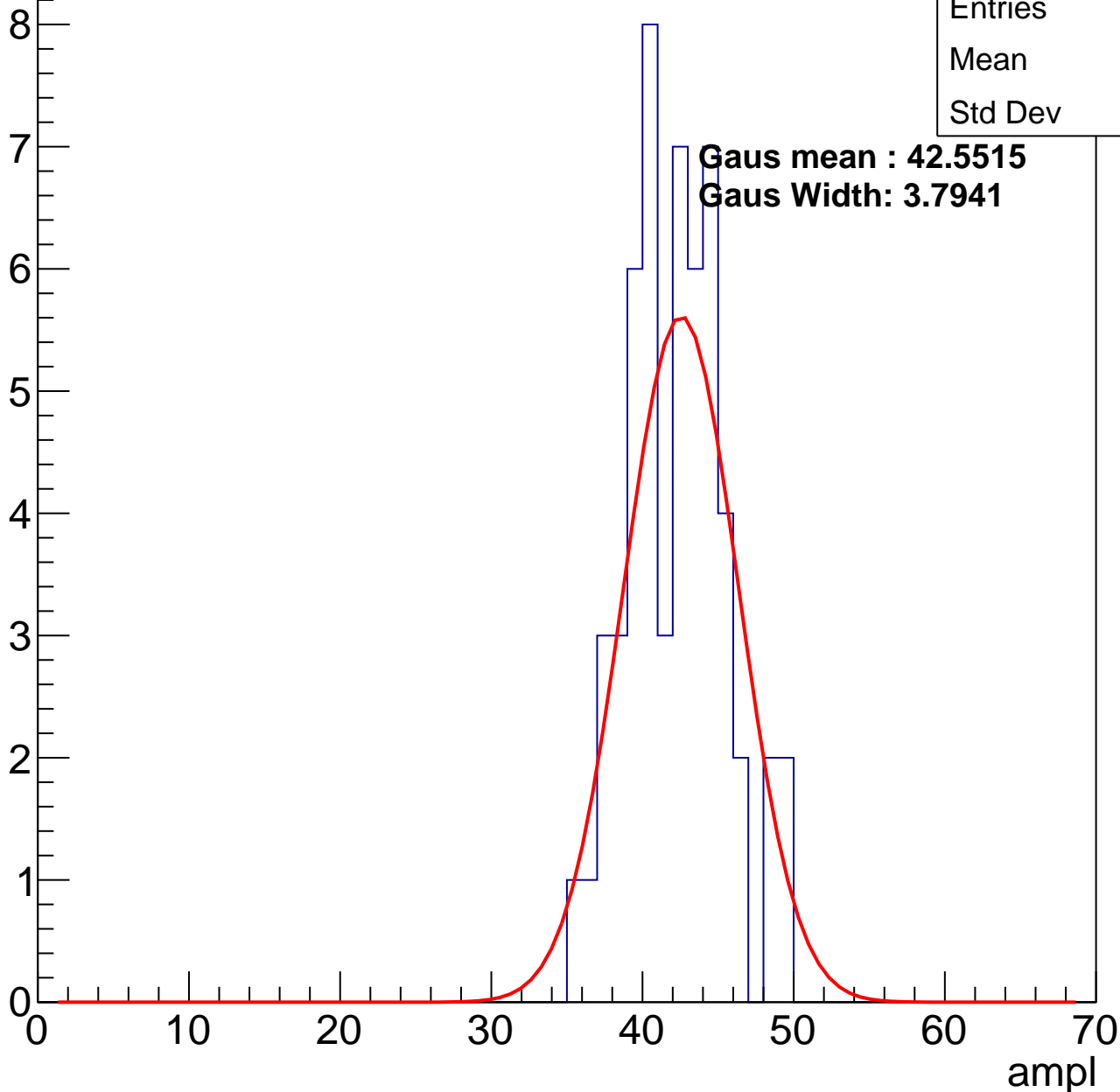
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.8
Std Dev	3.21

Gaus mean : 42.5515

Gaus Width: 3.7941



B1L102S, U9-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

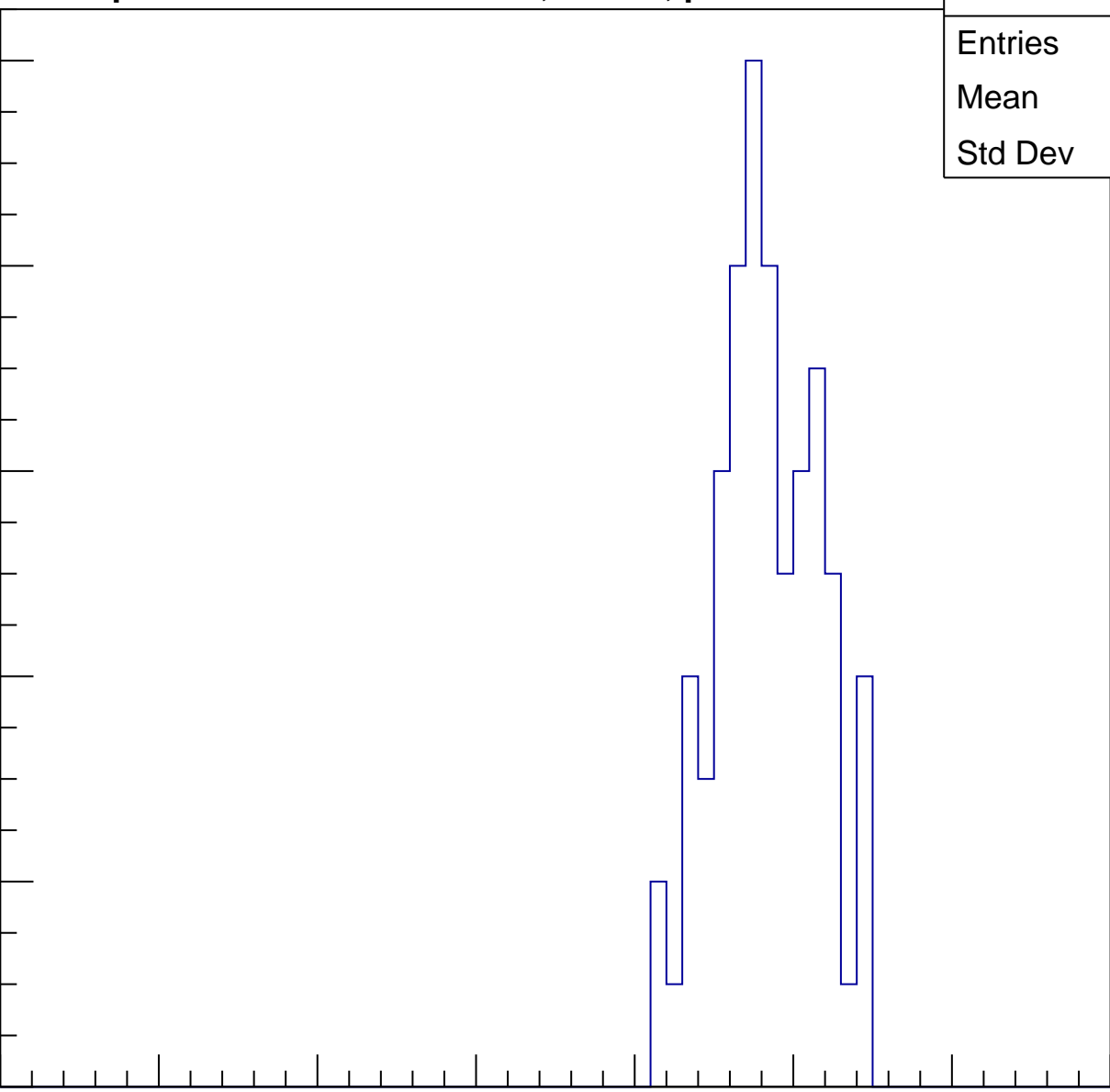
Entries	70
Mean	47.87
Std Dev	3.202

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

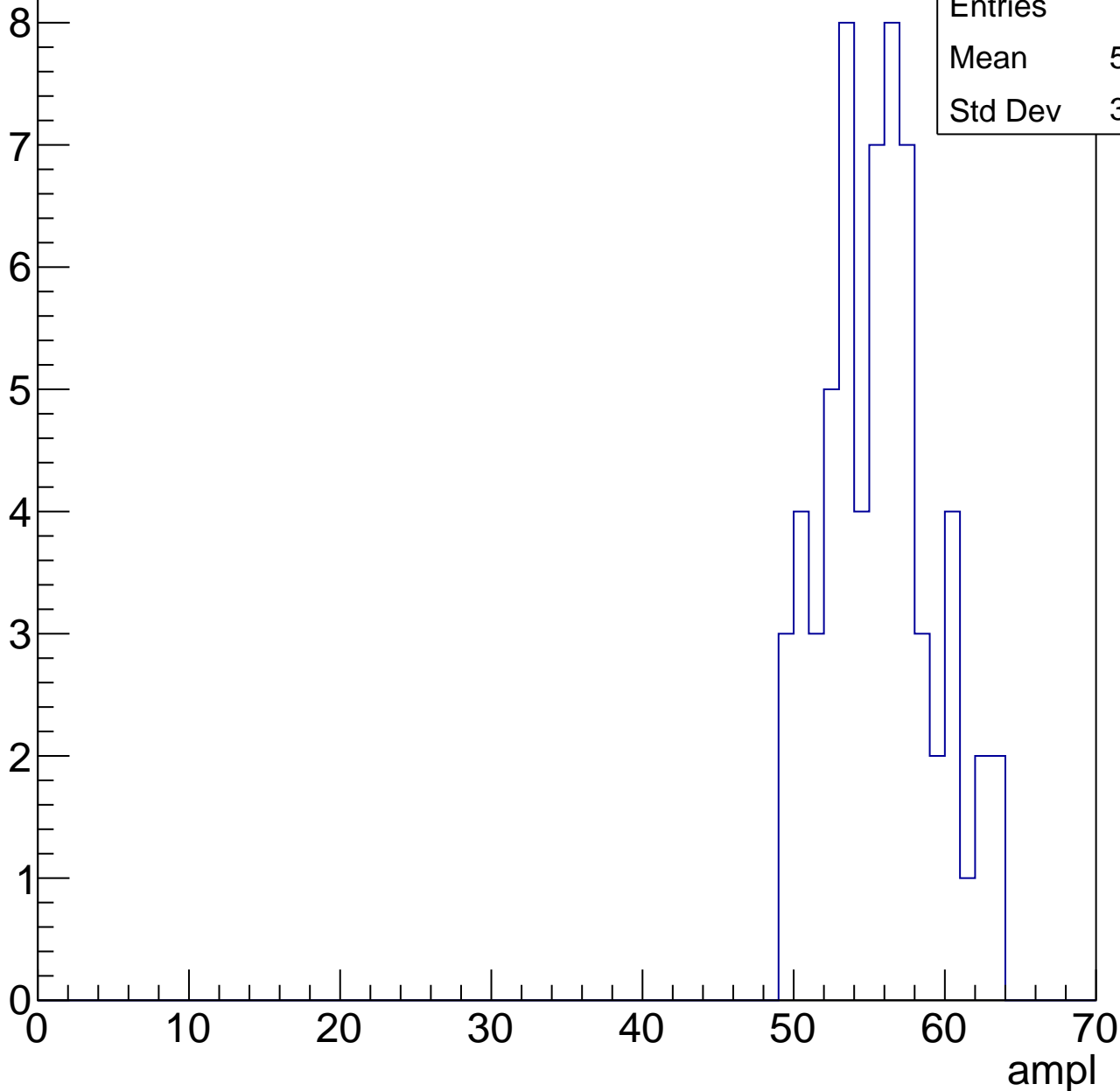


B1L102S, U9-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.16
Std Dev	3.547



B1L102S, U9-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	59.3
Std Dev	2.759

ampl

0

10

20

30

40

50

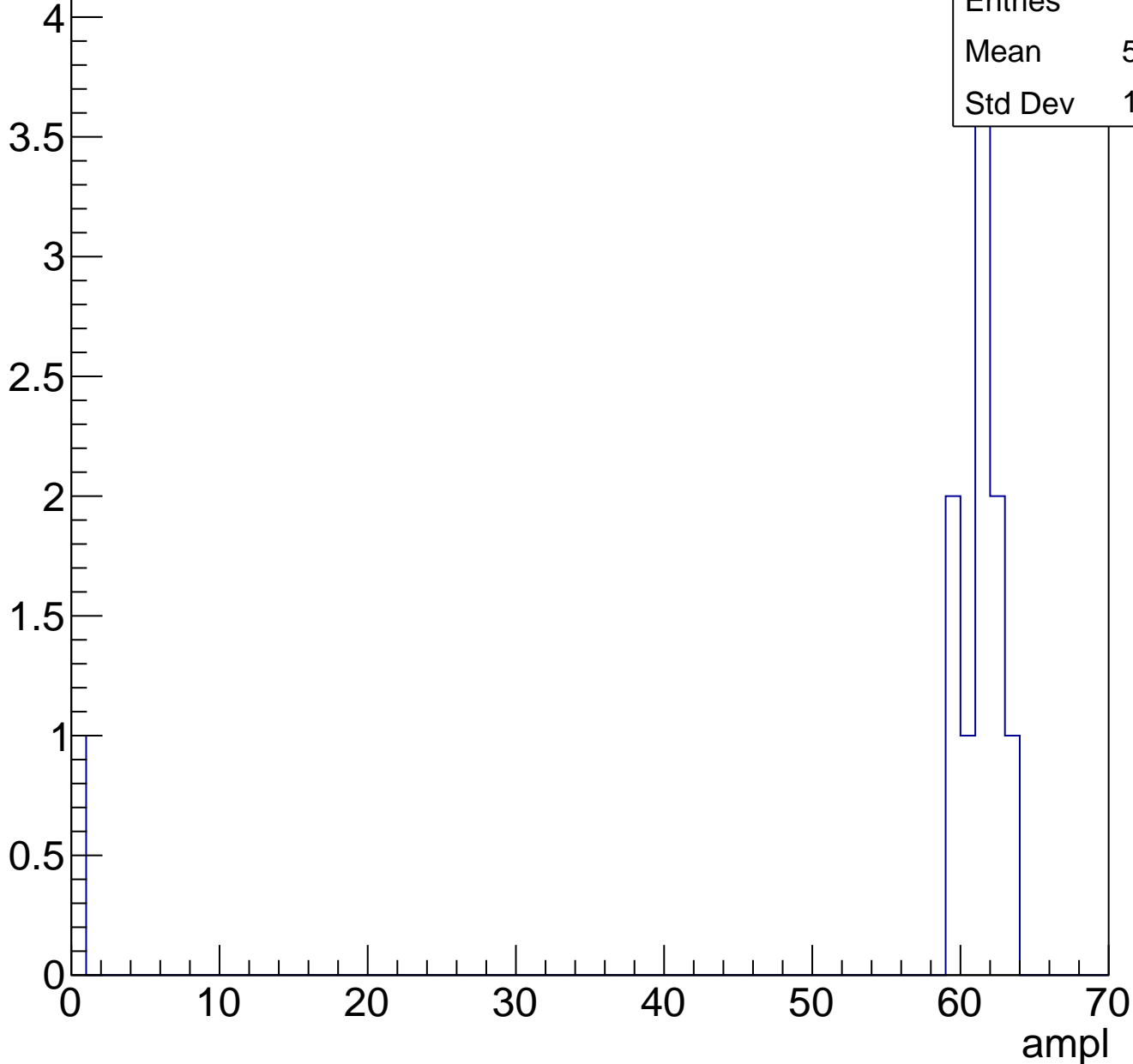
60

70

B1L102S, U9-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B1L102S, U9-ch90, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	26.72
Std Dev	5.498

Gaus mean : 27.3564

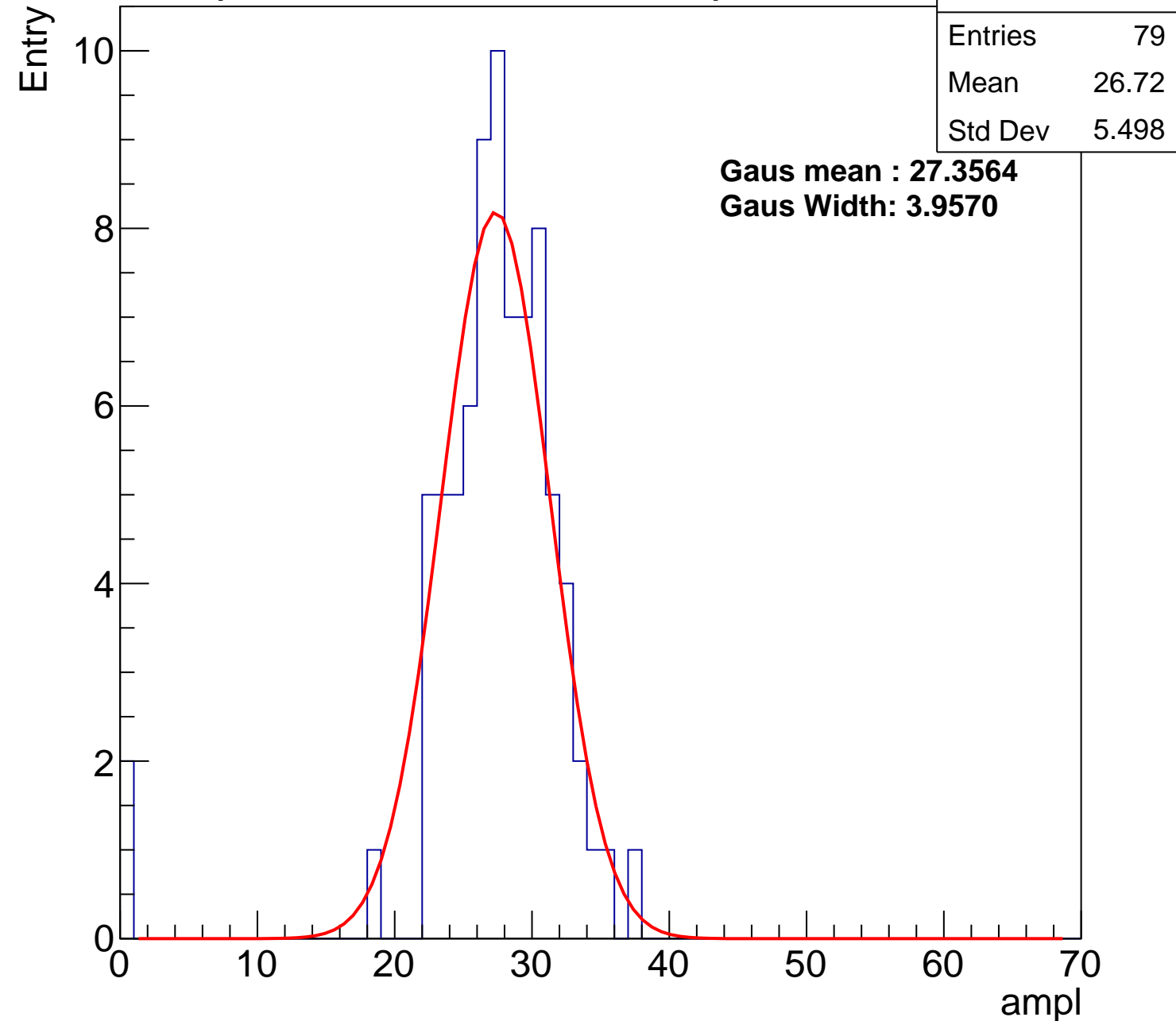
Gaus Width: 3.9570

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch90, adc1

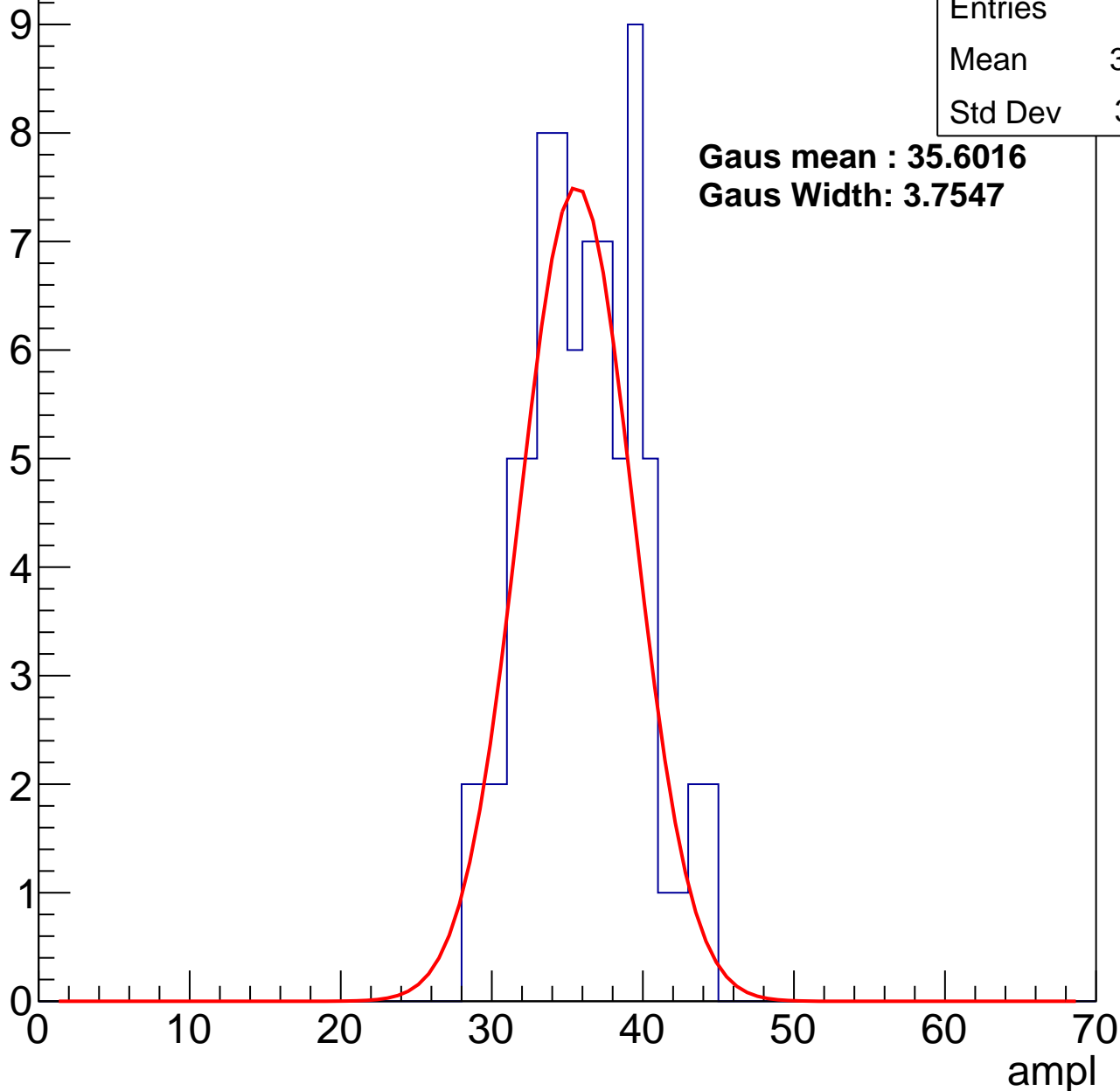
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.64
Std Dev	3.741

Gaus mean : 35.6016

Gaus Width: 3.7547



B1L102S, U9-ch90, adc2

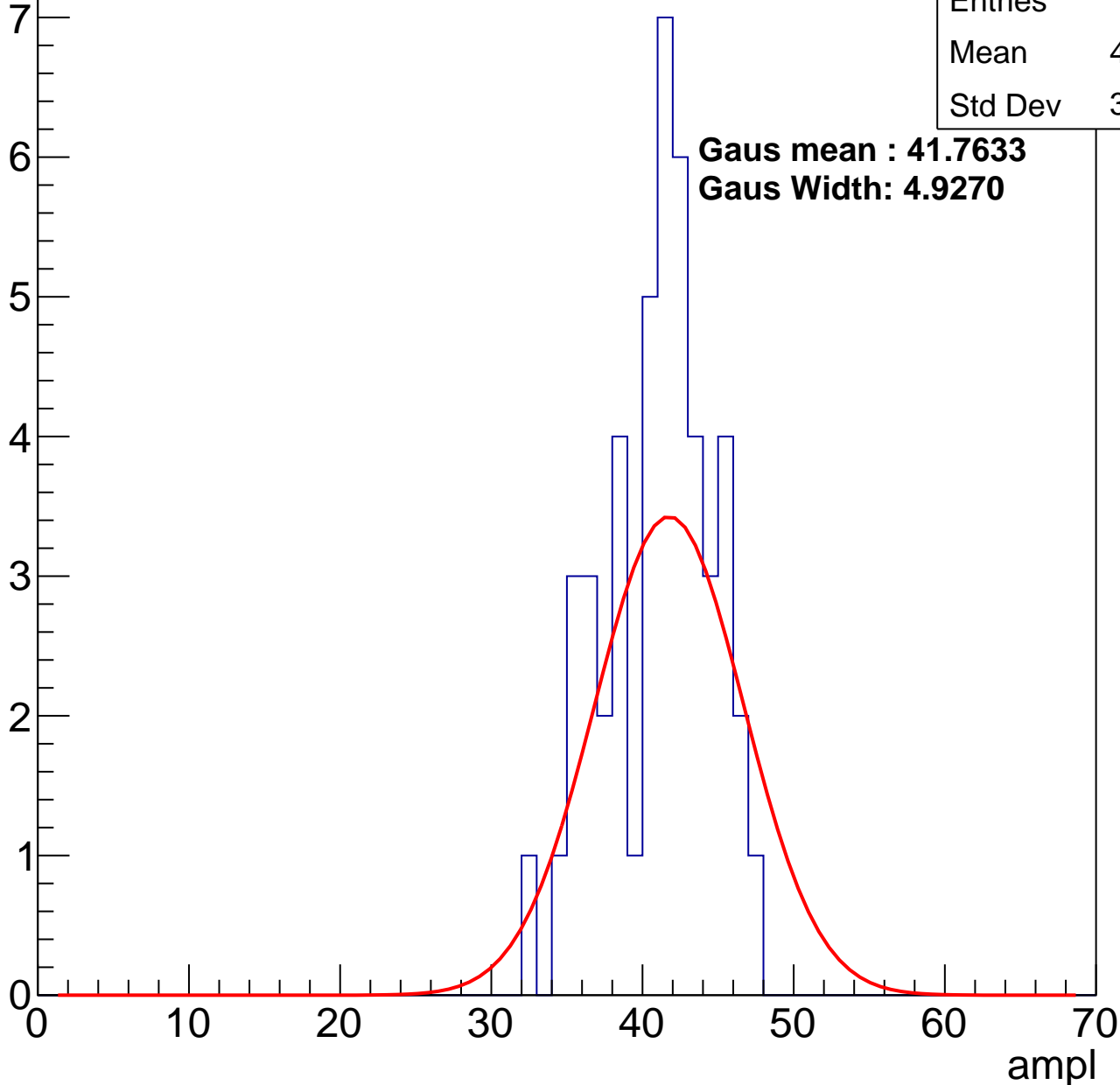
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	40.55
Std Dev	3.512

Gaus mean : 41.7633

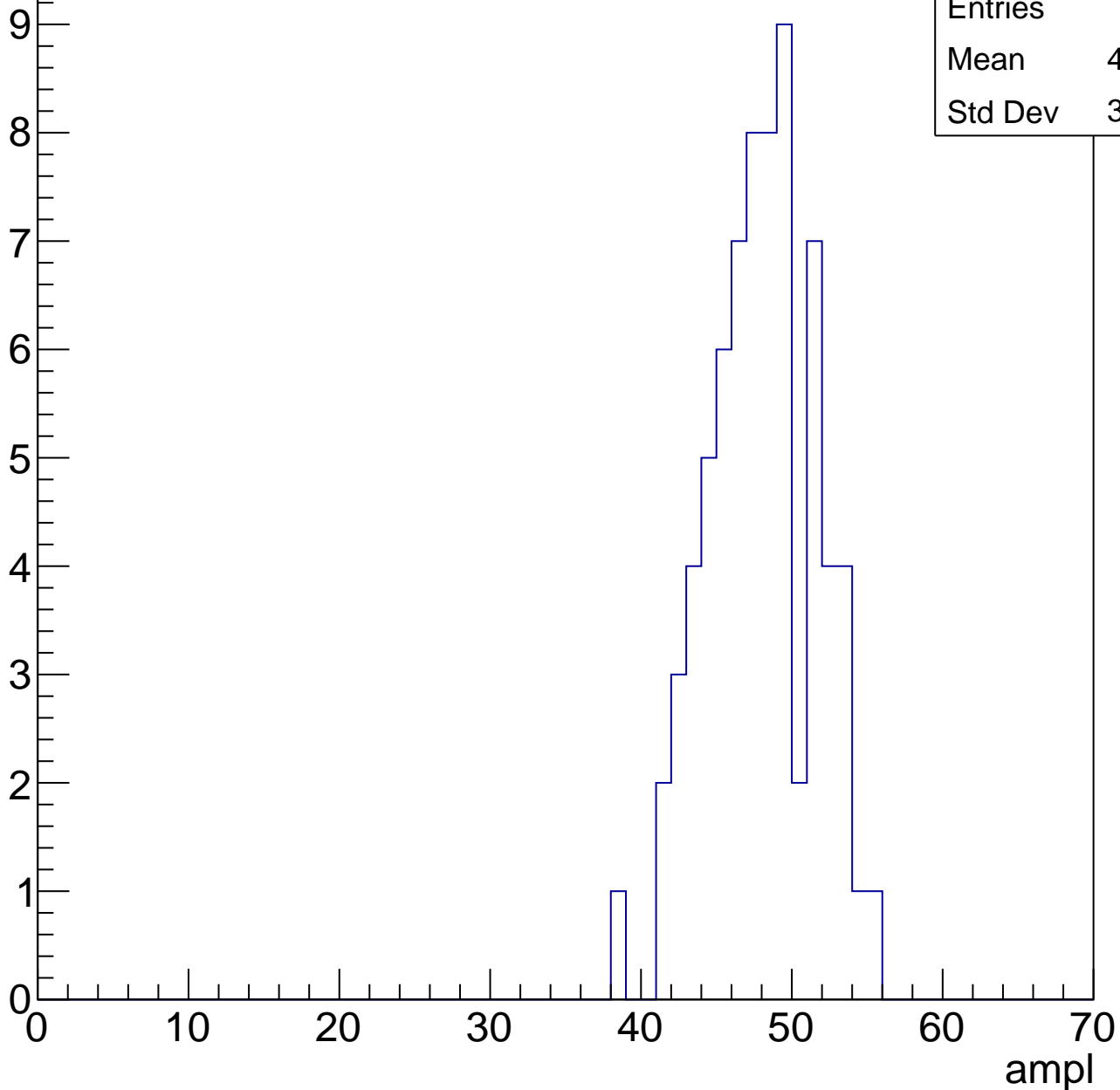
Gaus Width: 4.9270



B1L102S, U9-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



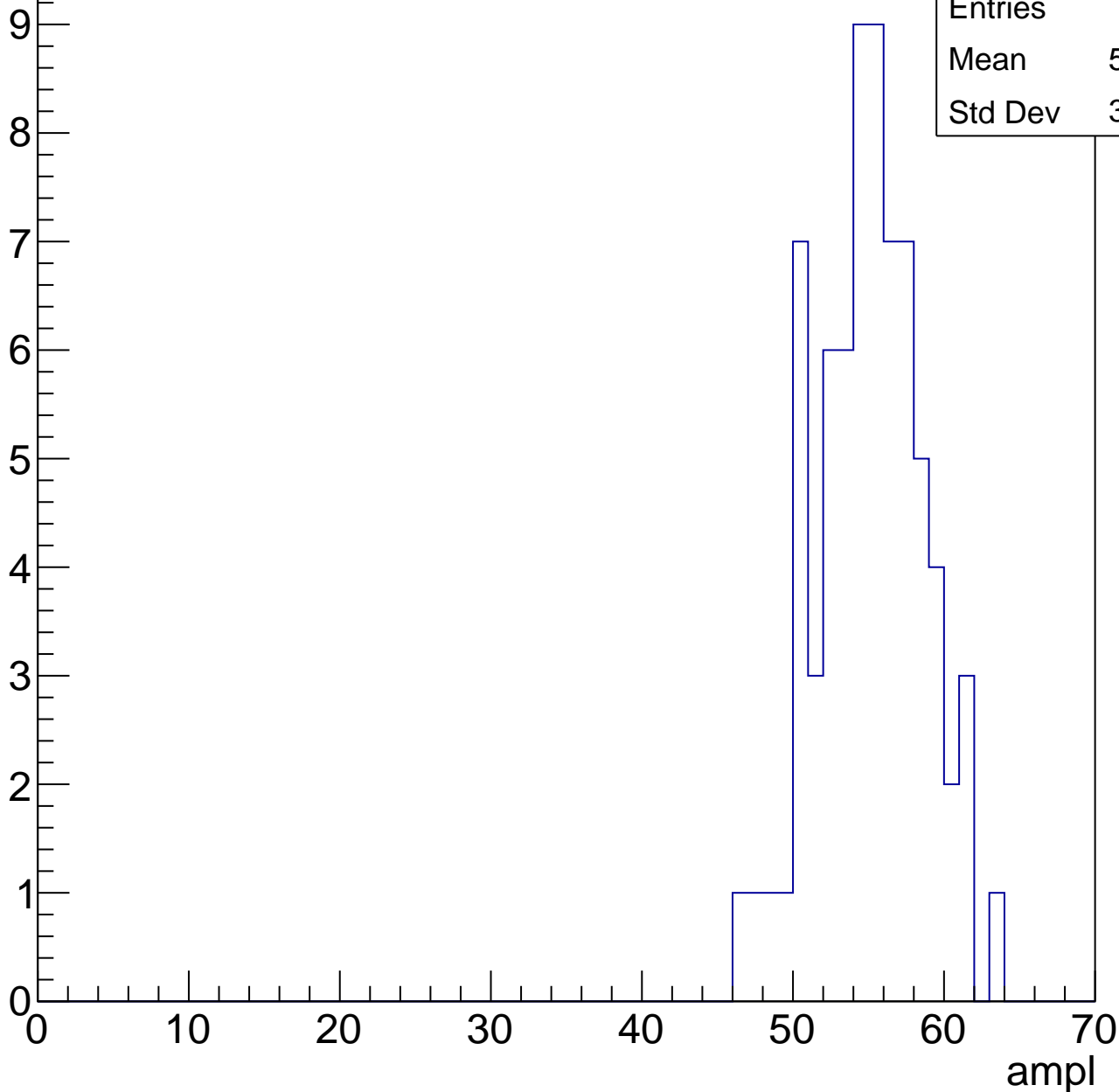
Entries	72
Mean	47.46
Std Dev	3.504

B1L102S, U9-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	54.62
Std Dev	3.502

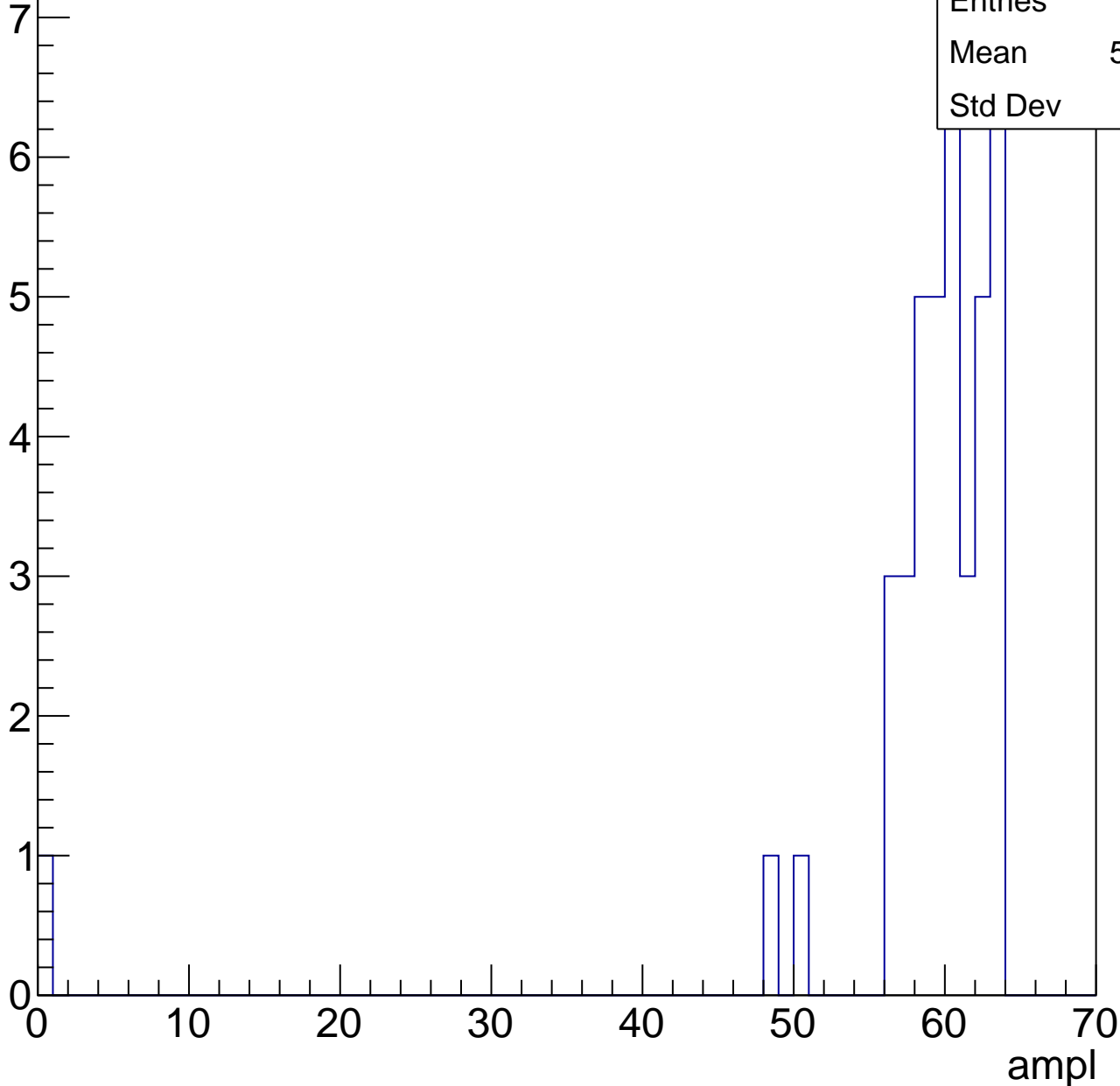


B1L102S, U9-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

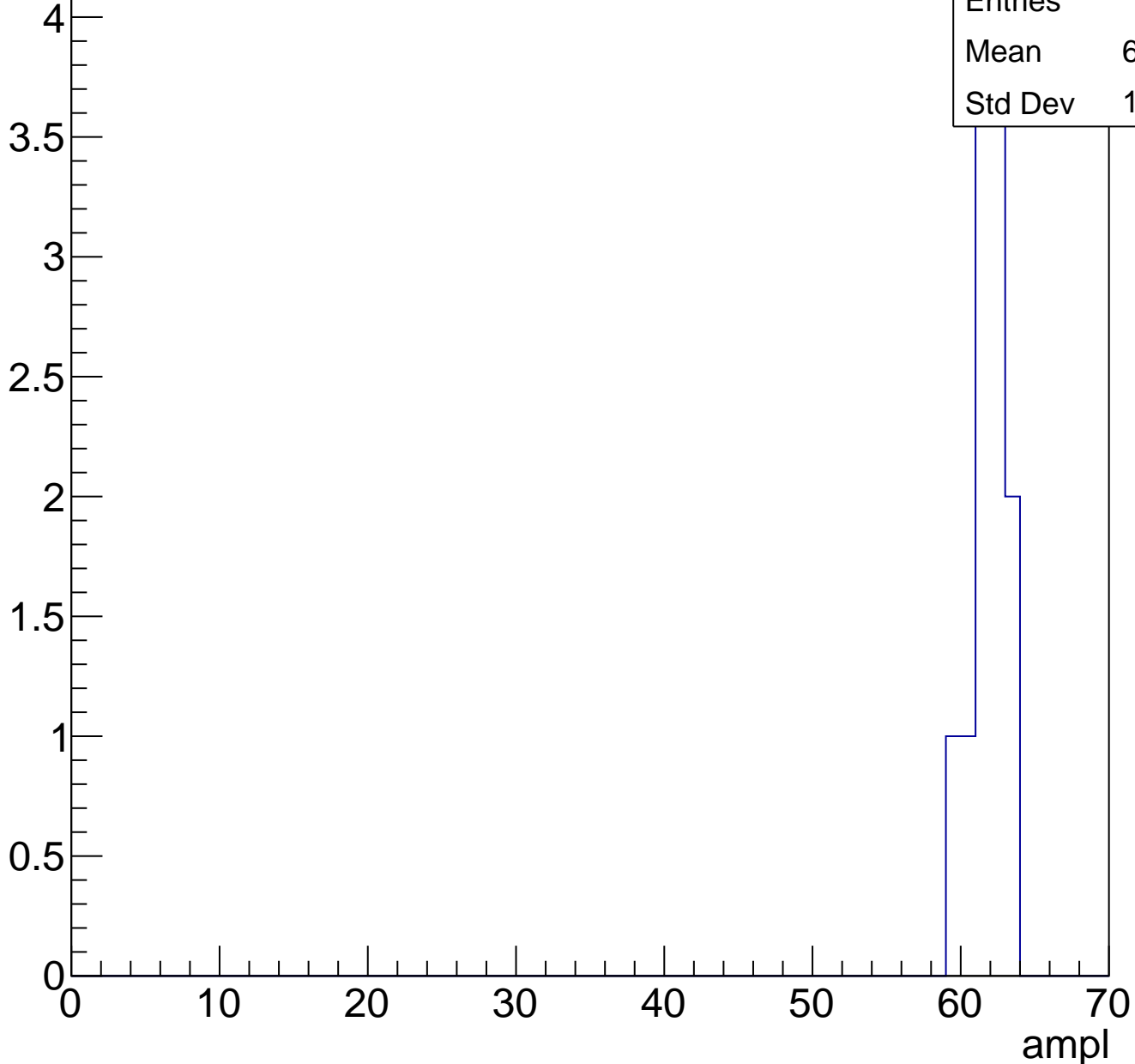
Entries	41
Mean	57.95
Std Dev	9.7



B1L102S, U9-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

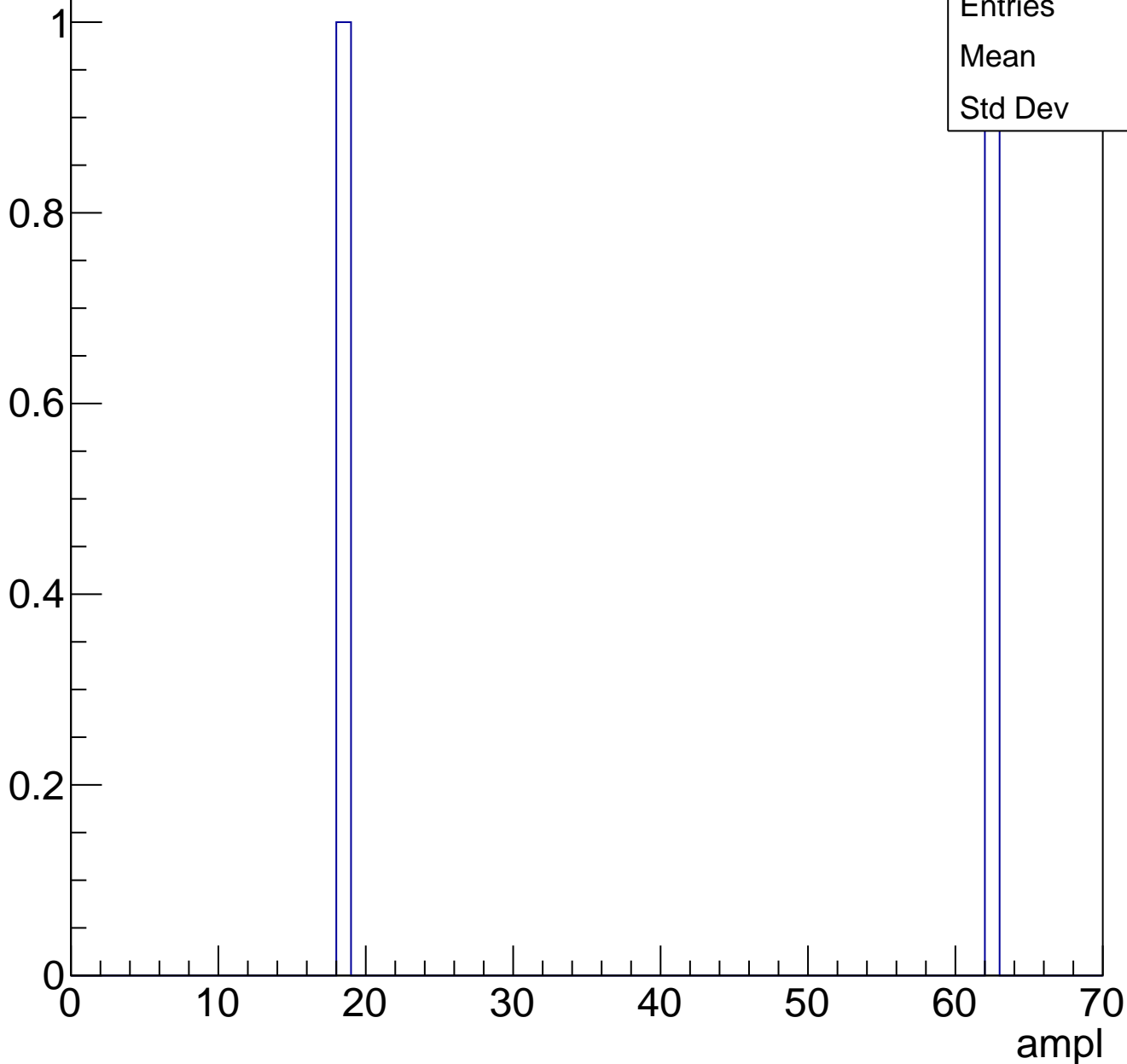
Entry



B1L102S, U9-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch91, adc0

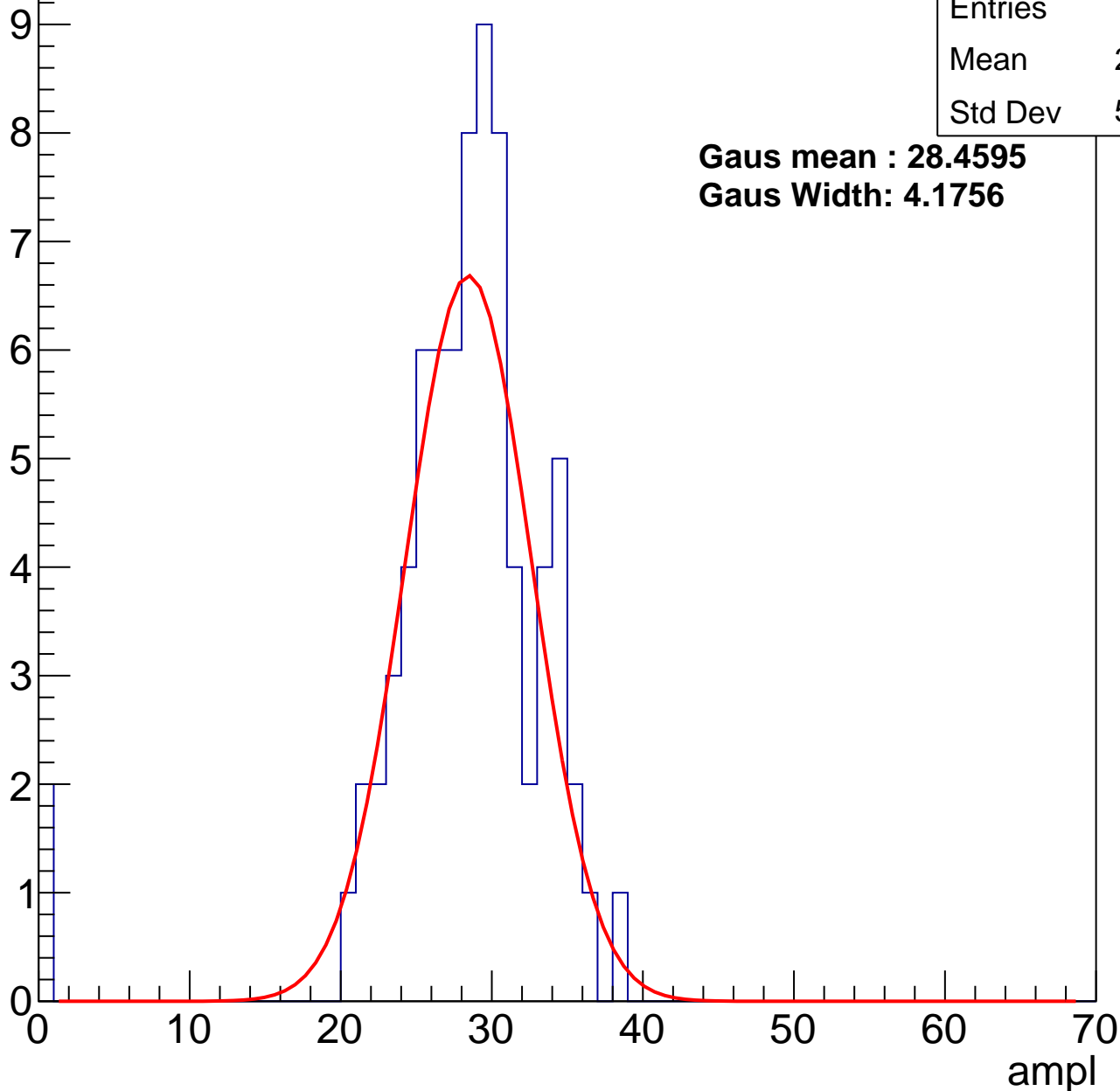
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.61
Std Dev	5.921

Gaus mean : 28.4595

Gaus Width: 4.1756



B1L102S, U9-ch91, adc1

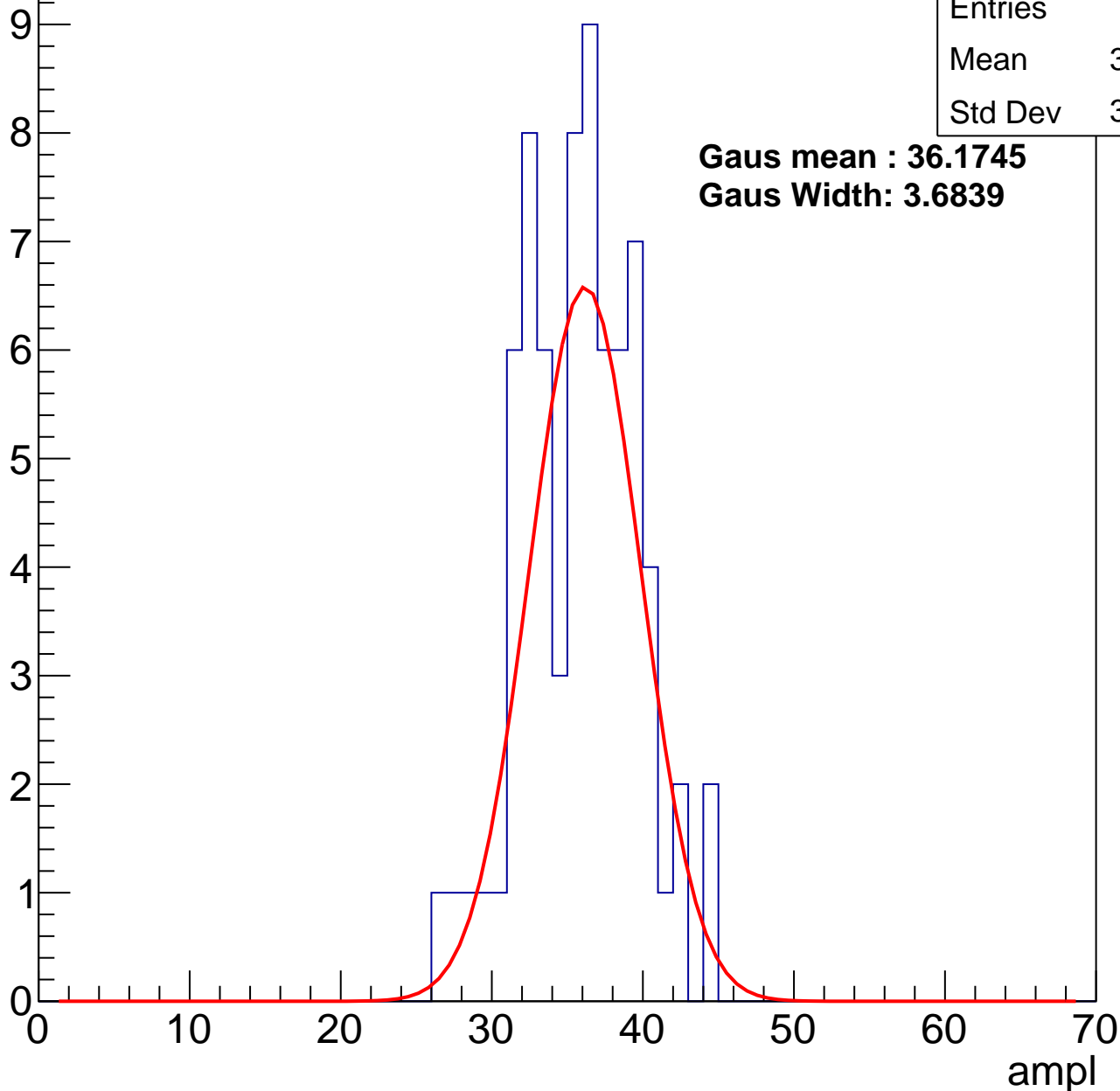
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.37
Std Dev	3.765

Gaus mean : 36.1745

Gaus Width: 3.6839



B1L102S, U9-ch91, adc2

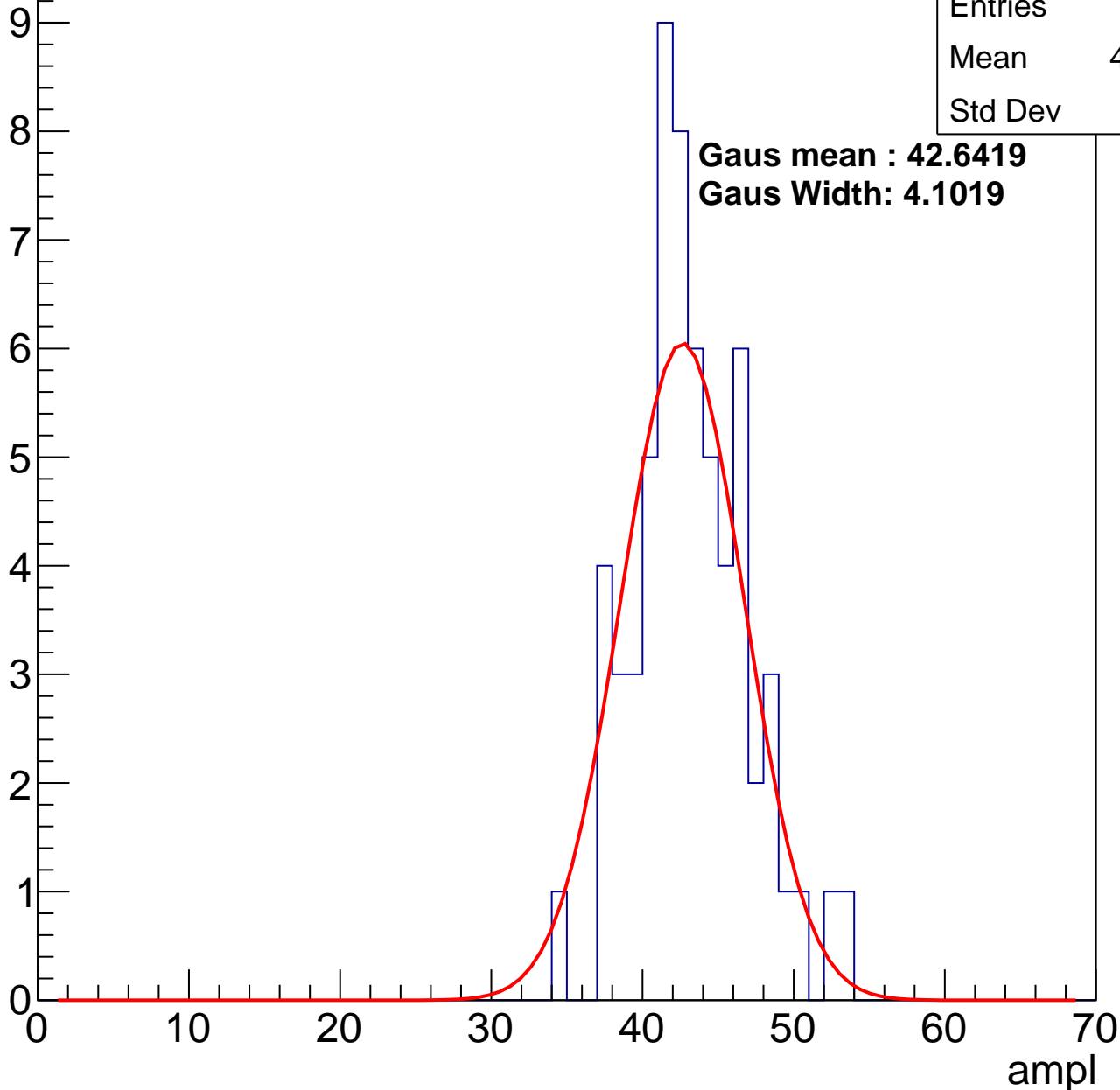
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.76
Std Dev	3.74

Gaus mean : 42.6419

Gaus Width: 4.1019

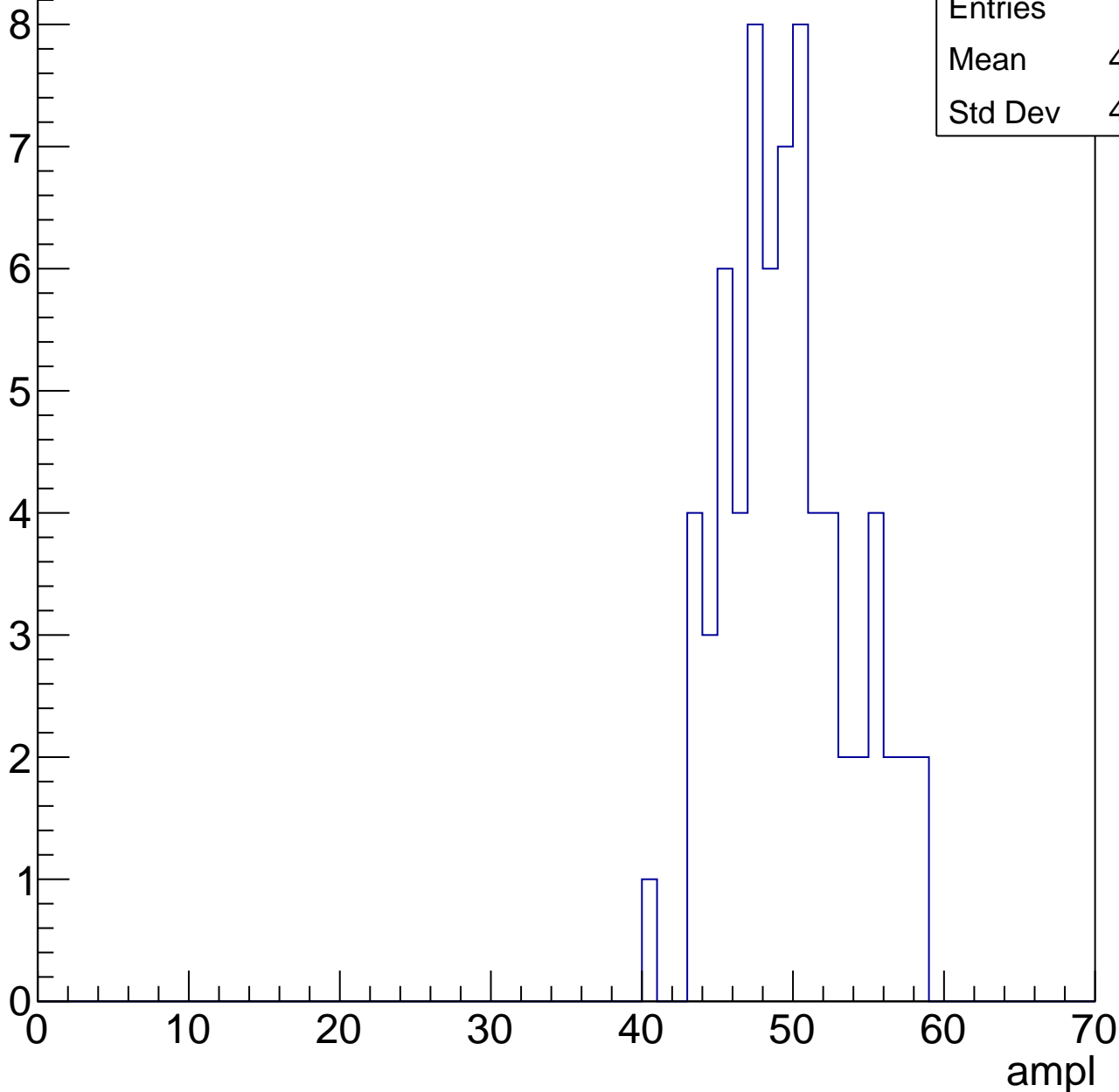


B1L102S, U9-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.17
Std Dev	4.068

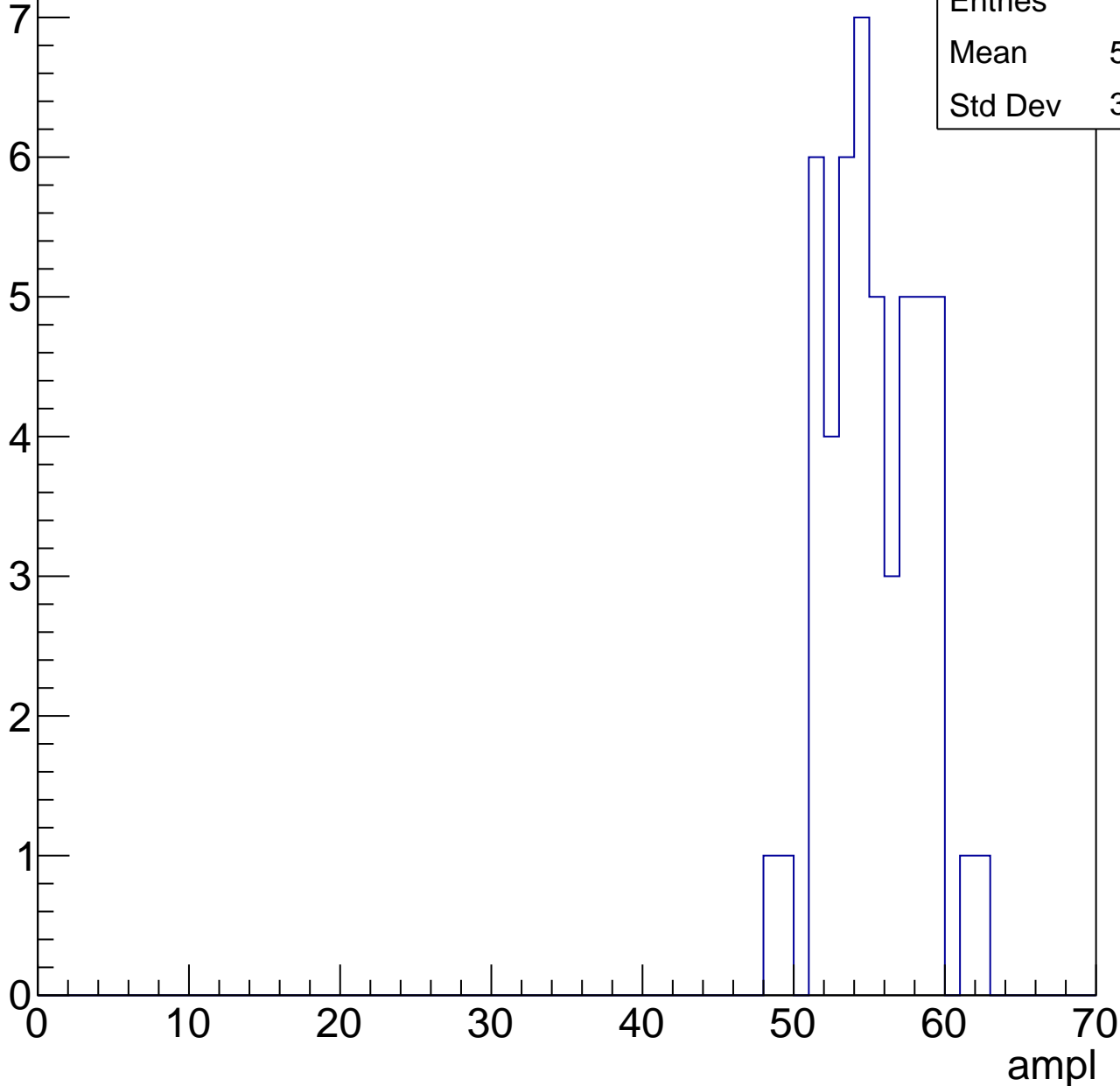


B1L102S, U9-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	54.86
Std Dev	3.098

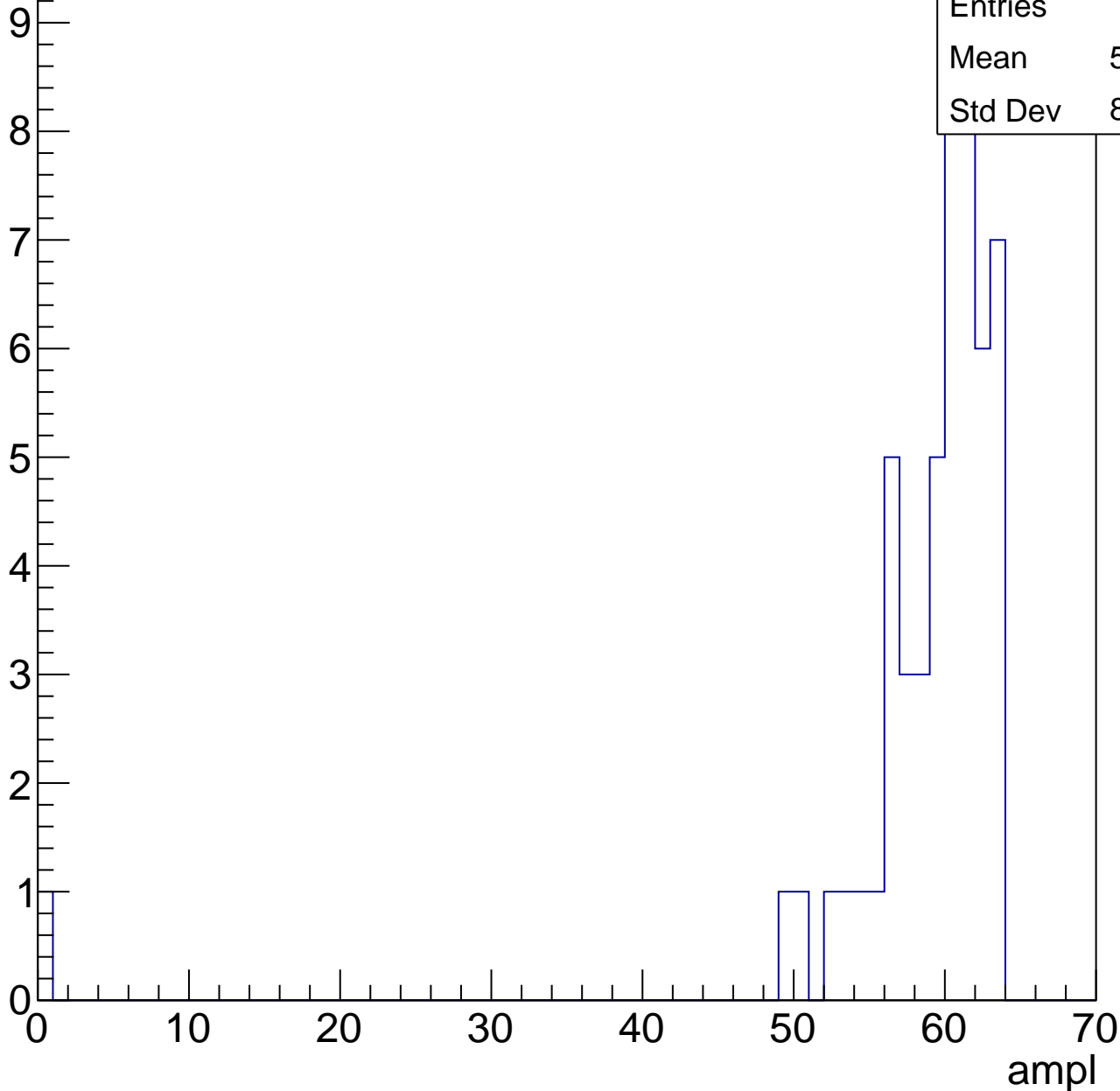


B1L102S, U9-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

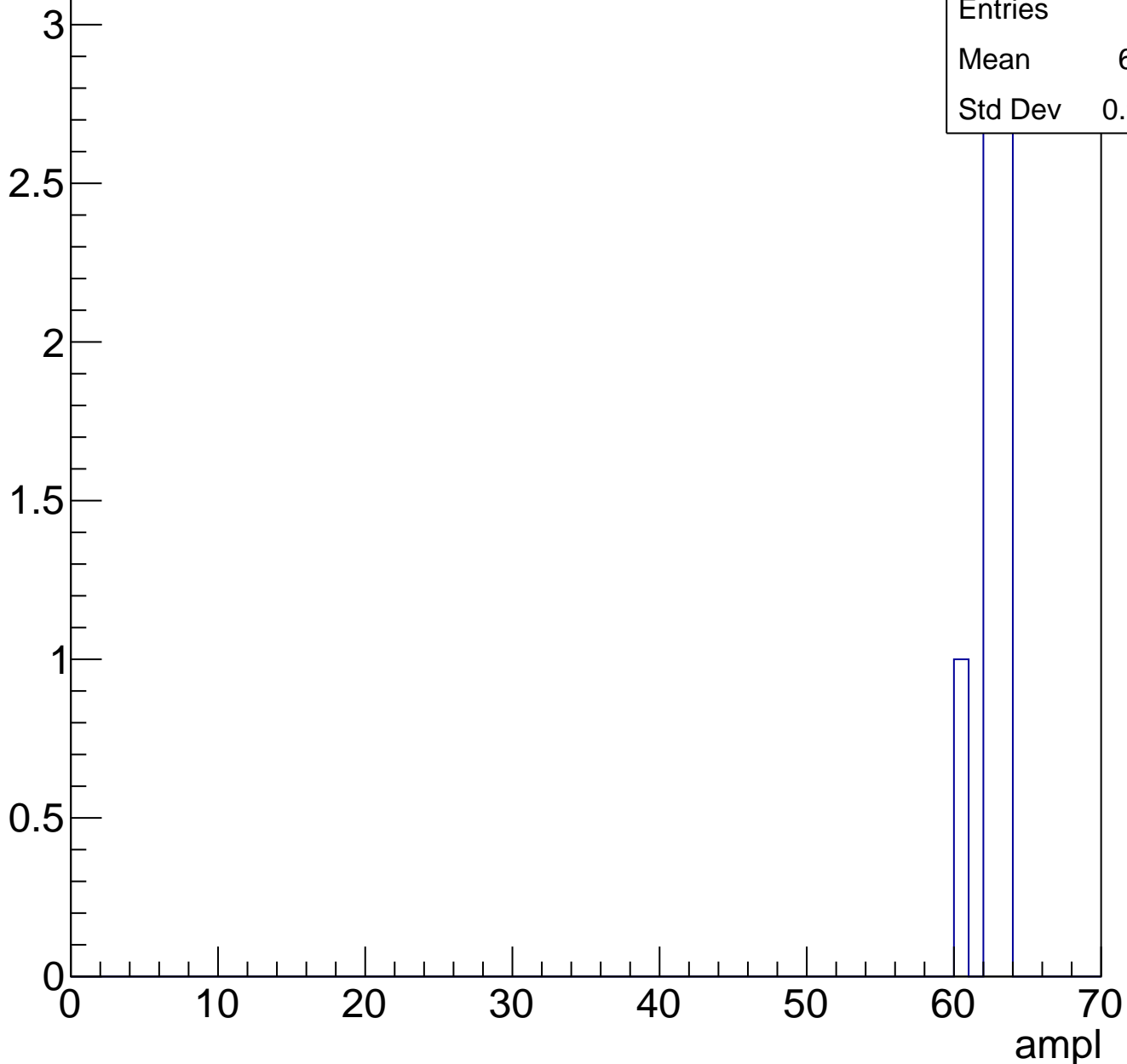
Entries	54
Mean	58.06
Std Dev	8.618



B1L102S, U9-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch92, adc0

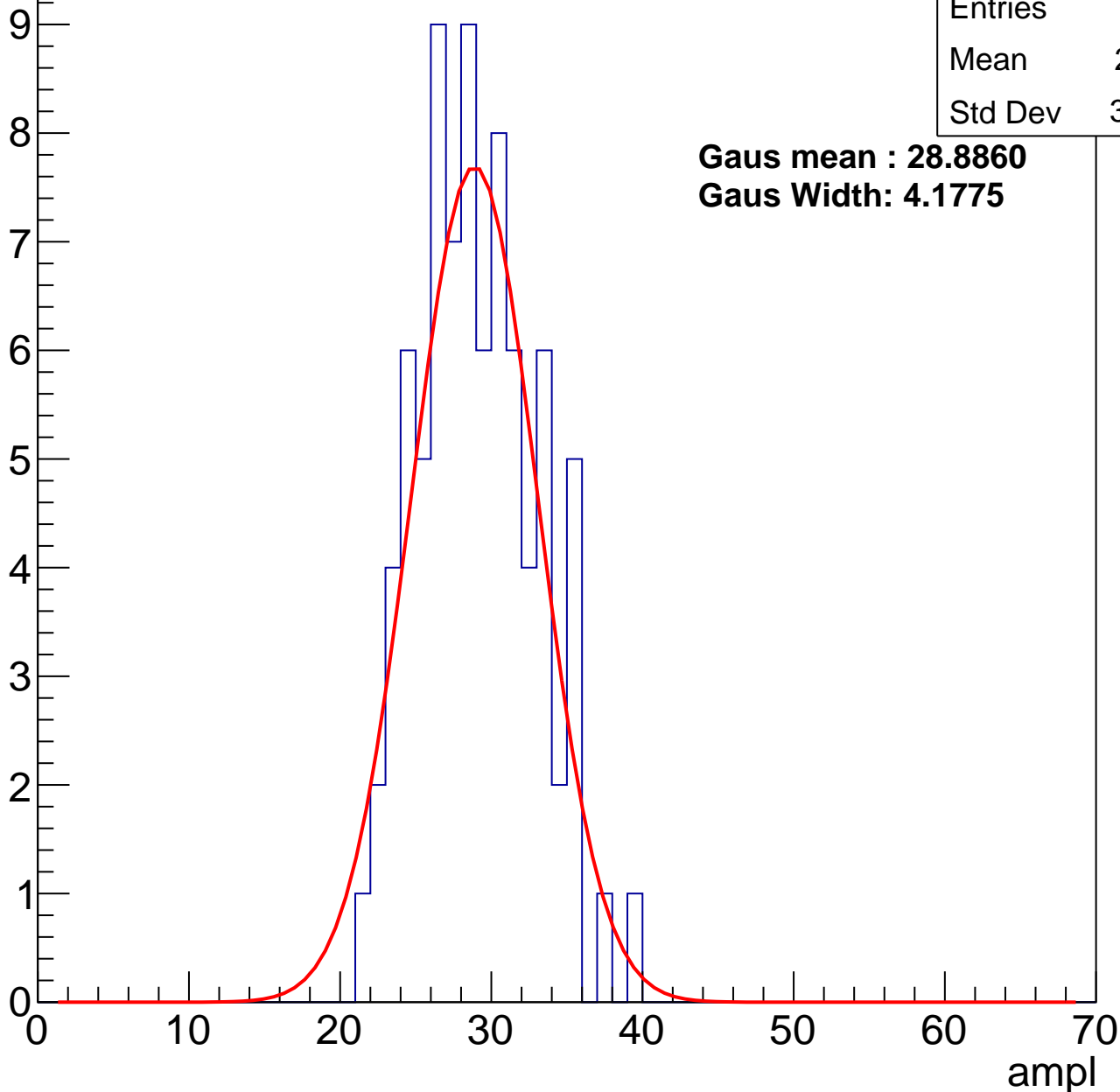
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.61
Std Dev	3.825

Gaus mean : 28.8860

Gaus Width: 4.1775



B1L102S, U9-ch92, adc1

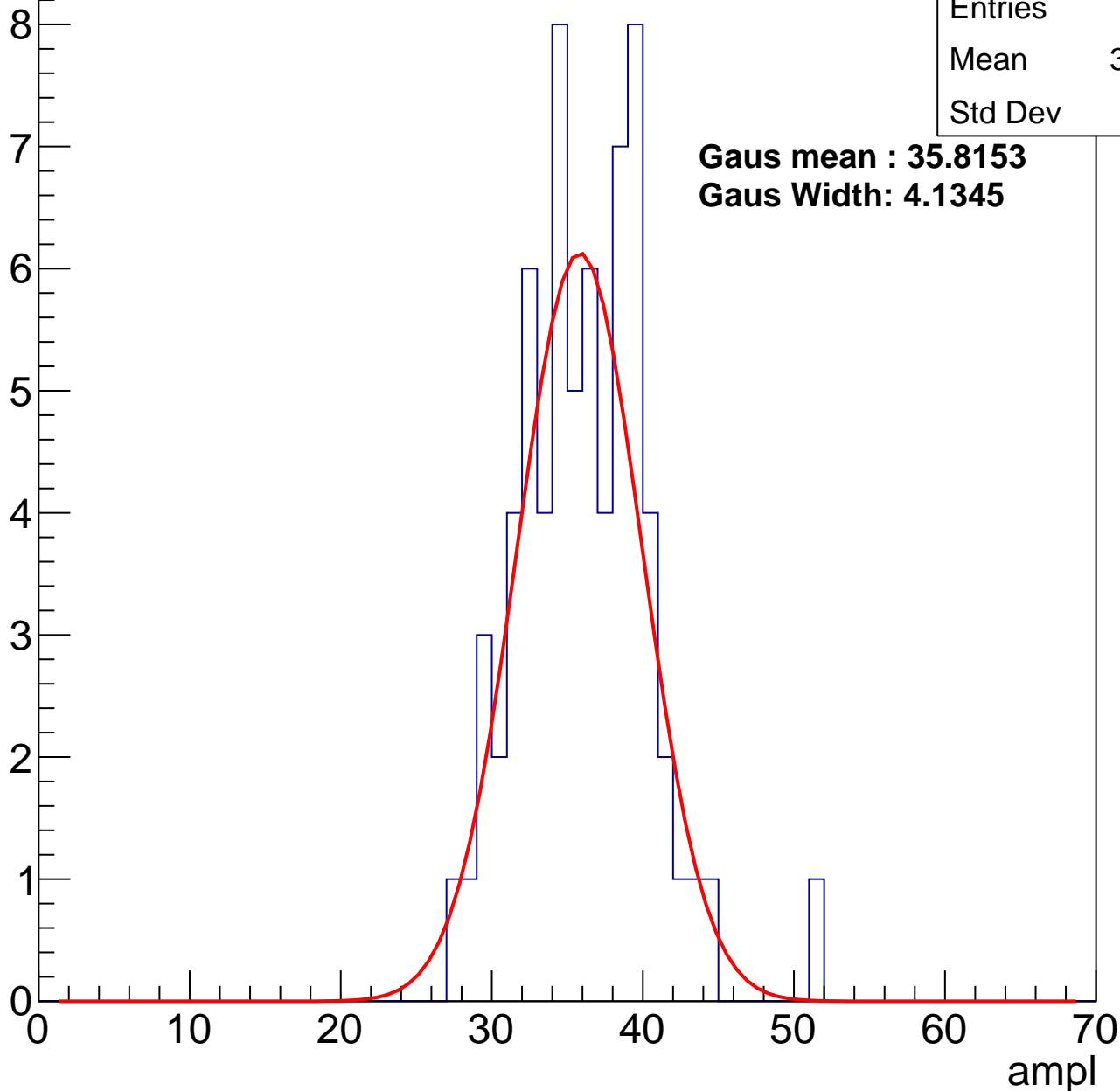
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.67
Std Dev	4.2

Gaus mean : 35.8153

Gaus Width: 4.1345



B1L102S, U9-ch92, adc2

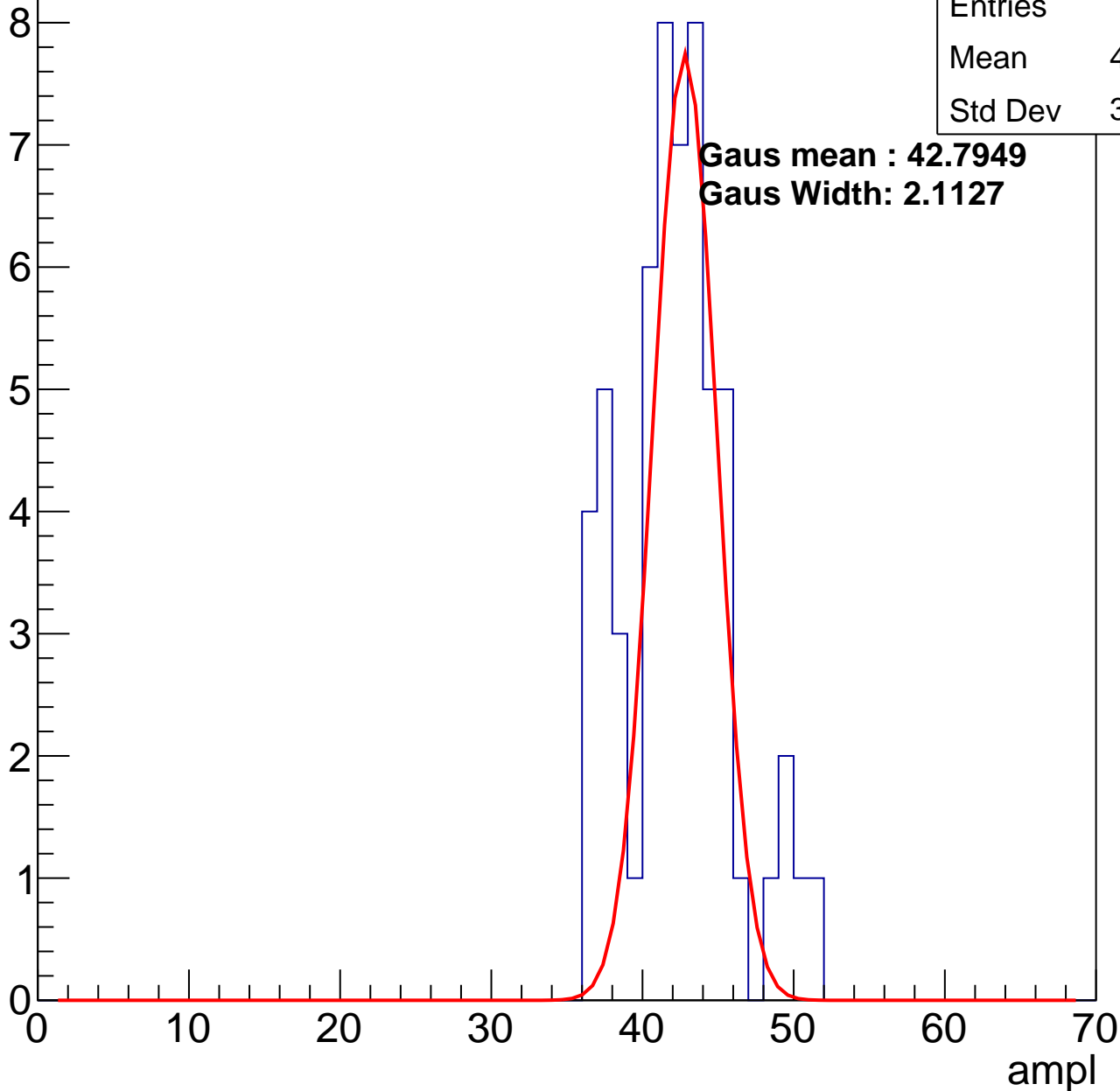
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	41.83
Std Dev	3.534

Gaus mean : 42.7949

Gaus Width: 2.1127

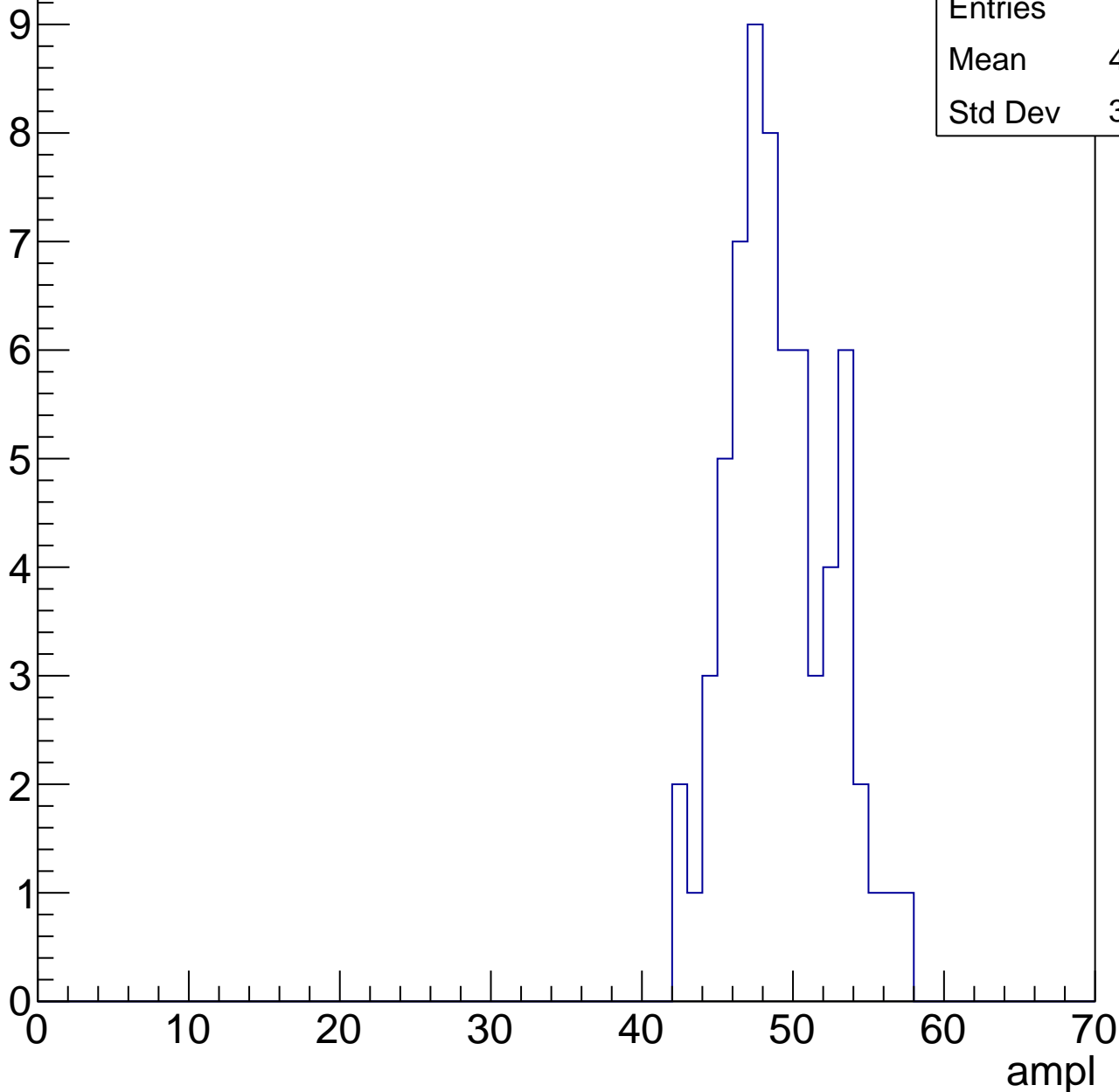


B1L102S, U9-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.65
Std Dev	3.367

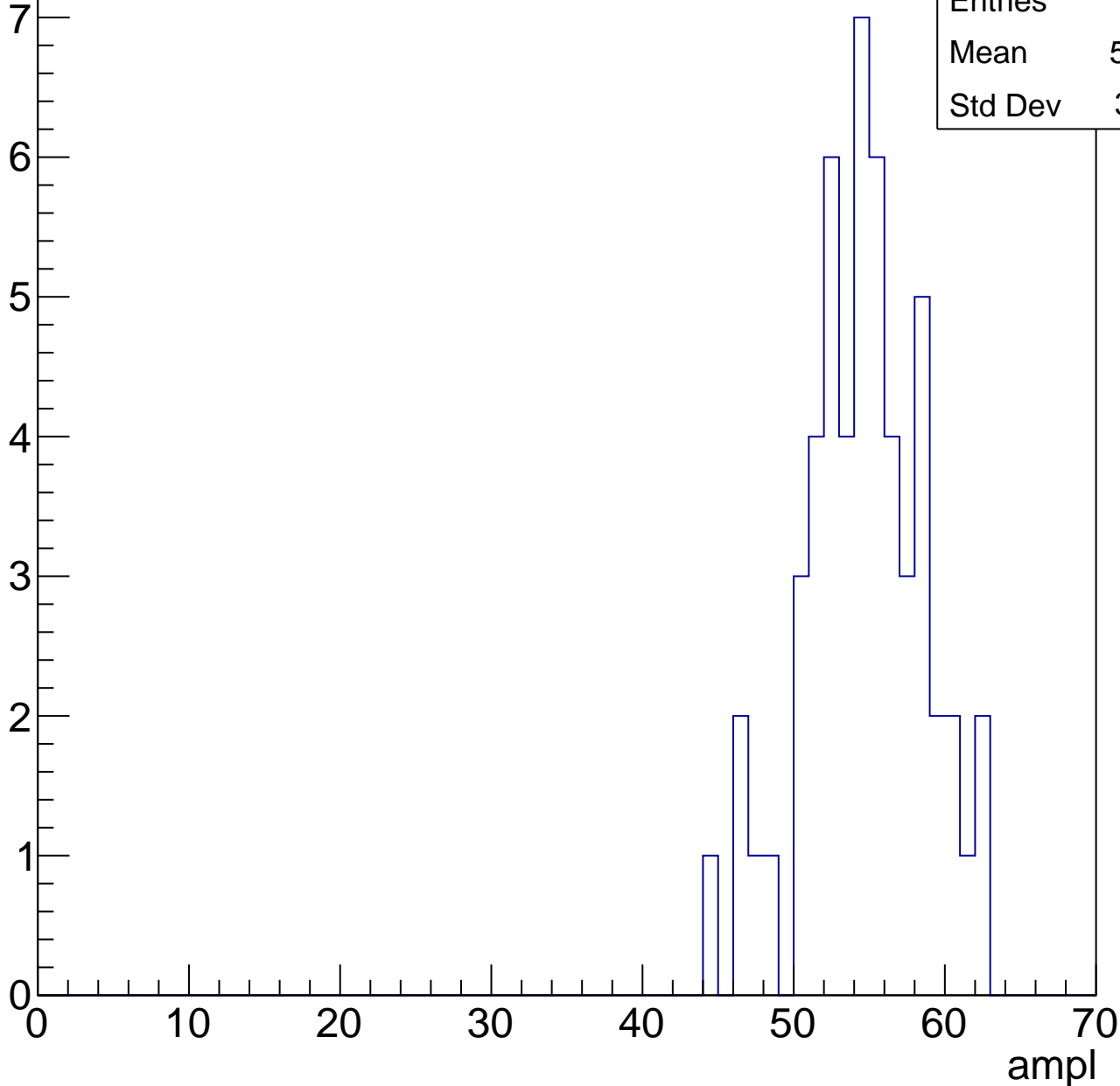


B1L102S, U9-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

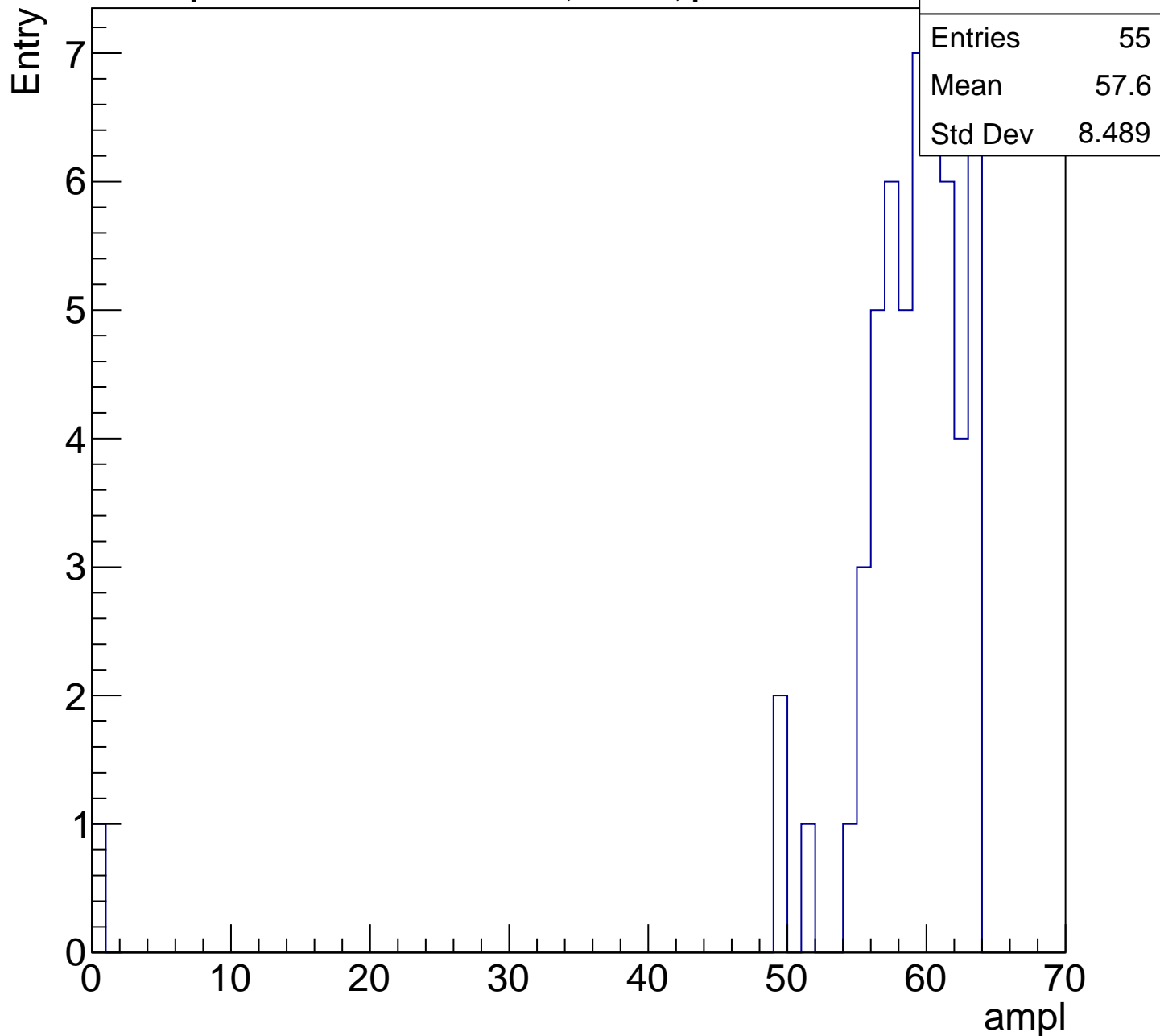
Entry

Entries	54
Mean	54.17
Std Dev	3.971



B1L102S, U9-ch92, adc5

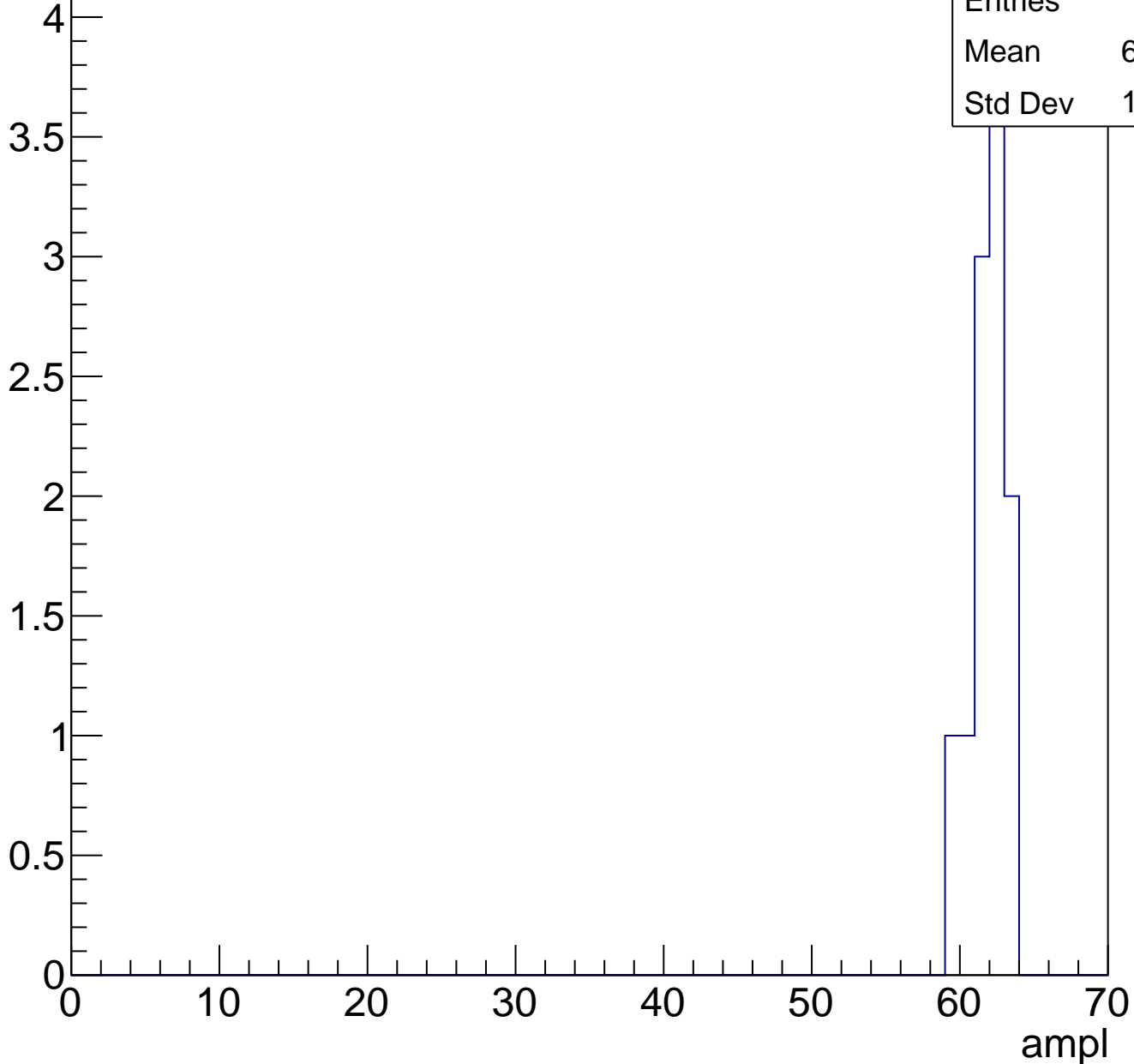
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U9-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	11
Mean	61.45
Std Dev	1.157

B1L102S, U9-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch93, adc0

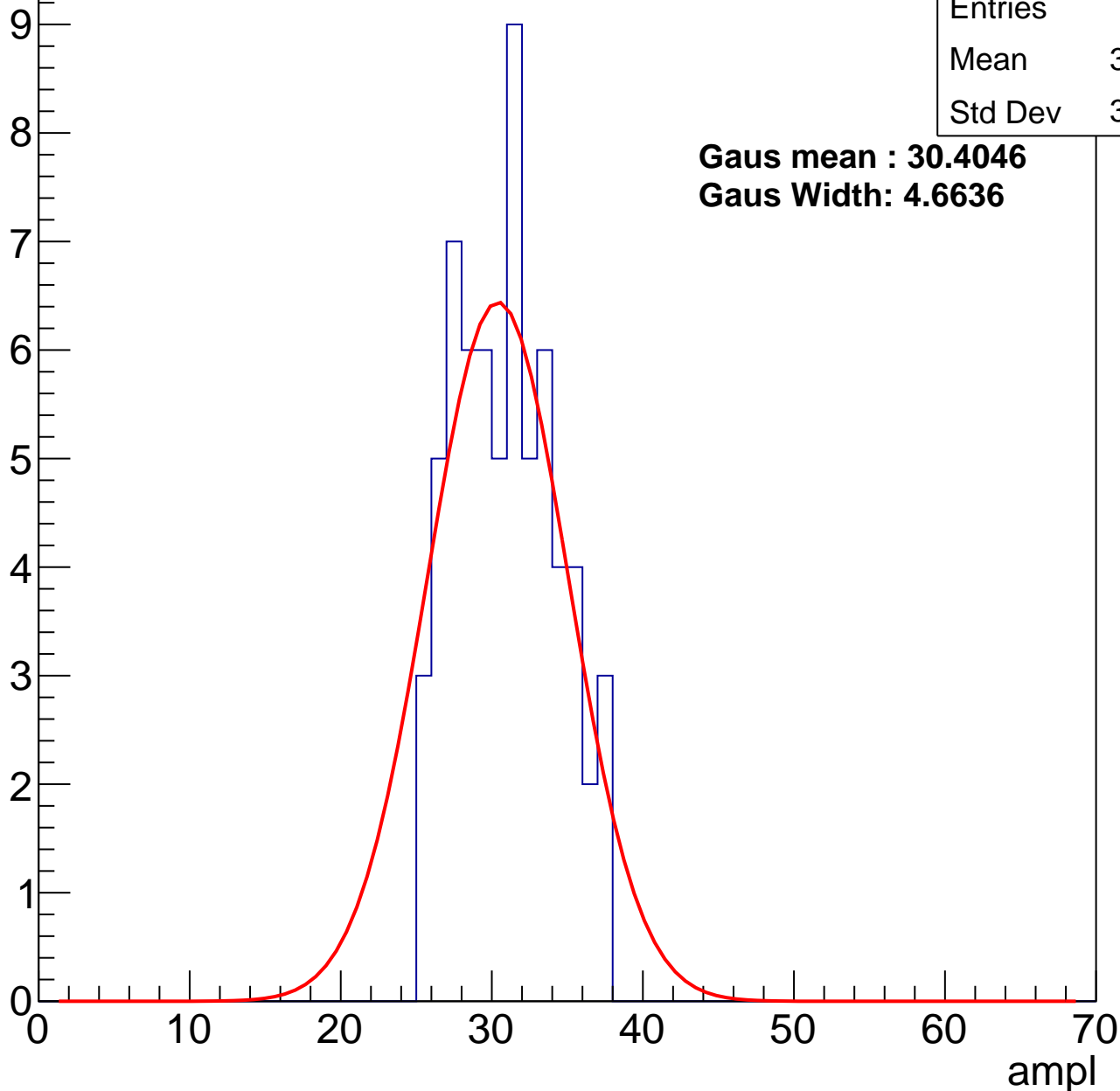
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.49
Std Dev	3.278

Gaus mean : 30.4046

Gaus Width: 4.6636



B1L102S, U9-ch93, adc1

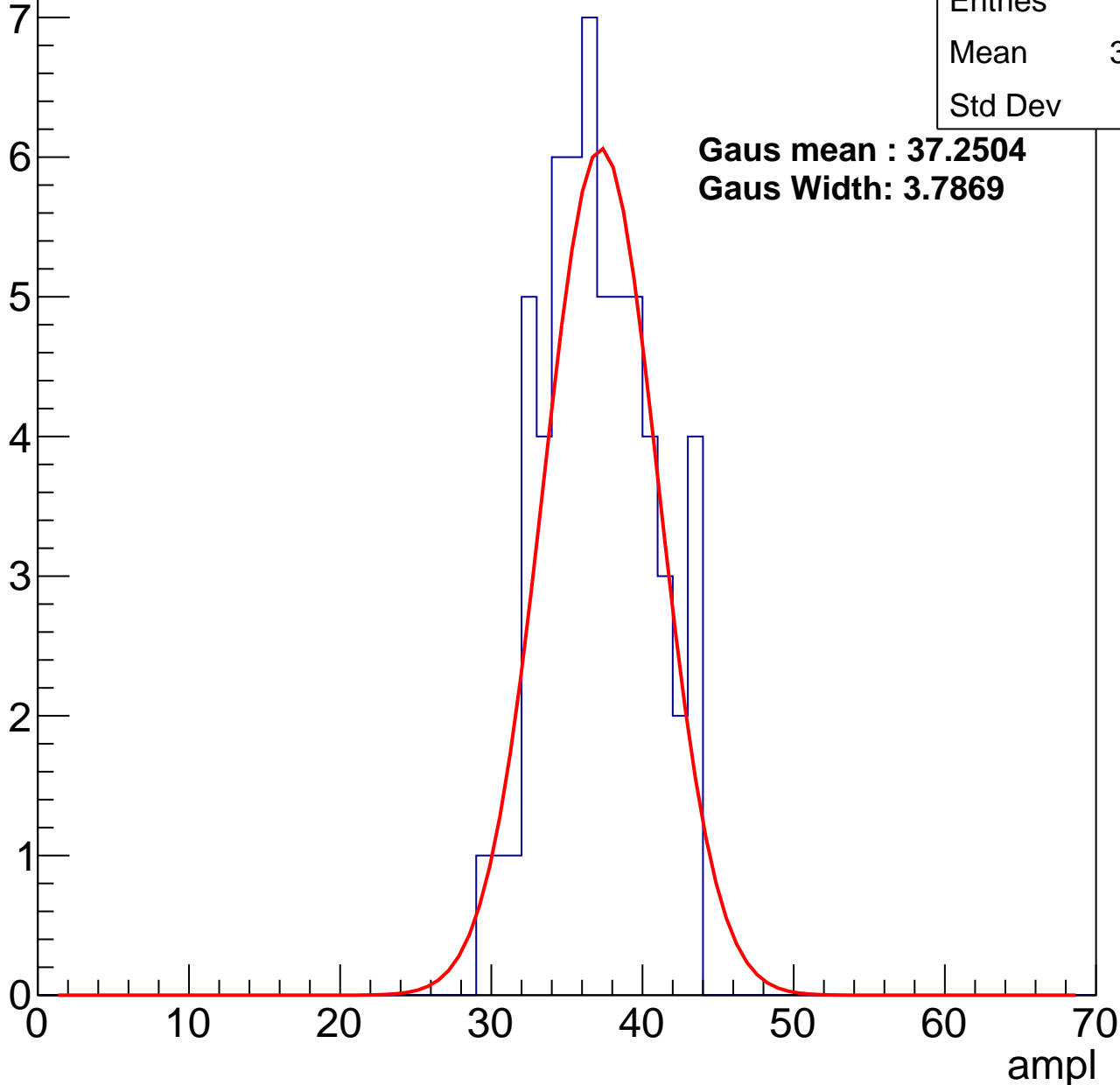
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.56
Std Dev	3.48

Gaus mean : 37.2504

Gaus Width: 3.7869



B1L102S, U9-ch93, adc2

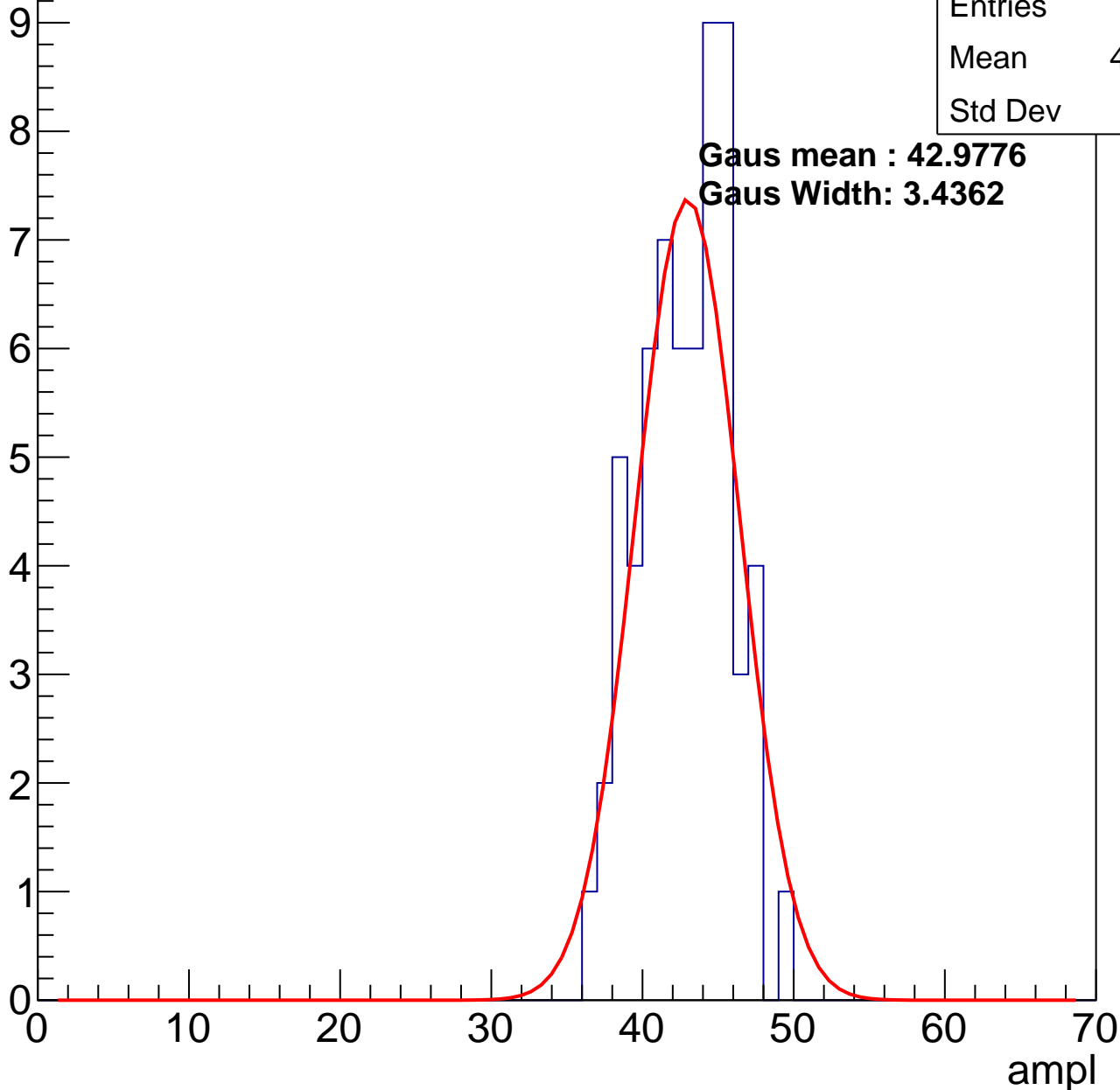
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.37
Std Dev	2.94

Gaus mean : 42.9776

Gaus Width: 3.4362



B1L102S, U9-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	49.79
Std Dev	3.364

Entry

10

8

6

4

2

0

0

10

20

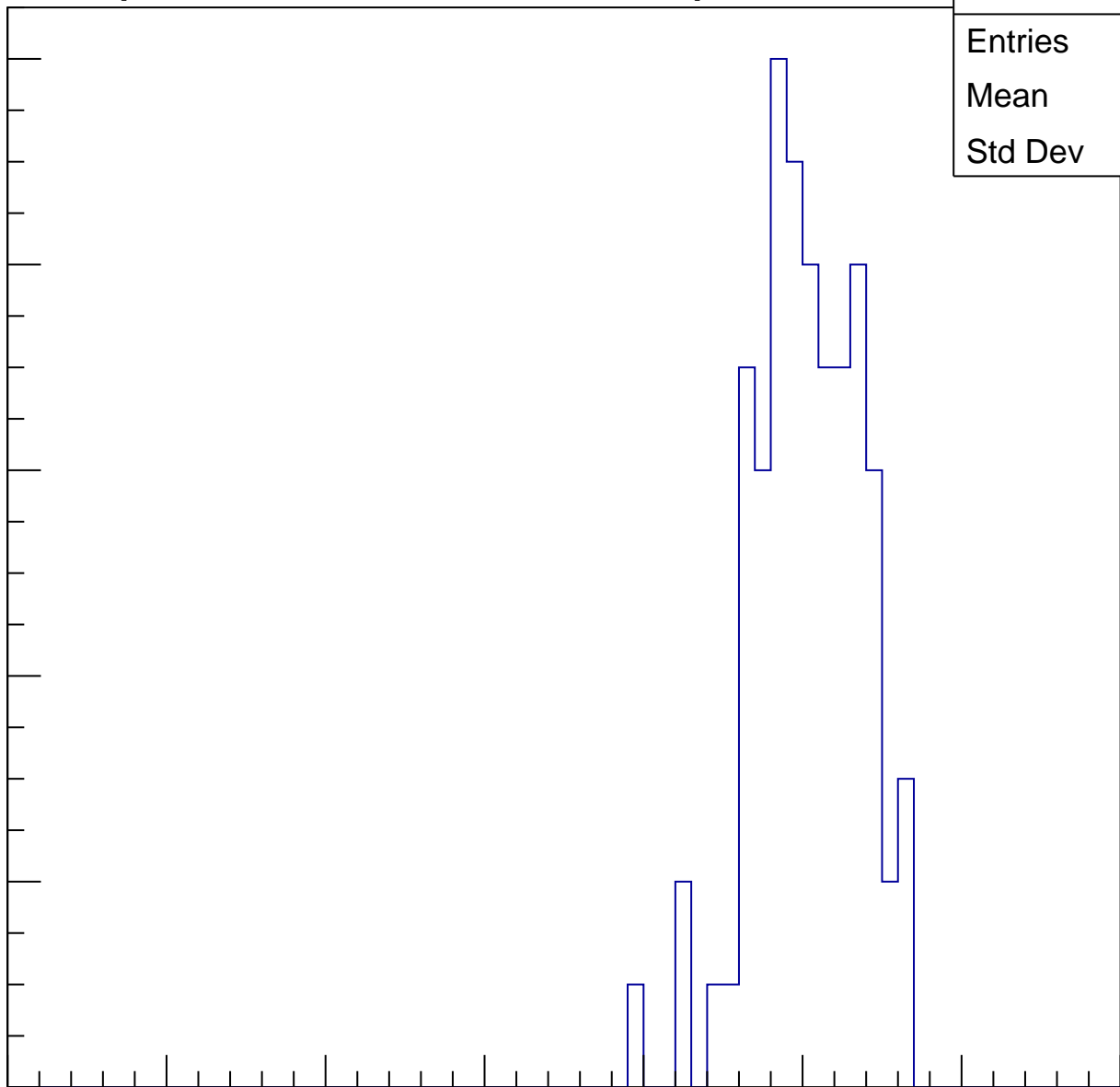
30

40

50

60

ampl

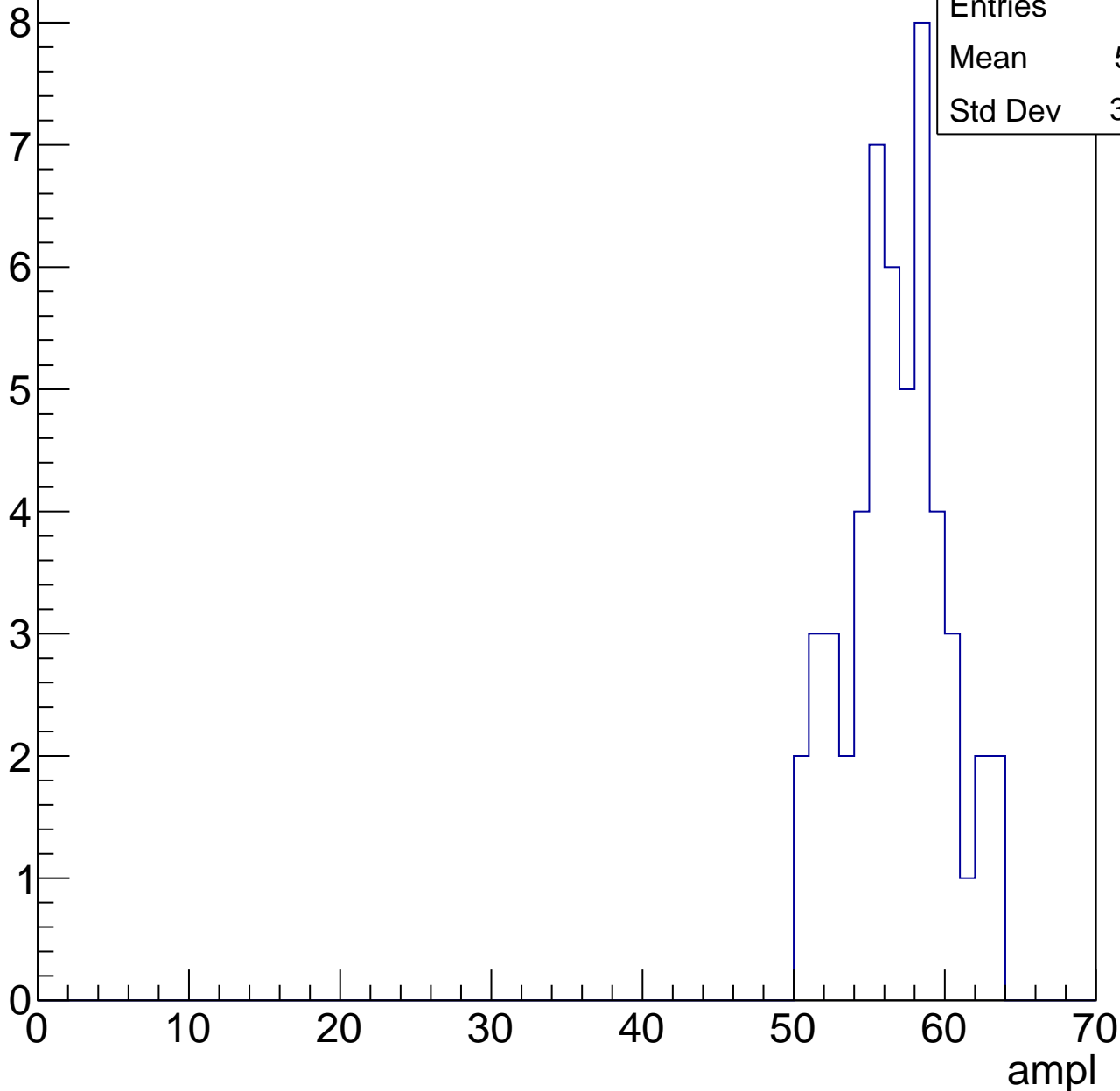


B1L102S, U9-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

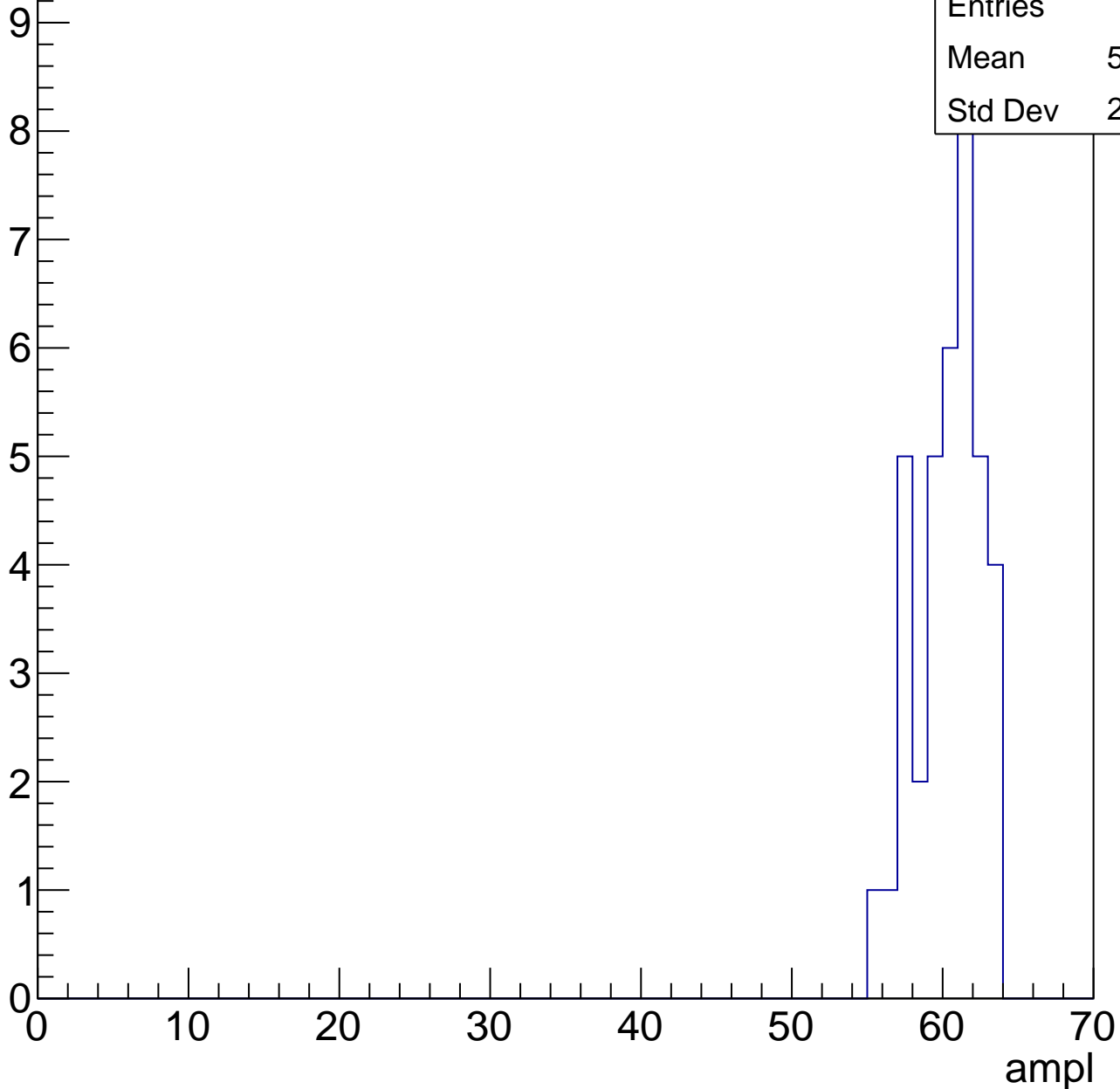
Entries	52
Mean	56.31
Std Dev	3.244



B1L102S, U9-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

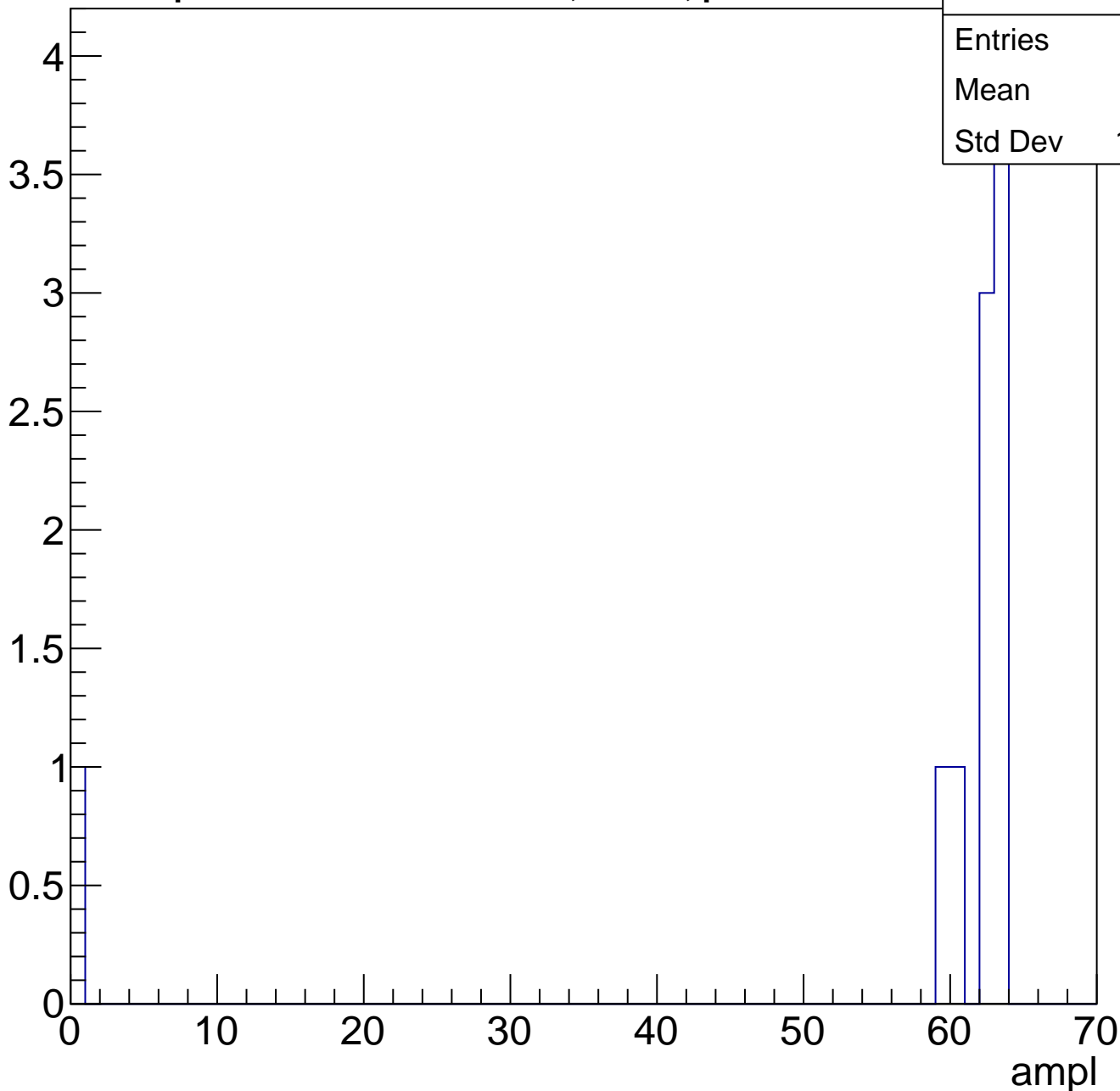
Entry



B1L102S, U9-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch94, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	28.43
Std Dev	3.866

Gaus mean : 28.4930

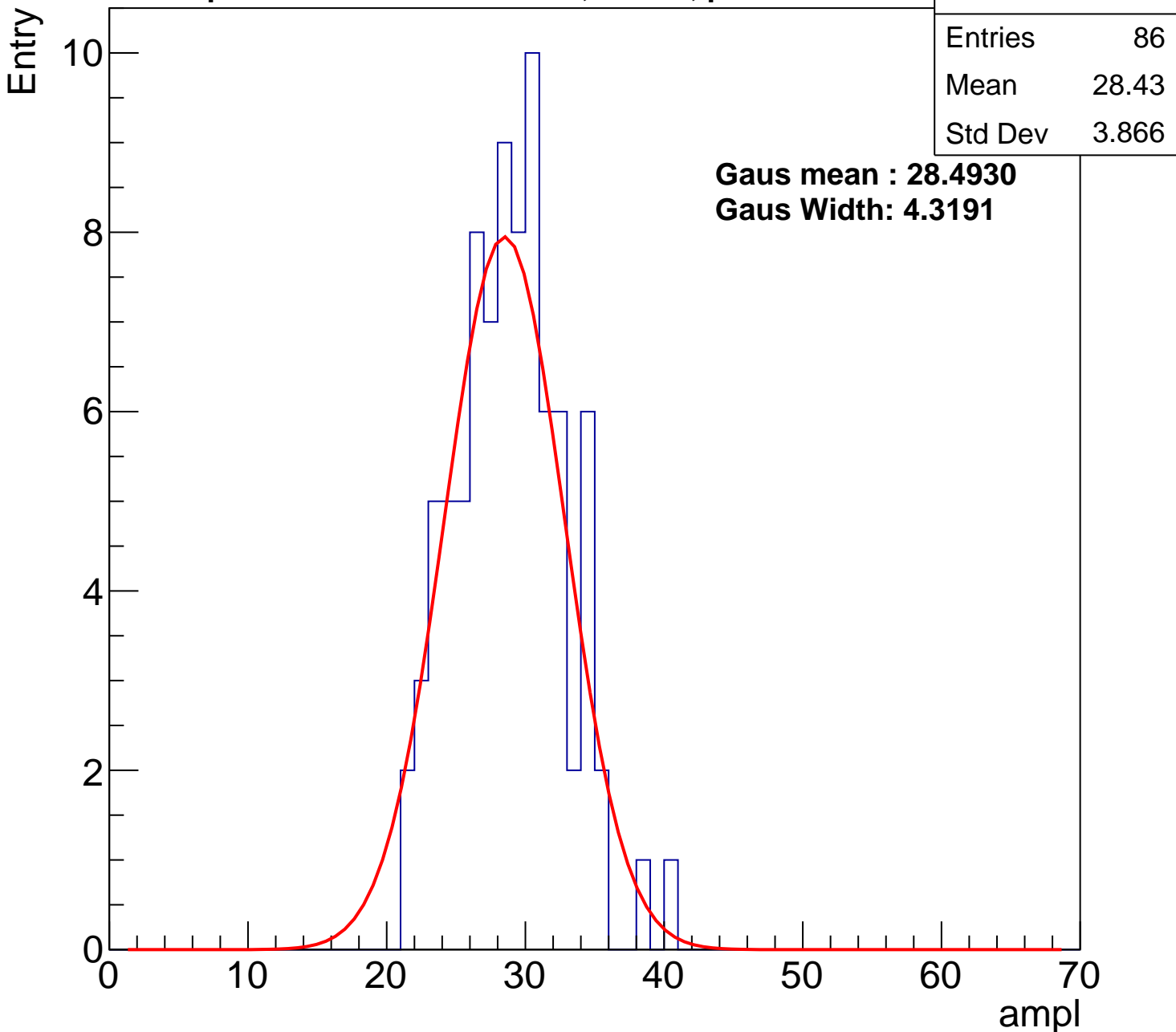
Gaus Width: 4.3191

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch94, adc1

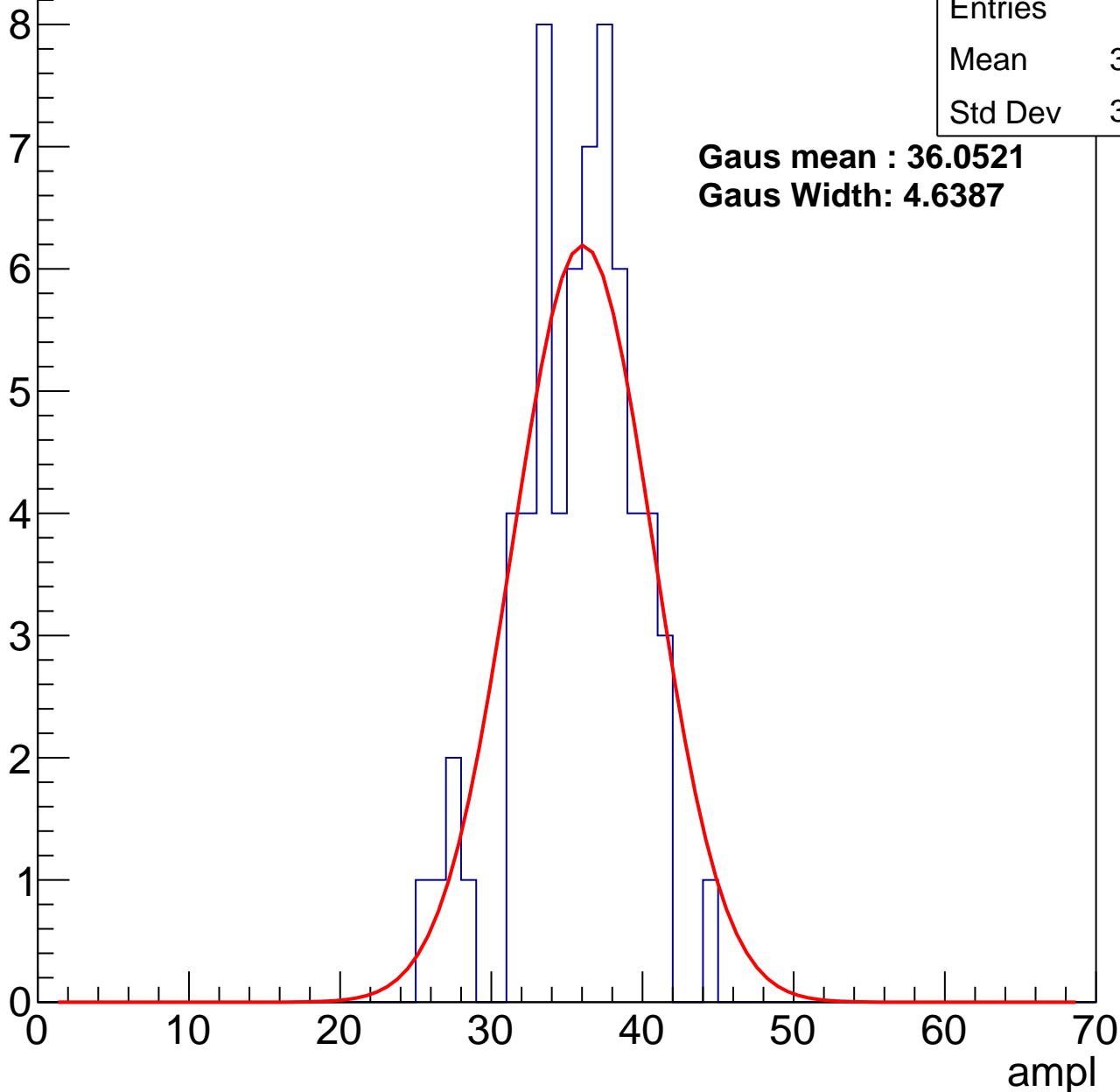
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.22
Std Dev	3.826

Gaus mean : 36.0521

Gaus Width: 4.6387



B1L102S, U9-ch94, adc2

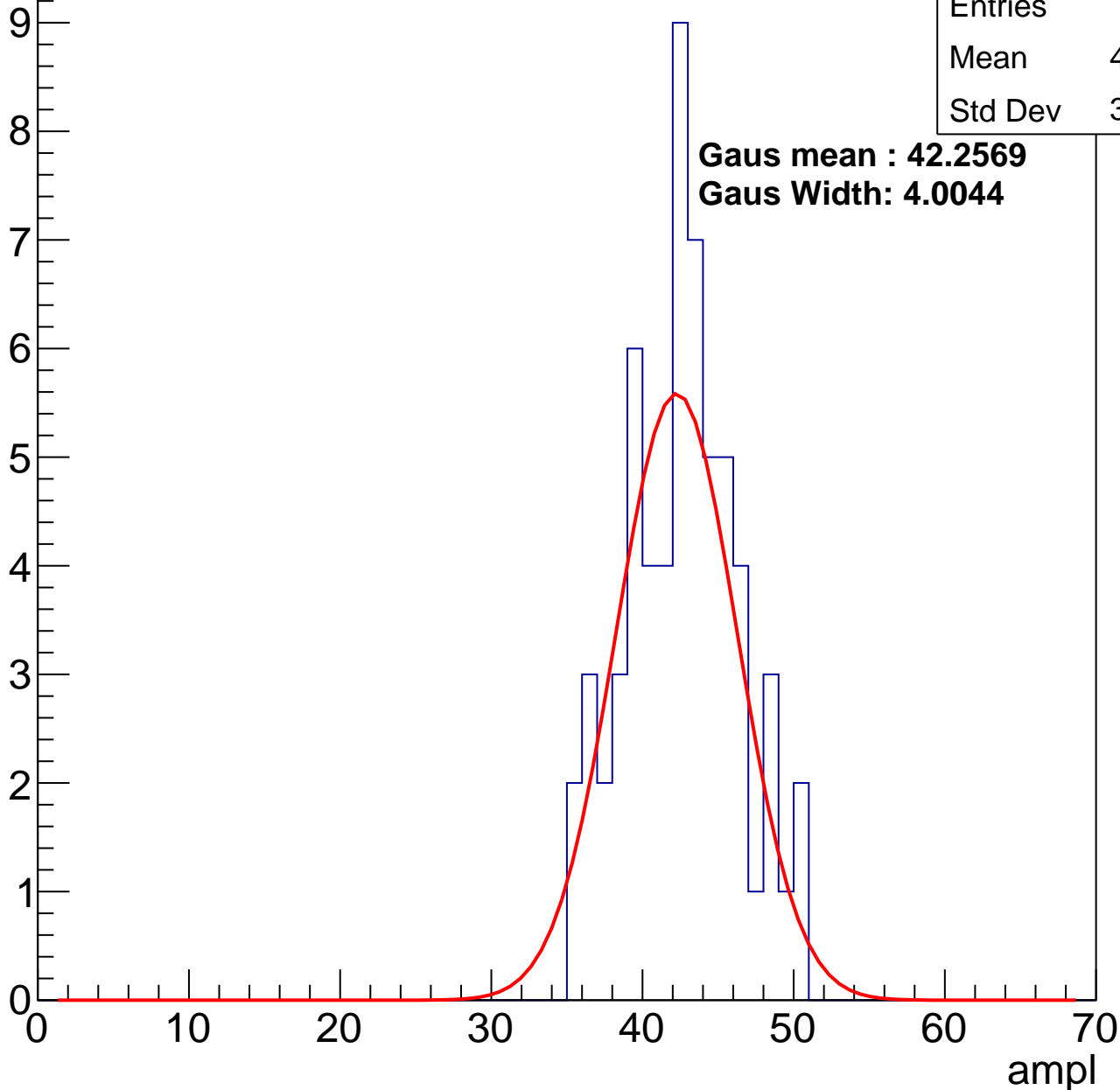
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.16
Std Dev	3.672

Gaus mean : 42.2569

Gaus Width: 4.0044

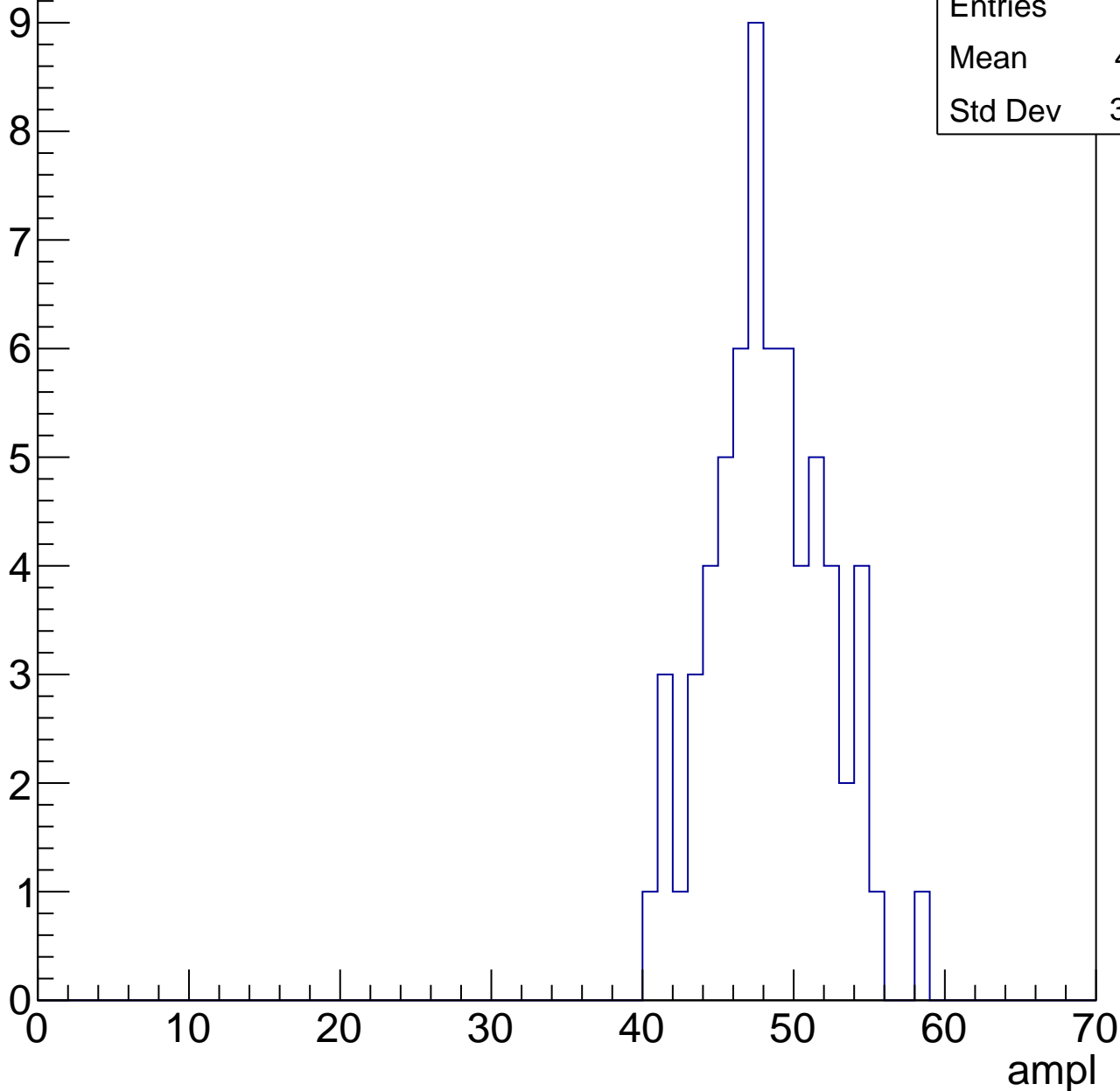


B1L102S, U9-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.91
Std Dev	3.798



B1L102S, U9-ch94, adc4

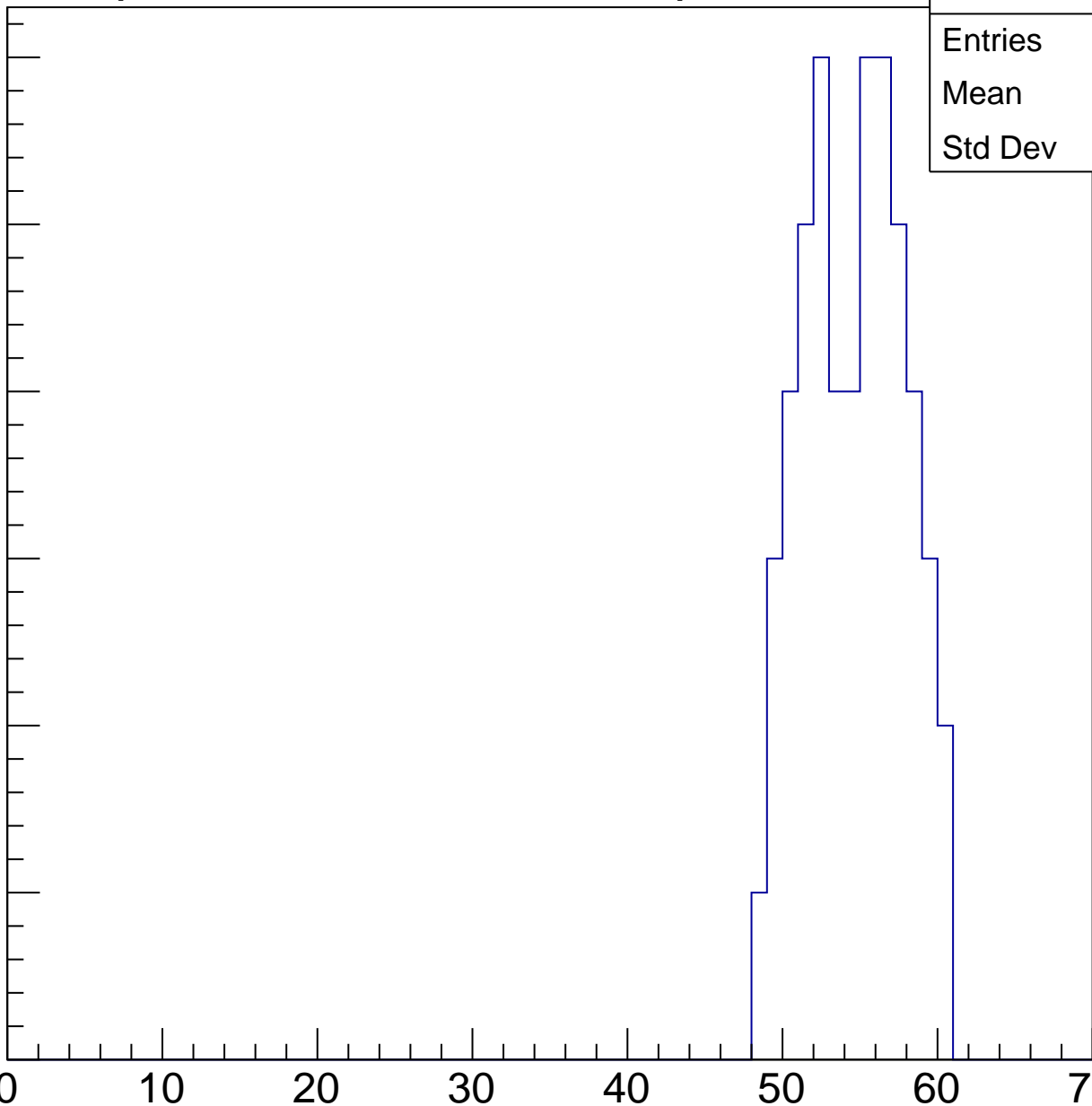
calib_packv5_042523_0143.root, FC#11, port A2

Entry

6
5
4
3
2
1
0

Entries	53
Mean	54.15
Std Dev	3.171

ampl

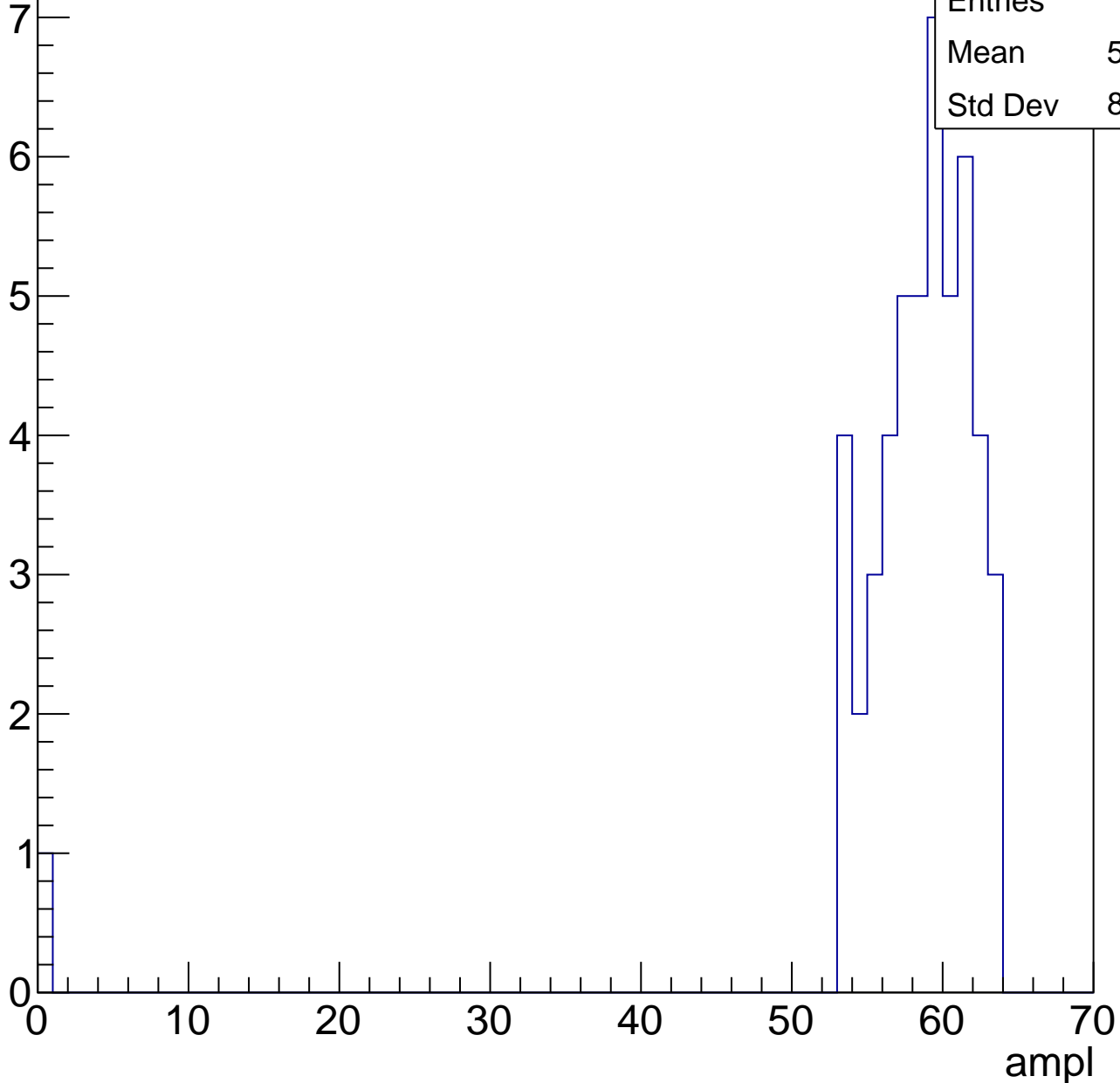


B1L102S, U9-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.14
Std Dev	8.722

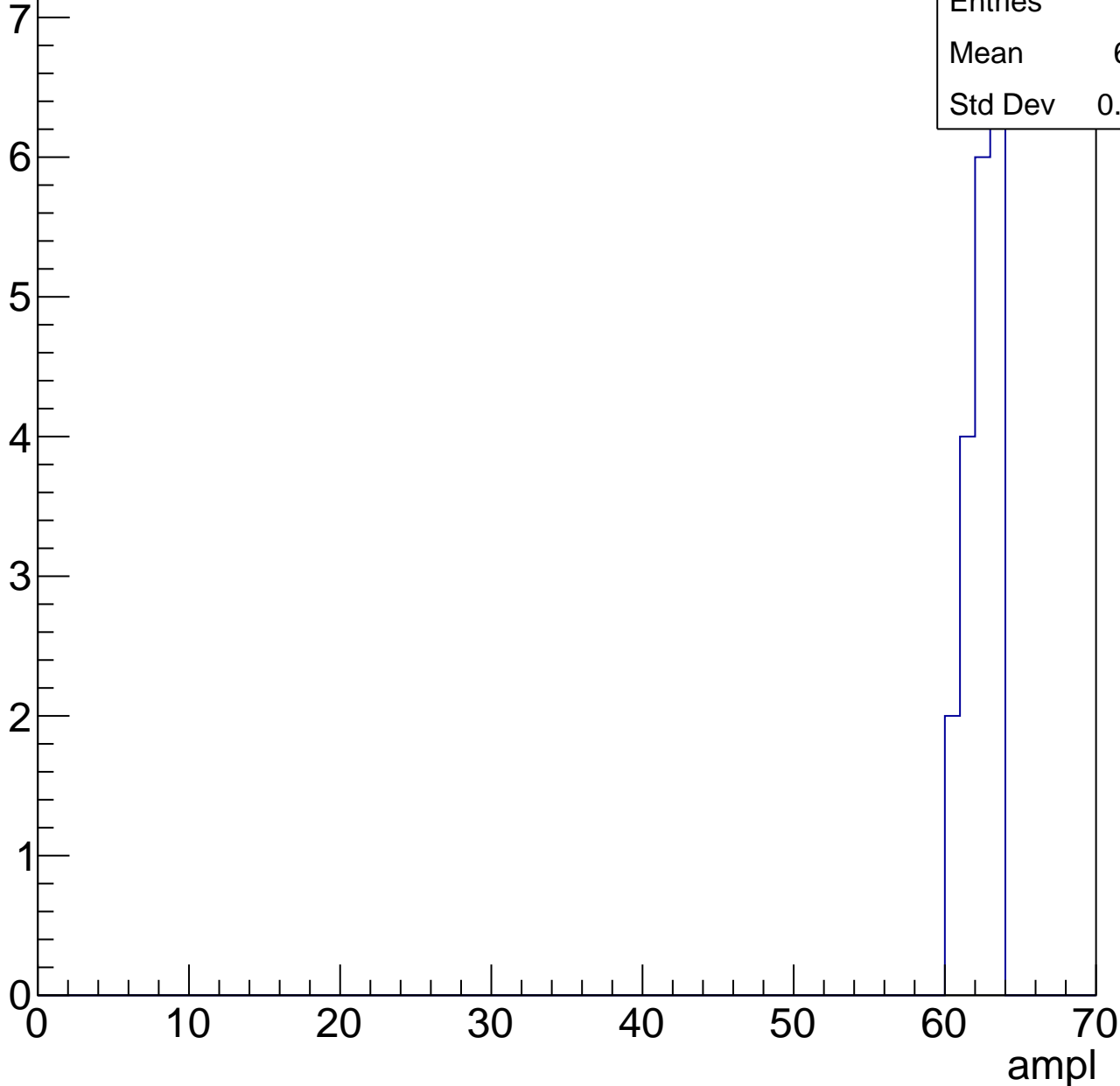


B1L102S, U9-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

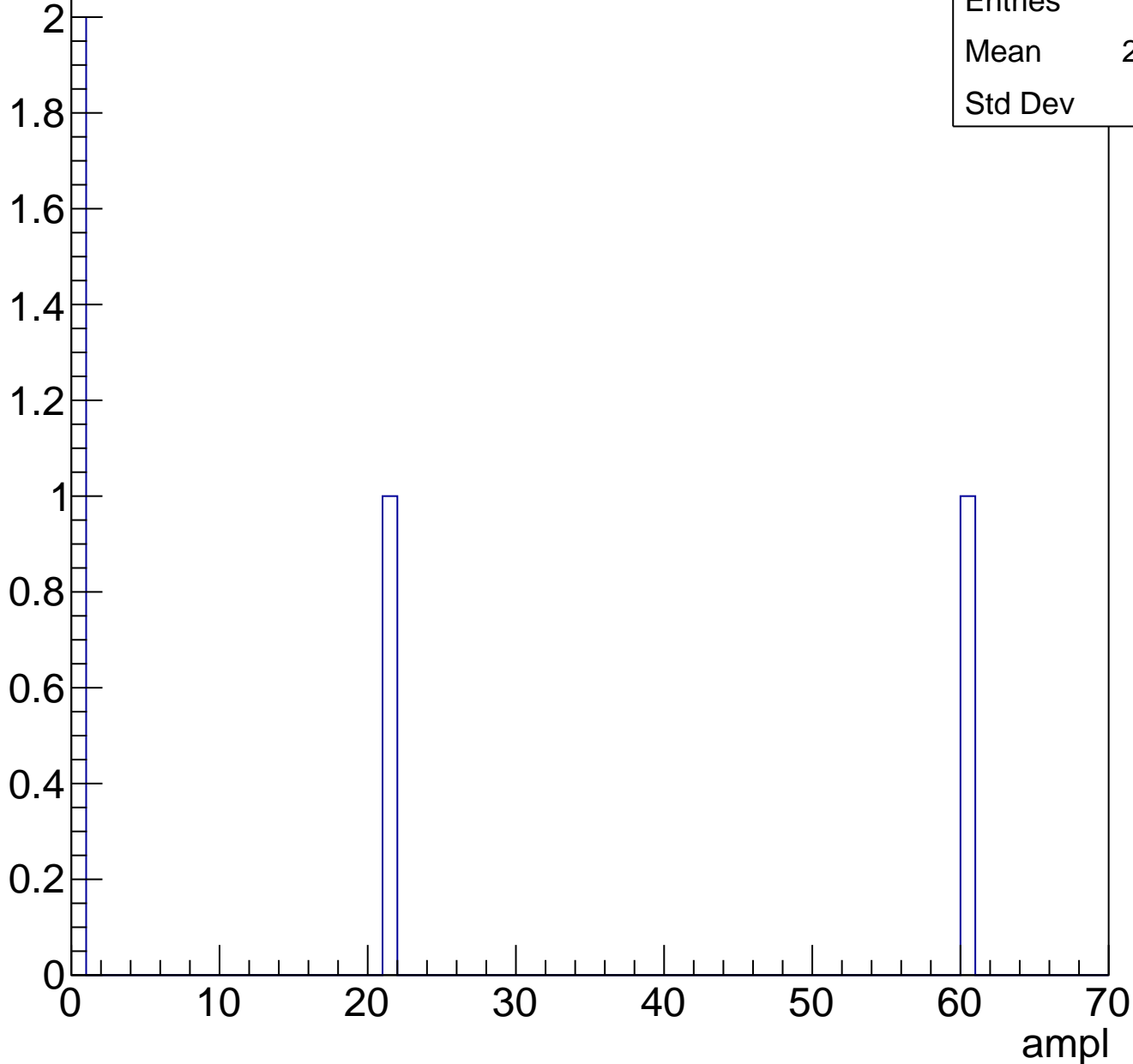
Entries	19
Mean	61.95
Std Dev	0.9986



B1L102S, U9-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch95, adc0

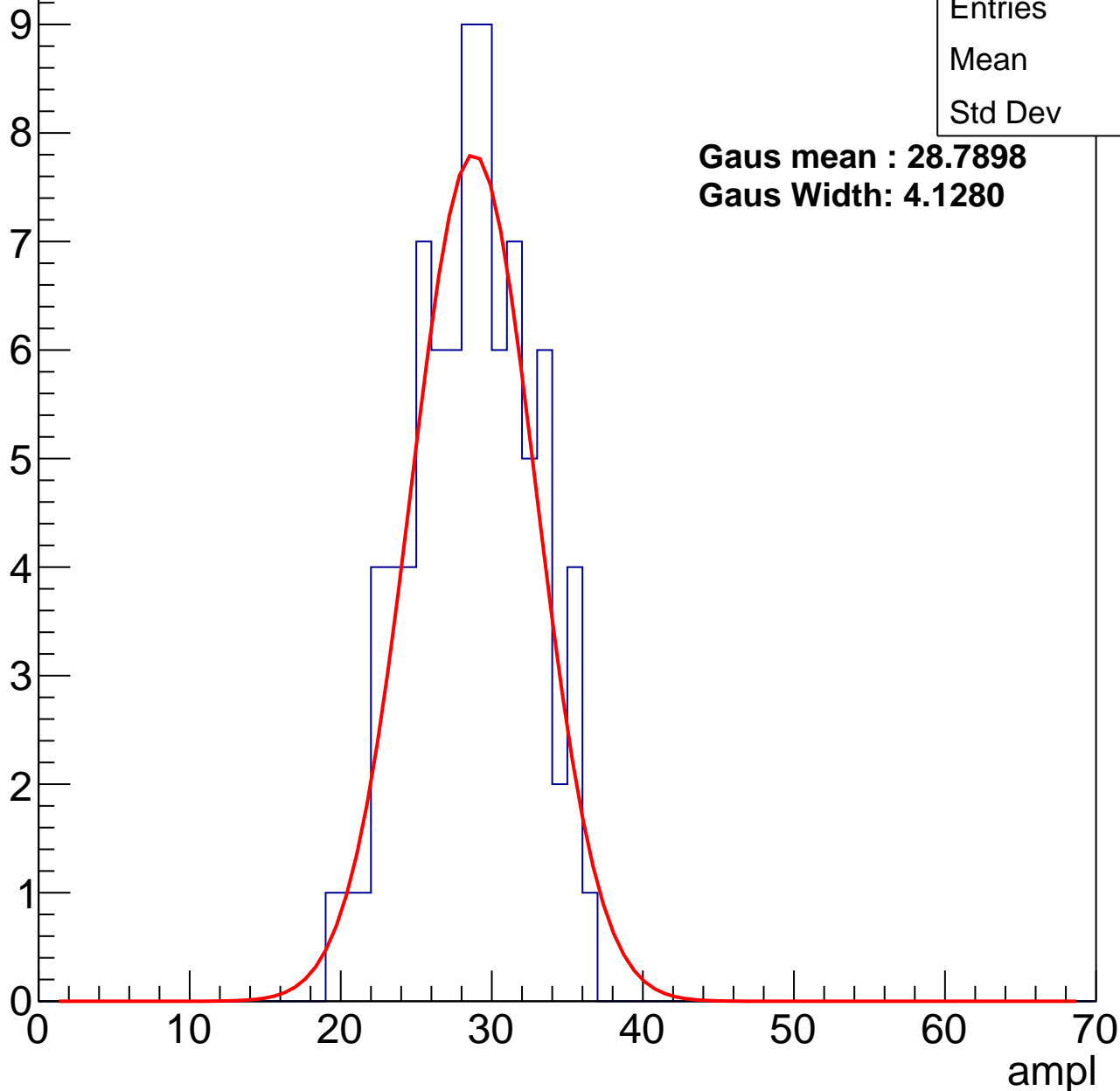
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.2
Std Dev	3.88

Gaus mean : 28.7898

Gaus Width: 4.1280



B1L102S, U9-ch95, adc1

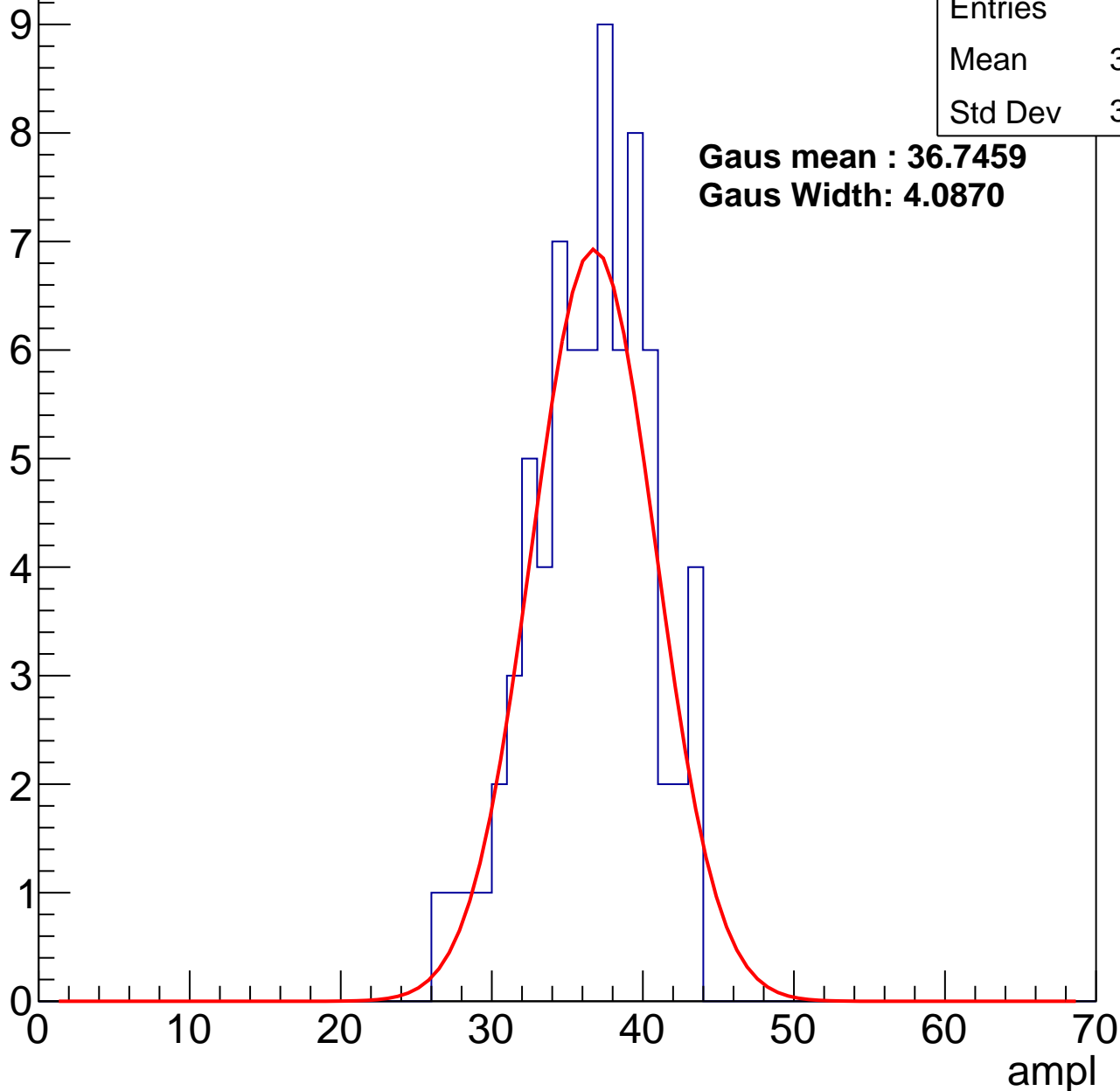
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.08
Std Dev	3.872

Gaus mean : 36.7459

Gaus Width: 4.0870



B1L102S, U9-ch95, adc2

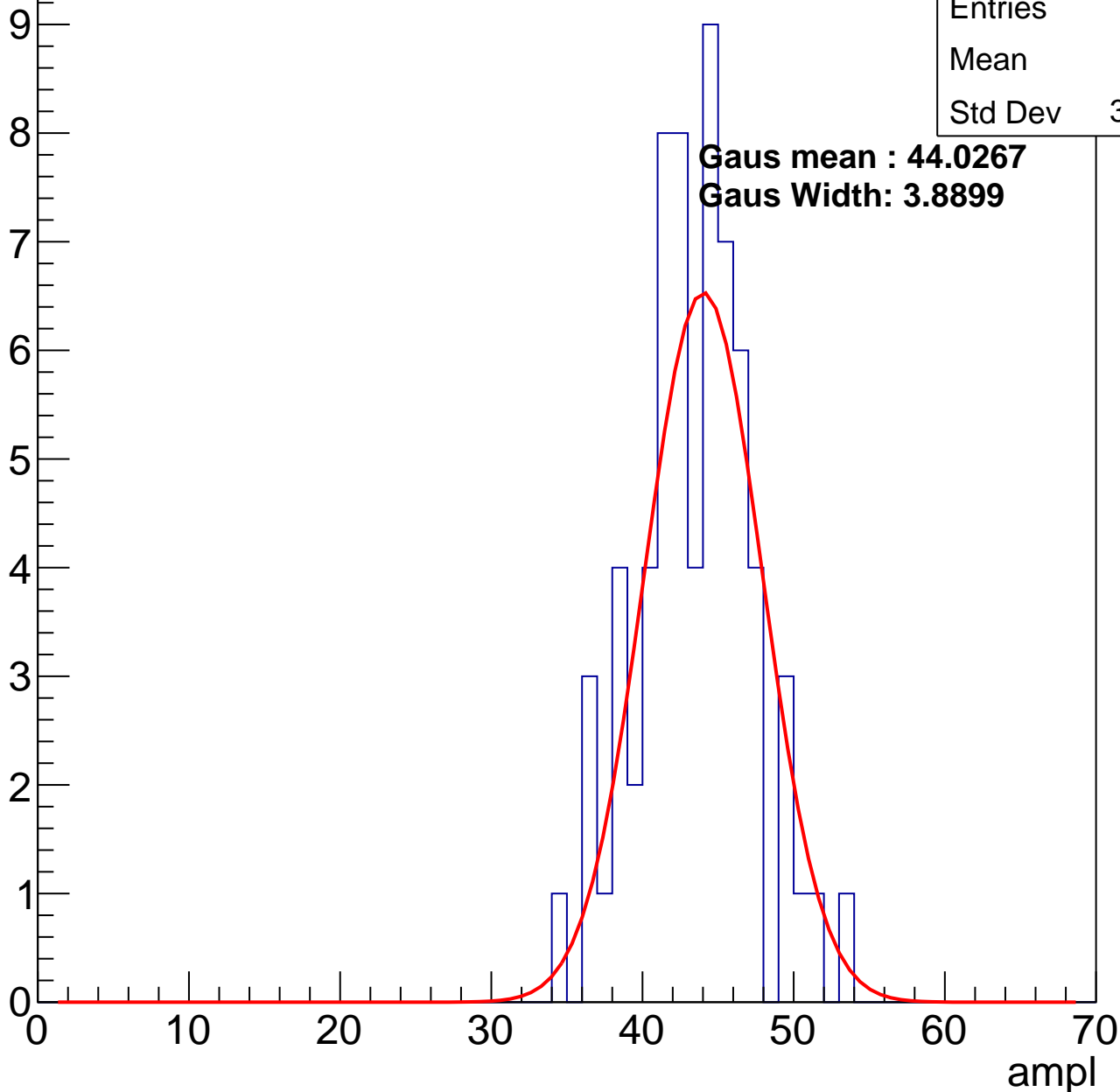
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43
Std Dev	3.762

Gaus mean : 44.0267

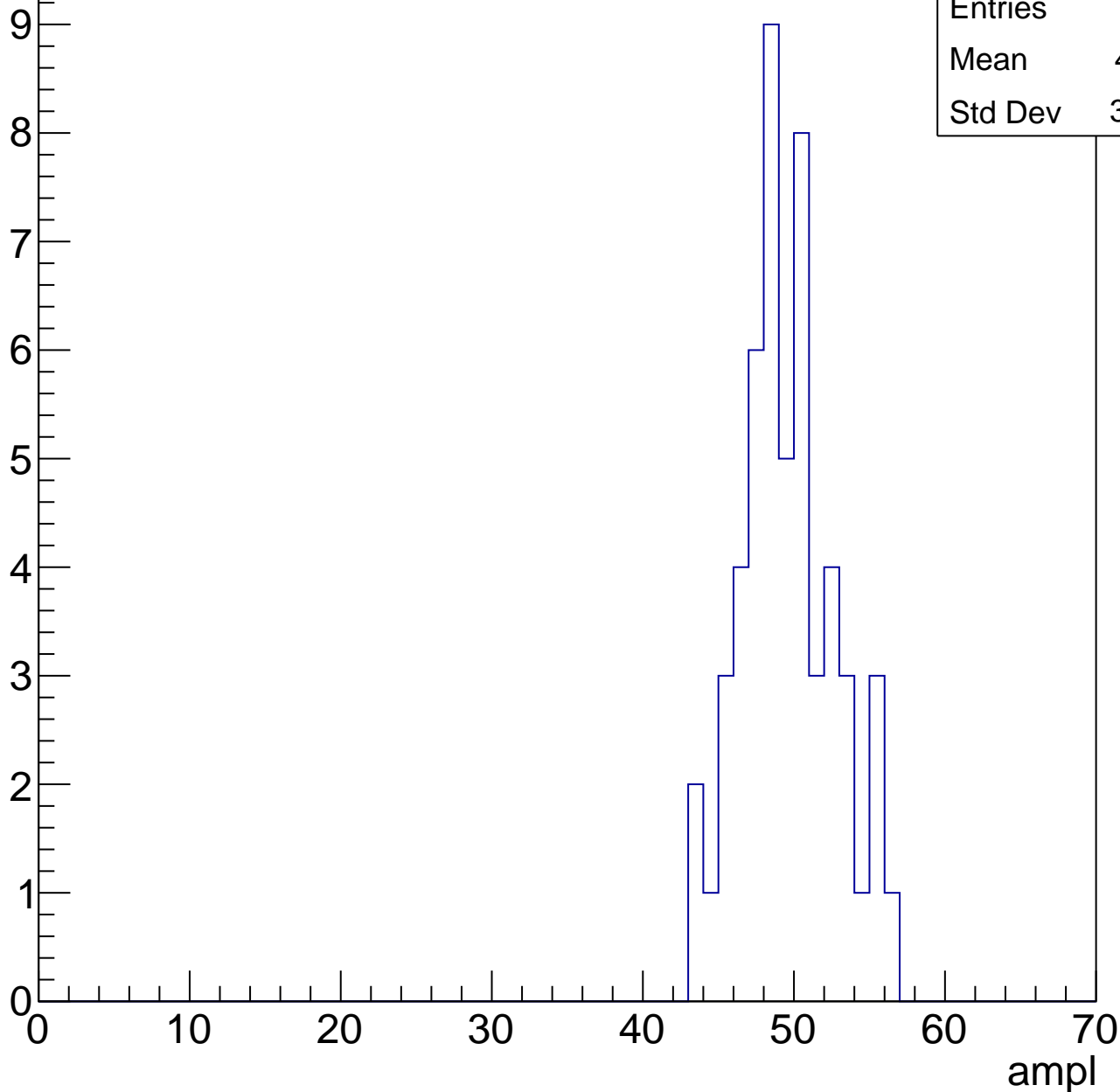
Gaus Width: 3.8899



B1L102S, U9-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



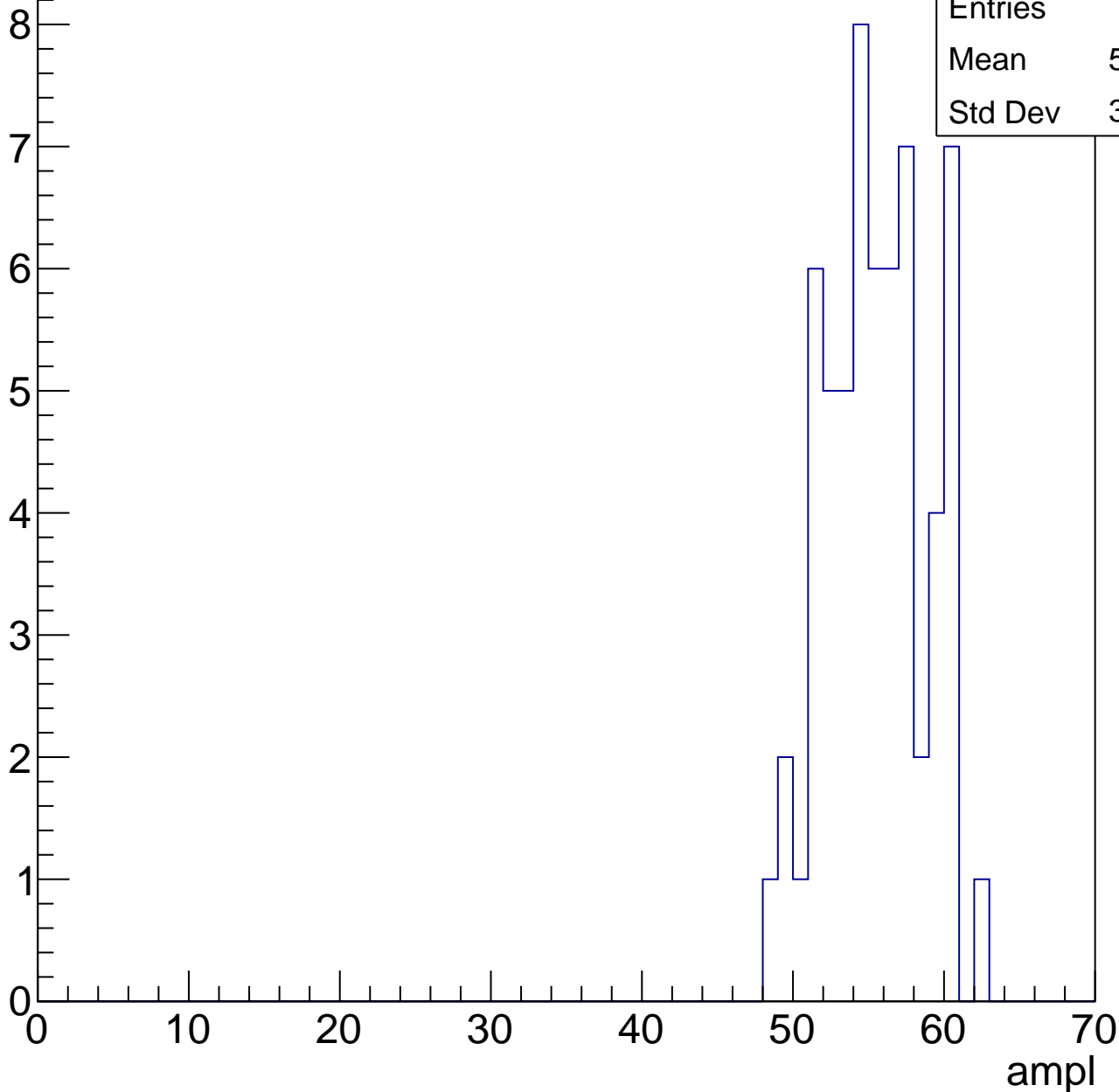
Entries	53
Mean	49.11
Std Dev	3.069

B1L102S, U9-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.05
Std Dev	3.276

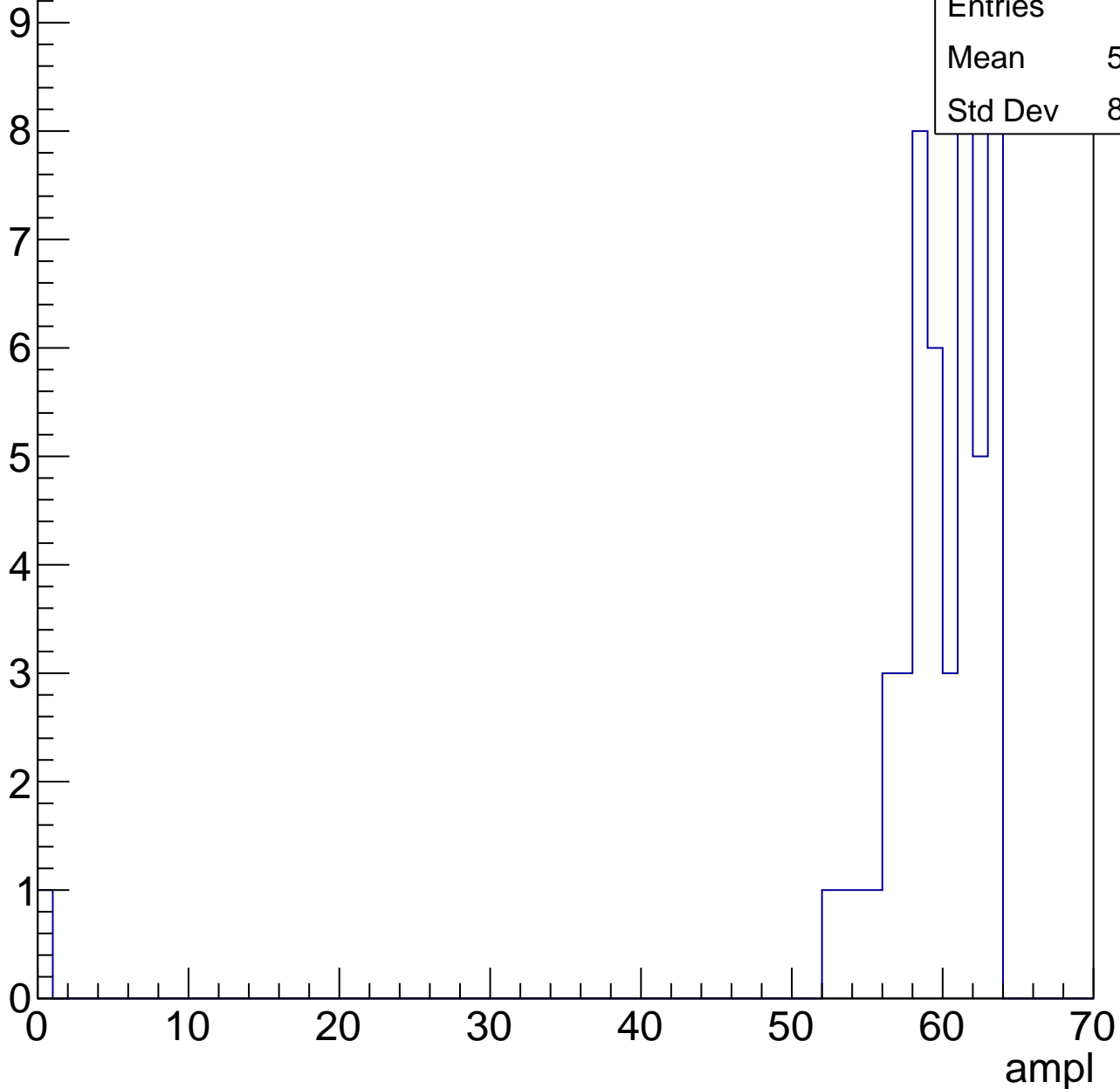


B1L102S, U9-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.28
Std Dev	8.764



B1L102S, U9-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch96, adc0

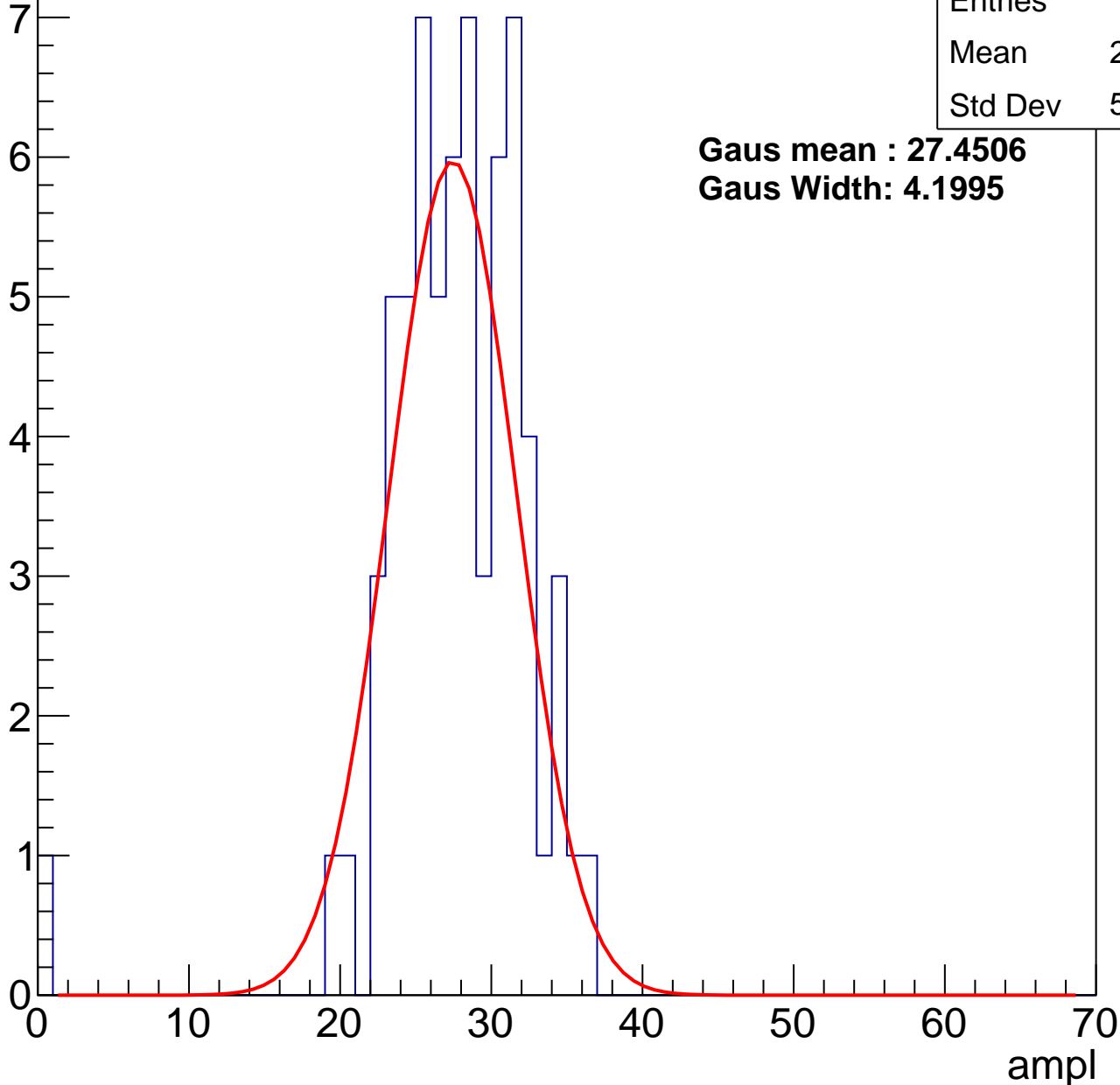
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.18
Std Dev	5.016

Gaus mean : 27.4506

Gaus Width: 4.1995



B1L102S, U9-ch96, adc1

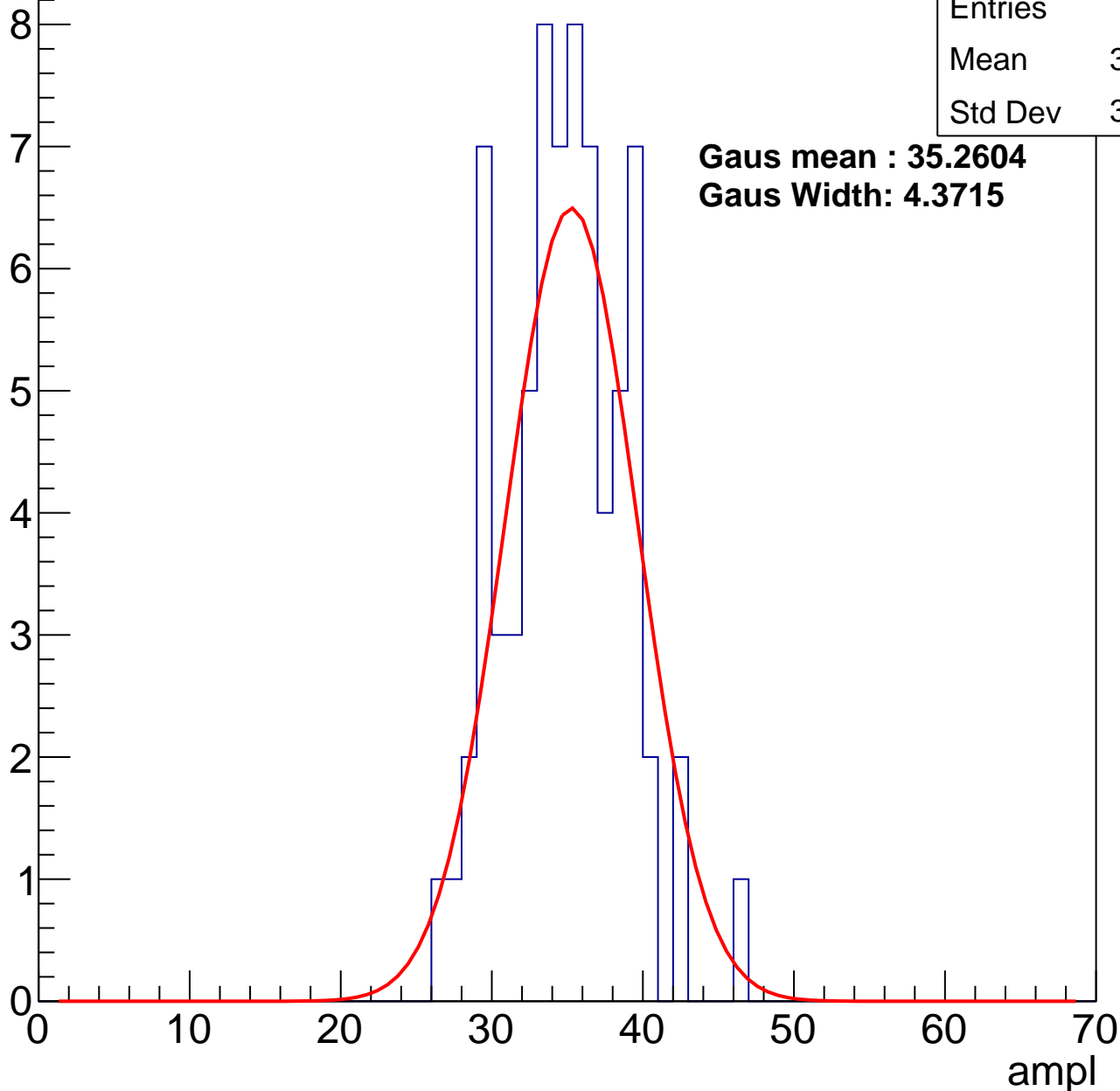
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.38
Std Dev	3.923

Gaus mean : 35.2604

Gaus Width: 4.3715



B1L102S, U9-ch96, adc2

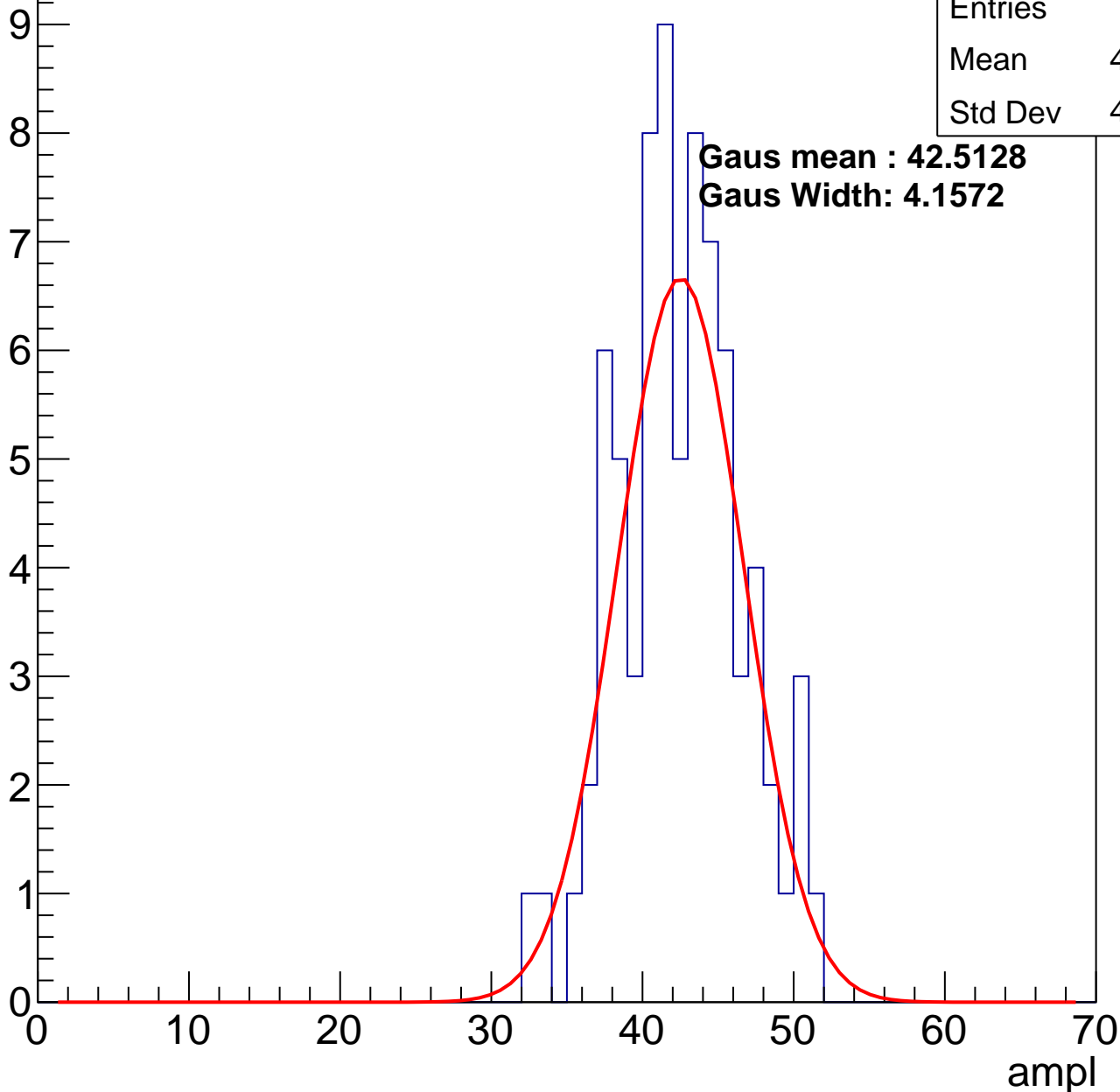
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.03
Std Dev	4.026

Gaus mean : 42.5128

Gaus Width: 4.1572

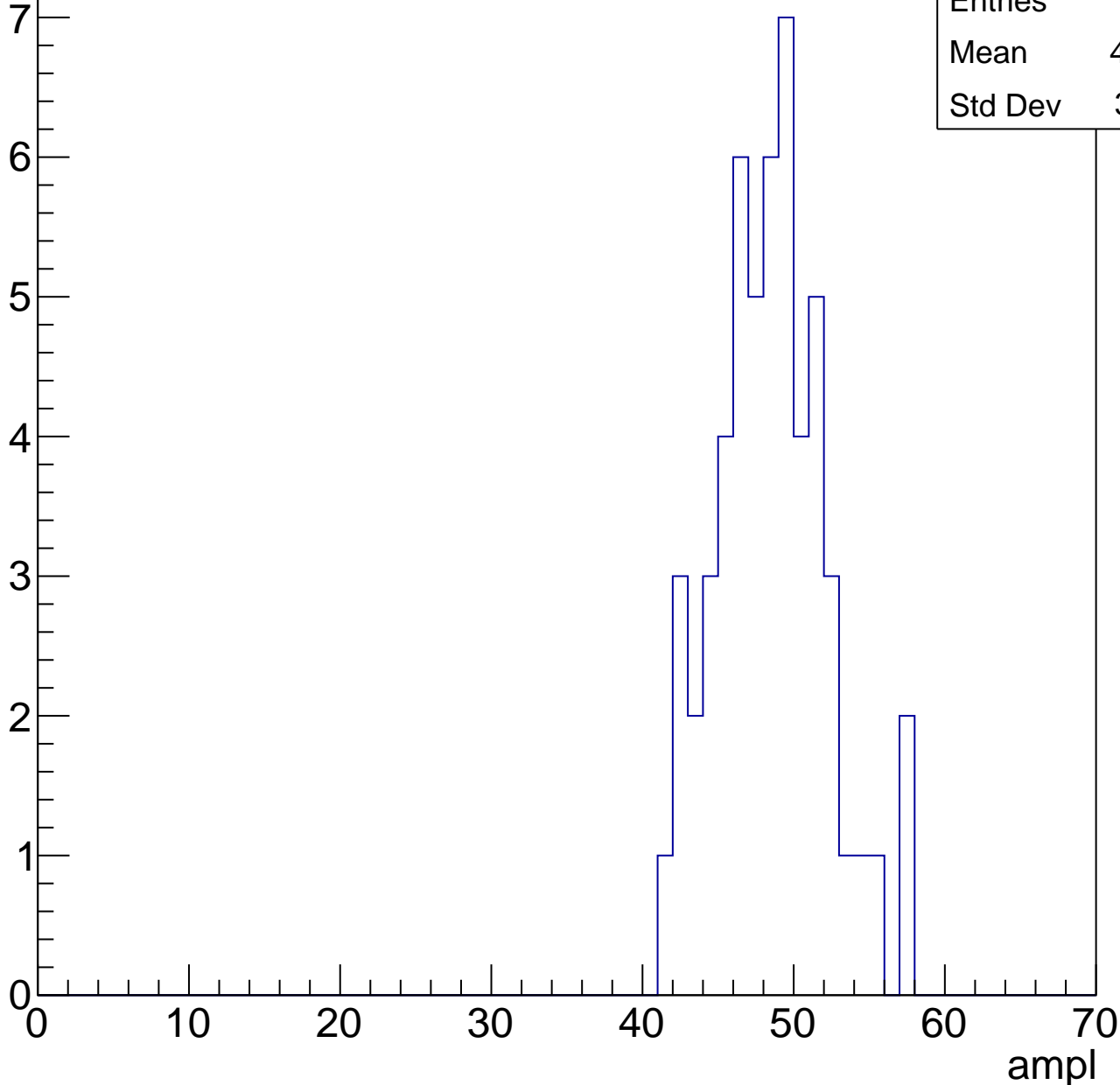


B1L102S, U9-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	48.04
Std Dev	3.621

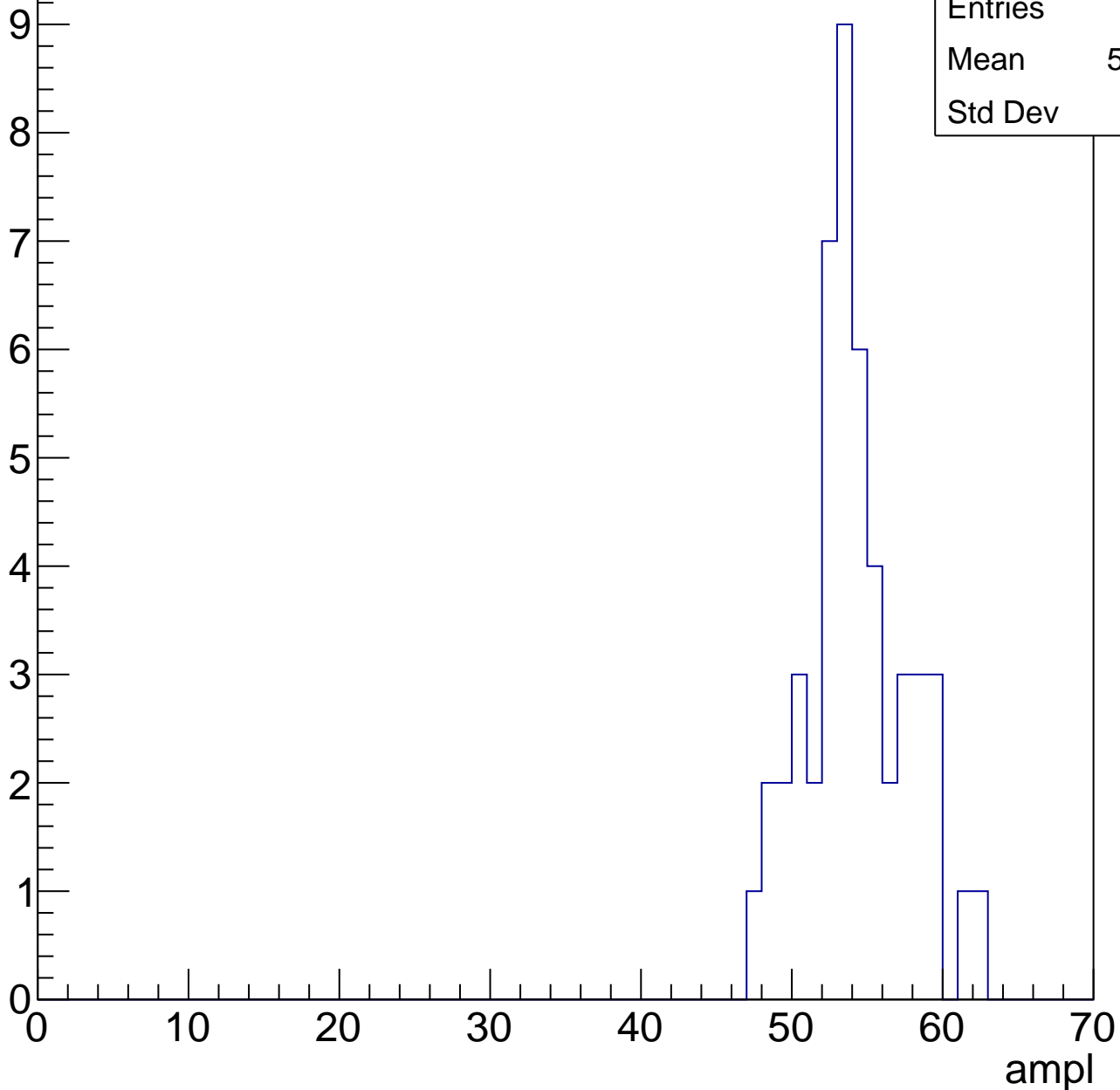


B1L102S, U9-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

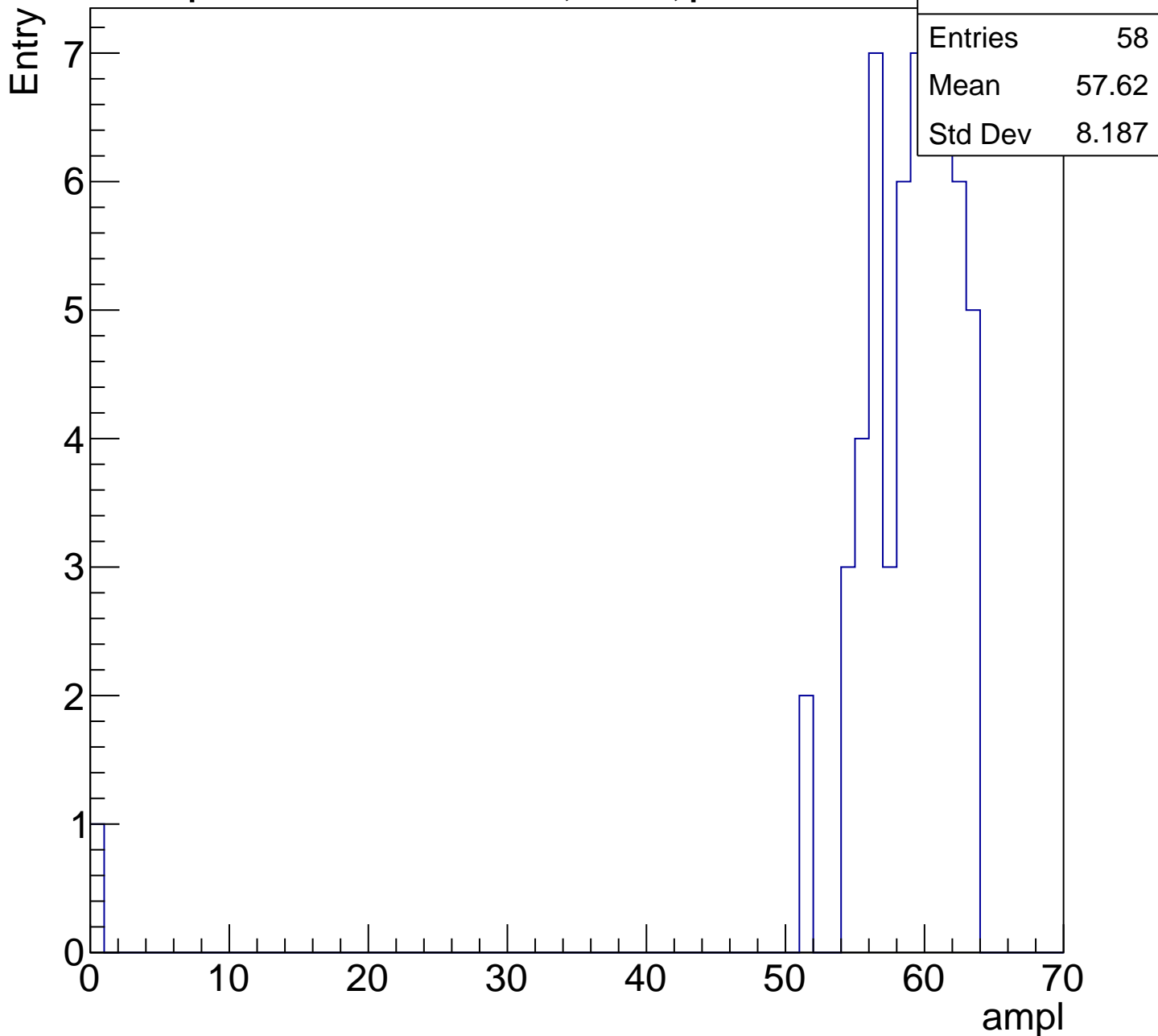
Entry

Entries	49
Mean	53.78
Std Dev	3.34



B1L102S, U9-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

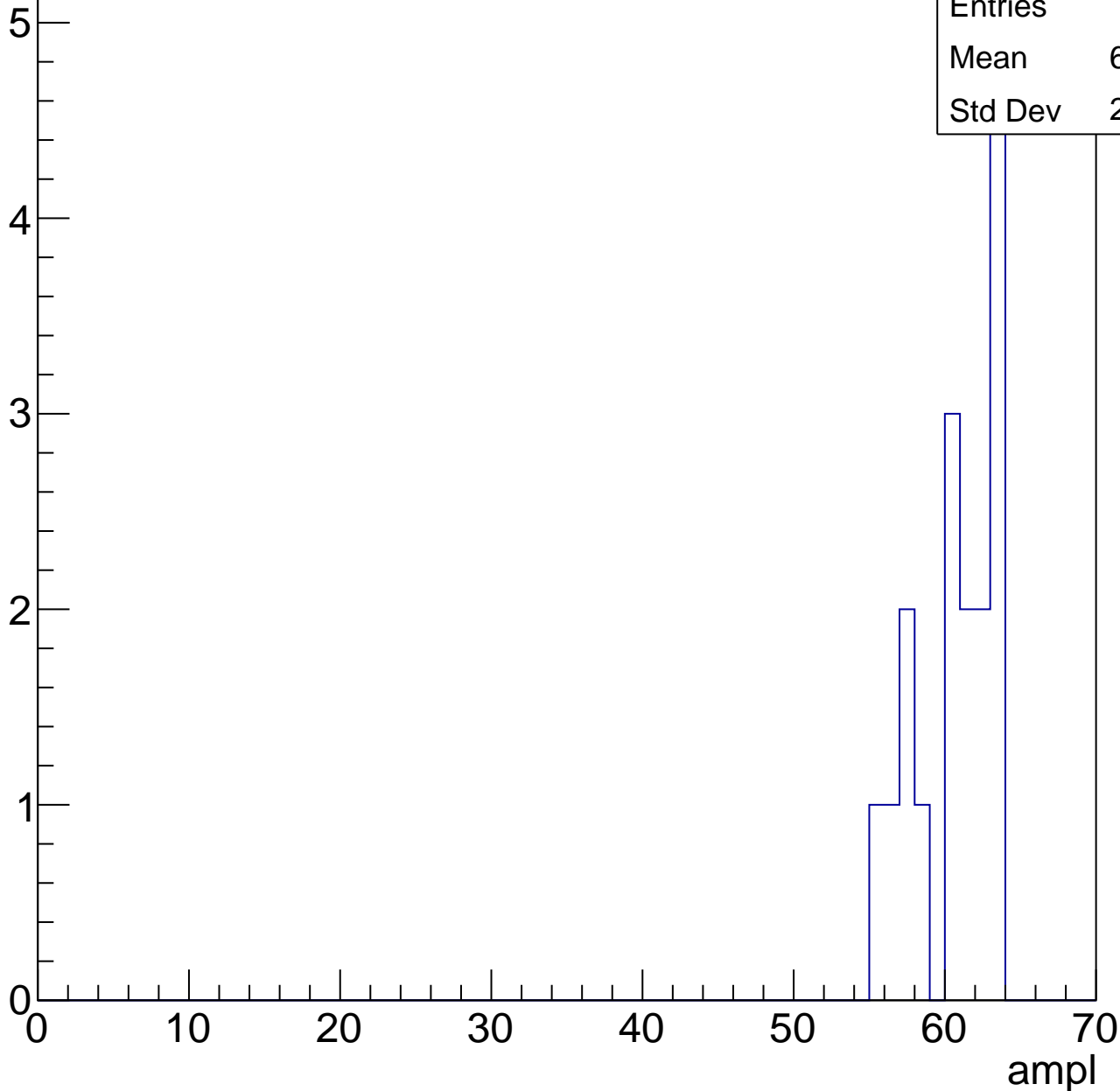


B1L102S, U9-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	60.24
Std Dev	2.624



B1L102S, U9-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

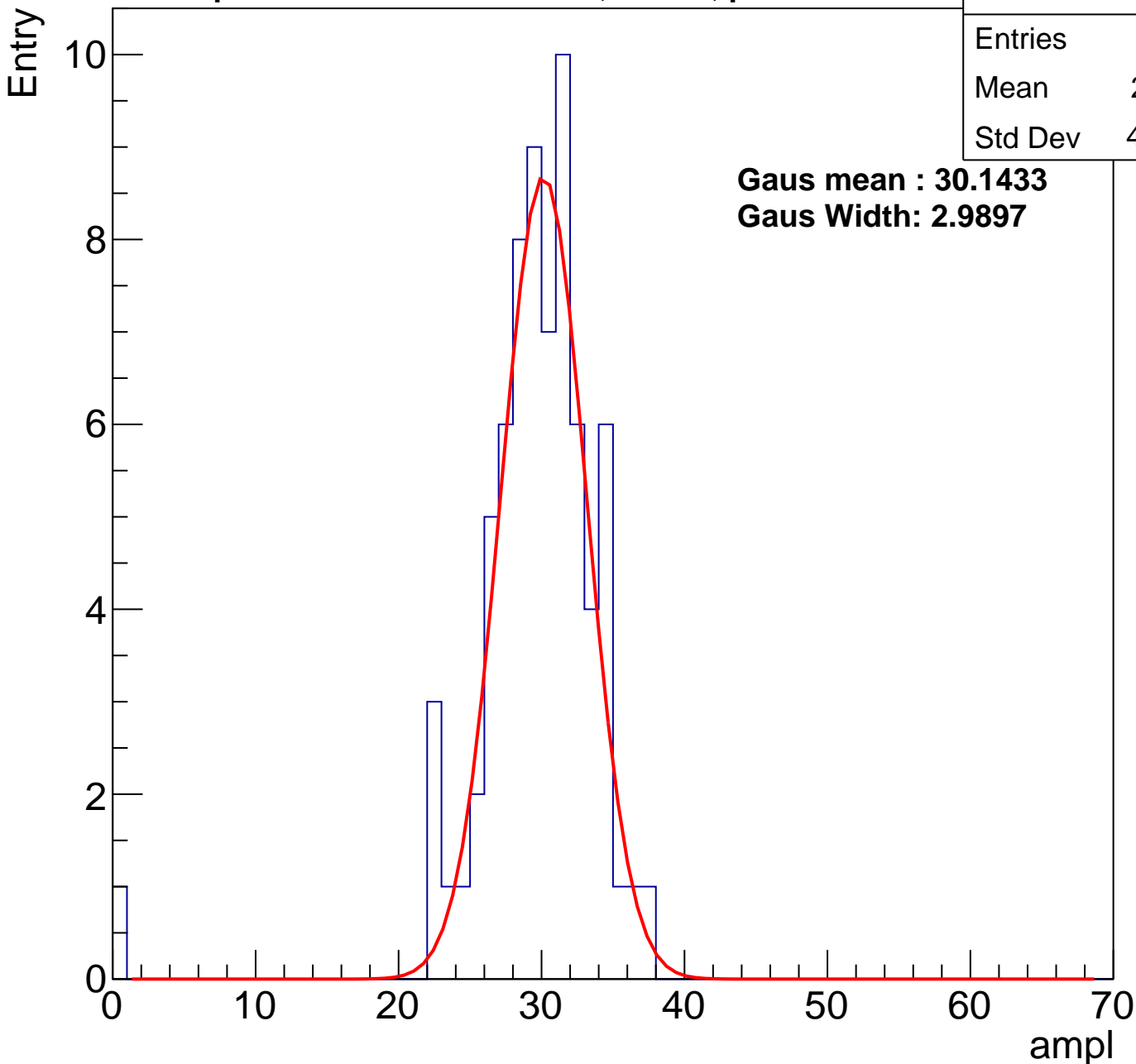


B1L102S, U9-ch97, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	29.11
Std Dev	4.745

Gaus mean : 30.1433
Gaus Width: 2.9897



B1L102S, U9-ch97, adc1

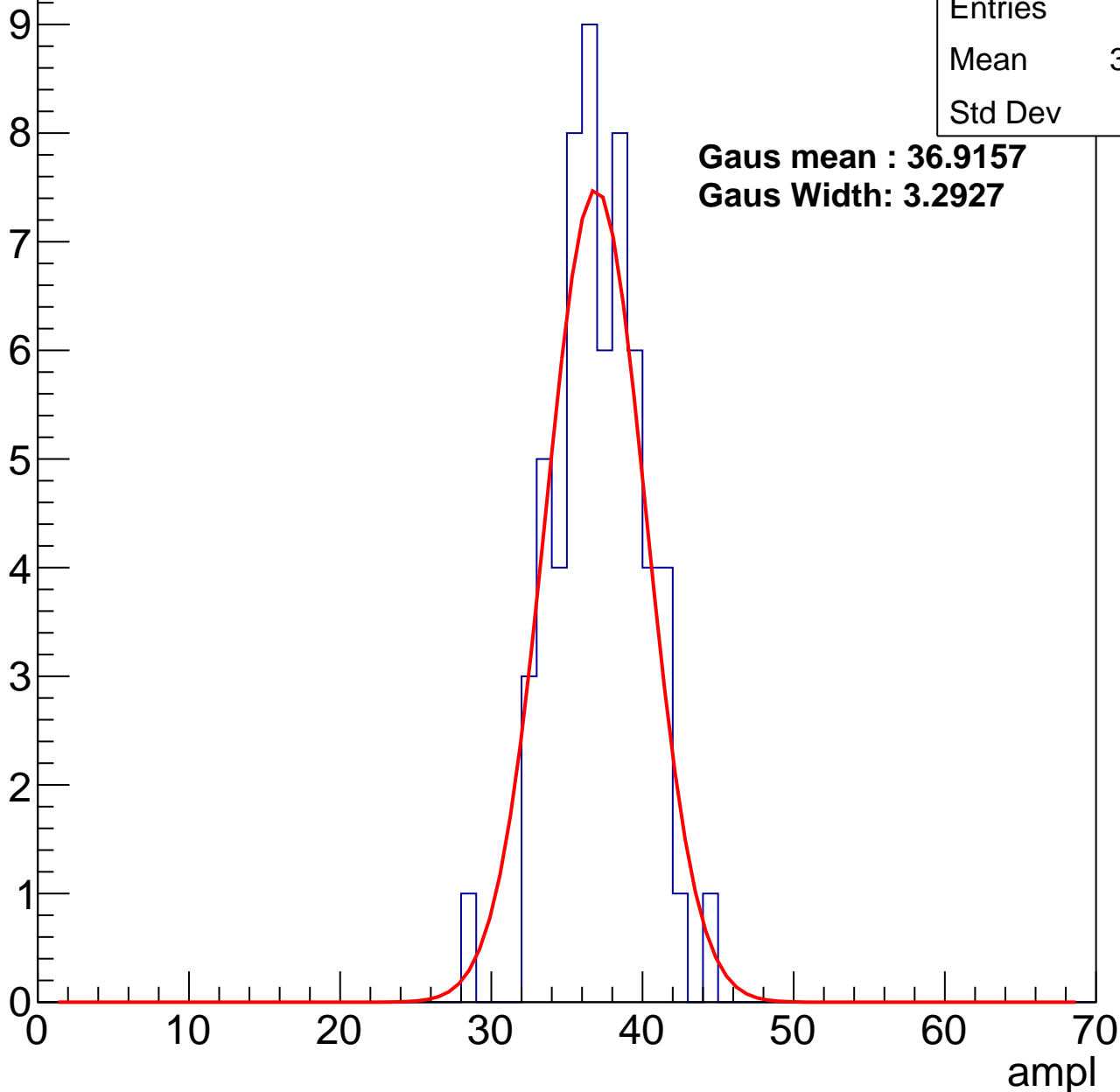
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	36.65
Std Dev	2.92

Gaus mean : 36.9157

Gaus Width: 3.2927



B1L102S, U9-ch97, adc2

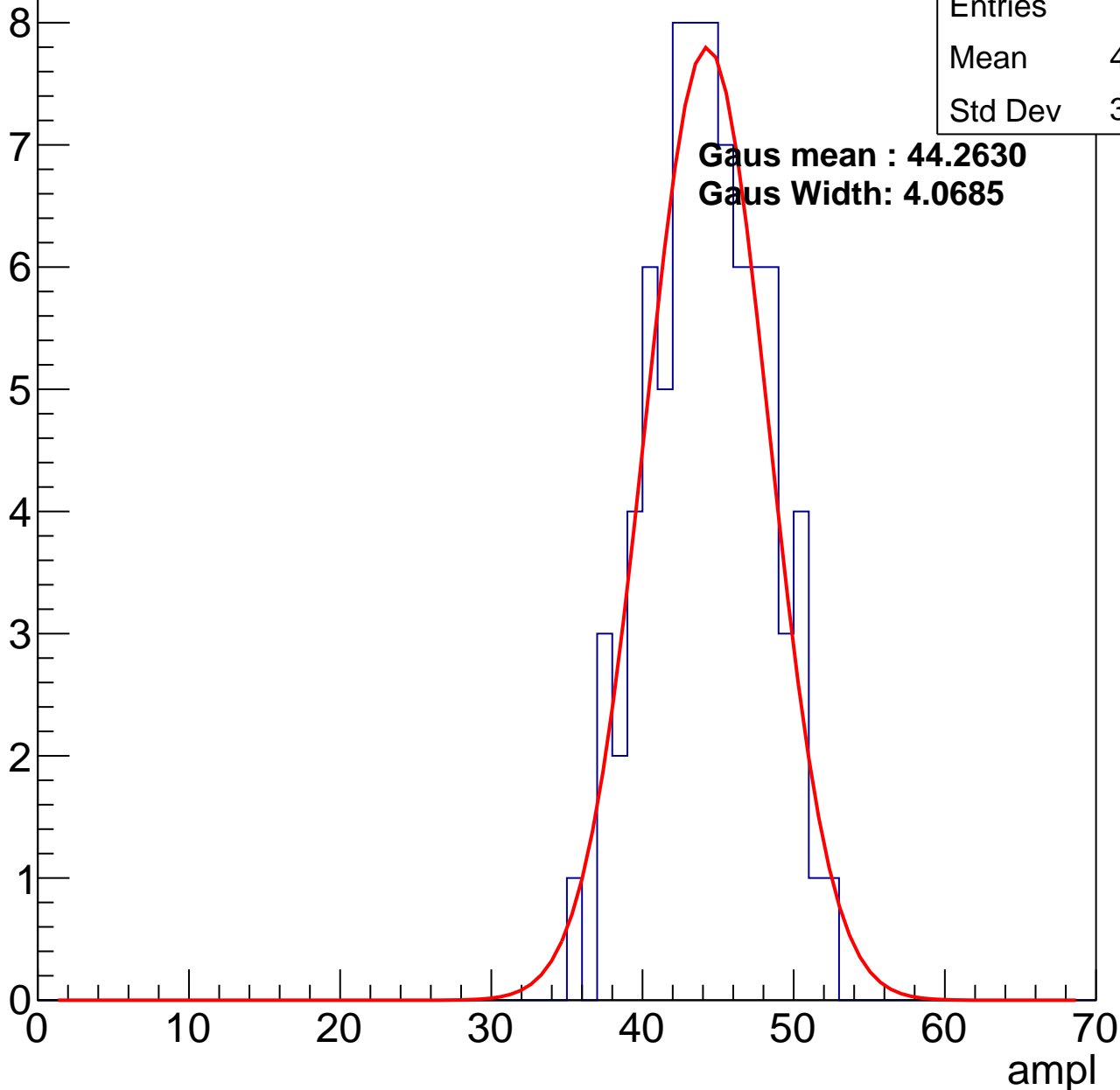
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	43.87
Std Dev	3.719

Gaus mean : 44.2630

Gaus Width: 4.0685

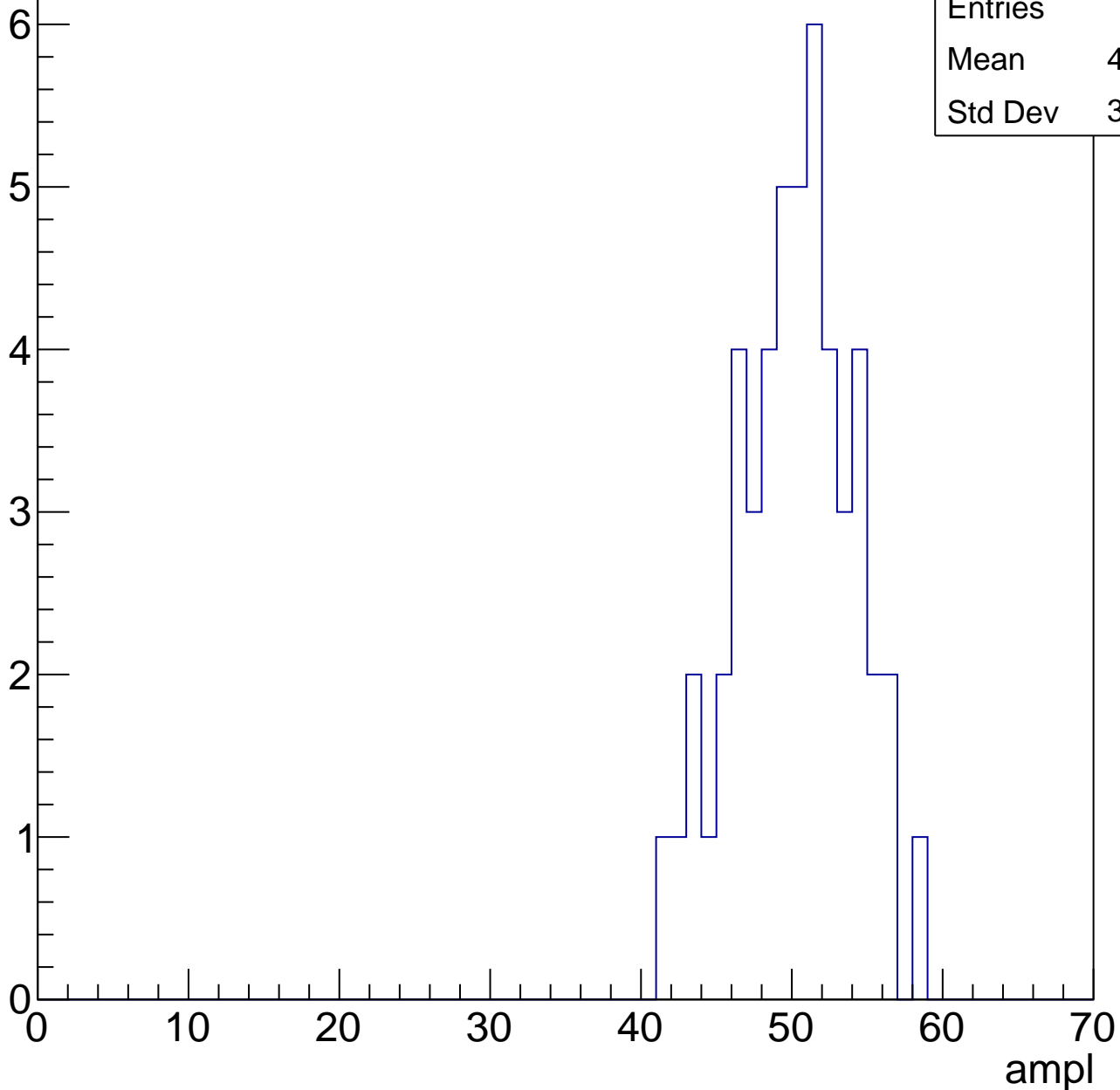


B1L102S, U9-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

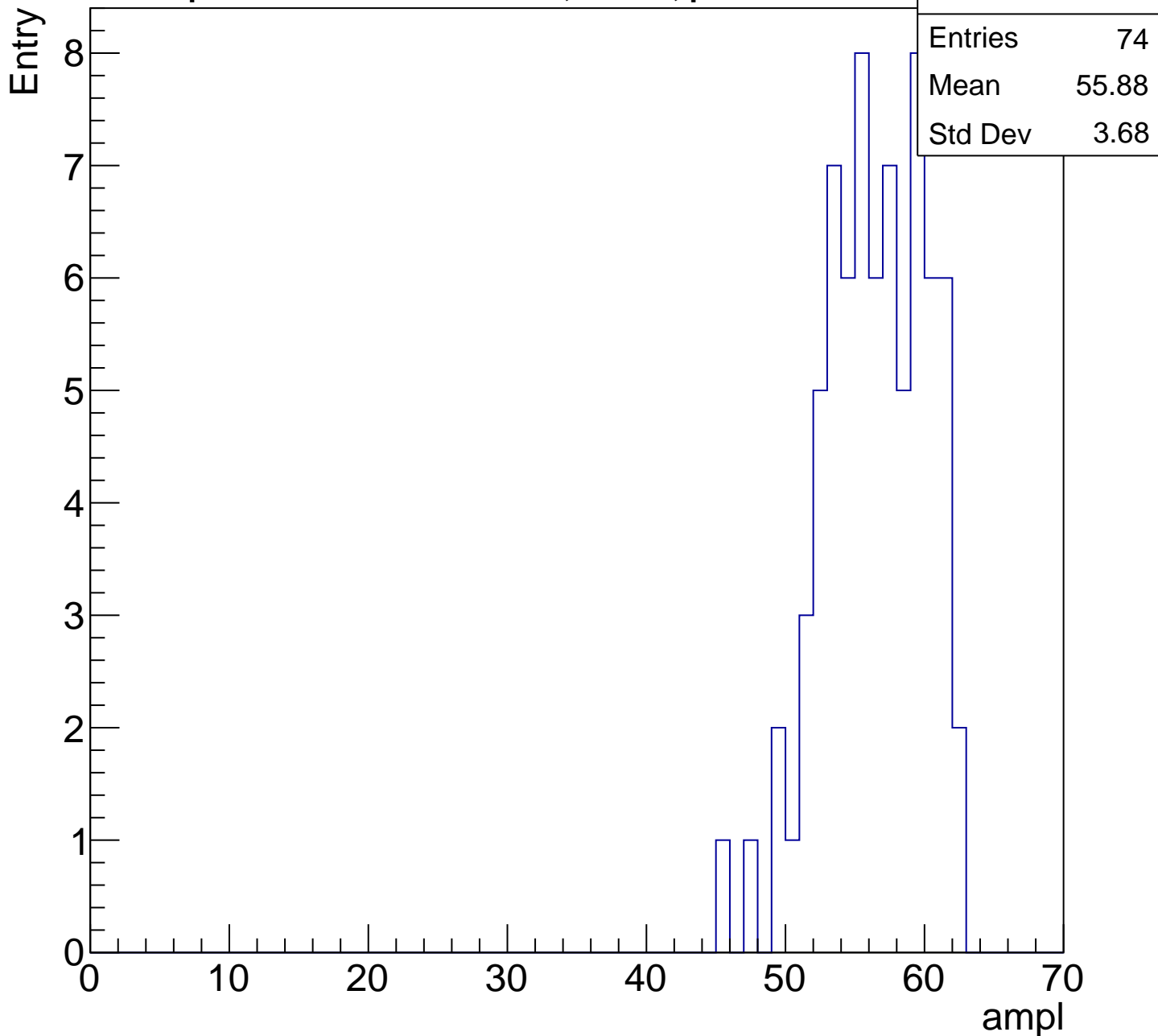
Entry

Entries	50
Mean	49.68
Std Dev	3.834



B1L102S, U9-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

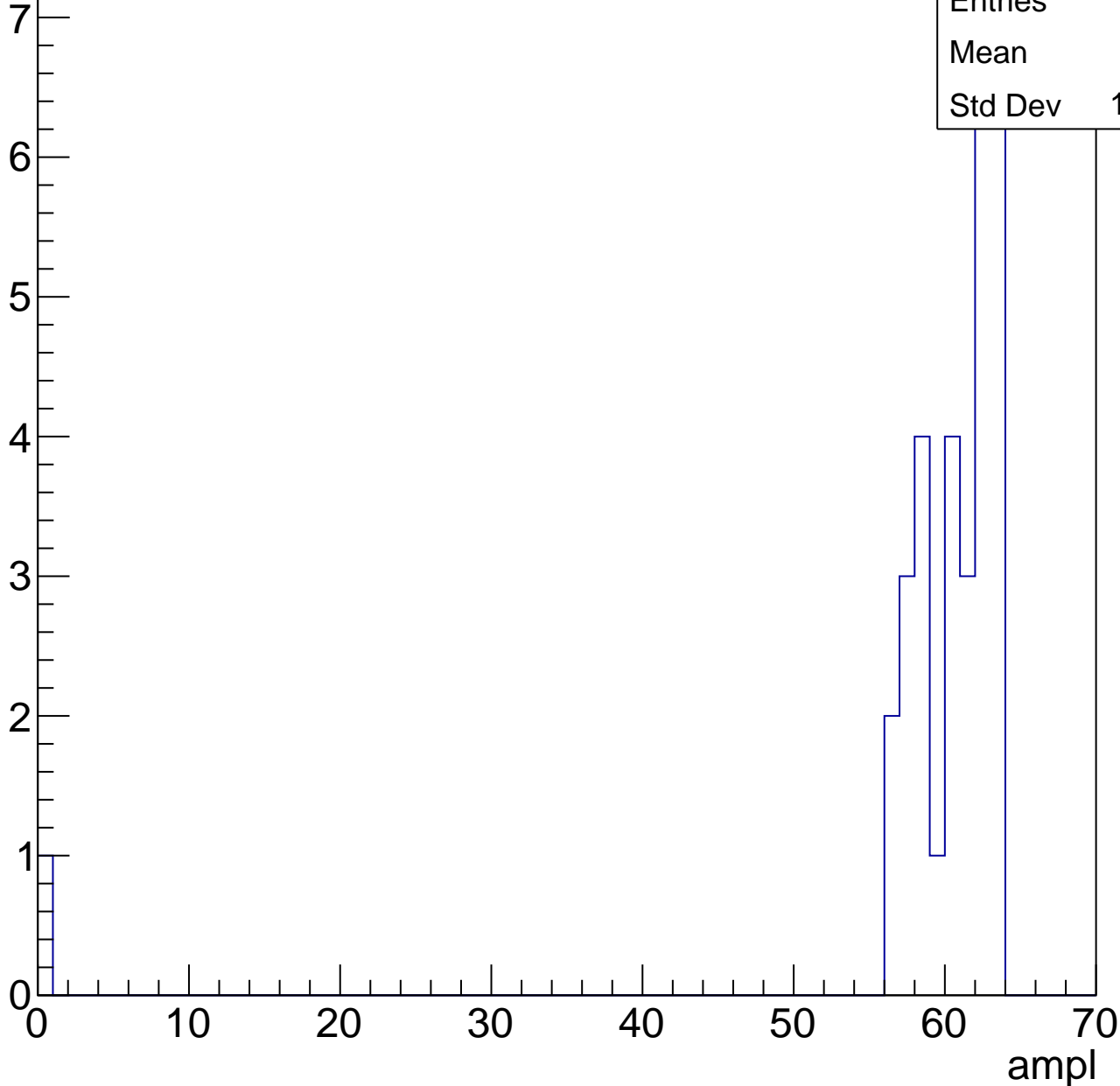


B1L102S, U9-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

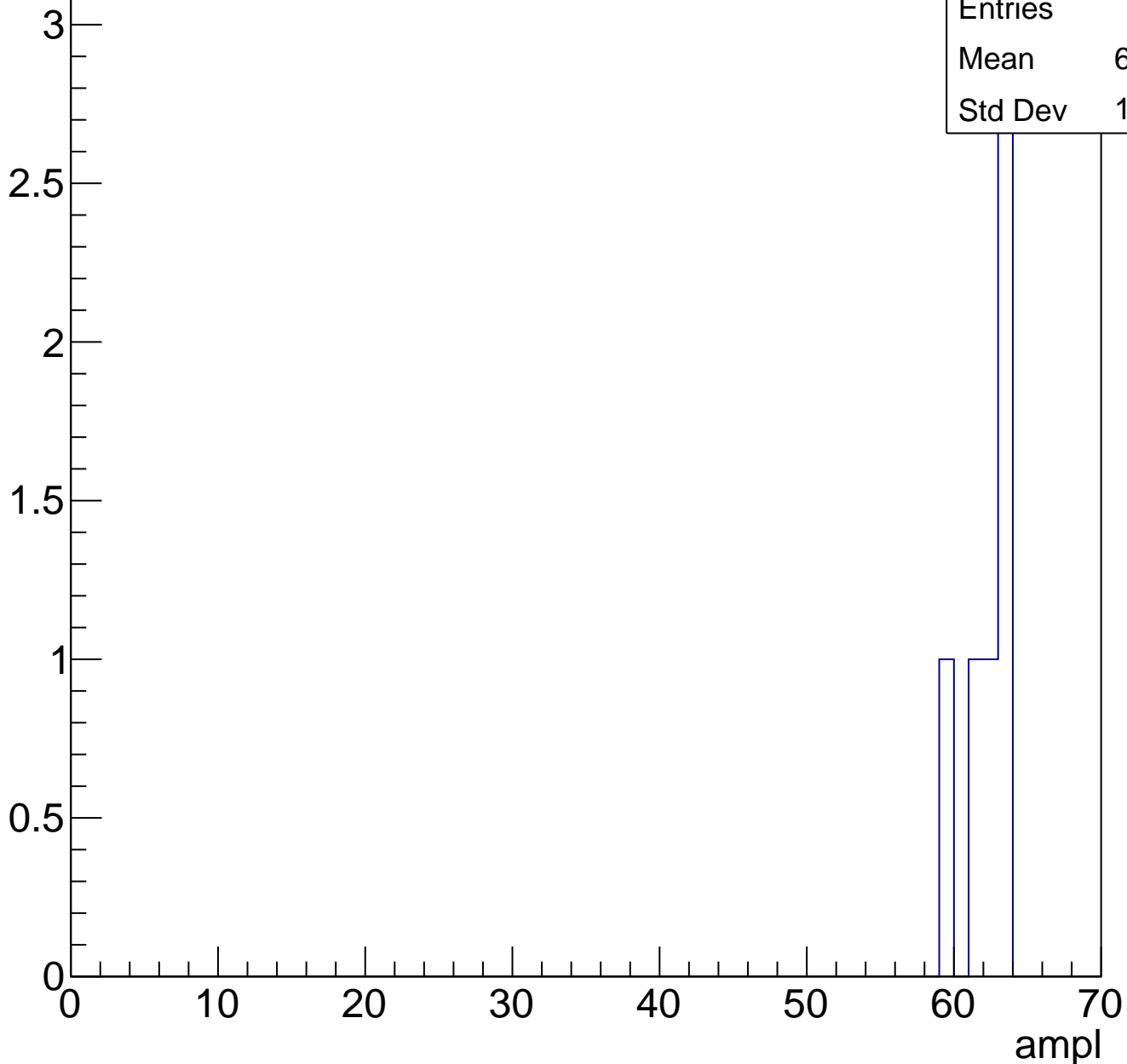
Entries	32
Mean	58.5
Std Dev	10.75



B1L102S, U9-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch98, adc0

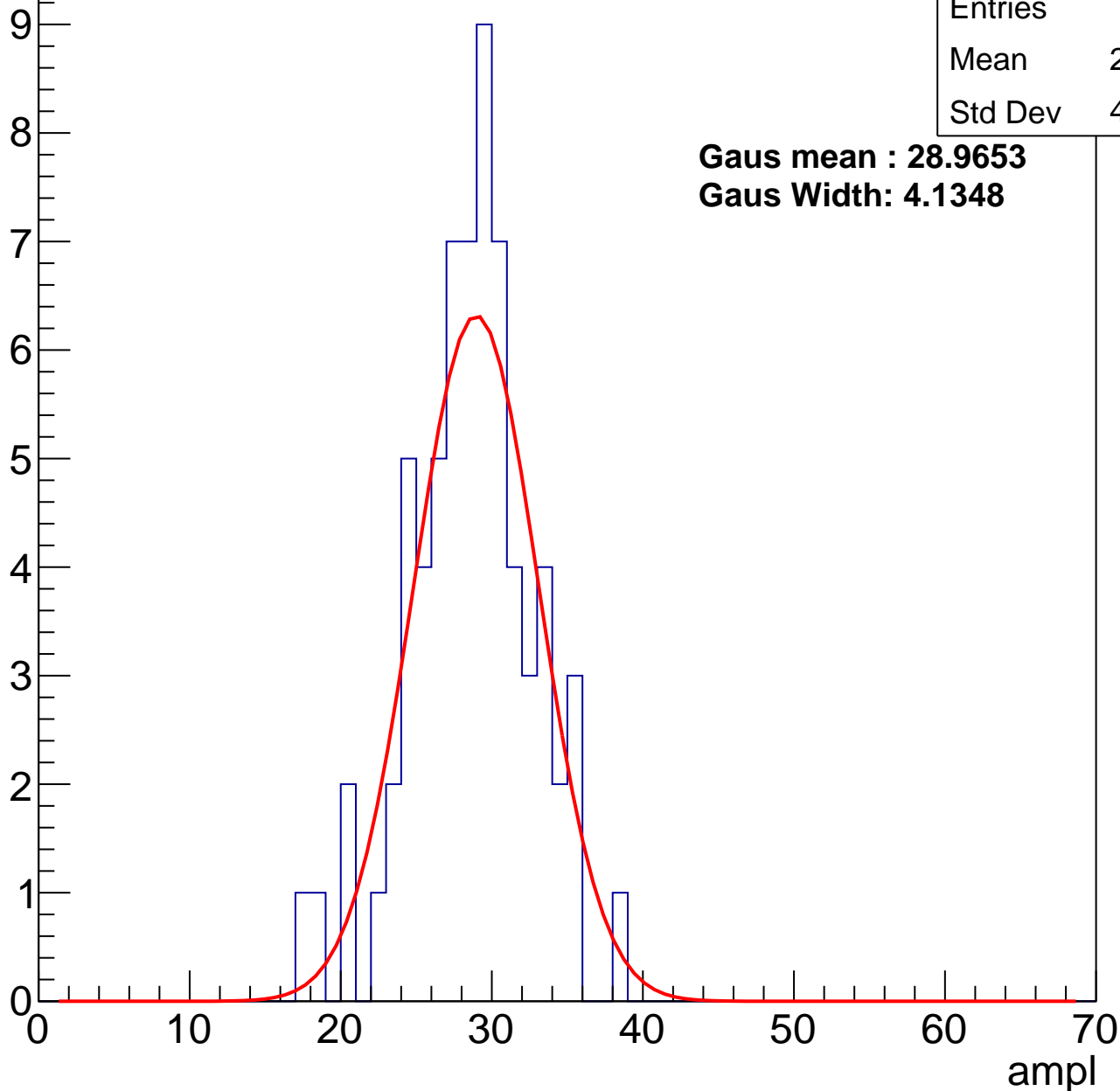
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.12
Std Dev	4.053

Gaus mean : 28.9653

Gaus Width: 4.1348



B1L102S, U9-ch98, adc1

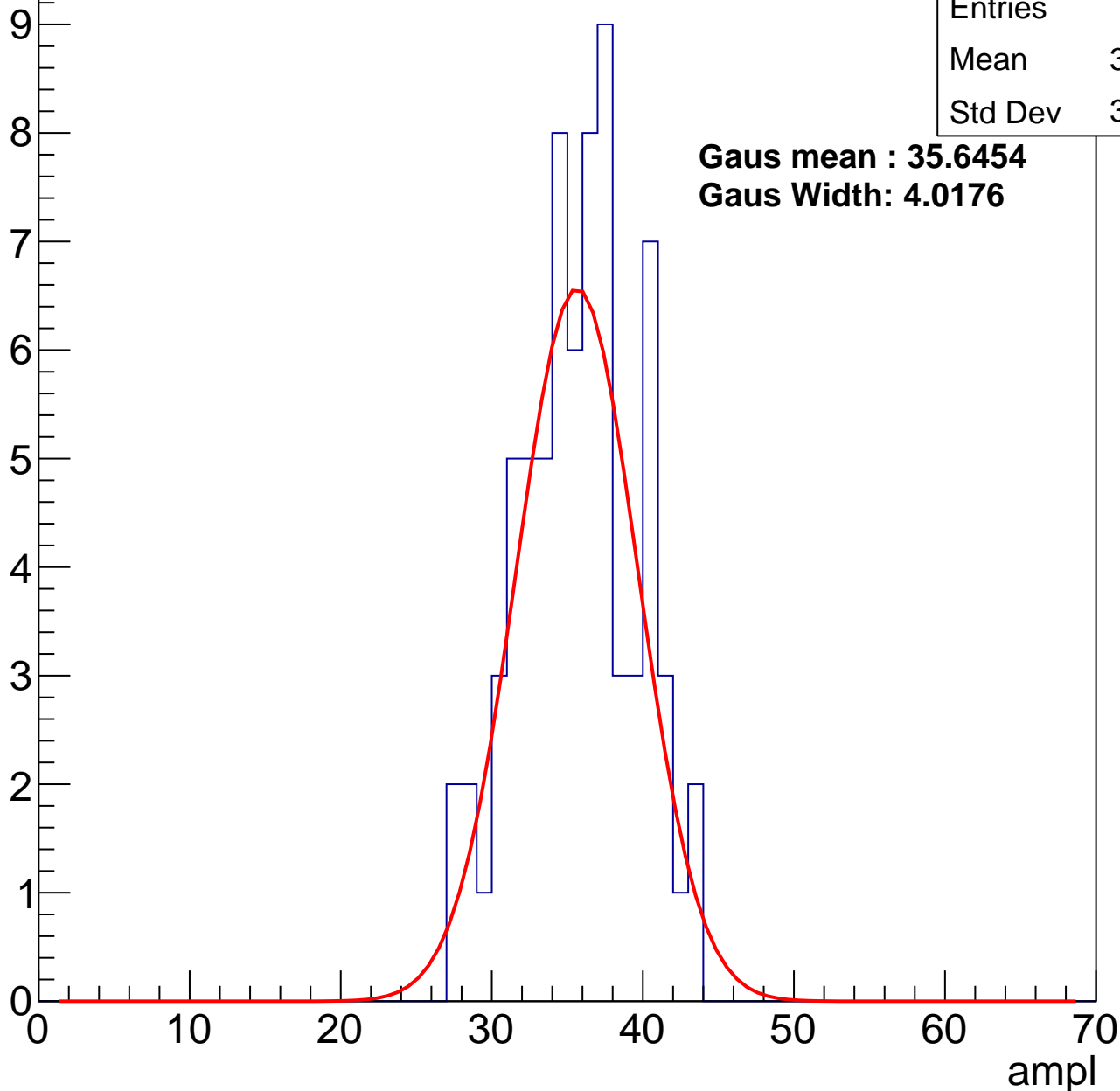
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.26
Std Dev	3.818

Gaus mean : 35.6454

Gaus Width: 4.0176



B1L102S, U9-ch98, adc2

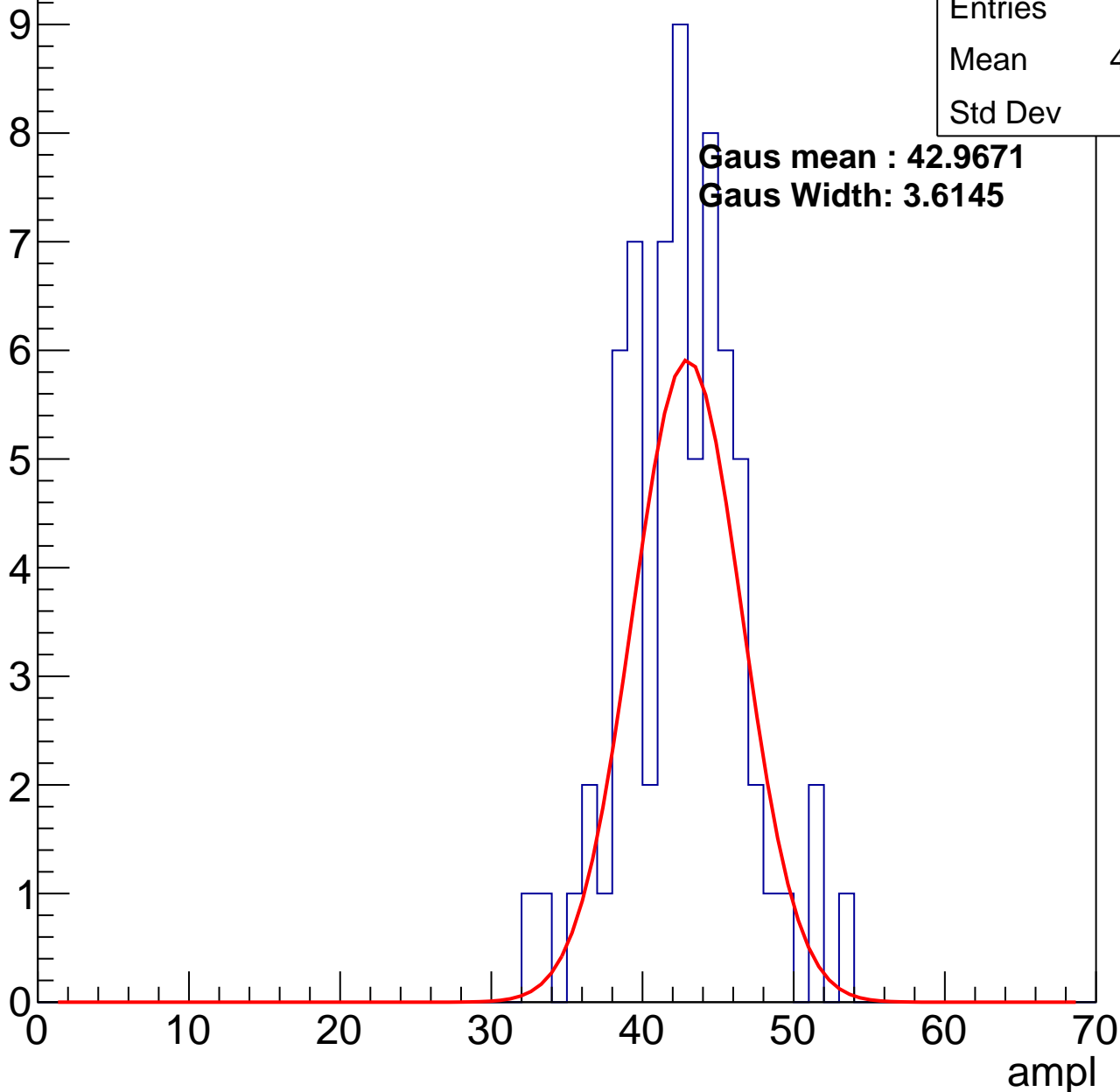
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.18
Std Dev	4

Gaus mean : 42.9671

Gaus Width: 3.6145

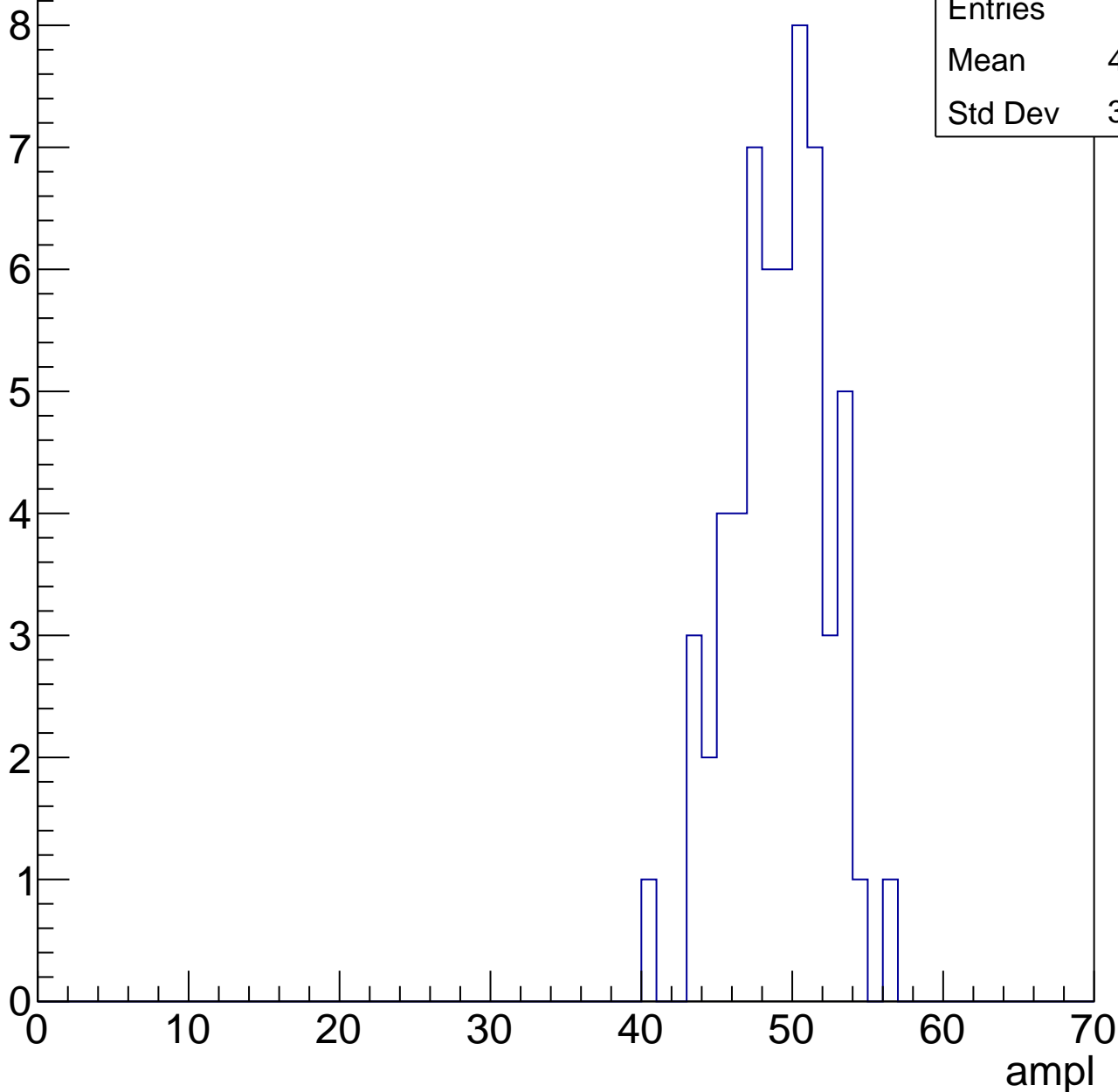


B1L102S, U9-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

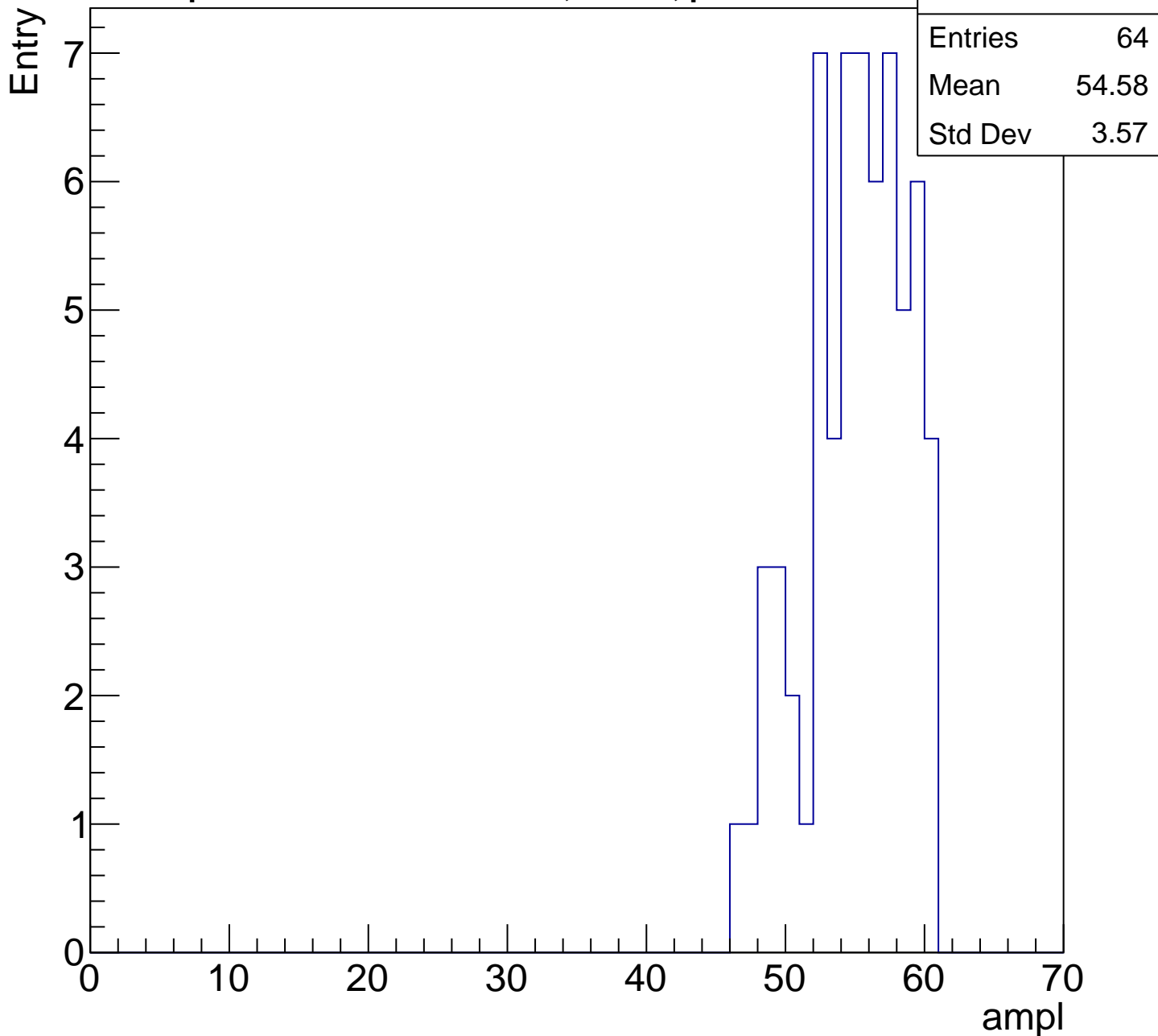
Entry

Entries	58
Mean	48.62
Std Dev	3.167



B1L102S, U9-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U9-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 42

Mean 59.33

Std Dev 2.714

ampl

0

10

20

30

40

50

60

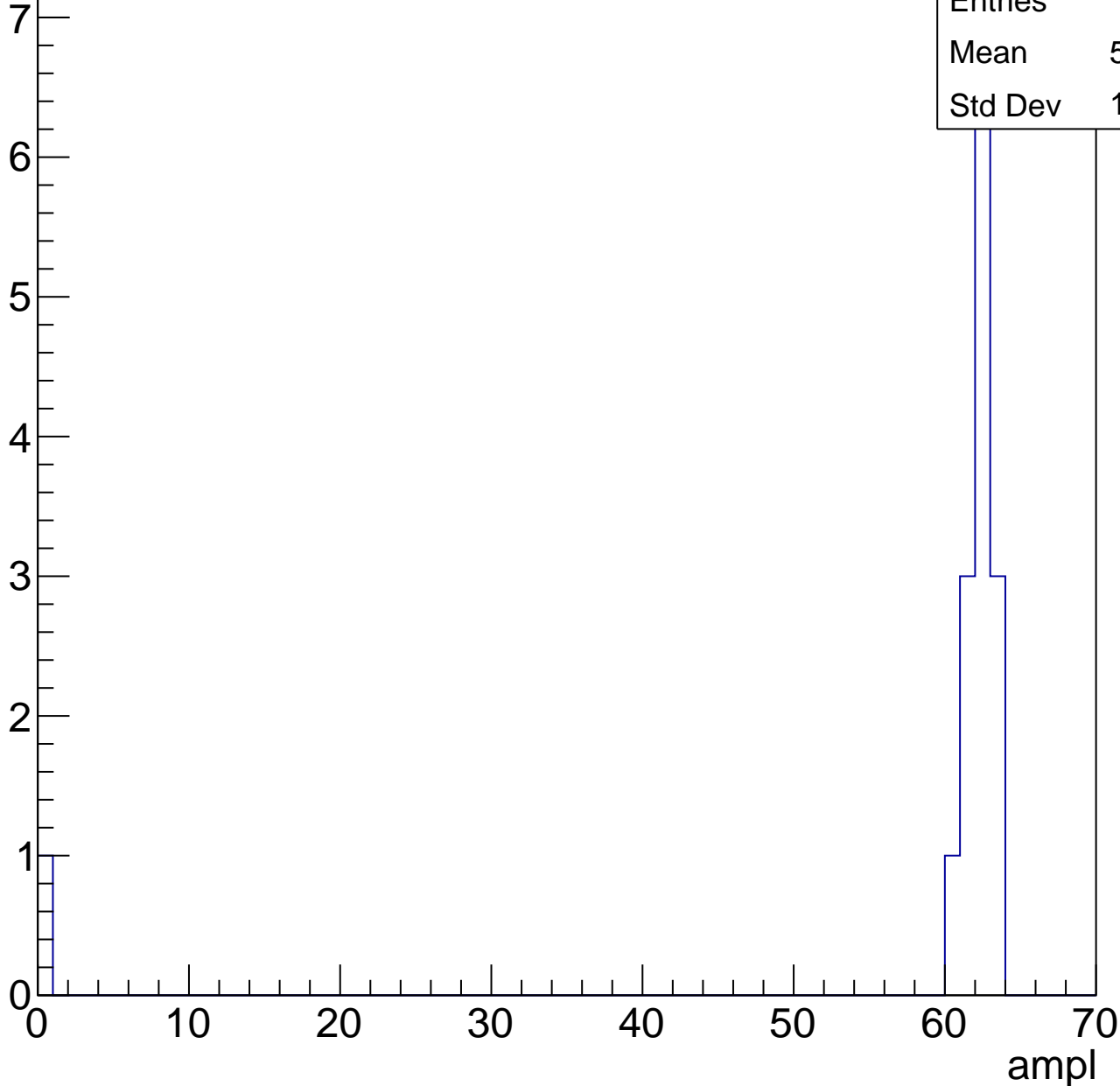
70

B1L102S, U9-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.73
Std Dev	15.45



B1L102S, U9-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch99, adc0

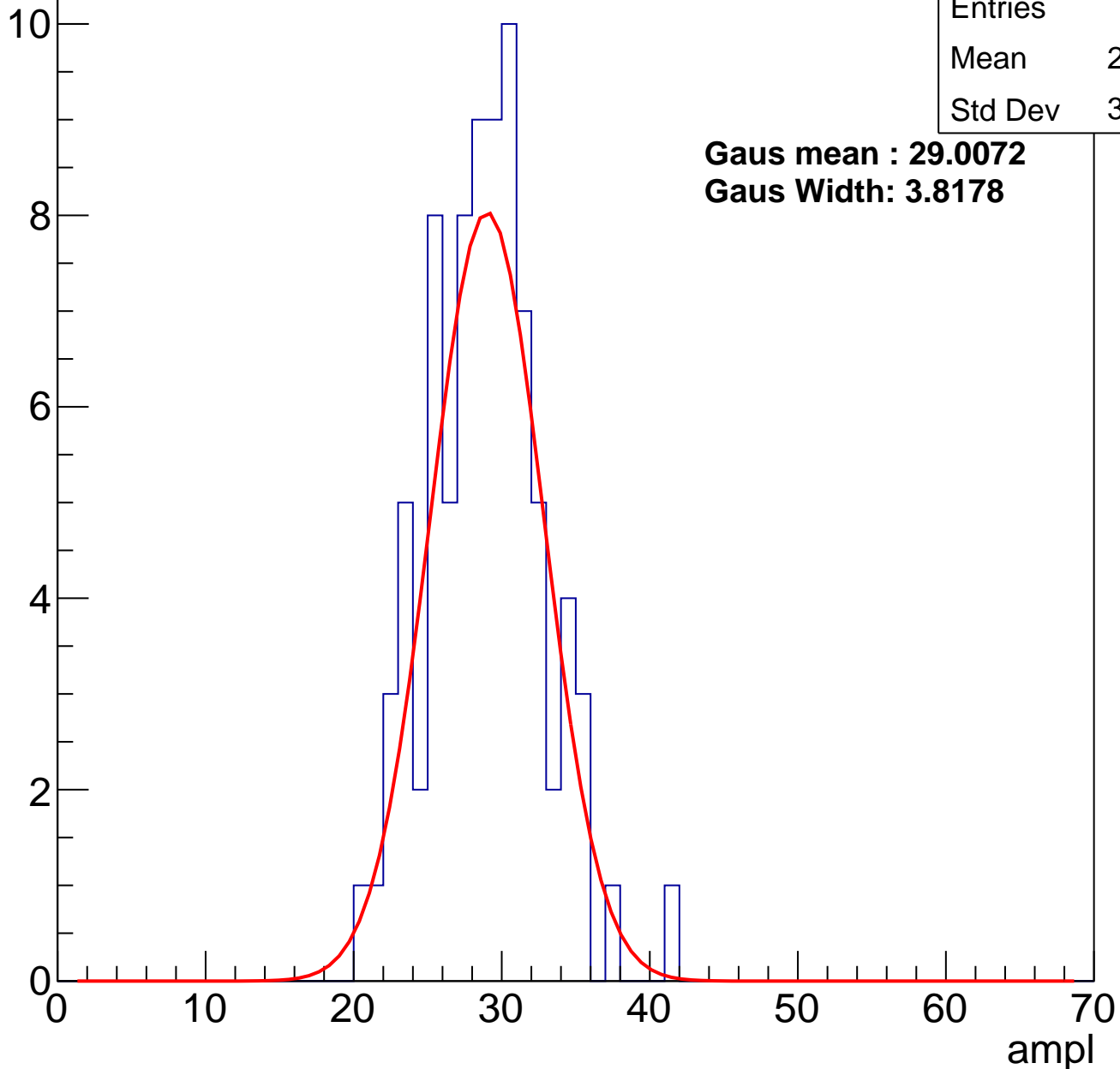
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	28.46
Std Dev	3.847

Gaus mean : 29.0072

Gaus Width: 3.8178

Entry



B1L102S, U9-ch99, adc1

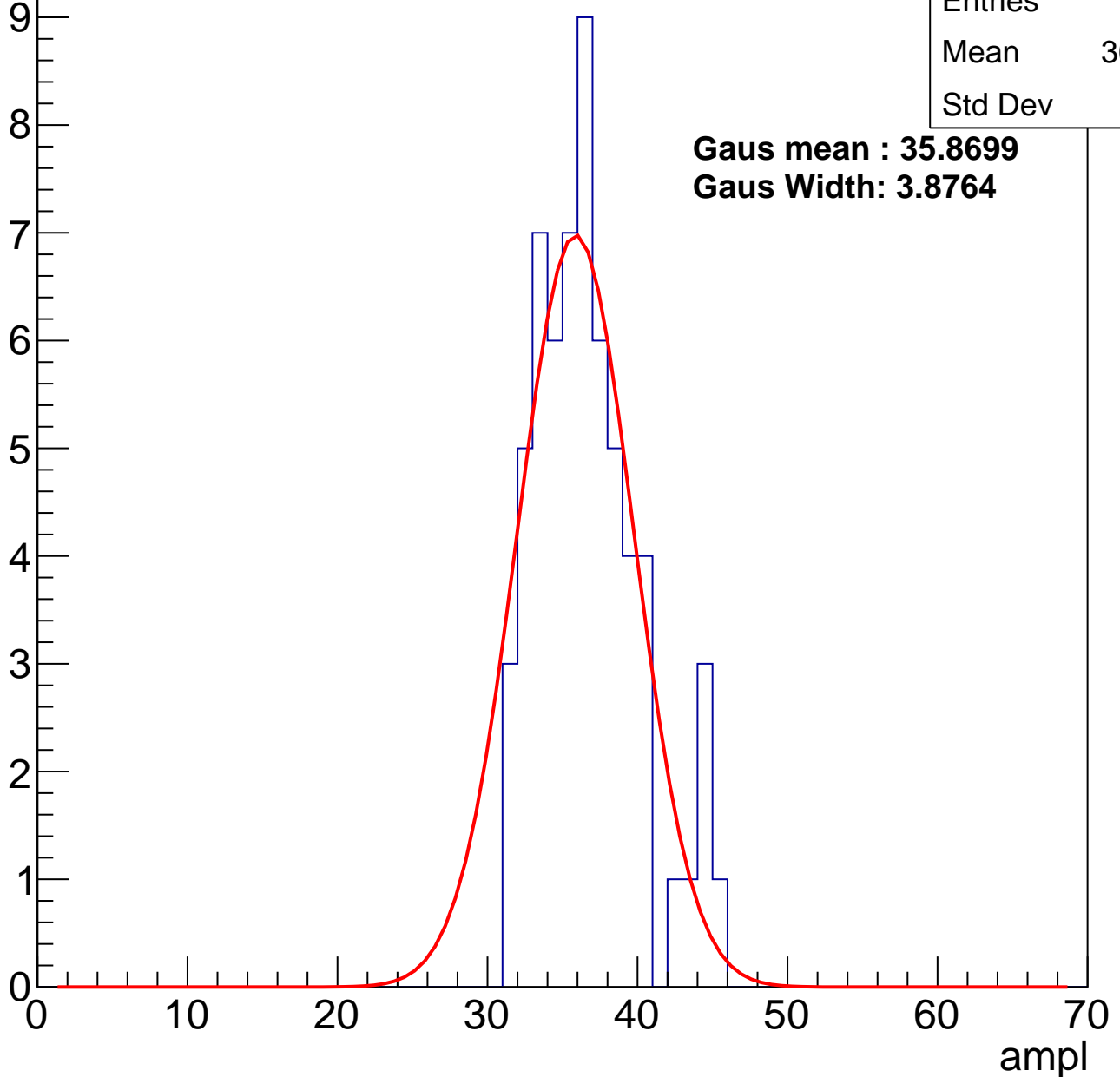
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.24
Std Dev	3.43

Gaus mean : 35.8699

Gaus Width: 3.8764



B1L102S, U9-ch99, adc2

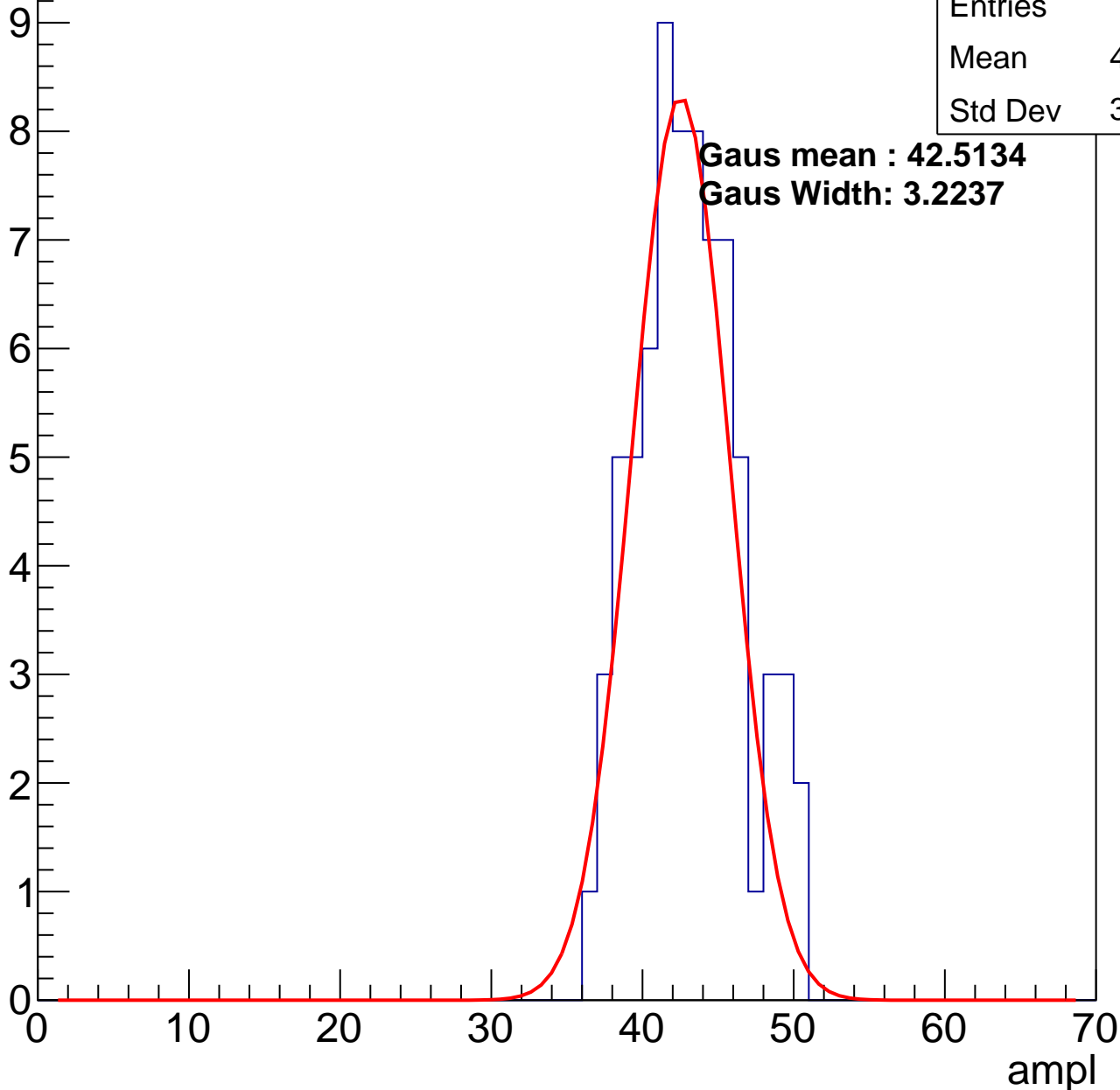
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.63
Std Dev	3.366

Gaus mean : 42.5134

Gaus Width: 3.2237

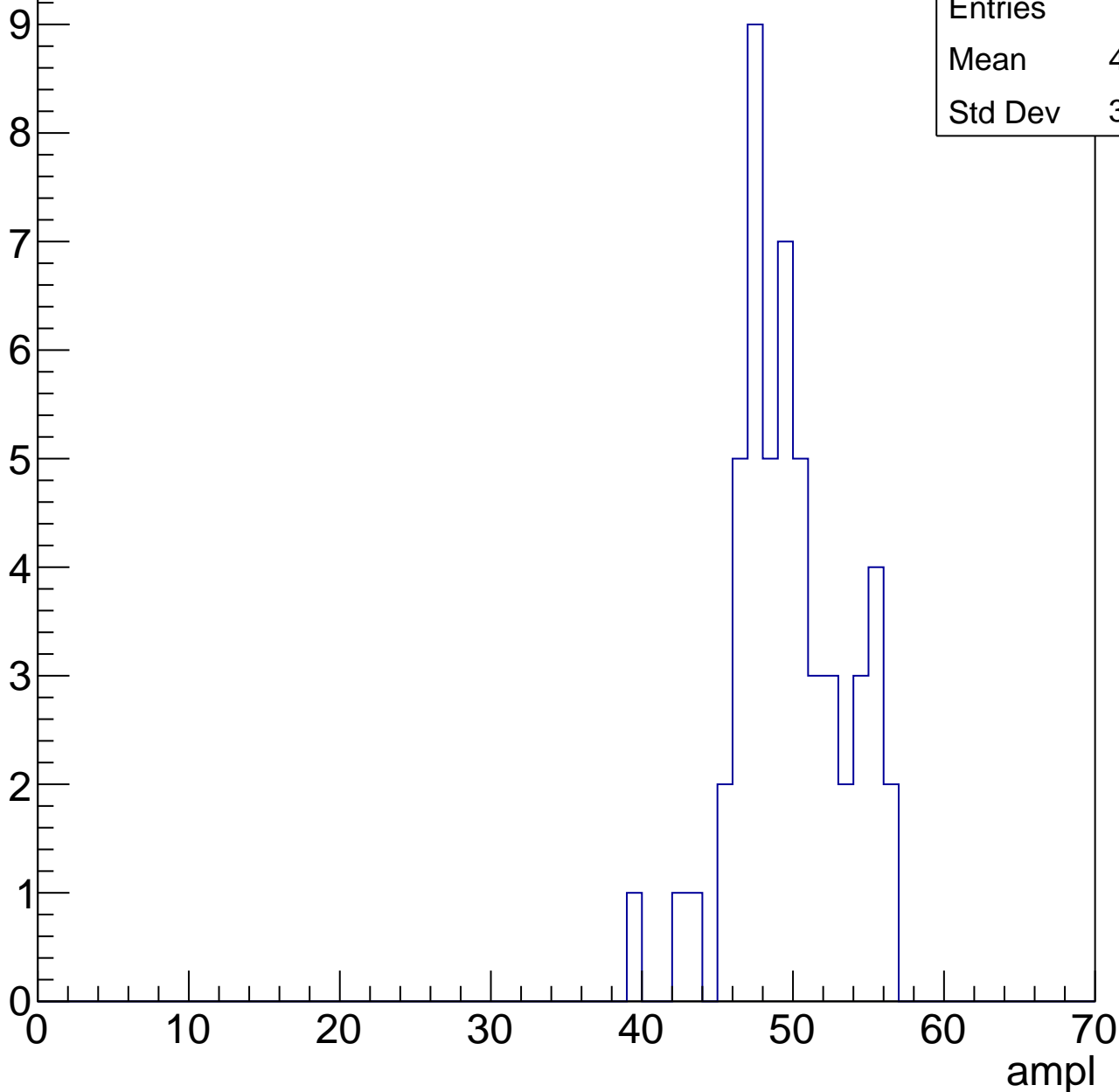


B1L102S, U9-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	49.23
Std Dev	3.617

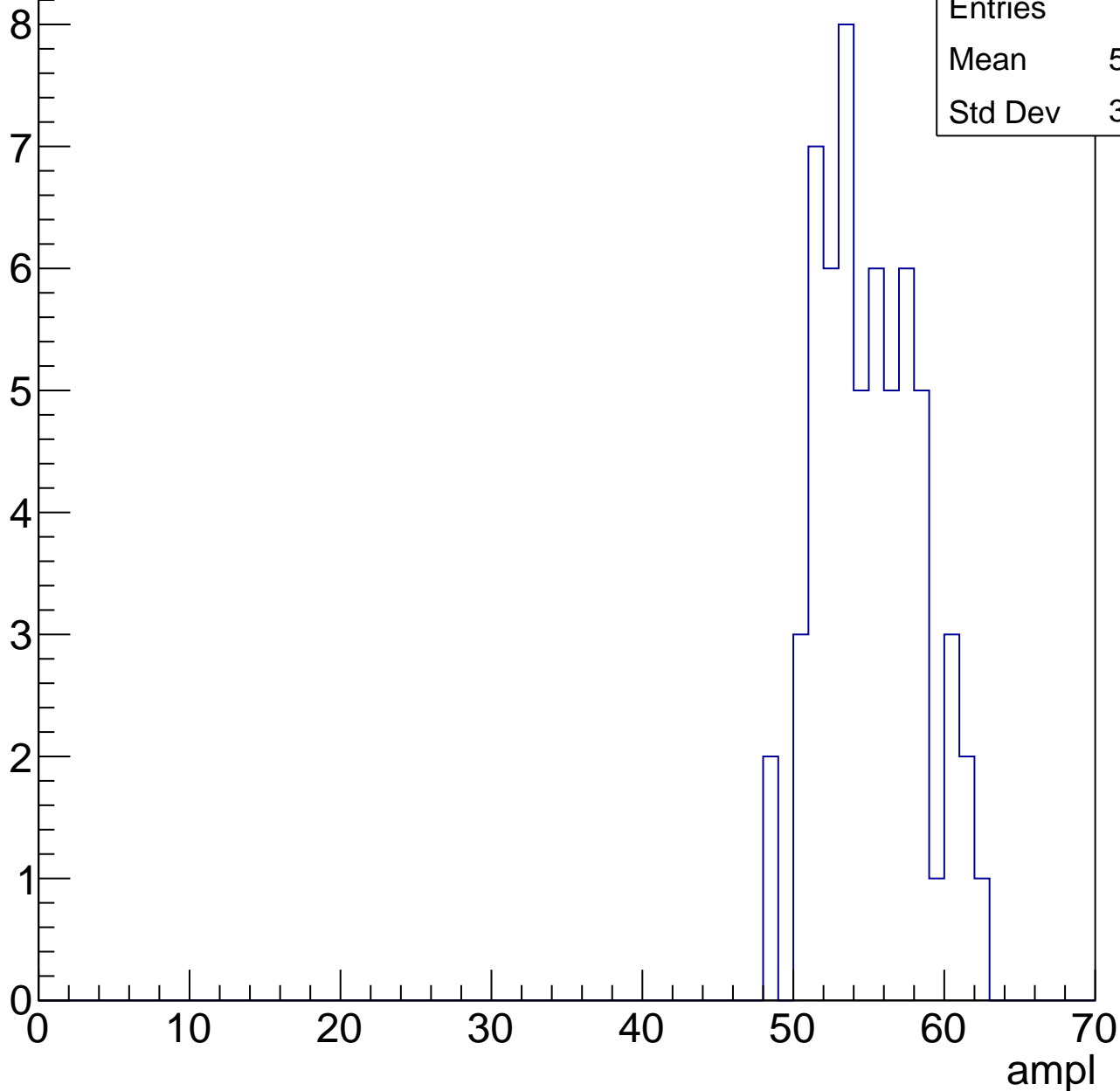


B1L102S, U9-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.57
Std Dev	3.293



B1L102S, U9-ch99, adc5

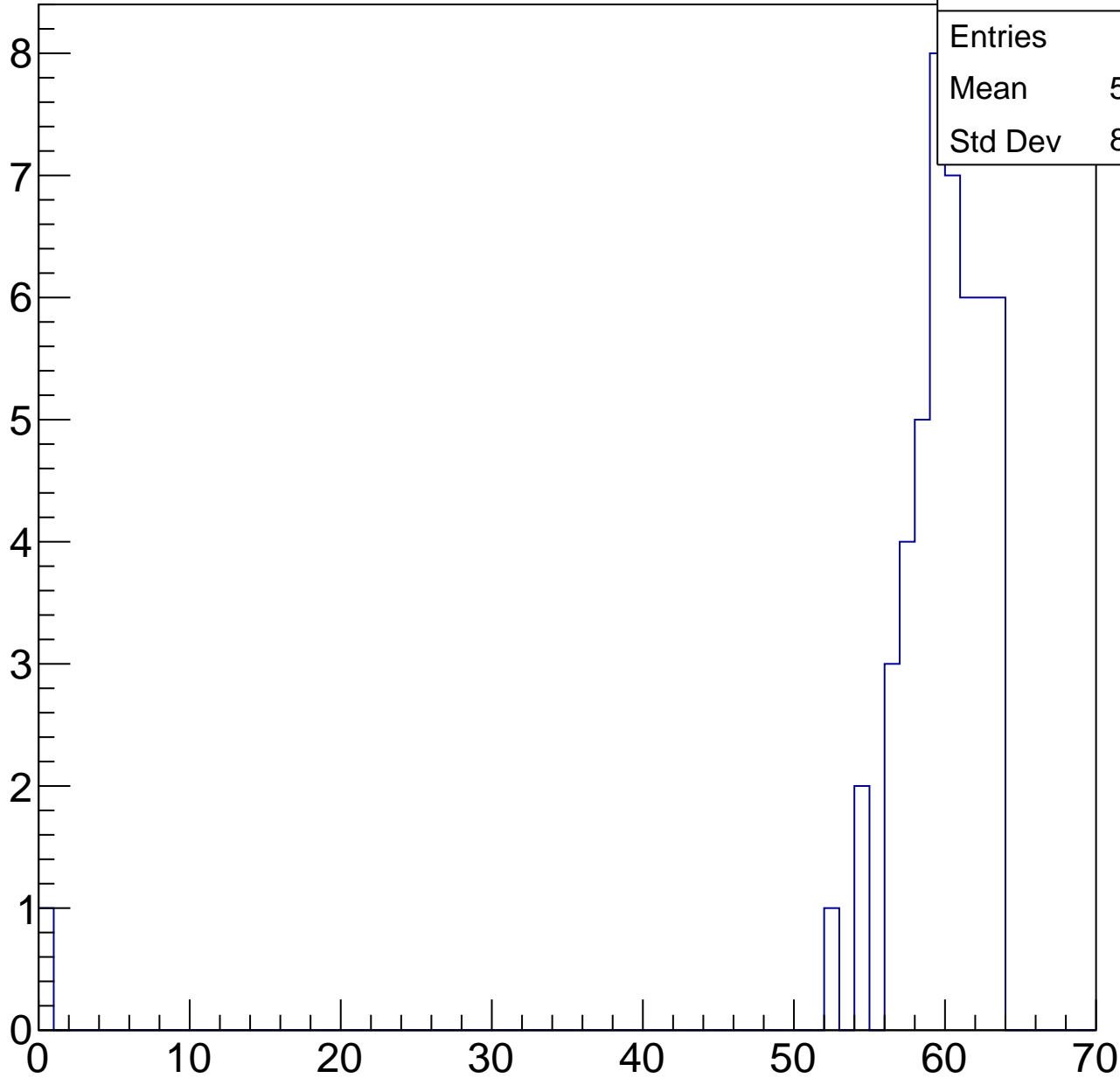
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.24
Std Dev	8.784

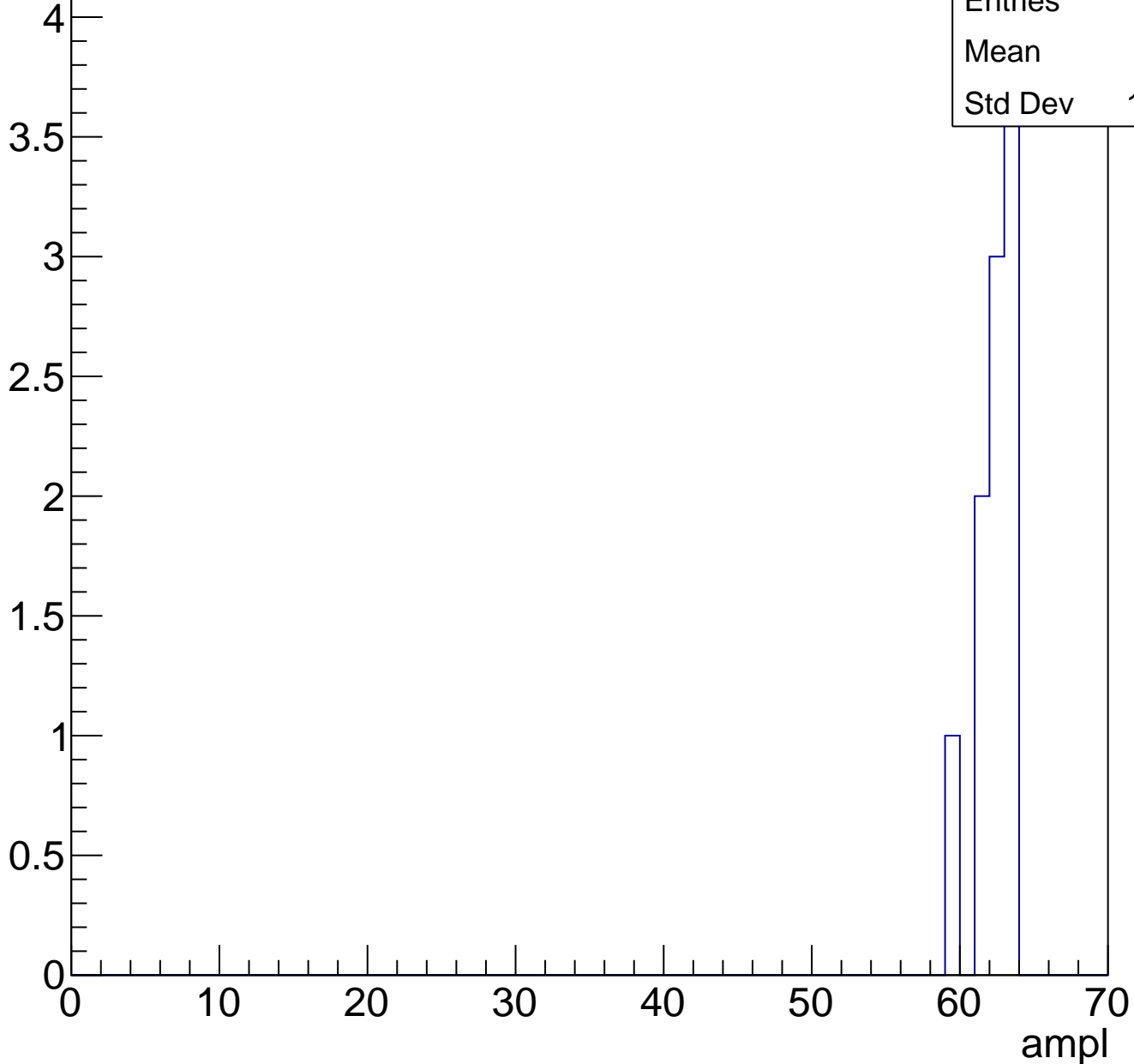
ampl



B1L102S, U9-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch100, adc0

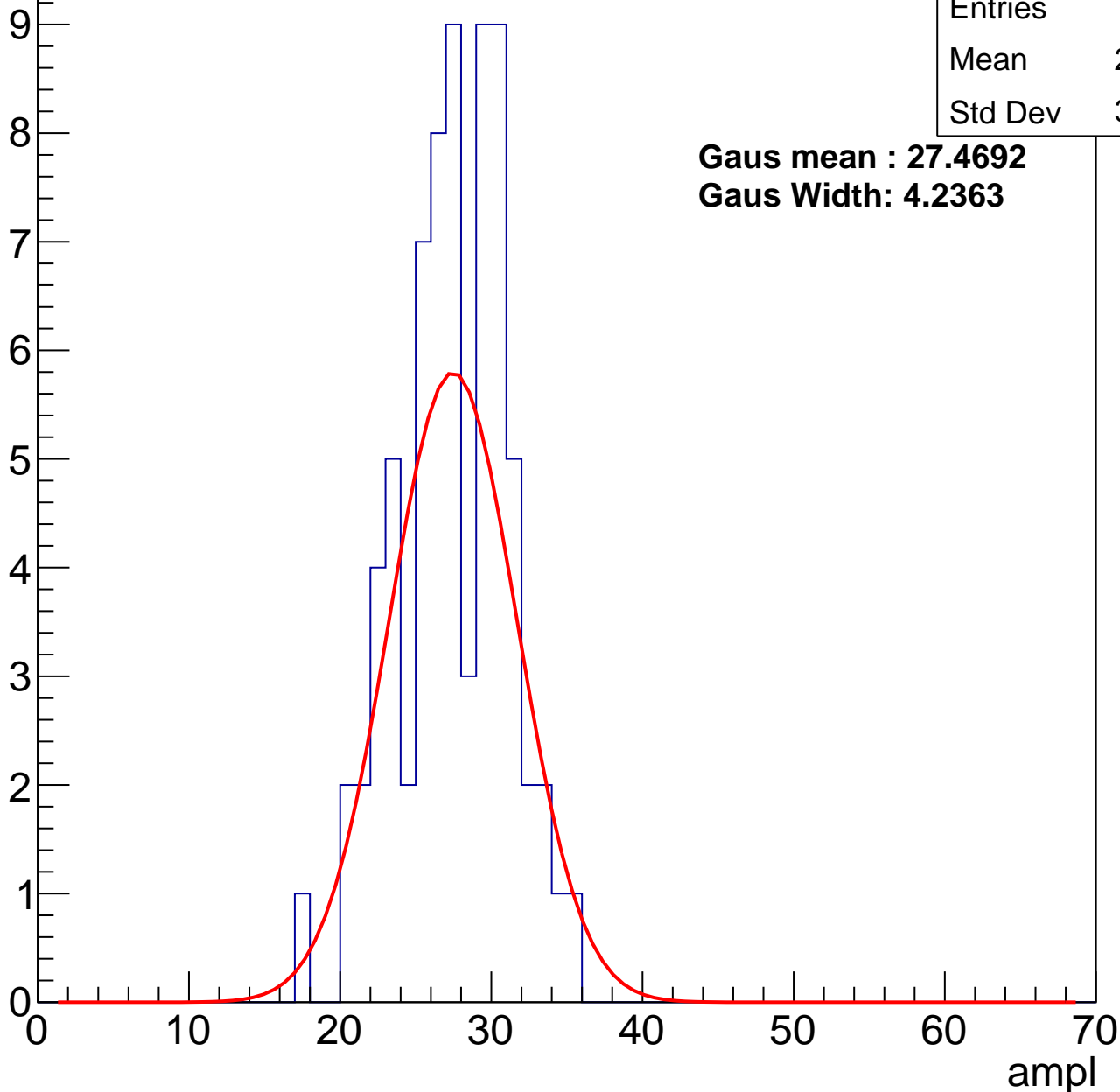
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.01
Std Dev	3.611

Gaus mean : 27.4692

Gaus Width: 4.2363



B1L102S, U9-ch100, adc1

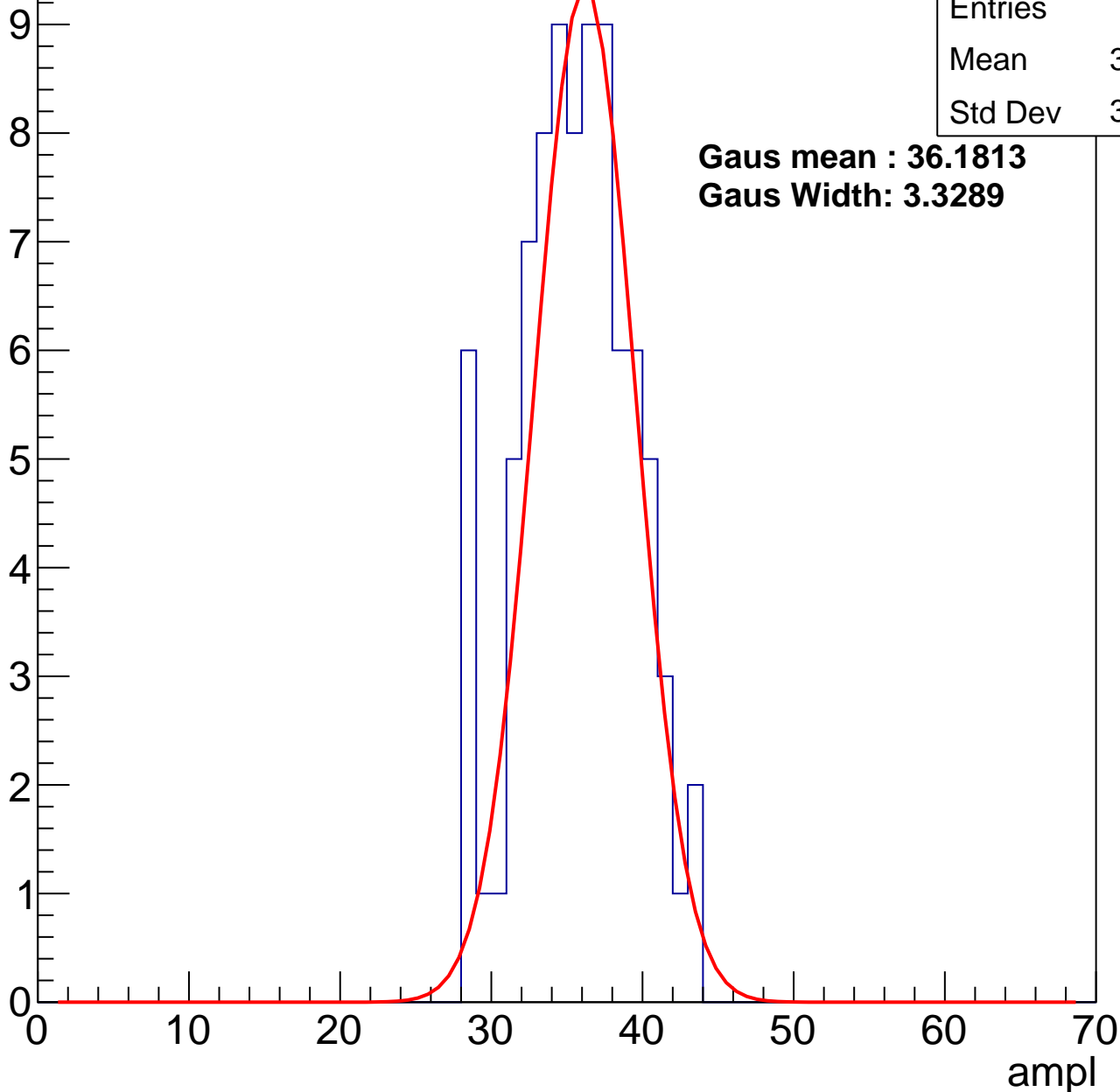
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	35.19
Std Dev	3.642

Gaus mean : 36.1813

Gaus Width: 3.3289



B1L102S, U9-ch100, adc2

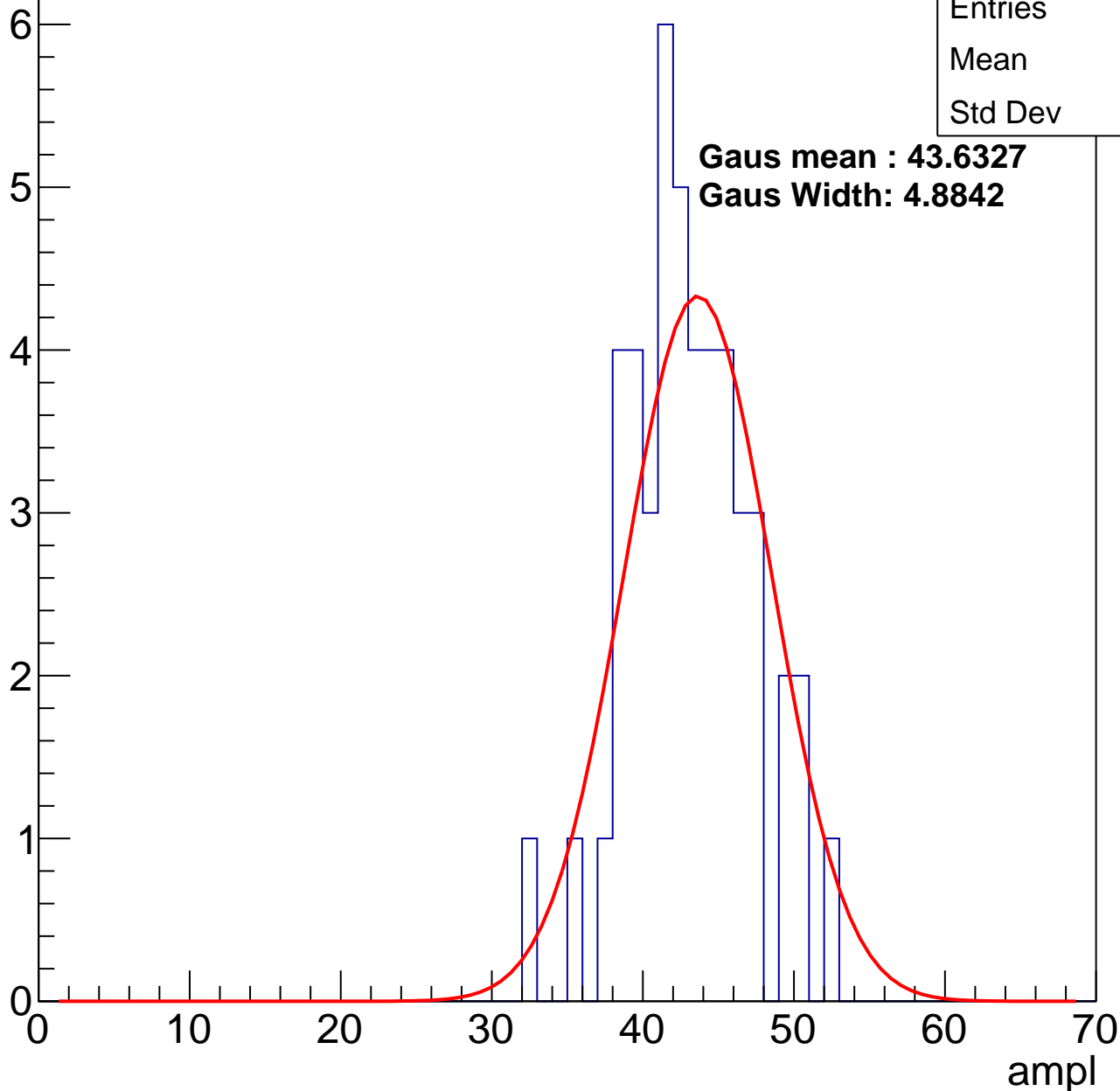
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	42.6
Std Dev	4.03

Gaus mean : 43.6327

Gaus Width: 4.8842

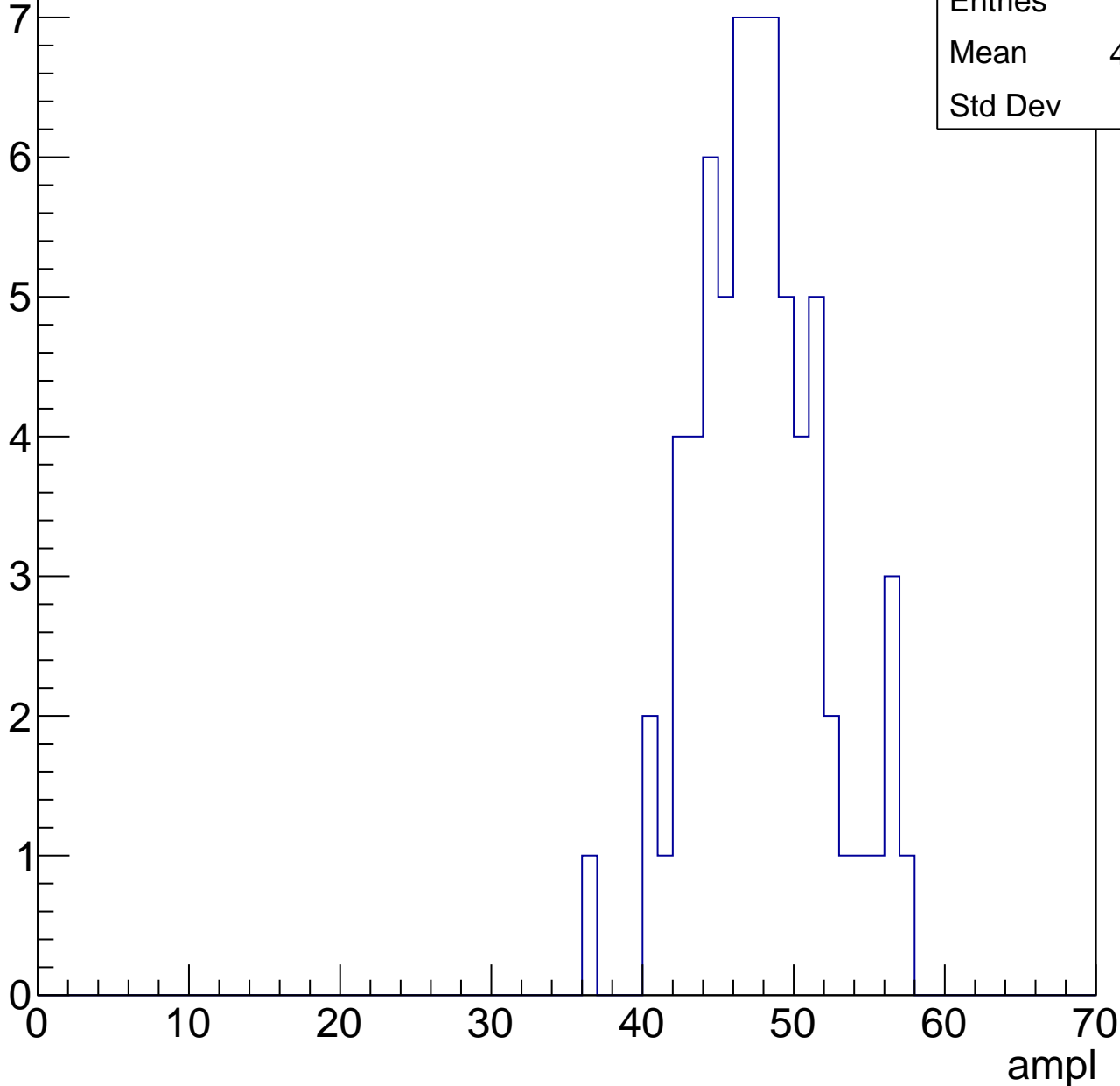


B1L102S, U9-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	47.22
Std Dev	4.2



B1L102S, U9-ch100, adc4

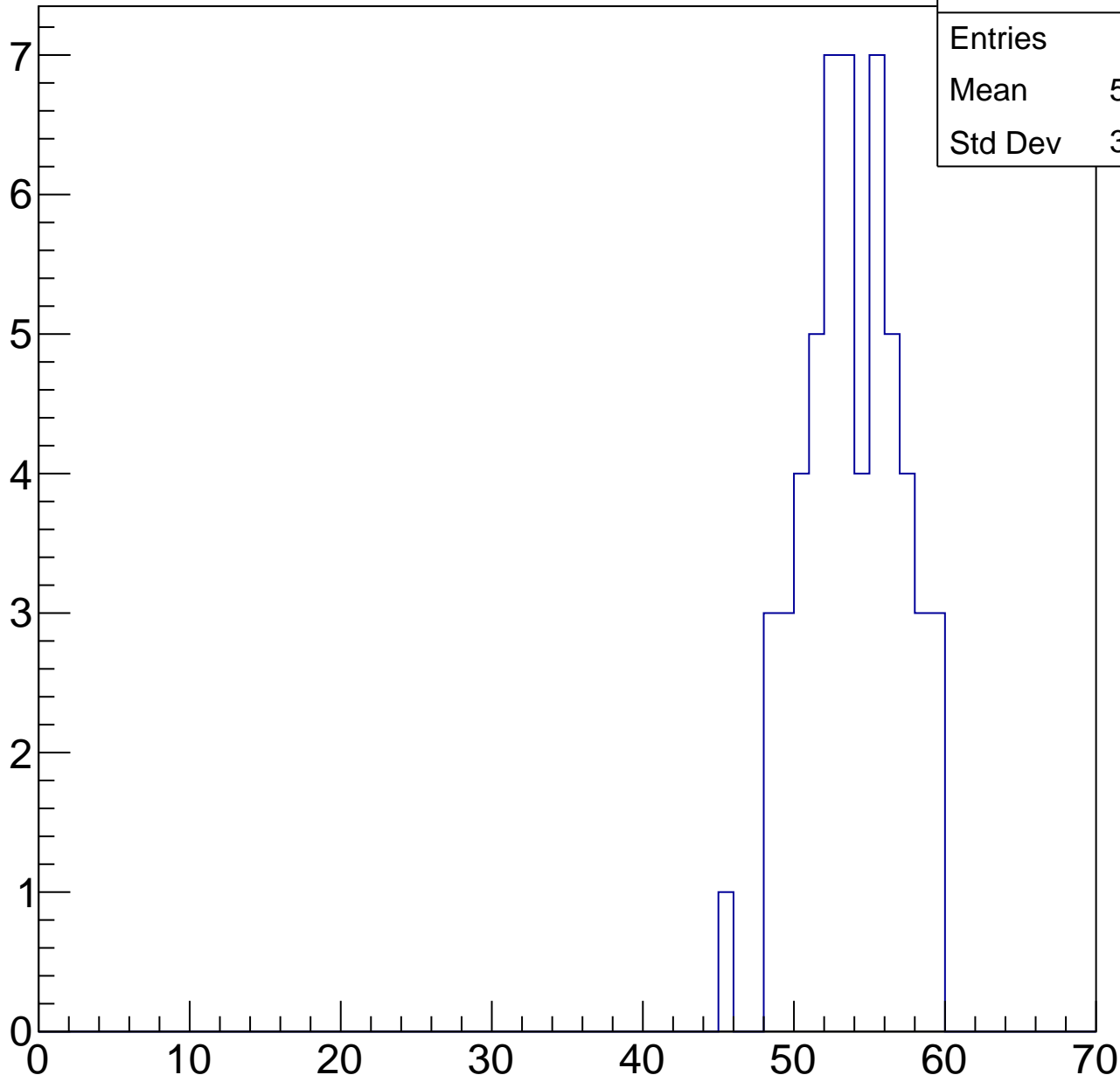
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	56
Mean	53.32
Std Dev	3.185

ampl

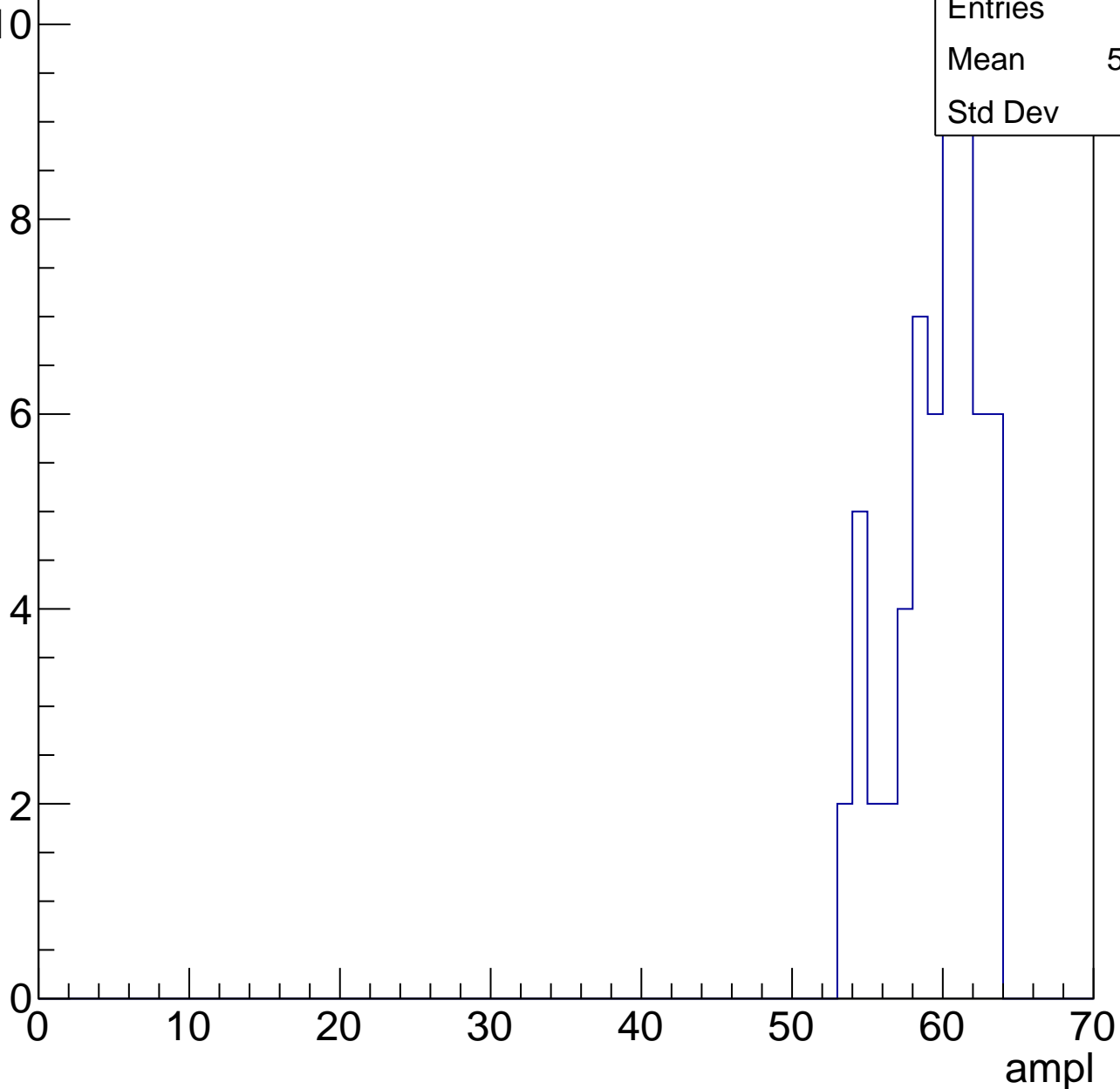


B1L102S, U9-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

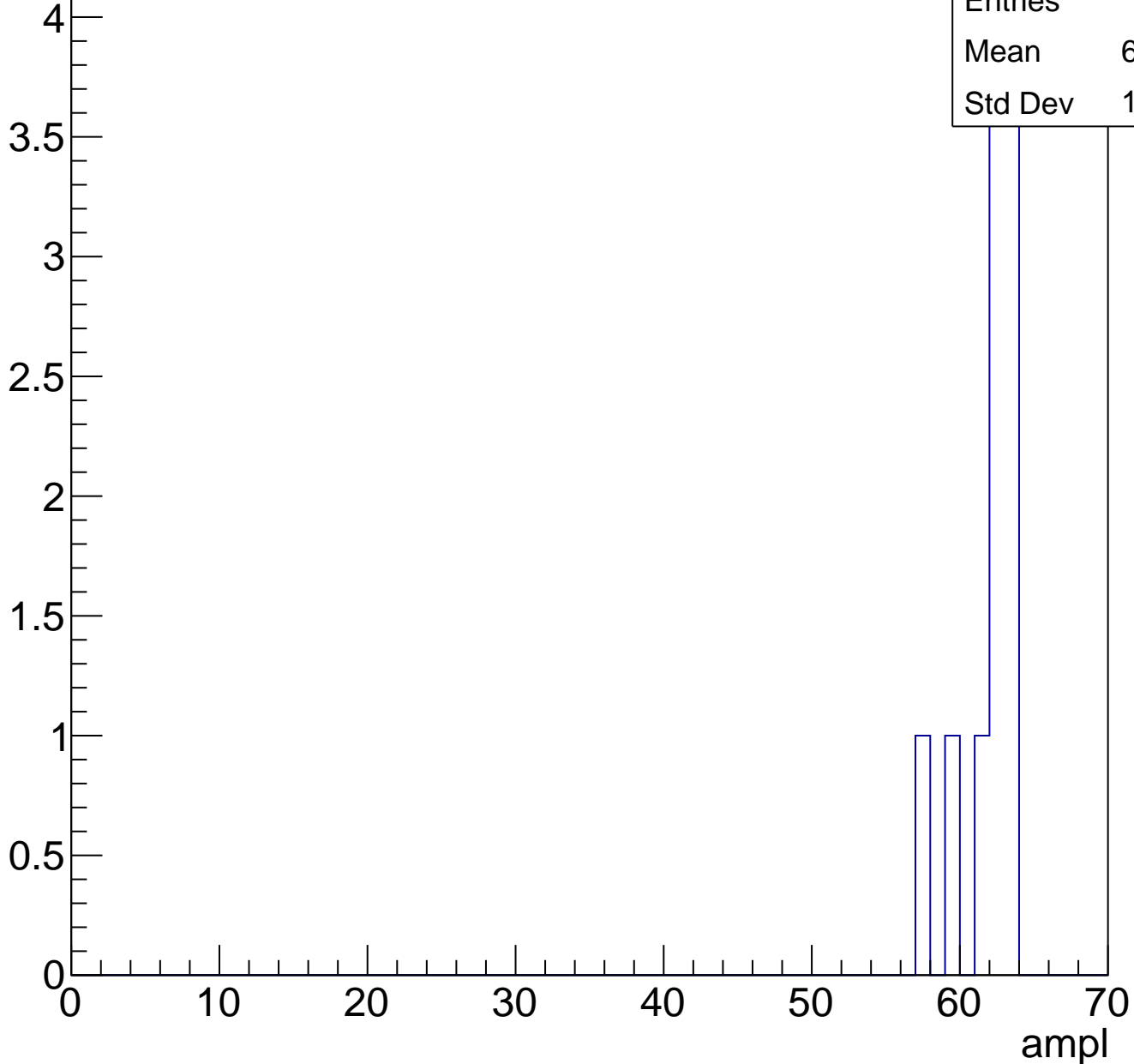
Entries	59
Mean	59.07
Std Dev	2.81



B1L102S, U9-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch101, adc0

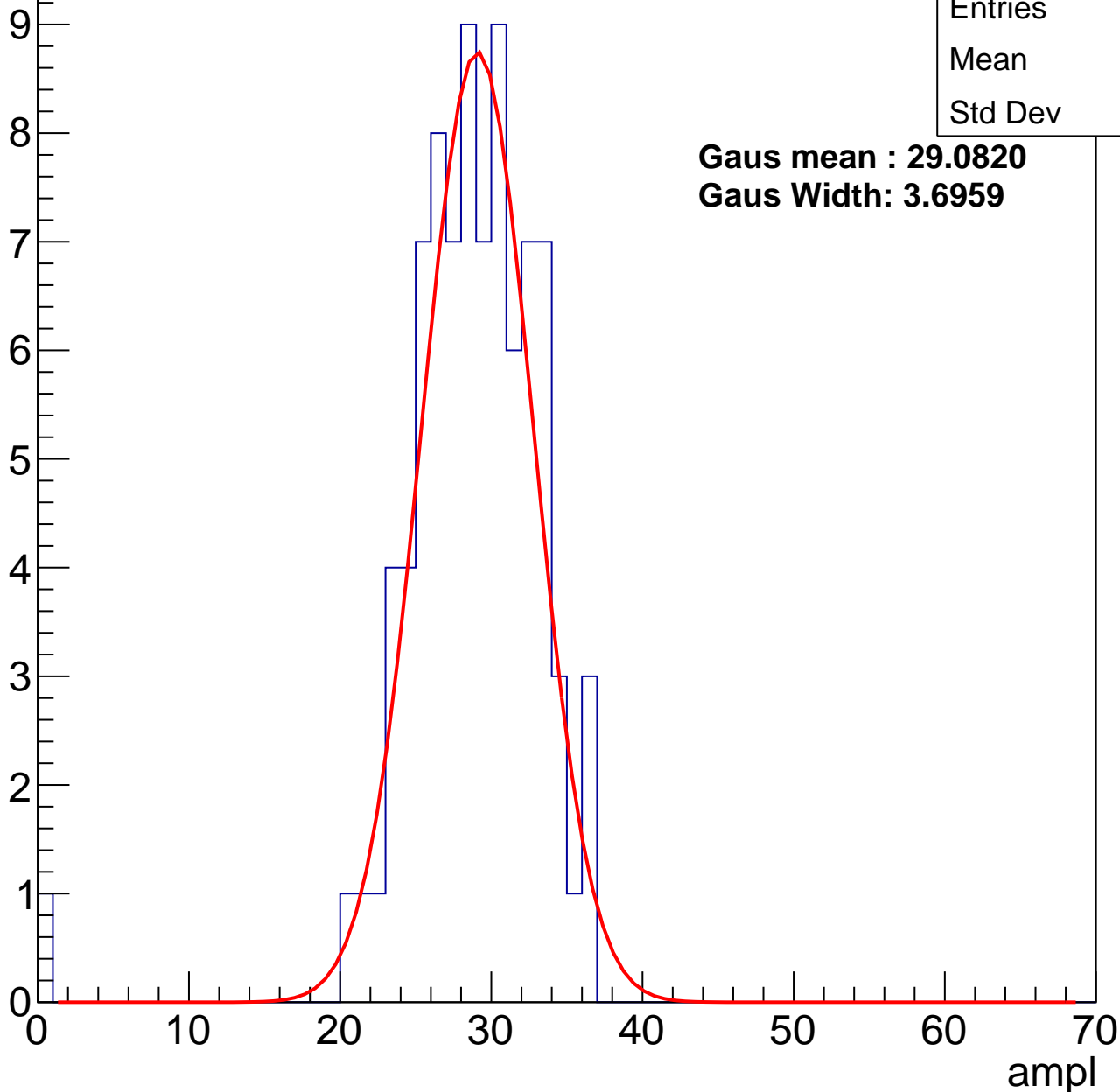
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	28.3
Std Dev	4.73

Gaus mean : 29.0820

Gaus Width: 3.6959



B1L102S, U9-ch101, adc1

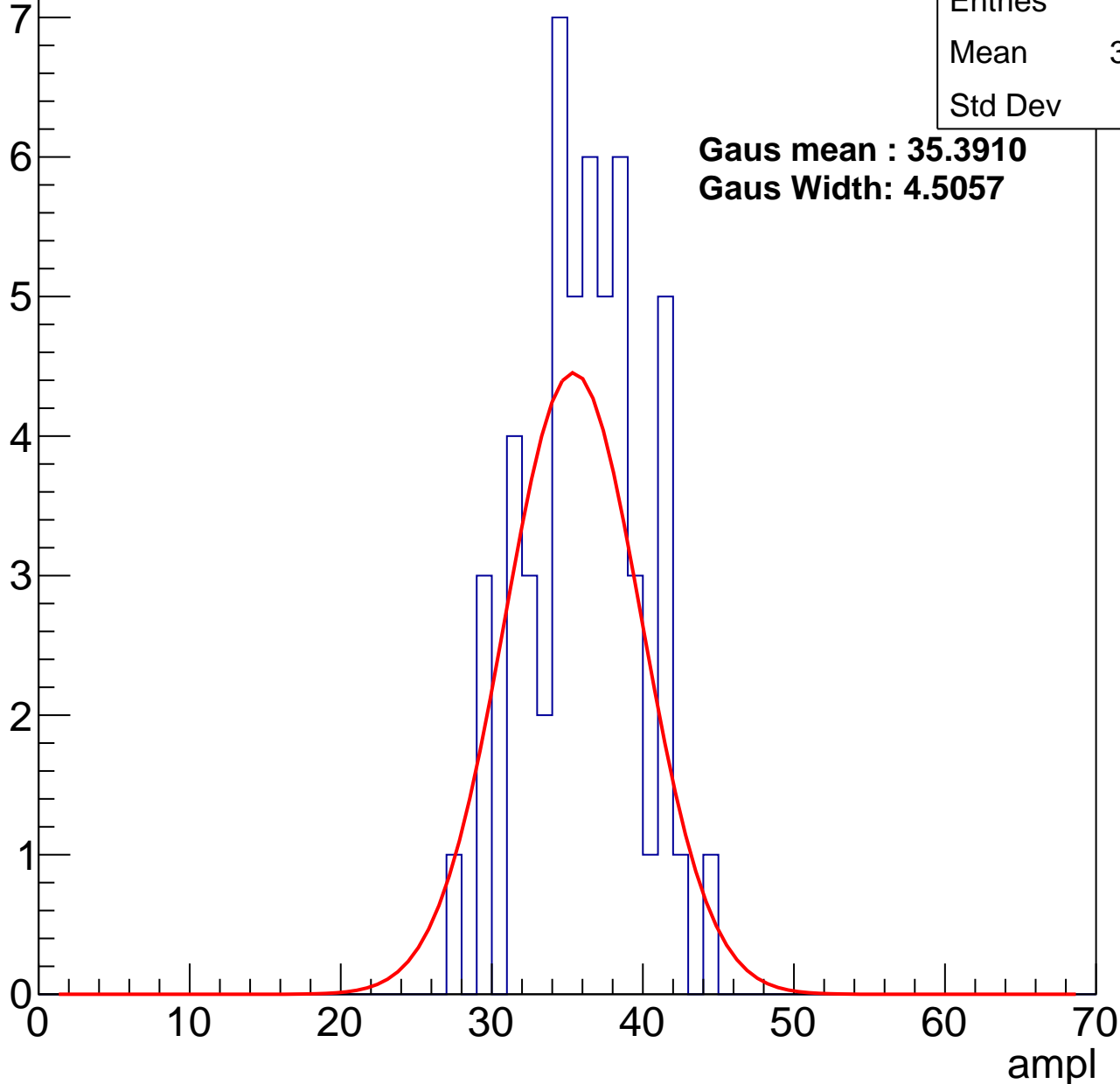
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	35.66
Std Dev	3.68

Gaus mean : 35.3910

Gaus Width: 4.5057



B1L102S, U9-ch101, adc2

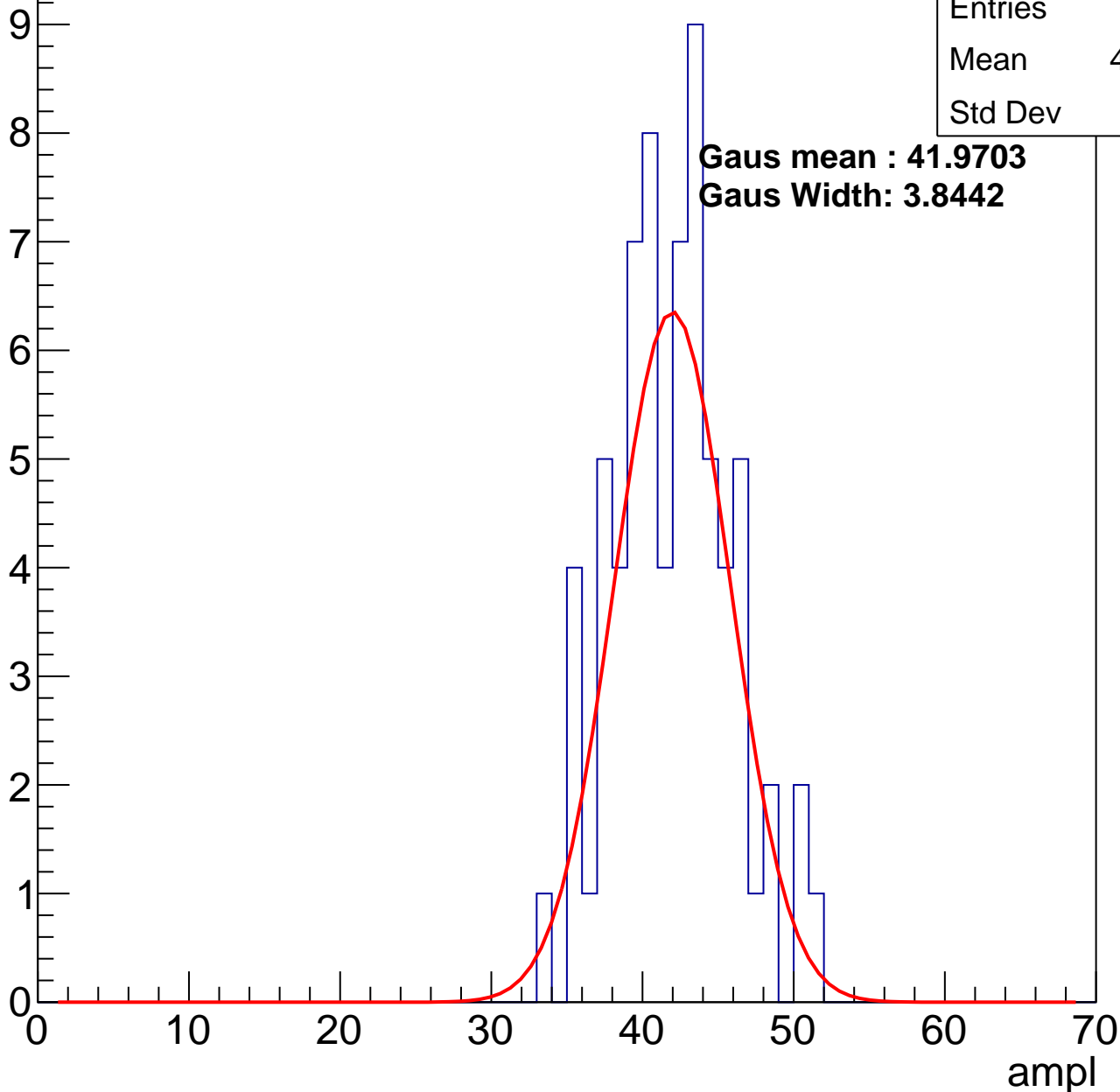
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.54
Std Dev	3.85

Gaus mean : 41.9703

Gaus Width: 3.8442

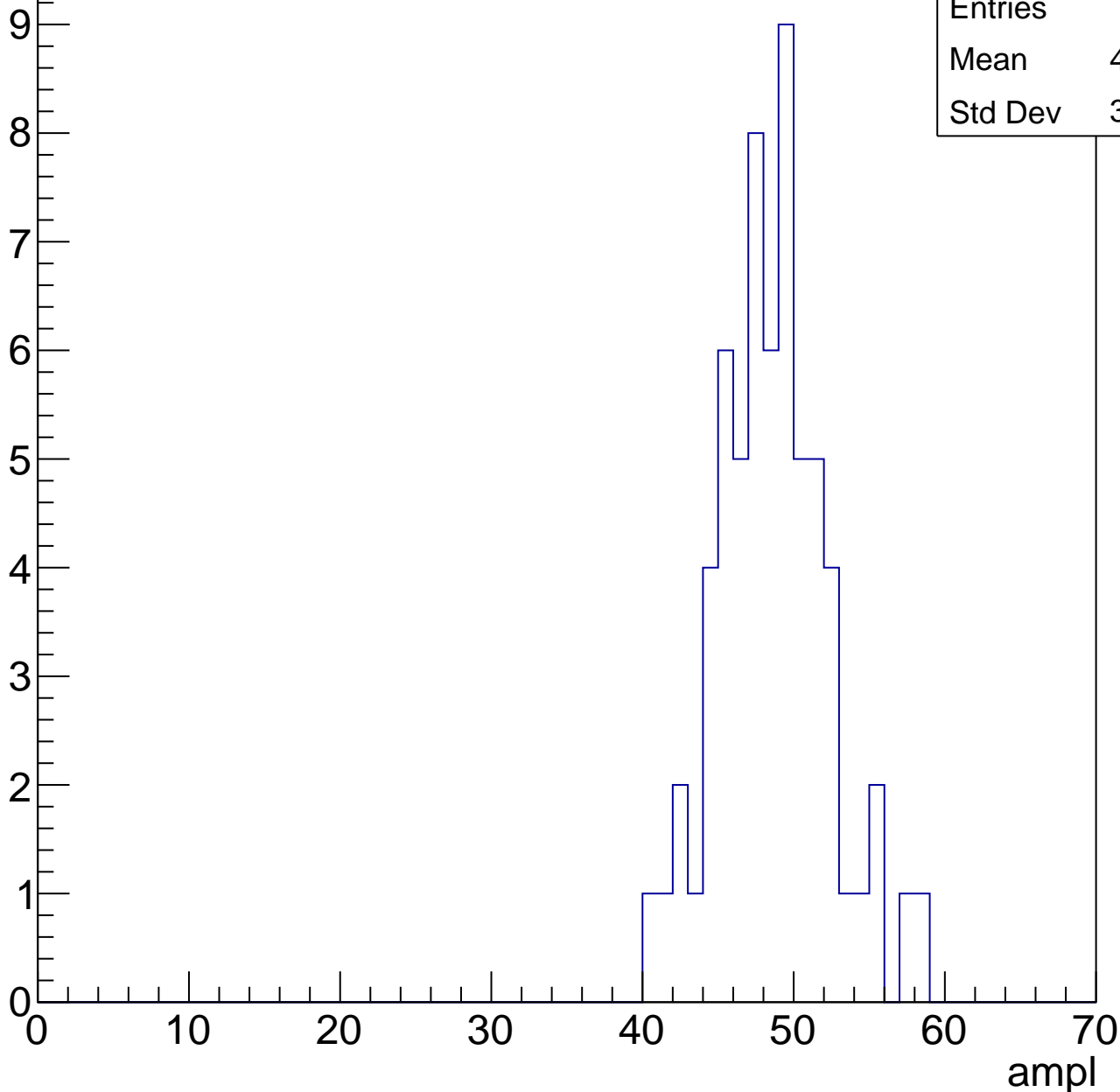


B1L102S, U9-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.16
Std Dev	3.635

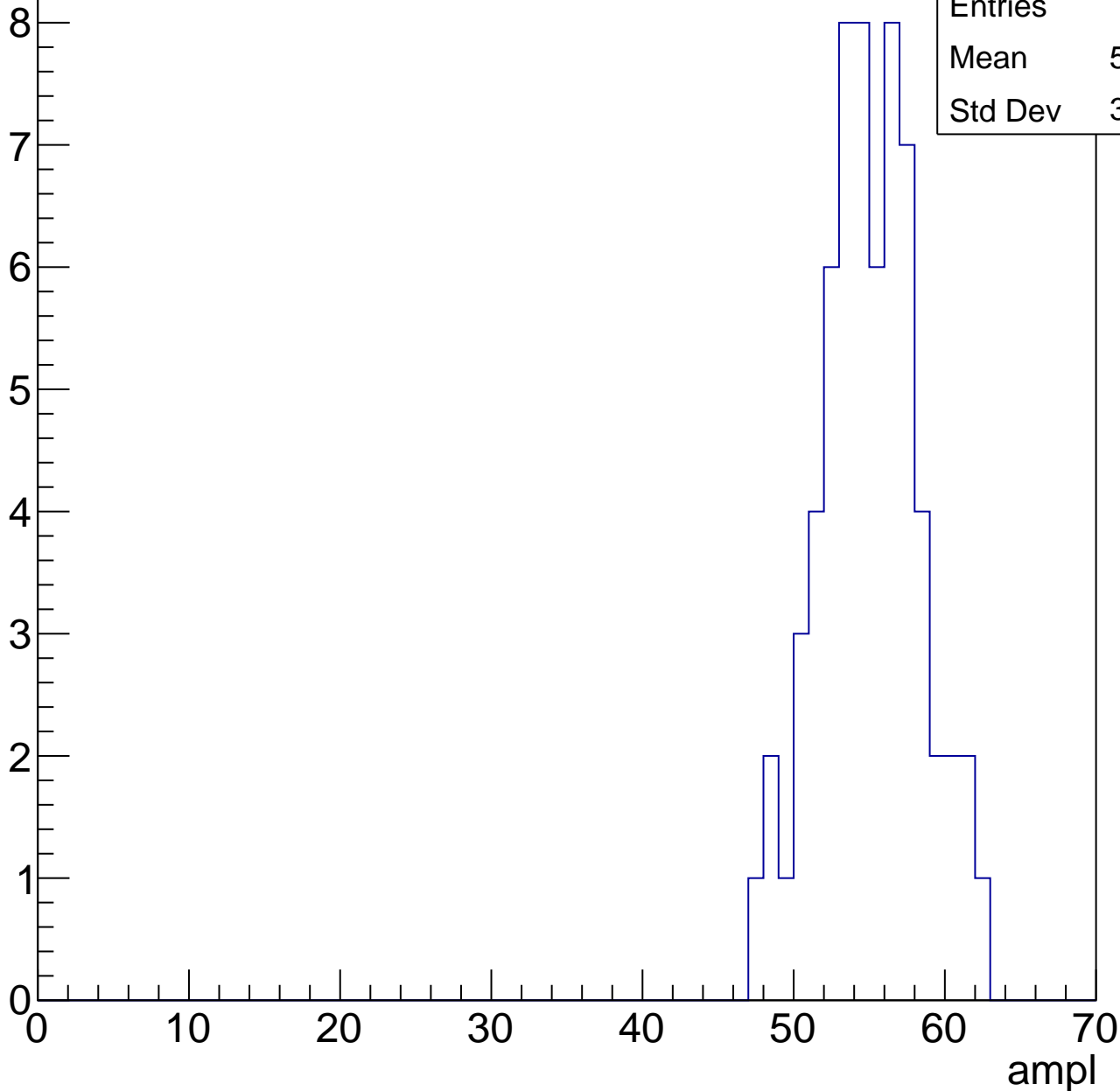


B1L102S, U9-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

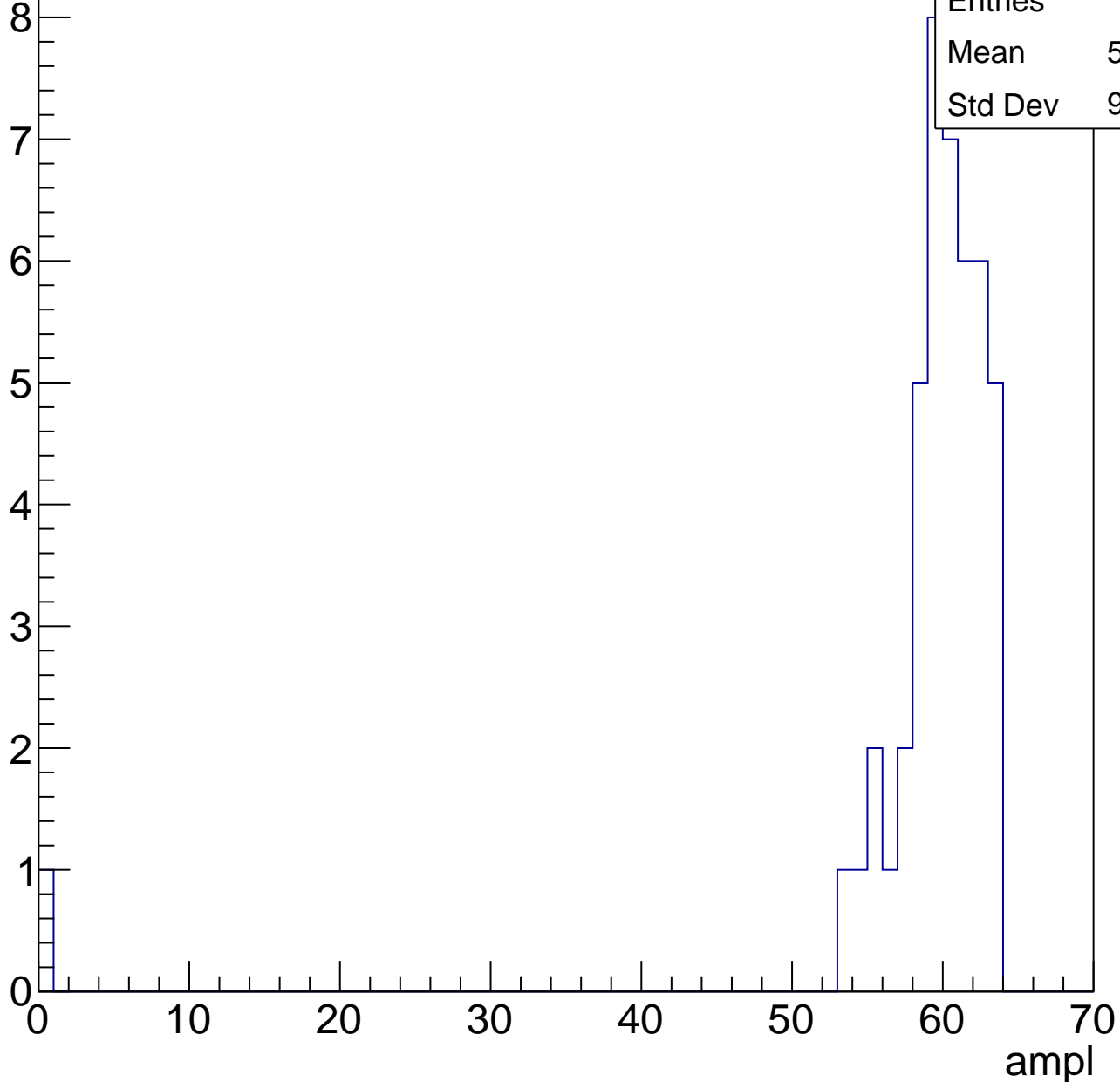
Entries	65
Mean	54.54
Std Dev	3.263



B1L102S, U9-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

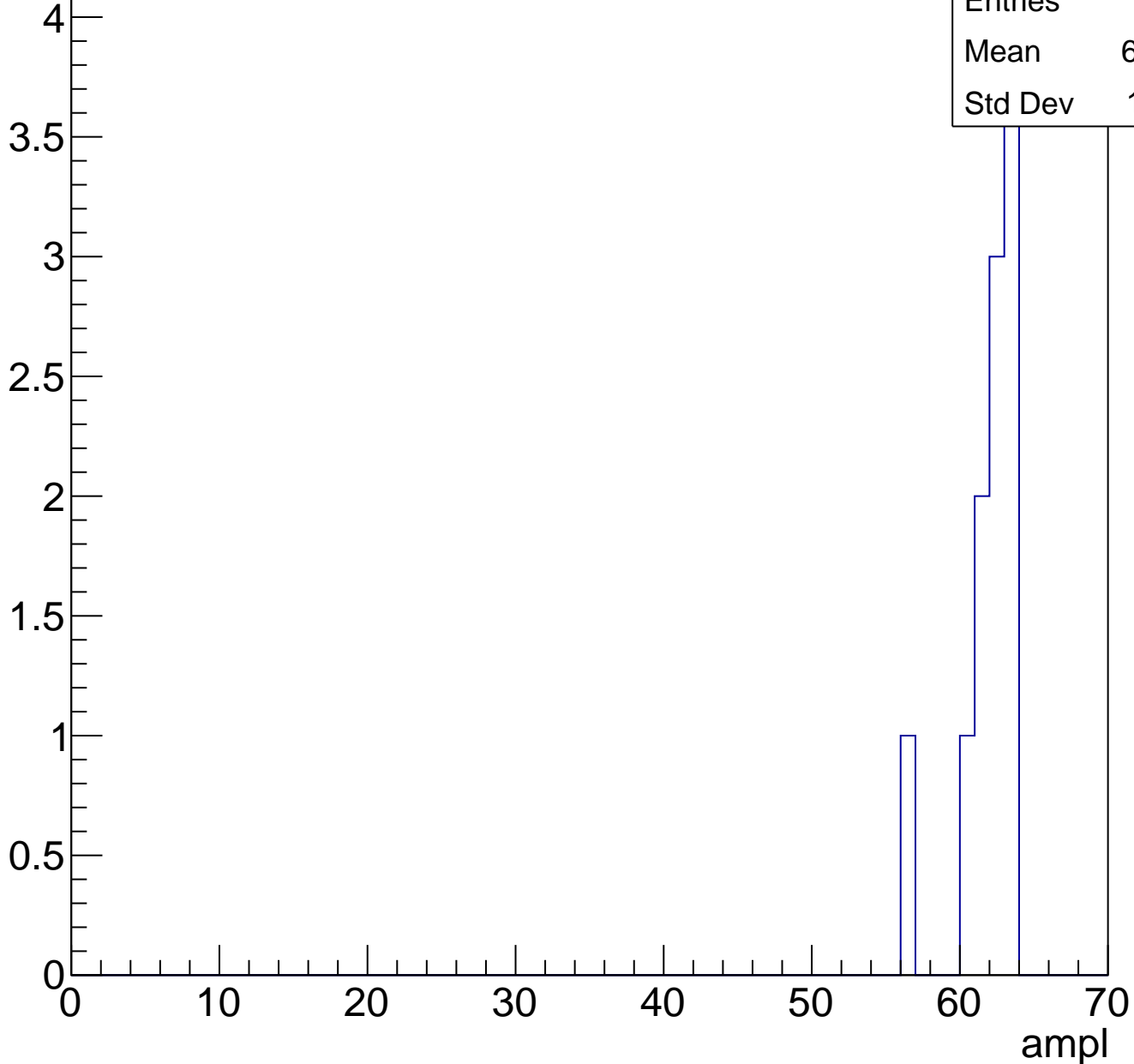
Entry



B1L102S, U9-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	11
Mean	61.45
Std Dev	1.971

B1L102S, U9-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U9-ch102, adc0

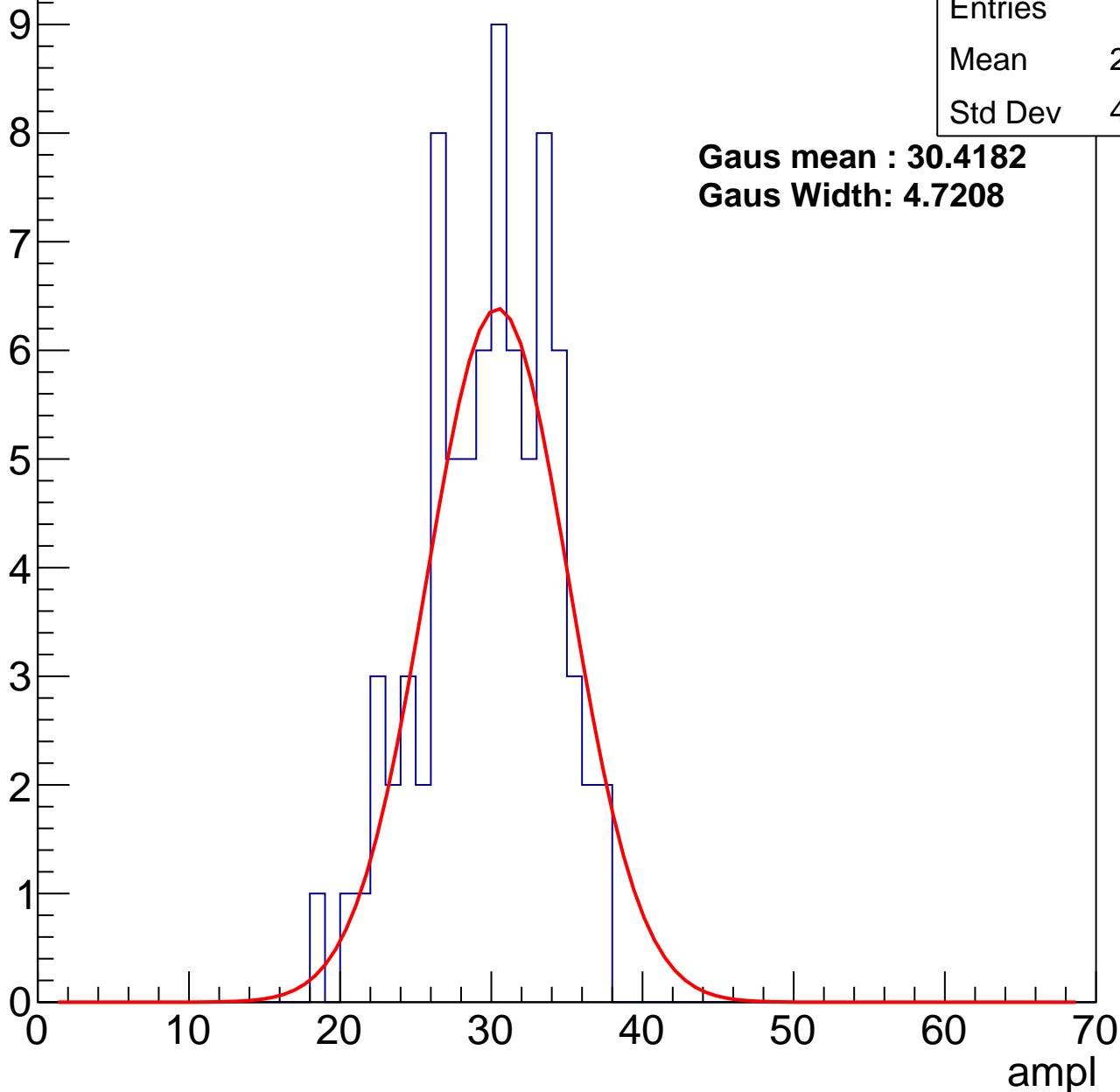
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.29
Std Dev	4.194

Gaus mean : 30.4182

Gaus Width: 4.7208



B1L102S, U9-ch102, adc1

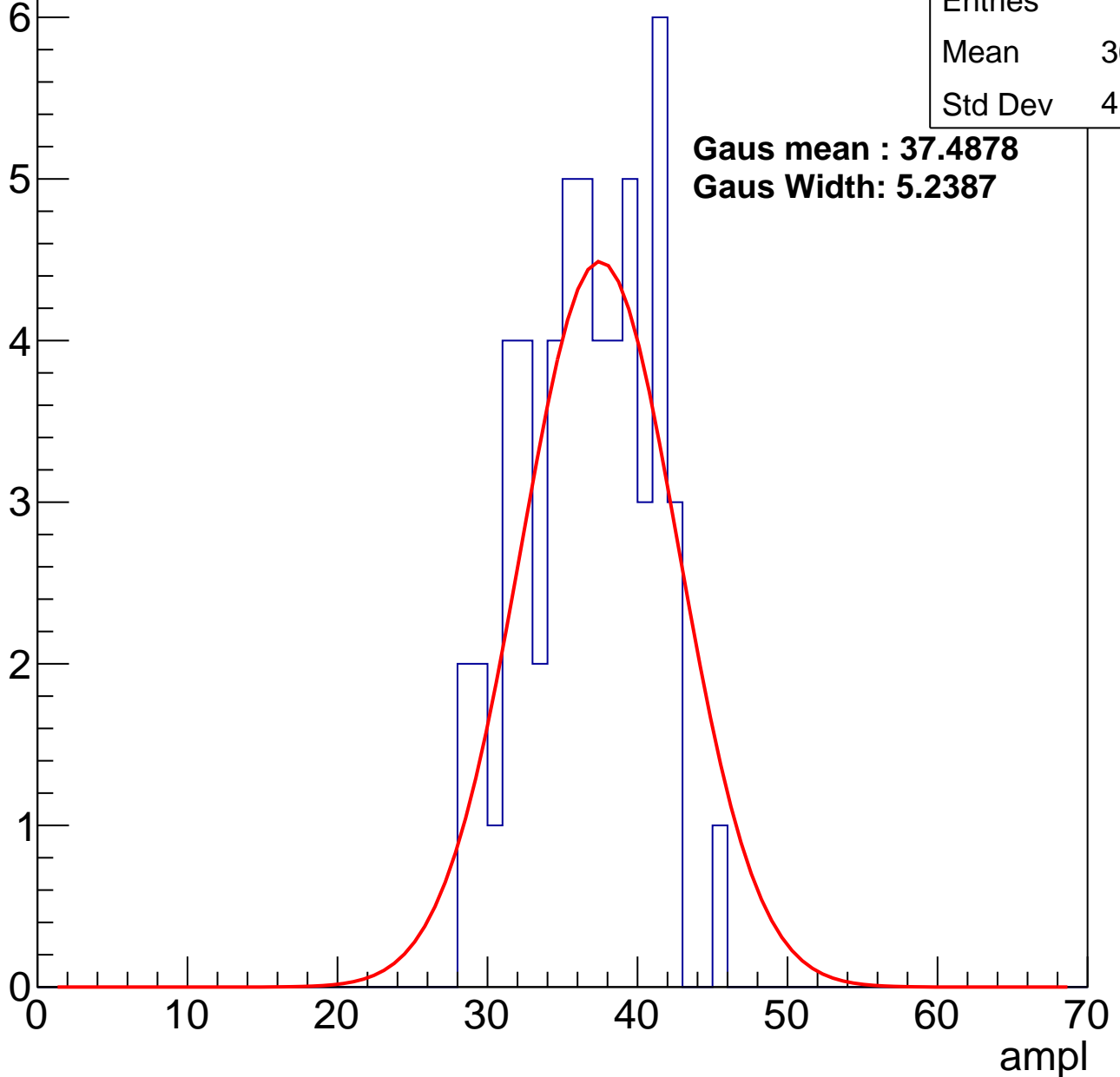
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	36.09
Std Dev	4.082

Gaus mean : 37.4878

Gaus Width: 5.2387



B1L102S, U9-ch102, adc2

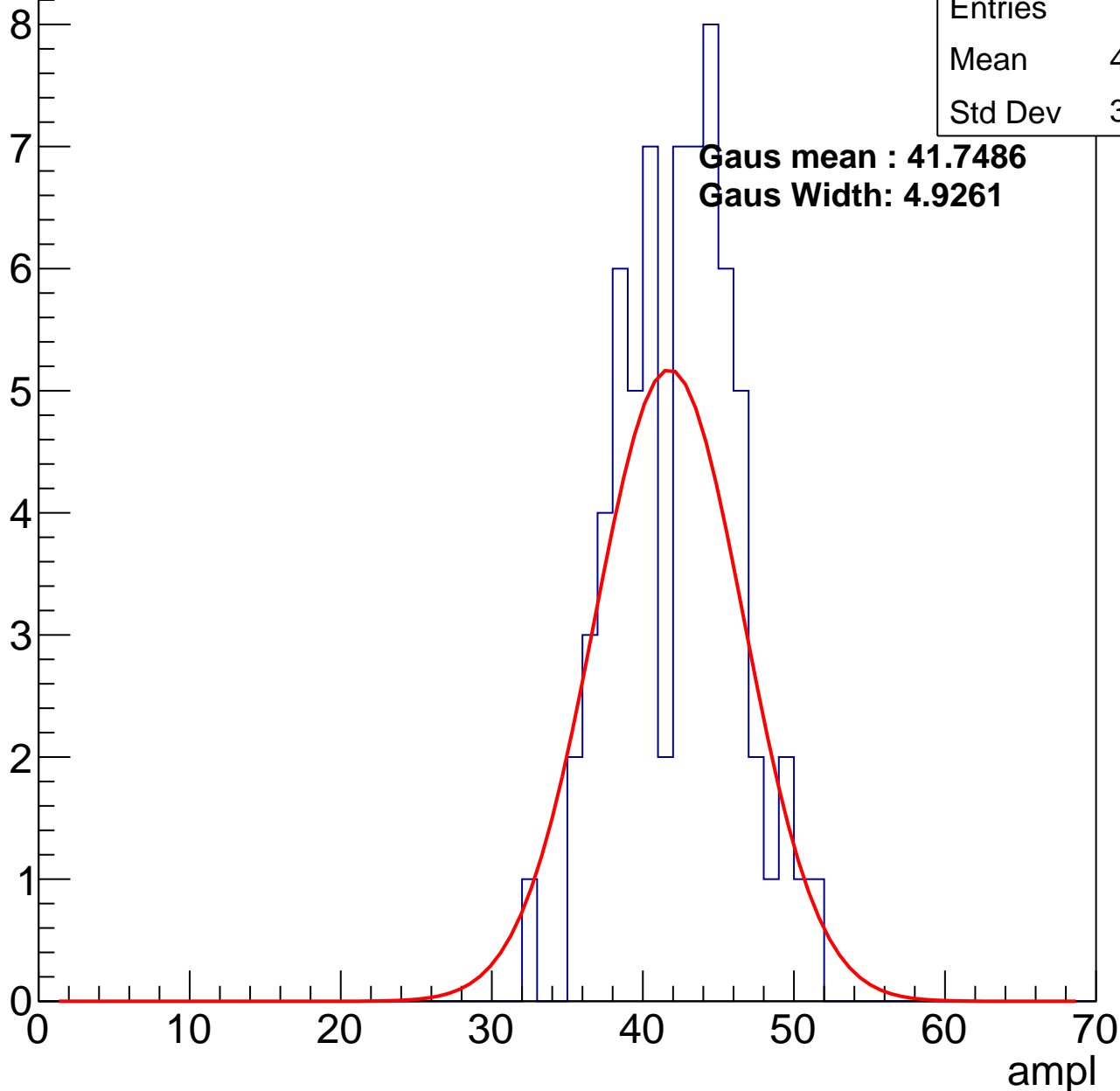
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.87
Std Dev	3.924

Gaus mean : 41.7486

Gaus Width: 4.9261

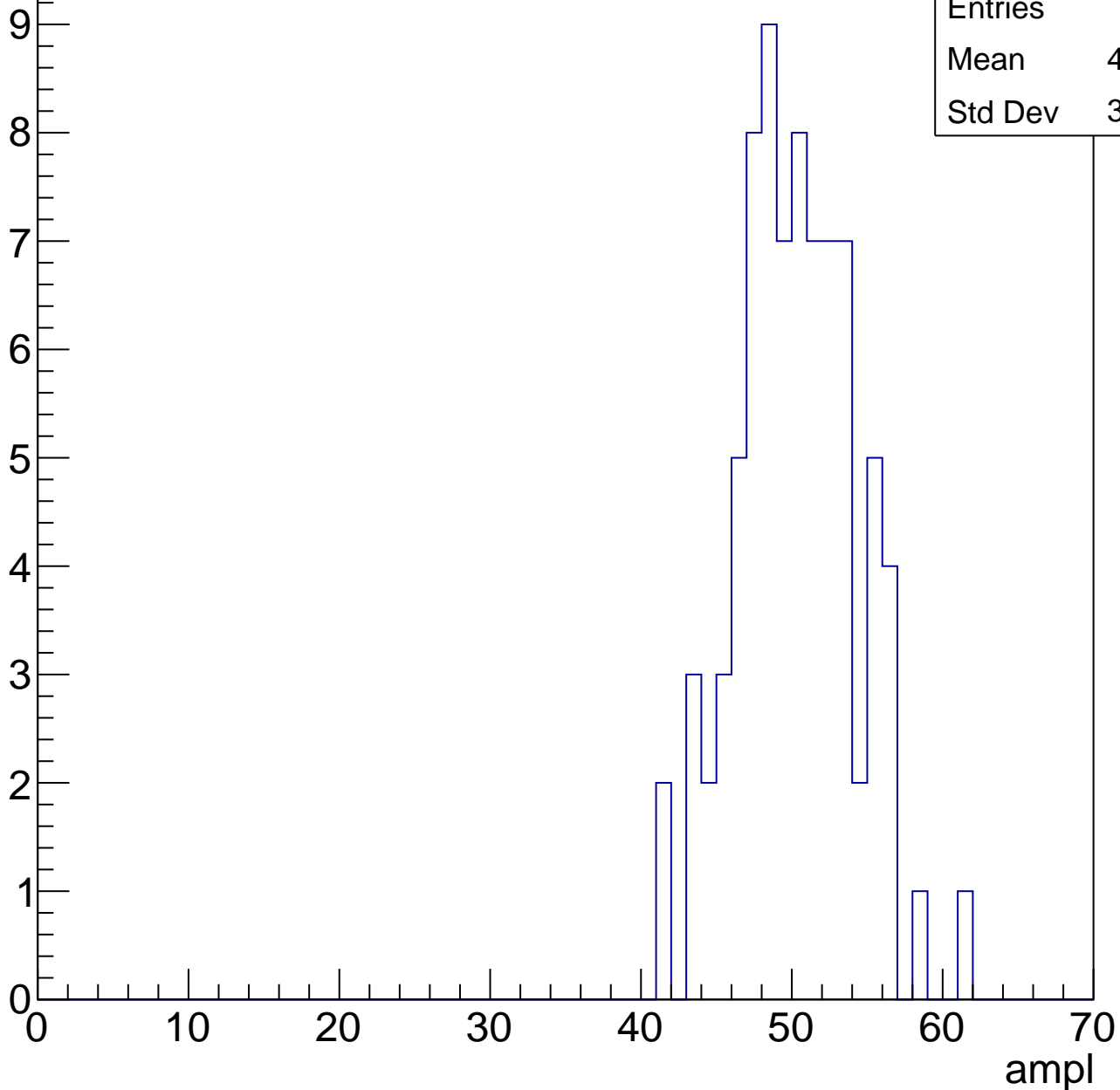


B1L102S, U9-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

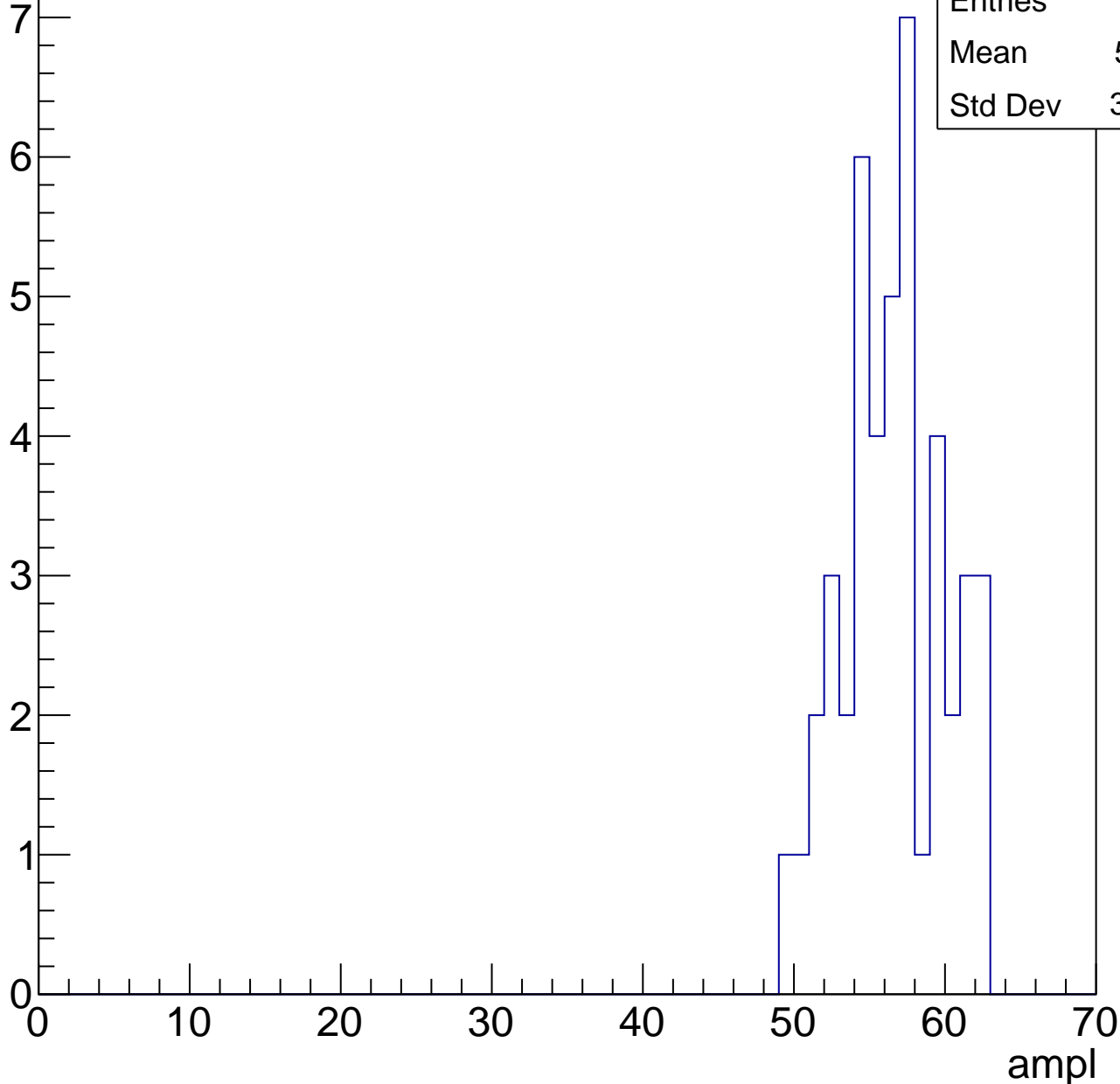
Entries	81
Mean	49.79
Std Dev	3.899



B1L102S, U9-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	44
Mean	56.11
Std Dev	3.339

B1L102S, U9-ch102, adc5

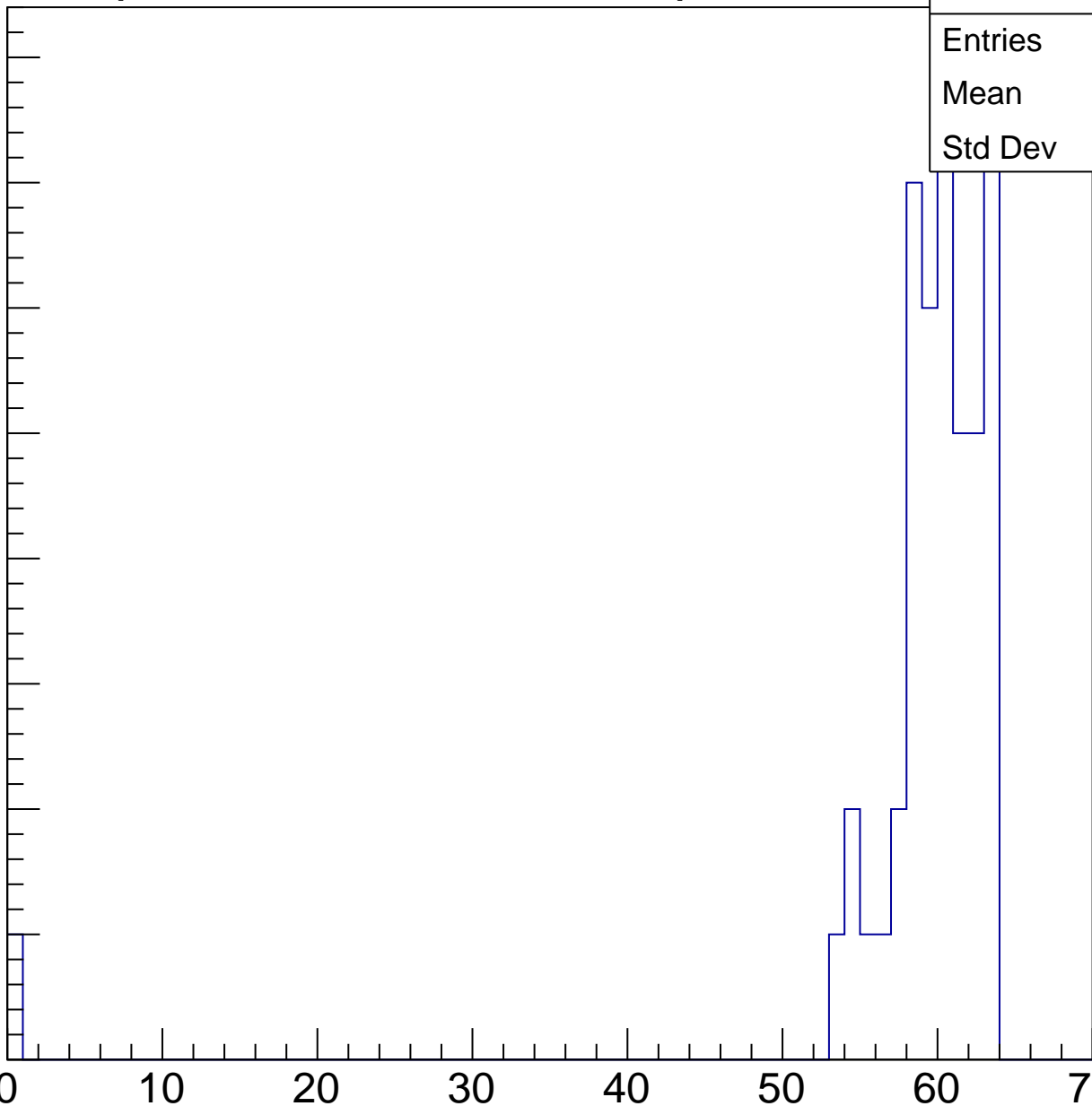
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.4
Std Dev	8.981

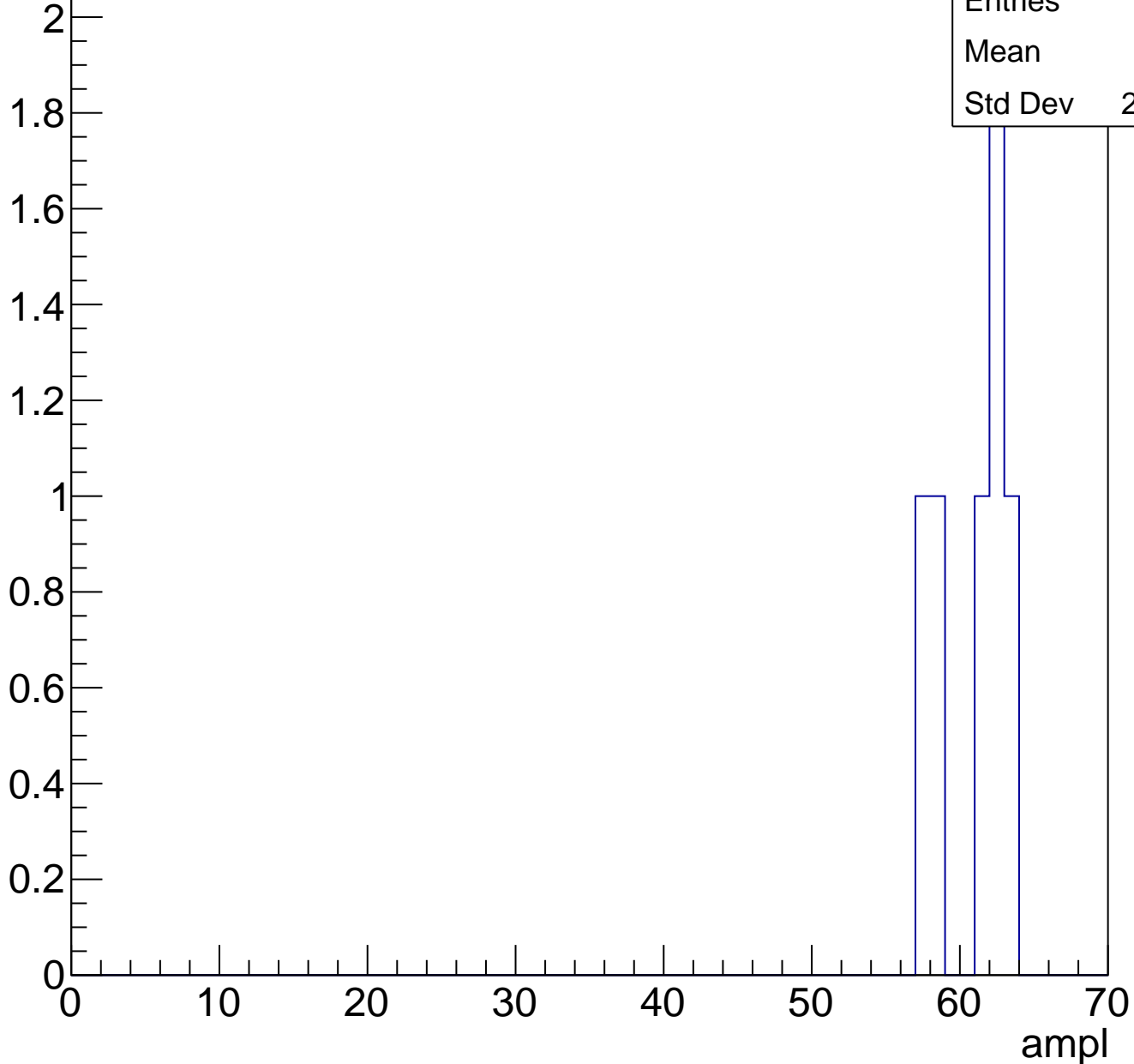
ampl



B1L102S, U9-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U9-ch103, adc0

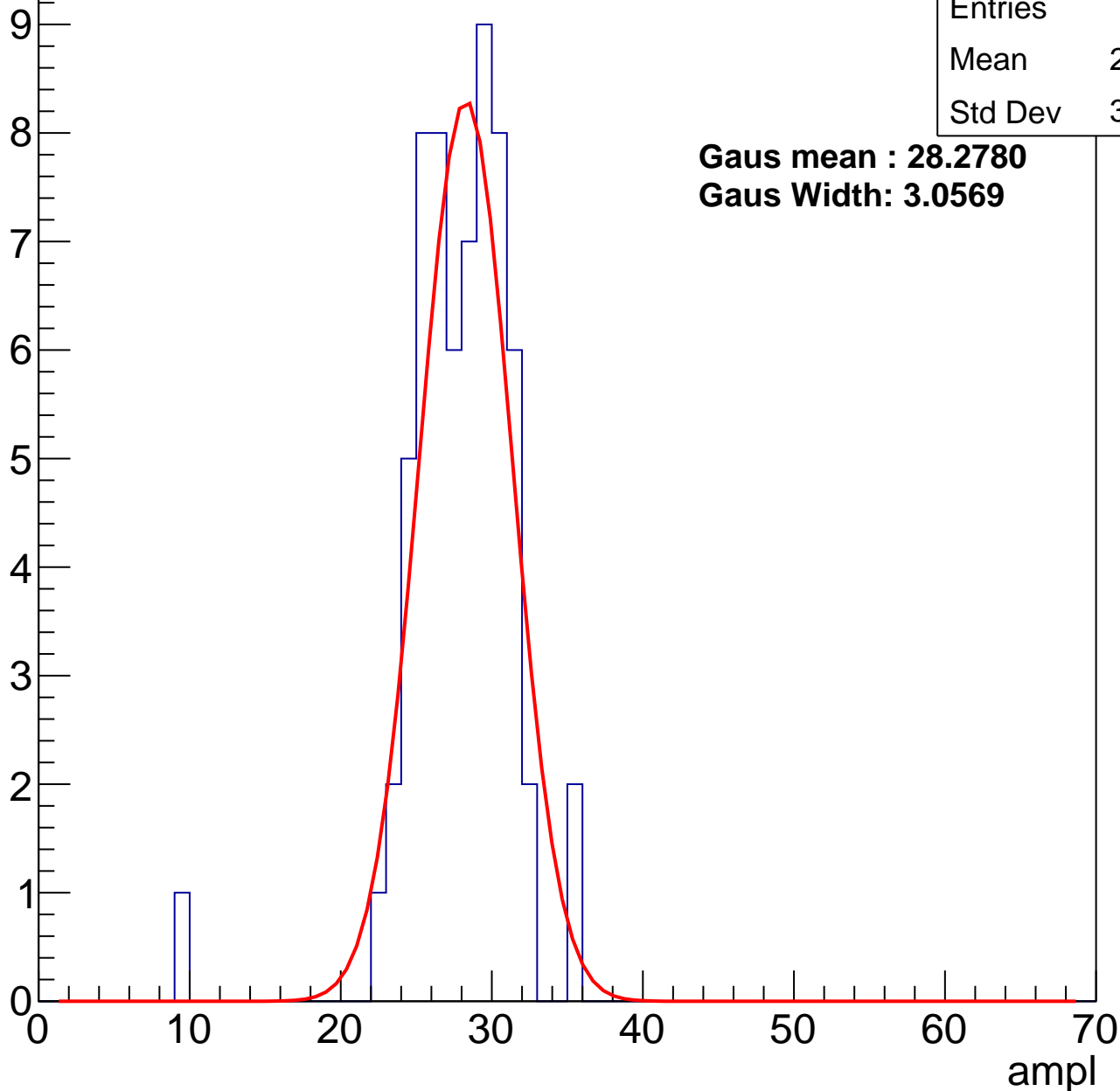
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.45
Std Dev	3.599

Gaus mean : 28.2780

Gaus Width: 3.0569



B1L102S, U9-ch103, adc1

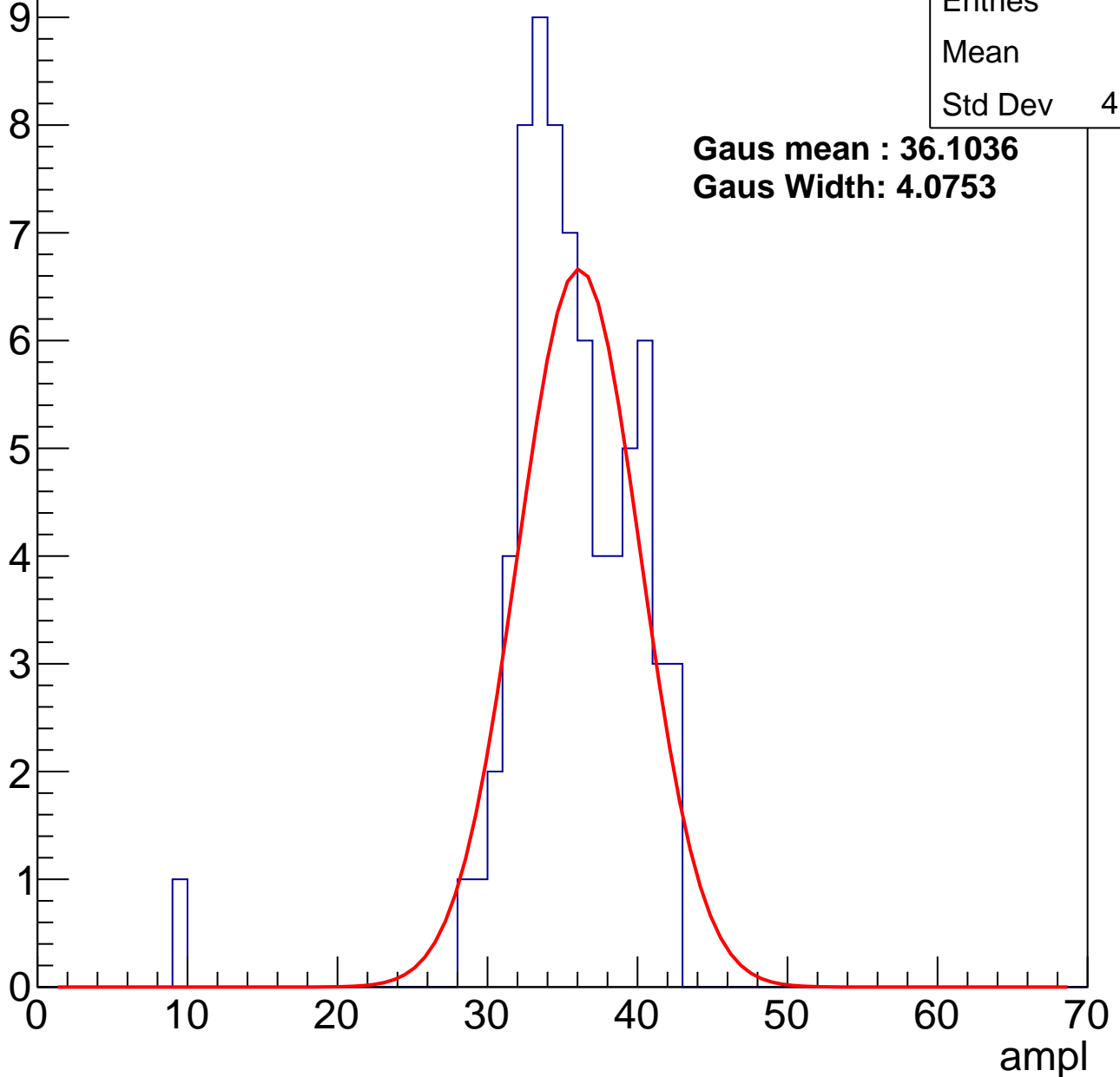
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35
Std Dev	4.616

Gaus mean : 36.1036

Gaus Width: 4.0753



B1L102S, U9-ch103, adc2

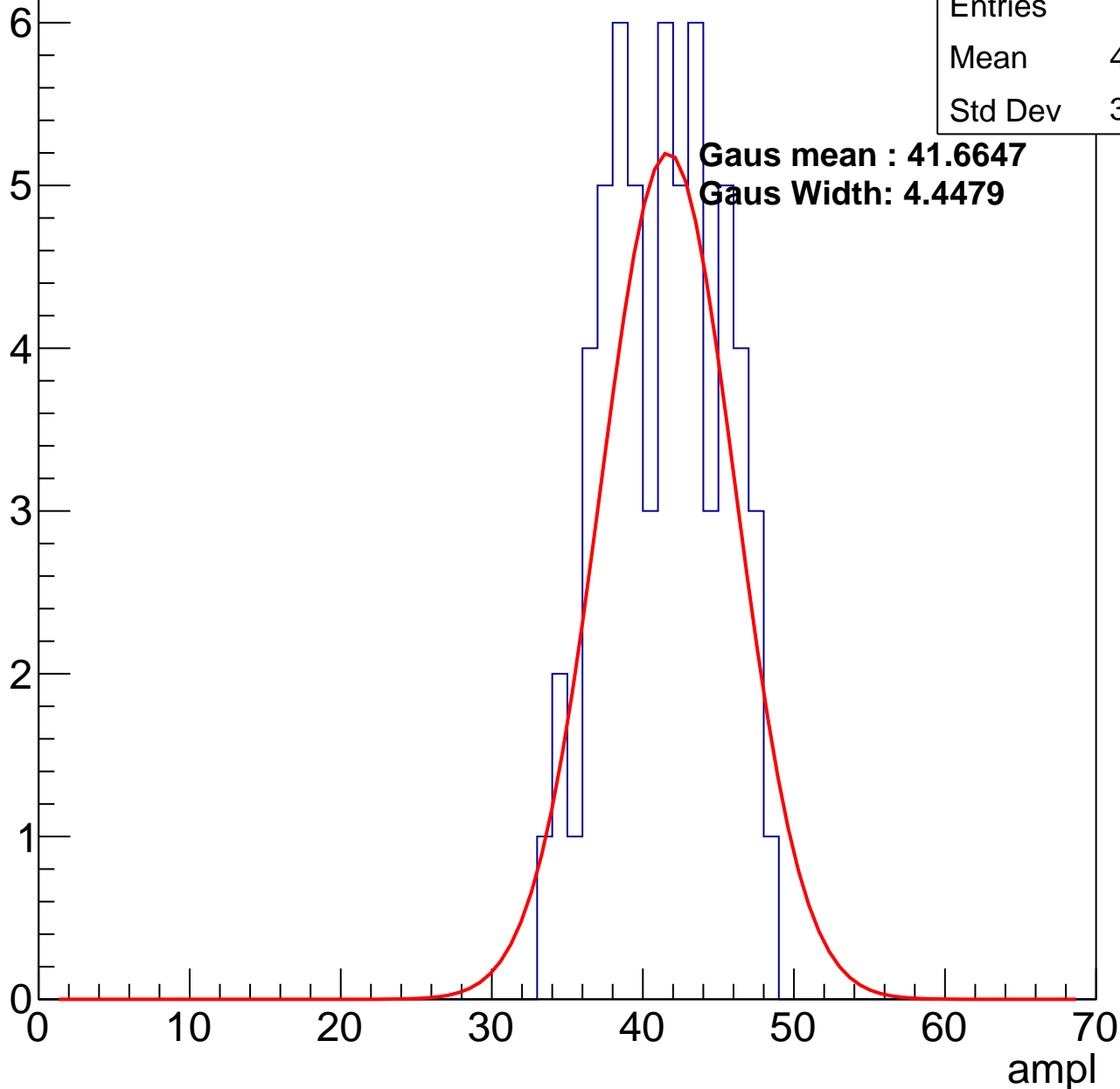
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	40.87
Std Dev	3.757

Gaus mean : 41.6647

Gaus Width: 4.4479

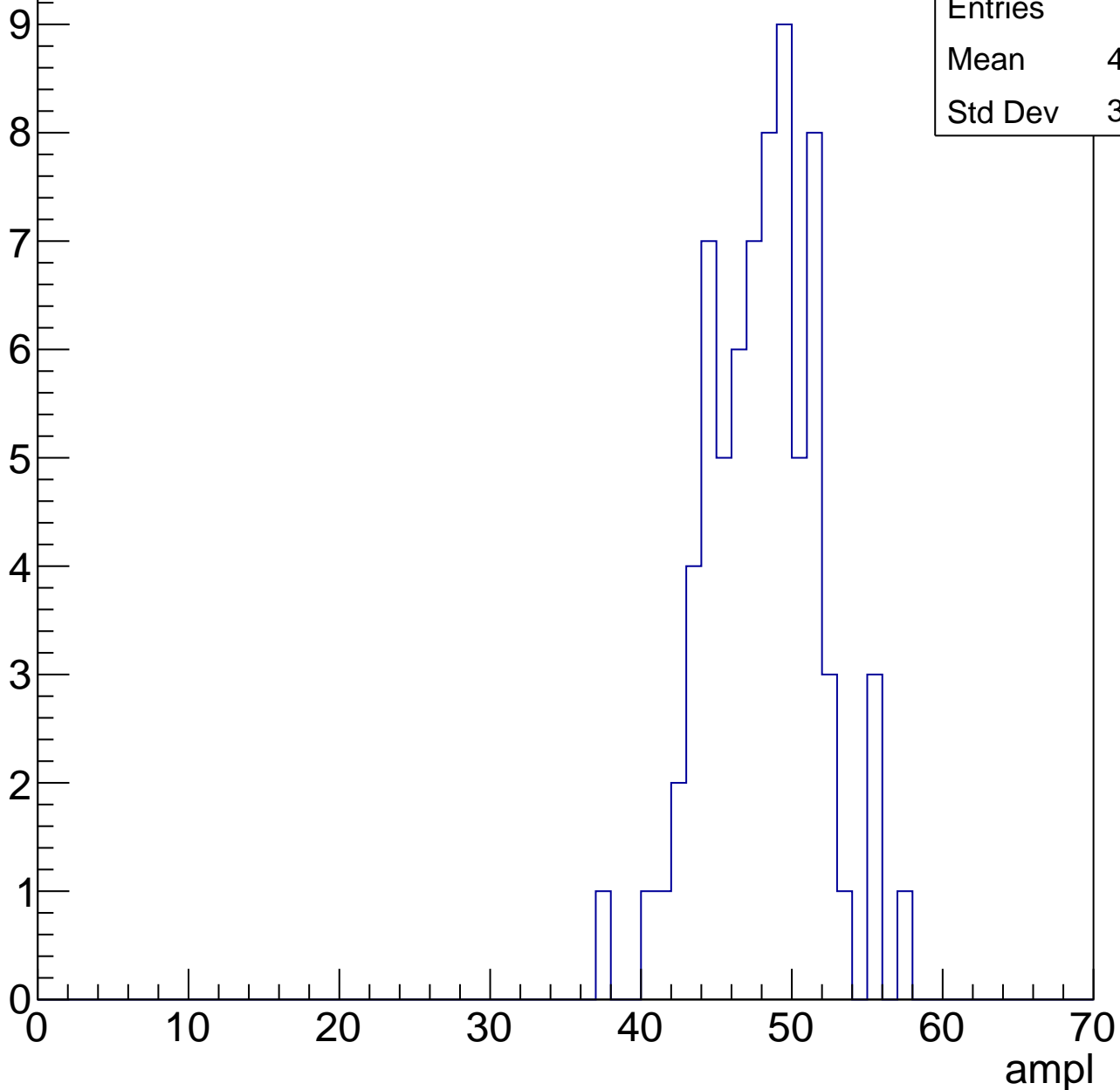


B1L102S, U9-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	47.58
Std Dev	3.696

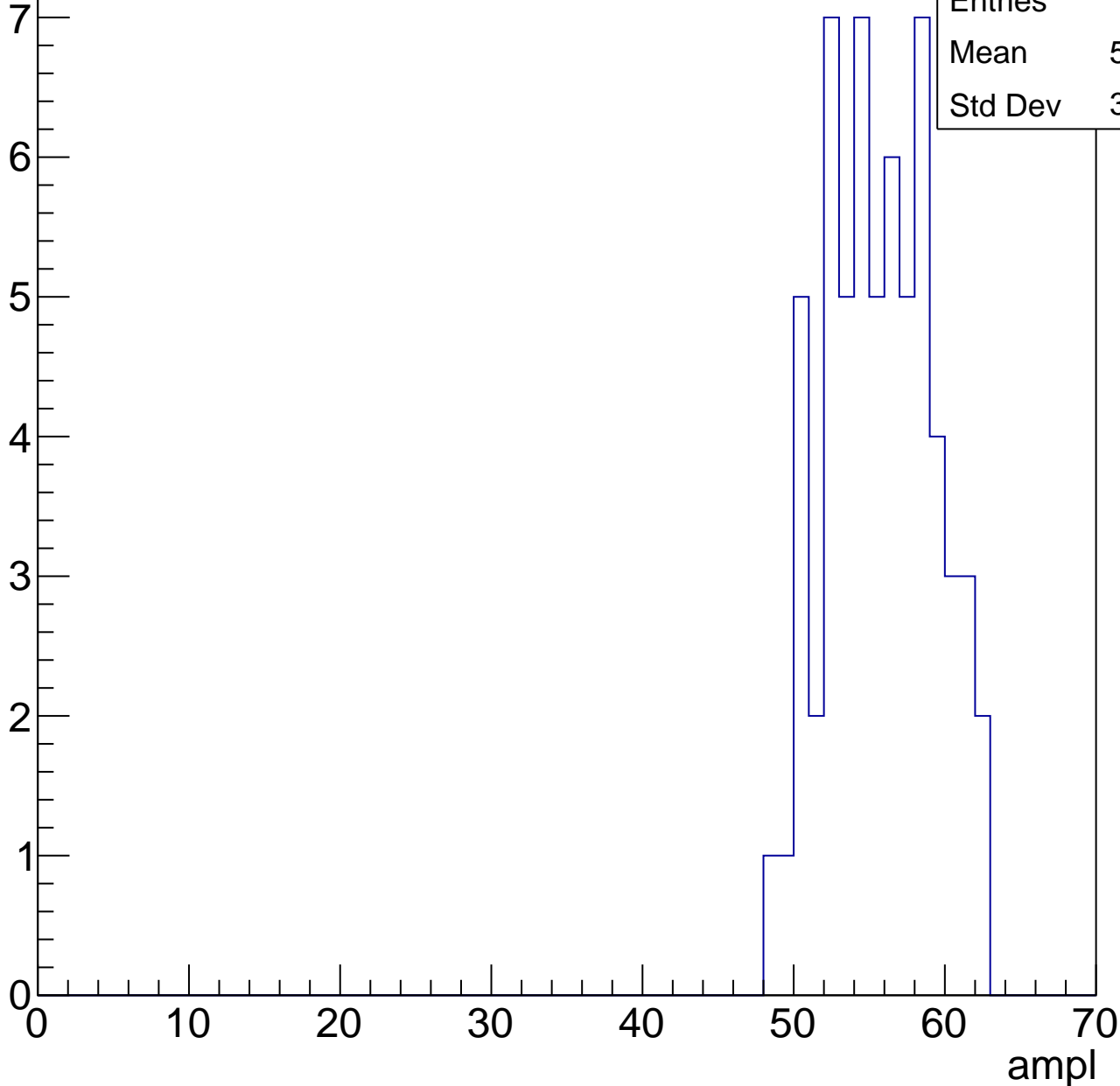


B1L102S, U9-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.25
Std Dev	3.478

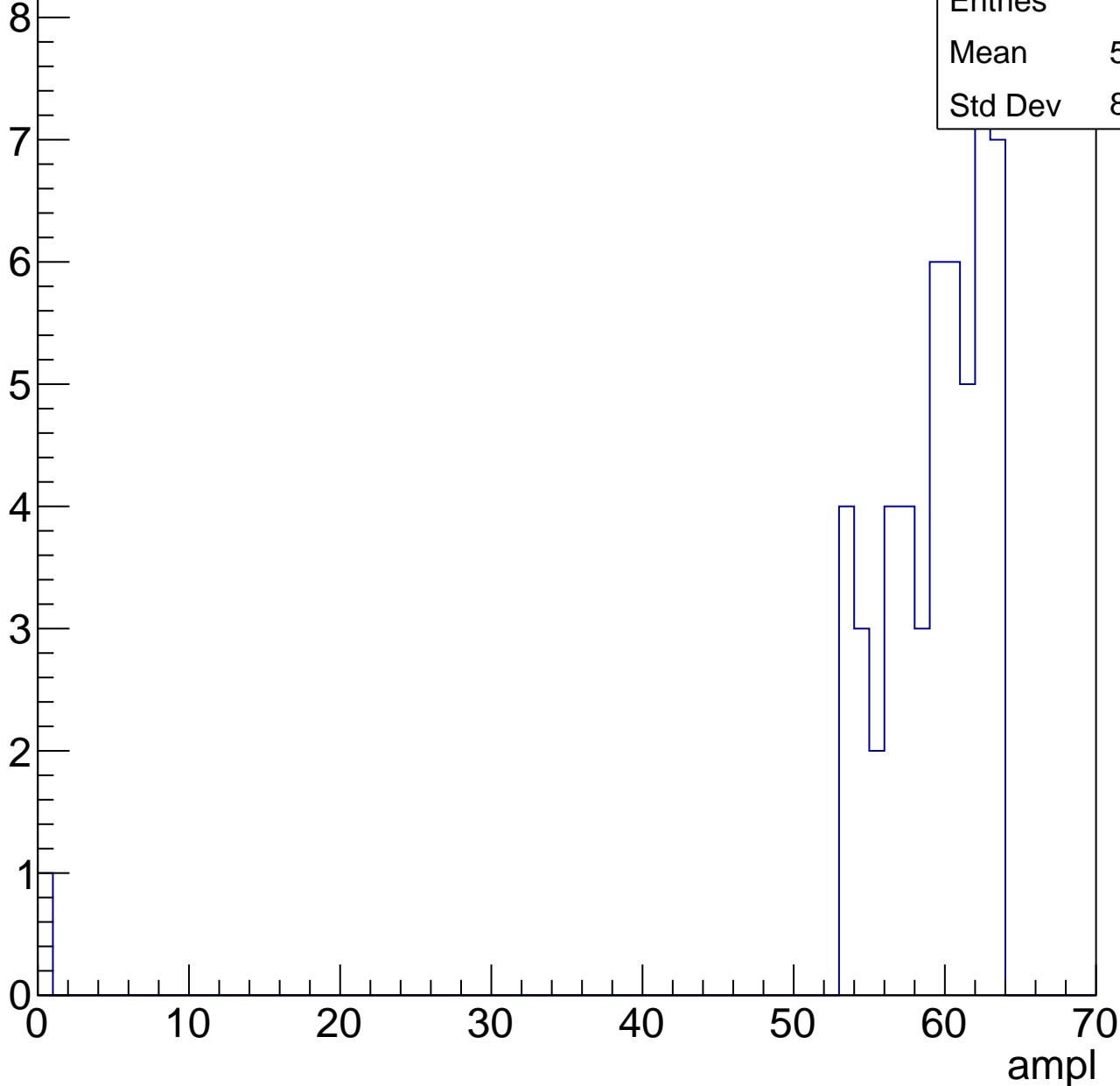


B1L102S, U9-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.85
Std Dev	8.608



B1L102S, U9-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

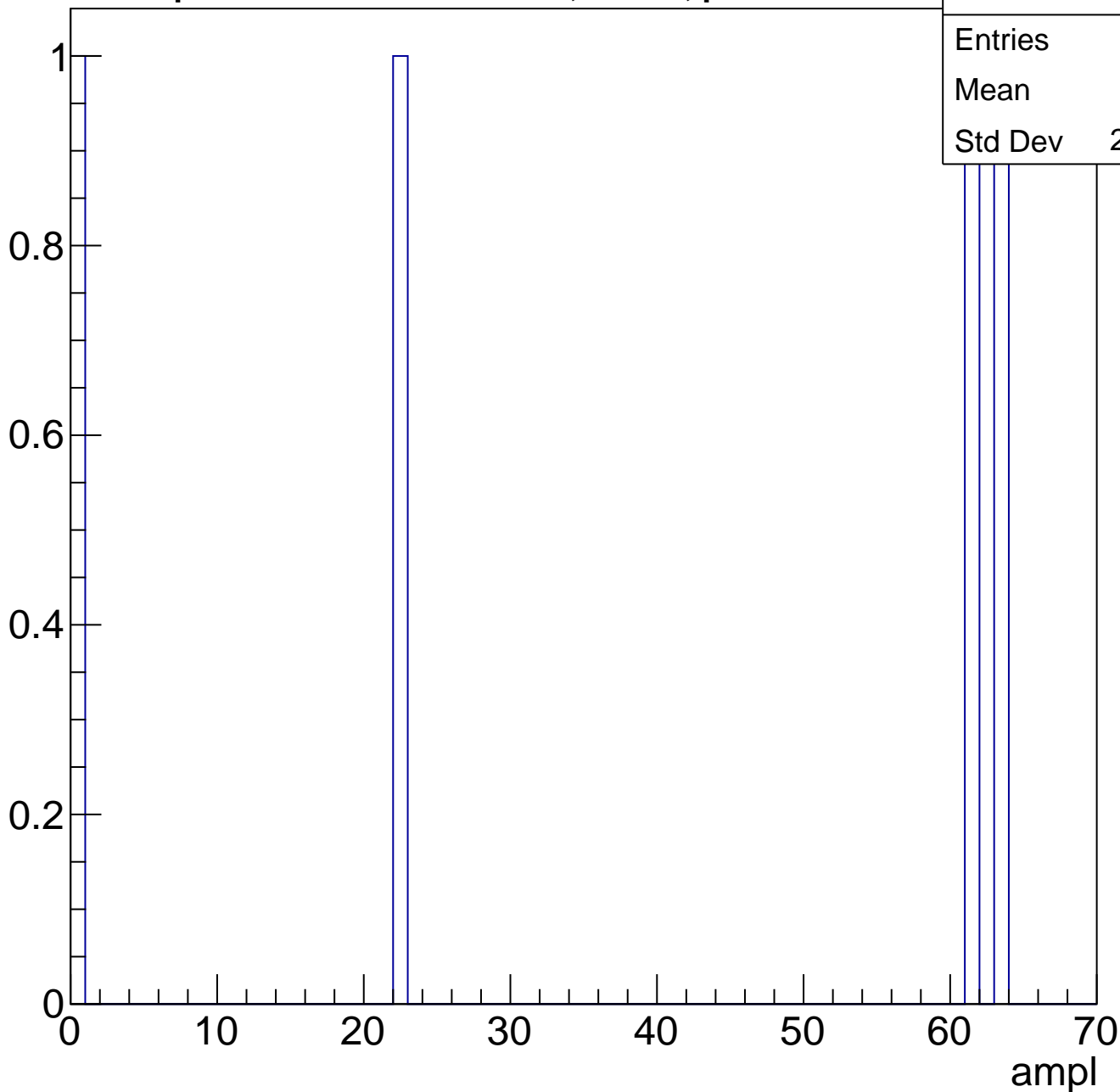
Entry



B1L102S, U9-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	36.5
Std Dev	26.67

B1L102S, U9-ch104, adc0

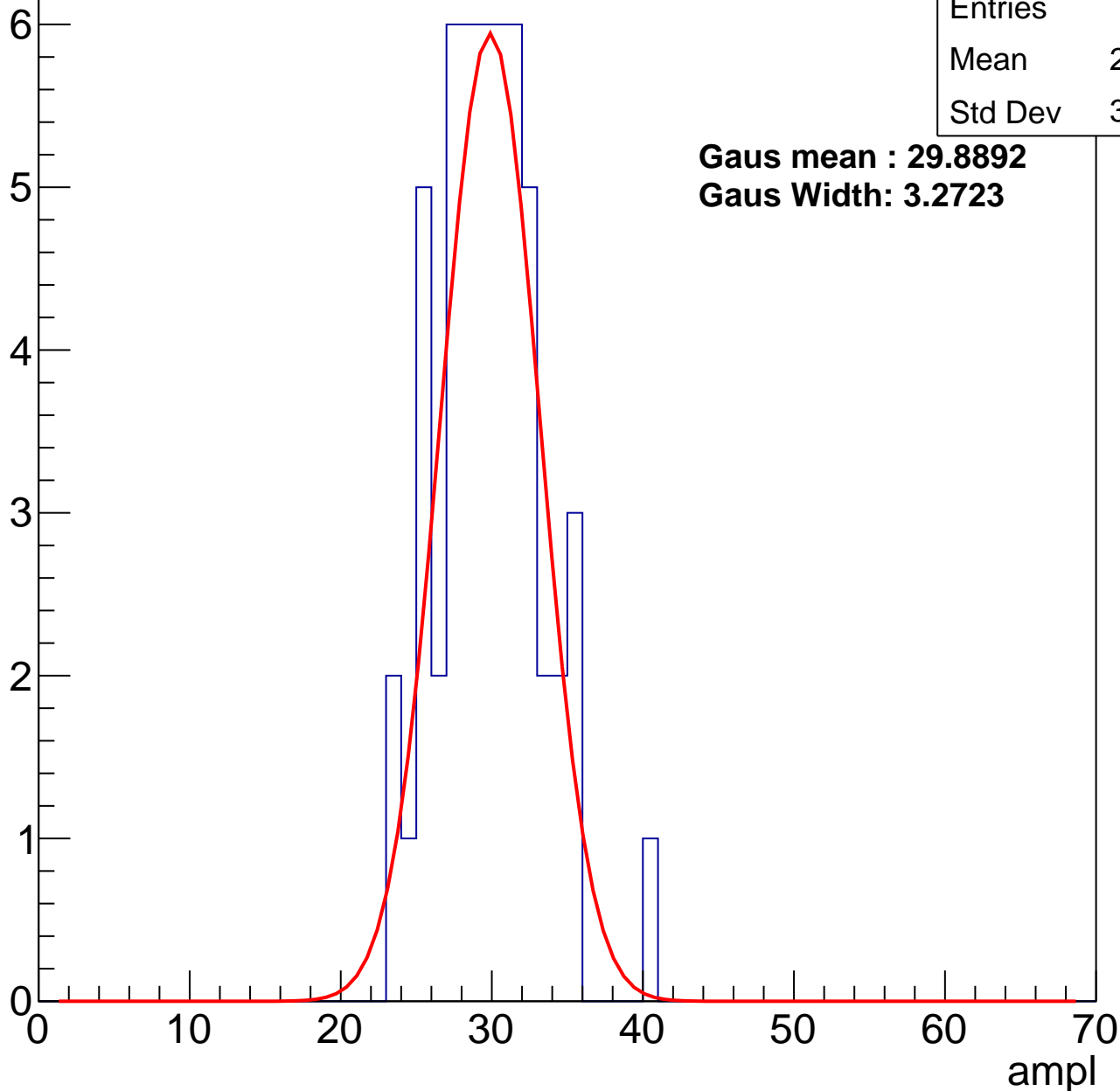
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	29.36
Std Dev	3.376

Gaus mean : 29.8892

Gaus Width: 3.2723



B1L102S, U9-ch104, adc1

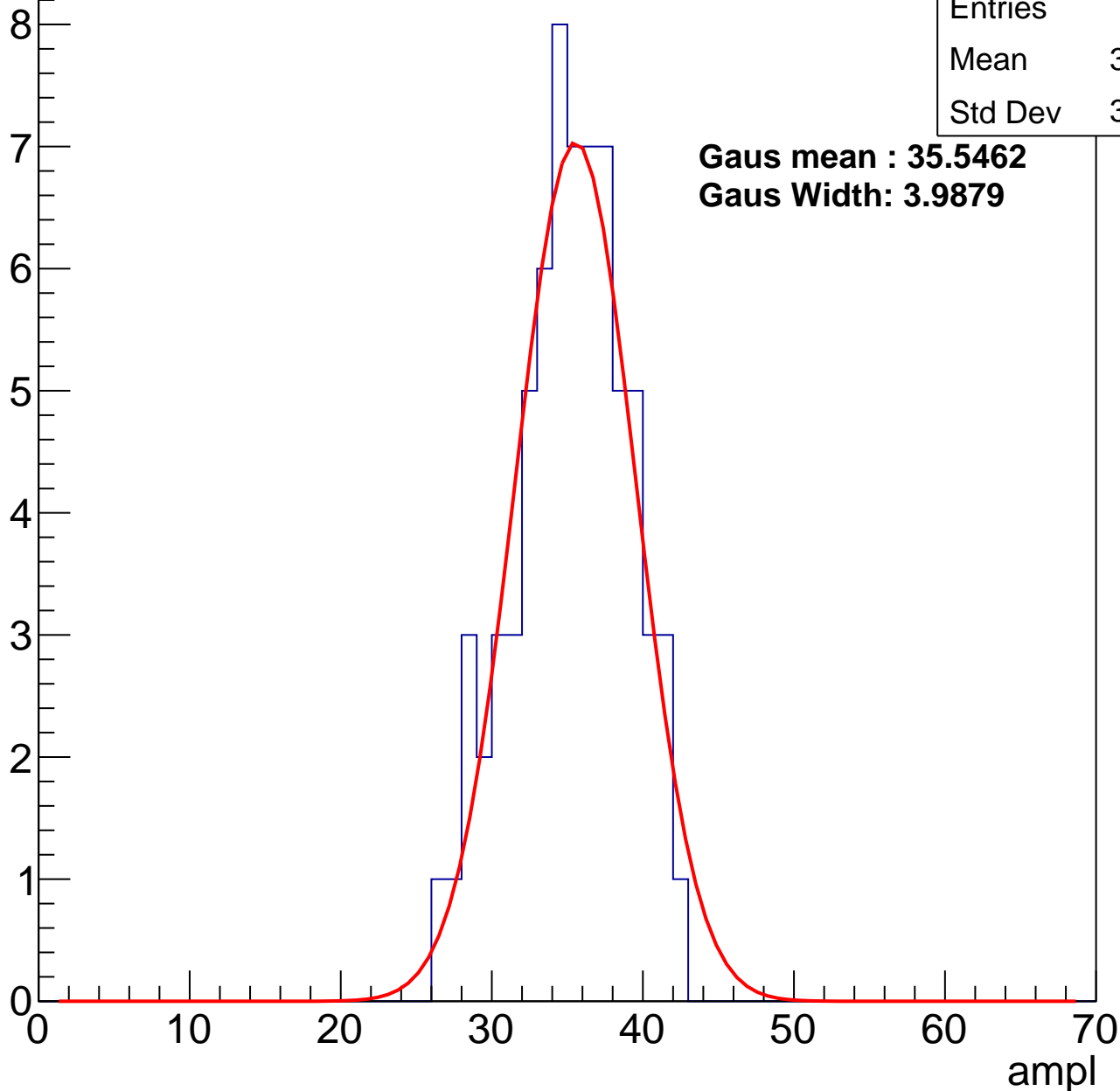
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.77
Std Dev	3.688

Gaus mean : 35.5462

Gaus Width: 3.9879



B1L102S, U9-ch104, adc2

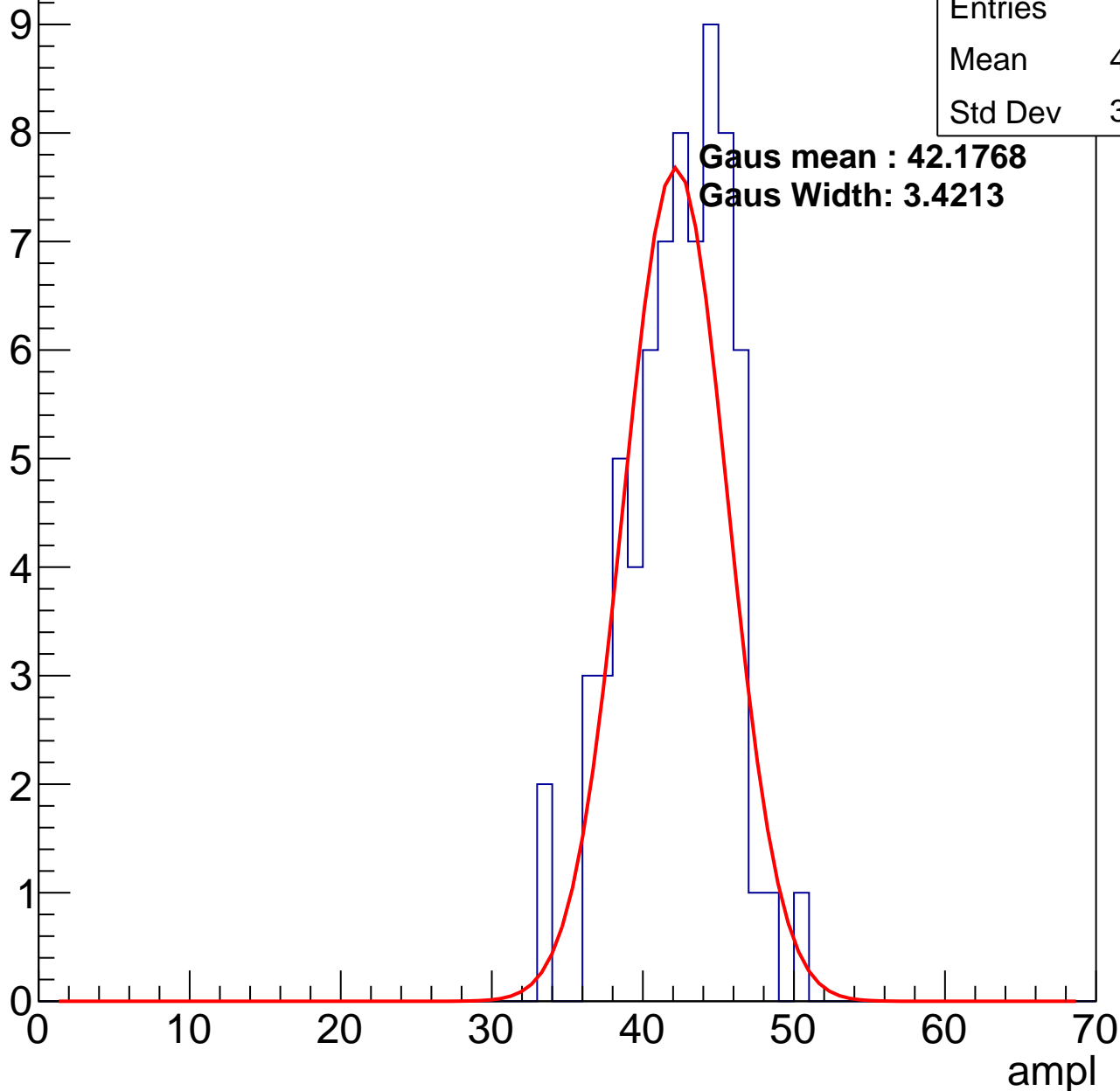
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.86
Std Dev	3.412

Gaus mean : 42.1768

Gaus Width: 3.4213

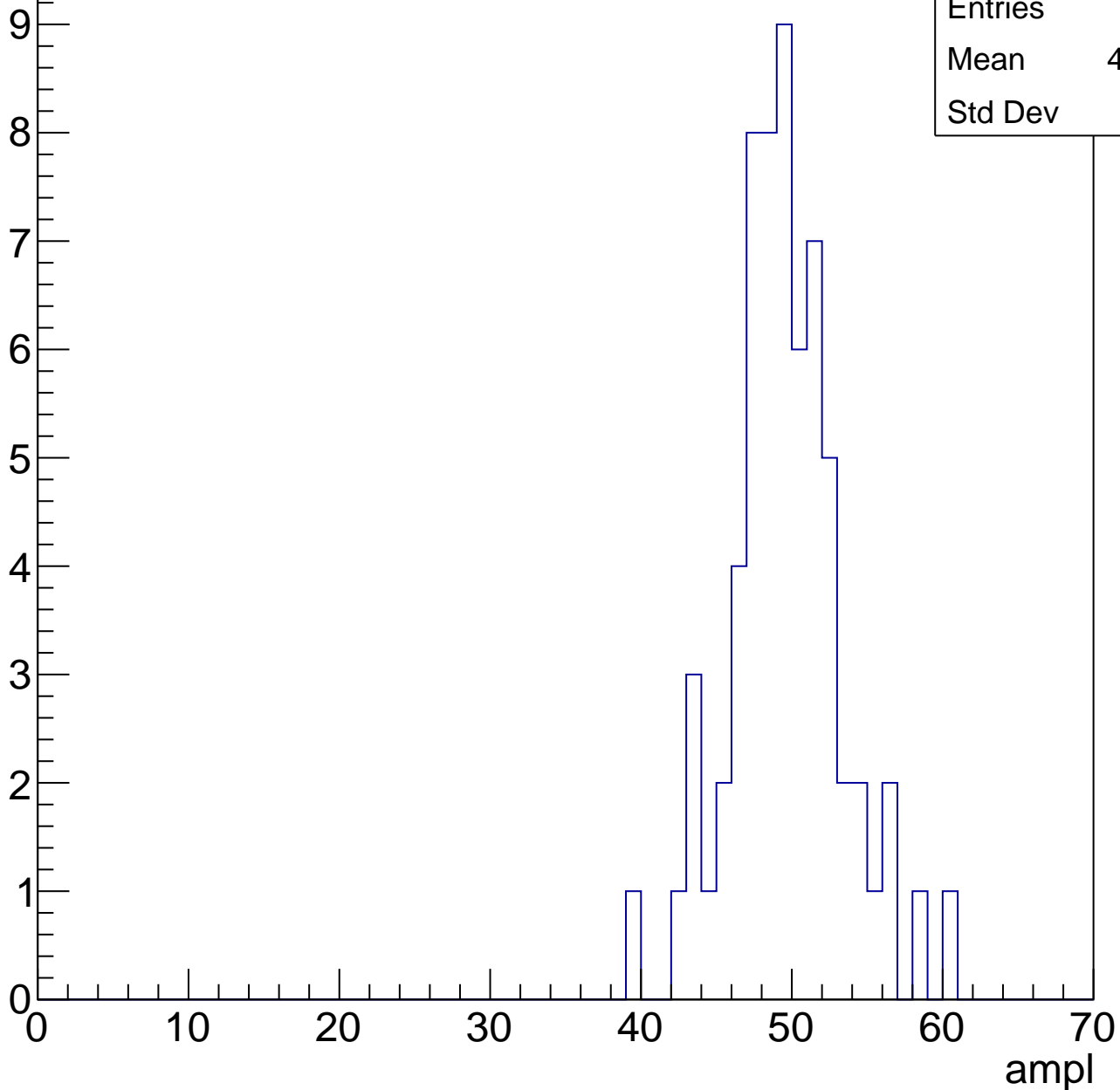


B1L102S, U9-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

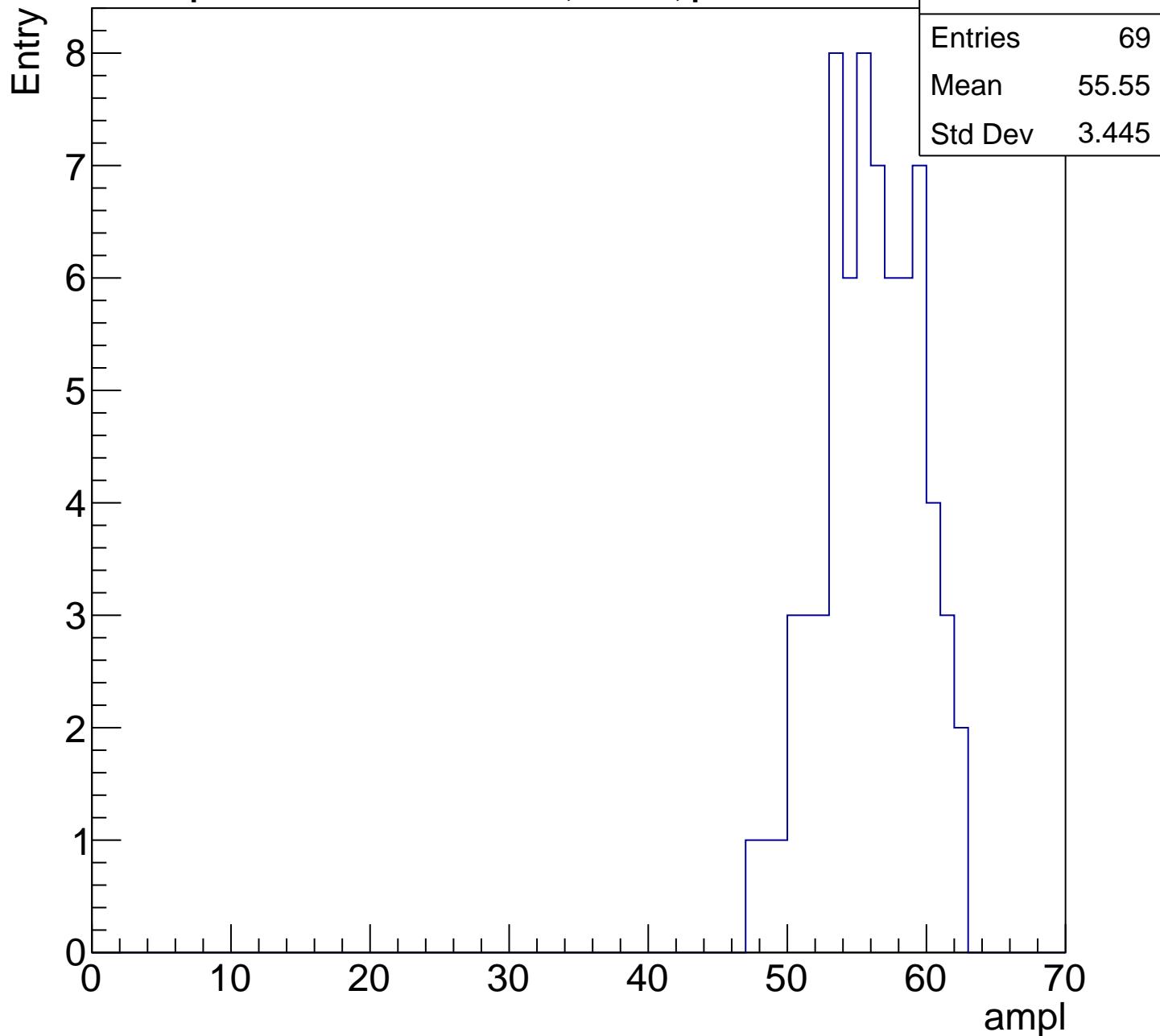
Entry

Entries	64
Mean	49.14
Std Dev	3.72



B1L102S, U9-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

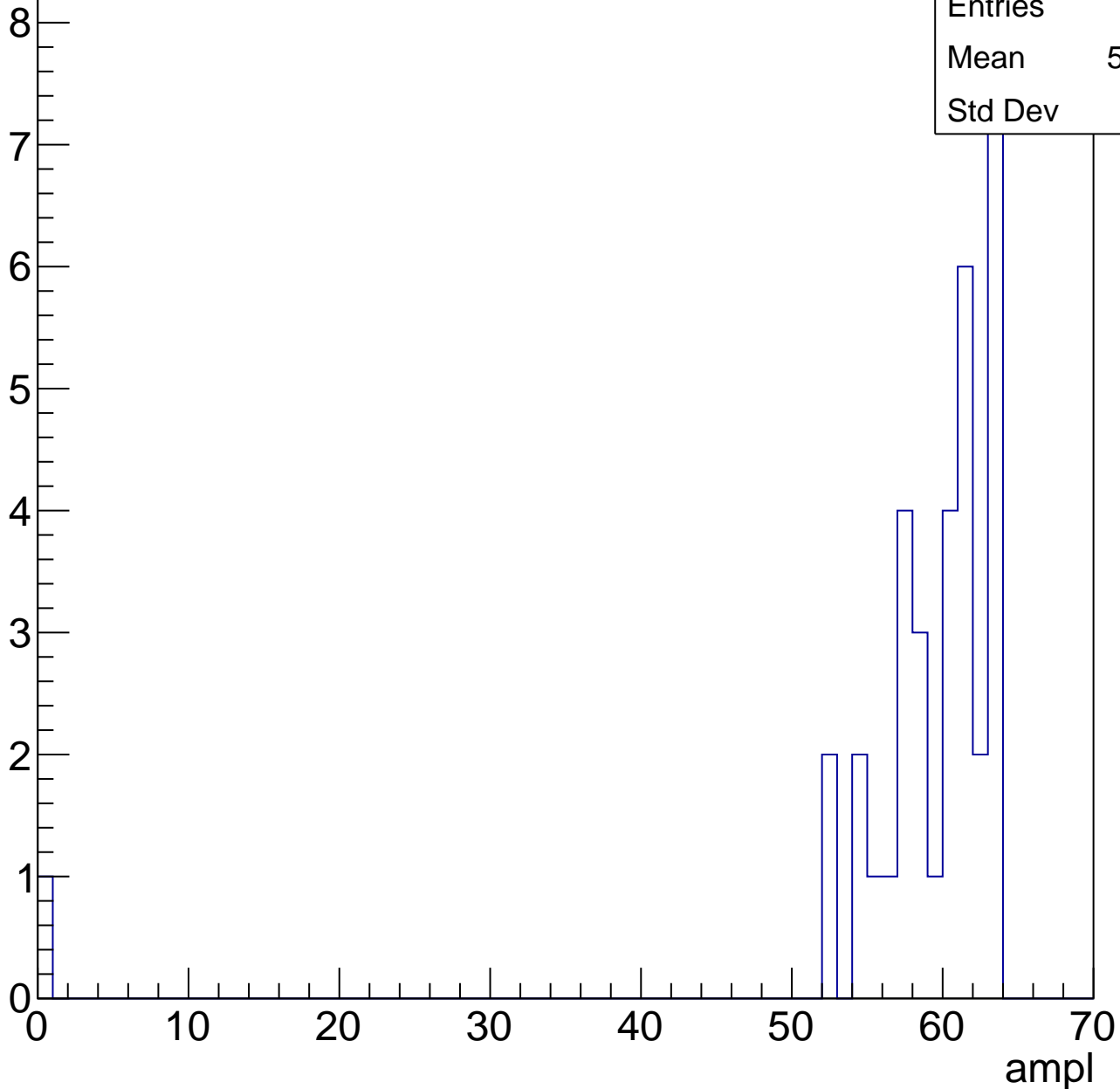


B1L102S, U9-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	57.66
Std Dev	10.4

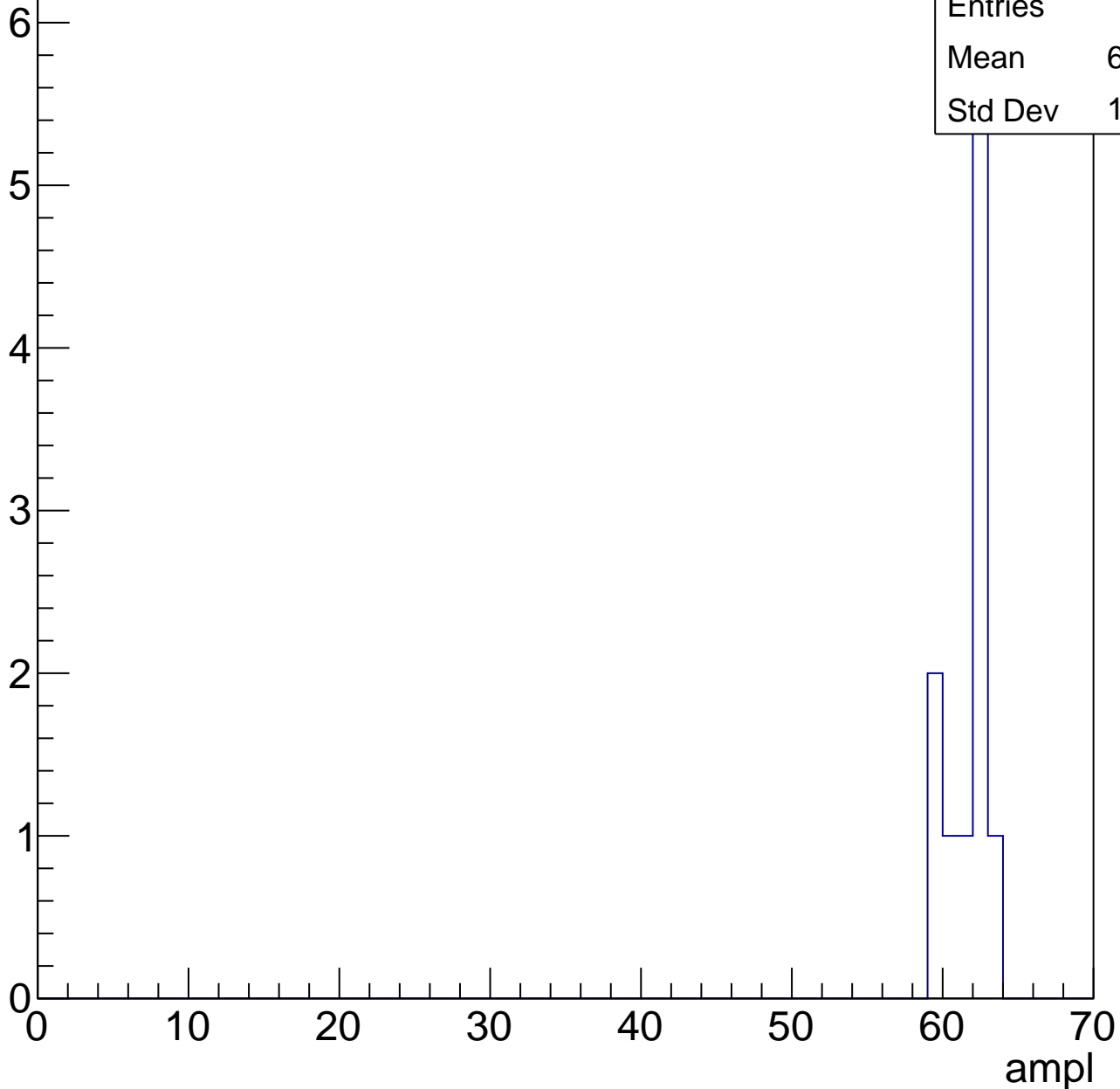


B1L102S, U9-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

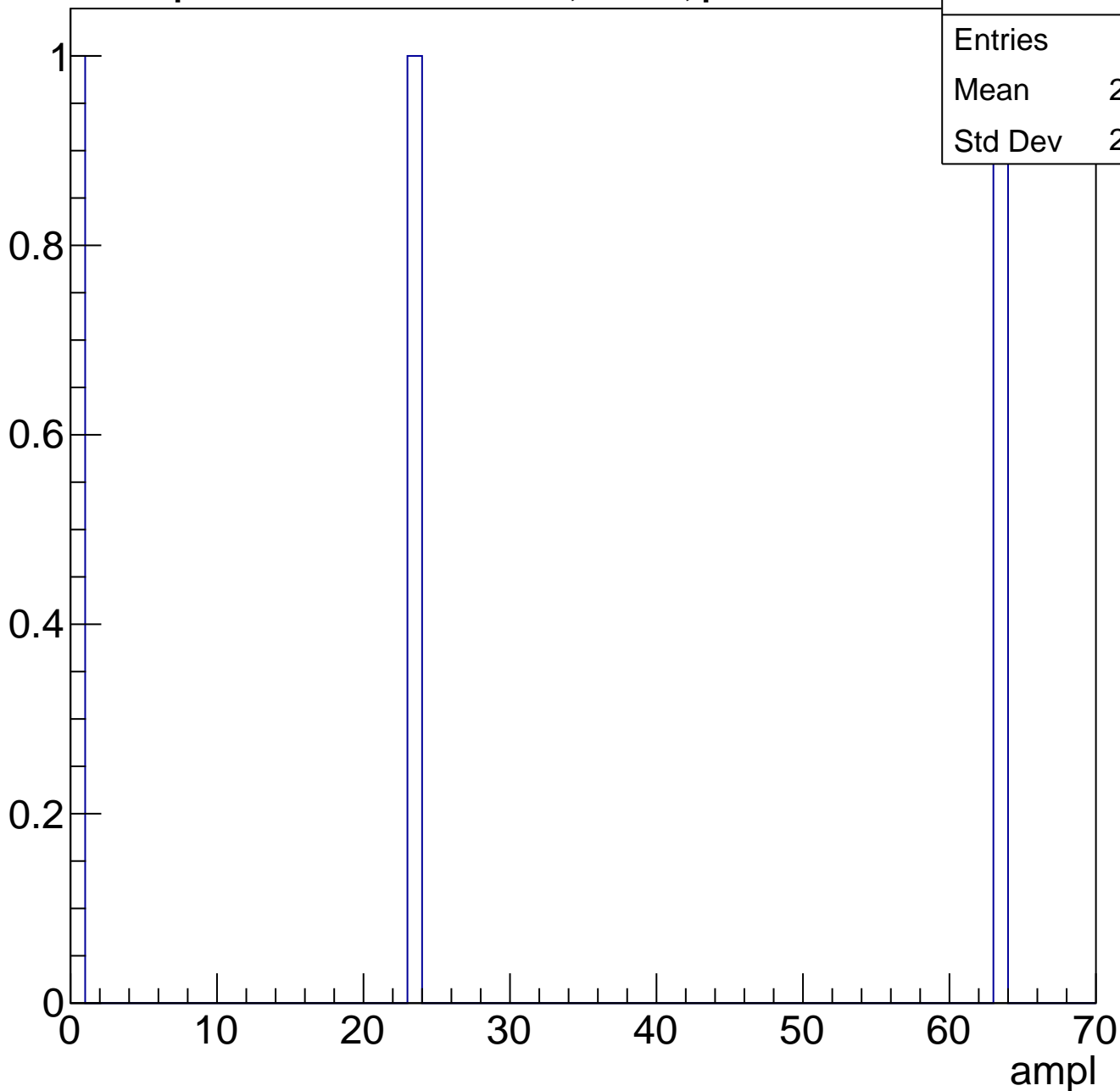
Entries	11
Mean	61.27
Std Dev	1.286



B1L102S, U9-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	28.67
Std Dev	26.03

B1L102S, U9-ch105, adc0

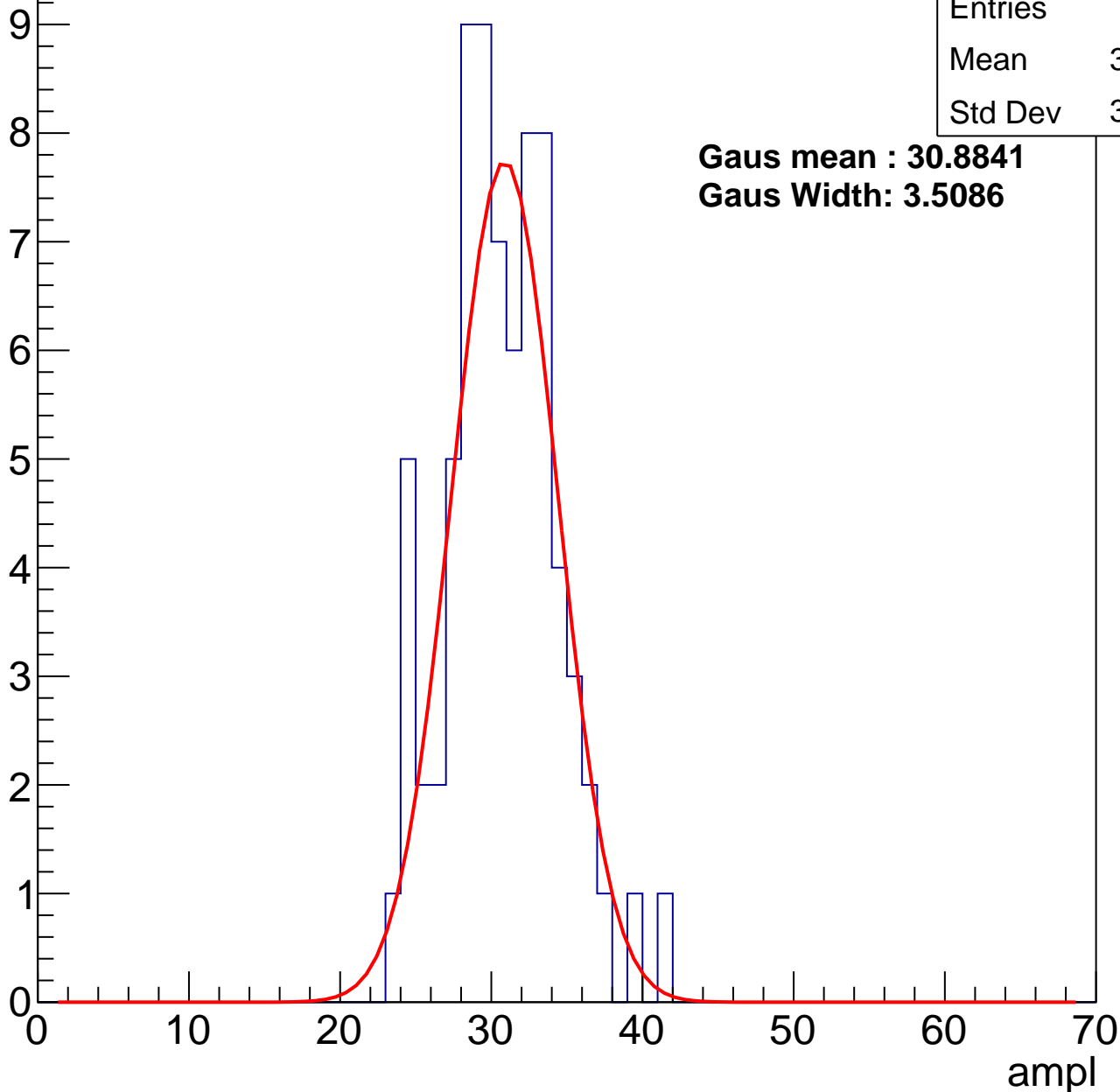
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	30.26
Std Dev	3.617

Gaus mean : 30.8841

Gaus Width: 3.5086



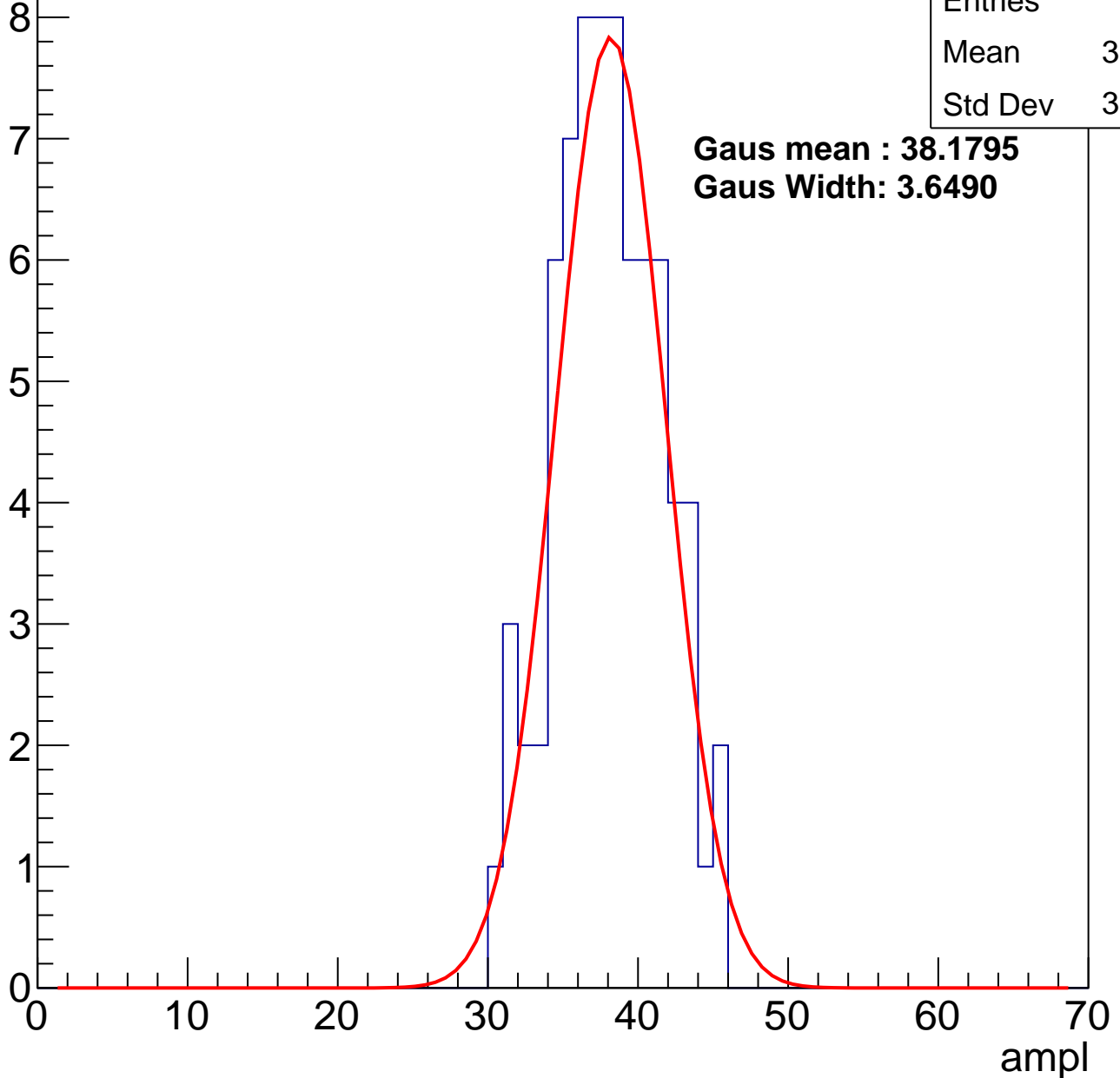
B1L102S, U9-ch105, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	37.62
Std Dev	3.494

Gaus mean : 38.1795
Gaus Width: 3.6490



B1L102S, U9-ch105, adc2

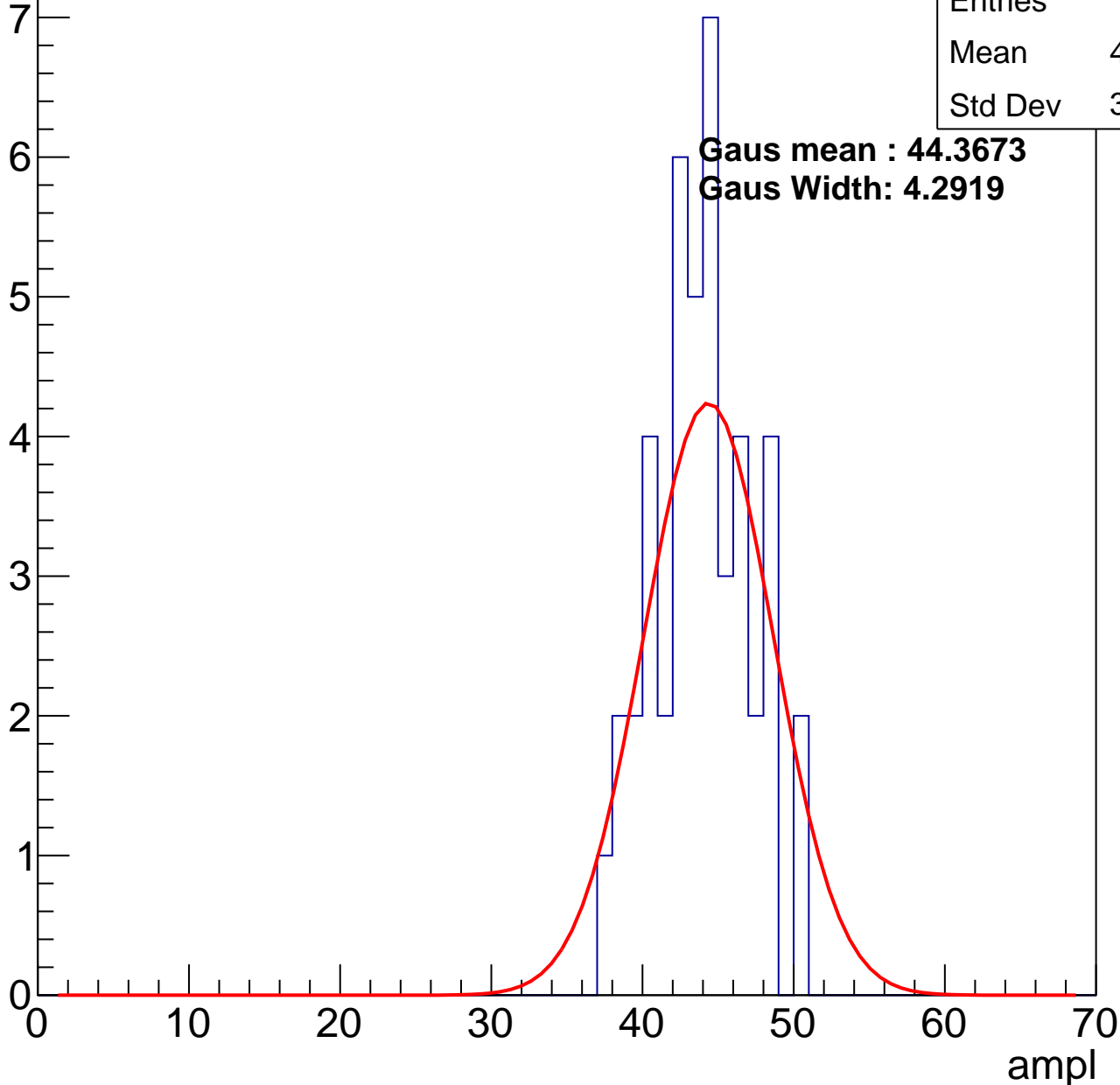
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	43.48
Std Dev	3.173

Gaus mean : 44.3673

Gaus Width: 4.2919

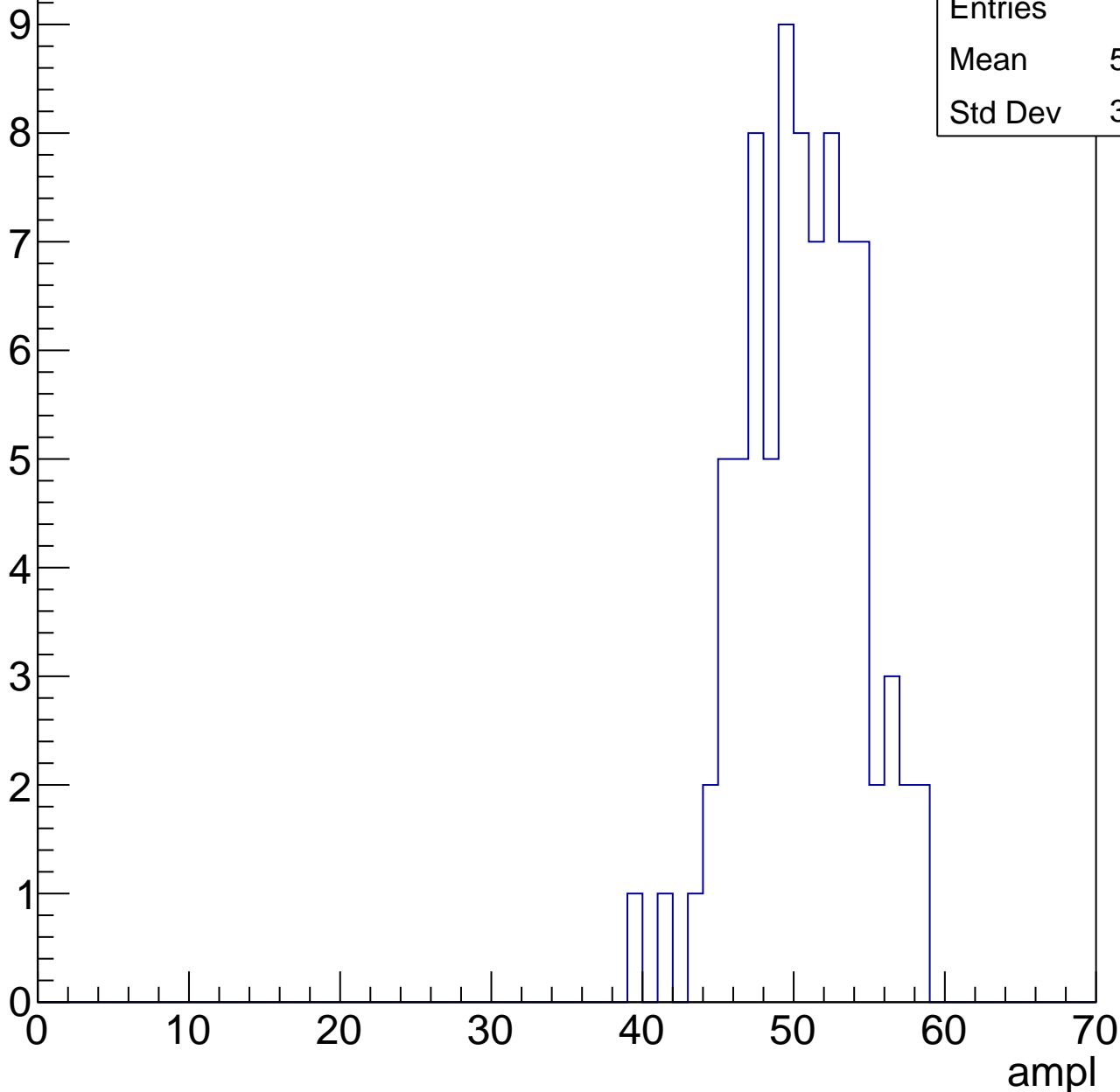


B1L102S, U9-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	50.04
Std Dev	3.845

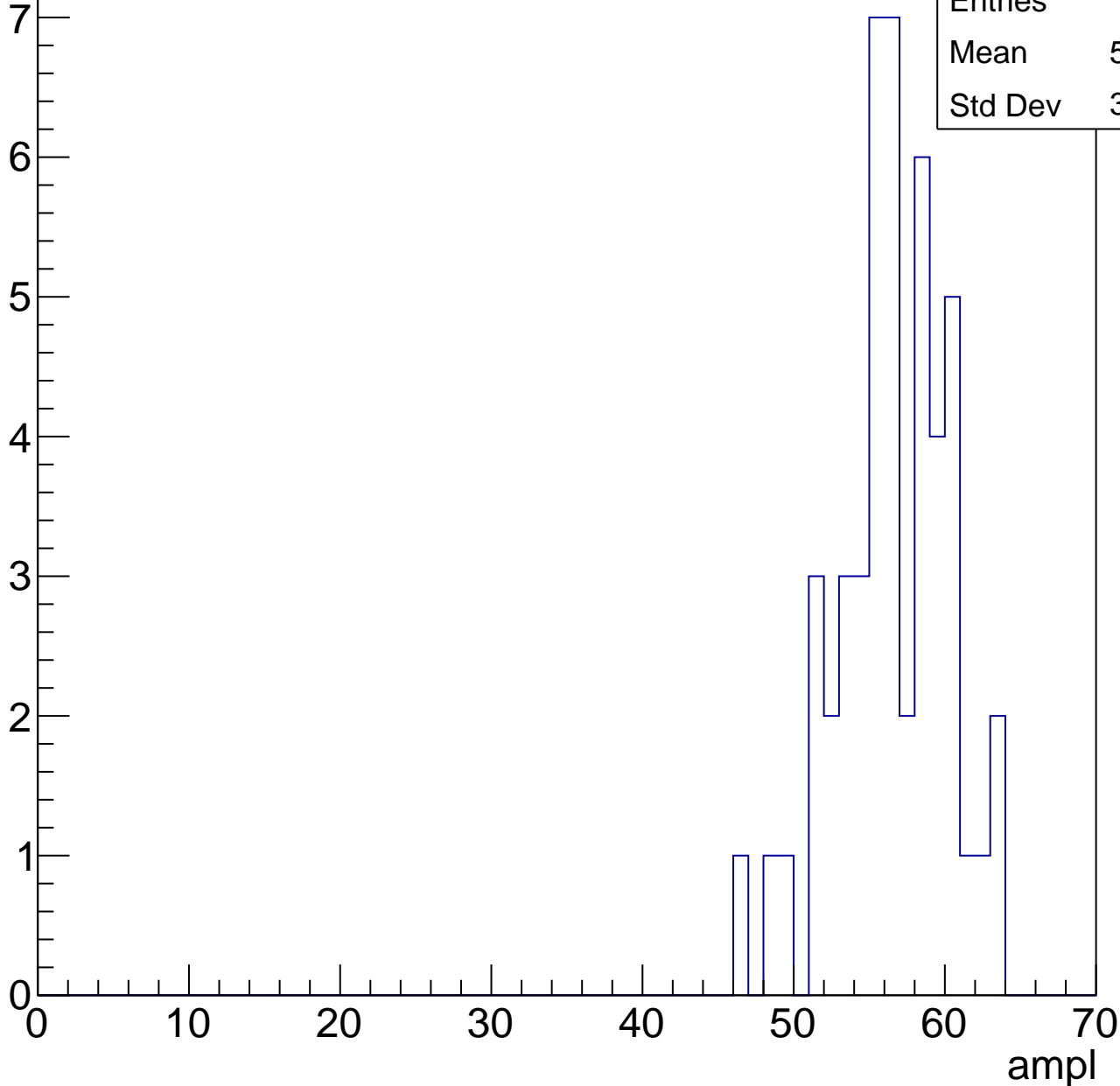


B1L102S, U9-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.02
Std Dev	3.689

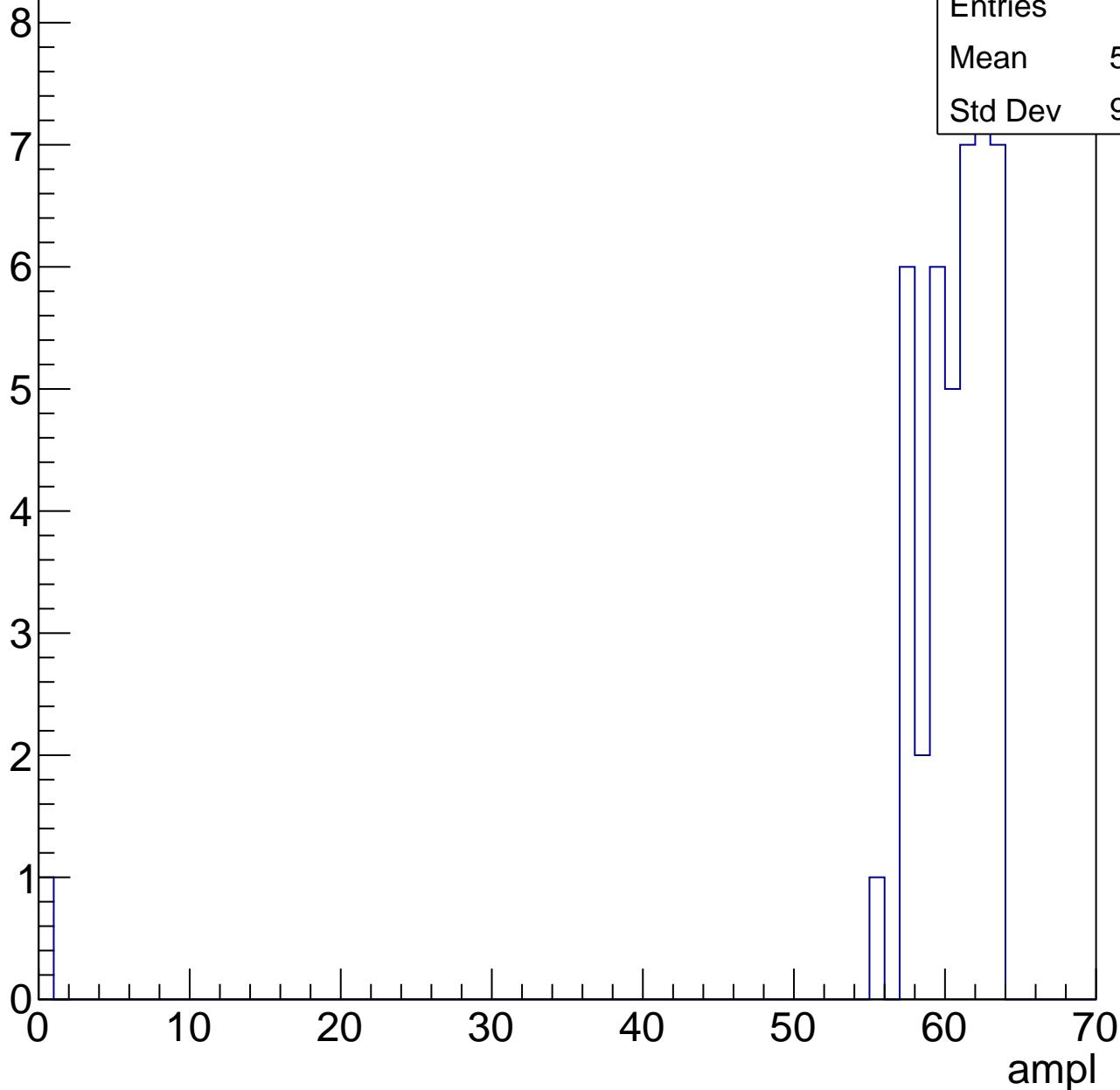


B1L102S, U9-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.86
Std Dev	9.325



B1L102S, U9-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

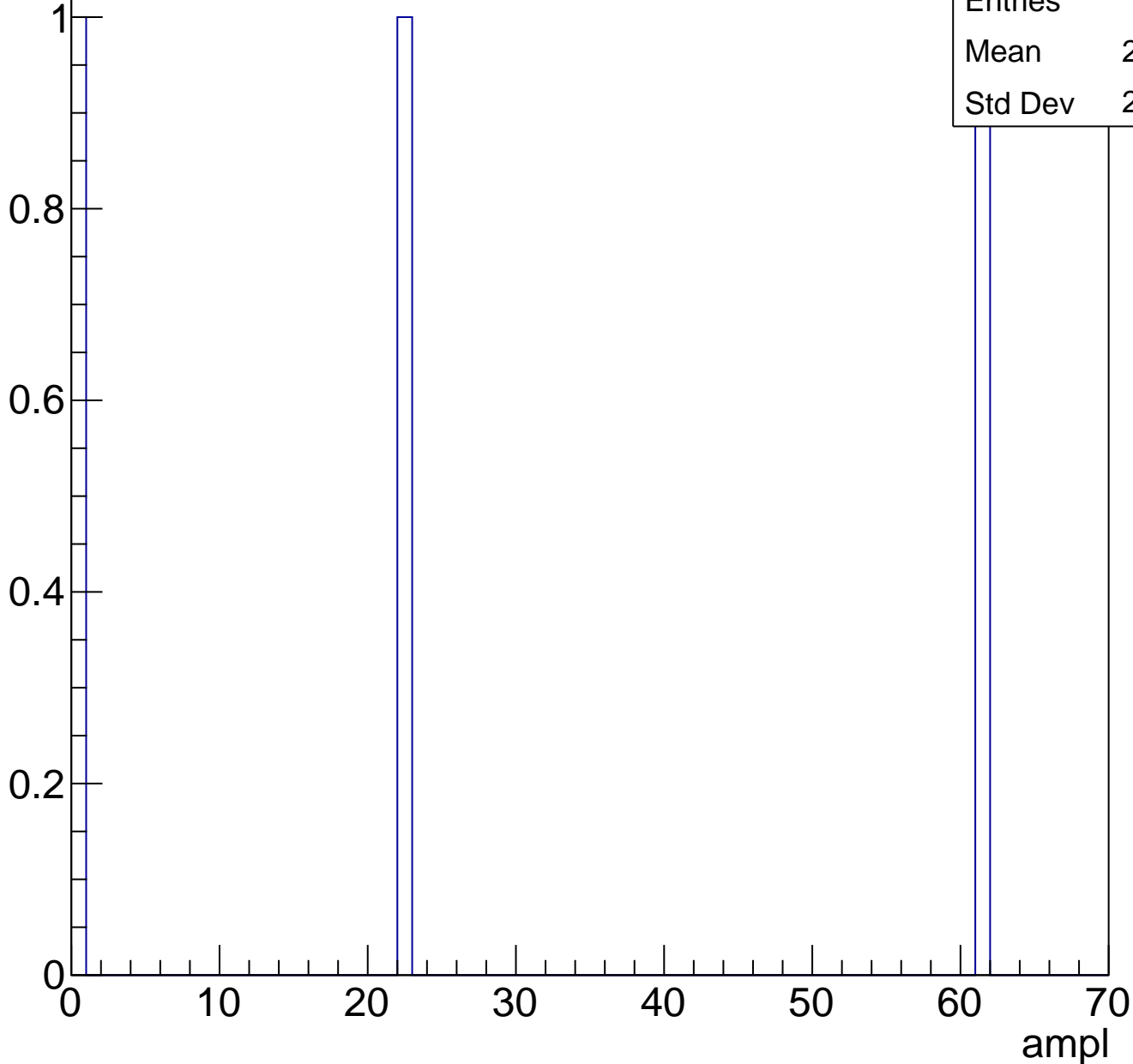
Entry



B1L102S, U9-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	27.67
Std Dev	25.22

B1L102S, U9-ch106, adc0

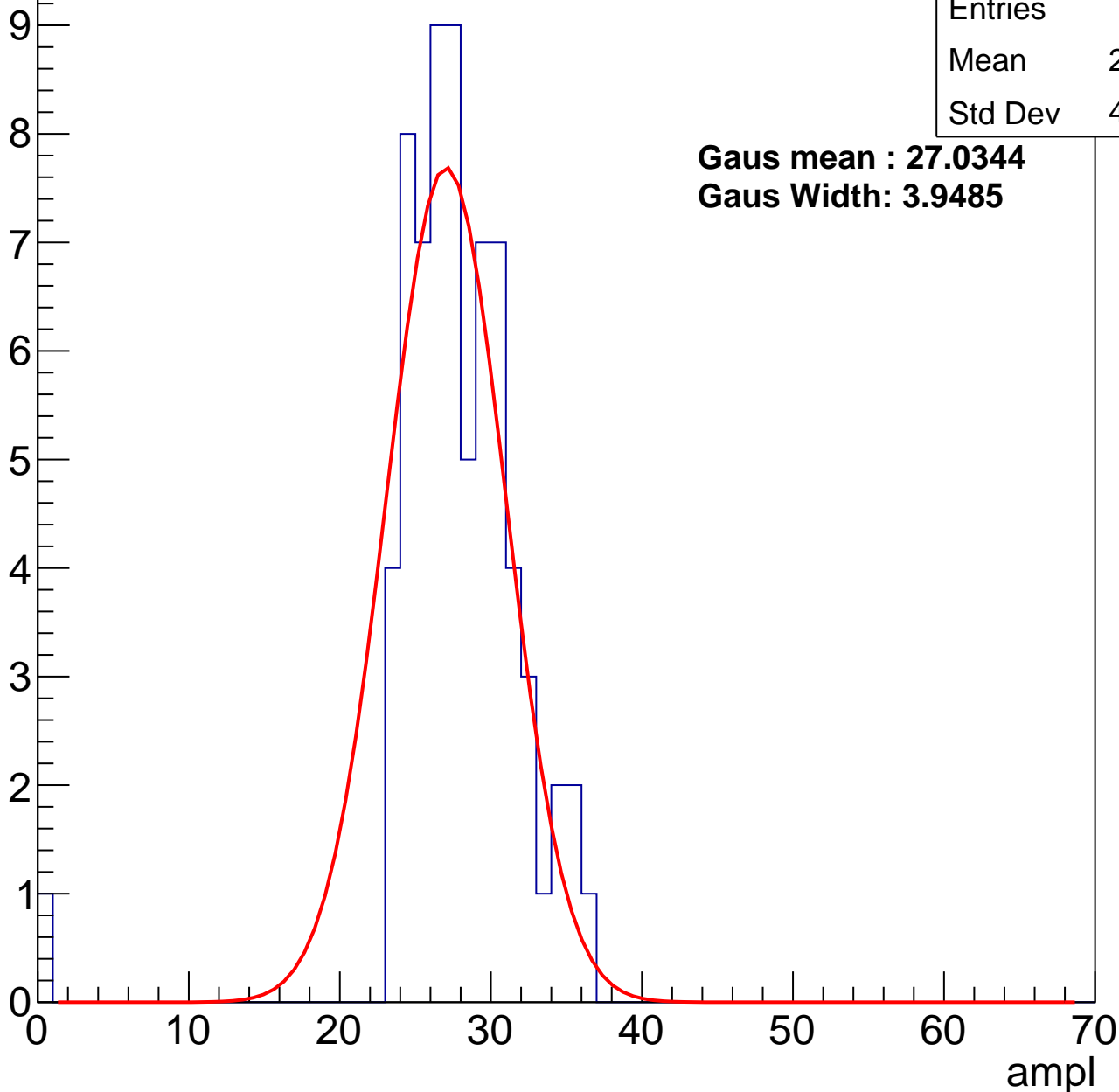
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.37
Std Dev	4.583

Gaus mean : 27.0344

Gaus Width: 3.9485



B1L102S, U9-ch106, adc1

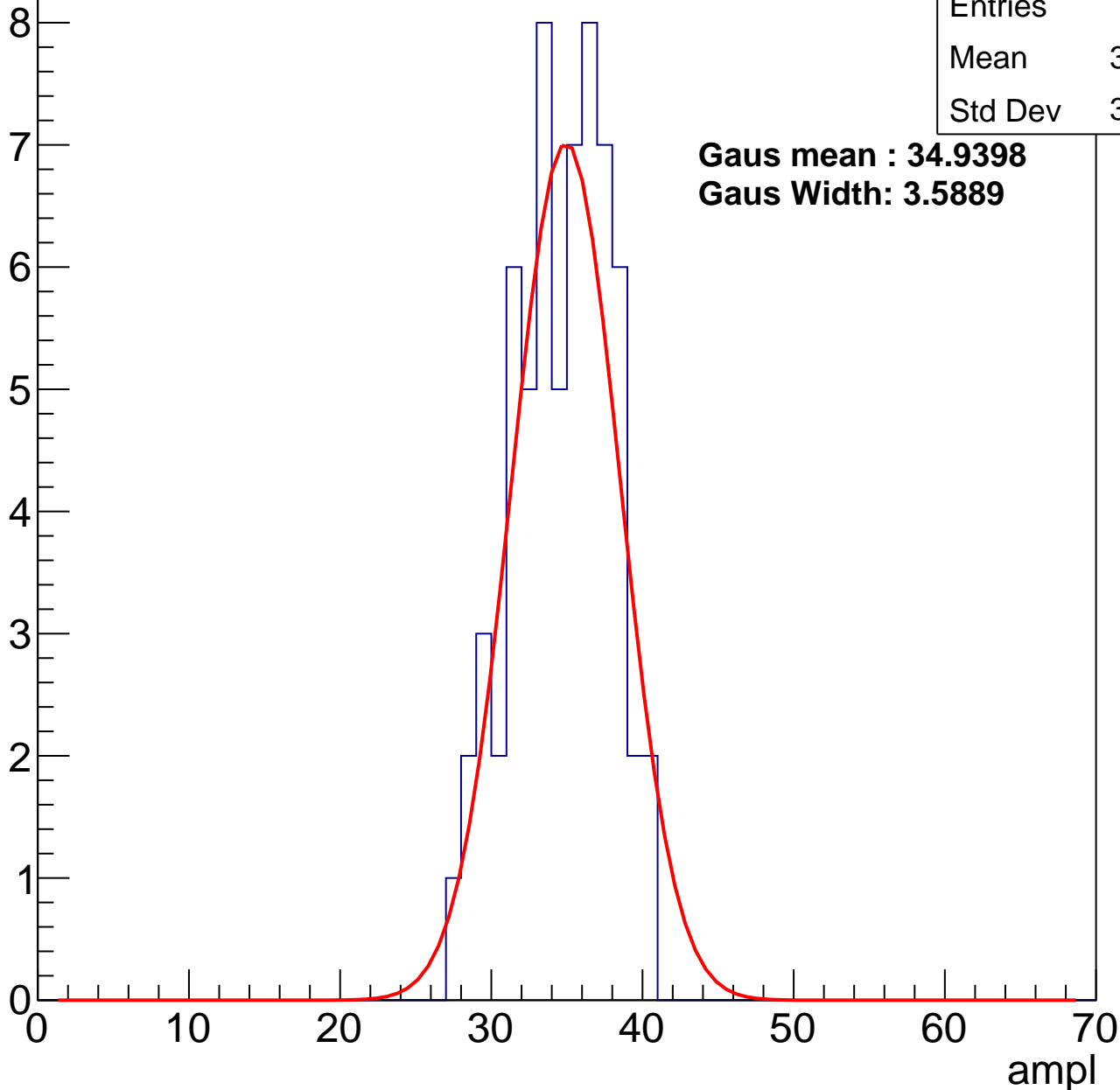
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	34.19
Std Dev	3.132

Gaus mean : 34.9398

Gaus Width: 3.5889



B1L102S, U9-ch106, adc2

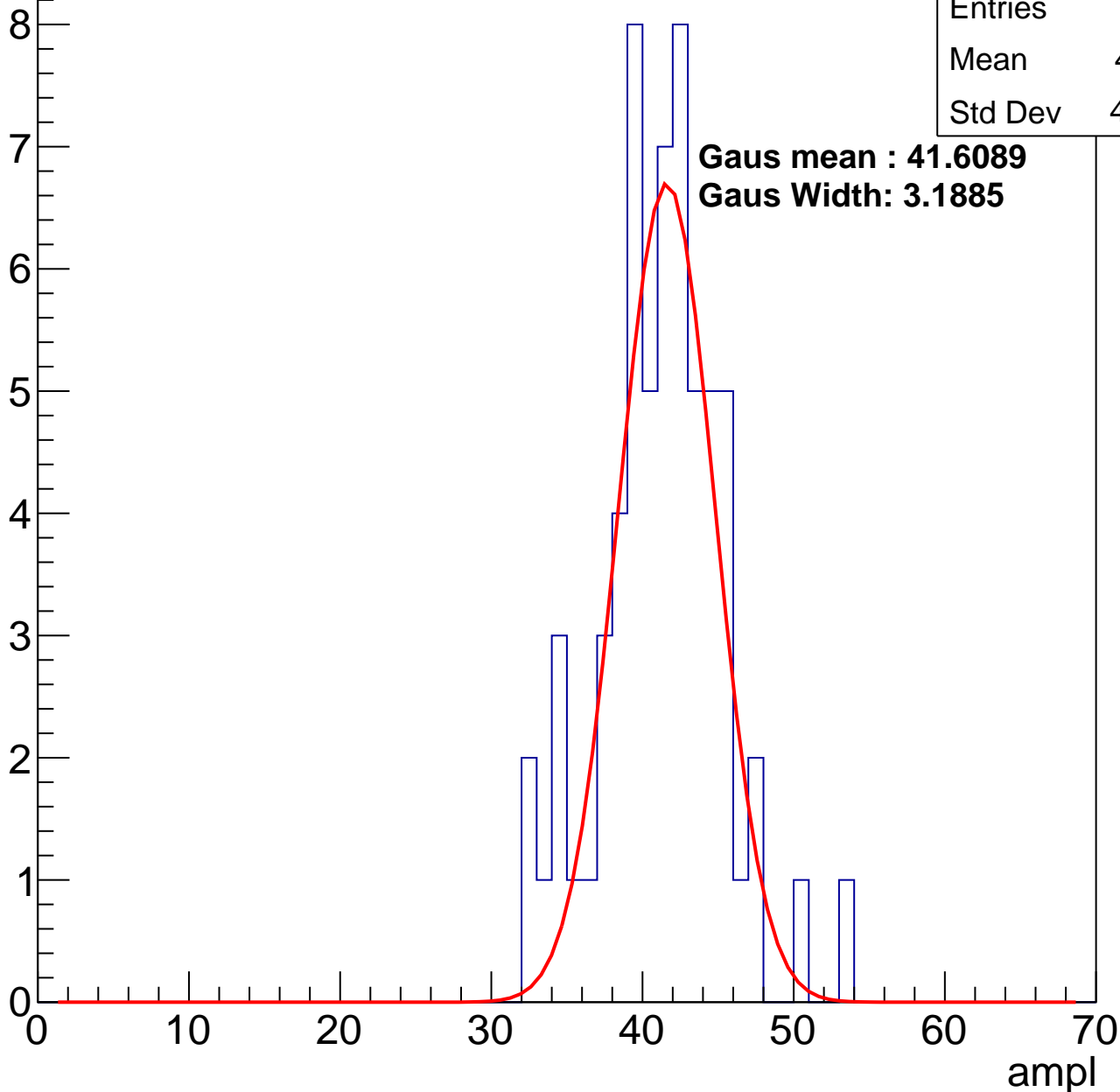
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	40.81
Std Dev	4.039

Gaus mean : 41.6089

Gaus Width: 3.1885

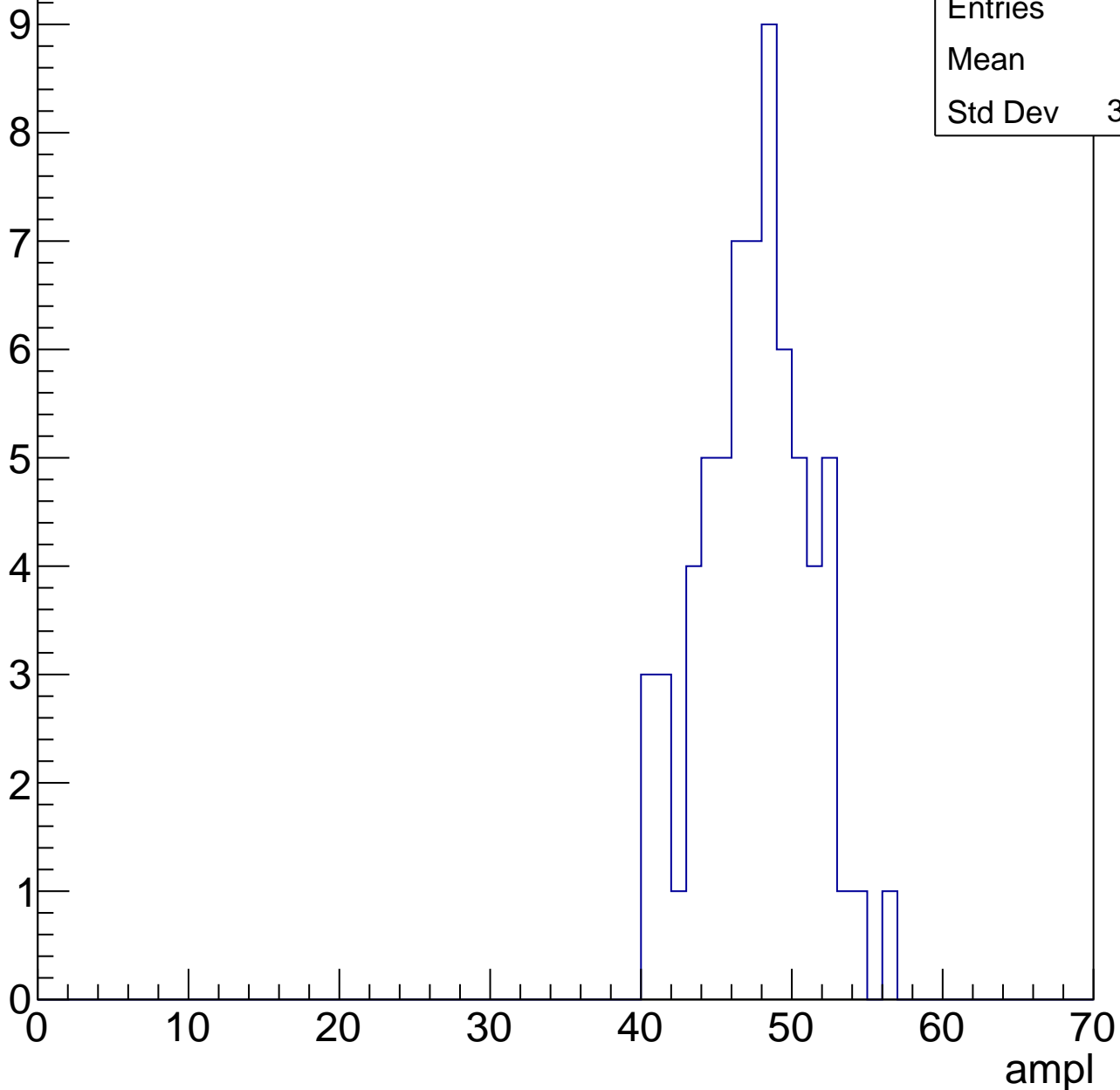


B1L102S, U9-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	47.1
Std Dev	3.558

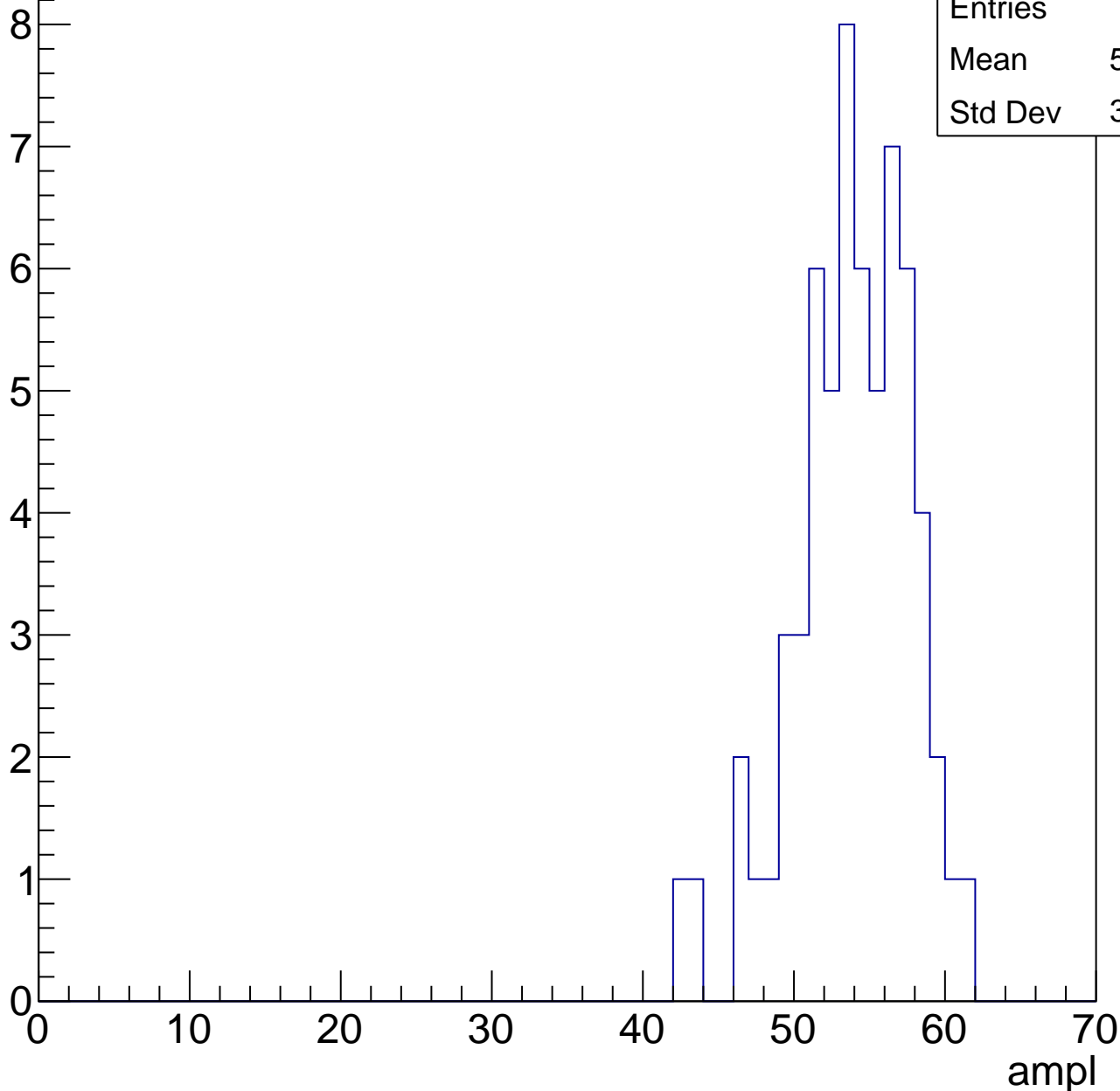


B1L102S, U9-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.38
Std Dev	3.873

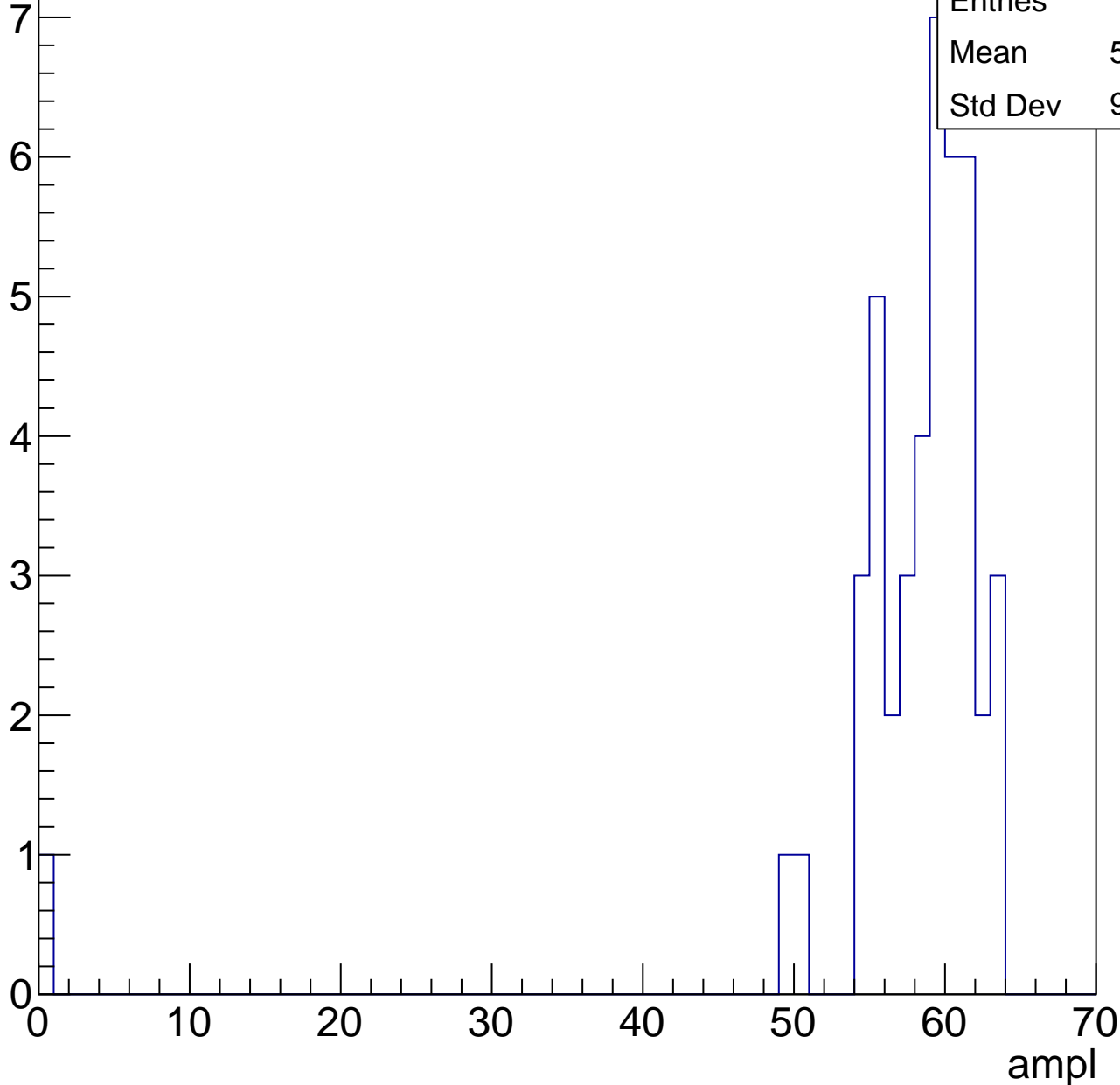


B1L102S, U9-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	56.89
Std Dev	9.232

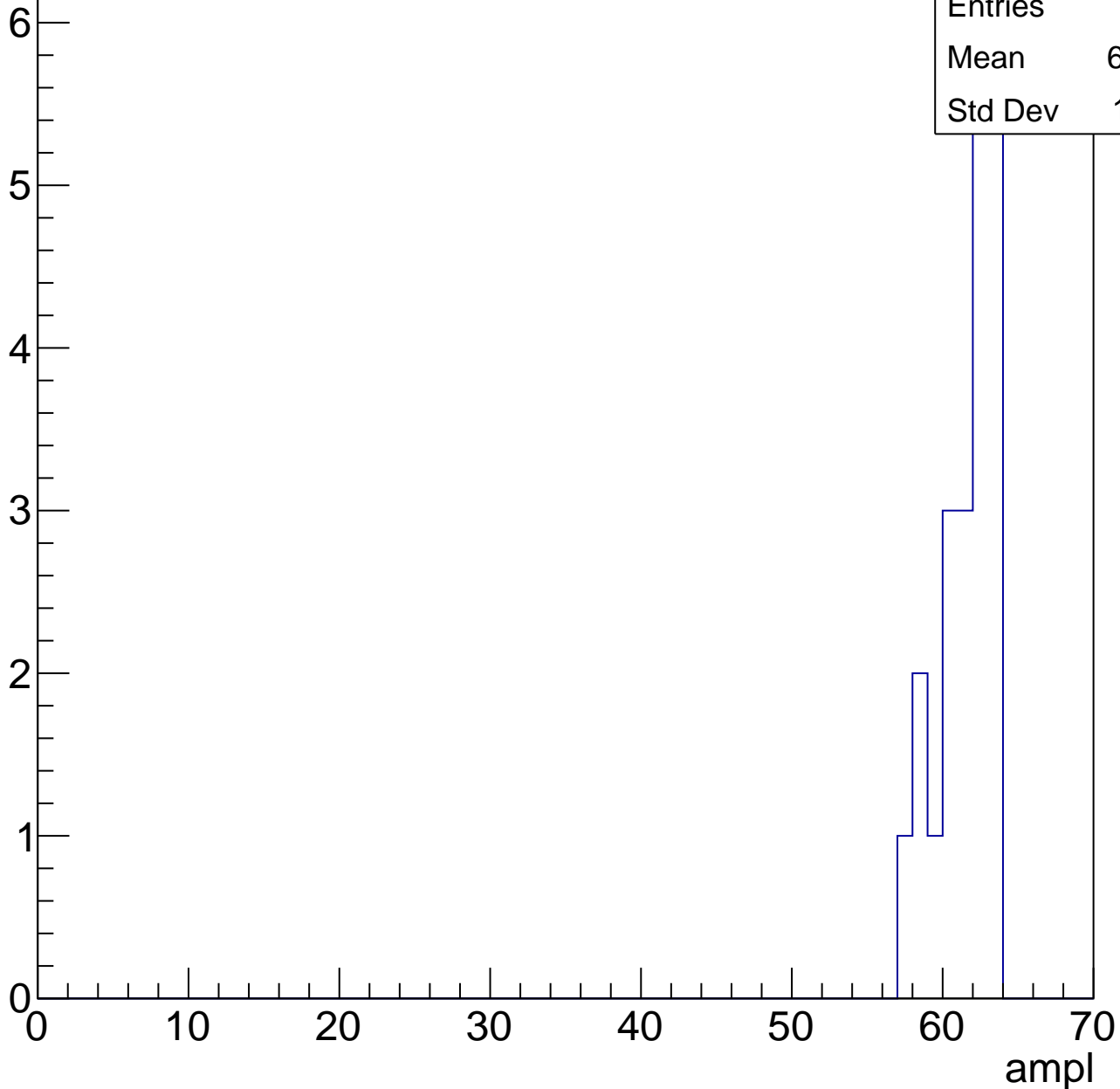


B1L102S, U9-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

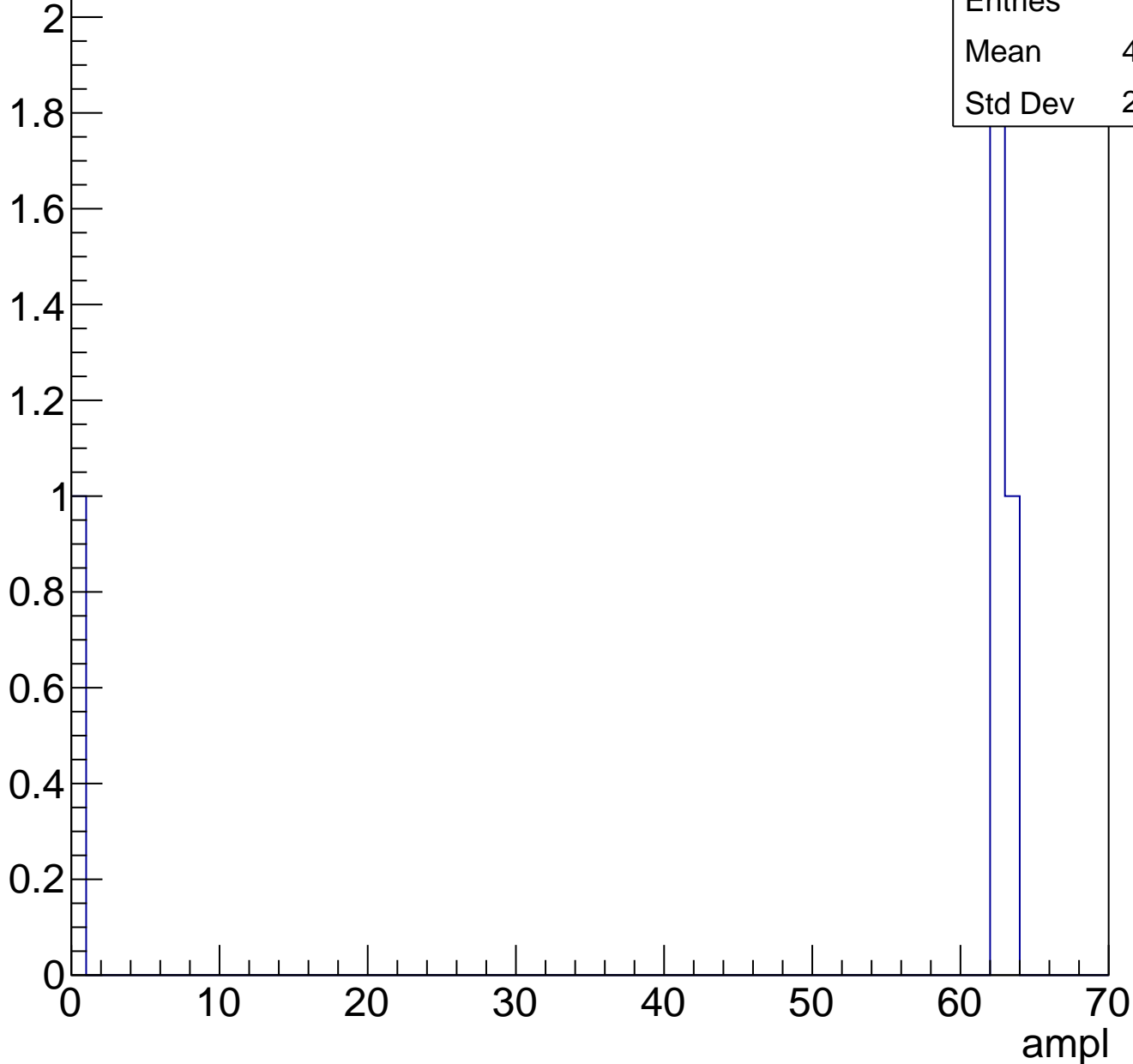
Entries	22
Mean	61.14
Std Dev	1.791



B1L102S, U9-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch107, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	28.58
Std Dev	4.845

Gaus mean : 29.3844

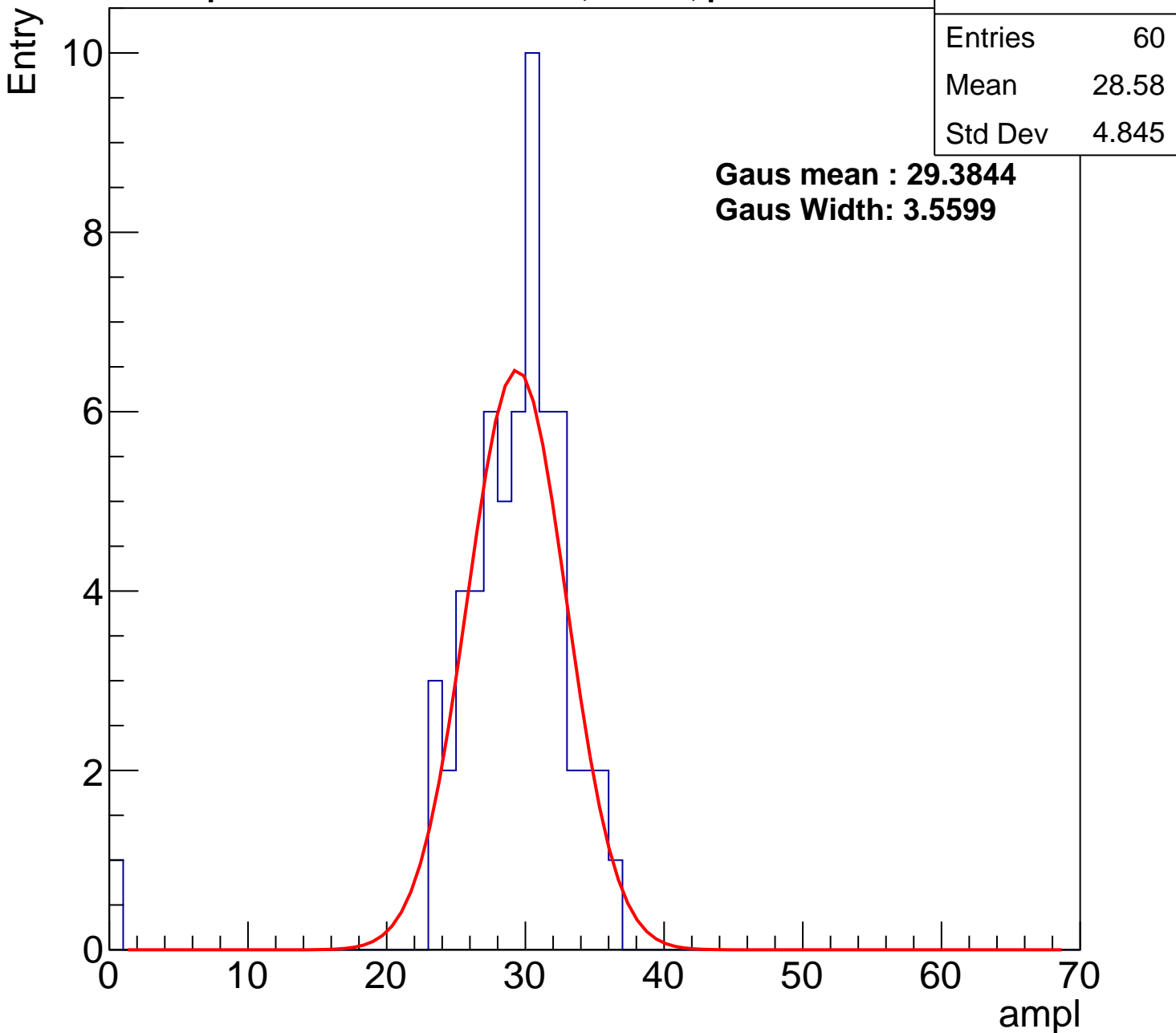
Gaus Width: 3.5599

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch107, adc1

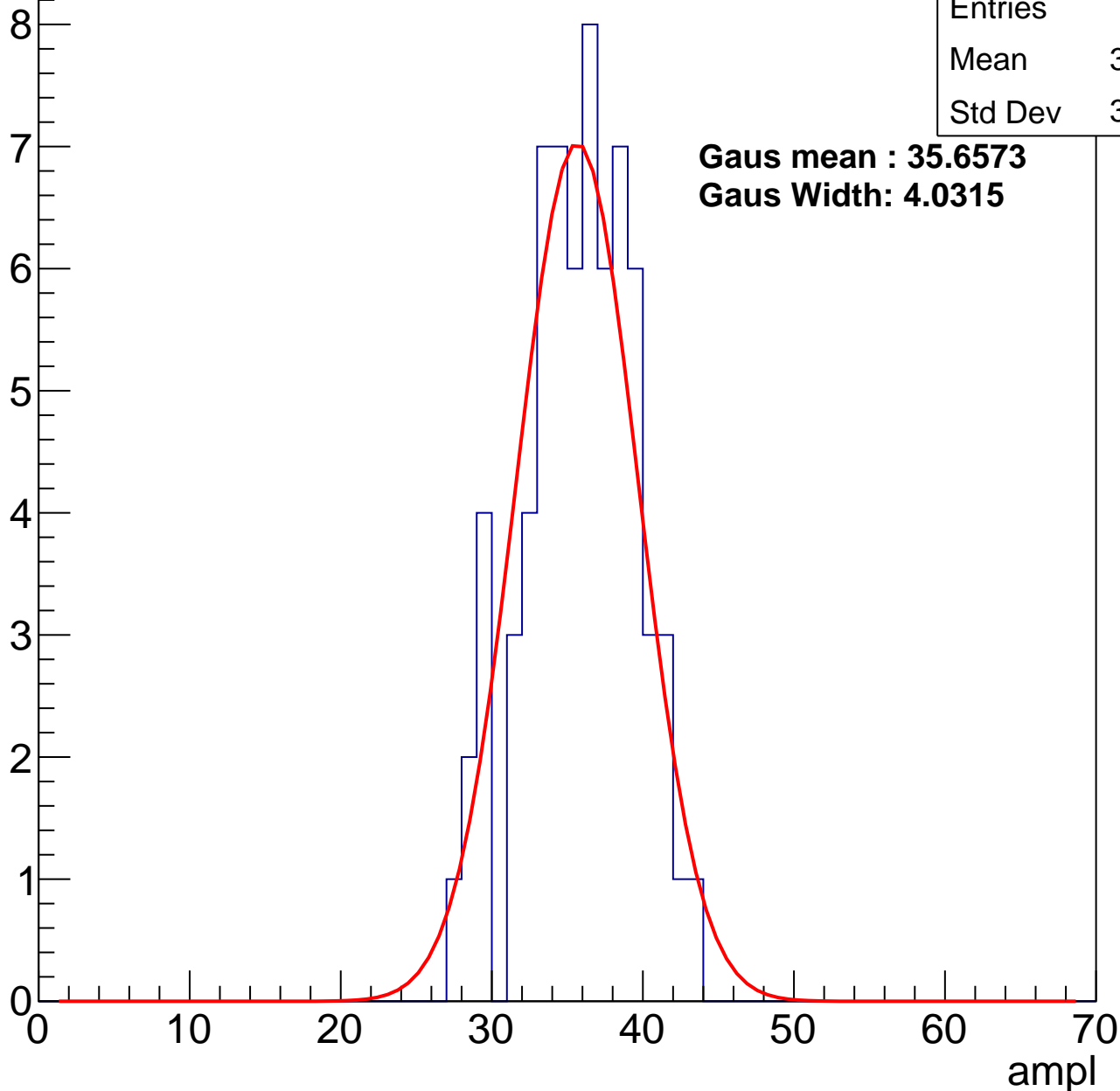
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.32
Std Dev	3.622

Gaus mean : 35.6573

Gaus Width: 4.0315



B1L102S, U9-ch107, adc2

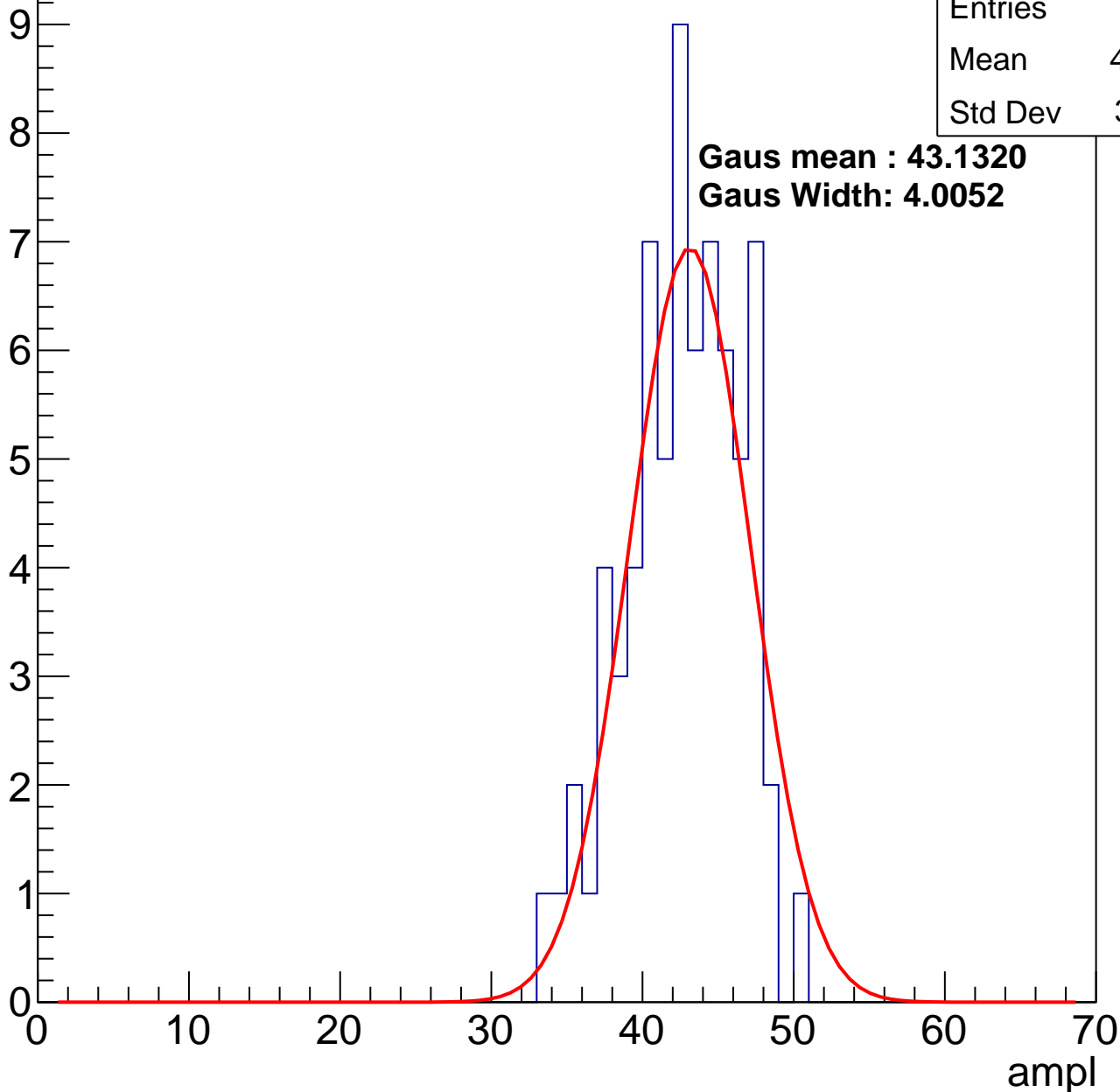
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.18
Std Dev	3.701

Gaus mean : 43.1320

Gaus Width: 4.0052

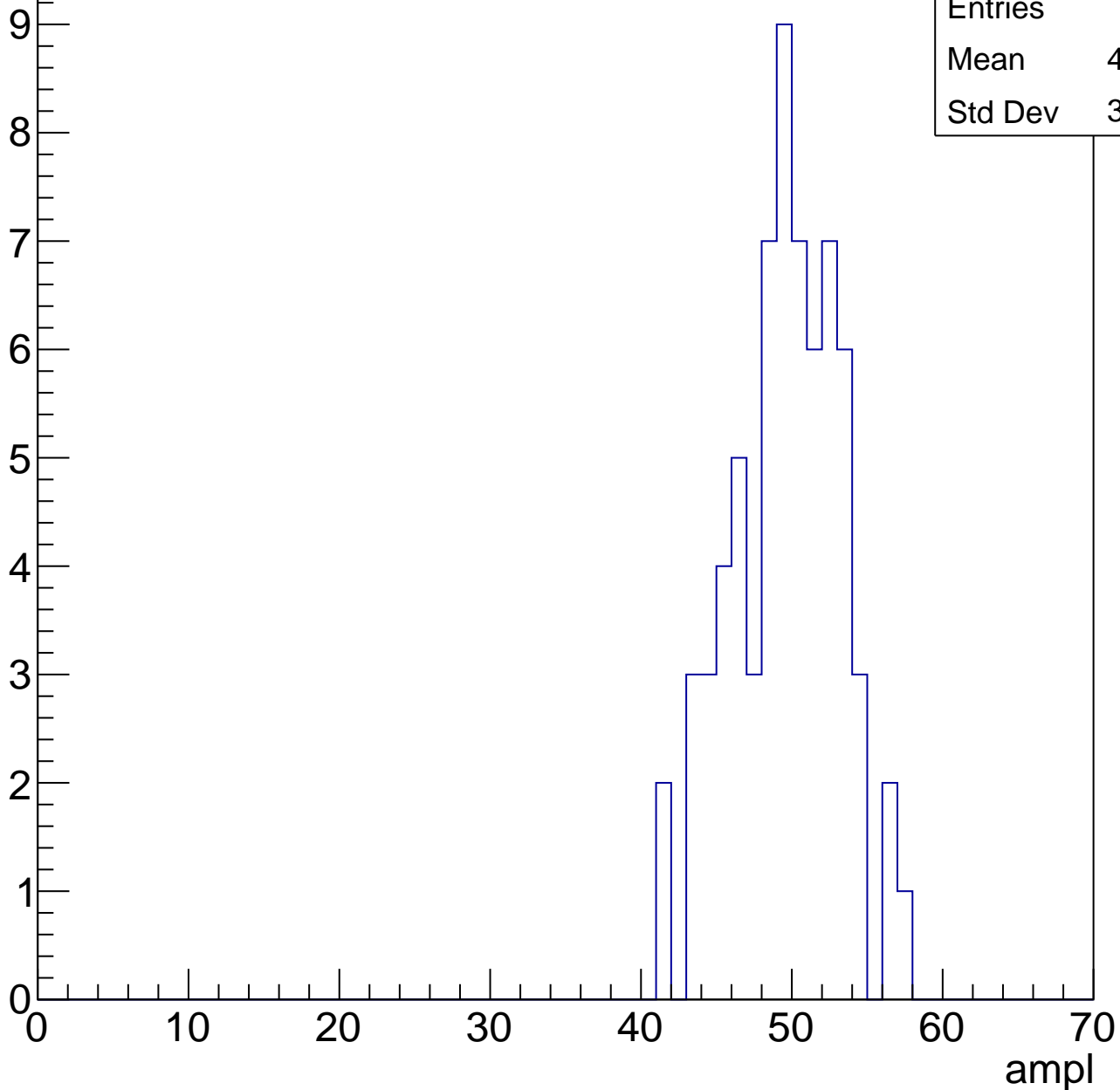


B1L102S, U9-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

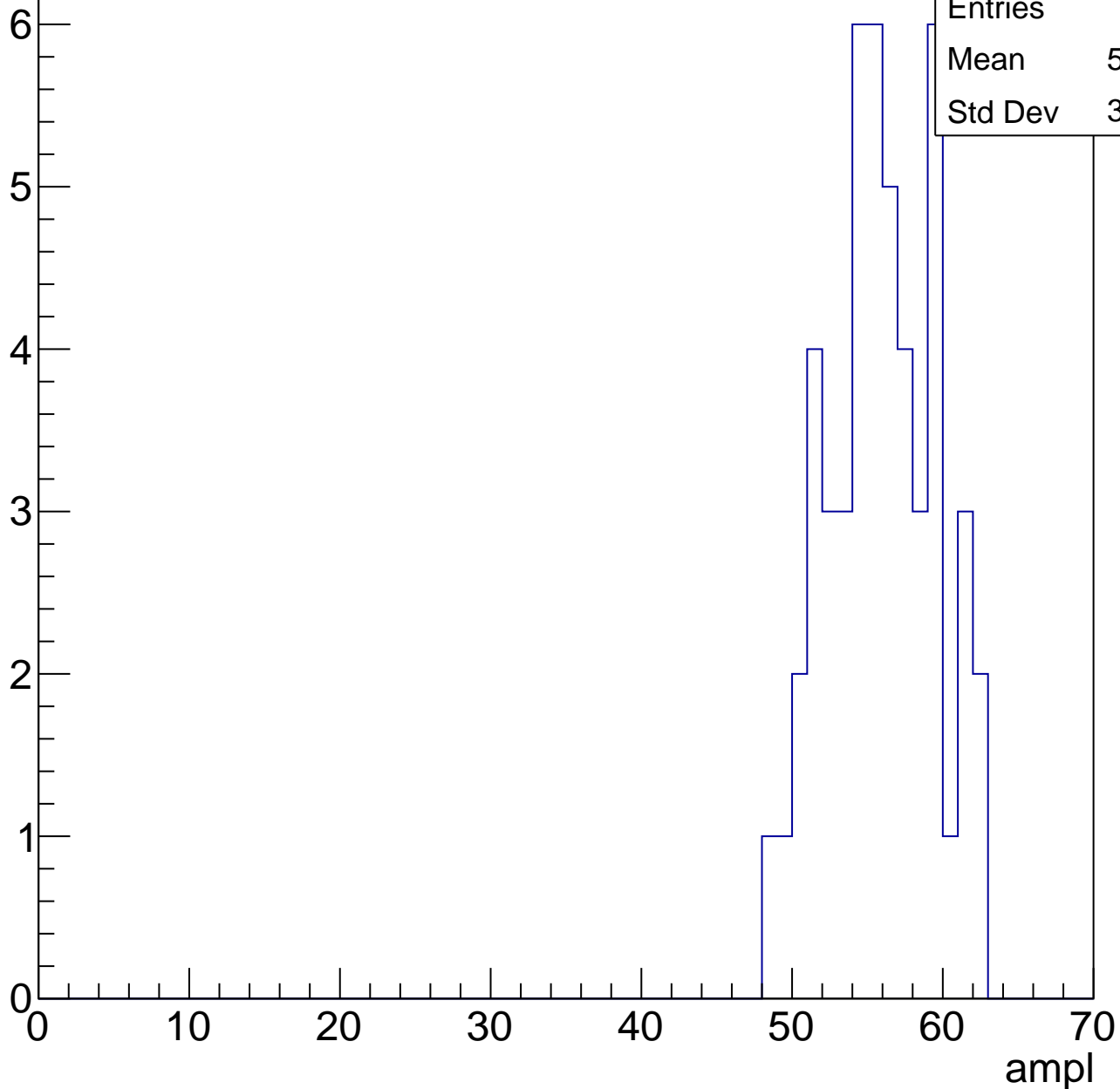
Entries	68
Mean	49.12
Std Dev	3.567



B1L102S, U9-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

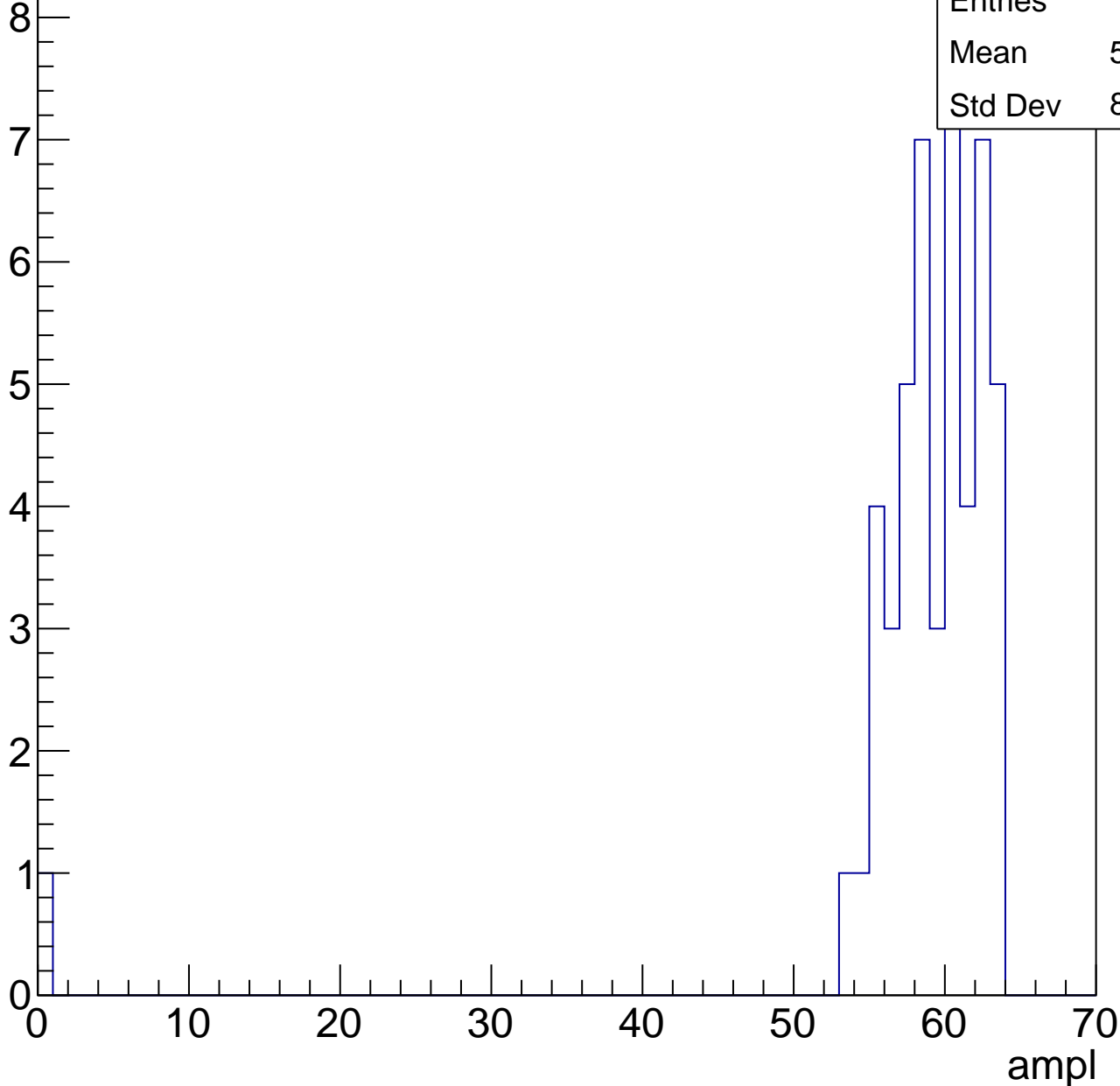


B1L102S, U9-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.88
Std Dev	8.766

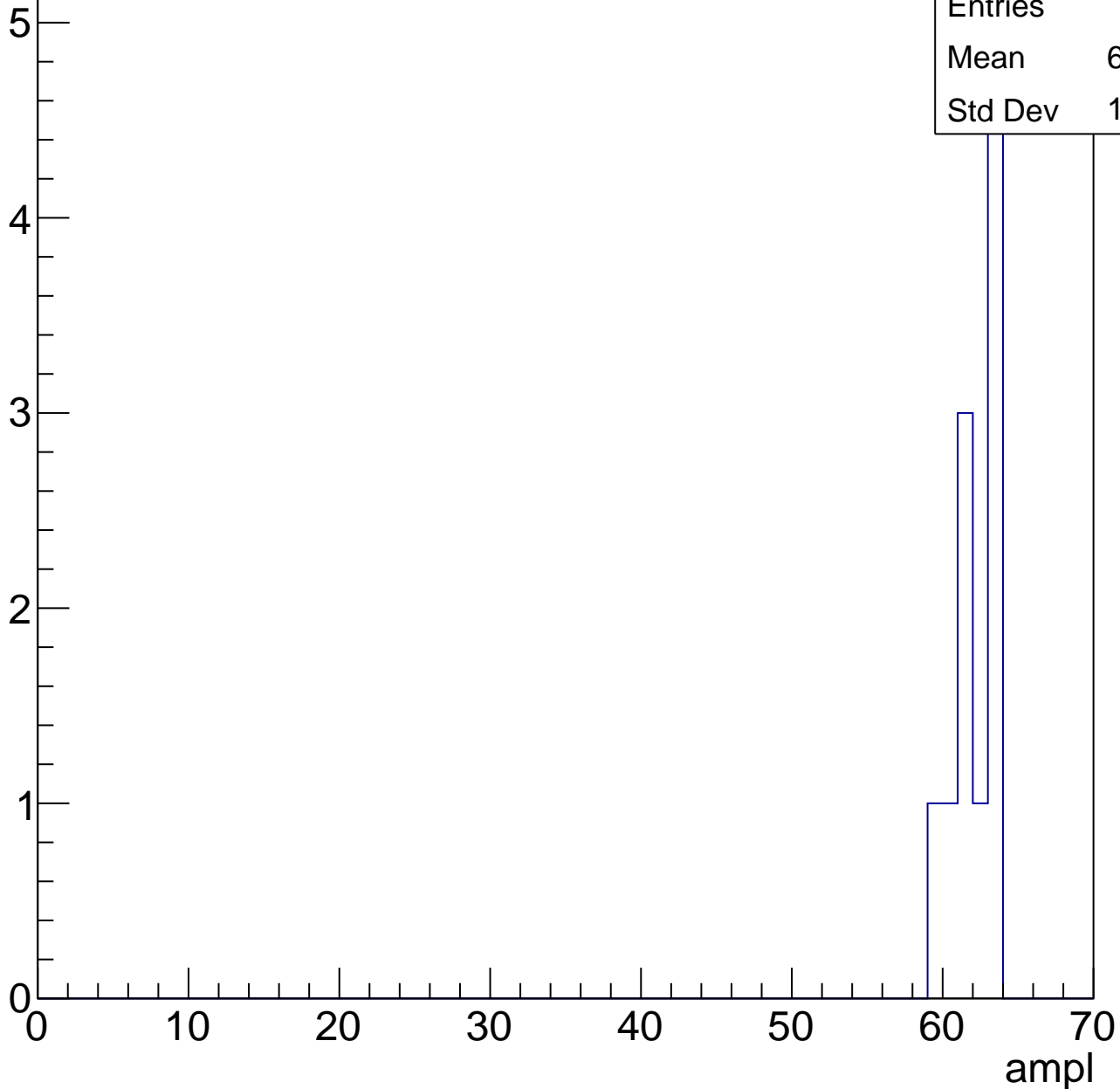


B1L102S, U9-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.73
Std Dev	1.355



B1L102S, U9-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch108, adc0

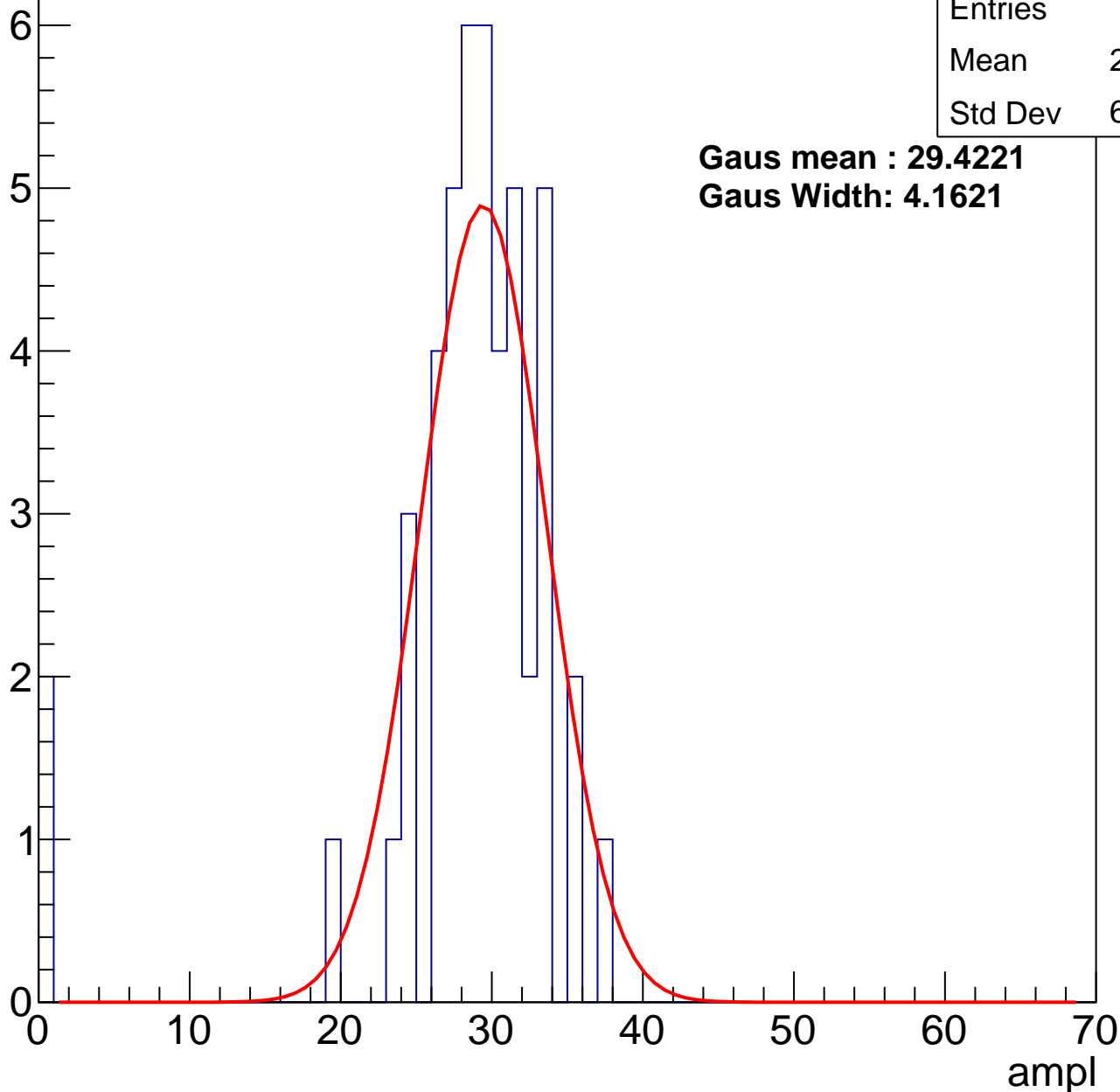
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	27.79
Std Dev	6.754

Gaus mean : 29.4221

Gaus Width: 4.1621



B1L102S, U9-ch108, adc1

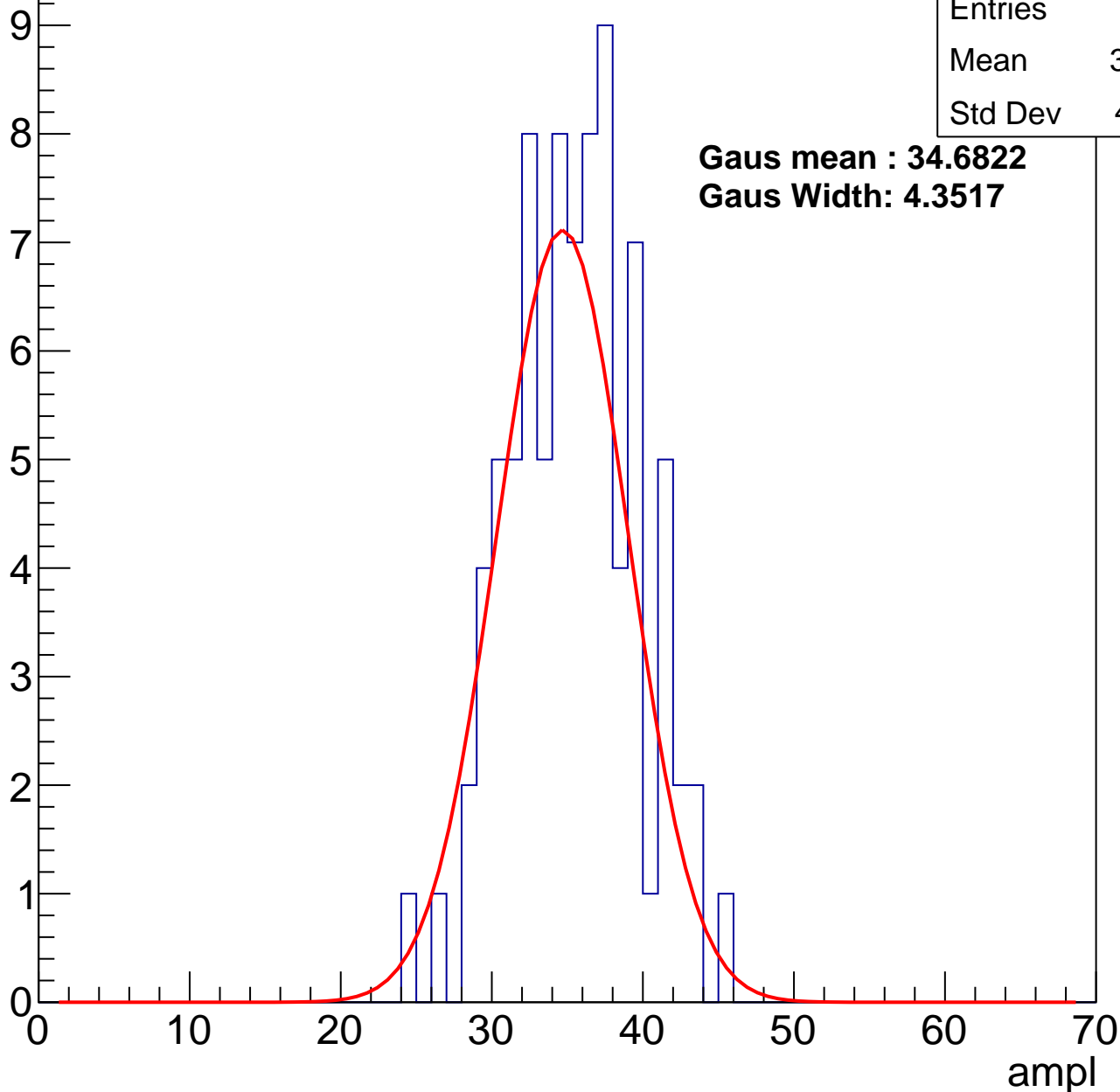
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	34.95
Std Dev	4.161

Gaus mean : 34.6822

Gaus Width: 4.3517



B1L102S, U9-ch108, adc2

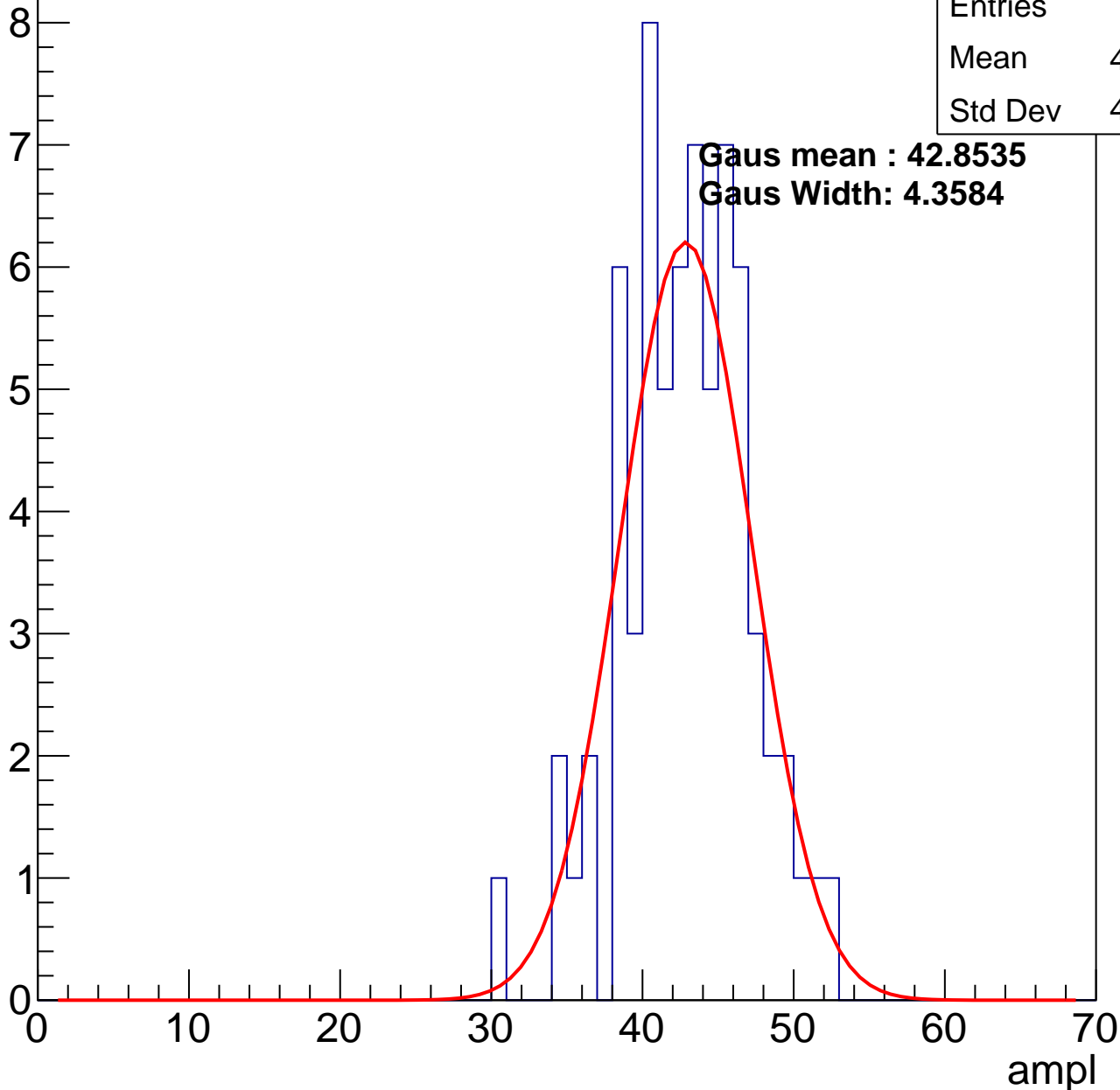
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.42
Std Dev	4.199

Gaus mean : 42.8535

Gaus Width: 4.3584

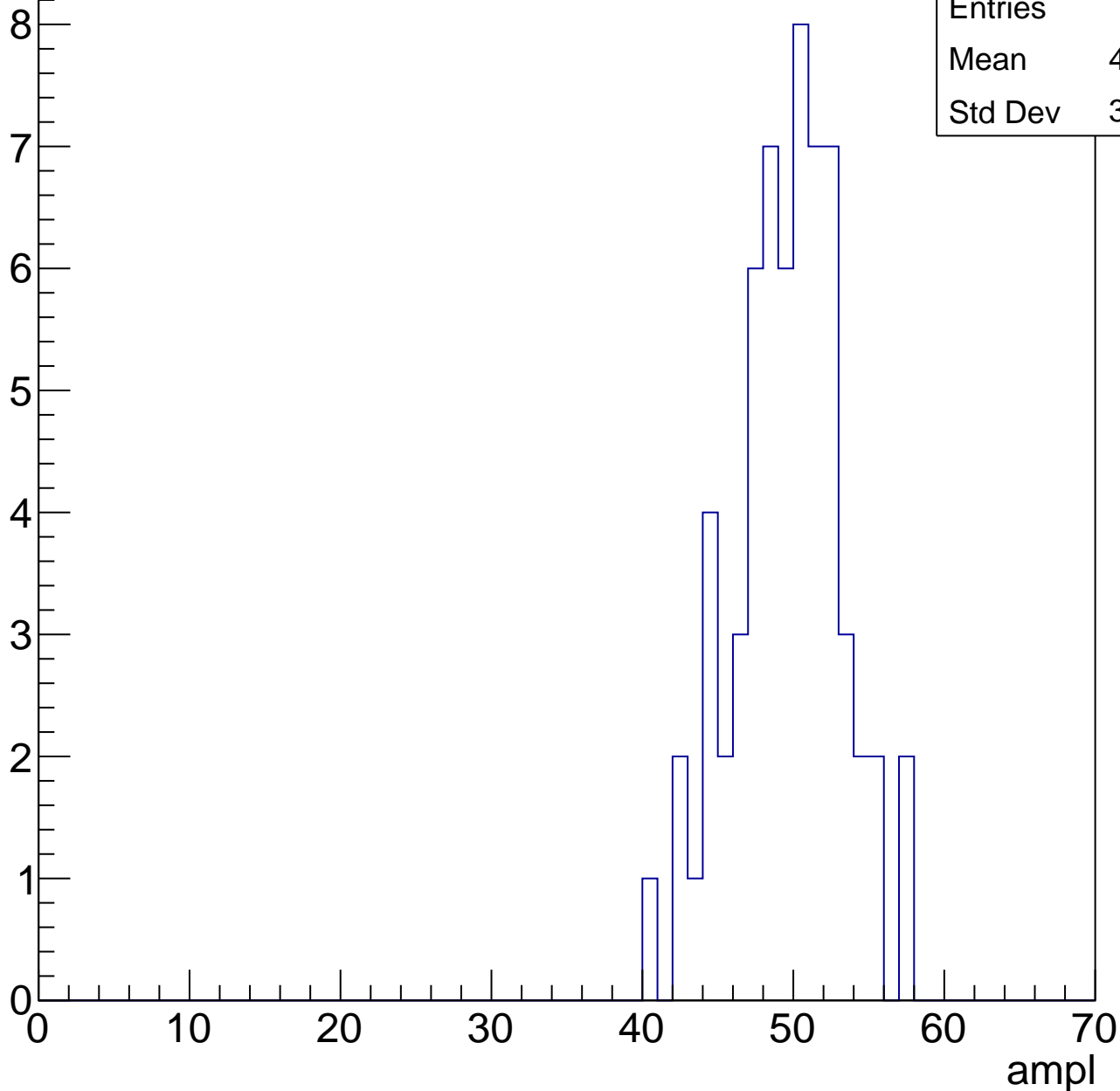


B1L102S, U9-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.13
Std Dev	3.566

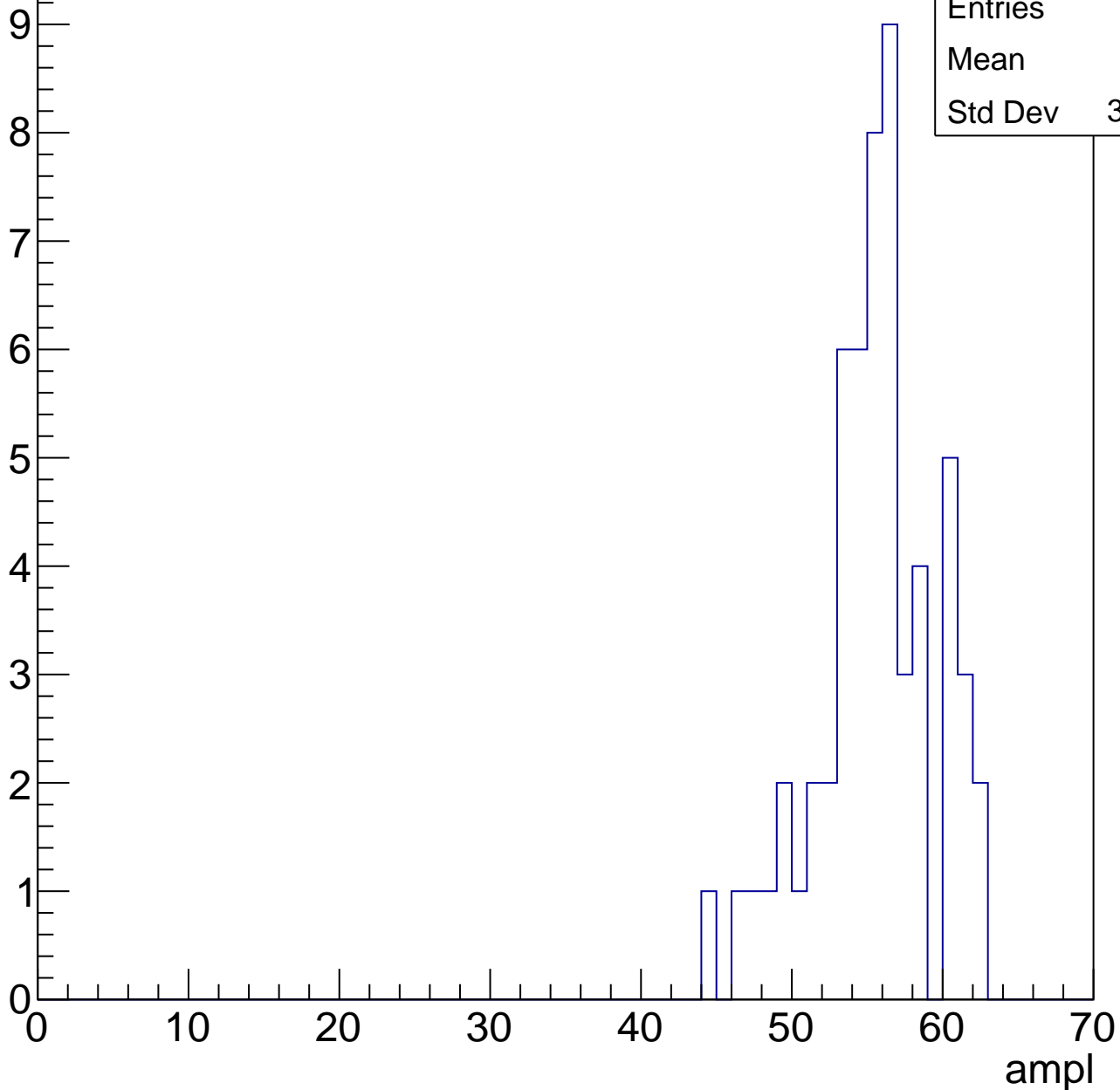


B1L102S, U9-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55
Std Dev	3.929

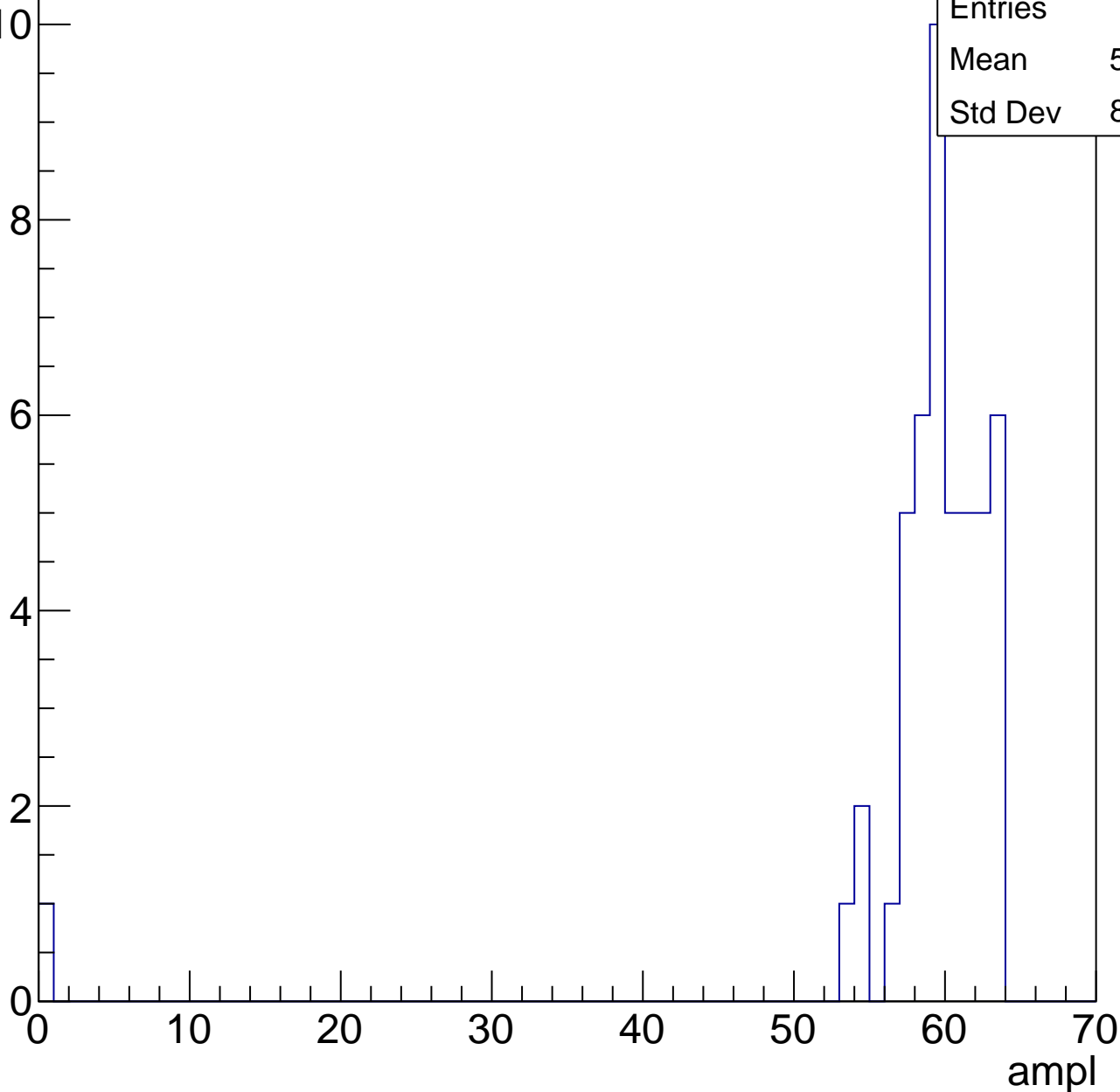


B1L102S, U9-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.15
Std Dev	8.913



B1L102S, U9-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch109, adc0

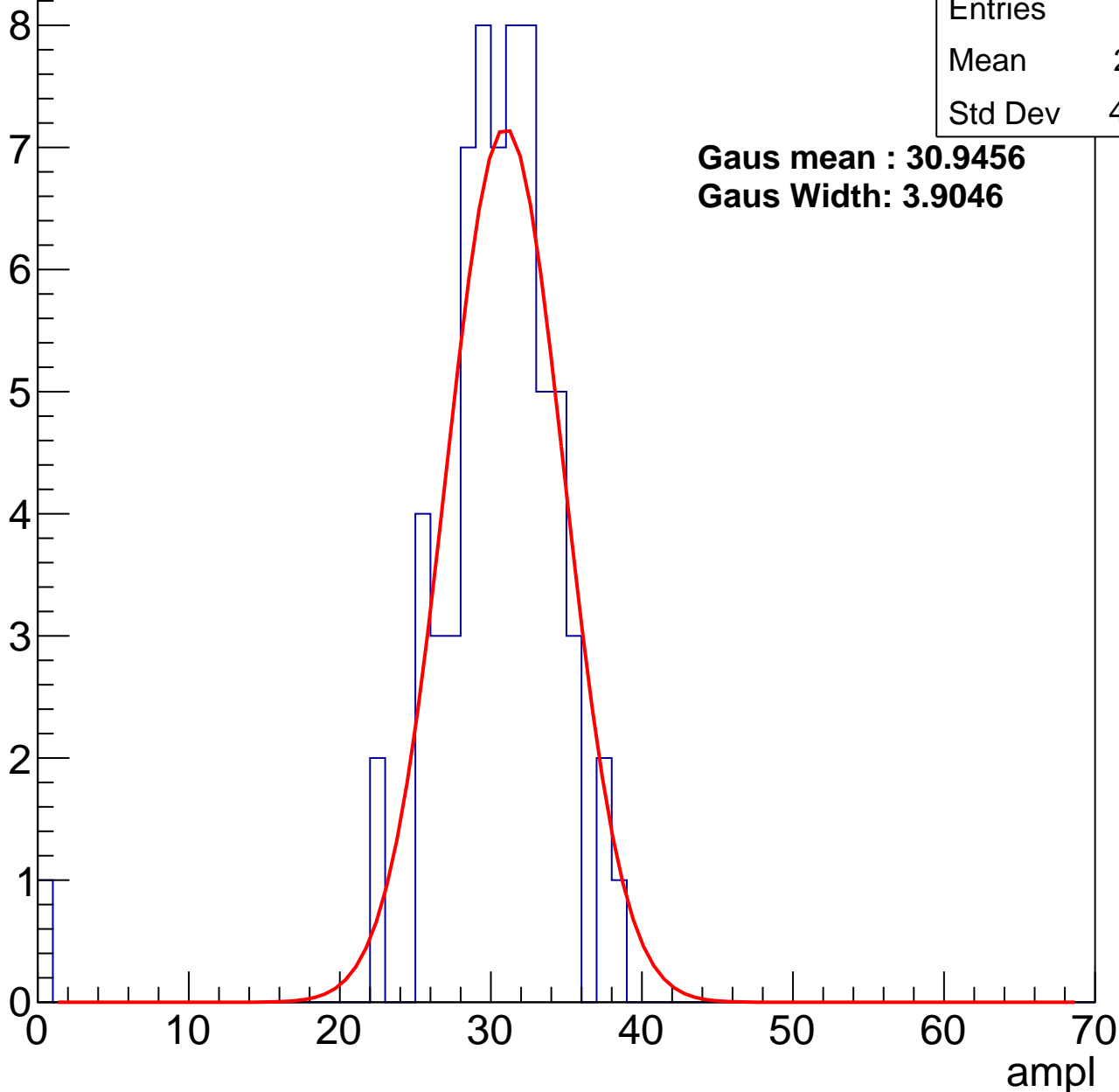
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.81
Std Dev	4.945

Gaus mean : 30.9456

Gaus Width: 3.9046

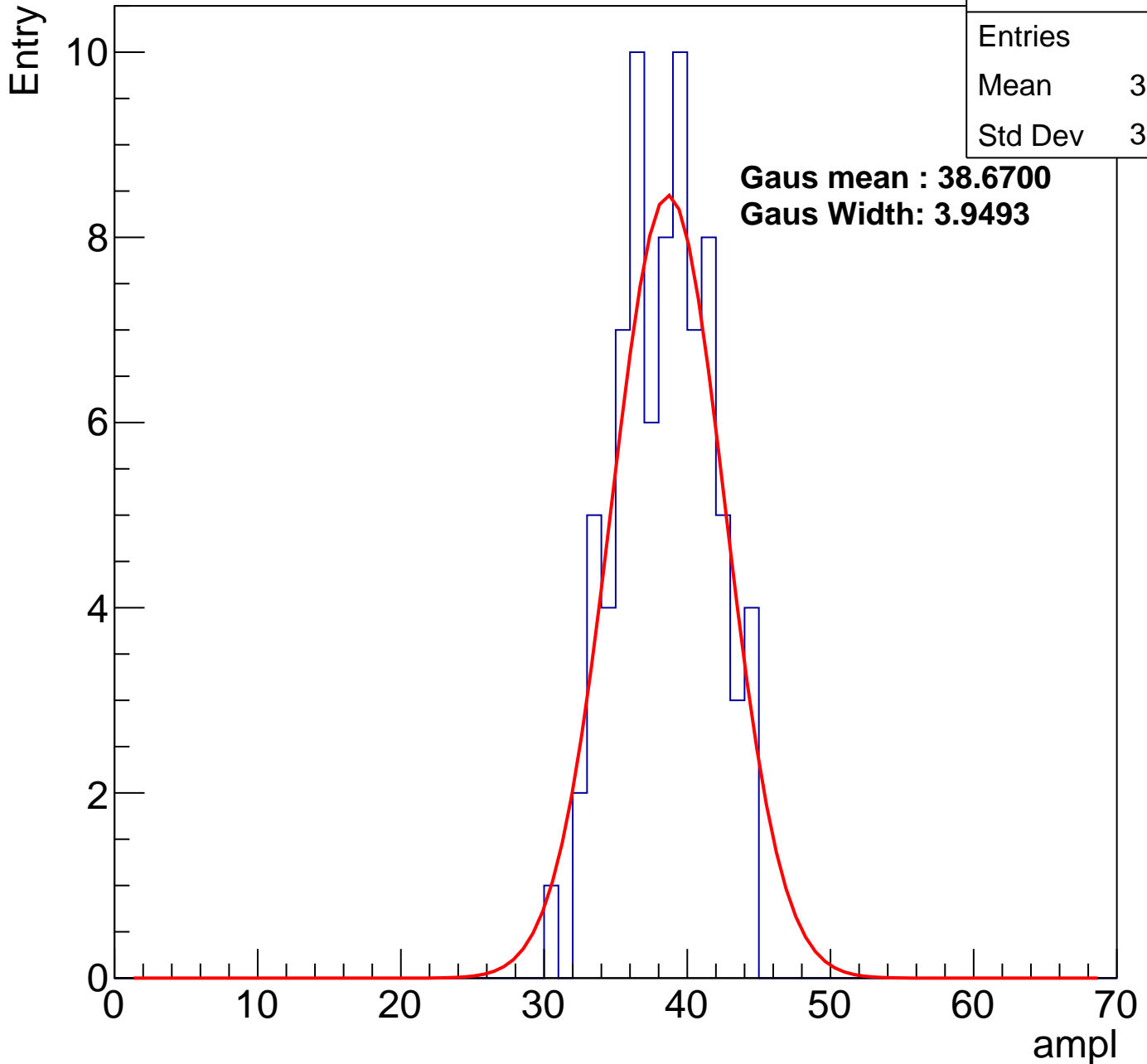


B1L102S, U9-ch109, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	37.99
Std Dev	3.246

Gaus mean : 38.6700
Gaus Width: 3.9493



B1L102S, U9-ch109, adc2

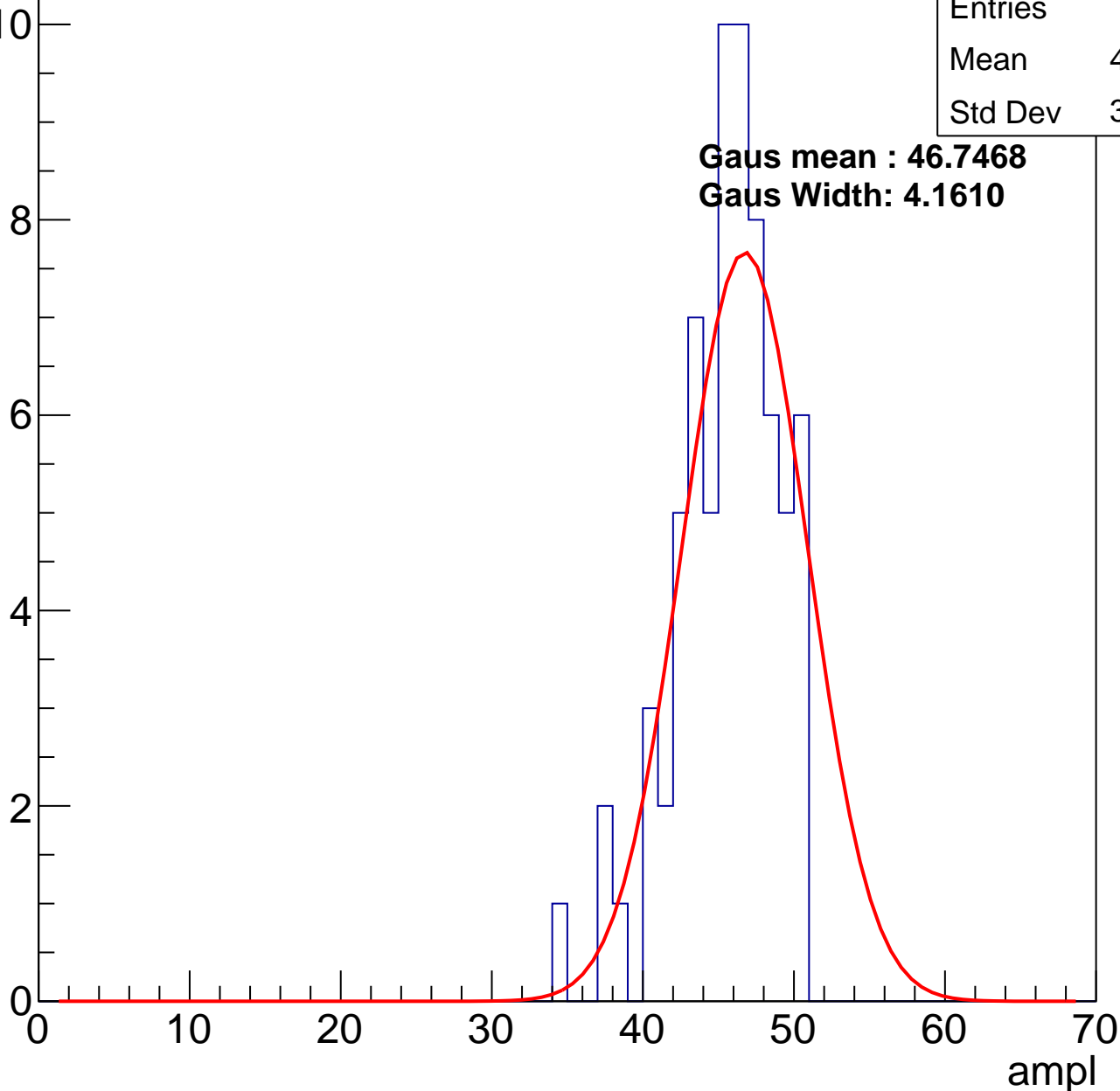
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	45.04
Std Dev	3.375

Gaus mean : 46.7468

Gaus Width: 4.1610

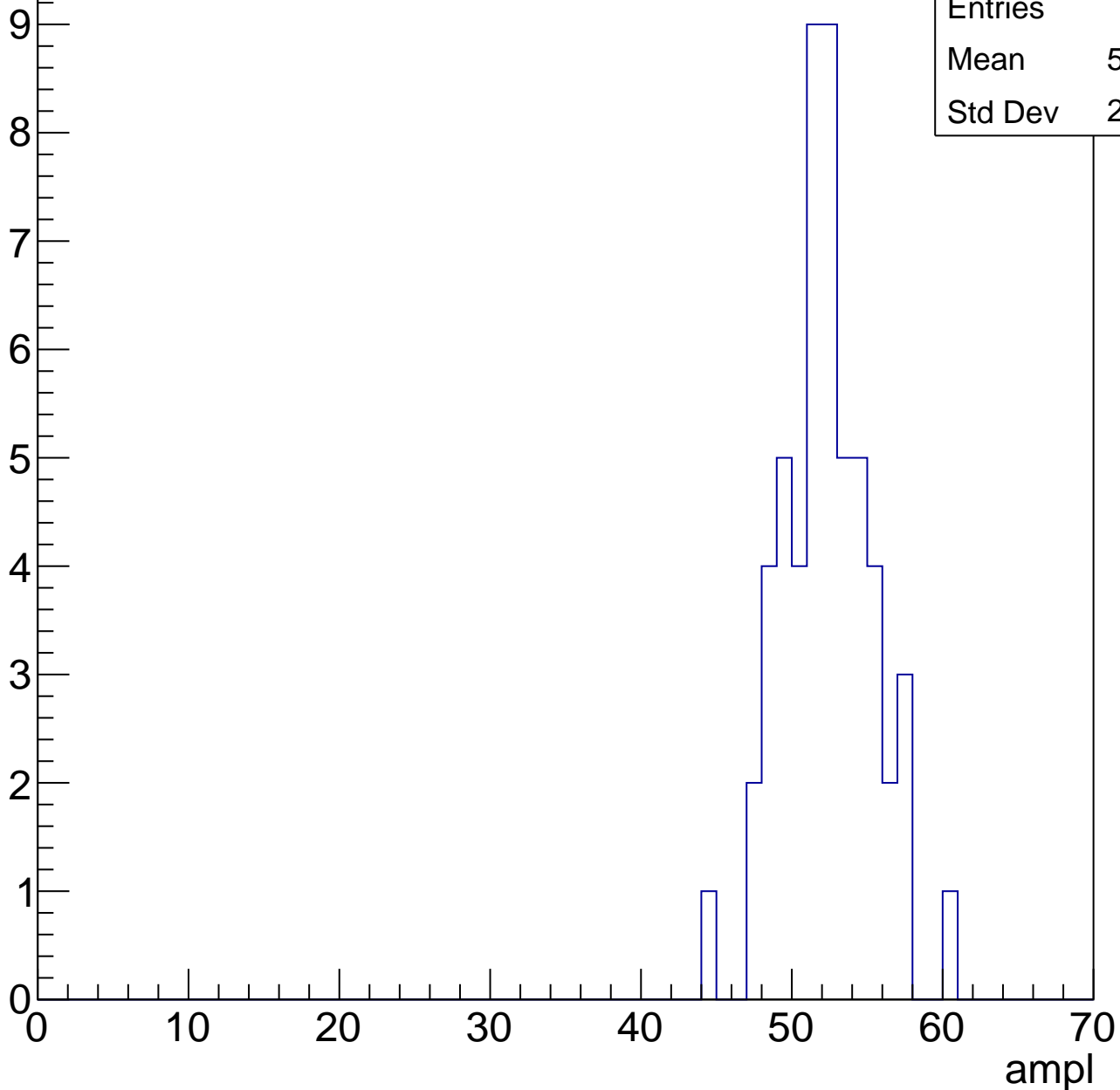


B1L102S, U9-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	51.85
Std Dev	2.978

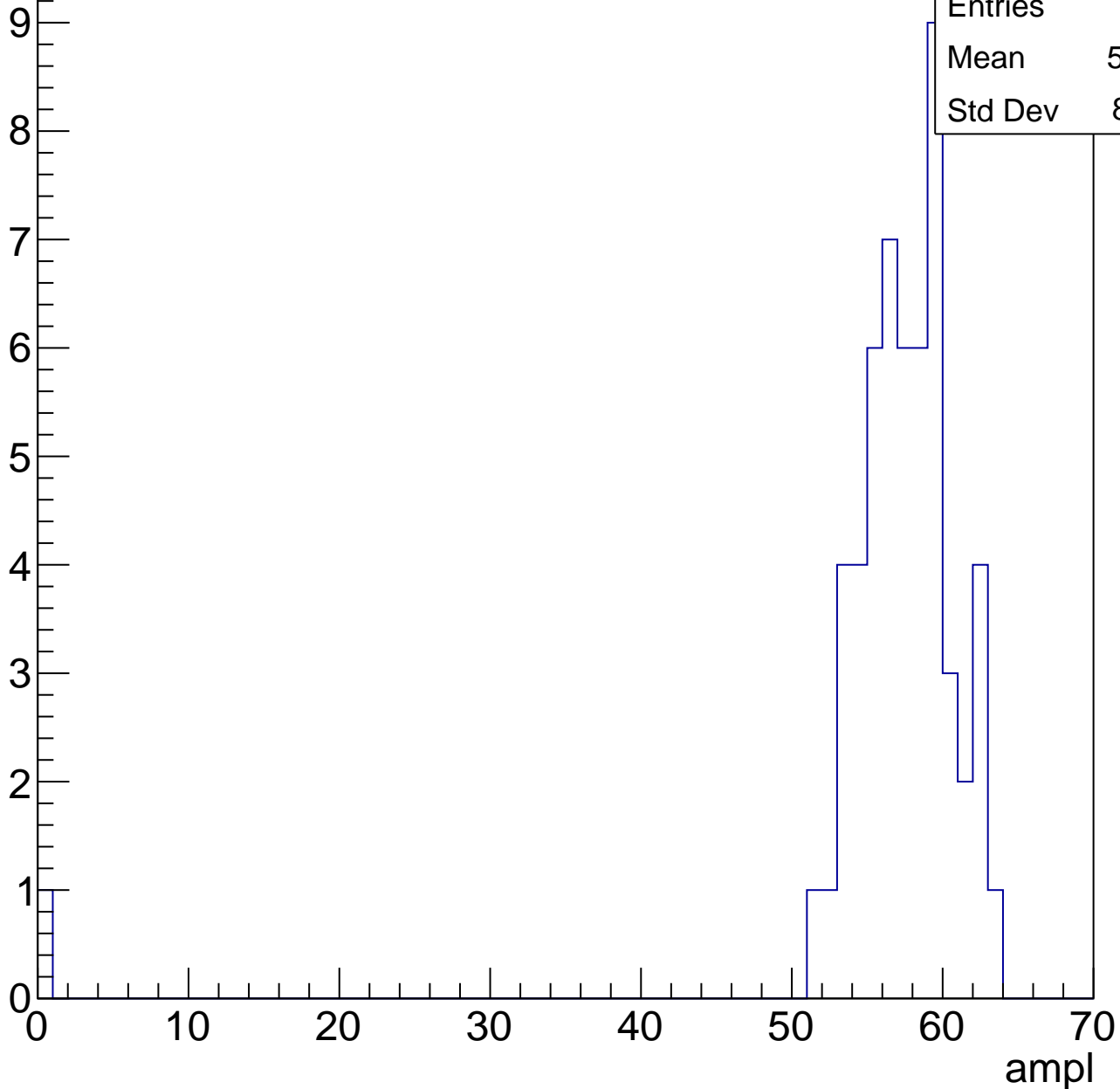


B1L102S, U9-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.13
Std Dev	8.131

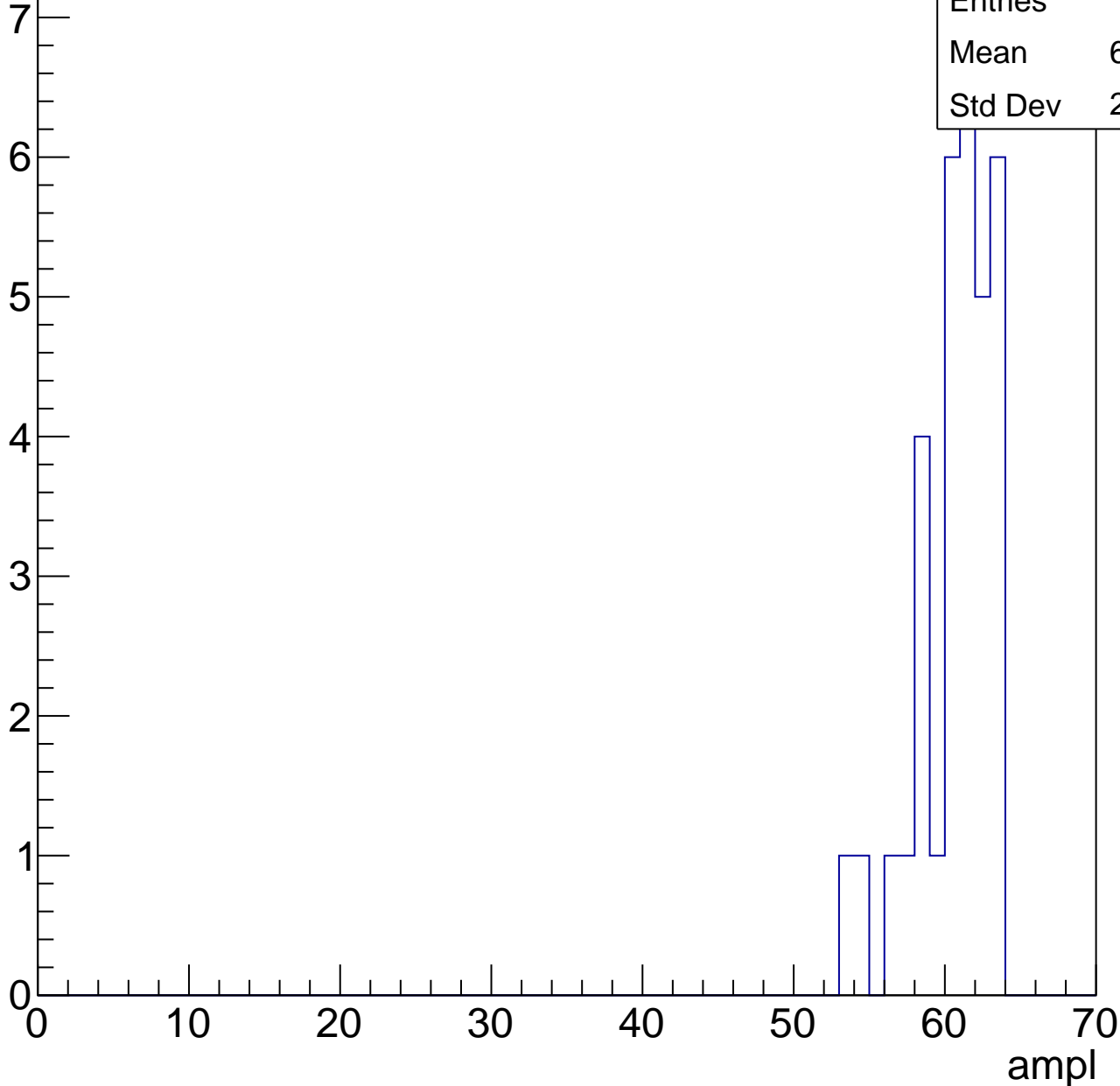


B1L102S, U9-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	33
Mean	60.18
Std Dev	2.504



B1L102S, U9-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch110, adc0

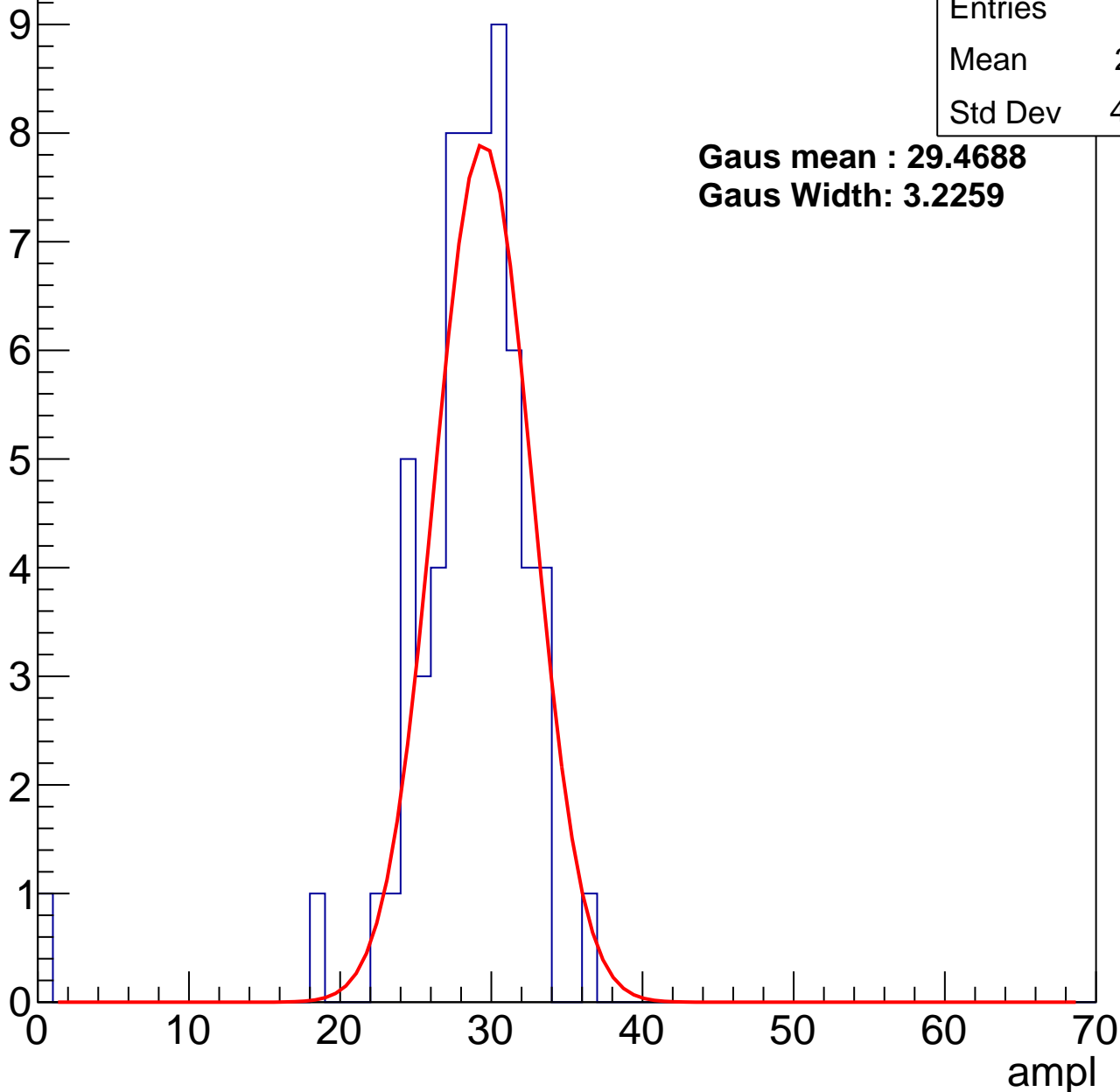
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.91
Std Dev	4.679

Gaus mean : 29.4688

Gaus Width: 3.2259



B1L102S, U9-ch110, adc1

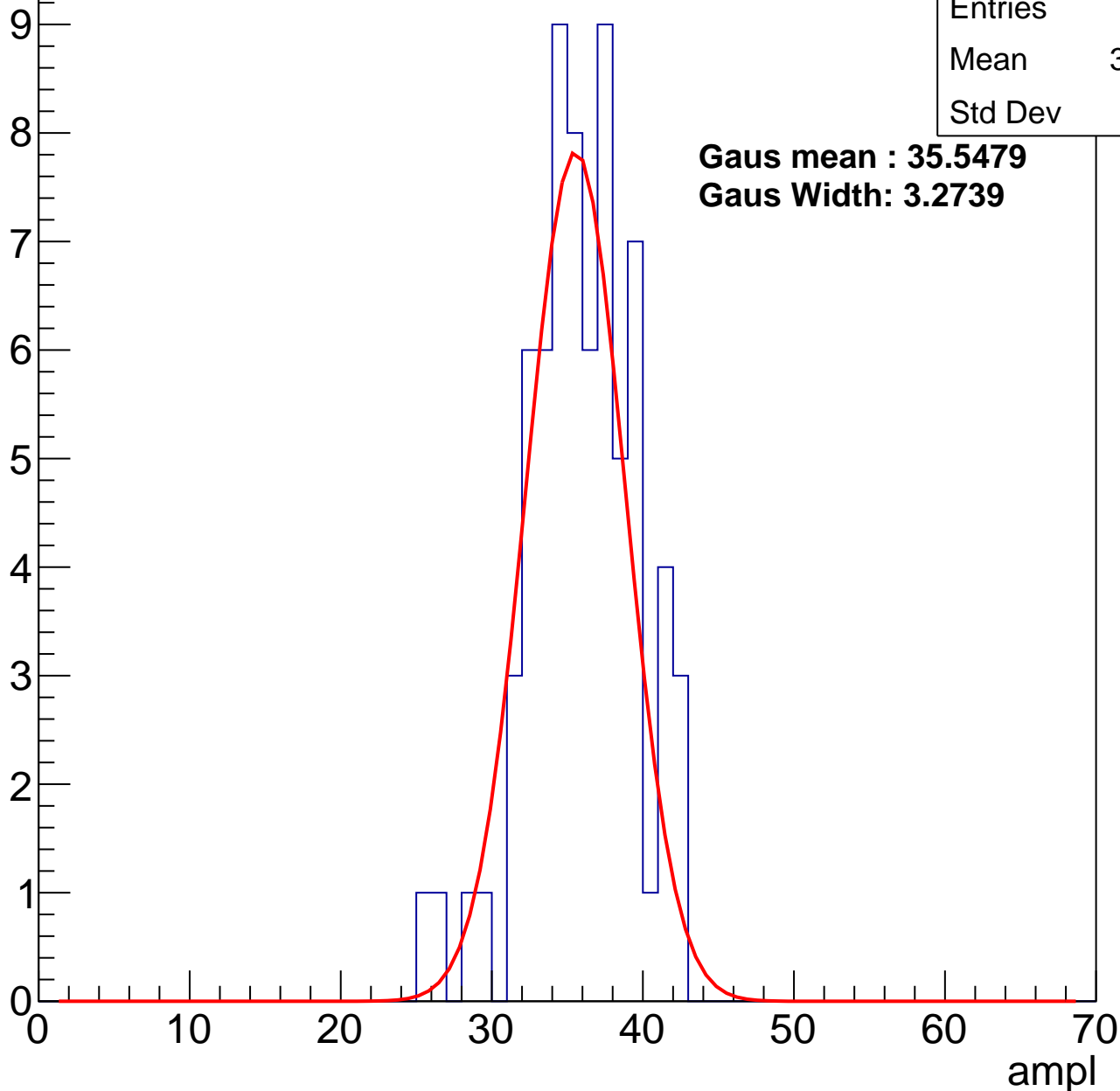
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.48
Std Dev	3.56

Gaus mean : 35.5479

Gaus Width: 3.2739



B1L102S, U9-ch110, adc2

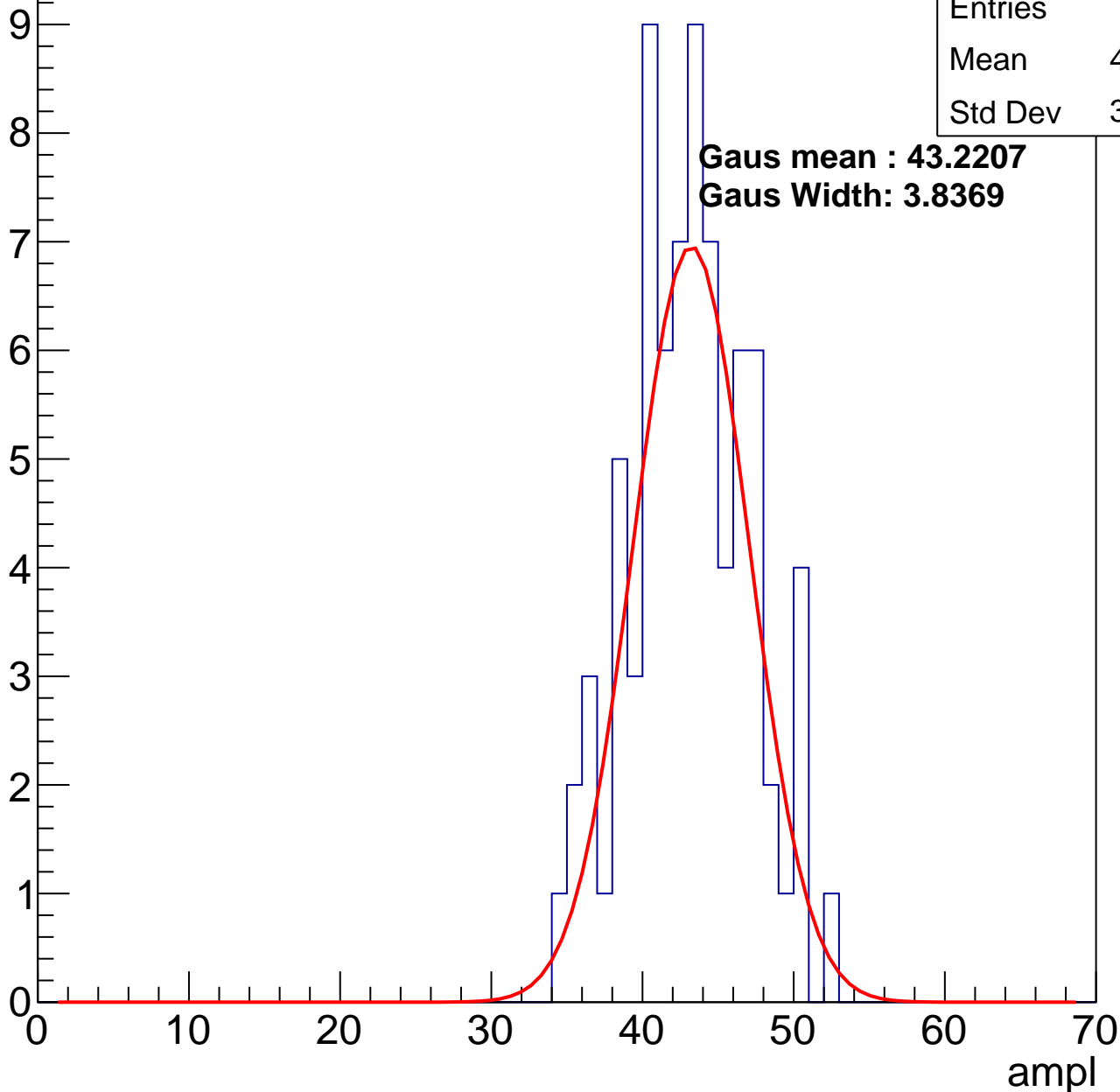
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42.68
Std Dev	3.956

Gaus mean : 43.2207

Gaus Width: 3.8369

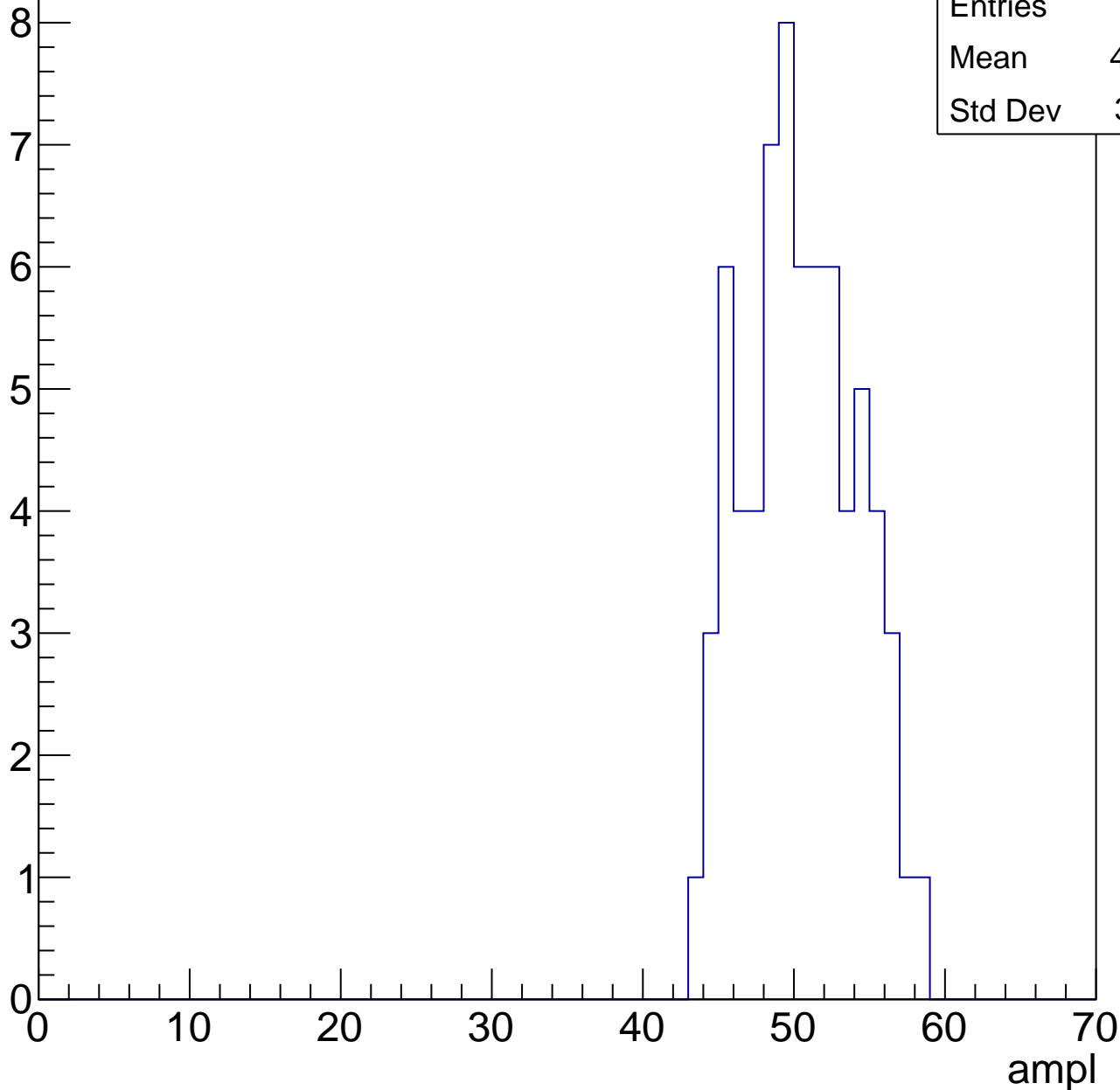


B1L102S, U9-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.97
Std Dev	3.631

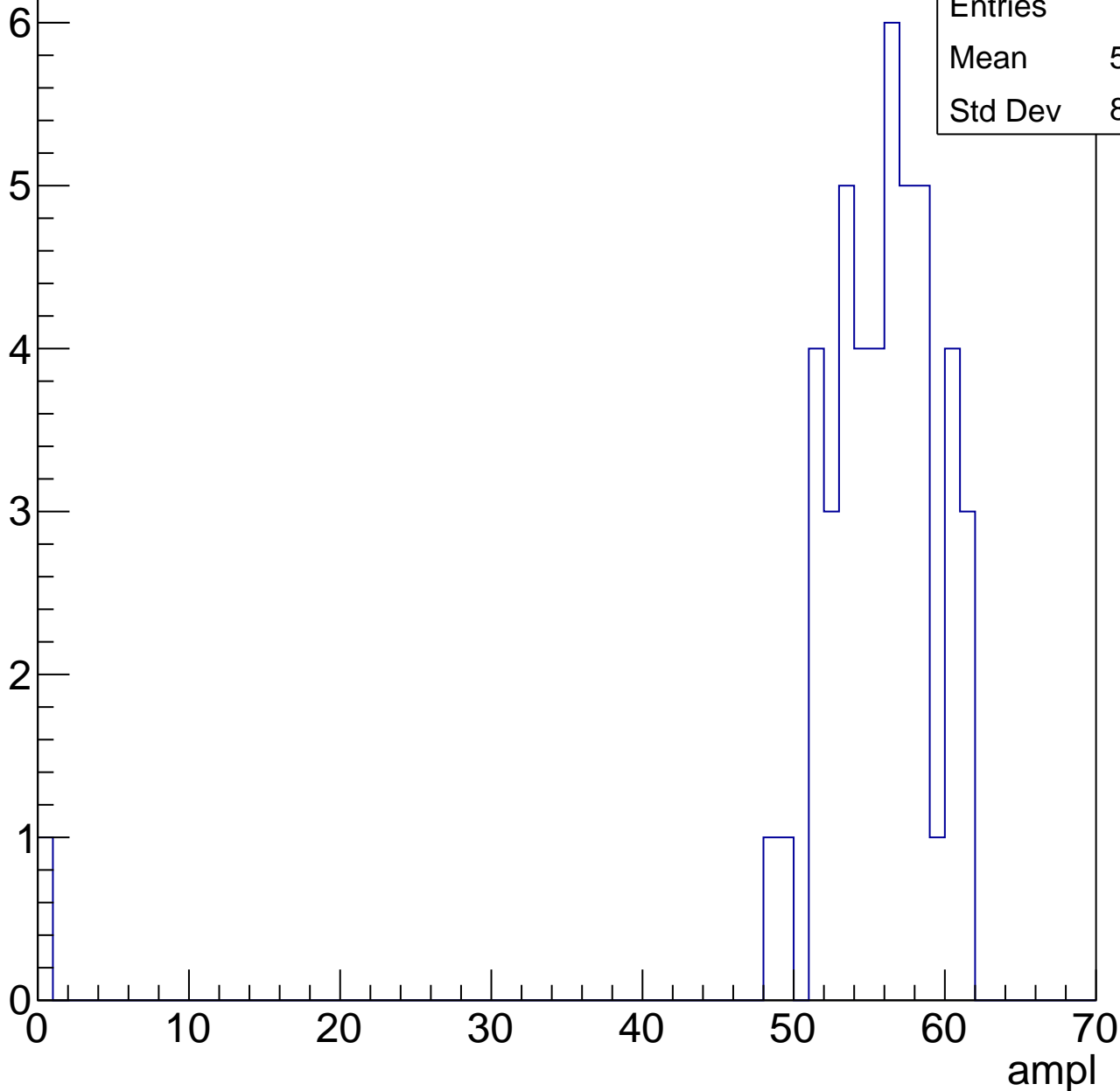


B1L102S, U9-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.28
Std Dev	8.624

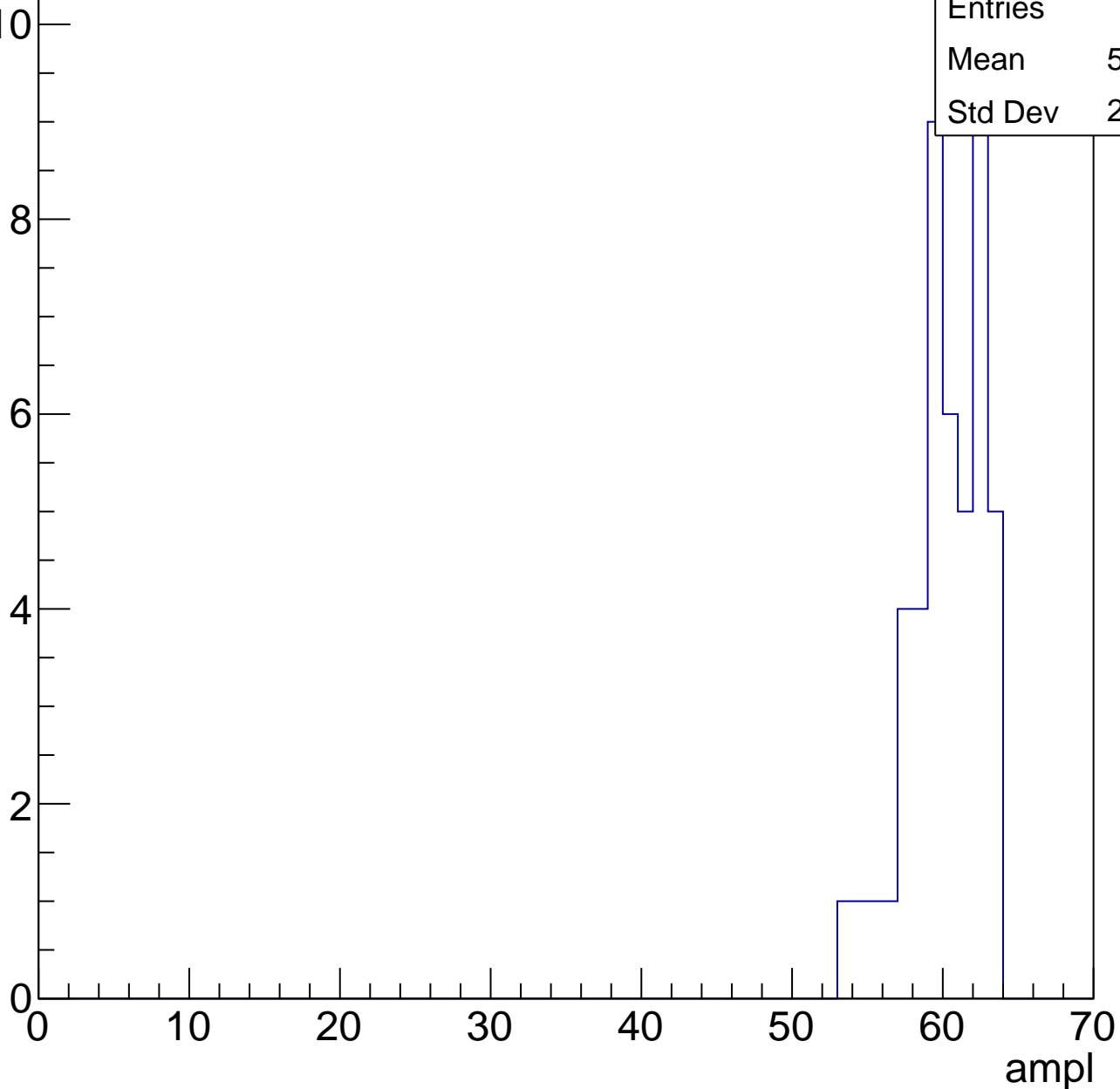


B1L102S, U9-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

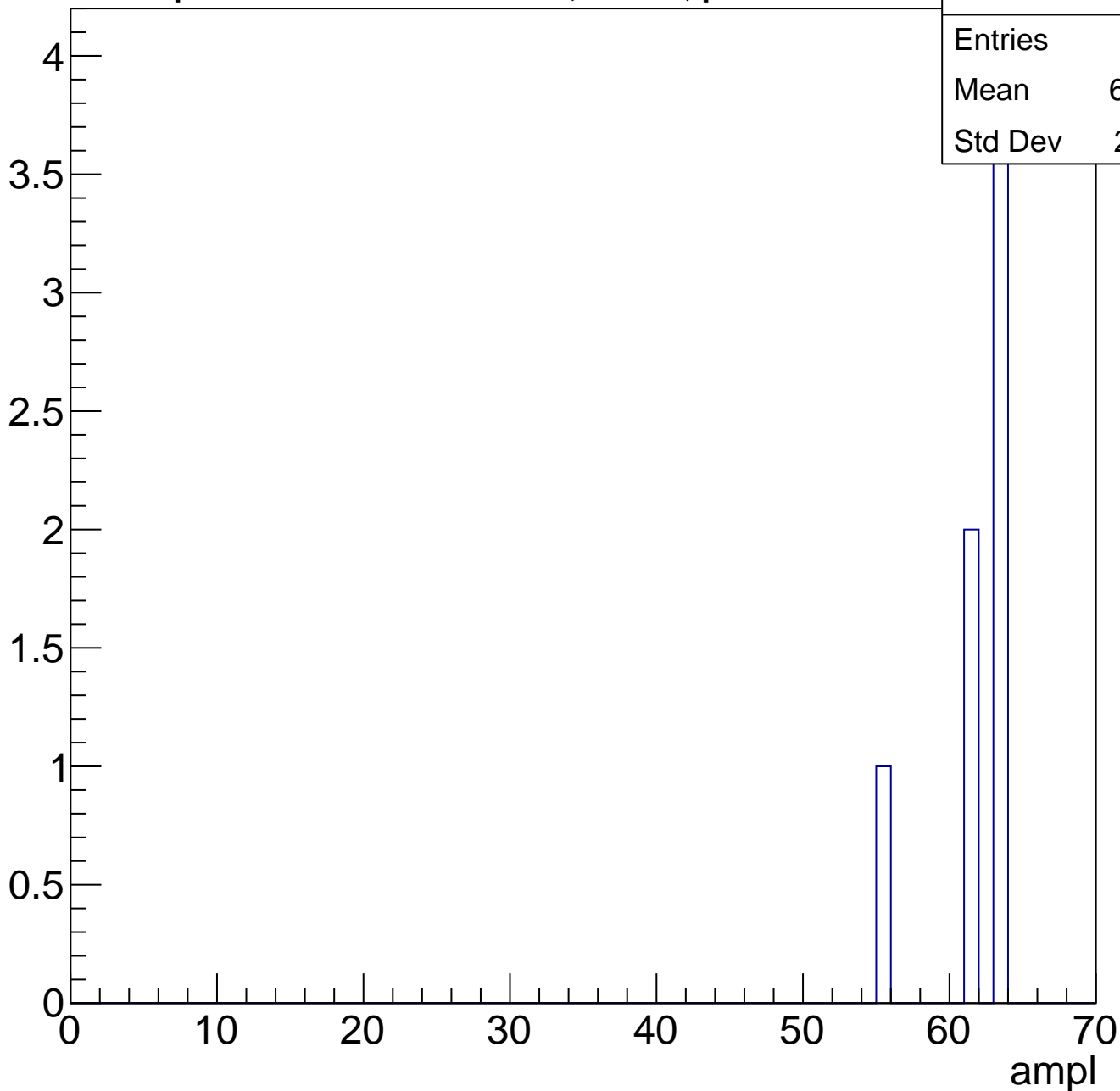
Entries	47
Mean	59.77
Std Dev	2.416



B1L102S, U9-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L102S, U9-ch111, adc0

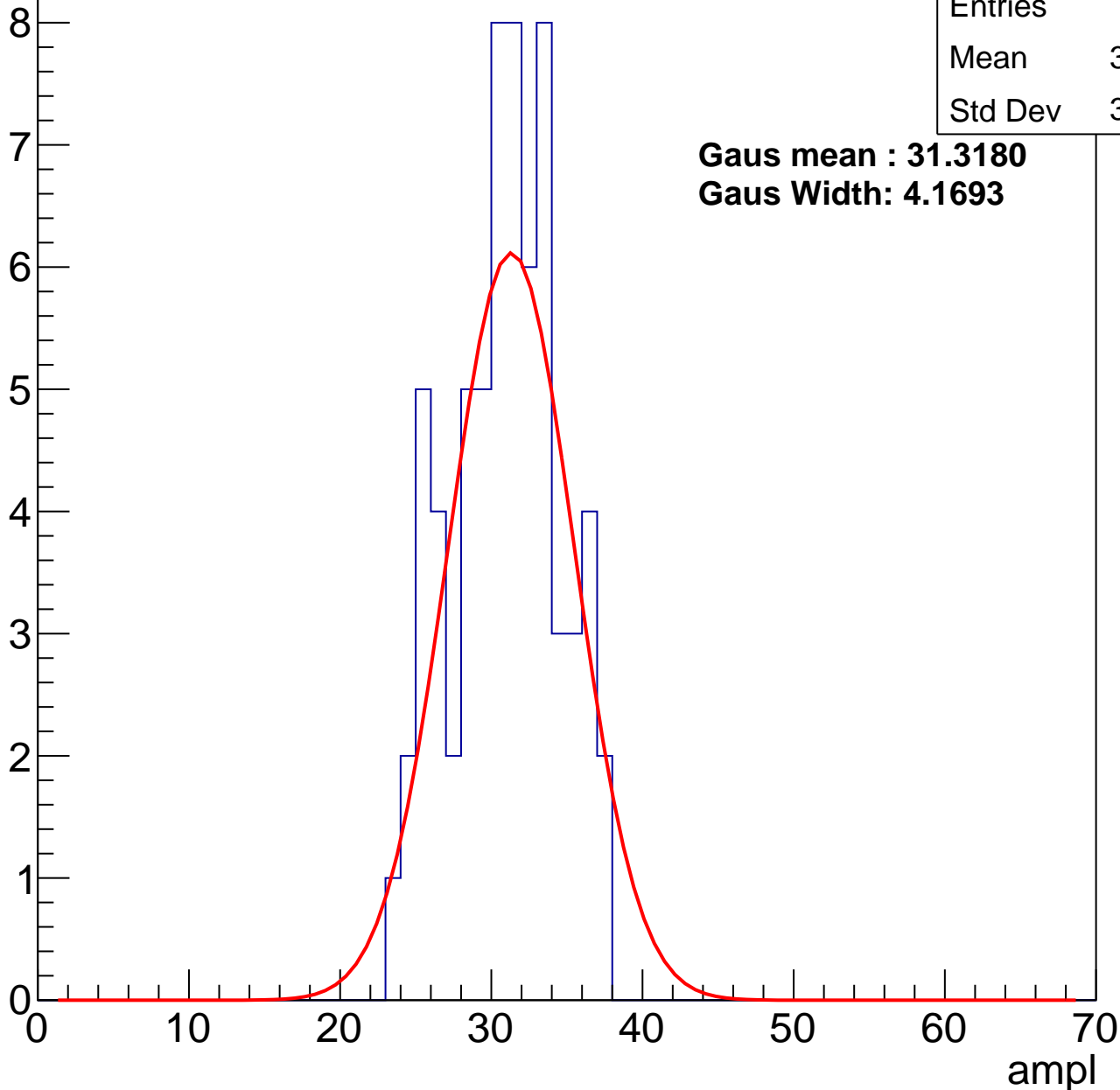
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	30.42
Std Dev	3.504

Gaus mean : 31.3180

Gaus Width: 4.1693



B1L102S, U9-ch111, adc1

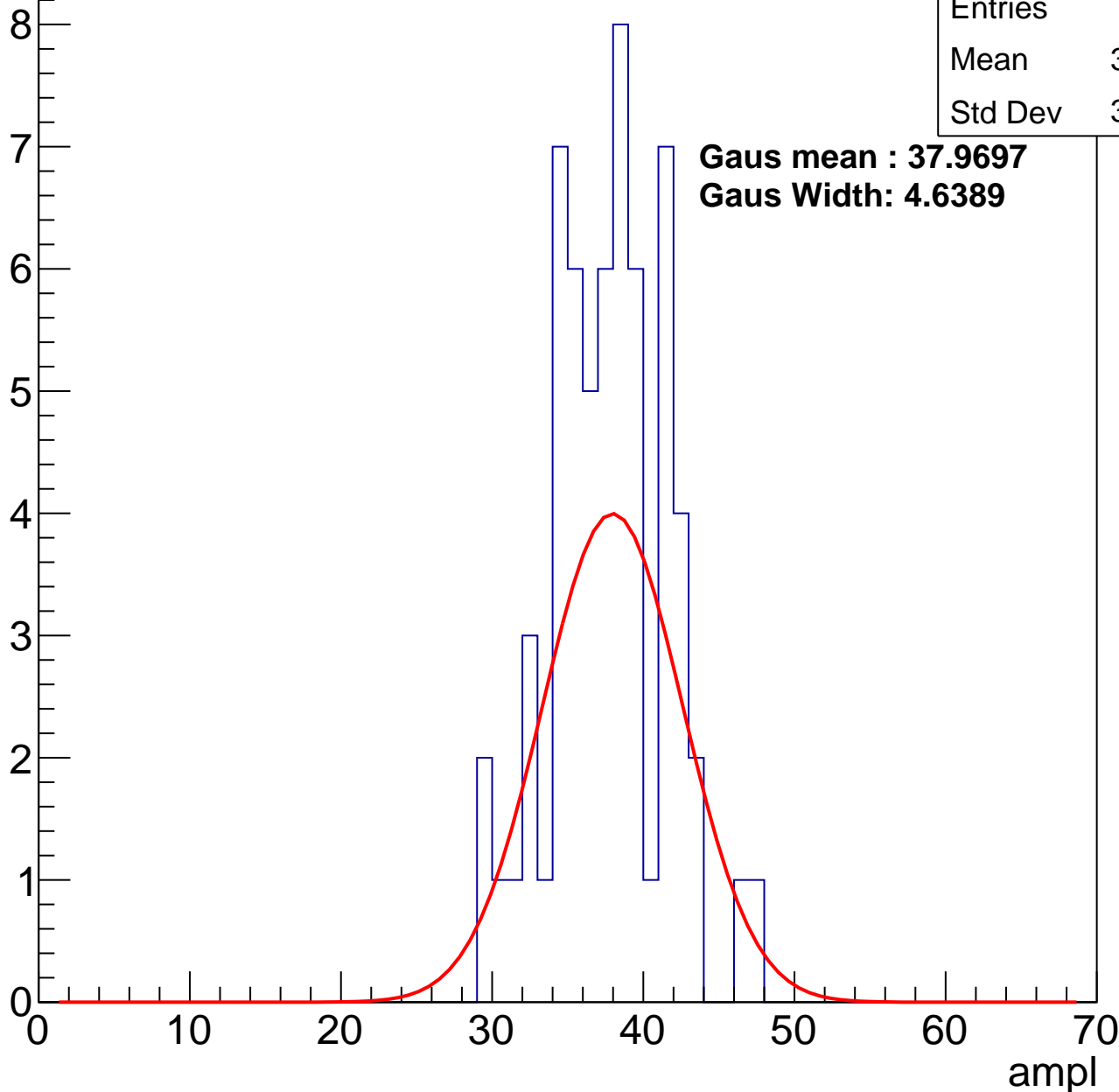
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.26
Std Dev	3.805

Gaus mean : 37.9697

Gaus Width: 4.6389



B1L102S, U9-ch111, adc2

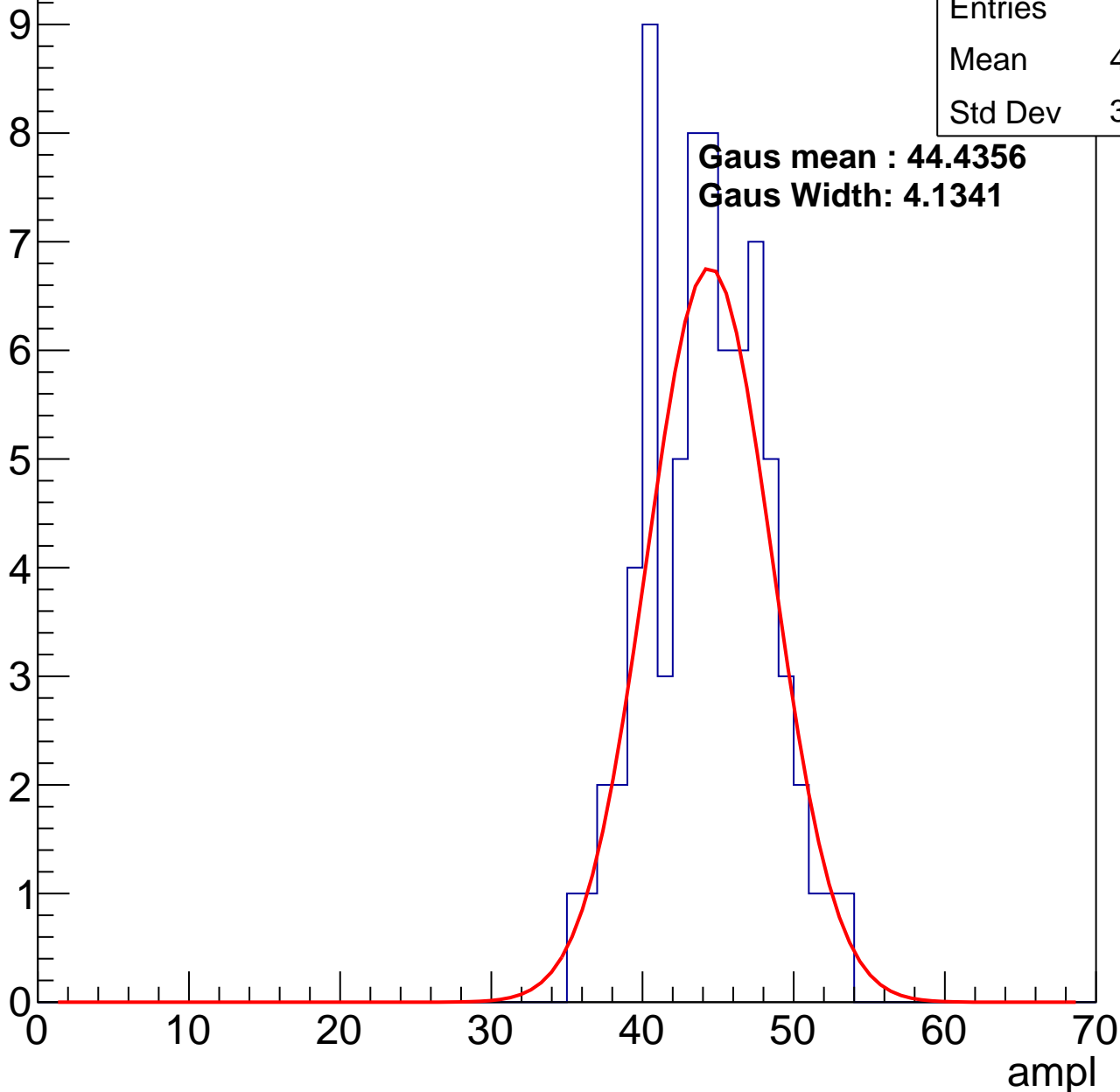
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.79
Std Dev	3.872

Gaus mean : 44.4356

Gaus Width: 4.1341

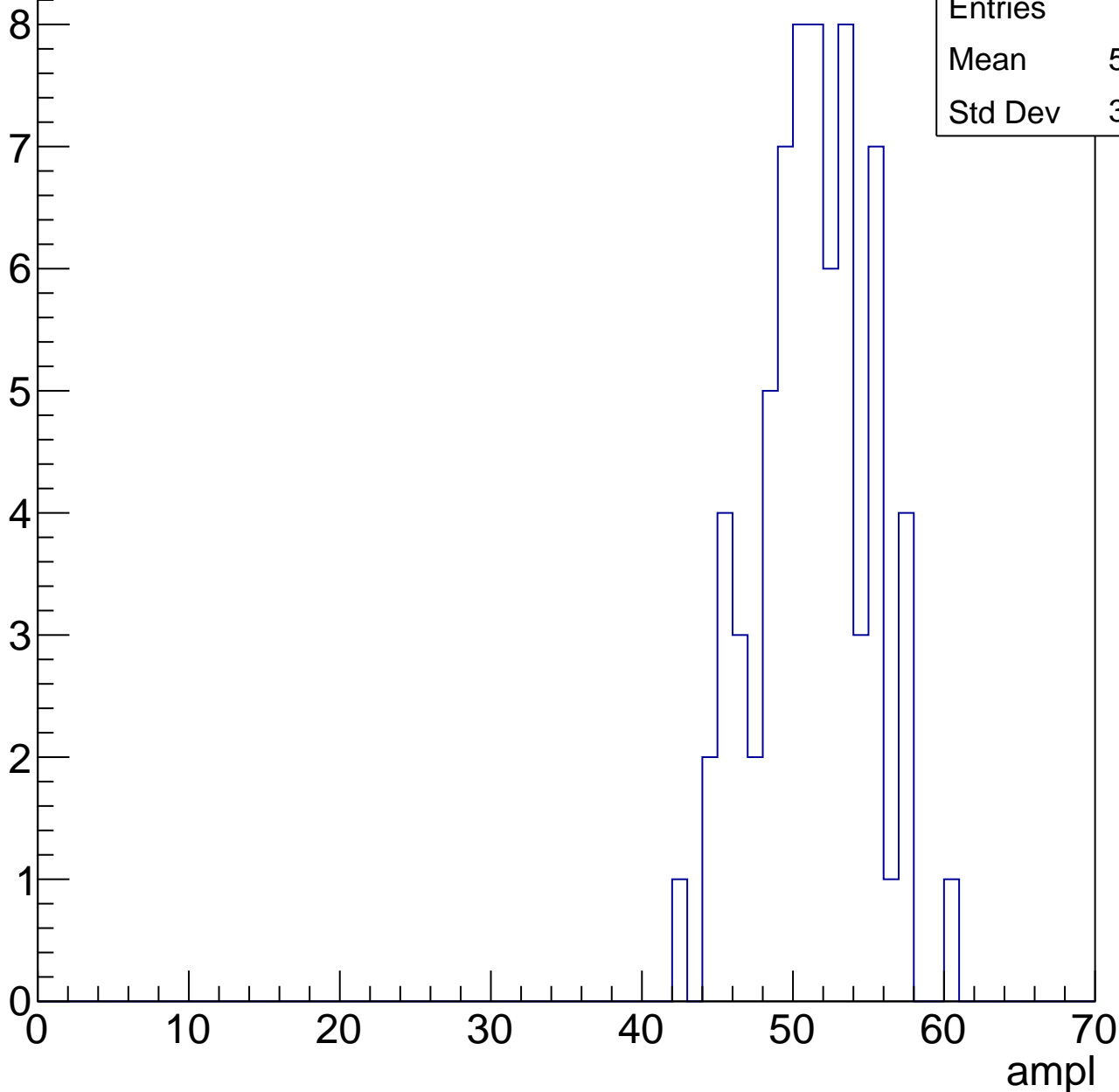


B1L102S, U9-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

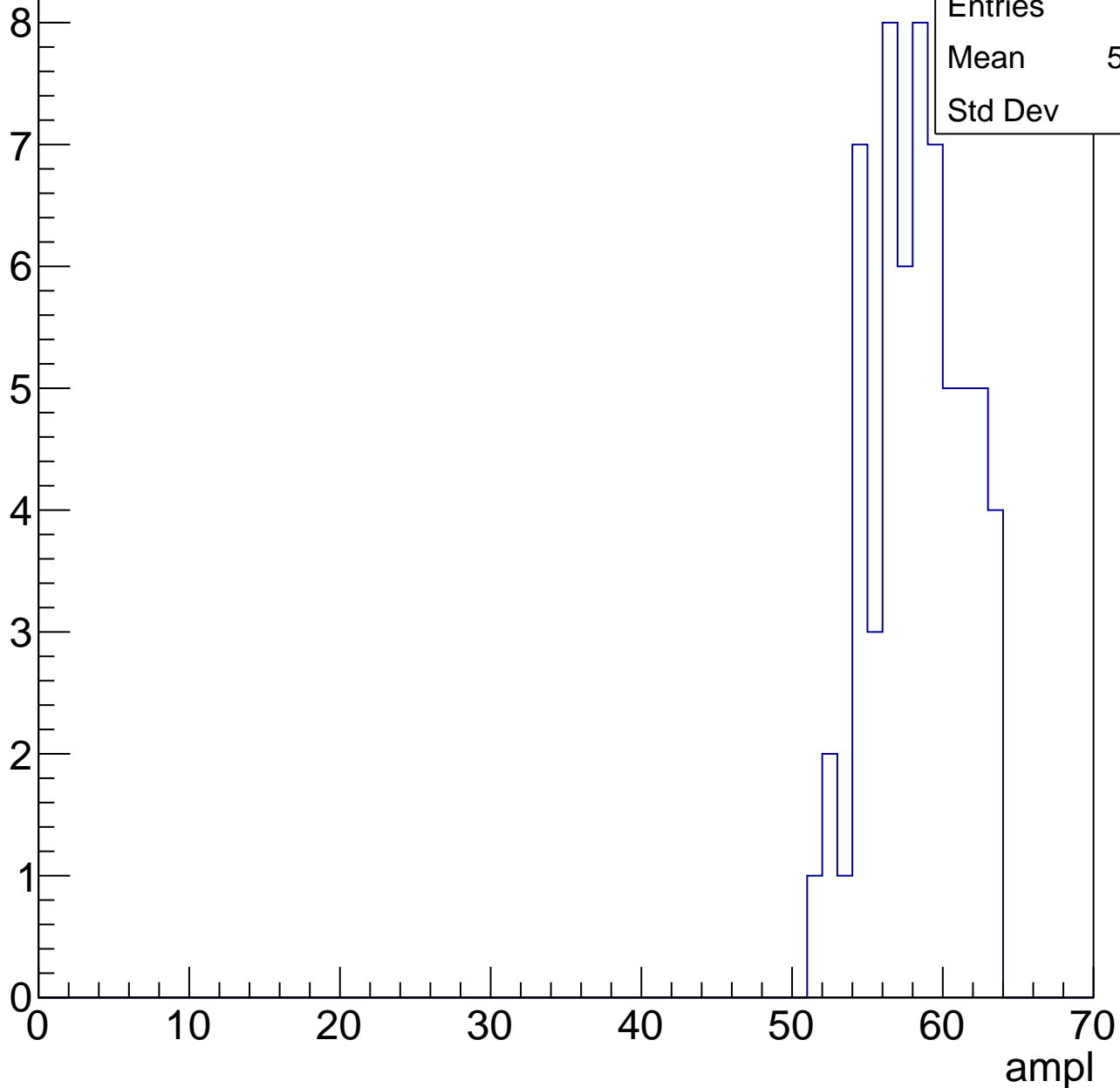
Entries	70
Mean	50.86
Std Dev	3.658



B1L102S, U9-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



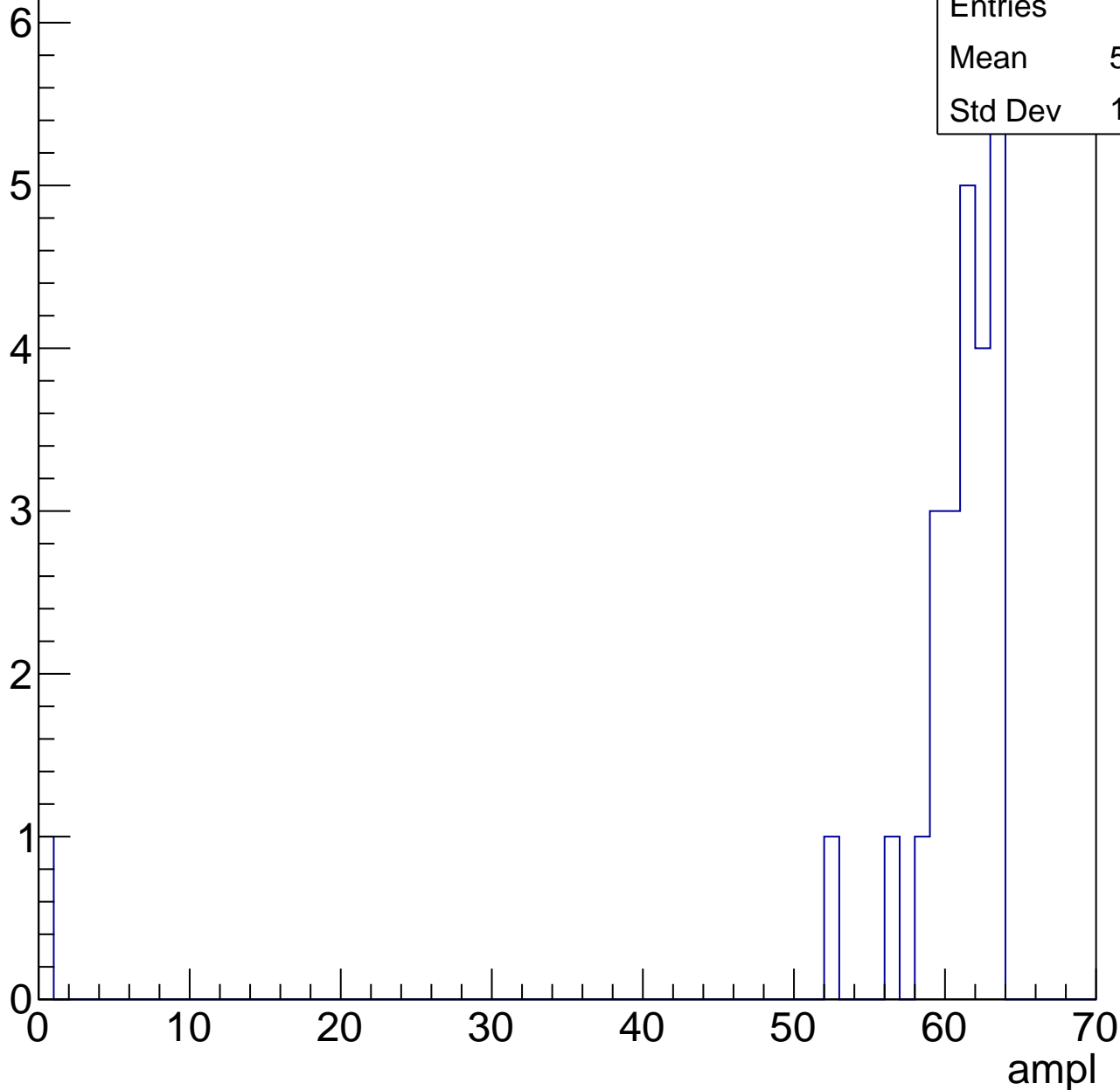
Entries	62
Mean	57.82
Std Dev	3.04

B1L102S, U9-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

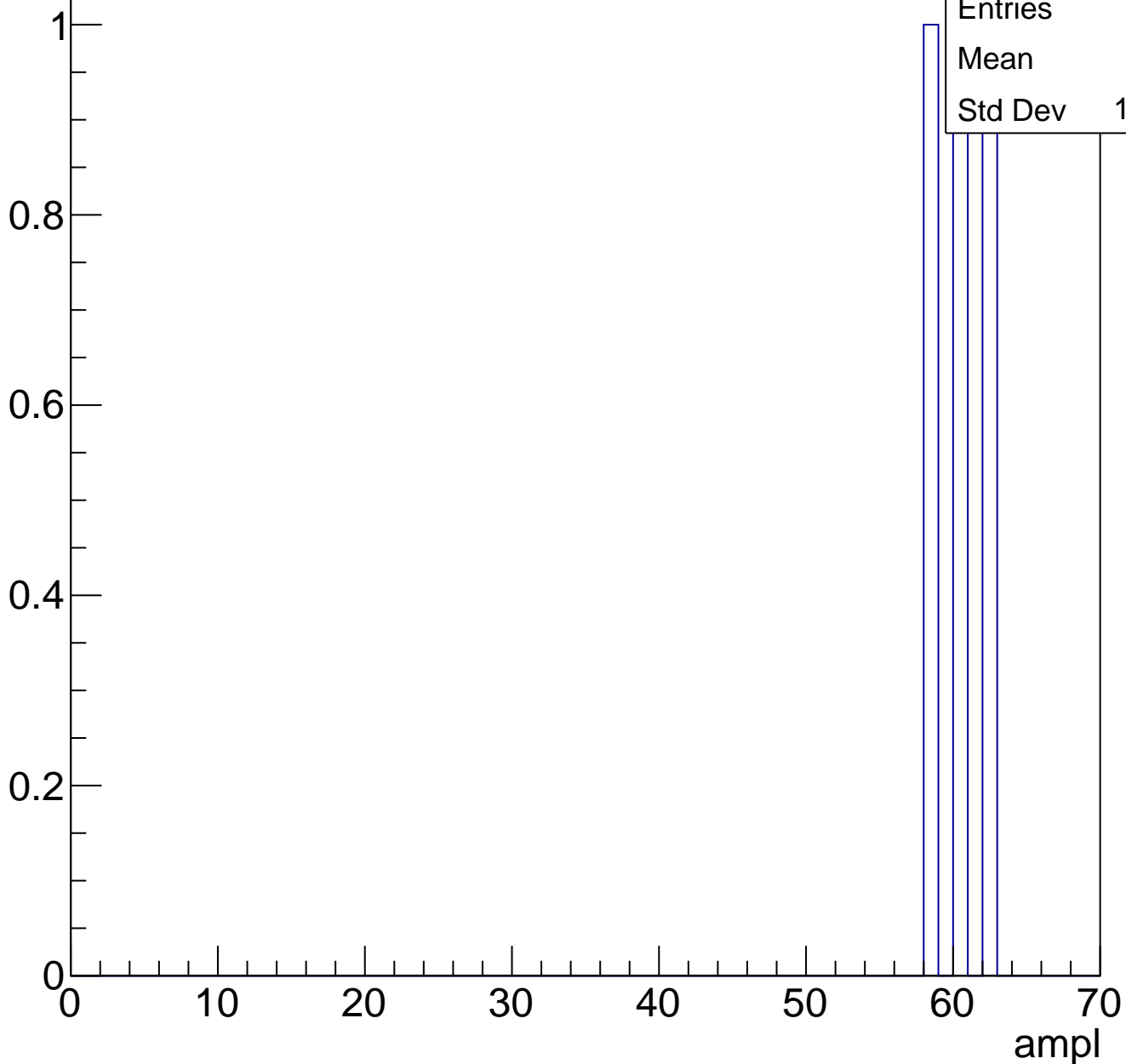
Entries	25
Mean	58.16
Std Dev	12.13



B1L102S, U9-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U9-ch112, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	29.44
Std Dev	3.896

Gaus mean : 29.8860

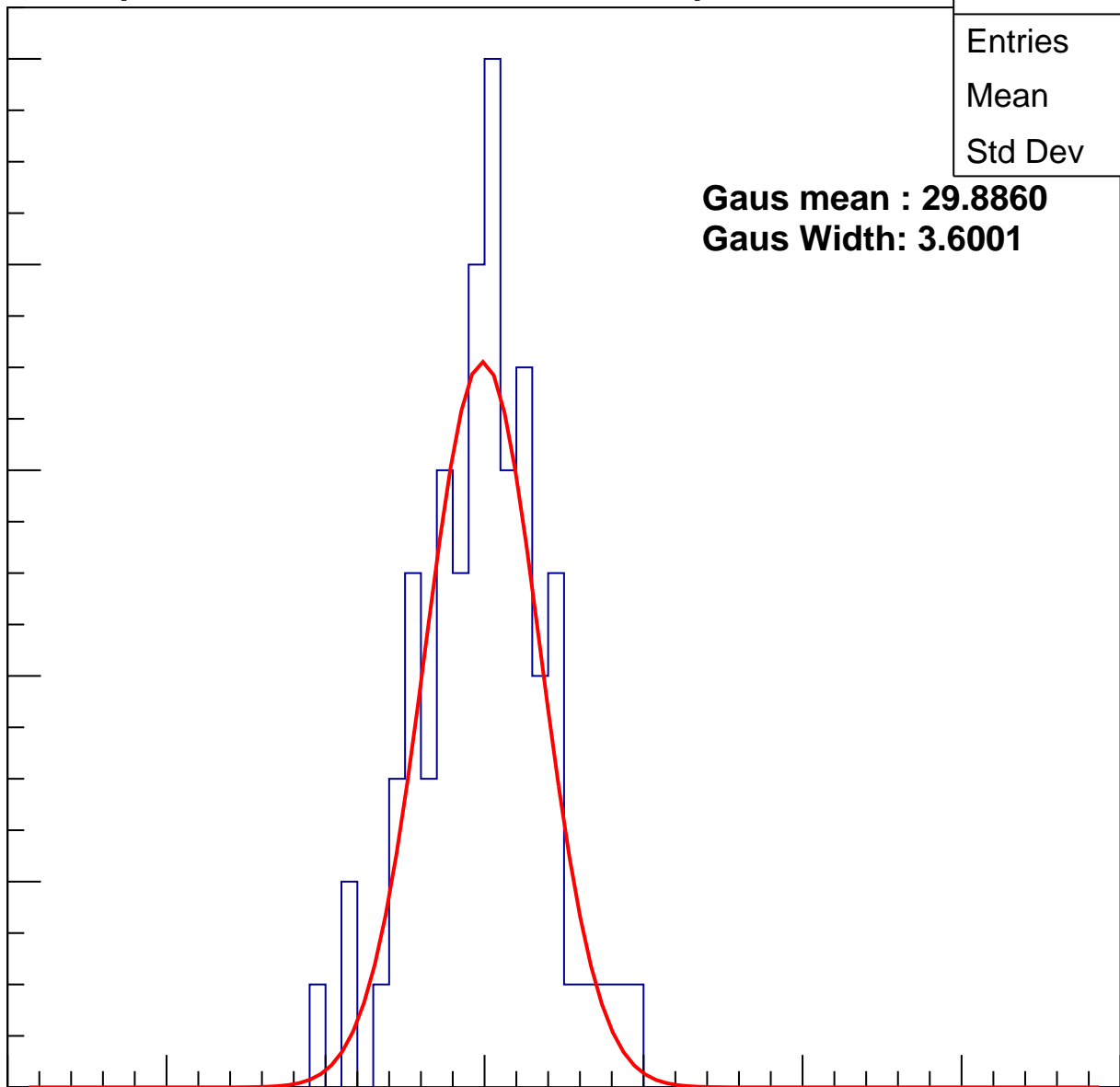
Gaus Width: 3.6001

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch112, adc1

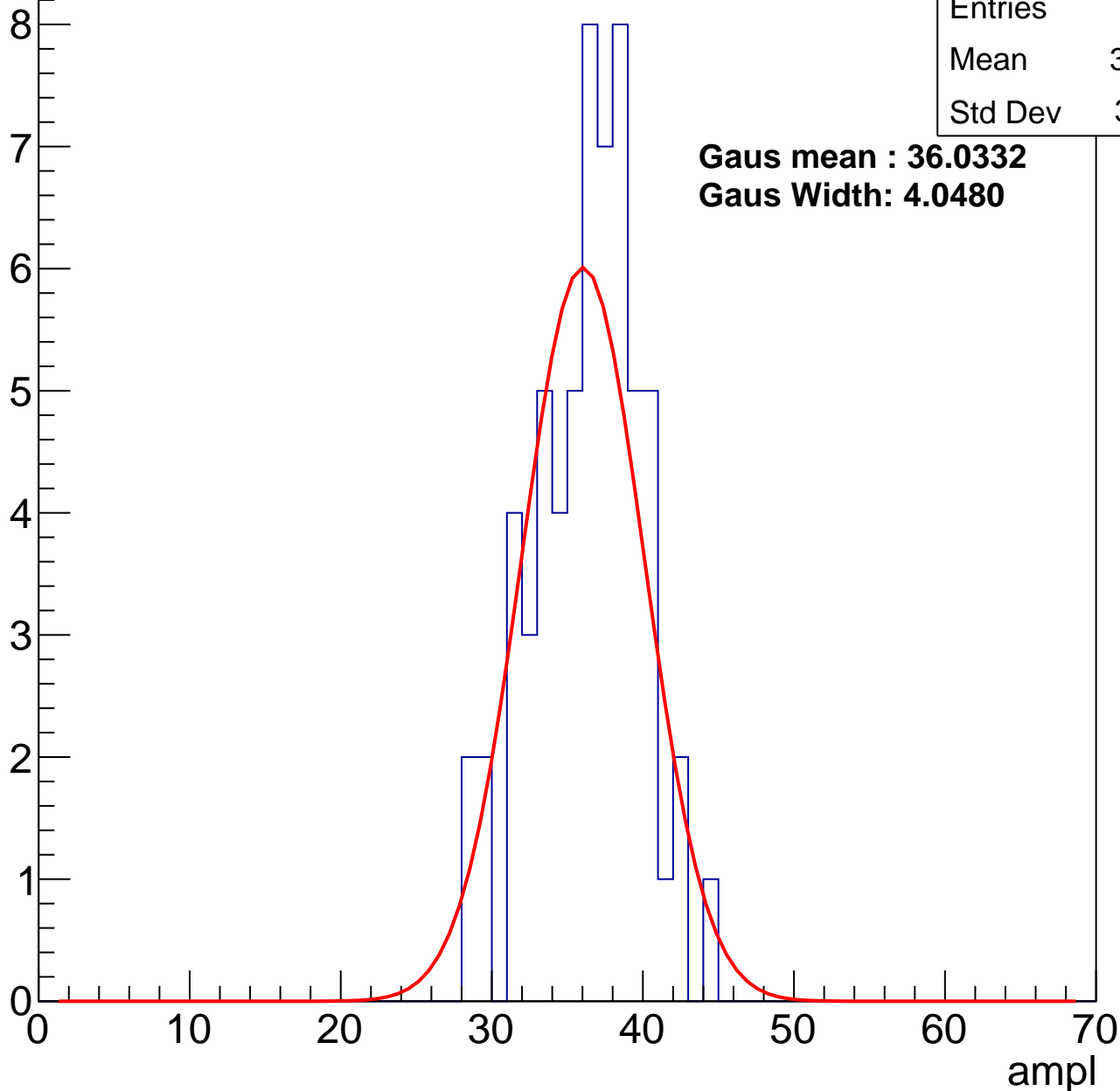
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.89
Std Dev	3.511

Gaus mean : 36.0332

Gaus Width: 4.0480



B1L102S, U9-ch112, adc2

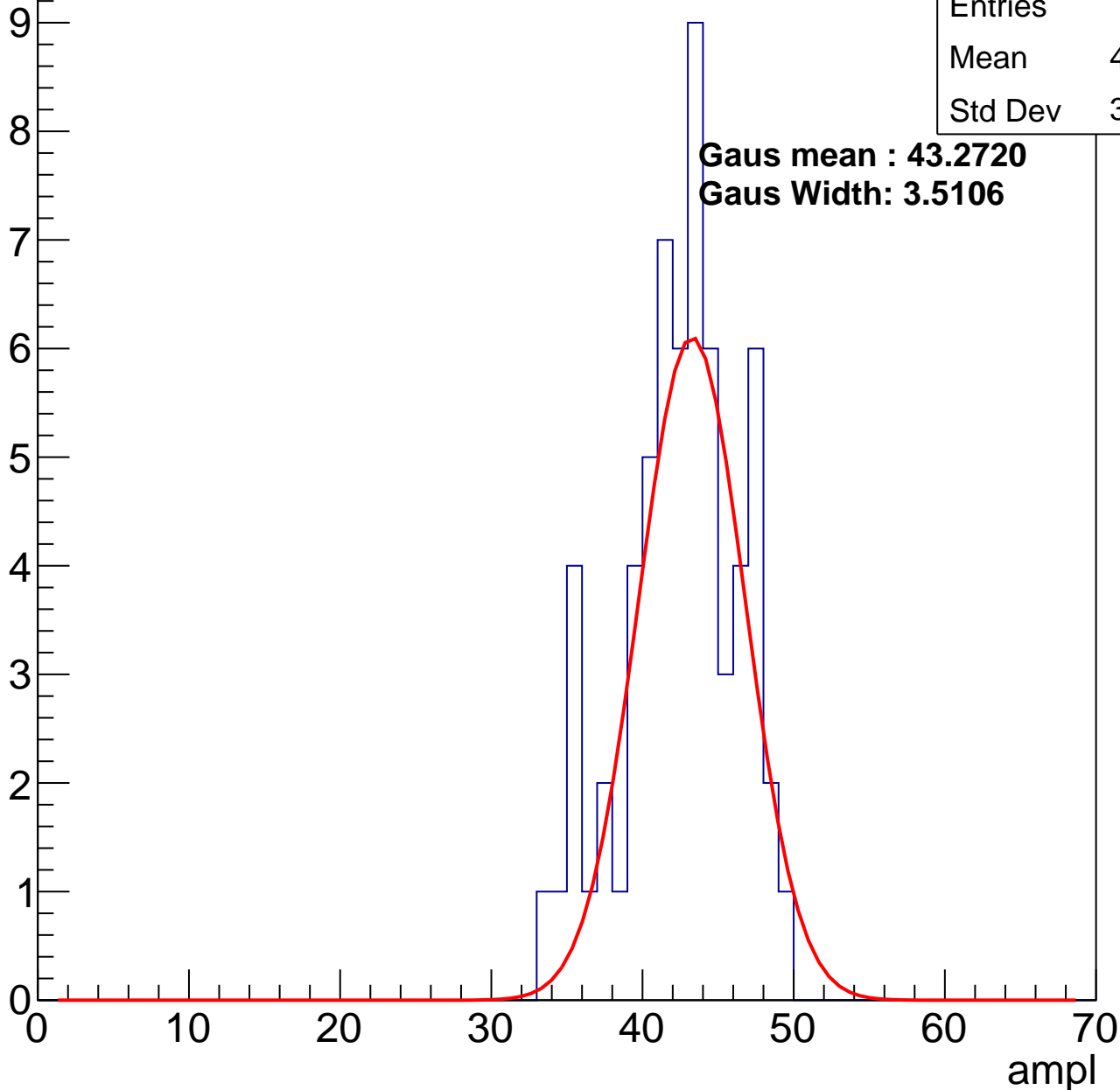
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.02
Std Dev	3.782

Gaus mean : 43.2720

Gaus Width: 3.5106

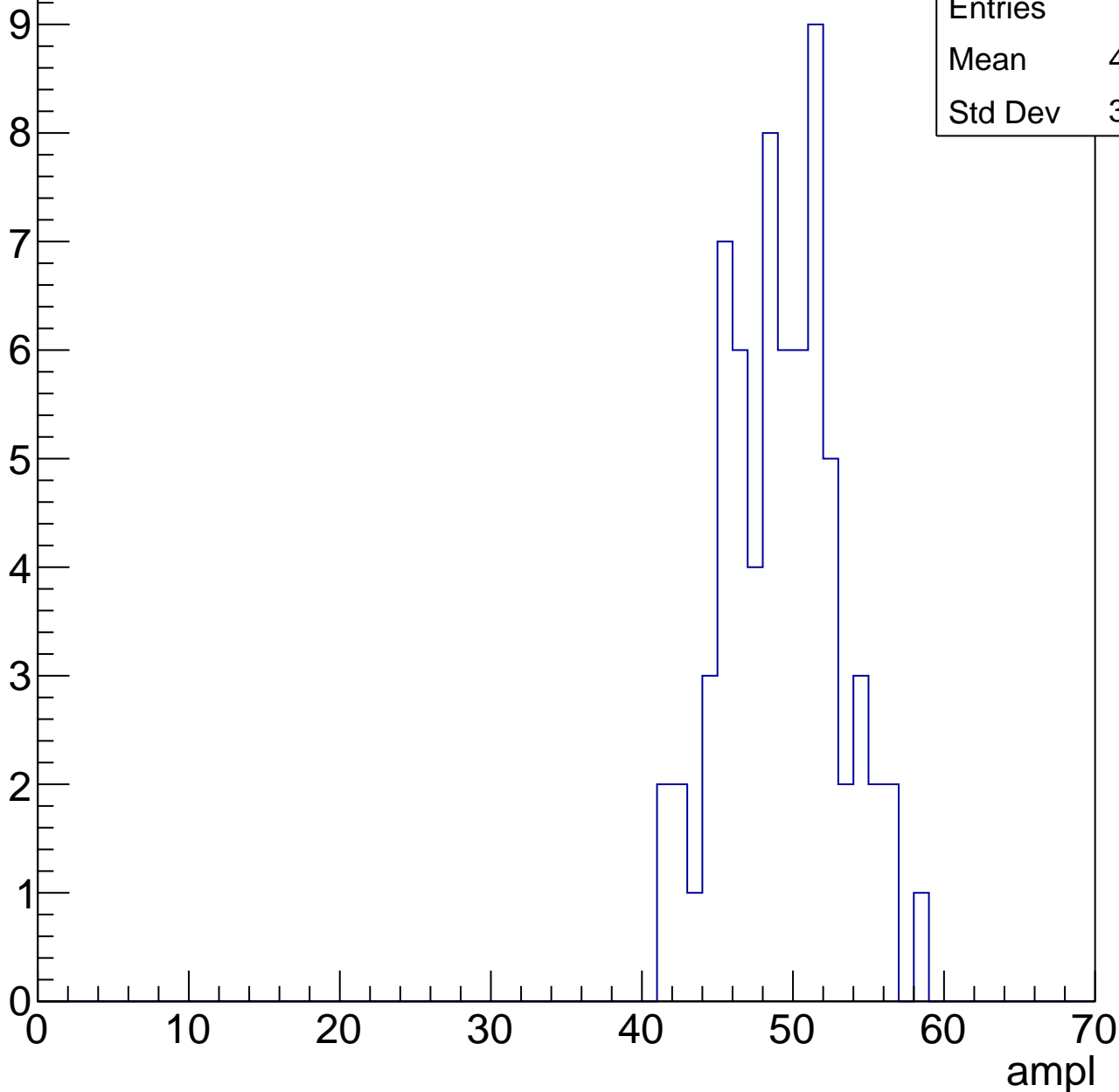


B1L102S, U9-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.77
Std Dev	3.746

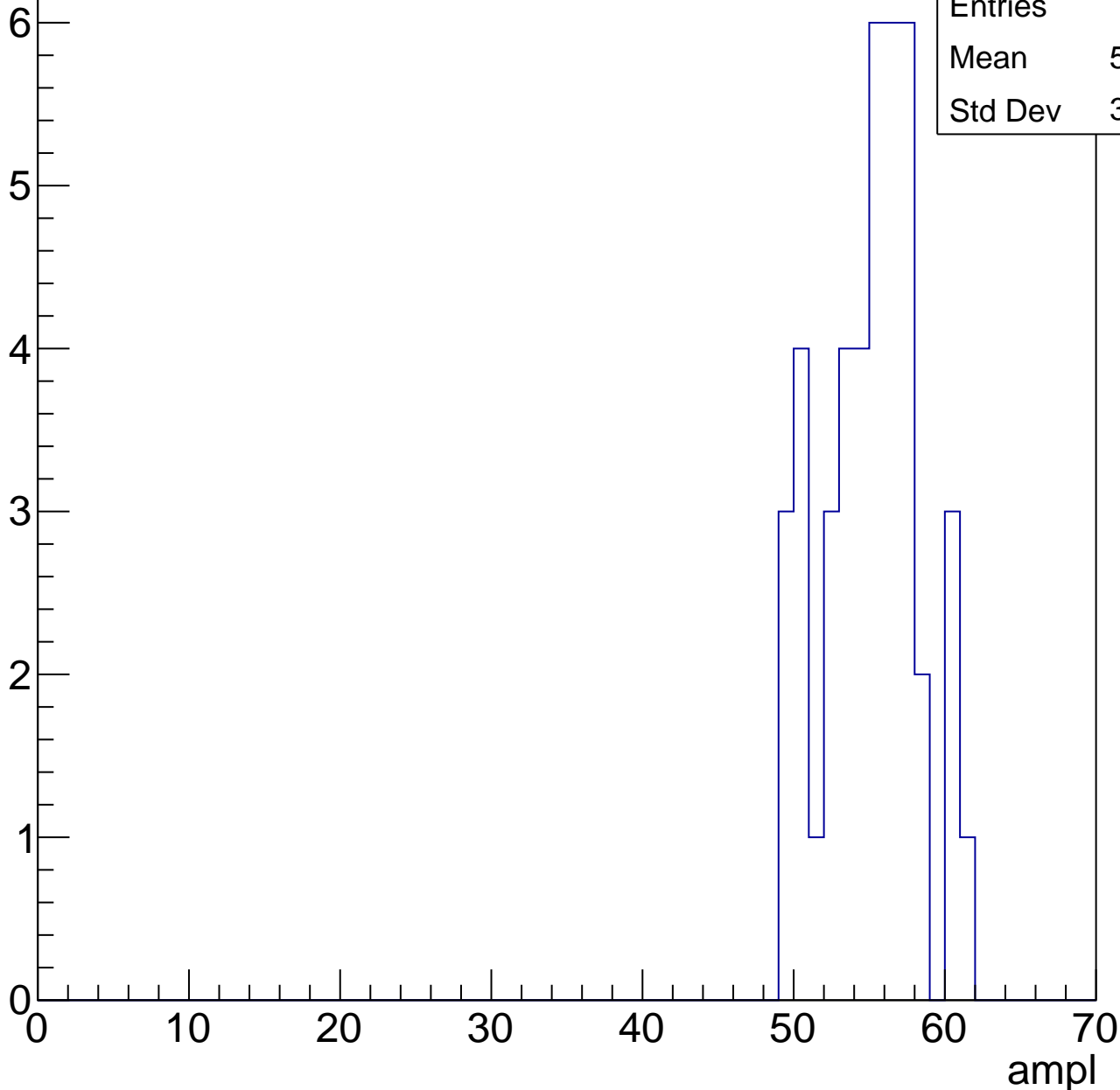


B1L102S, U9-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	54.58
Std Dev	3.134

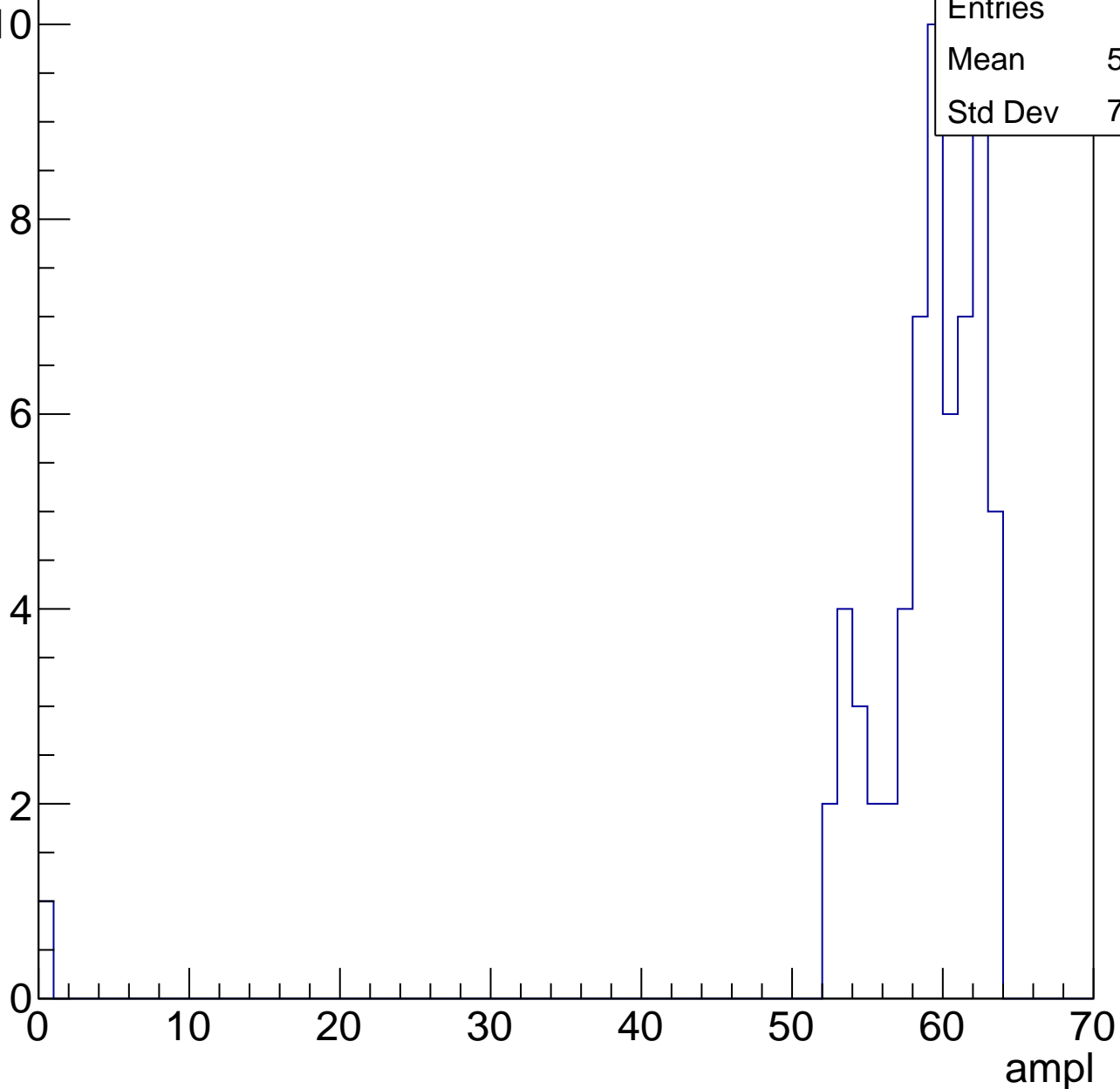


B1L102S, U9-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	57.87
Std Dev	7.963

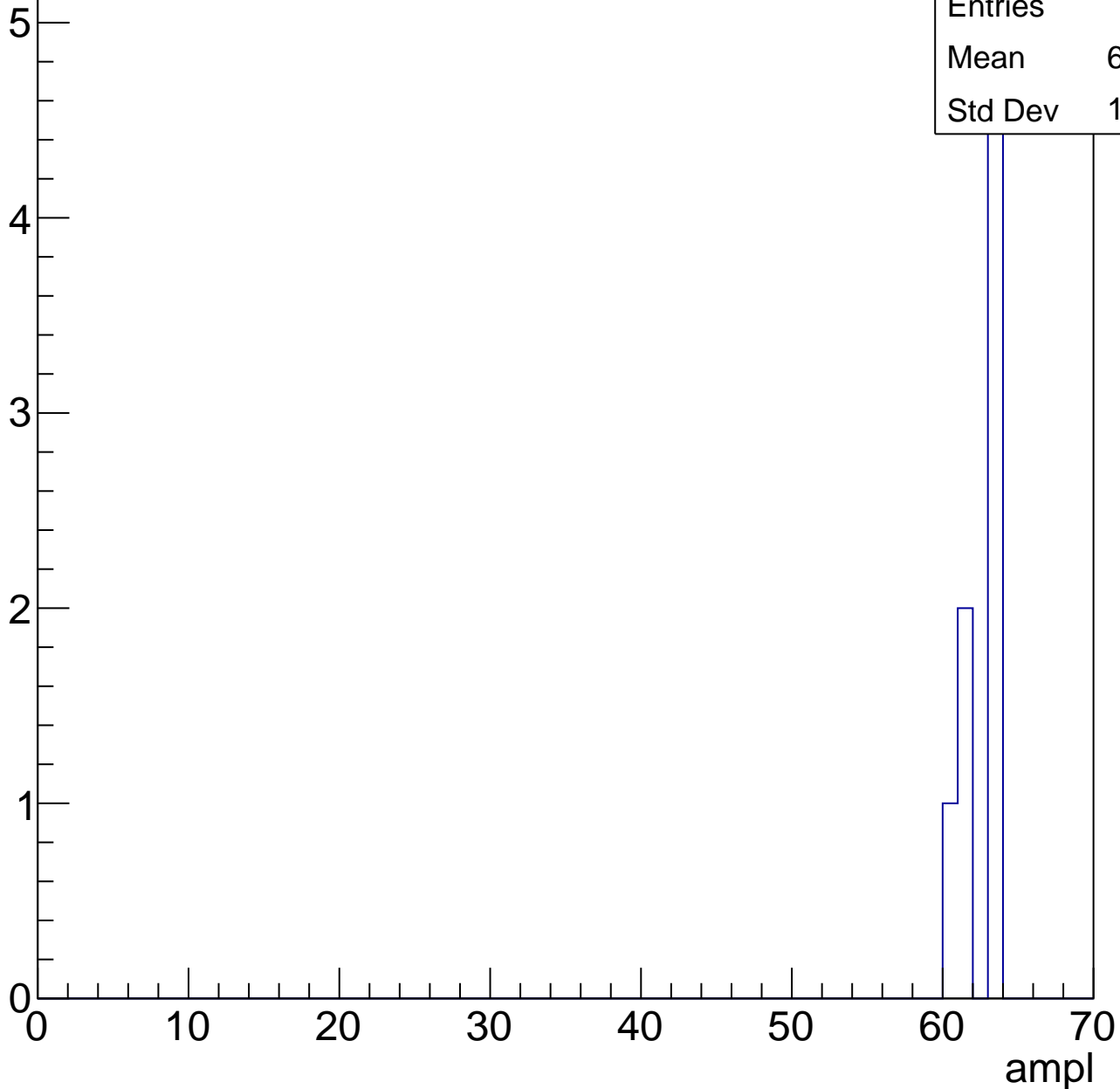


B1L102S, U9-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	62.12
Std Dev	1.166



B1L102S, U9-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch113, adc0

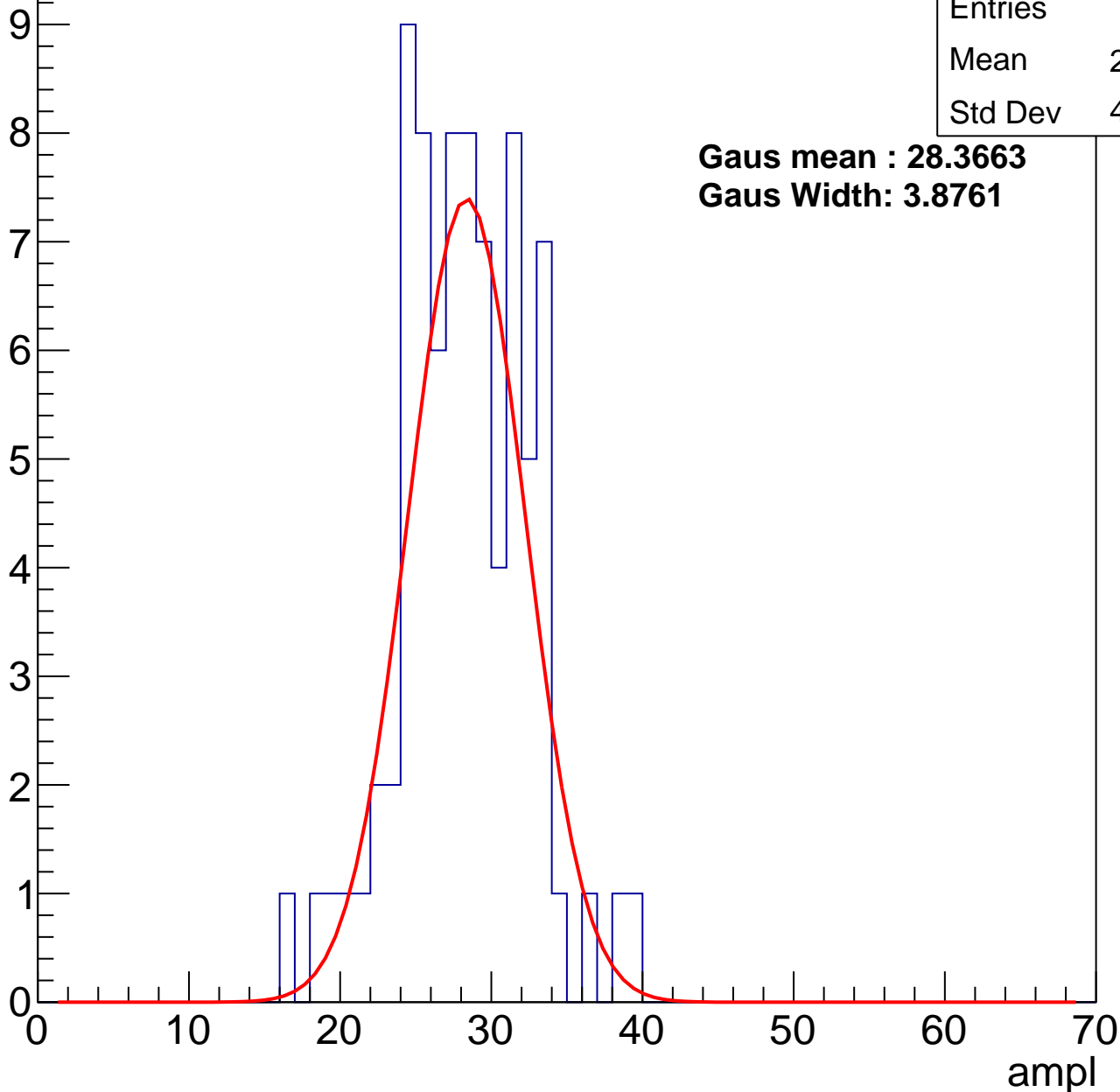
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.77
Std Dev	4.215

Gaus mean : 28.3663

Gaus Width: 3.8761



B1L102S, U9-ch113, adc1

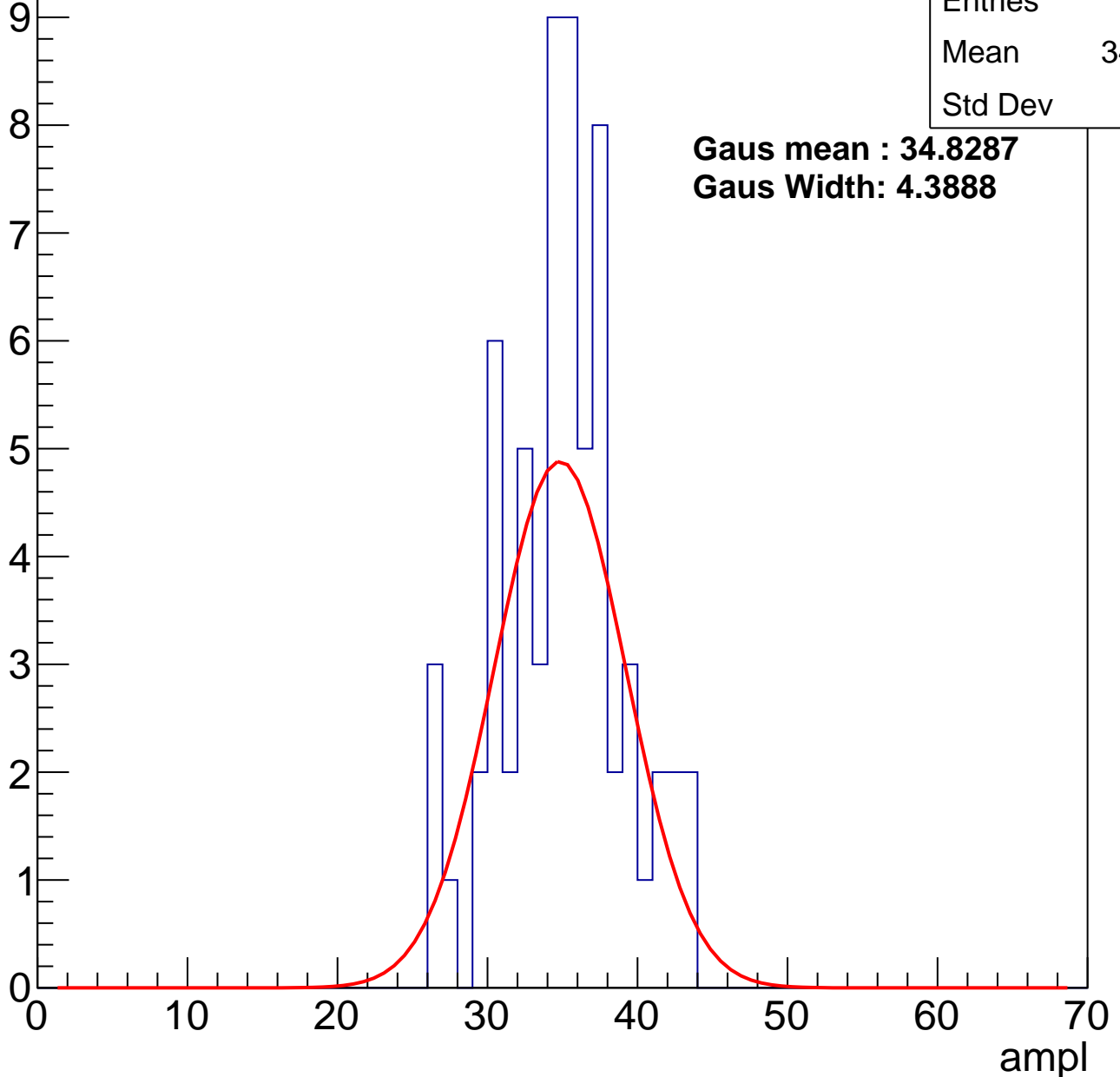
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.55
Std Dev	4

Gaus mean : 34.8287

Gaus Width: 4.3888



B1L102S, U9-ch113, adc2

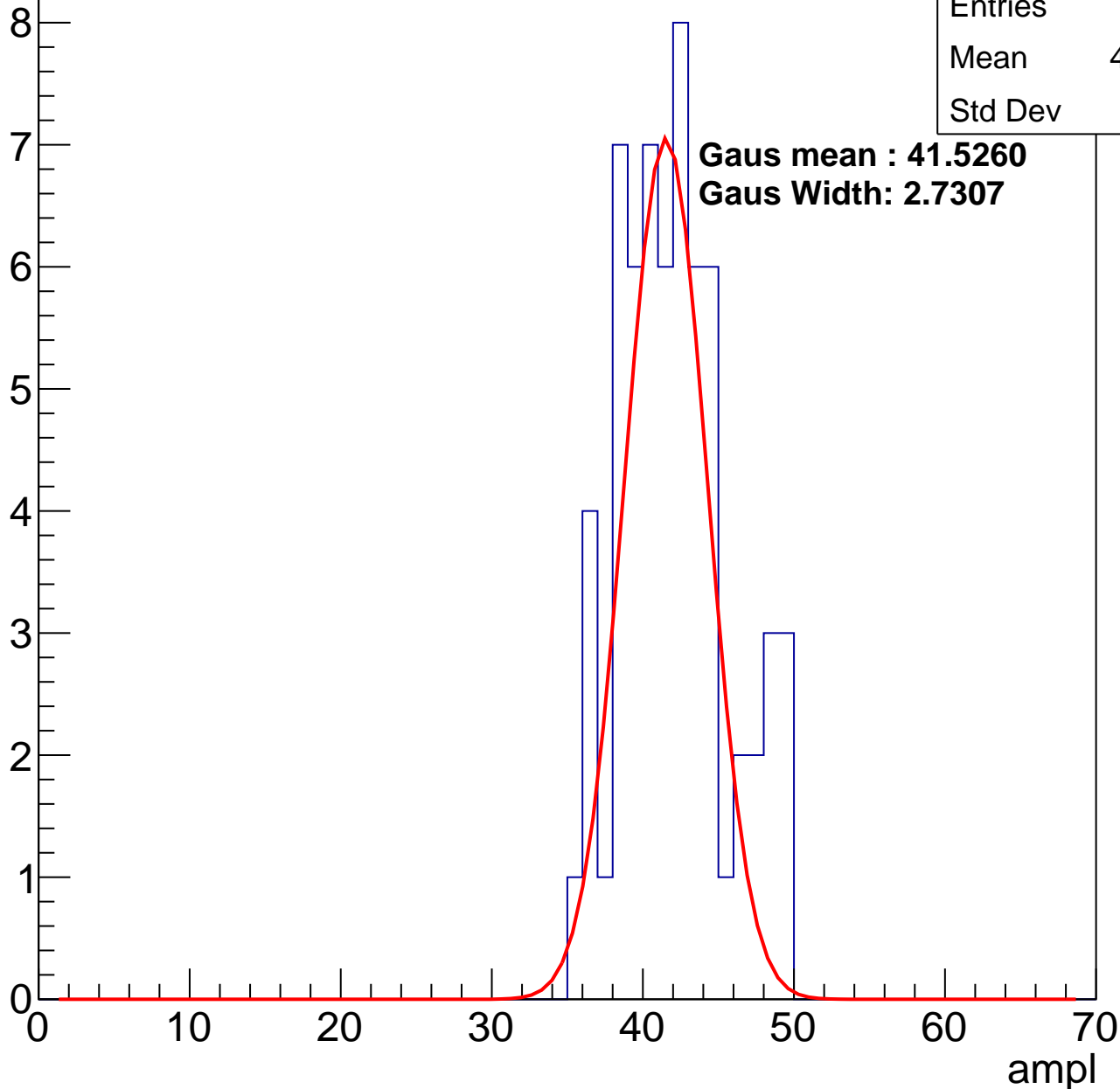
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.62
Std Dev	3.53

Gaus mean : 41.5260

Gaus Width: 2.7307

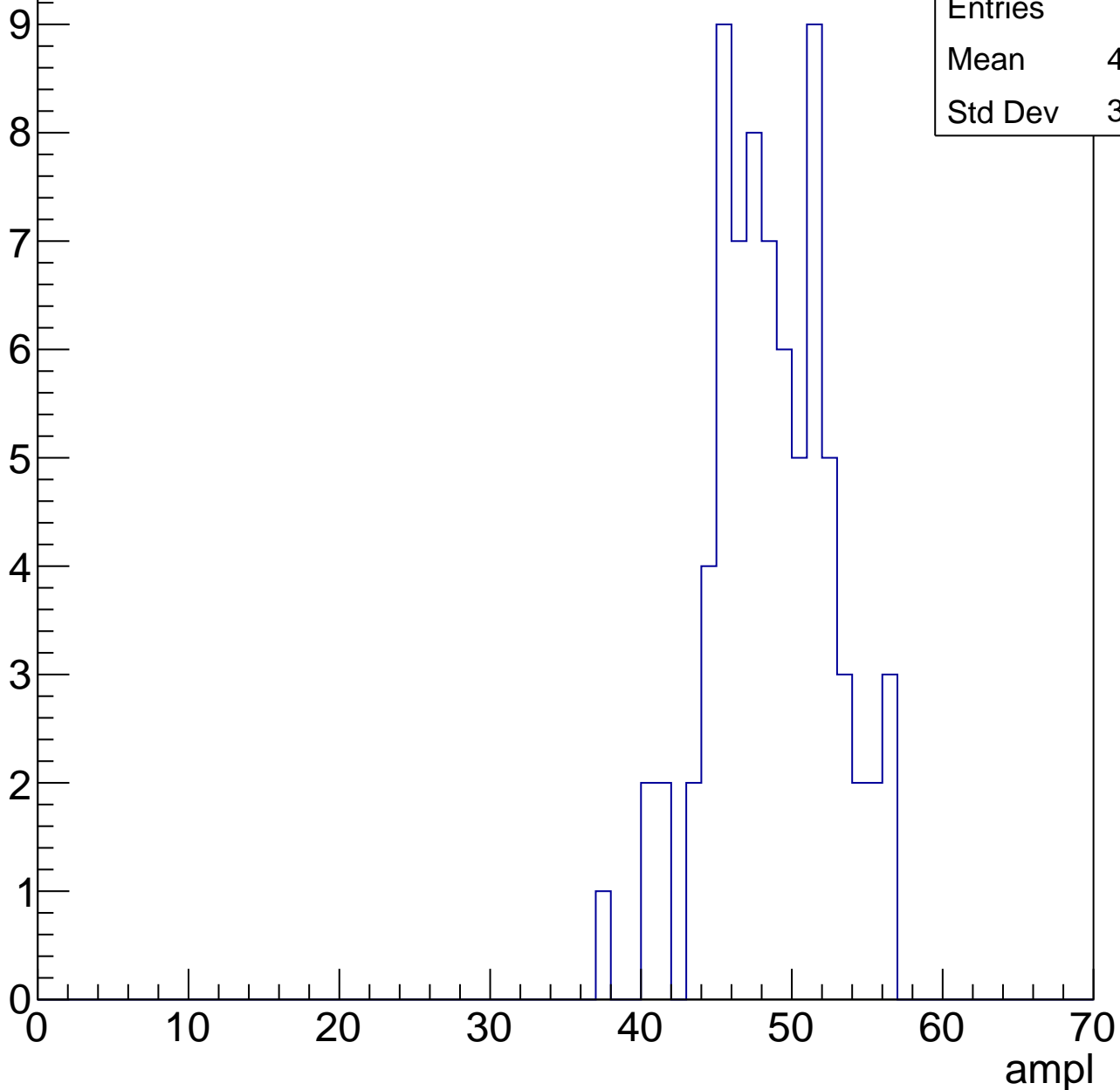


B1L102S, U9-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.16
Std Dev	3.948

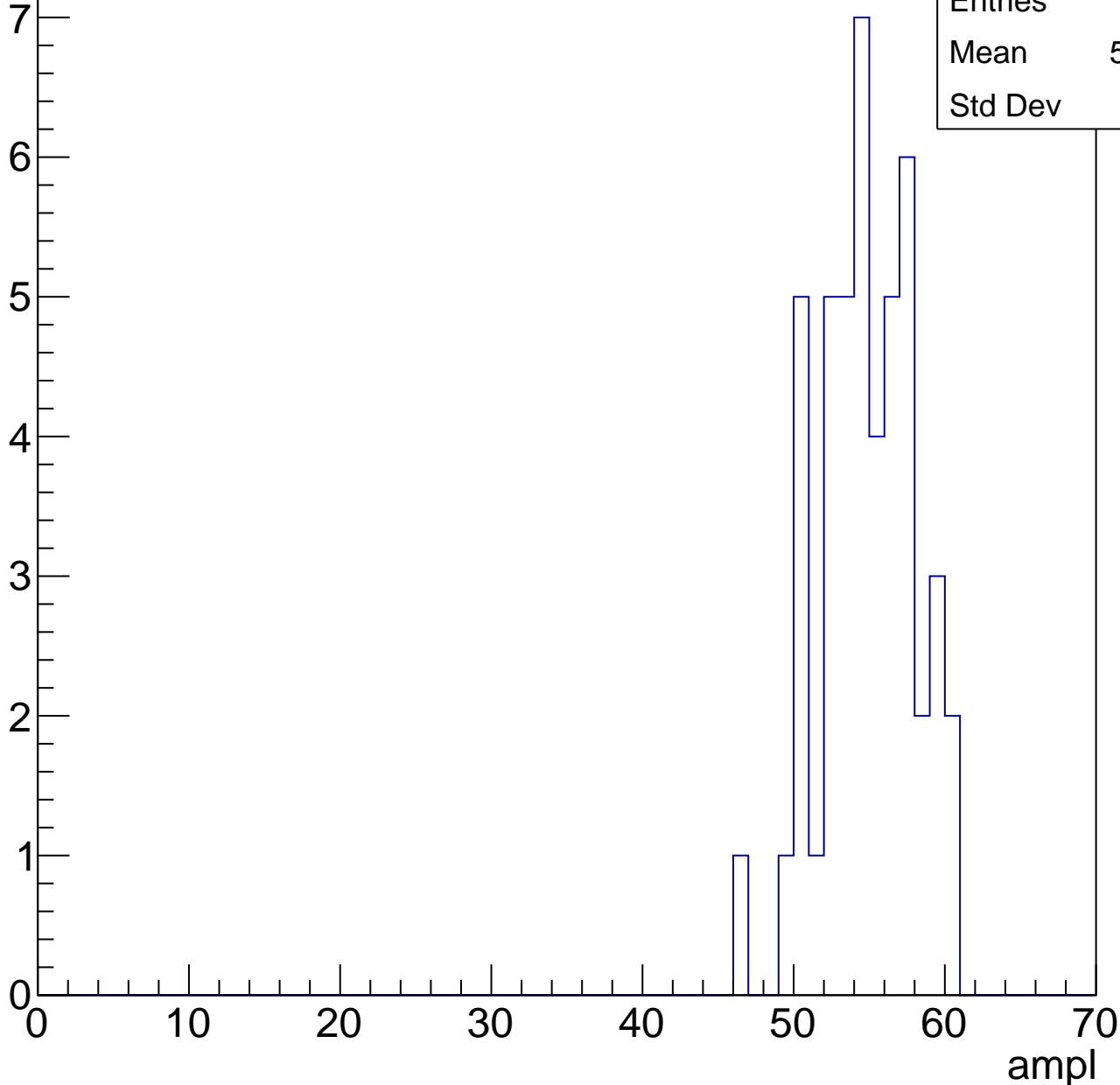


B1L102S, U9-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.34
Std Dev	3.11



B1L102S, U9-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	57
Mean	59.32
Std Dev	2.683

Entry

10

8

6

4

2

0

0

10

20

30

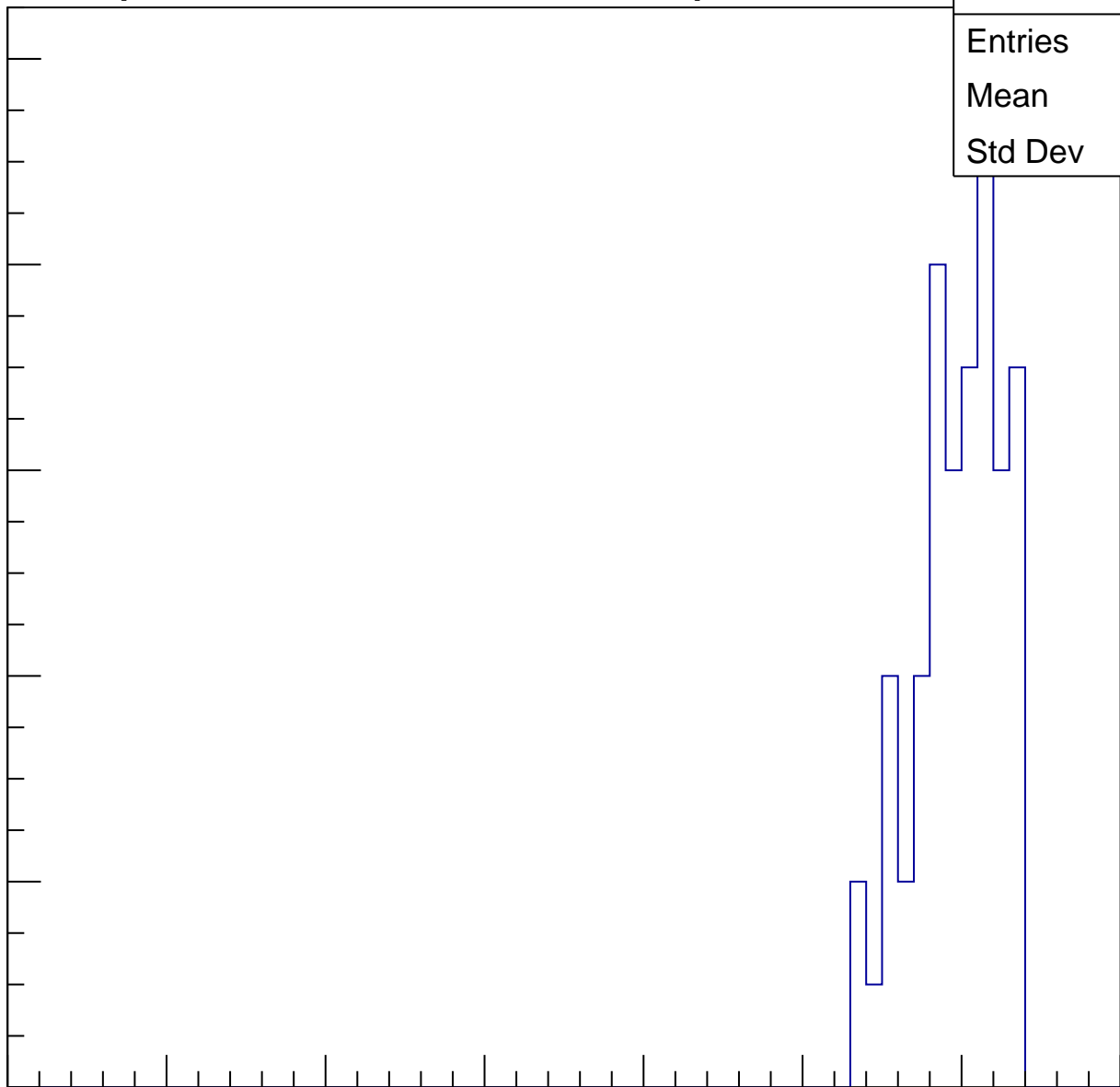
40

50

60

70

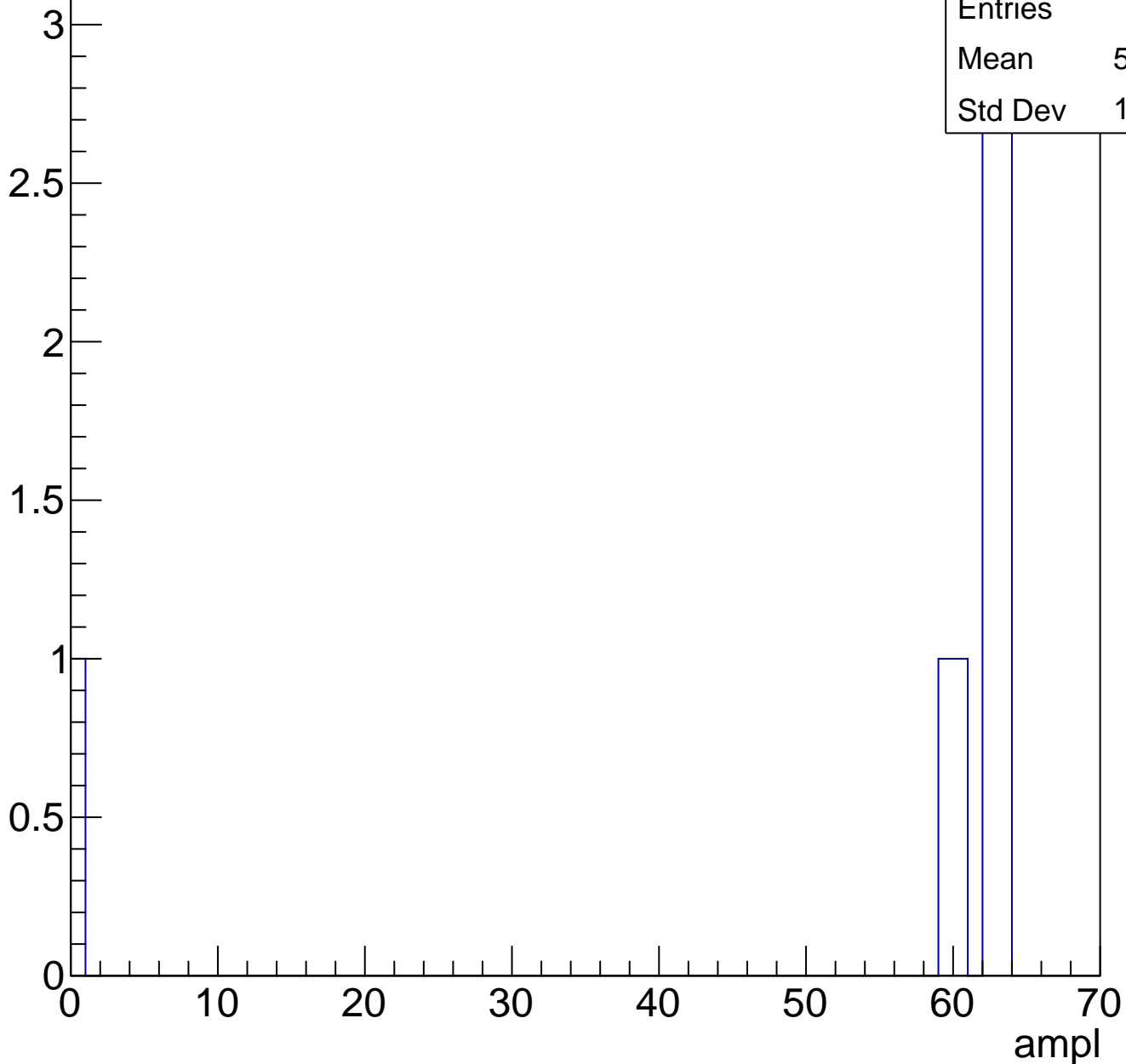
ampl



B1L102S, U9-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	9
Mean	54.89
Std Dev	19.45

B1L102S, U9-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch114, adc0

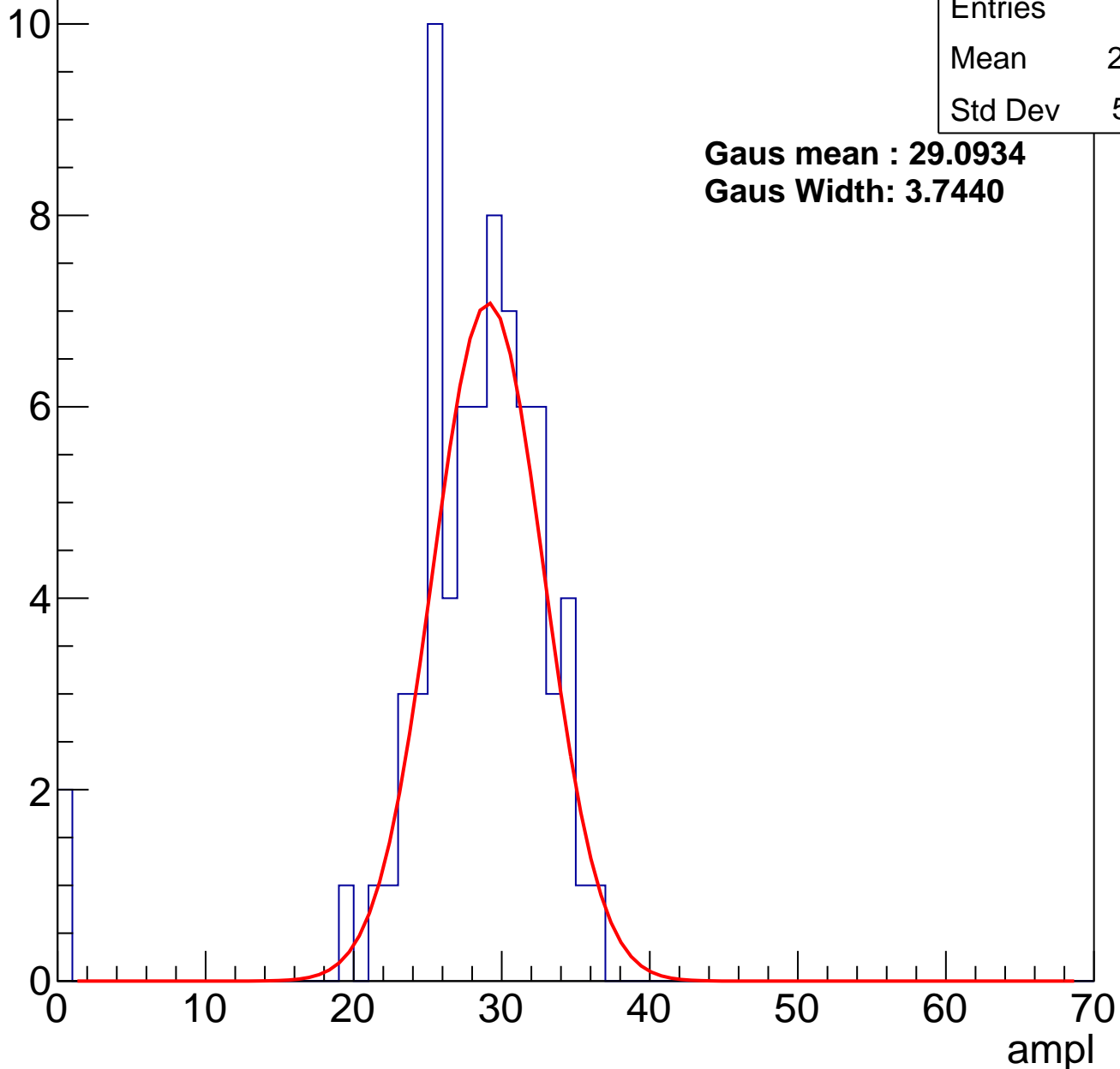
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	27.58
Std Dev	5.821

Gaus mean : 29.0934

Gaus Width: 3.7440

Entry



B1L102S, U9-ch114, adc1

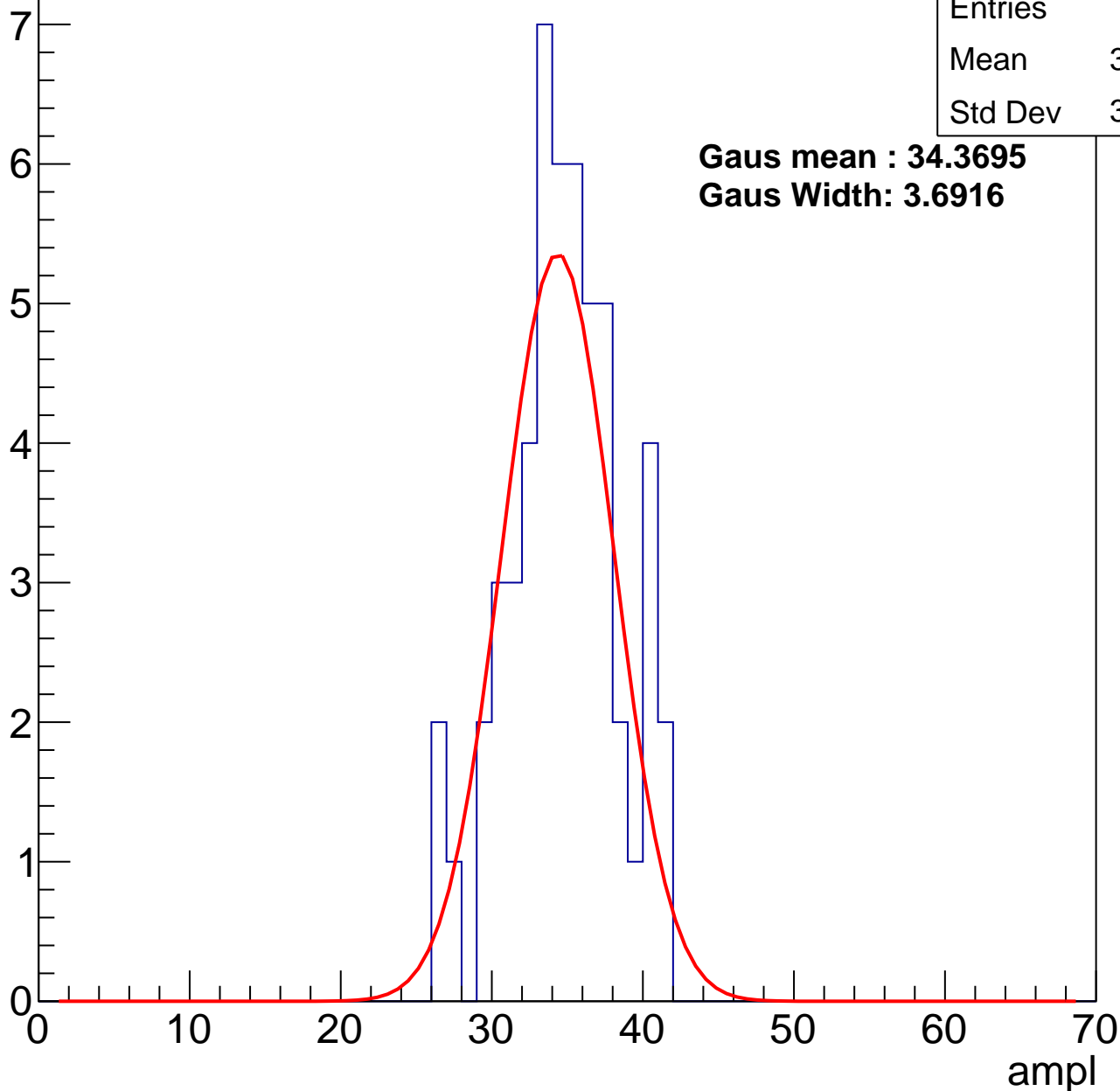
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	34.25
Std Dev	3.613

Gaus mean : 34.3695

Gaus Width: 3.6916



B1L102S, U9-ch114, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	41.24
Std Dev	3.692

Gaus mean : 41.3264

Gaus Width: 4.3299

10

8

6

4

2

0

0

10

20

30

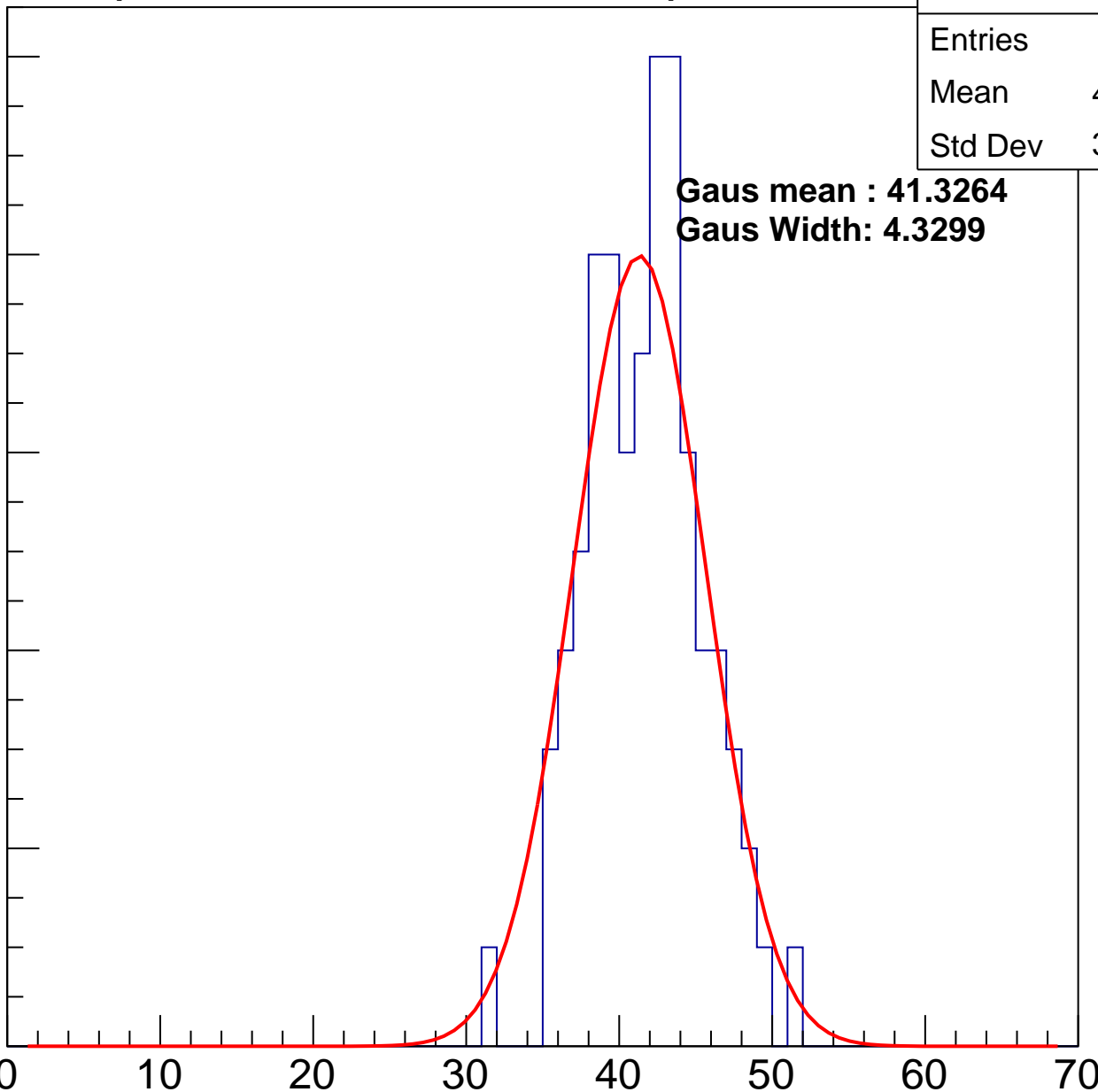
40

50

60

70

ampl

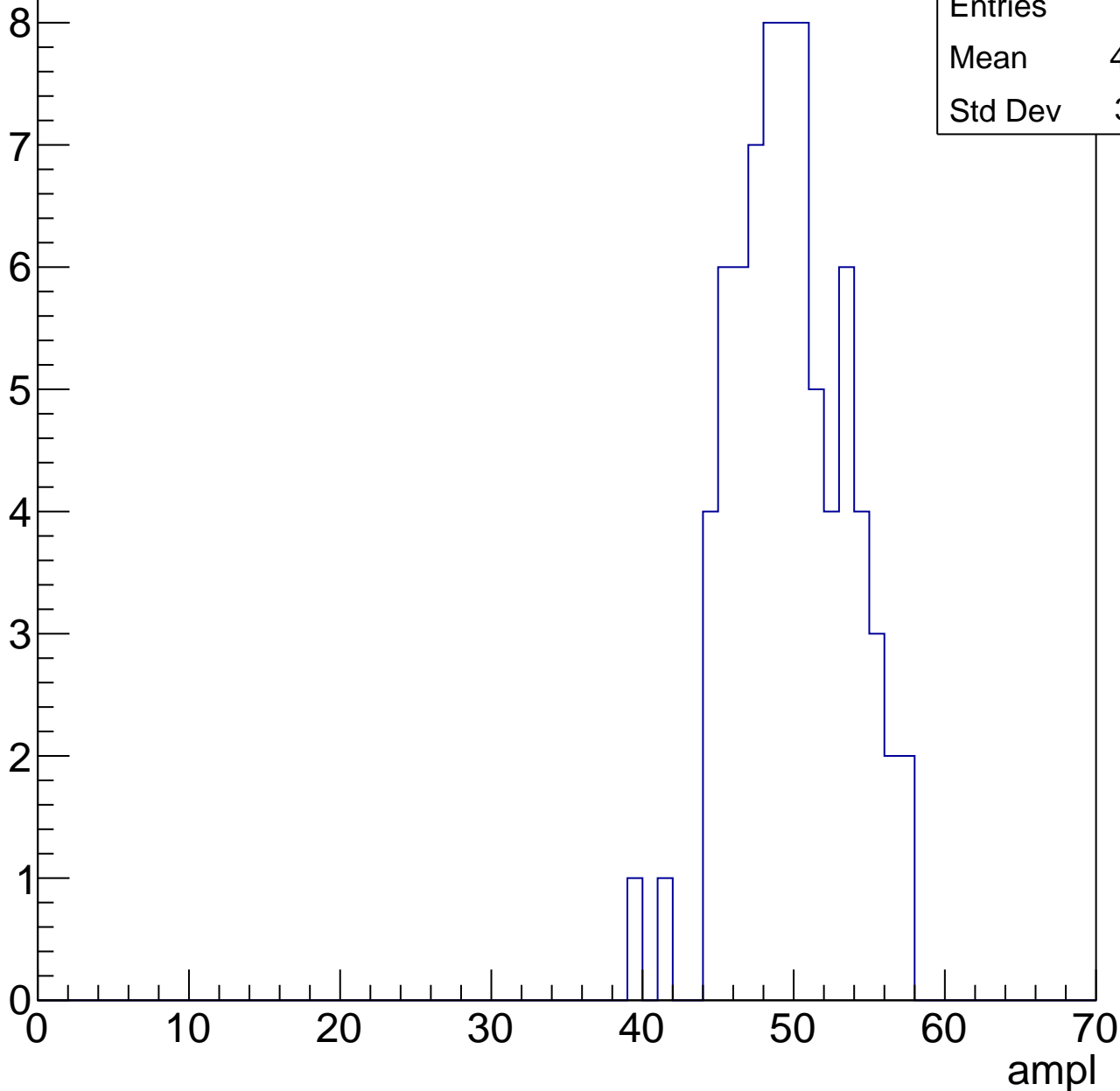


B1L102S, U9-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	49.27
Std Dev	3.721

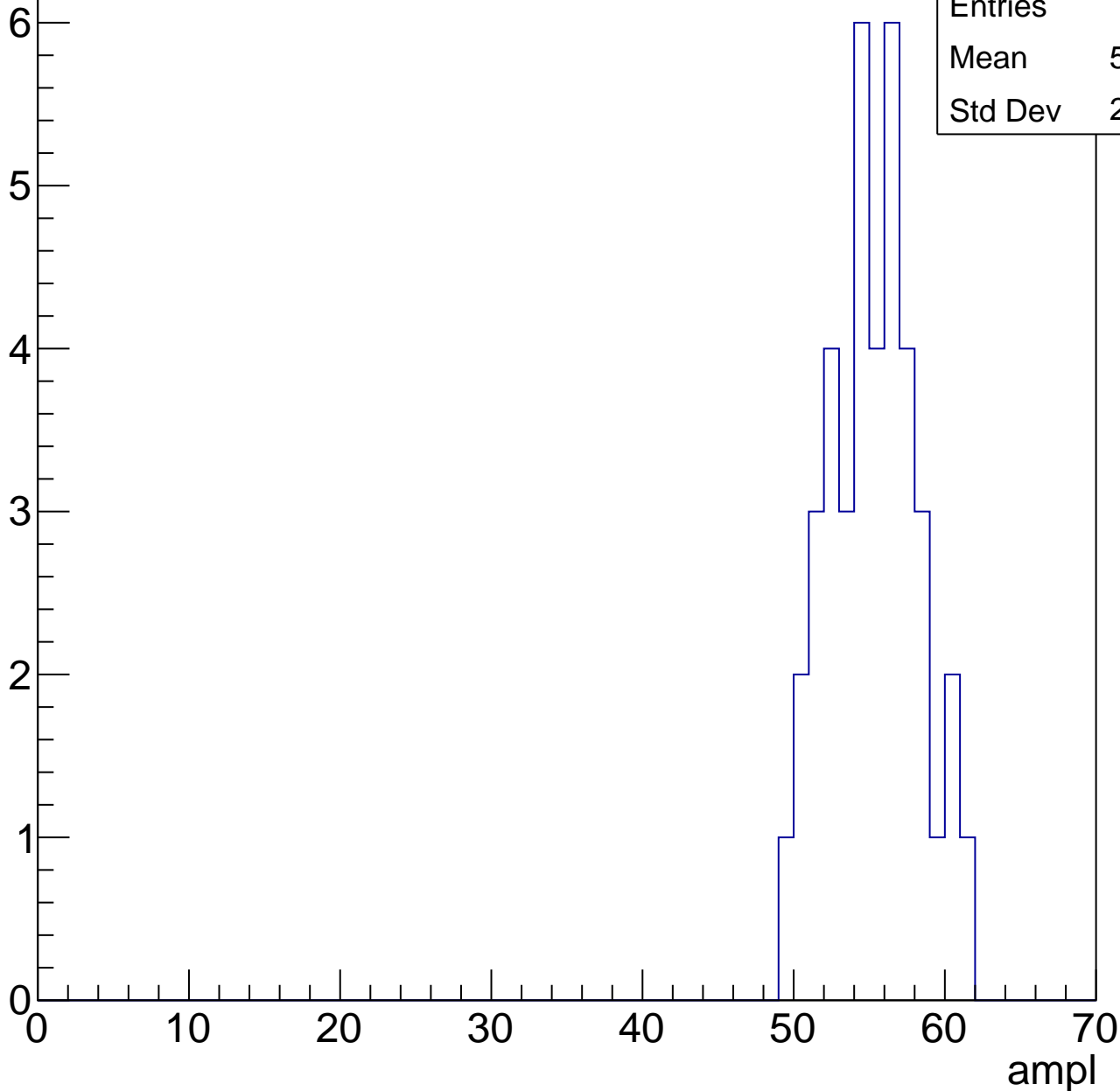


B1L102S, U9-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	54.77
Std Dev	2.902

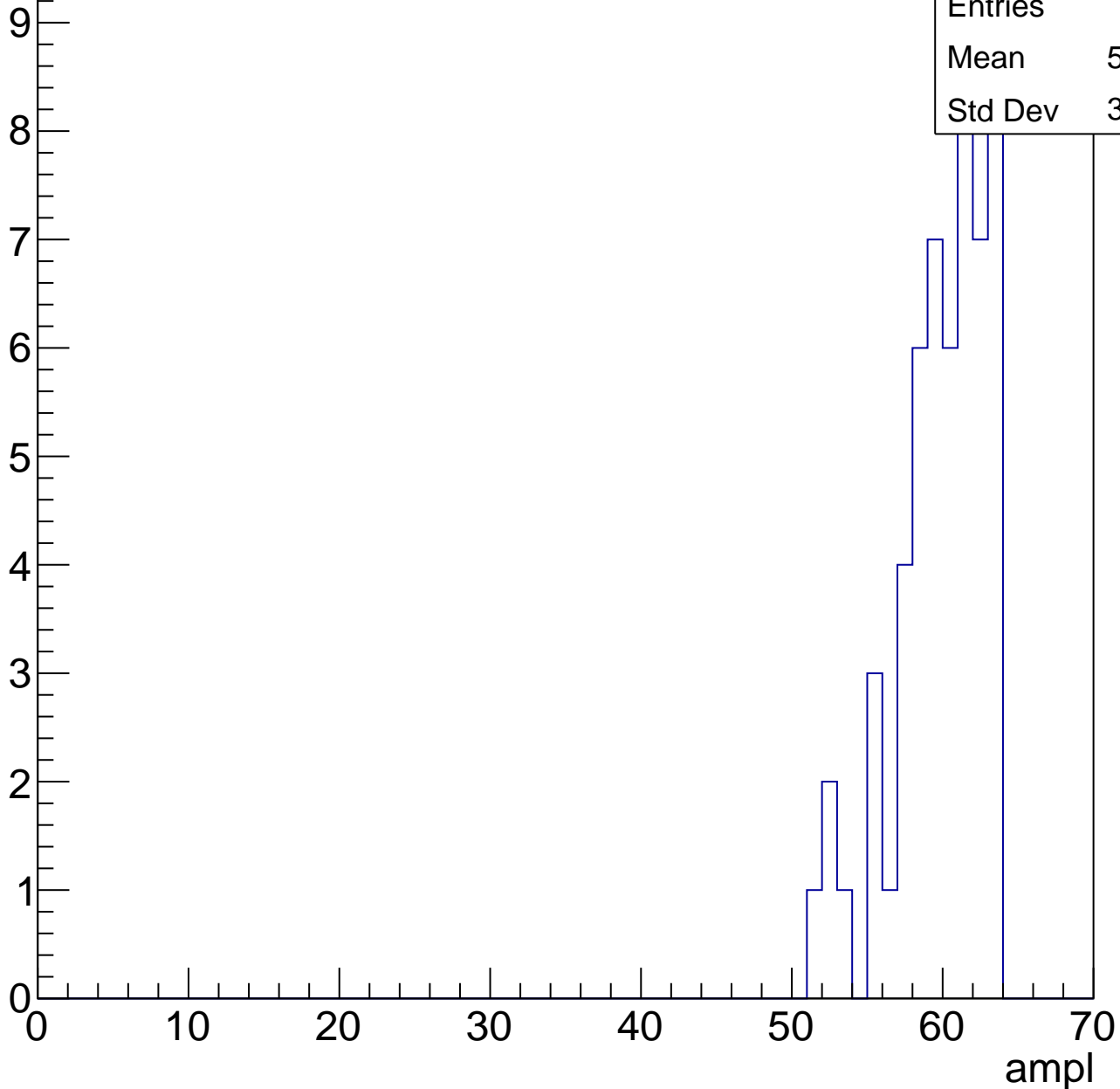


B1L102S, U9-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

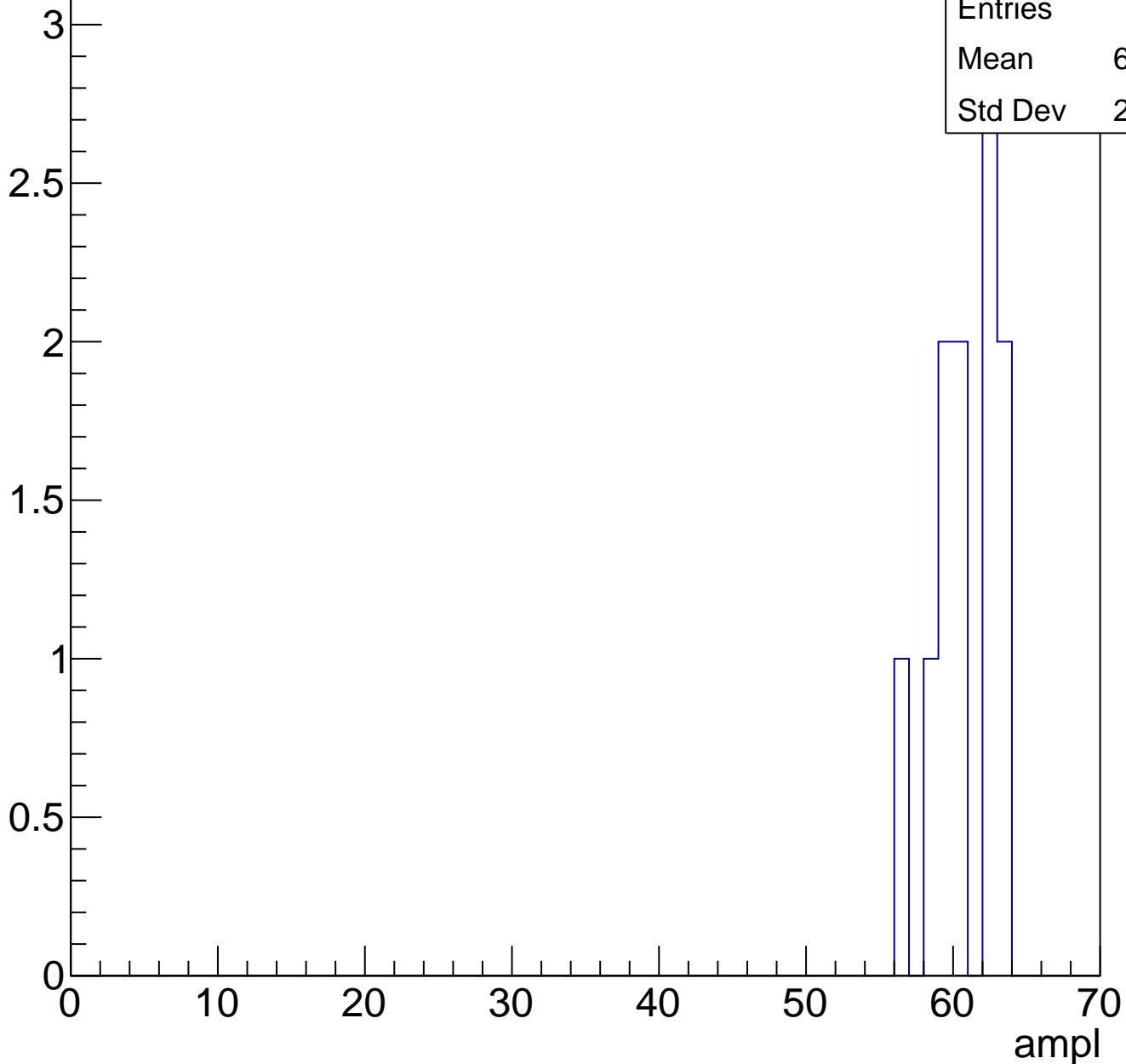
Entries	55
Mean	59.36
Std Dev	3.023



B1L102S, U9-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch115, adc0

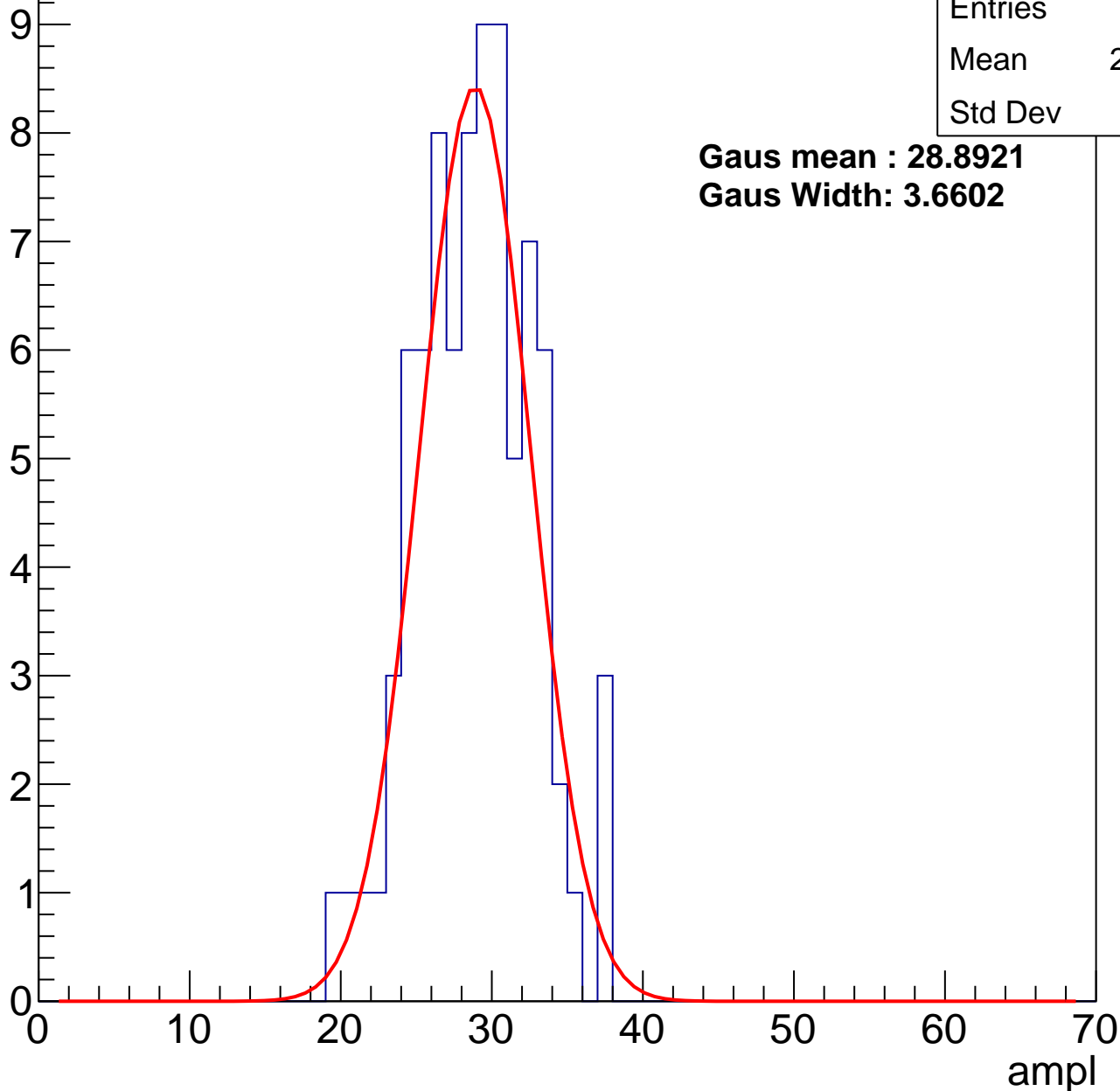
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.45
Std Dev	3.79

Gaus mean : 28.8921

Gaus Width: 3.6602



B1L102S, U9-ch115, adc1

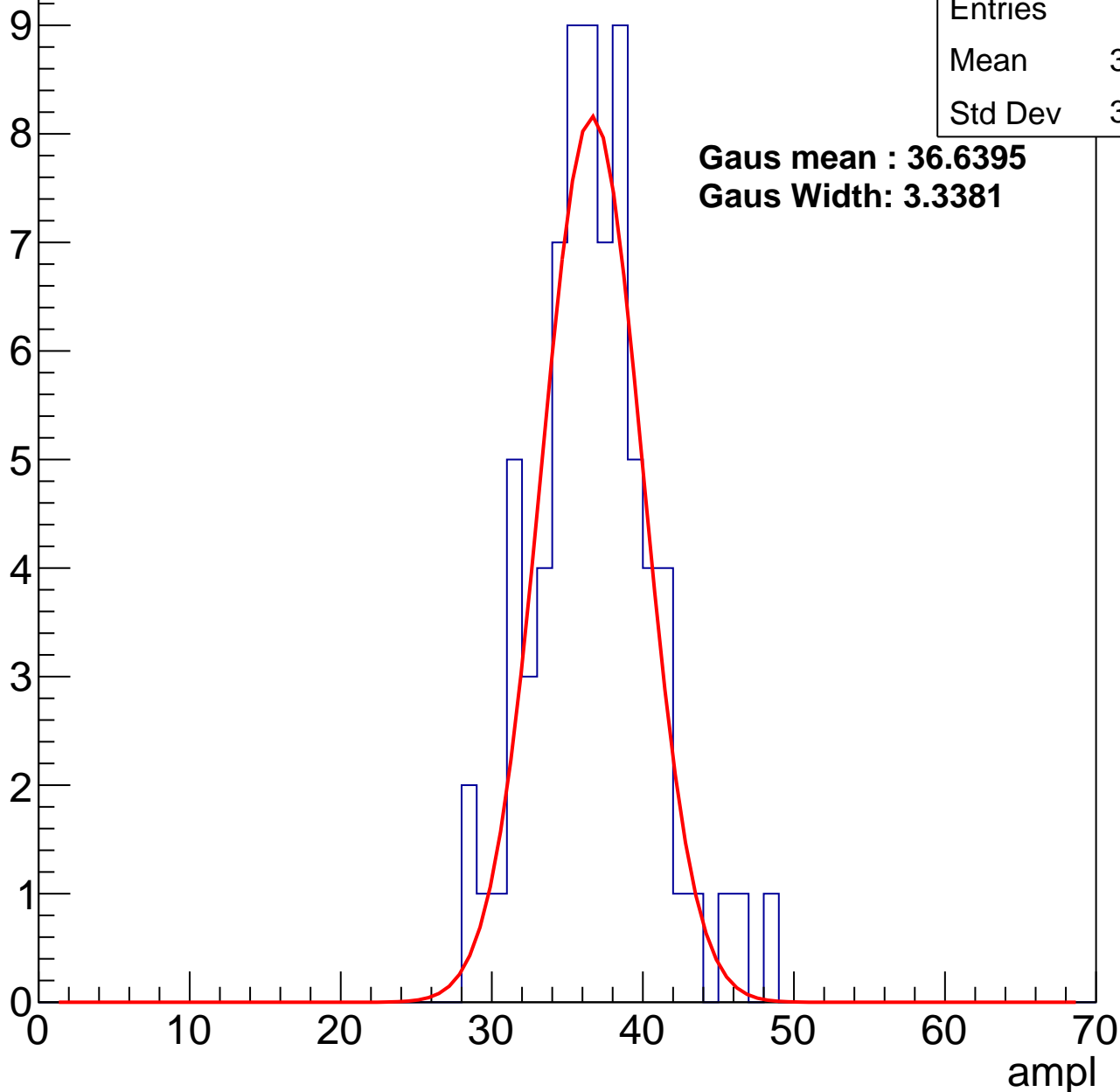
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.25
Std Dev	3.858

Gaus mean : 36.6395

Gaus Width: 3.3381



B1L102S, U9-ch115, adc2

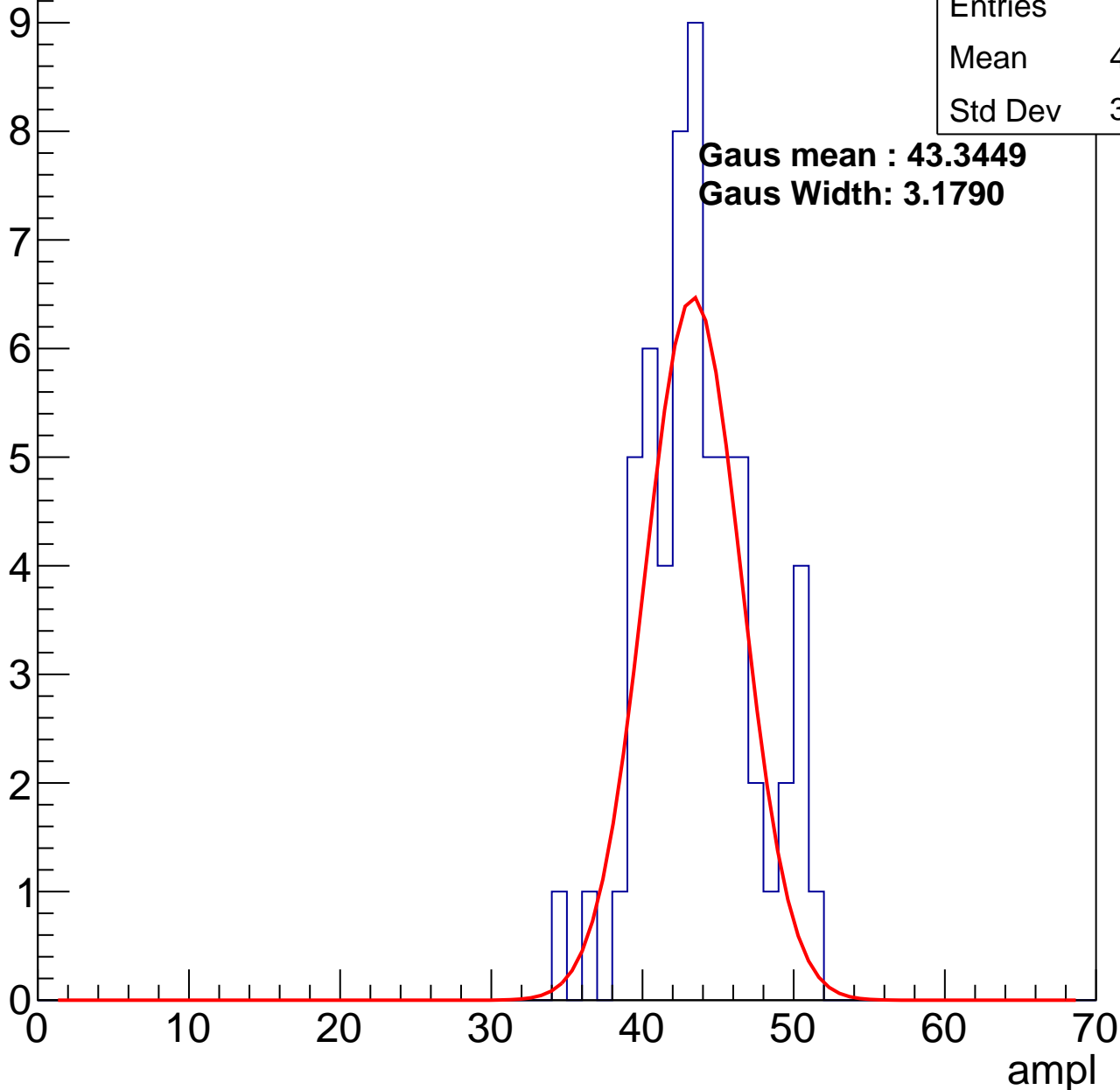
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.27
Std Dev	3.577

Gaus mean : 43.3449

Gaus Width: 3.1790

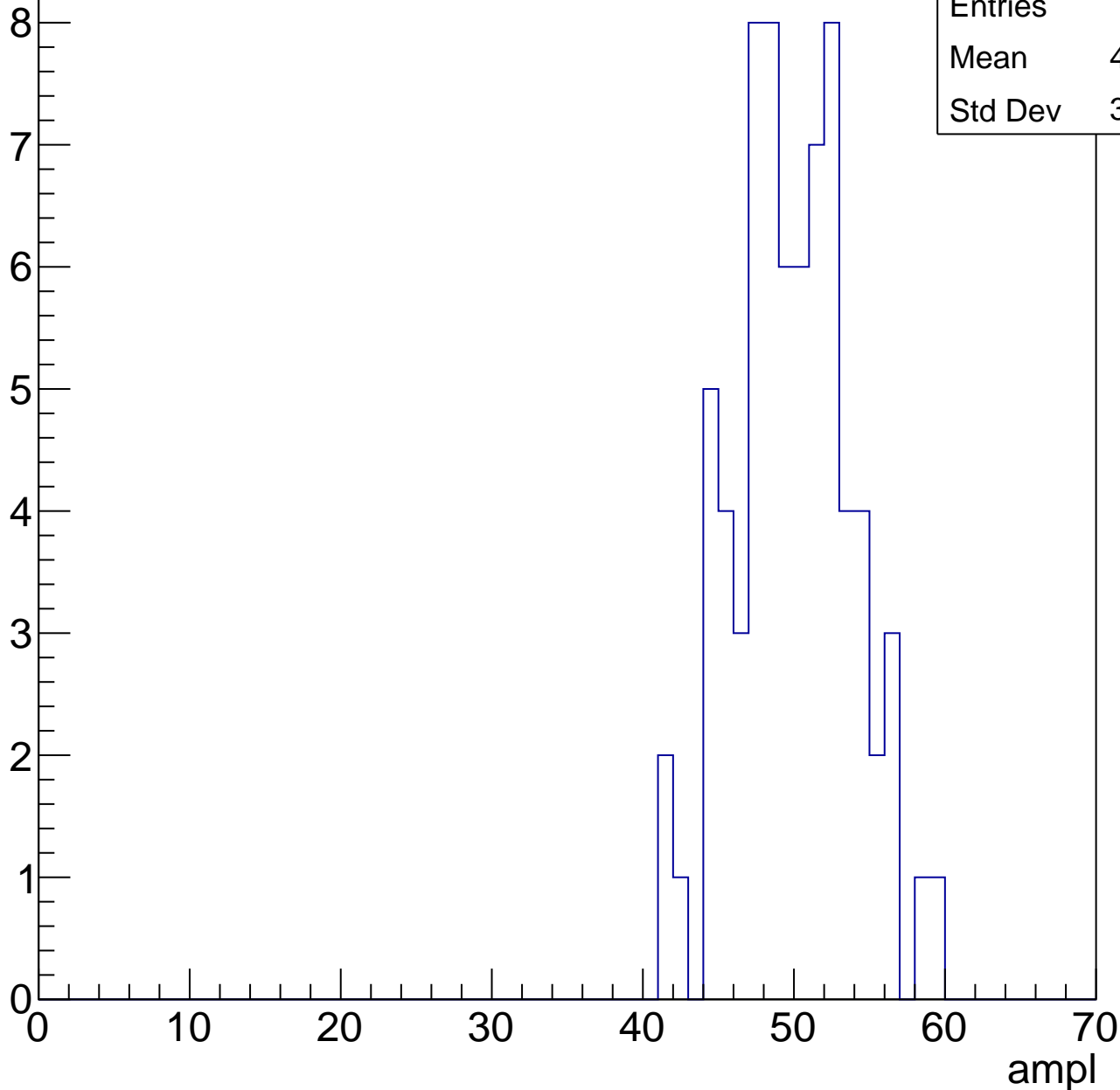


B1L102S, U9-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.48
Std Dev	3.857

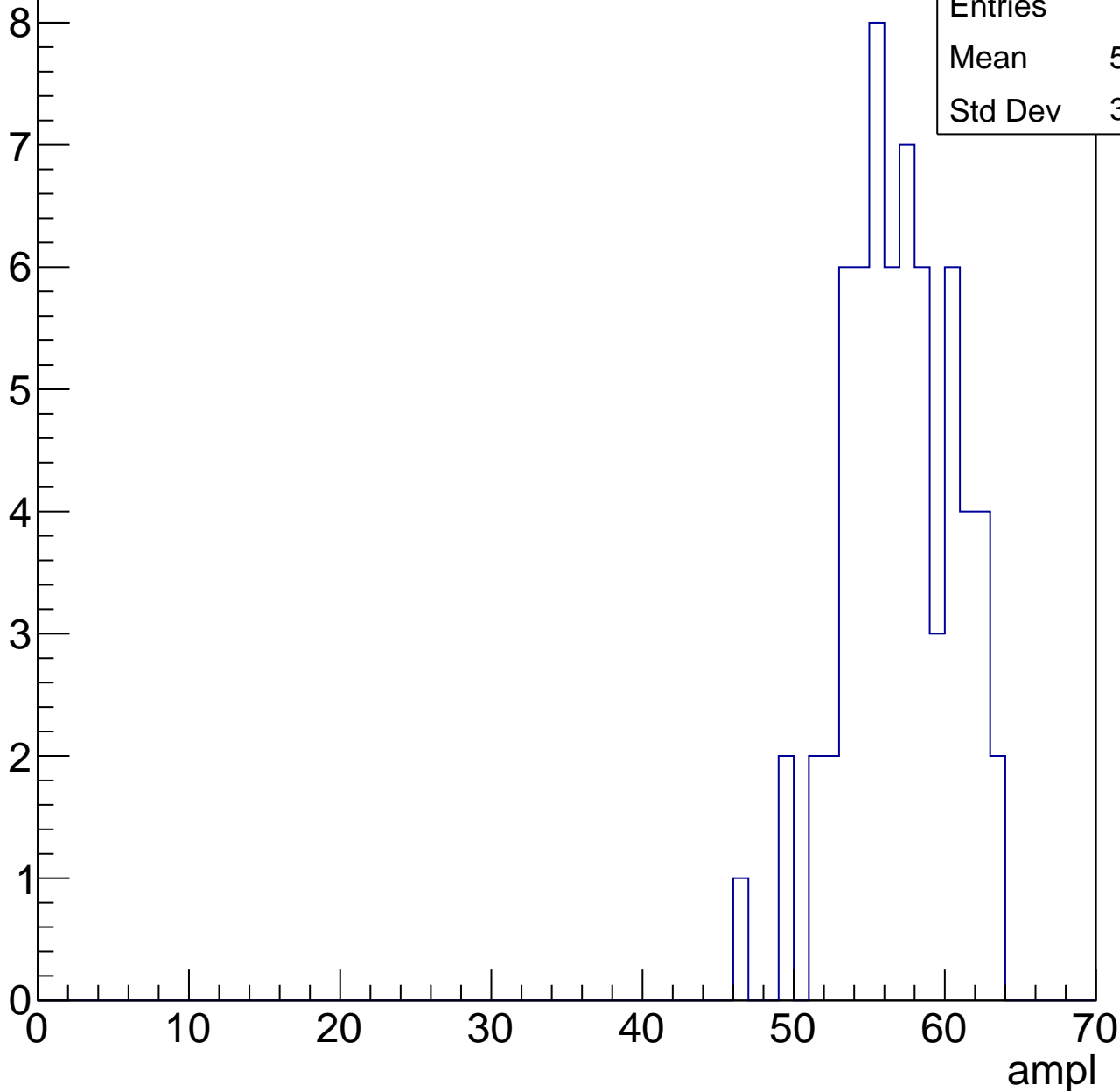


B1L102S, U9-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	56.46
Std Dev	3.612

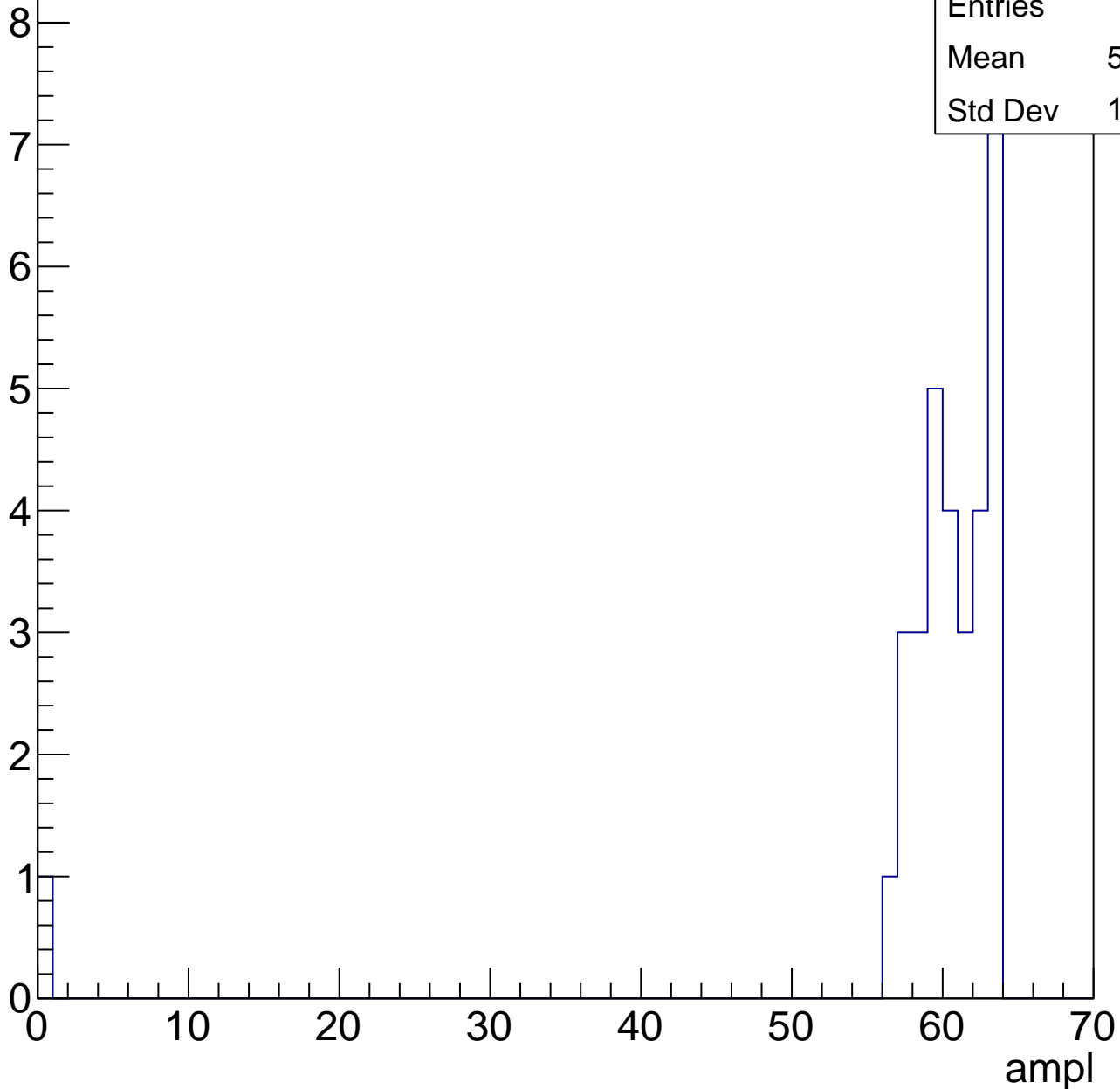


B1L102S, U9-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

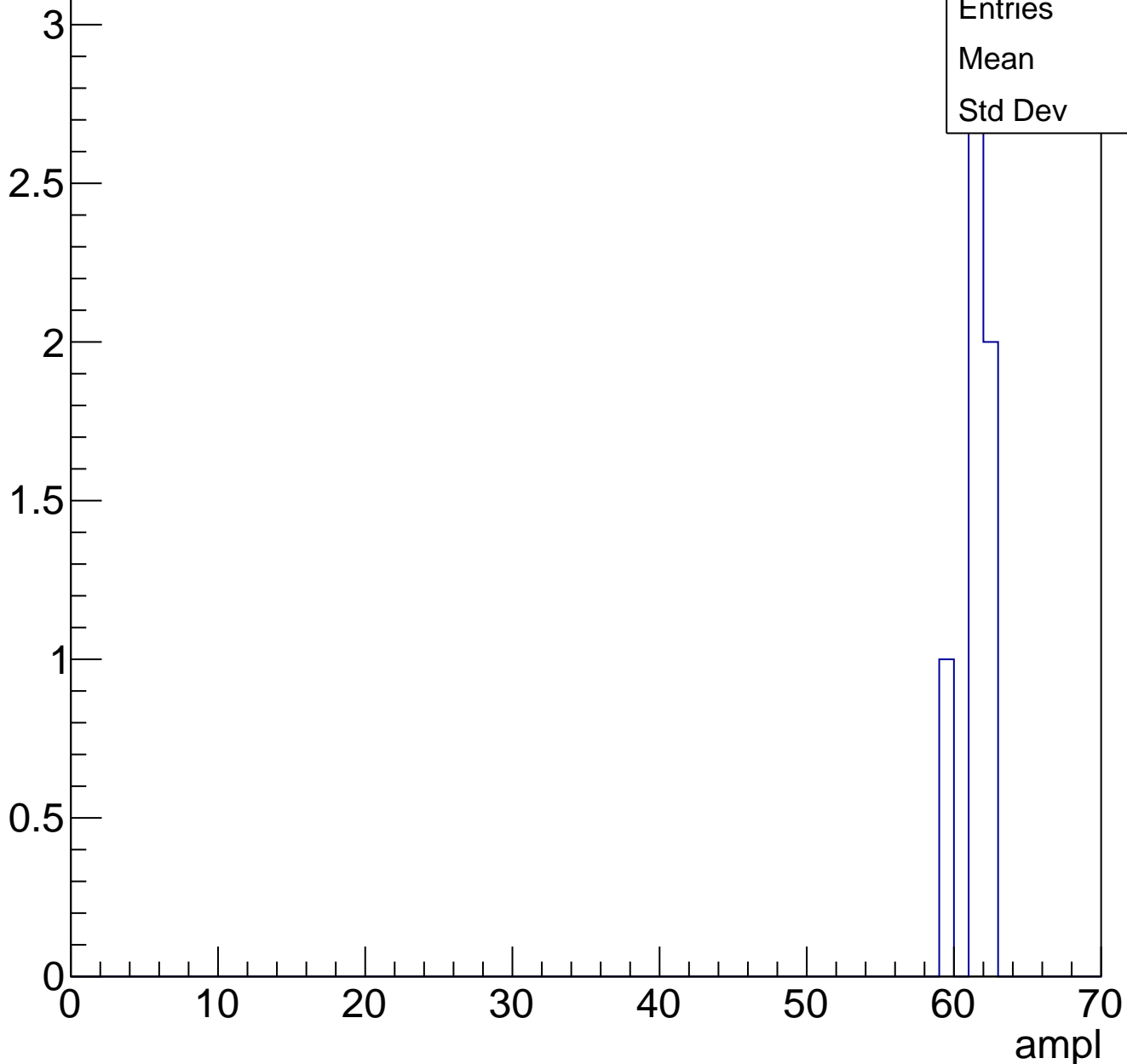
Entries	32
Mean	58.47
Std Dev	10.72



B1L102S, U9-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch116, adc0

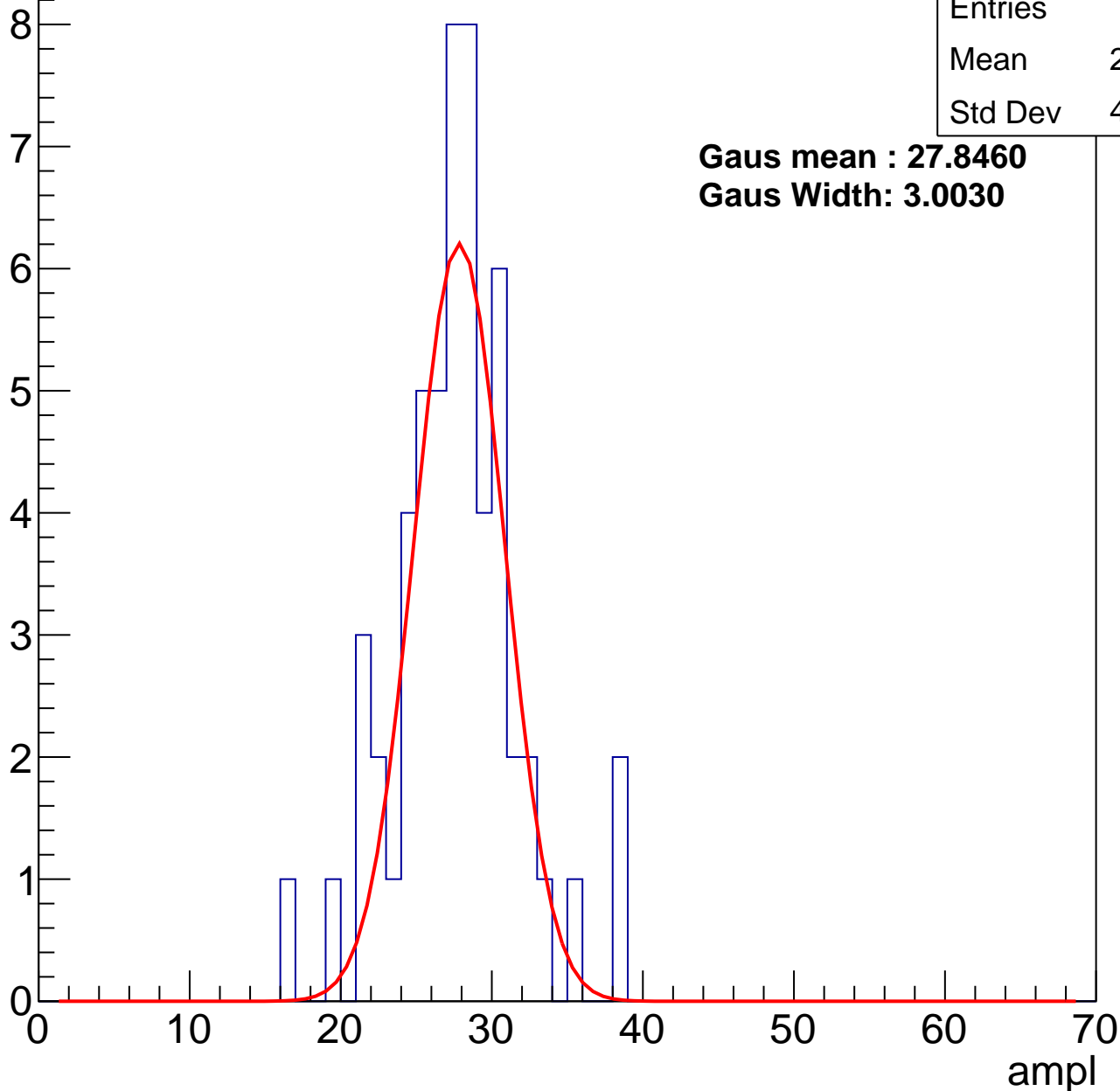
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	27.18
Std Dev	4.054

Gaus mean : 27.8460

Gaus Width: 3.0030



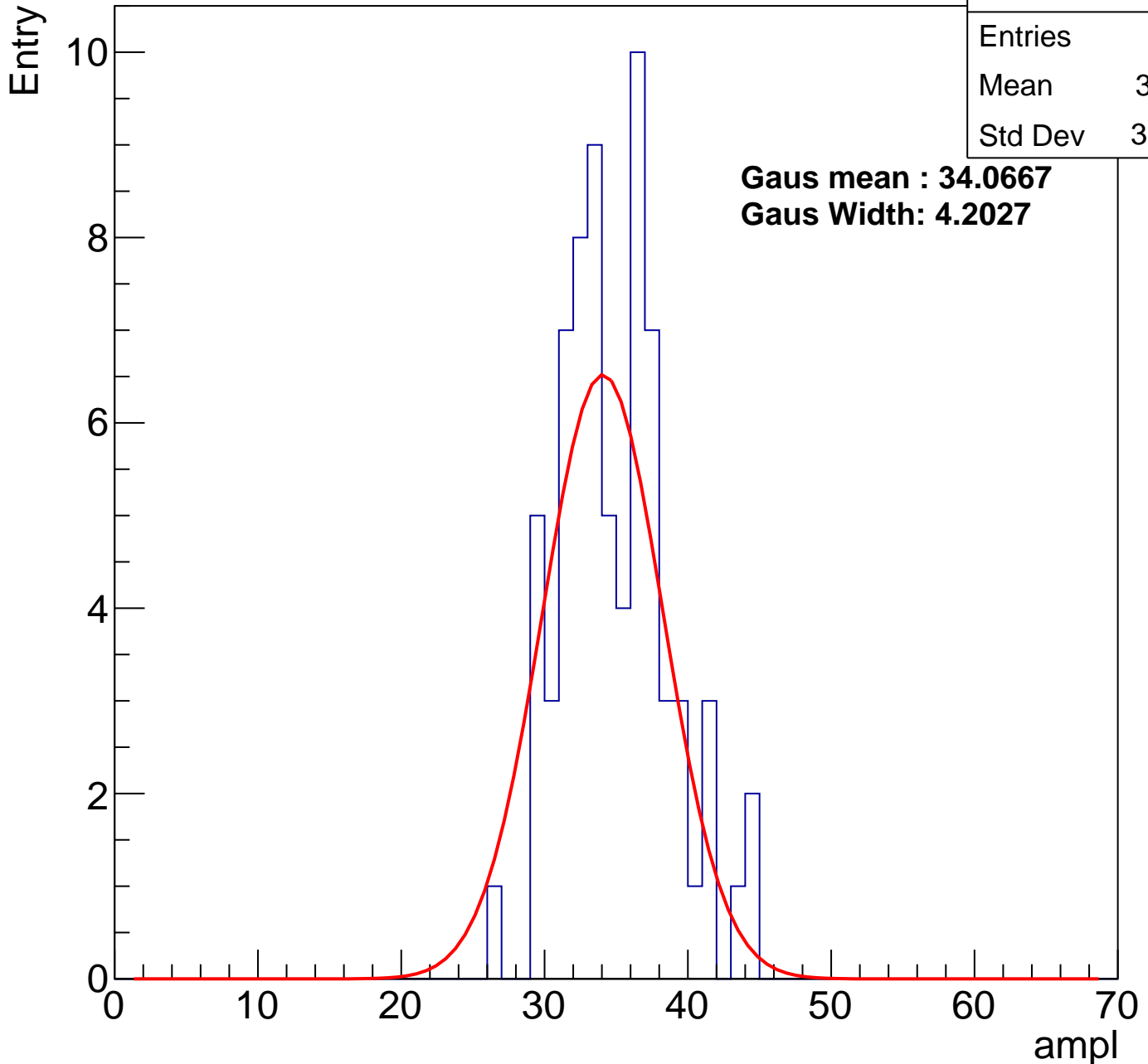
B1L102S, U9-ch116, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	34.51
Std Dev	3.756

Gaus mean : 34.0667

Gaus Width: 4.2027



B1L102S, U9-ch116, adc2

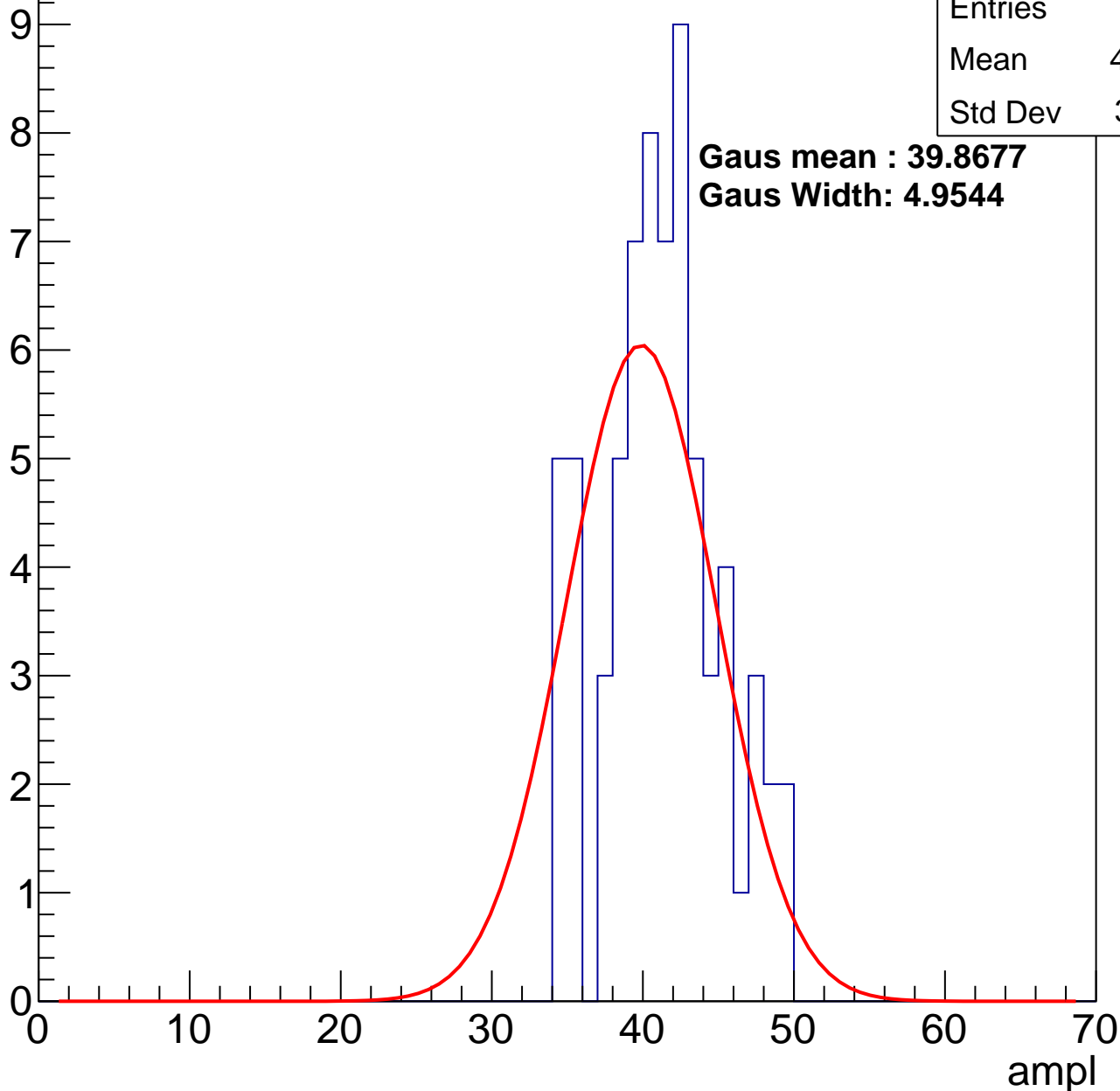
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	40.75
Std Dev	3.861

Gaus mean : 39.8677

Gaus Width: 4.9544

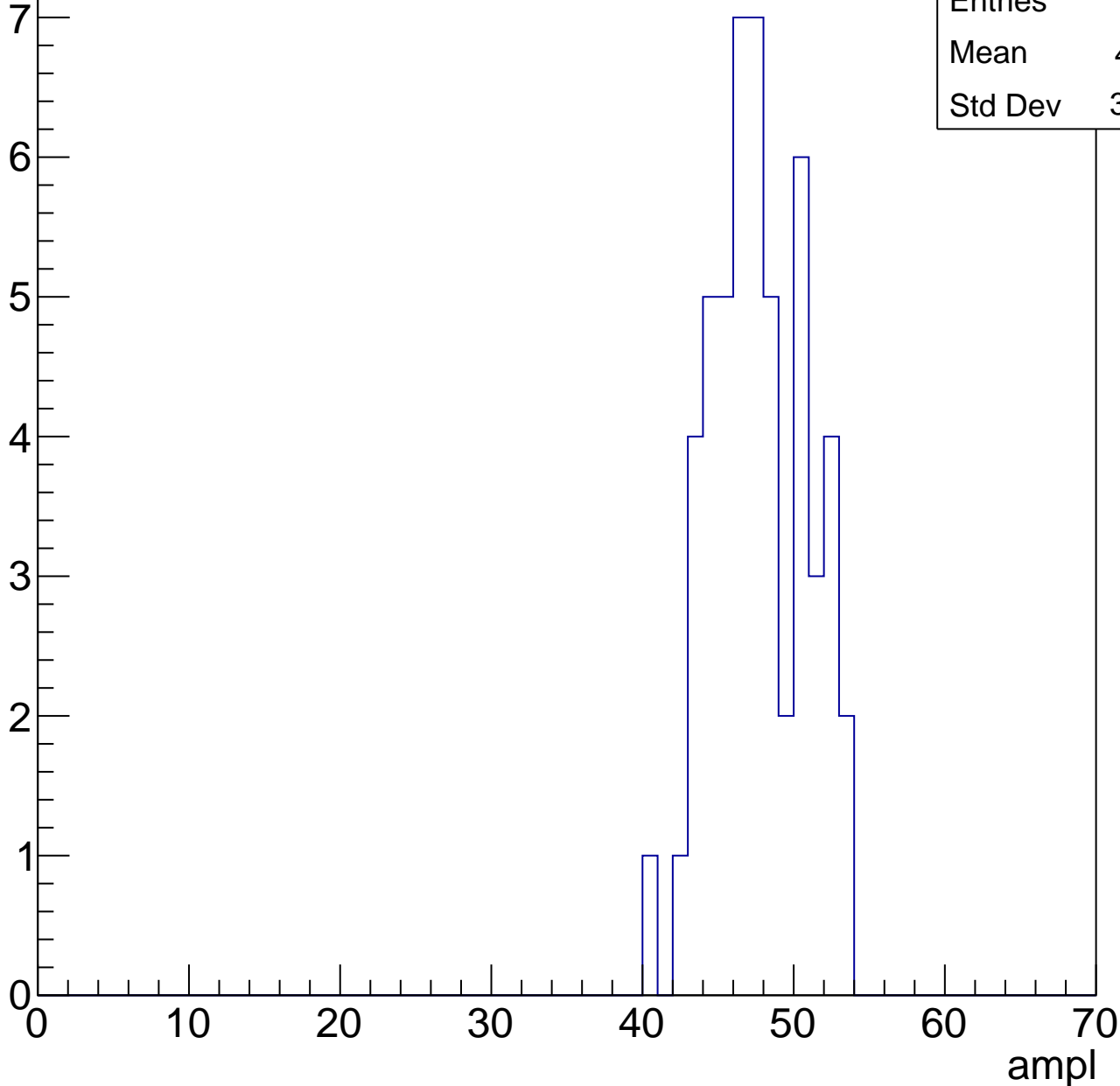


B1L102S, U9-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	47.21
Std Dev	3.084

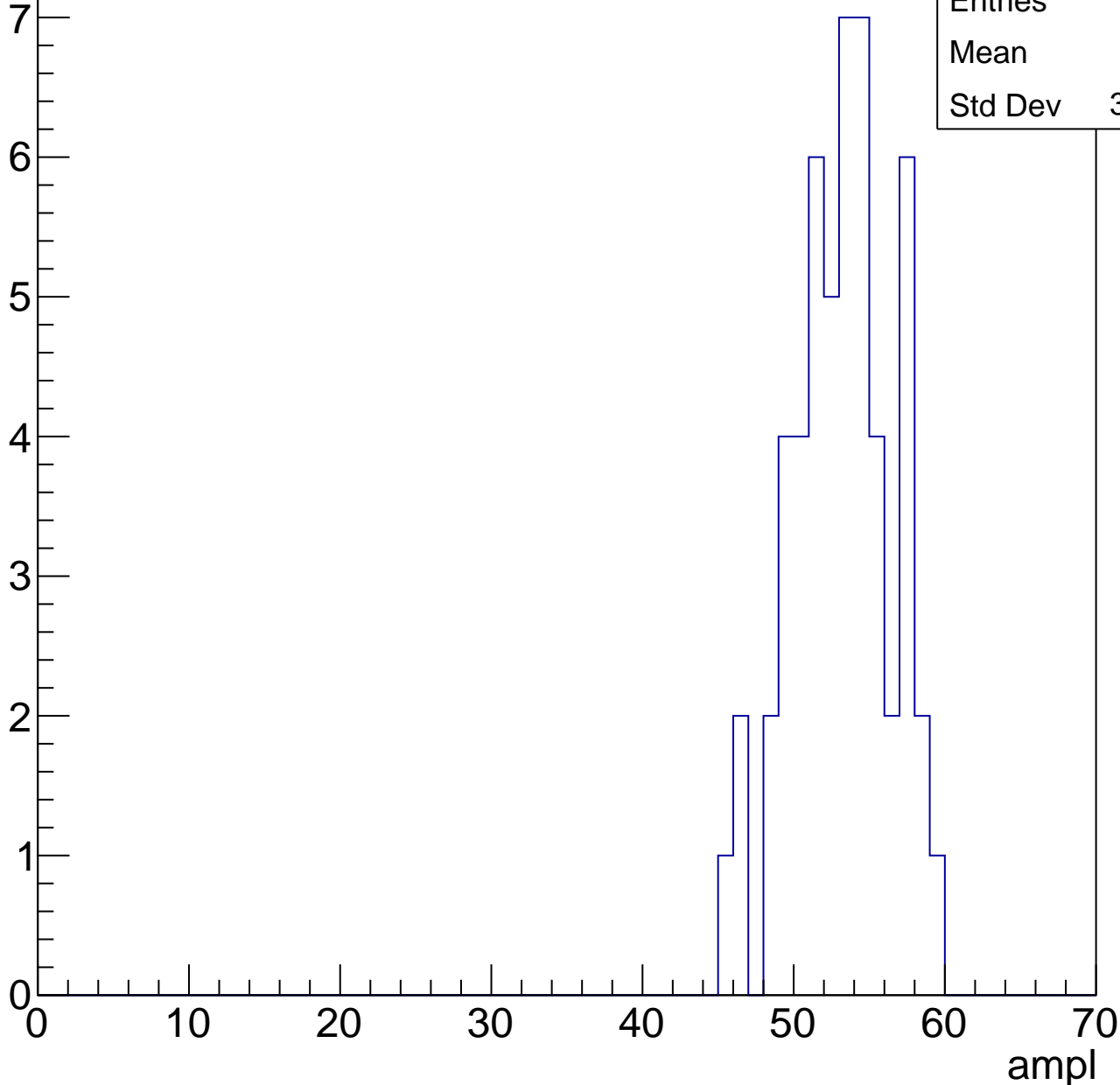


B1L102S, U9-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	52.7
Std Dev	3.248

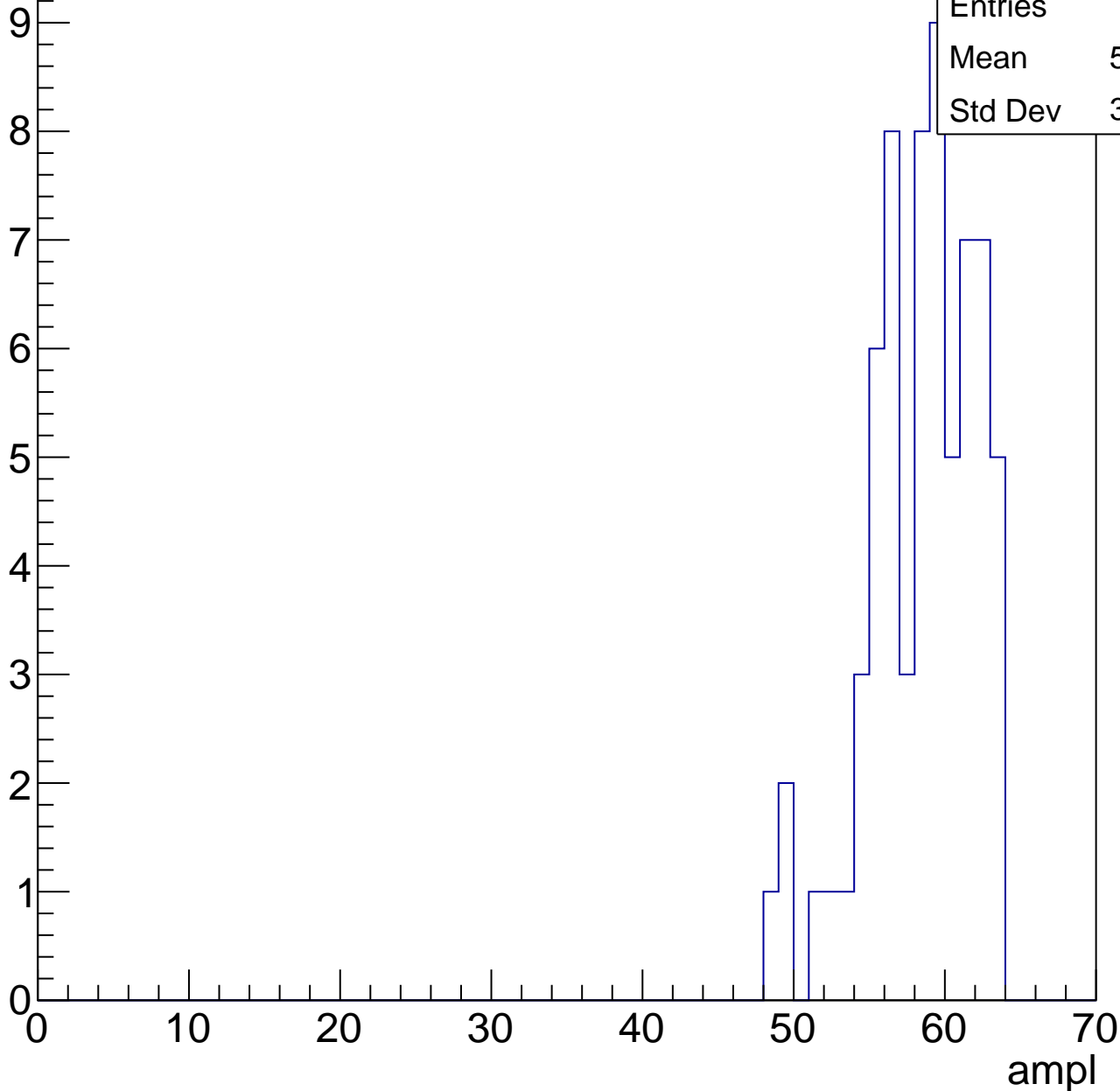


B1L102S, U9-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	57.97
Std Dev	3.536

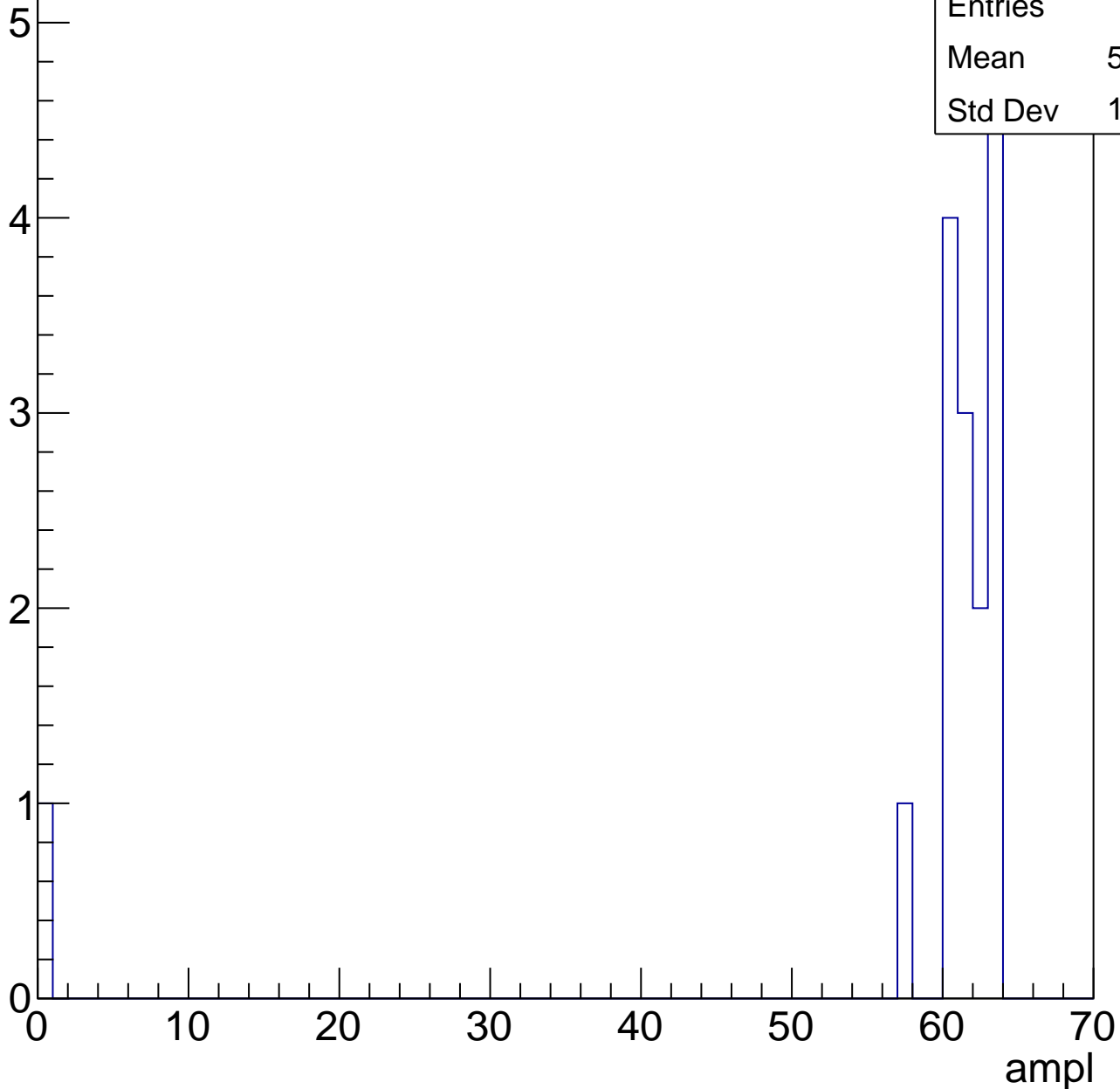


B1L102S, U9-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

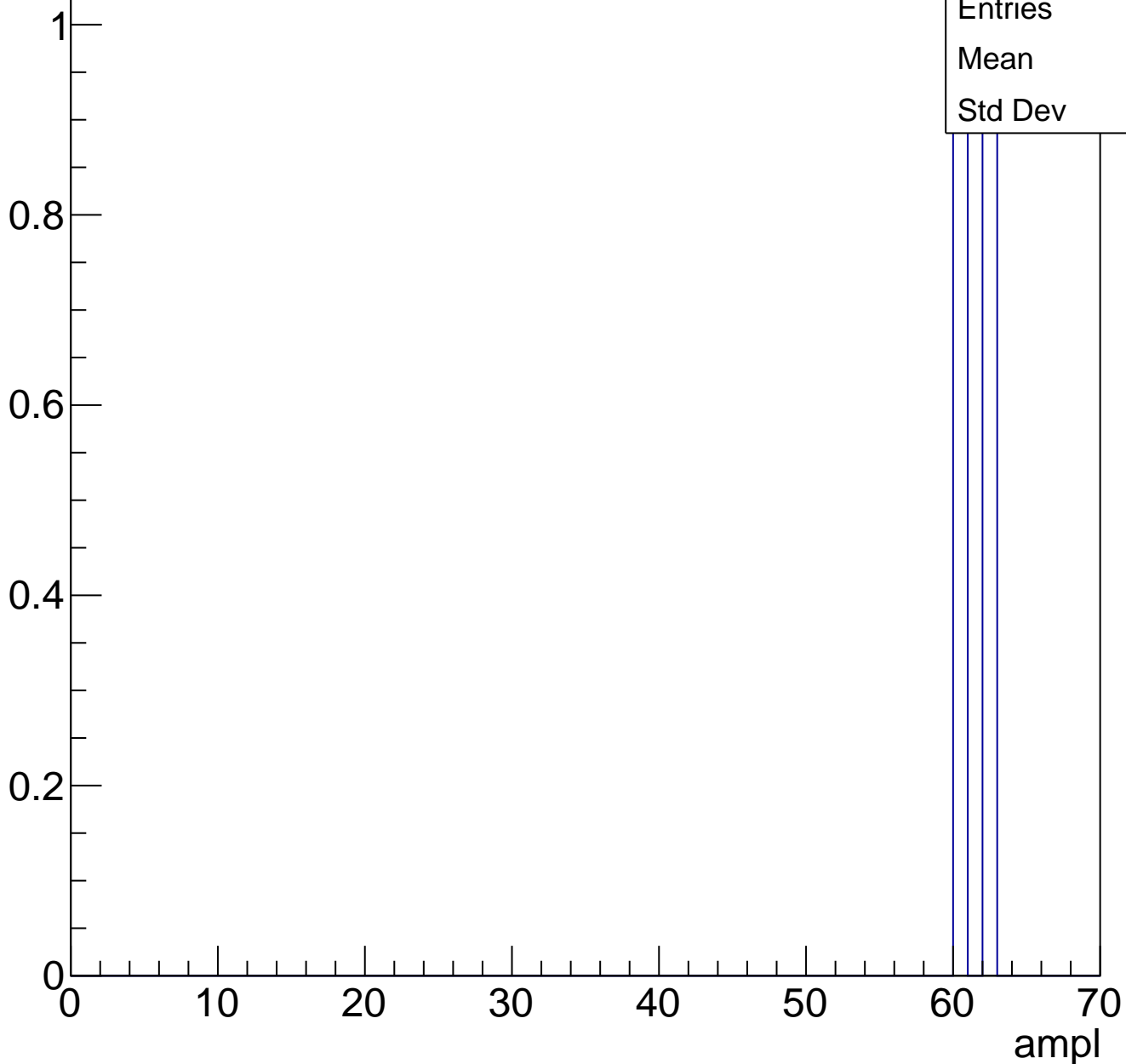
Entries	16
Mean	57.44
Std Dev	14.92



B1L102S, U9-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch117, adc0

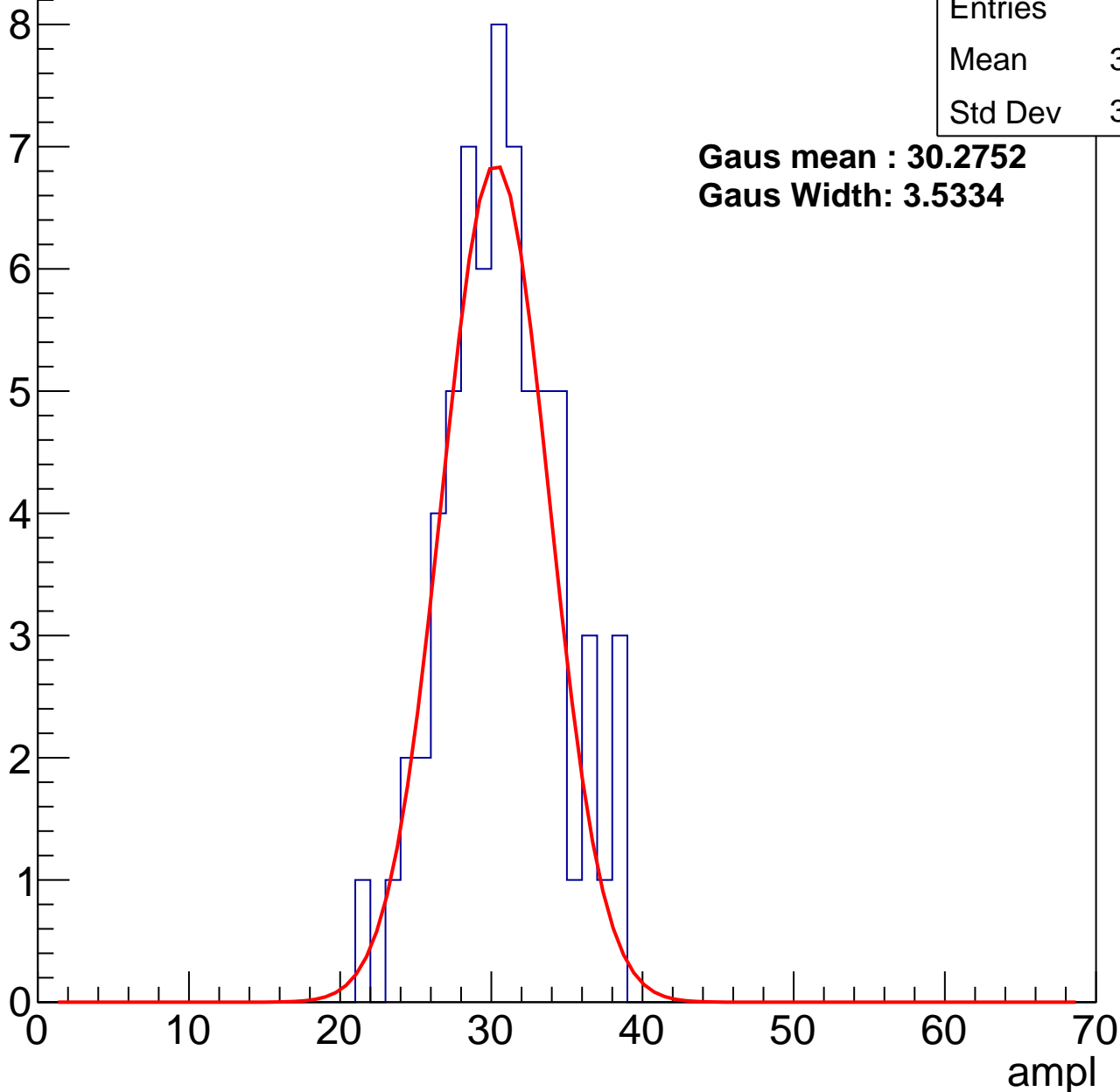
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	30.26
Std Dev	3.727

Gaus mean : 30.2752

Gaus Width: 3.5334



B1L102S, U9-ch117, adc1

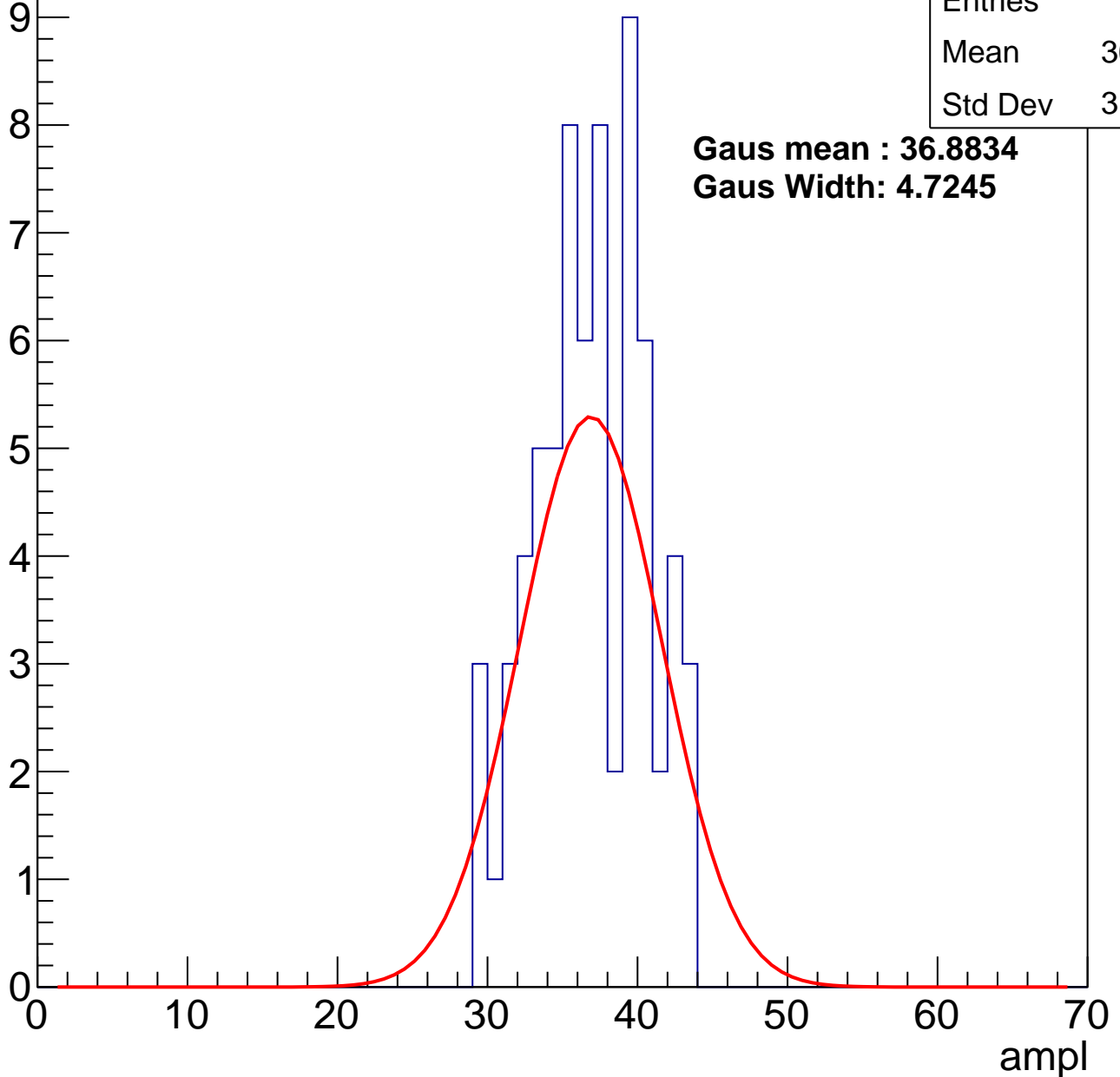
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.39
Std Dev	3.648

Gaus mean : 36.8834

Gaus Width: 4.7245



B1L102S, U9-ch117, adc2

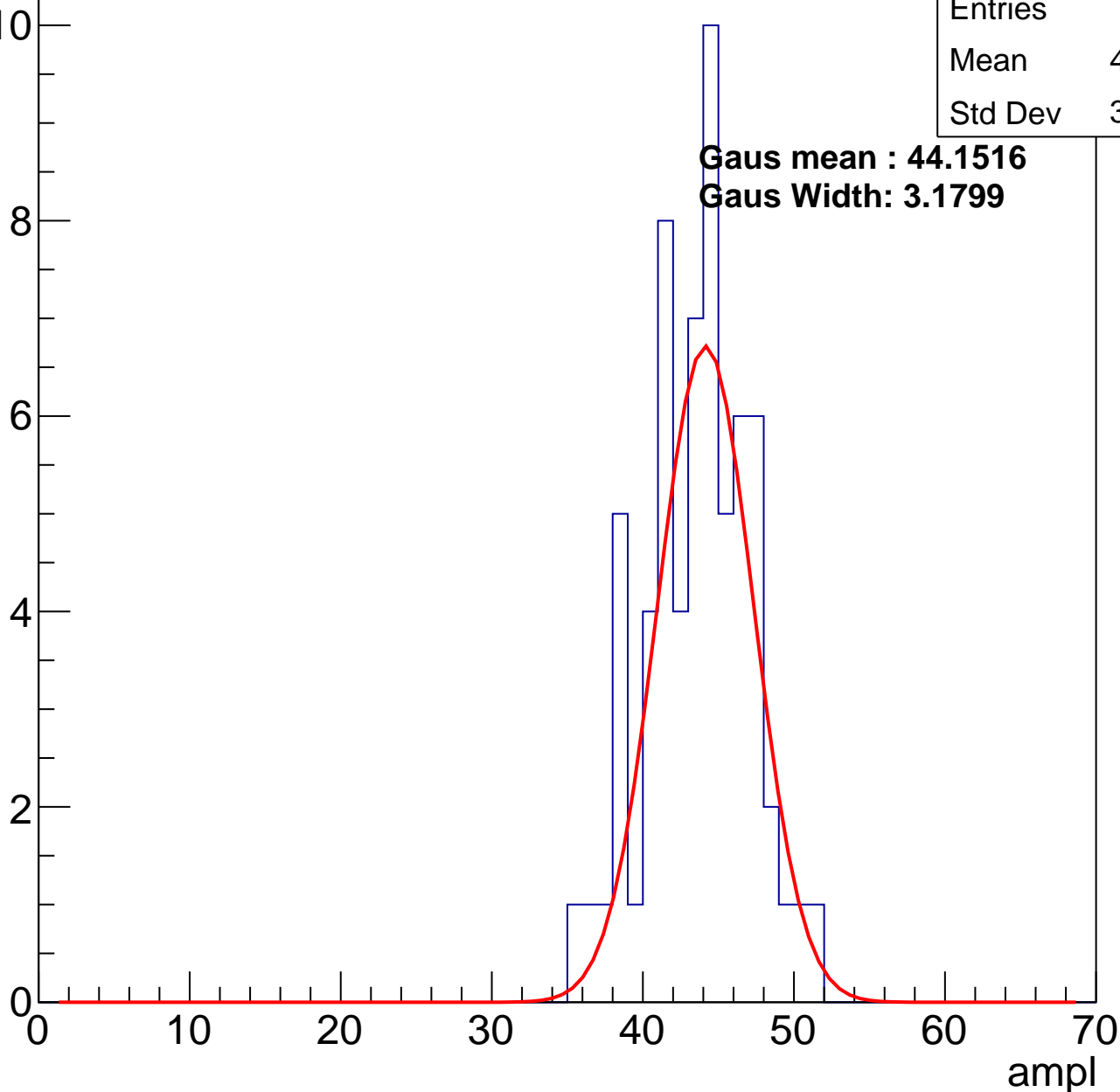
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.17
Std Dev	3.398

Gaus mean : 44.1516

Gaus Width: 3.1799

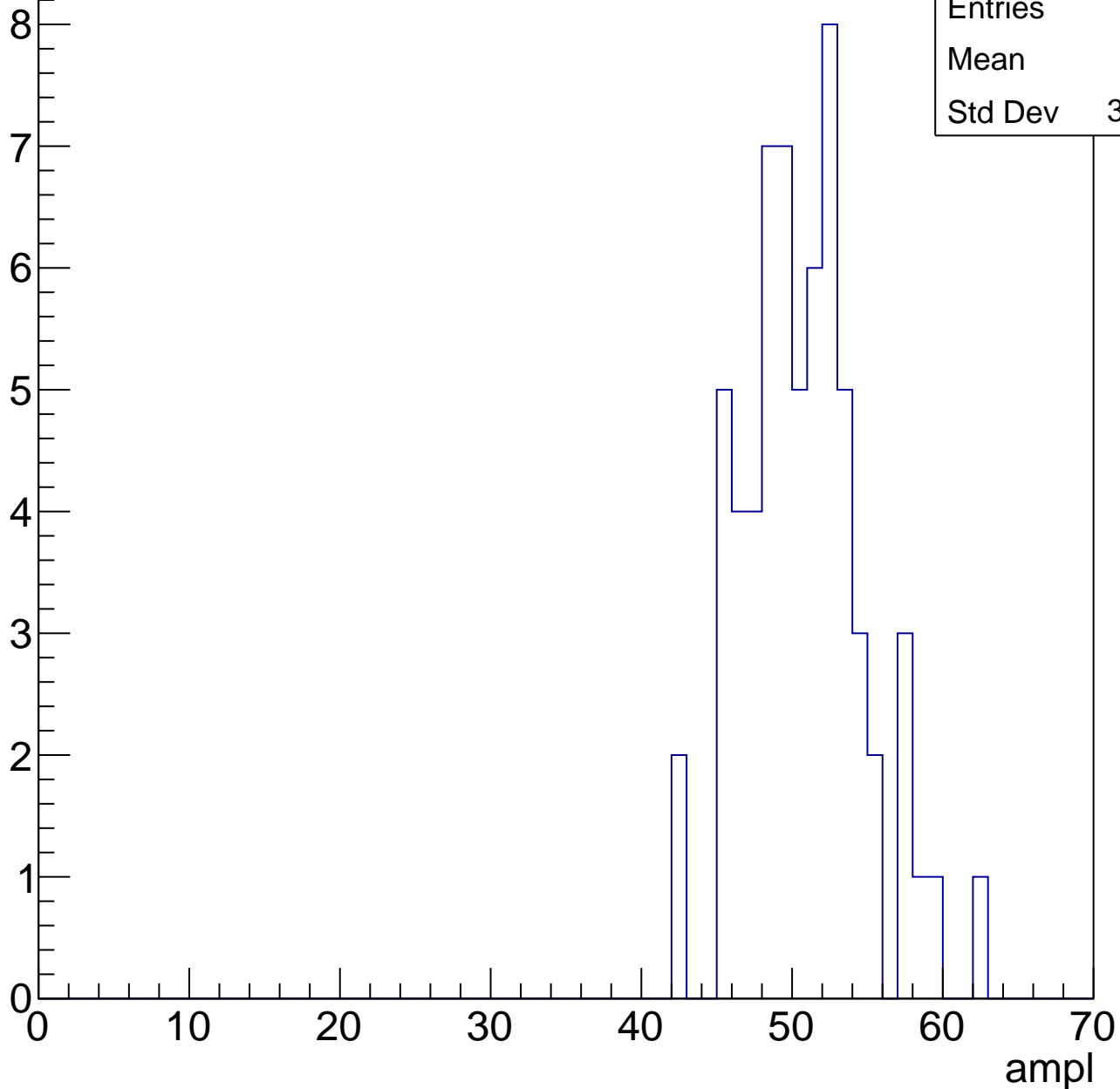


B1L102S, U9-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	50.3
Std Dev	3.952

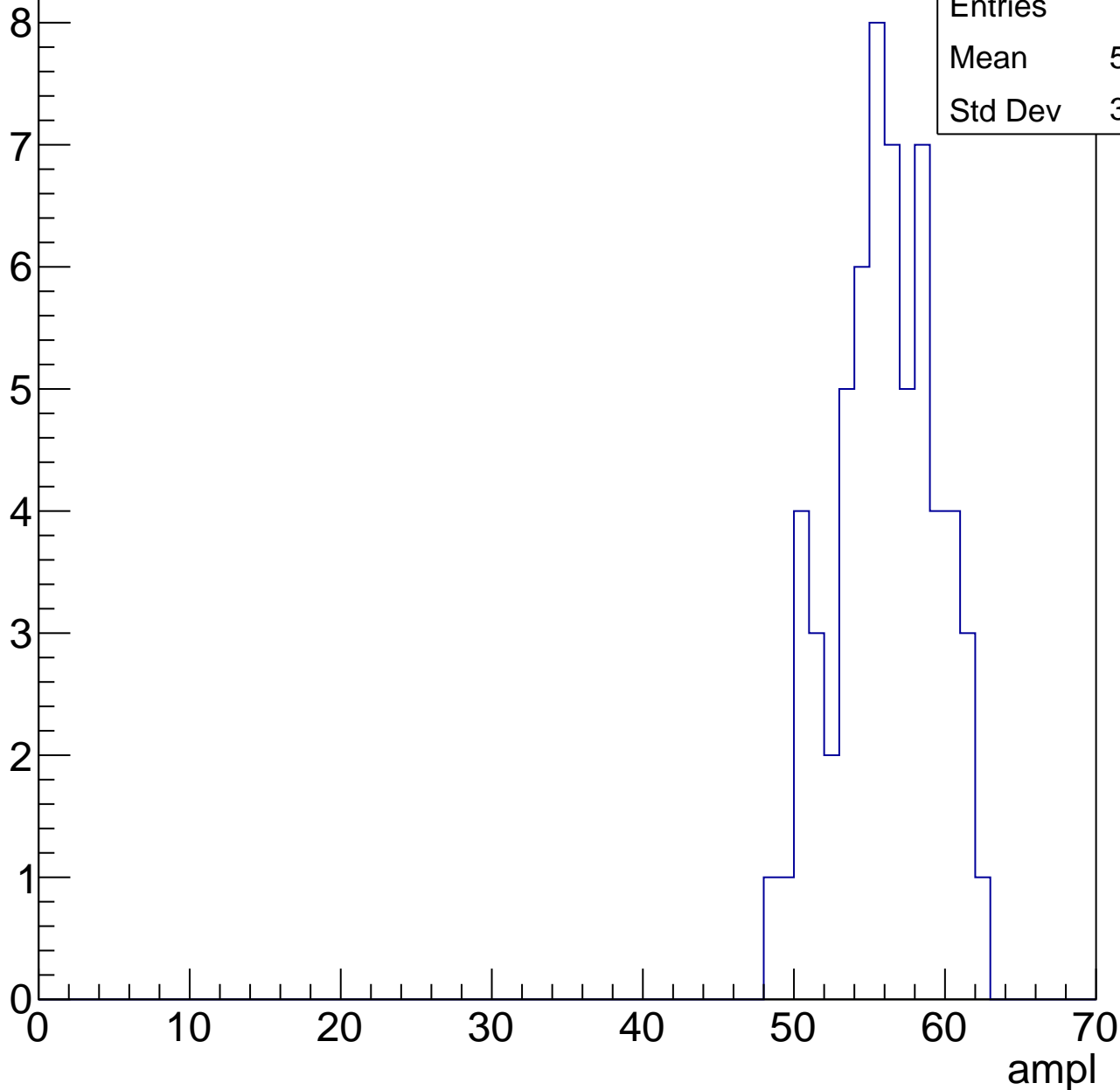


B1L102S, U9-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.52
Std Dev	3.317

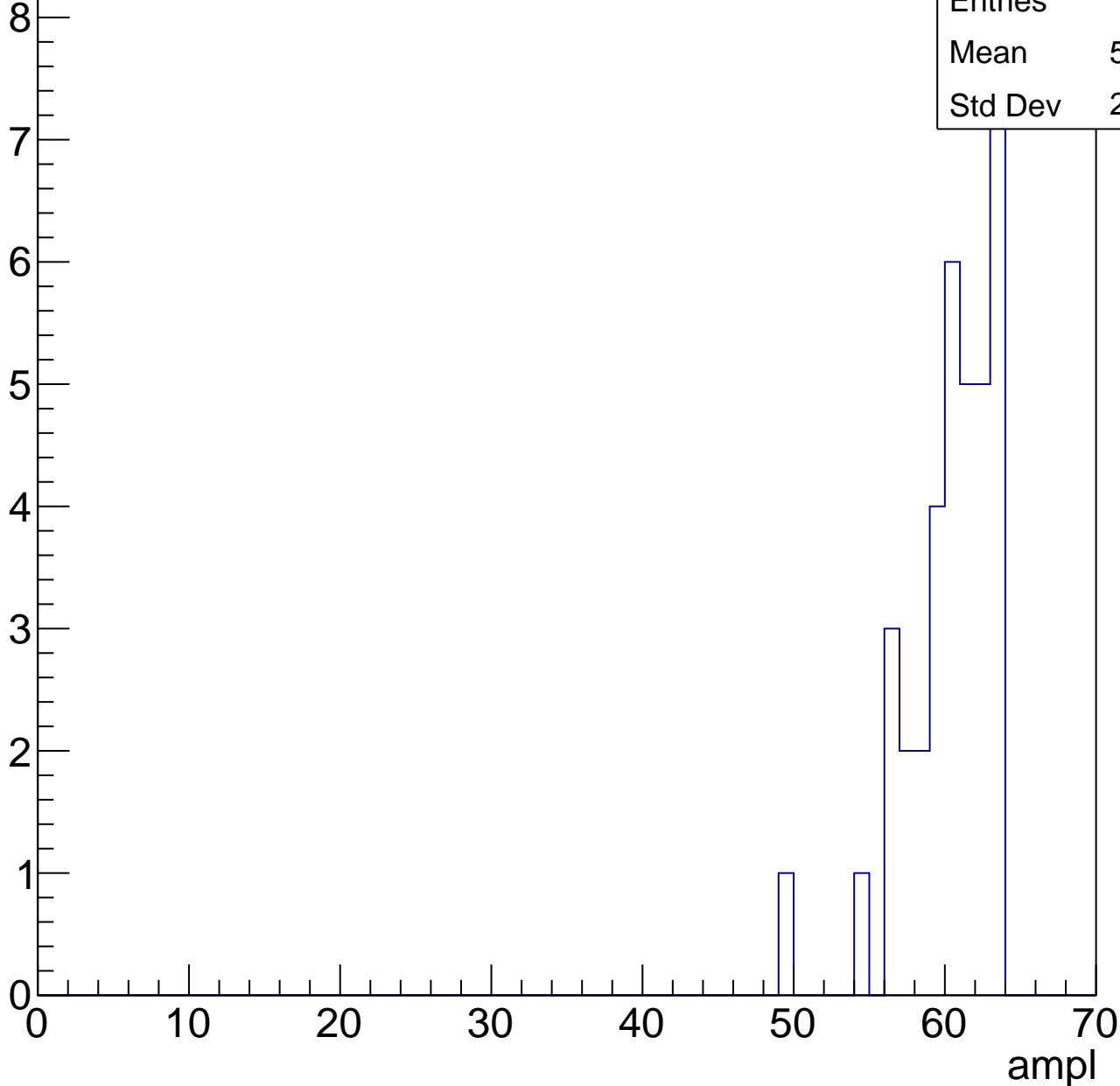


B1L102S, U9-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

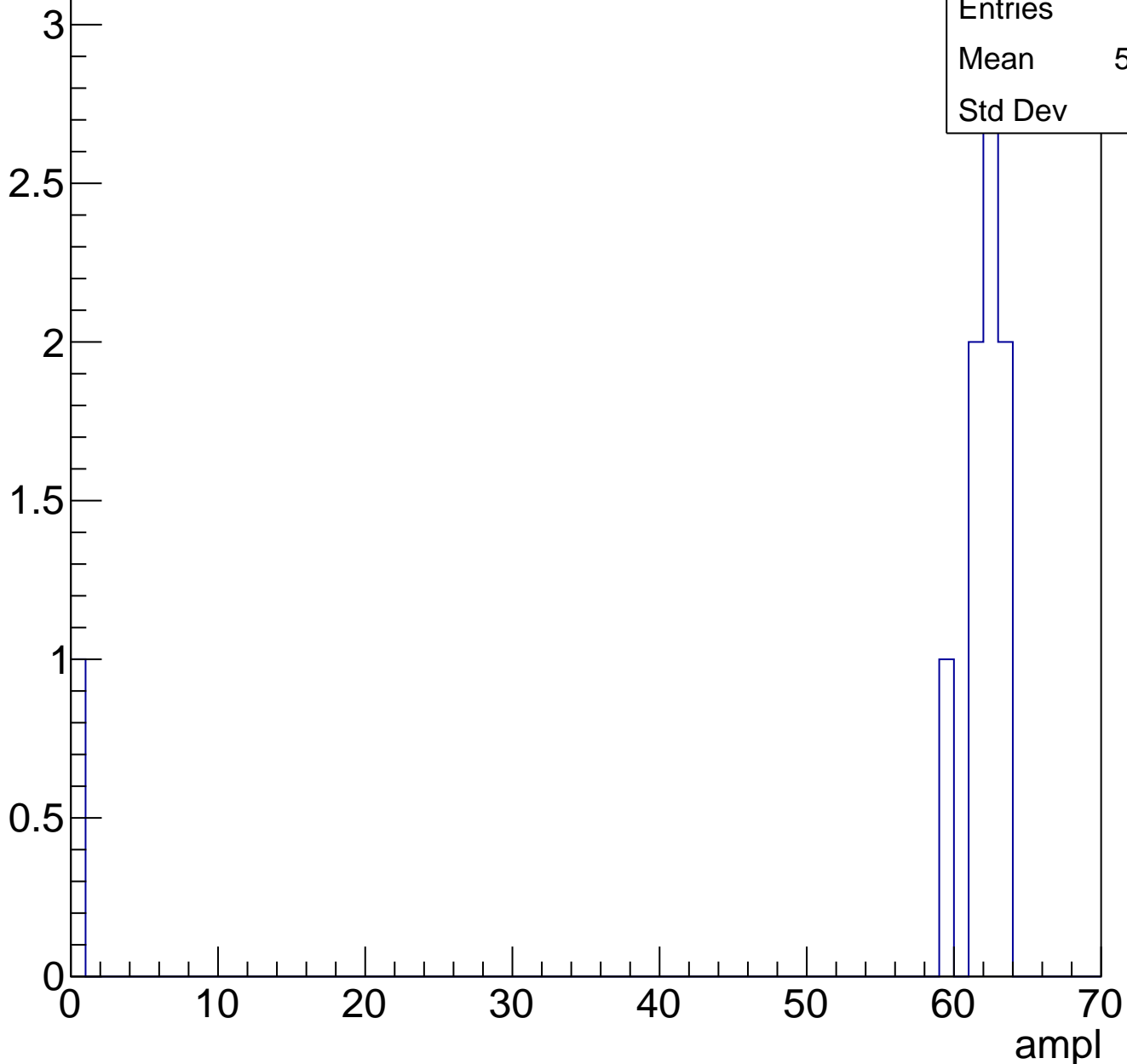
Entries	37
Mean	59.89
Std Dev	2.994



B1L102S, U9-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch118, adc0

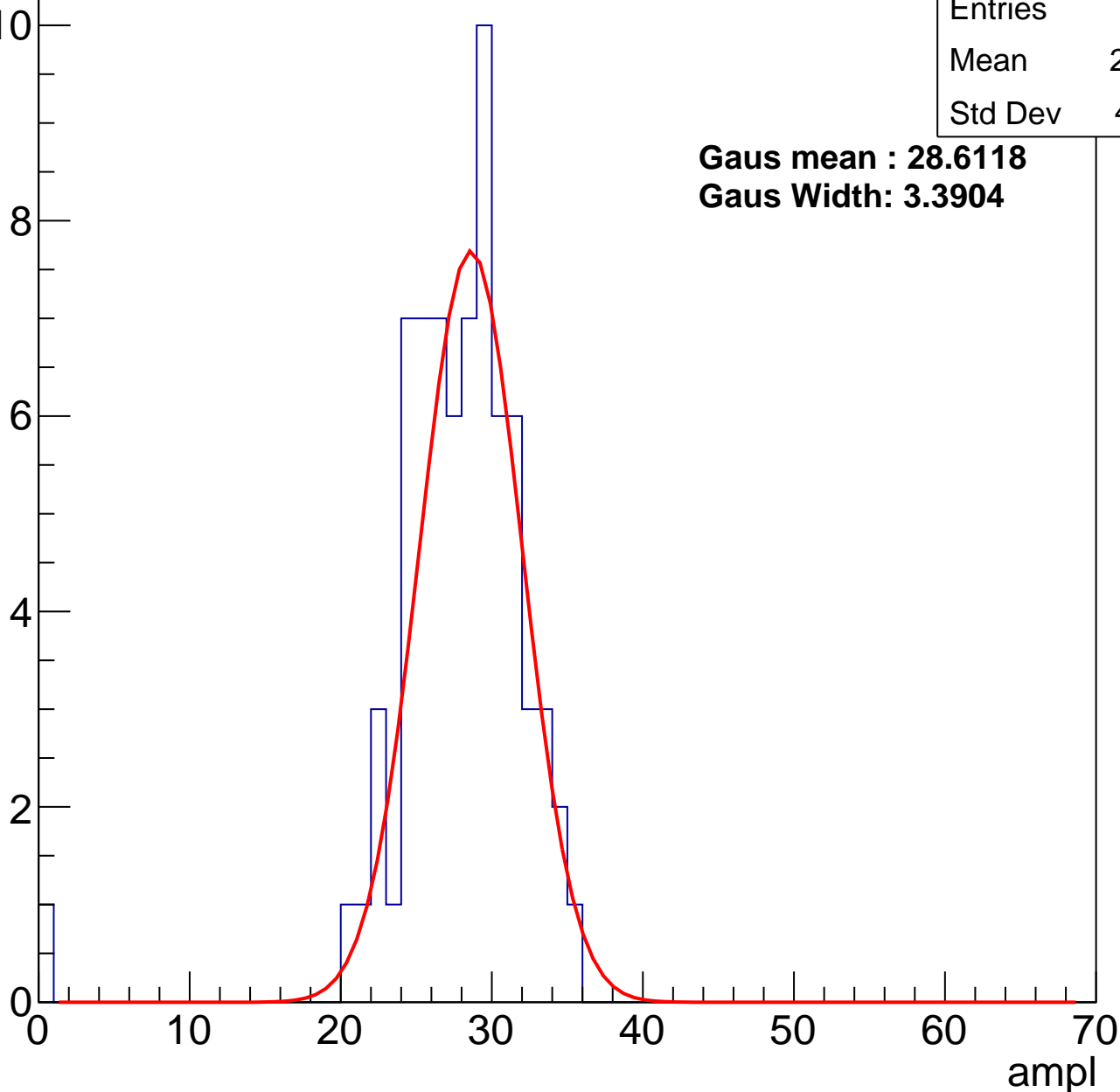
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.32
Std Dev	4.621

Gaus mean : 28.6118

Gaus Width: 3.3904



B1L102S, U9-ch118, adc1

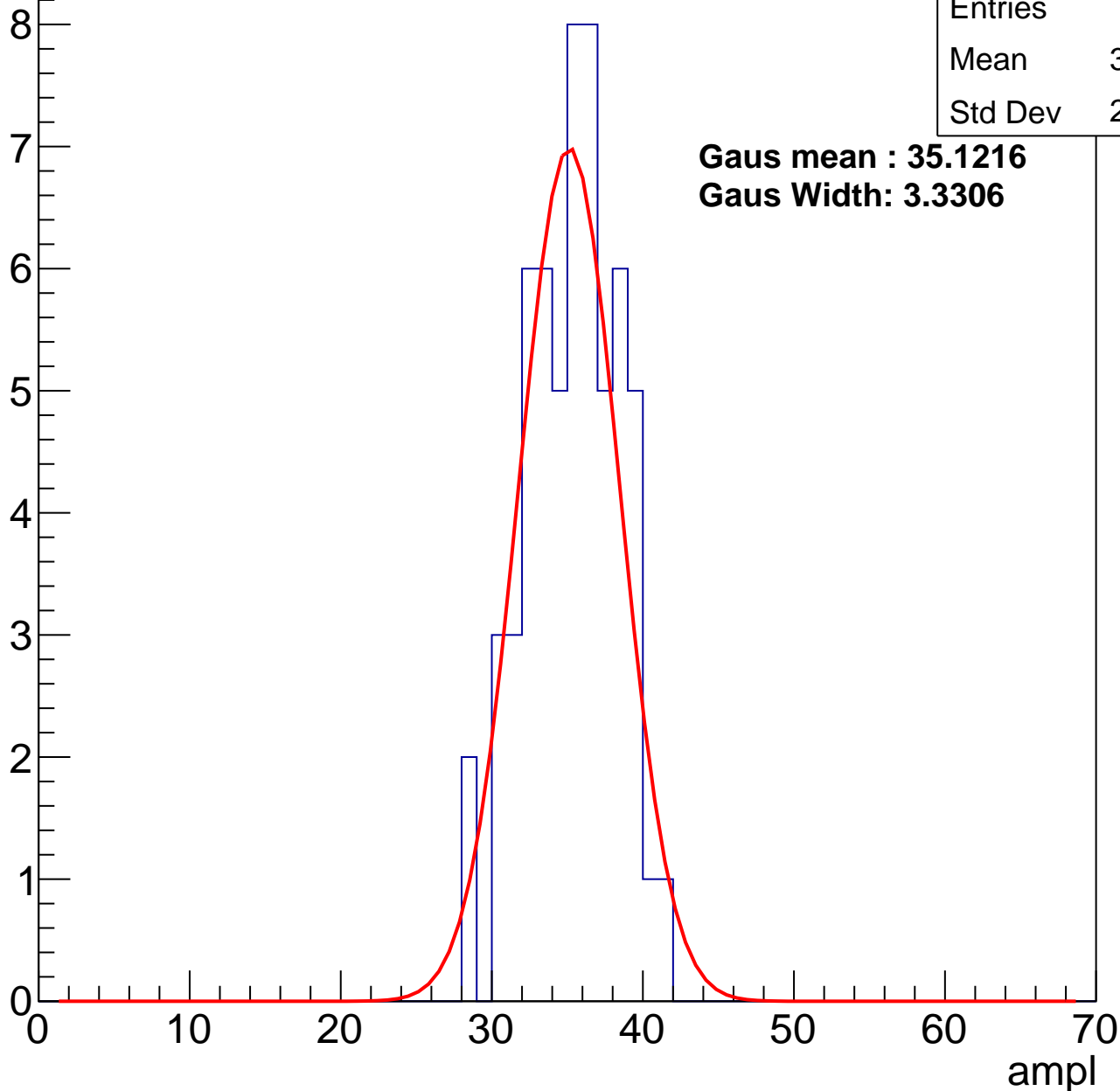
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	34.85
Std Dev	2.985

Gaus mean : 35.1216

Gaus Width: 3.3306



B1L102S, U9-ch118, adc2

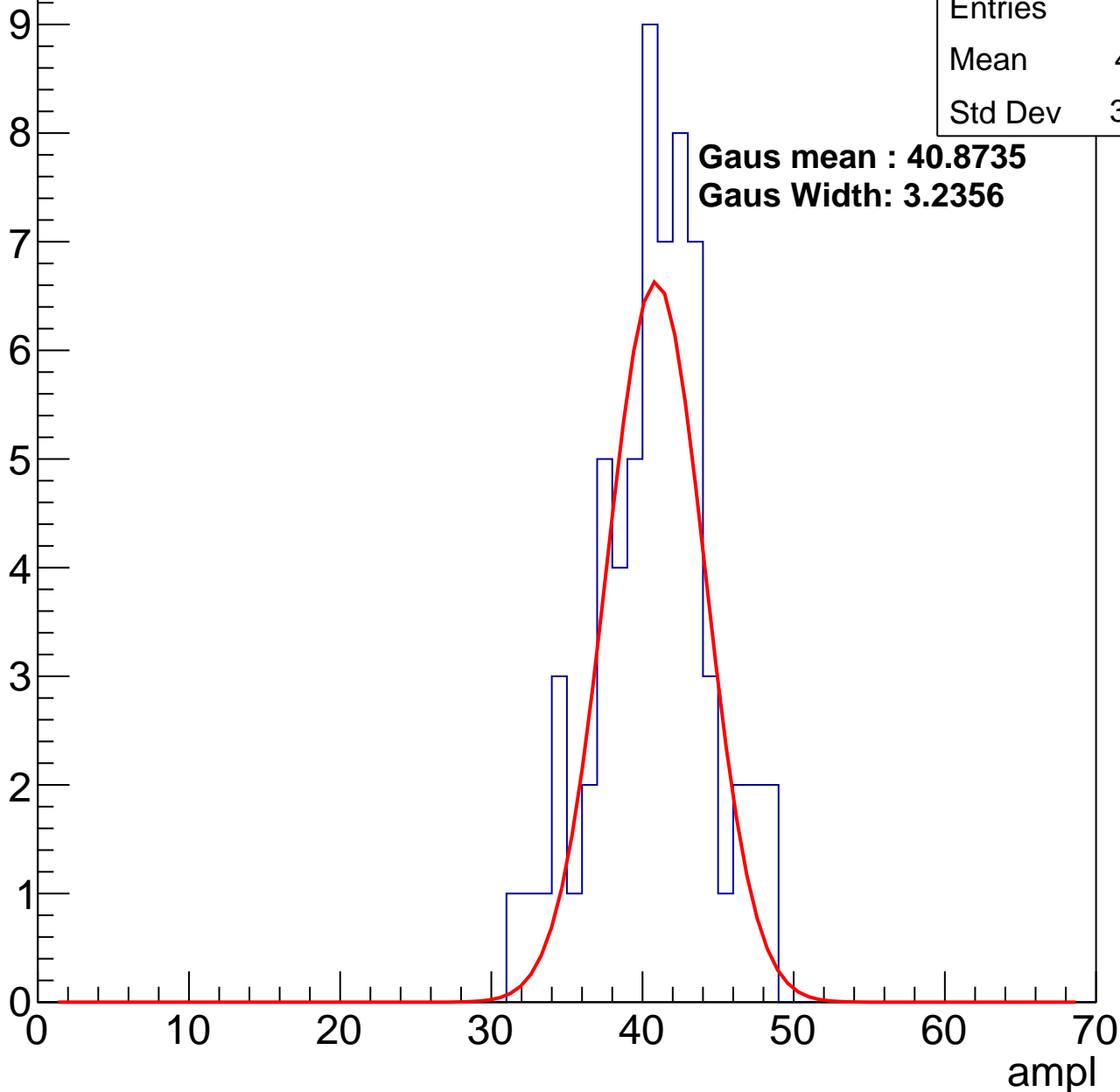
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	40.31
Std Dev	3.733

Gaus mean : 40.8735

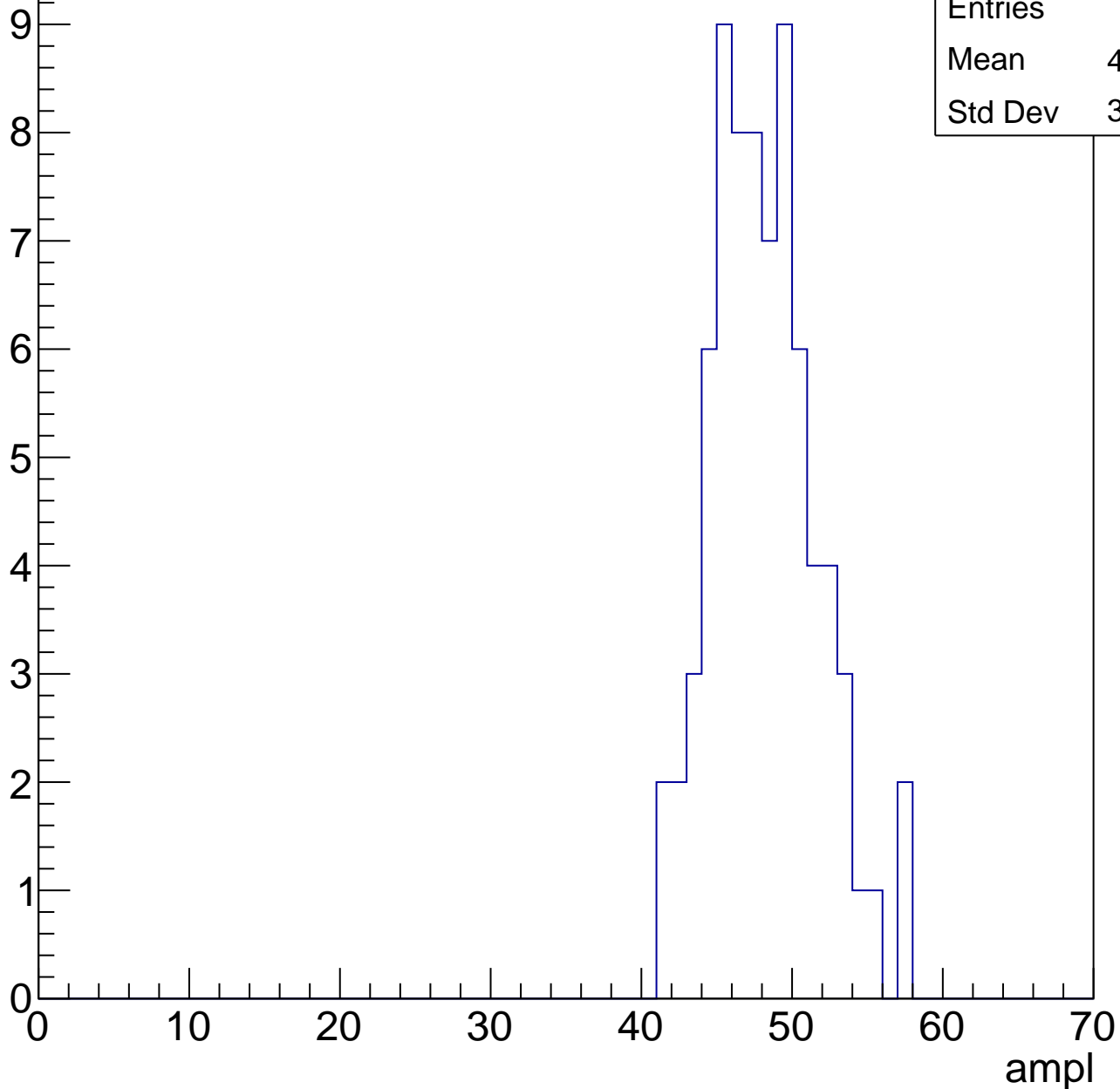
Gaus Width: 3.2356



B1L102S, U9-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

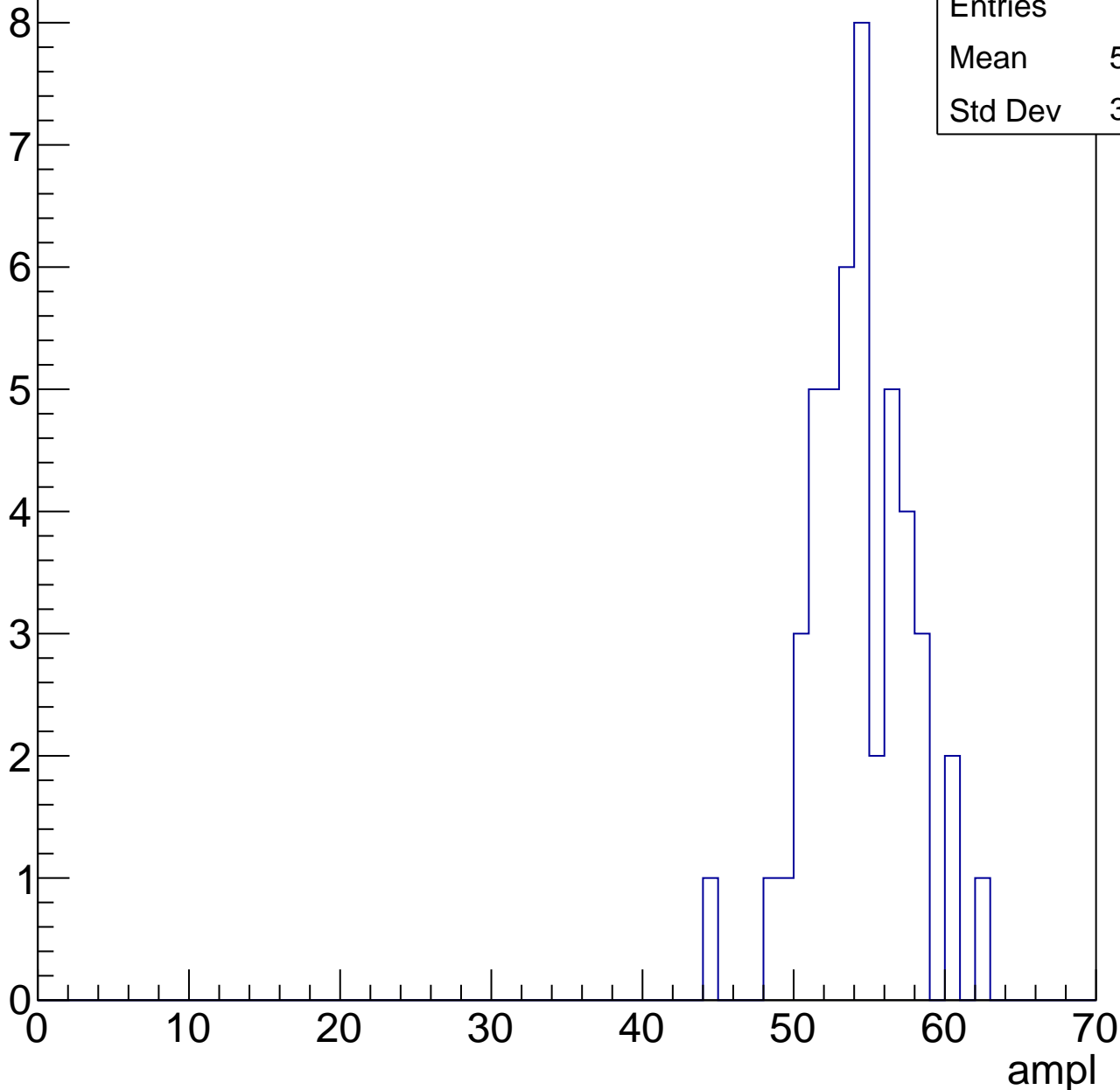


B1L102S, U9-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	53.83
Std Dev	3.328

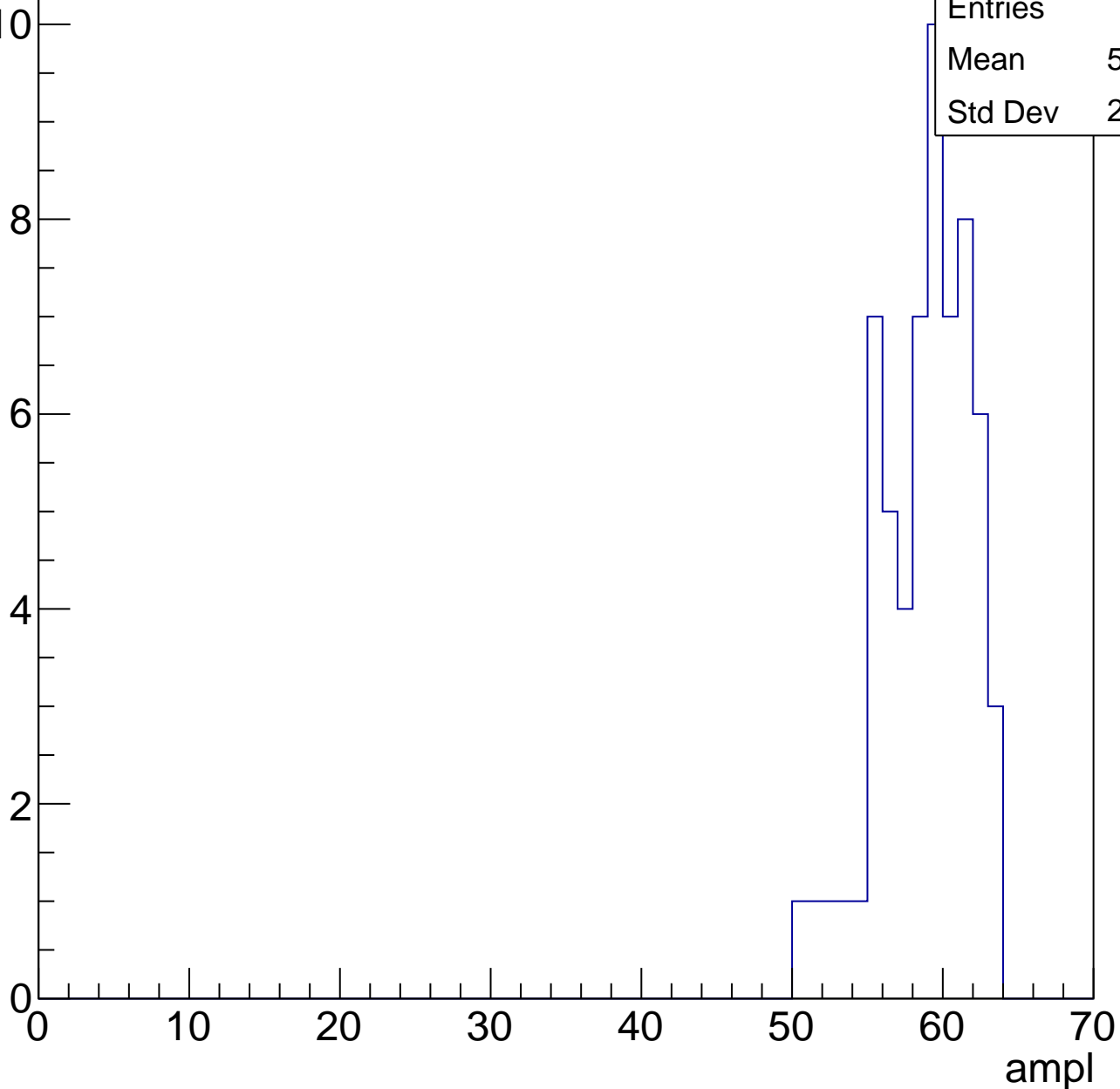


B1L102S, U9-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	58.35
Std Dev	2.979

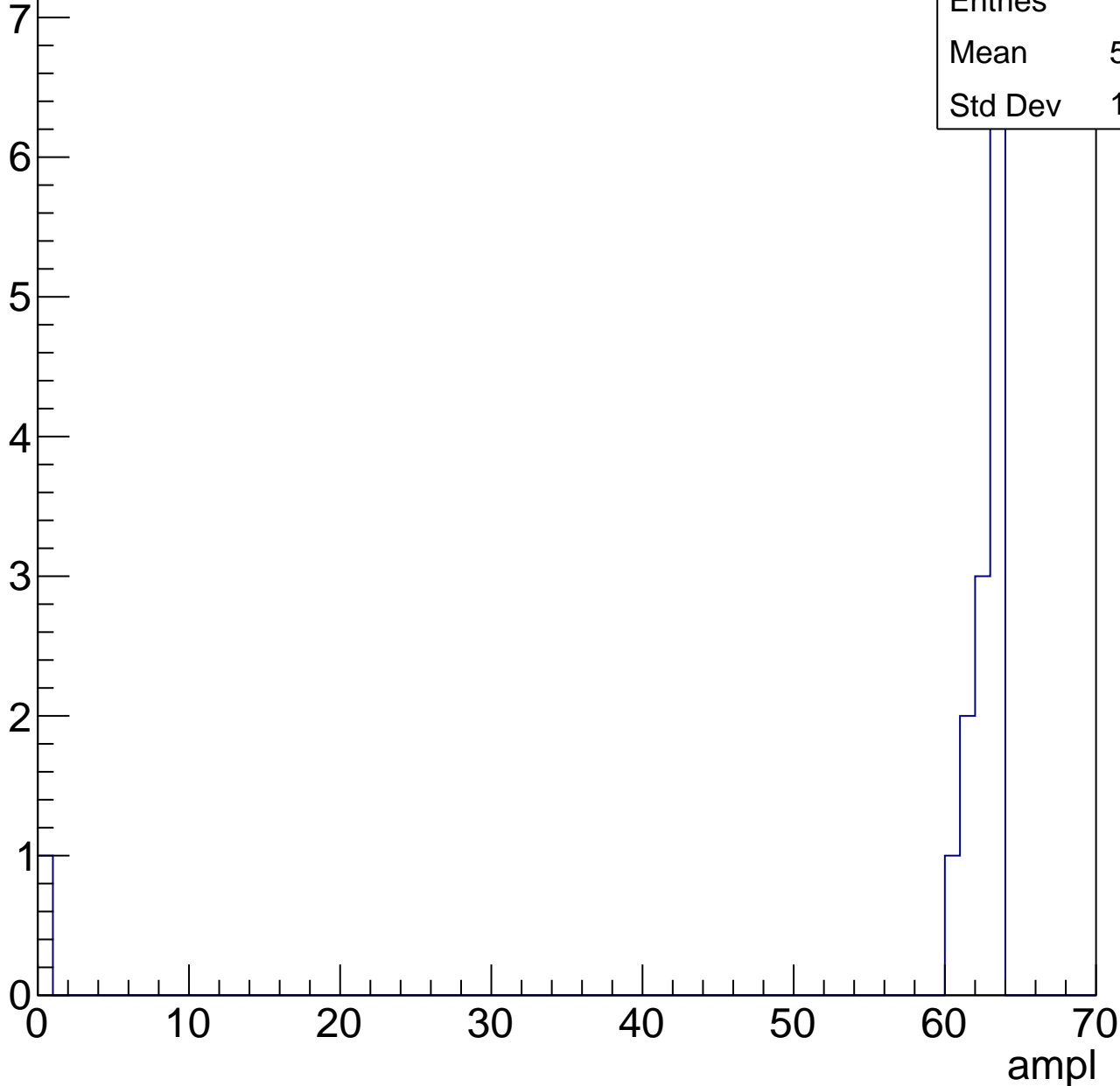


B1L102S, U9-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.79
Std Dev	16.05



B1L102S, U9-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch119, adc0

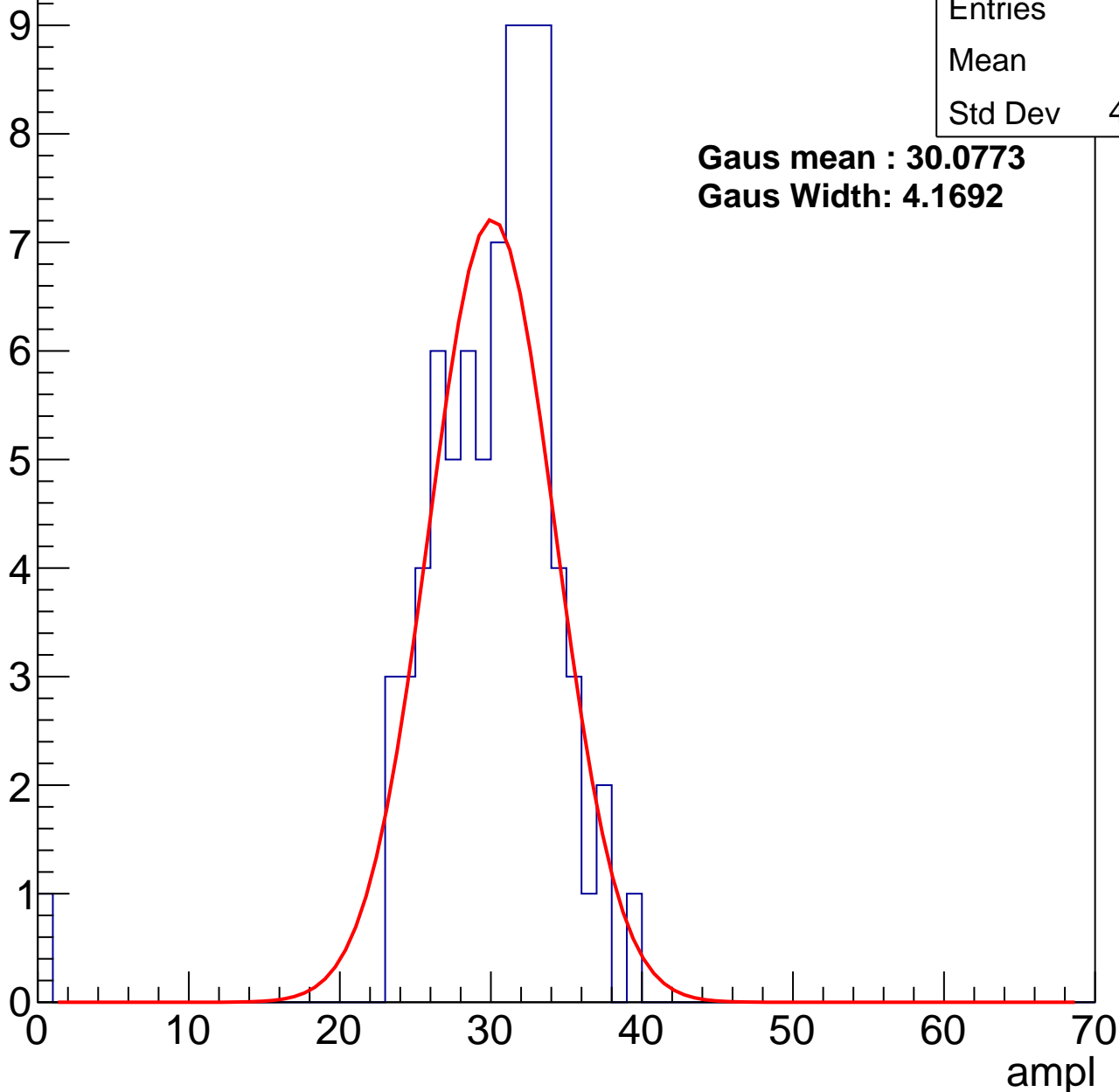
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.6
Std Dev	4.926

Gaus mean : 30.0773

Gaus Width: 4.1692



B1L102S, U9-ch119, adc1

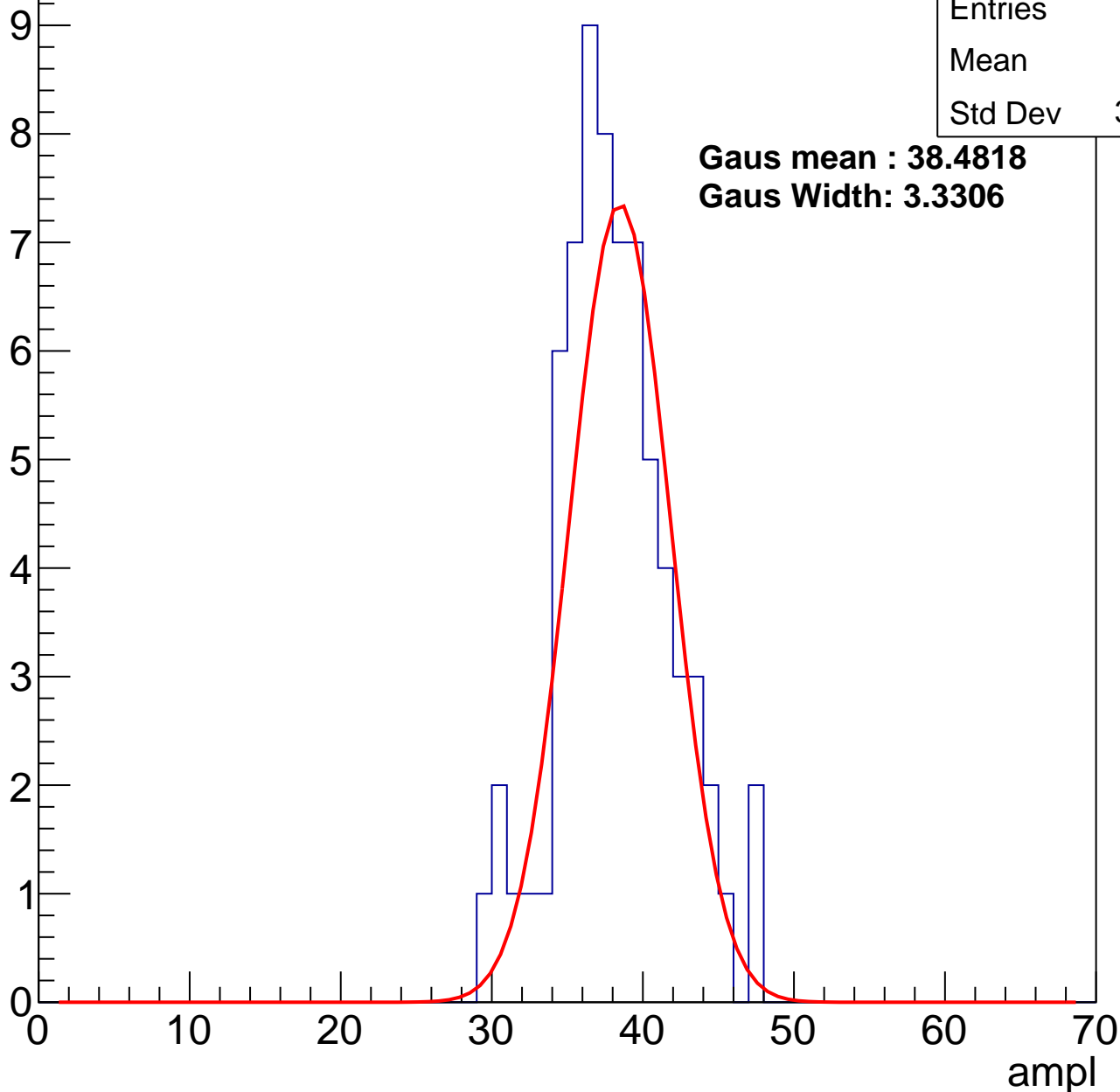
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	37.7
Std Dev	3.751

Gaus mean : 38.4818

Gaus Width: 3.3306



B1L102S, U9-ch119, adc2

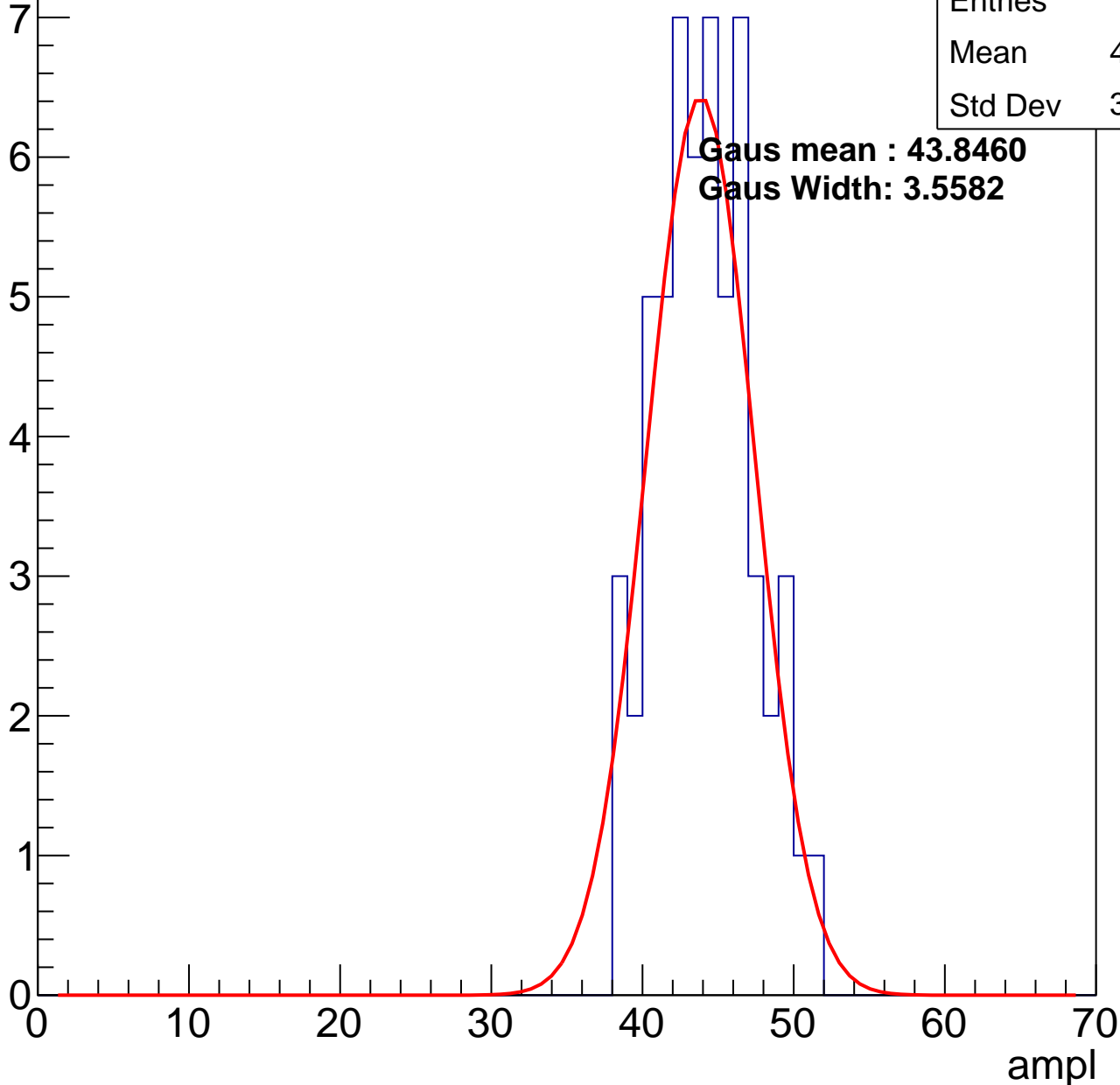
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.67
Std Dev	3.142

Gaus mean : 43.8460

Gaus Width: 3.5582

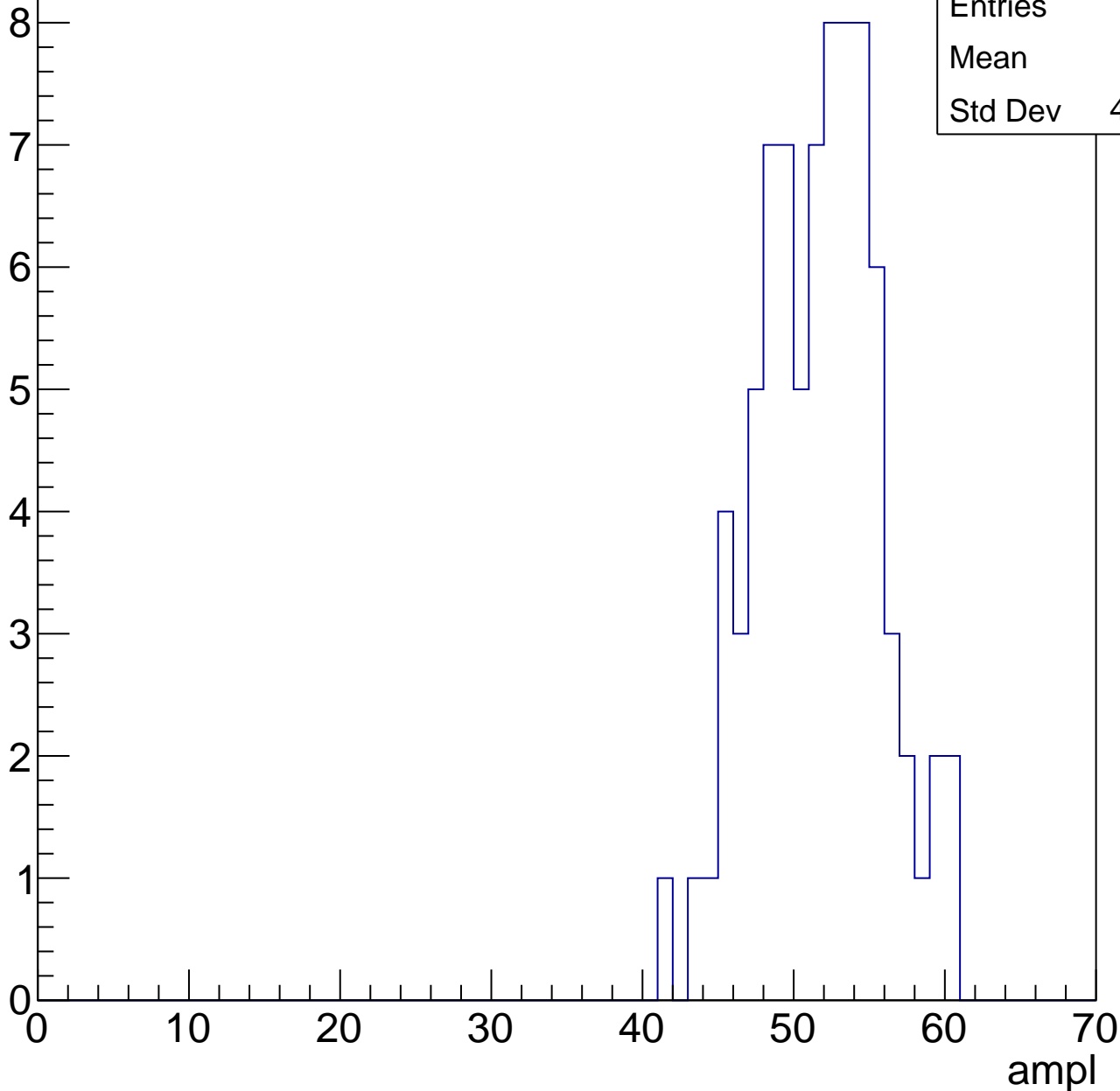


B1L102S, U9-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	51.2
Std Dev	4.004

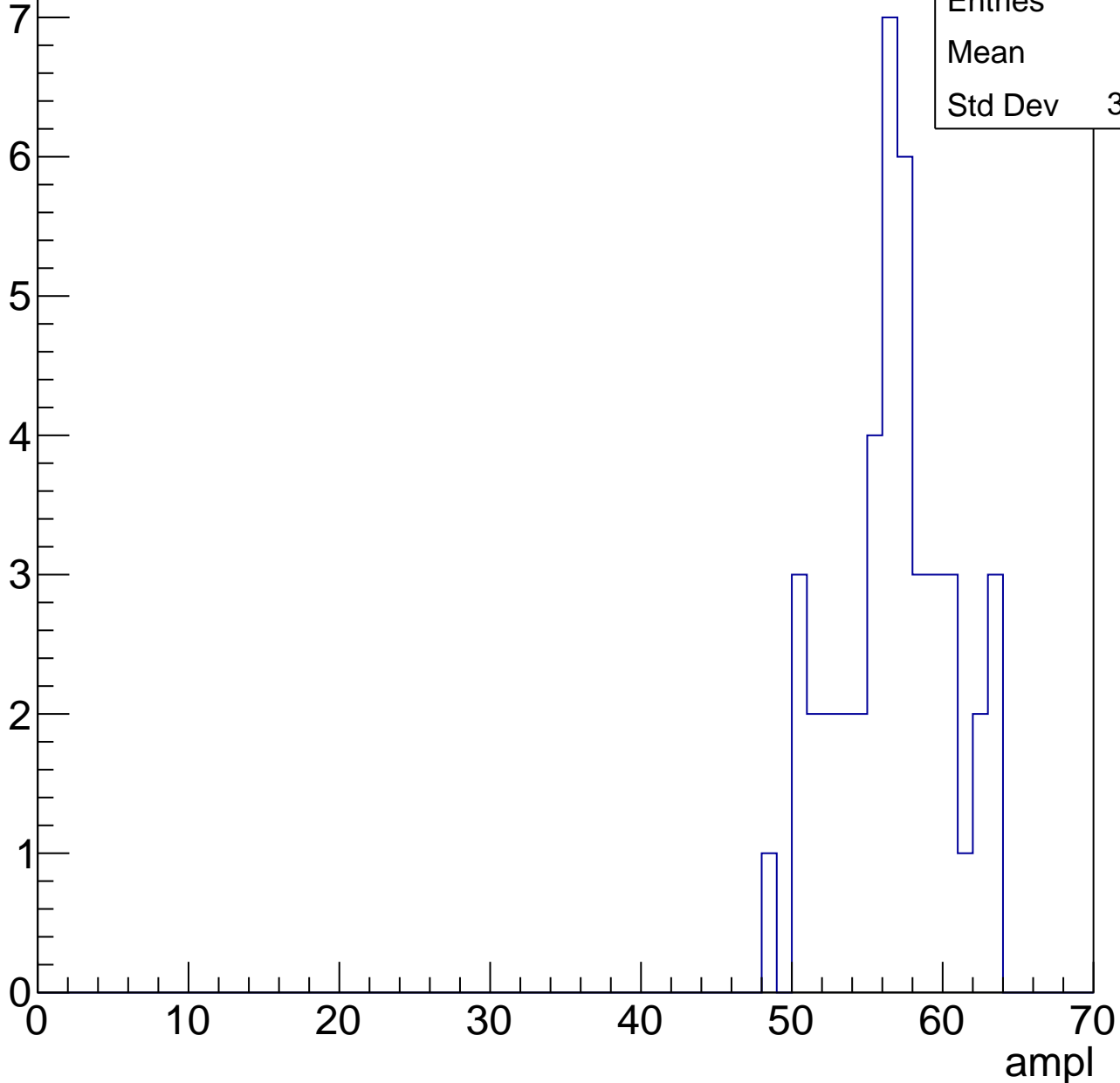


B1L102S, U9-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	56.3
Std Dev	3.745

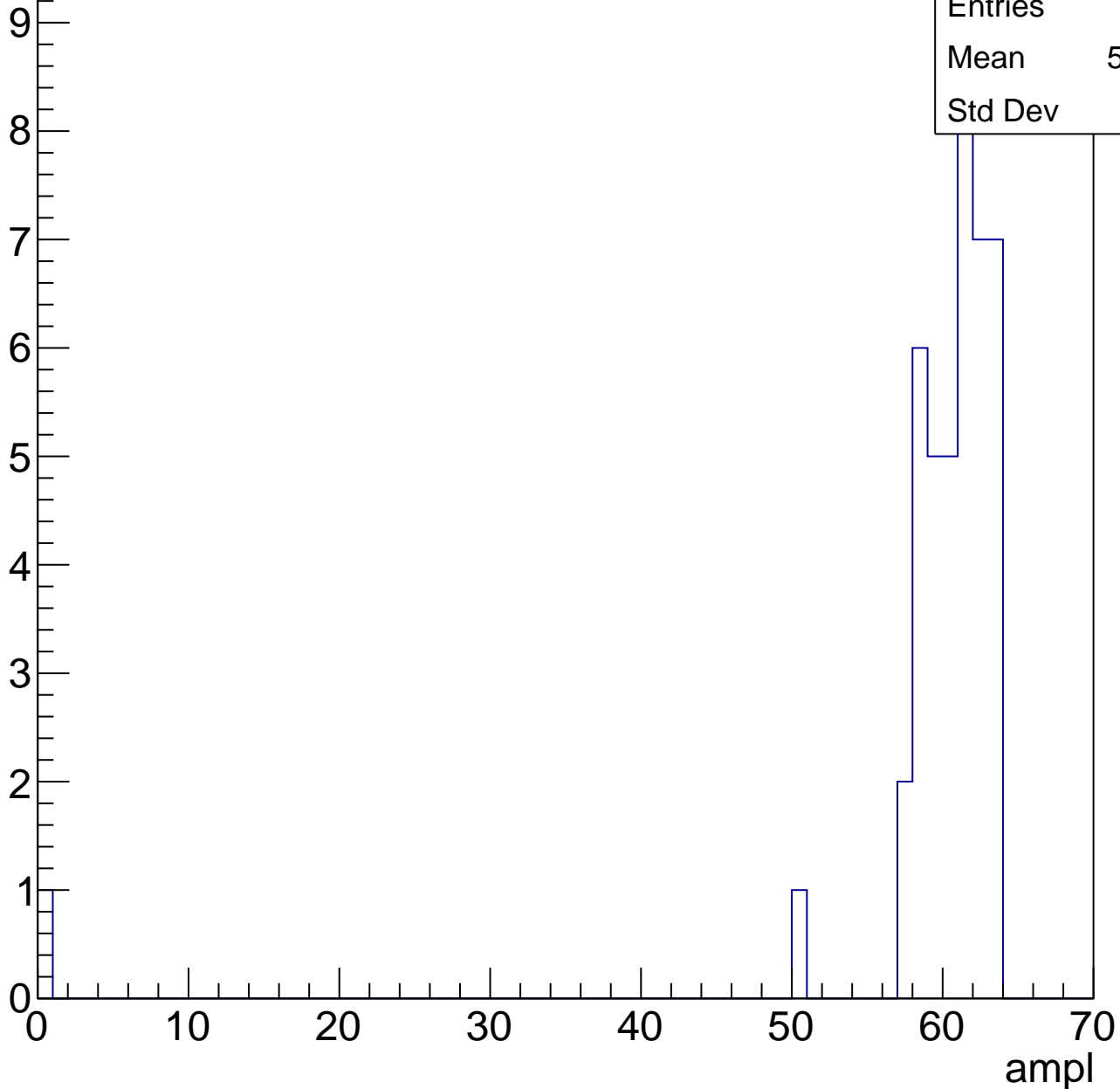


B1L102S, U9-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.86
Std Dev	9.39



B1L102S, U9-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U9-ch120, adc0

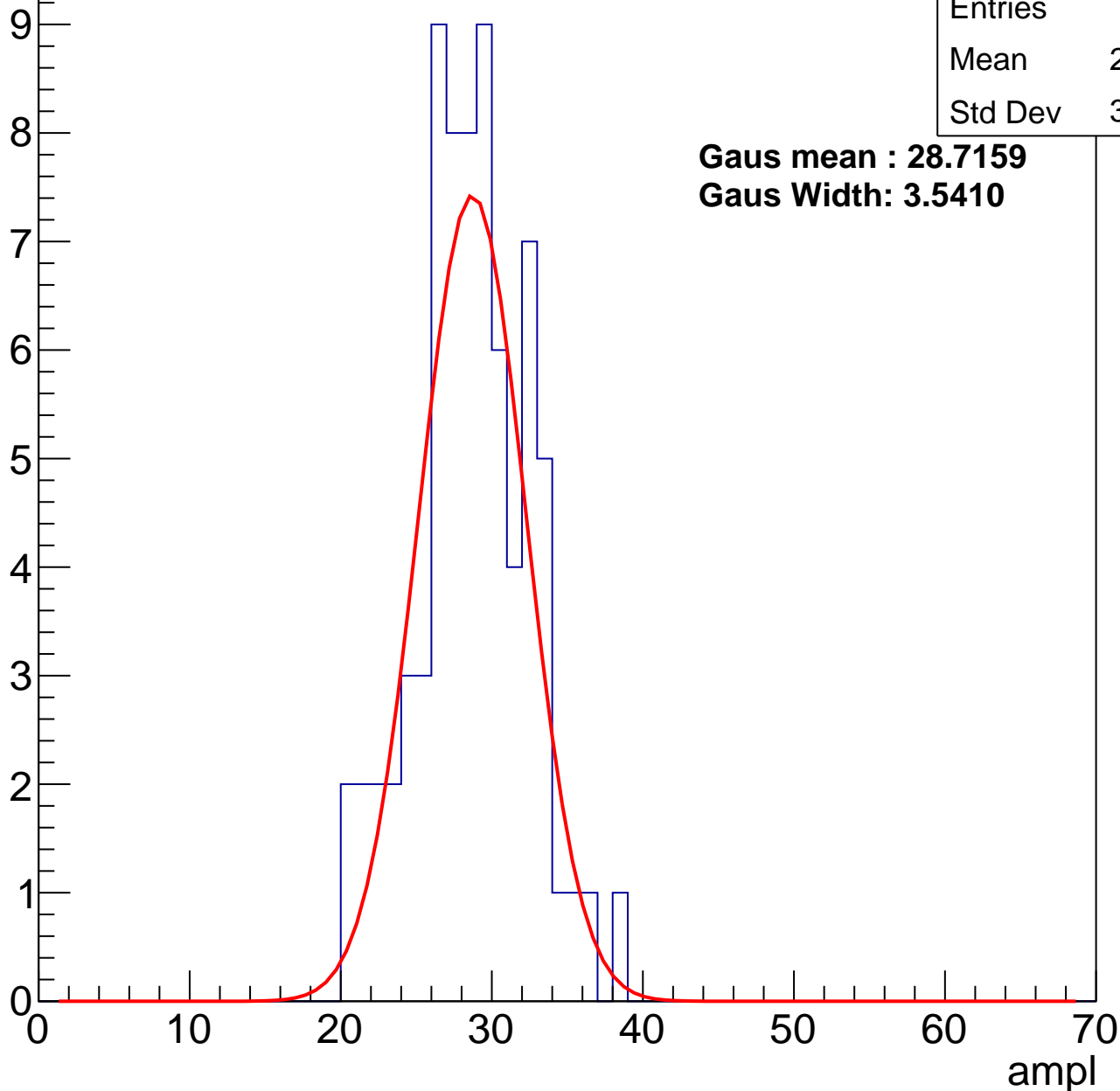
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.24
Std Dev	3.708

Gaus mean : 28.7159

Gaus Width: 3.5410



B1L102S, U9-ch120, adc1

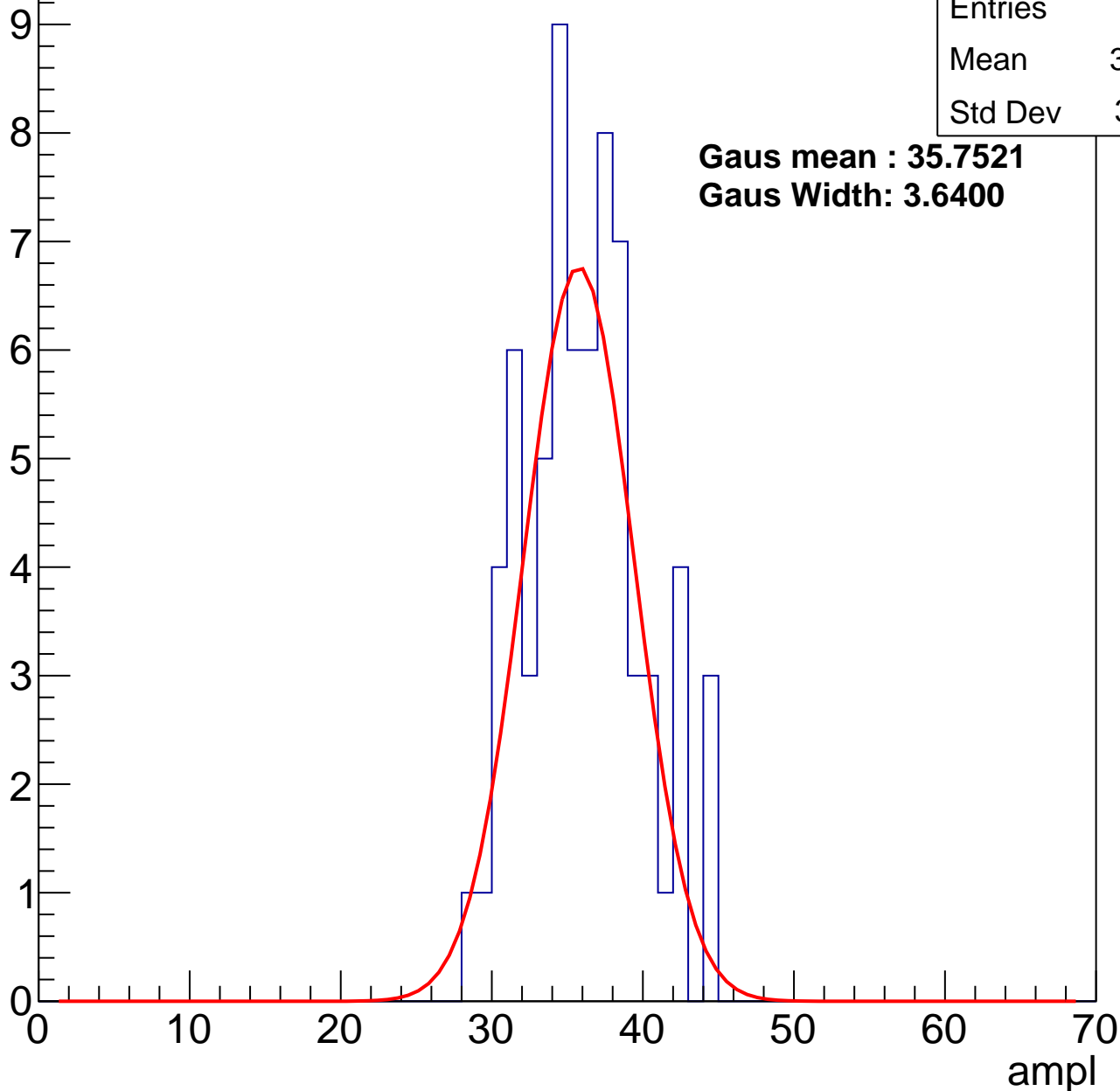
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.66
Std Dev	3.791

Gaus mean : 35.7521

Gaus Width: 3.6400



B1L102S, U9-ch120, adc2

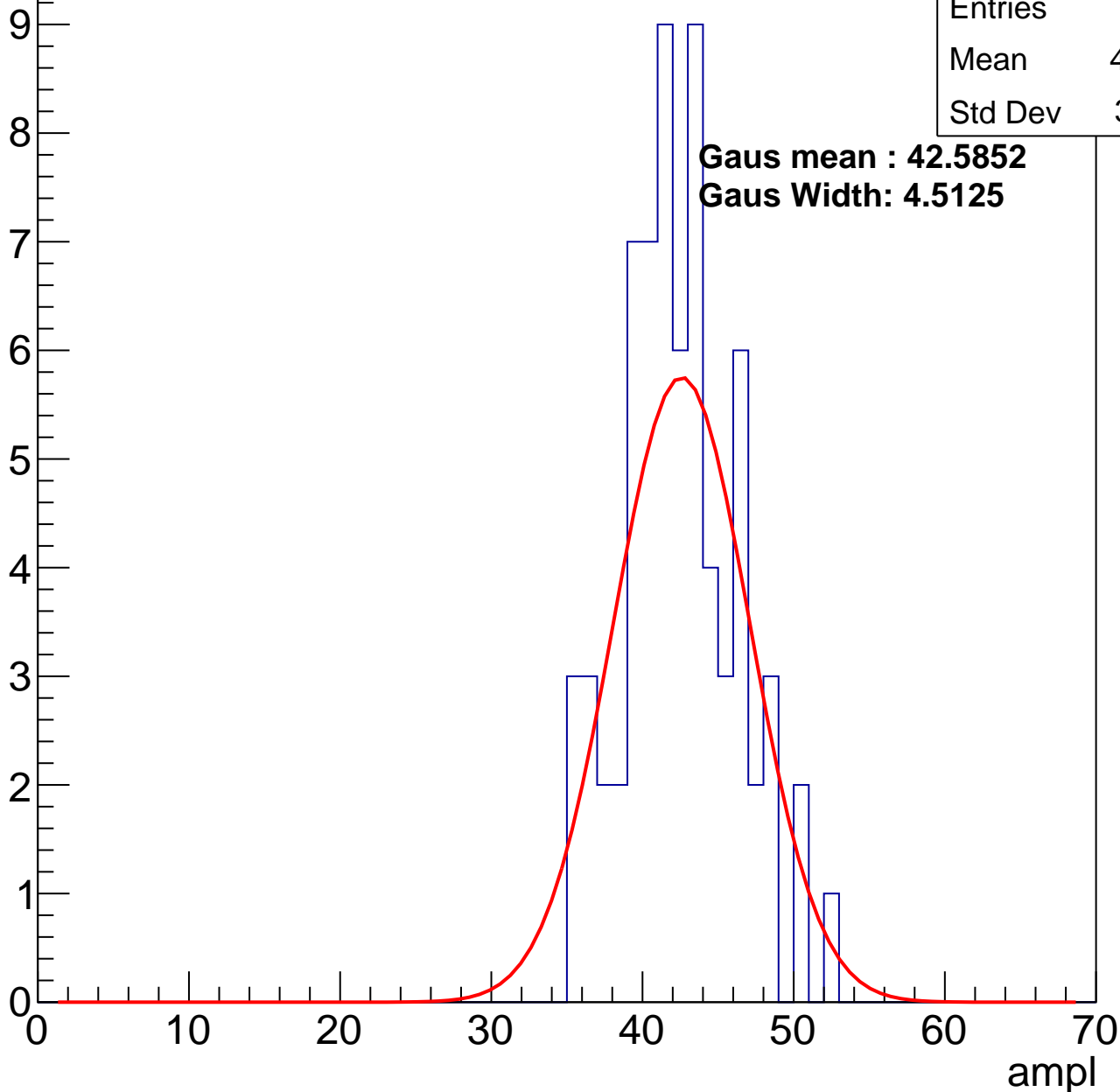
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.04
Std Dev	3.751

Gaus mean : 42.5852

Gaus Width: 4.5125

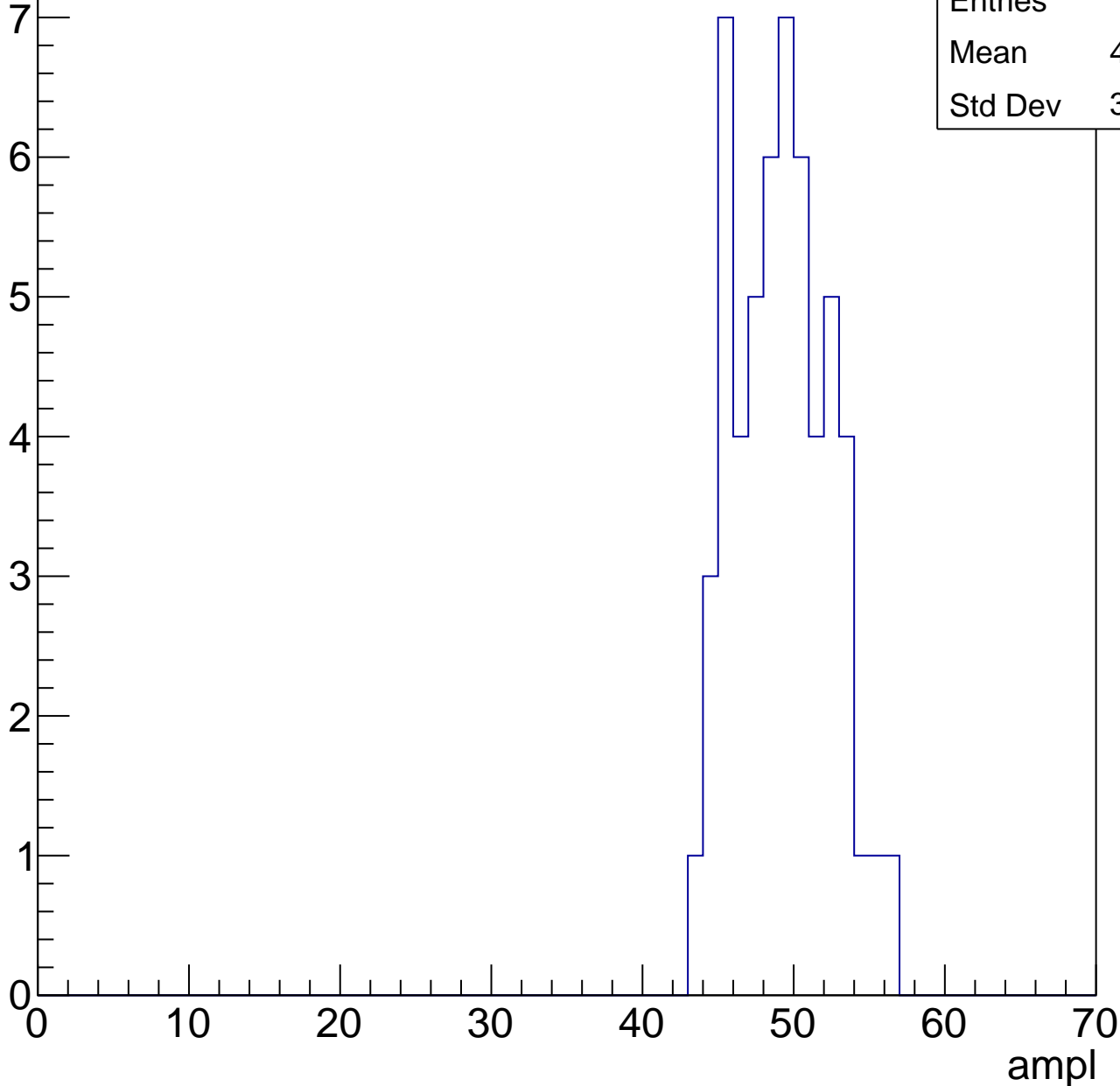


B1L102S, U9-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.75
Std Dev	3.082

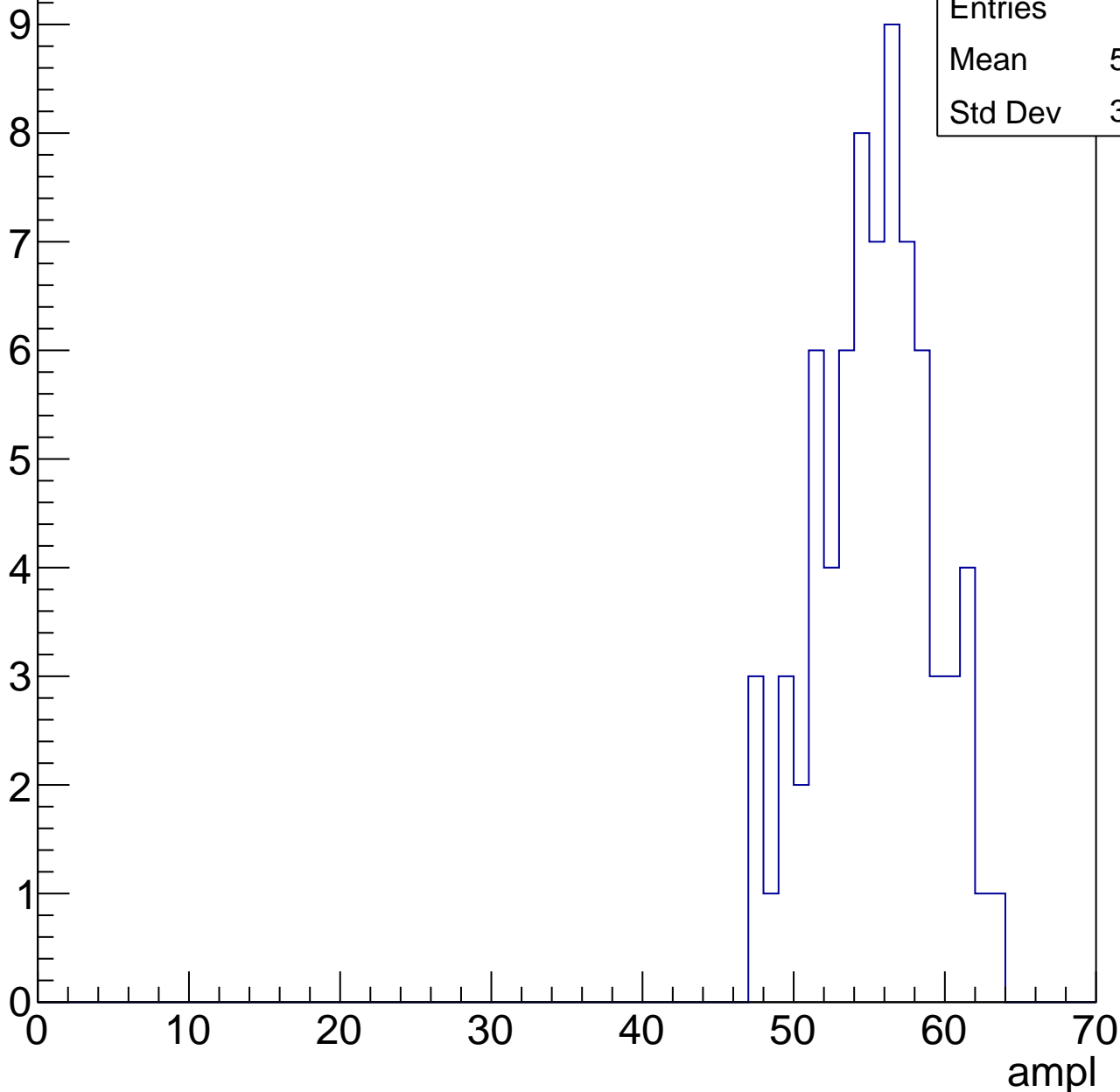


B1L102S, U9-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

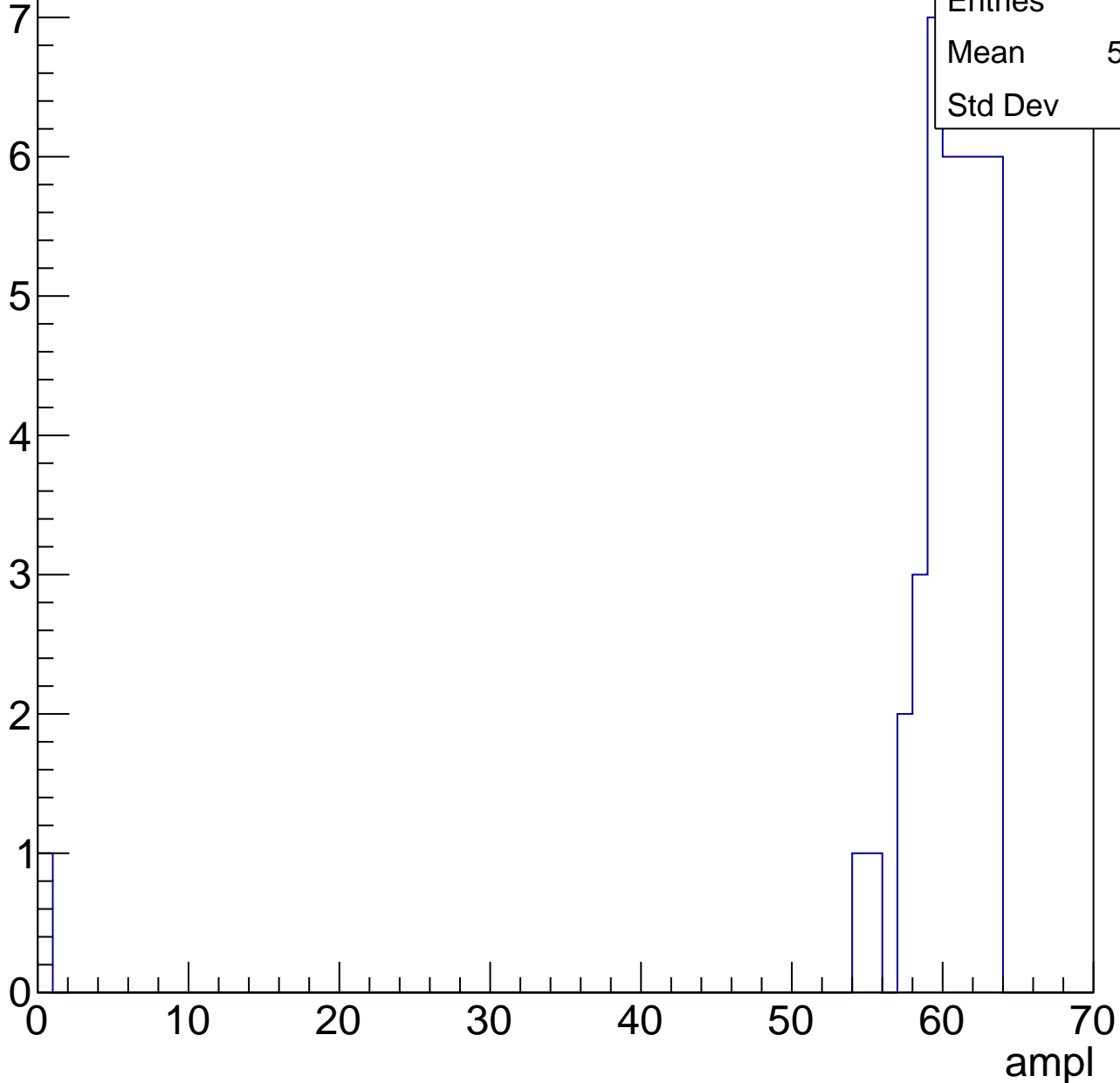
Entries	74
Mean	54.89
Std Dev	3.736



B1L102S, U9-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

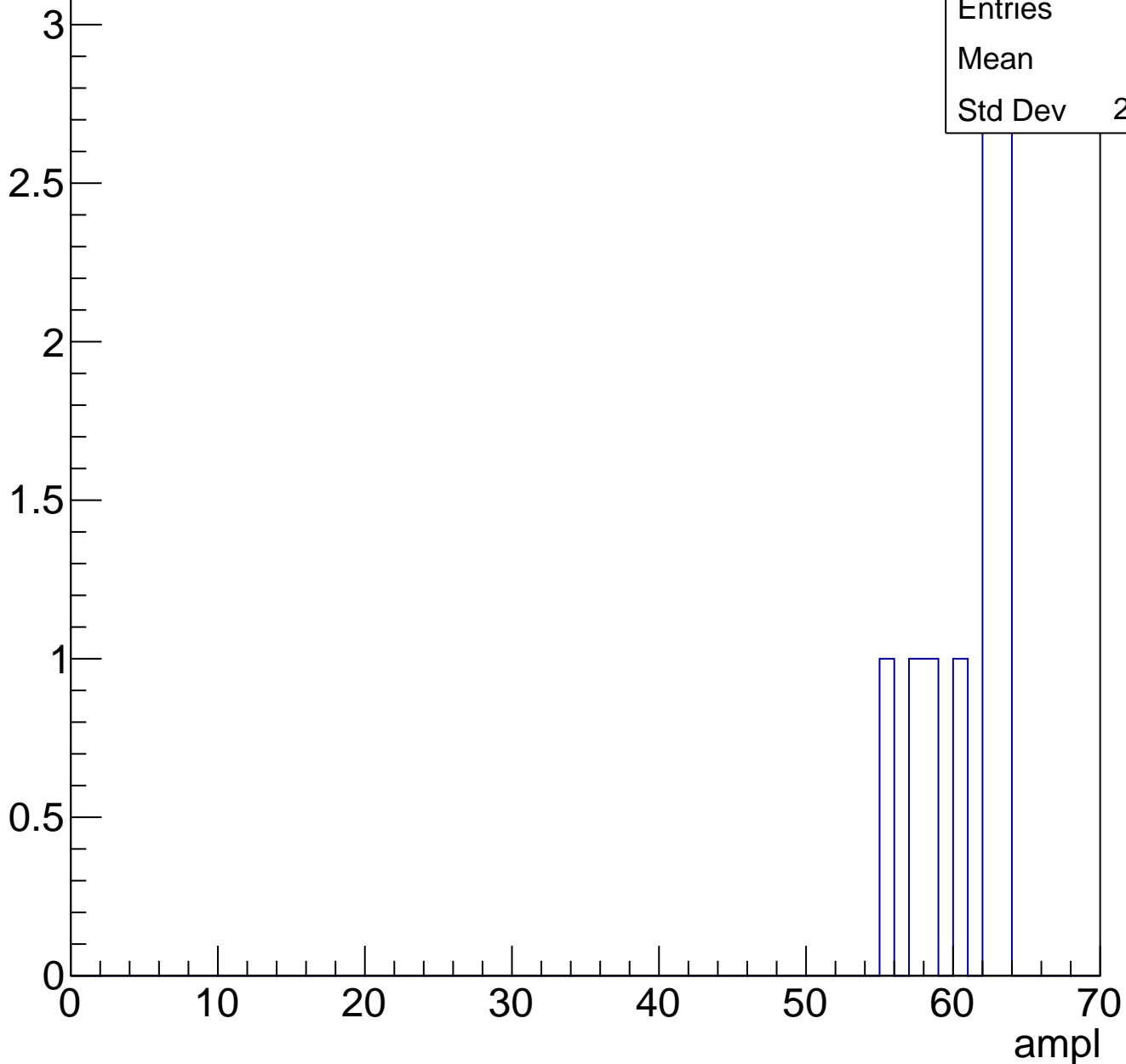
Entry



B1L102S, U9-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch121, adc0

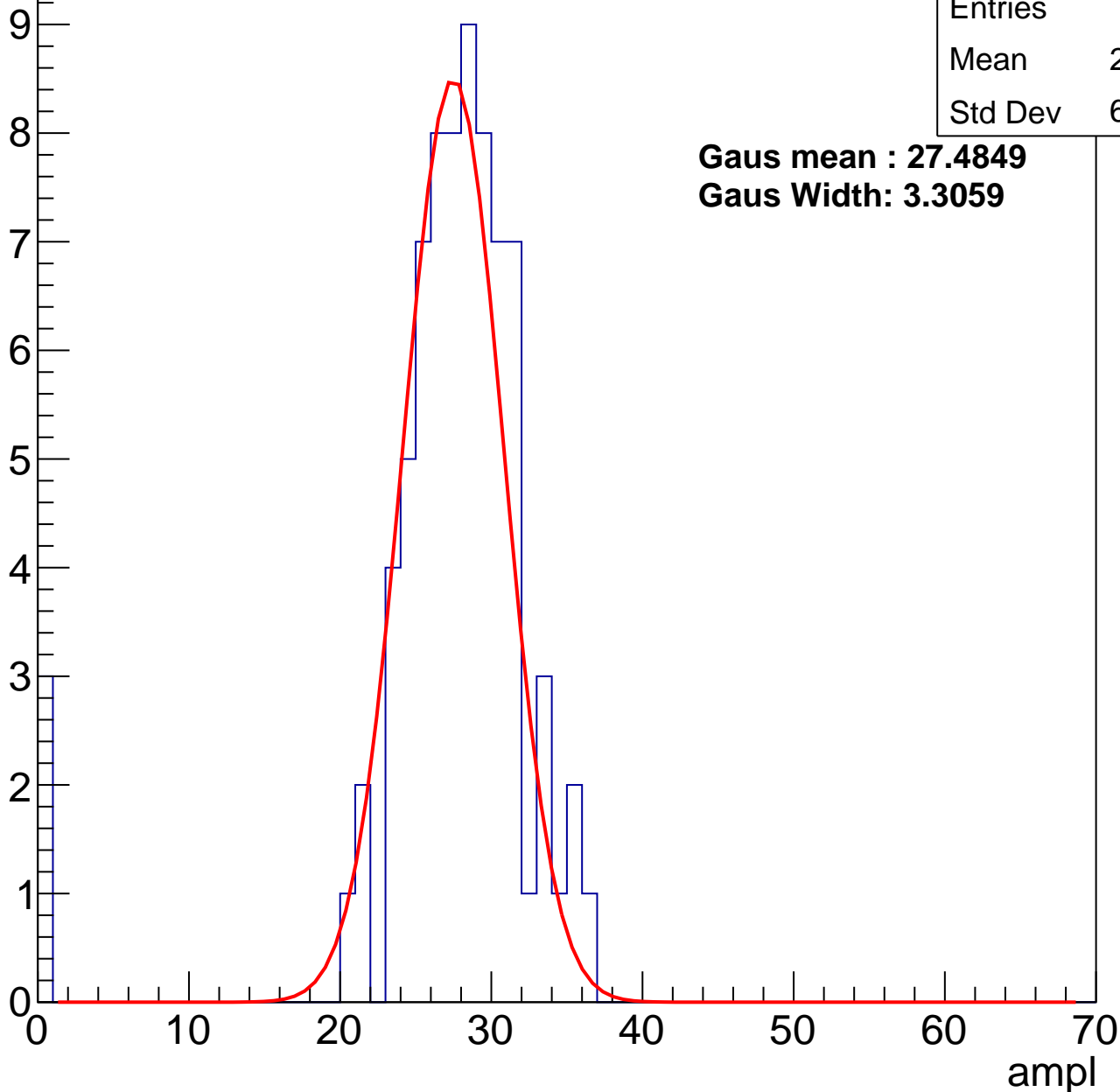
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	26.69
Std Dev	6.296

Gaus mean : 27.4849

Gaus Width: 3.3059



B1L102S, U9-ch121, adc1

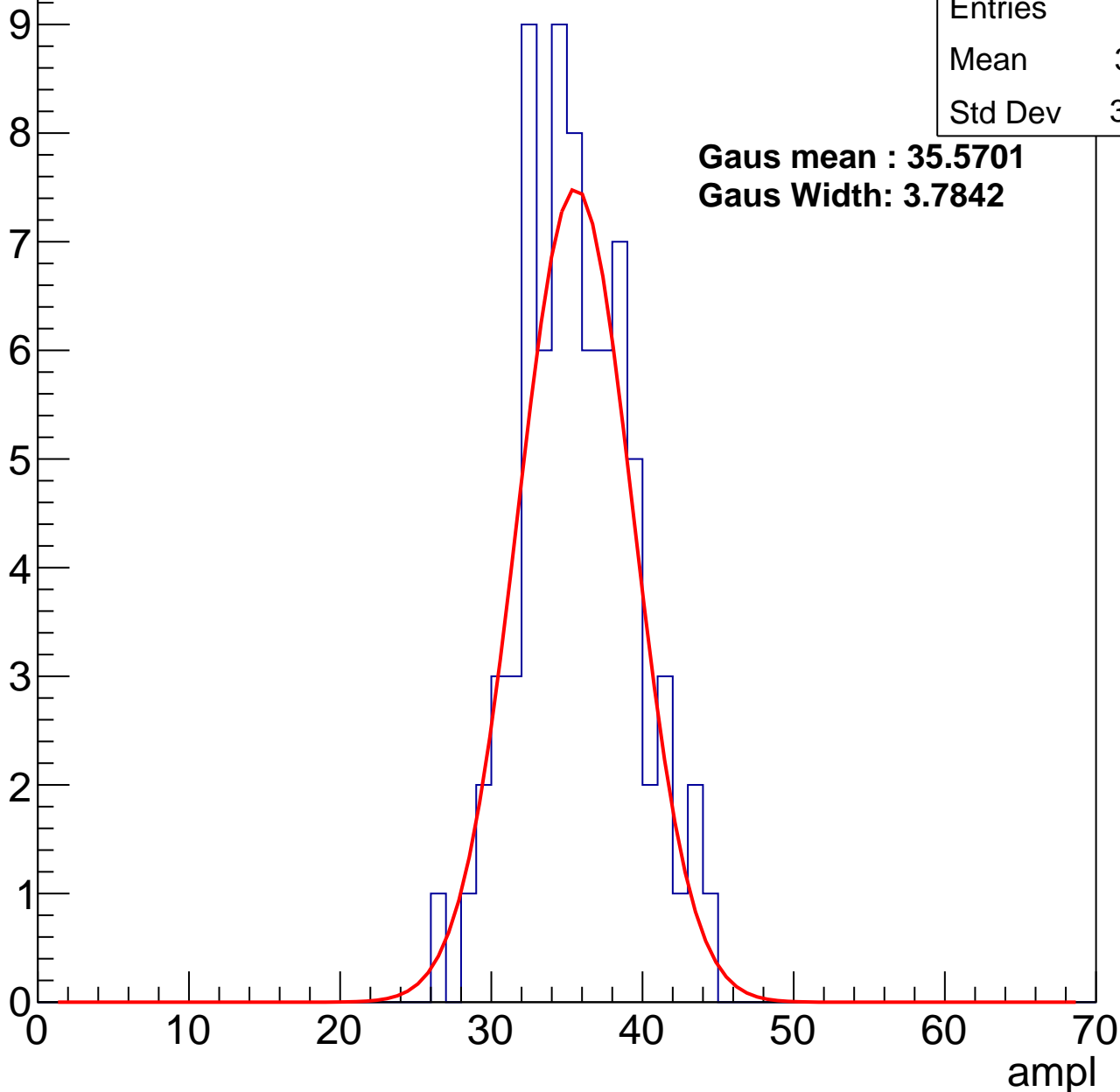
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.21
Std Dev	3.696

Gaus mean : 35.5701

Gaus Width: 3.7842



B1L102S, U9-ch121, adc2

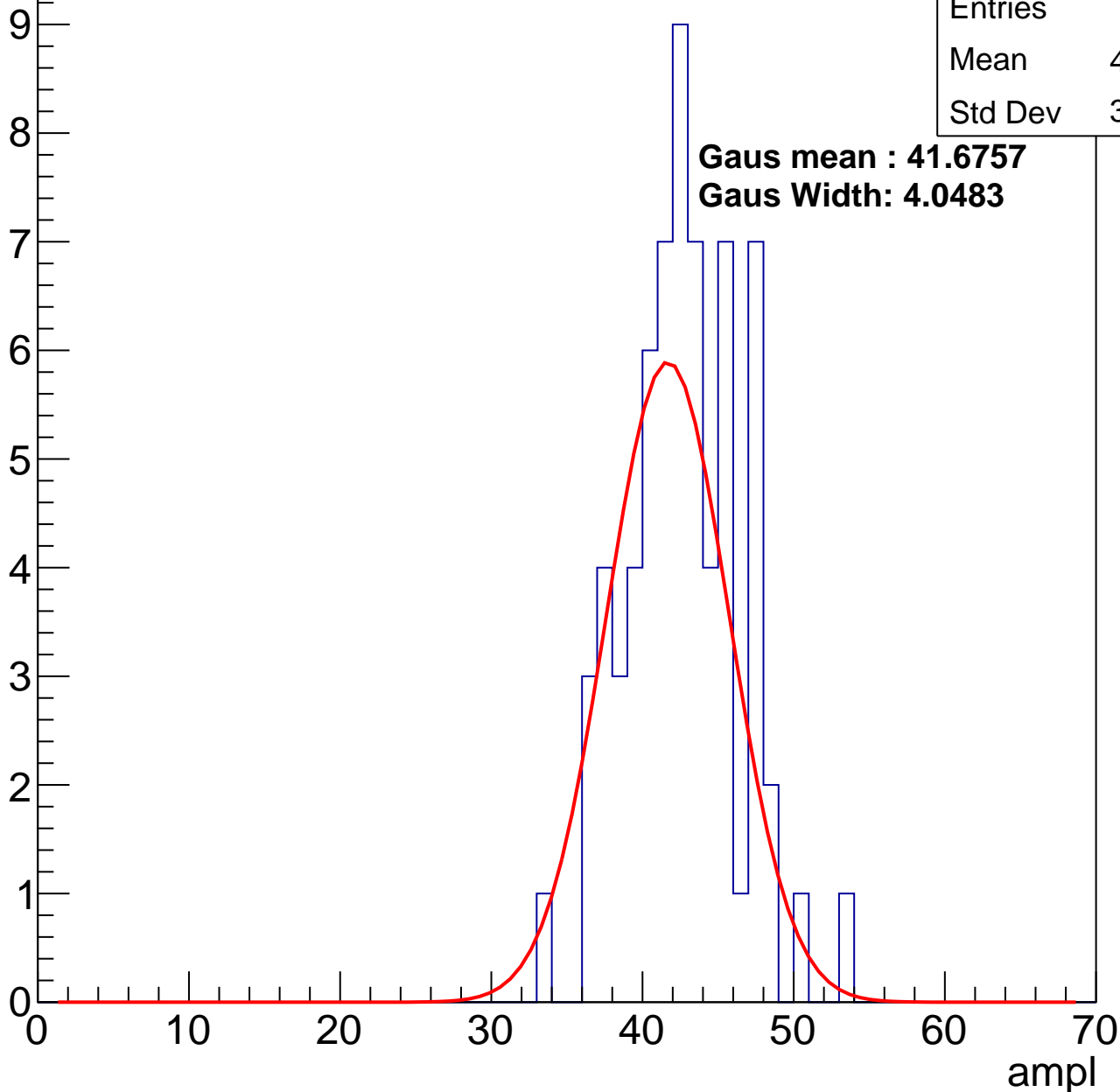
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.24
Std Dev	3.734

Gaus mean : 41.6757

Gaus Width: 4.0483

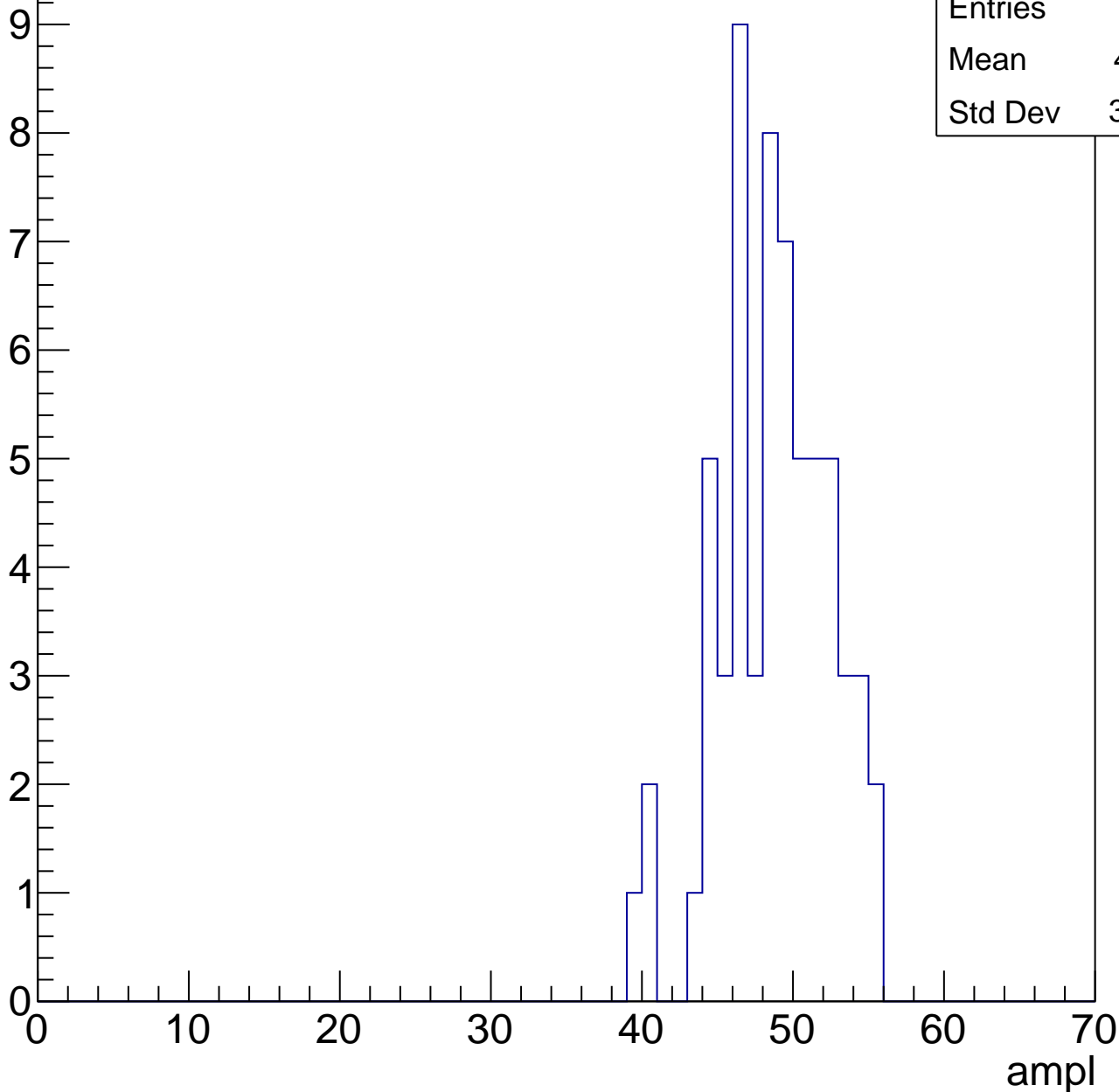


B1L102S, U9-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

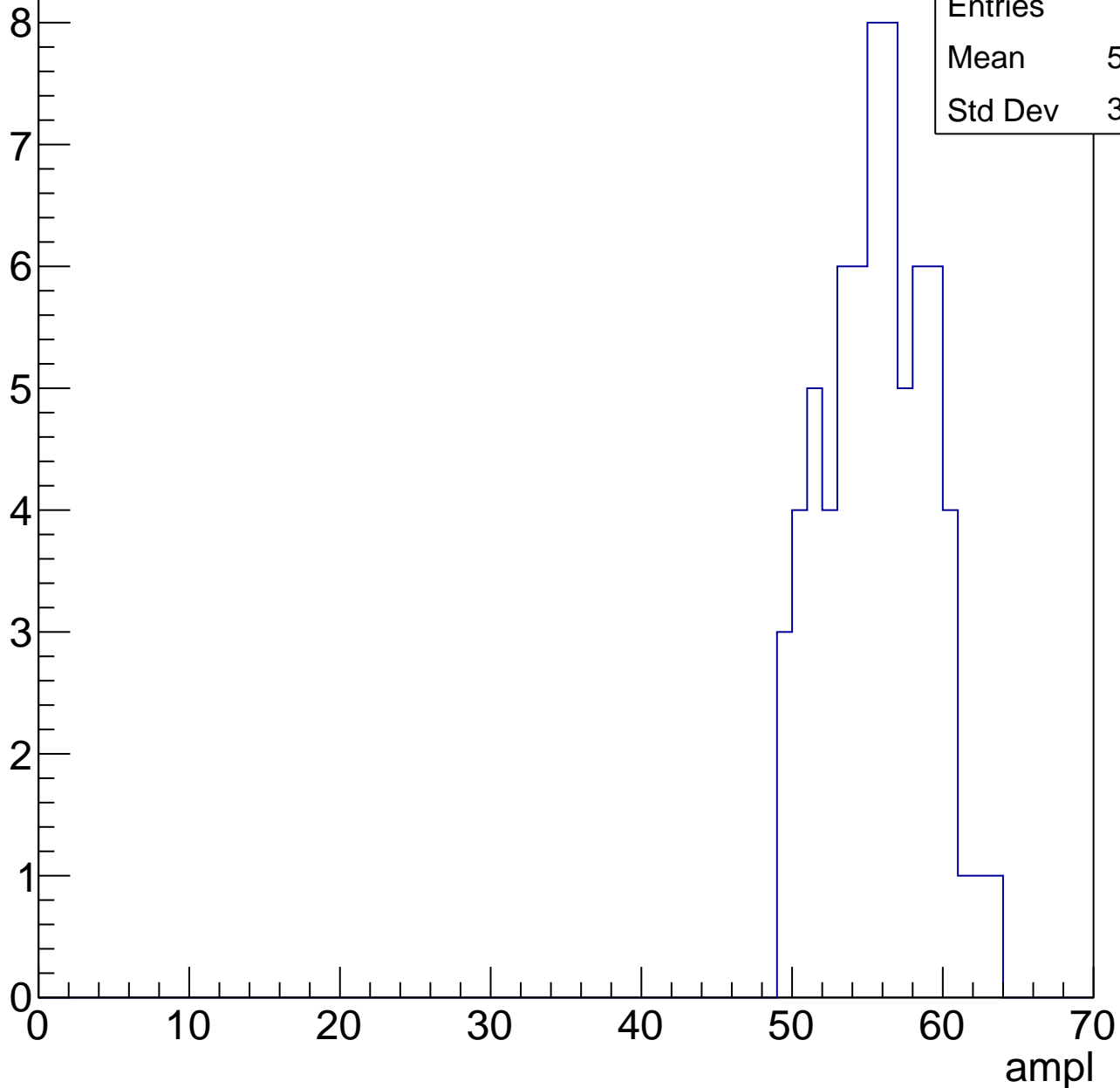
Entries	62
Mean	48.31
Std Dev	3.608



B1L102S, U9-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

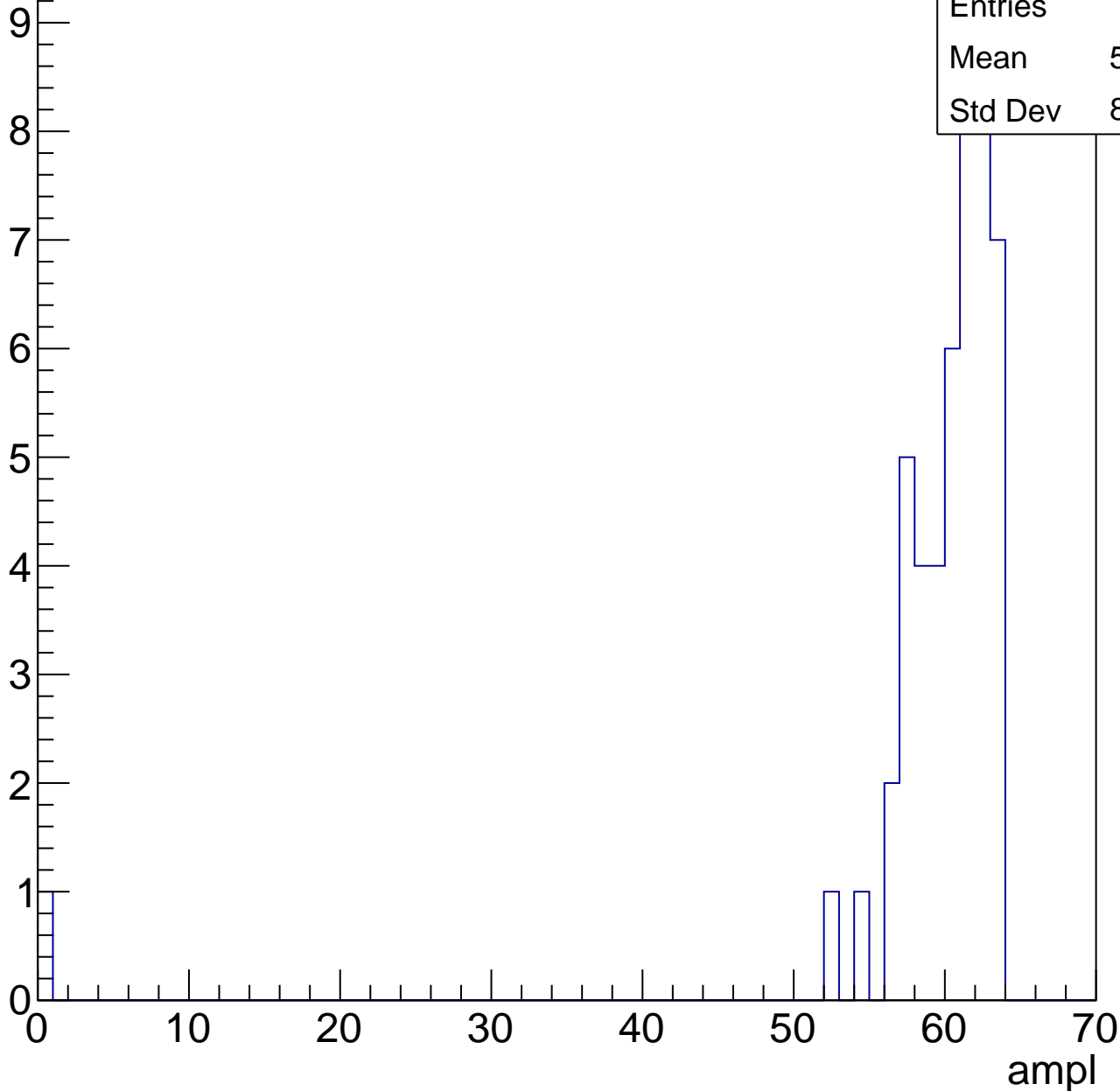


B1L102S, U9-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.69
Std Dev	8.919



B1L102S, U9-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U9-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch122, adc0

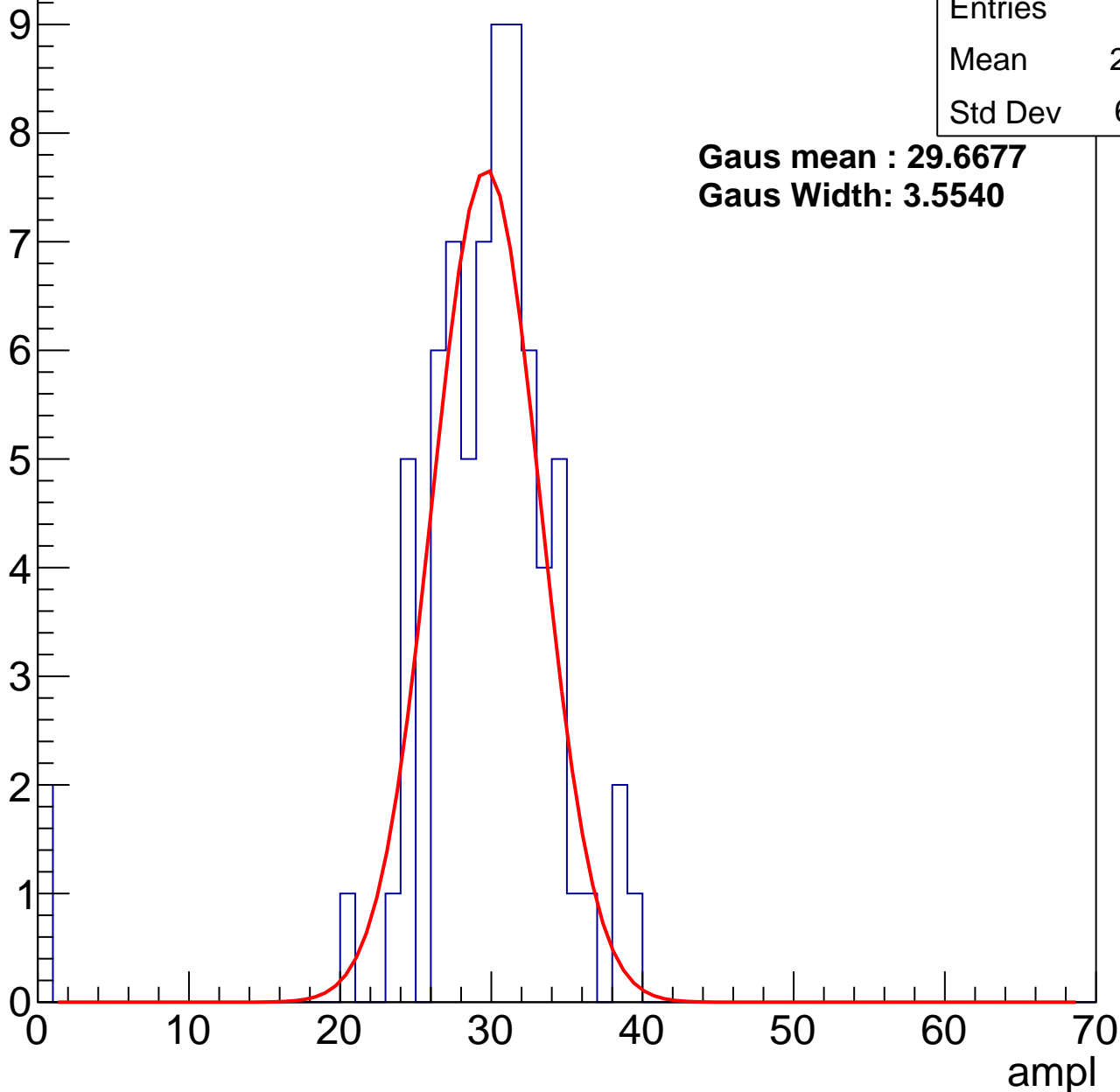
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.89
Std Dev	6.061

Gaus mean : 29.6677

Gaus Width: 3.5540



B1L102S, U9-ch122, adc1

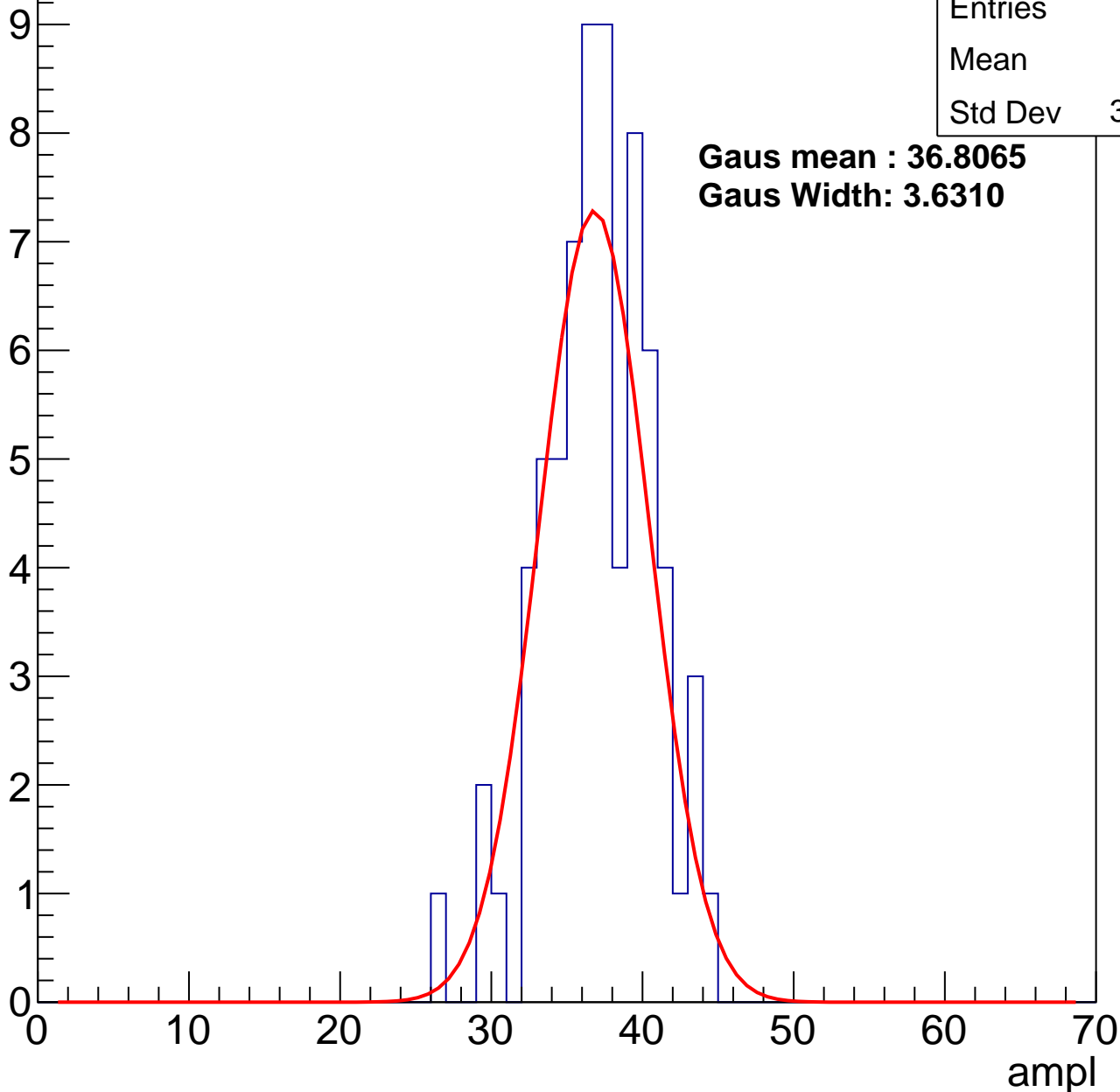
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.6
Std Dev	3.555

Gaus mean : 36.8065

Gaus Width: 3.6310



B1L102S, U9-ch122, adc2

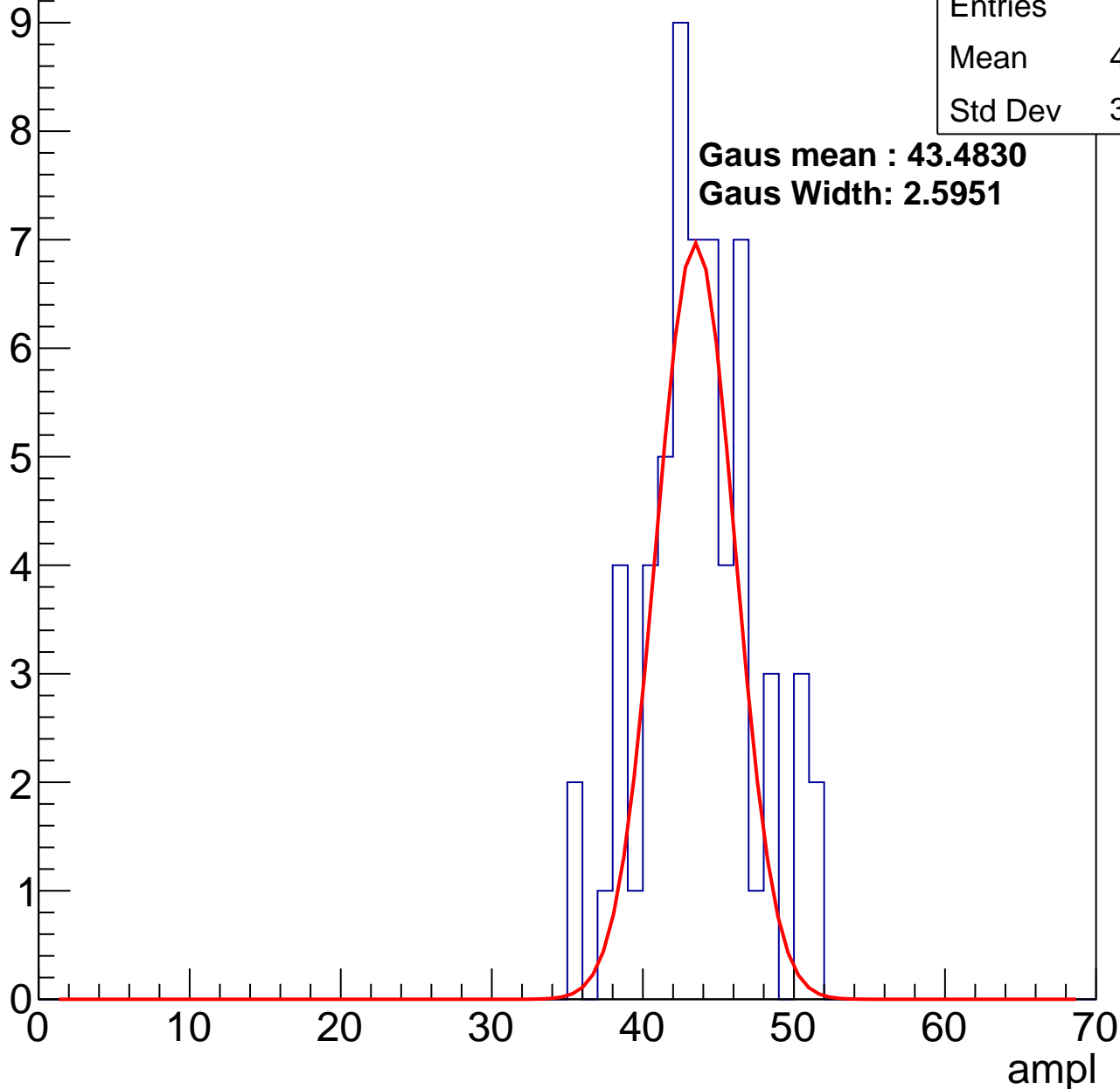
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.25
Std Dev	3.636

Gaus mean : 43.4830

Gaus Width: 2.5951

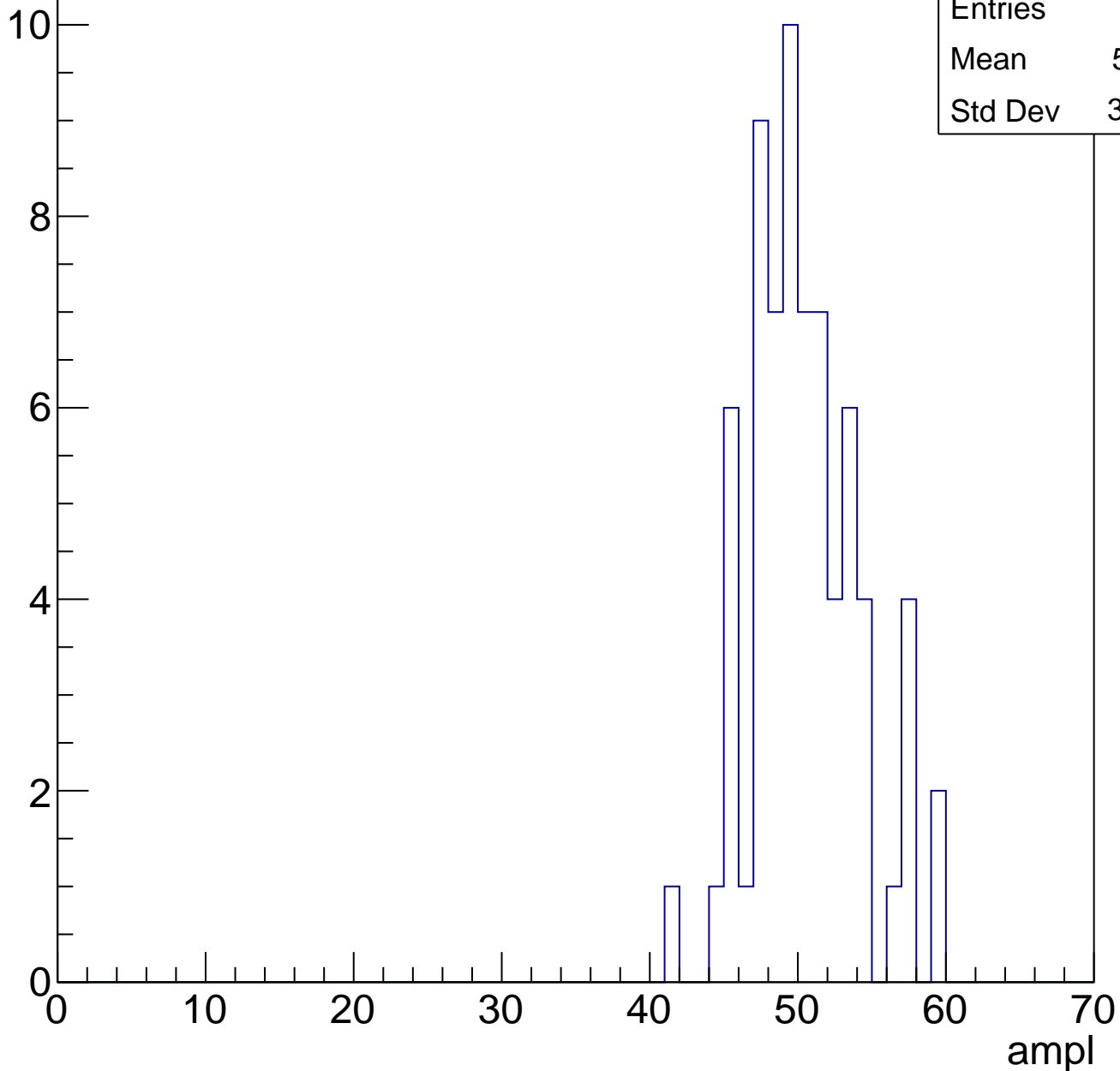


B1L102S, U9-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	50.01
Std Dev	3.659

Entry

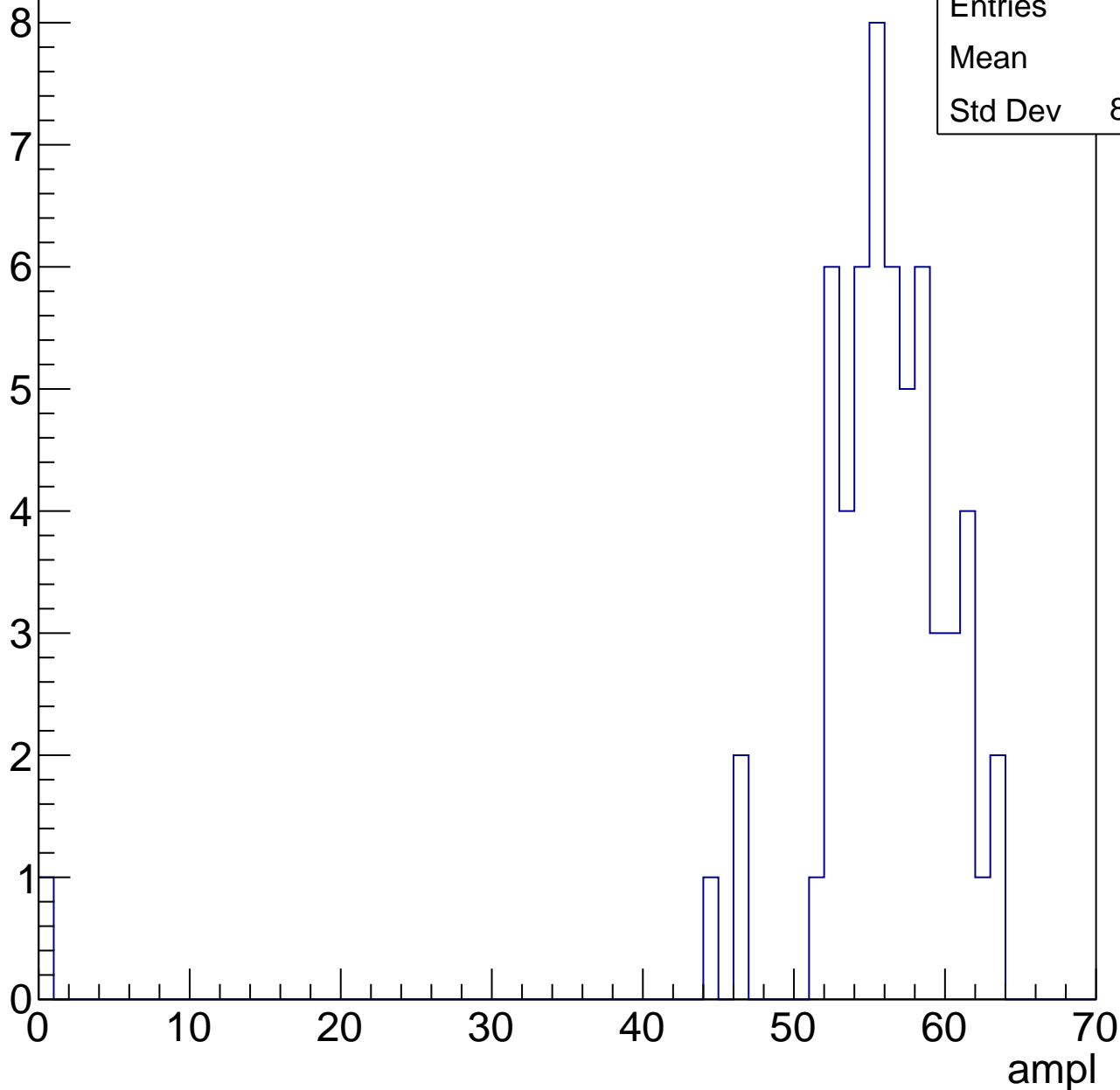


B1L102S, U9-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.8
Std Dev	8.153

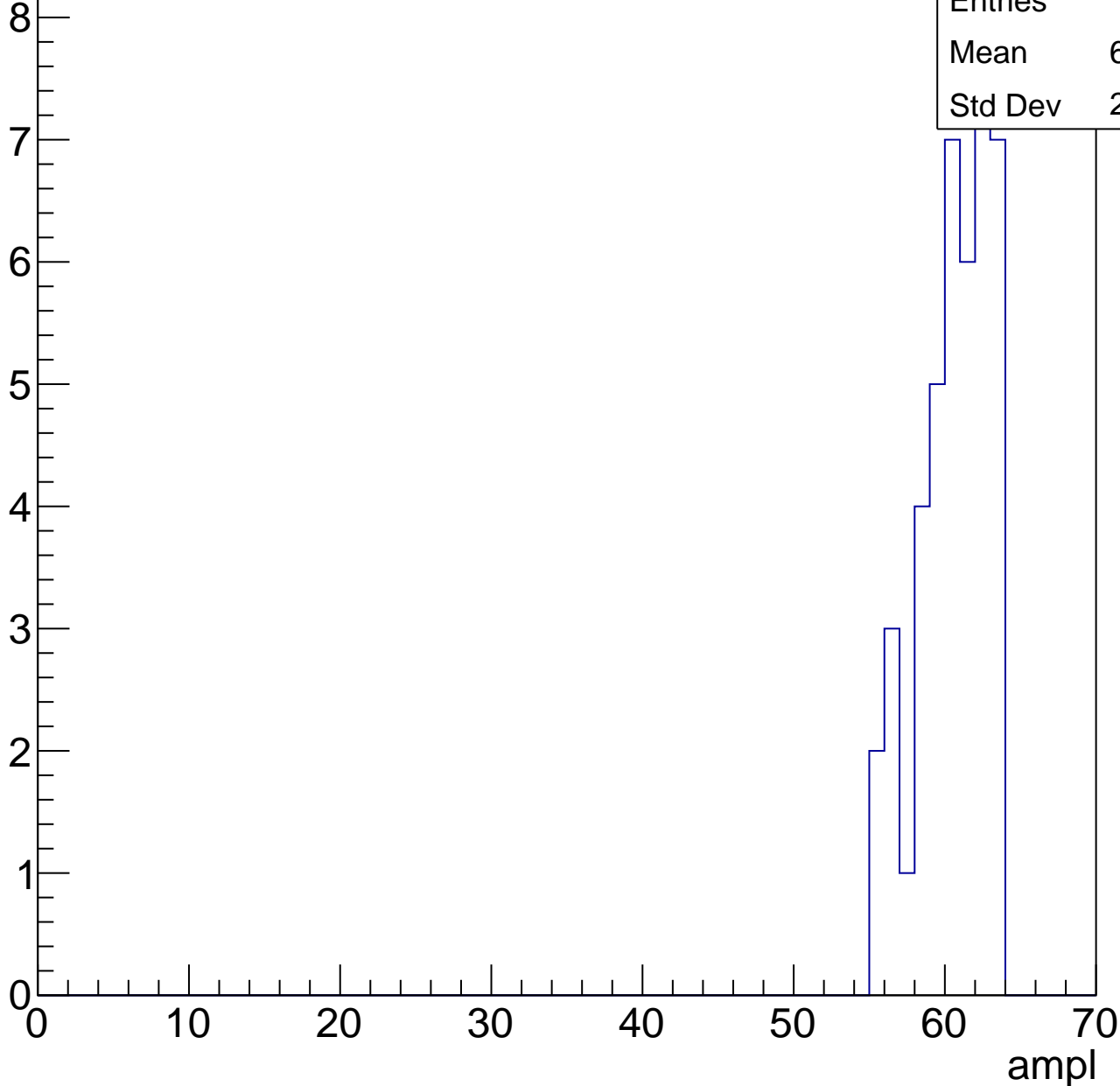


B1L102S, U9-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	60.12
Std Dev	2.305



B1L102S, U9-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U9-ch123, adc0

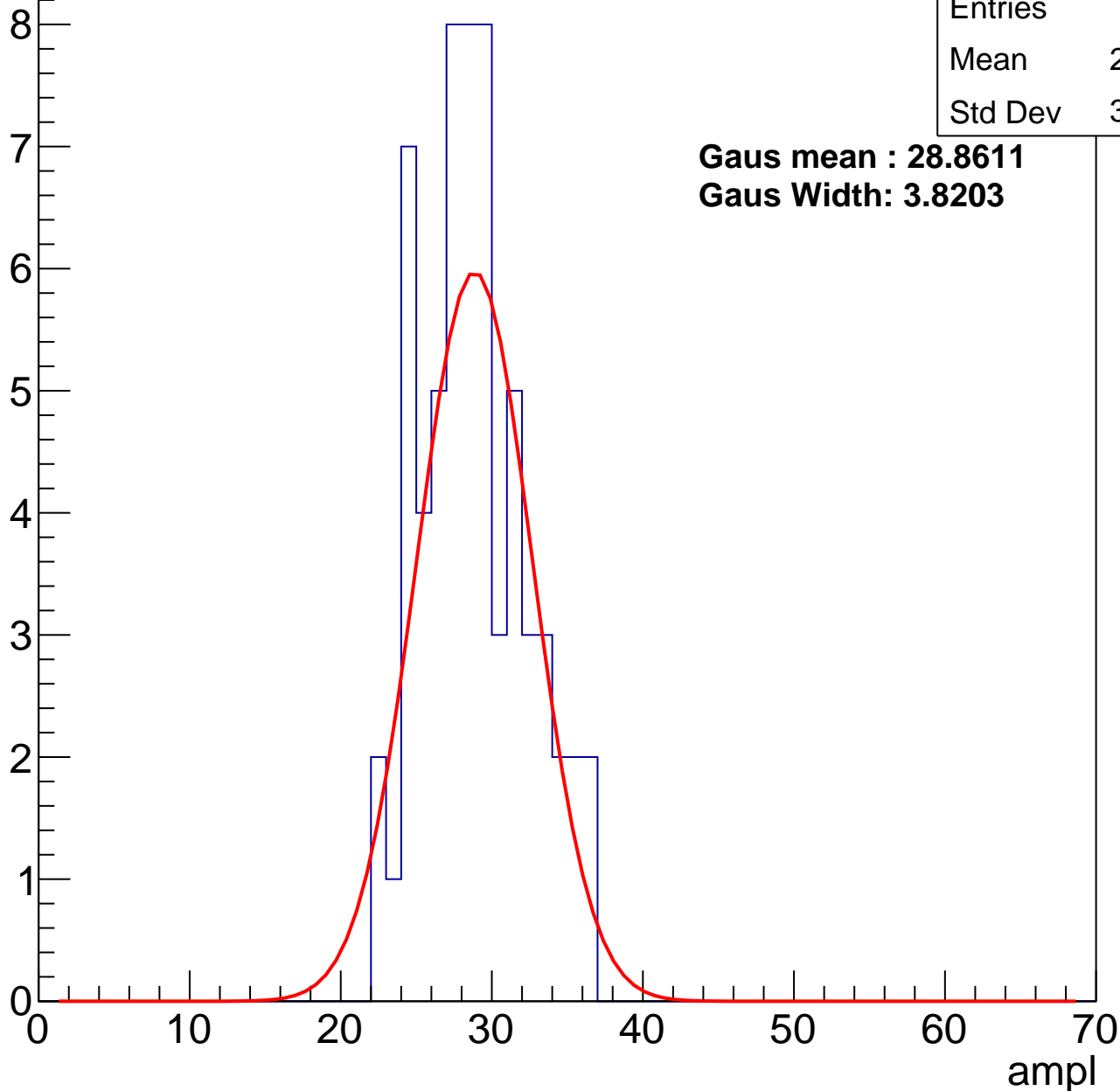
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.37
Std Dev	3.452

Gaus mean : 28.8611

Gaus Width: 3.8203



B1L102S, U9-ch123, adc1

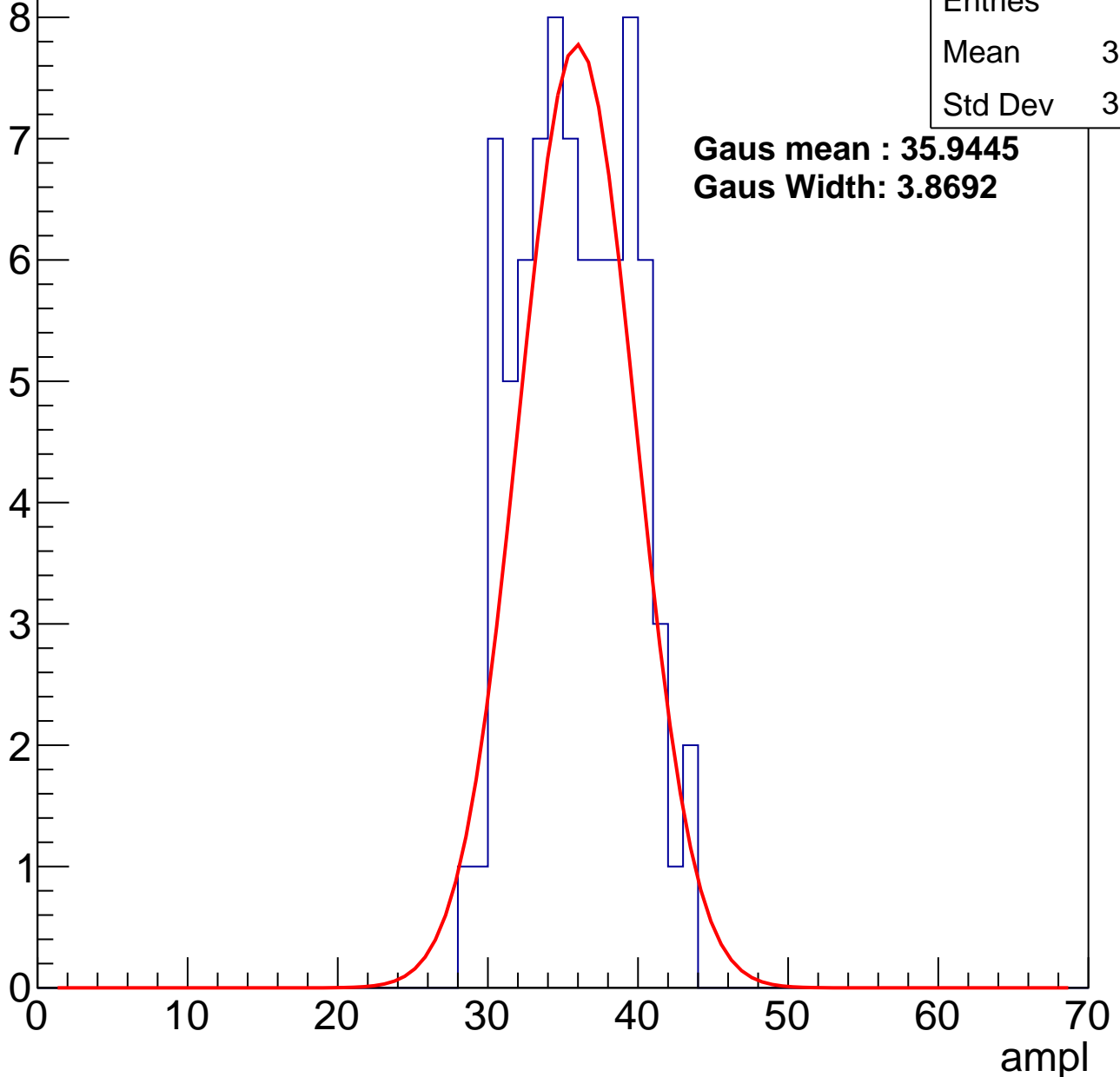
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.39
Std Dev	3.649

Gaus mean : 35.9445

Gaus Width: 3.8692



B1L102S, U9-ch123, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	42.93
Std Dev	3.541

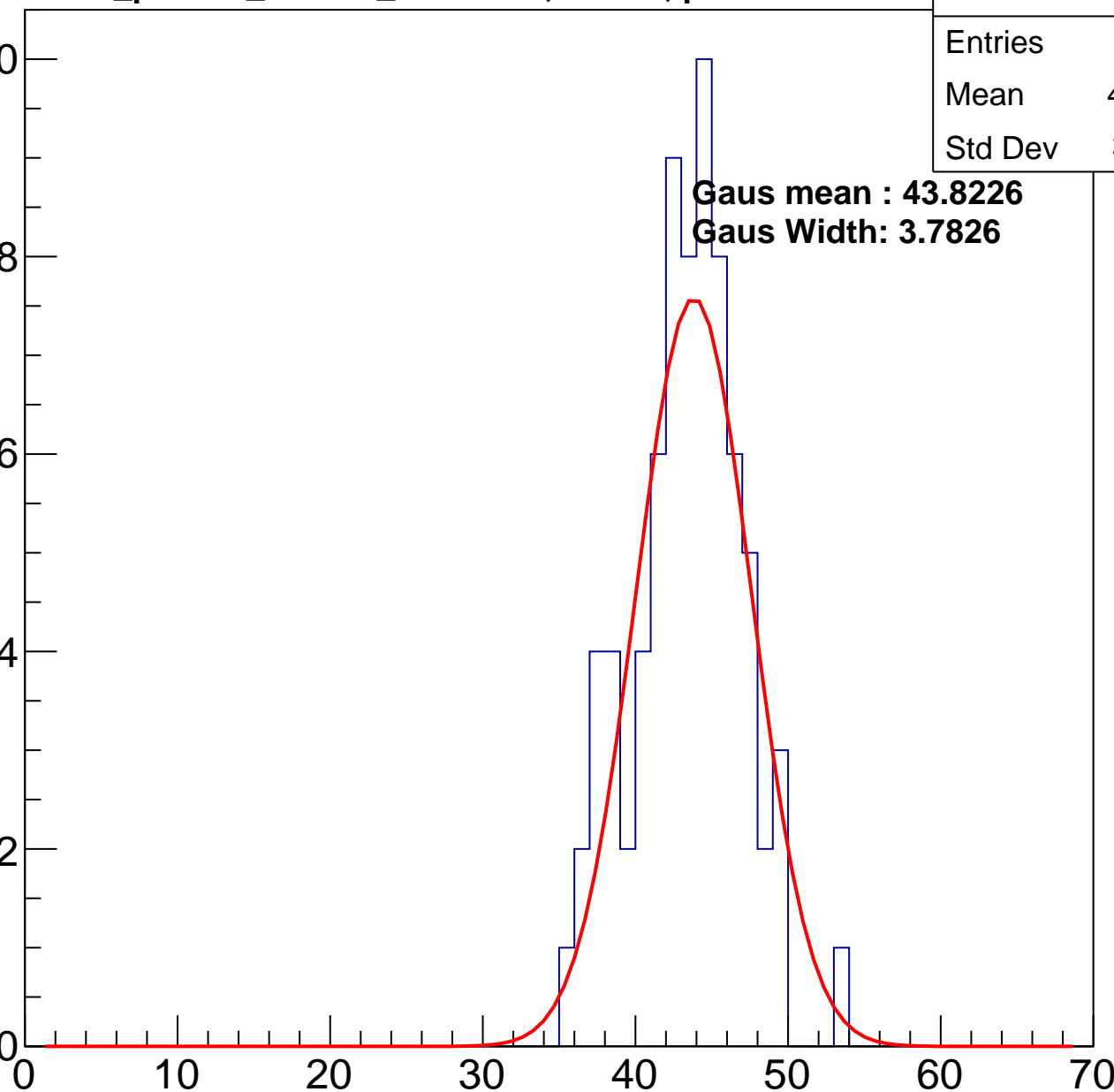
Gaus mean : 43.8226

Gaus Width: 3.7826

Entry

10
8
6
4
2
0

ampl

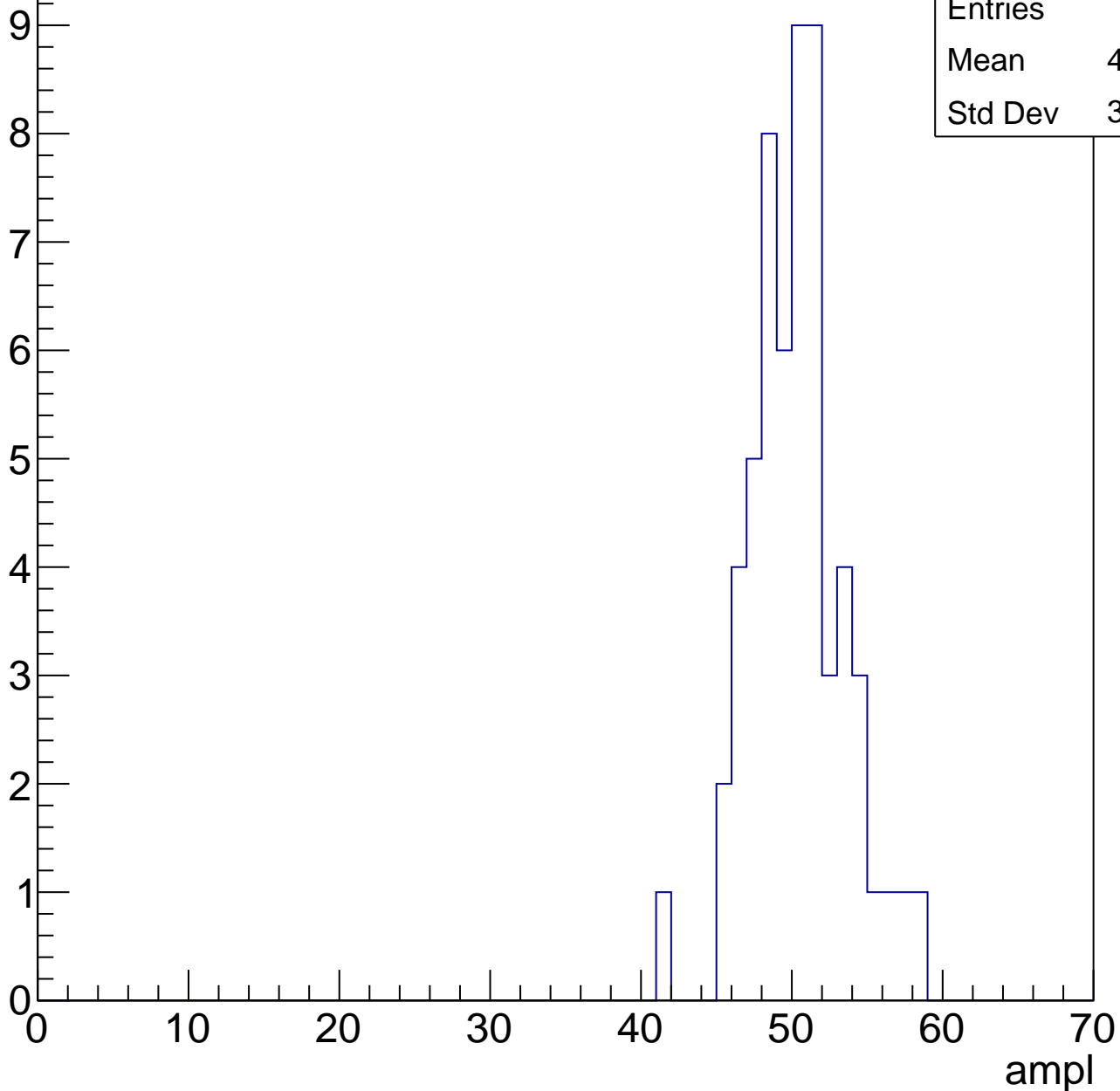


B1L102S, U9-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	49.88
Std Dev	3.097

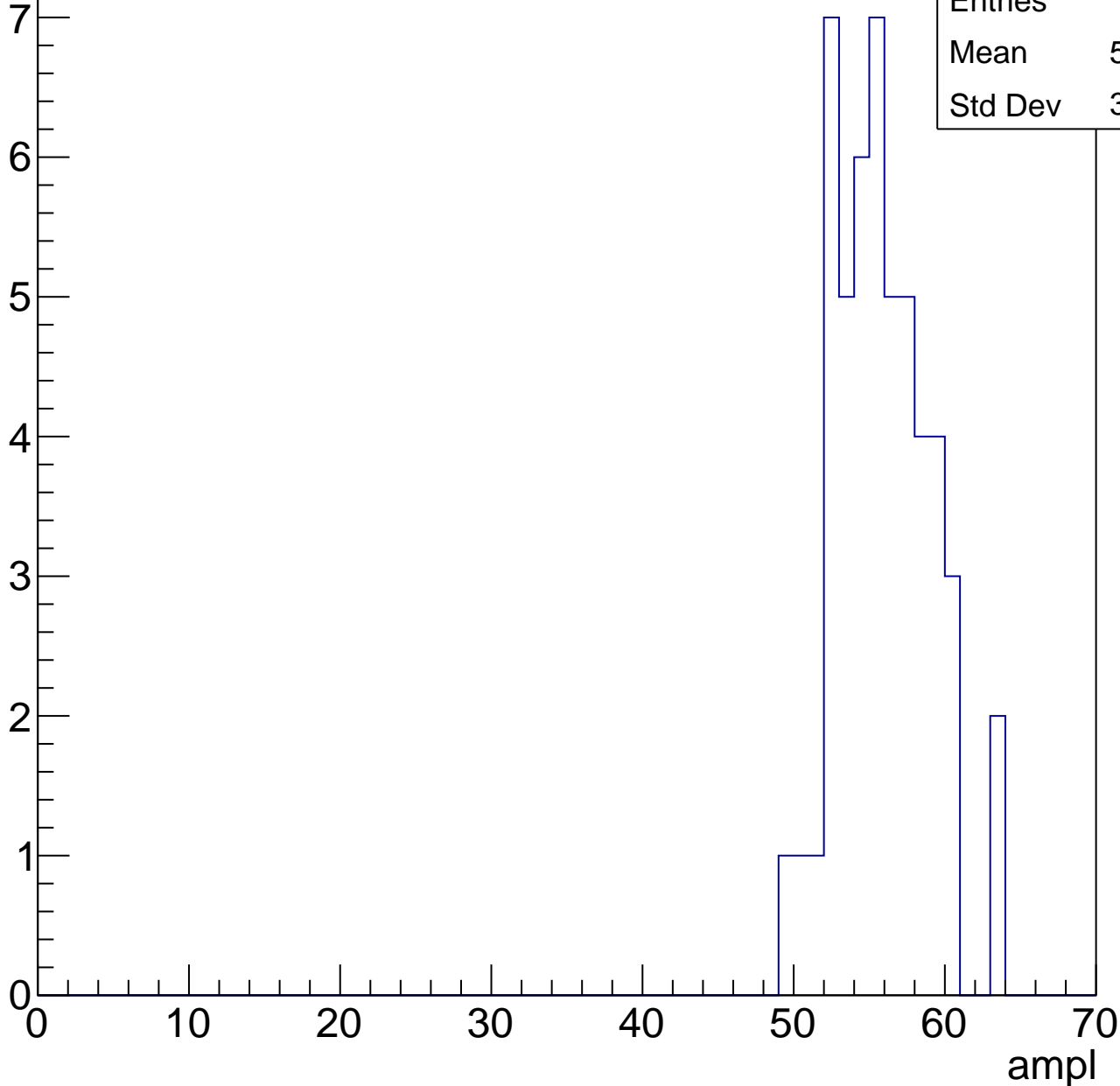


B1L102S, U9-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.43
Std Dev	3.082

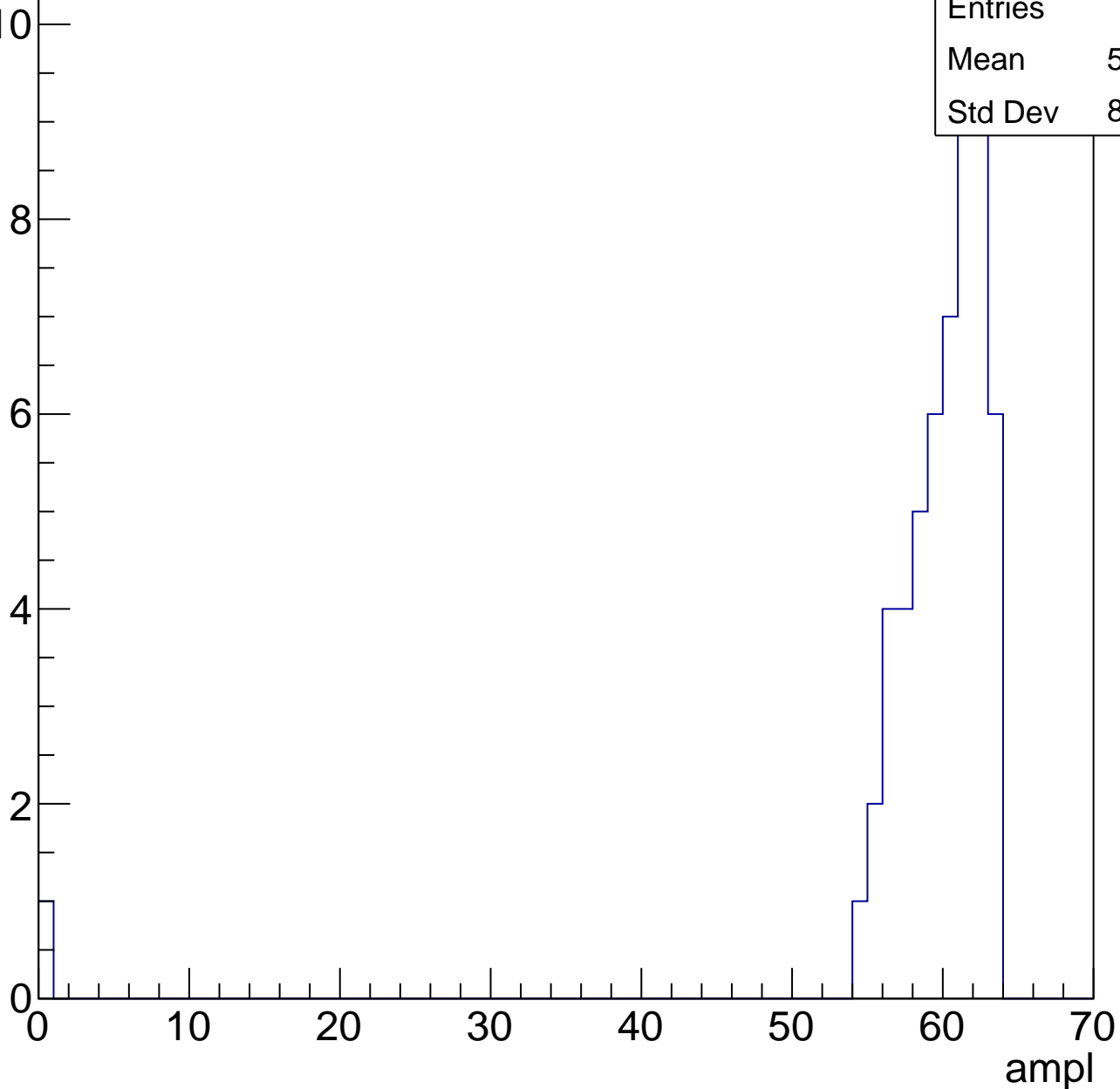


B1L102S, U9-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.67
Std Dev	8.332



B1L102S, U9-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

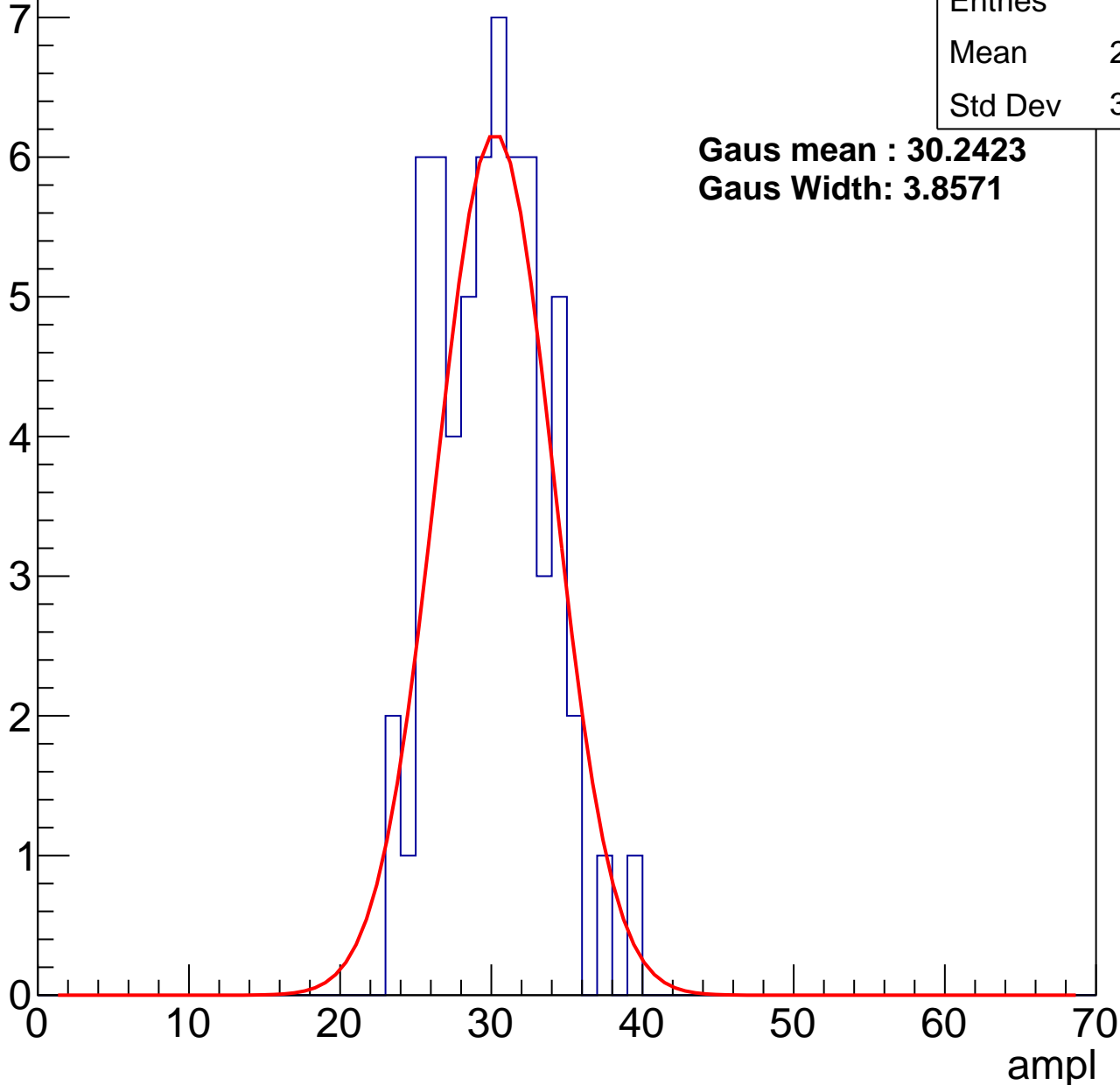
B1L102S, U9-ch124, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	29.52
Std Dev	3.495

Gaus mean : 30.2423
Gaus Width: 3.8571



B1L102S, U9-ch124, adc1

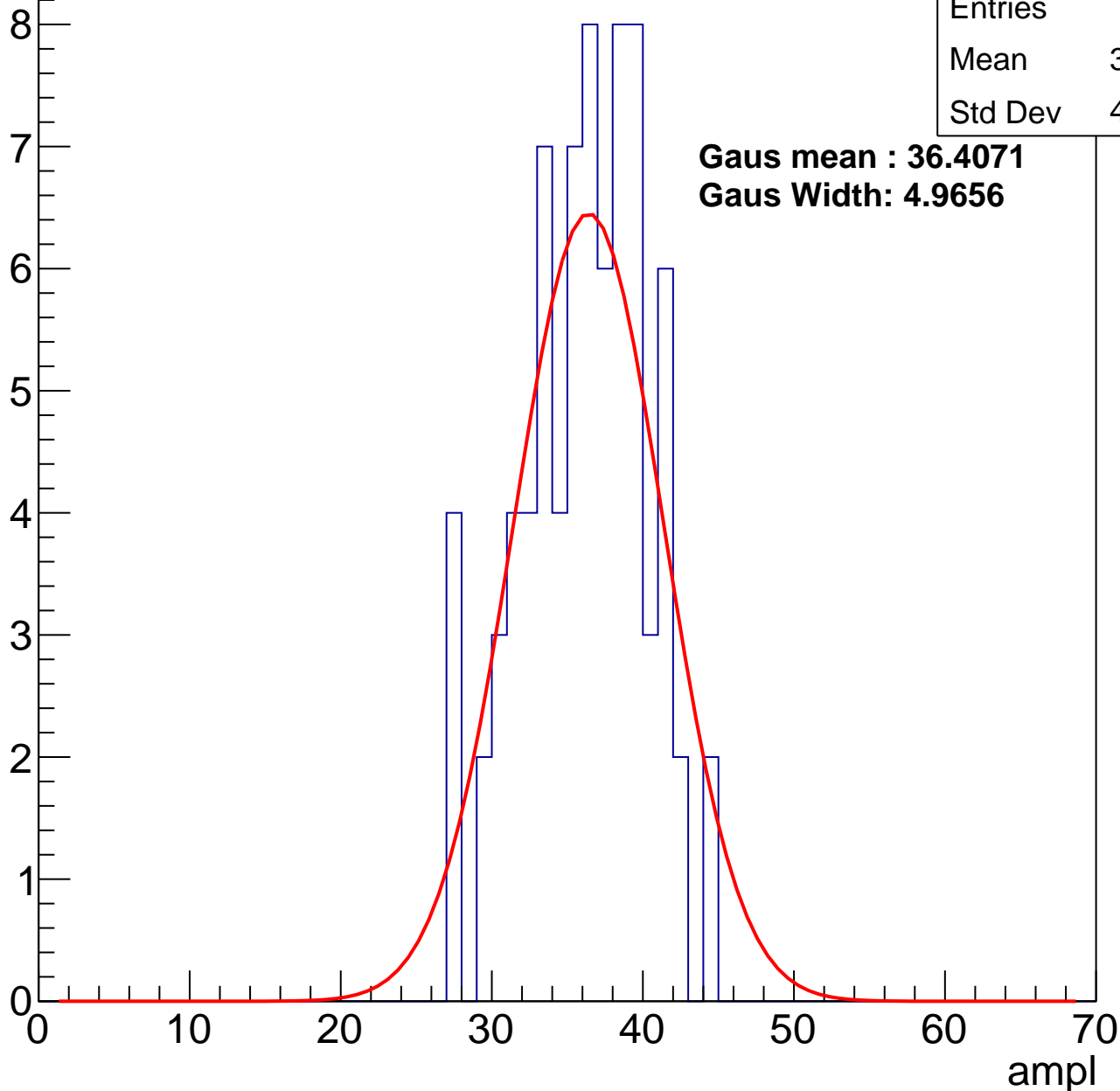
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.69
Std Dev	4.052

Gaus mean : 36.4071

Gaus Width: 4.9656



B1L102S, U9-ch124, adc2

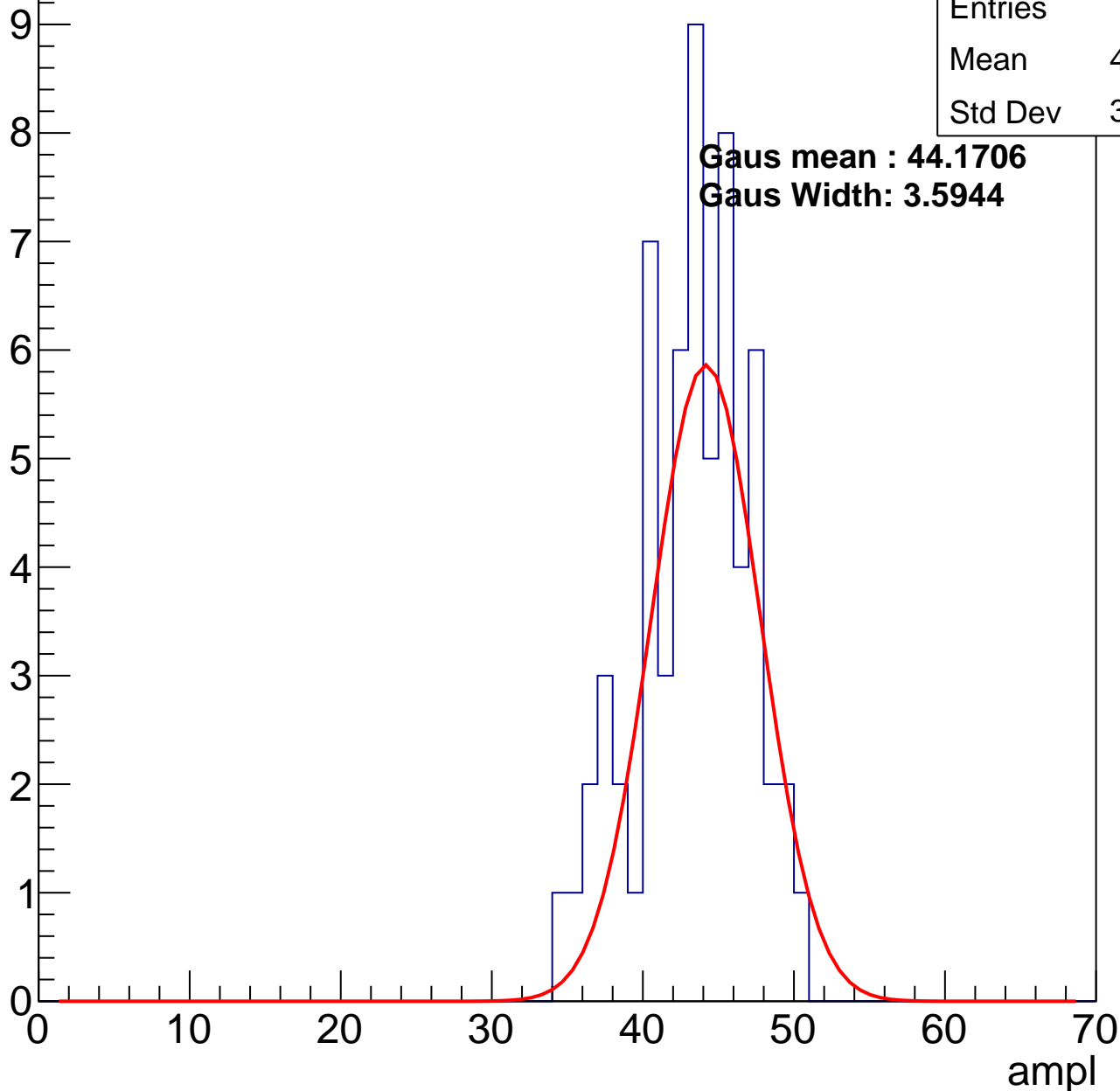
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.84
Std Dev	3.644

Gaus mean : 44.1706

Gaus Width: 3.5944

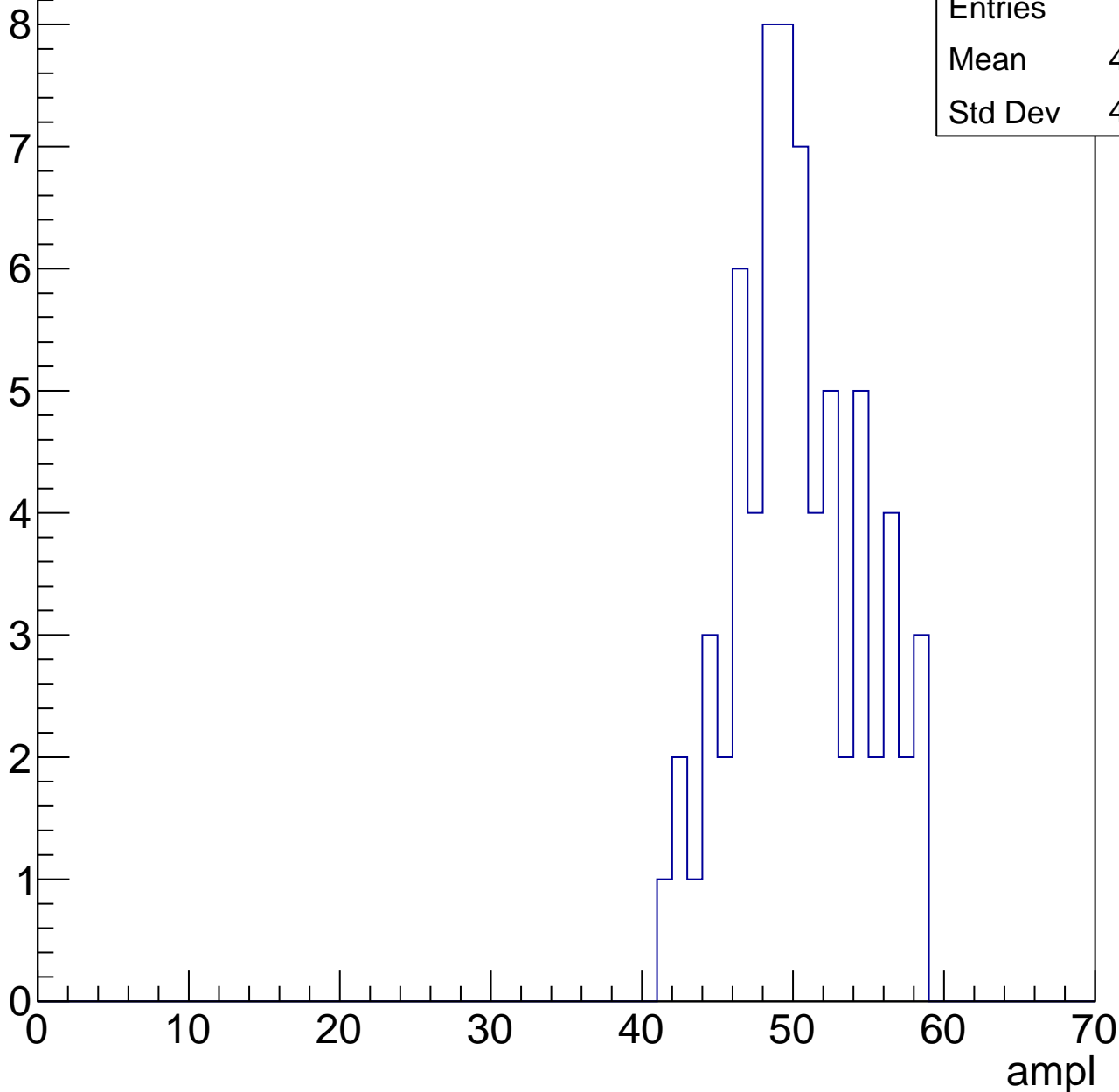


B1L102S, U9-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.88
Std Dev	4.158

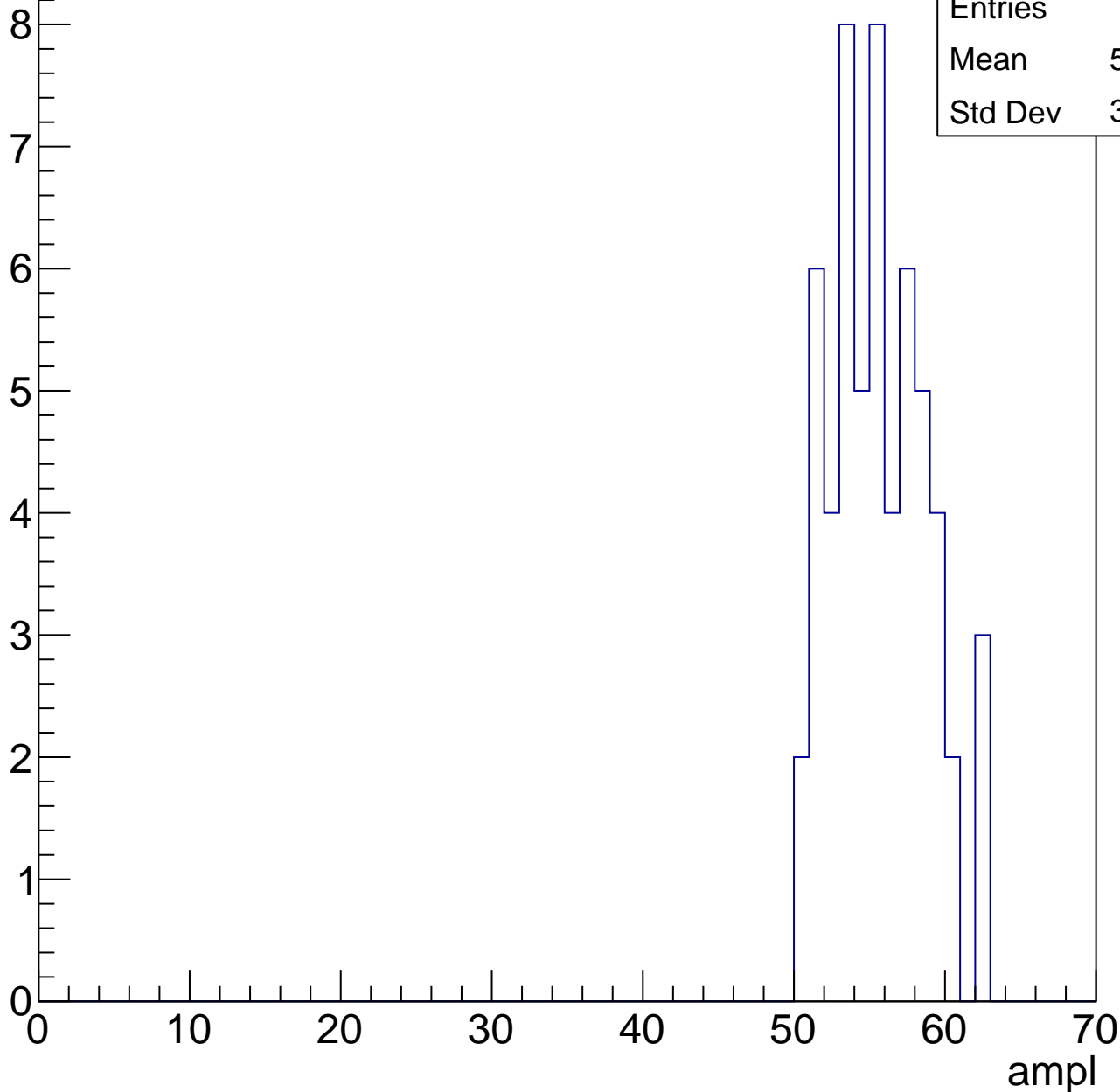


B1L102S, U9-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.19
Std Dev	3.109

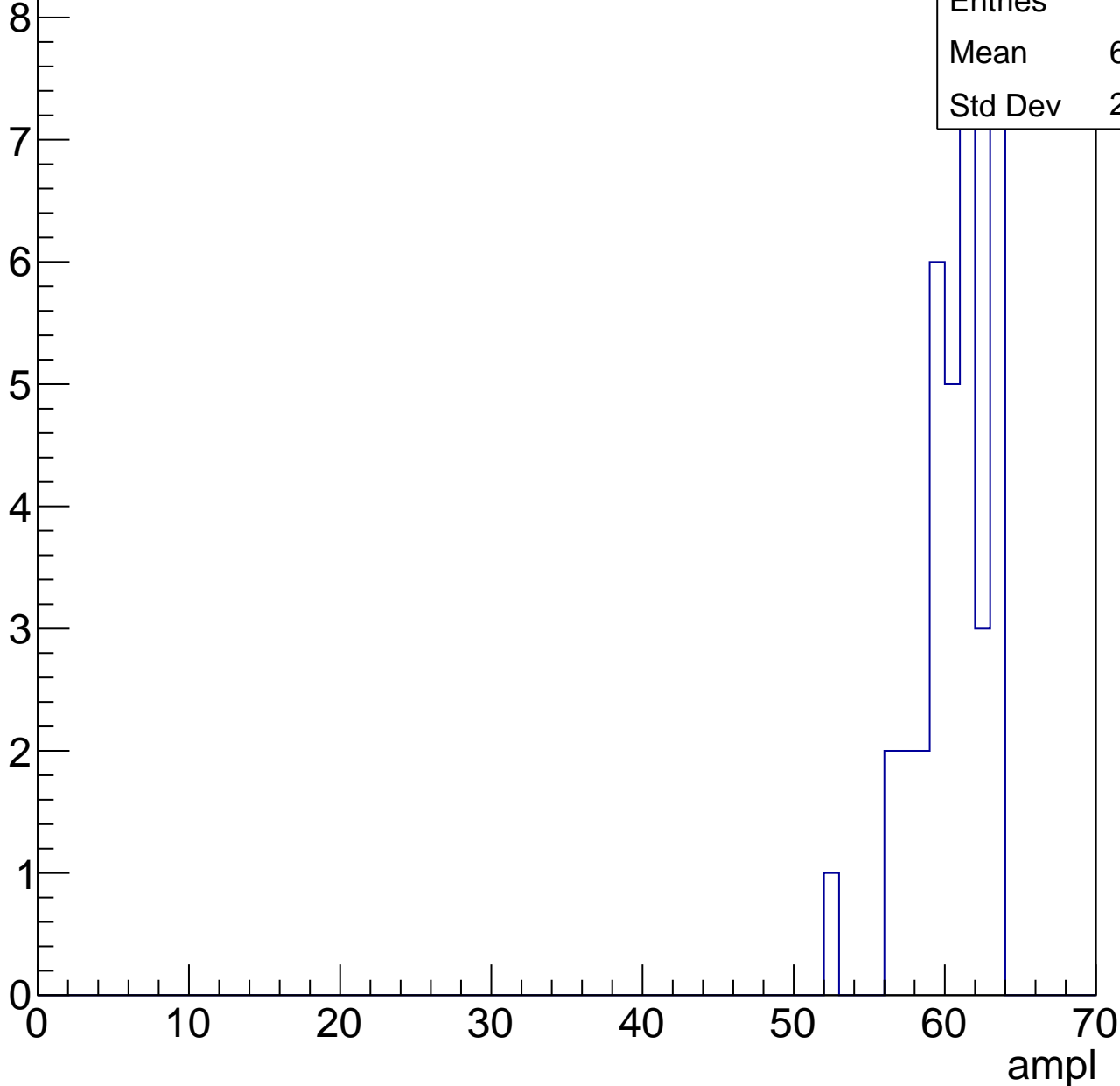


B1L102S, U9-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

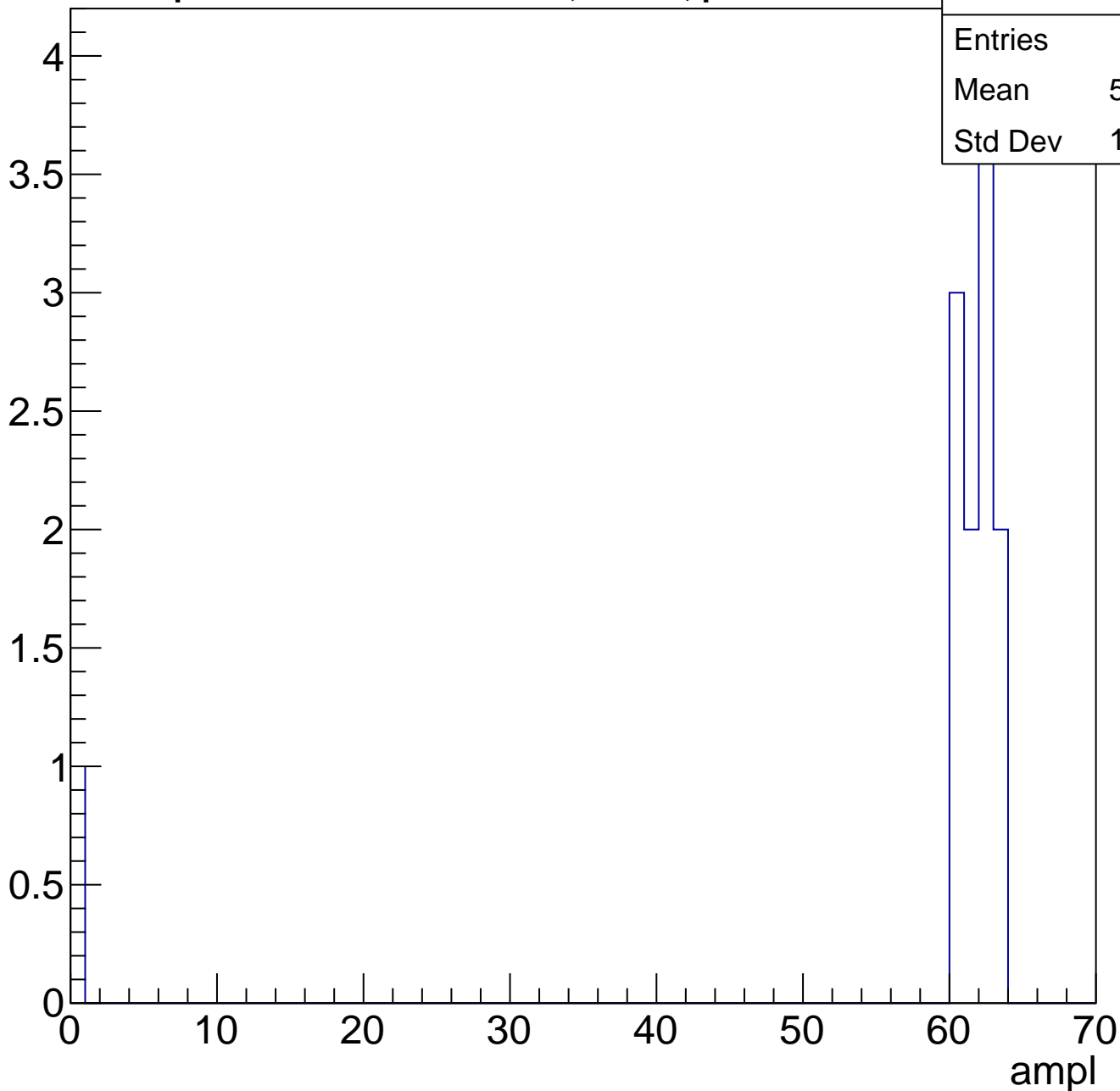
Entries	37
Mean	60.16
Std Dev	2.433



B1L102S, U9-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	12
Mean	56.33
Std Dev	17.02

B1L102S, U9-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L102S, U9-ch125, adc0

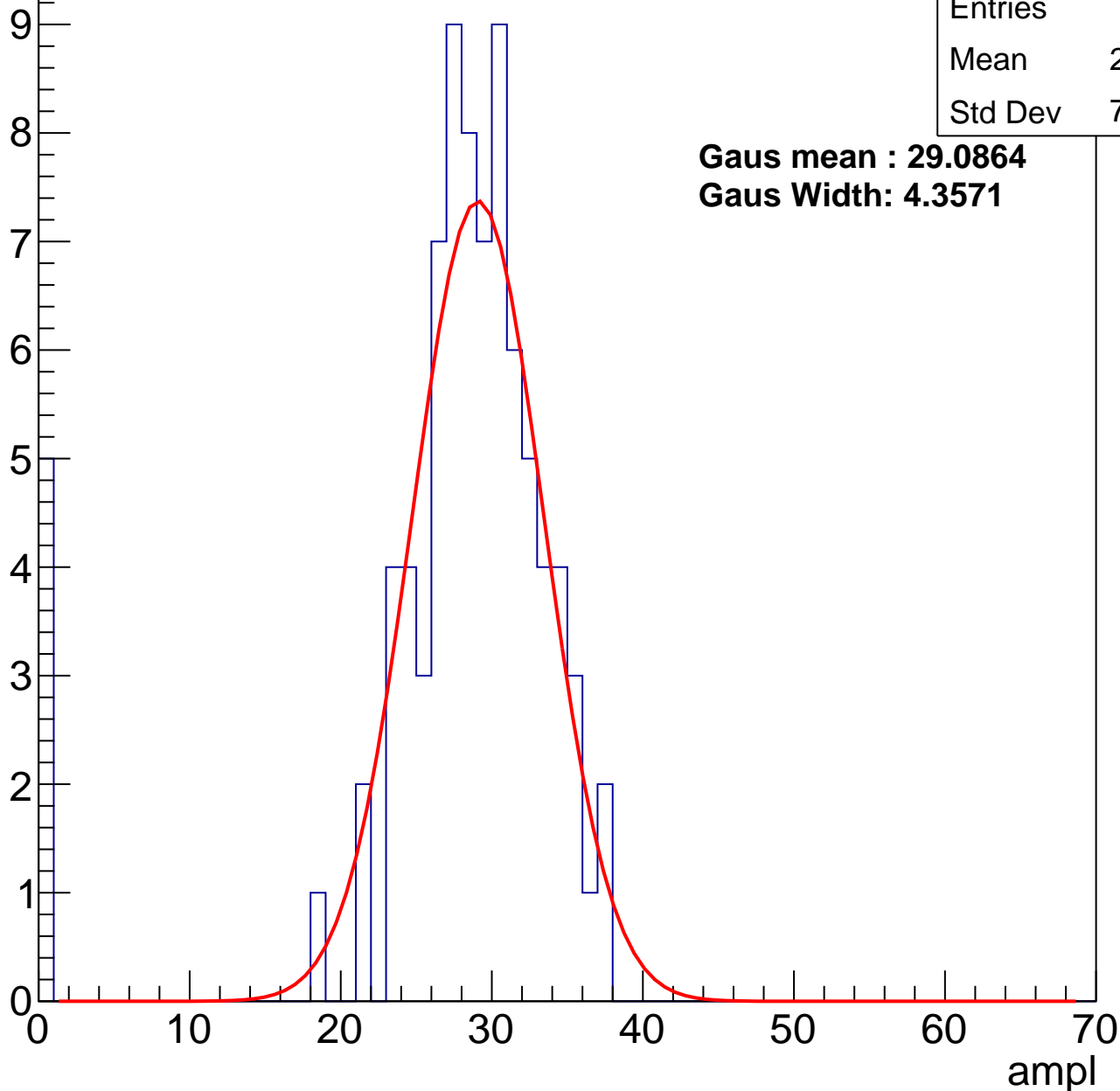
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	27.07
Std Dev	7.767

Gaus mean : 29.0864

Gaus Width: 4.3571



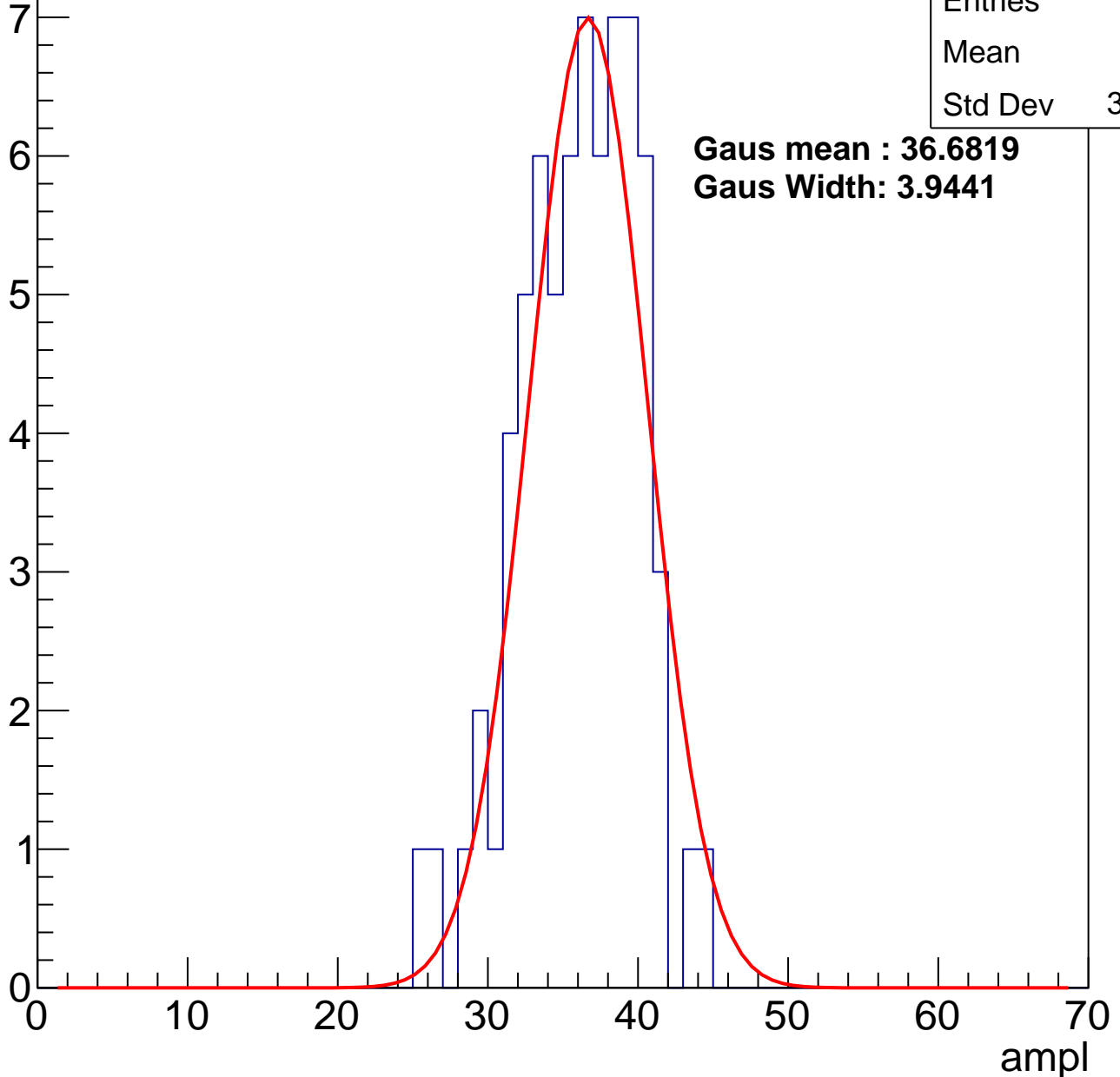
B1L102S, U9-ch125, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.6
Std Dev	3.871

Gaus mean : 36.6819
Gaus Width: 3.9441



B1L102S, U9-ch125, adc2

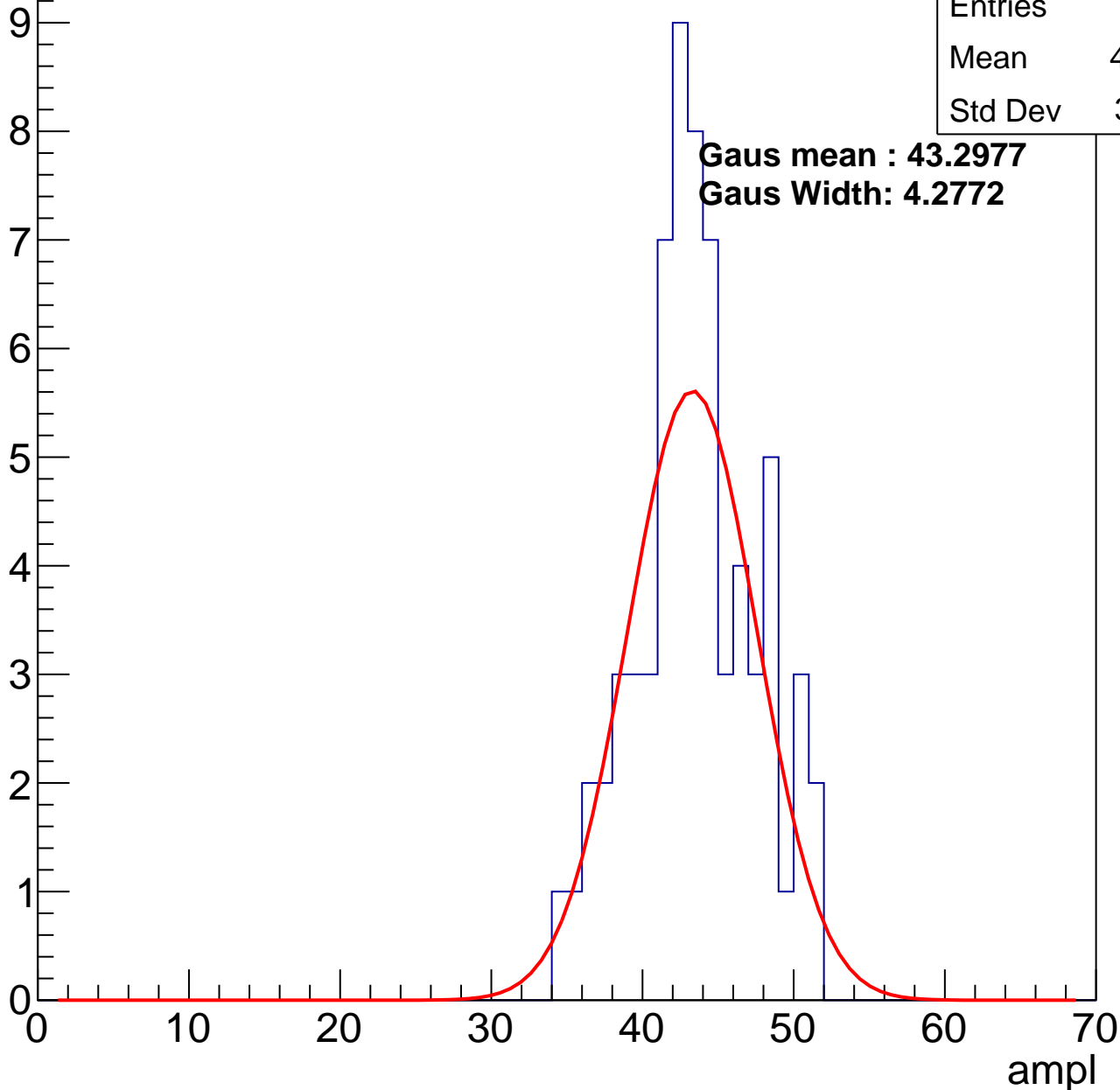
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.04
Std Dev	3.941

Gaus mean : 43.2977

Gaus Width: 4.2772

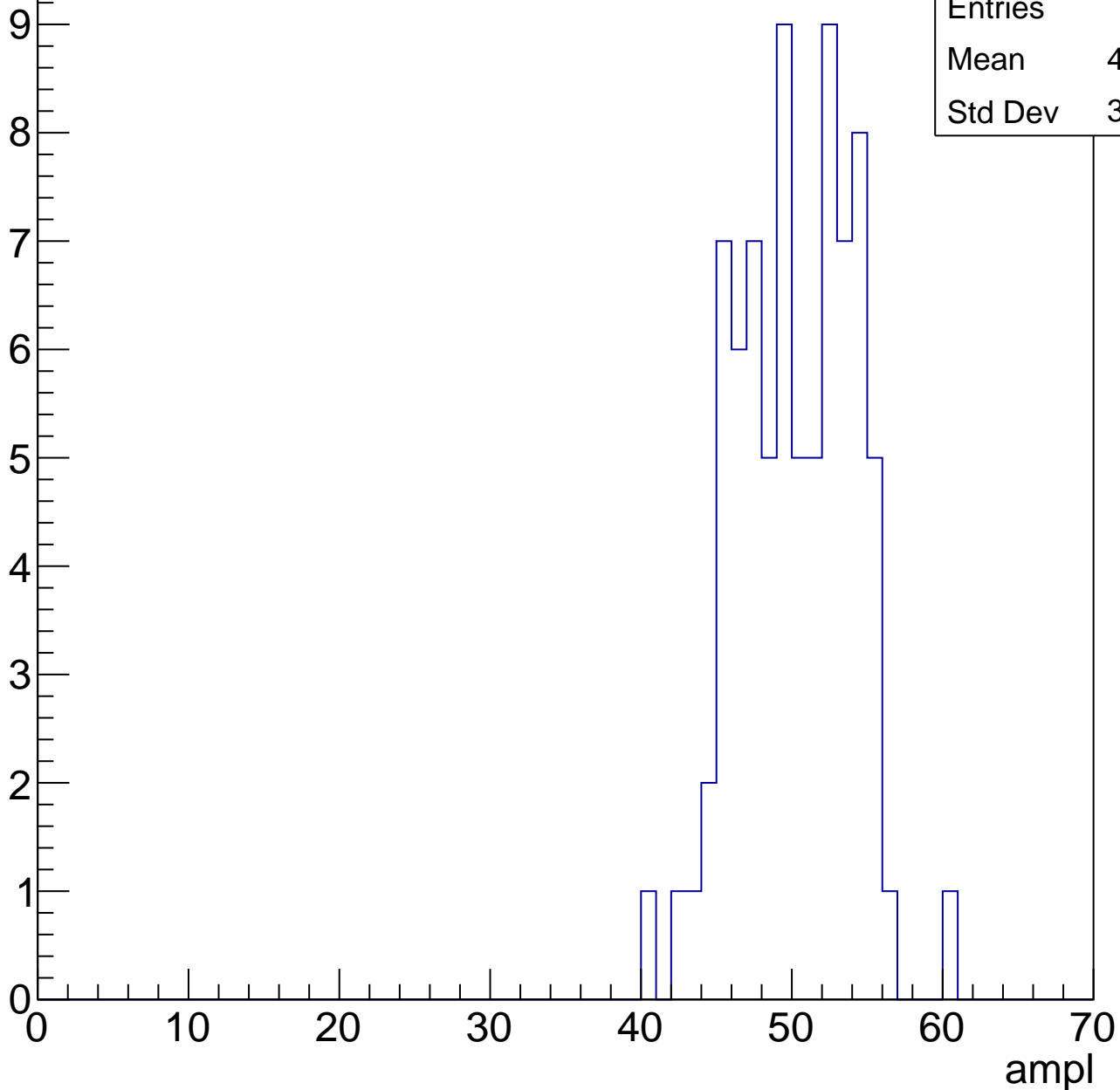


B1L102S, U9-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	49.76
Std Dev	3.769

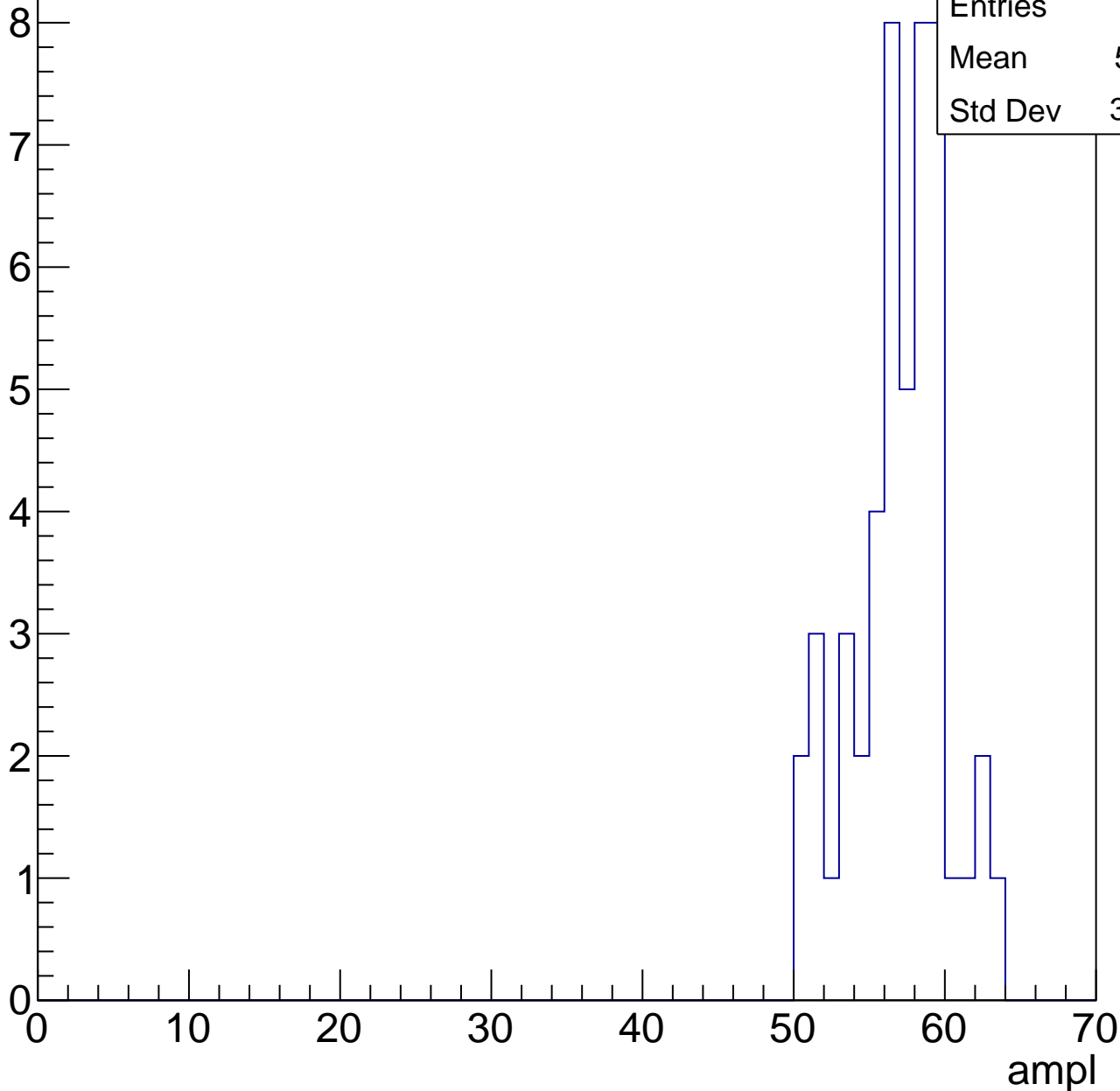


B1L102S, U9-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.51
Std Dev	3.065



B1L102S, U9-ch125, adc5

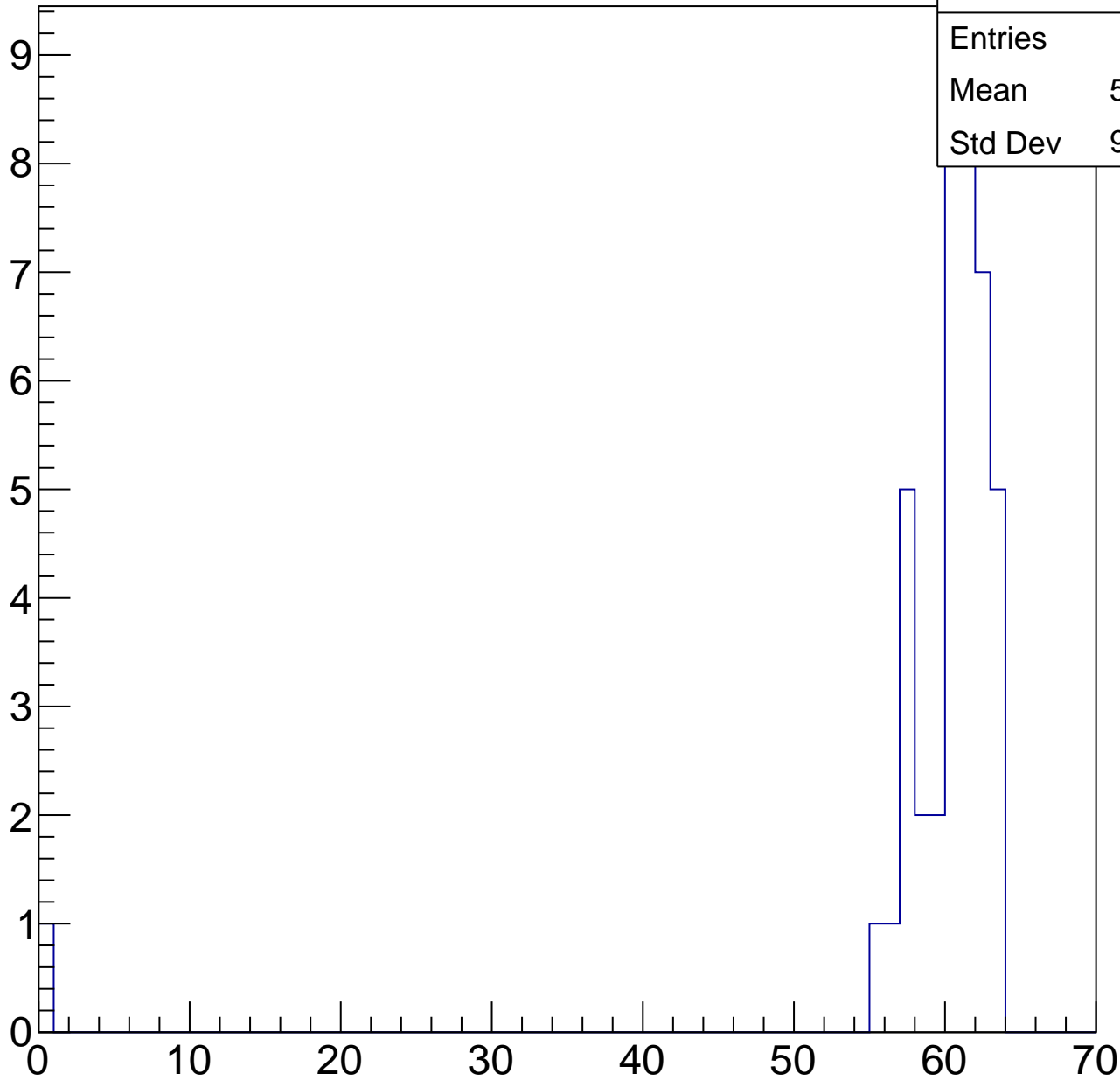
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.73
Std Dev	9.515

ampl



B1L102S, U9-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

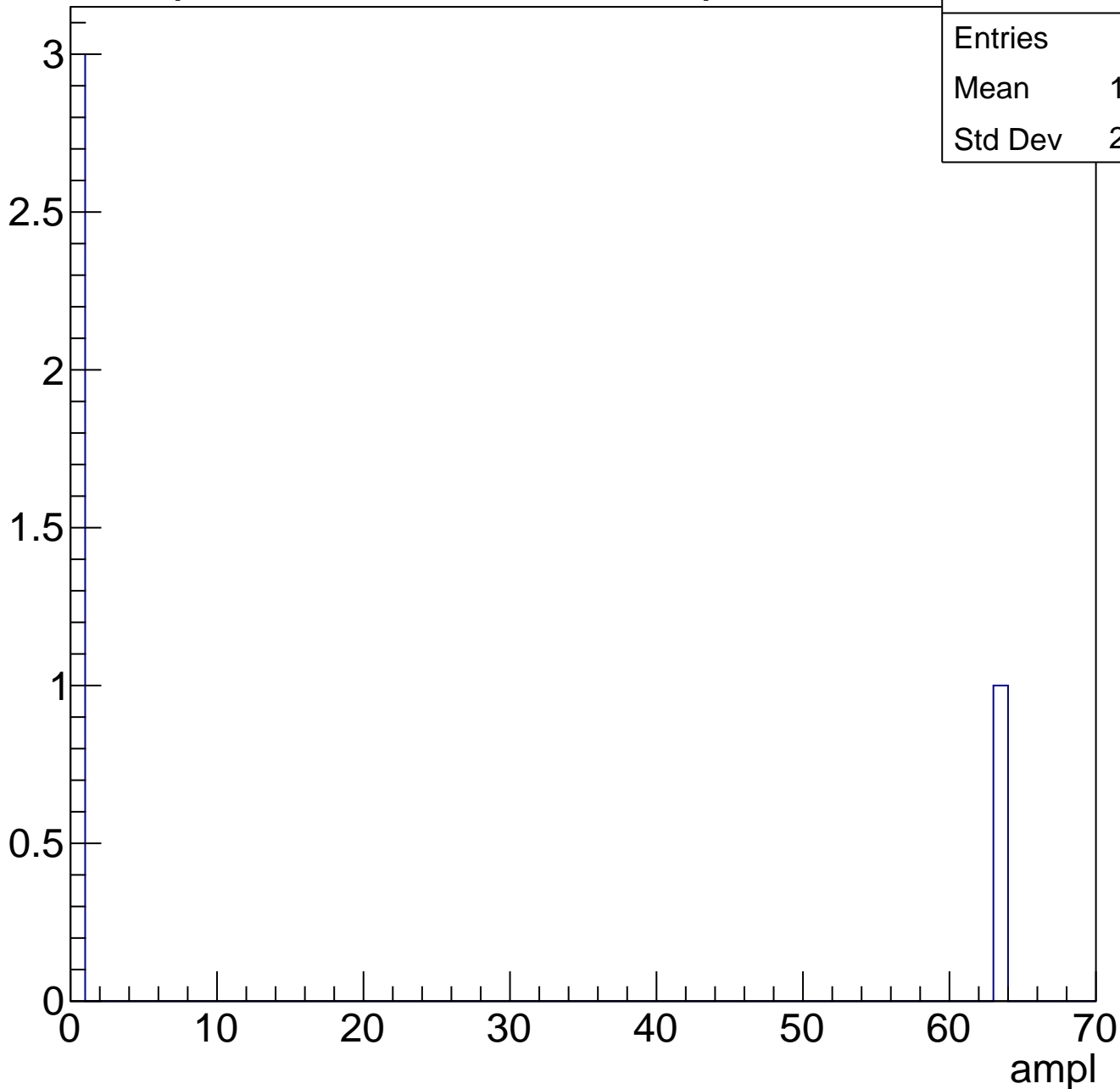
Entry



B1L102S, U9-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	15.75
Std Dev	27.28

B1L102S, U9-ch126, adc0

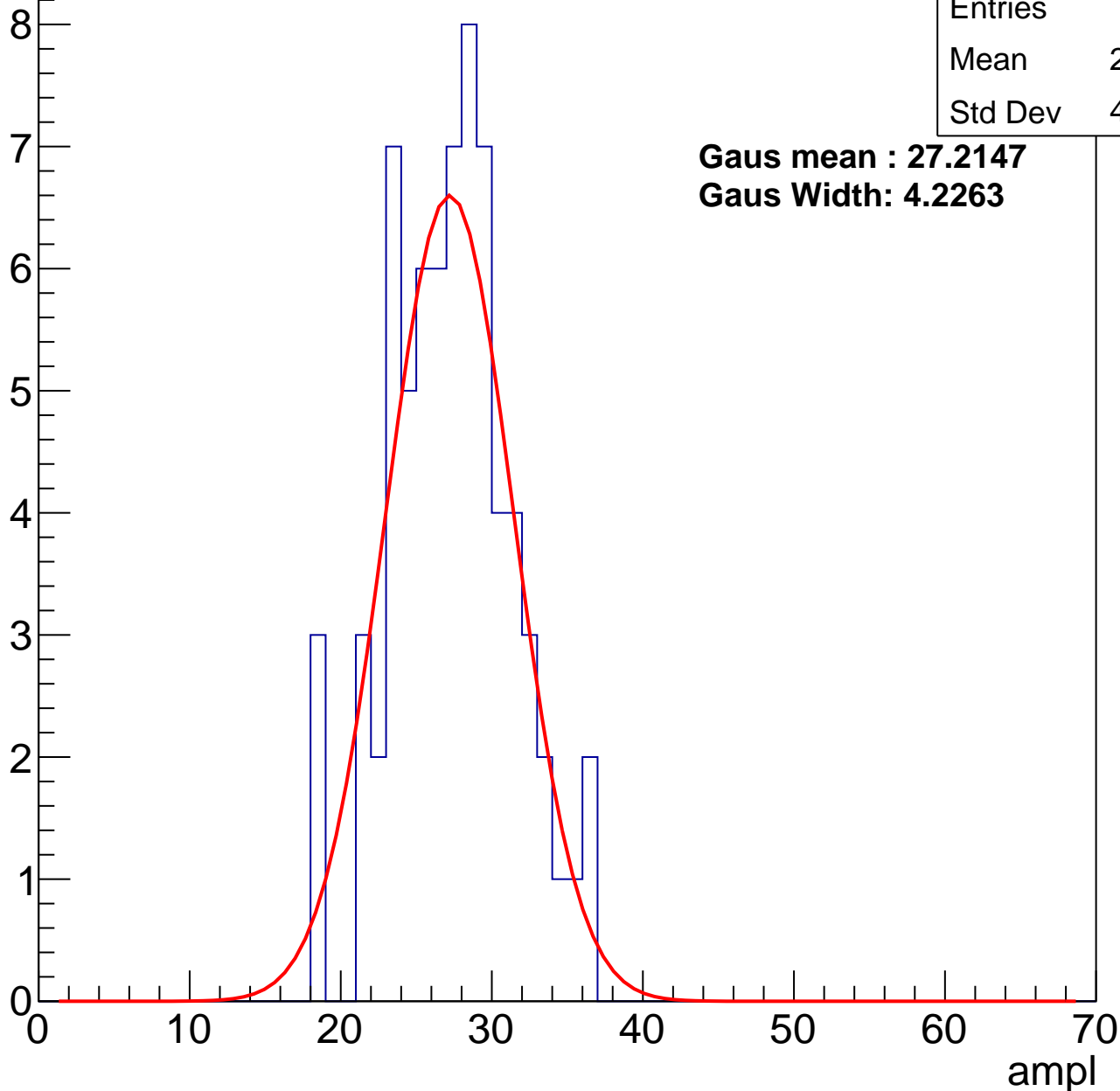
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	26.92
Std Dev	4.017

Gaus mean : 27.2147

Gaus Width: 4.2263



B1L102S, U9-ch126, adc1

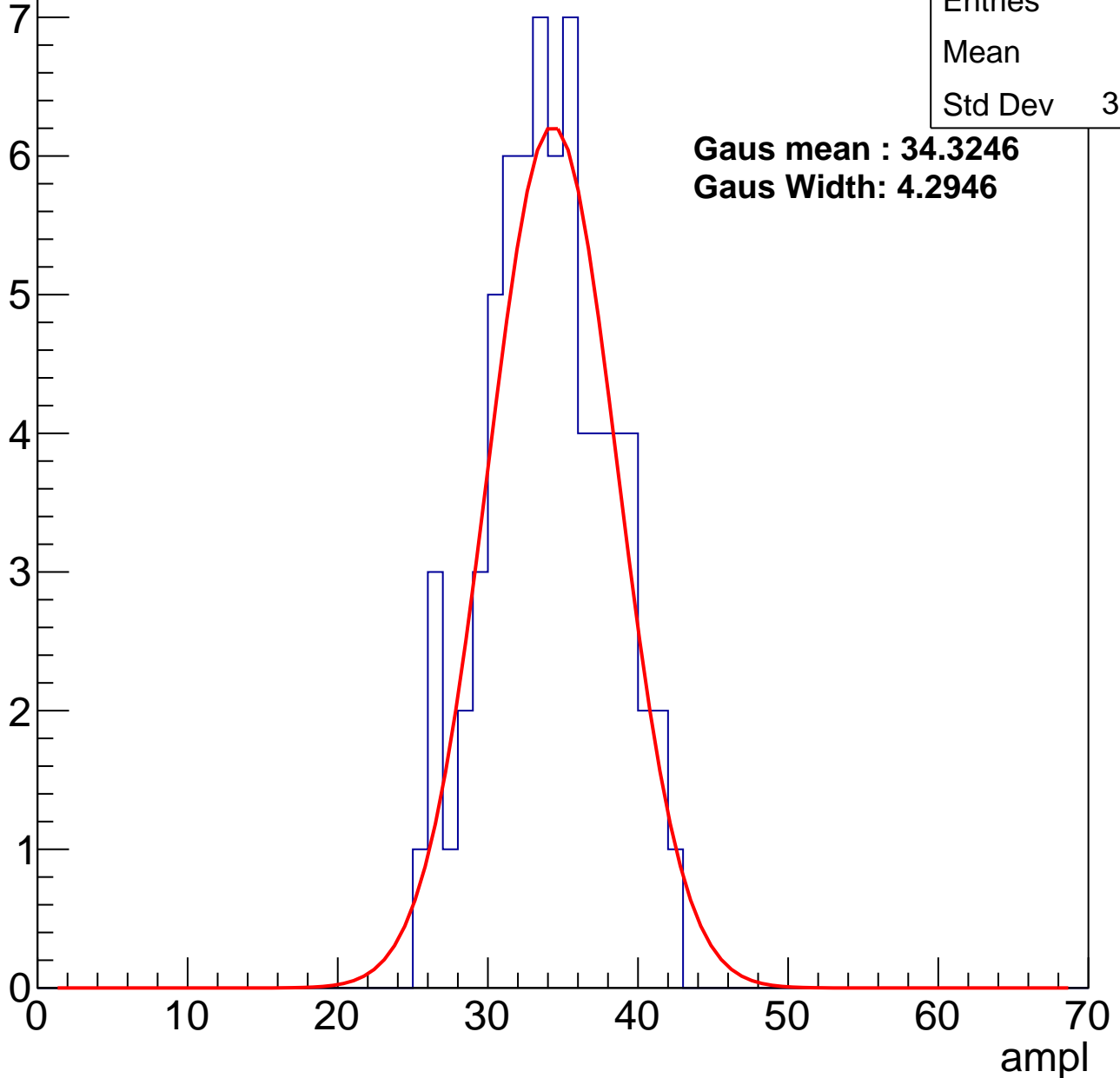
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	33.6
Std Dev	3.986

Gaus mean : 34.3246

Gaus Width: 4.2946



B1L102S, U9-ch126, adc2

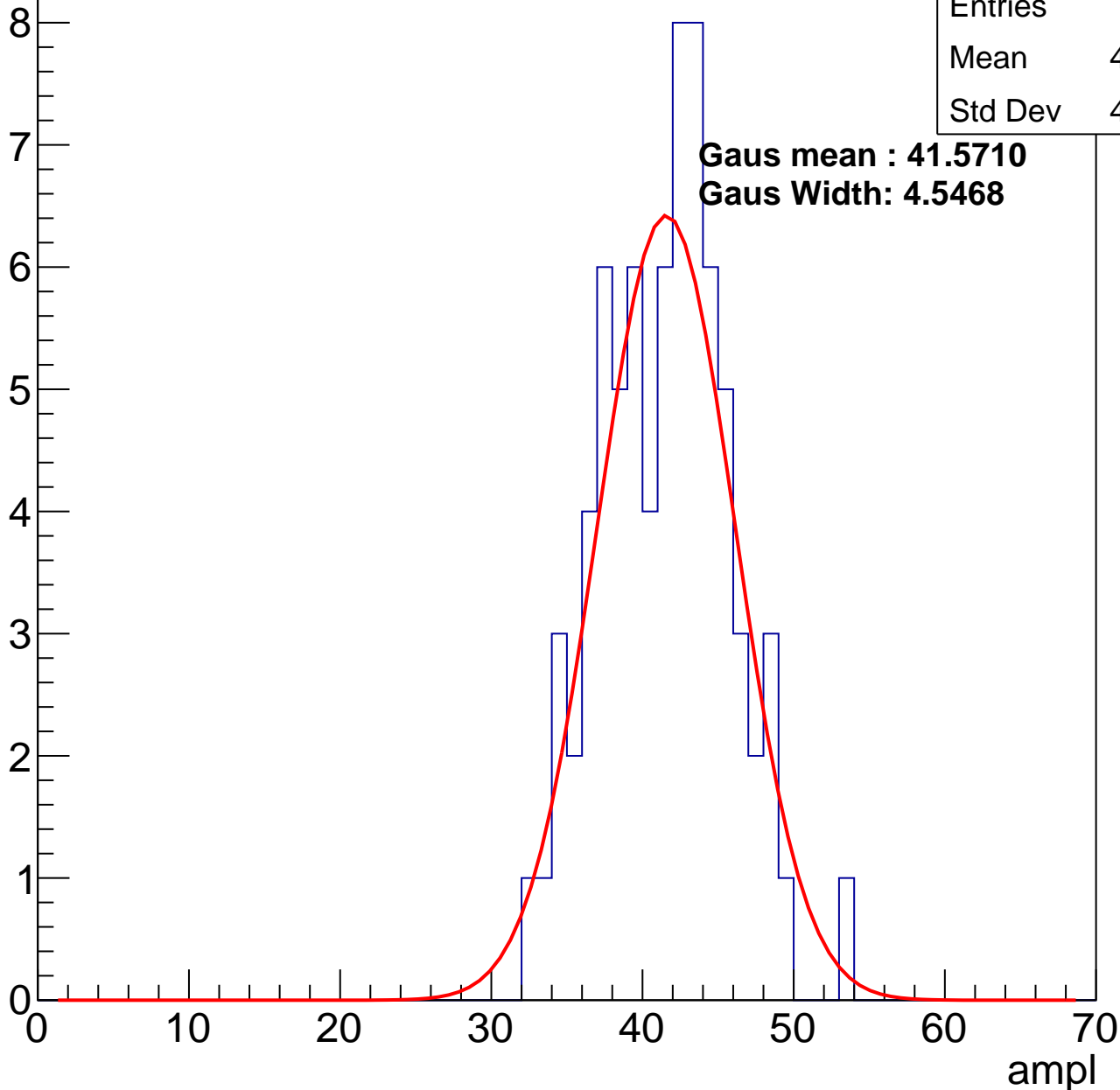
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.07
Std Dev	4.177

Gaus mean : 41.5710

Gaus Width: 4.5468

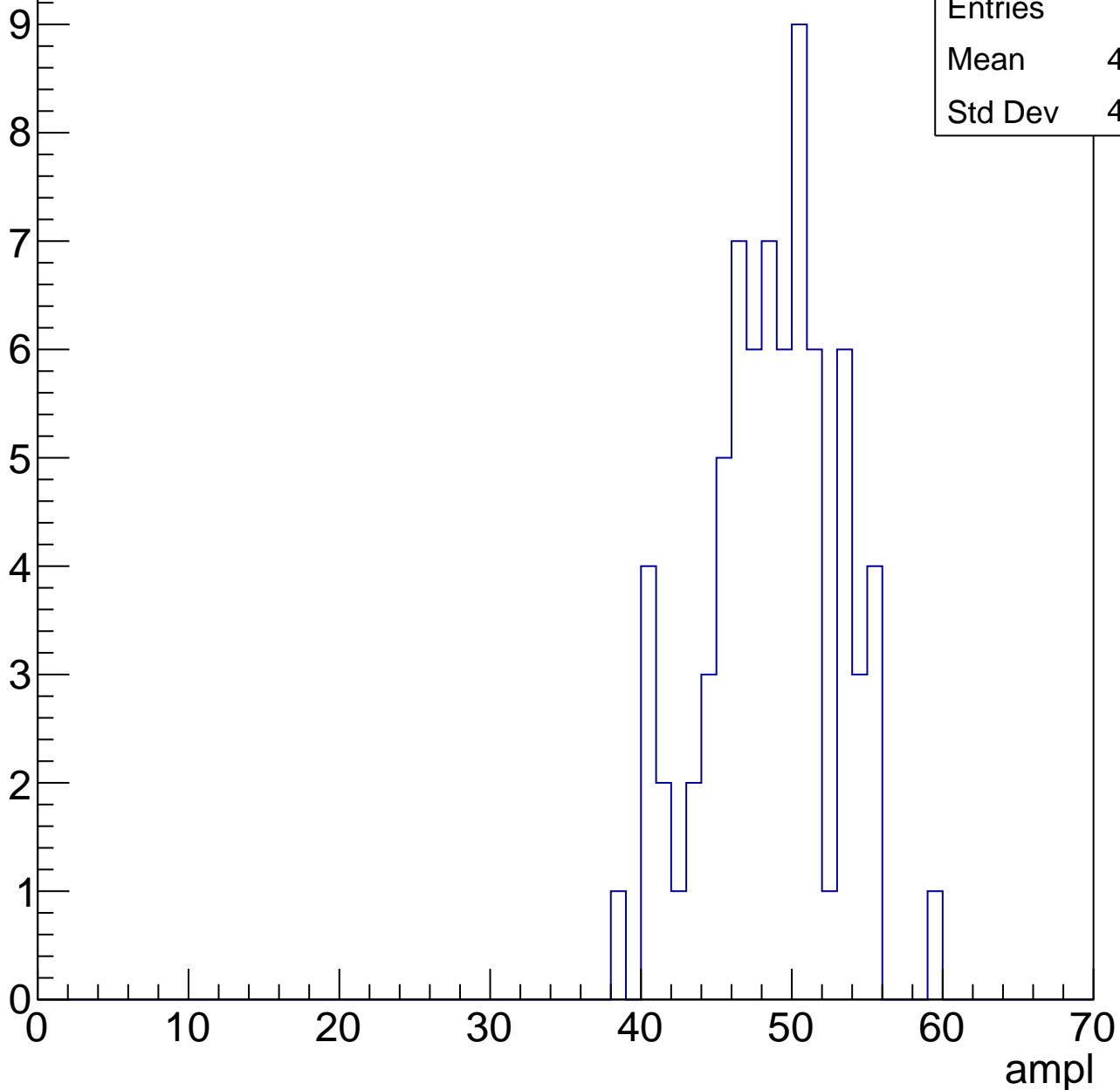


B1L102S, U9-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.19
Std Dev	4.273

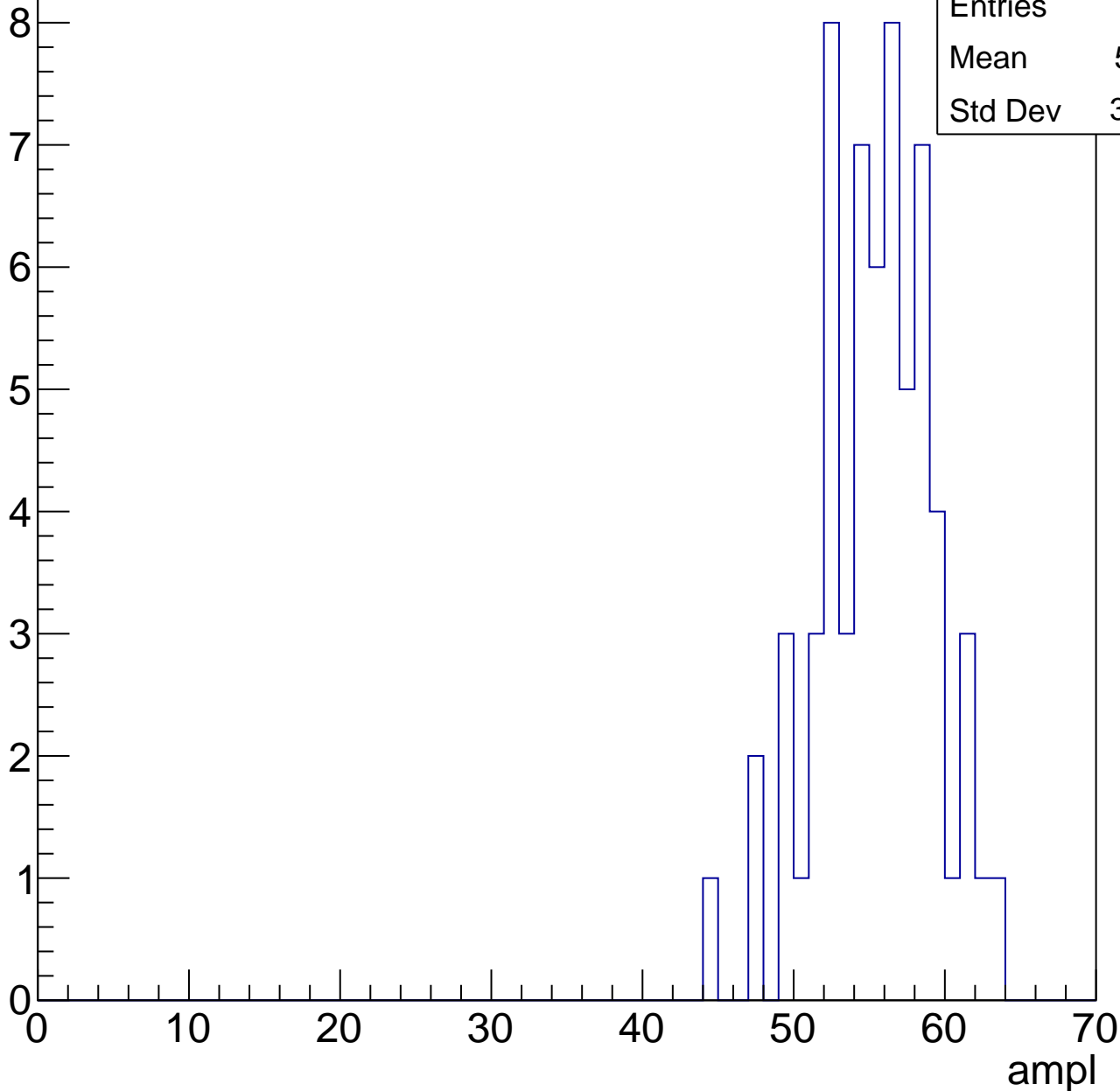


B1L102S, U9-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.91
Std Dev	3.794



B1L102S, U9-ch126, adc5

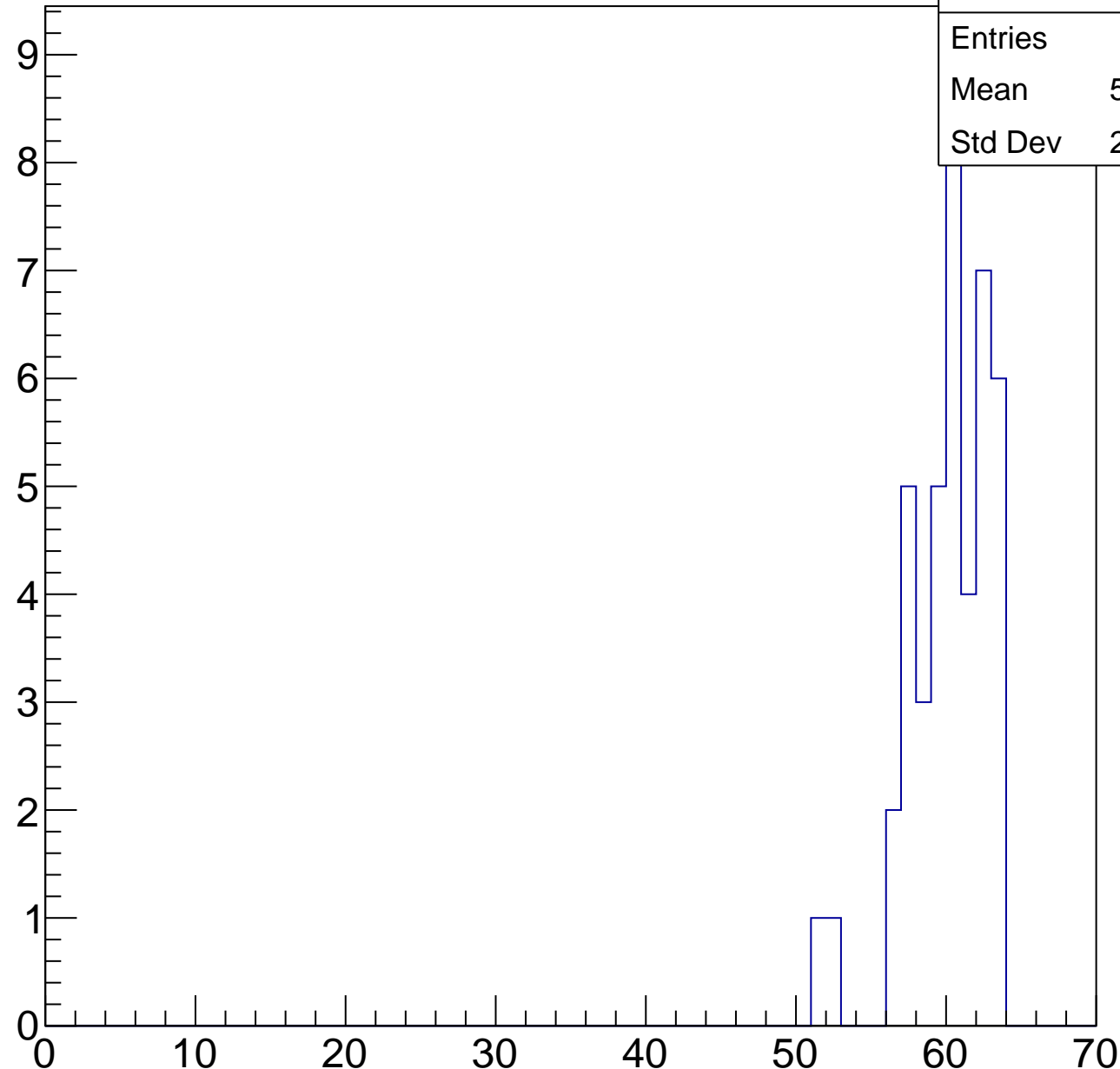
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.65
Std Dev	2.727

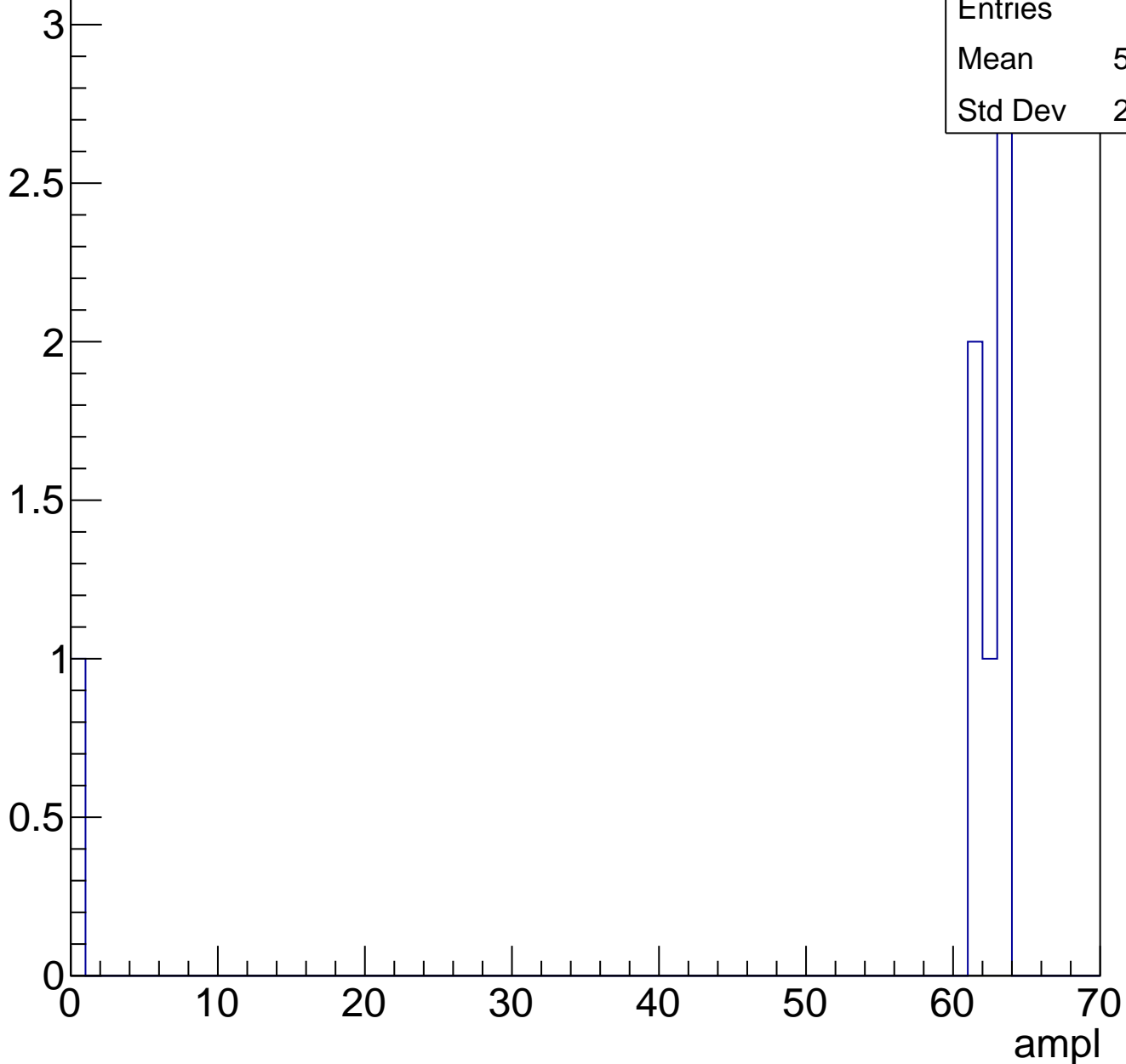
ampl



B1L102S, U9-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

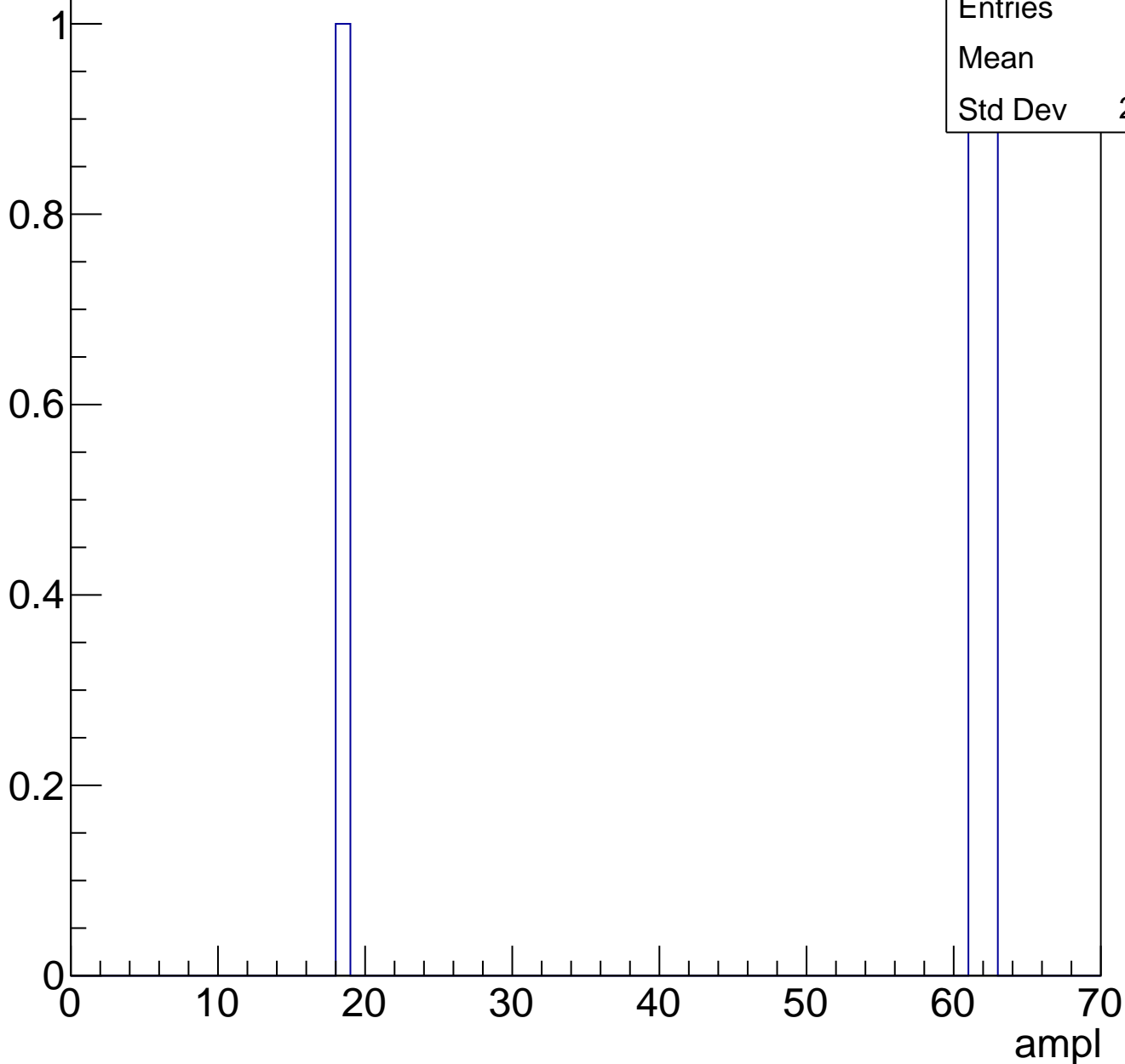
Entry



B1L102S, U9-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch127, adc0

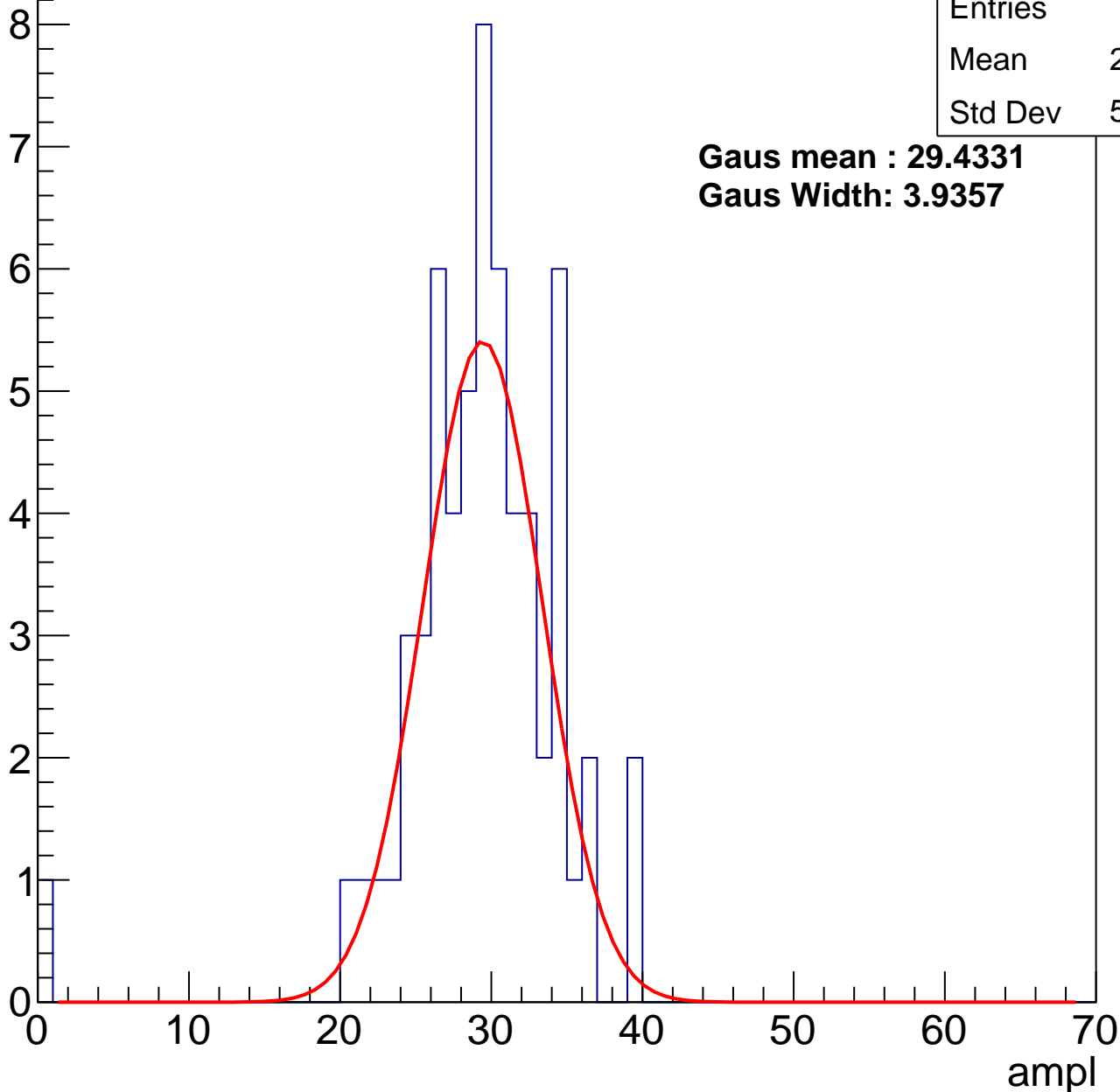
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.79
Std Dev	5.493

Gaus mean : 29.4331

Gaus Width: 3.9357



B1L102S, U9-ch127, adc1

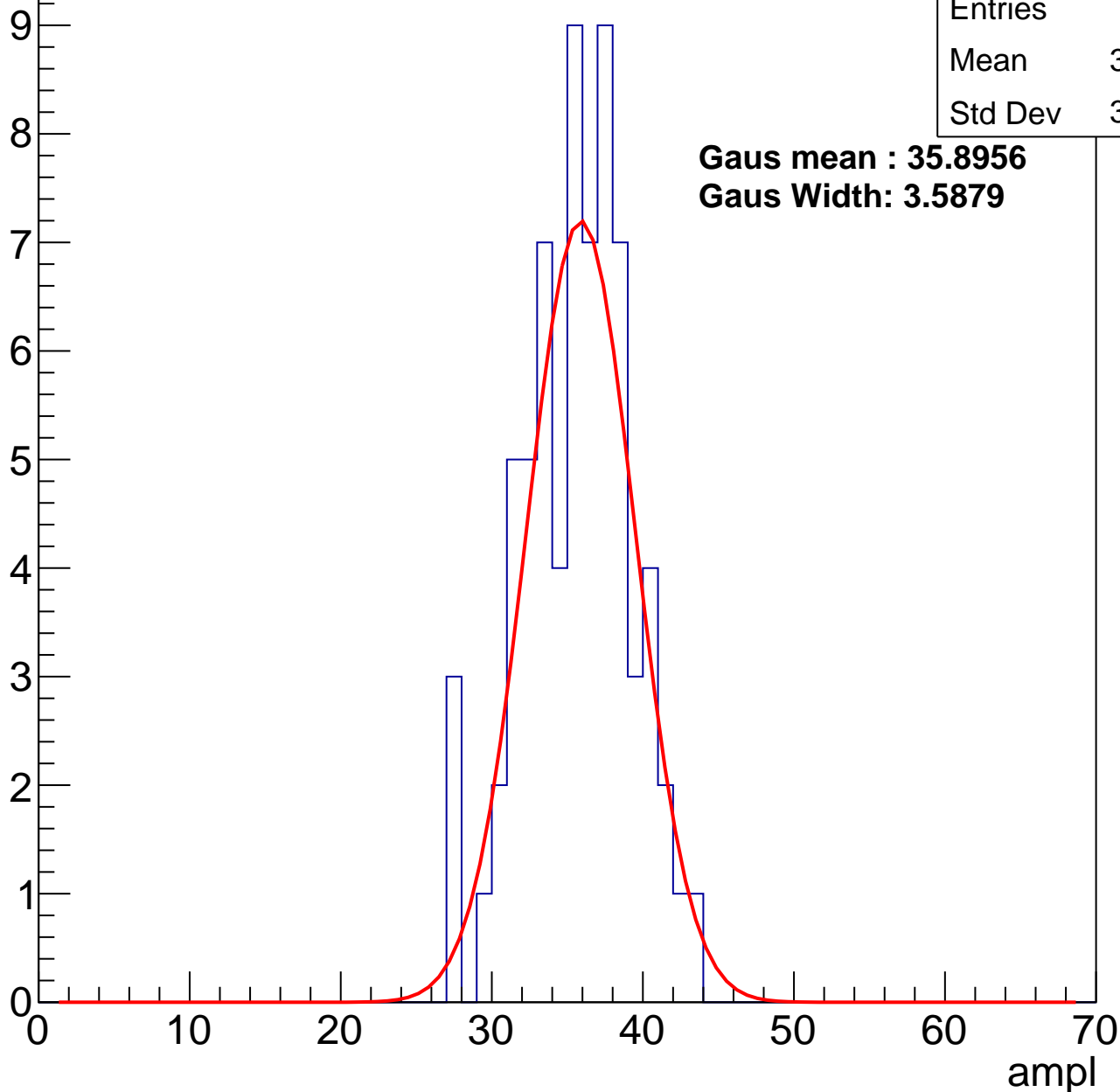
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.17
Std Dev	3.529

Gaus mean : 35.8956

Gaus Width: 3.5879



B1L102S, U9-ch127, adc2

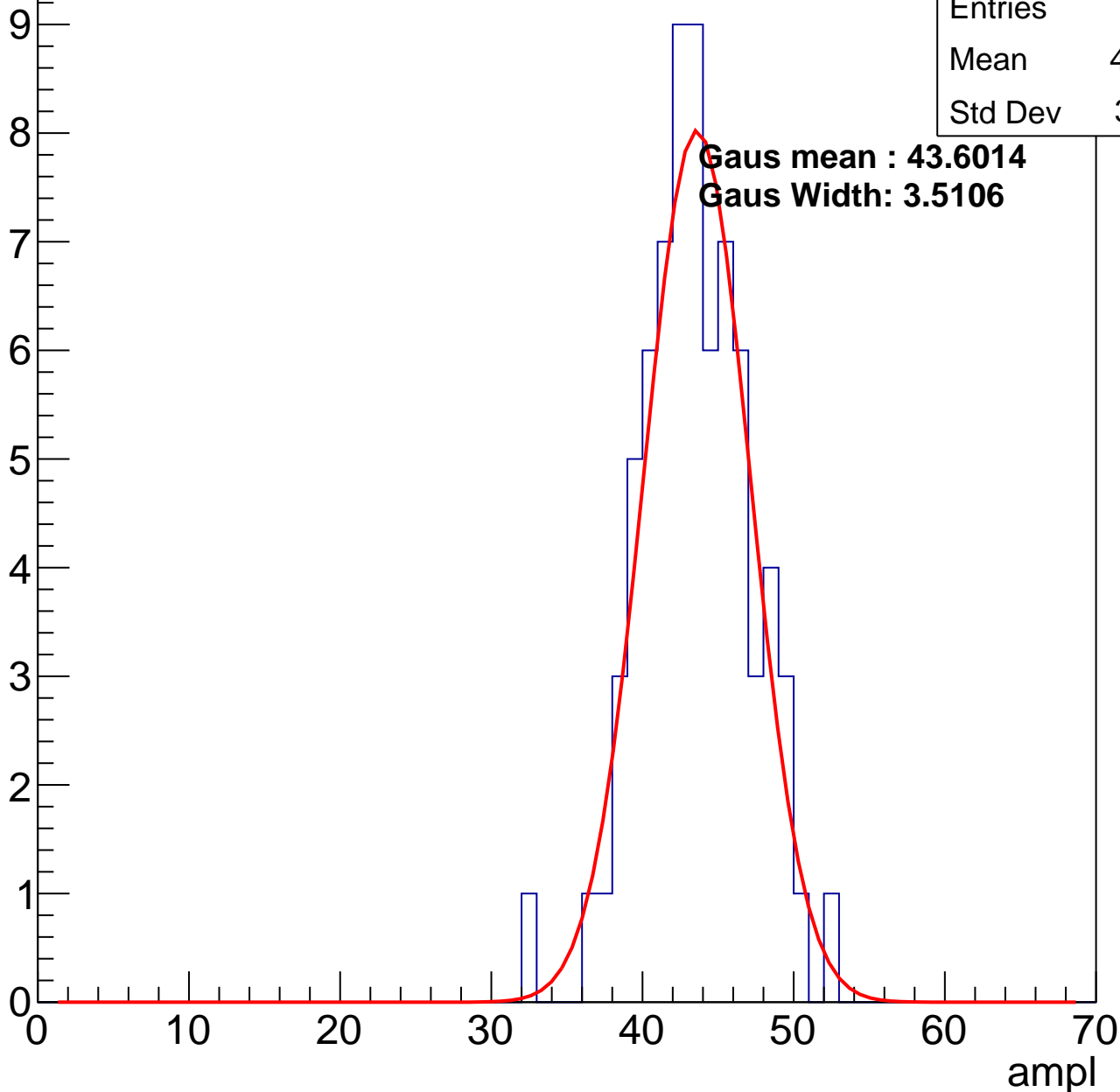
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.05
Std Dev	3.561

Gaus mean : 43.6014

Gaus Width: 3.5106

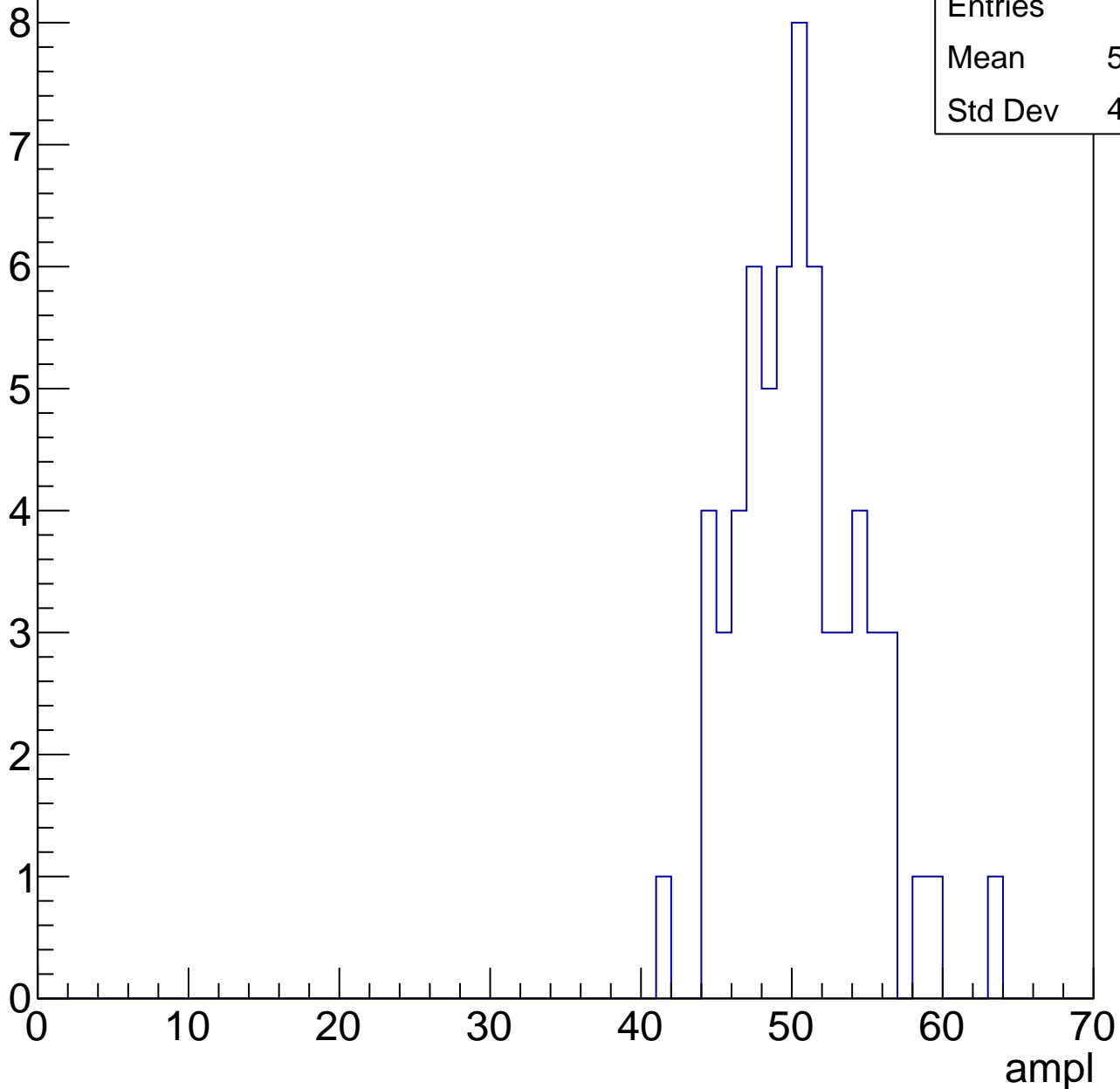


B1L102S, U9-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	50.03
Std Dev	4.115

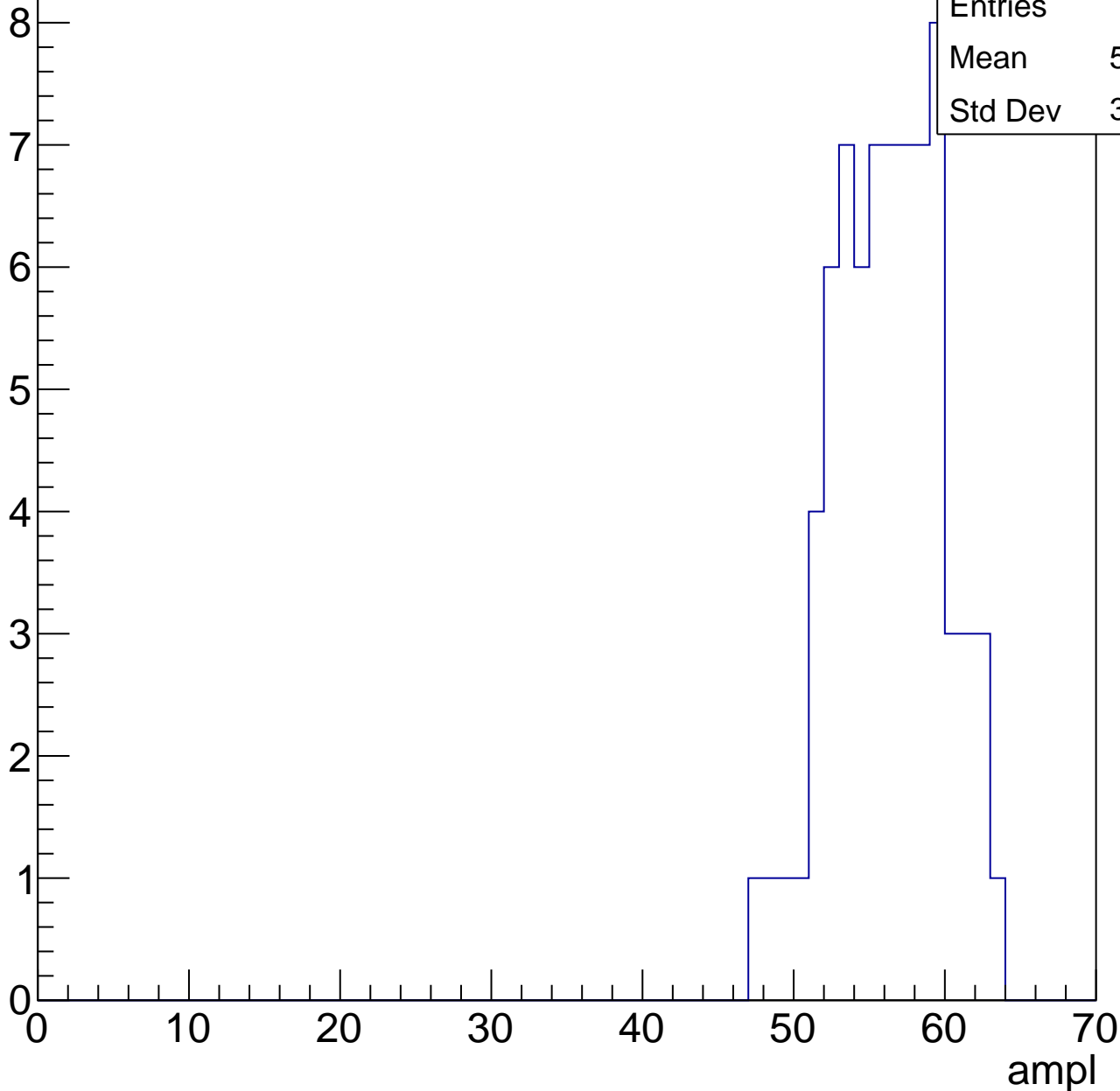


B1L102S, U9-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	55.77
Std Dev	3.517

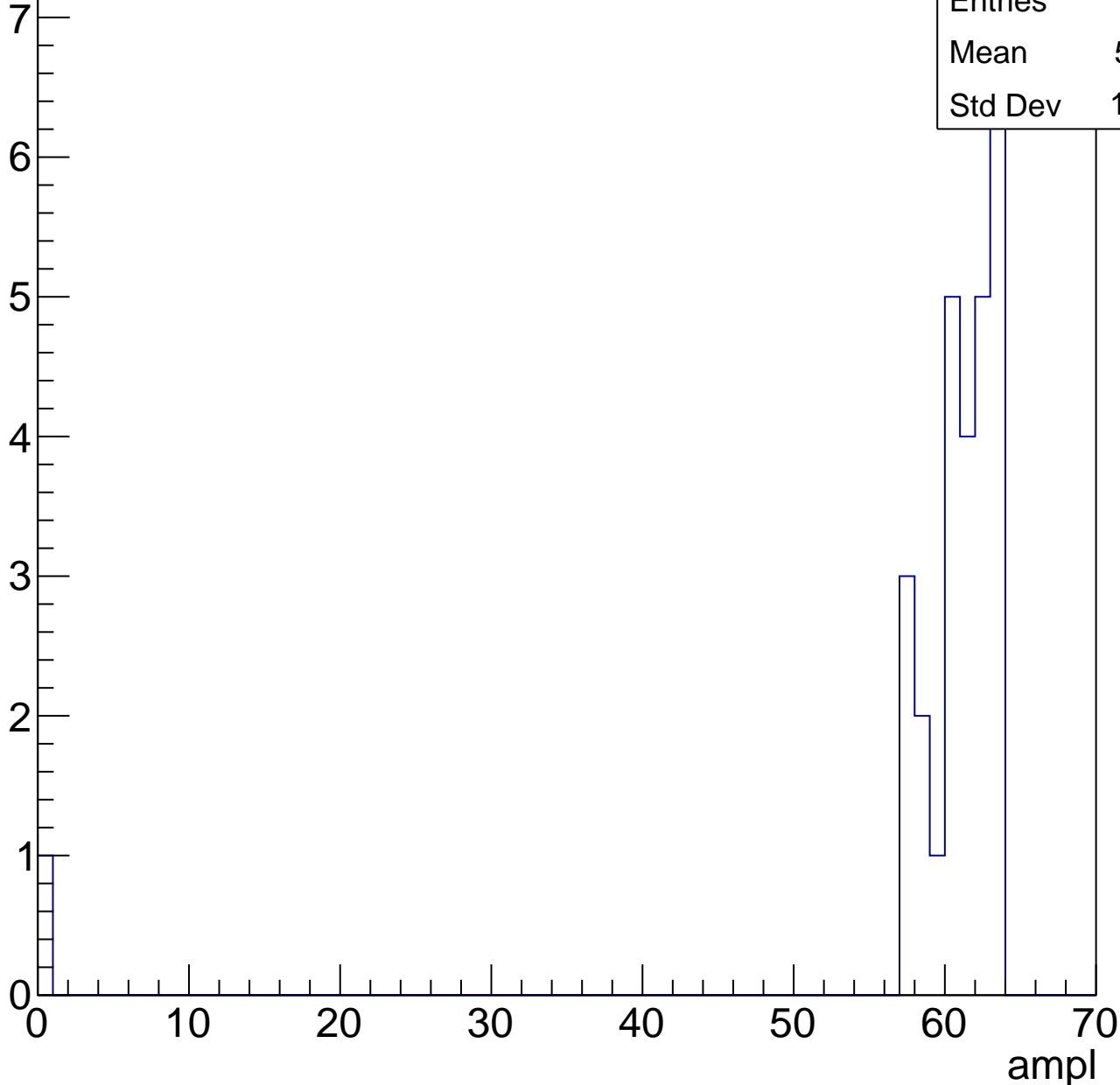


B1L102S, U9-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

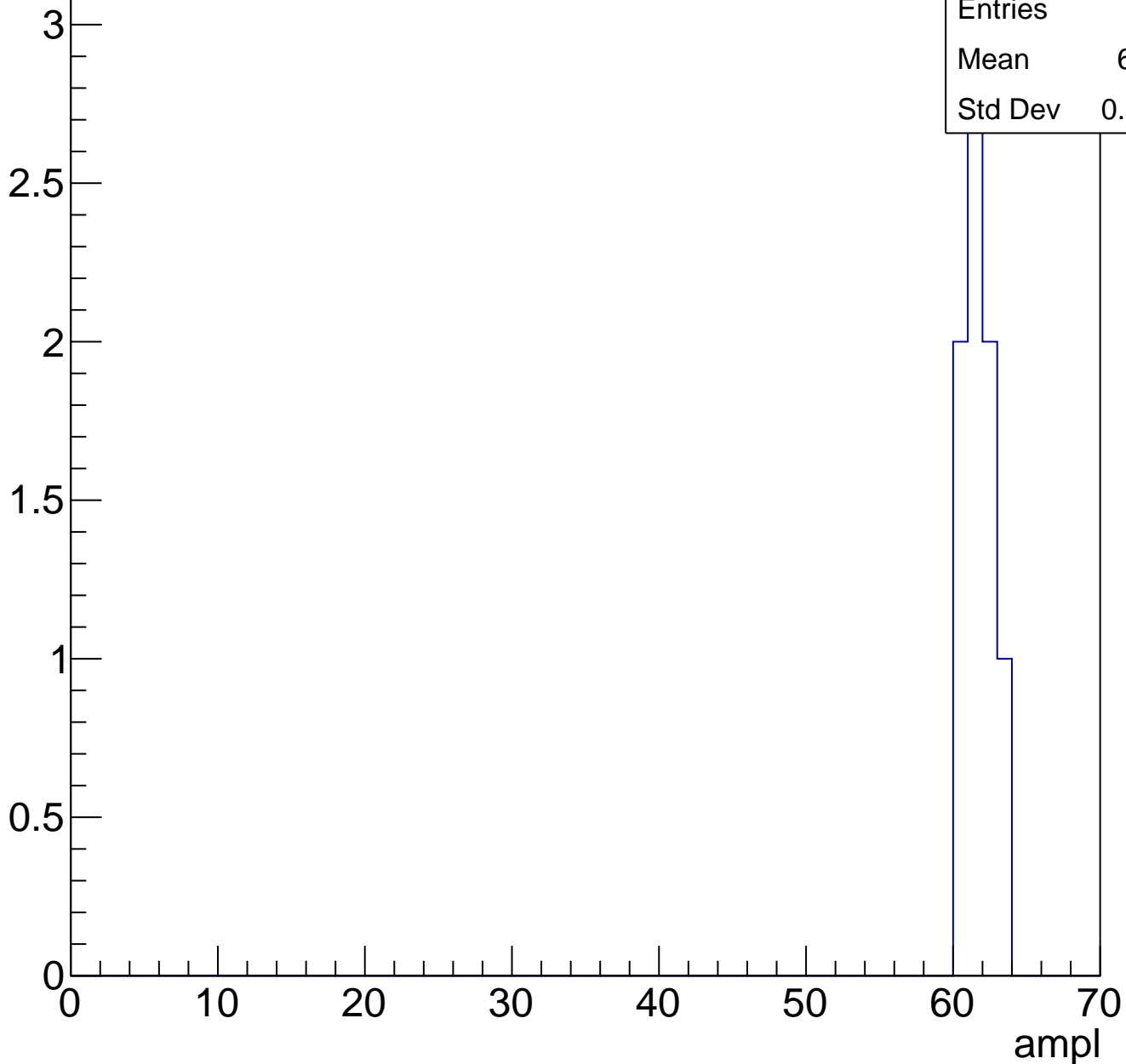
Entries	28
Mean	58.61
Std Dev	11.45



B1L102S, U9-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	8
Mean	61.25
Std Dev	0.9682

B1L102S, U9-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U9-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0