

B1L000S, U9-ch0

calib_packv5_042523_0143.root, FC#3, port B3

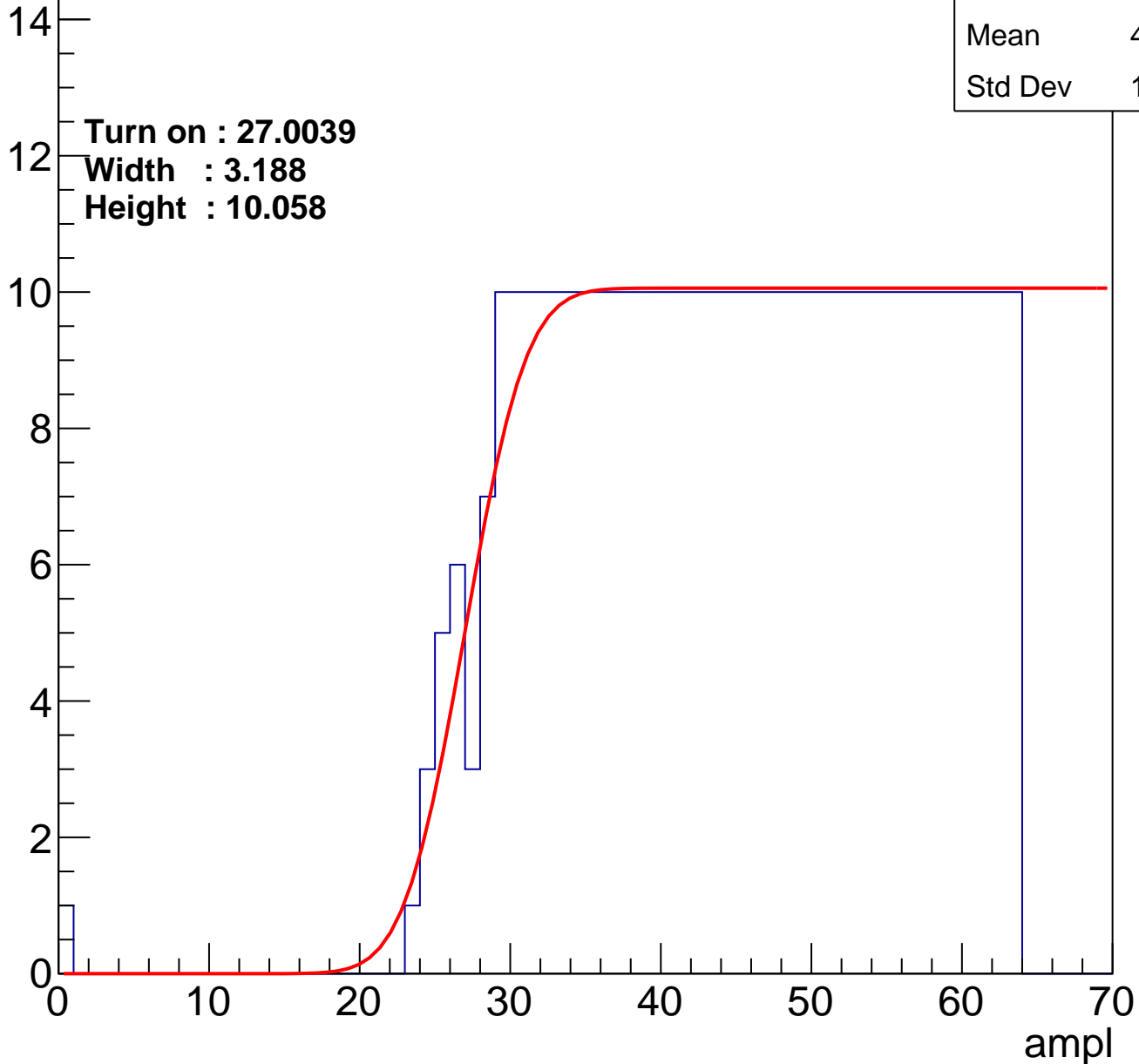
Entries	376
Mean	44.56
Std Dev	11.18

Turn on : 27.0039

Width : 3.188

Height : 10.058

Entry



B1L000S, U9-ch1

calib_packv5_042523_0143.root, FC#3, port B3

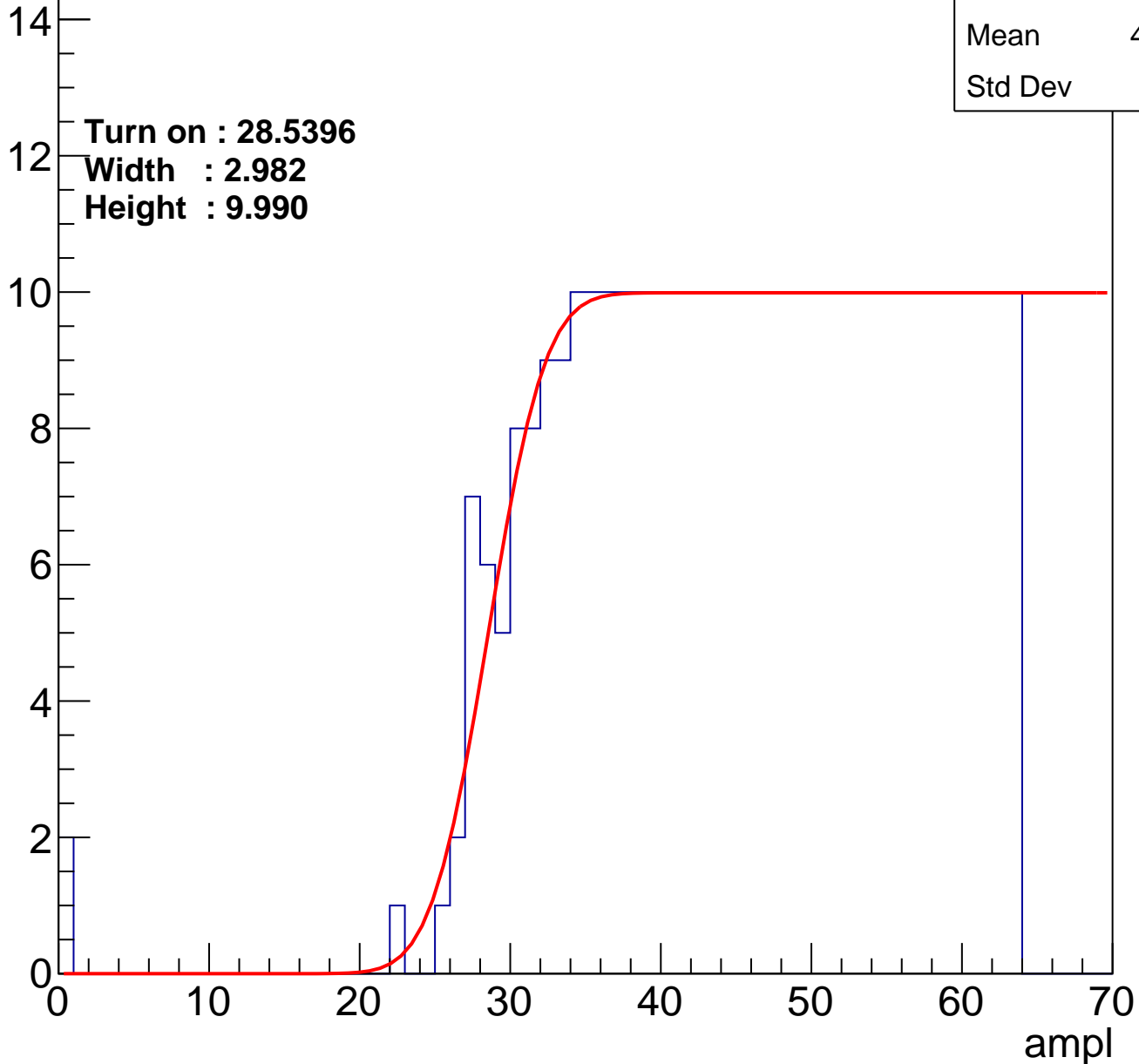
Entries	358
Mean	45.32
Std Dev	11

Turn on : 28.5396

Width : 2.982

Height : 9.990

Entry



B1L000S, U9-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.09
Std Dev	11.88

Turn on : 26.8464

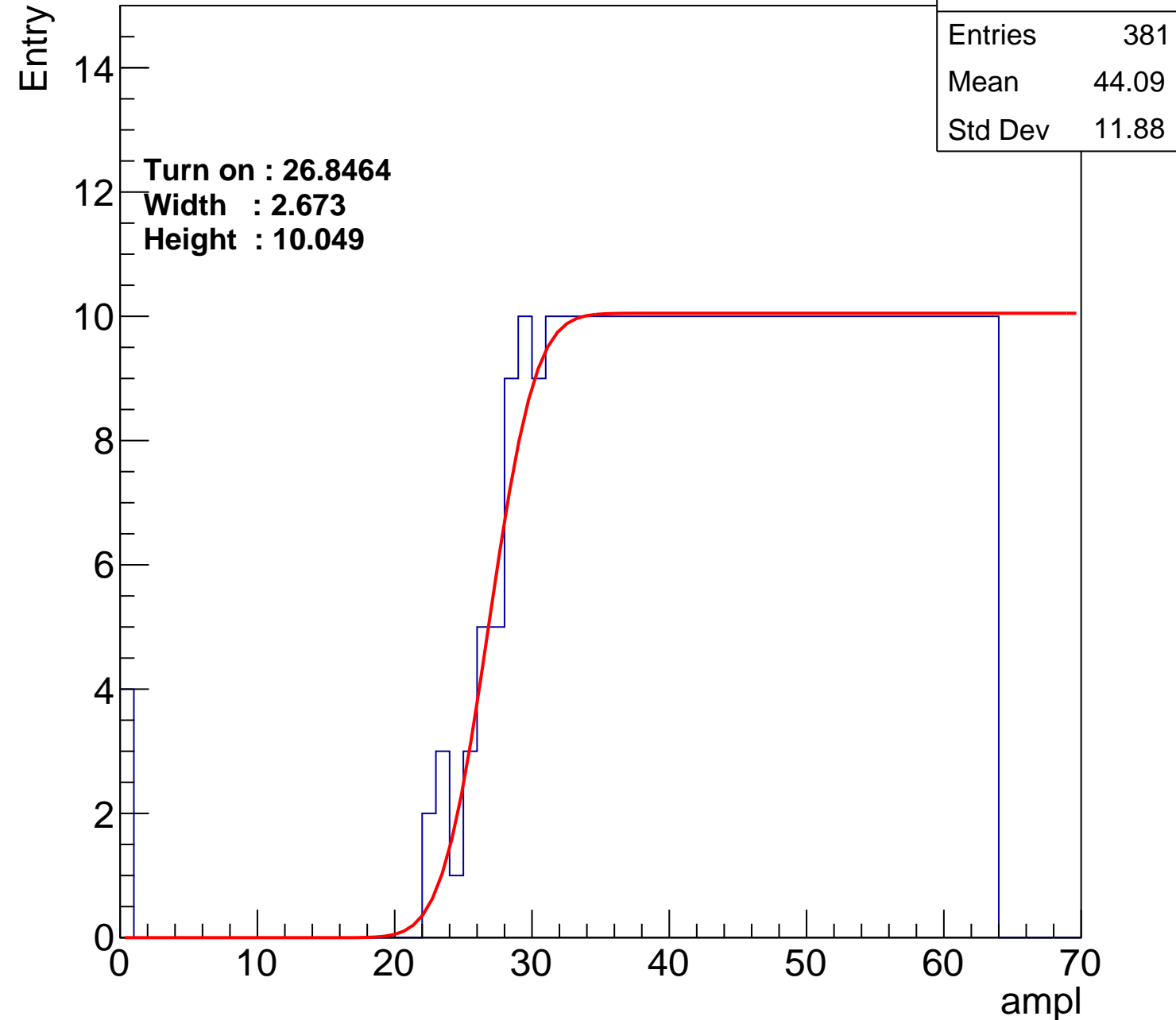
Width : 2.673

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch3

calib_packv5_042523_0143.root, FC#3, port B3

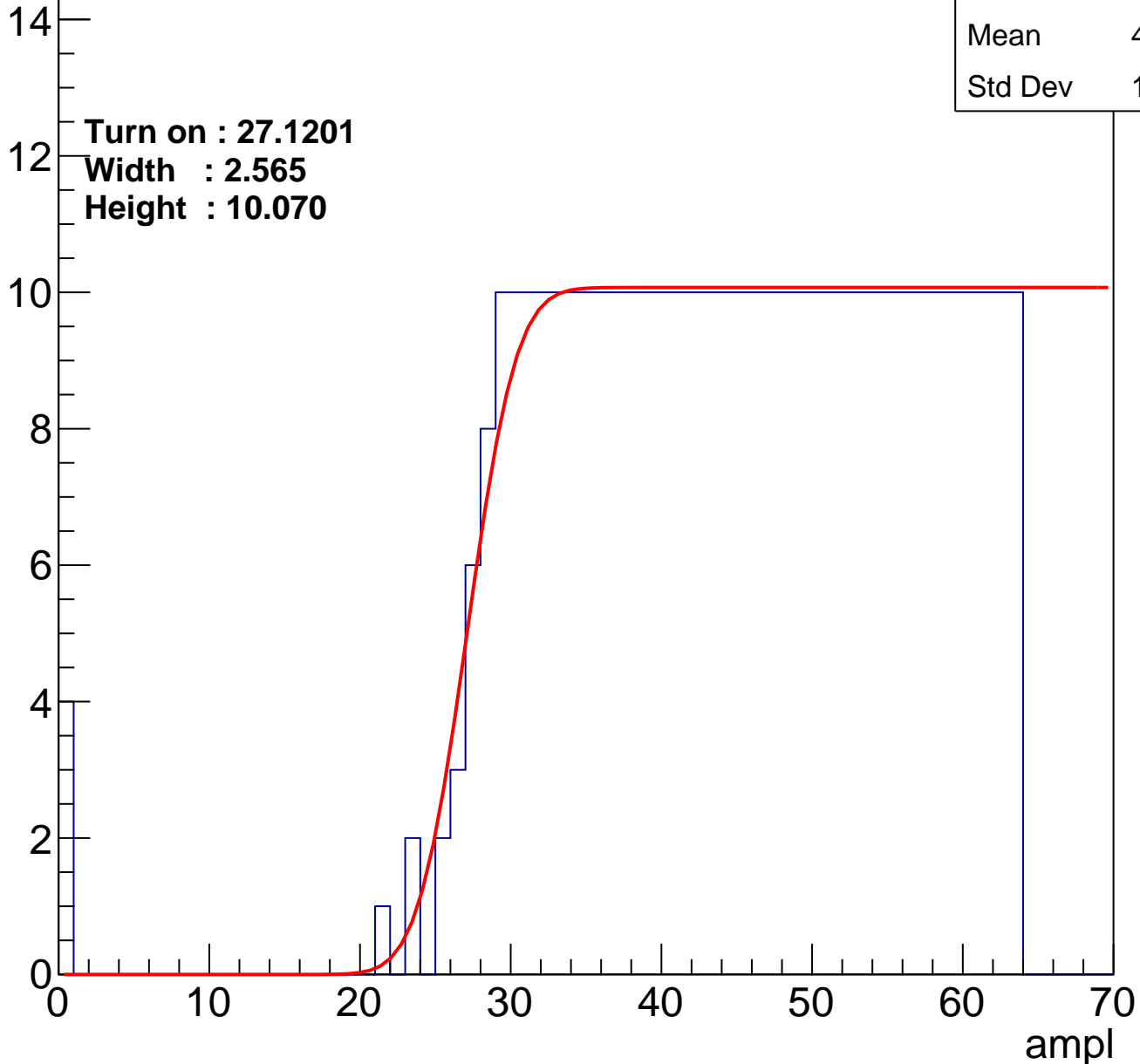
Entries	376
Mean	44.36
Std Dev	11.72

Turn on : 27.1201

Width : 2.565

Height : 10.070

Entry



B1L000S, U9-ch4

calib_packv5_042523_0143.root, FC#3, port B3

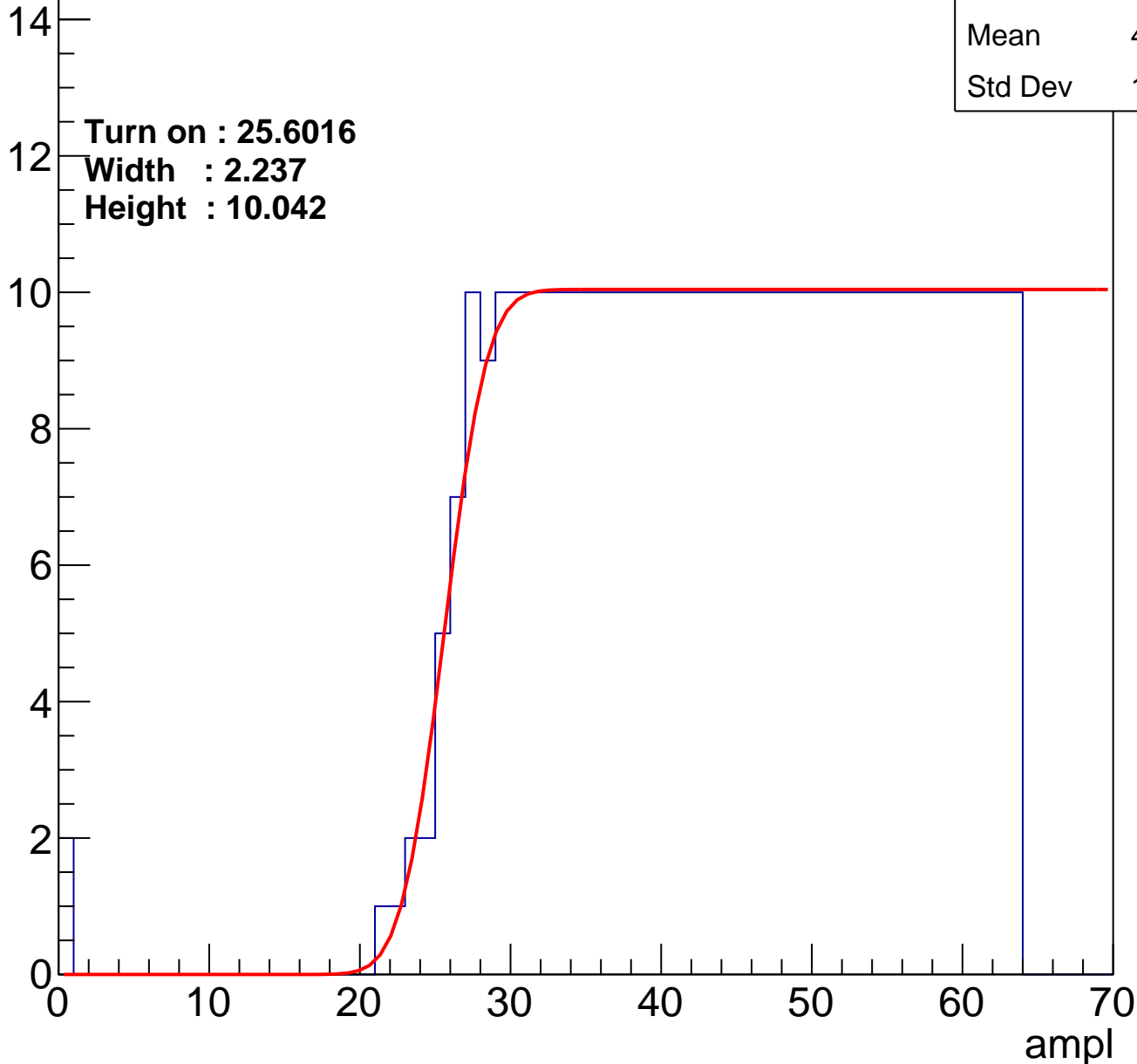
Entries	389
Mean	43.87
Std Dev	11.66

Turn on : 25.6016

Width : 2.237

Height : 10.042

Entry



B1L000S, U9-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.7469

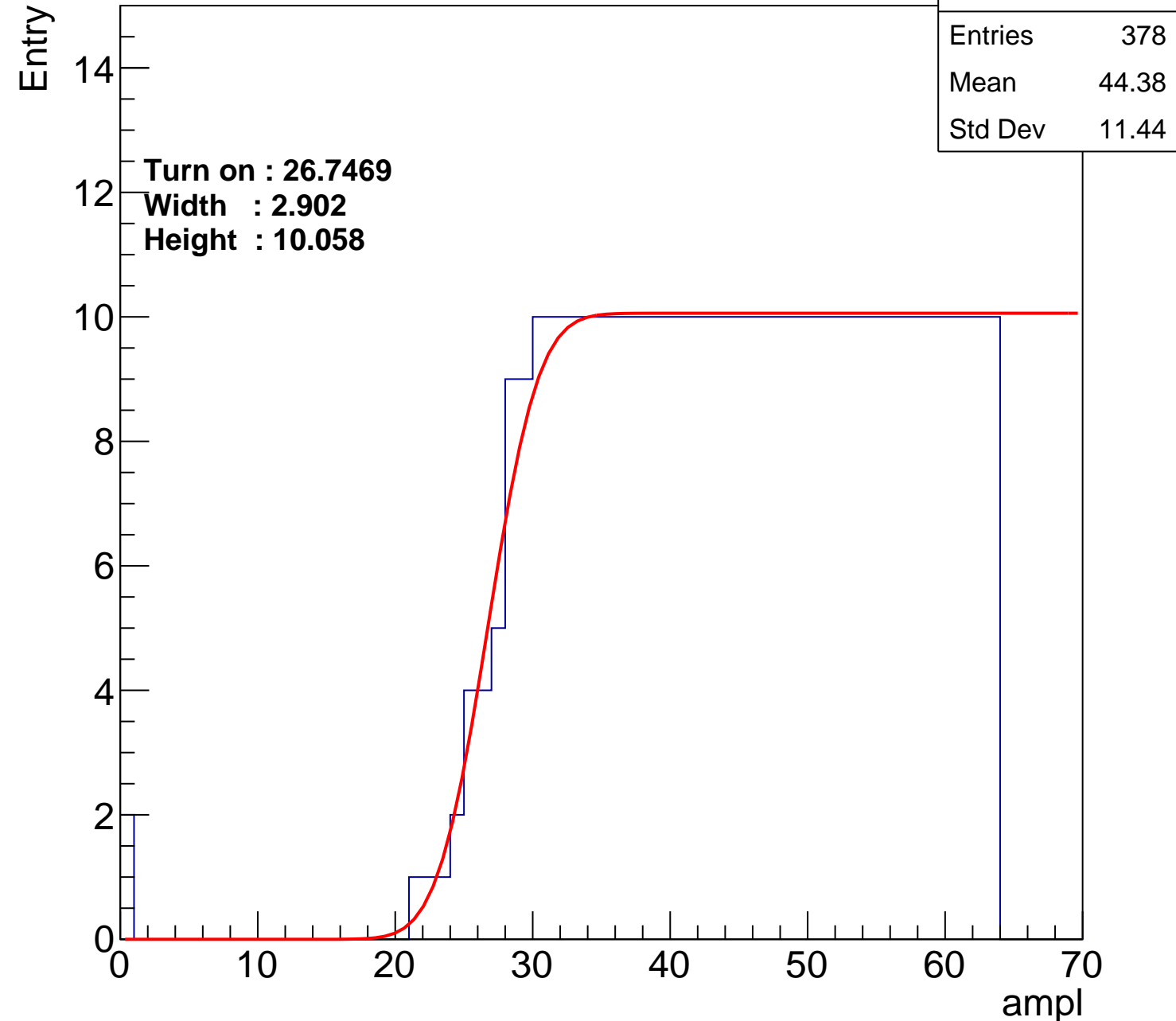
Width : 2.902

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch6

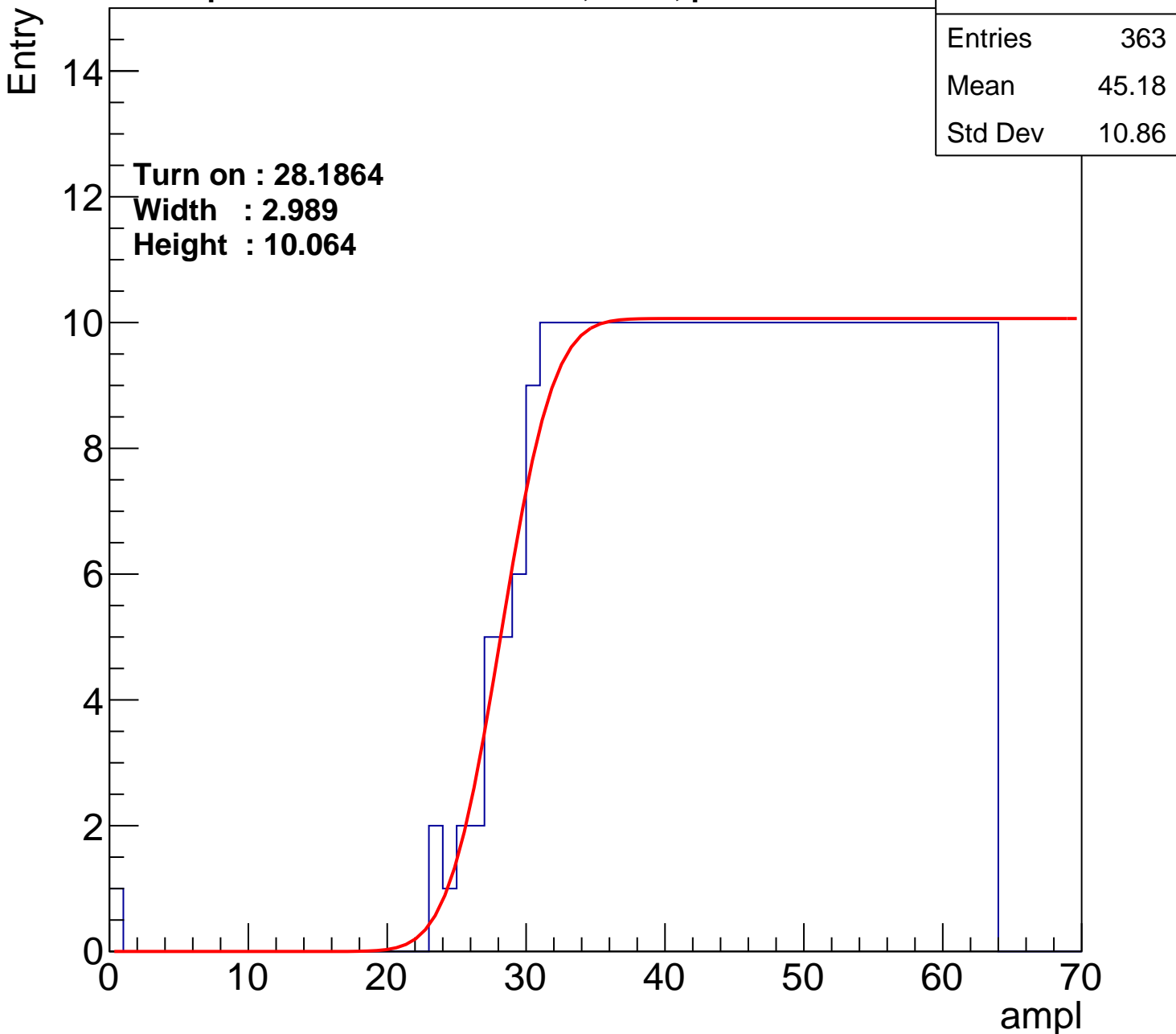
calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.18
Std Dev	10.86

Turn on : 28.1864

Width : 2.989

Height : 10.064



B1L000S, U9-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.39
Std Dev	12.07

Turn on : 24.7472

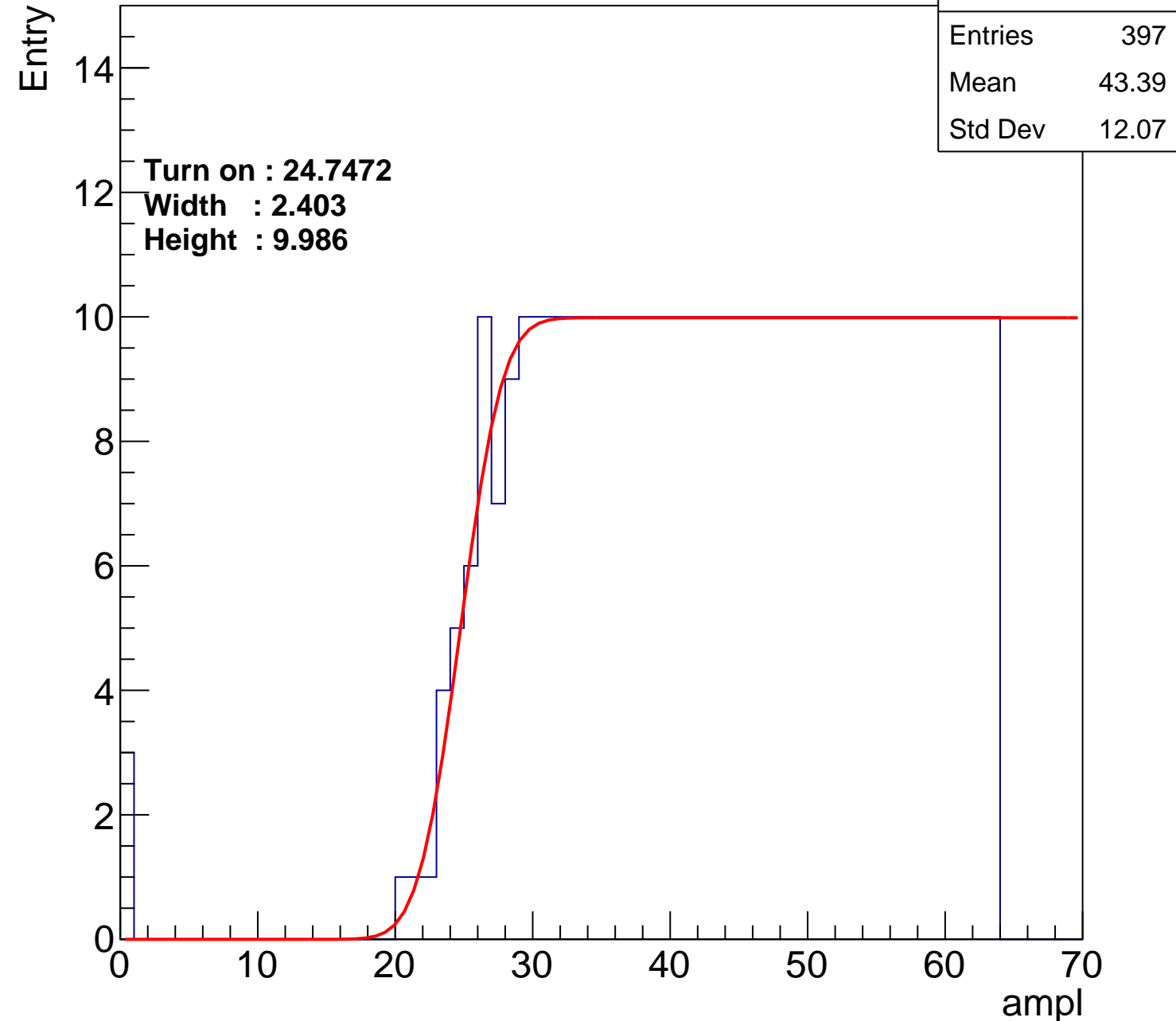
Width : 2.403

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.8000

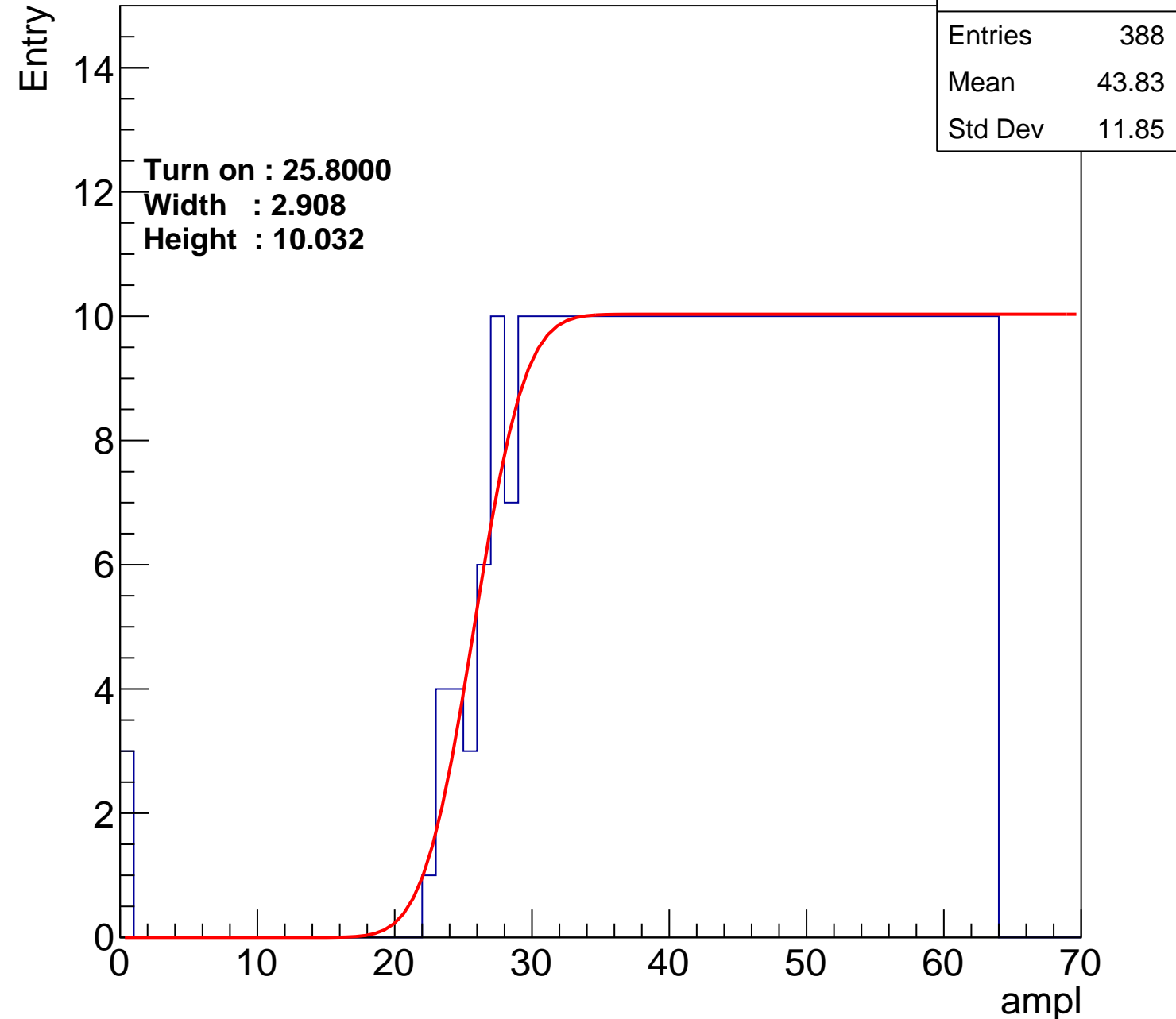
Width : 2.908

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 25.4709

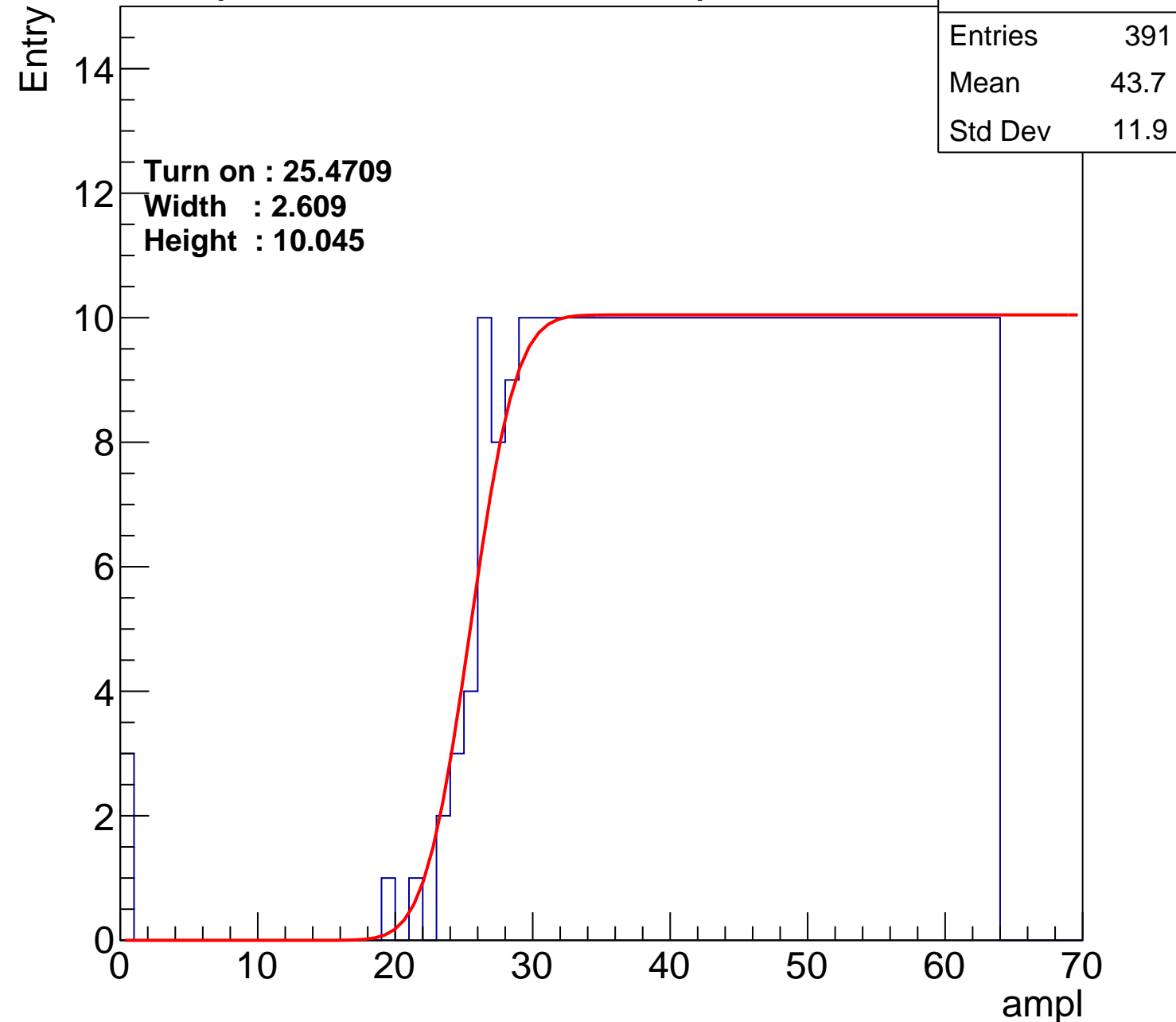
Width : 2.609

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.7816

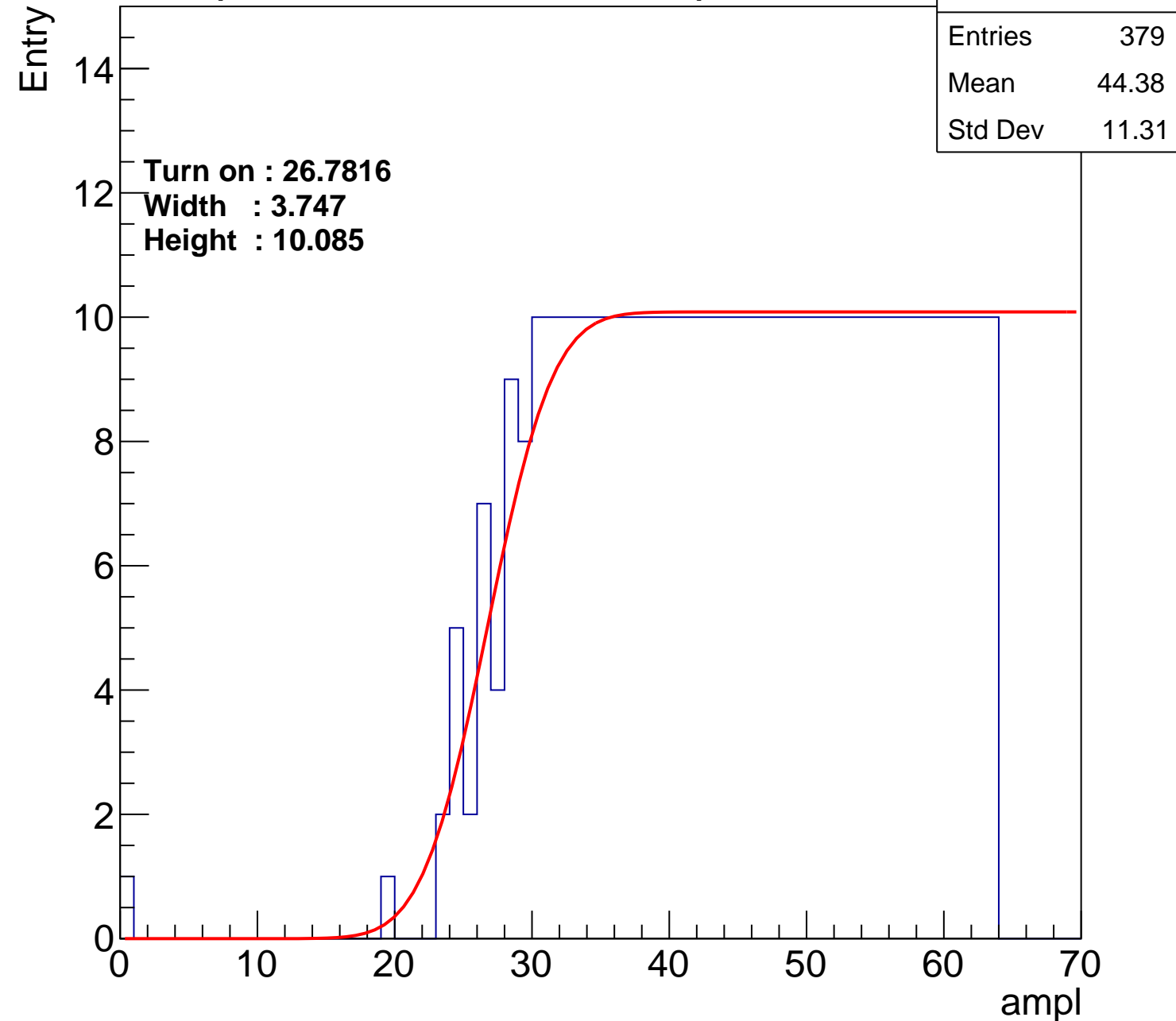
Width : 3.747

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries 364

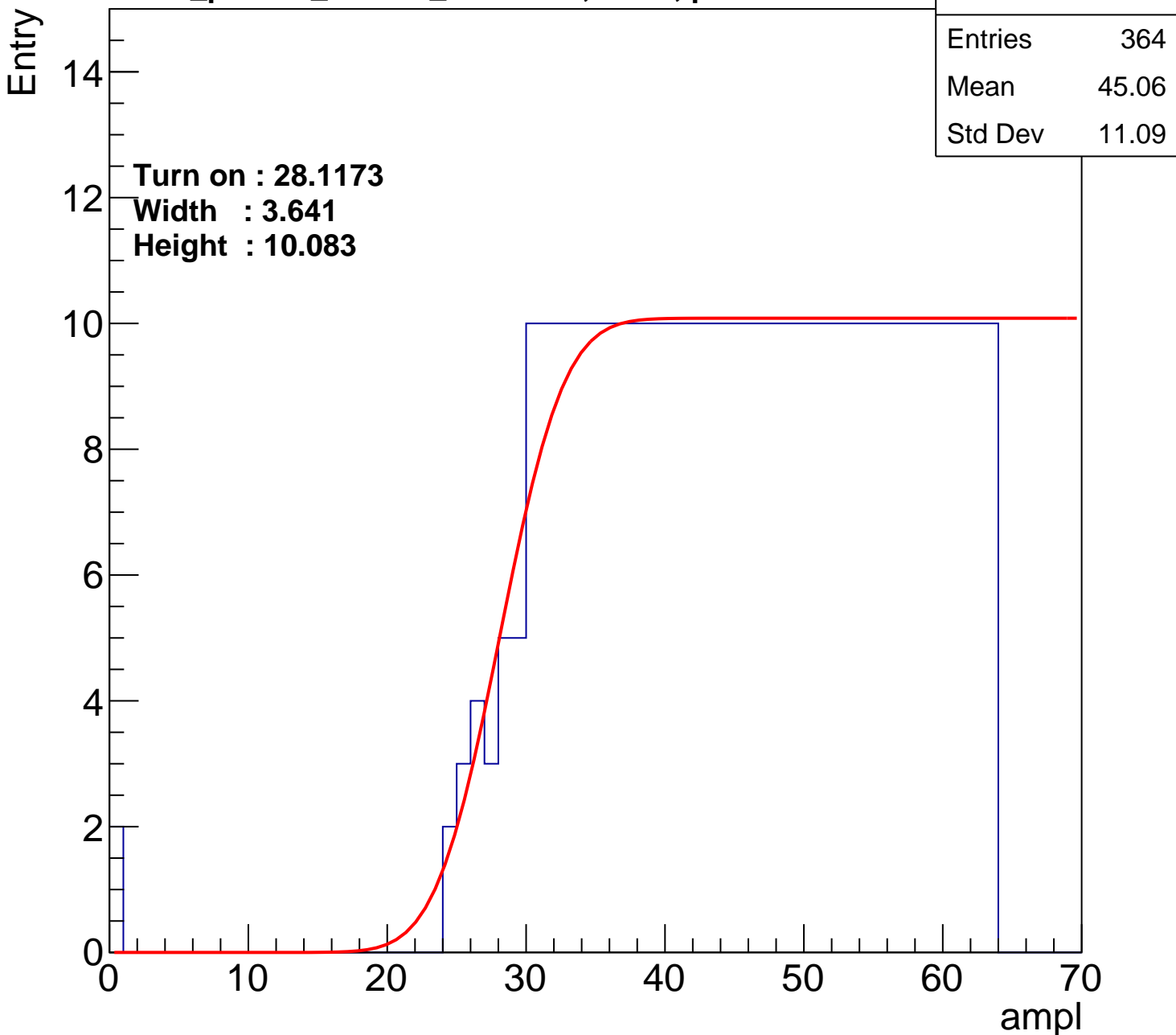
Mean 45.06

Std Dev 11.09

Turn on : 28.1173

Width : 3.641

Height : 10.083



B1L000S, U9-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.3322

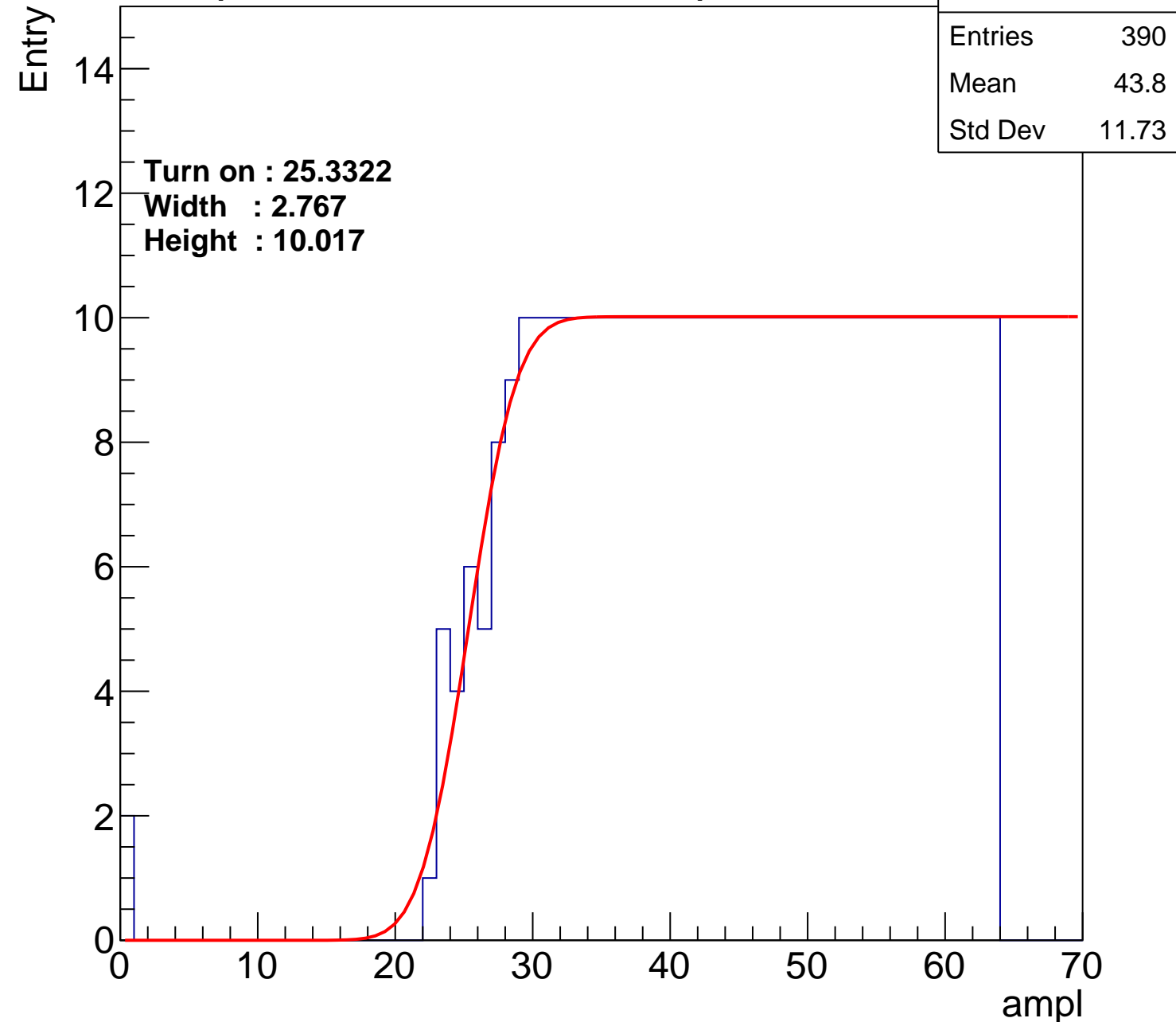
Width : 2.767

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.24
Std Dev	11.68

Turn on : 26.4915

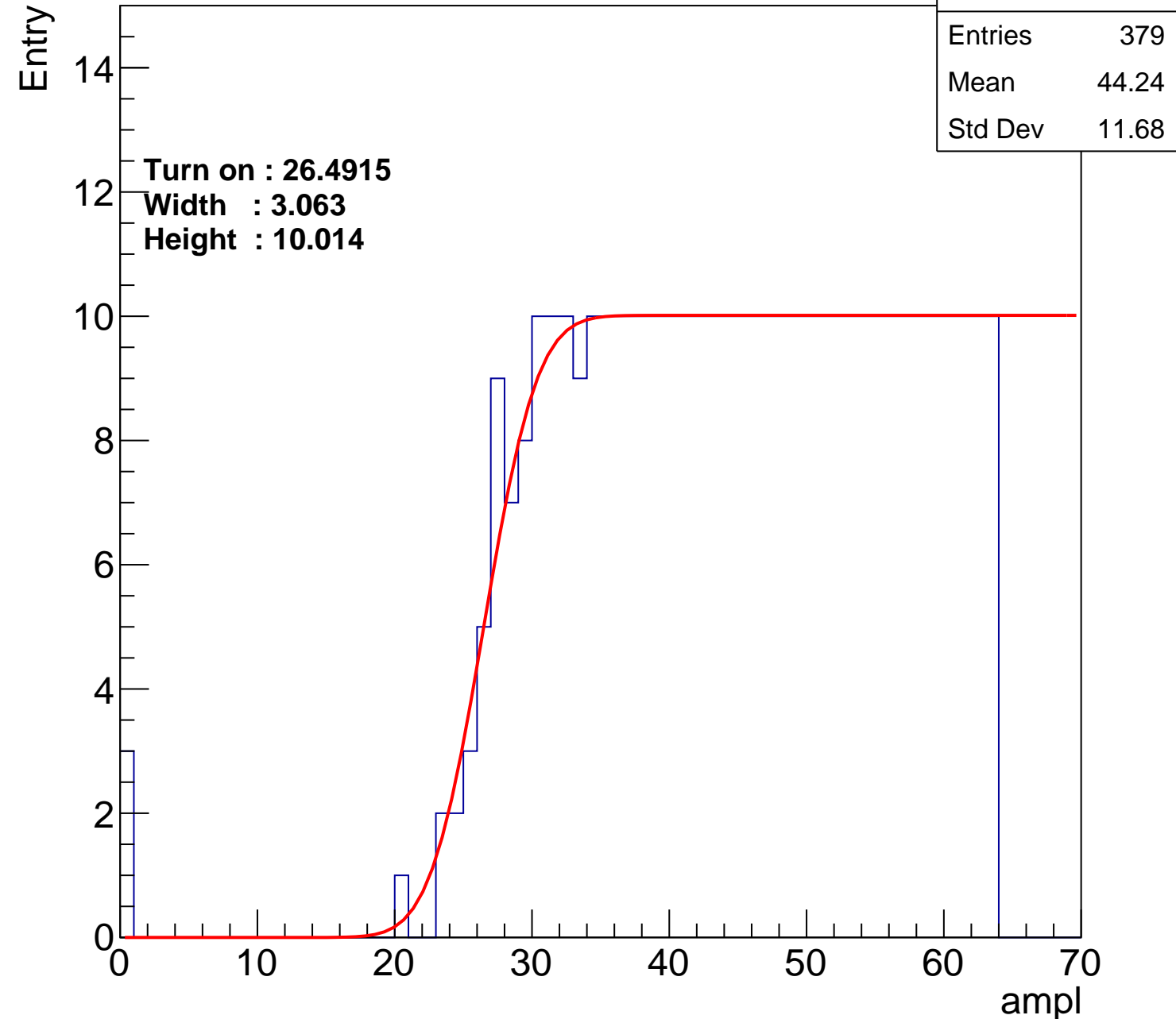
Width : 3.063

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	349
Mean	45.79
Std Dev	10.73

Turn on : 29.8005

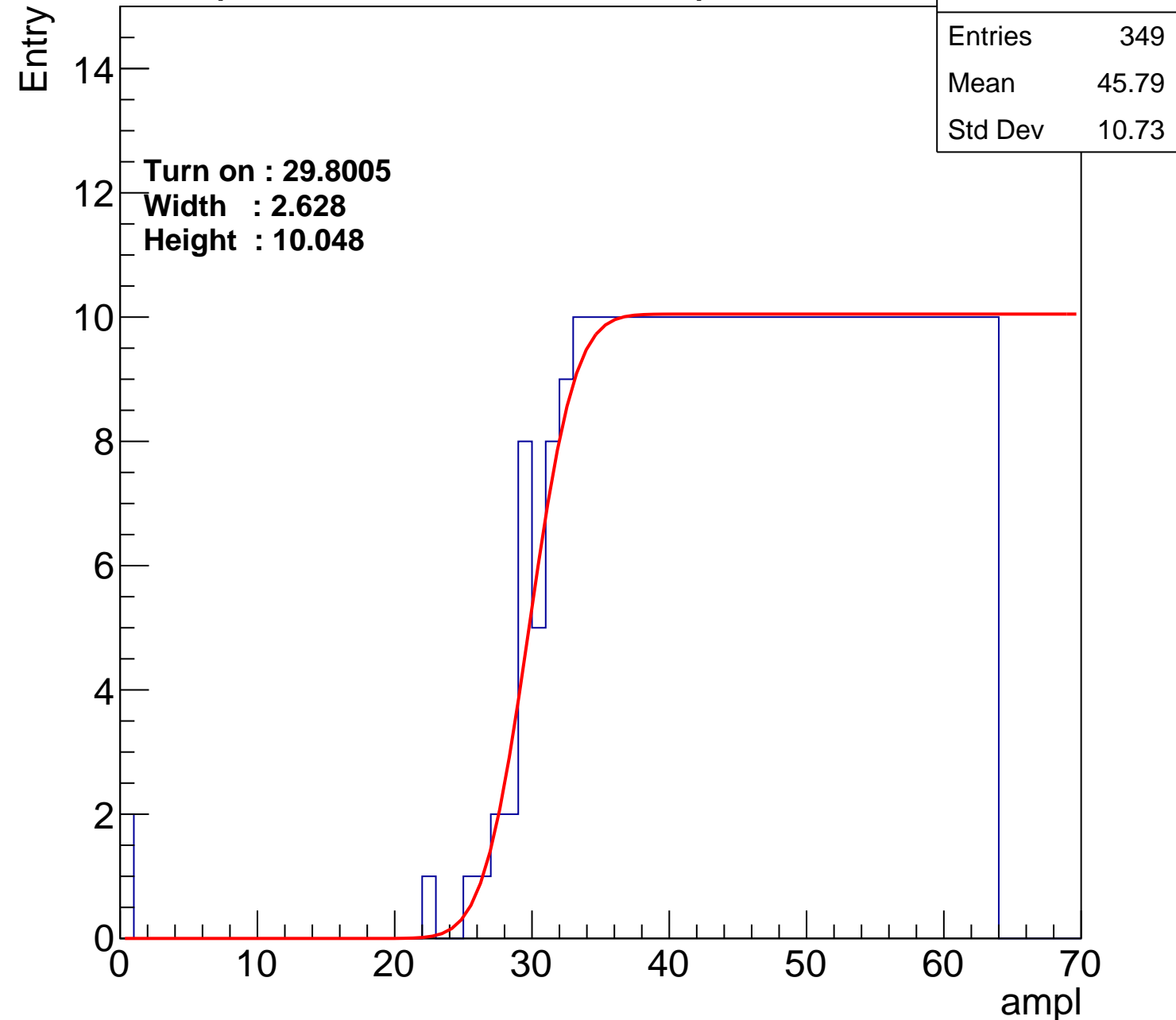
Width : 2.628

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.28
Std Dev	12.03

Turn on : 24.2714

Width : 3.640

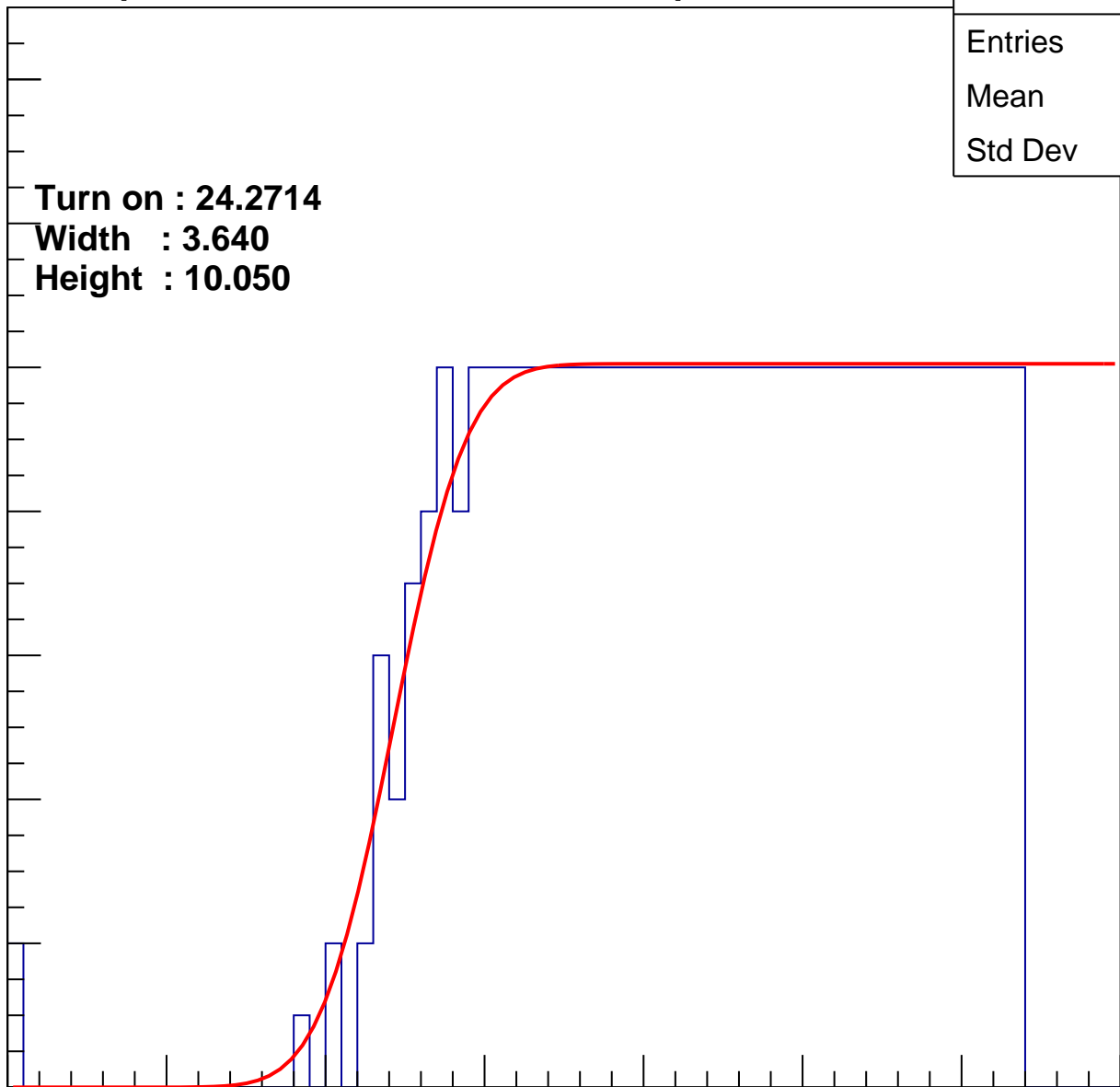
Height : 10.050

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L000S, U9-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.7399

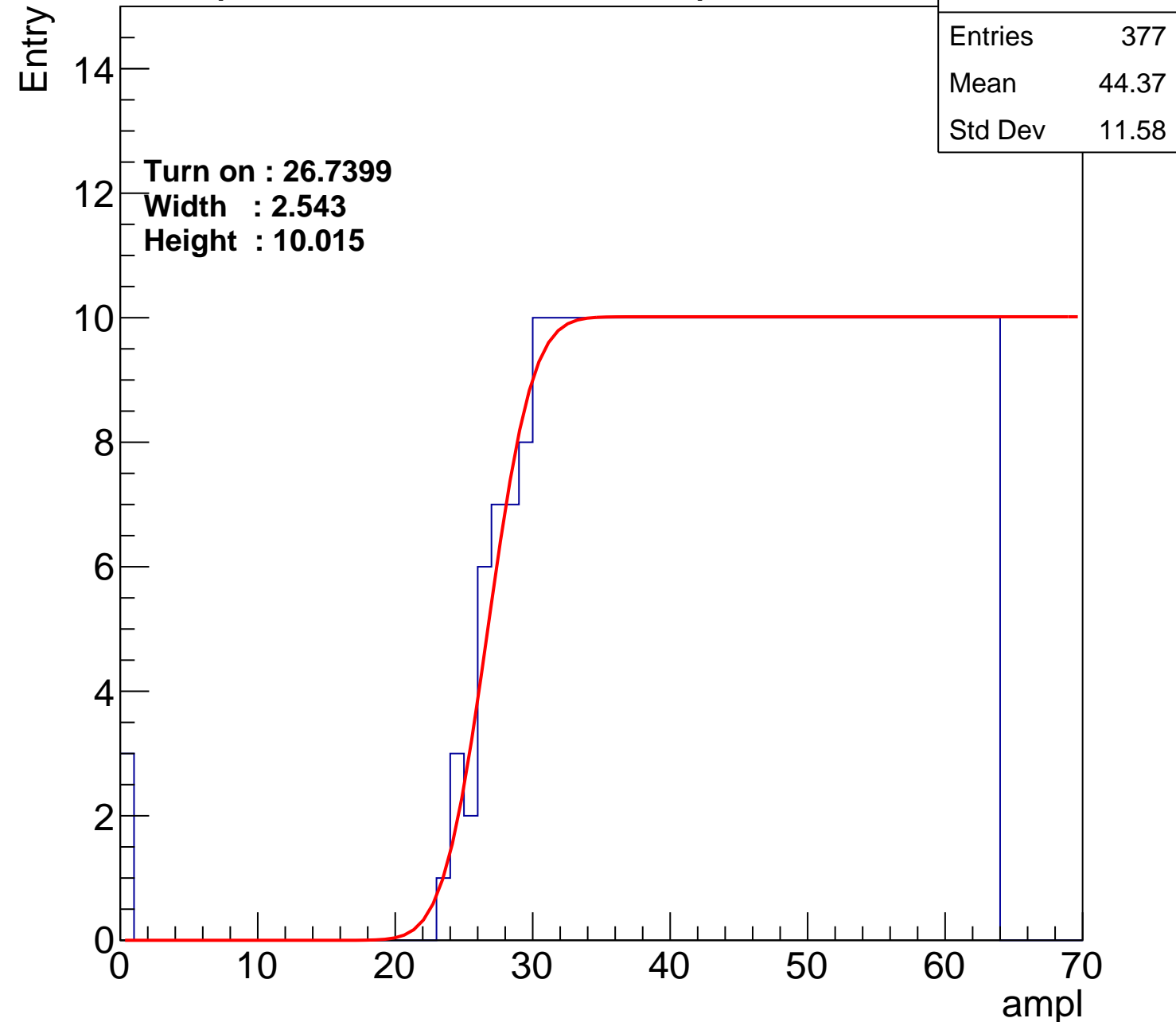
Width : 2.543

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch17

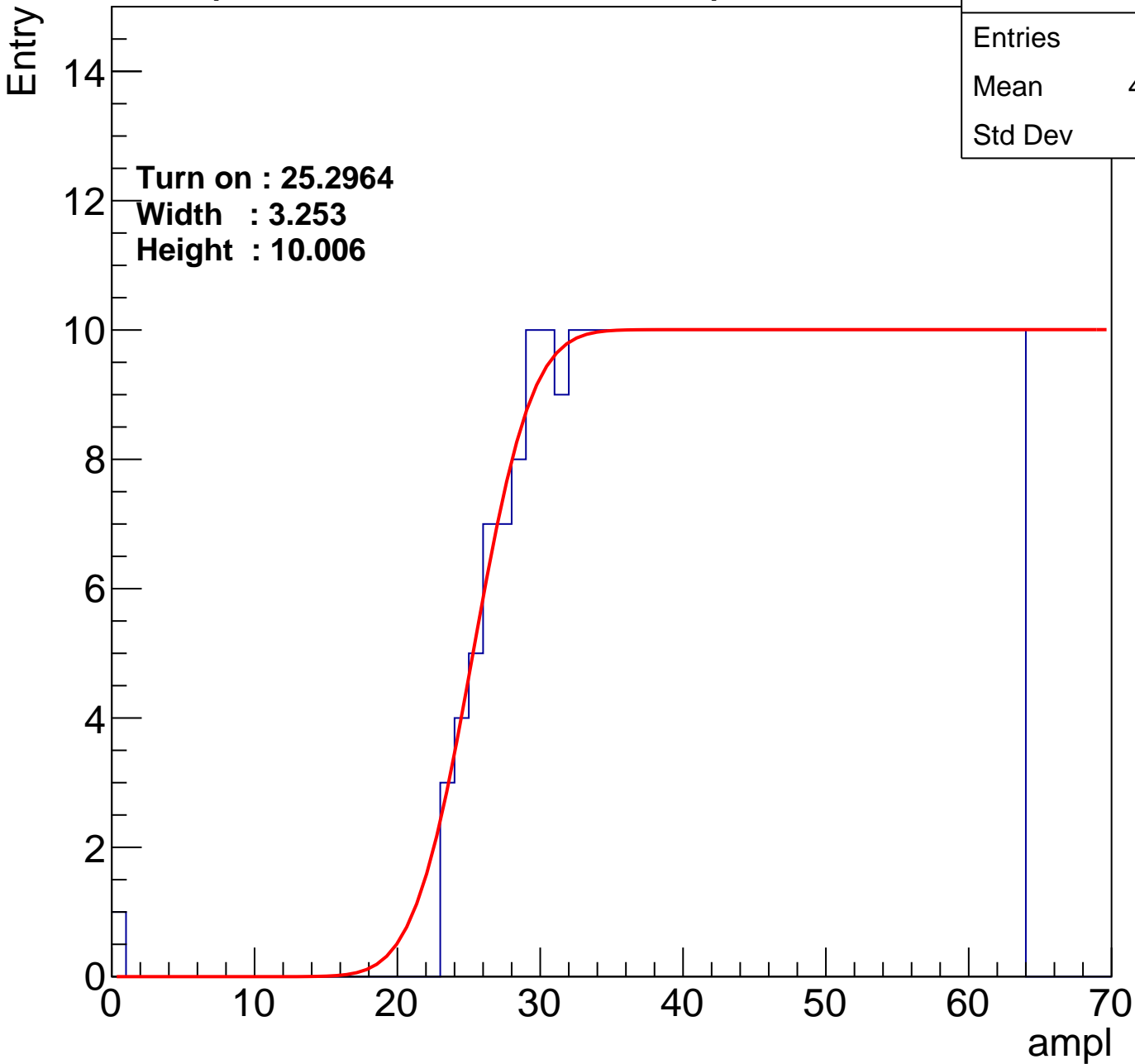
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.2964

Width : 3.253

Height : 10.006

Entries	384
Mean	44.15
Std Dev	11.4



B1L000S, U9-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.98
Std Dev	11.89

Turn on : 26.0343

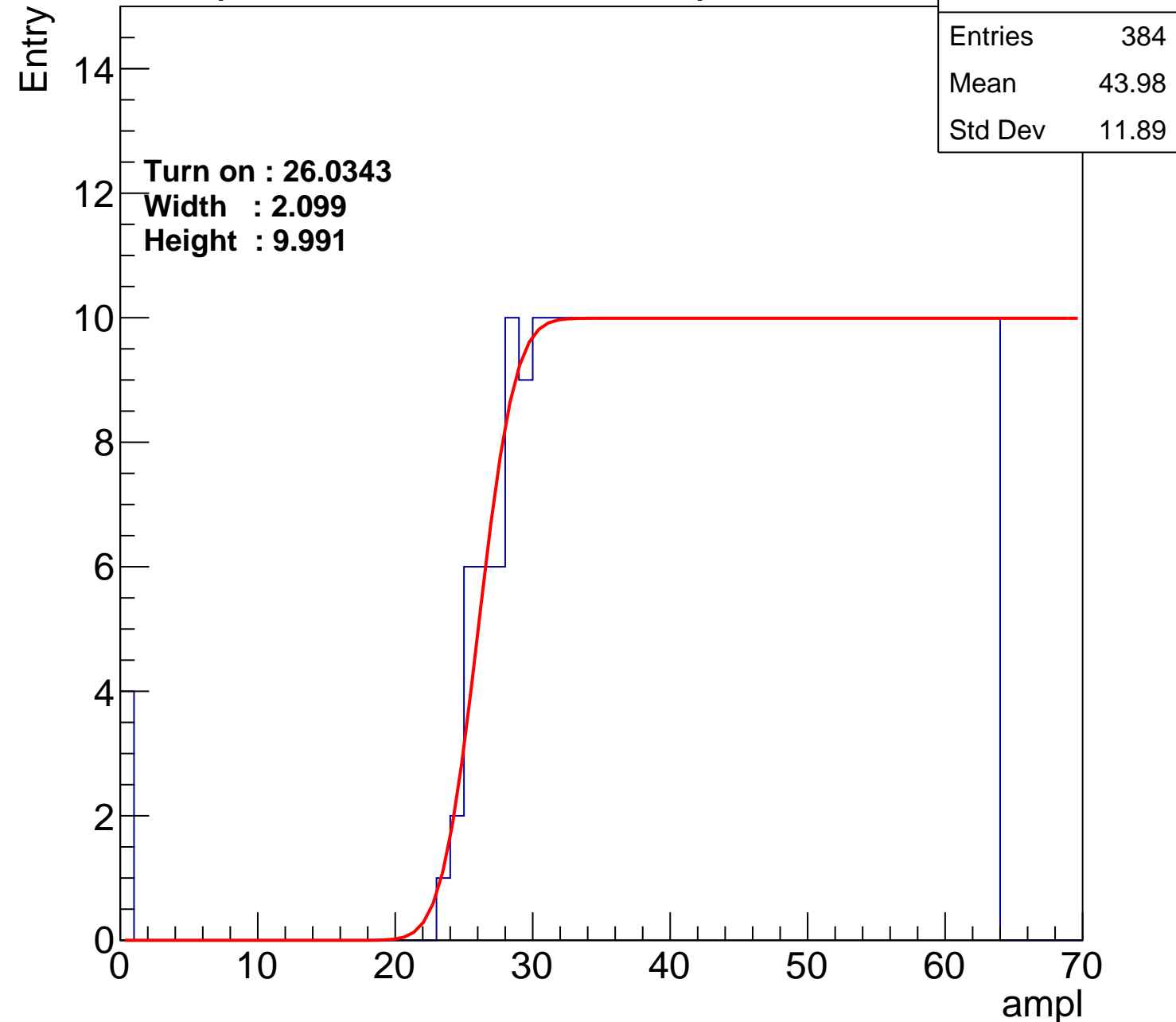
Width : 2.099

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.6
Std Dev	12.11

Turn on : 25.5046

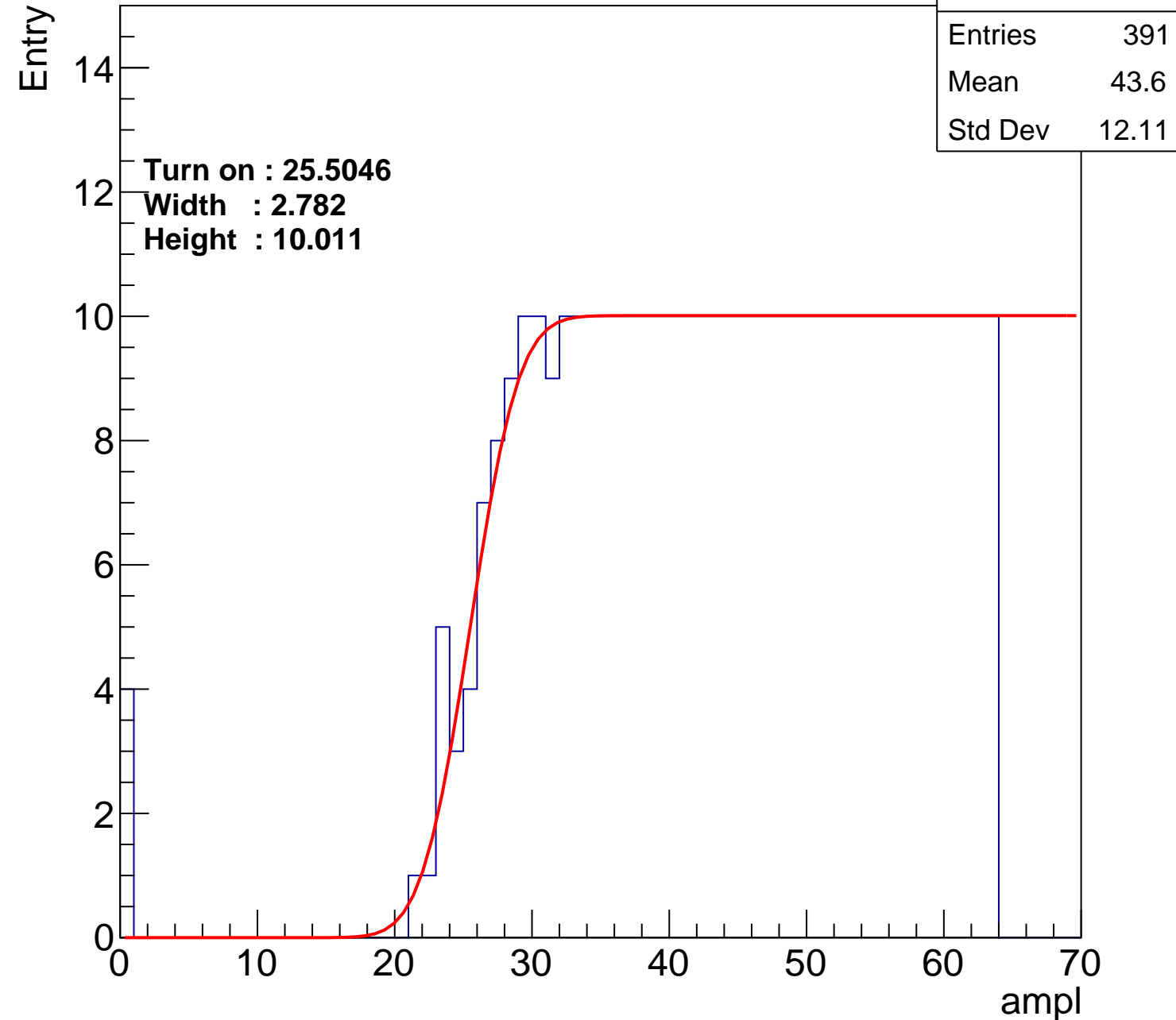
Width : 2.782

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.39
Std Dev	12.08

Turn on : 25.2377

Width : 3.270

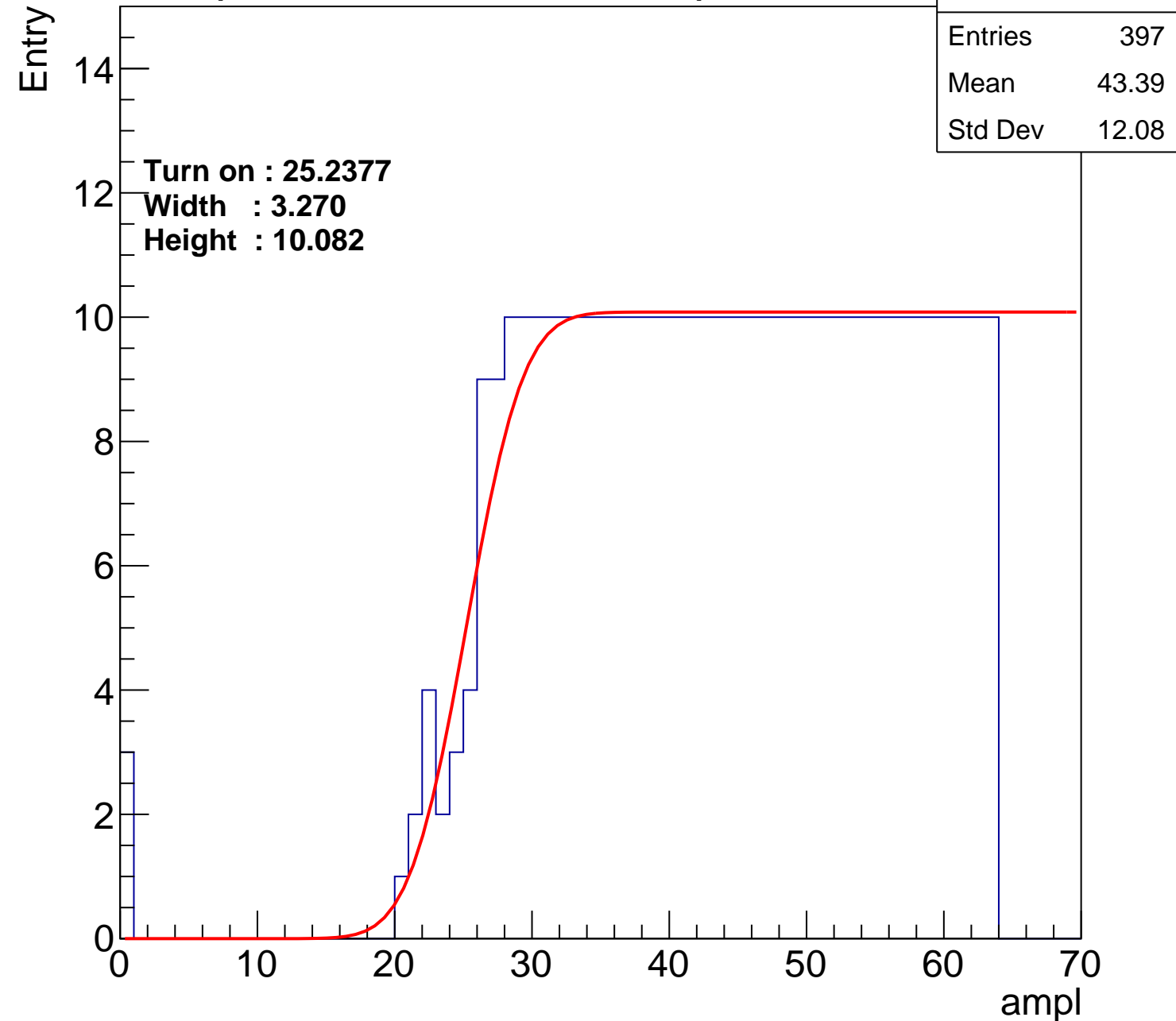
Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U9-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.98
Std Dev	11.64

Turn on : 26.2141

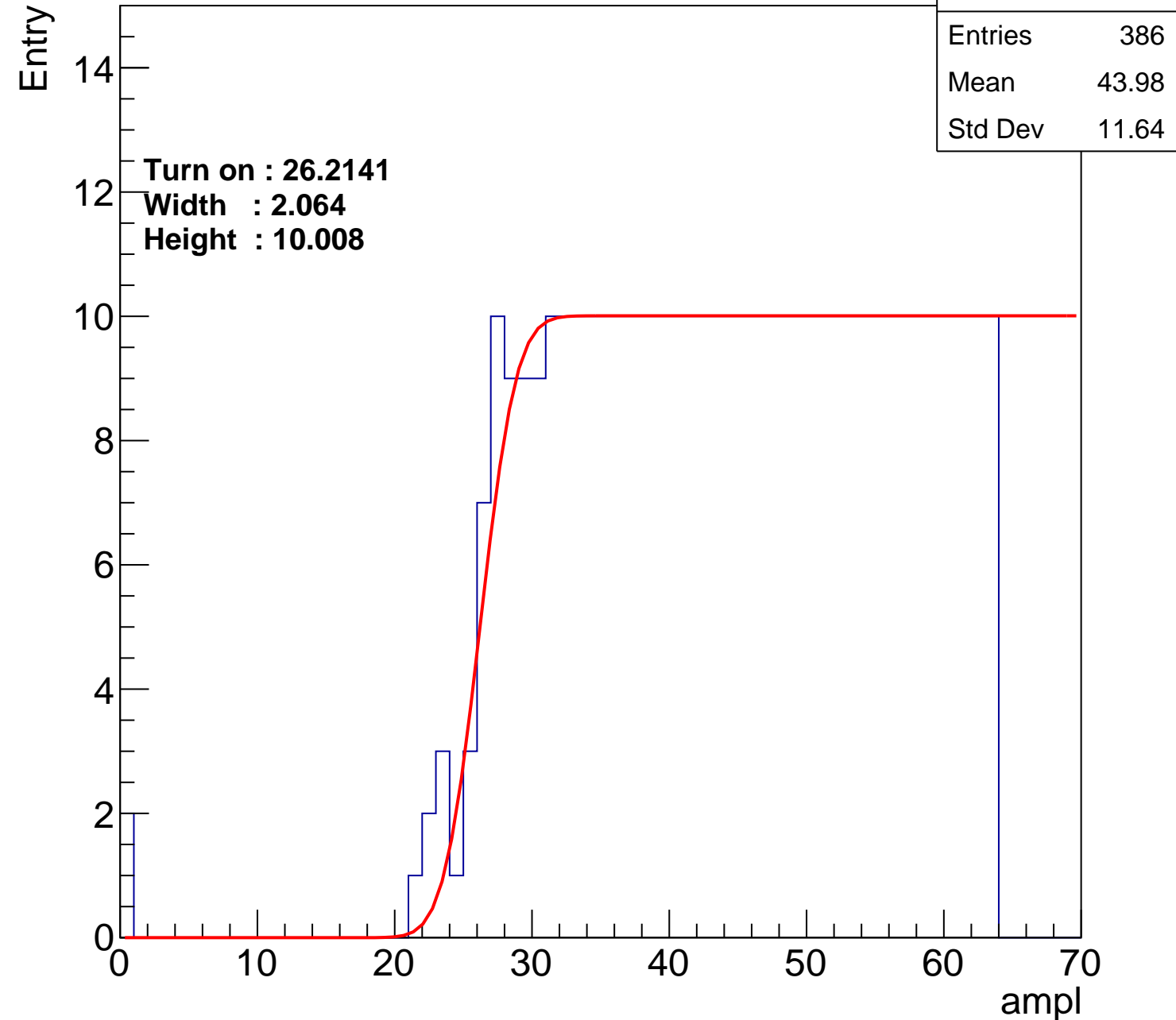
Width : 2.064

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.69
Std Dev	11.91

Turn on : 25.1709

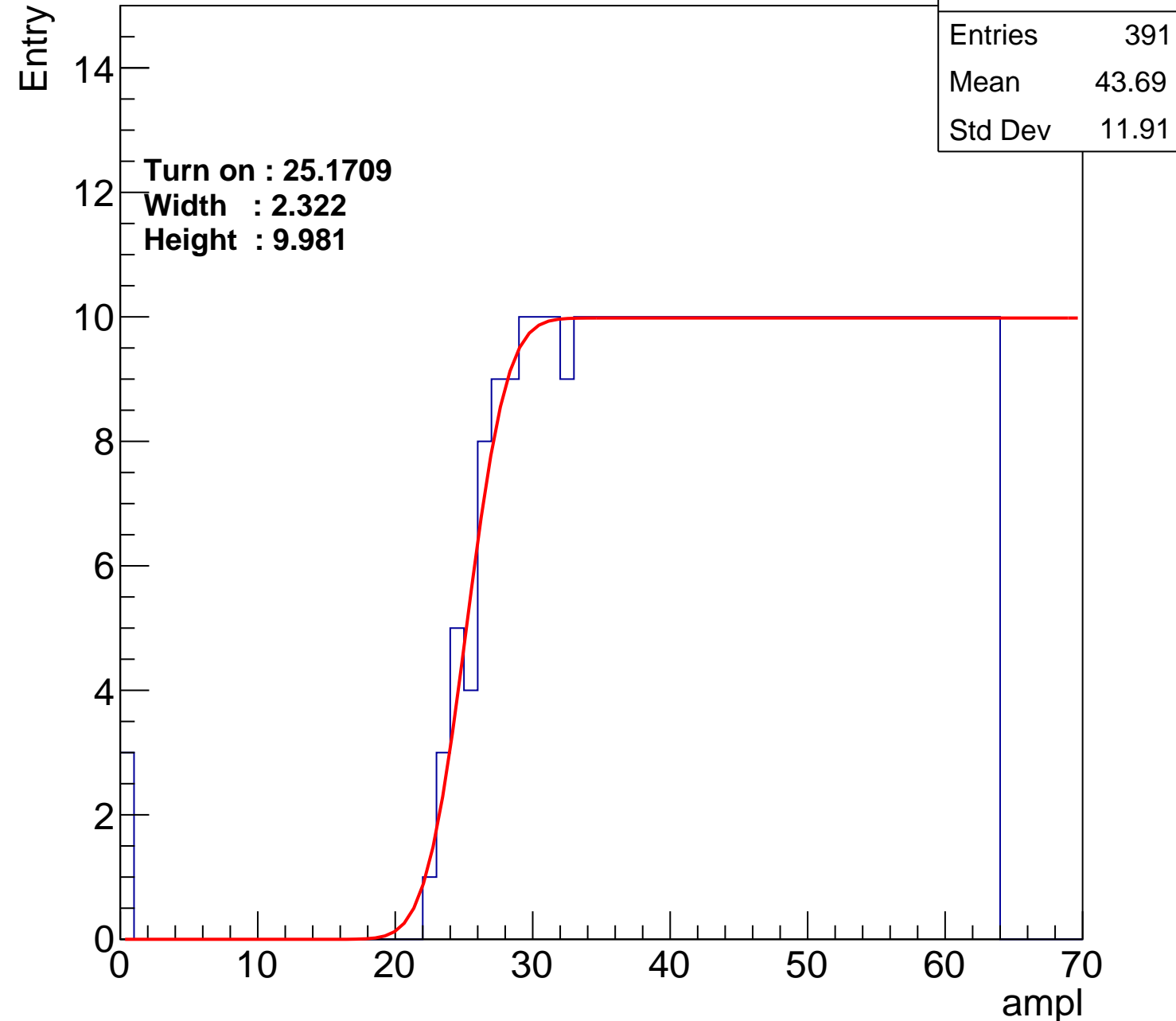
Width : 2.322

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.45
Std Dev	11.69

Turn on : 27.3744

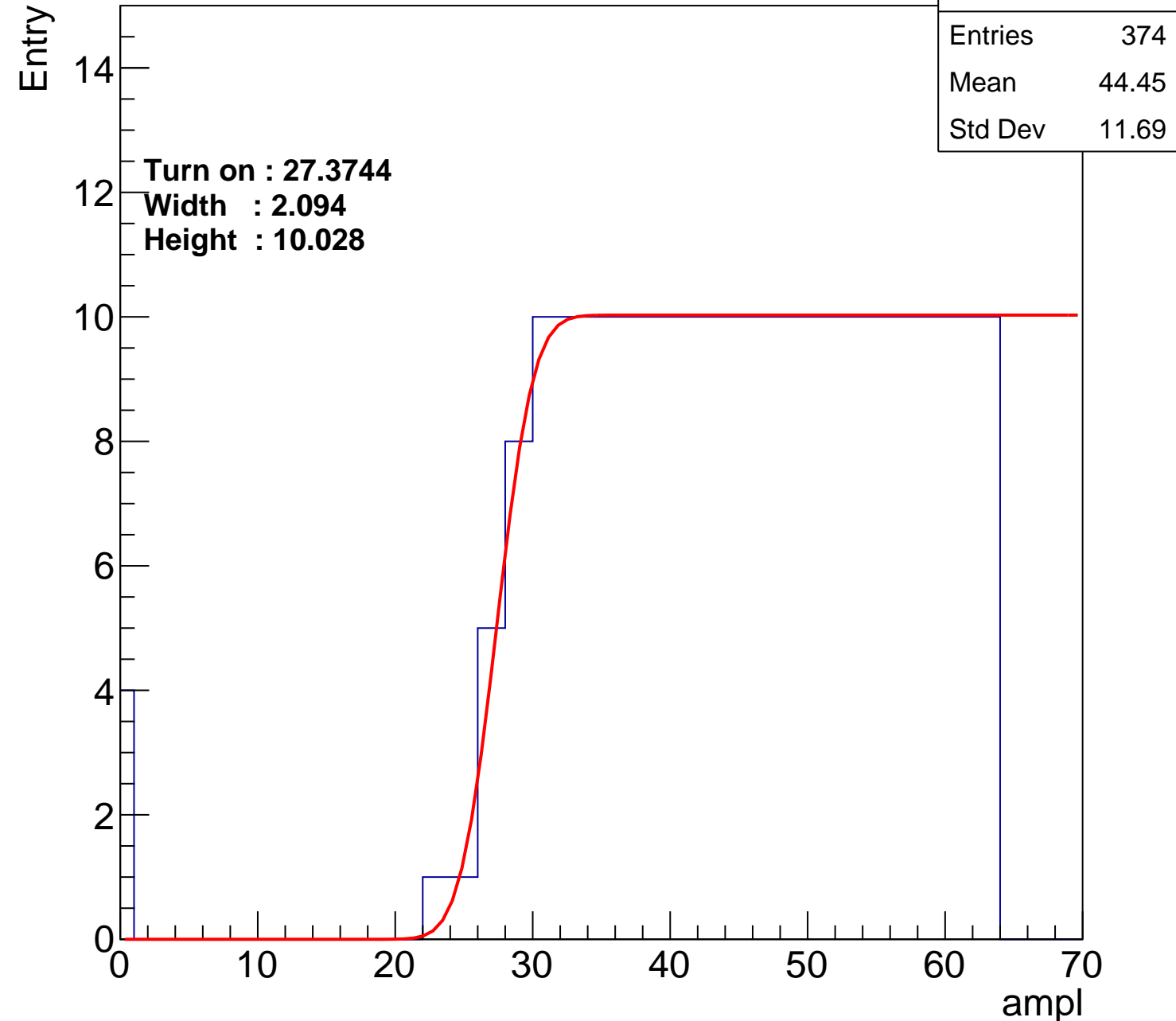
Width : 2.094

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.66
Std Dev	11.12

Turn on : 26.9731

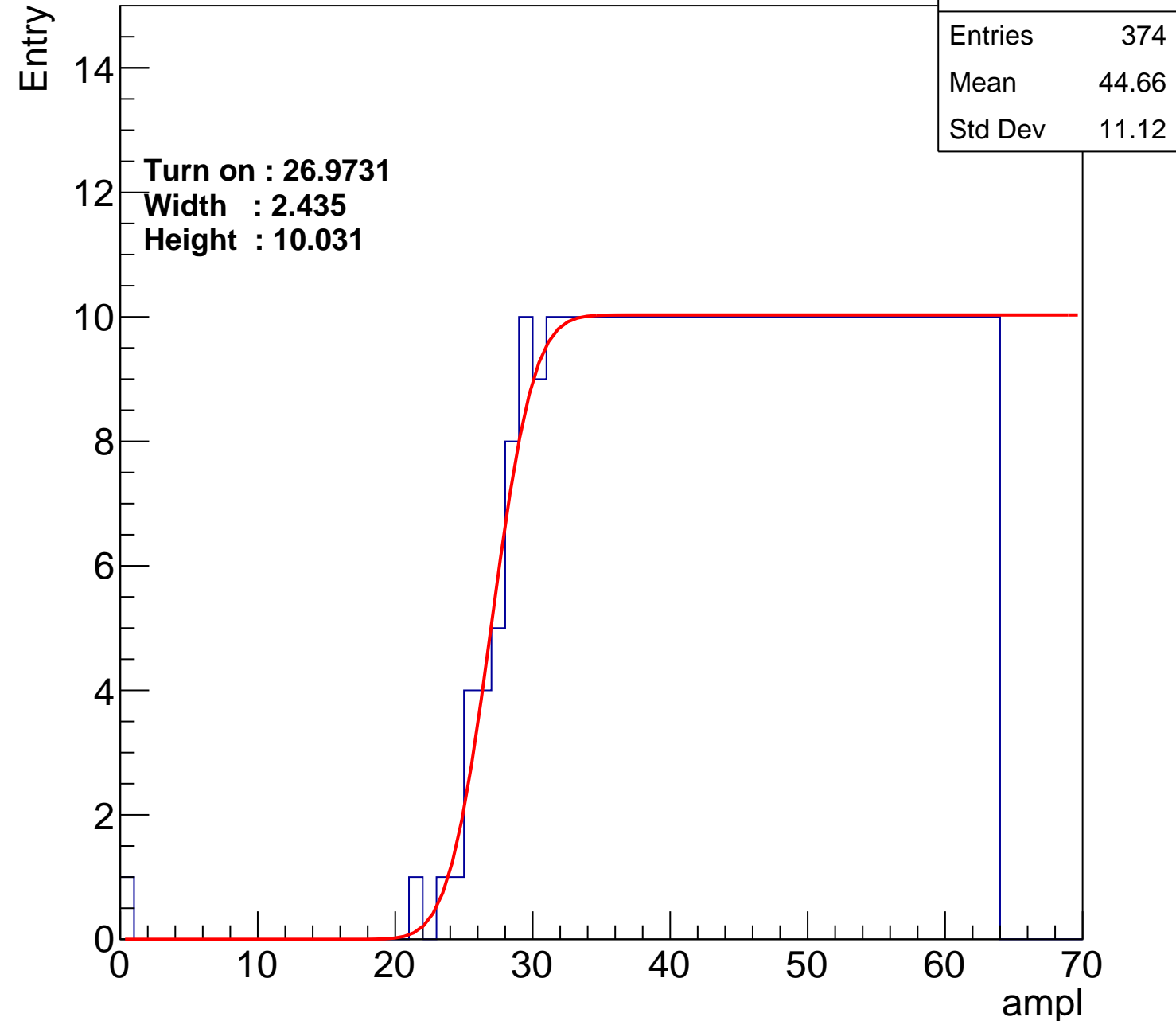
Width : 2.435

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.59

Turn on : 26.4564

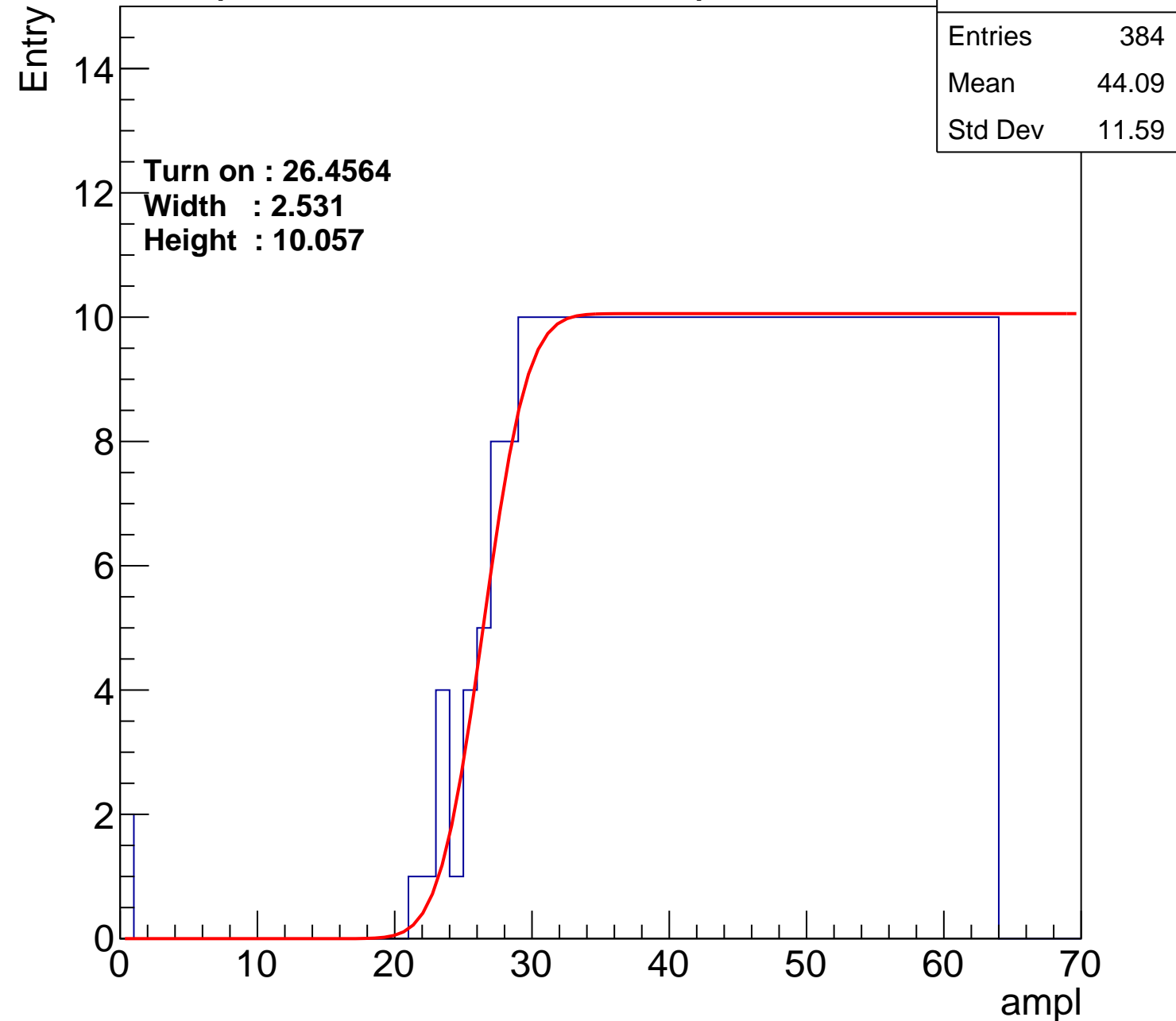
Width : 2.531

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.55
Std Dev	12.15

Turn on : 25.2638

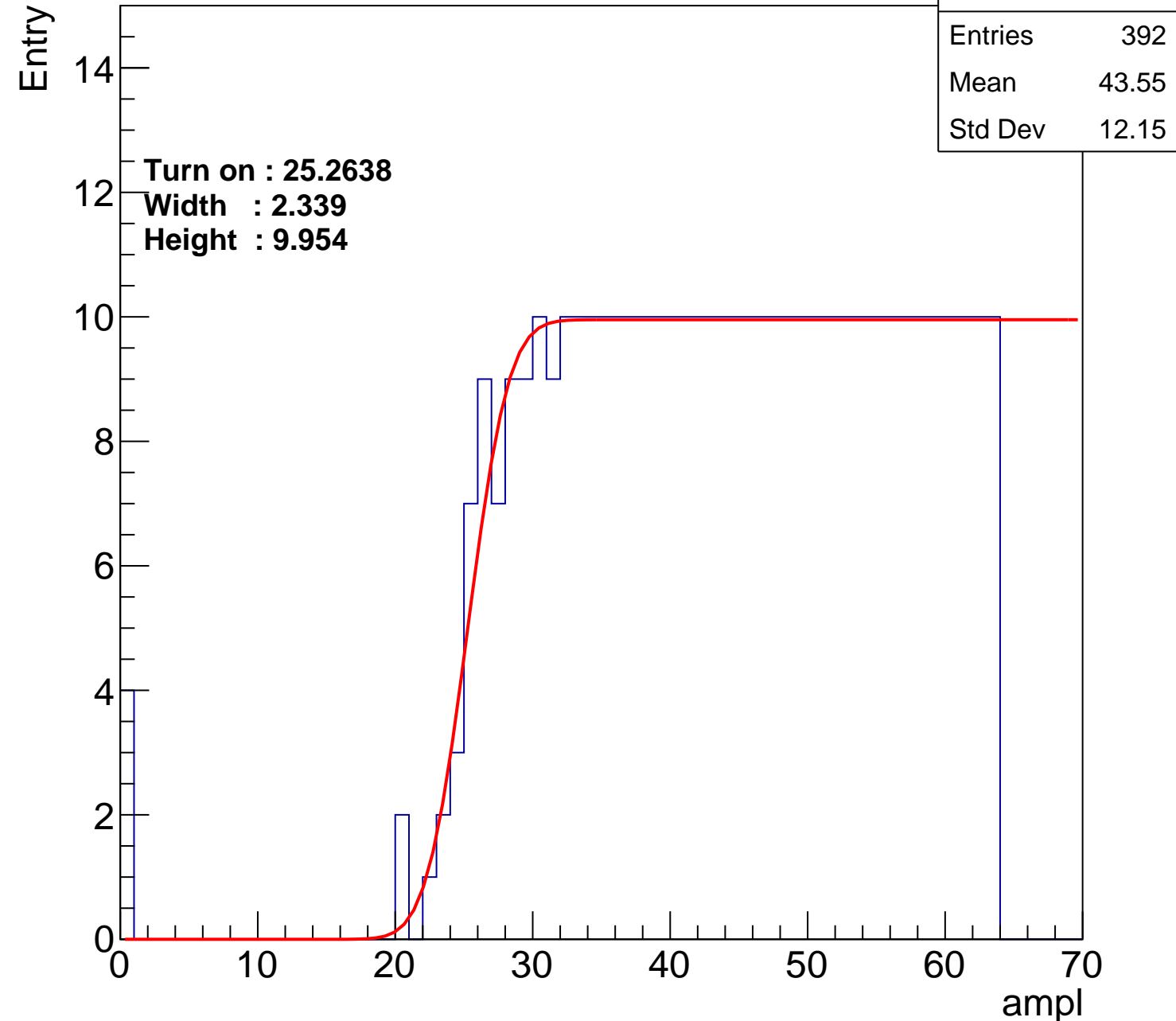
Width : 2.339

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.24
Std Dev	11.49

Turn on : 26.2720

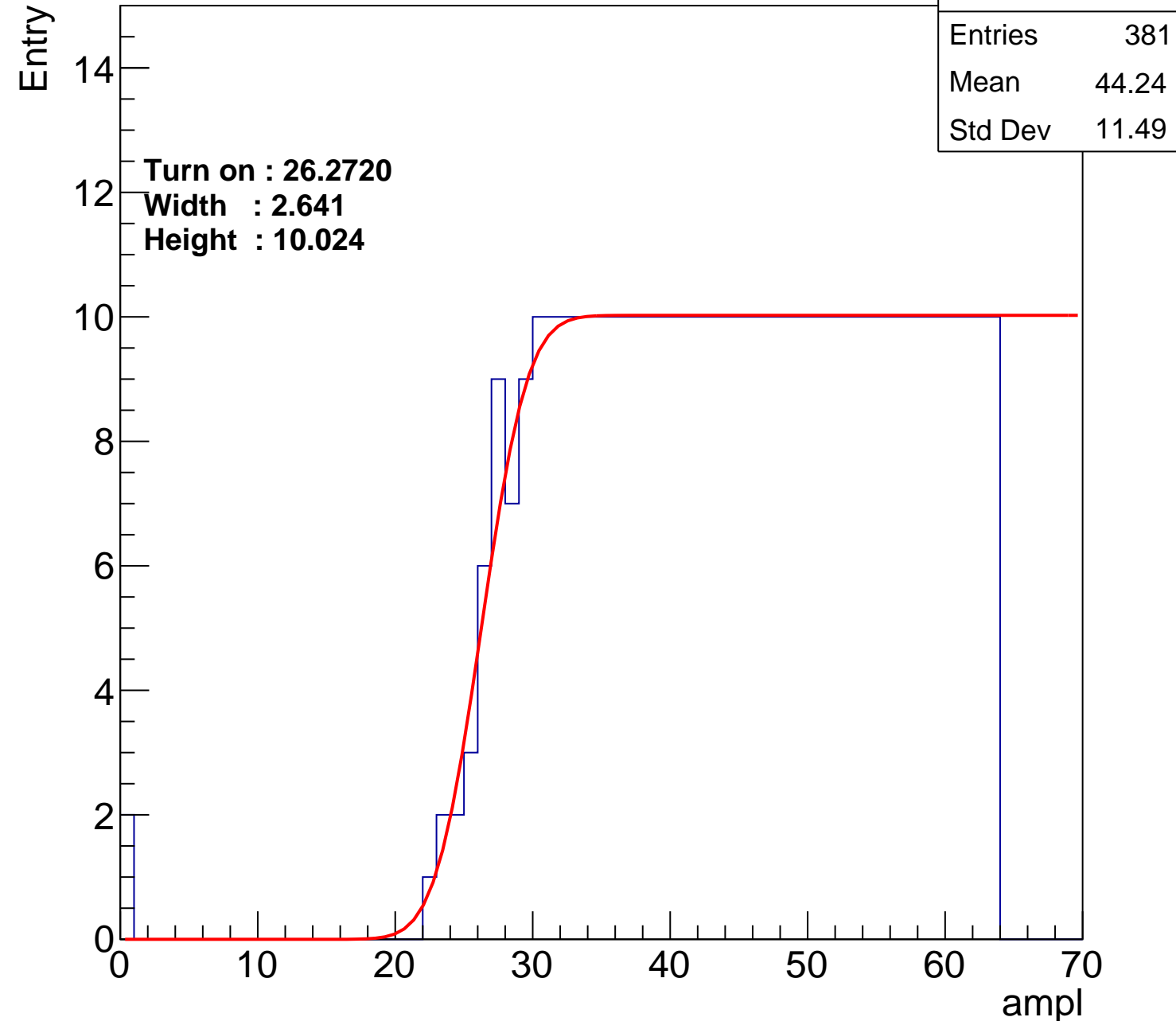
Width : 2.641

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.6683

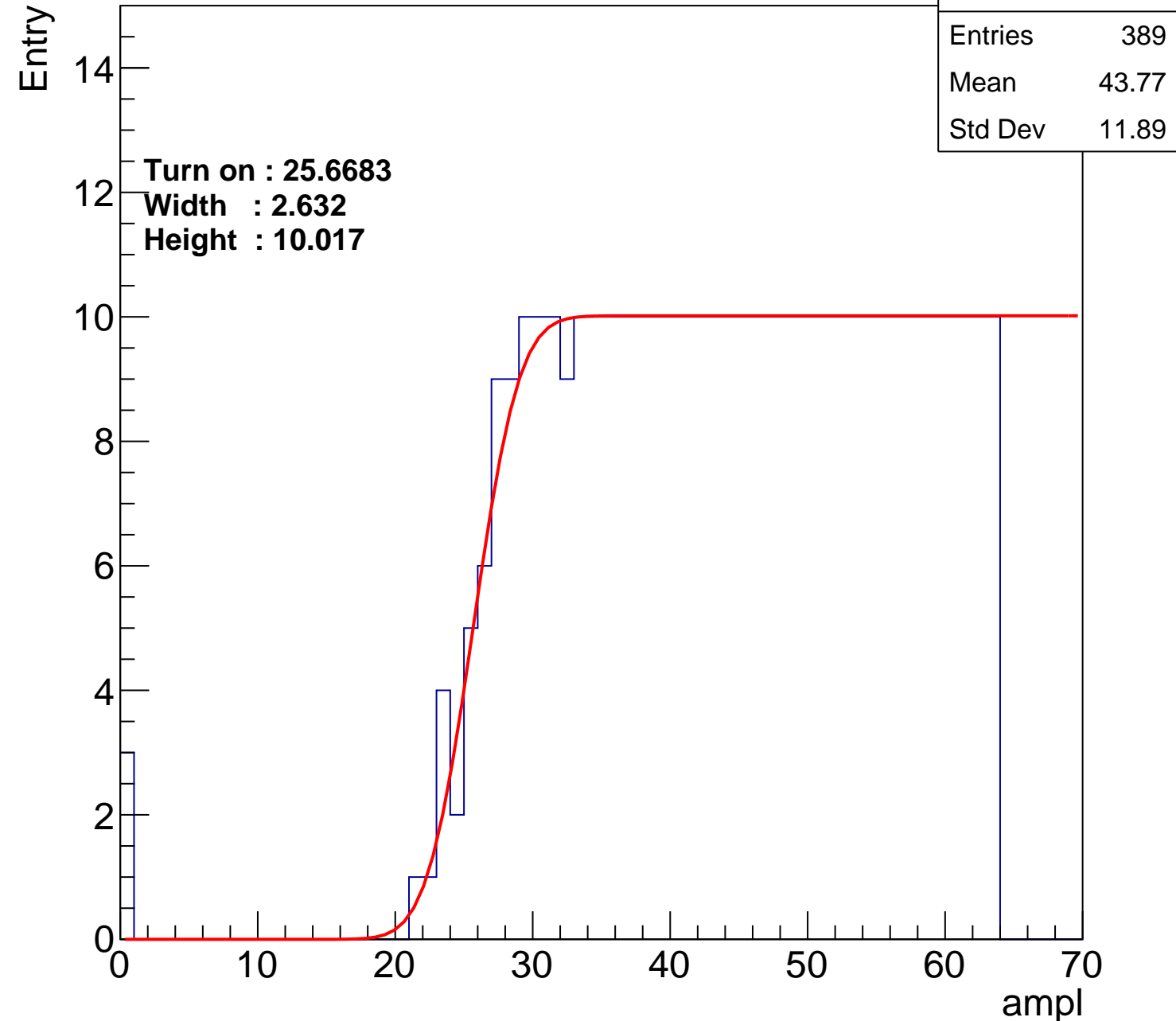
Width : 2.632

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.64
Std Dev	11.94

Turn on : 24.2768

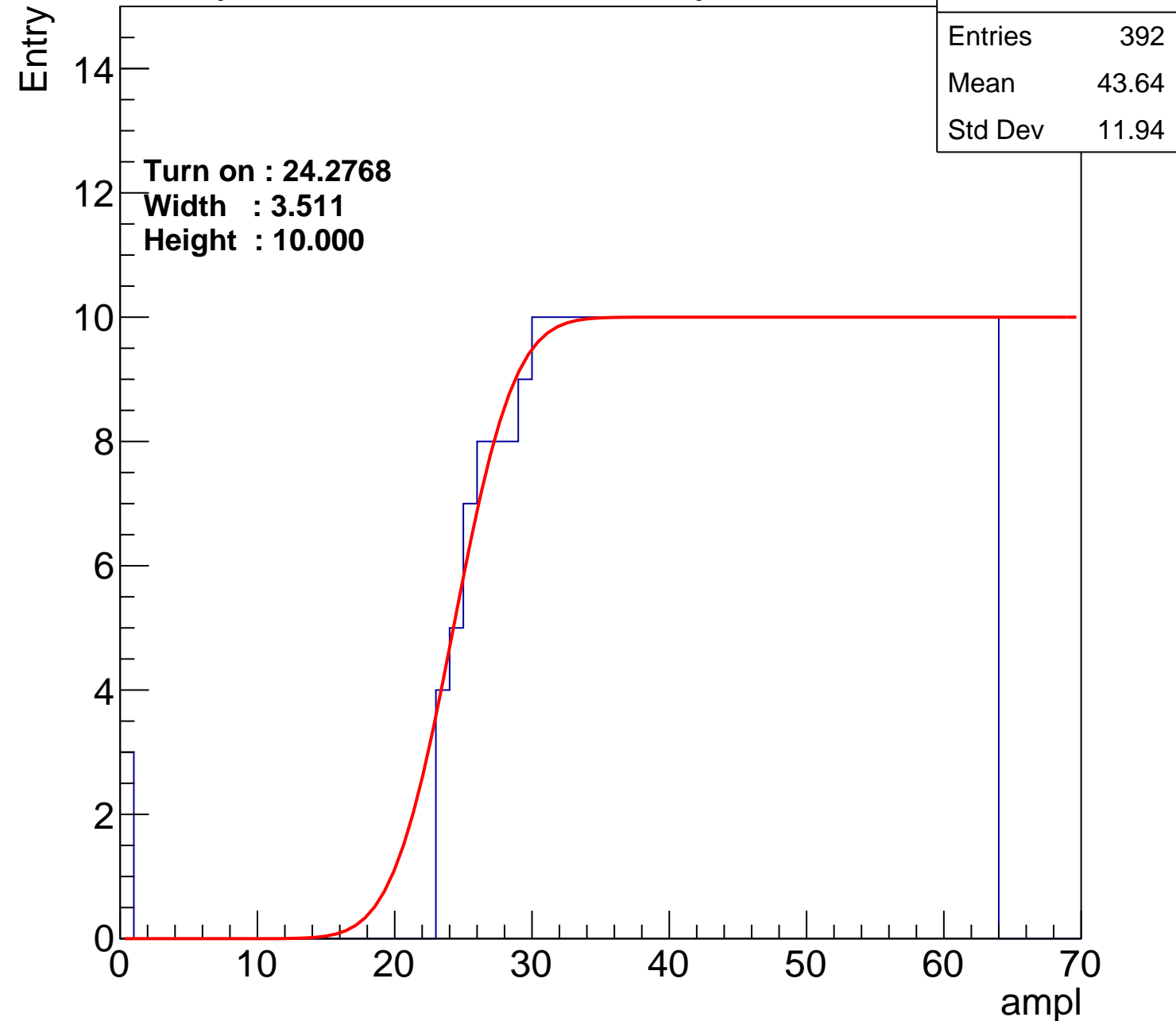
Width : 3.511

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.54
Std Dev	11.33

Turn on : 27.2707

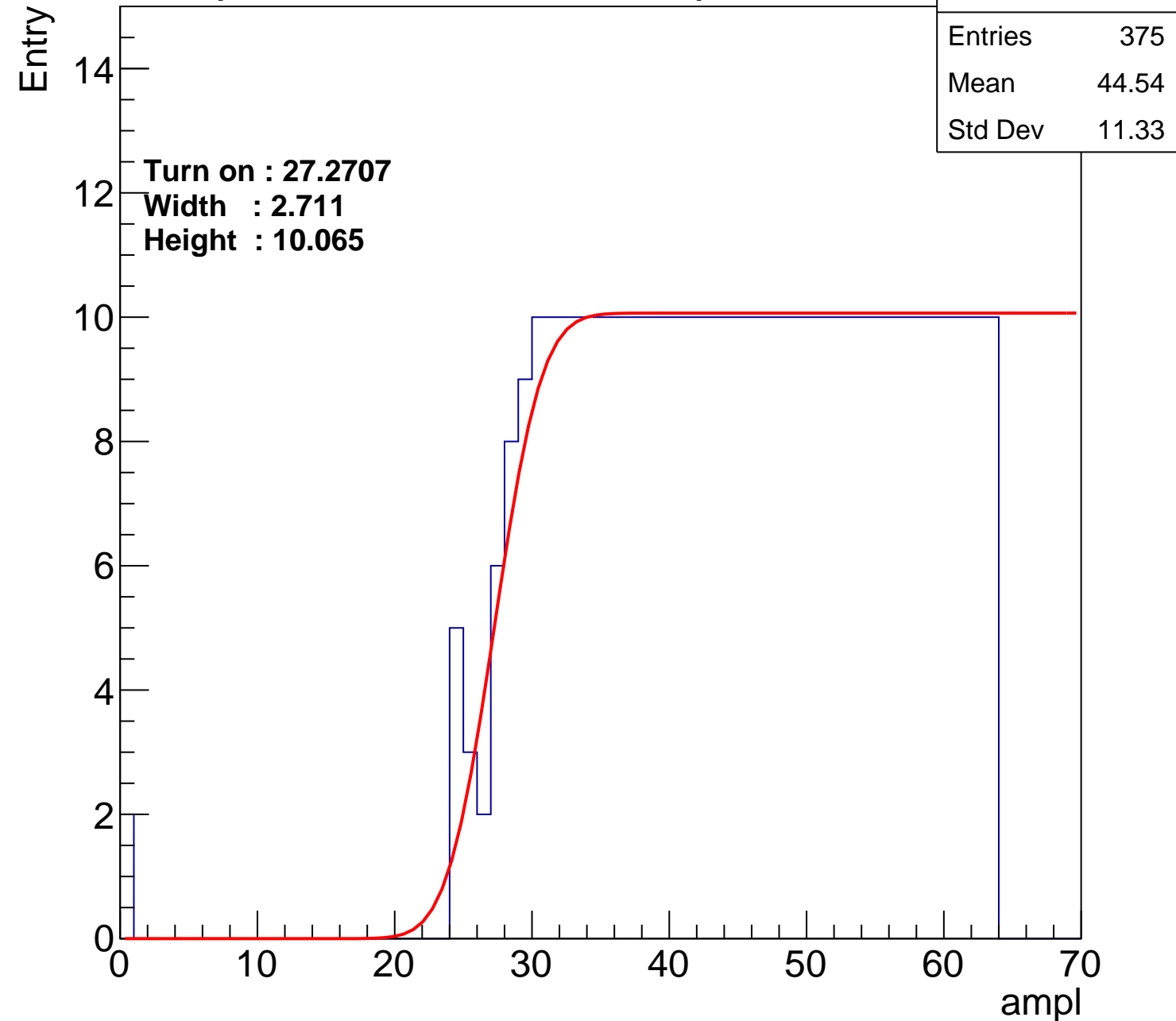
Width : 2.711

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch31

calib_packv5_042523_0143.root, FC#3, port B3

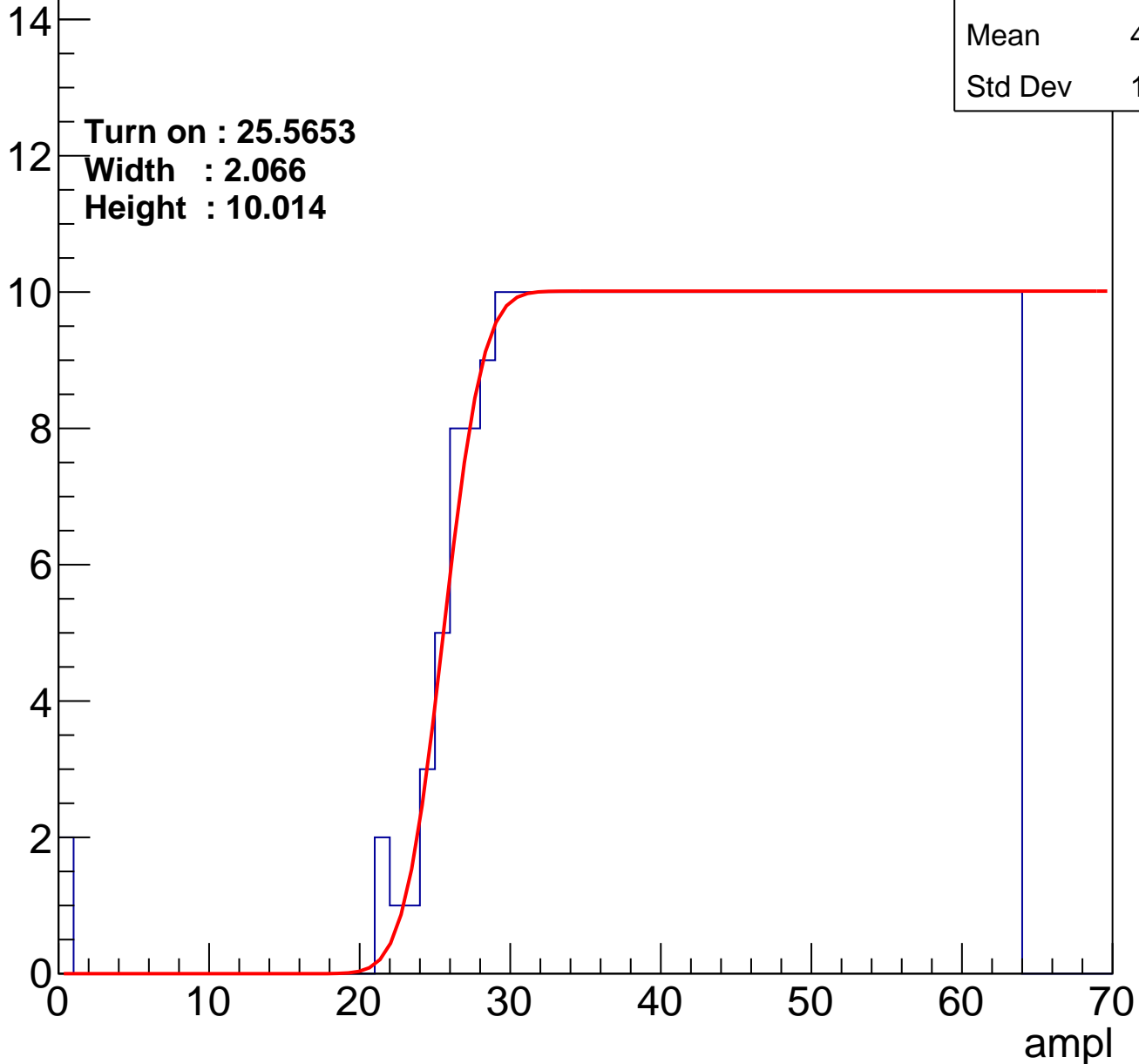
Entries	389
Mean	43.86
Std Dev	11.69

Turn on : 25.5653

Width : 2.066

Height : 10.014

Entry



B1L000S, U9-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.3
Std Dev	12.11

Turn on : 24.5246

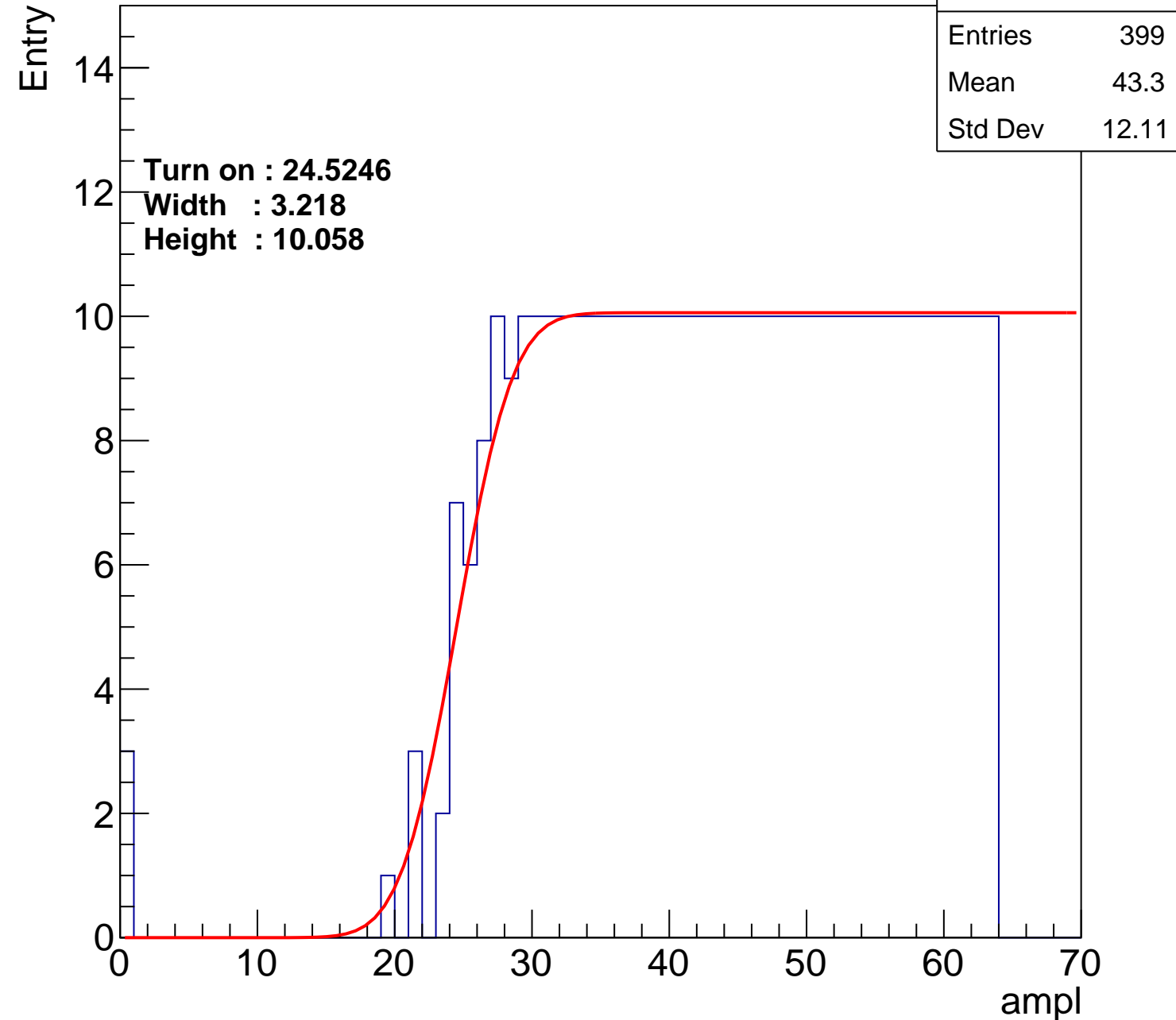
Width : 3.218

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	411
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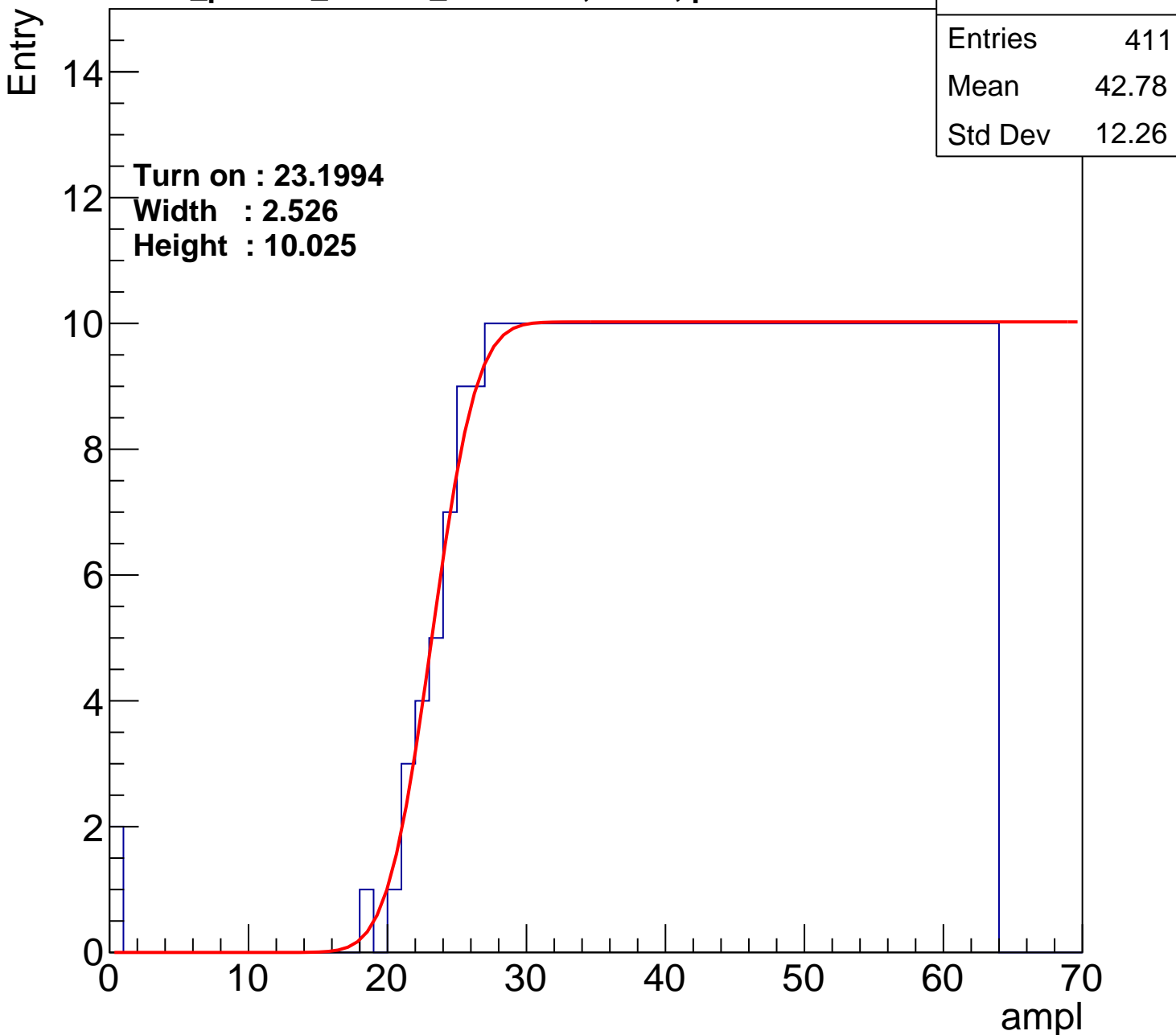
Mean	42.78
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Std Dev	12.26
---------	-------

Turn on : 23.1994

Width : 2.526

Height : 10.025



B1L000S, U9-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.9510

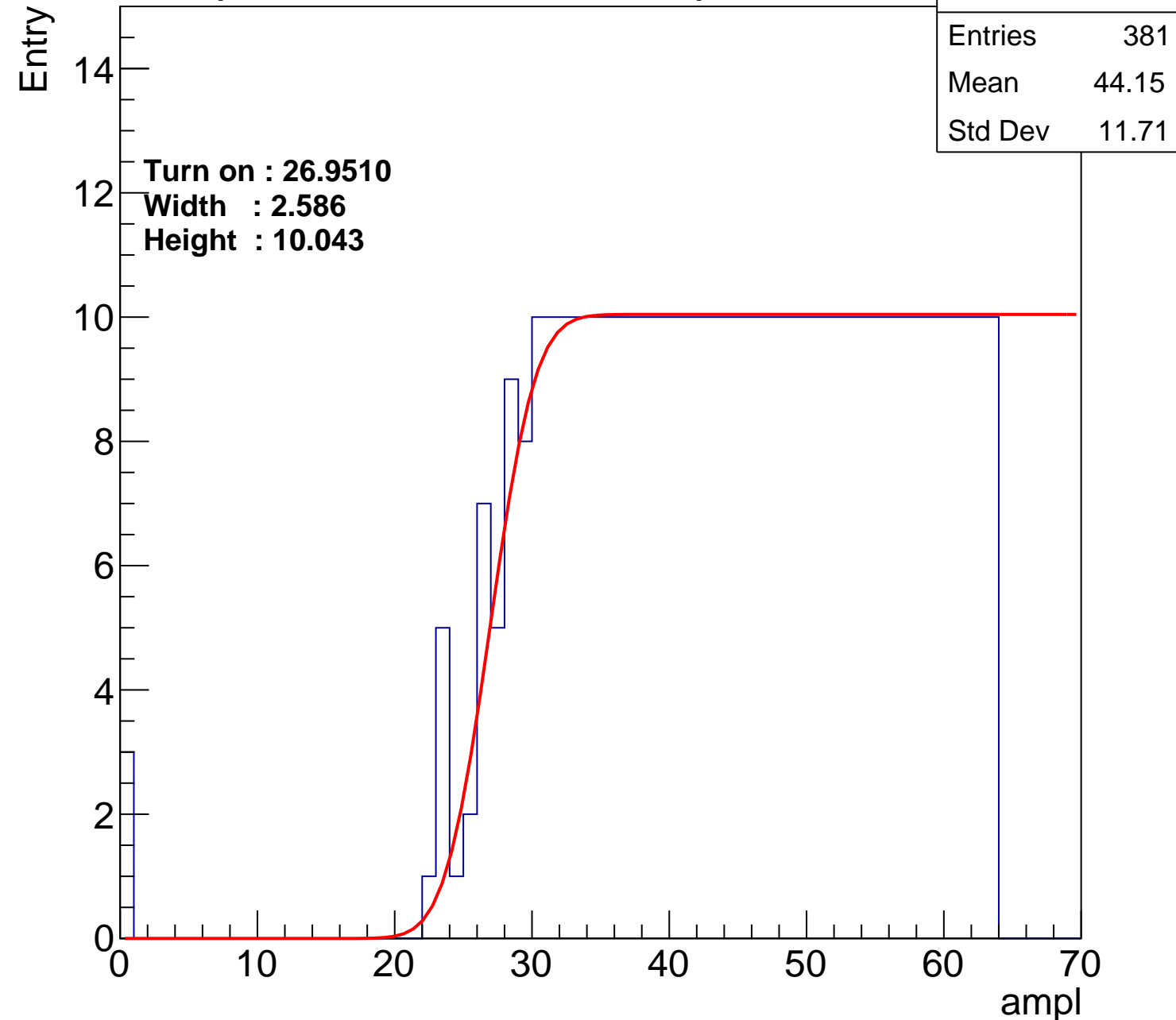
Width : 2.586

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.92
Std Dev	11.64

Turn on : 25.4295

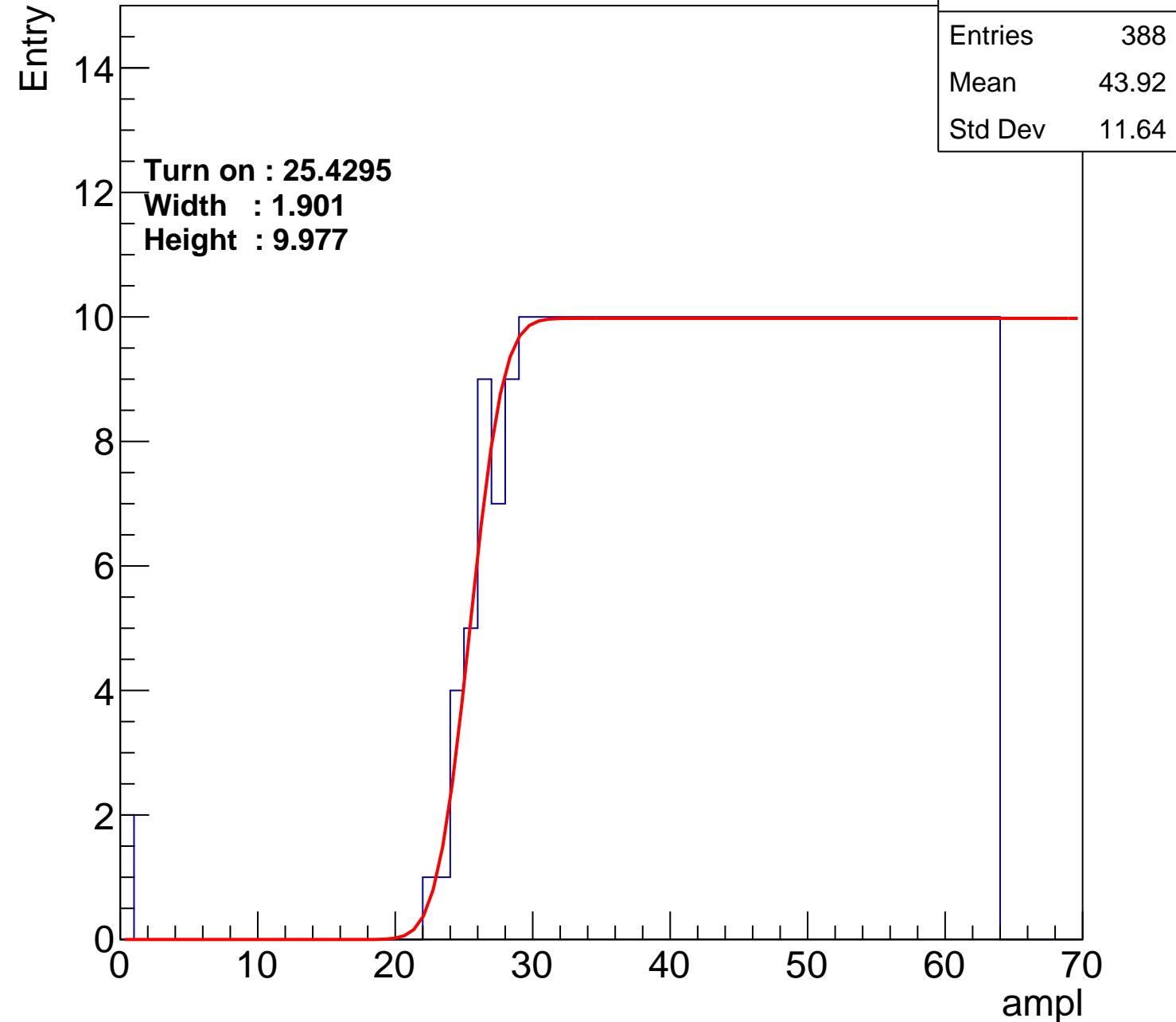
Width : 1.901

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.53
Std Dev	12.12

Turn on : 25.0994

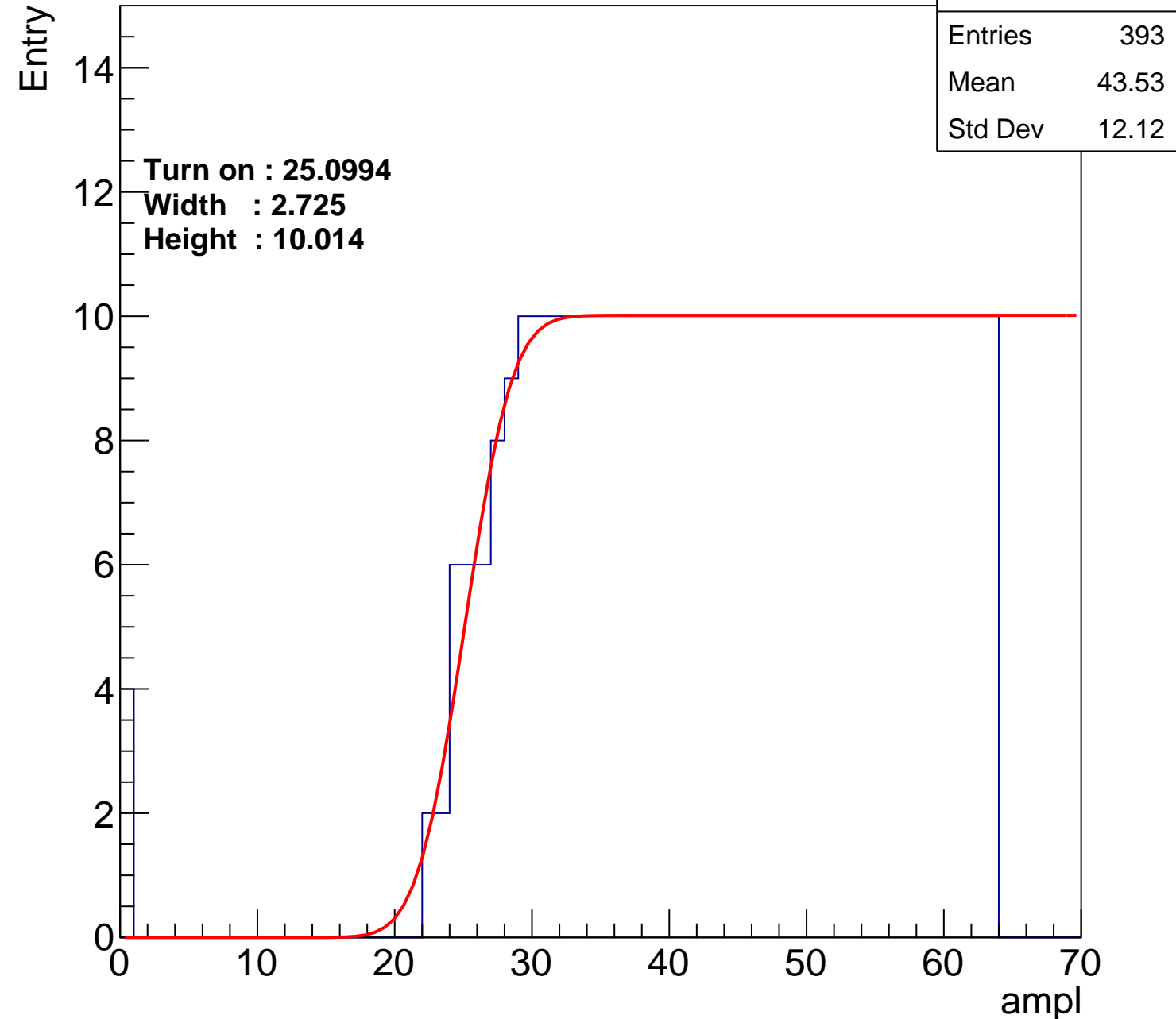
Width : 2.725

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.86
Std Dev	11.67

Turn on : 24.8258

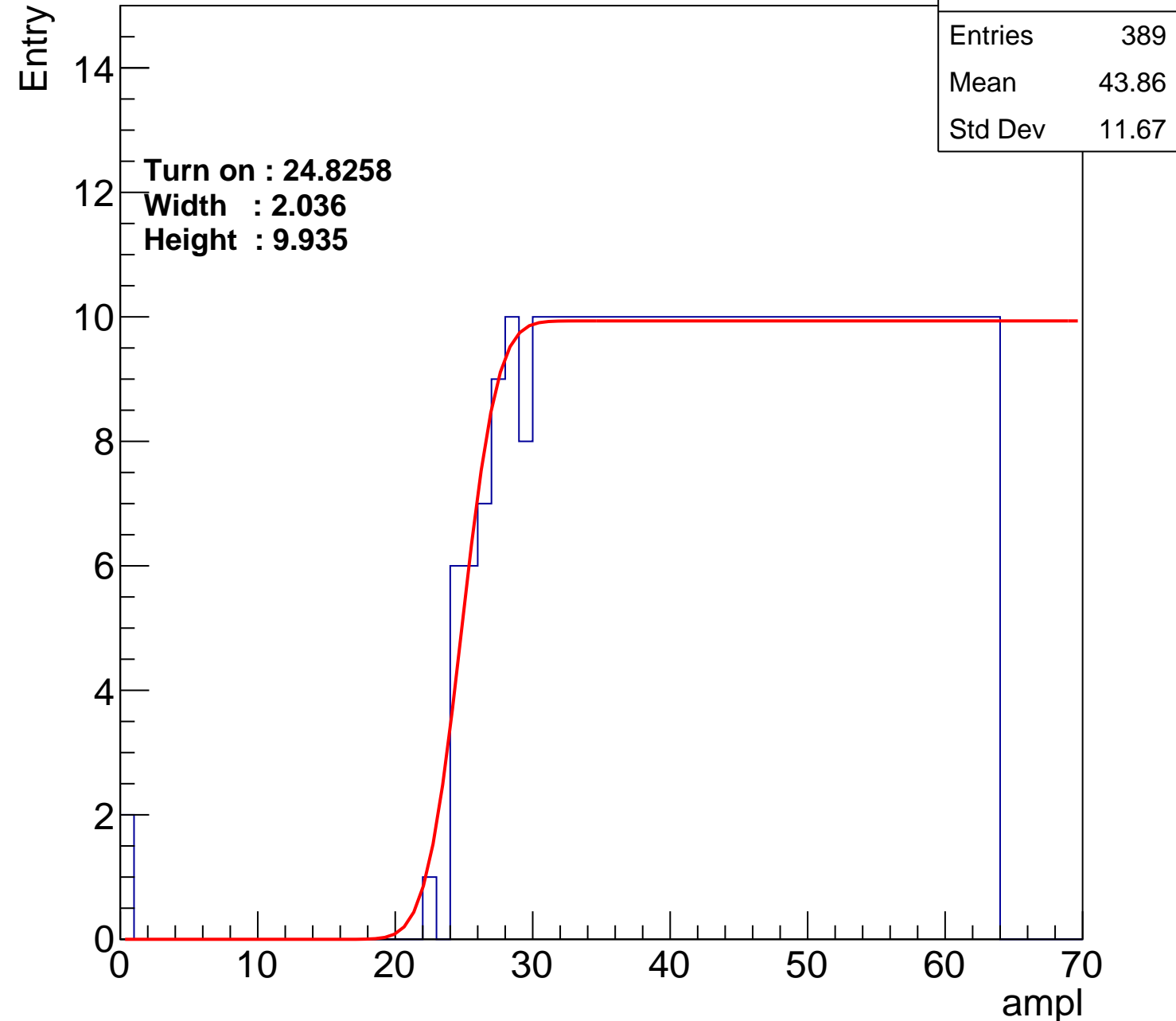
Width : 2.036

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.65
Std Dev	12.1

Turn on : 25.3597

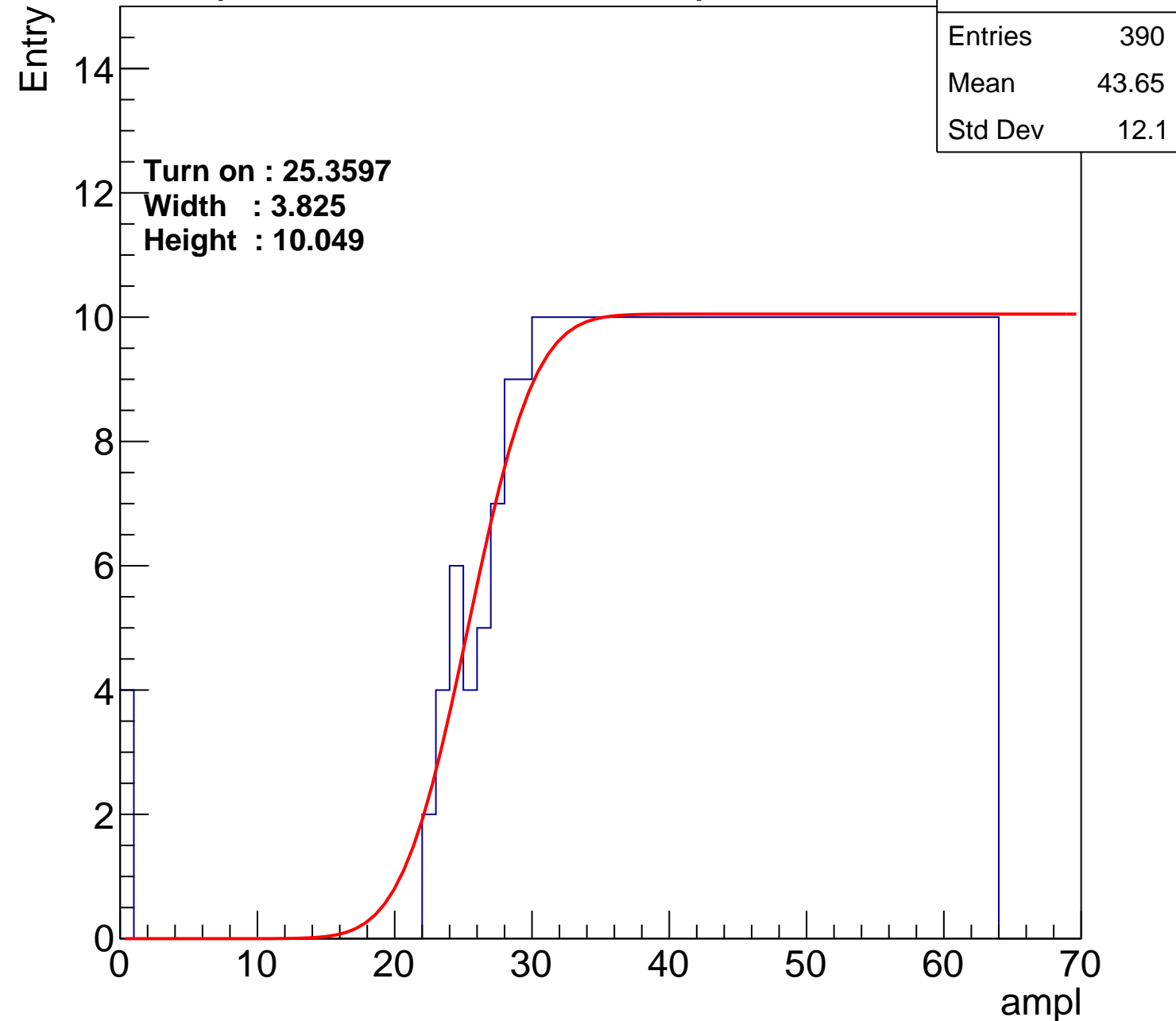
Width : 3.825

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch39

calib_packv5_042523_0143.root, FC#3, port B3

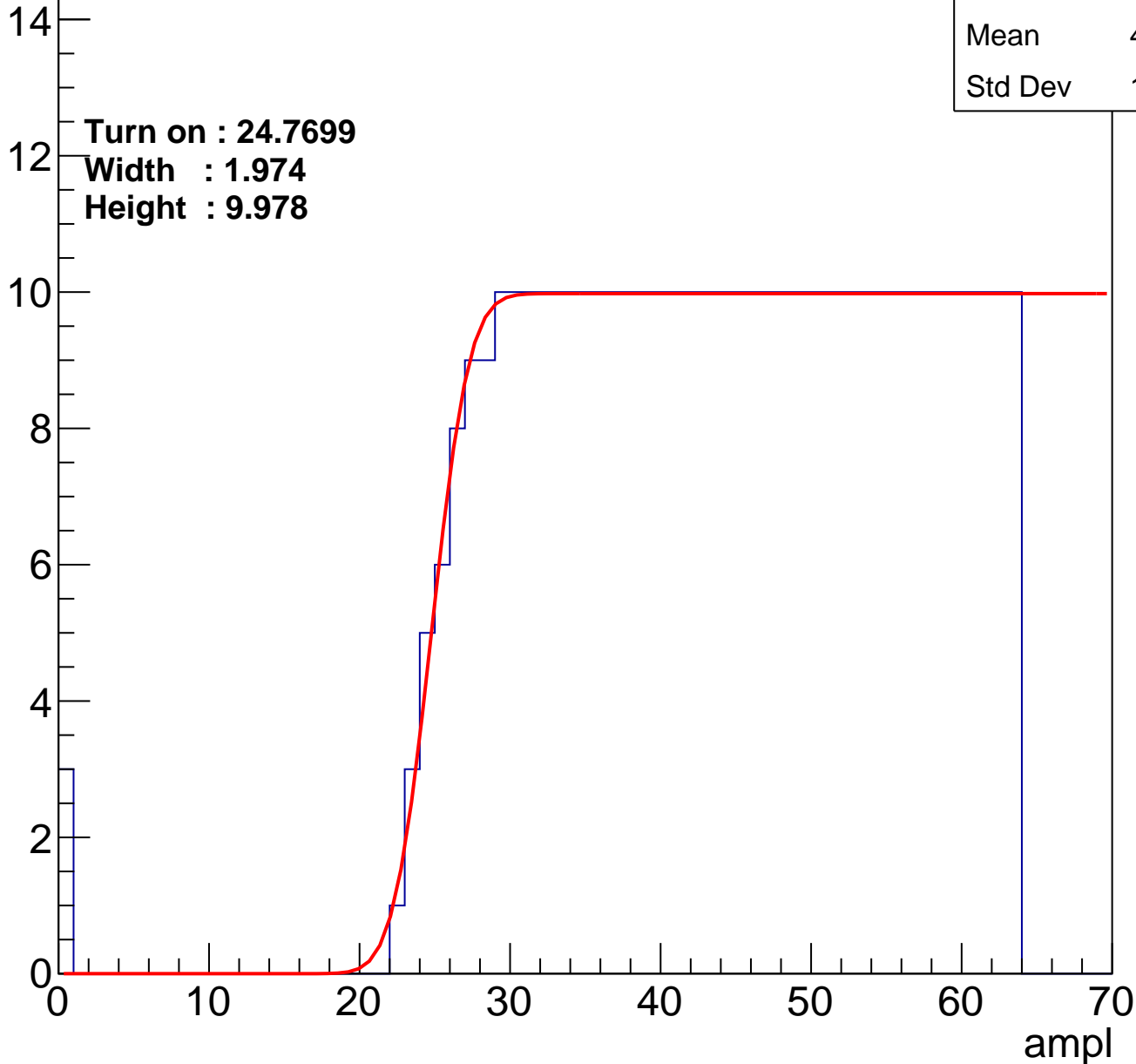
Entries	394
Mean	43.56
Std Dev	11.95

Turn on : 24.7699

Width : 1.974

Height : 9.978

Entry



B1L000S, U9-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.73
Std Dev	11.9

Turn on : 25.3321

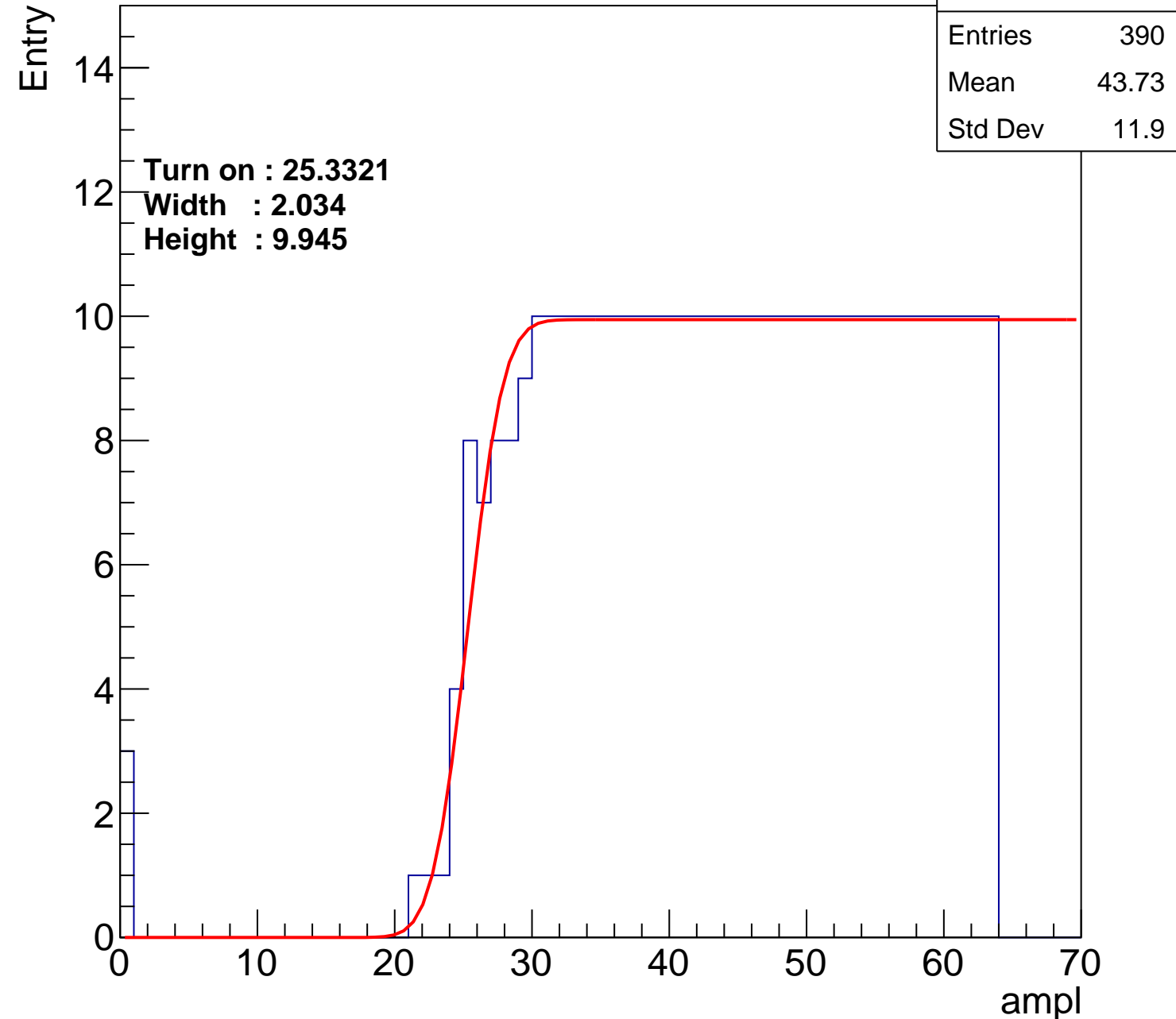
Width : 2.034

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch41

calib_packv5_042523_0143.root, FC#3, port B3

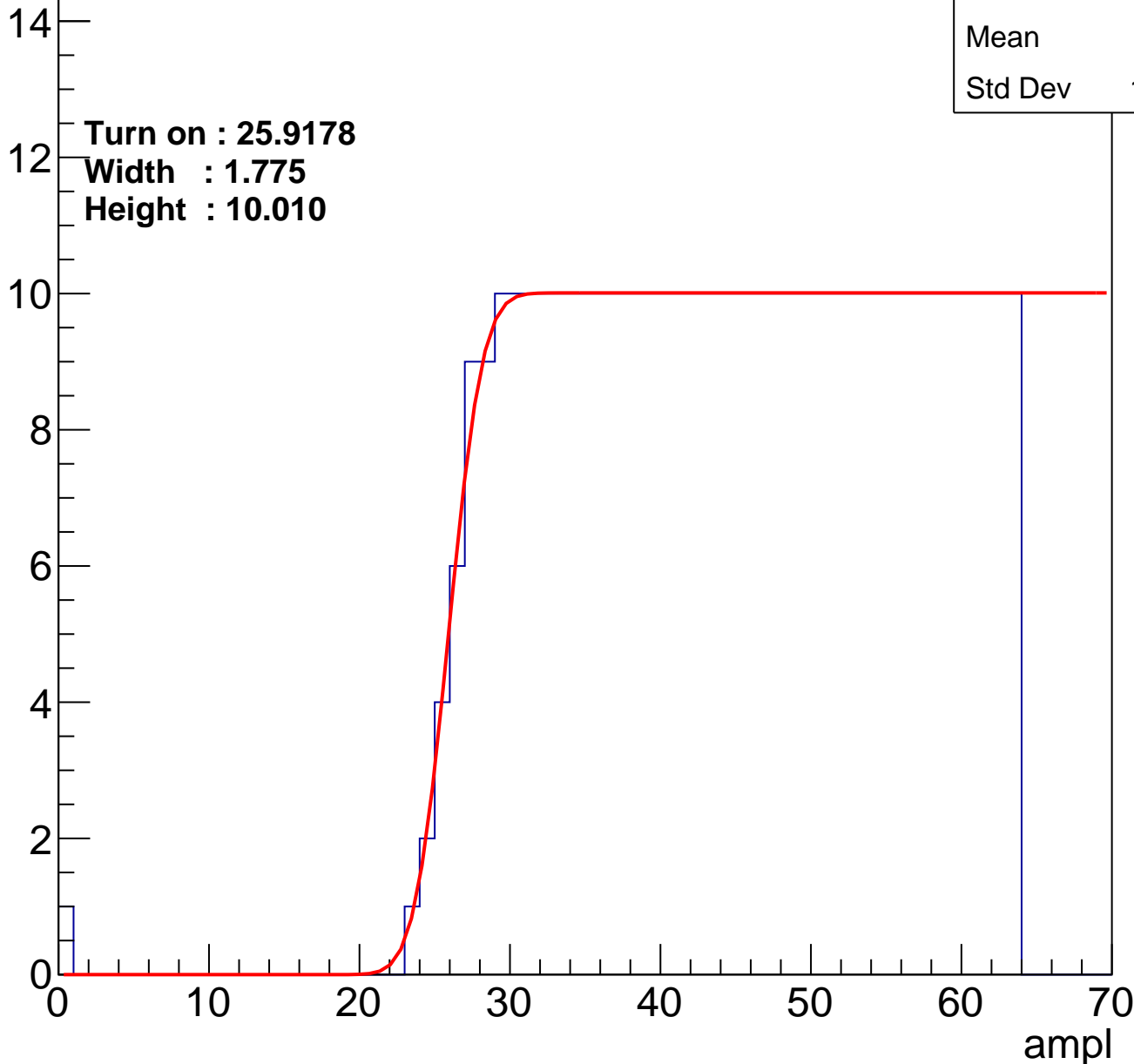
Entries	382
Mean	44.3
Std Dev	11.27

Turn on : 25.9178

Width : 1.775

Height : 10.010

Entry



B1L000S, U9-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.98
Std Dev	11.95

Turn on : 26.1415

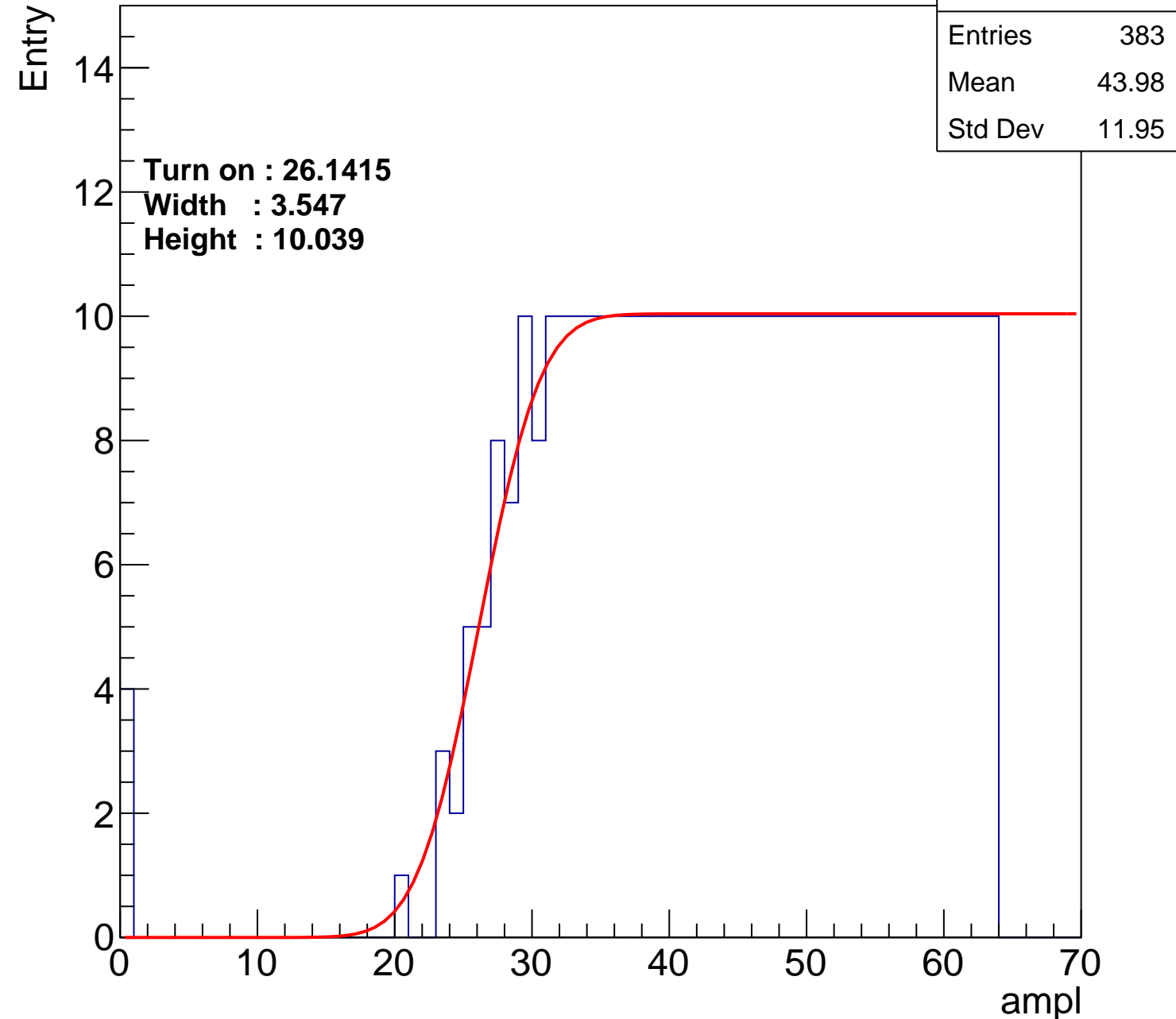
Width : 3.547

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.4
Std Dev	11.39

Turn on : 26.6088

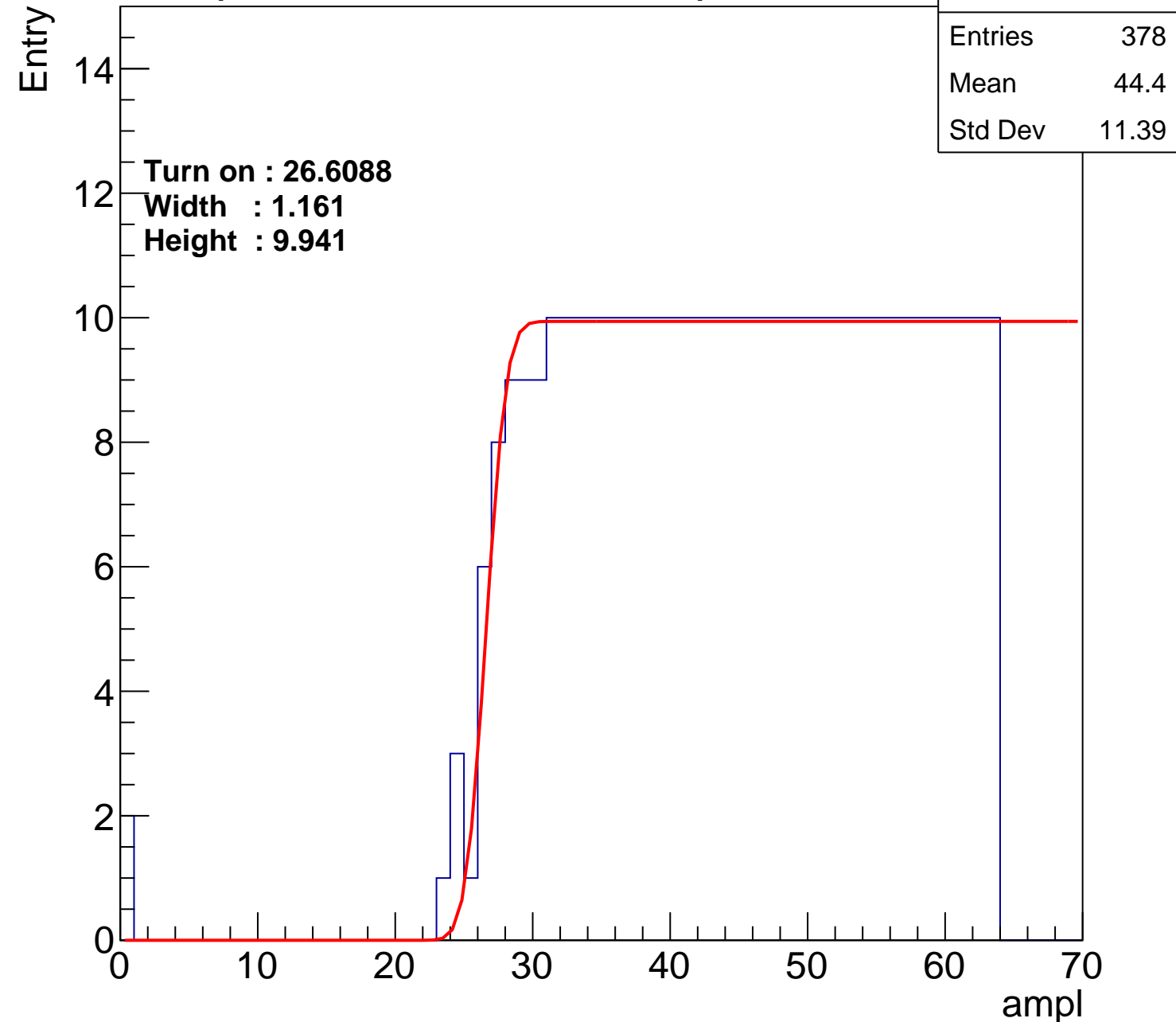
Width : 1.161

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch44

calib_packv5_042523_0143.root, FC#3, port B3

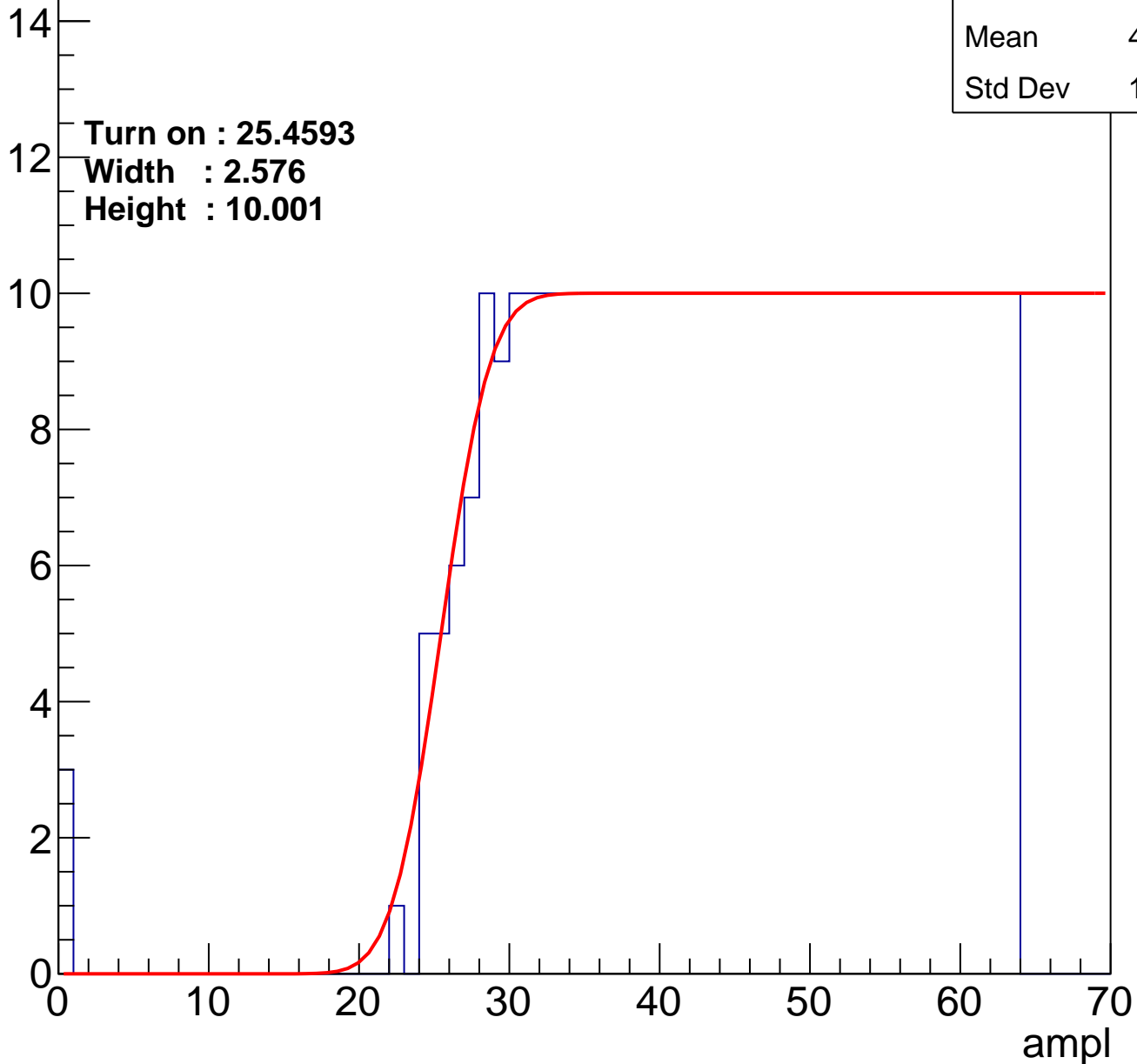
Entries	386
Mean	43.95
Std Dev	11.77

Turn on : 25.4593

Width : 2.576

Height : 10.001

Entry



B1L000S, U9-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.73
Std Dev	11.61

Turn on : 24.4537

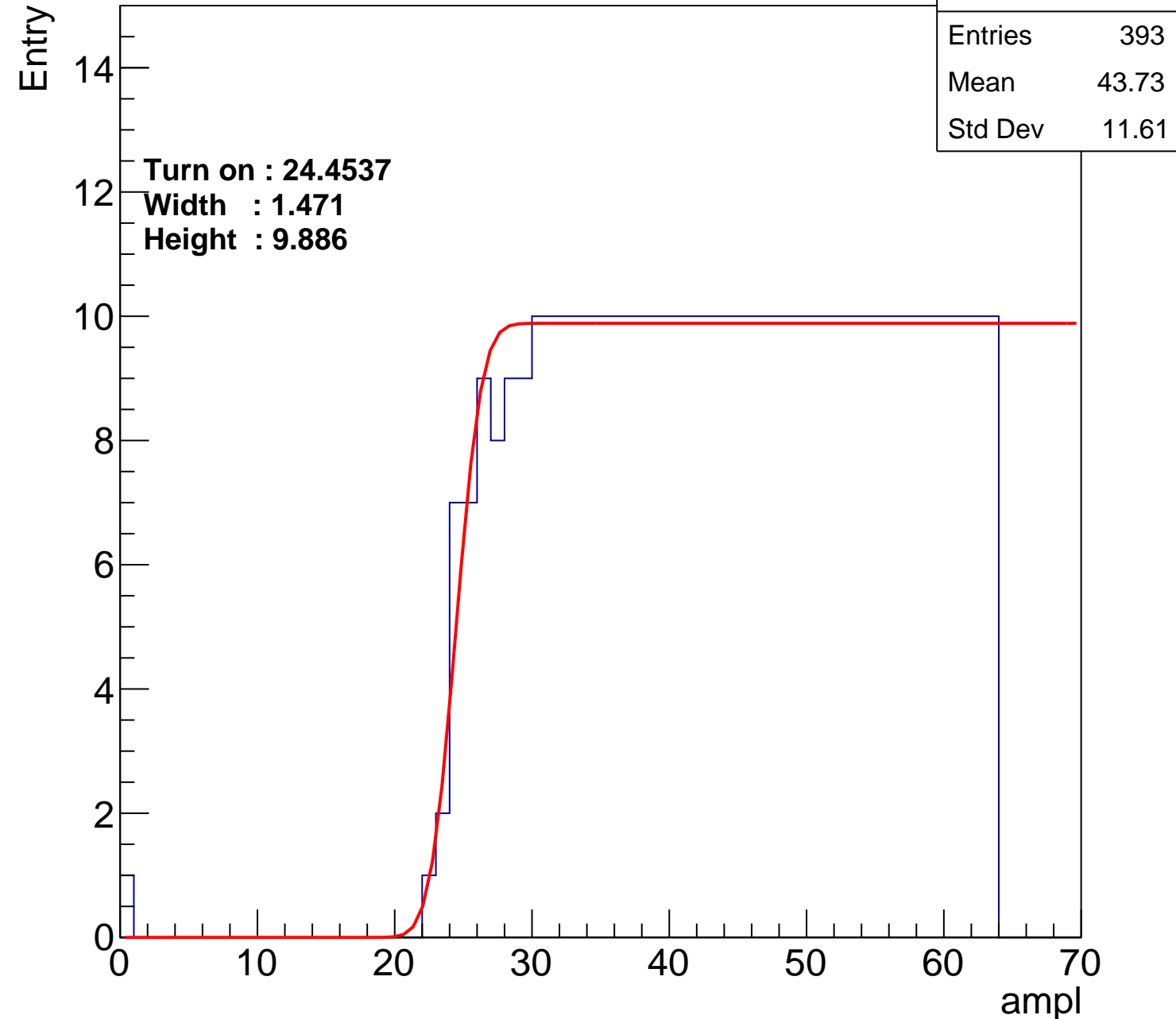
Width : 1.471

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.0891

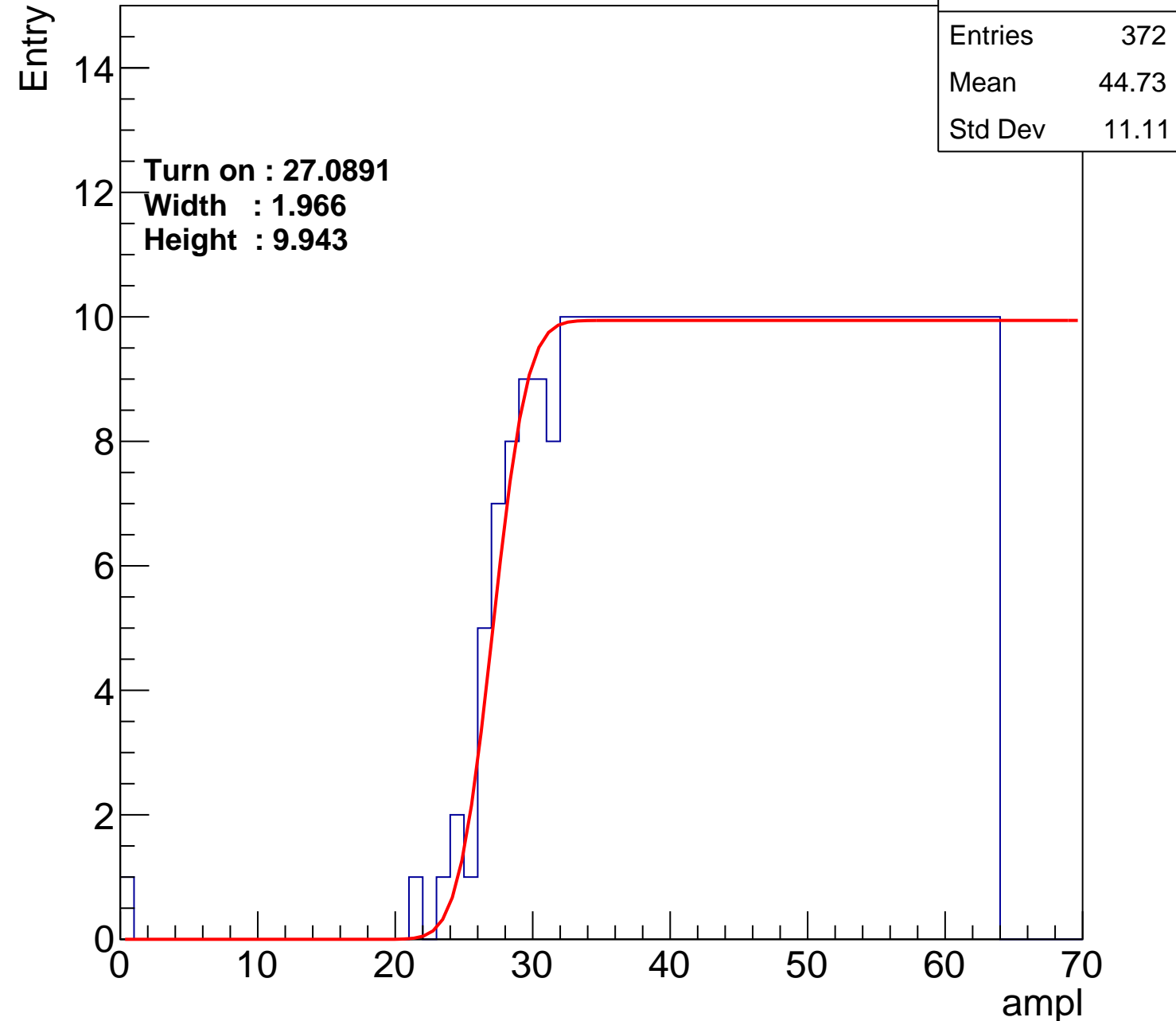
Width : 1.966

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	356
Mean	45.5
Std Dev	10.72

Turn on : 29.1754

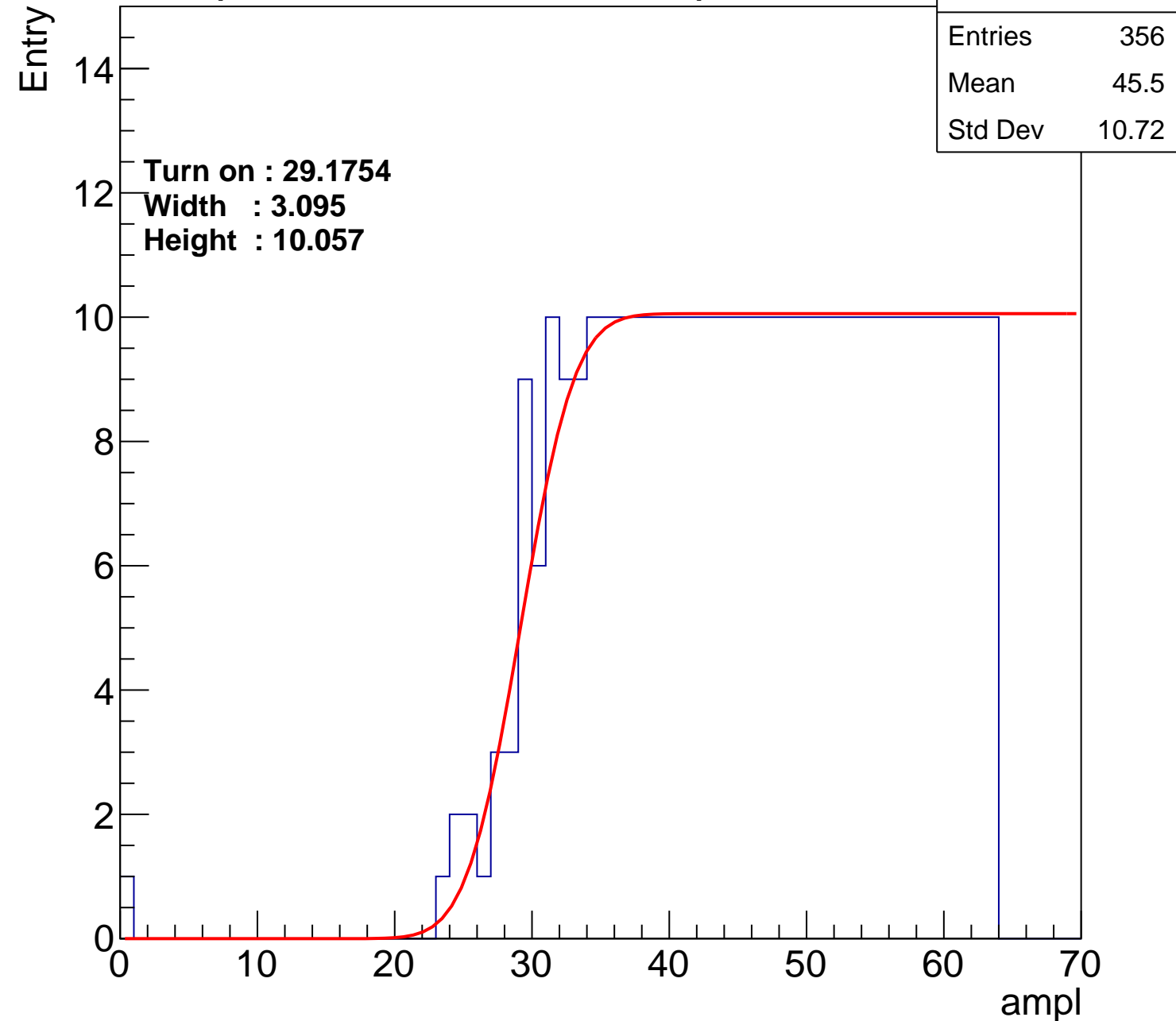
Width : 3.095

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.56
Std Dev	12.13

Turn on : 25.5044

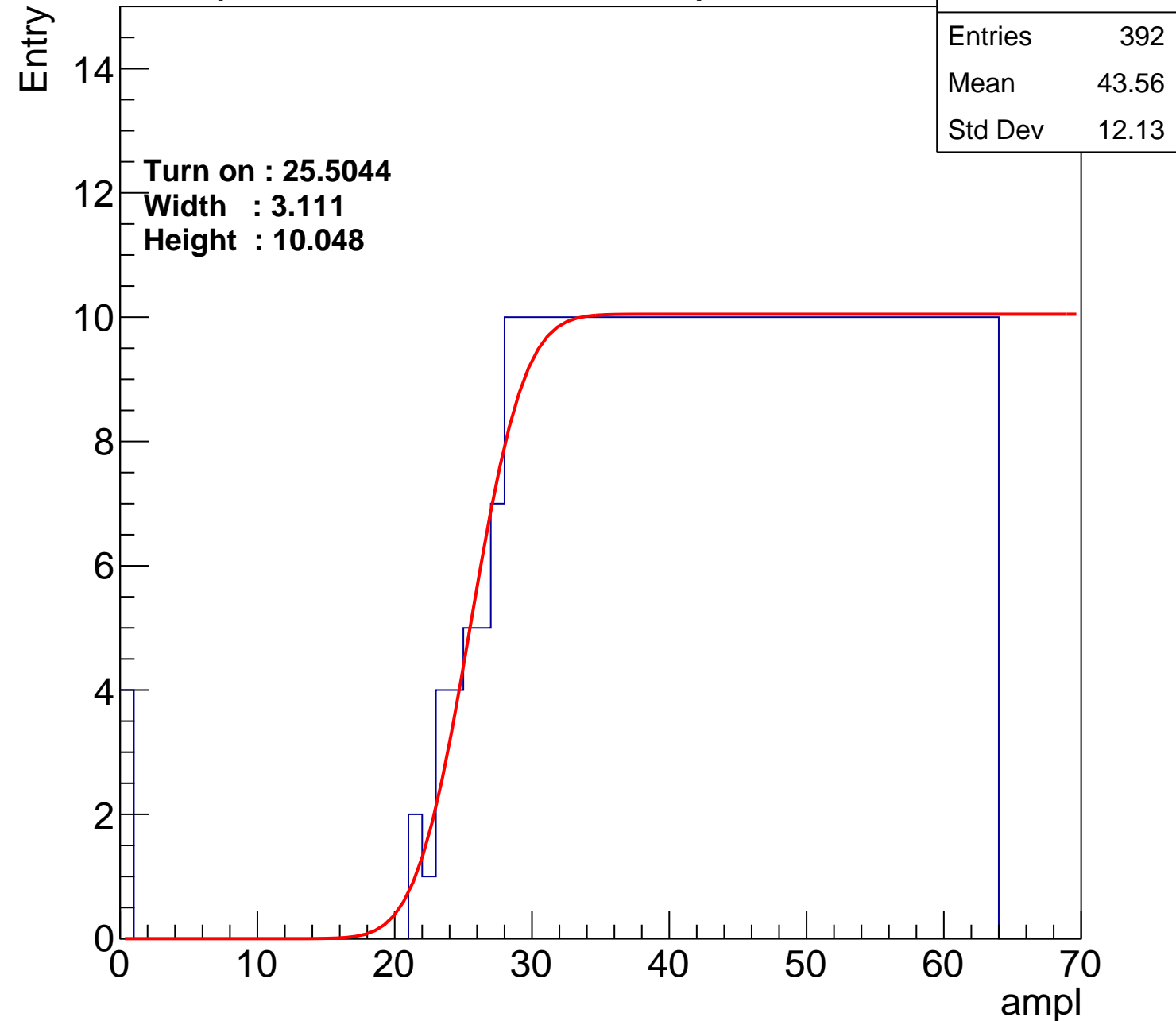
Width : 3.111

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.1337

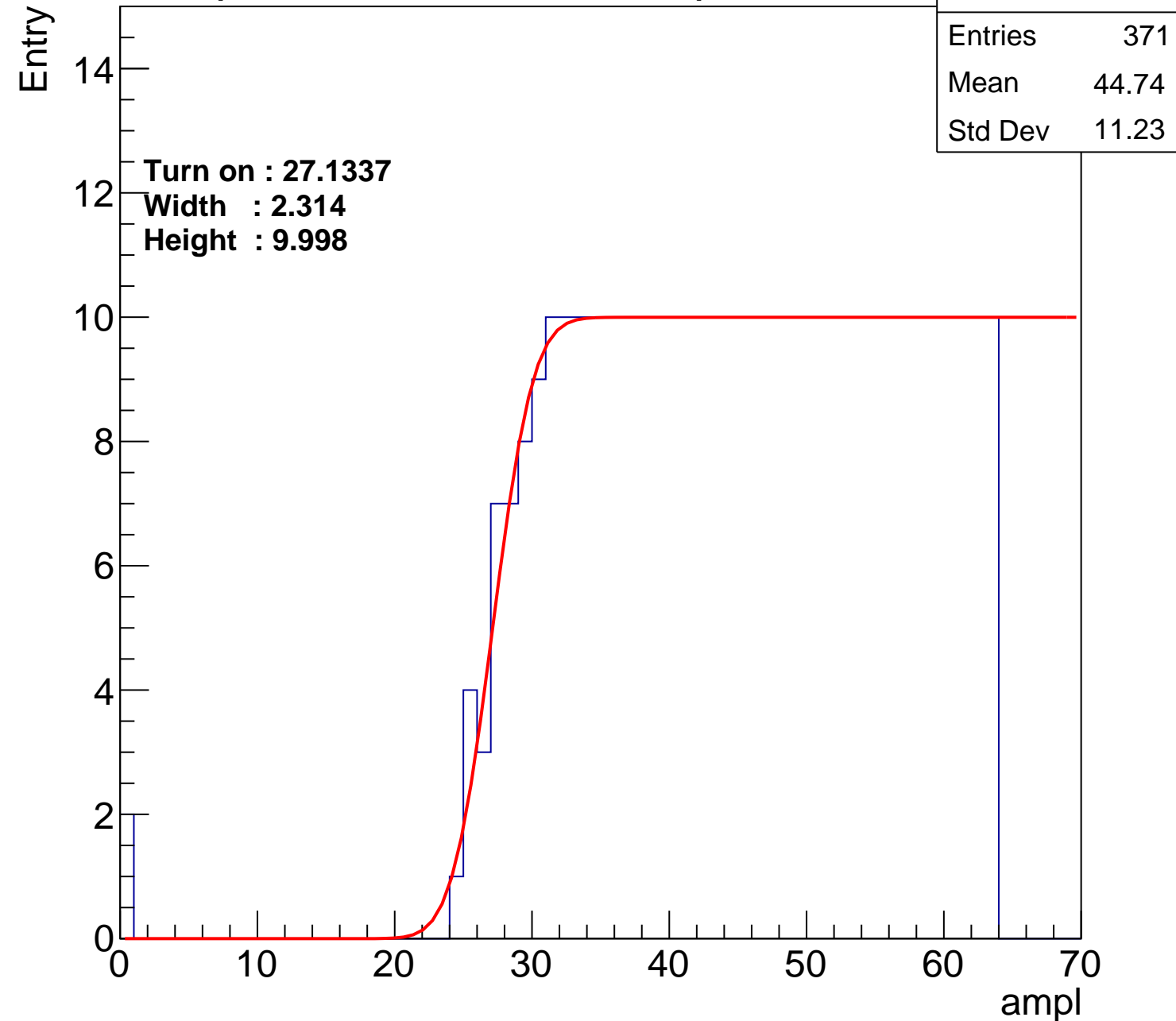
Width : 2.314

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.6
Std Dev	11.29

Turn on : 26.4733

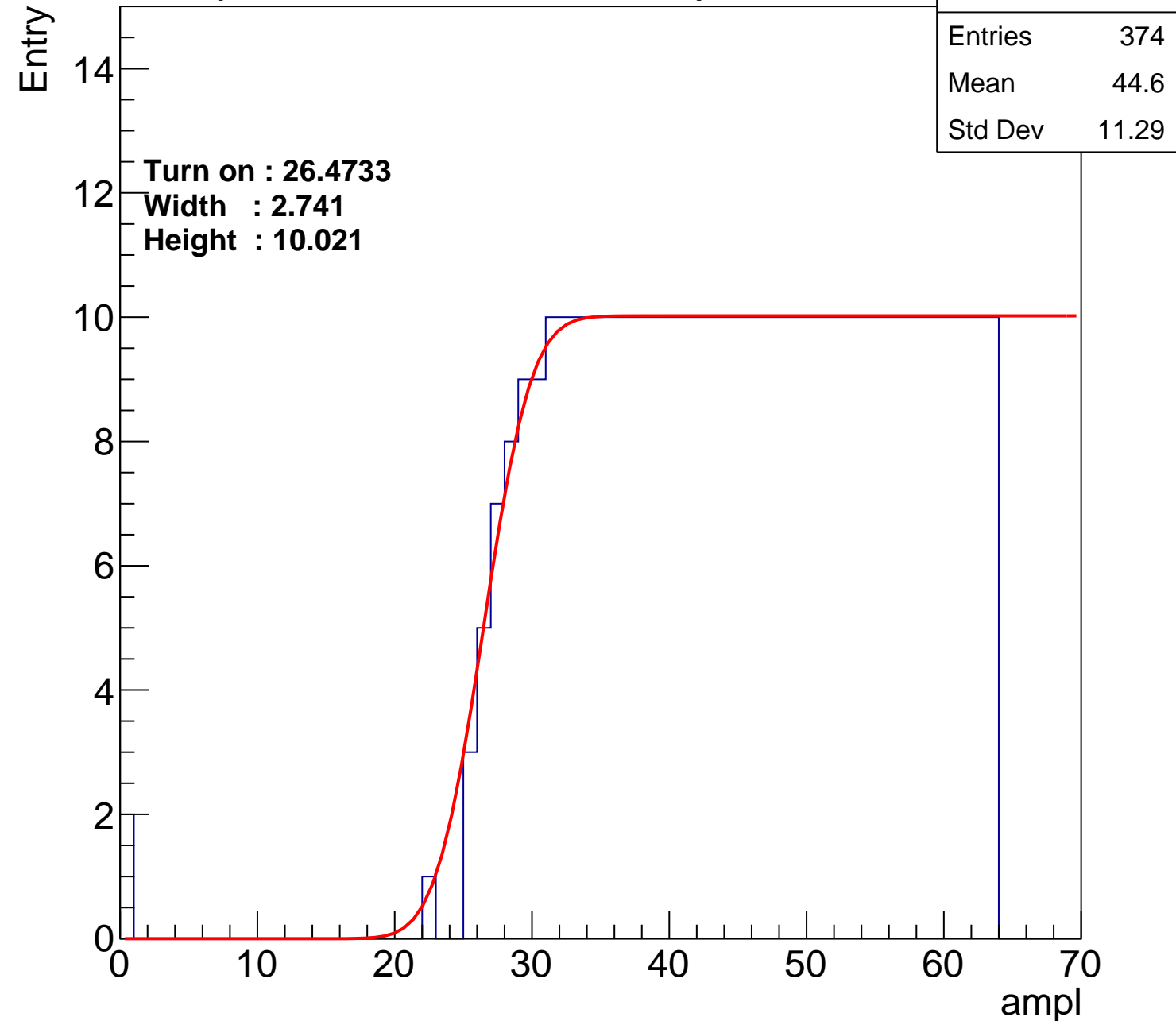
Width : 2.741

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.1
Std Dev	11.43

Turn on : 25.6892

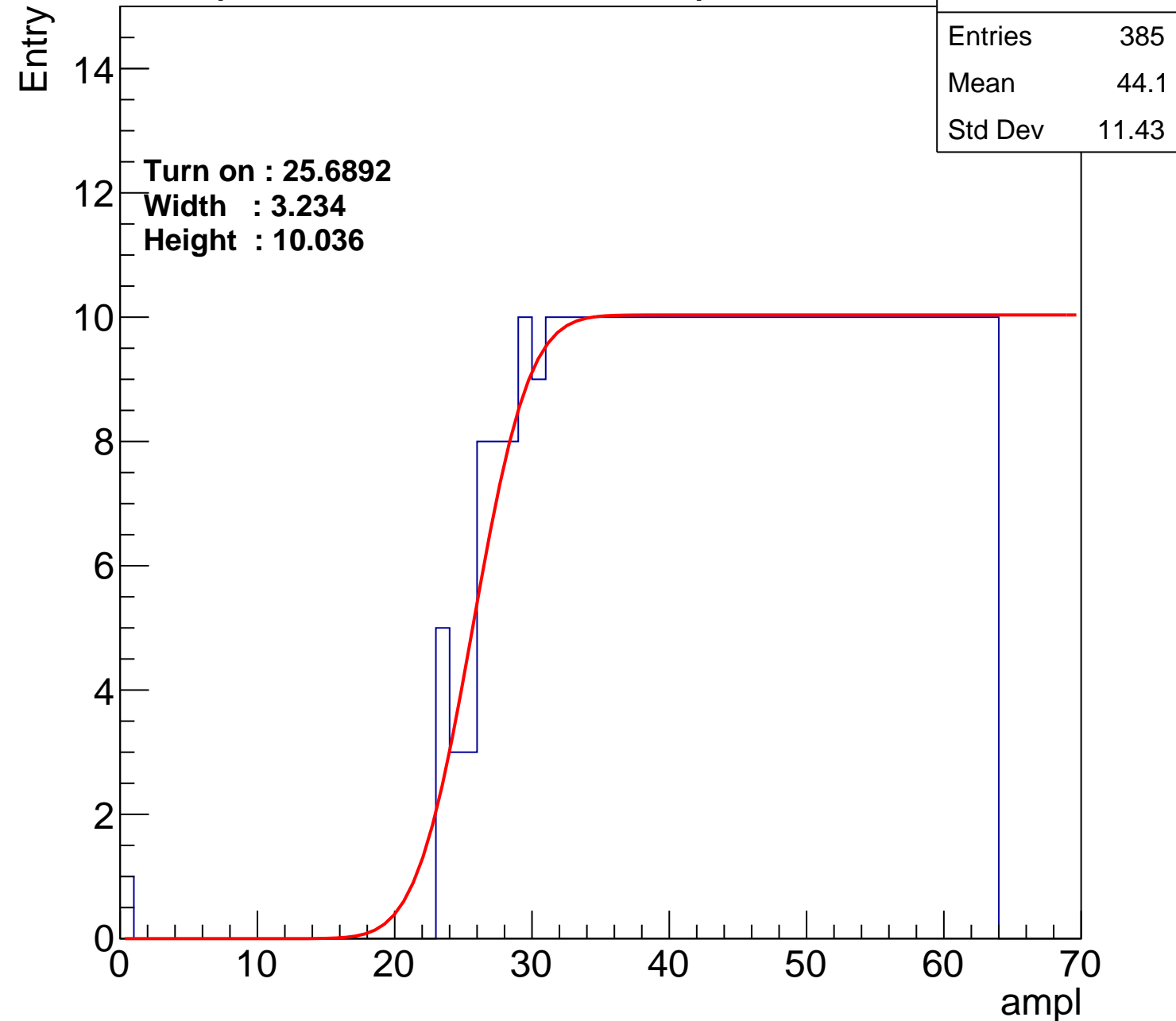
Width : 3.234

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.21
Std Dev	12.21

Turn on : 24.6832

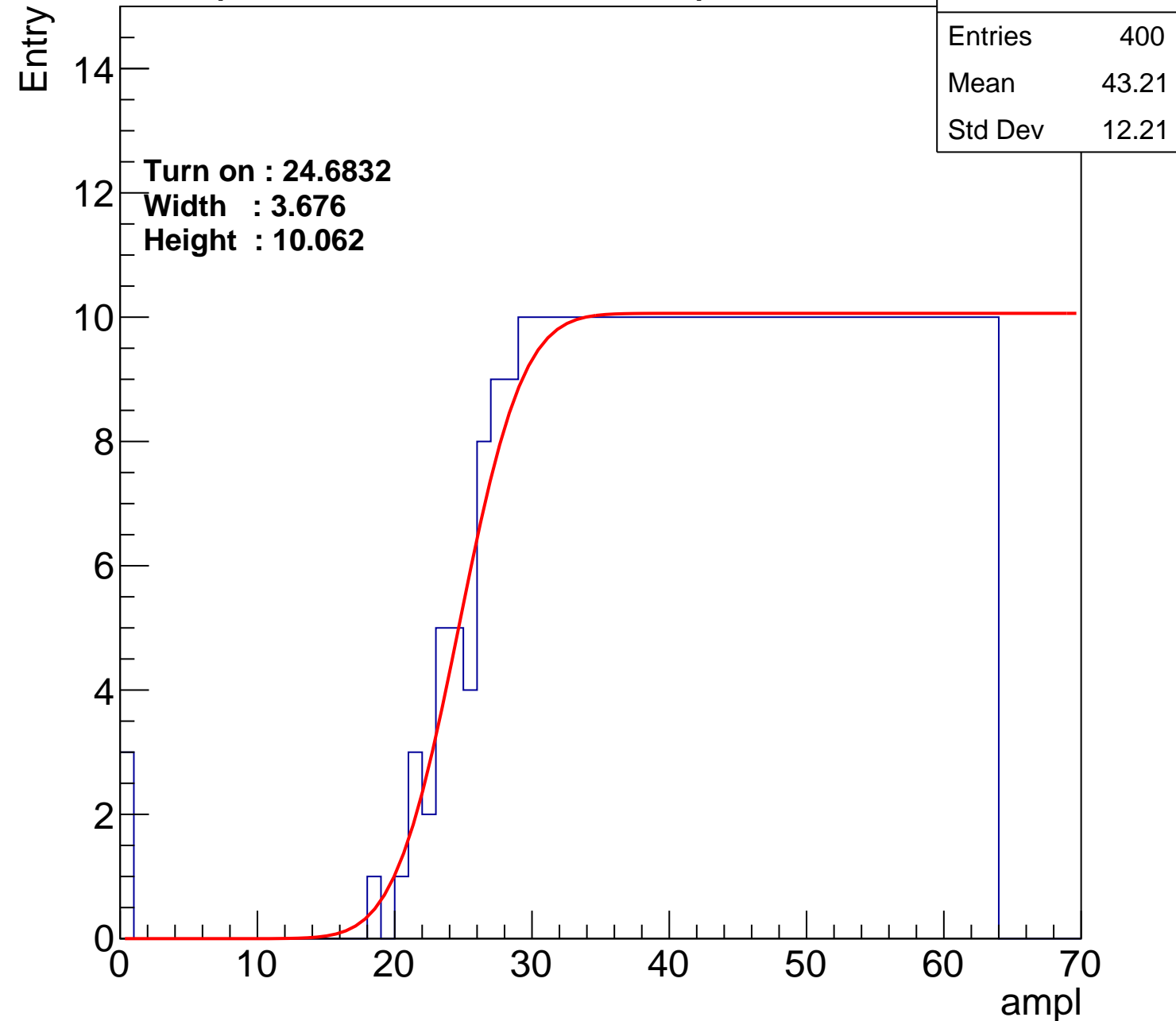
Width : 3.676

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.41
Std Dev	11.97

Turn on : 24.7308

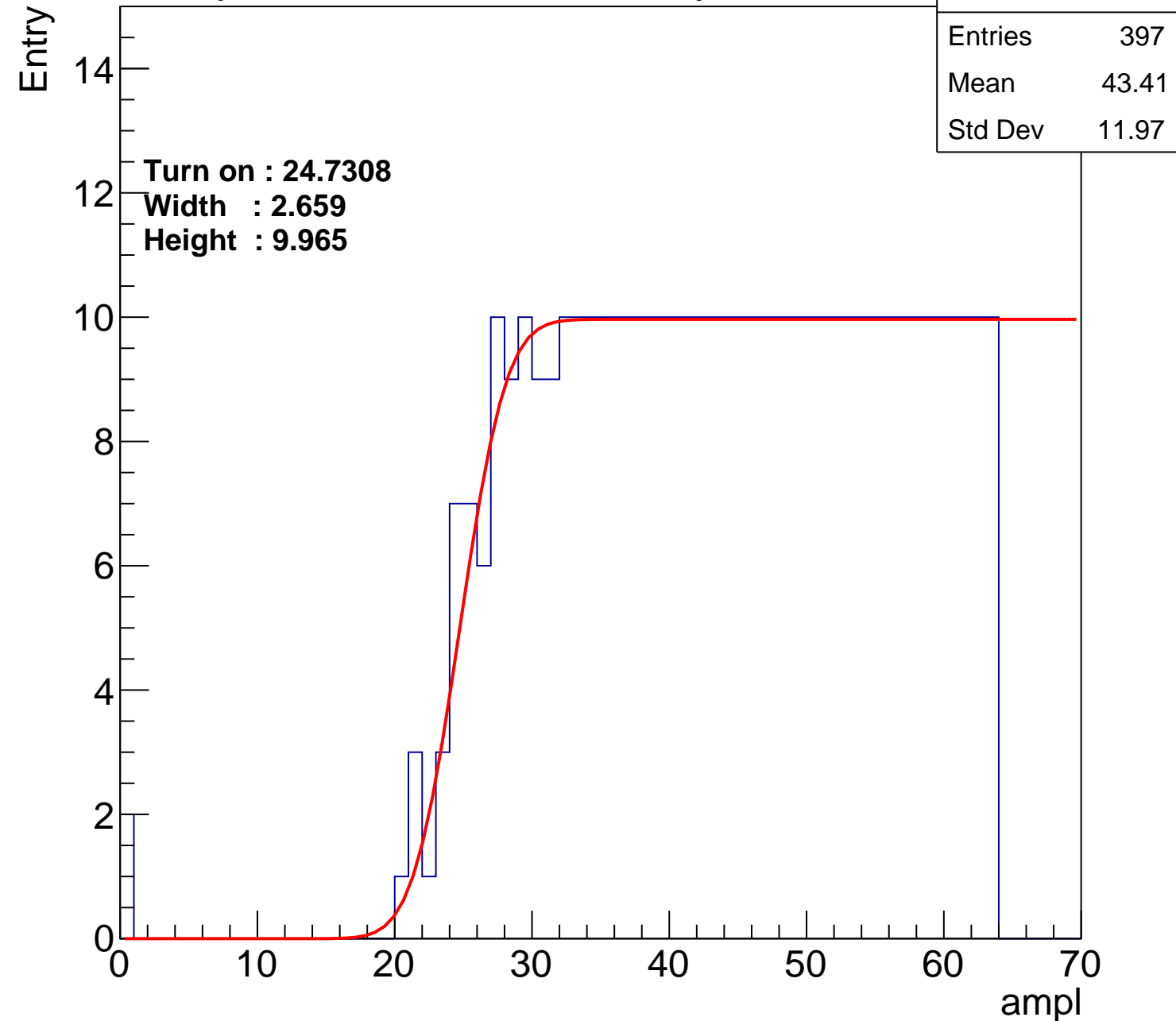
Width : 2.659

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch54

calib_packv5_042523_0143.root, FC#3, port B3

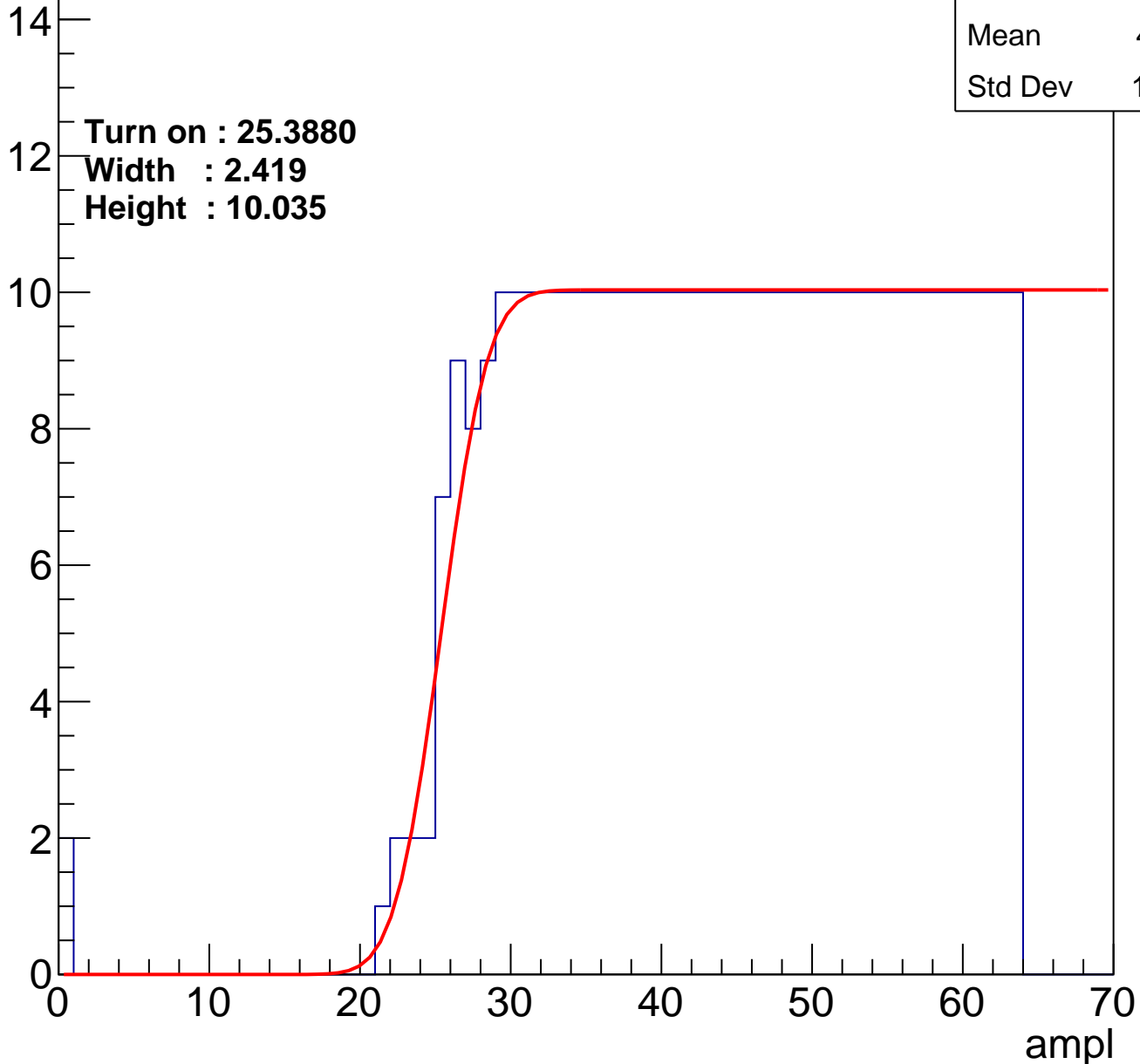
Entries	392
Mean	43.71
Std Dev	11.76

Turn on : 25.3880

Width : 2.419

Height : 10.035

Entry



B1L000S, U9-ch55

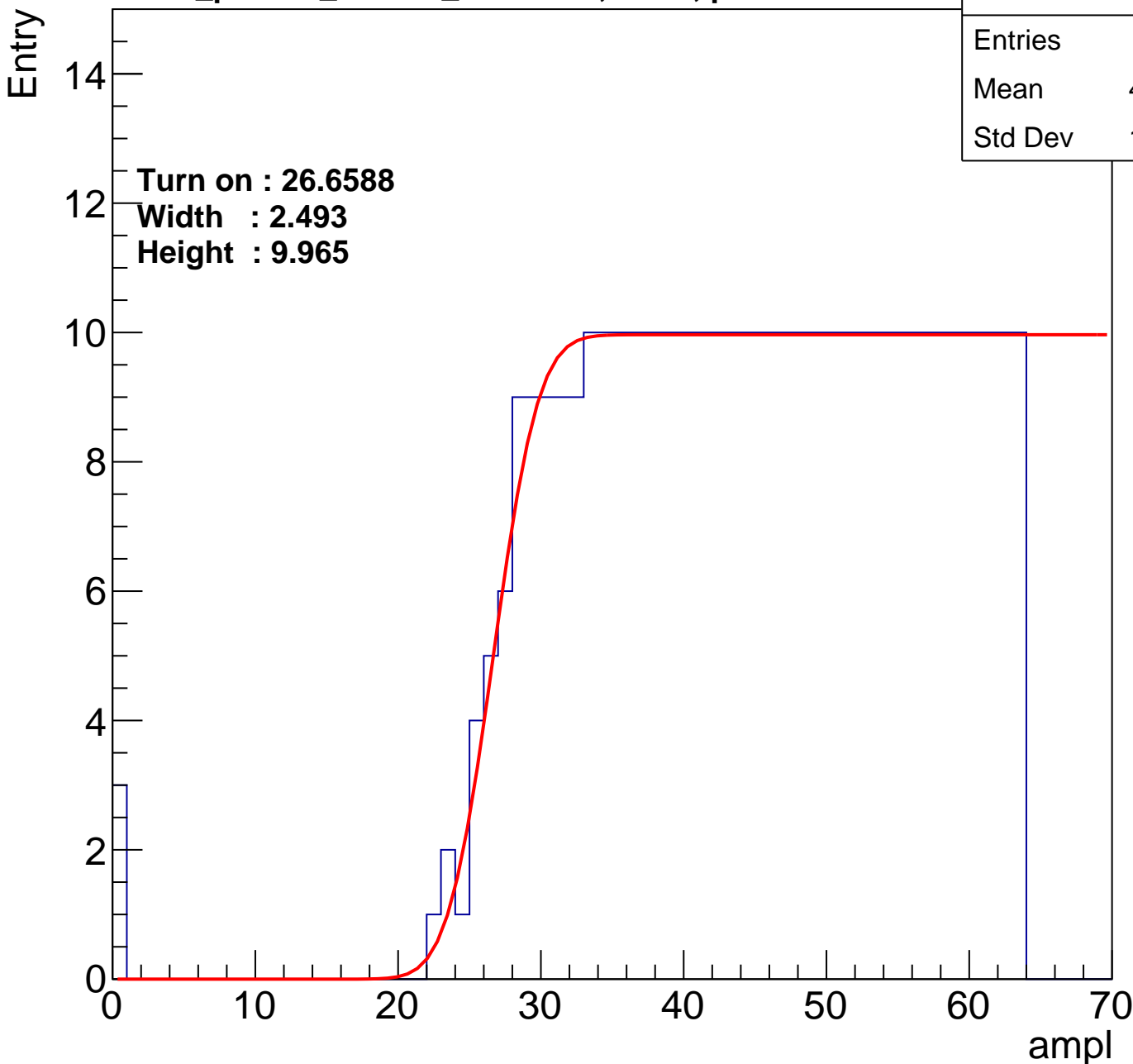
calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.33
Std Dev	11.63

Turn on : 26.6588

Width : 2.493

Height : 9.965



B1L000S, U9-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.43
Std Dev	11.94

Turn on : 24.3464

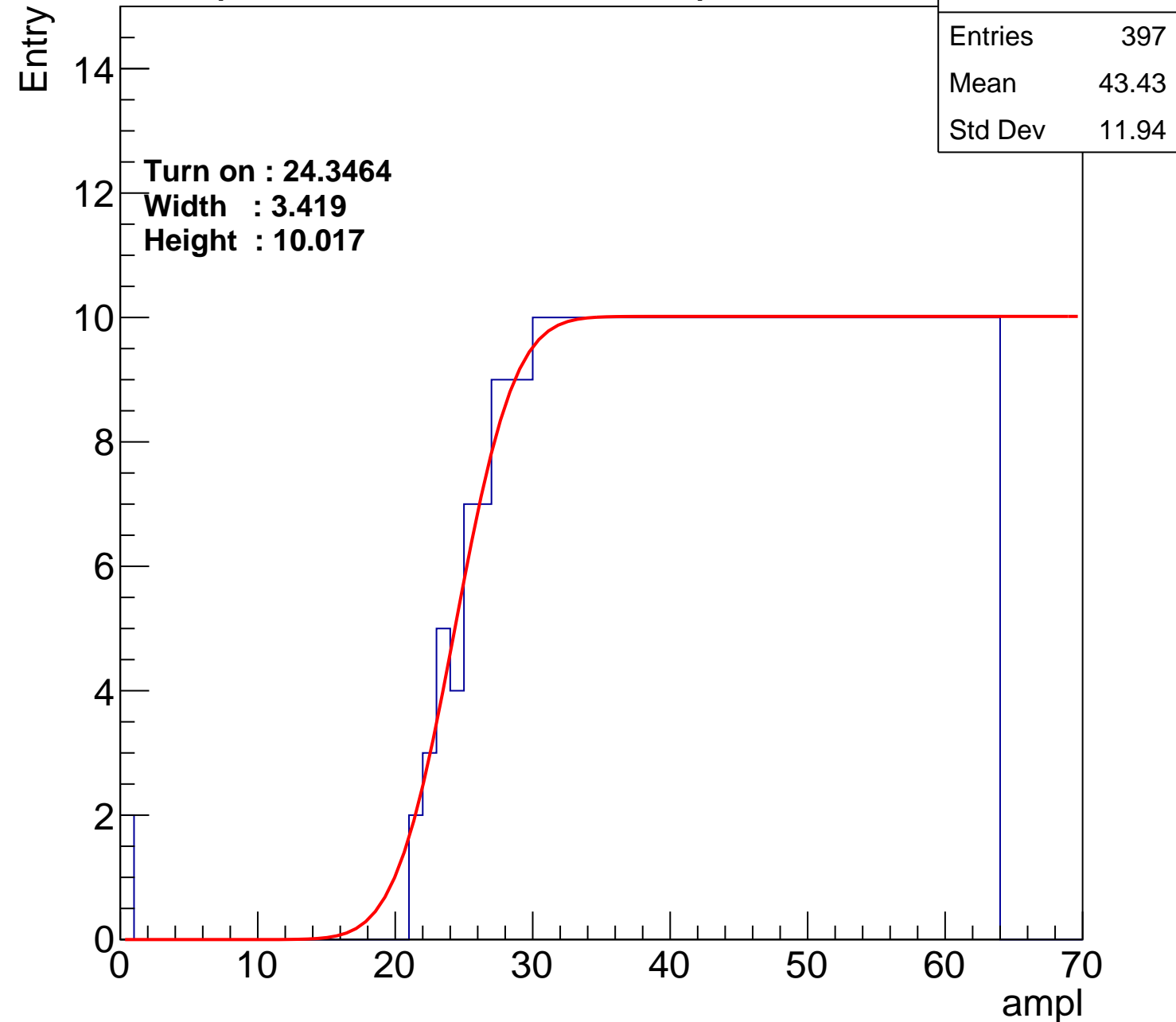
Width : 3.419

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.66
Std Dev	12.08

Turn on : 25.2968

Width : 3.146

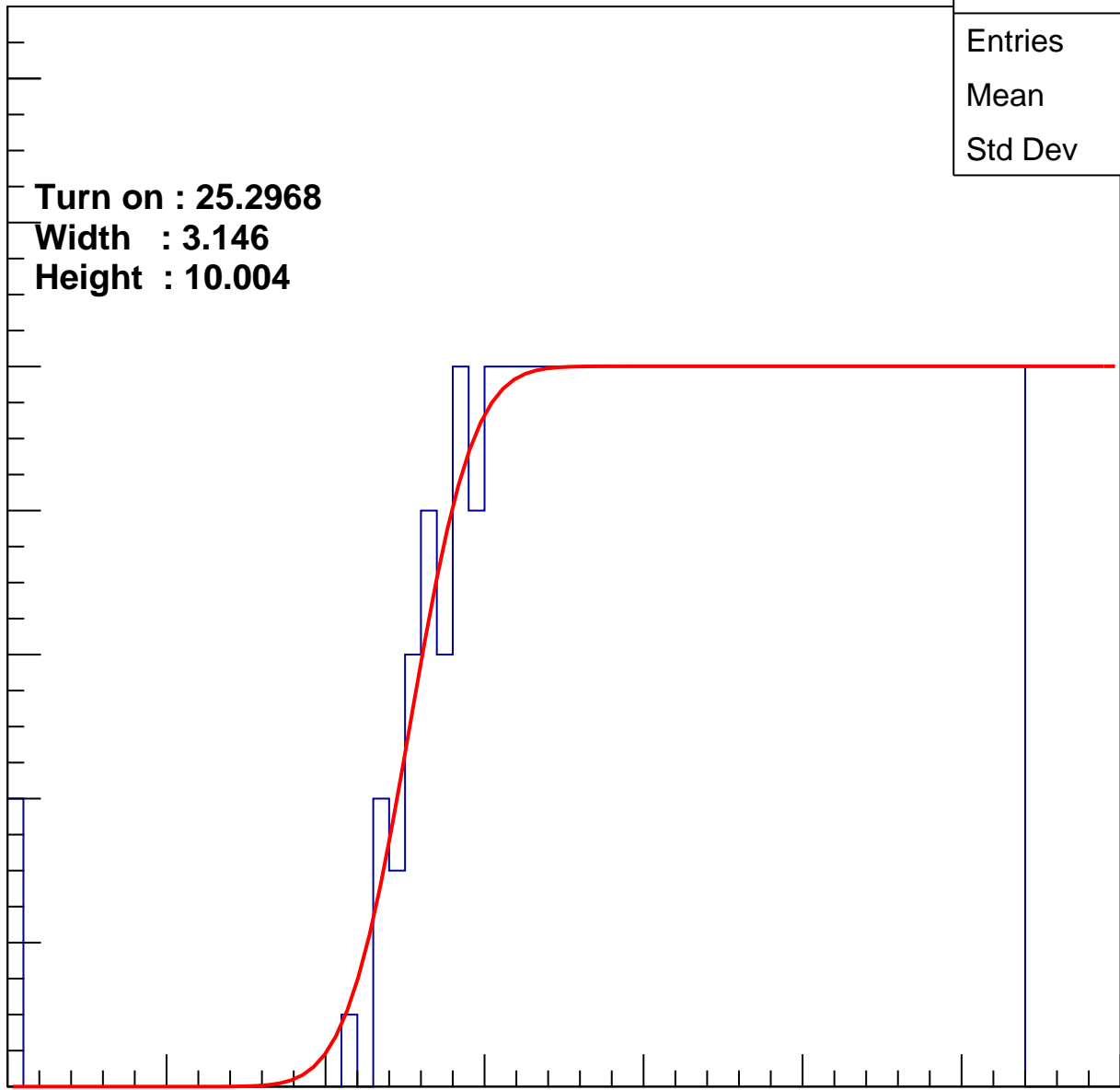
Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U9-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.56
Std Dev	11.63

Turn on : 26.9307

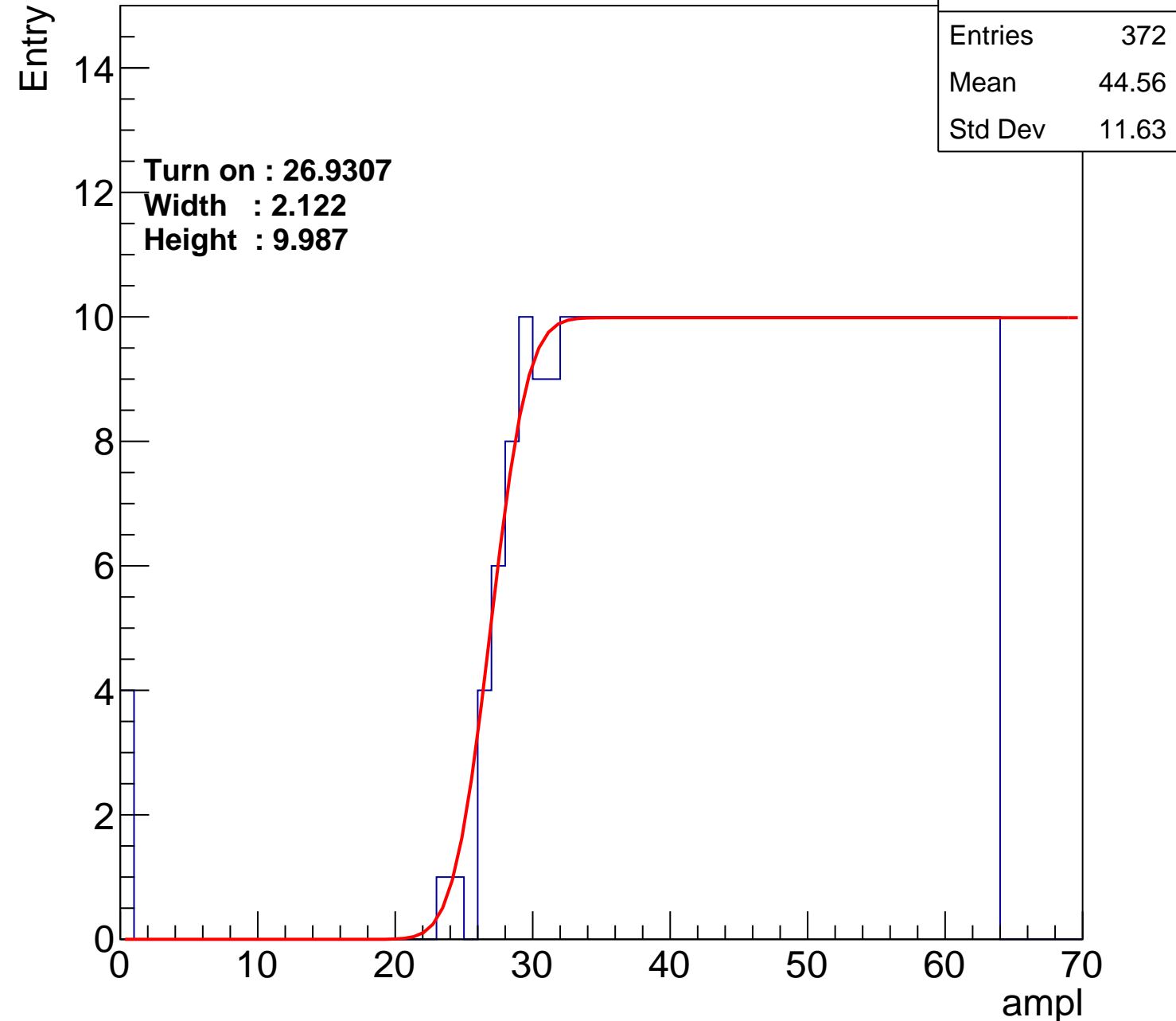
Width : 2.122

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.23
Std Dev	10.98

Turn on : 28.4032

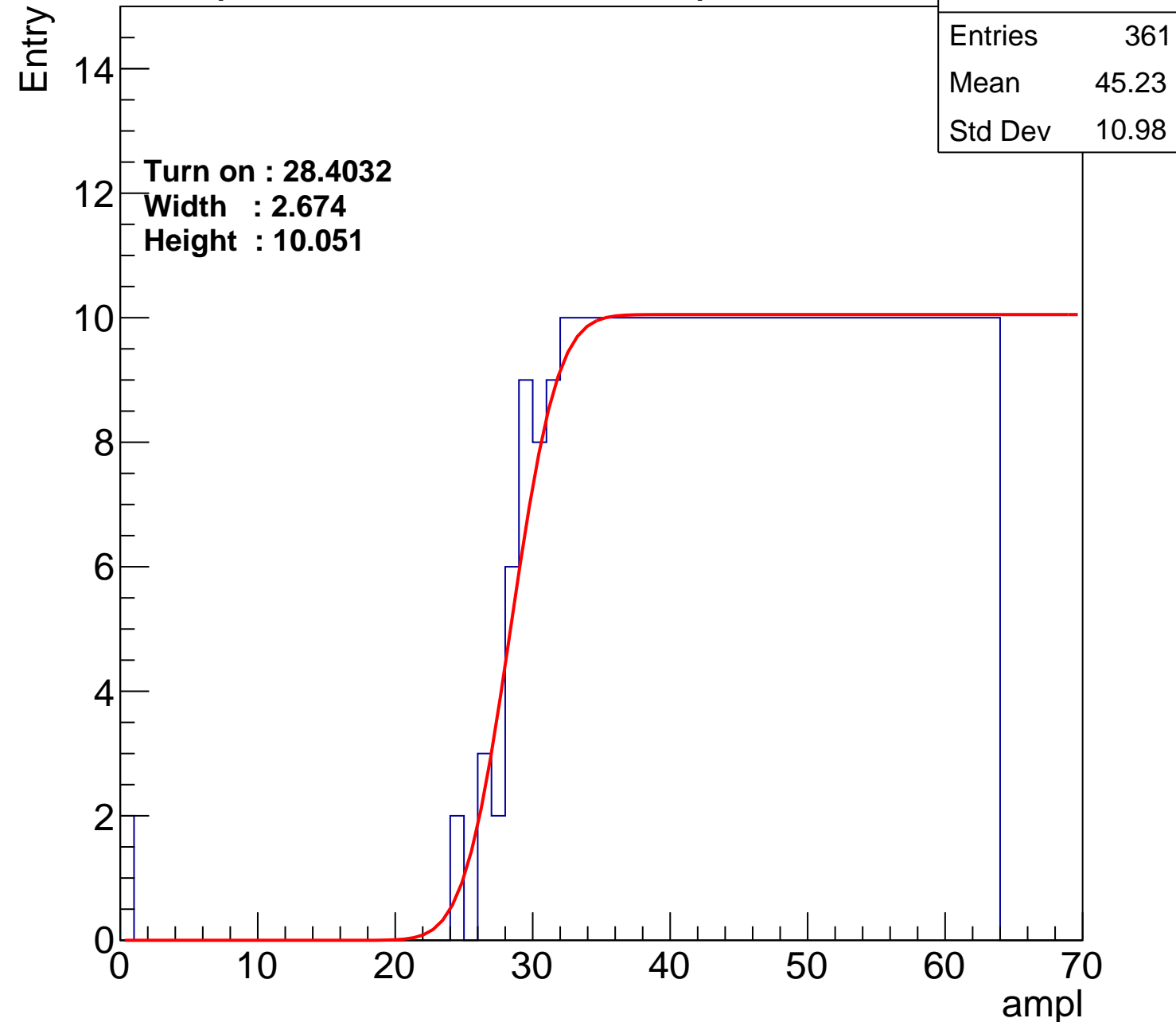
Width : 2.674

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 26.9994

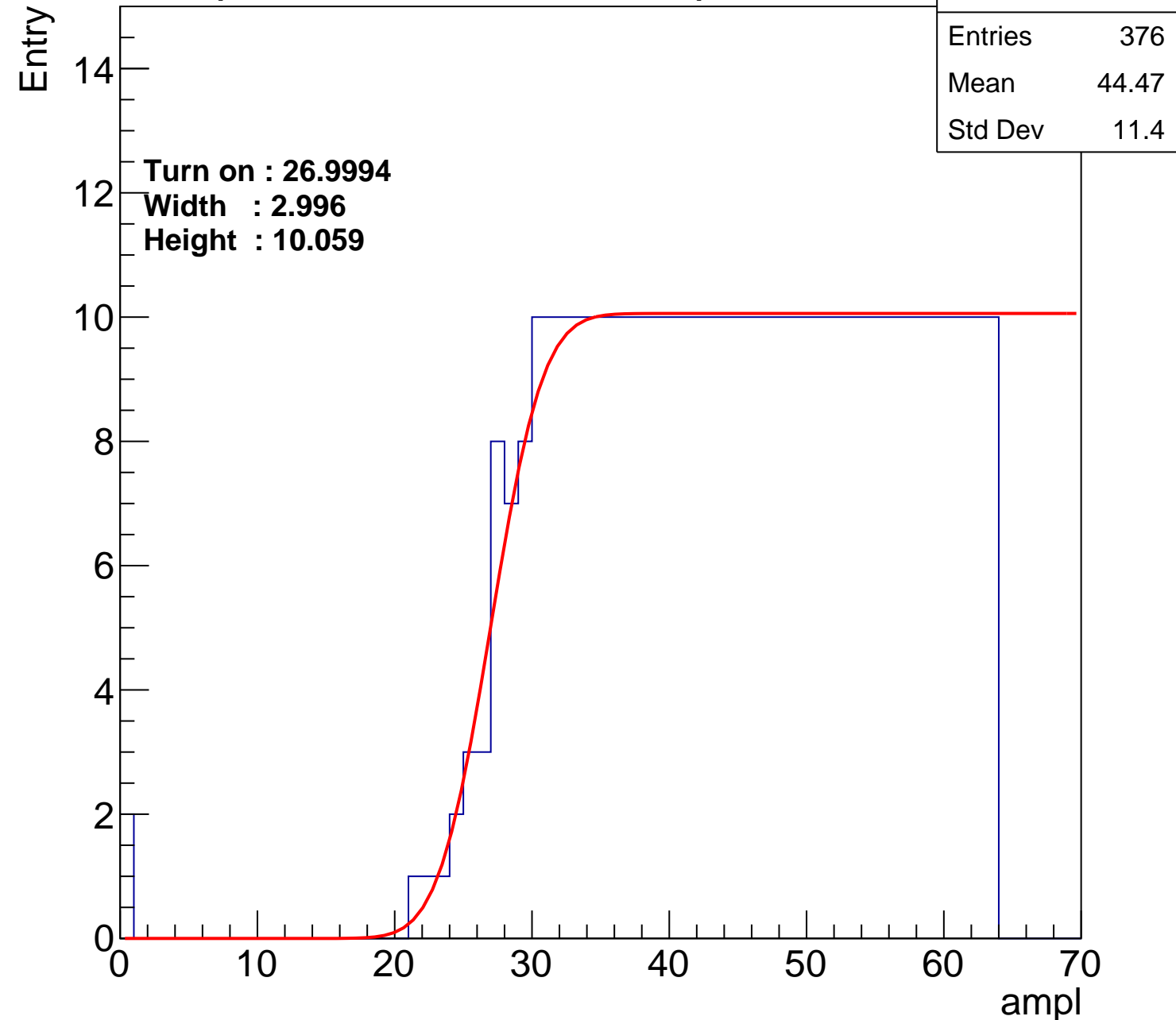
Width : 2.996

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 26.4821

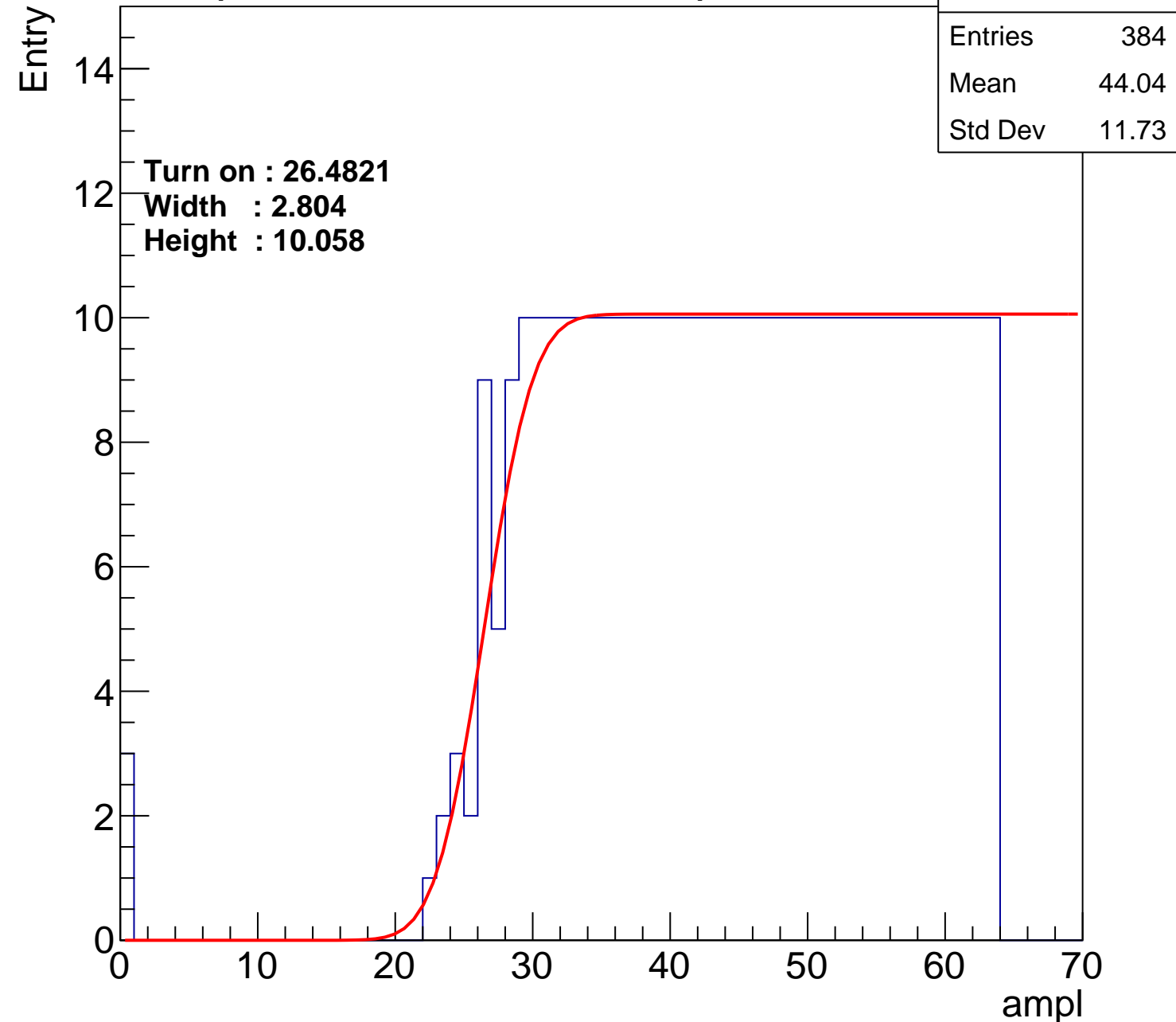
Width : 2.804

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch62

calib_packv5_042523_0143.root, FC#3, port B3

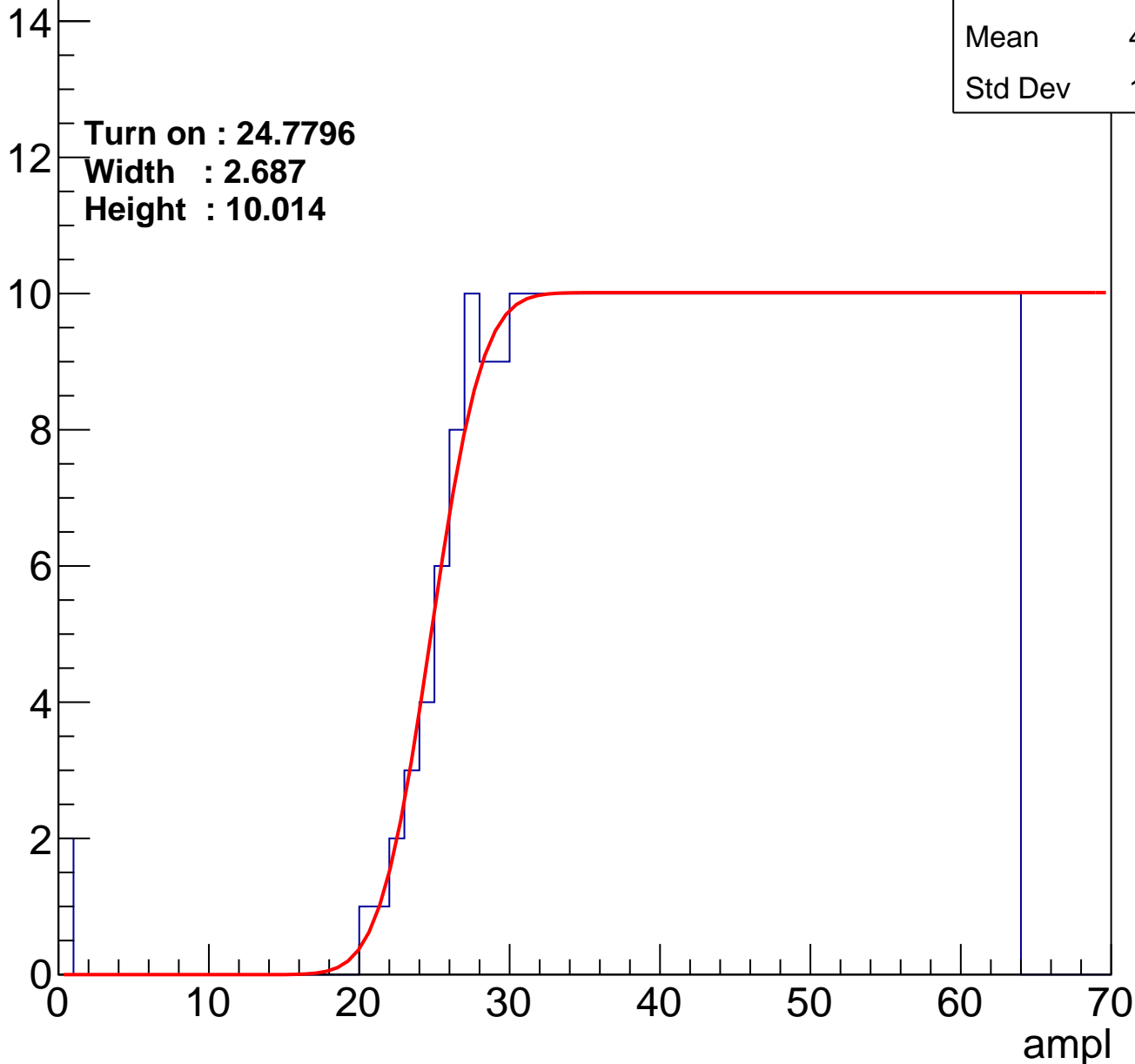
Entry

Entries	395
Mean	43.55
Std Dev	11.87

Turn on : 24.7796

Width : 2.687

Height : 10.014



B1L000S, U9-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 25.2012

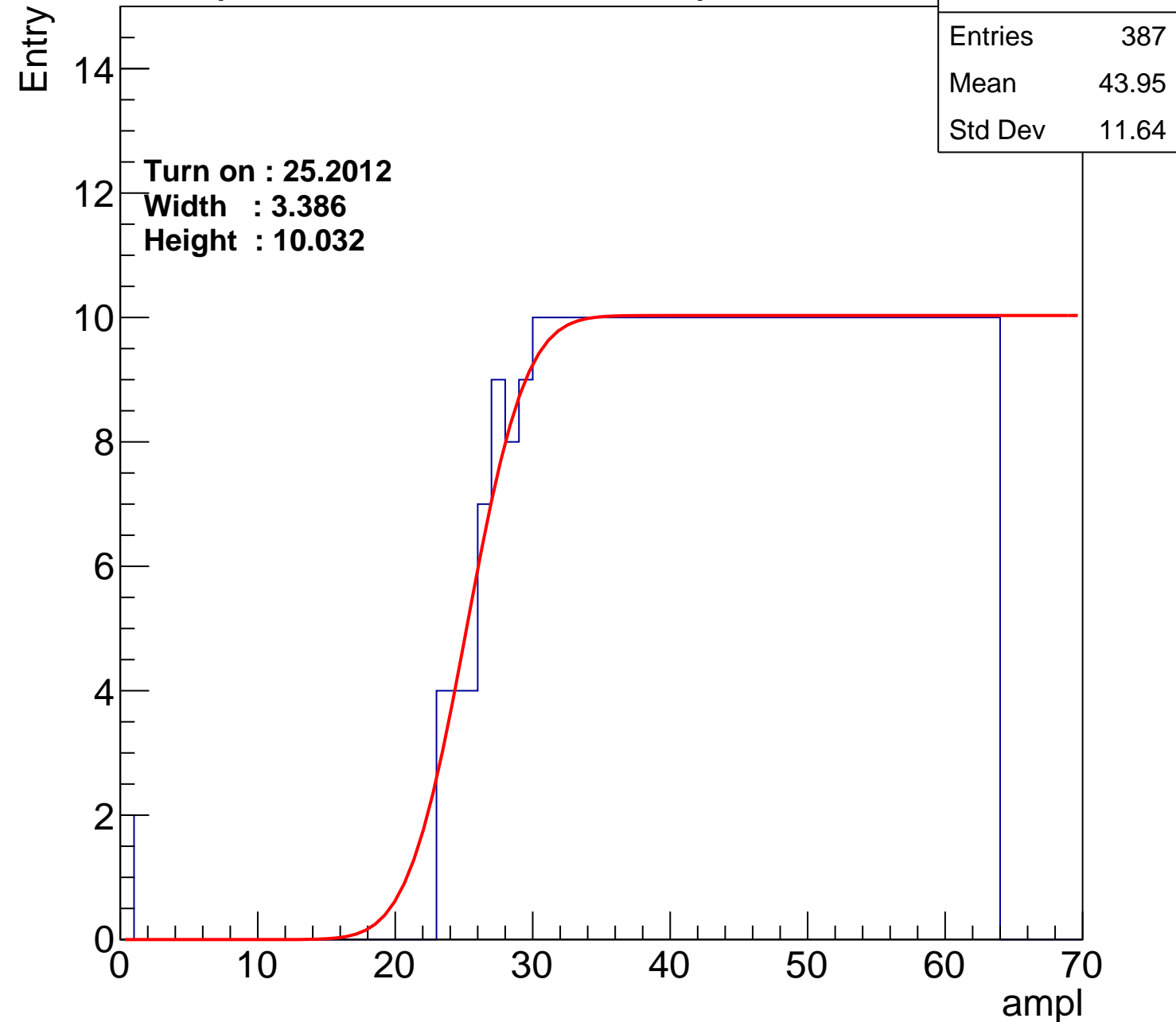
Width : 3.386

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 25.9567

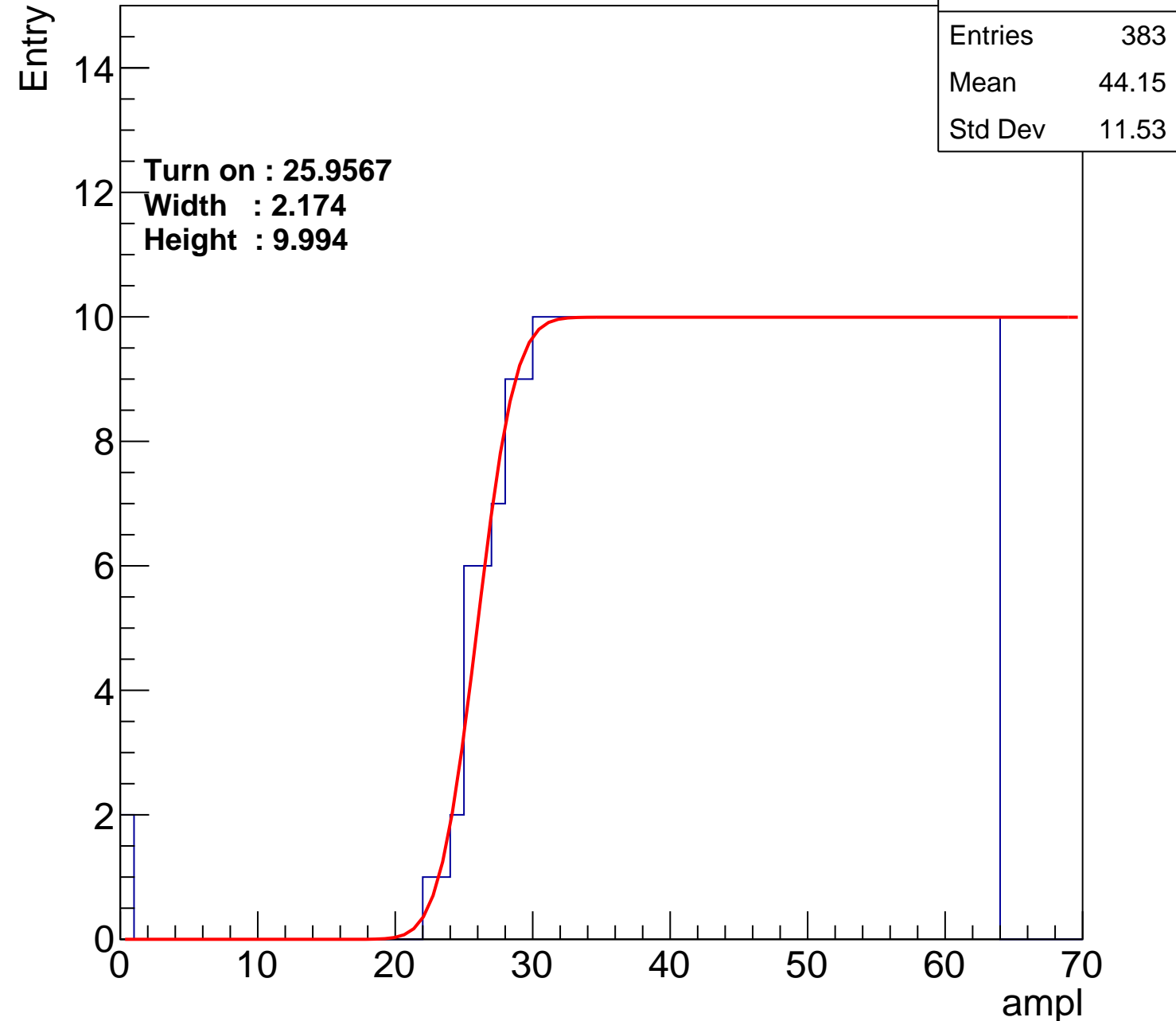
Width : 2.174

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 27.1623

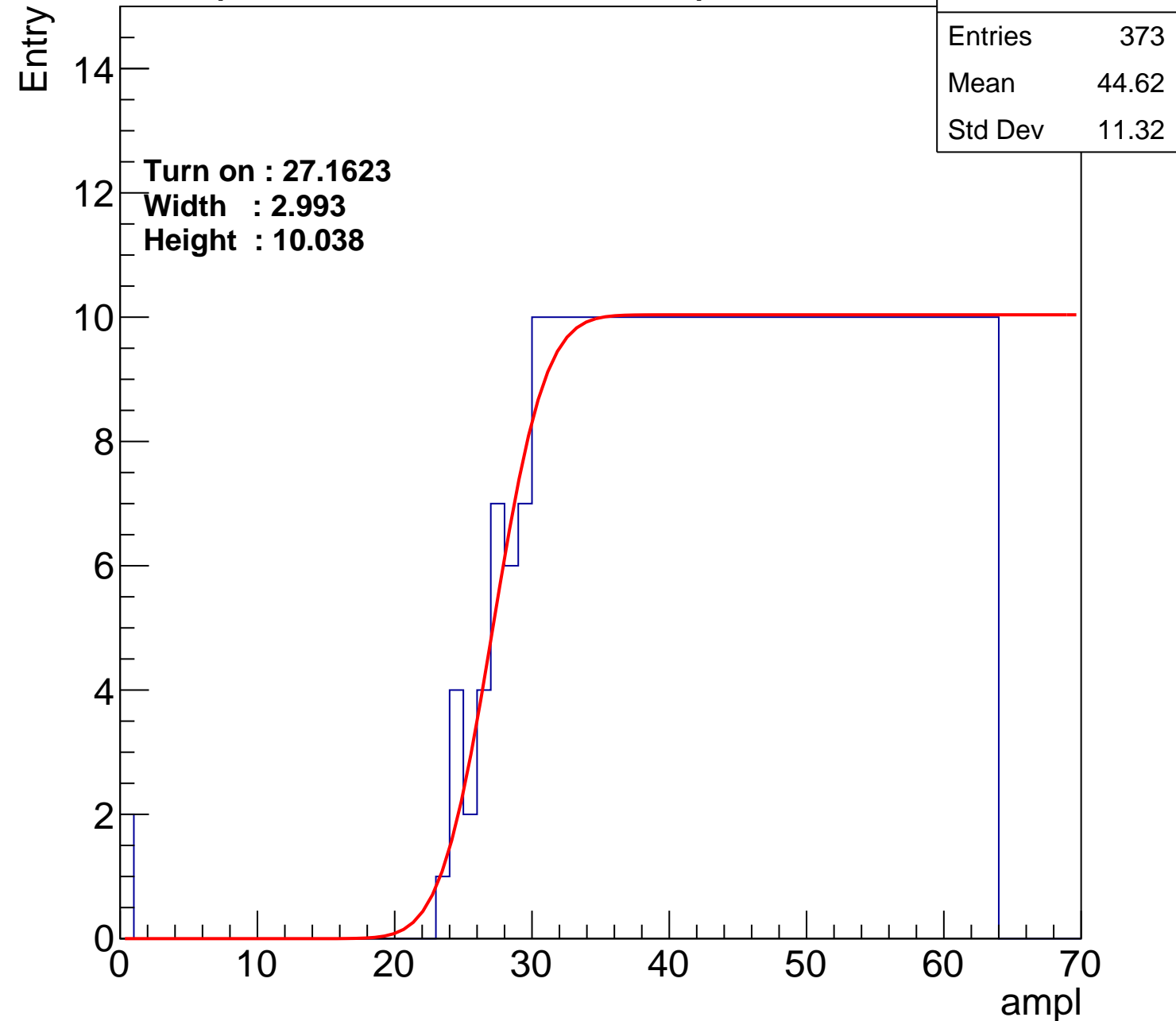
Width : 2.993

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.38
Std Dev	12.05

Turn on : 27.5112

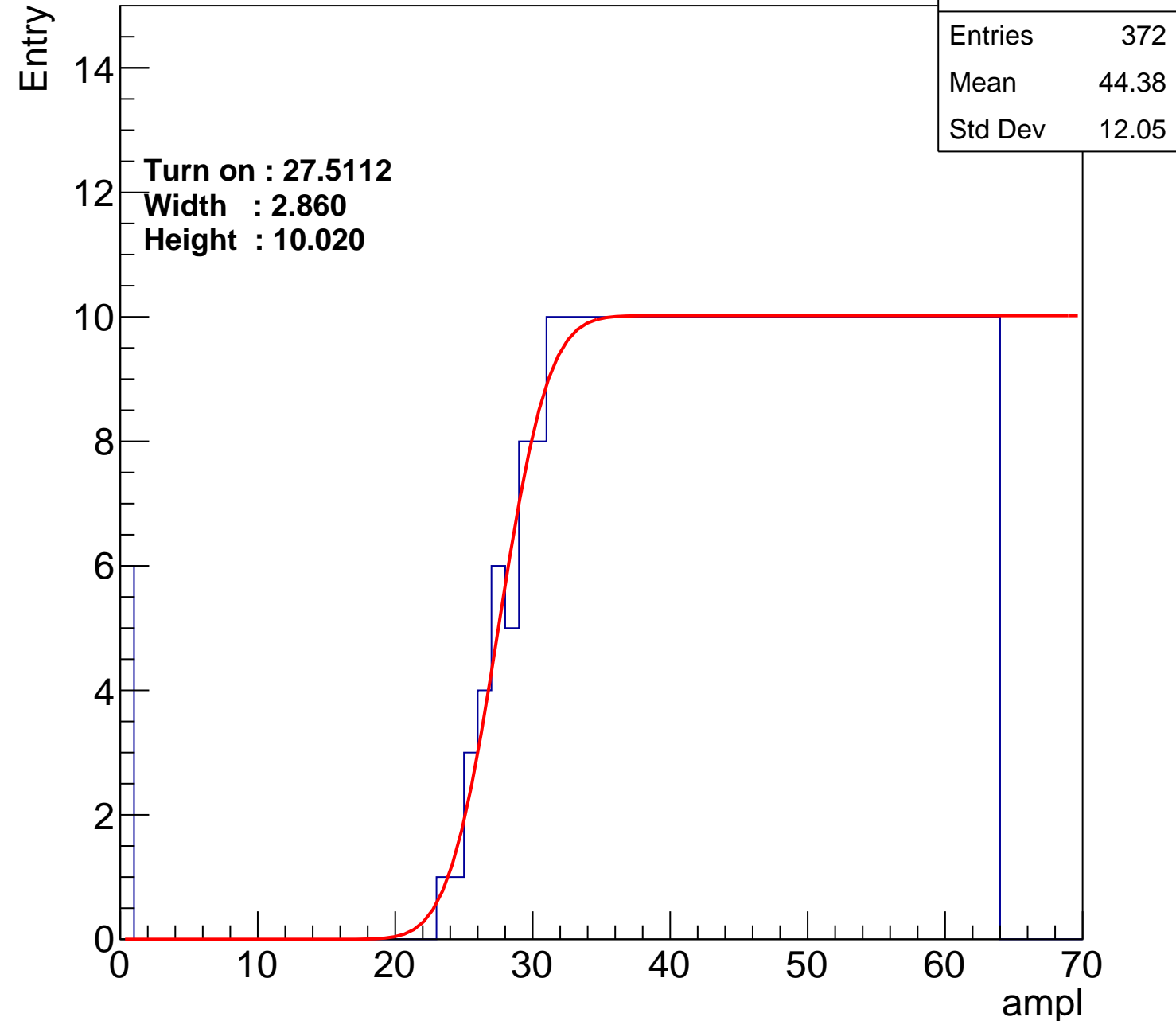
Width : 2.860

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.92
Std Dev	11.69

Turn on : 25.7778

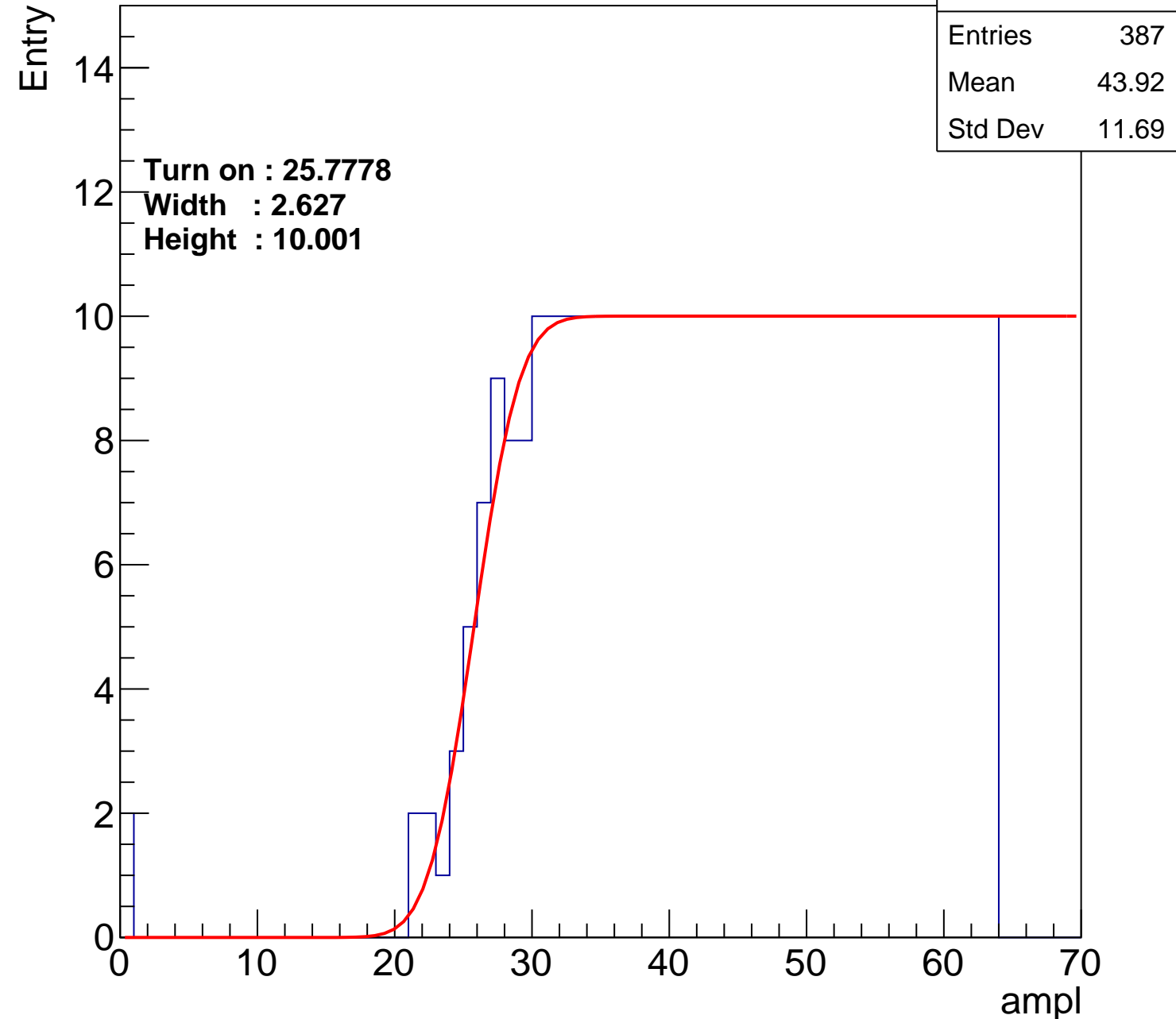
Width : 2.627

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.65
Std Dev	11.33

Turn on : 27.3177

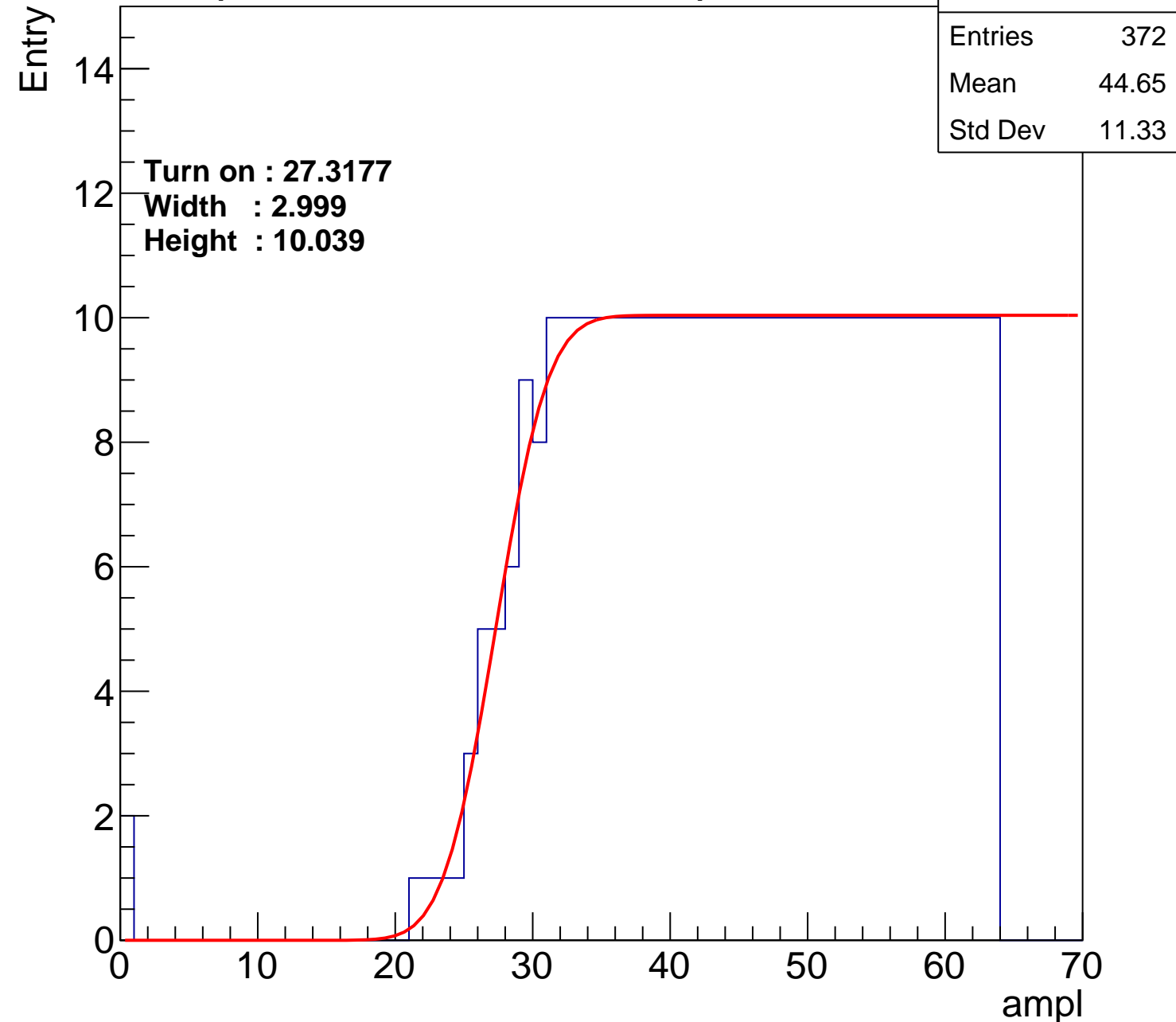
Width : 2.999

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch69

calib_packv5_042523_0143.root, FC#3, port B3

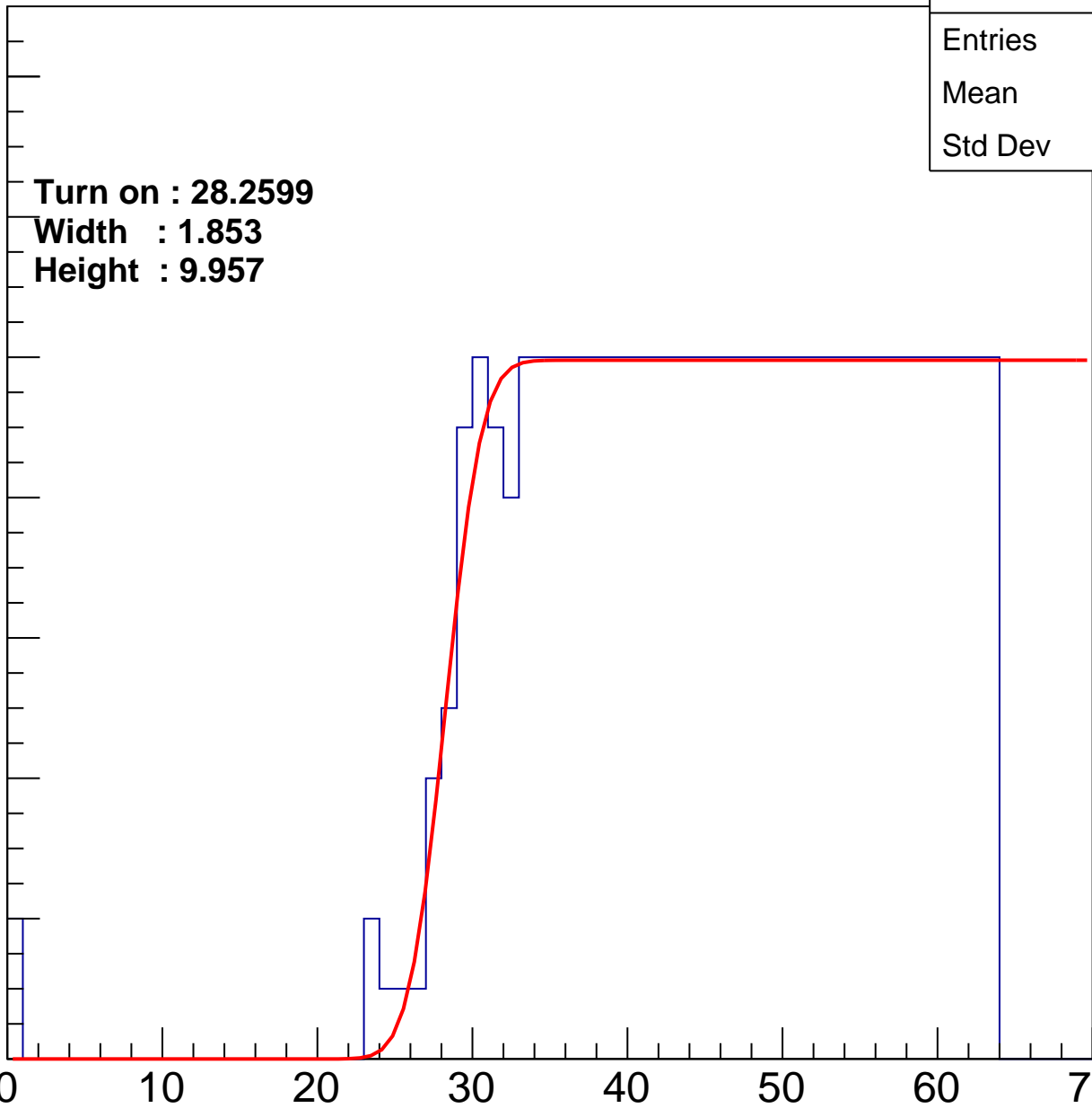
Entry

14
12
10
8
6
4
2
0

Turn on : 28.2599
Width : 1.853
Height : 9.957

Entries	362
Mean	45.15
Std Dev	11.05

ampl



B1L000S, U9-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 23.9389

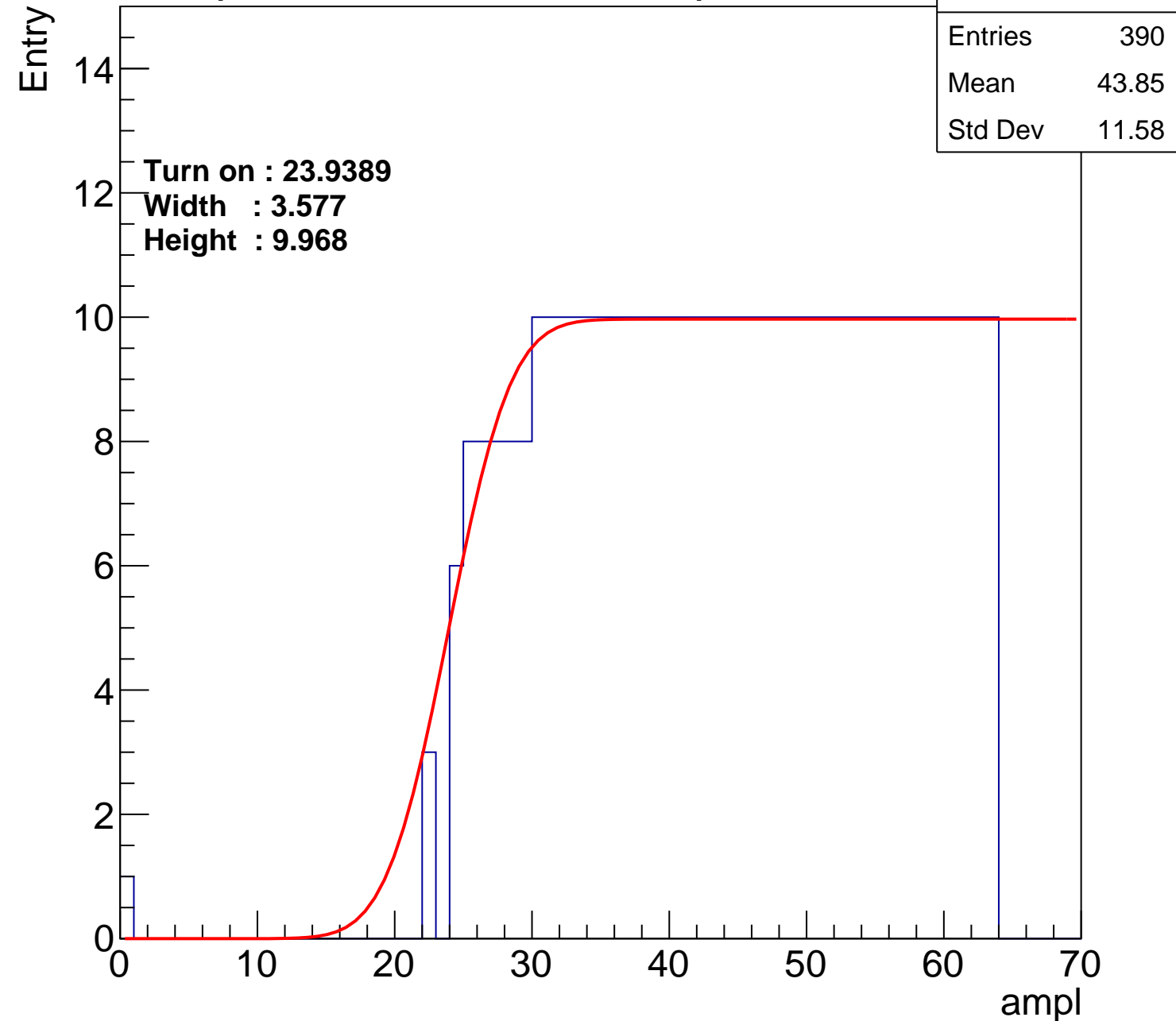
Width : 3.577

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.91
Std Dev	10.98

Turn on : 27.9629

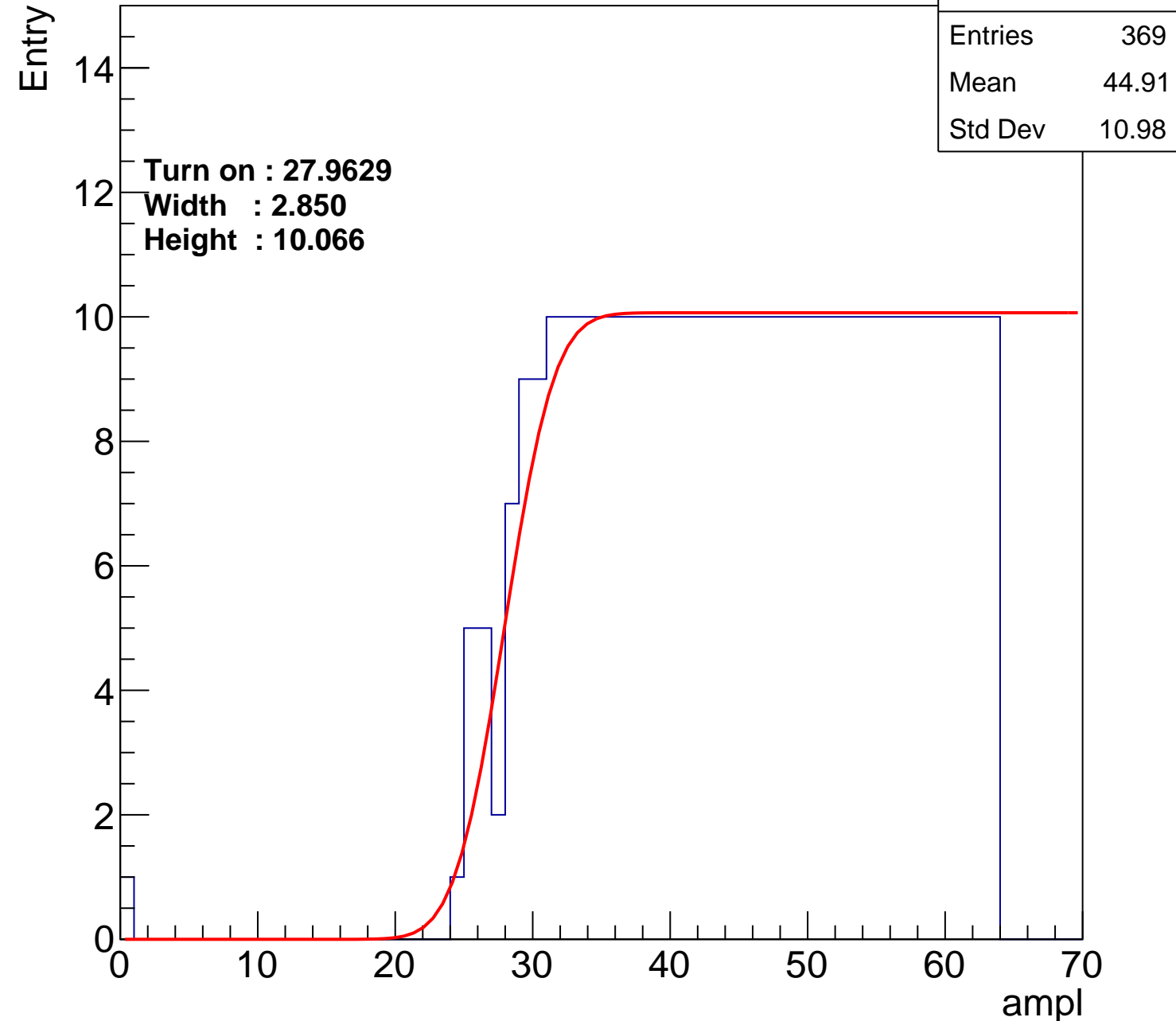
Width : 2.850

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.44
Std Dev	12.17

Turn on : 24.8211

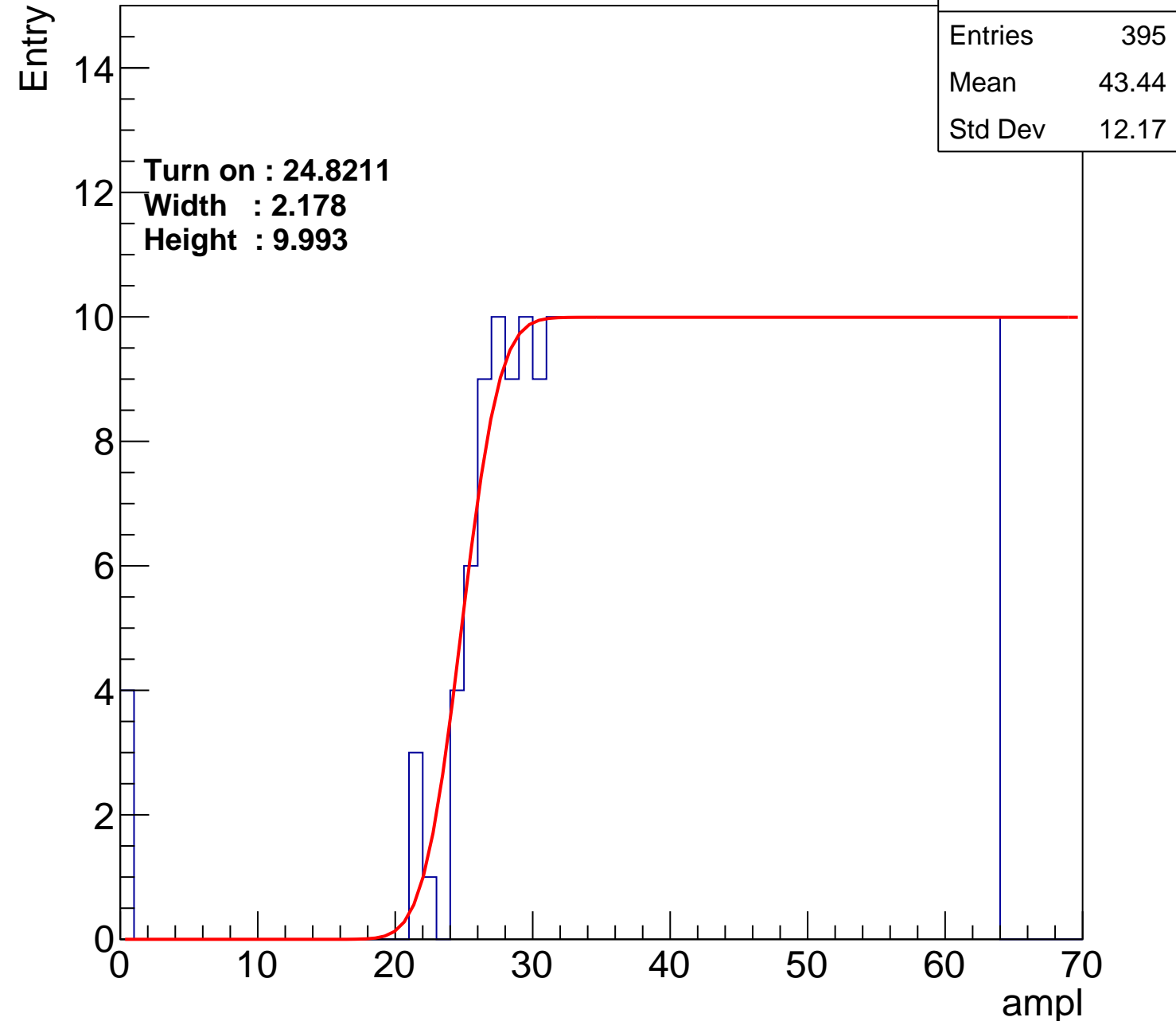
Width : 2.178

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries 383

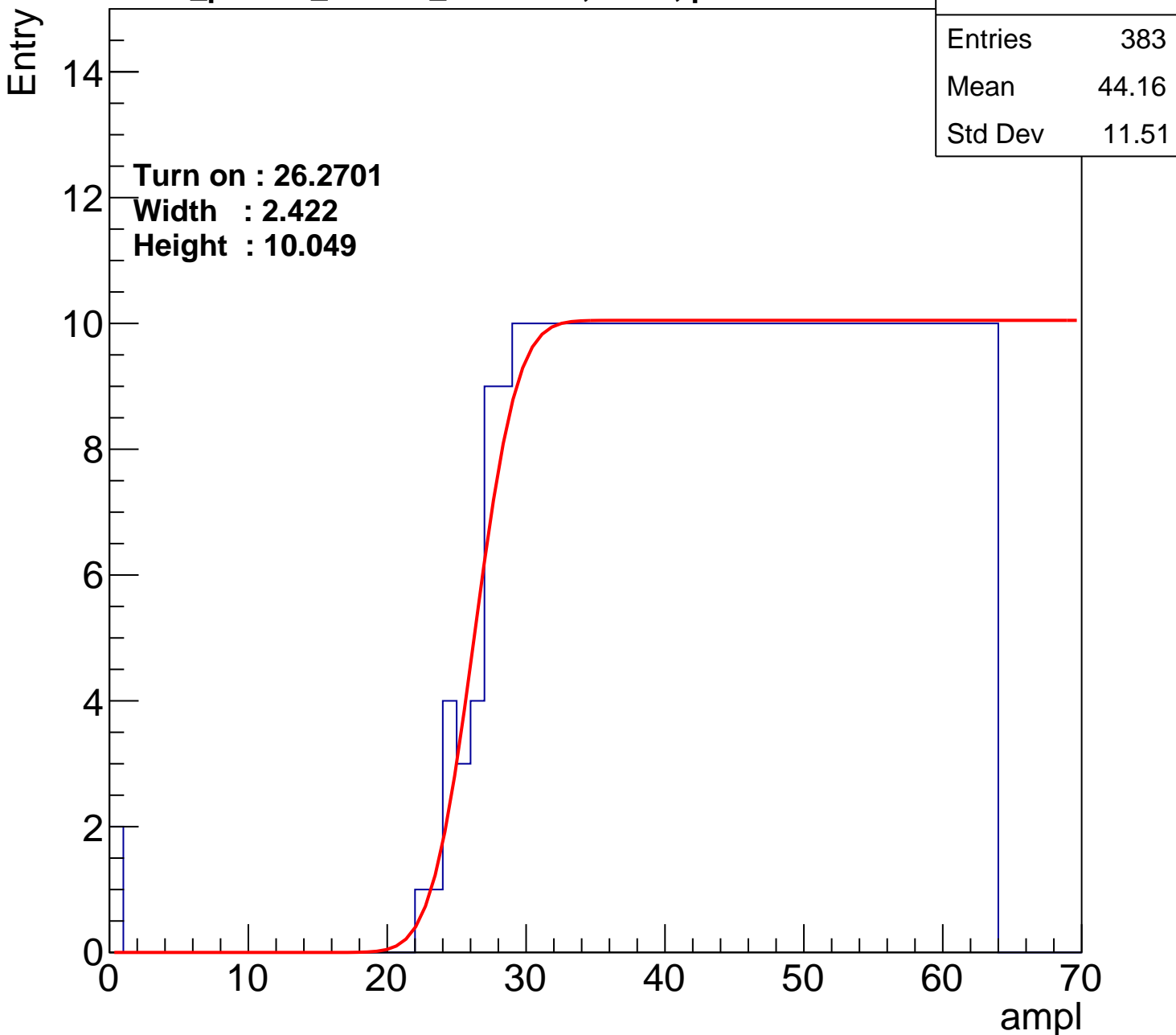
Mean 44.16

Std Dev 11.51

Turn on : 26.2701

Width : 2.422

Height : 10.049



B1L000S, U9-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.31
Std Dev	11.62

Turn on : 26.5734

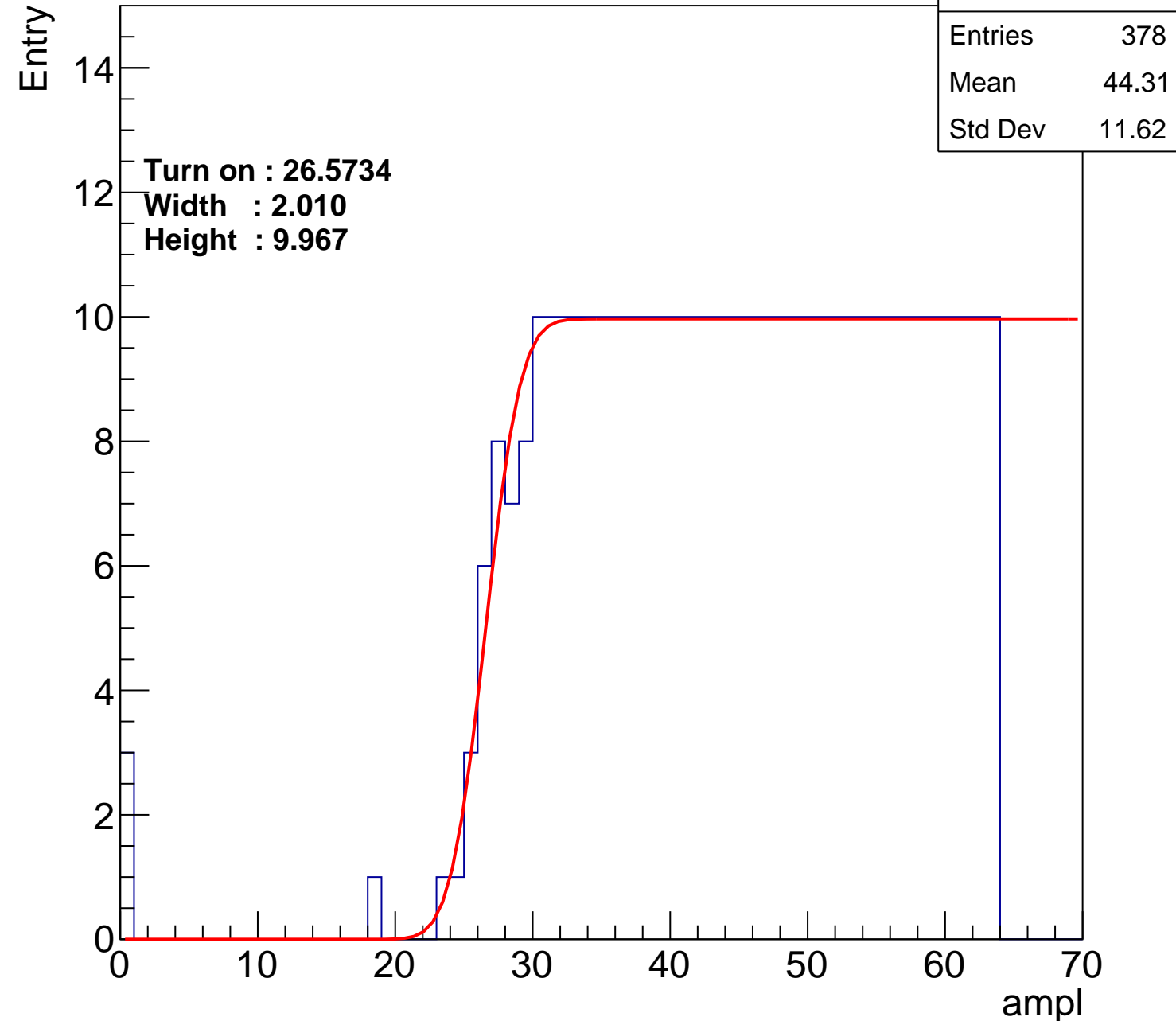
Width : 2.010

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.72
Std Dev	11.26

Turn on : 27.7121

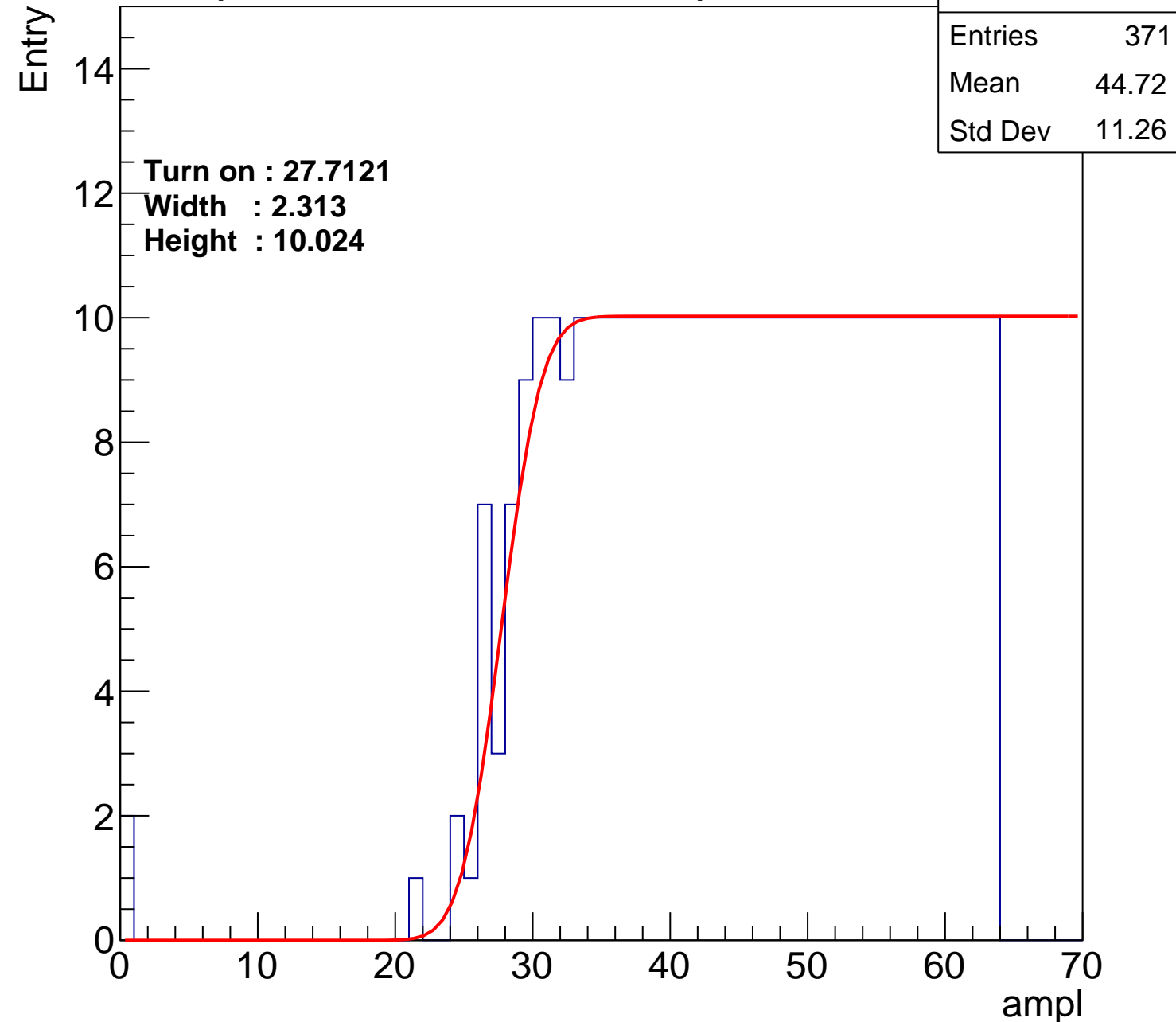
Width : 2.313

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch76

calib_packv5_042523_0143.root, FC#3, port B3

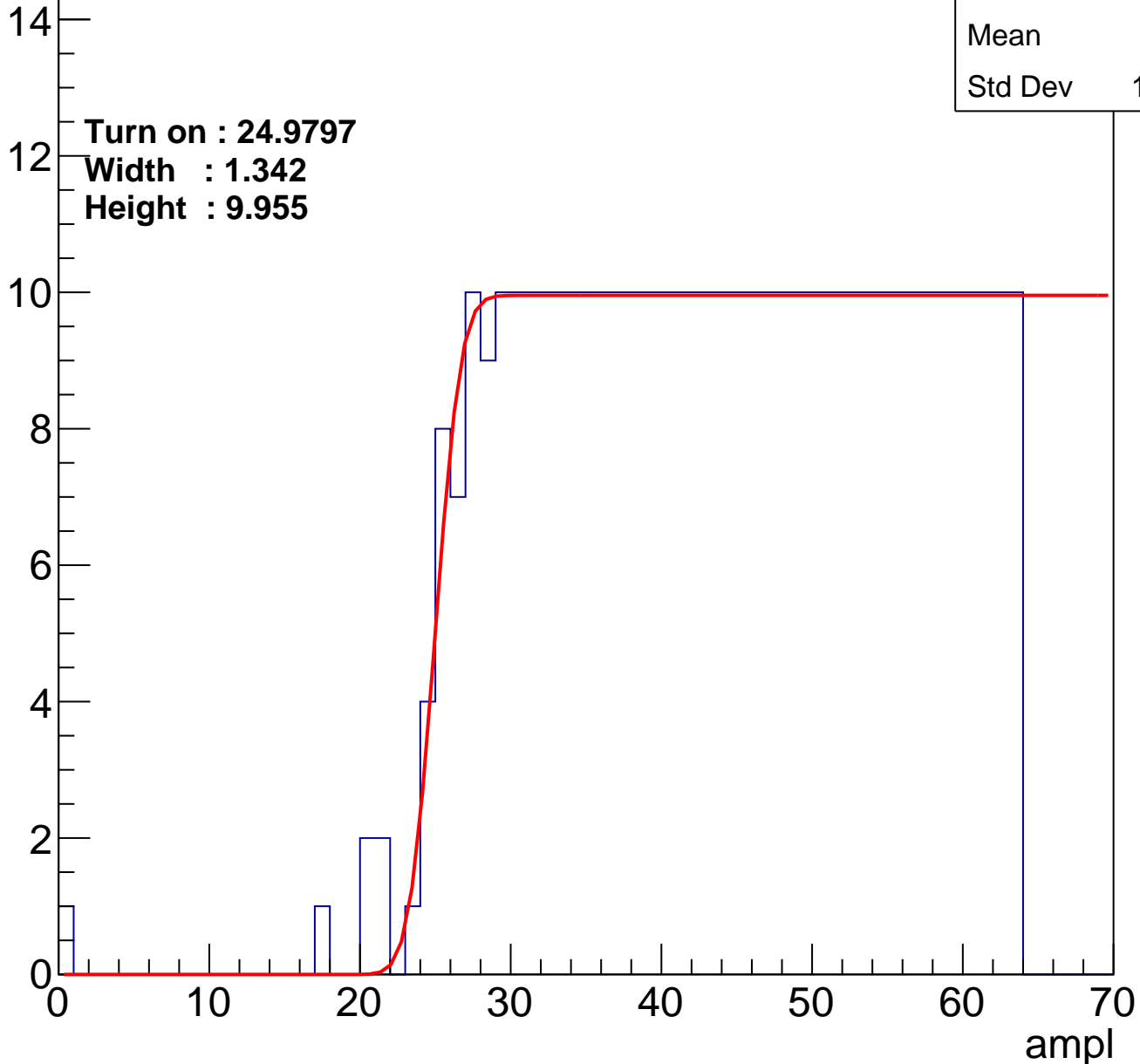
Entries	395
Mean	43.6
Std Dev	11.72

Turn on : 24.9797

Width : 1.342

Height : 9.955

Entry



B1L000S, U9-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.36
Std Dev	11.28

Turn on : 26.1520

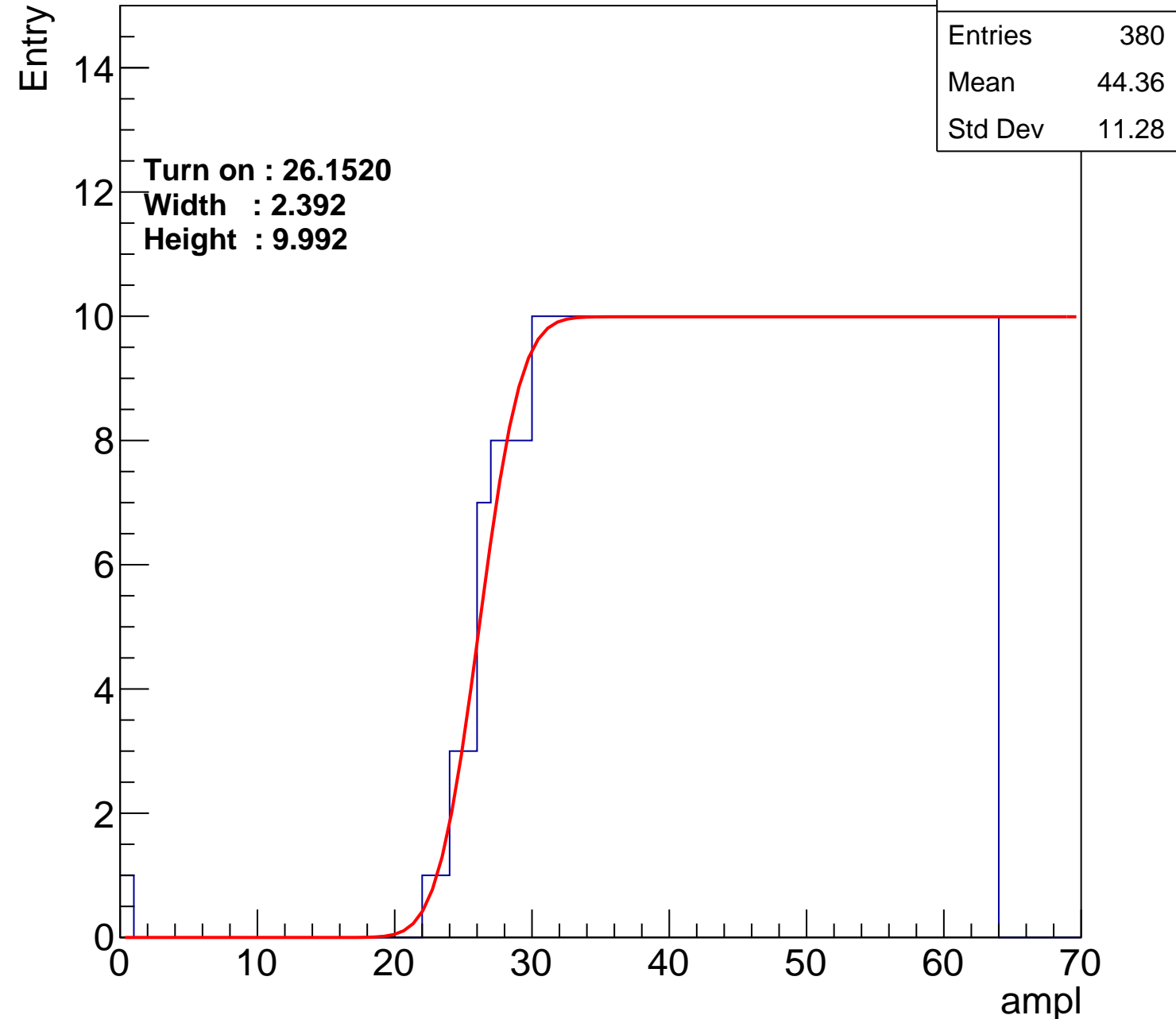
Width : 2.392

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch78

calib_packv5_042523_0143.root, FC#3, port B3

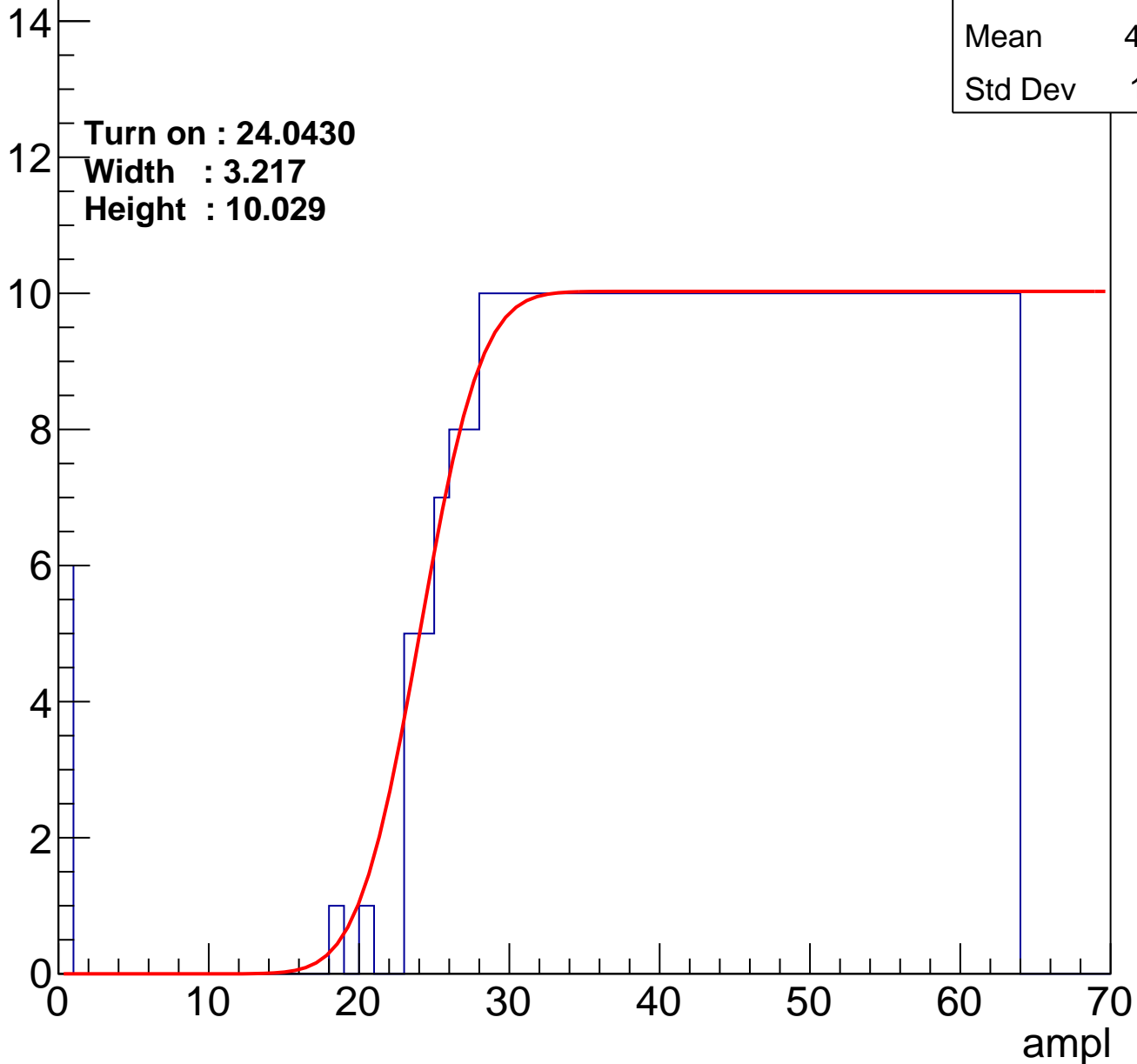
Entries	401
Mean	43.02
Std Dev	12.61

Turn on : 24.0430

Width : 3.217

Height : 10.029

Entry



B1L000S, U9-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.42
Std Dev	11.78

Turn on : 27.1135

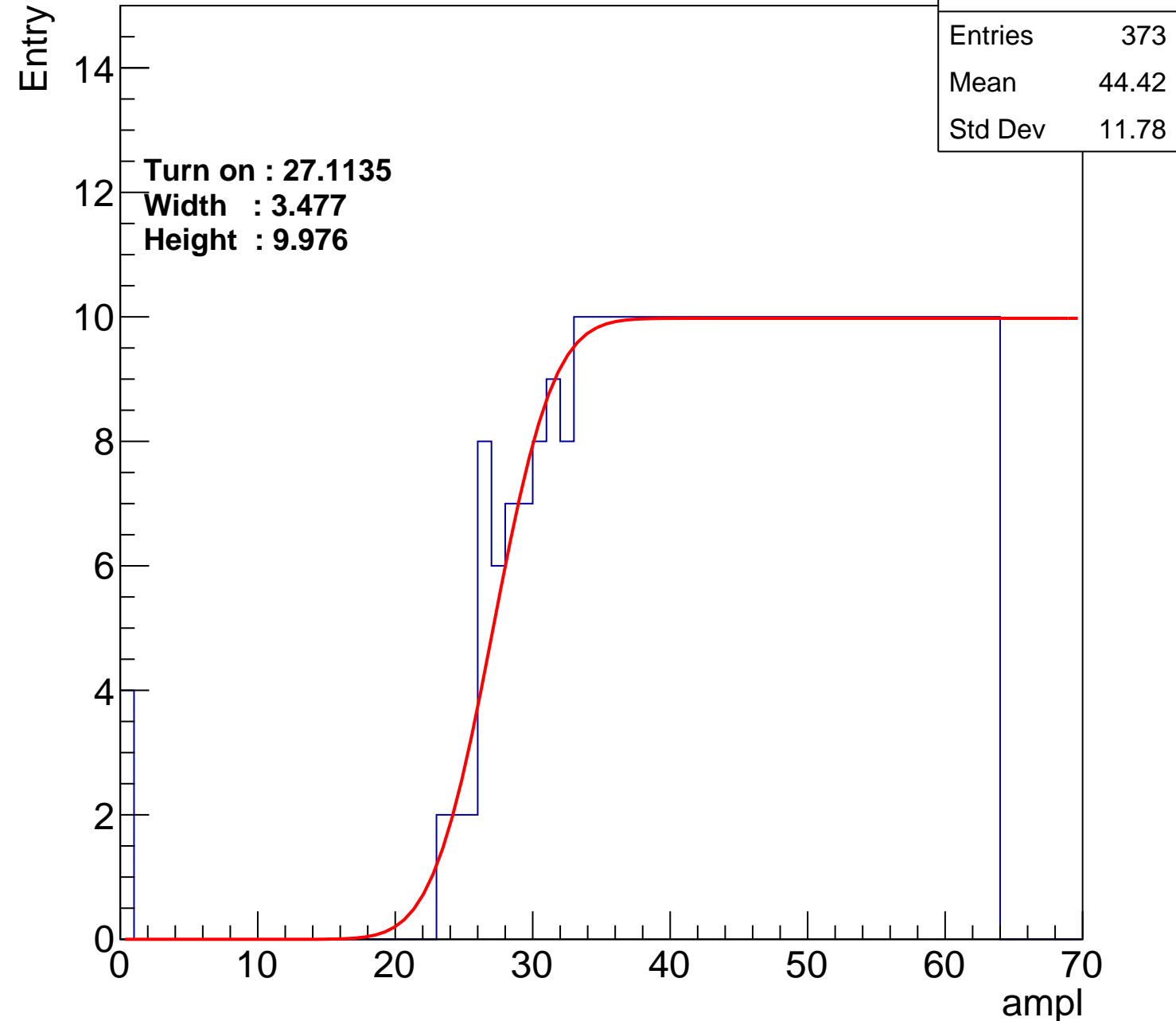
Width : 3.477

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch80

calib_packv5_042523_0143.root, FC#3, port B3

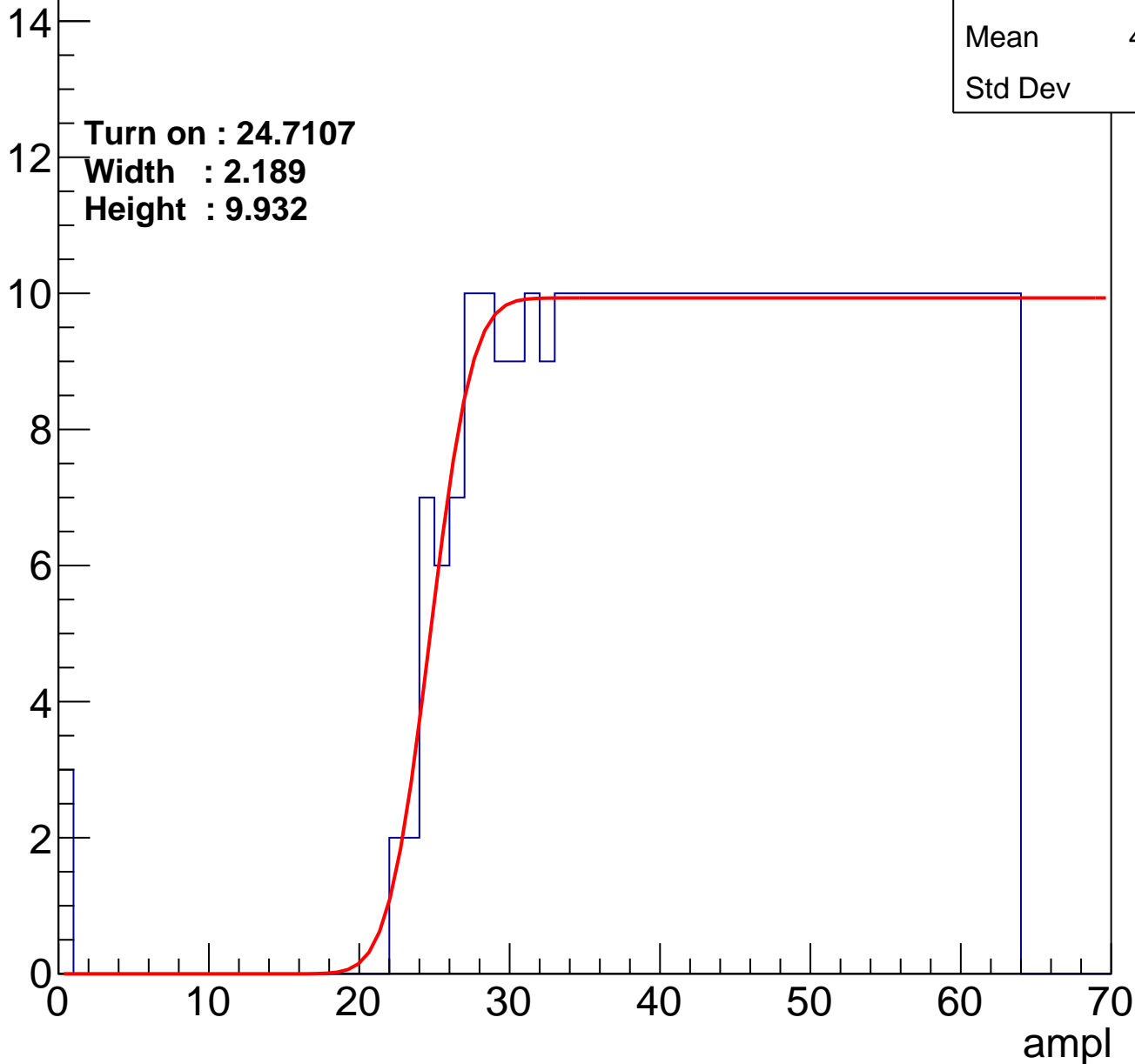
Entry

Entries	394
Mean	43.53
Std Dev	12.01

Turn on : 24.7107

Width : 2.189

Height : 9.932



B1L000S, U9-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	408
Mean	42.92
Std Dev	12.19

Turn on : 23.7231

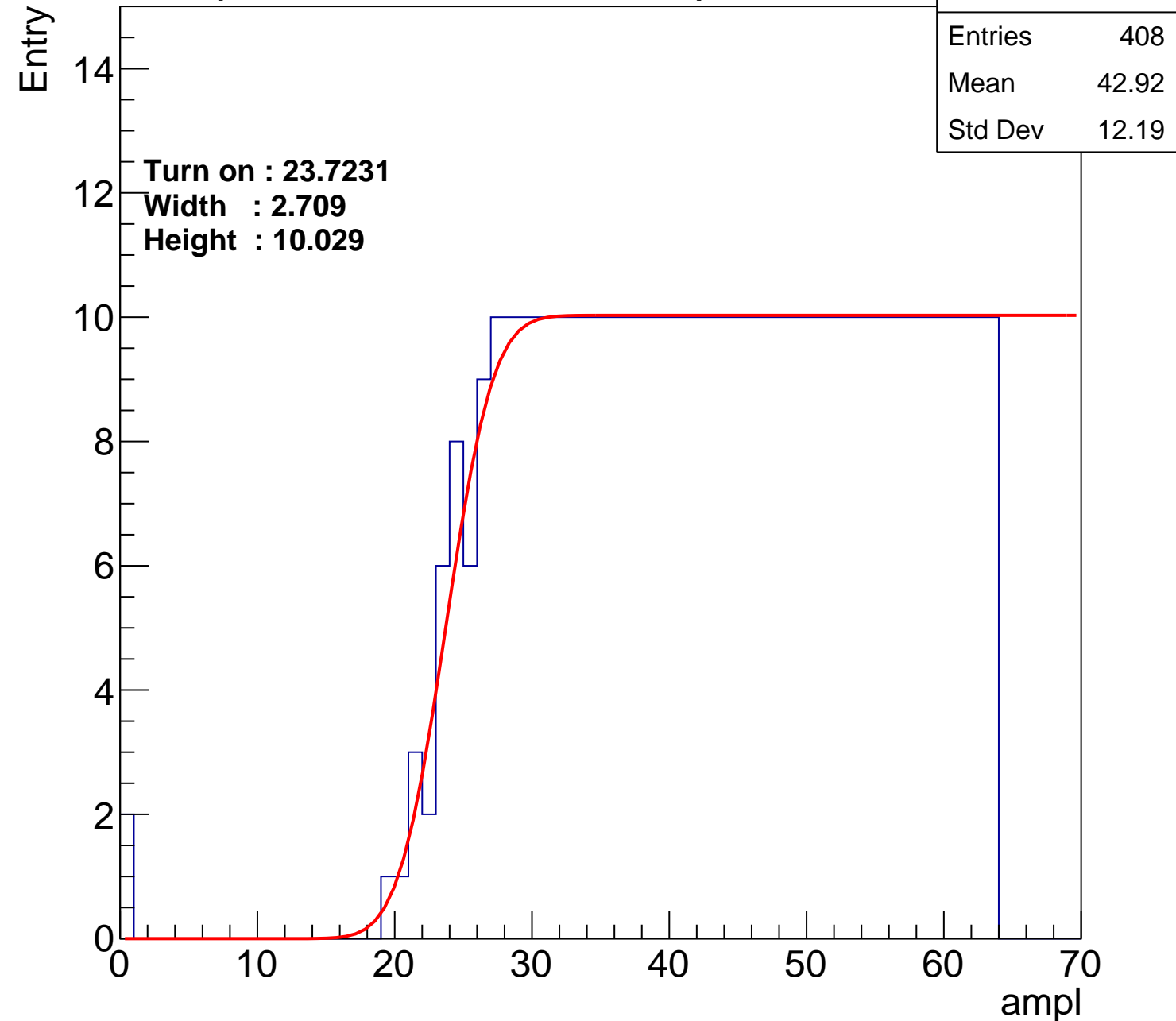
Width : 2.709

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.5
Std Dev	12.12

Turn on : 24.9640

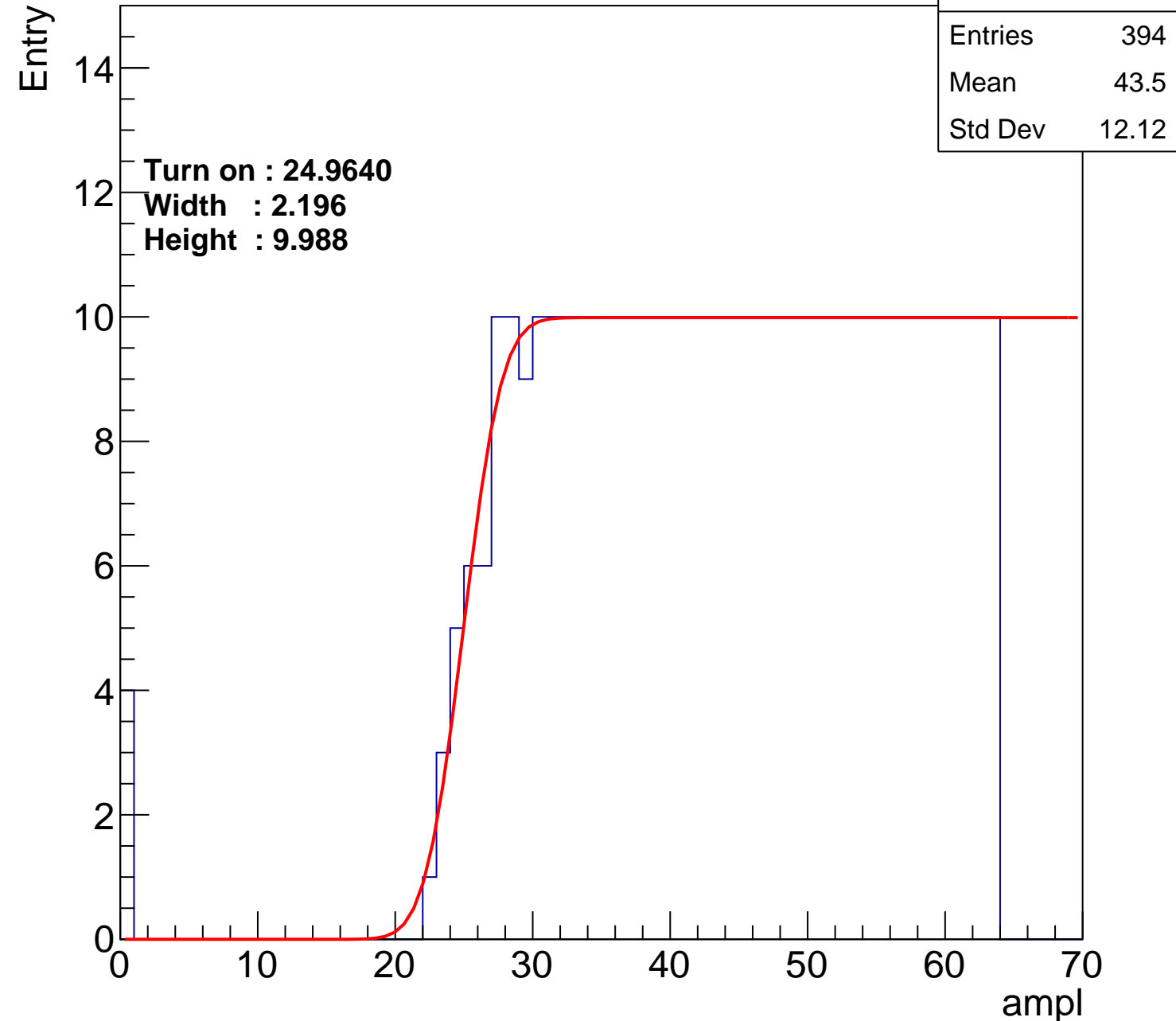
Width : 2.196

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch83

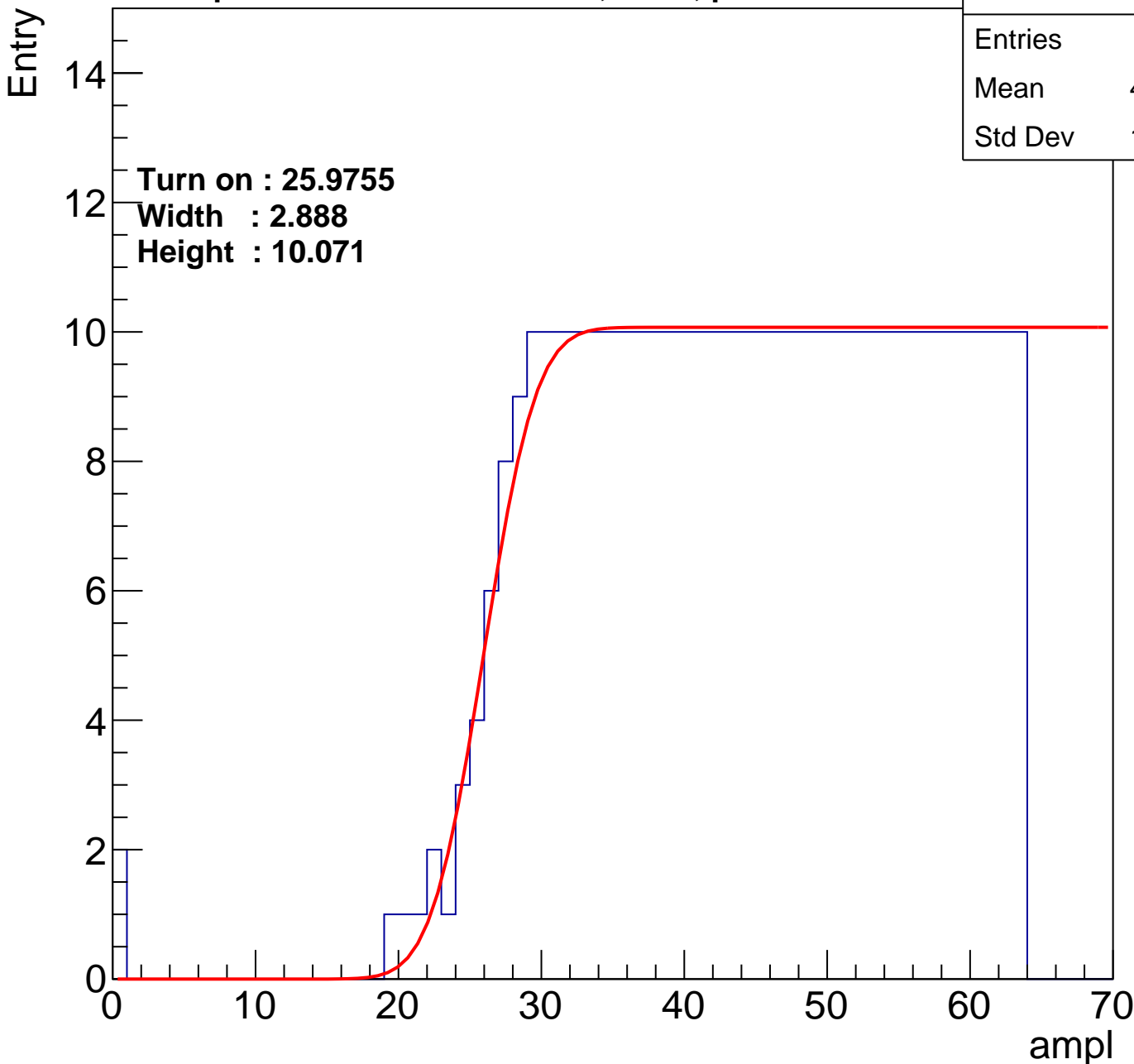
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.9755

Width : 2.888

Height : 10.071

Entries	388
Mean	43.87
Std Dev	11.72



B1L000S, U9-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.68
Std Dev	11.46

Turn on : 27.2573

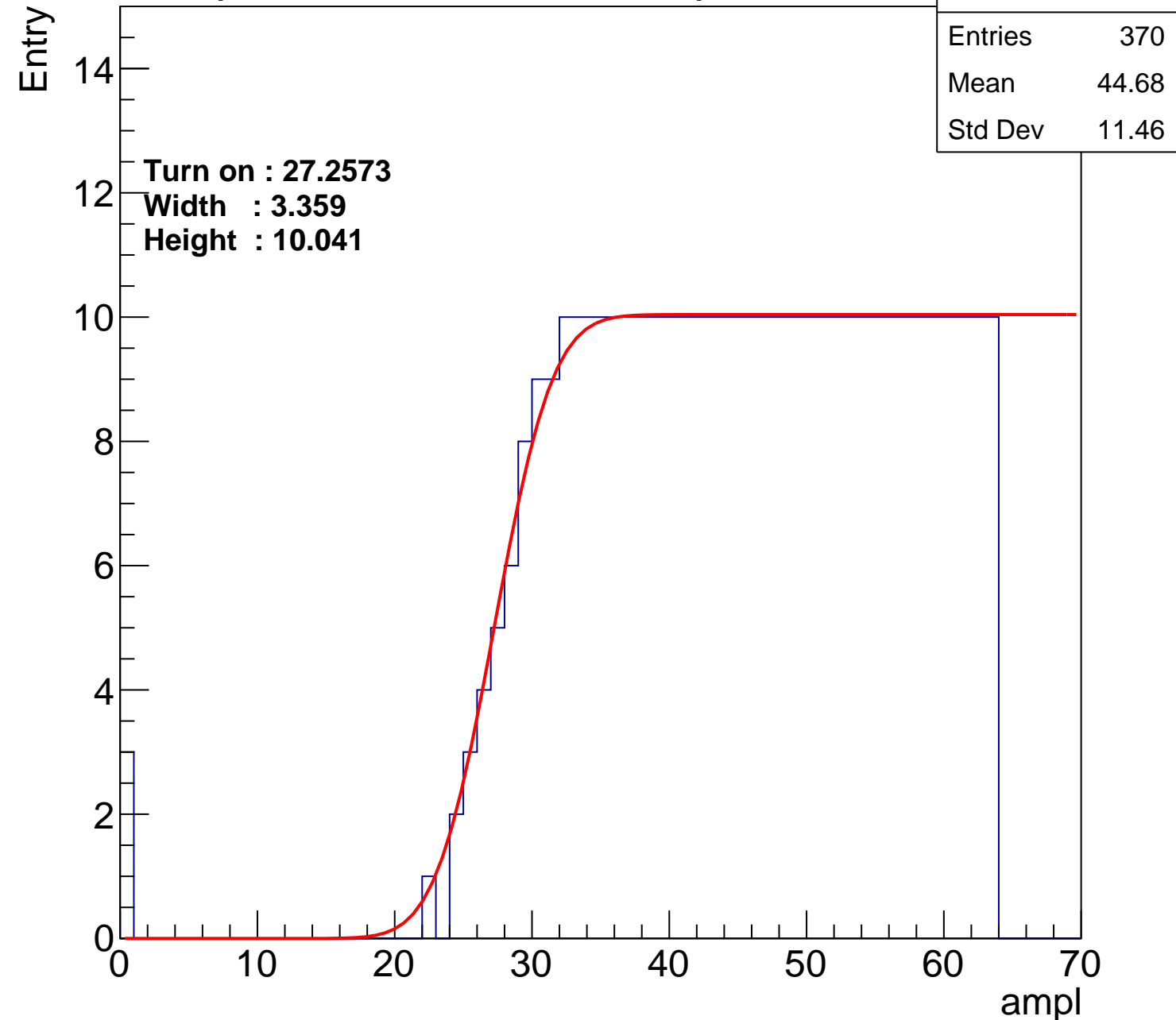
Width : 3.359

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.32
Std Dev	10.74

Turn on : 28.0062

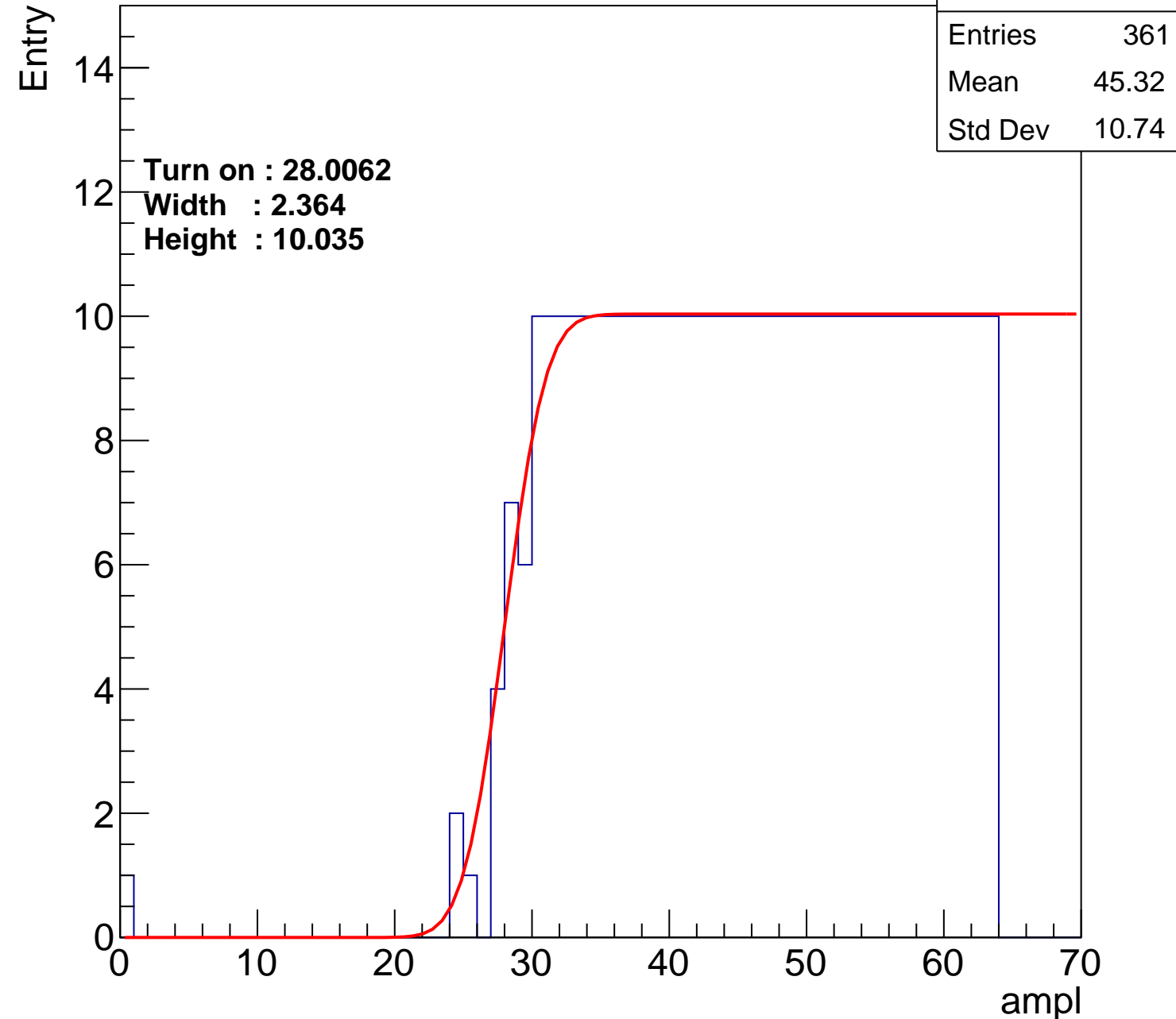
Width : 2.364

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 27.3188

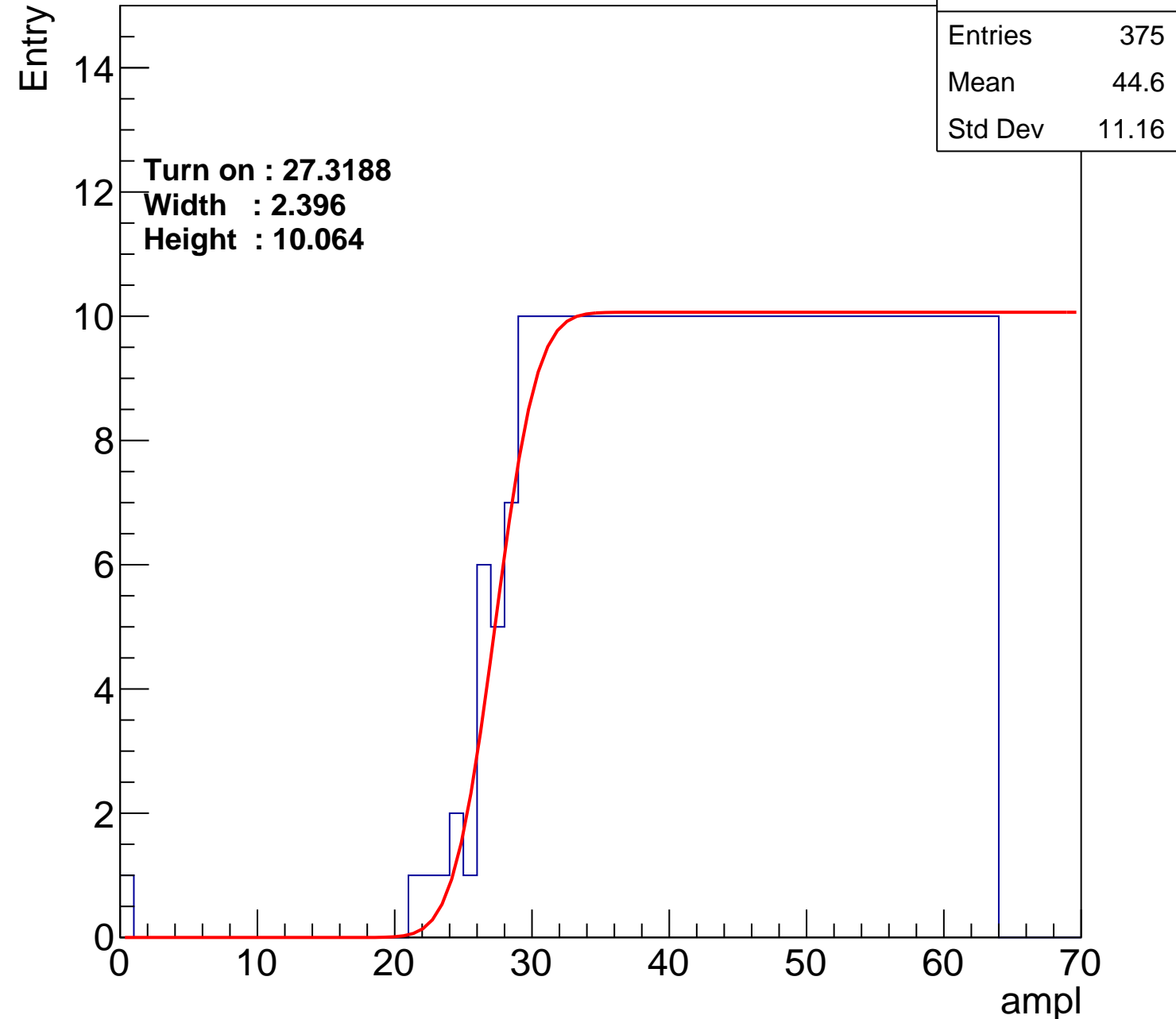
Width : 2.396

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries 371

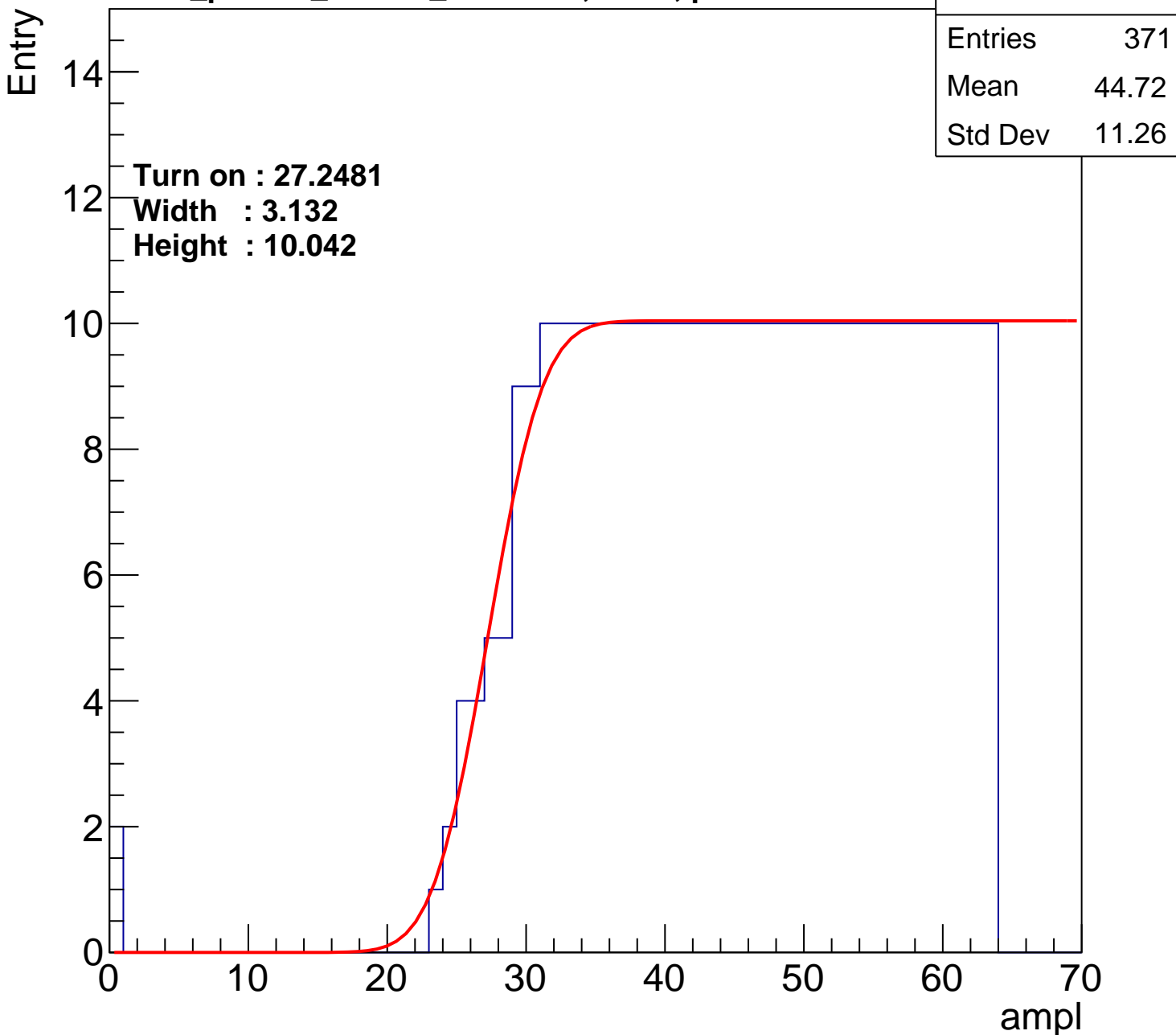
Mean 44.72

Std Dev 11.26

Turn on : 27.2481

Width : 3.132

Height : 10.042



B1L000S, U9-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.81
Std Dev	11.83

Turn on : 25.4522

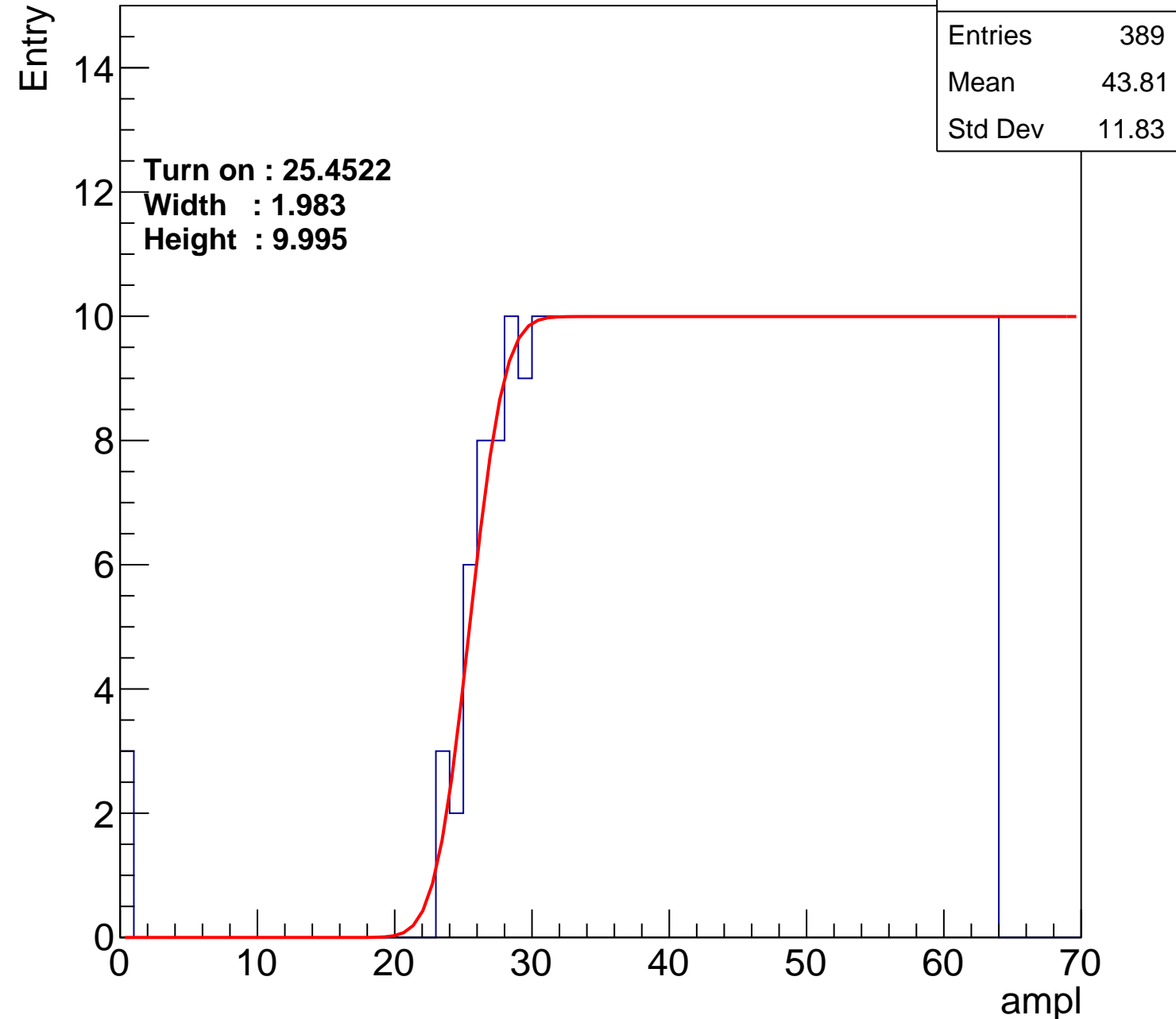
Width : 1.983

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.4
Std Dev	12.06

Turn on : 24.6780

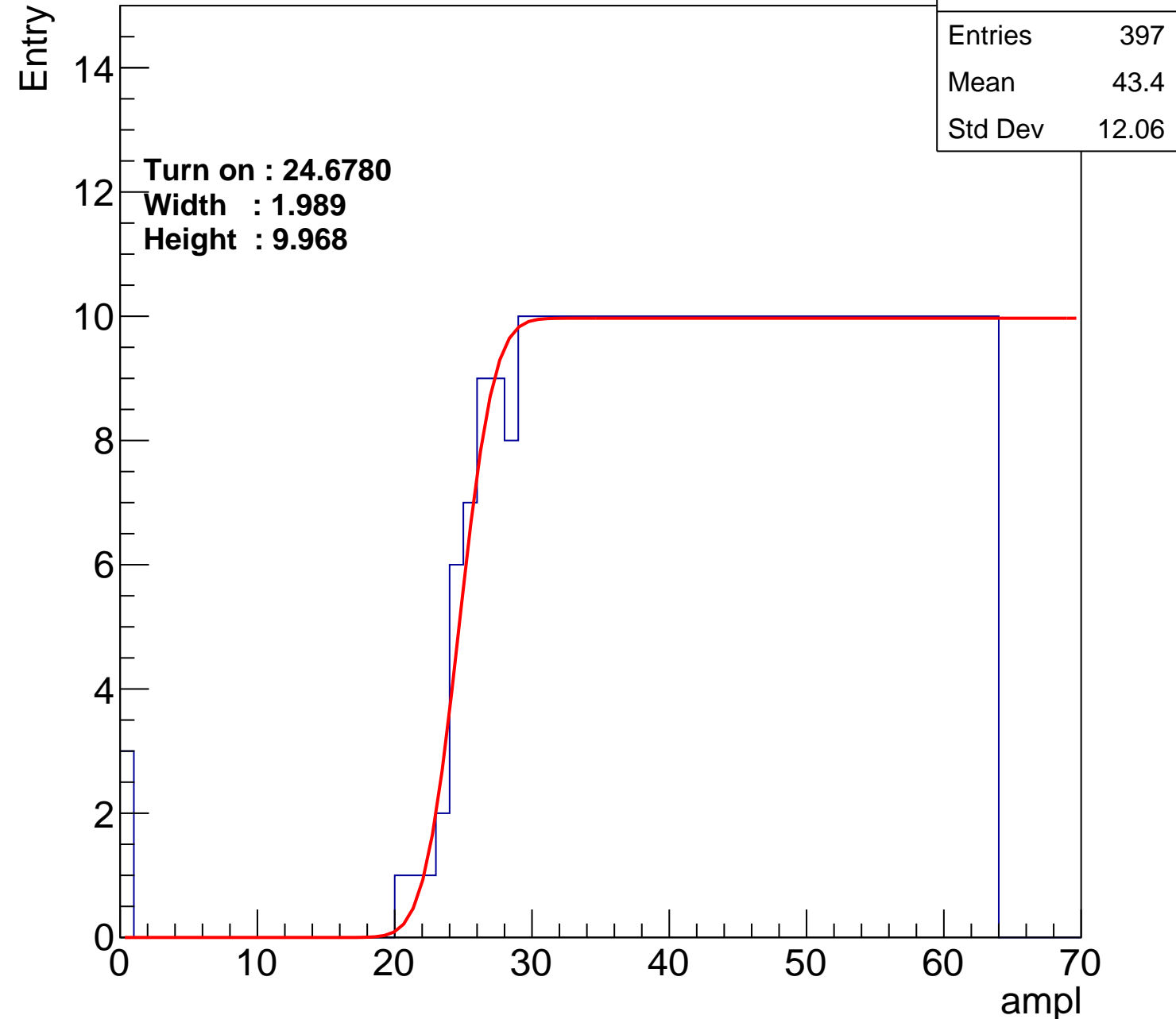
Width : 1.989

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.41
Std Dev	11.92

Turn on : 24.6740

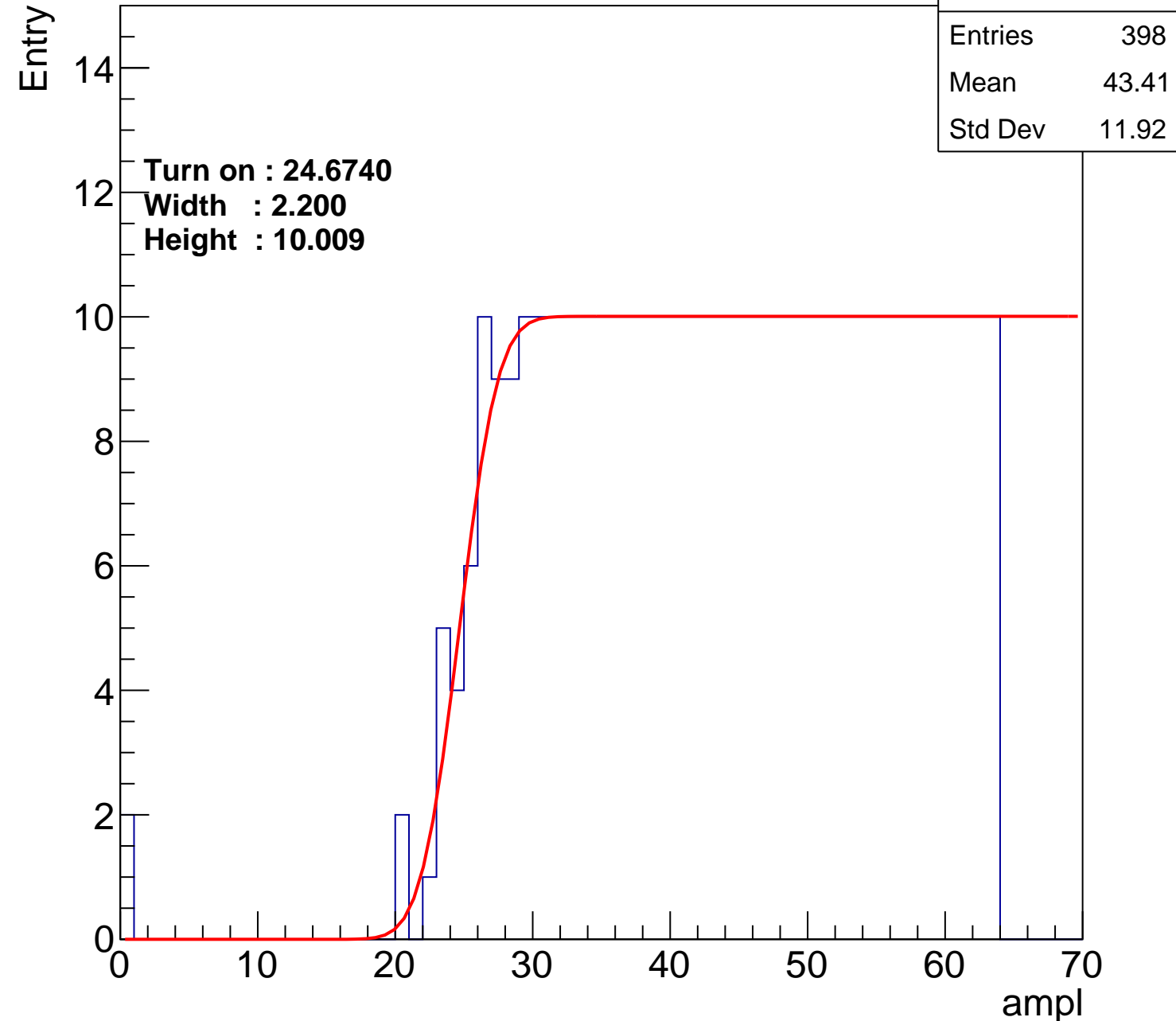
Width : 2.200

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.16
Std Dev	11.83

Turn on : 25.9945

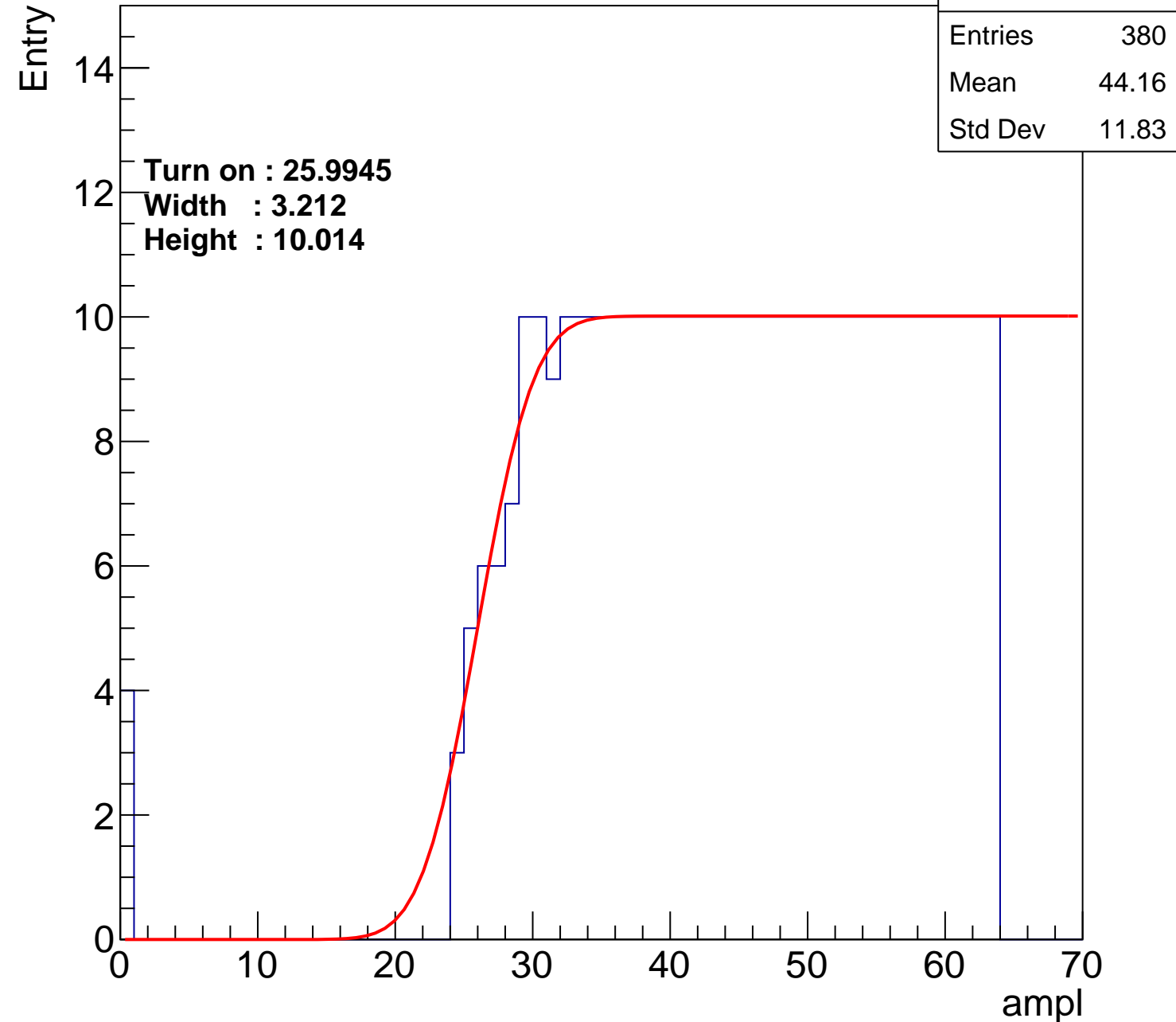
Width : 3.212

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.72
Std Dev	12.05

Turn on : 25.6263

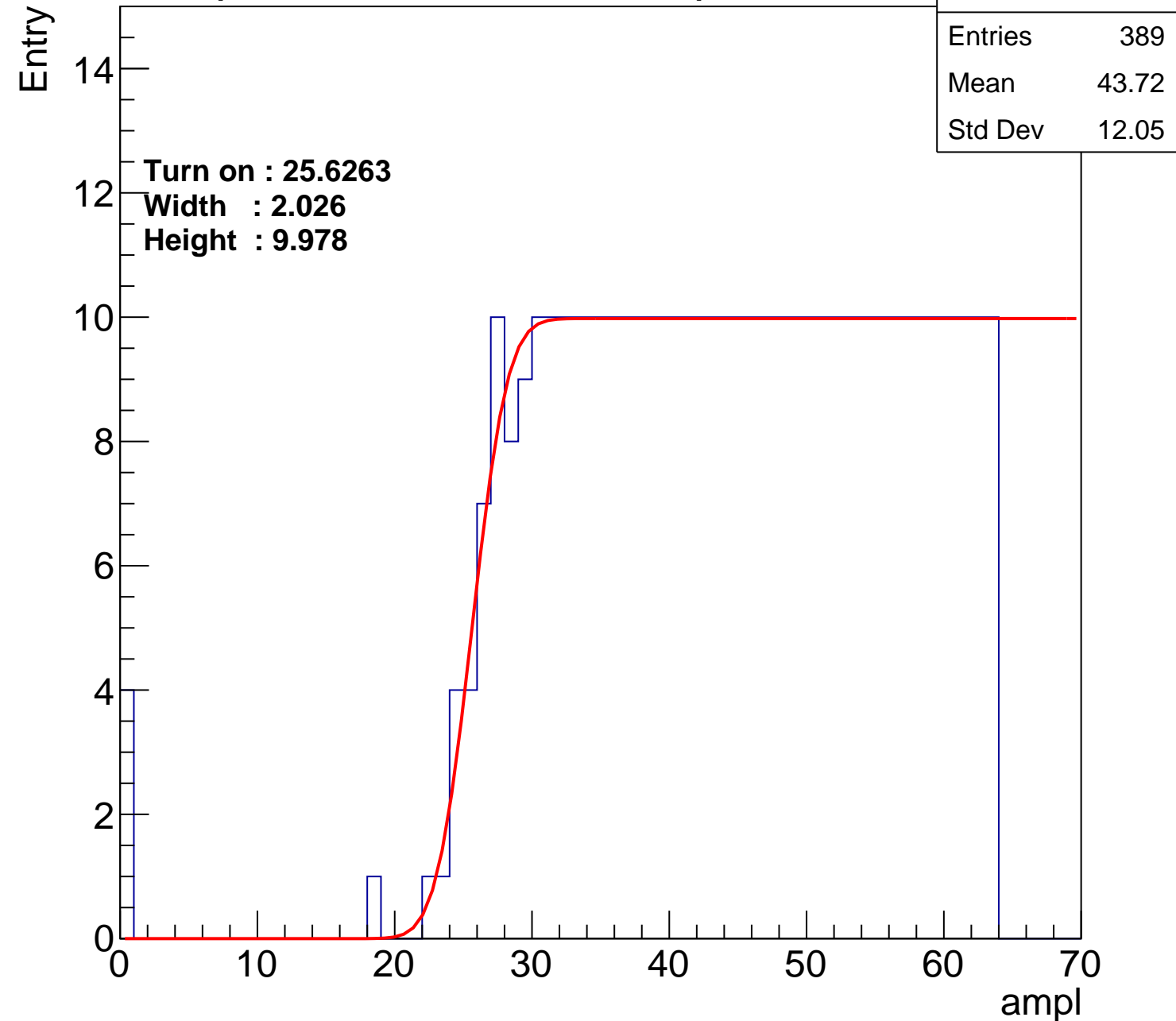
Width : 2.026

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.63
Std Dev	11.29

Turn on : 27.0051

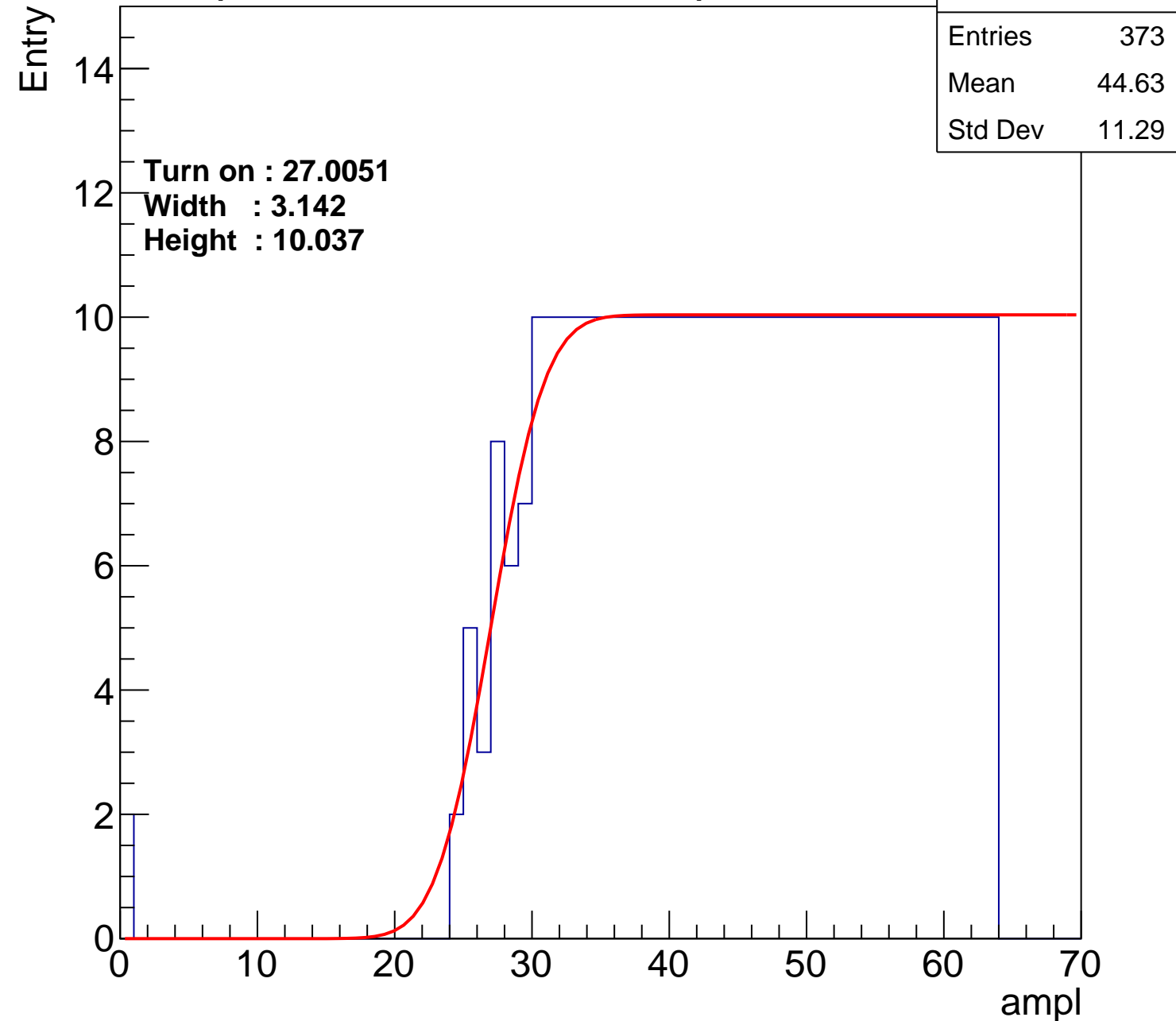
Width : 3.142

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 25.9836

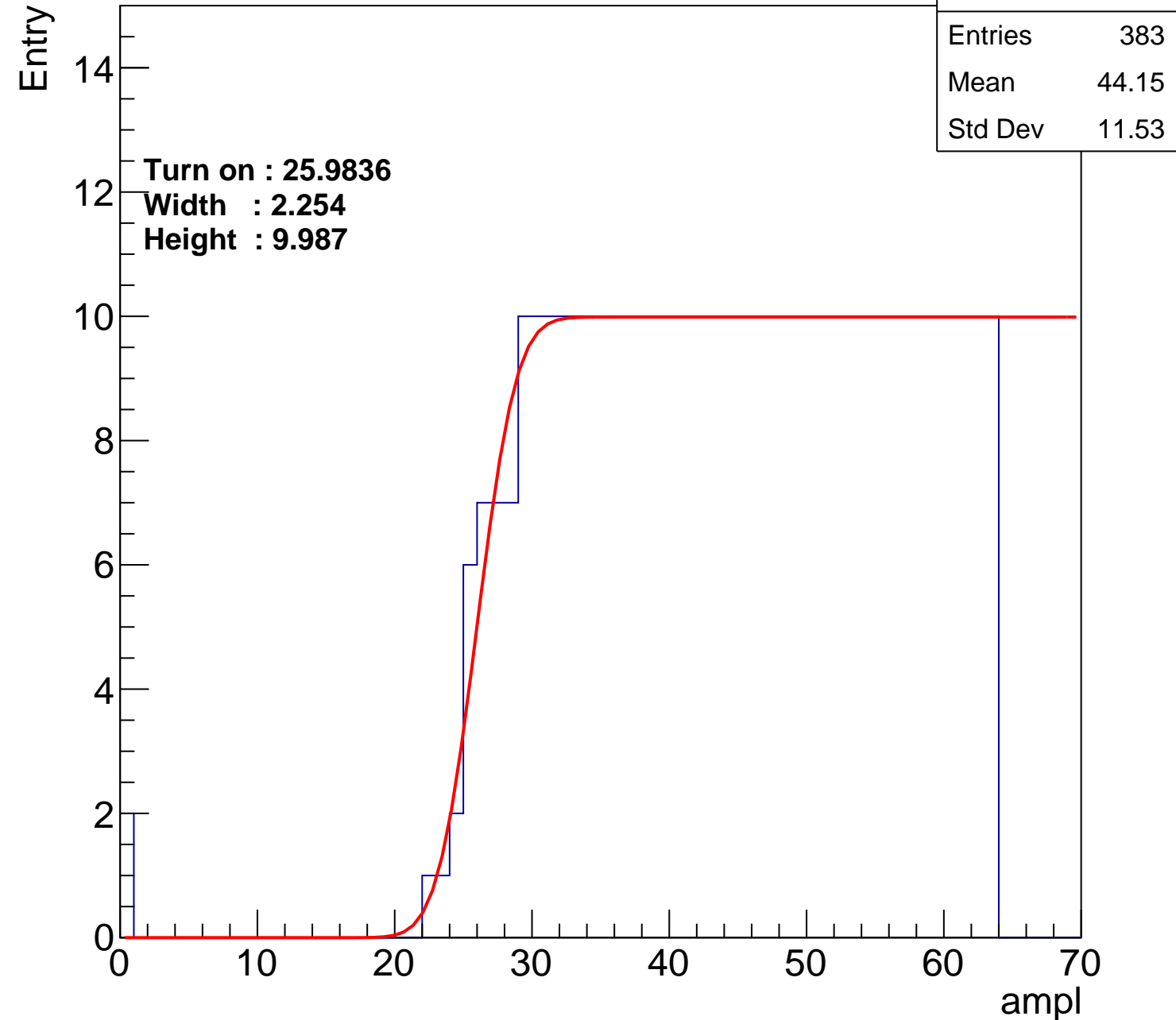
Width : 2.254

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 25.8581

Width : 4.423

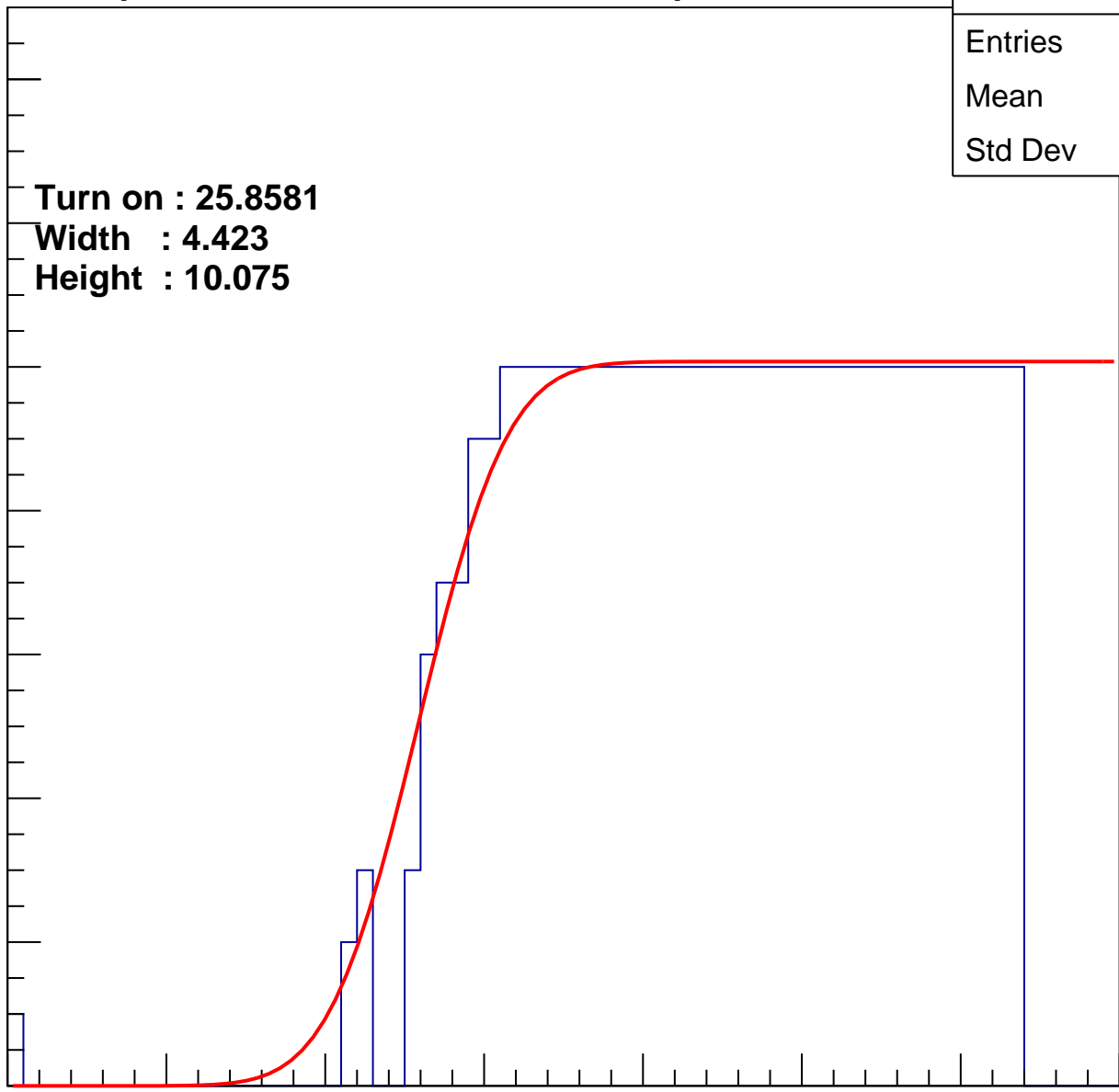
Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U9-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.54
Std Dev	11.99

Turn on : 24.8933

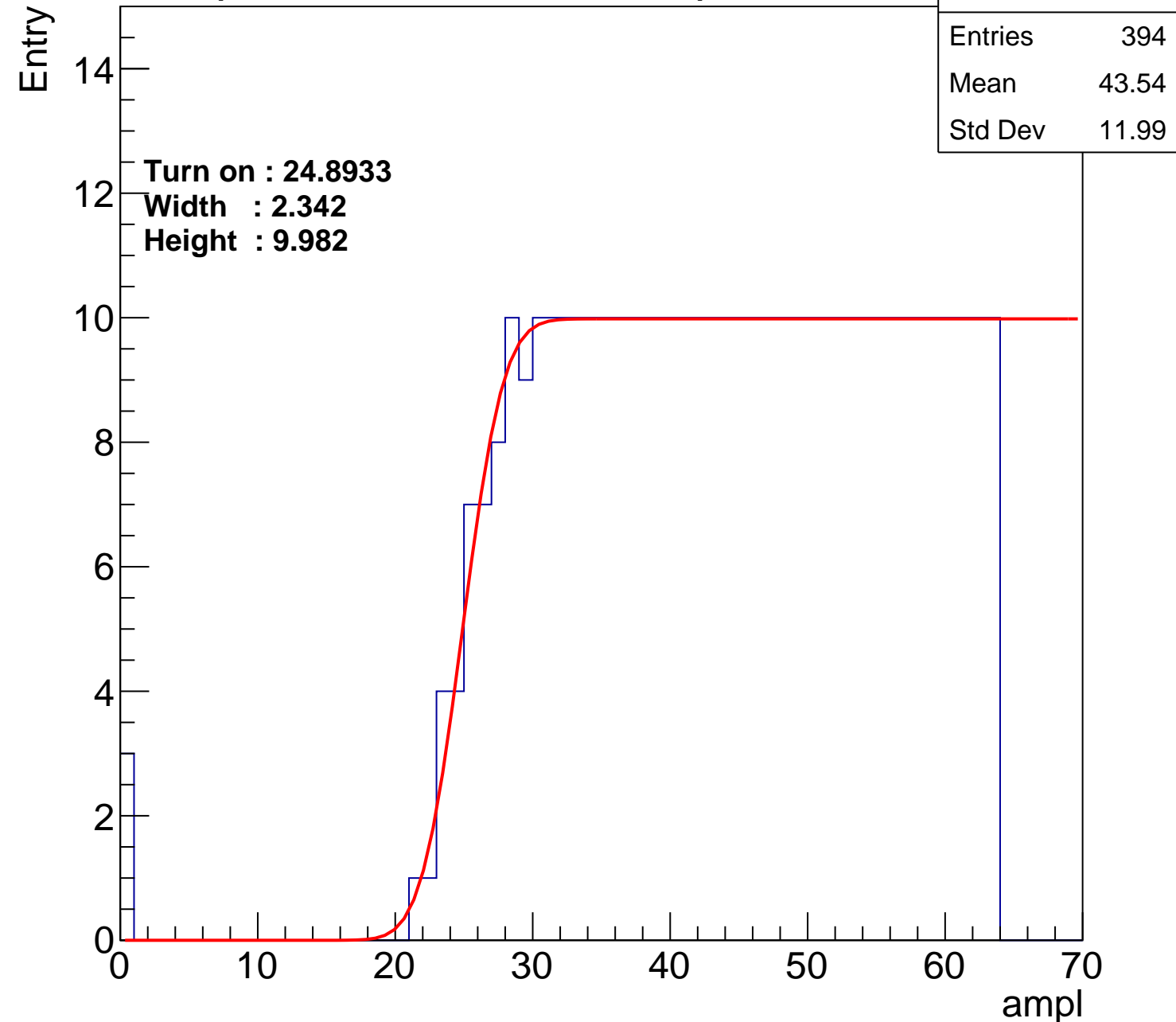
Width : 2.342

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 24.0103

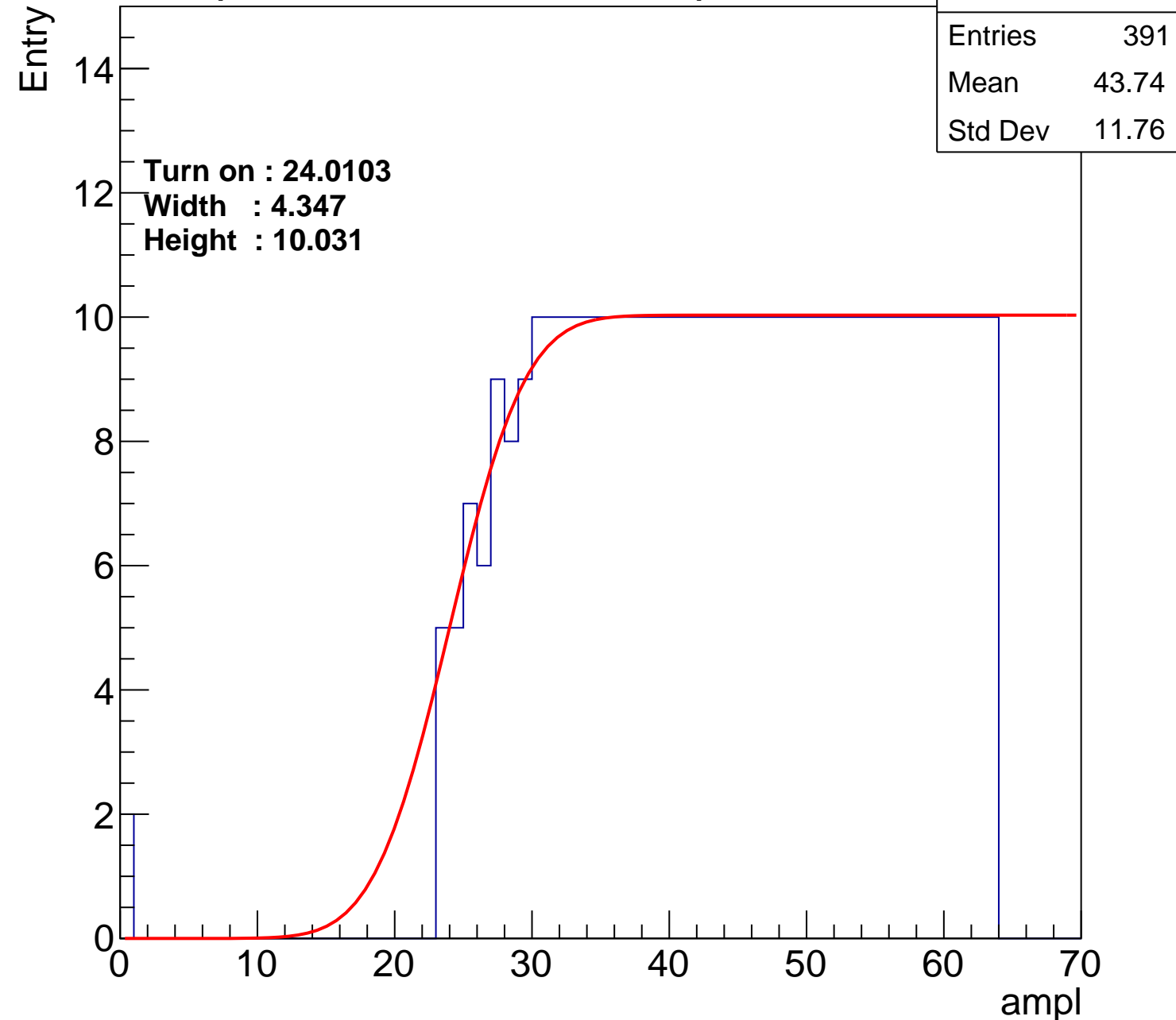
Width : 4.347

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch98

calib_packv5_042523_0143.root, FC#3, port B3

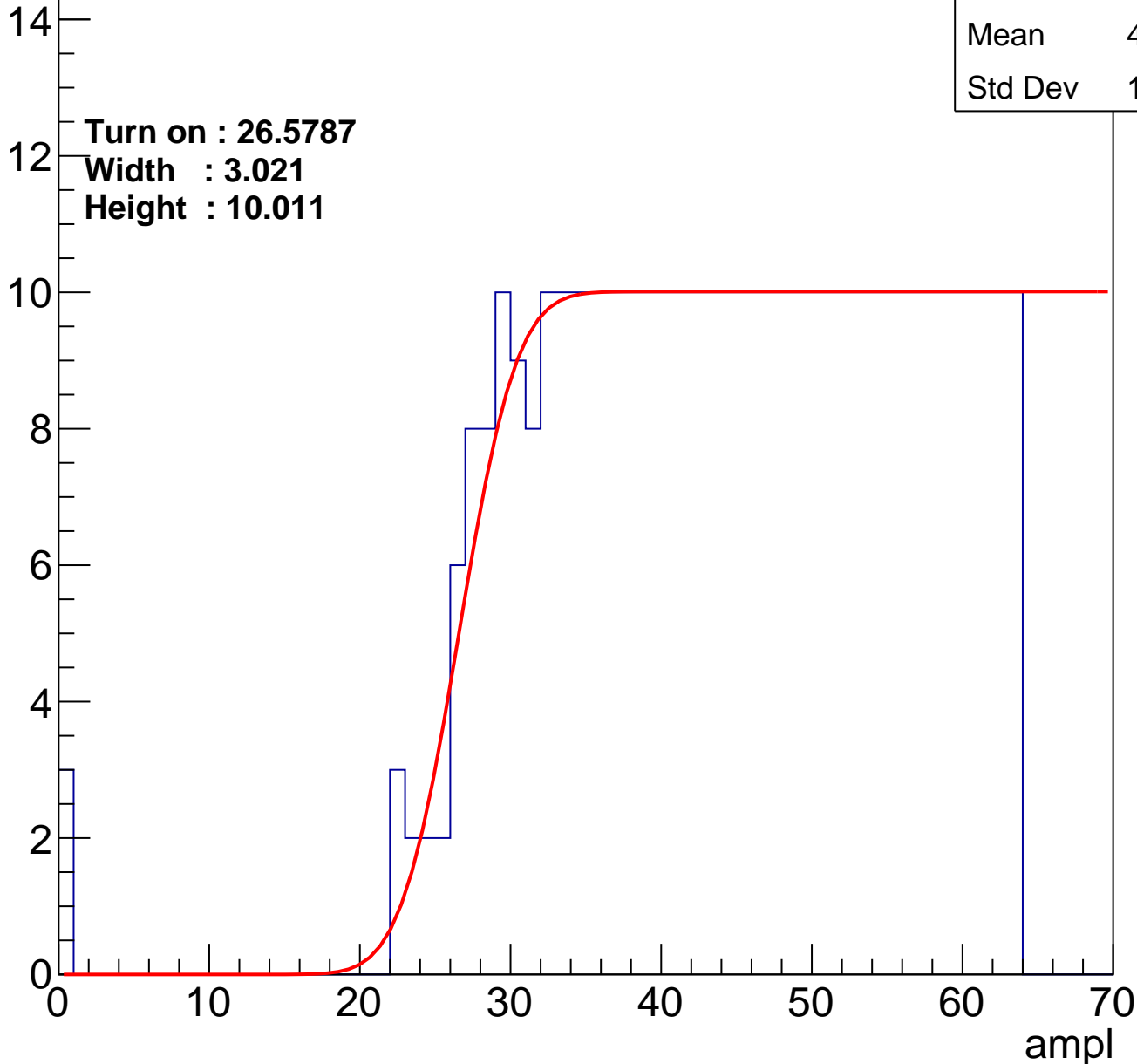
Entry

Entries	381
Mean	44.13
Std Dev	11.74

Turn on : 26.5787

Width : 3.021

Height : 10.011



B1L000S, U9-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 26.4970

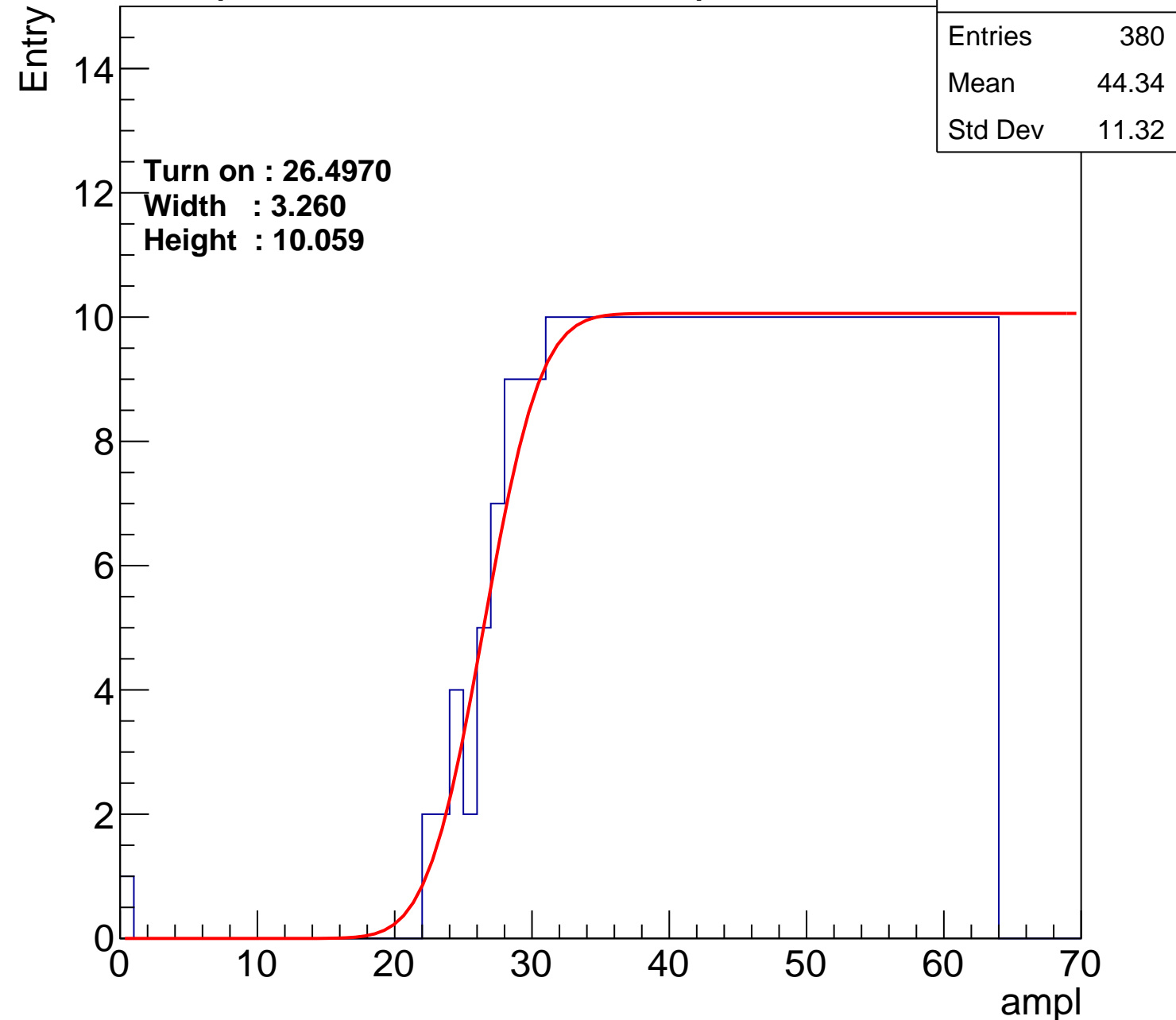
Width : 3.260

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch100

calib_packv5_042523_0143.root, FC#3, port B3

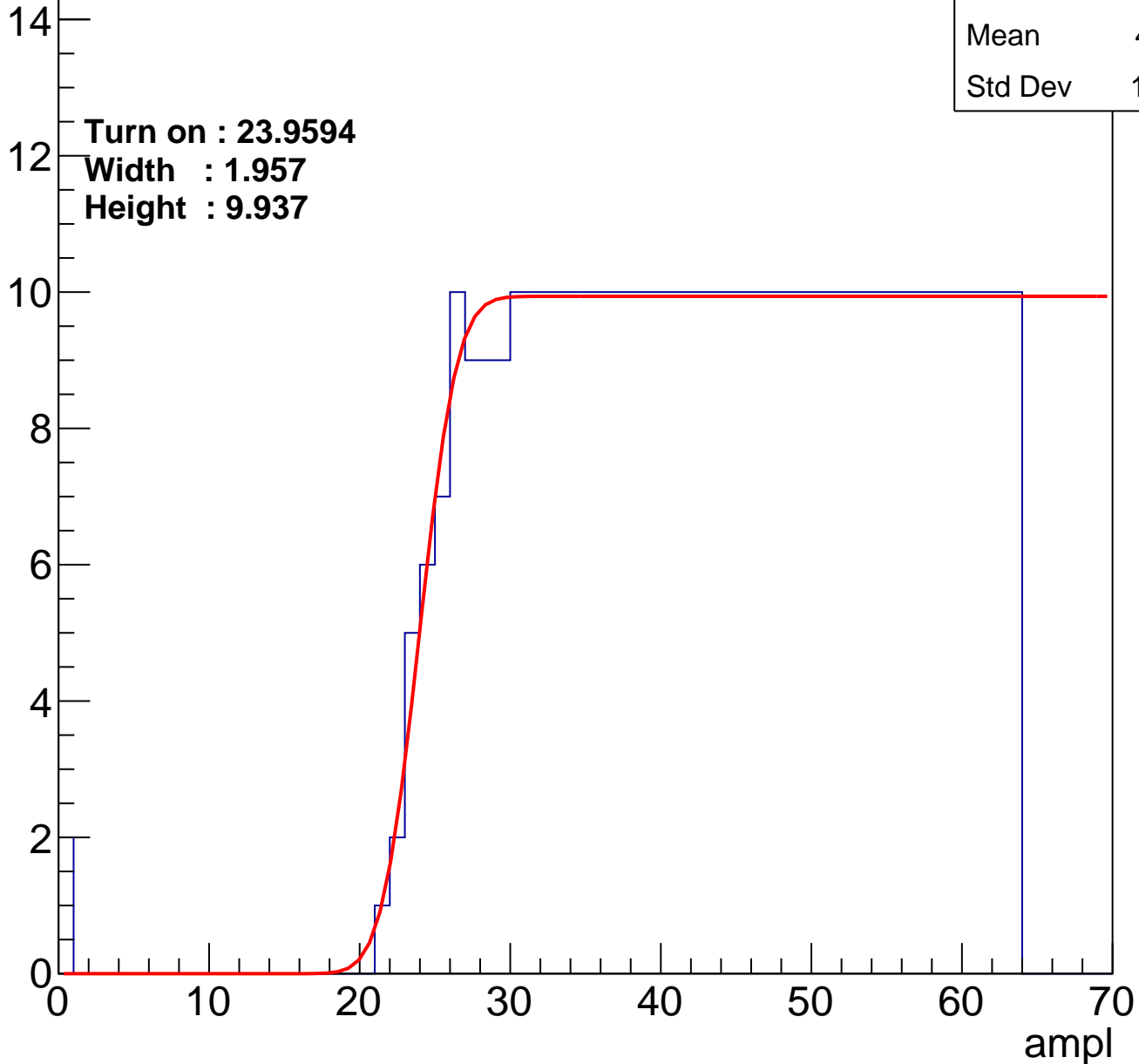
Entries	400
Mean	43.31
Std Dev	11.97

Turn on : 23.9594

Width : 1.957

Height : 9.937

Entry



B1L000S, U9-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.82
Std Dev	12.05

Turn on : 26.3308

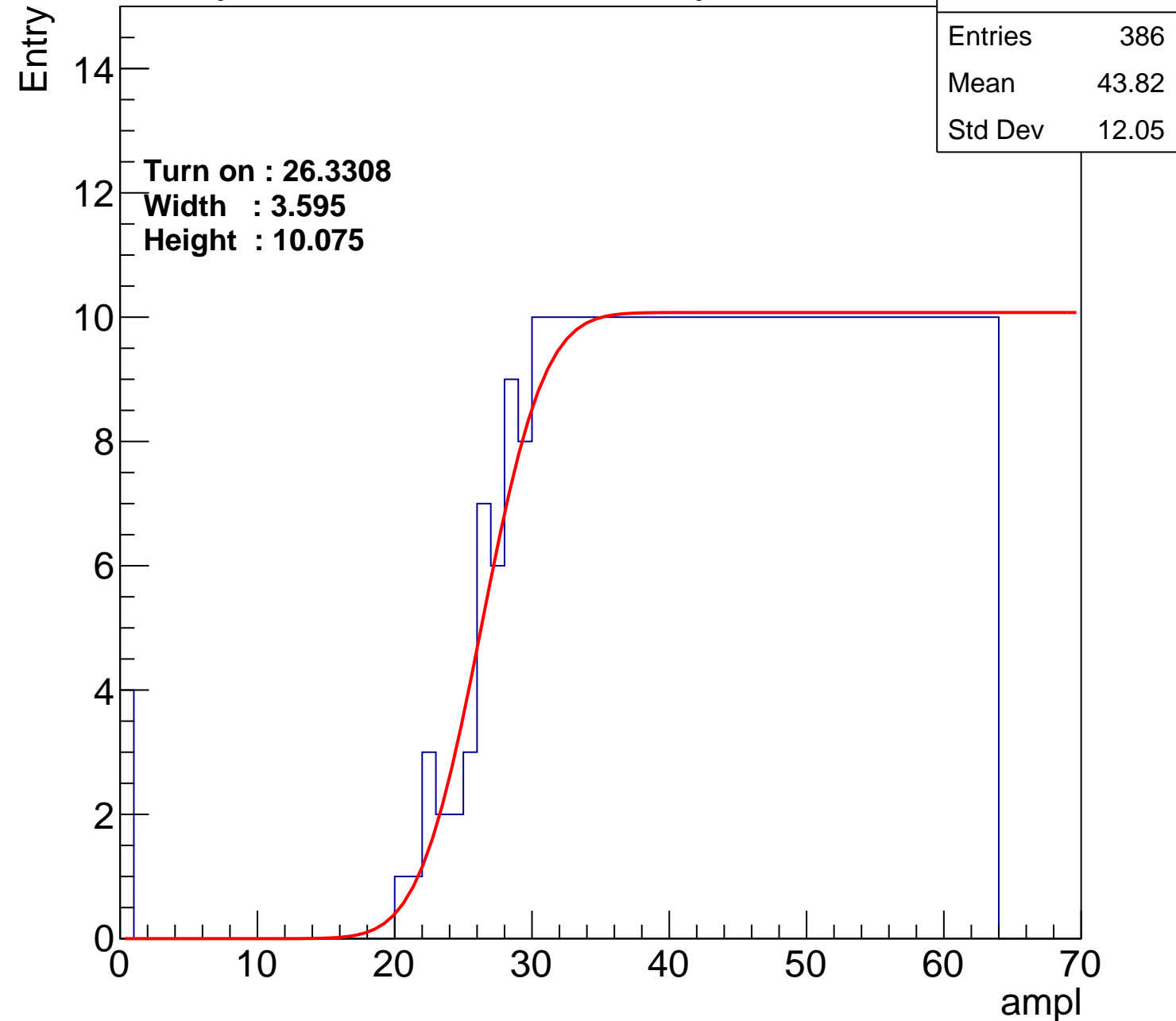
Width : 3.595

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.72
Std Dev	11.92

Turn on : 25.2611

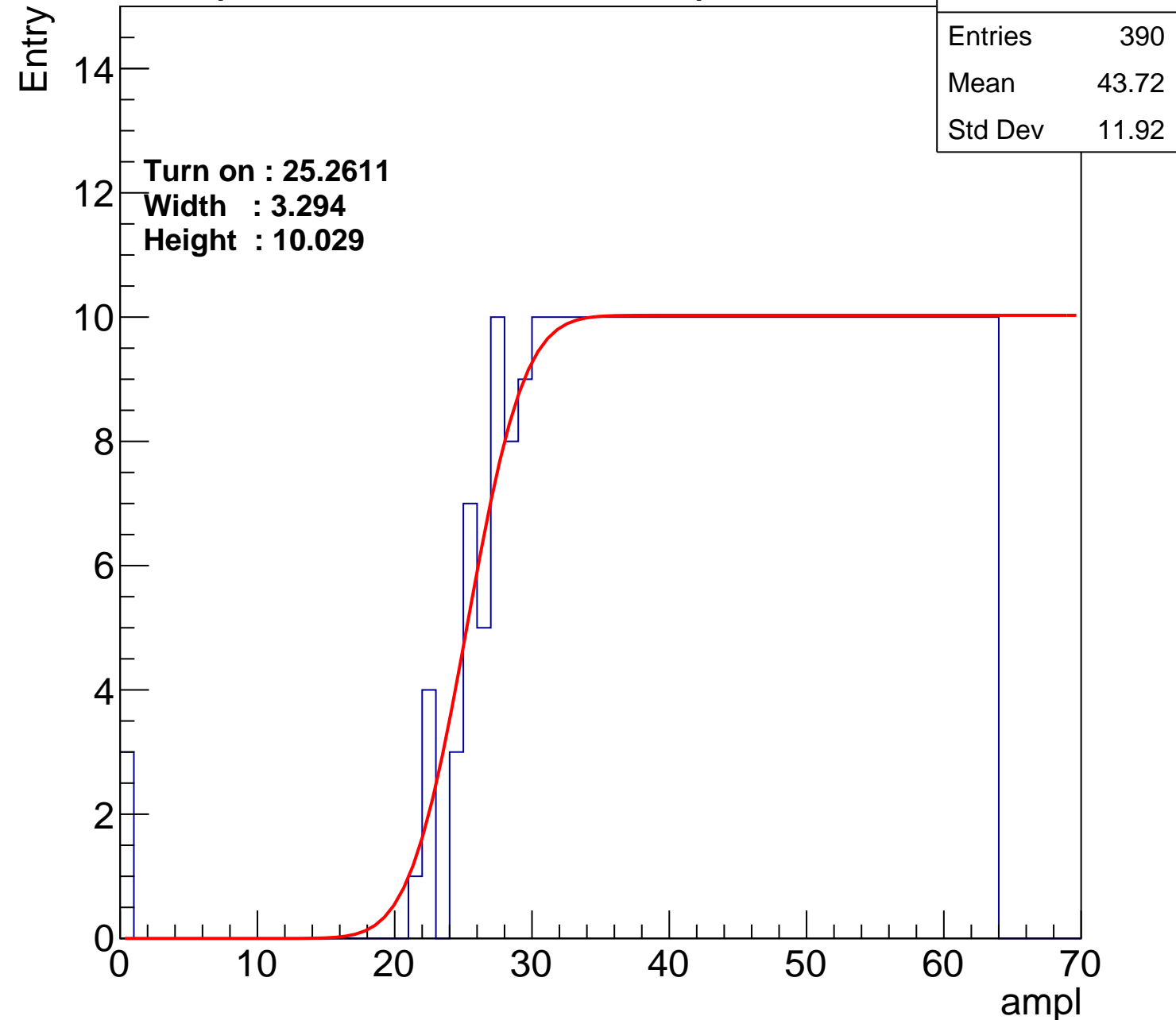
Width : 3.294

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch103

calib_packv5_042523_0143.root, FC#3, port B3

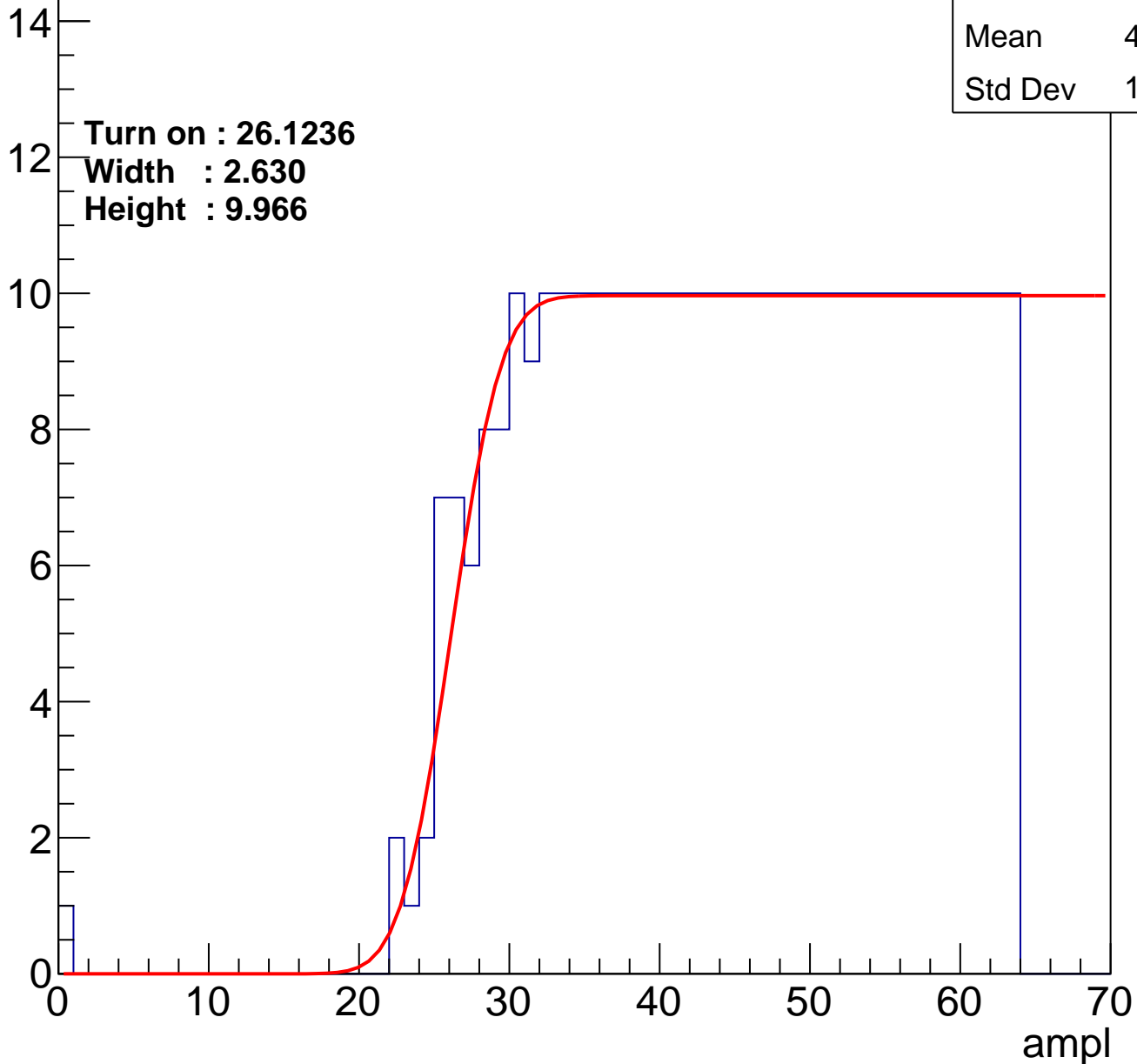
Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.1236

Width : 2.630

Height : 9.966

Entry



B1L000S, U9-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.9
Std Dev	11.79

Turn on : 25.4146

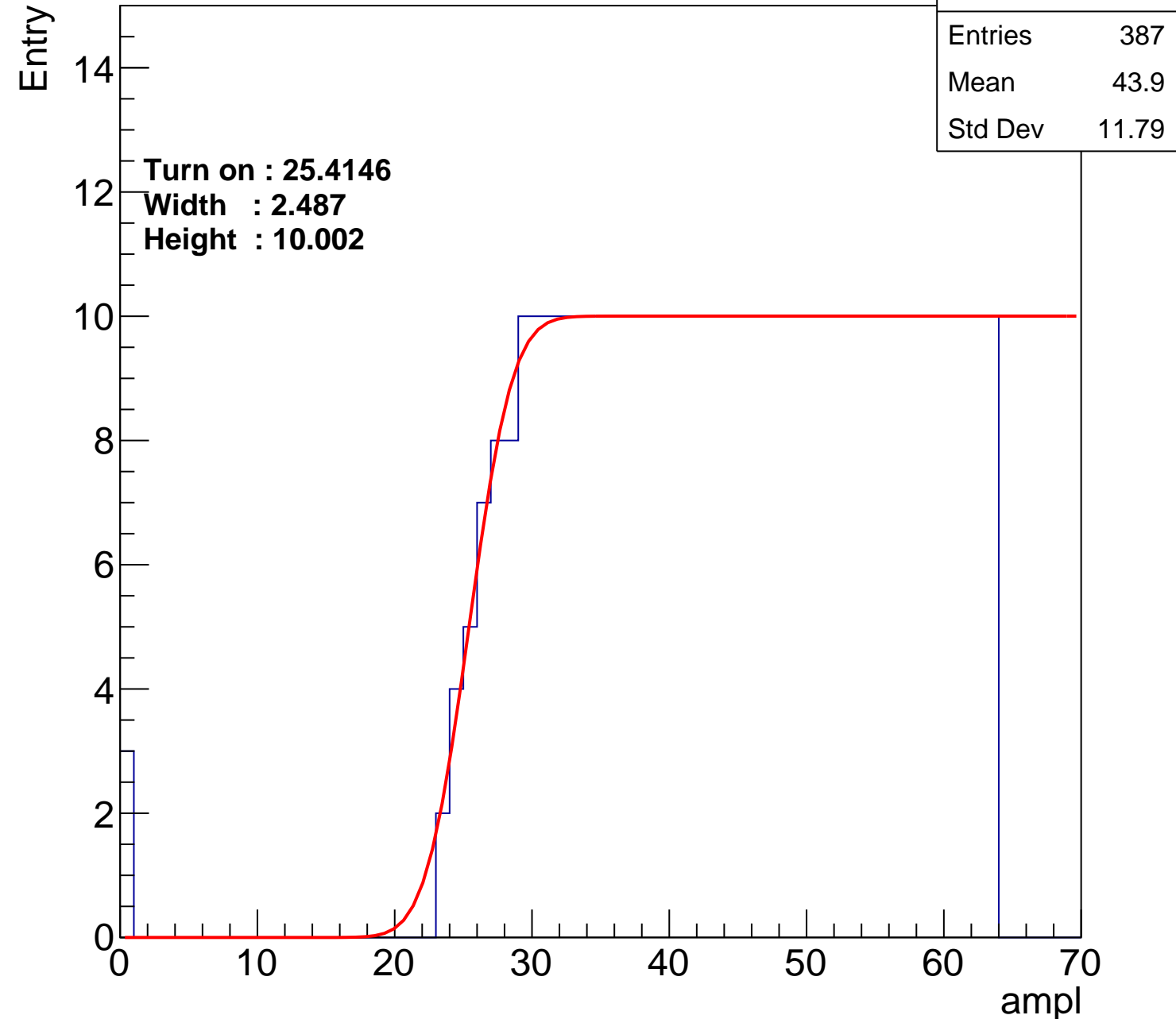
Width : 2.487

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.89
Std Dev	12.15

Turn on : 25.8029

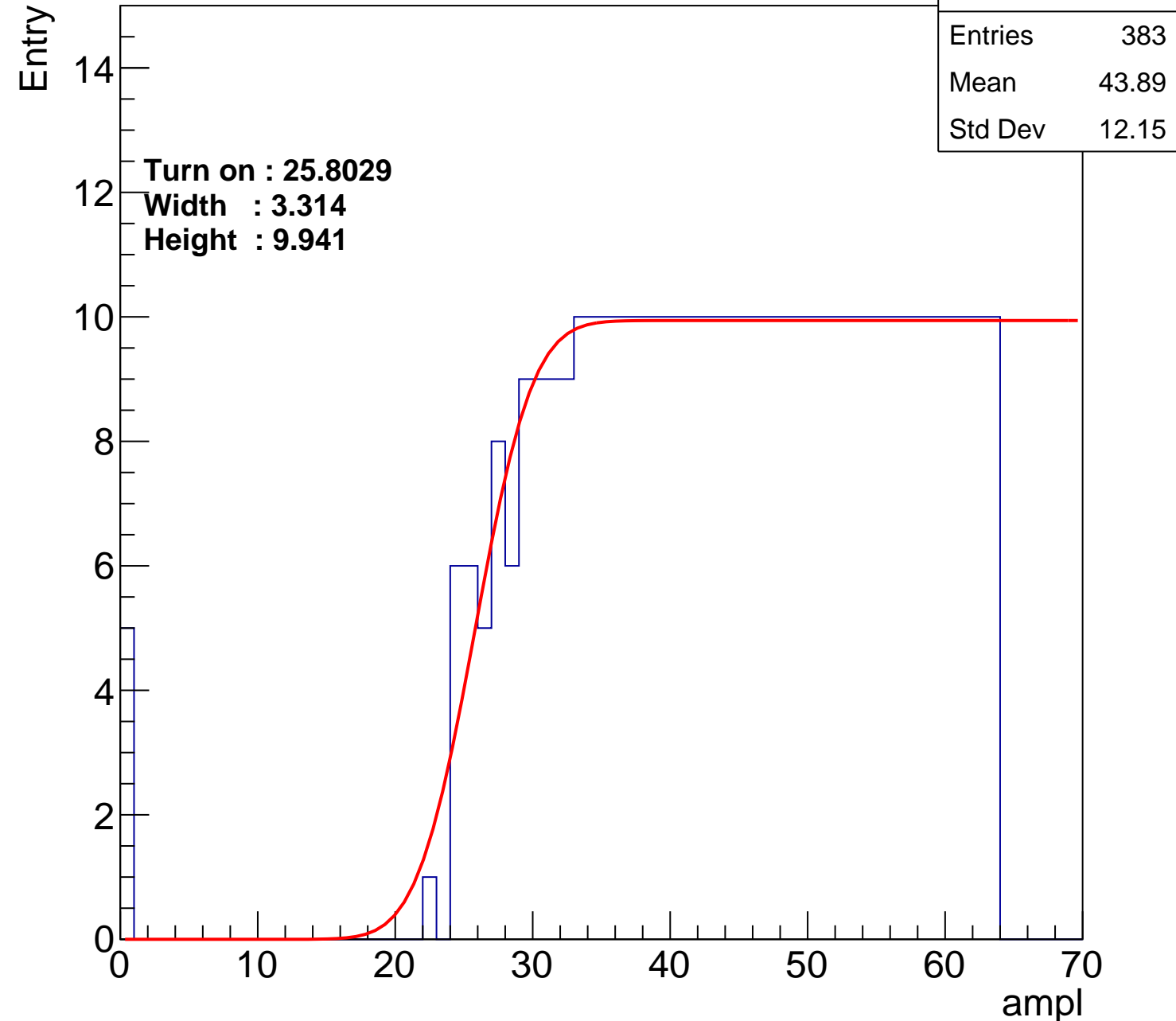
Width : 3.314

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.78
Std Dev	11.68

Turn on : 27.9454

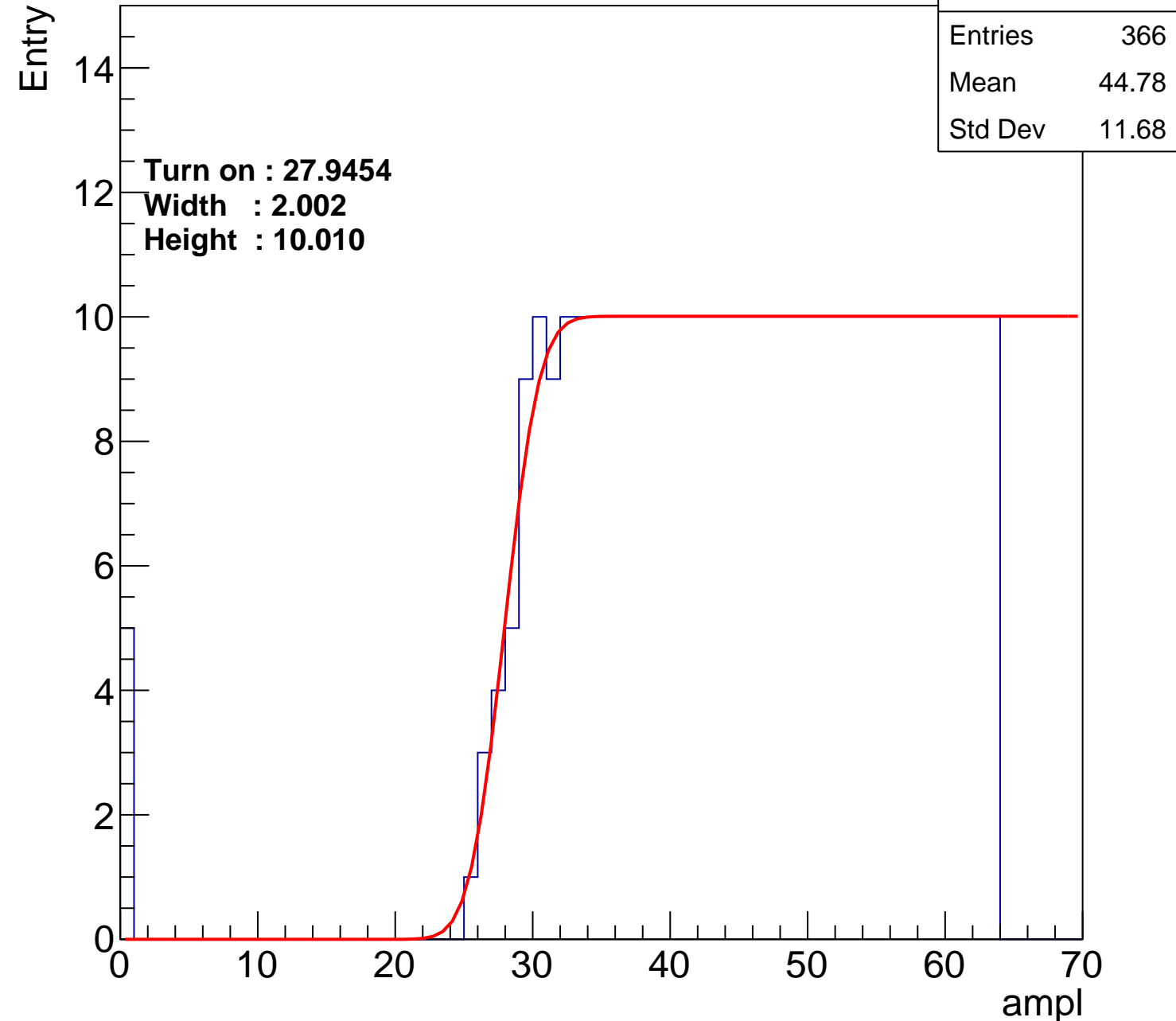
Width : 2.002

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch107

calib_packv5_042523_0143.root, FC#3, port B3

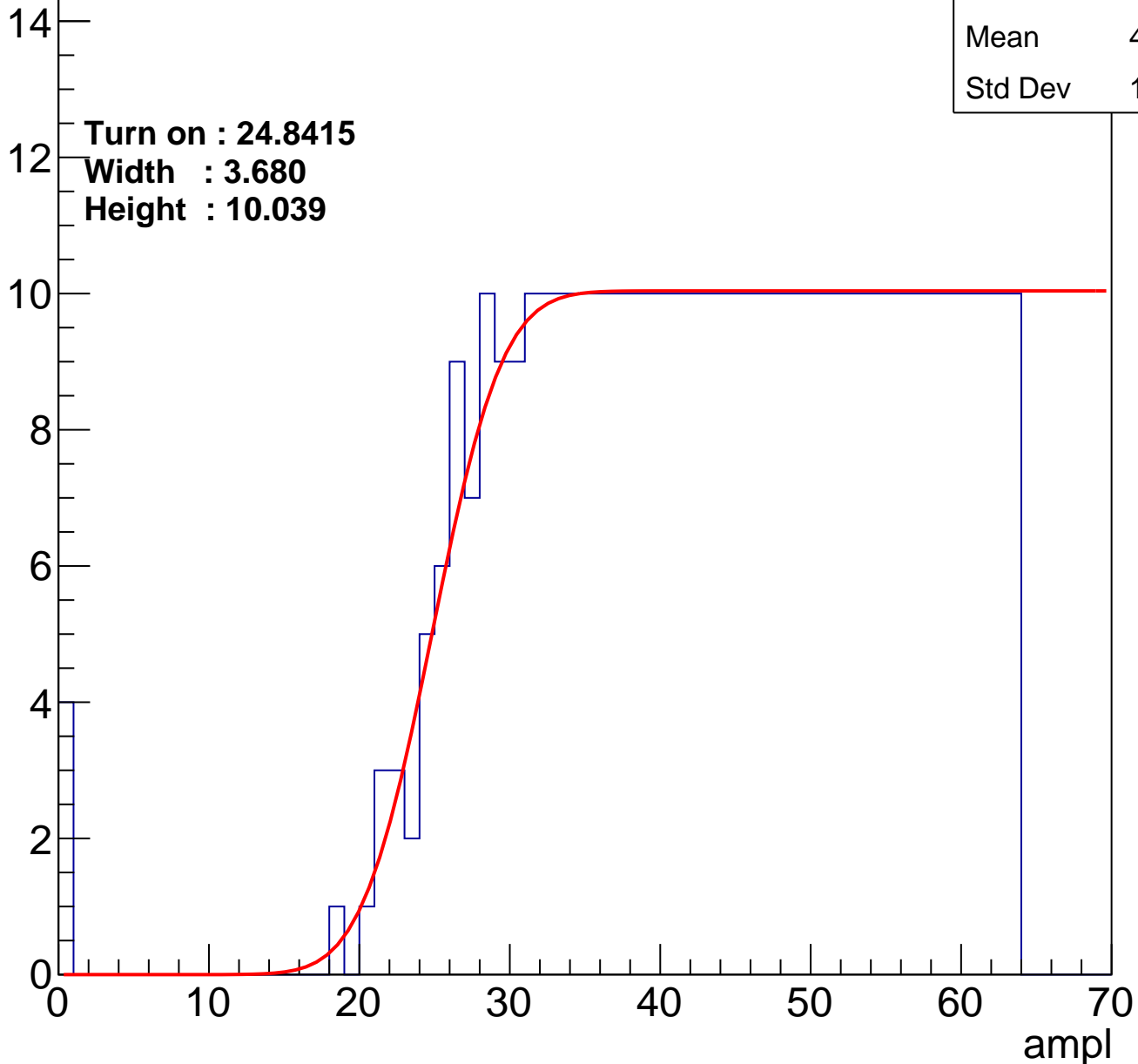
Entries	399
Mean	43.18
Std Dev	12.36

Turn on : 24.8415

Width : 3.680

Height : 10.039

Entry



B1L000S, U9-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.29
Std Dev	12.09

Turn on : 27.7373

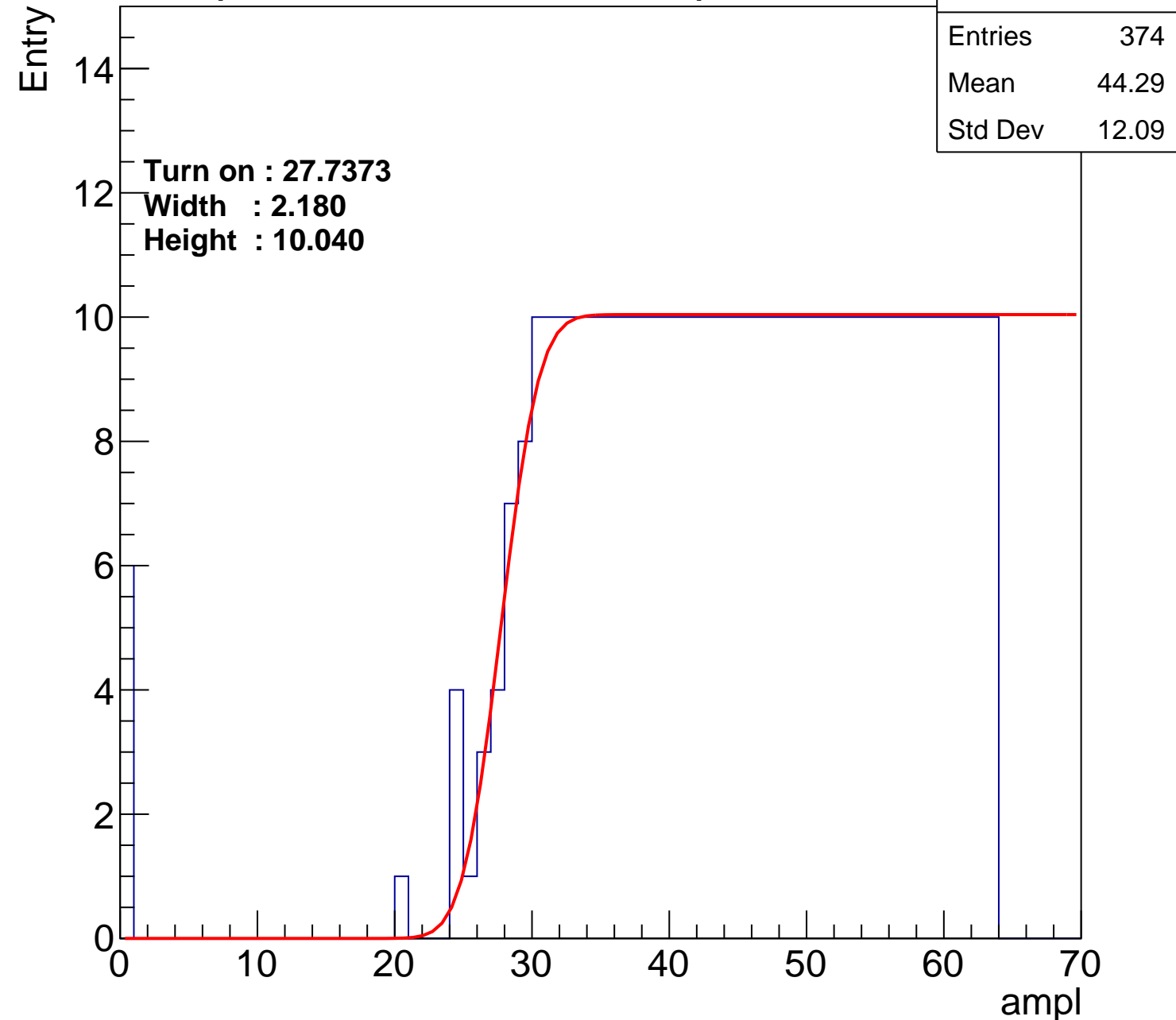
Width : 2.180

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.18
Std Dev	11

Turn on : 28.2426

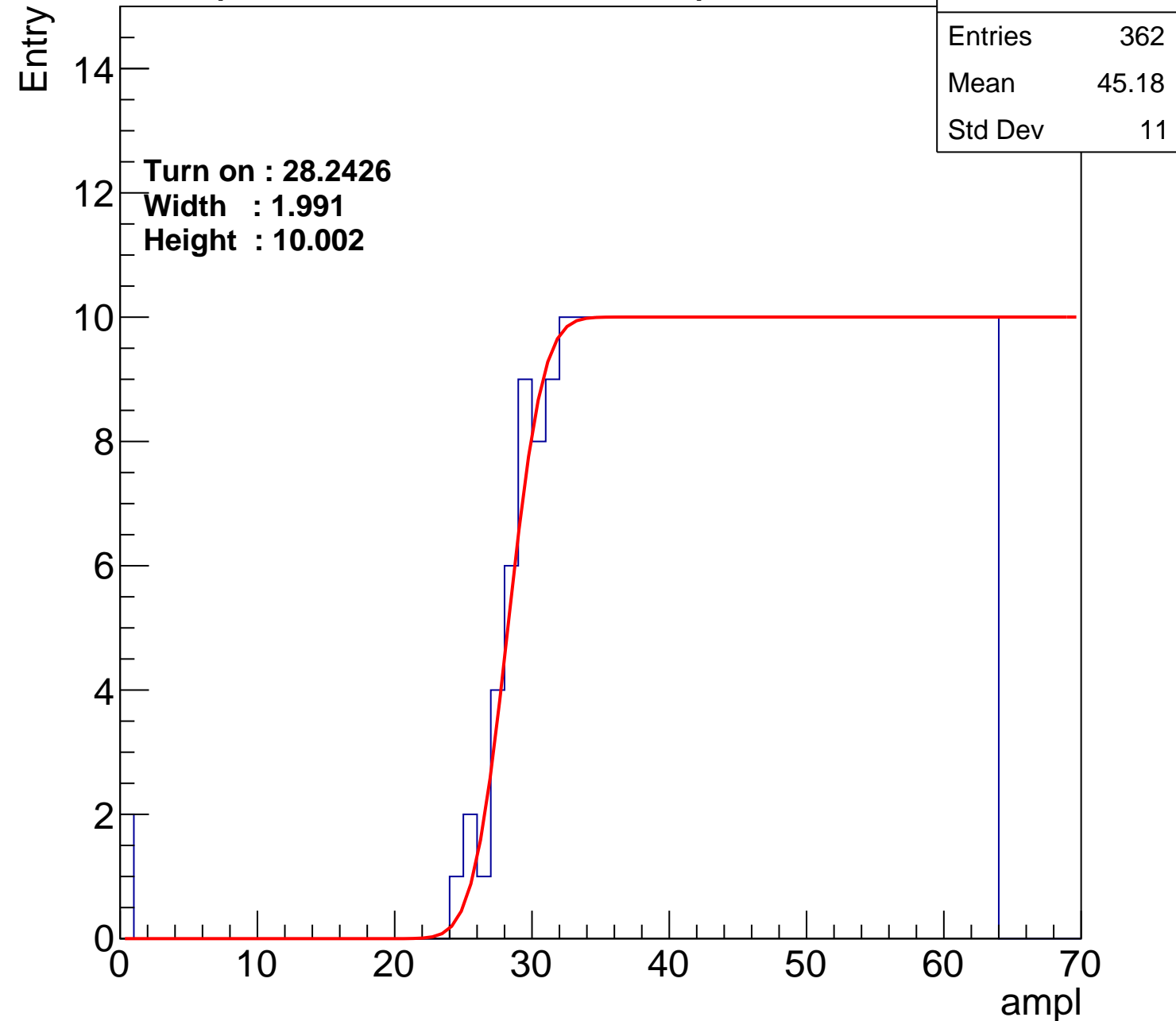
Width : 1.991

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch110

calib_packv5_042523_0143.root, FC#3, port B3

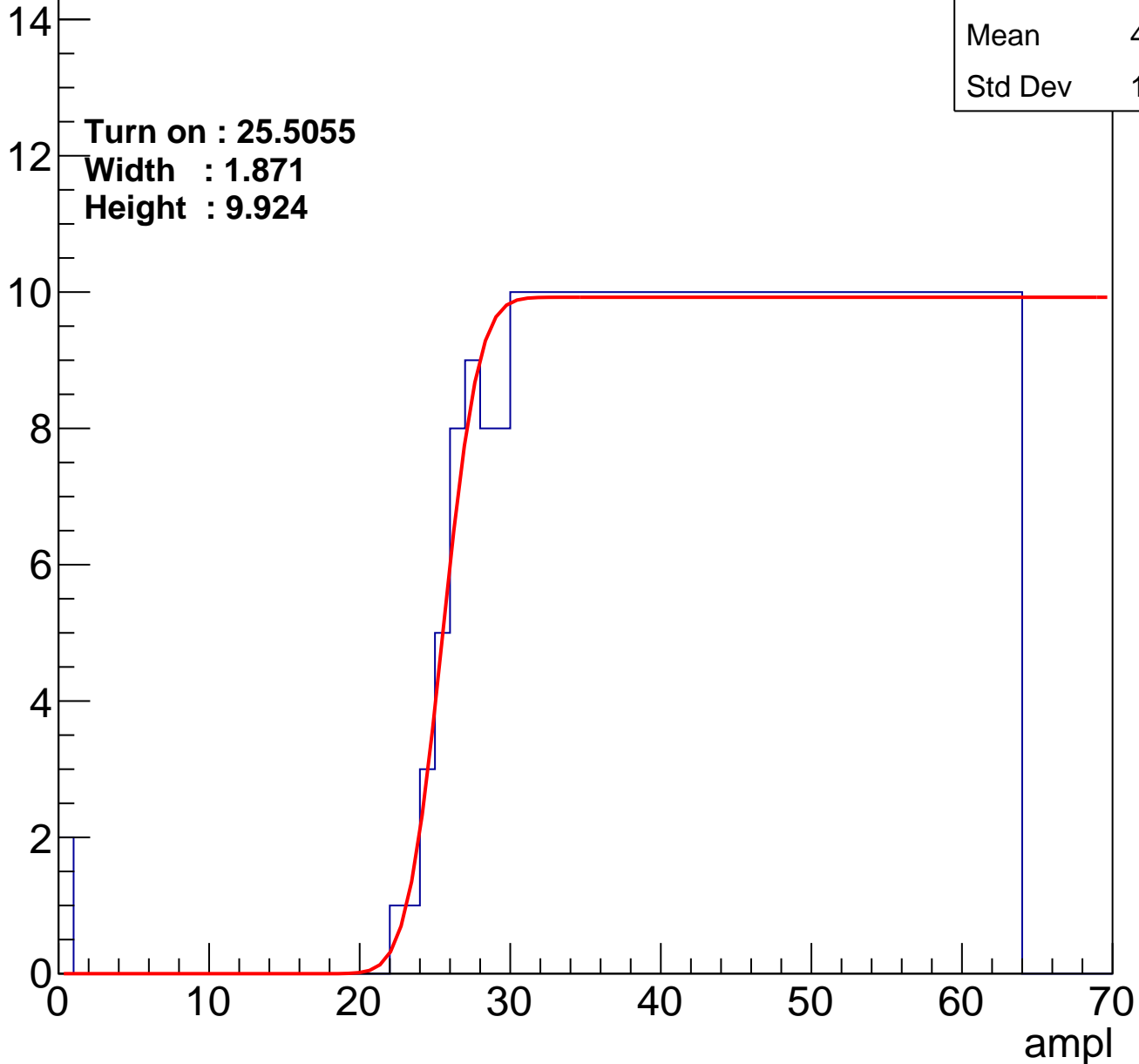
Entries	385
Mean	44.05
Std Dev	11.59

Turn on : 25.5055

Width : 1.871

Height : 9.924

Entry



B1L000S, U9-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 25.9128

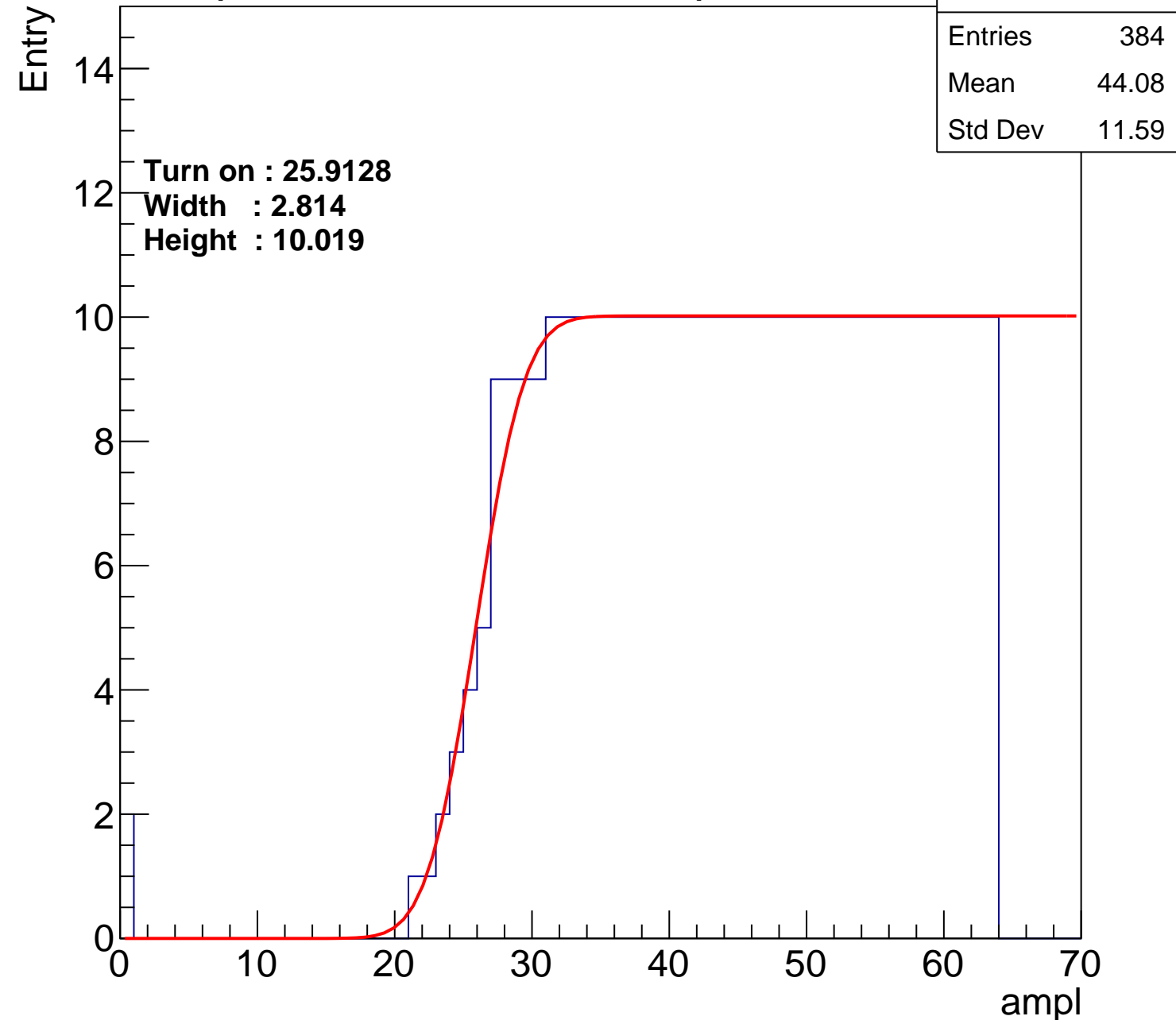
Width : 2.814

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.31
Std Dev	11.44

Turn on : 26.4106

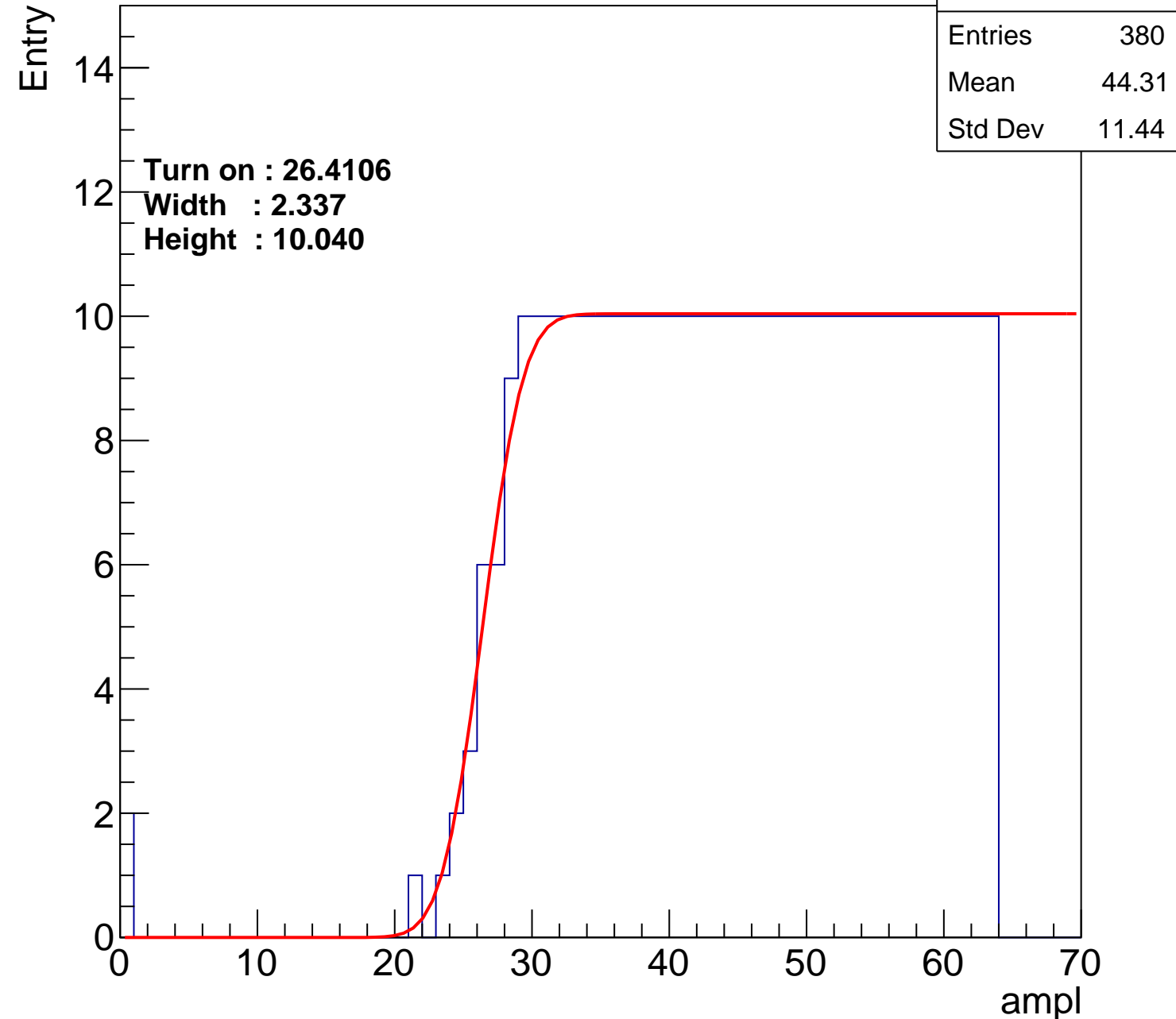
Width : 2.337

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.14
Std Dev	10.87

Turn on : 27.9152

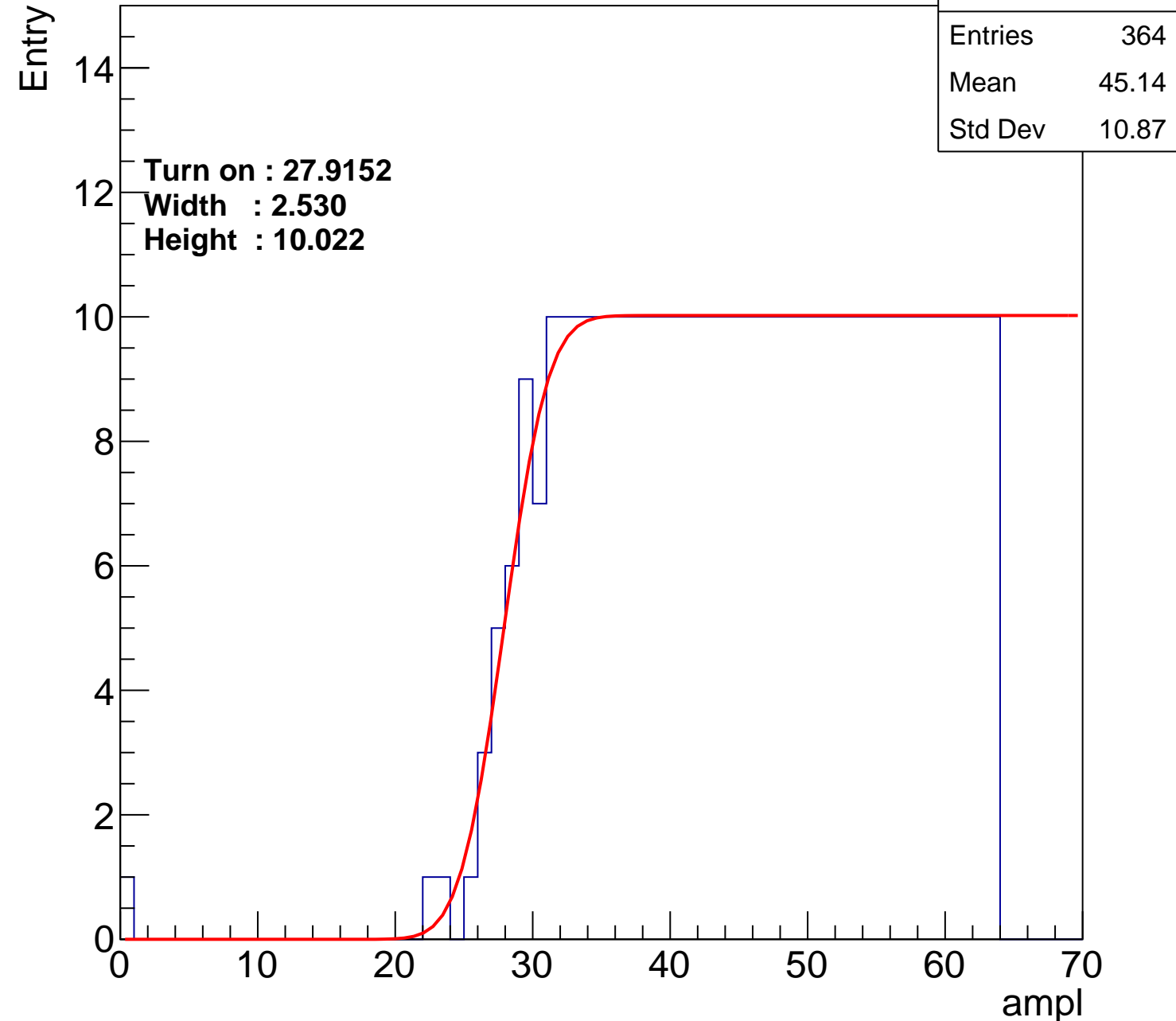
Width : 2.530

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch114

calib_packv5_042523_0143.root, FC#3, port B3

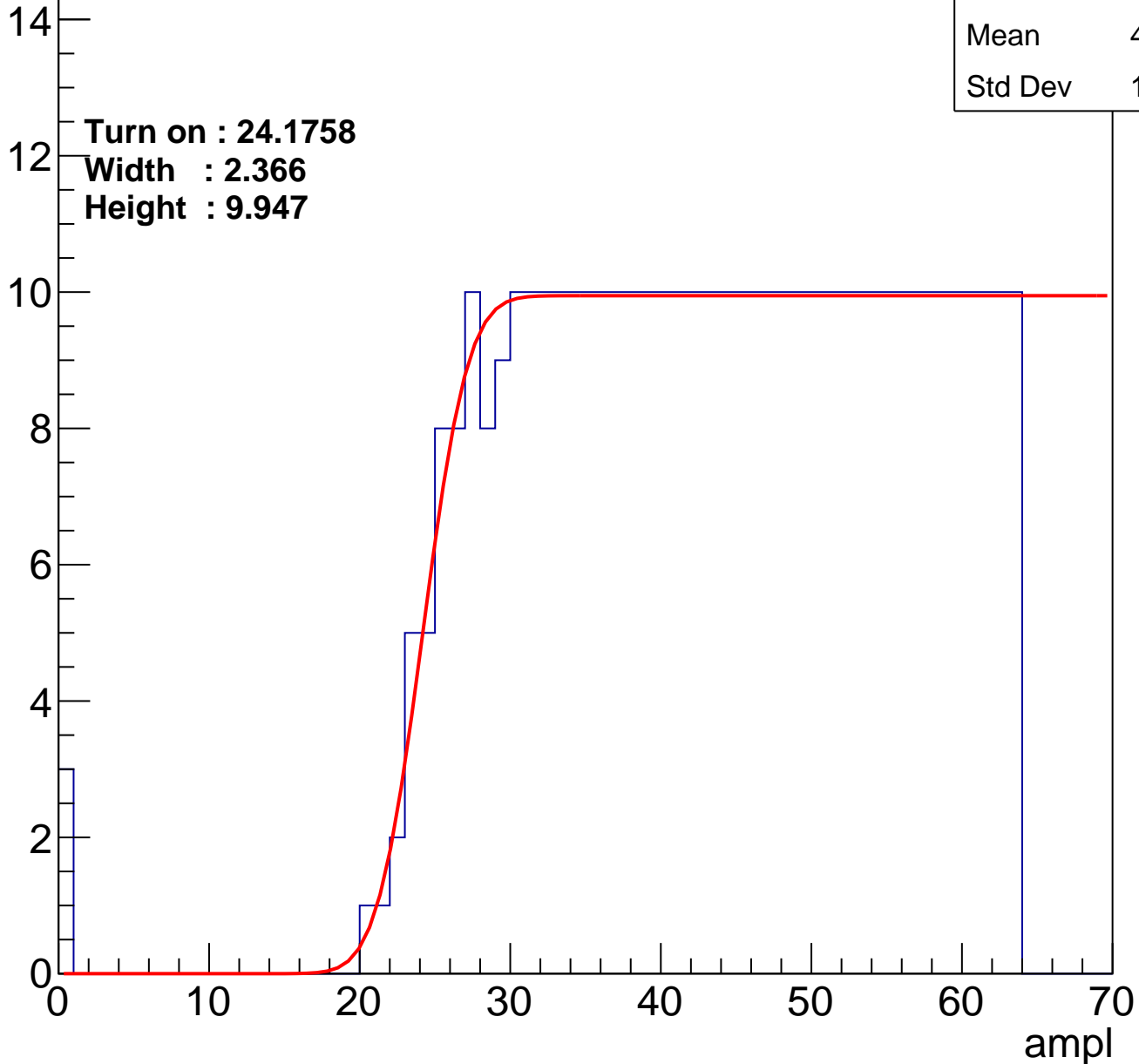
Entries	400
Mean	43.23
Std Dev	12.16

Turn on : 24.1758

Width : 2.366

Height : 9.947

Entry



B1L000S, U9-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.11
Std Dev	11.55

Turn on : 26.3694

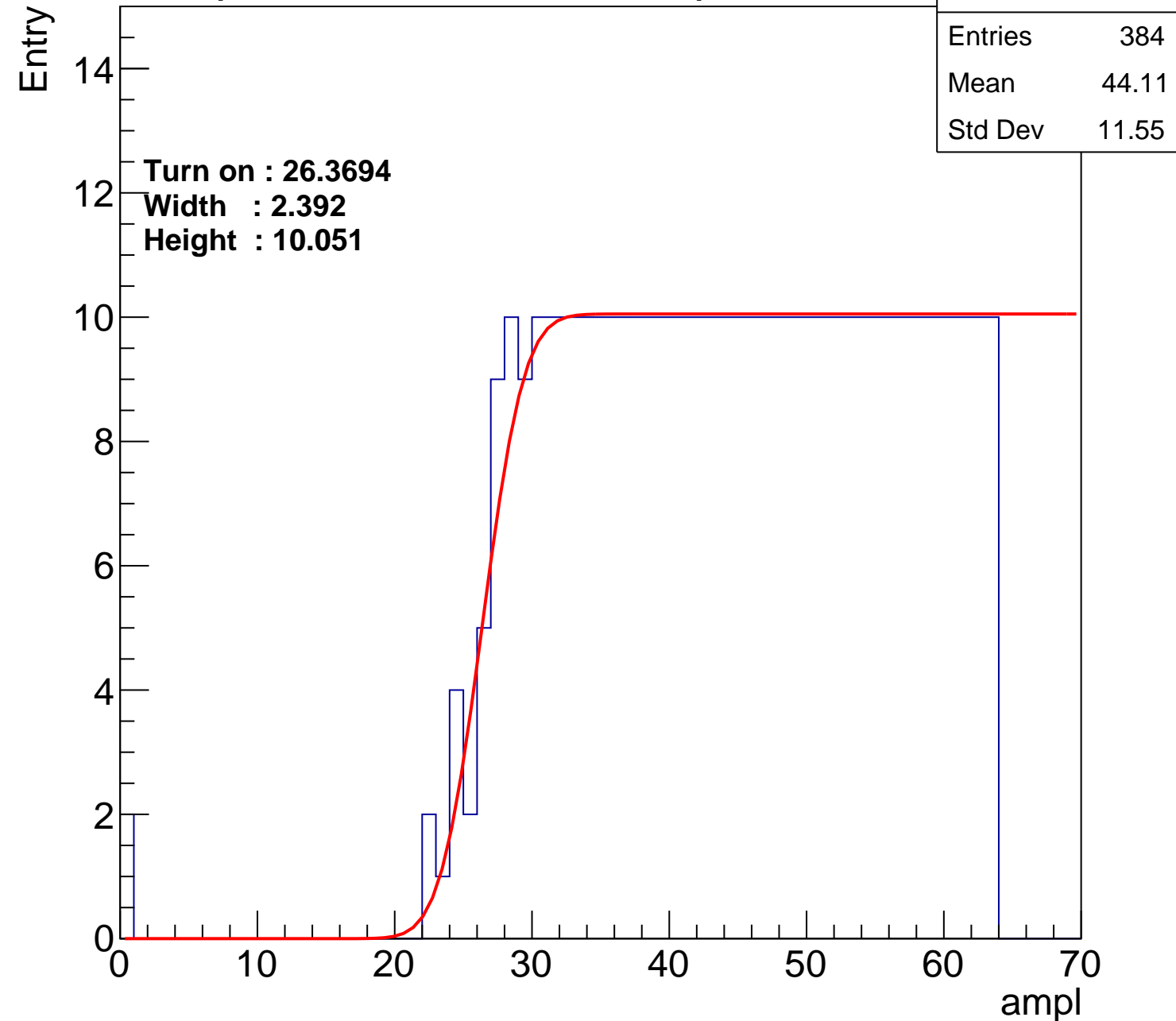
Width : 2.392

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch116

calib_packv5_042523_0143.root, FC#3, port B3

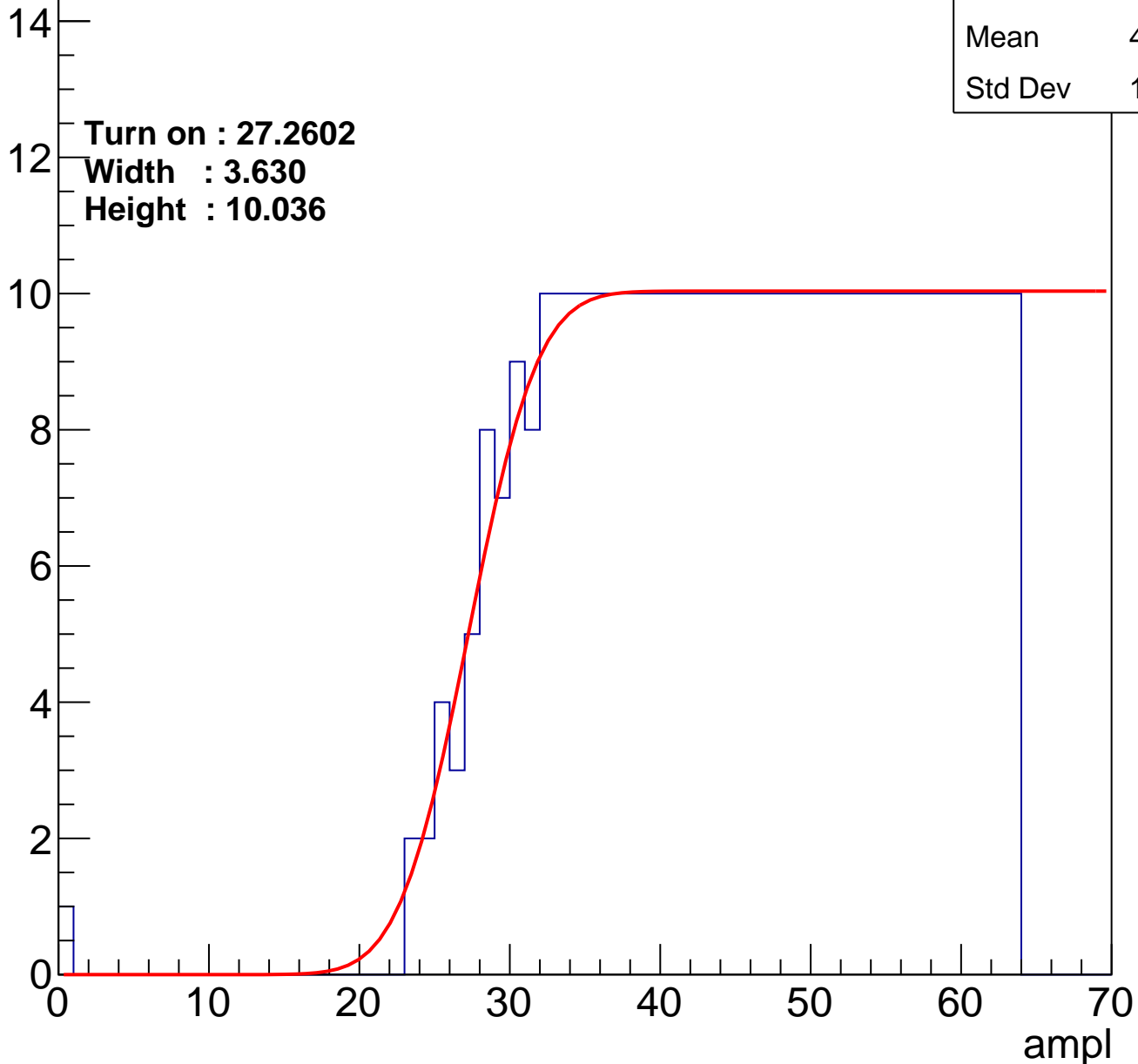
Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.2602

Width : 3.630

Height : 10.036

Entry



B1L000S, U9-ch117

calib_packv5_042523_0143.root, FC#3, port B3

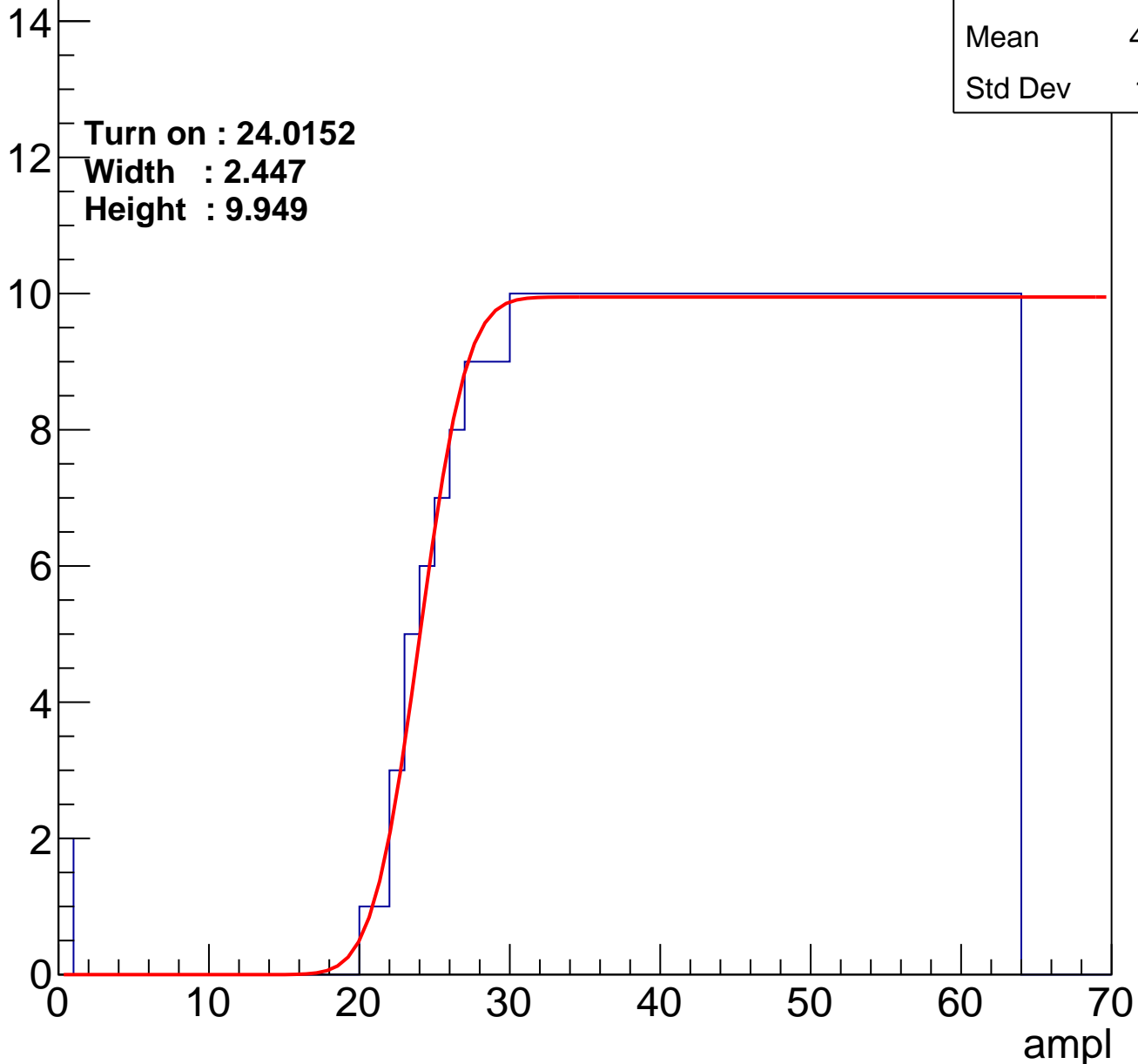
Entries	400
Mean	43.29
Std Dev	12.01

Turn on : 24.0152

Width : 2.447

Height : 9.949

Entry



B1L000S, U9-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.03
Std Dev	11.75

Turn on : 25.7847

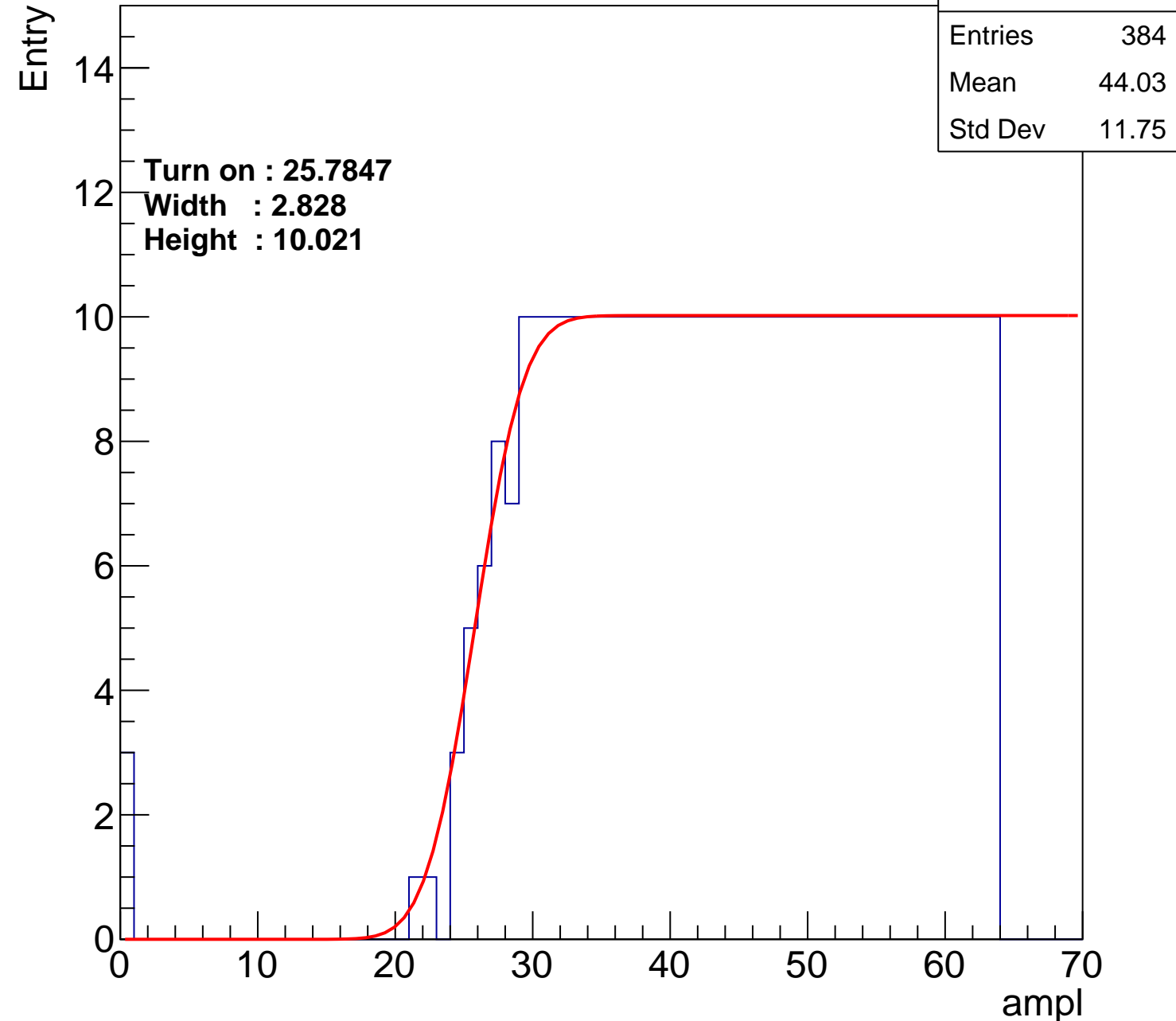
Width : 2.828

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch119

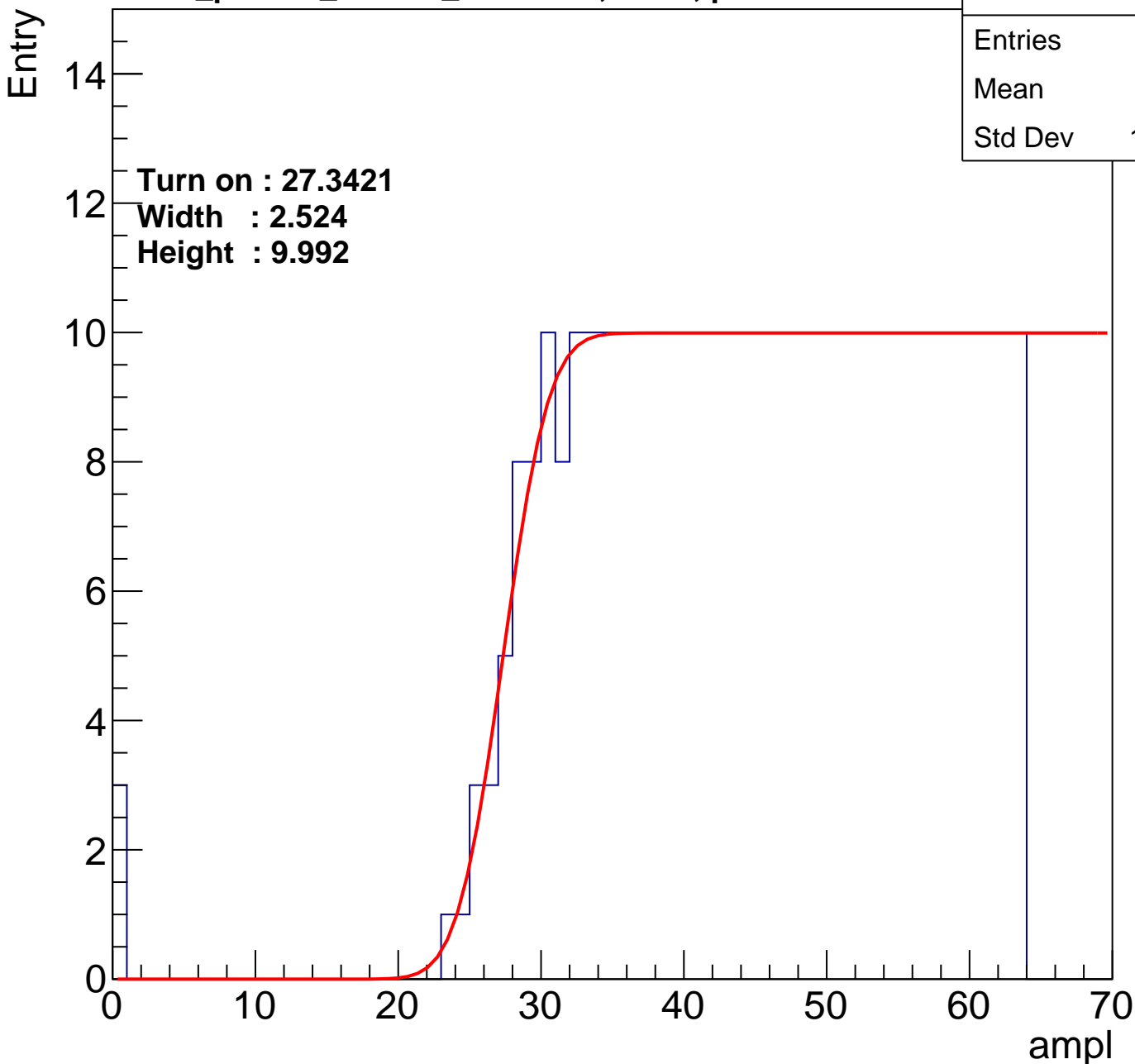
calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.3421

Width : 2.524

Height : 9.992



B1L000S, U9-ch120

calib_packv5_042523_0143.root, FC#3, port B3

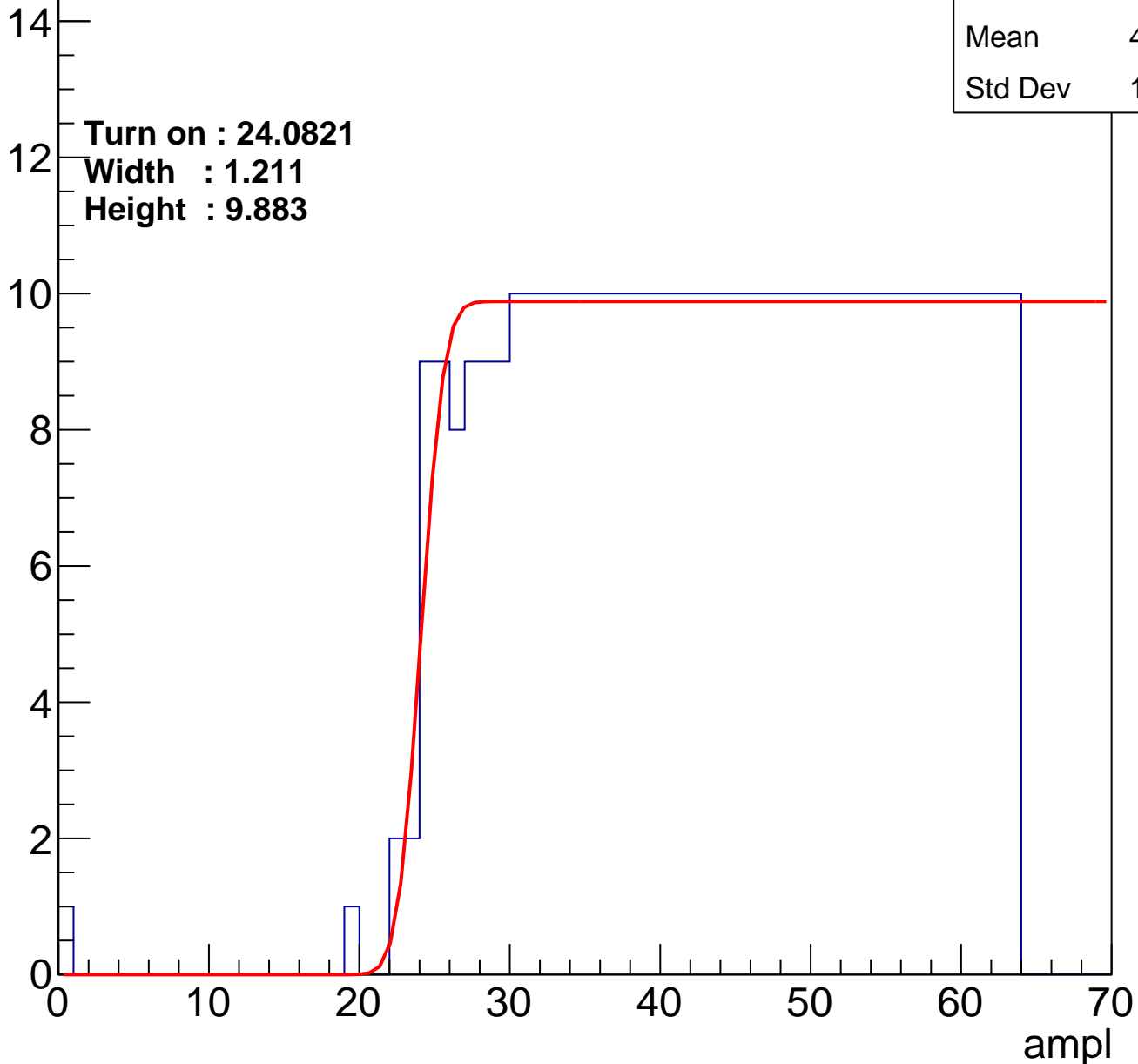
Entries	399
Mean	43.42
Std Dev	11.79

Turn on : 24.0821

Width : 1.211

Height : 9.883

Entry



B1L000S, U9-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.1651

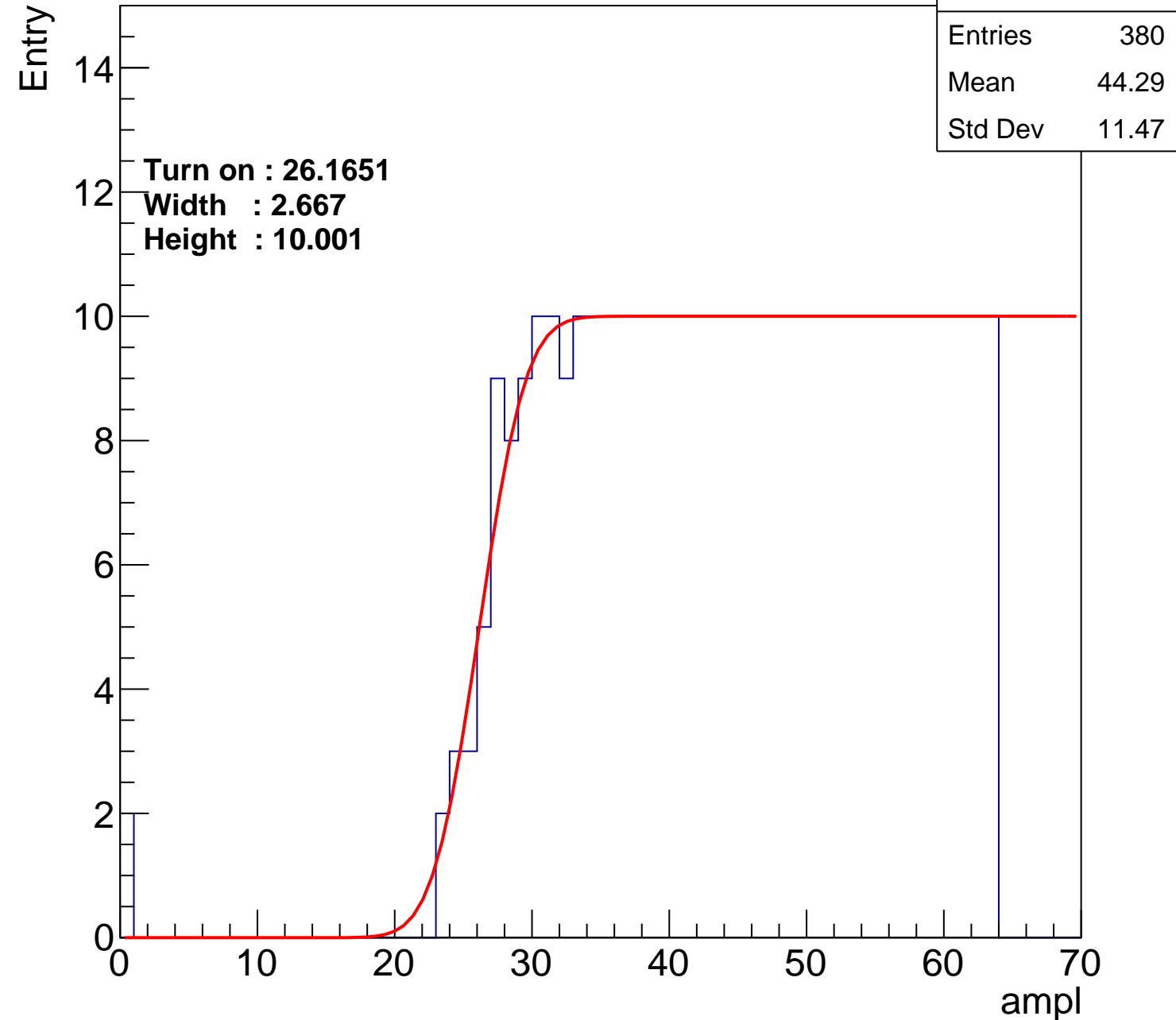
Width : 2.667

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.92
Std Dev	11.3

Turn on : 27.9305

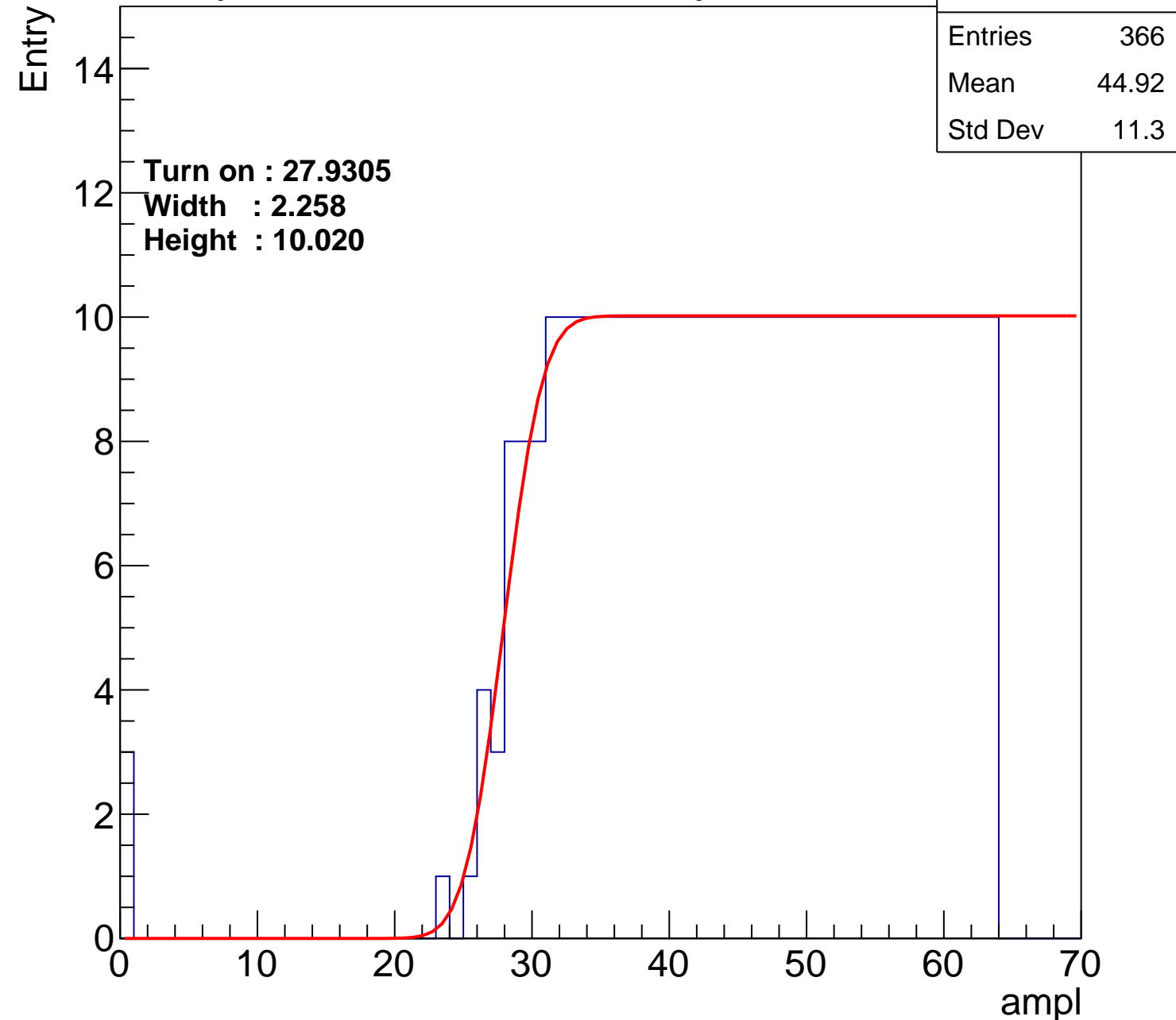
Width : 2.258

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch123

calib_packv5_042523_0143.root, FC#3, port B3

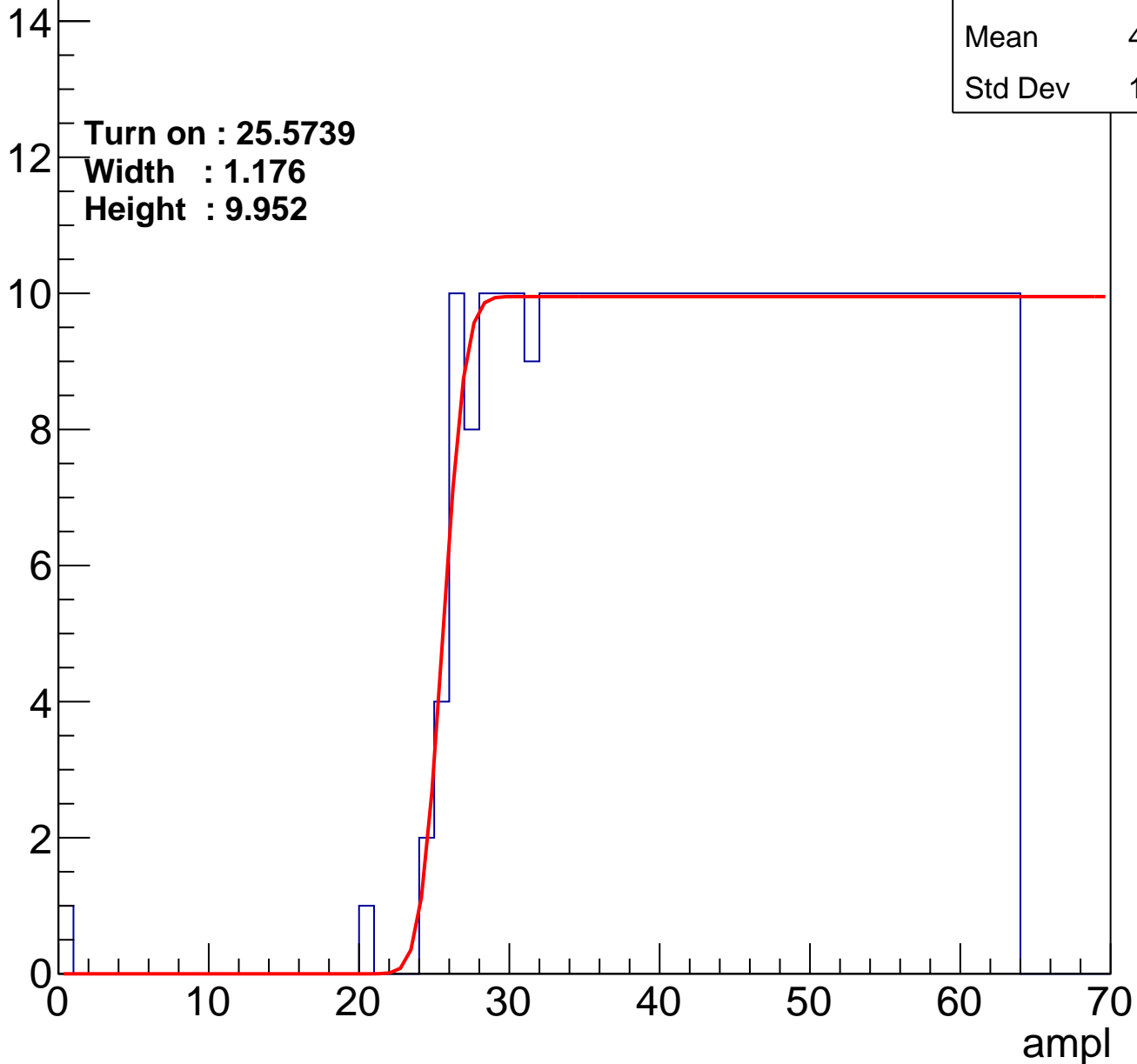
Entries	385
Mean	44.14
Std Dev	11.37

Turn on : 25.5739

Width : 1.176

Height : 9.952

Entry



B1L000S, U9-ch124

calib_packv5_042523_0143.root, FC#3, port B3

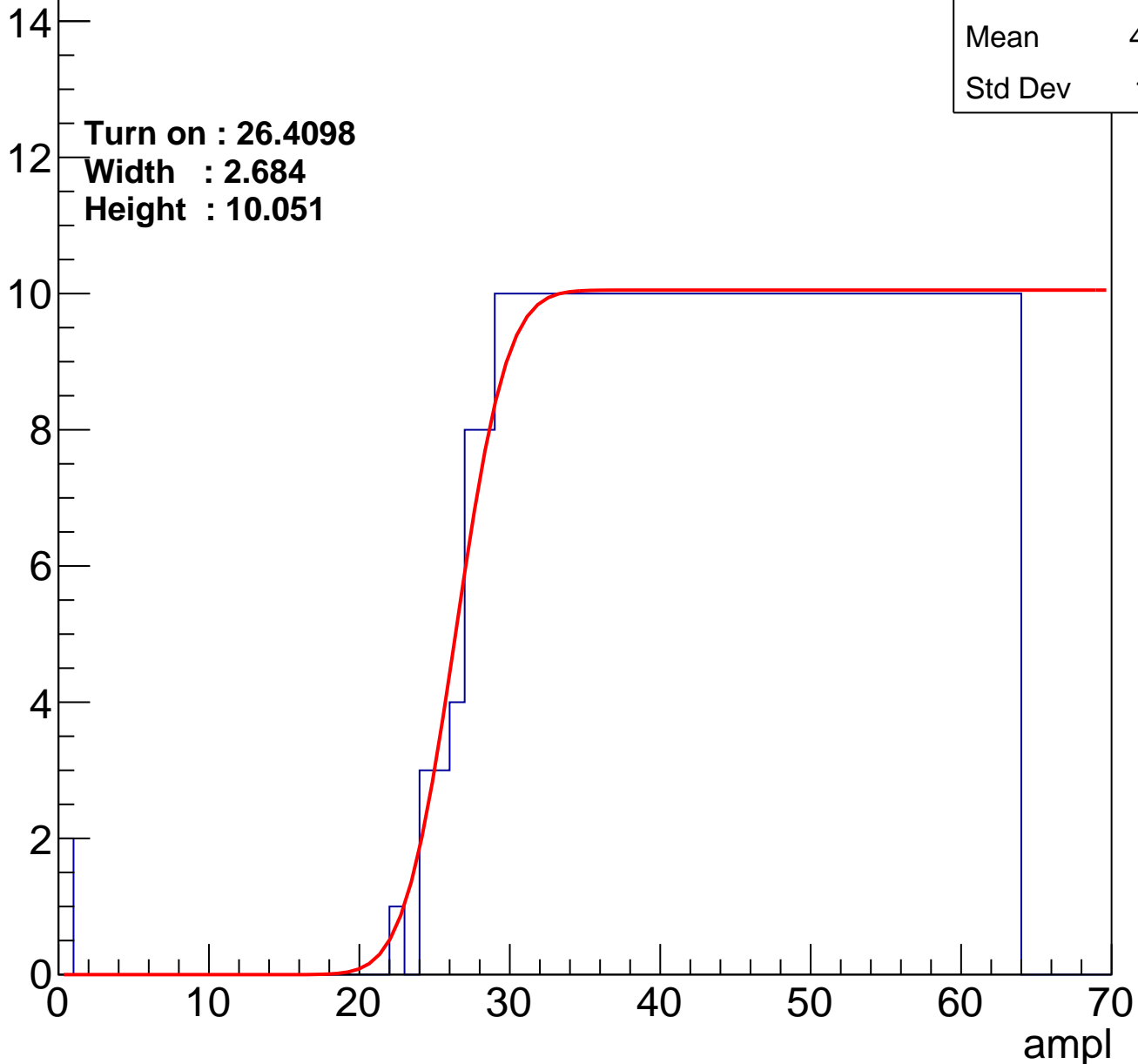
Entries	379
Mean	44.36
Std Dev	11.41

Turn on : 26.4098

Width : 2.684

Height : 10.051

Entry



B1L000S, U9-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 25.9200

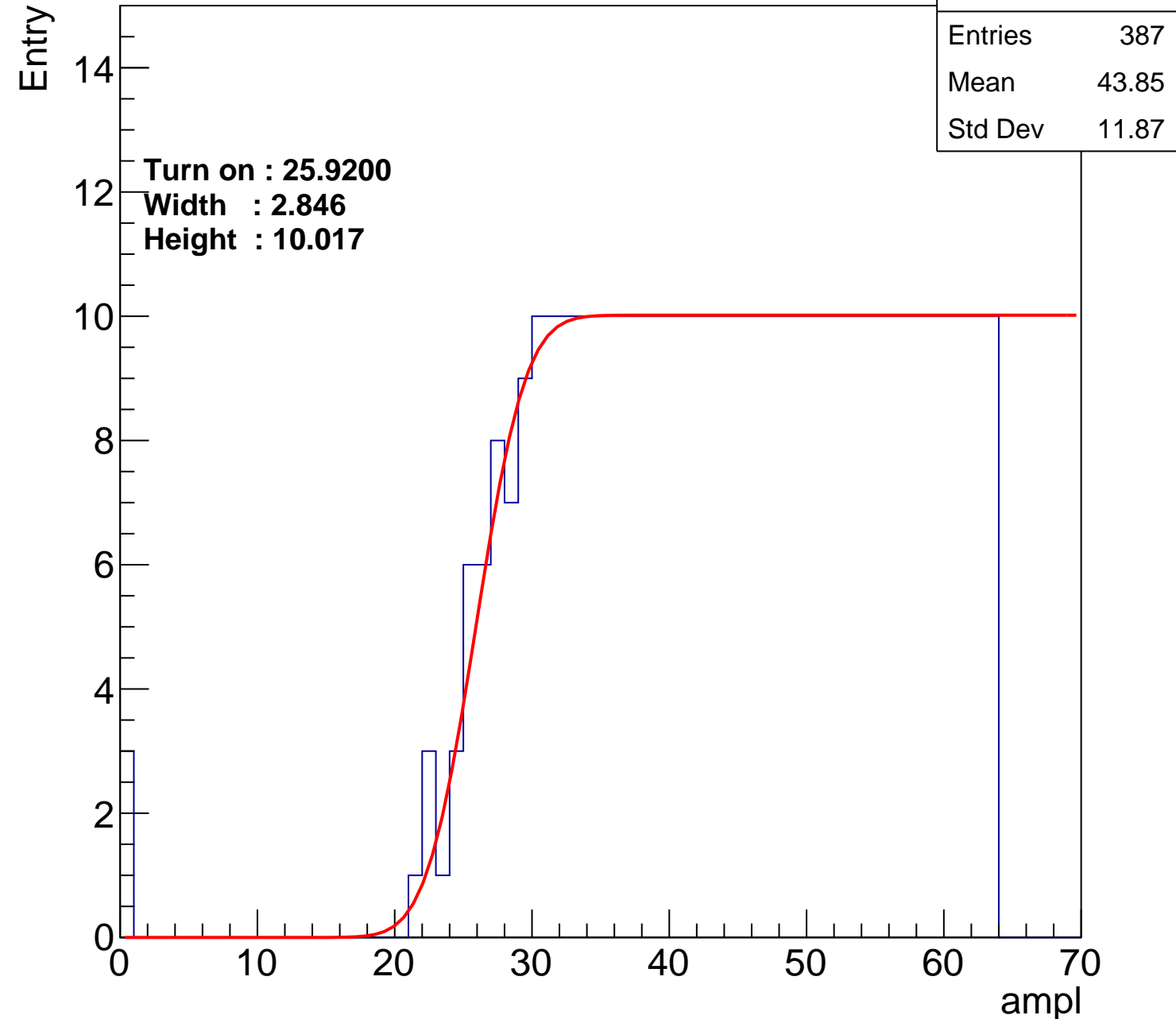
Width : 2.846

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U9-ch126

calib_packv5_042523_0143.root, FC#3, port B3

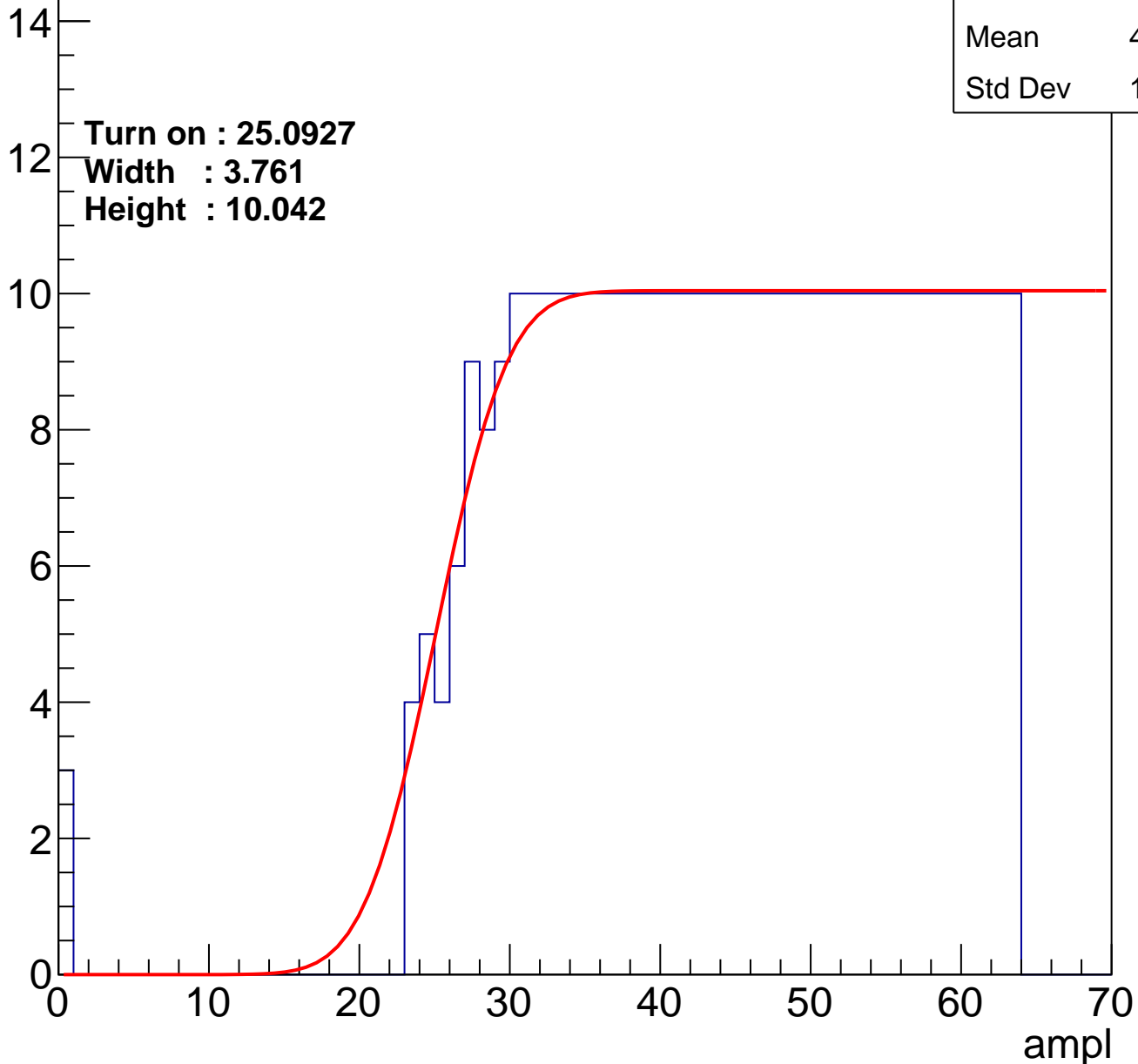
Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.0927

Width : 3.761

Height : 10.042

Entry



B1L000S, U9-ch127

calib_packv5_042523_0143.root, FC#3, port B3

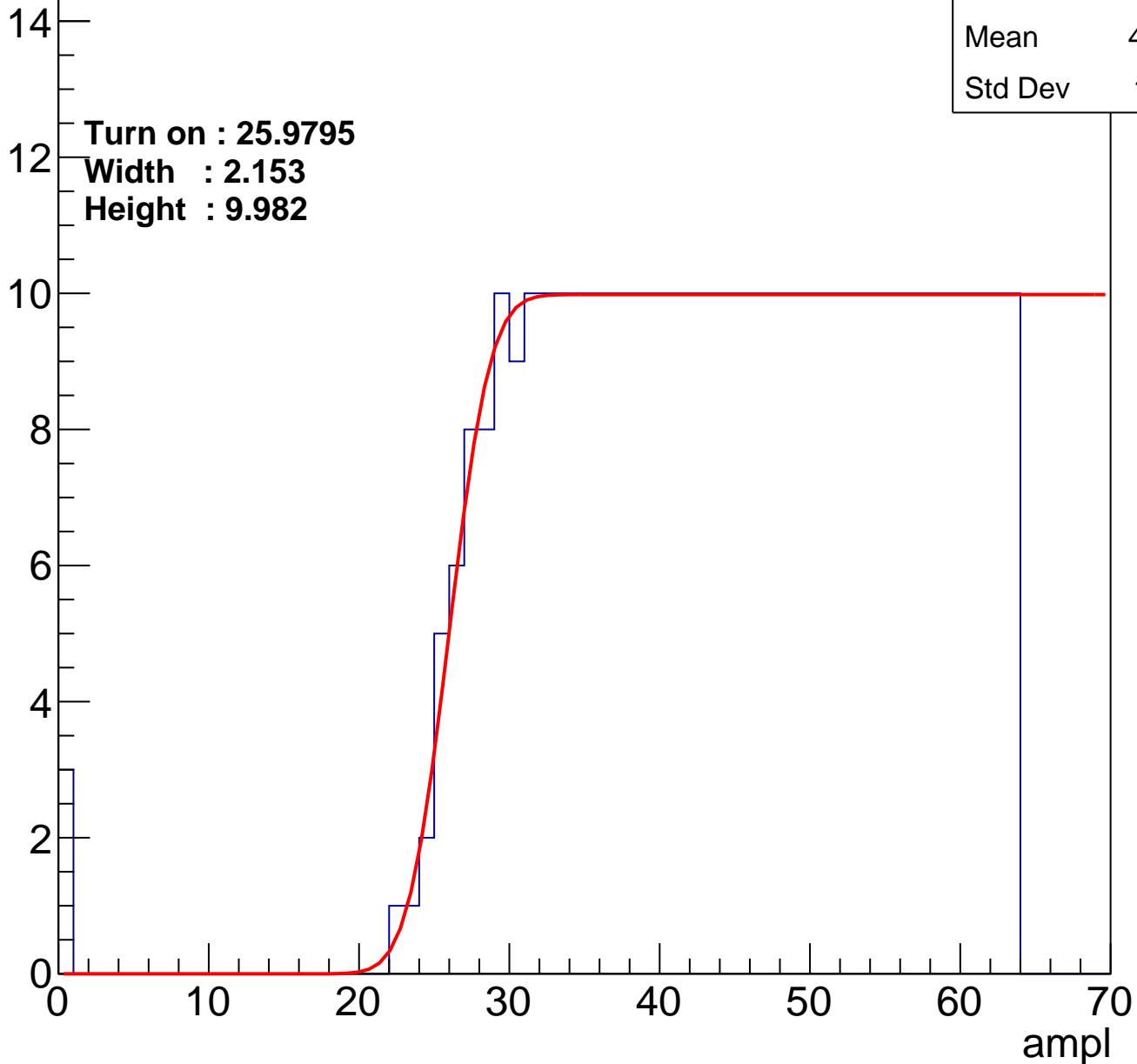
Entries	383
Mean	44.08
Std Dev	11.71

Turn on : 25.9795

Width : 2.153

Height : 9.982

Entry



B1L000S, U9-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.08
Std Dev	11.71

Turn on : 25.9795

Width : 2.153

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl

