

B0L000S, U4-ch0, adc0

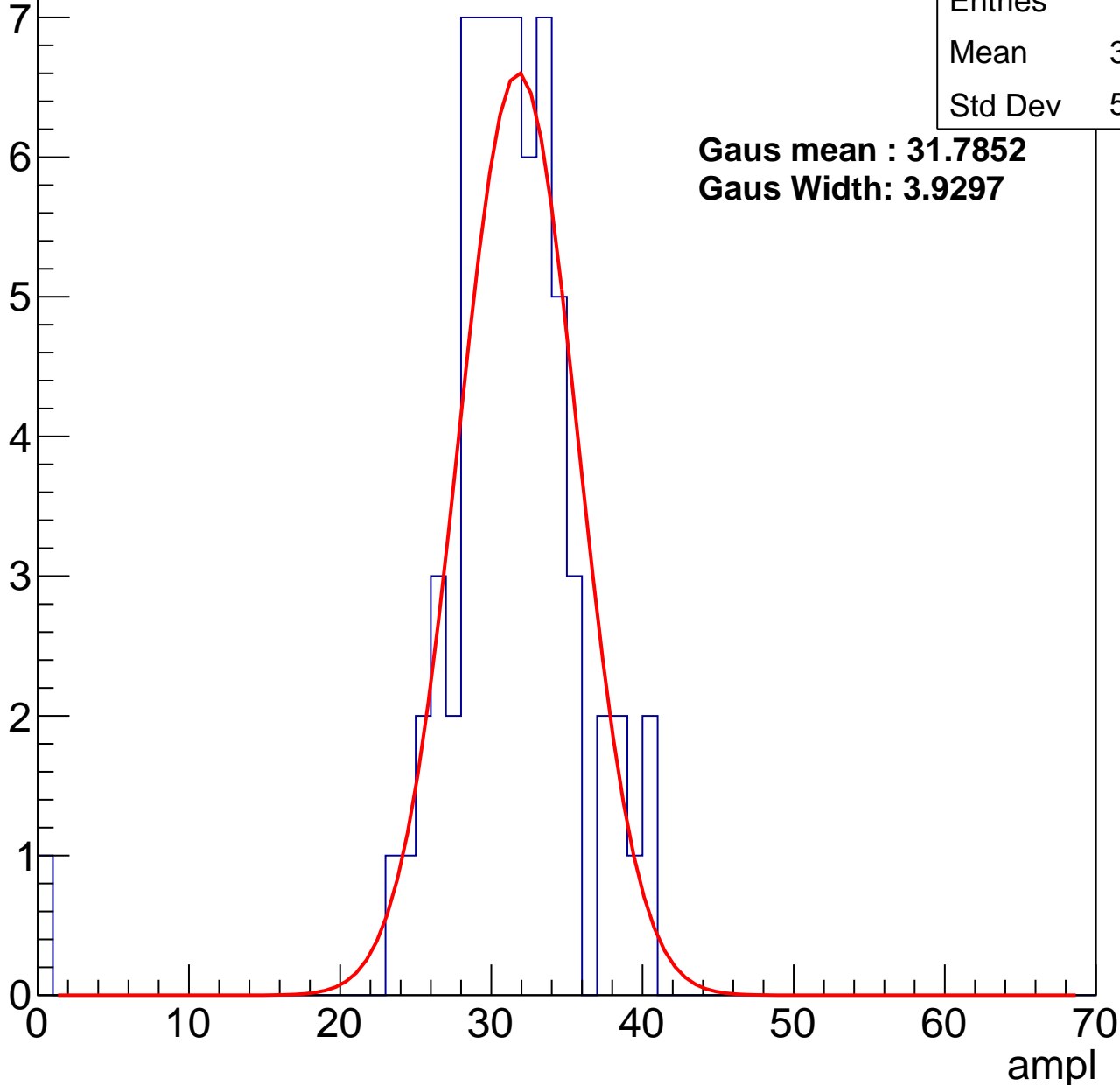
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	30.64
Std Dev	5.316

Gaus mean : 31.7852

Gaus Width: 3.9297



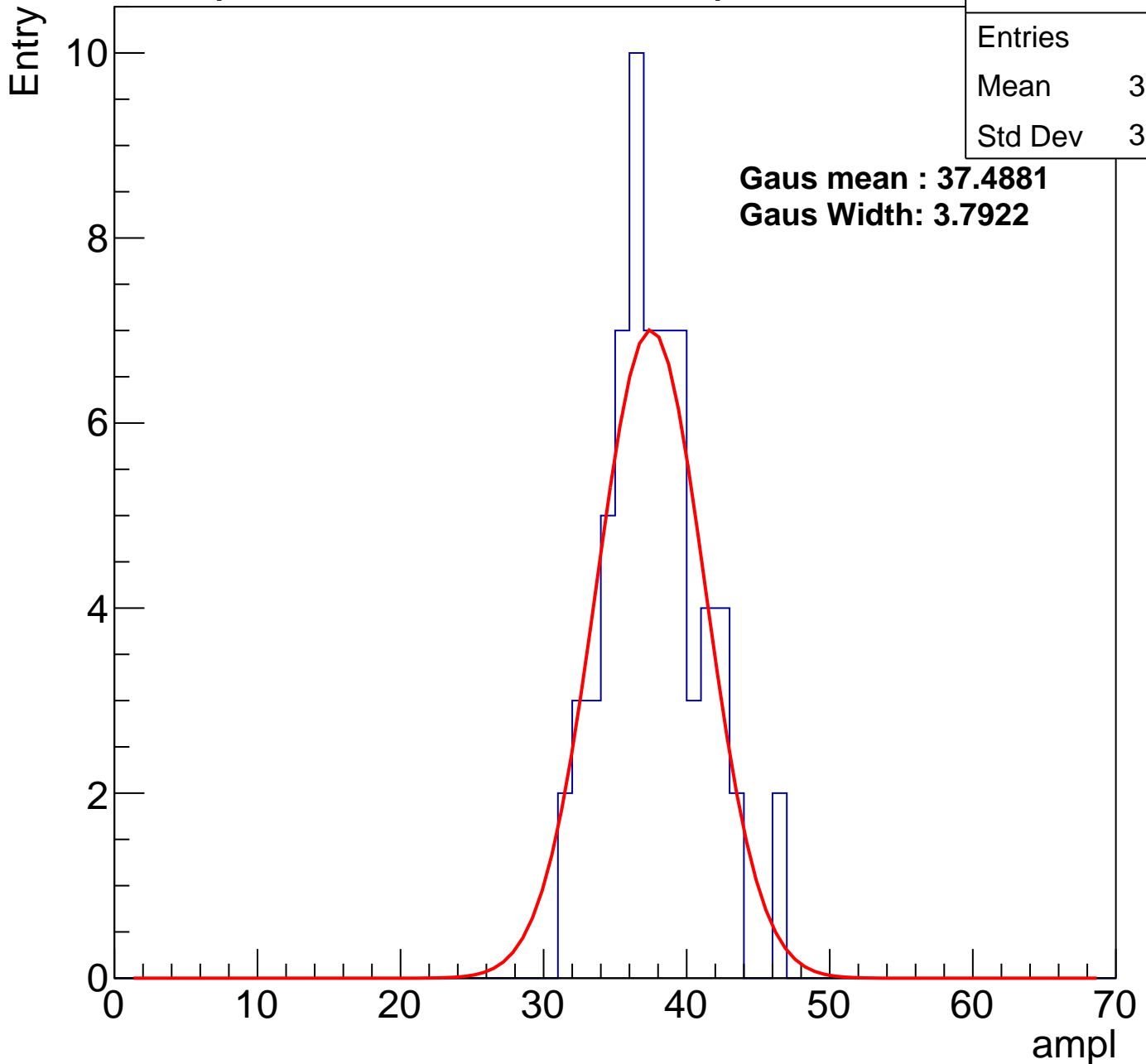
B0L000S, U4-ch0, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	66
Mean	37.27
Std Dev	3.333

Gaus mean : 37.4881

Gaus Width: 3.7922



B0L000S, U4-ch0, adc2

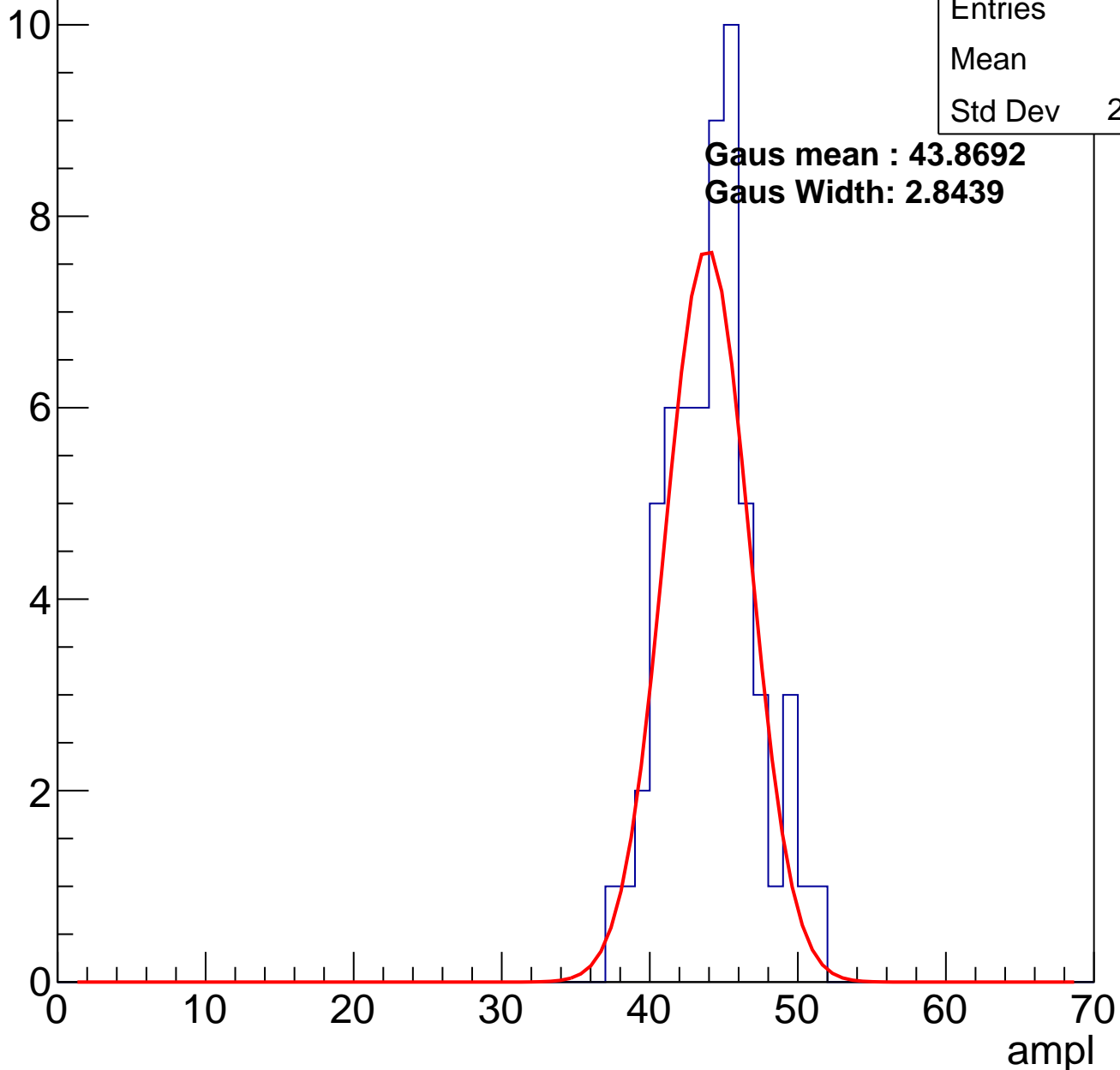
calib_packv5_042523_0143.root, FC#5, port B1

Entries	60
Mean	43.7
Std Dev	2.963

Gaus mean : 43.8692

Gaus Width: 2.8439

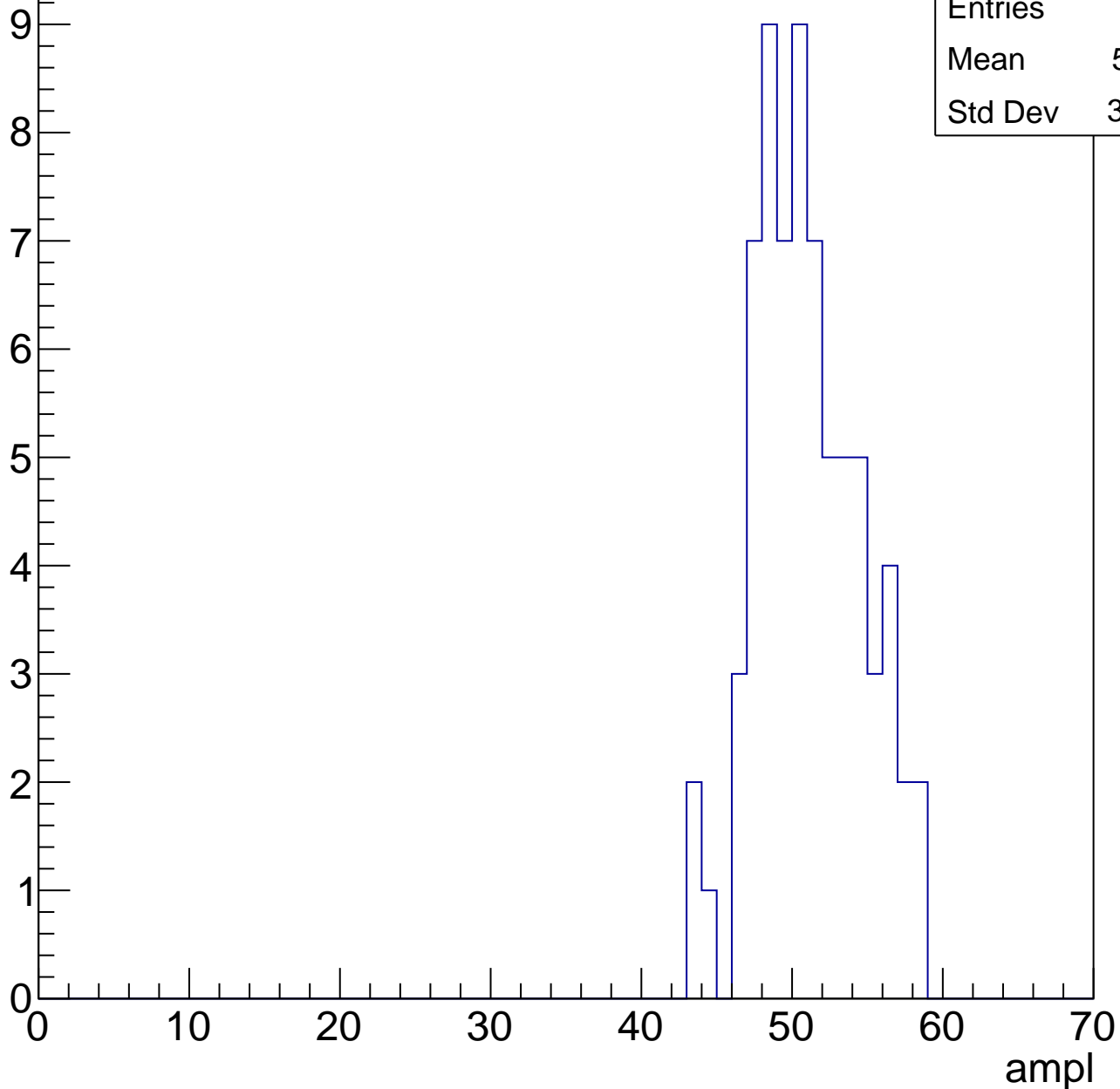
Entry



B0L000S, U4-ch0, adc3

calib_packv5_042523_0143.root, FC#5, port B1

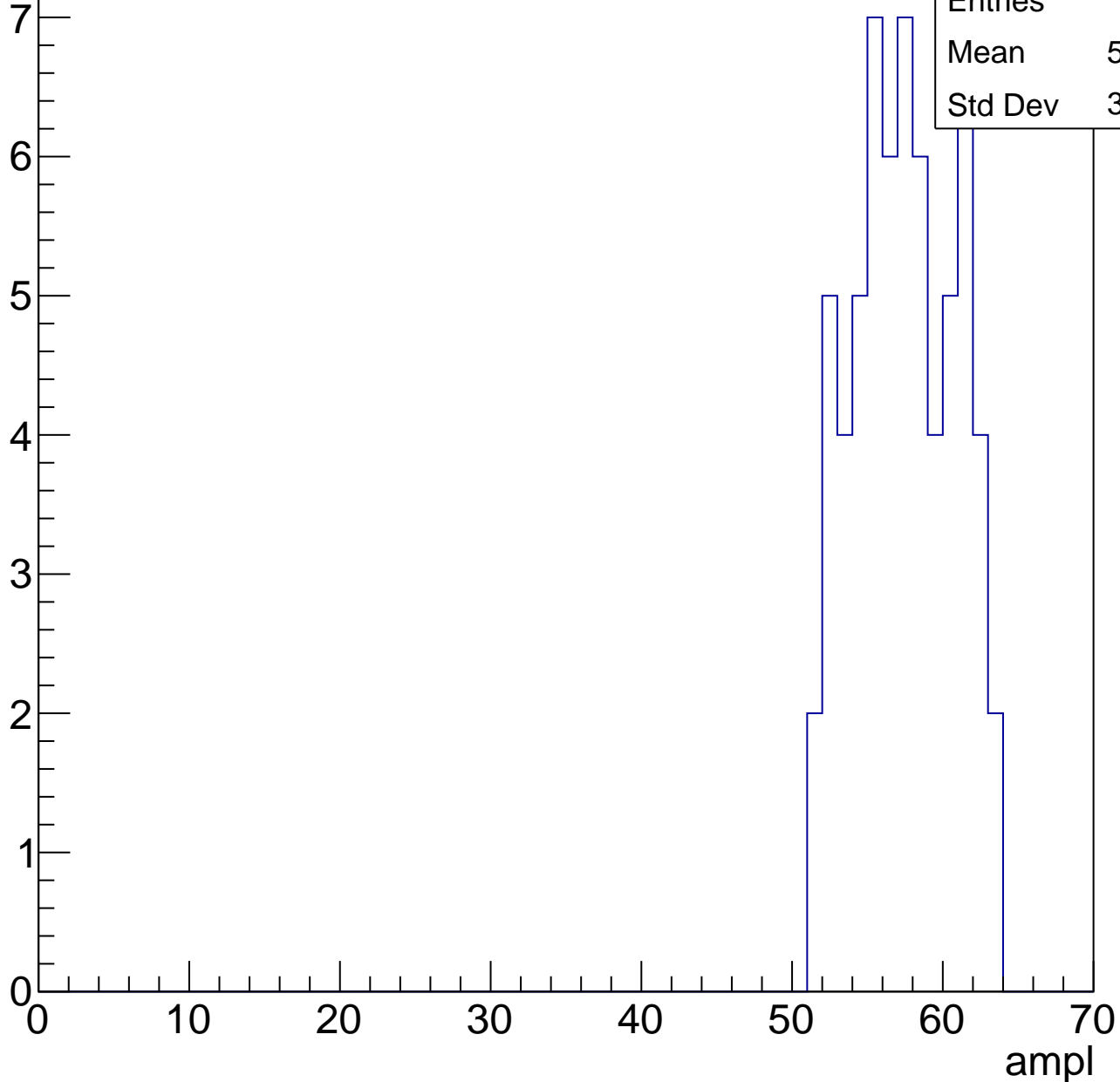
Entry



B0L000S, U4-ch0, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

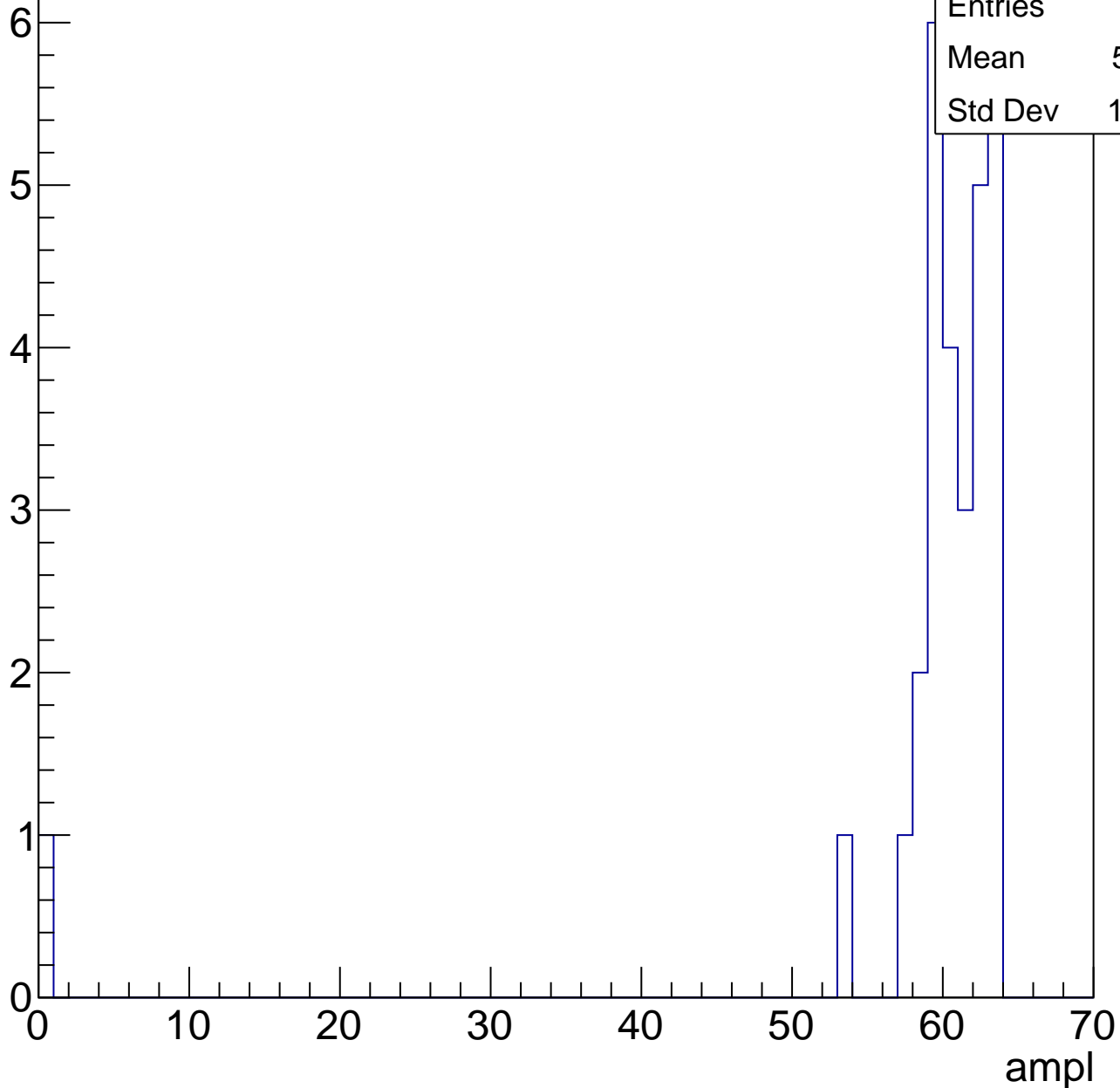


Entries	64
Mean	57.02
Std Dev	3.286

B0L000S, U4-ch0, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch0, adc6

calib_packv5_042523_0143.root, FC#5, port B1

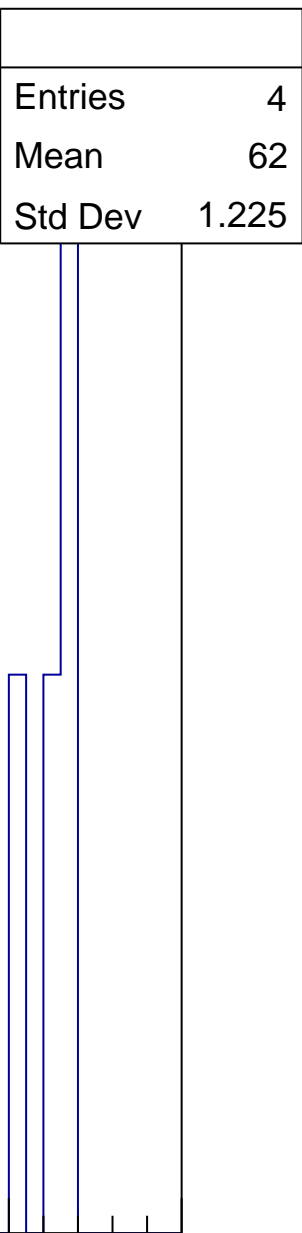
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

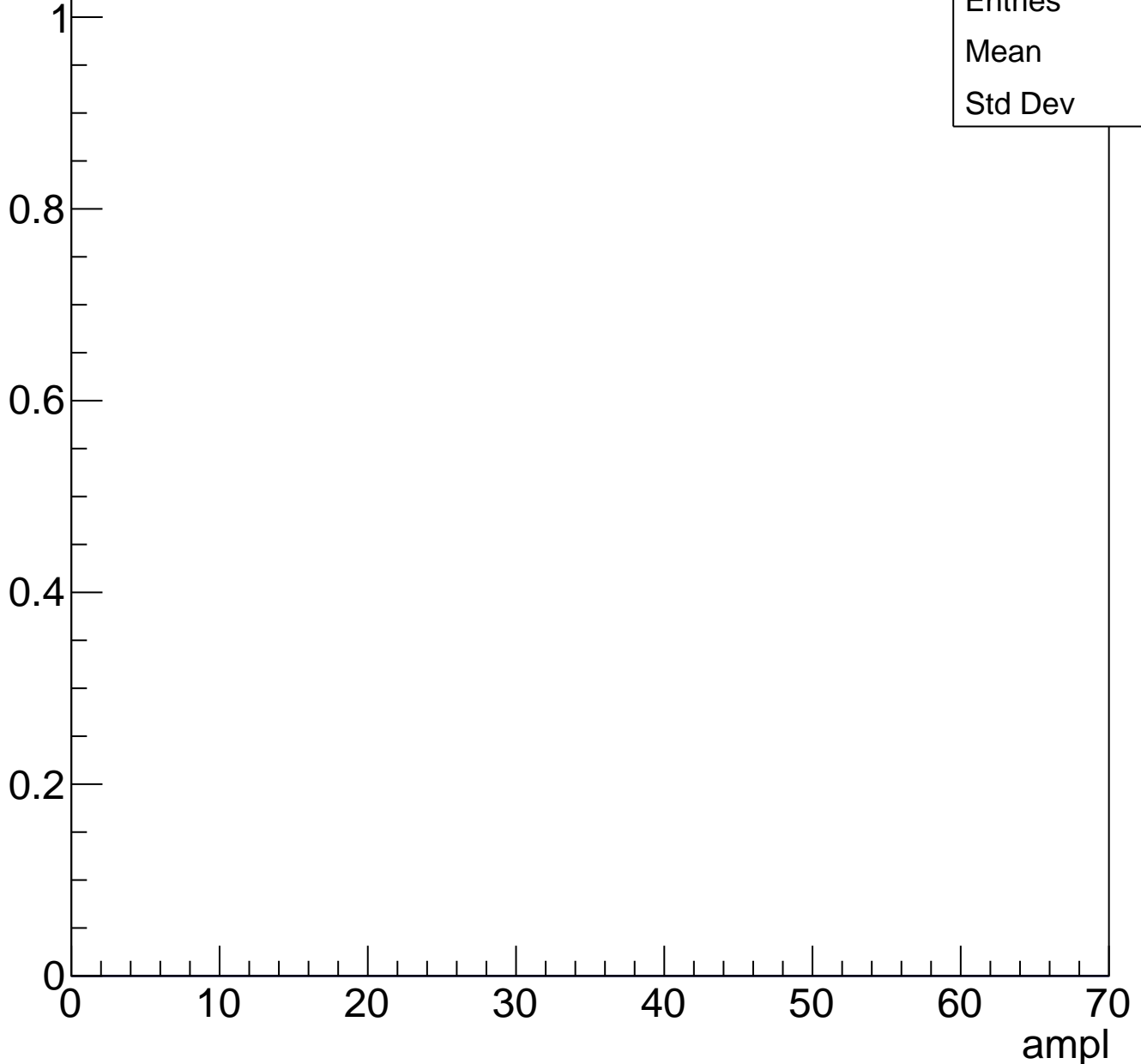
ampl



B0L000S, U4-ch0, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



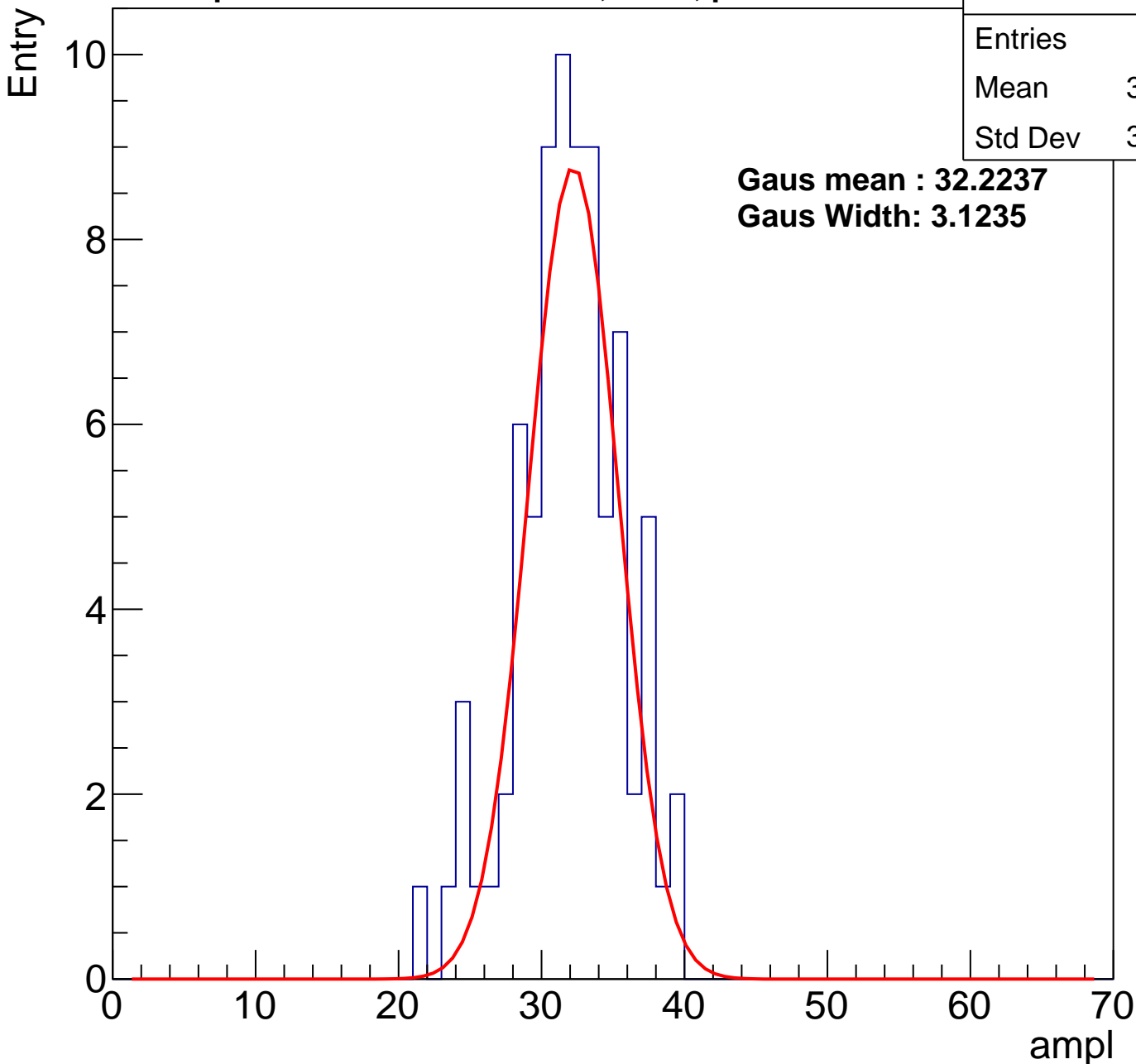
Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch1, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	79
Mean	31.48
Std Dev	3.694

Gaus mean : 32.2237
Gaus Width: 3.1235



B0L000S, U4-ch1, adc1

calib_packv5_042523_0143.root, FC#5, port B1

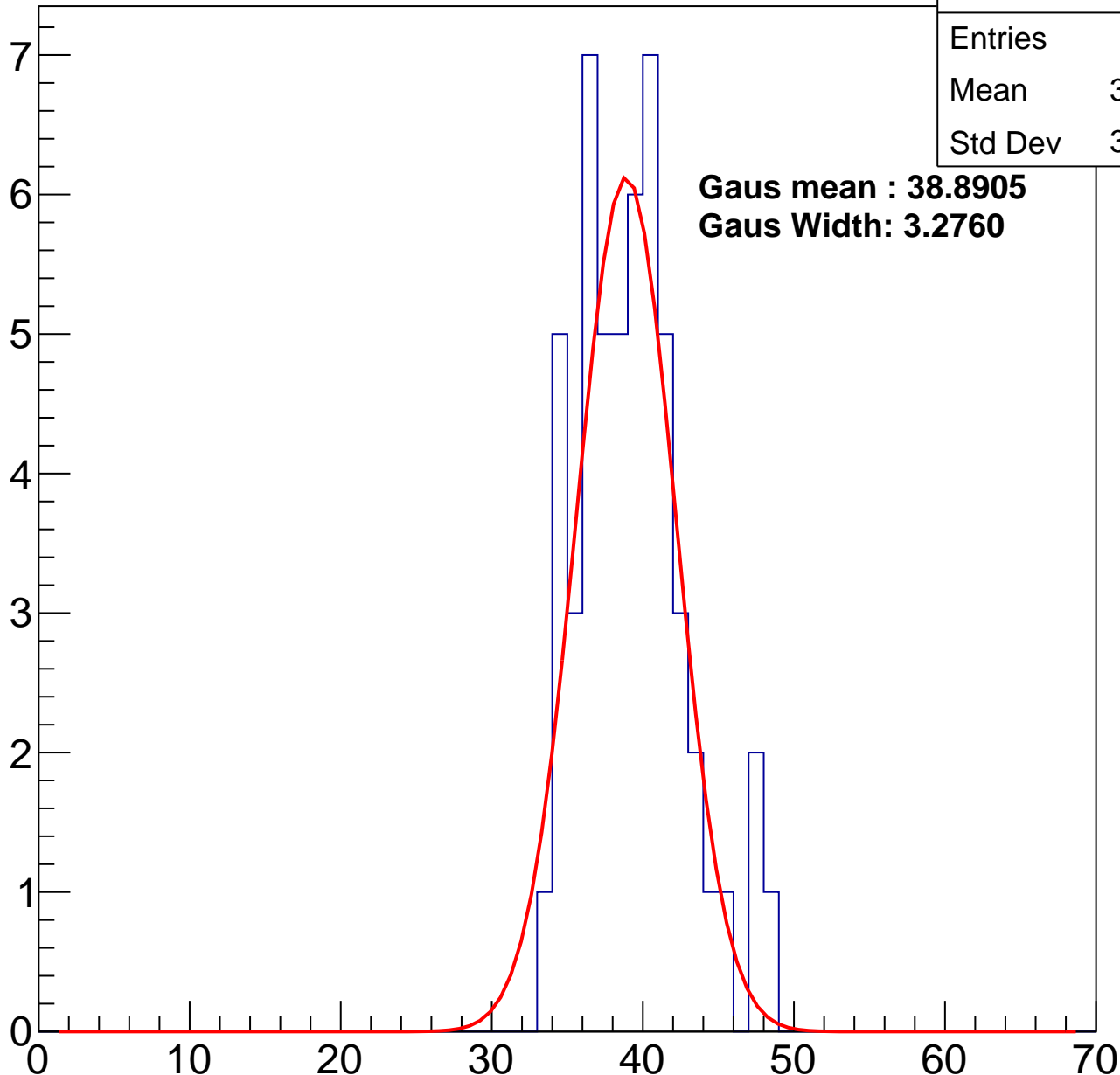
Entry

7
6
5
4
3
2
1
0

Entries	54
Mean	38.83
Std Dev	3.473

Gaus mean : 38.8905
Gaus Width: 3.2760

ampl

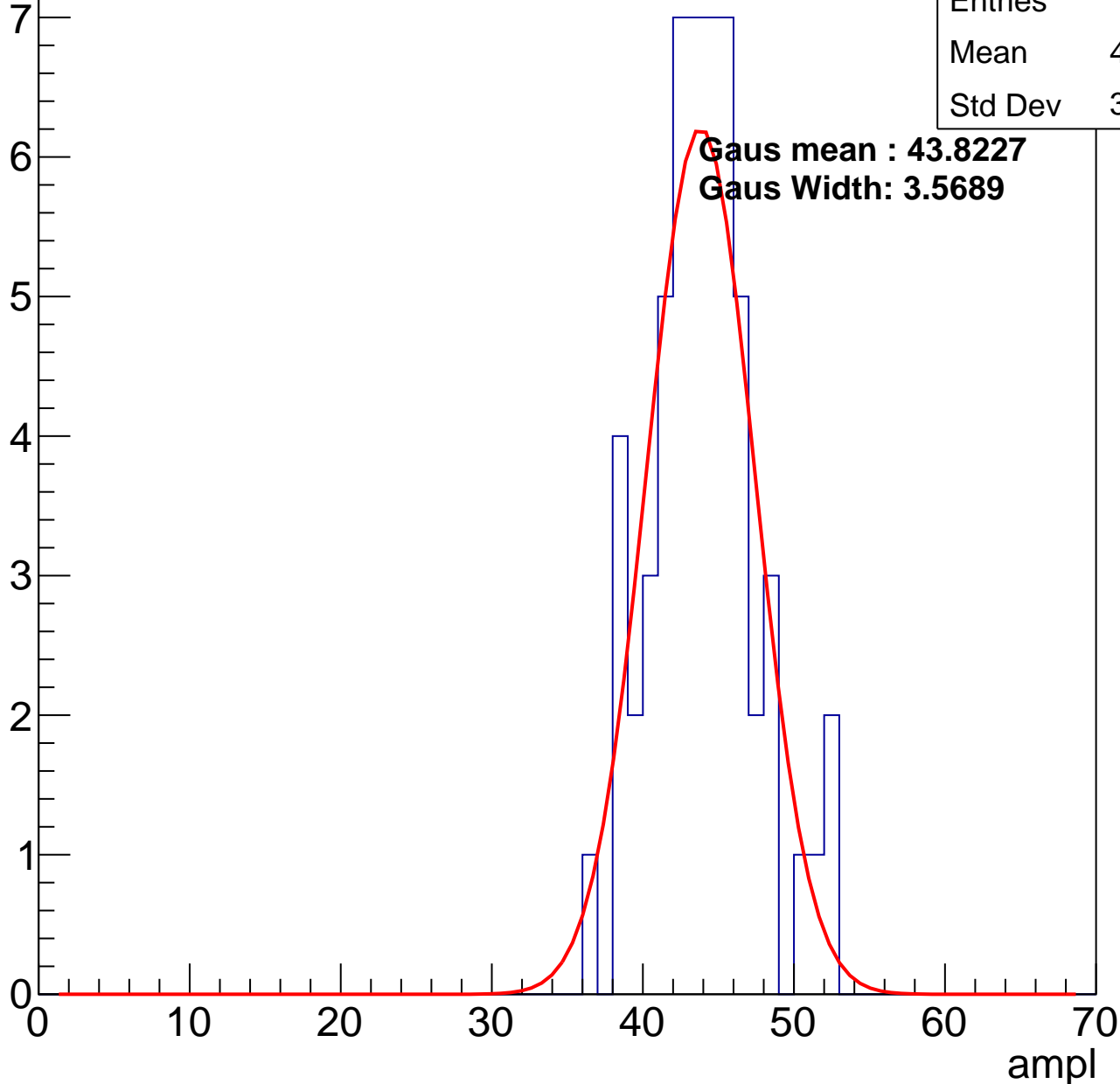


B0L000S, U4-ch1, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

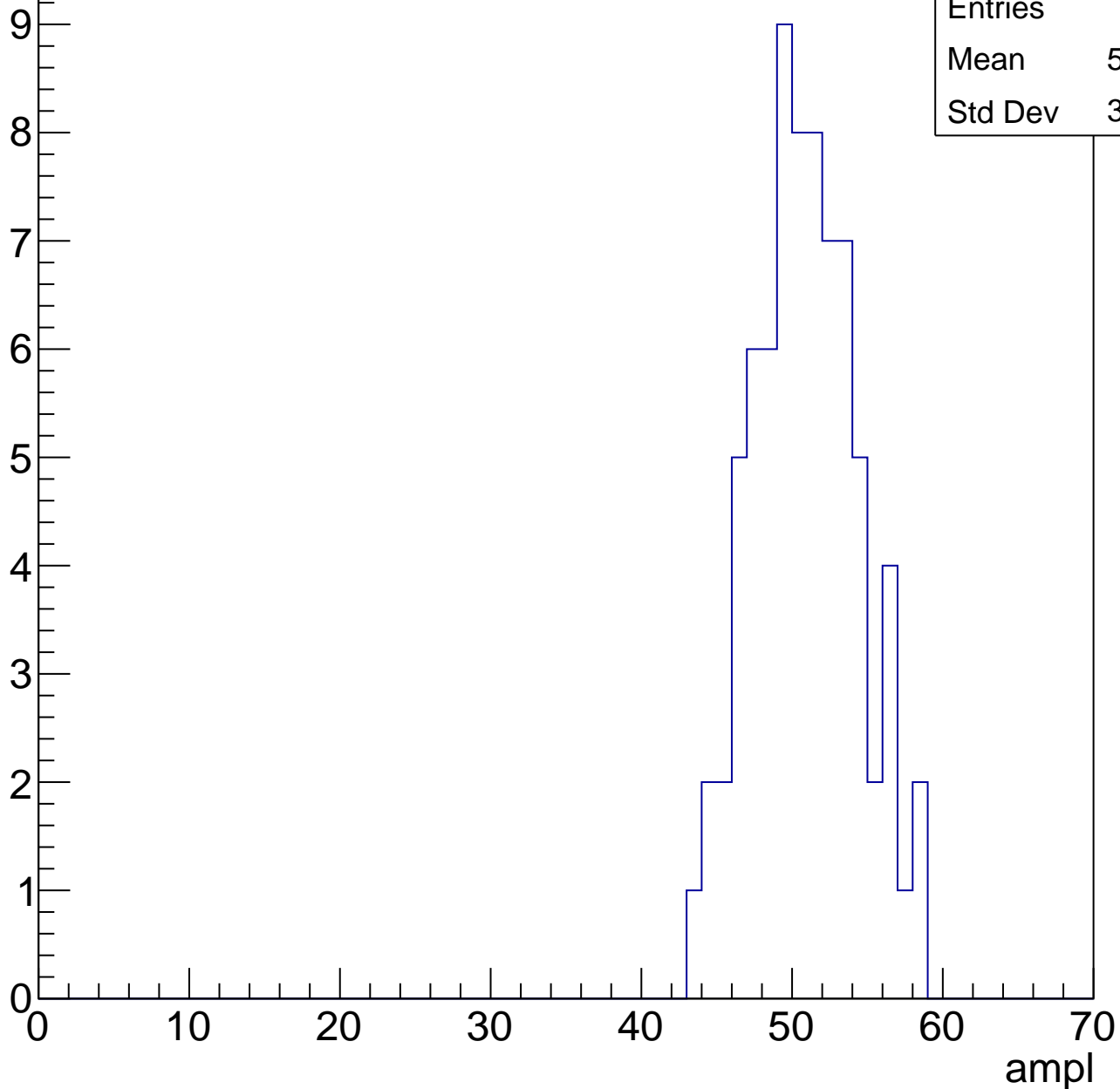
Entries	57
Mean	43.54
Std Dev	3.454



B0L000S, U4-ch1, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

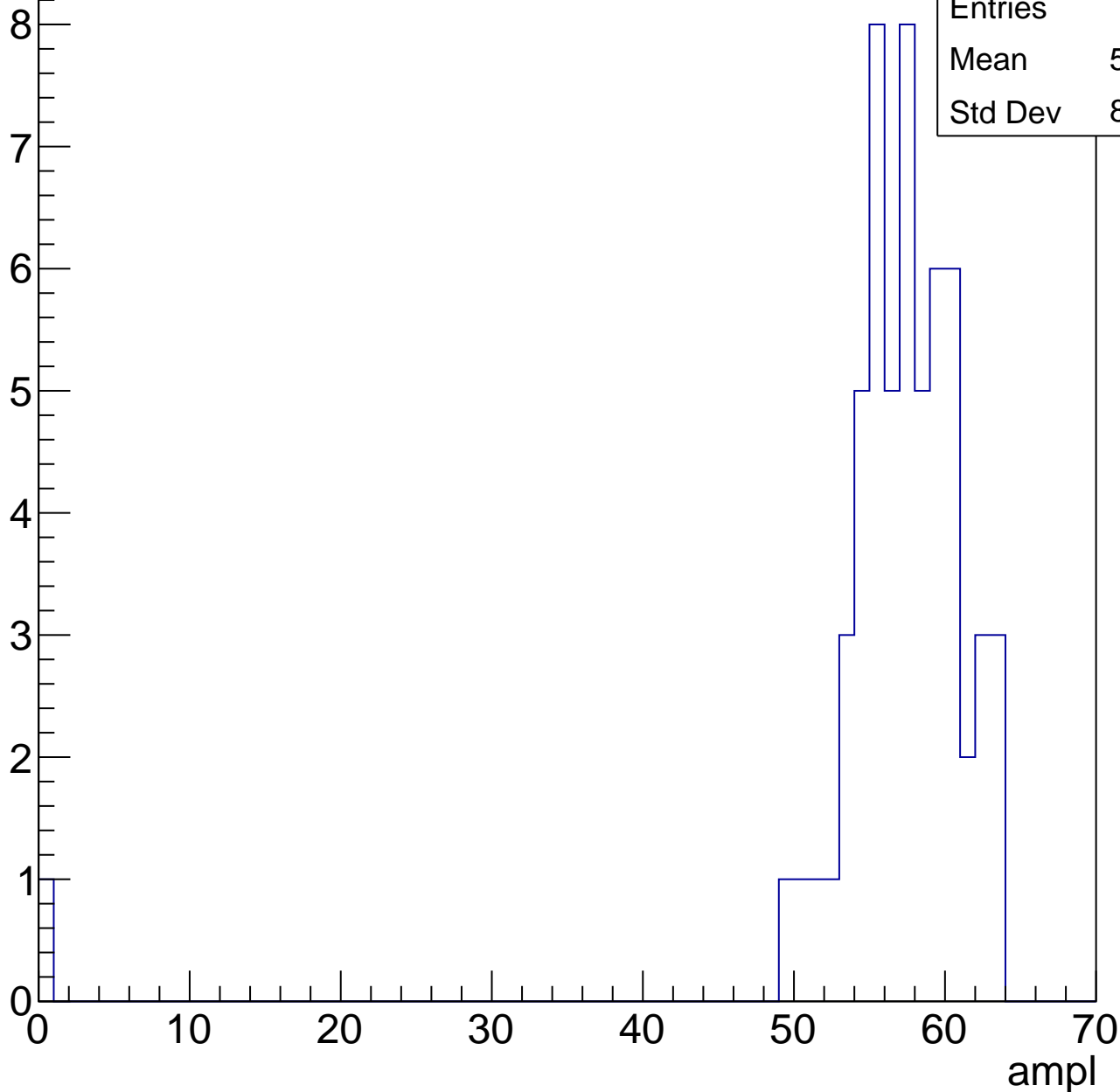


B0L000S, U4-ch1, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

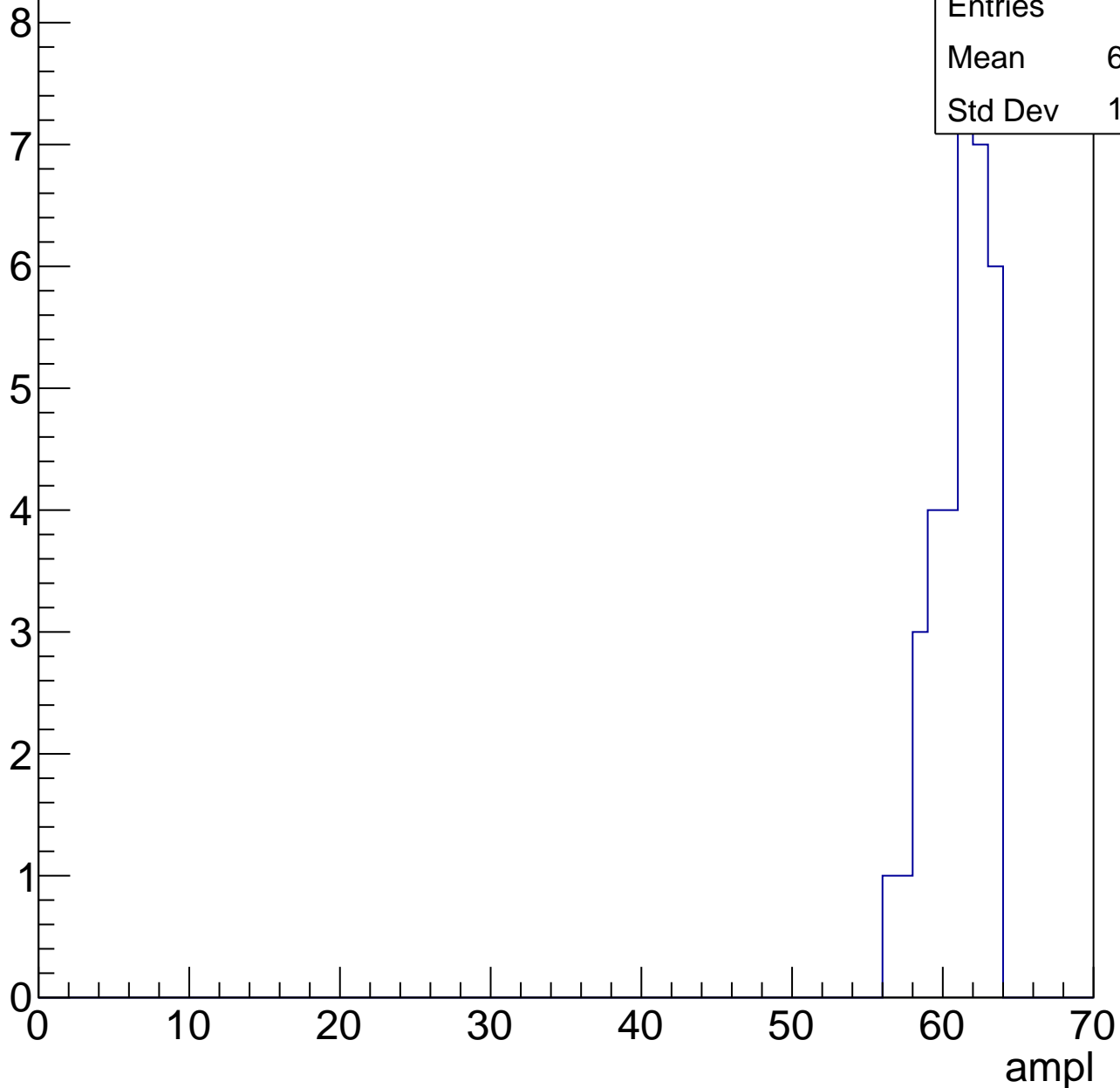
Entries	59
Mean	56.07
Std Dev	8.025



B0L000S, U4-ch1, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch1, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch1, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch2, adc0

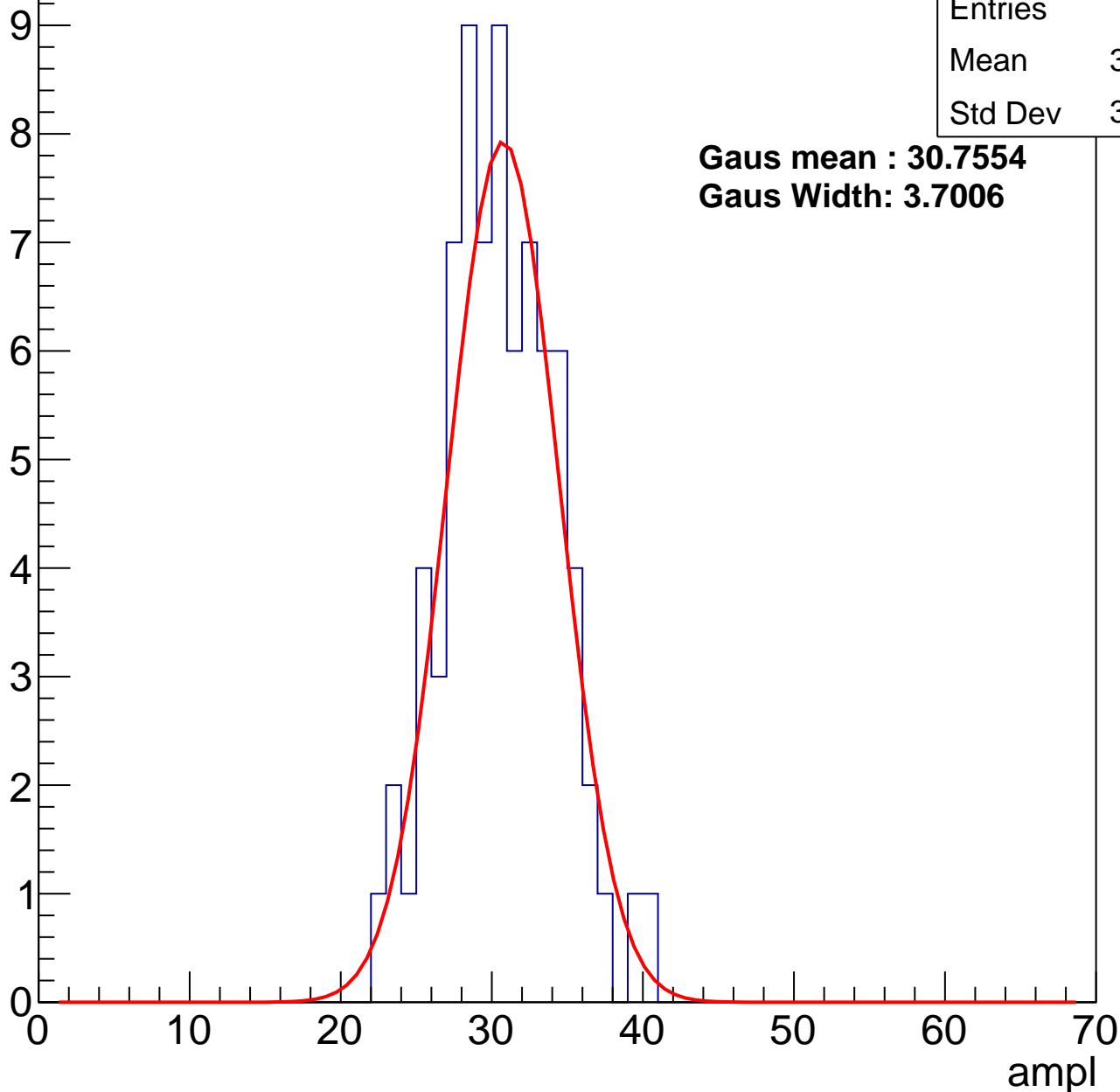
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	30.18
Std Dev	3.667

Gaus mean : 30.7554

Gaus Width: 3.7006



B0L000S, U4-ch2, adc1

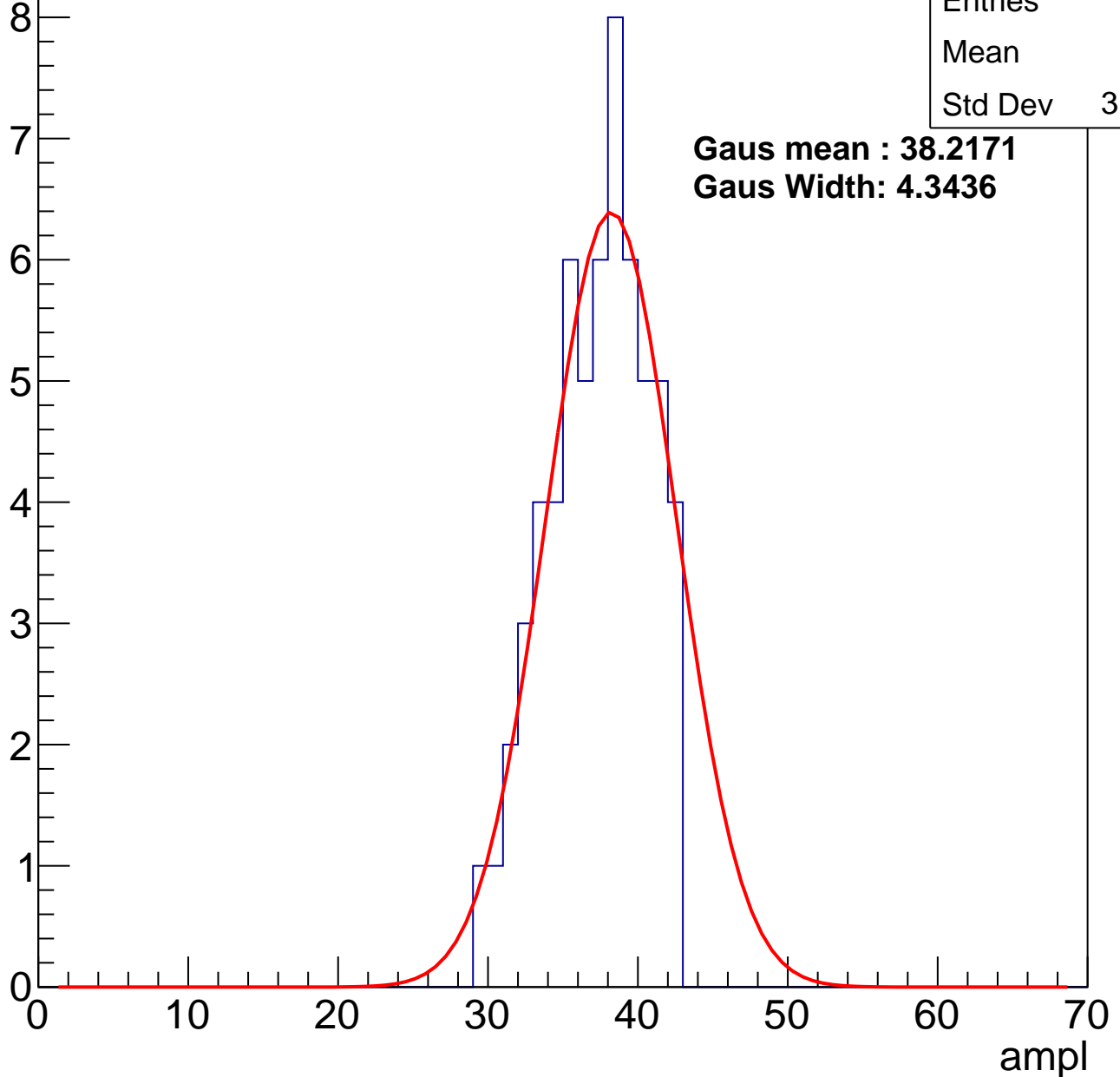
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.8
Std Dev	3.275

Gaus mean : 38.2171

Gaus Width: 4.3436



B0L000S, U4-ch2, adc2

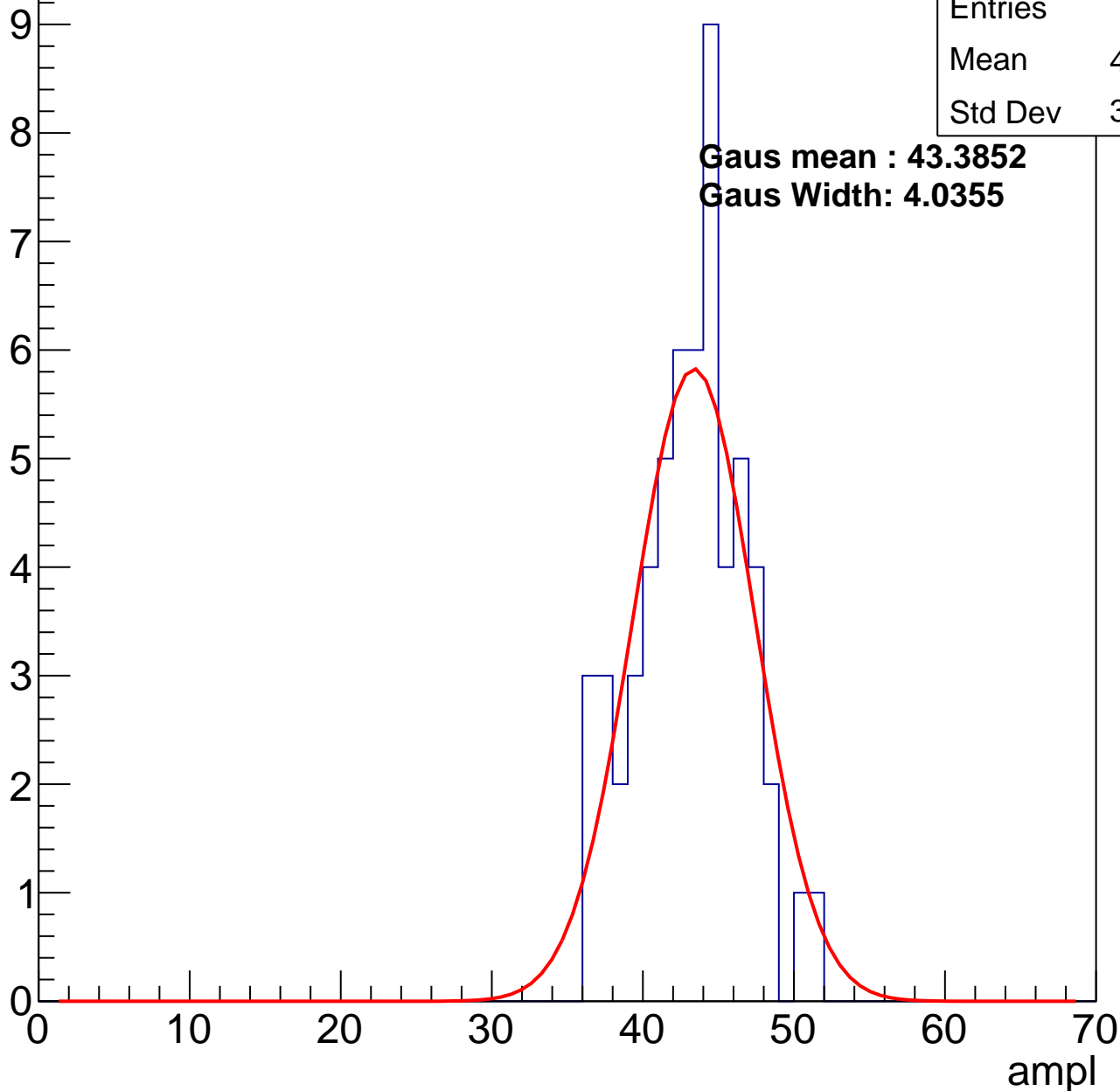
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.72
Std Dev	3.493

Gaus mean : 43.3852

Gaus Width: 4.0355

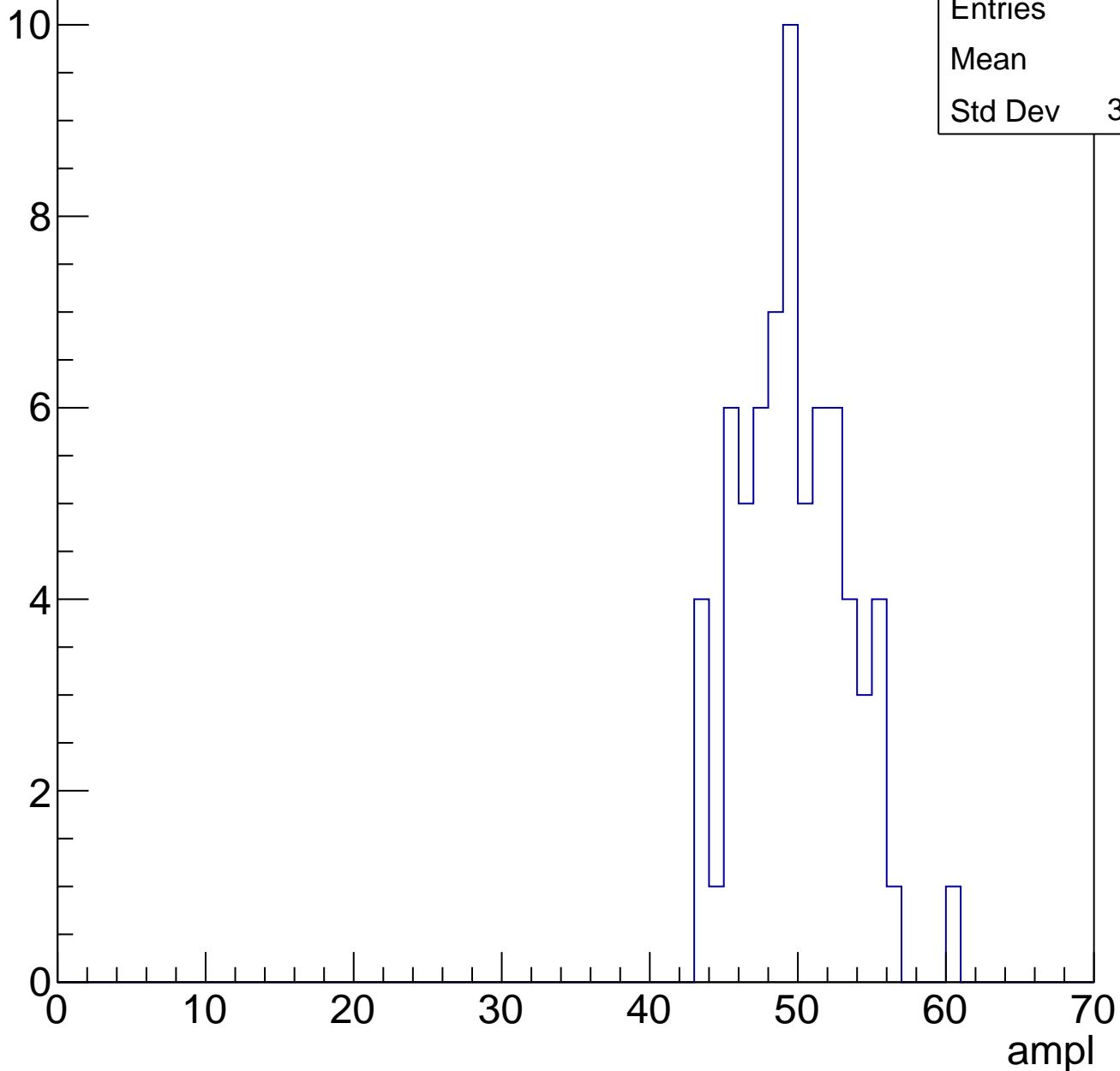


B0L000S, U4-ch2, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.3
Std Dev	3.552

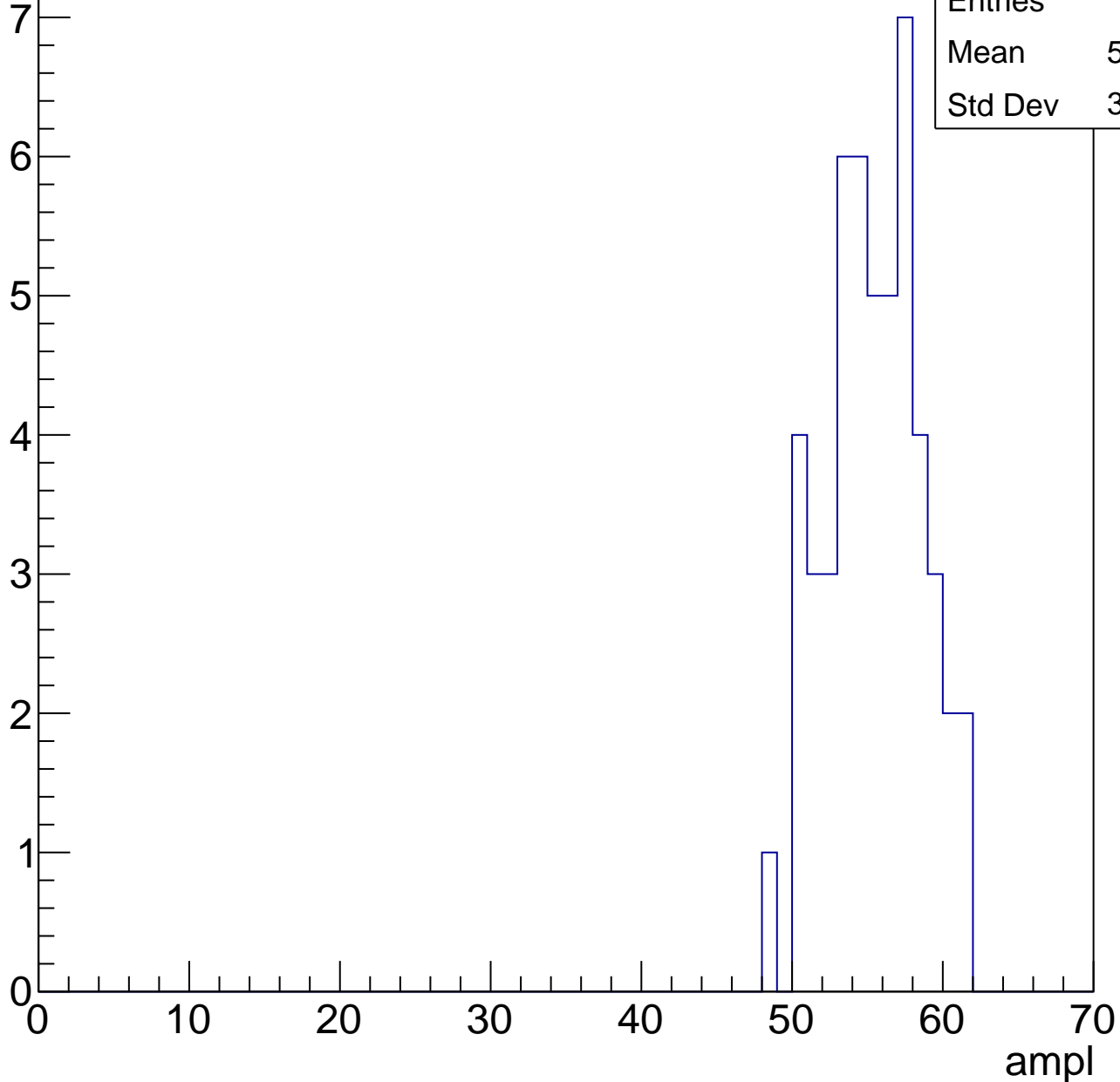


B0L000S, U4-ch2, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	54.98
Std Dev	3.109



B0L000S, U4-ch2, adc5

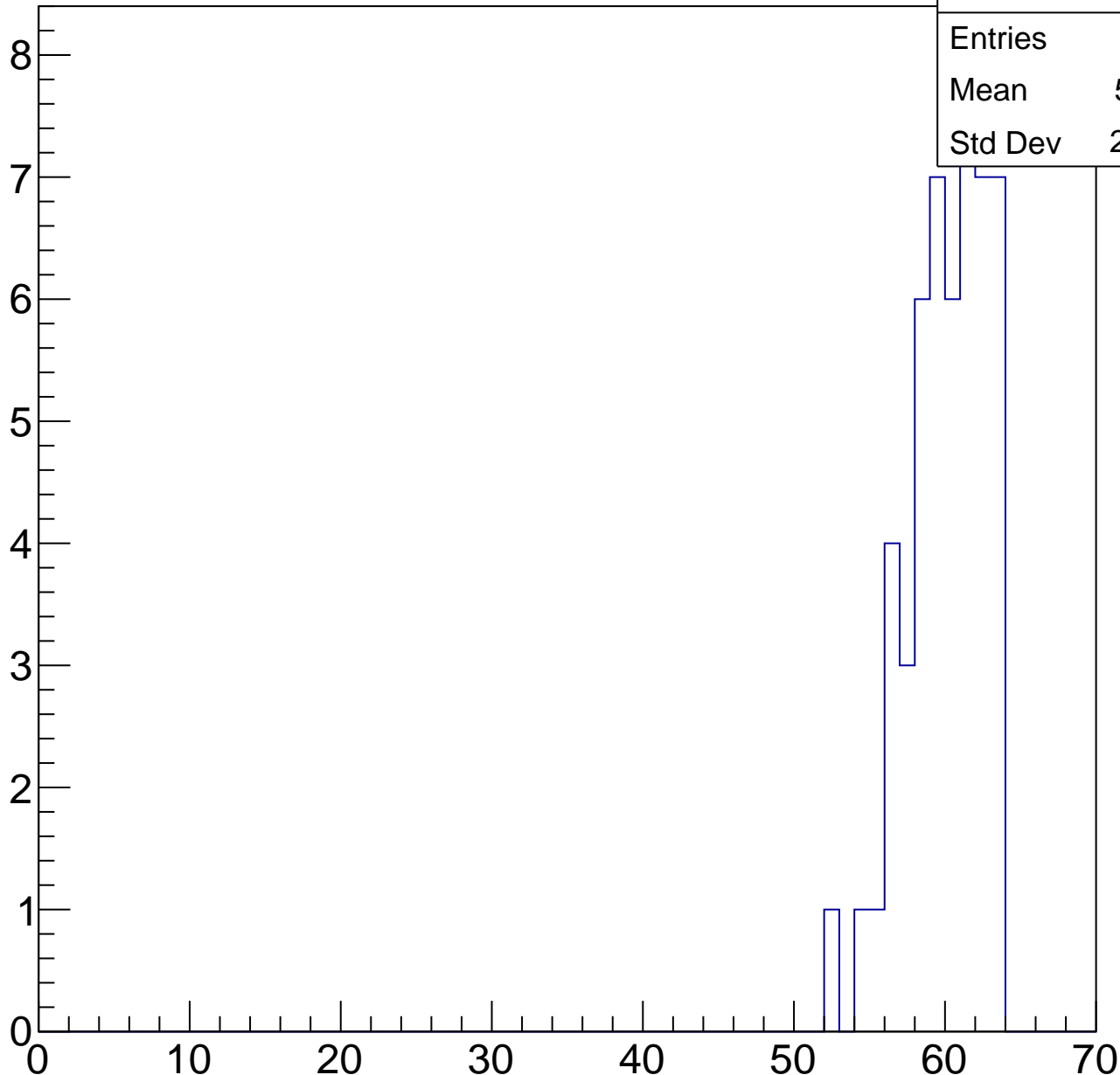
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.61
Std Dev	2.575

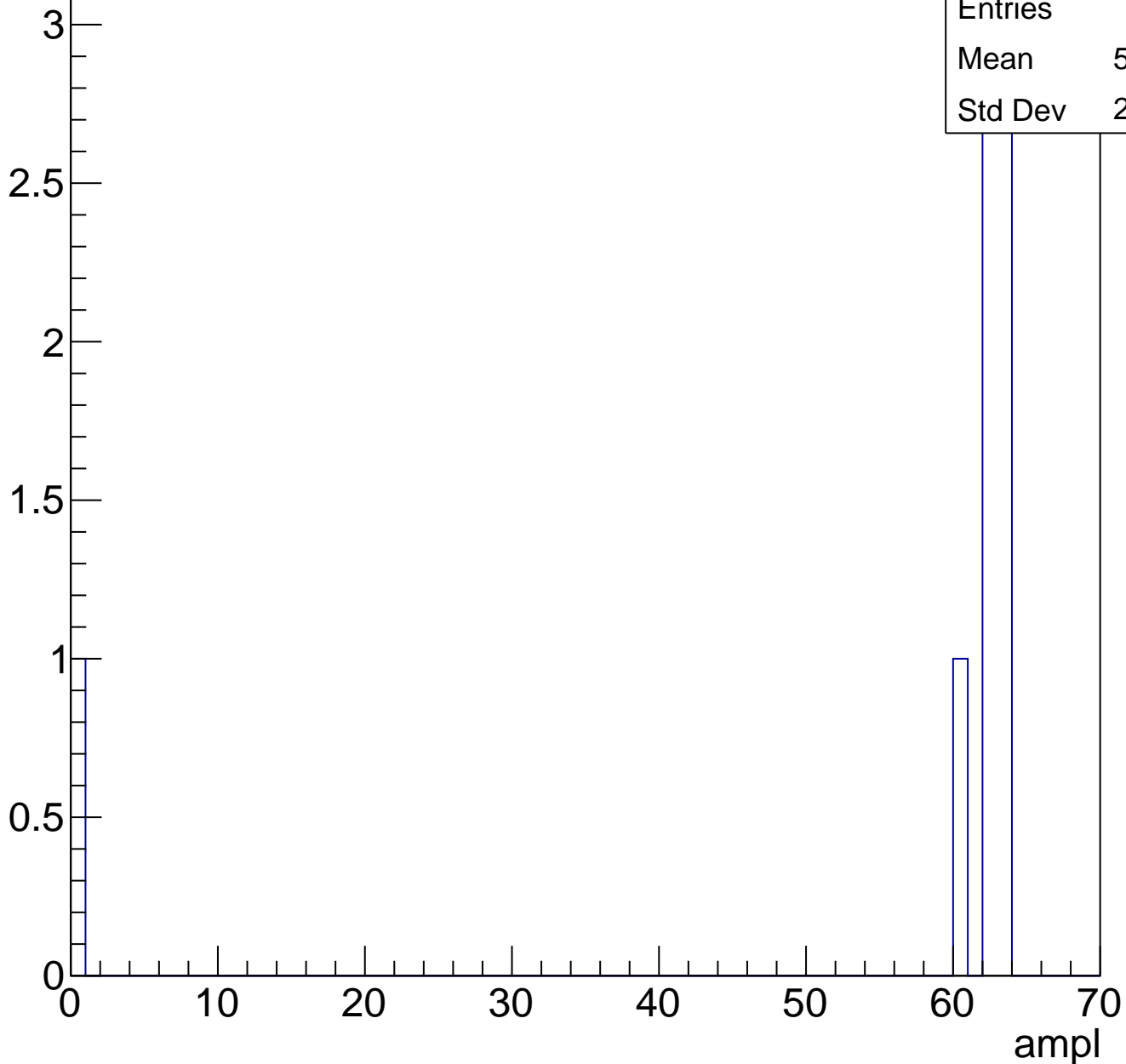
ampl



B0L000S, U4-ch2, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch2, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch3, adc0

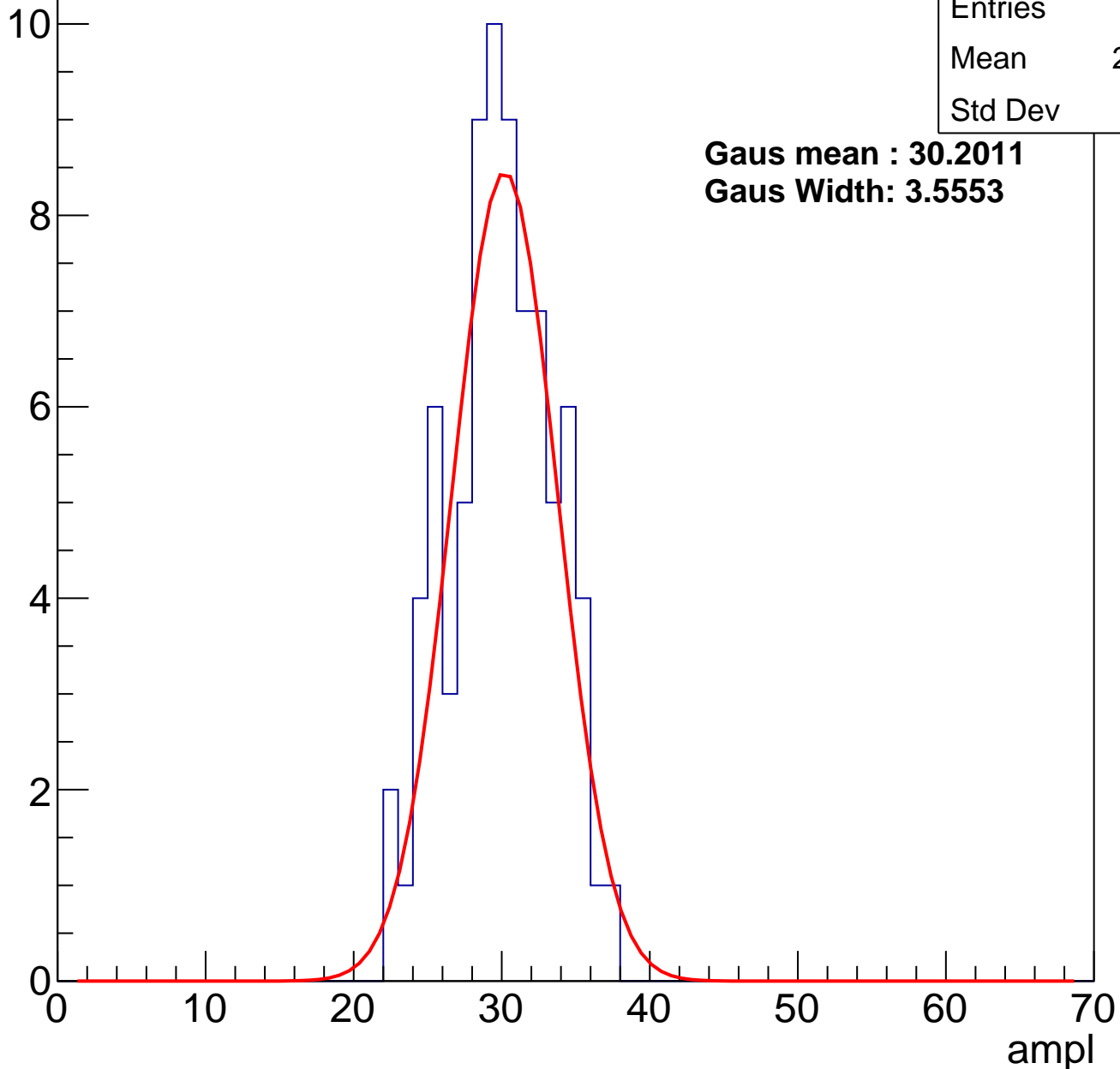
calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	29.51
Std Dev	3.45

Gaus mean : 30.2011

Gaus Width: 3.5553

Entry



B0L000S, U4-ch3, adc1

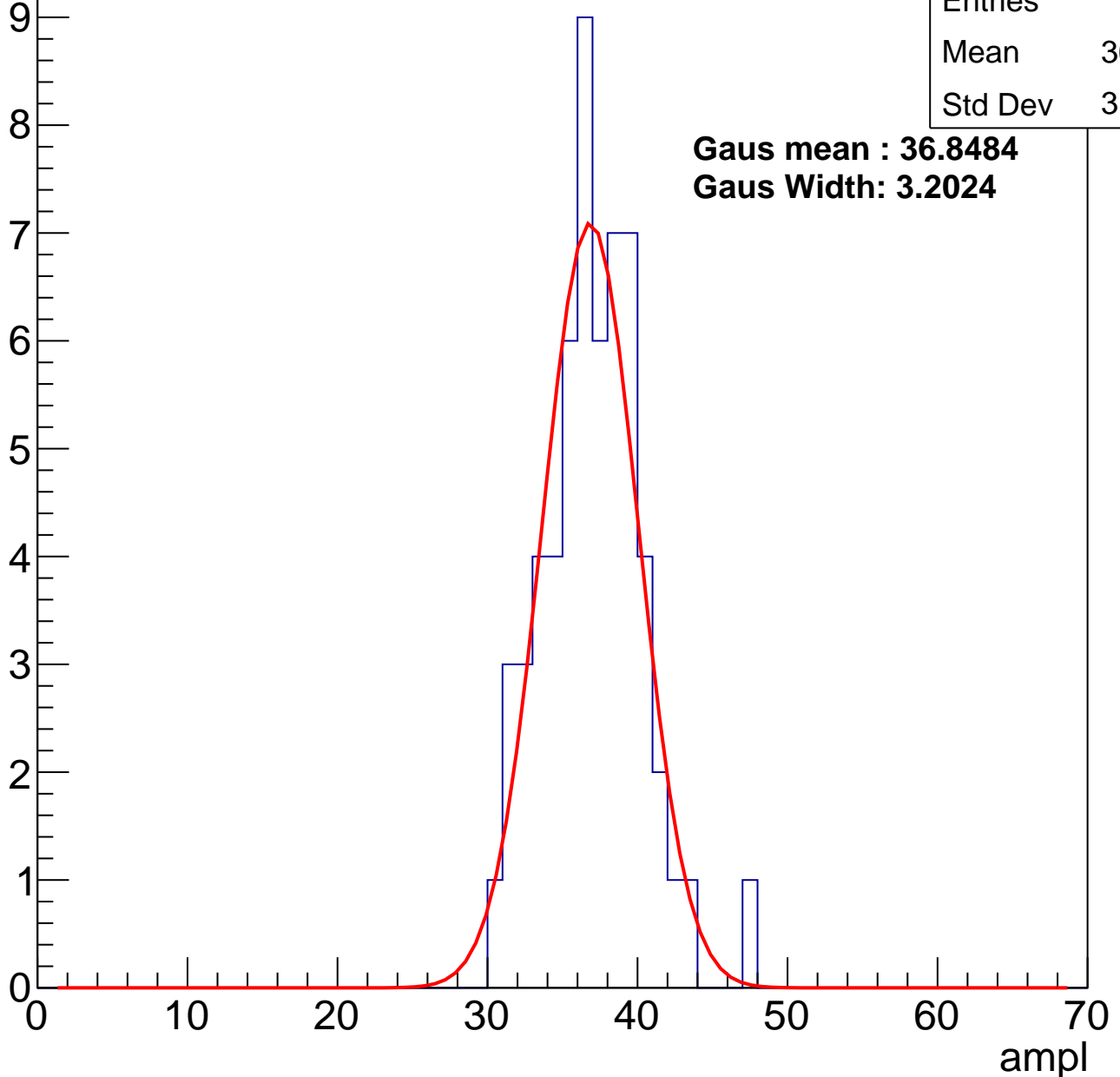
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.54
Std Dev	3.233

Gaus mean : 36.8484

Gaus Width: 3.2024



B0L000S, U4-ch3, adc2

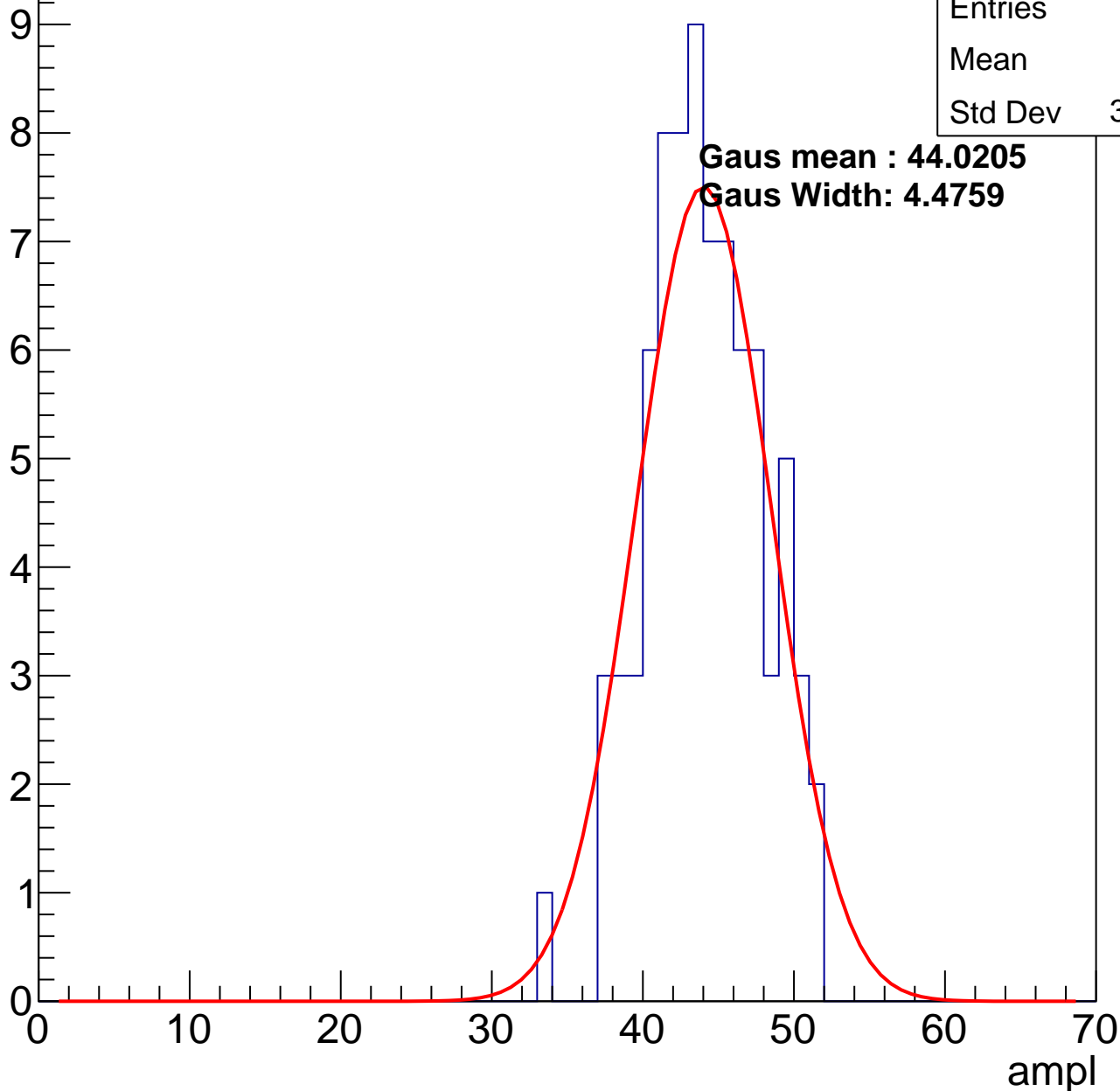
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	43.6
Std Dev	3.727

Gaus mean : 44.0205

Gaus Width: 4.4759

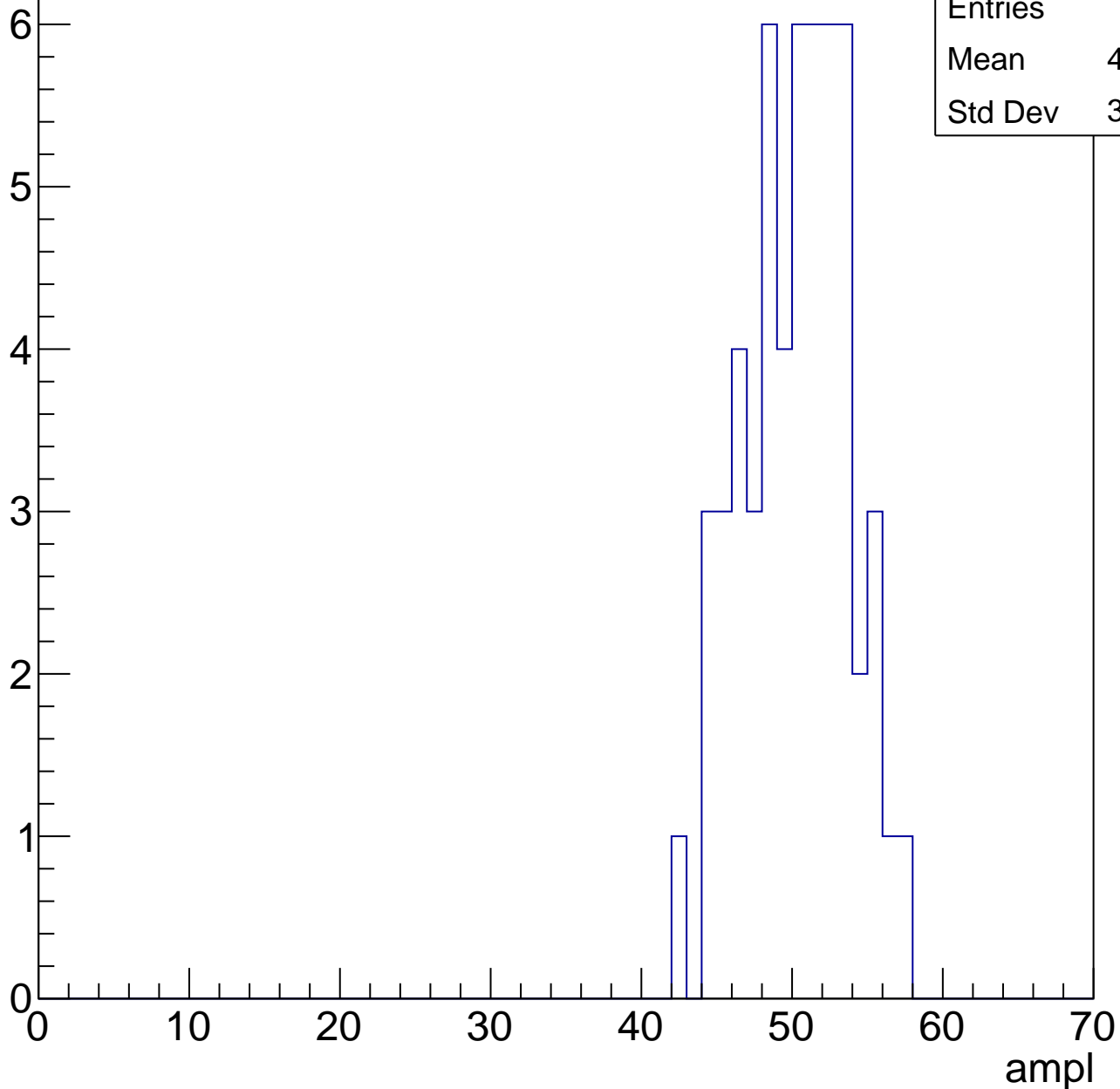


B0L000S, U4-ch3, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.82
Std Dev	3.412

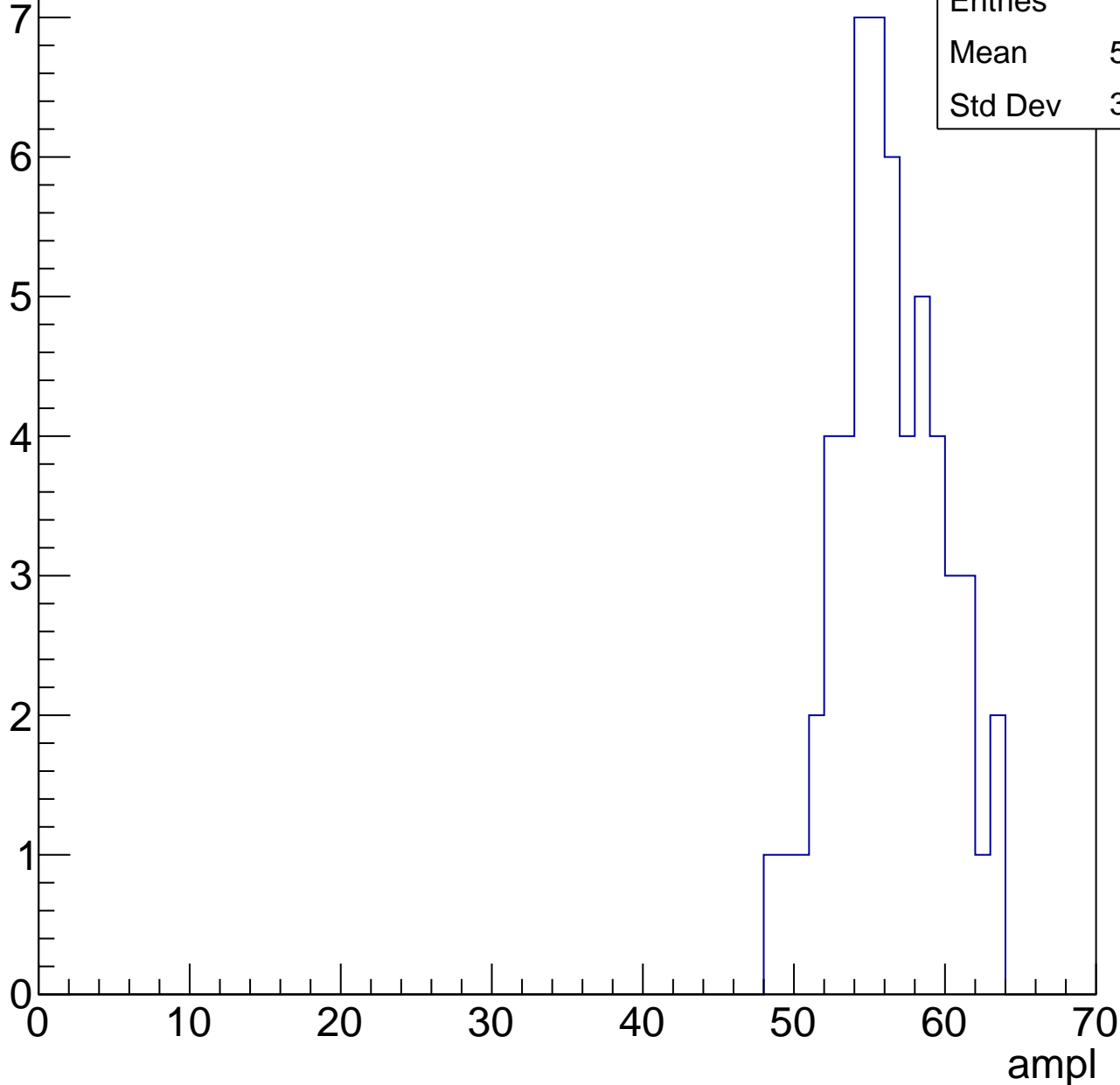


B0L000S, U4-ch3, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.87
Std Dev	3.438

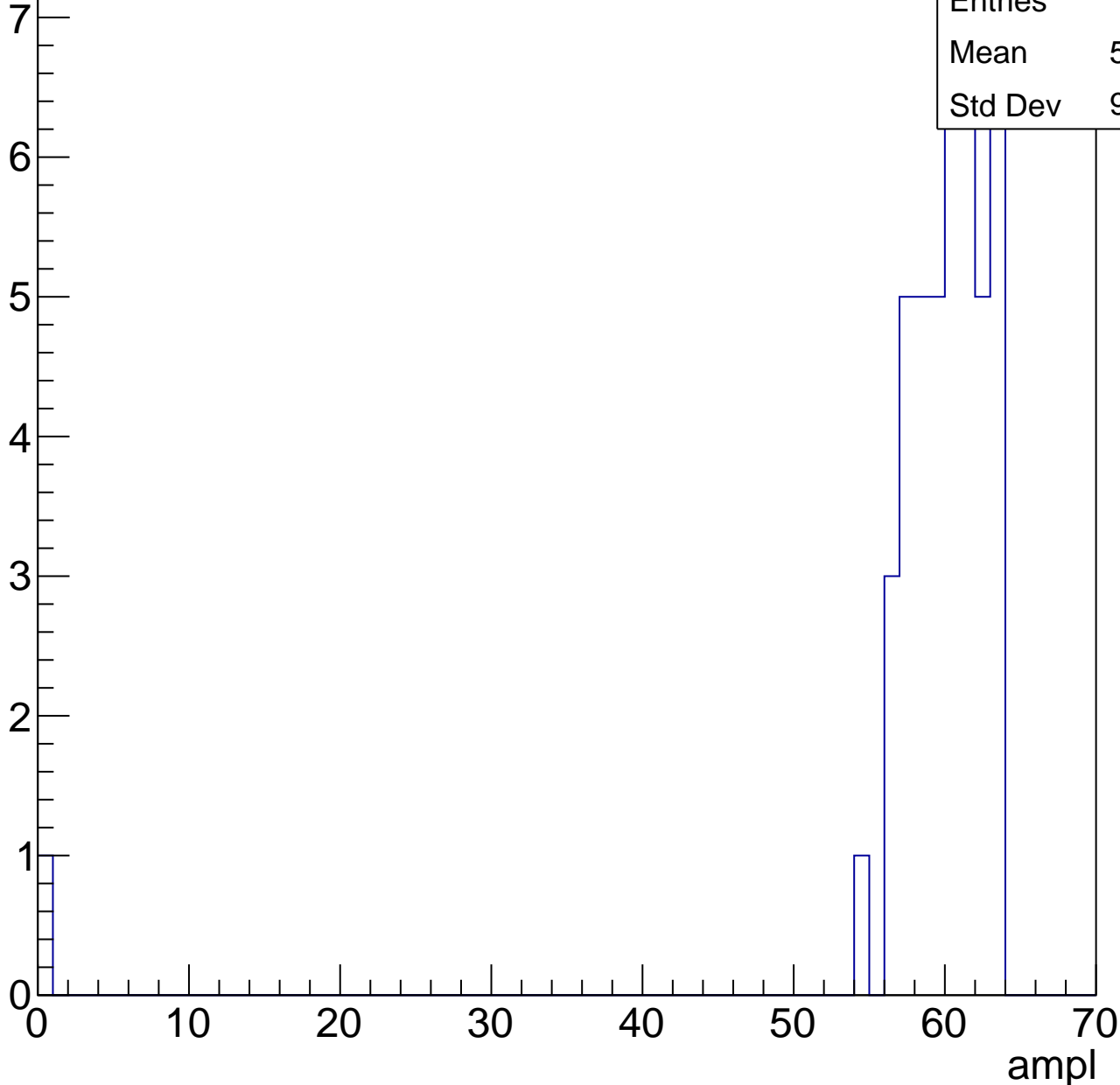


B0L000S, U4-ch3, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

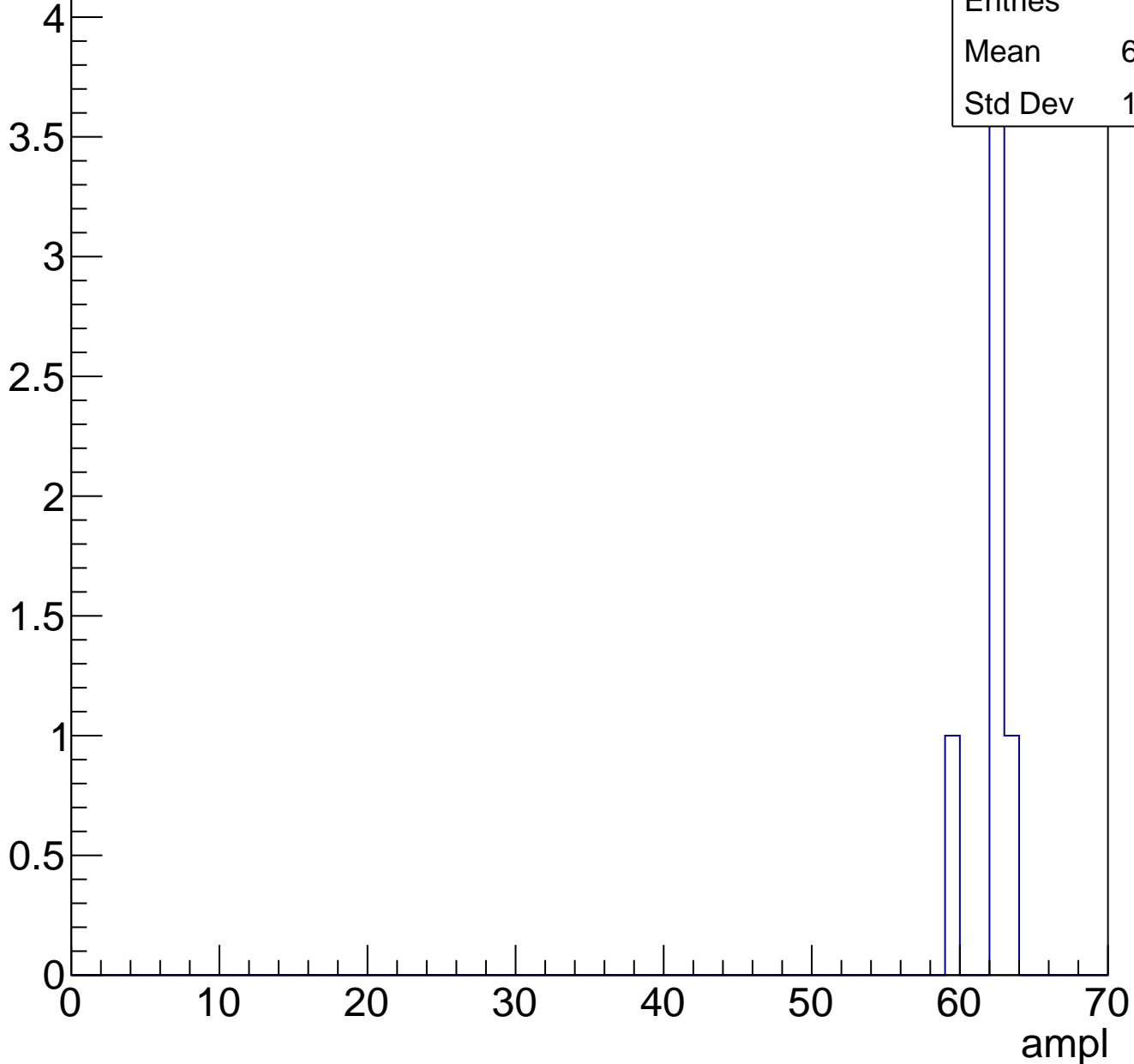
Entries	46
Mean	58.48
Std Dev	9.014



B0L000S, U4-ch3, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch3, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



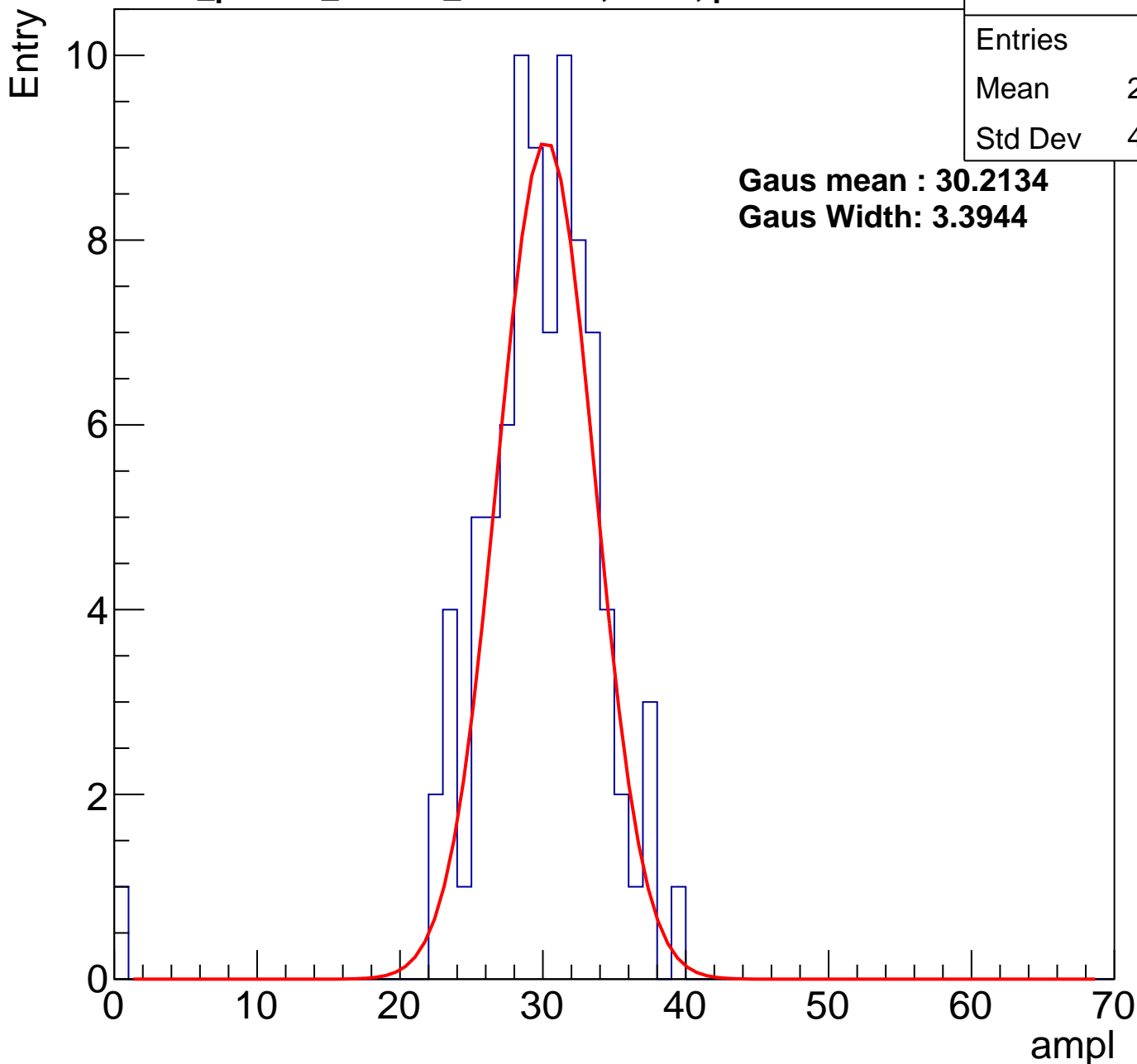
B0L000S, U4-ch4, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	86
Mean	29.27
Std Dev	4.828

Gaus mean : 30.2134

Gaus Width: 3.3944



B0L000S, U4-ch4, adc1

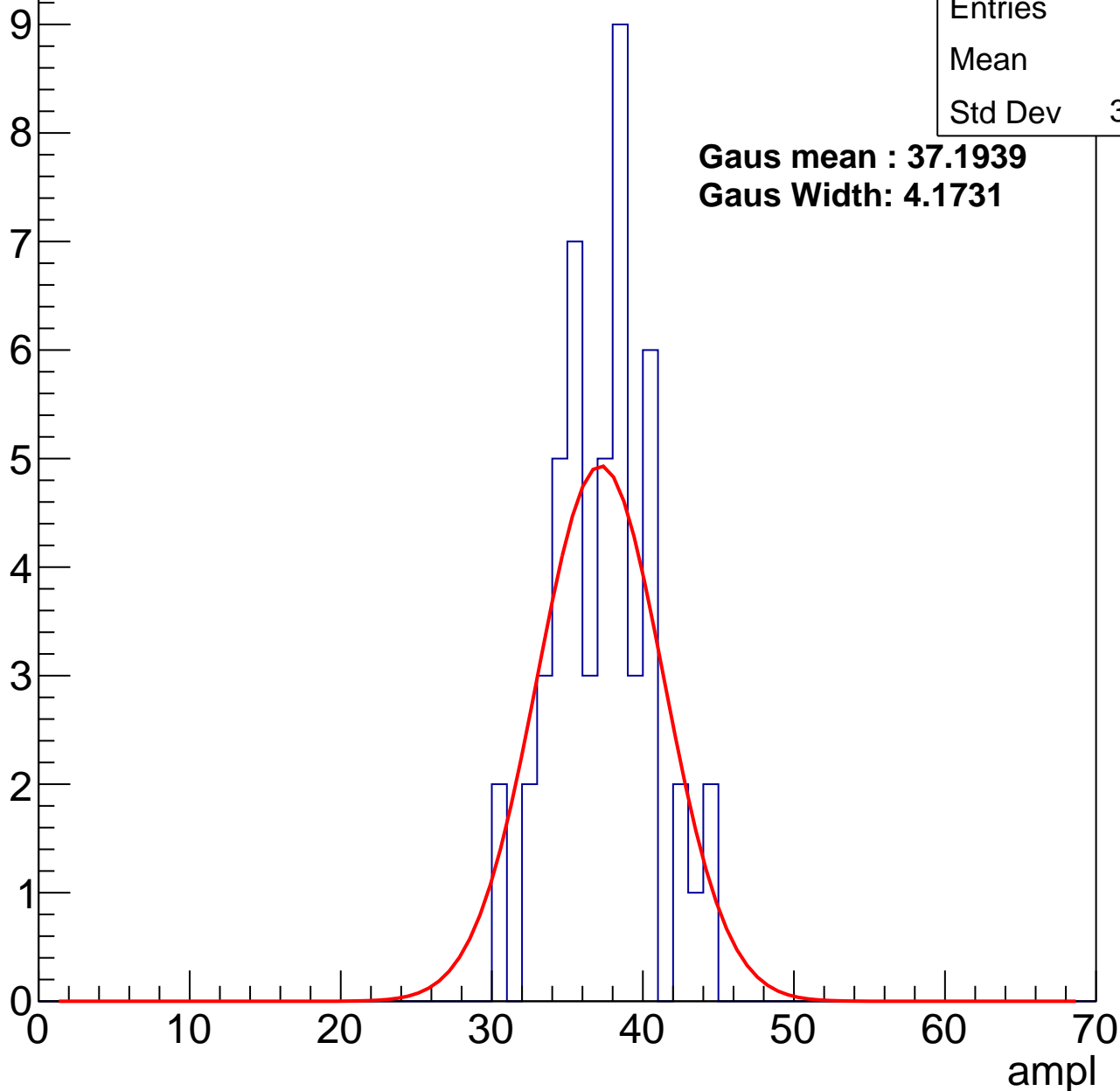
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	36.9
Std Dev	3.257

Gaus mean : 37.1939

Gaus Width: 4.1731



B0L000S, U4-ch4, adc2

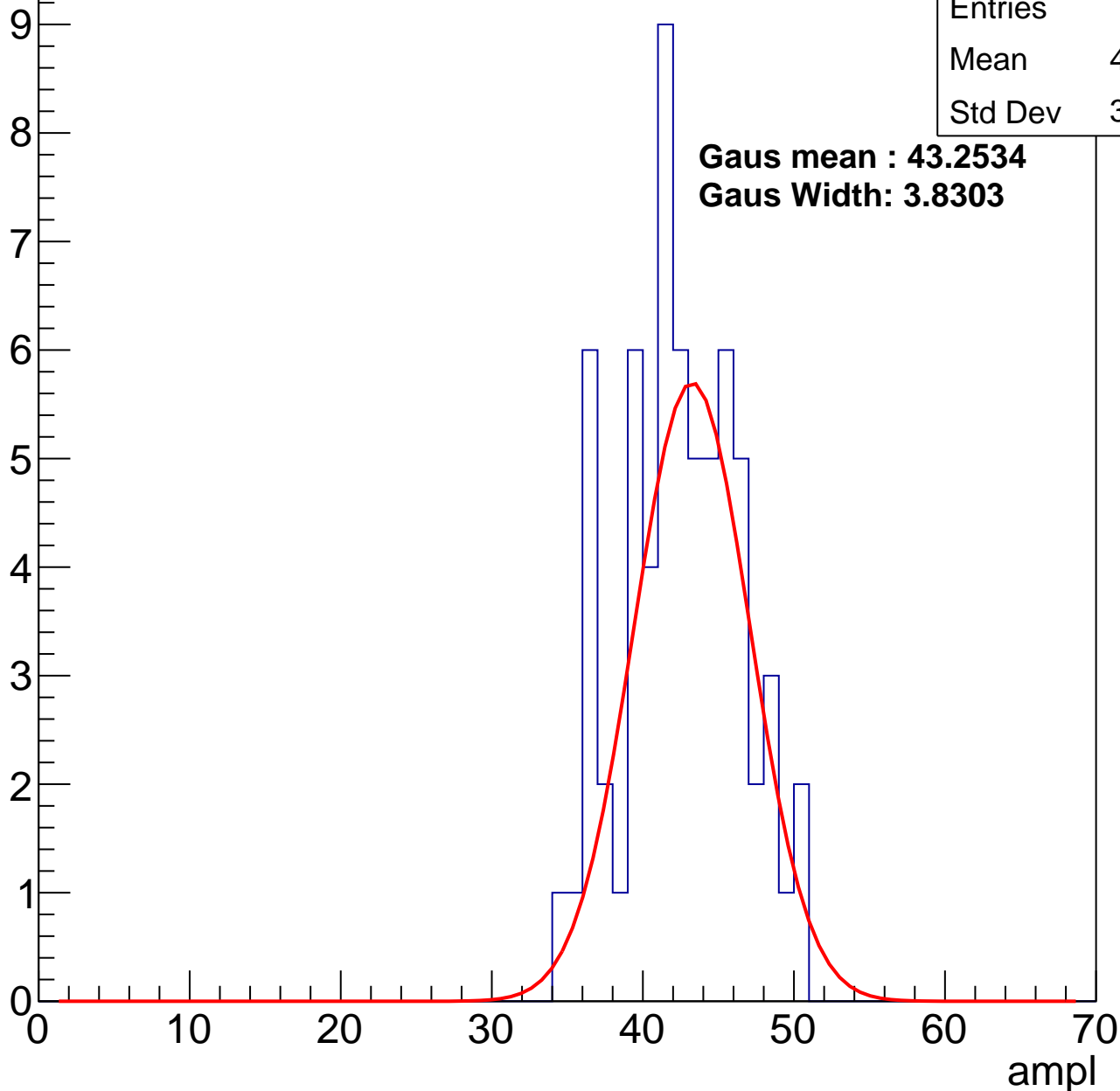
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.06
Std Dev	3.859

Gaus mean : 43.2534

Gaus Width: 3.8303

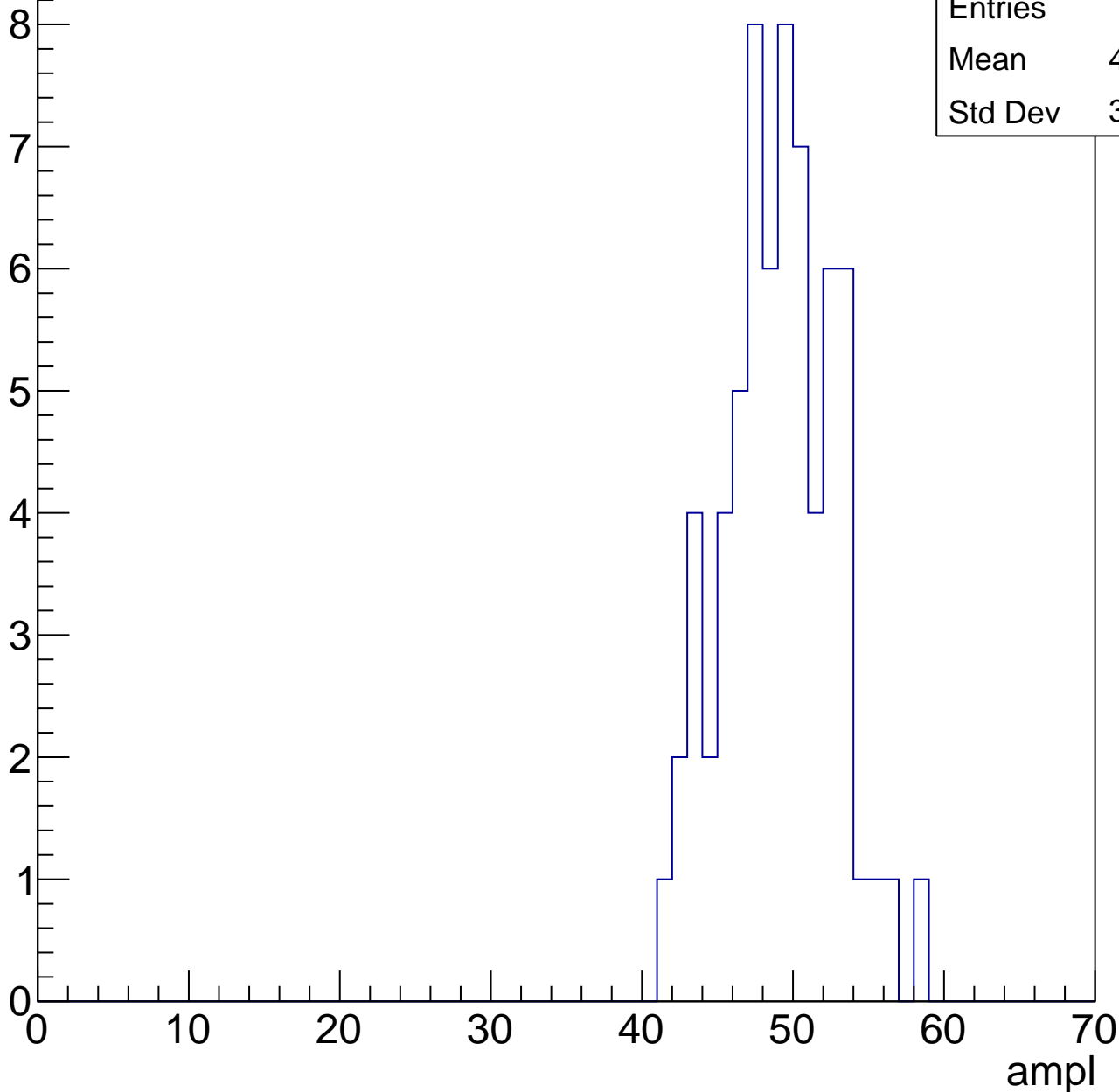


B0L000S, U4-ch4, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.63
Std Dev	3.578



B0L000S, U4-ch4, adc4

calib_packv5_042523_0143.root, FC#5, port B1

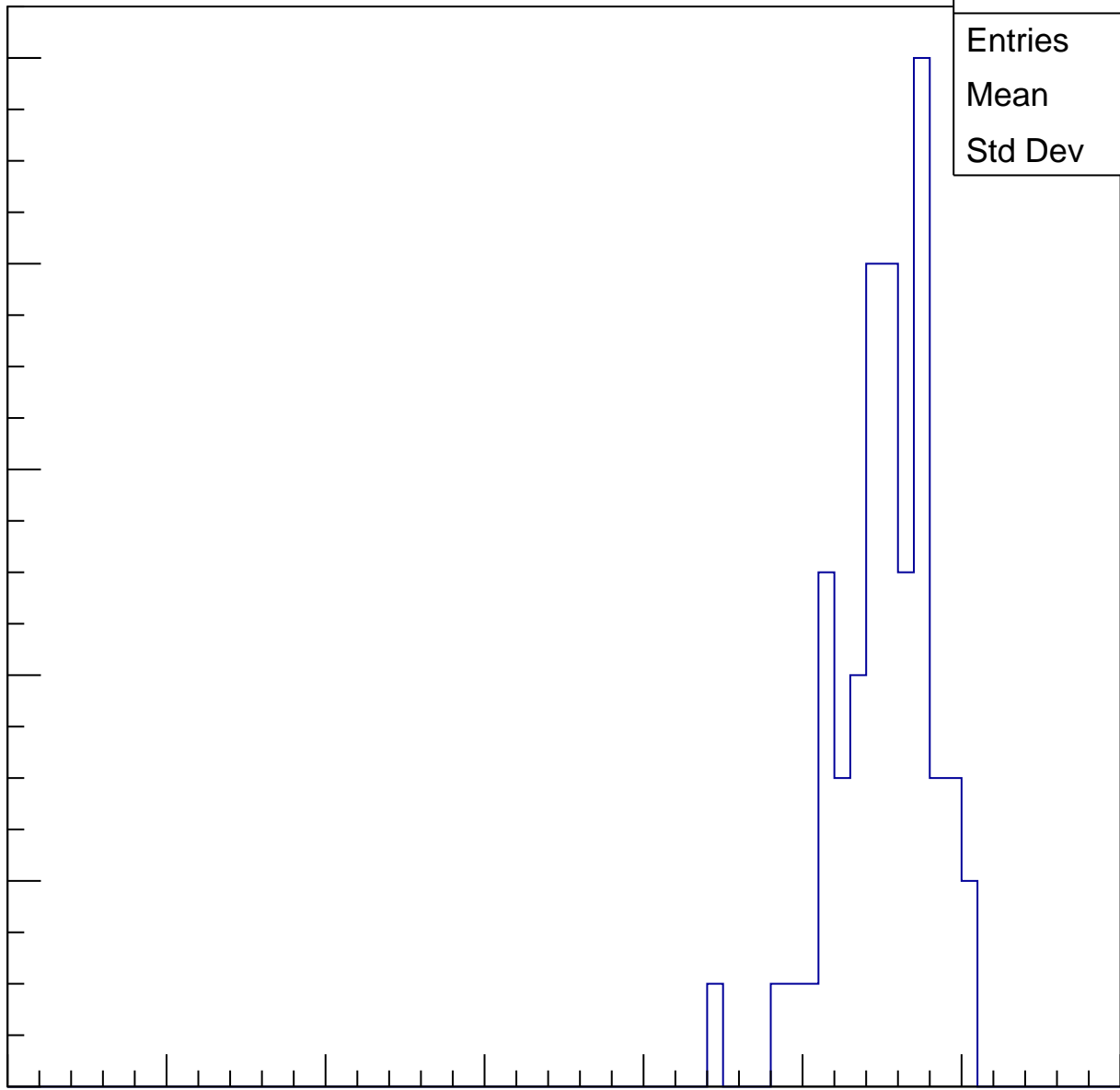
Entries	55
Mean	54.67
Std Dev	3.093

Entry

10
8
6
4
2
0

ampl

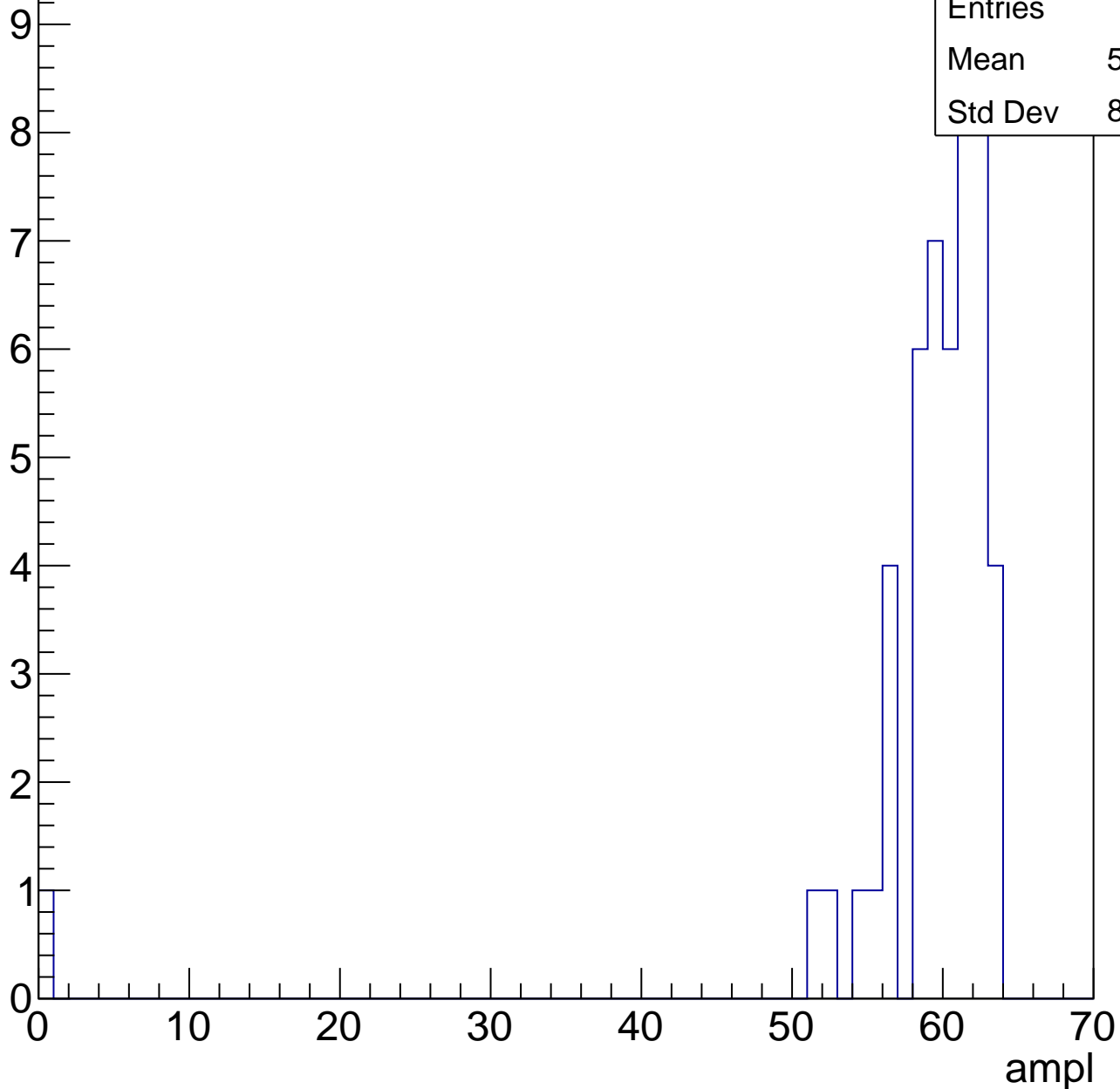
0 10 20 30 40 50 60 70



B0L000S, U4-ch4, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

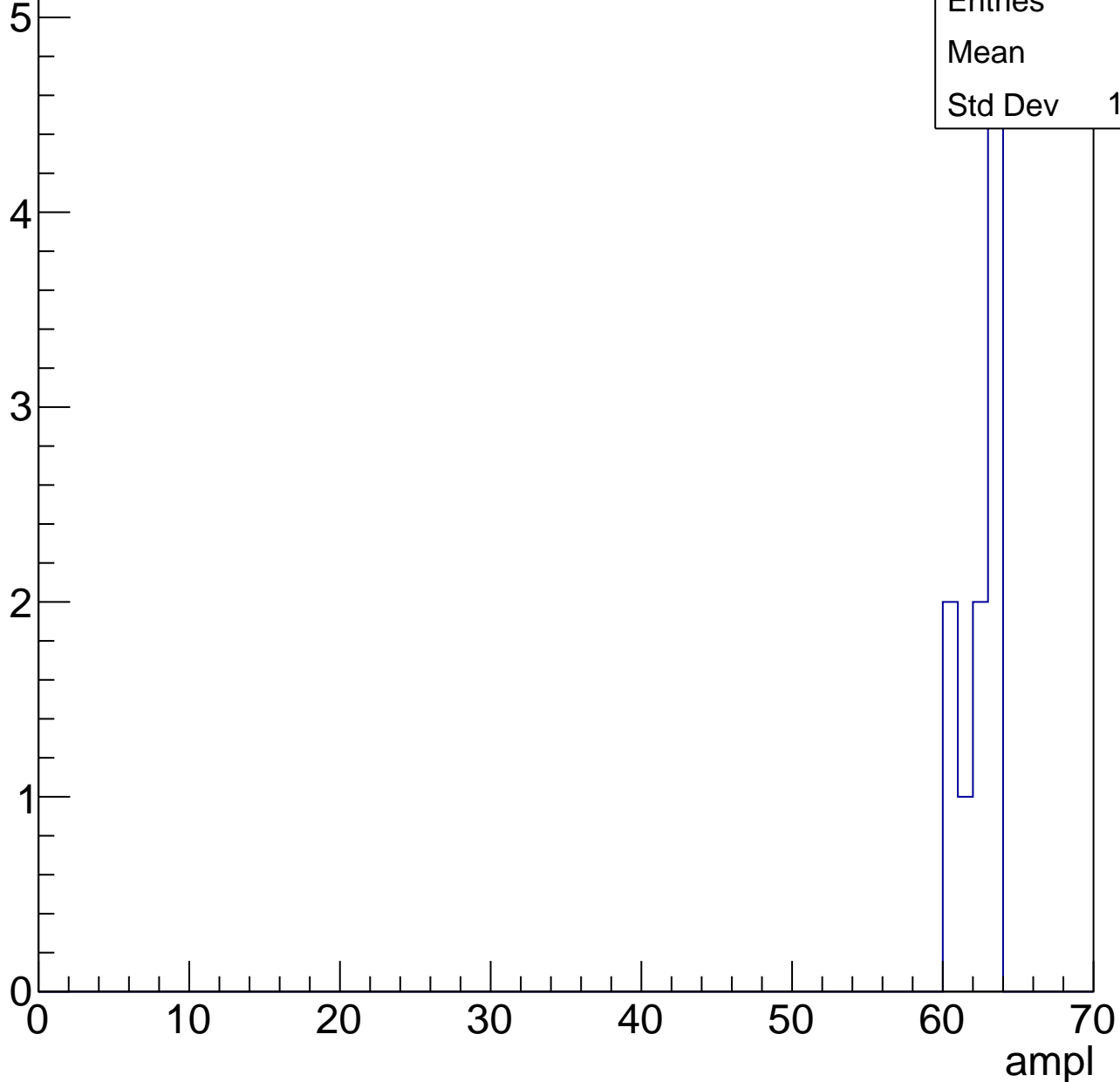


B0L000S, U4-ch4, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62
Std Dev	1.183



B0L000S, U4-ch4, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L000S, U4-ch5, adc0

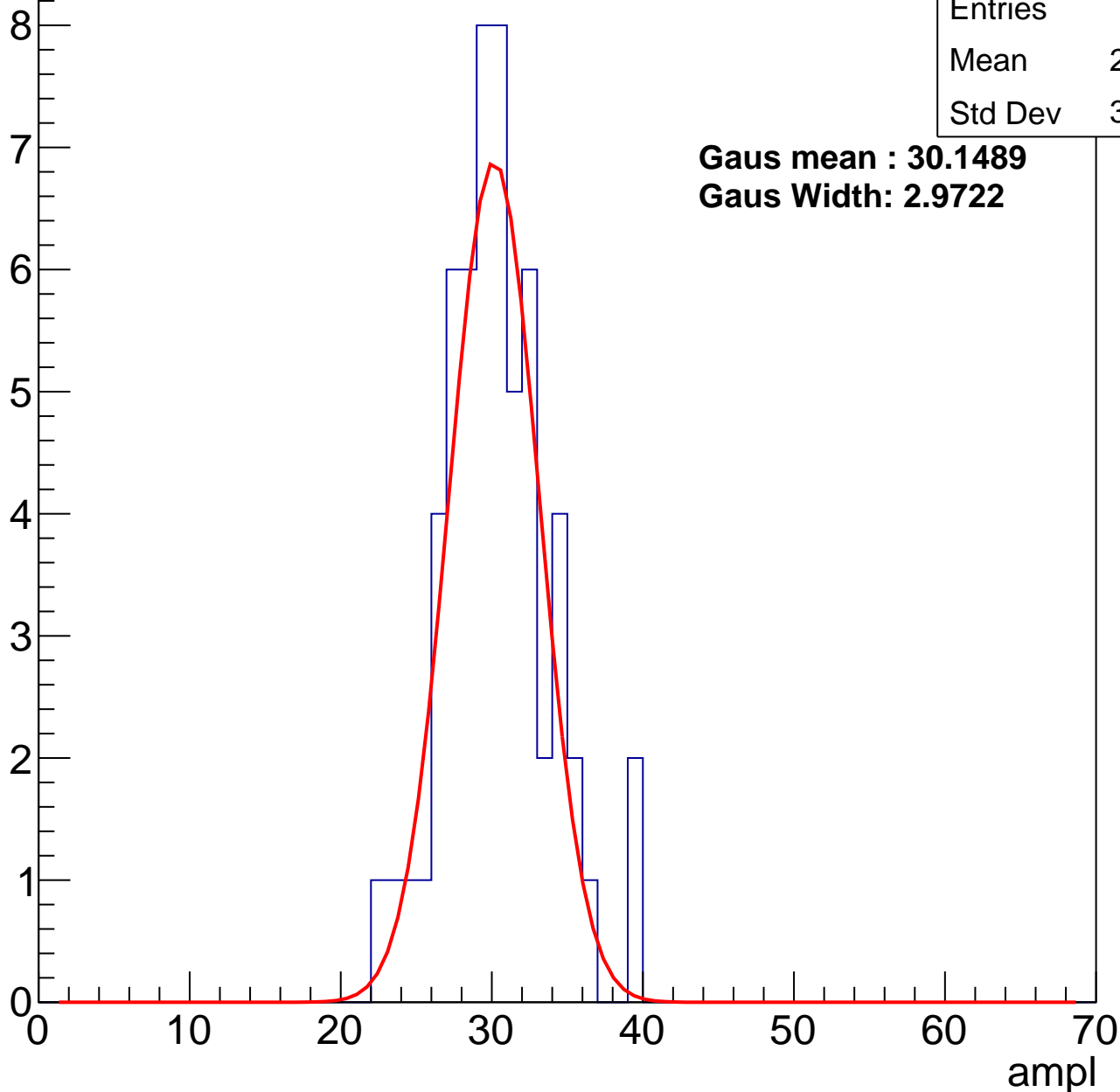
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.88
Std Dev	3.429

Gaus mean : 30.1489

Gaus Width: 2.9722



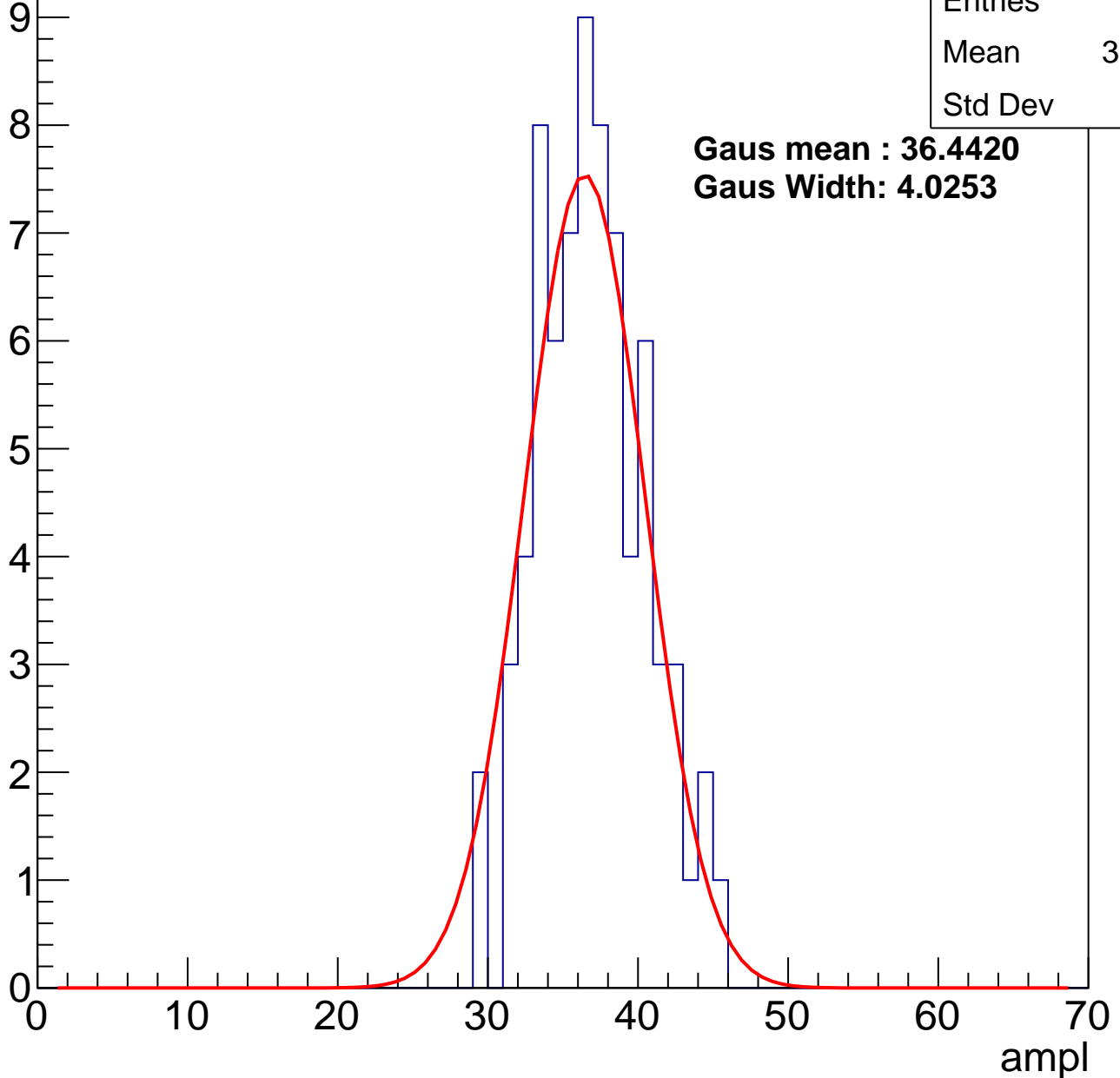
B0L000S, U4-ch5, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.47
Std Dev	3.55

Gaus mean : 36.4420
Gaus Width: 4.0253



B0L000S, U4-ch5, adc2

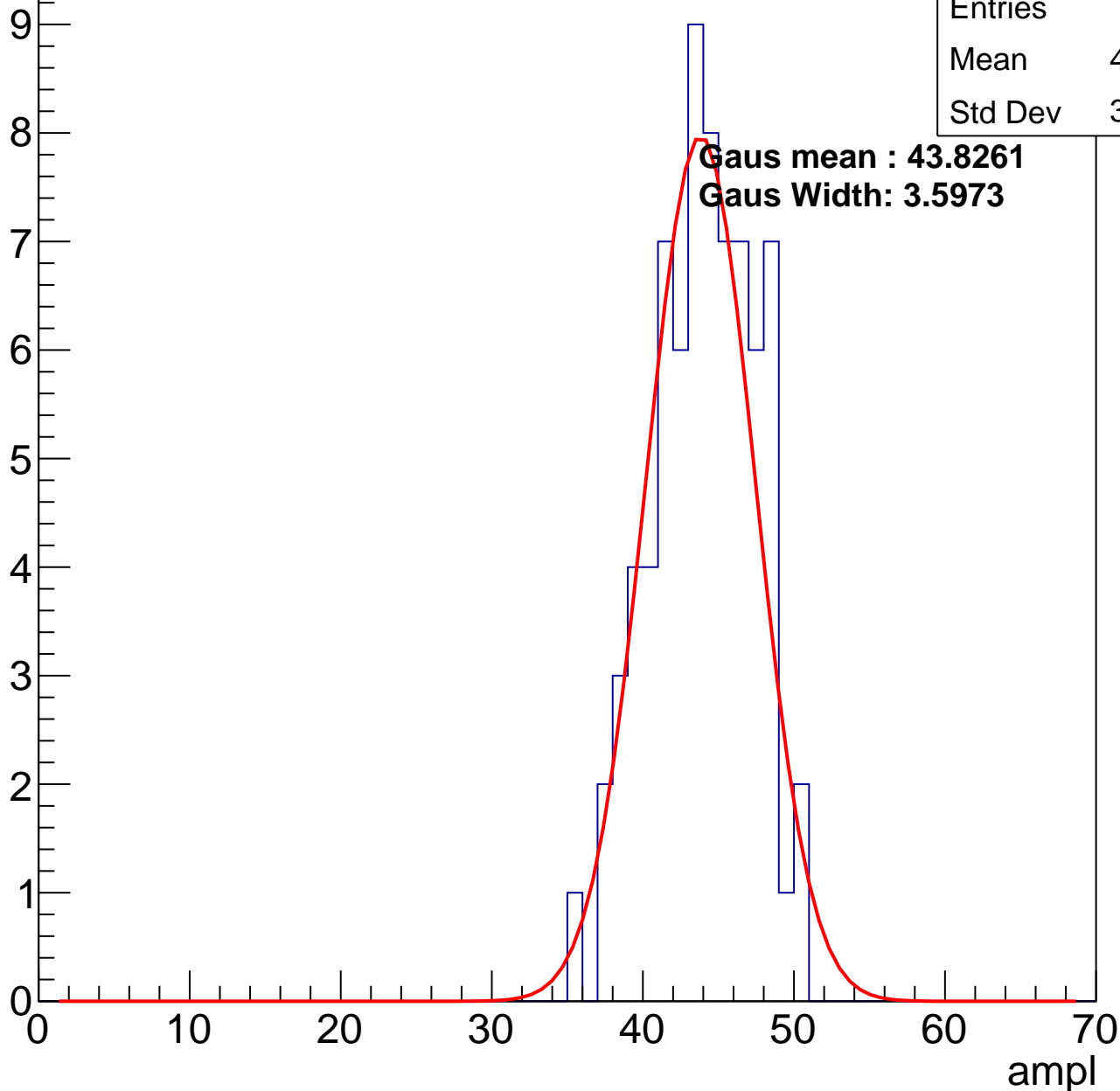
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.53
Std Dev	3.346

Gaus mean : 43.8261

Gaus Width: 3.5973

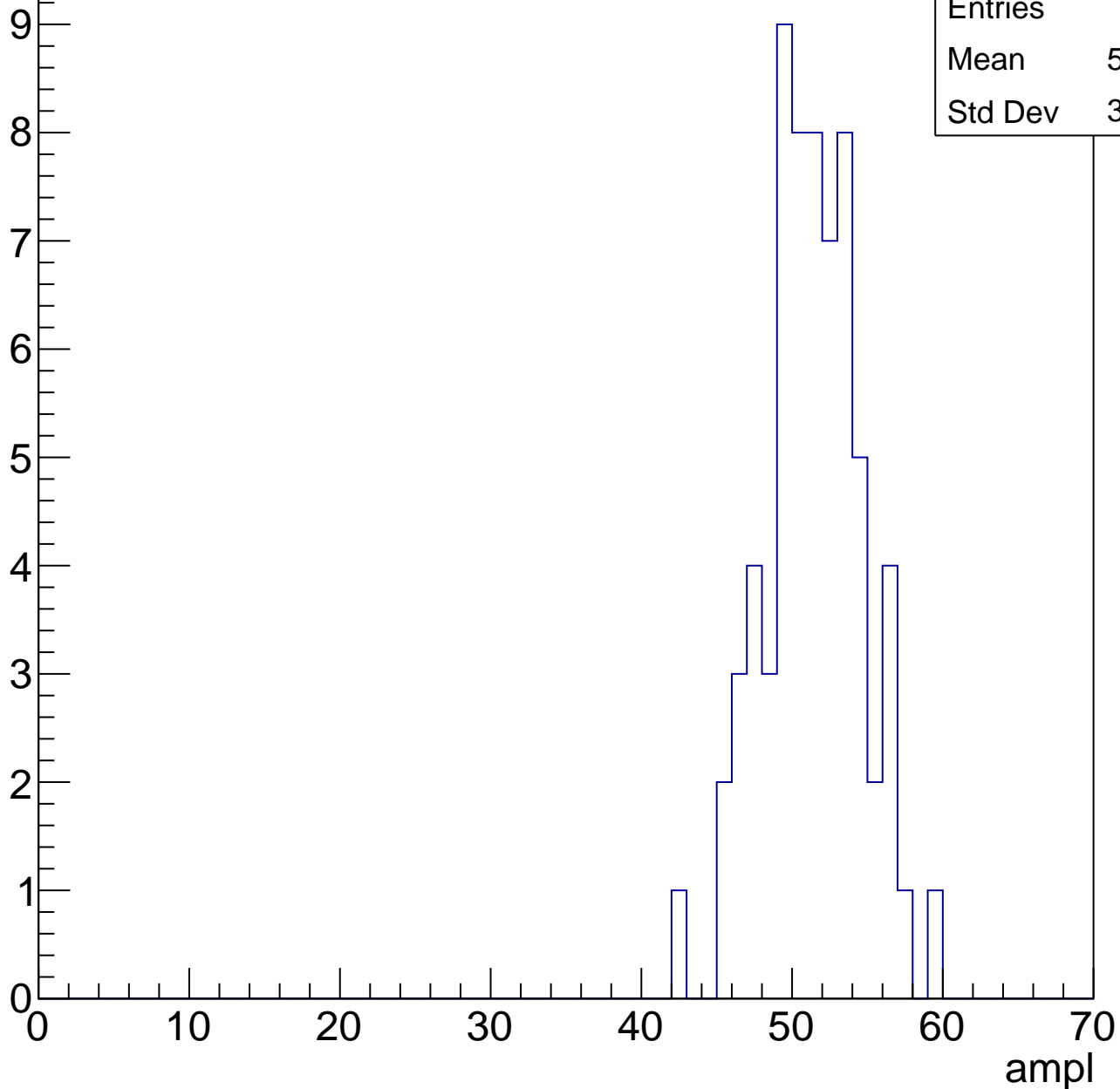


B0L000S, U4-ch5, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.89
Std Dev	3.206

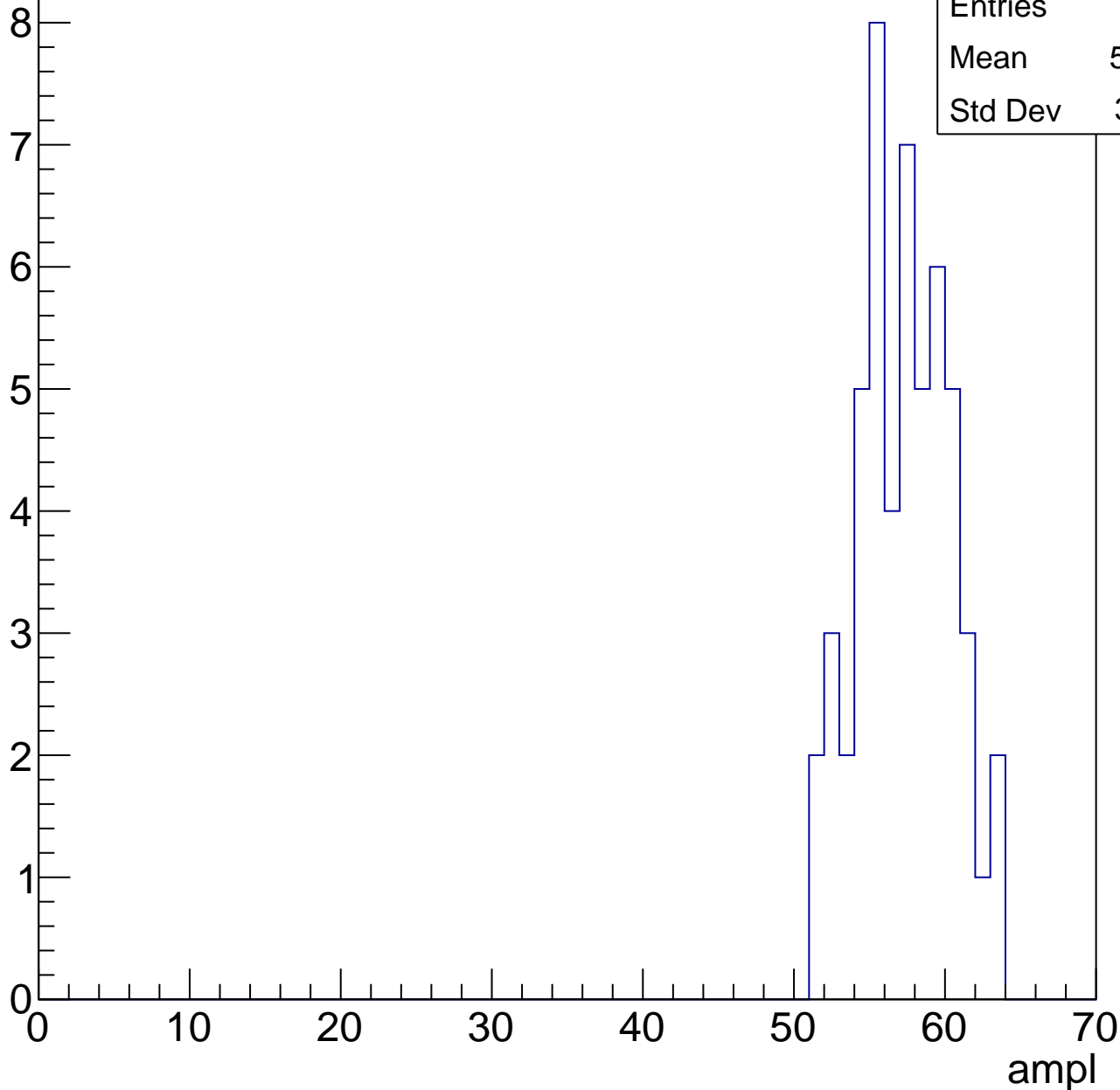


B0L000S, U4-ch5, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.83
Std Dev	3.001



B0L000S, U4-ch5, adc5

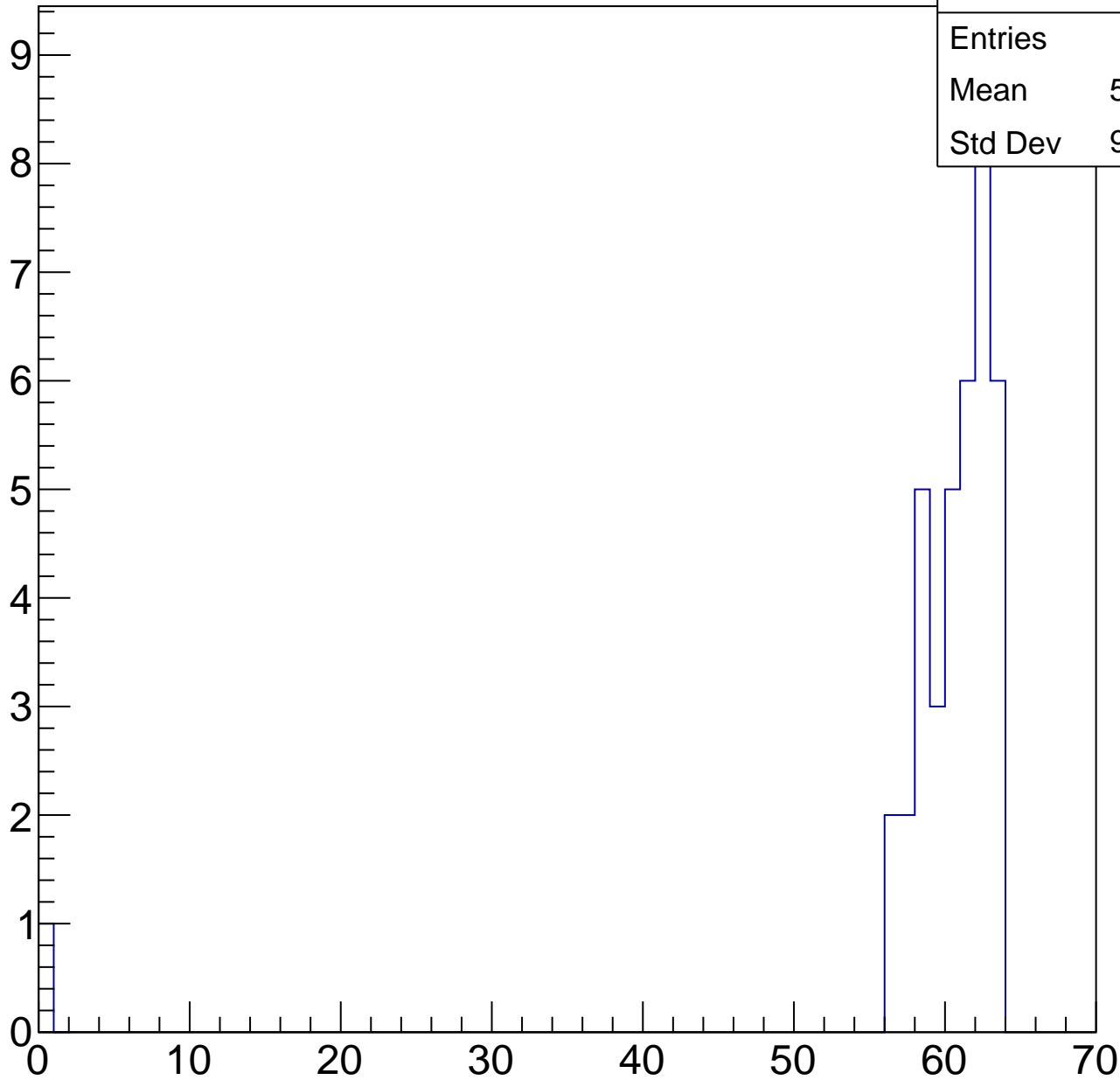
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	58.85
Std Dev	9.763

ampl



B0L000S, U4-ch5, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch5, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	20
Std Dev	0

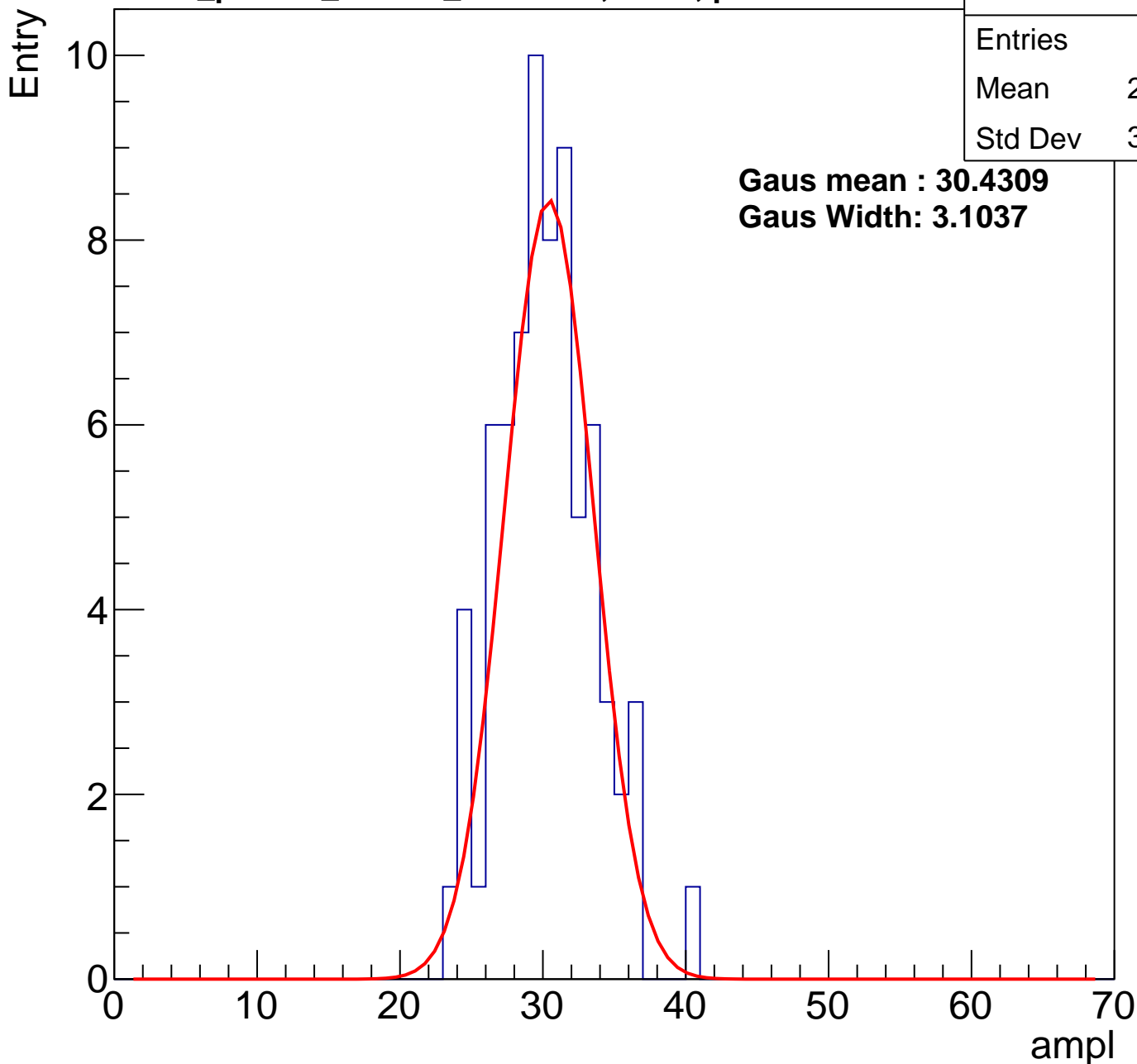
B0L000S, U4-ch6, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	72
Mean	29.79
Std Dev	3.316

Gaus mean : 30.4309

Gaus Width: 3.1037



B0L000S, U4-ch6, adc1

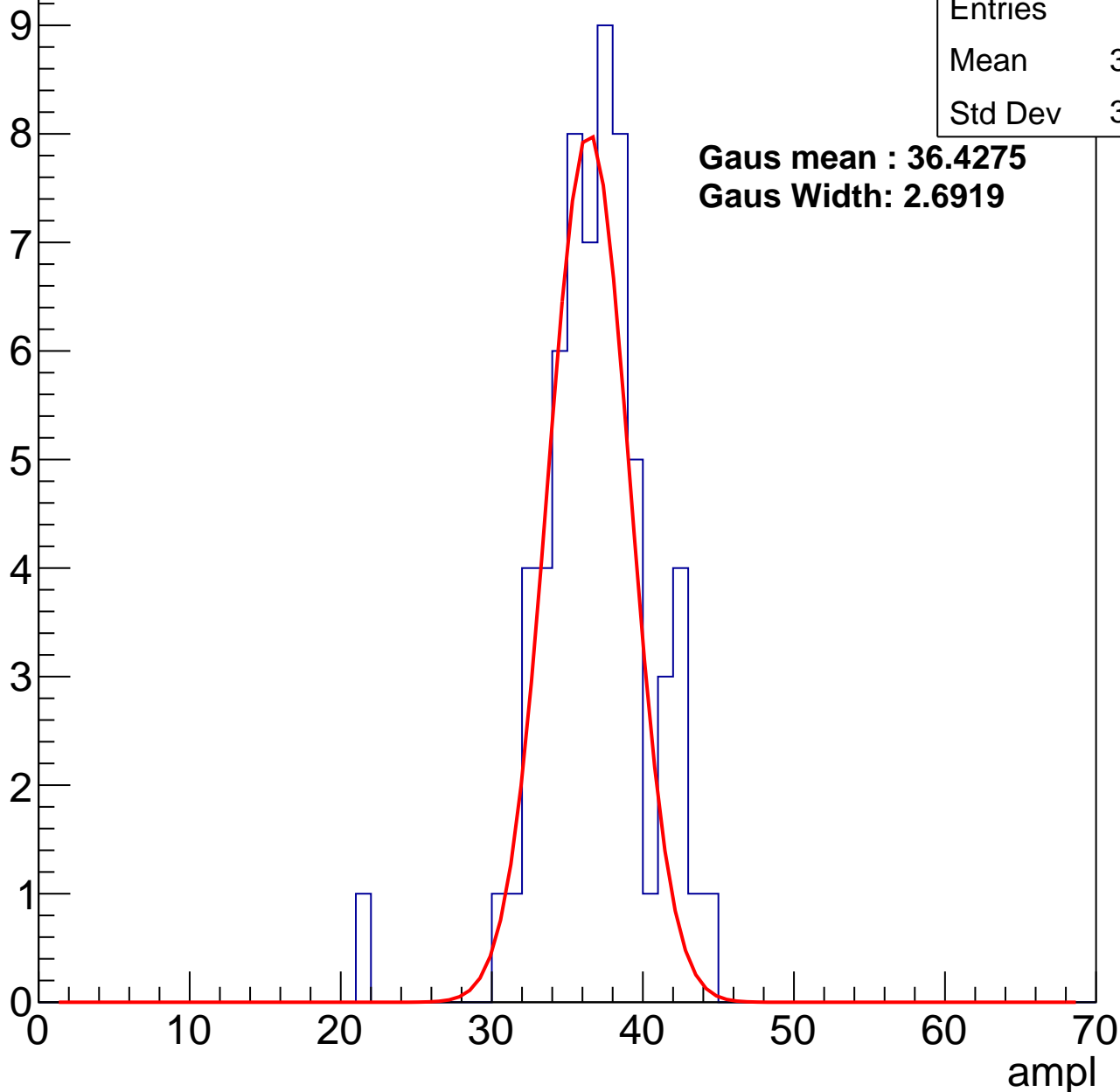
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.38
Std Dev	3.629

Gaus mean : 36.4275

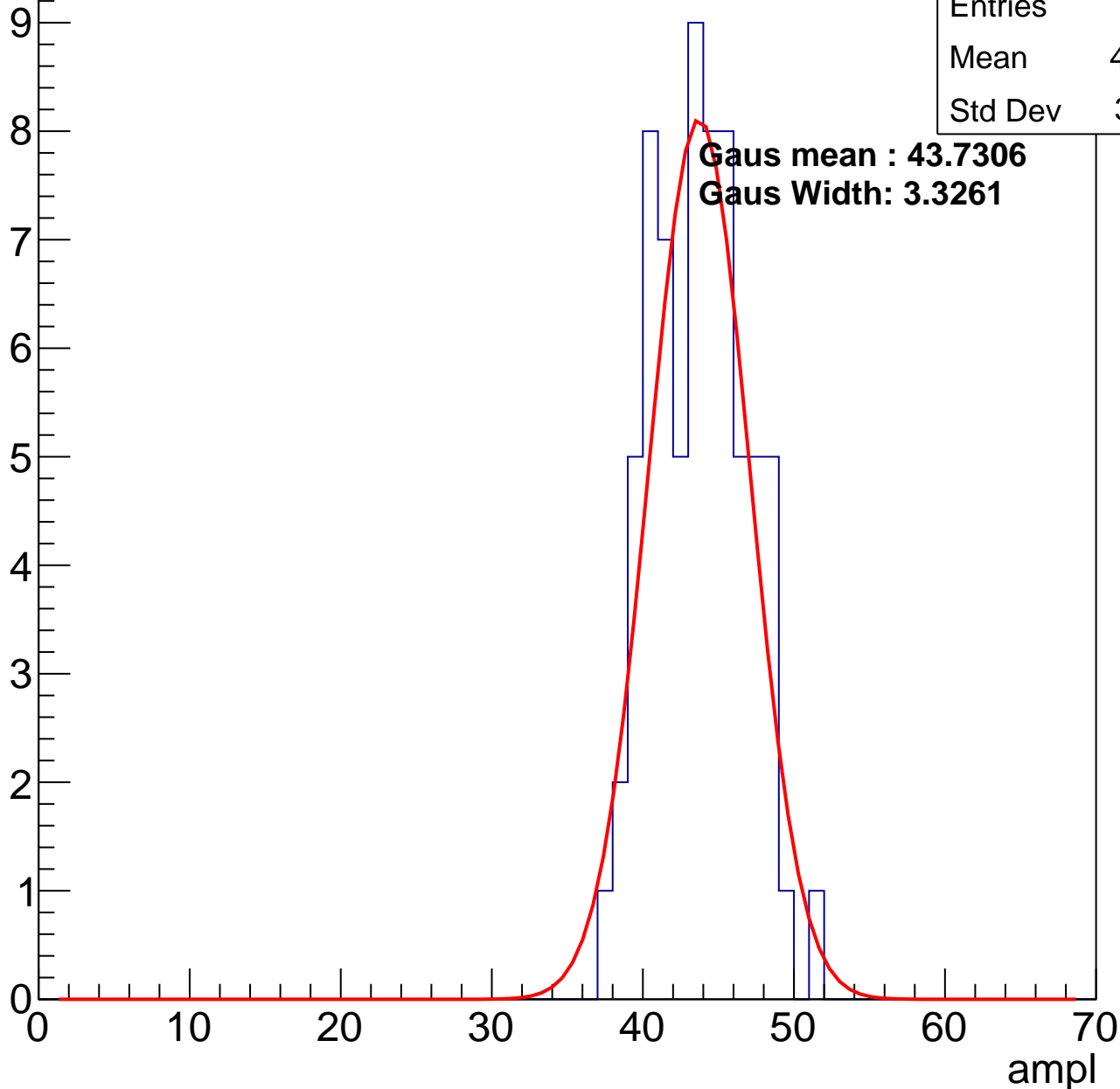
Gaus Width: 2.6919



B0L000S, U4-ch6, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch6, adc3

calib_packv5_042523_0143.root, FC#5, port B1

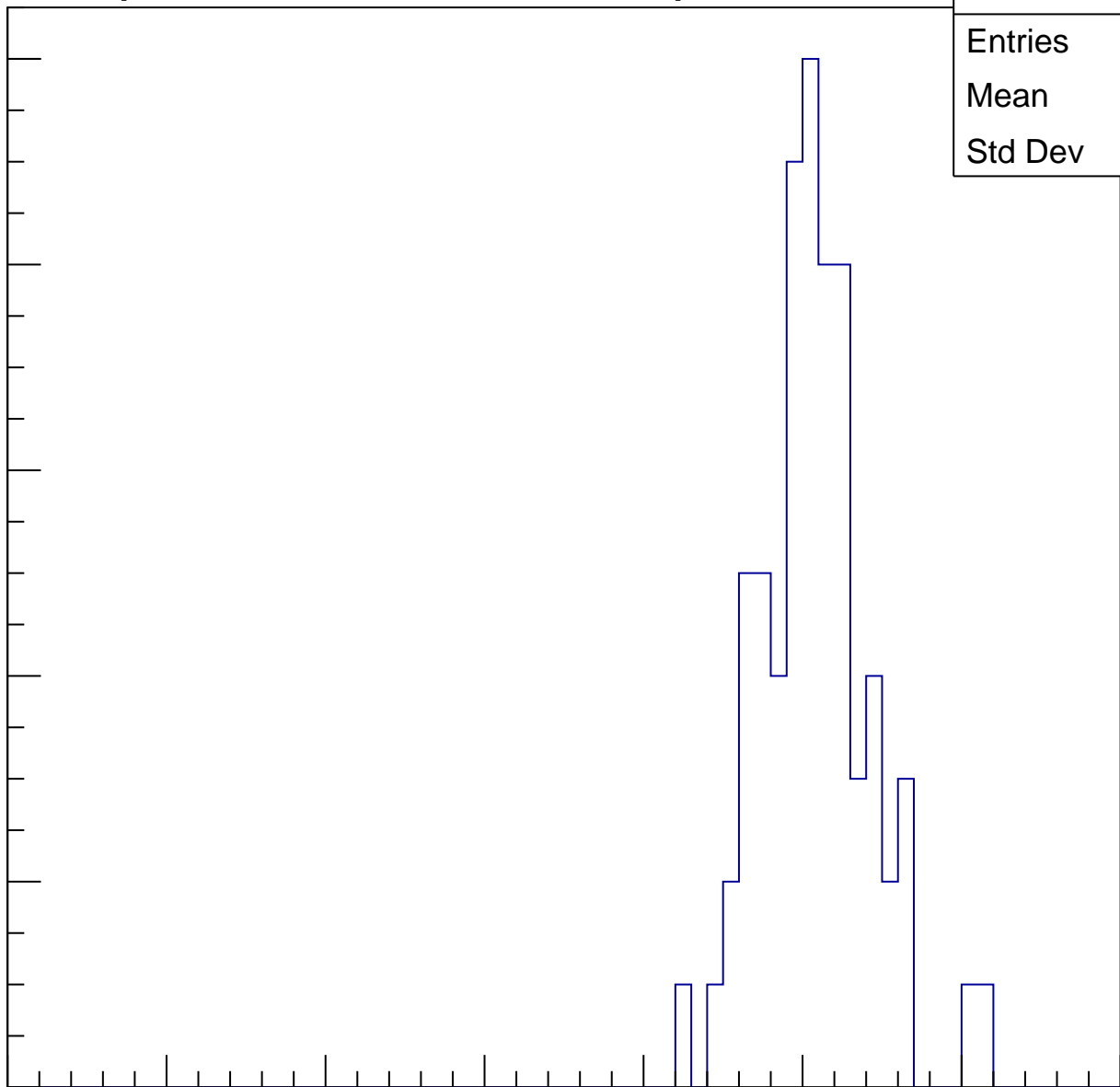
Entries	67
Mean	50.33
Std Dev	3.461

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

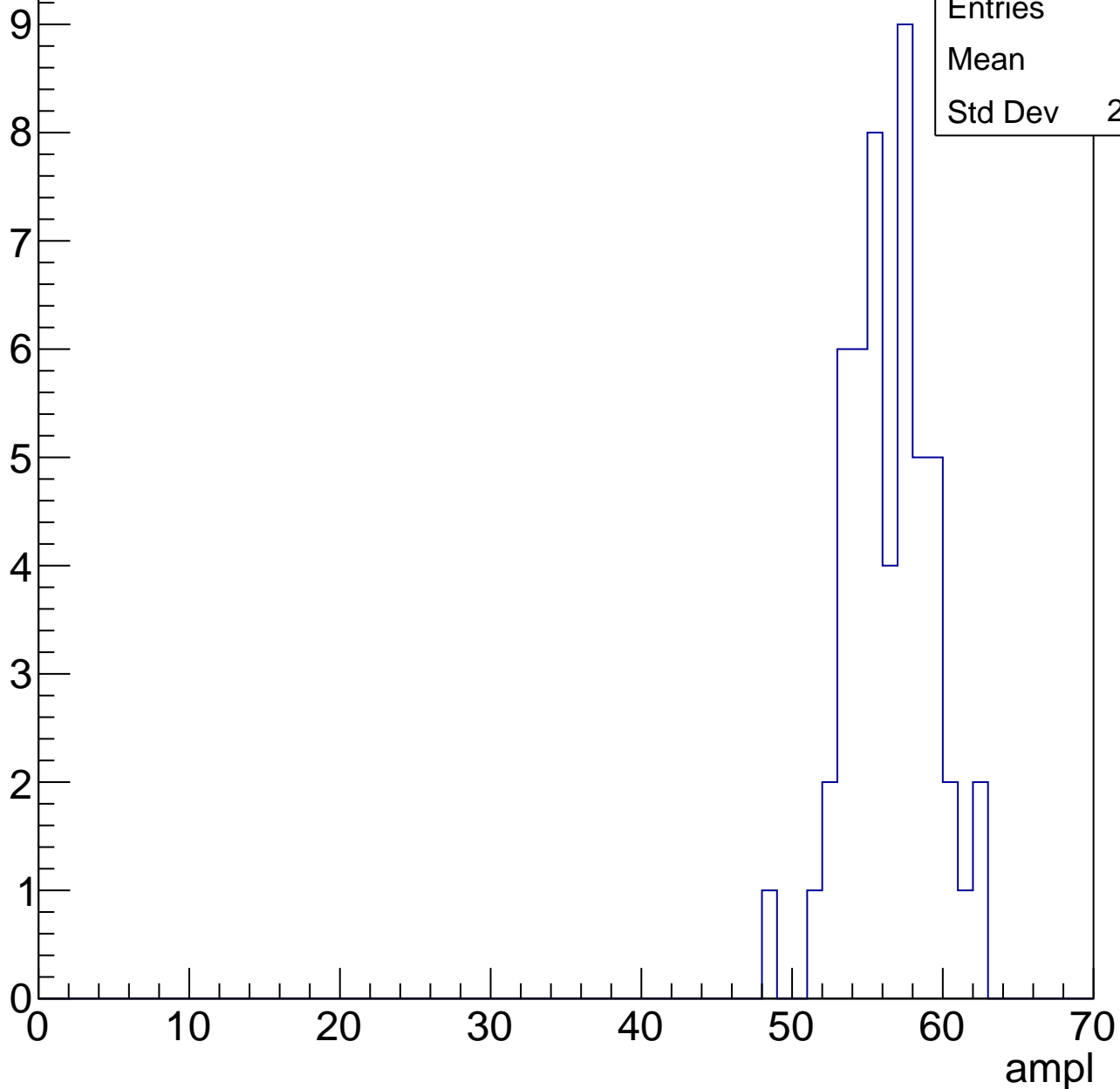


B0L000S, U4-ch6, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	56
Std Dev	2.808



B0L000S, U4-ch6, adc5

calib_packv5_042523_0143.root, FC#5, port B1

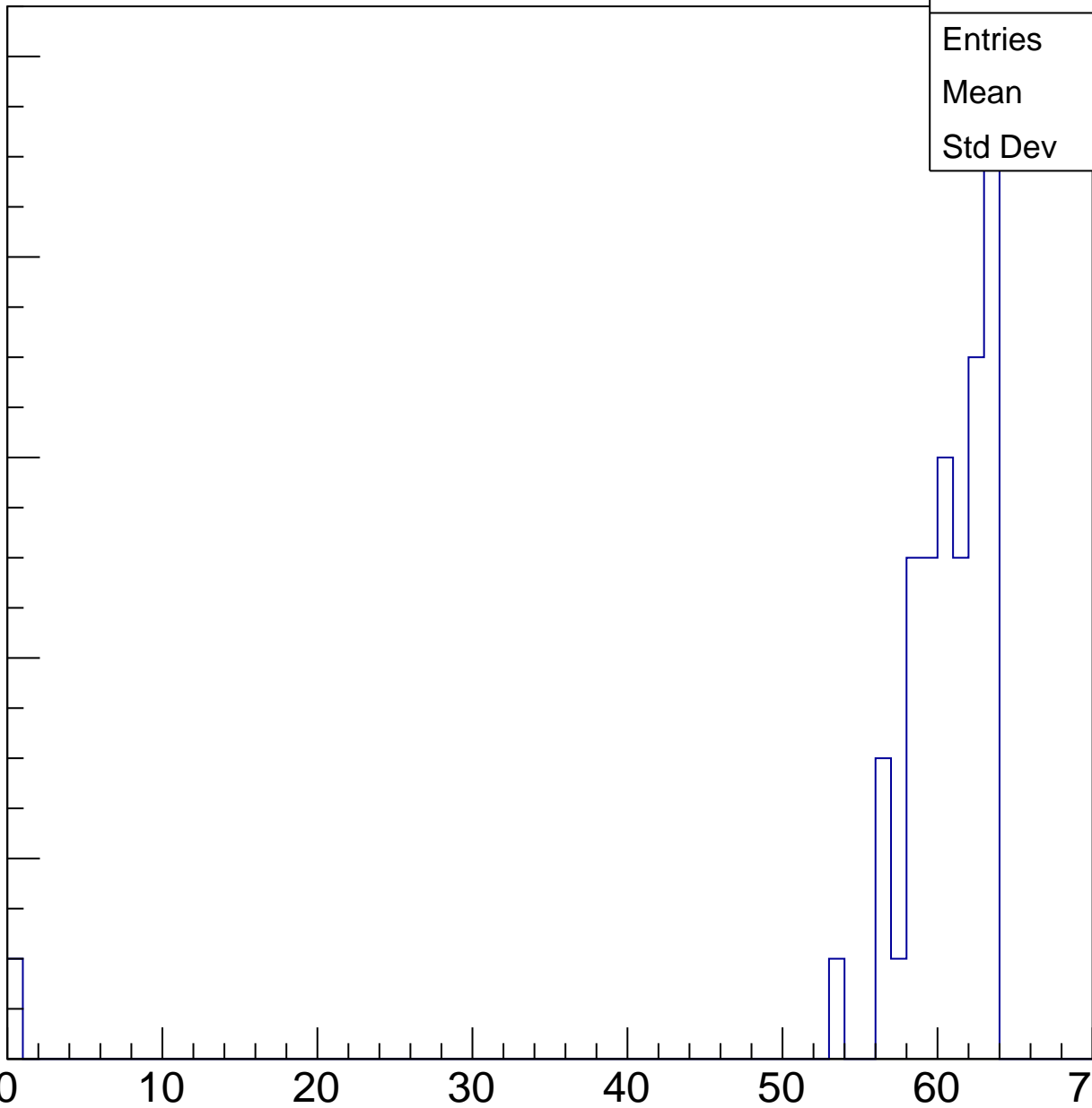
Entries	44
Mean	58.91
Std Dev	9.295

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

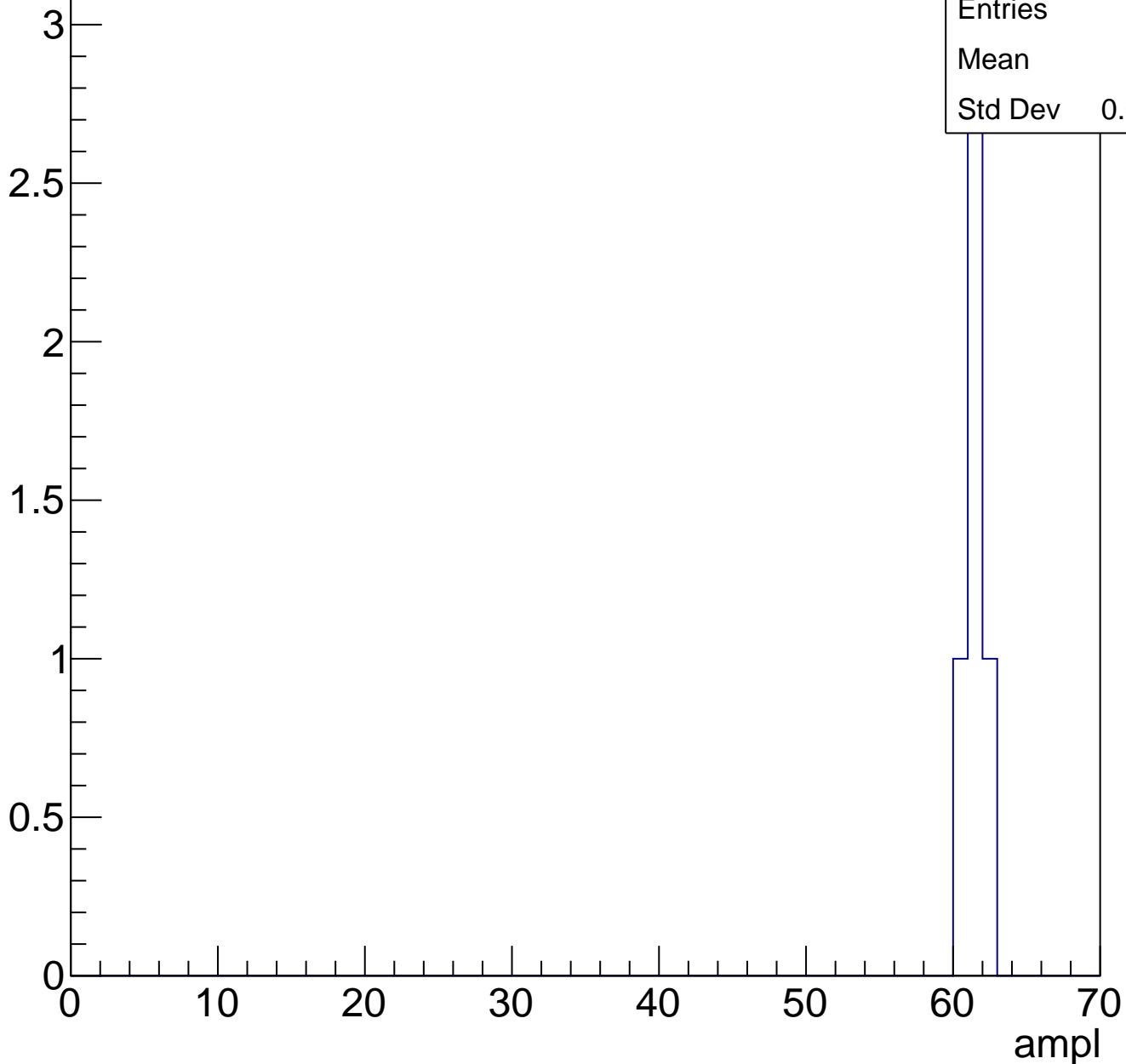
ampl



B0L000S, U4-ch6, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch6, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch7, adc0

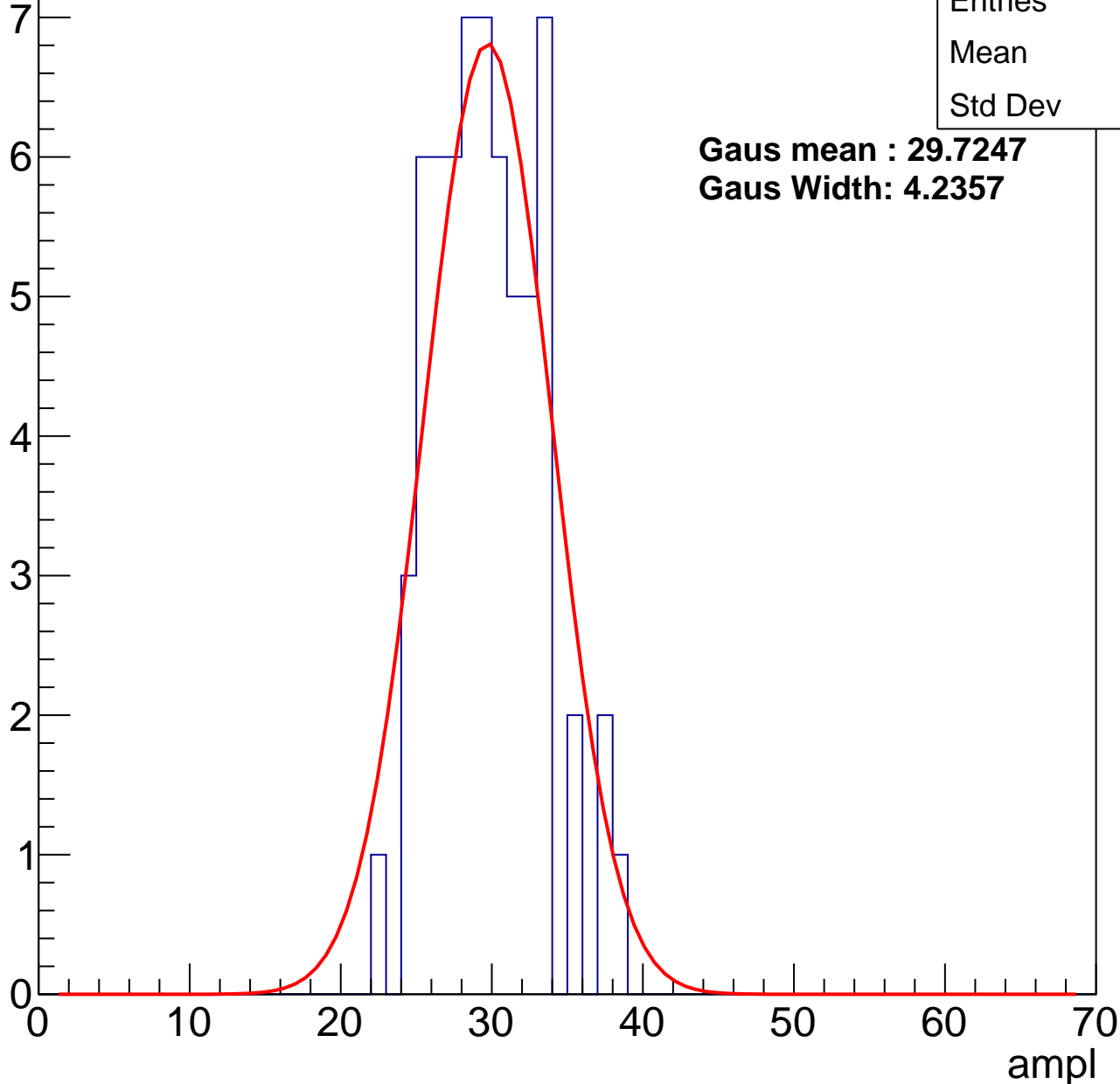
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.2
Std Dev	3.46

Gaus mean : 29.7247

Gaus Width: 4.2357



B0L000S, U4-ch7, adc1

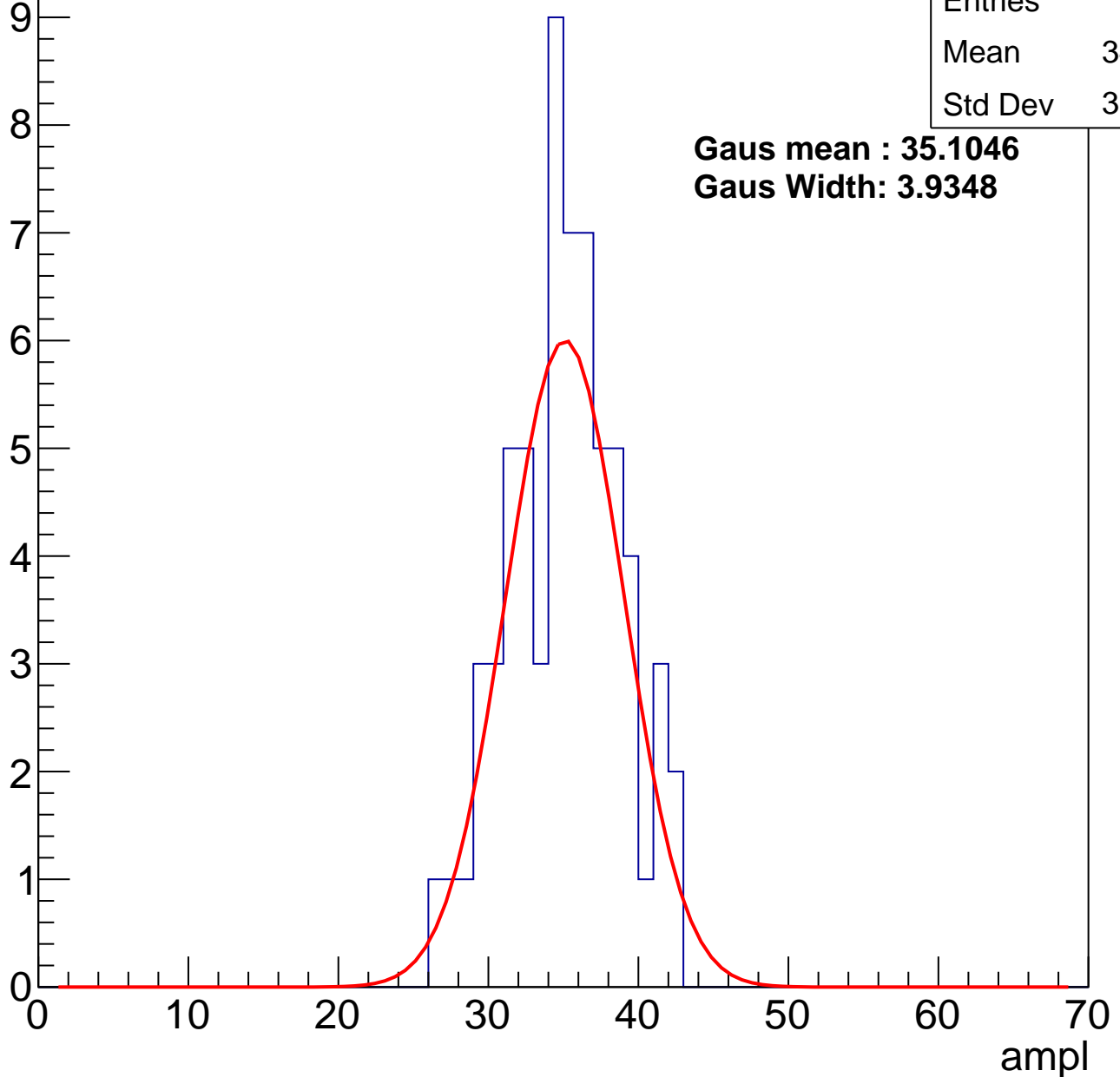
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	34.66
Std Dev	3.685

Gaus mean : 35.1046

Gaus Width: 3.9348



B0L000S, U4-ch7, adc2

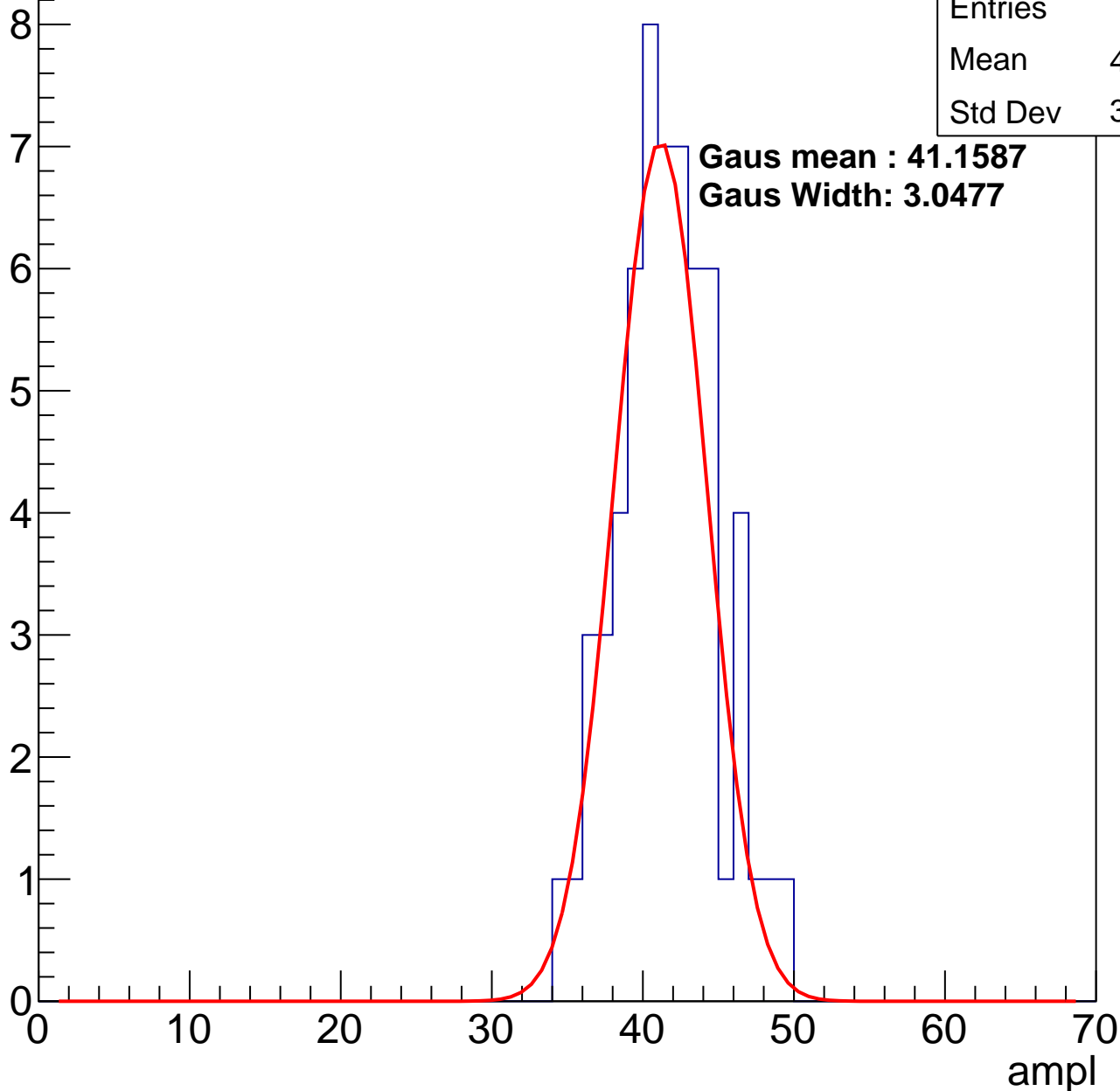
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.17
Std Dev	3.226

Gaus mean : 41.1587

Gaus Width: 3.0477

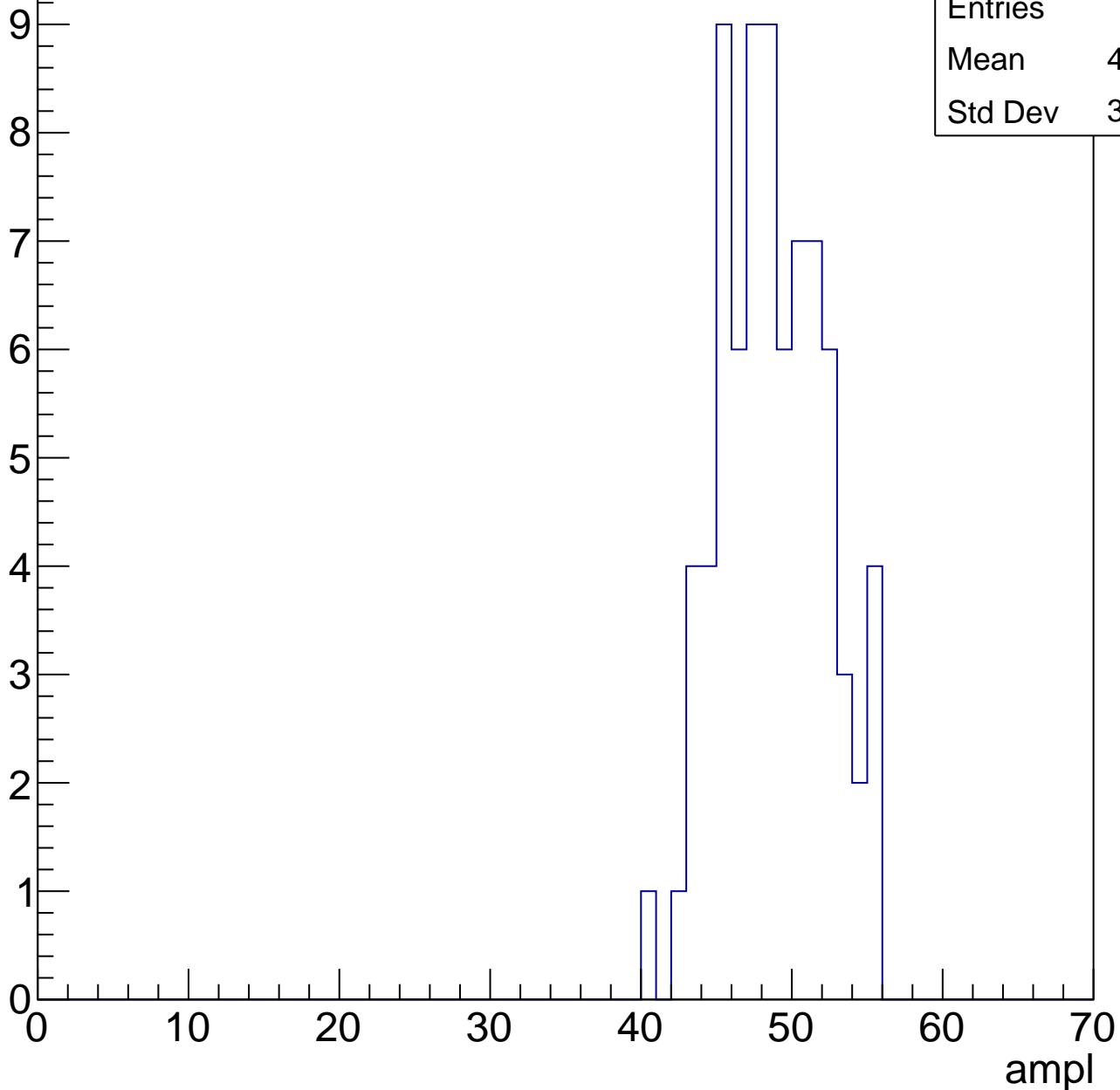


B0L000S, U4-ch7, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	48.28
Std Dev	3.412

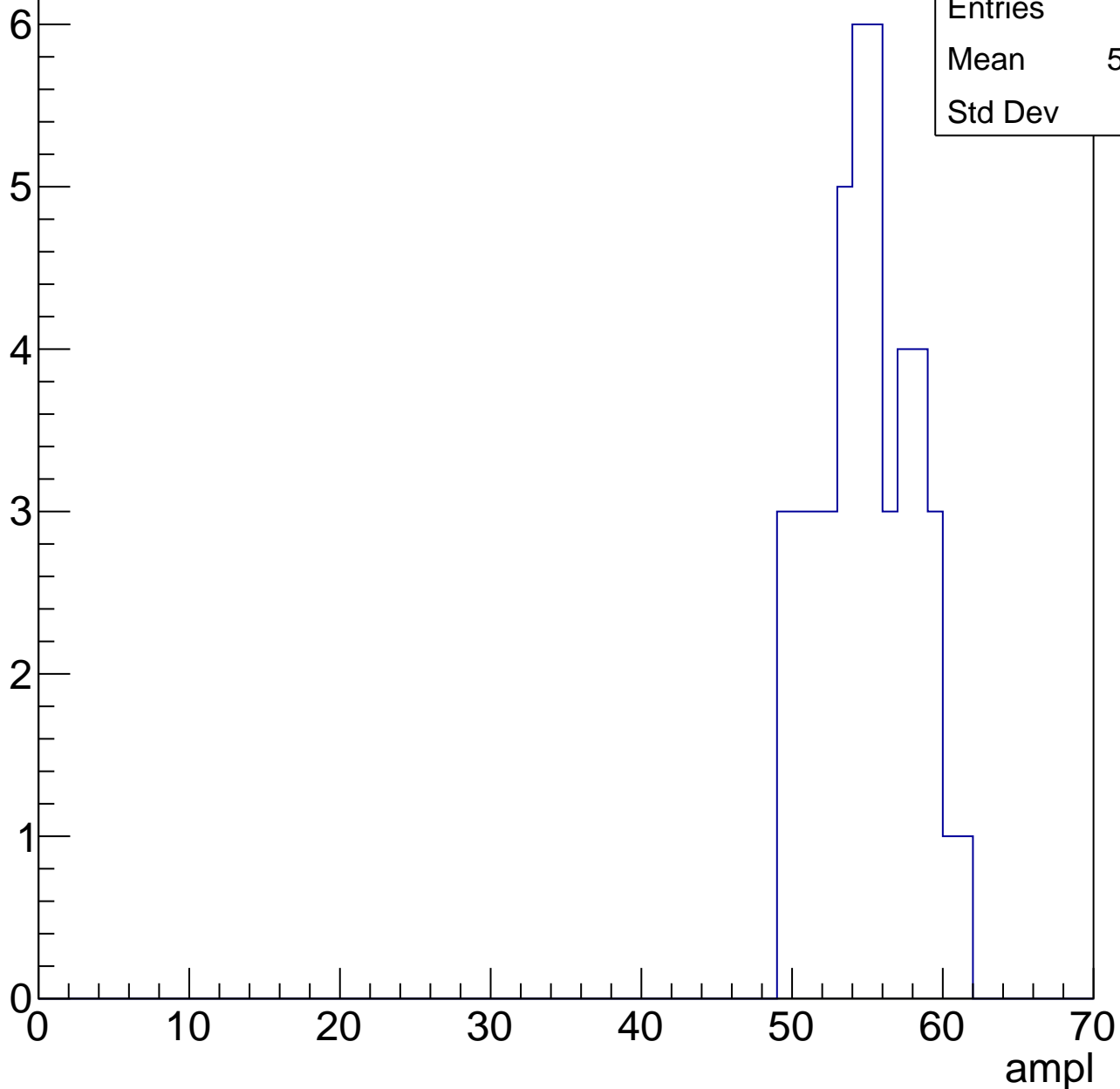


B0L000S, U4-ch7, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	45
Mean	54.47
Std Dev	3.11



B0L000S, U4-ch7, adc5

calib_packv5_042523_0143.root, FC#5, port B1

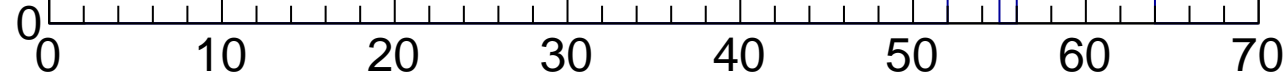
Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	59.08
Std Dev	2.761

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch7, adc6

calib_packv5_042523_0143.root, FC#5, port B1

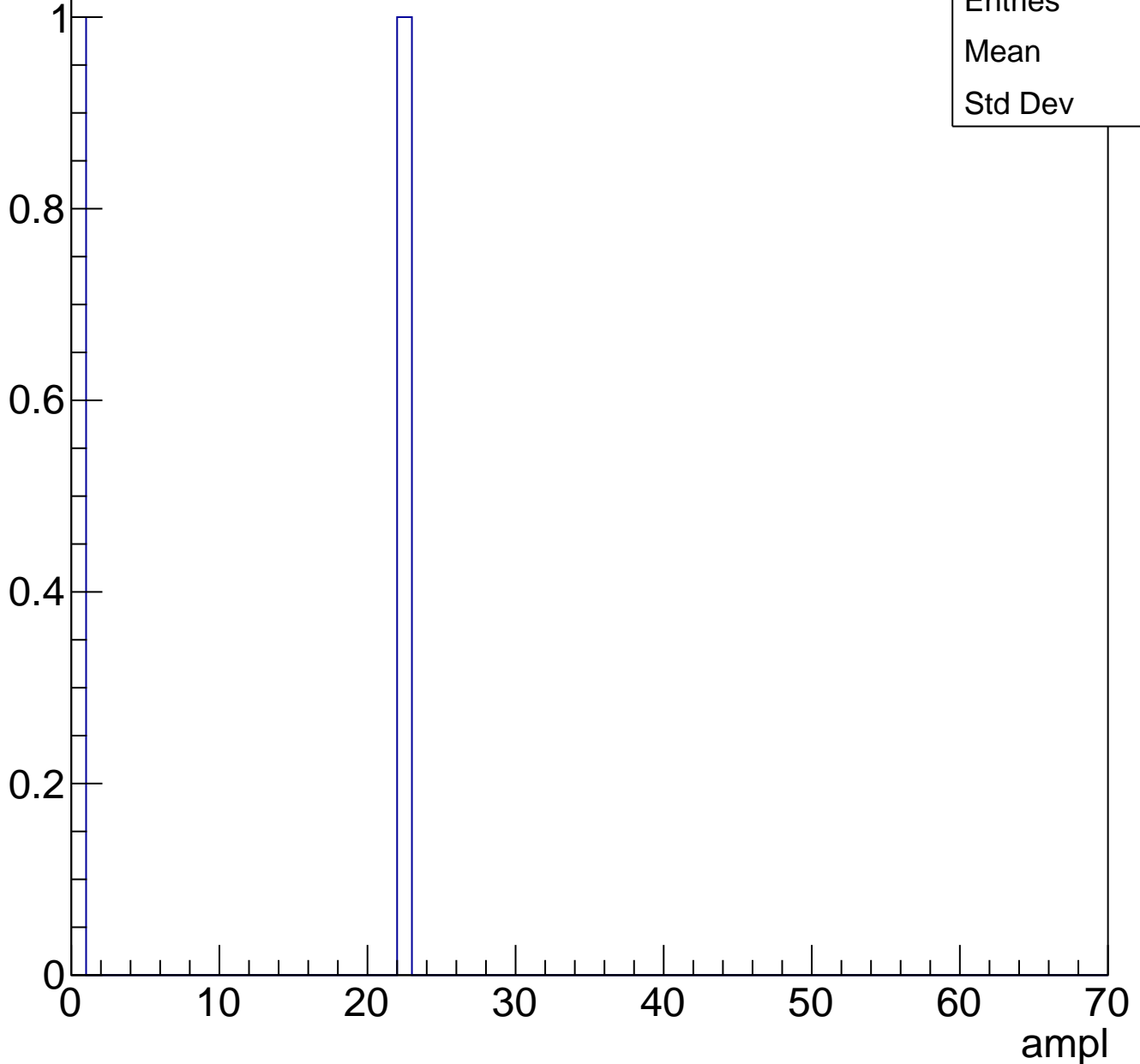
Entry



B0L000S, U4-ch7, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

B0L000S, U4-ch8, adc0

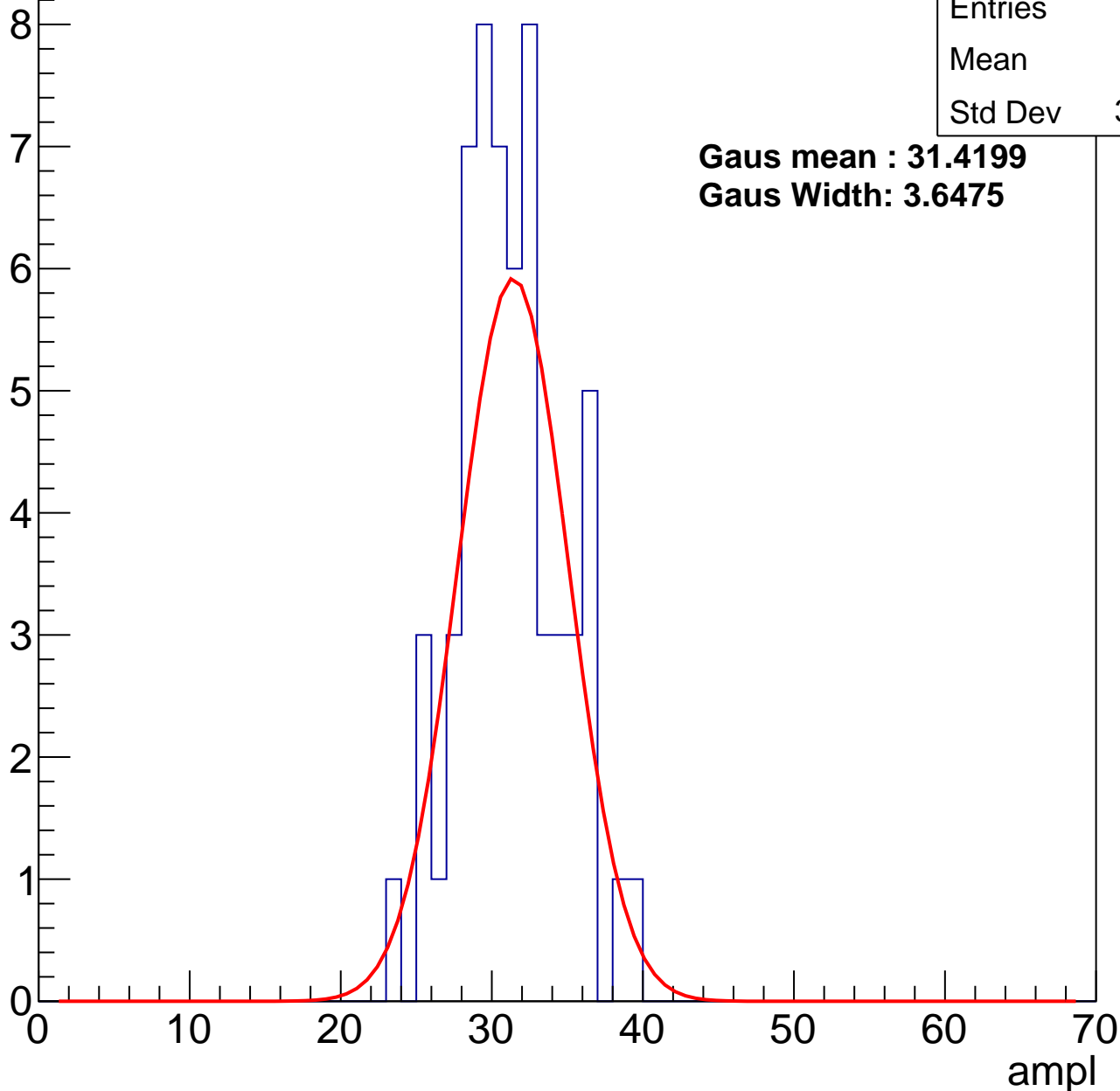
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.8
Std Dev	3.361

Gaus mean : 31.4199

Gaus Width: 3.6475

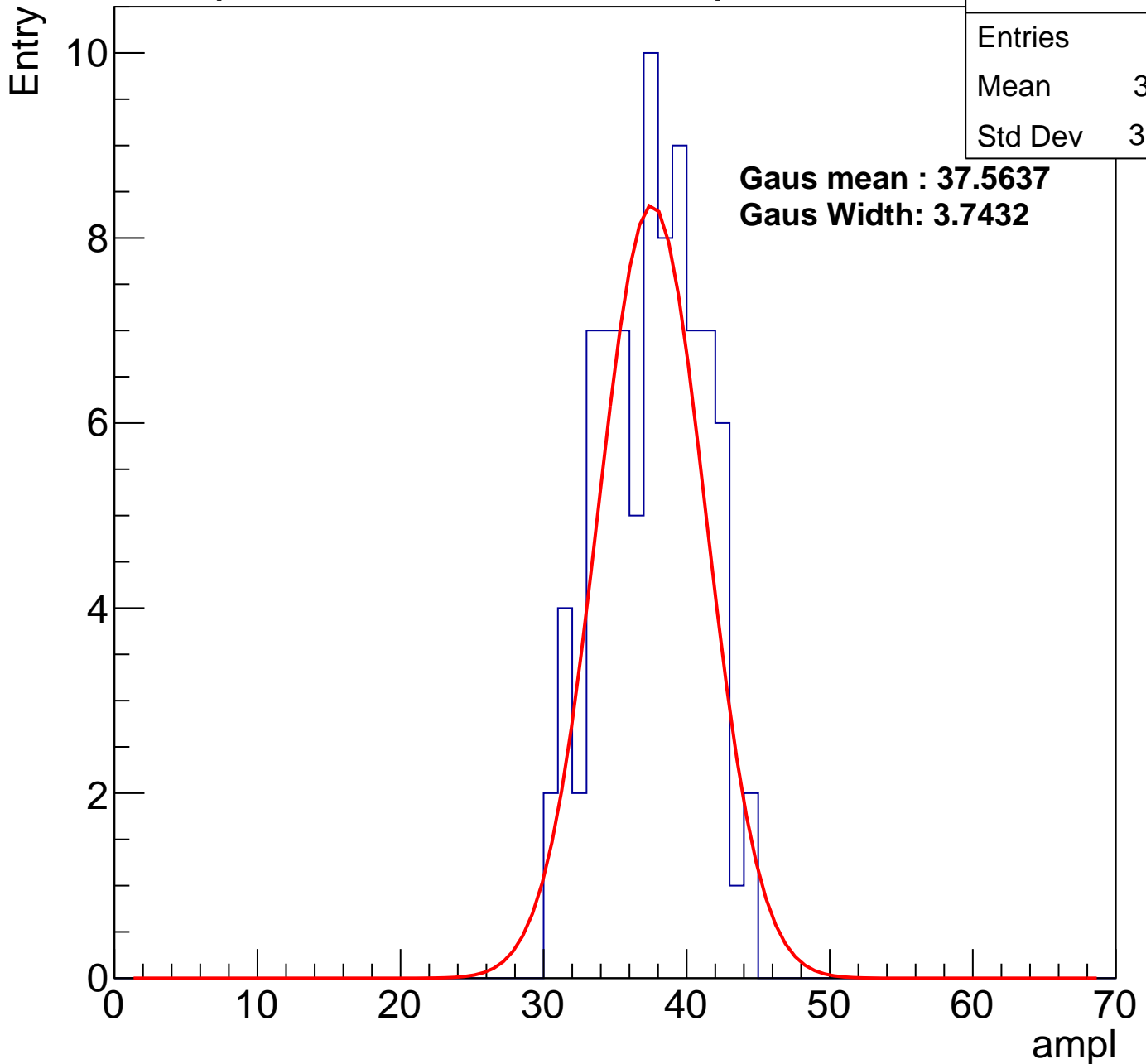


B0L000S, U4-ch8, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	84
Mean	37.11
Std Dev	3.454

Gaus mean : 37.5637
Gaus Width: 3.7432



B0L000S, U4-ch8, adc2

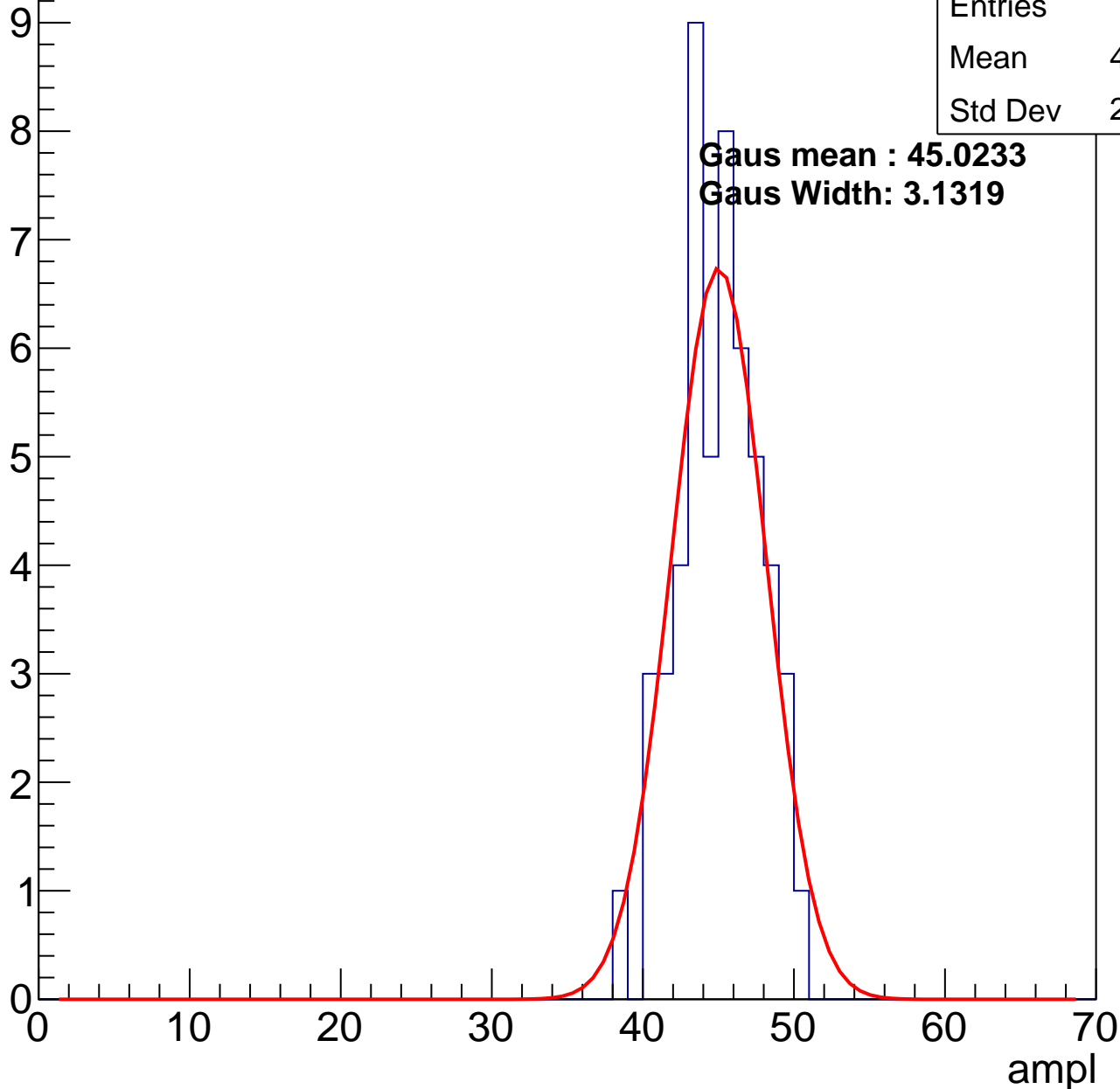
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.54
Std Dev	2.678

Gaus mean : 45.0233

Gaus Width: 3.1319

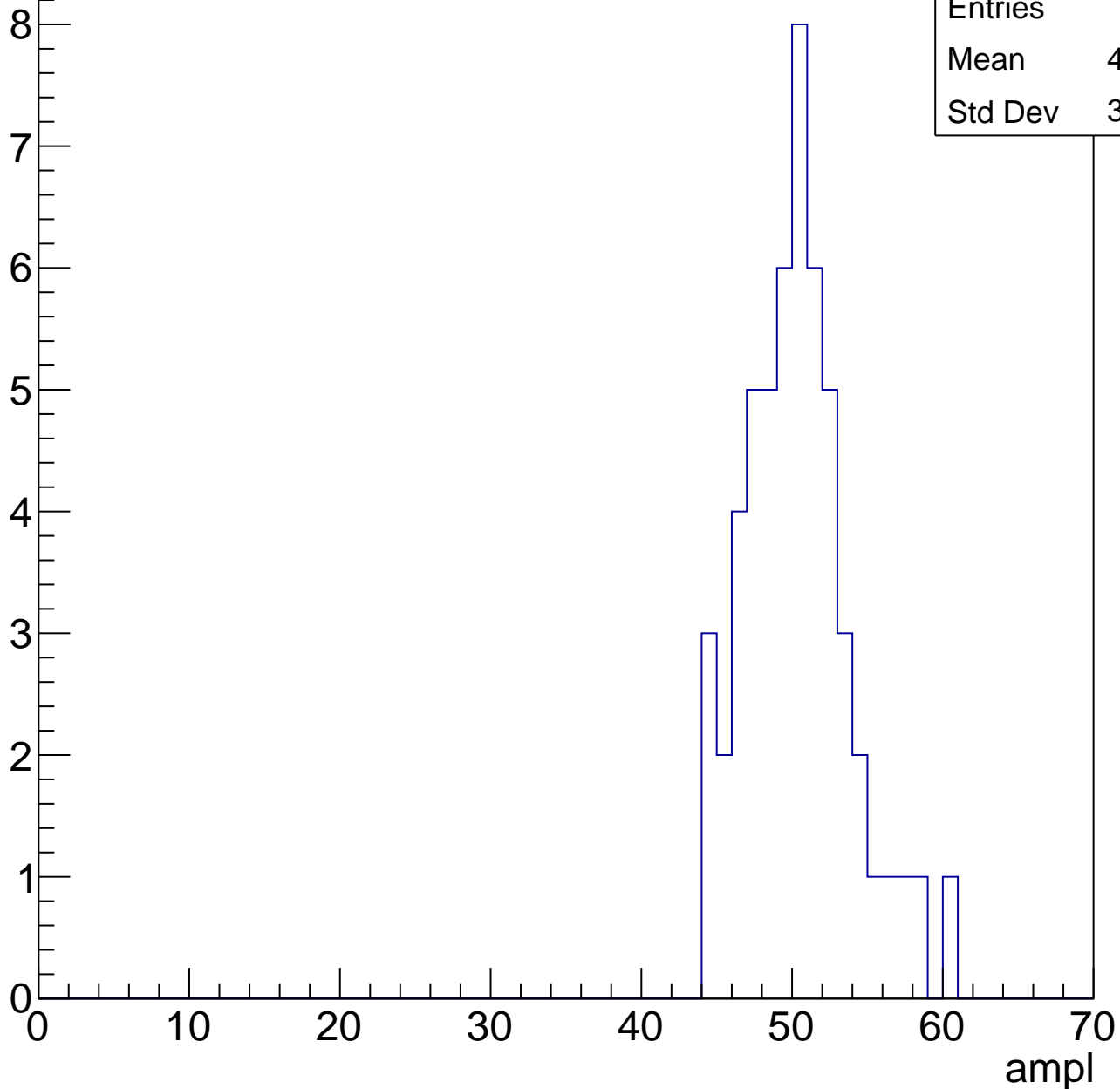


B0L000S, U4-ch8, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

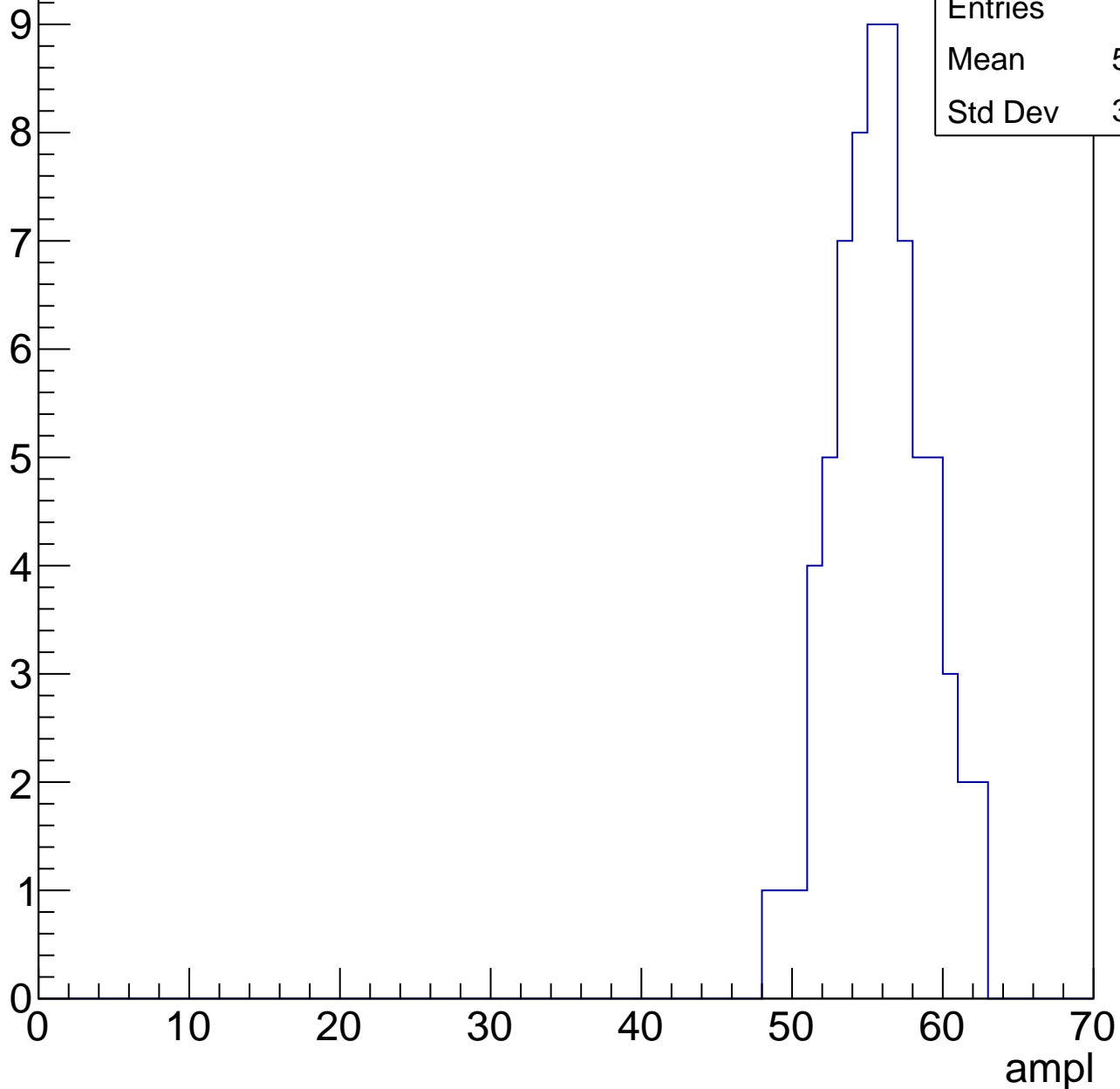
Entries	54
Mean	49.89
Std Dev	3.473



B0L000S, U4-ch8, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	69
Mean	55.41
Std Dev	3.071

B0L000S, U4-ch8, adc5

calib_packv5_042523_0143.root, FC#5, port B1

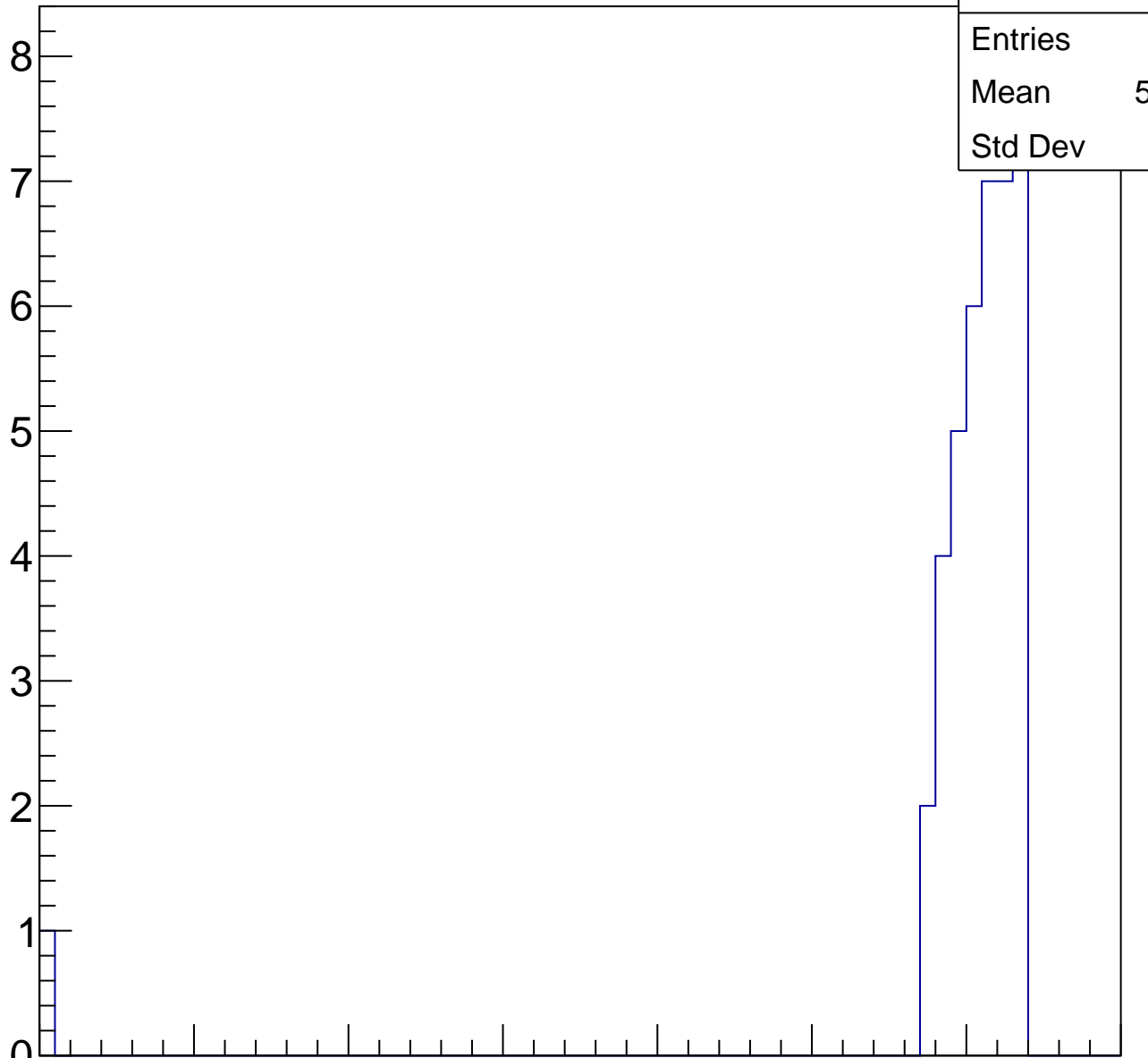
Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.15
Std Dev	9.64

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch8, adc6

calib_packv5_042523_0143.root, FC#5, port B1

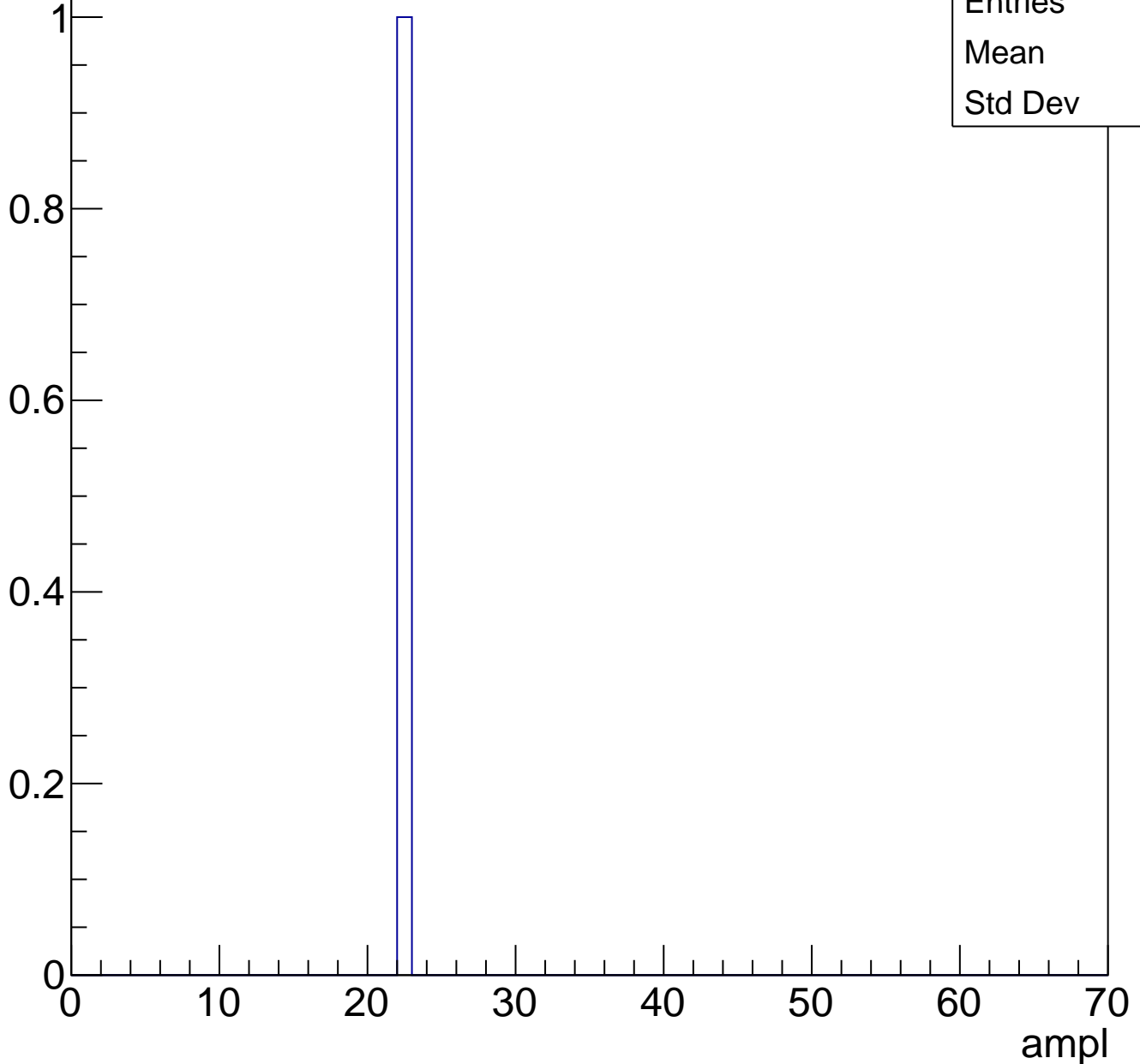
Entry



B0L000S, U4-ch8, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch9, adc0

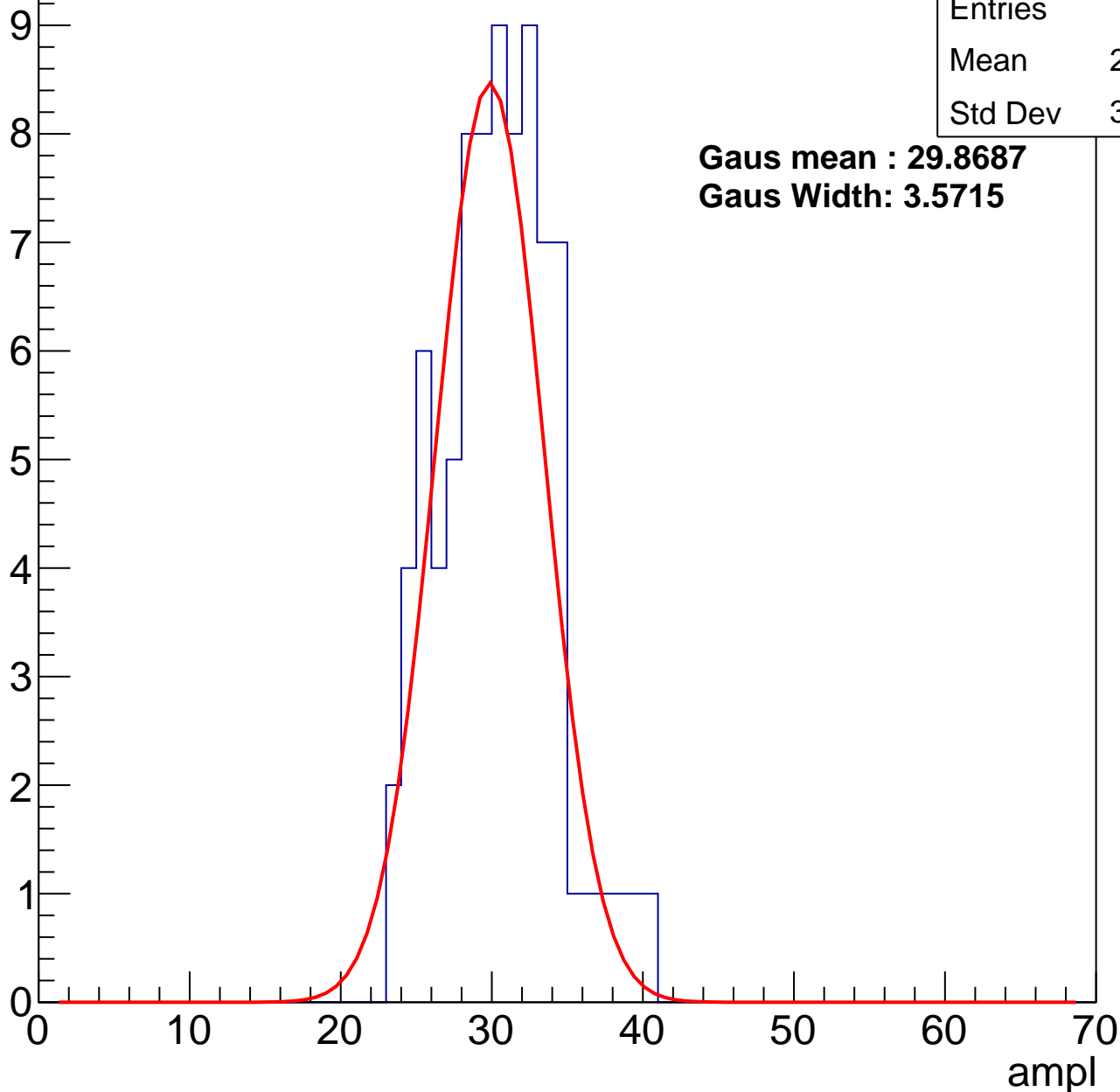
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	83
Mean	29.96
Std Dev	3.665

Gaus mean : 29.8687

Gaus Width: 3.5715



B0L000S, U4-ch9, adc1

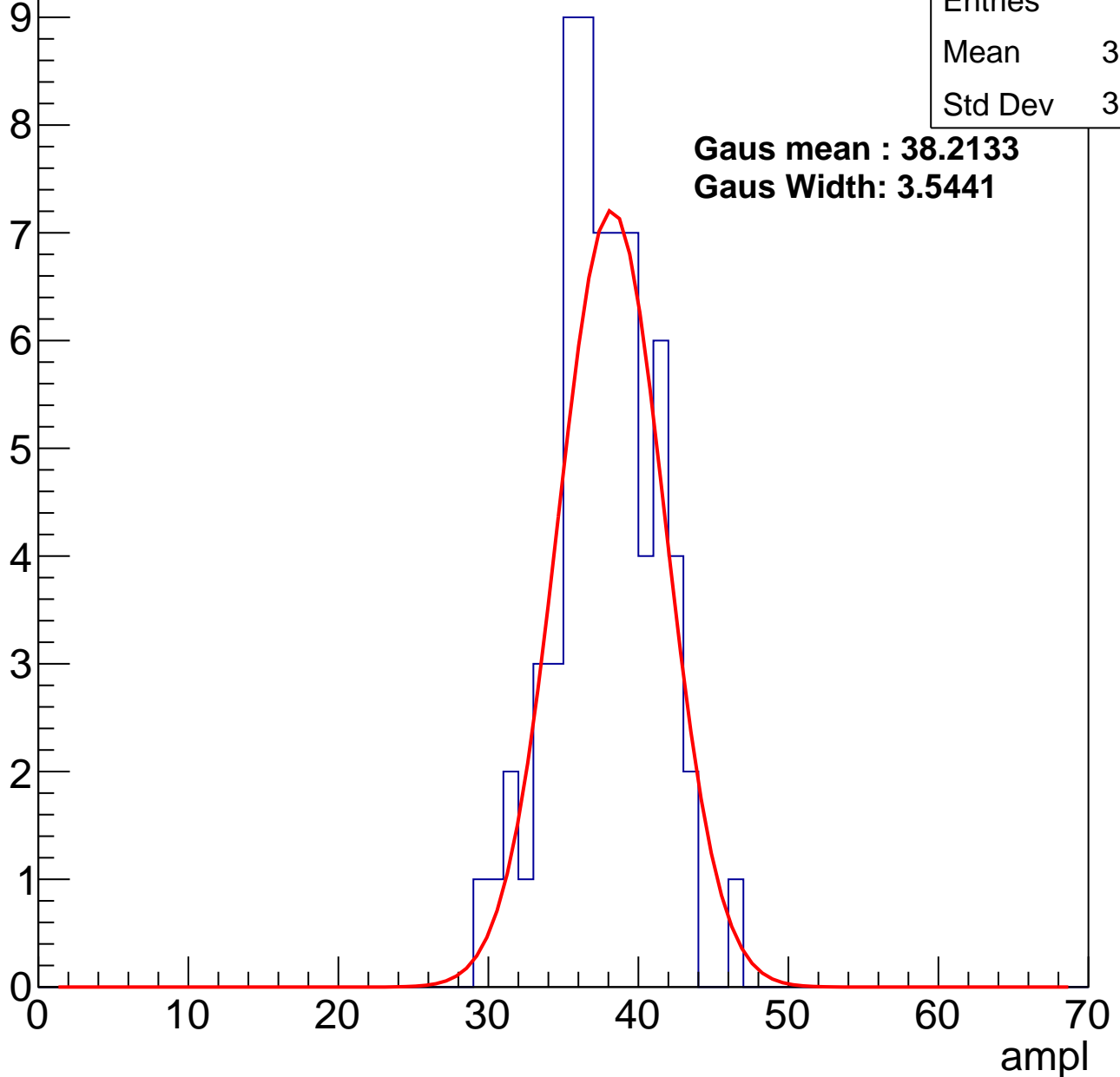
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.27
Std Dev	3.335

Gaus mean : 38.2133

Gaus Width: 3.5441



B0L000S, U4-ch9, adc2

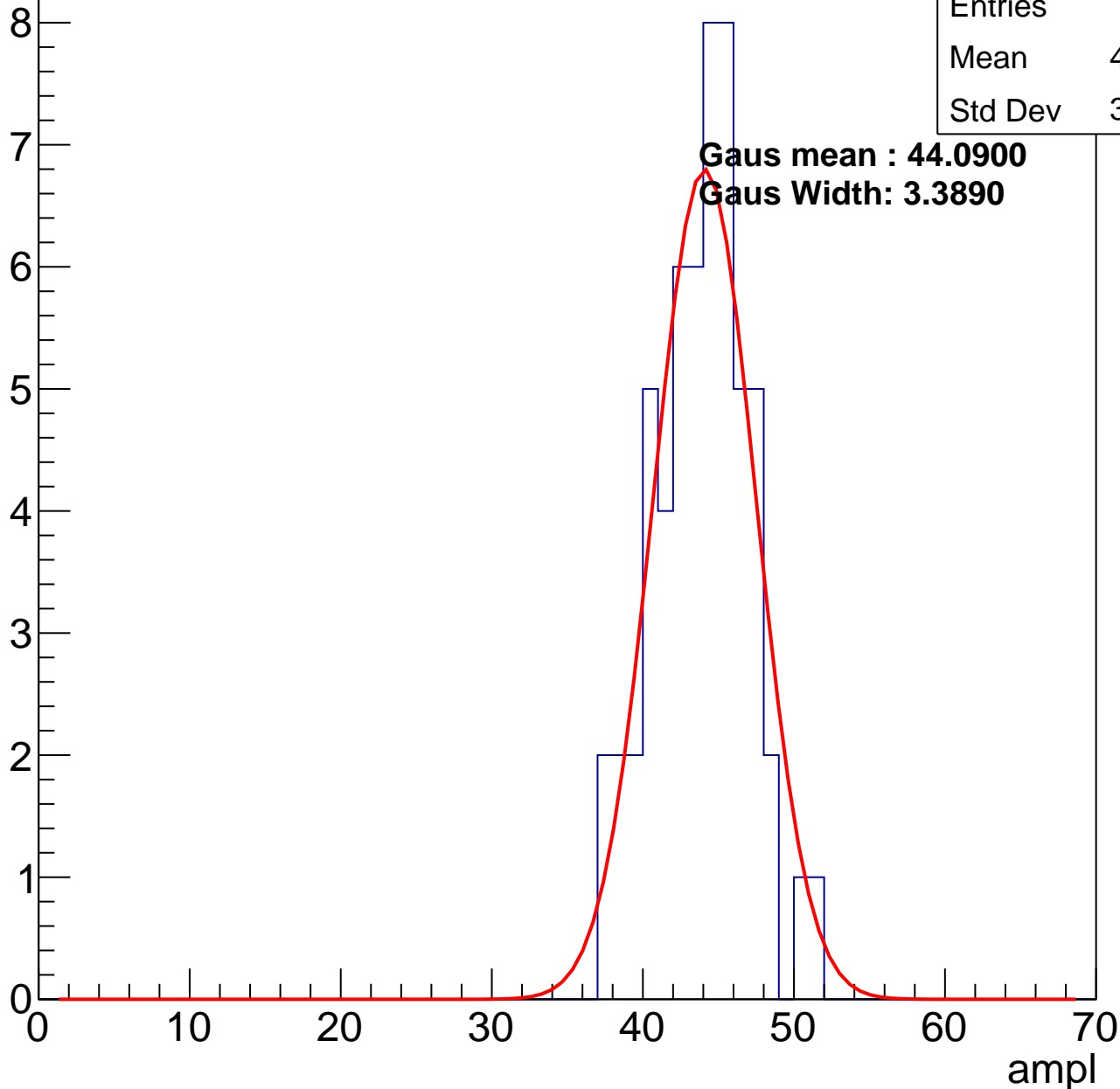
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.44
Std Dev	3.072

Gaus mean : 44.0900

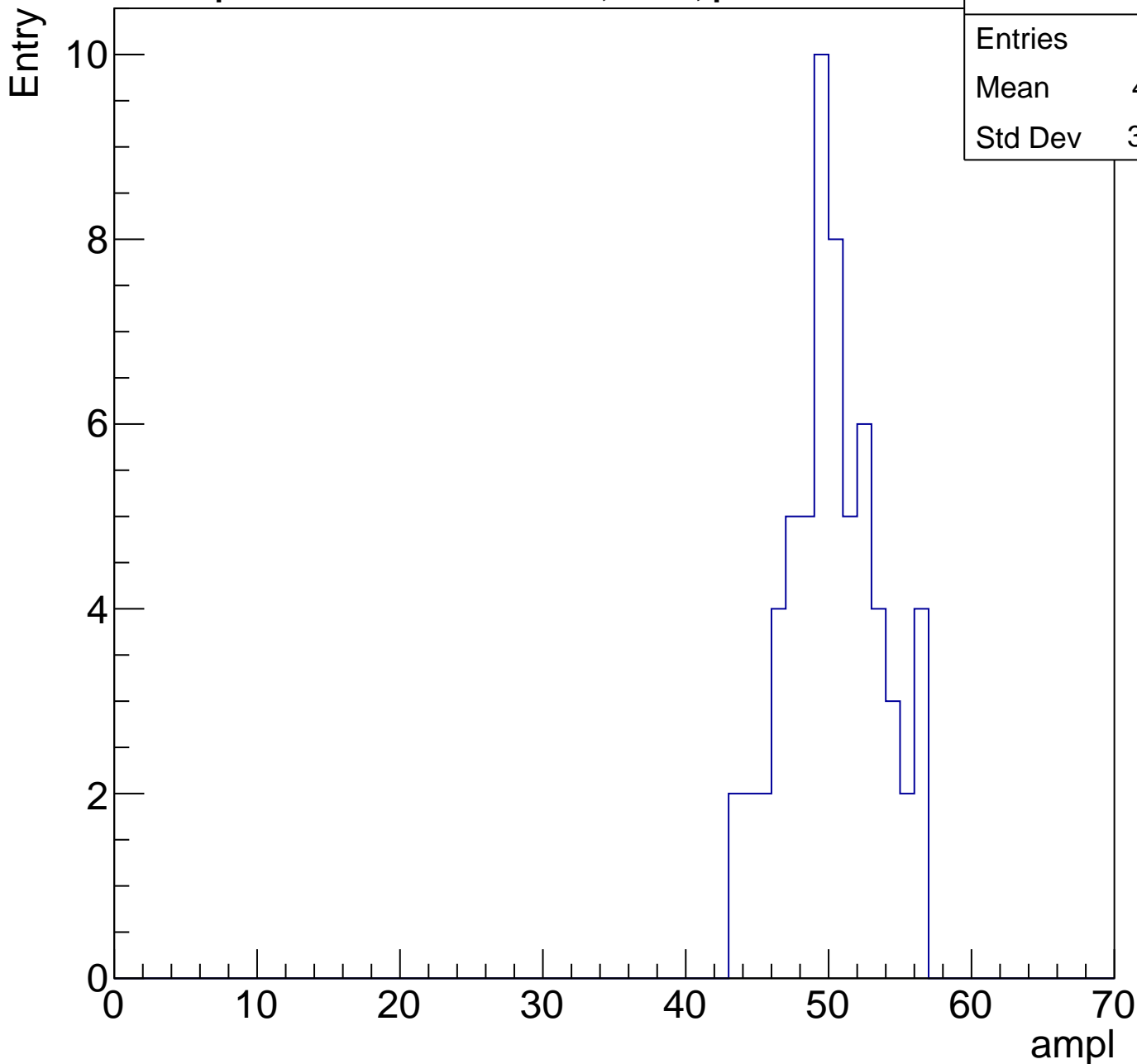
Gaus Width: 3.3890



B0L000S, U4-ch9, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	62
Mean	49.81
Std Dev	3.272

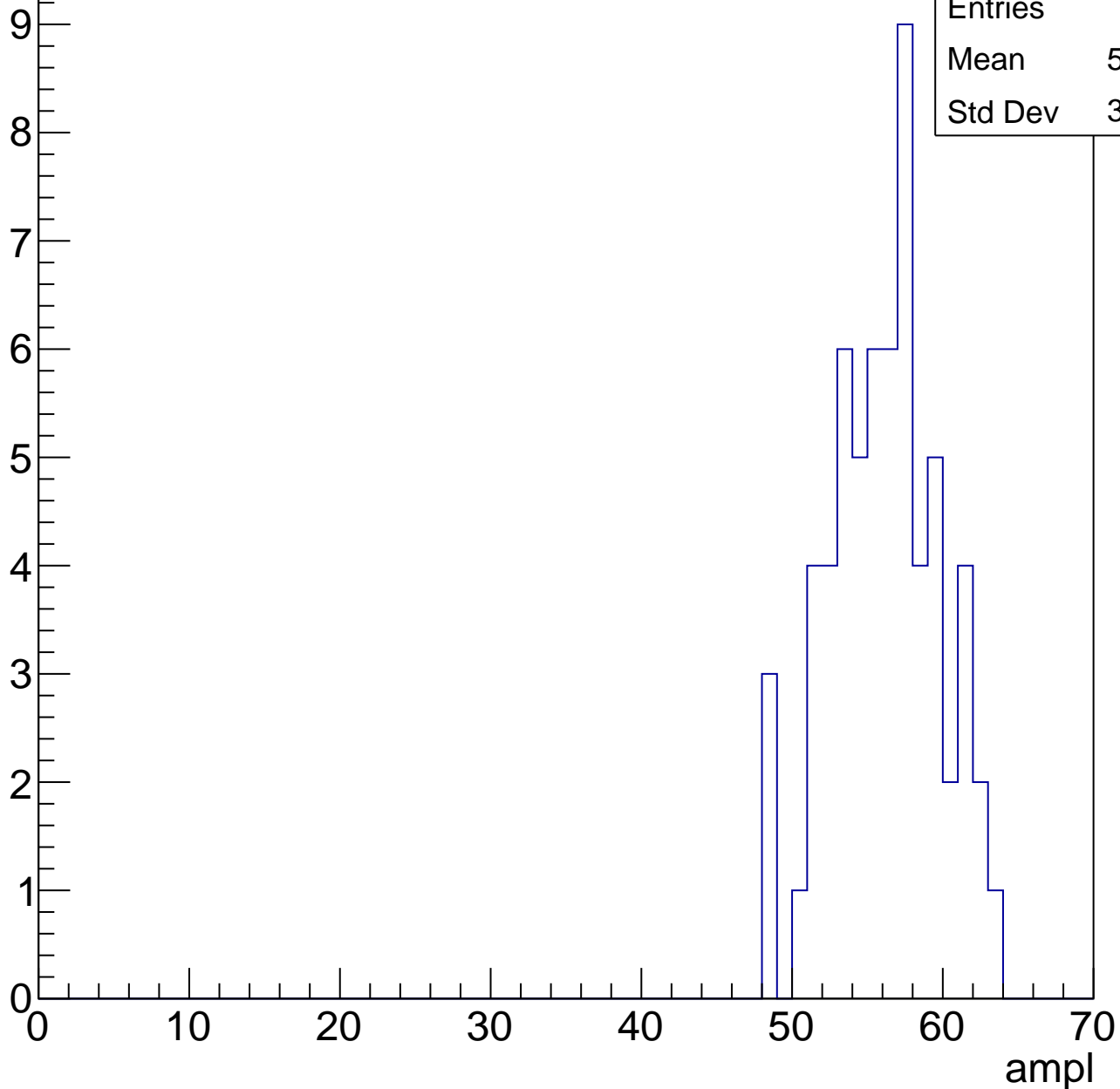


B0L000S, U4-ch9, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.66
Std Dev	3.565



B0L000S, U4-ch9, adc5

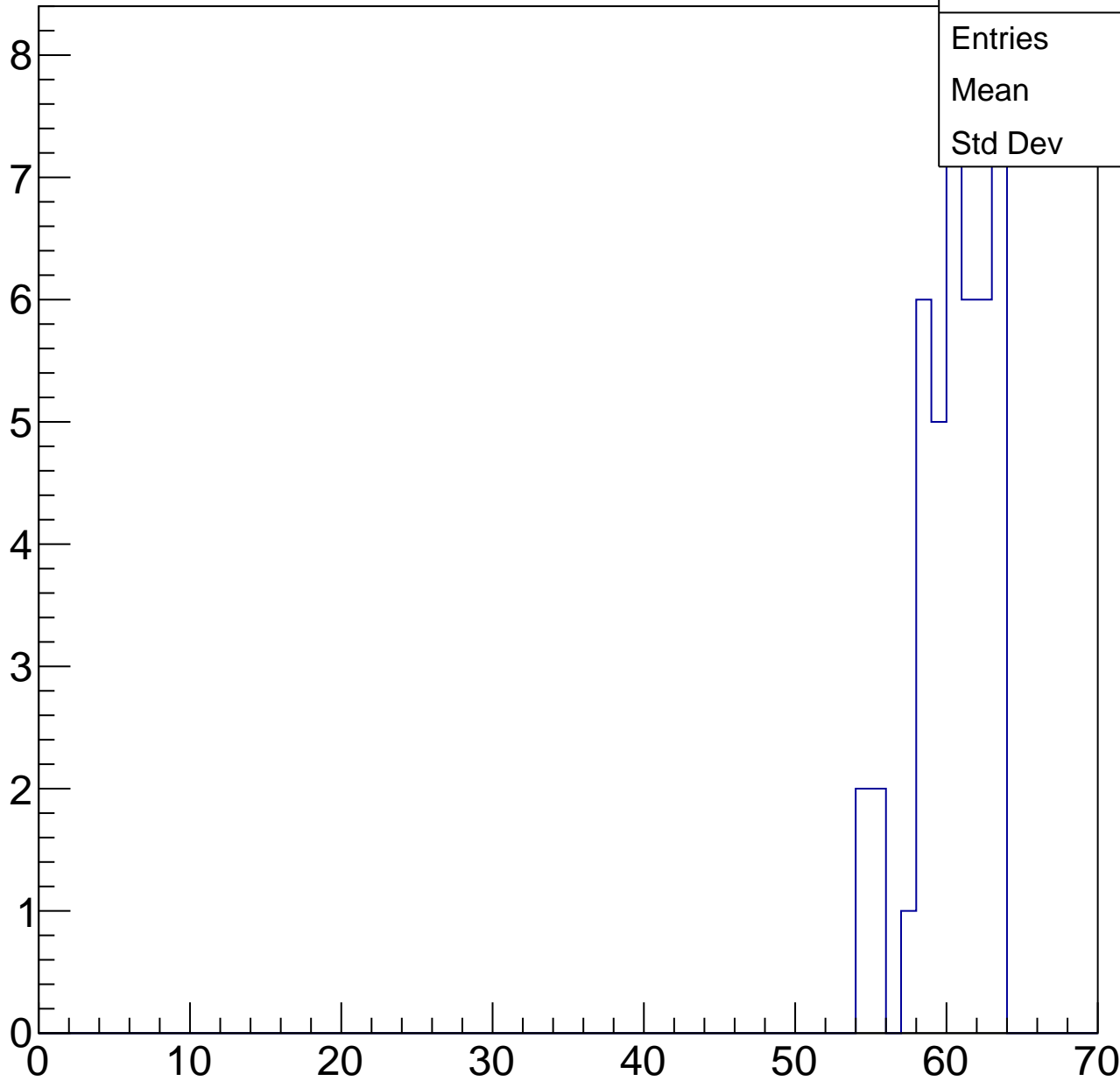
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	60
Std Dev	2.44

ampl



B0L000S, U4-ch9, adc6

calib_packv5_042523_0143.root, FC#5, port B1

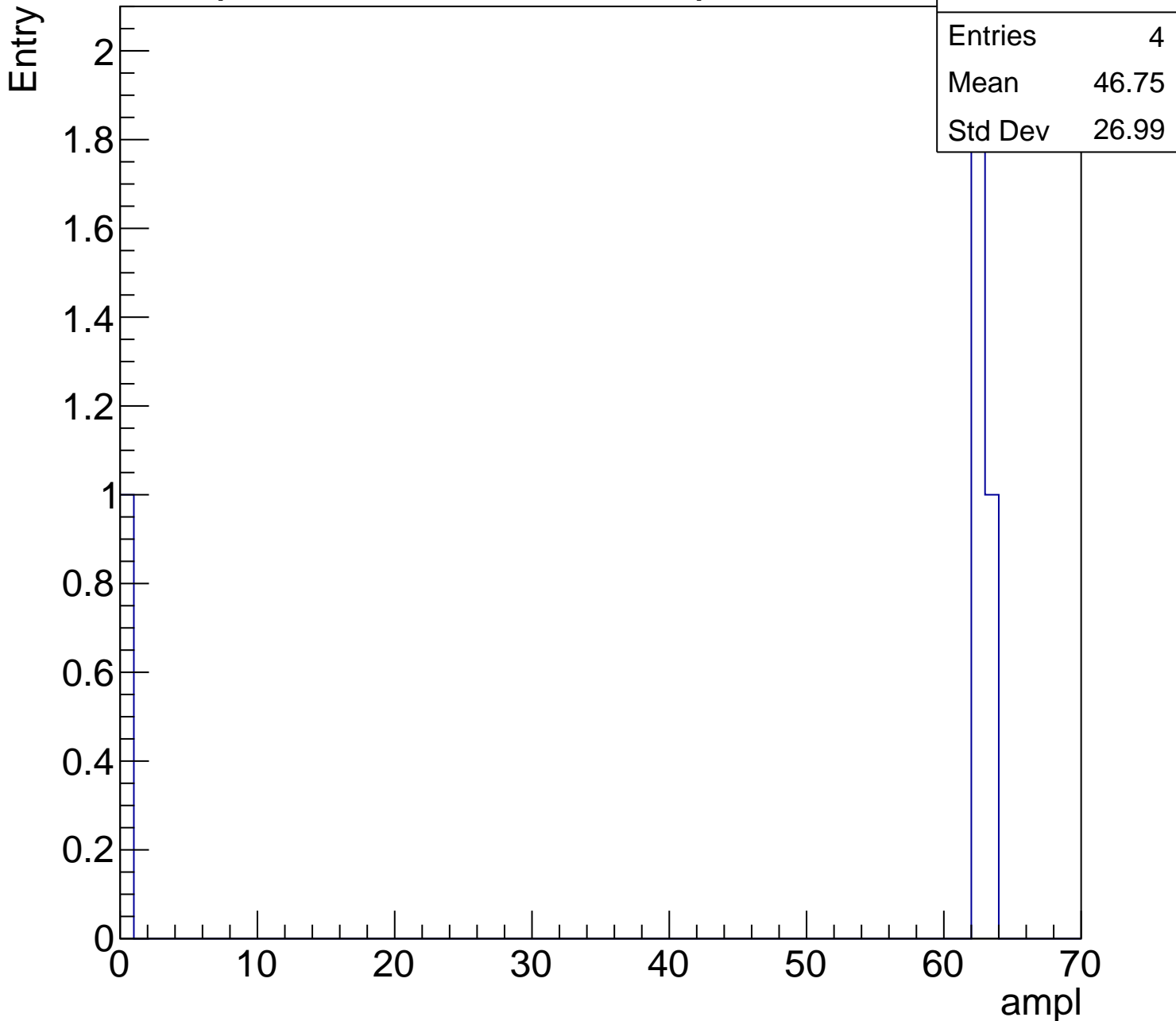
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	26.99

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch9, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch10, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	86
Mean	29.73
Std Dev	3.591

Gaus mean : 30.0931

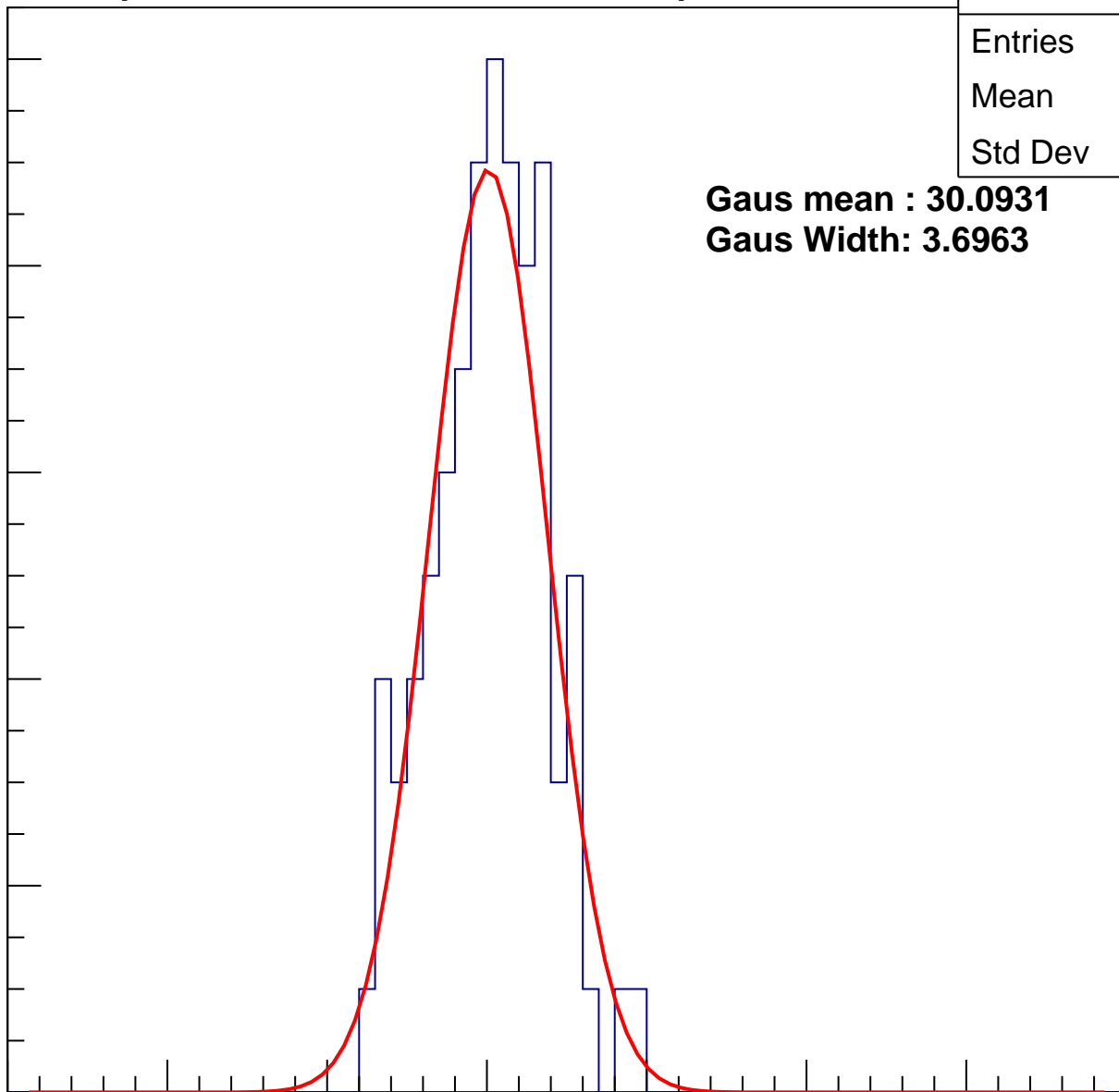
Gaus Width: 3.6963

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch10, adc1

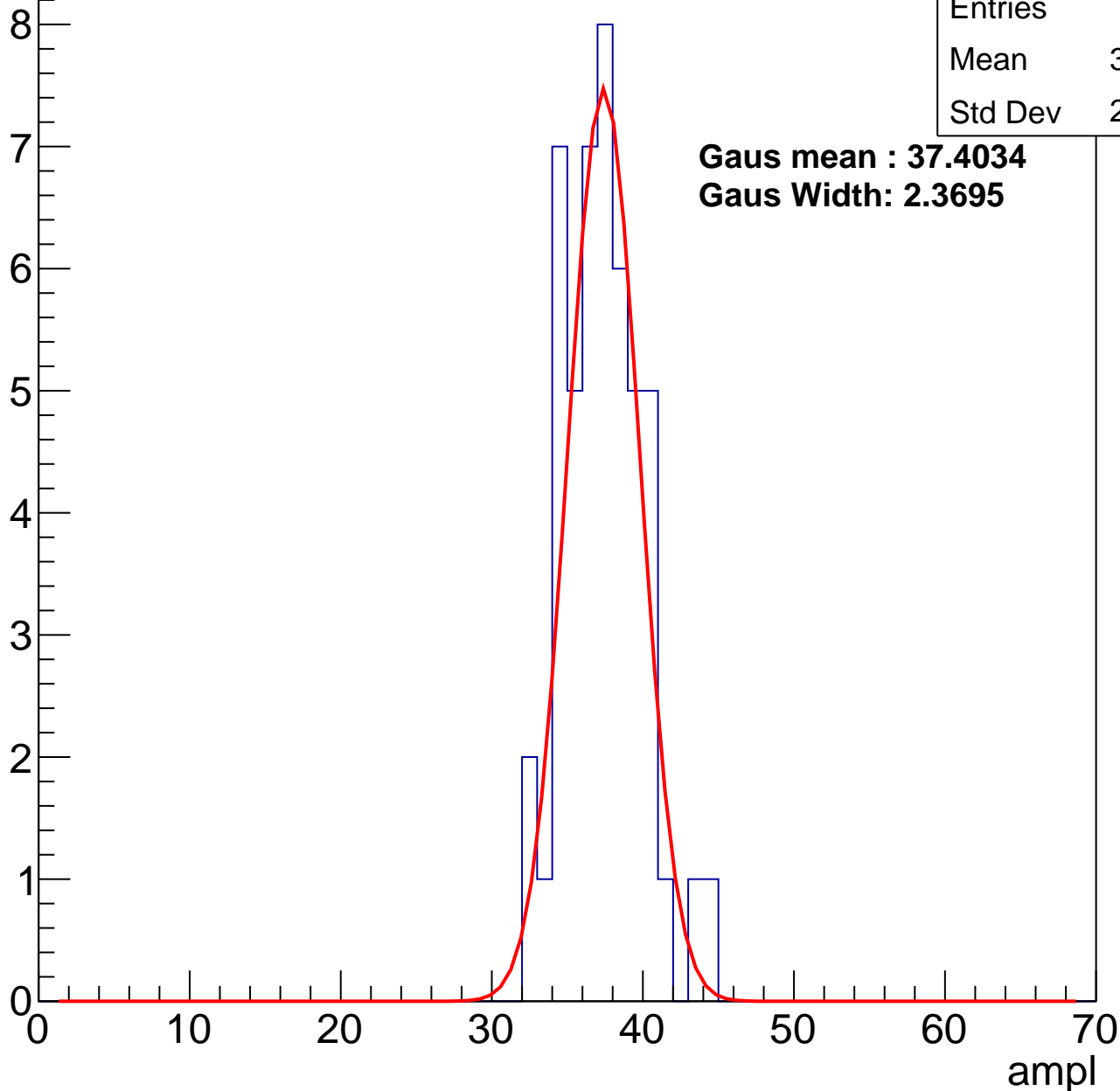
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.92
Std Dev	2.586

Gaus mean : 37.4034

Gaus Width: 2.3695



B0L000S, U4-ch10, adc2

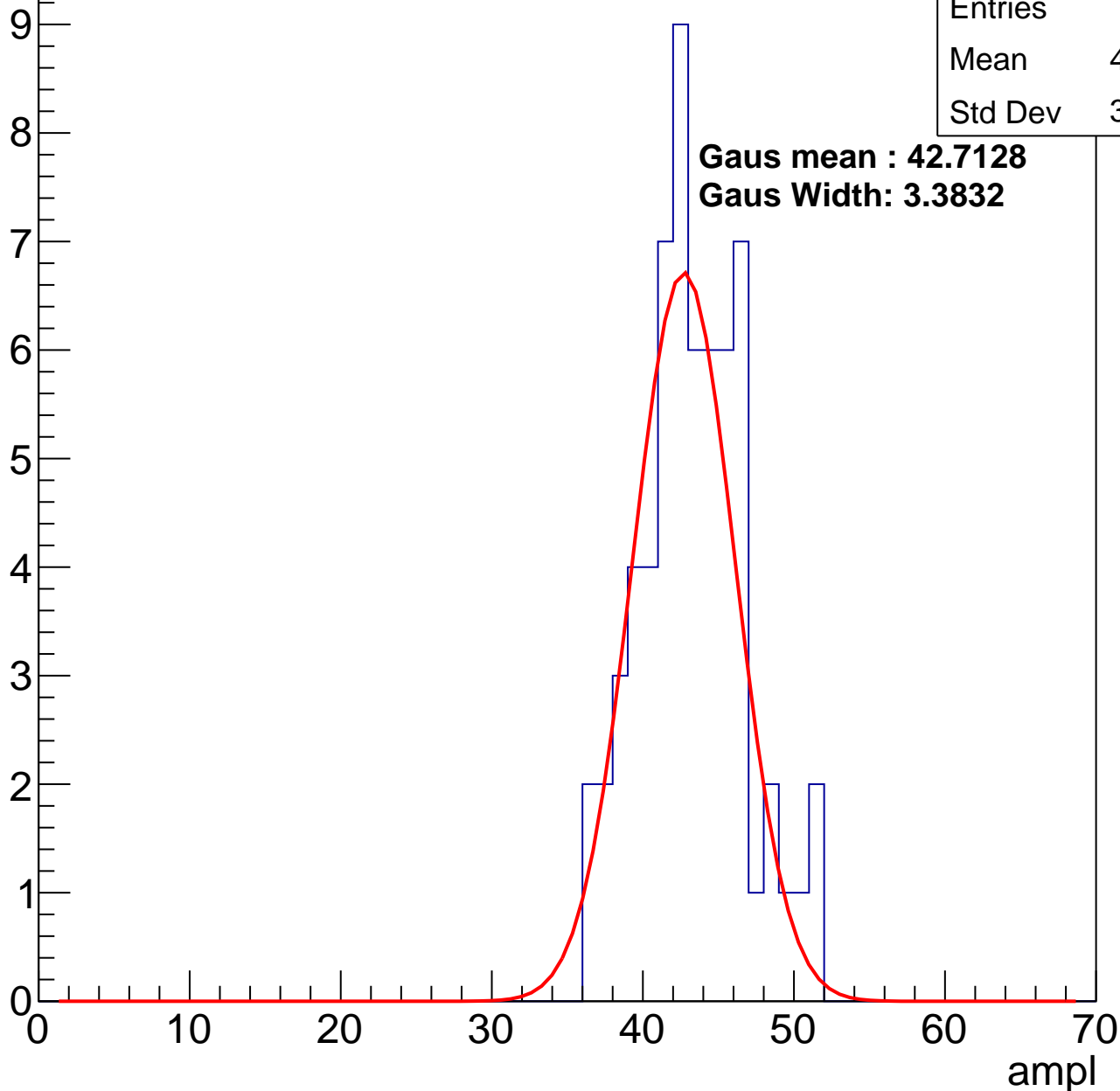
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.84
Std Dev	3.456

Gaus mean : 42.7128

Gaus Width: 3.3832

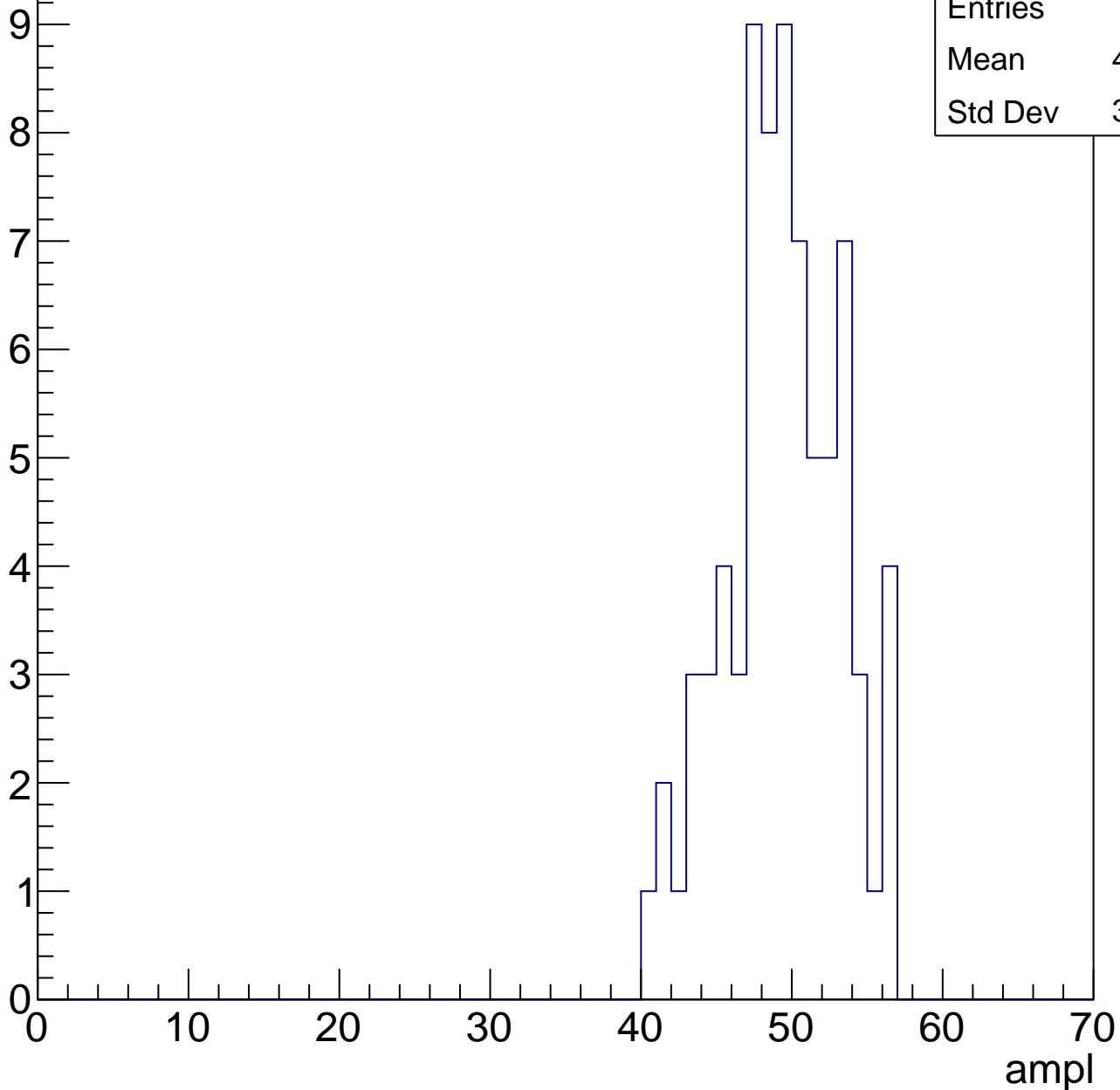


B0L000S, U4-ch10, adc3

calib_packv5_042523_0143.root, FC#5, port B1

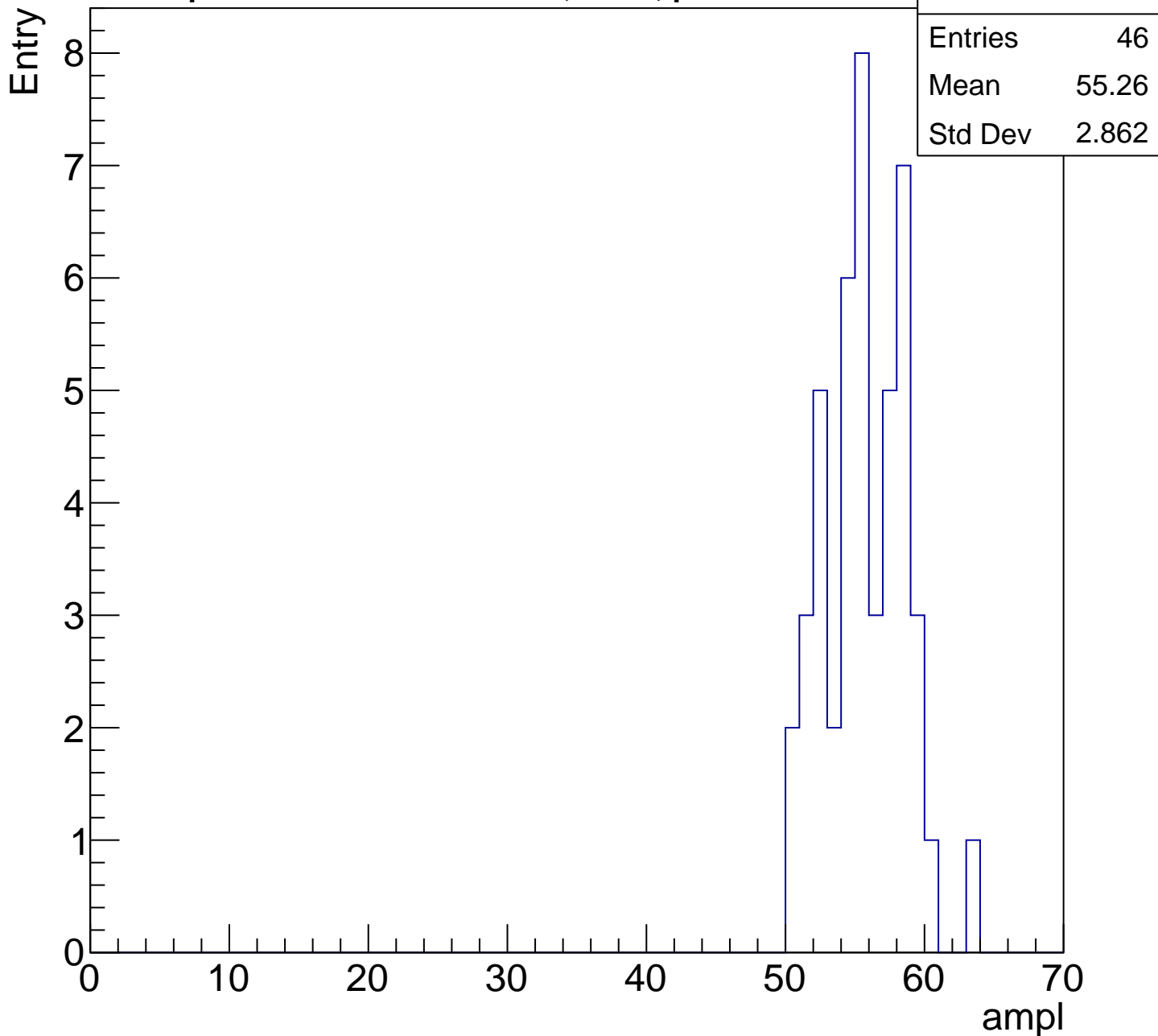
Entry

Entries	75
Mean	48.91
Std Dev	3.771



B0L000S, U4-ch10, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch10, adc5

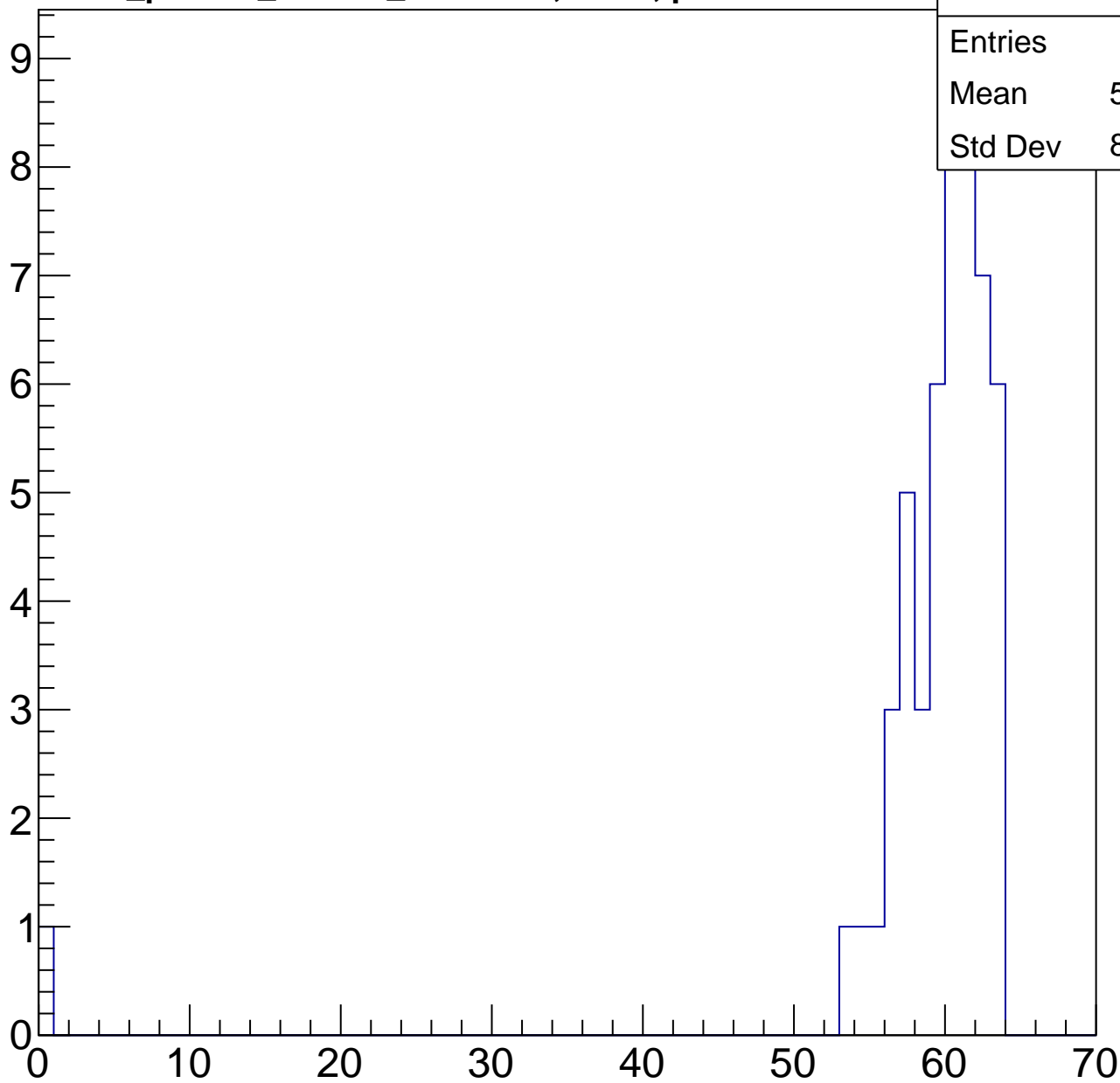
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.49
Std Dev	8.626

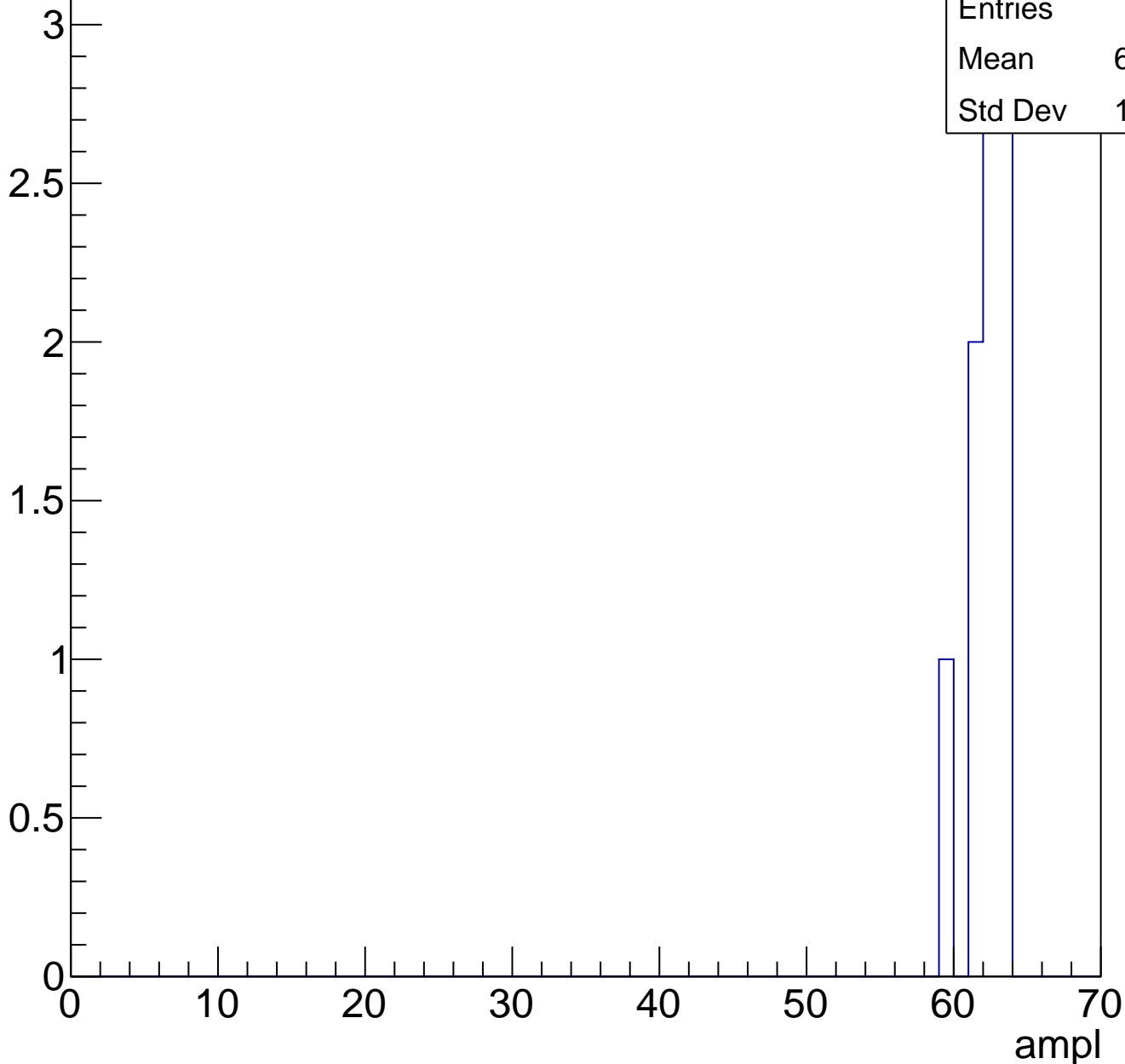
ampl



B0L000S, U4-ch10, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch10, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch11, adc0

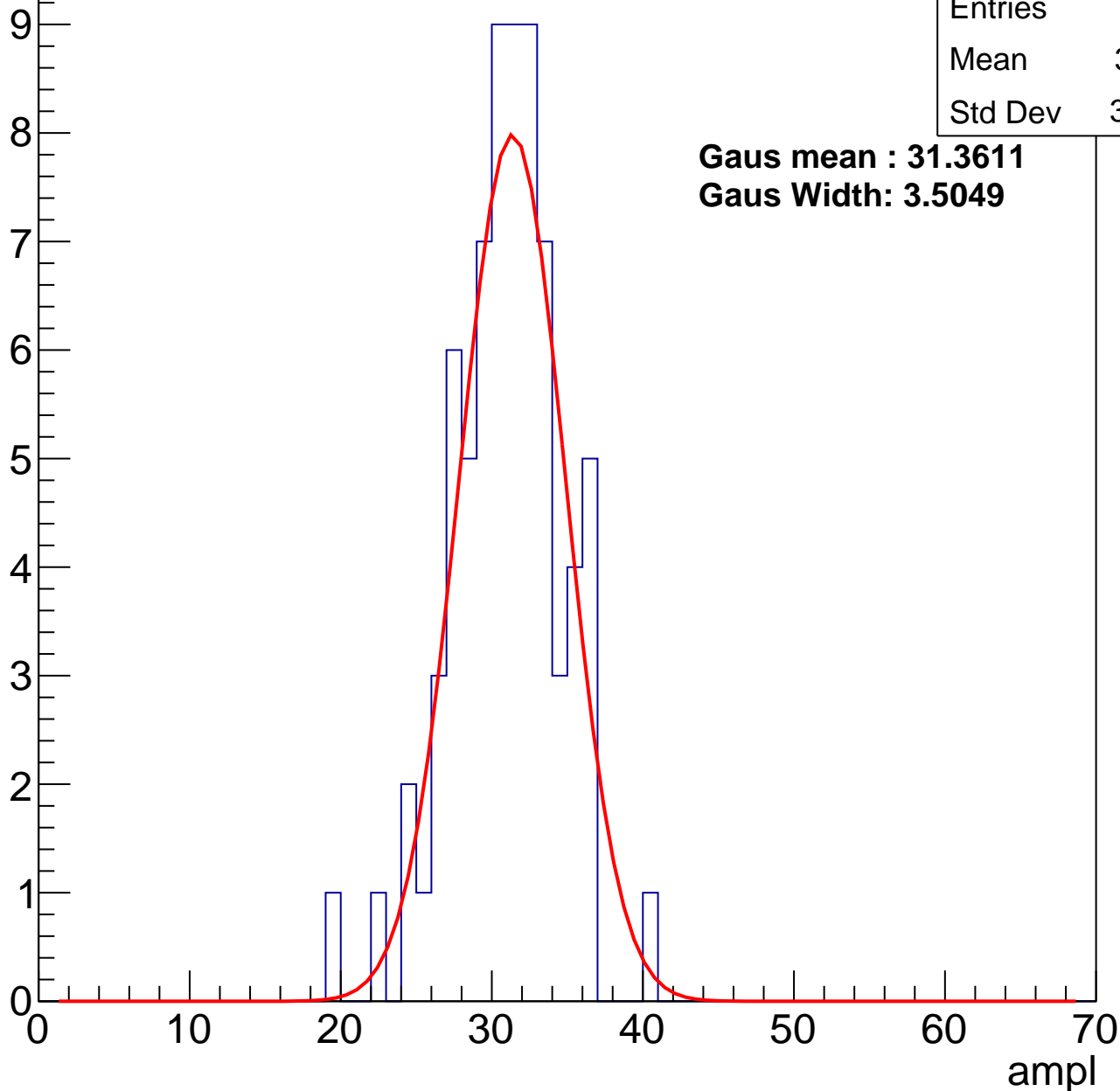
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	30.51
Std Dev	3.566

Gaus mean : 31.3611

Gaus Width: 3.5049



B0L000S, U4-ch11, adc1

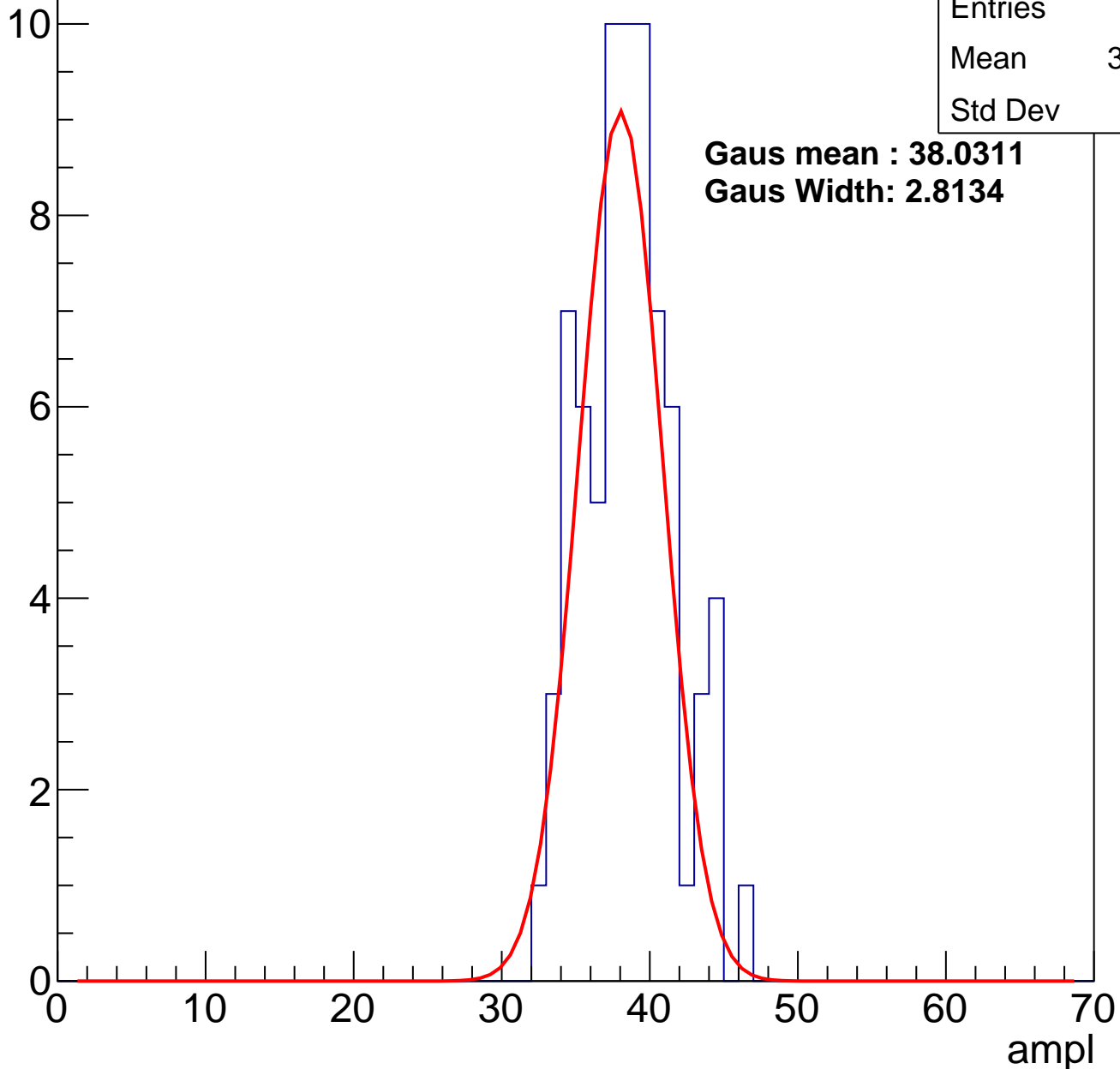
calib_packv5_042523_0143.root, FC#5, port B1

Entries	74
Mean	38.08
Std Dev	3.07

Gaus mean : 38.0311

Gaus Width: 2.8134

Entry



B0L000S, U4-ch11, adc2

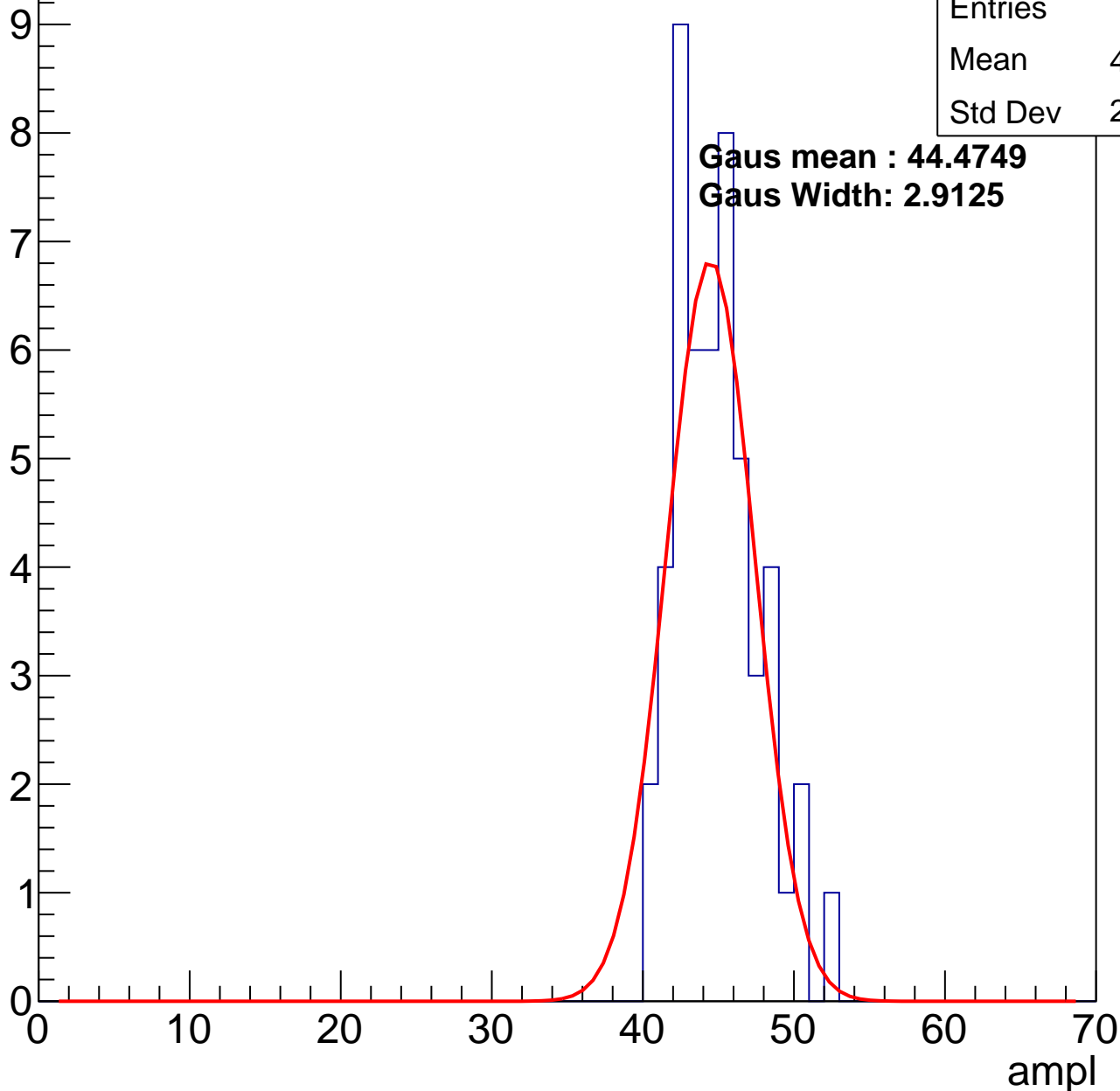
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	44.47
Std Dev	2.725

Gaus mean : 44.4749

Gaus Width: 2.9125

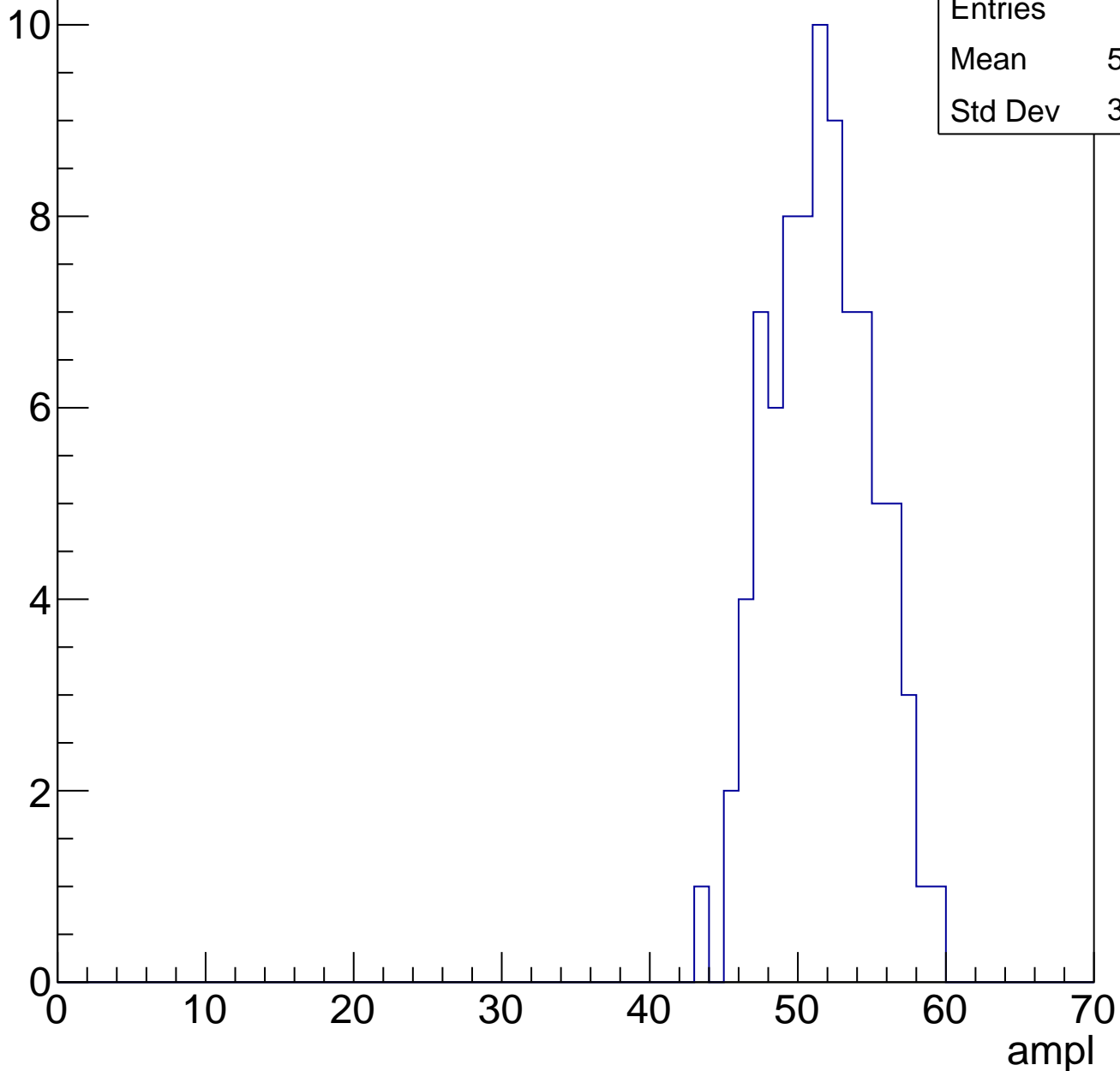


B0L000S, U4-ch11, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	84
Mean	51.14
Std Dev	3.392

Entry

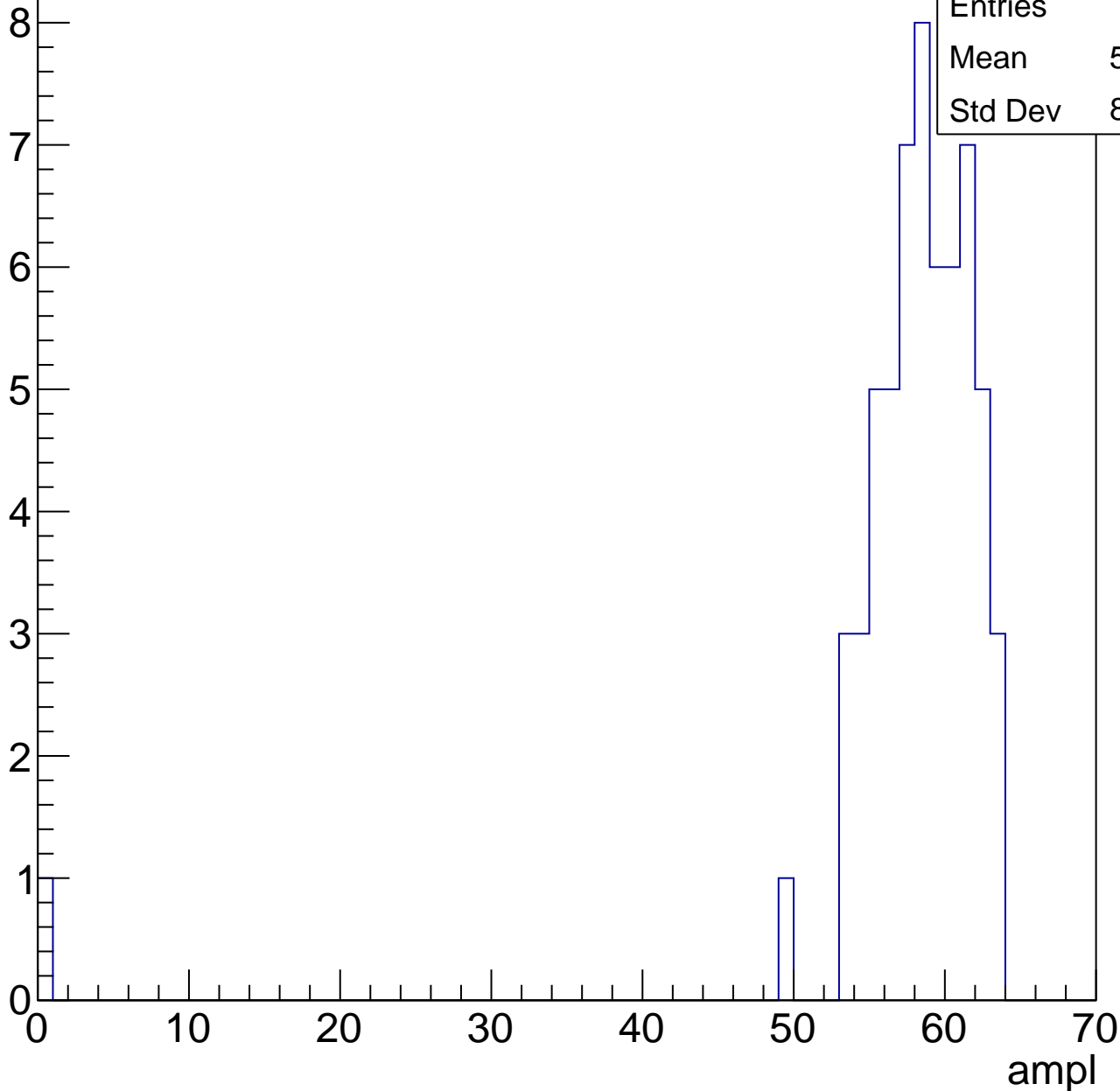


B0L000S, U4-ch11, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57.13
Std Dev	8.003

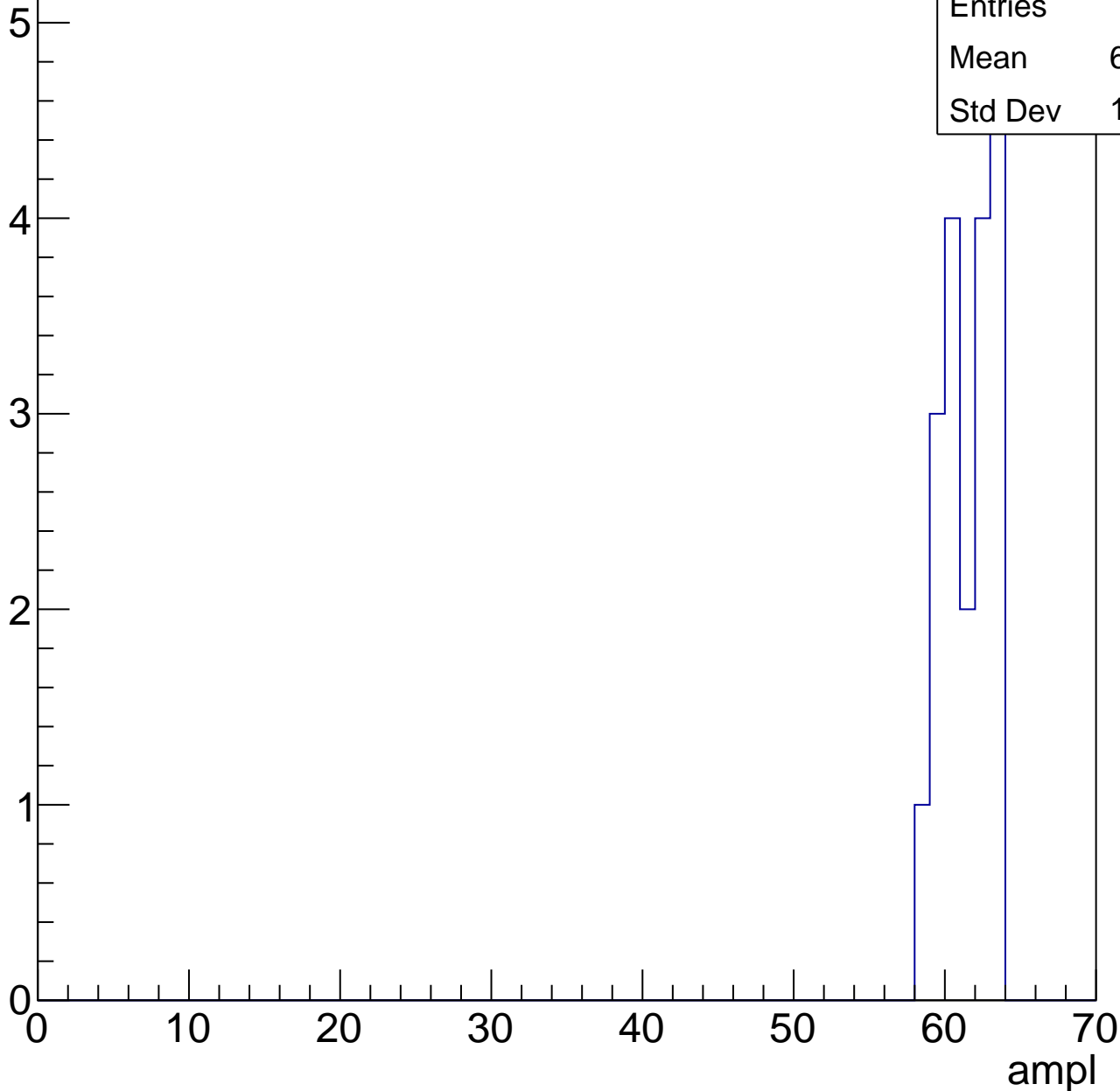


B0L000S, U4-ch11, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	19
Mean	61.05
Std Dev	1.605



B0L000S, U4-ch11, adc6

calib_packv5_042523_0143.root, FC#5, port B1

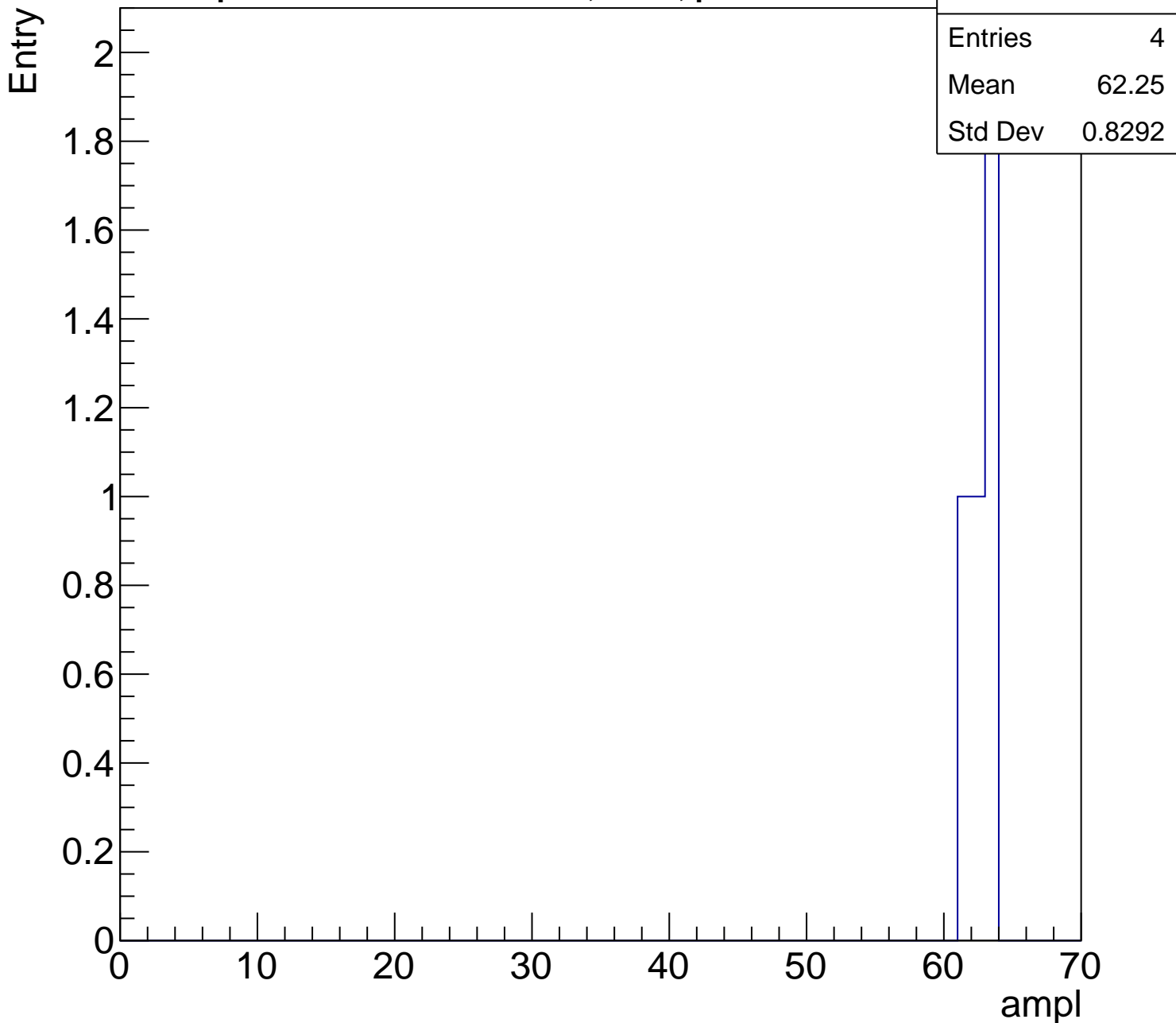
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch11, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch12, adc0

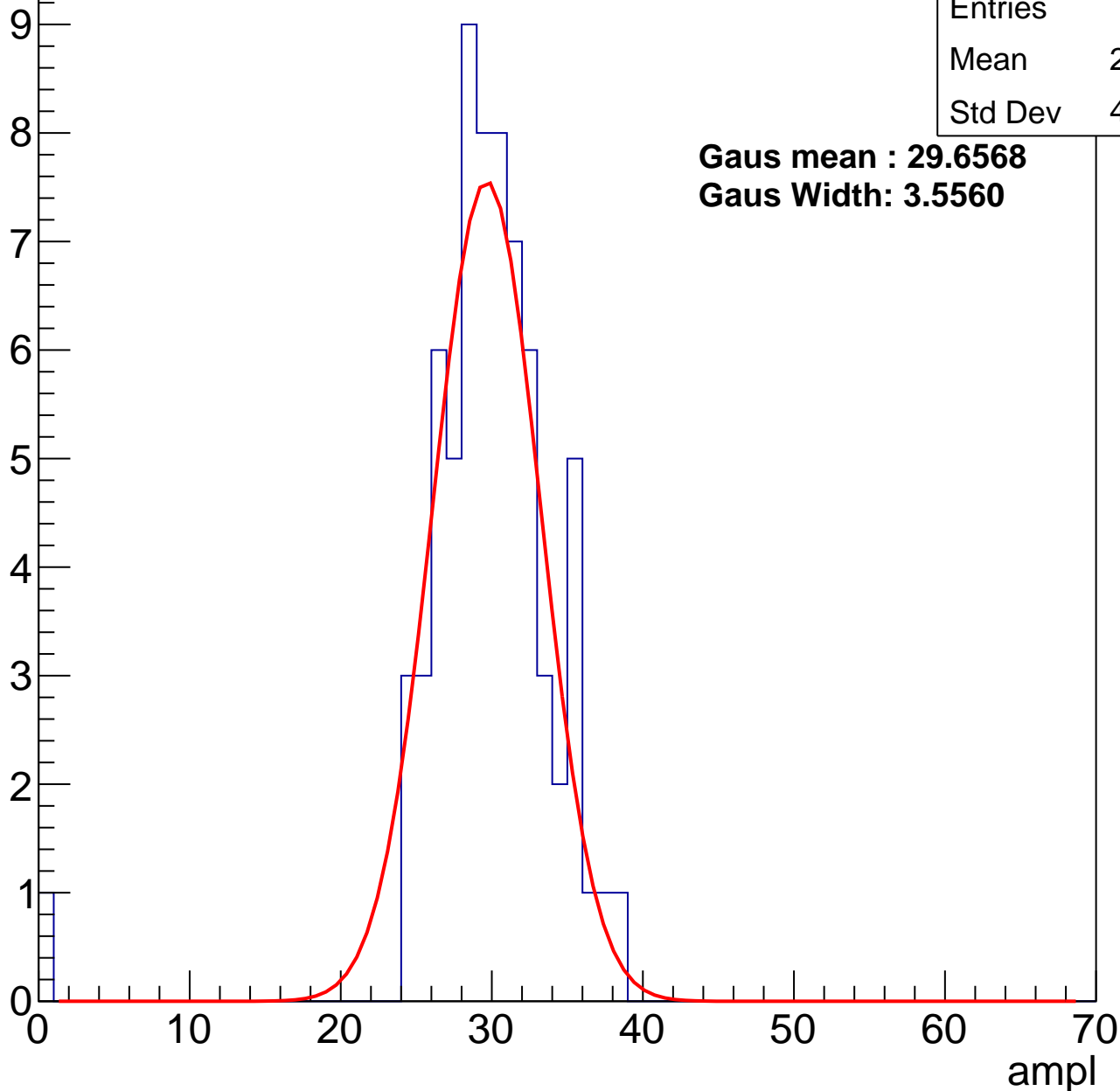
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.33
Std Dev	4.817

Gaus mean : 29.6568

Gaus Width: 3.5560



B0L000S, U4-ch12, adc1

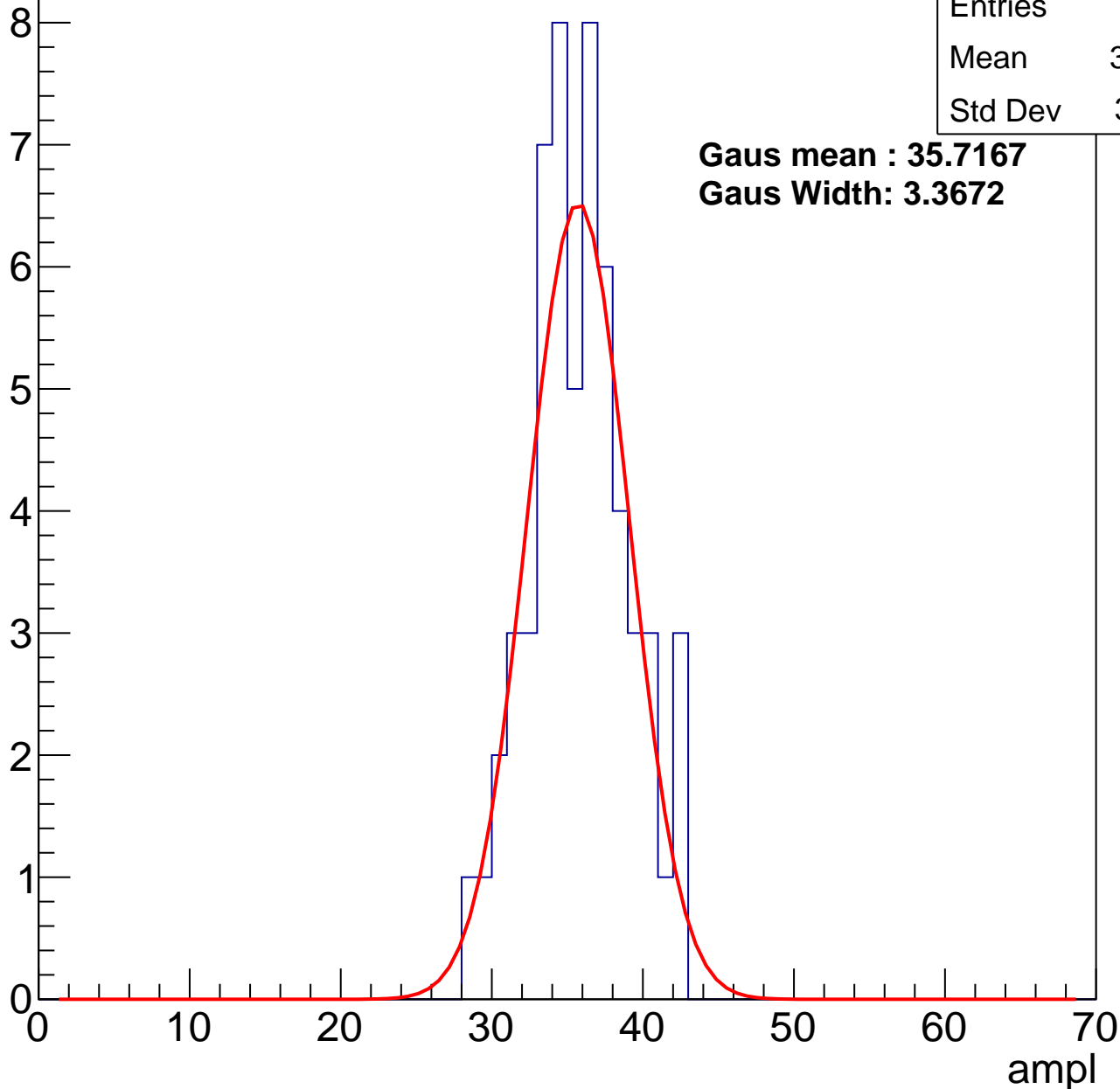
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	35.34
Std Dev	3.251

Gaus mean : 35.7167

Gaus Width: 3.3672



B0L000S, U4-ch12, adc2

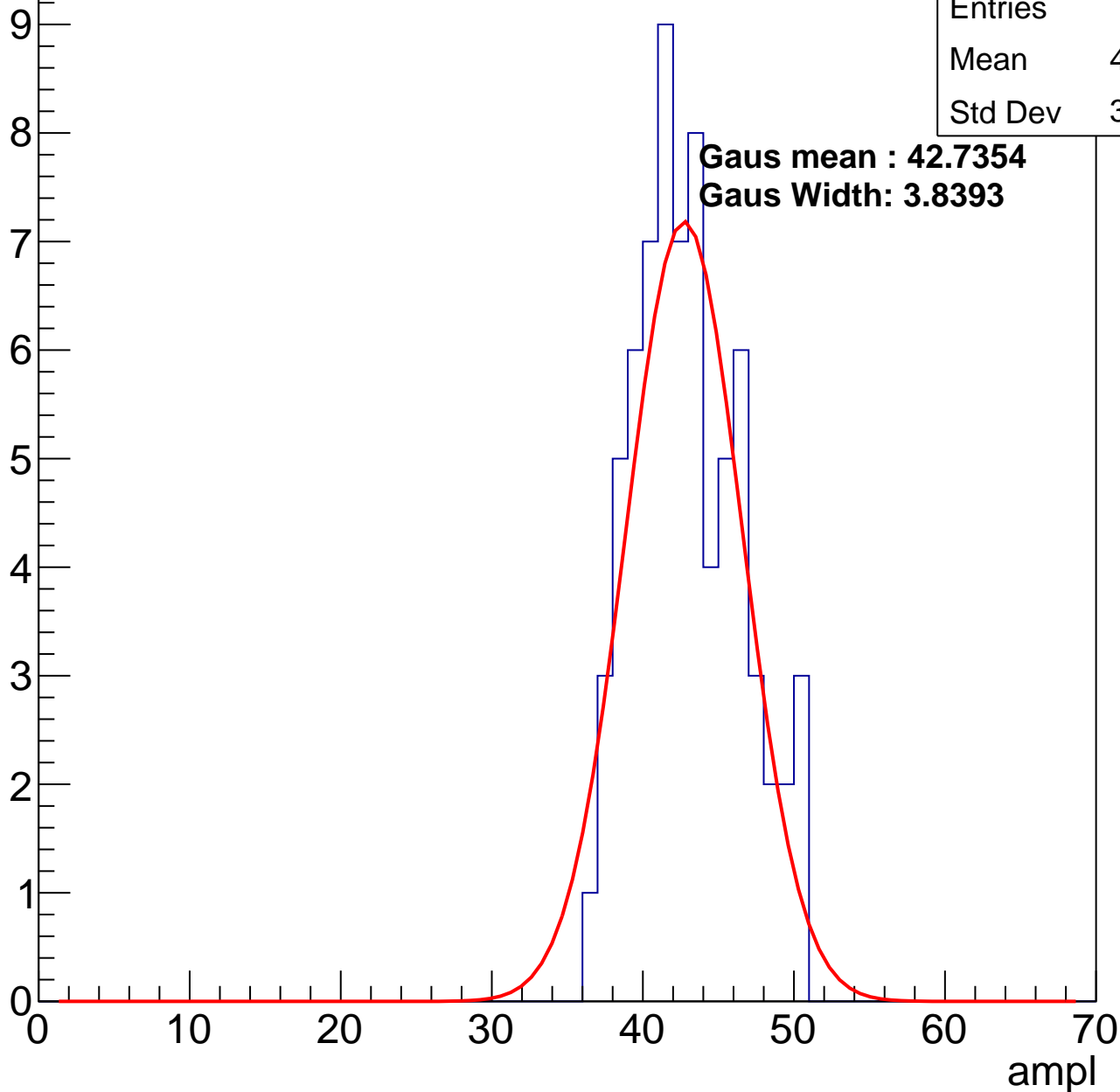
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	42.54
Std Dev	3.488

Gaus mean : 42.7354

Gaus Width: 3.8393

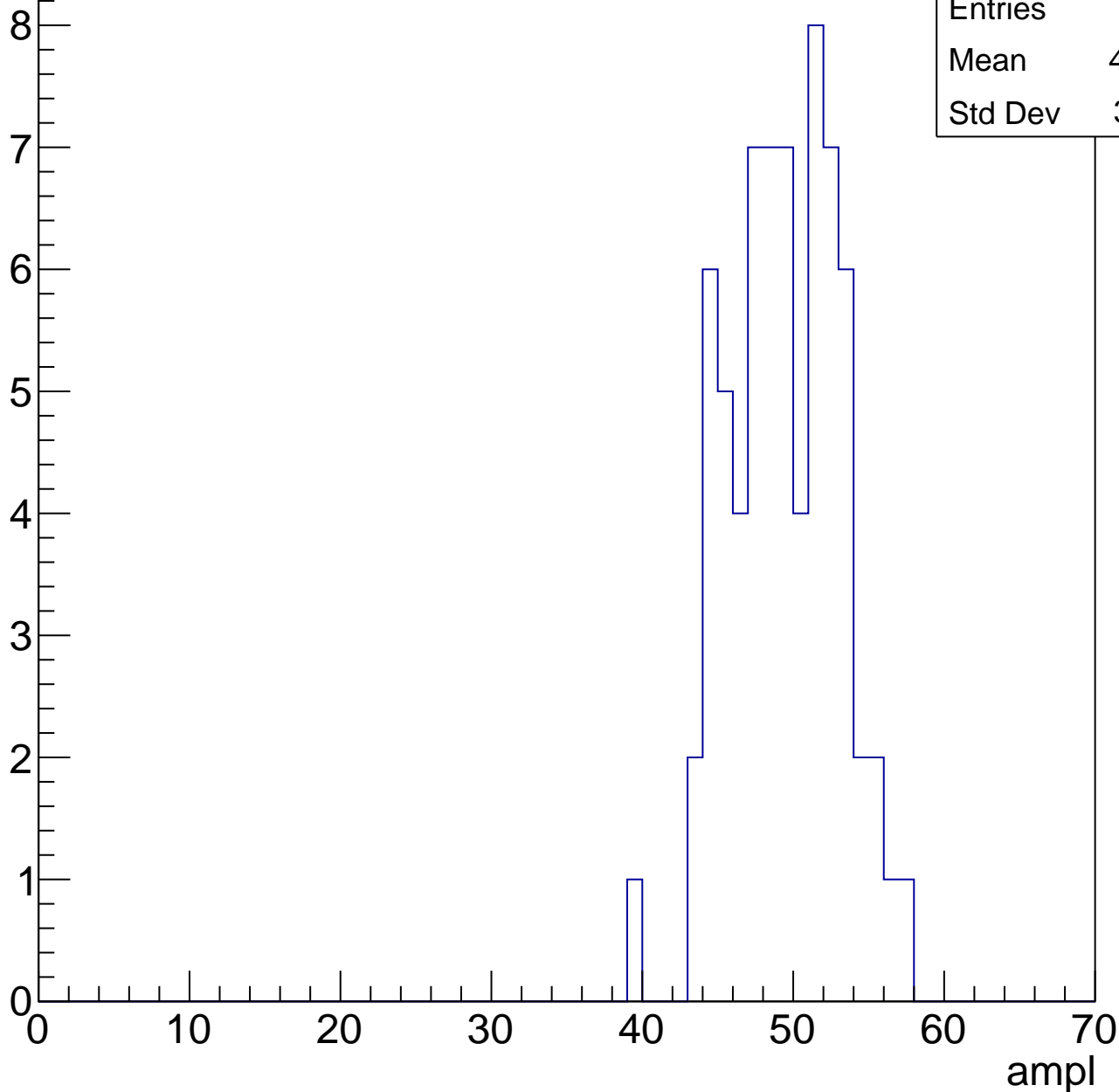


B0L000S, U4-ch12, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	48.96
Std Dev	3.591

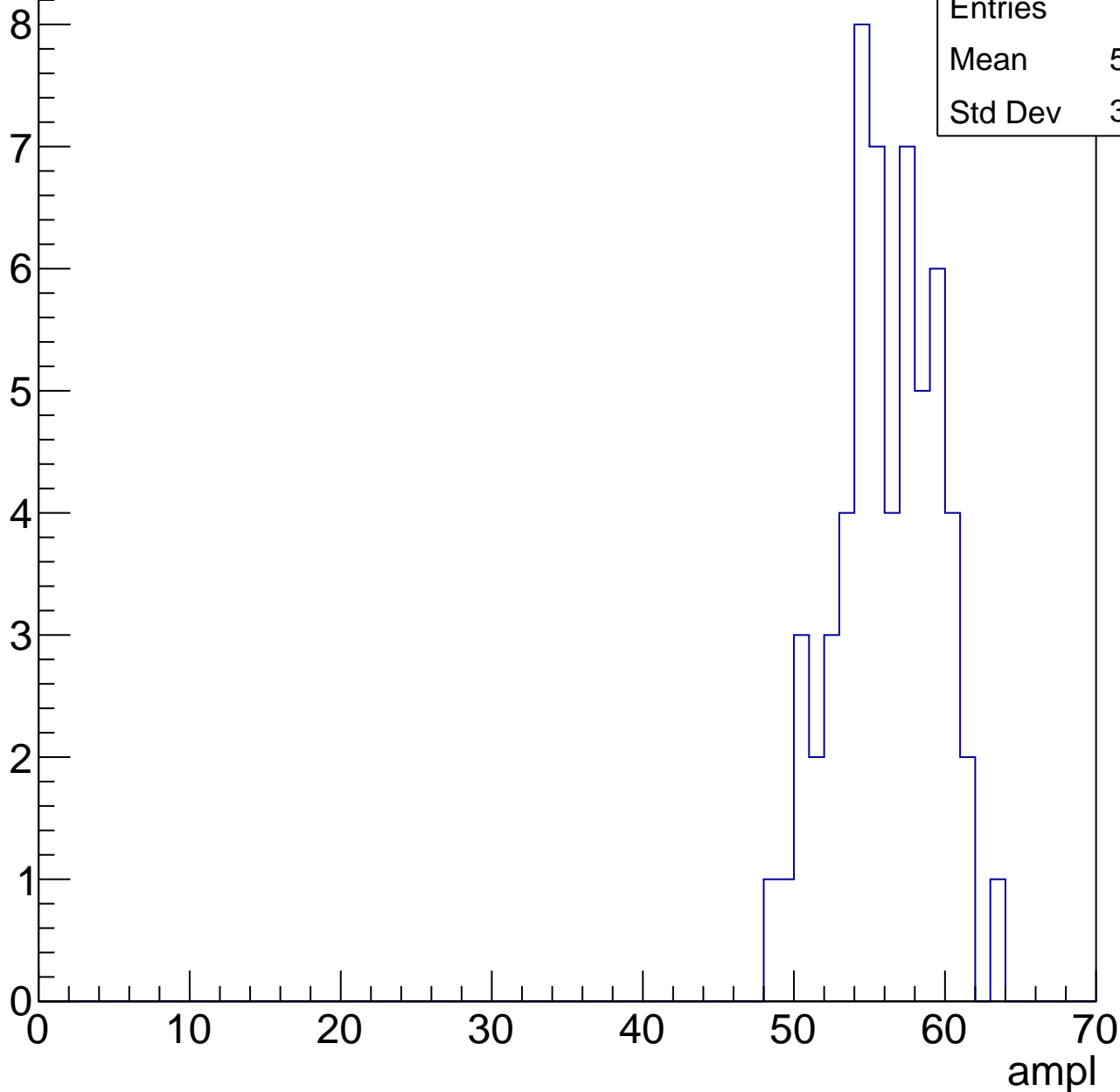


B0L000S, U4-ch12, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.62
Std Dev	3.295

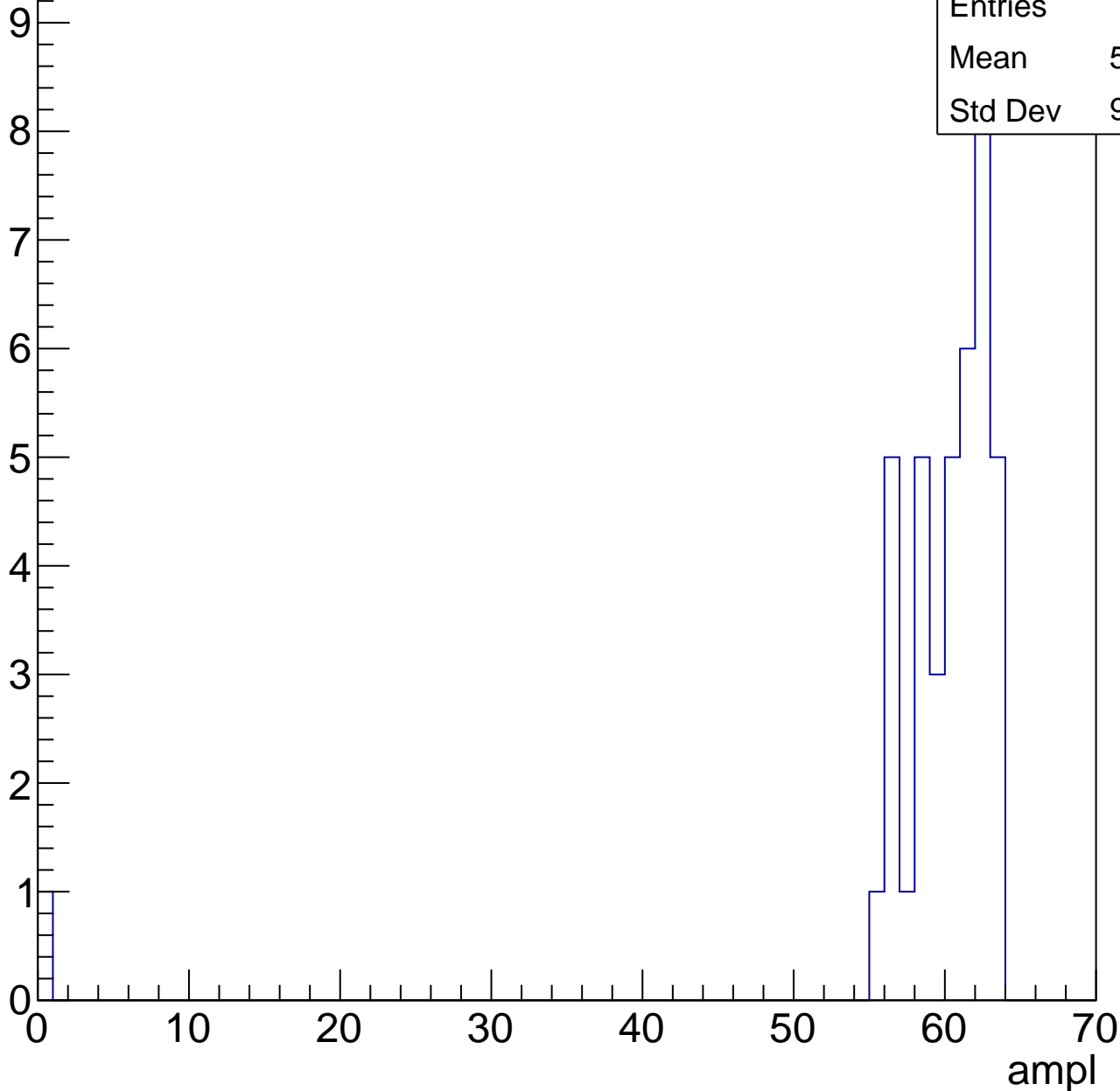


B0L000S, U4-ch12, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.49
Std Dev	9.538



B0L000S, U4-ch12, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

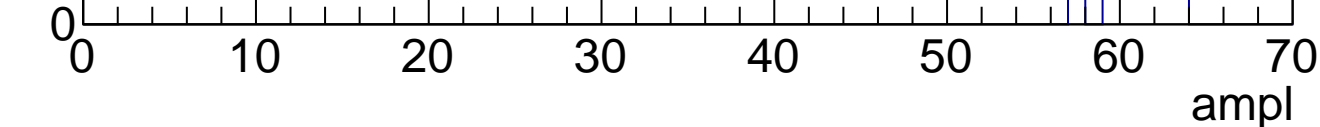
9

Mean

61

Std Dev

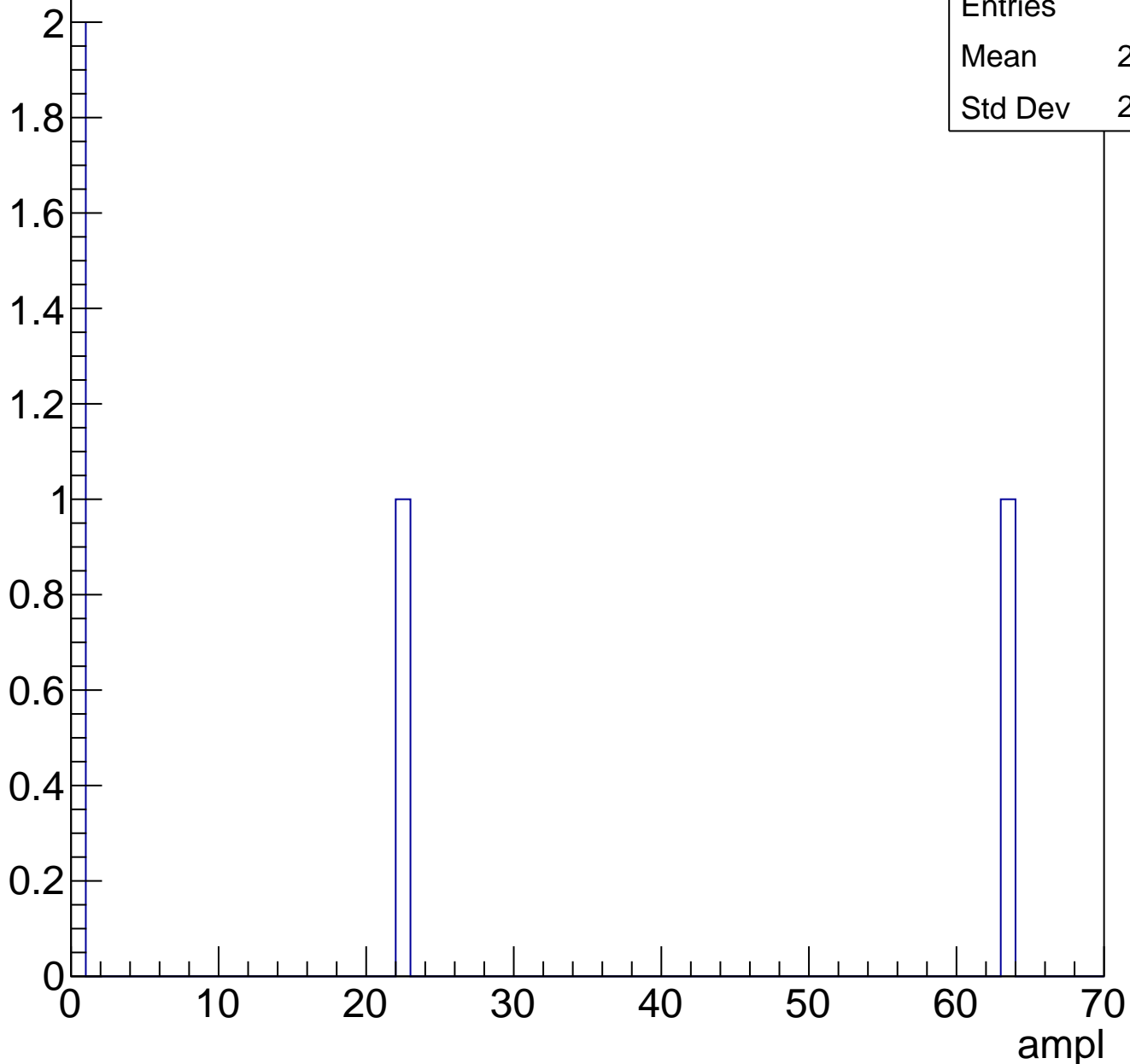
1.944



B0L000S, U4-ch12, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	4
Mean	21.25
Std Dev	25.72

B0L000S, U4-ch13, adc0

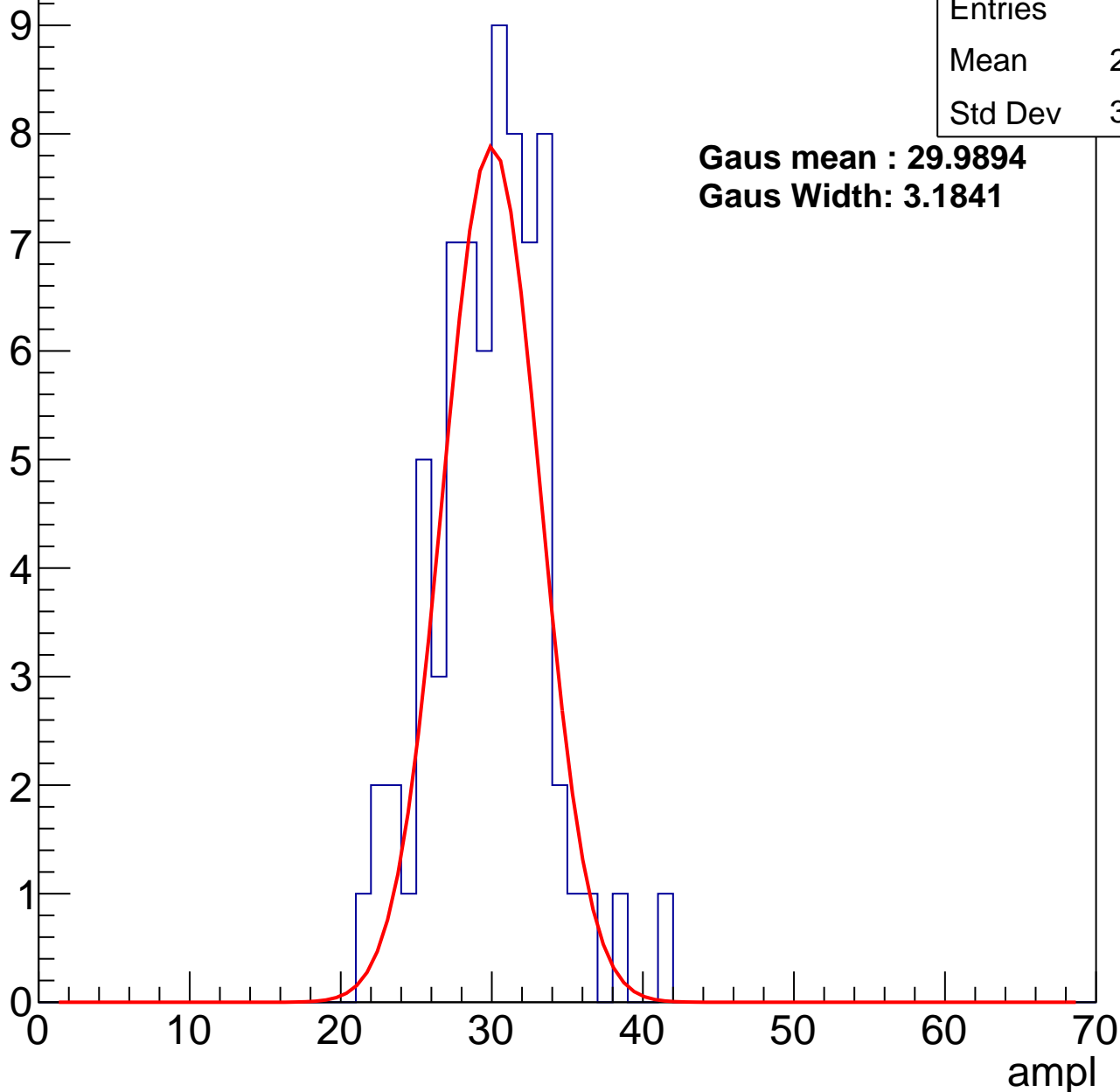
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	29.46
Std Dev	3.685

Gaus mean : 29.9894

Gaus Width: 3.1841



B0L000S, U4-ch13, adc1

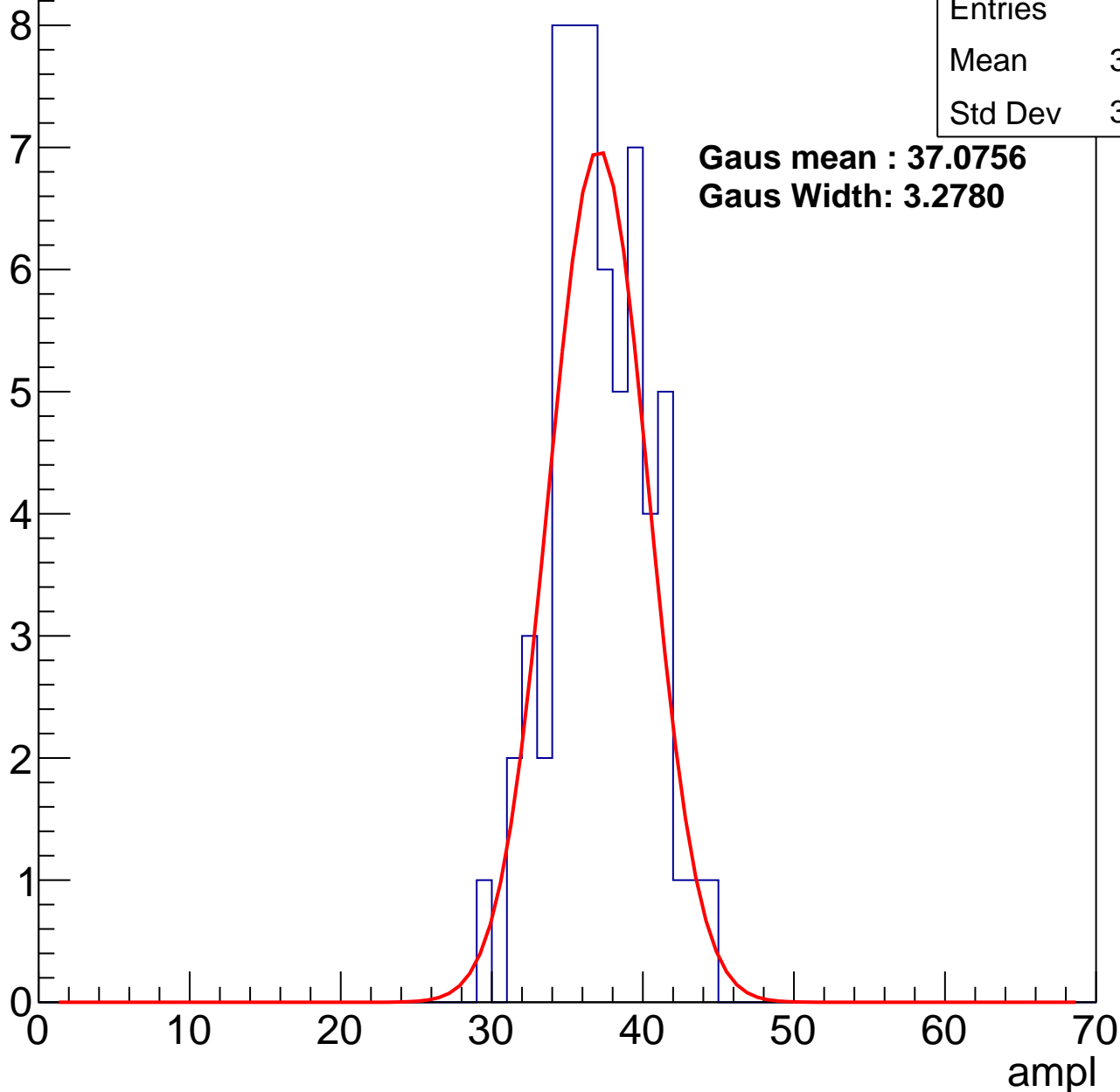
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.65
Std Dev	3.127

Gaus mean : 37.0756

Gaus Width: 3.2780



B0L000S, U4-ch13, adc2

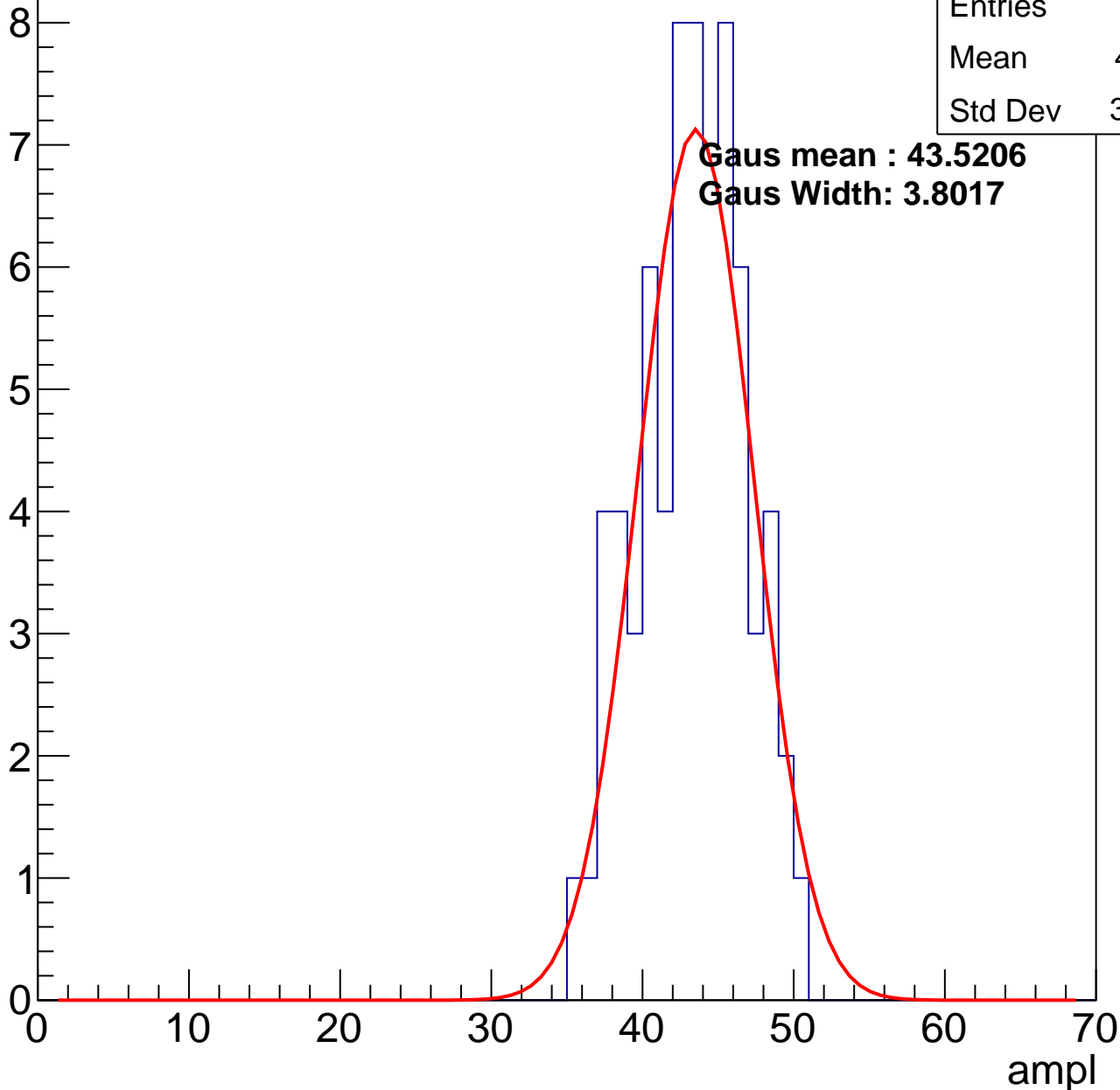
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.81
Std Dev	3.469

Gaus mean : 43.5206

Gaus Width: 3.8017

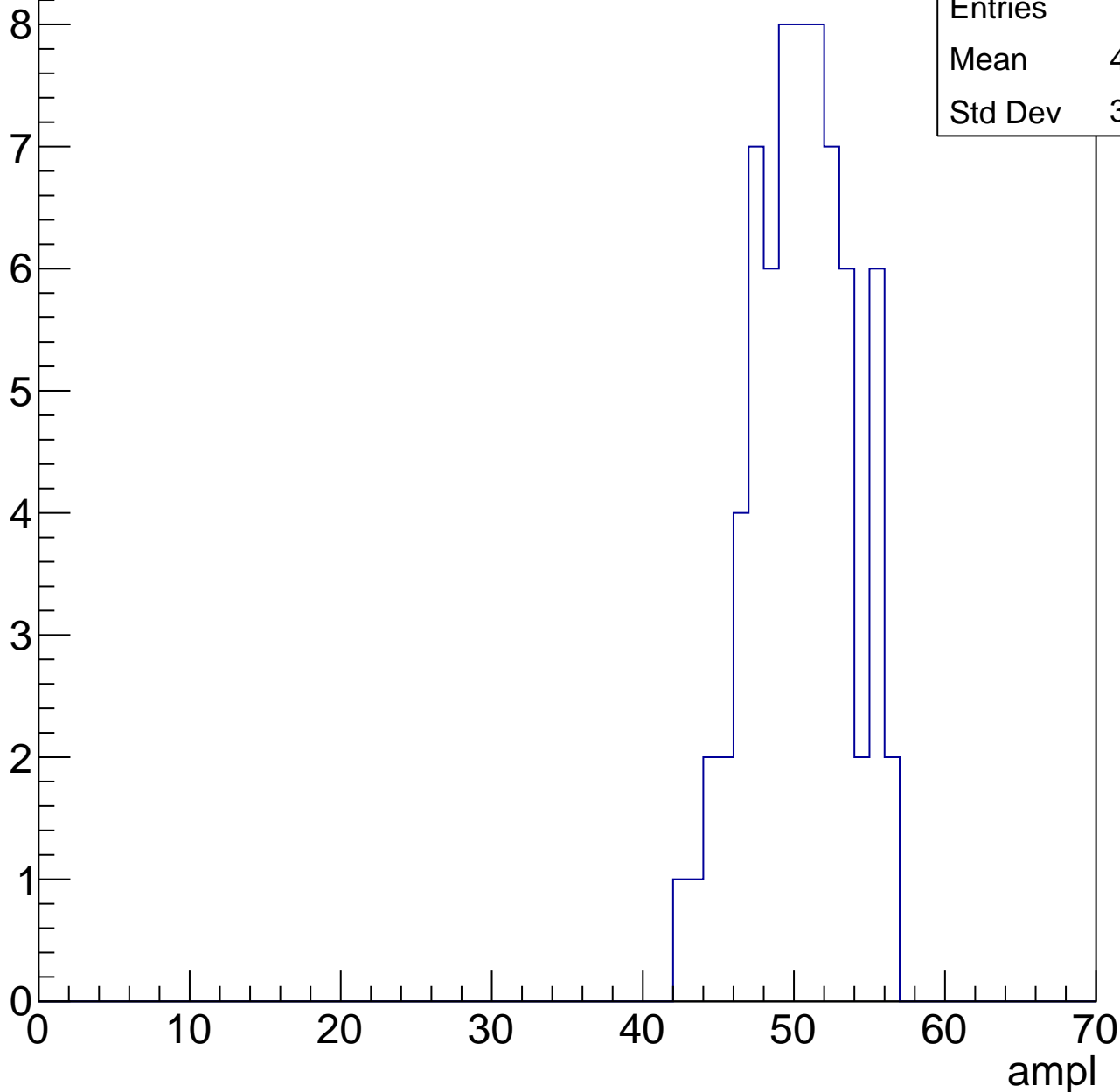


B0L000S, U4-ch13, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.94
Std Dev	3.246

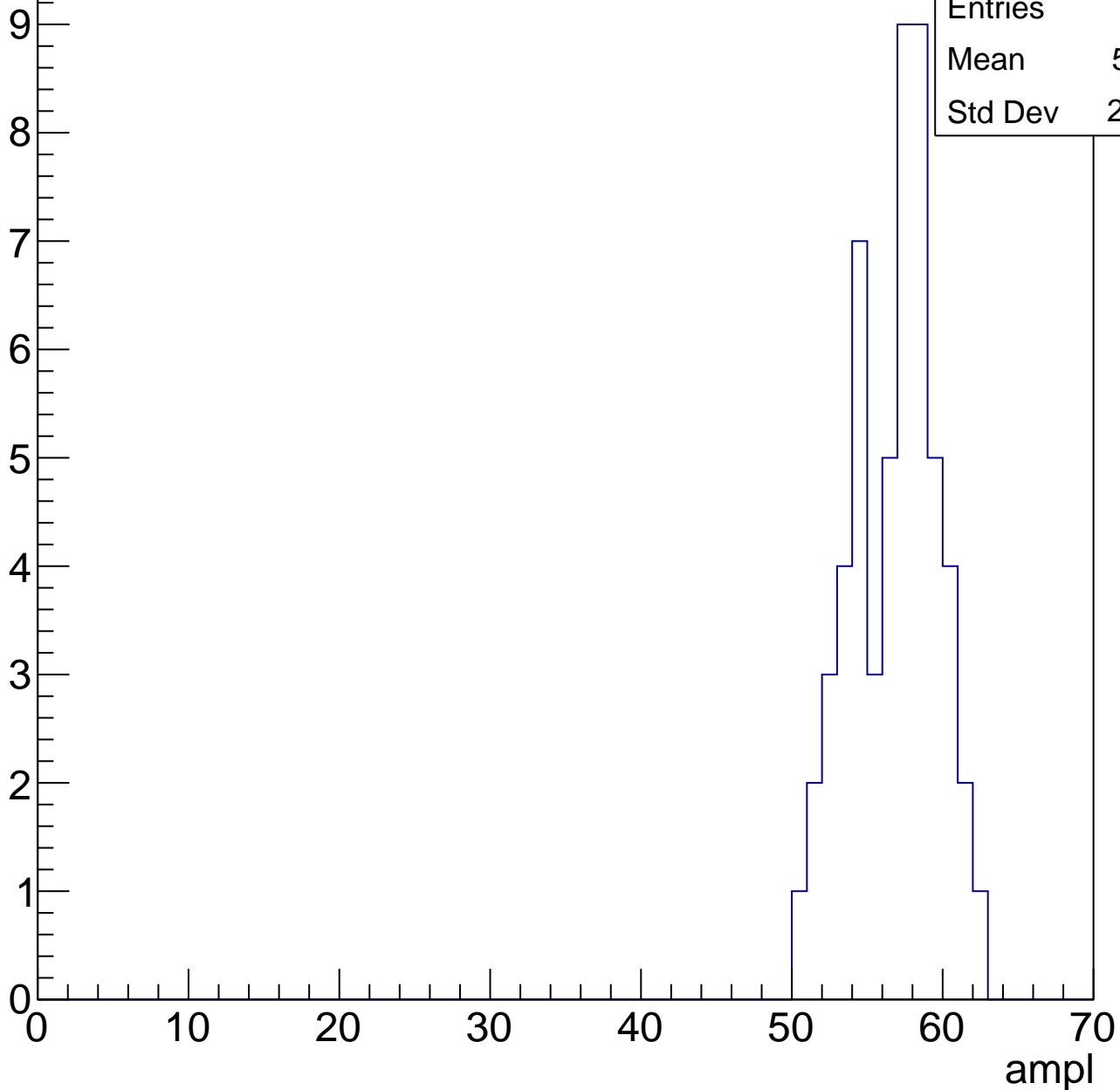


B0L000S, U4-ch13, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.31
Std Dev	2.815

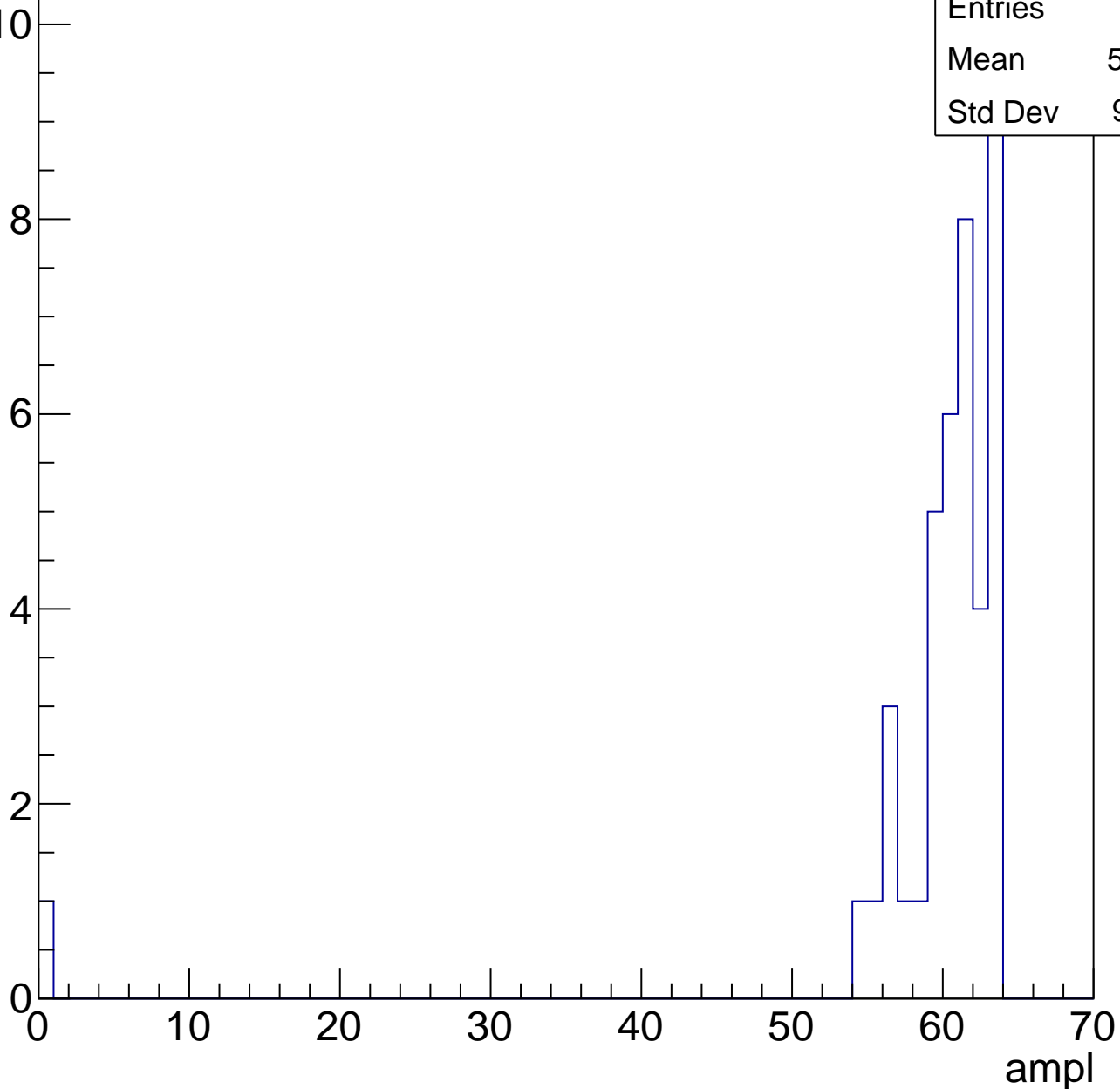


B0L000S, U4-ch13, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.85
Std Dev	9.611

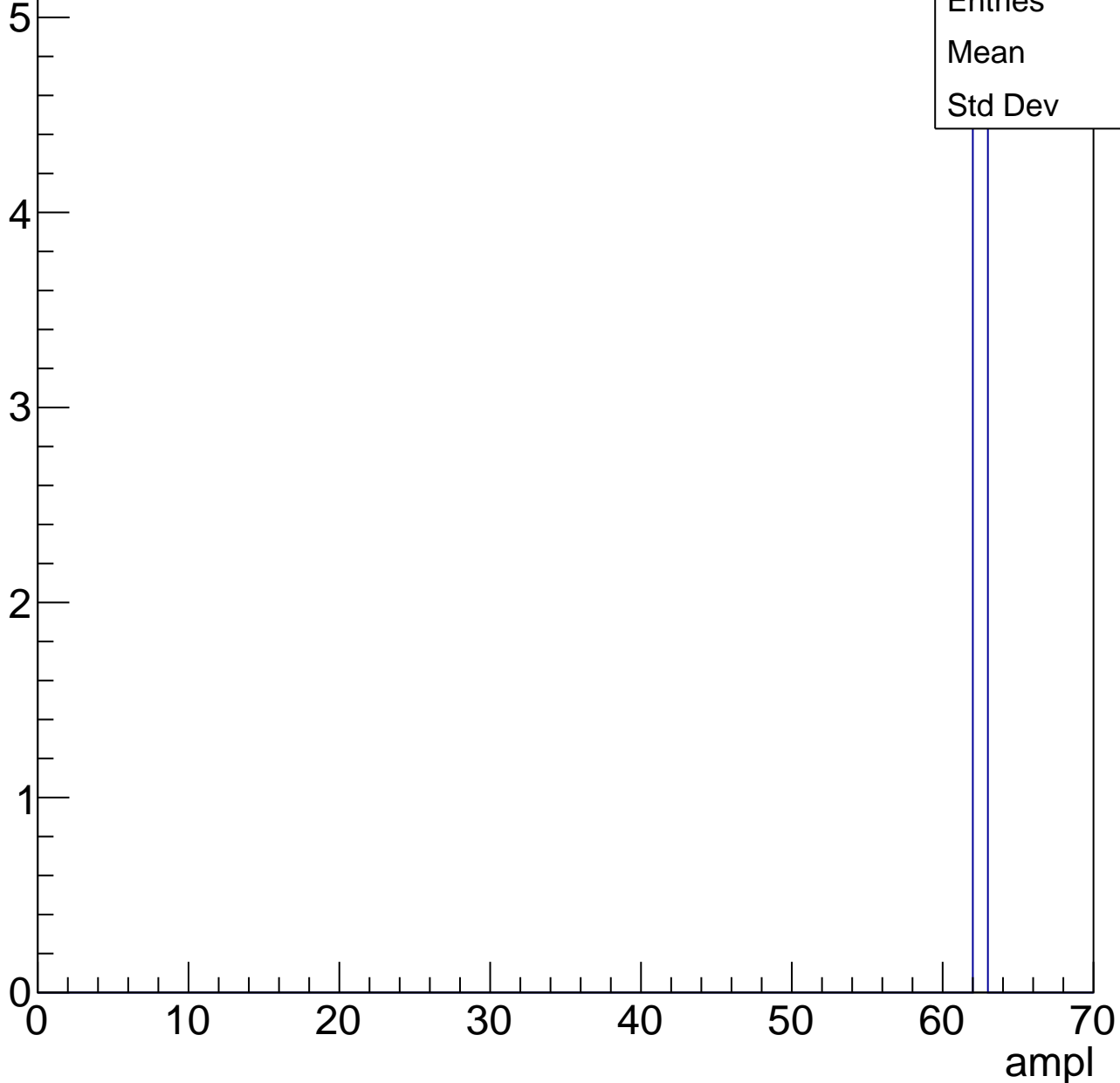


B0L000S, U4-ch13, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	5
Mean	62
Std Dev	0



B0L000S, U4-ch13, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch14, adc0

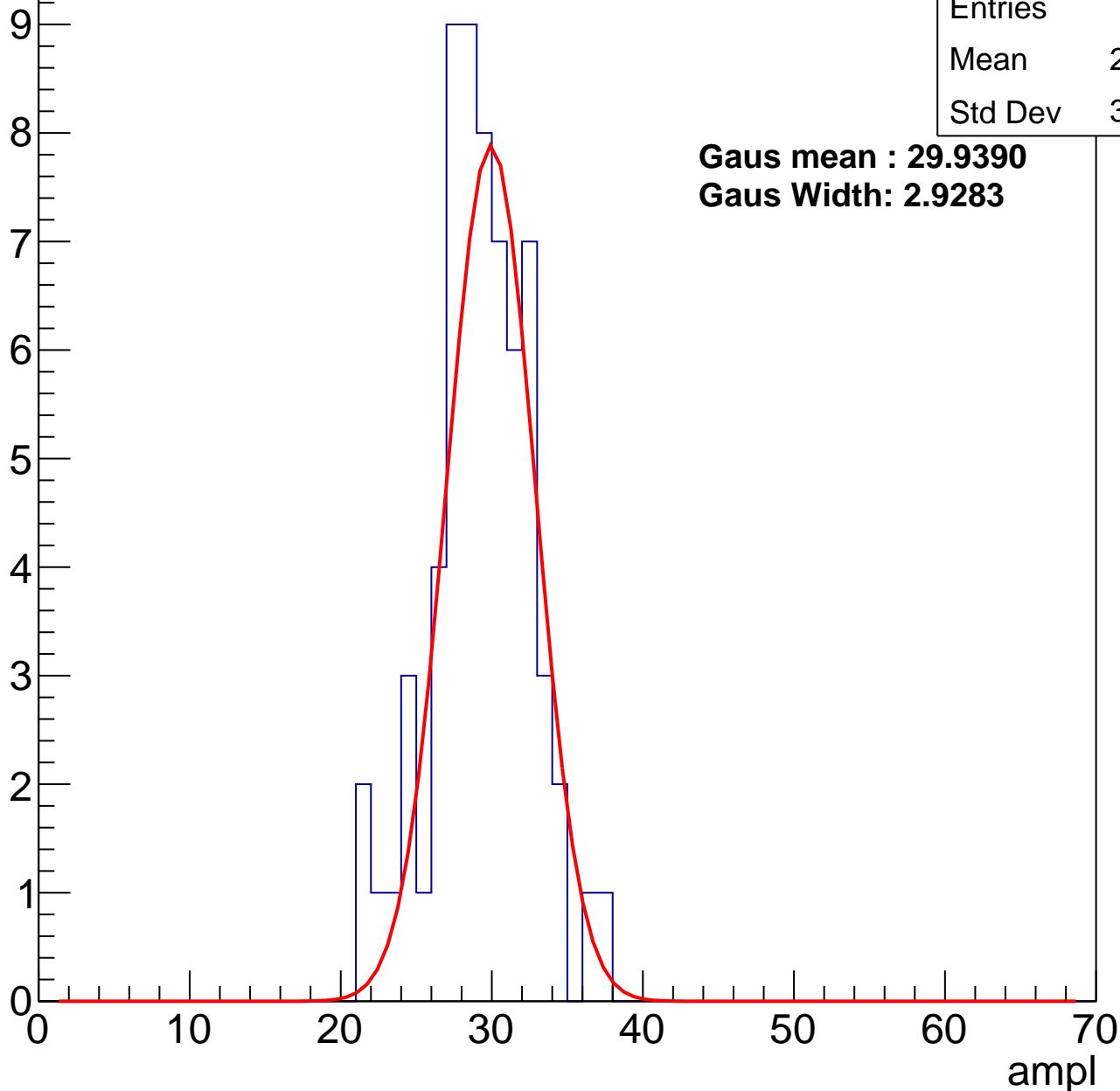
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	28.85
Std Dev	3.254

Gaus mean : 29.9390

Gaus Width: 2.9283



B0L000S, U4-ch14, adc1

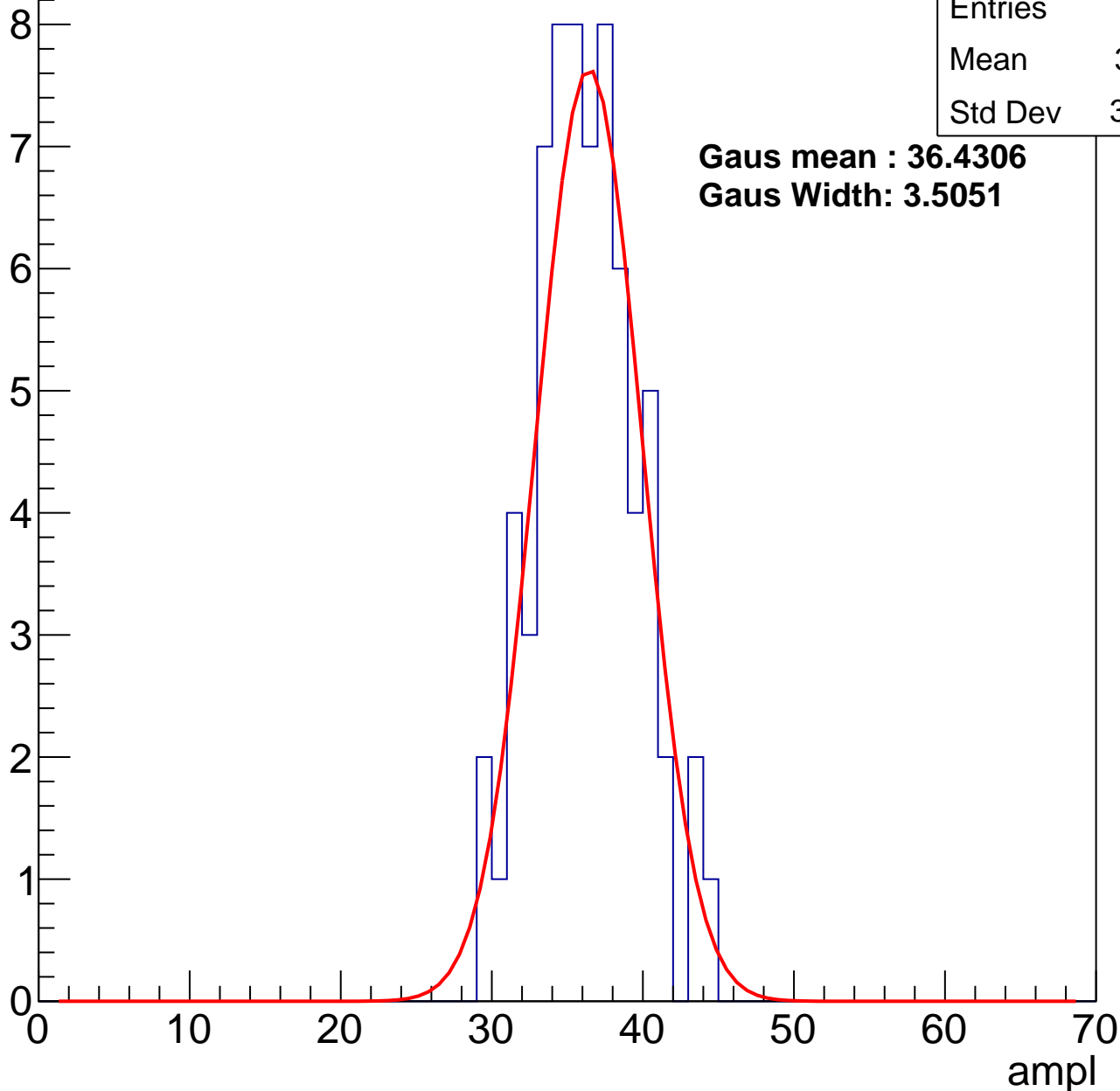
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	35.81
Std Dev	3.304

Gaus mean : 36.4306

Gaus Width: 3.5051



B0L000S, U4-ch14, adc2

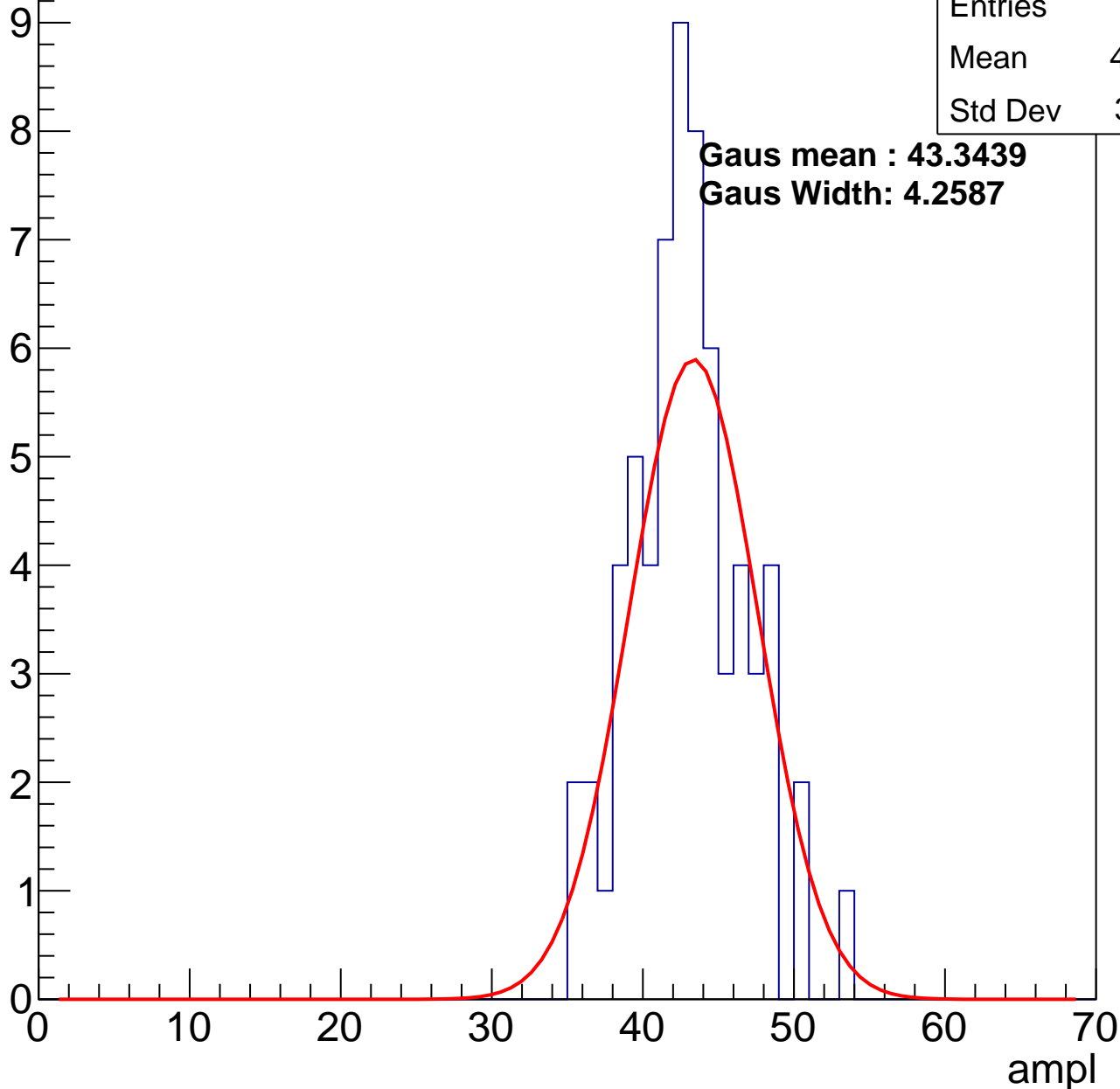
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.52
Std Dev	3.721

Gaus mean : 43.3439

Gaus Width: 4.2587

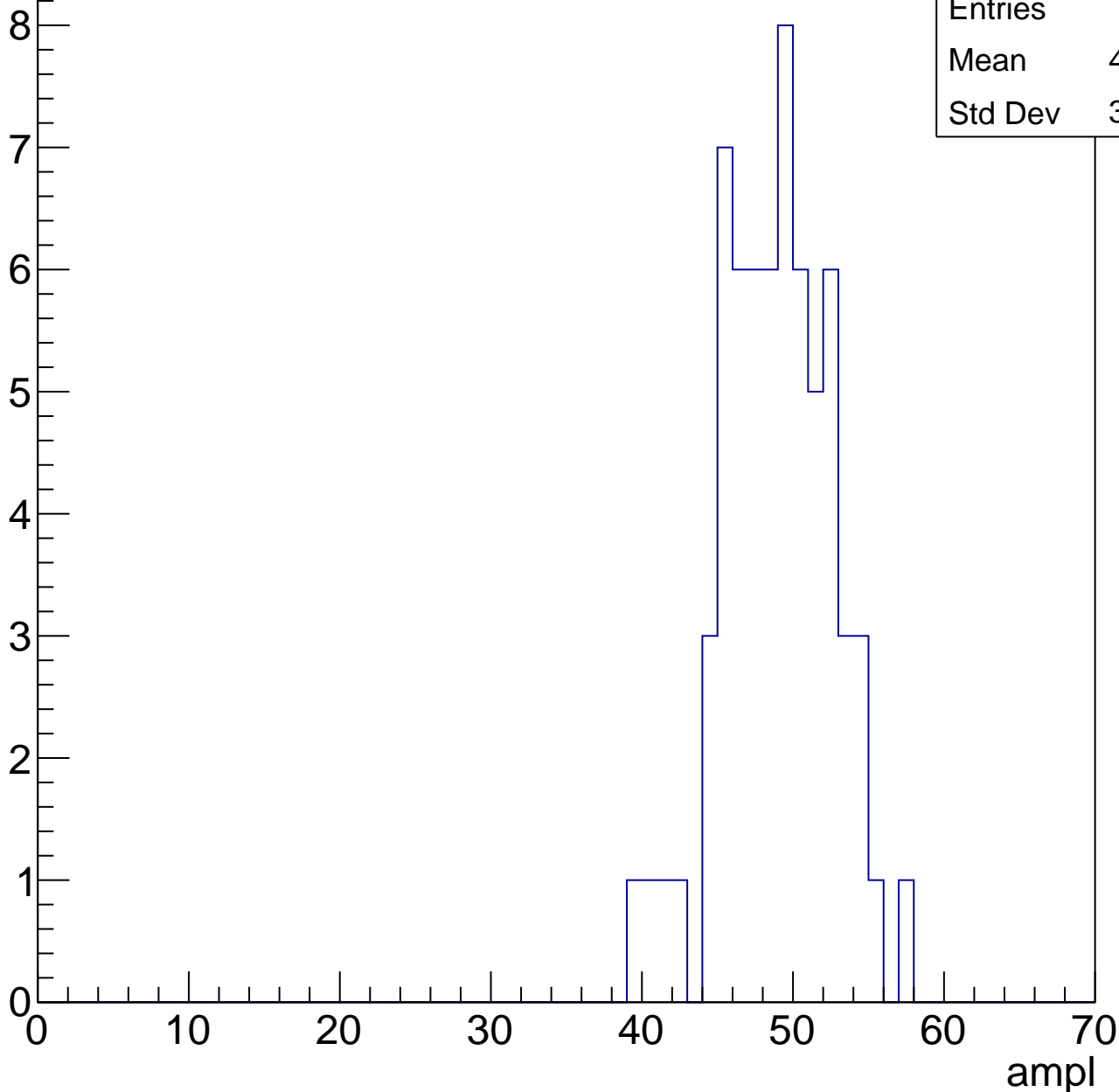


B0L000S, U4-ch14, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.42
Std Dev	3.607

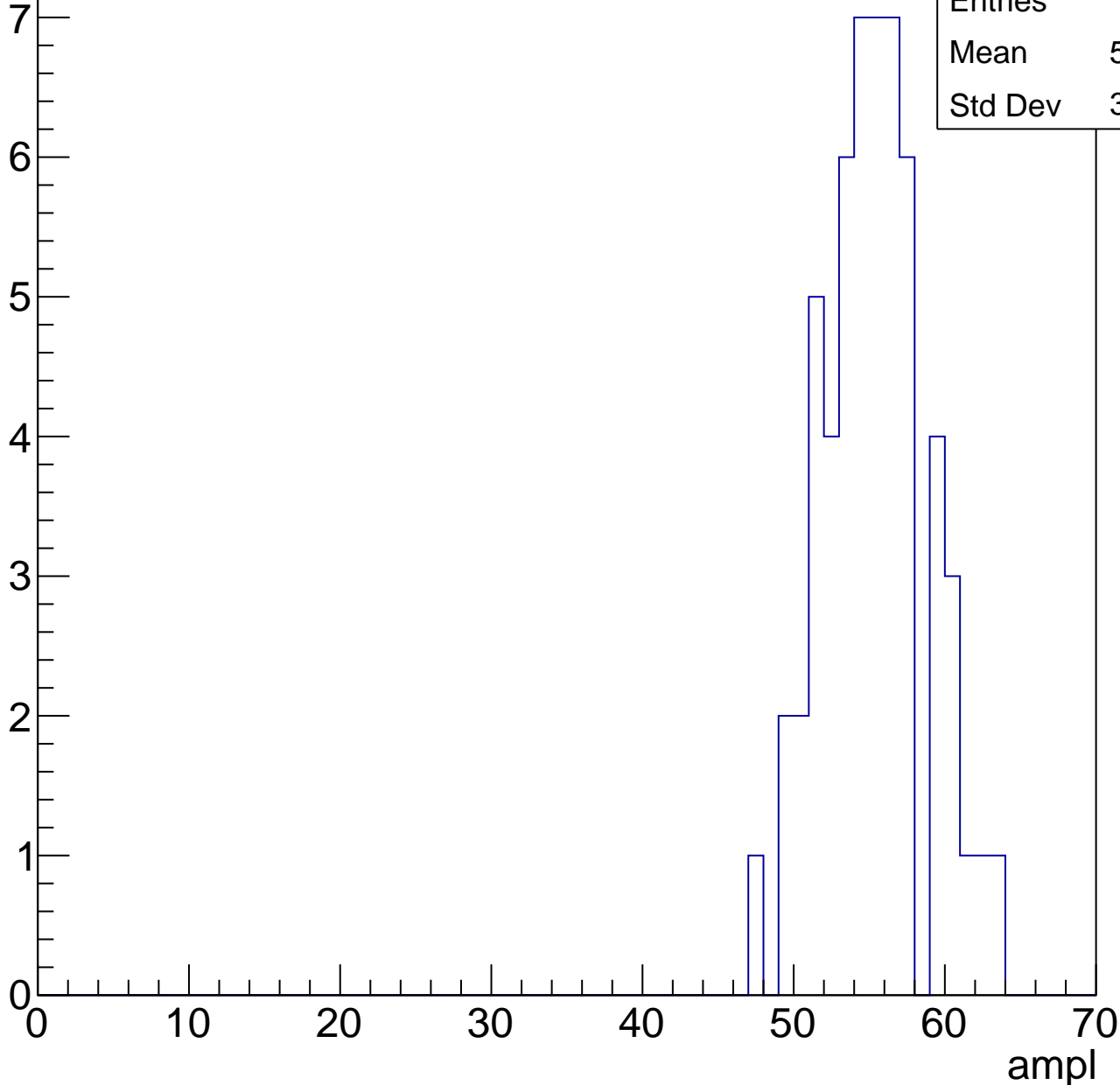


B0L000S, U4-ch14, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	54.82
Std Dev	3.378

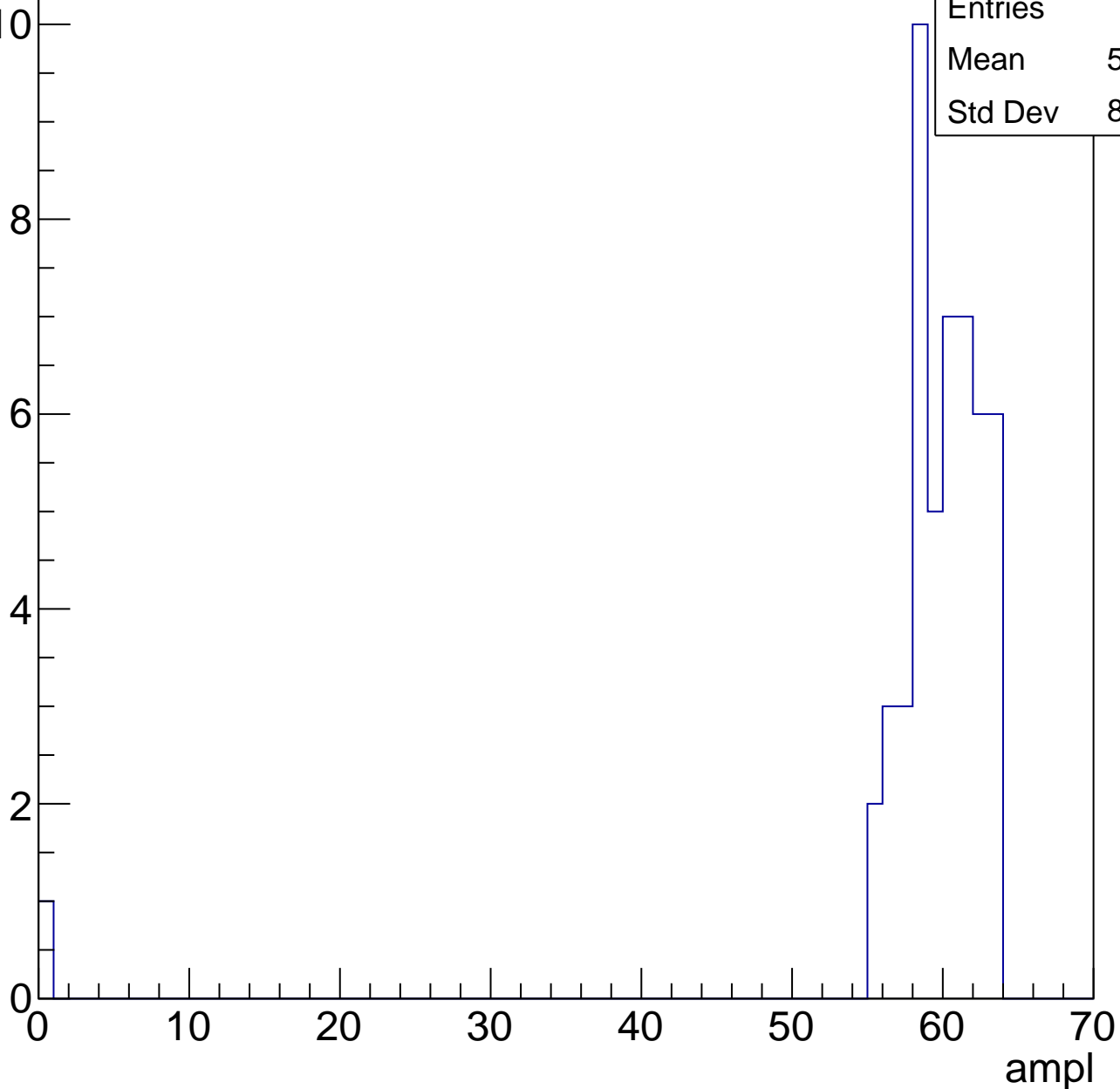


B0L000S, U4-ch14, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

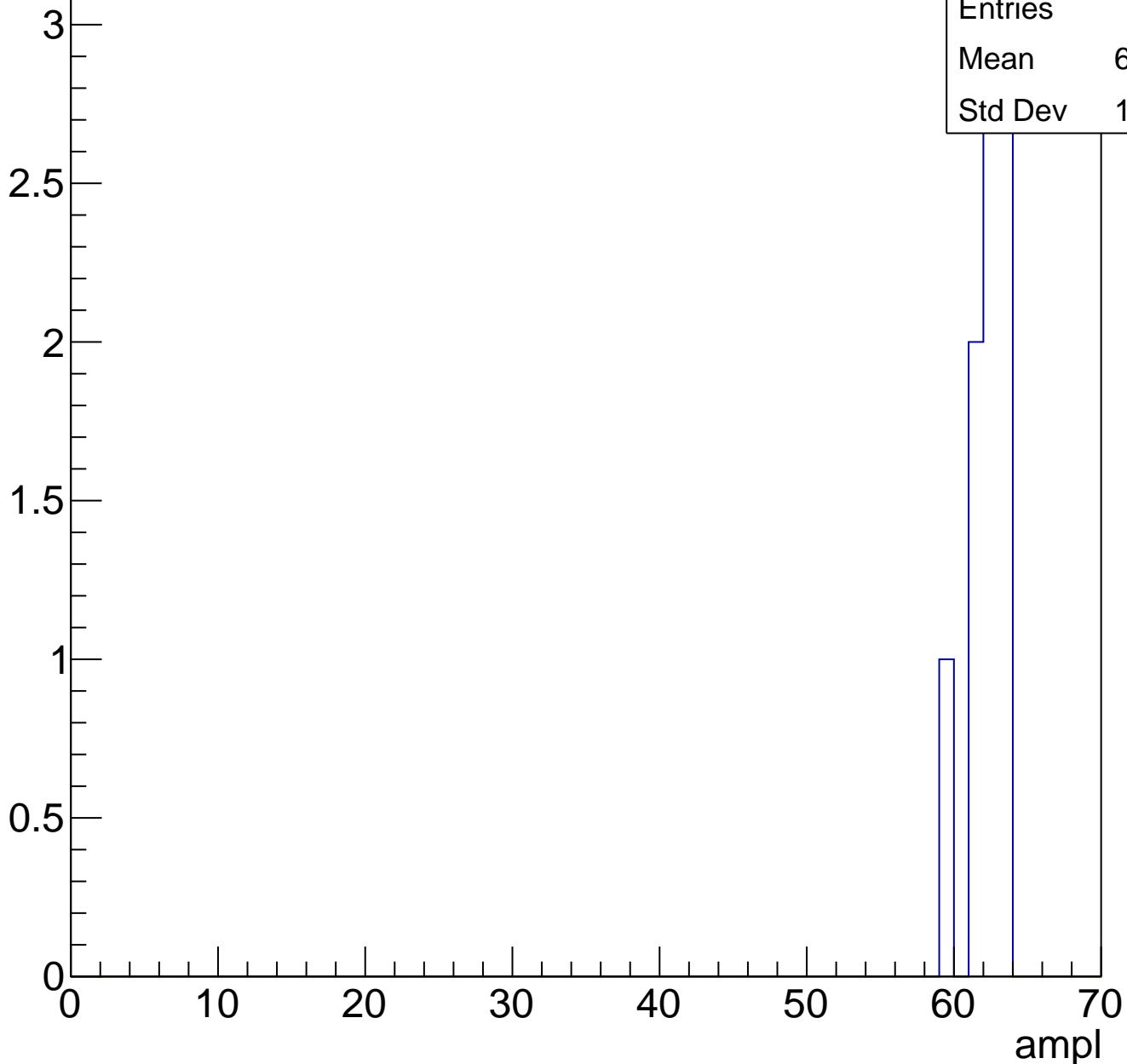
Entries	50
Mean	58.42
Std Dev	8.637



B0L000S, U4-ch14, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch14, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch15, adc0

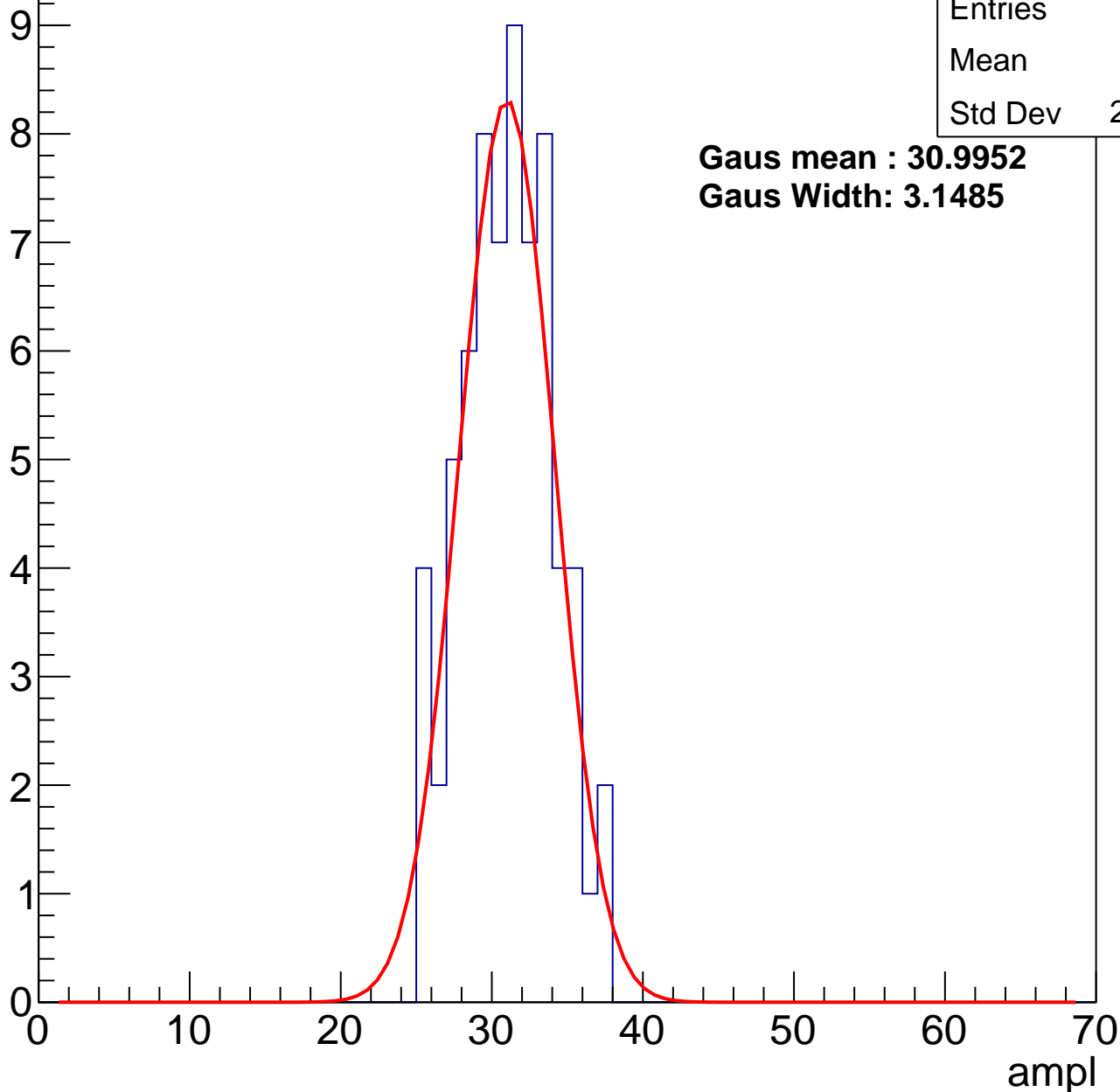
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.6
Std Dev	2.973

Gaus mean : 30.9952

Gaus Width: 3.1485



B0L000S, U4-ch15, adc1

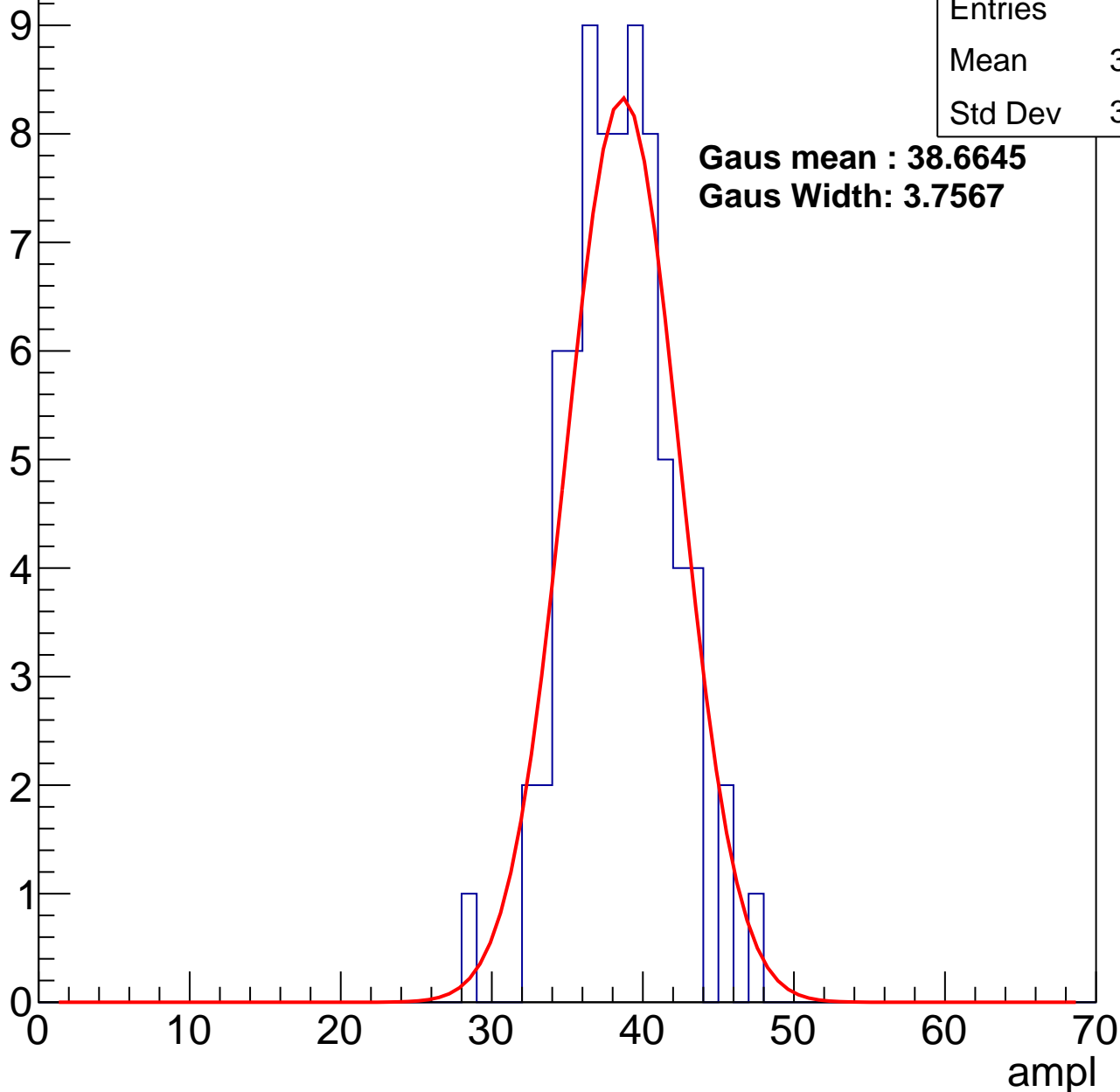
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.99
Std Dev	3.357

Gaus mean : 38.6645

Gaus Width: 3.7567



B0L000S, U4-ch15, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	65
Mean	44.77
Std Dev	3.2

Gaus mean : 45.0391

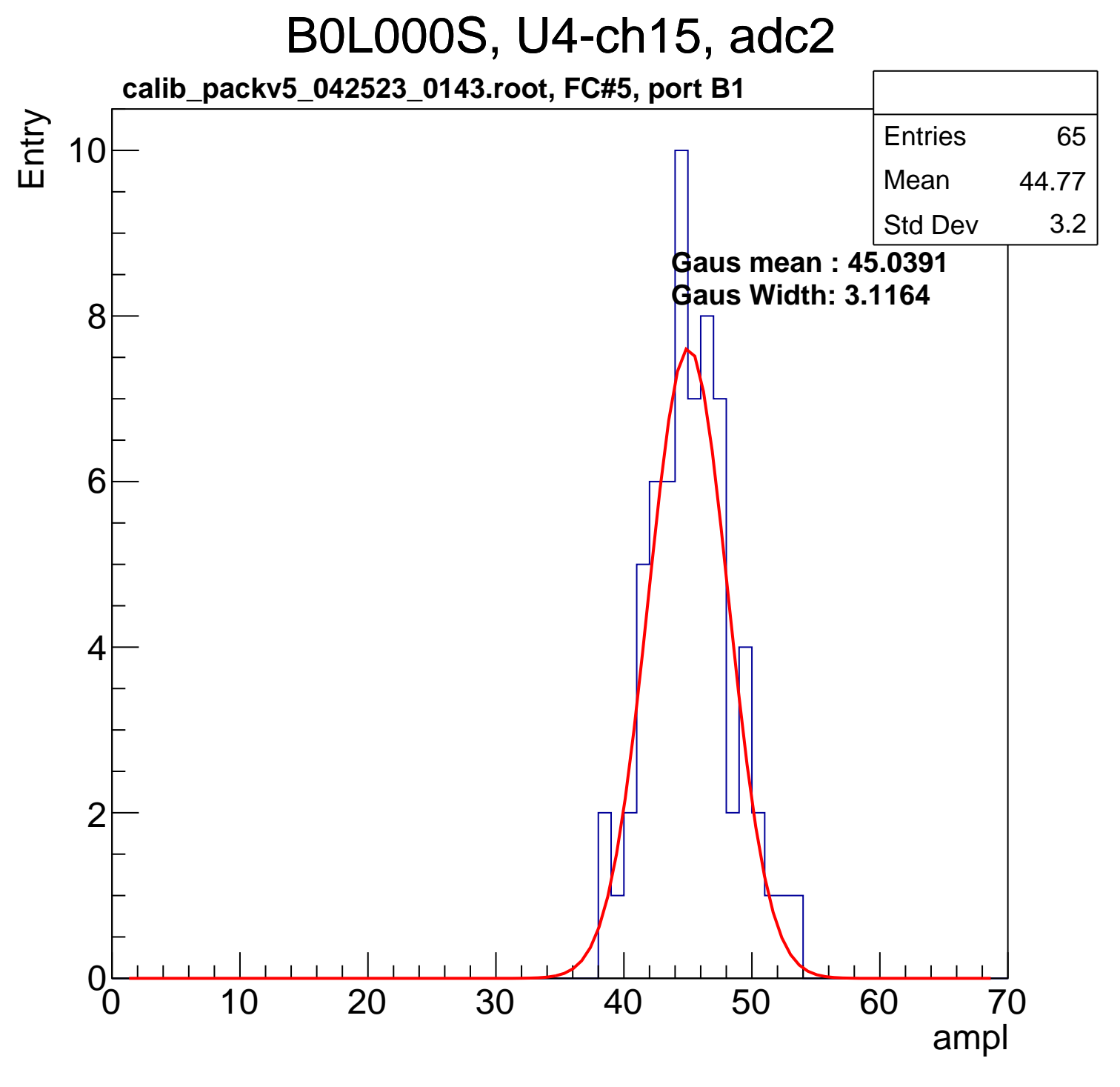
Gaus Width: 3.1164

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

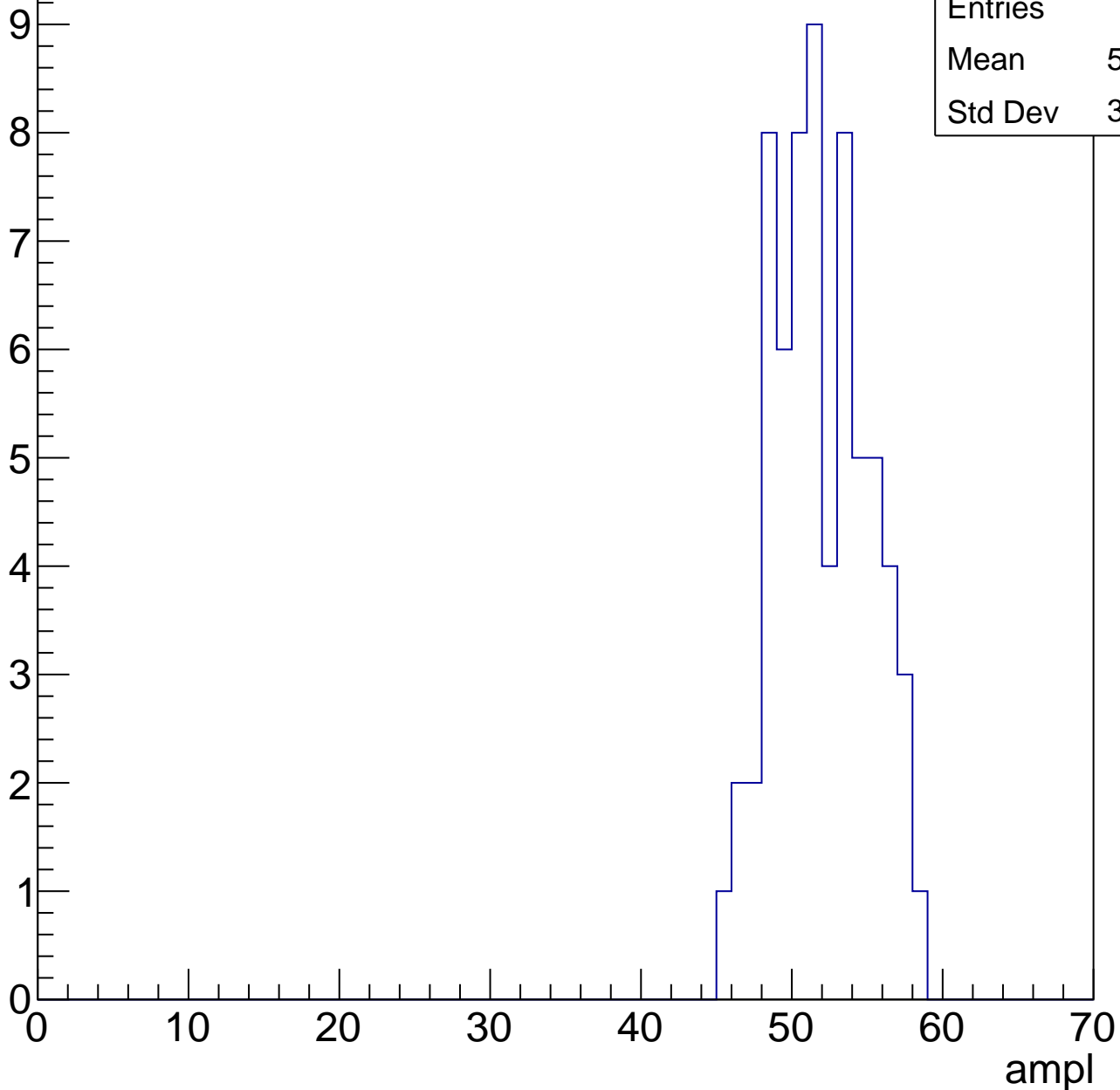


B0L000S, U4-ch15, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	51.48
Std Dev	3.076

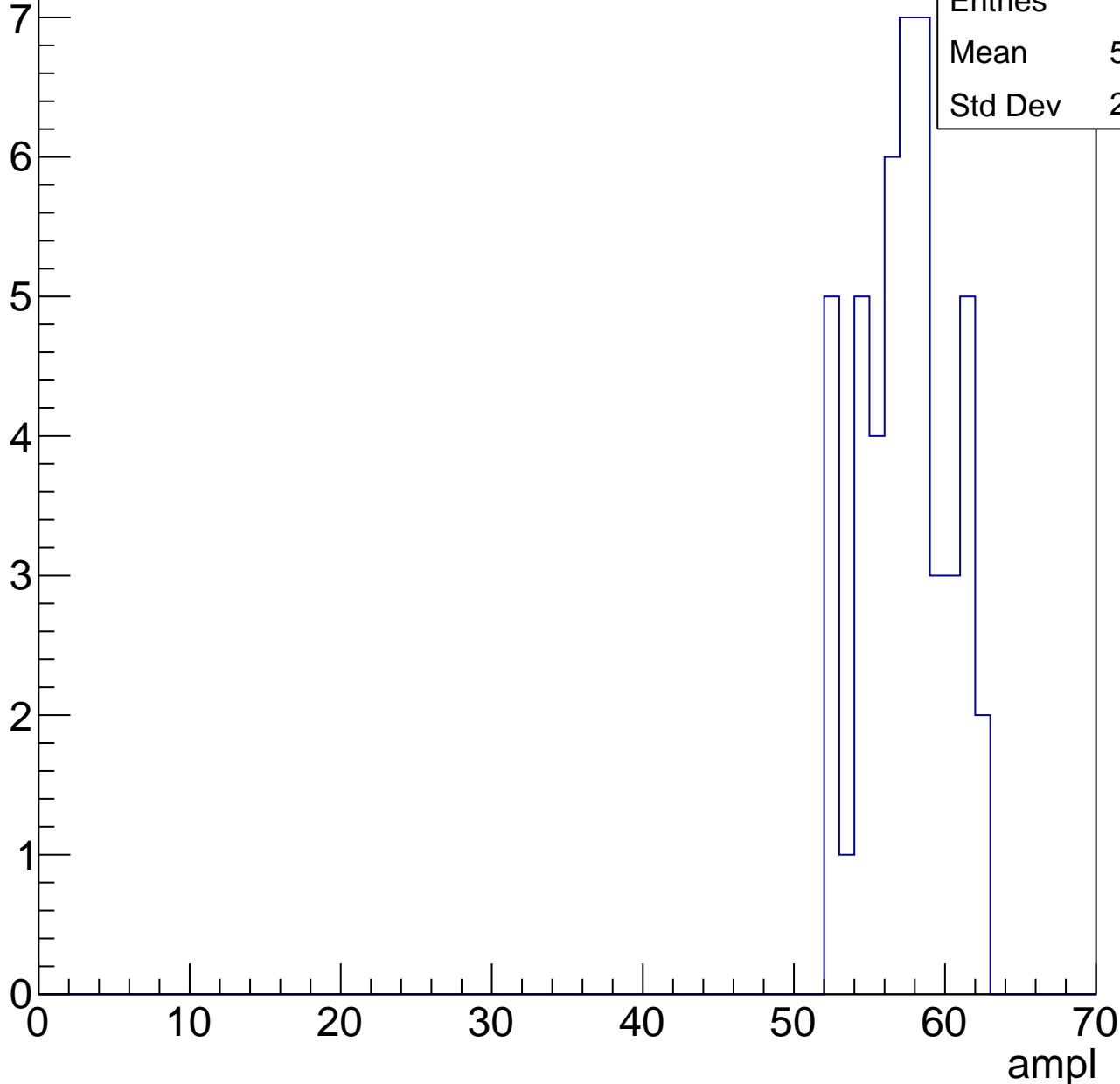


B0L000S, U4-ch15, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	56.88
Std Dev	2.826



B0L000S, U4-ch15, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

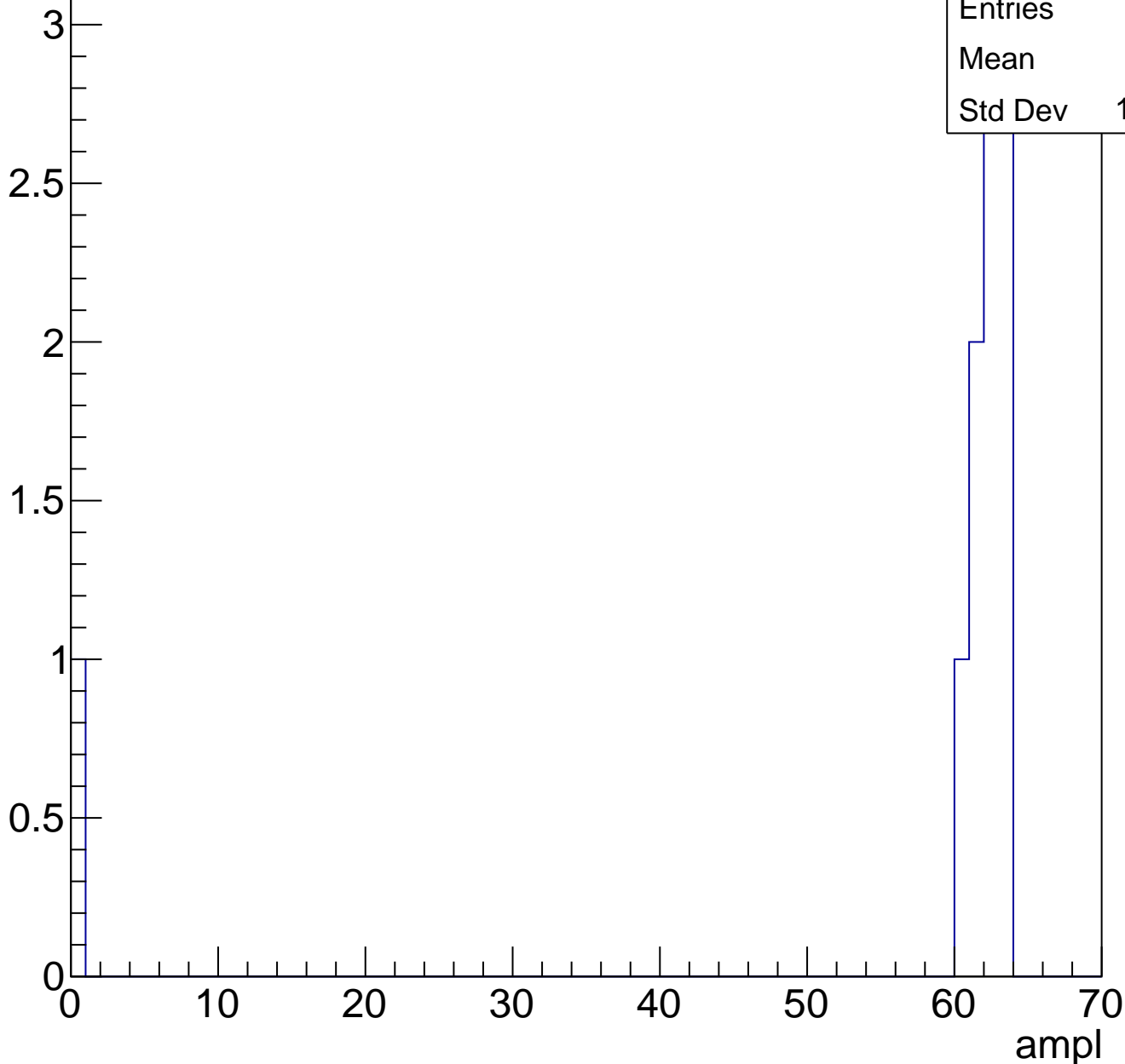
ampl

Entries	31
Mean	60.58
Std Dev	1.931

B0L000S, U4-ch15, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch15, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L000S, U4-ch16, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	77
Mean	28.82
Std Dev	3.751

Gaus mean : 29.5028

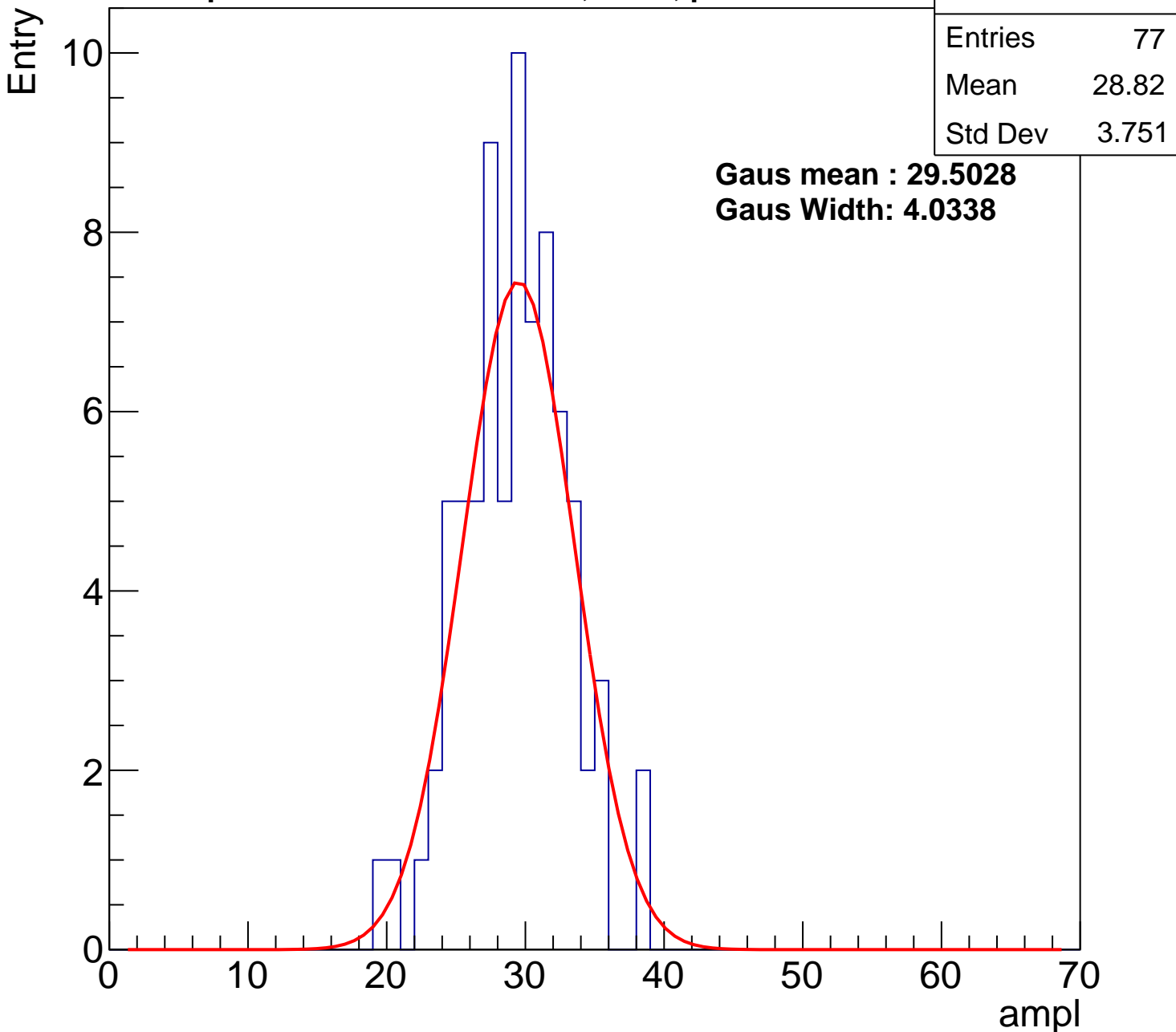
Gaus Width: 4.0338

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch16, adc1

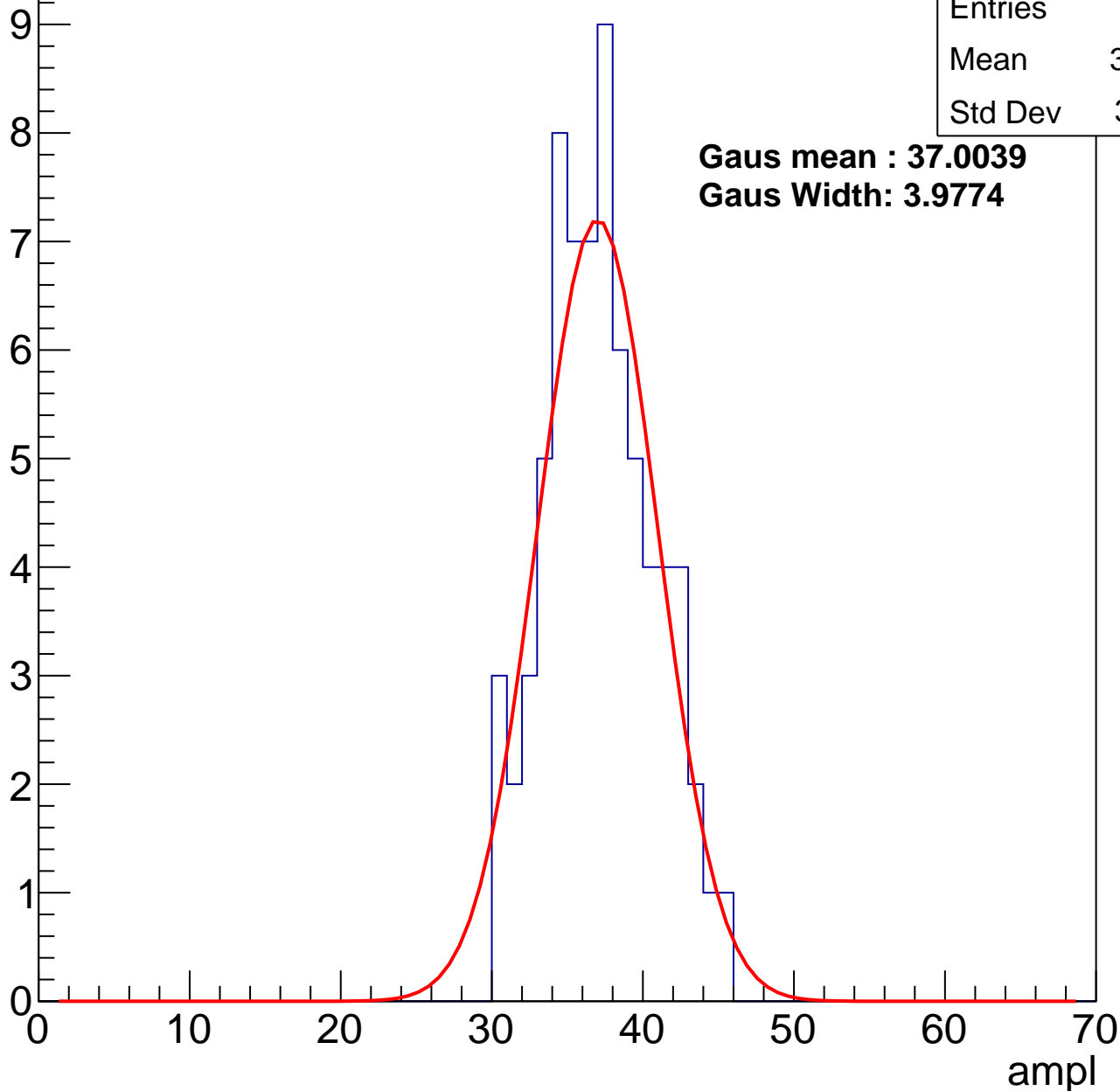
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.69
Std Dev	3.531

Gaus mean : 37.0039

Gaus Width: 3.9774



B0L000S, U4-ch16, adc2

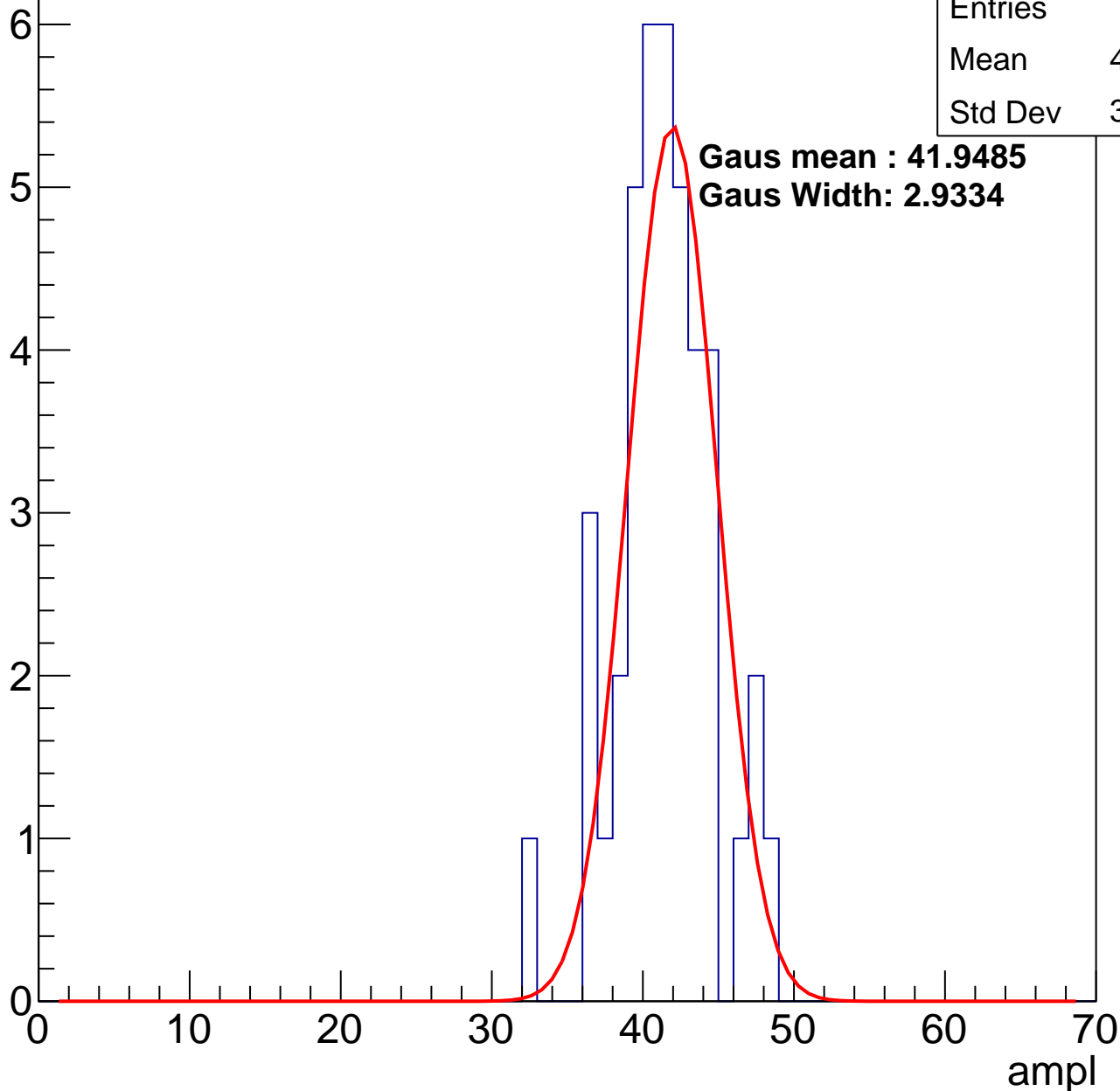
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	41
Mean	40.98
Std Dev	3.197

Gaus mean : 41.9485

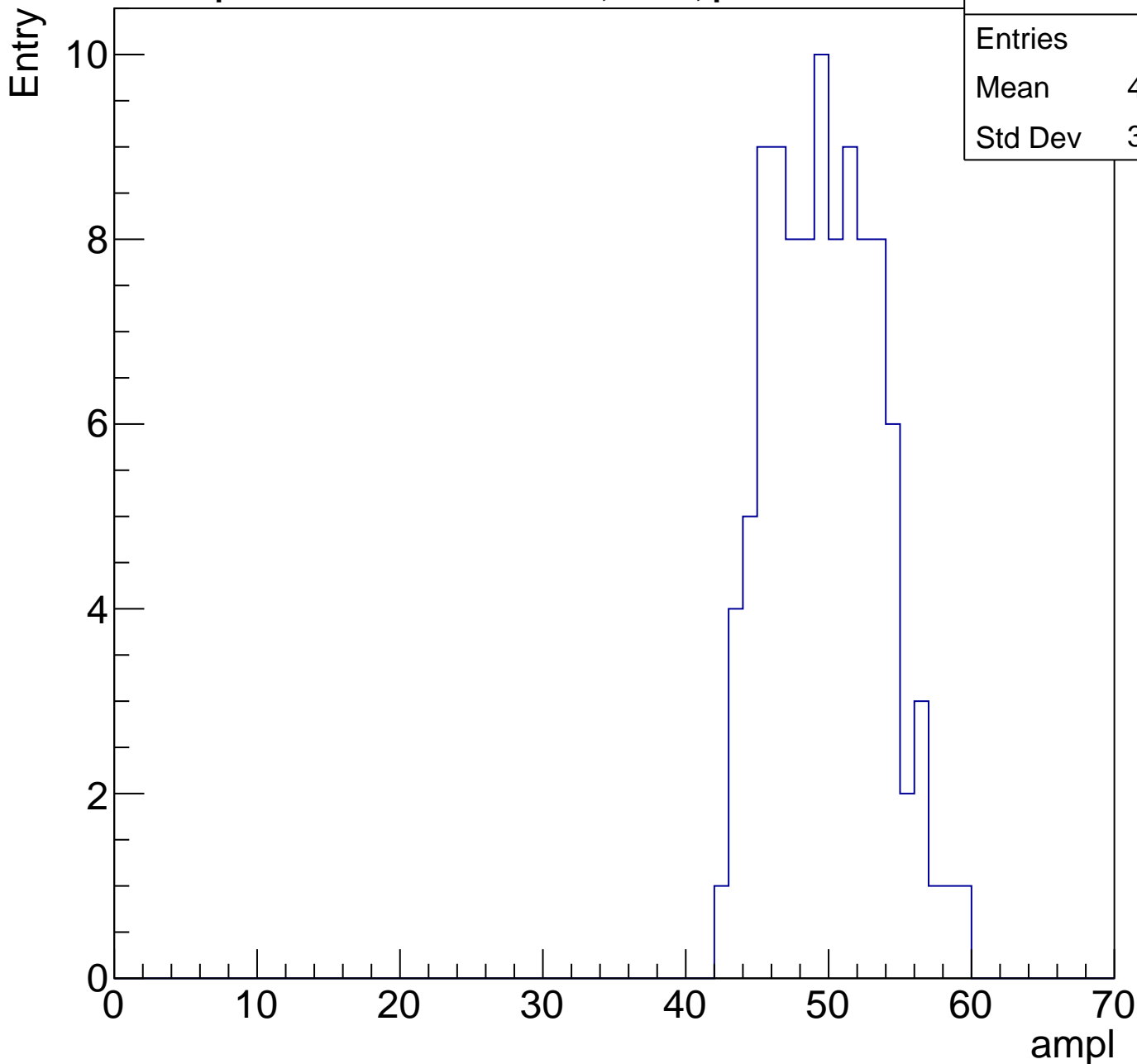
Gaus Width: 2.9334



B0L000S, U4-ch16, adc3

calib_packv5_042523_0143.root, FC#5, port B1

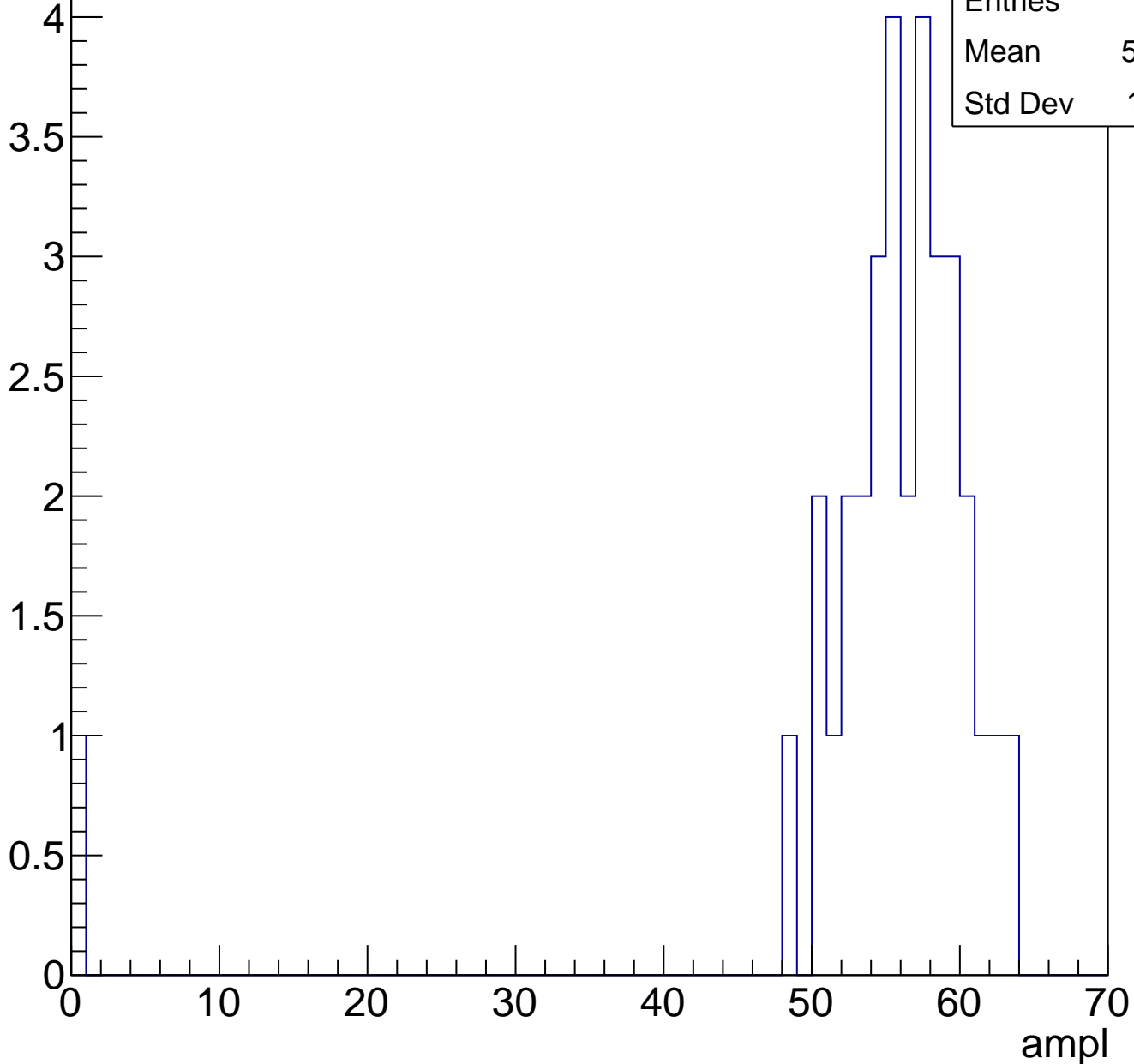
Entries	101
Mean	49.29
Std Dev	3.766



B0L000S, U4-ch16, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

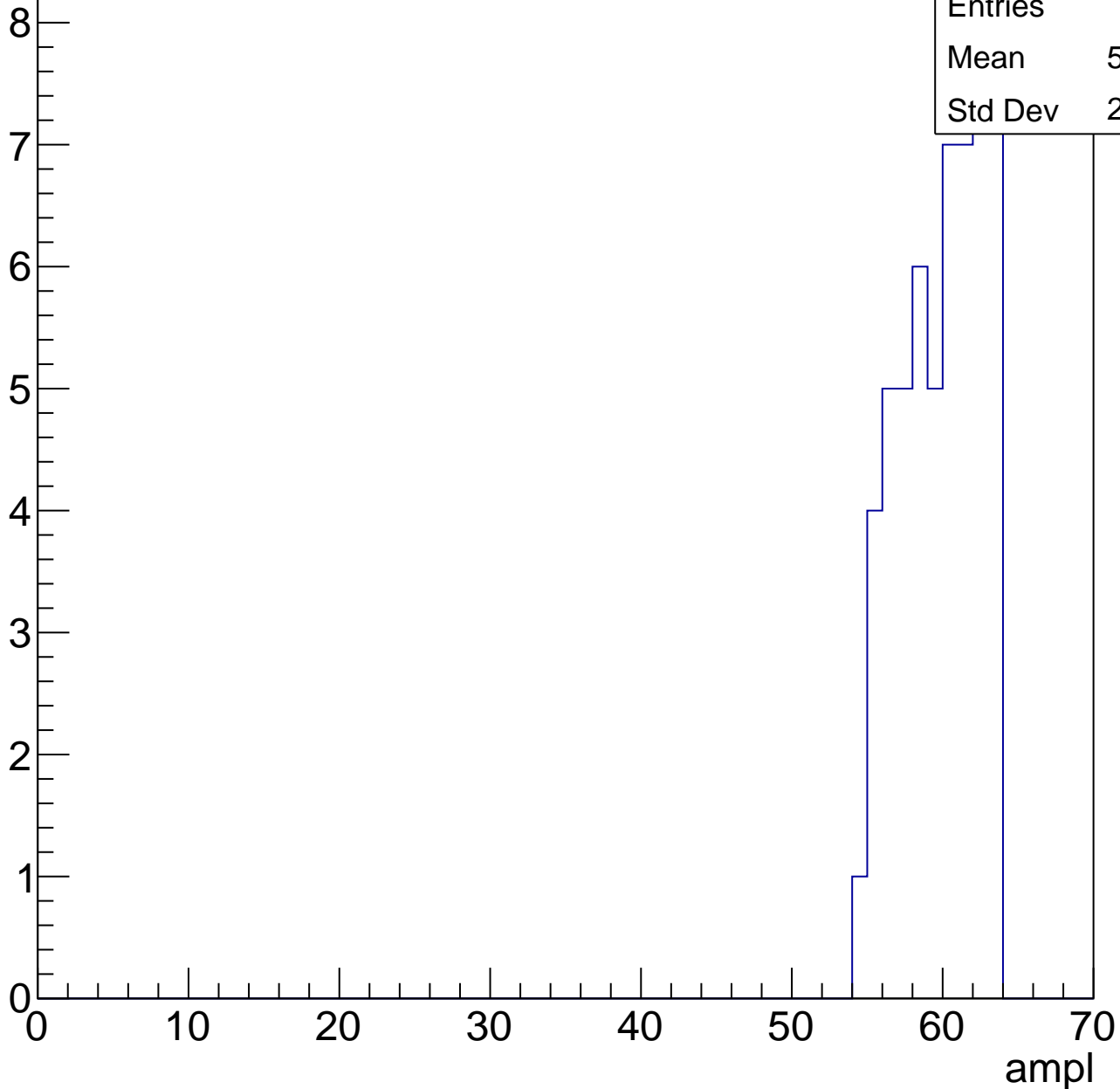


B0L000S, U4-ch16, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

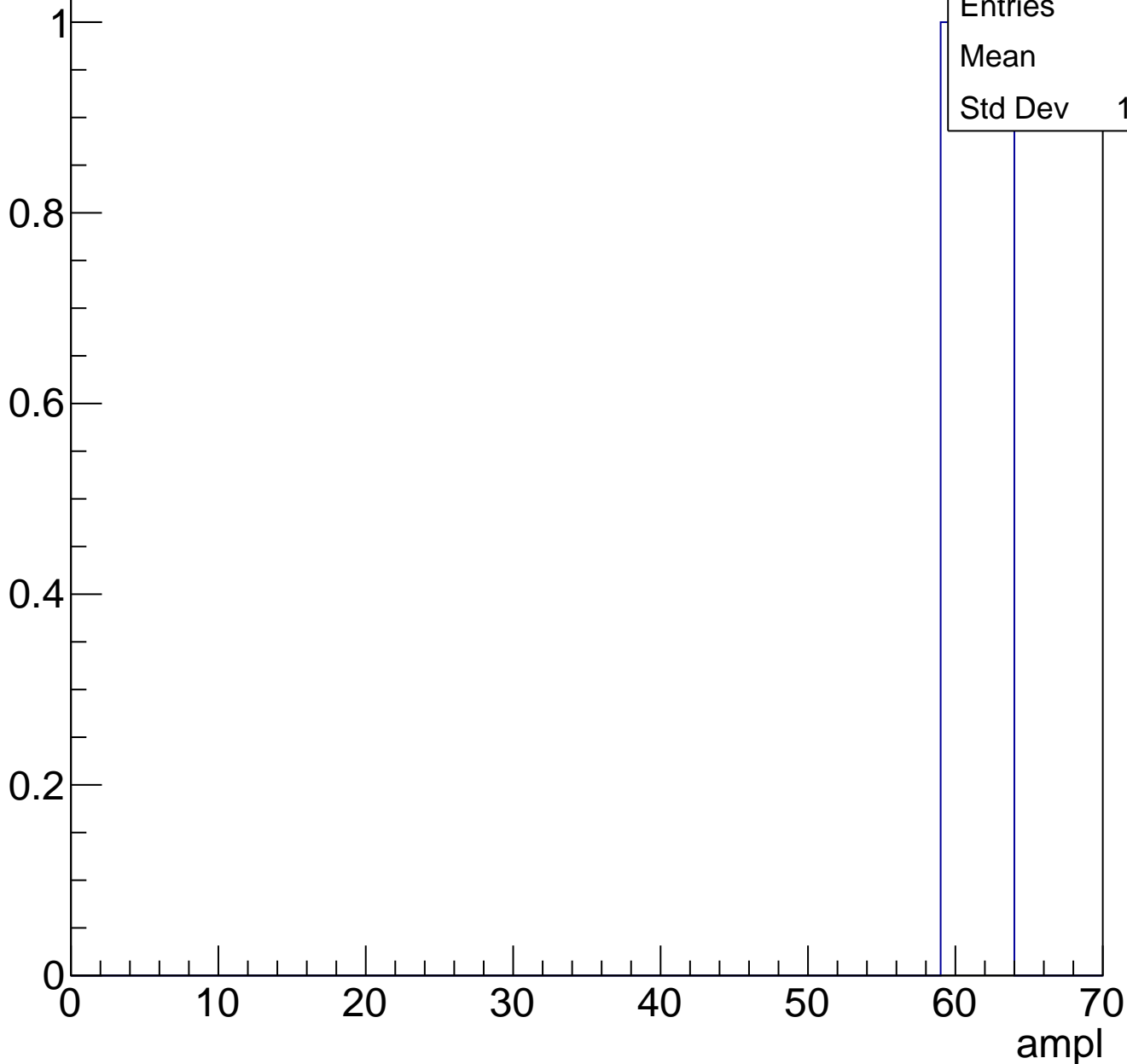
Entries	56
Mean	59.45
Std Dev	2.618



B0L000S, U4-ch16, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch16, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch17, adc0

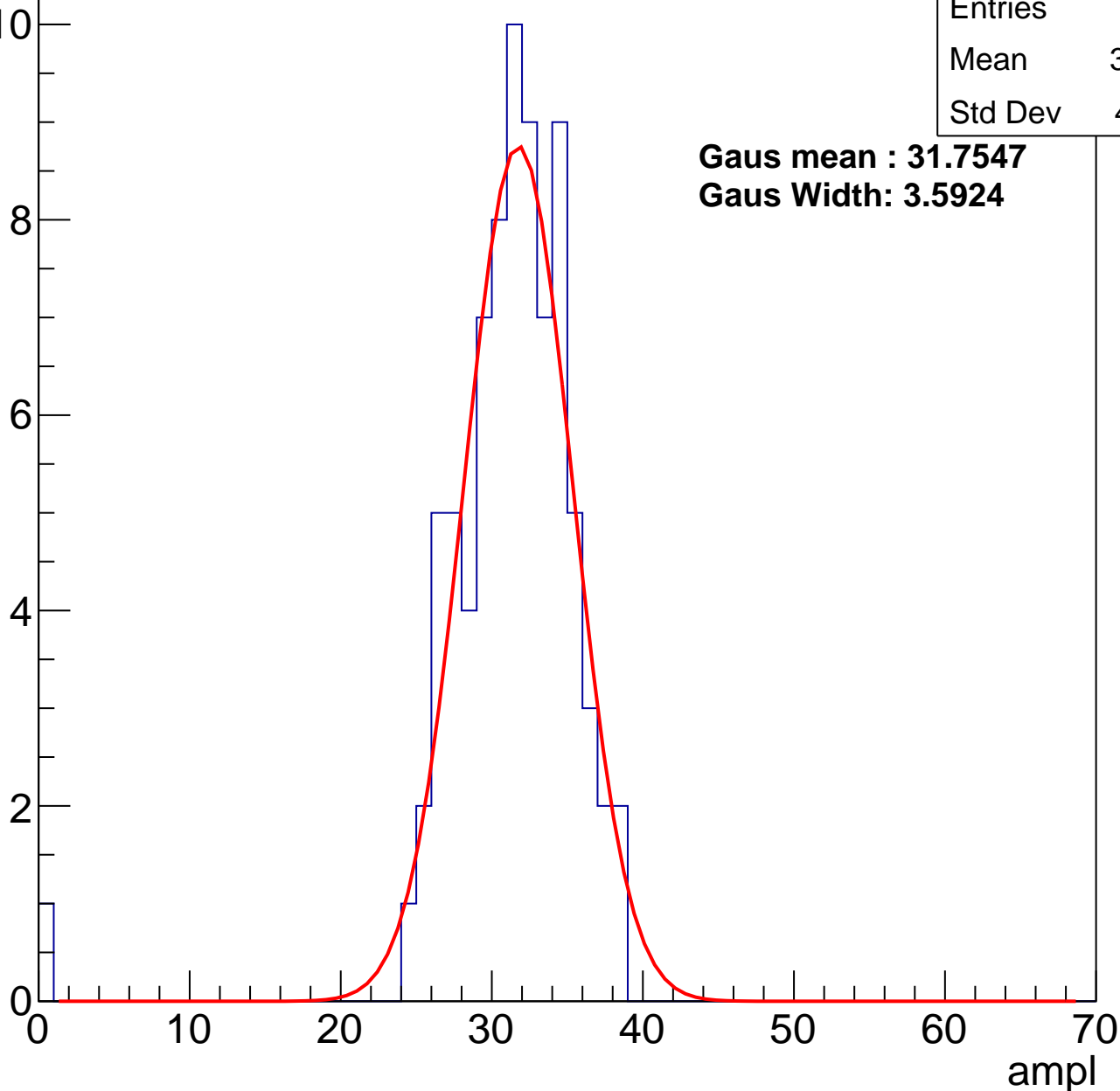
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	30.77
Std Dev	4.741

Gaus mean : 31.7547

Gaus Width: 3.5924



B0L000S, U4-ch17, adc1

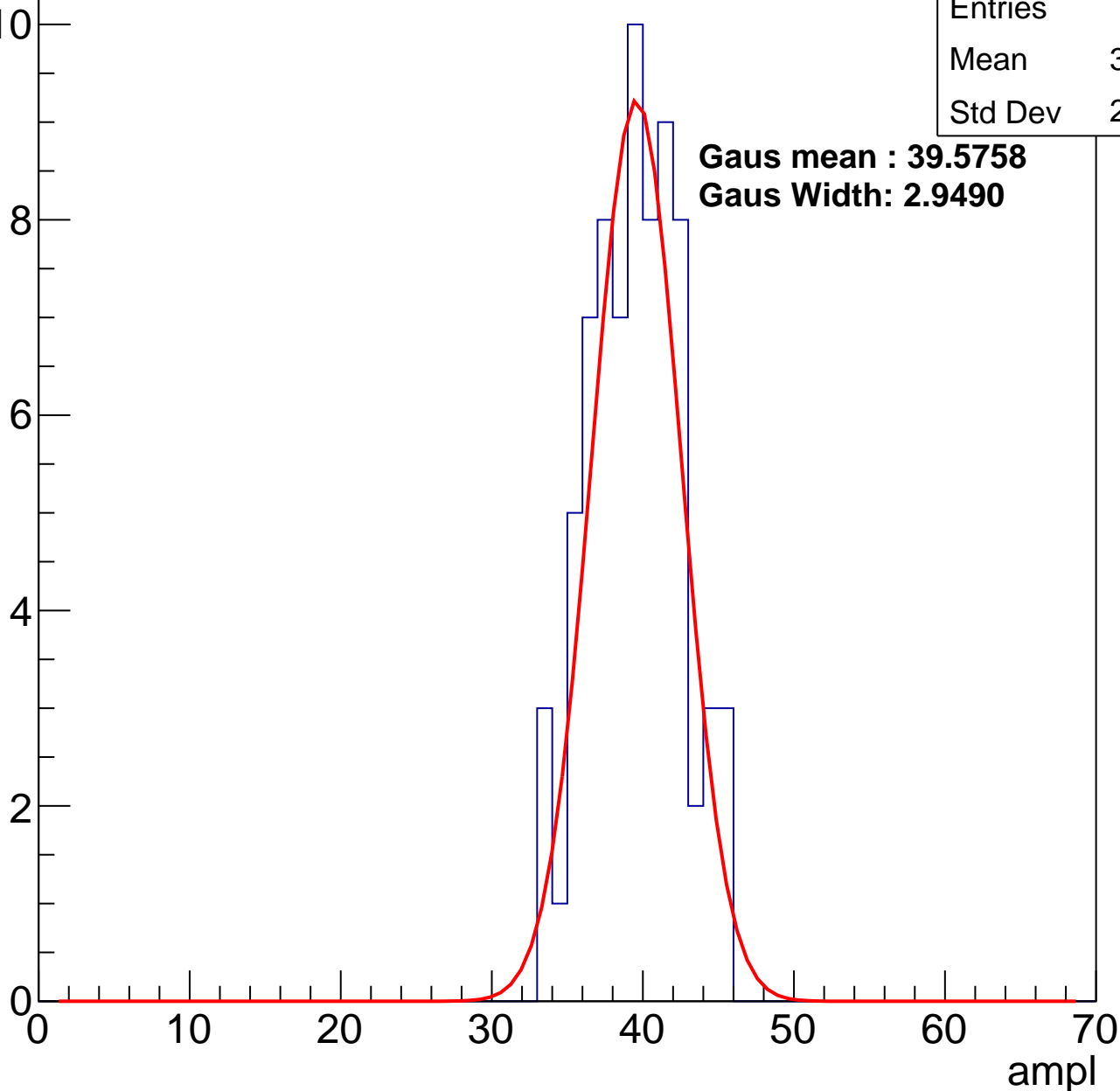
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	39.05
Std Dev	2.954

Gaus mean : 39.5758

Gaus Width: 2.9490



B0L000S, U4-ch17, adc2

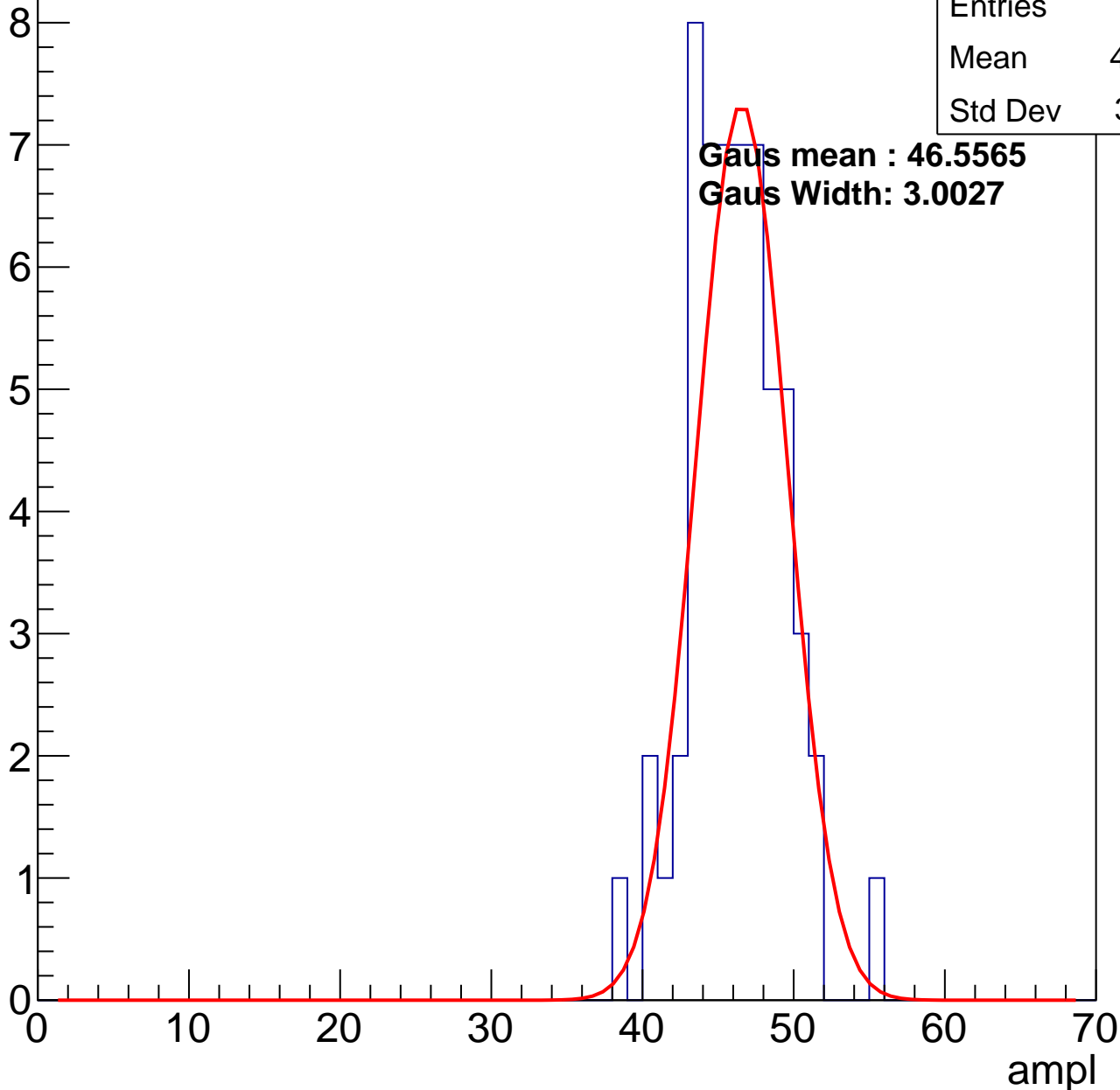
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	45.74
Std Dev	3.071

Gaus mean : 46.5565

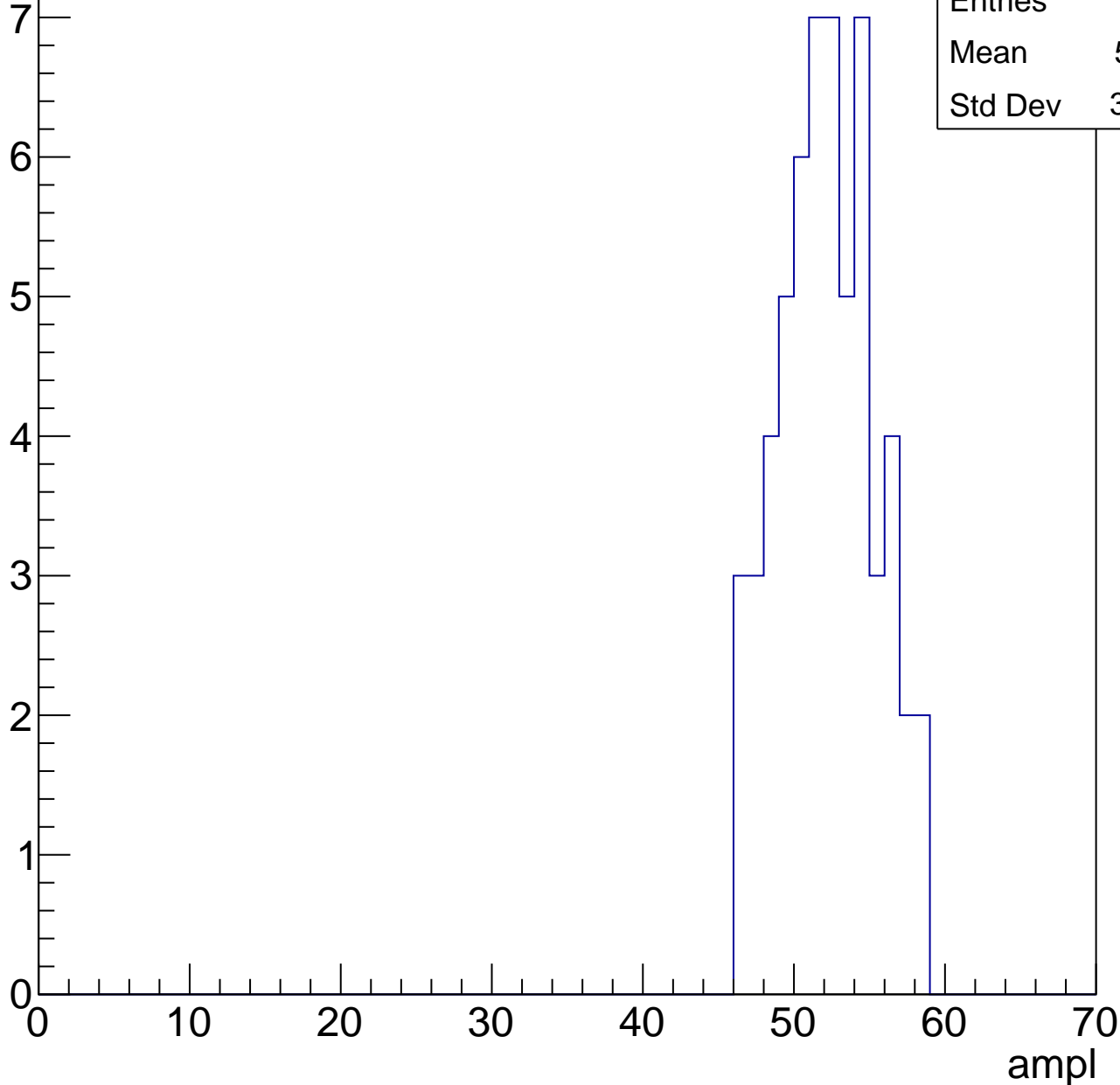
Gaus Width: 3.0027



B0L000S, U4-ch17, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

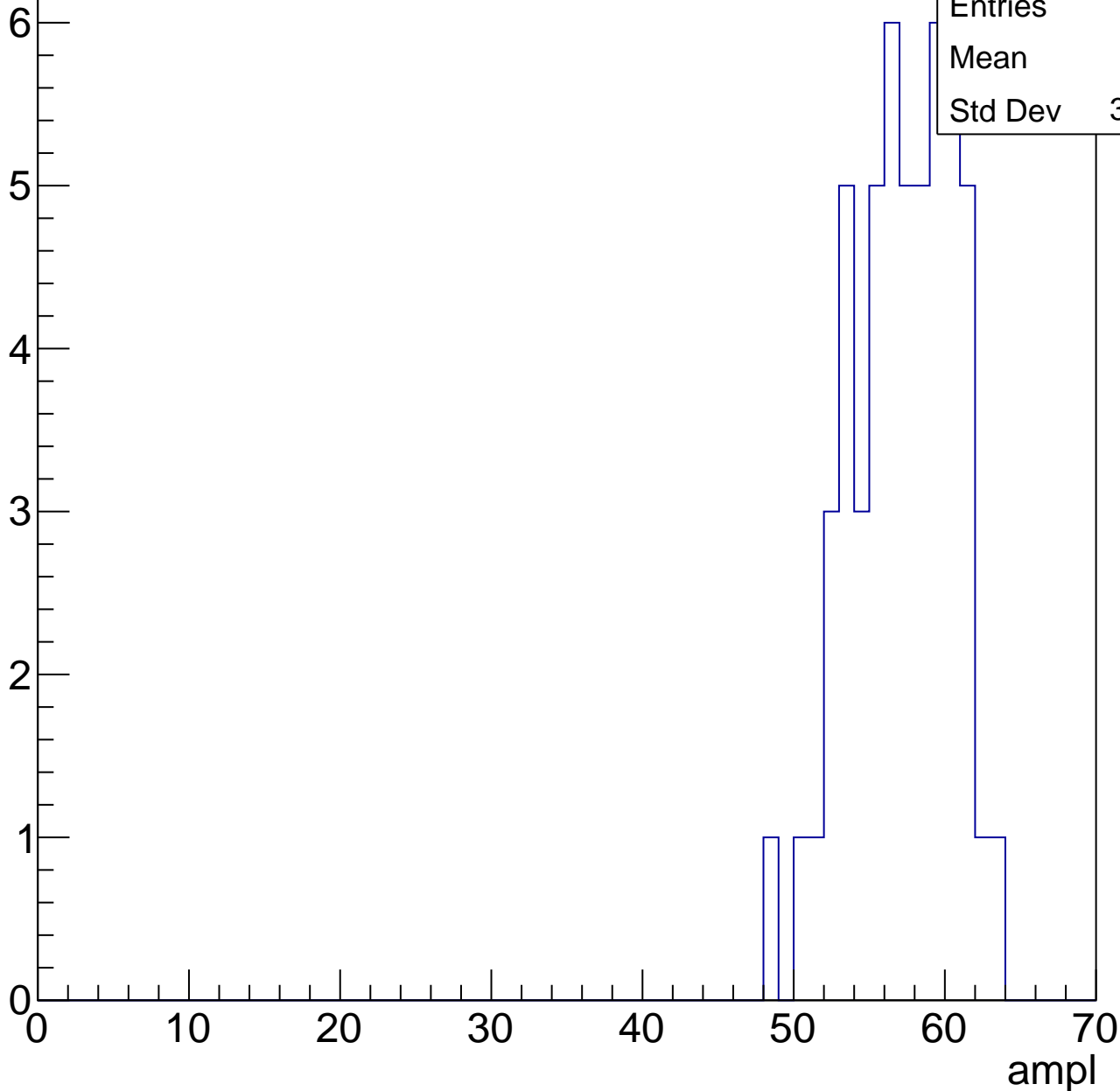


B0L000S, U4-ch17, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	56.7
Std Dev	3.315

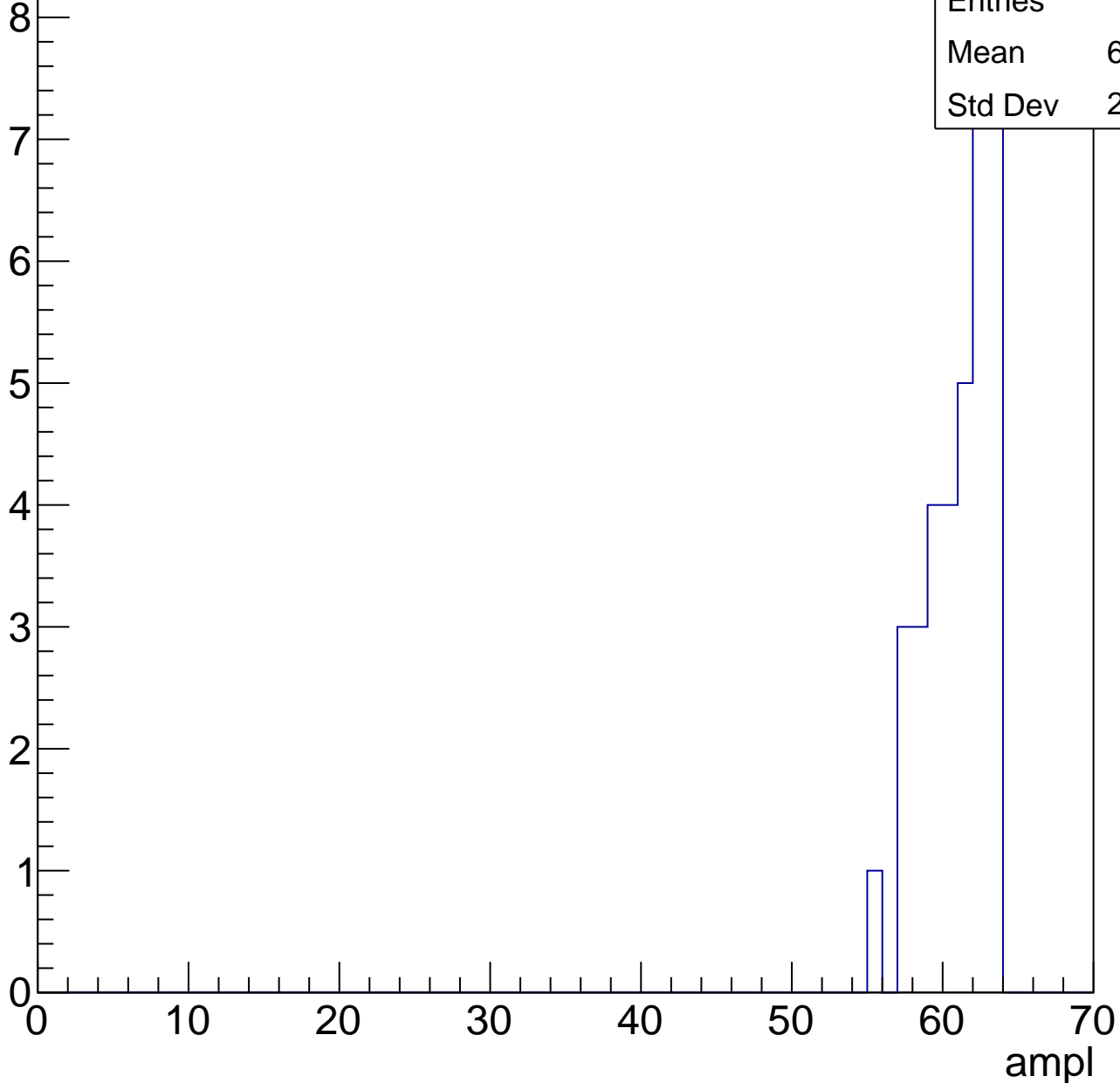


B0L000S, U4-ch17, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

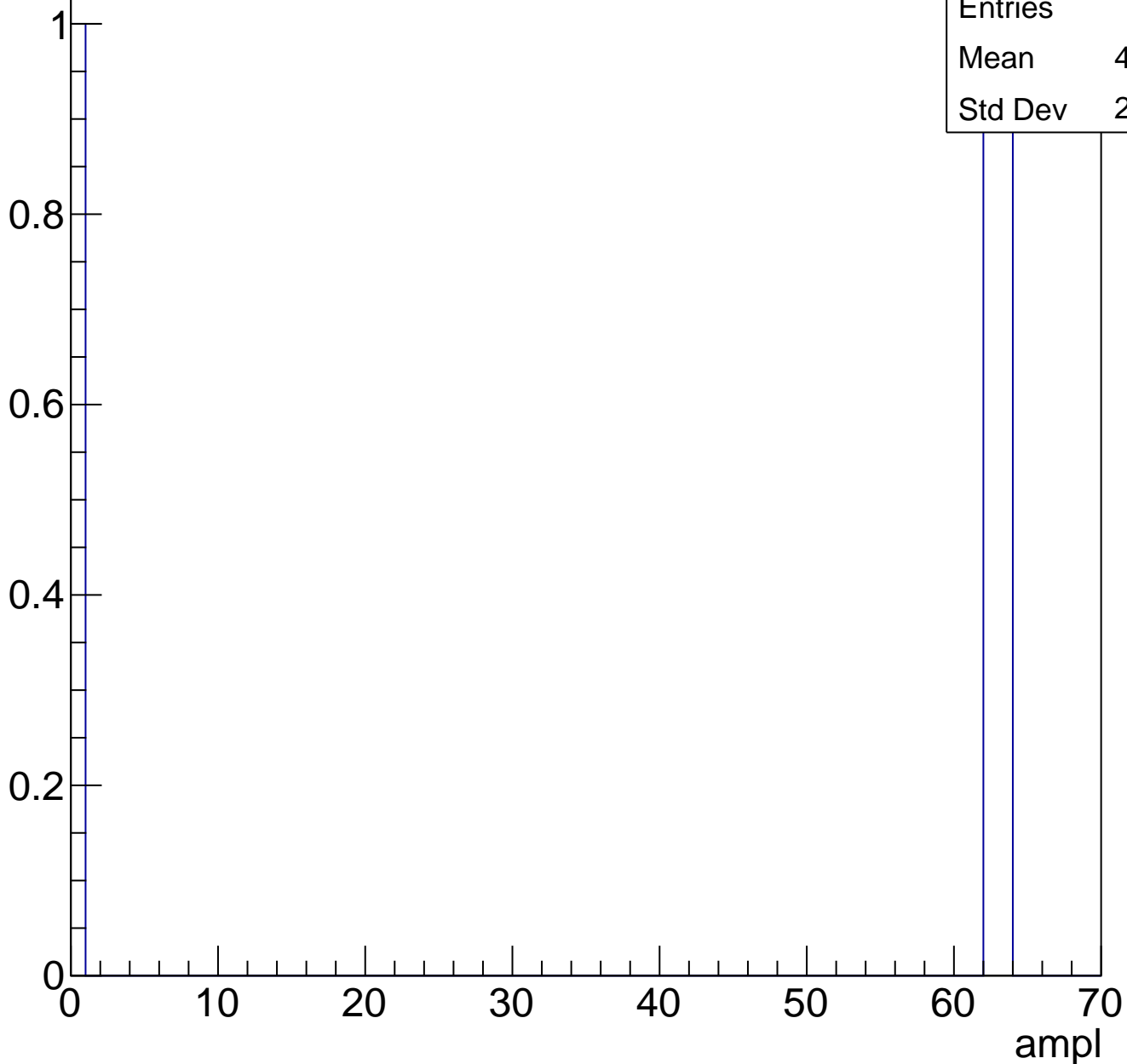
Entries	36
Mean	60.58
Std Dev	2.139



B0L000S, U4-ch17, adc6

calib_packv5_042523_0143.root, FC#5, port B1

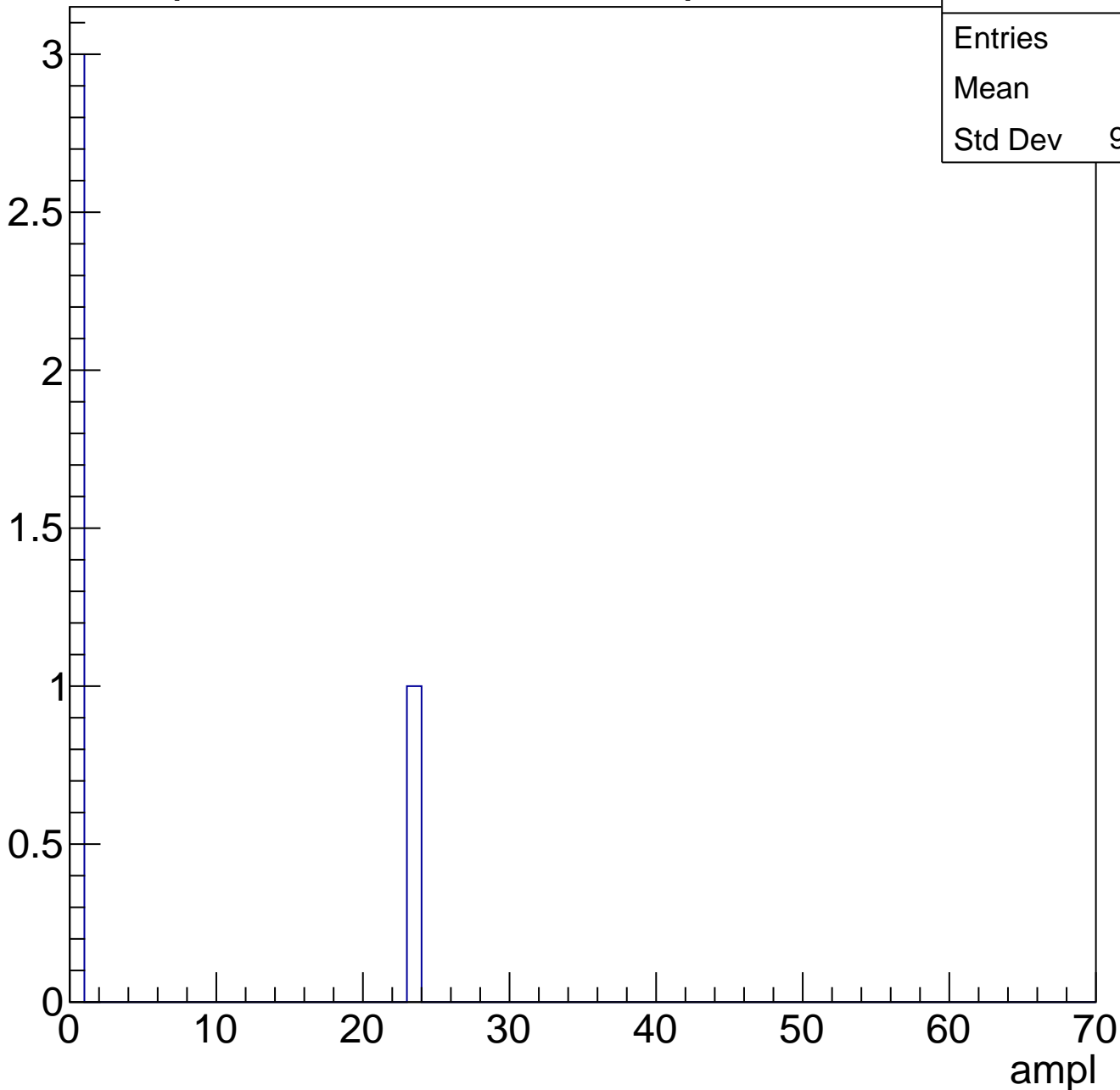
Entry



B0L000S, U4-ch17, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch18, adc0

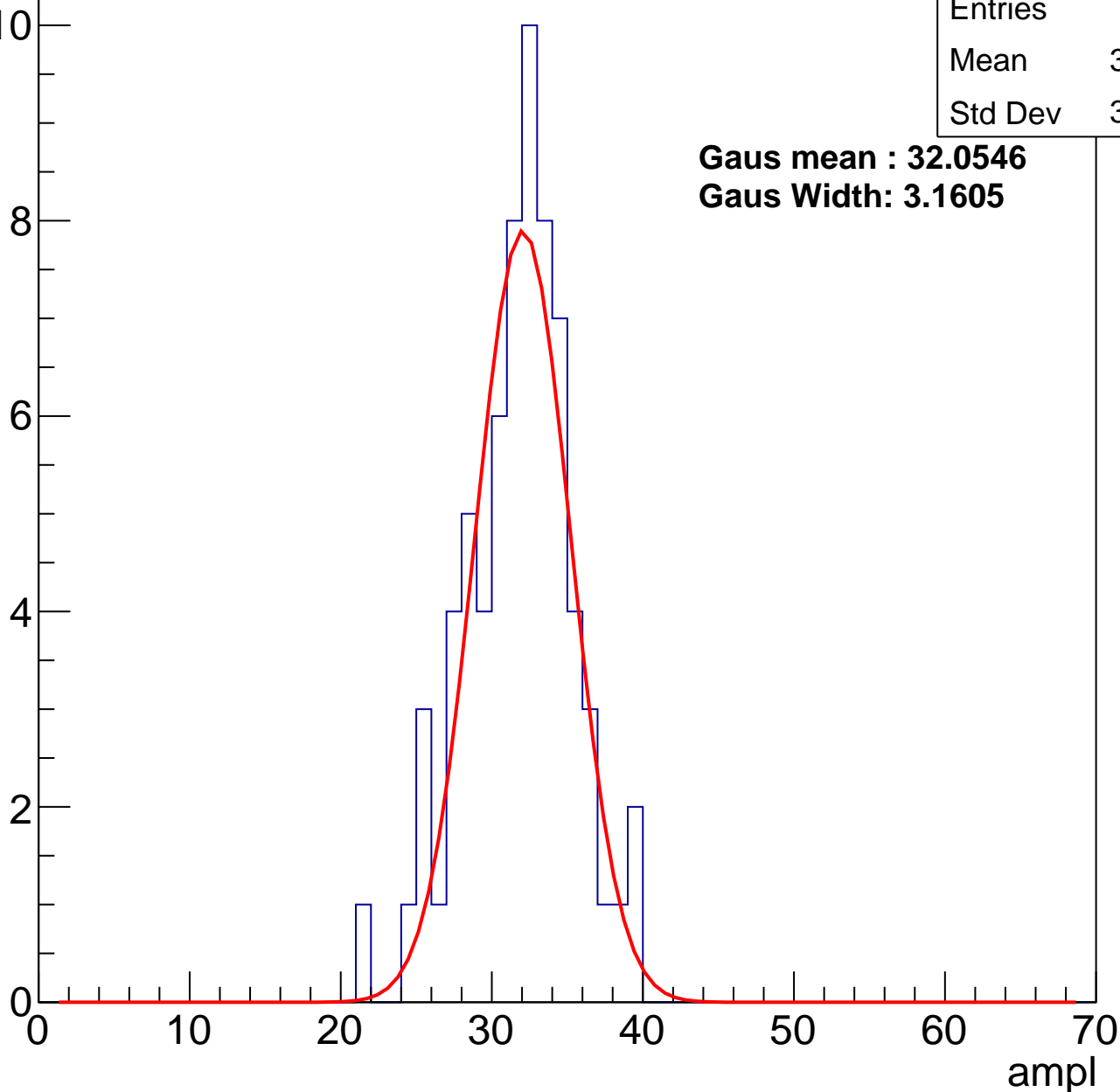
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	31.32
Std Dev	3.537

Gaus mean : 32.0546

Gaus Width: 3.1605



B0L000S, U4-ch18, adc1

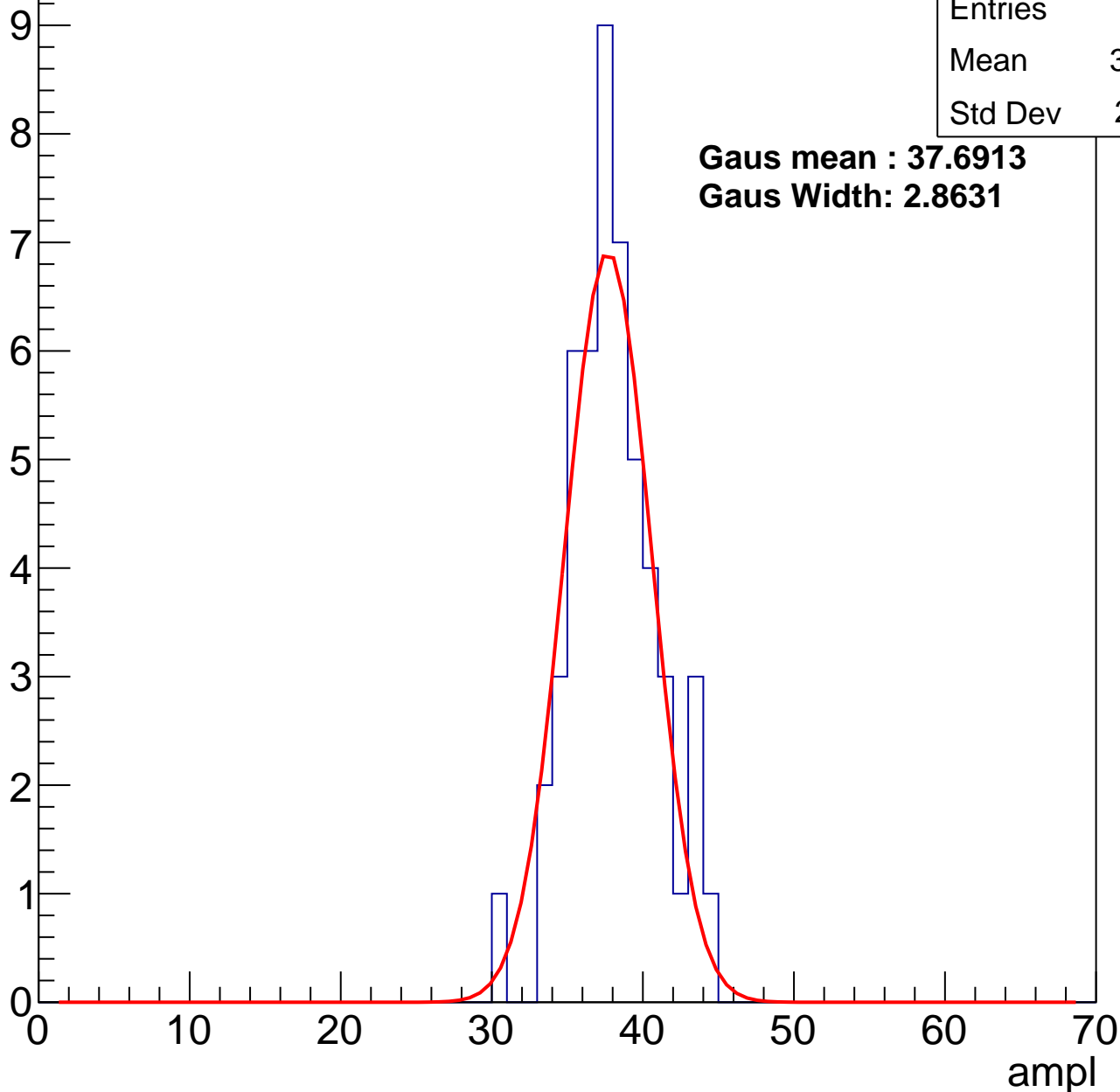
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	37.57
Std Dev	2.851

Gaus mean : 37.6913

Gaus Width: 2.8631



B0L000S, U4-ch18, adc2

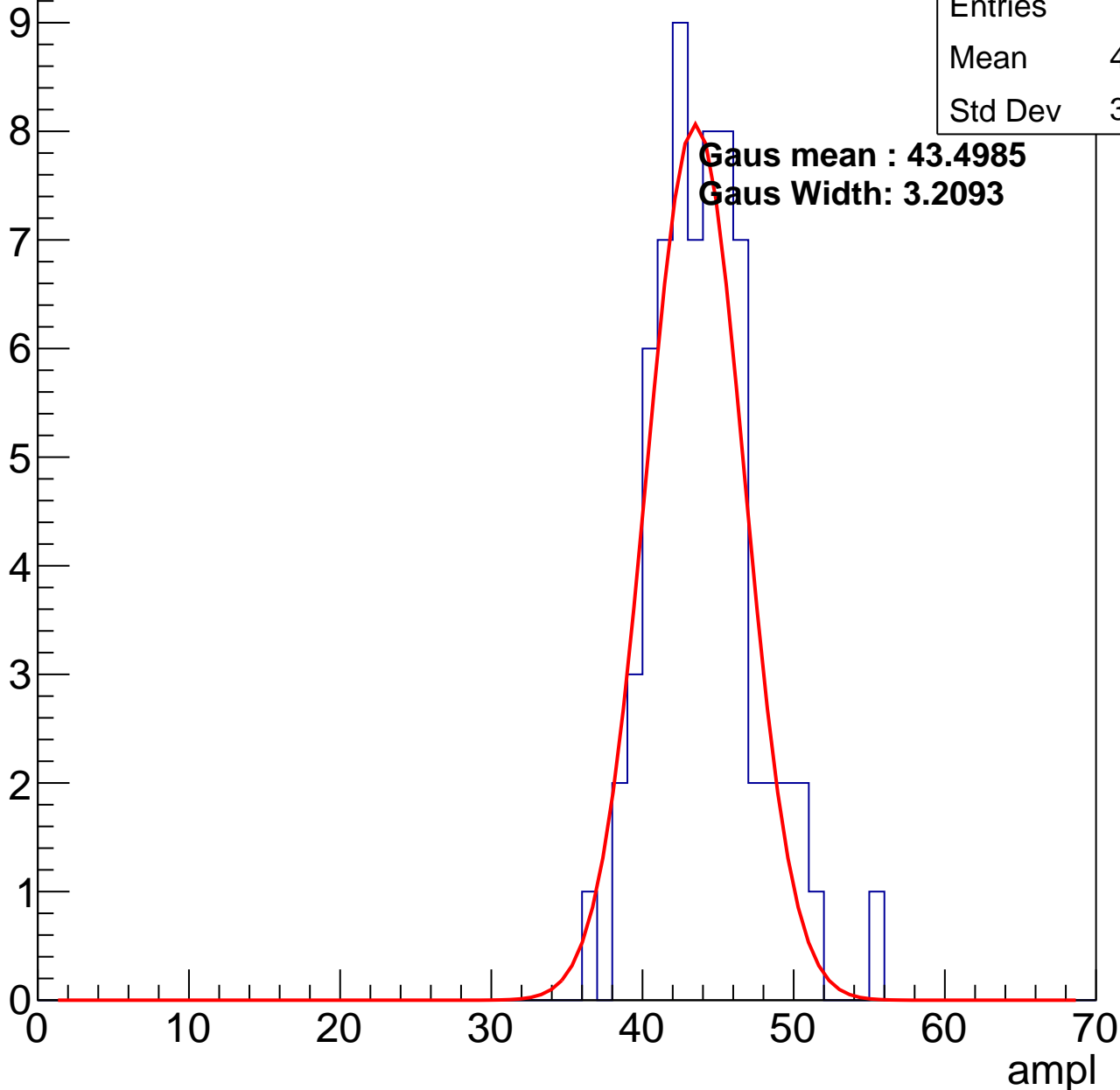
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.57
Std Dev	3.384

Gaus mean : 43.4985

Gaus Width: 3.2093

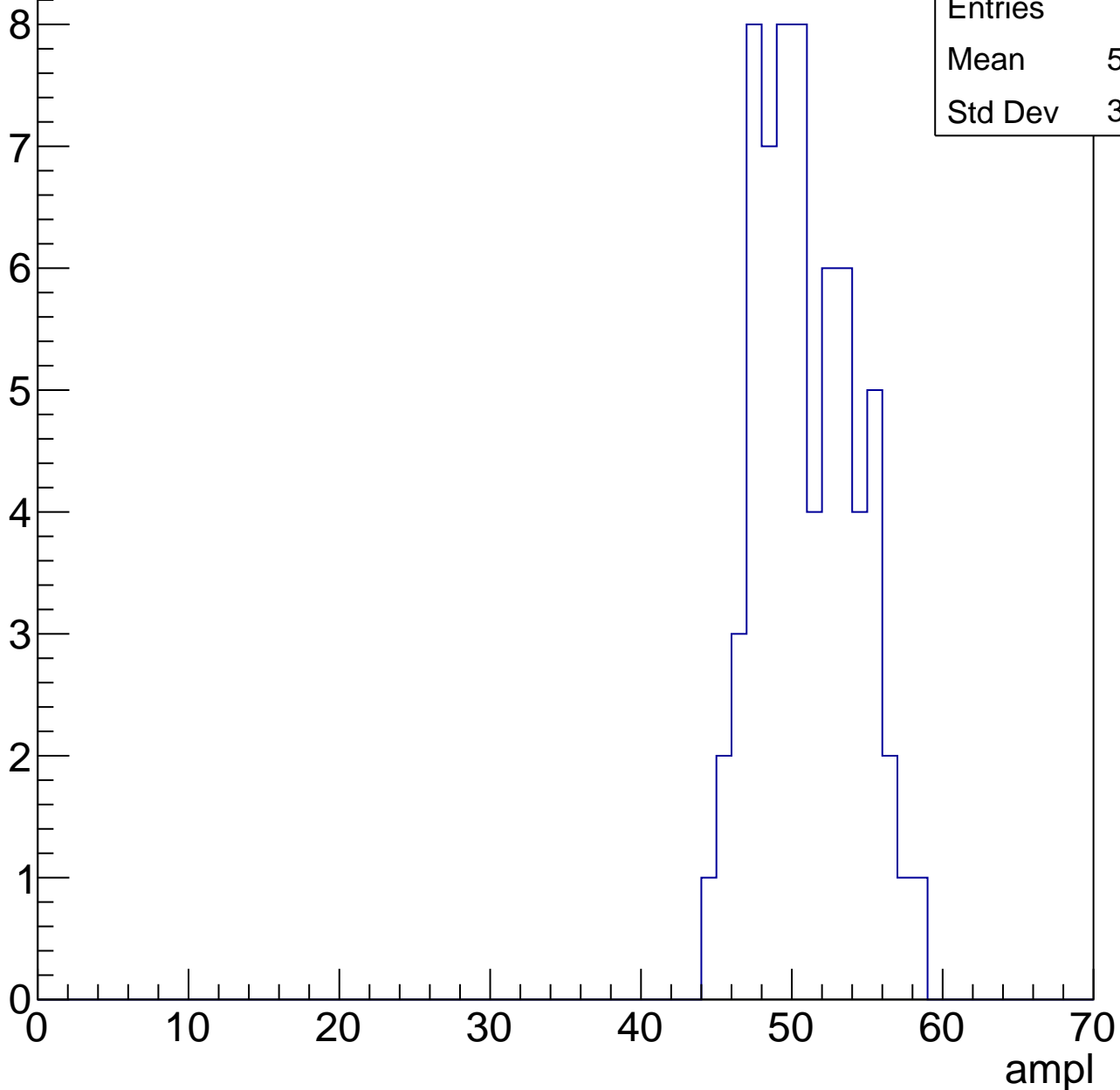


B0L000S, U4-ch18, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.42
Std Dev	3.224

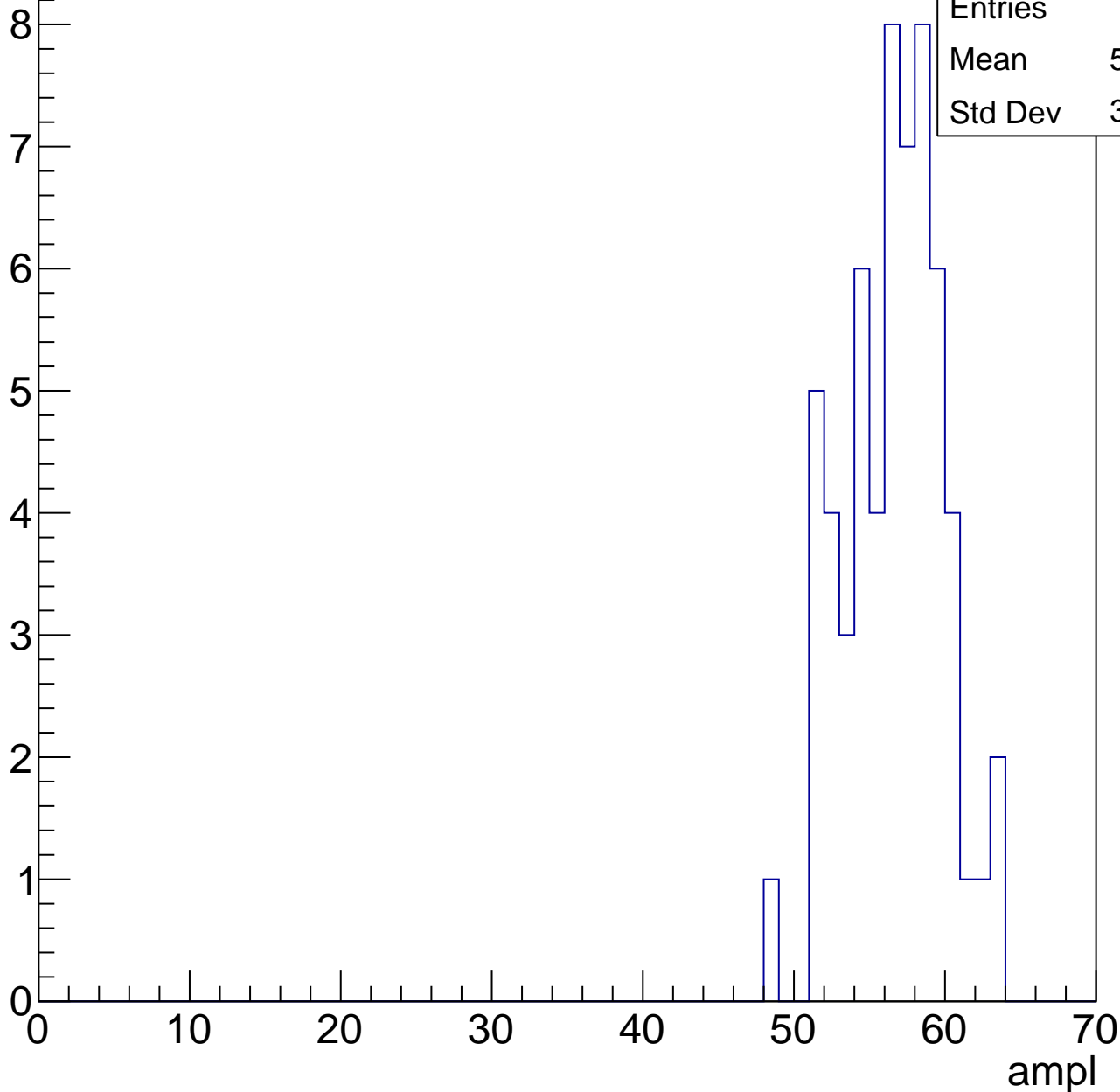


B0L000S, U4-ch18, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	56.13
Std Dev	3.222



B0L000S, U4-ch18, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.76
Std Dev	2.083

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B0L000S, U4-ch18, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch18, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch19, adc0

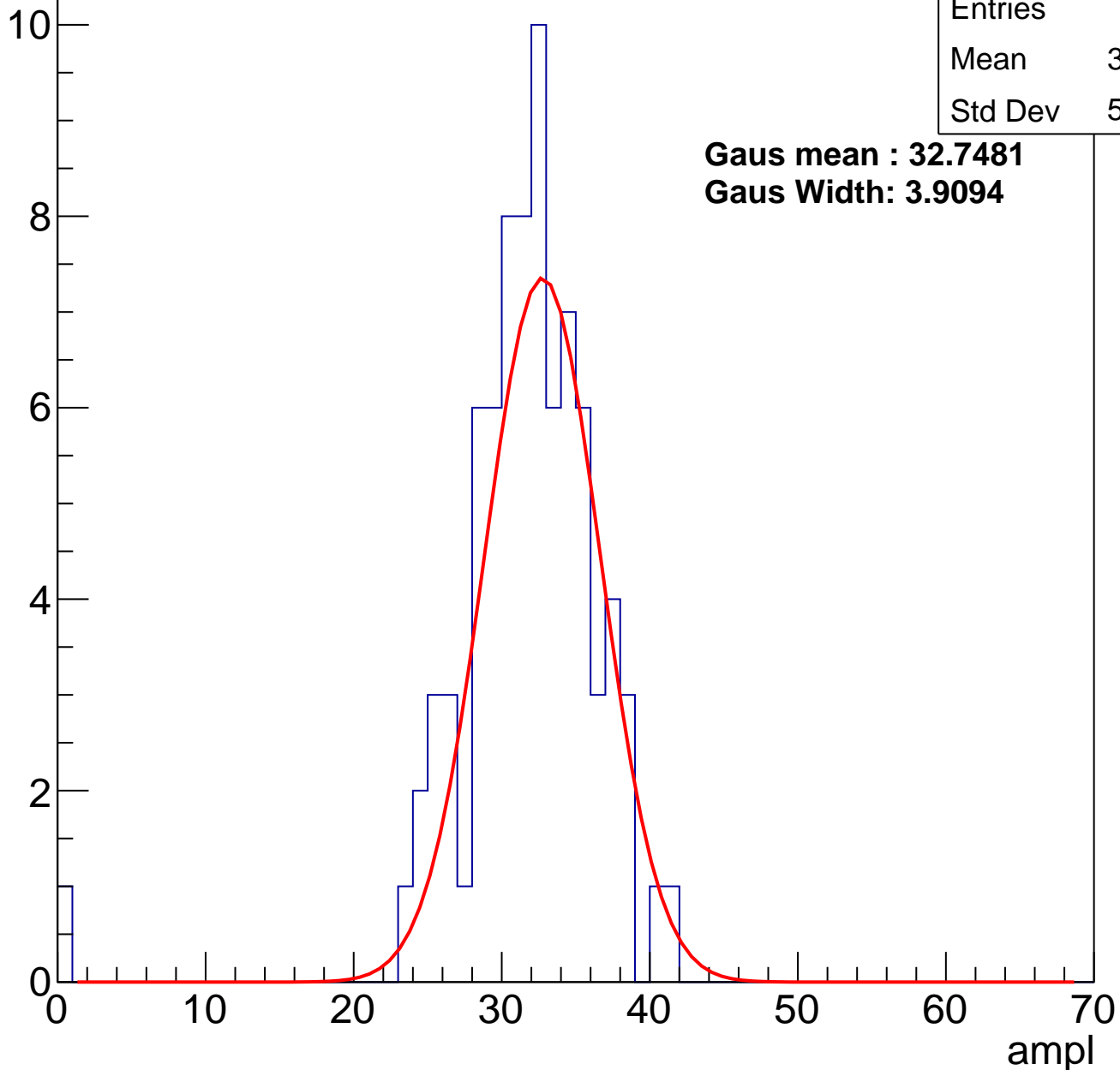
calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	31.23
Std Dev	5.177

Gaus mean : 32.7481

Gaus Width: 3.9094

Entry



B0L000S, U4-ch19, adc1

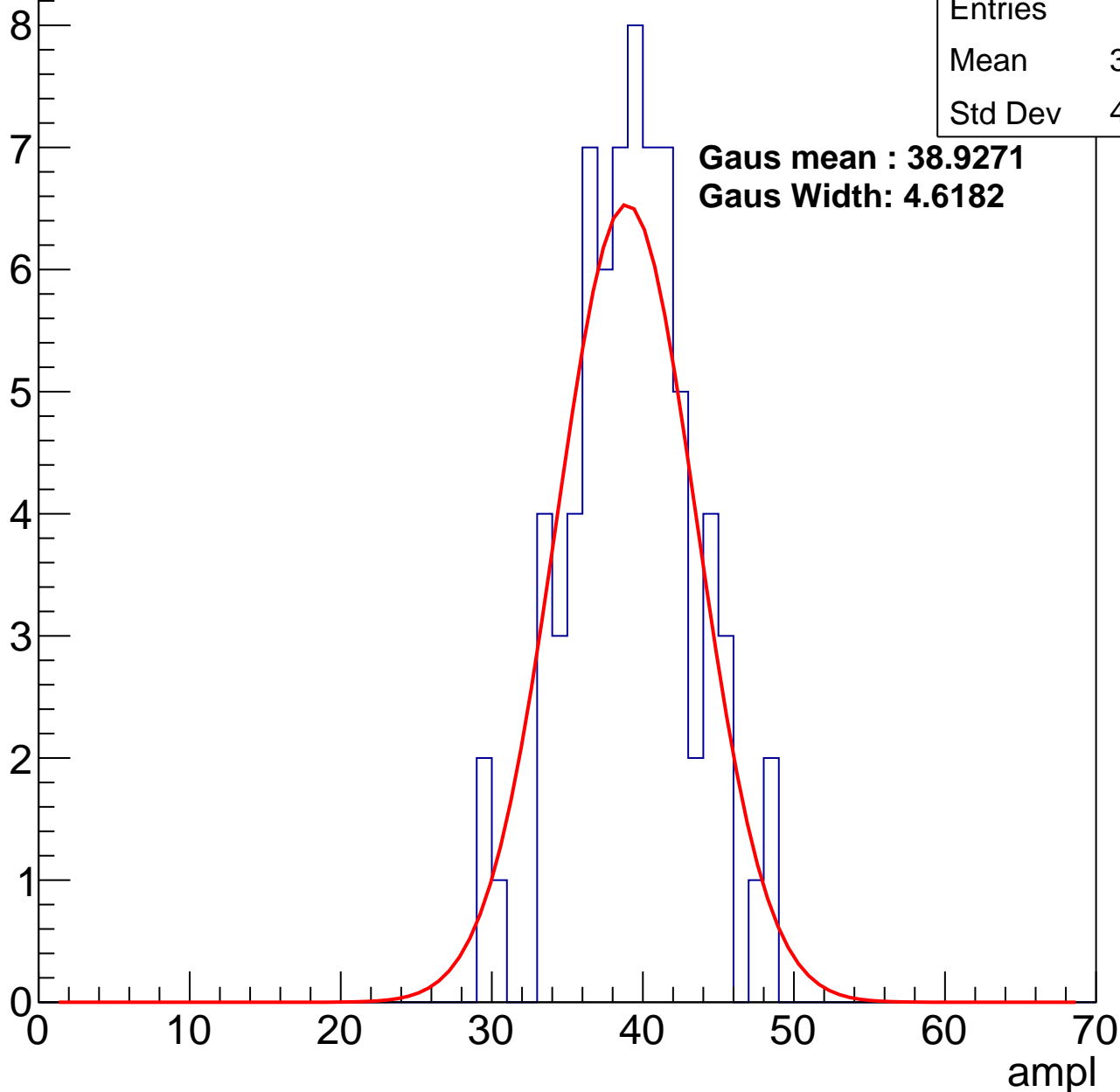
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	38.78
Std Dev	4.055

Gaus mean : 38.9271

Gaus Width: 4.6182



B0L000S, U4-ch19, adc2

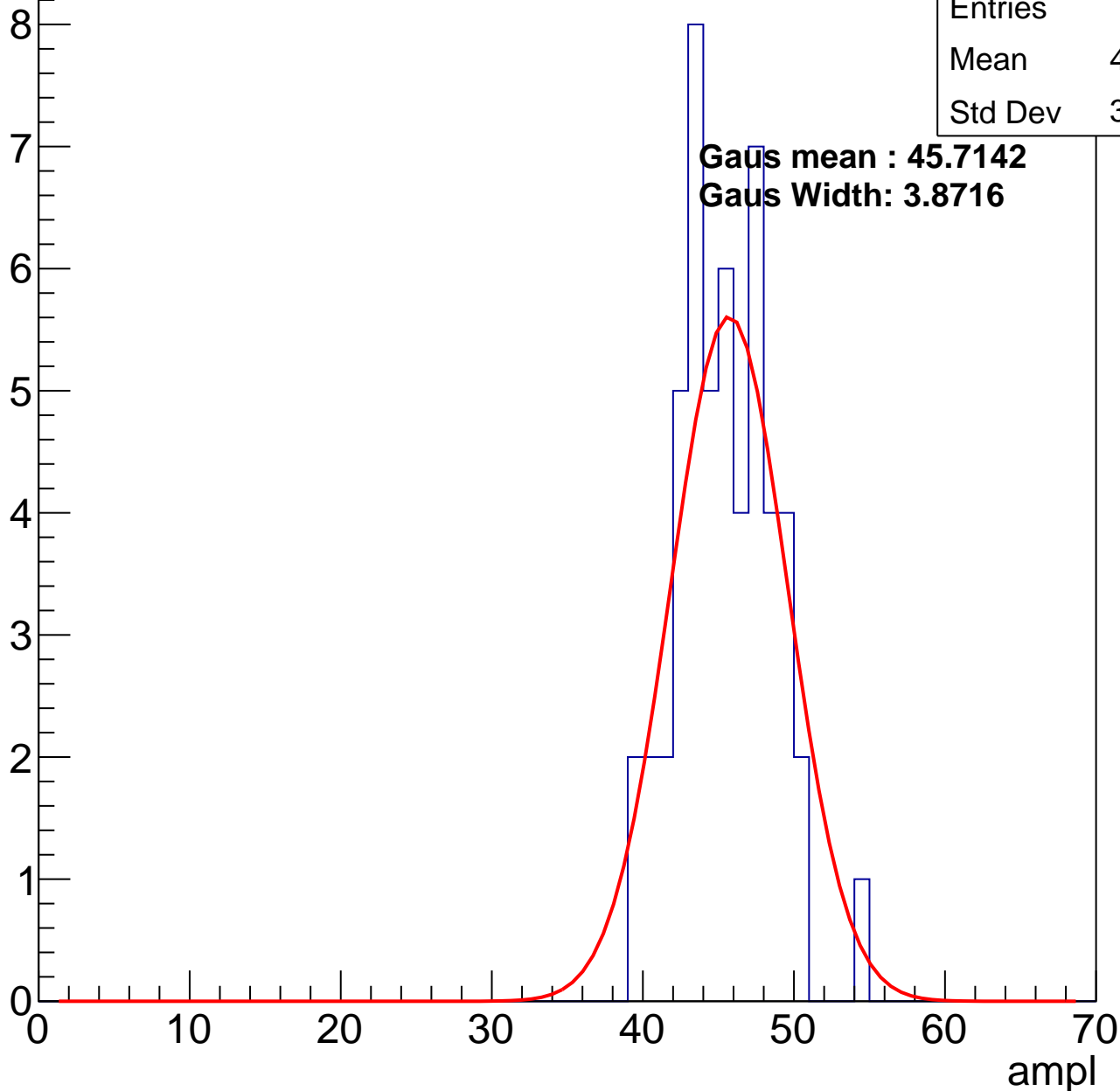
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.98
Std Dev	3.098

Gaus mean : 45.7142

Gaus Width: 3.8716



B0L000S, U4-ch19, adc3

calib_packv5_042523_0143.root, FC#5, port B1

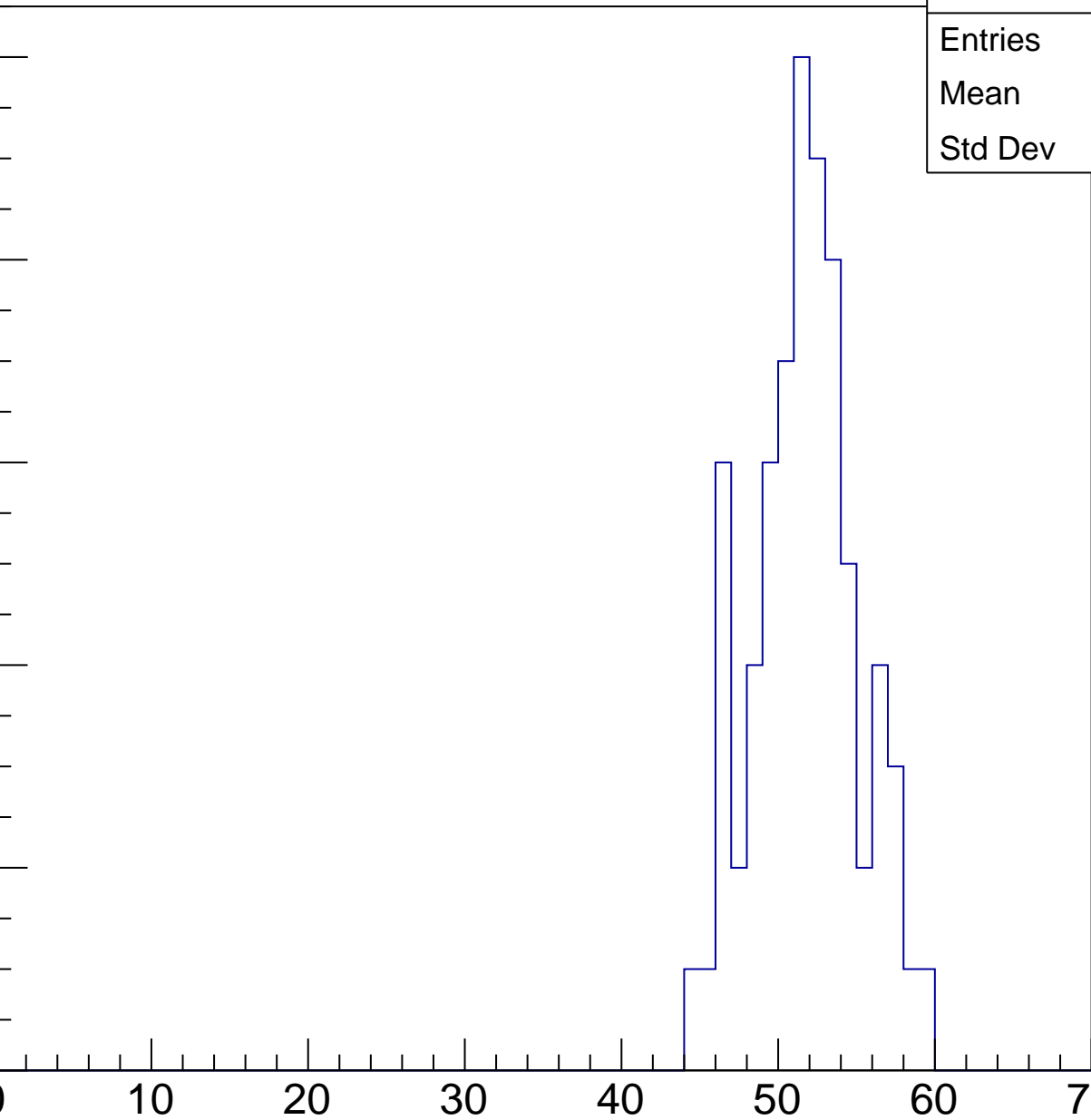
Entries	70
Mean	51.27
Std Dev	3.312

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

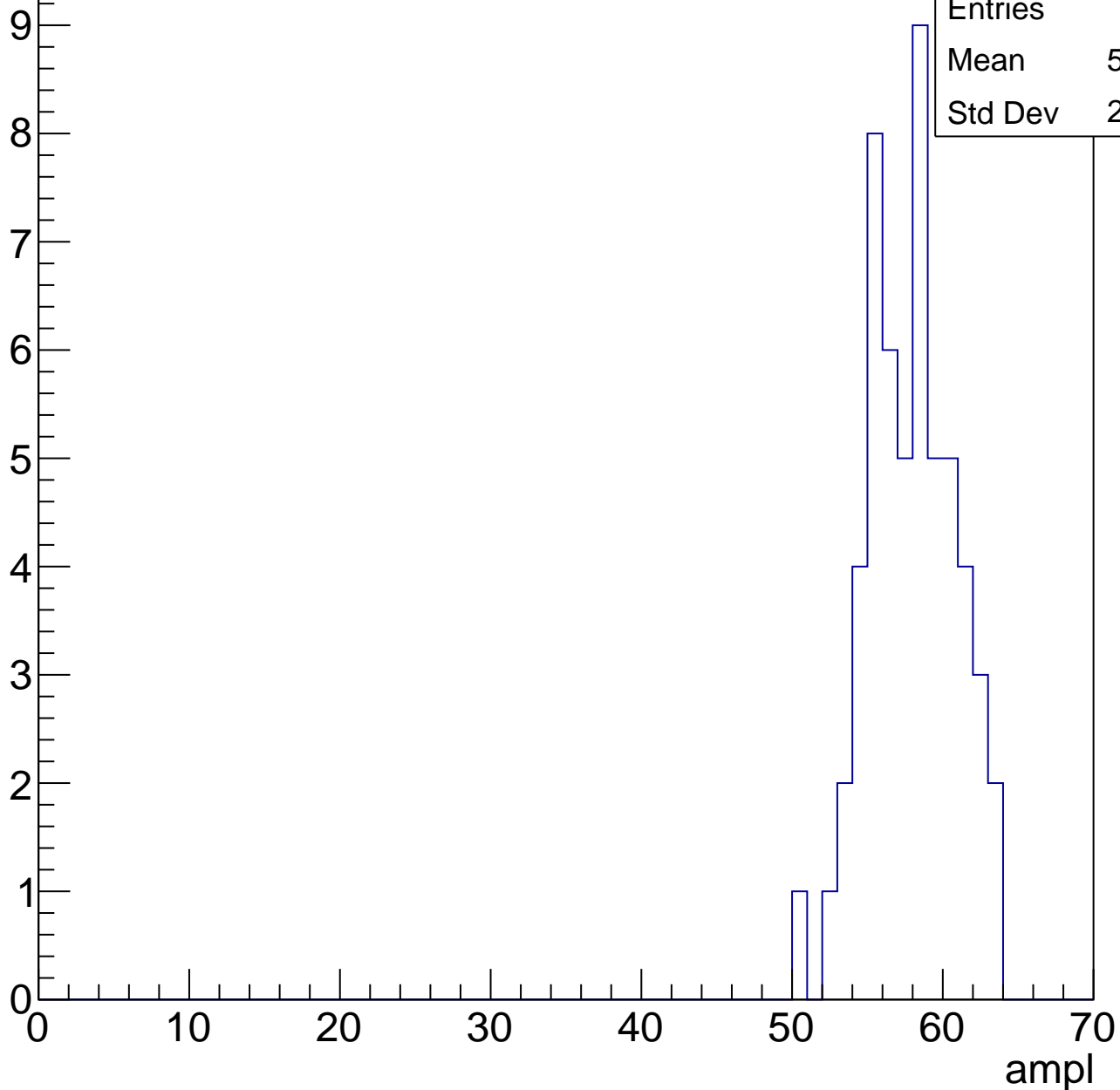
ampl



B0L000S, U4-ch19, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



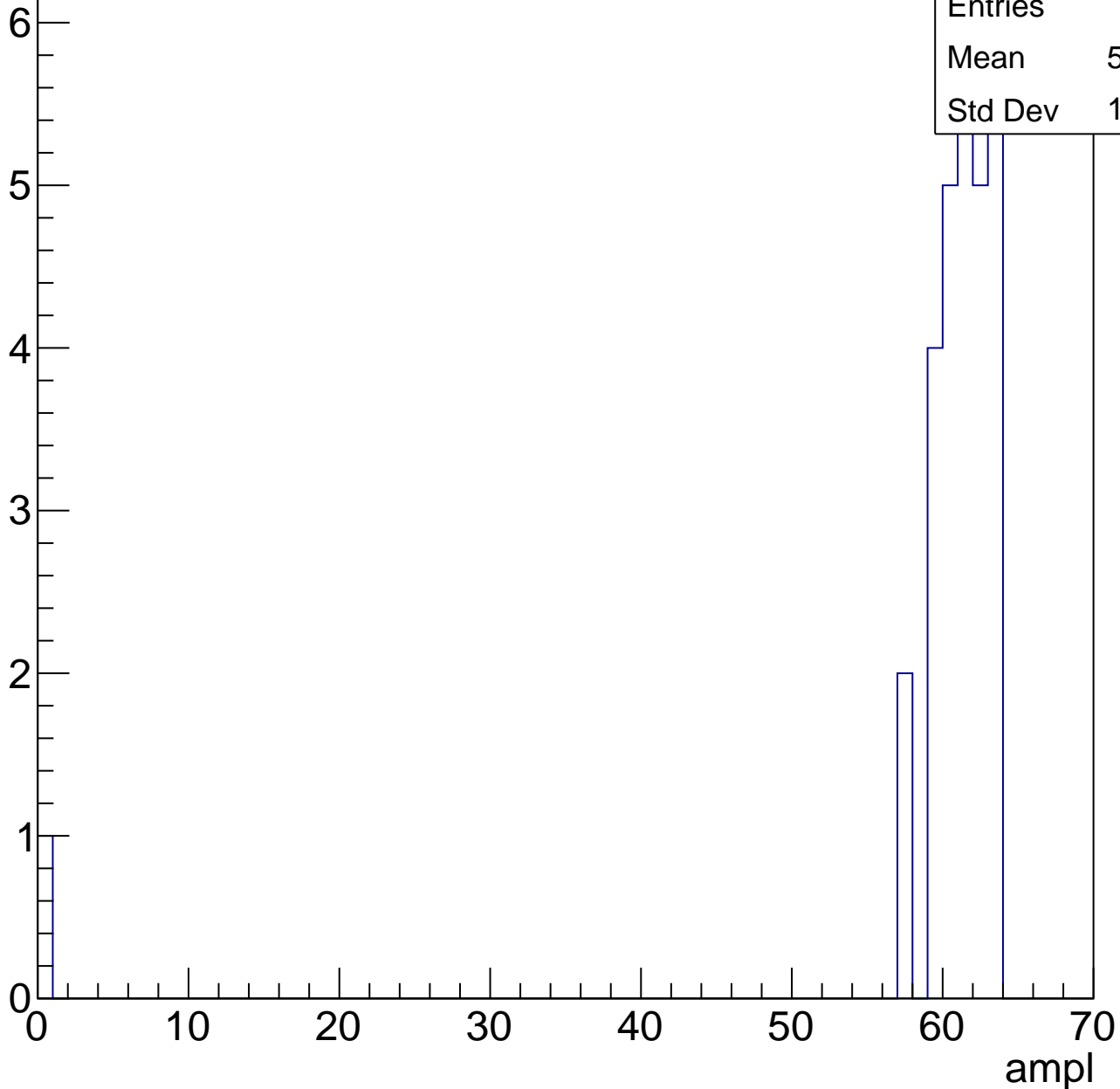
Entries	55
Mean	57.42
Std Dev	2.877

B0L000S, U4-ch19, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

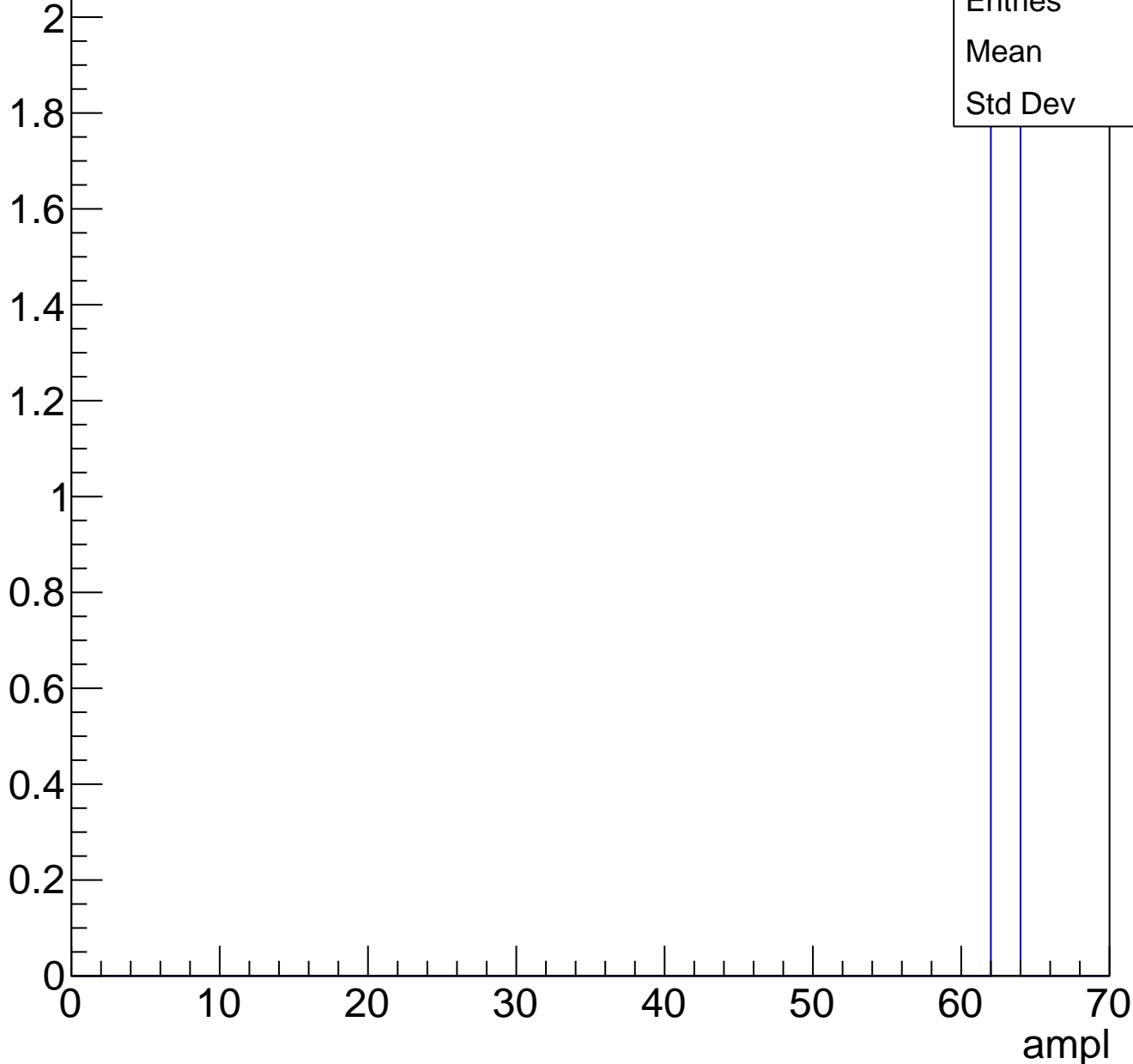
Entries	29
Mean	58.76
Std Dev	11.23



B0L000S, U4-ch19, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch19, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch20, adc0

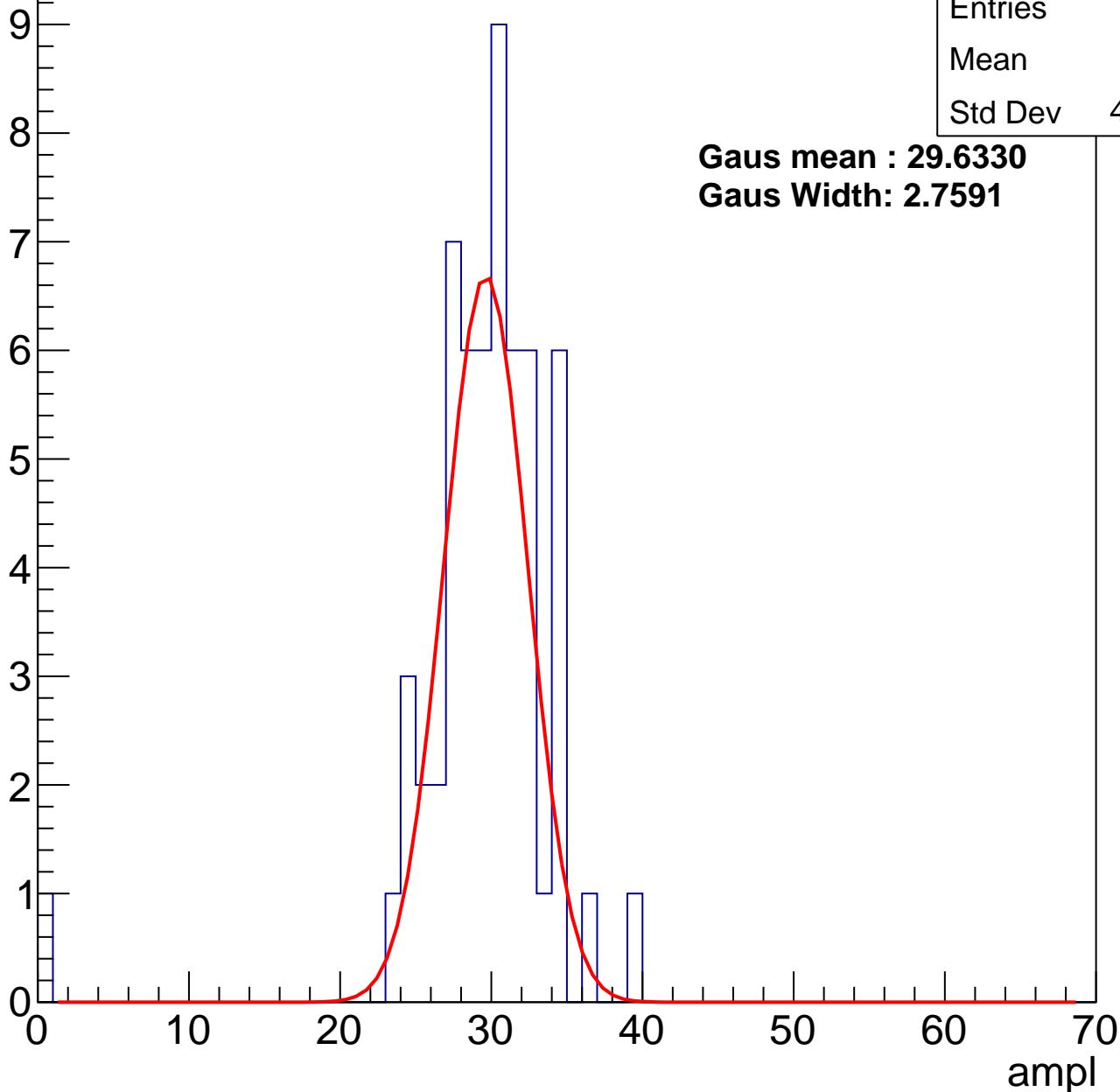
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.1
Std Dev	4.978

Gaus mean : 29.6330

Gaus Width: 2.7591



B0L000S, U4-ch20, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	53
Mean	35.08
Std Dev	3.125

Gaus mean : 35.2199

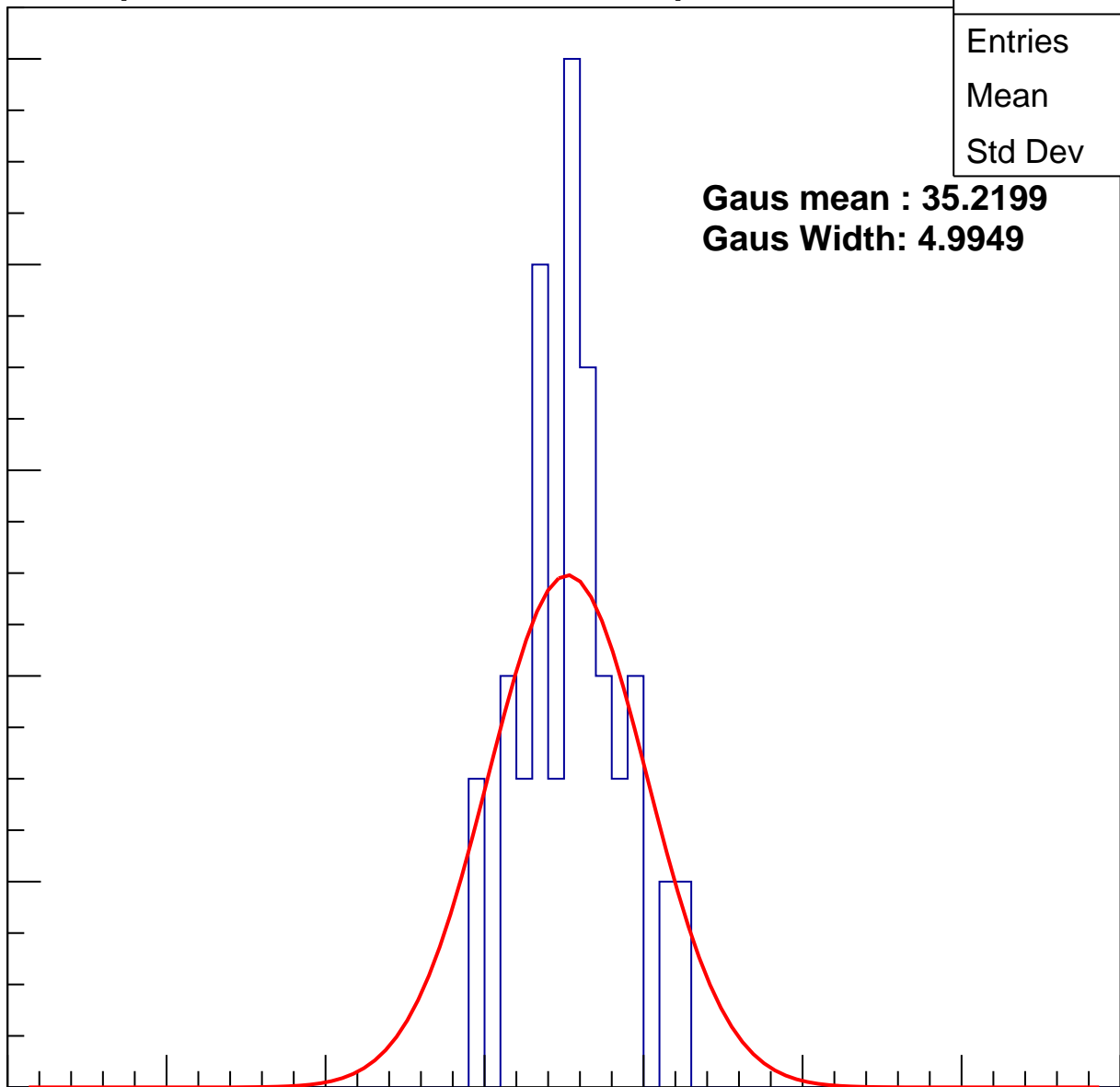
Gaus Width: 4.9949

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch20, adc2

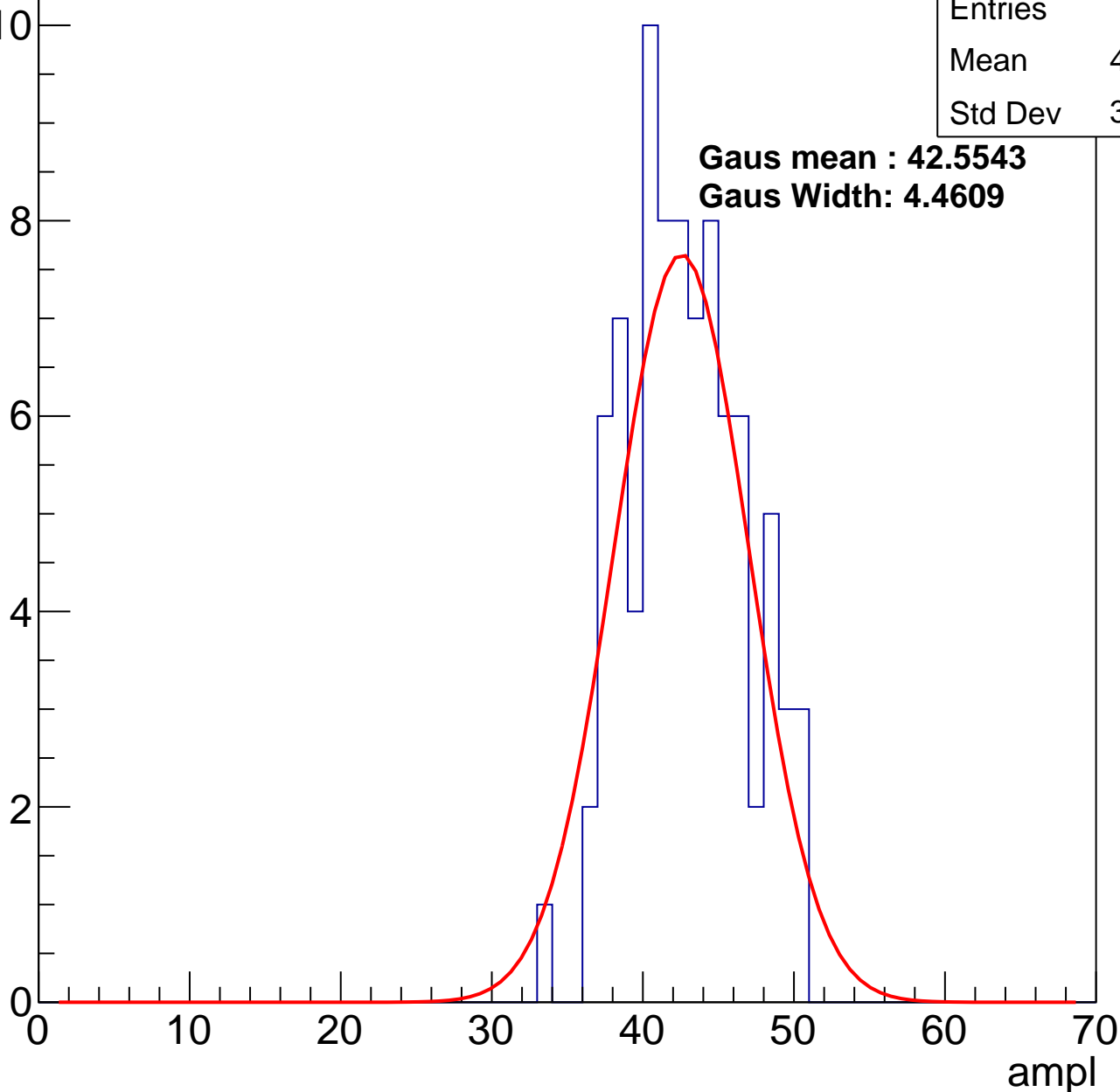
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	86
Mean	42.36
Std Dev	3.806

Gaus mean : 42.5543

Gaus Width: 4.4609

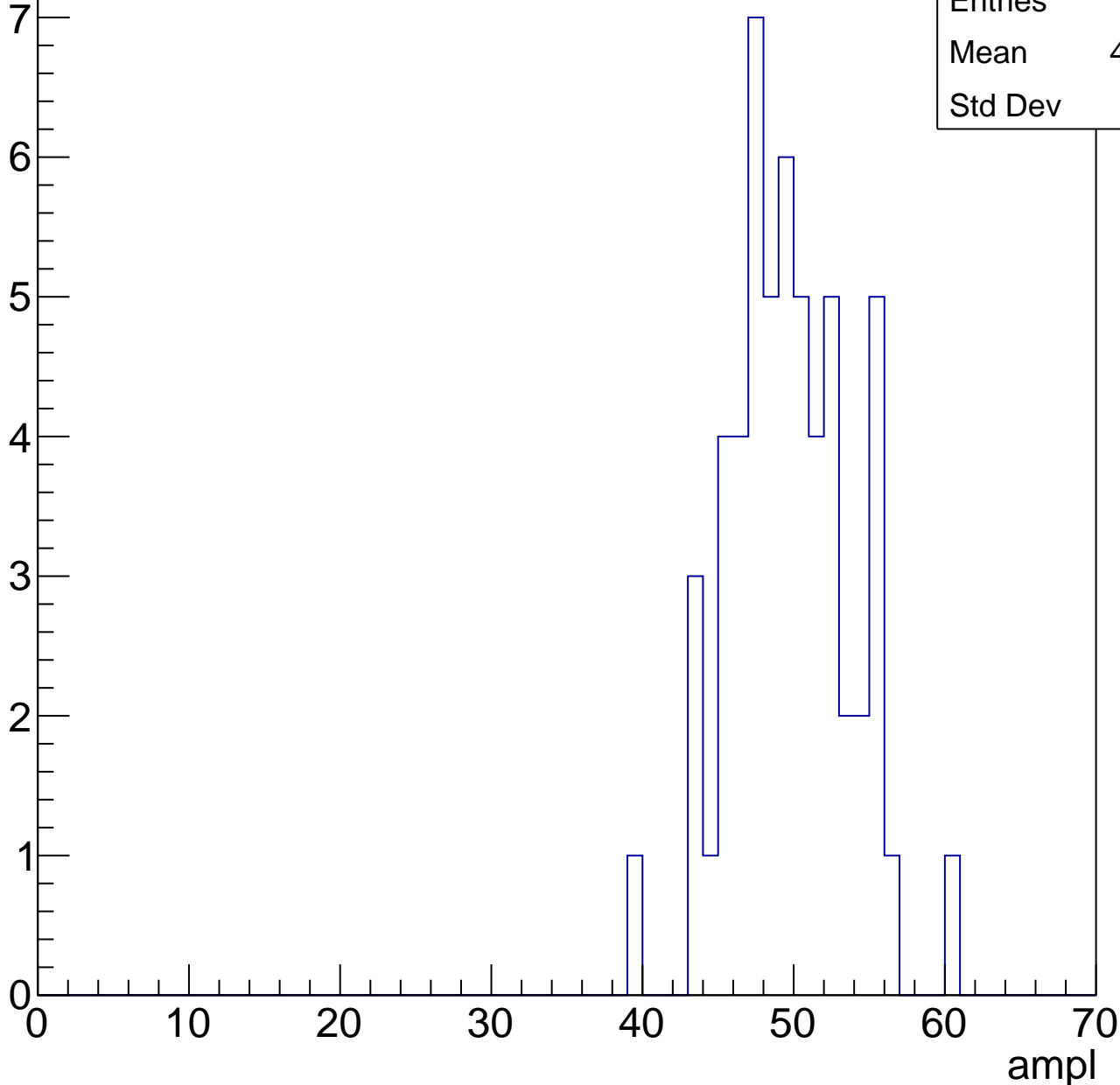


B0L000S, U4-ch20, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	49.25
Std Dev	3.92

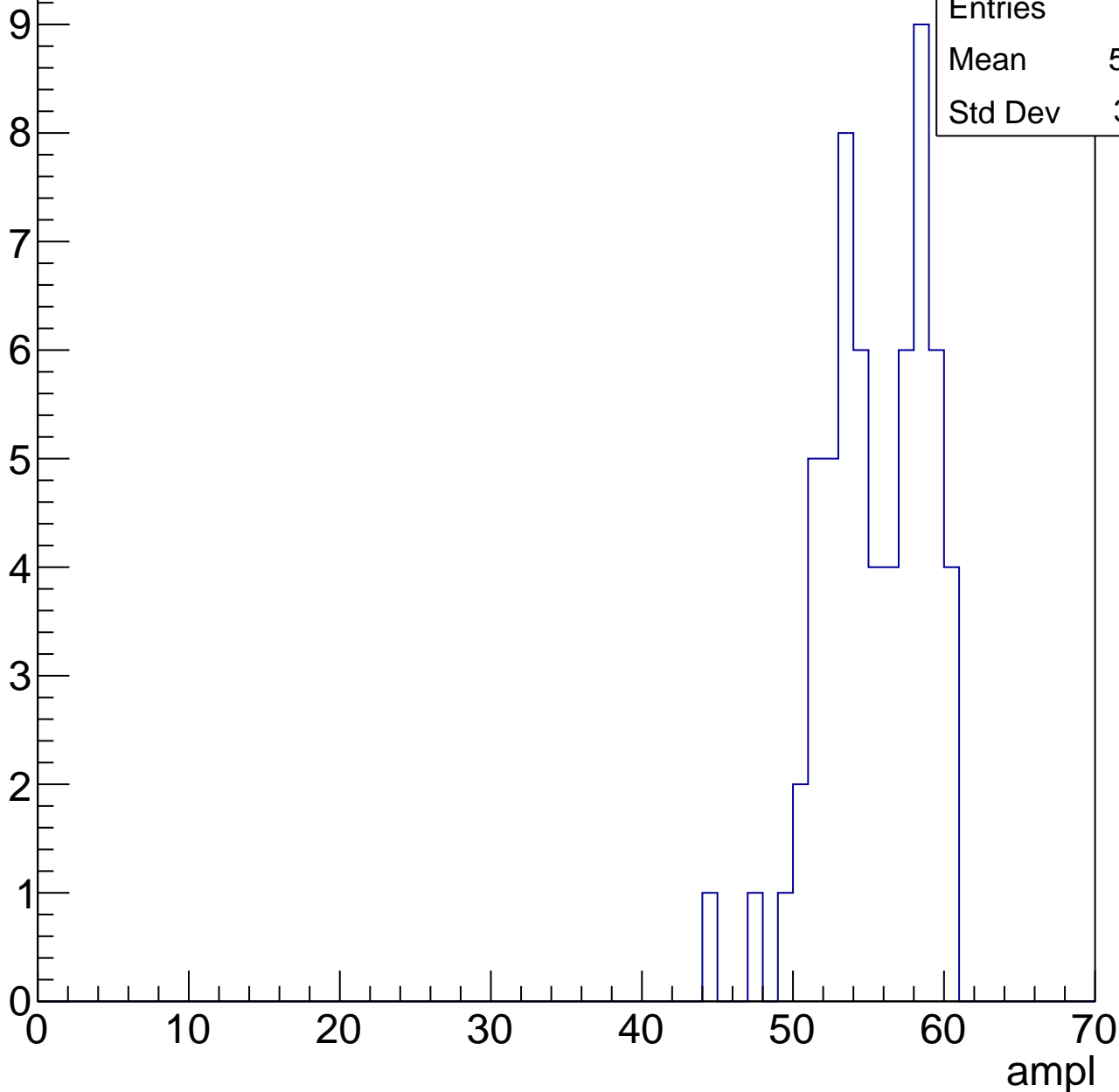


B0L000S, U4-ch20, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	54.92
Std Dev	3.451

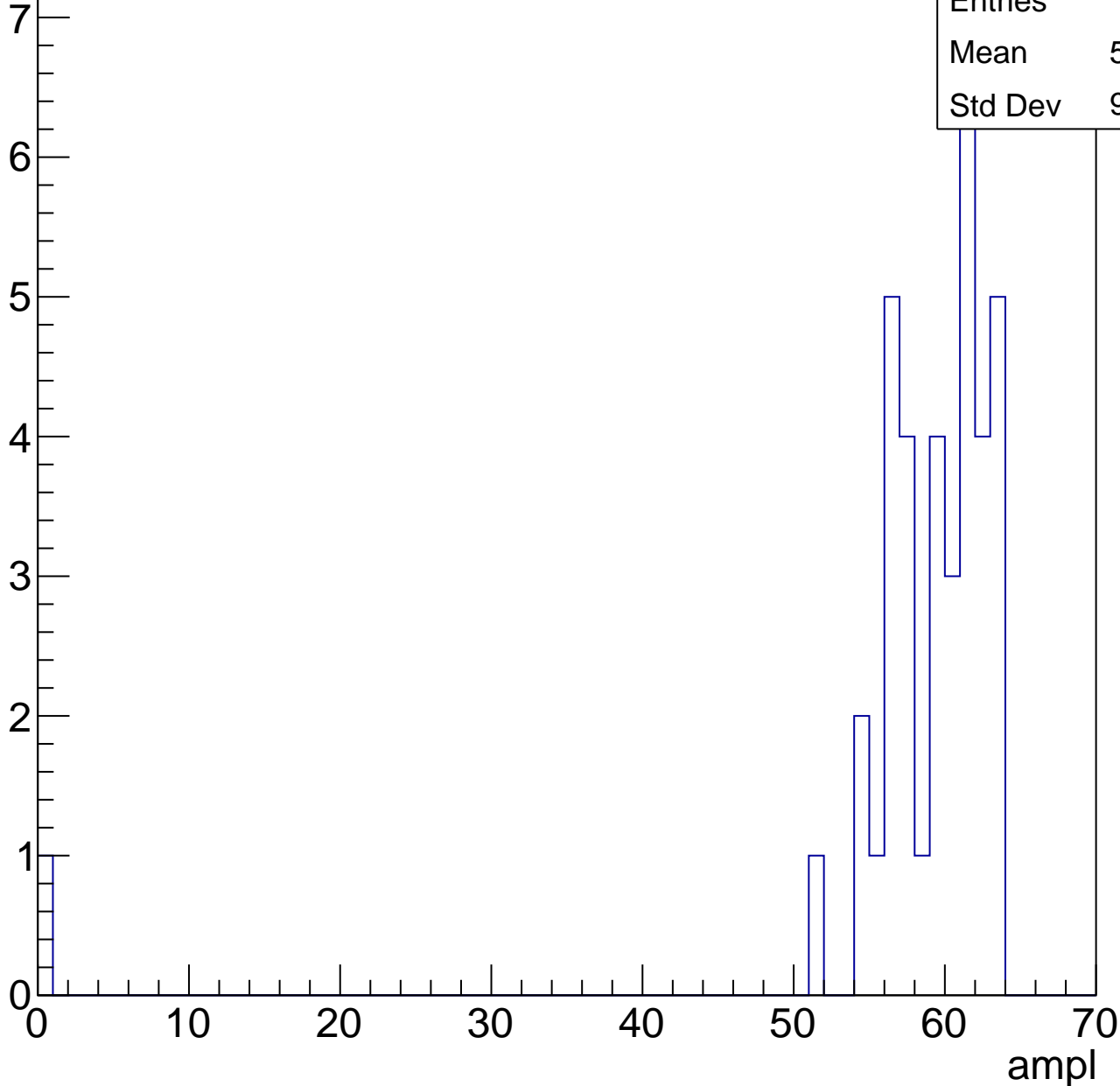


B0L000S, U4-ch20, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	38
Mean	57.53
Std Dev	9.917

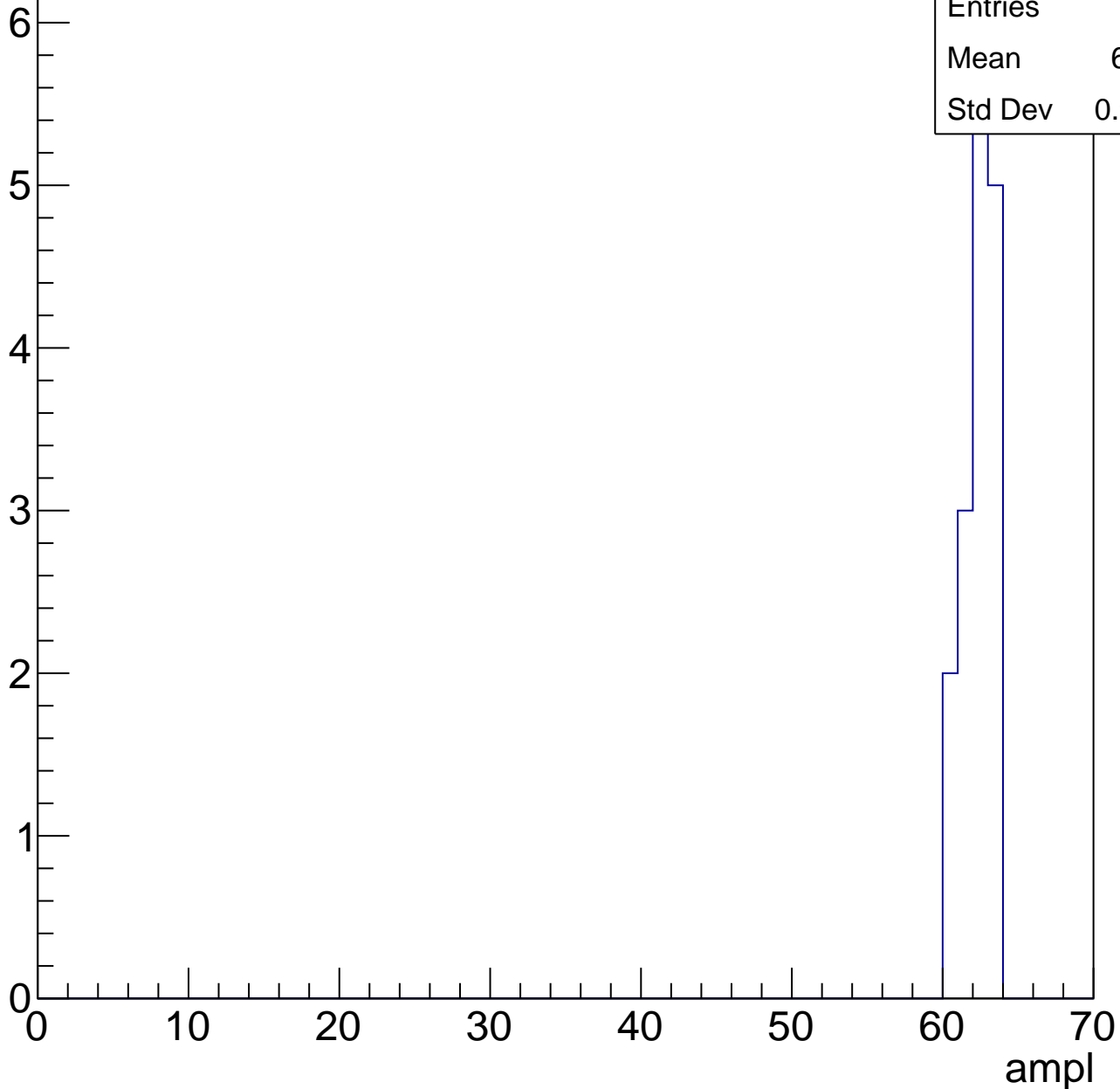


B0L000S, U4-ch20, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.88
Std Dev	0.9922



B0L000S, U4-ch20, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch21, adc0

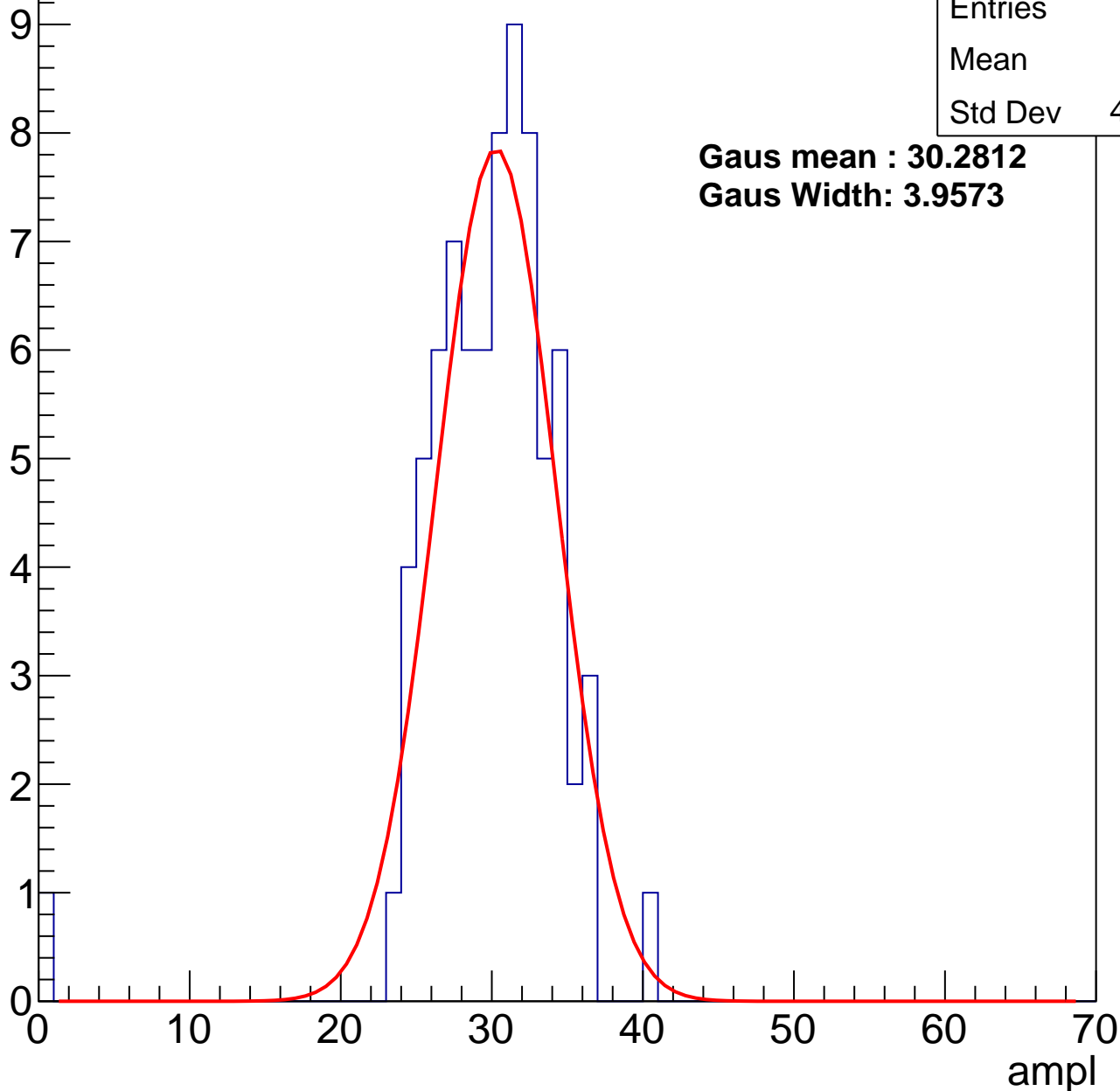
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	29.4
Std Dev	4.829

Gaus mean : 30.2812

Gaus Width: 3.9573



B0L000S, U4-ch21, adc1

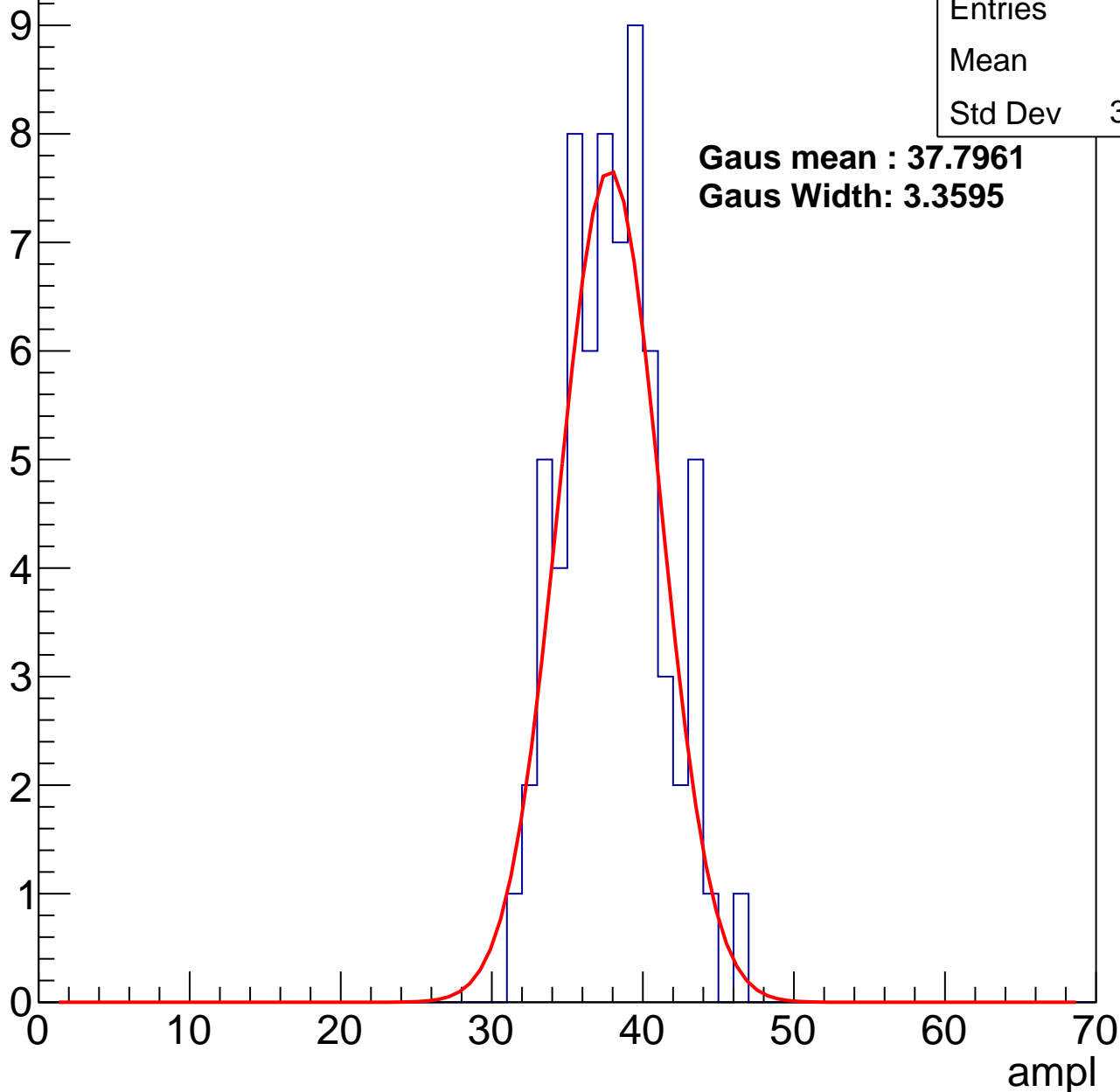
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.6
Std Dev	3.255

Gaus mean : 37.7961

Gaus Width: 3.3595



B0L000S, U4-ch21, adc2

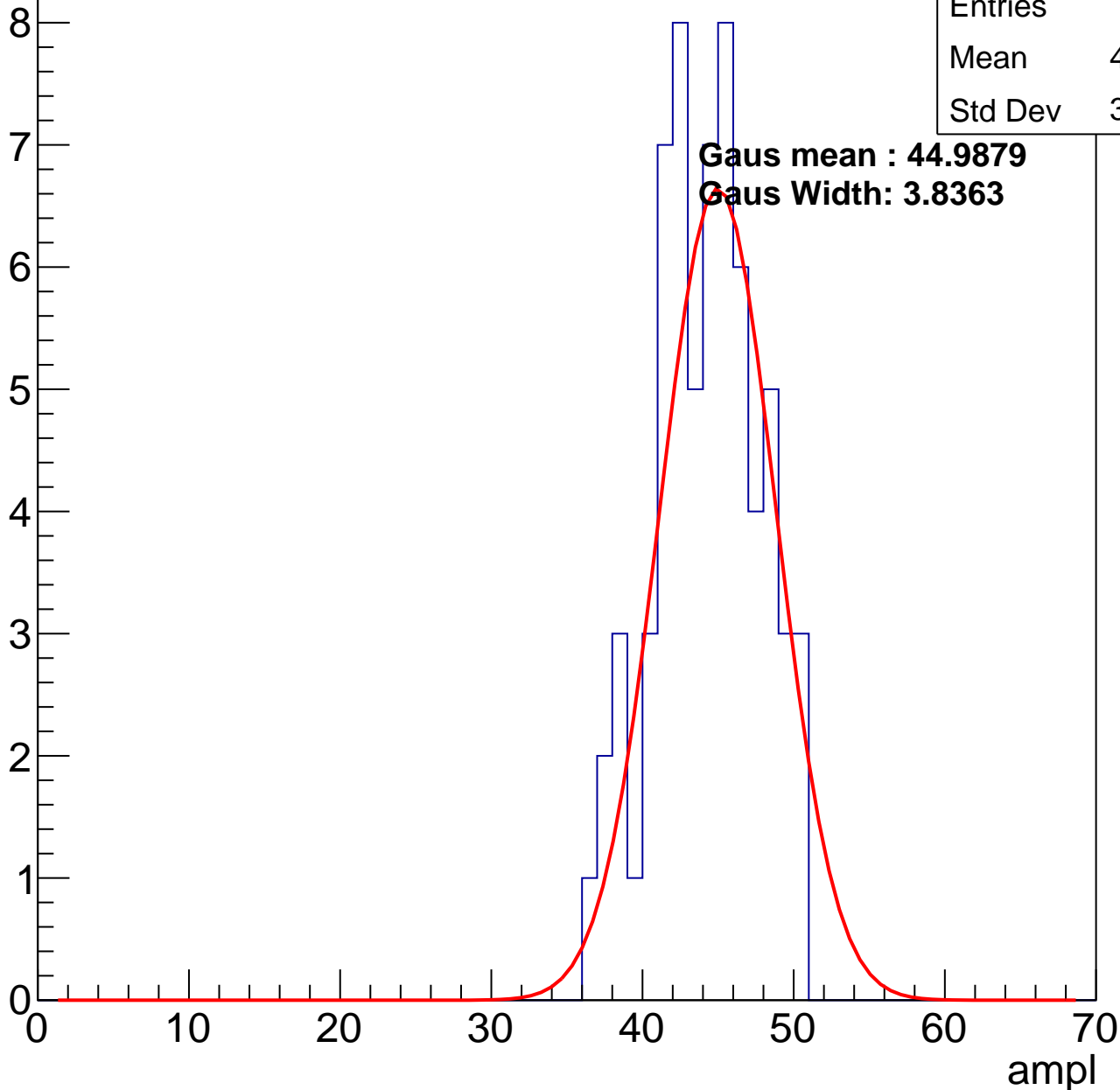
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.79
Std Dev	3.418

Gaus mean : 44.9879

Gaus Width: 3.8363

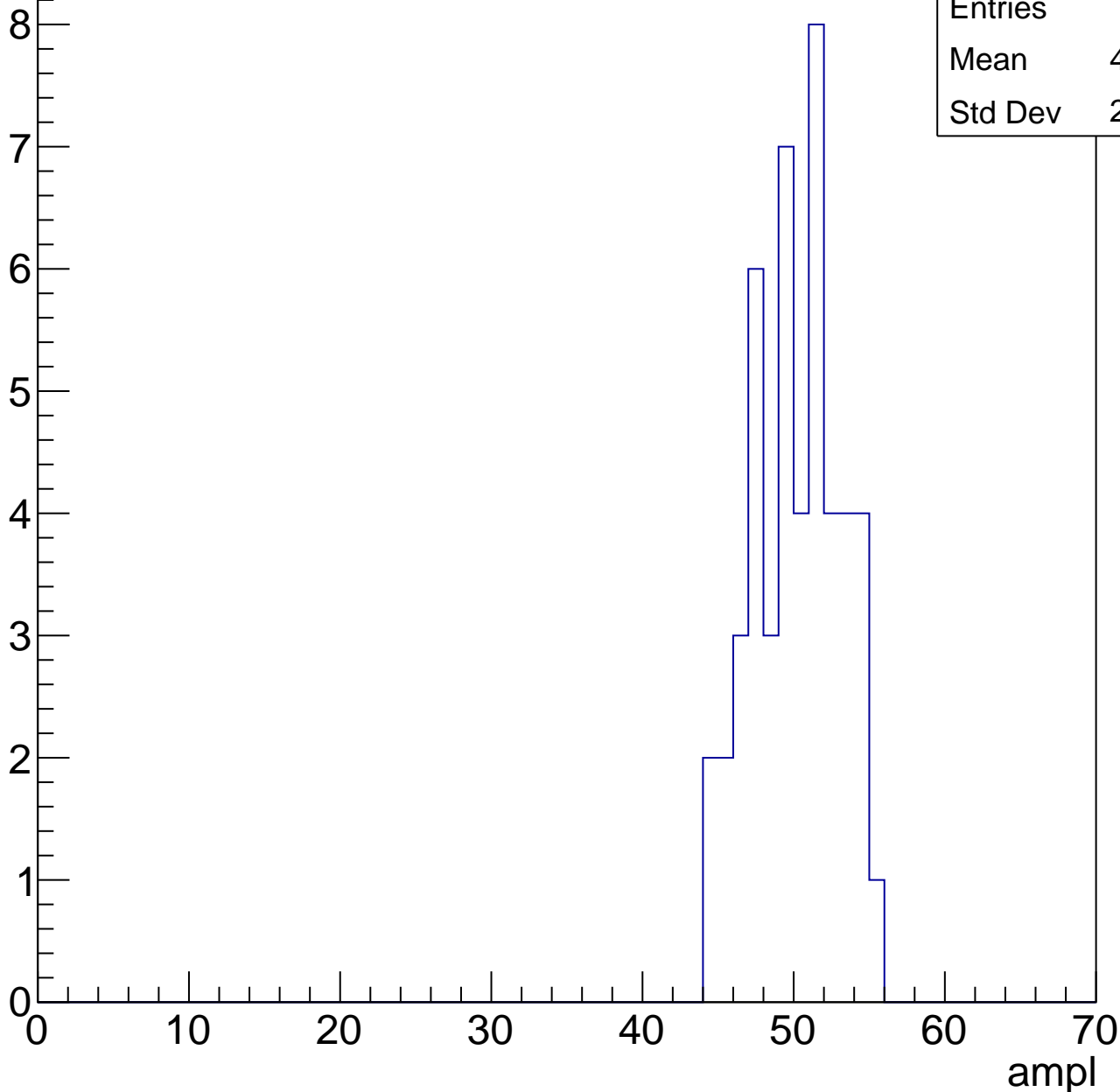


B0L000S, U4-ch21, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	49.67
Std Dev	2.838

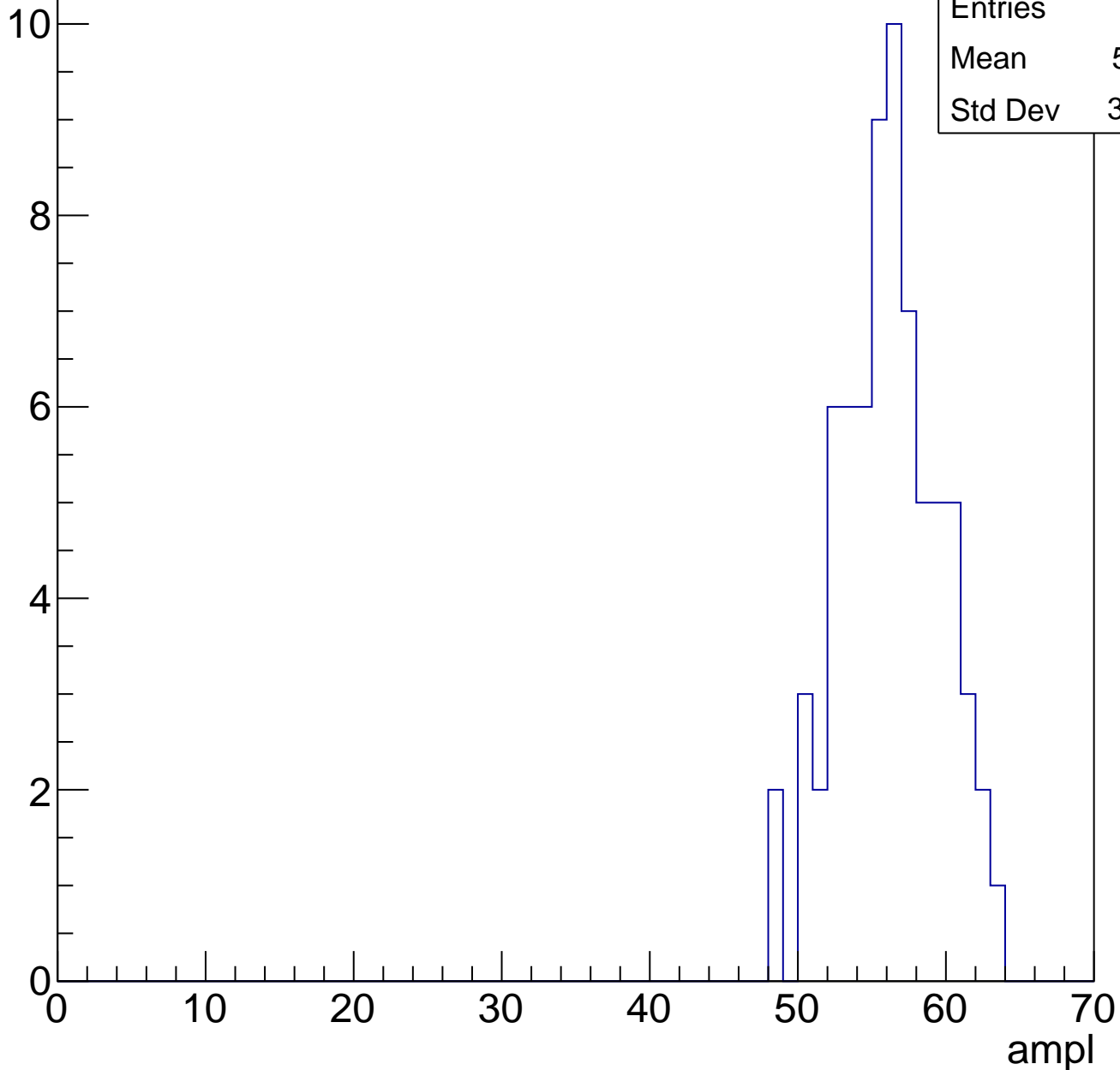


B0L000S, U4-ch21, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	72
Mean	55.71
Std Dev	3.356

Entry



B0L000S, U4-ch21, adc5

calib_packv5_042523_0143.root, FC#5, port B1

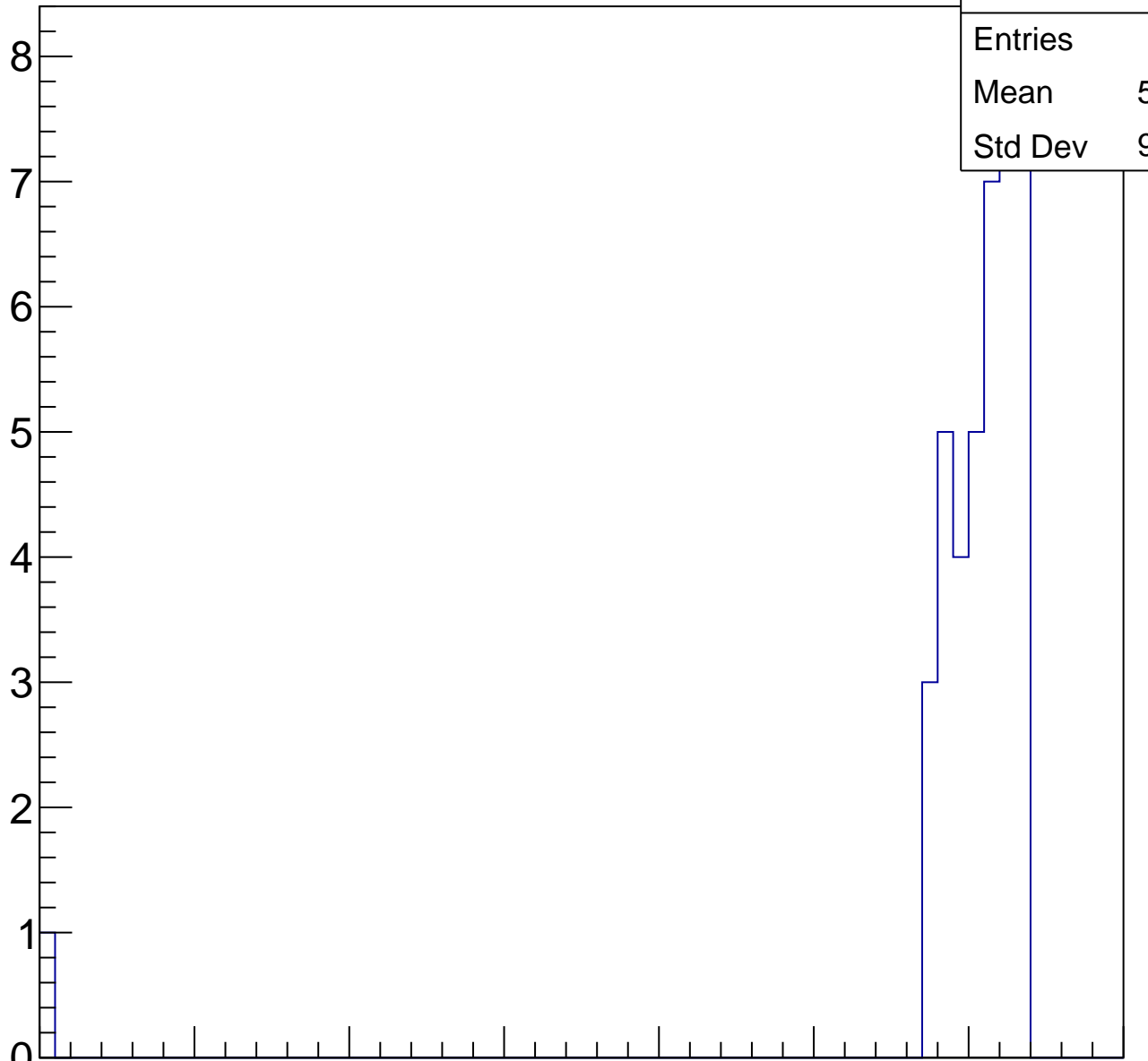
Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.12
Std Dev	9.539

ampl

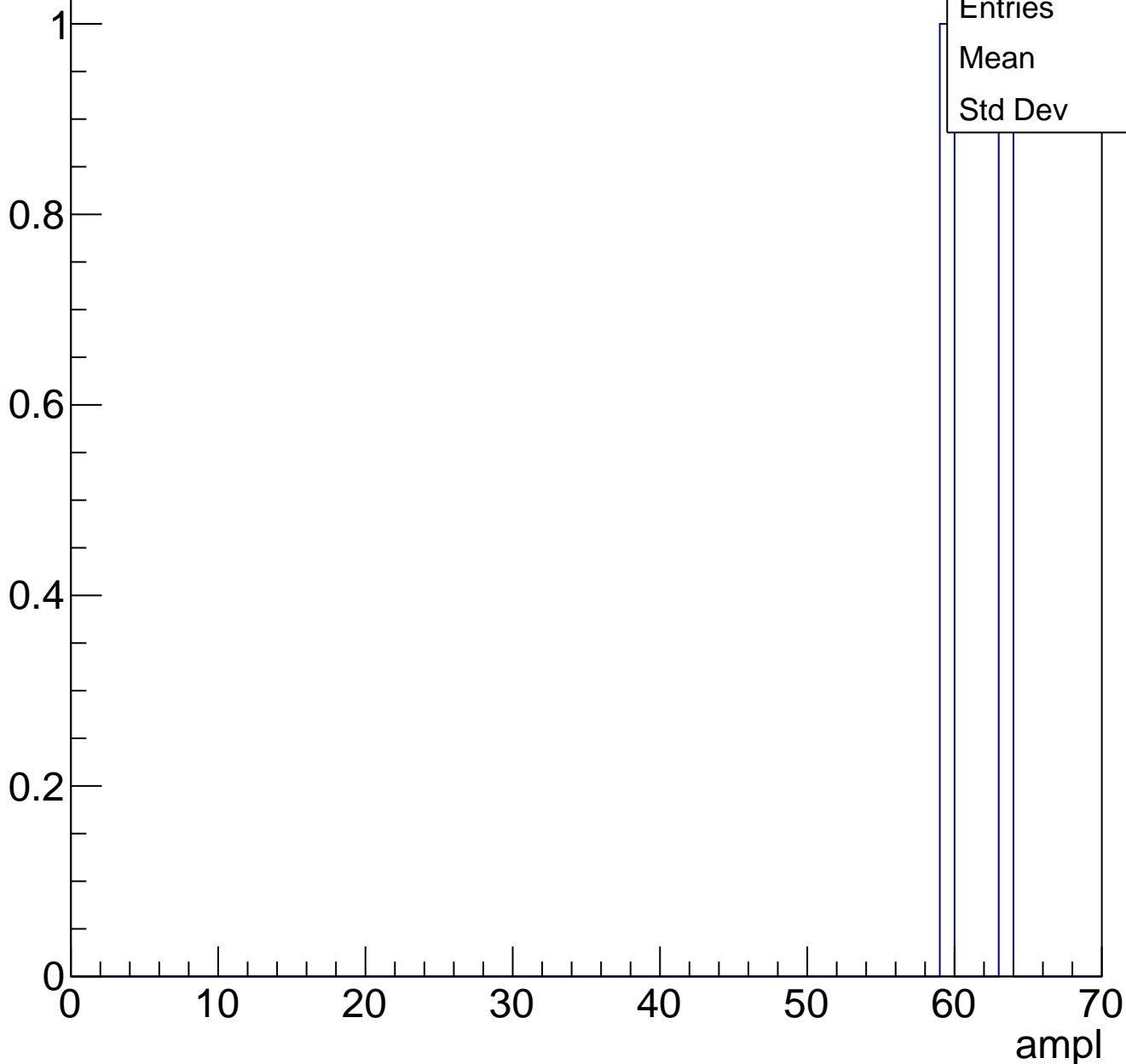
0 10 20 30 40 50 60 70



B0L000S, U4-ch21, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch21, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

B0L000S, U4-ch22, adc0

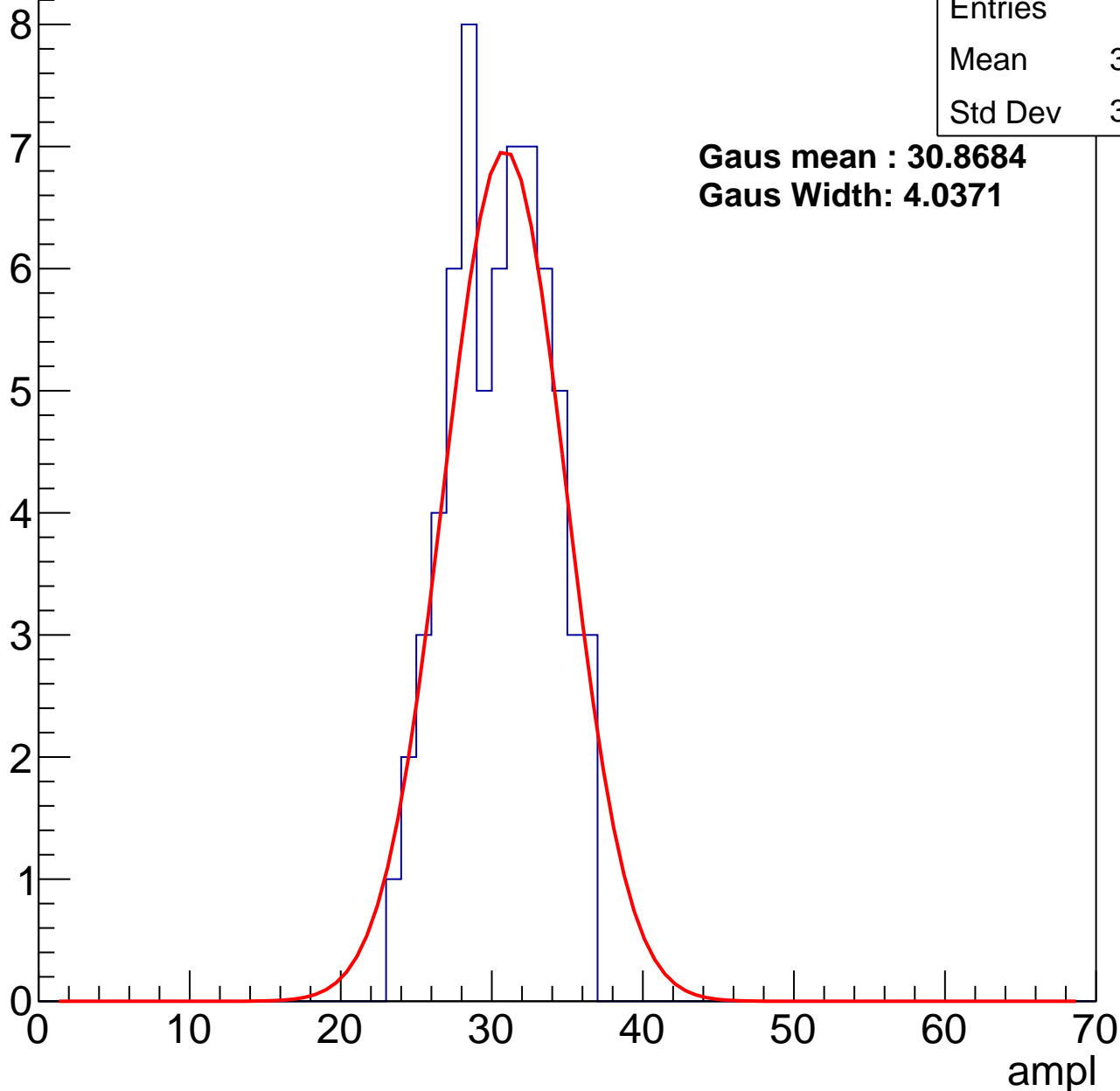
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	30.05
Std Dev	3.263

Gaus mean : 30.8684

Gaus Width: 4.0371



B0L000S, U4-ch22, adc1

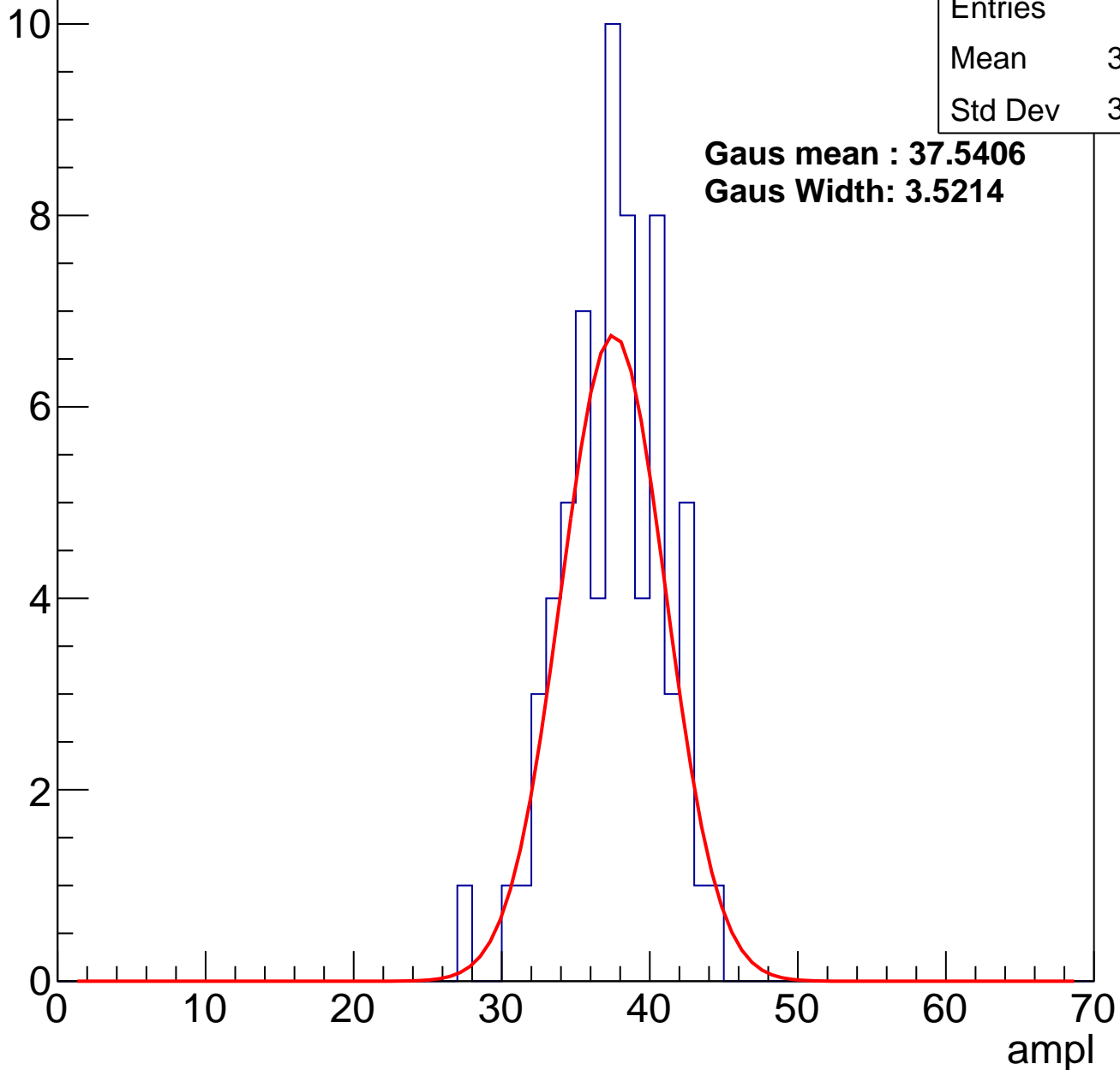
calib_packv5_042523_0143.root, FC#5, port B1

Entries	66
Mean	37.05
Std Dev	3.382

Gaus mean : 37.5406

Gaus Width: 3.5214

Entry

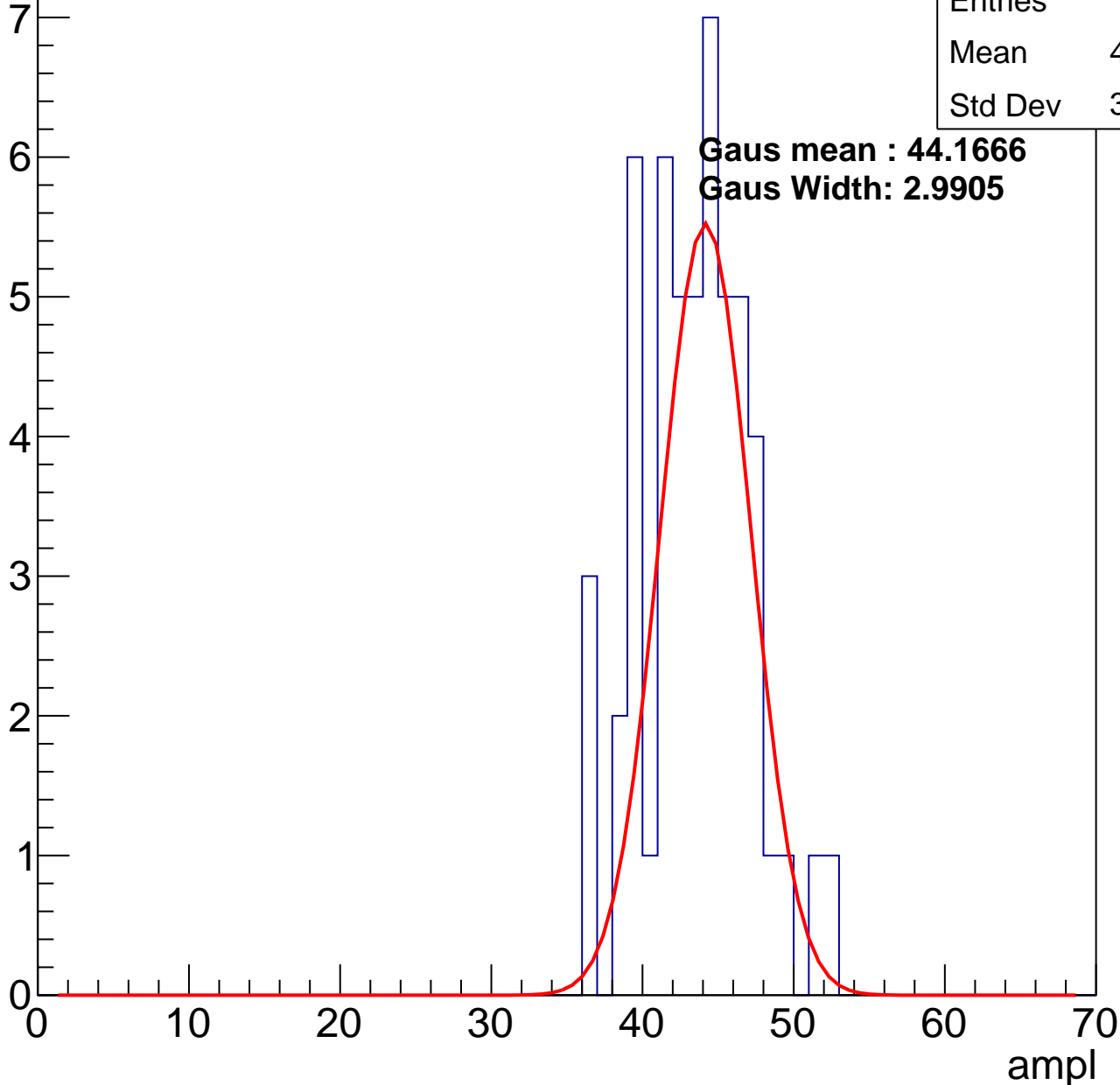


B0L000S, U4-ch22, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.02
Std Dev	3.563

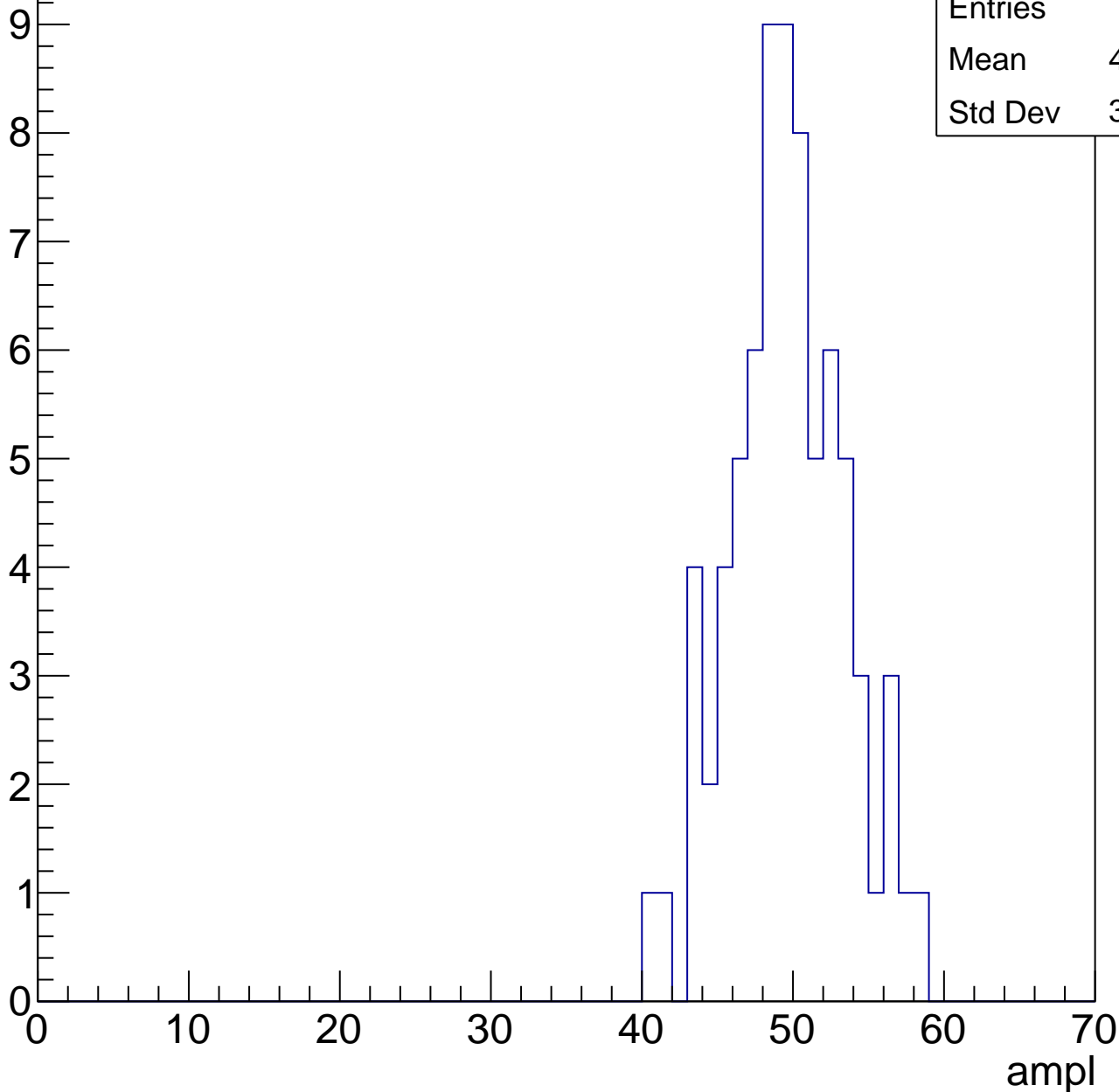


B0L000S, U4-ch22, adc3

calib_packv5_042523_0143.root, FC#5, port B1

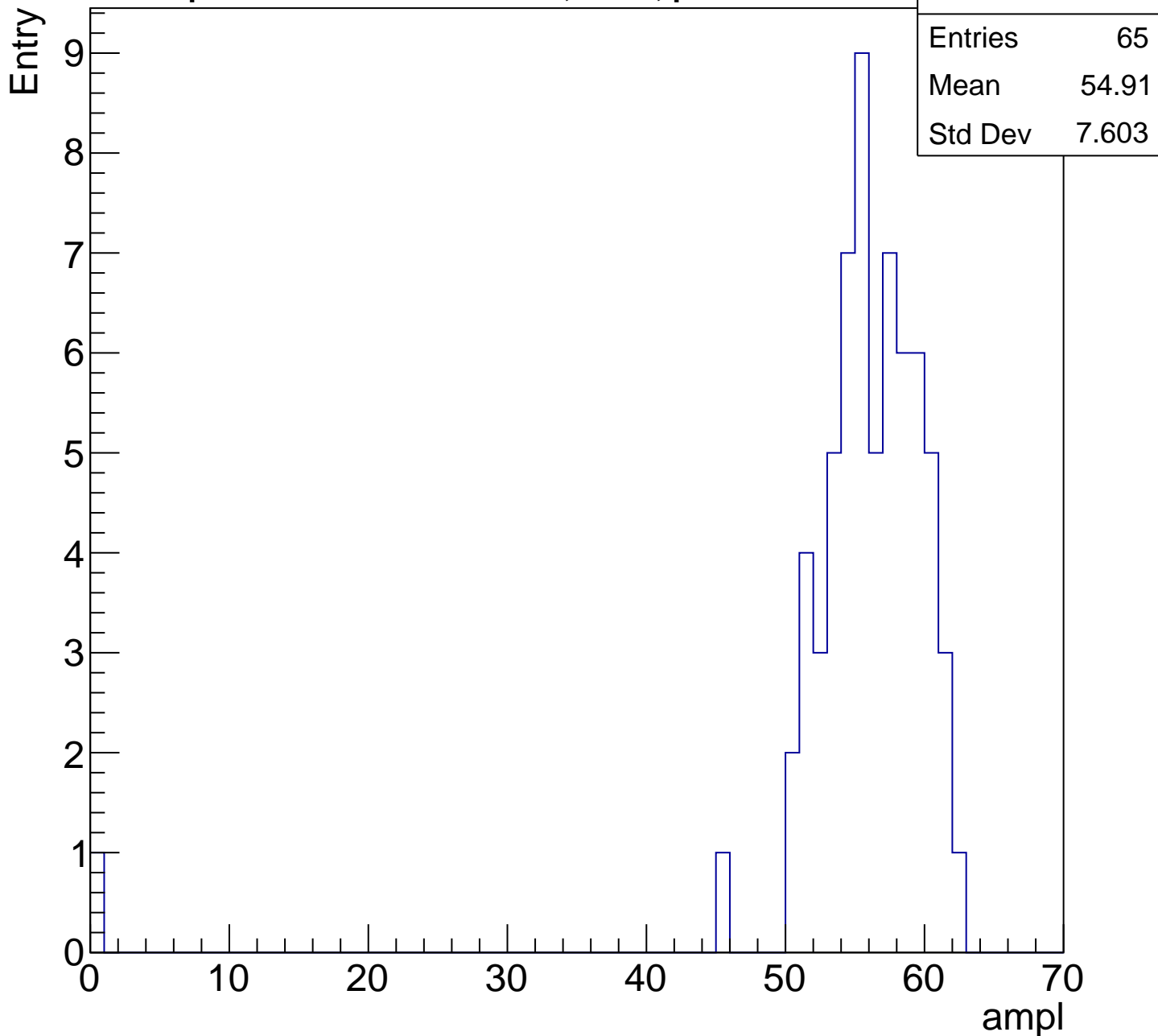
Entry

Entries	74
Mean	49.16
Std Dev	3.756



B0L000S, U4-ch22, adc4

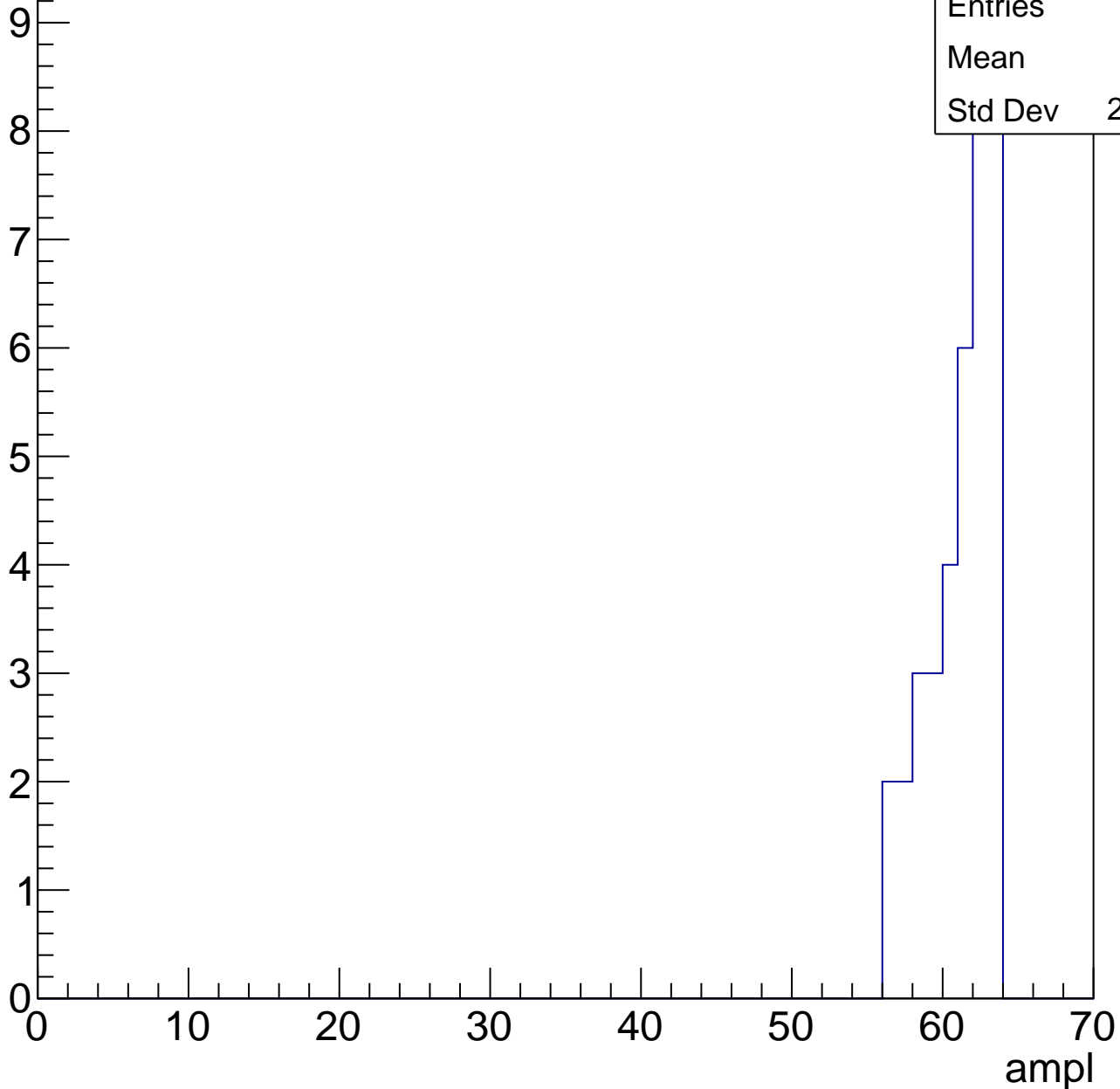
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch22, adc5

calib_packv5_042523_0143.root, FC#5, port B1

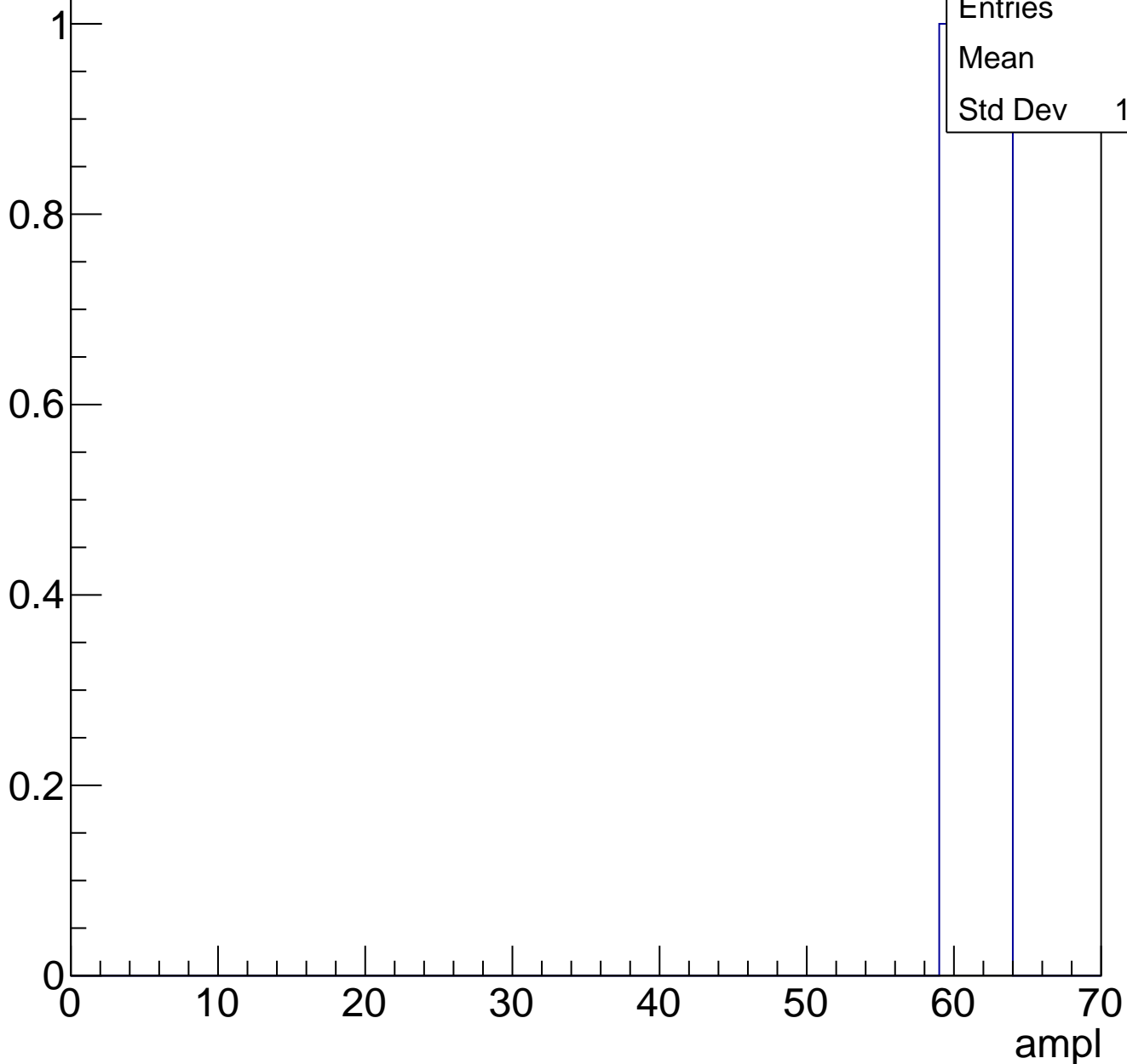
Entry



B0L000S, U4-ch22, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch22, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch23, adc0

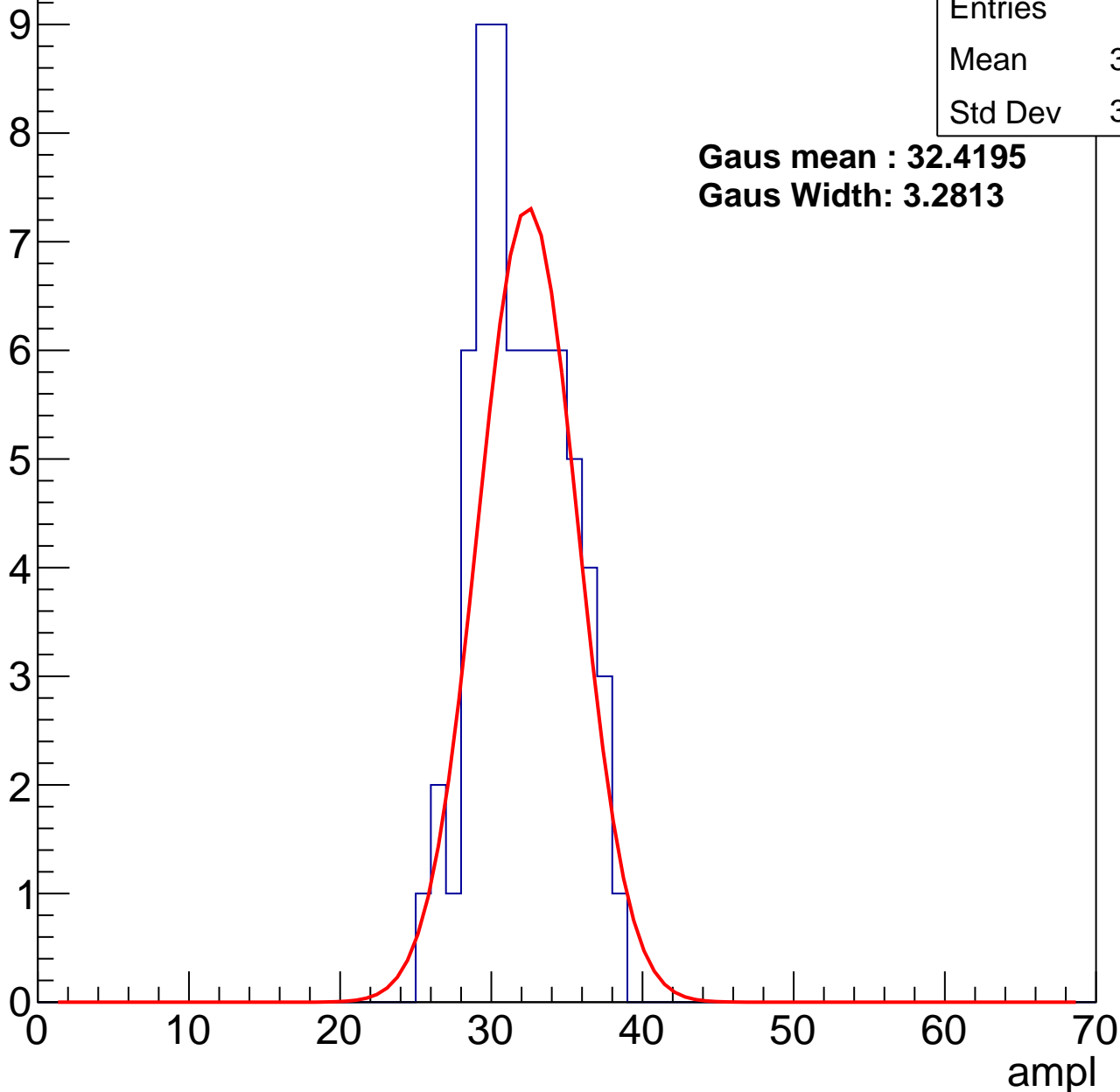
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	31.55
Std Dev	3.043

Gaus mean : 32.4195

Gaus Width: 3.2813



B0L000S, U4-ch23, adc1

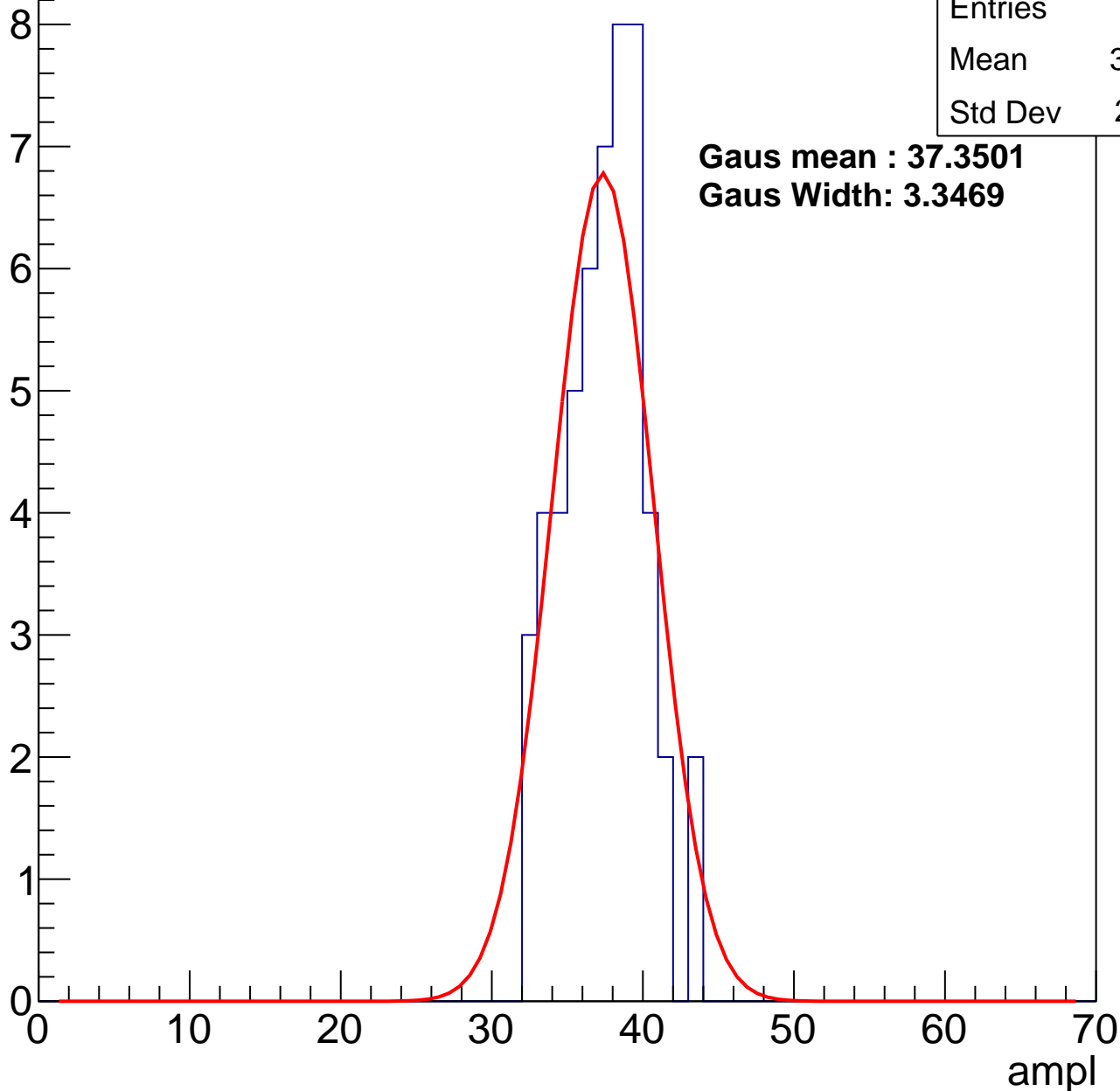
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.94
Std Dev	2.681

Gaus mean : 37.3501

Gaus Width: 3.3469



B0L000S, U4-ch23, adc2

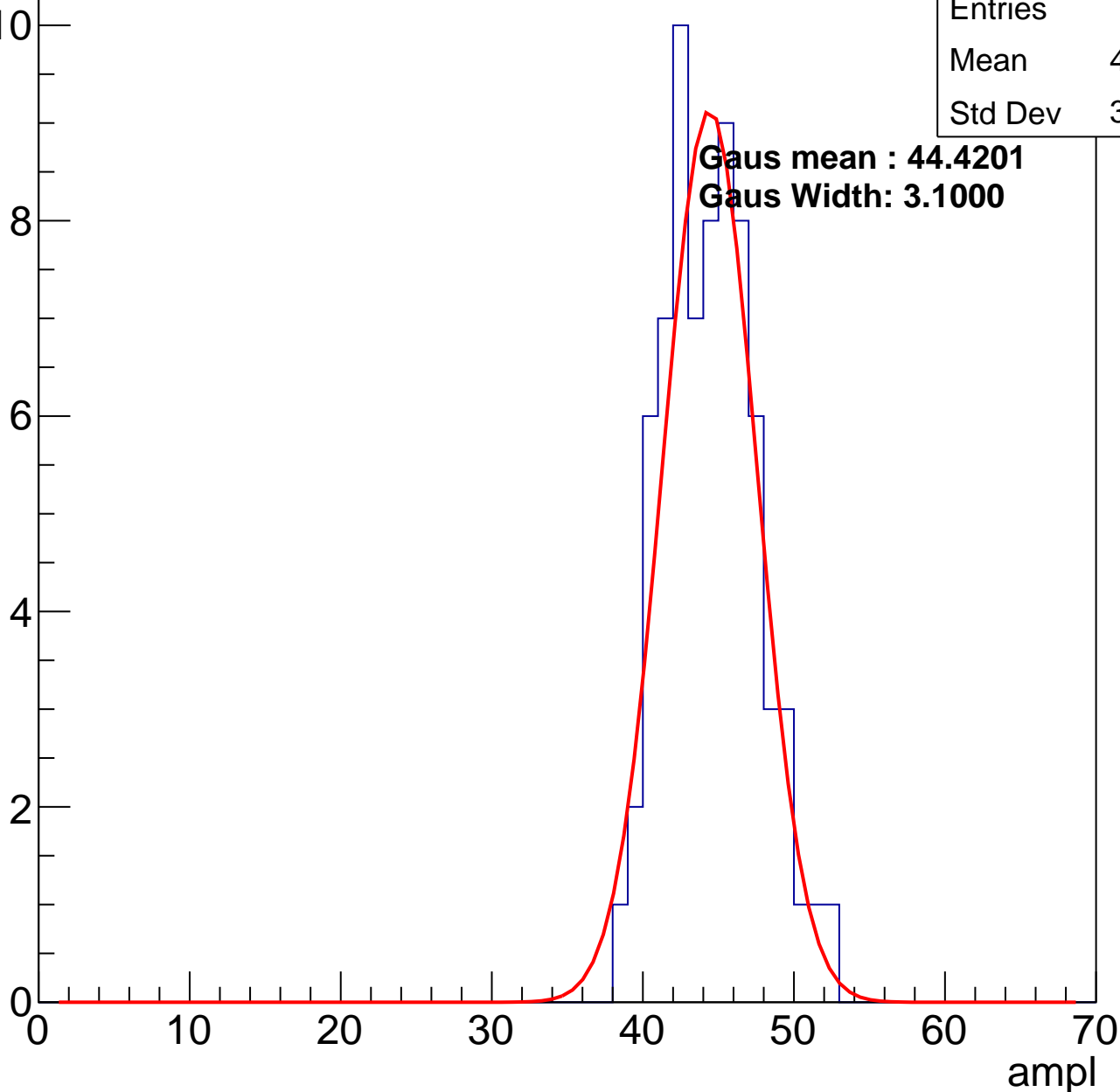
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	44.04
Std Dev	3.004

Gaus mean : 44.4201

Gaus Width: 3.1000

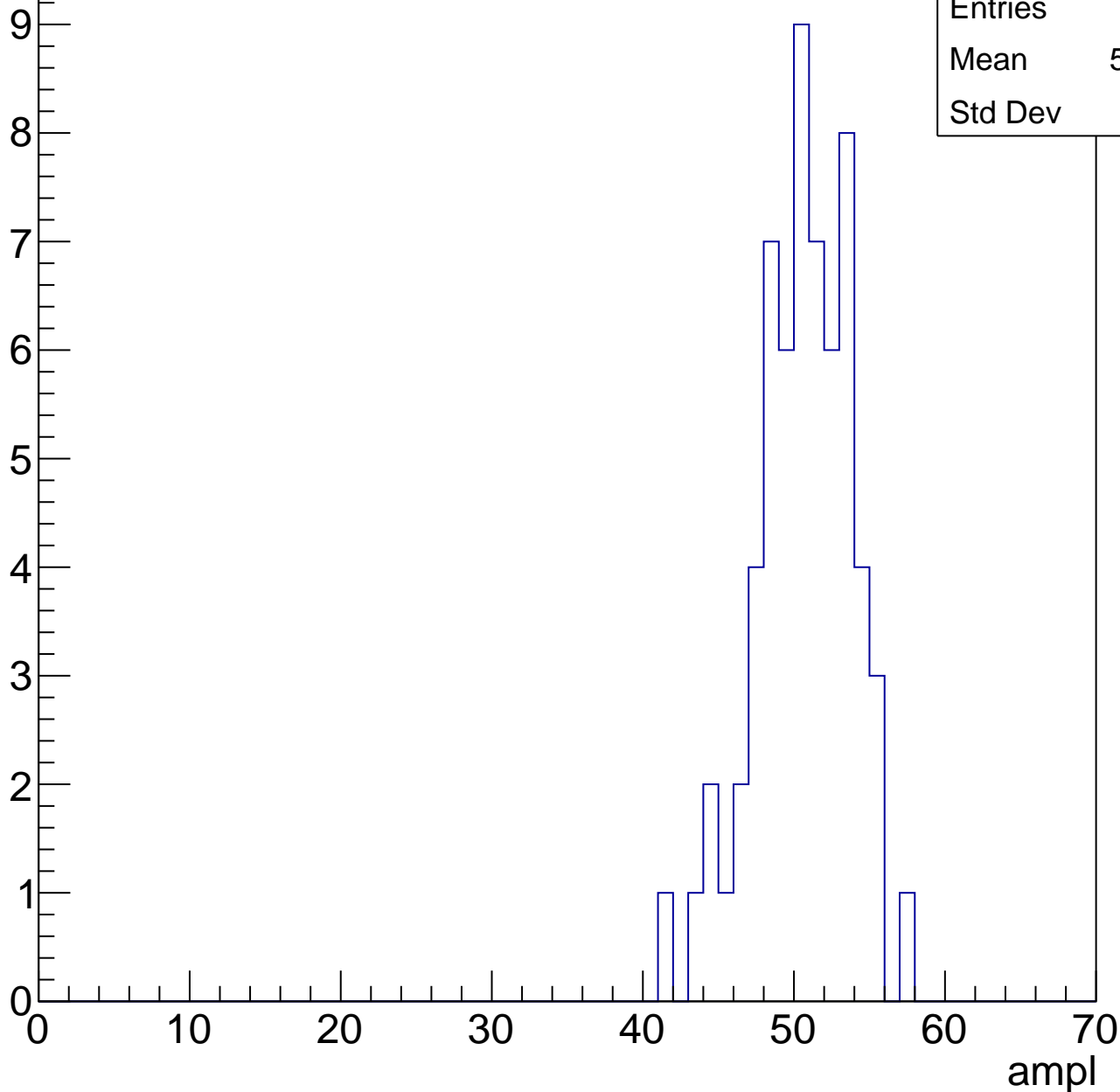


B0L000S, U4-ch23, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.13
Std Dev	3.16



B0L000S, U4-ch23, adc4

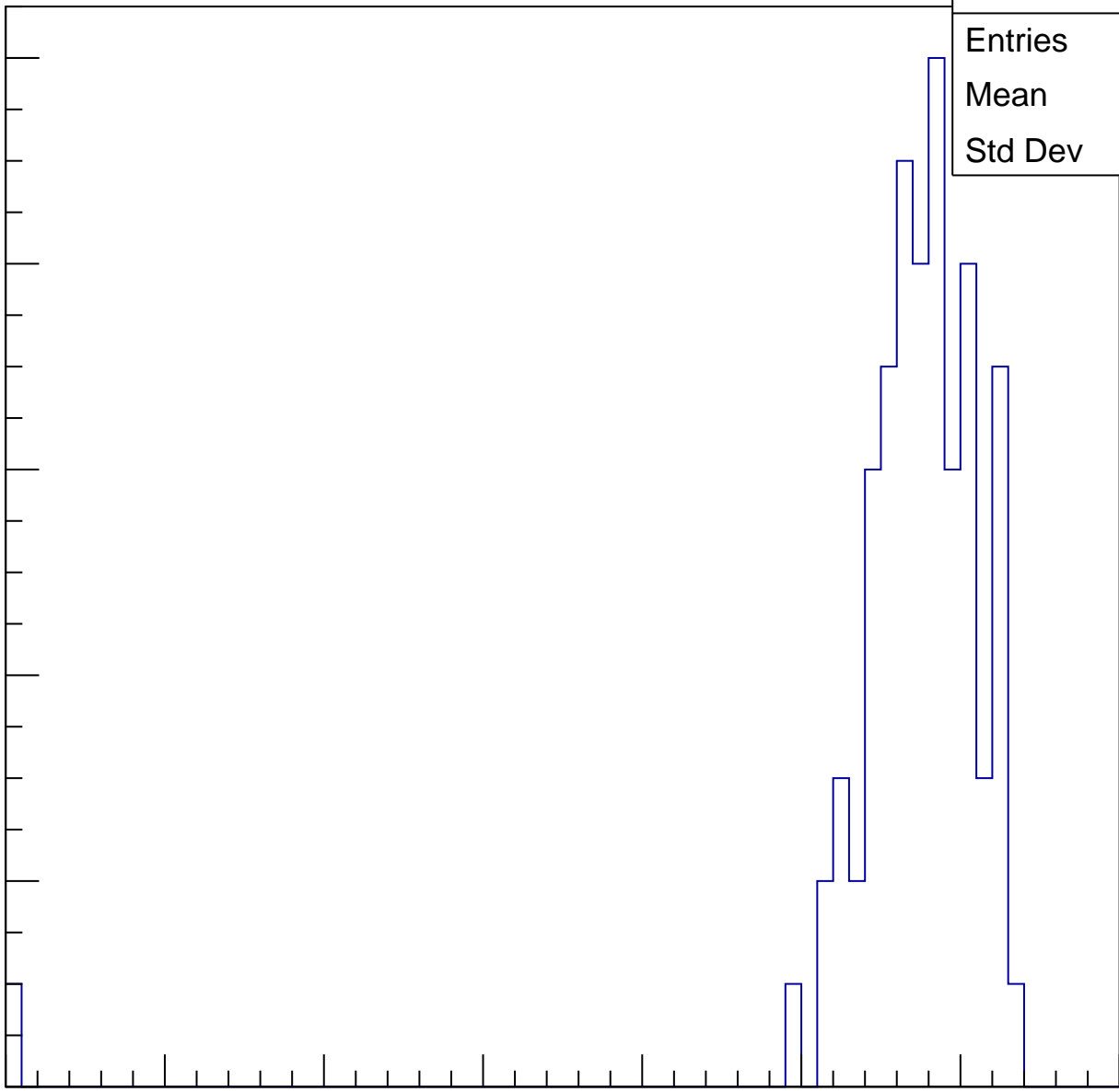
calib_packv5_042523_0143.root, FC#5, port B1

Entries	74
Mean	56.43
Std Dev	7.282

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

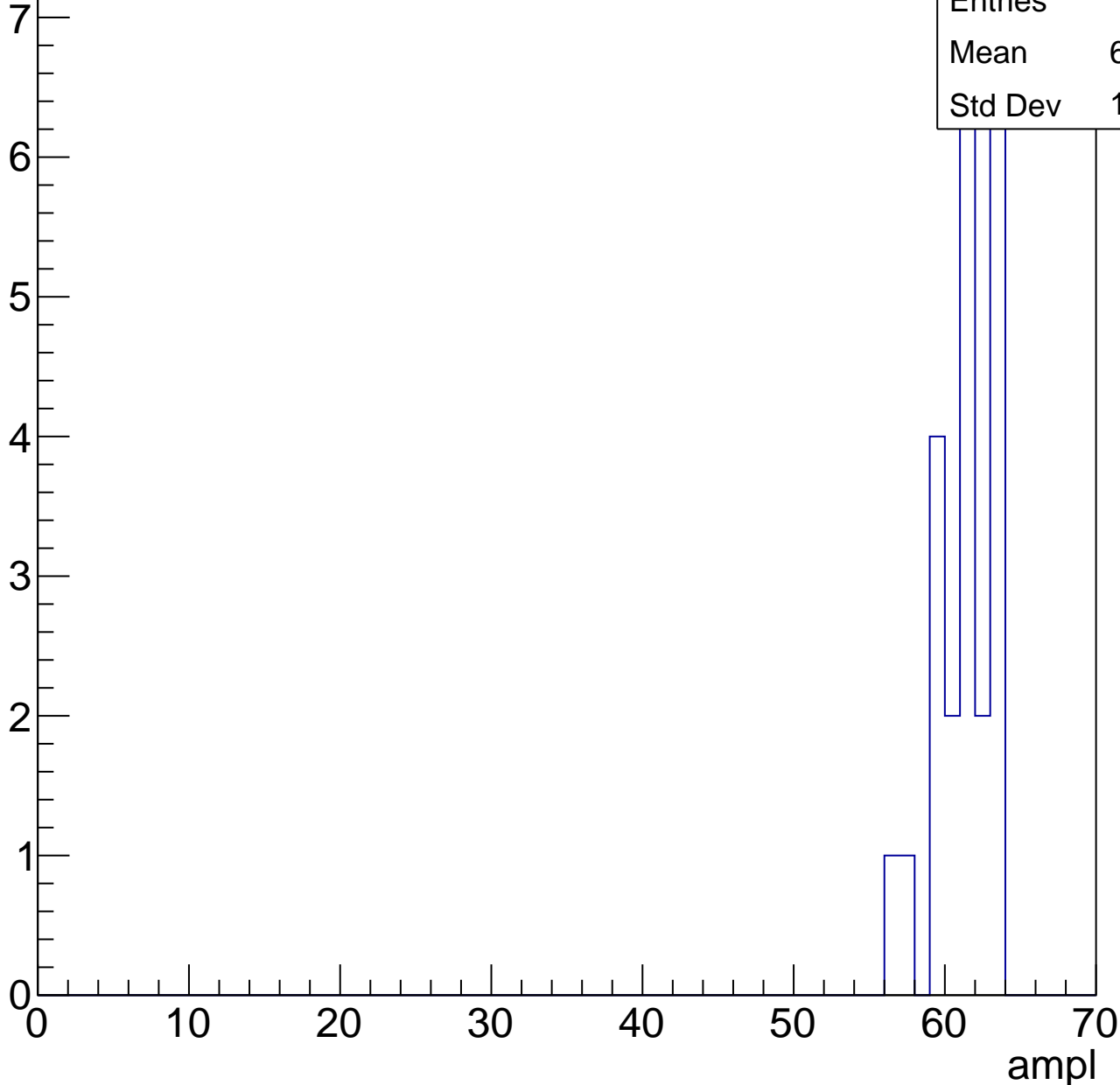


B0L000S, U4-ch23, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	24
Mean	60.88
Std Dev	1.922



B0L000S, U4-ch23, adc6

calib_packv5_042523_0143.root, FC#5, port B1

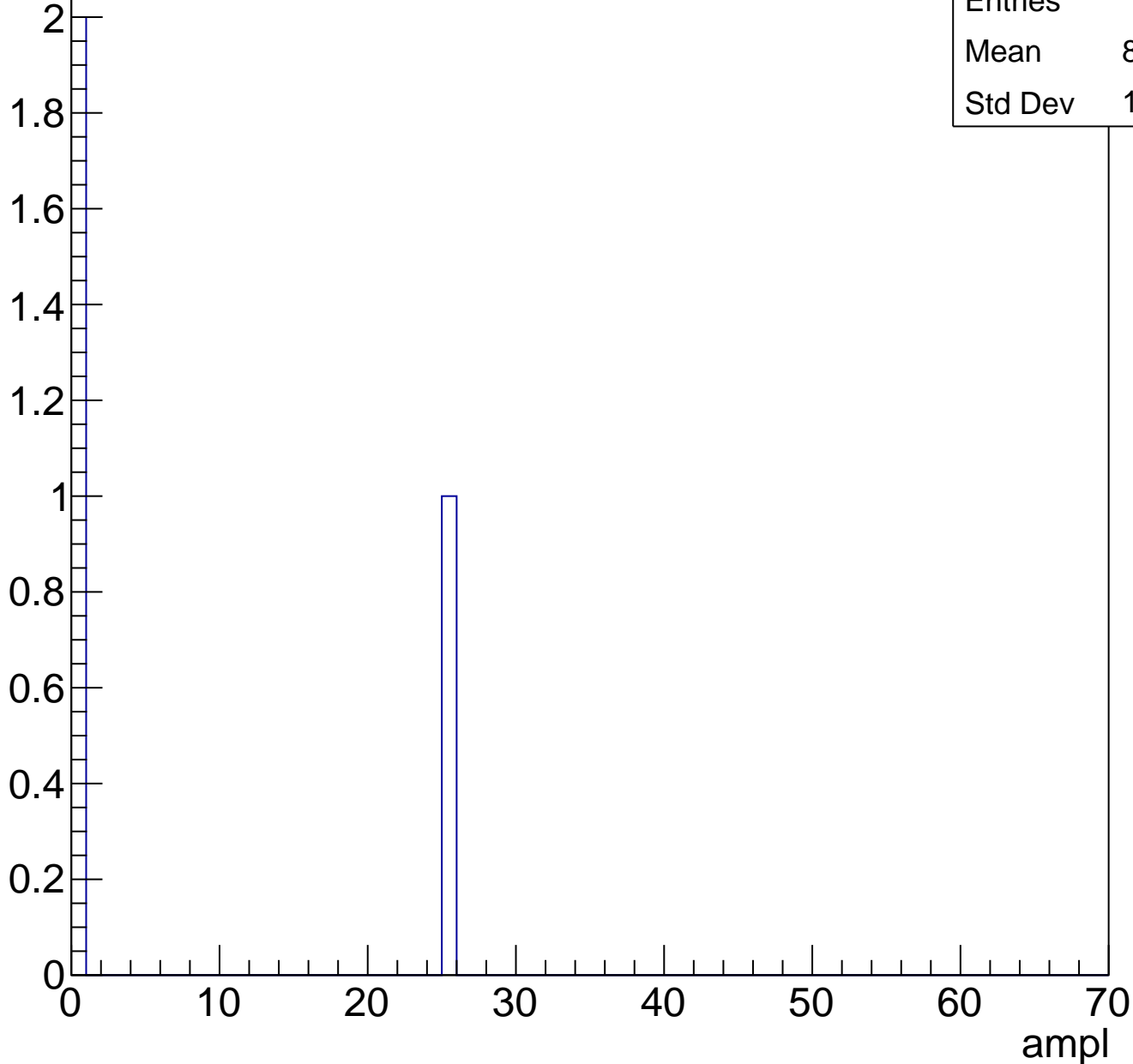
Entry



B0L000S, U4-ch23, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L000S, U4-ch24, adc0

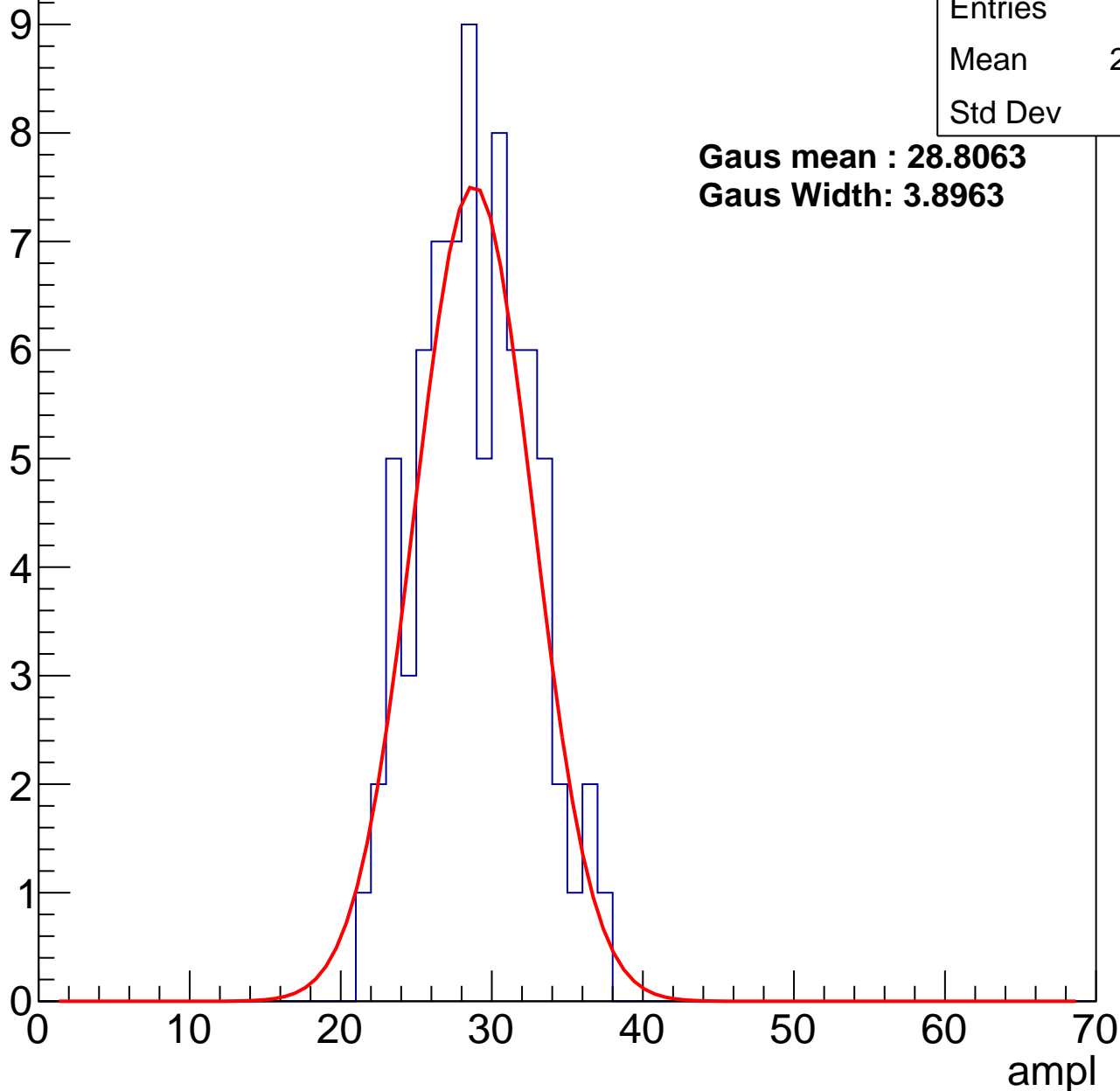
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	28.49
Std Dev	3.64

Gaus mean : 28.8063

Gaus Width: 3.8963



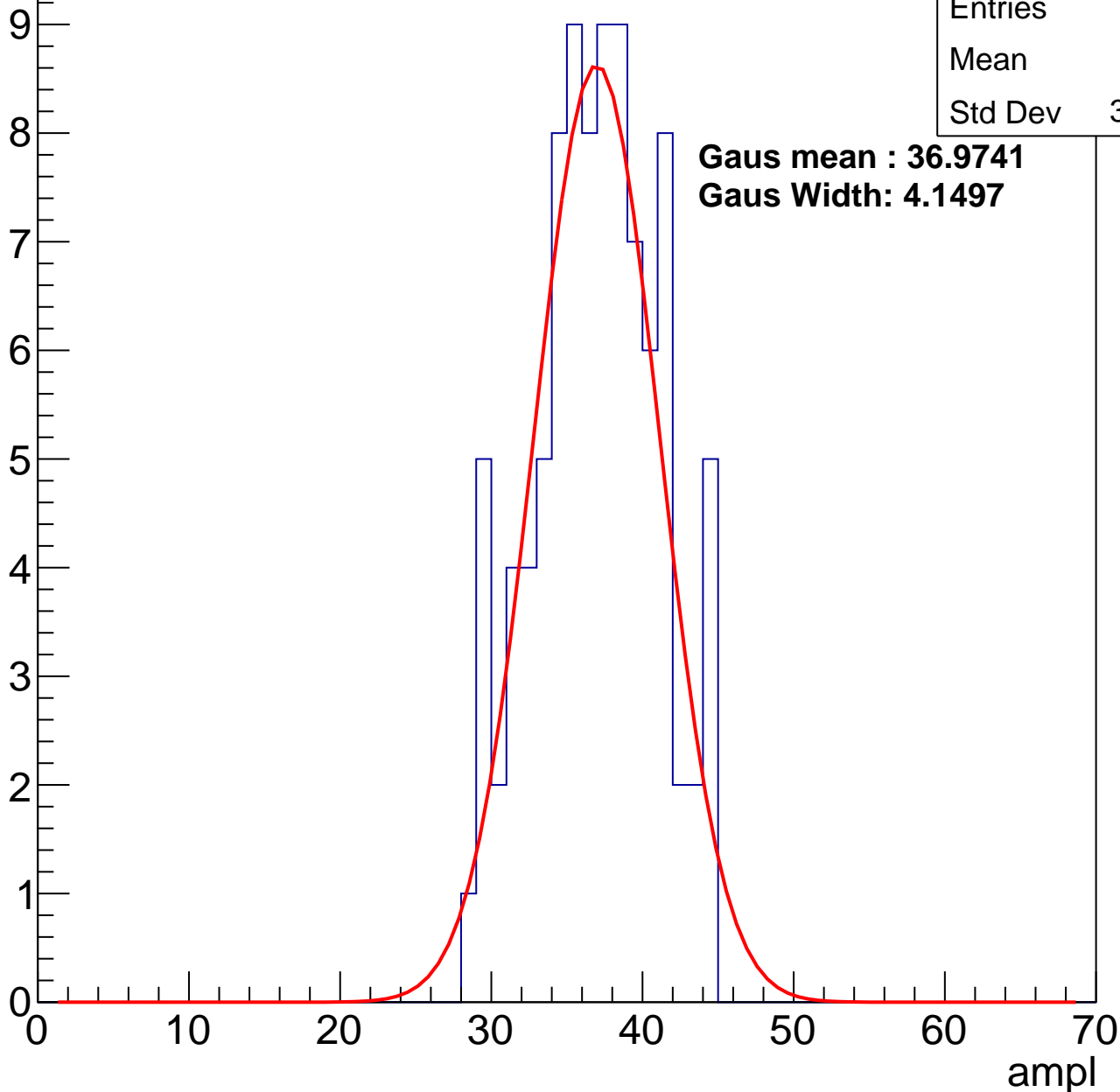
B0L000S, U4-ch24, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	94
Mean	36.5
Std Dev	3.997

Gaus mean : 36.9741
Gaus Width: 4.1497



B0L000S, U4-ch24, adc2

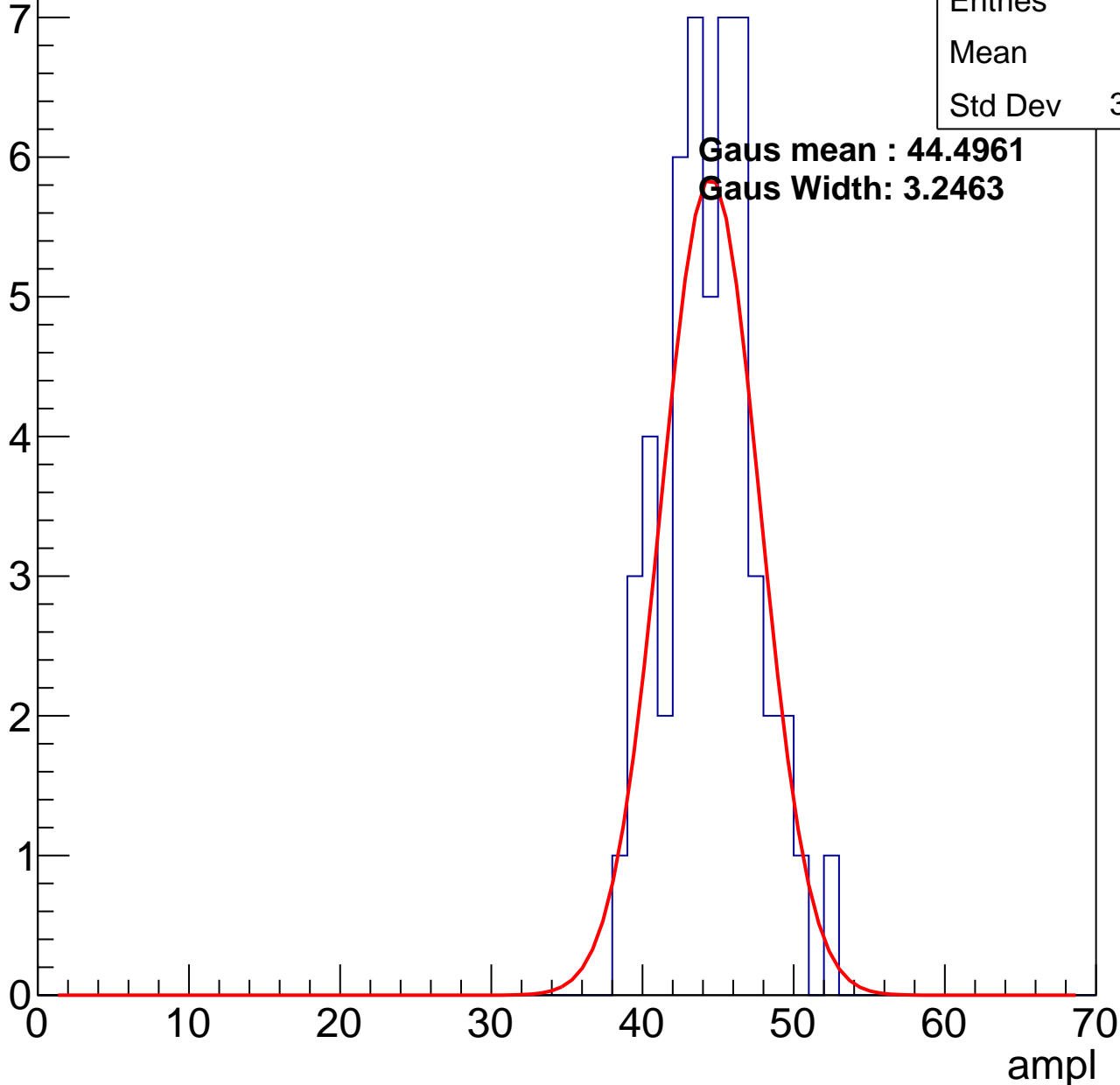
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	44
Std Dev	3.029

Gaus mean : 44.4961

Gaus Width: 3.2463

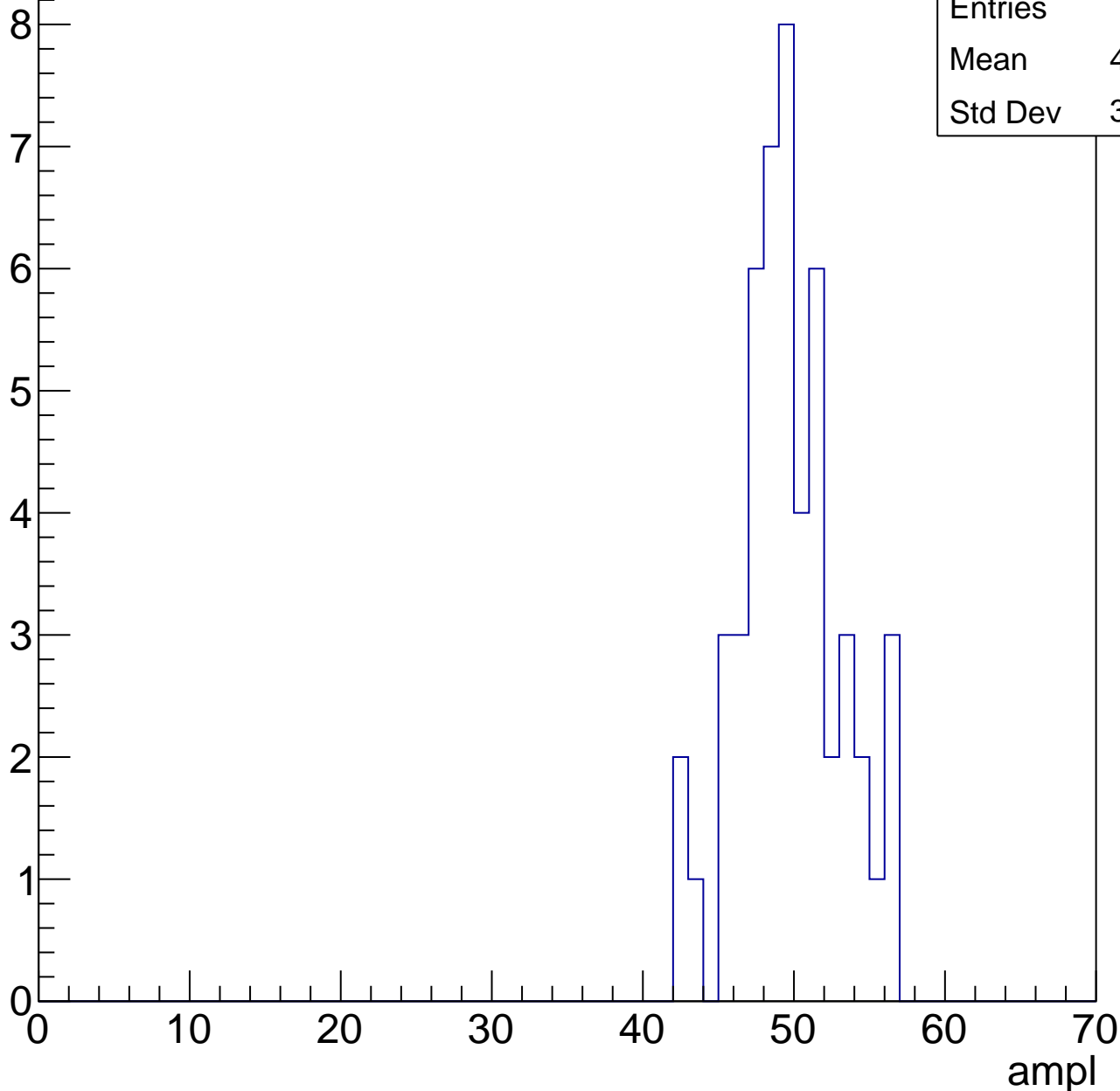


B0L000S, U4-ch24, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	49.22
Std Dev	3.327

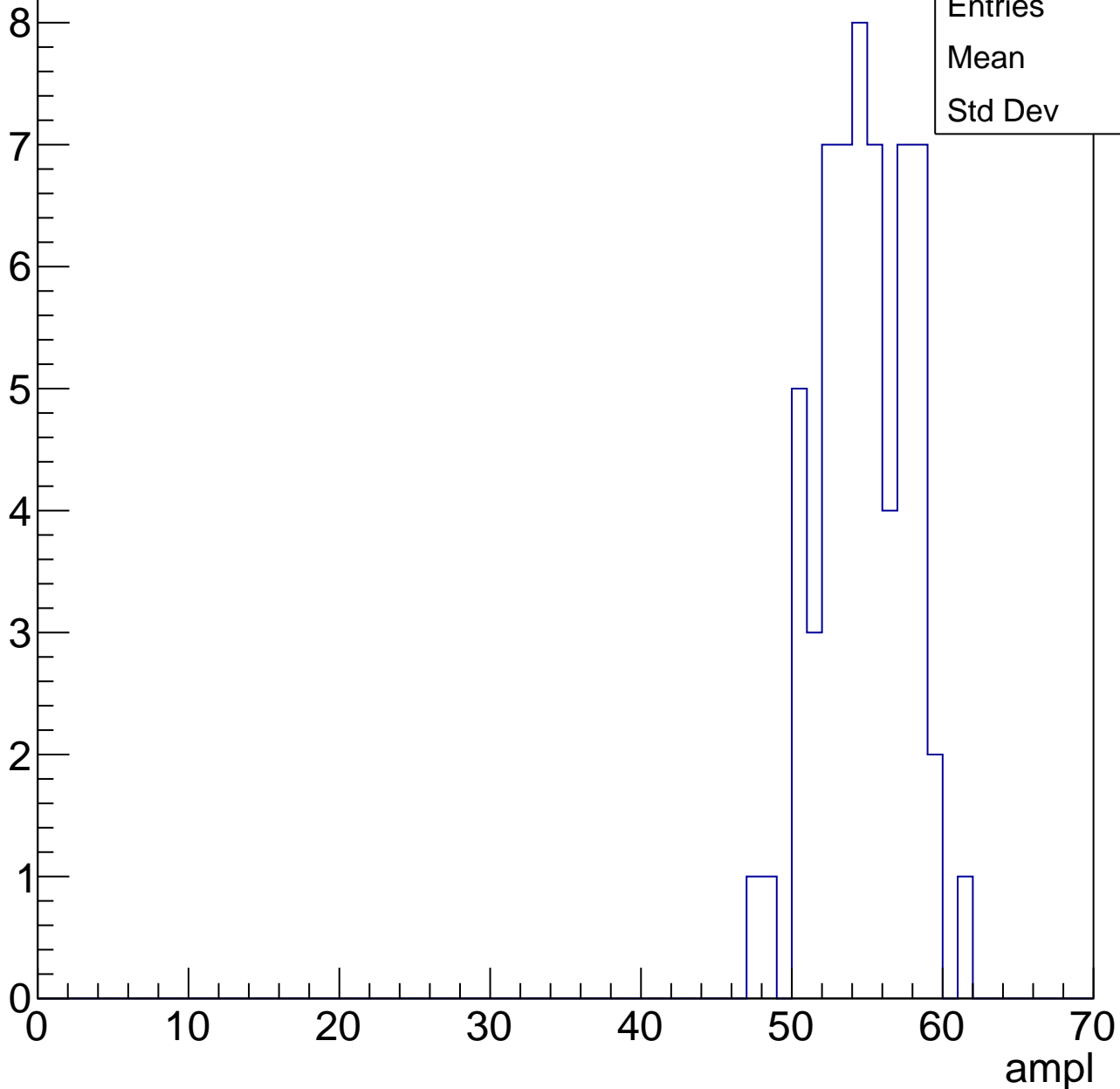


B0L000S, U4-ch24, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.3
Std Dev	2.94



B0L000S, U4-ch24, adc5

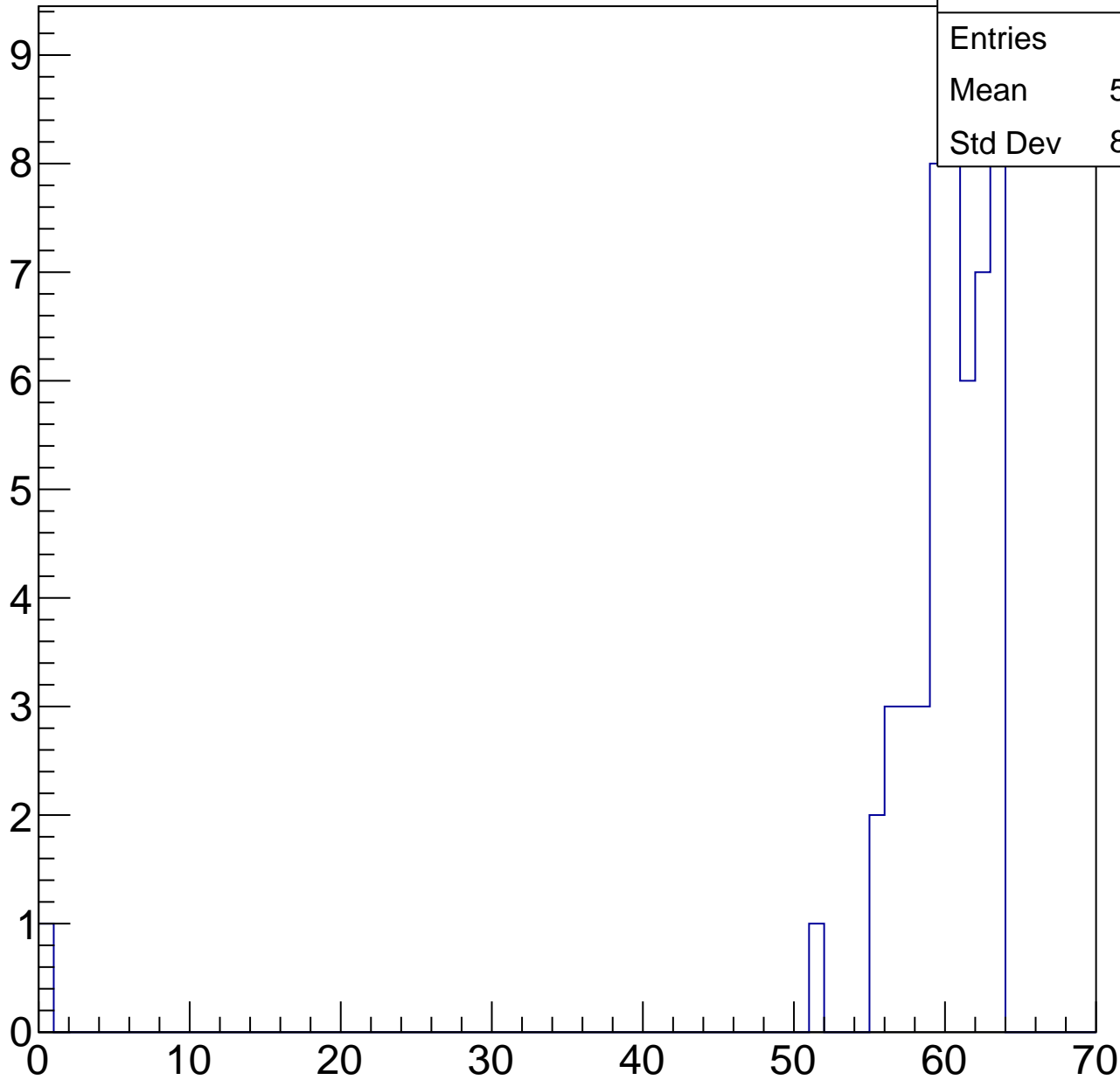
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.63
Std Dev	8.673

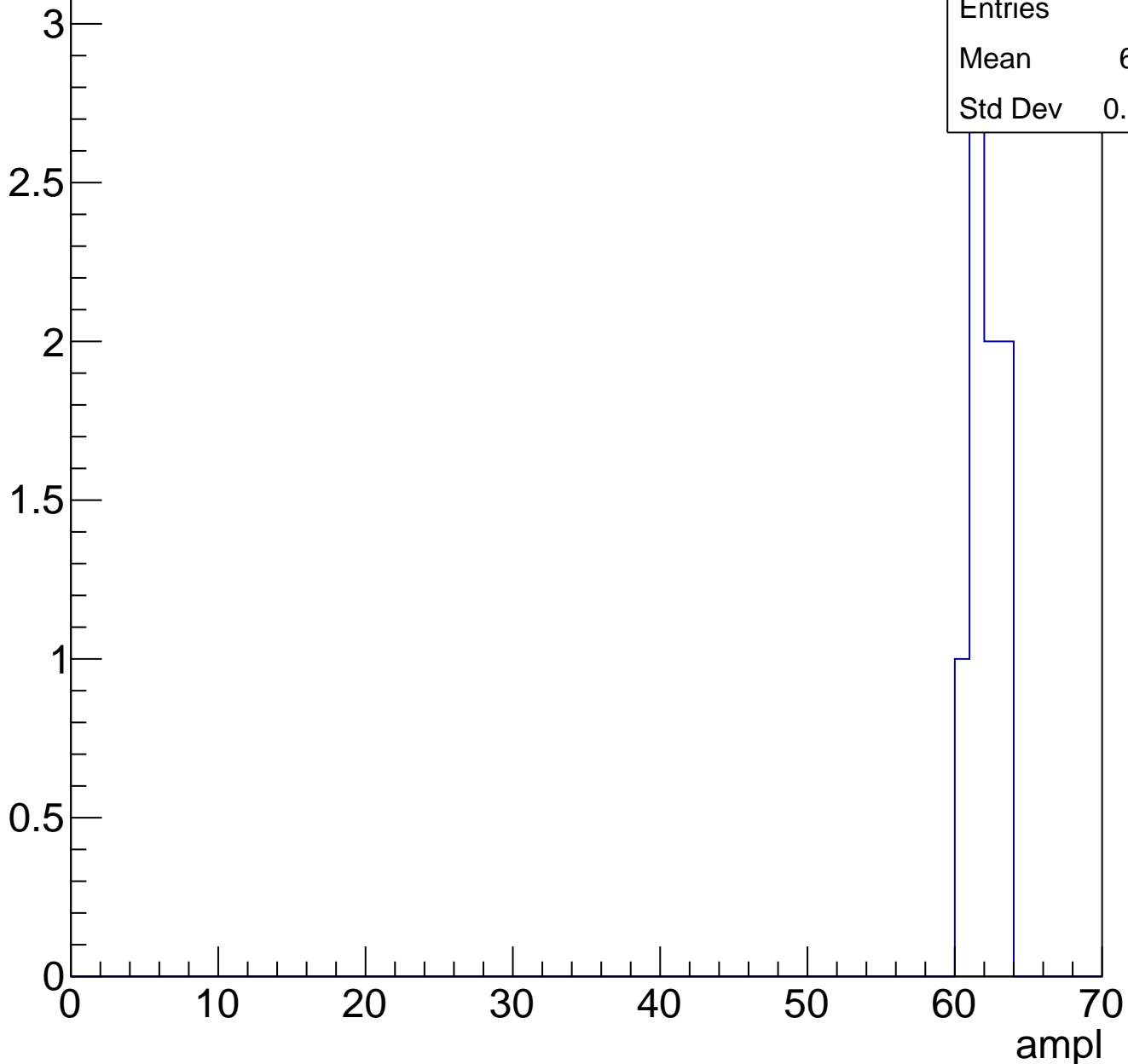
ampl



B0L000S, U4-ch24, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

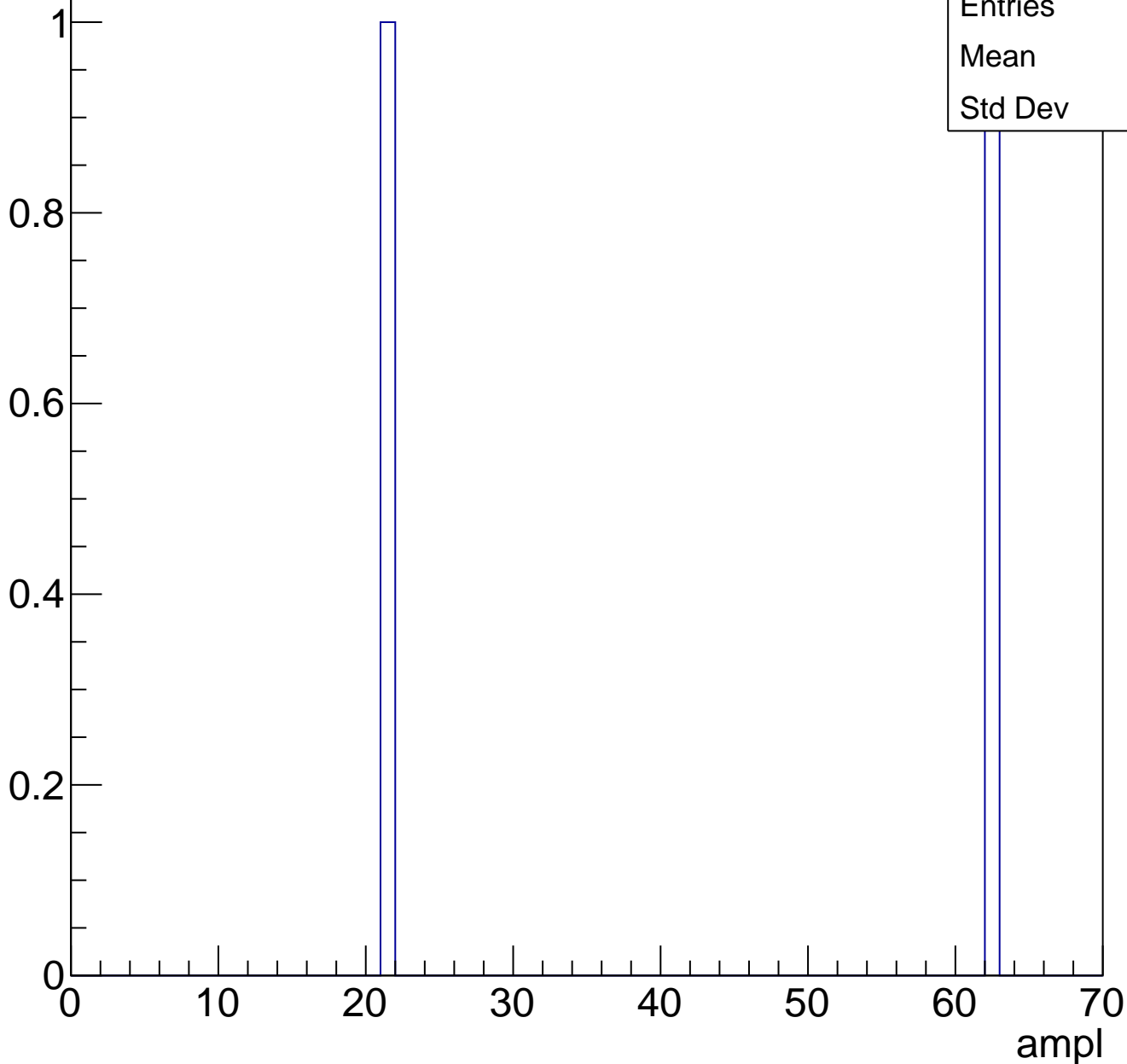


Entries	8
Mean	61.62
Std Dev	0.9922

B0L000S, U4-ch24, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch25, adc0

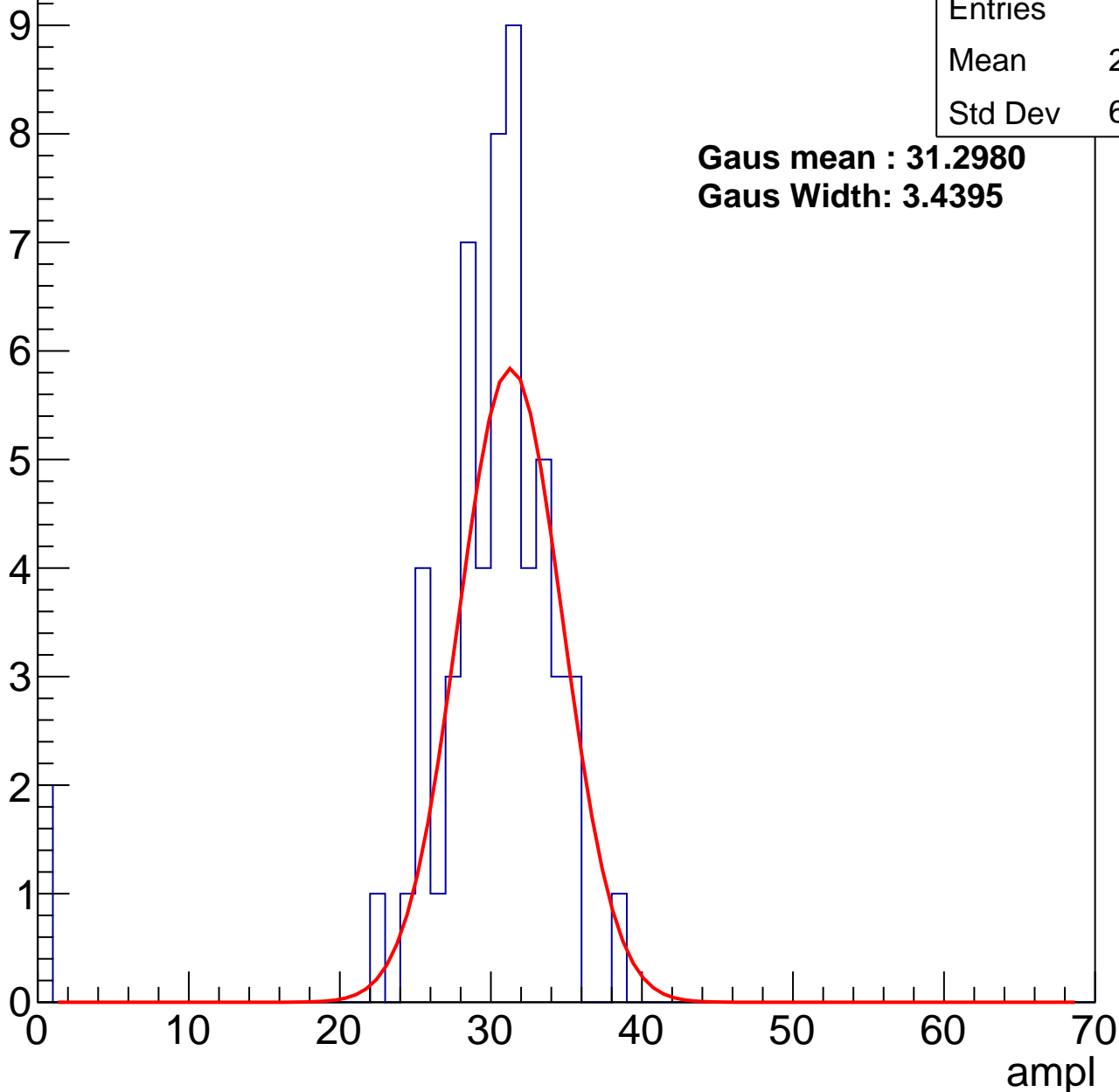
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	28.96
Std Dev	6.369

Gaus mean : 31.2980

Gaus Width: 3.4395



B0L000S, U4-ch25, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	36.98
Std Dev	3.698

Gaus mean : 37.6726

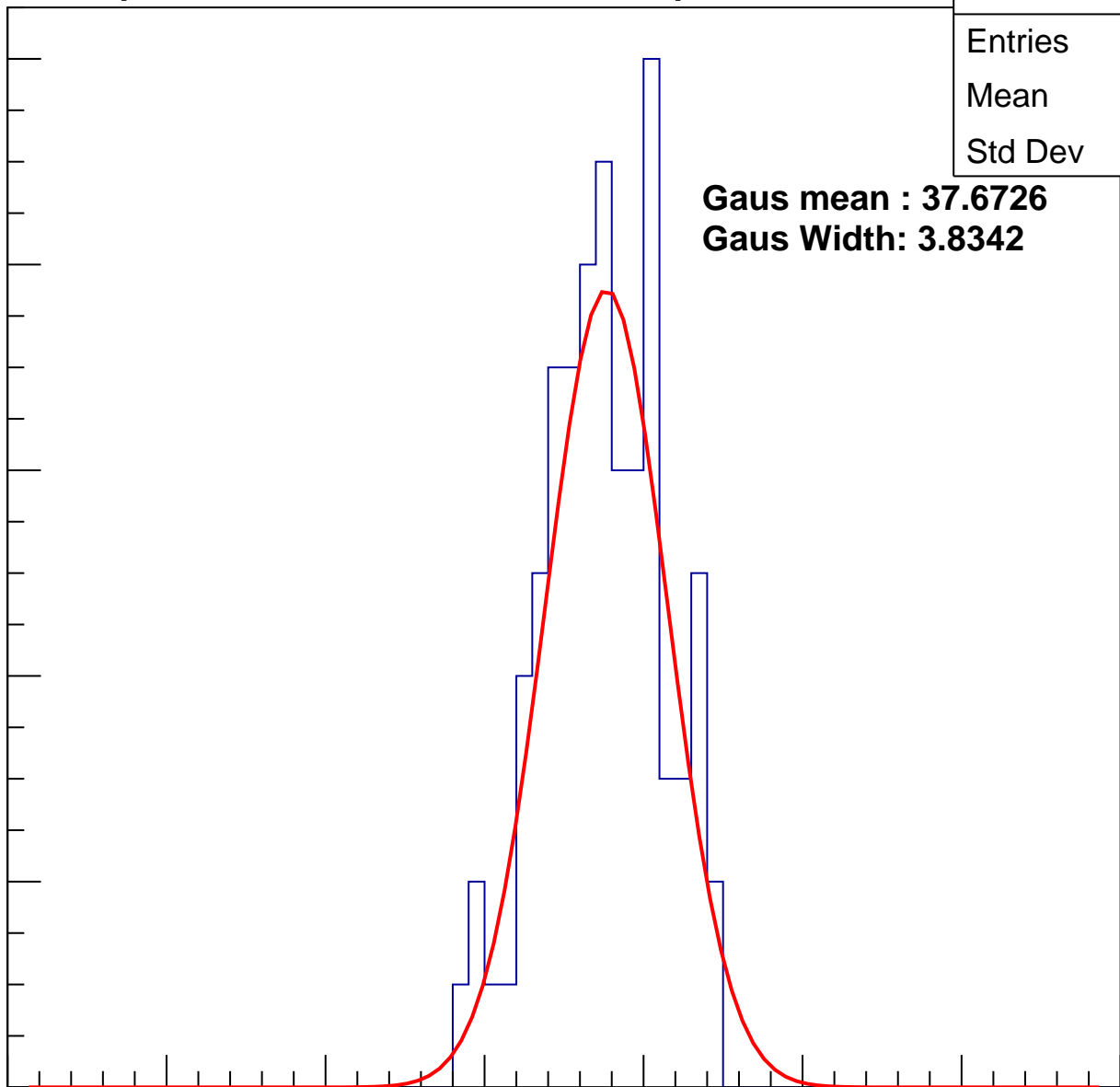
Gaus Width: 3.8342

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch25, adc2

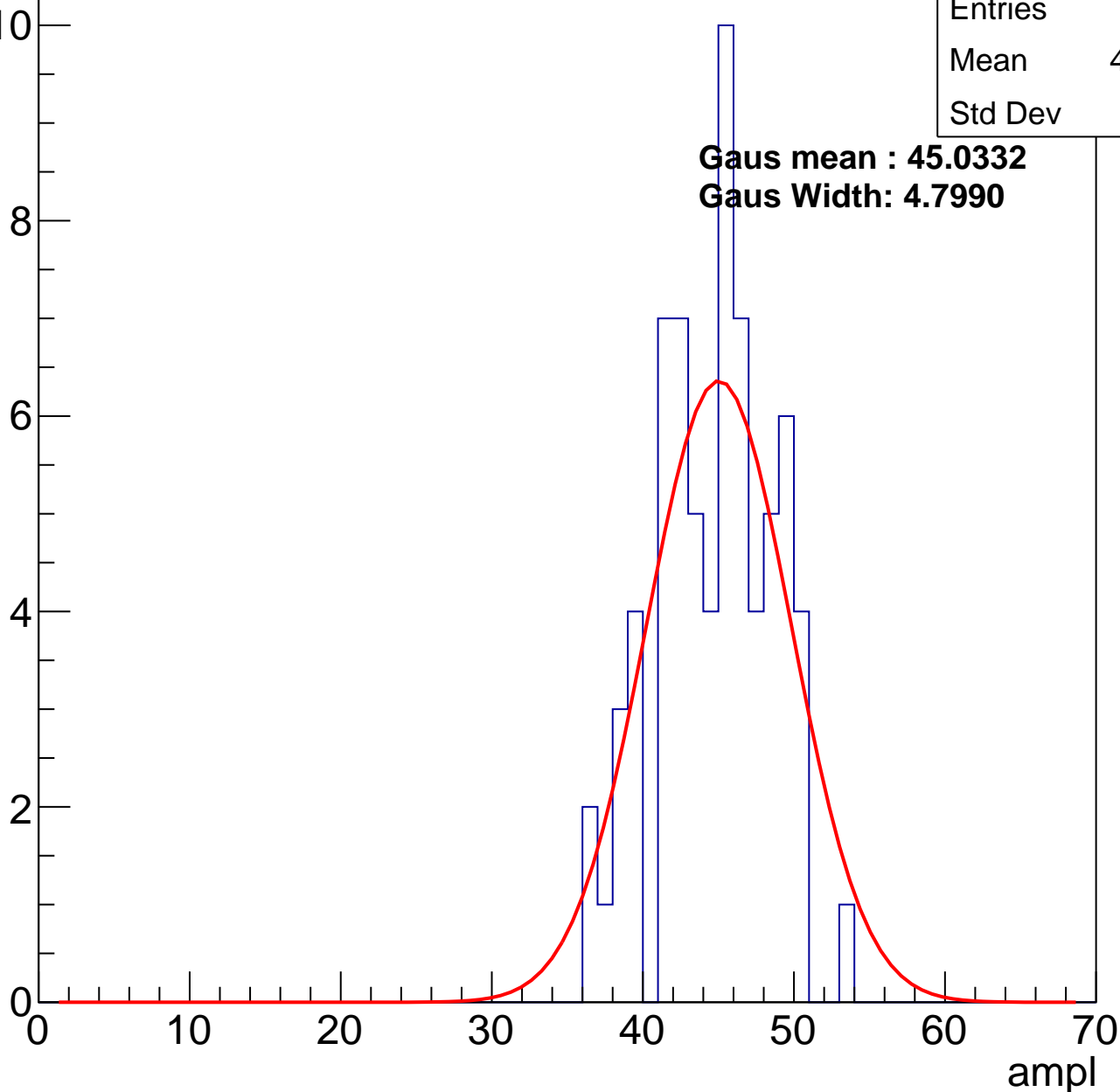
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	44.26
Std Dev	3.79

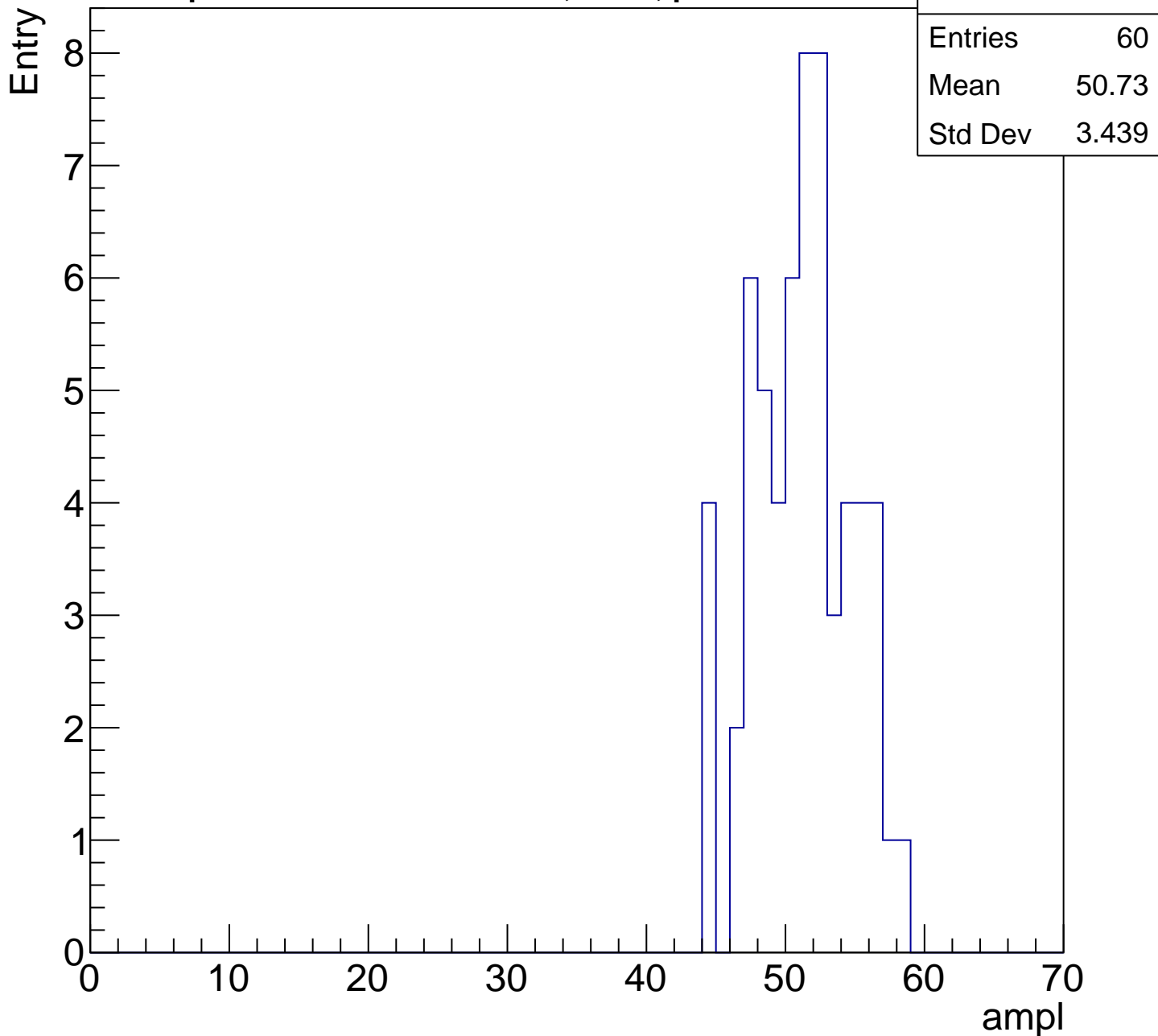
Gaus mean : 45.0332

Gaus Width: 4.7990



B0L000S, U4-ch25, adc3

calib_packv5_042523_0143.root, FC#5, port B1

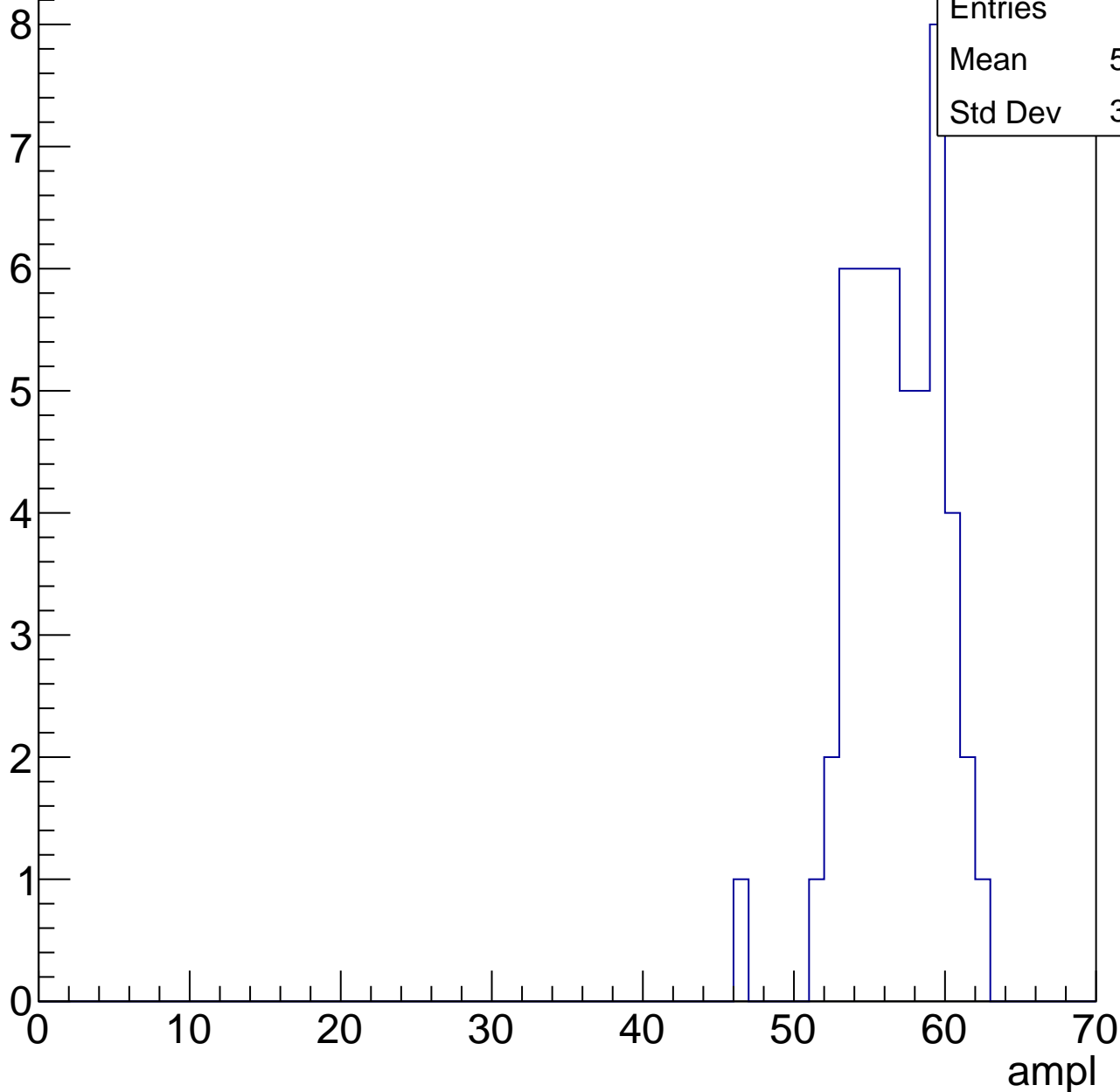


B0L000S, U4-ch25, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.23
Std Dev	3.026



B0L000S, U4-ch25, adc5

calib_packv5_042523_0143.root, FC#5, port B1

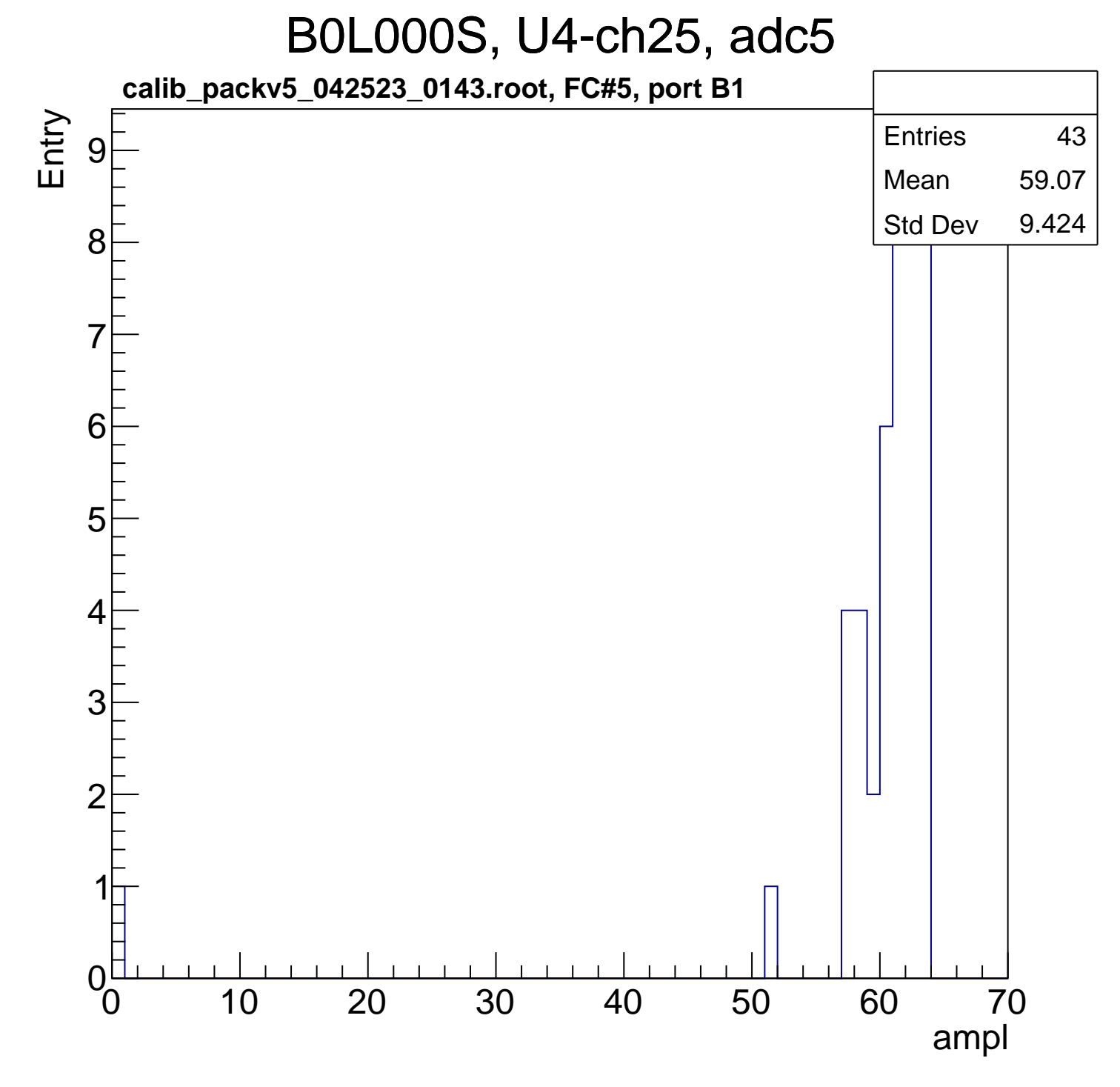
Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.07
Std Dev	9.424

ampl

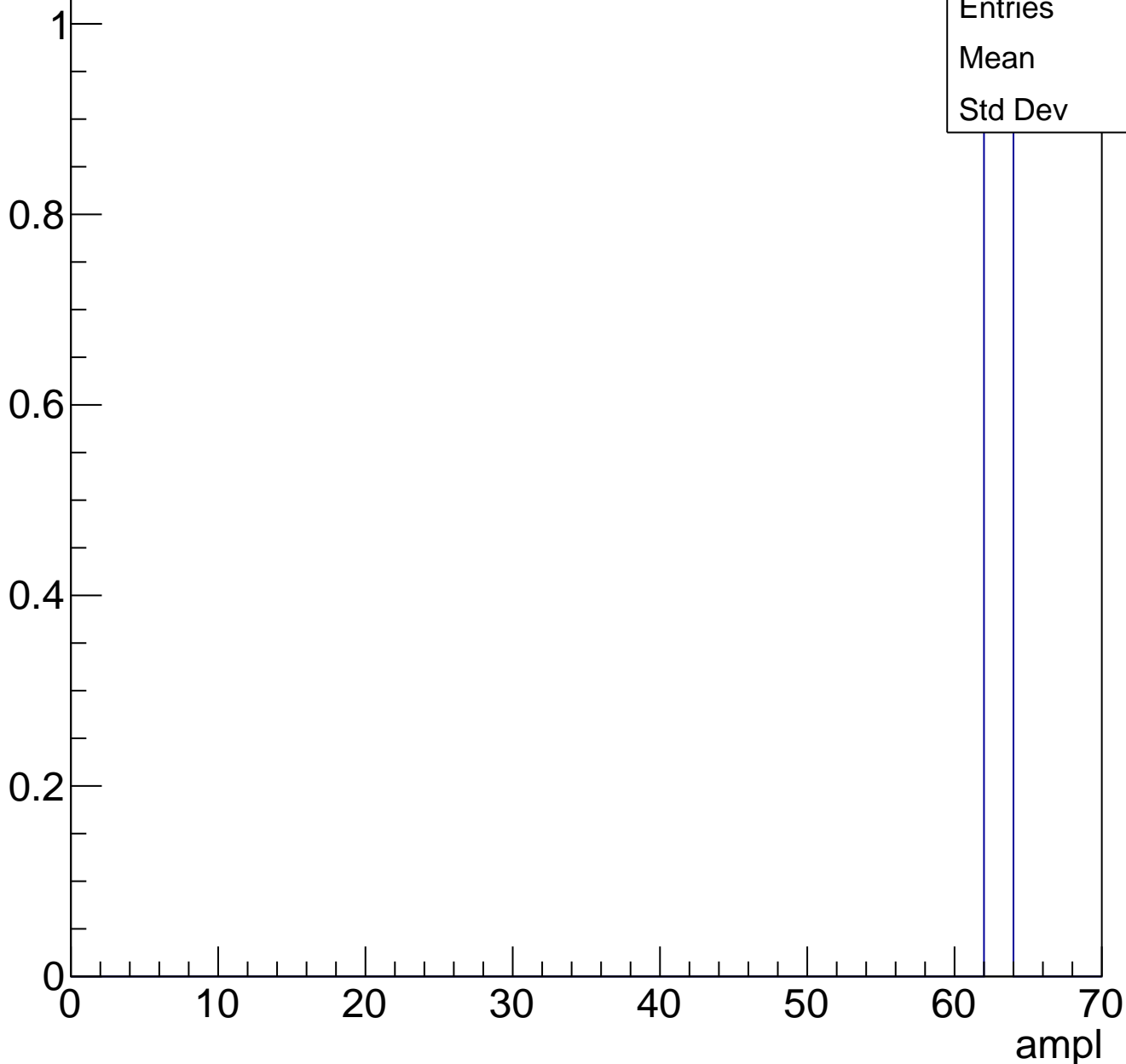
0 10 20 30 40 50 60 70



B0L000S, U4-ch25, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch25, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch26, adc0

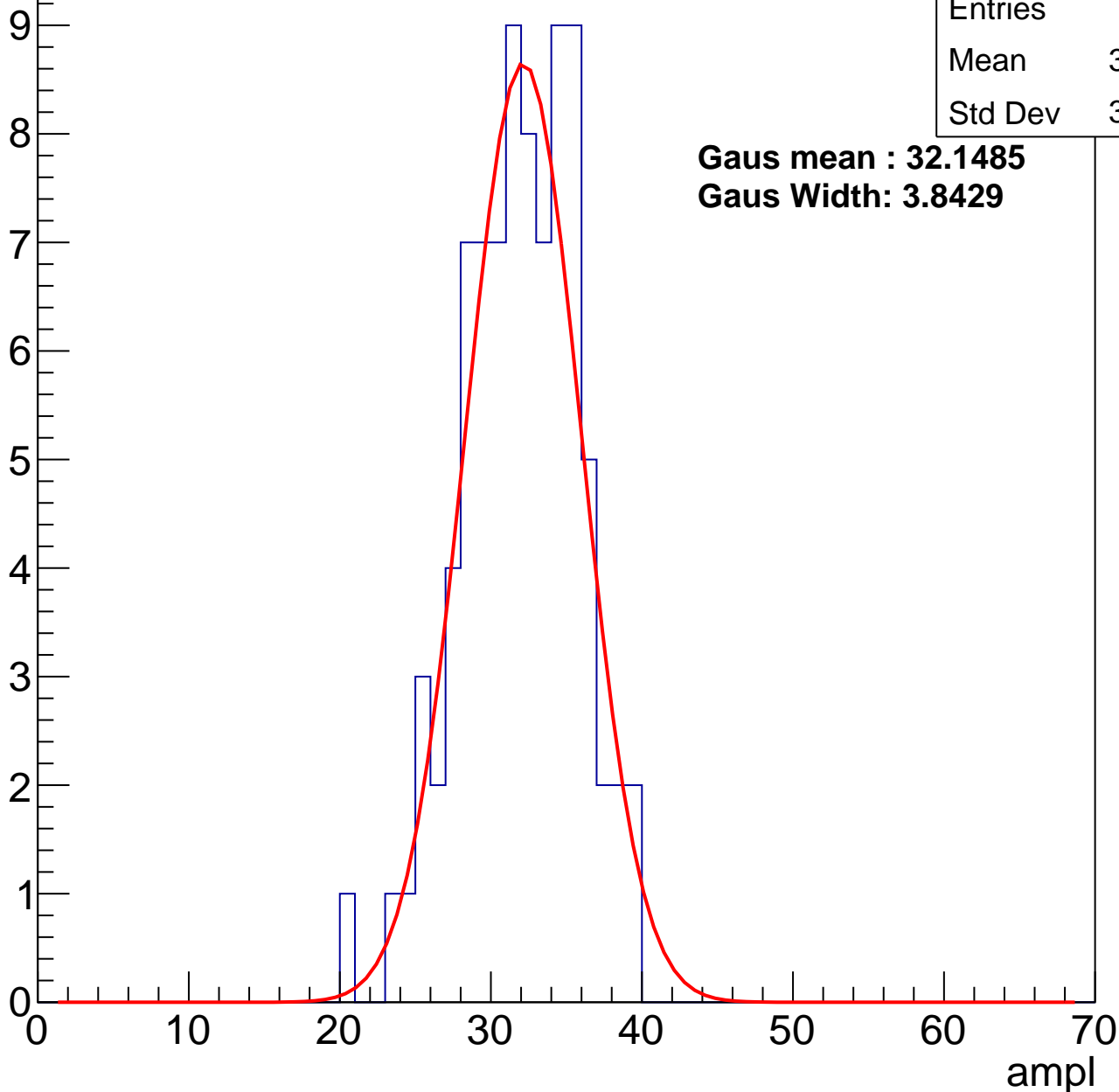
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	86
Mean	31.47
Std Dev	3.766

Gaus mean : 32.1485

Gaus Width: 3.8429



B0L000S, U4-ch26, adc1

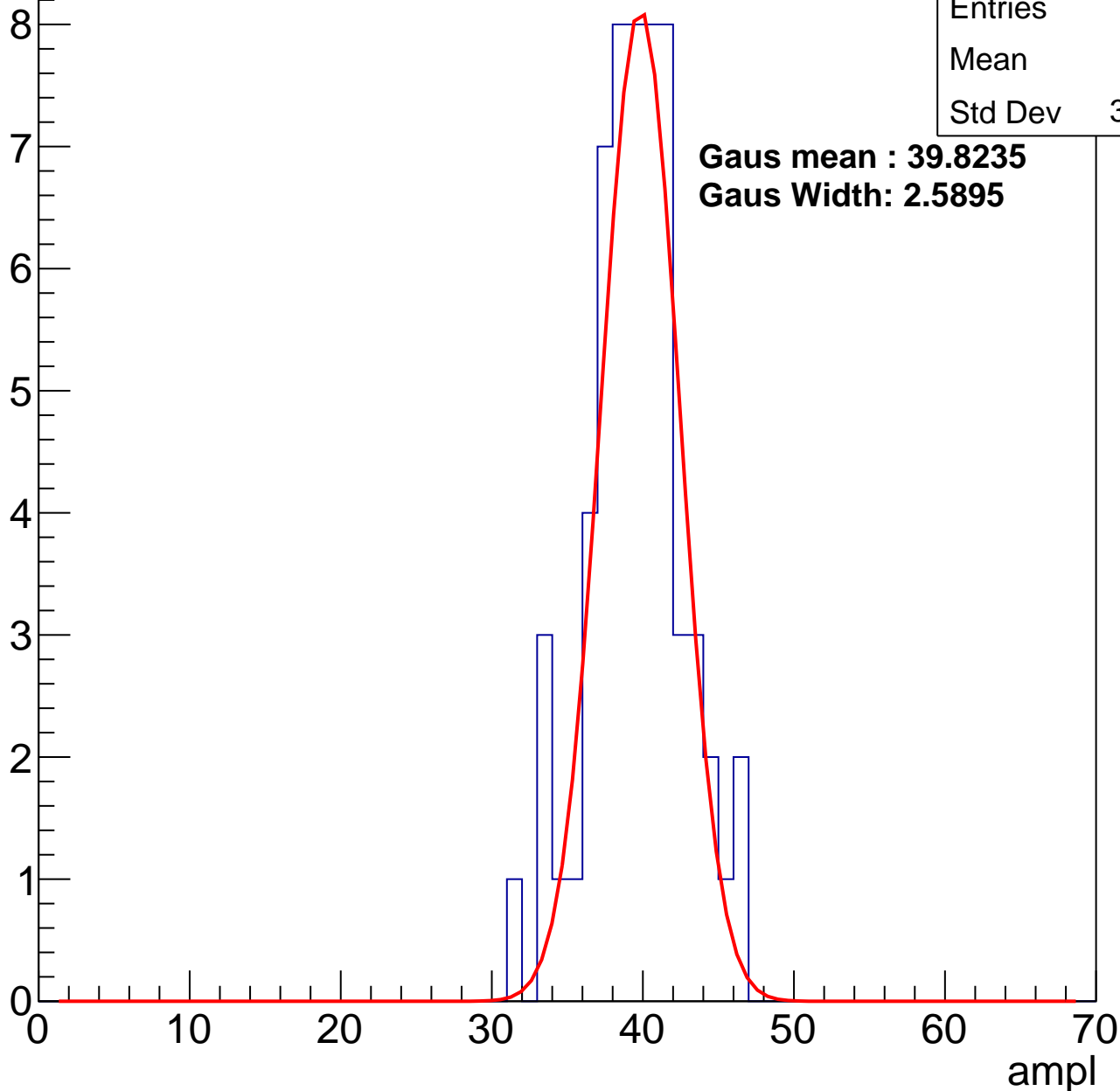
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	39.1
Std Dev	3.118

Gaus mean : 39.8235

Gaus Width: 2.5895



B0L000S, U4-ch26, adc2

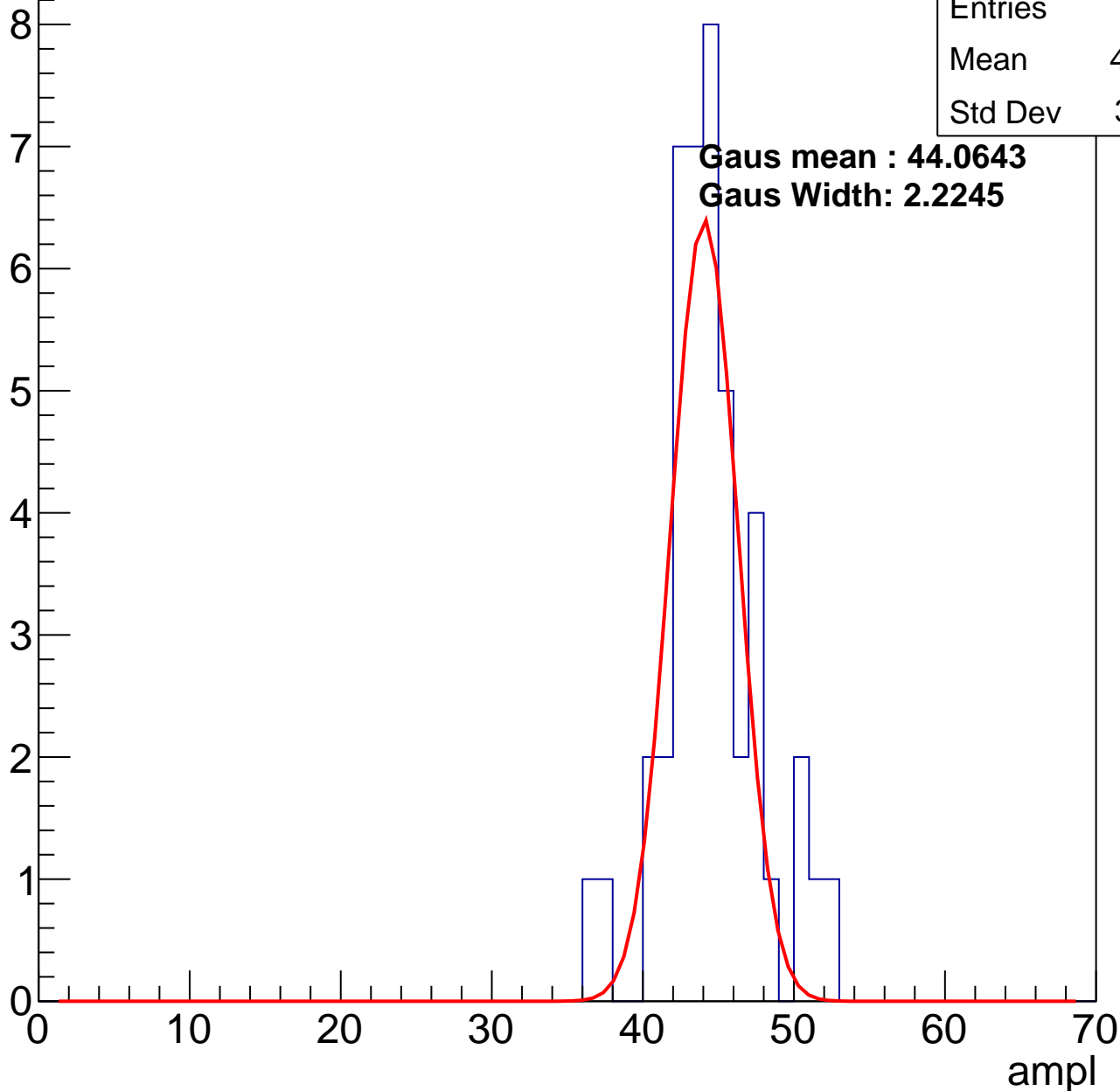
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	44.05
Std Dev	3.191

Gaus mean : 44.0643

Gaus Width: 2.2245

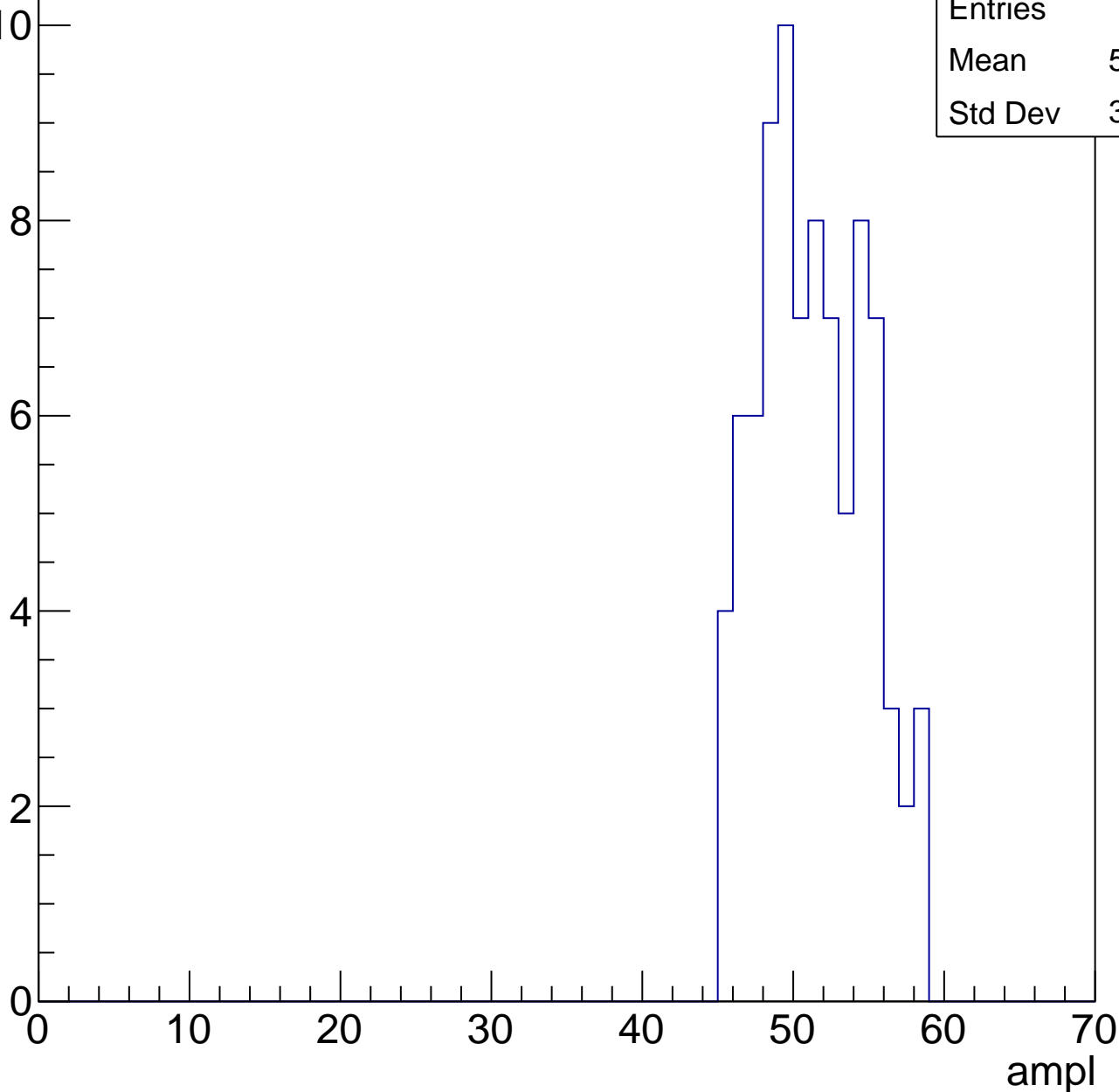


B0L000S, U4-ch26, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	85
Mean	50.82
Std Dev	3.465



B0L000S, U4-ch26, adc4

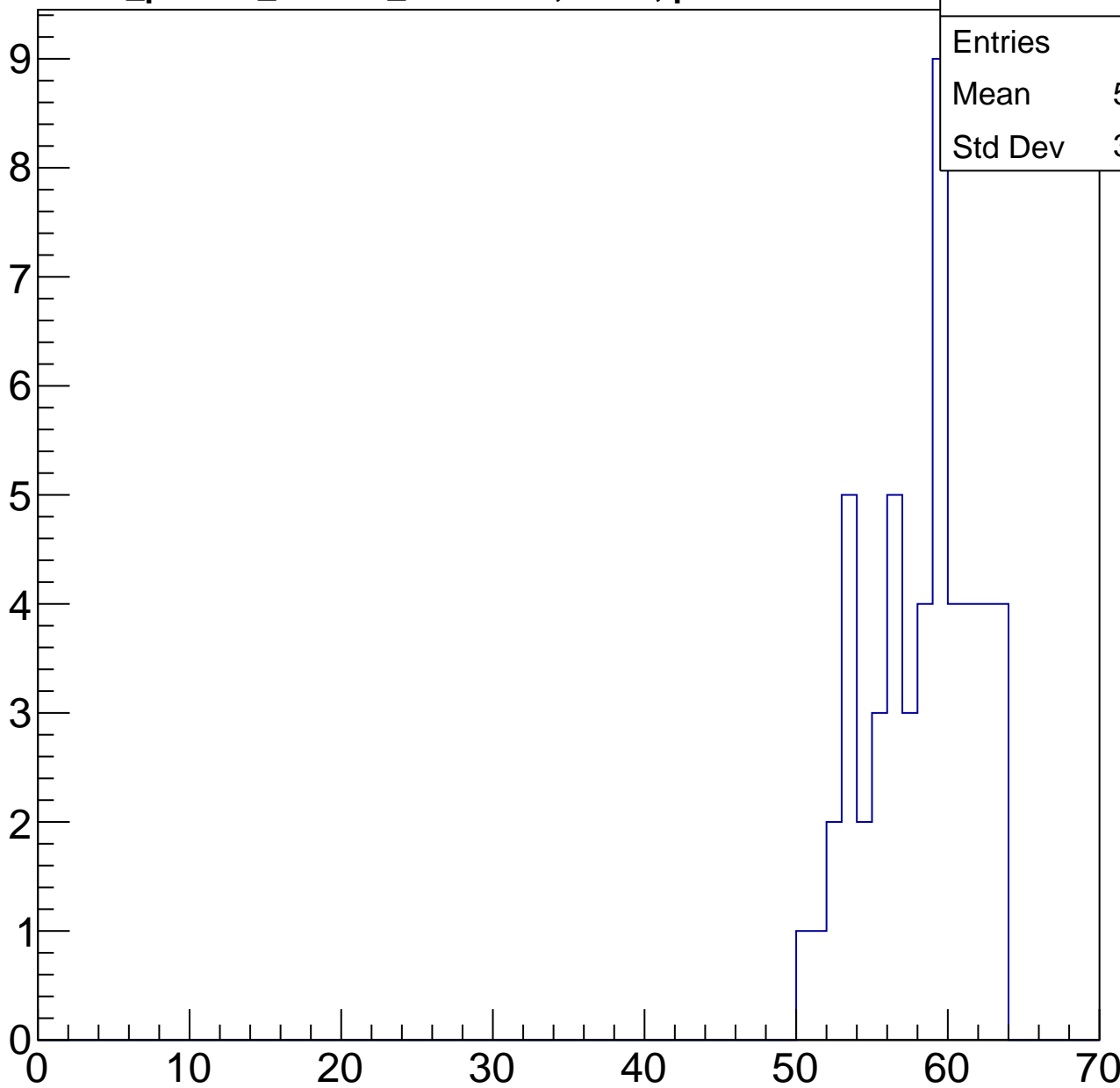
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	57.67
Std Dev	3.462

ampl



B0L000S, U4-ch26, adc5

calib_packv5_042523_0143.root, FC#5, port B1

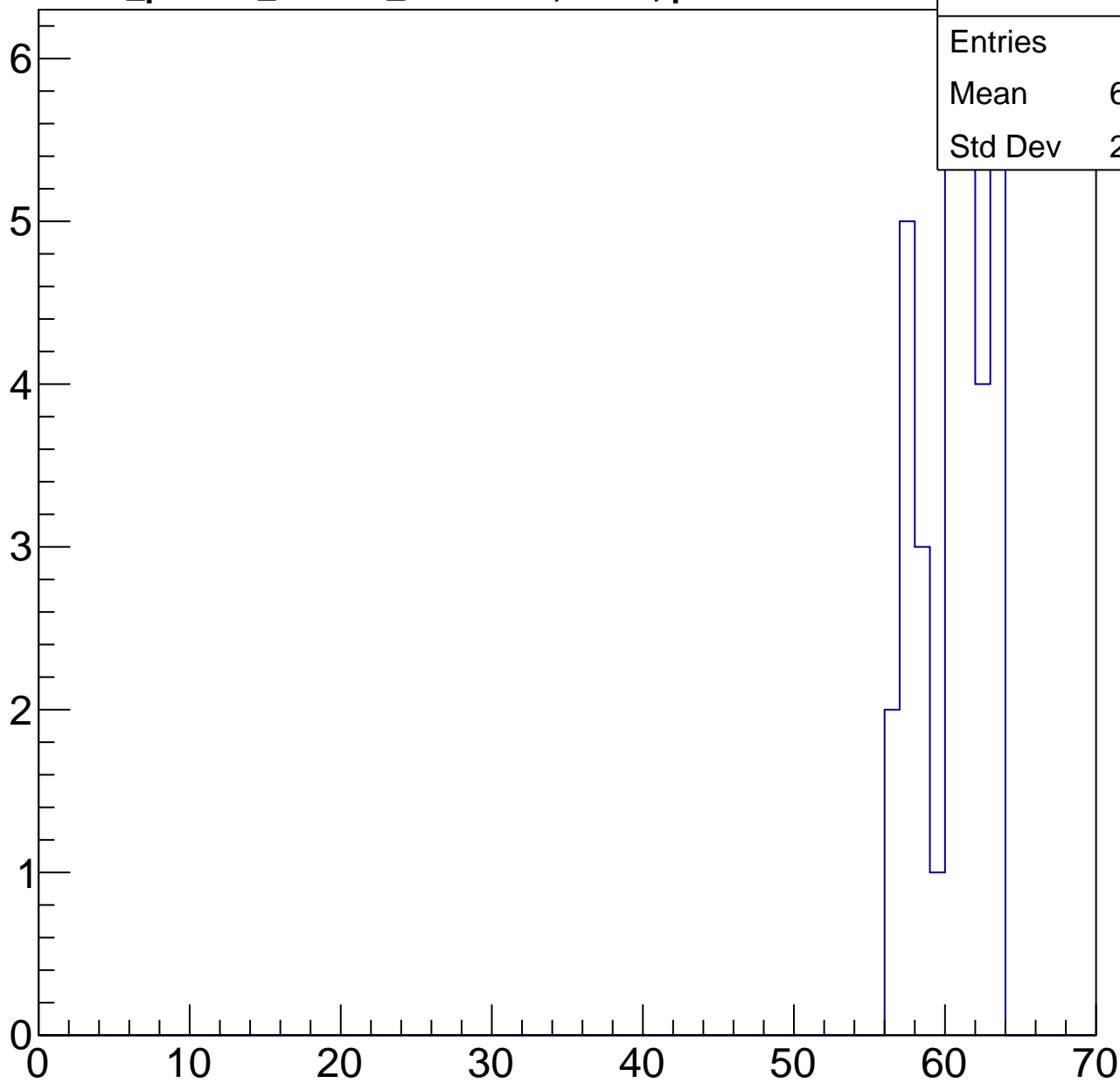
Entry

6
5
4
3
2
1
0

Entries	33
Mean	60.06
Std Dev	2.242

ampl

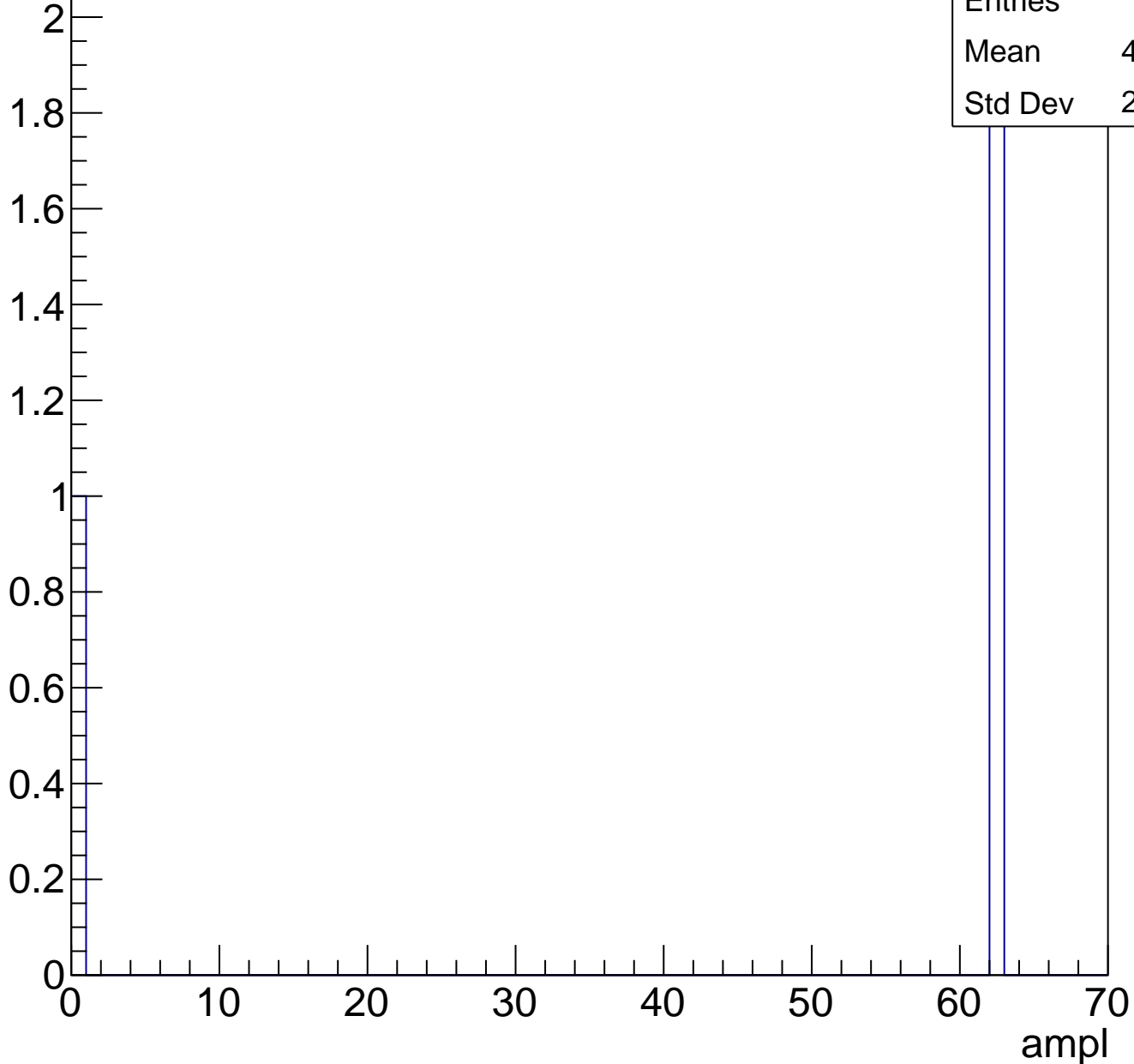
0 10 20 30 40 50 60 70



B0L000S, U4-ch26, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch26, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch27, adc0

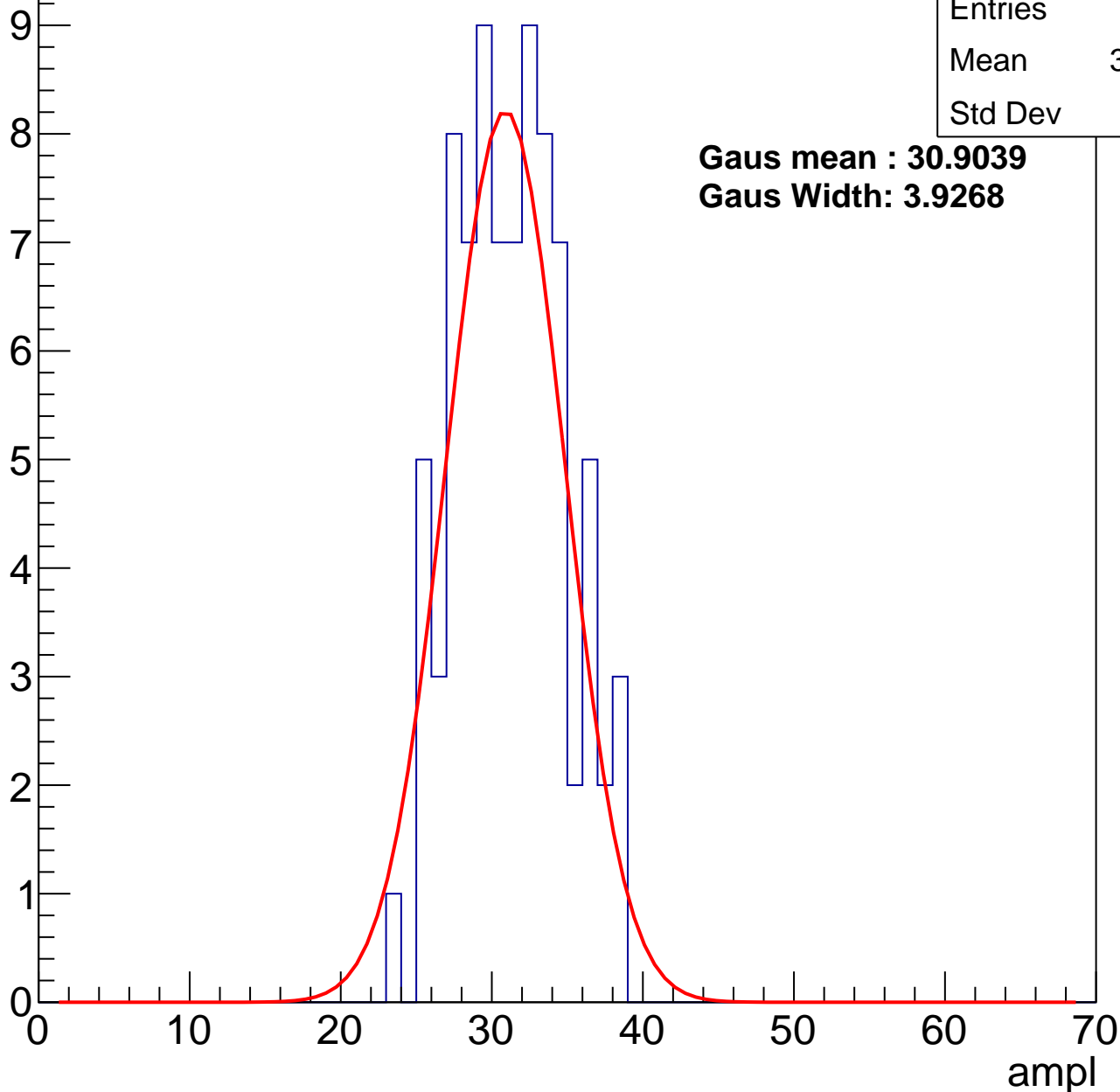
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	83
Mean	30.77
Std Dev	3.52

Gaus mean : 30.9039

Gaus Width: 3.9268



B0L000S, U4-ch27, adc1

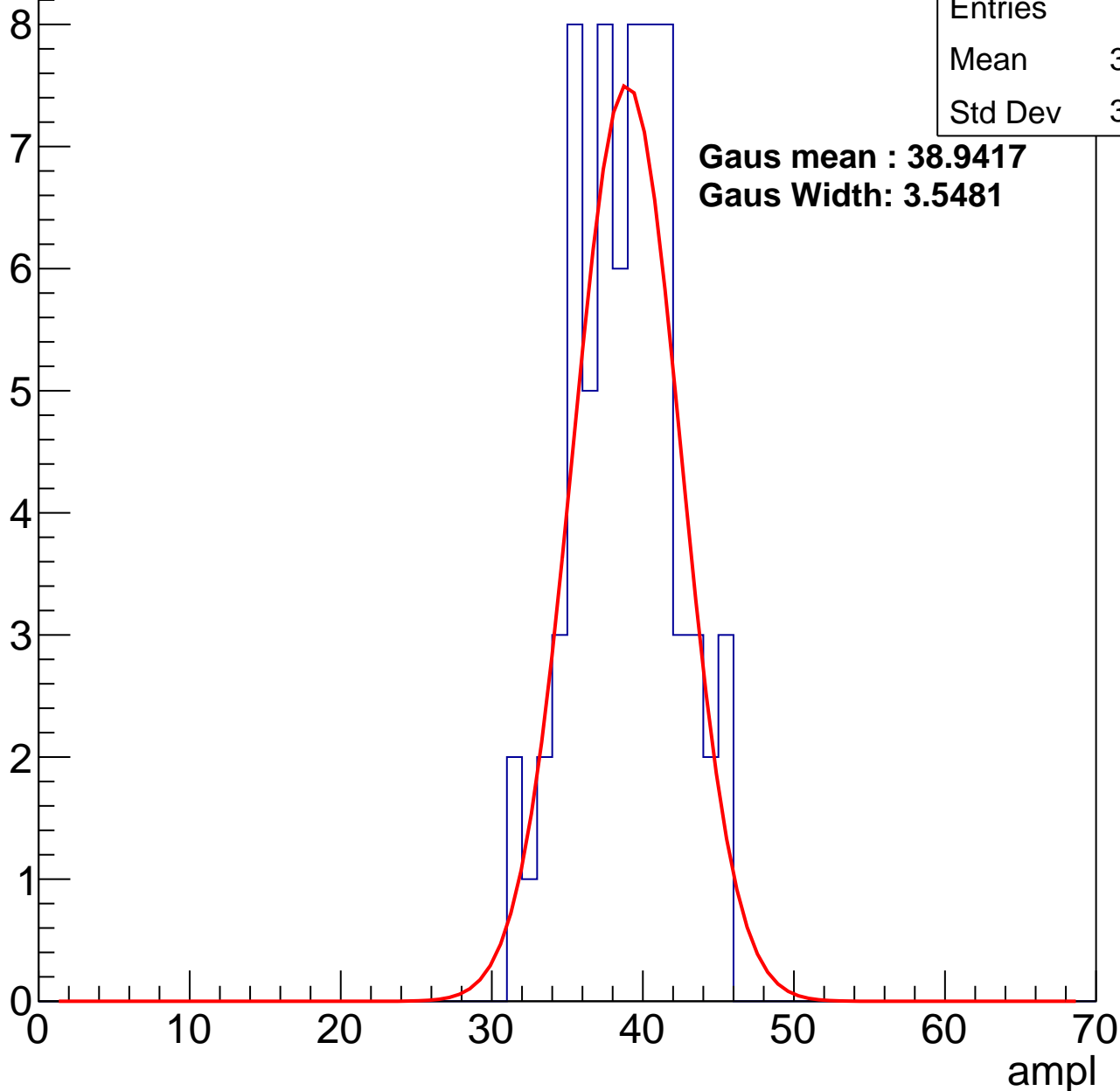
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	38.34
Std Dev	3.333

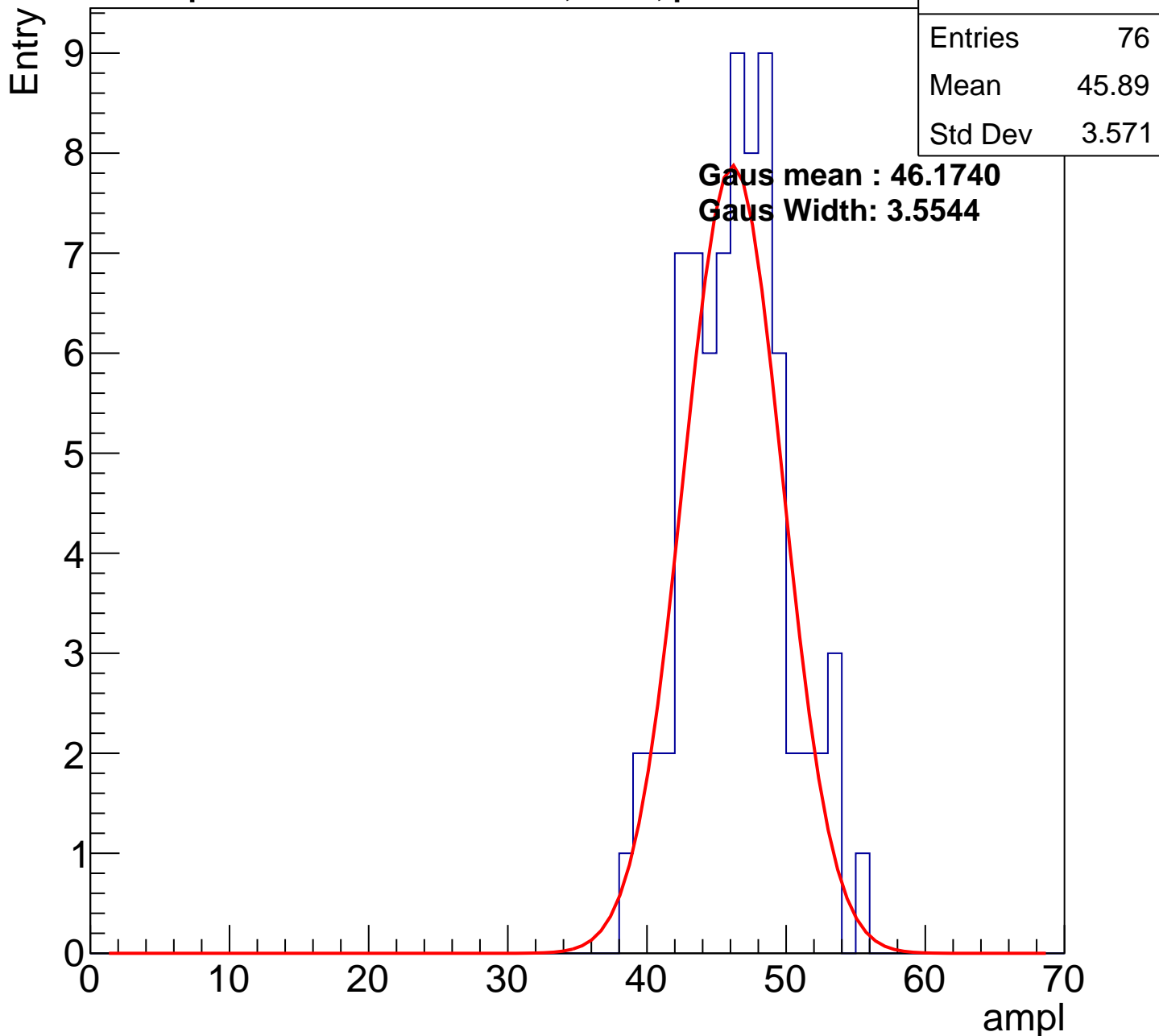
Gaus mean : 38.9417

Gaus Width: 3.5481



B0L000S, U4-ch27, adc2

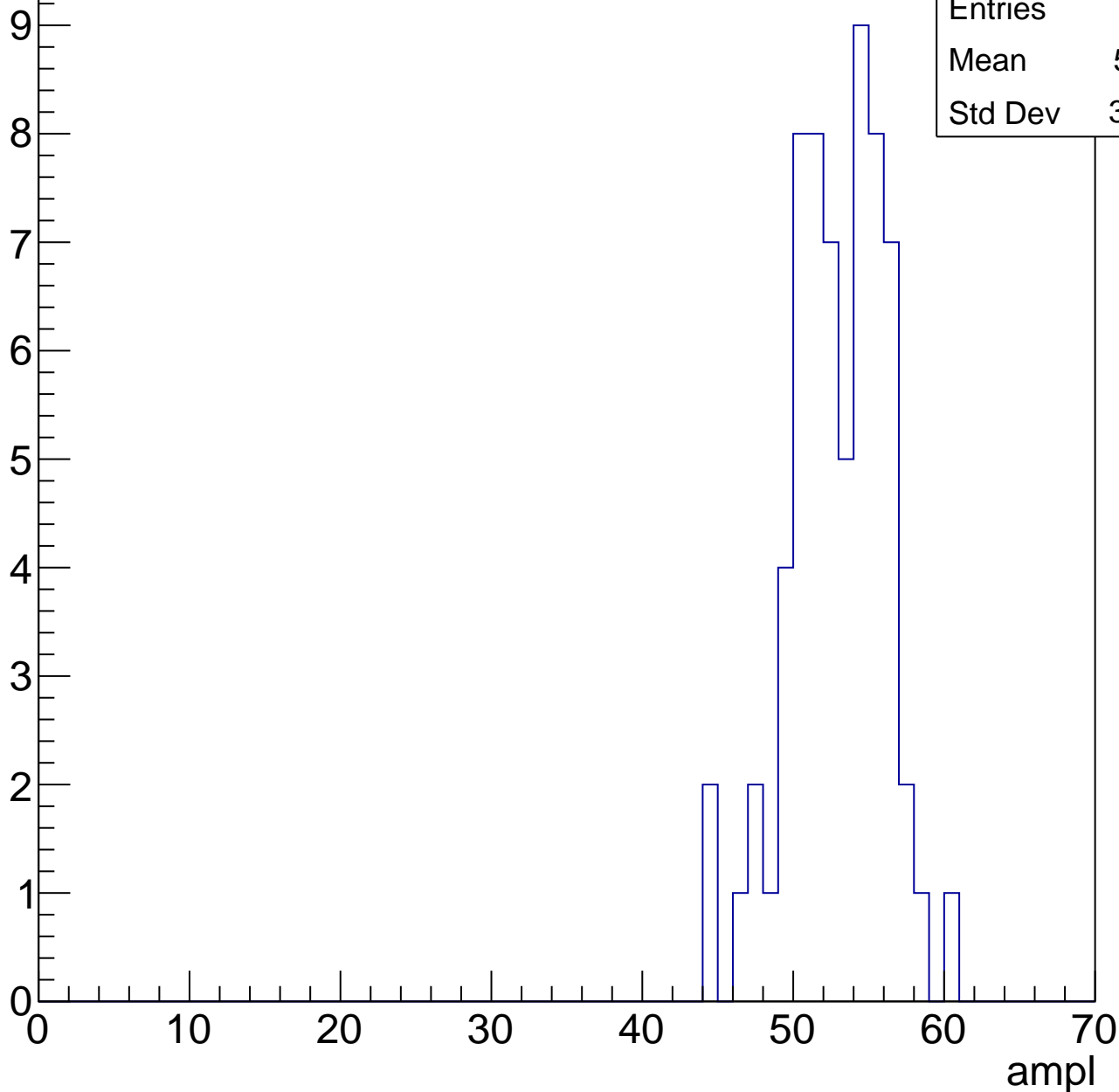
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch27, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



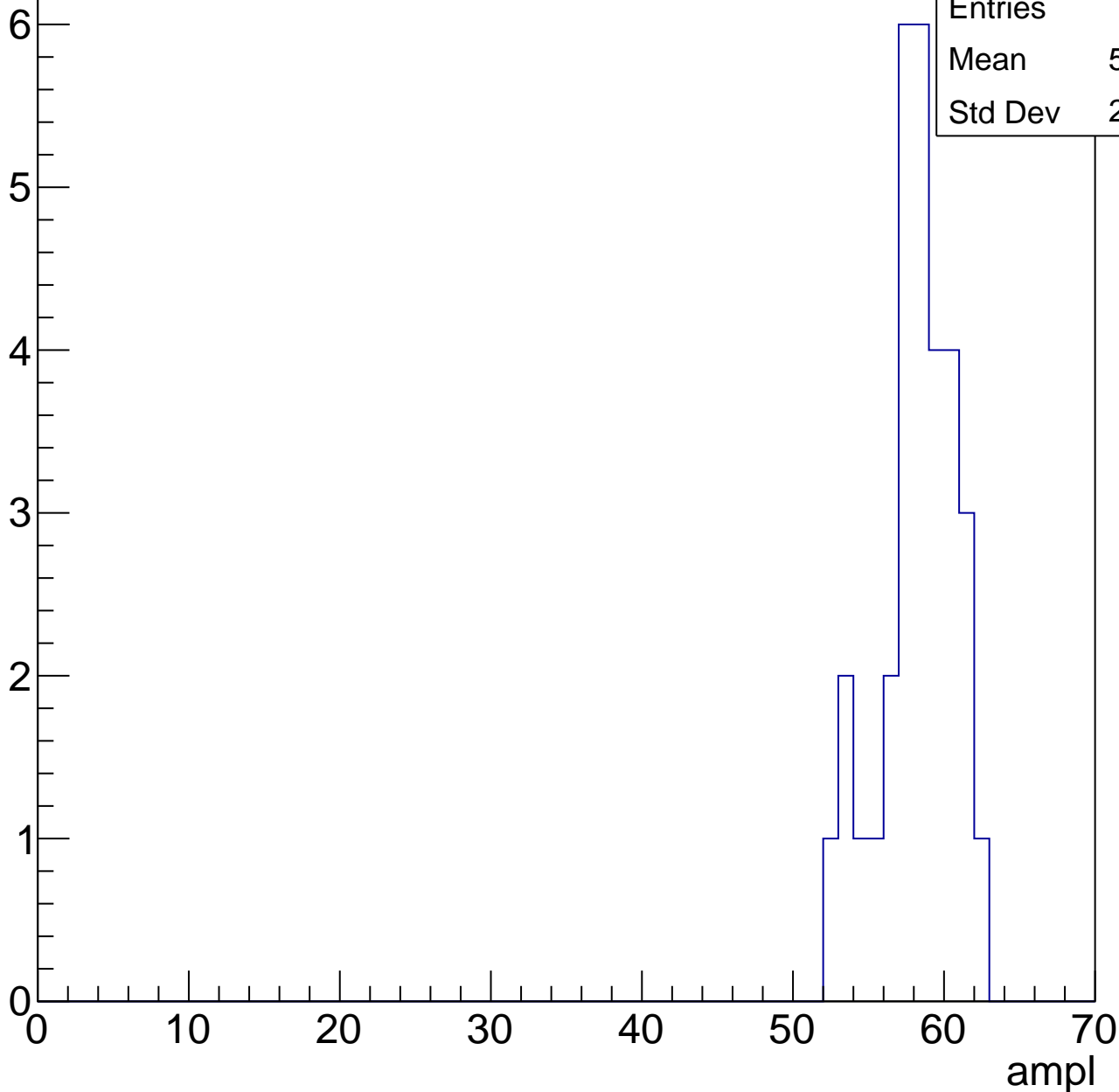
Entries	66
Mean	52.41
Std Dev	3.186

B0L000S, U4-ch27, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	31
Mean	57.74
Std Dev	2.449

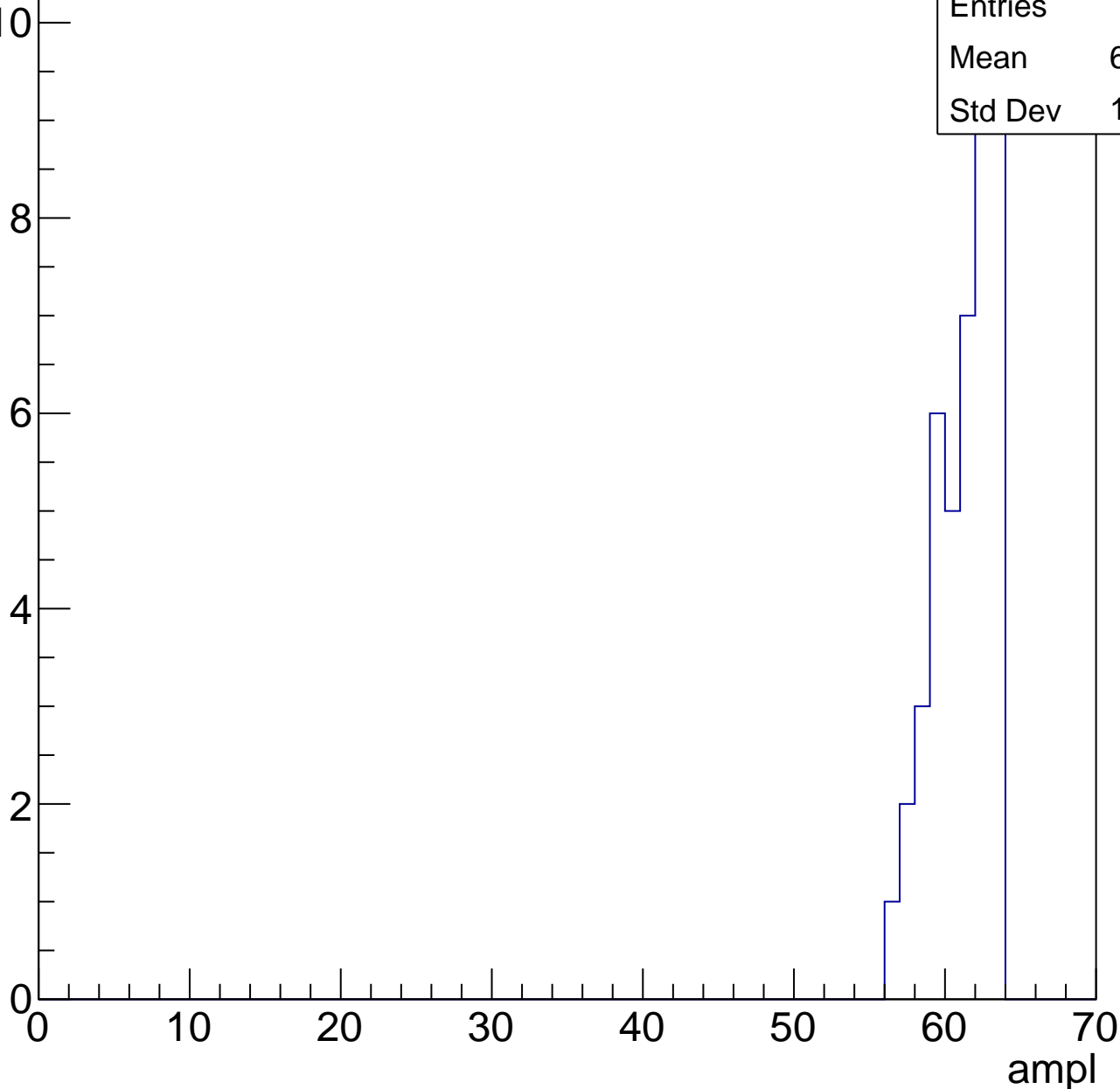


B0L000S, U4-ch27, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	43
Mean	60.77
Std Dev	1.927



B0L000S, U4-ch27, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch27, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch28, adc0

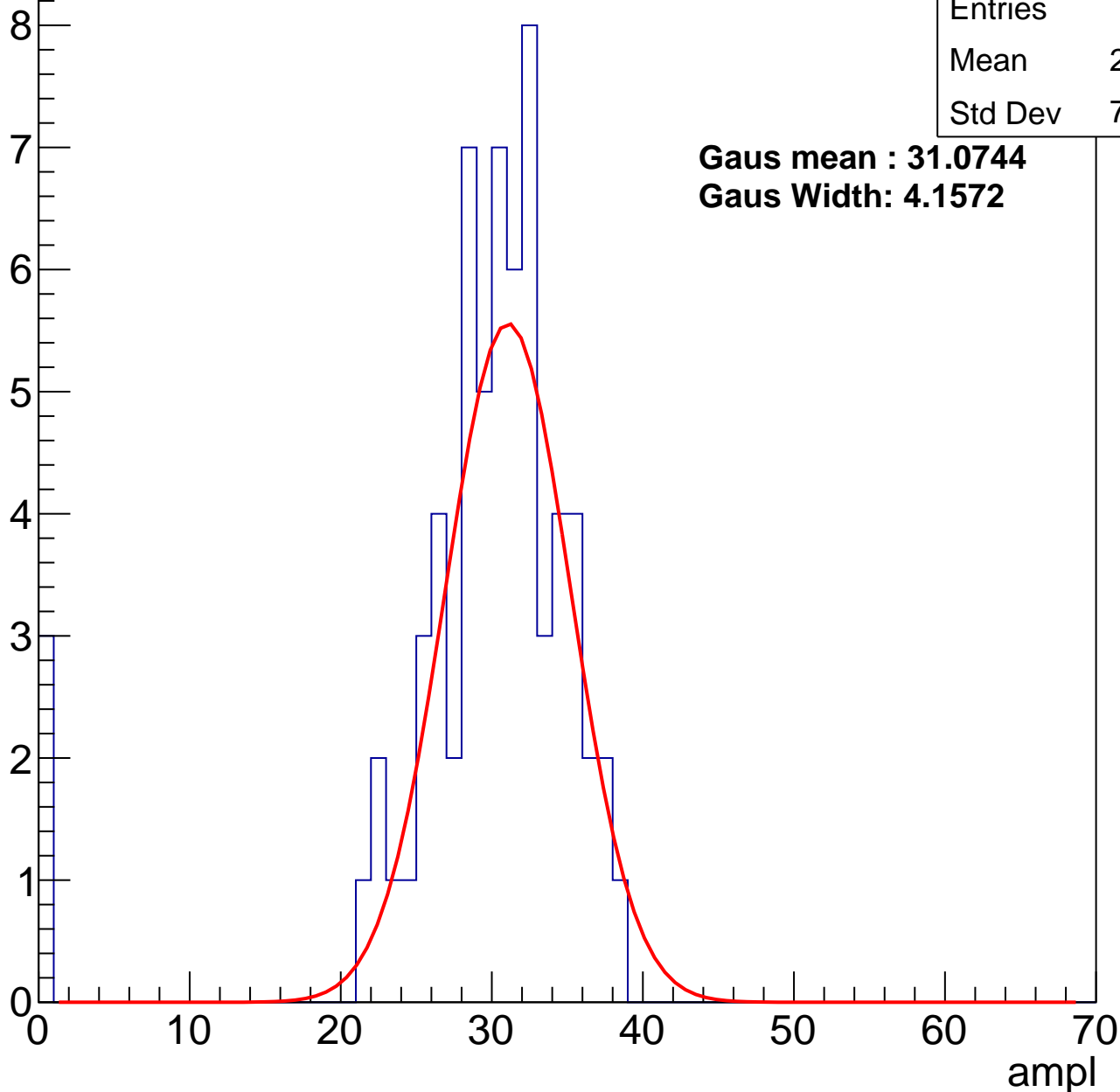
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.74
Std Dev	7.326

Gaus mean : 31.0744

Gaus Width: 4.1572



B0L000S, U4-ch28, adc1

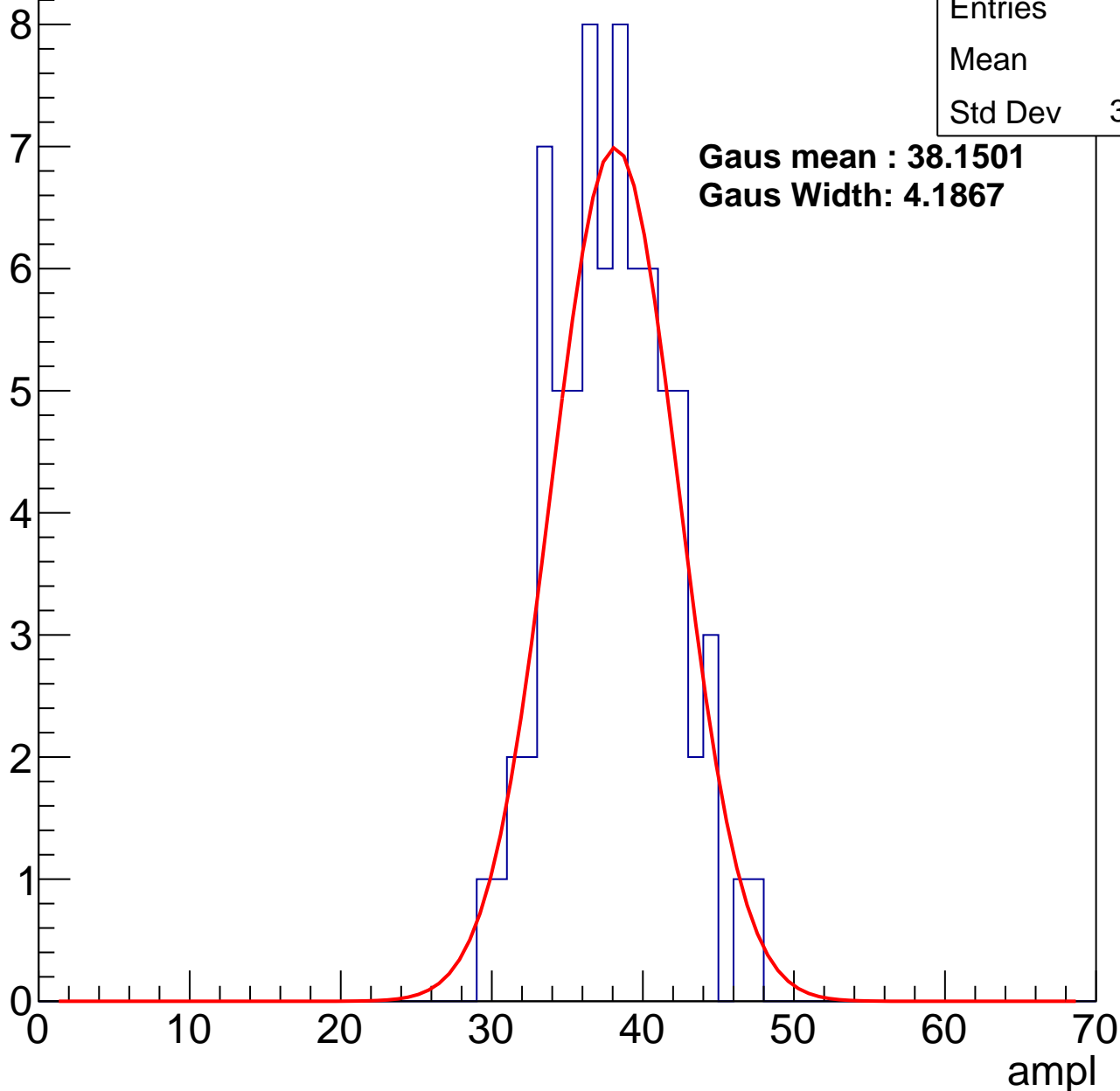
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.5
Std Dev	3.842

Gaus mean : 38.1501

Gaus Width: 4.1867



B0L000S, U4-ch28, adc2

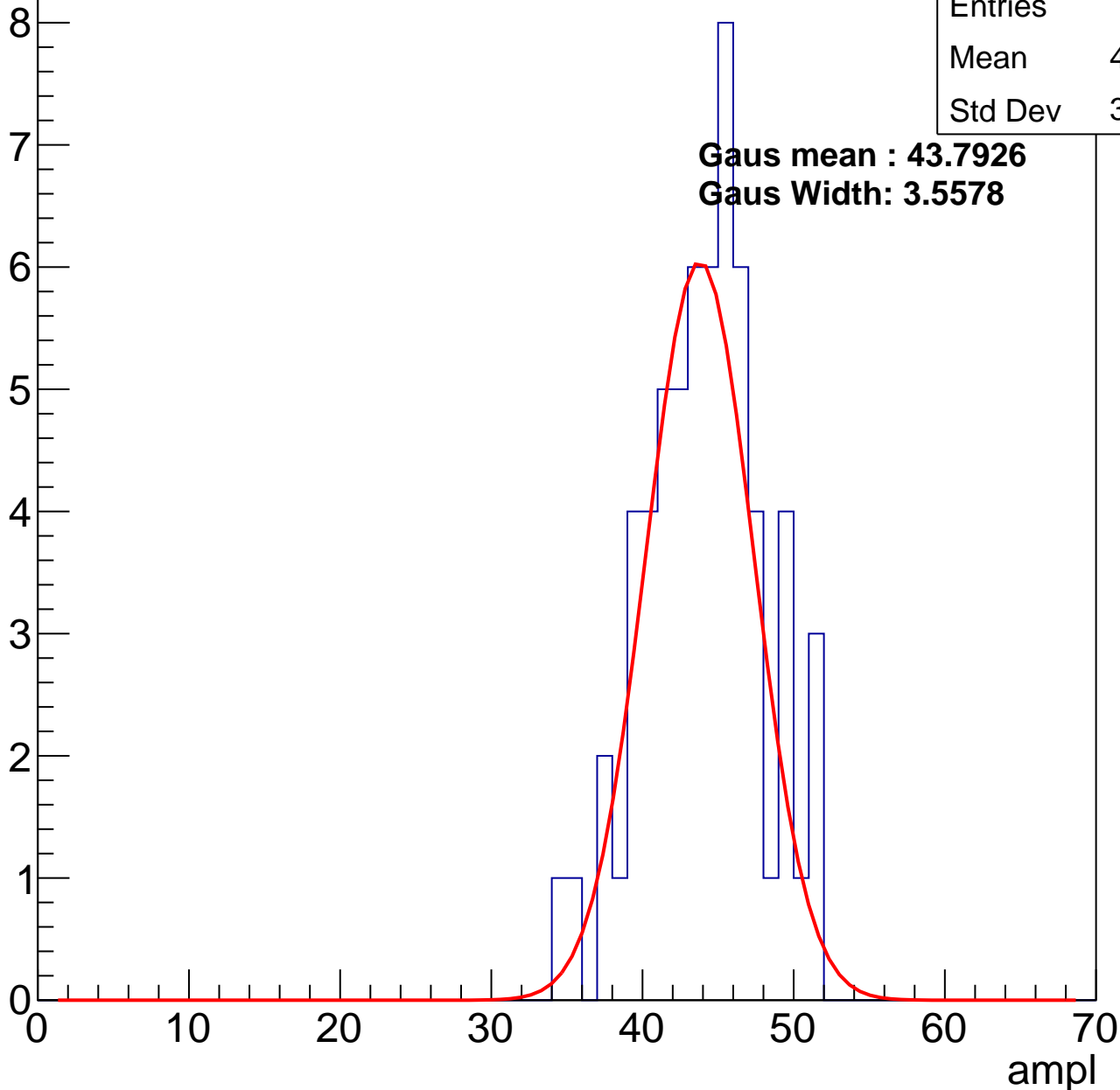
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.63
Std Dev	3.828

Gaus mean : 43.7926

Gaus Width: 3.5578

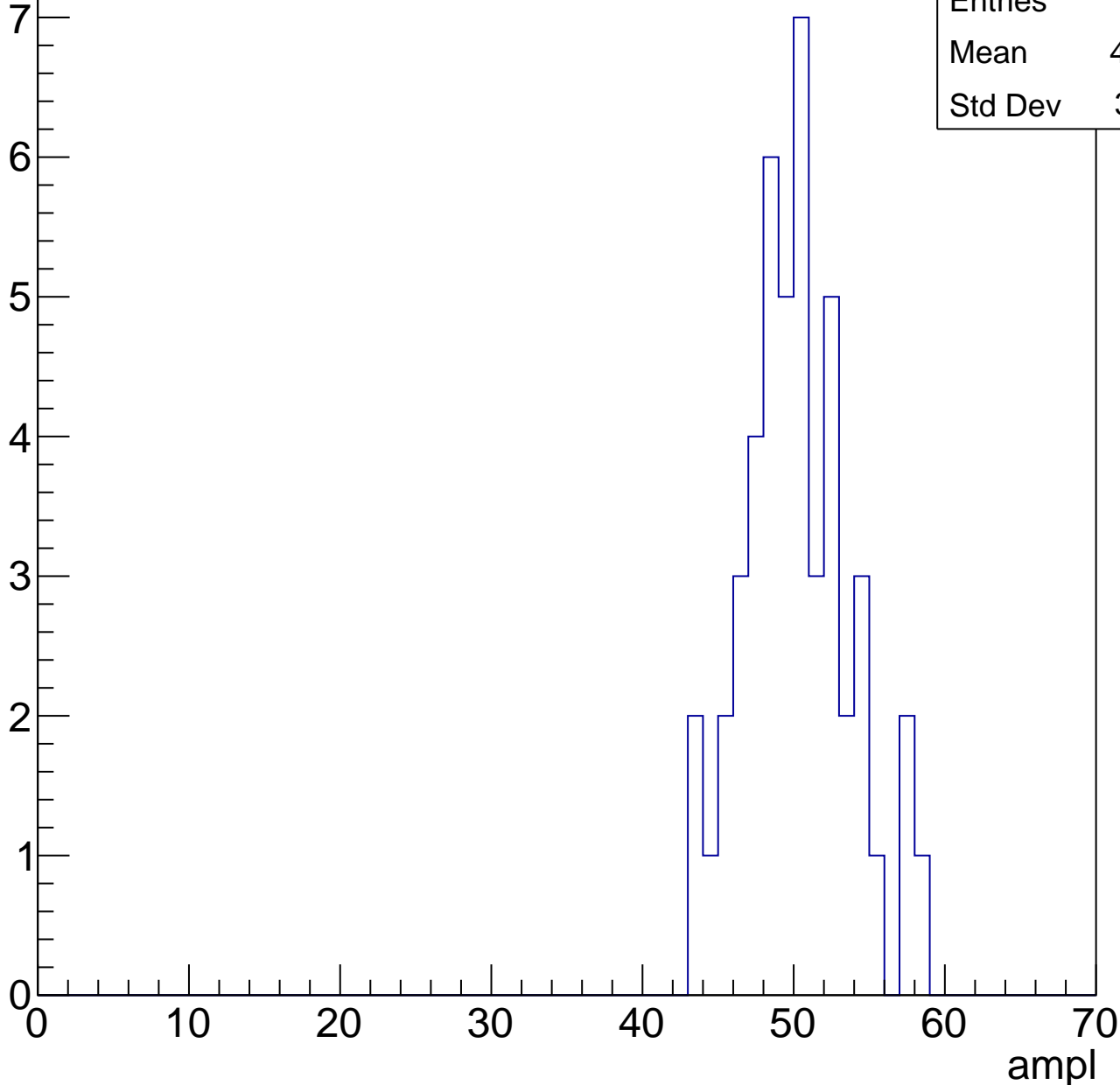


B0L000S, U4-ch28, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

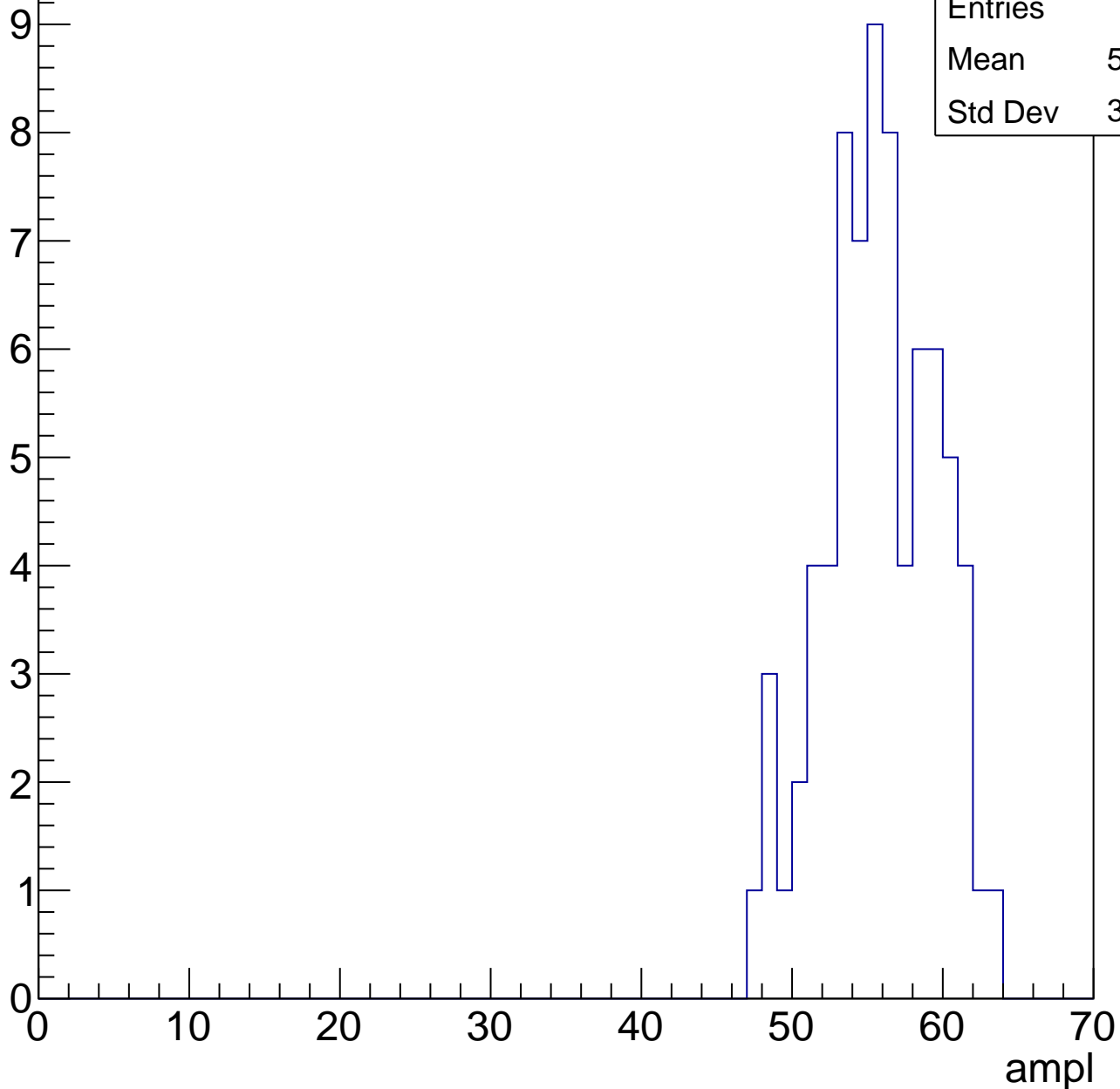
Entries	47
Mean	49.72
Std Dev	3.481



B0L000S, U4-ch28, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



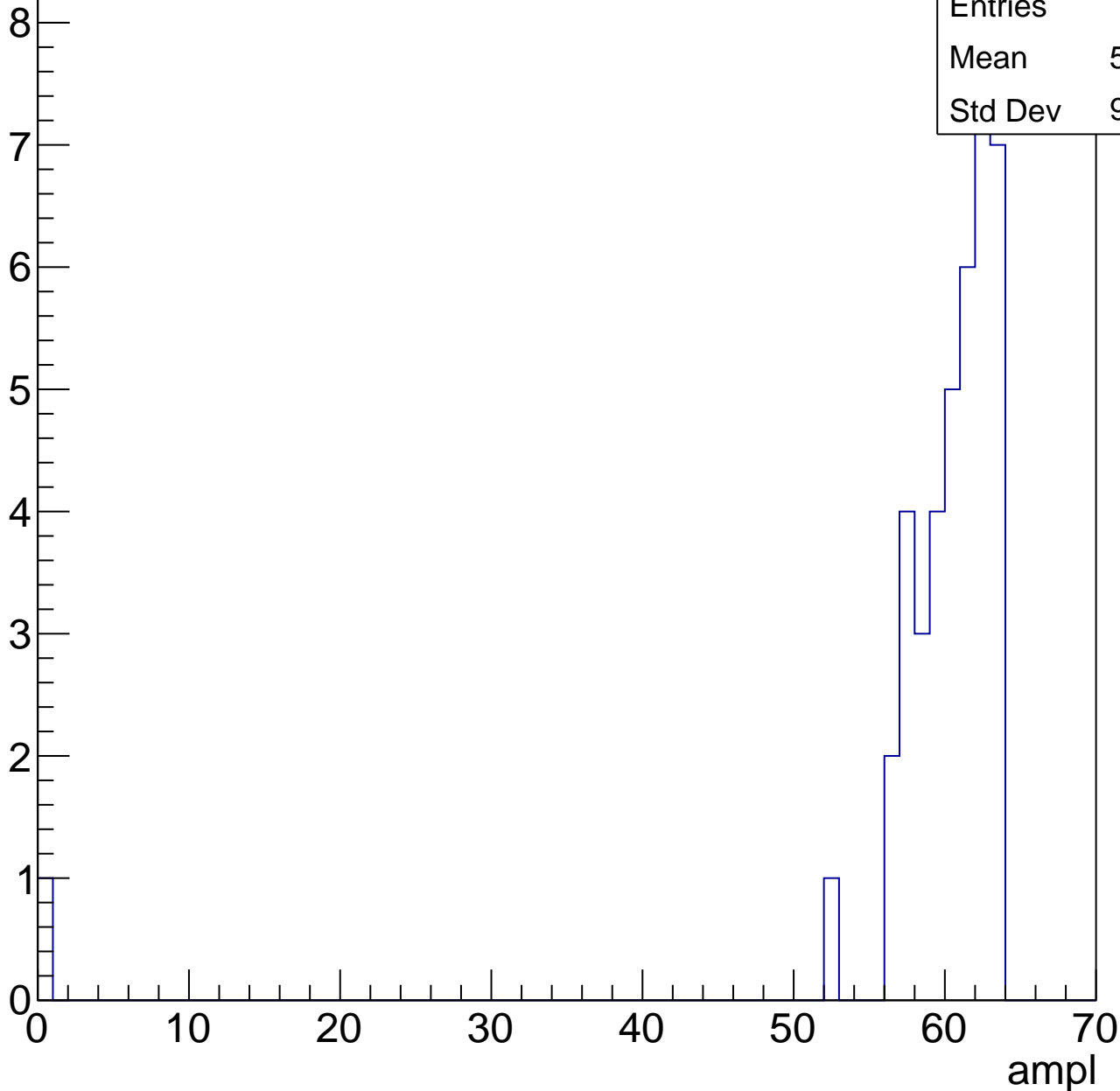
Entries	74
Mean	55.35
Std Dev	3.644

B0L000S, U4-ch28, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.66
Std Dev	9.596



B0L000S, U4-ch28, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L000S, U4-ch28, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch29, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	87
Mean	29.57
Std Dev	5.636

Gaus mean : 30.8129

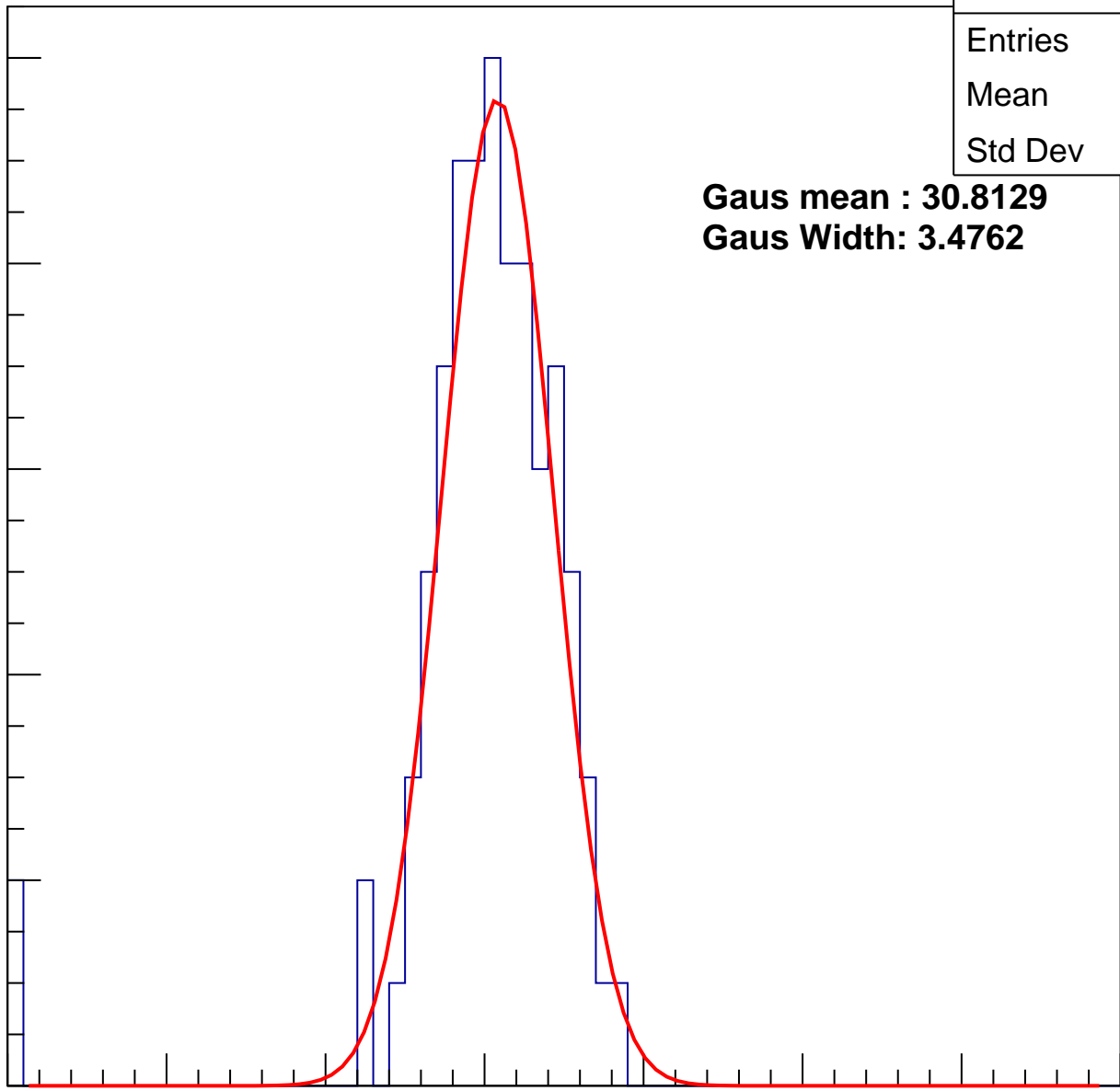
Gaus Width: 3.4762

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



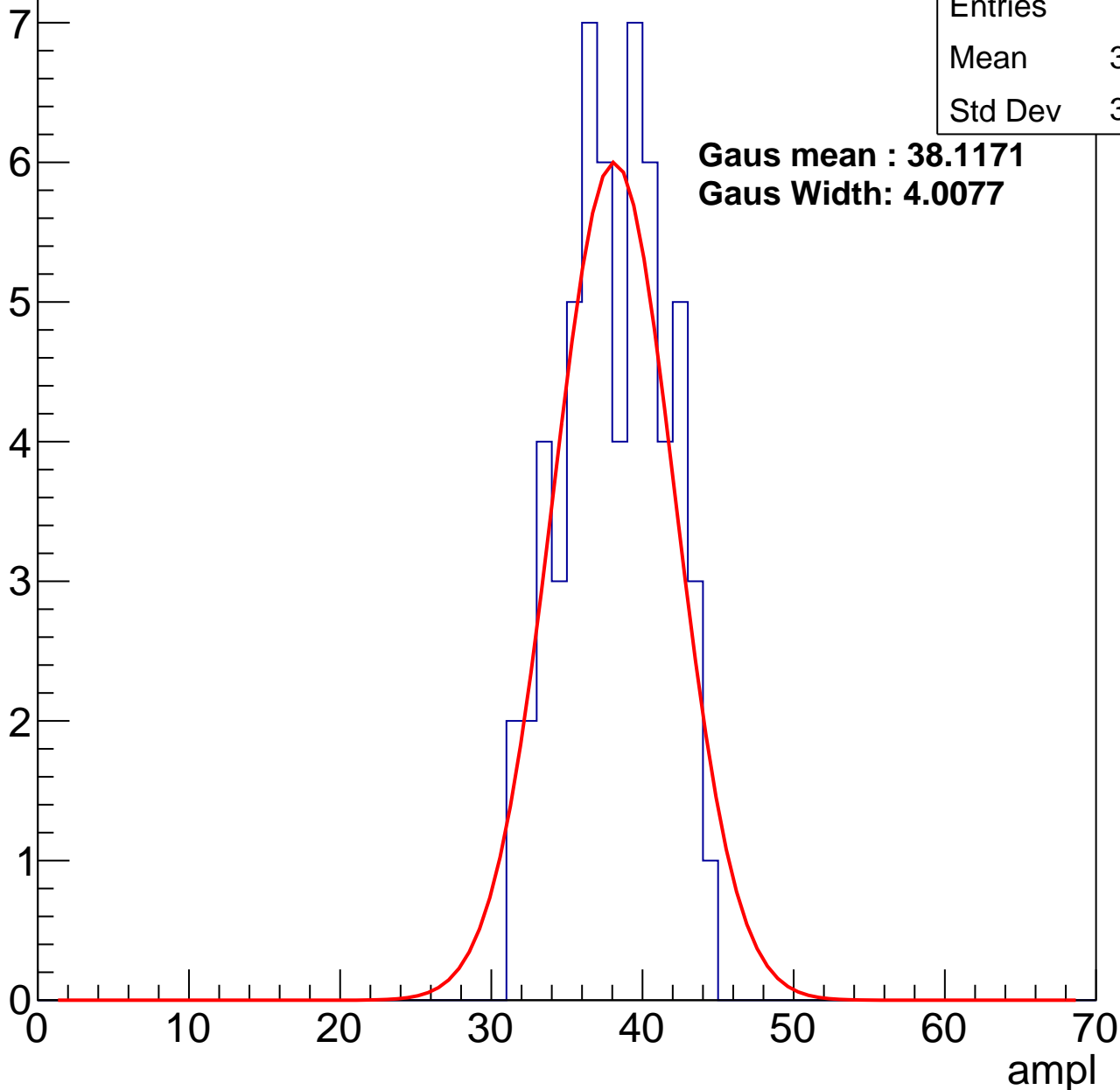
B0L000S, U4-ch29, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.64
Std Dev	3.313

Gaus mean : 38.1171
Gaus Width: 4.0077



B0L000S, U4-ch29, adc2

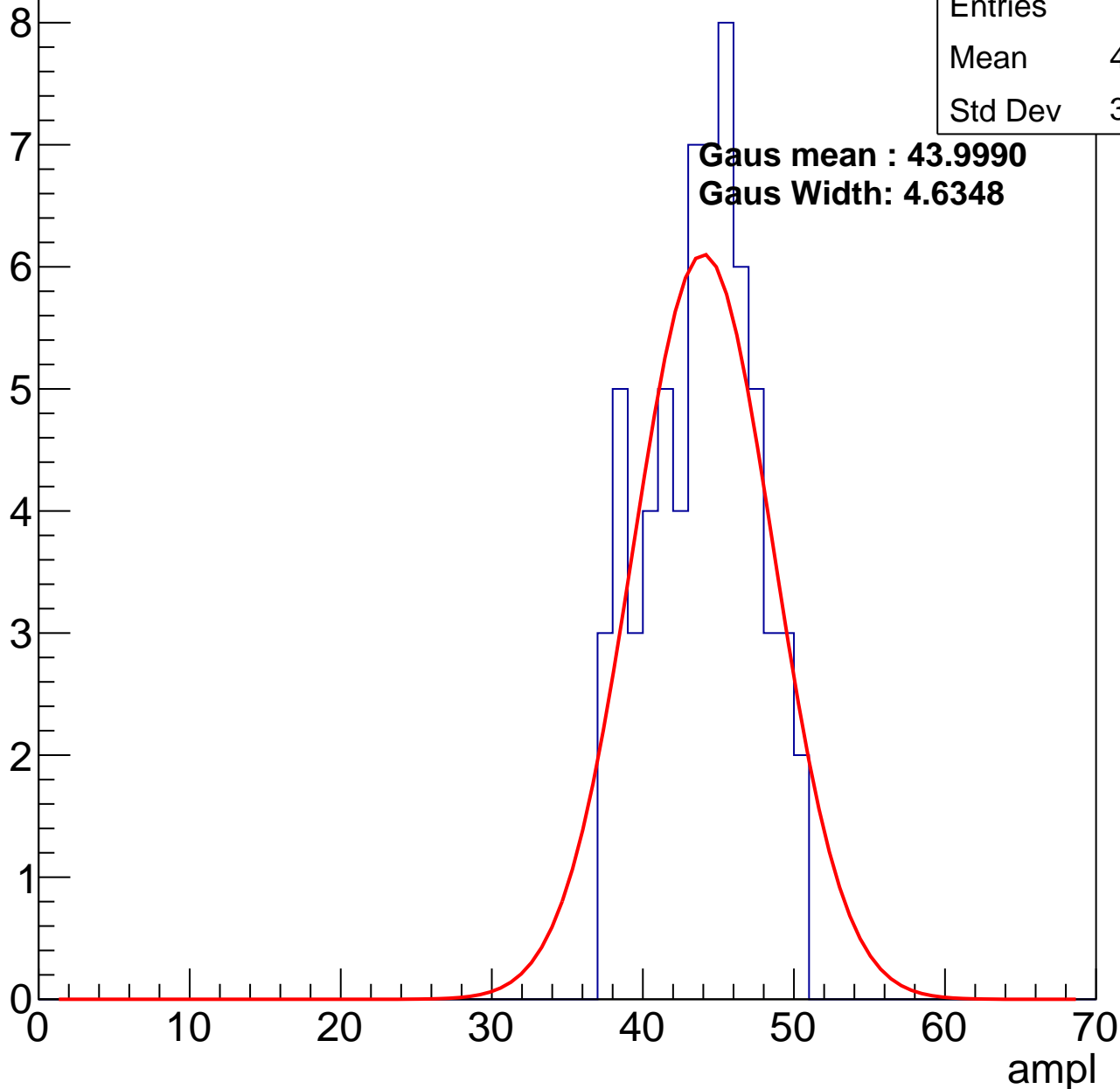
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.42
Std Dev	3.472

Gaus mean : 43.9990

Gaus Width: 4.6348

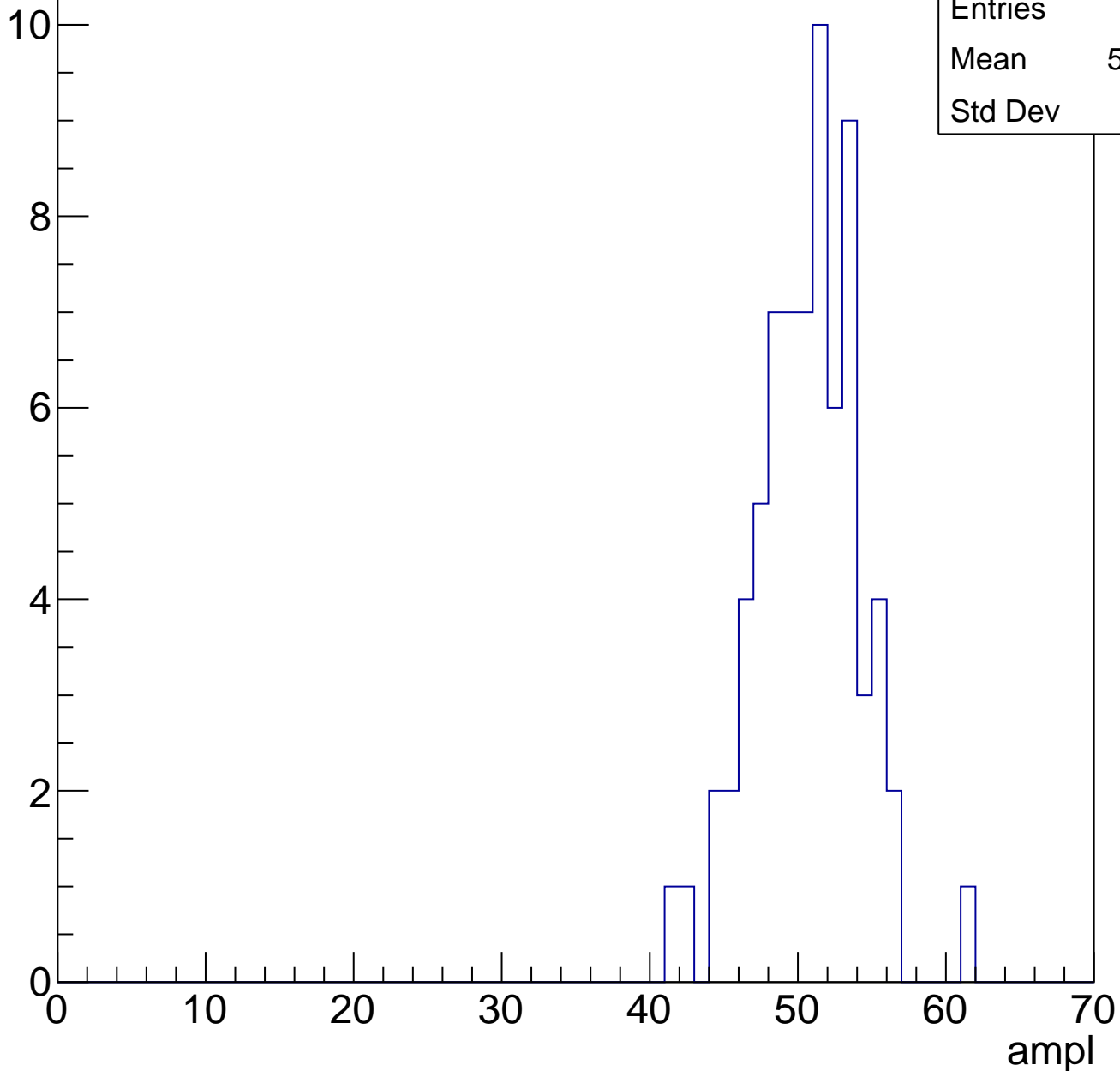


B0L000S, U4-ch29, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	50.18
Std Dev	3.49

Entry

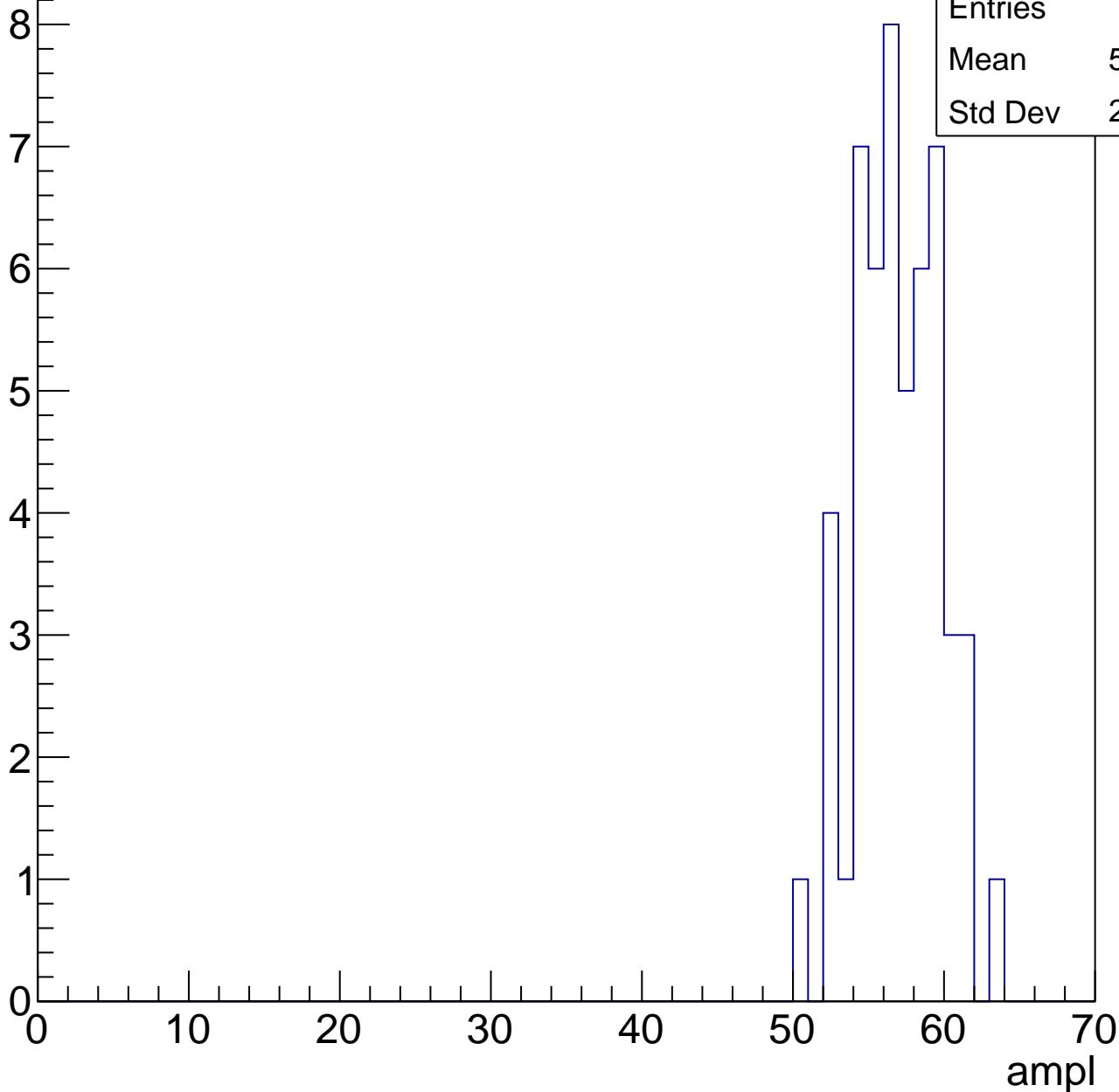


B0L000S, U4-ch29, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	56.52
Std Dev	2.749



B0L000S, U4-ch29, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.05
Std Dev	9.431

ampl

0

10

20

30

40

50

60

70

B0L000S, U4-ch29, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch29, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch30, adc0

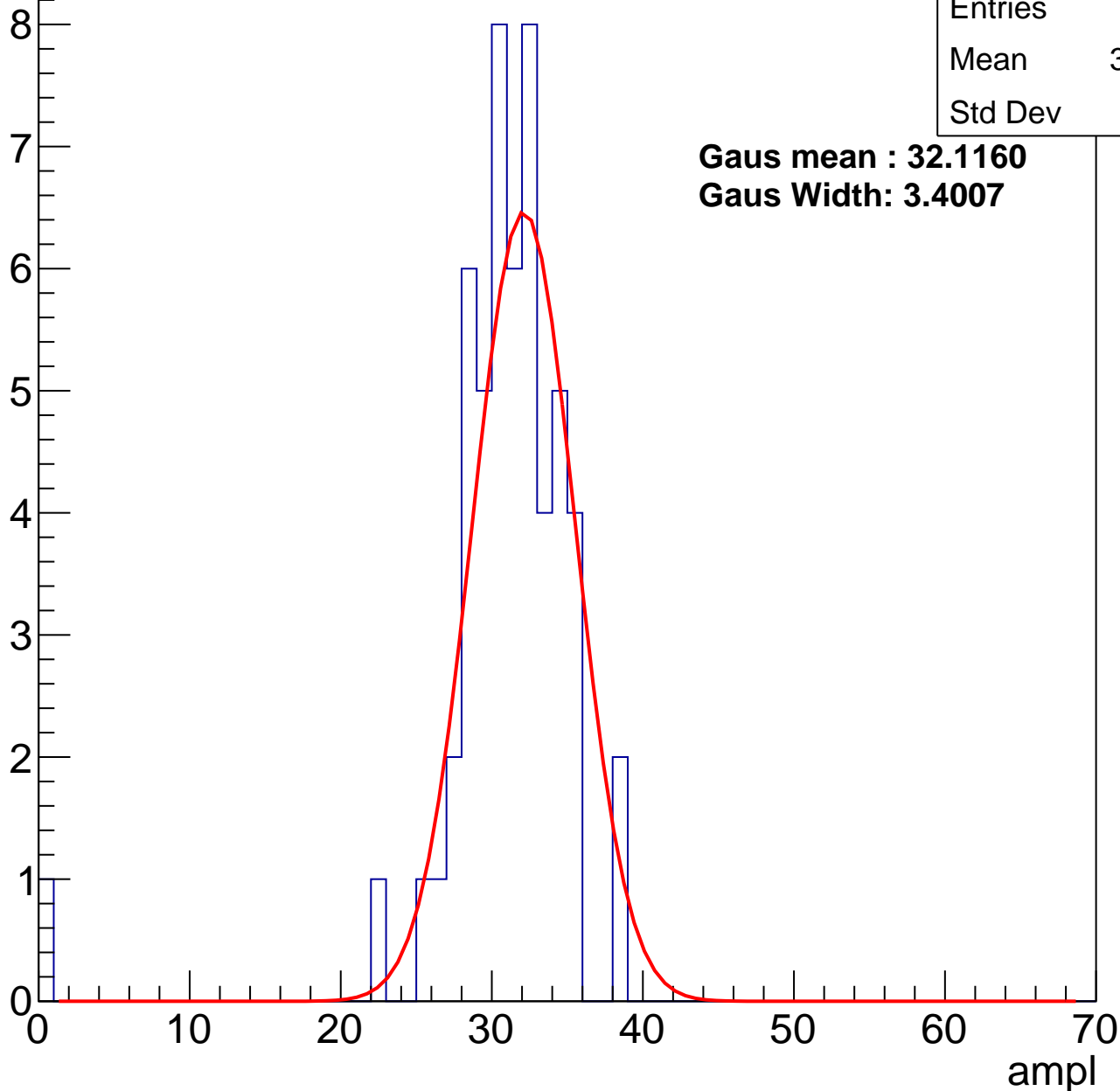
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	30.37
Std Dev	5.14

Gaus mean : 32.1160

Gaus Width: 3.4007



B0L000S, U4-ch30, adc1

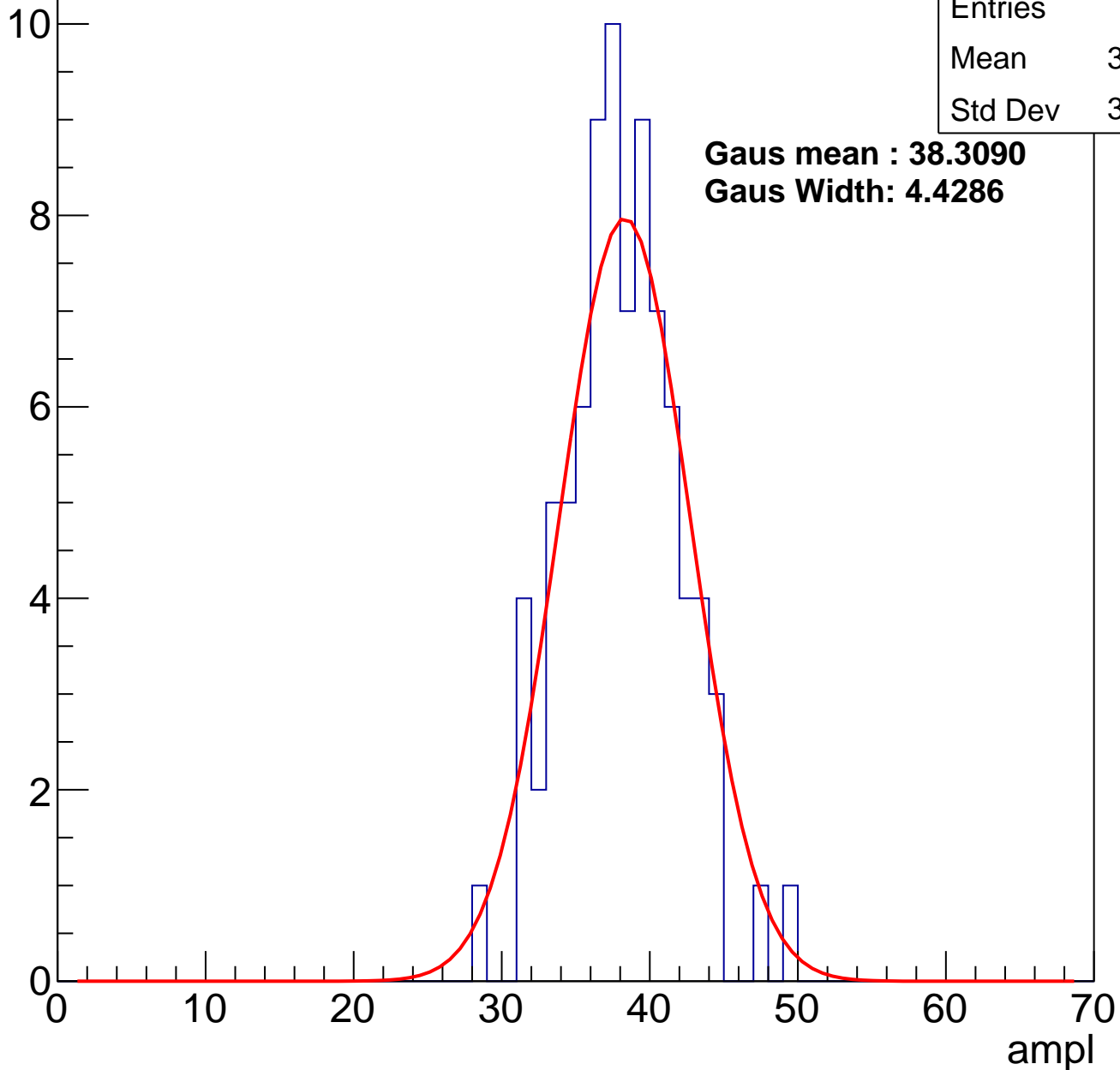
calib_packv5_042523_0143.root, FC#5, port B1

Entries	84
Mean	37.69
Std Dev	3.827

Gaus mean : 38.3090

Gaus Width: 4.4286

Entry



B0L000S, U4-ch30, adc2

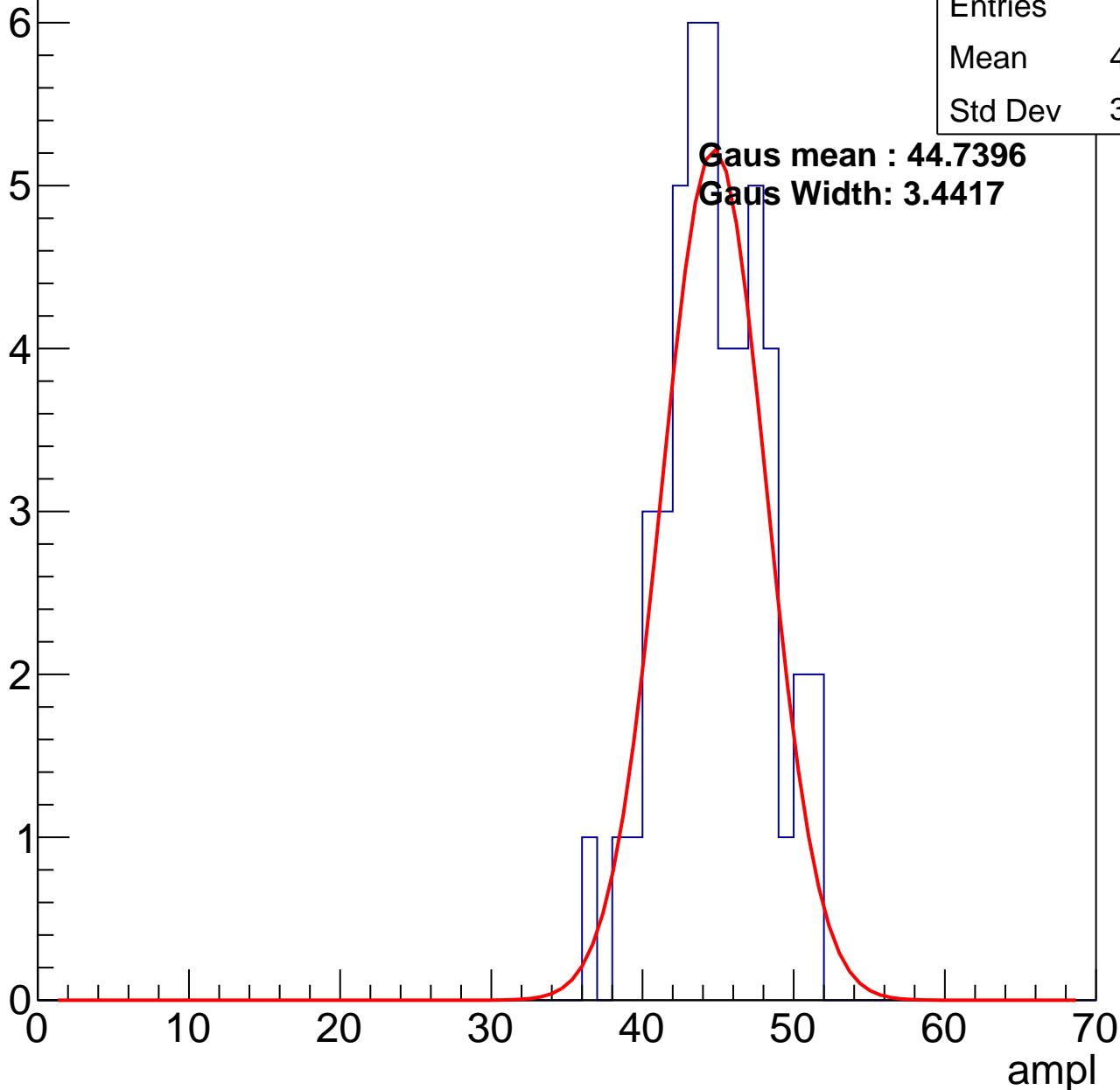
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	44.38
Std Dev	3.364

Gaus mean : 44.7396

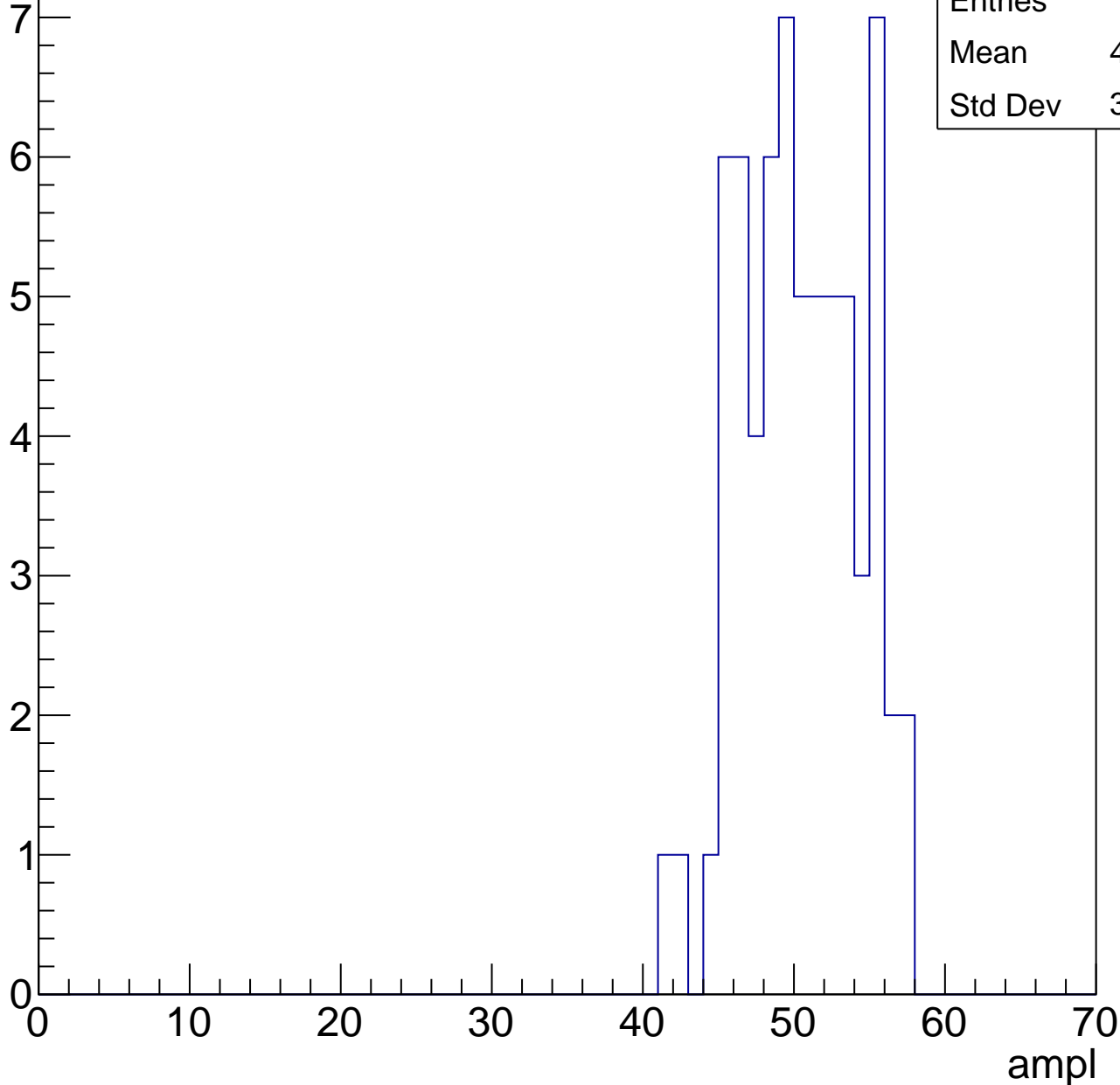
Gaus Width: 3.4417



B0L000S, U4-ch30, adc3

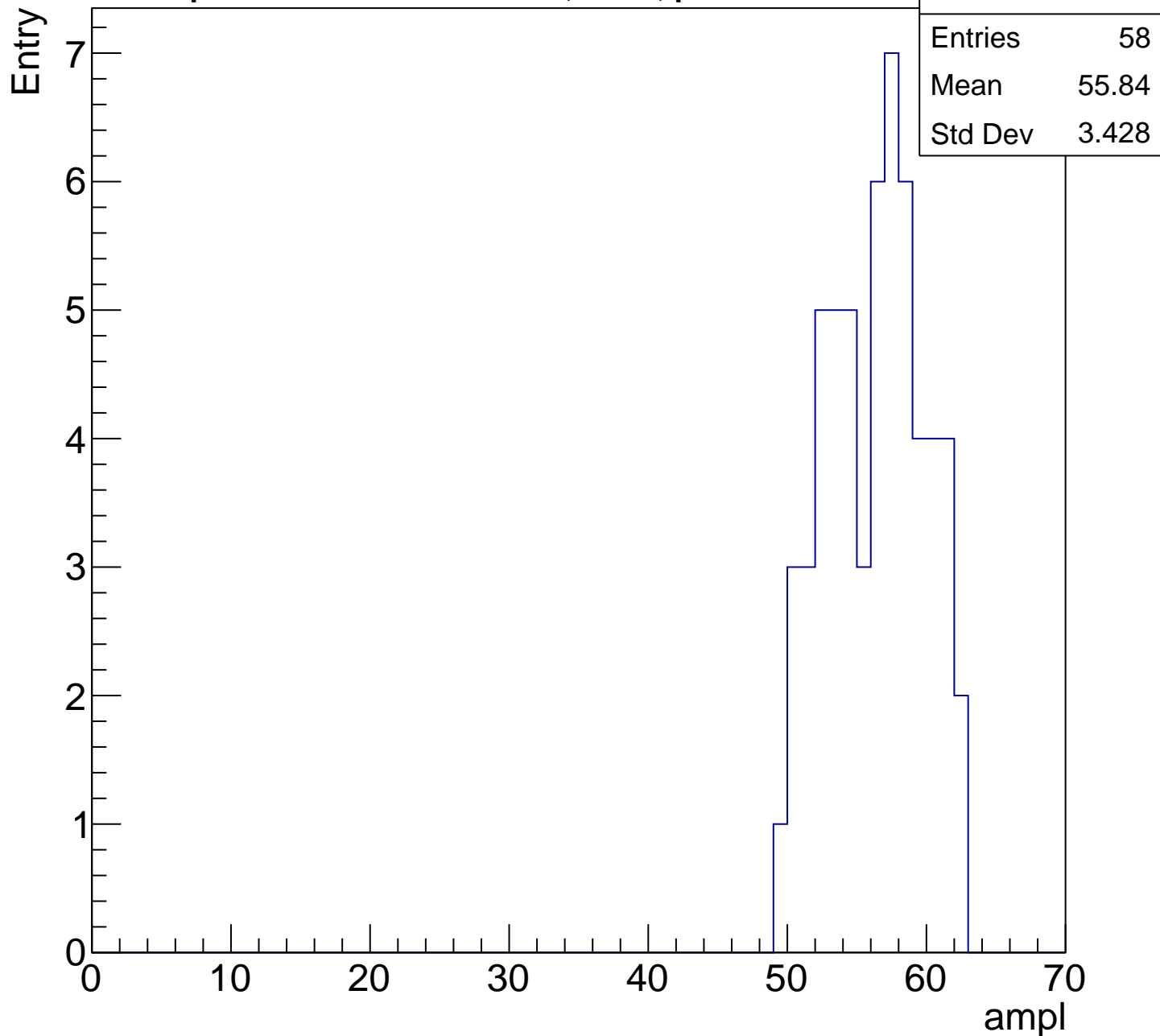
calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch30, adc4

calib_packv5_042523_0143.root, FC#5, port B1

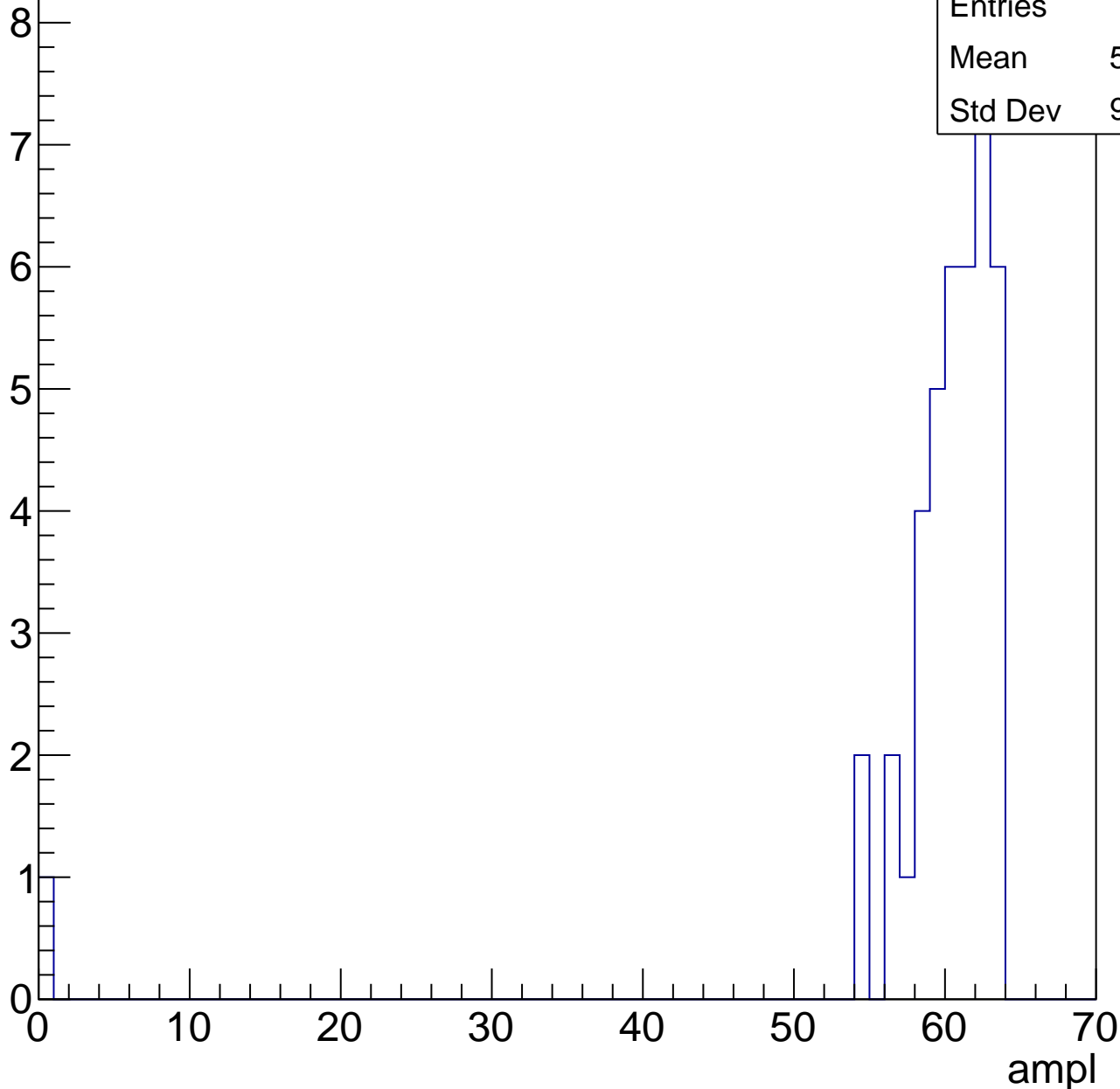


B0L000S, U4-ch30, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

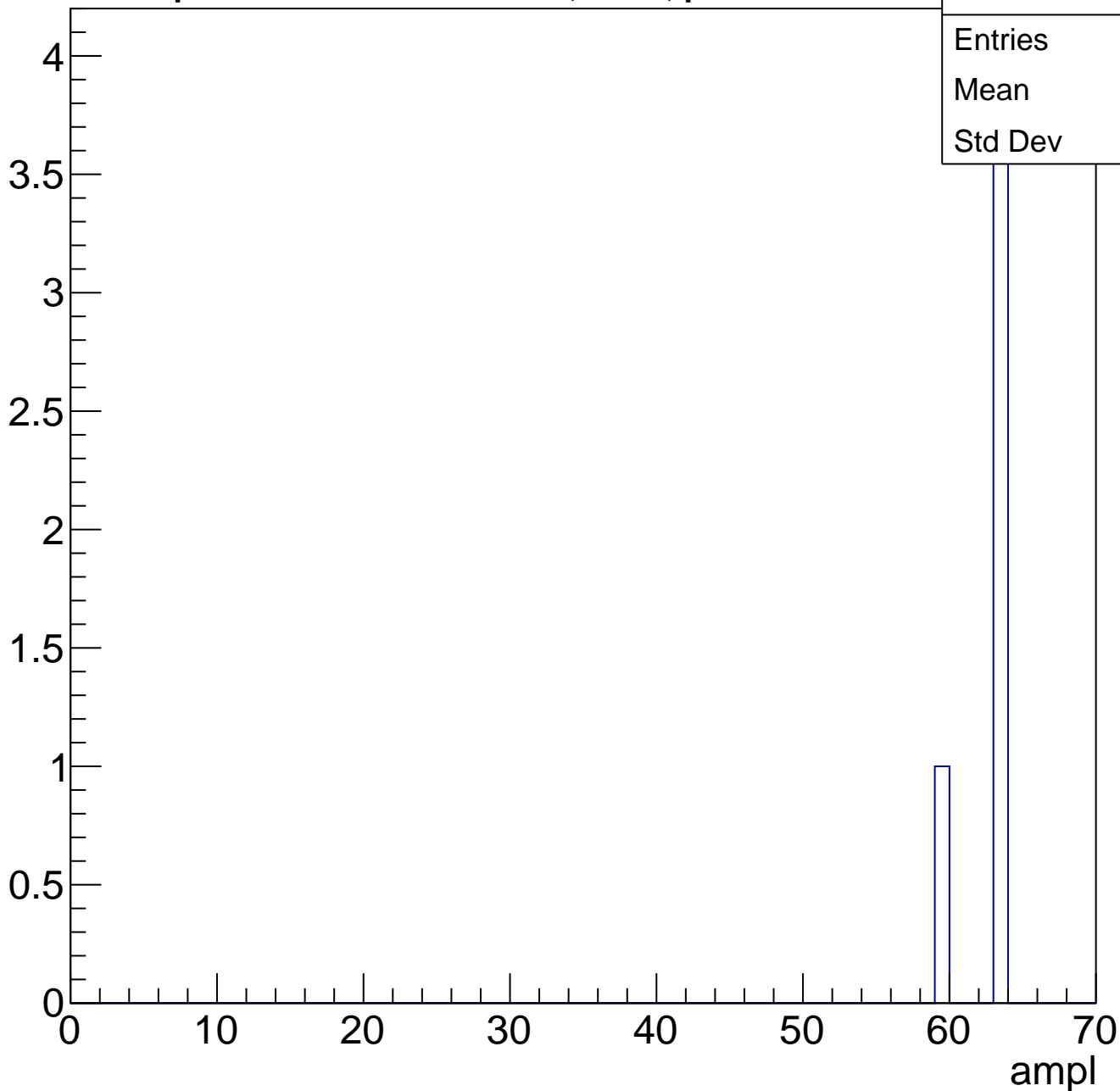
Entries	41
Mean	58.63
Std Dev	9.563



B0L000S, U4-ch30, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch30, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U4-ch31, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	81
Mean	30.77
Std Dev	3.511

Gaus mean : 31.0987

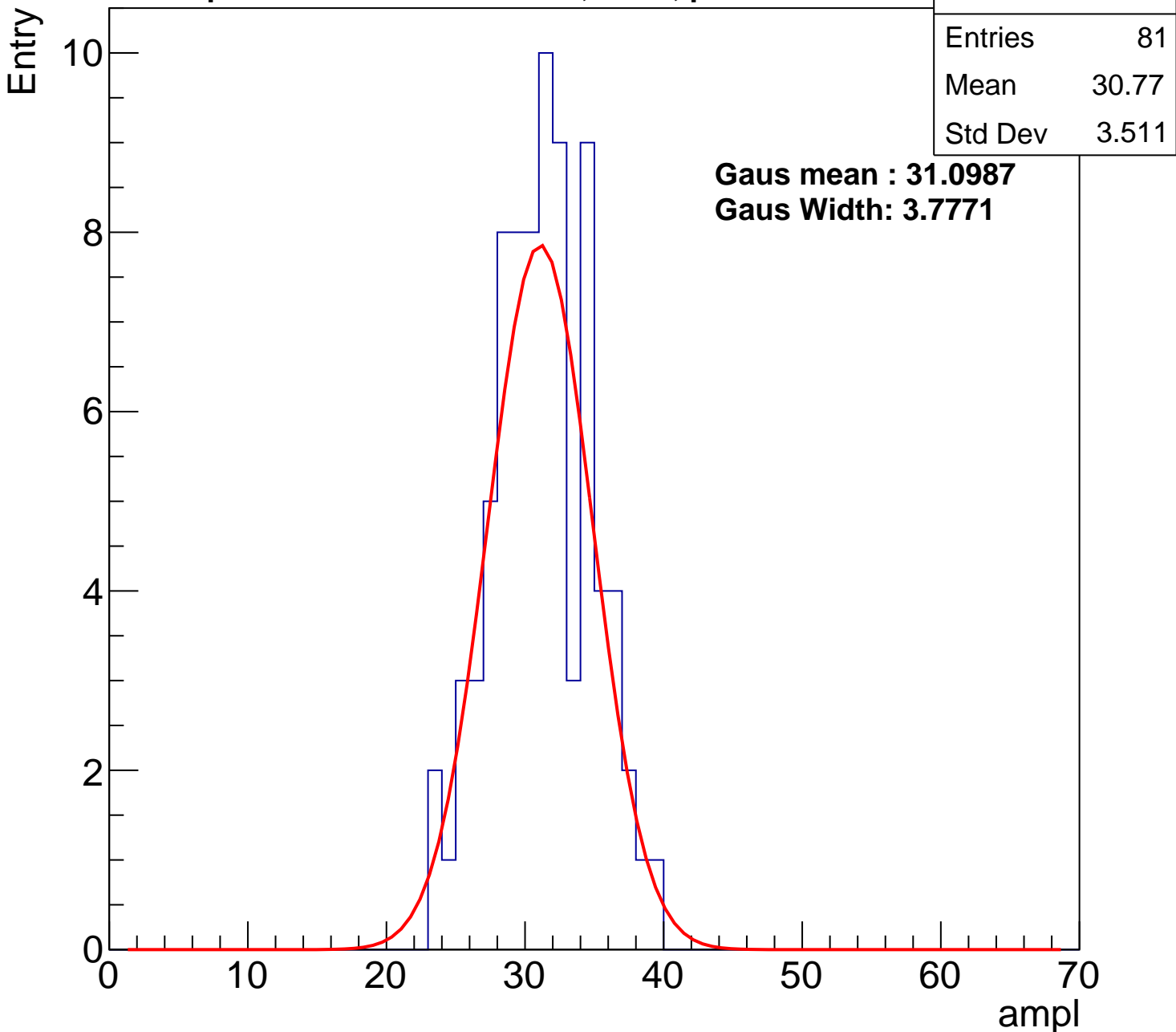
Gaus Width: 3.7771

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch31, adc1

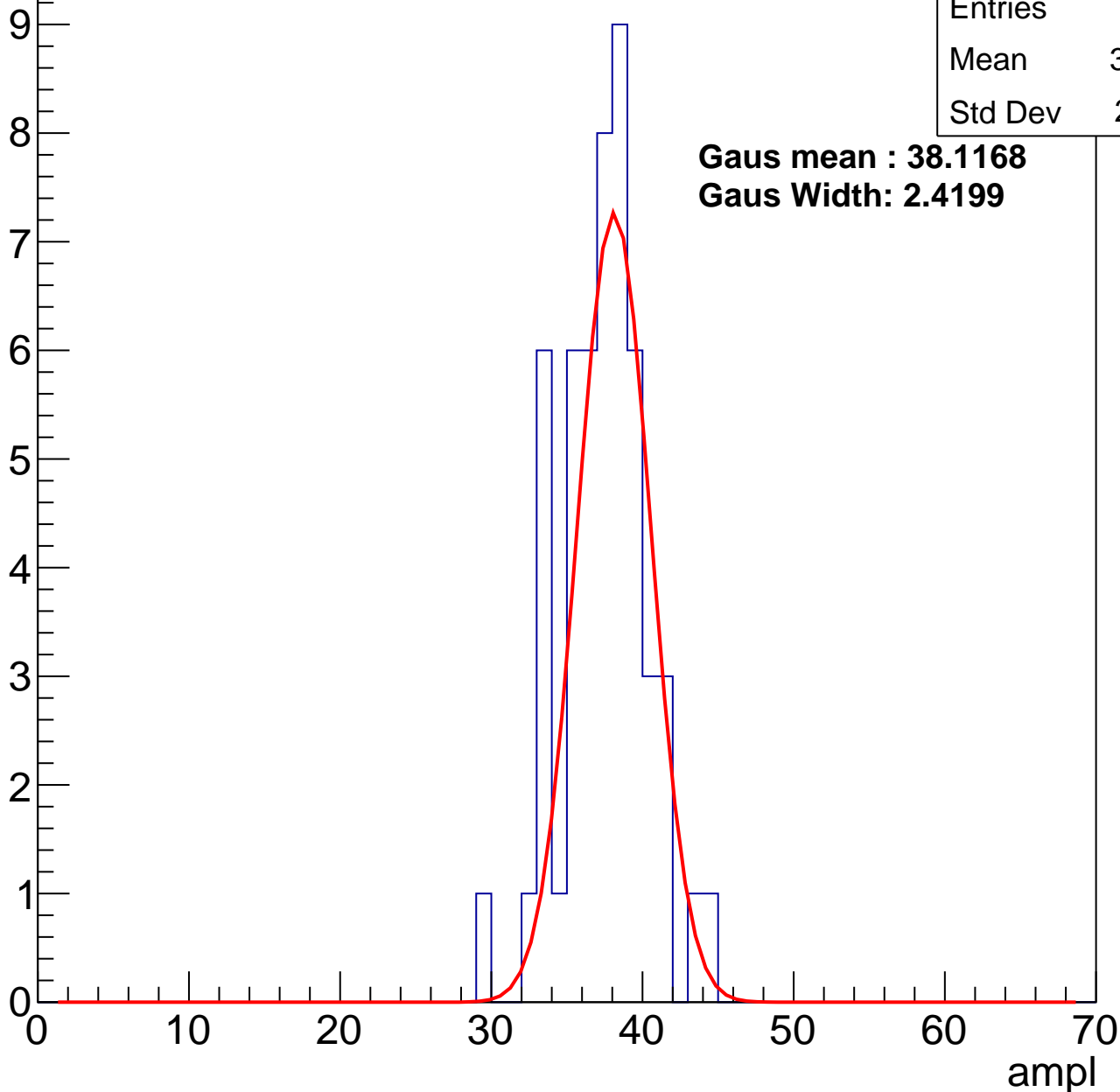
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	36.94
Std Dev	2.831

Gaus mean : 38.1168

Gaus Width: 2.4199



B0L000S, U4-ch31, adc2

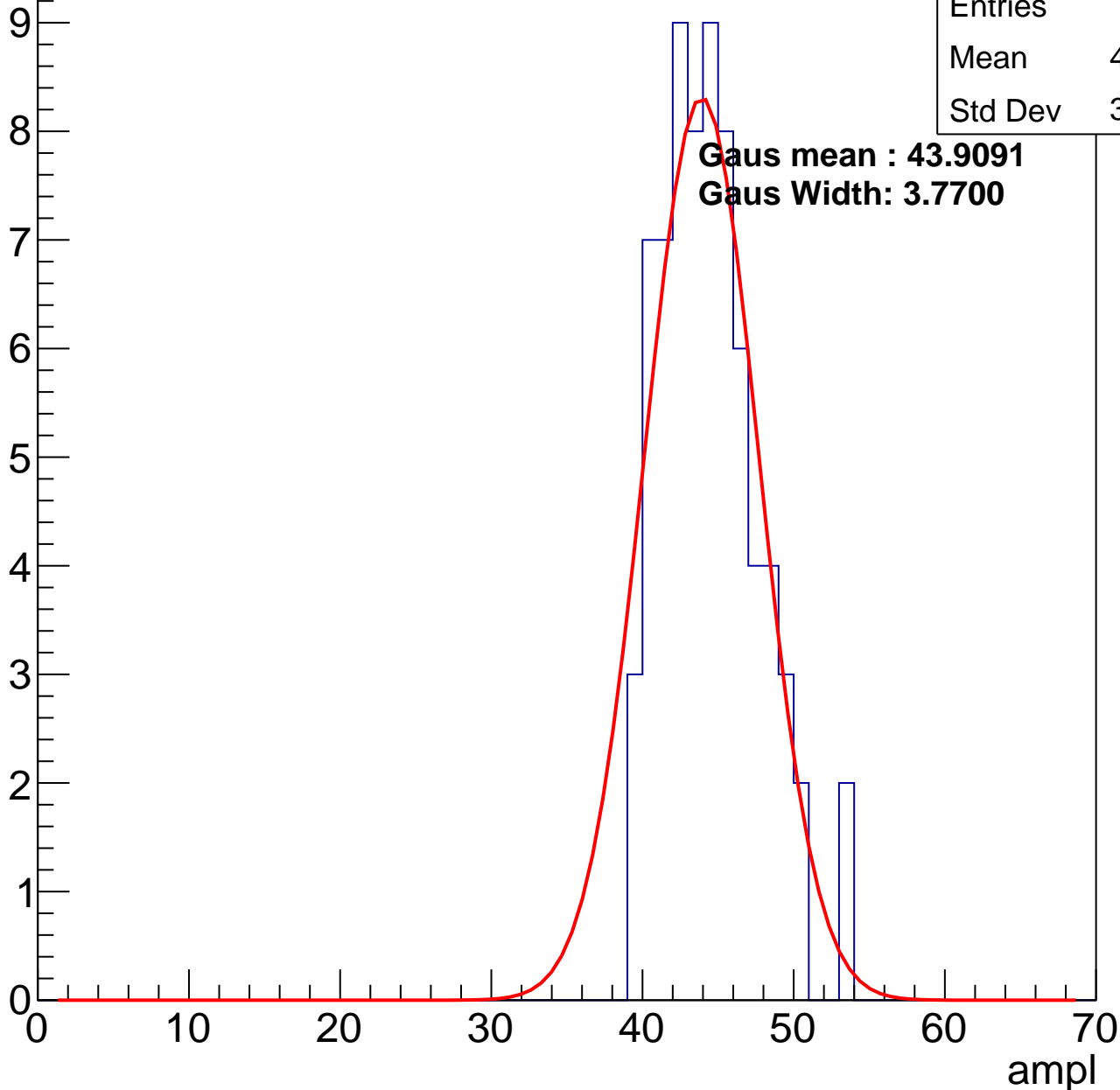
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	44.04
Std Dev	3.195

Gaus mean : 43.9091

Gaus Width: 3.7700

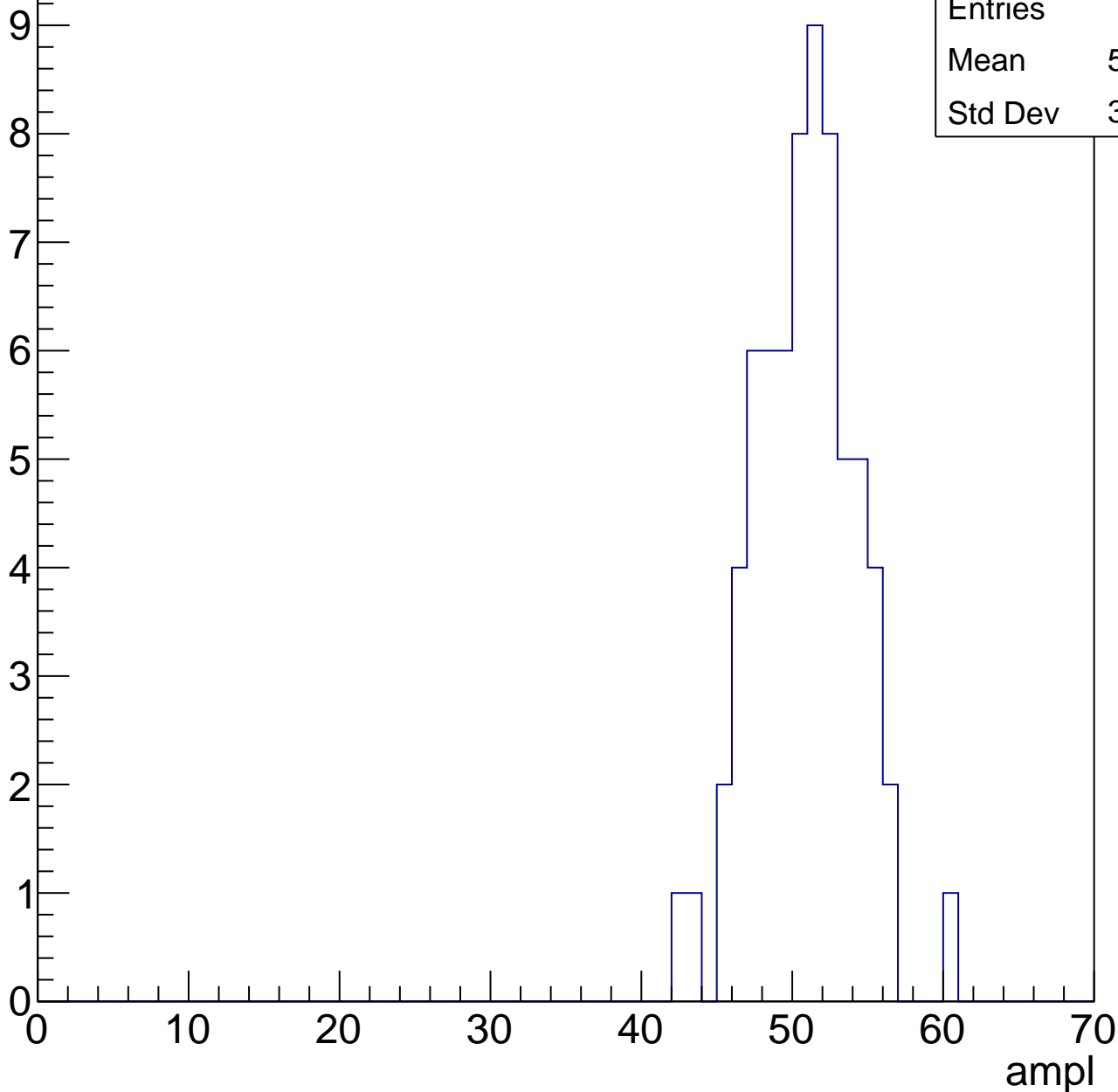


B0L000S, U4-ch31, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

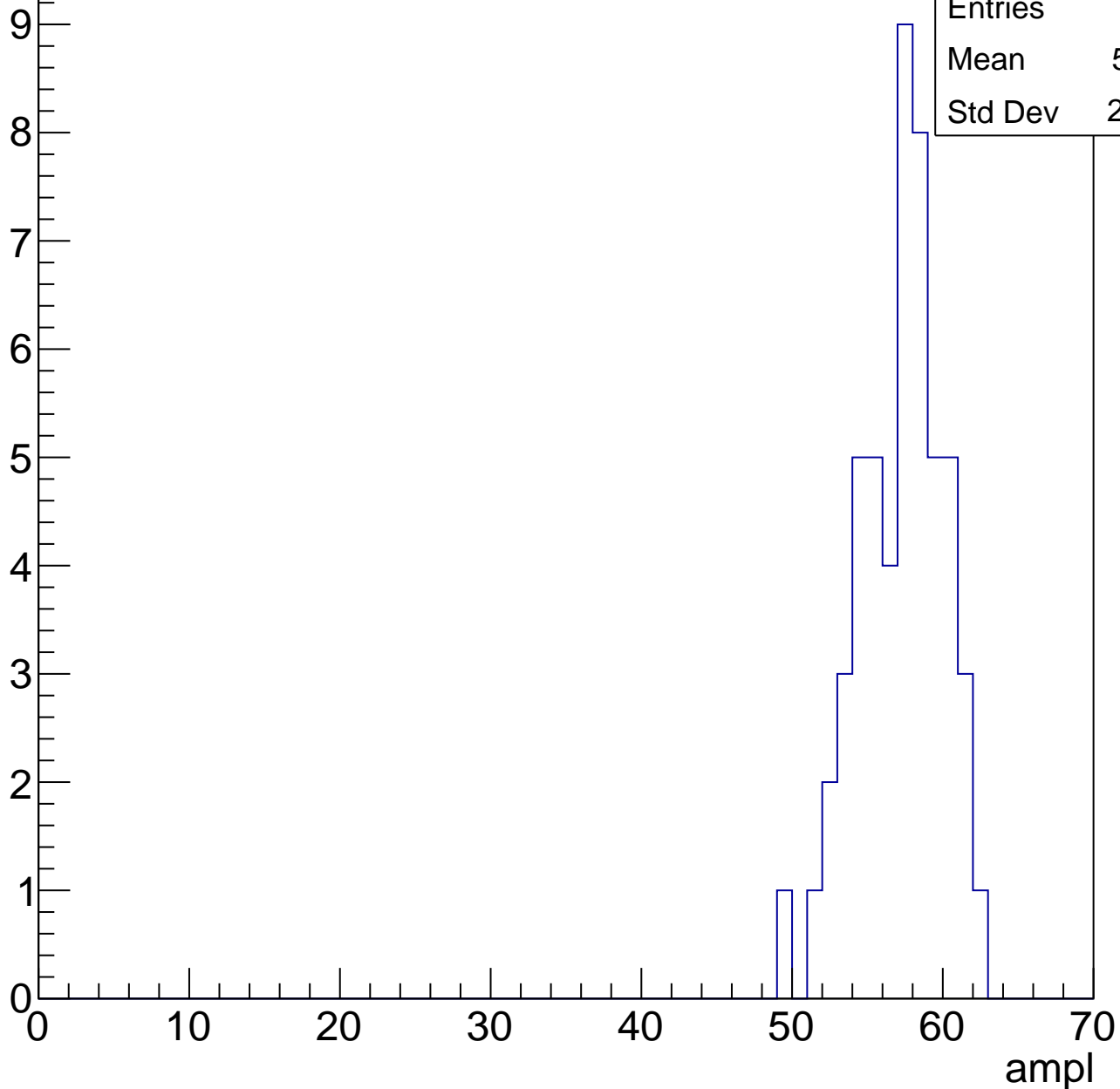
Entries	68
Mean	50.37
Std Dev	3.298



B0L000S, U4-ch31, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

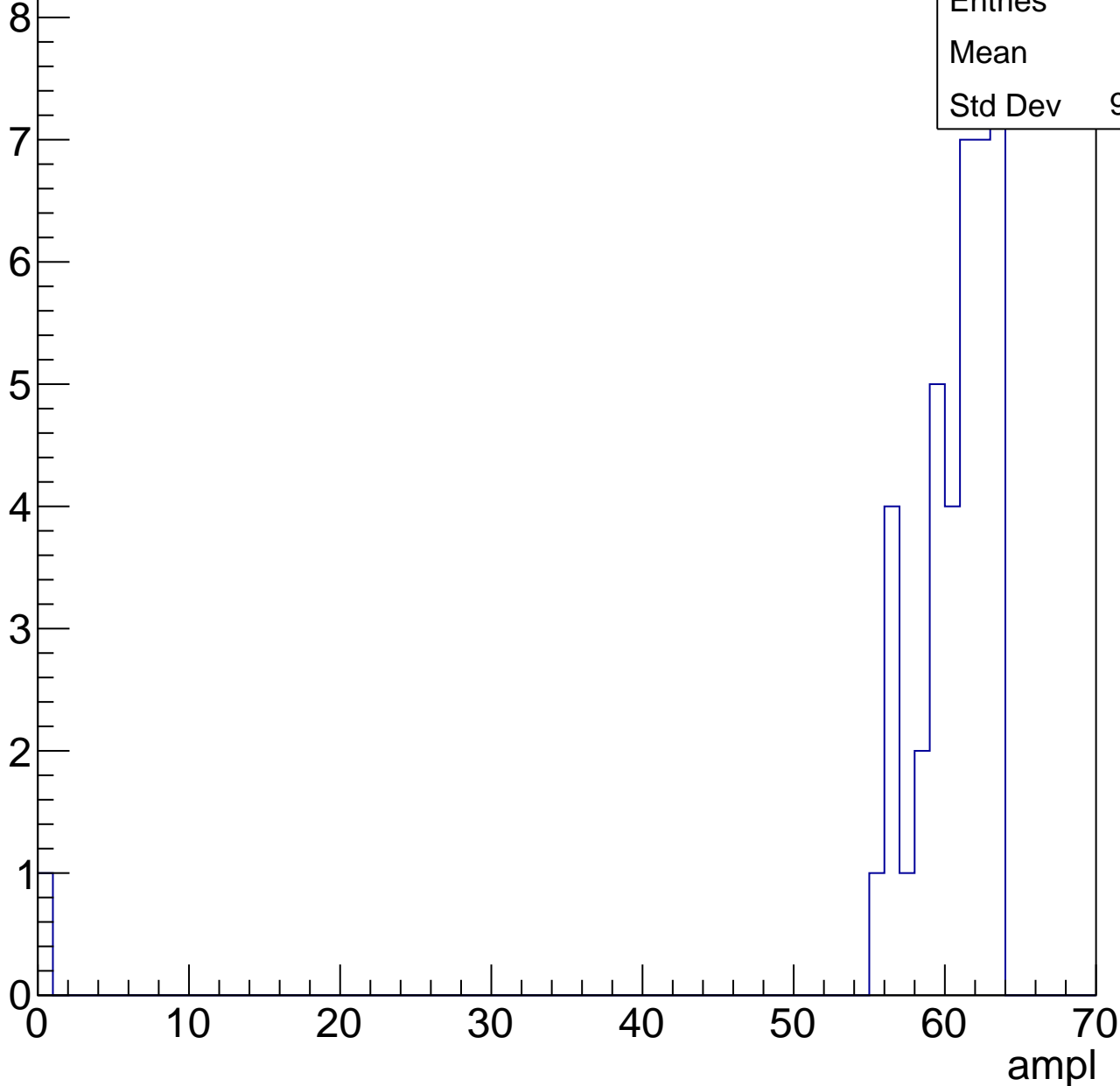


B0L000S, U4-ch31, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.8
Std Dev	9.696



B0L000S, U4-ch31, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch31, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch32, adc0

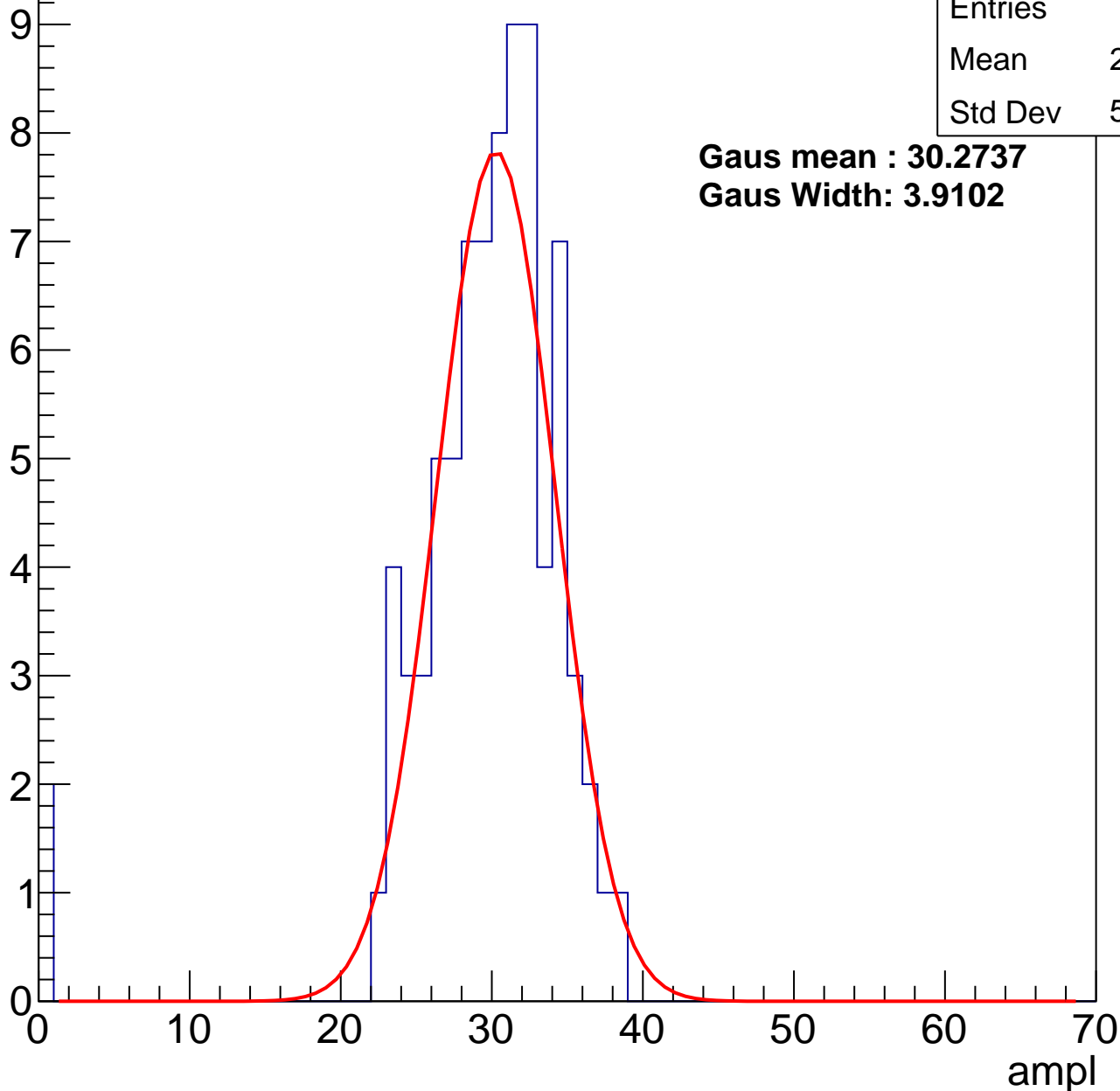
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	81
Mean	29.06
Std Dev	5.863

Gaus mean : 30.2737

Gaus Width: 3.9102



B0L000S, U4-ch32, adc1

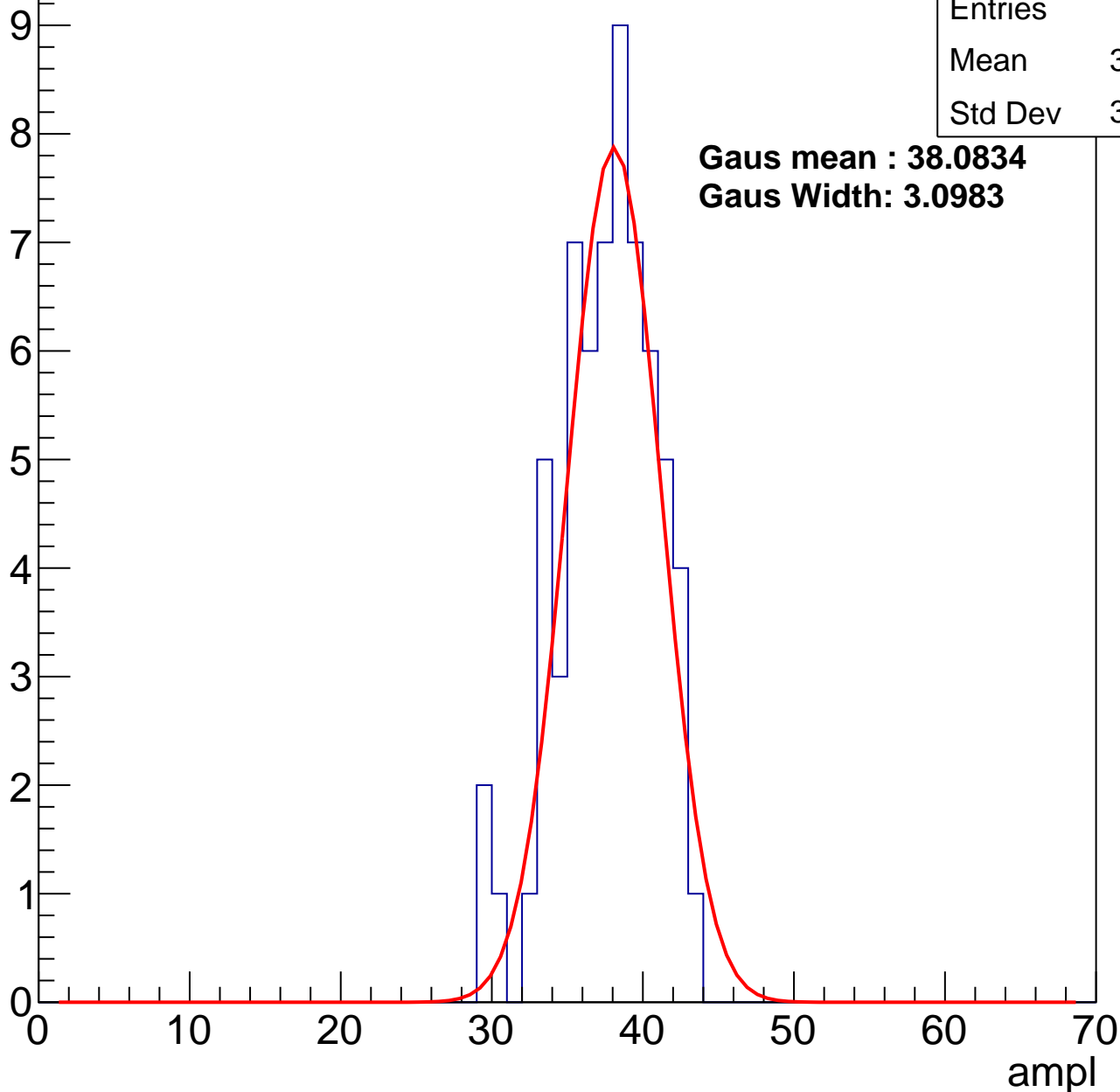
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	37.16
Std Dev	3.183

Gaus mean : 38.0834

Gaus Width: 3.0983



B0L000S, U4-ch32, adc2

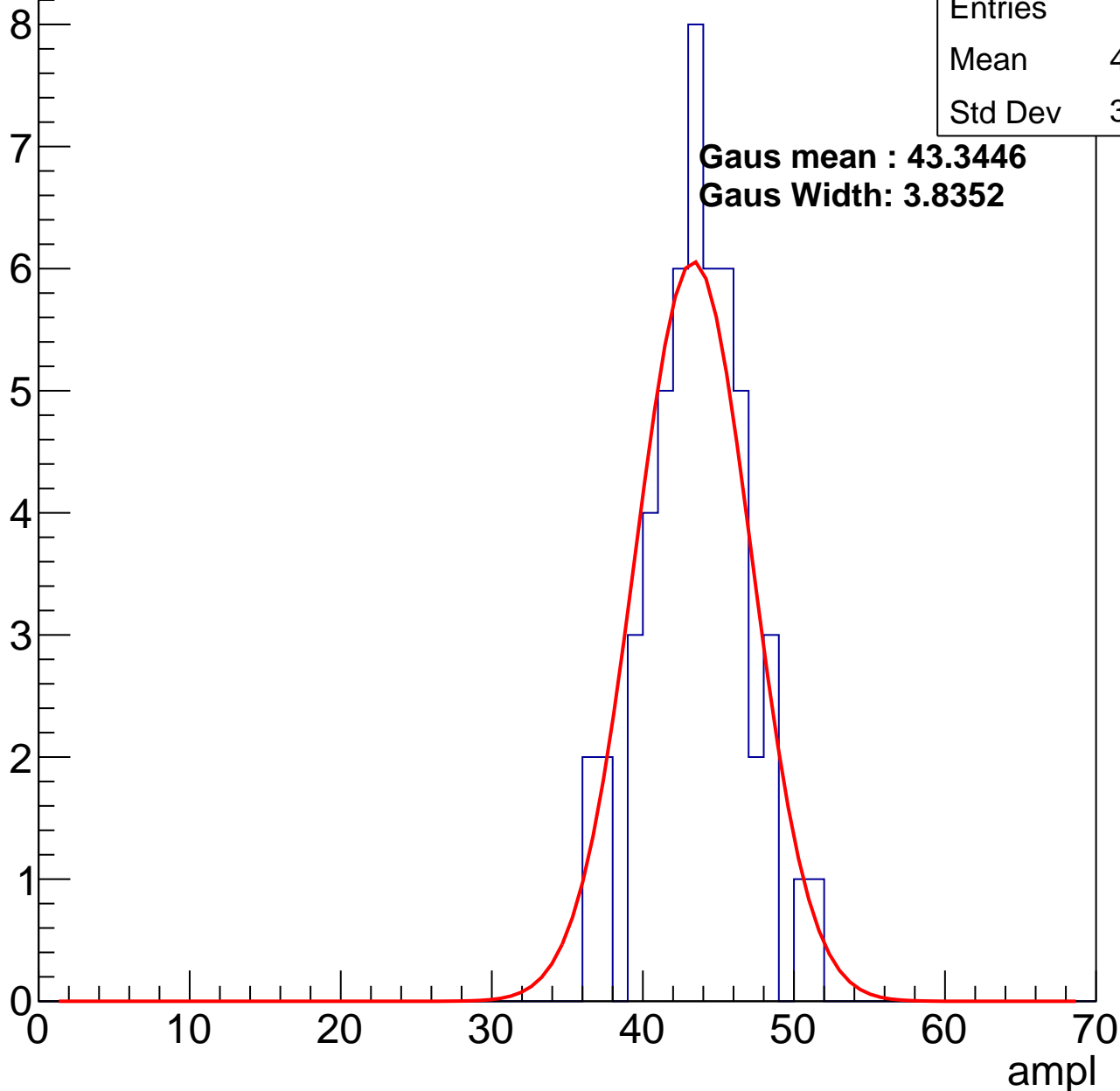
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	43.09
Std Dev	3.262

Gaus mean : 43.3446

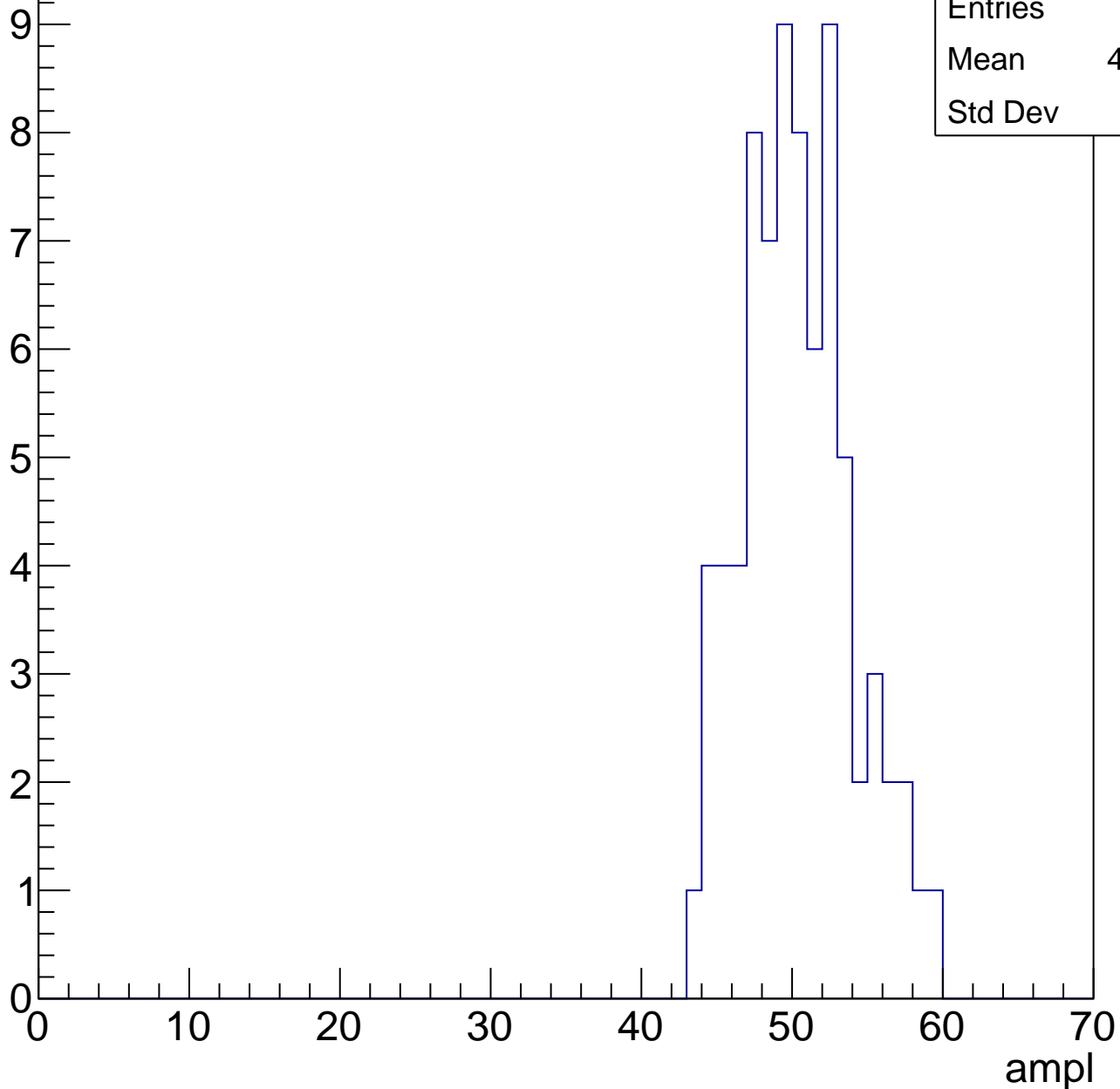
Gaus Width: 3.8352



B0L000S, U4-ch32, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

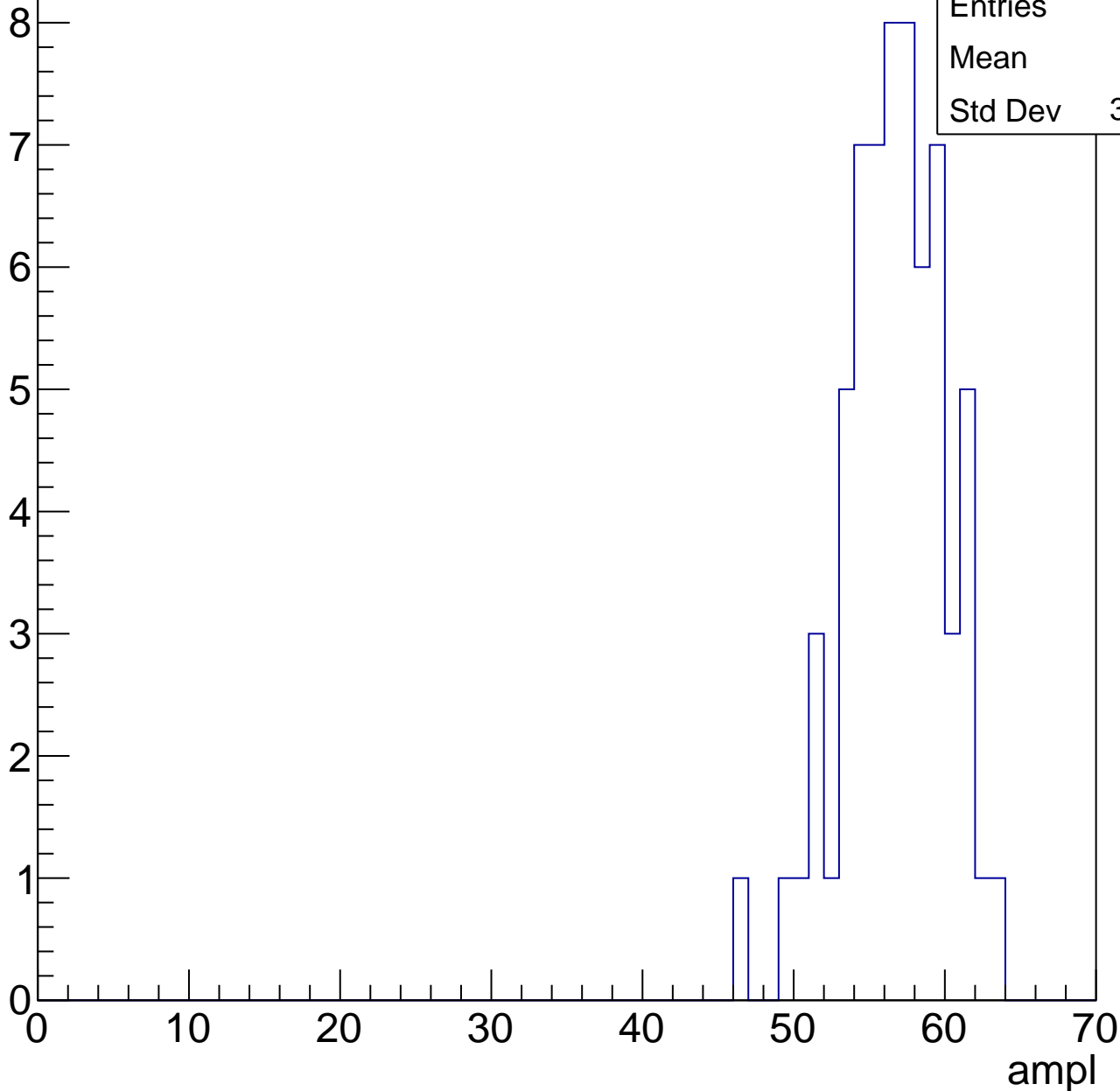


B0L000S, U4-ch32, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

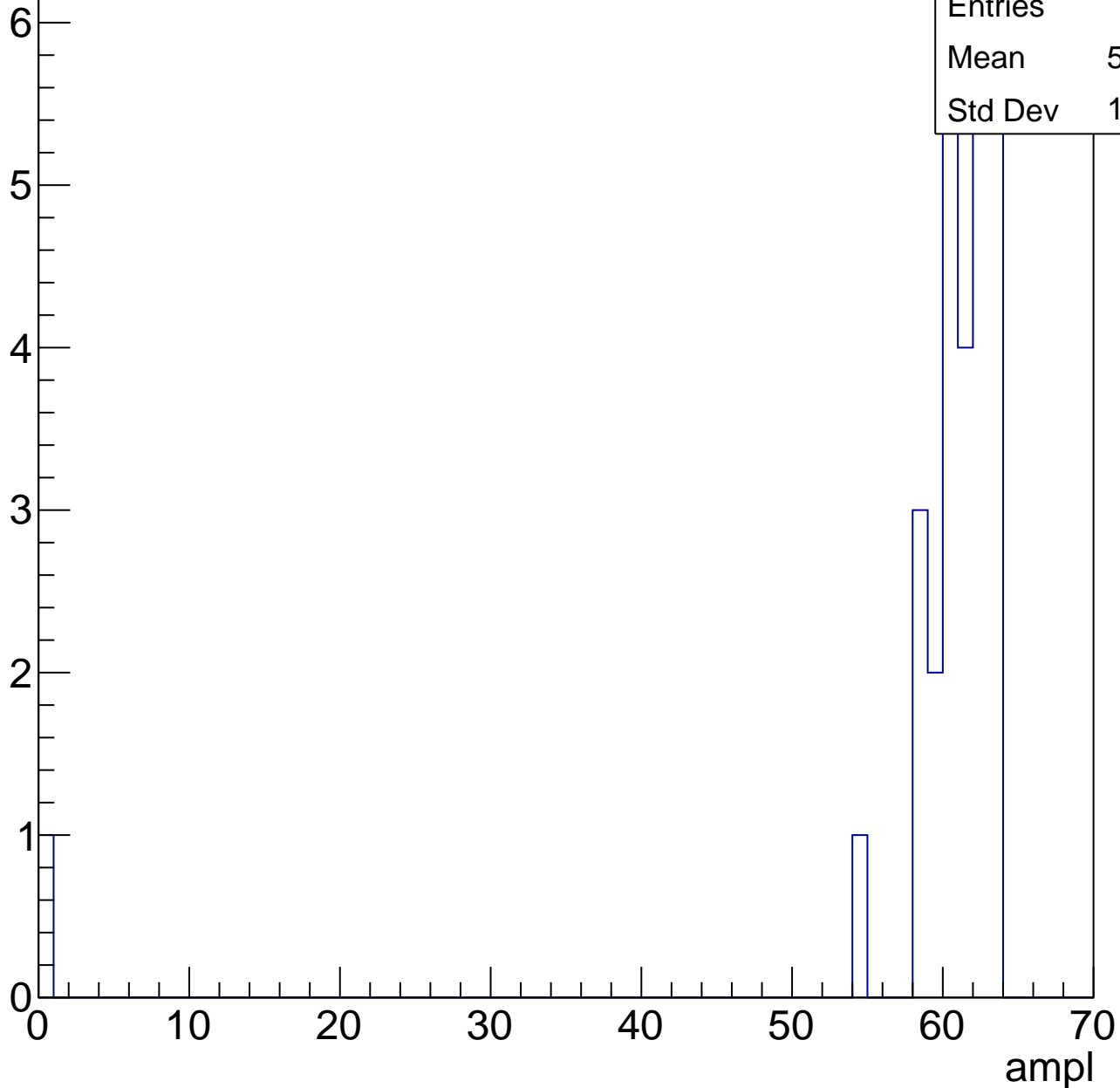
Entries	65
Mean	56.2
Std Dev	3.297



B0L000S, U4-ch32, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch32, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch32, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch33, adc0

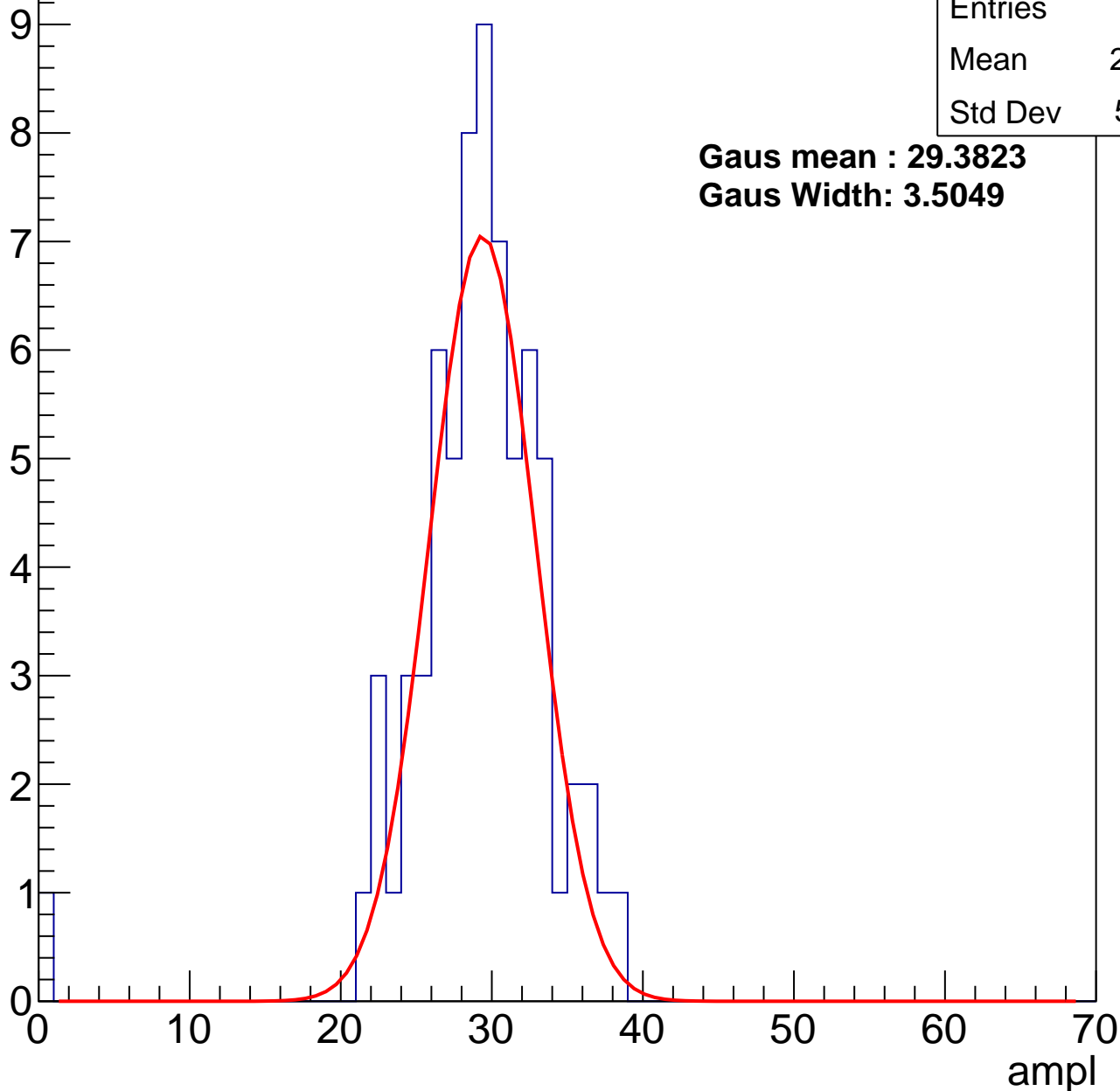
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	28.66
Std Dev	5.051

Gaus mean : 29.3823

Gaus Width: 3.5049



B0L000S, U4-ch33, adc1

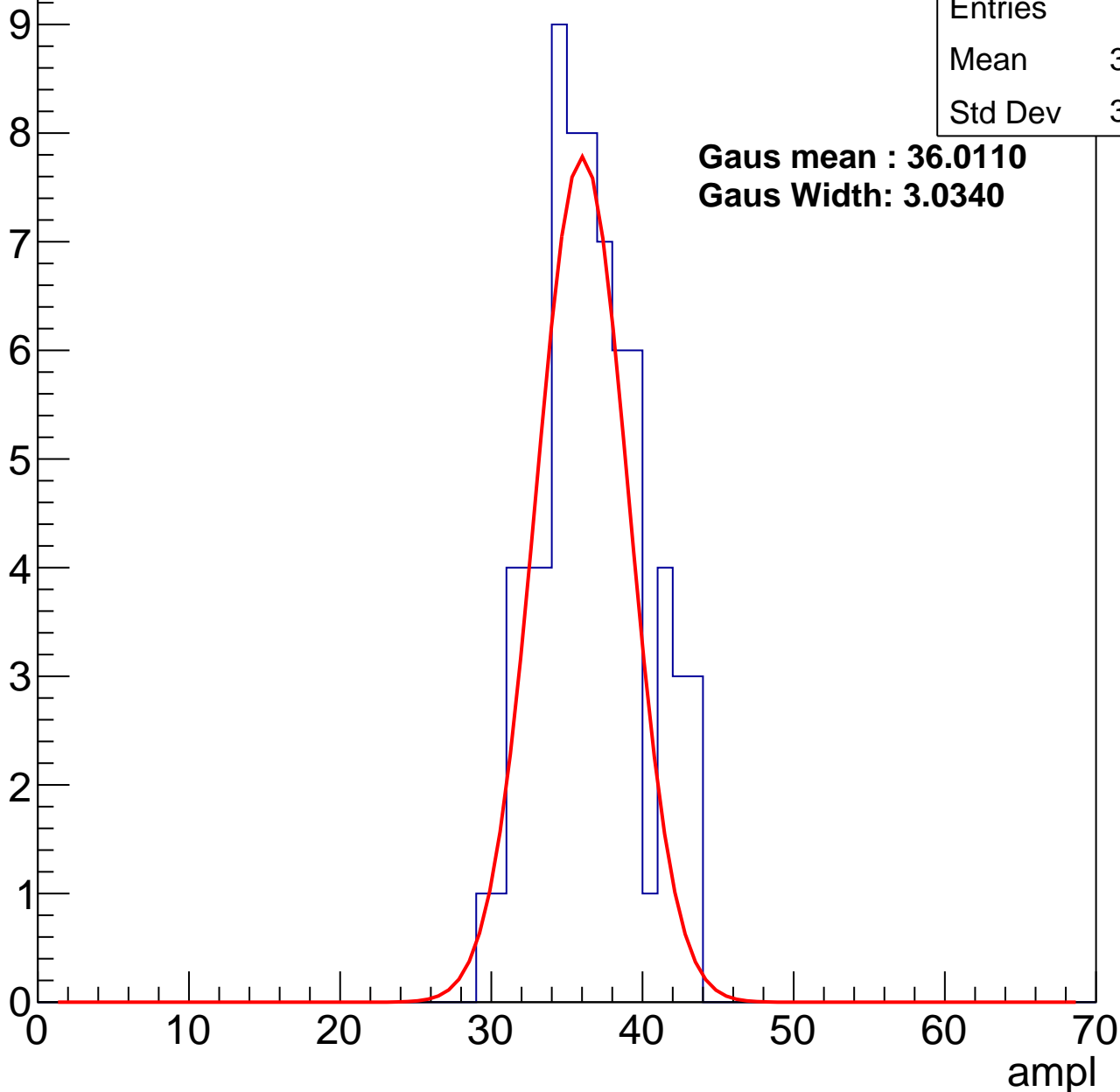
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.19
Std Dev	3.368

Gaus mean : 36.0110

Gaus Width: 3.0340



B0L000S, U4-ch33, adc2

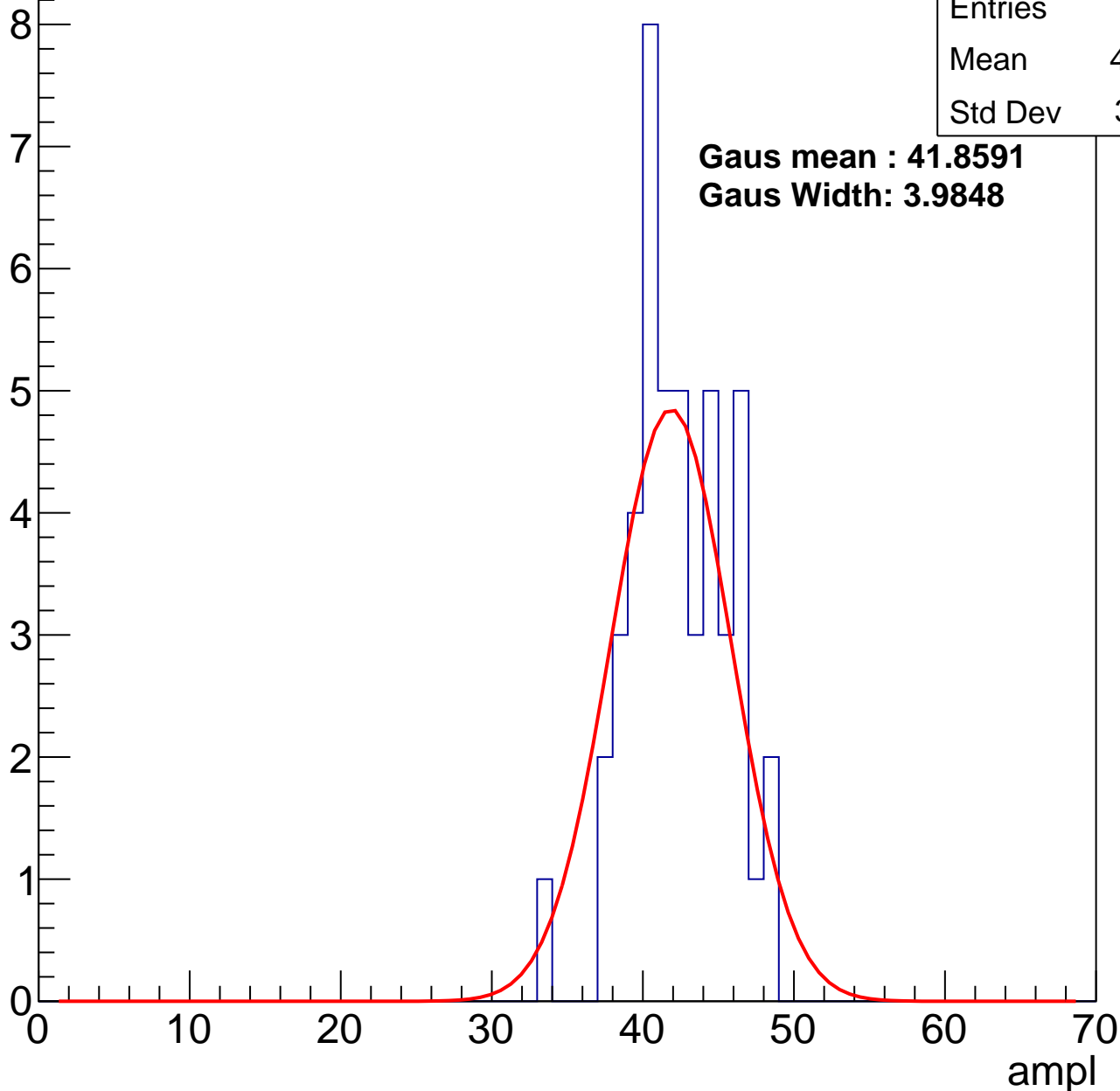
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	41.89
Std Dev	3.191

Gaus mean : 41.8591

Gaus Width: 3.9848



B0L000S, U4-ch33, adc3

calib_packv5_042523_0143.root, FC#5, port B1

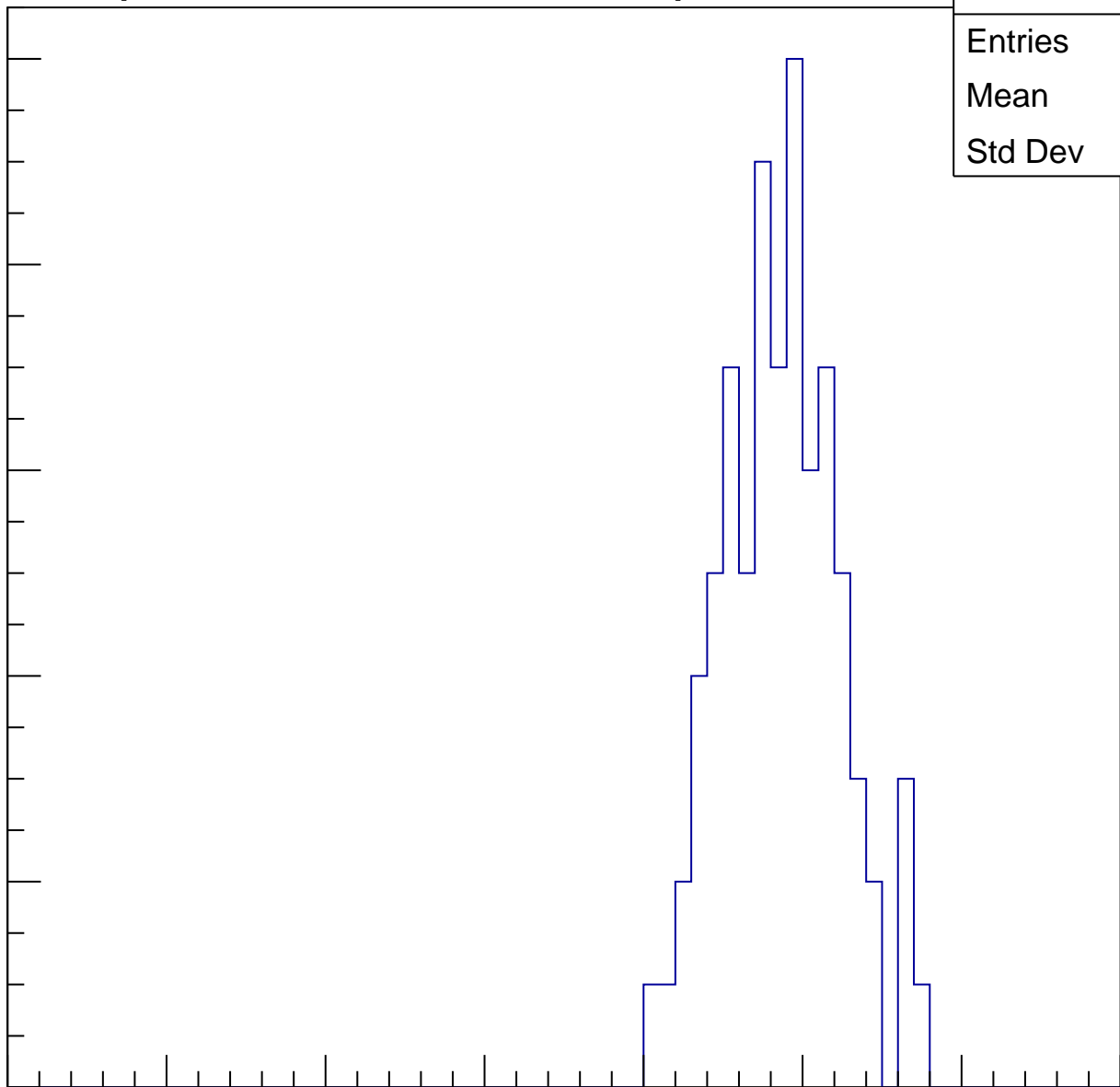
Entries	78
Mean	48.21
Std Dev	3.67

Entry

10
8
6
4
2
0

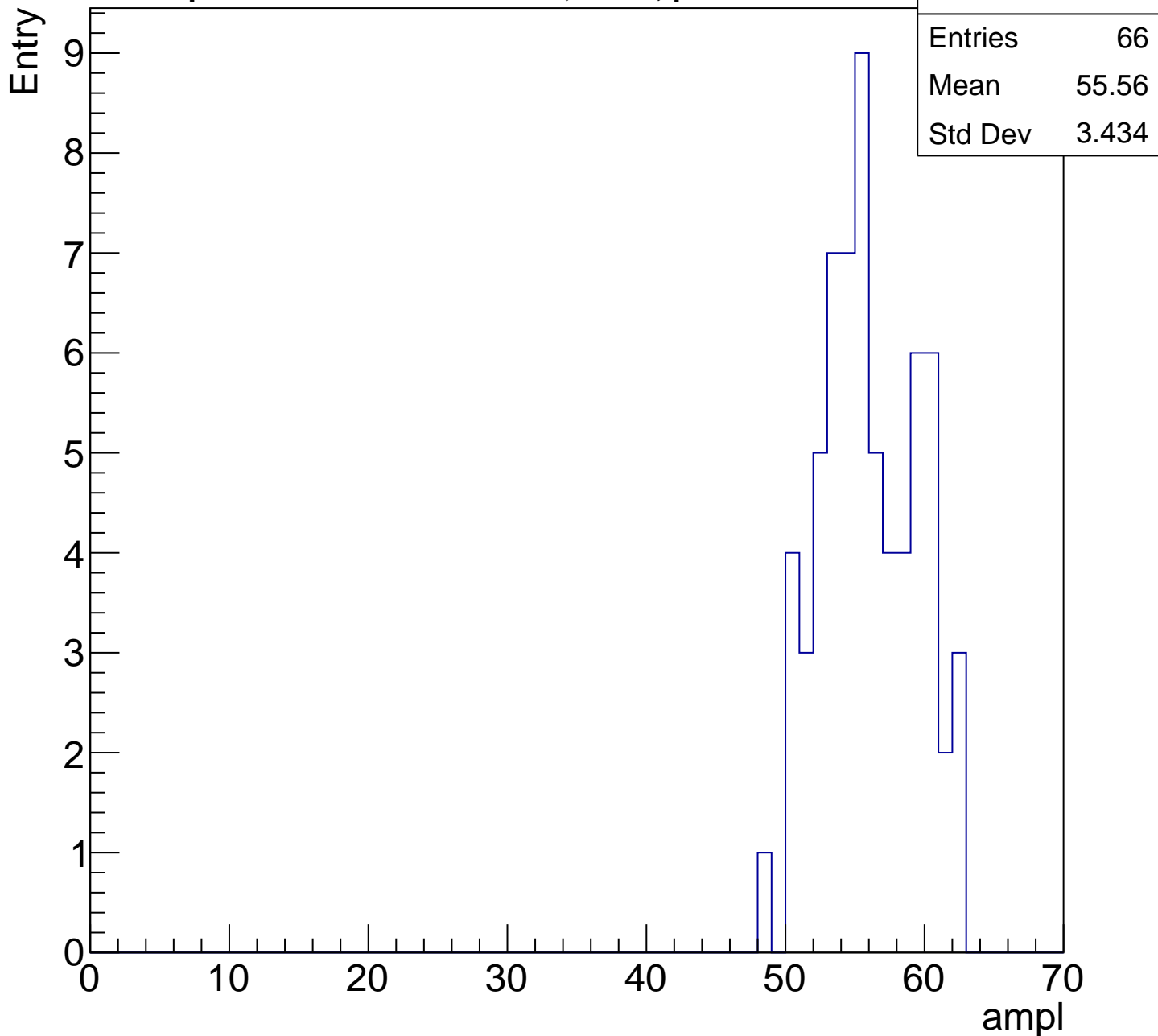
0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch33, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch33, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

5

4

3

2

1

0

Entries	33
Mean	57.58
Std Dev	10.48

ampl

0

10

20

30

40

50

60

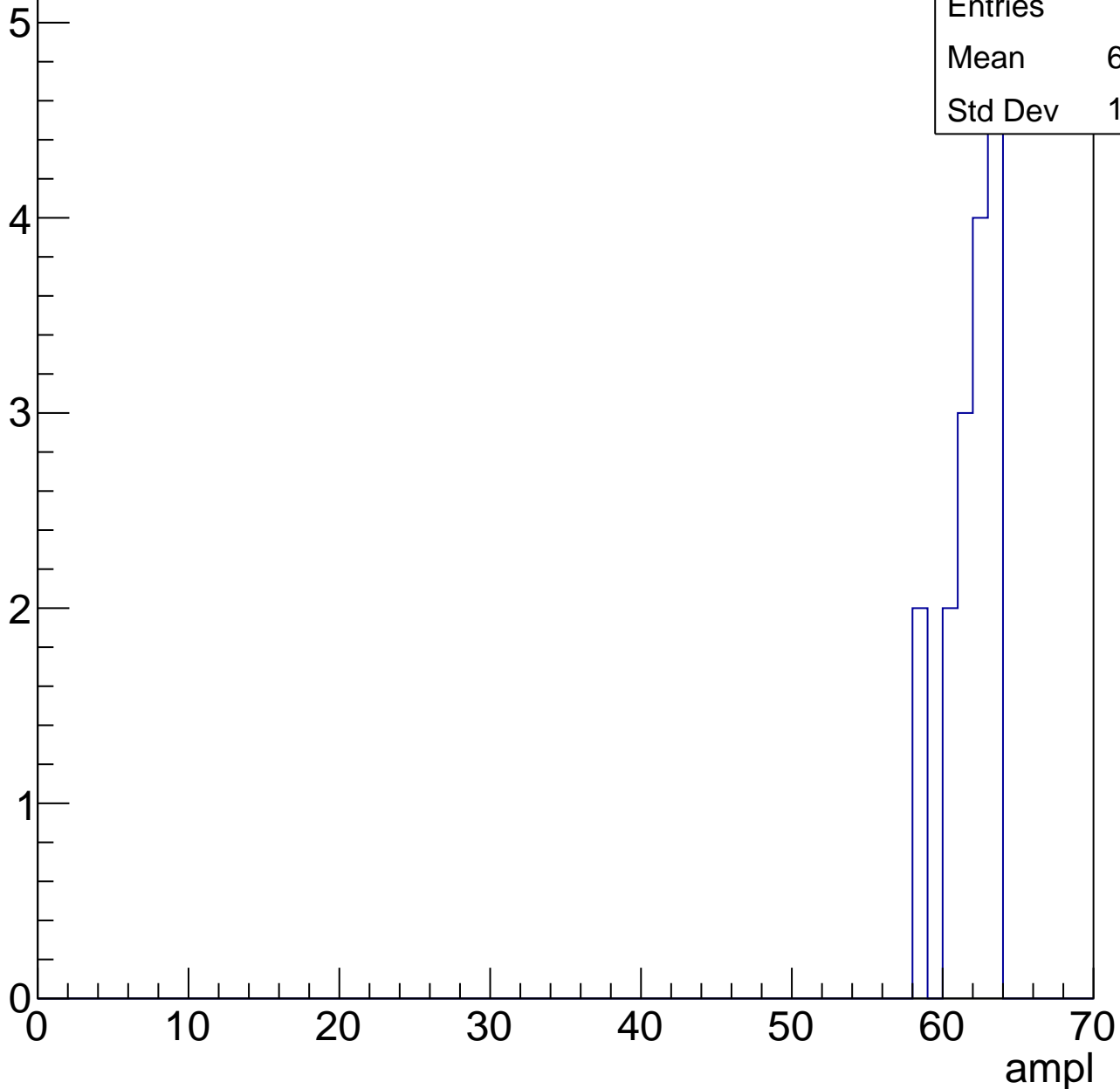
70

B0L000S, U4-ch33, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.38
Std Dev	1.615



B0L000S, U4-ch33, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch34, adc0

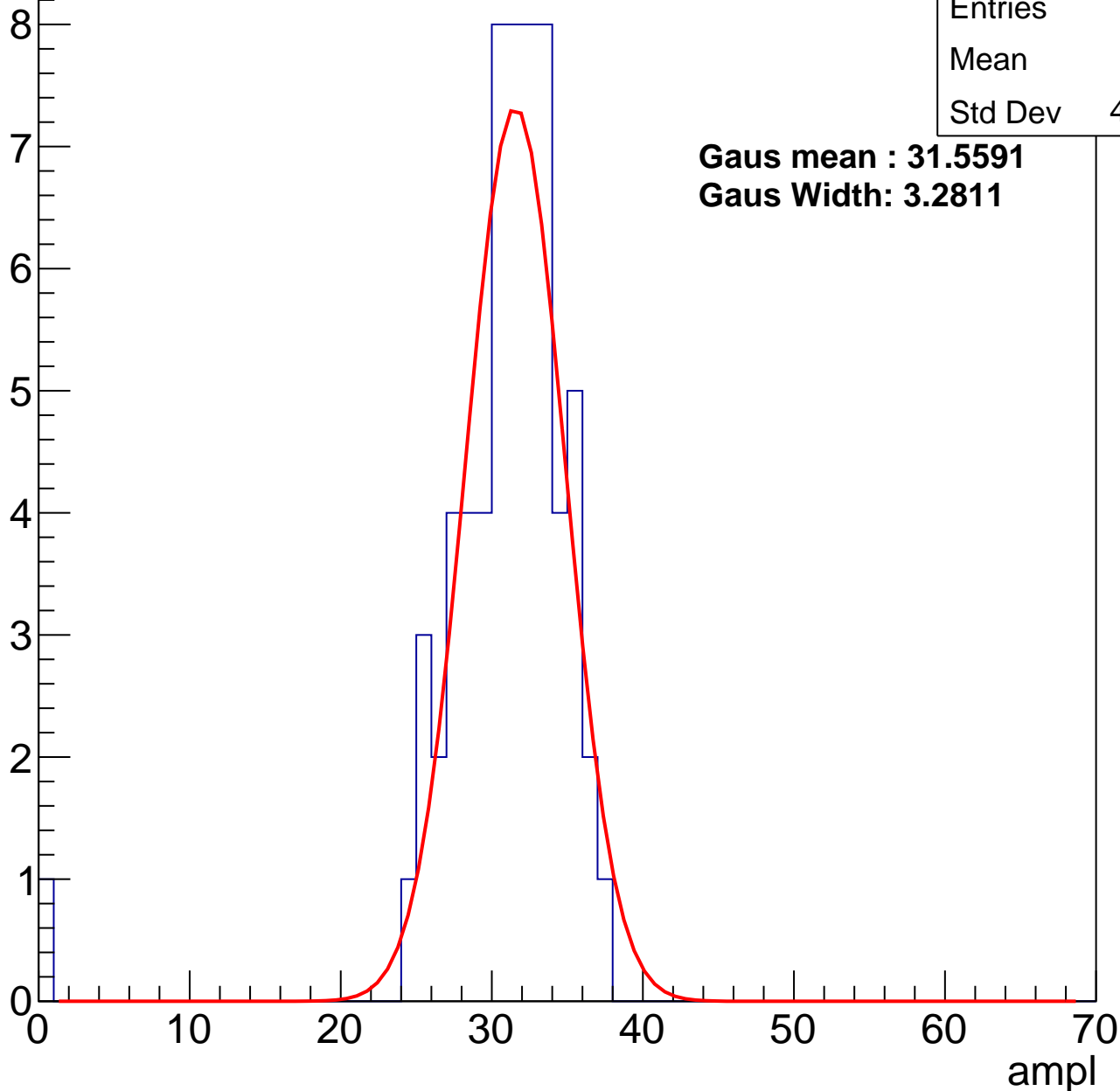
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.4
Std Dev	4.897

Gaus mean : 31.5591

Gaus Width: 3.2811



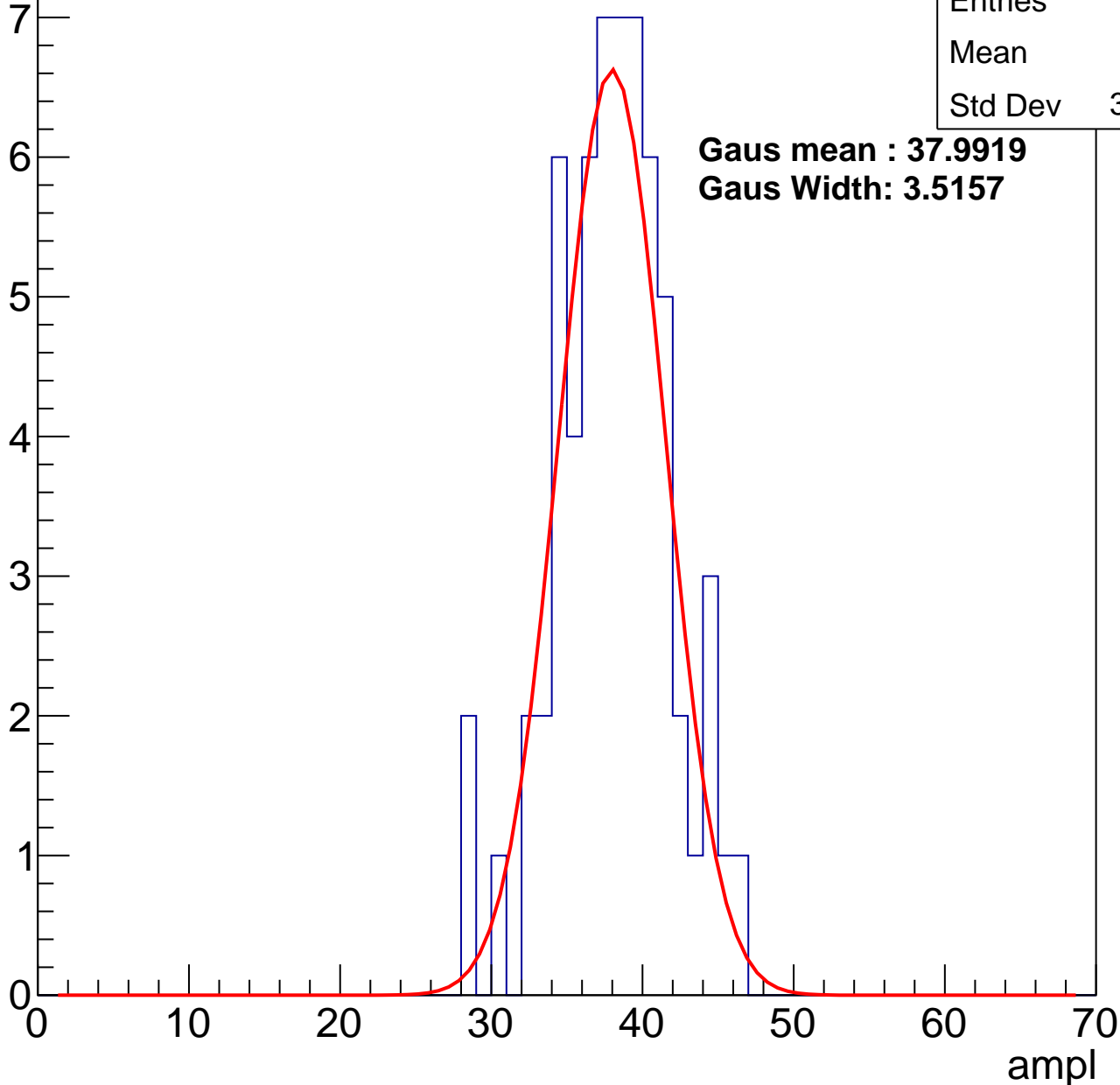
B0L000S, U4-ch34, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	37.6
Std Dev	3.765

Gaus mean : 37.9919
Gaus Width: 3.5157



B0L000S, U4-ch34, adc2

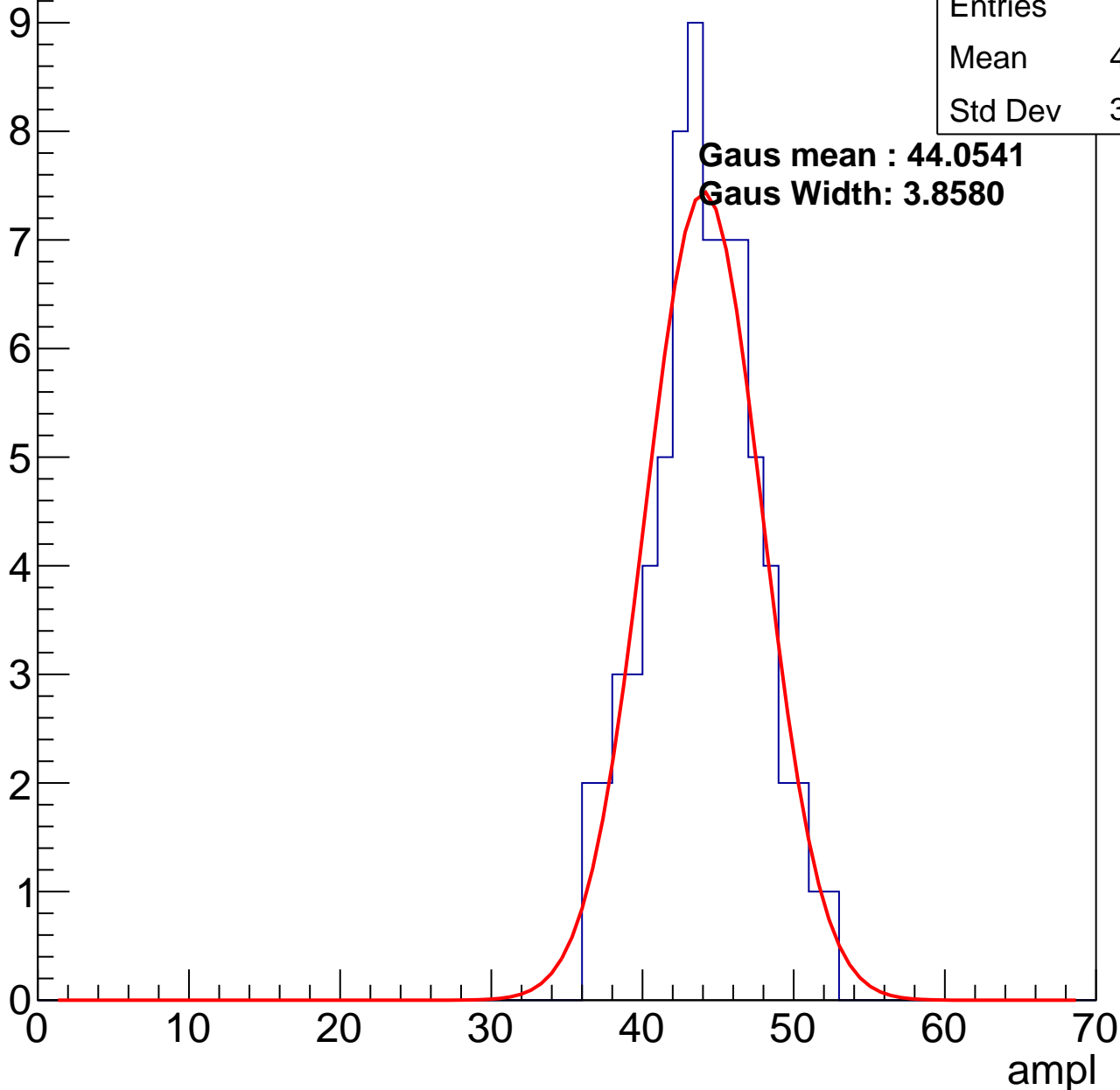
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	43.58
Std Dev	3.585

Gaus mean : 44.0541

Gaus Width: 3.8580

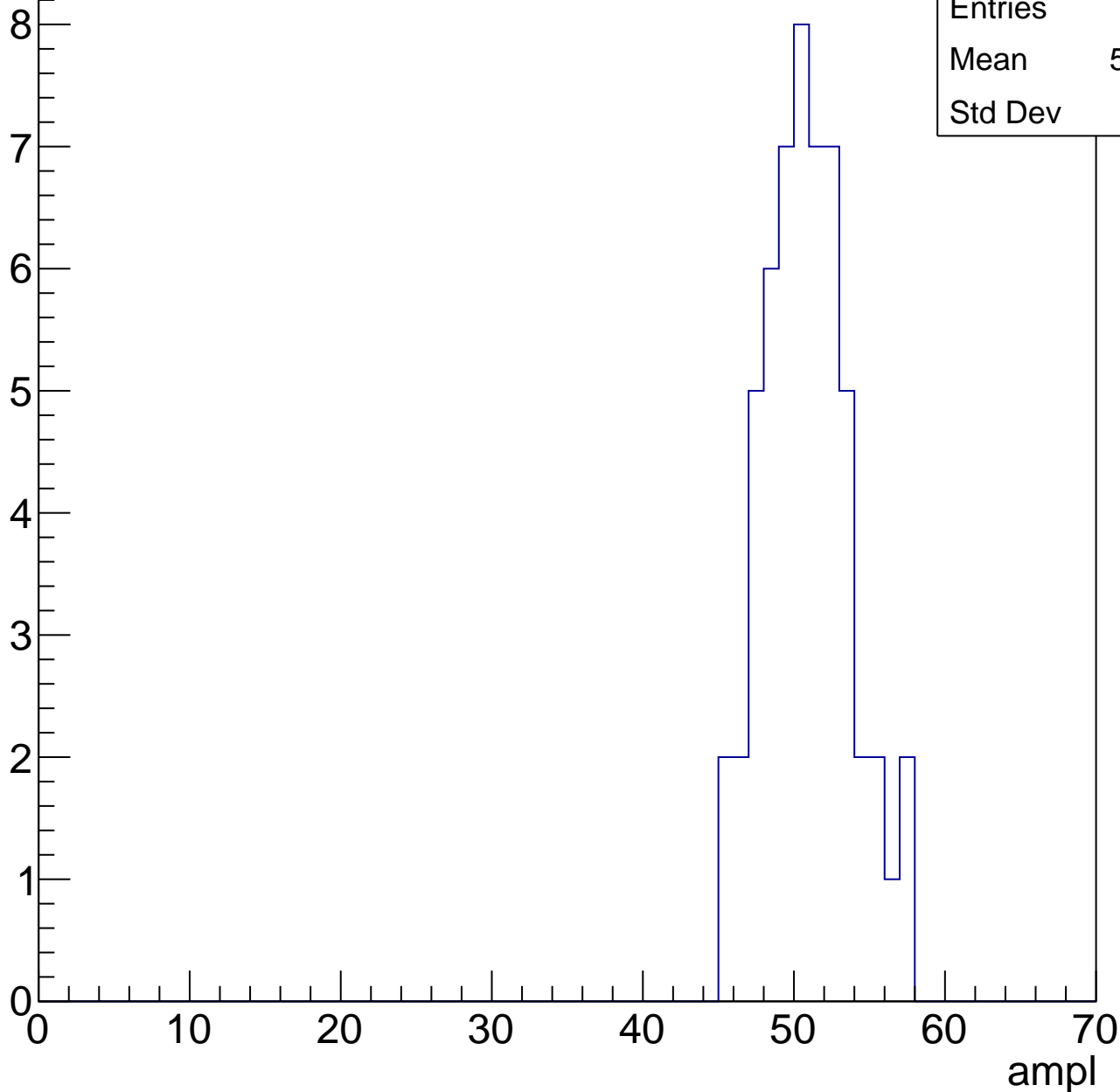


B0L000S, U4-ch34, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	50.39
Std Dev	2.82

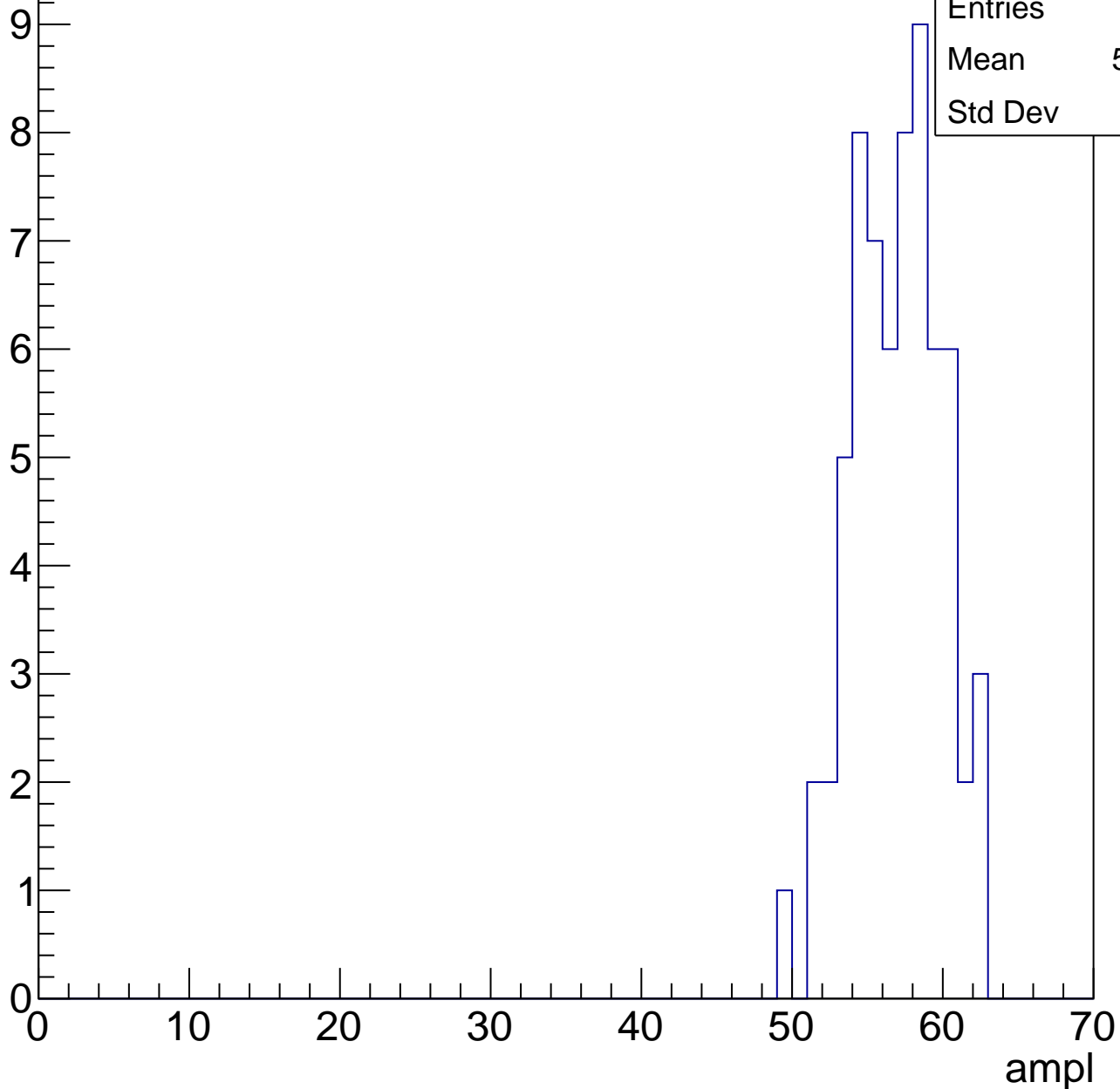


B0L000S, U4-ch34, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	56.51
Std Dev	2.91

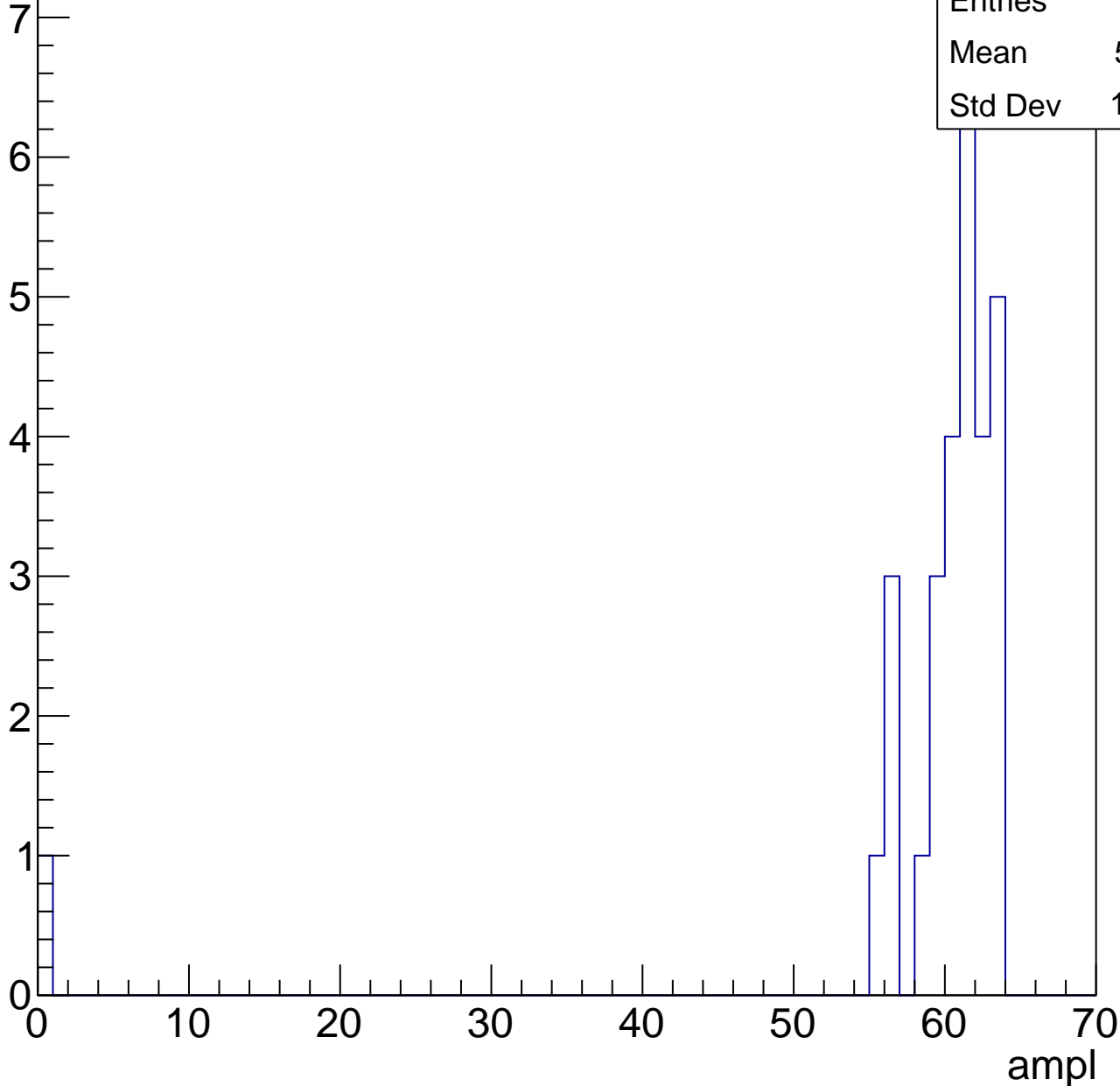


B0L000S, U4-ch34, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	29
Mean	58.21
Std Dev	11.23

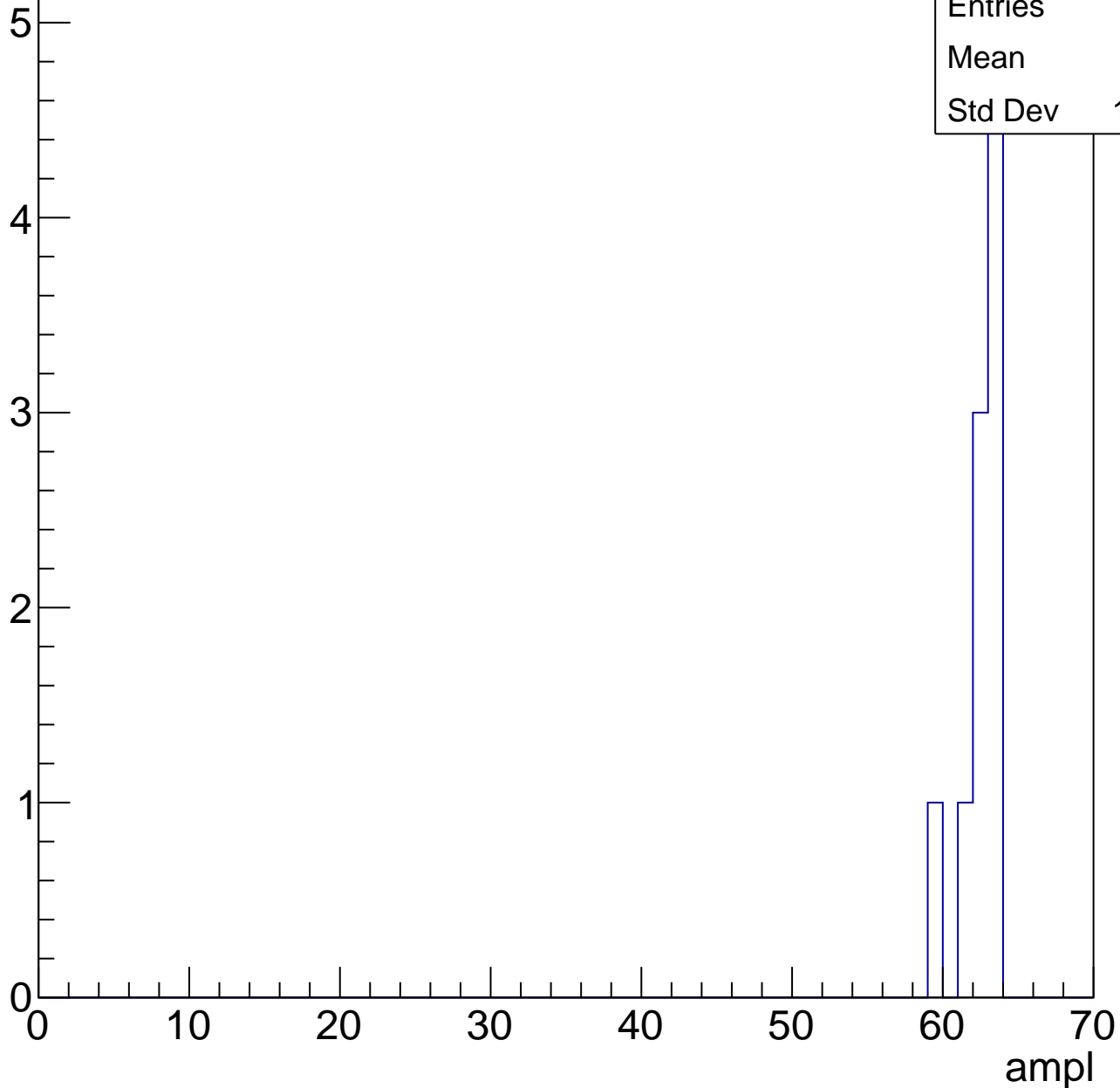


B0L000S, U4-ch34, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62.1
Std Dev	1.221



B0L000S, U4-ch34, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch35, adc0

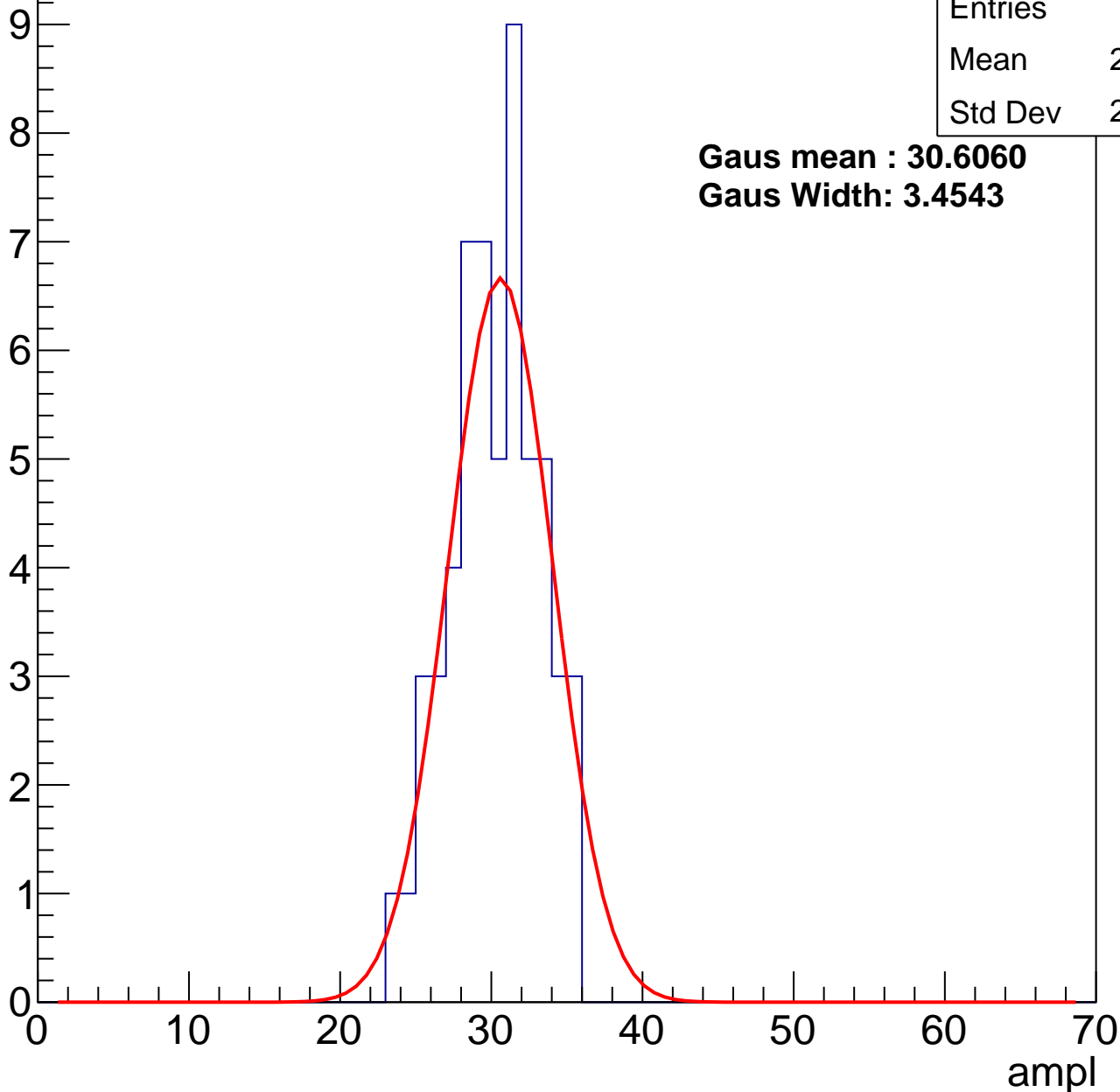
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.79
Std Dev	2.908

Gaus mean : 30.6060

Gaus Width: 3.4543



B0L000S, U4-ch35, adc1

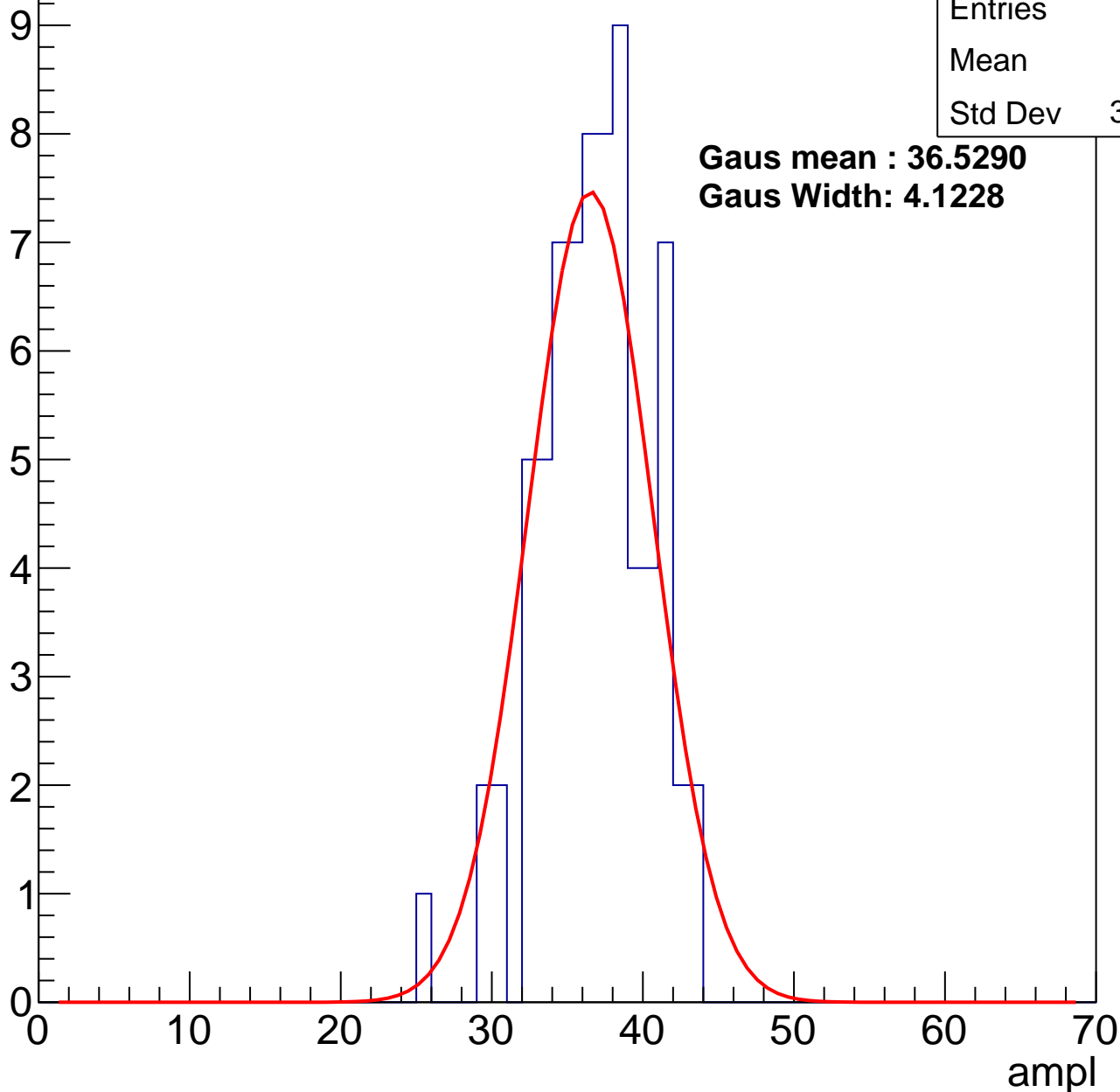
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	36.3
Std Dev	3.576

Gaus mean : 36.5290

Gaus Width: 4.1228



B0L000S, U4-ch35, adc2

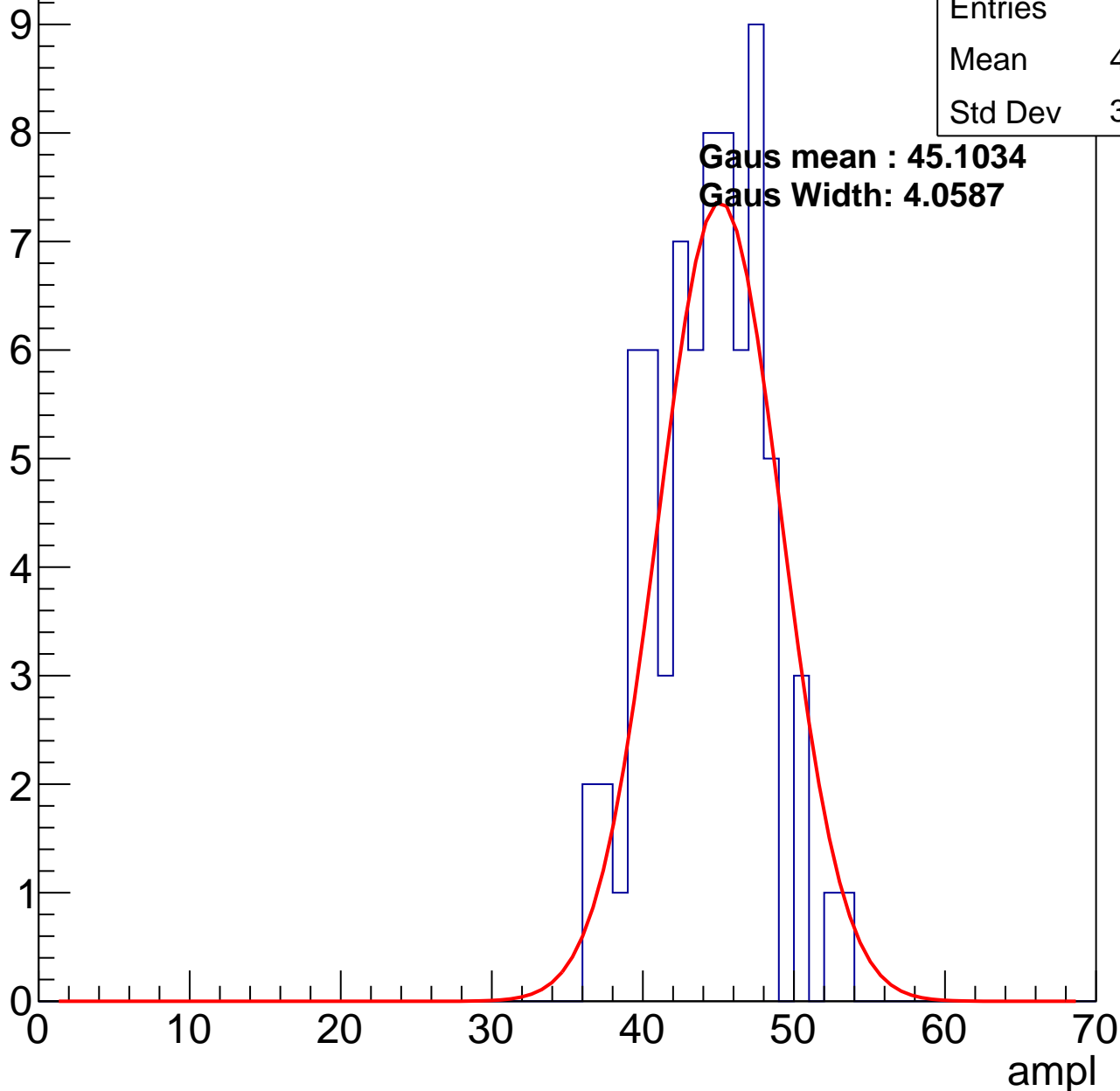
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.77
Std Dev	3.696

Gaus mean : 45.1034

Gaus Width: 4.0587

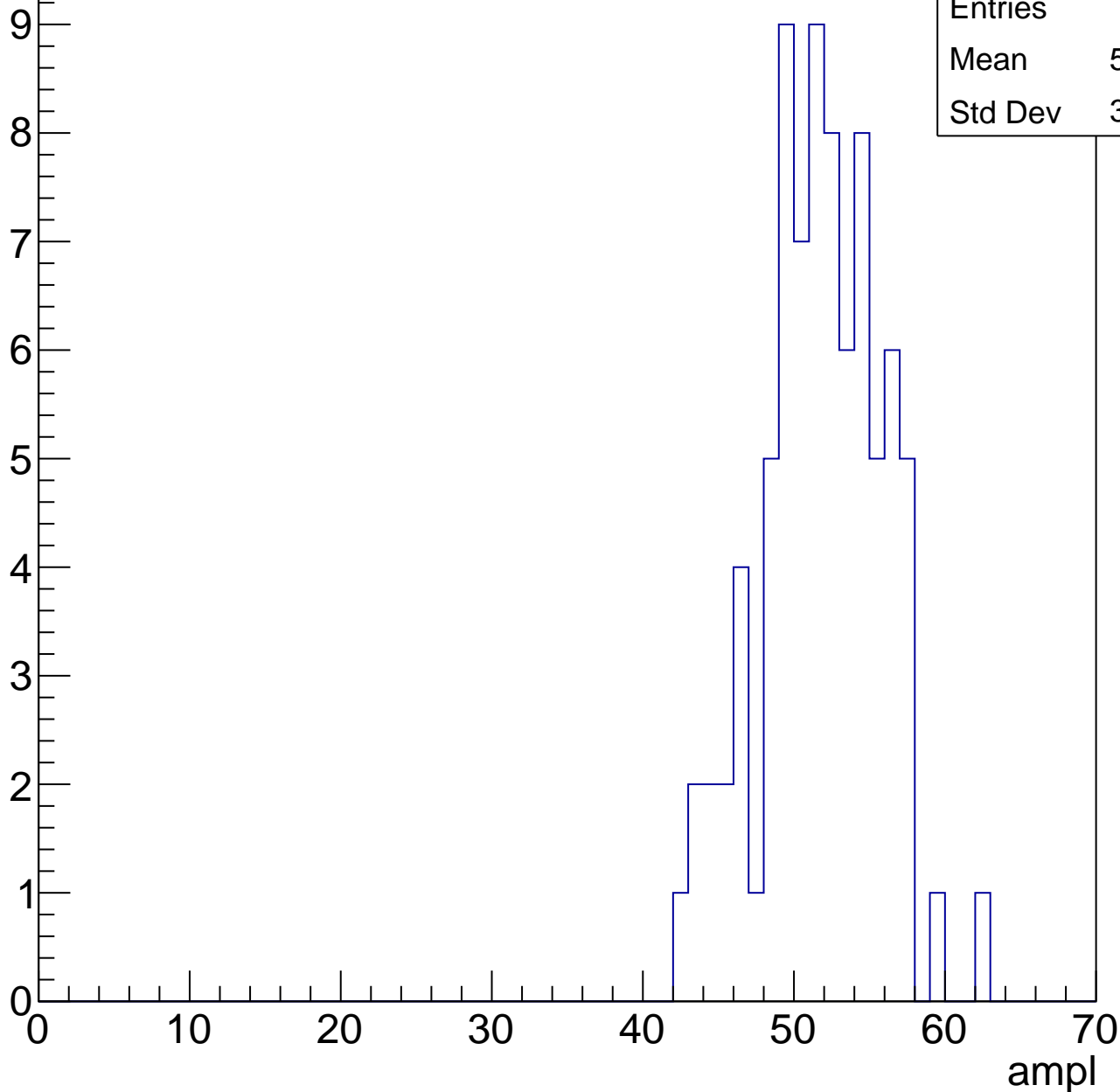


B0L000S, U4-ch35, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	51.34
Std Dev	3.939

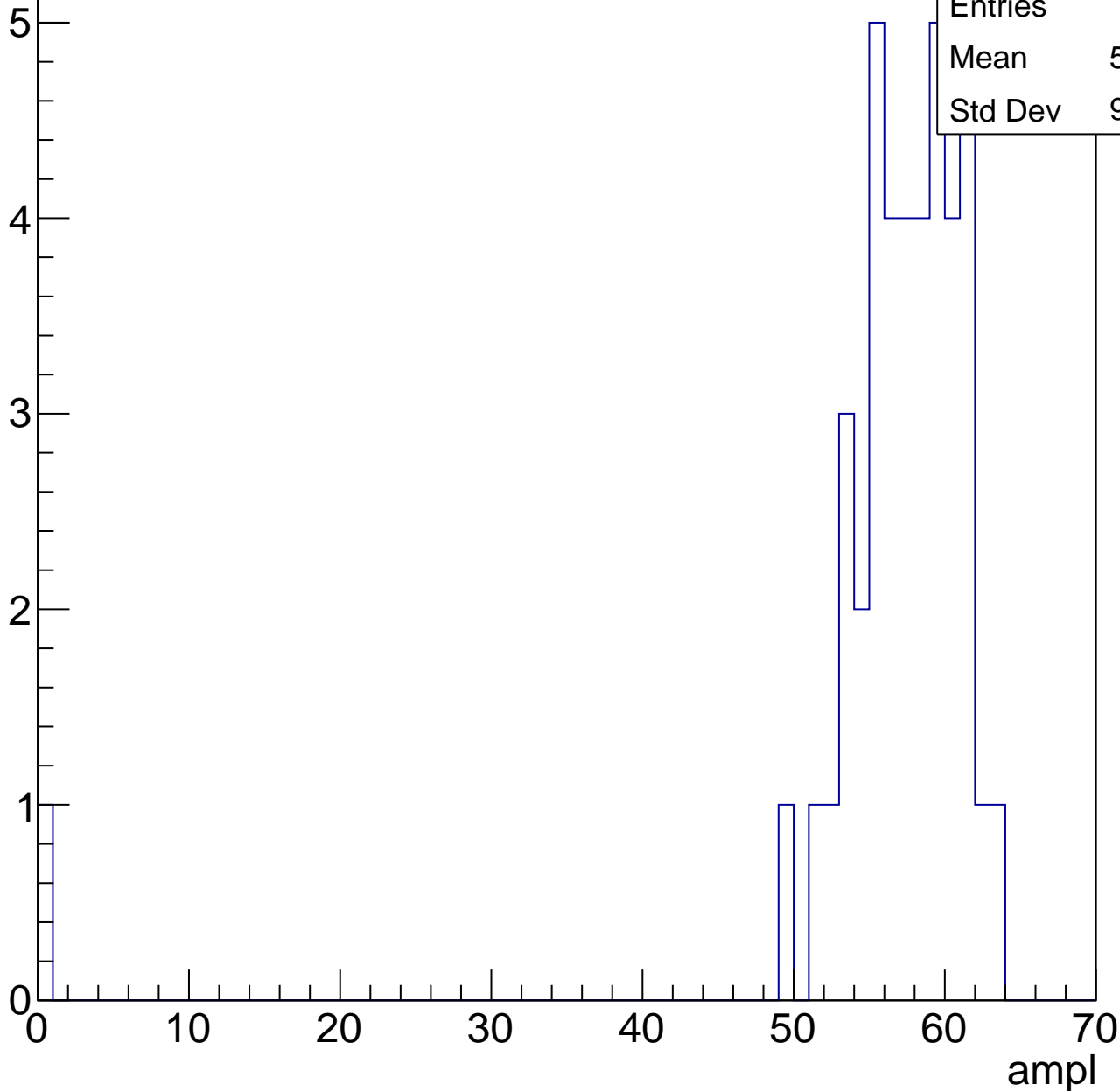


B0L000S, U4-ch35, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	42
Mean	55.79
Std Dev	9.262

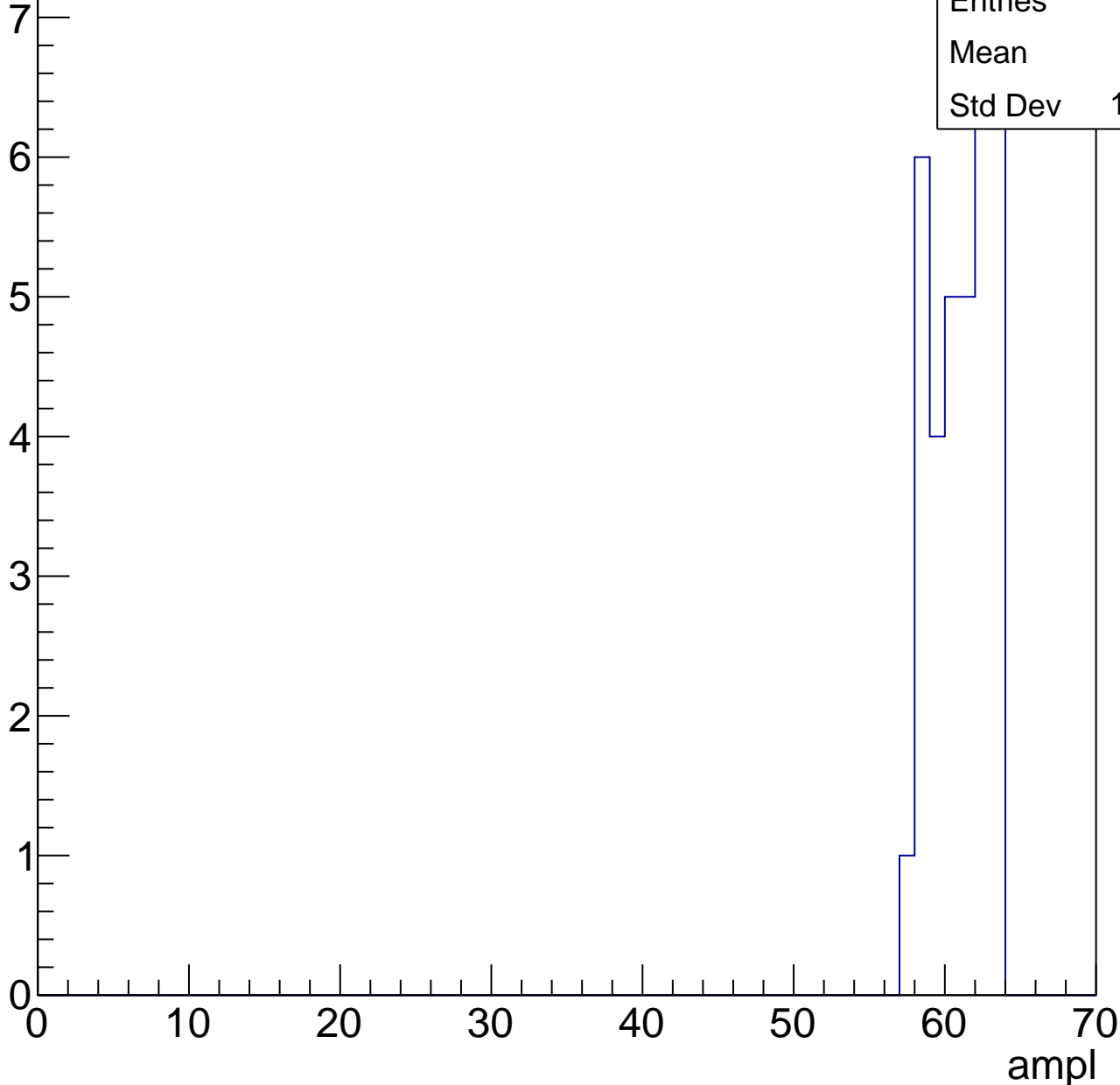


B0L000S, U4-ch35, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

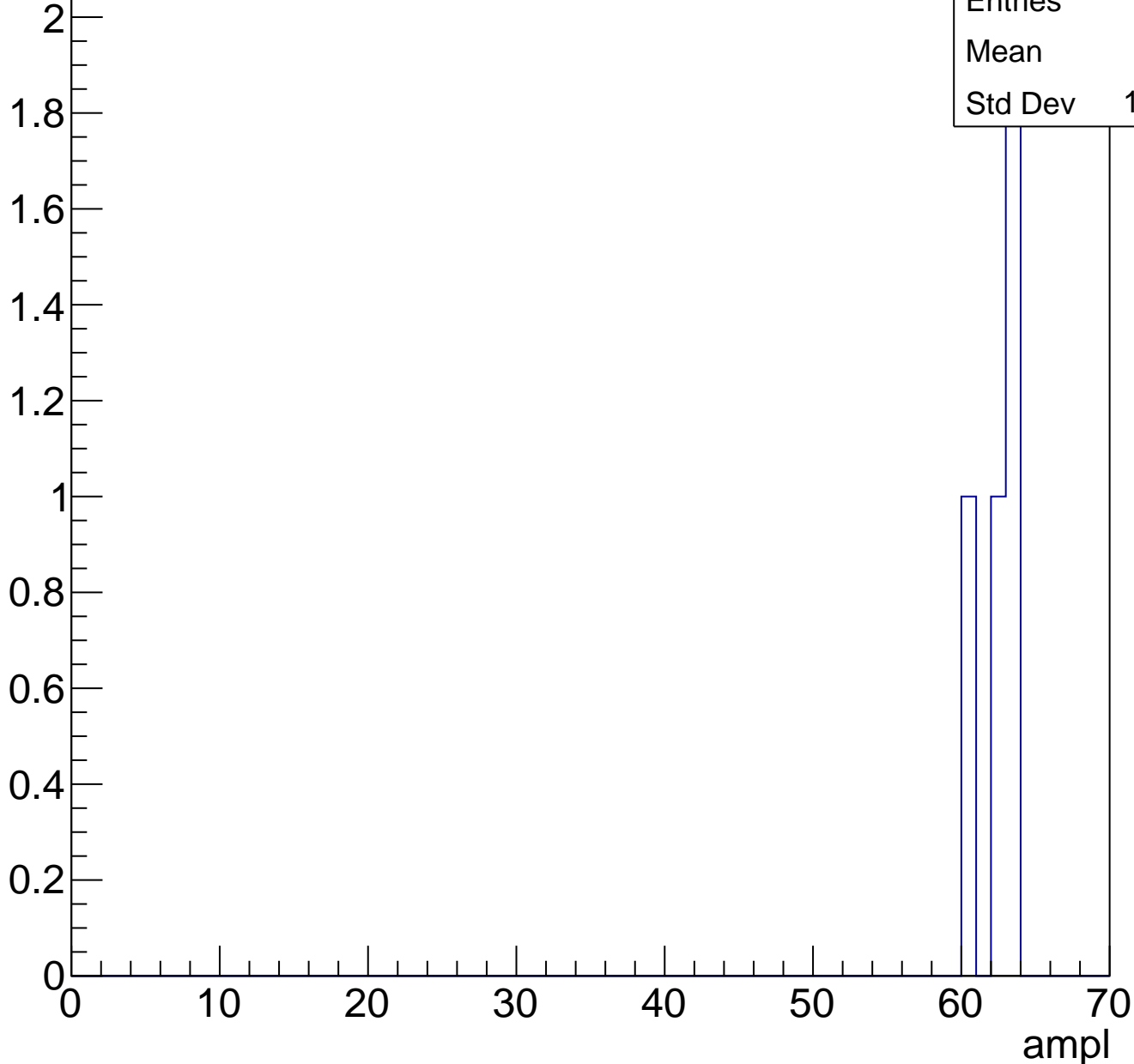
Entries	35
Mean	60.6
Std Dev	1.855



B0L000S, U4-ch35, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch35, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch36, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	30.59
Std Dev	4.901

Gaus mean : 31.5225

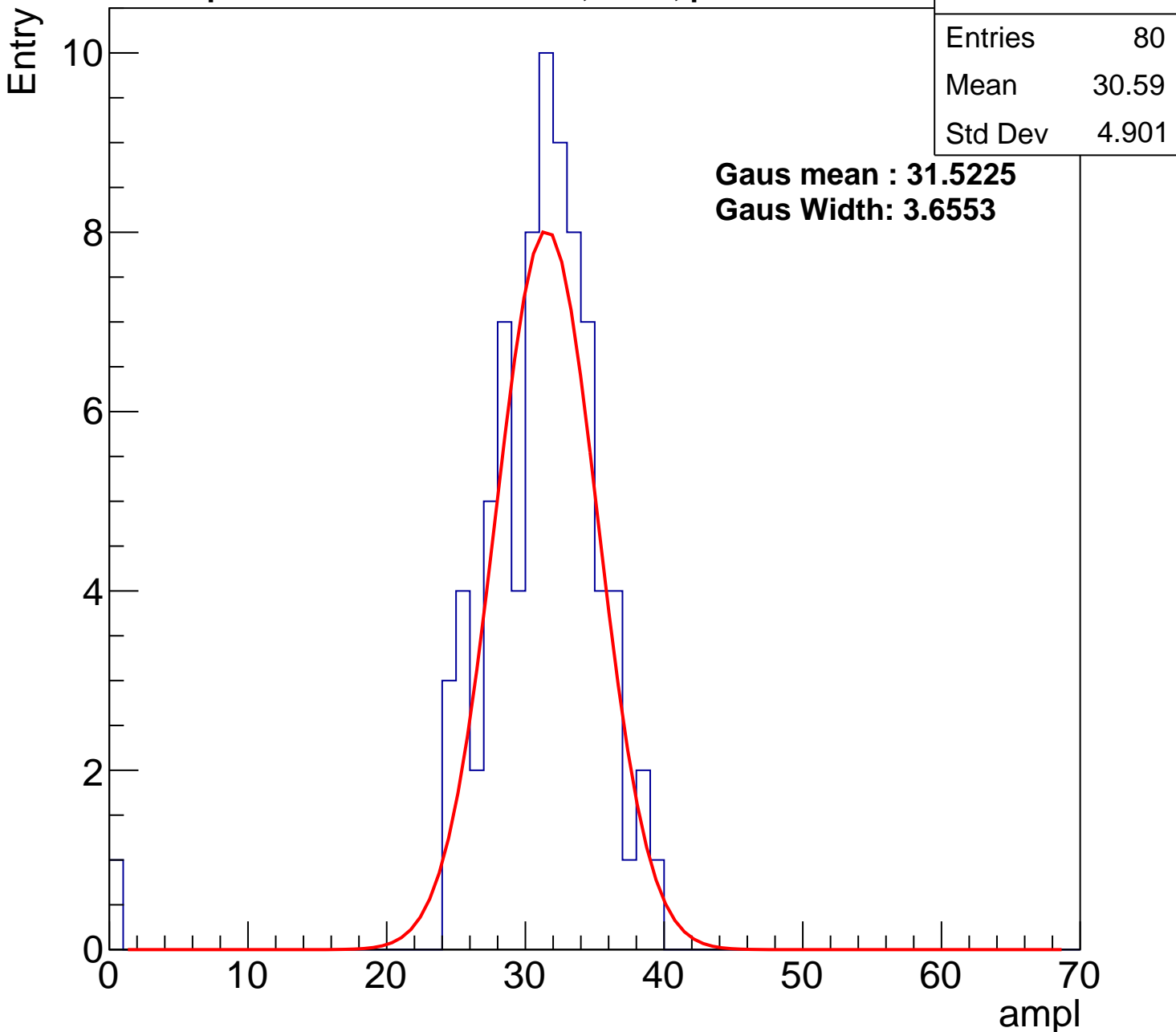
Gaus Width: 3.6553

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch36, adc1

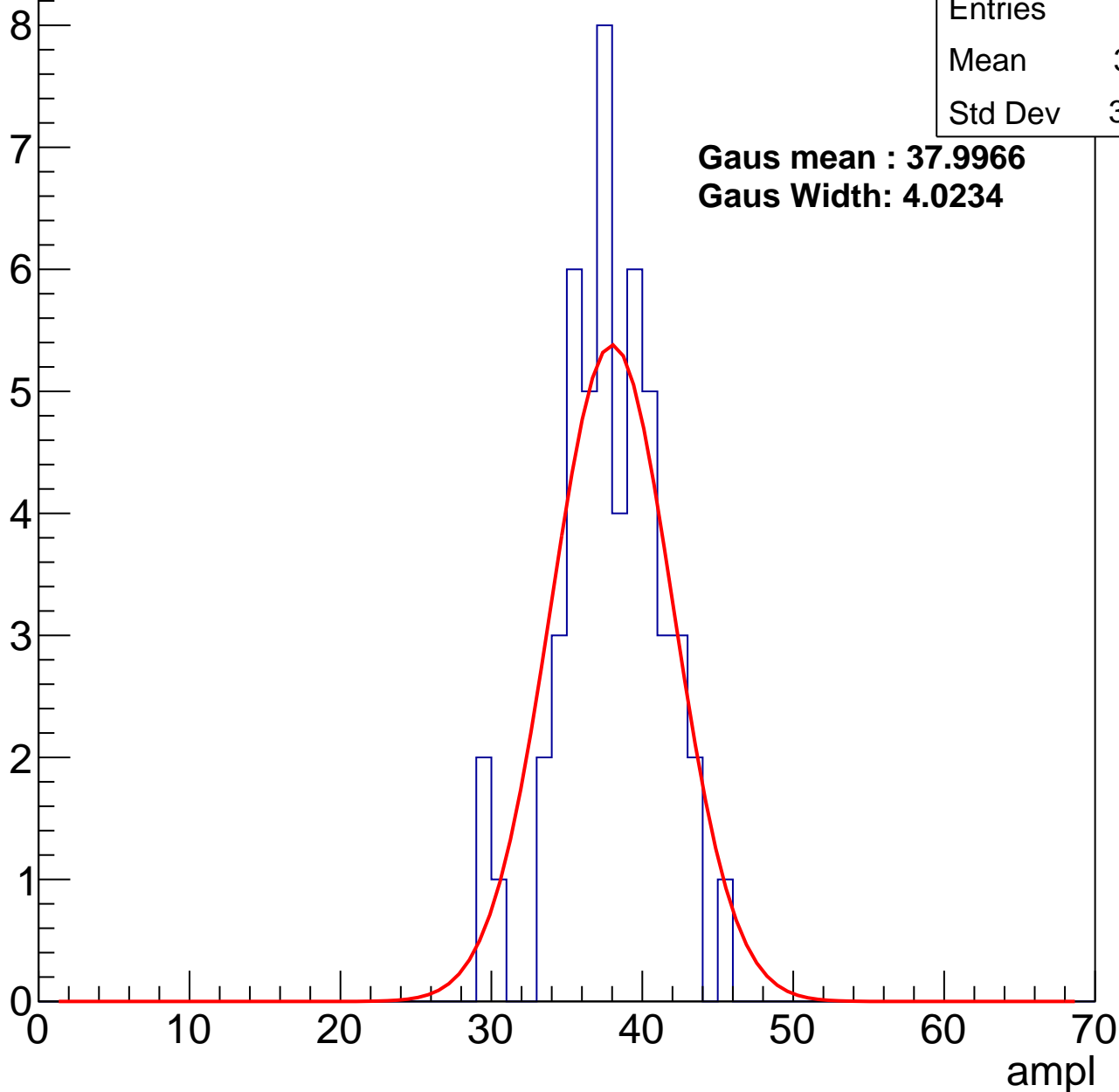
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	37.41
Std Dev	3.397

Gaus mean : 37.9966

Gaus Width: 4.0234



B0L000S, U4-ch36, adc2

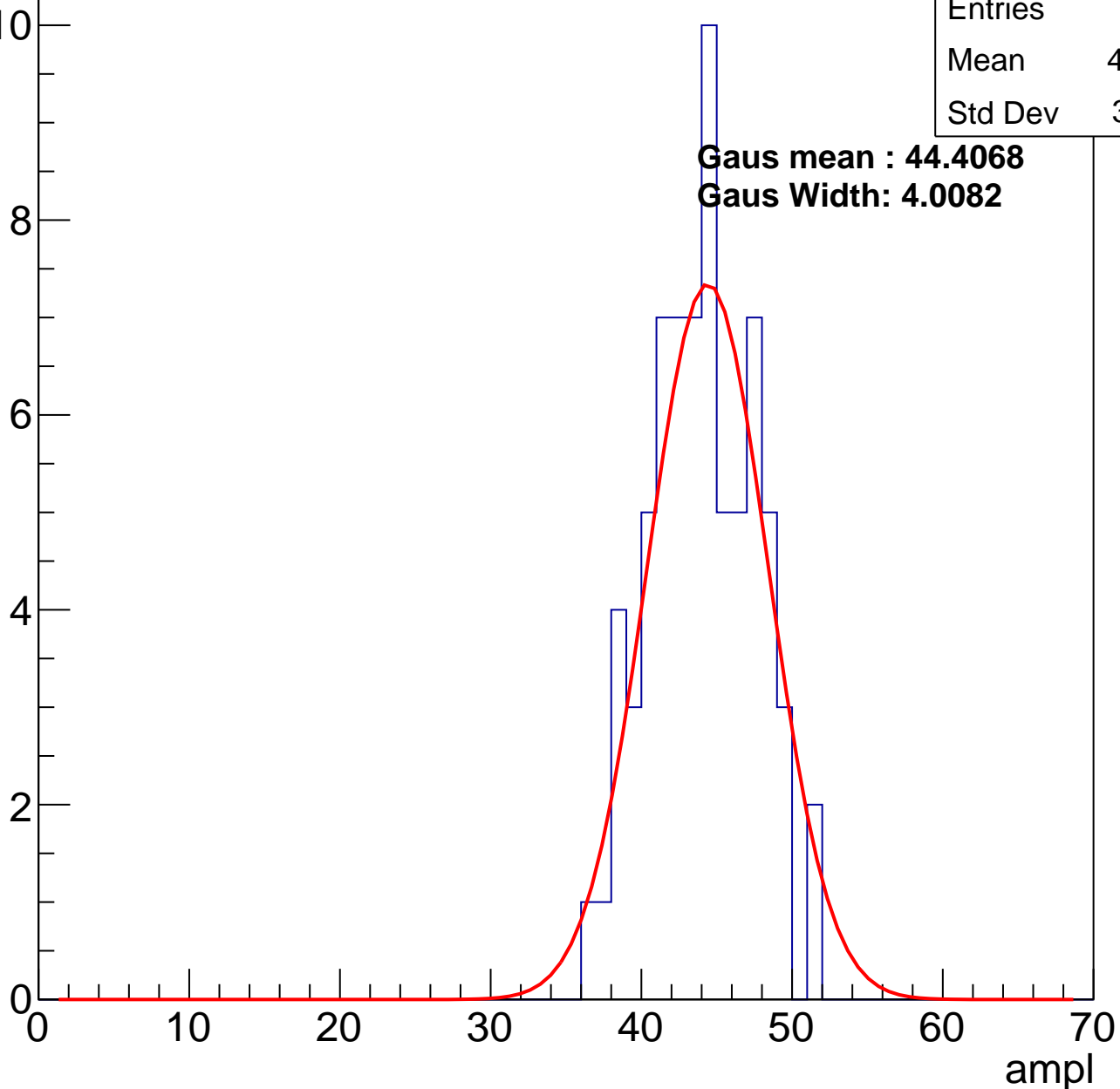
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	43.57
Std Dev	3.411

Gaus mean : 44.4068

Gaus Width: 4.0082

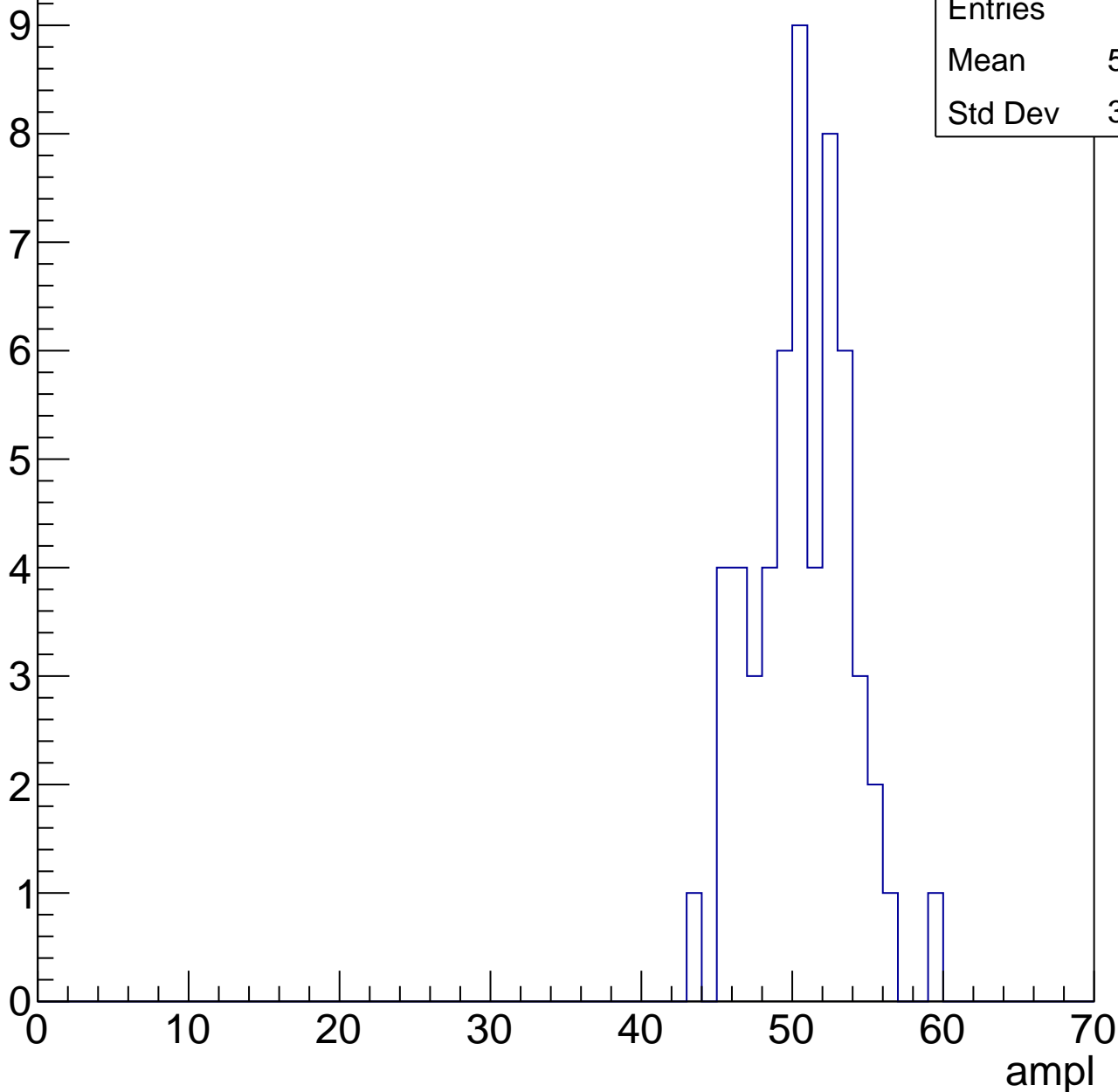


B0L000S, U4-ch36, adc3

calib_packv5_042523_0143.root, FC#5, port B1

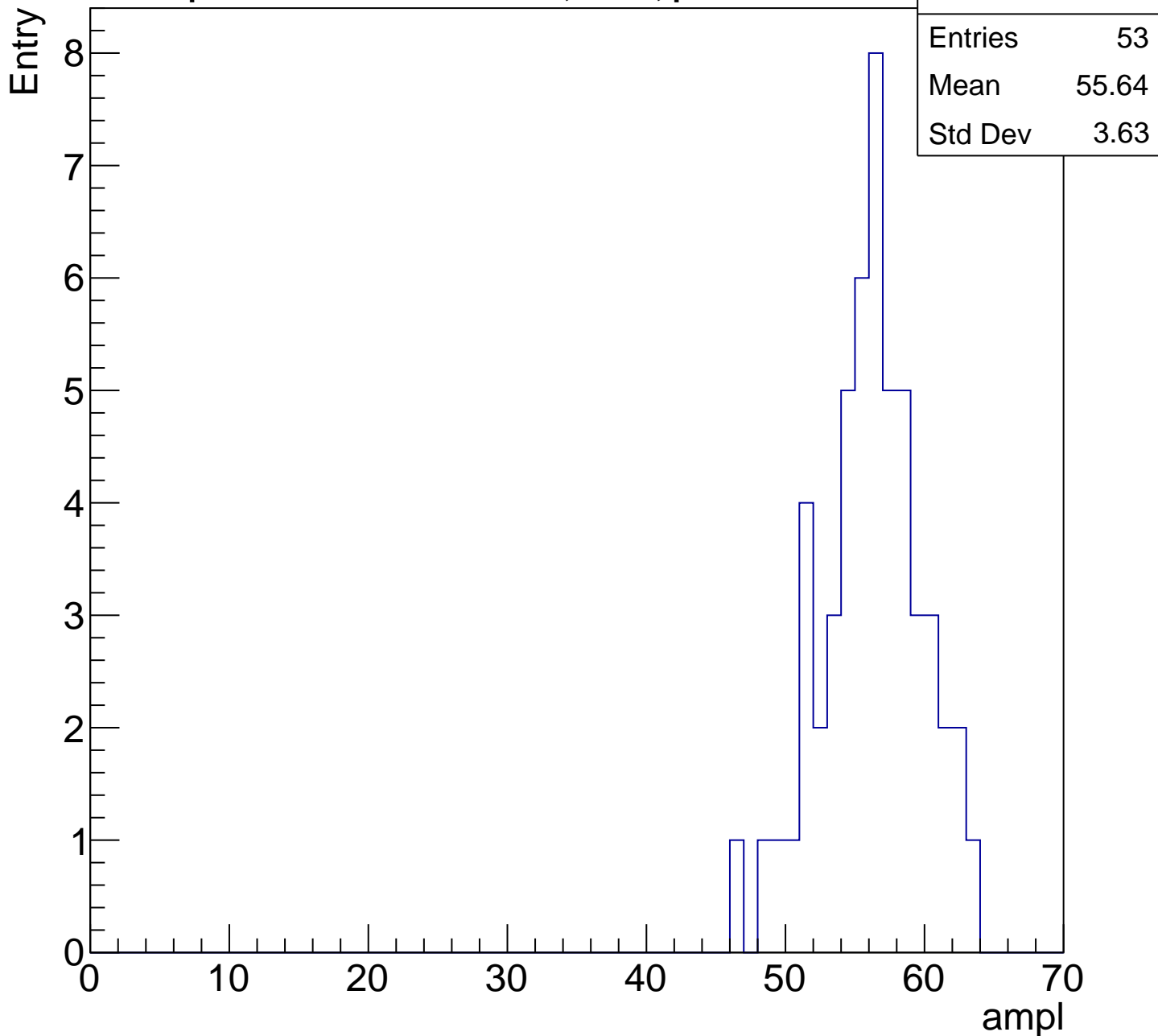
Entry

Entries	56
Mean	50.16
Std Dev	3.178



B0L000S, U4-ch36, adc4

calib_packv5_042523_0143.root, FC#5, port B1

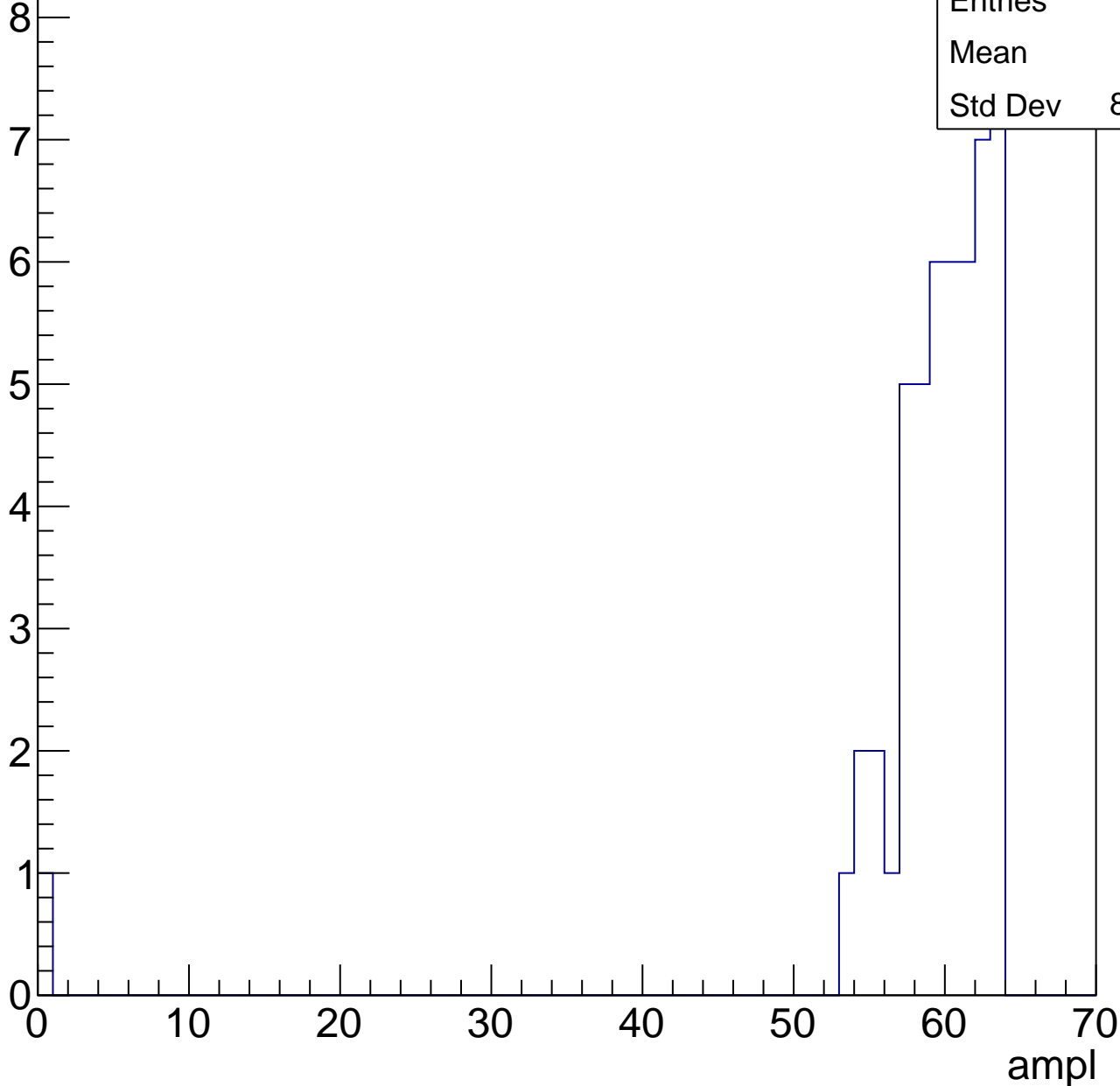


B0L000S, U4-ch36, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

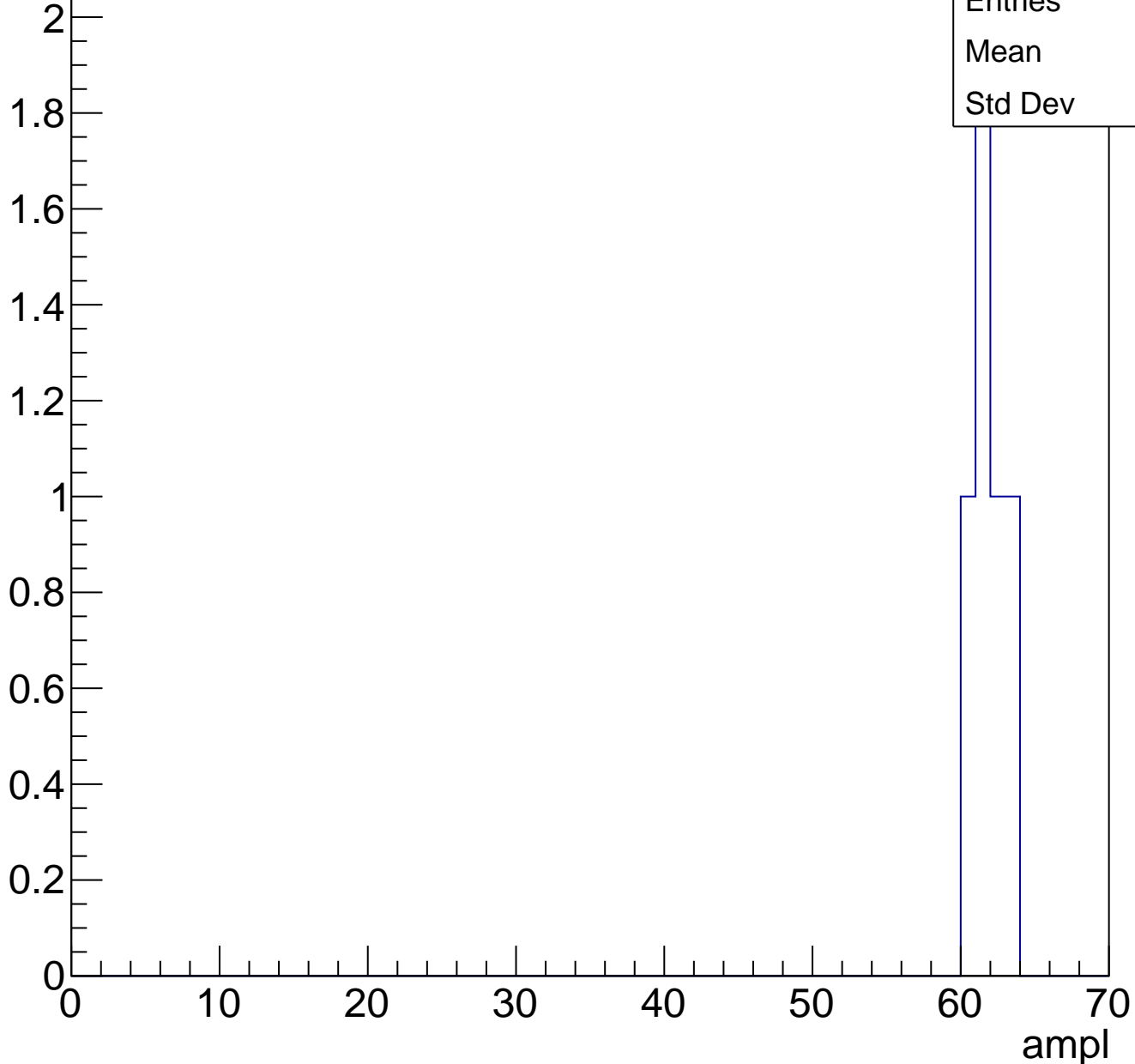
Entries	50
Mean	58.4
Std Dev	8.759



B0L000S, U4-ch36, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	5
Mean	61.4
Std Dev	1.02

B0L000S, U4-ch36, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

B0L000S, U4-ch37, adc0

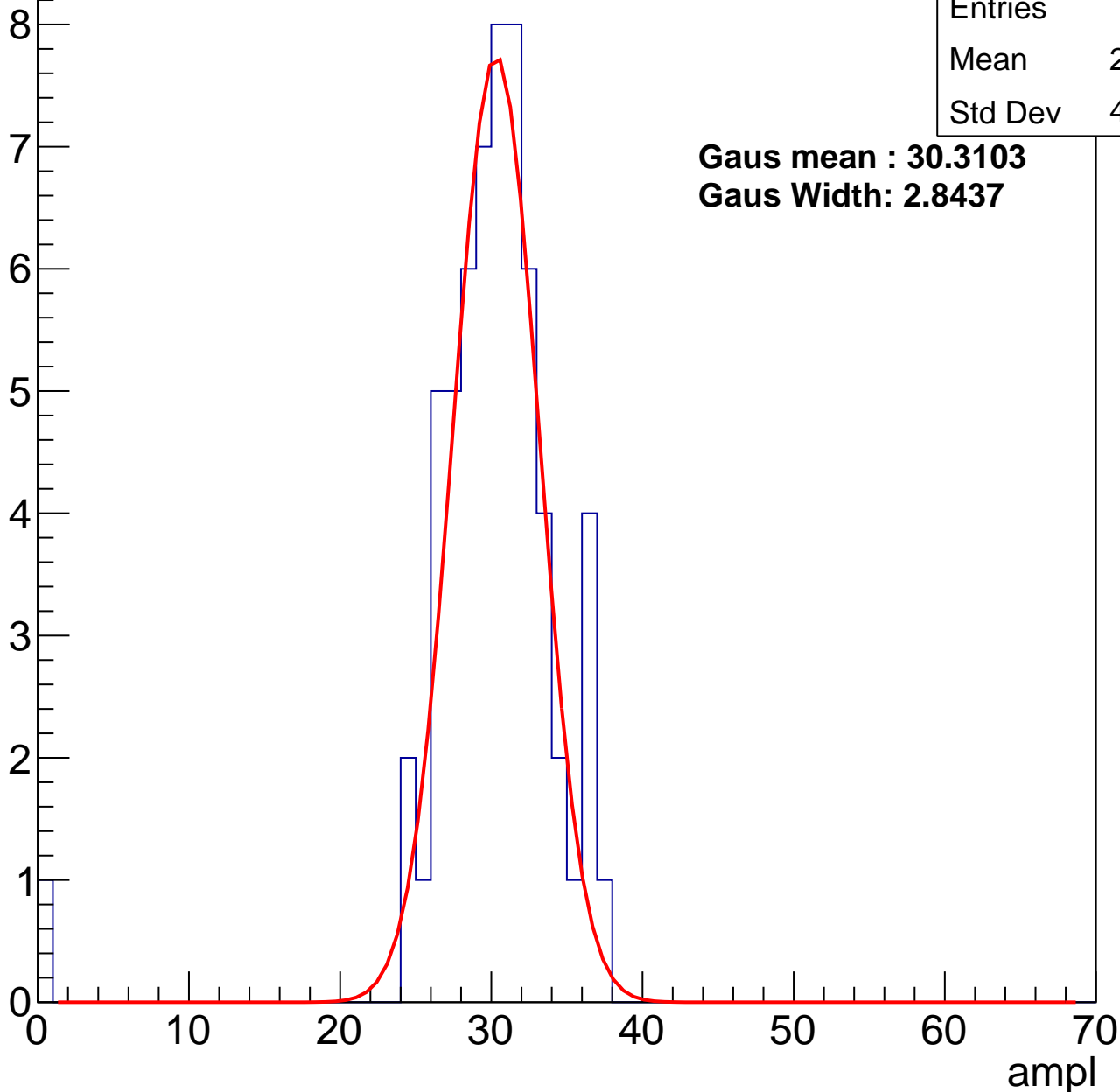
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.59
Std Dev	4.894

Gaus mean : 30.3103

Gaus Width: 2.8437



B0L000S, U4-ch37, adc1

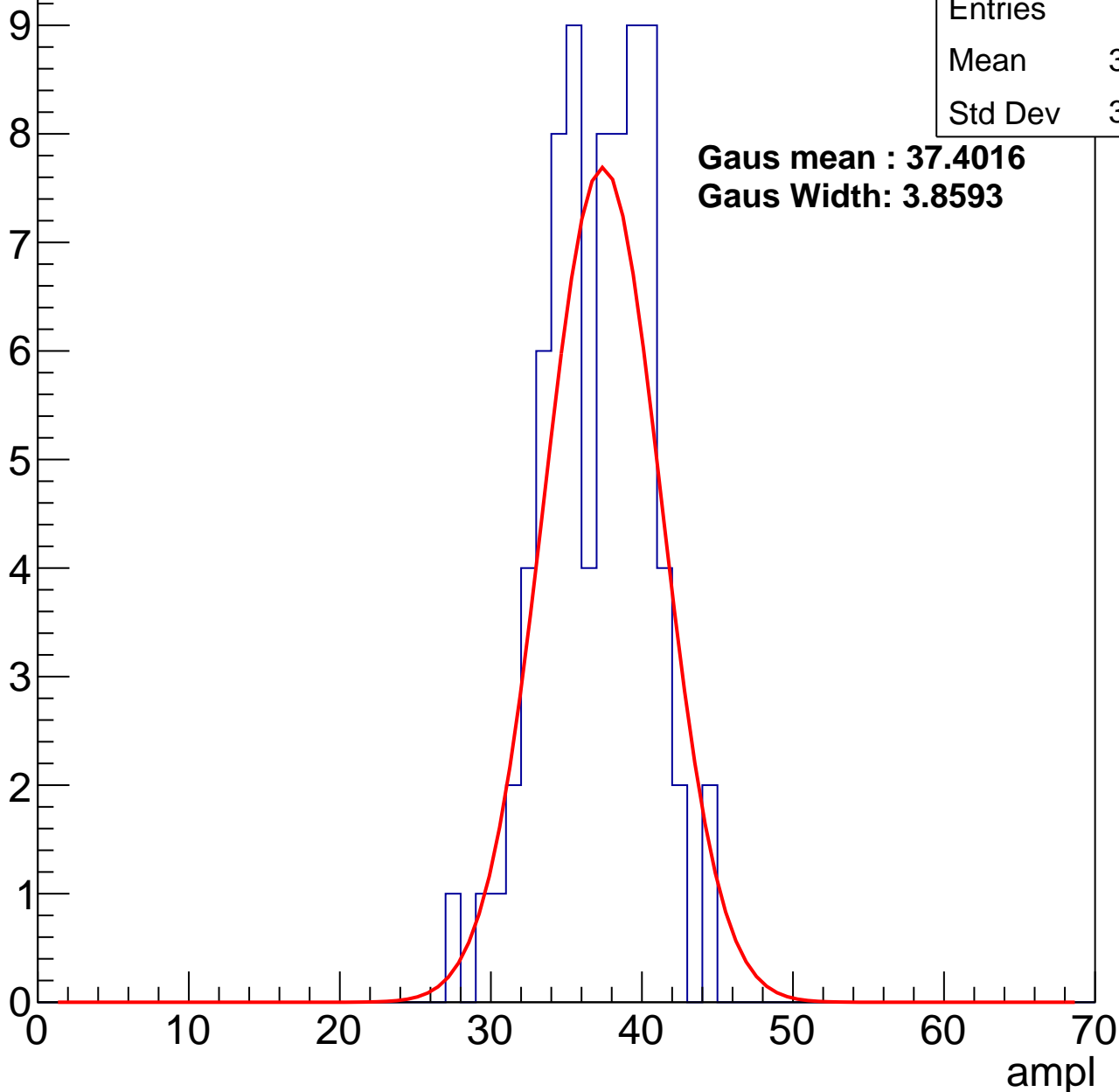
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	36.56
Std Dev	3.425

Gaus mean : 37.4016

Gaus Width: 3.8593



B0L000S, U4-ch37, adc2

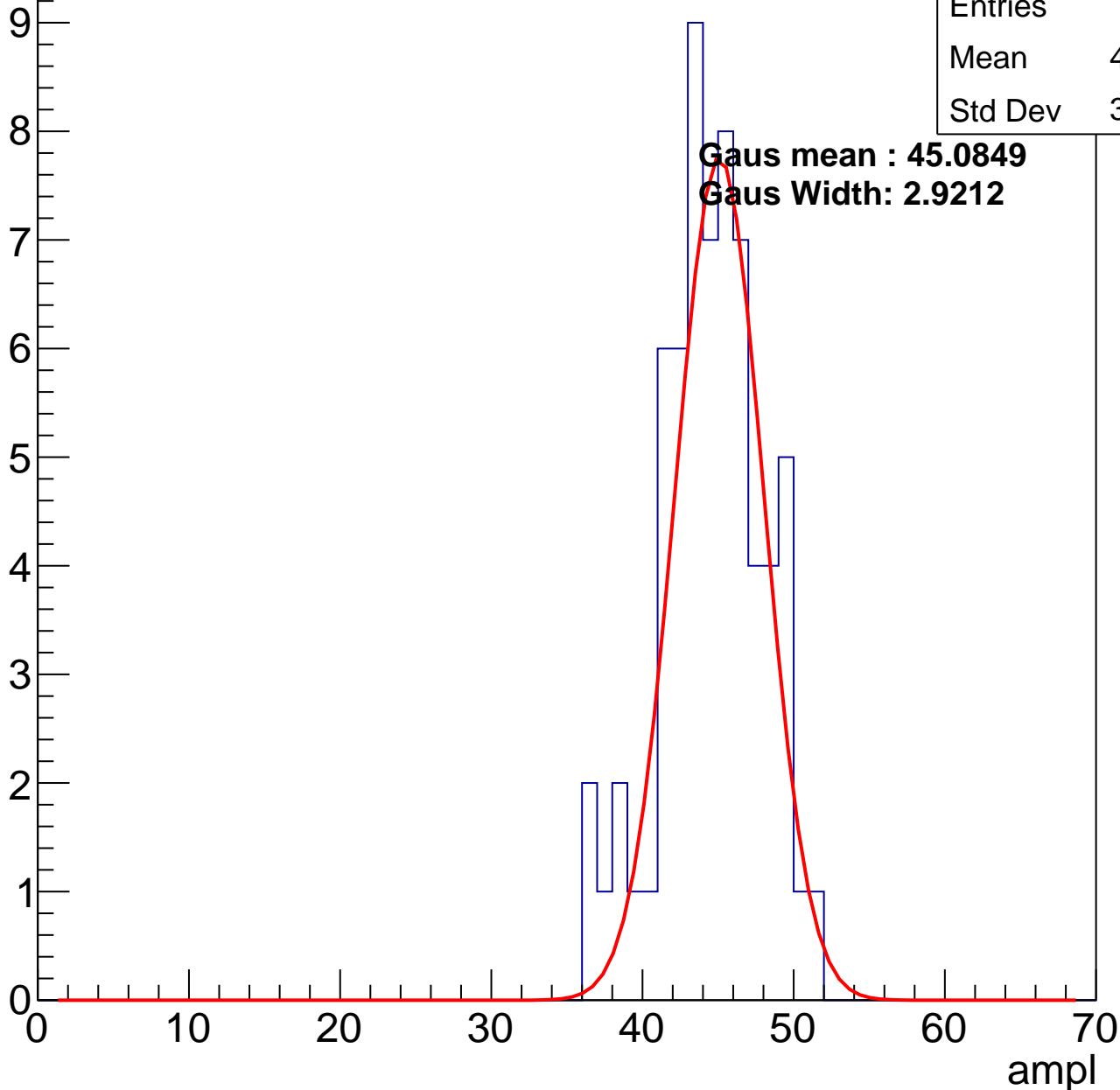
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.08
Std Dev	3.334

Gaus mean : 45.0849

Gaus Width: 2.9212

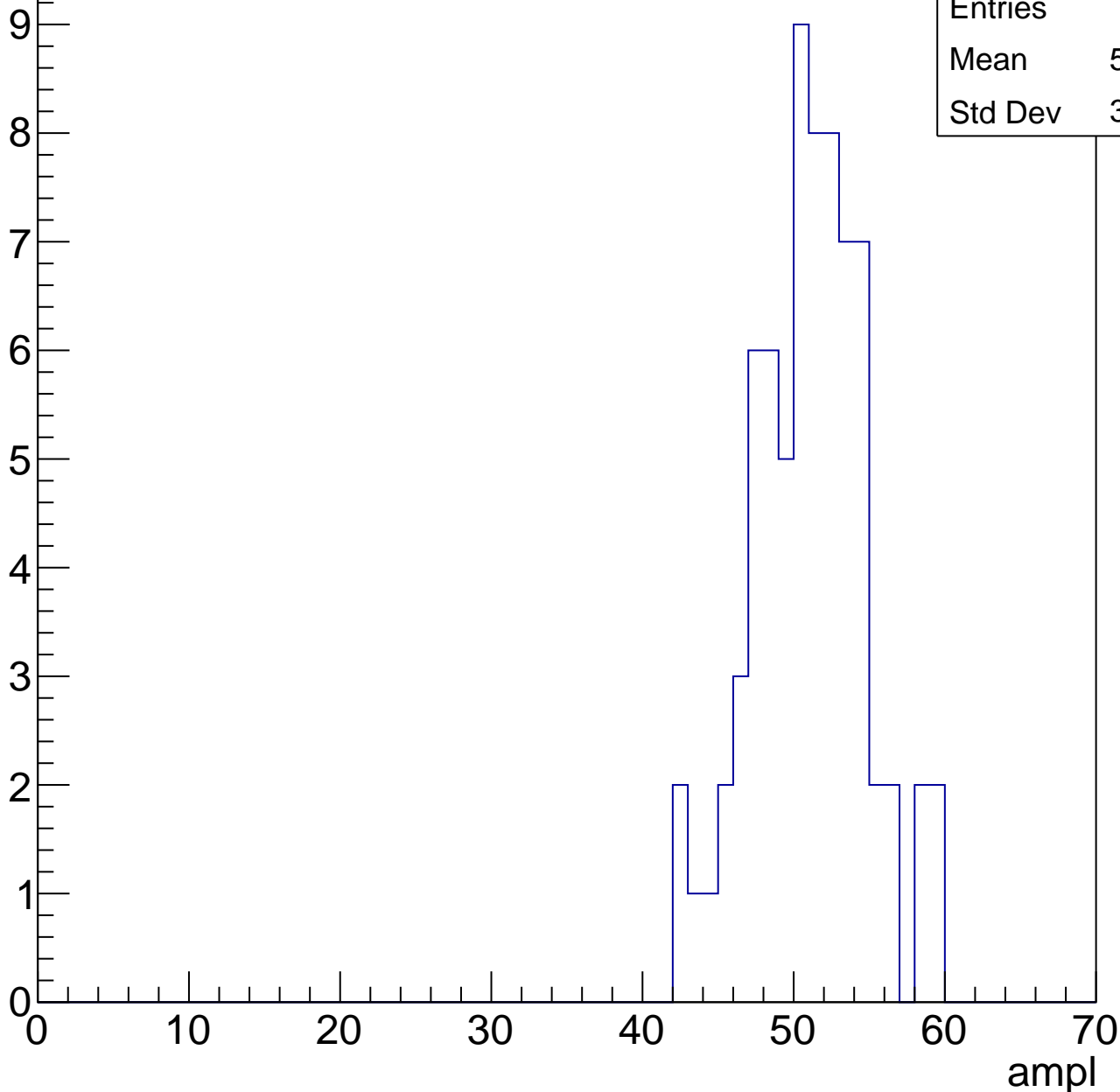


B0L000S, U4-ch37, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	50.59
Std Dev	3.693

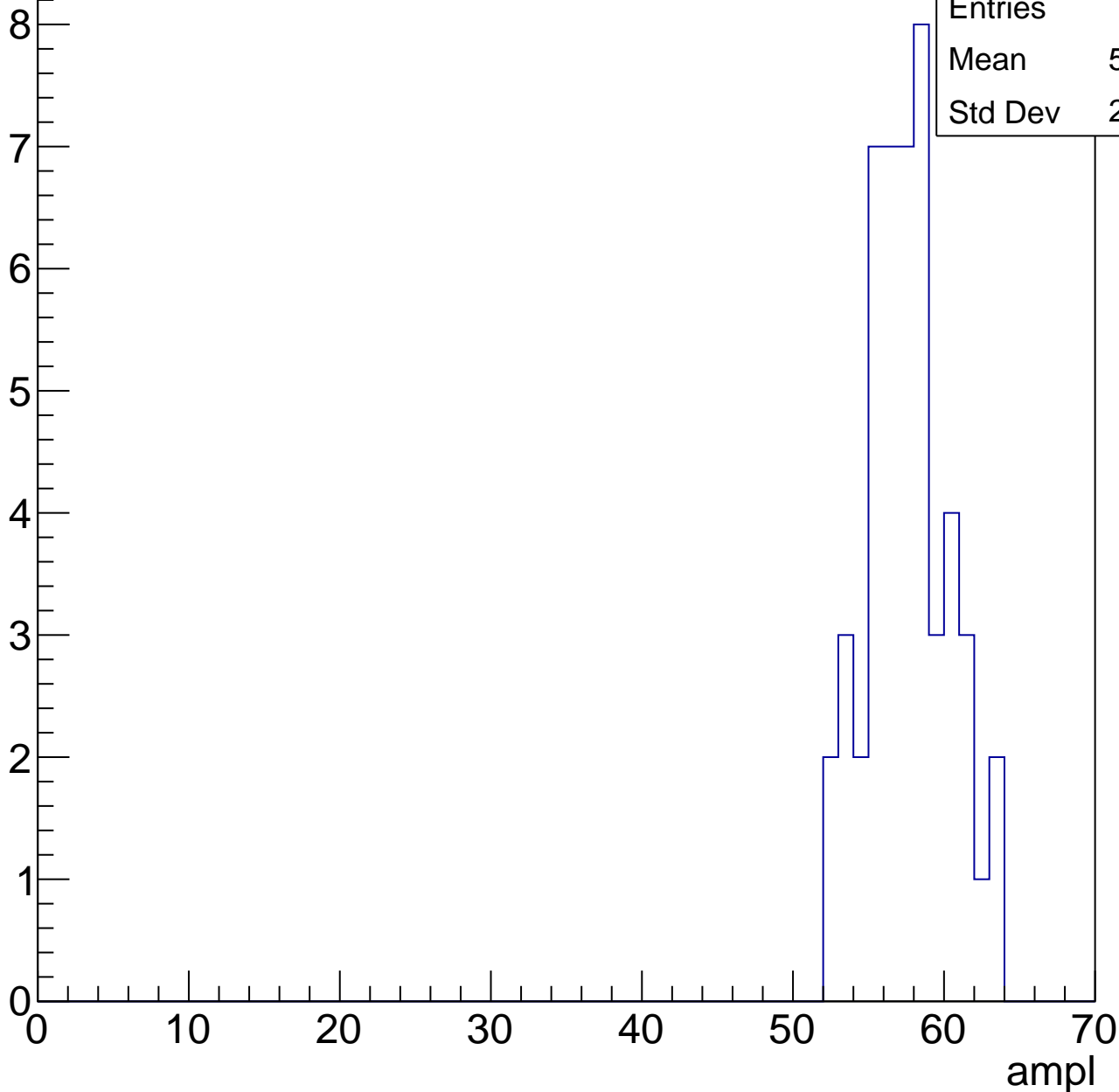


B0L000S, U4-ch37, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	57.12
Std Dev	2.677

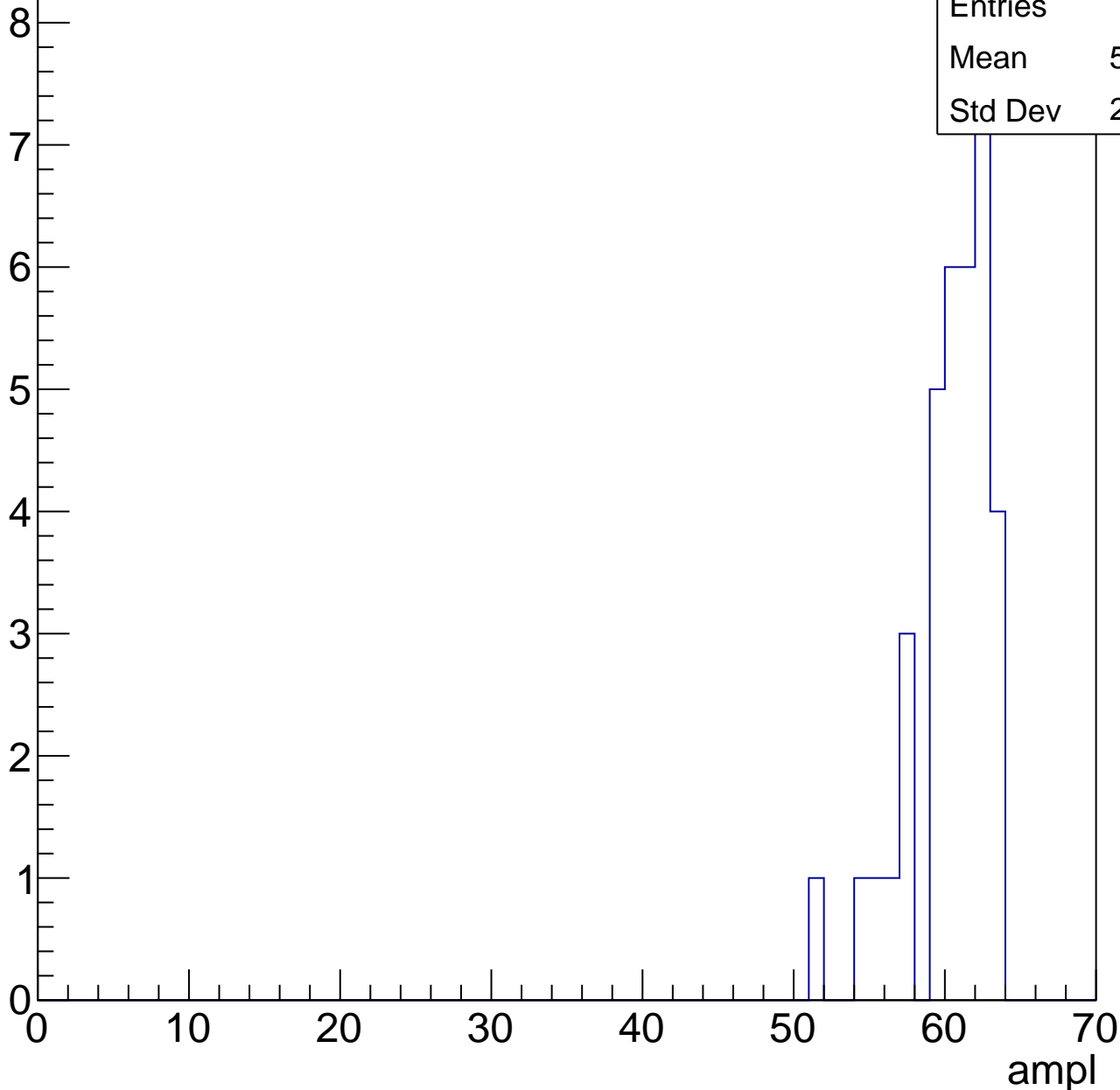


B0L000S, U4-ch37, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

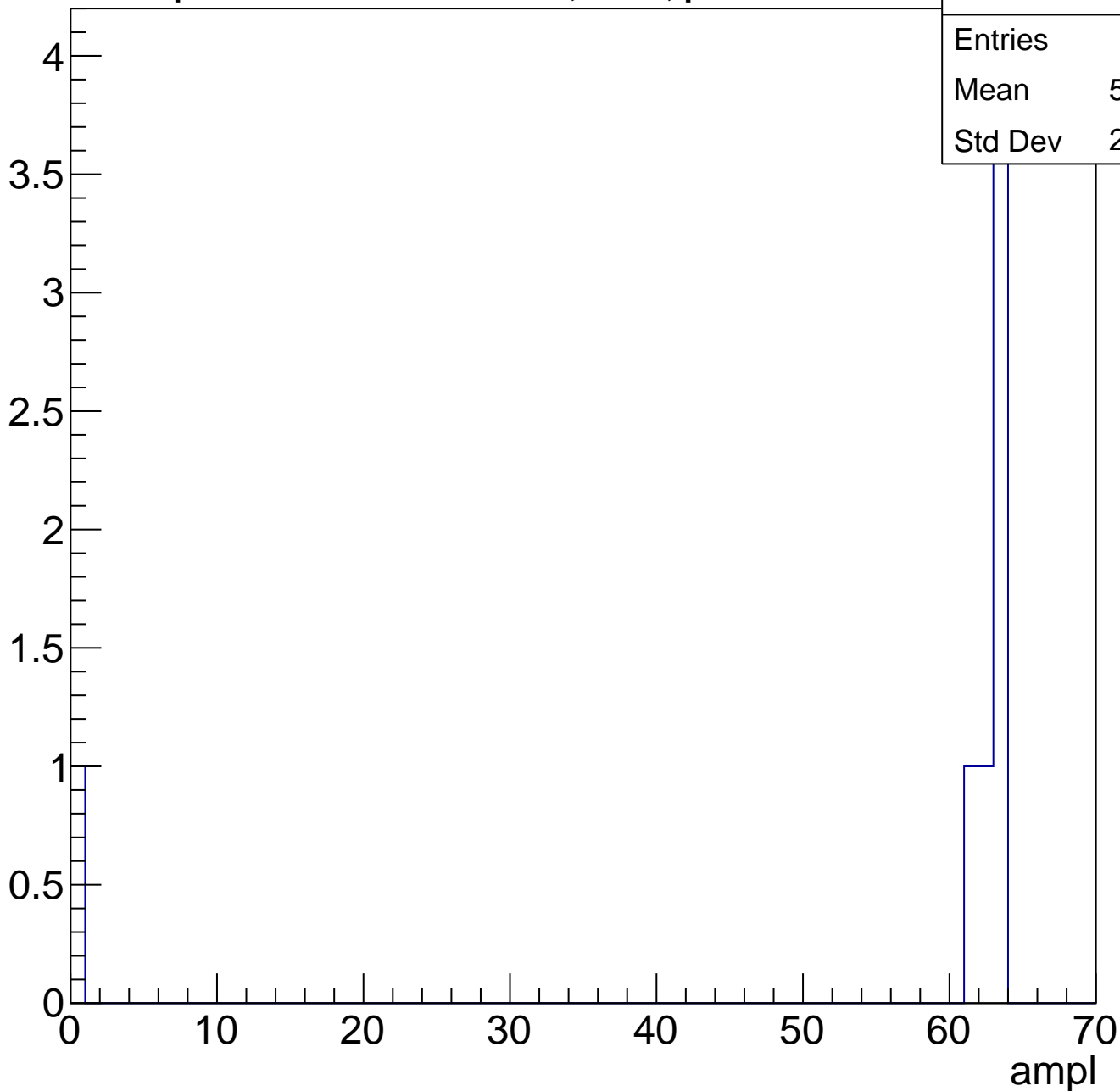
Entries	36
Mean	59.89
Std Dev	2.706



B0L000S, U4-ch37, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch37, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L000S, U4-ch38, adc0

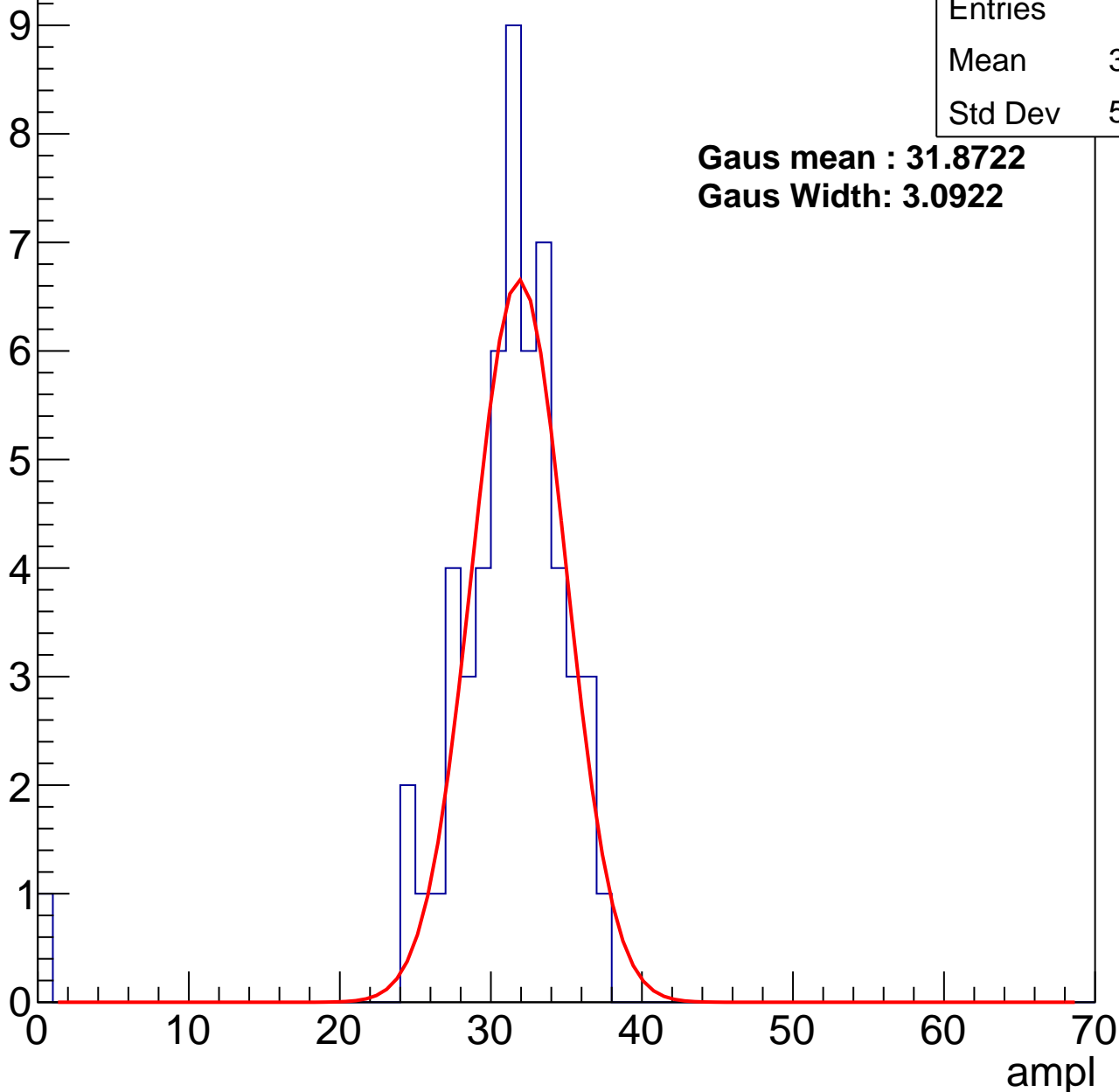
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	30.45
Std Dev	5.127

Gaus mean : 31.8722

Gaus Width: 3.0922



B0L000S, U4-ch38, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	84
Mean	38.04
Std Dev	3.604

Gaus mean : 38.6438

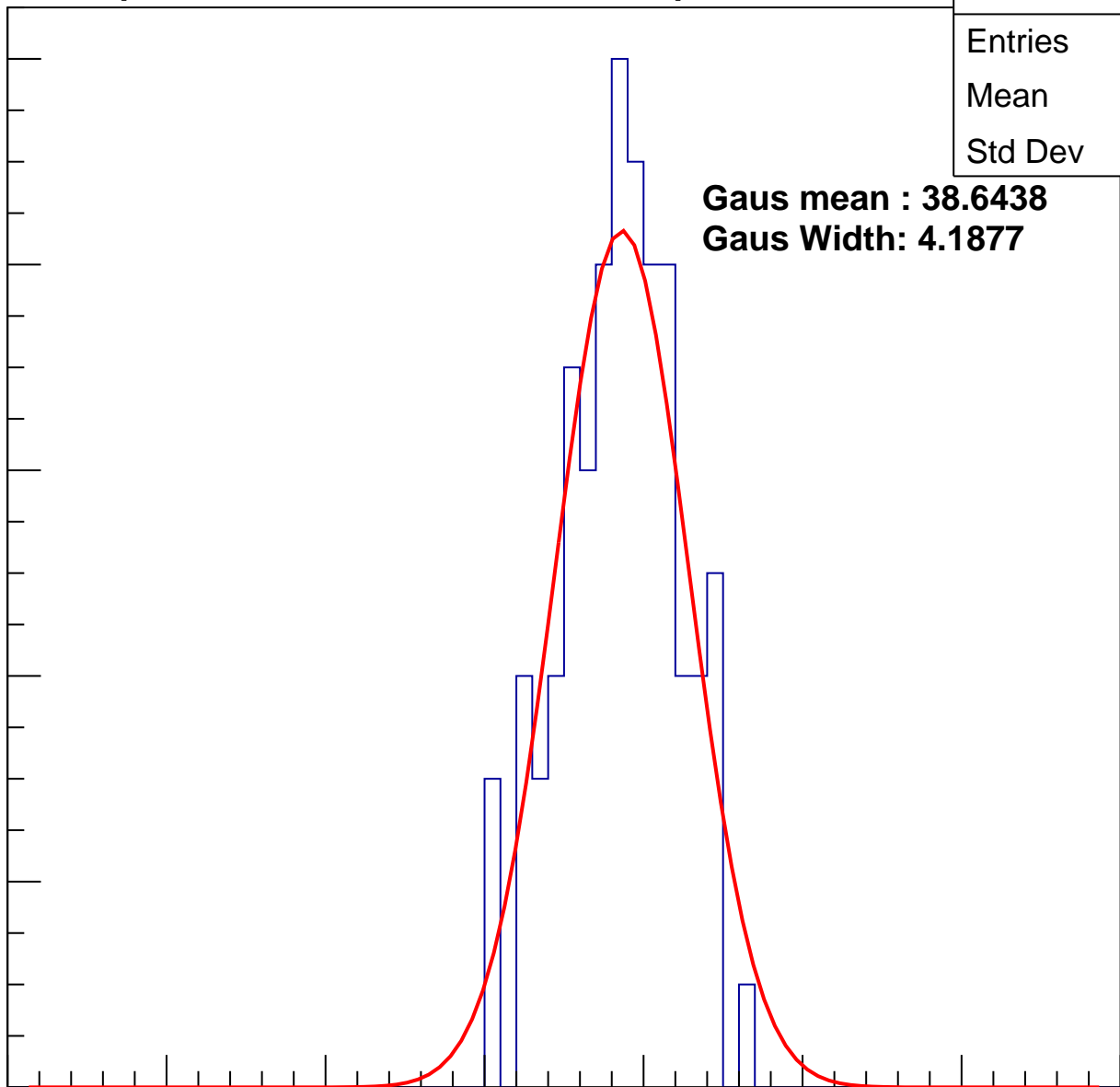
Gaus Width: 4.1877

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch38, adc2

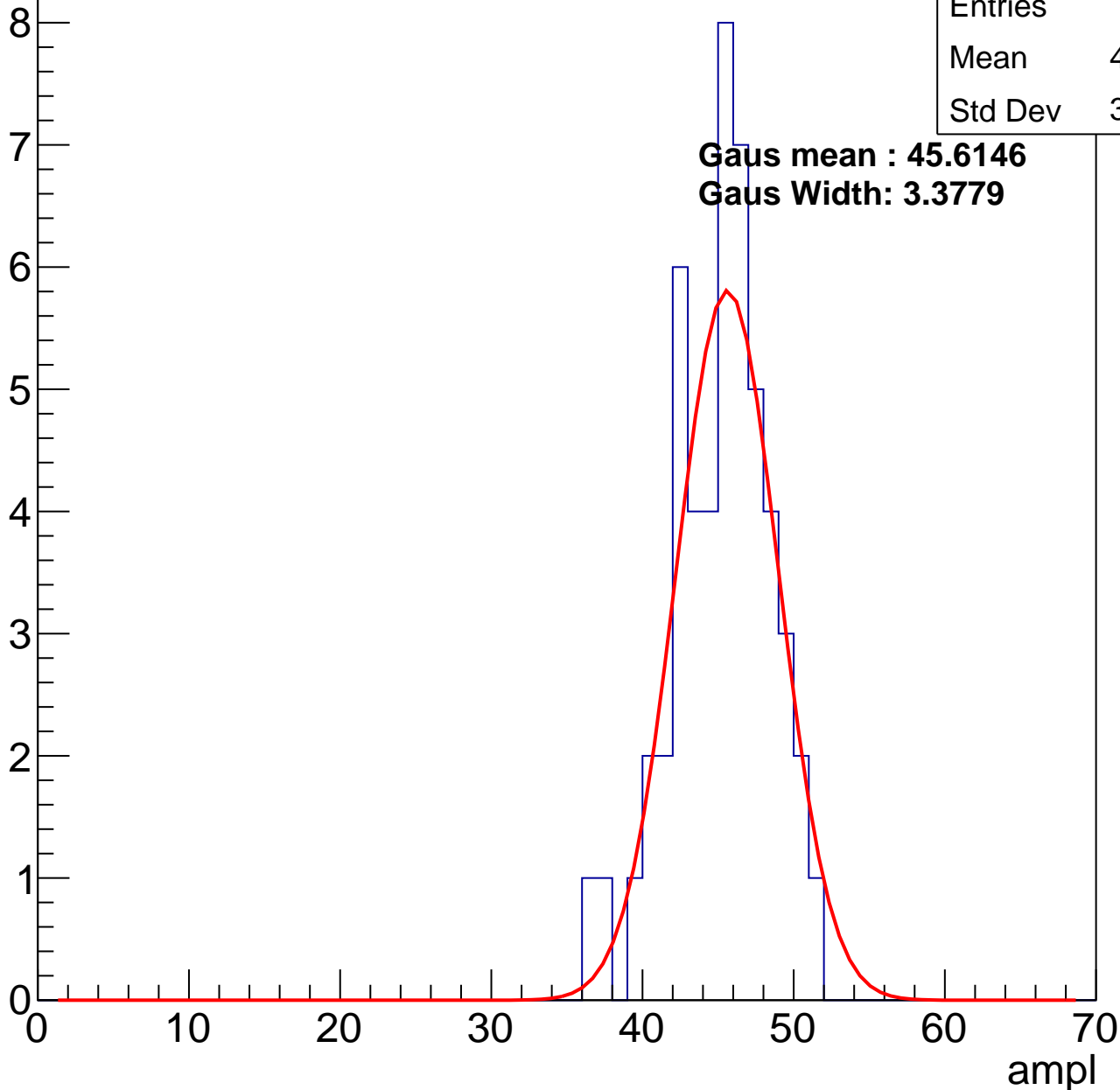
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	44.73
Std Dev	3.224

Gaus mean : 45.6146

Gaus Width: 3.3779

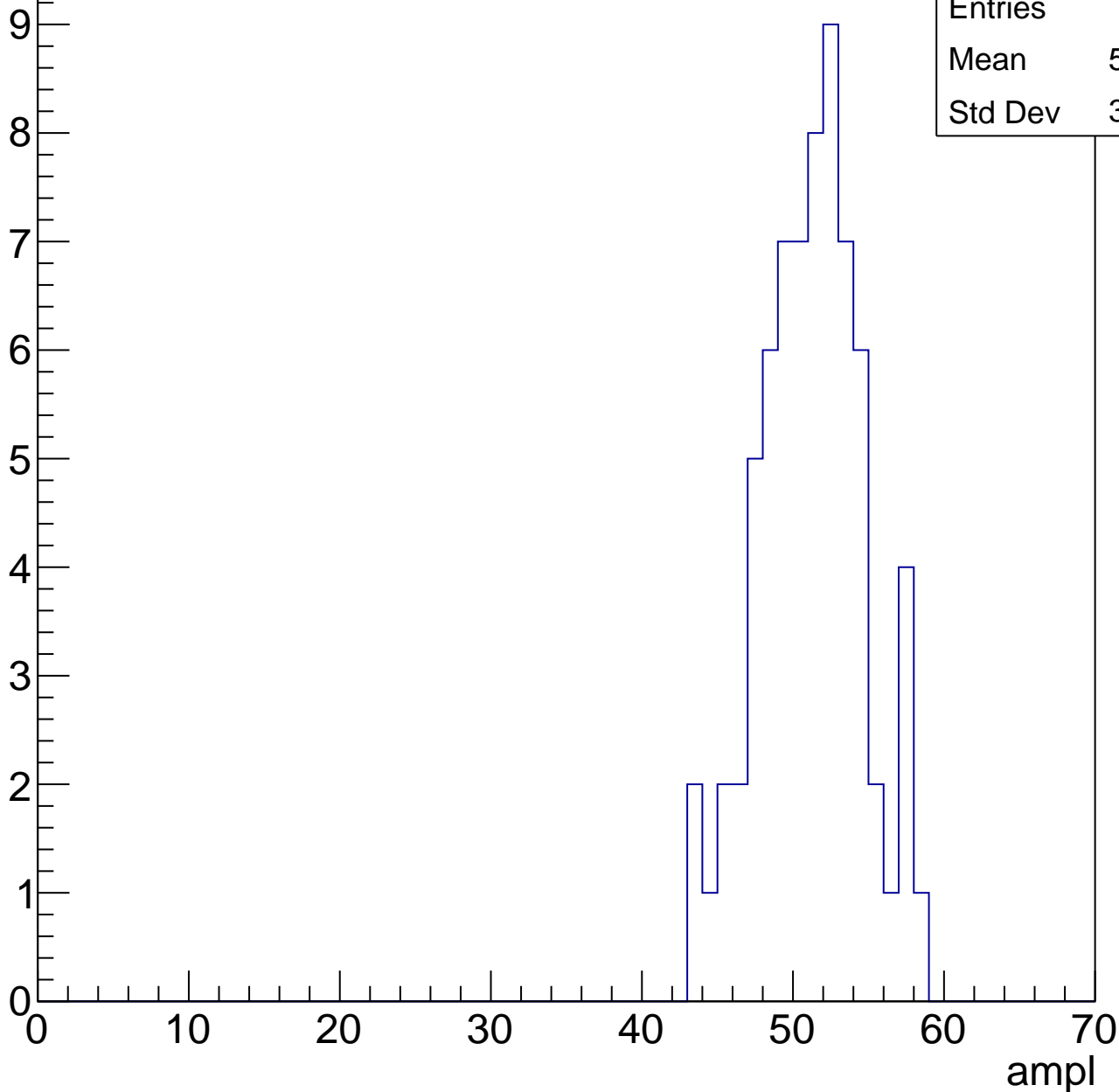


B0L000S, U4-ch38, adc3

calib_packv5_042523_0143.root, FC#5, port B1

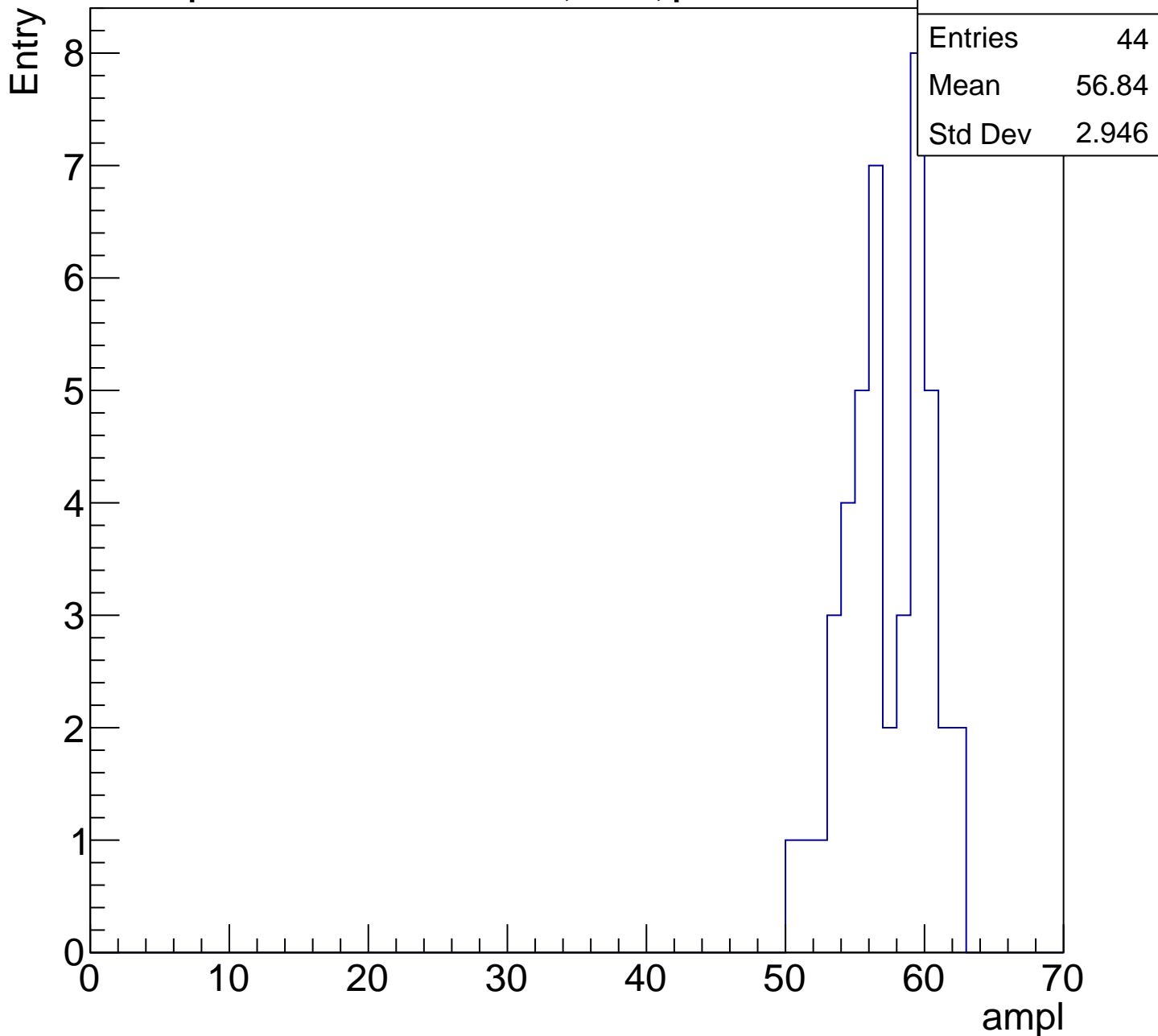
Entry

Entries	70
Mean	50.73
Std Dev	3.389



B0L000S, U4-ch38, adc4

calib_packv5_042523_0143.root, FC#5, port B1

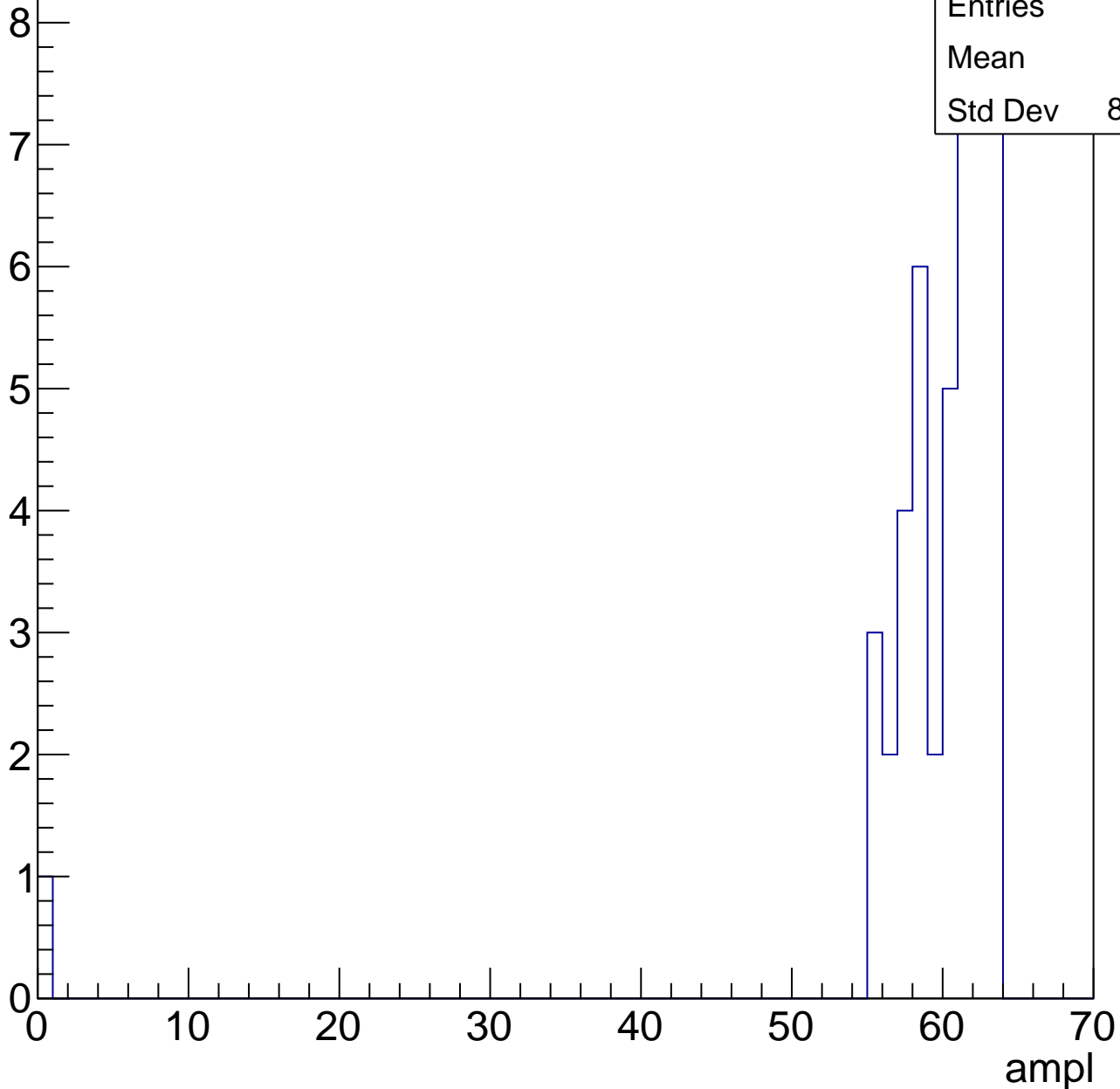


B0L000S, U4-ch38, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	58.7
Std Dev	8.994



B0L000S, U4-ch38, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	63
Std Dev	0

B0L000S, U4-ch38, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch39, adc0

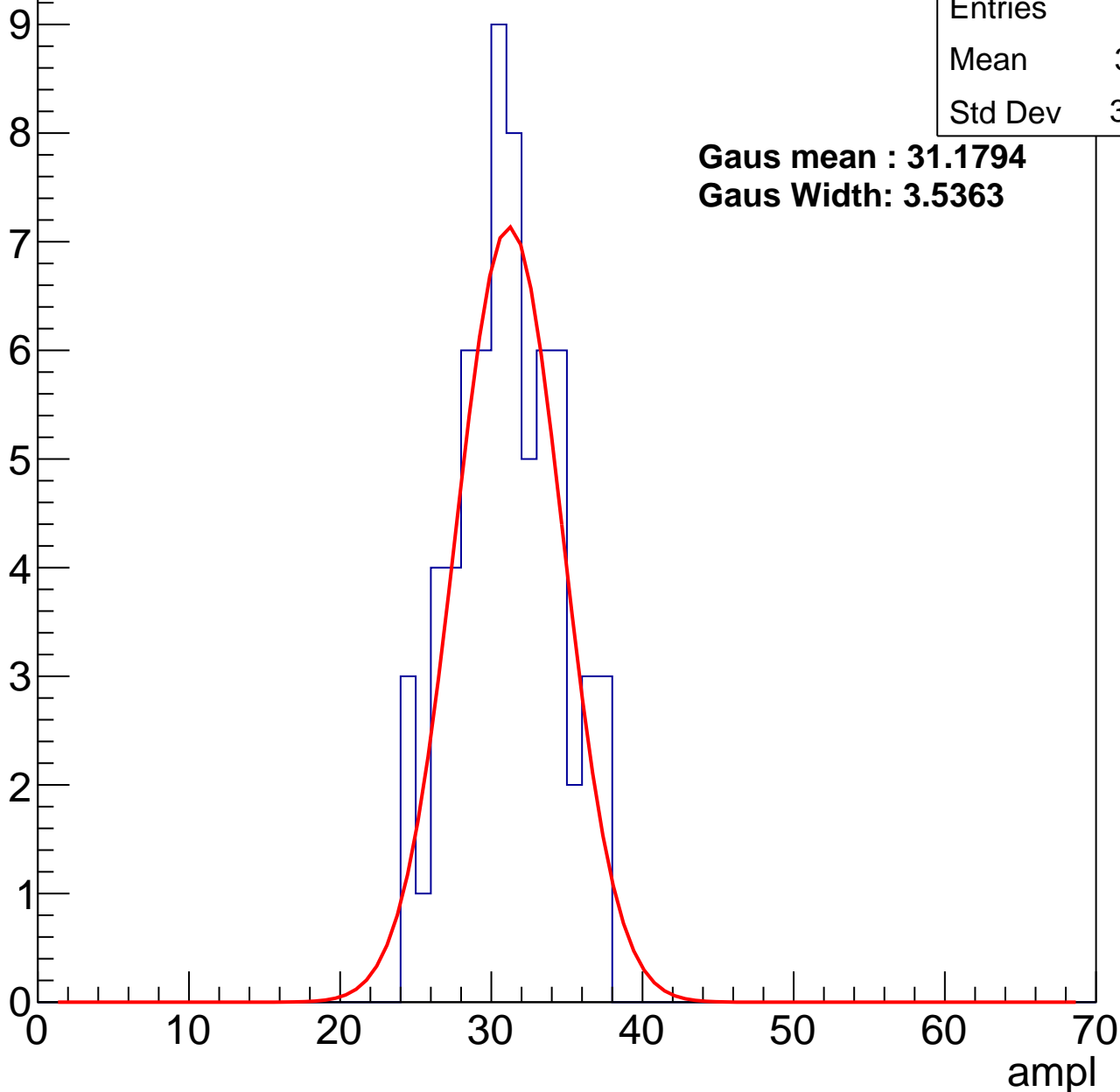
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	30.61
Std Dev	3.307

Gaus mean : 31.1794

Gaus Width: 3.5363



B0L000S, U4-ch39, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	37.24
Std Dev	3.458

Gaus mean : 37.6631

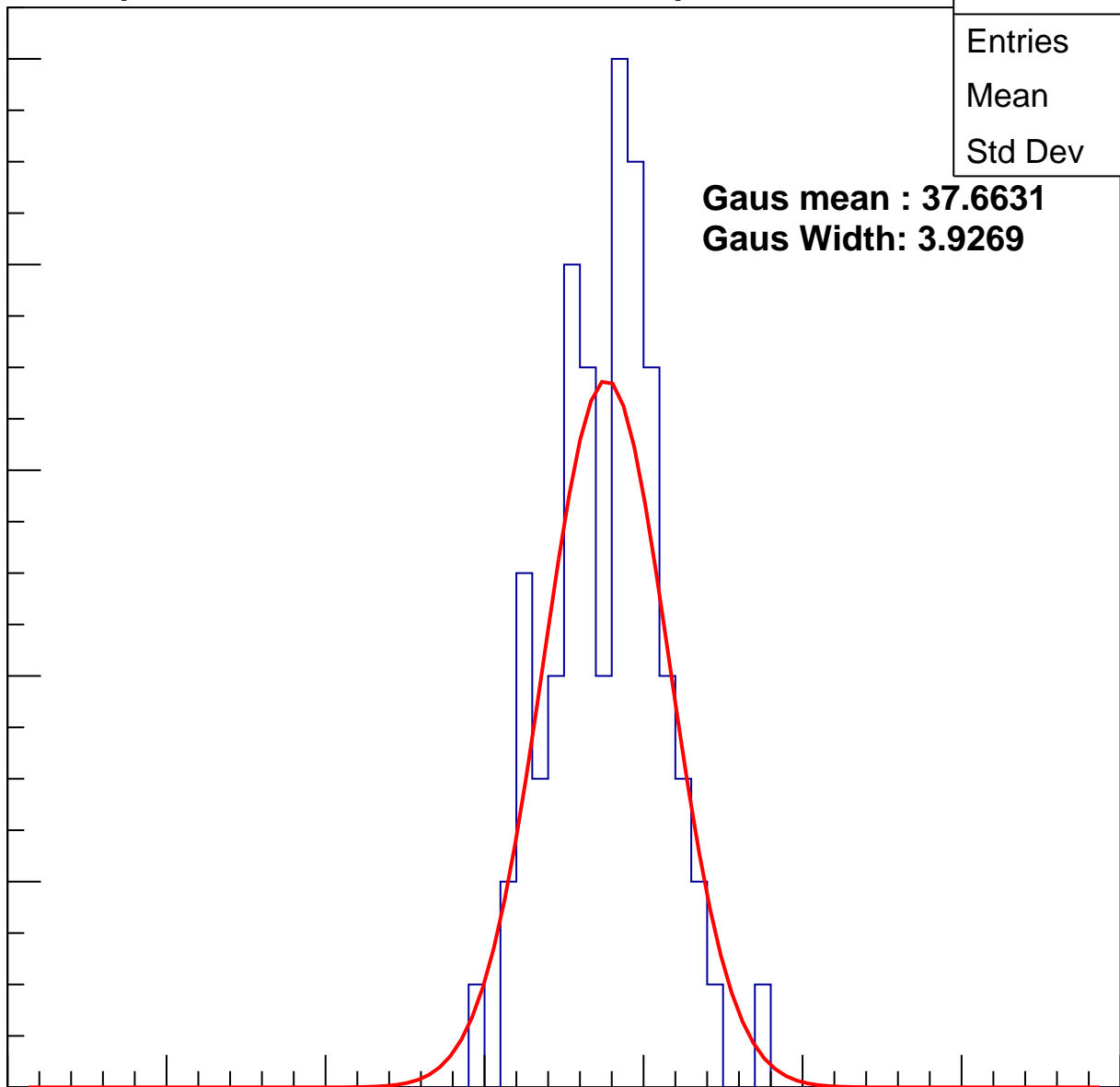
Gaus Width: 3.9269

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch39, adc2

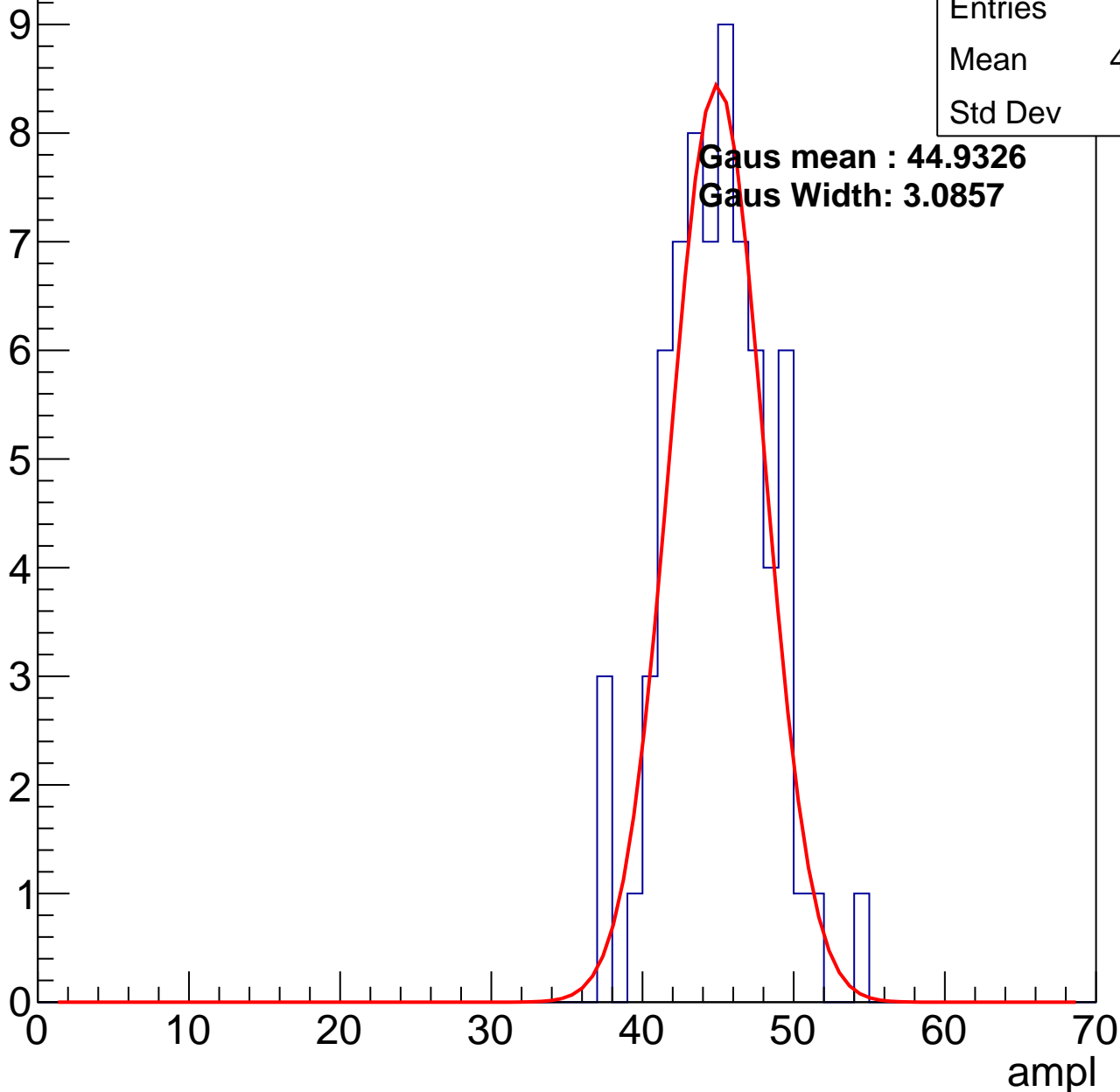
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	44.46
Std Dev	3.35

Gaus mean : 44.9326

Gaus Width: 3.0857

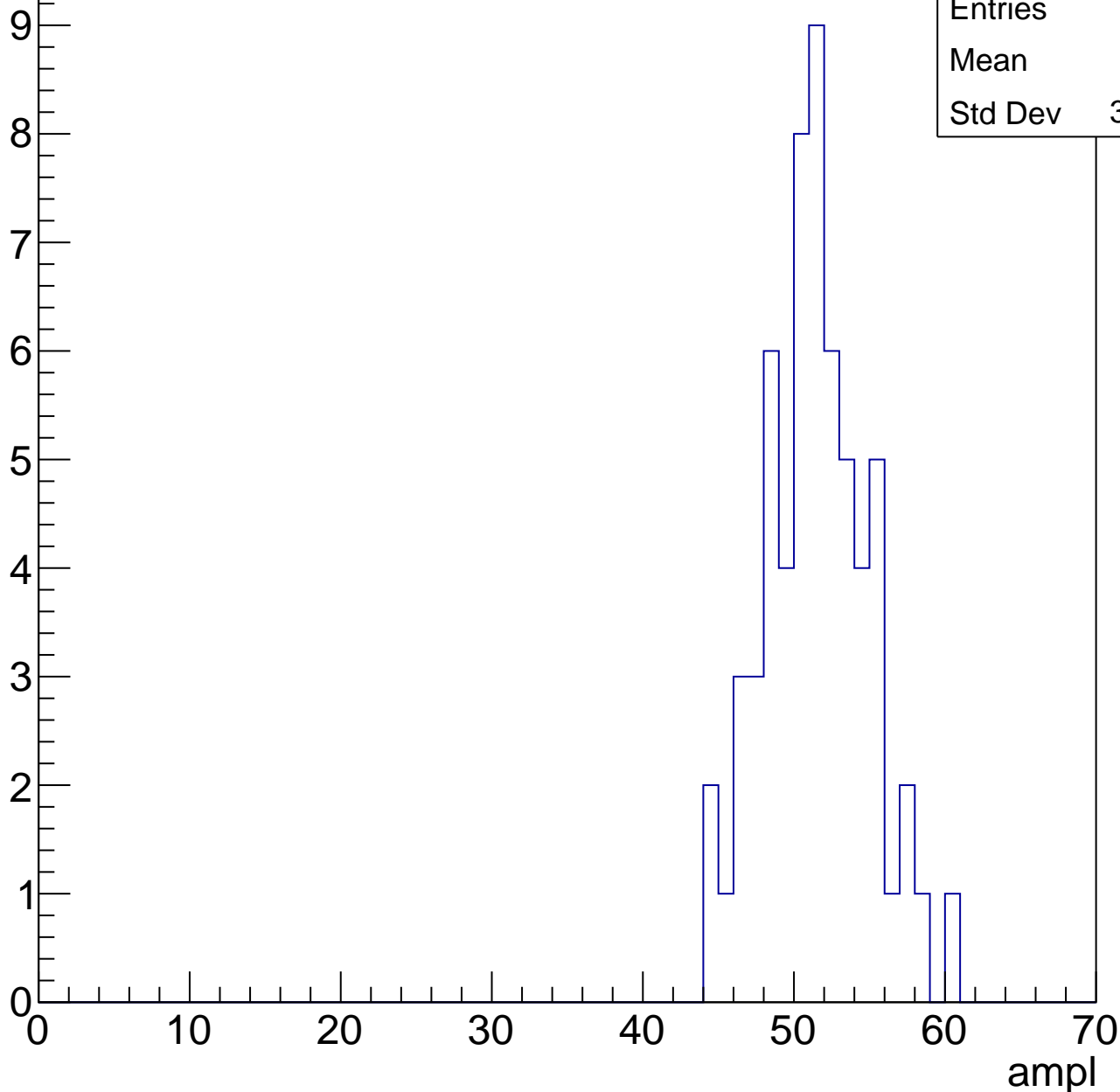


B0L000S, U4-ch39, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	51
Std Dev	3.397



B0L000S, U4-ch39, adc4

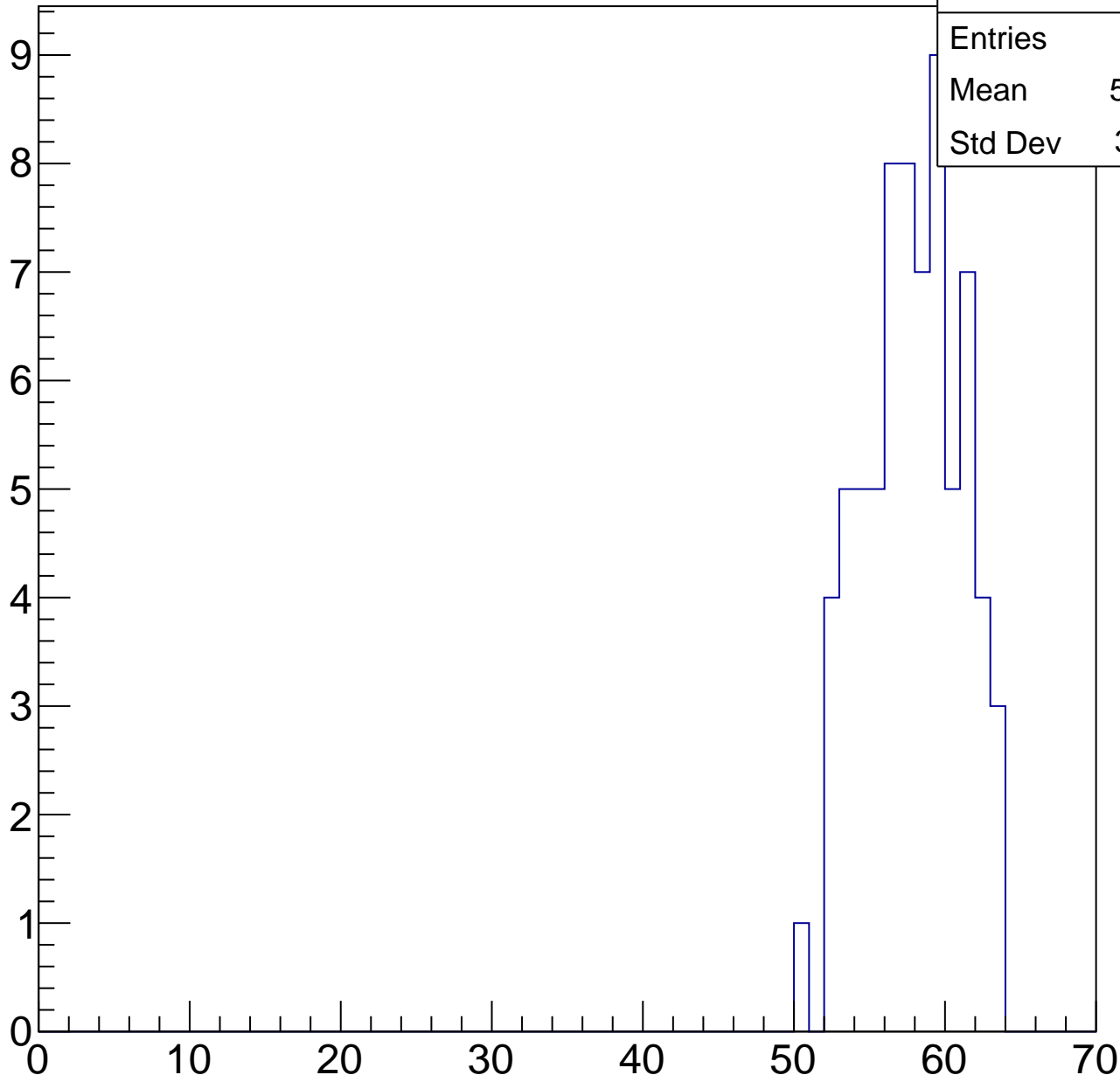
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	57.37
Std Dev	3.141

ampl

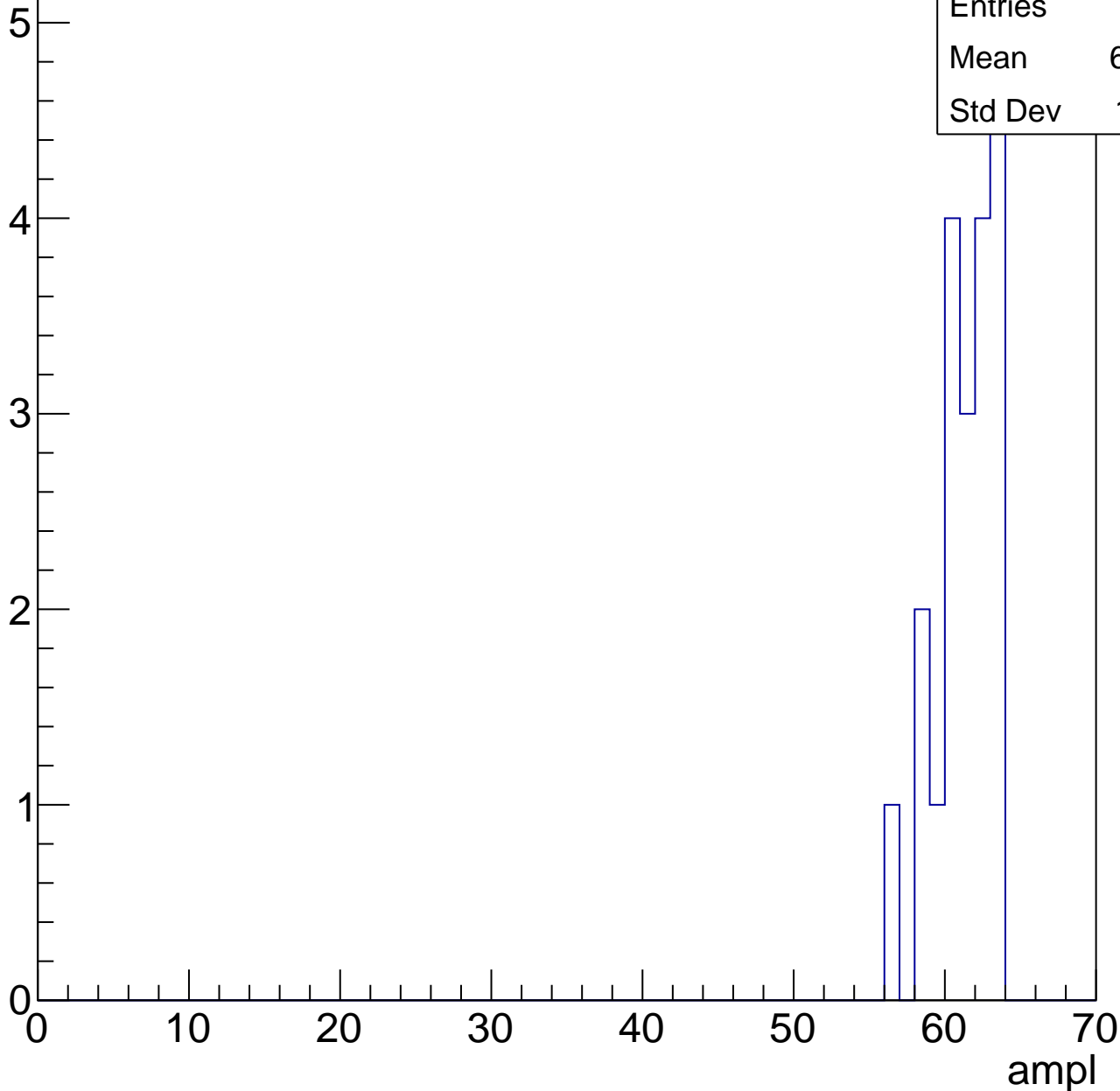


B0L000S, U4-ch39, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	20
Mean	60.85
Std Dev	1.931



B0L000S, U4-ch39, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch39, adc7

calib_packv5_042523_0143.root, FC#5, port B1

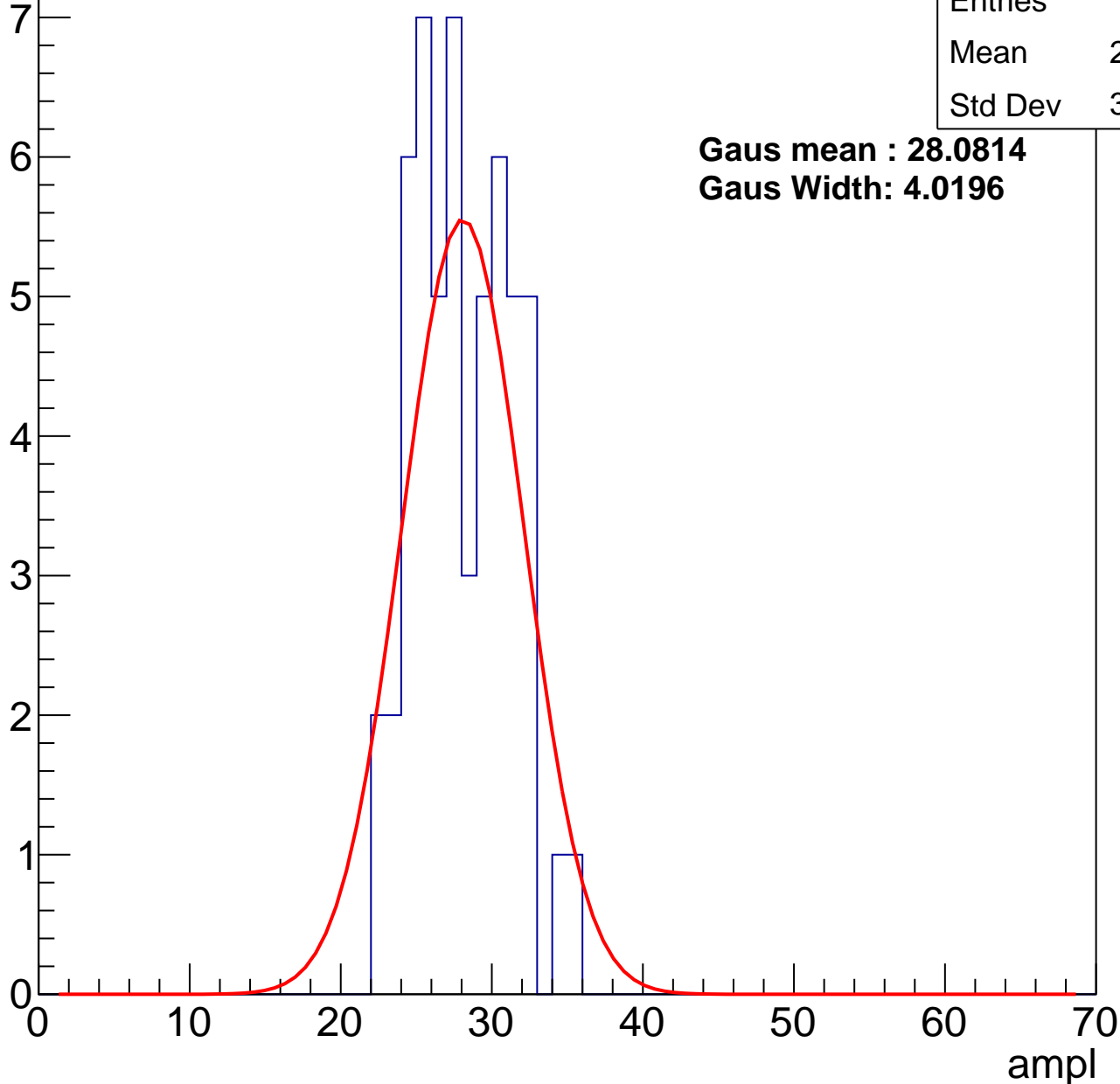
Entry



B0L000S, U4-ch40, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch40, adc1

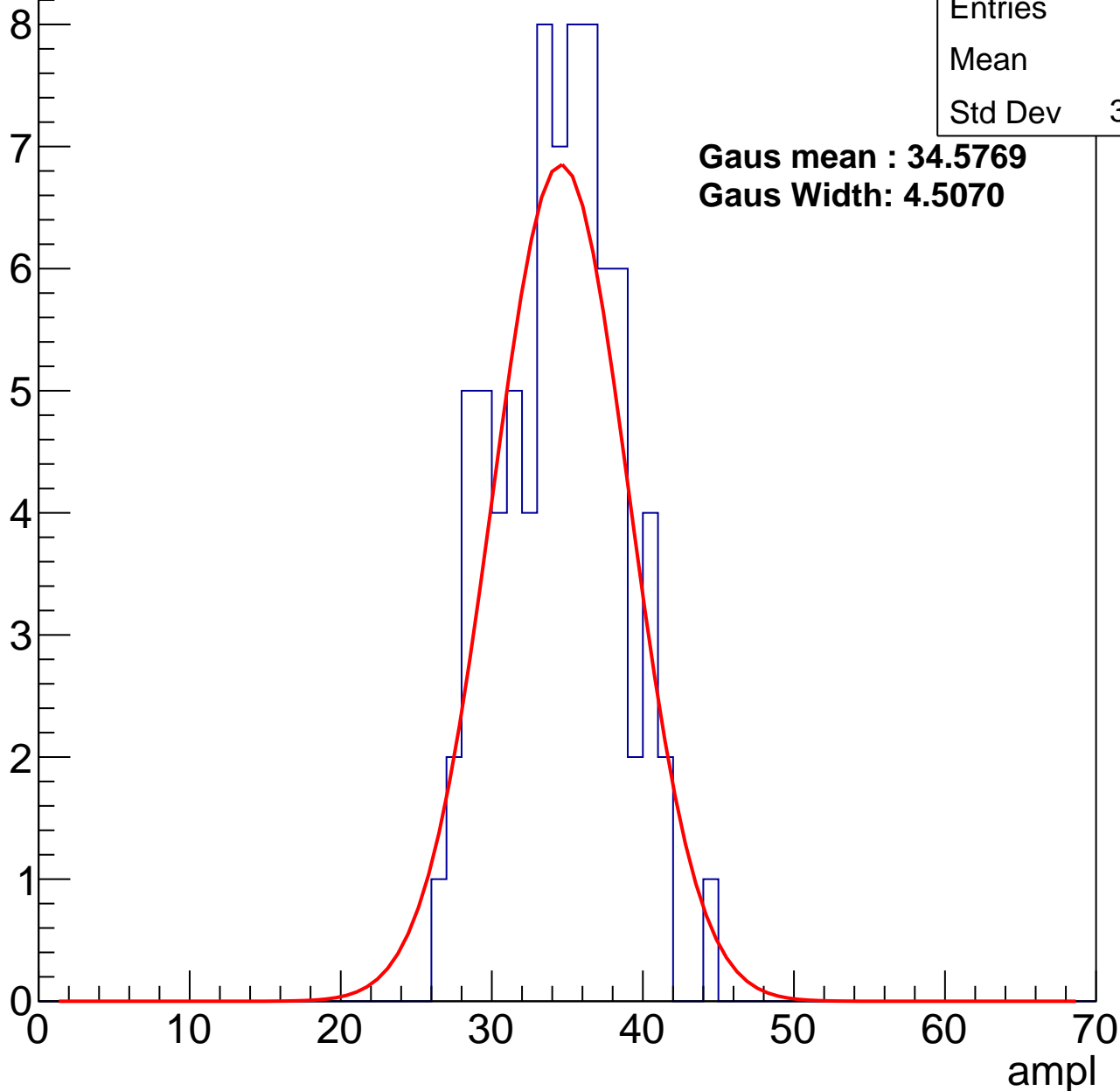
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	34
Std Dev	3.893

Gaus mean : 34.5769

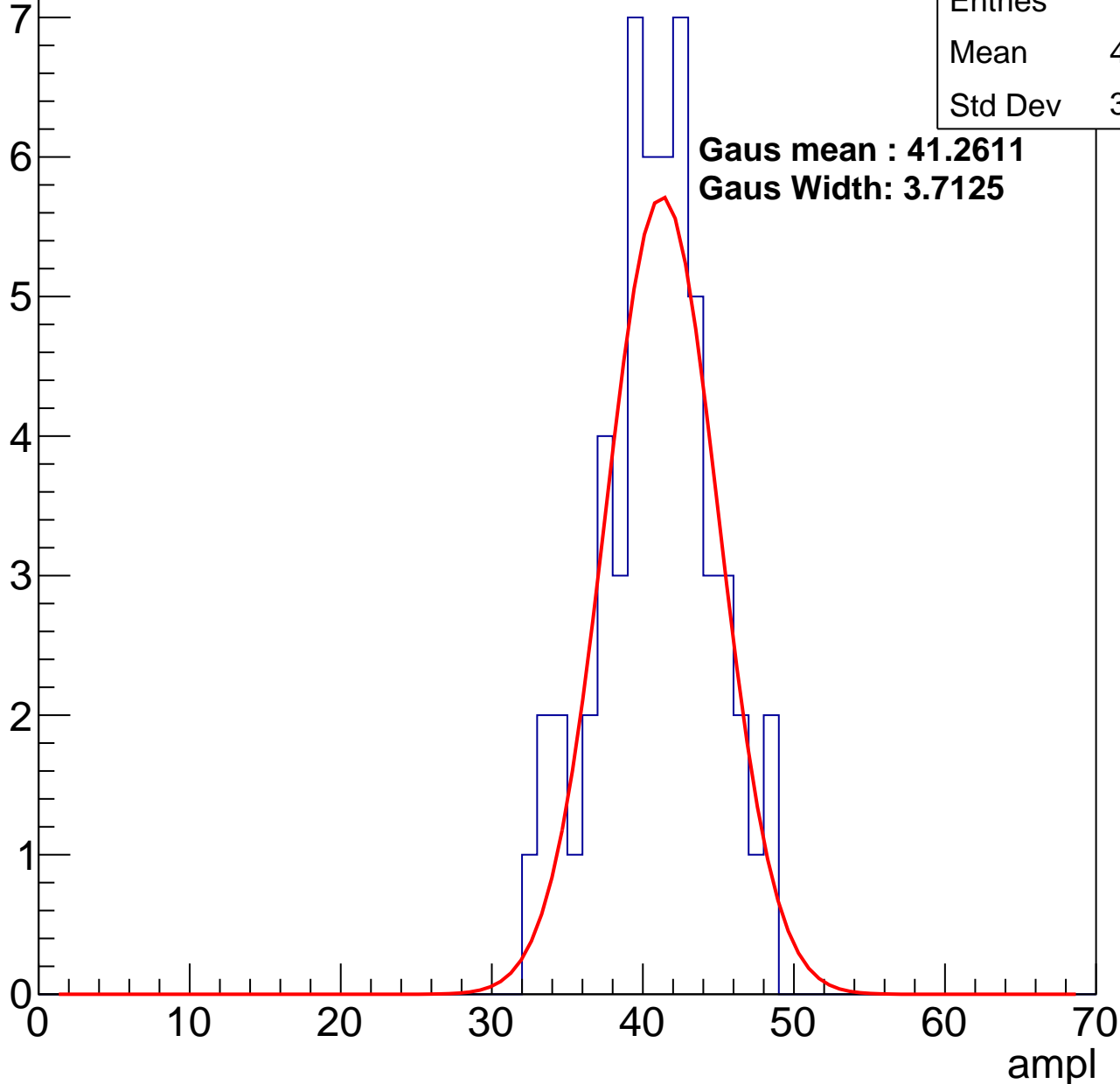
Gaus Width: 4.5070



B0L000S, U4-ch40, adc2

calib_packv5_042523_0143.root, FC#5, port B1

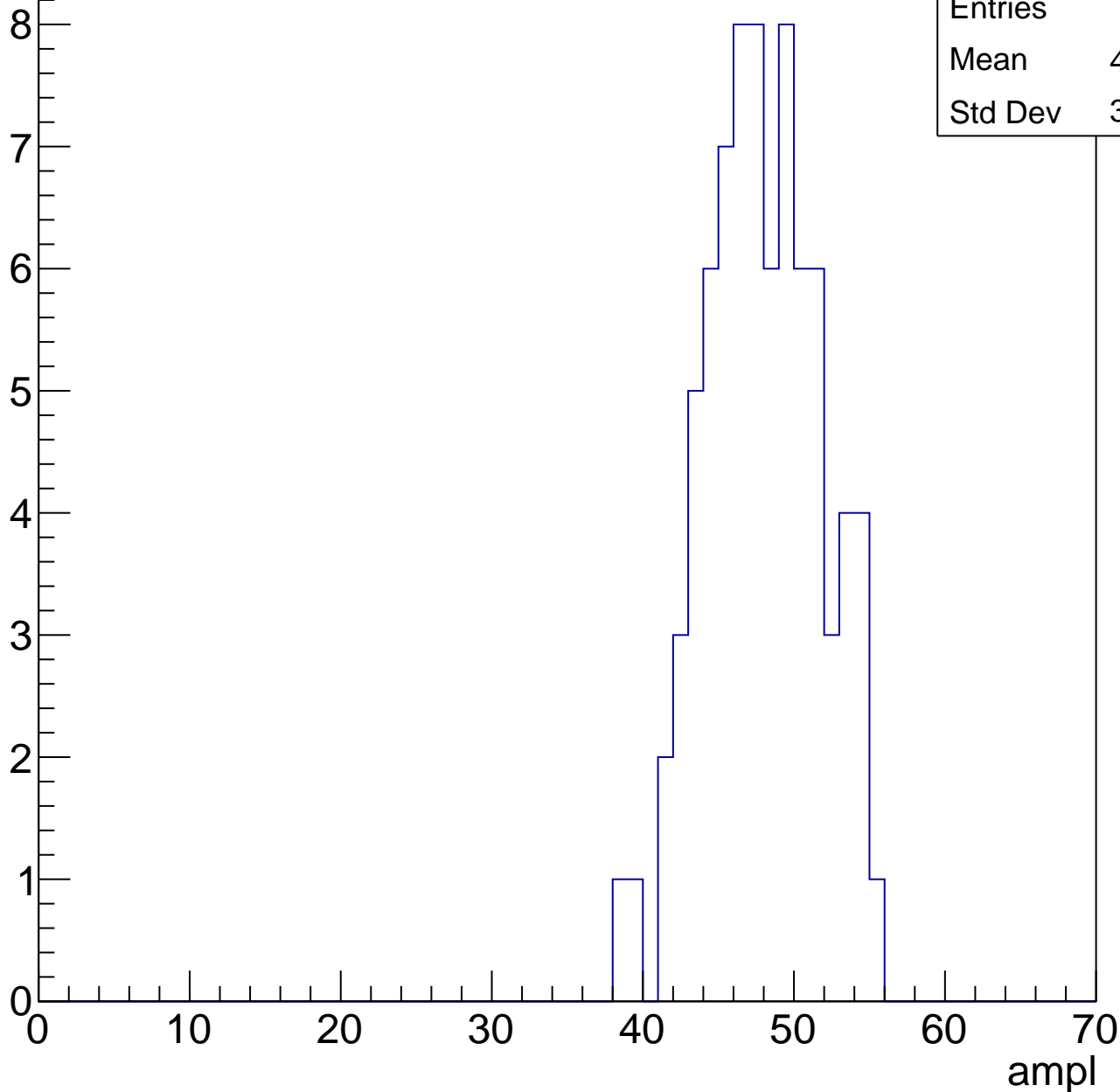
Entry



B0L000S, U4-ch40, adc3

calib_packv5_042523_0143.root, FC#5, port B1

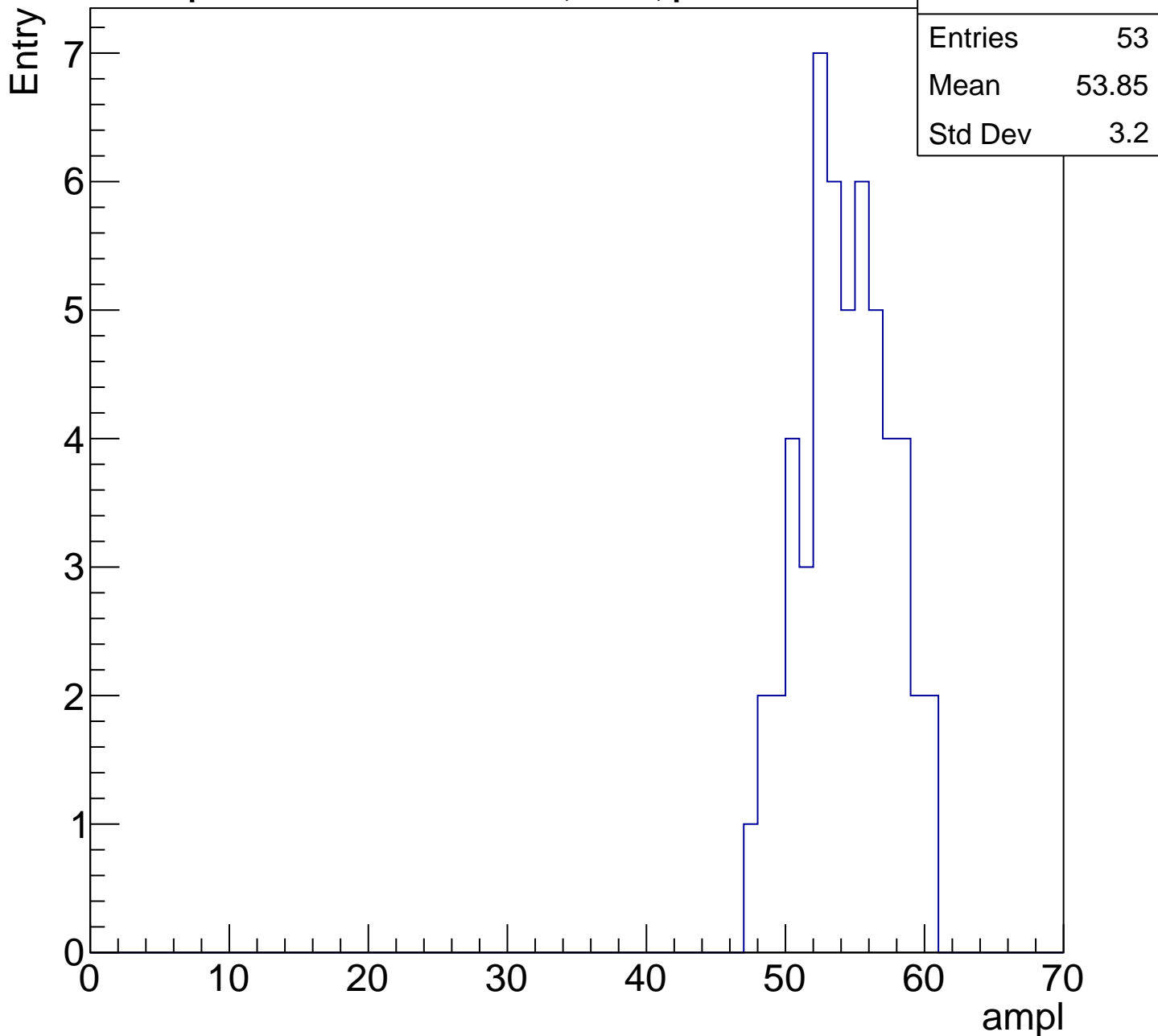
Entry



Entries	79
Mean	47.44
Std Dev	3.754

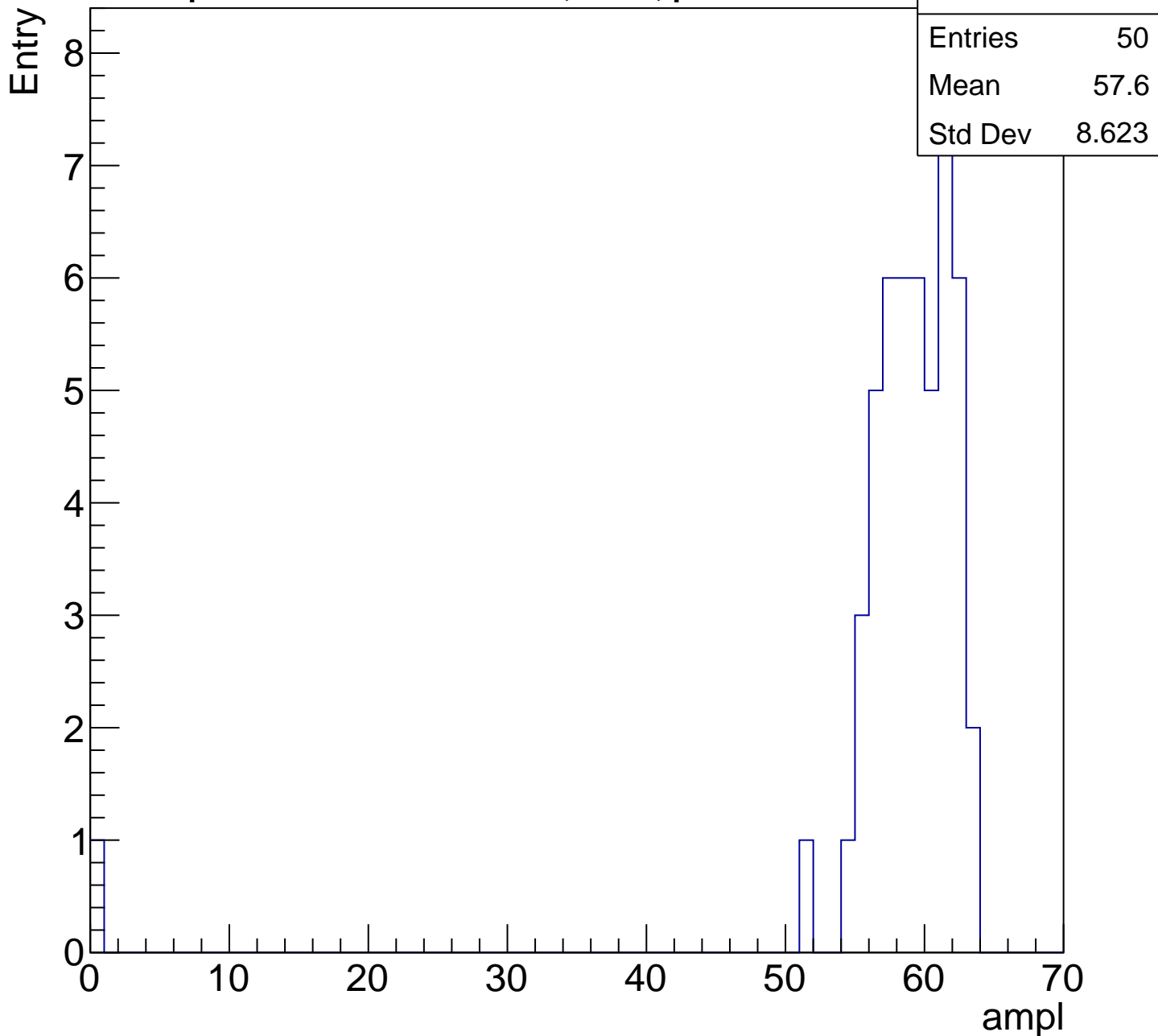
B0L000S, U4-ch40, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch40, adc5

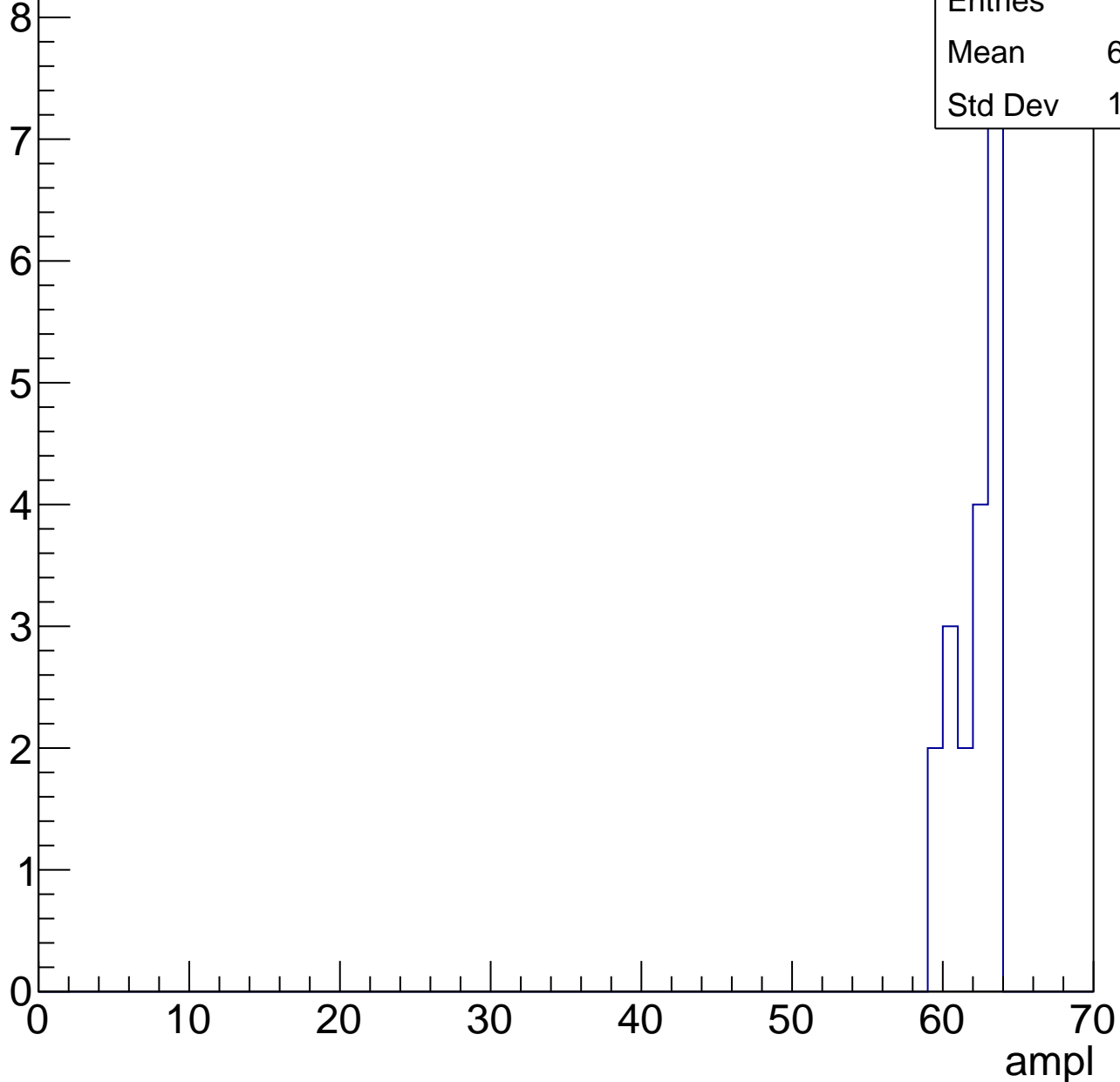
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch40, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch40, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch41, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	84
Mean	30.55
Std Dev	3.701

Gaus mean : 30.9960

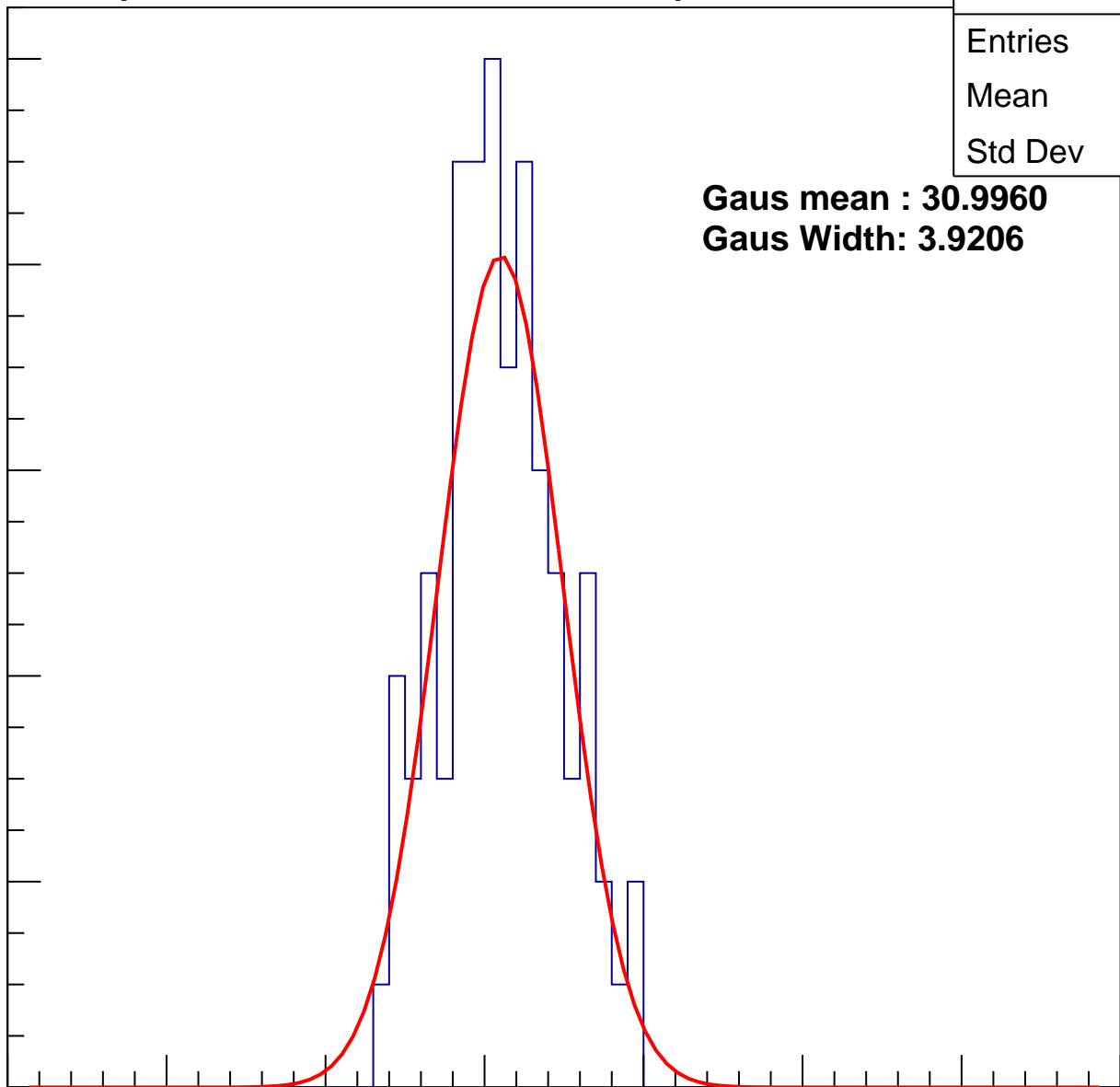
Gaus Width: 3.9206

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch41, adc1

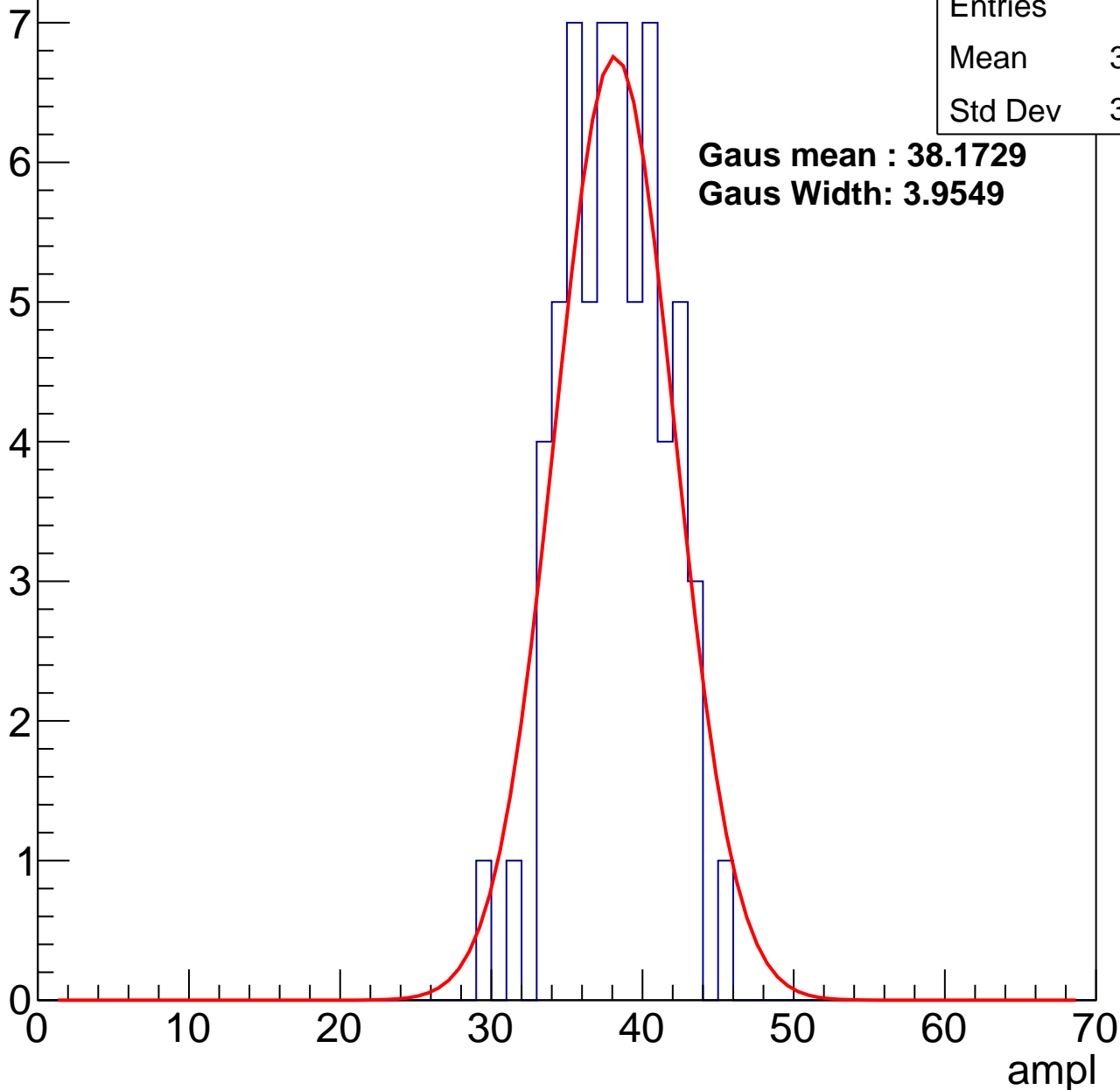
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.66
Std Dev	3.277

Gaus mean : 38.1729

Gaus Width: 3.9549



B0L000S, U4-ch41, adc2

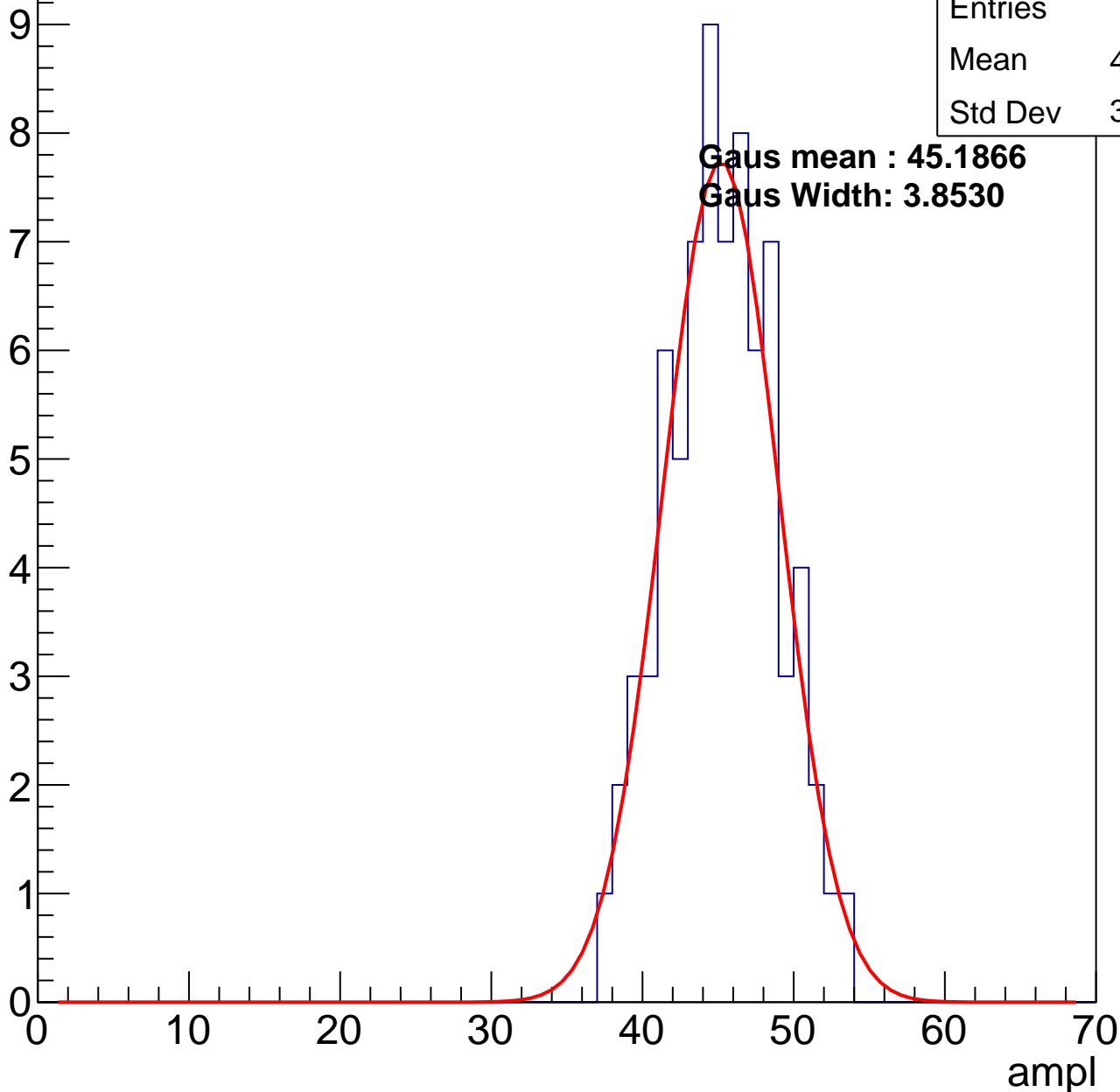
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.77
Std Dev	3.554

Gaus mean : 45.1866

Gaus Width: 3.8530

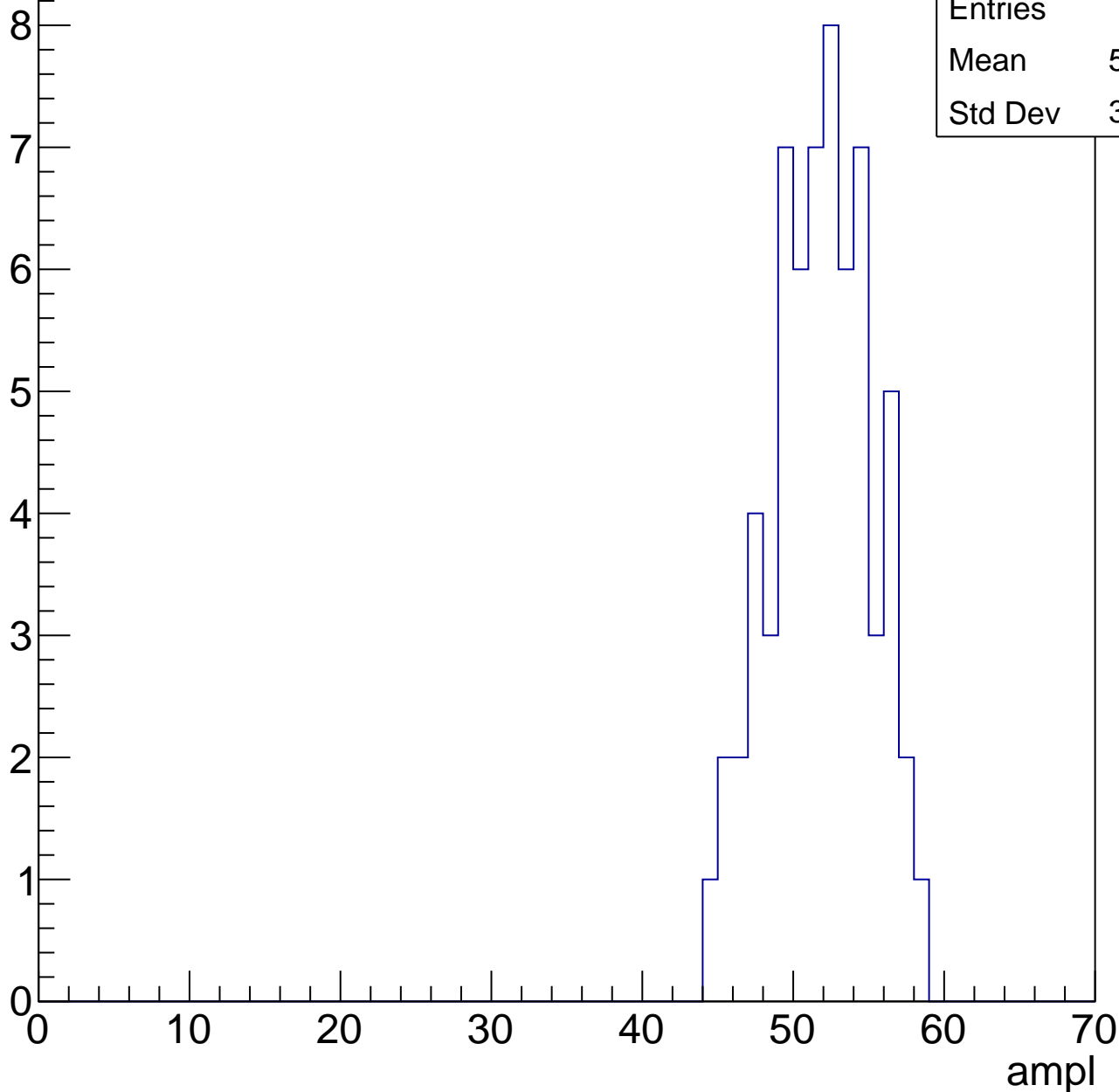


B0L000S, U4-ch41, adc3

calib_packv5_042523_0143.root, FC#5, port B1

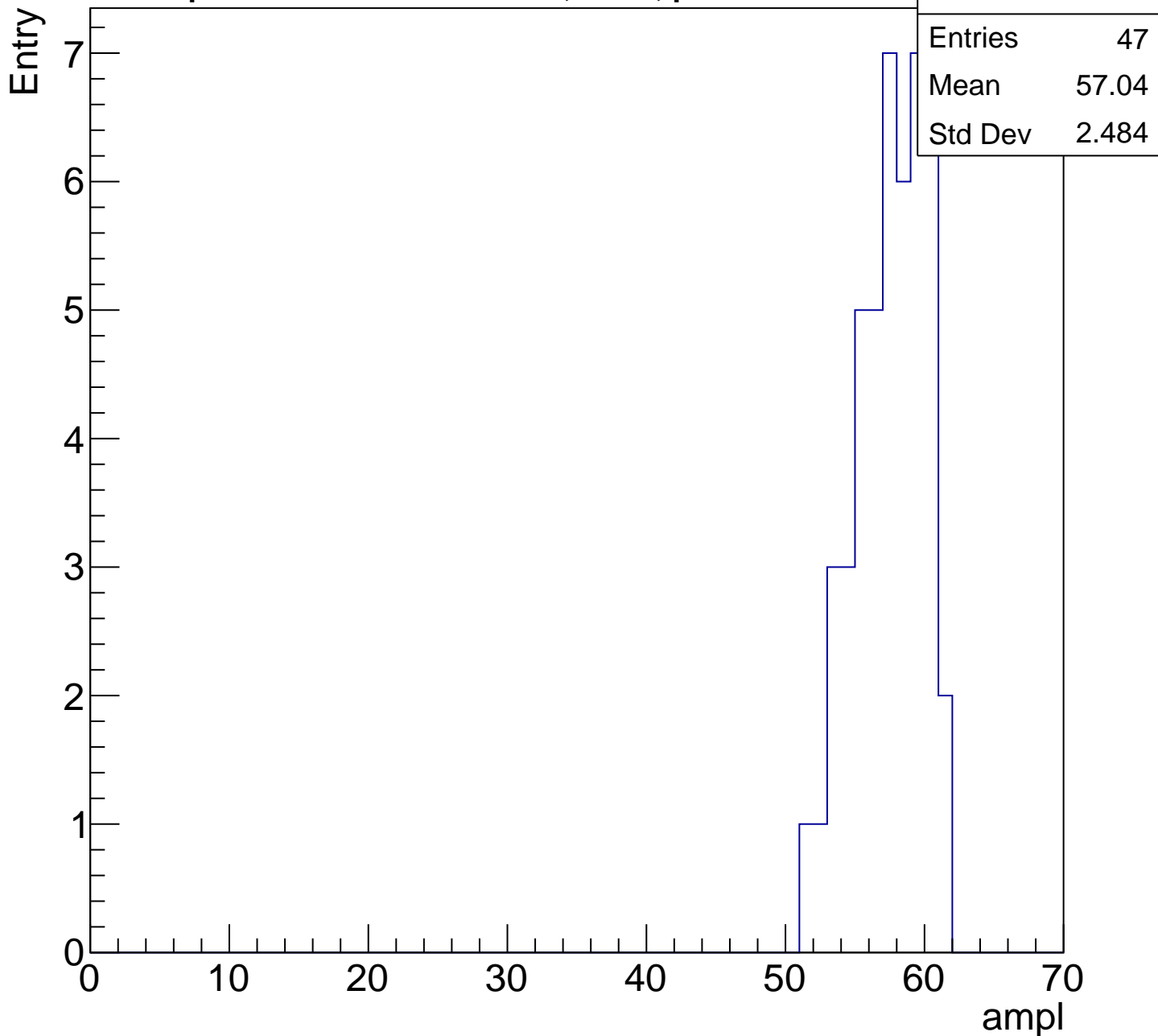
Entry

Entries	64
Mean	51.36
Std Dev	3.252



B0L000S, U4-ch41, adc4

calib_packv5_042523_0143.root, FC#5, port B1

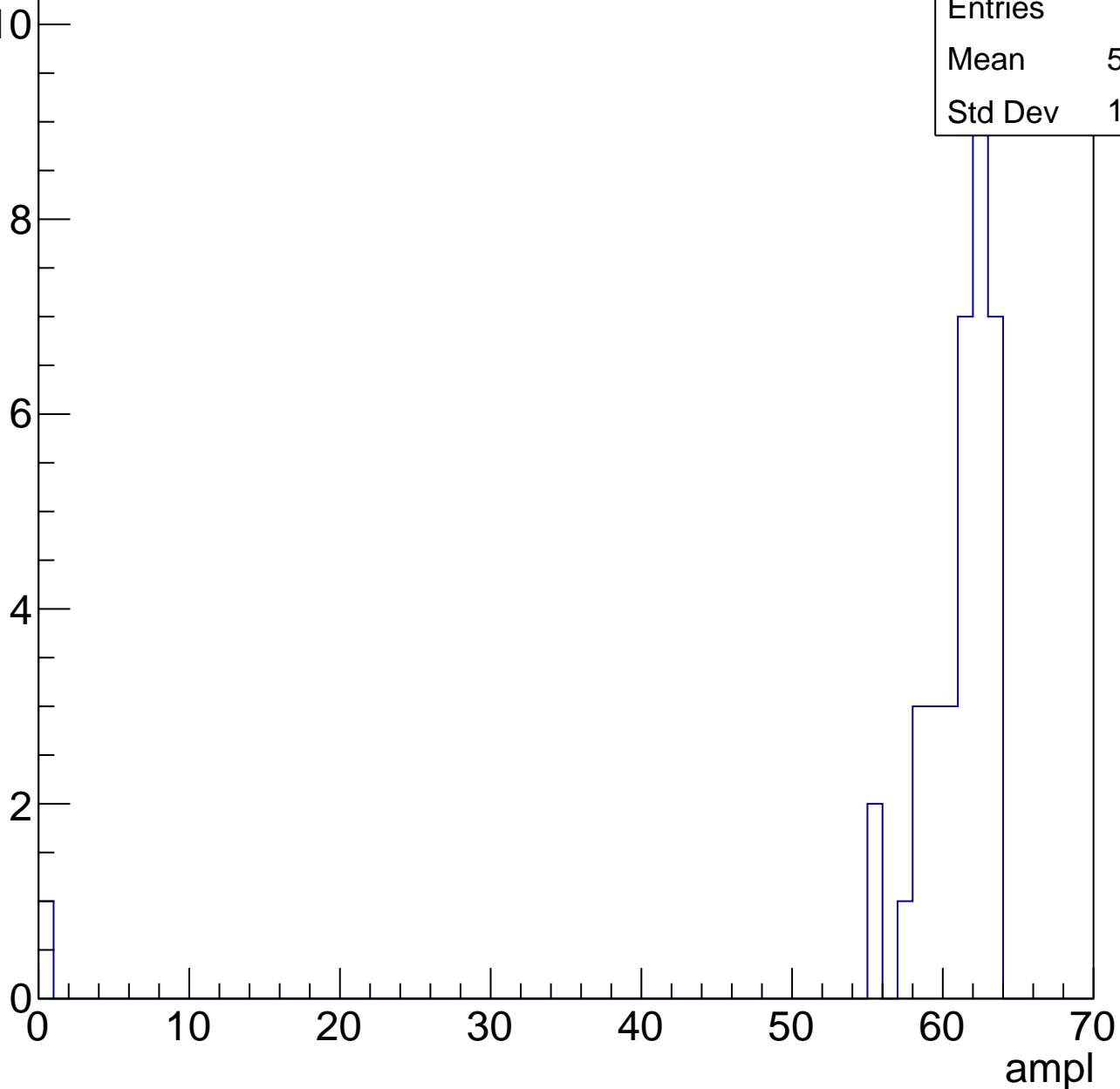


B0L000S, U4-ch41, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	37
Mean	59.08
Std Dev	10.07



B0L000S, U4-ch41, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch41, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch42, adc0

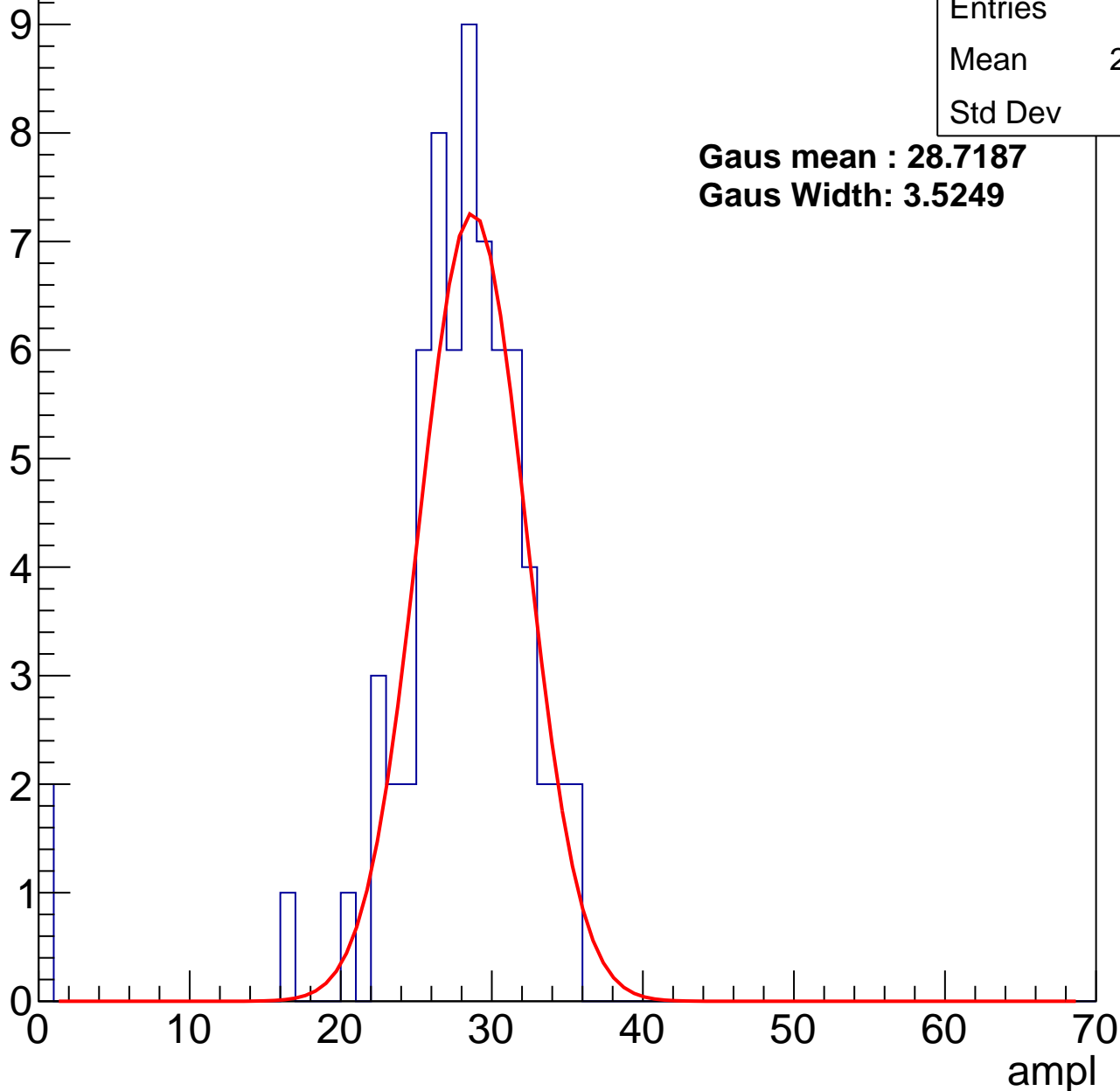
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	27.09
Std Dev	5.86

Gaus mean : 28.7187

Gaus Width: 3.5249



B0L000S, U4-ch42, adc1

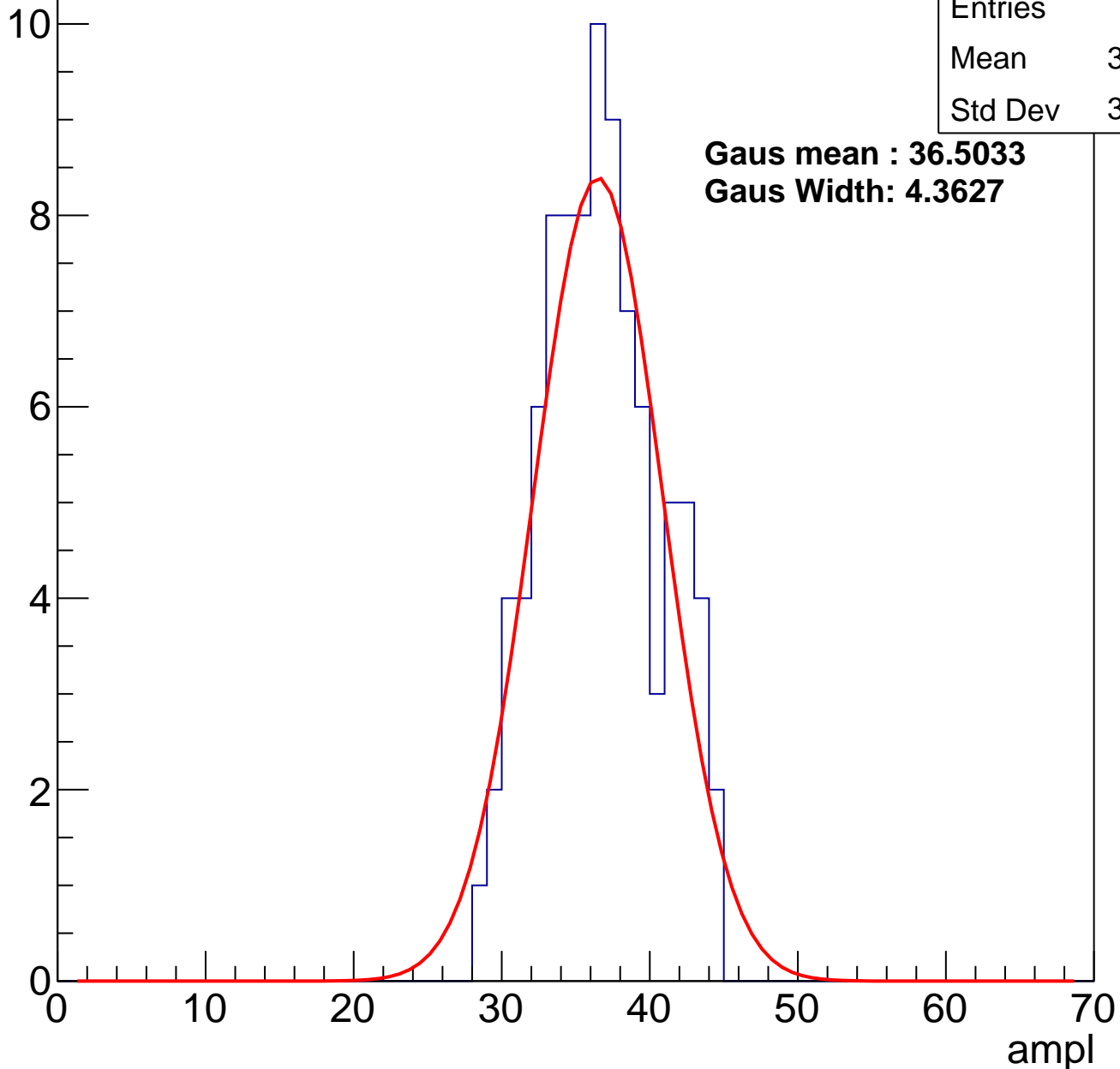
calib_packv5_042523_0143.root, FC#5, port B1

Entries	92
Mean	36.15
Std Dev	3.873

Gaus mean : 36.5033

Gaus Width: 4.3627

Entry

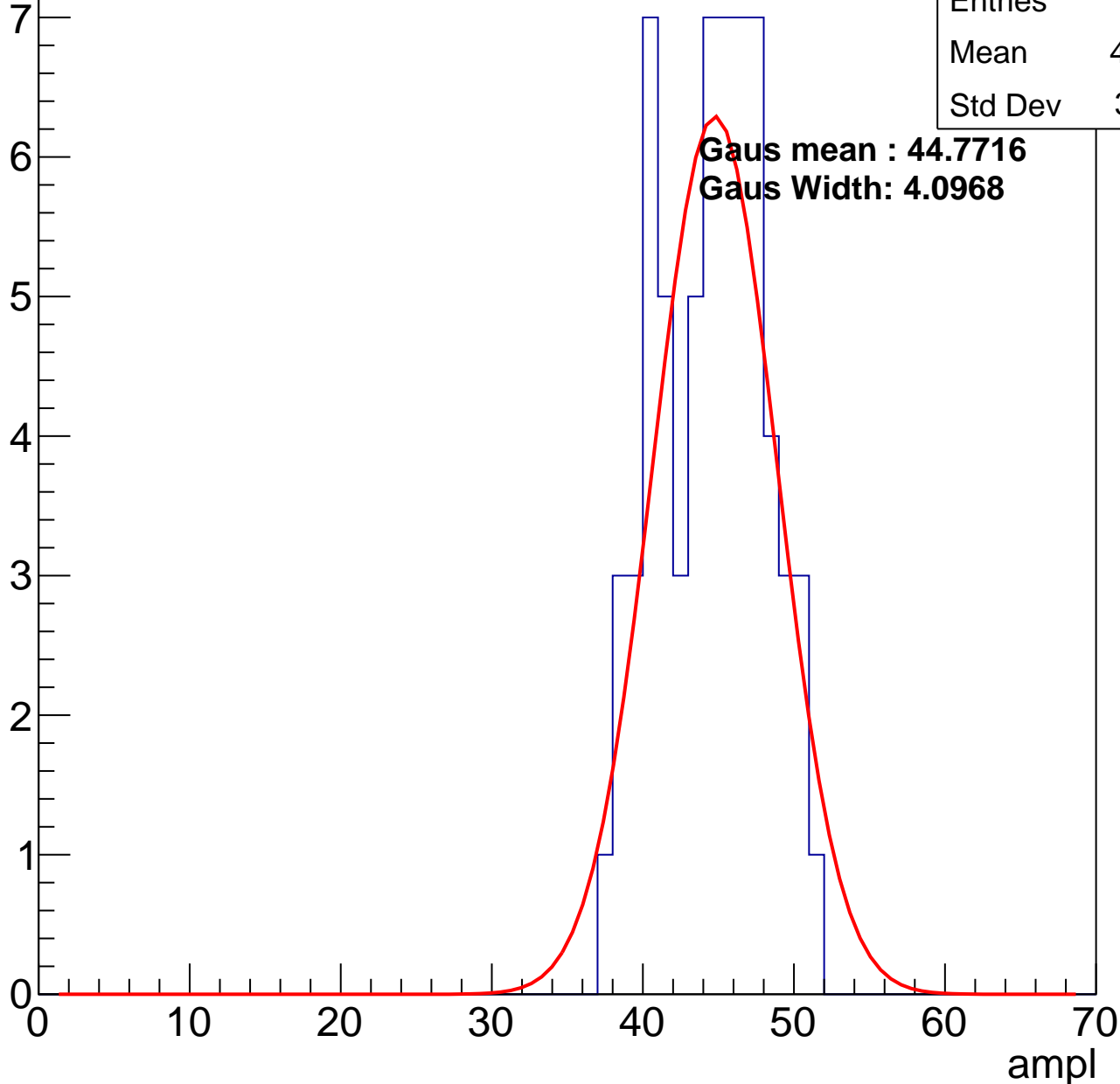


B0L000S, U4-ch42, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.06
Std Dev	3.481

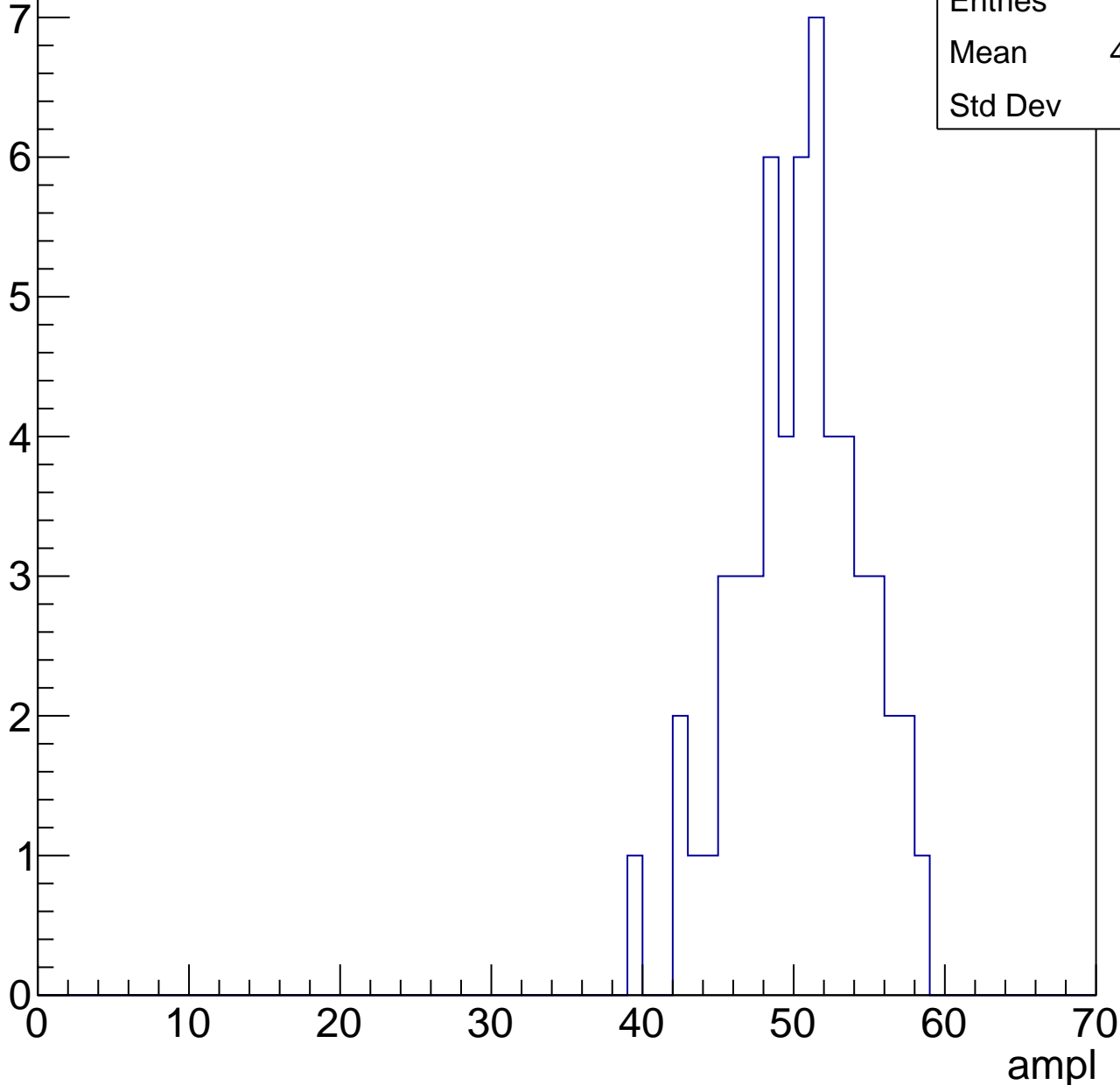


B0L000S, U4-ch42, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	49.93
Std Dev	4.07

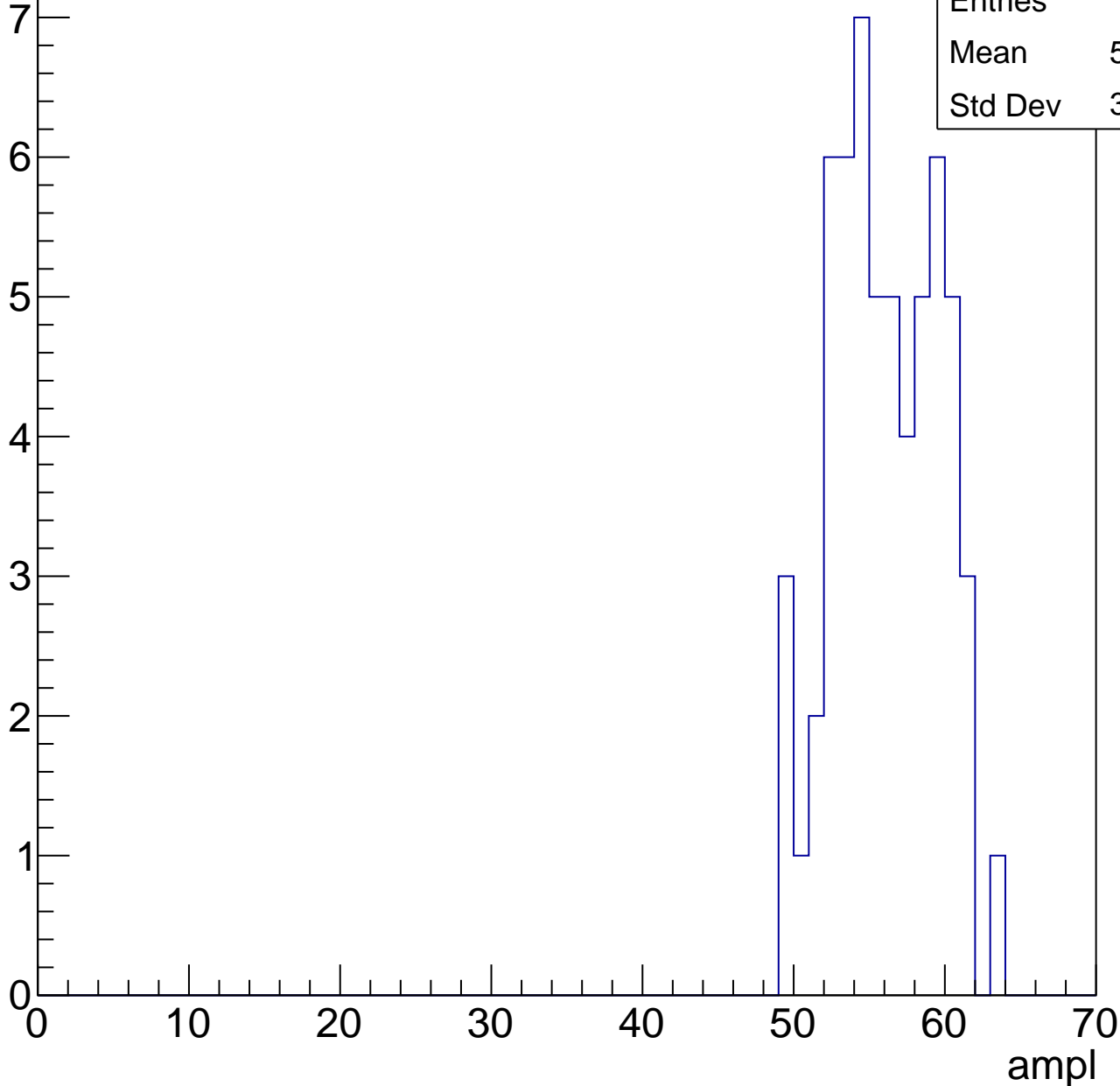


B0L000S, U4-ch42, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.59
Std Dev	3.415



B0L000S, U4-ch42, adc5

calib_packv5_042523_0143.root, FC#5, port B1

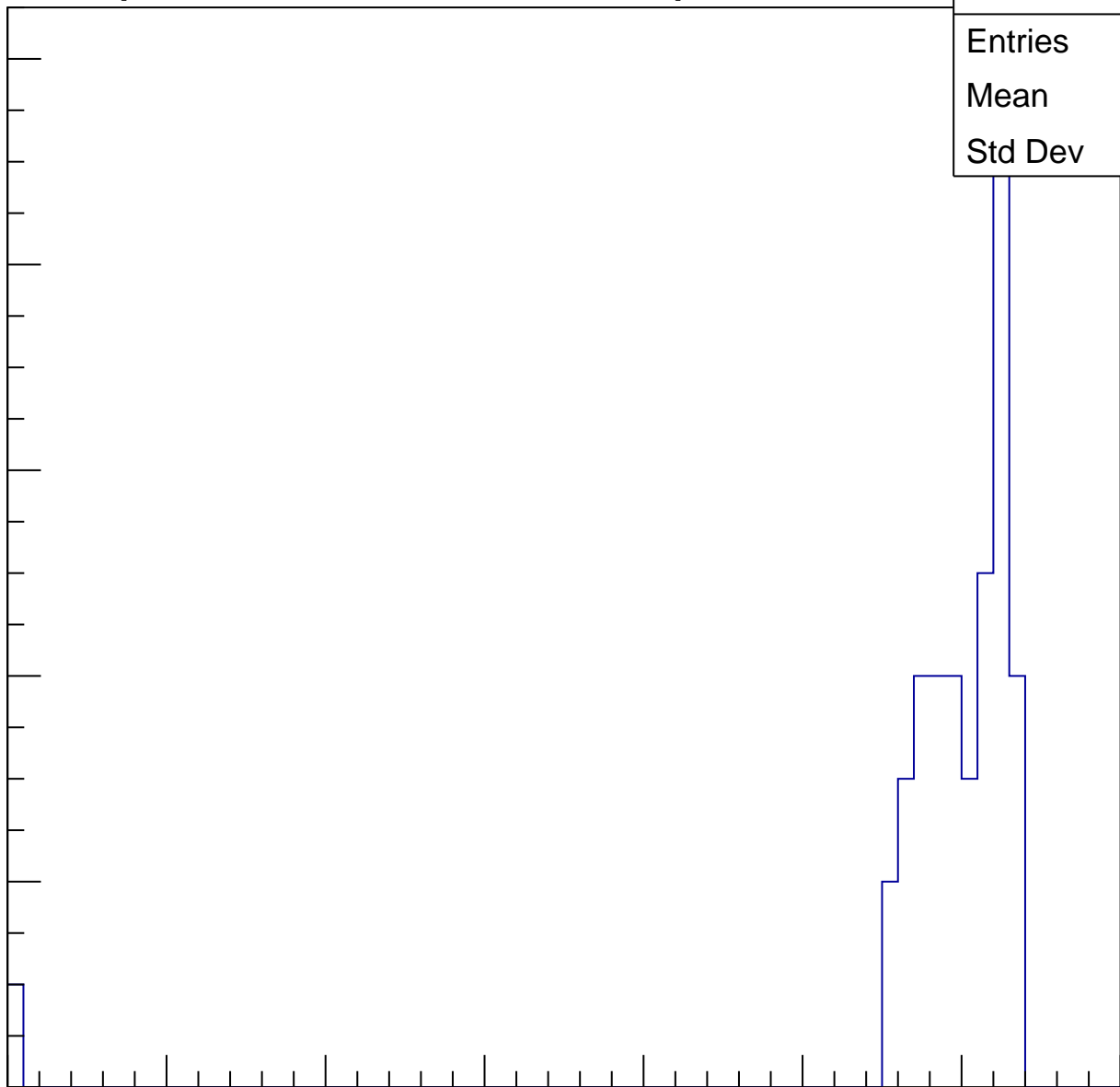
Entries	40
Mean	58.27
Std Dev	9.638

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

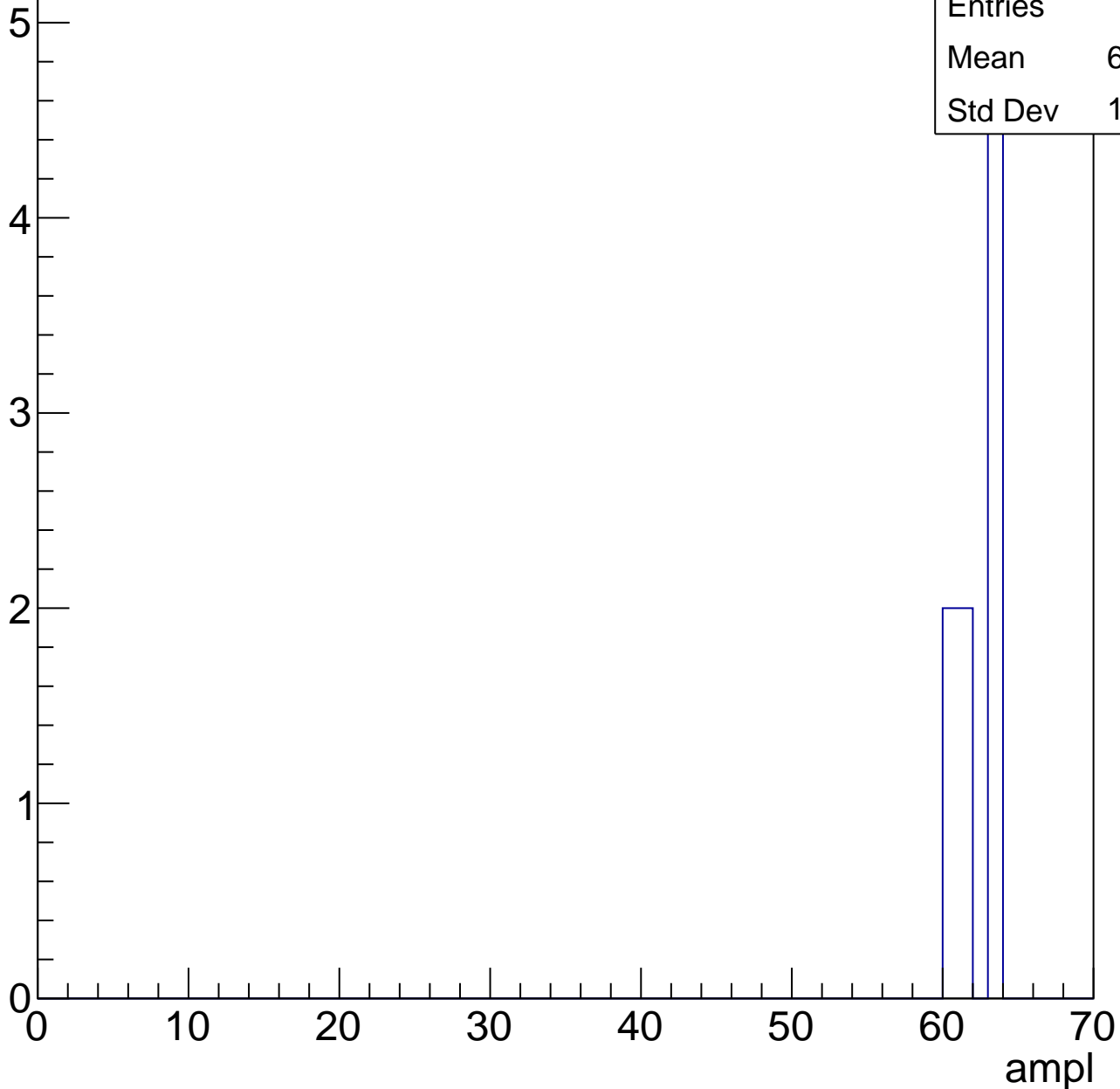


B0L000S, U4-ch42, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	9
Mean	61.89
Std Dev	1.286



B0L000S, U4-ch42, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch43, adc0

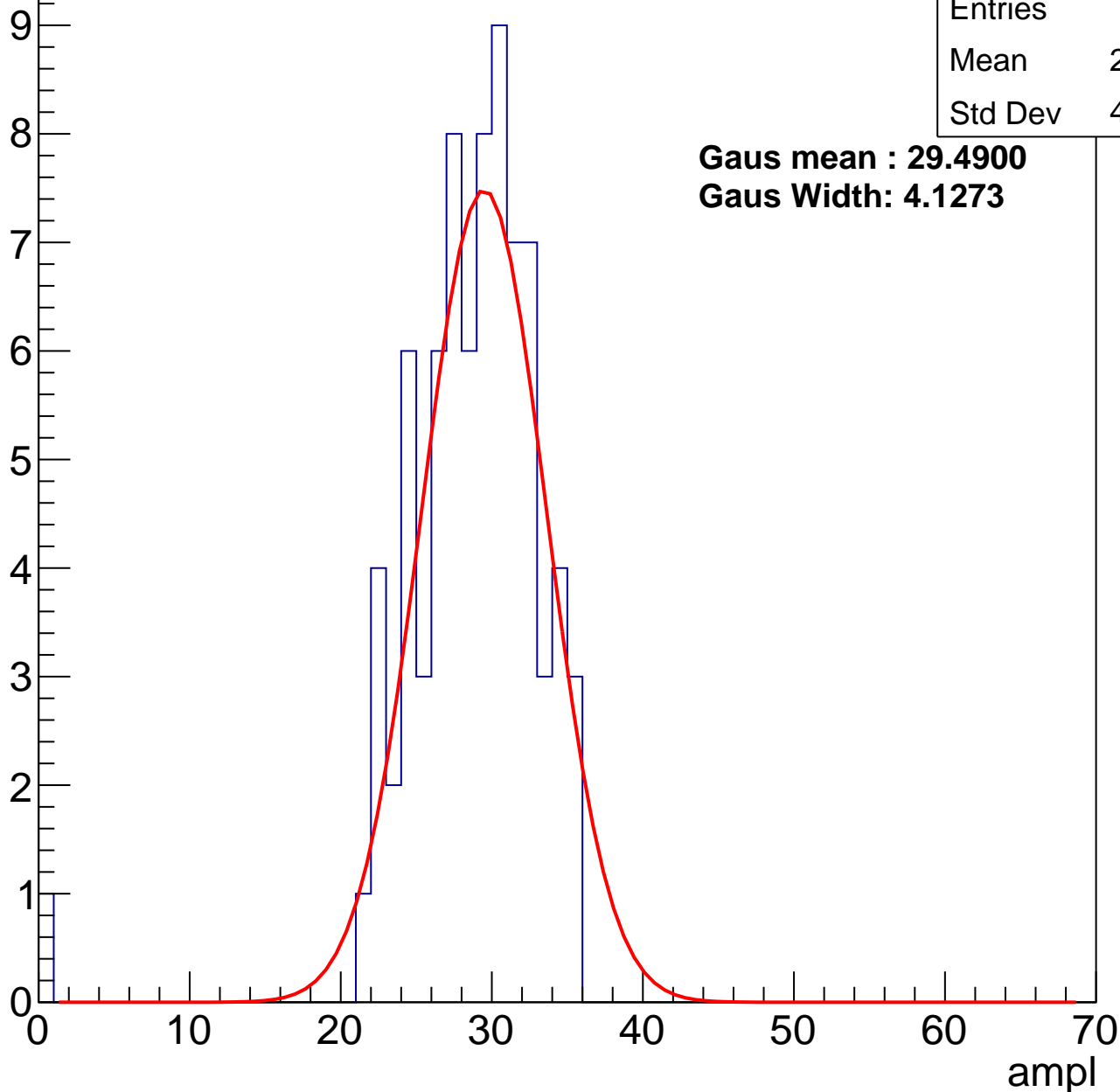
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.17
Std Dev	4.757

Gaus mean : 29.4900

Gaus Width: 4.1273



B0L000S, U4-ch43, adc1

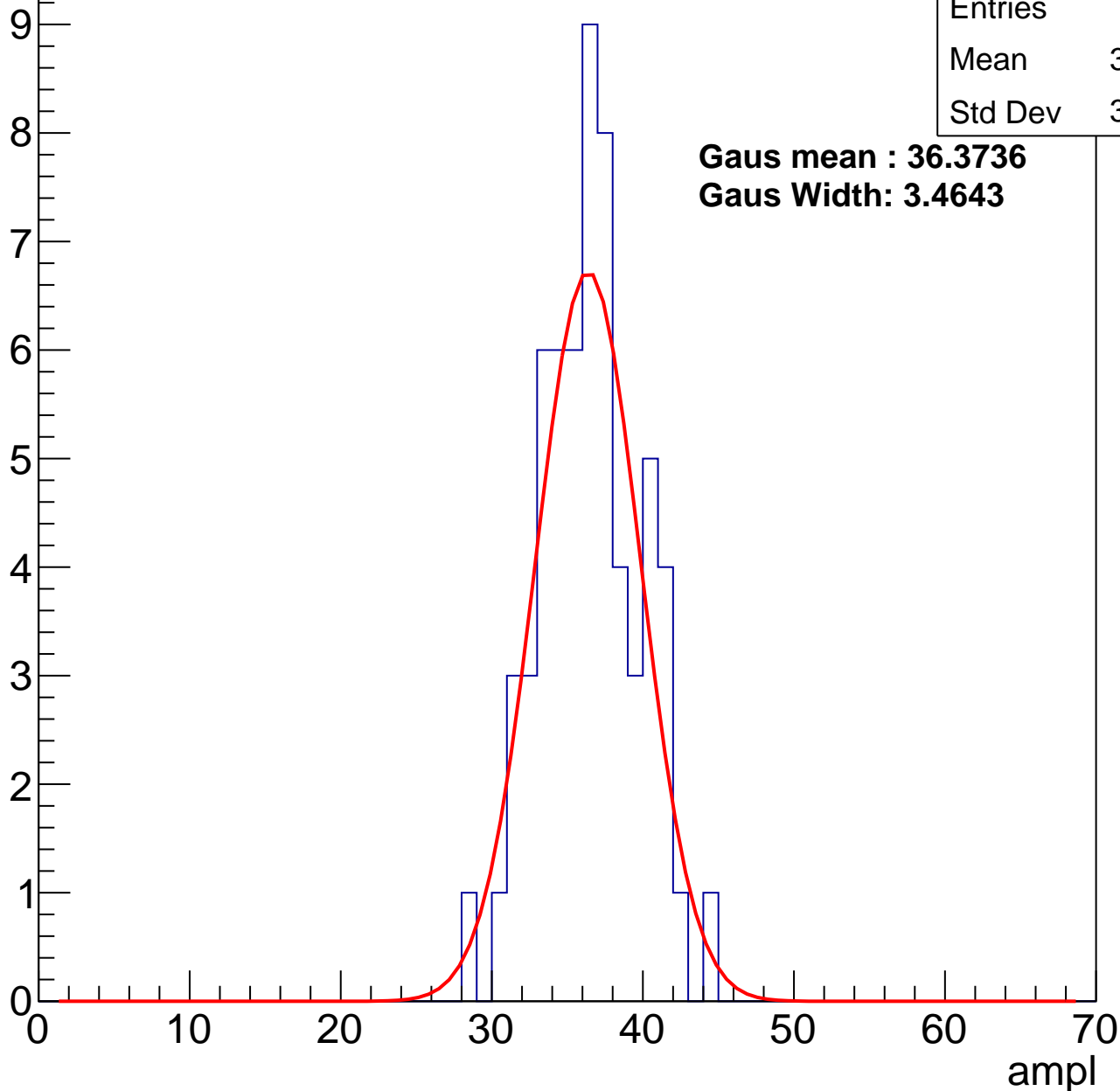
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.03
Std Dev	3.234

Gaus mean : 36.3736

Gaus Width: 3.4643



B0L000S, U4-ch43, adc2

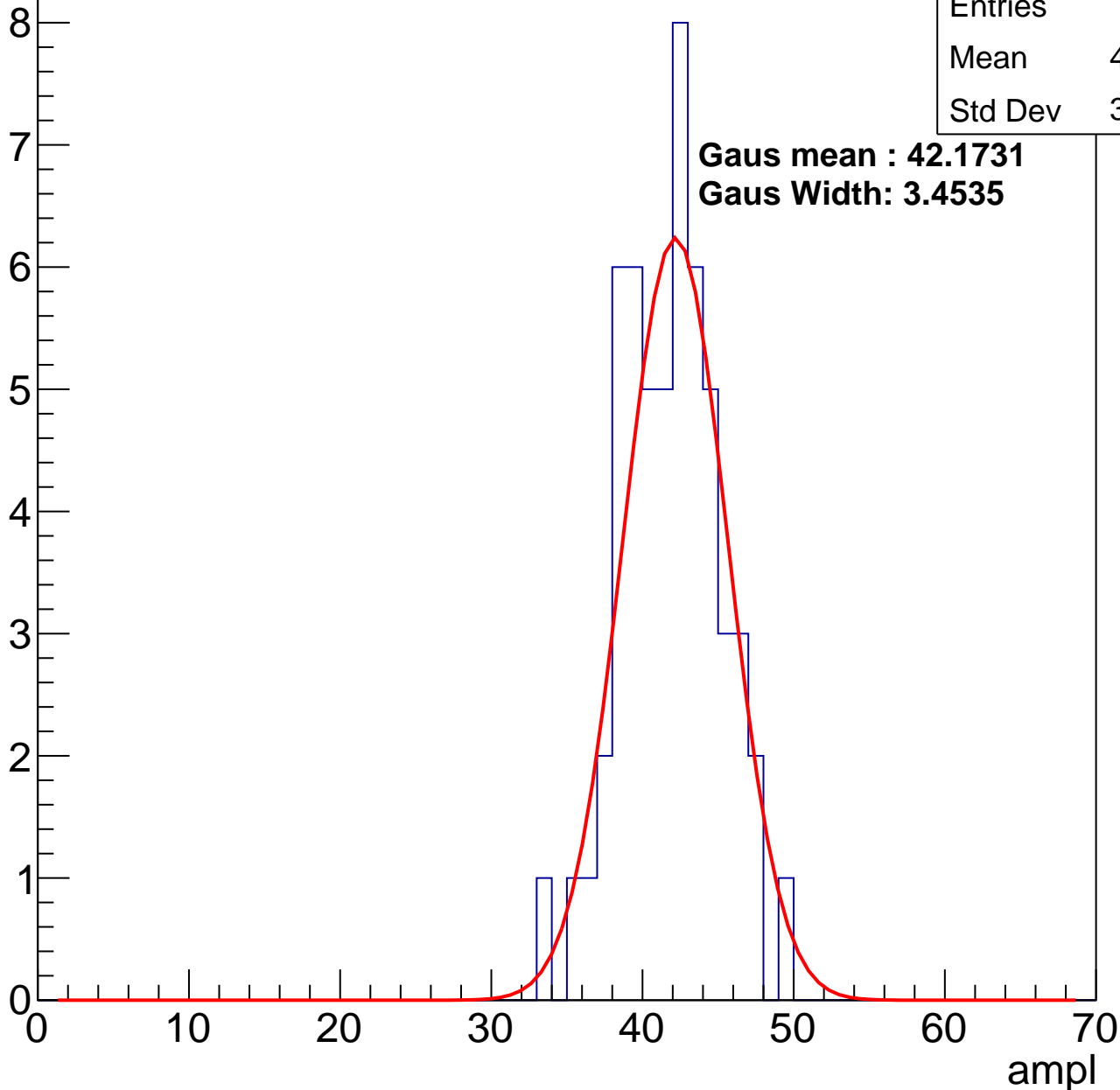
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	41.36
Std Dev	3.216

Gaus mean : 42.1731

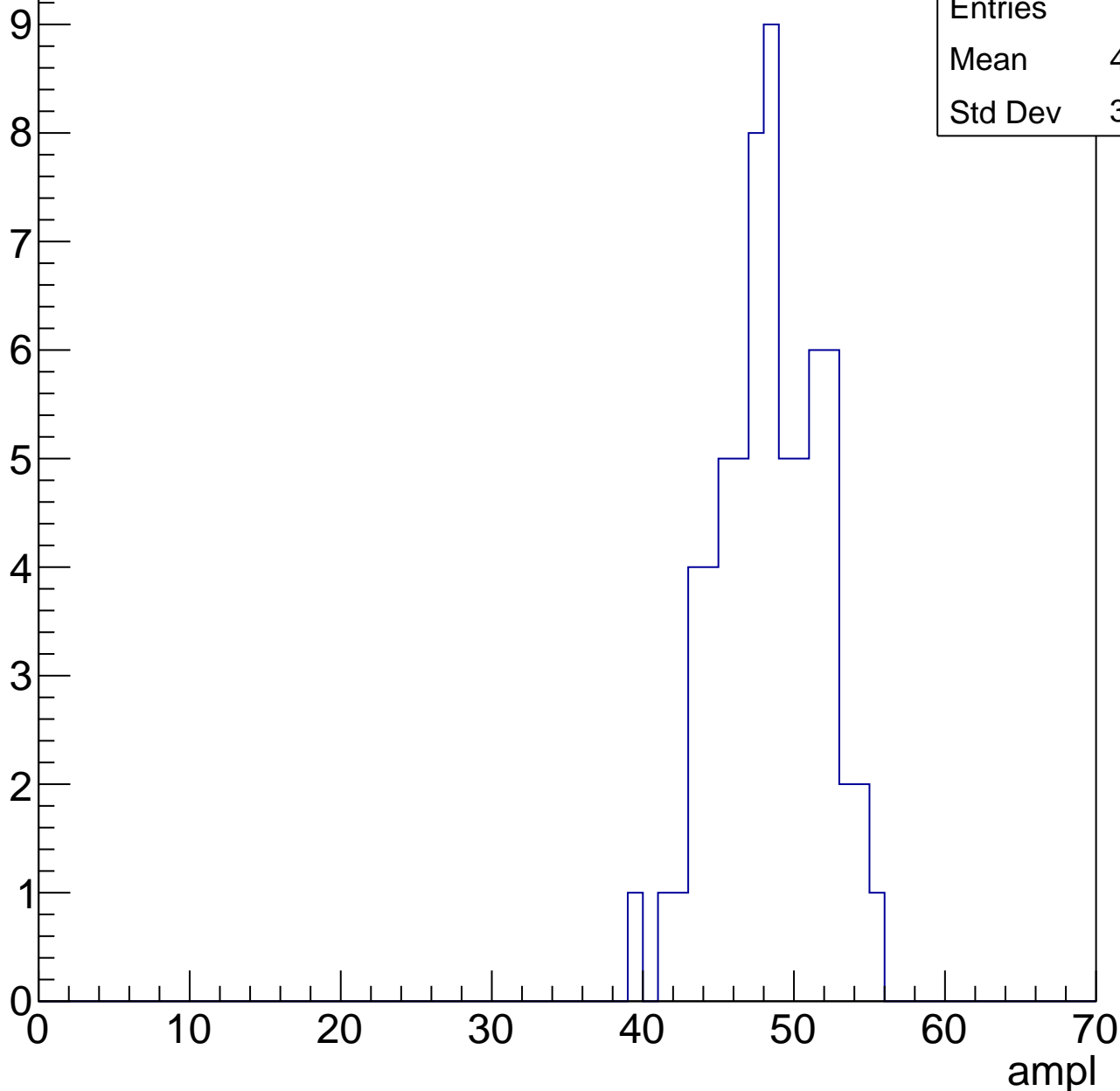
Gaus Width: 3.4535



B0L000S, U4-ch43, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



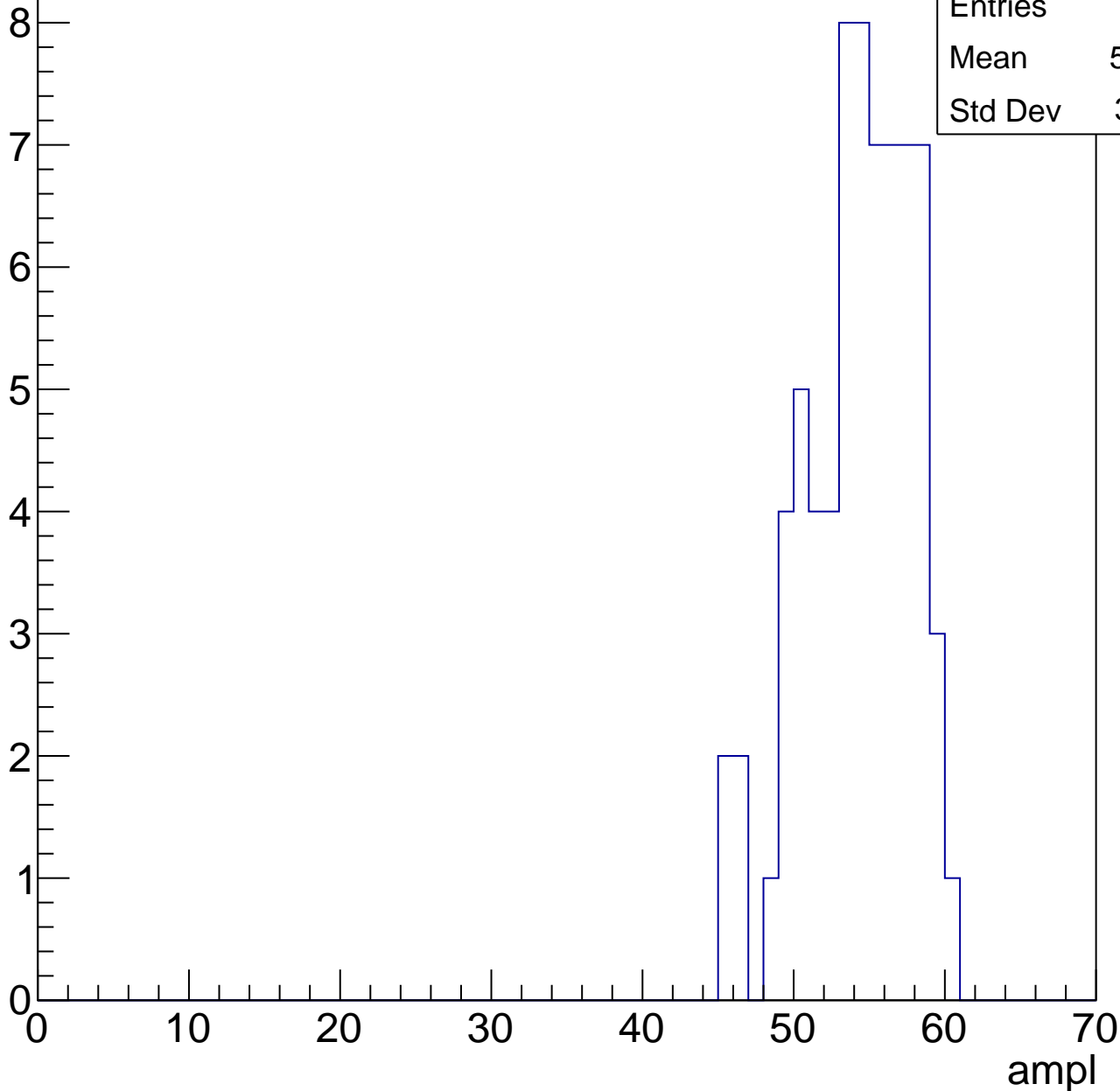
Entries	65
Mean	47.92
Std Dev	3.394

B0L000S, U4-ch43, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	53.76
Std Dev	3.551



B0L000S, U4-ch43, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.42
Std Dev	9.071

ampl

0

10

20

30

40

50

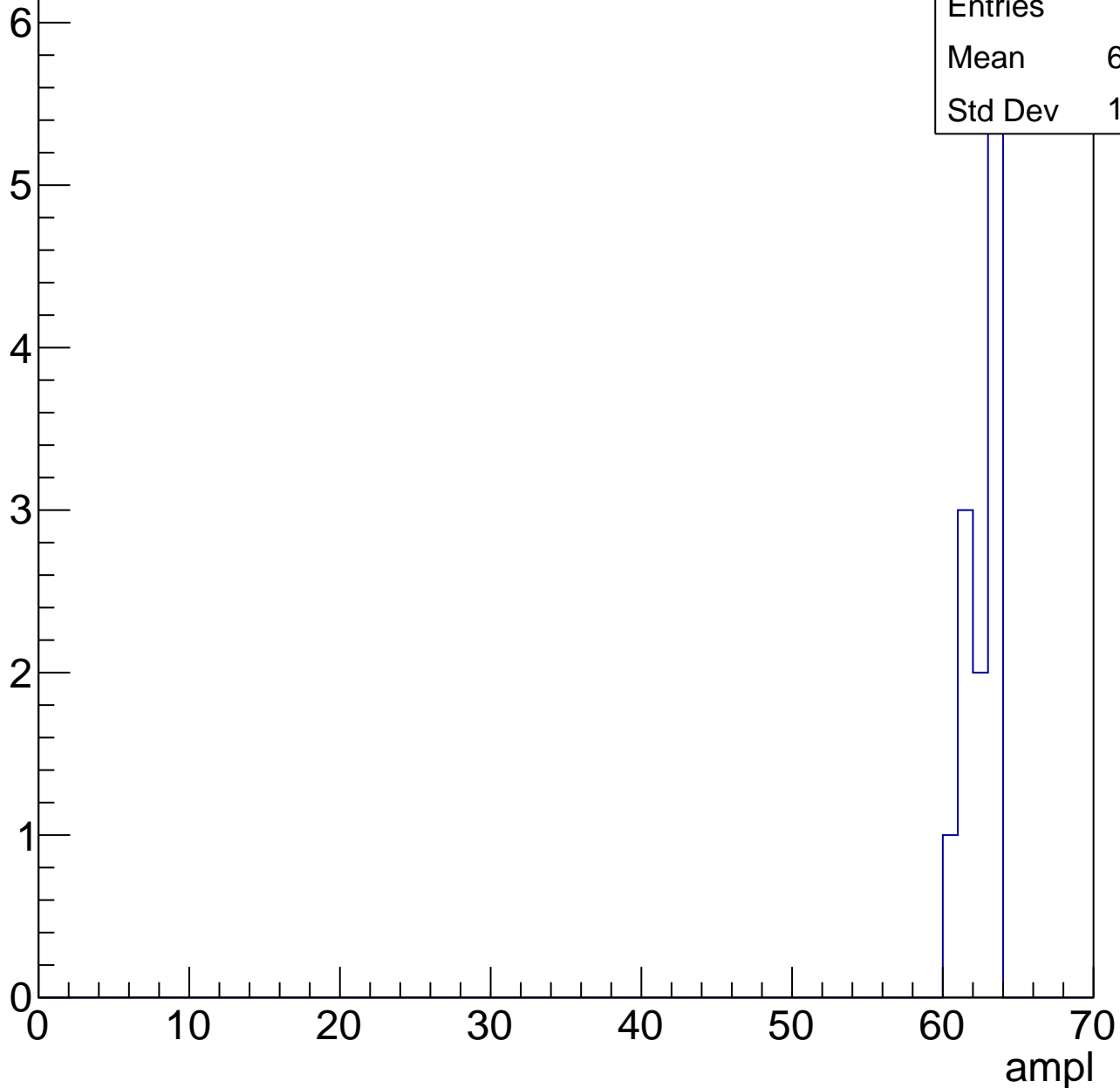
60

70

B0L000S, U4-ch43, adc6

calib_packv5_042523_0143.root, FC#5, port B1

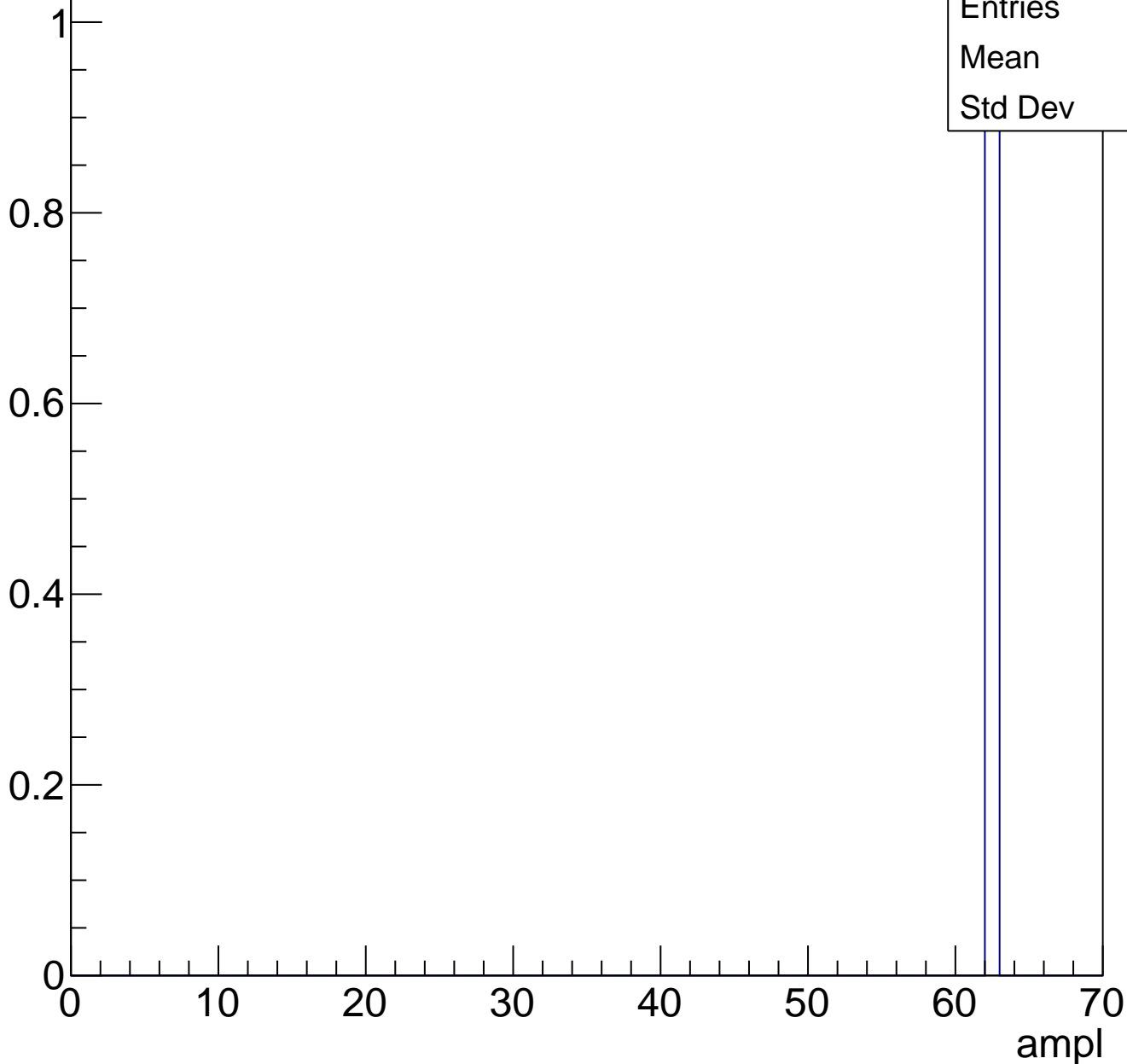
Entry



B0L000S, U4-ch43, adc7

calib_packv5_042523_0143.root, FC#5, port B1

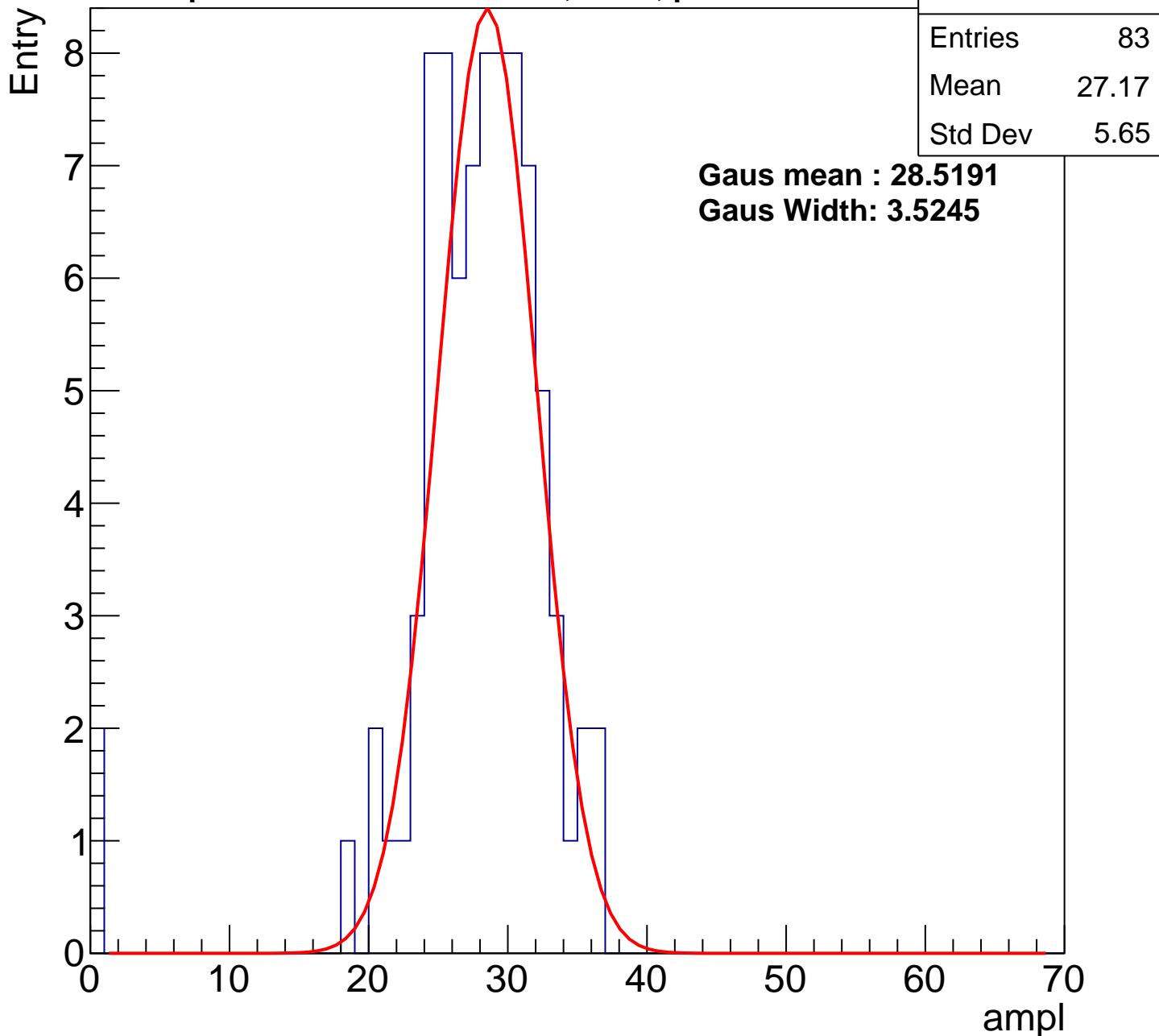
Entry



Entries	1
Mean	62
Std Dev	0

B0L000S, U4-ch44, adc0

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch44, adc1

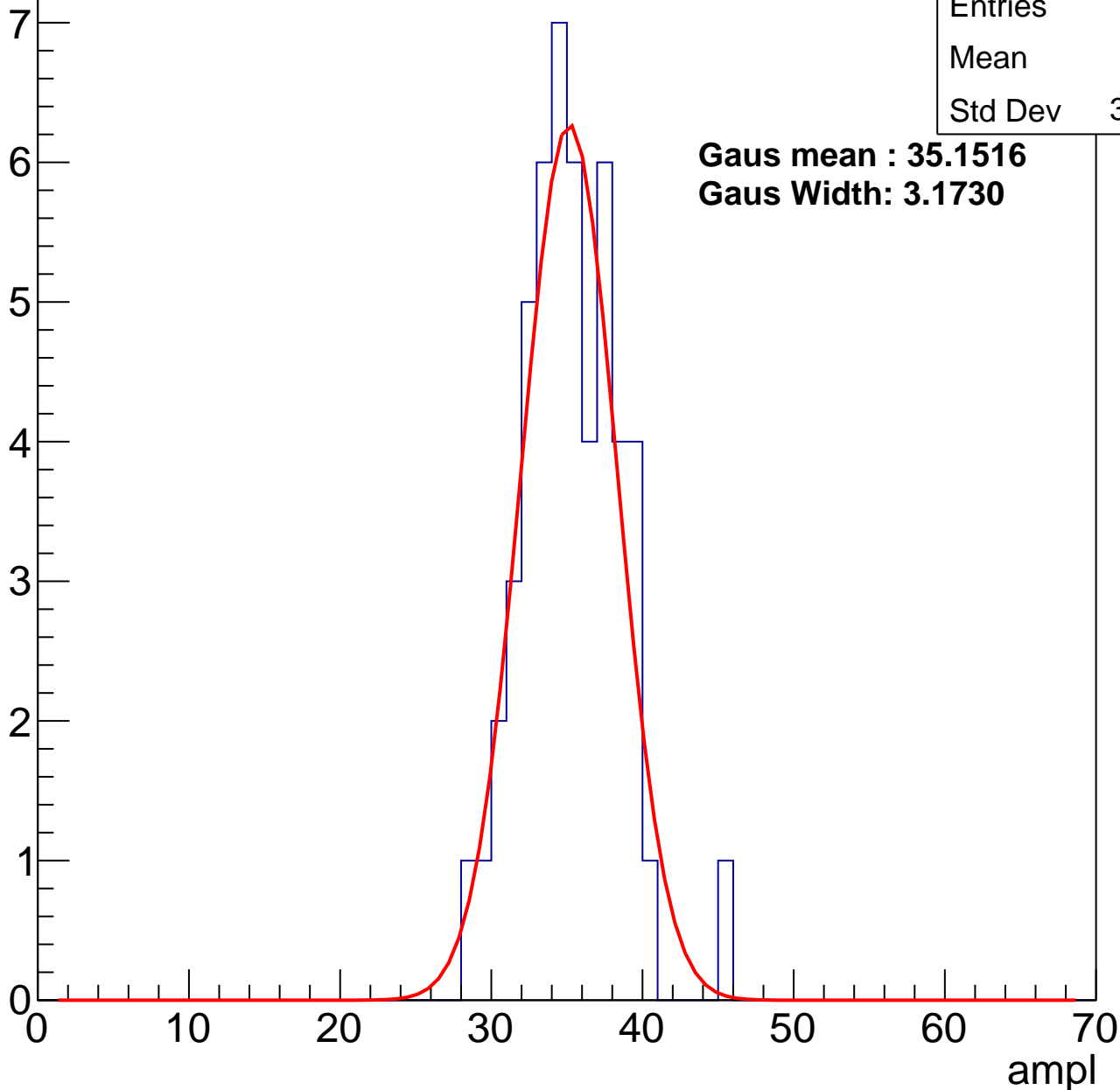
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	34.8
Std Dev	3.162

Gaus mean : 35.1516

Gaus Width: 3.1730



B0L000S, U4-ch44, adc2

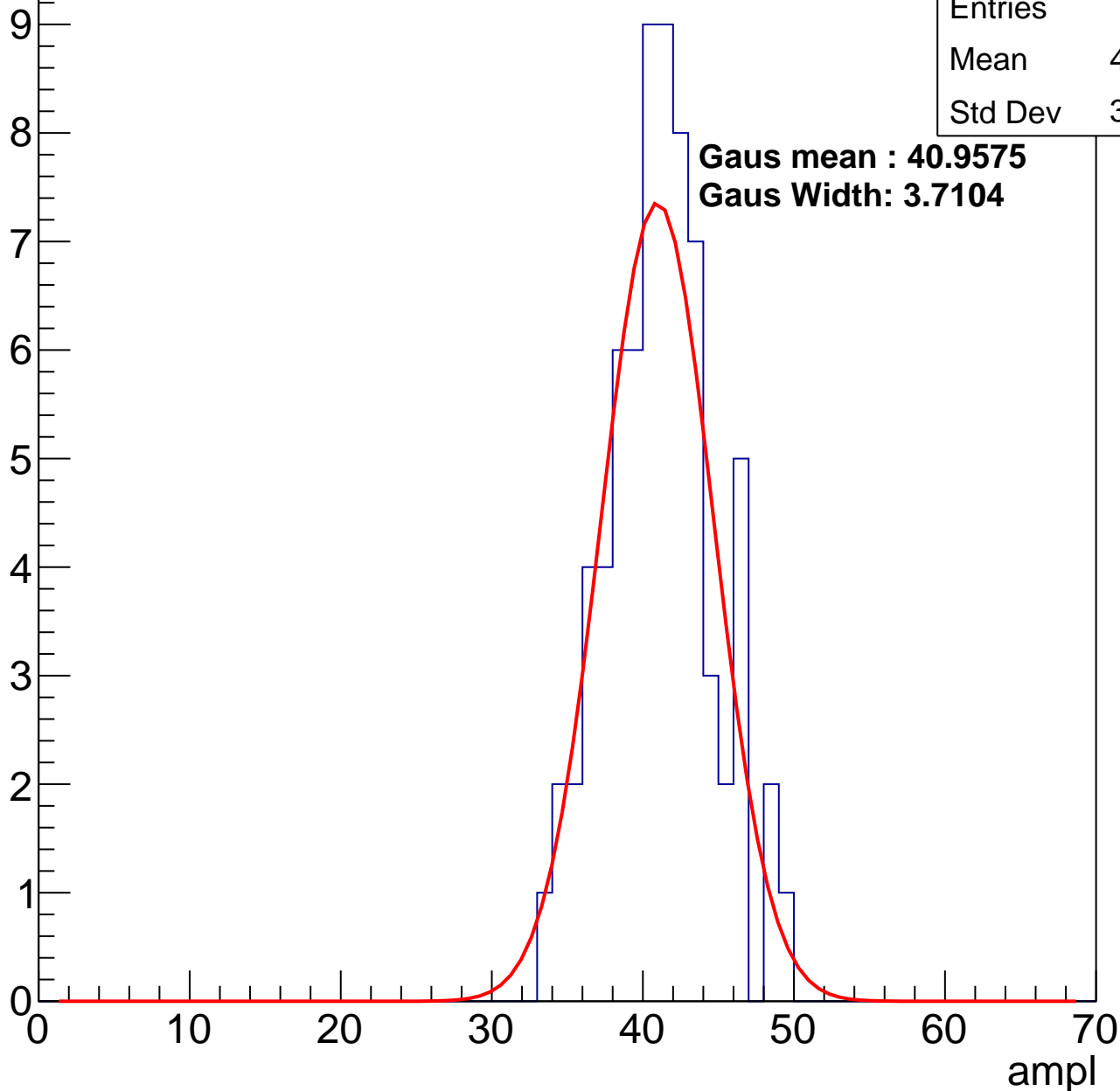
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	40.68
Std Dev	3.479

Gaus mean : 40.9575

Gaus Width: 3.7104

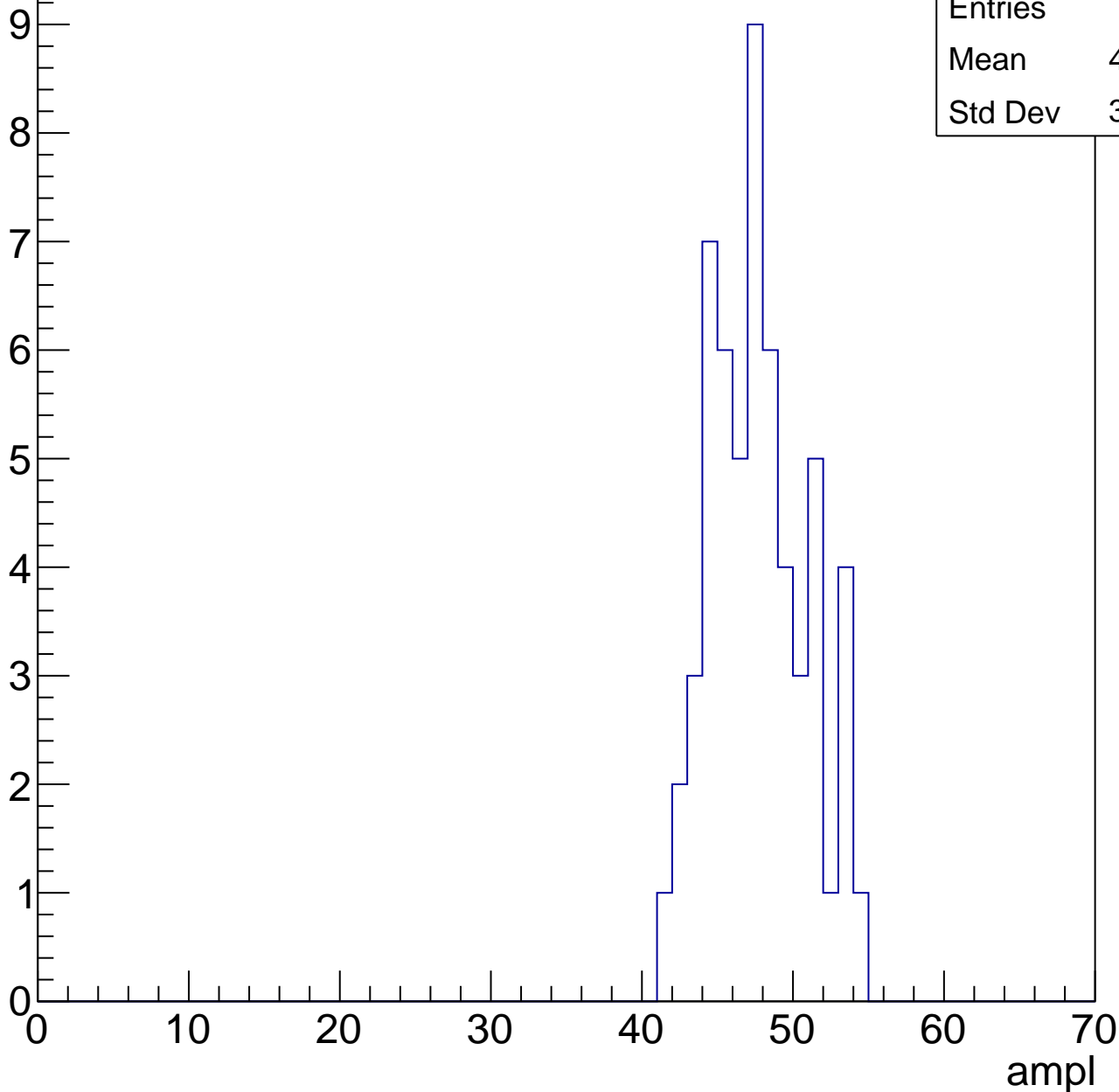


B0L000S, U4-ch44, adc3

calib_packv5_042523_0143.root, FC#5, port B1

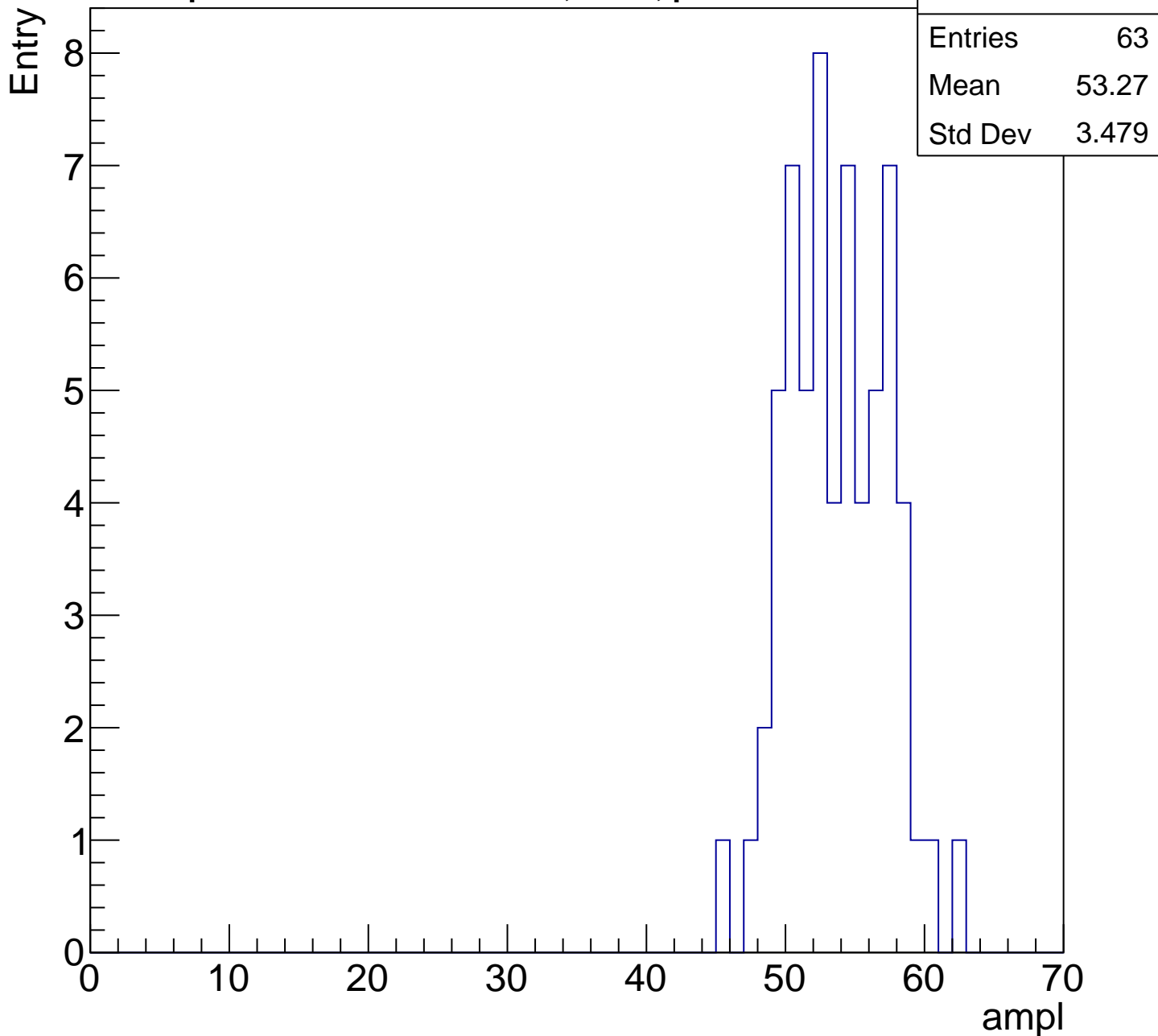
Entry

Entries	57
Mean	47.23
Std Dev	3.162



B0L000S, U4-ch44, adc4

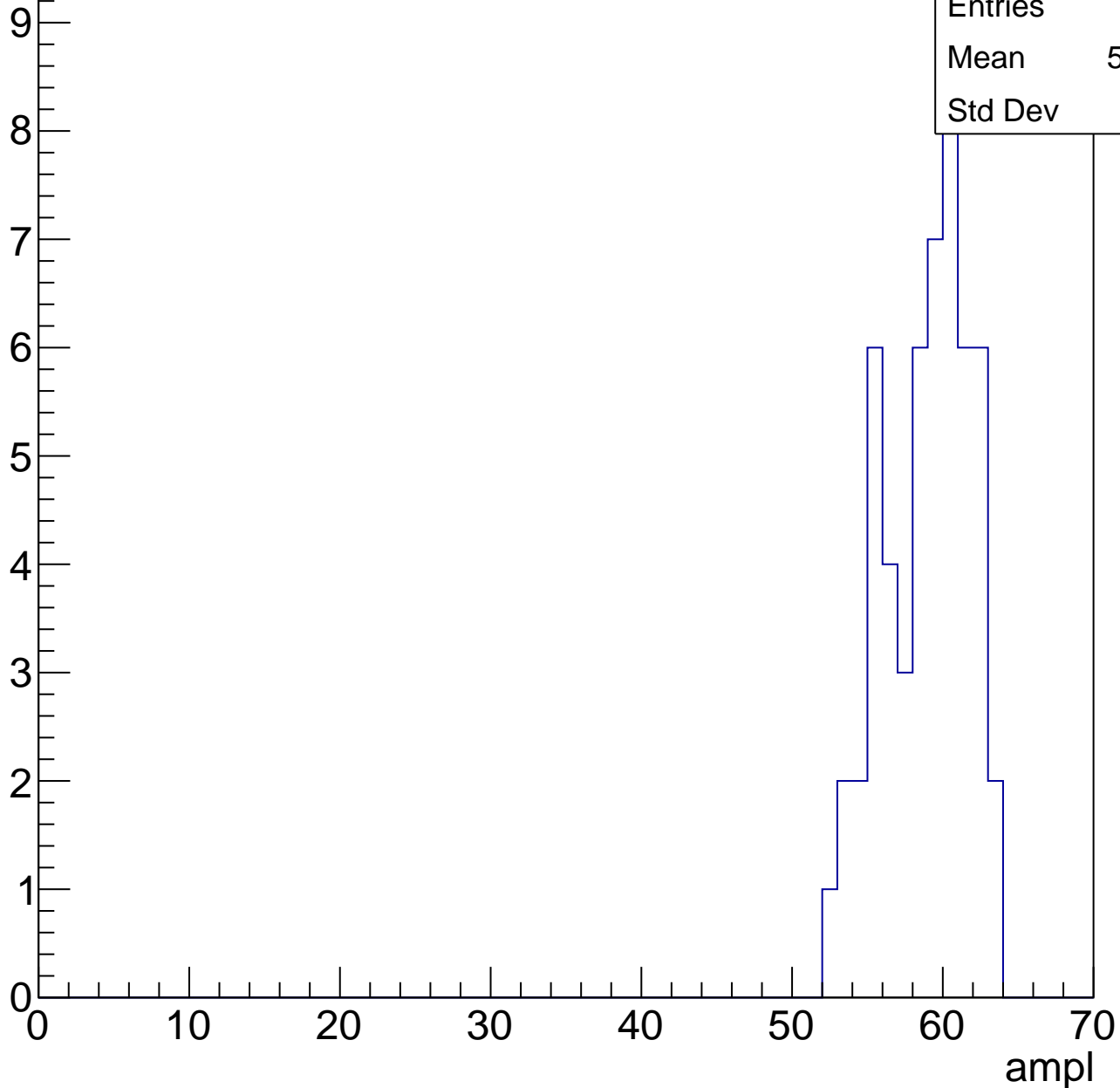
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch44, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

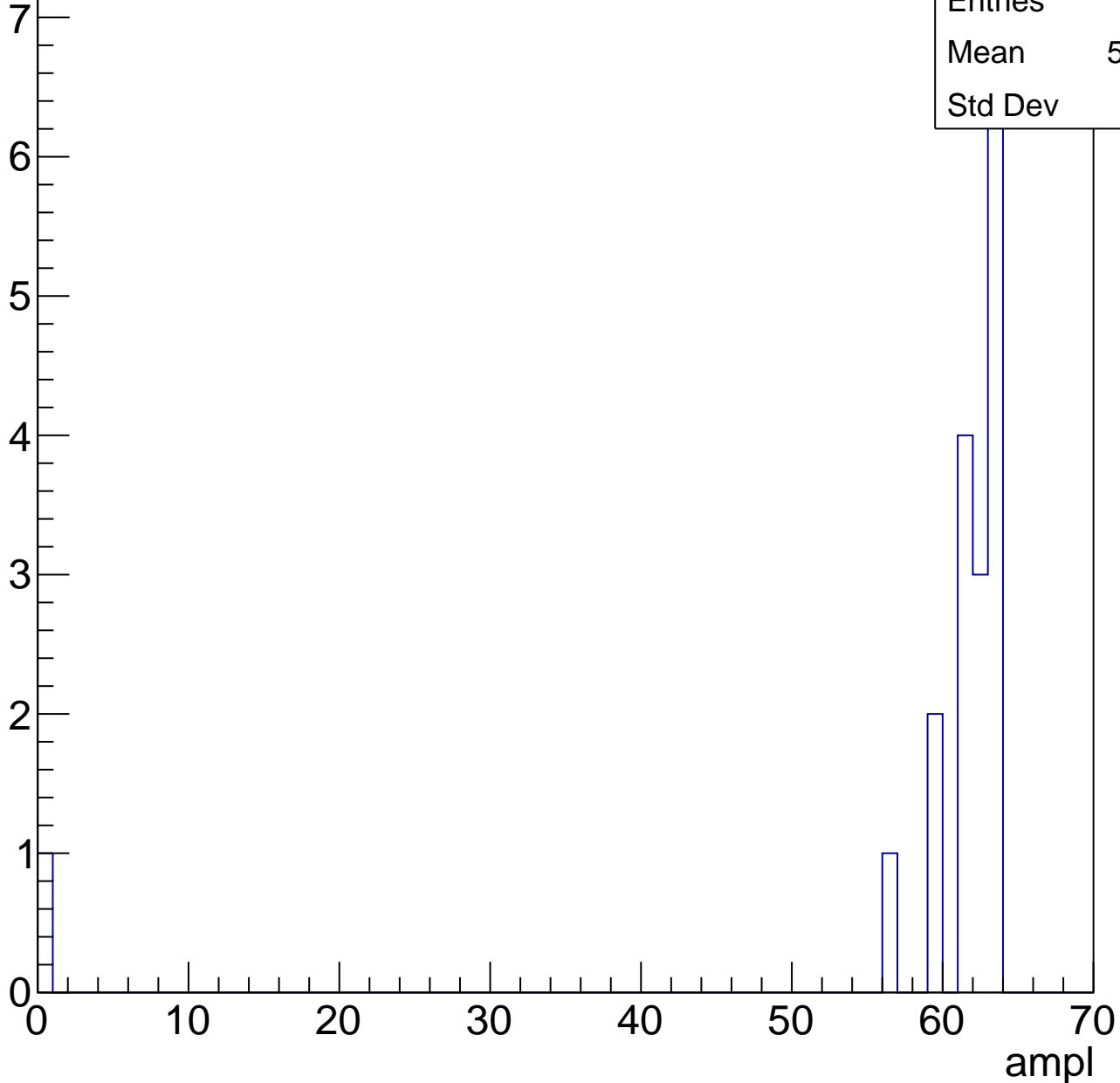


B0L000S, U4-ch44, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

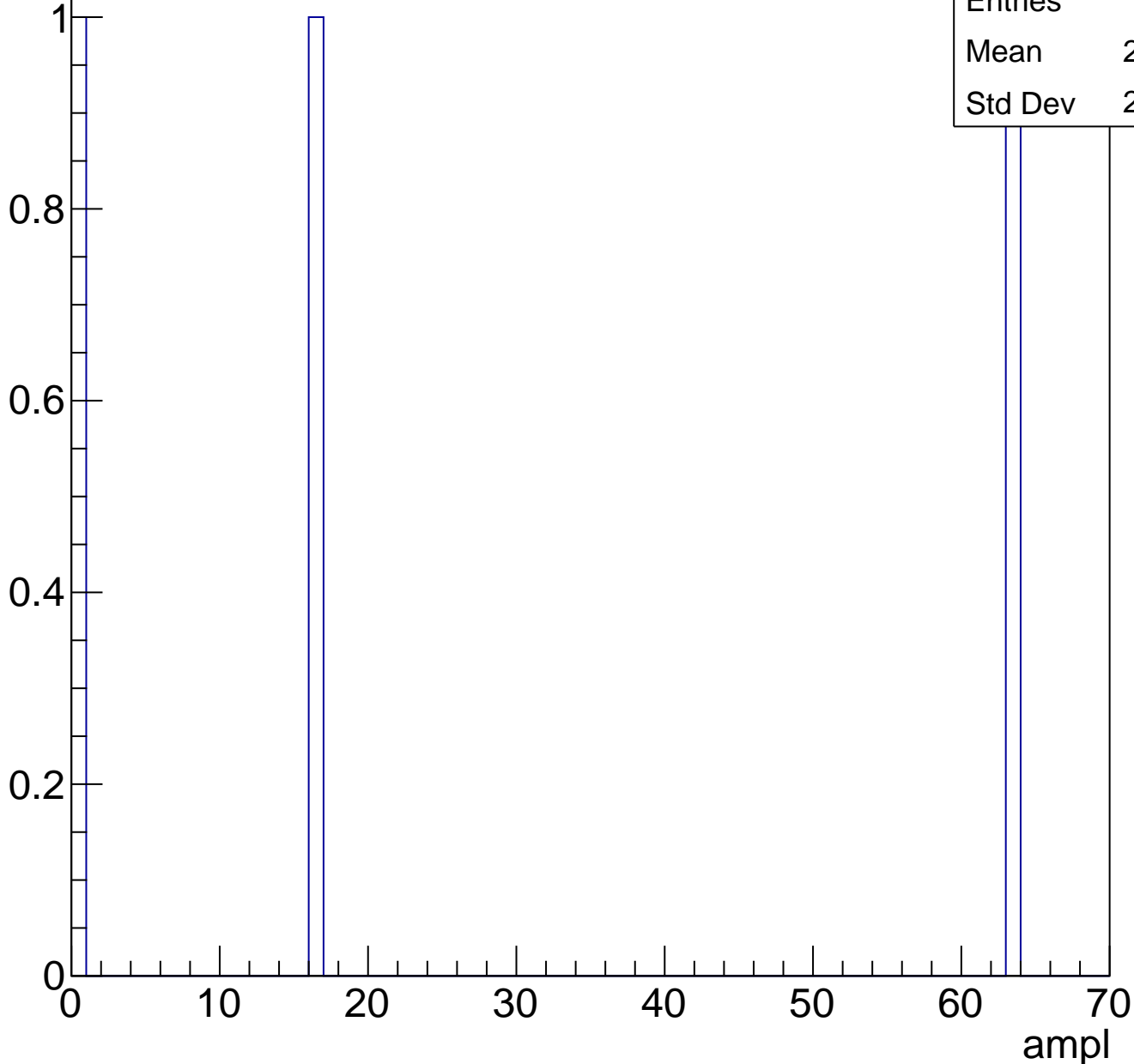
Entries	18
Mean	58.06
Std Dev	14.2



B0L000S, U4-ch44, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	26.33
Std Dev	26.74

B0L000S, U4-ch45, adc0

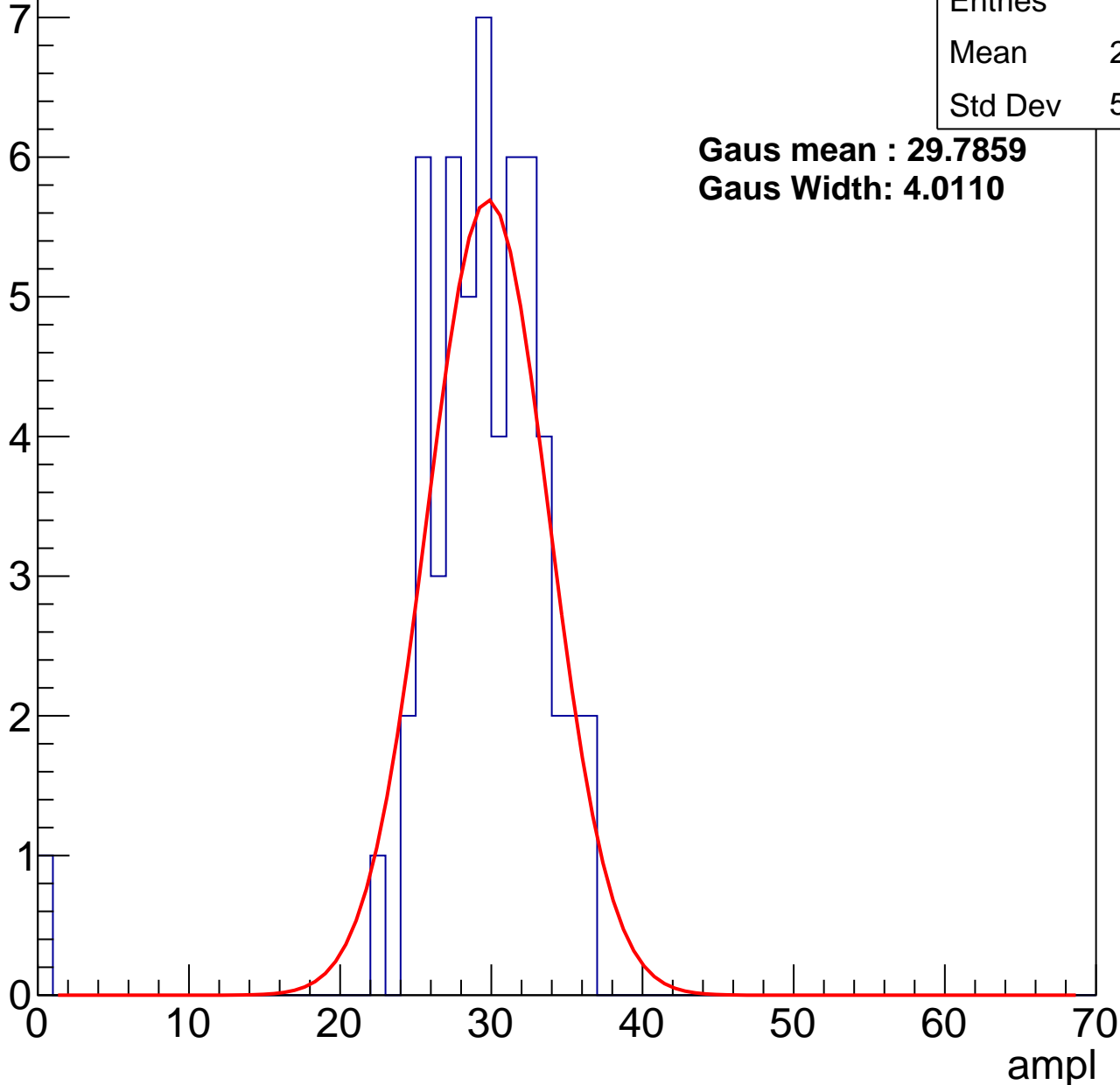
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	28.82
Std Dev	5.055

Gaus mean : 29.7859

Gaus Width: 4.0110



B0L000S, U4-ch45, adc1

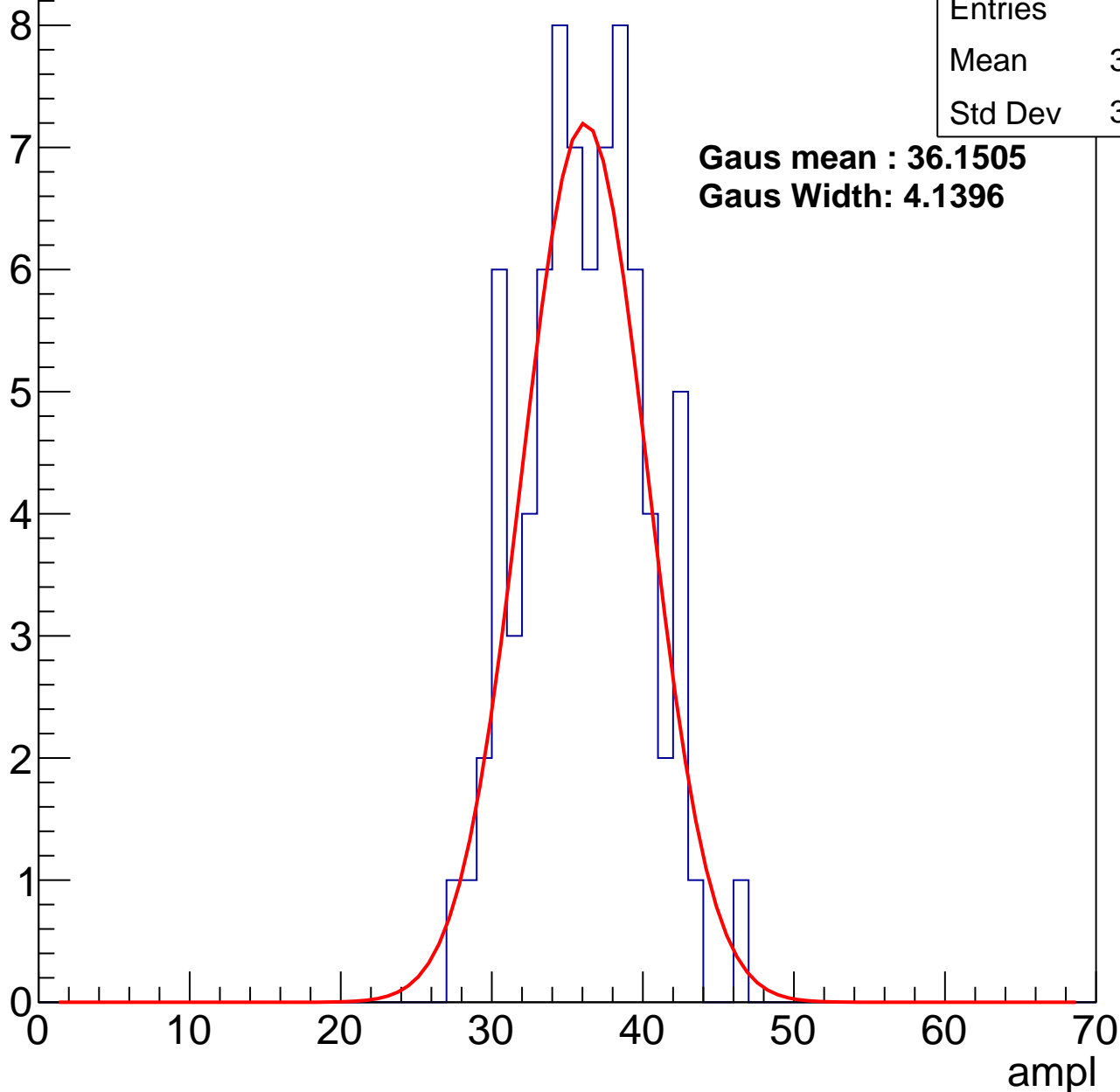
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	35.68
Std Dev	3.953

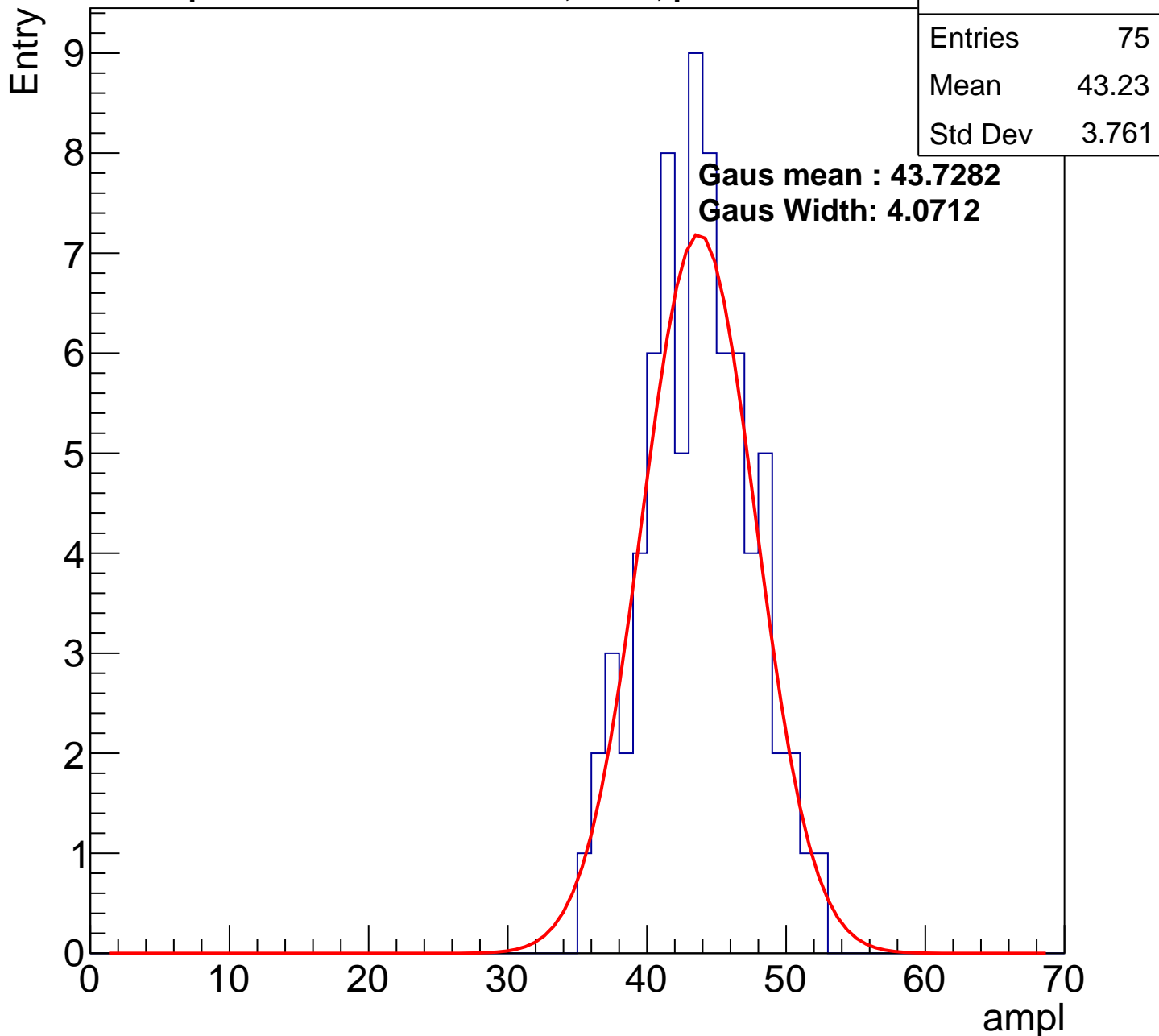
Gaus mean : 36.1505

Gaus Width: 4.1396



B0L000S, U4-ch45, adc2

calib_packv5_042523_0143.root, FC#5, port B1

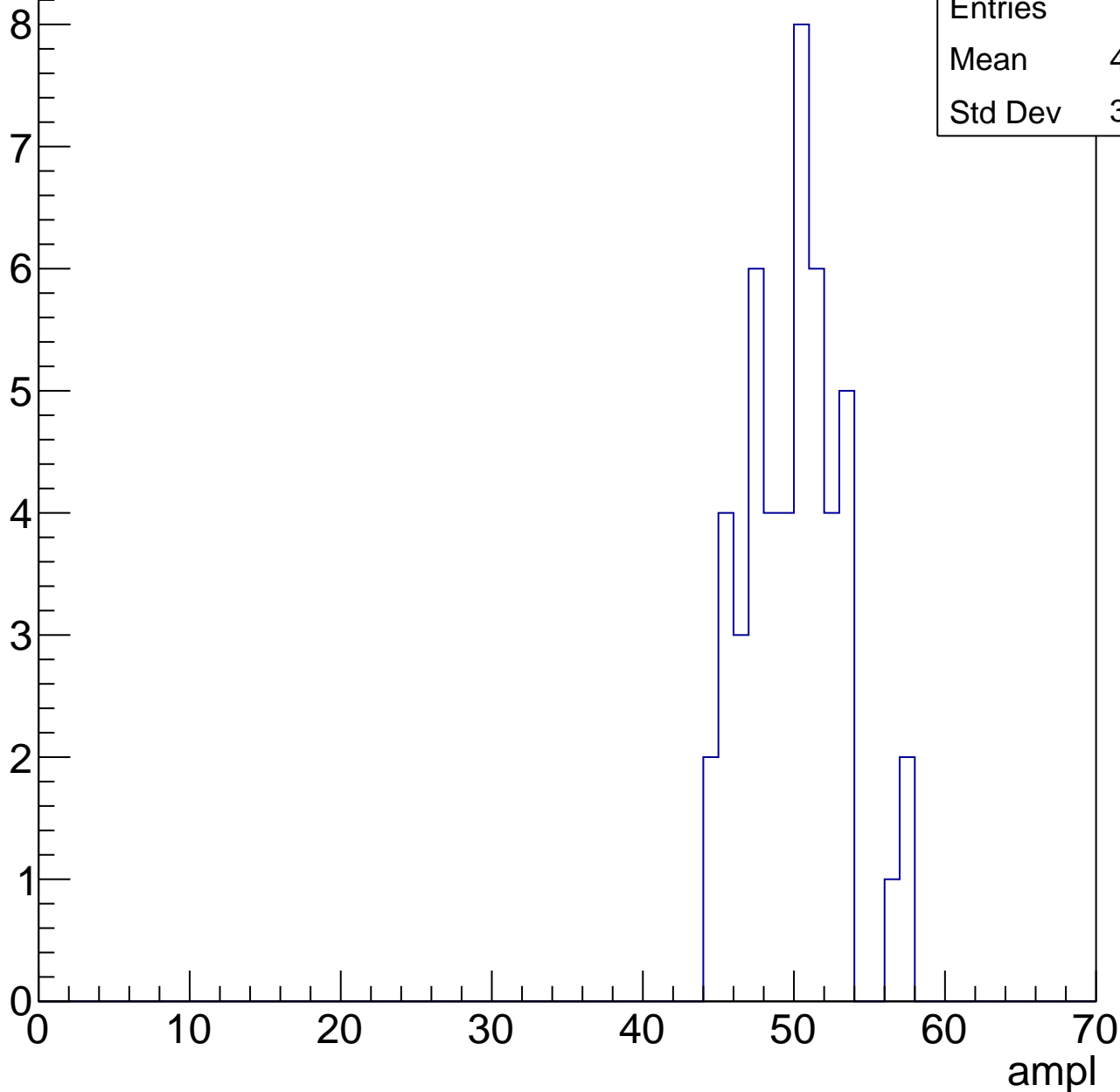


B0L000S, U4-ch45, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

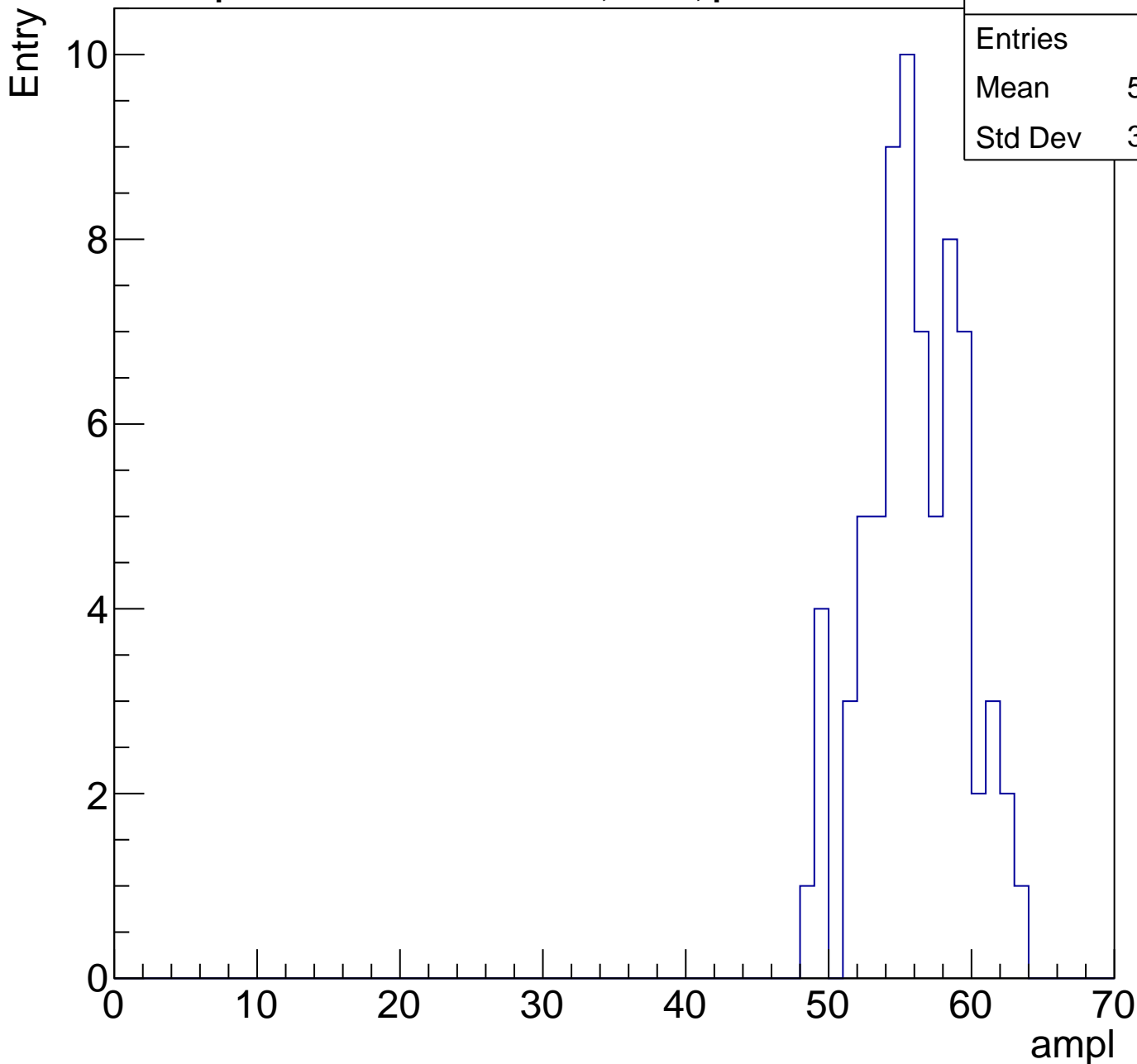
Entries	49
Mean	49.49
Std Dev	3.137



B0L000S, U4-ch45, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	72
Mean	55.58
Std Dev	3.378



B0L000S, U4-ch45, adc5

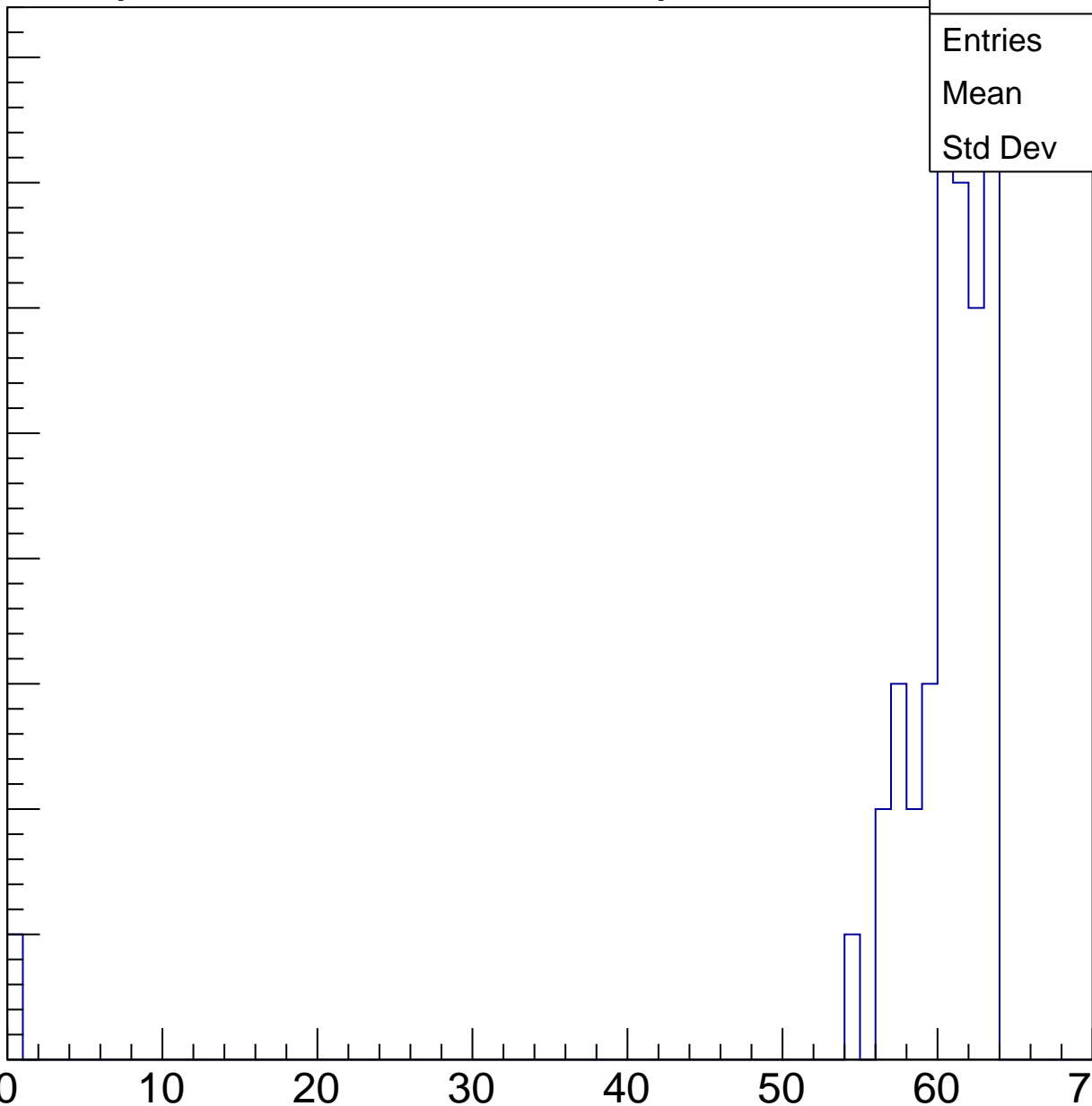
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.85
Std Dev	9.57

ampl



B0L000S, U4-ch45, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch45, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L000S, U4-ch46, adc0

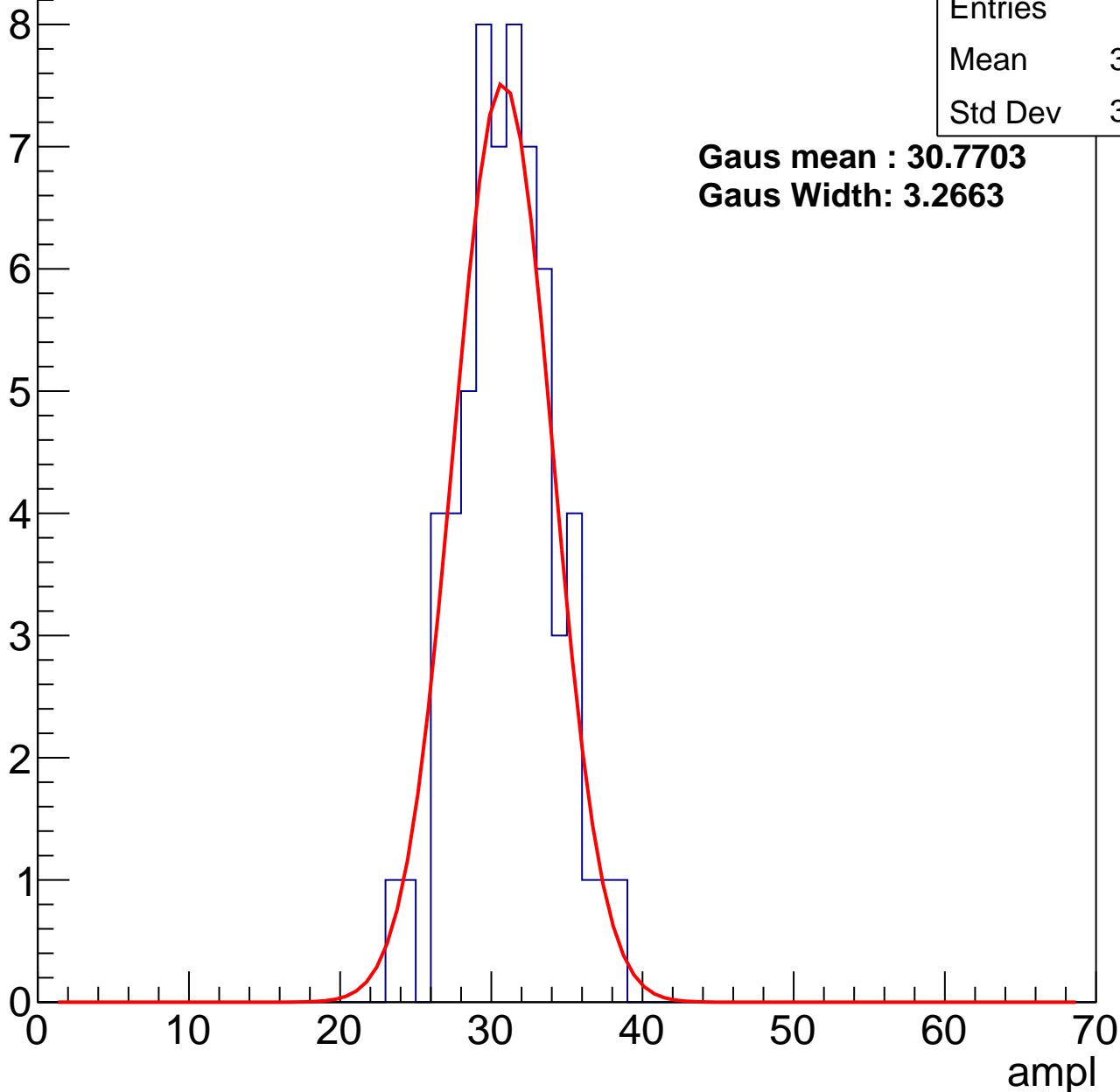
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	30.56
Std Dev	3.086

Gaus mean : 30.7703

Gaus Width: 3.2663



B0L000S, U4-ch46, adc1

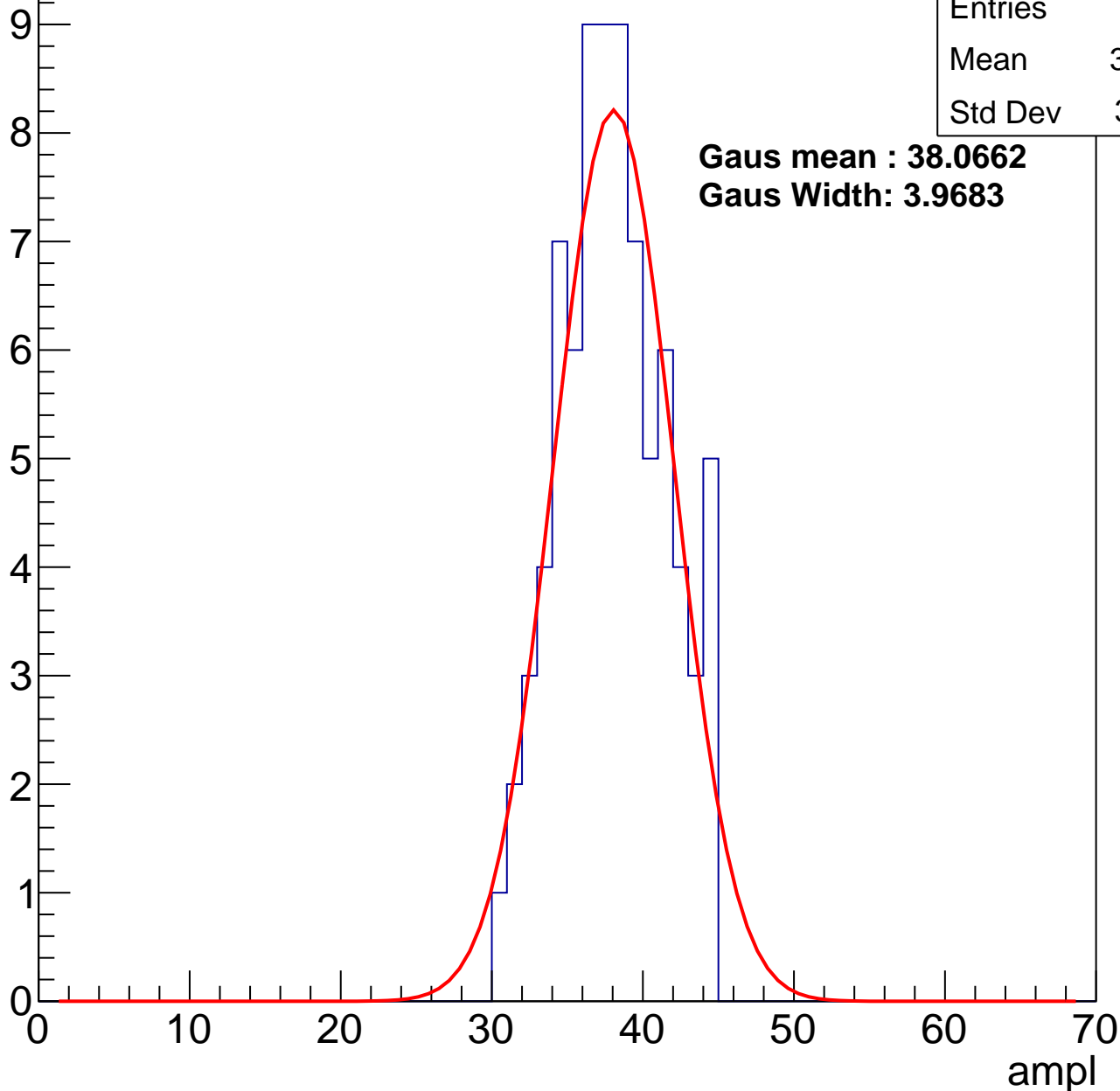
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	37.54
Std Dev	3.471

Gaus mean : 38.0662

Gaus Width: 3.9683



B0L000S, U4-ch46, adc2

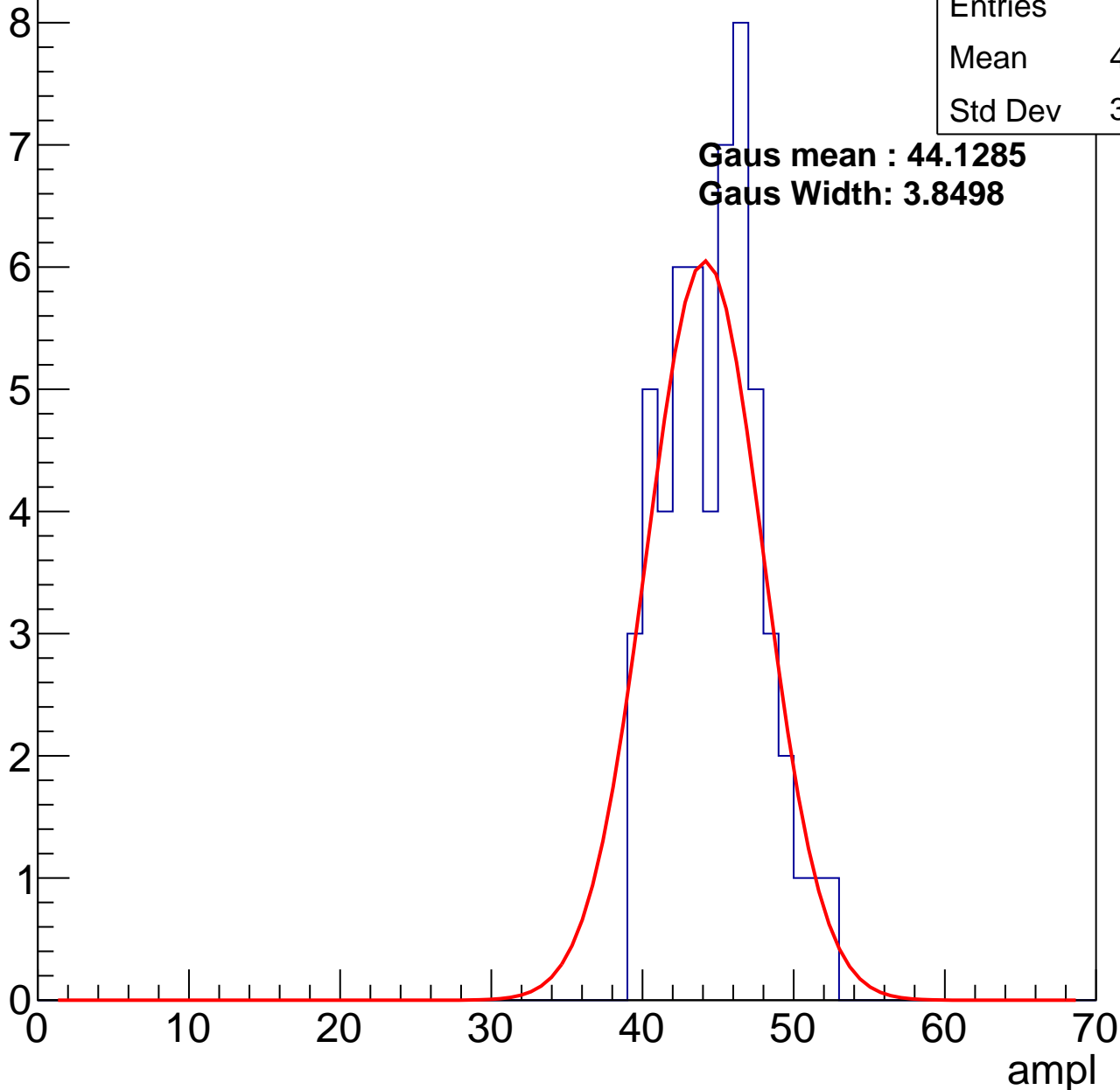
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	44.29
Std Dev	3.127

Gaus mean : 44.1285

Gaus Width: 3.8498

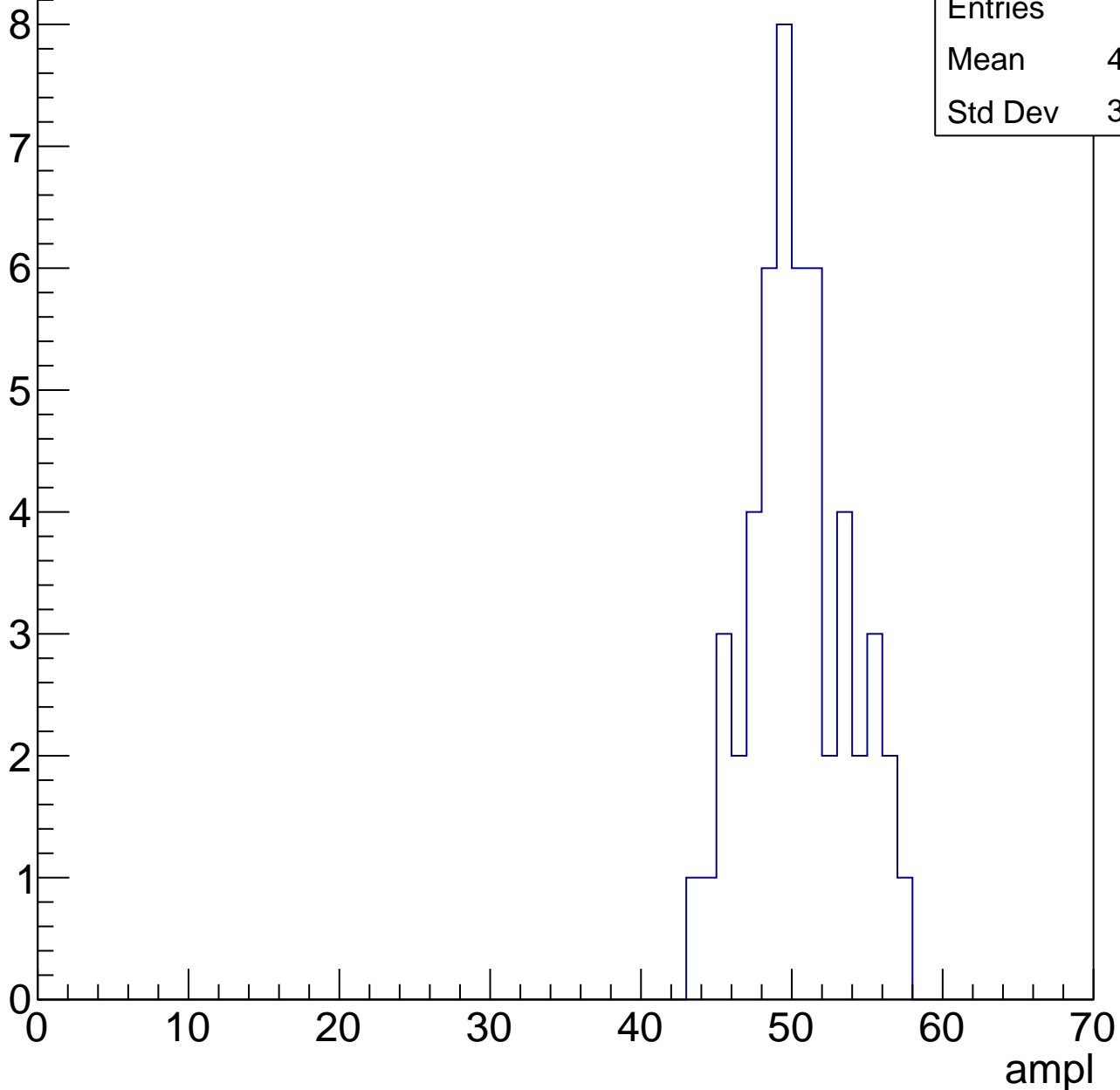


B0L000S, U4-ch46, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	49.92
Std Dev	3.247

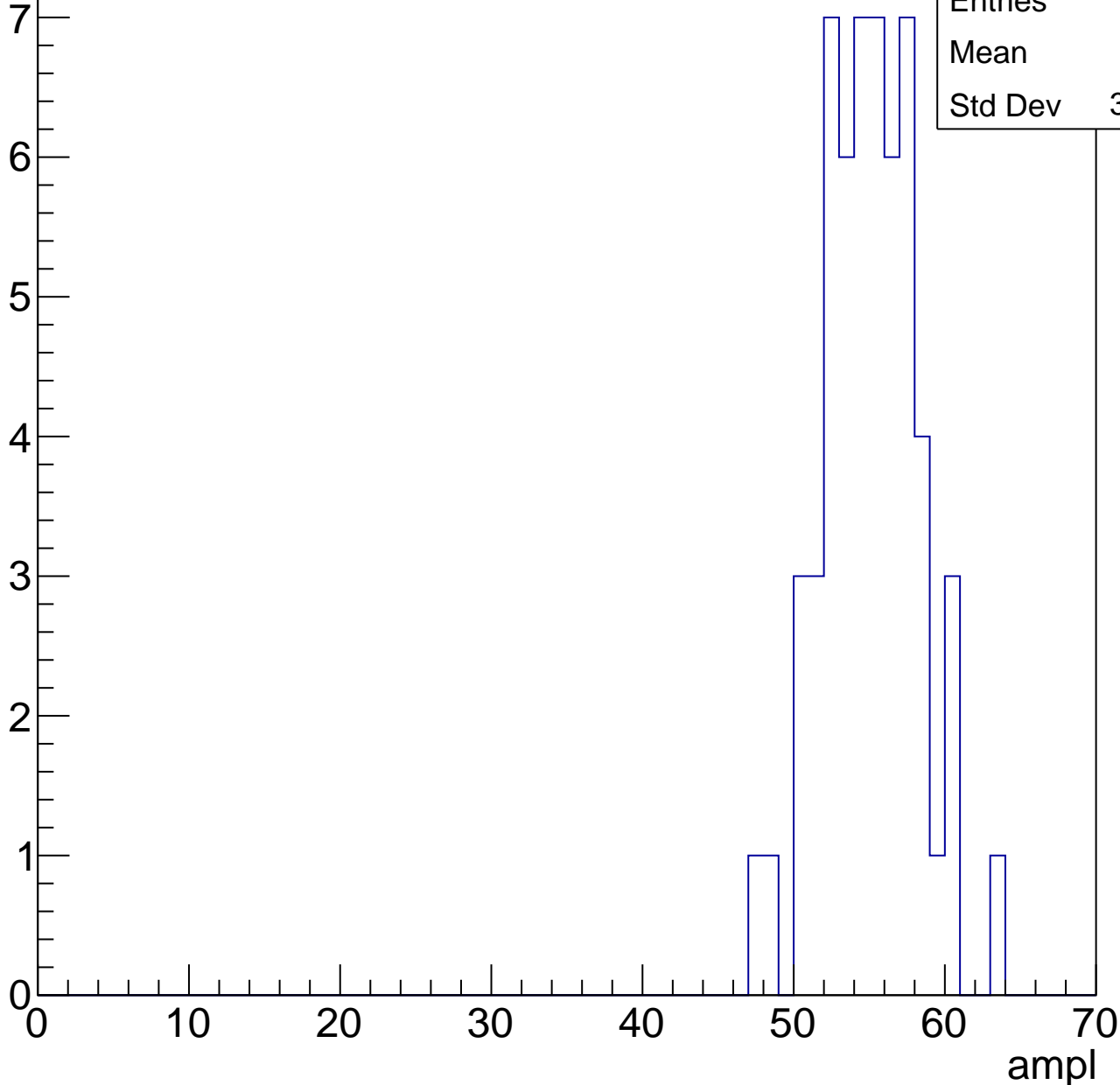


B0L000S, U4-ch46, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

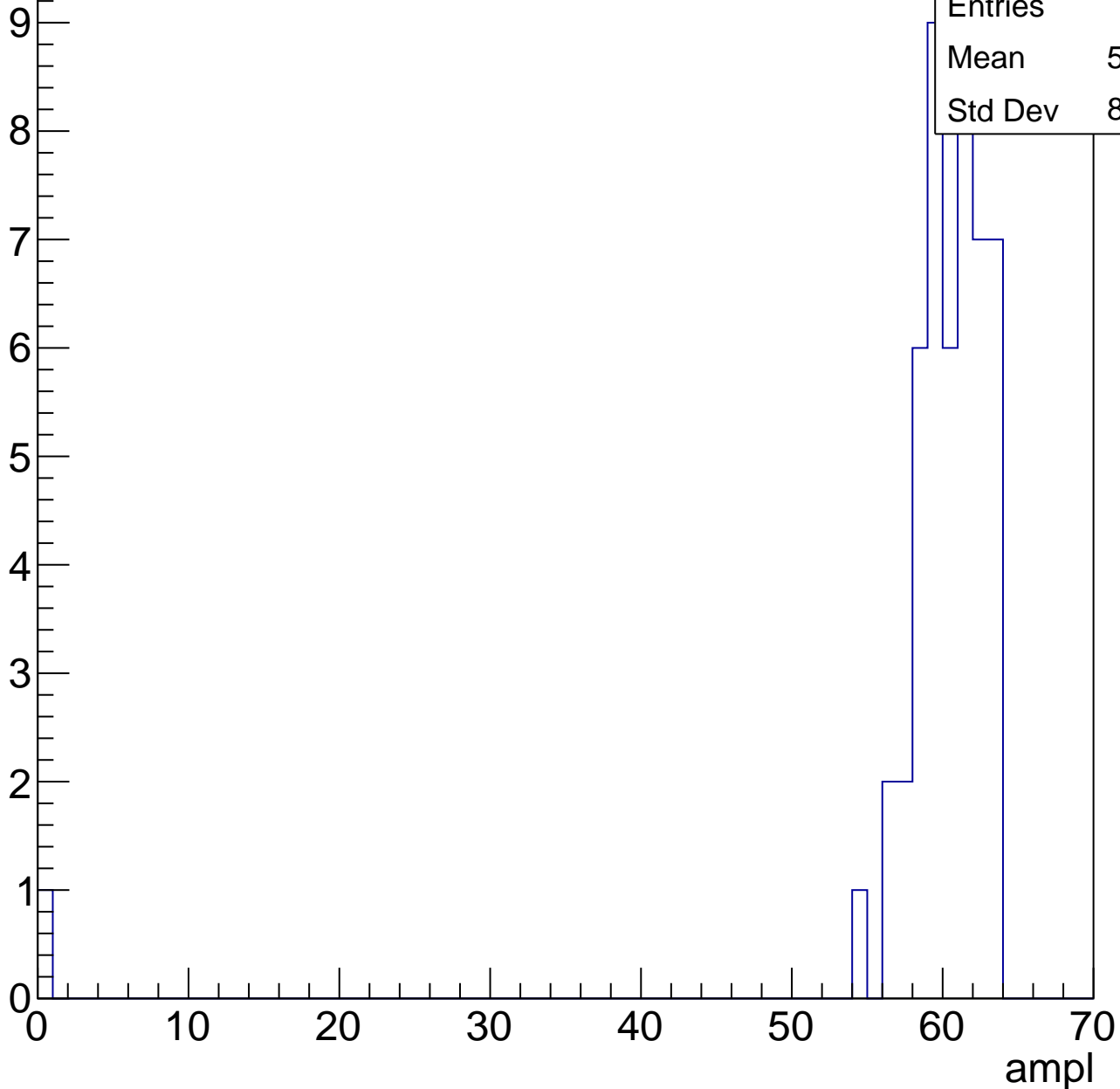
Entries	57
Mean	54.6
Std Dev	3.094



B0L000S, U4-ch46, adc5

calib_packv5_042523_0143.root, FC#5, port B1

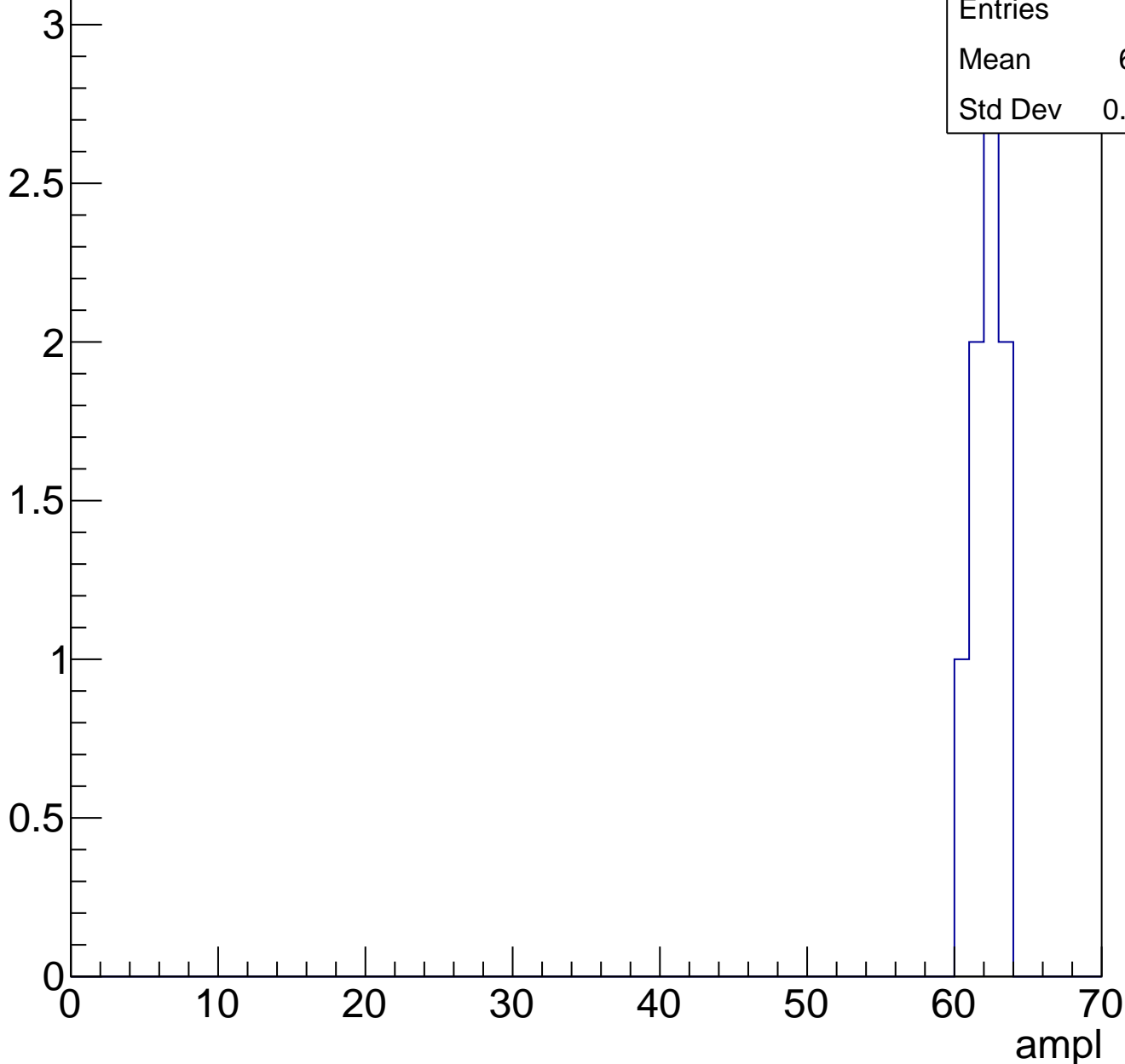
Entry



B0L000S, U4-ch46, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch46, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch47, adc0

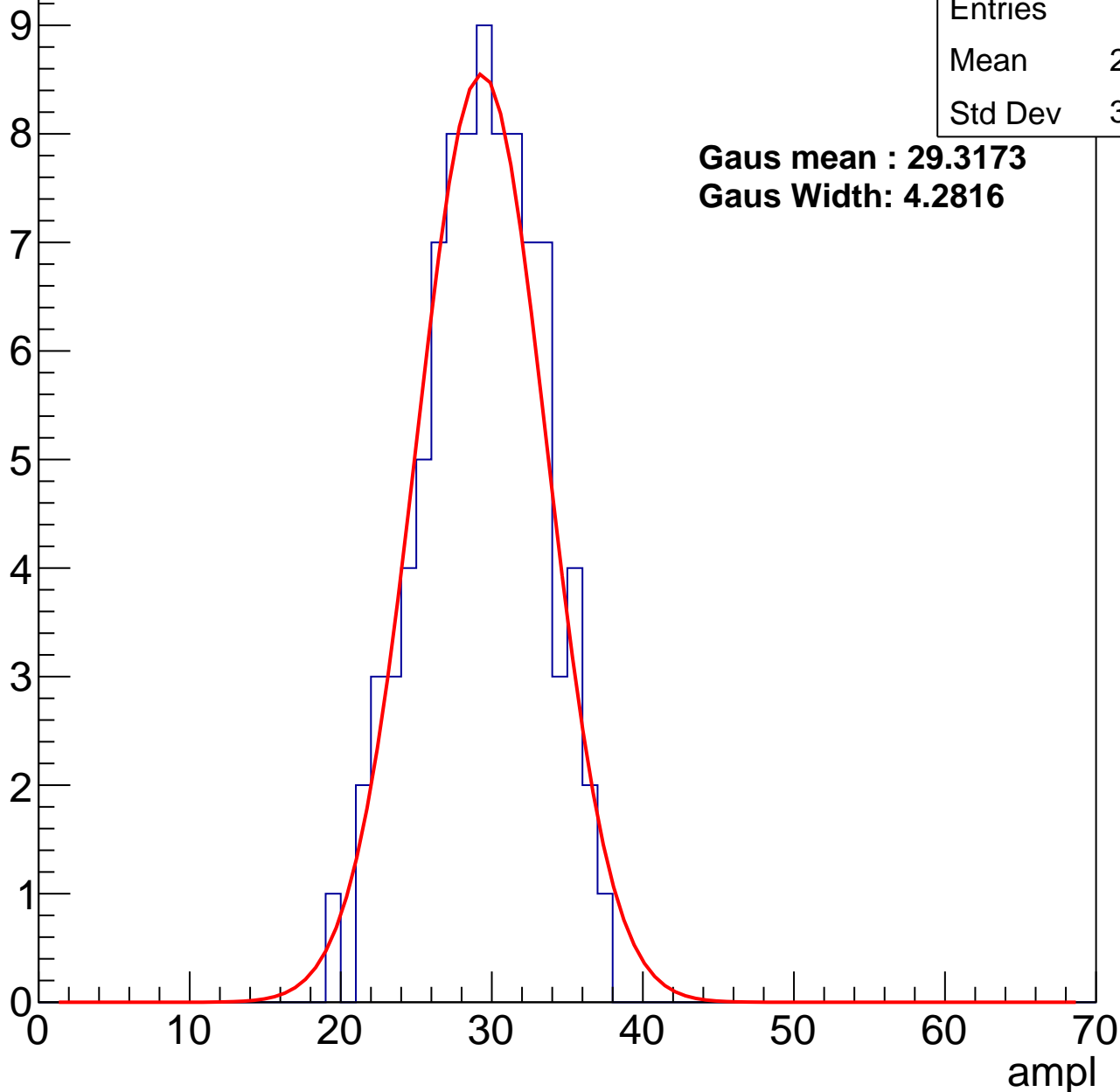
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	90
Mean	28.82
Std Dev	3.886

Gaus mean : 29.3173

Gaus Width: 4.2816



B0L000S, U4-ch47, adc1

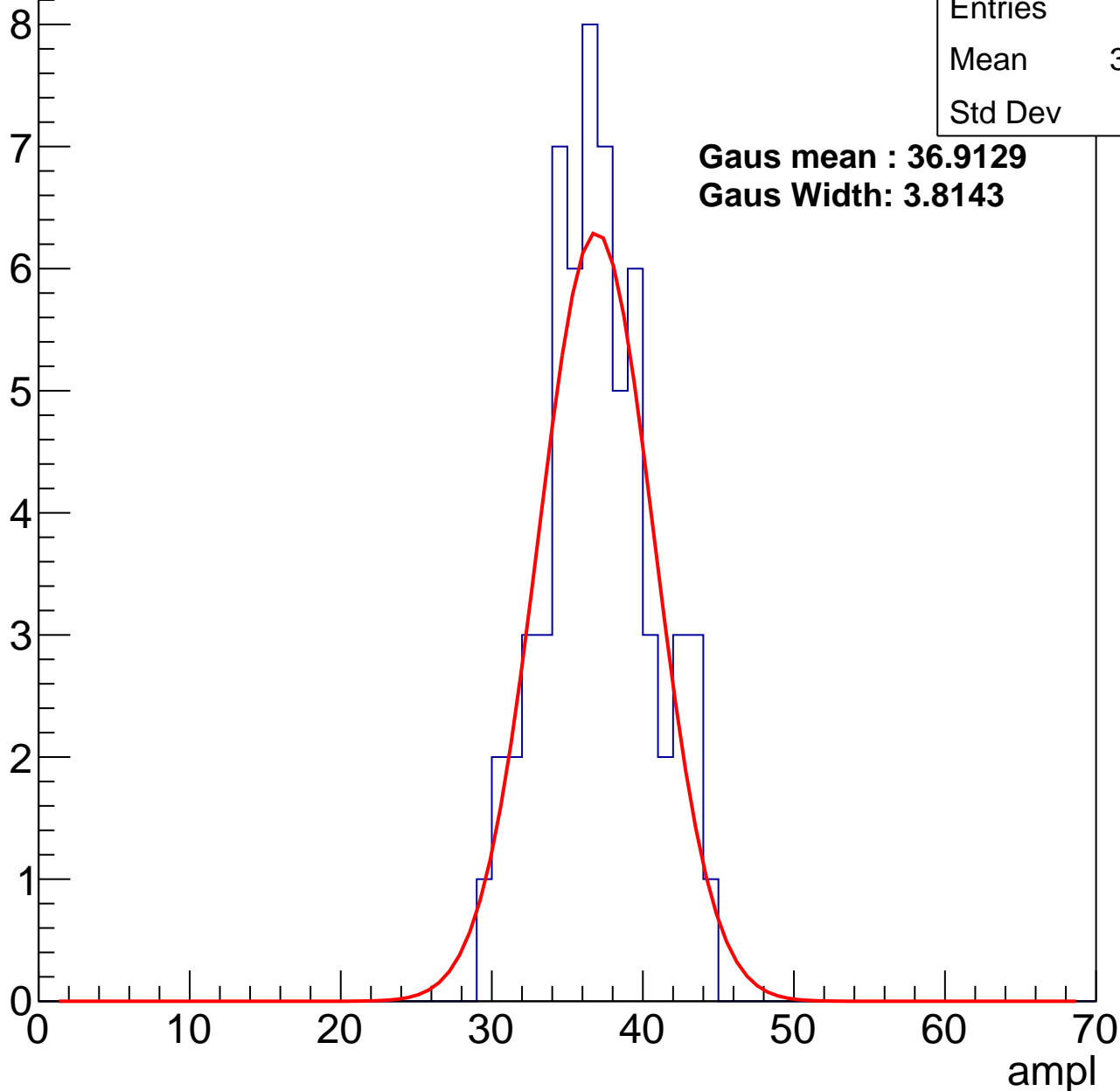
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.55
Std Dev	3.5

Gaus mean : 36.9129

Gaus Width: 3.8143



B0L000S, U4-ch47, adc2

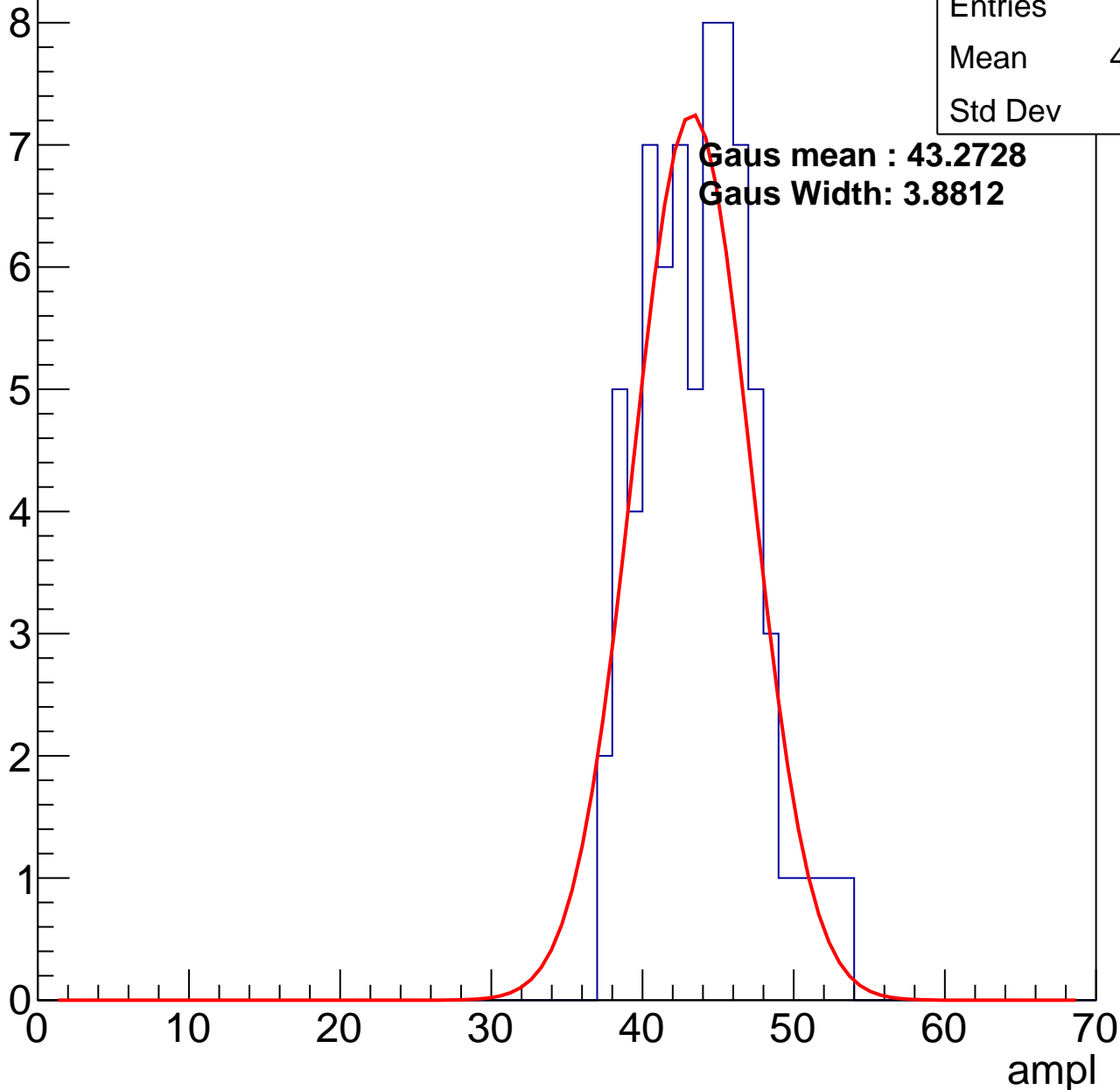
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	43.38
Std Dev	3.6

Gaus mean : 43.2728

Gaus Width: 3.8812

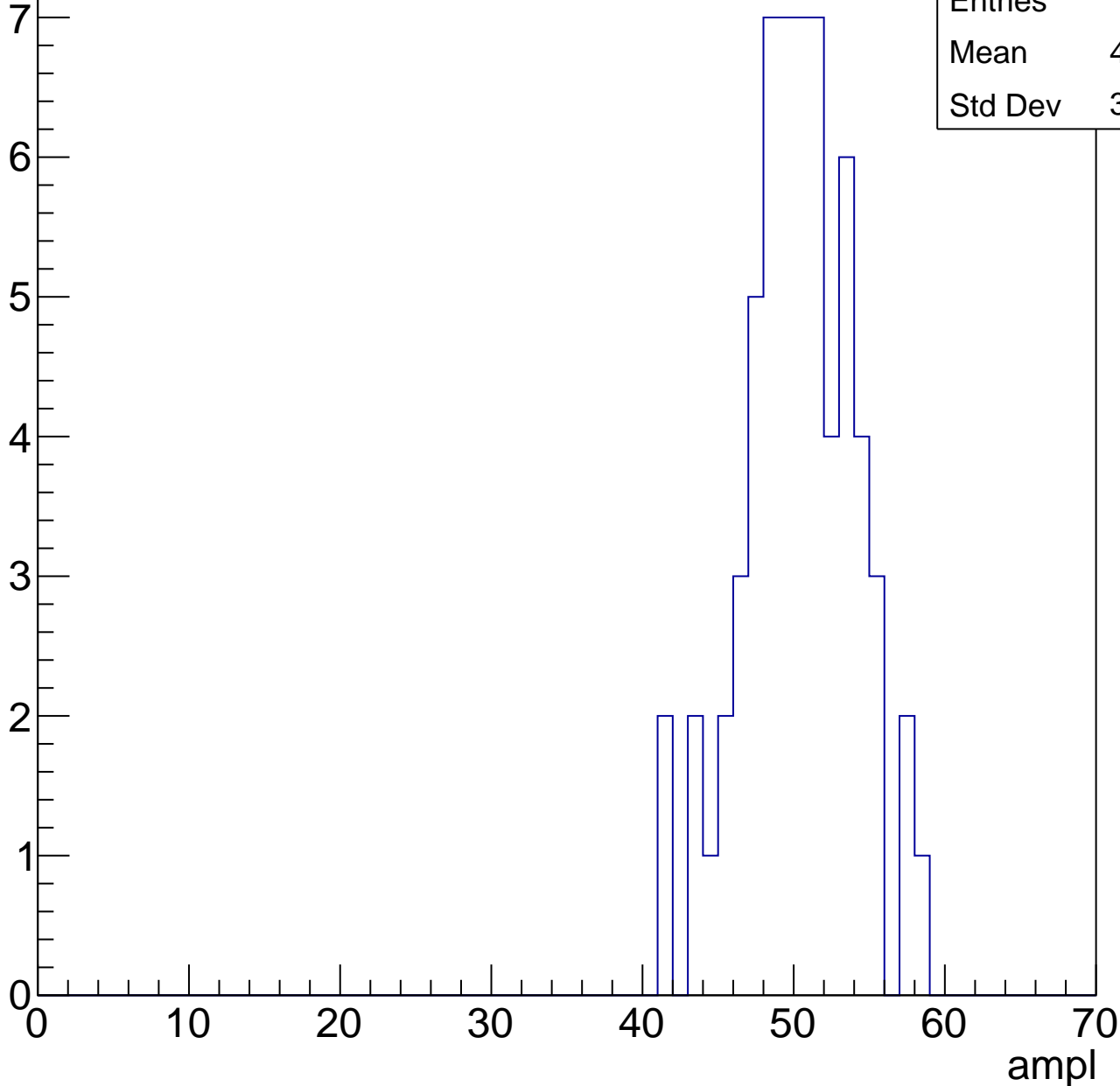


B0L000S, U4-ch47, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

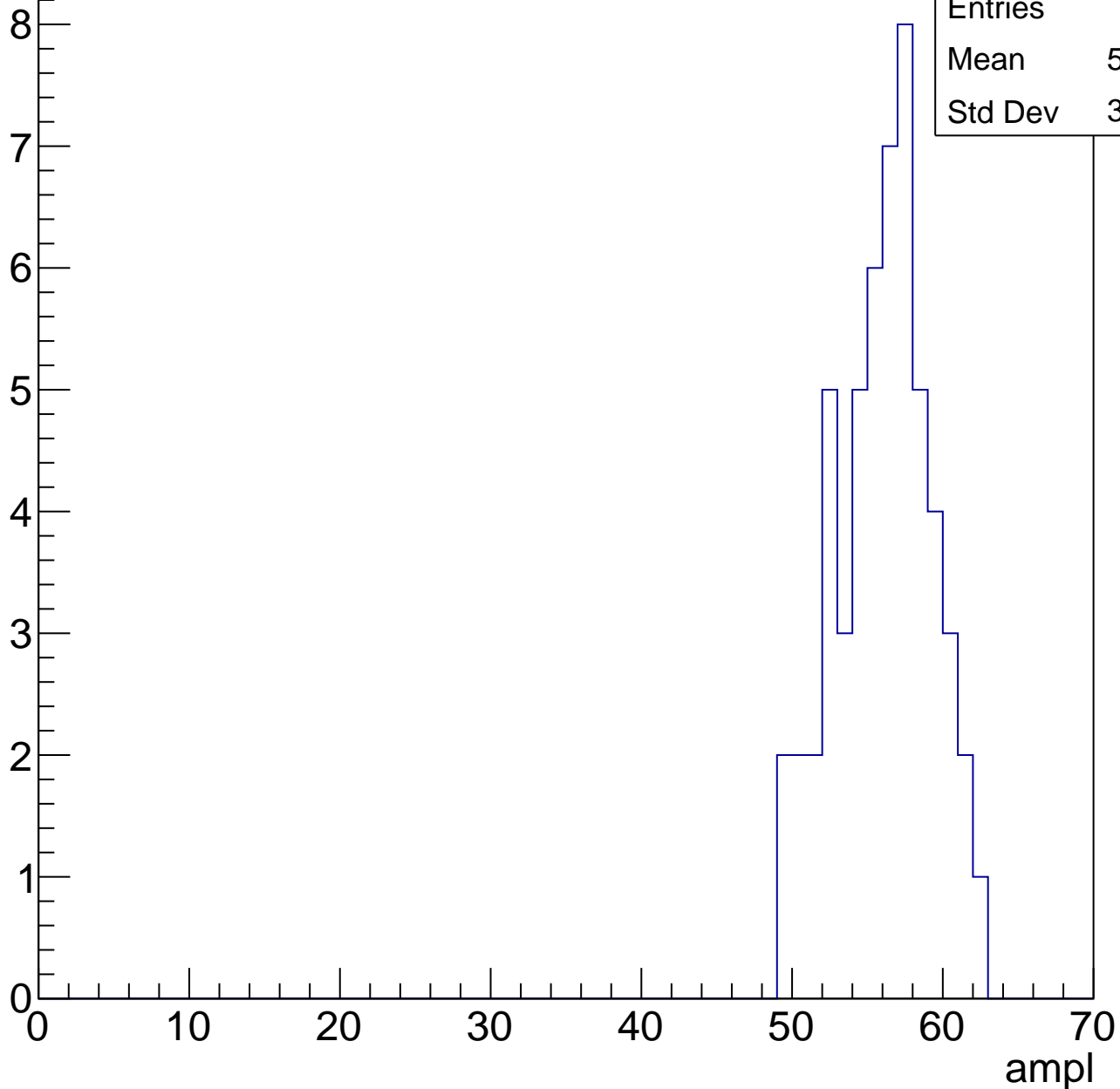
Entries	63
Mean	49.84
Std Dev	3.674



B0L000S, U4-ch47, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch47, adc5

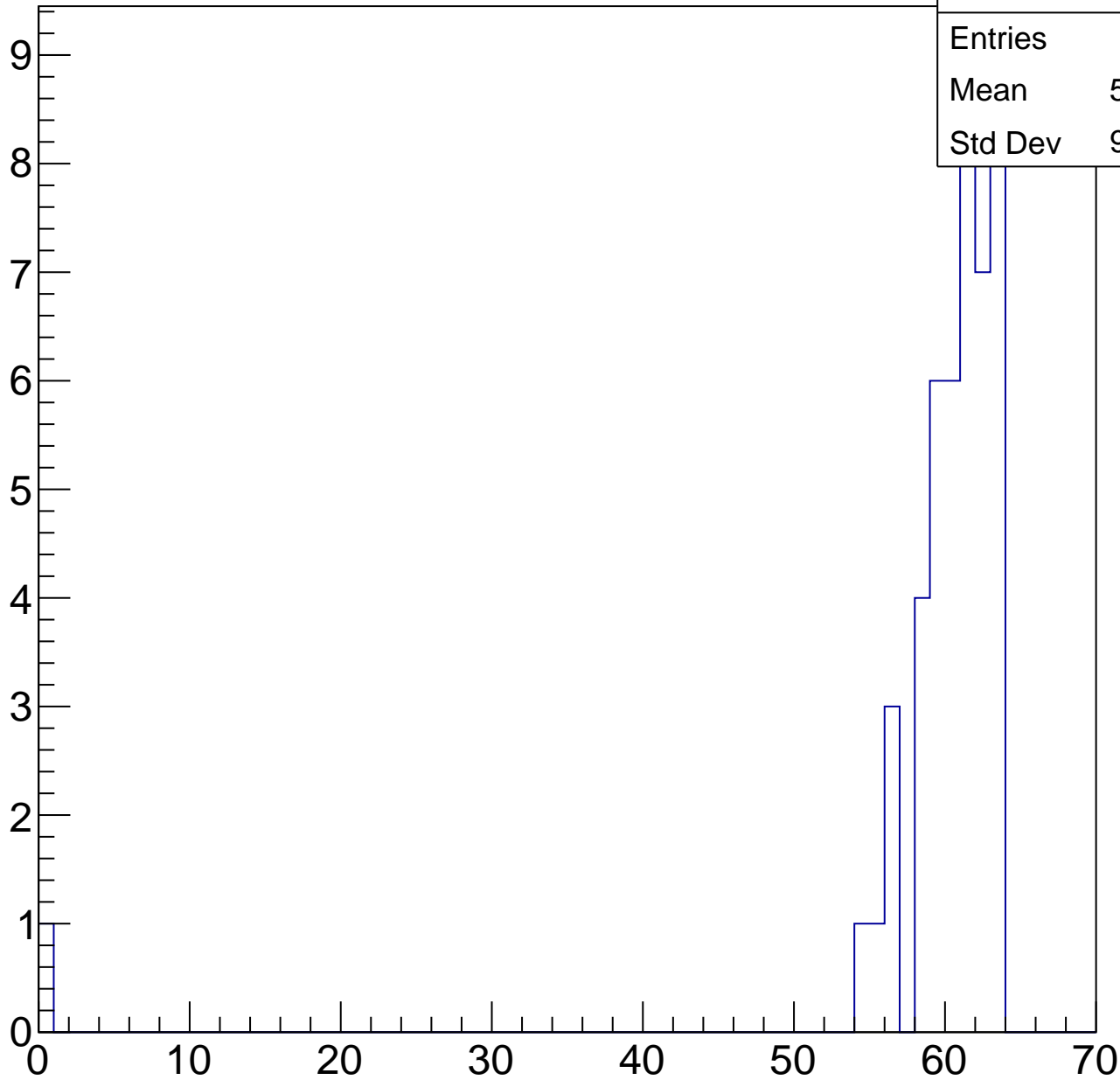
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.96
Std Dev	9.086

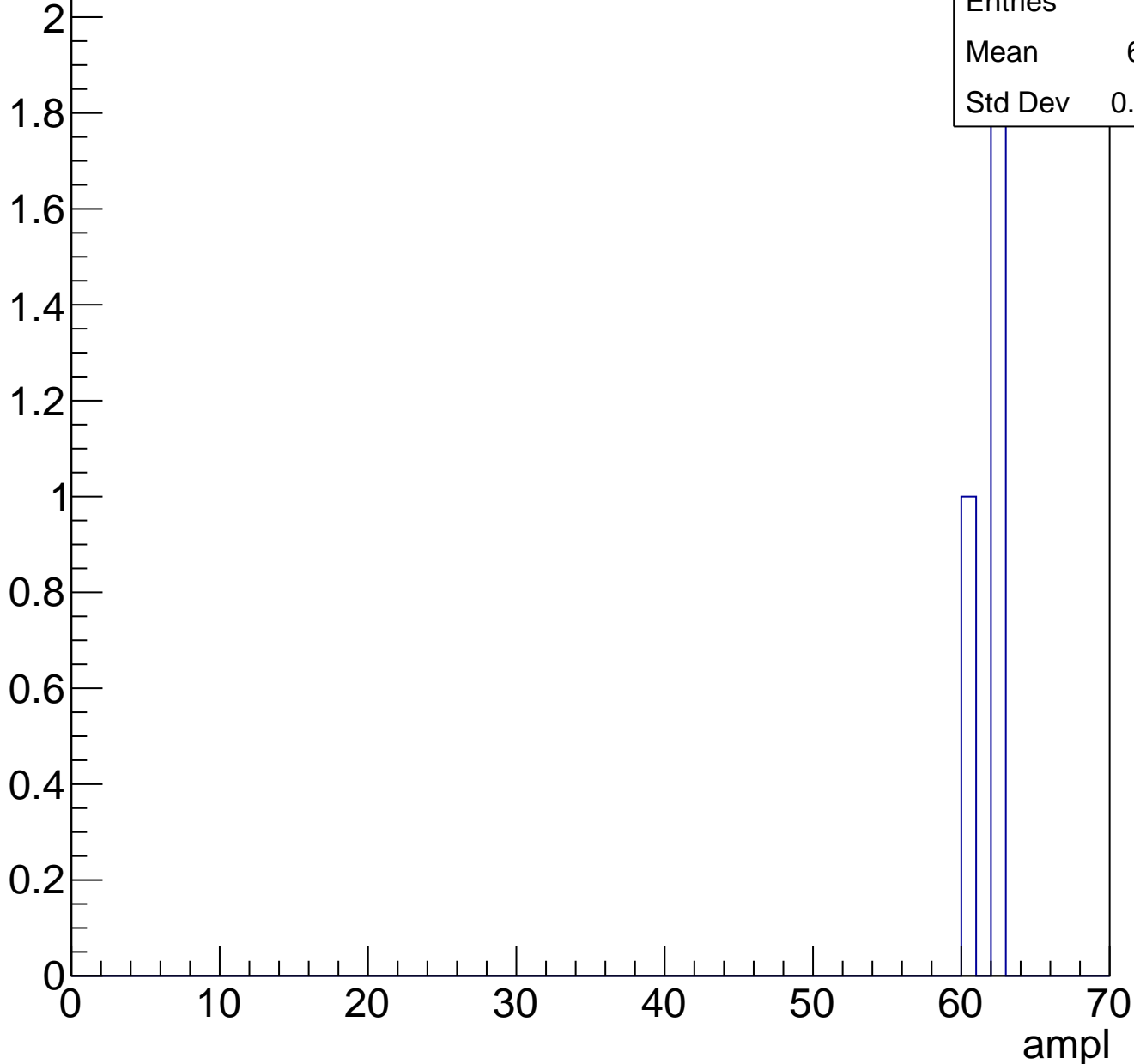
ampl



B0L000S, U4-ch47, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch47, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L000S, U4-ch48, adc0

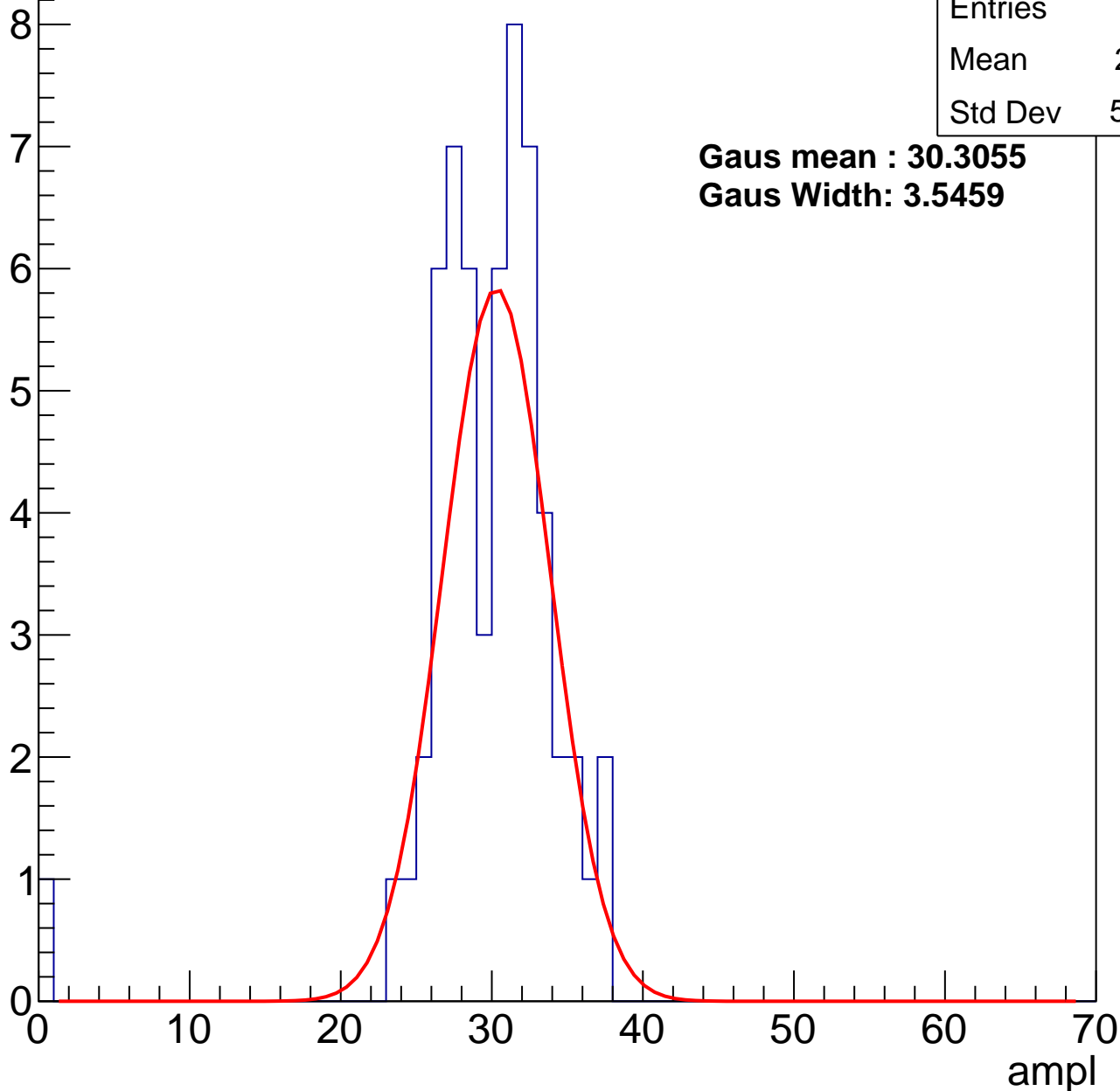
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.31
Std Dev	5.009

Gaus mean : 30.3055

Gaus Width: 3.5459



B0L000S, U4-ch48, adc1

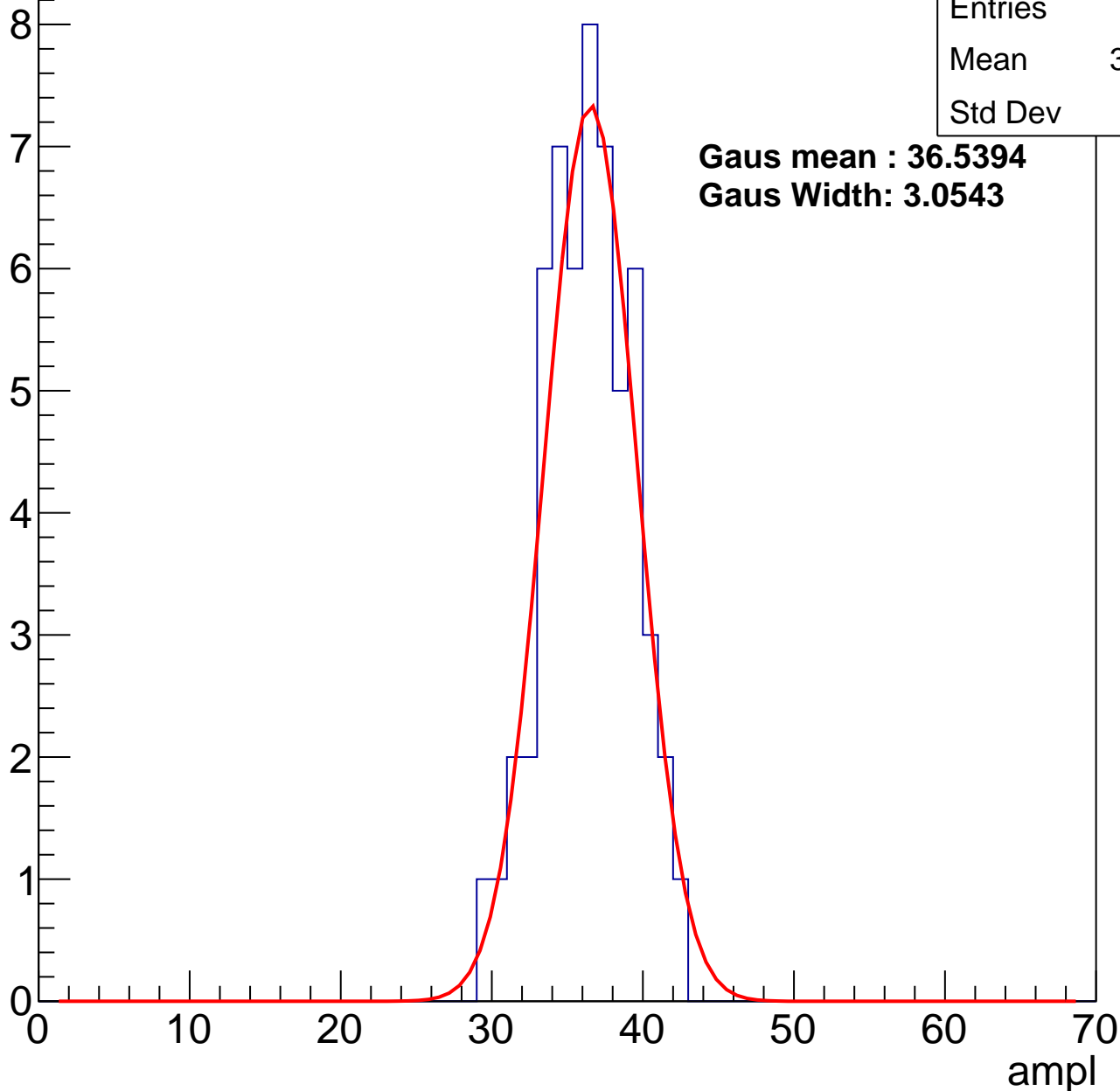
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.89
Std Dev	2.87

Gaus mean : 36.5394

Gaus Width: 3.0543



B0L000S, U4-ch48, adc2

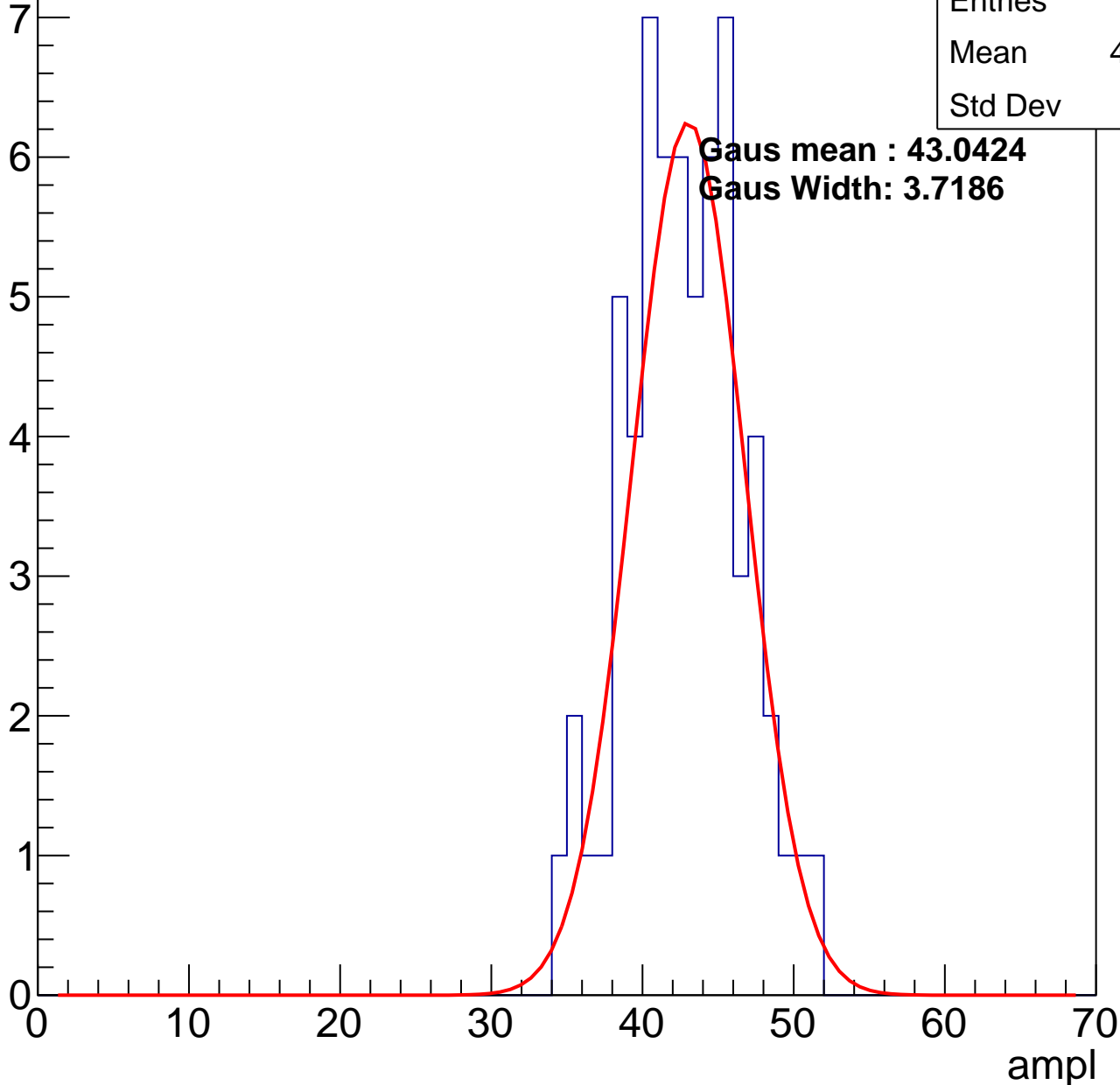
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.33
Std Dev	3.72

Gaus mean : 43.0424

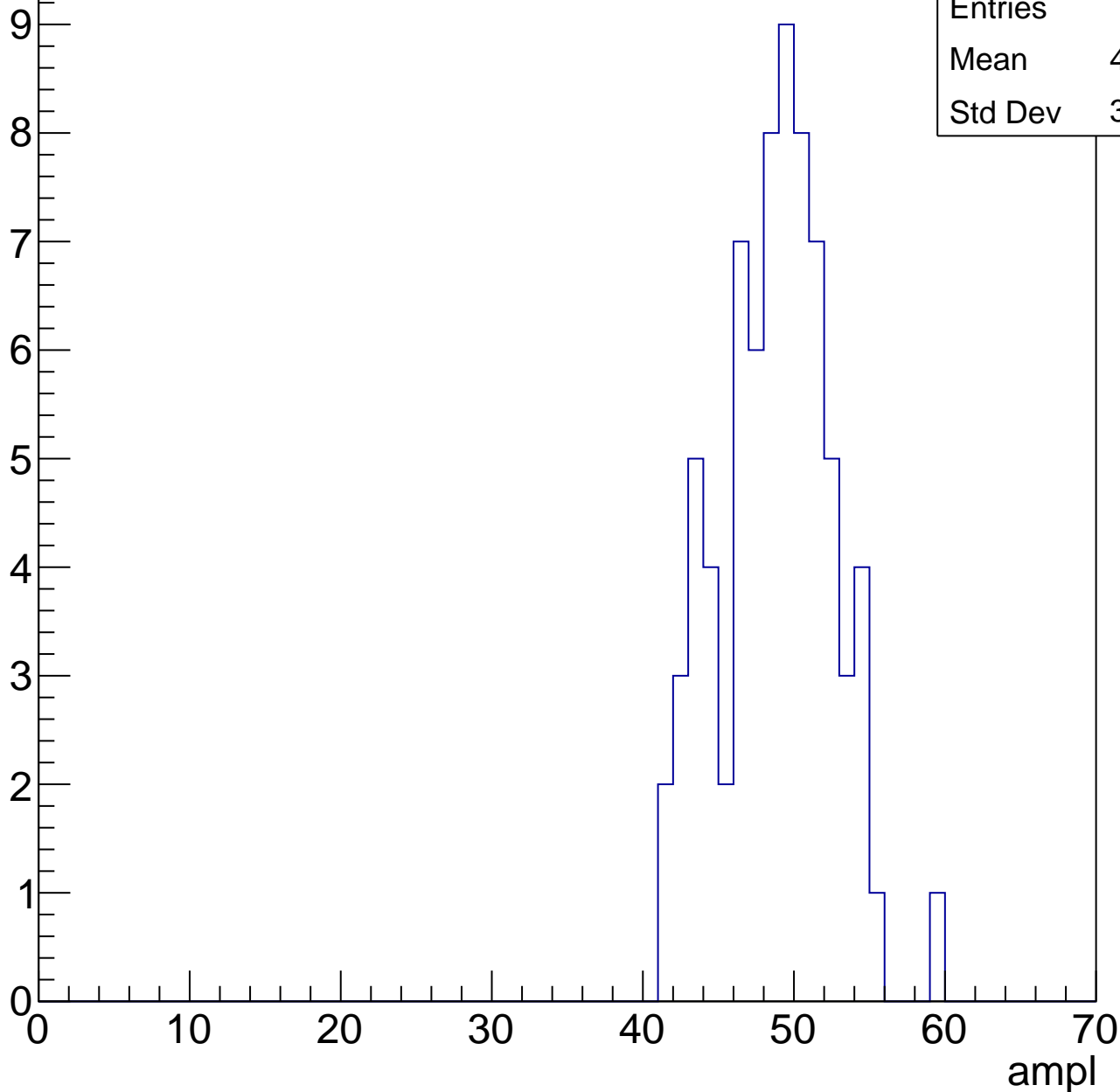
Gaus Width: 3.7186



B0L000S, U4-ch48, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



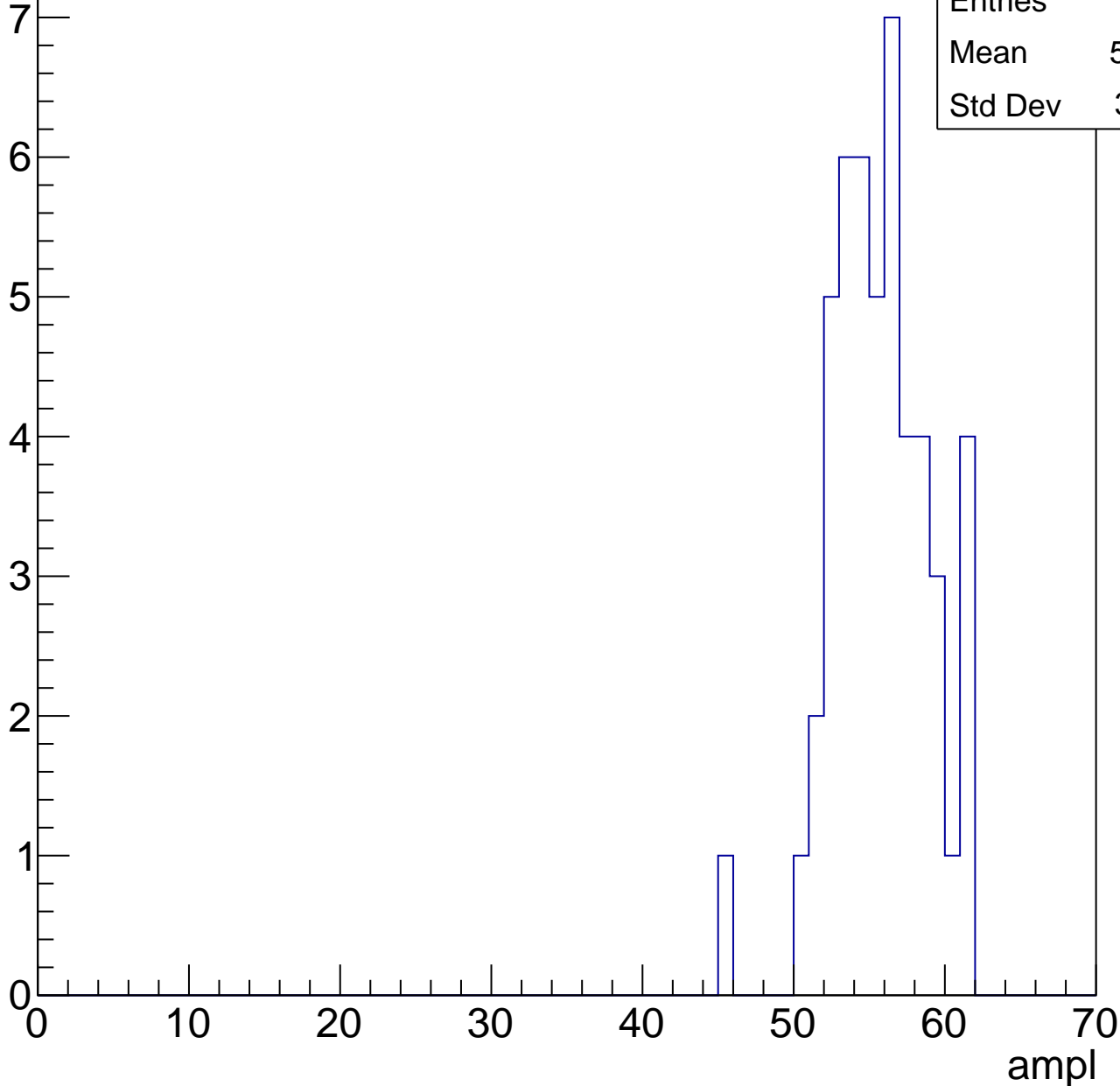
Entries	75
Mean	48.32
Std Dev	3.674

B0L000S, U4-ch48, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.24
Std Dev	3.211

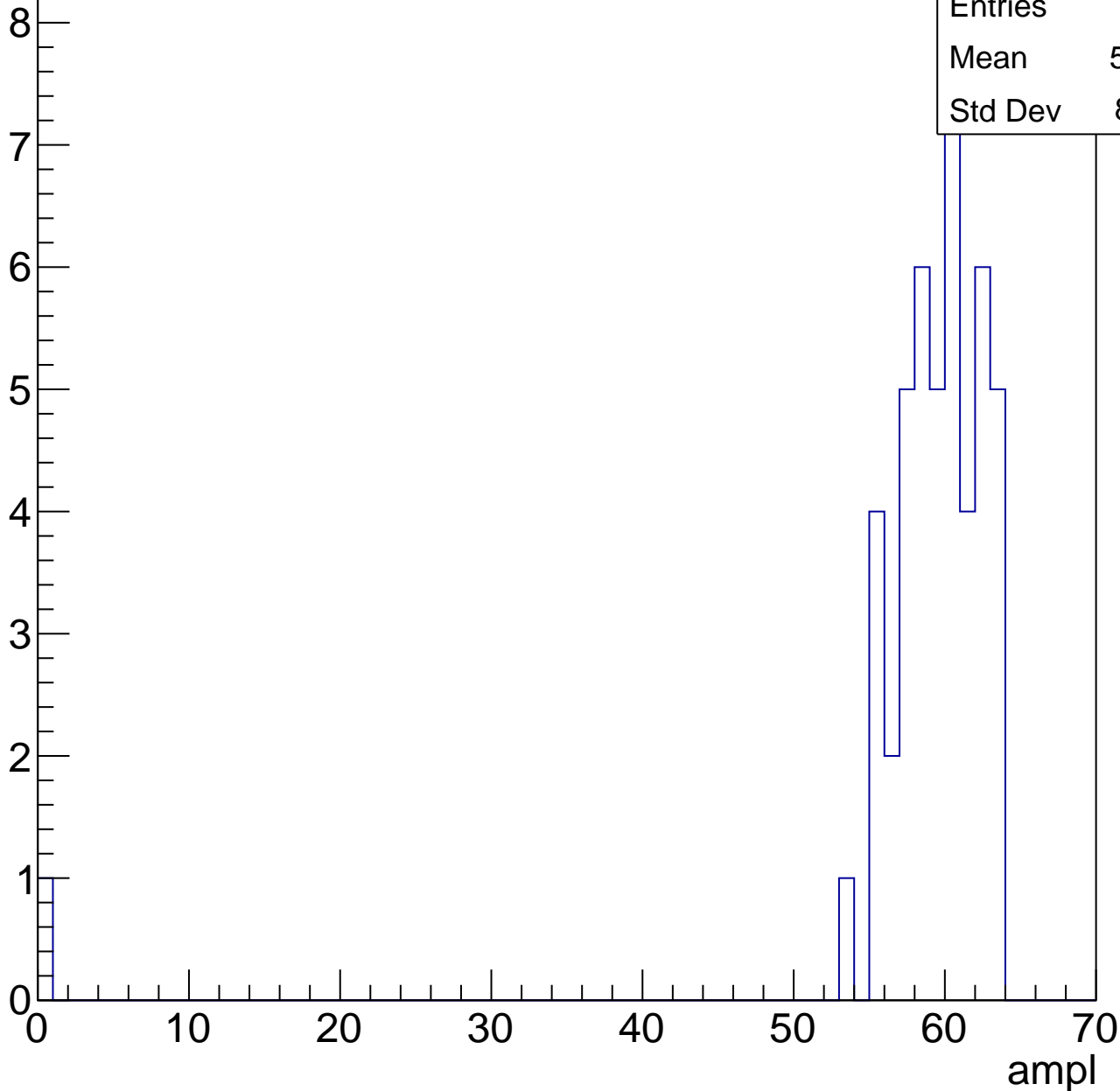


B0L000S, U4-ch48, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	57.96
Std Dev	8.911

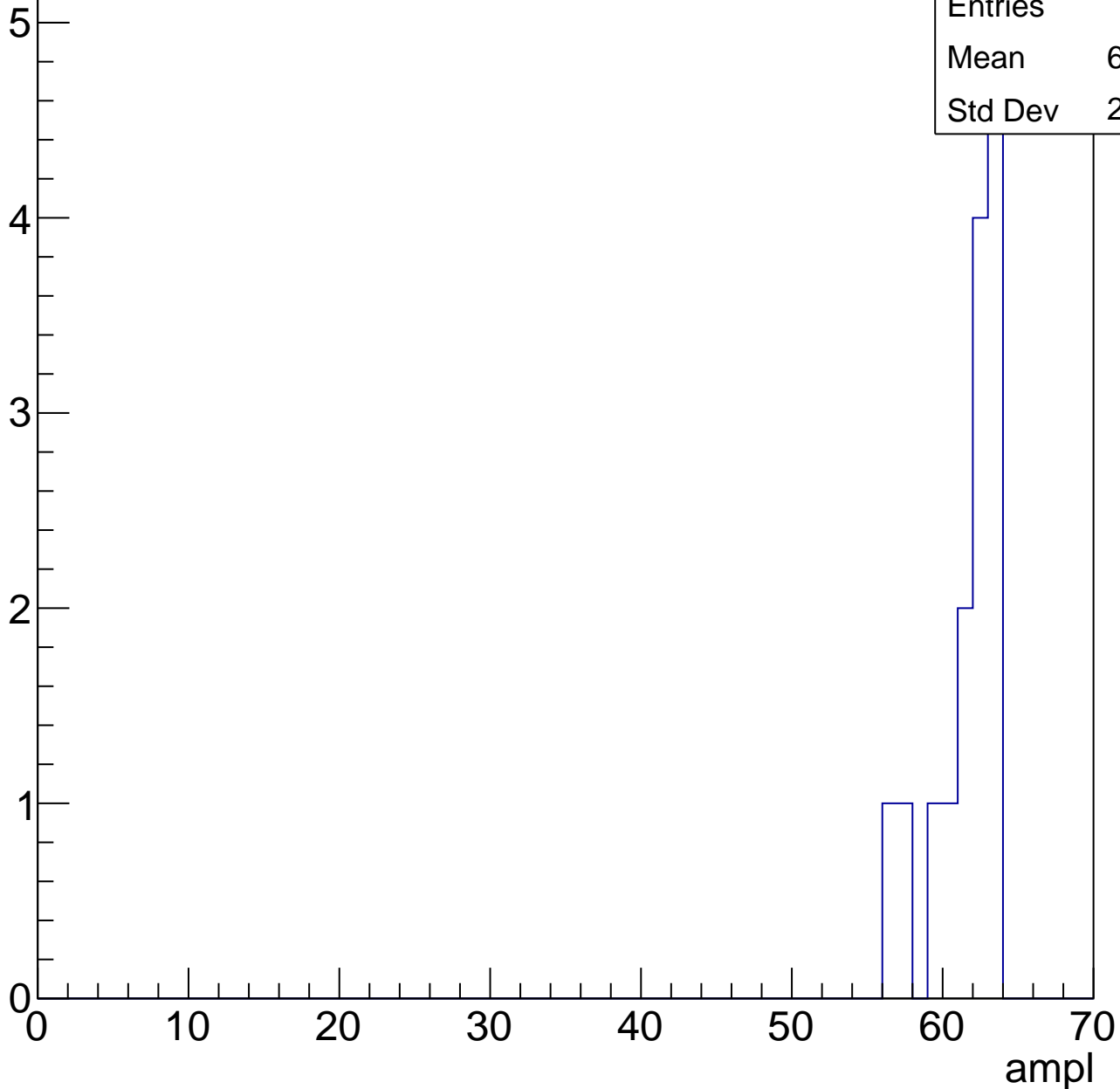


B0L000S, U4-ch48, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

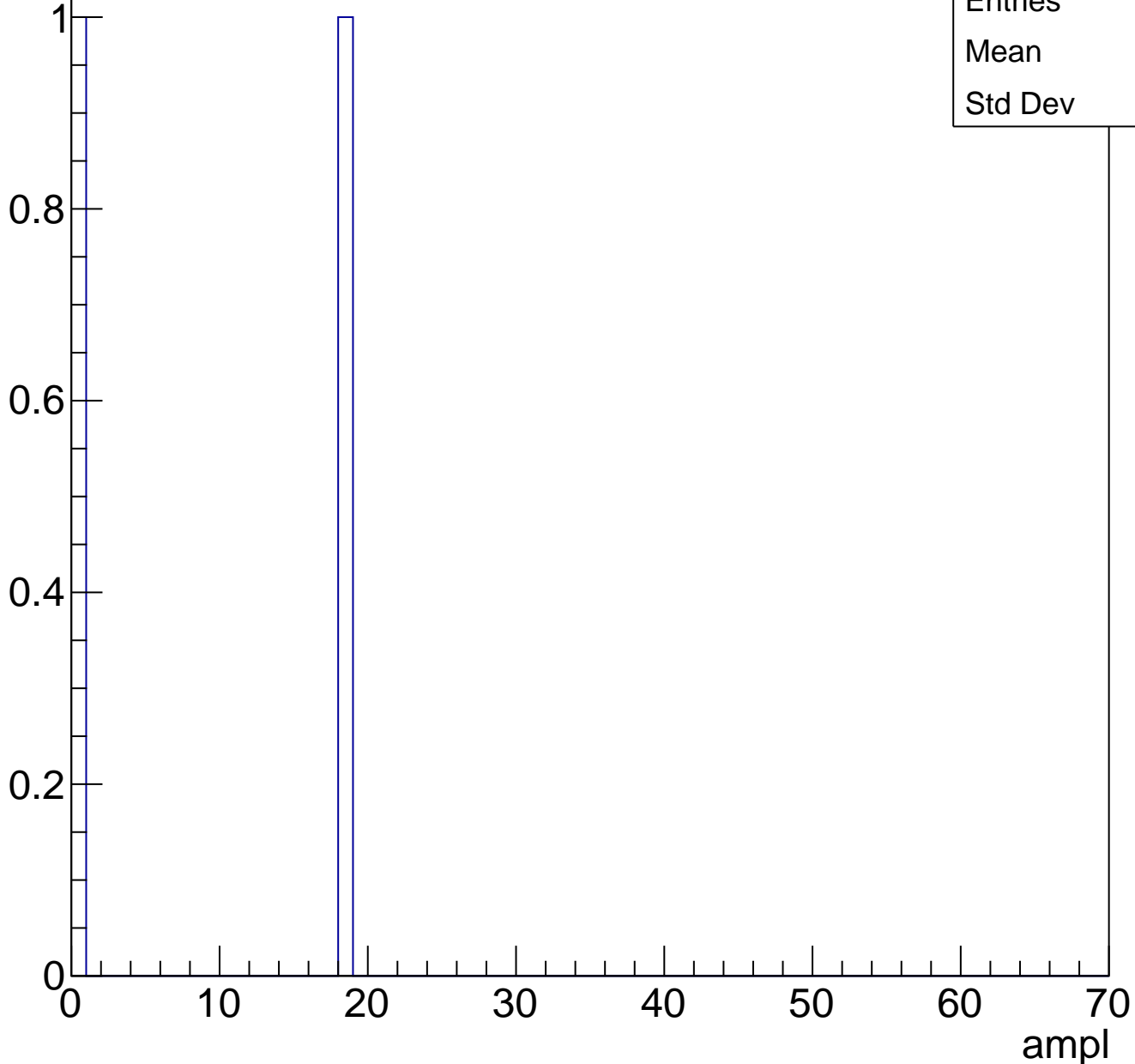
Entries	15
Mean	61.13
Std Dev	2.156



B0L000S, U4-ch48, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	9
Std Dev	9

B0L000S, U4-ch49, adc0

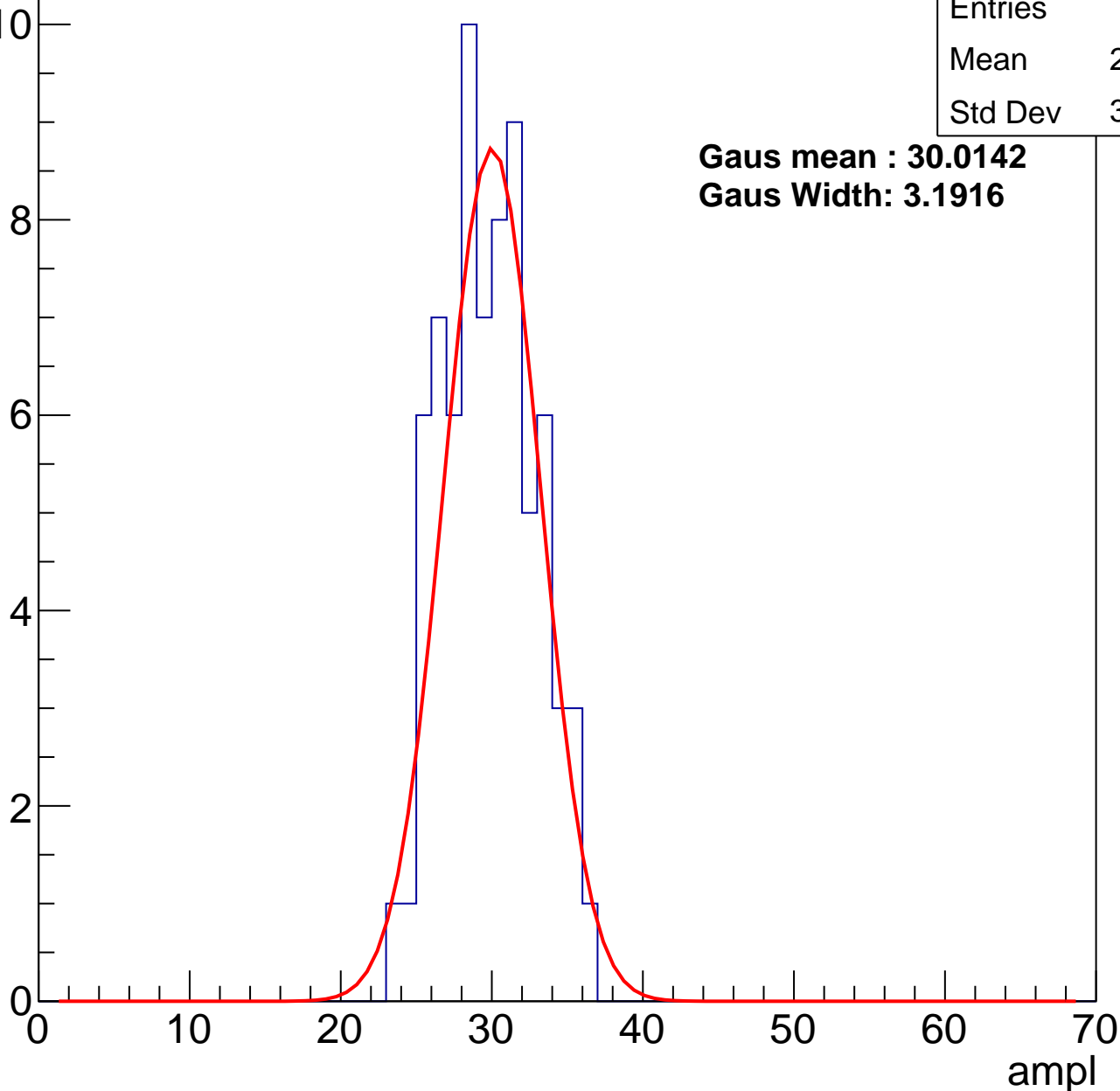
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.37
Std Dev	3.009

Gaus mean : 30.0142

Gaus Width: 3.1916



B0L000S, U4-ch49, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	36.94
Std Dev	3.344

Gaus mean : 37.4991

Gaus Width: 3.3284

10

8

6

4

2

0

0

2

4

6

8

10

ampl

0

10

20

30

40

50

60

70

B0L000S, U4-ch49, adc2

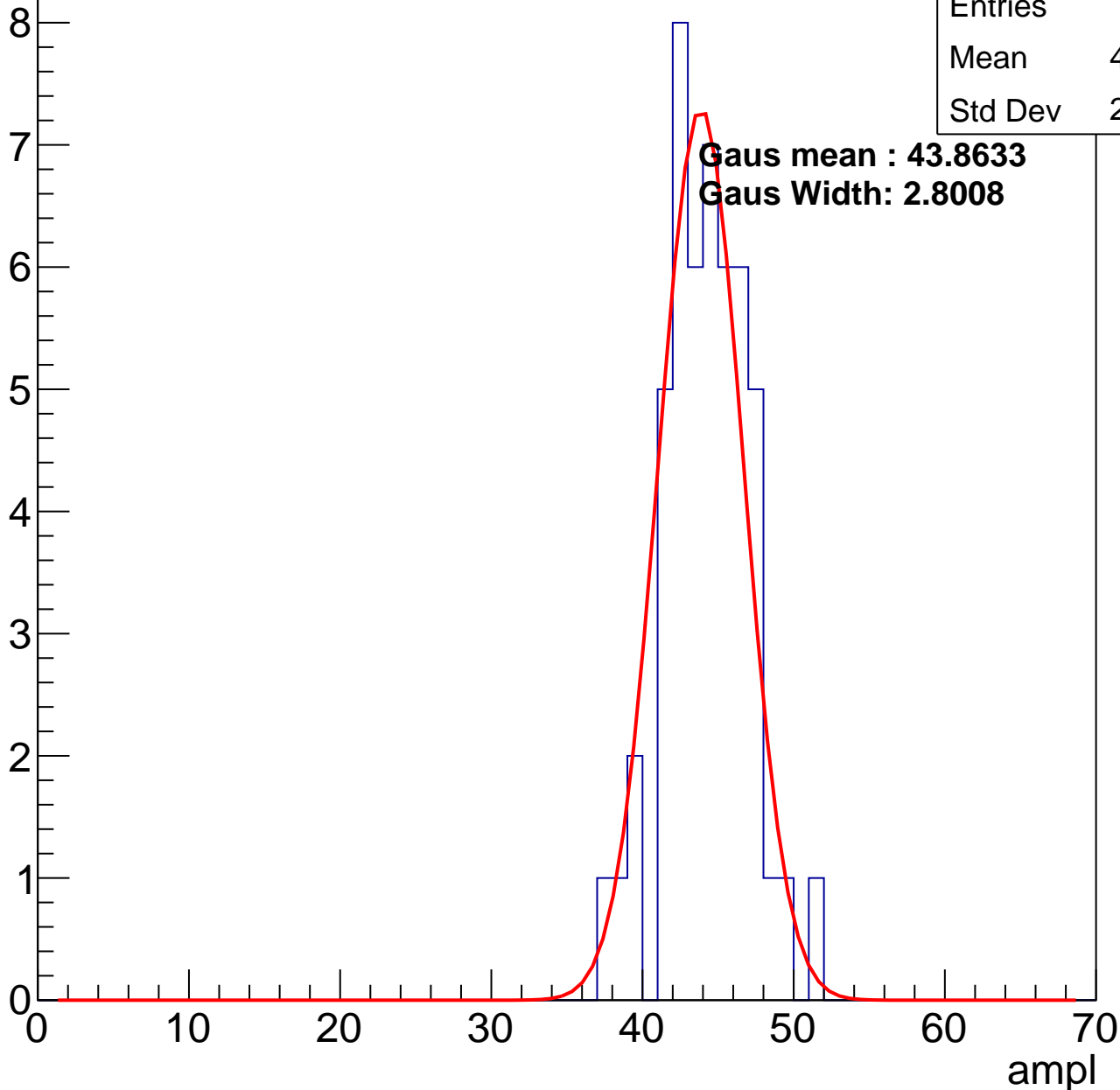
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	43.78
Std Dev	2.759

Gaus mean : 43.8633

Gaus Width: 2.8008

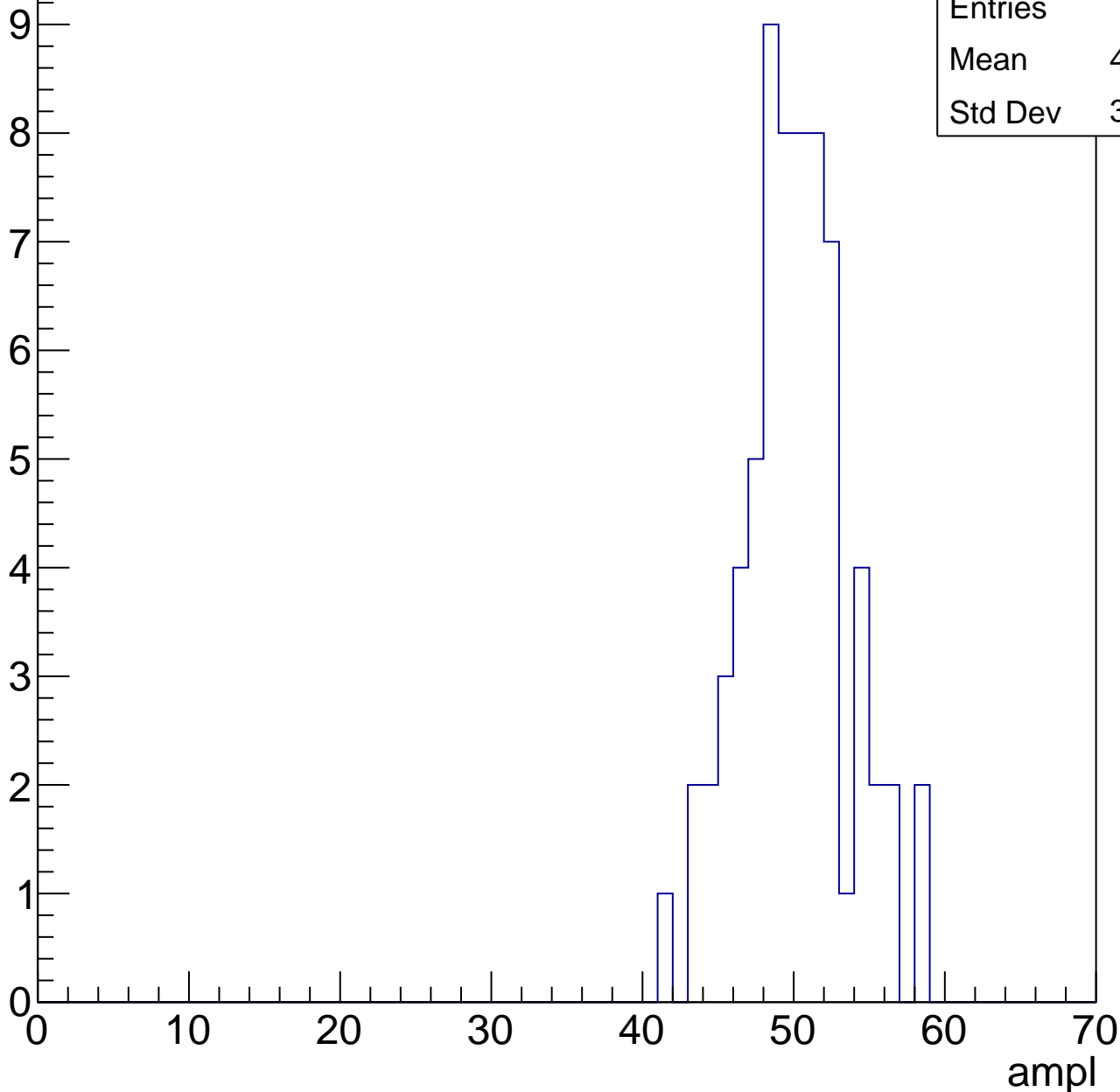


B0L000S, U4-ch49, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.59
Std Dev	3.486

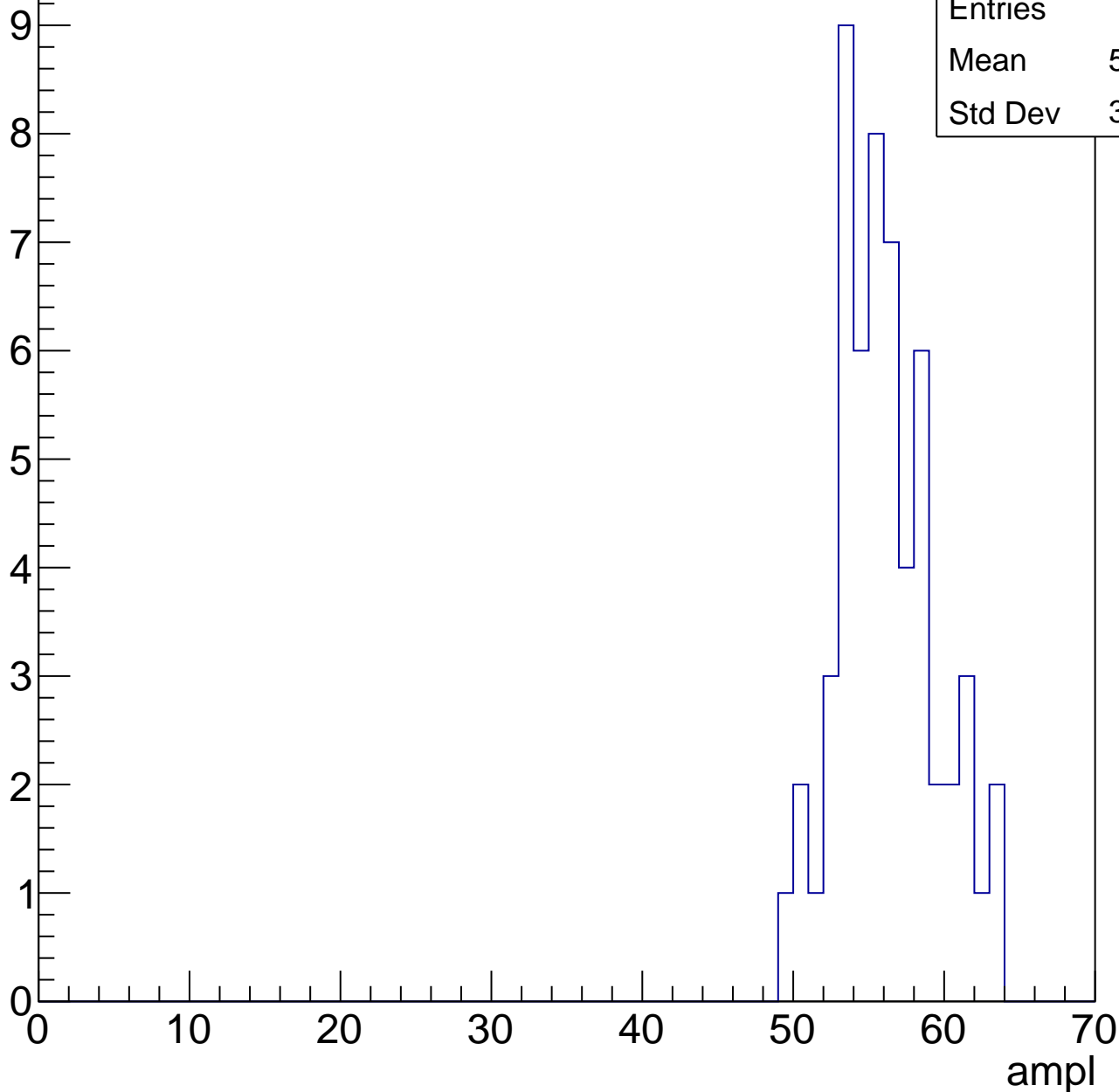


B0L000S, U4-ch49, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	55.68
Std Dev	3.213



B0L000S, U4-ch49, adc5

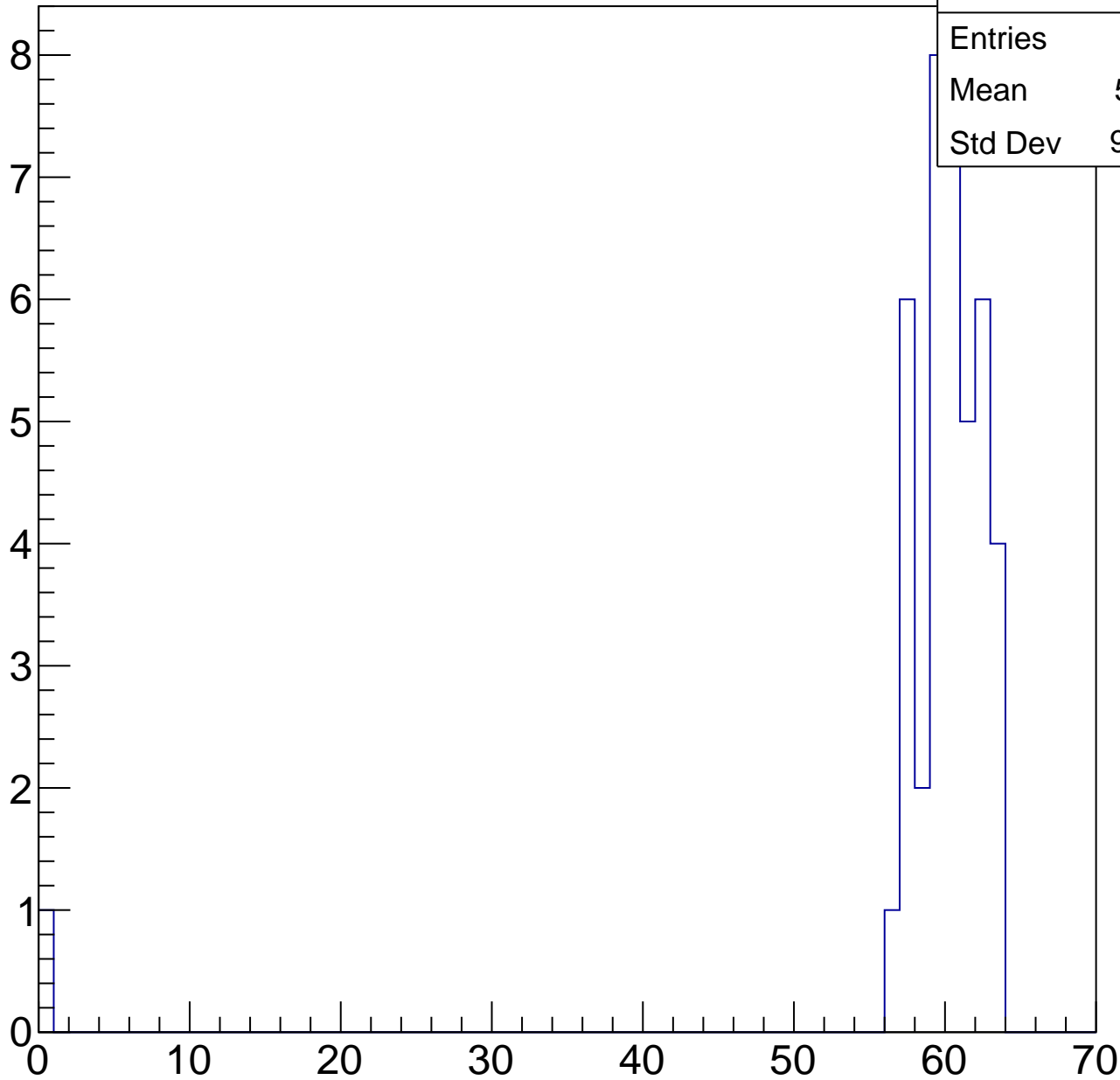
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.41
Std Dev	9.433

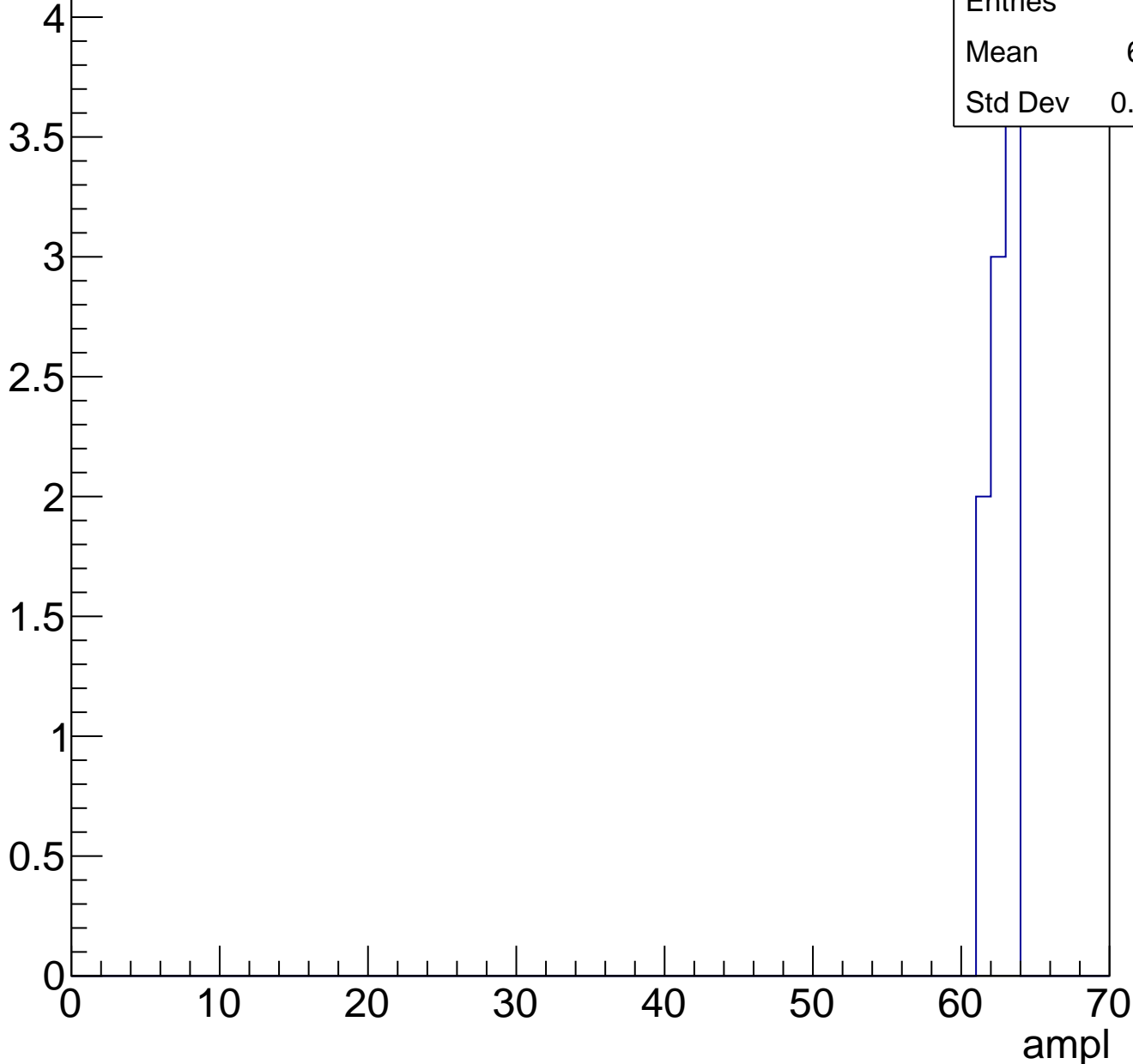
ampl



B0L000S, U4-ch49, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch49, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch50, adc0

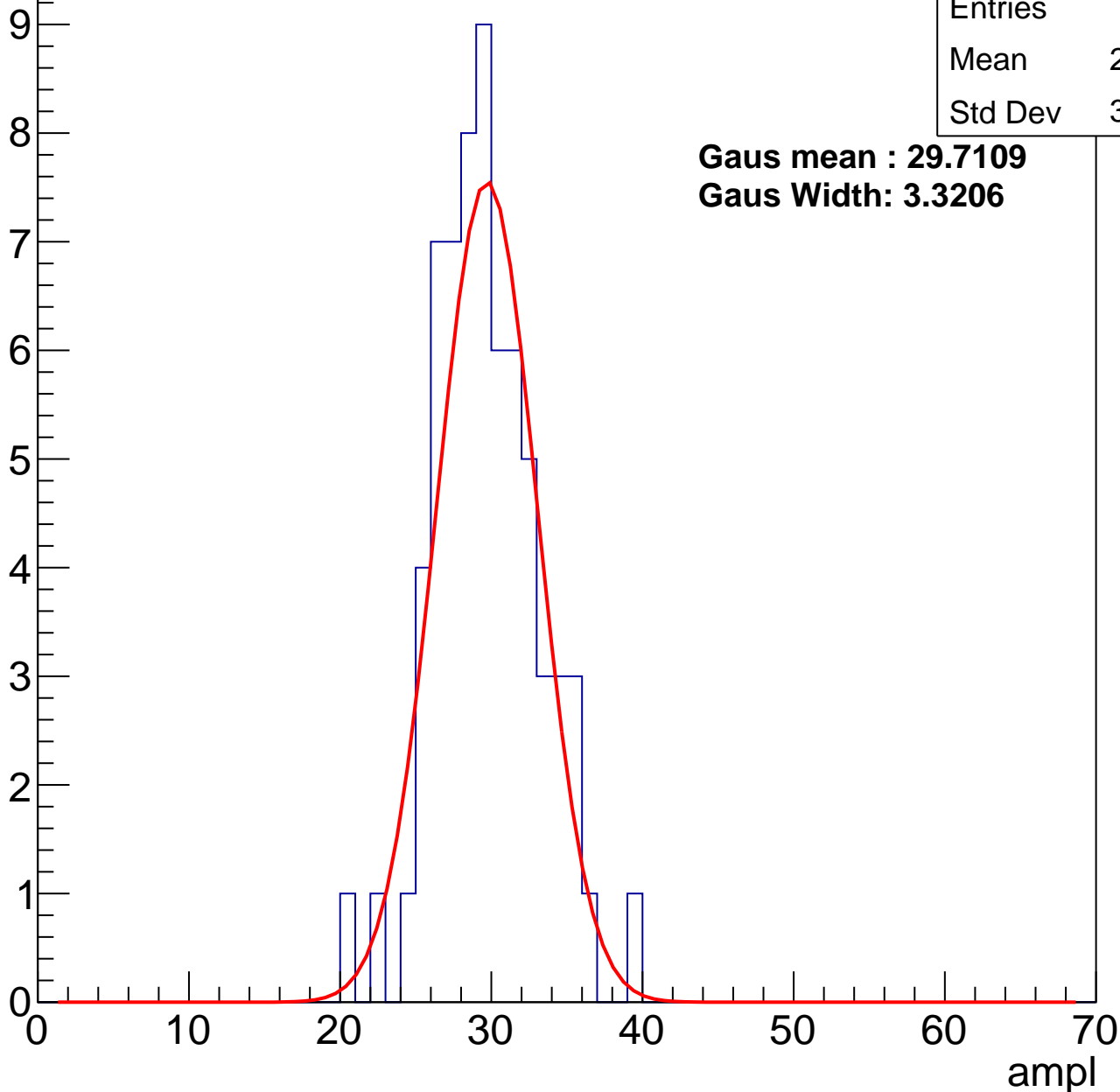
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.23
Std Dev	3.415

Gaus mean : 29.7109

Gaus Width: 3.3206



B0L000S, U4-ch50, adc1

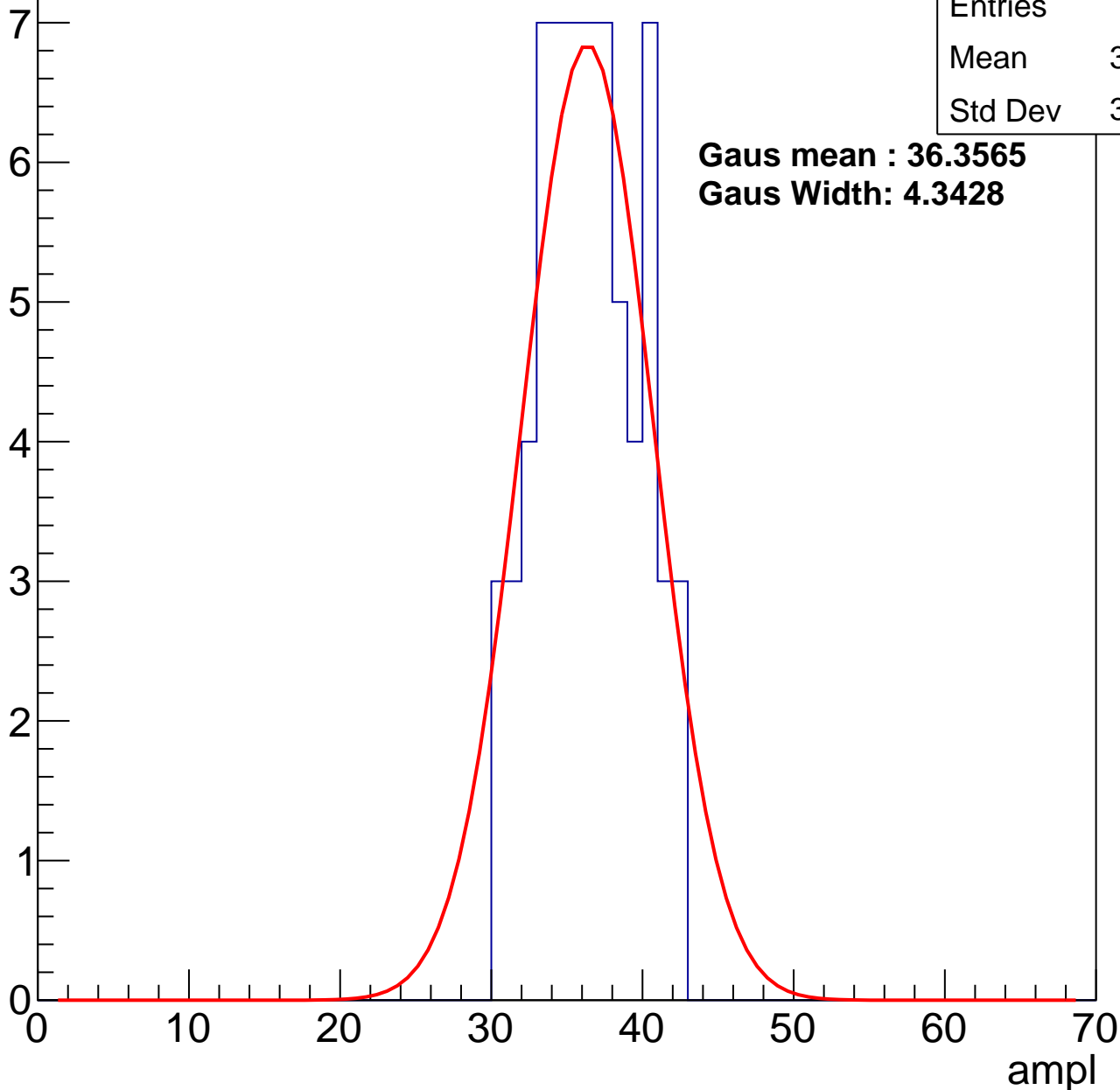
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.99
Std Dev	3.239

Gaus mean : 36.3565

Gaus Width: 4.3428



B0L000S, U4-ch50, adc2

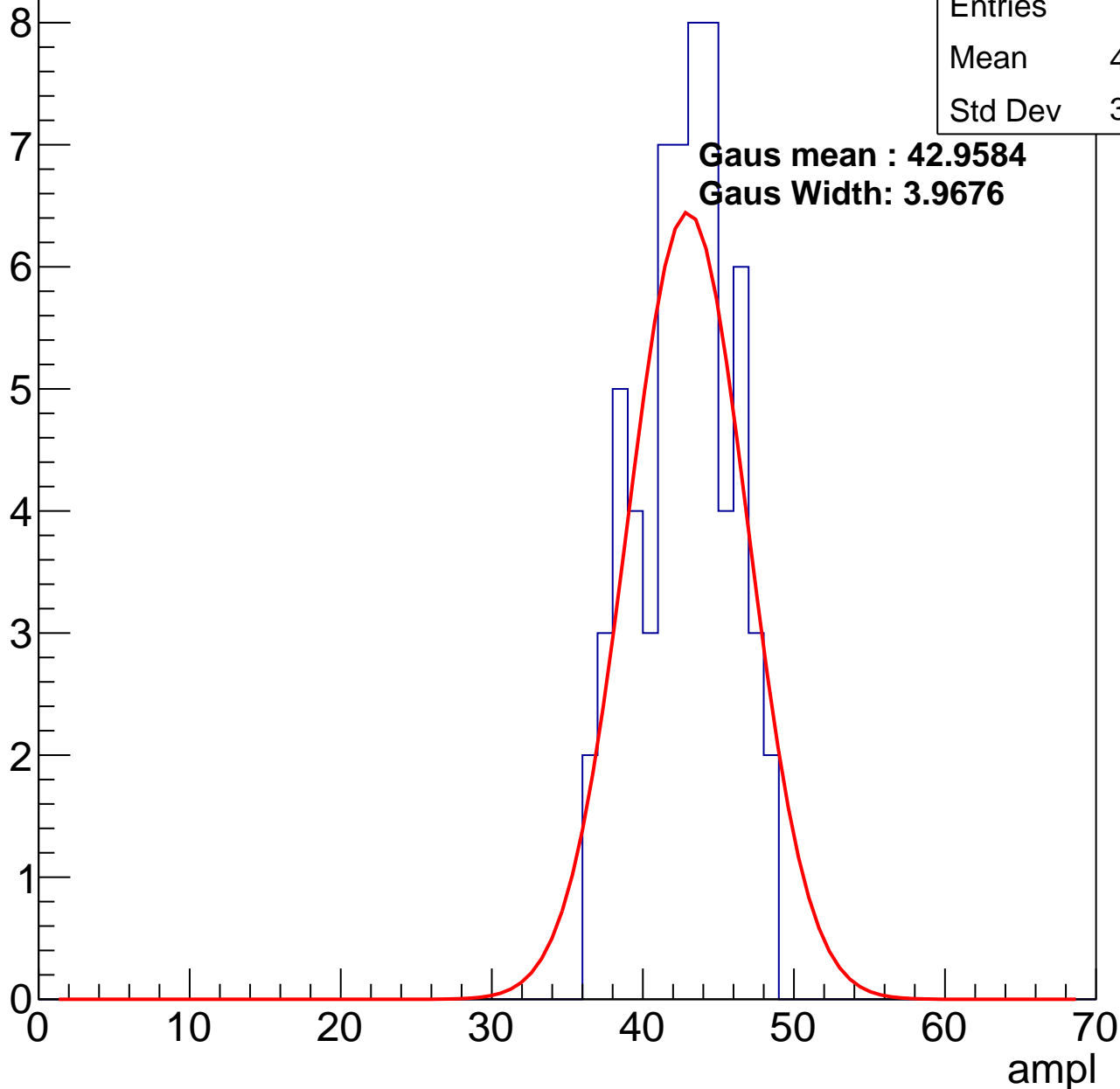
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	42.24
Std Dev	3.104

Gaus mean : 42.9584

Gaus Width: 3.9676

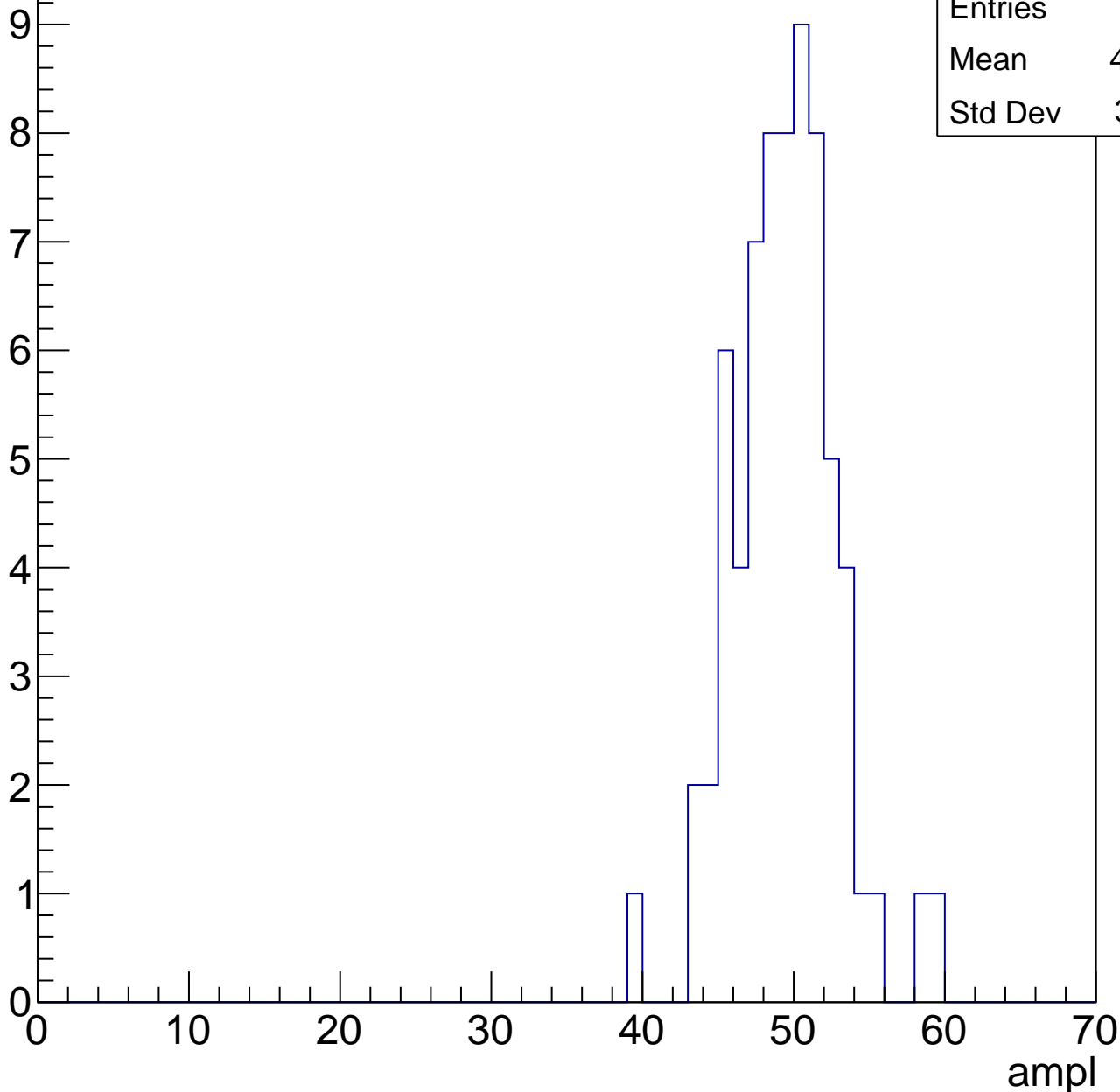


B0L000S, U4-ch50, adc3

calib_packv5_042523_0143.root, FC#5, port B1

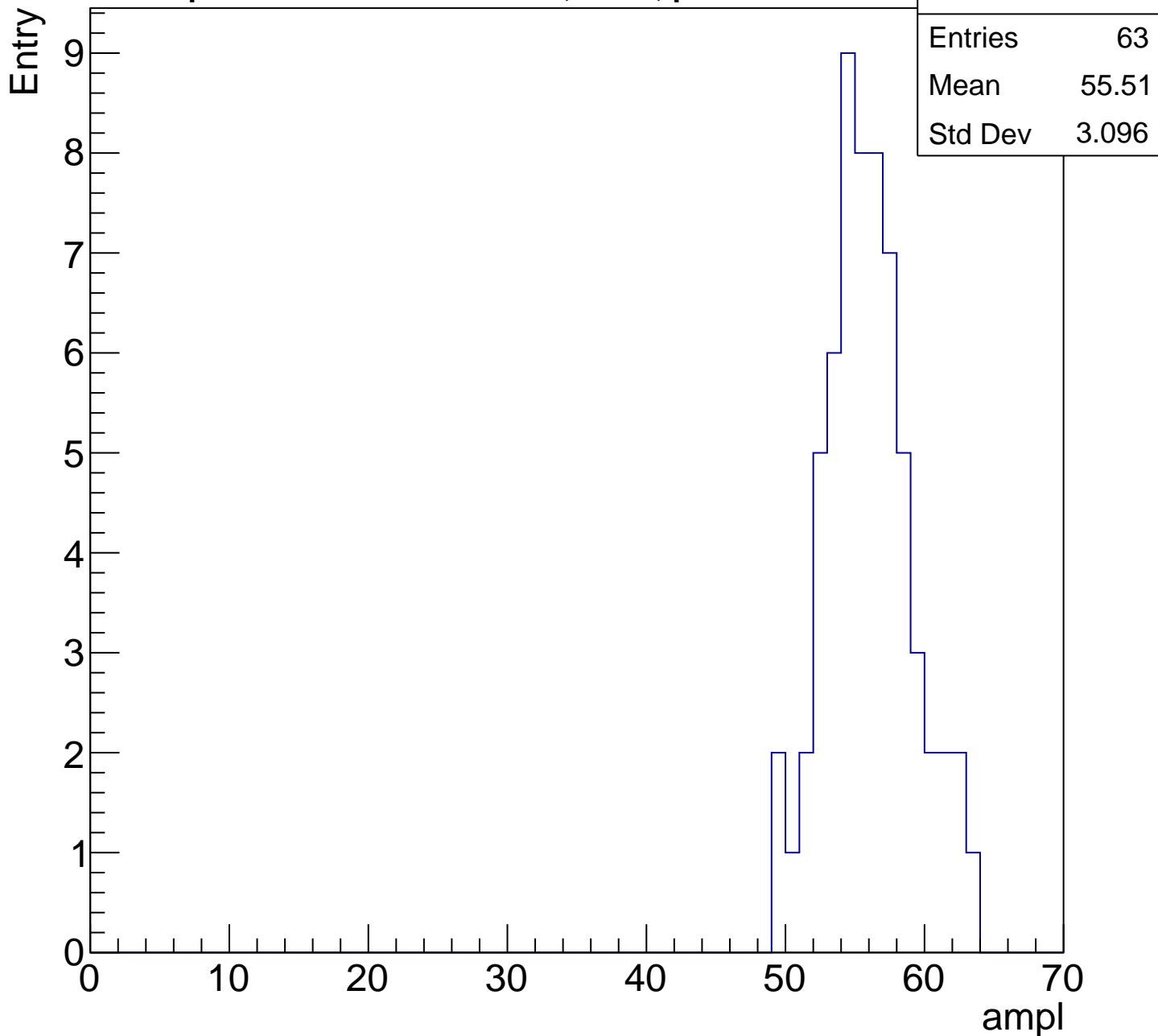
Entry

Entries	68
Mean	48.94
Std Dev	3.391



B0L000S, U4-ch50, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch50, adc5

calib_packv5_042523_0143.root, FC#5, port B1

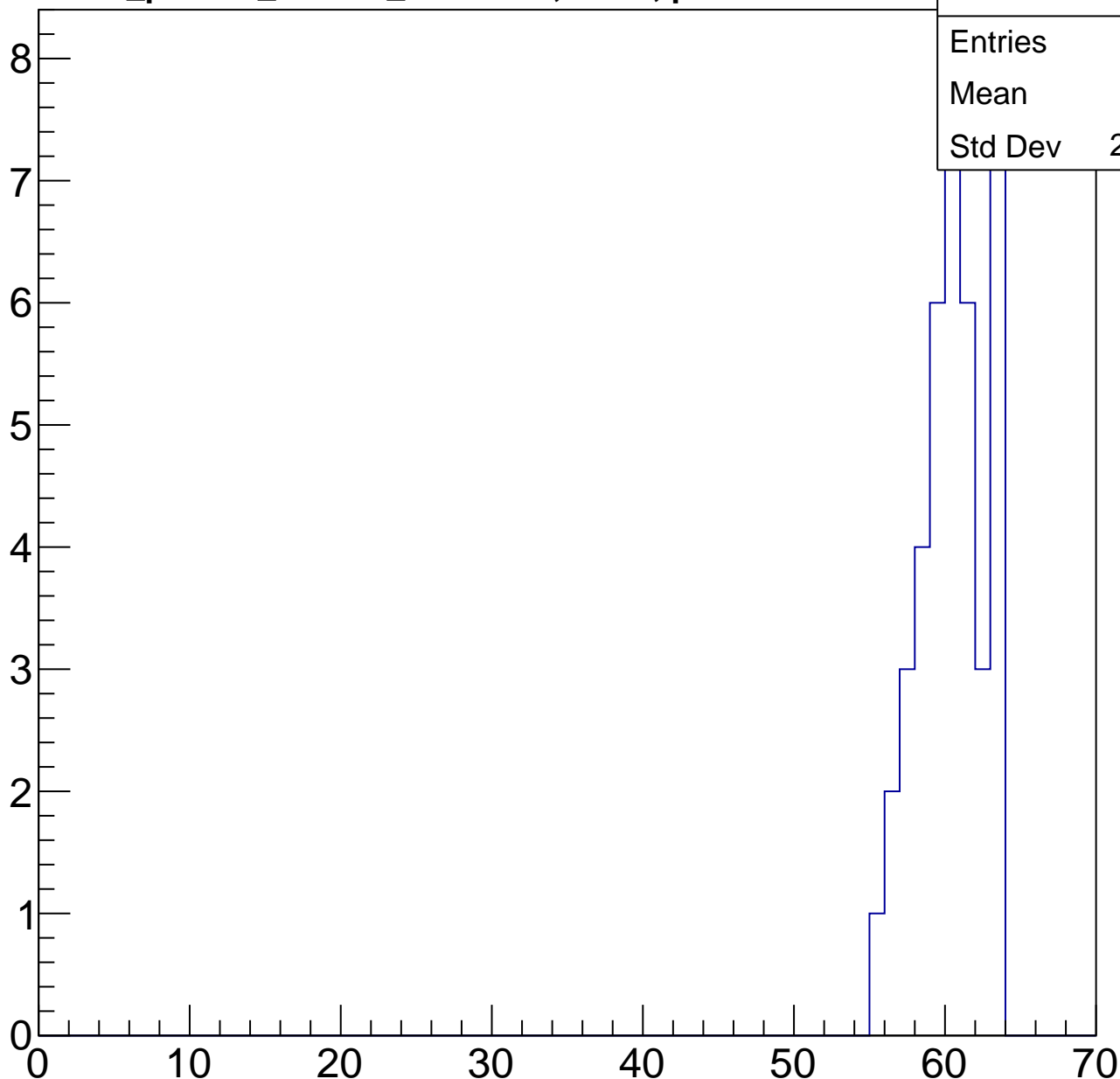
Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60
Std Dev	2.186

ampl

0 10 20 30 40 50 60 70

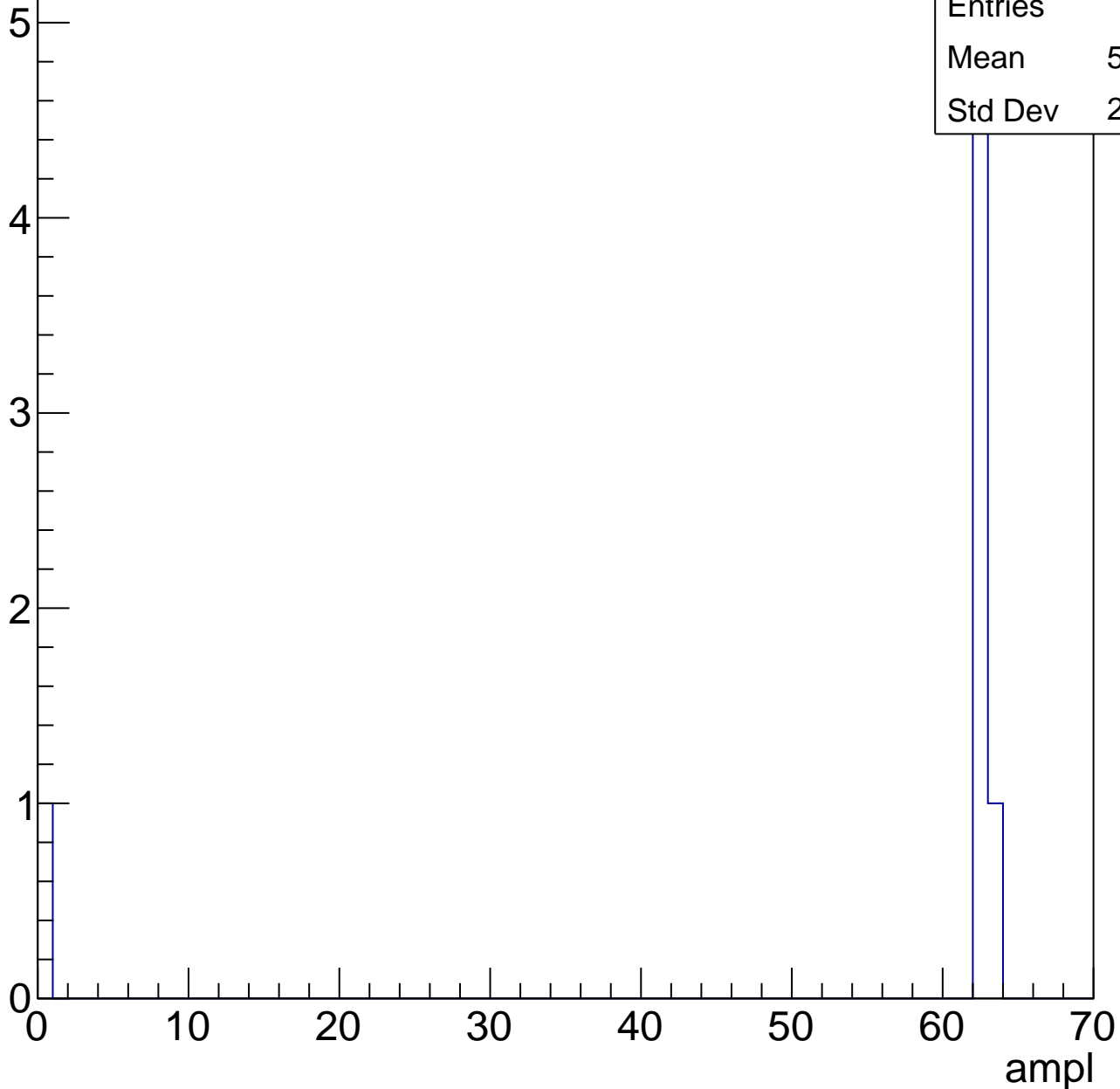


B0L000S, U4-ch50, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

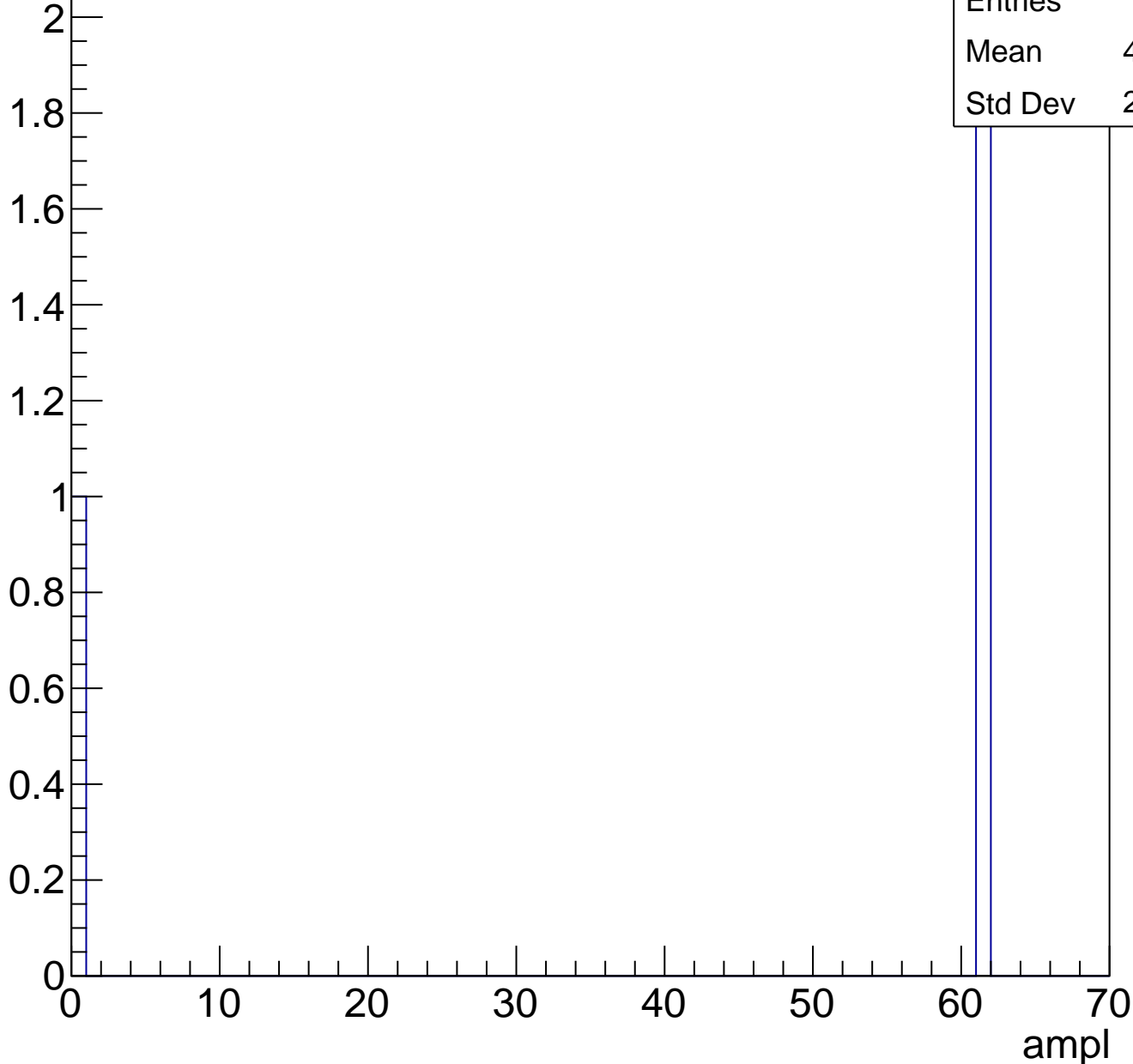
Entries	7
Mean	53.29
Std Dev	21.76



B0L000S, U4-ch50, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch51, adc0

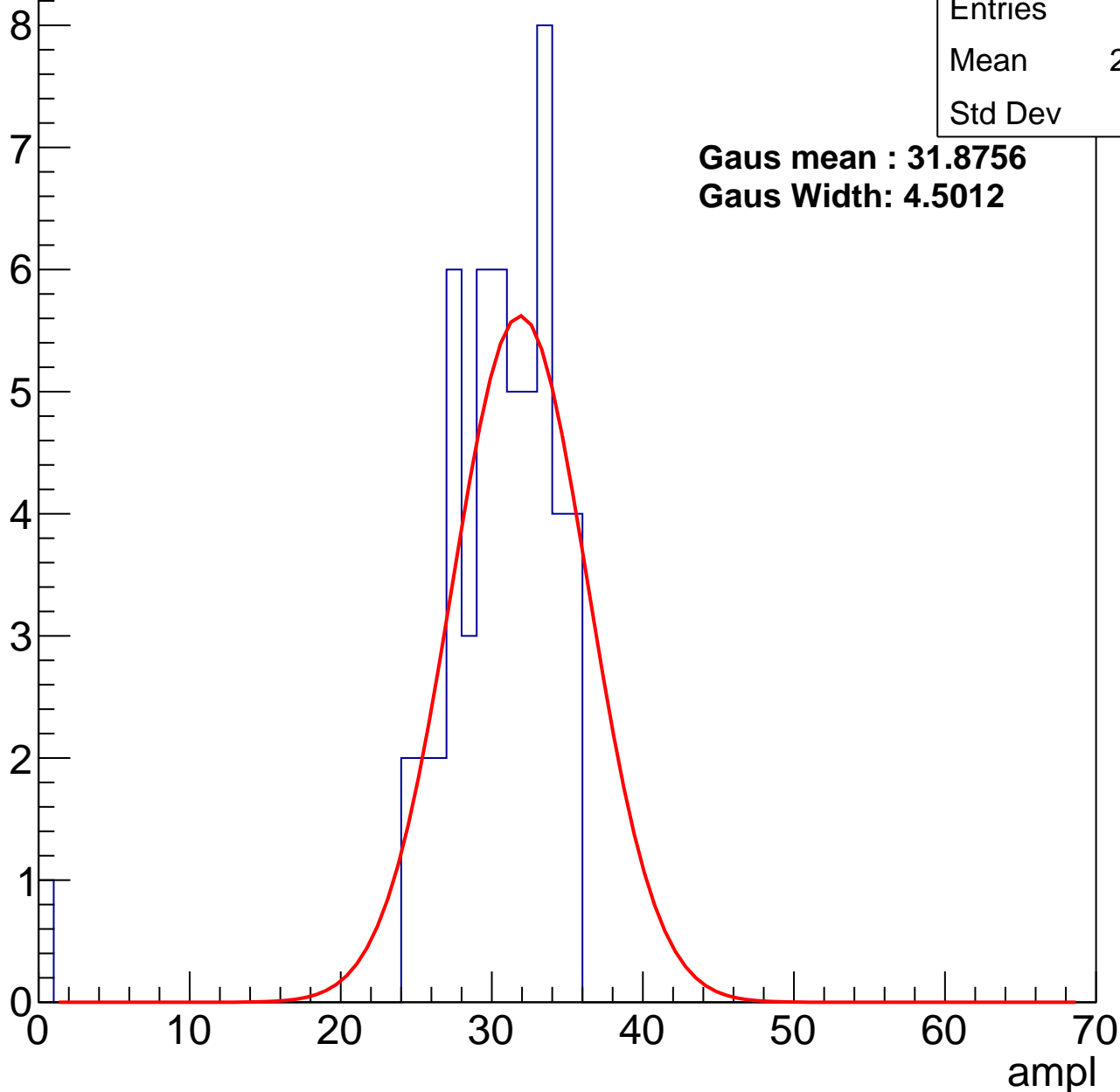
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	29.72
Std Dev	5.06

Gaus mean : 31.8756

Gaus Width: 4.5012



B0L000S, U4-ch51, adc1

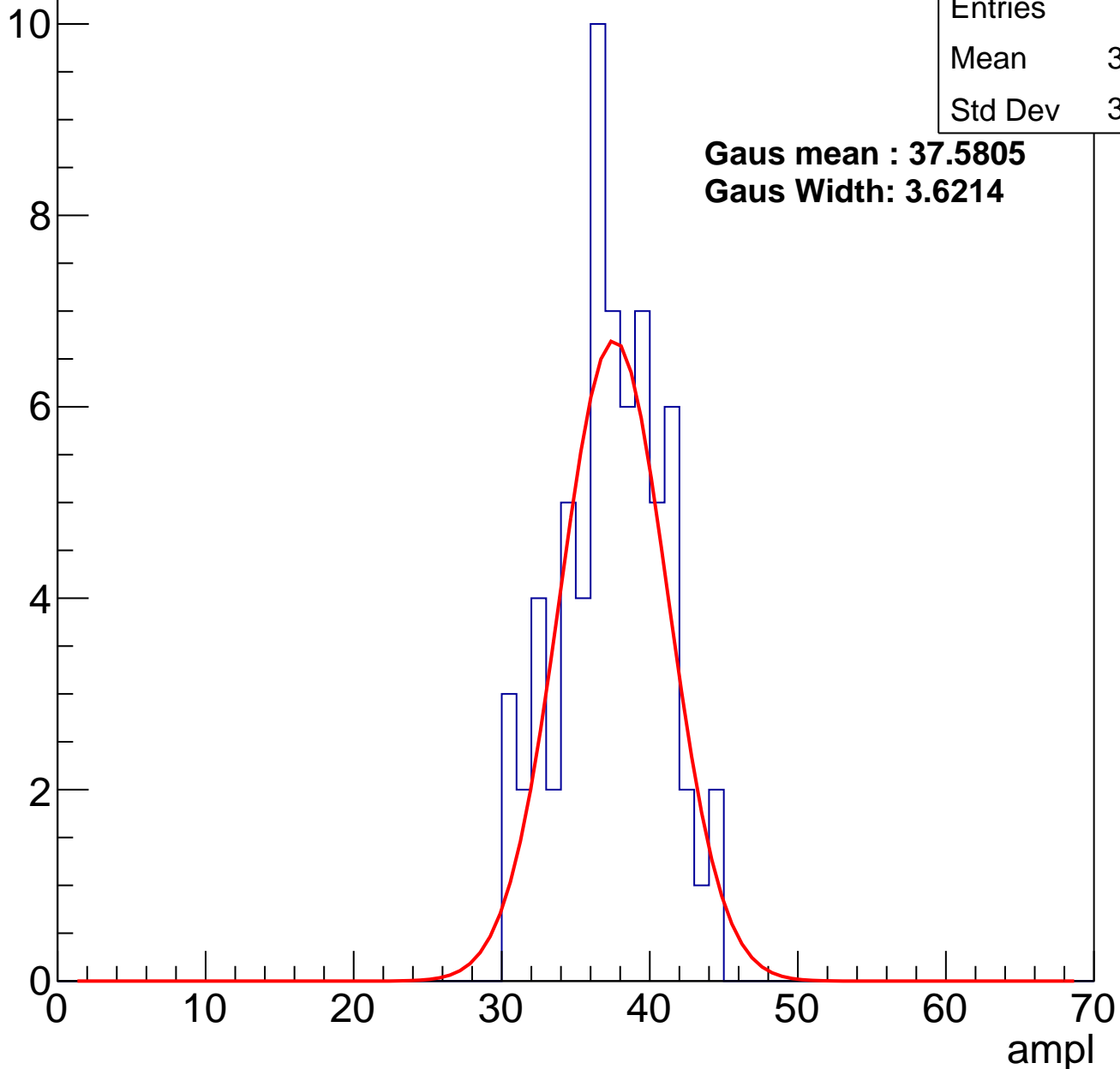
calib_packv5_042523_0143.root, FC#5, port B1

Entries	66
Mean	36.92
Std Dev	3.439

Gaus mean : 37.5805

Gaus Width: 3.6214

Entry



B0L000S, U4-ch51, adc2

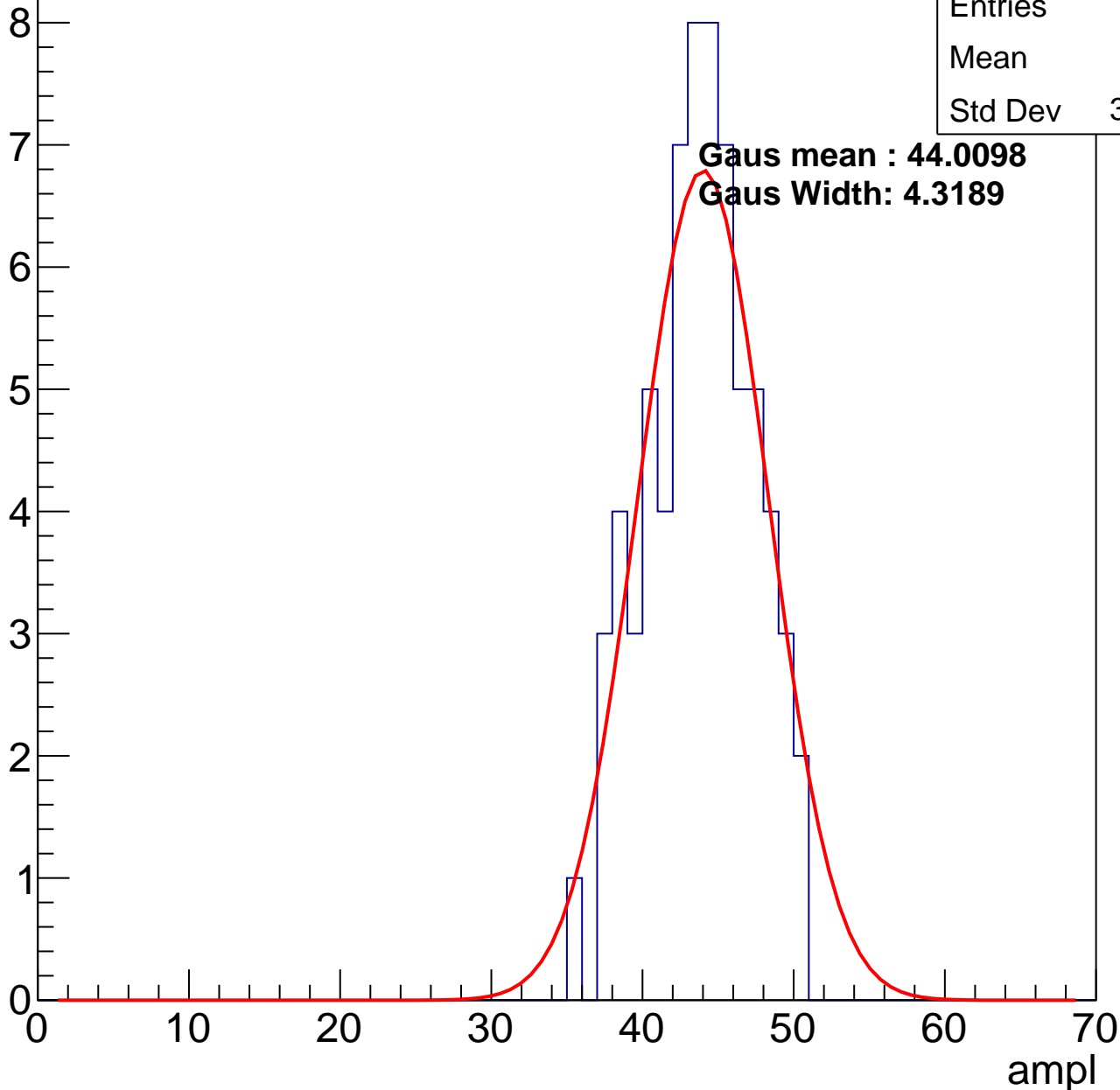
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.3
Std Dev	3.507

Gaus mean : 44.0098

Gaus Width: 4.3189

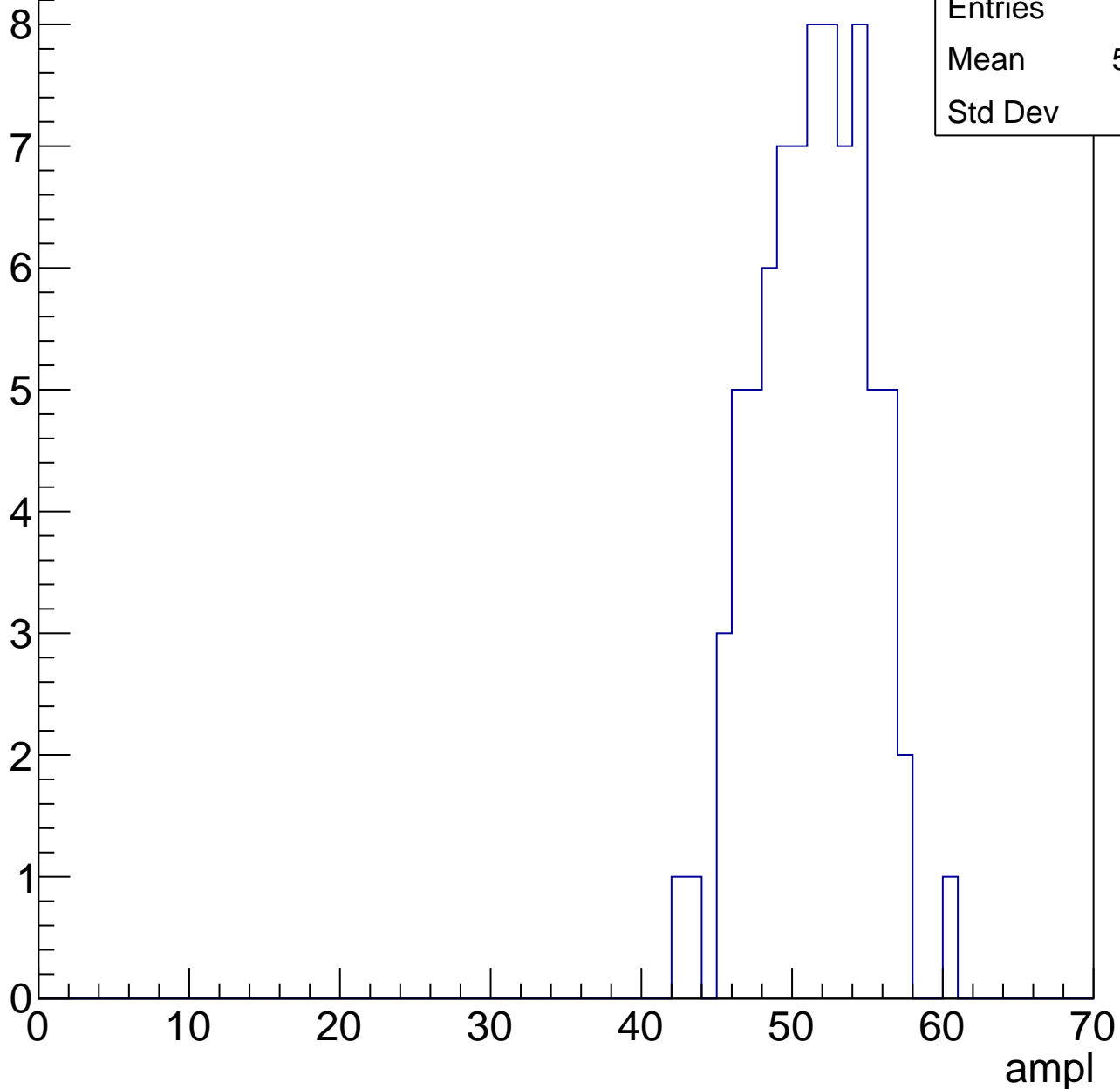


B0L000S, U4-ch51, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

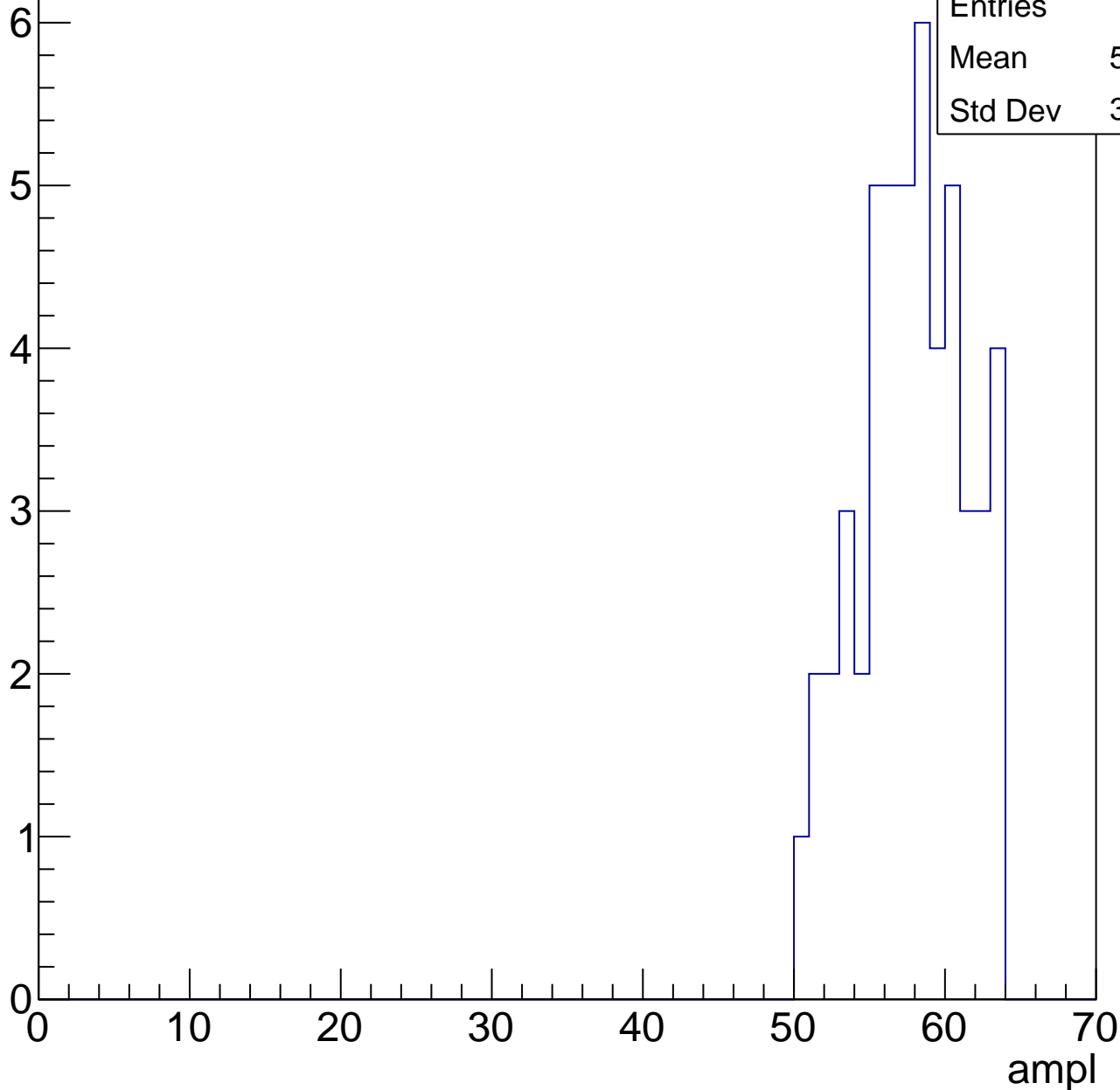
Entries	79
Mean	50.91
Std Dev	3.58



B0L000S, U4-ch51, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



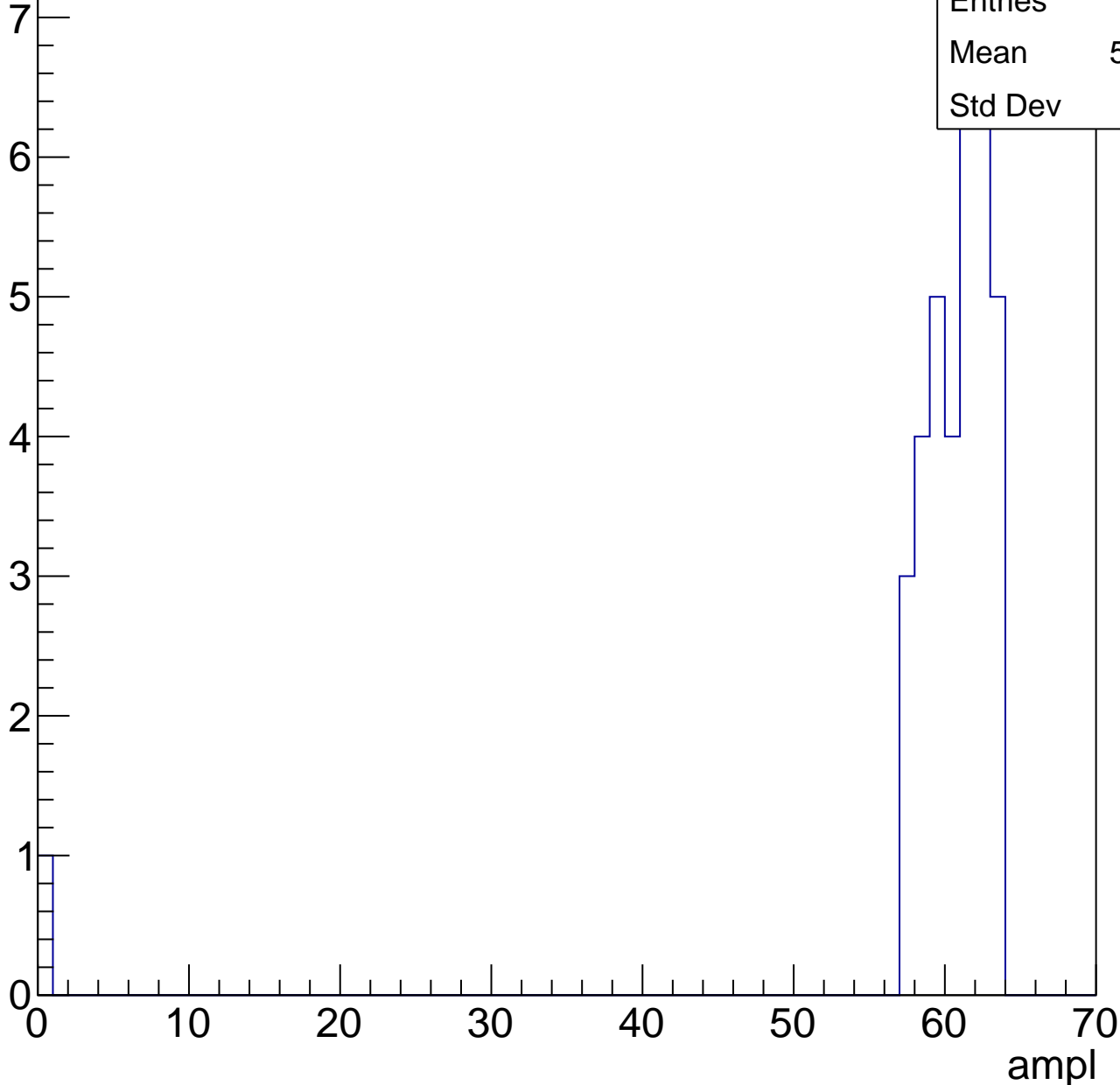
Entries	50
Mean	57.36
Std Dev	3.434

B0L000S, U4-ch51, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	36
Mean	58.72
Std Dev	10.1



B0L000S, U4-ch51, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	2
Mean	61
Std Dev	2

B0L000S, U4-ch51, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch52, adc0

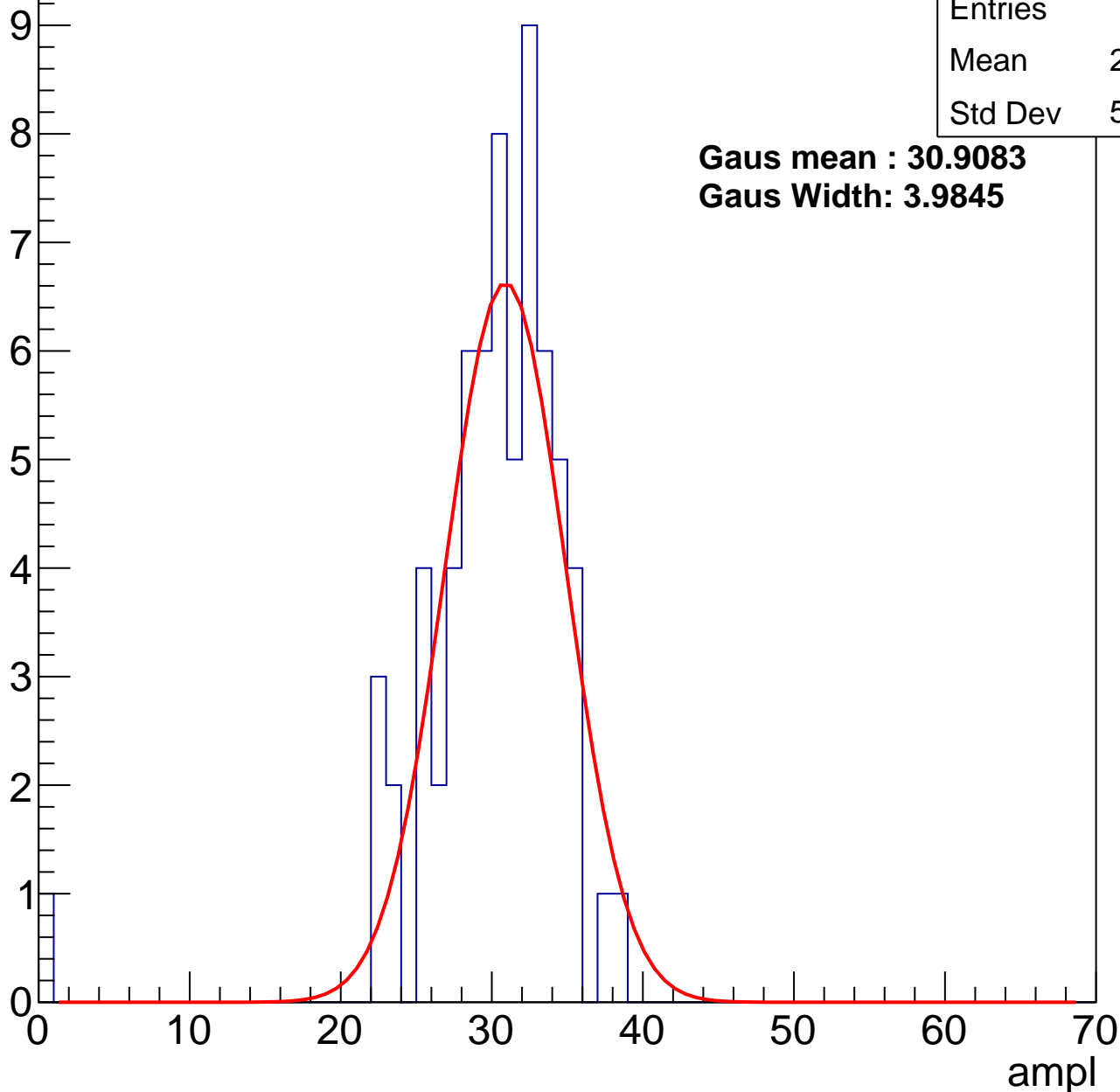
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.55
Std Dev	5.129

Gaus mean : 30.9083

Gaus Width: 3.9845



B0L000S, U4-ch52, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	62
Mean	36.76
Std Dev	3.5

Gaus mean : 37.4555

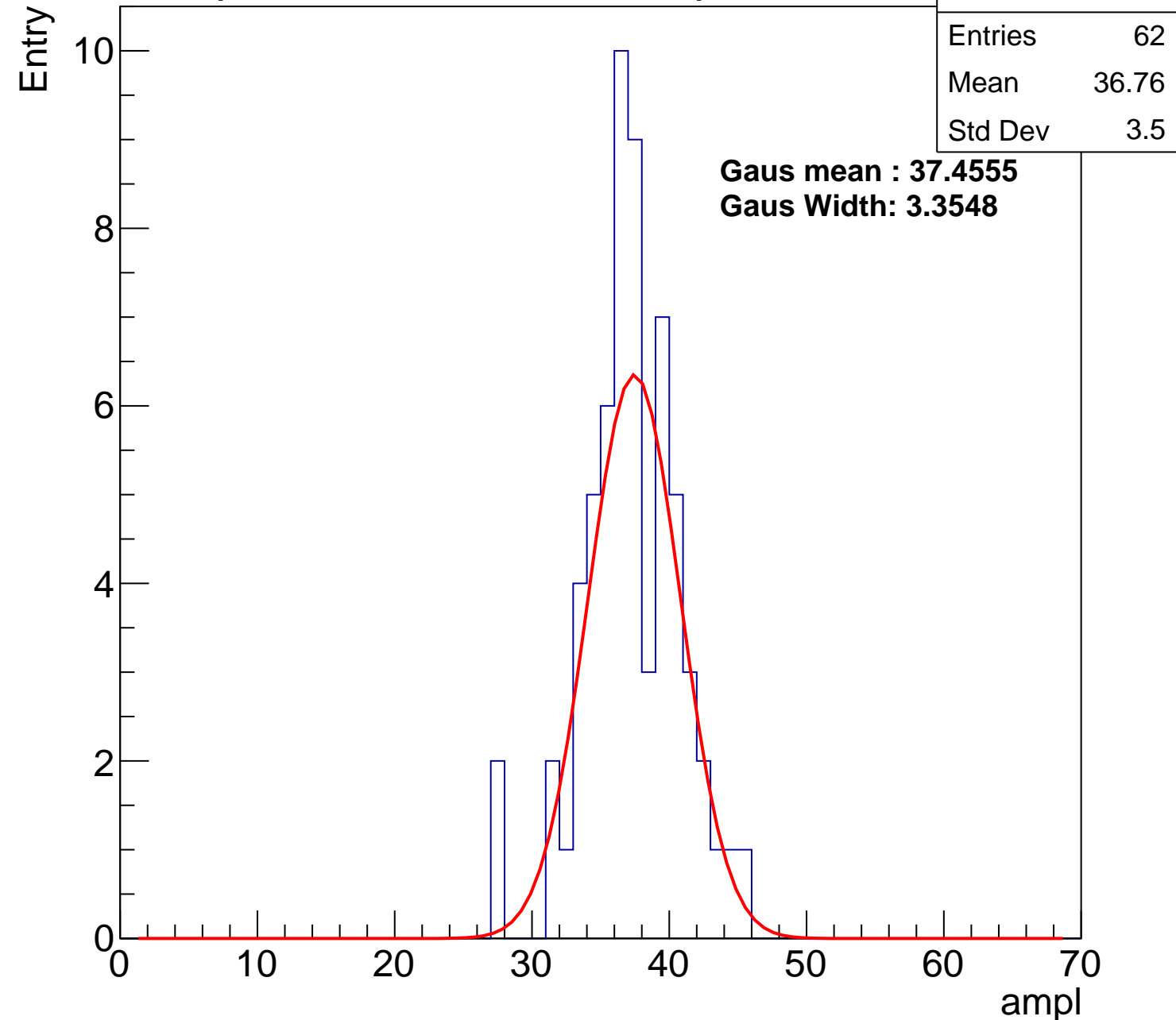
Gaus Width: 3.3548

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch52, adc2

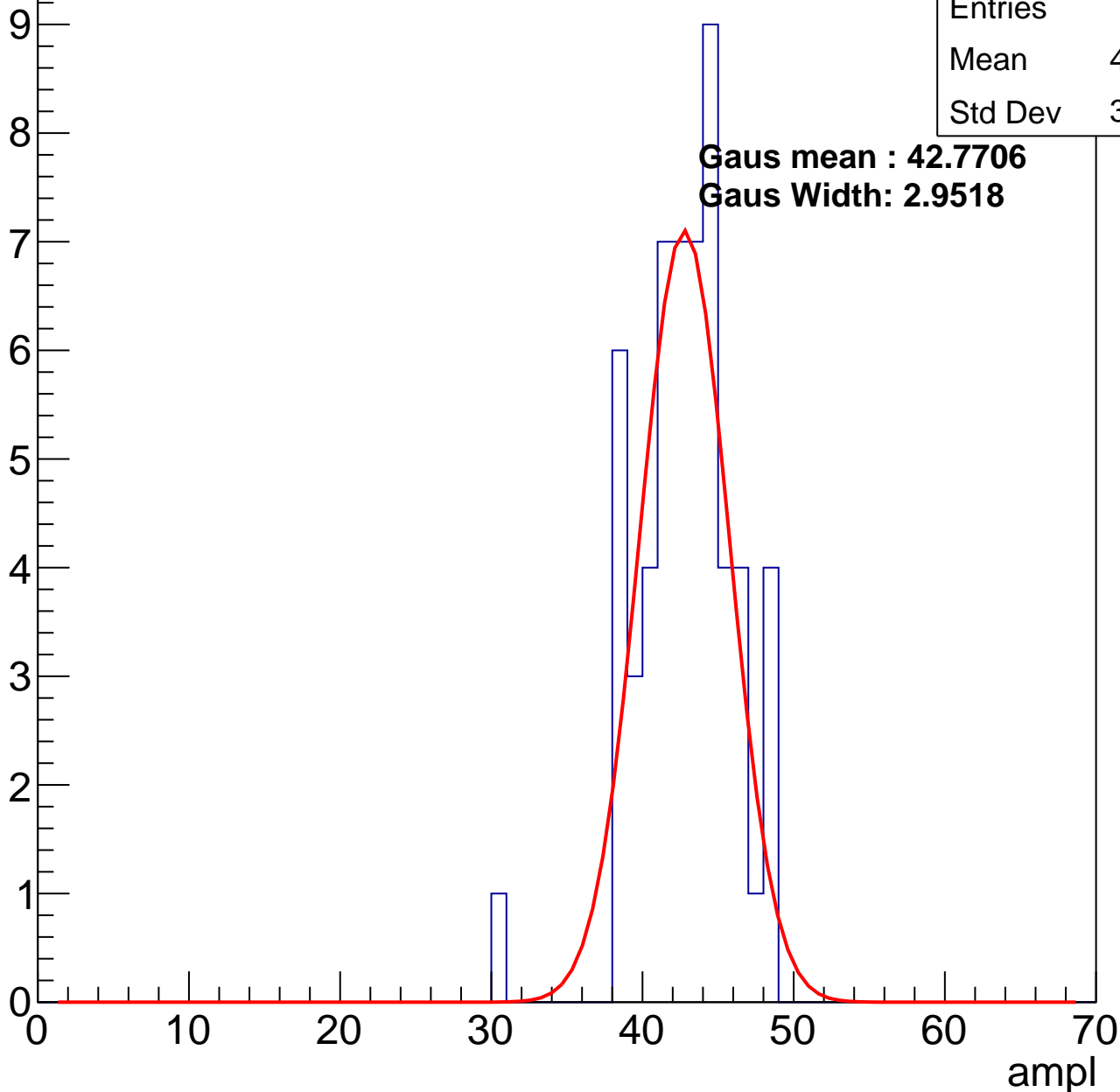
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.39
Std Dev	3.227

Gaus mean : 42.7706

Gaus Width: 2.9518

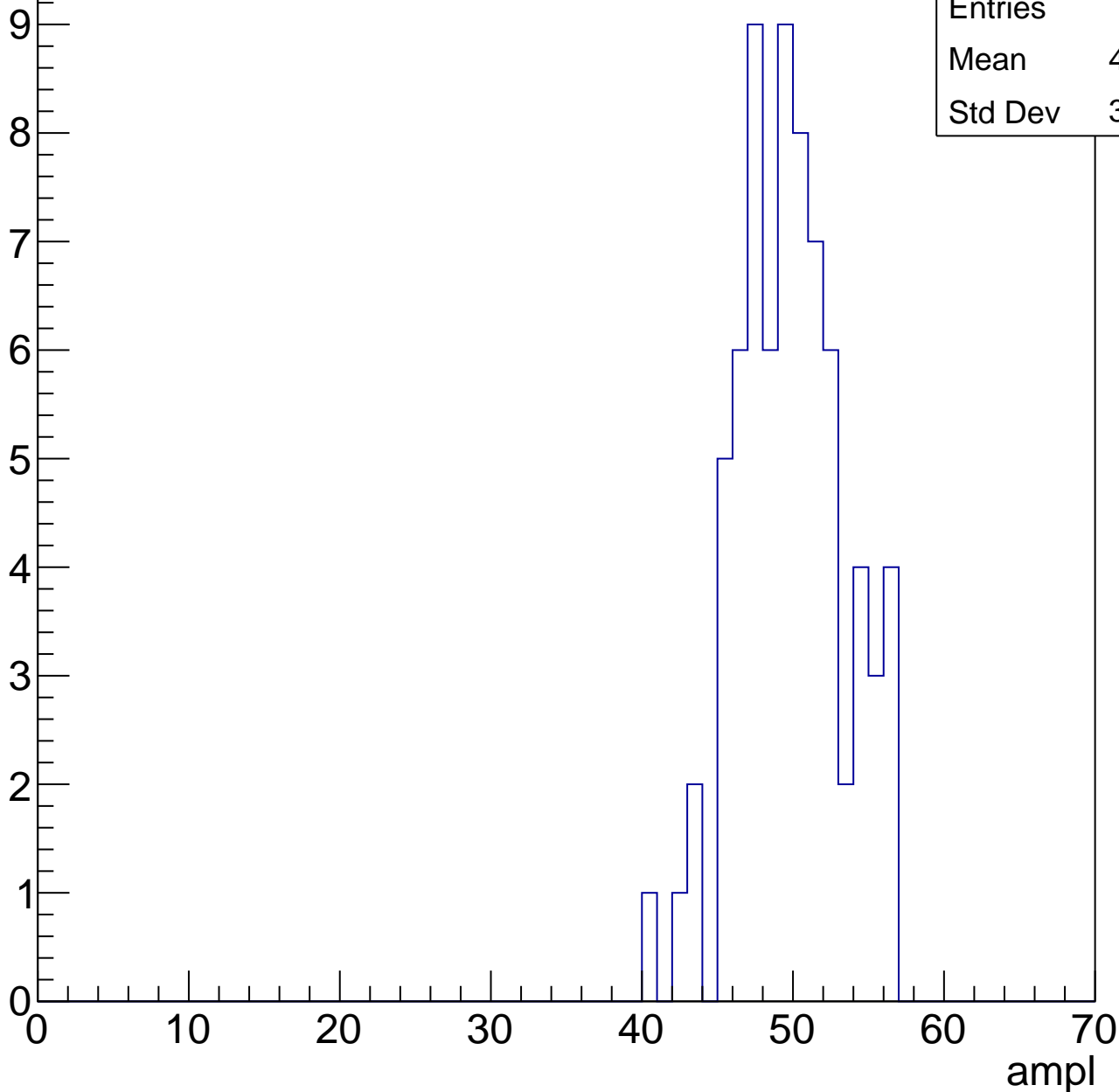


B0L000S, U4-ch52, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	49.33
Std Dev	3.504

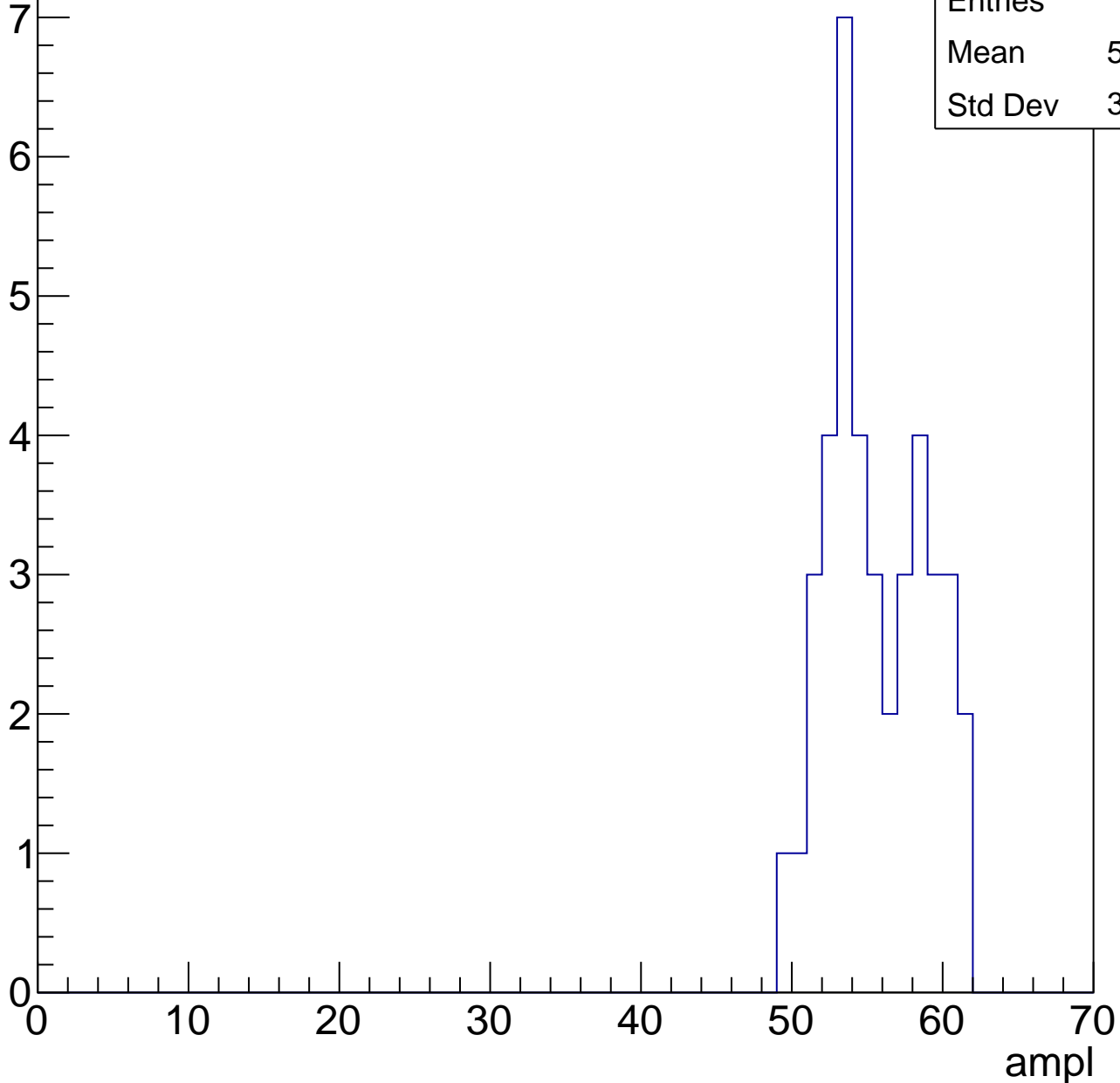


B0L000S, U4-ch52, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

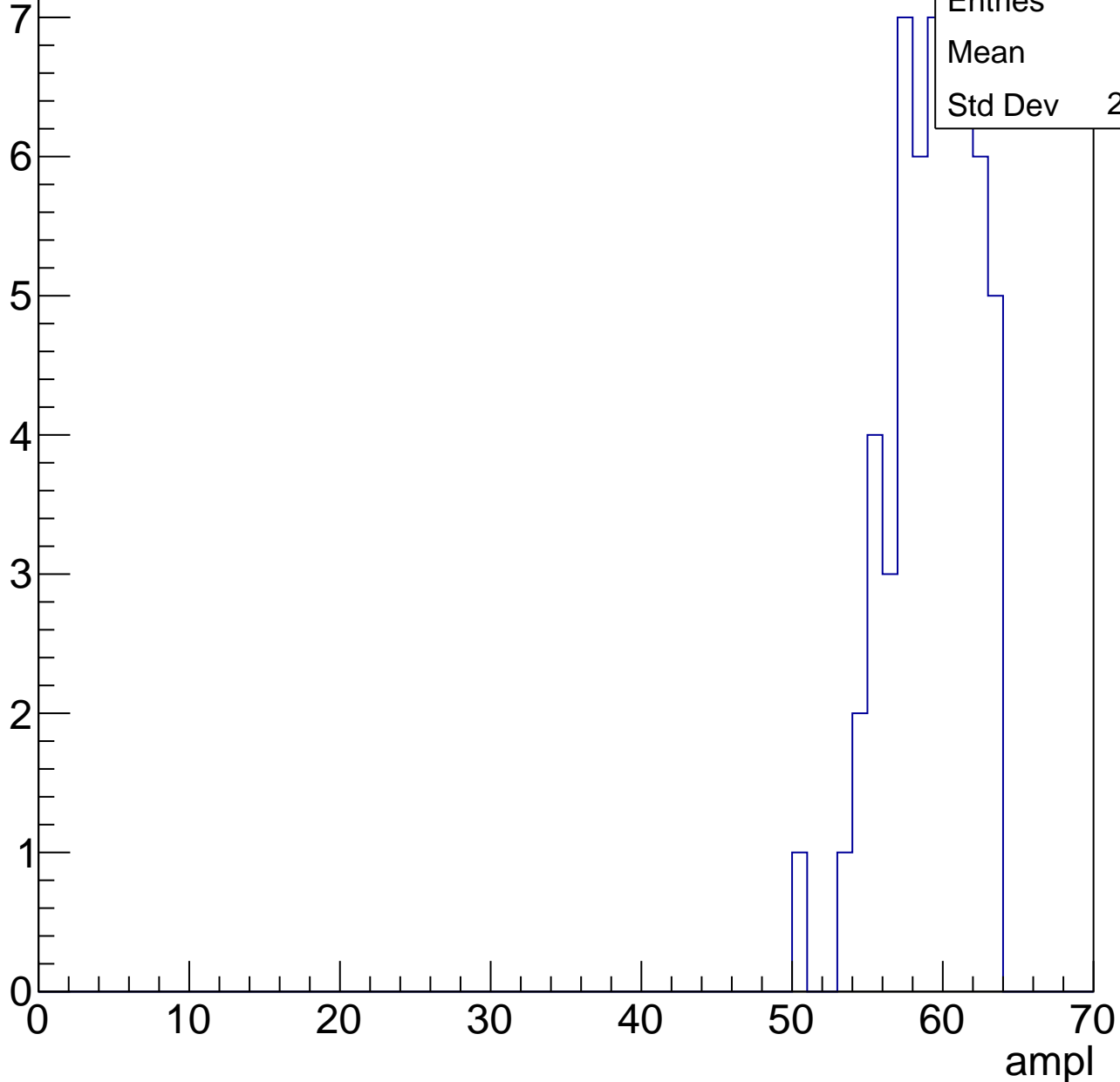
Entries	40
Mean	55.15
Std Dev	3.245



B0L000S, U4-ch52, adc5

calib_packv5_042523_0143.root, FC#5, port B1

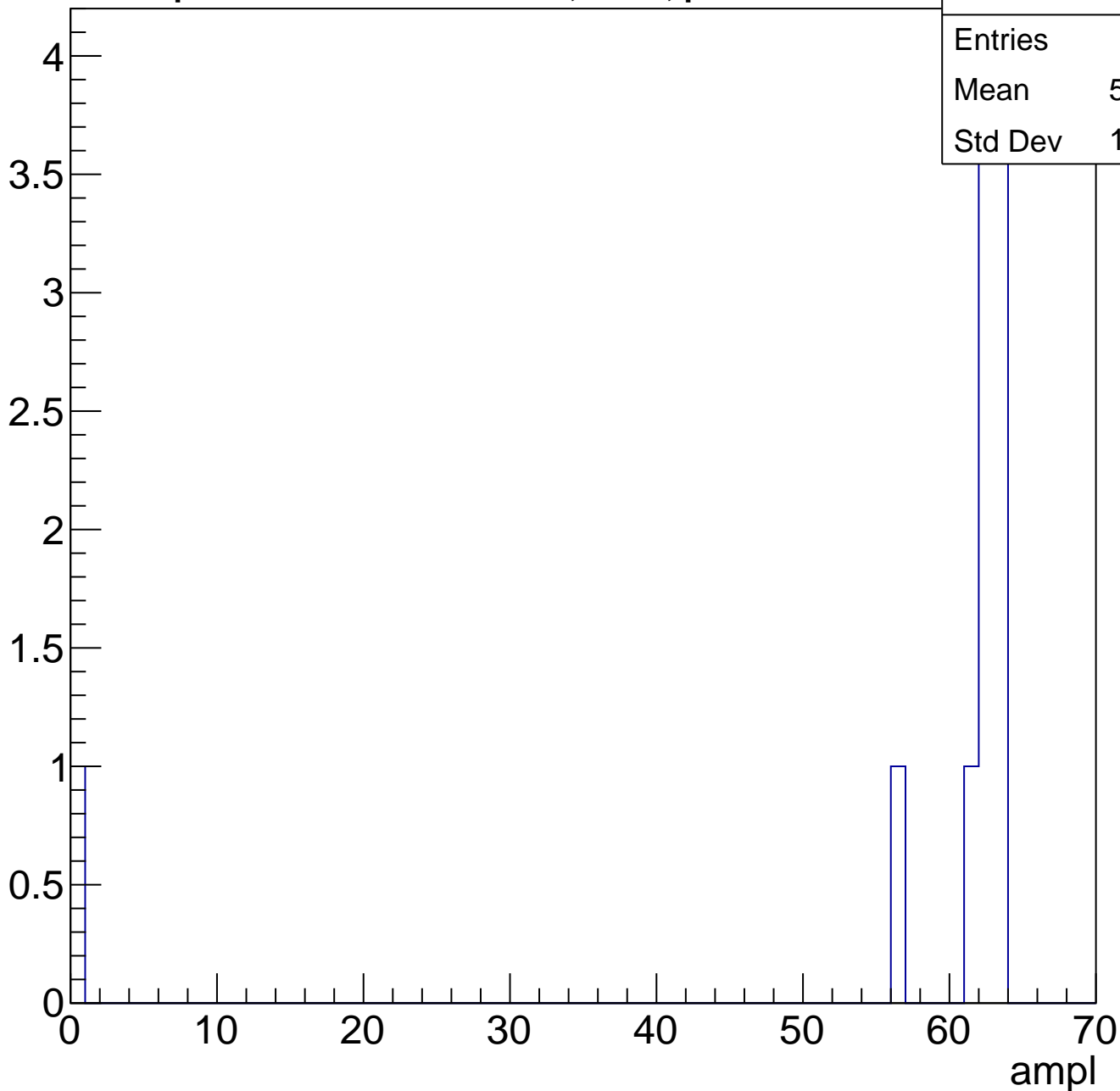
Entry



B0L000S, U4-ch52, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch52, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch53, adc0

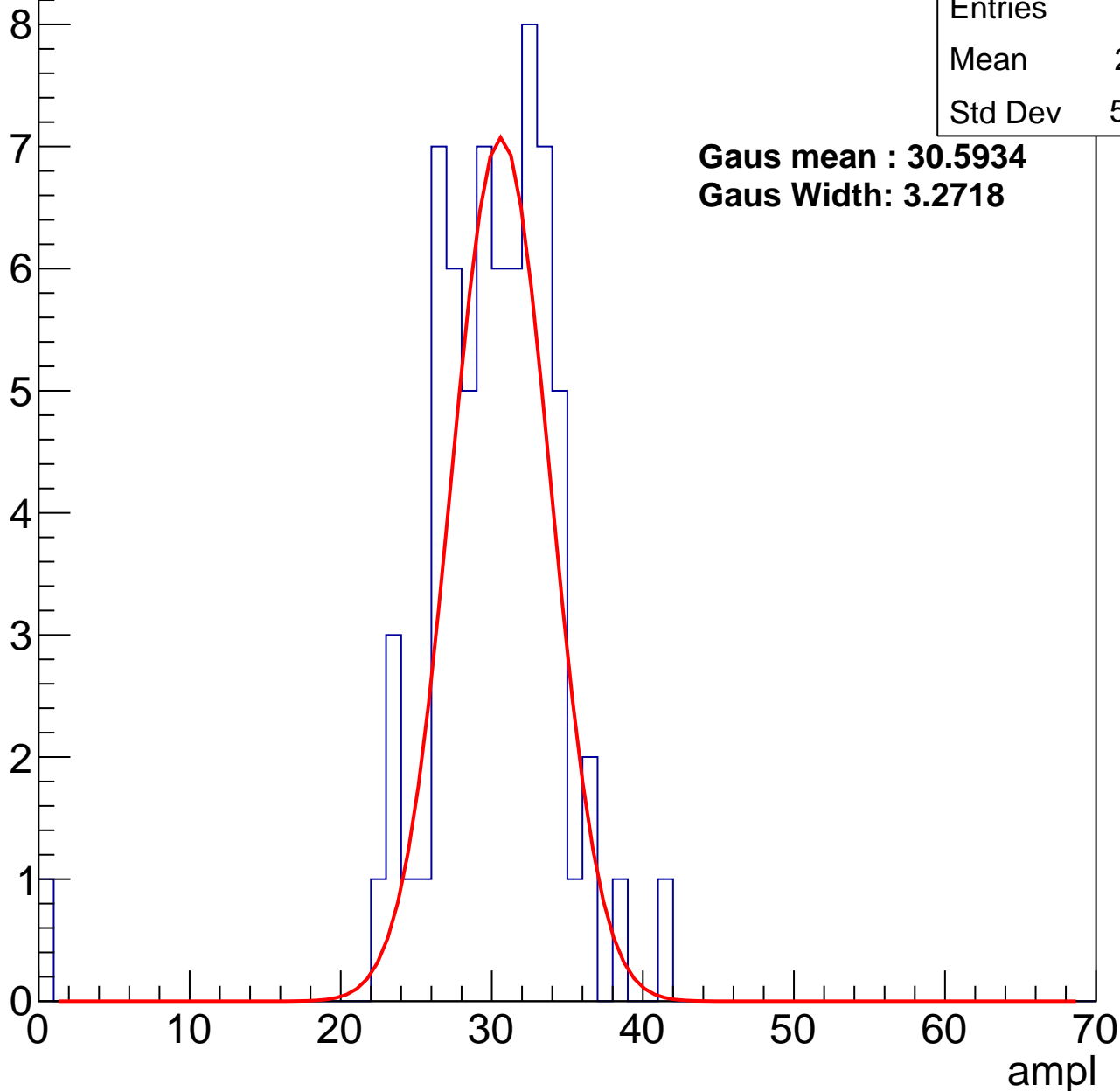
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.51
Std Dev	5.123

Gaus mean : 30.5934

Gaus Width: 3.2718



B0L000S, U4-ch53, adc1

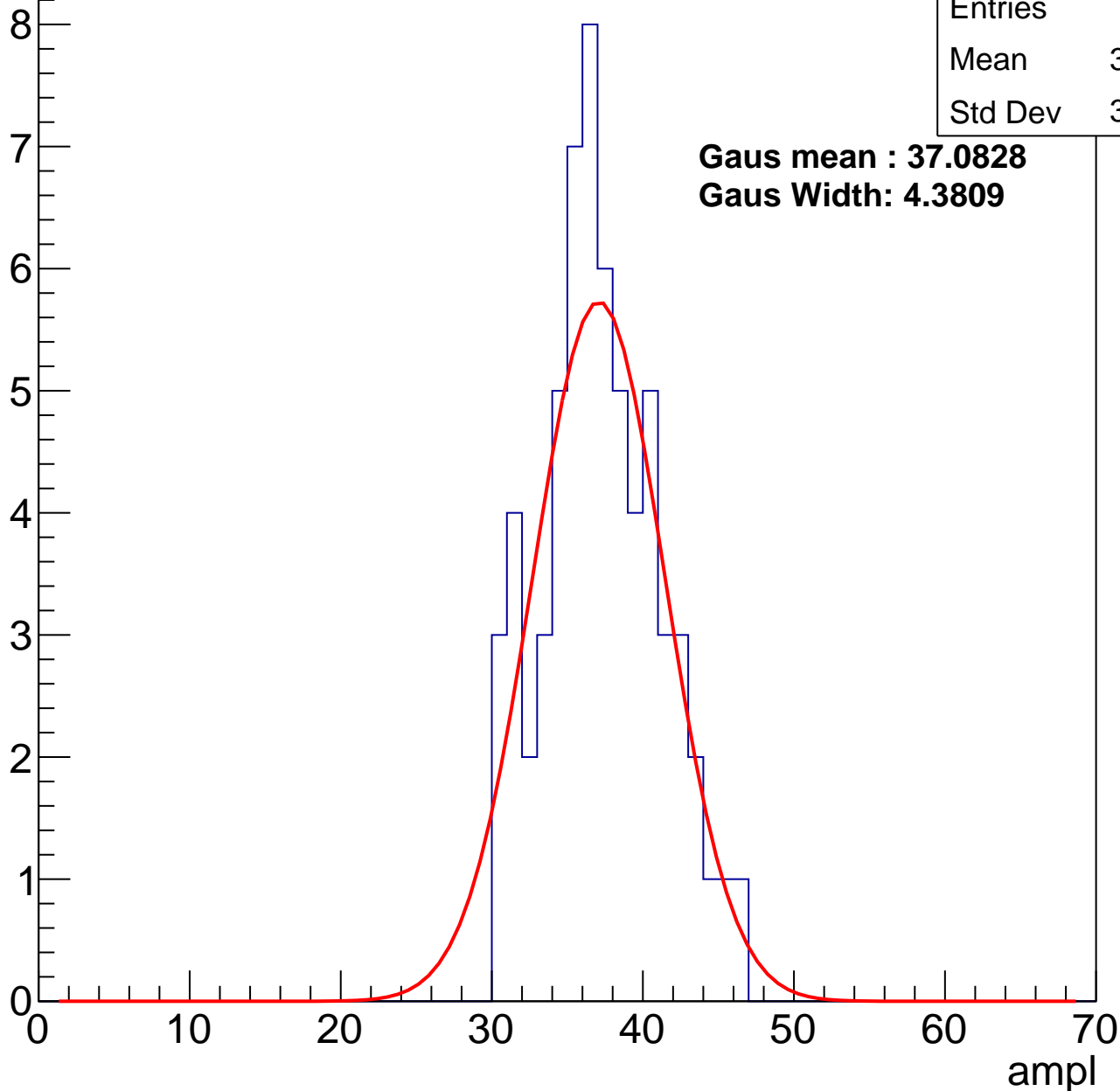
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.79
Std Dev	3.826

Gaus mean : 37.0828

Gaus Width: 4.3809



B0L000S, U4-ch53, adc2

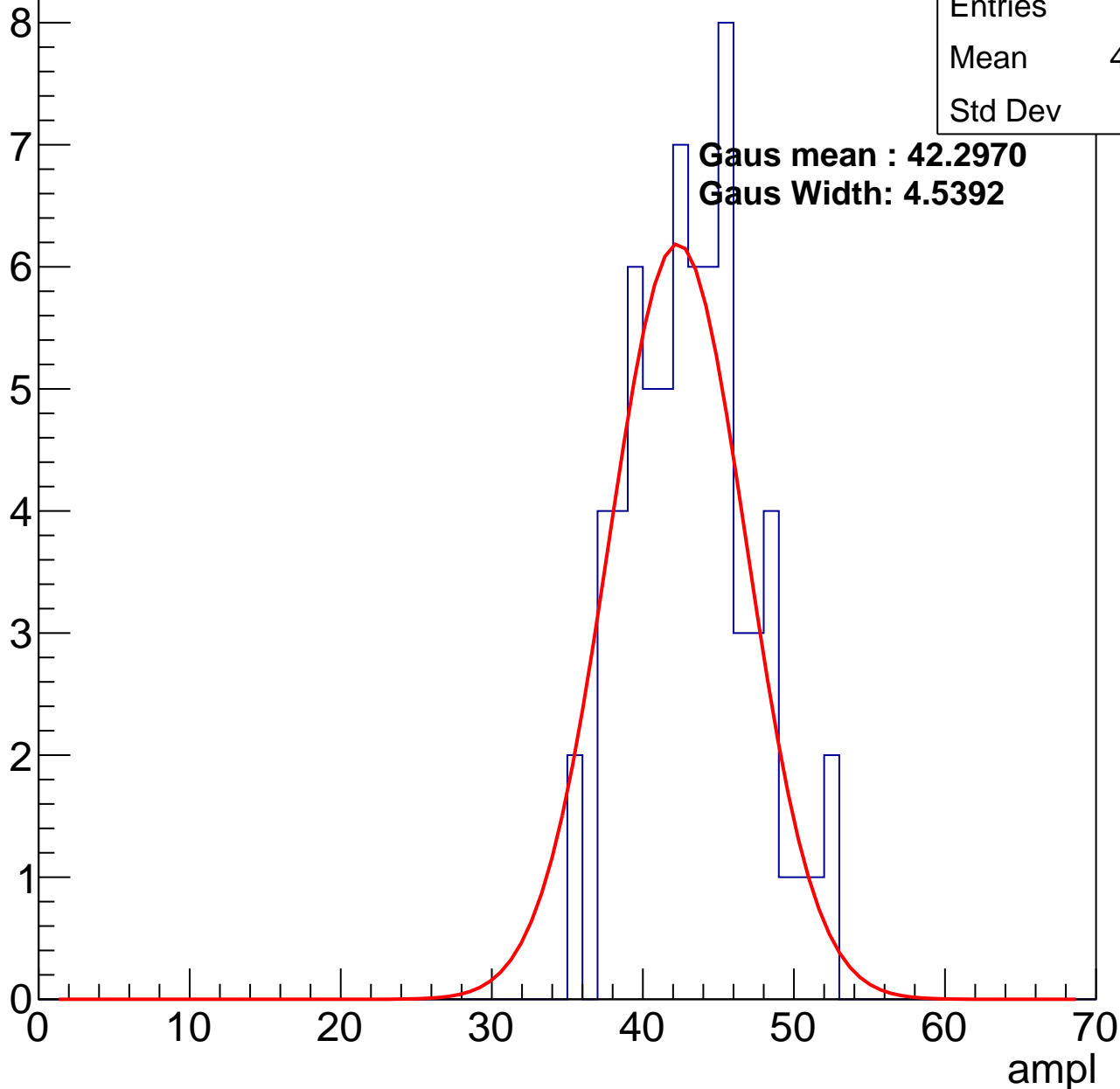
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.79
Std Dev	3.95

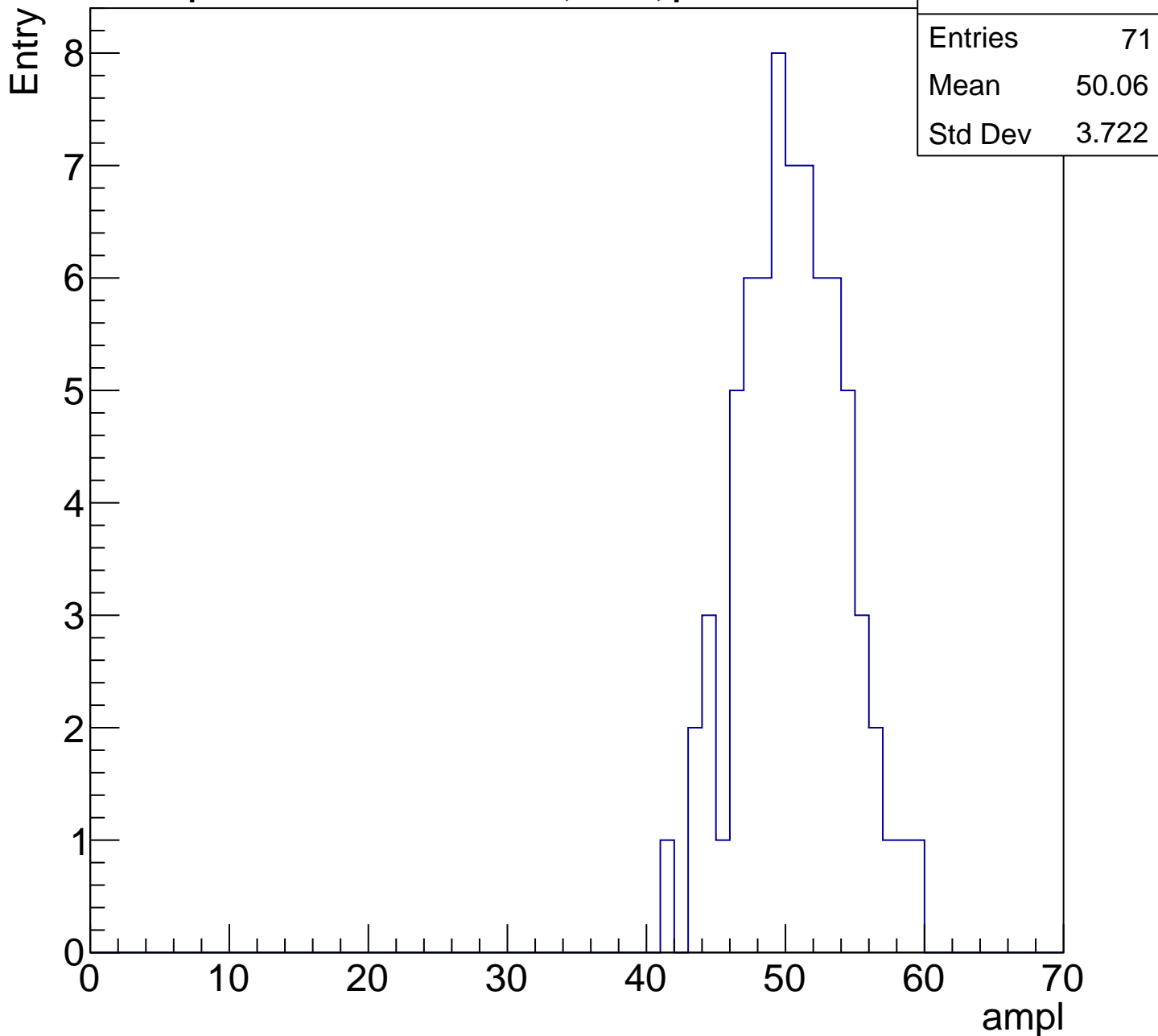
Gaus mean : 42.2970

Gaus Width: 4.5392



B0L000S, U4-ch53, adc3

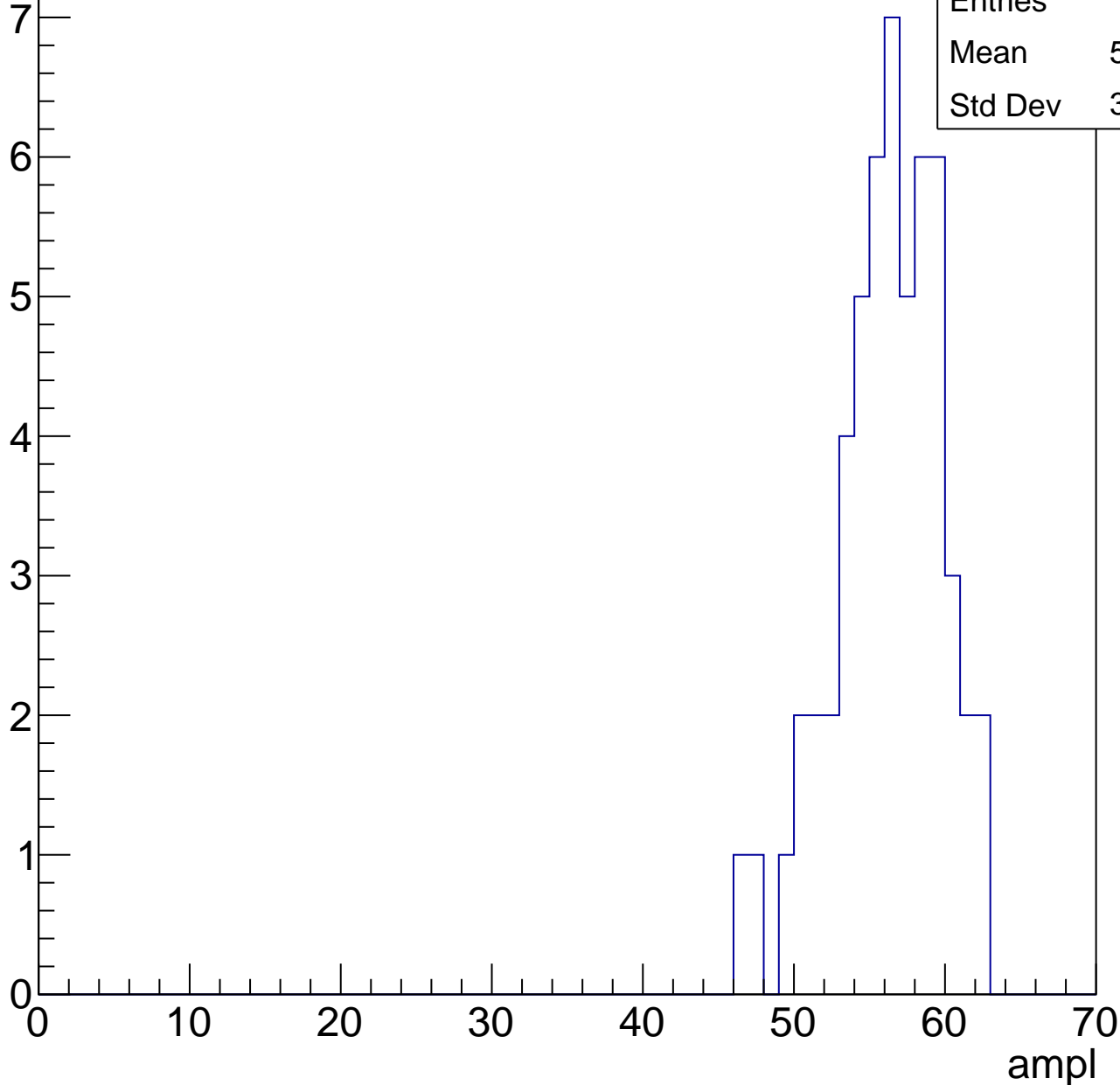
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch53, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

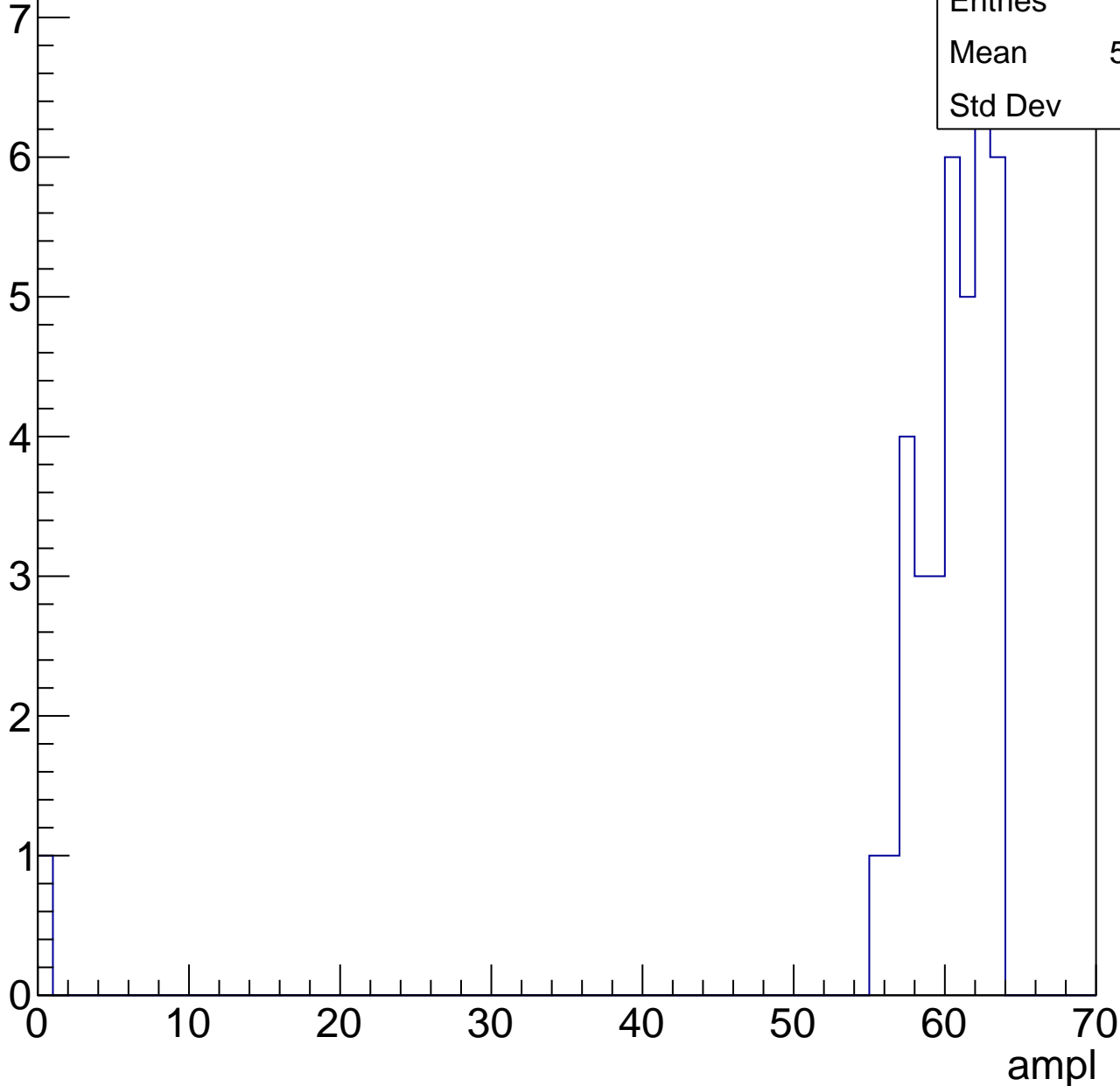


B0L000S, U4-ch53, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

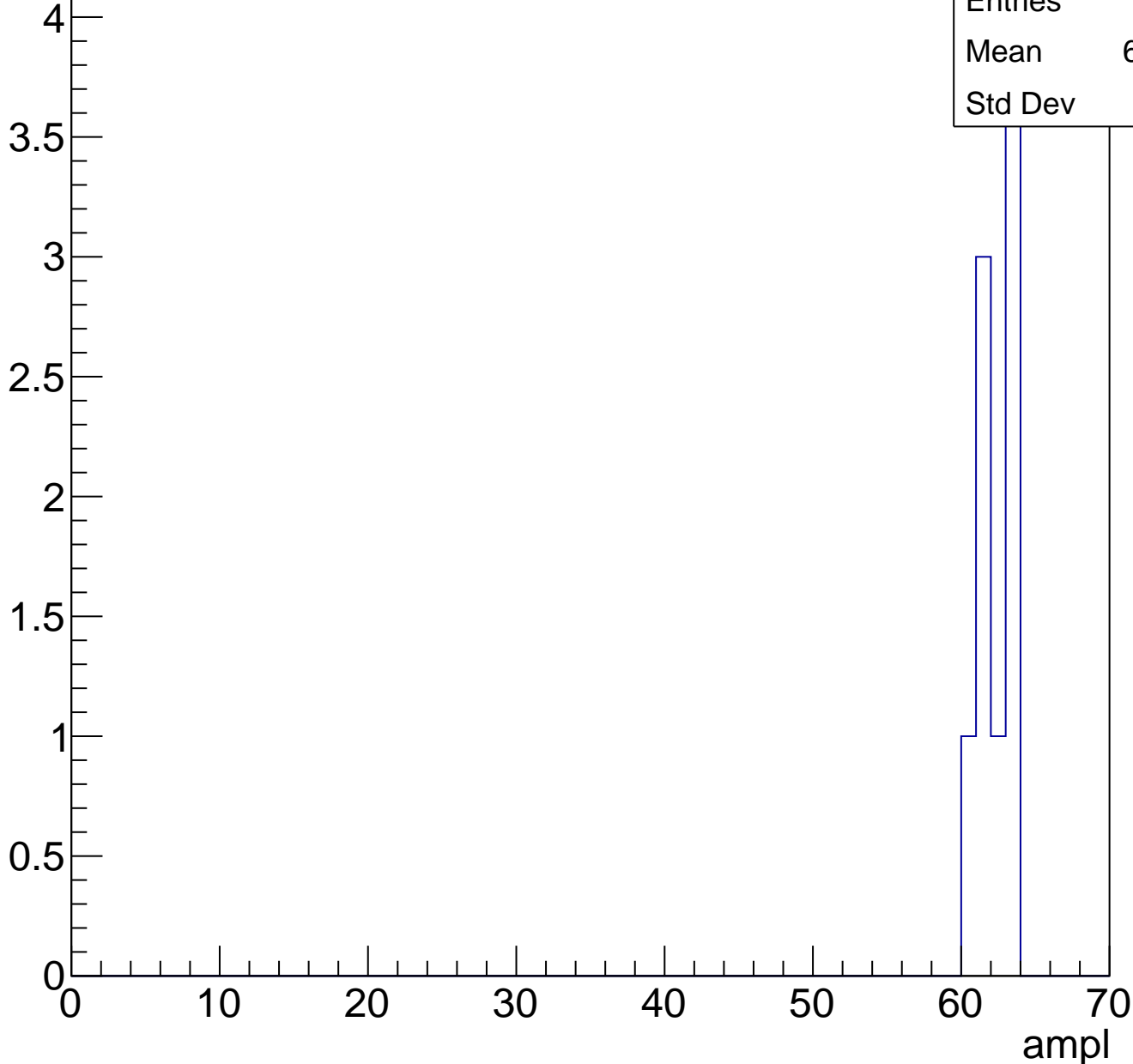
Entries	37
Mean	58.57
Std Dev	10



B0L000S, U4-ch53, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch53, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch54, adc0

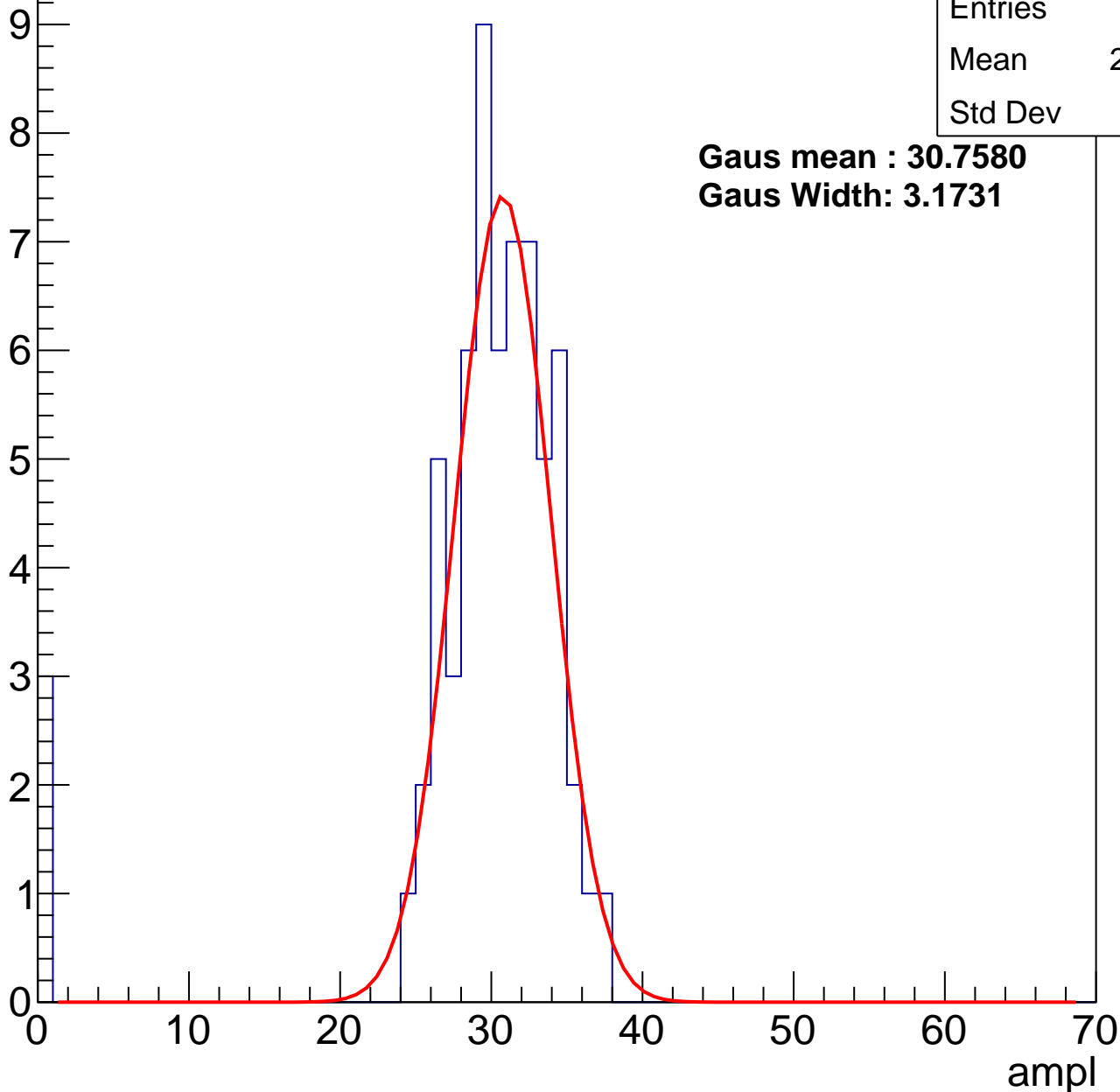
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	28.86
Std Dev	7.02

Gaus mean : 30.7580

Gaus Width: 3.1731



B0L000S, U4-ch54, adc1

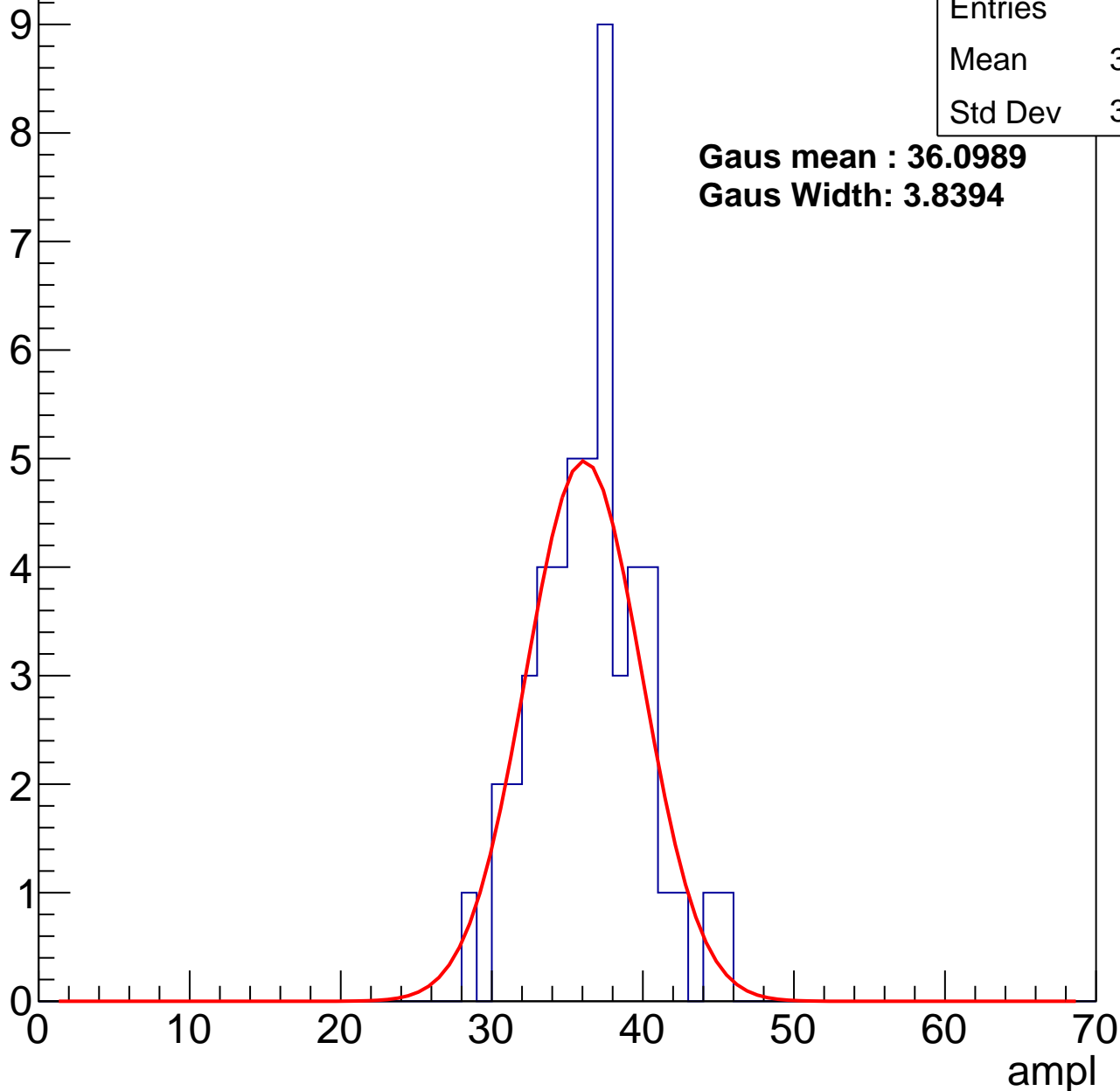
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	36.08
Std Dev	3.515

Gaus mean : 36.0989

Gaus Width: 3.8394



B0L000S, U4-ch54, adc2

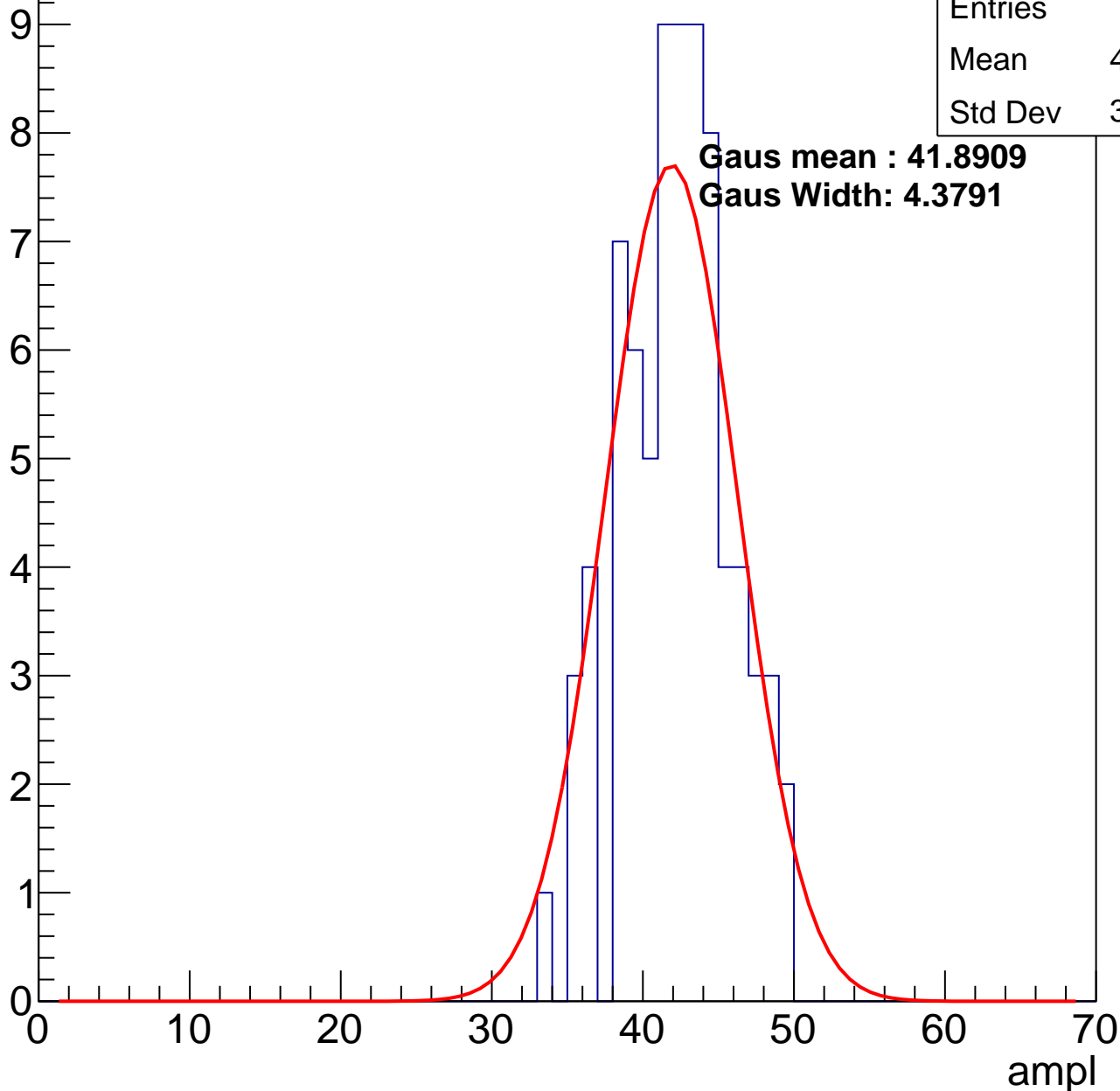
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	41.75
Std Dev	3.575

Gaus mean : 41.8909

Gaus Width: 4.3791



B0L000S, U4-ch54, adc3

calib_packv5_042523_0143.root, FC#5, port B1

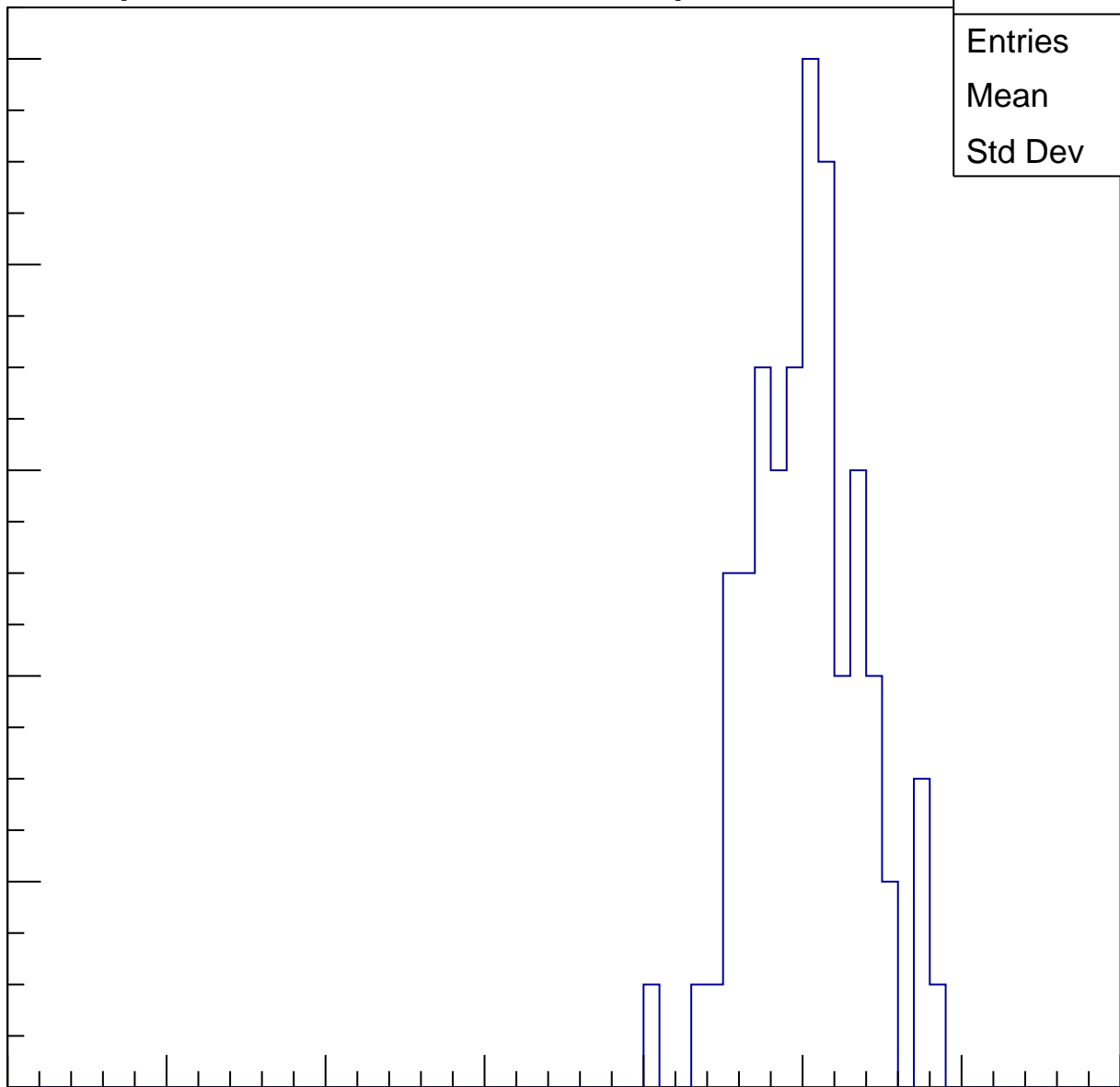
Entries	72
Mean	49.75
Std Dev	3.503

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

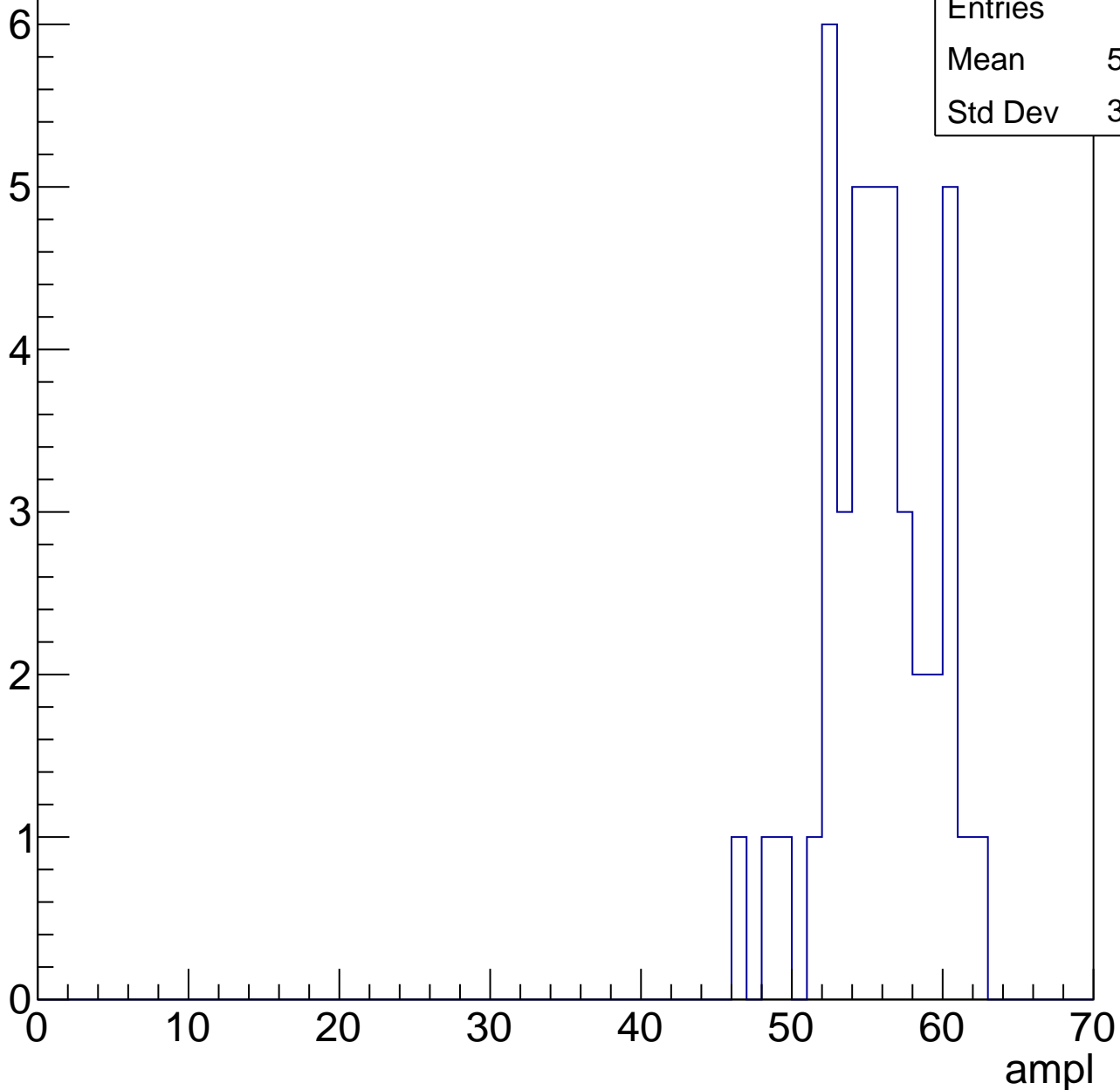


B0L000S, U4-ch54, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	42
Mean	55.19
Std Dev	3.547



B0L000S, U4-ch54, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 59.02

Std Dev 2.681

0

0

10

20

30

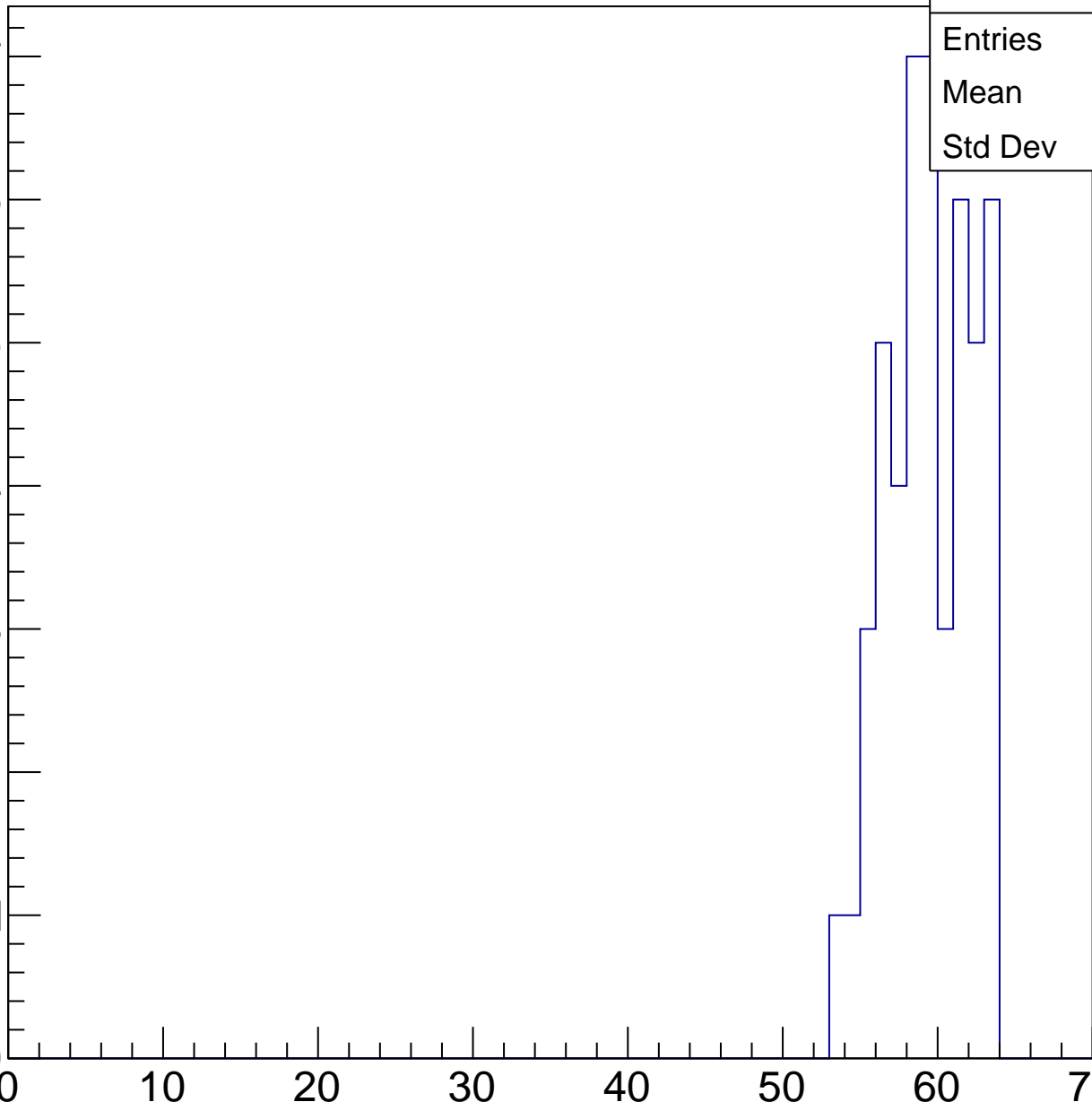
40

50

60

70

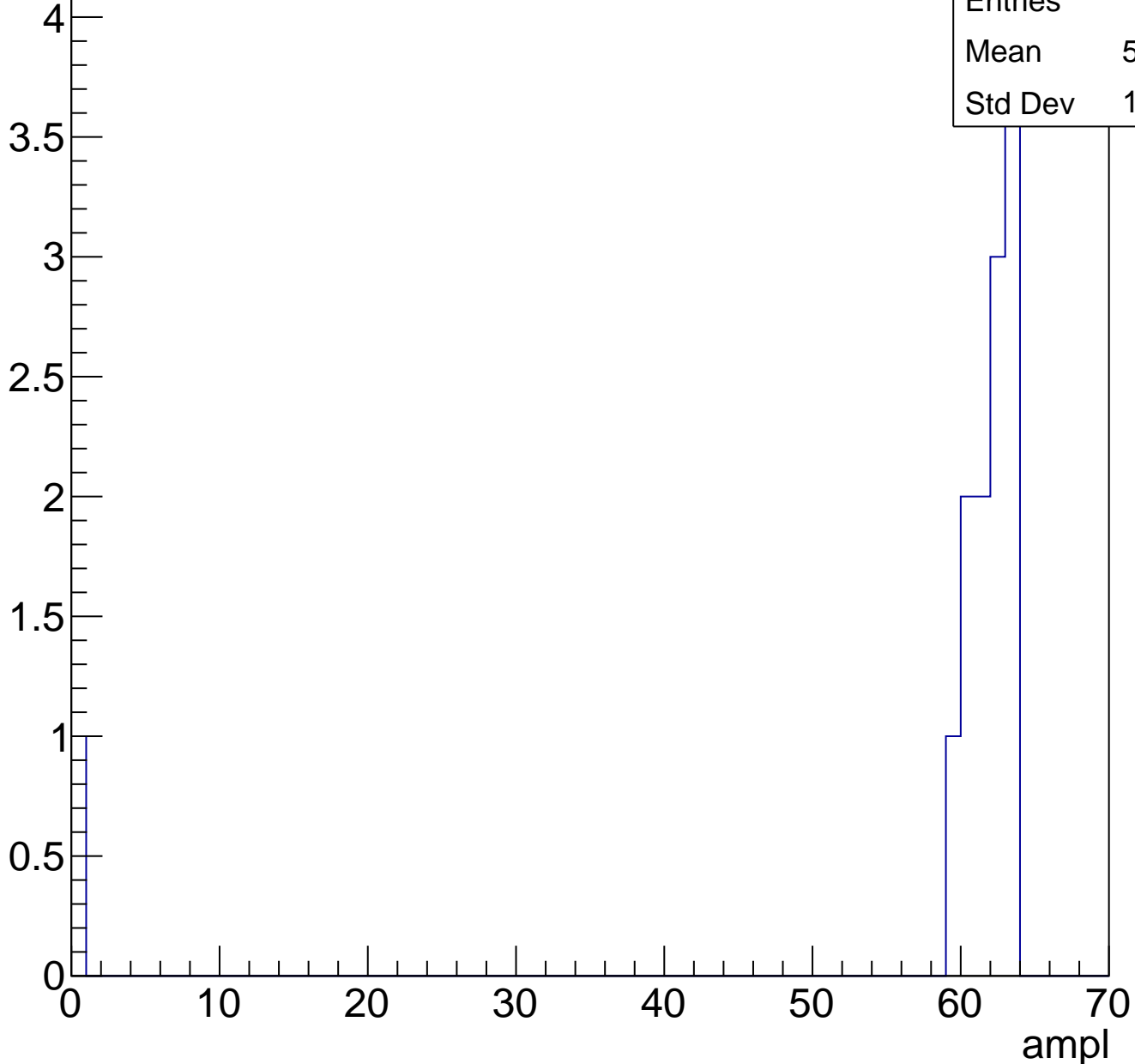
ampl



B0L000S, U4-ch54, adc6

calib_packv5_042523_0143.root, FC#5, port B1

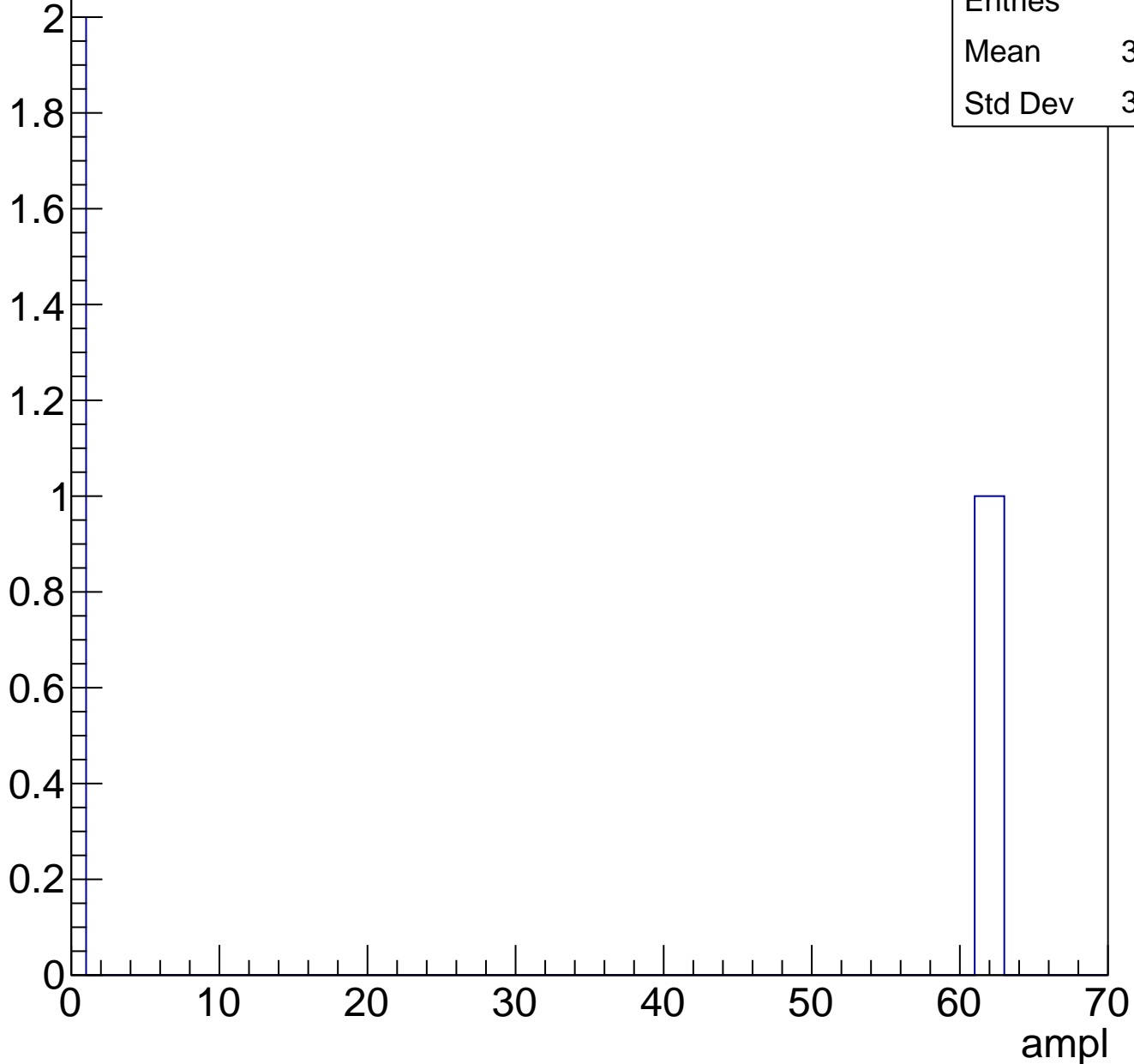
Entry



B0L000S, U4-ch54, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch55, adc0

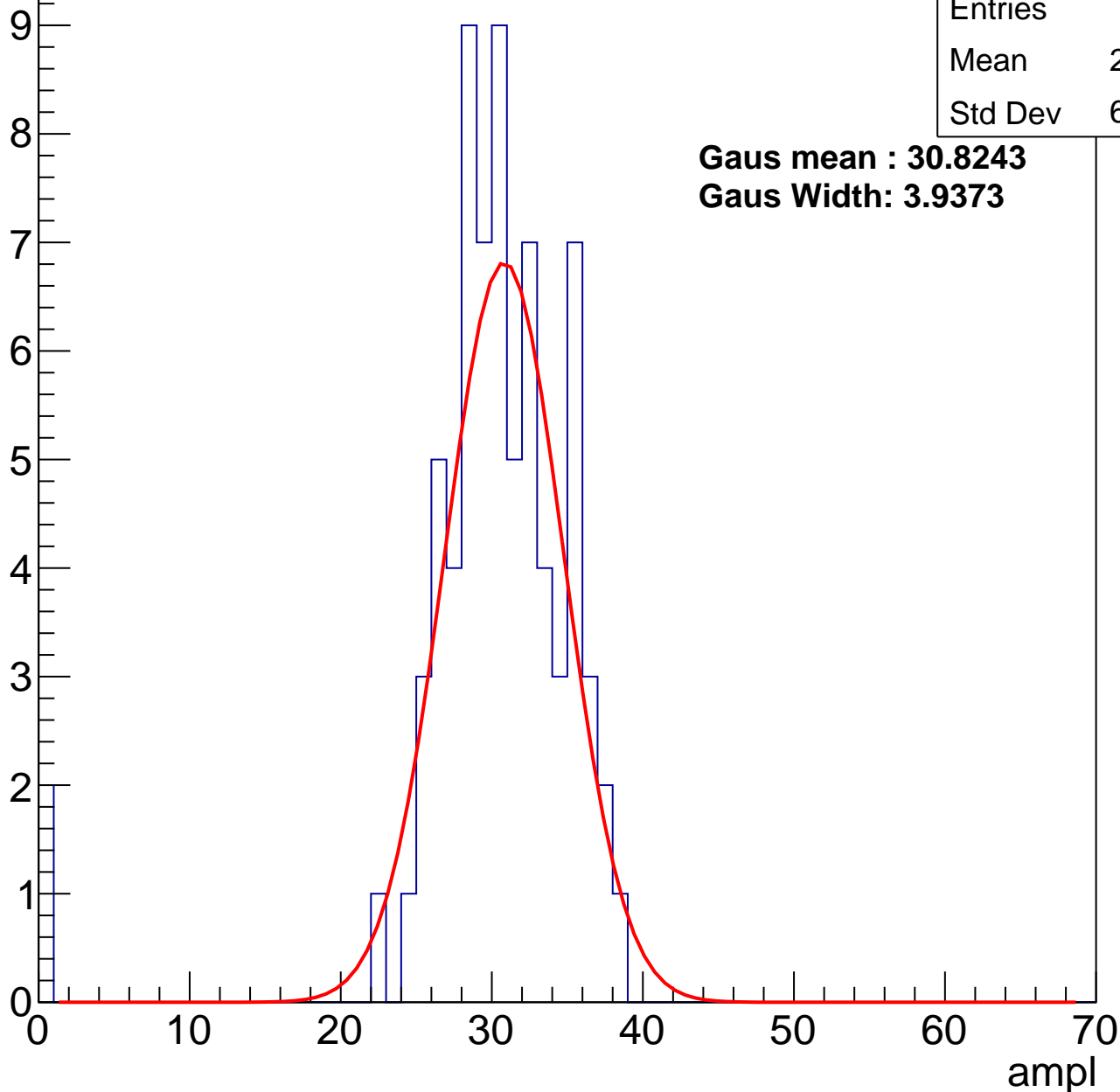
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.62
Std Dev	6.065

Gaus mean : 30.8243

Gaus Width: 3.9373



B0L000S, U4-ch55, adc1

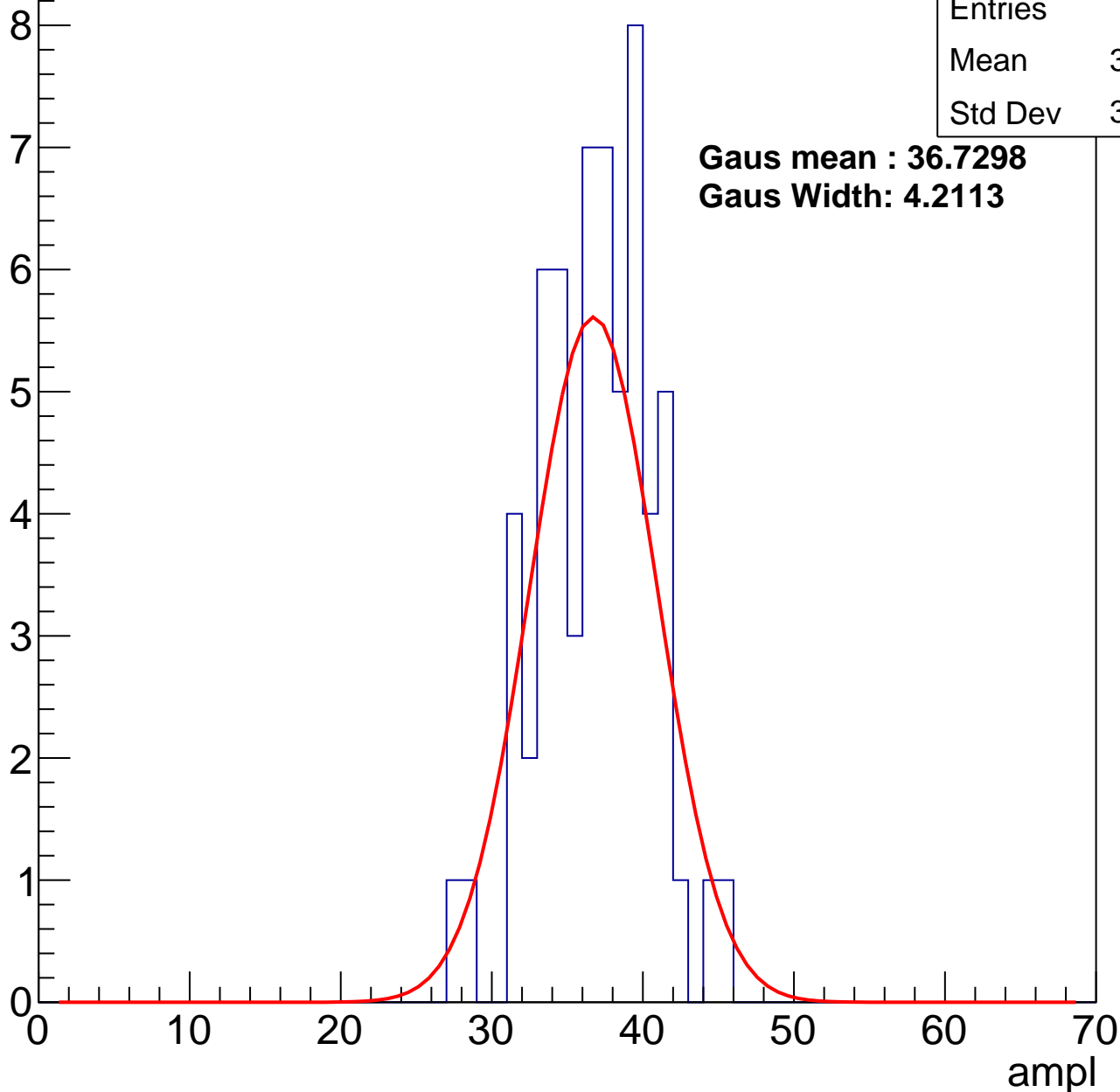
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.44
Std Dev	3.626

Gaus mean : 36.7298

Gaus Width: 4.2113



B0L000S, U4-ch55, adc2

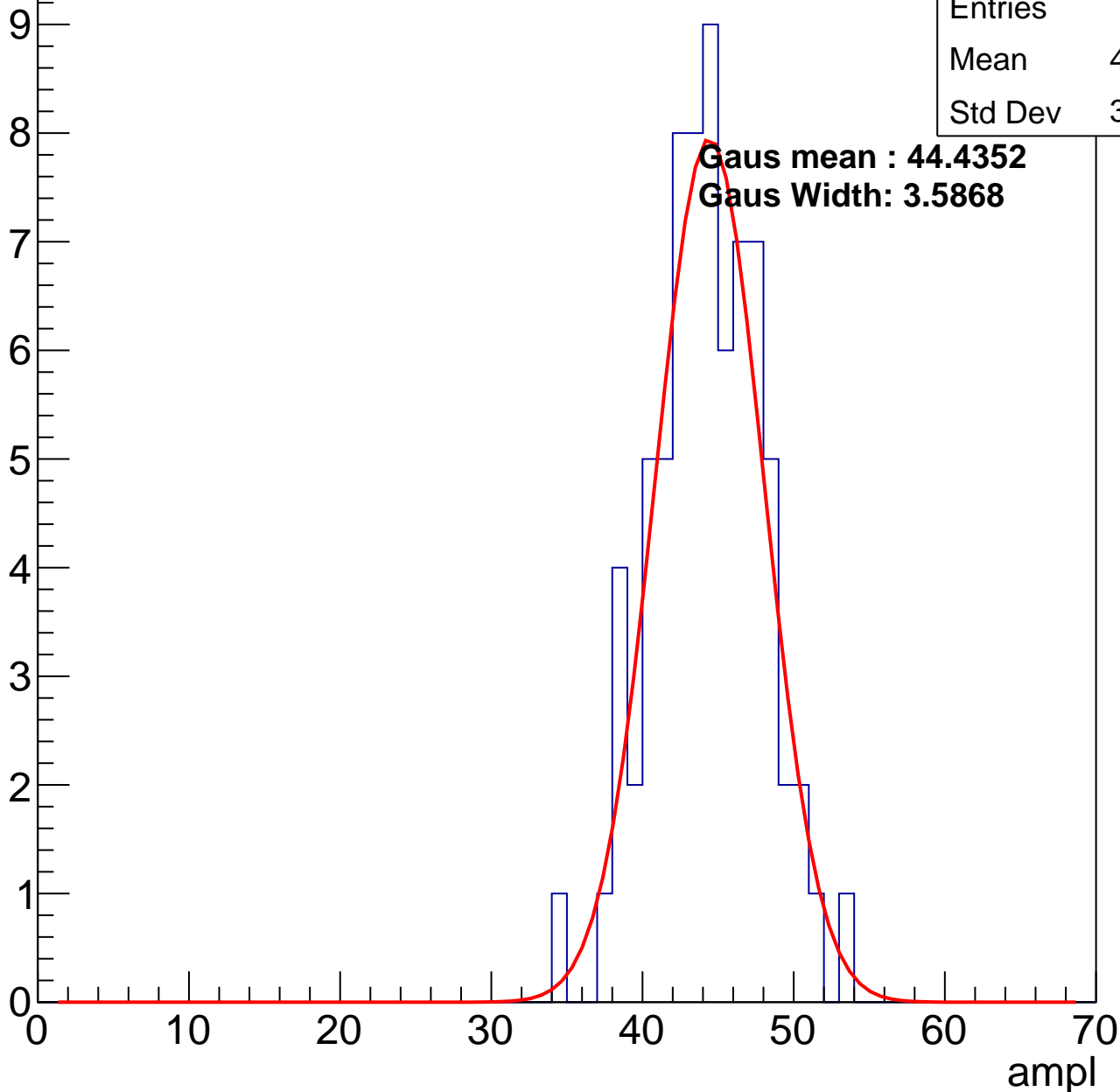
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.85
Std Dev	3.555

Gaus mean : 44.4352

Gaus Width: 3.5868

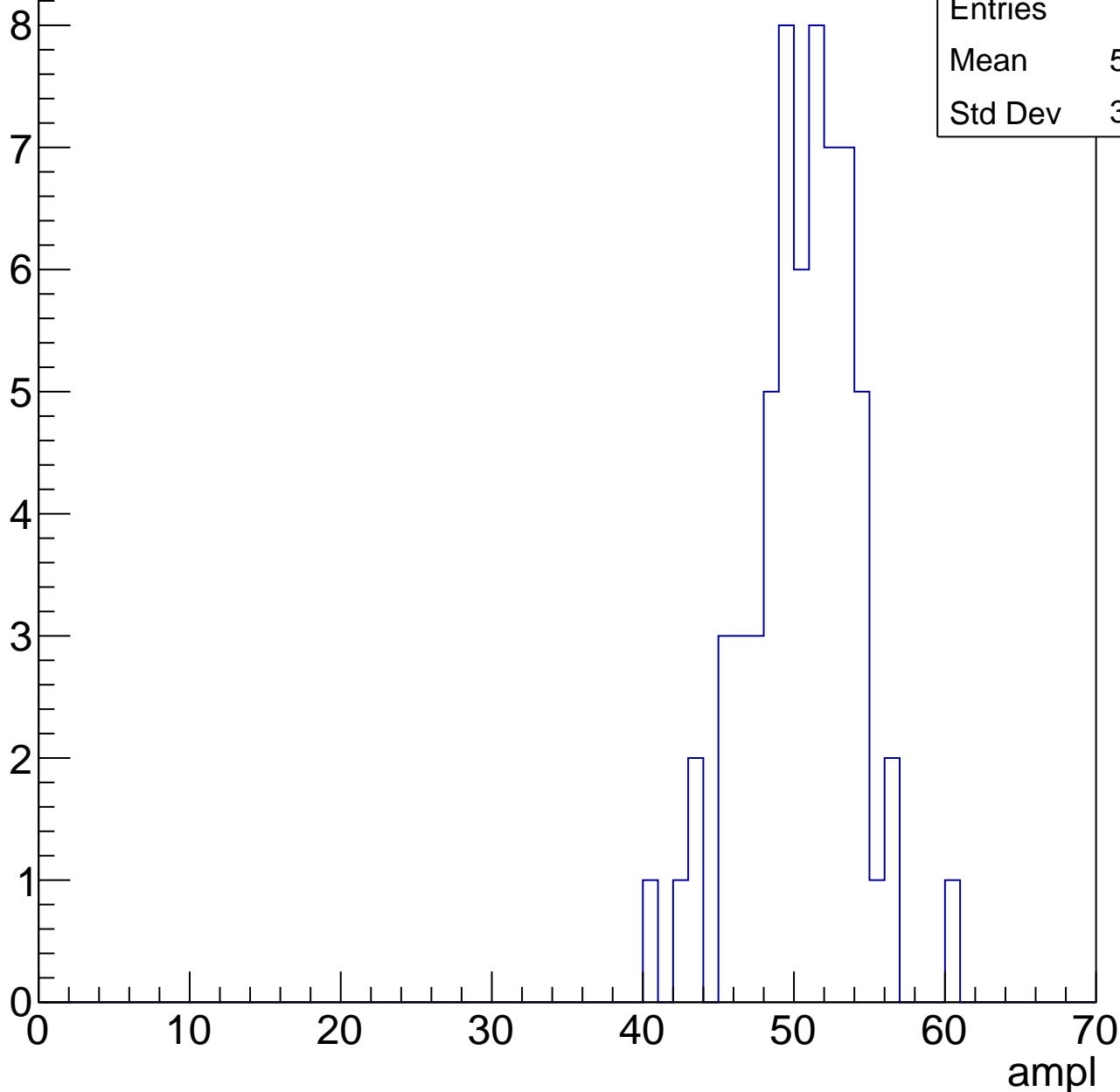


B0L000S, U4-ch55, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

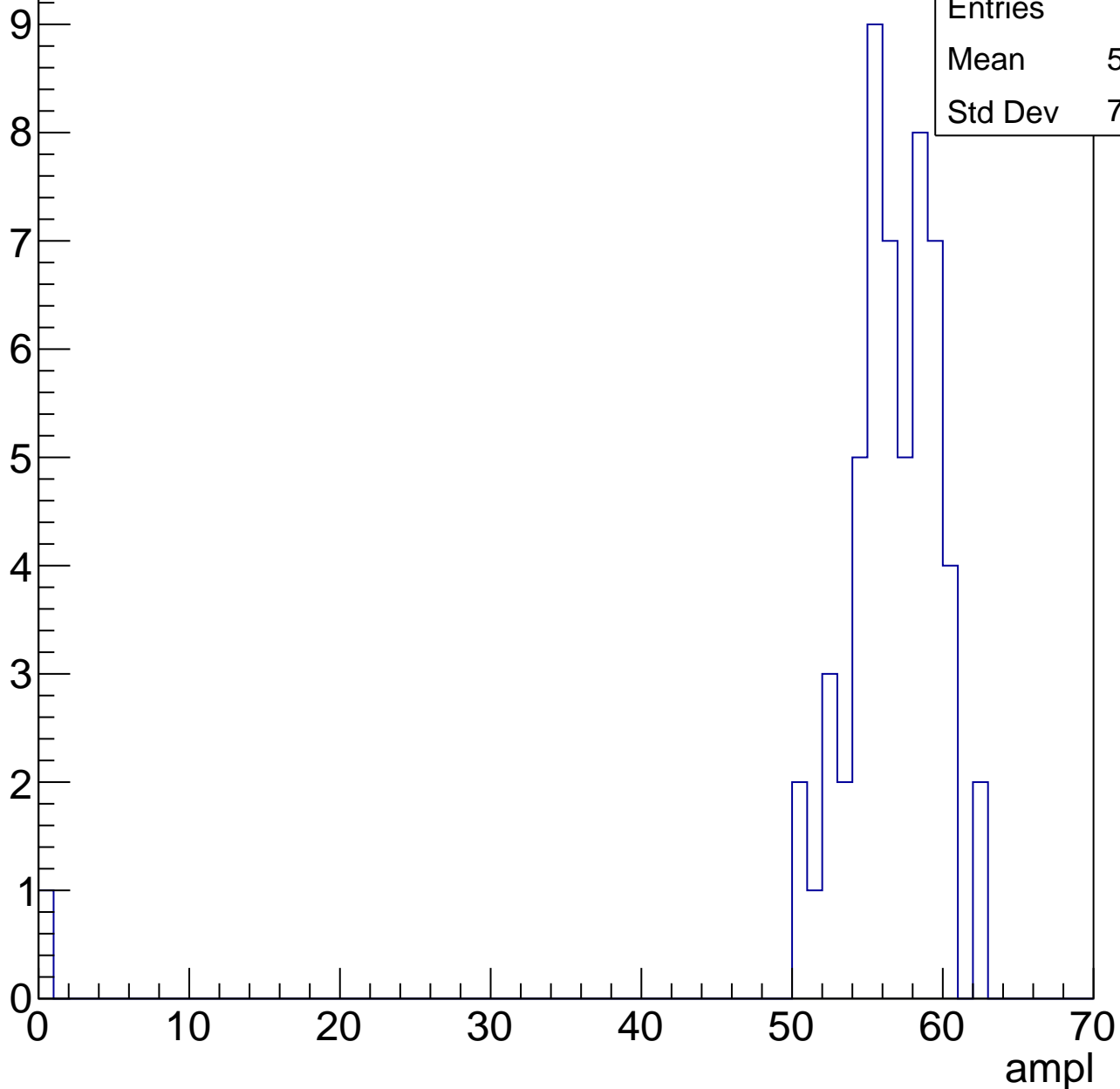
Entries	63
Mean	50.06
Std Dev	3.598



B0L000S, U4-ch55, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	56
Mean	55.29
Std Dev	7.946

B0L000S, U4-ch55, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

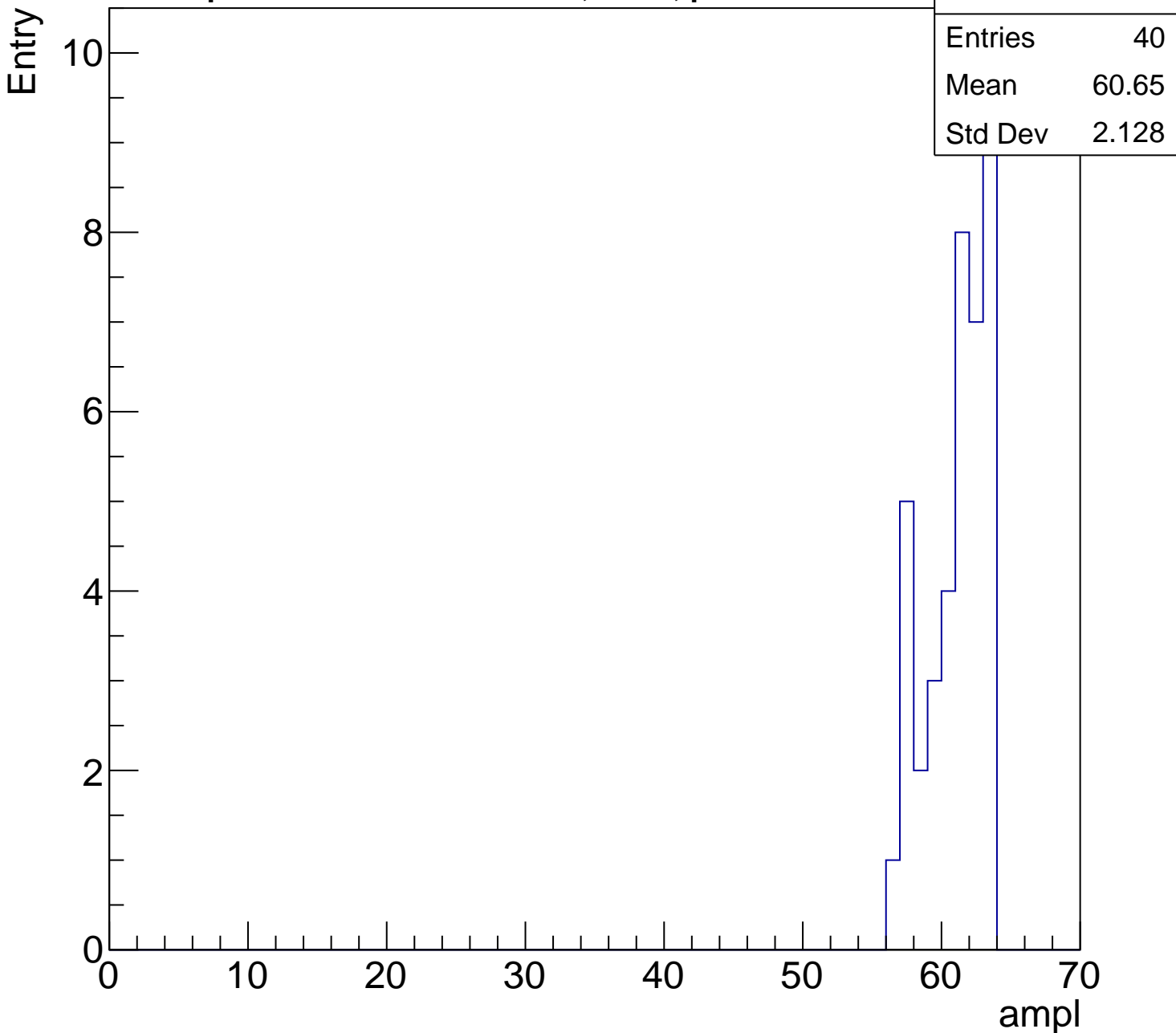
40

50

60

ampl

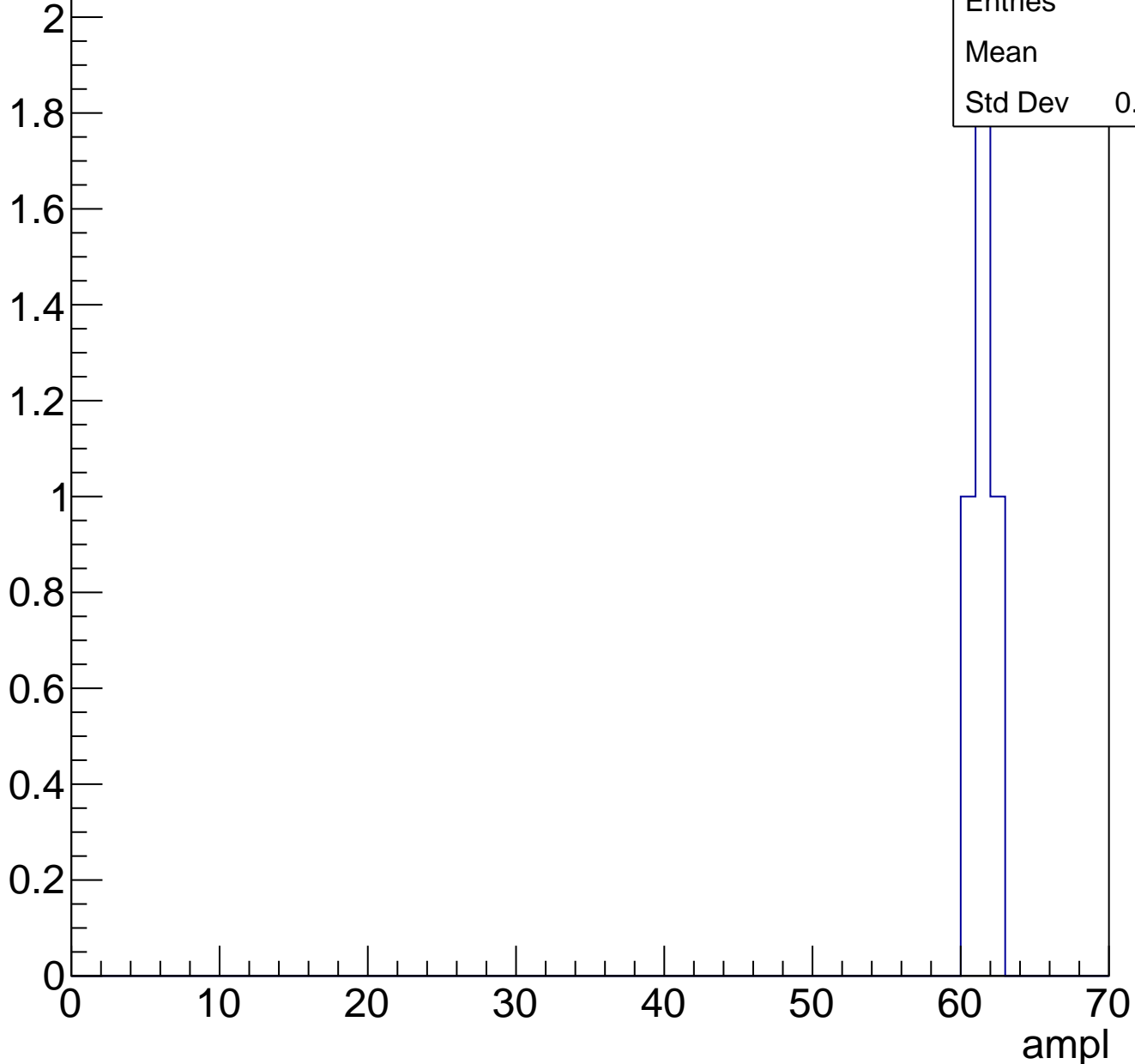
Entries	40
Mean	60.65
Std Dev	2.128



B0L000S, U4-ch55, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch55, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch56, adc0

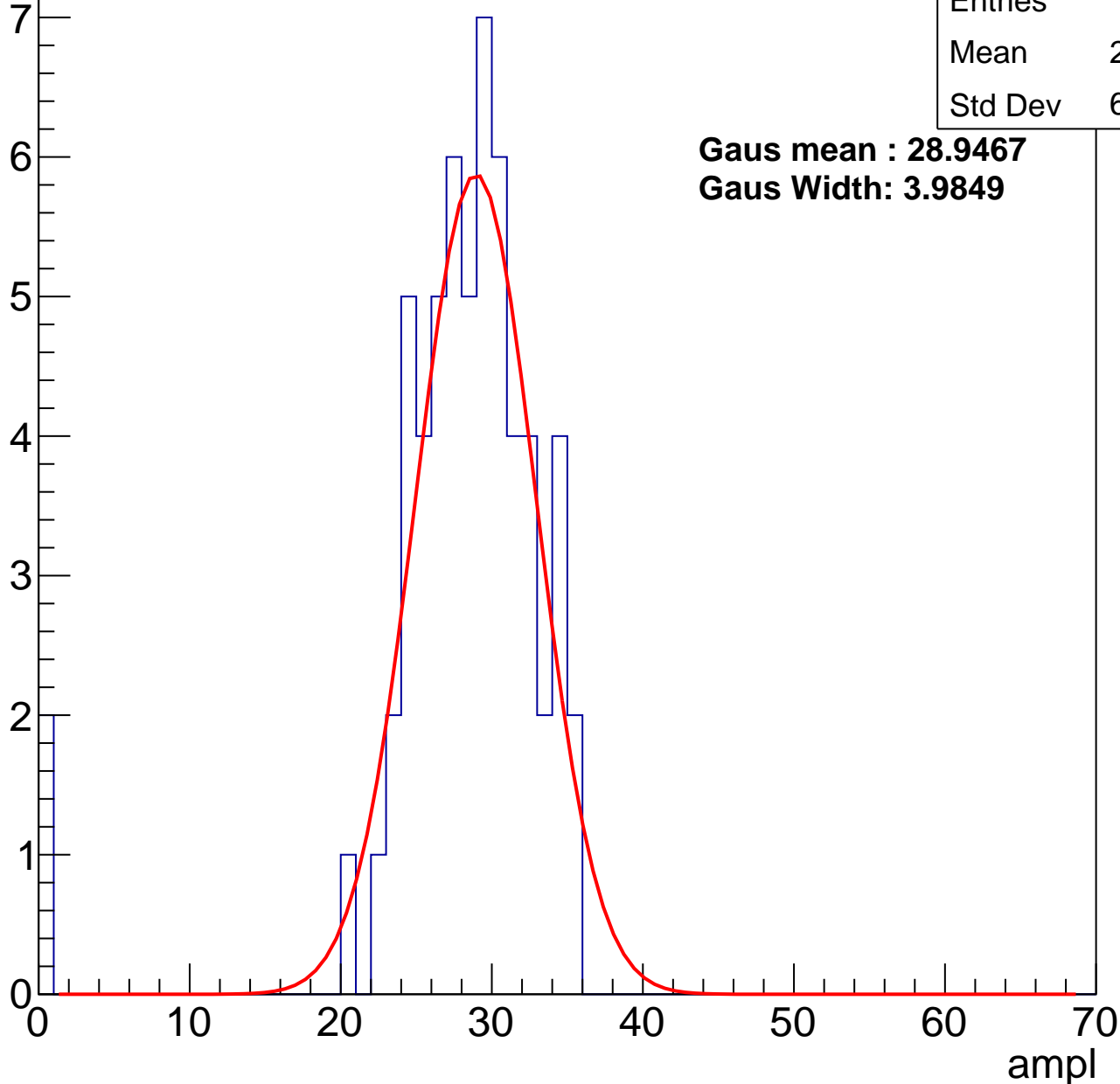
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	27.45
Std Dev	6.144

Gaus mean : 28.9467

Gaus Width: 3.9849



B0L000S, U4-ch56, adc1

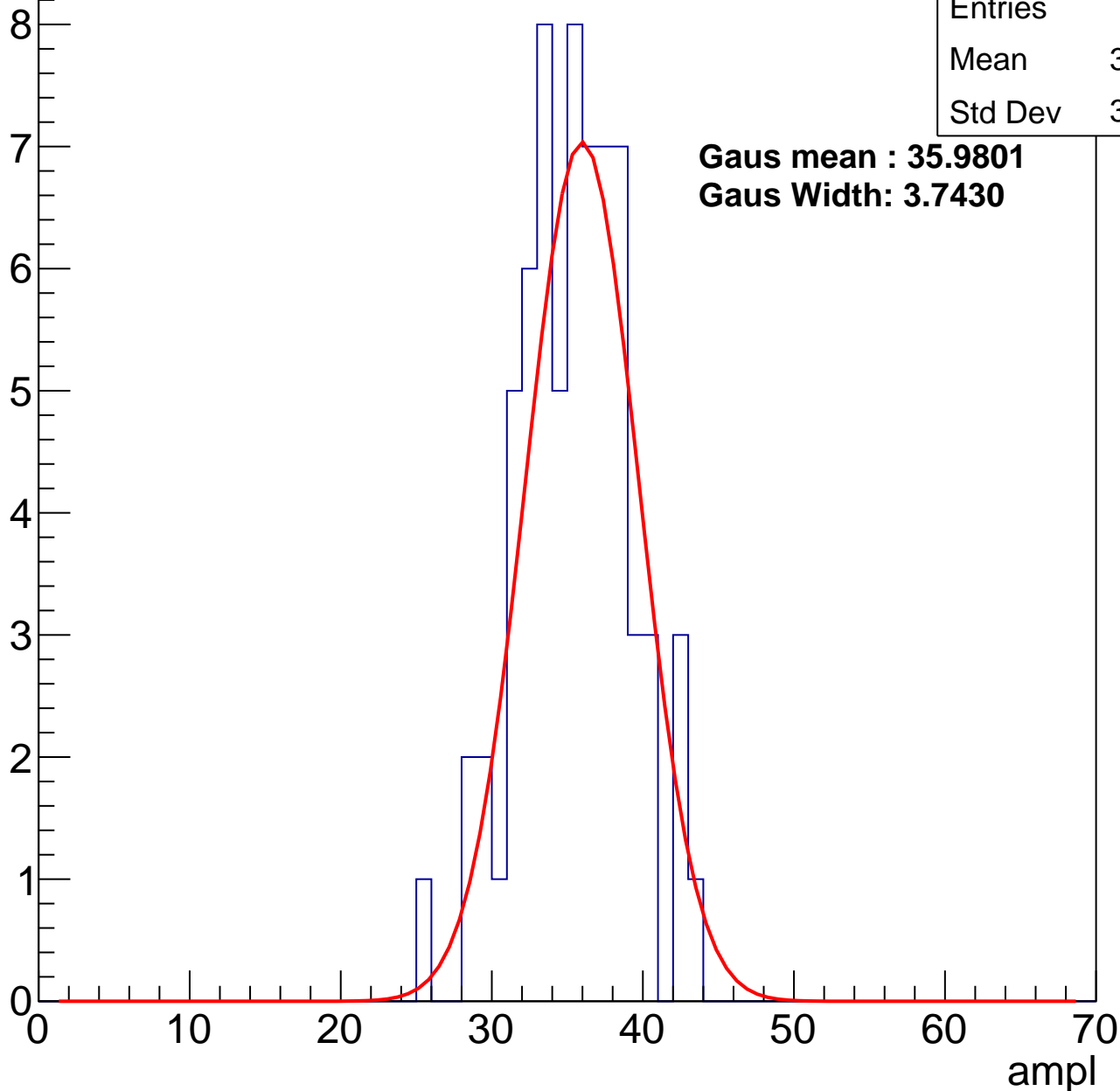
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	34.97
Std Dev	3.607

Gaus mean : 35.9801

Gaus Width: 3.7430



B0L000S, U4-ch56, adc2

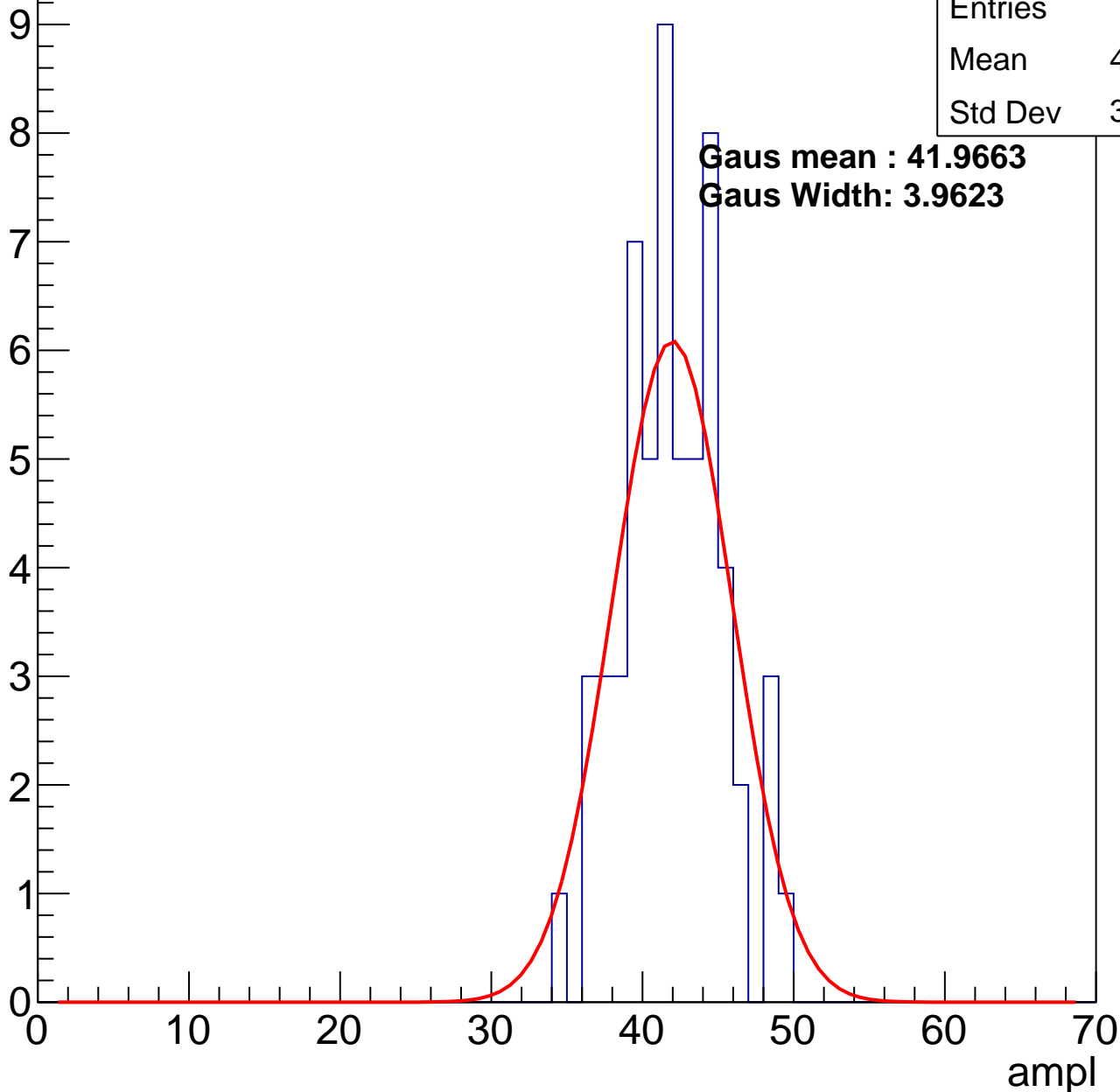
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	41.54
Std Dev	3.285

Gaus mean : 41.9663

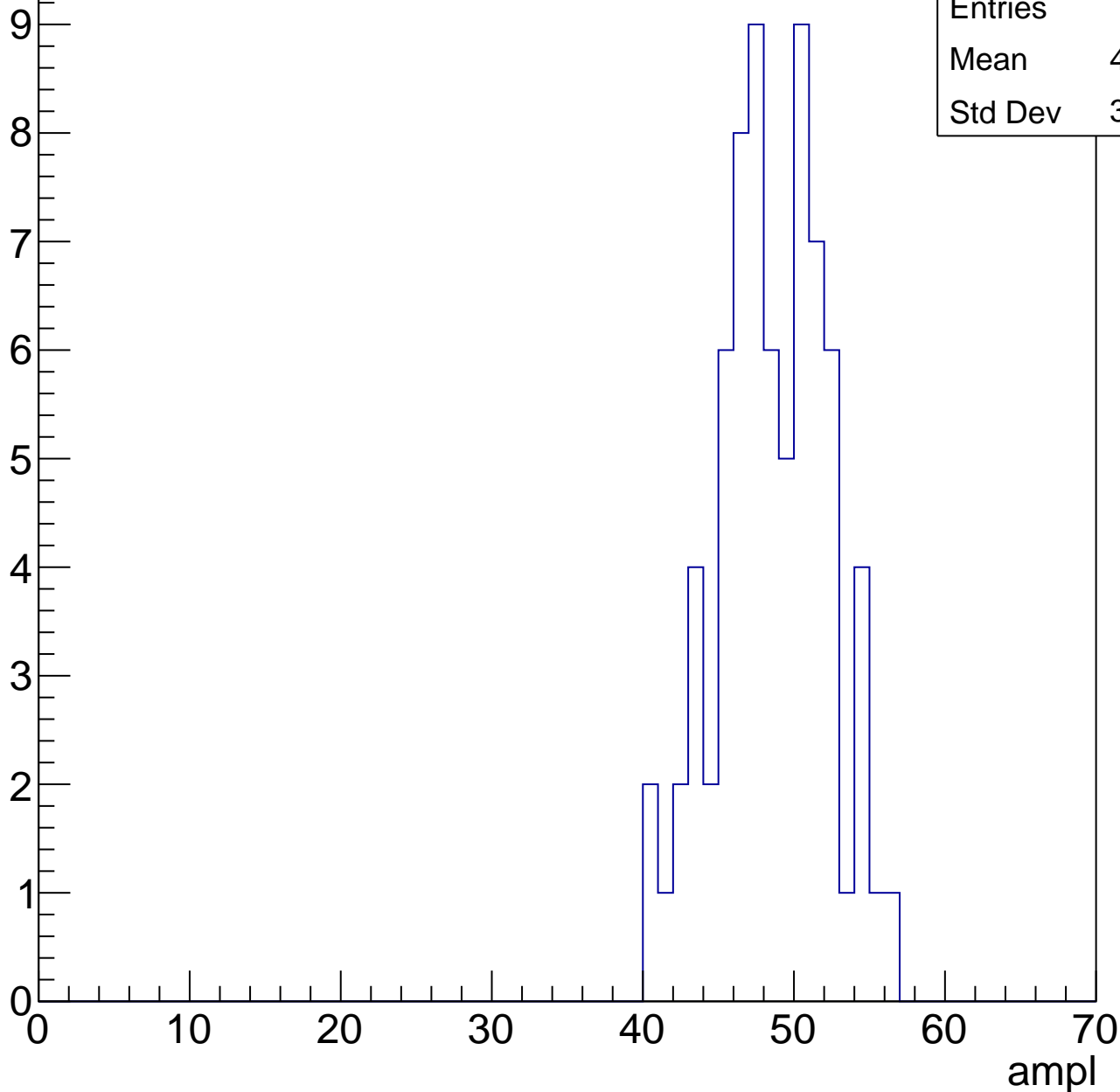
Gaus Width: 3.9623



B0L000S, U4-ch56, adc3

calib_packv5_042523_0143.root, FC#5, port B1

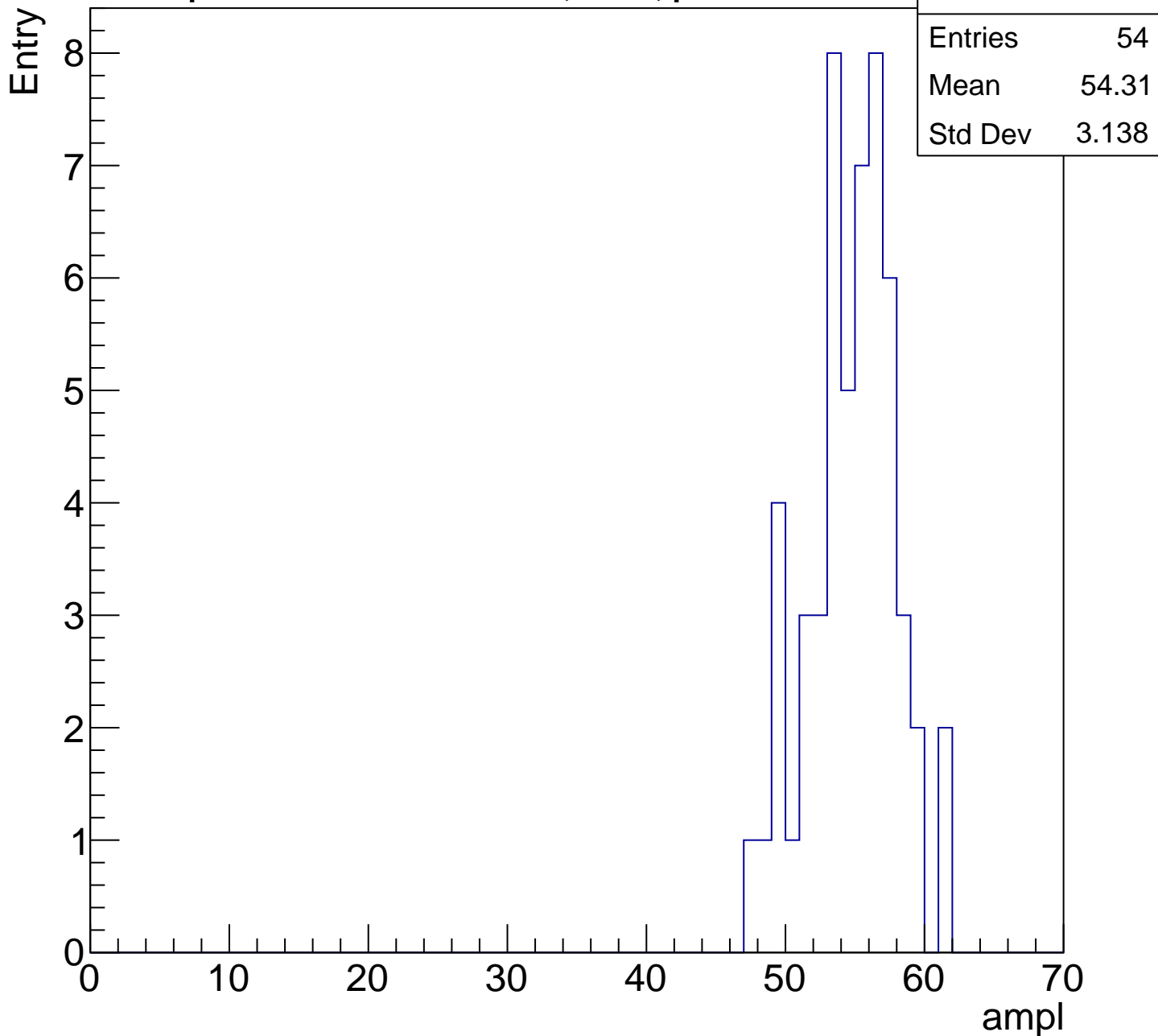
Entry



Entries	74
Mean	48.08
Std Dev	3.597

B0L000S, U4-ch56, adc4

calib_packv5_042523_0143.root, FC#5, port B1

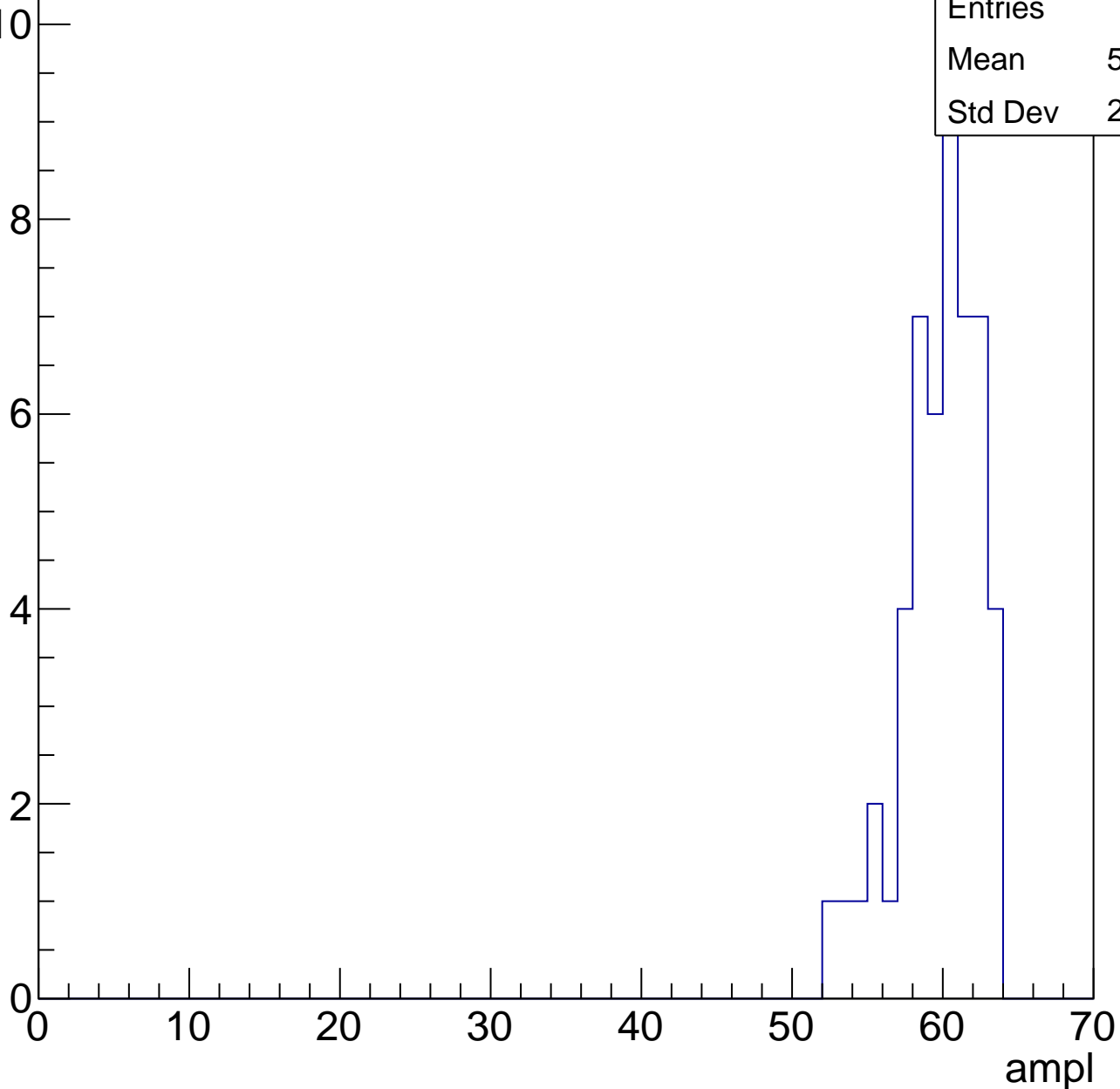


B0L000S, U4-ch56, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	59.33
Std Dev	2.557

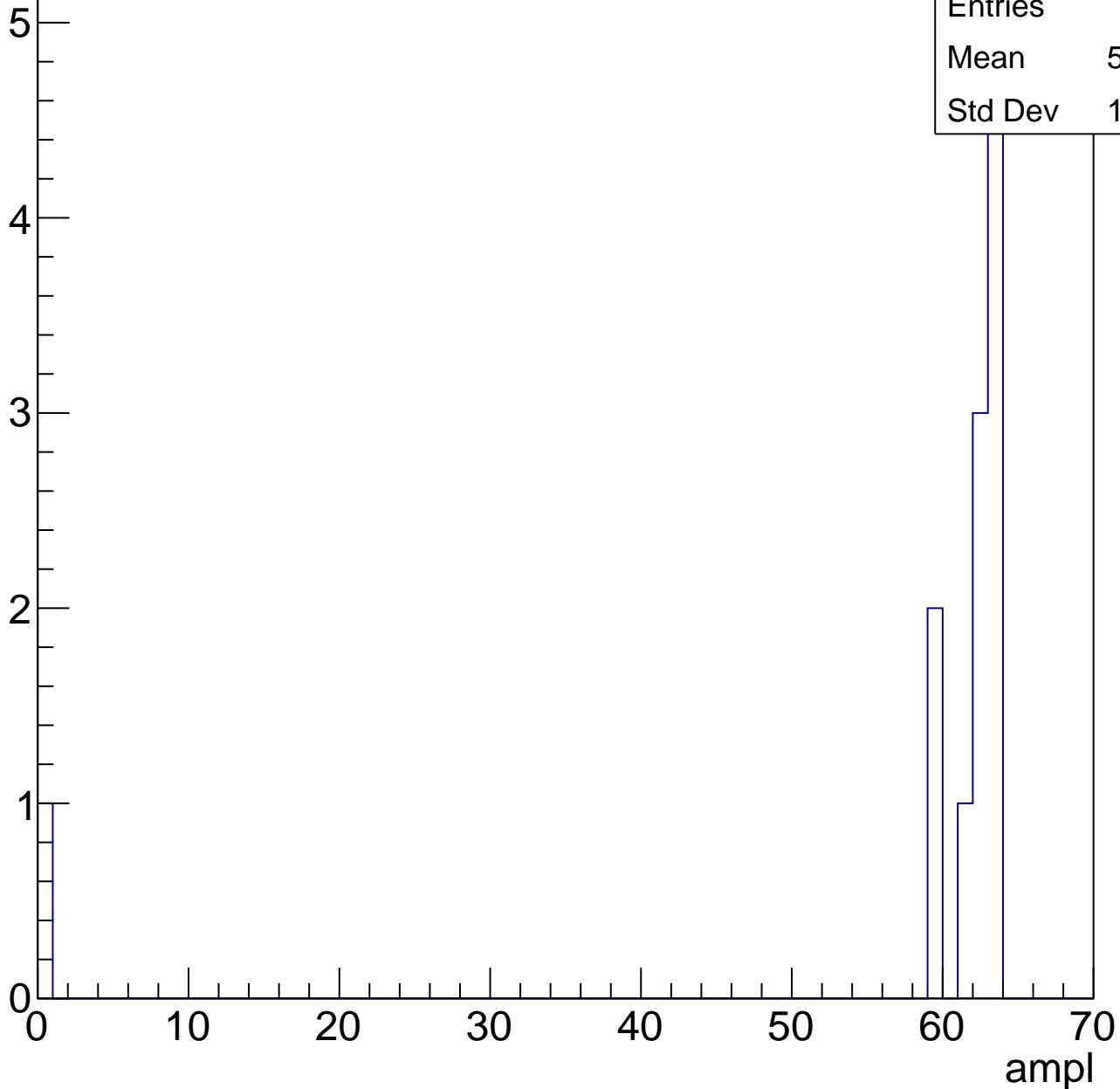


B0L000S, U4-ch56, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	12
Mean	56.67
Std Dev	17.14



B0L000S, U4-ch56, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch57, adc0

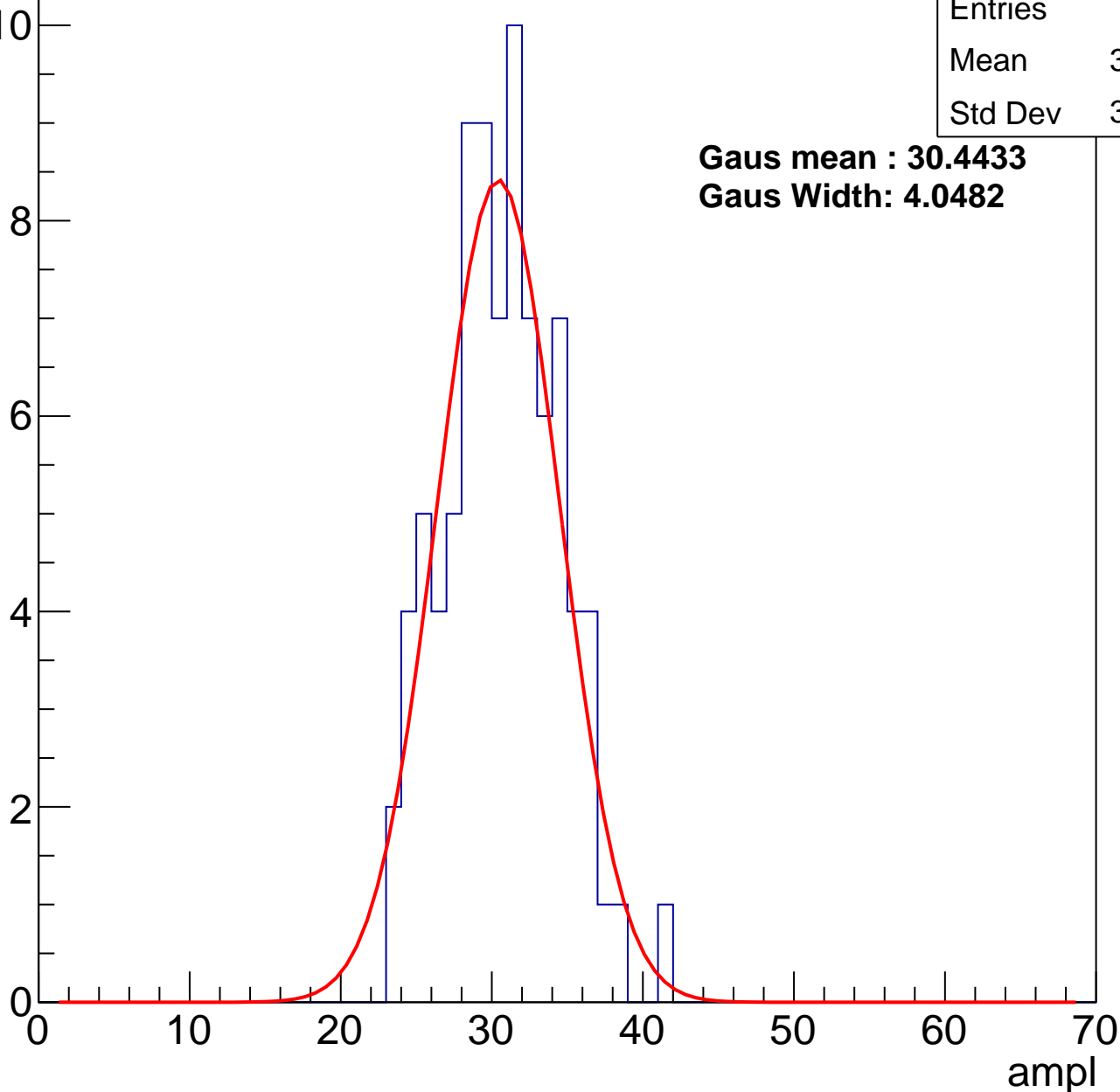
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	86
Mean	30.22
Std Dev	3.734

Gaus mean : 30.4433

Gaus Width: 4.0482



B0L000S, U4-ch57, adc1

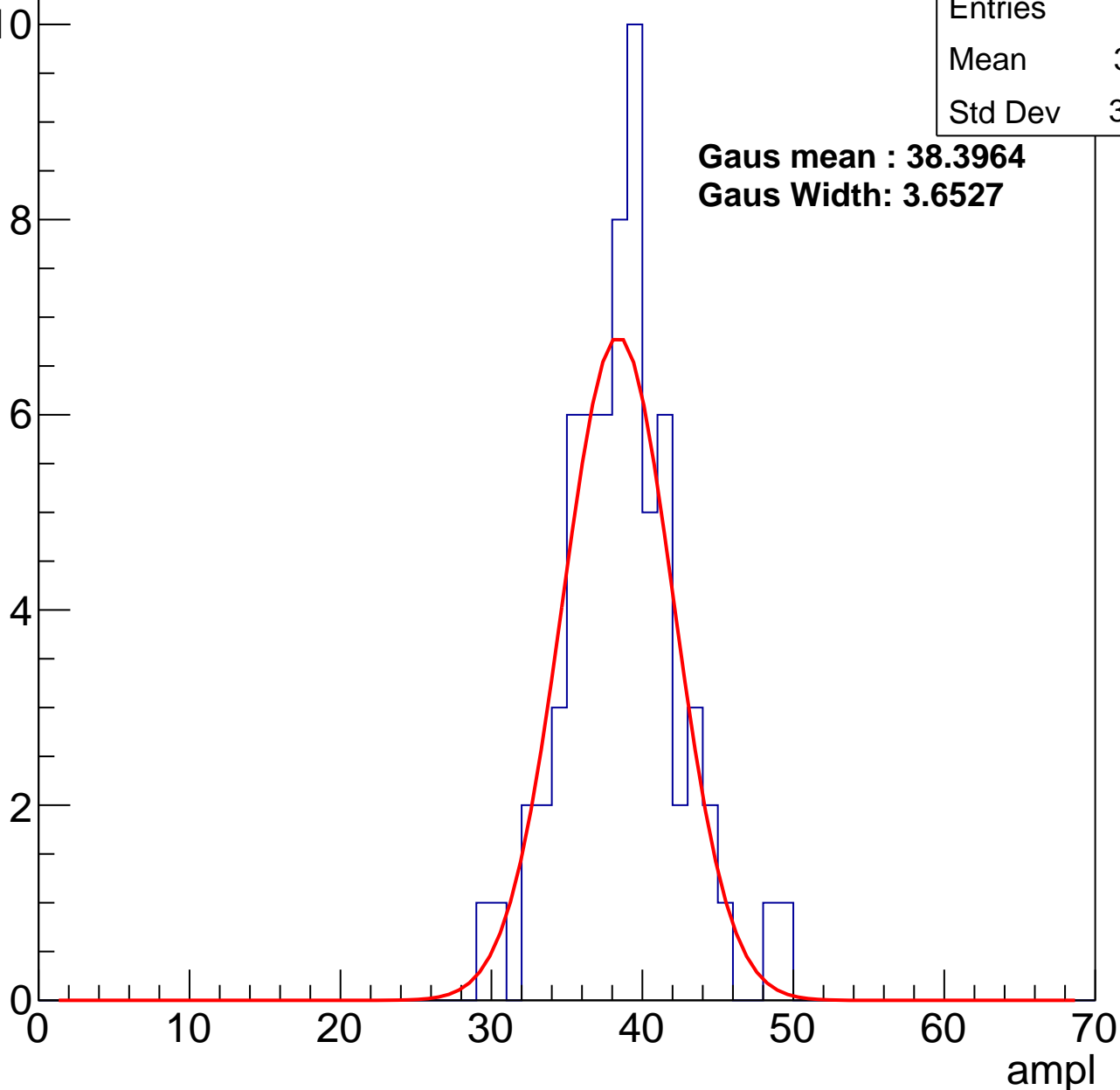
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	38.21
Std Dev	3.752

Gaus mean : 38.3964

Gaus Width: 3.6527



B0L000S, U4-ch57, adc2

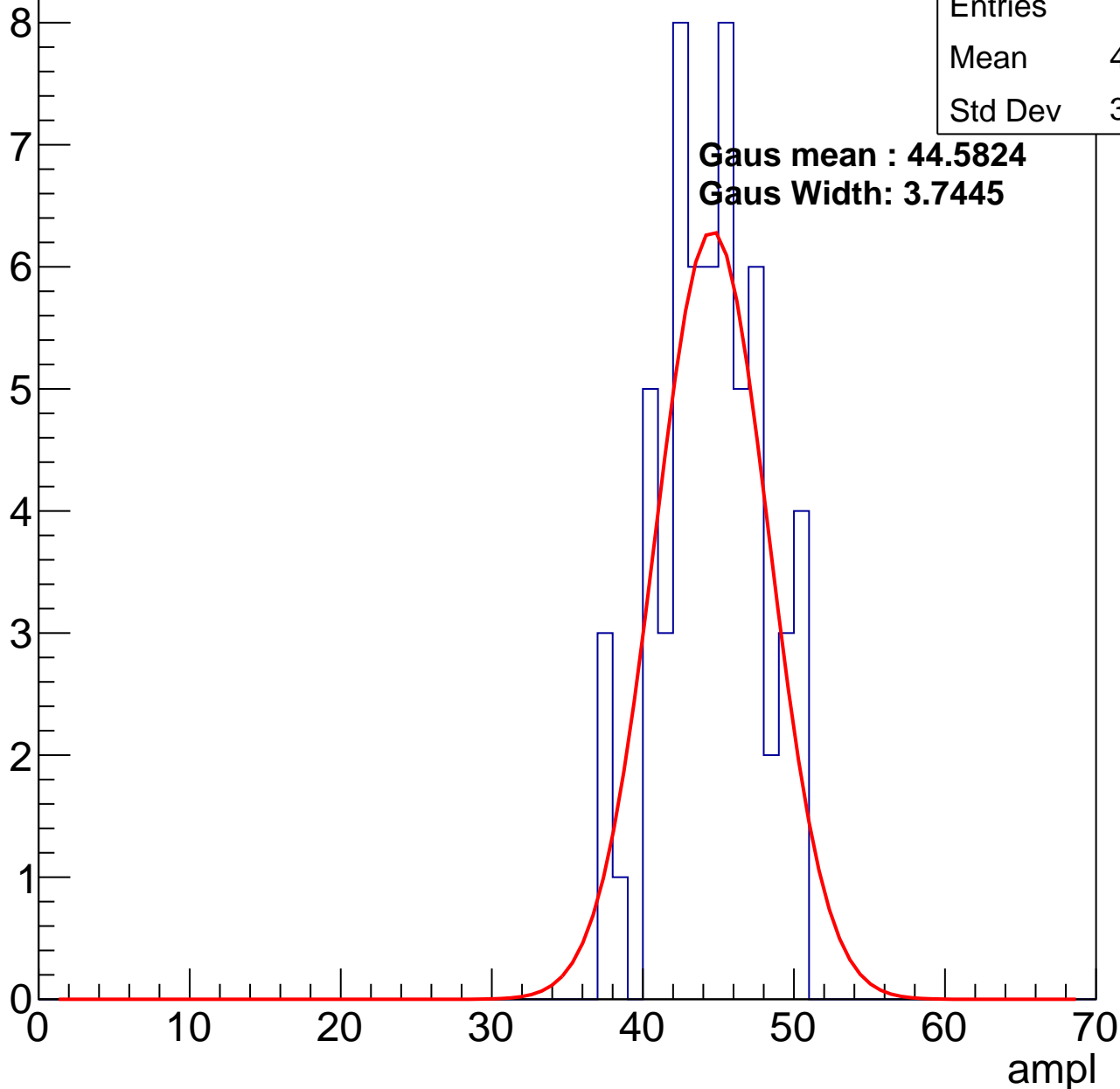
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.08
Std Dev	3.318

Gaus mean : 44.5824

Gaus Width: 3.7445

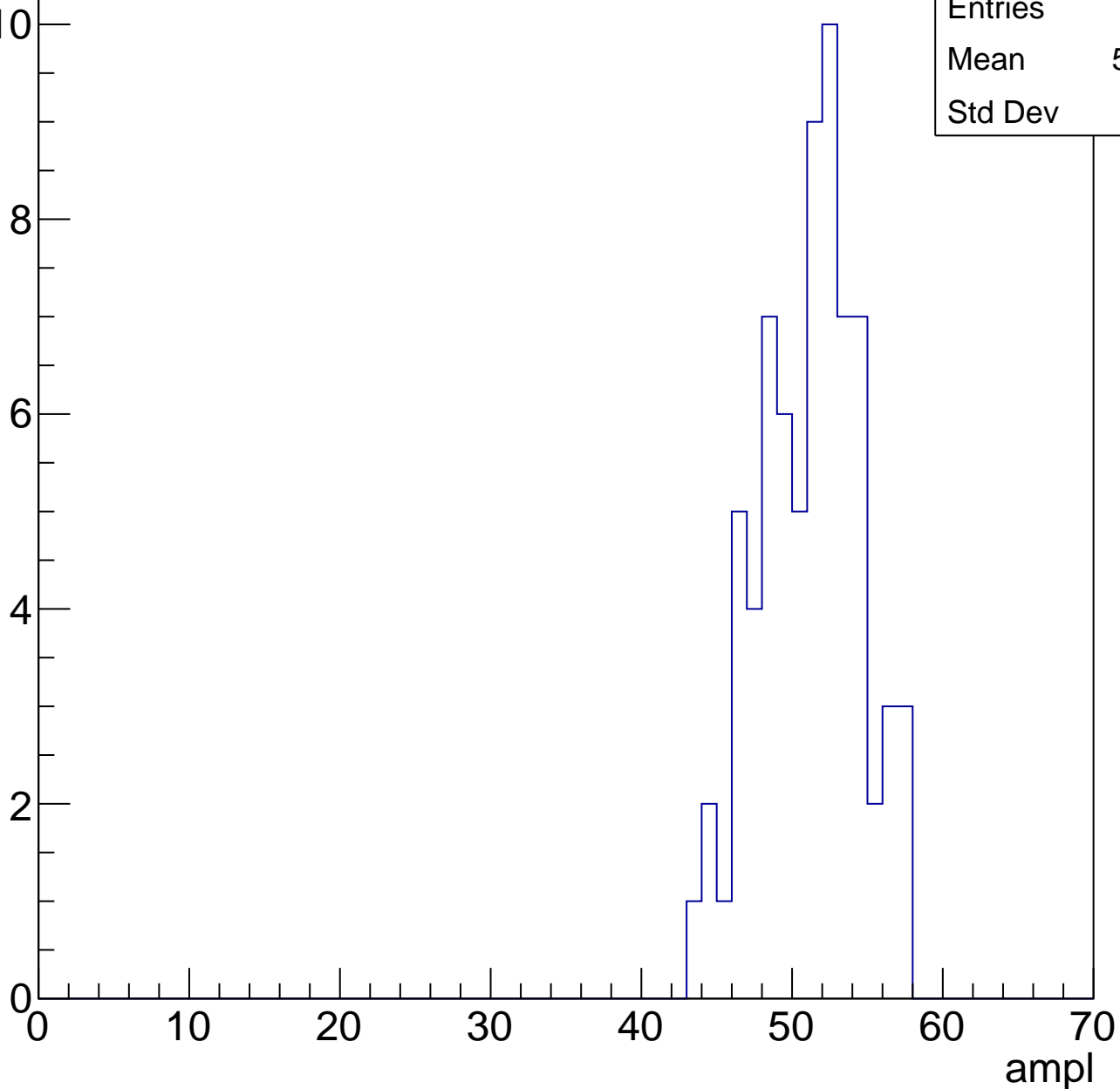


B0L000S, U4-ch57, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	50.71
Std Dev	3.31



B0L000S, U4-ch57, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	56.75
Std Dev	7.867

Entry

10

8

6

4

2

0

0

10

20

30

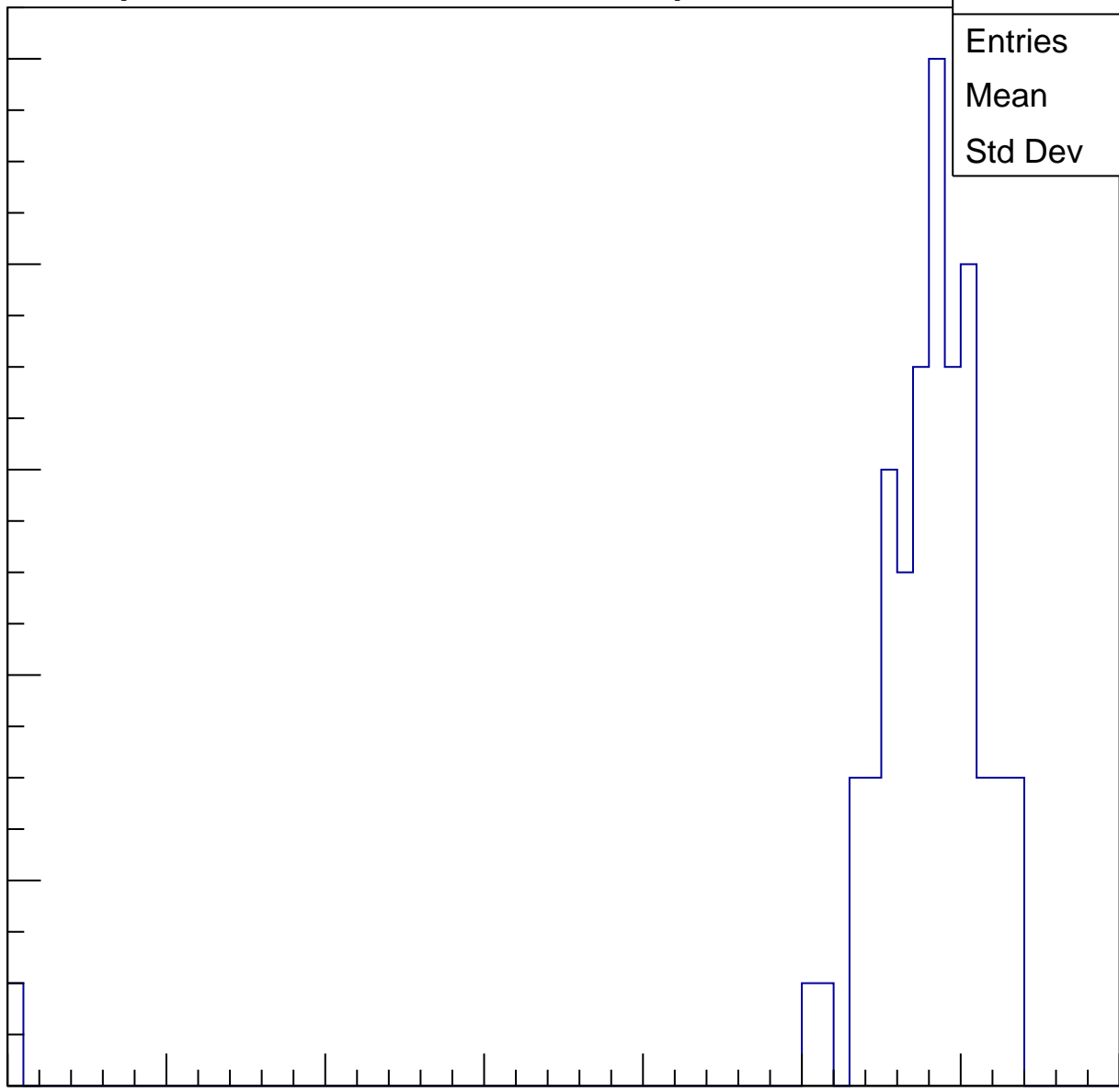
40

50

60

70

ampl

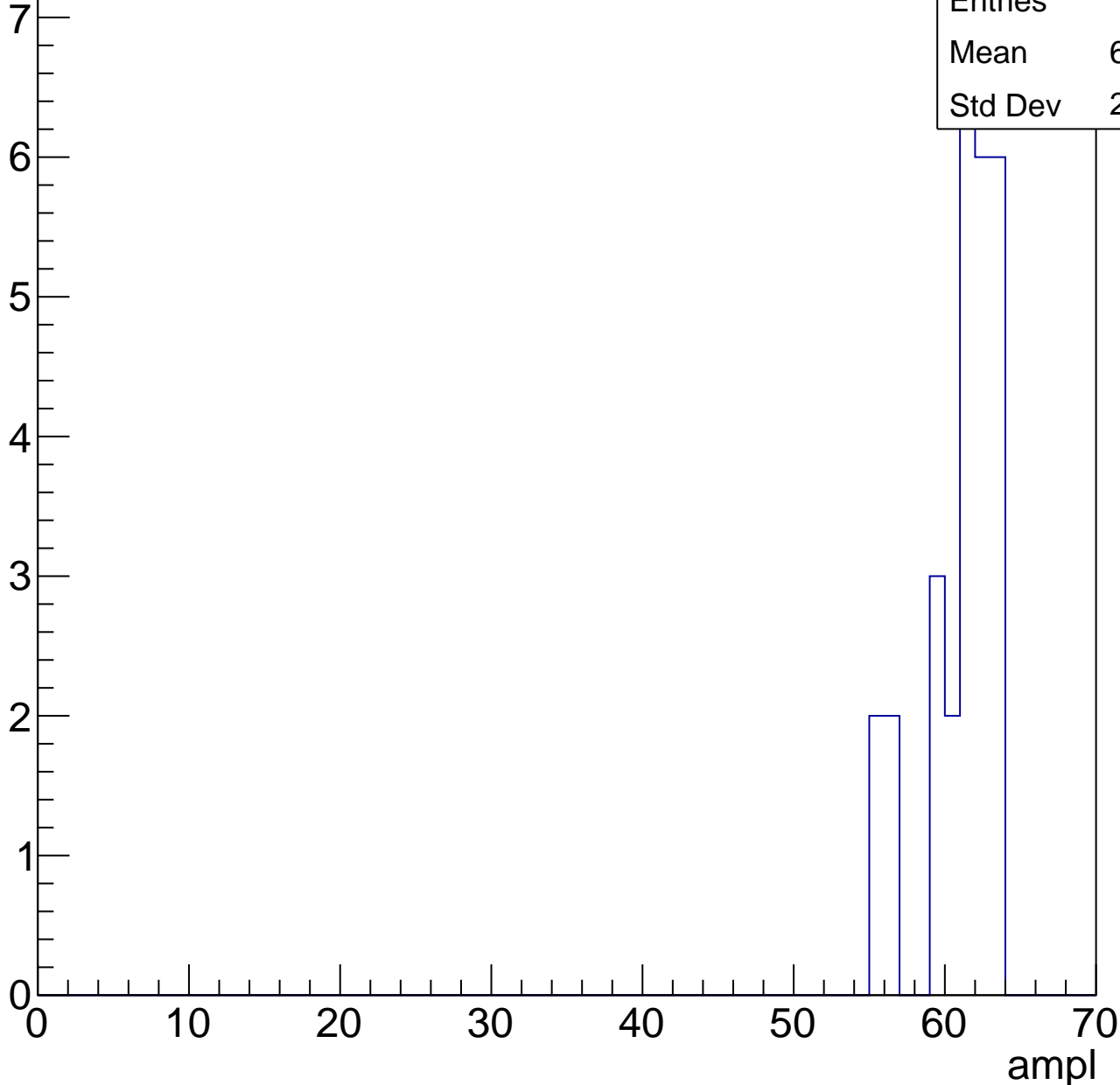


B0L000S, U4-ch57, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	28
Mean	60.57
Std Dev	2.397



B0L000S, U4-ch57, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch57, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

B0L000S, U4-ch58, adc0

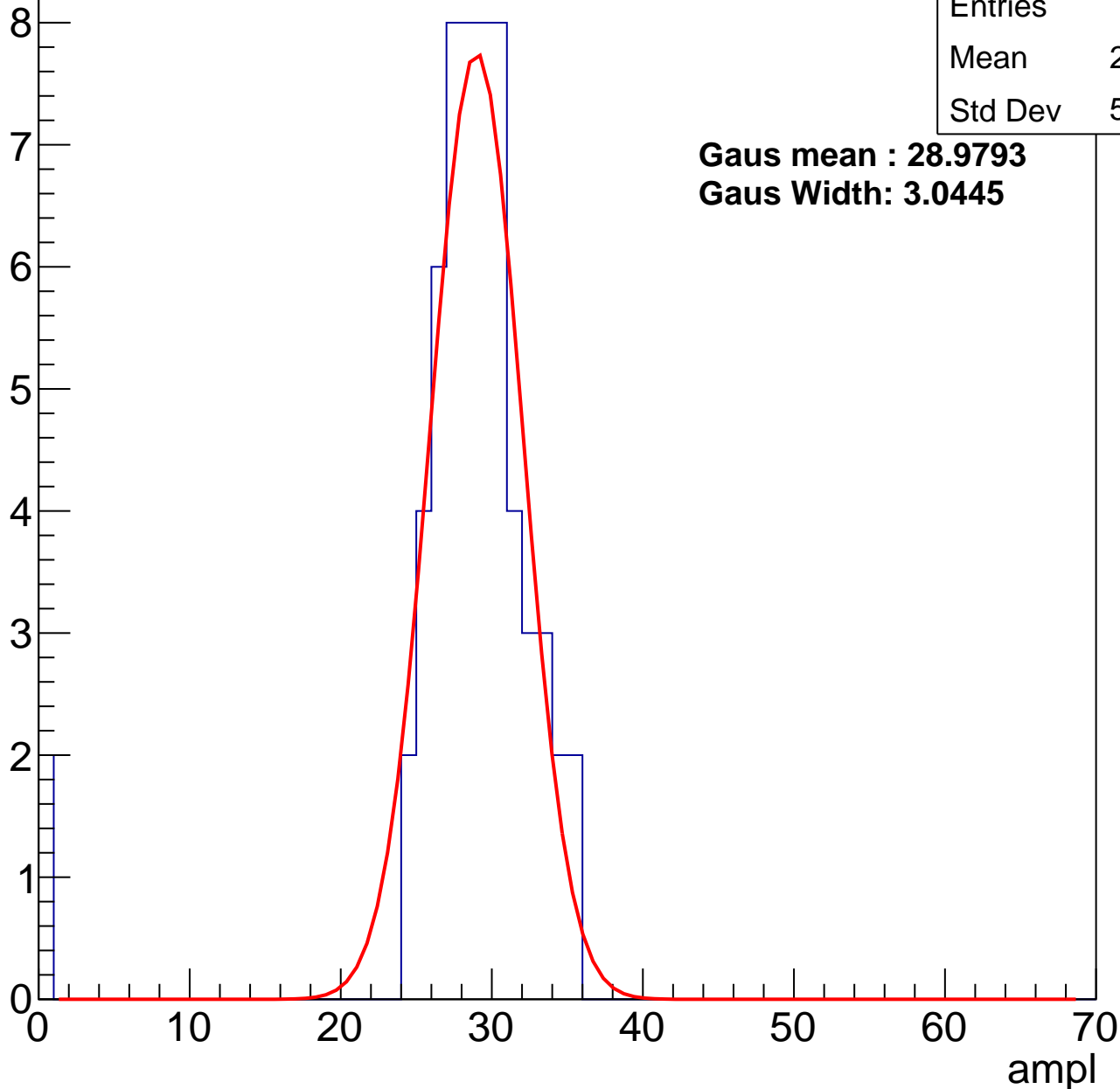
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	27.88
Std Dev	5.825

Gaus mean : 28.9793

Gaus Width: 3.0445



B0L000S, U4-ch58, adc1

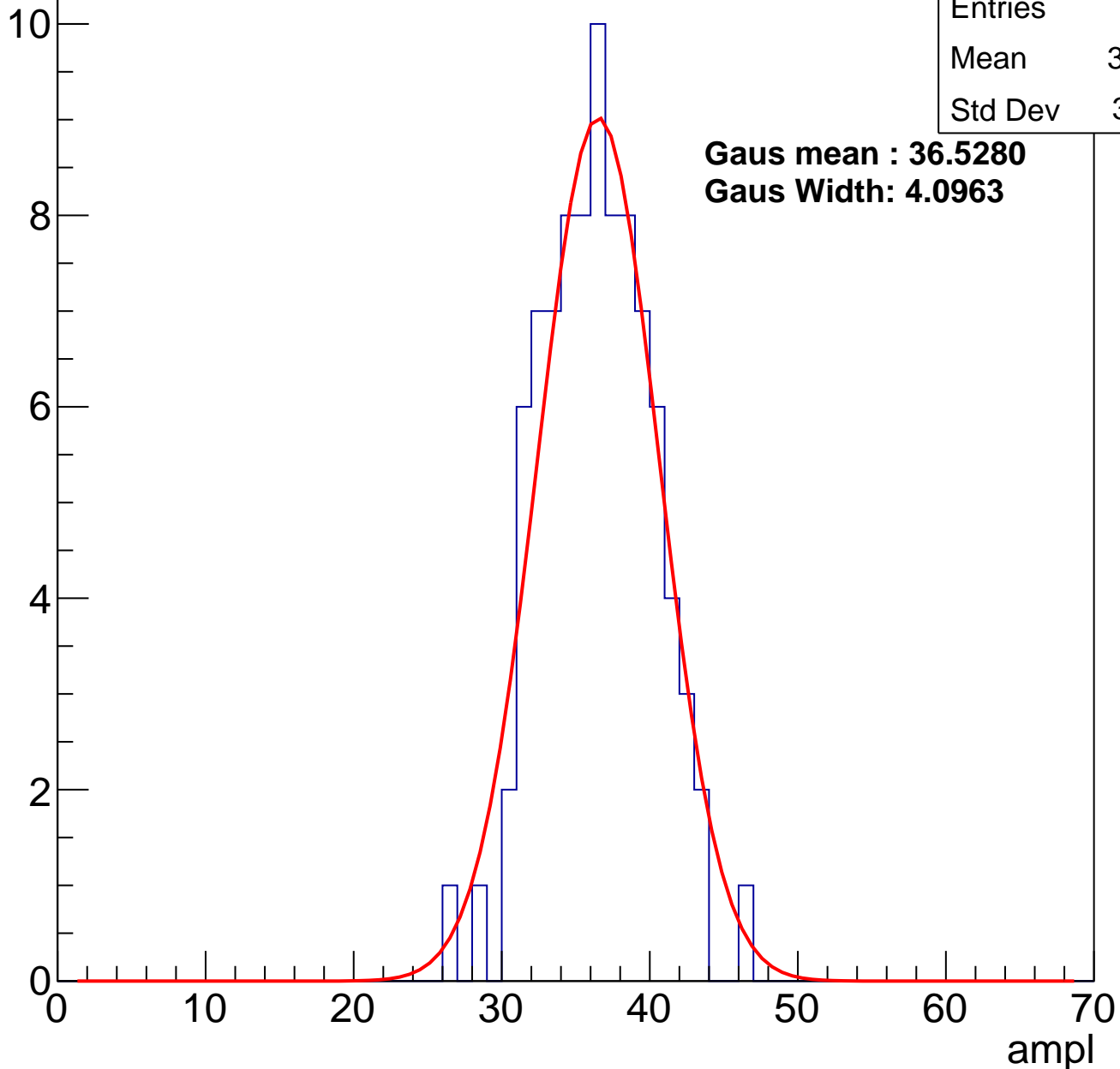
calib_packv5_042523_0143.root, FC#5, port B1

Entries	89
Mean	35.98
Std Dev	3.681

Gaus mean : 36.5280

Gaus Width: 4.0963

Entry



B0L000S, U4-ch58, adc2

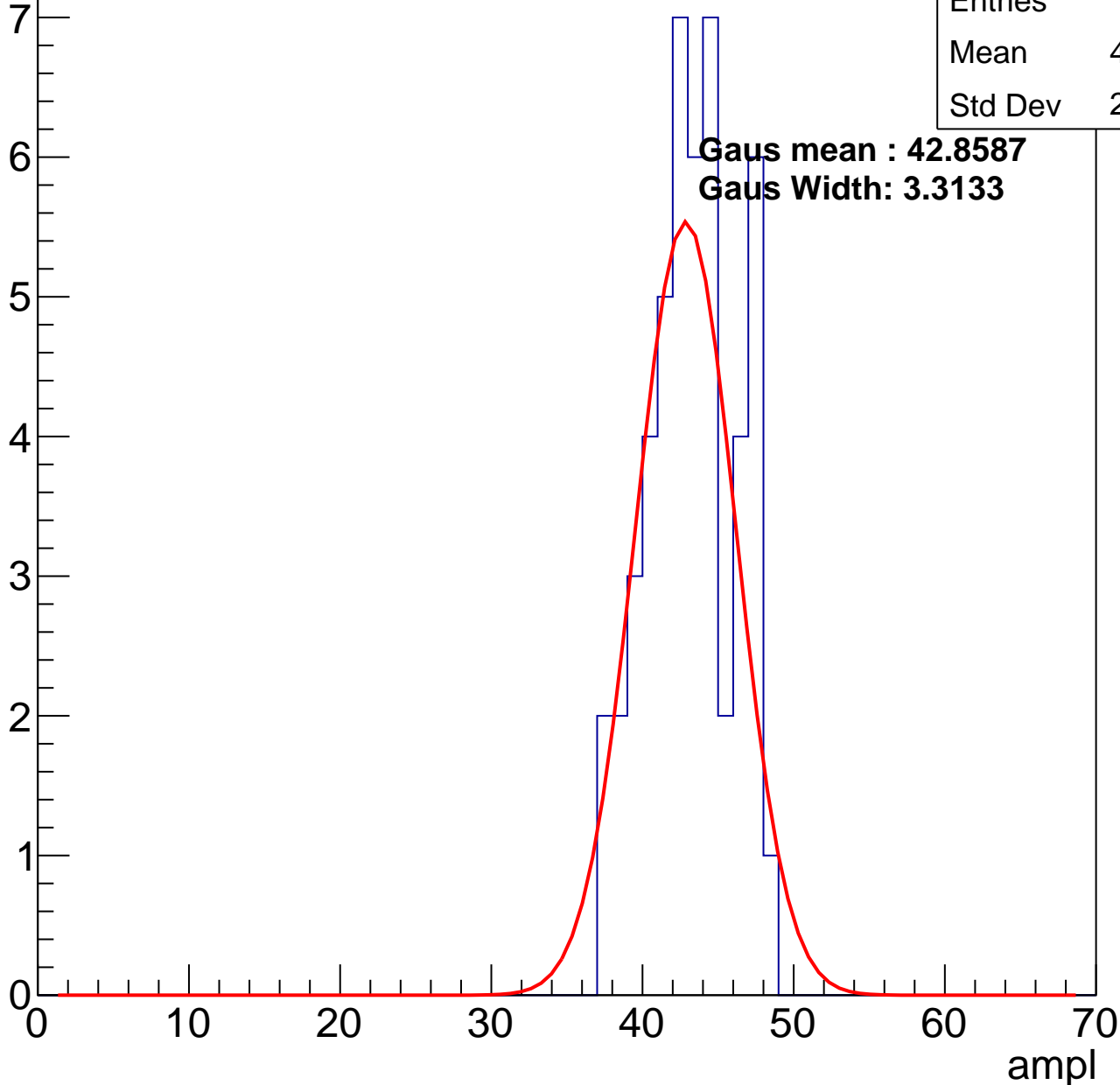
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	42.78
Std Dev	2.866

Gaus mean : 42.8587

Gaus Width: 3.3133



B0L000S, U4-ch58, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	64
Mean	48.3
Std Dev	2.967

Entry

10

8

6

4

2

0

0

10

20

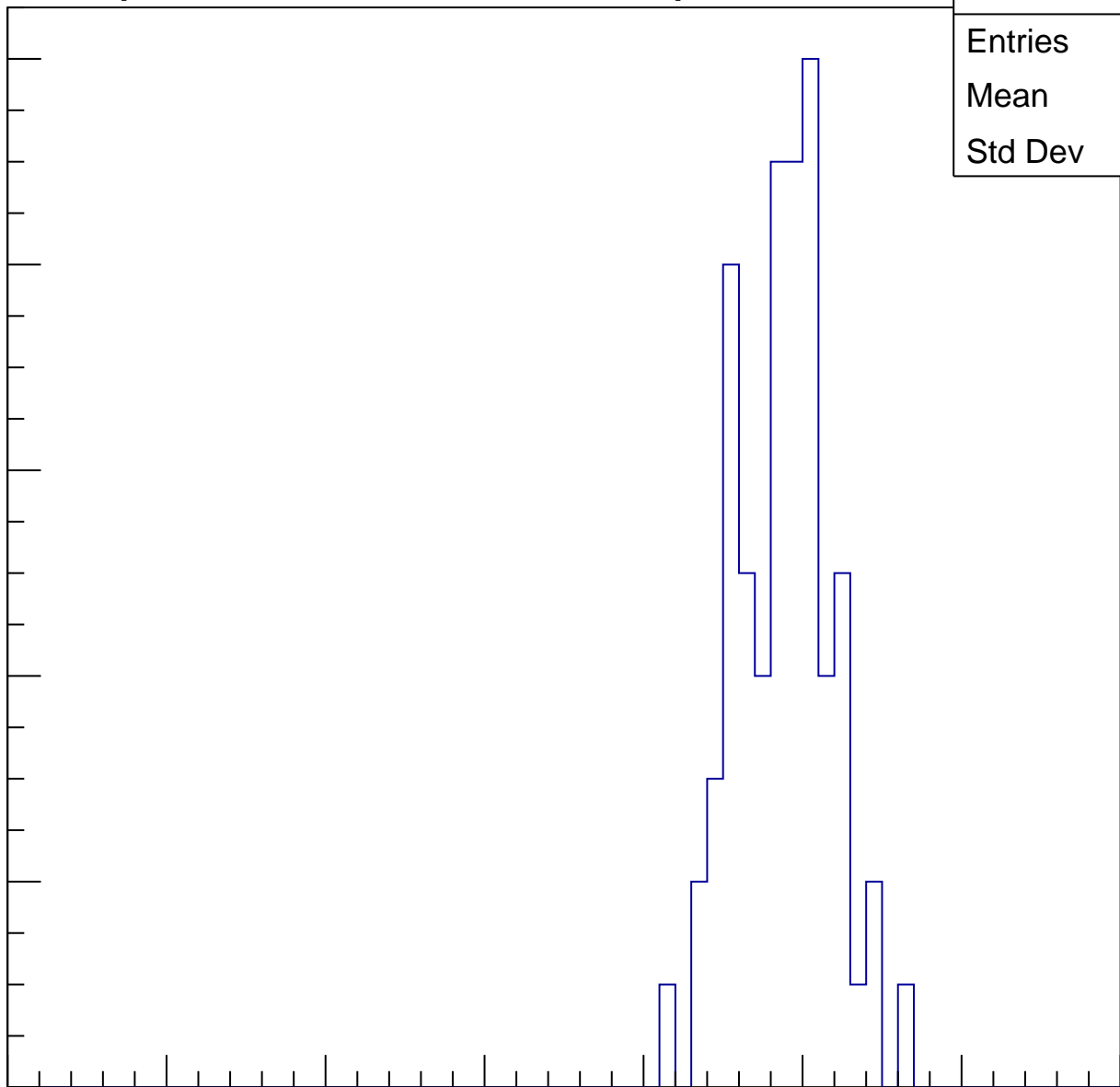
30

40

50

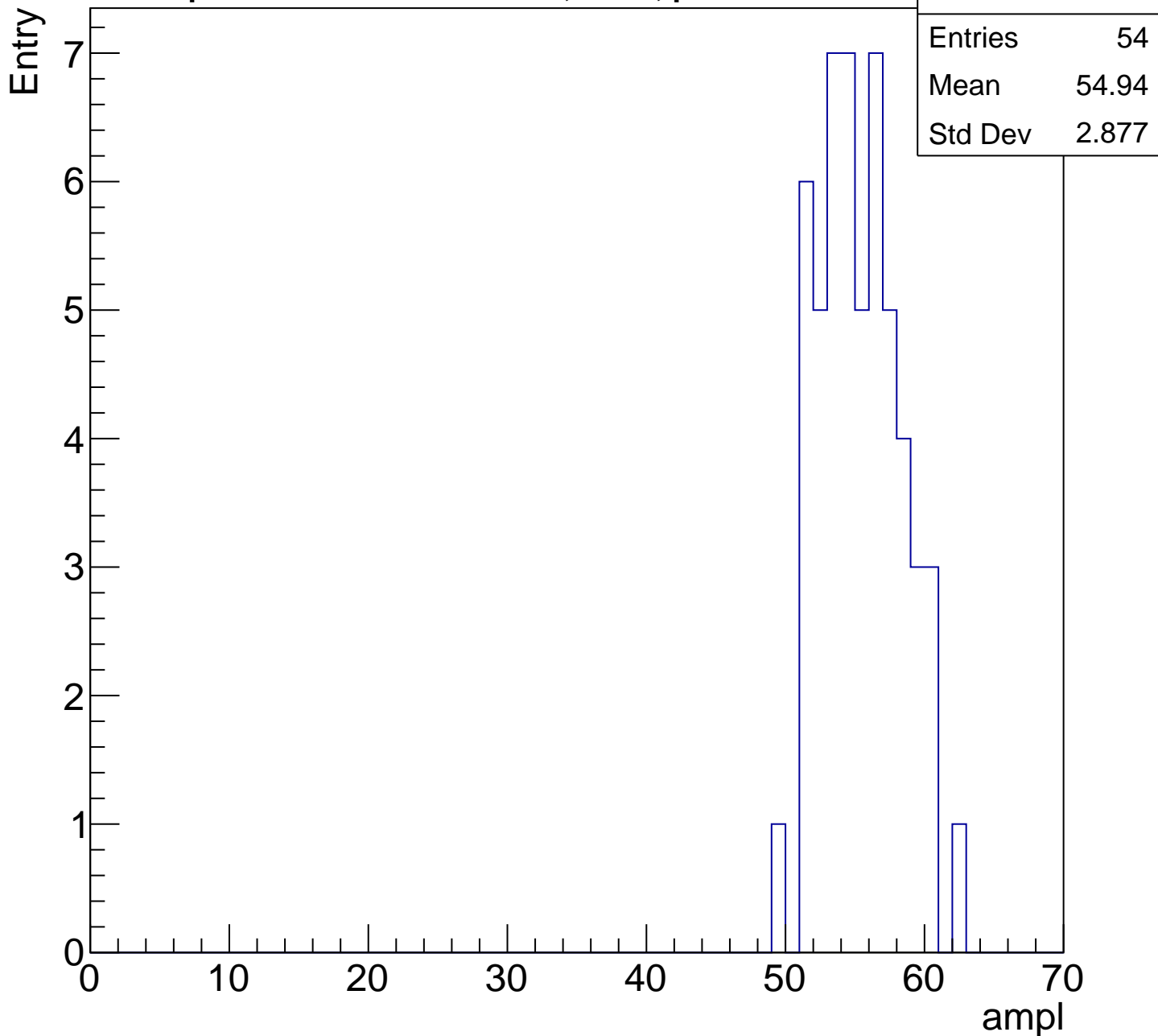
60

ampl



B0L000S, U4-ch58, adc4

calib_packv5_042523_0143.root, FC#5, port B1

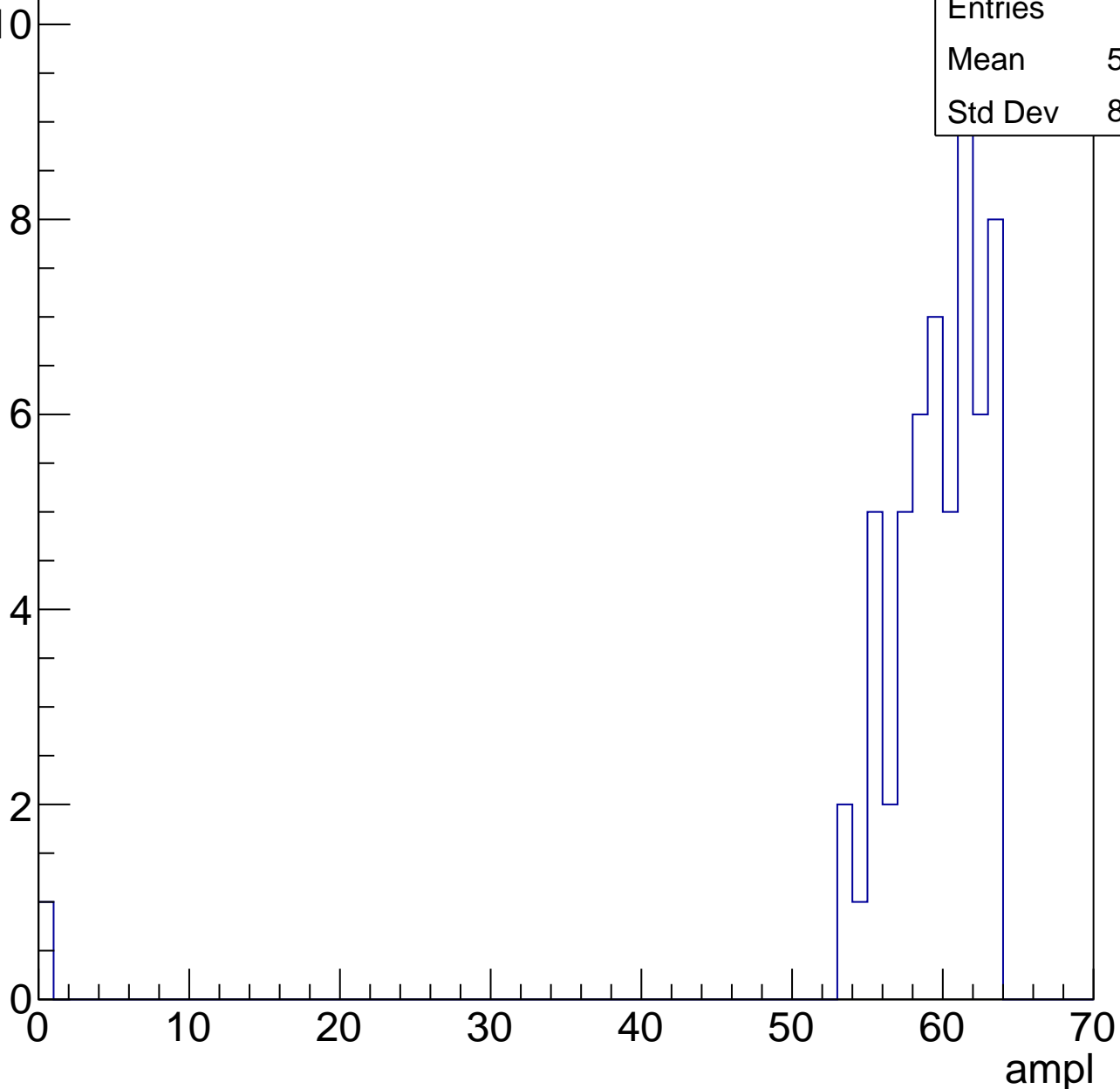


B0L000S, U4-ch58, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

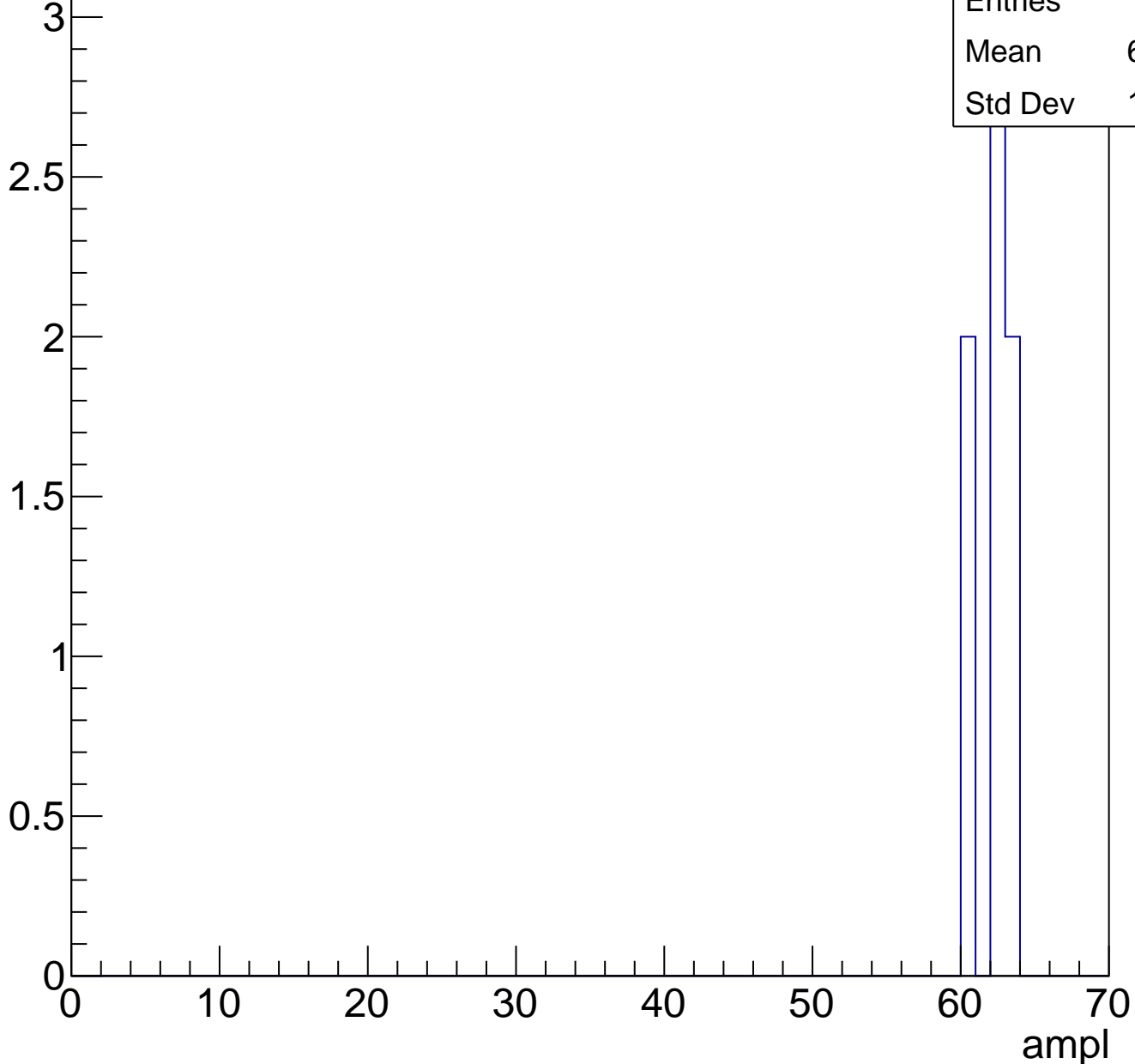
Entries	58
Mean	58.26
Std Dev	8.197



B0L000S, U4-ch58, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch58, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch59, adc0

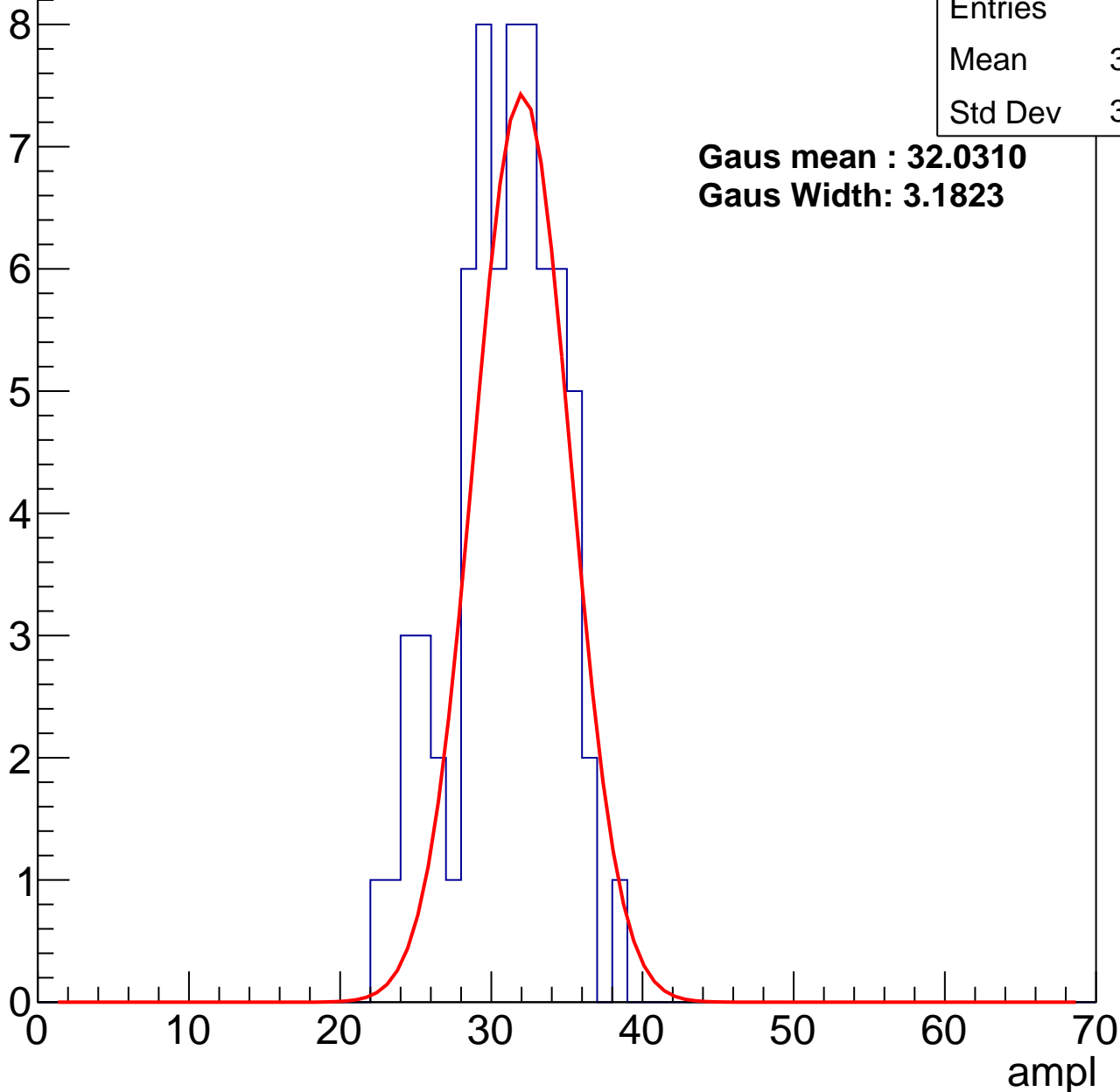
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.48
Std Dev	3.466

Gaus mean : 32.0310

Gaus Width: 3.1823



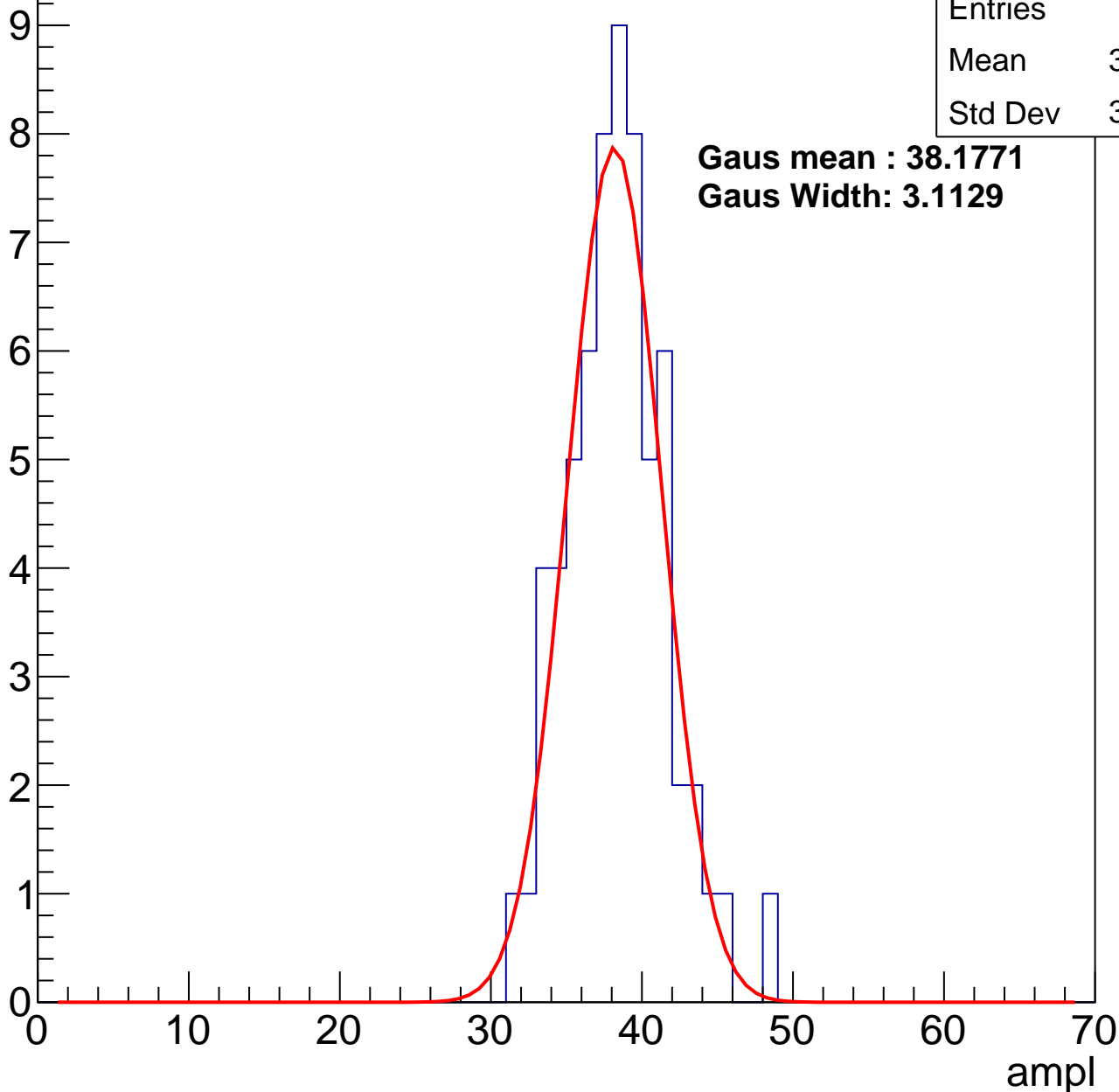
B0L000S, U4-ch59, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	37.89
Std Dev	3.246

Gaus mean : 38.1771
Gaus Width: 3.1129



B0L000S, U4-ch59, adc2

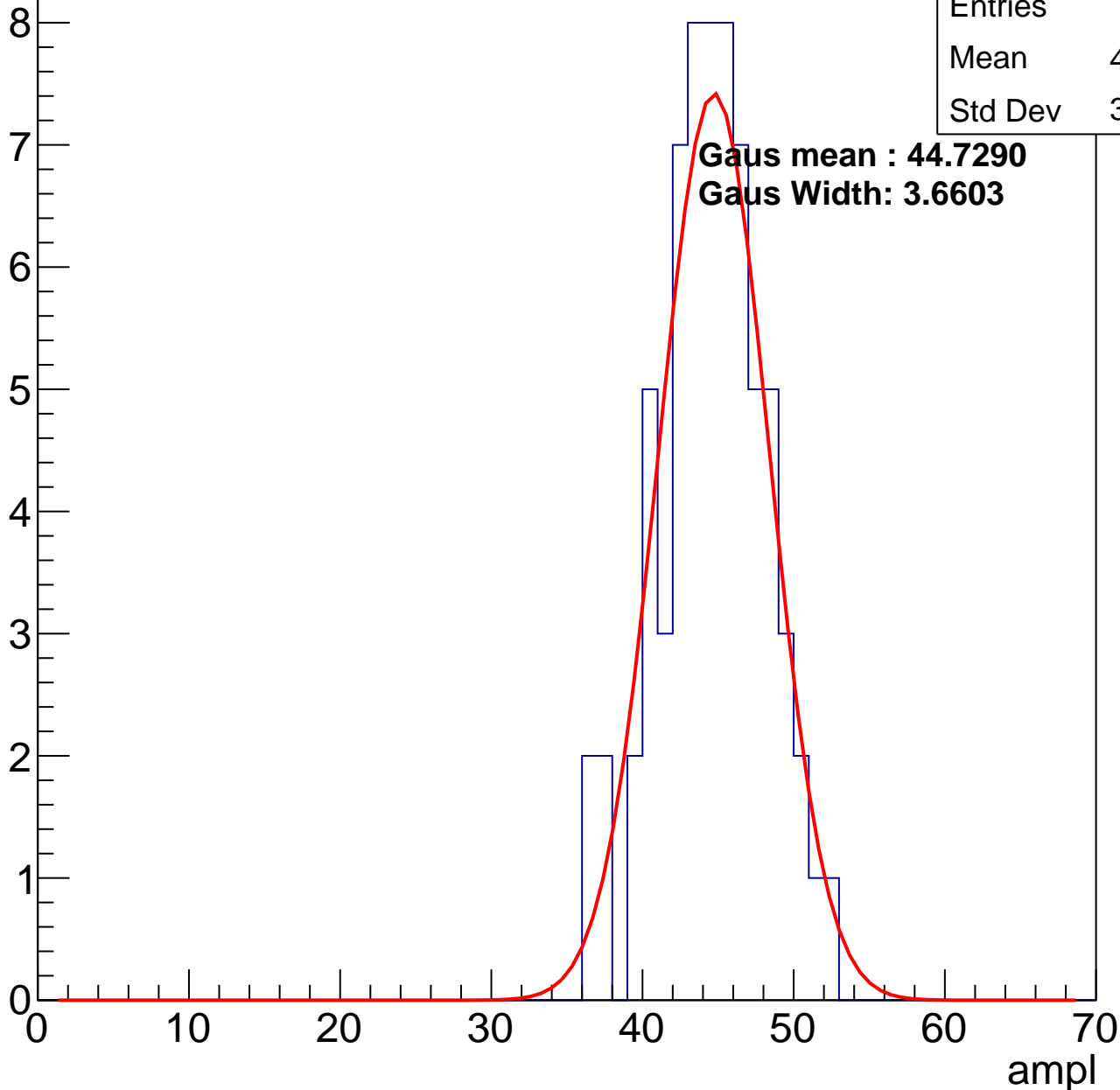
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	44.12
Std Dev	3.487

Gaus mean : 44.7290

Gaus Width: 3.6603

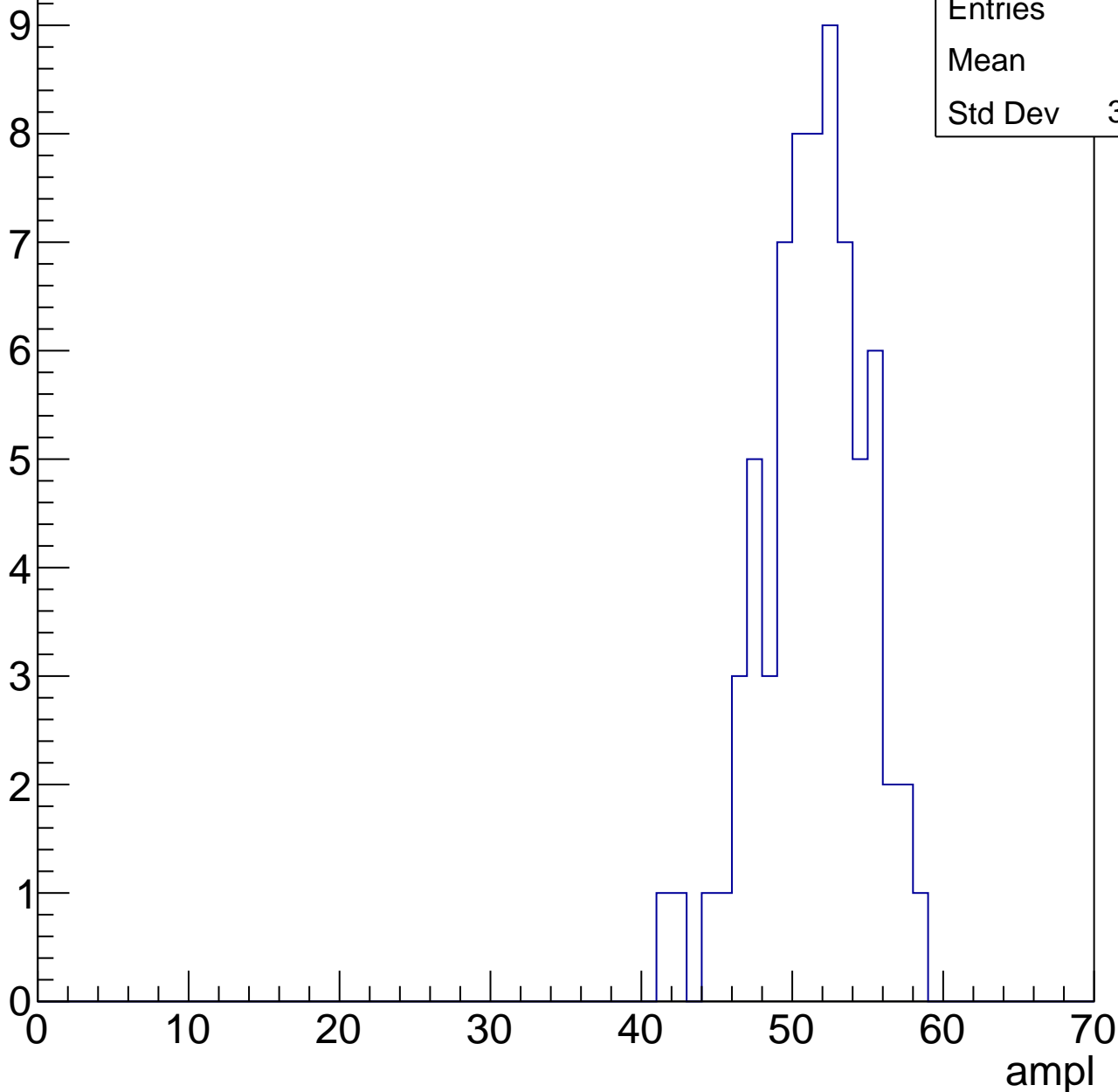


B0L000S, U4-ch59, adc3

calib_packv5_042523_0143.root, FC#5, port B1

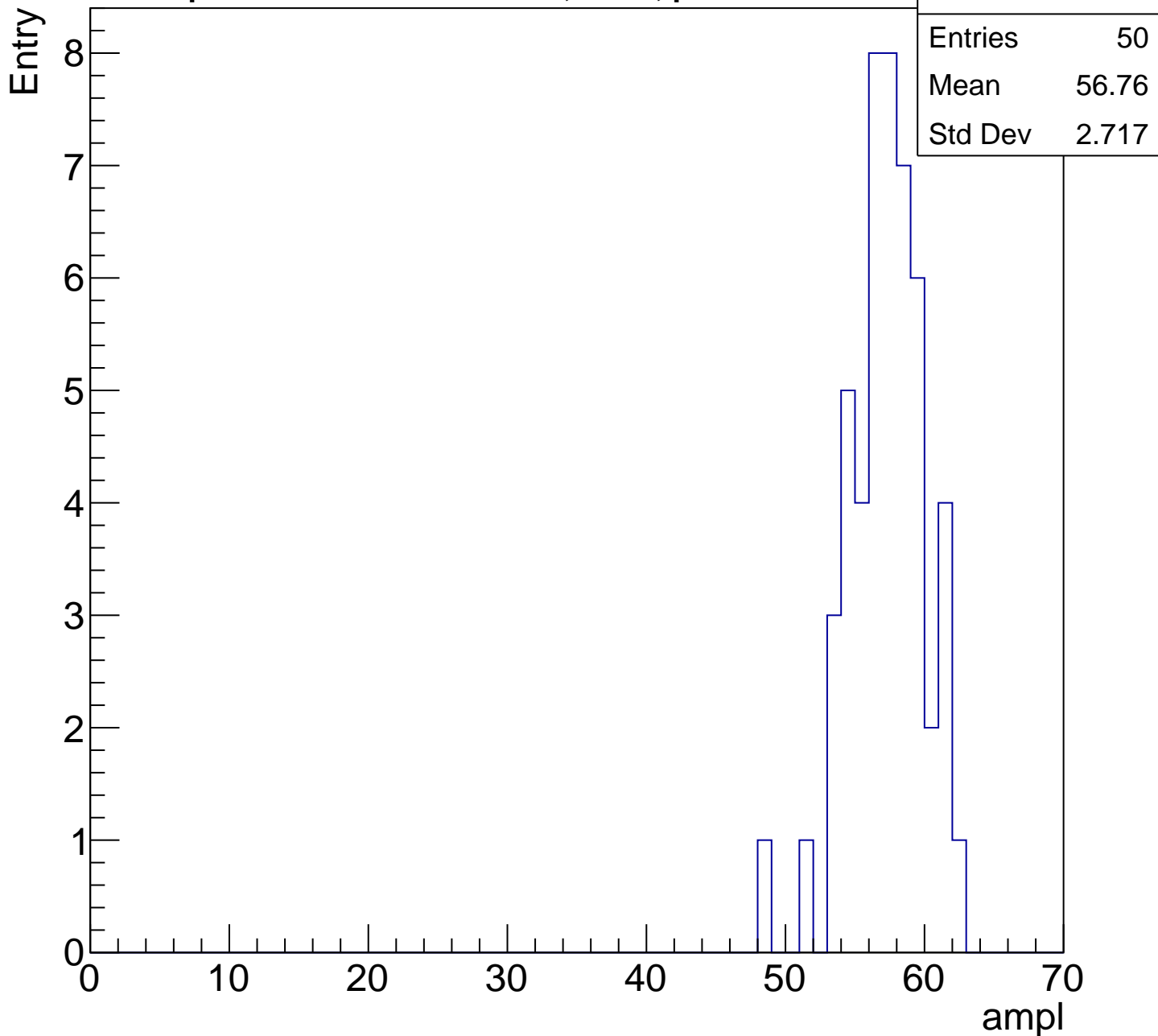
Entry

Entries	70
Mean	50.9
Std Dev	3.448



B0L000S, U4-ch59, adc4

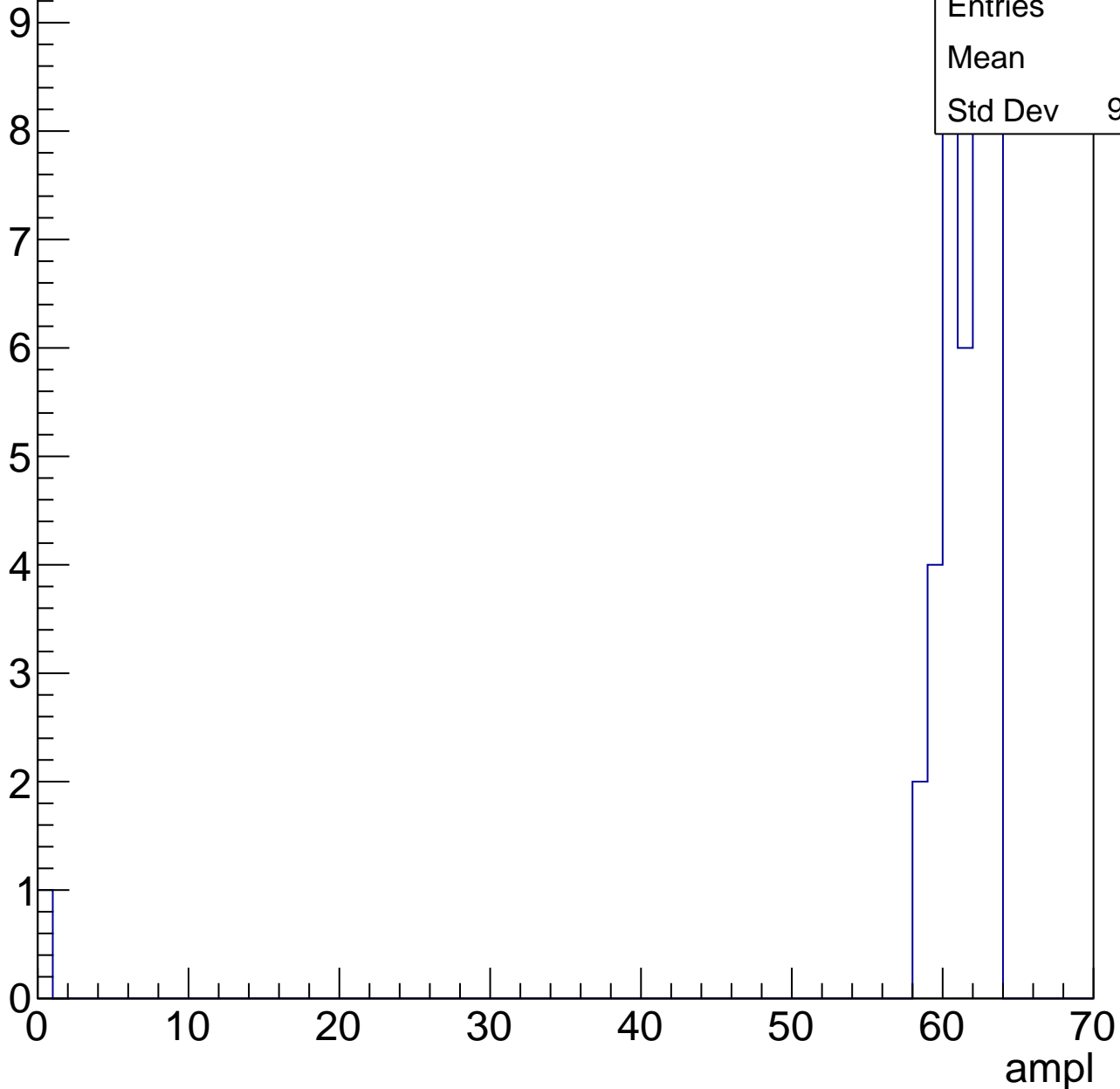
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch59, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch59, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch59, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch60, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	69
Mean	28.77
Std Dev	5.979

Gaus mean : 30.6702

Gaus Width: 3.9369

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L000S, U4-ch60, adc1

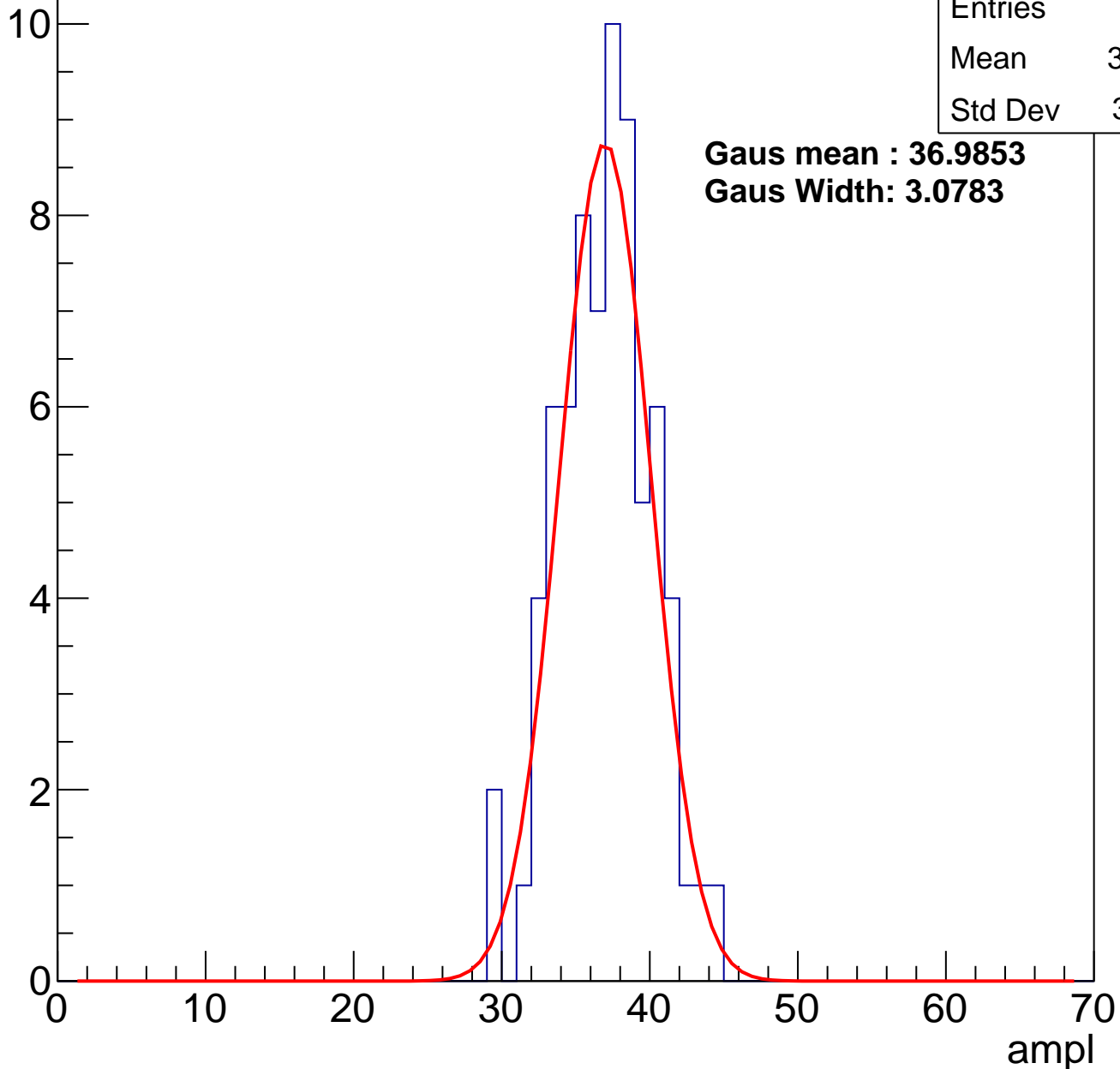
calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	36.49
Std Dev	3.121

Gaus mean : 36.9853

Gaus Width: 3.0783

Entry



B0L000S, U4-ch60, adc2

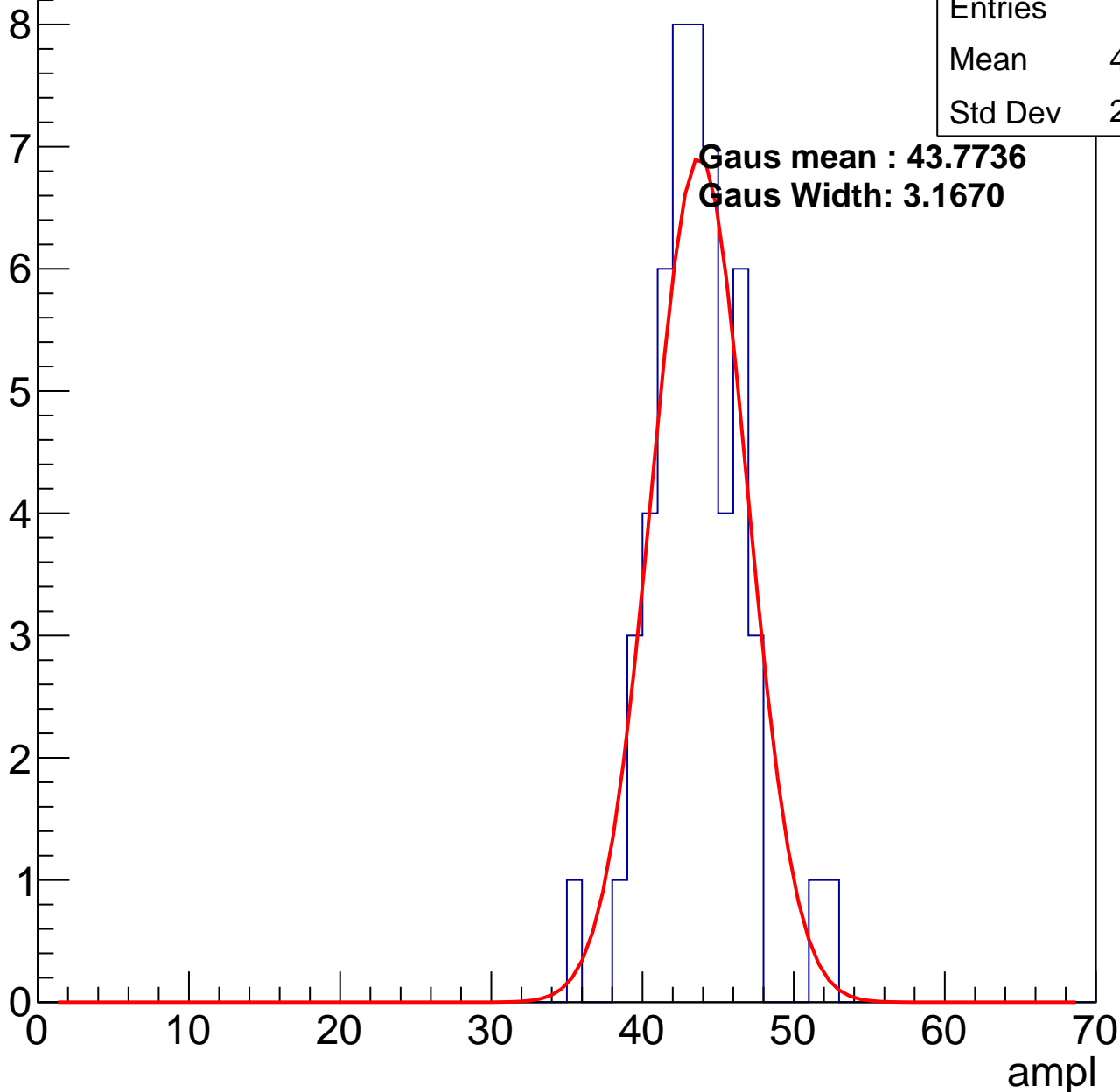
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.09
Std Dev	2.992

Gaus mean : 43.7736

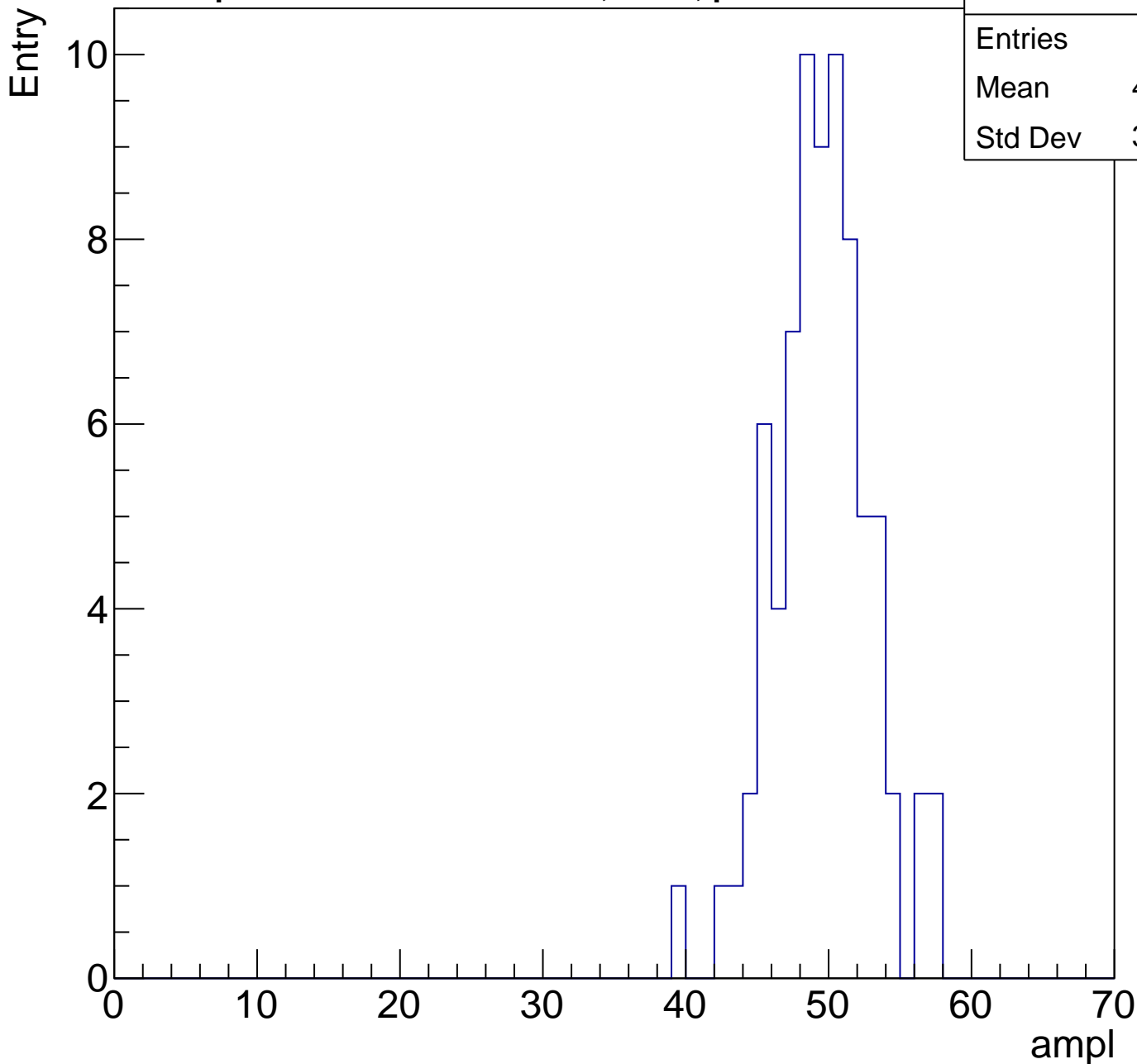
Gaus Width: 3.1670



B0L000S, U4-ch60, adc3

calib_packv5_042523_0143.root, FC#5, port B1

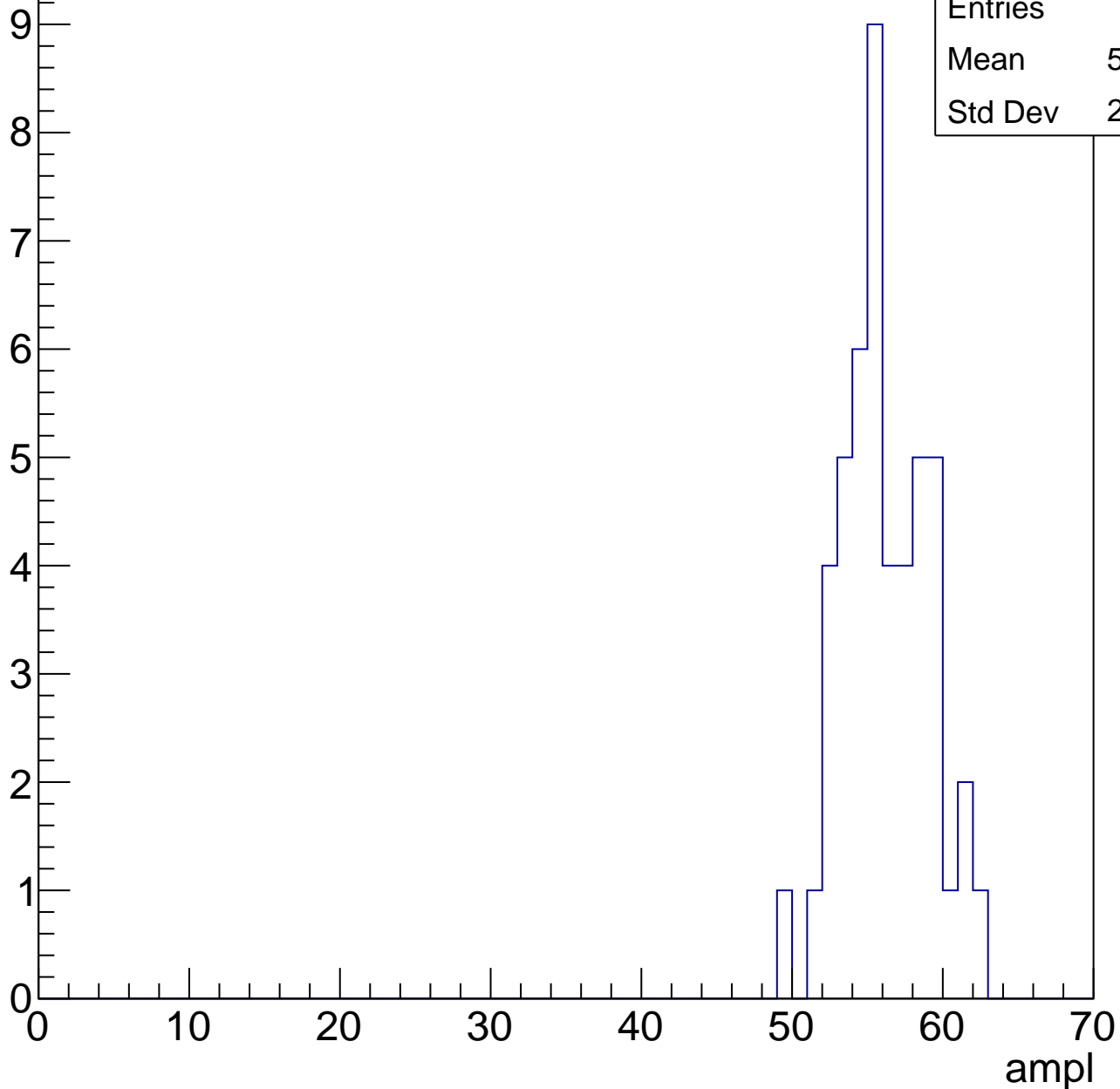
Entries	75
Mean	49.11
Std Dev	3.361



B0L000S, U4-ch60, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch60, adc5

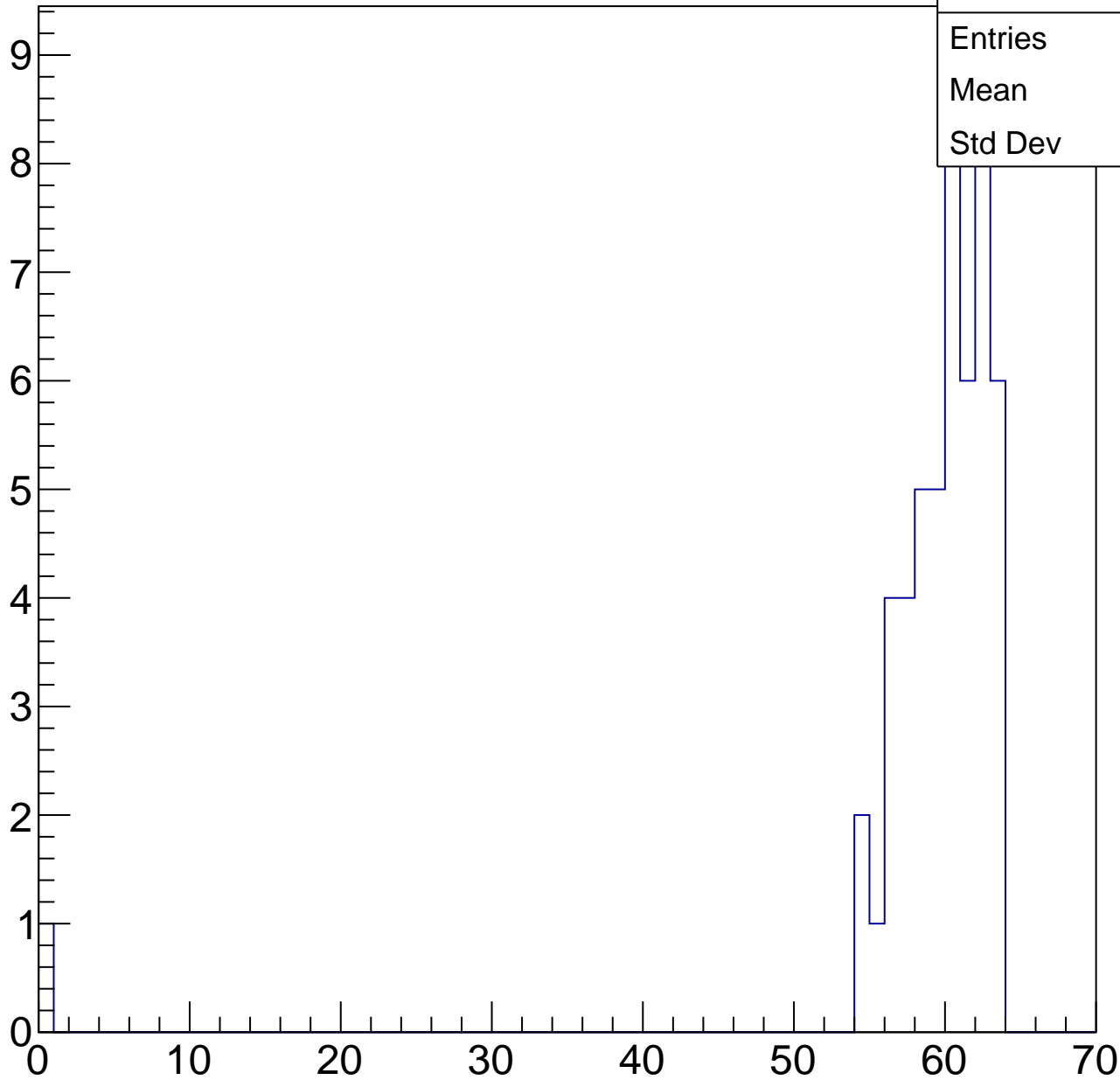
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.5
Std Dev	8.55

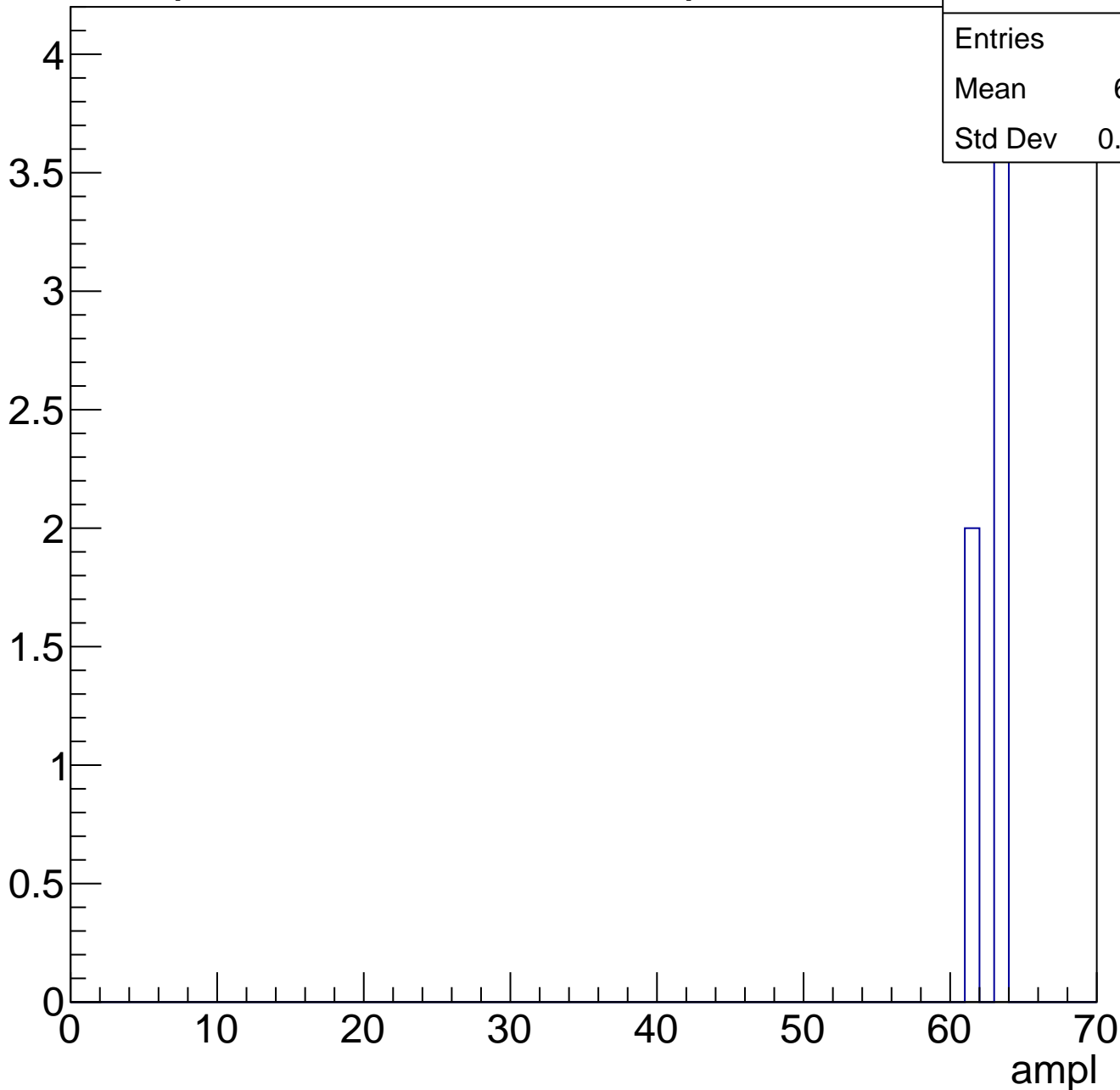
ampl



B0L000S, U4-ch60, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch60, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch61, adc0

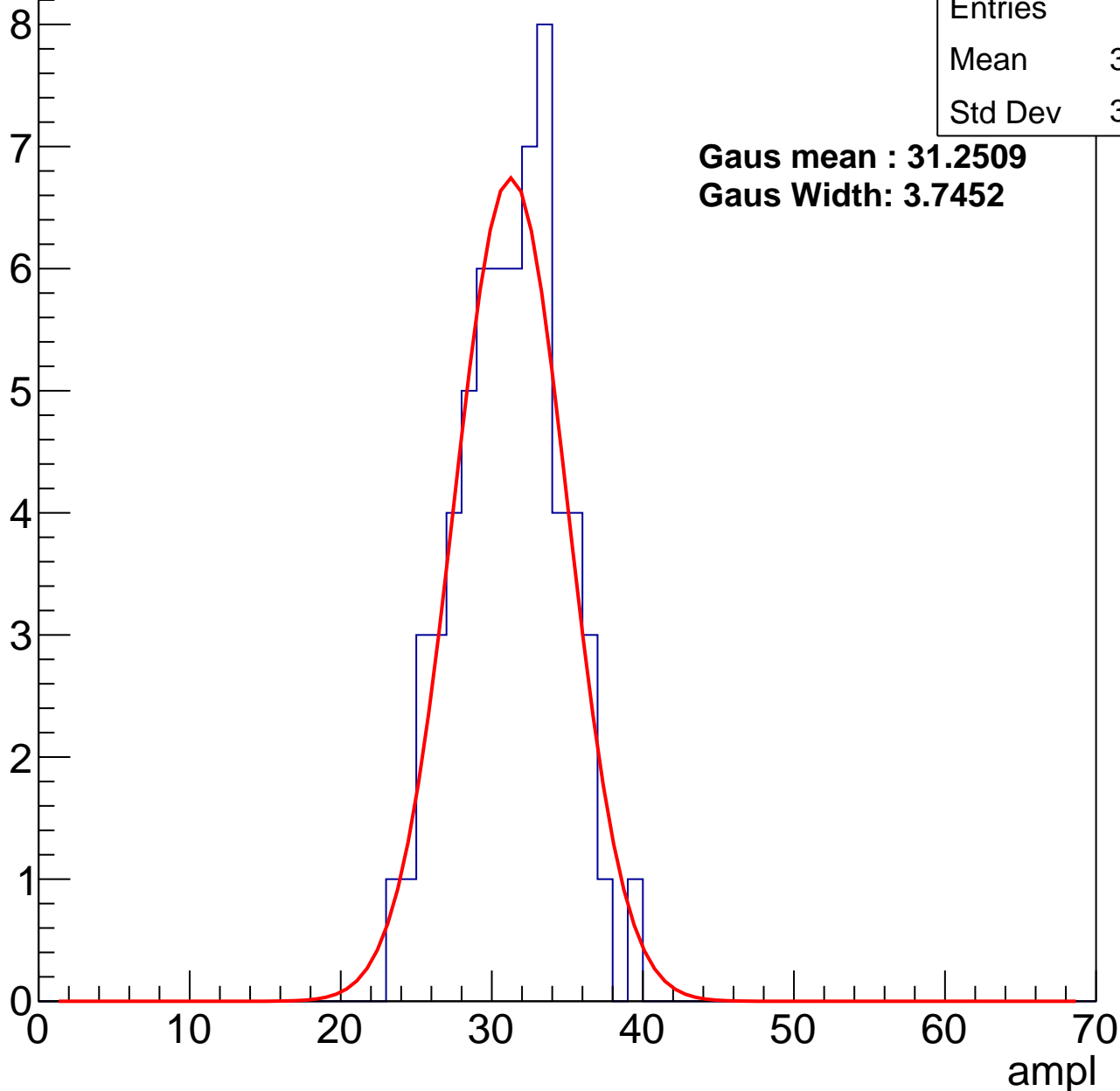
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.73
Std Dev	3.437

Gaus mean : 31.2509

Gaus Width: 3.7452



B0L000S, U4-ch61, adc1

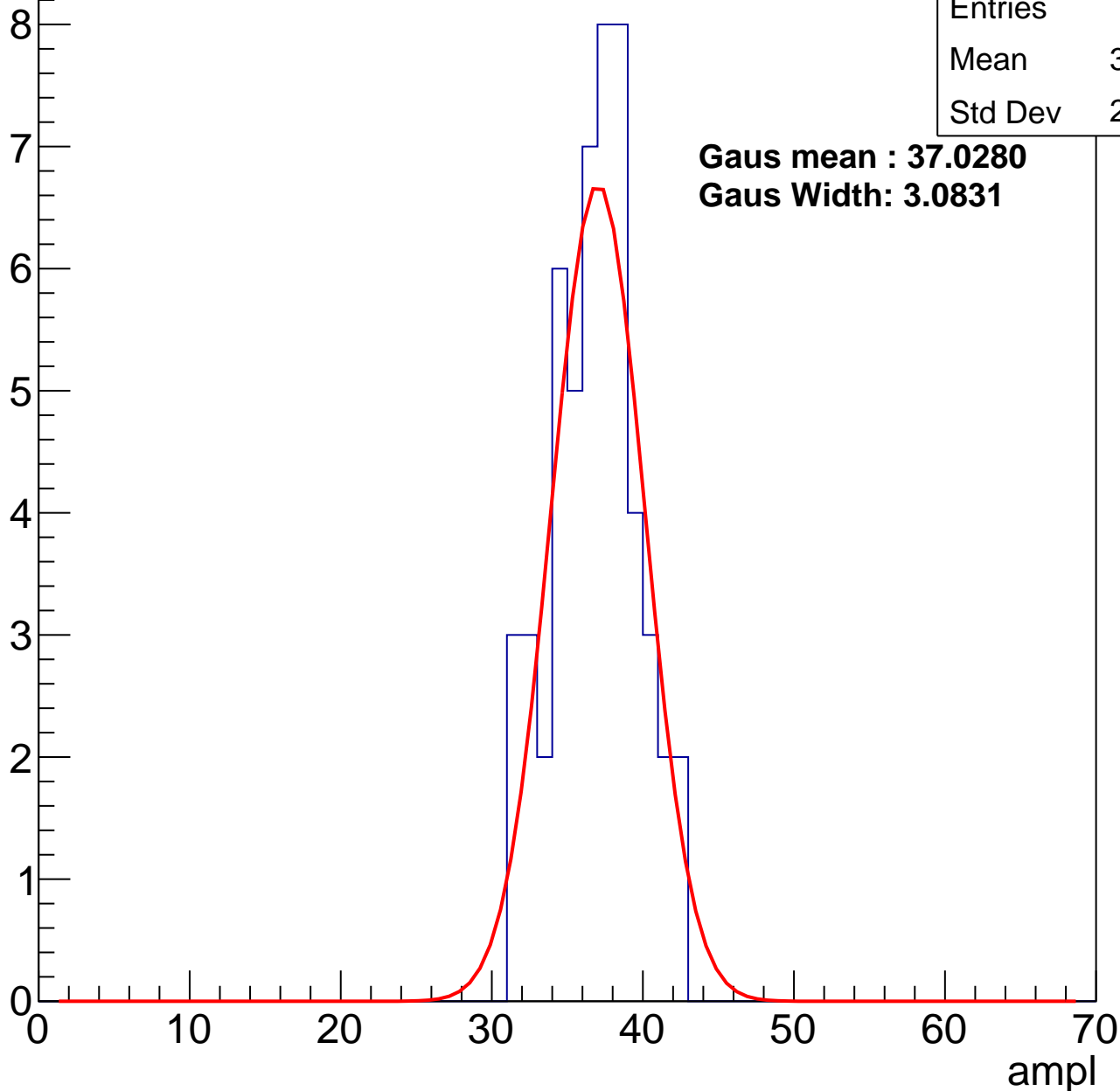
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.38
Std Dev	2.776

Gaus mean : 37.0280

Gaus Width: 3.0831



B0L000S, U4-ch61, adc2

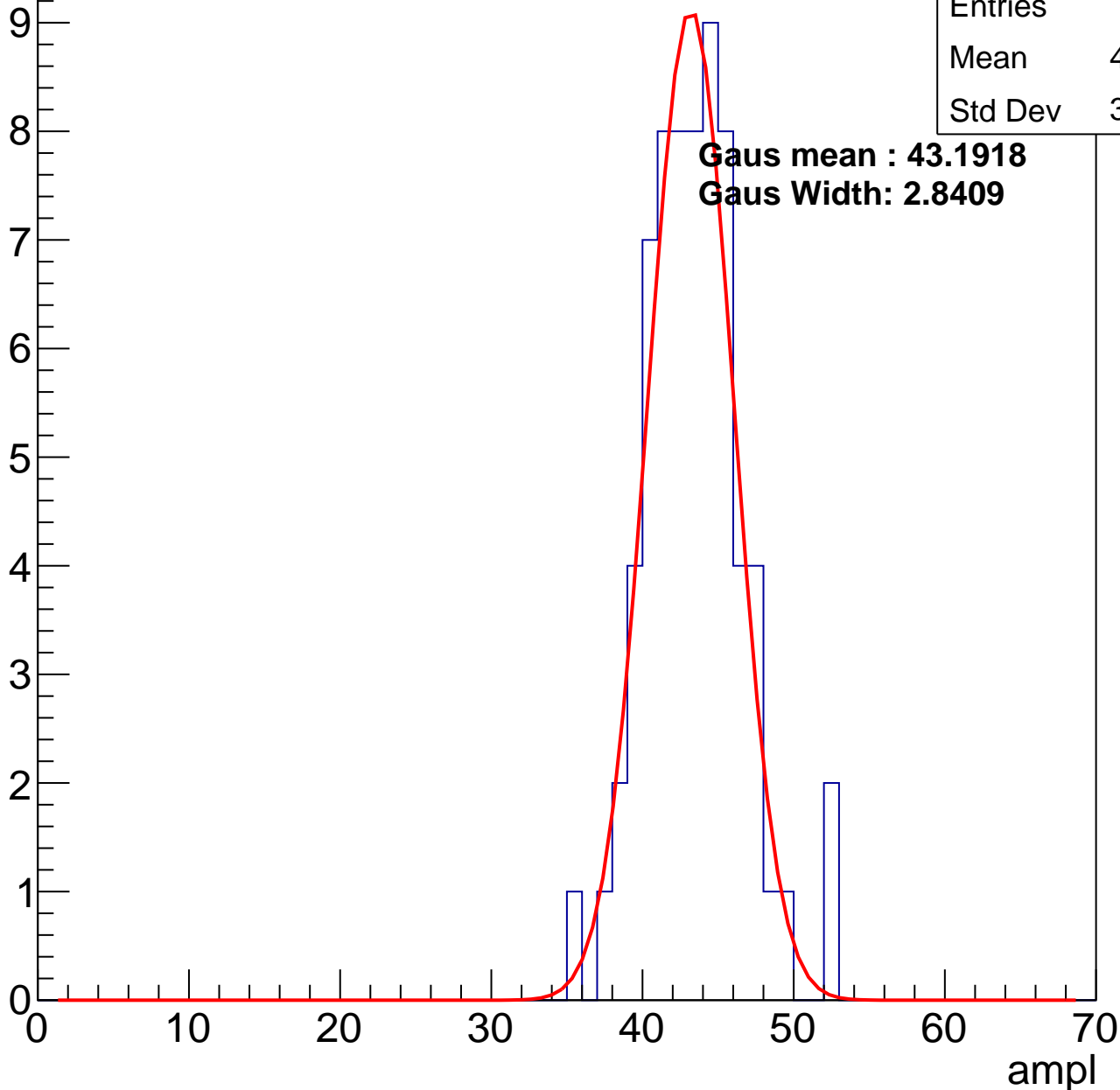
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.96
Std Dev	3.164

Gaus mean : 43.1918

Gaus Width: 2.8409

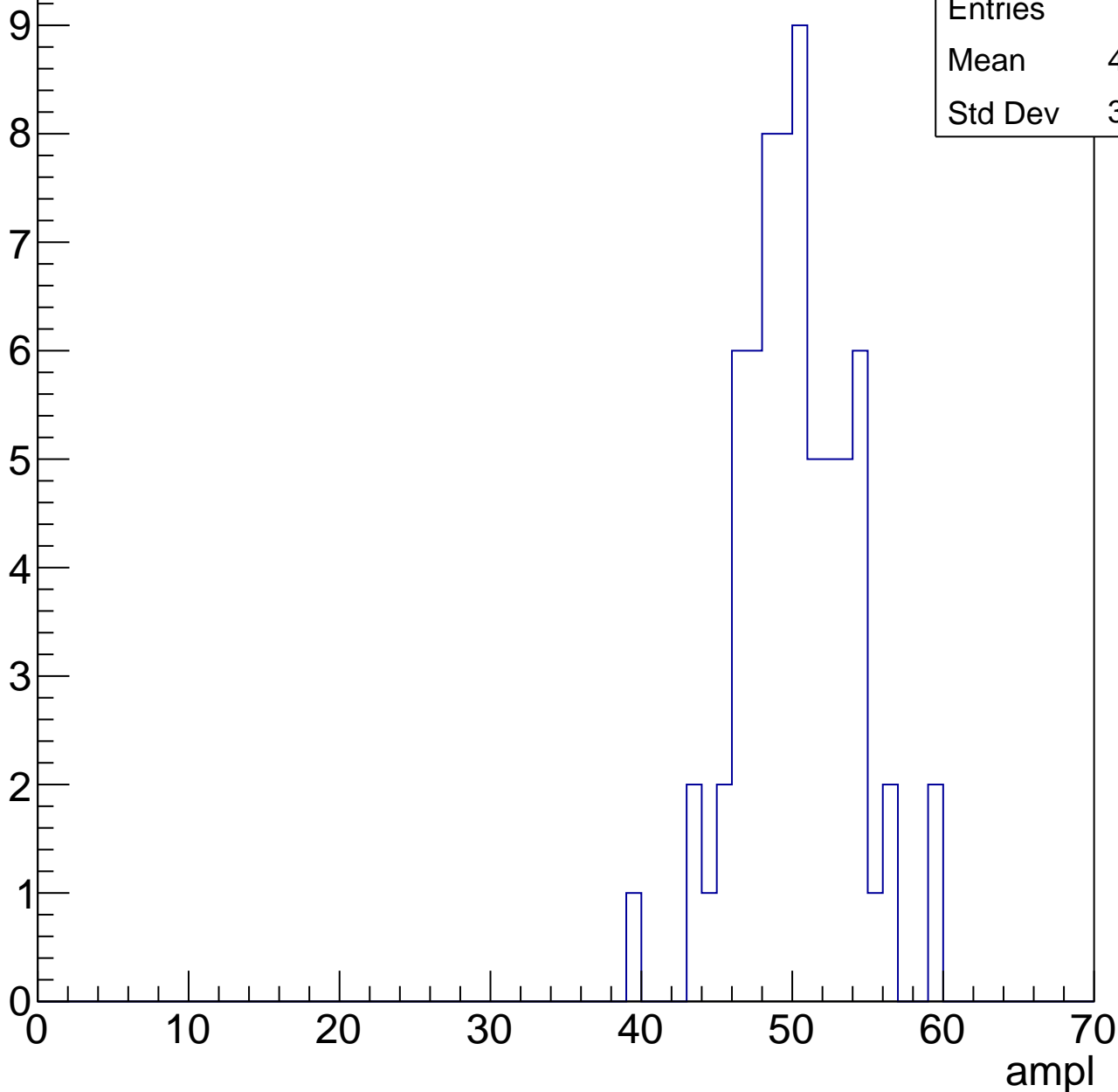


B0L000S, U4-ch61, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.74
Std Dev	3.642

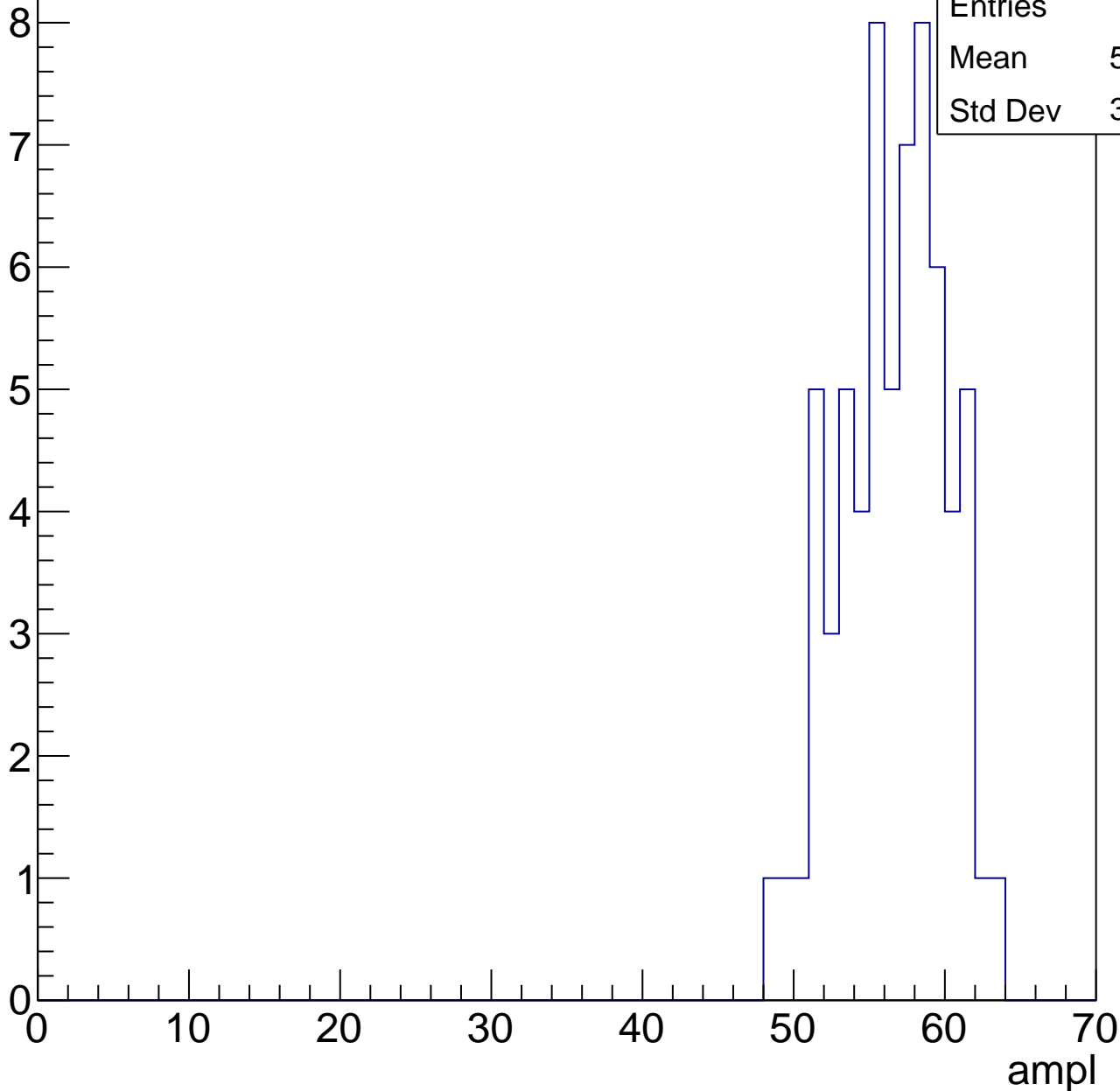


B0L000S, U4-ch61, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	56.09
Std Dev	3.414

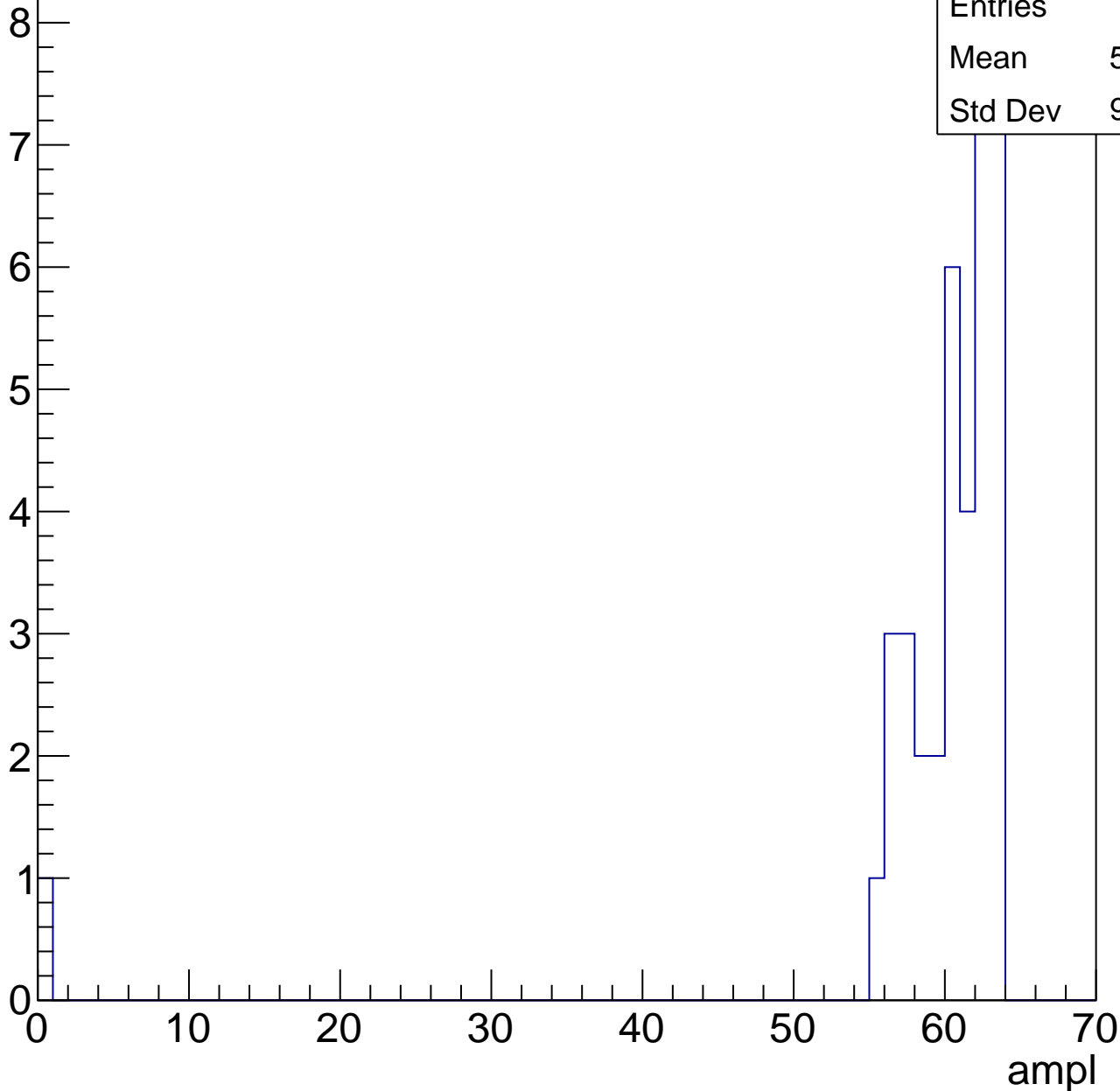


B0L000S, U4-ch61, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.74
Std Dev	9.944



B0L000S, U4-ch61, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch61, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch62, adc0

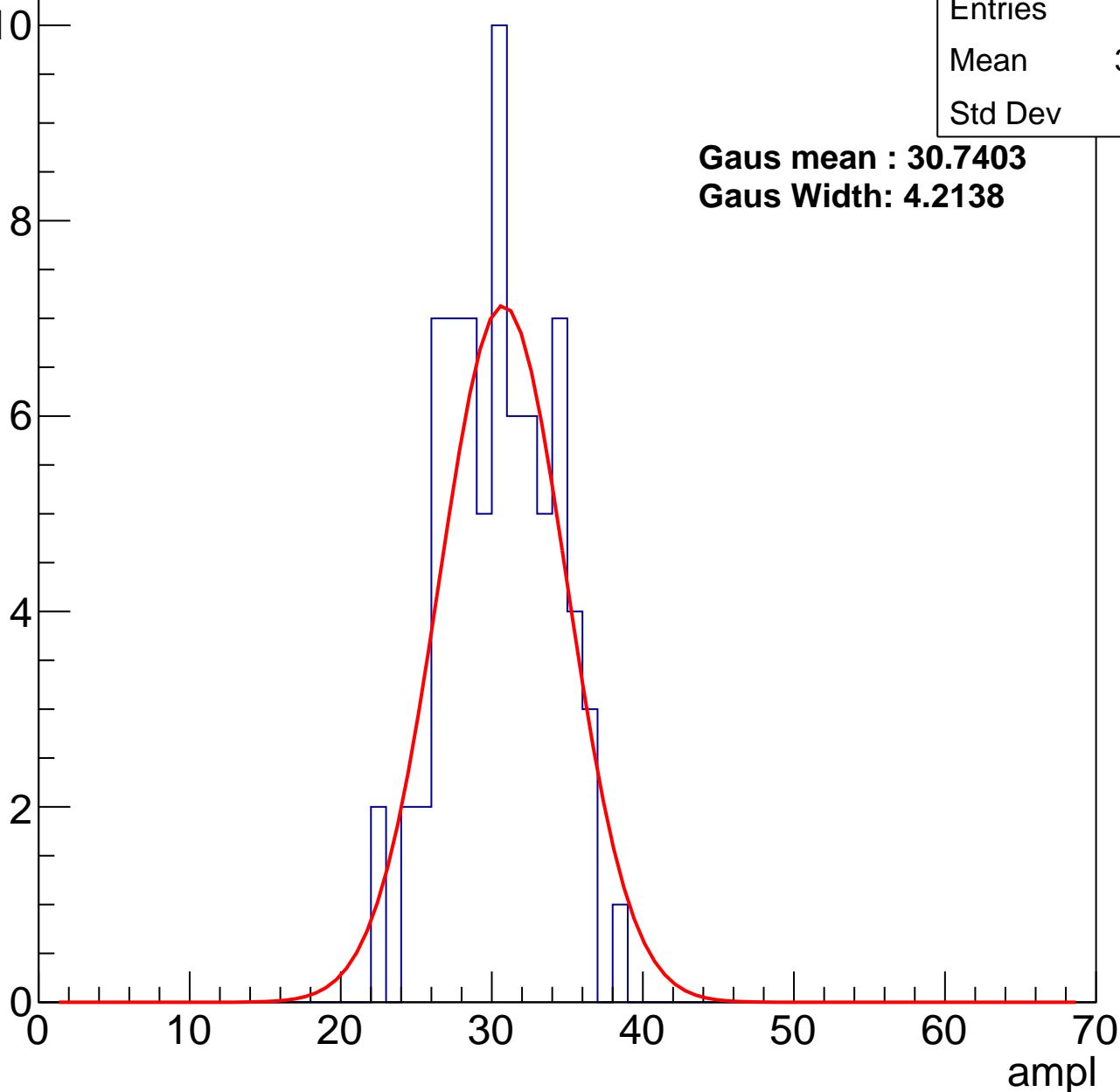
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	30.01
Std Dev	3.52

Gaus mean : 30.7403

Gaus Width: 4.2138



B0L000S, U4-ch62, adc1

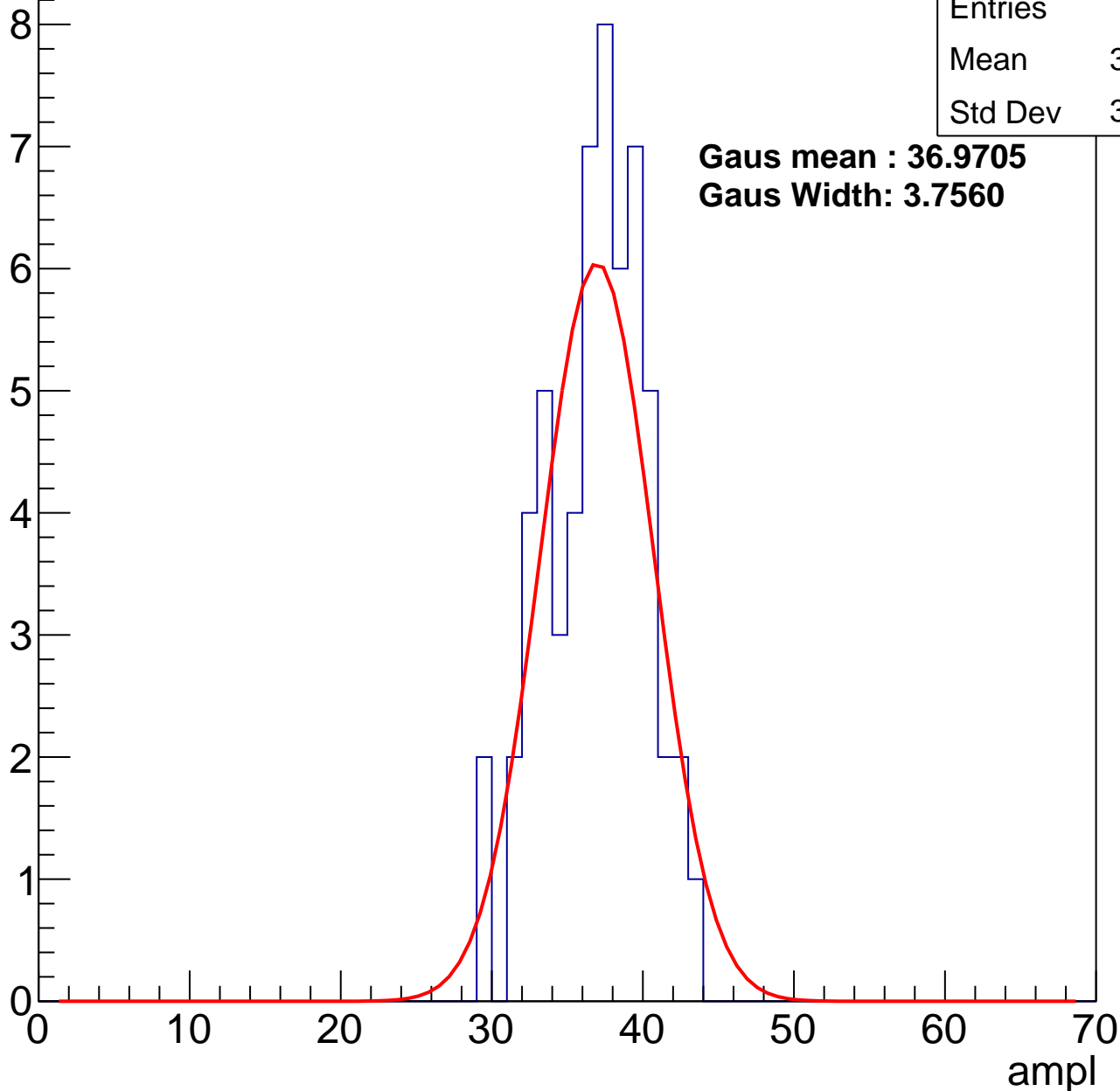
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	36.43
Std Dev	3.238

Gaus mean : 36.9705

Gaus Width: 3.7560

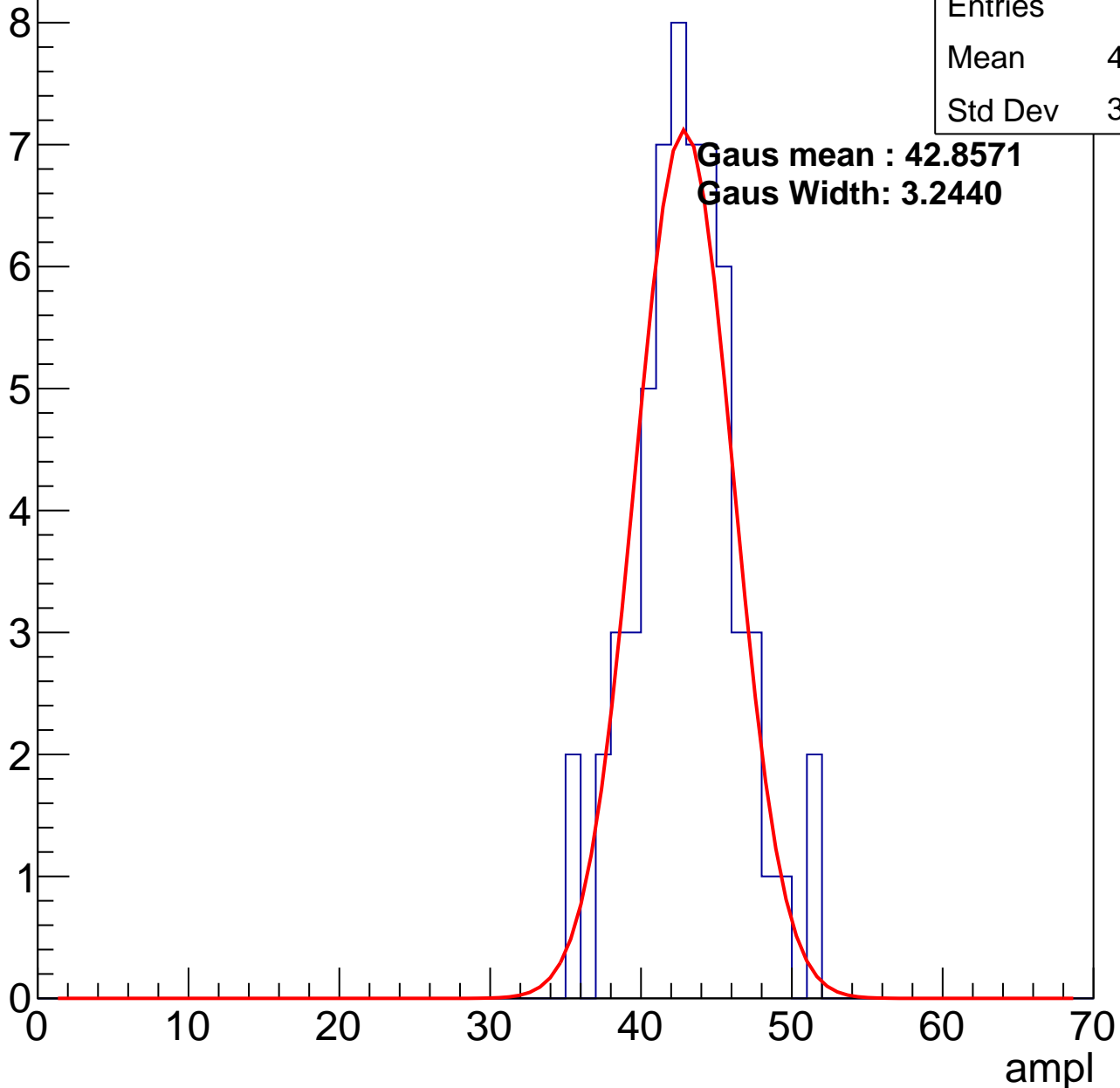


B0L000S, U4-ch62, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.58
Std Dev	3.388

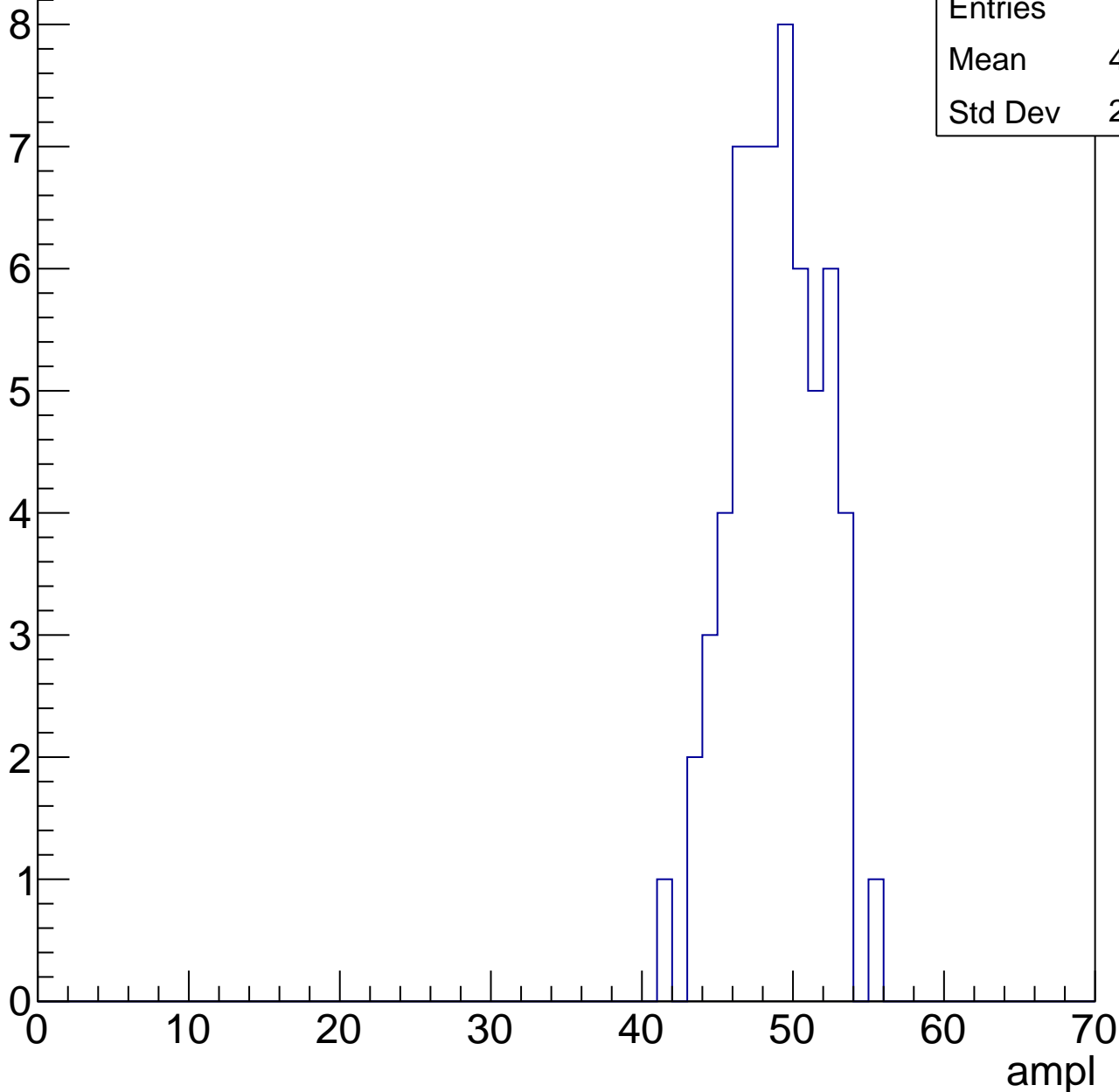


B0L000S, U4-ch62, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

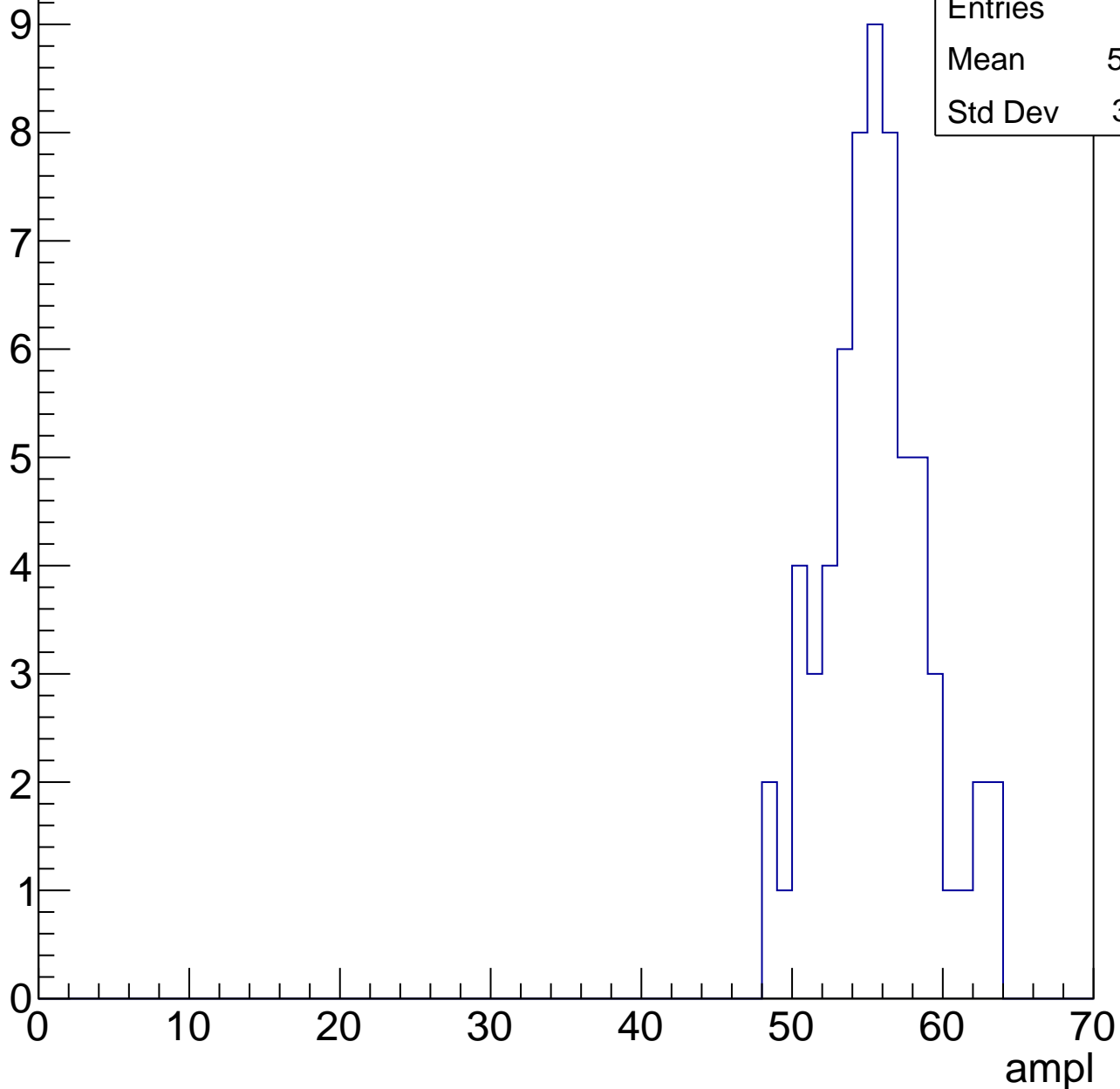
Entries	61
Mean	48.39
Std Dev	2.949



B0L000S, U4-ch62, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	64
Mean	55.03
Std Dev	3.441

B0L000S, U4-ch62, adc5

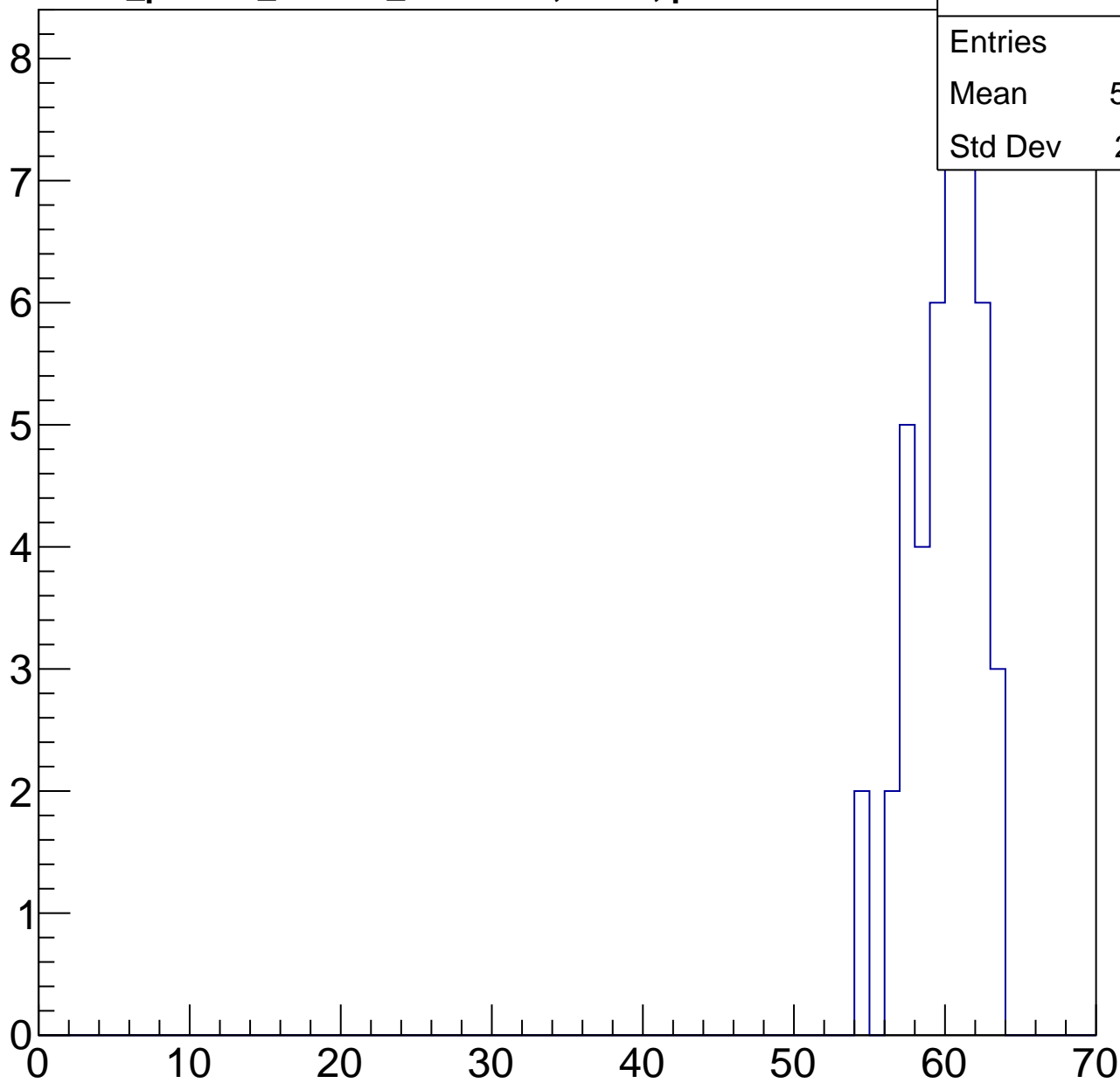
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	59.55
Std Dev	2.241

ampl

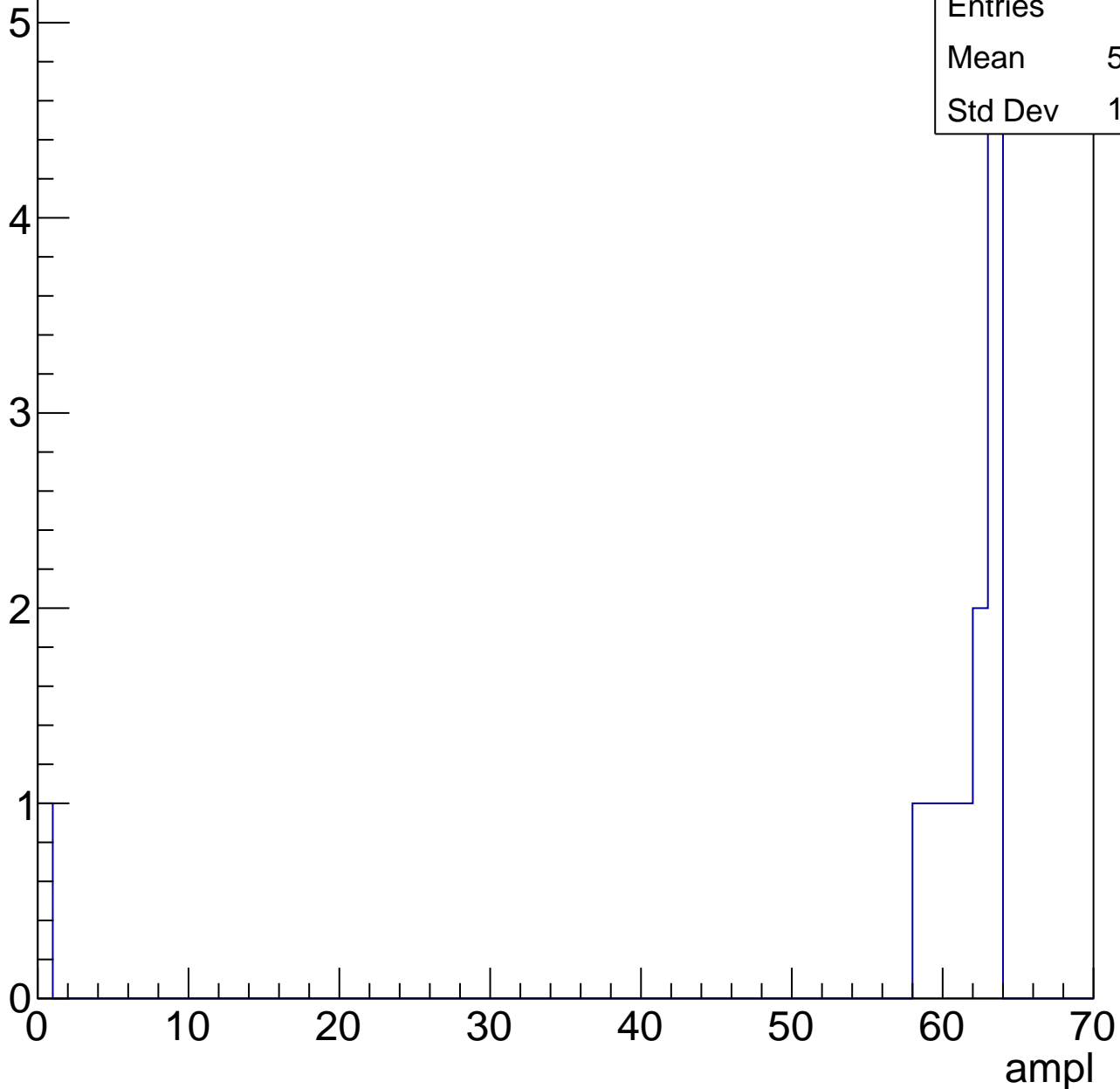


B0L000S, U4-ch62, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	12
Mean	56.42
Std Dev	17.09



B0L000S, U4-ch62, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch63, adc0

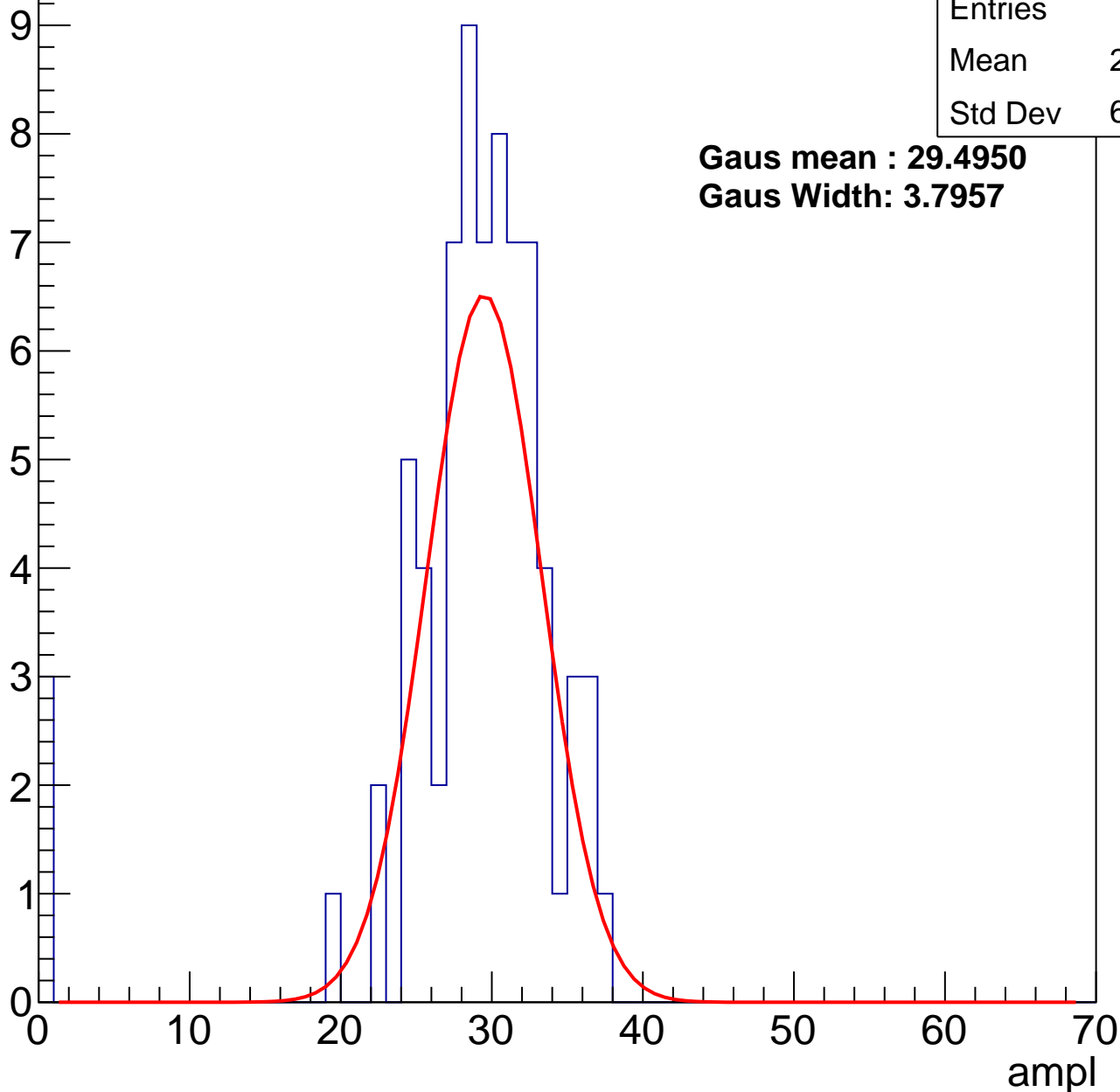
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	28.05
Std Dev	6.784

Gaus mean : 29.4950

Gaus Width: 3.7957



B0L000S, U4-ch63, adc1

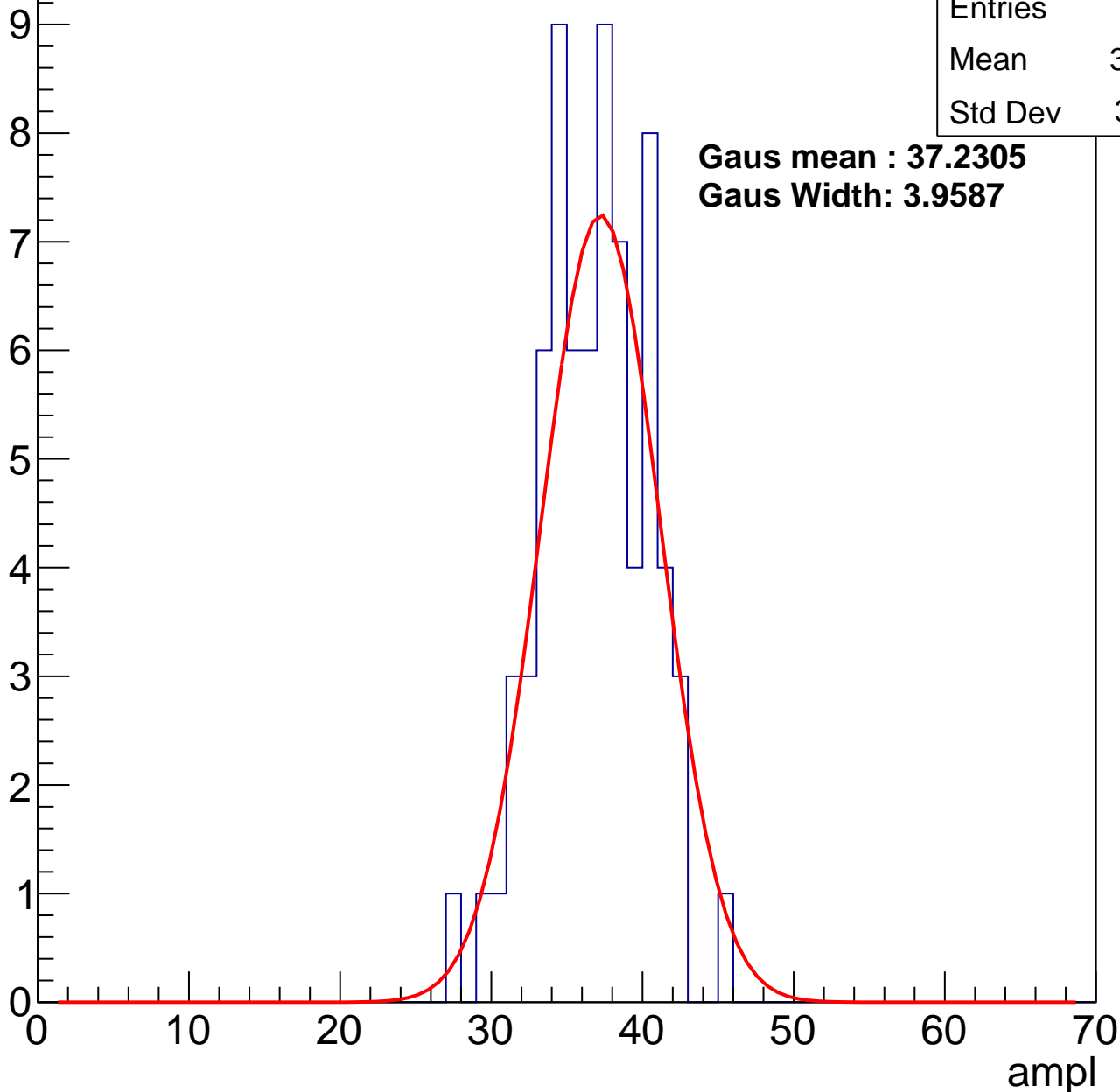
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.32
Std Dev	3.471

Gaus mean : 37.2305

Gaus Width: 3.9587



B0L000S, U4-ch63, adc2

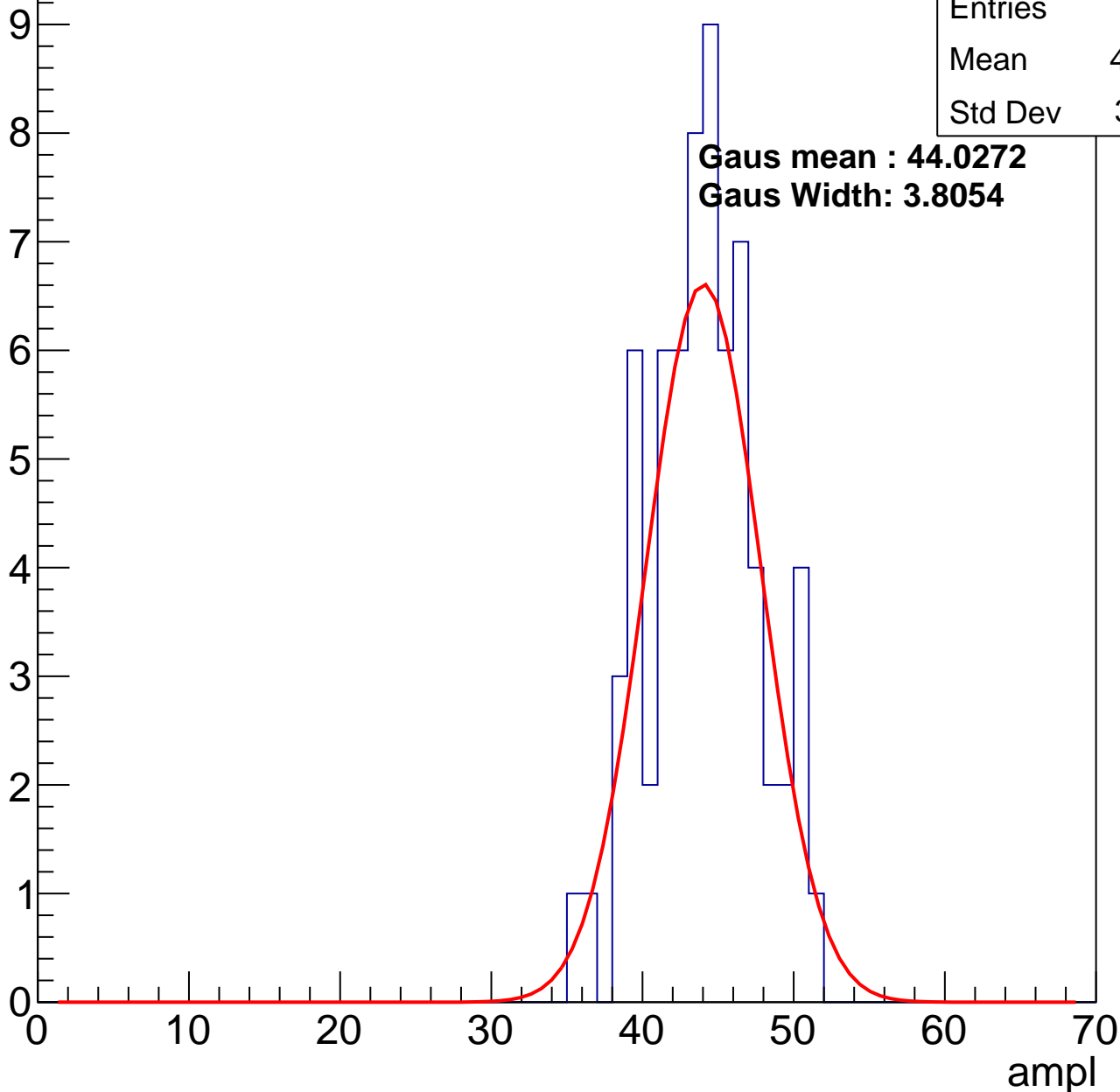
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.56
Std Dev	3.541

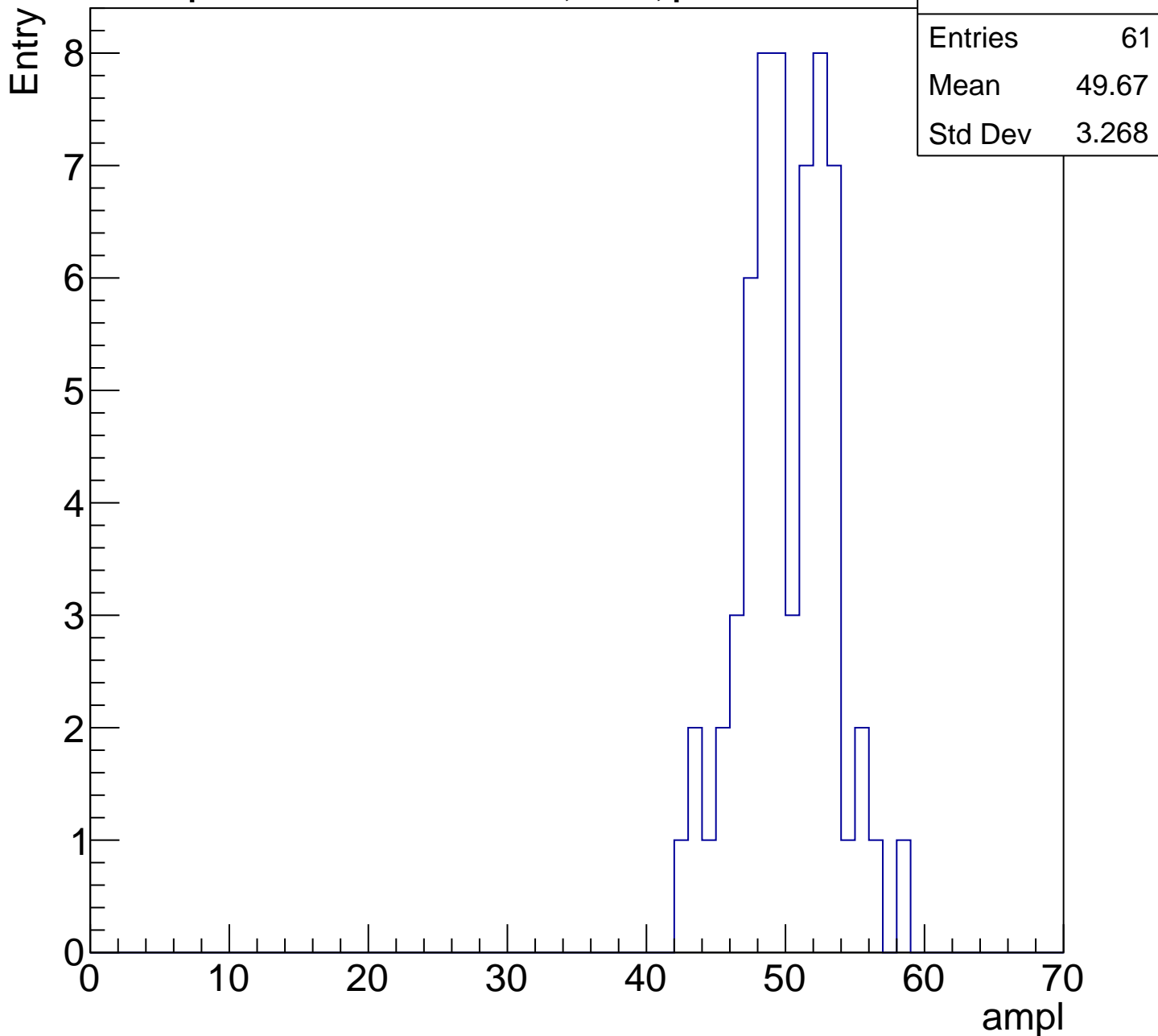
Gaus mean : 44.0272

Gaus Width: 3.8054



B0L000S, U4-ch63, adc3

calib_packv5_042523_0143.root, FC#5, port B1

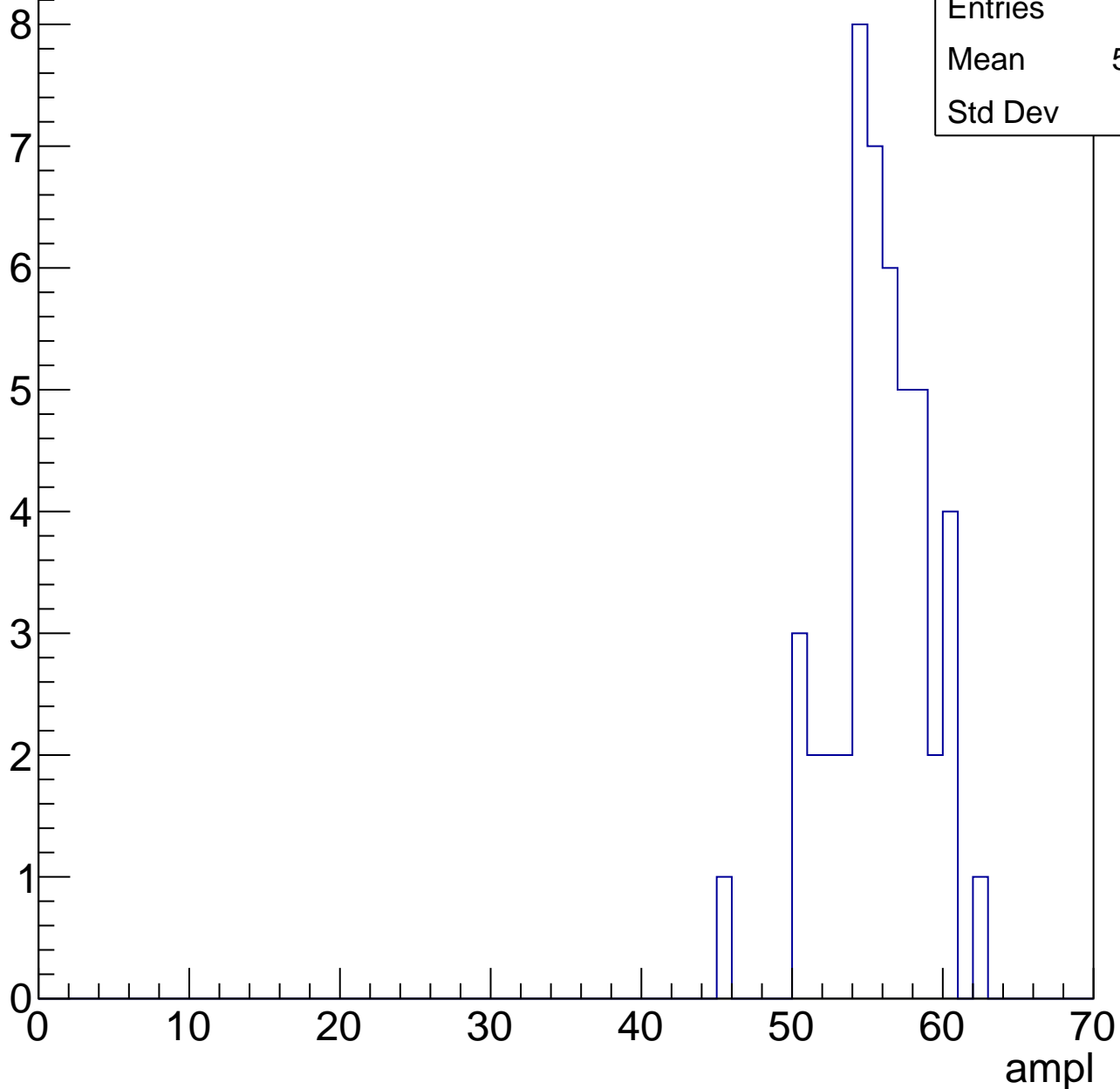


B0L000S, U4-ch63, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	55.31
Std Dev	3.19



B0L000S, U4-ch63, adc5

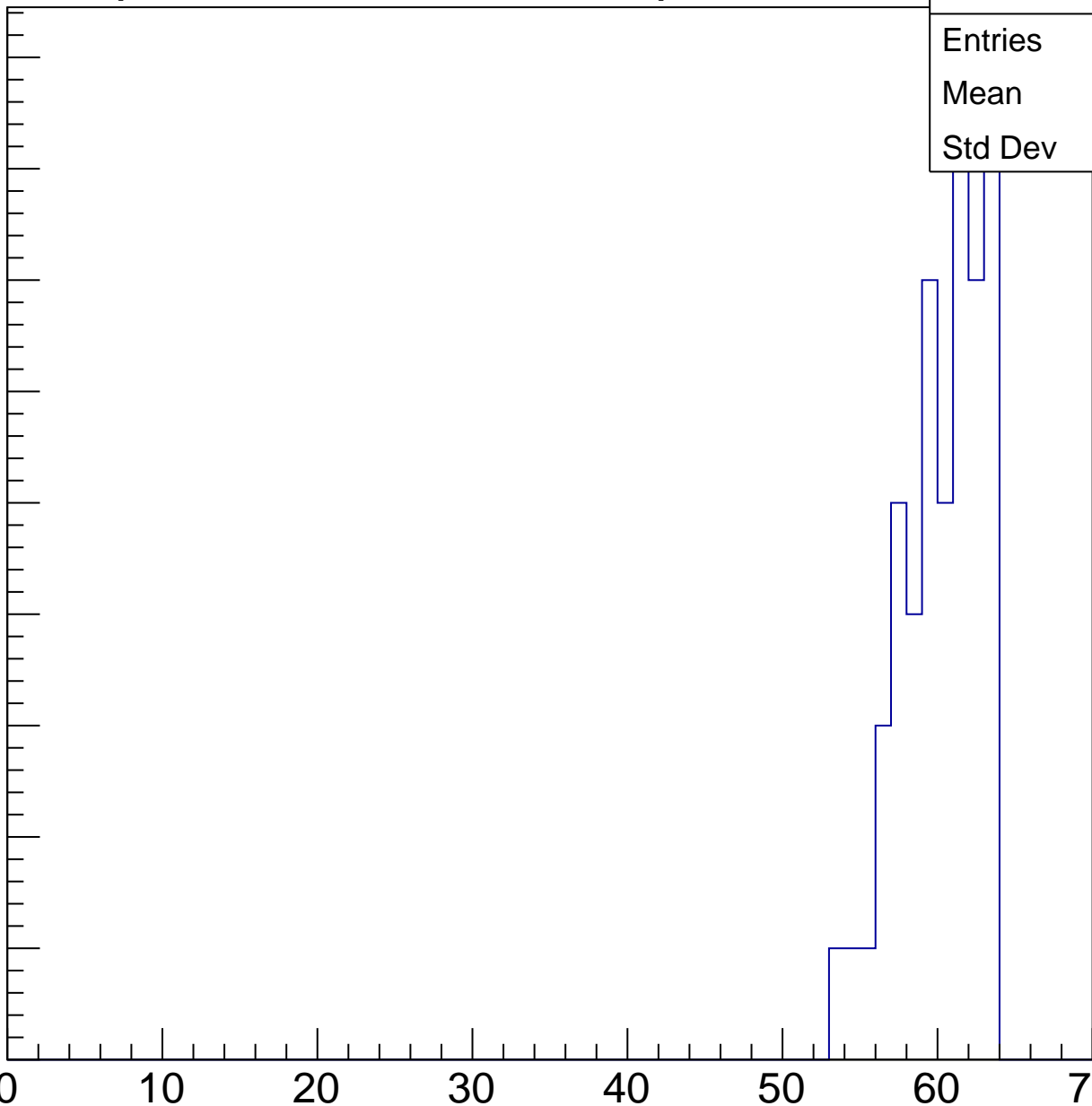
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.75
Std Dev	2.55

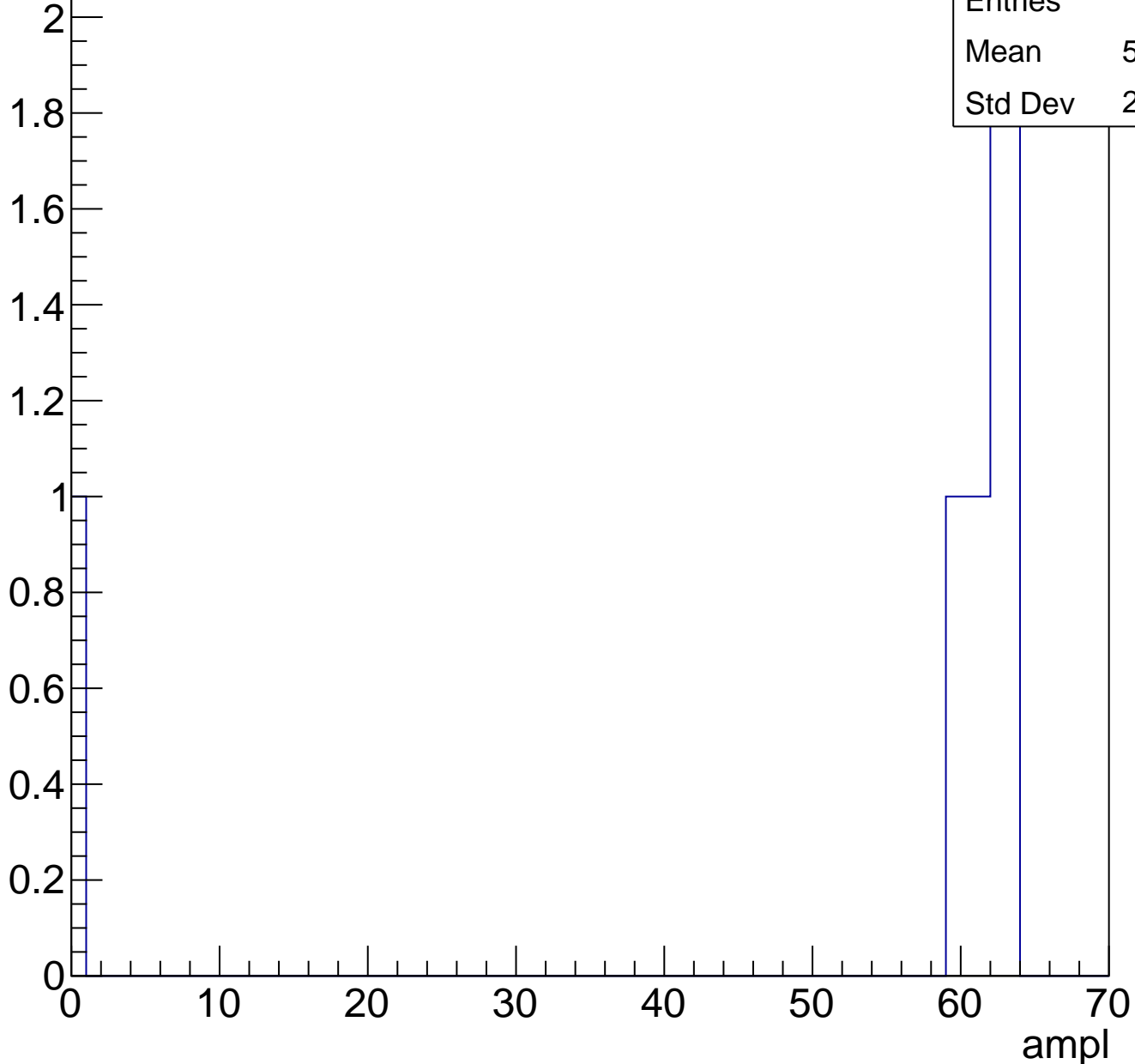
ampl



B0L000S, U4-ch63, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch63, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U4-ch64, adc0

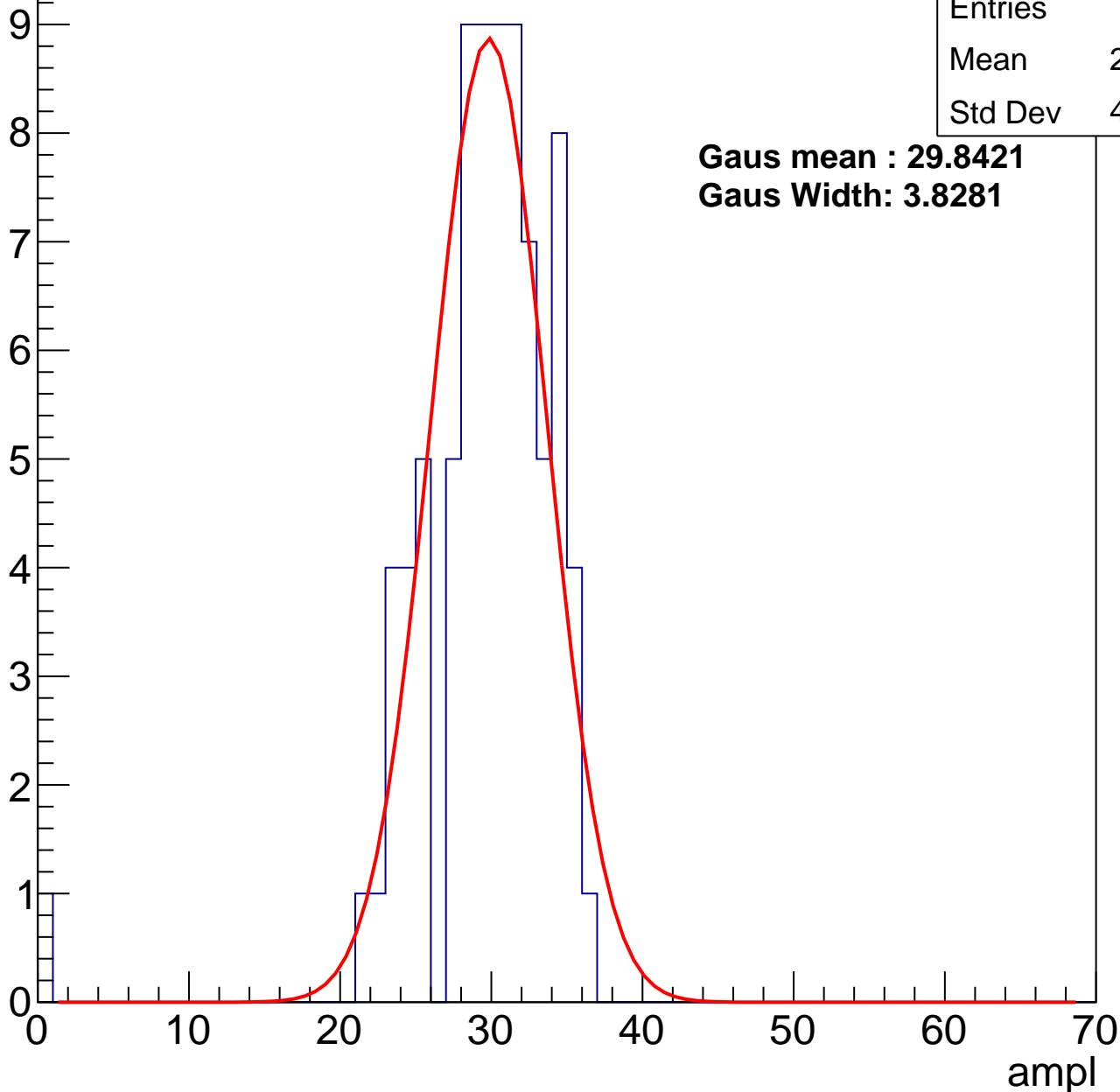
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	29.15
Std Dev	4.796

Gaus mean : 29.8421

Gaus Width: 3.8281



B0L000S, U4-ch64, adc1

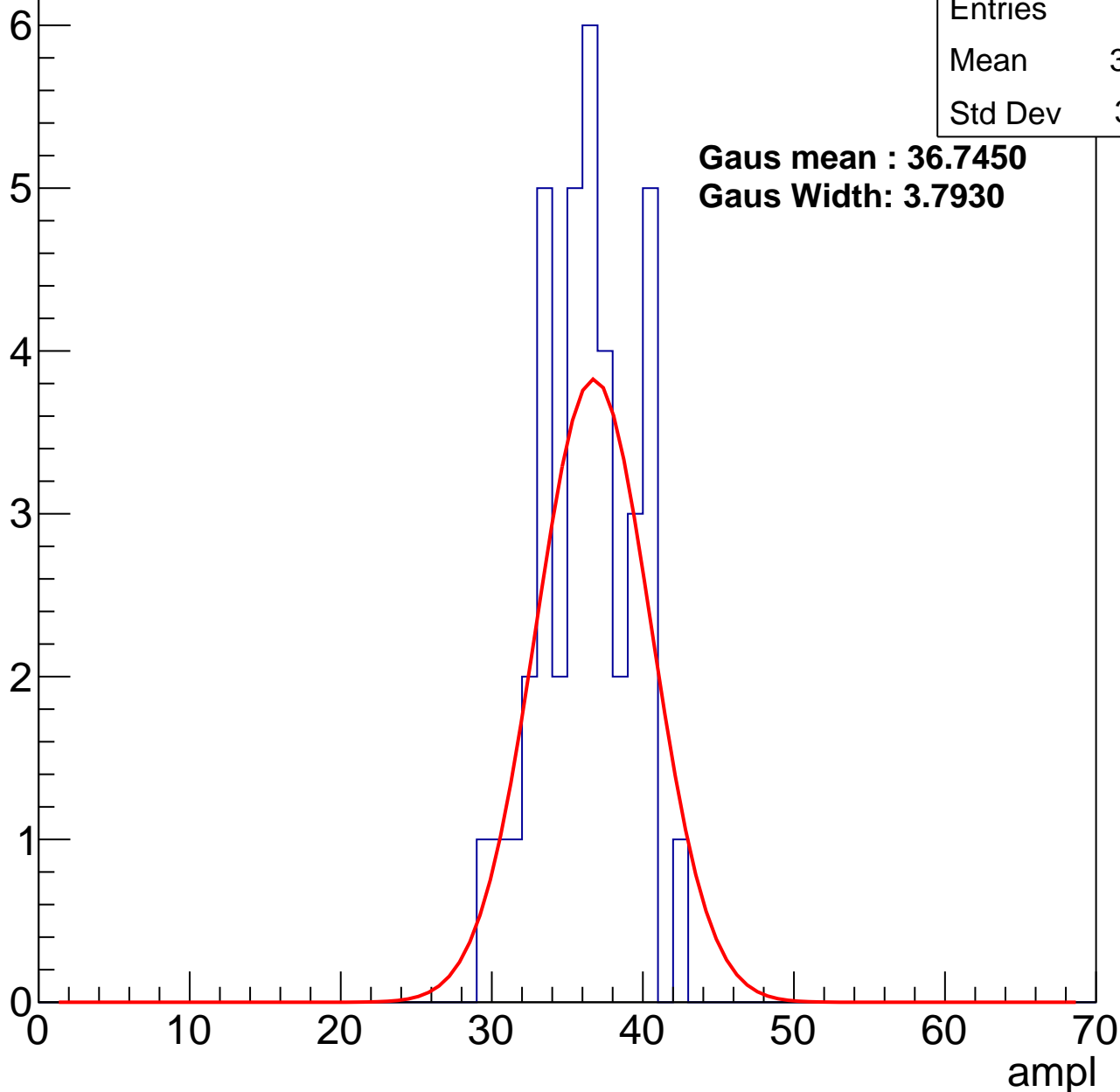
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	38
Mean	35.82
Std Dev	3.051

Gaus mean : 36.7450

Gaus Width: 3.7930



B0L000S, U4-ch64, adc2

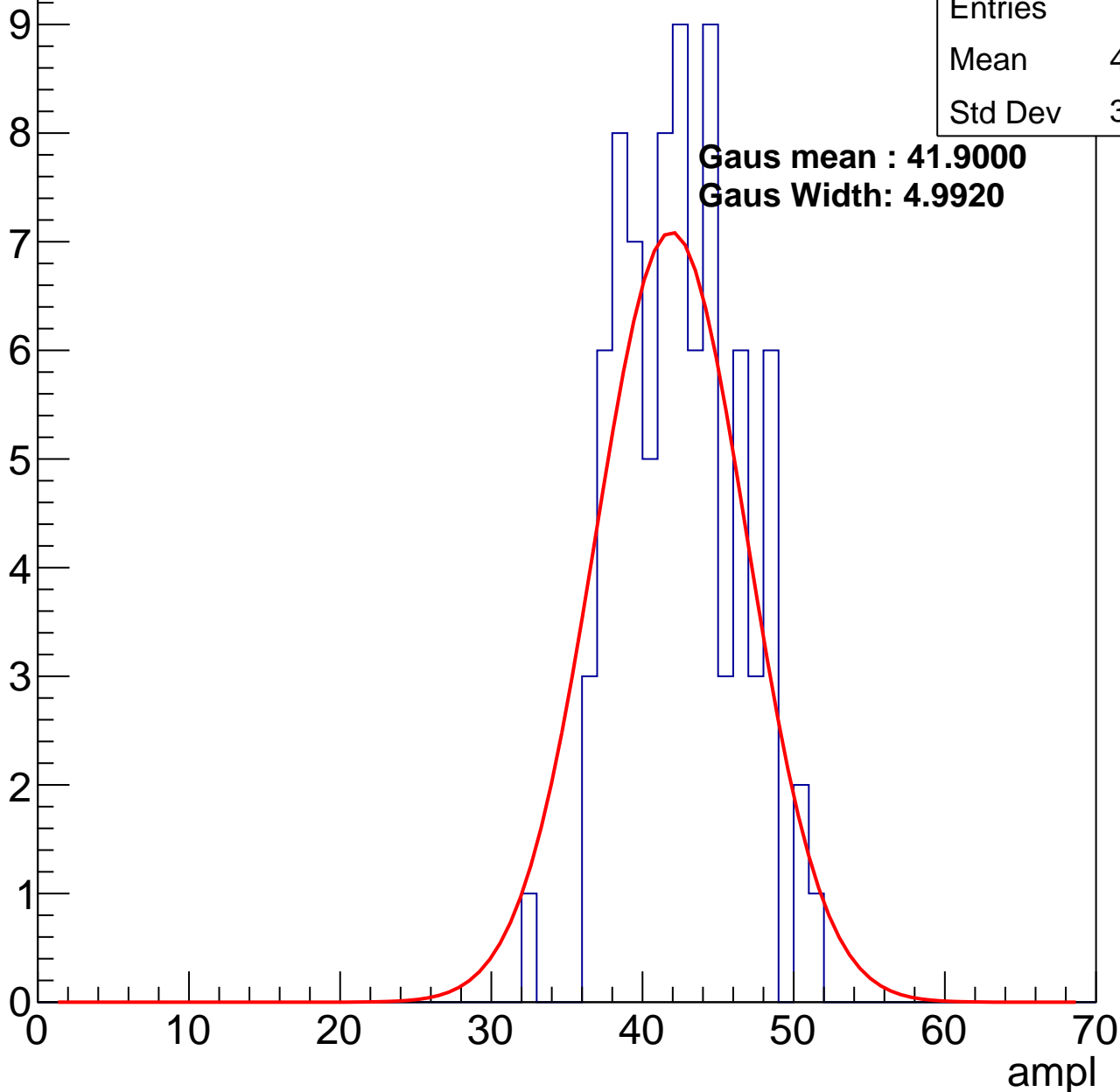
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	83
Mean	42.05
Std Dev	3.868

Gaus mean : 41.9000

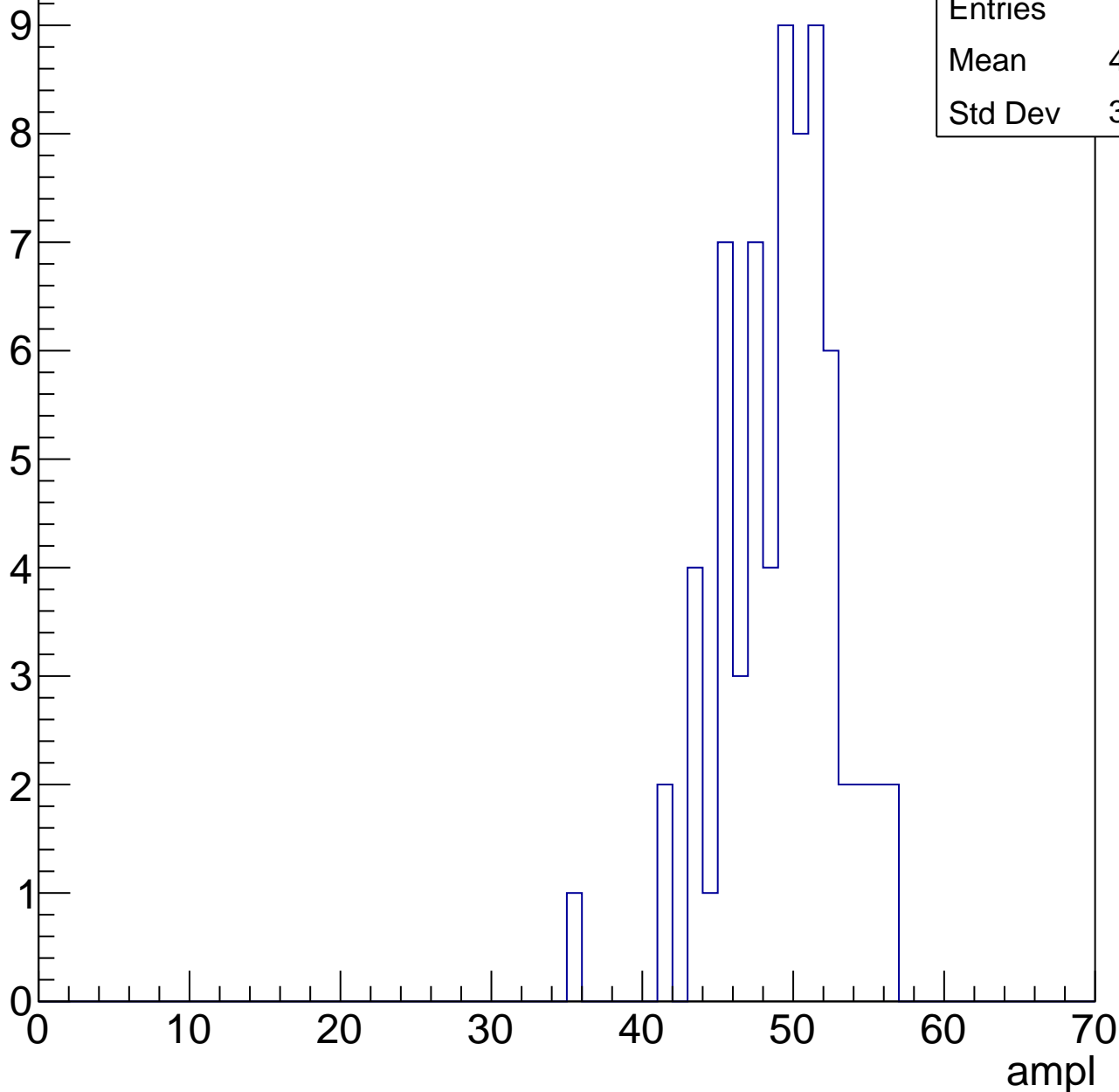
Gaus Width: 4.9920



B0L000S, U4-ch64, adc3

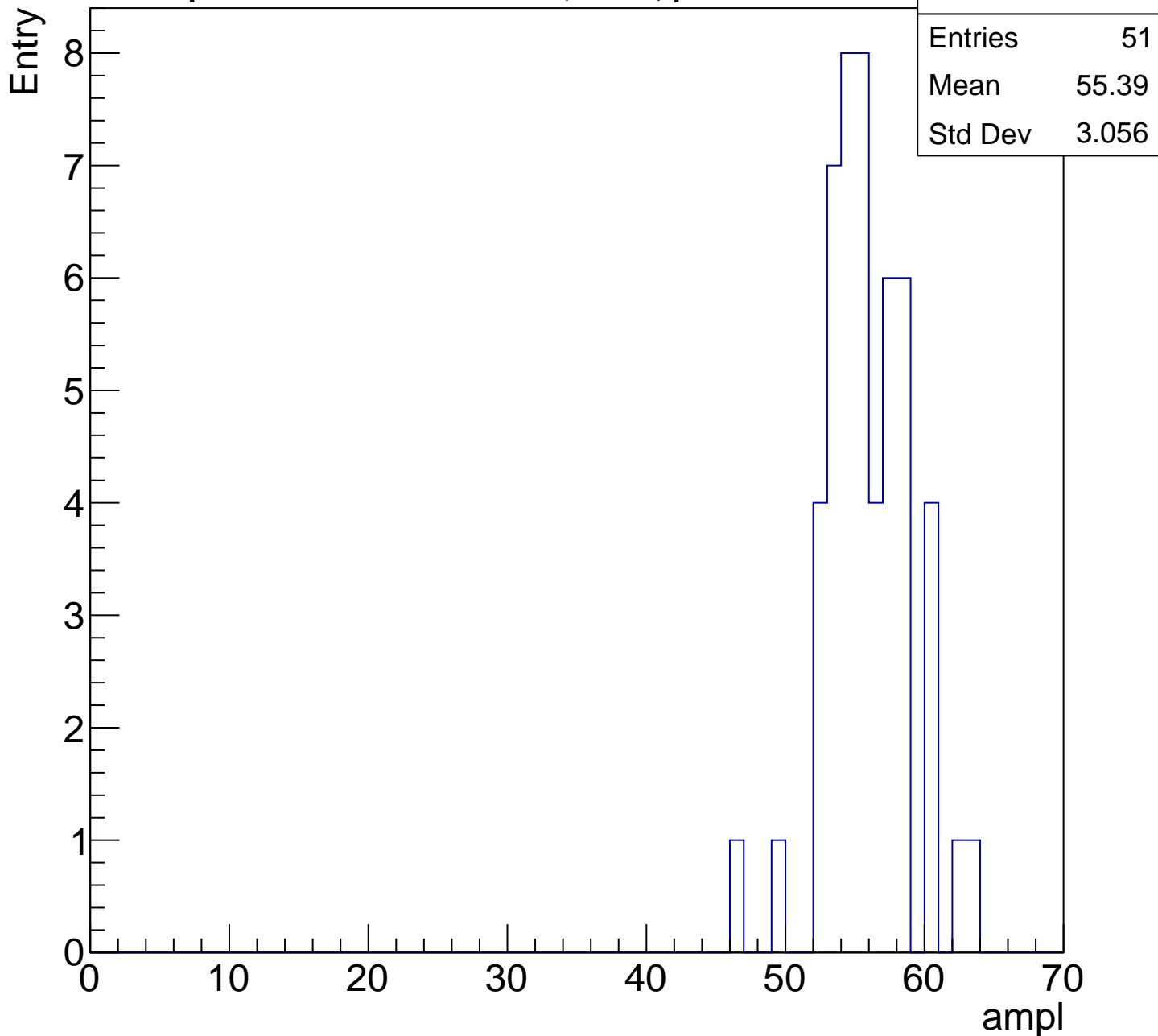
calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch64, adc4

calib_packv5_042523_0143.root, FC#5, port B1

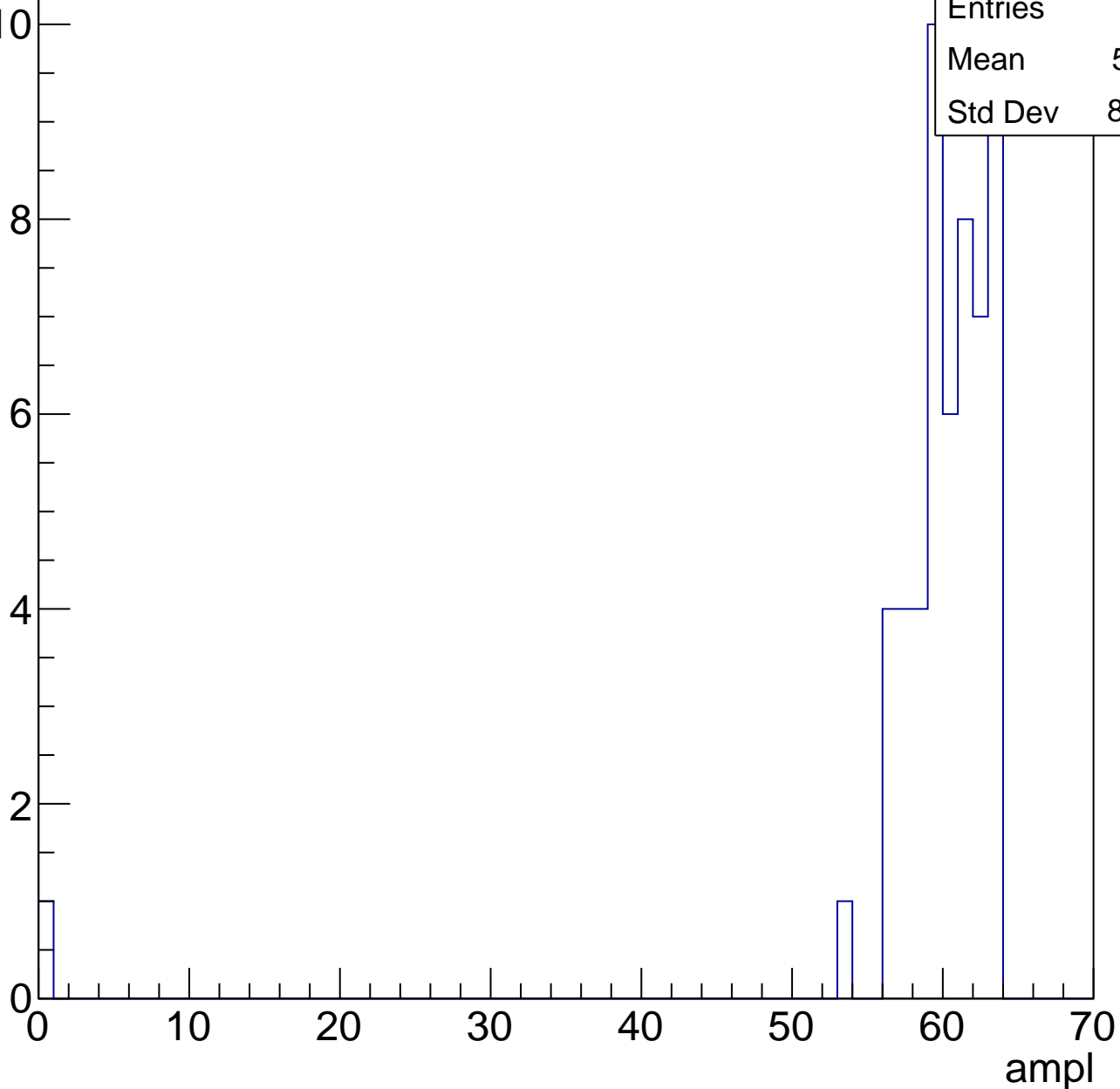


B0L000S, U4-ch64, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

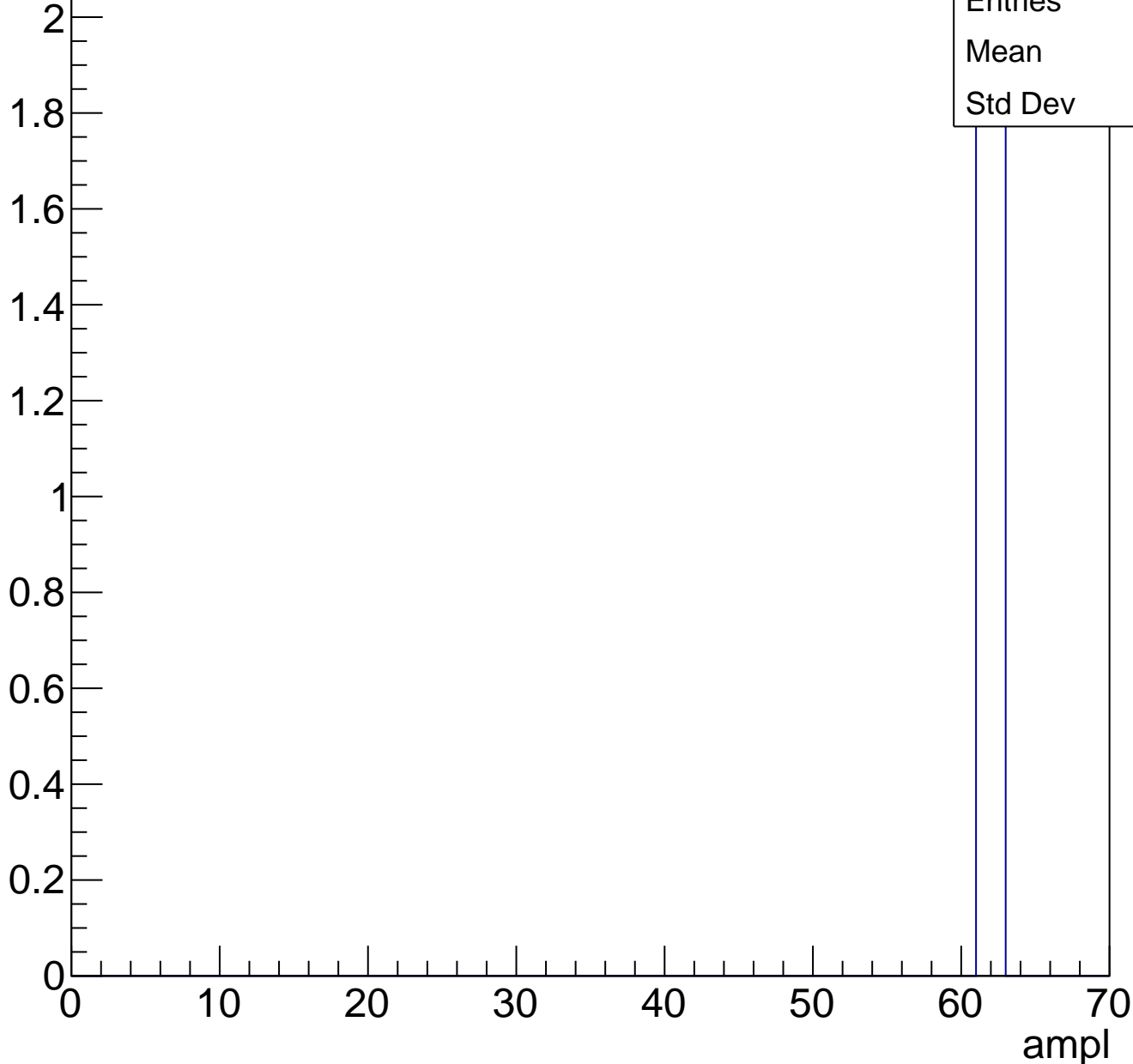
Entries	54
Mean	58.81
Std Dev	8.407



B0L000S, U4-ch64, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch64, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch65, adc0

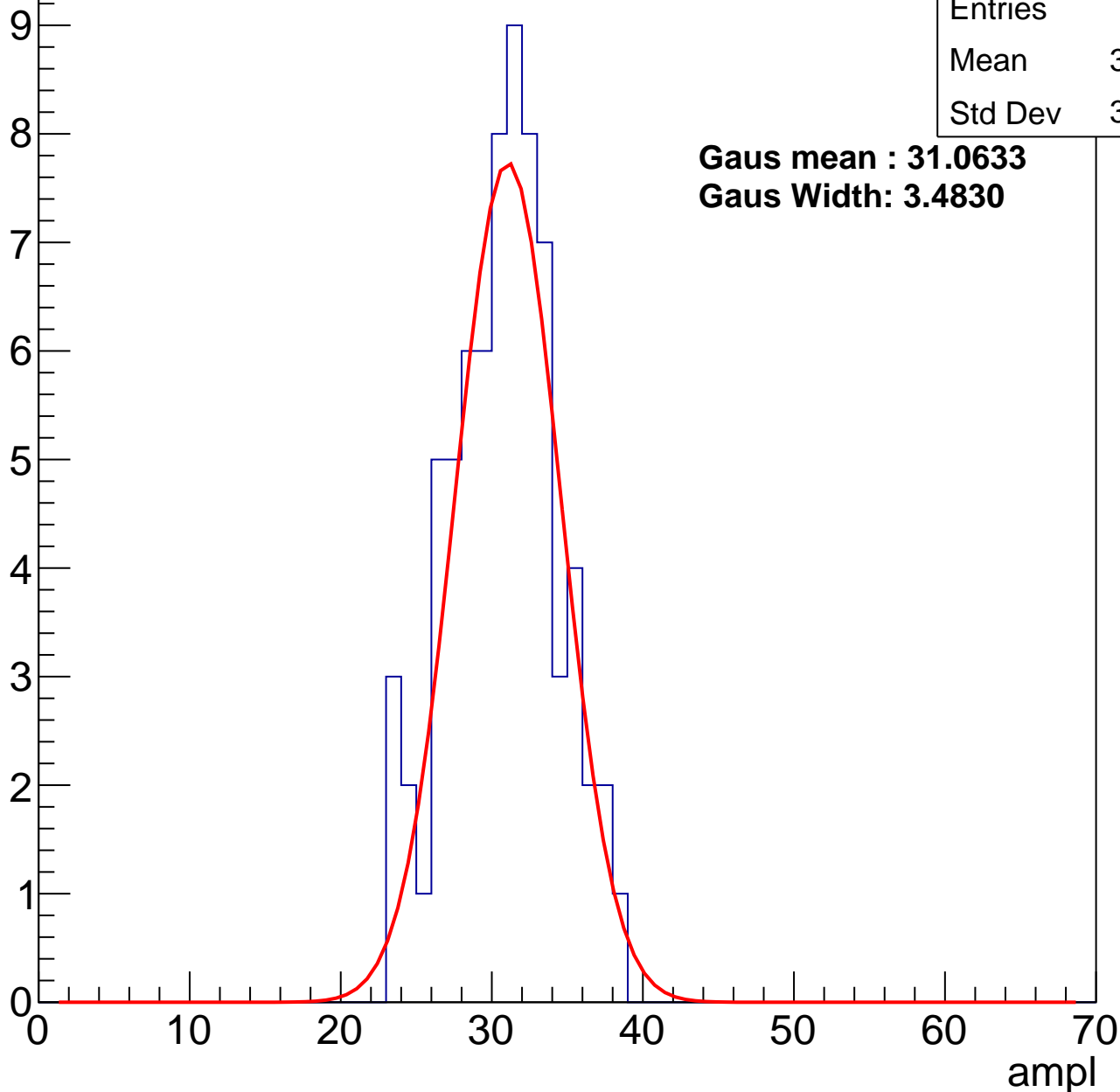
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.29
Std Dev	3.494

Gaus mean : 31.0633

Gaus Width: 3.4830



B0L000S, U4-ch65, adc1

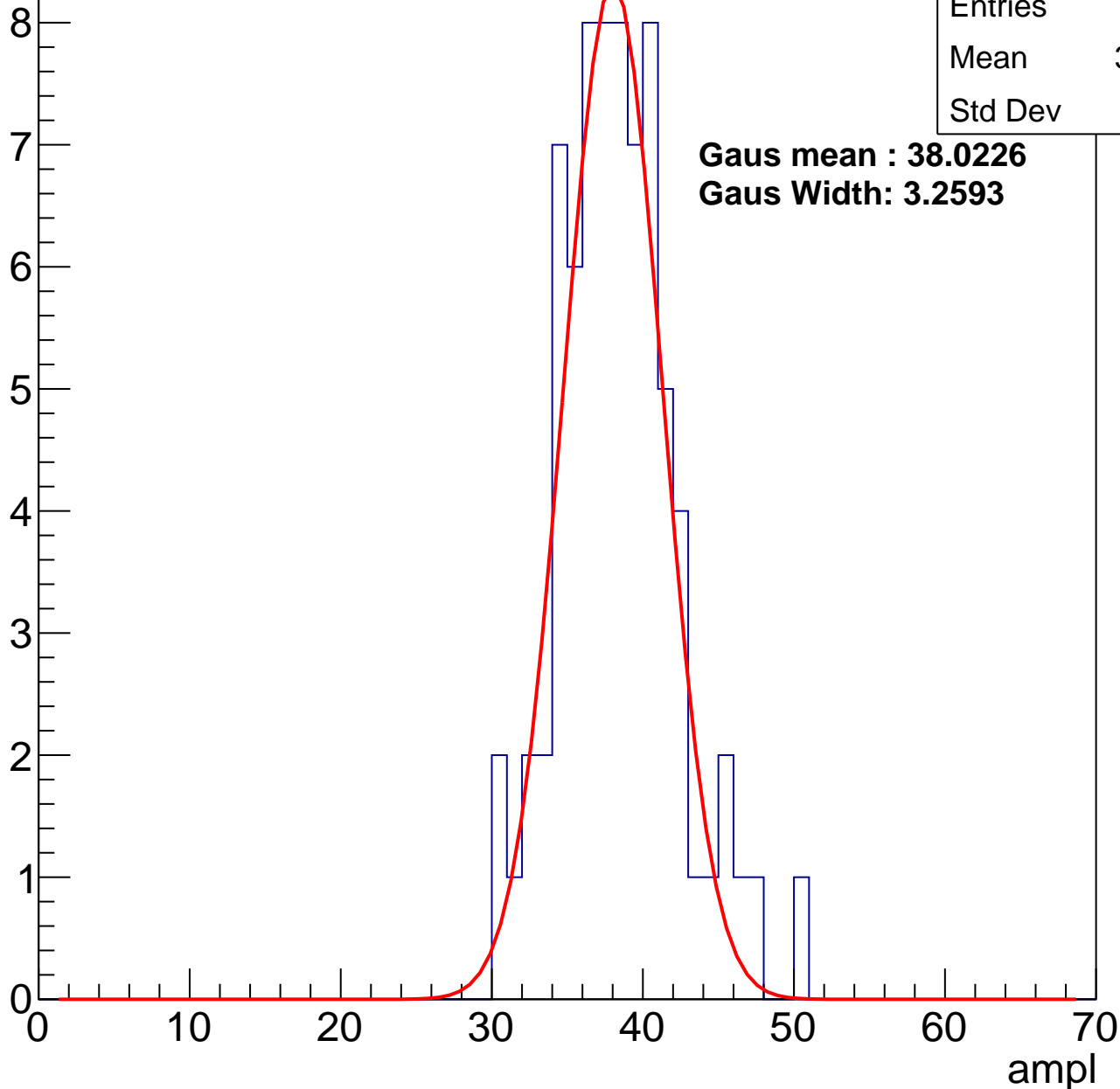
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.91
Std Dev	3.83

Gaus mean : 38.0226

Gaus Width: 3.2593



B0L000S, U4-ch65, adc2

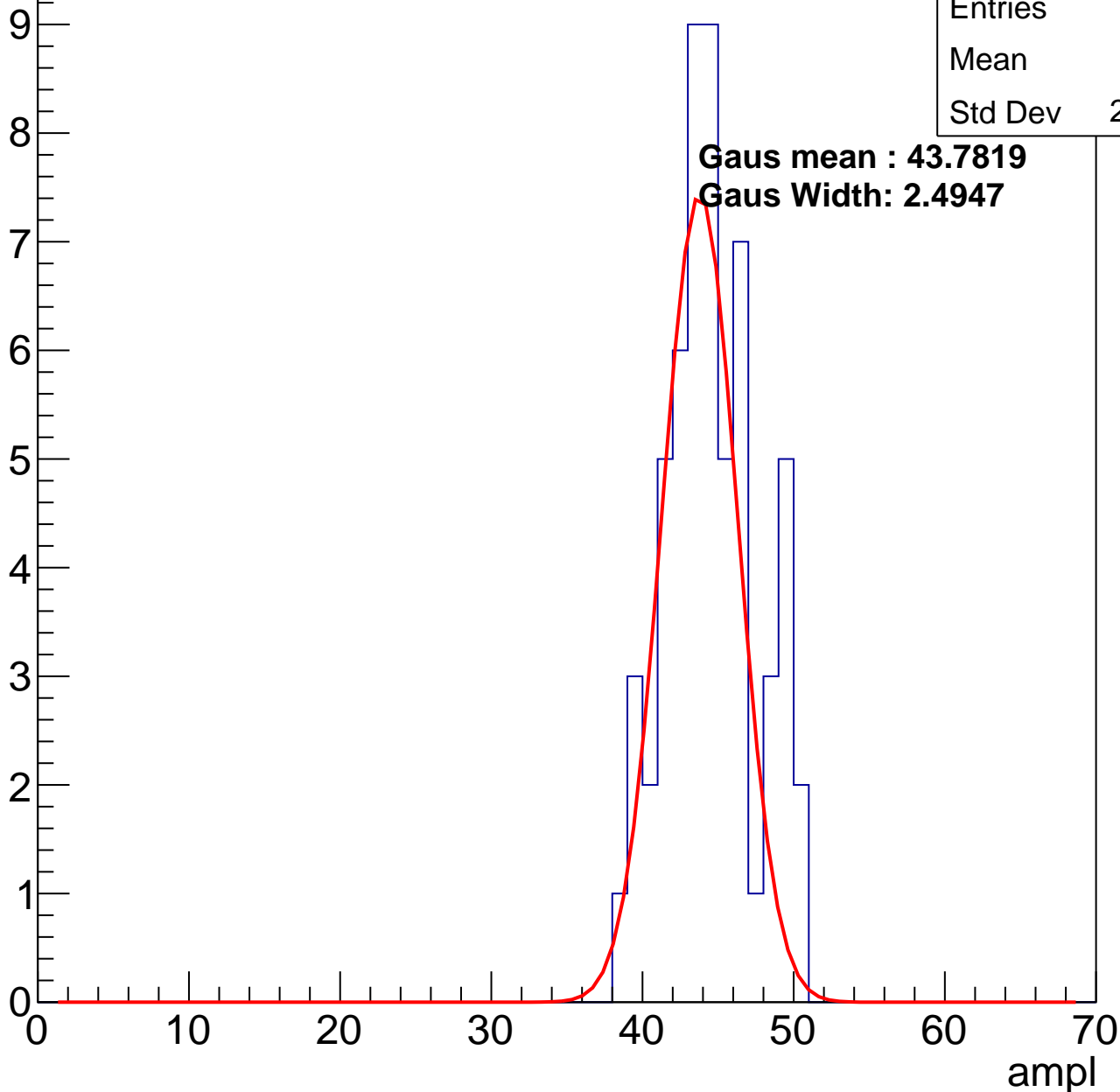
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.1
Std Dev	2.958

Gaus mean : 43.7819

Gaus Width: 2.4947

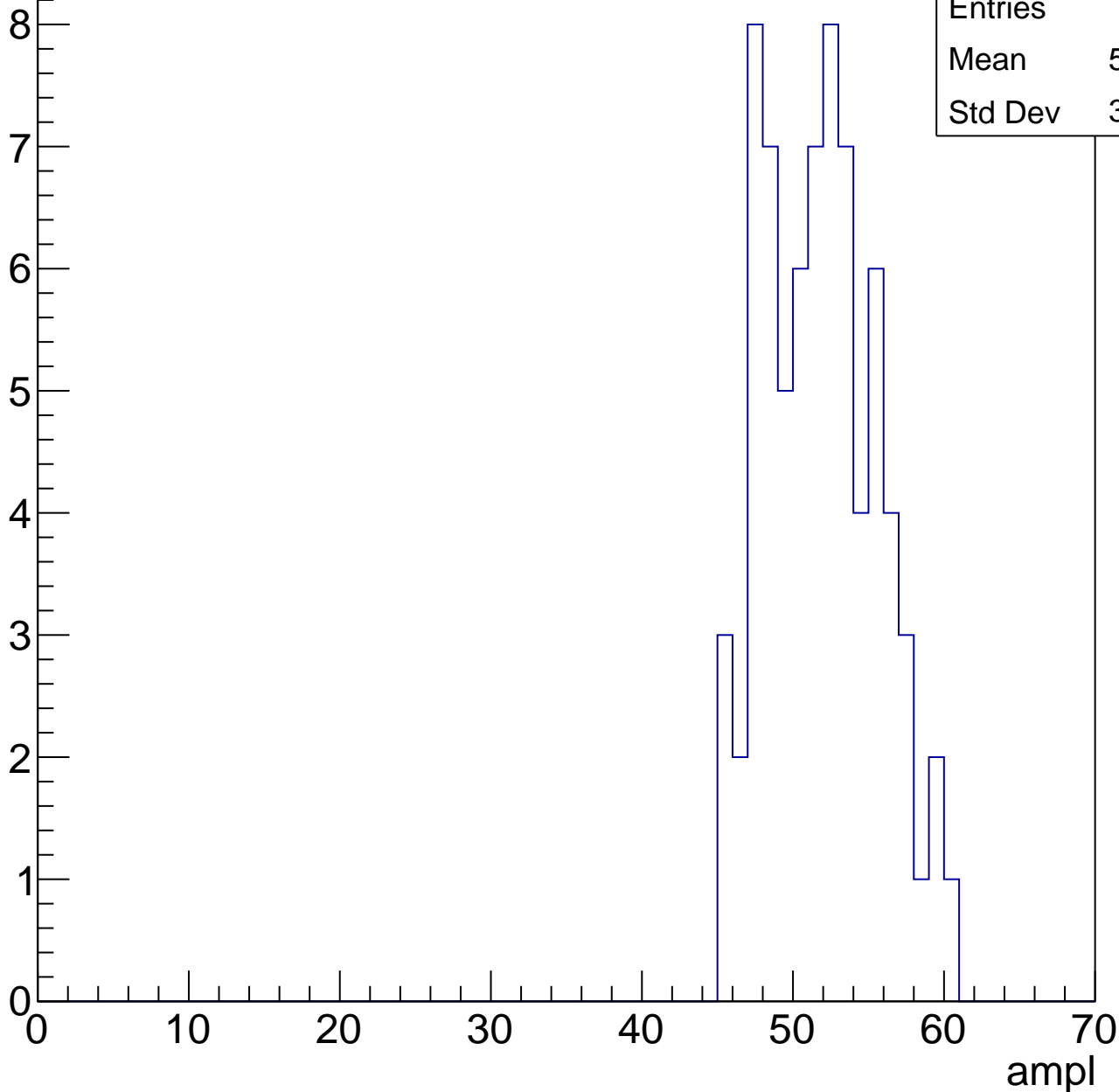


B0L000S, U4-ch65, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	51.42
Std Dev	3.669

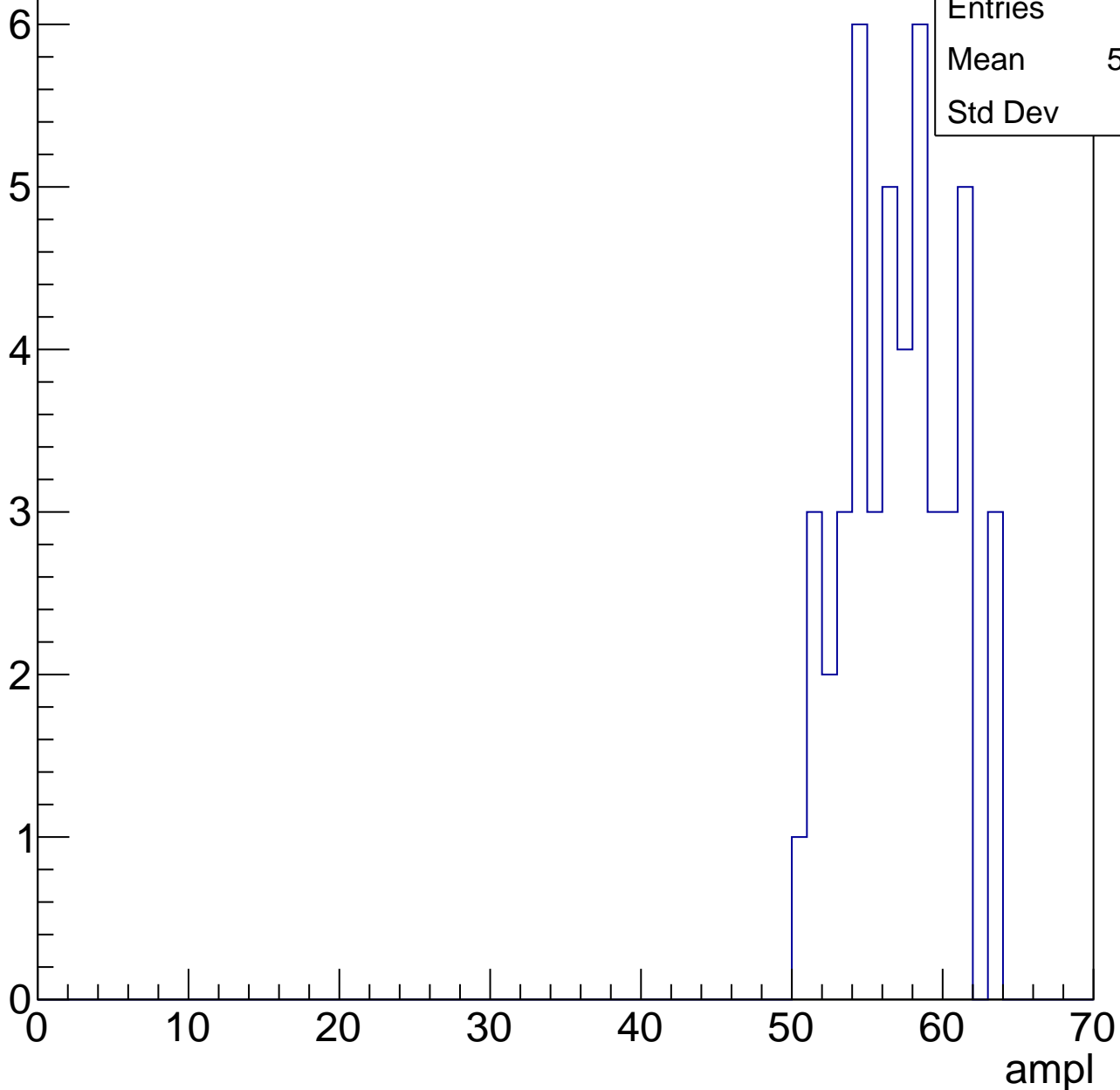


B0L000S, U4-ch65, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	56.64
Std Dev	3.43

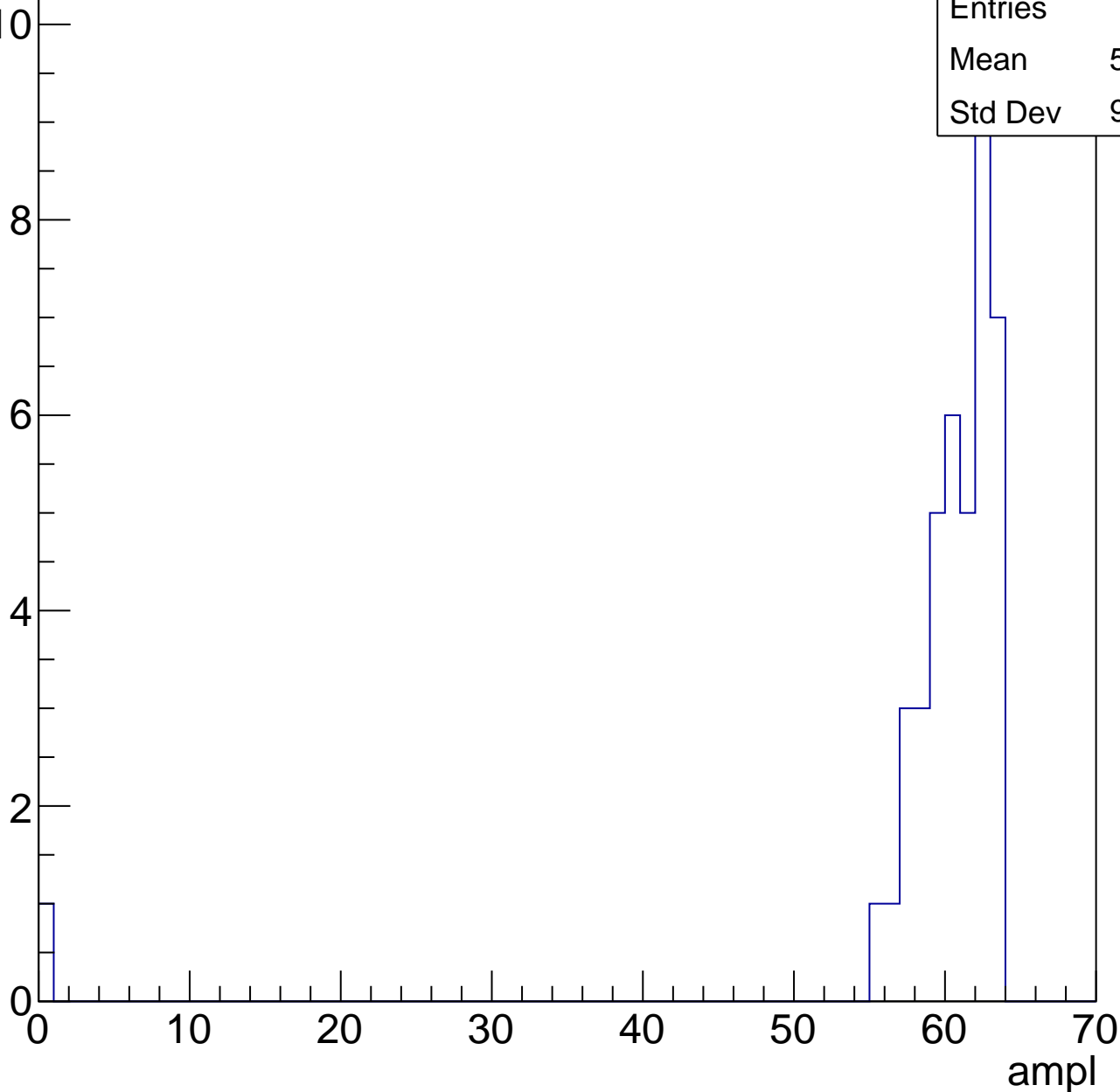


B0L000S, U4-ch65, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.98
Std Dev	9.448



B0L000S, U4-ch65, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch65, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch66, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	59
Mean	27.59
Std Dev	2.675

Gaus mean : 27.9907

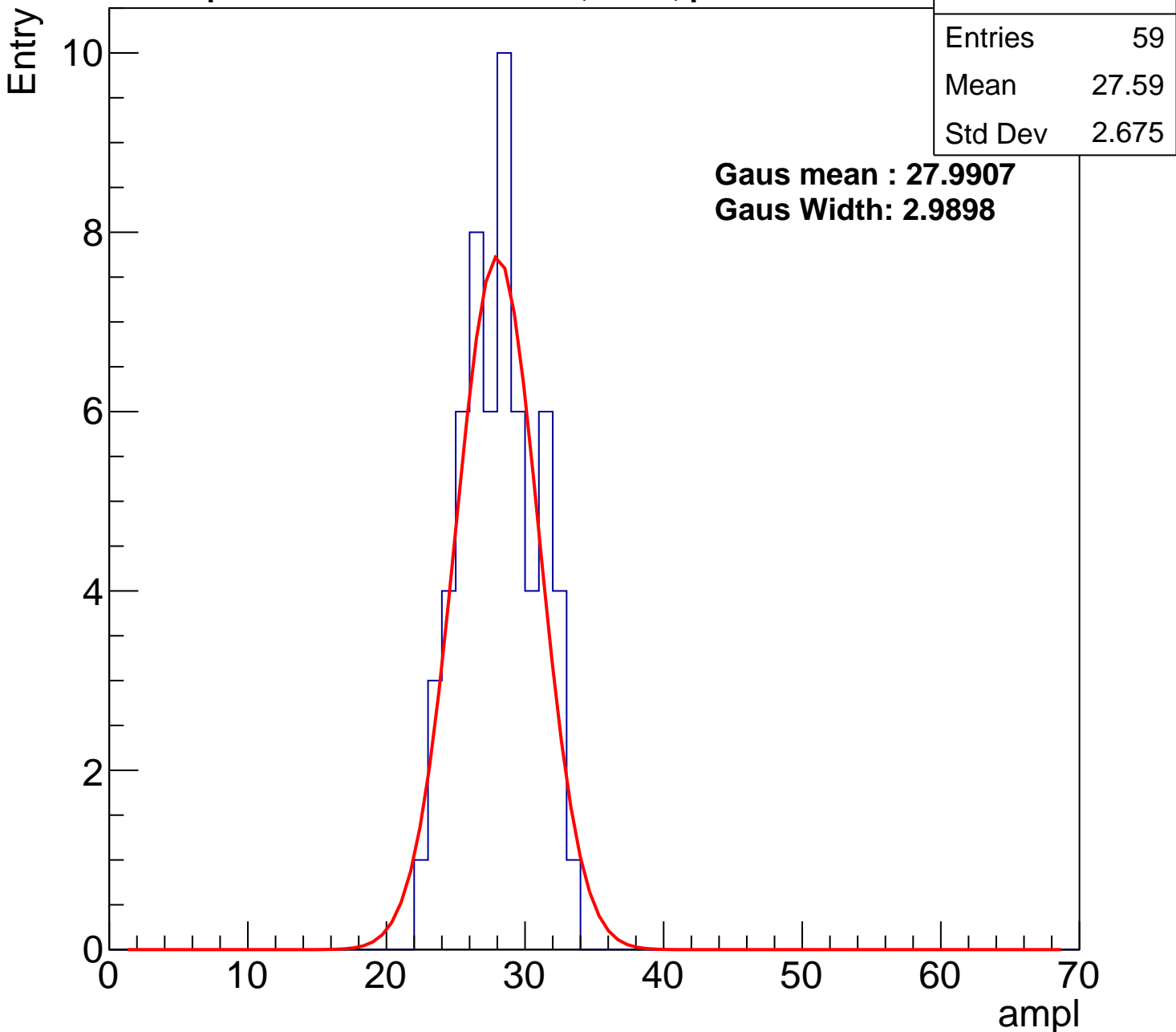
Gaus Width: 2.9898

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch66, adc1

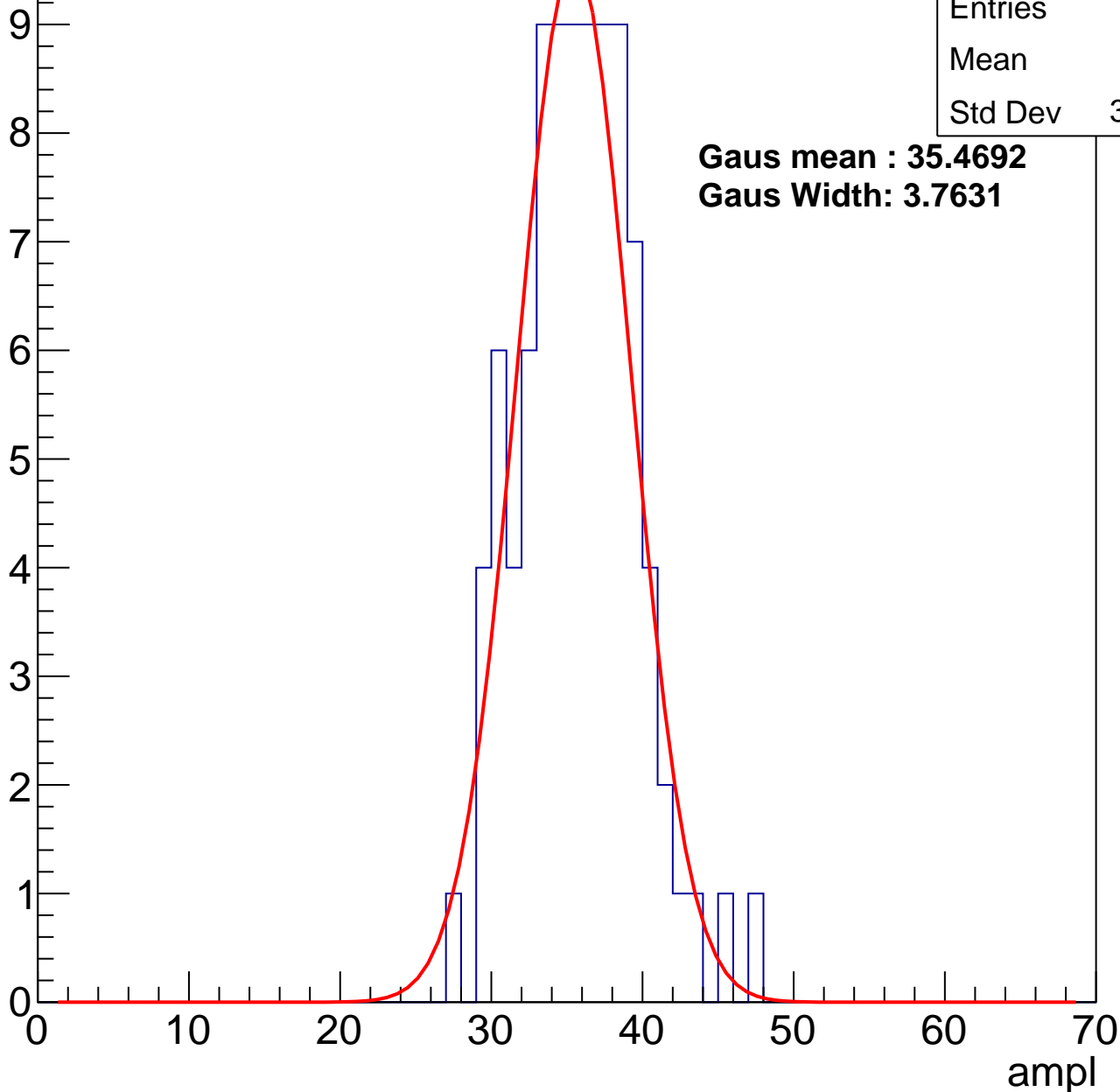
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	92
Mean	35.3
Std Dev	3.729

Gaus mean : 35.4692

Gaus Width: 3.7631

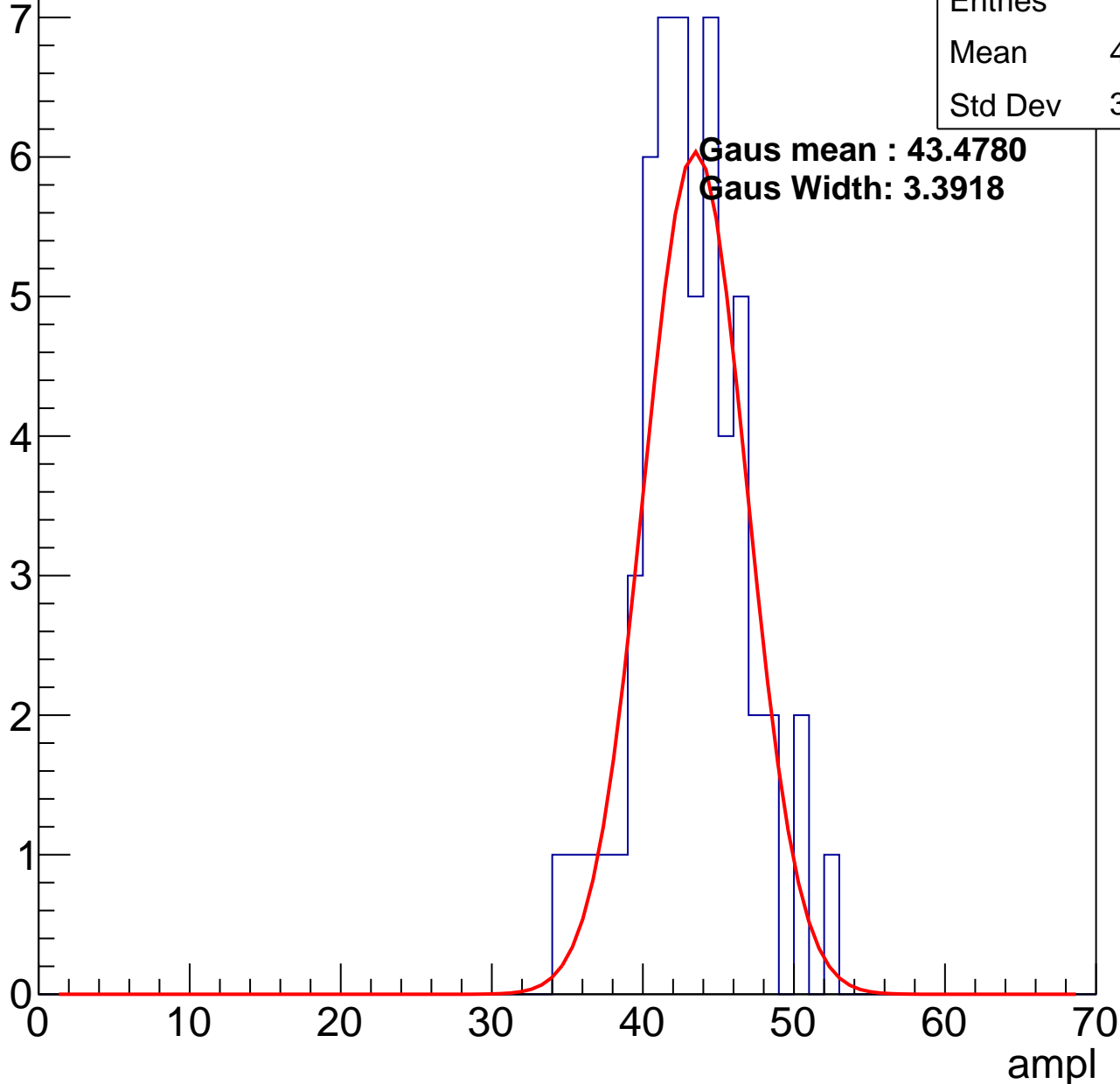


B0L000S, U4-ch66, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.73
Std Dev	3.578

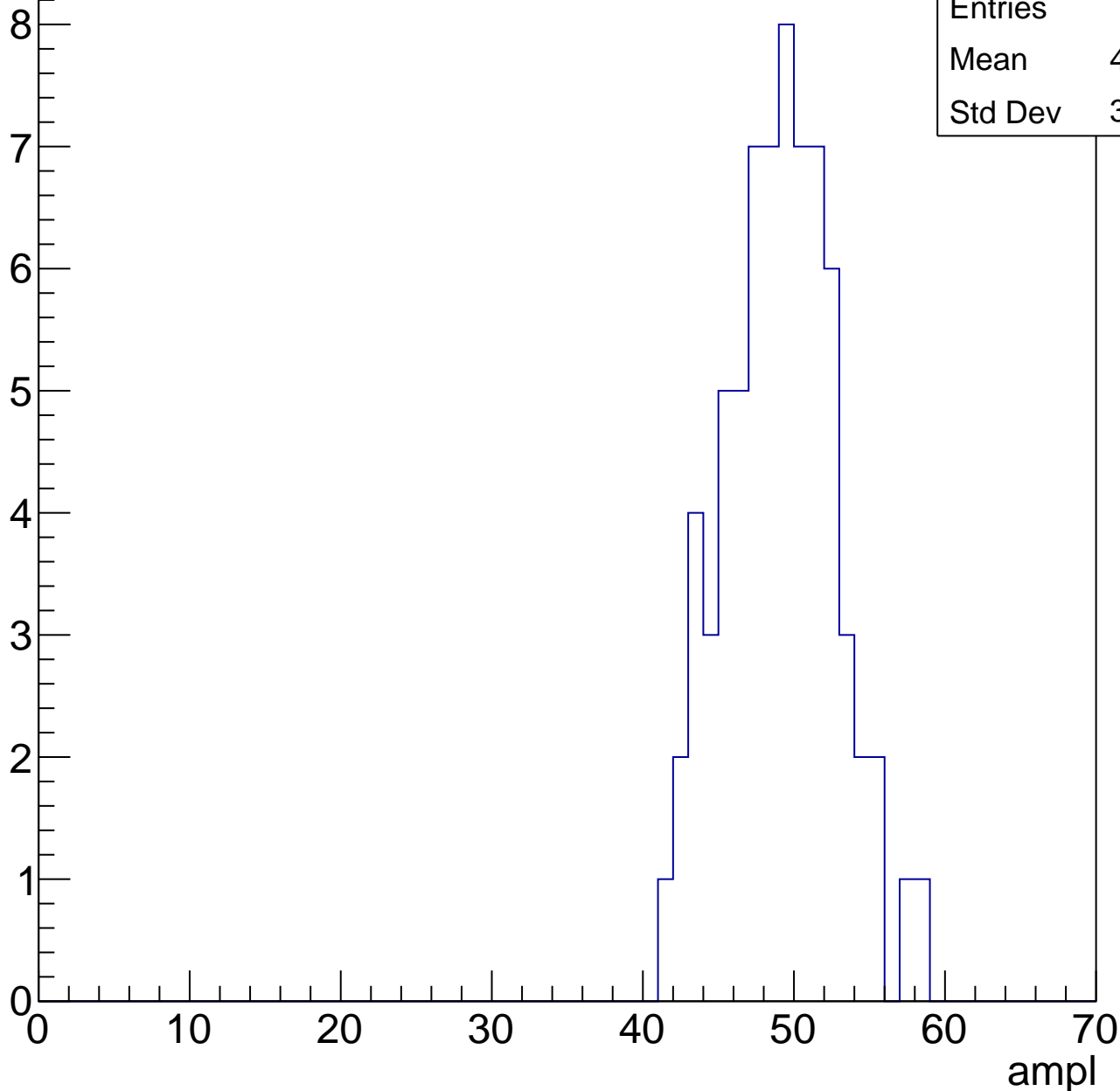


B0L000S, U4-ch66, adc3

calib_packv5_042523_0143.root, FC#5, port B1

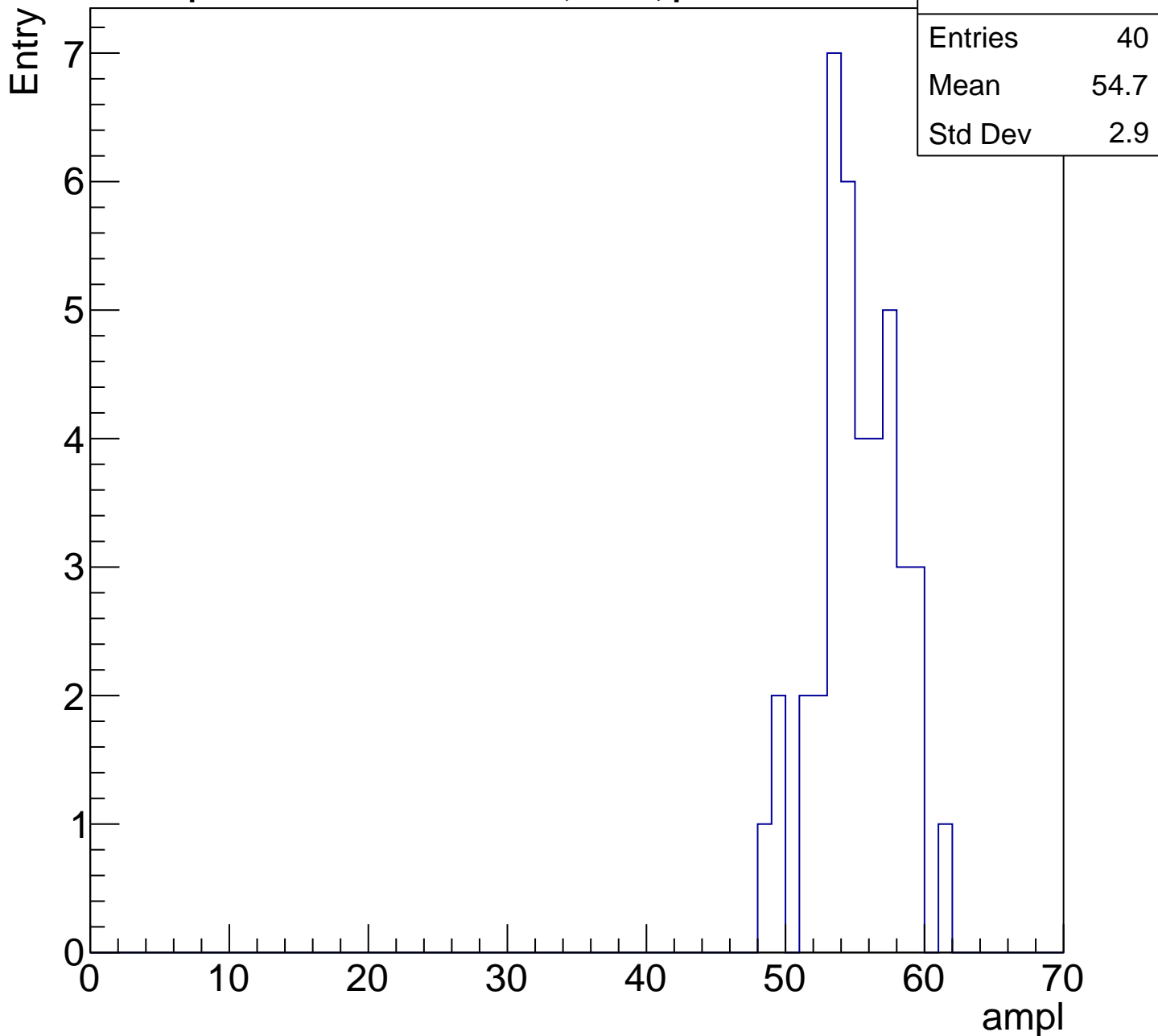
Entry

Entries	71
Mean	48.62
Std Dev	3.628



B0L000S, U4-ch66, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch66, adc5

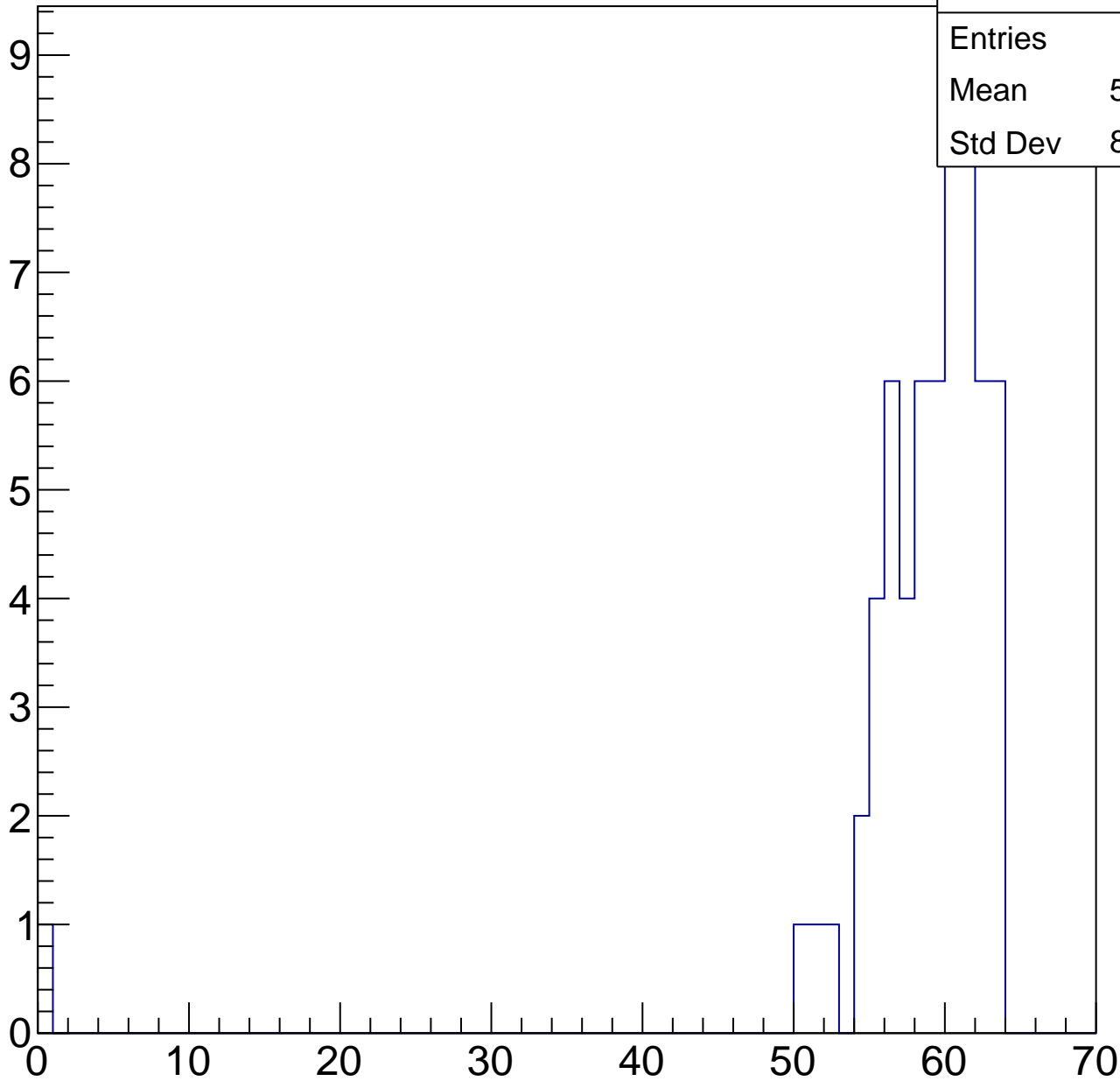
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	57.84
Std Dev	8.008

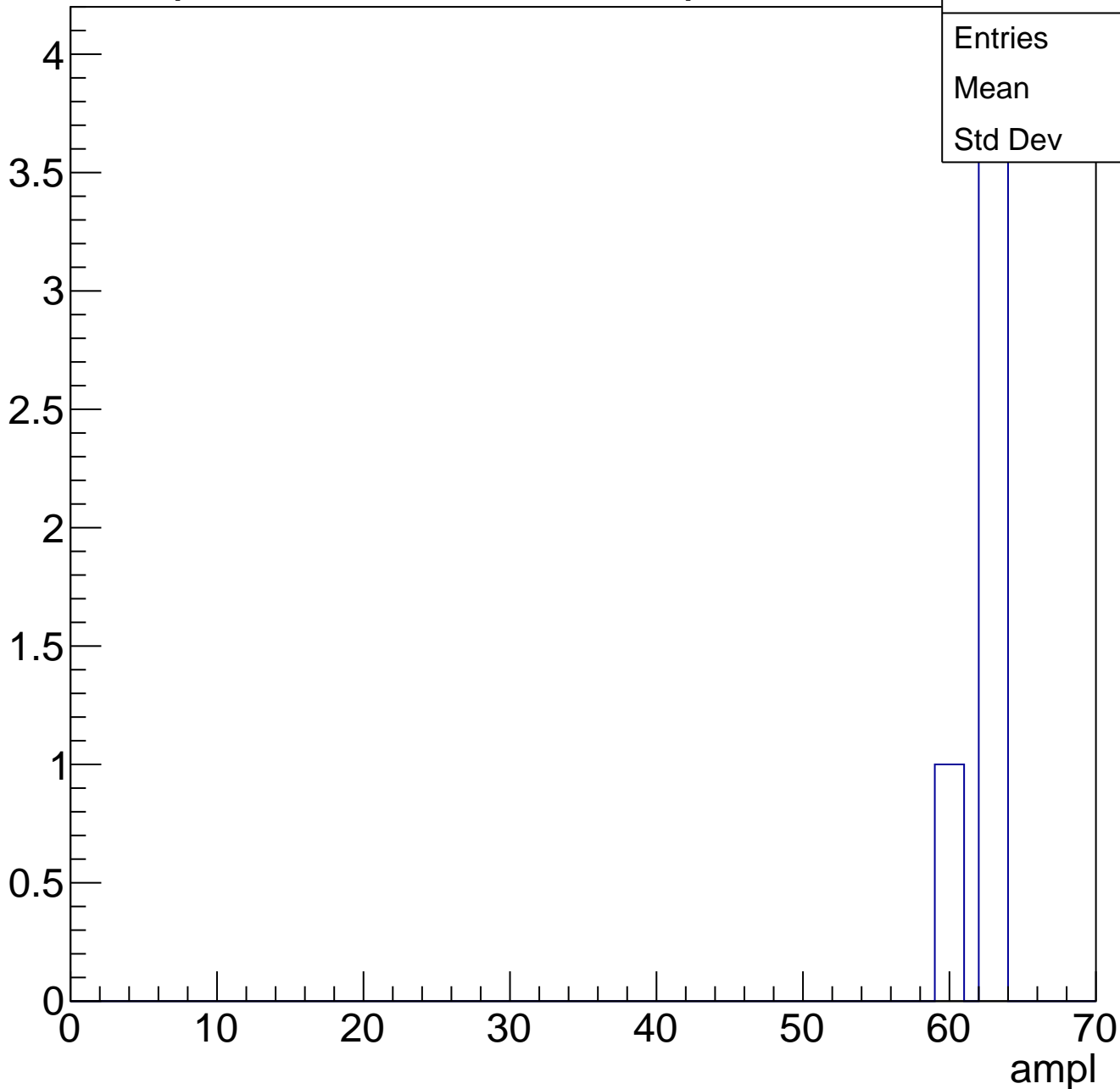
ampl



B0L000S, U4-ch66, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch66, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch67, adc0

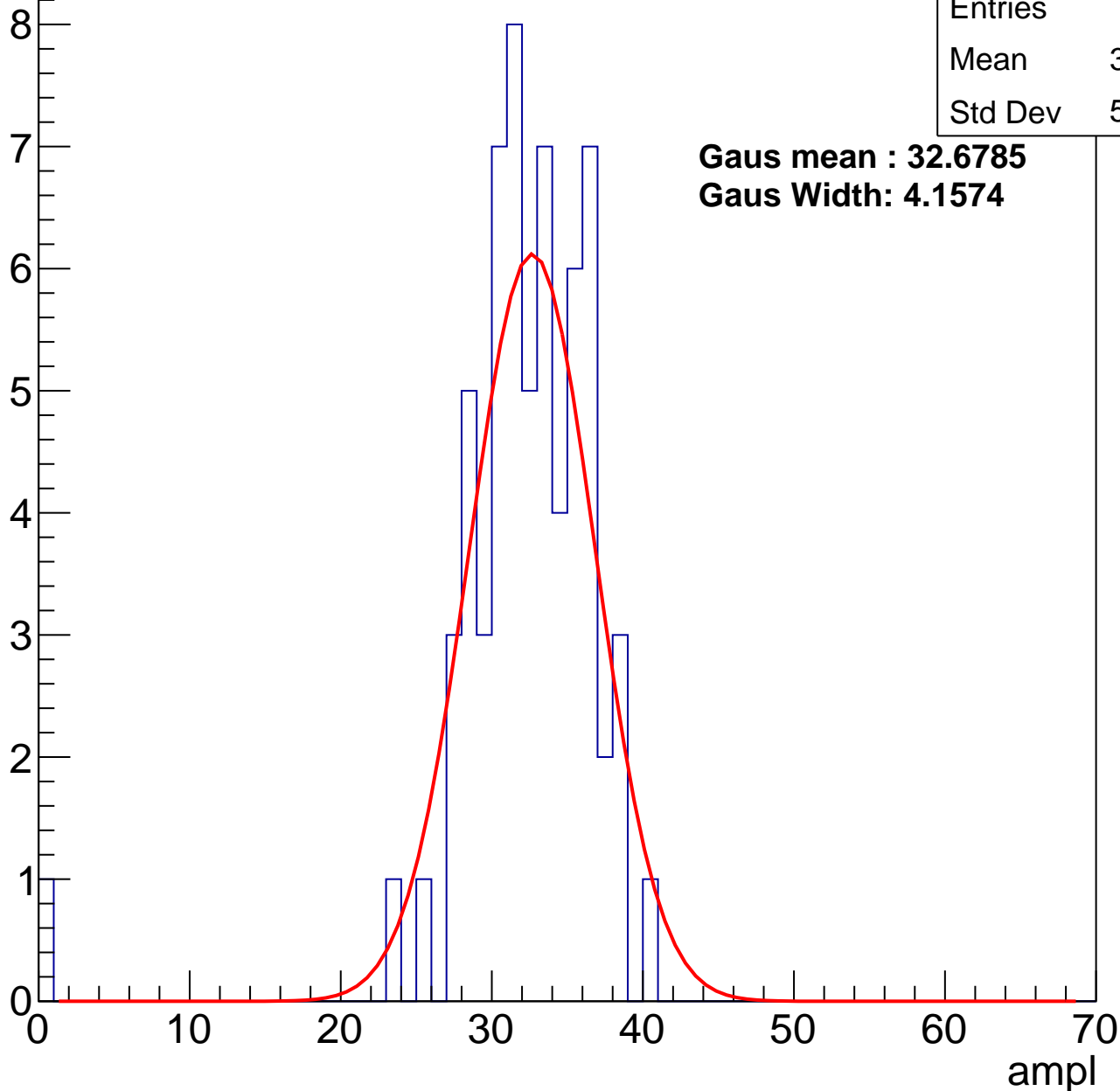
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	31.73
Std Dev	5.272

Gaus mean : 32.6785

Gaus Width: 4.1574



B0L000S, U4-ch67, adc1

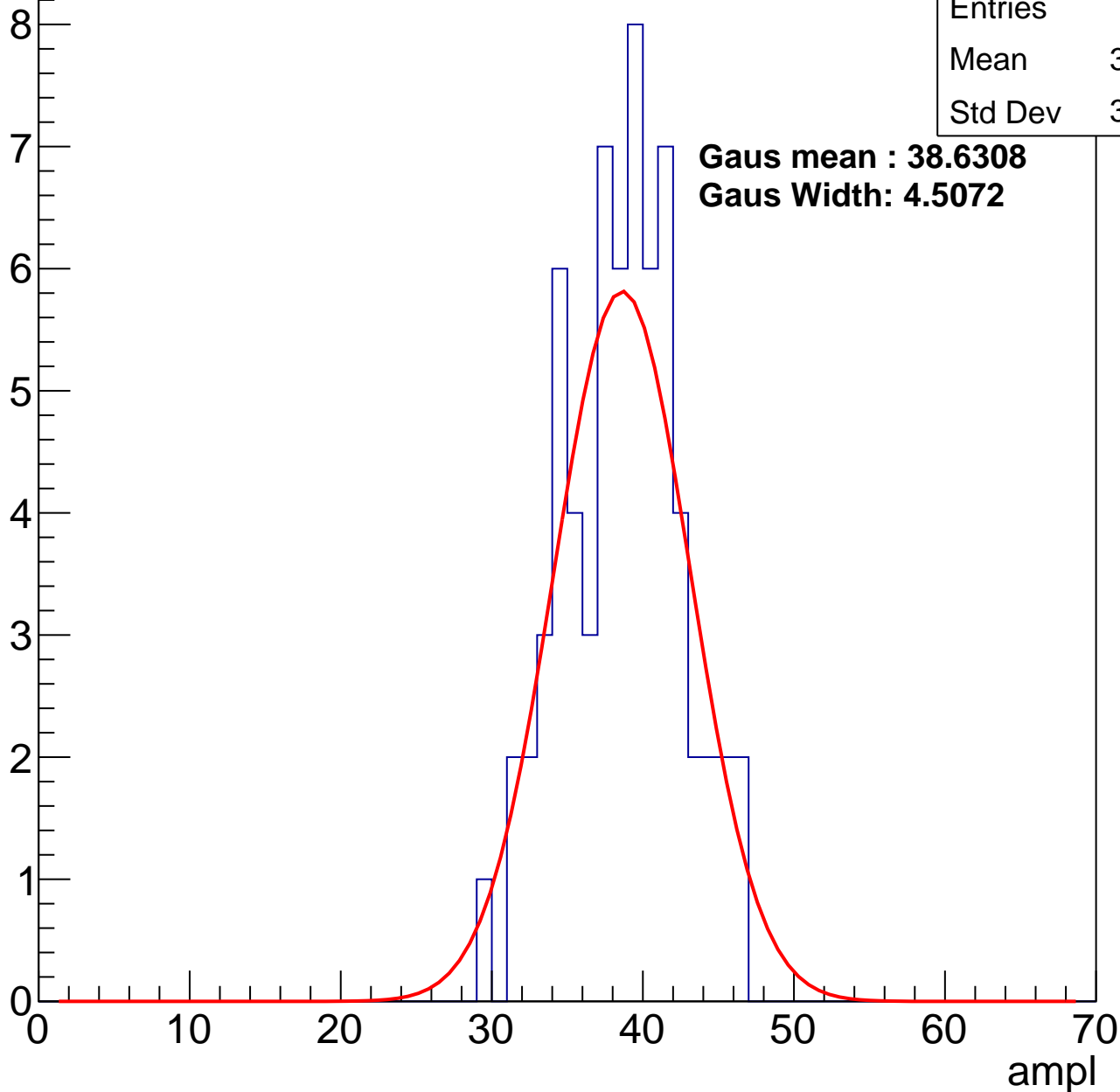
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	38.15
Std Dev	3.829

Gaus mean : 38.6308

Gaus Width: 4.5072



B0L000S, U4-ch67, adc2

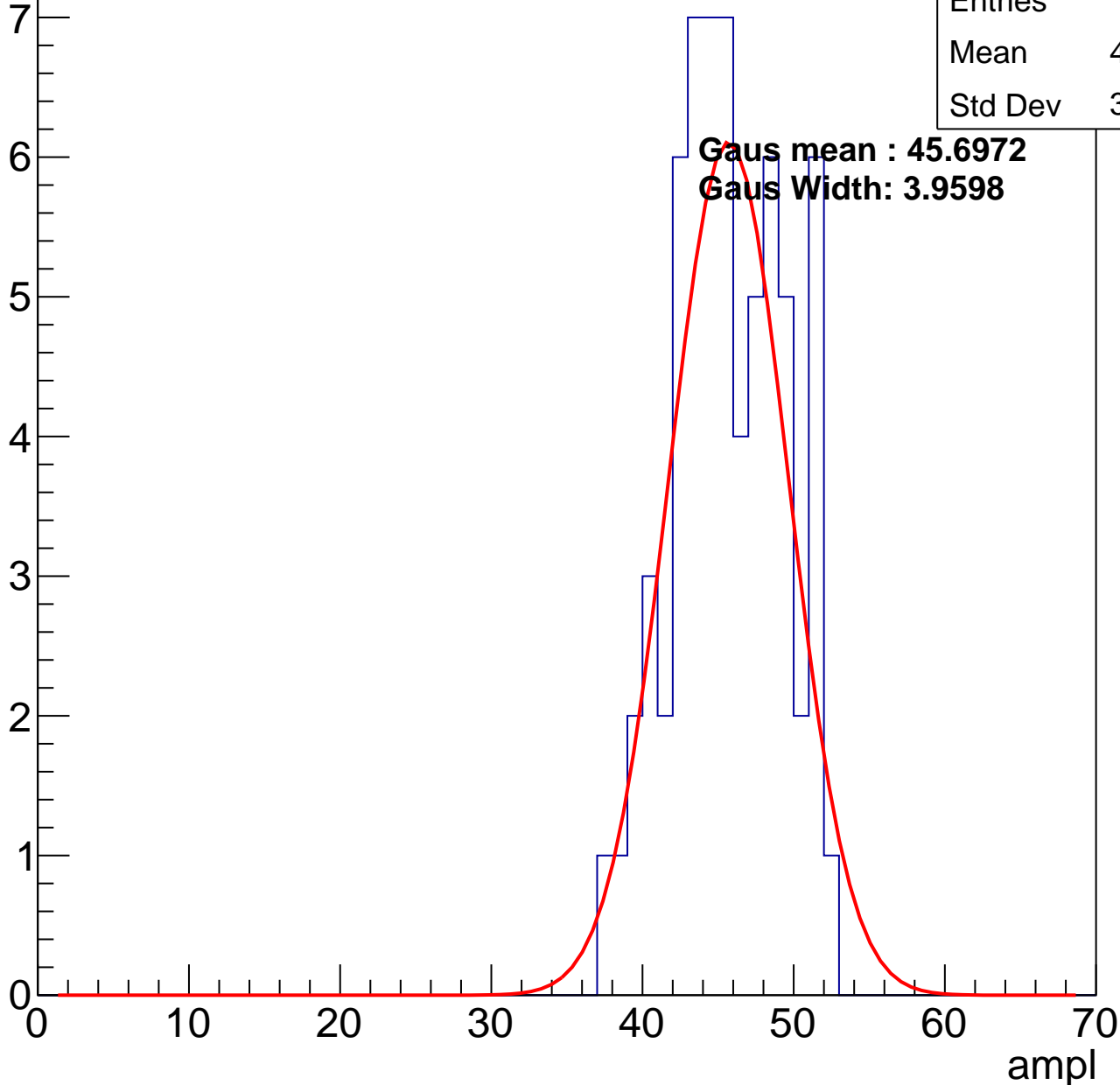
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	45.25
Std Dev	3.616

Gaus mean : 45.6972

Gaus Width: 3.9598

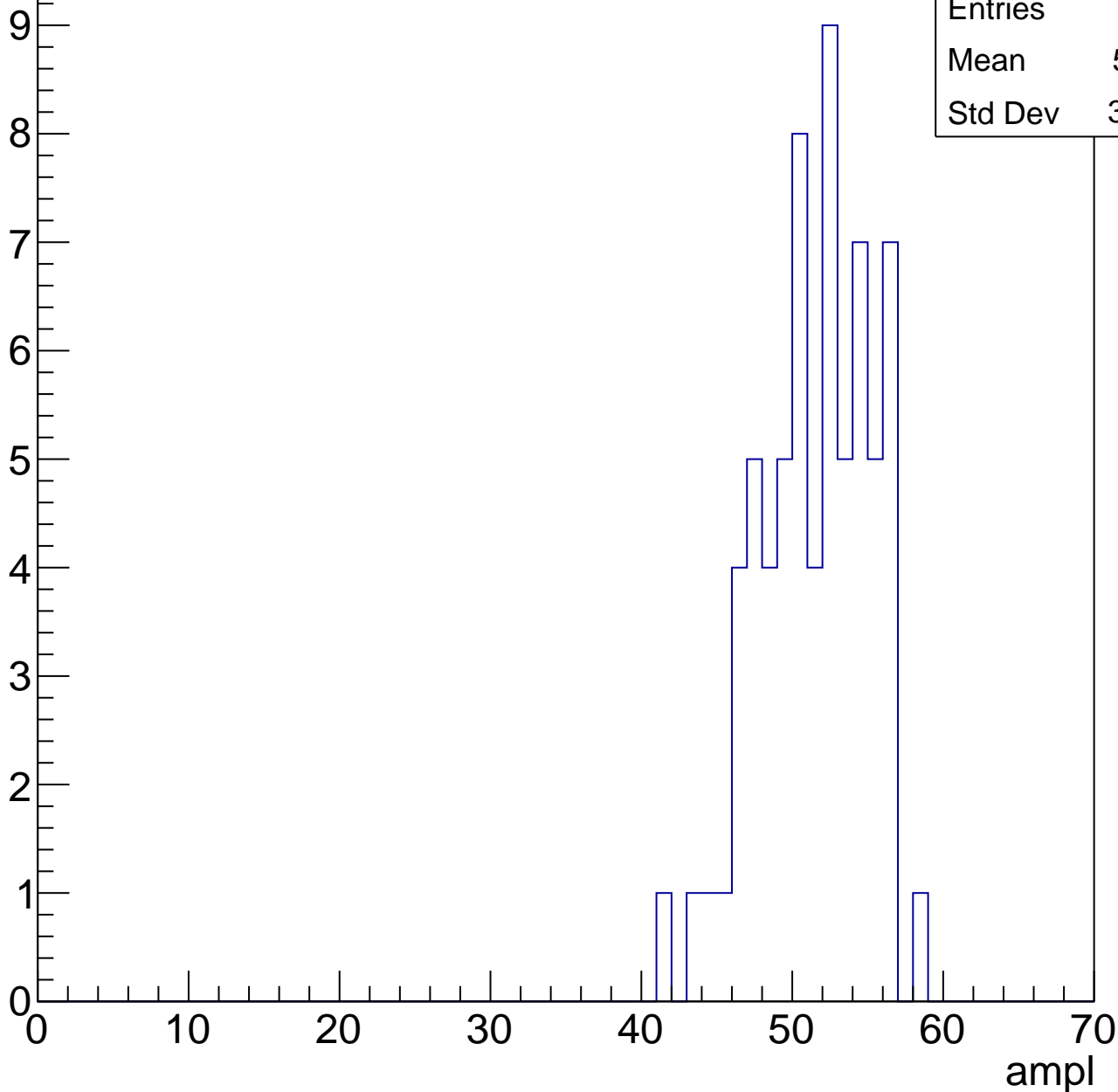


B0L000S, U4-ch67, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	51.01
Std Dev	3.616



B0L000S, U4-ch67, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

Entries	58
Mean	58
Std Dev	2.798

58

58

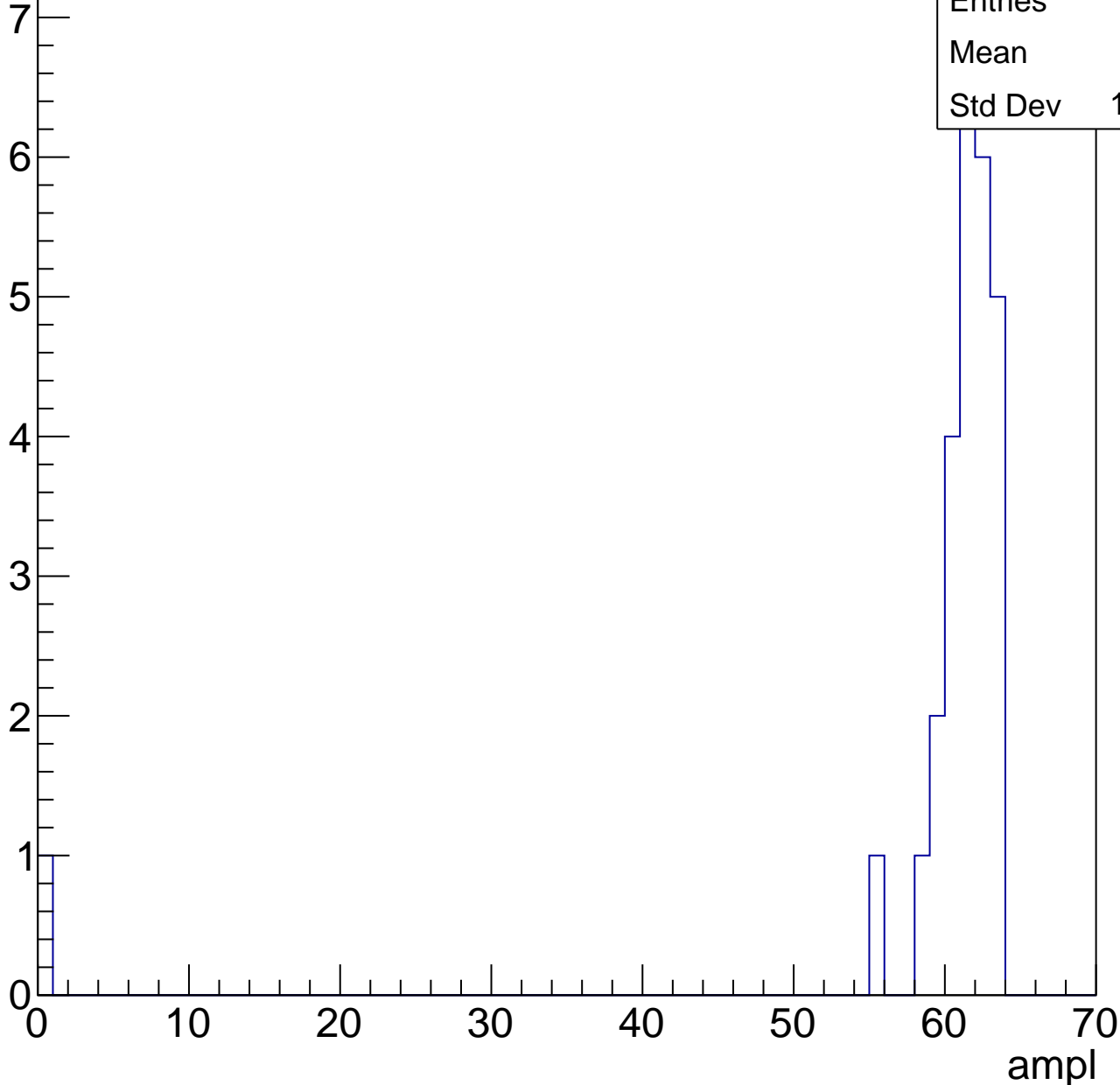
2.798

B0L000S, U4-ch67, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	27
Mean	58.7
Std Dev	11.65



B0L000S, U4-ch67, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch67, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch68, adc0

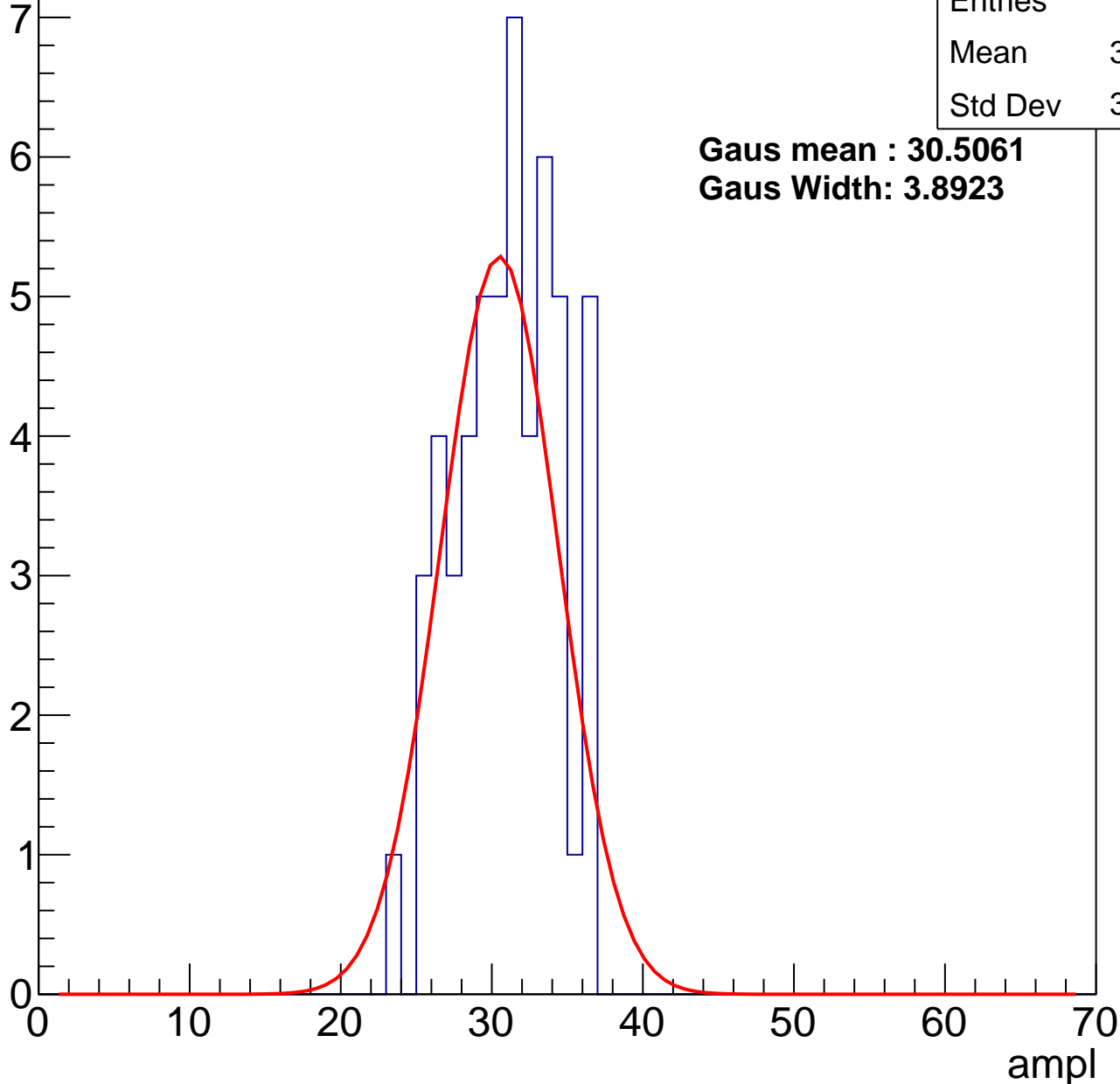
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	30.53
Std Dev	3.317

Gaus mean : 30.5061

Gaus Width: 3.8923



B0L000S, U4-ch68, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	93
Mean	37.87
Std Dev	3.794

Gaus mean : 38.5570

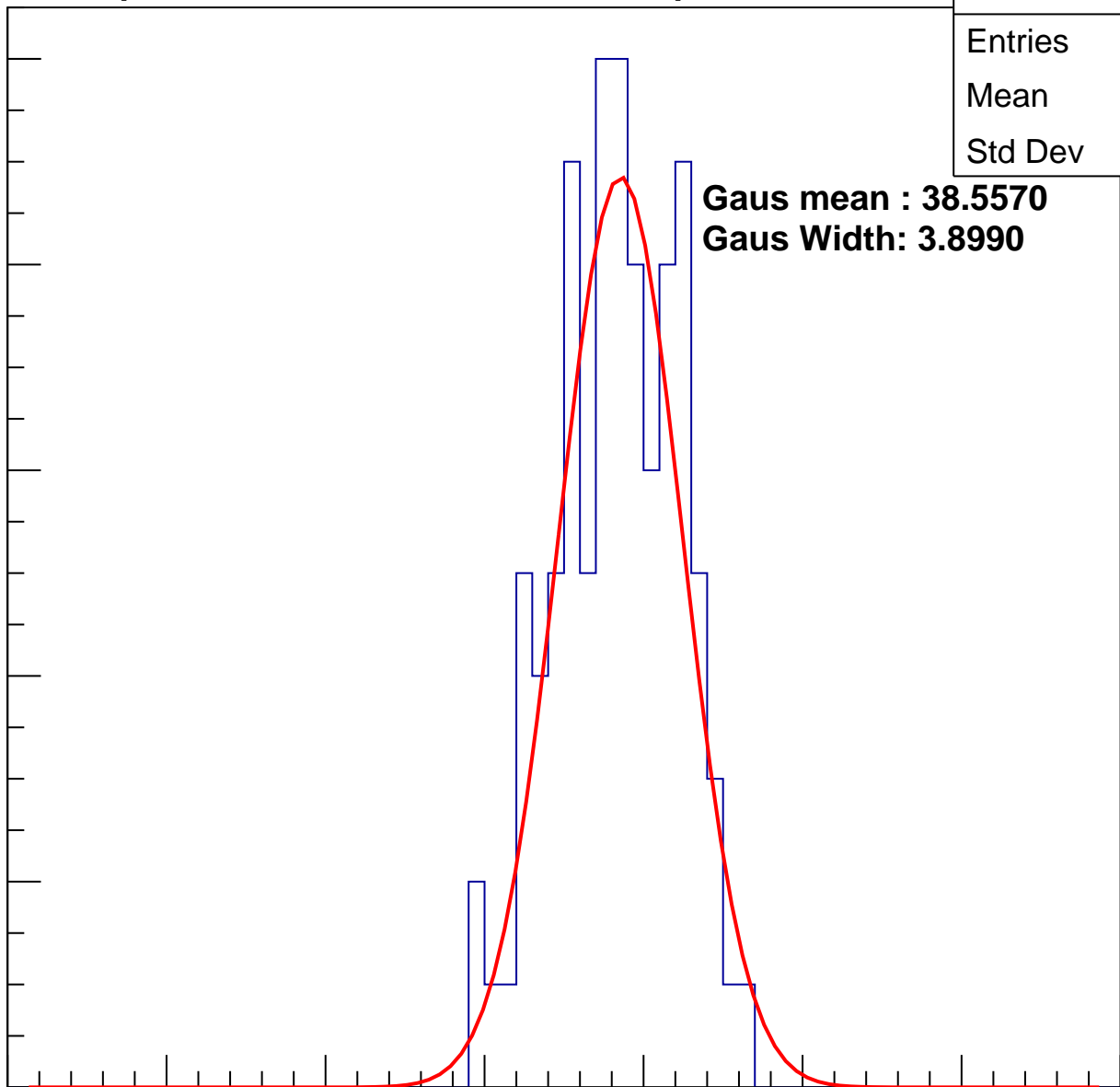
Gaus Width: 3.8990

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch68, adc2

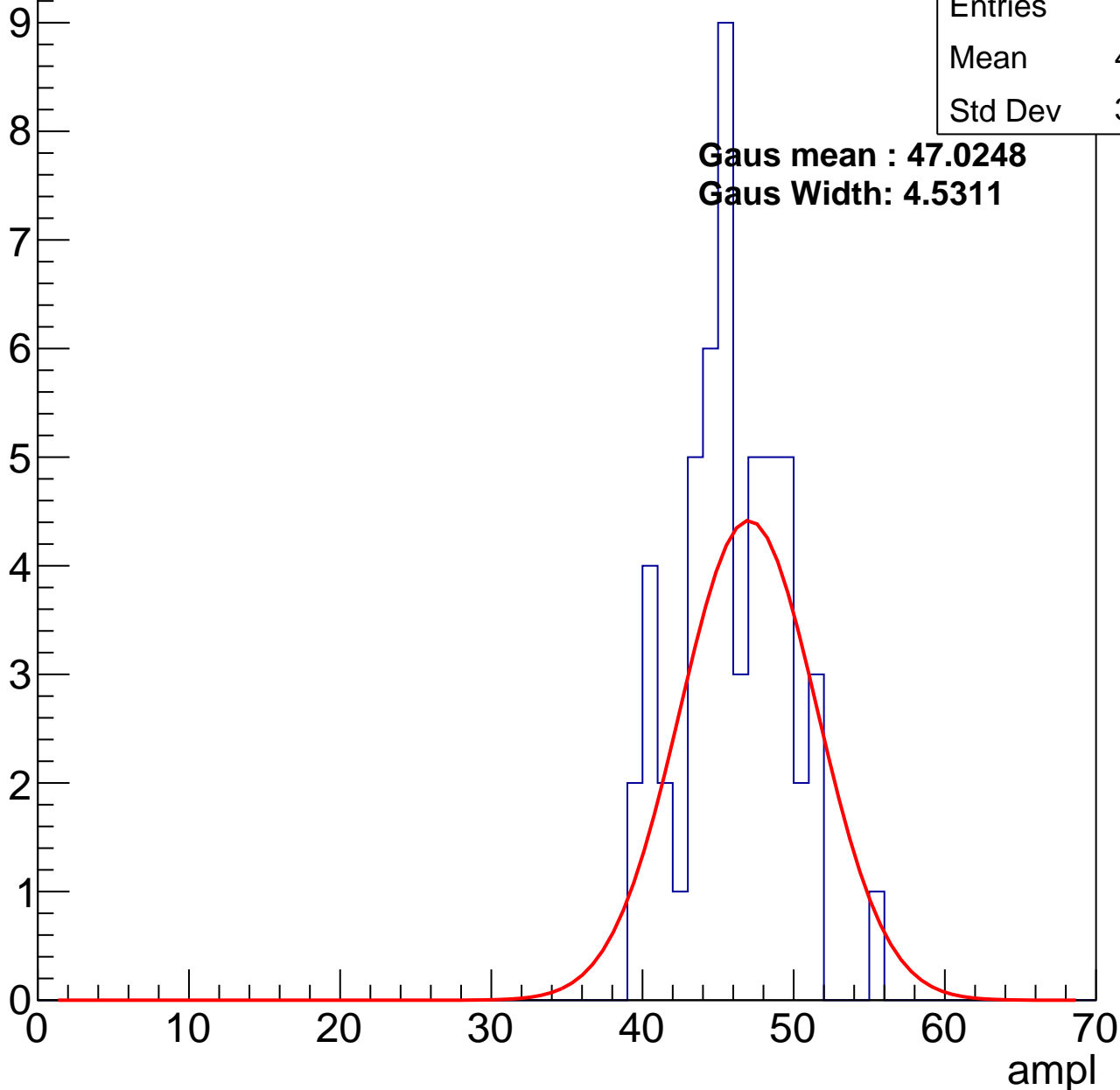
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	45.51
Std Dev	3.451

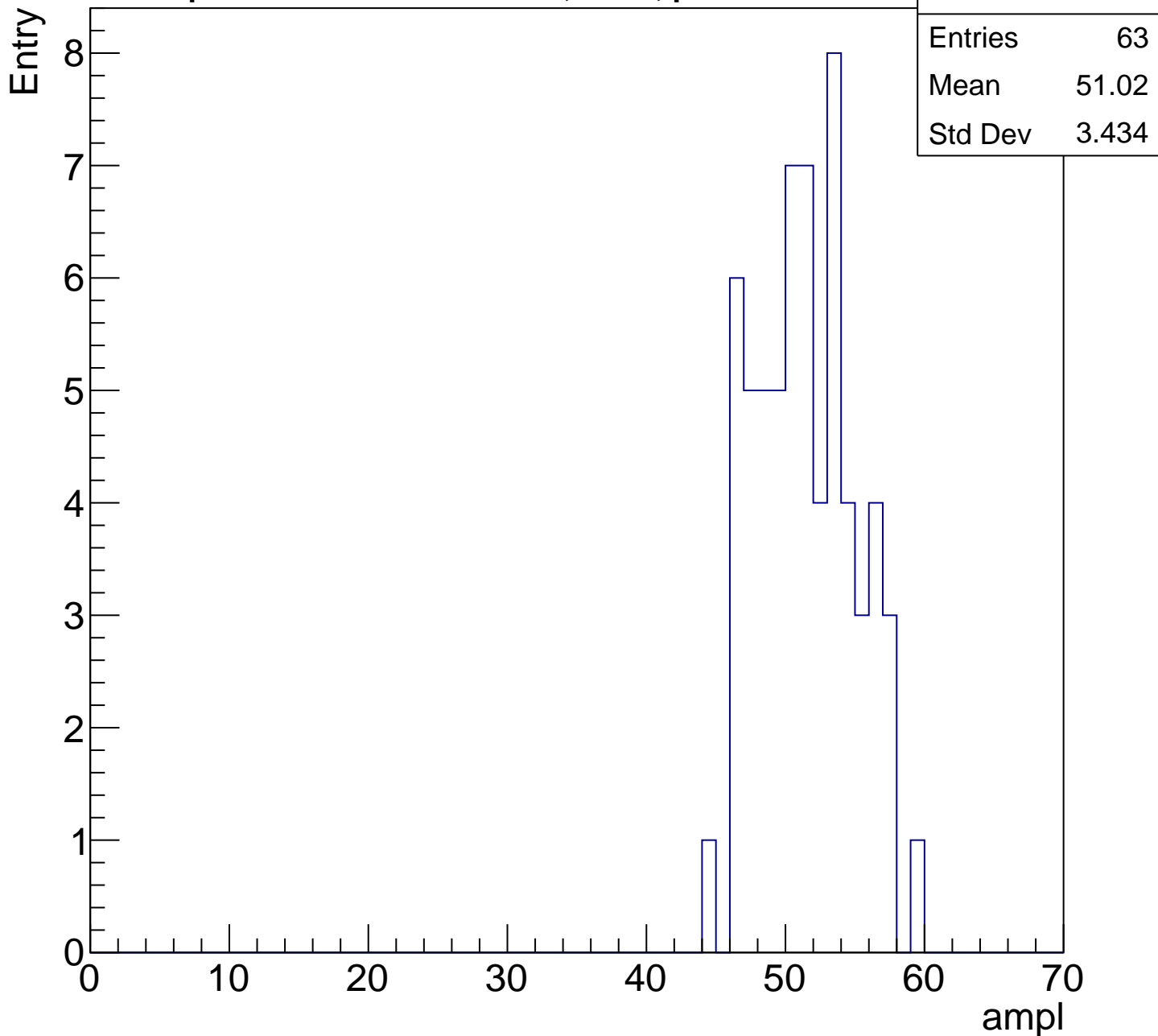
Gaus mean : 47.0248

Gaus Width: 4.5311



B0L000S, U4-ch68, adc3

calib_packv5_042523_0143.root, FC#5, port B1

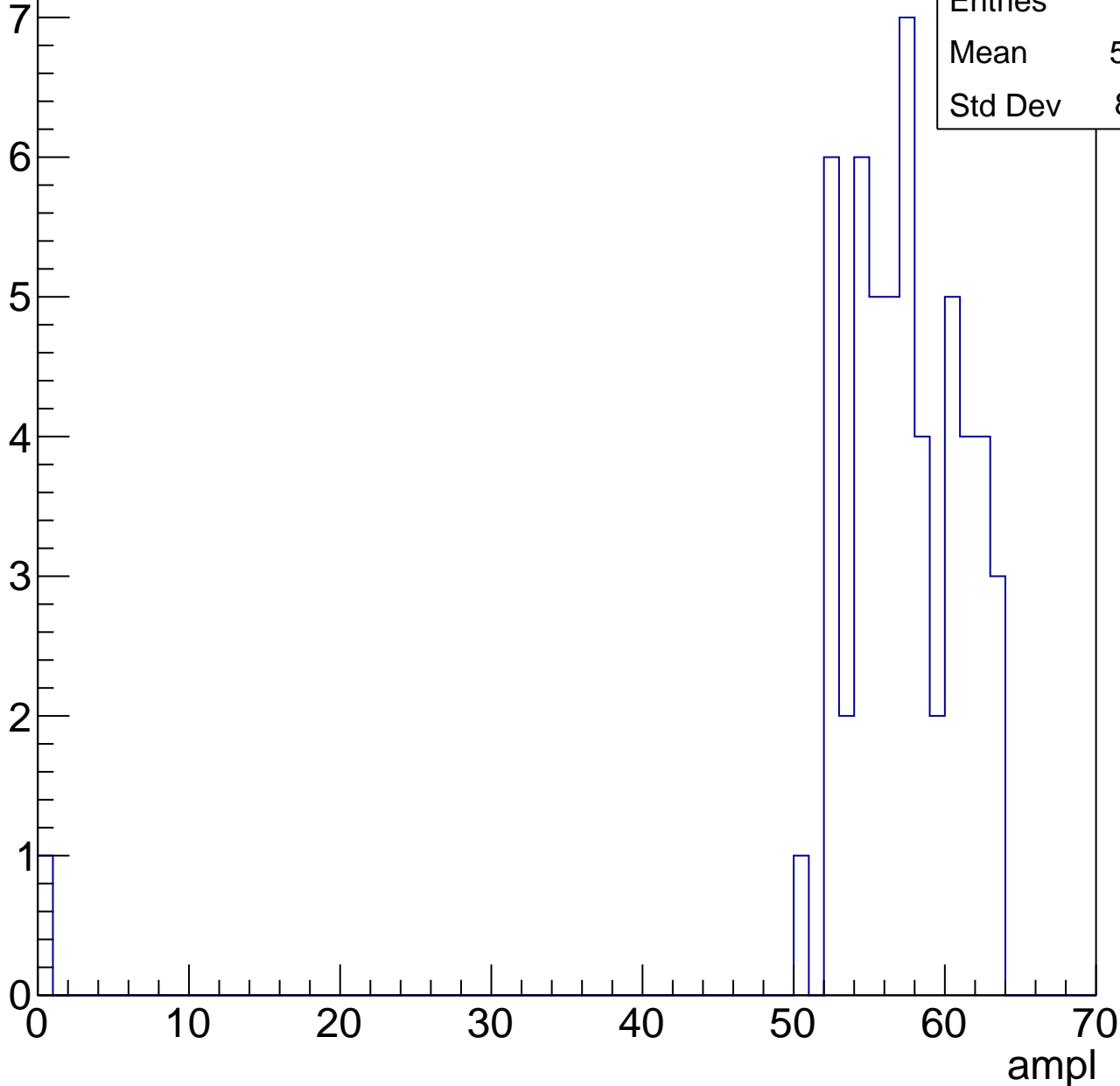


B0L000S, U4-ch68, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

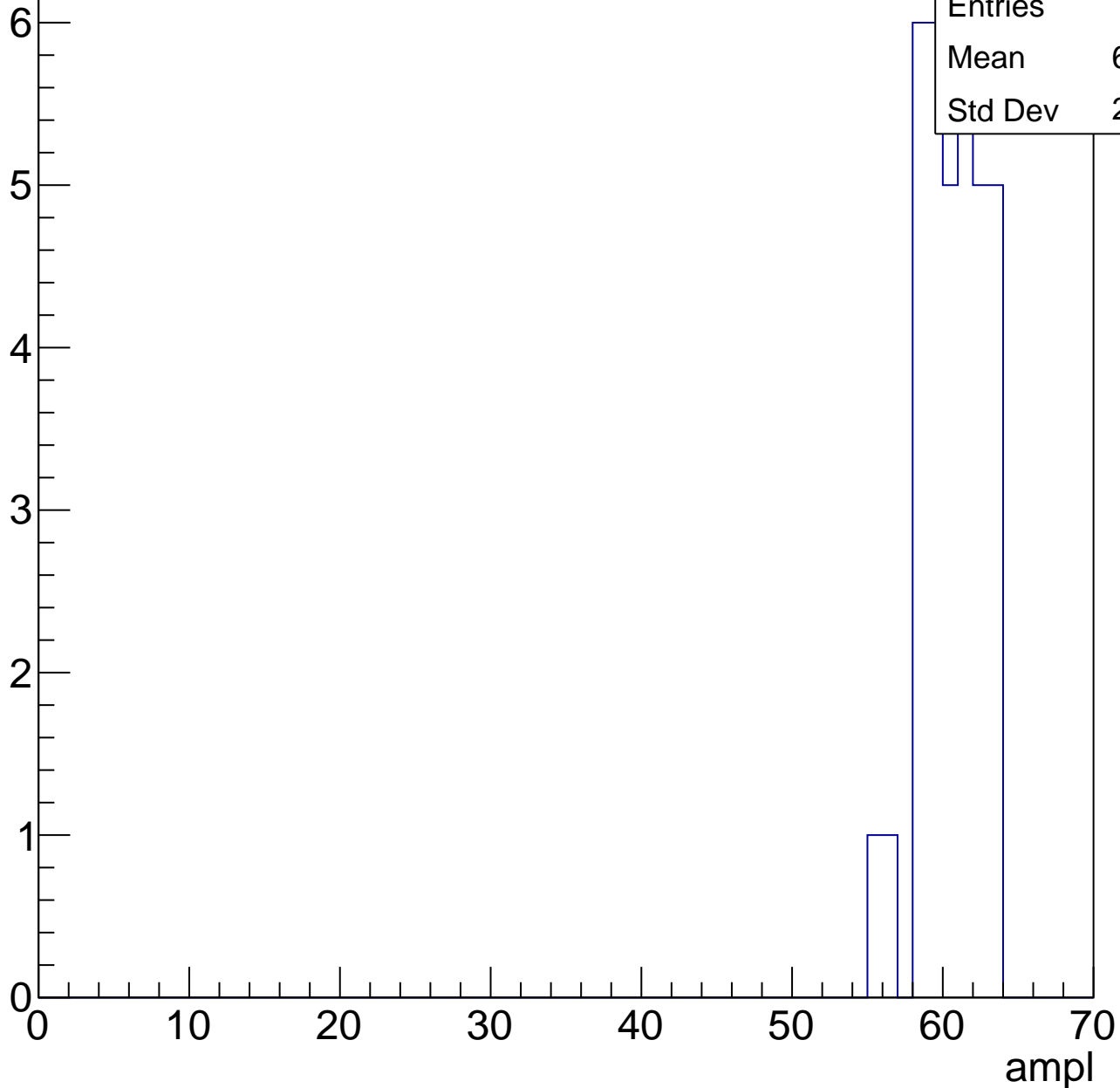
Entries	55
Mean	55.95
Std Dev	8.341



B0L000S, U4-ch68, adc5

calib_packv5_042523_0143.root, FC#5, port B1

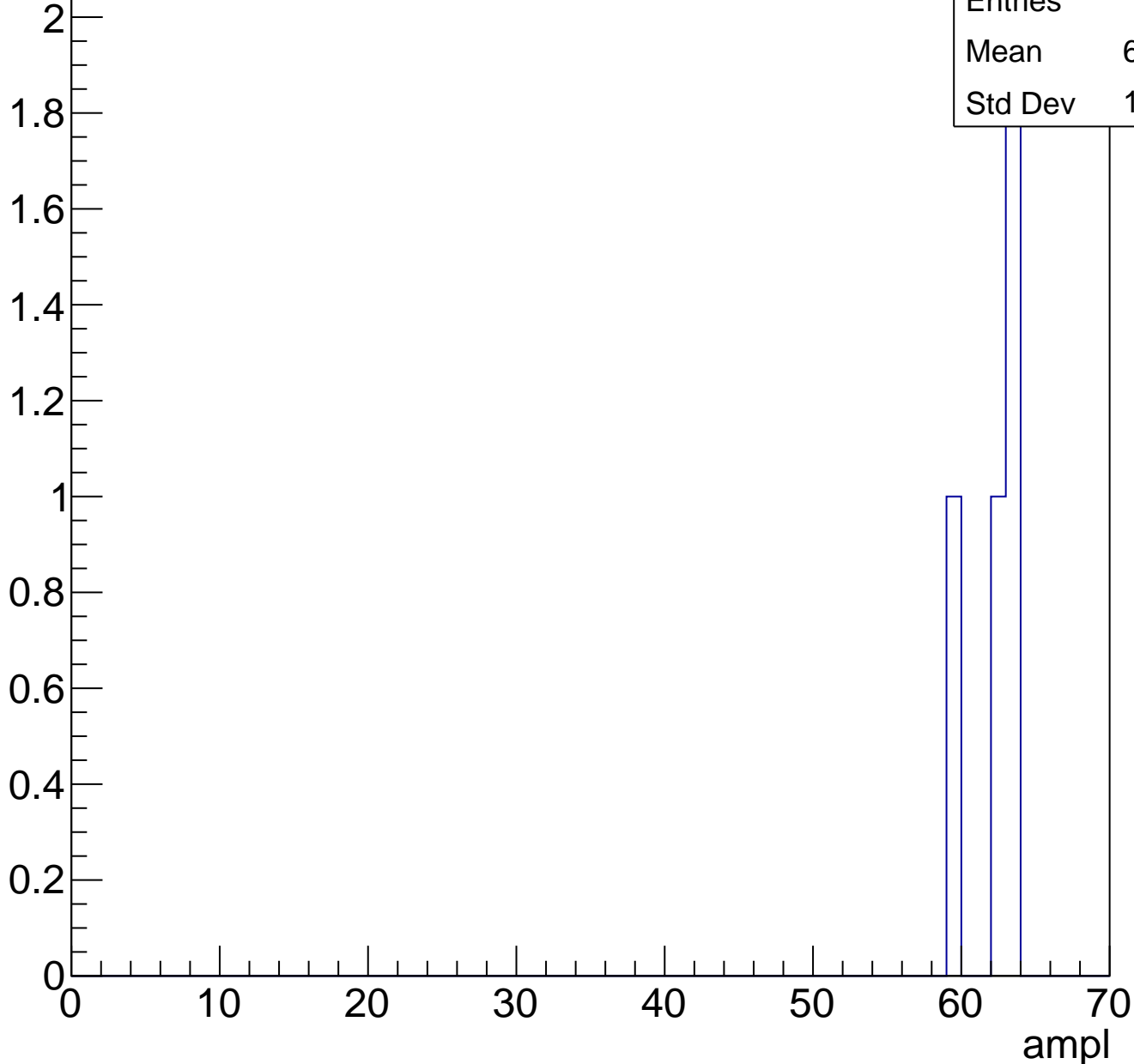
Entry



B0L000S, U4-ch68, adc6

calib_packv5_042523_0143.root, FC#5, port B1

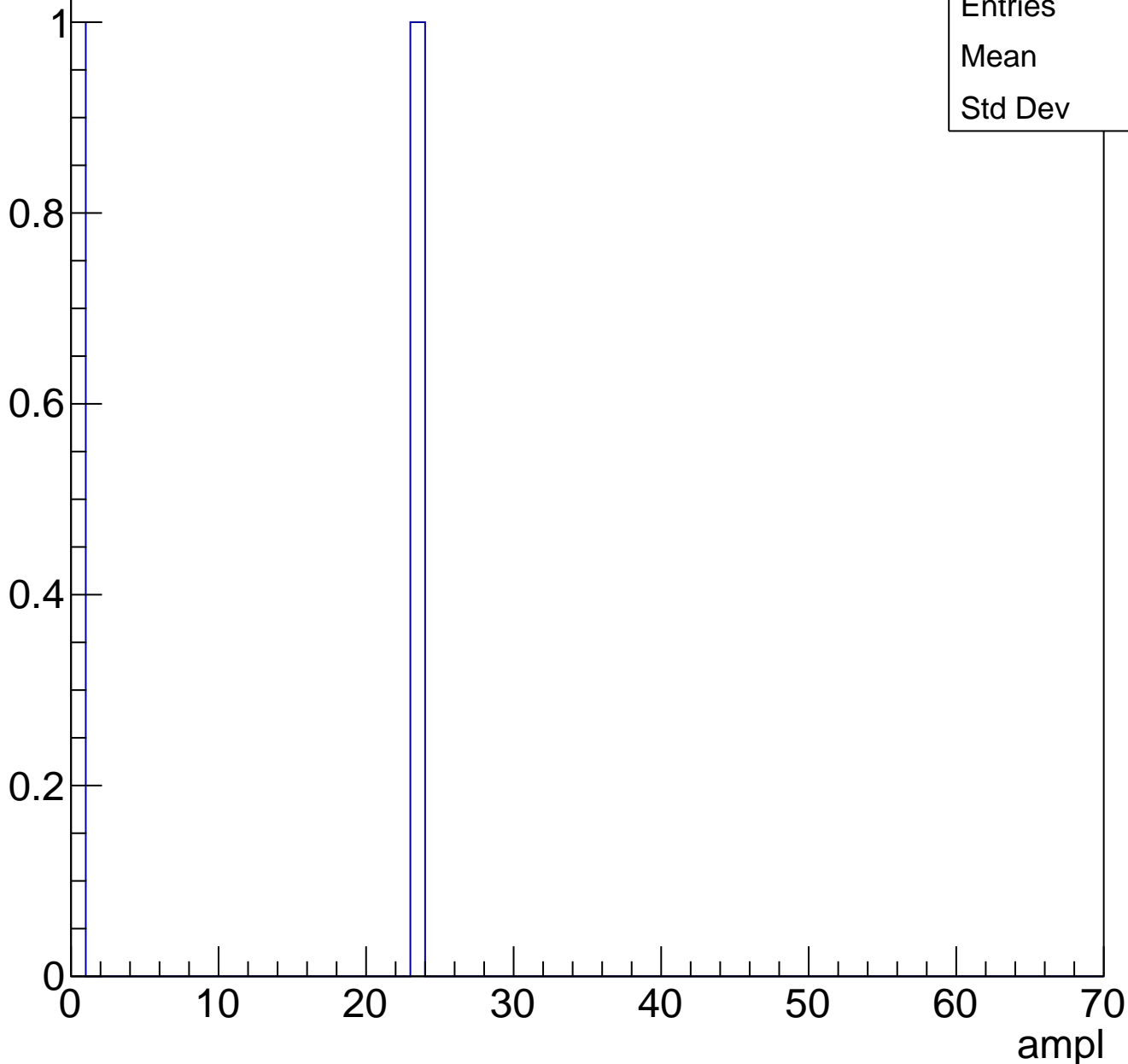
Entry



B0L000S, U4-ch68, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L000S, U4-ch69, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	65
Mean	30.51
Std Dev	3.42

Gaus mean : 30.8042

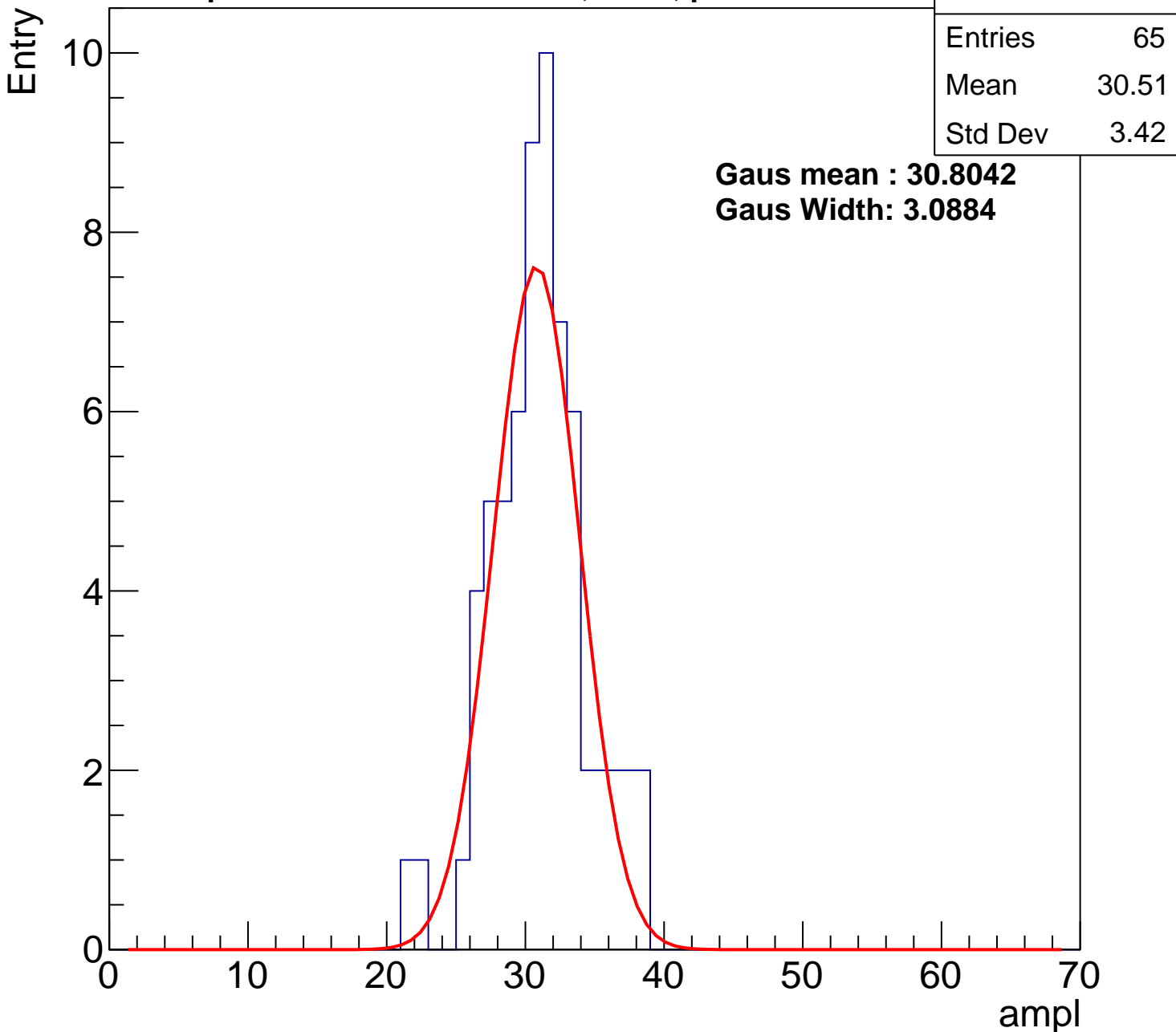
Gaus Width: 3.0884

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch69, adc1

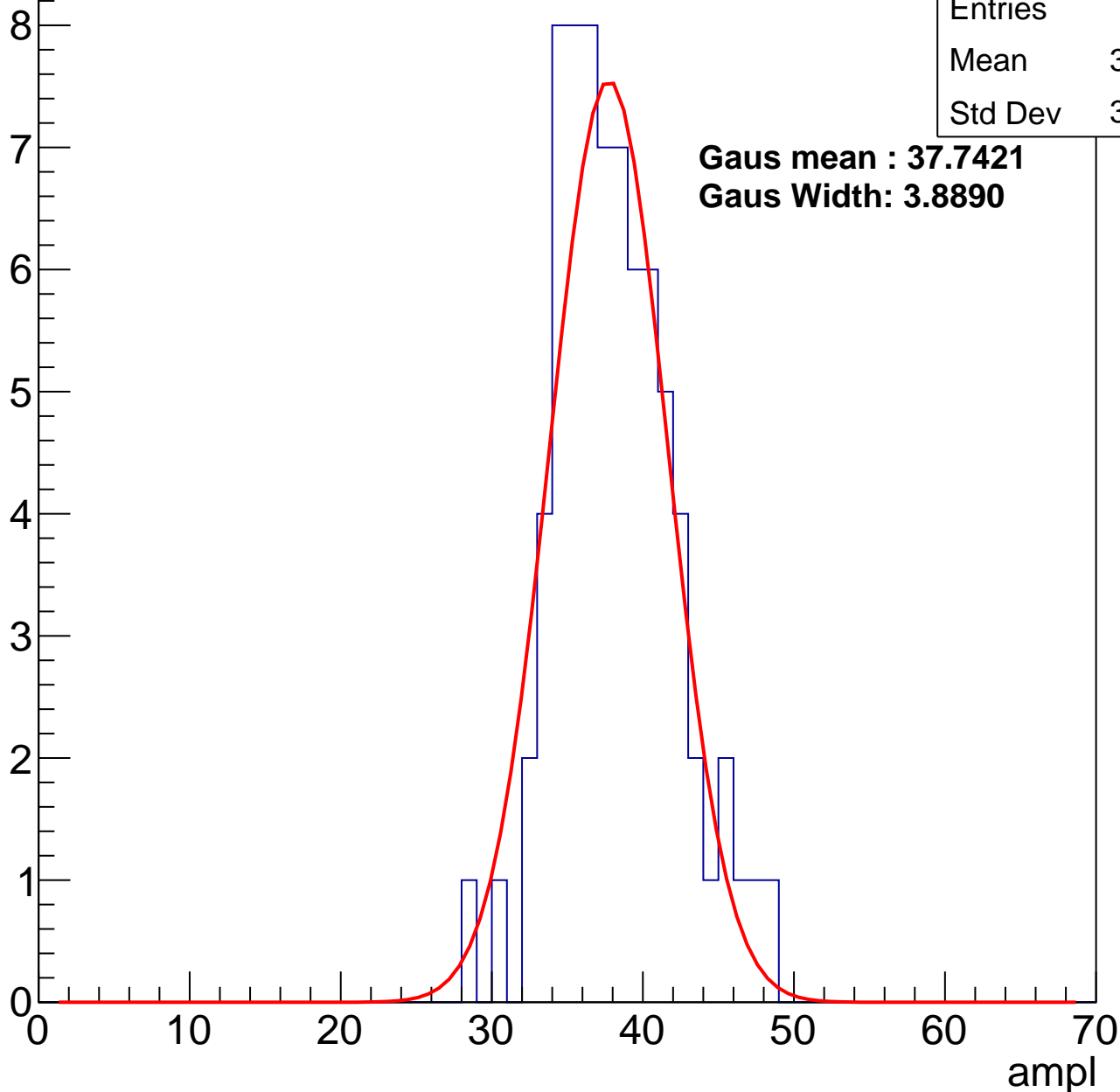
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.69
Std Dev	3.892

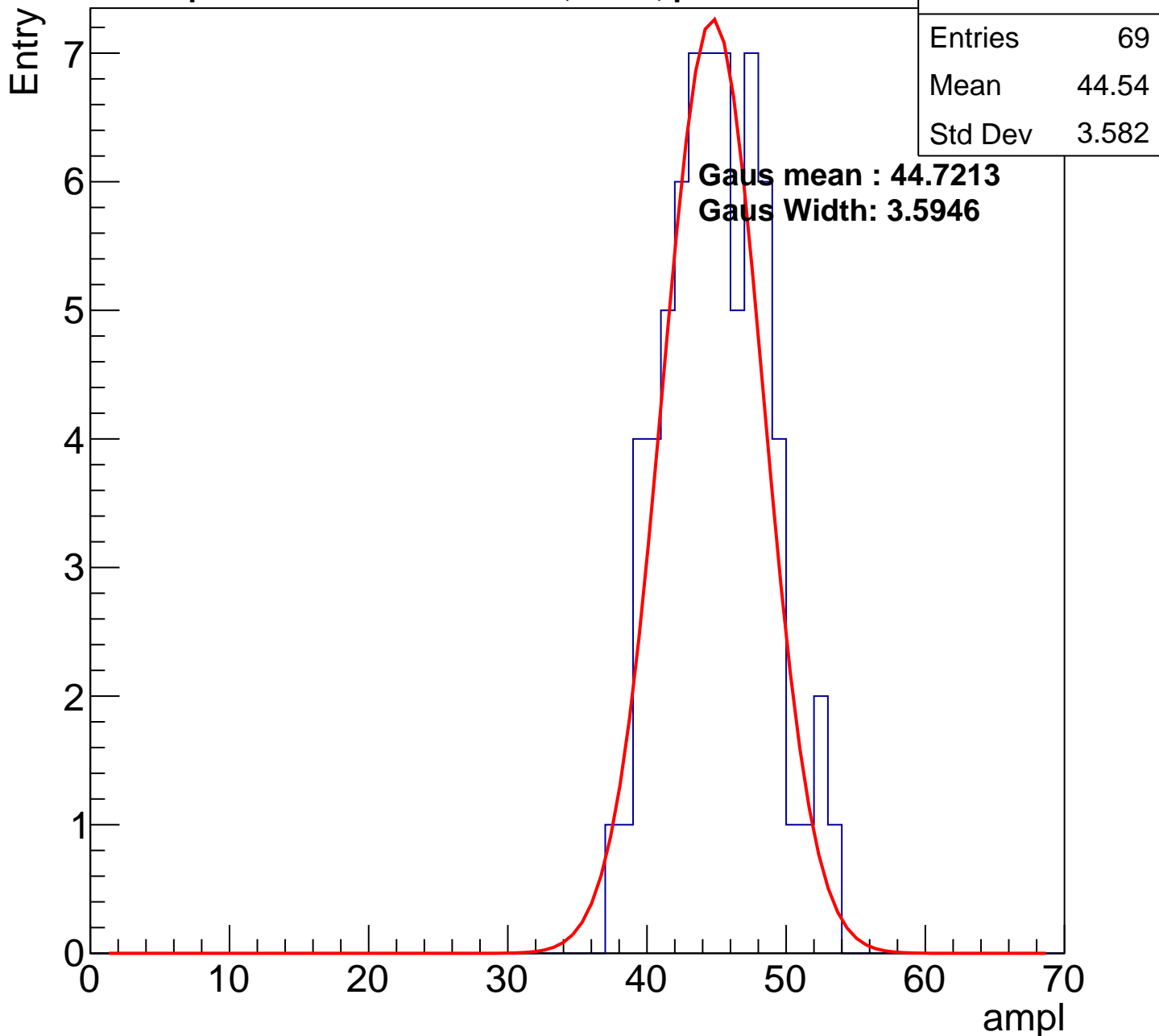
Gaus mean : 37.7421

Gaus Width: 3.8890



B0L000S, U4-ch69, adc2

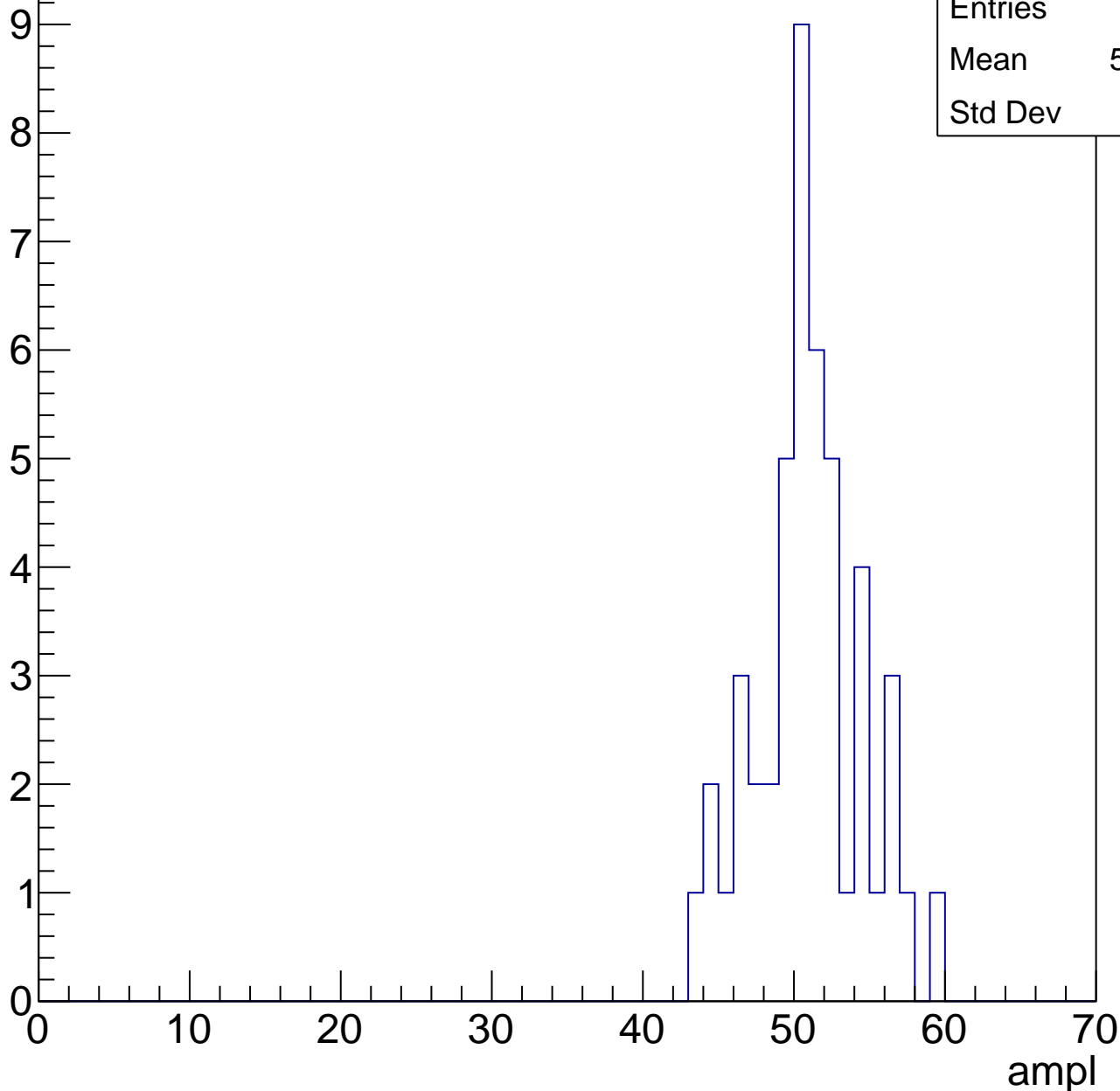
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch69, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



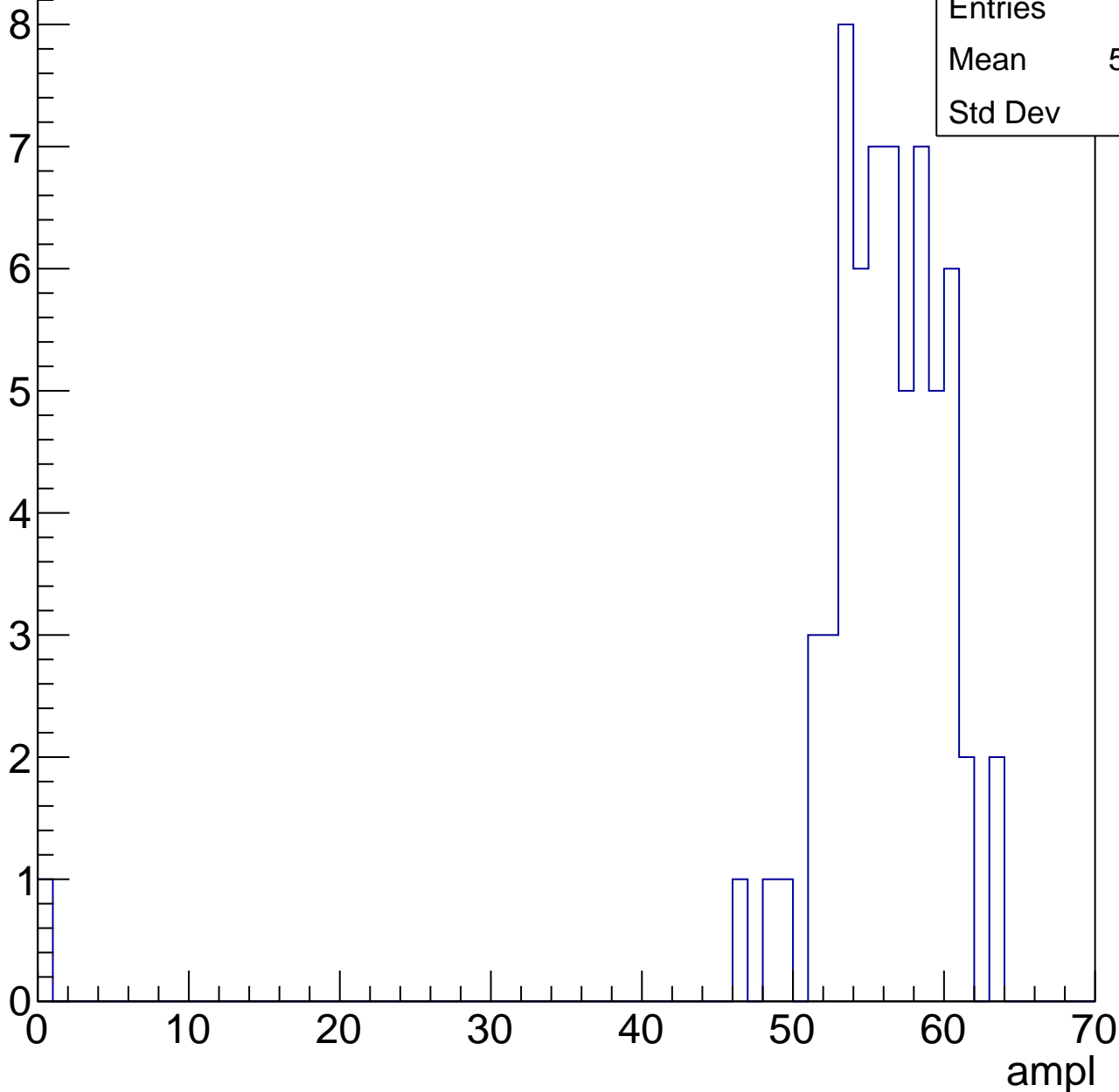
Entries	47
Mean	50.49
Std Dev	3.5

B0L000S, U4-ch69, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	54.94
Std Dev	7.67

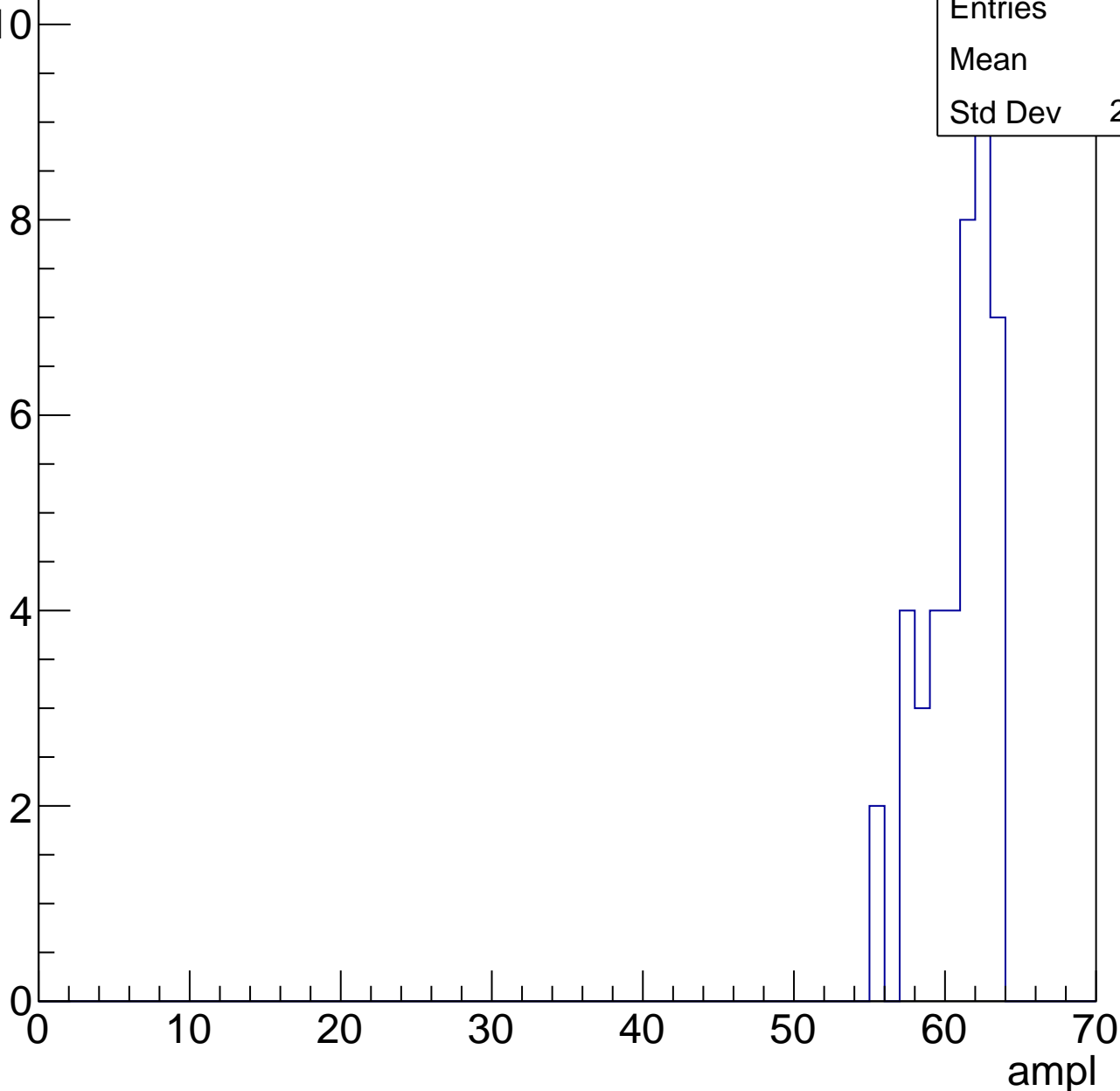


B0L000S, U4-ch69, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

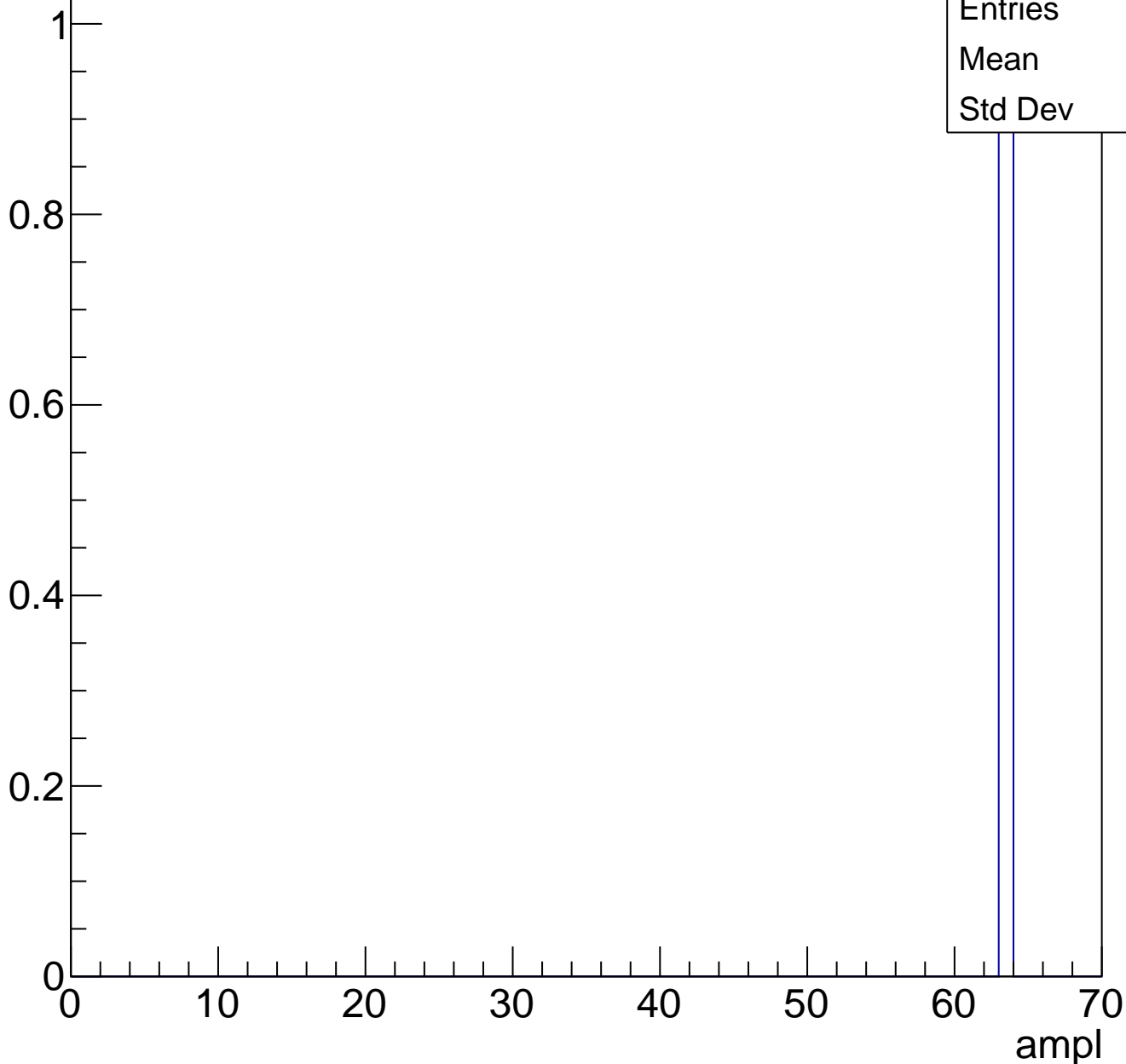
Entries	42
Mean	60.4
Std Dev	2.215



B0L000S, U4-ch69, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch69, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch70, adc0

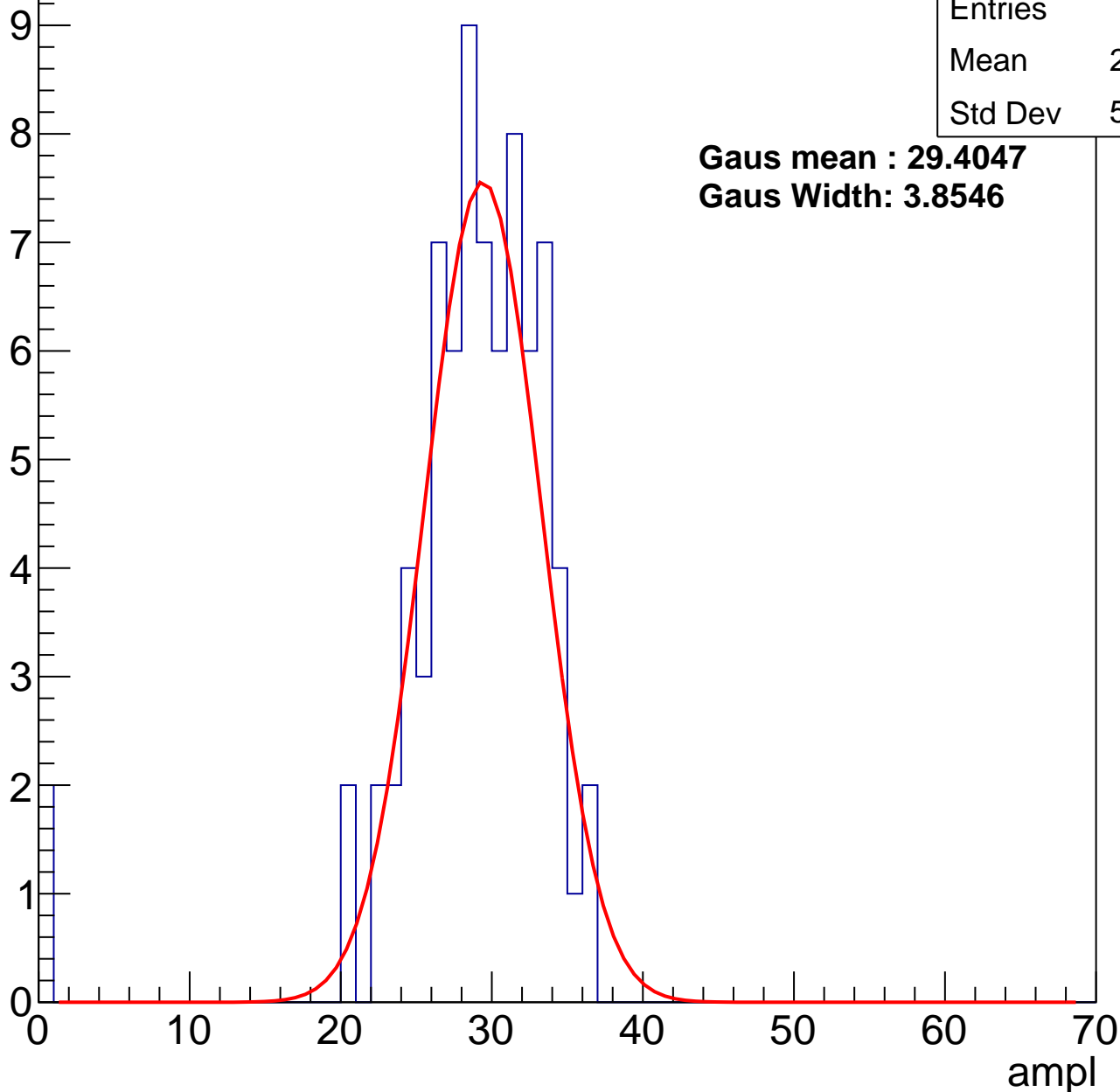
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.13
Std Dev	5.814

Gaus mean : 29.4047

Gaus Width: 3.8546



B0L000S, U4-ch70, adc1

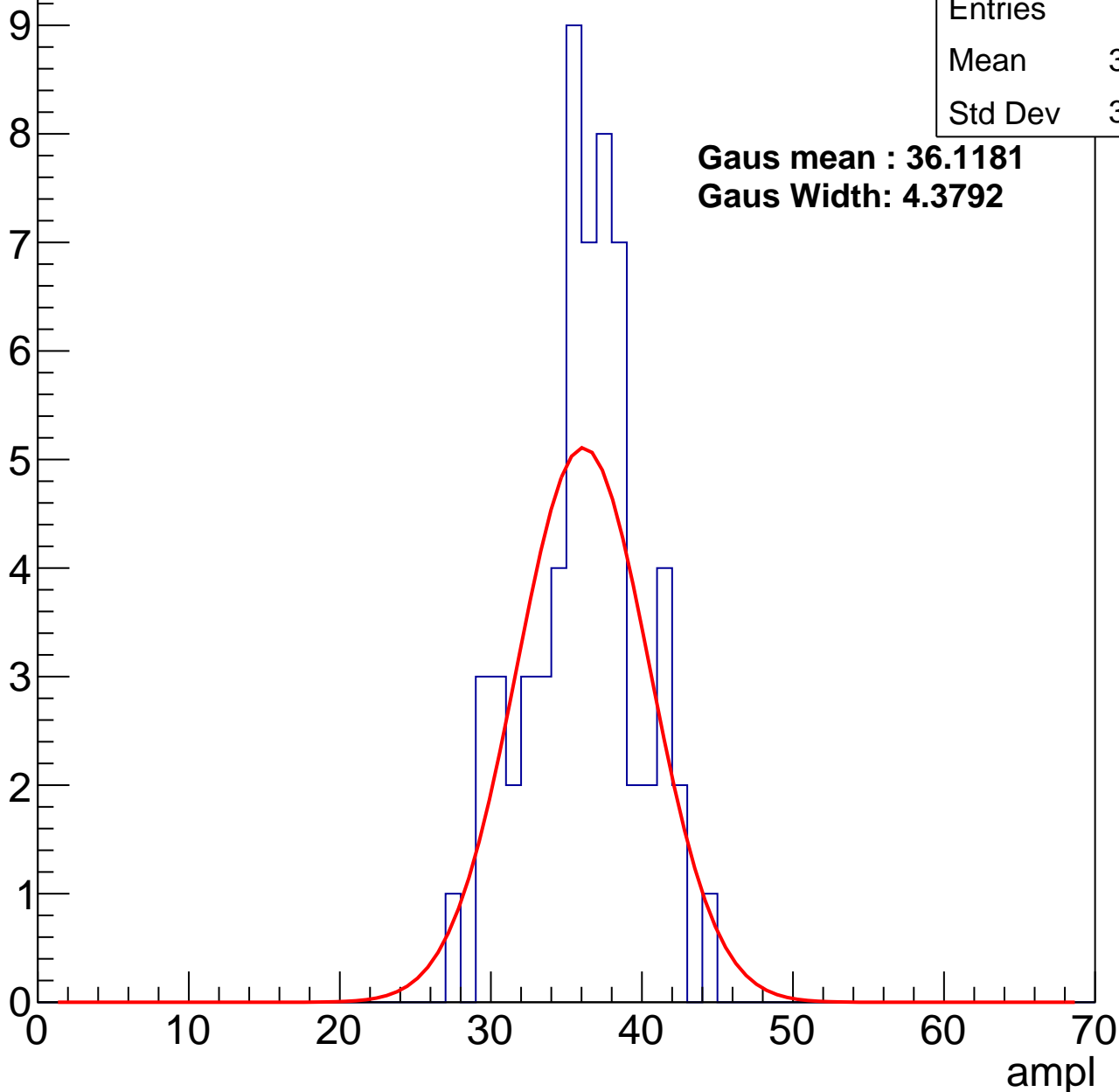
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.67
Std Dev	3.625

Gaus mean : 36.1181

Gaus Width: 4.3792



B0L000S, U4-ch70, adc2

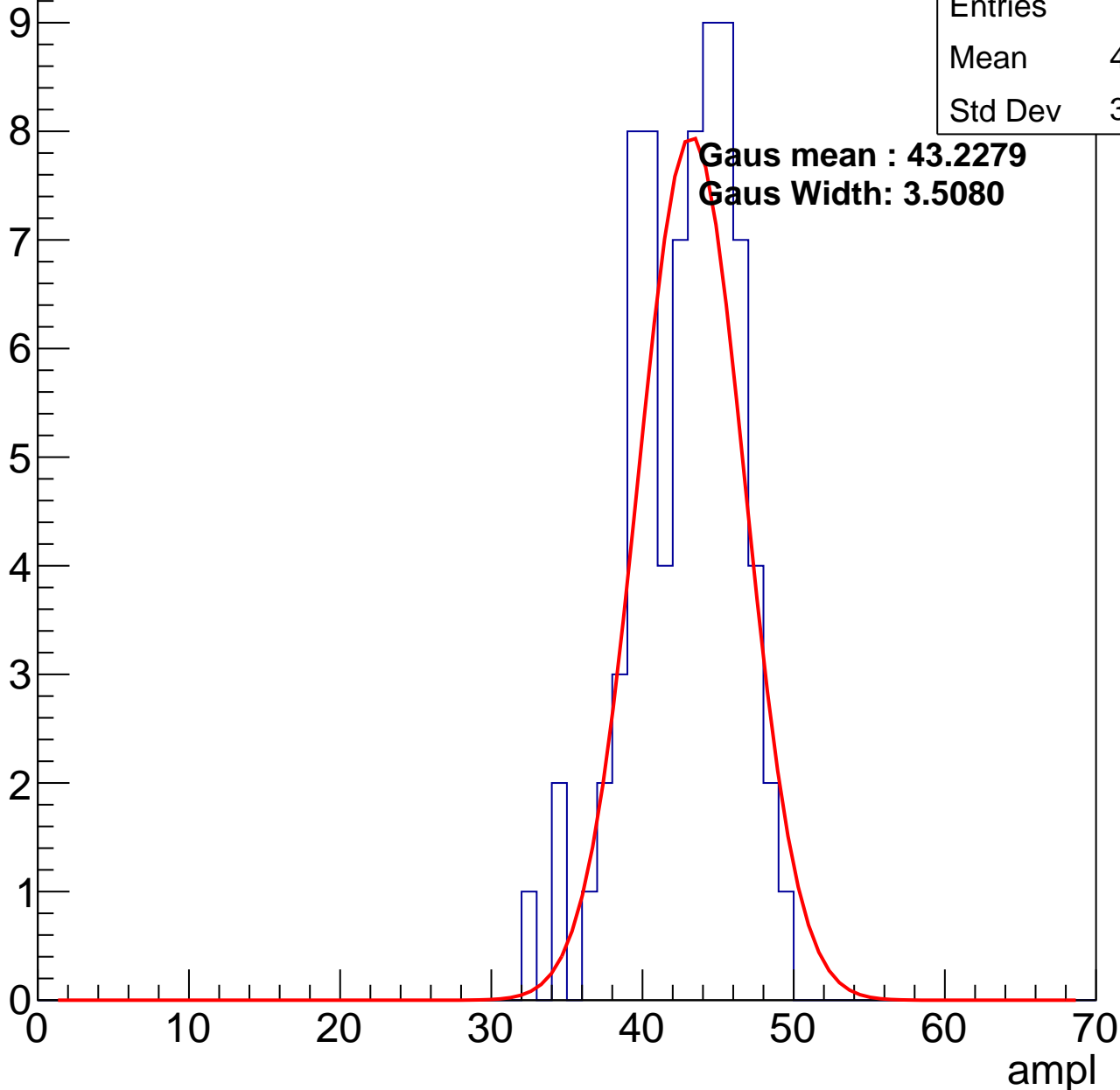
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.29
Std Dev	3.482

Gaus mean : 43.2279

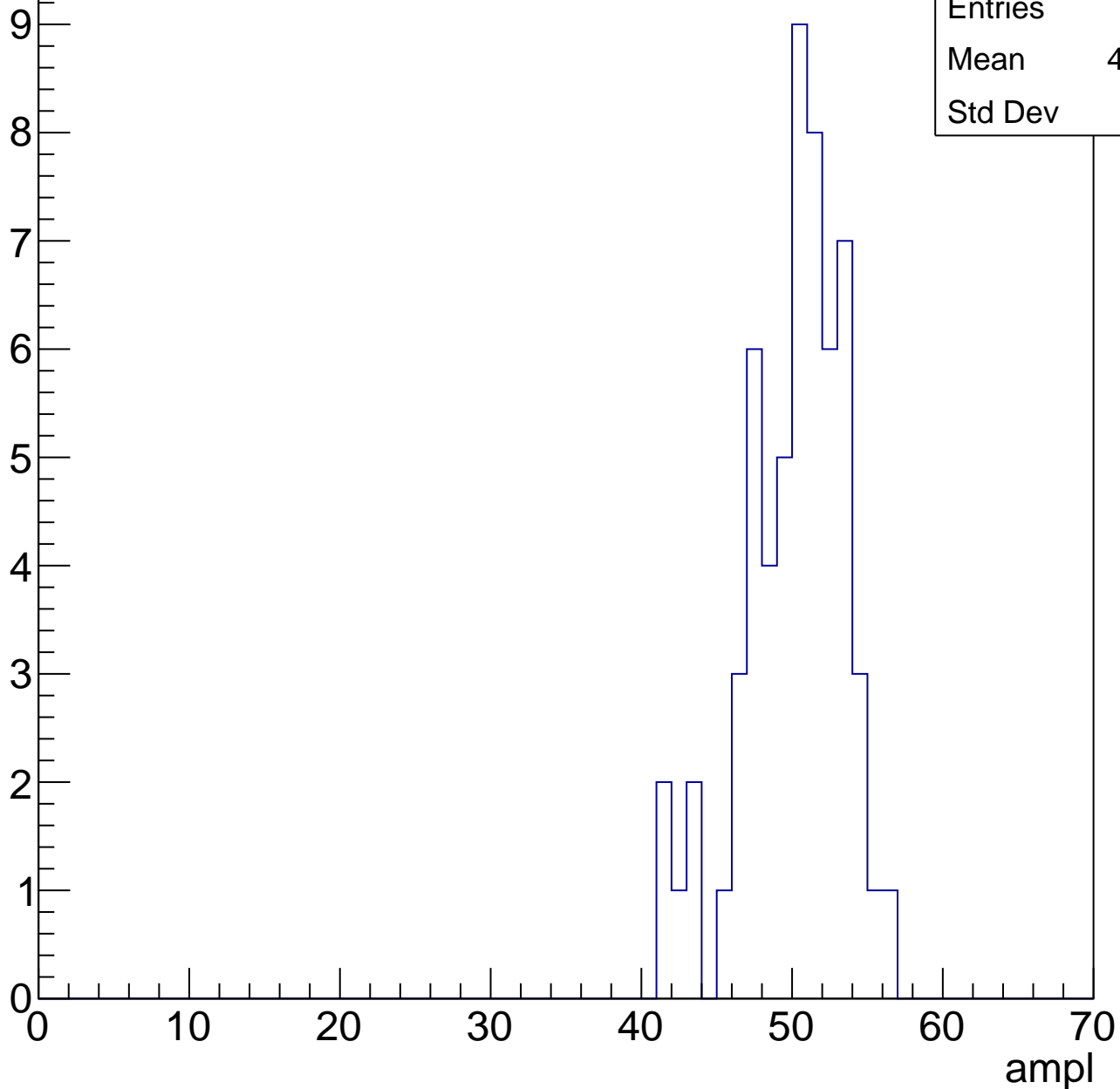
Gaus Width: 3.5080



B0L000S, U4-ch70, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

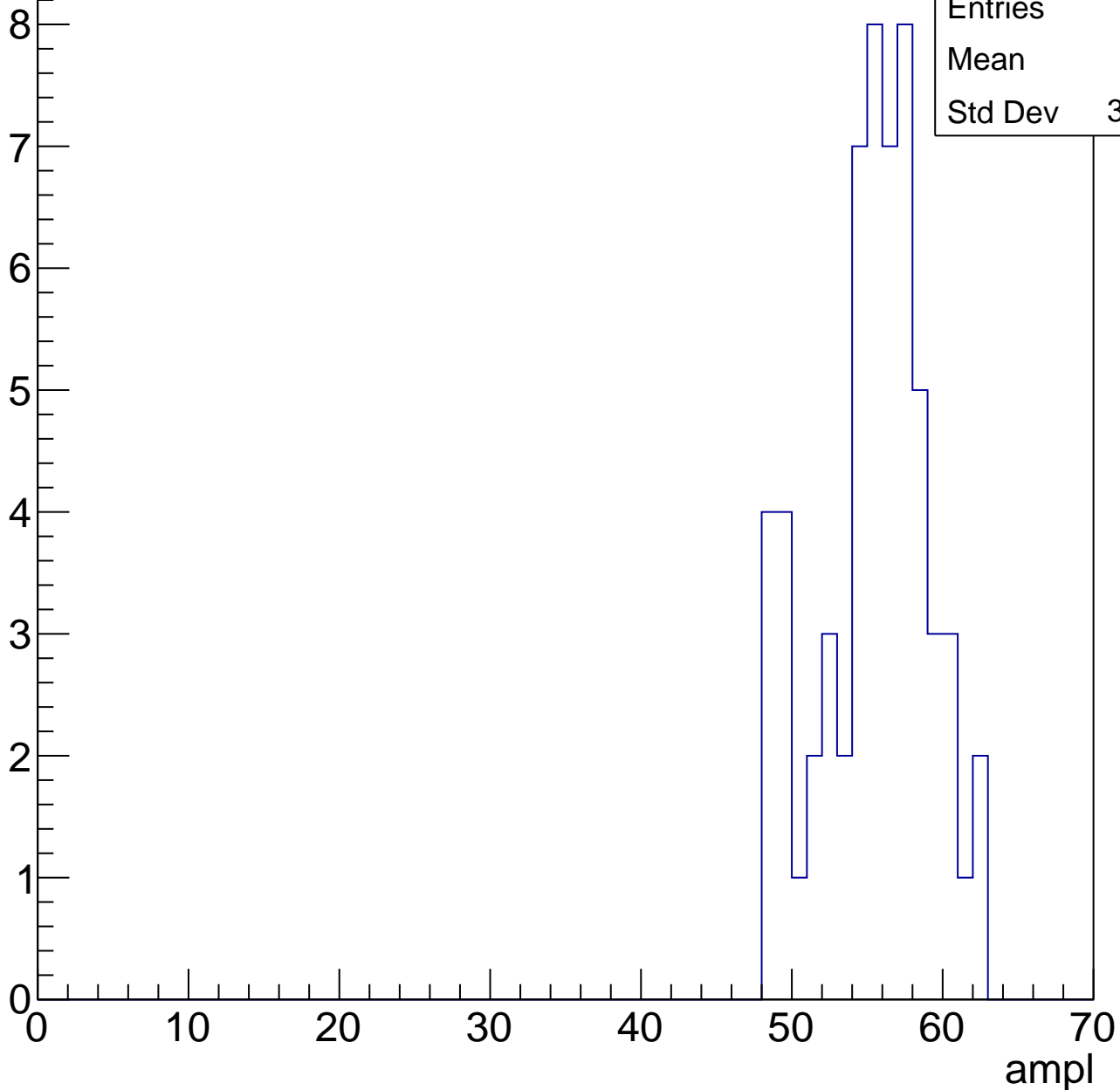


B0L000S, U4-ch70, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	55
Std Dev	3.606



B0L000S, U4-ch70, adc5

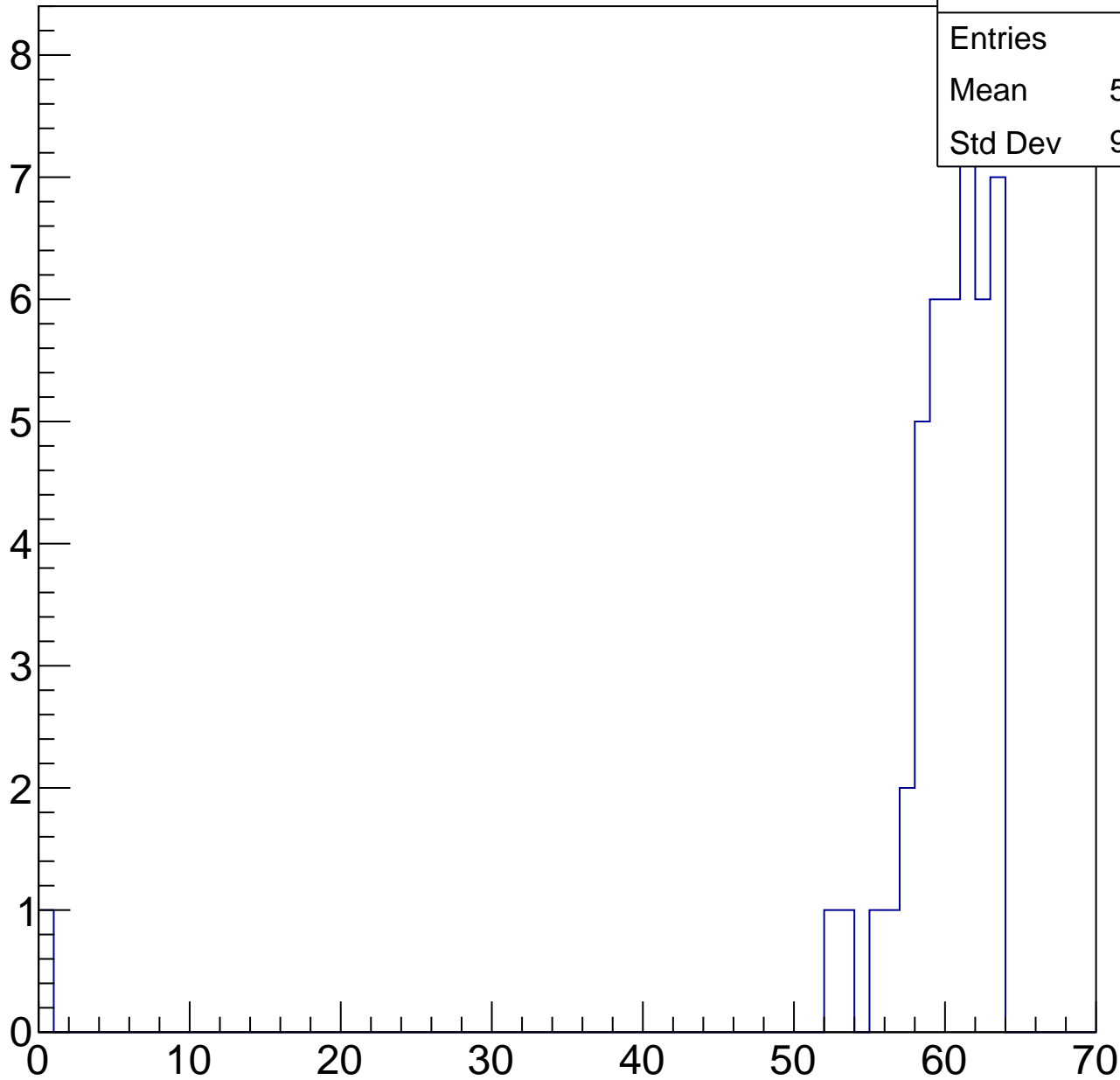
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.56
Std Dev	9.188

ampl



B0L000S, U4-ch70, adc6

calib_packv5_042523_0143.root, FC#5, port B1

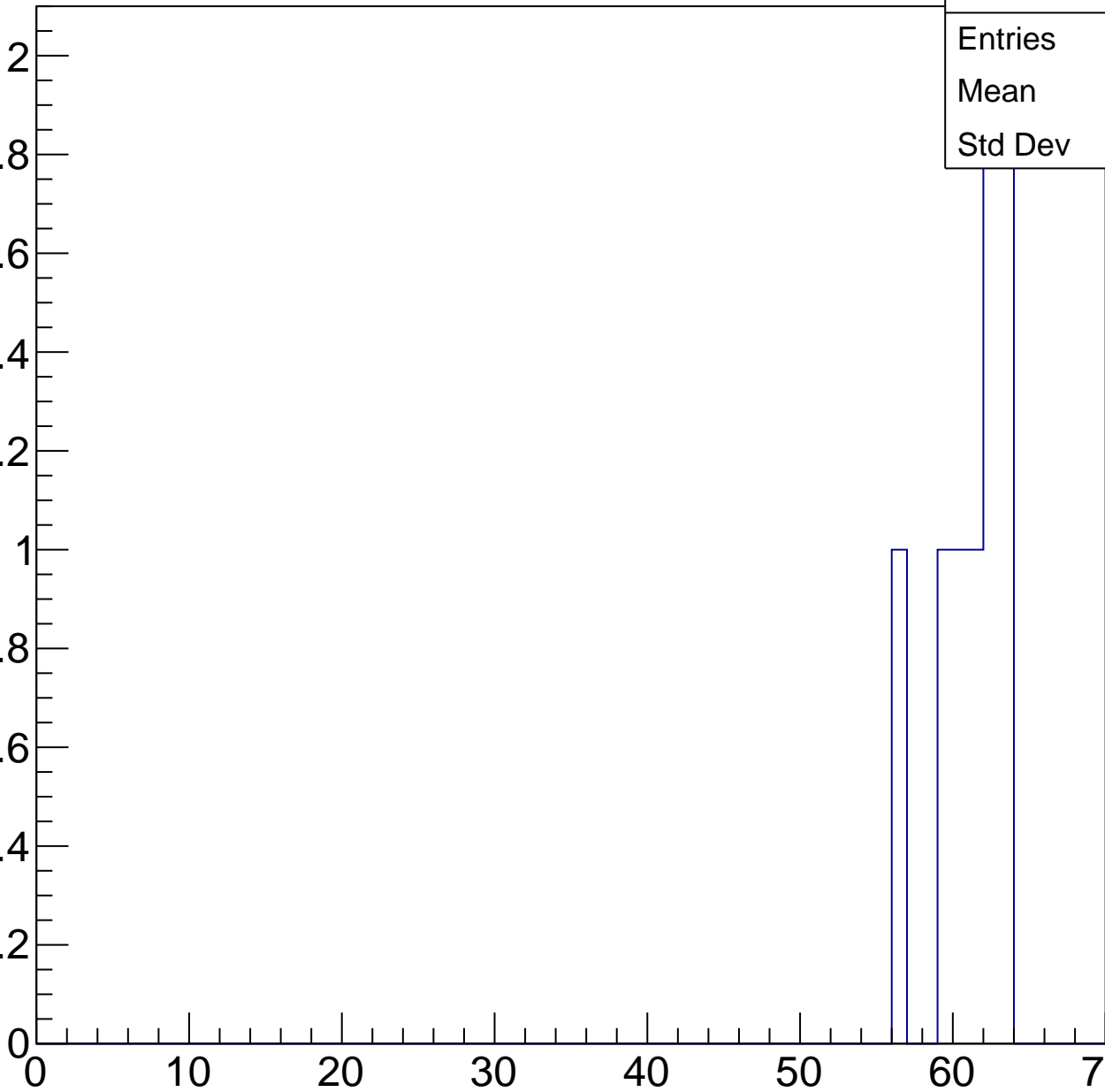
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	60.75
Std Dev	2.222

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch70, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch71, adc0

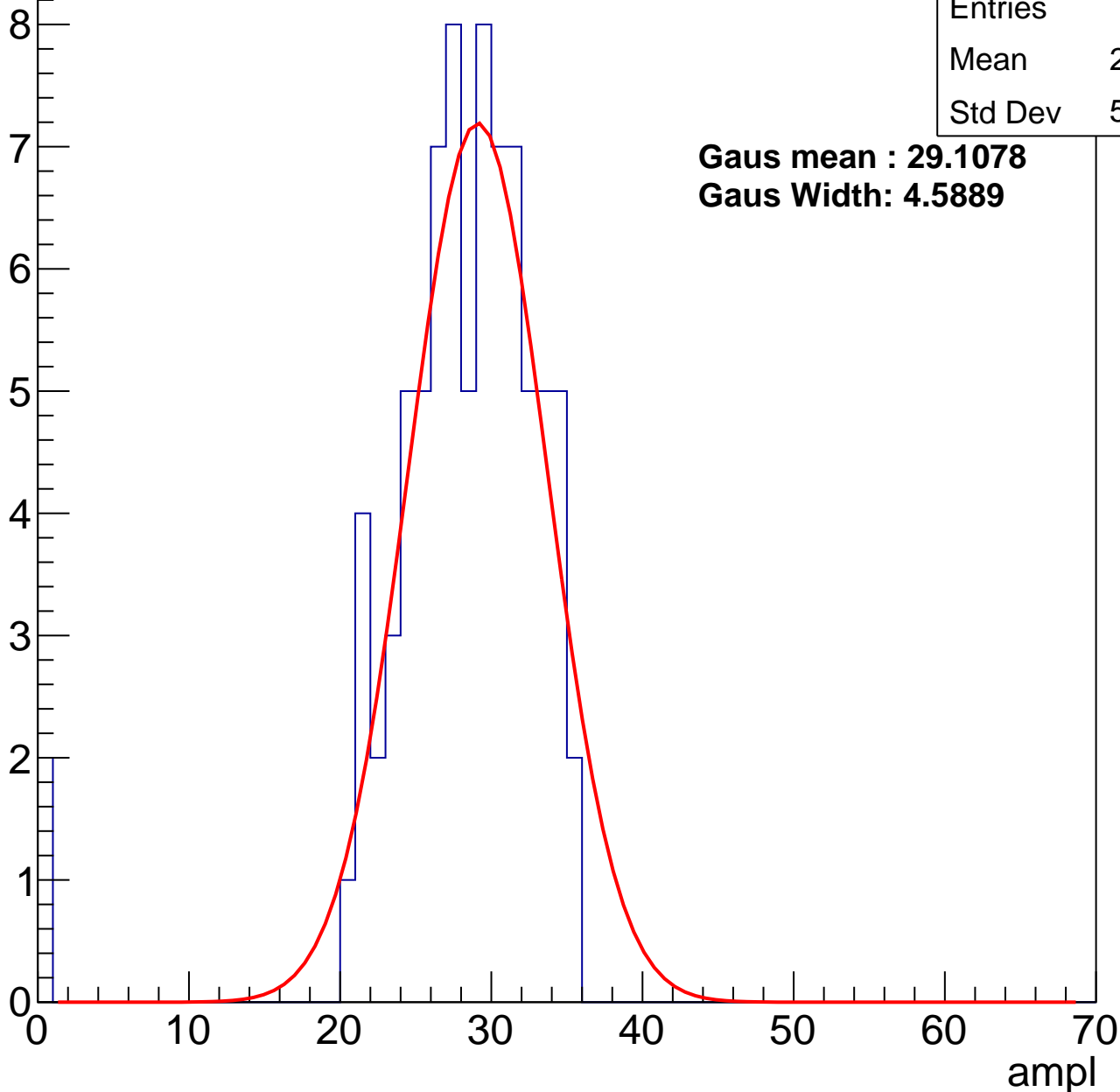
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	81
Mean	27.46
Std Dev	5.767

Gaus mean : 29.1078

Gaus Width: 4.5889



B0L000S, U4-ch71, adc1

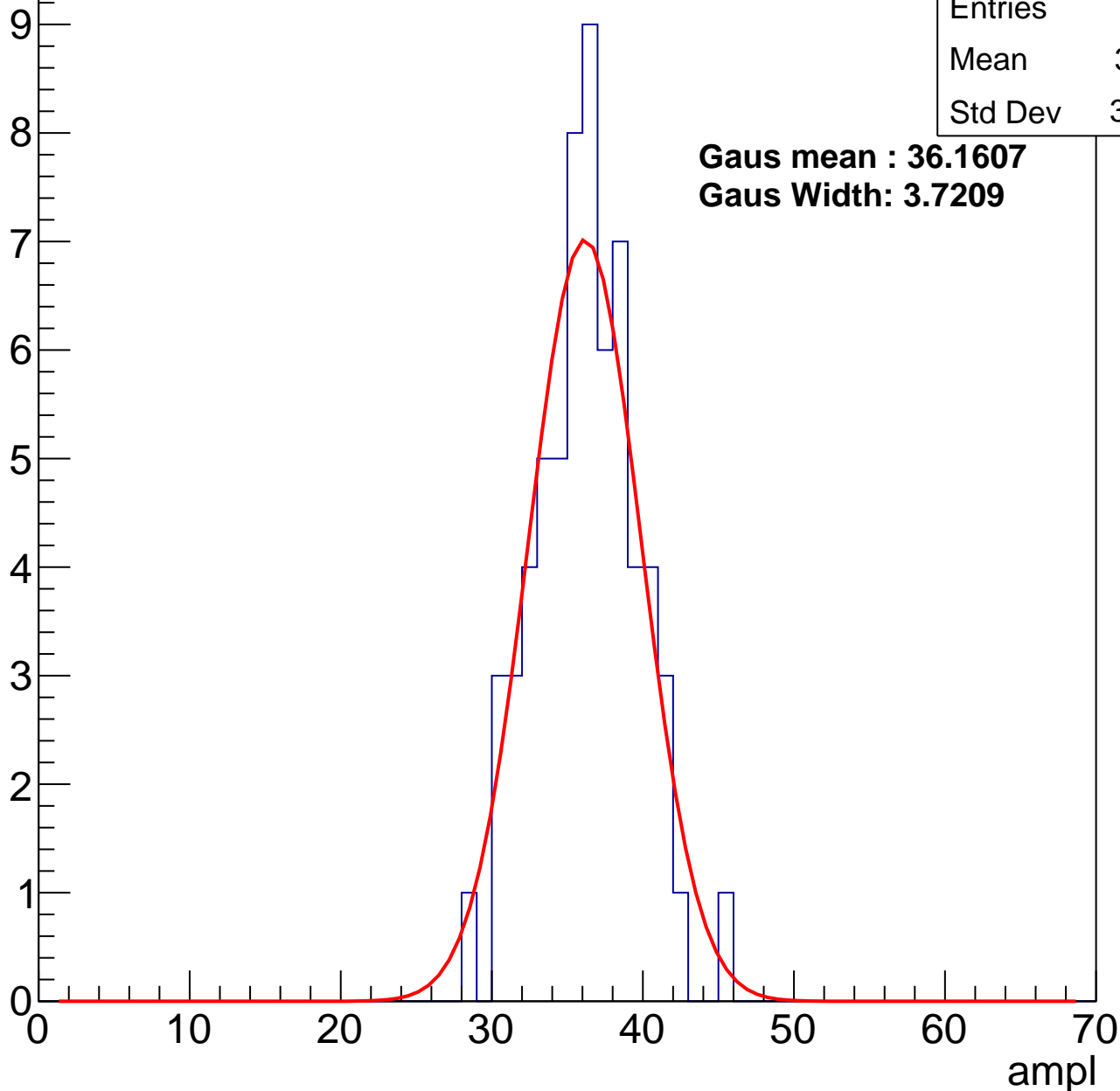
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	35.81
Std Dev	3.325

Gaus mean : 36.1607

Gaus Width: 3.7209



B0L000S, U4-ch71, adc2

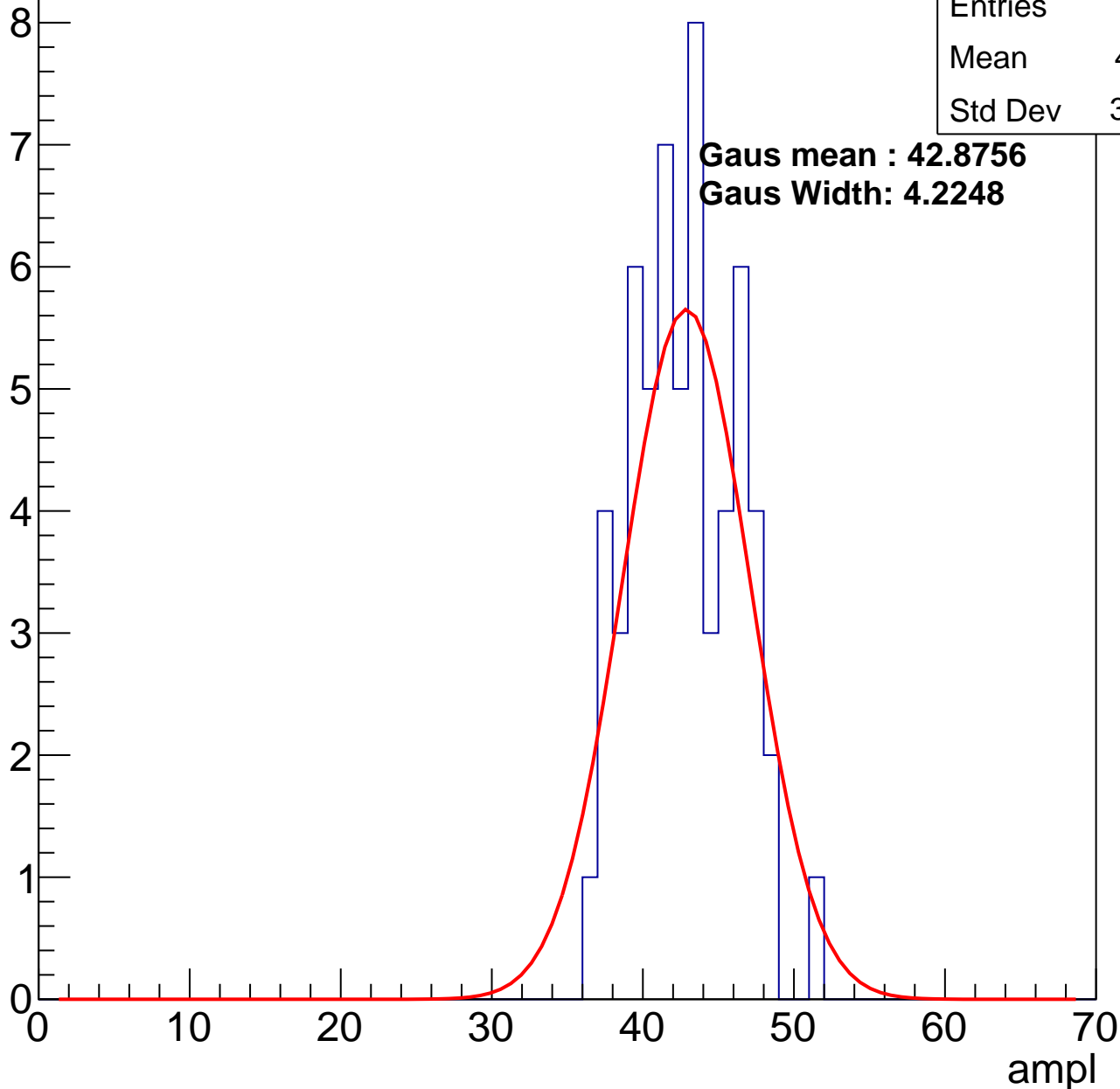
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.31
Std Dev	3.356

Gaus mean : 42.8756

Gaus Width: 4.2248

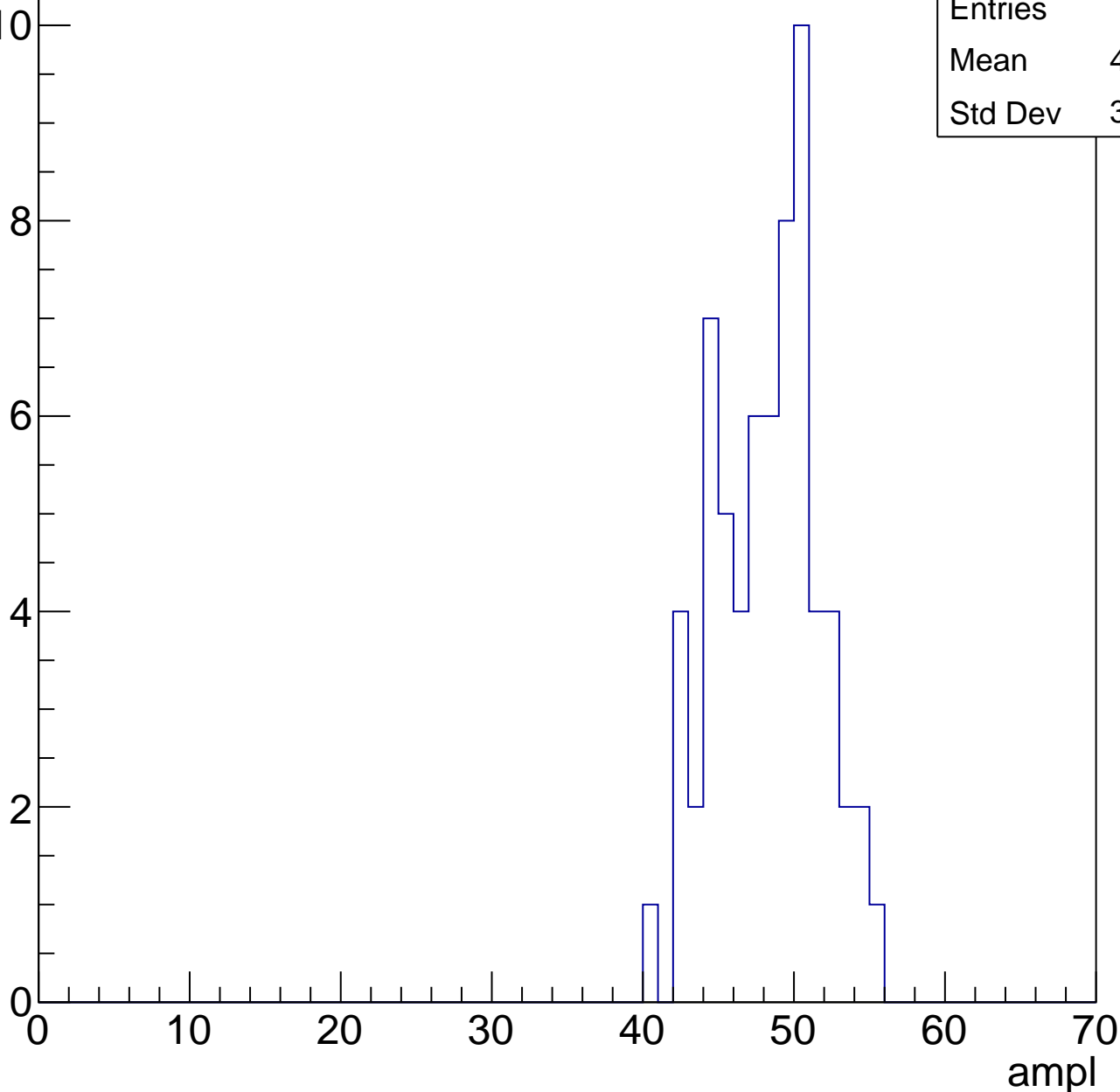


B0L000S, U4-ch71, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

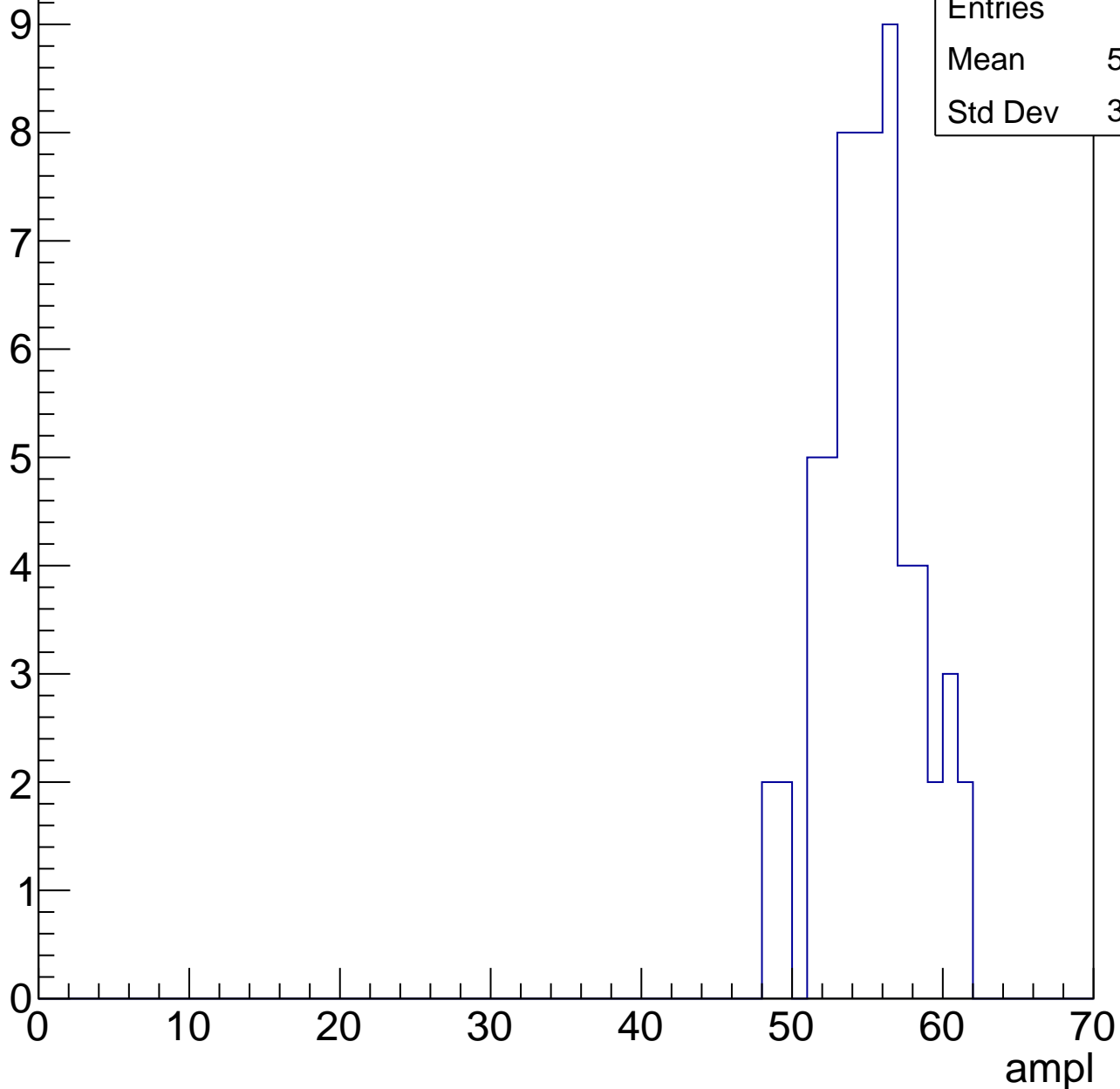
Entries	66
Mean	47.79
Std Dev	3.378



B0L000S, U4-ch71, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	62
Mean	54.66
Std Dev	3.026

B0L000S, U4-ch71, adc5

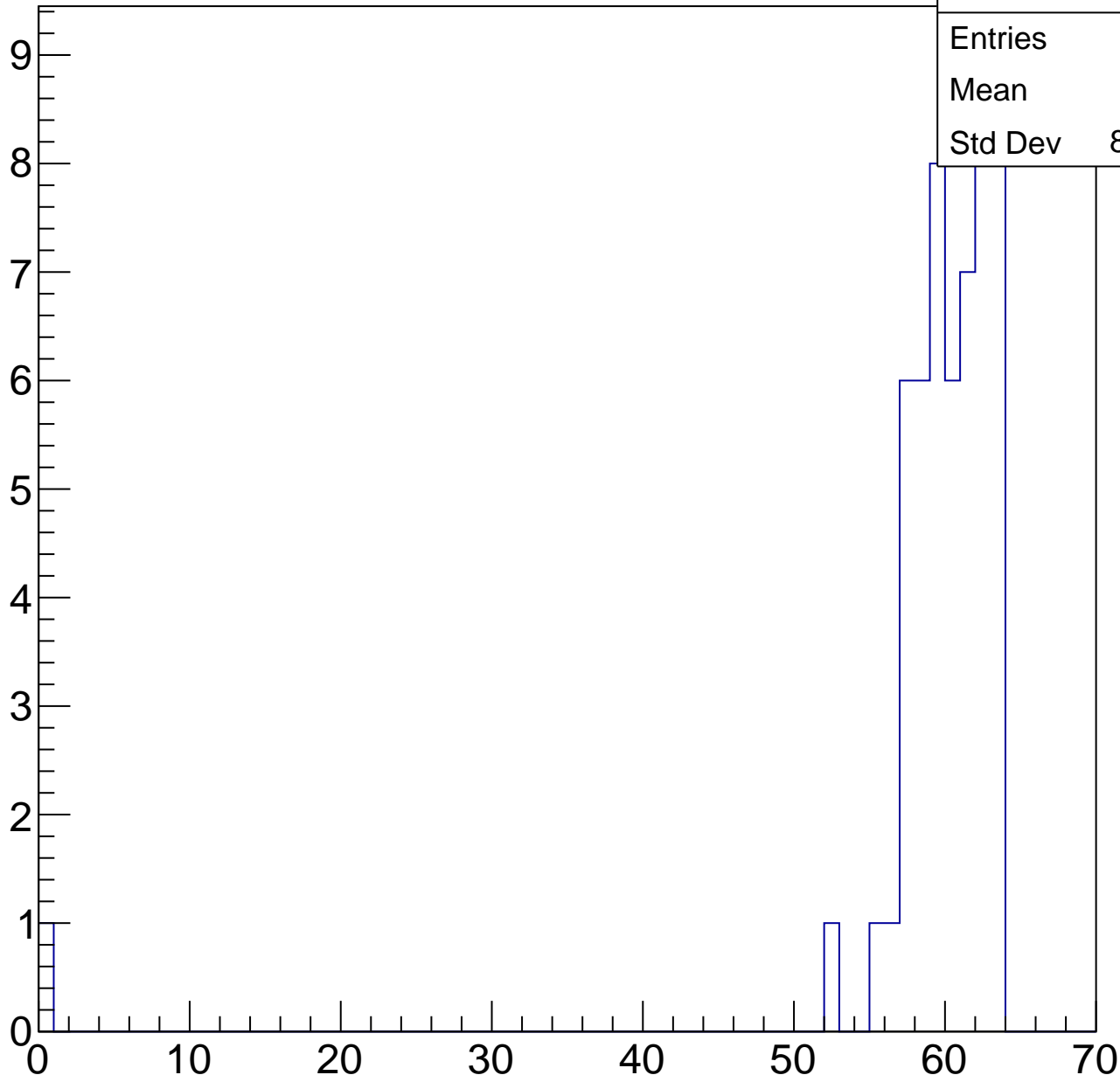
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.8
Std Dev	8.425

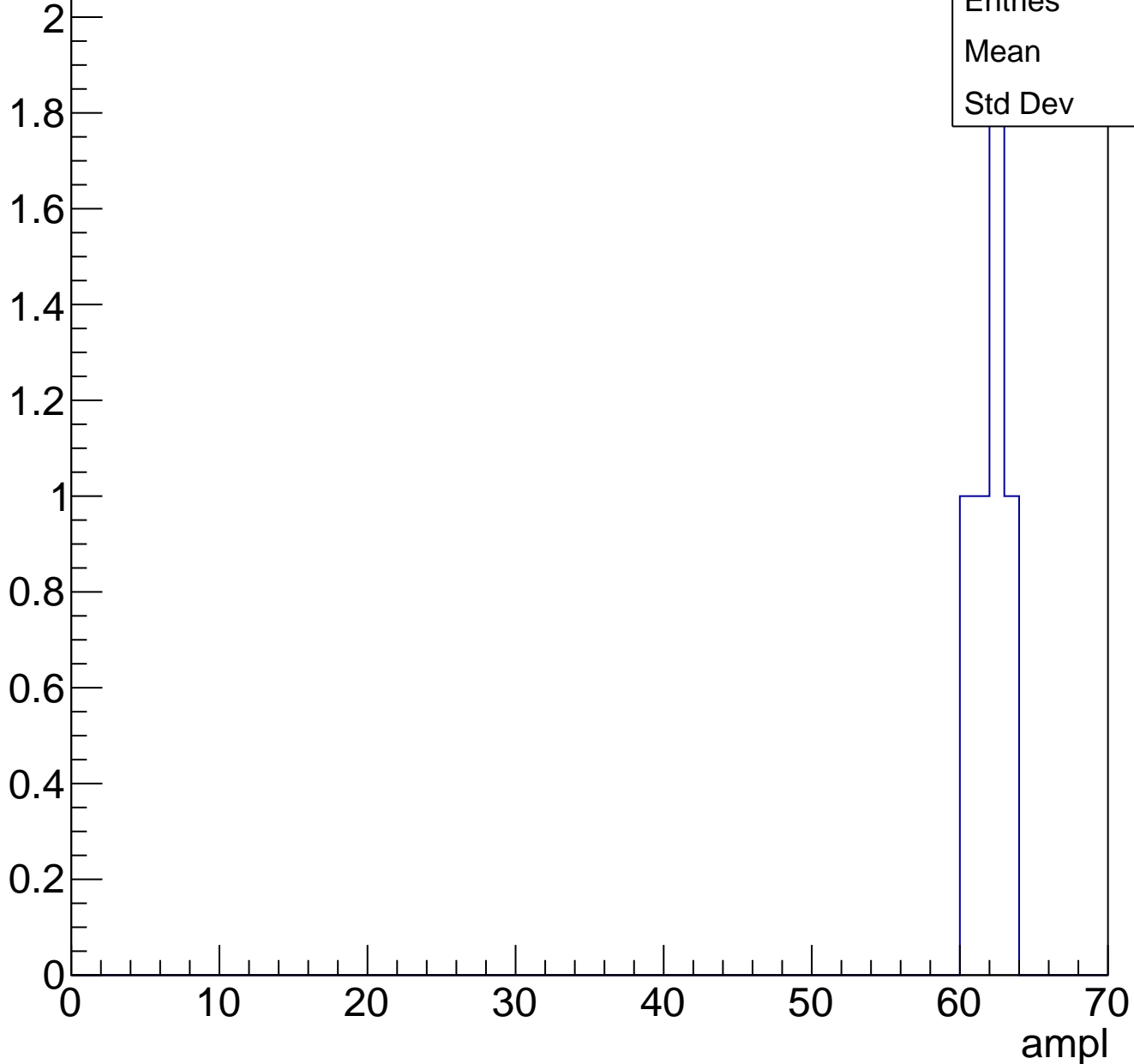
ampl



B0L000S, U4-ch71, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch71, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch72, adc0

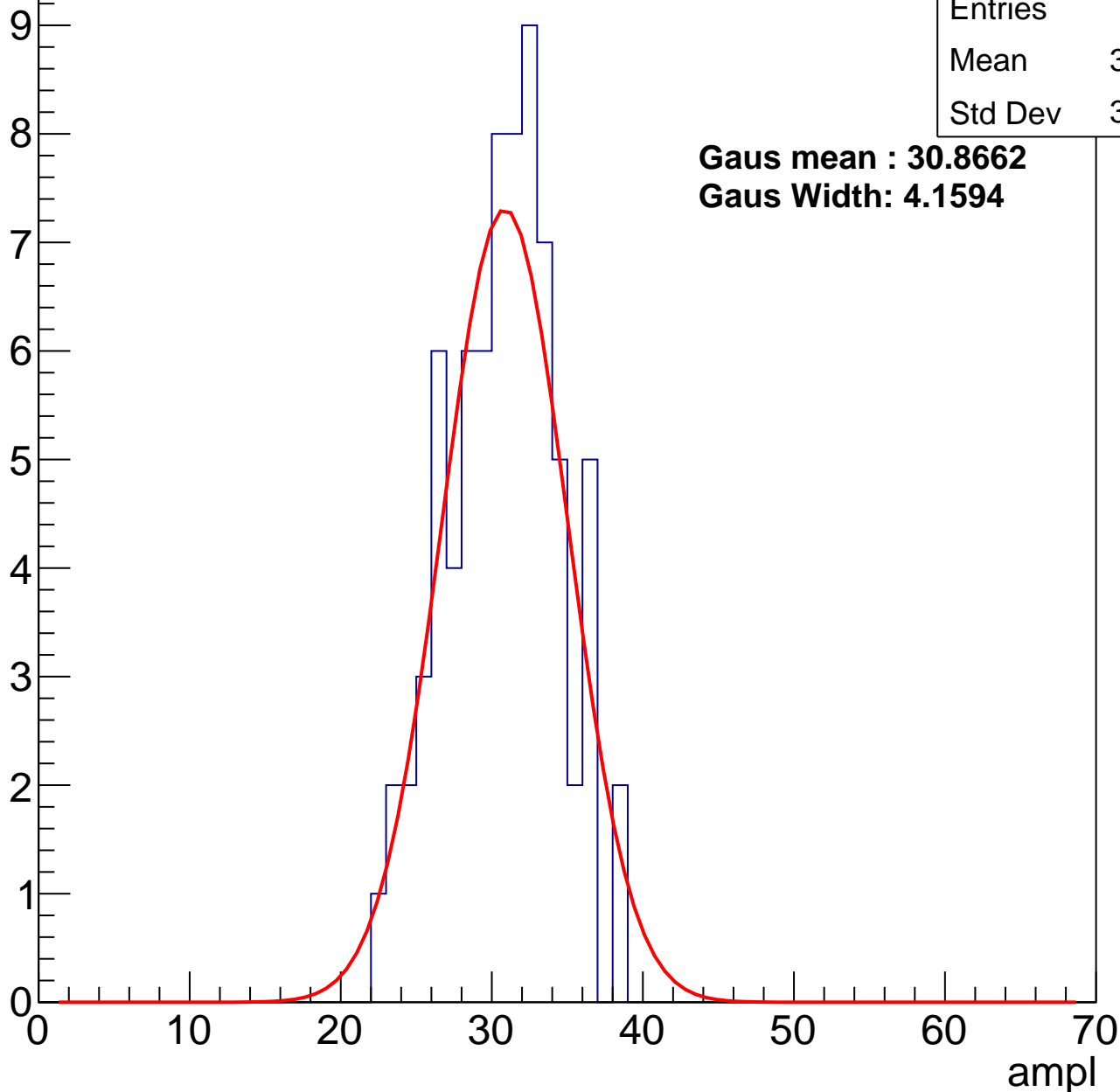
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	30.26
Std Dev	3.647

Gaus mean : 30.8662

Gaus Width: 4.1594



B0L000S, U4-ch72, adc1

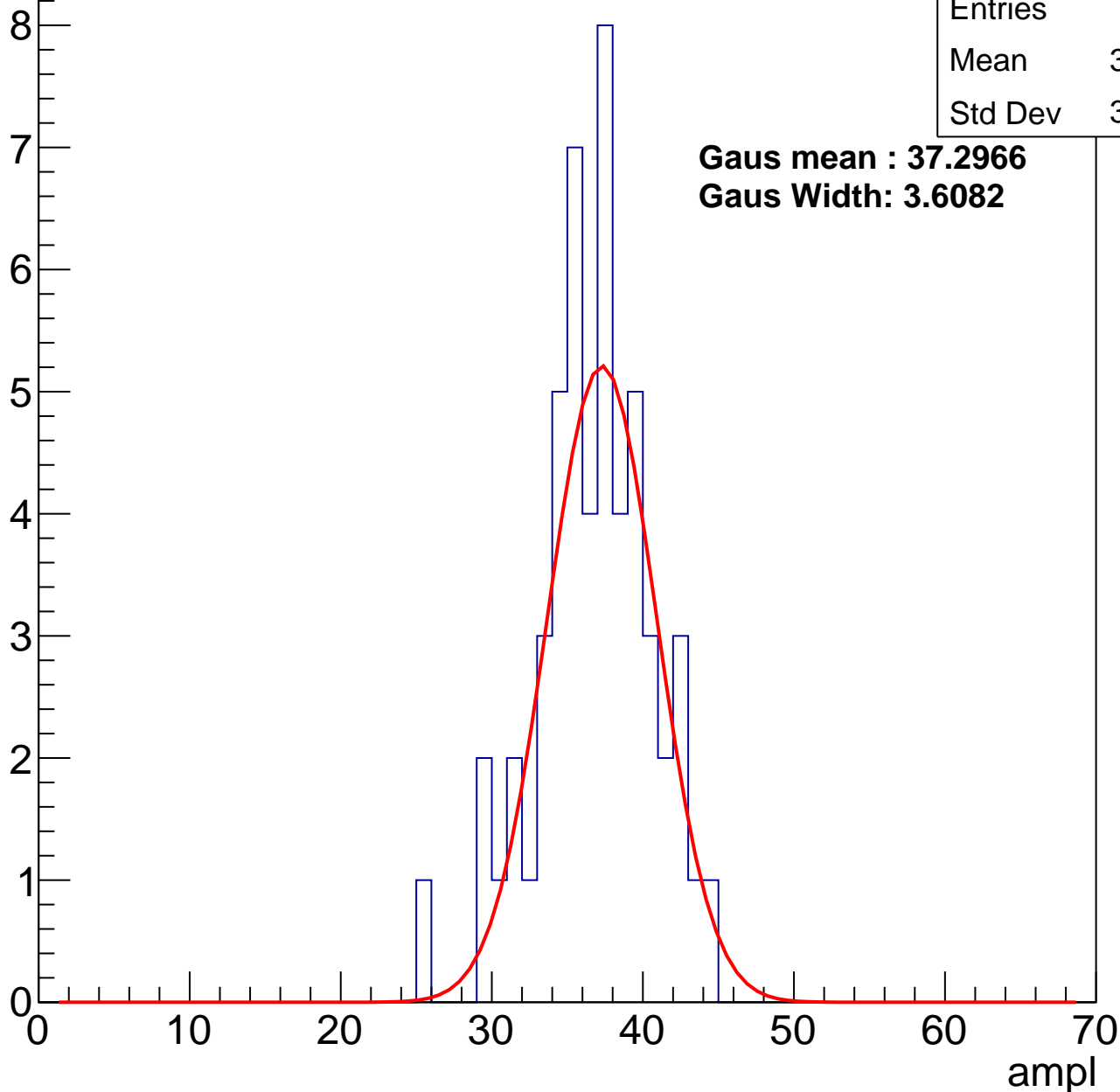
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.28
Std Dev	3.784

Gaus mean : 37.2966

Gaus Width: 3.6082



B0L000S, U4-ch72, adc2

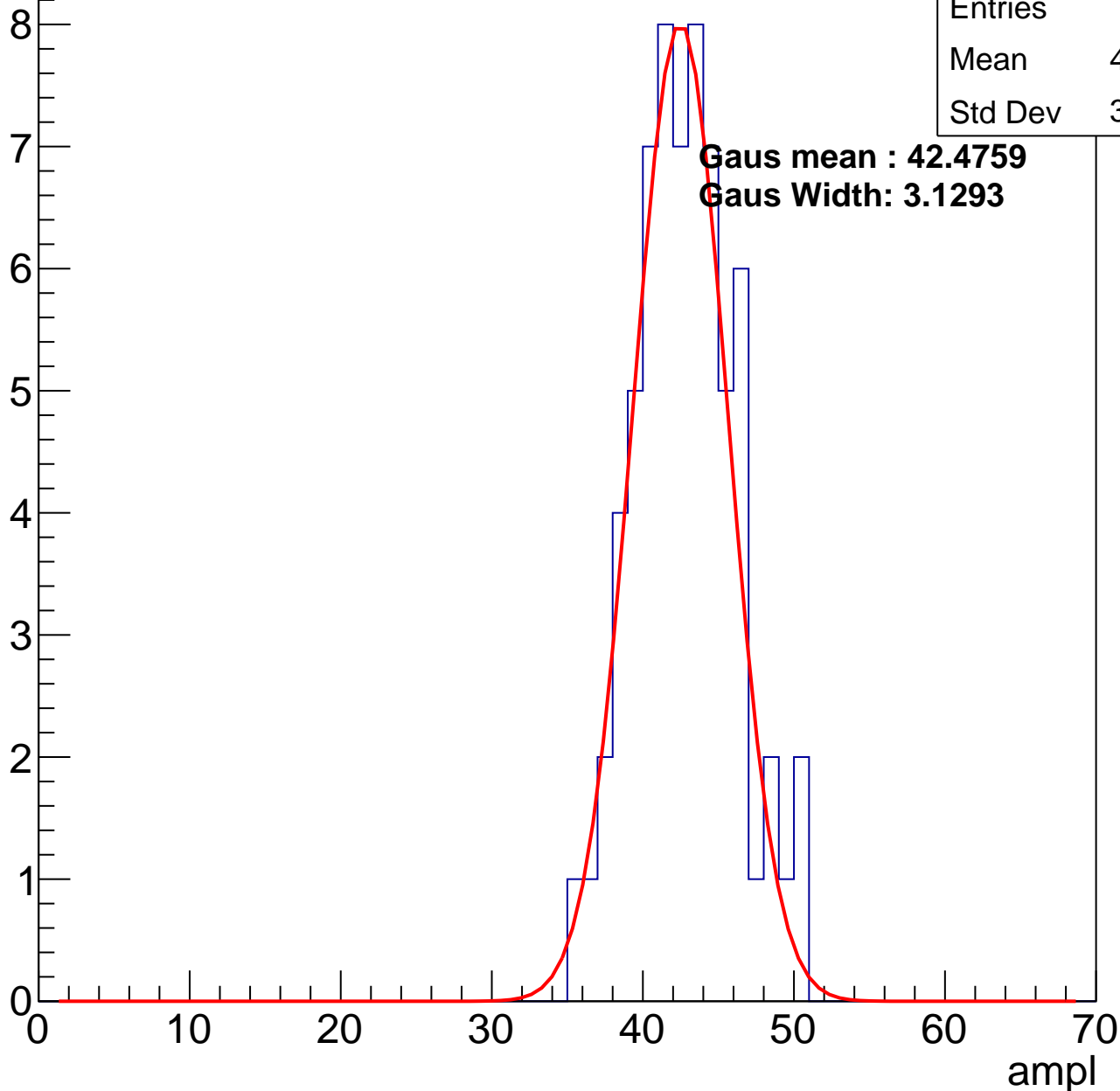
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	42.37
Std Dev	3.282

Gaus mean : 42.4759

Gaus Width: 3.1293

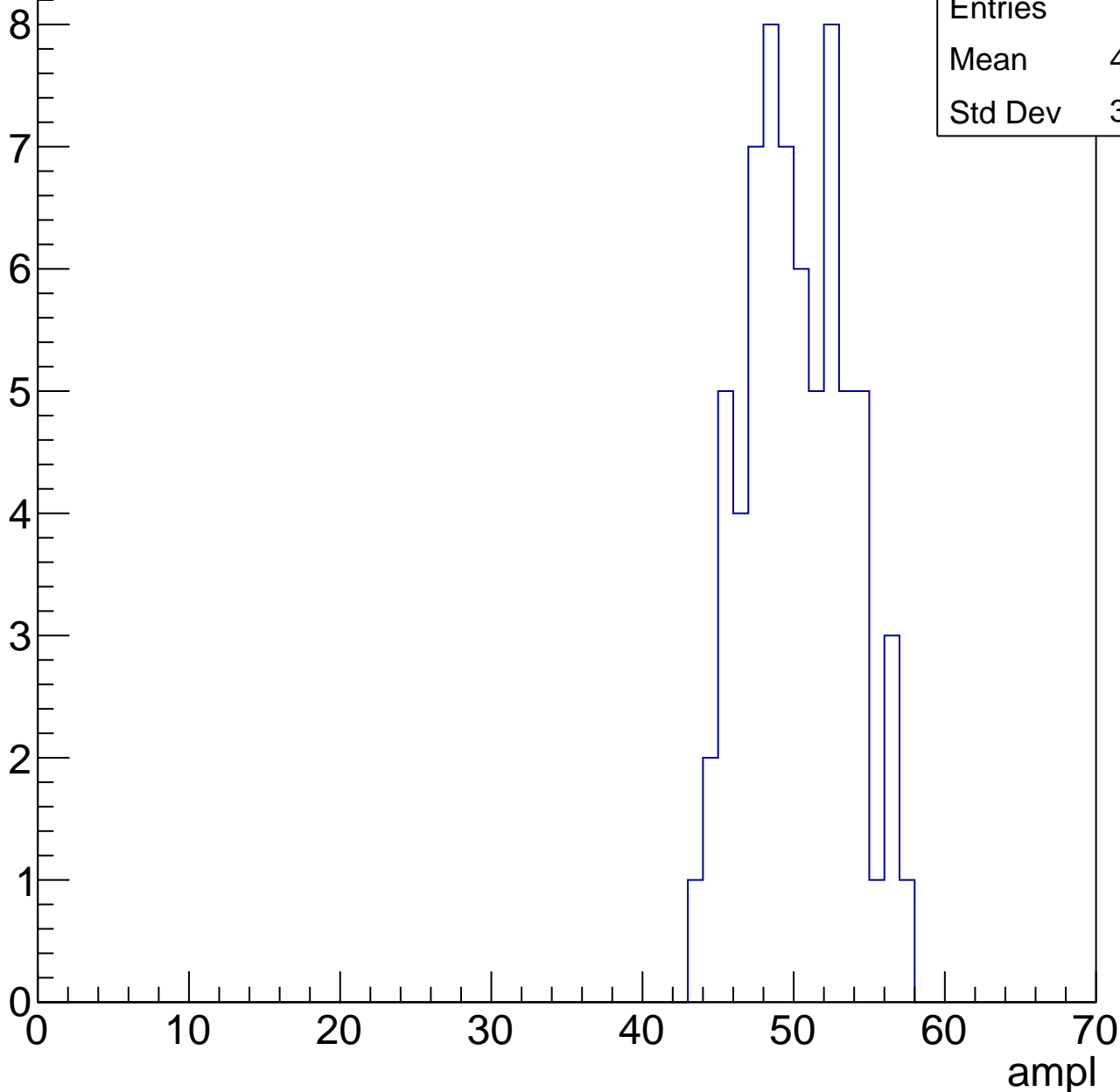


B0L000S, U4-ch72, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.74
Std Dev	3.324

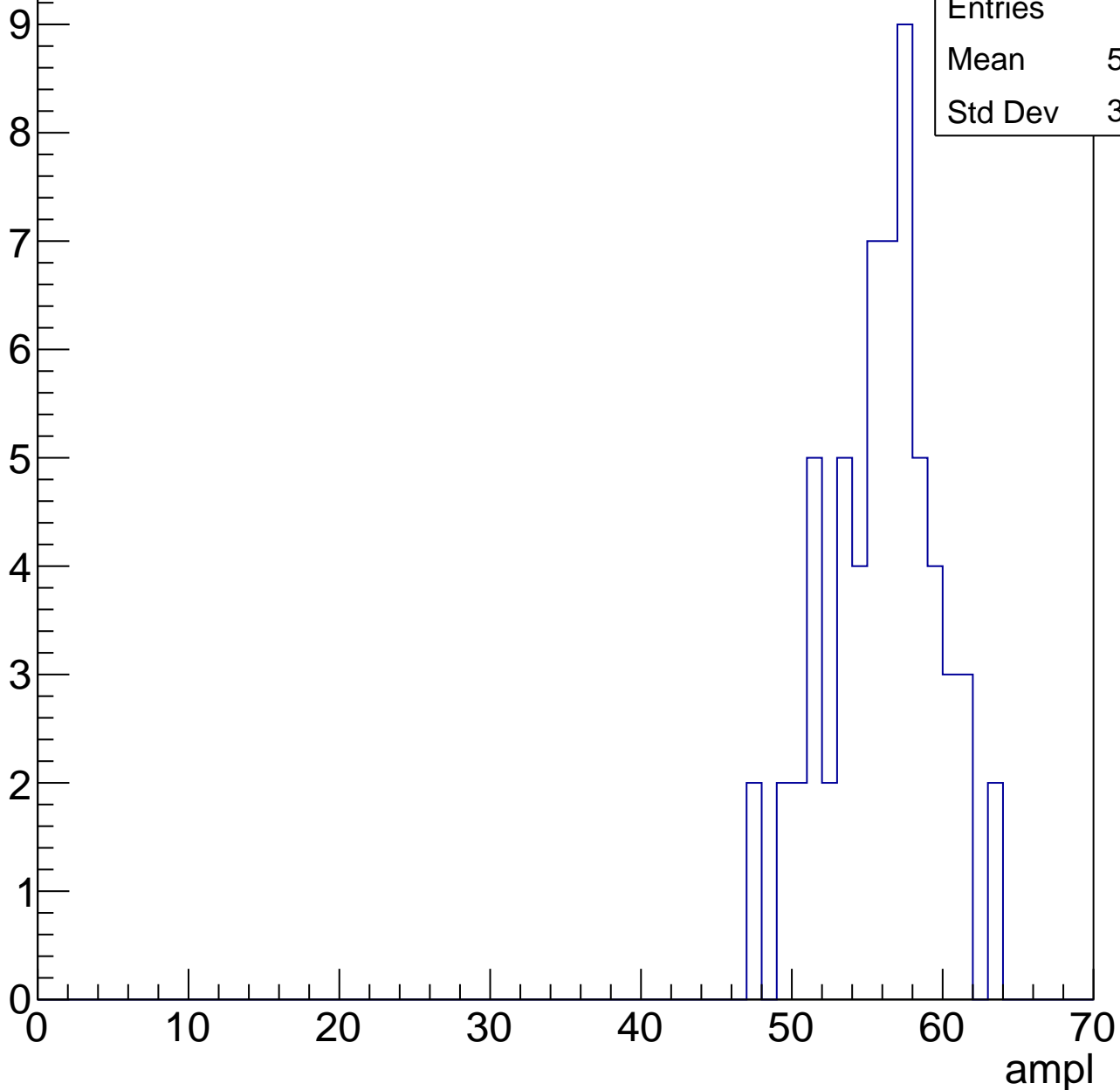


B0L000S, U4-ch72, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.44
Std Dev	3.644



B0L000S, U4-ch72, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

58.38

Std Dev

9.977

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

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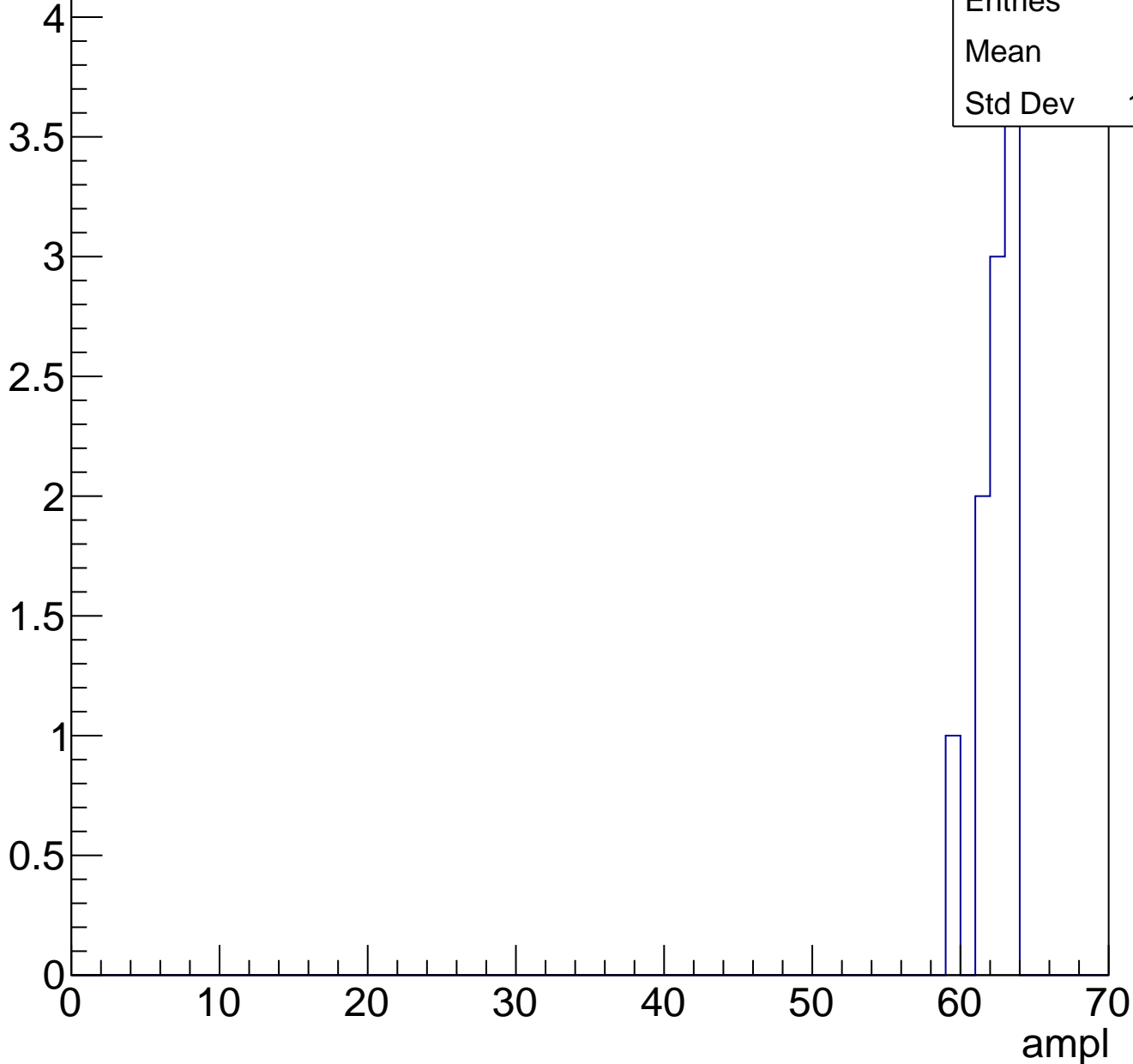
318

319

B0L000S, U4-ch72, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch72, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch73, adc0

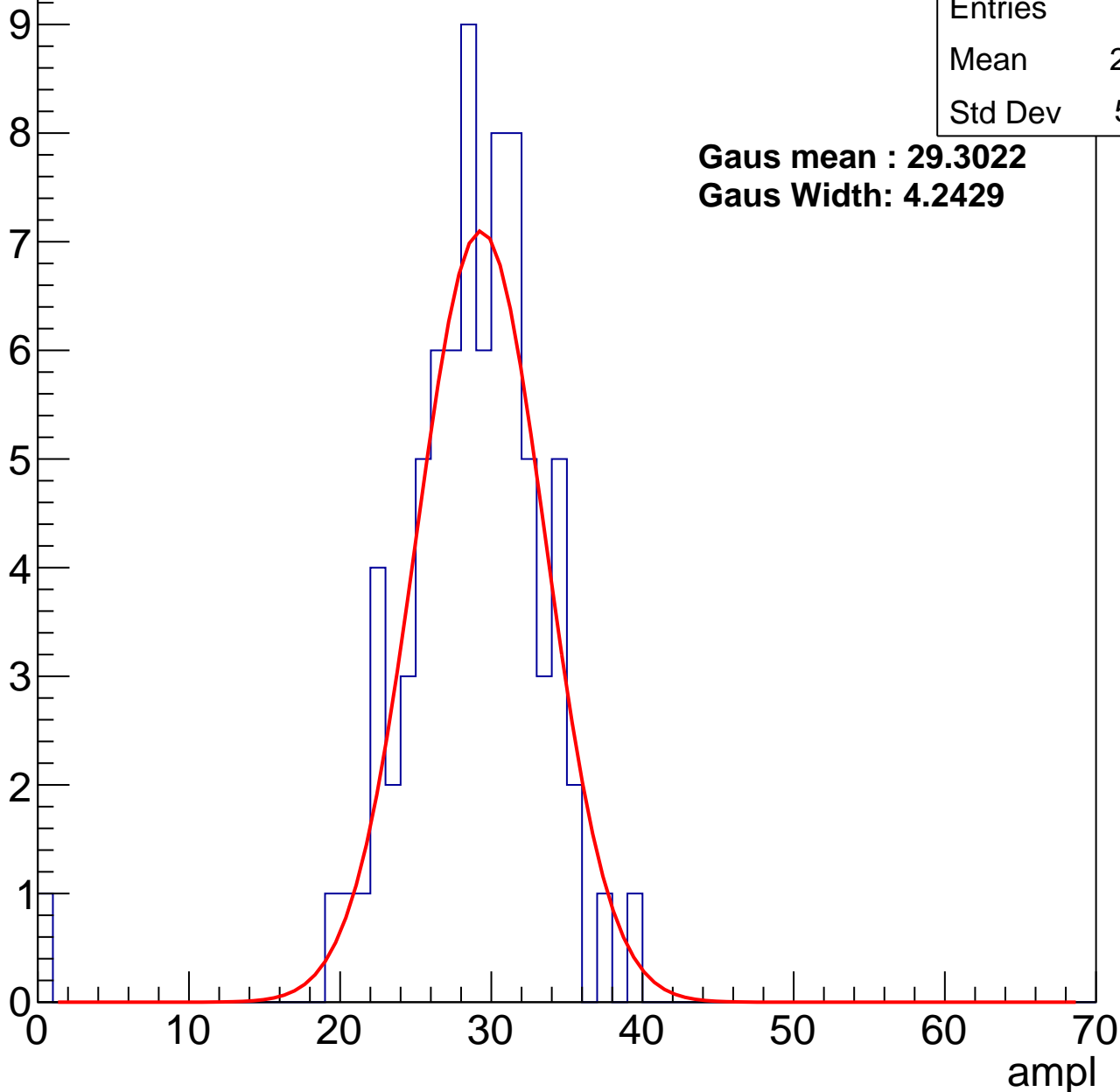
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.18
Std Dev	5.111

Gaus mean : 29.3022

Gaus Width: 4.2429



B0L000S, U4-ch73, adc1

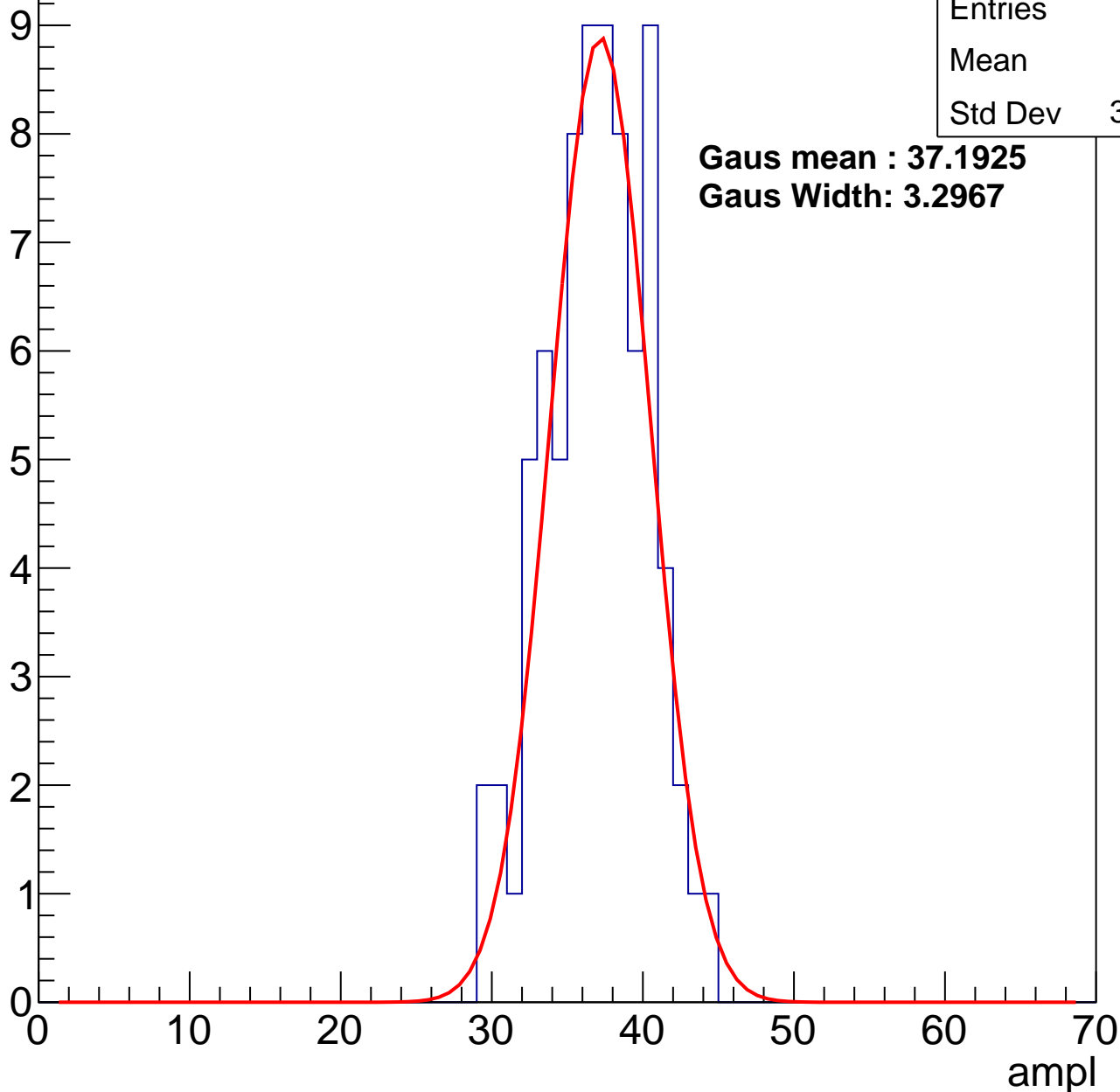
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	36.5
Std Dev	3.323

Gaus mean : 37.1925

Gaus Width: 3.2967



B0L000S, U4-ch73, adc2

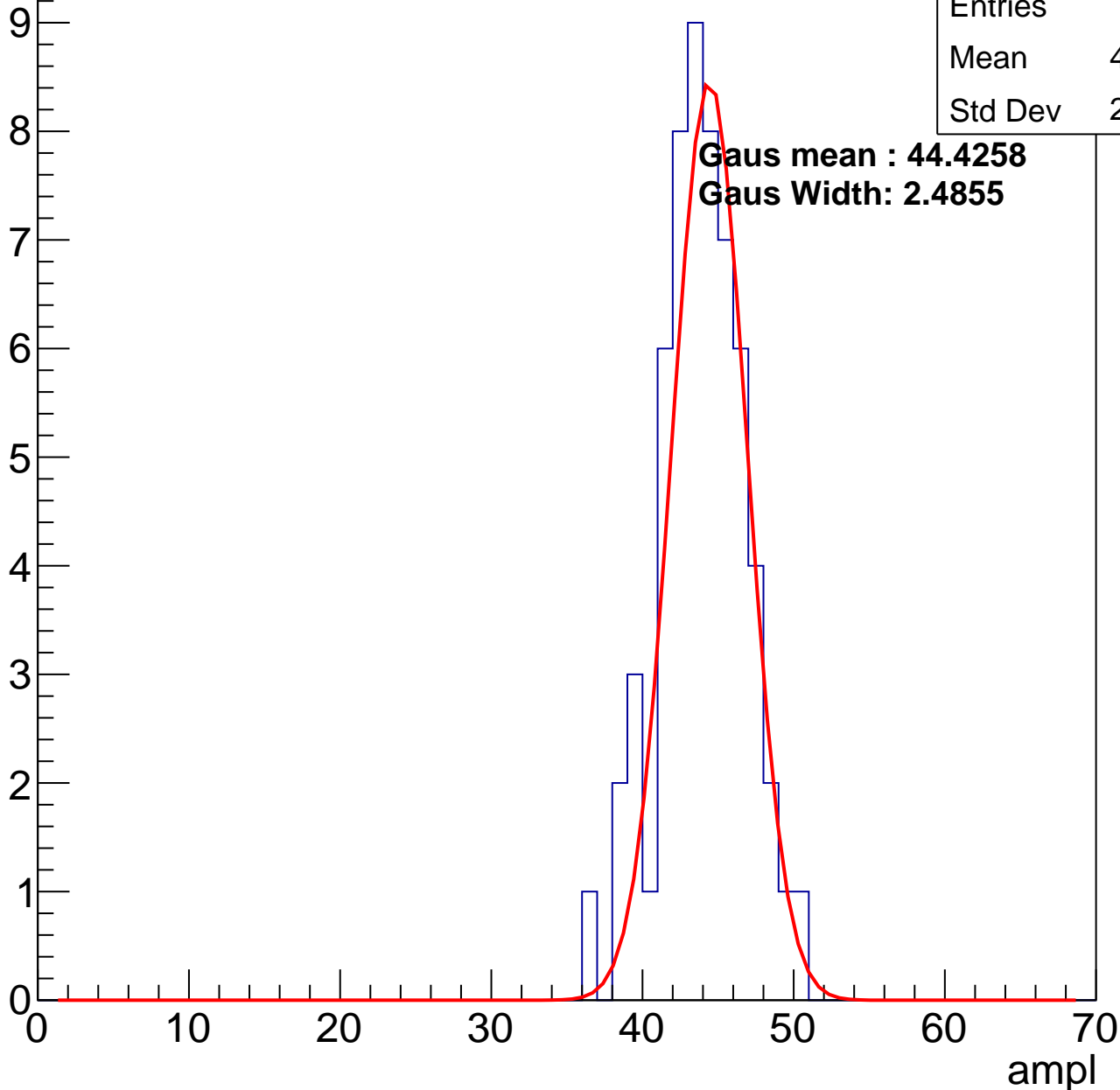
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	43.46
Std Dev	2.806

Gaus mean : 44.4258

Gaus Width: 2.4855

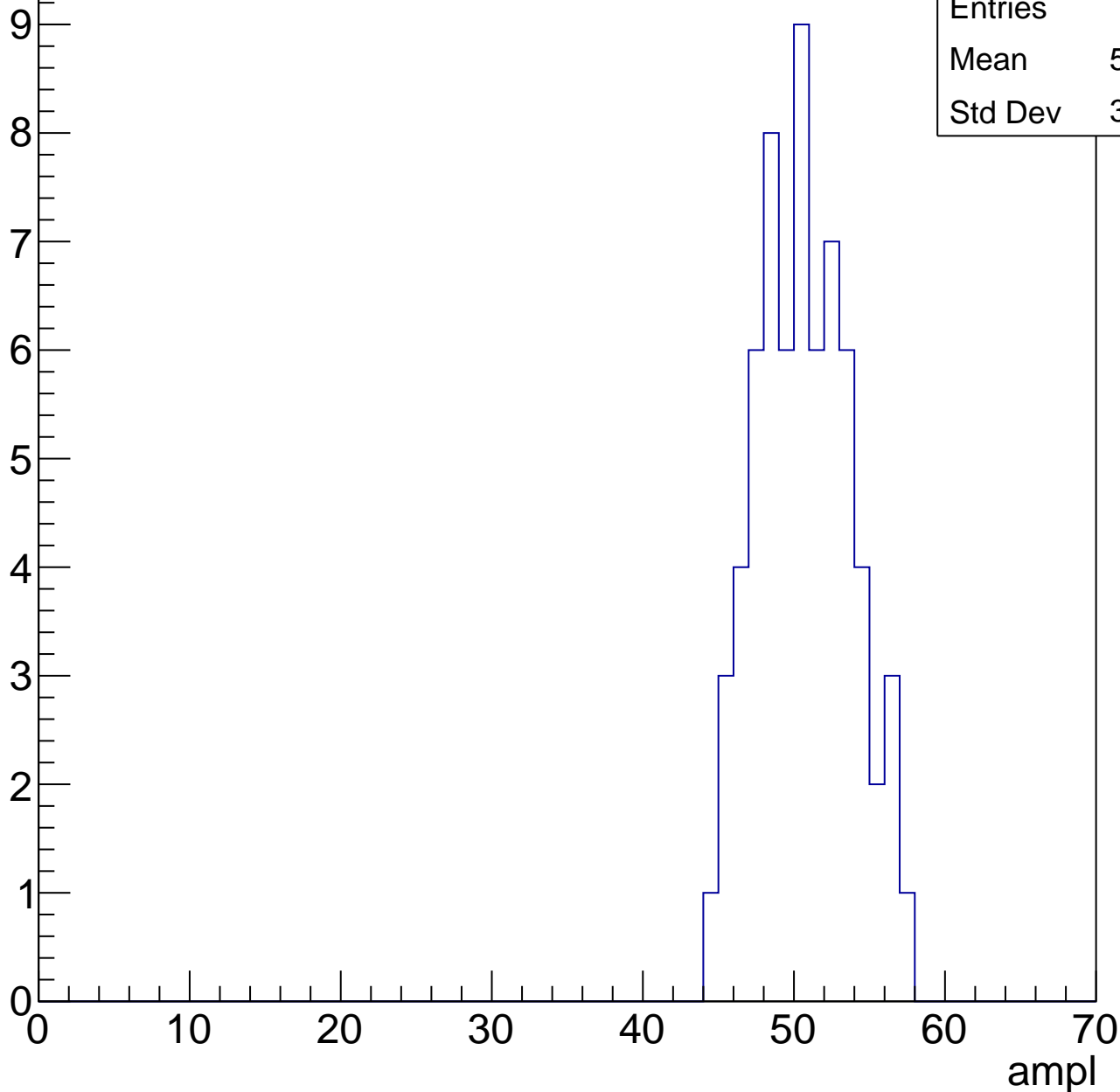


B0L000S, U4-ch73, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

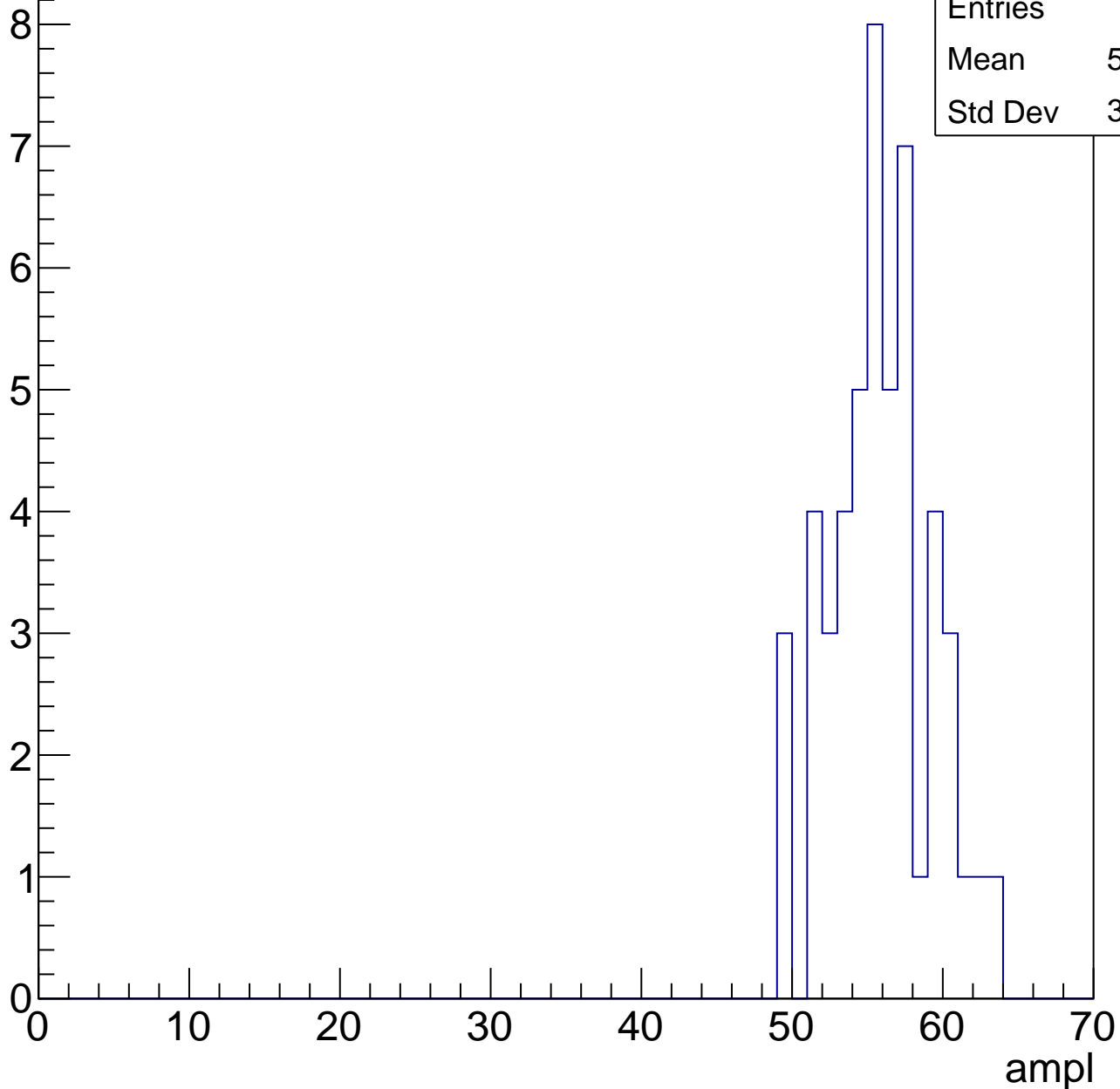
Entries	66
Mean	50.18
Std Dev	3.074



B0L000S, U4-ch73, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

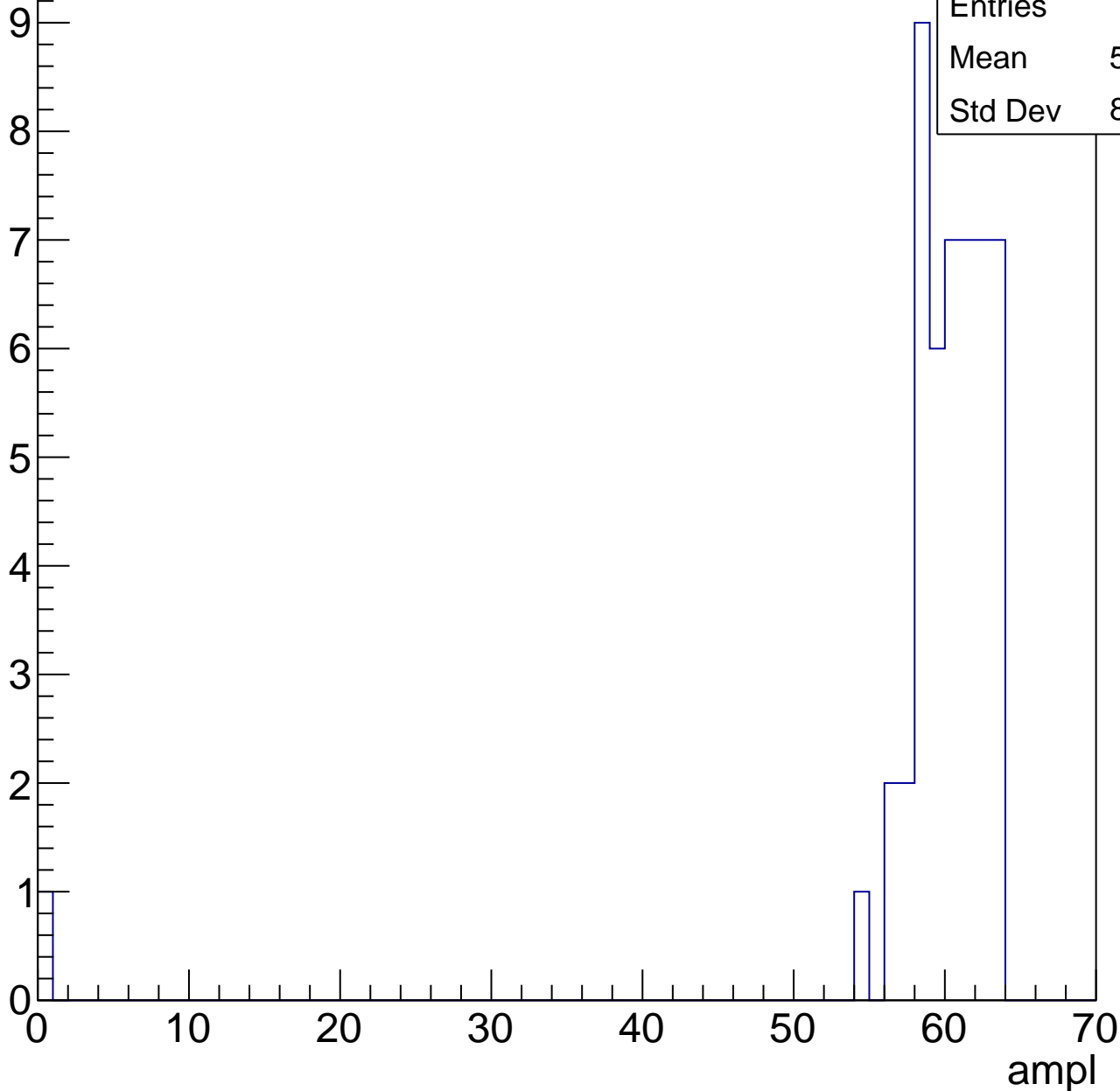


B0L000S, U4-ch73, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	58.73
Std Dev	8.745



B0L000S, U4-ch73, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	5
Mean	61.8
Std Dev	0.7483

B0L000S, U4-ch73, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch74, adc0

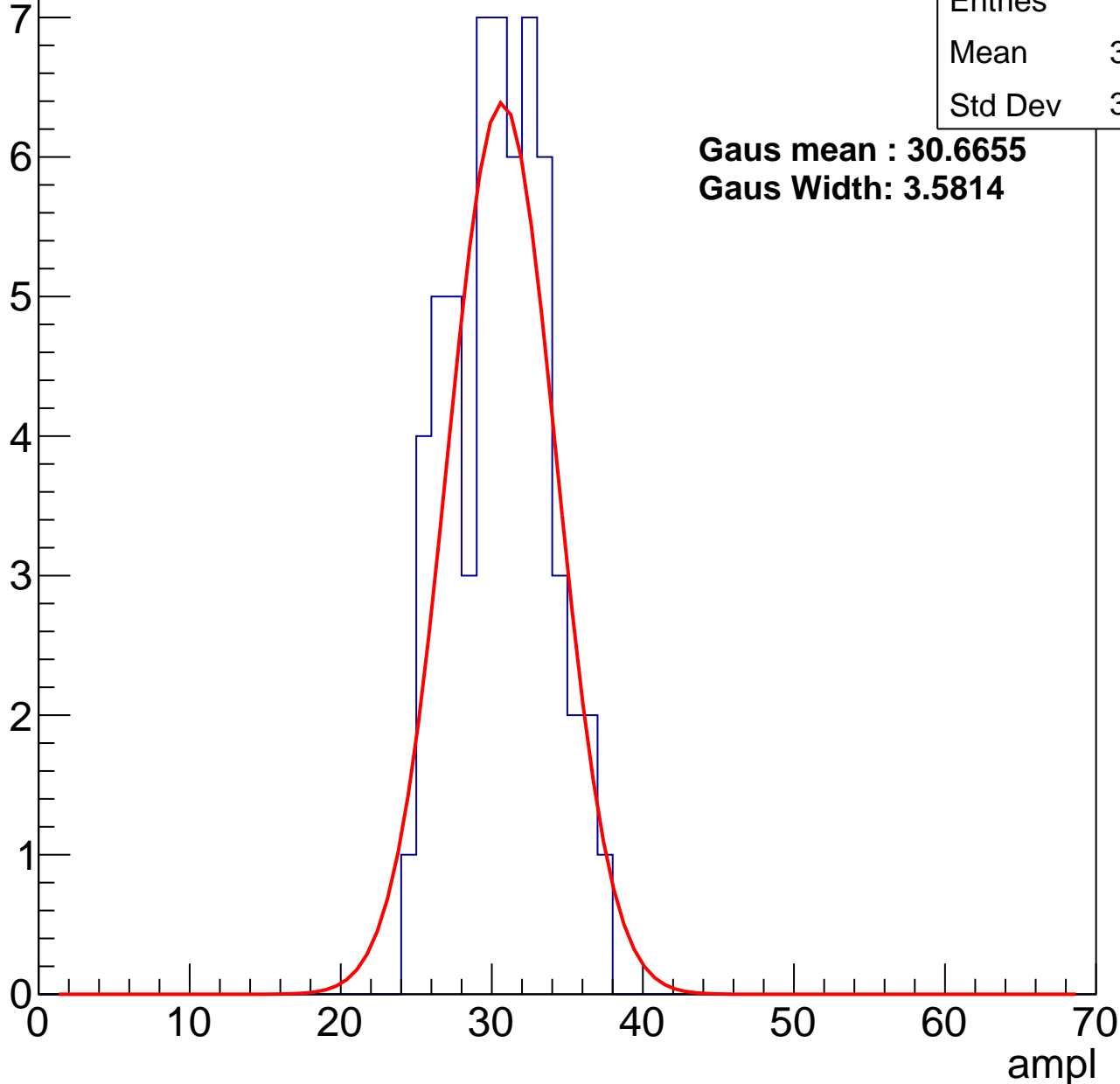
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	30.08
Std Dev	3.153

Gaus mean : 30.6655

Gaus Width: 3.5814



B0L000S, U4-ch74, adc1

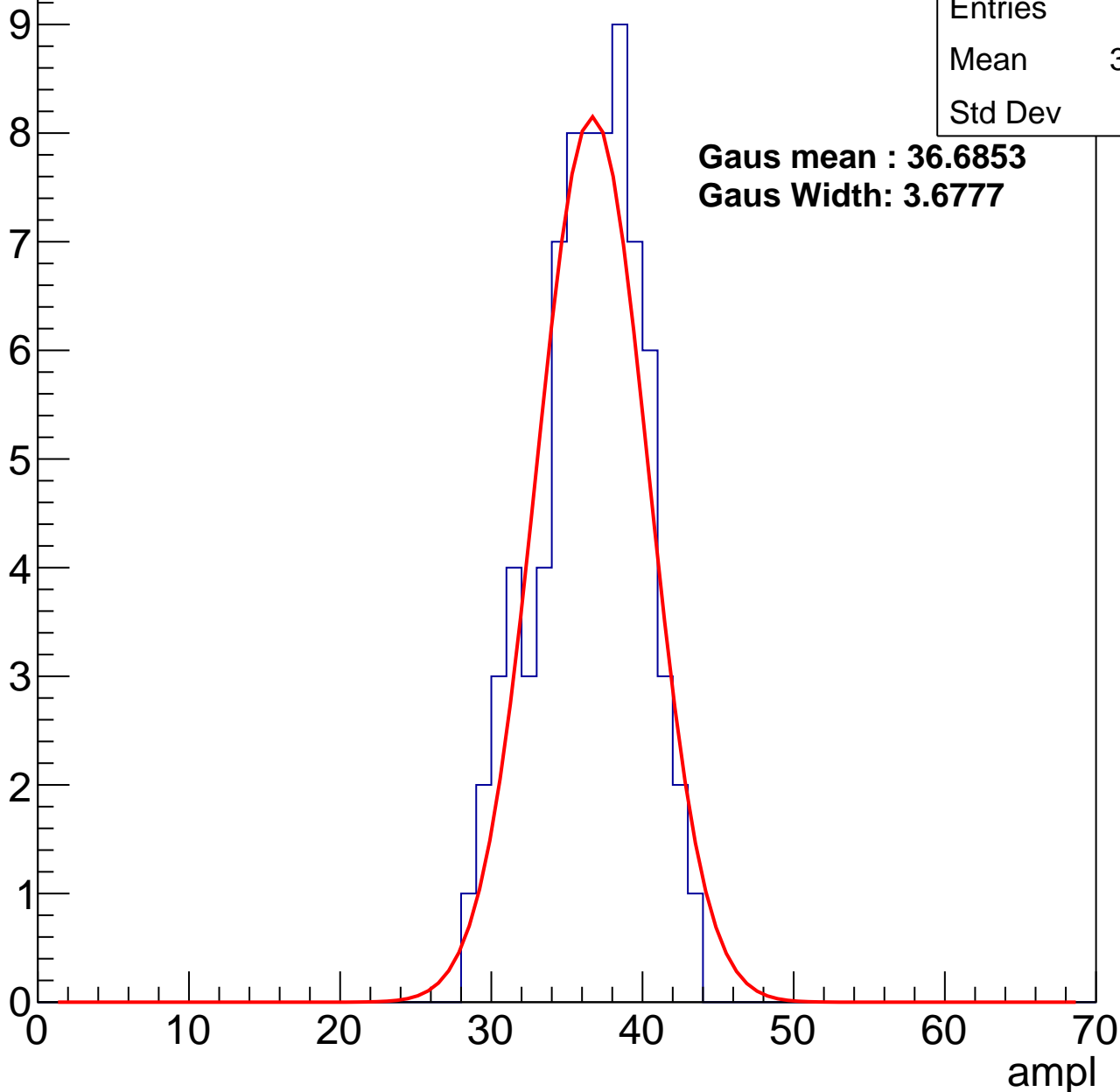
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	35.99
Std Dev	3.42

Gaus mean : 36.6853

Gaus Width: 3.6777



B0L000S, U4-ch74, adc2

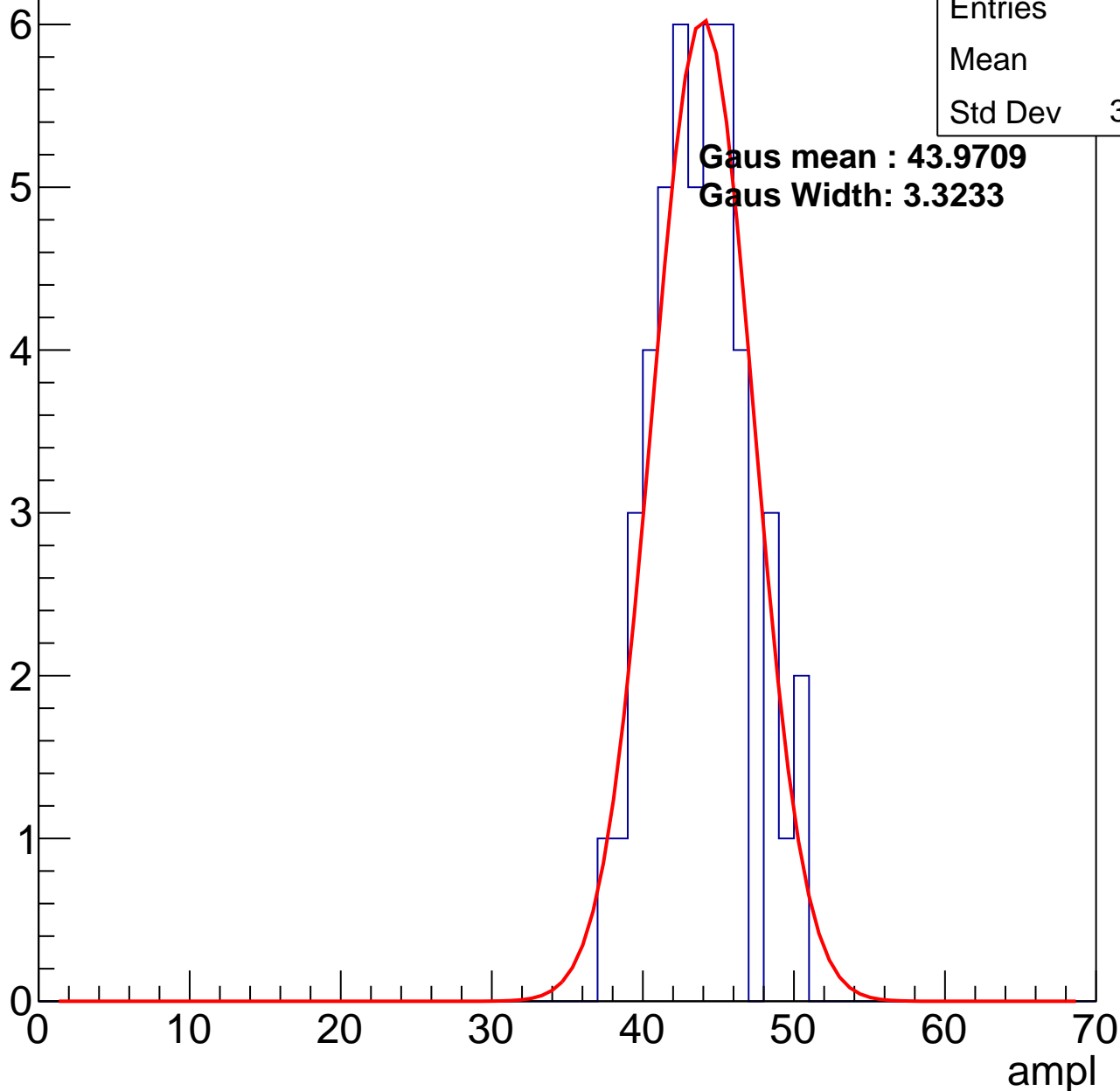
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	43.3
Std Dev	3.066

Gaus mean : 43.9709

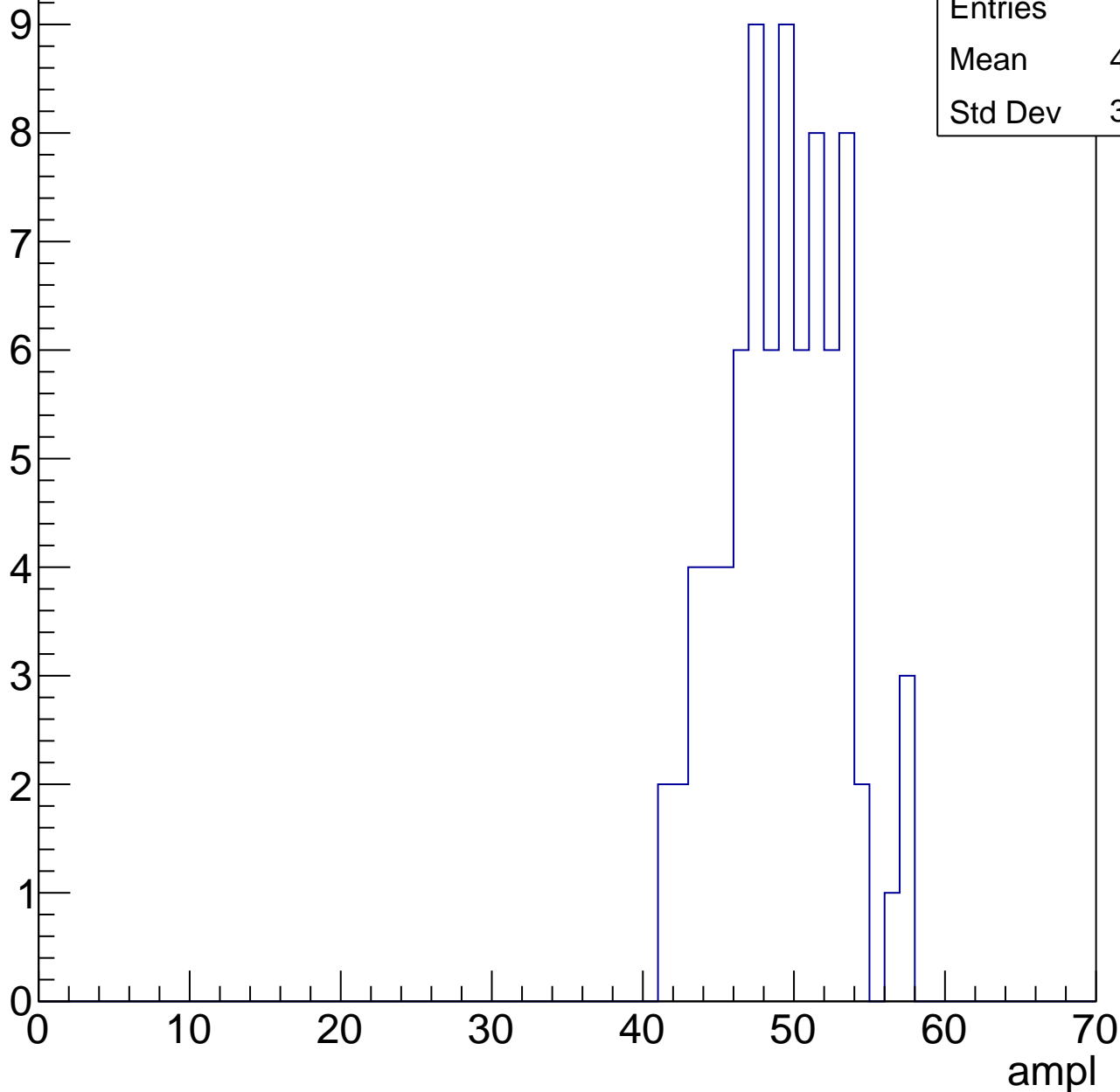
Gaus Width: 3.3233



B0L000S, U4-ch74, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



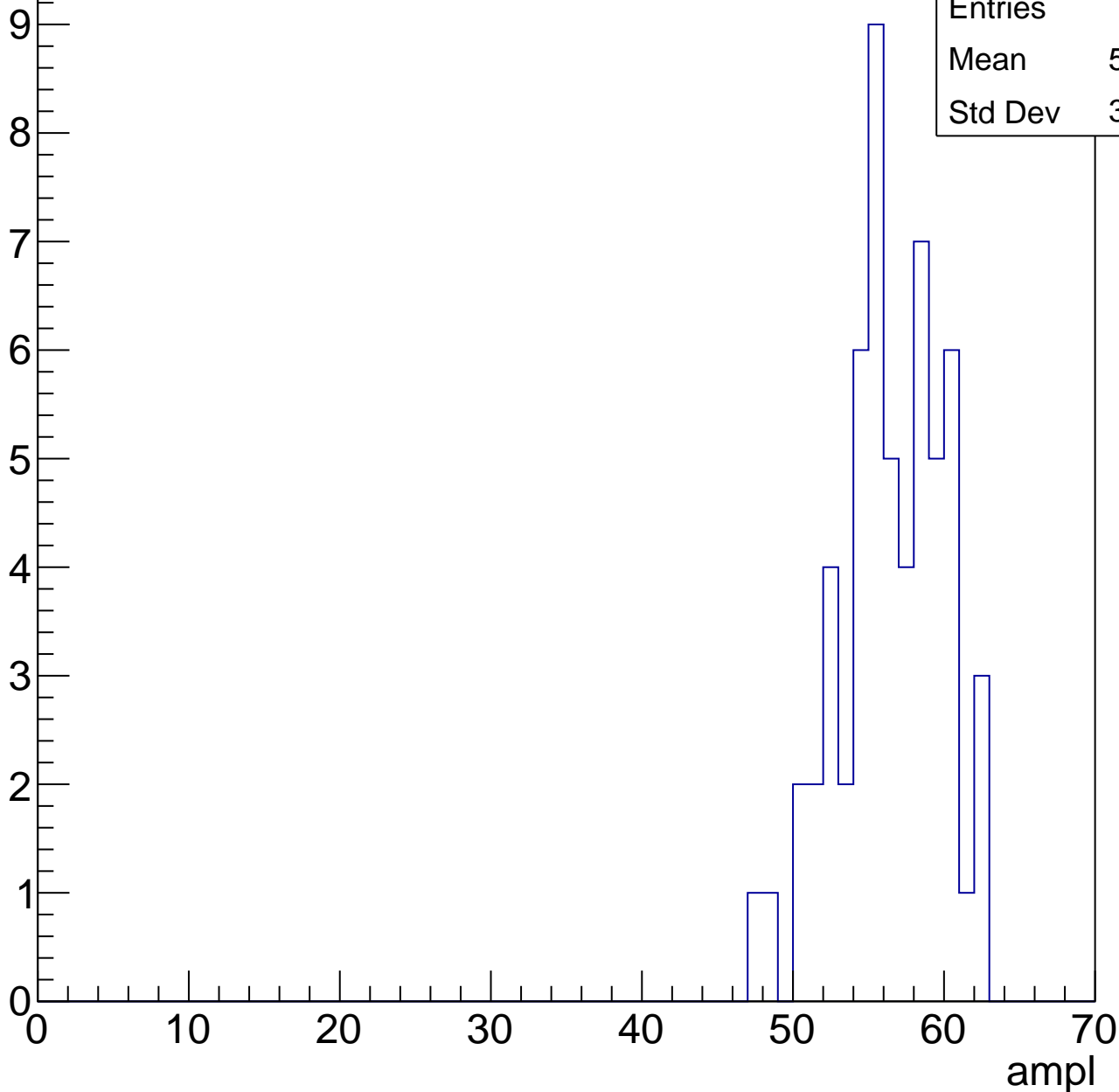
Entries	80
Mean	48.76
Std Dev	3.769

B0L000S, U4-ch74, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.97
Std Dev	3.444

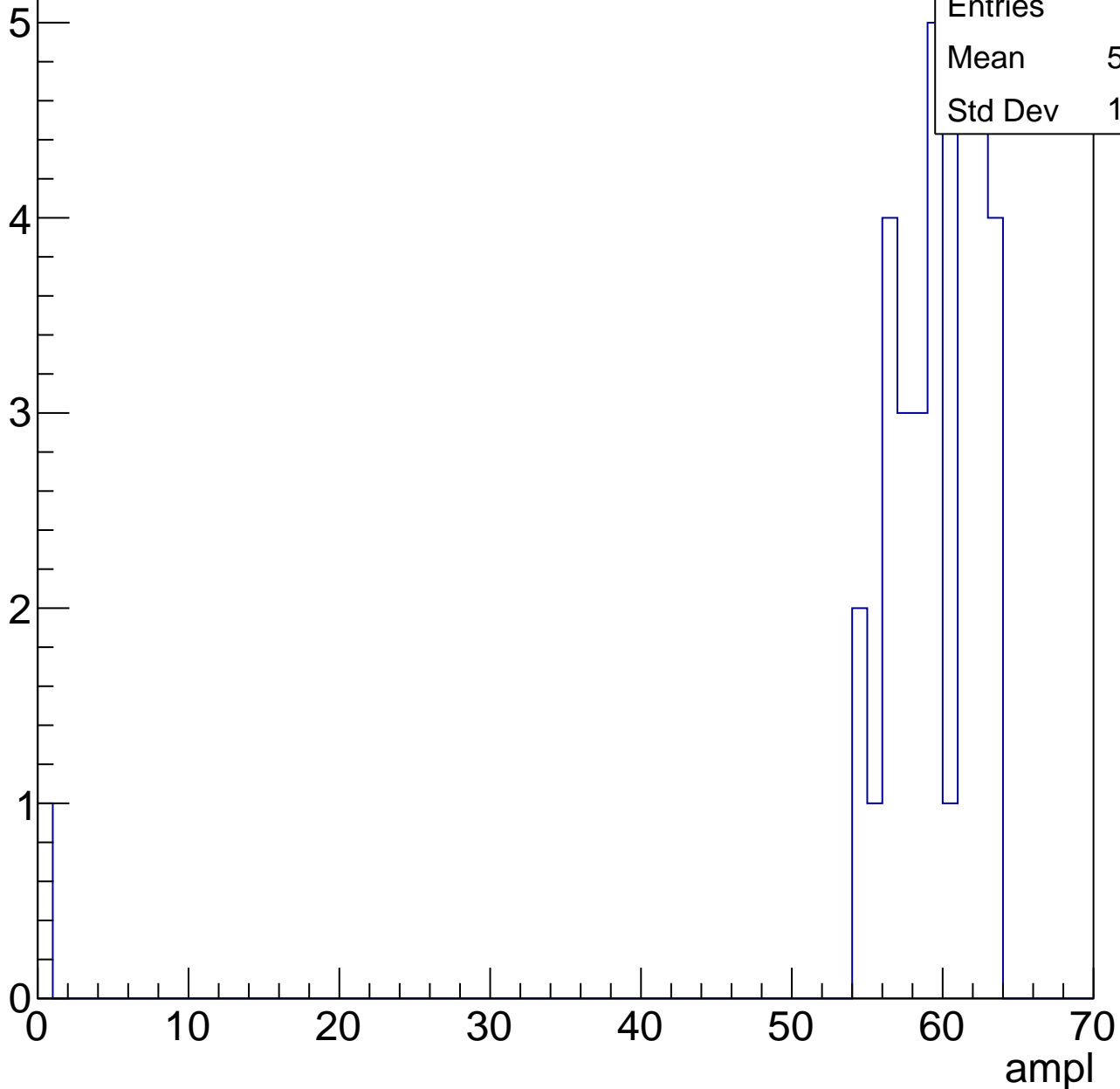


B0L000S, U4-ch74, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	34
Mean	57.47
Std Dev	10.36



B0L000S, U4-ch74, adc6

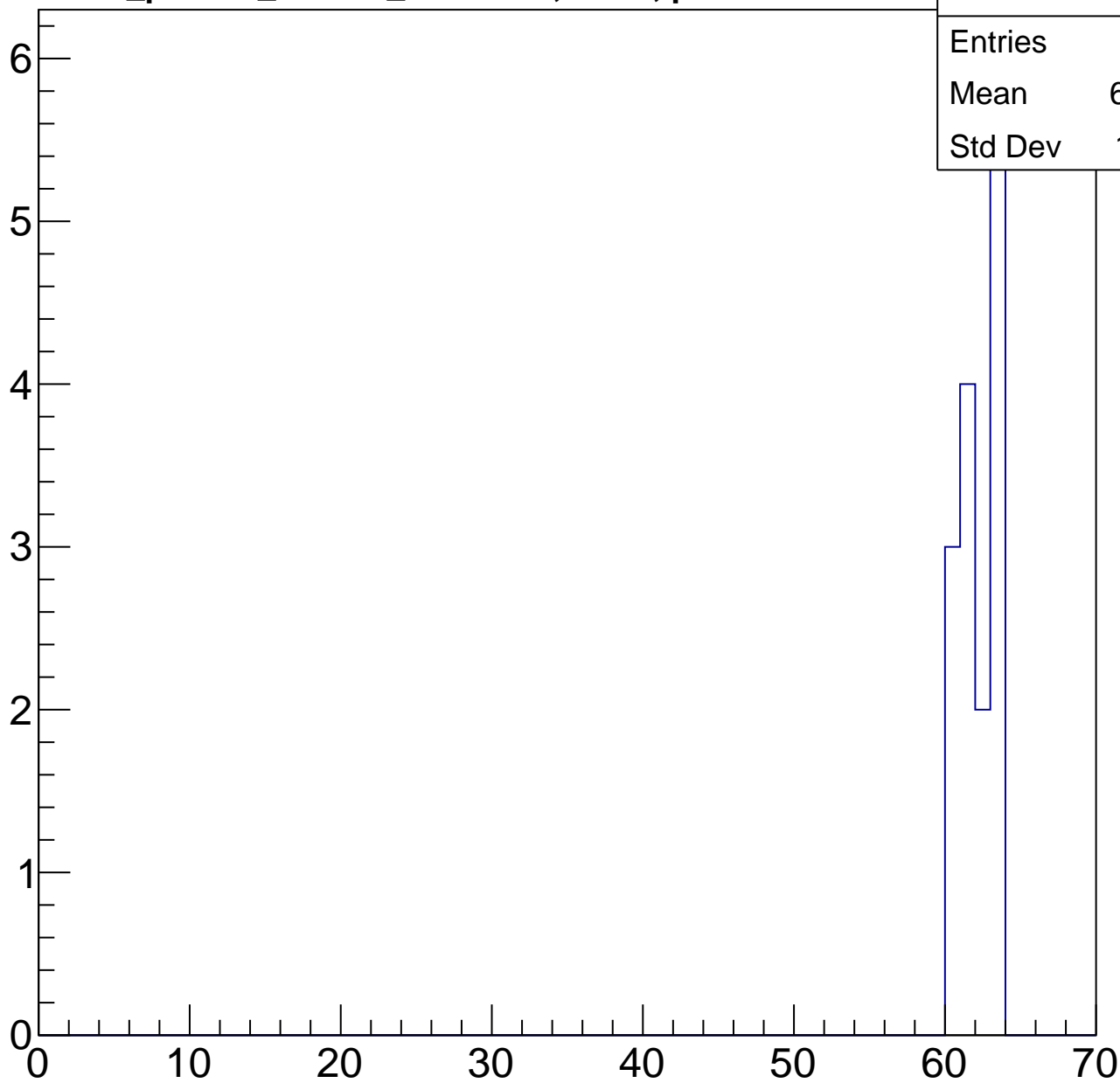
calib_packv5_042523_0143.root, FC#5, port B1

Entry

6
5
4
3
2
1
0

Entries	15
Mean	61.73
Std Dev	1.181

ampl



B0L000S, U4-ch74, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch75, adc0

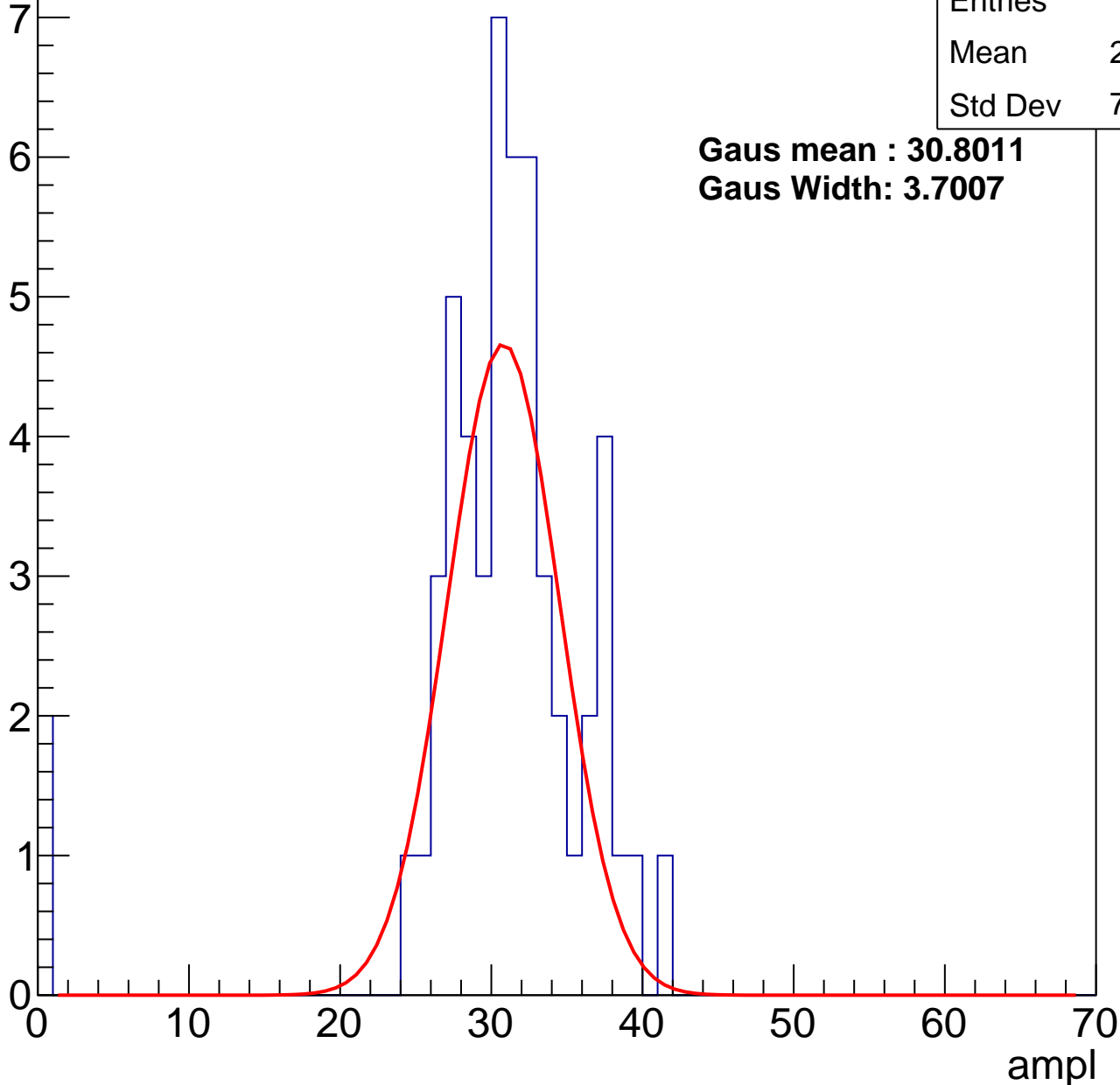
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	29.98
Std Dev	7.032

Gaus mean : 30.8011

Gaus Width: 3.7007



B0L000S, U4-ch75, adc1

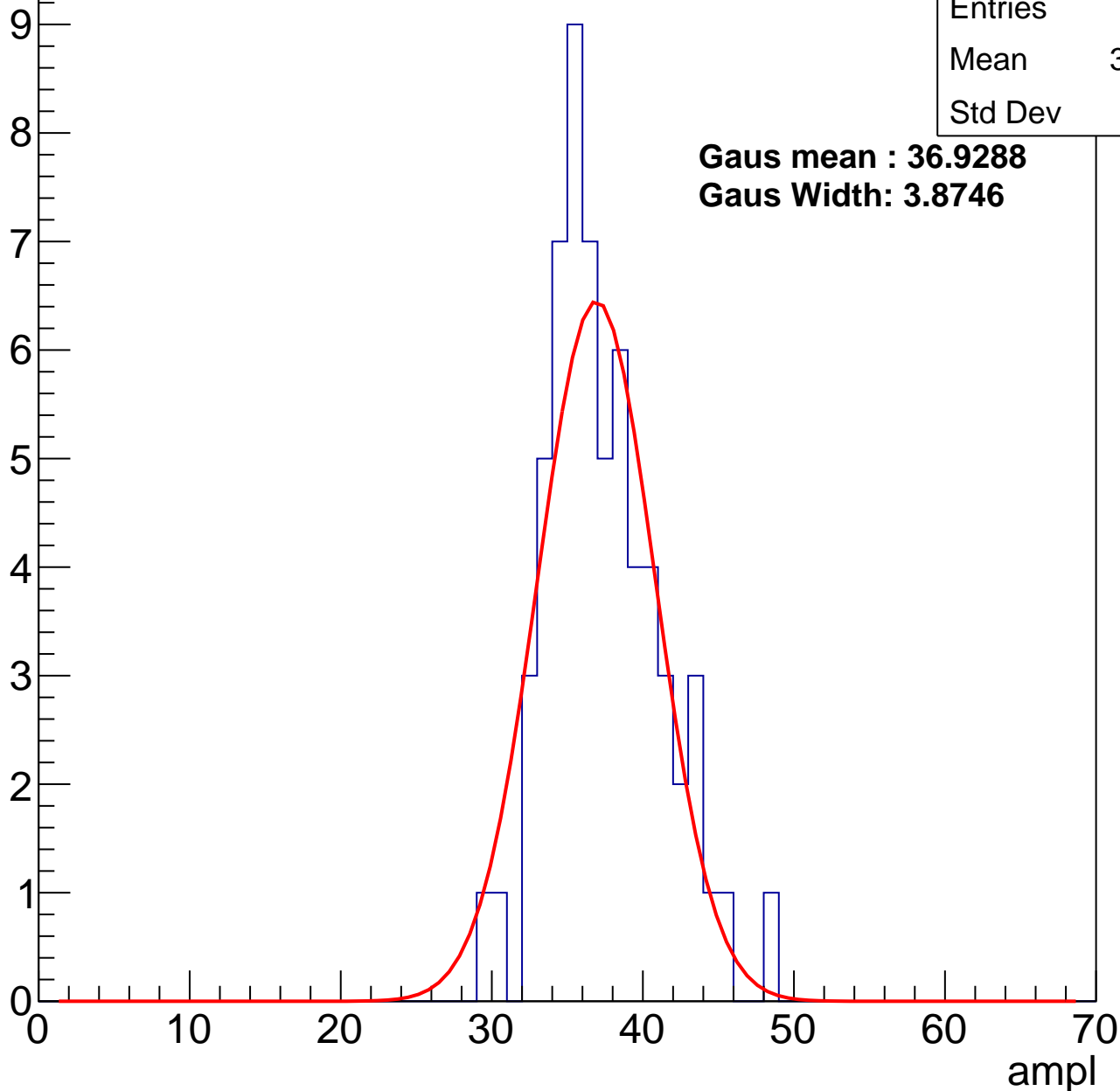
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.94
Std Dev	3.72

Gaus mean : 36.9288

Gaus Width: 3.8746



B0L000S, U4-ch75, adc2

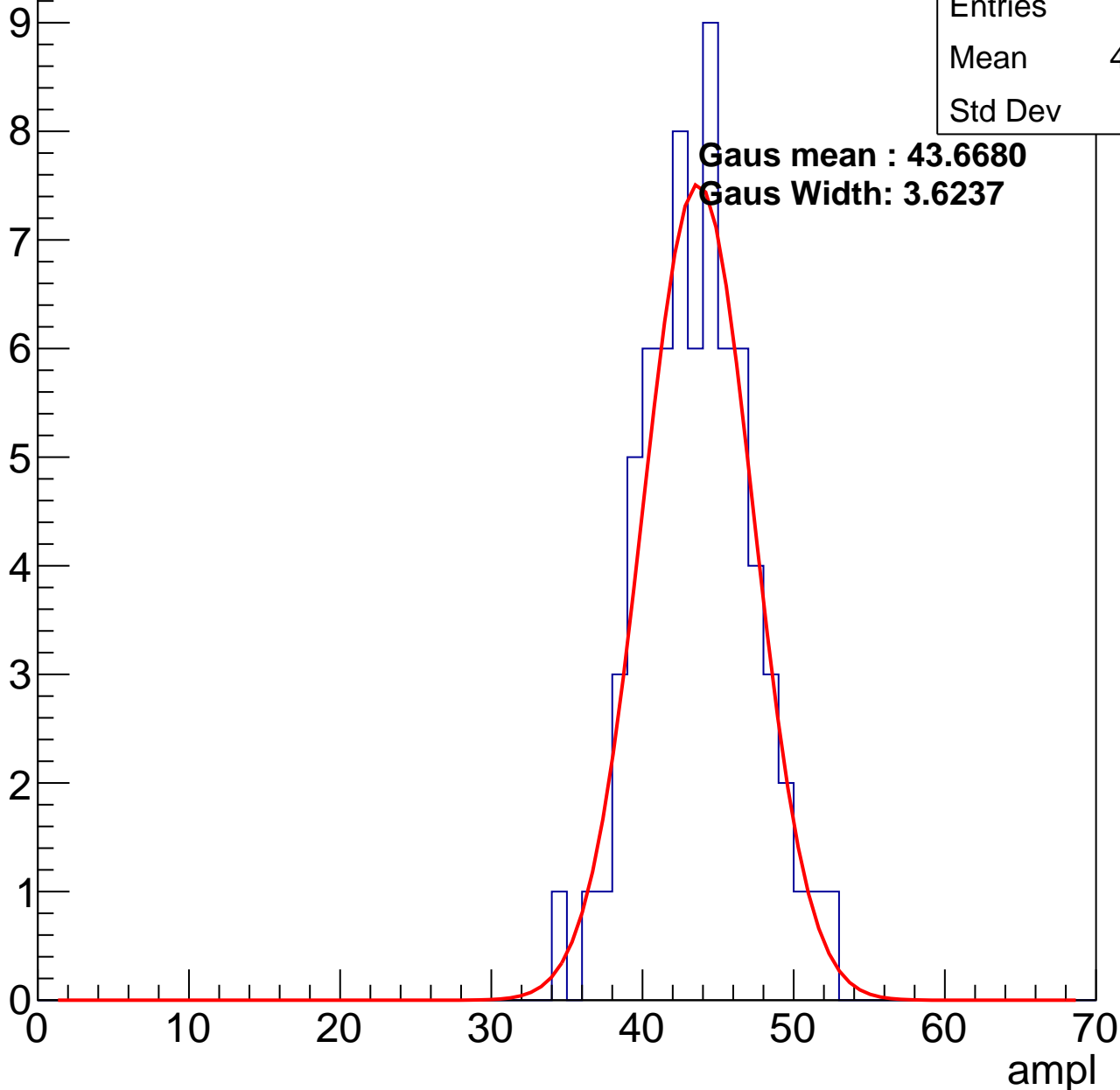
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.16
Std Dev	3.58

Gaus mean : 43.6680

Gaus Width: 3.6237

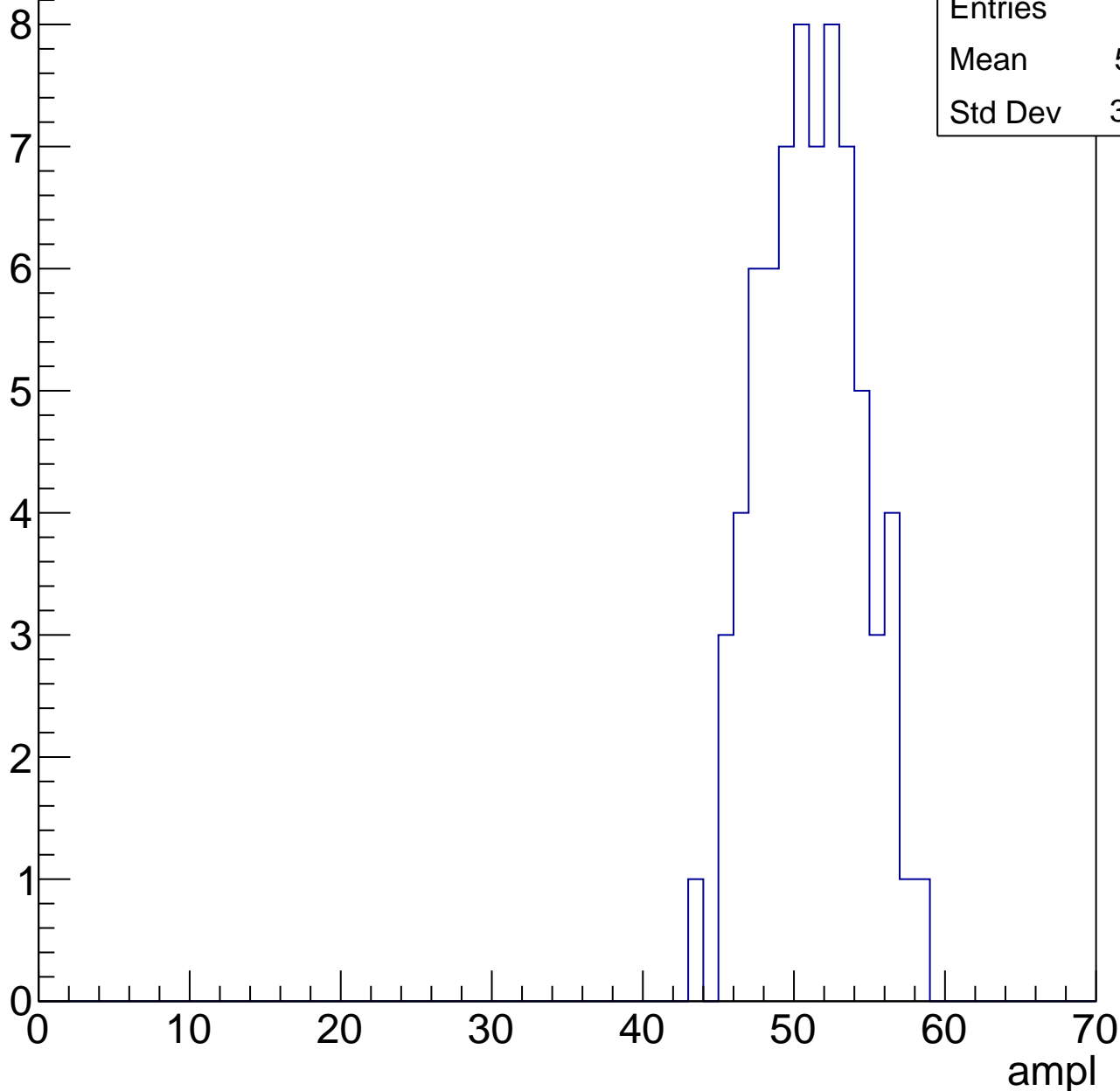


B0L000S, U4-ch75, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	50.61
Std Dev	3.274

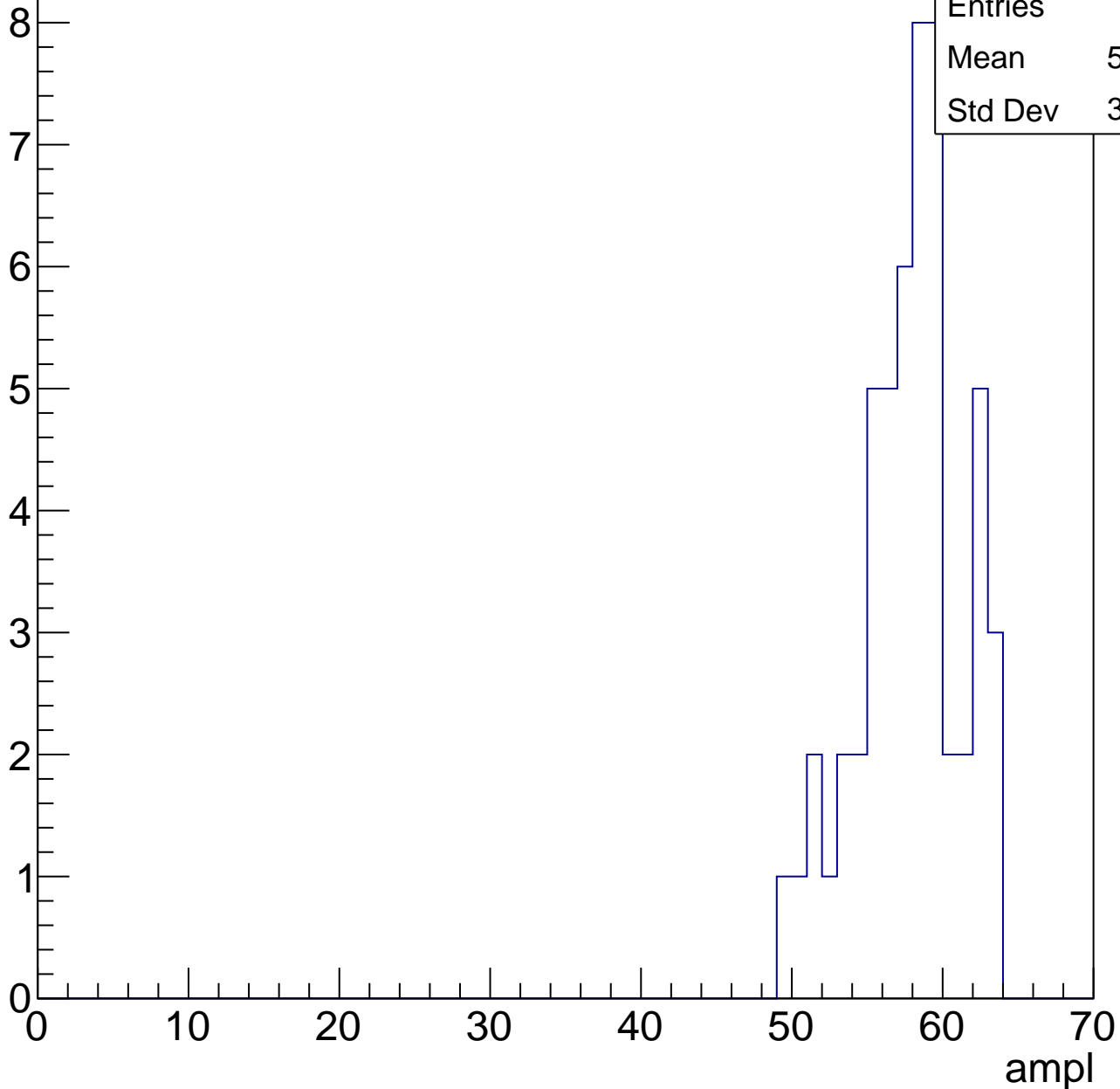


B0L000S, U4-ch75, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.38
Std Dev	3.366



B0L000S, U4-ch75, adc5

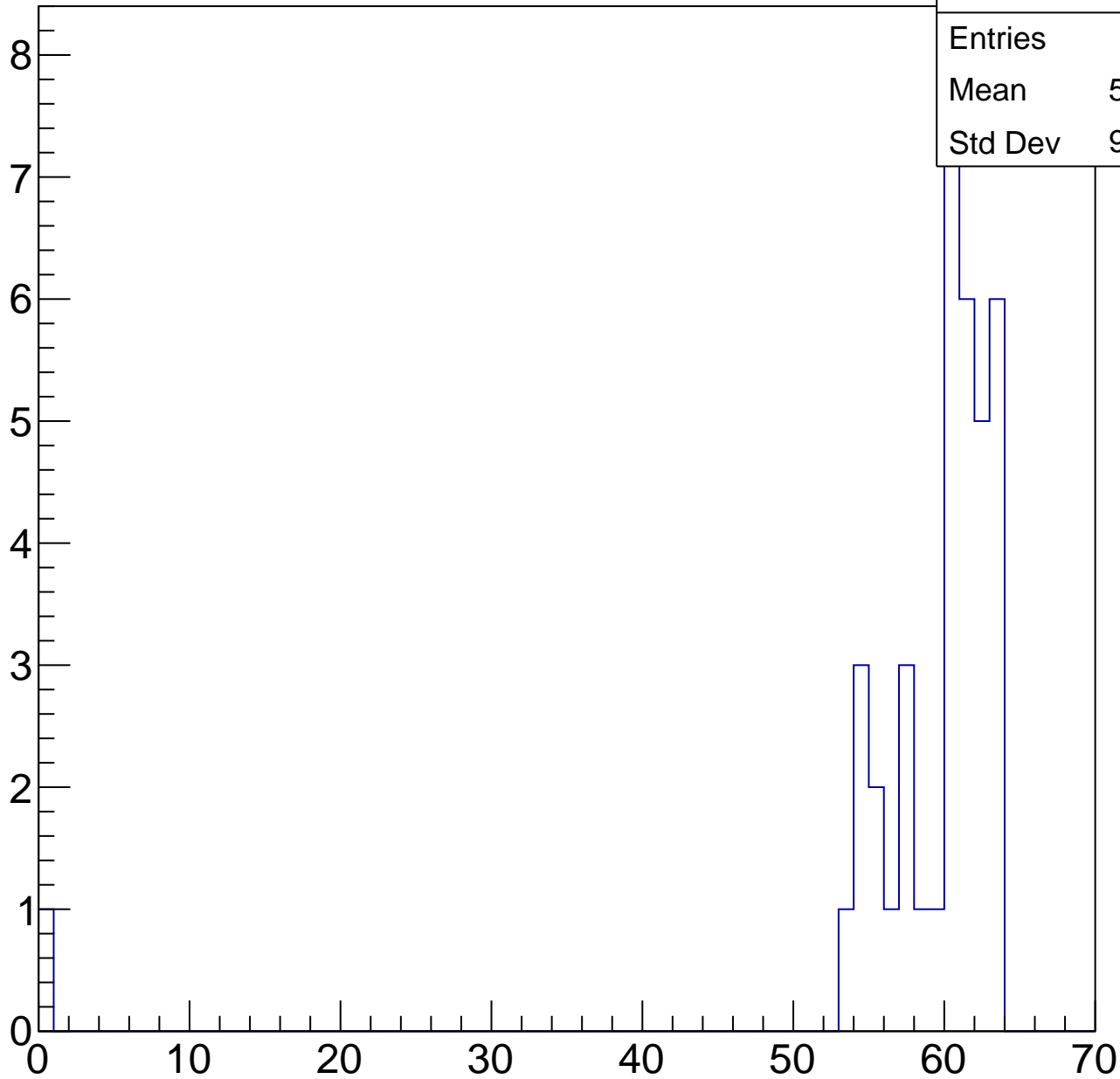
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	57.97
Std Dev	9.972

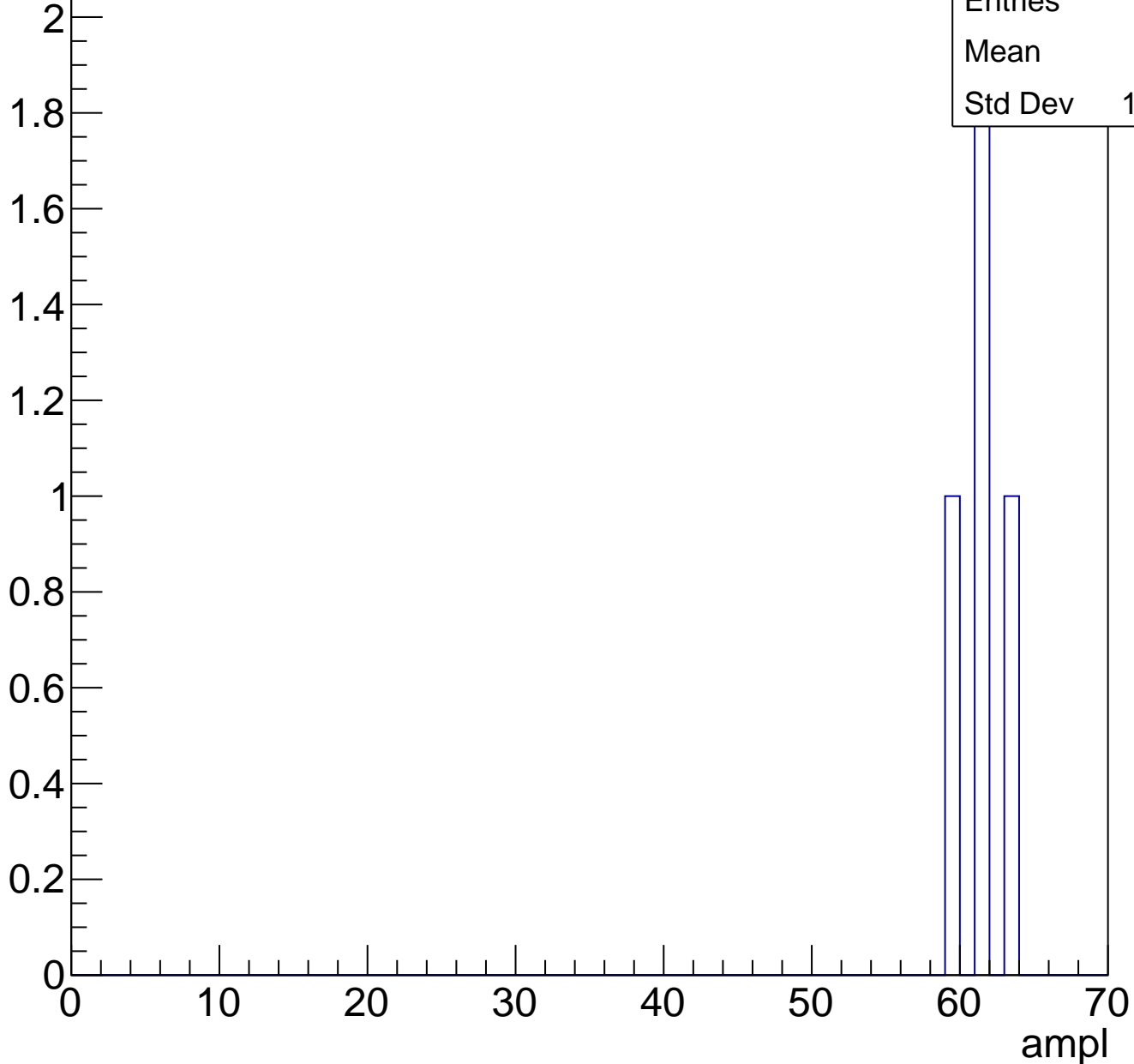
ampl



B0L000S, U4-ch75, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch75, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch76, adc0

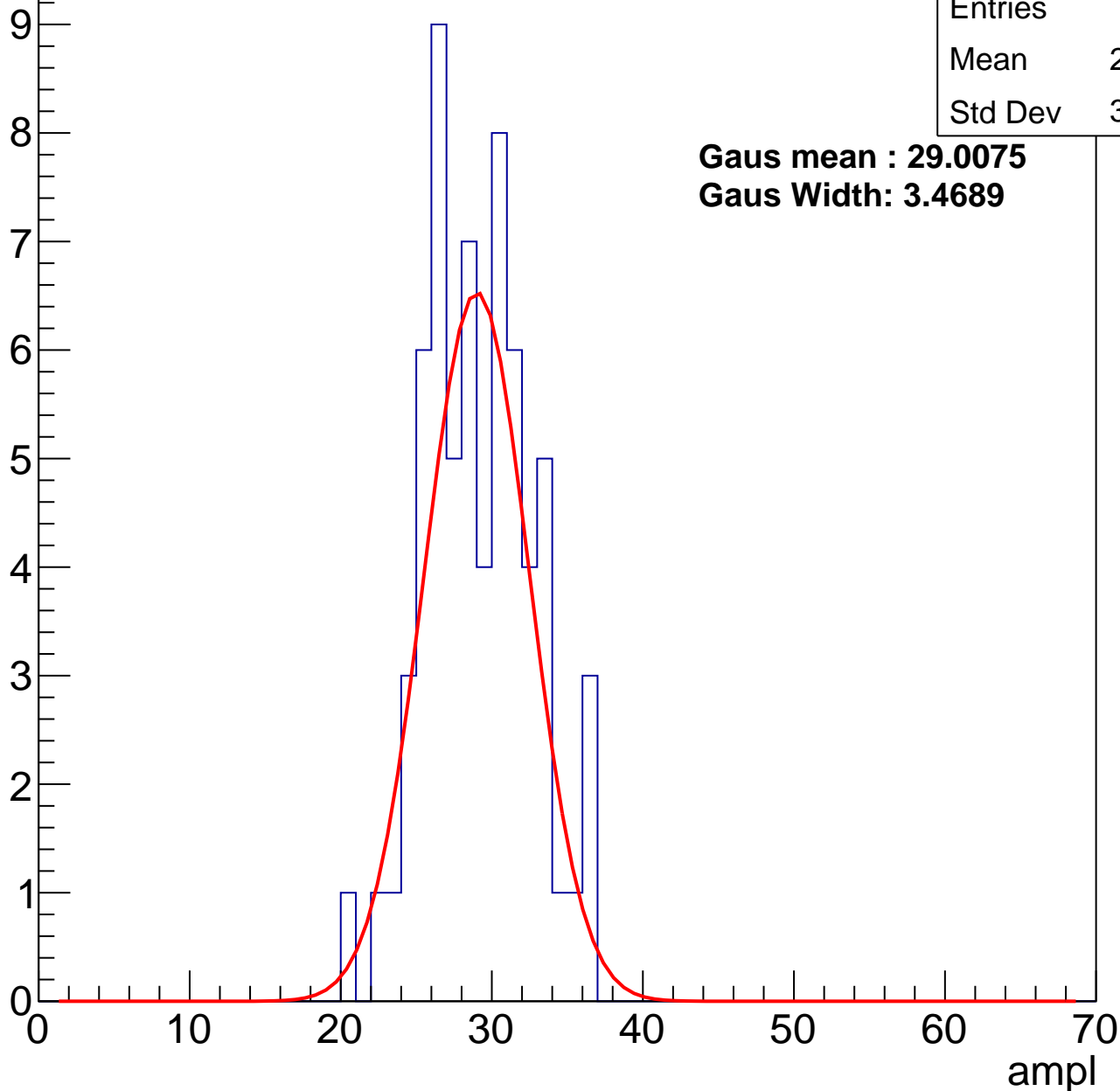
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	28.68
Std Dev	3.496

Gaus mean : 29.0075

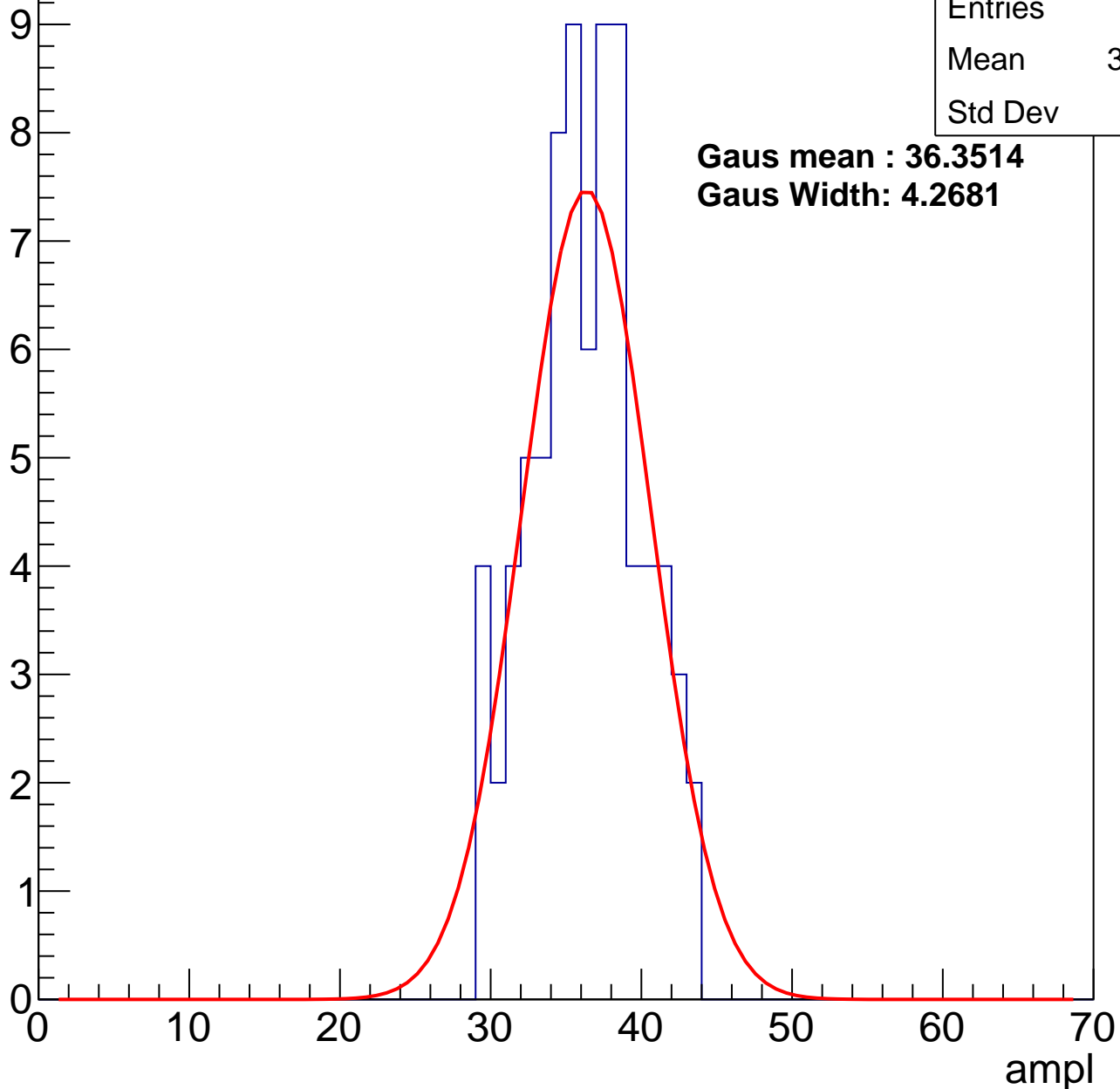
Gaus Width: 3.4689



B0L000S, U4-ch76, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch76, adc2

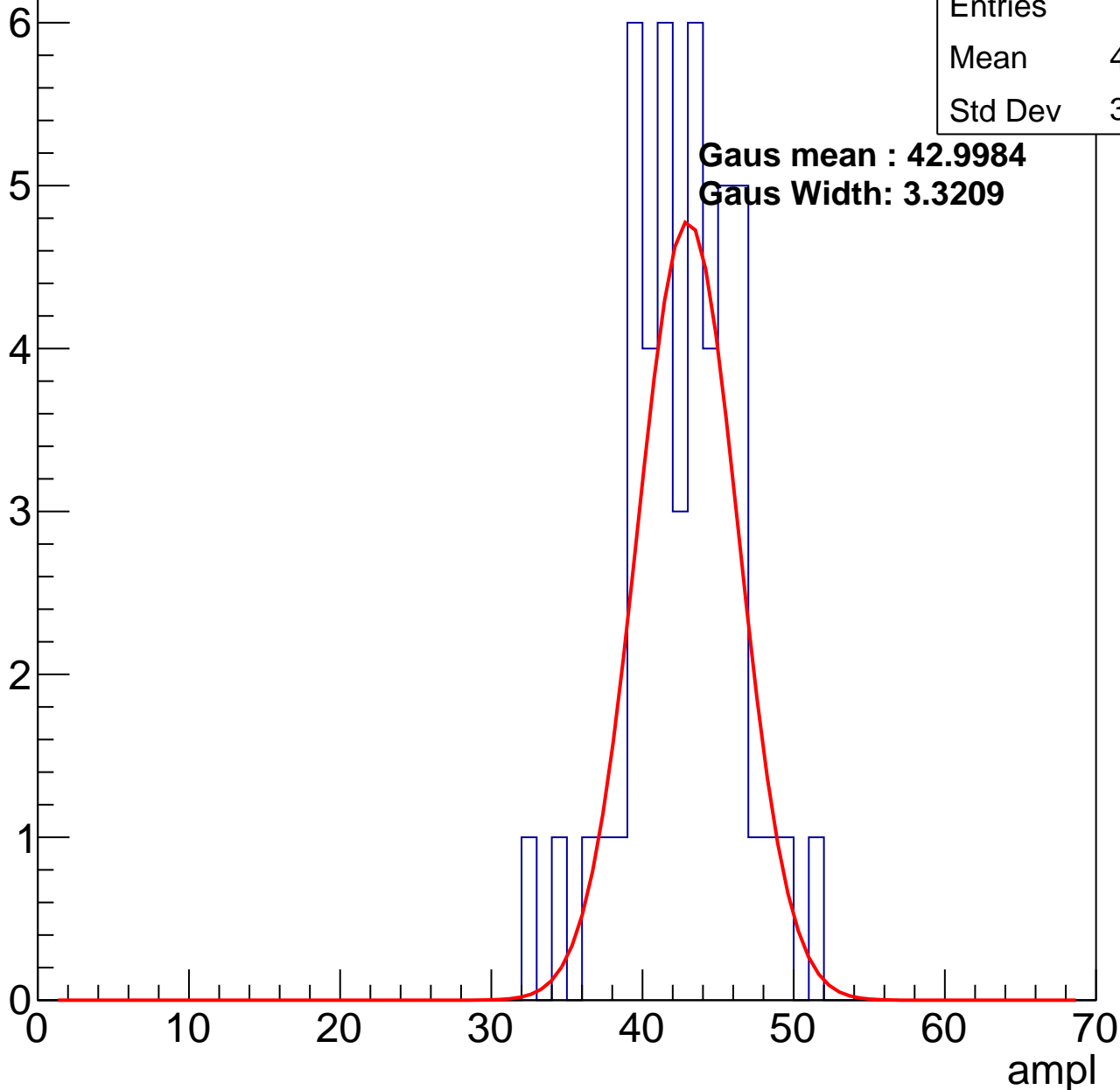
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	42.23
Std Dev	3.687

Gaus mean : 42.9984

Gaus Width: 3.3209

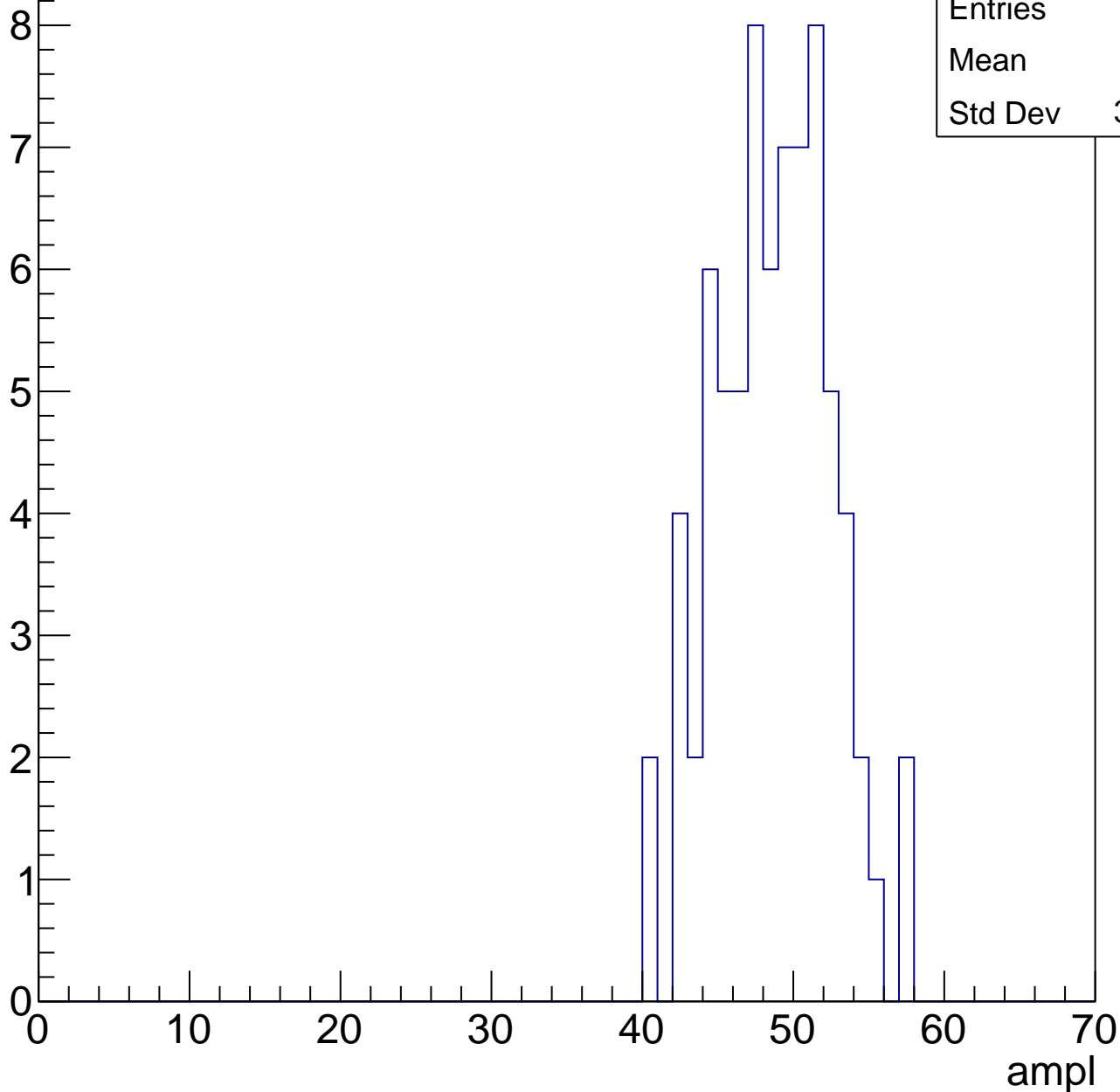


B0L000S, U4-ch76, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

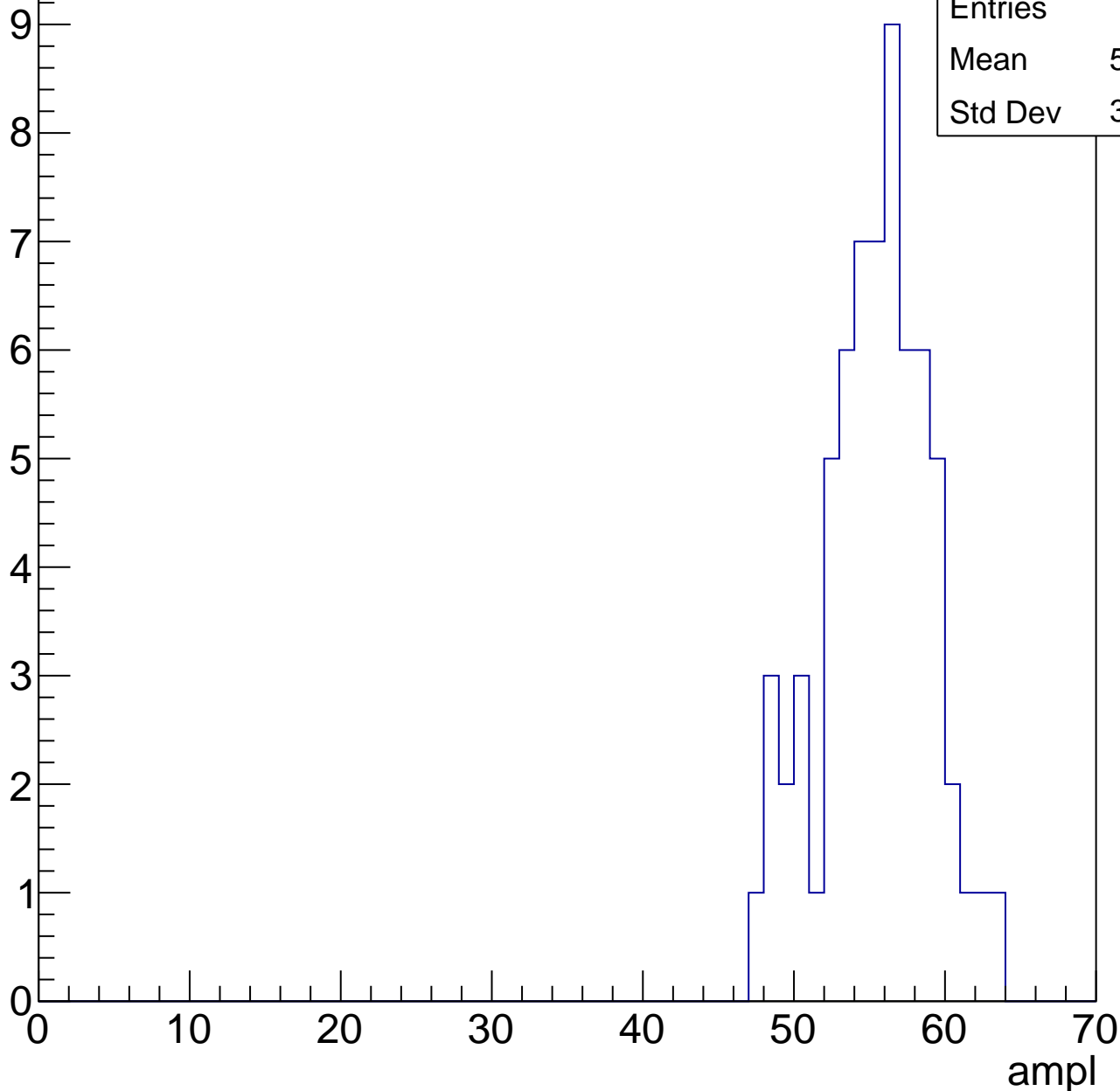
Entries	74
Mean	48.2
Std Dev	3.781



B0L000S, U4-ch76, adc4

calib_packv5_042523_0143.root, FC#5, port B1

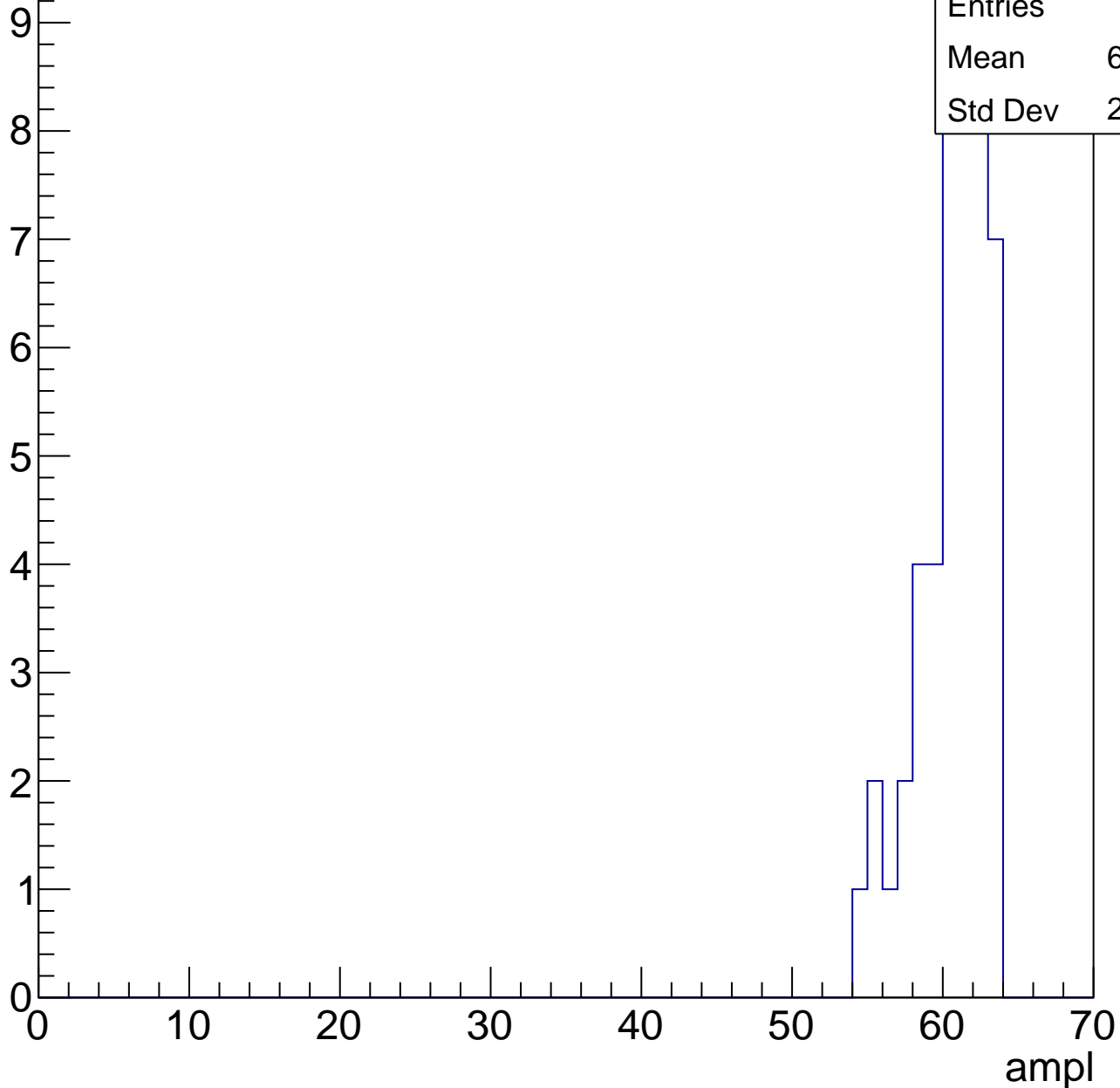
Entry



B0L000S, U4-ch76, adc5

calib_packv5_042523_0143.root, FC#5, port B1

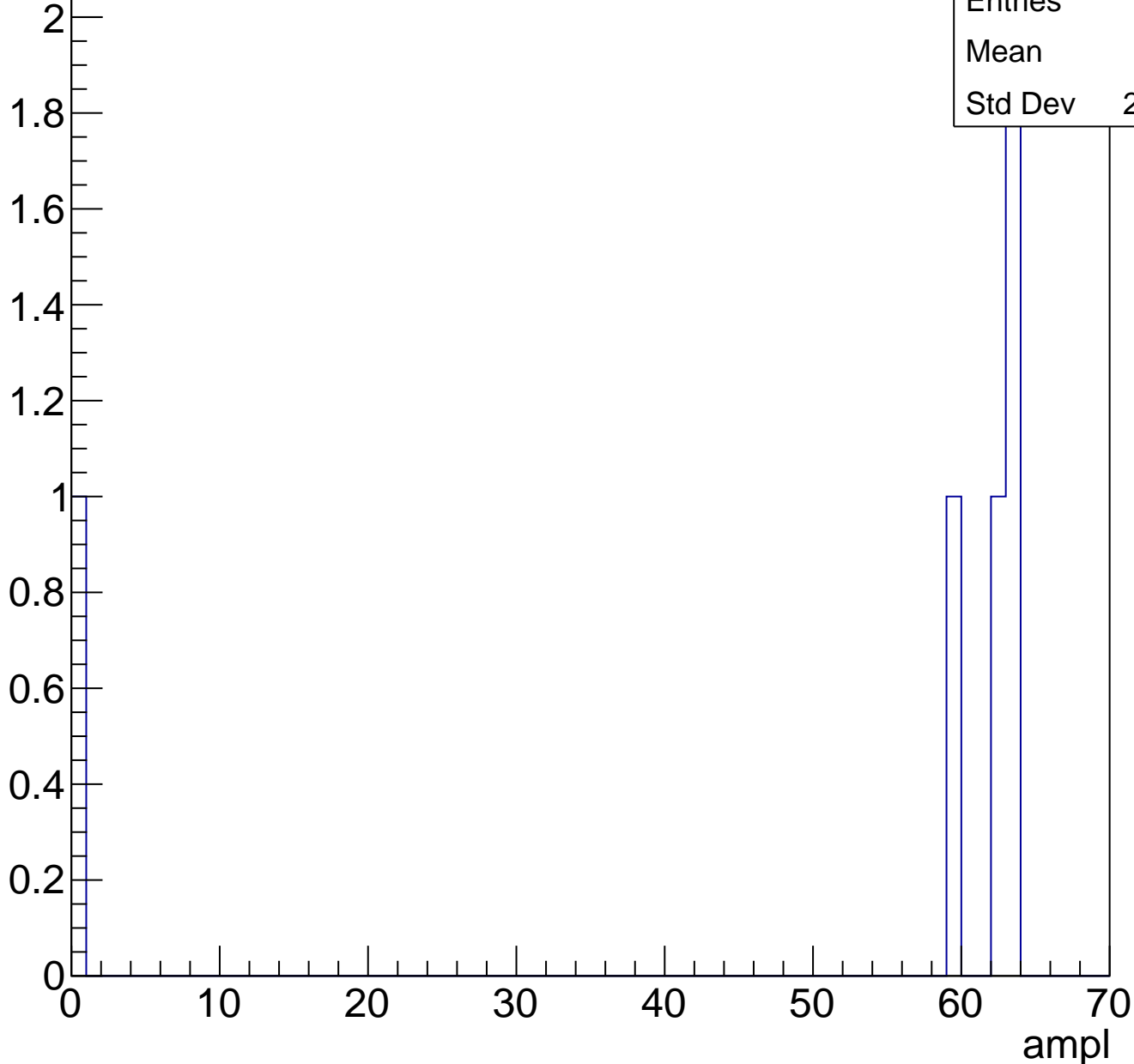
Entry



B0L000S, U4-ch76, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	5
Mean	49.4
Std Dev	24.74

B0L000S, U4-ch76, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch77, adc0

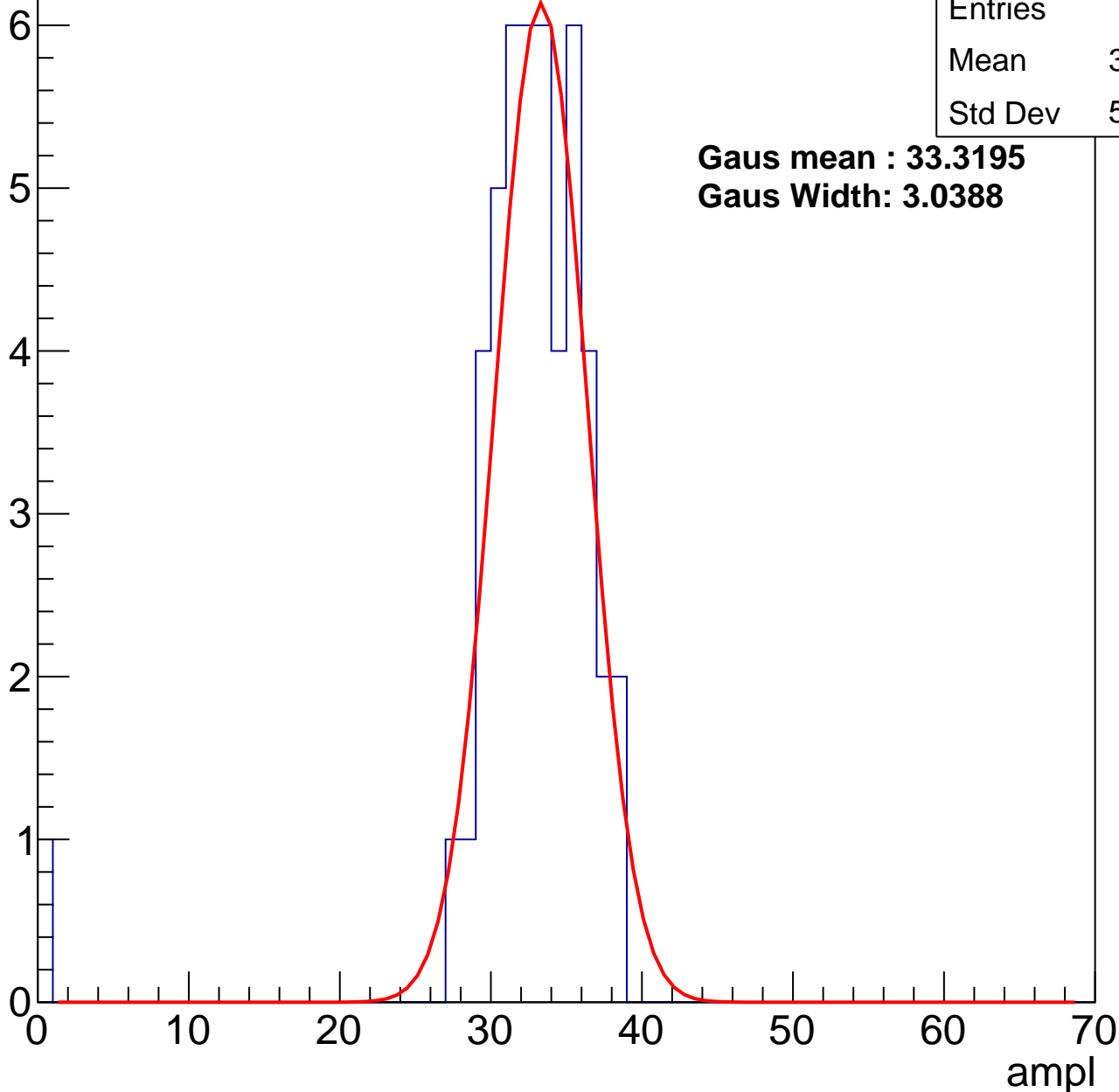
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	32.02
Std Dev	5.379

Gaus mean : 33.3195

Gaus Width: 3.0388



B0L000S, U4-ch77, adc1

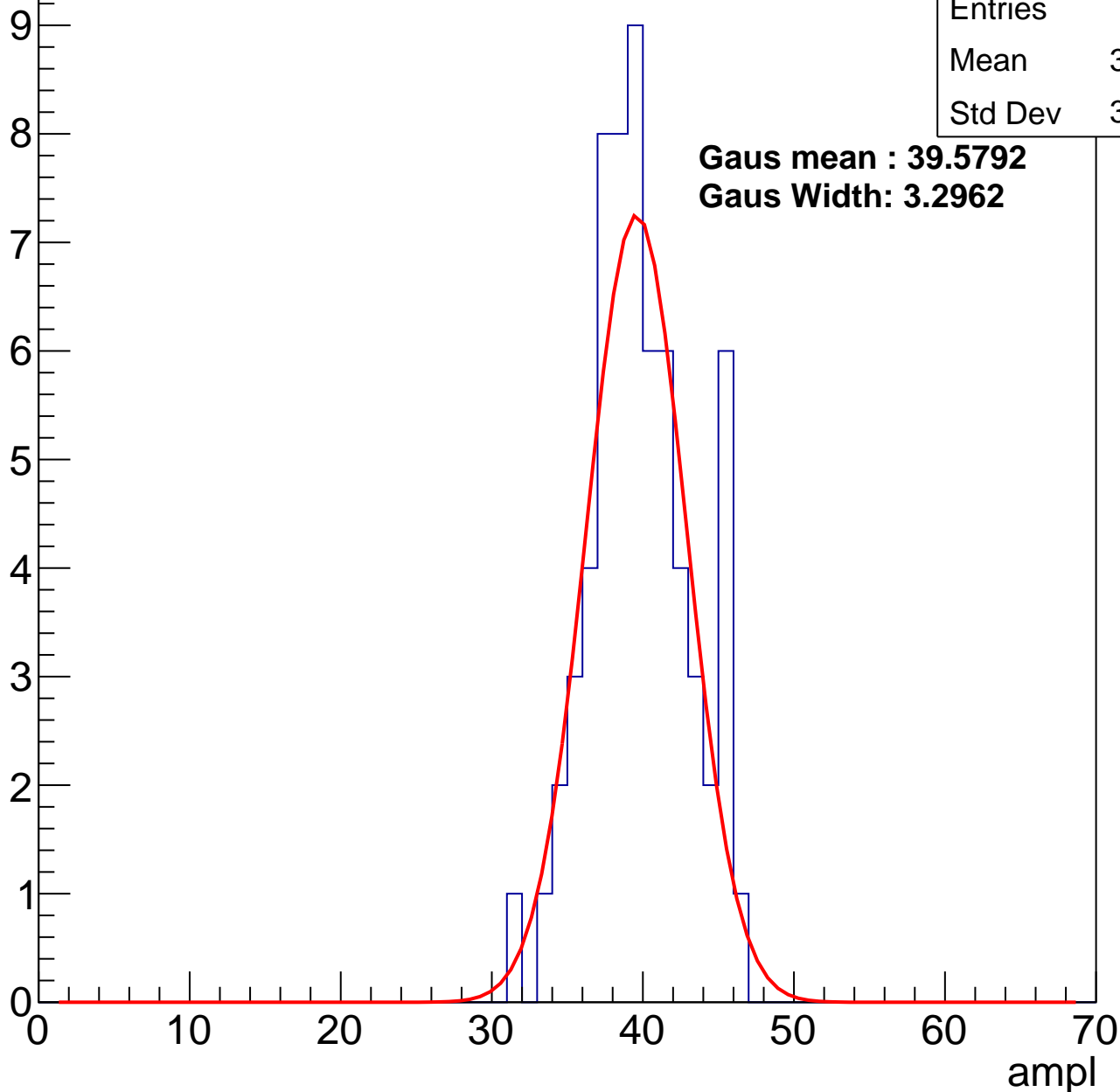
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	39.36
Std Dev	3.295

Gaus mean : 39.5792

Gaus Width: 3.2962



B0L000S, U4-ch77, adc2

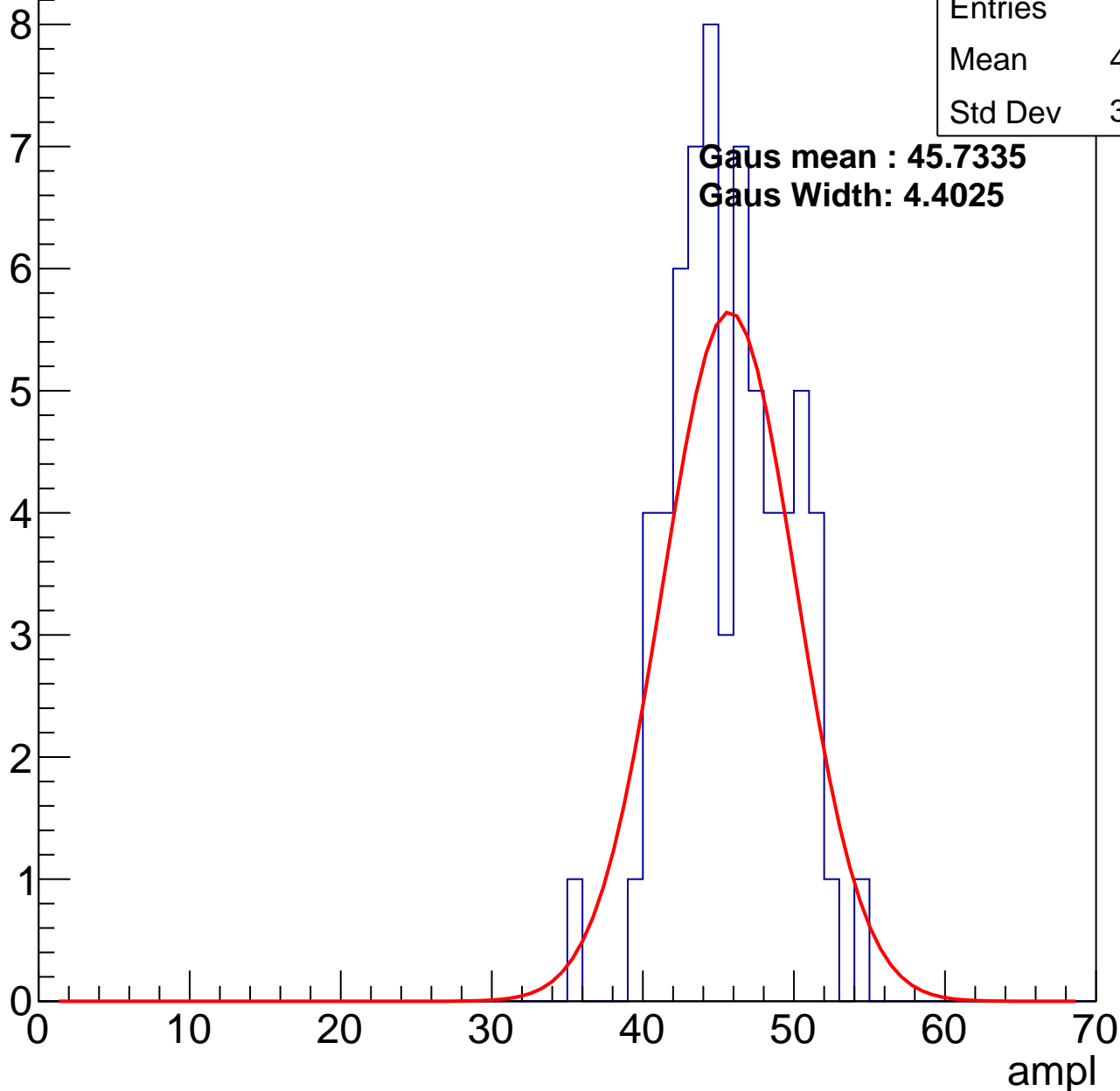
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	45.28
Std Dev	3.744

Gaus mean : 45.7335

Gaus Width: 4.4025

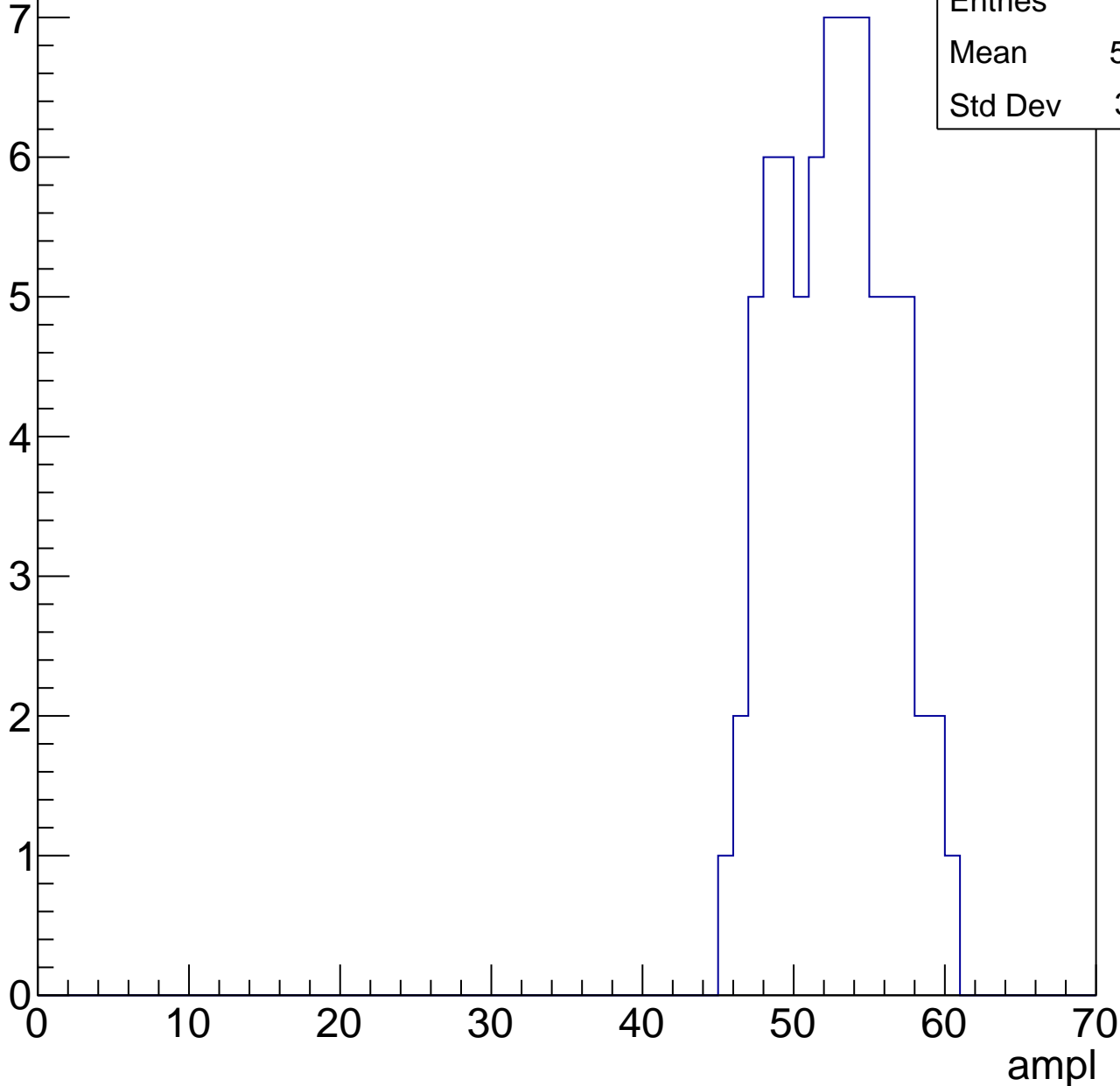


B0L000S, U4-ch77, adc3

calib_packv5_042523_0143.root, FC#5, port B1

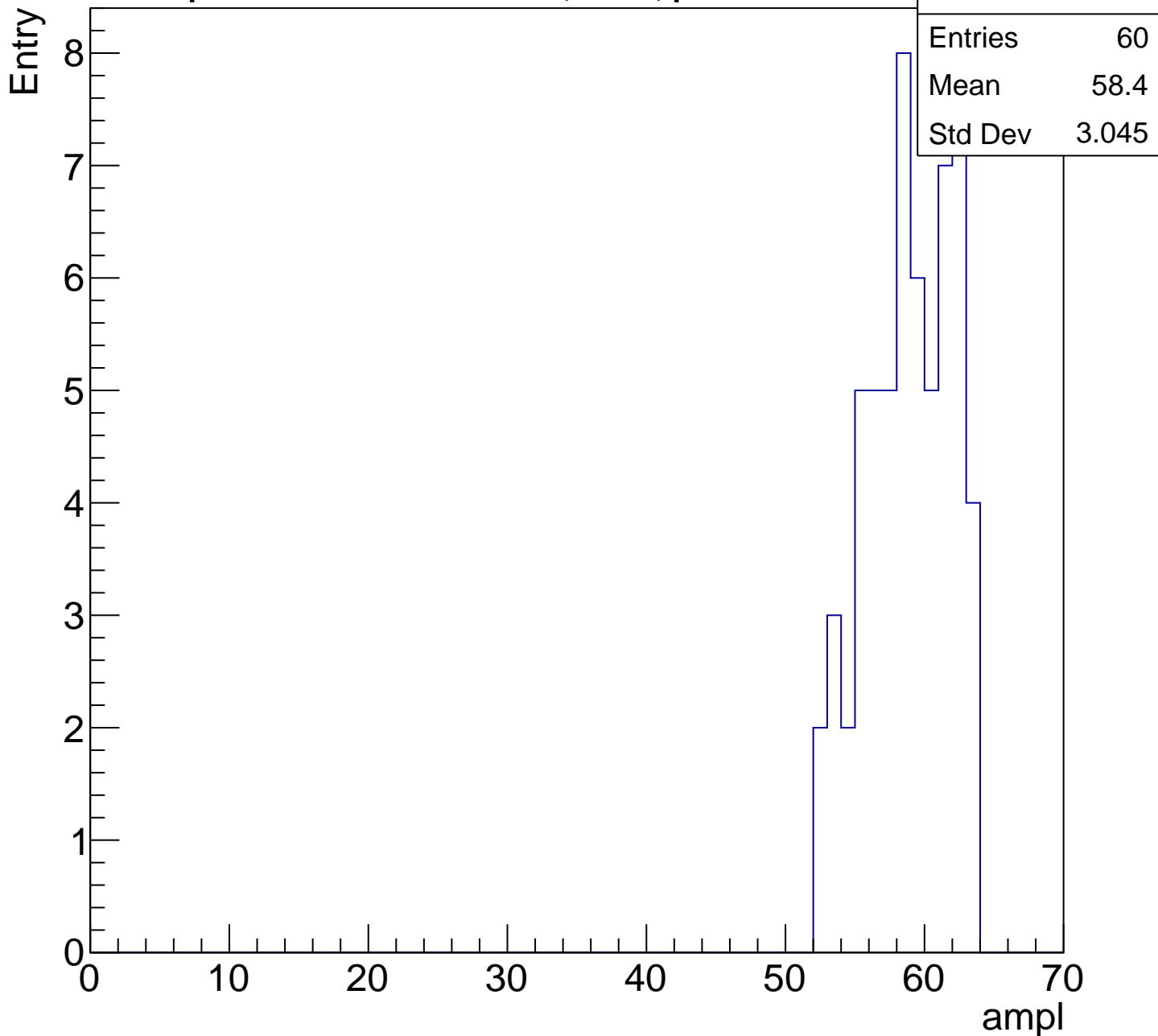
Entry

Entries	72
Mean	52.18
Std Dev	3.611



B0L000S, U4-ch77, adc4

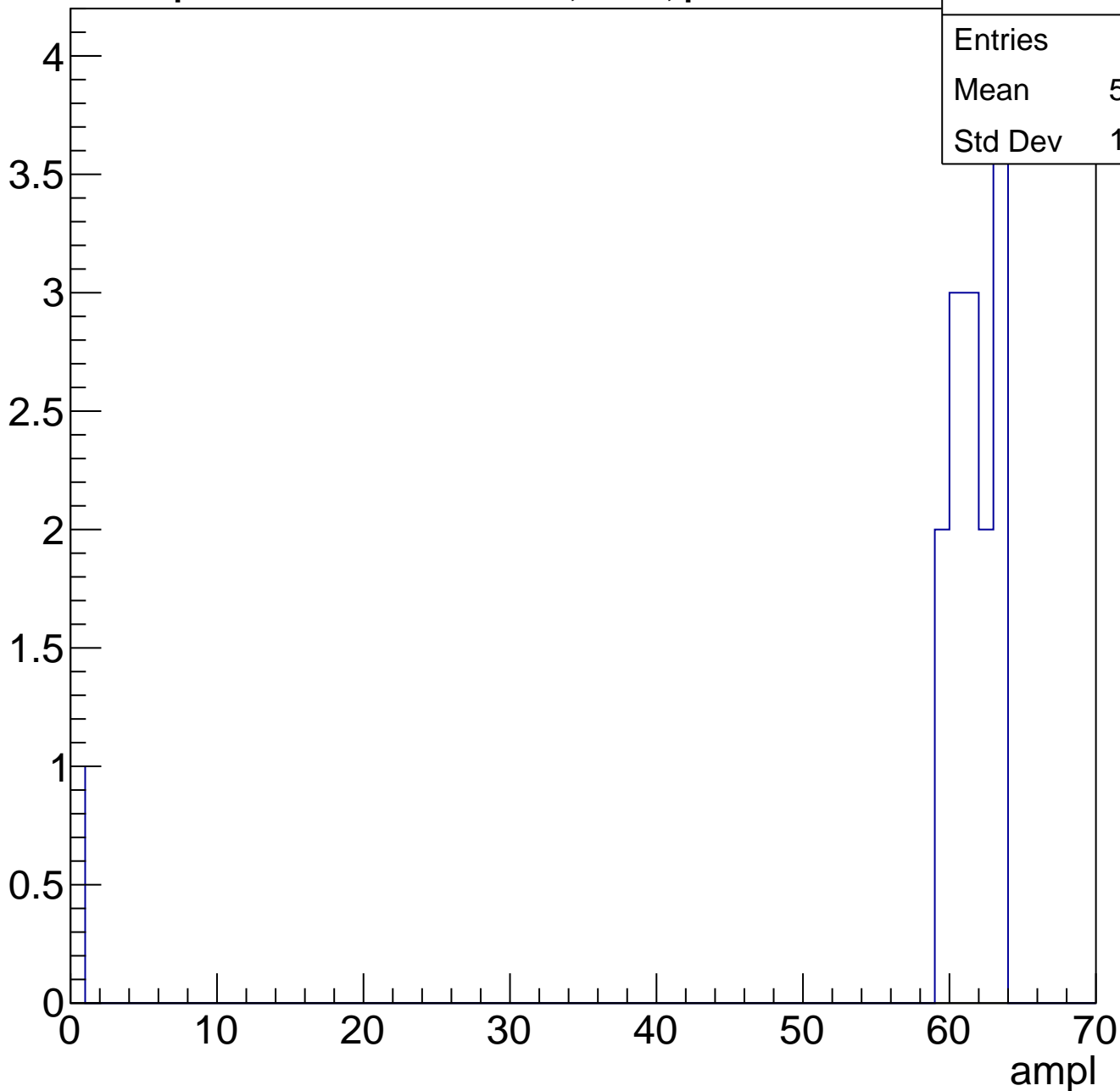
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch77, adc5

calib_packv5_042523_0143.root, FC#5, port B1

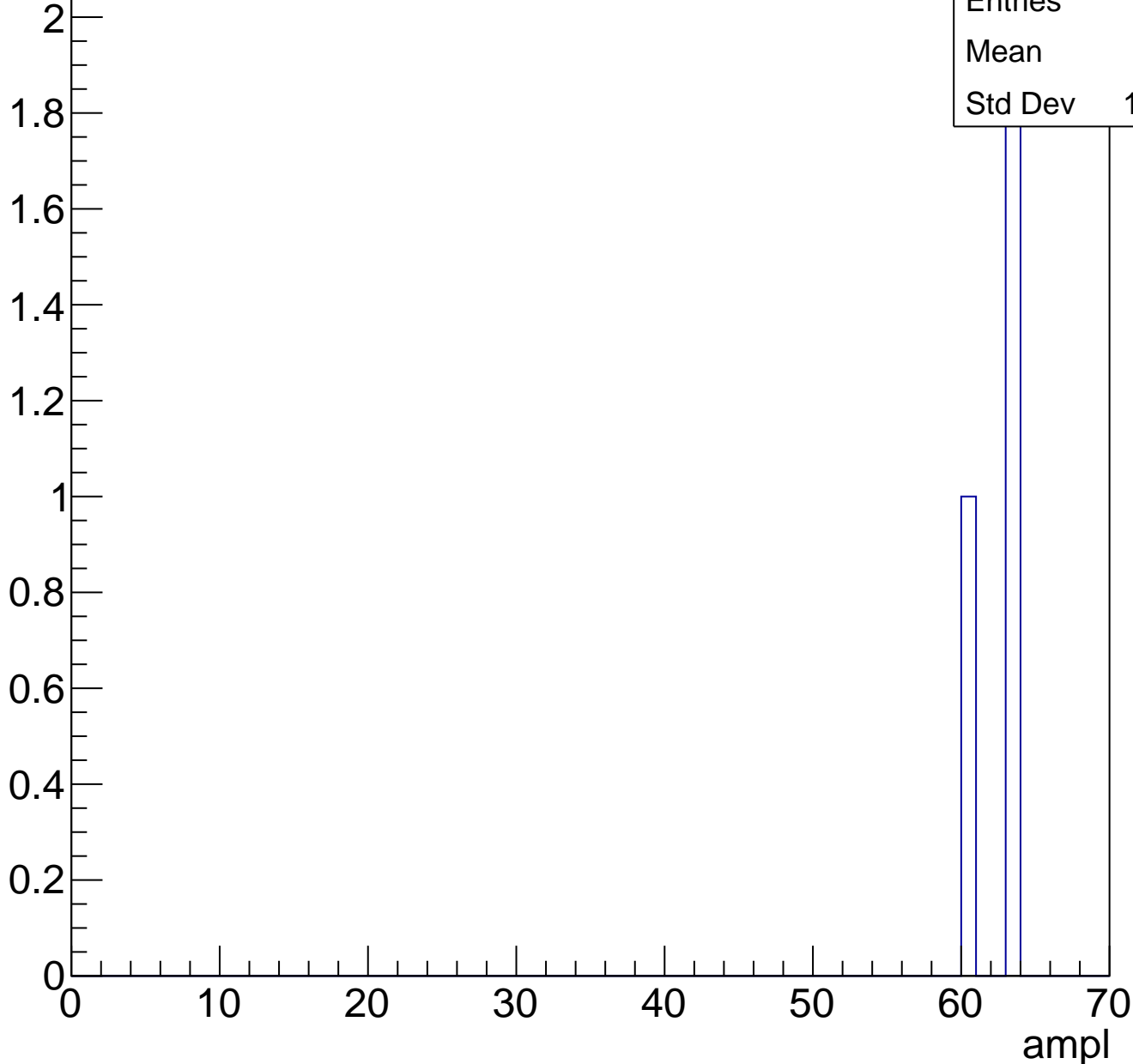
Entry



B0L000S, U4-ch77, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch77, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch78, adc0

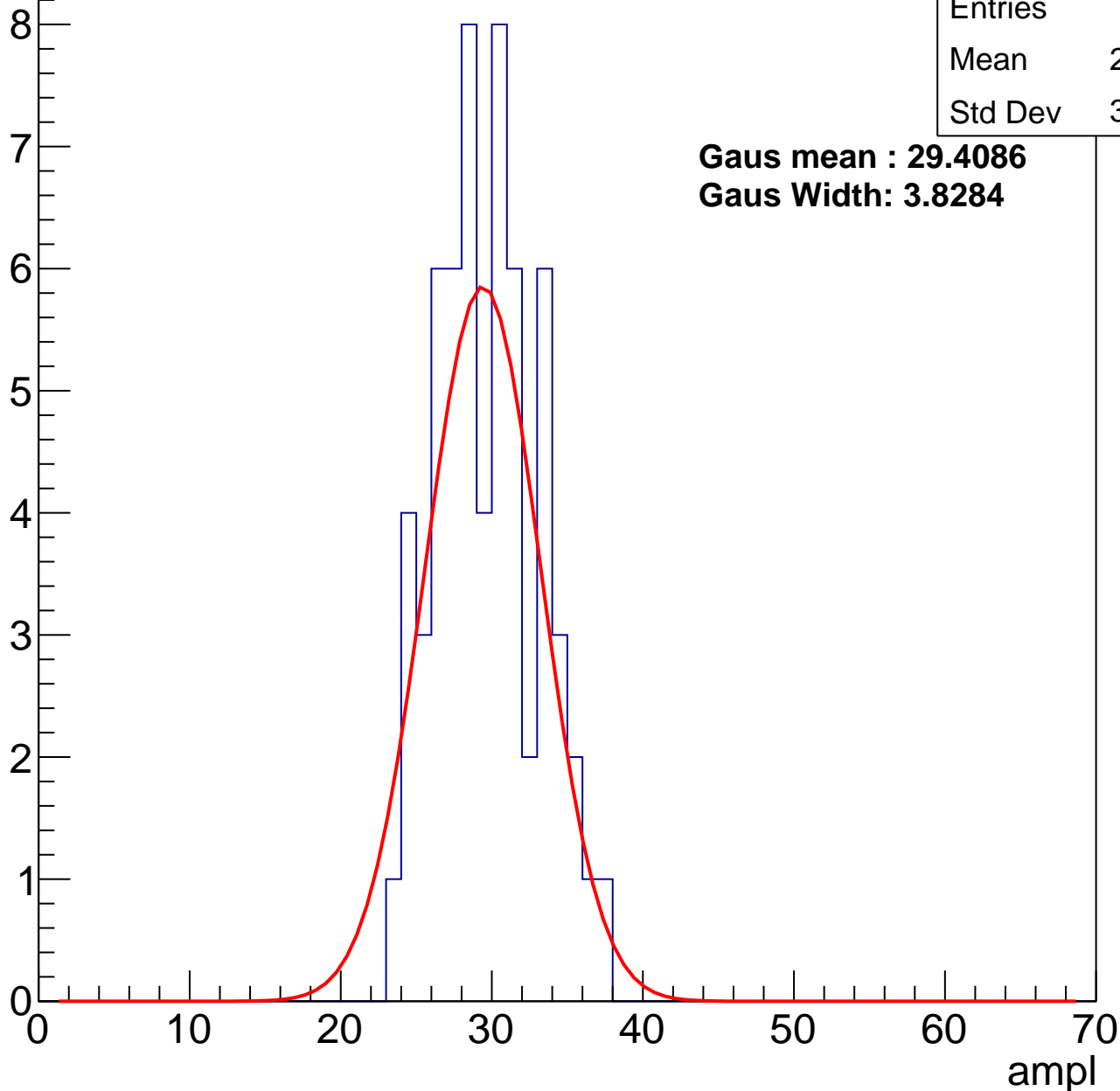
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.26
Std Dev	3.319

Gaus mean : 29.4086

Gaus Width: 3.8284



B0L000S, U4-ch78, adc1

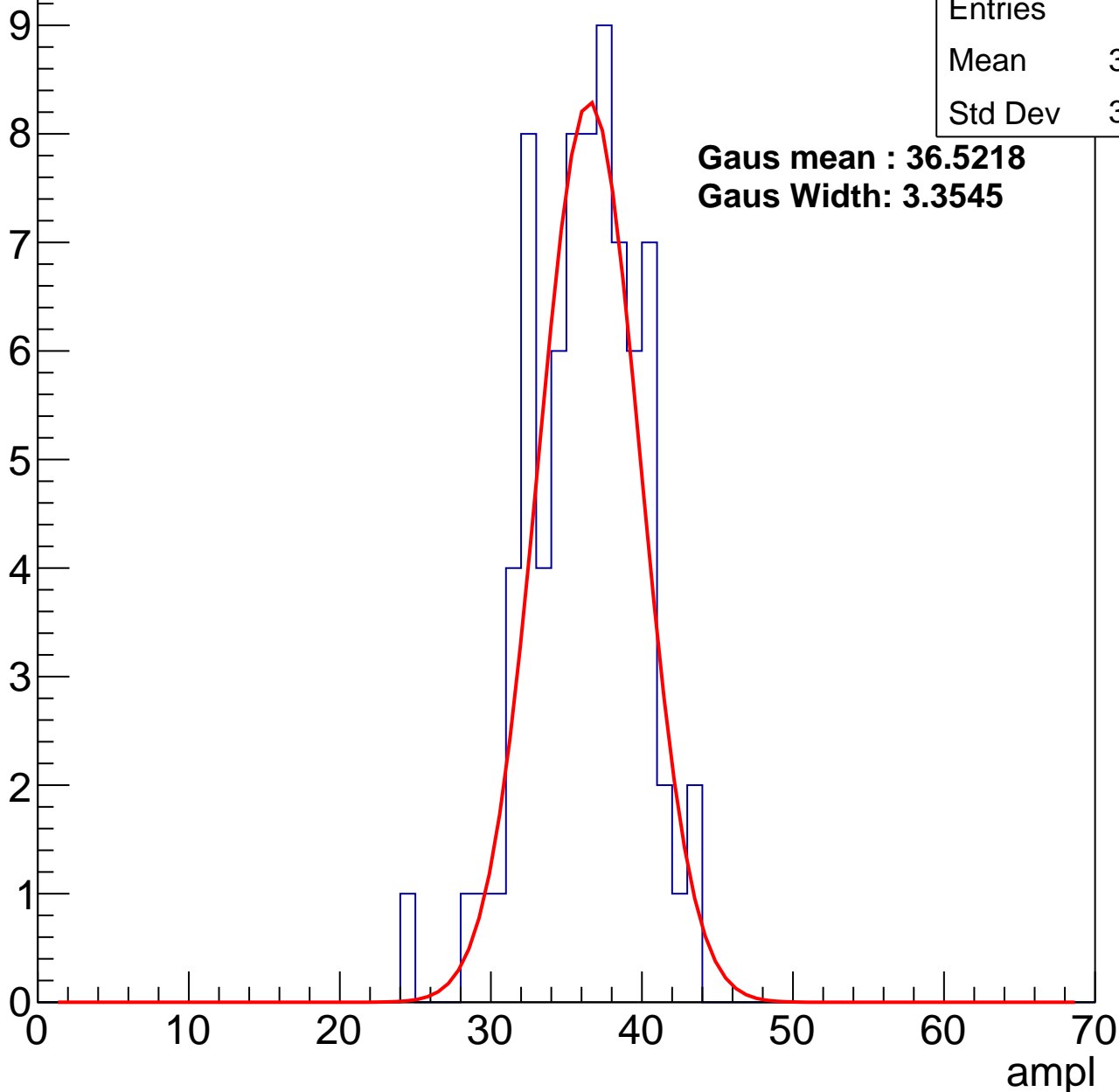
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	35.76
Std Dev	3.576

Gaus mean : 36.5218

Gaus Width: 3.3545



B0L000S, U4-ch78, adc2

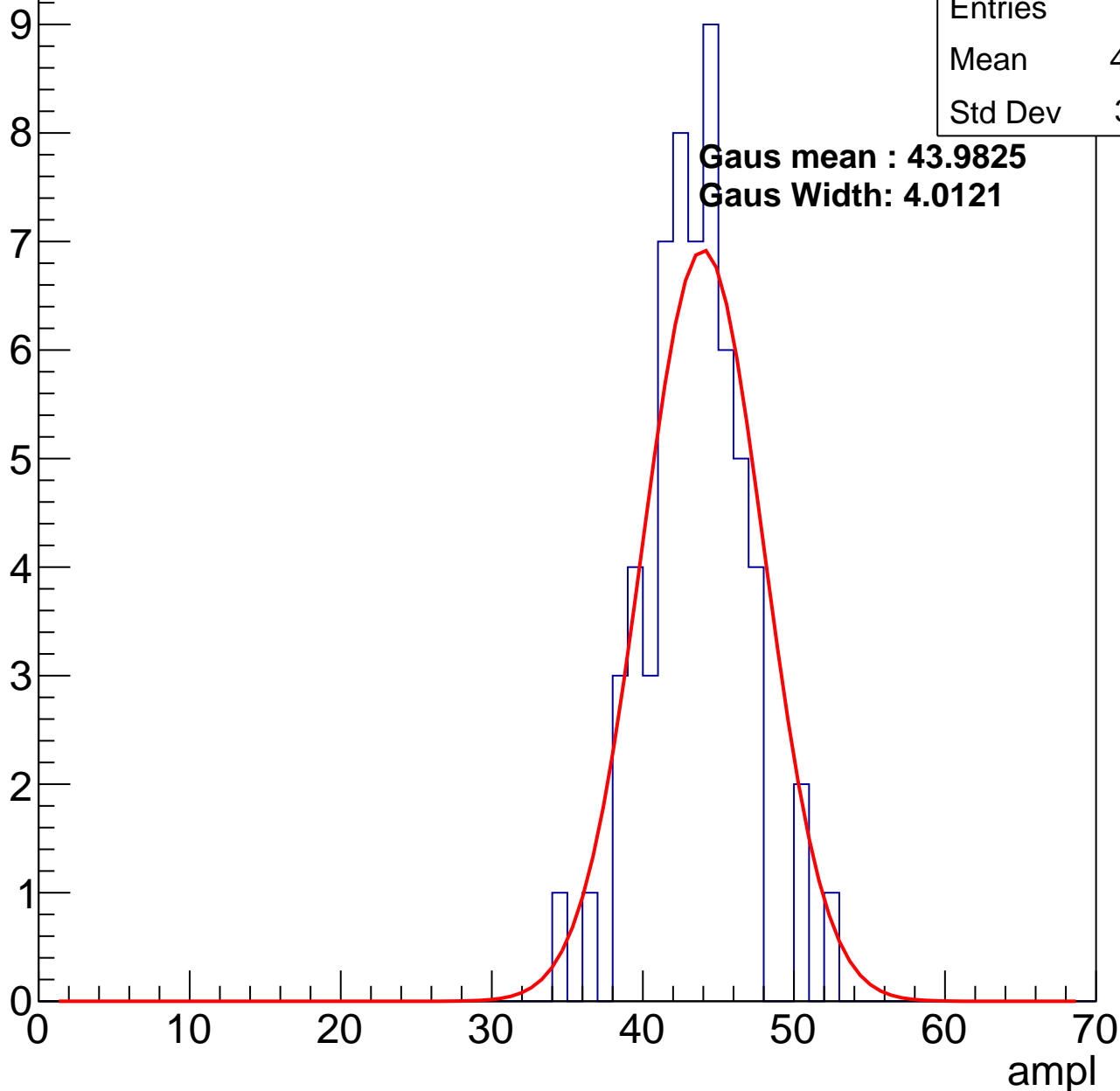
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42.95
Std Dev	3.271

Gaus mean : 43.9825

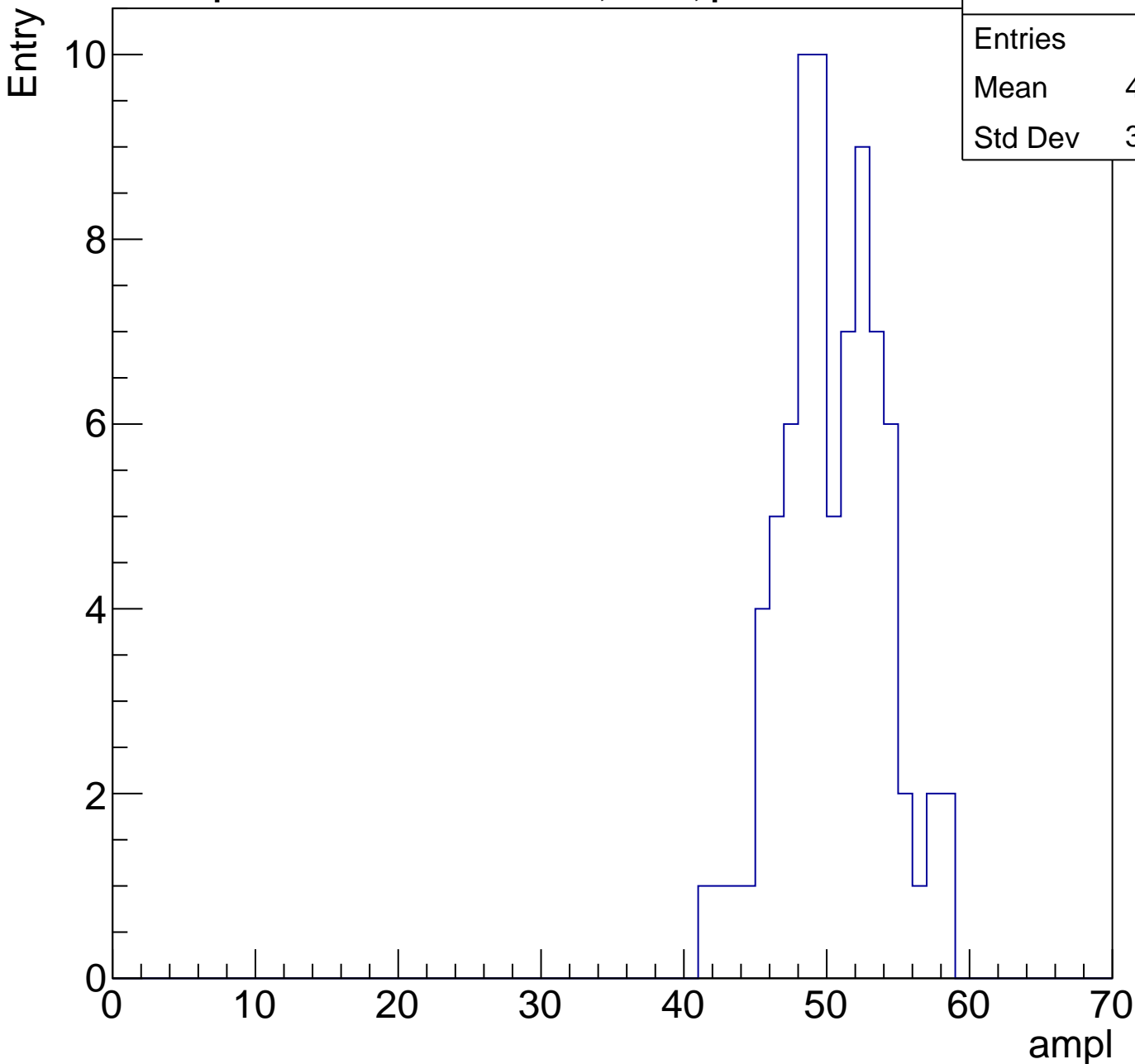
Gaus Width: 4.0121



B0L000S, U4-ch78, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	49.98
Std Dev	3.585

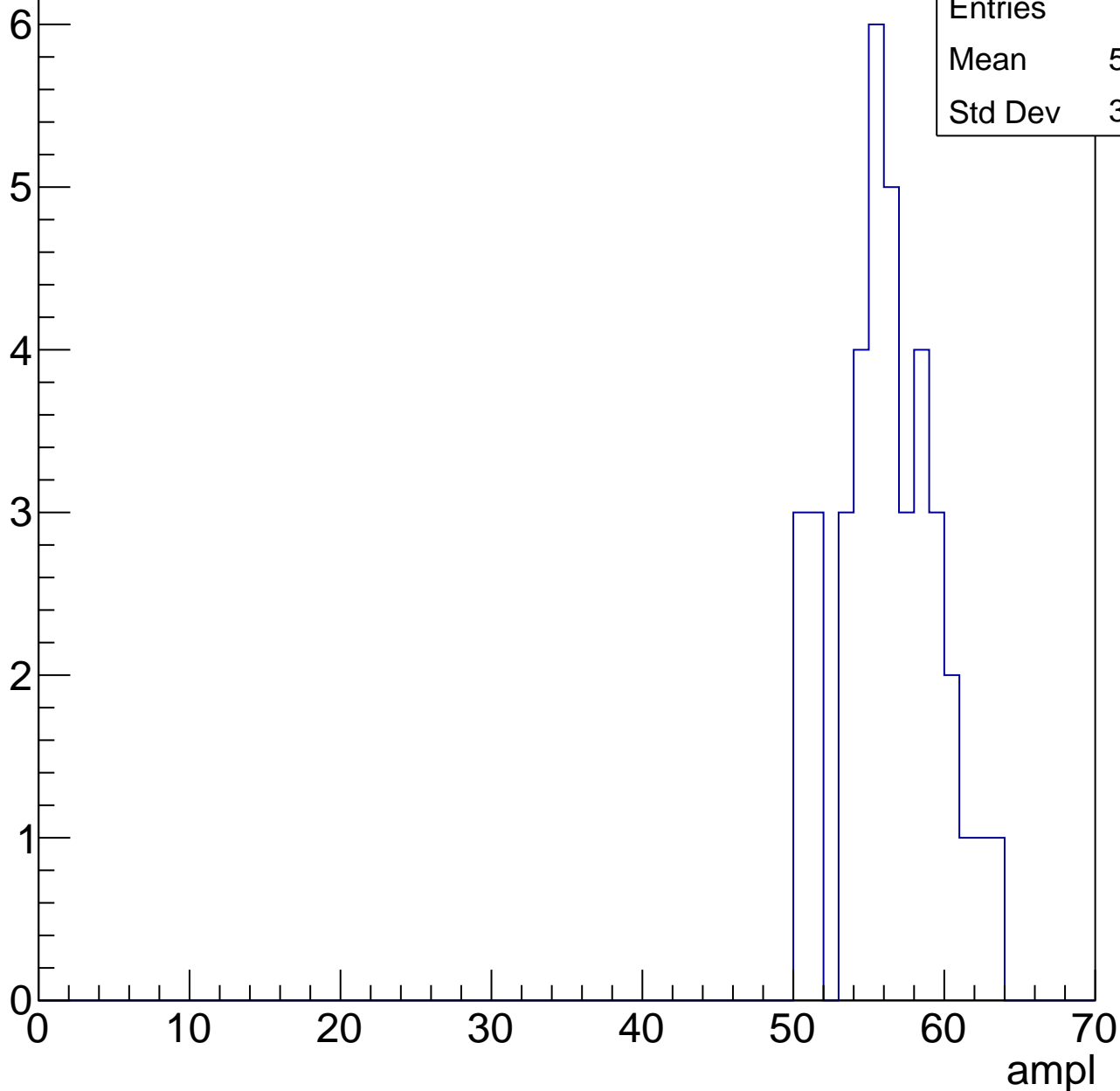


B0L000S, U4-ch78, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

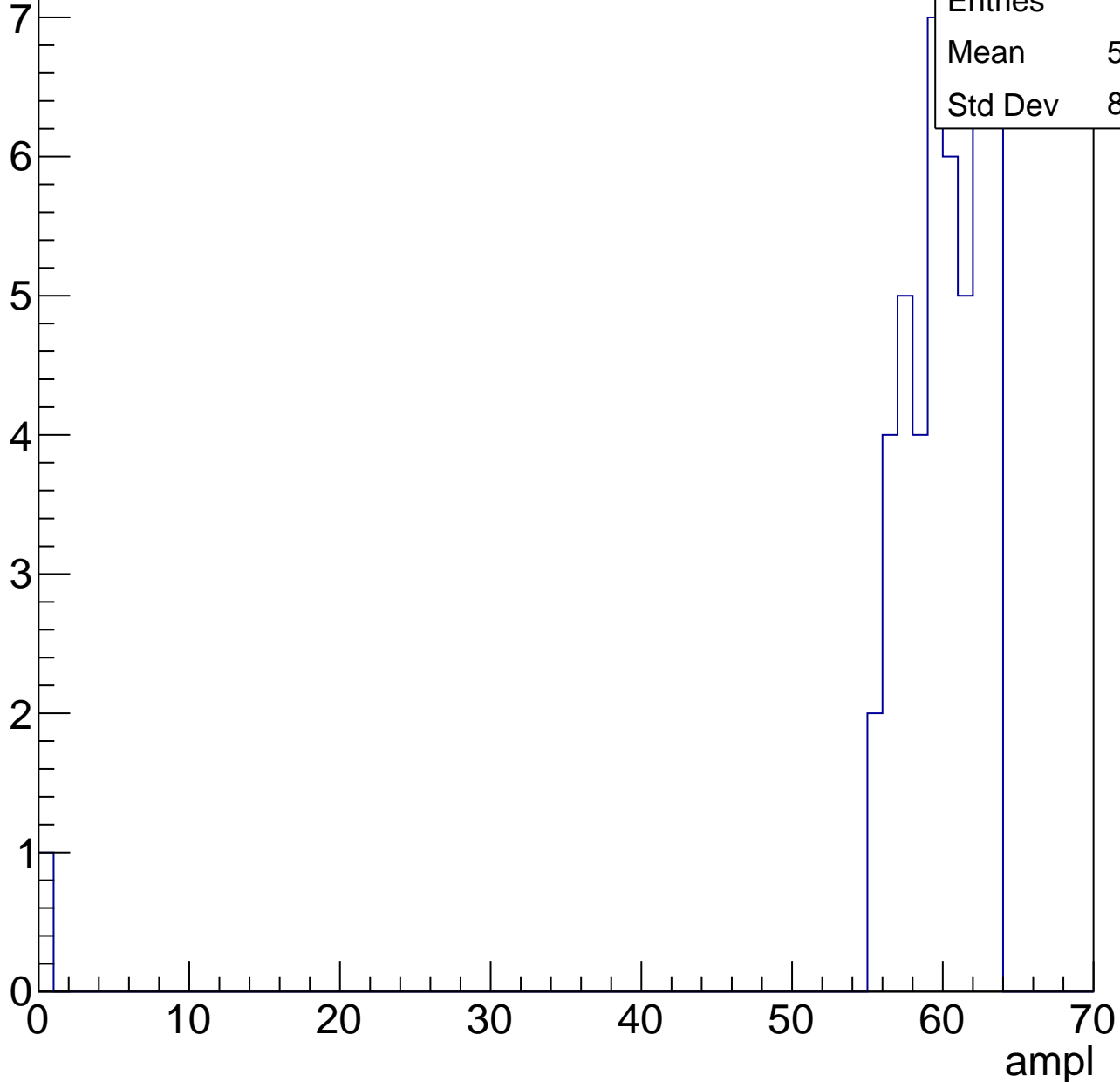
Entries	39
Mean	55.74
Std Dev	3.272



B0L000S, U4-ch78, adc5

calib_packv5_042523_0143.root, FC#5, port B1

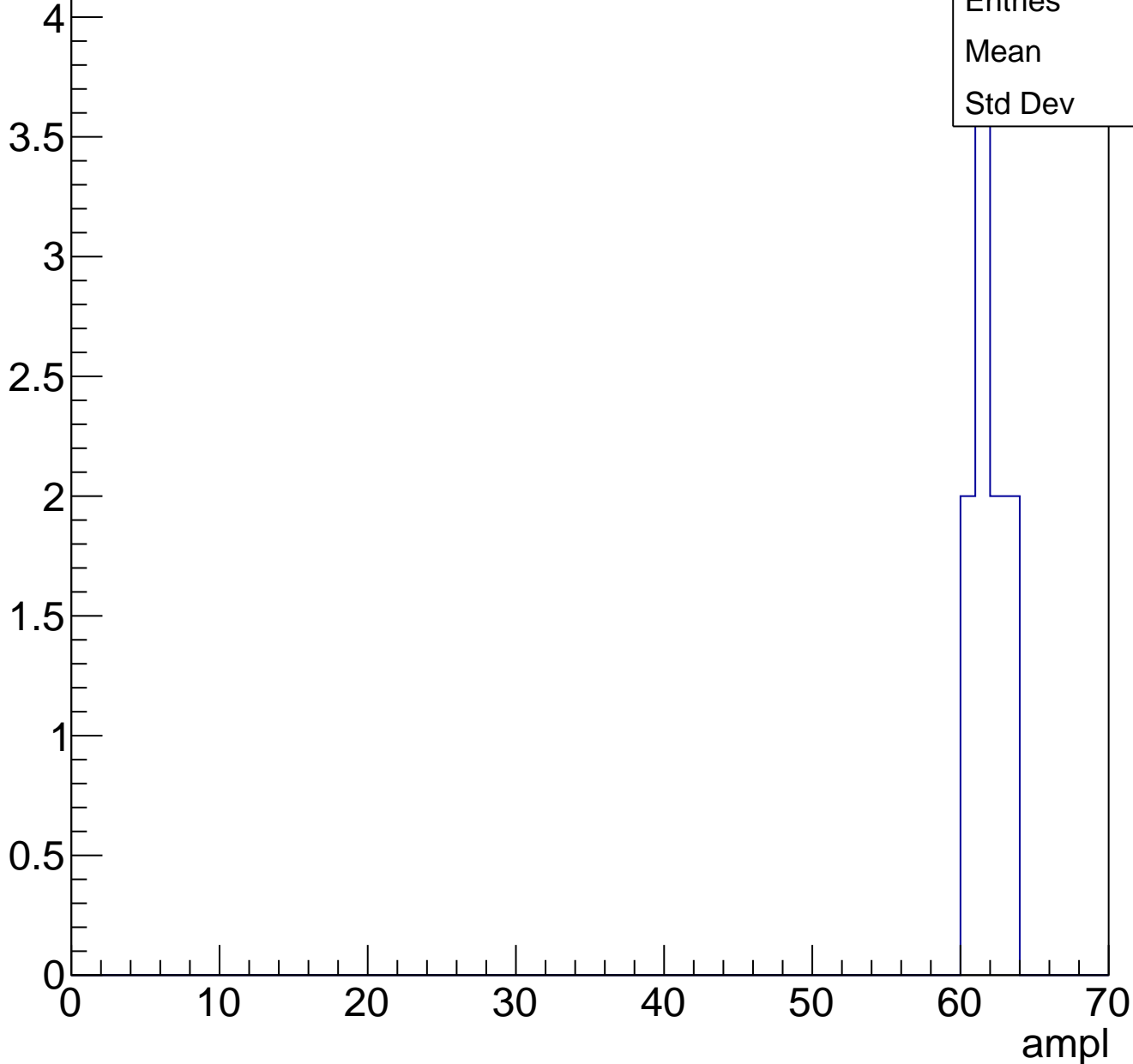
Entry



B0L000S, U4-ch78, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch78, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch79, adc0

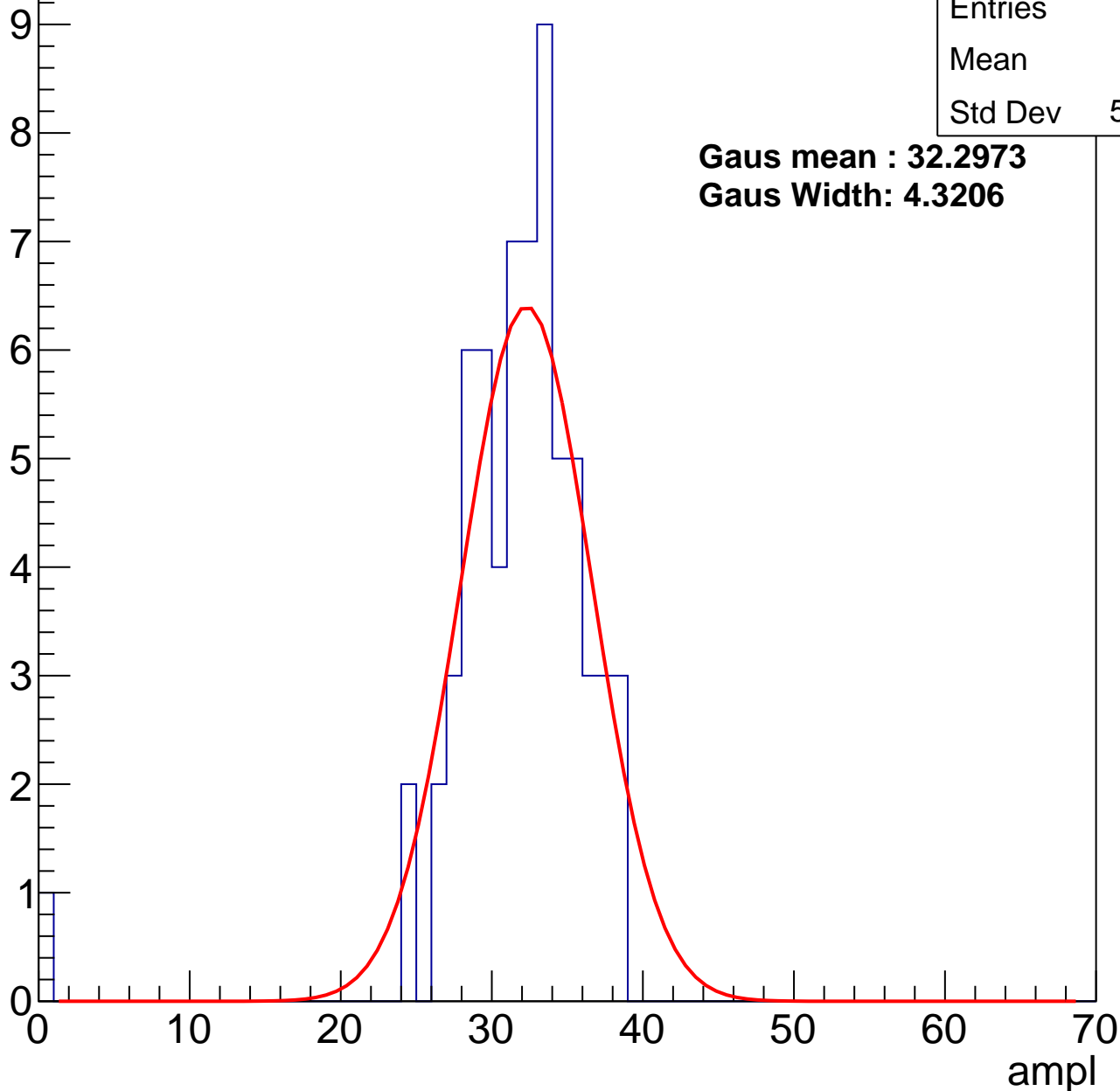
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	31.2
Std Dev	5.126

Gaus mean : 32.2973

Gaus Width: 4.3206



B0L000S, U4-ch79, adc1

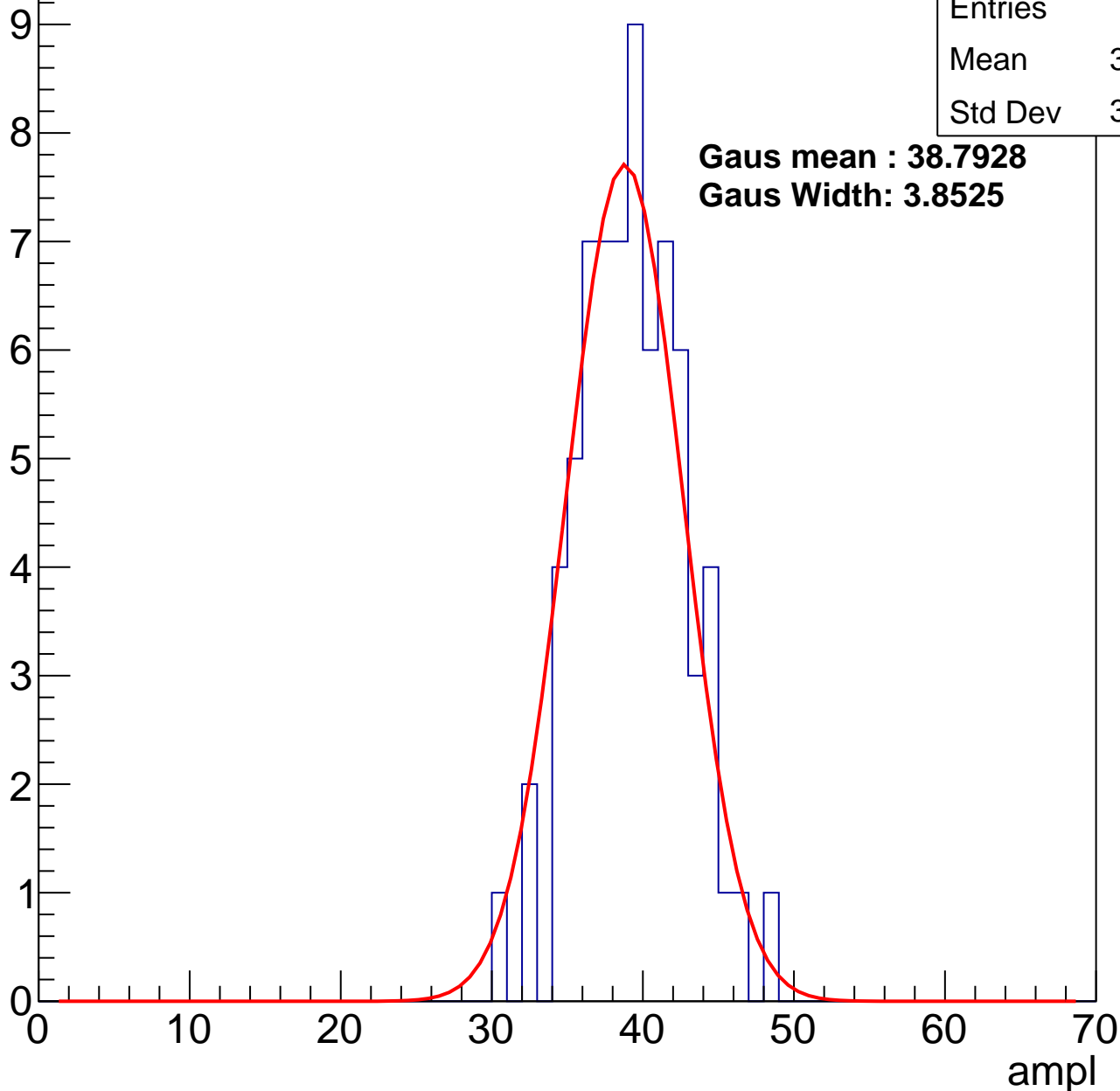
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	38.82
Std Dev	3.473

Gaus mean : 38.7928

Gaus Width: 3.8525



B0L000S, U4-ch79, adc2

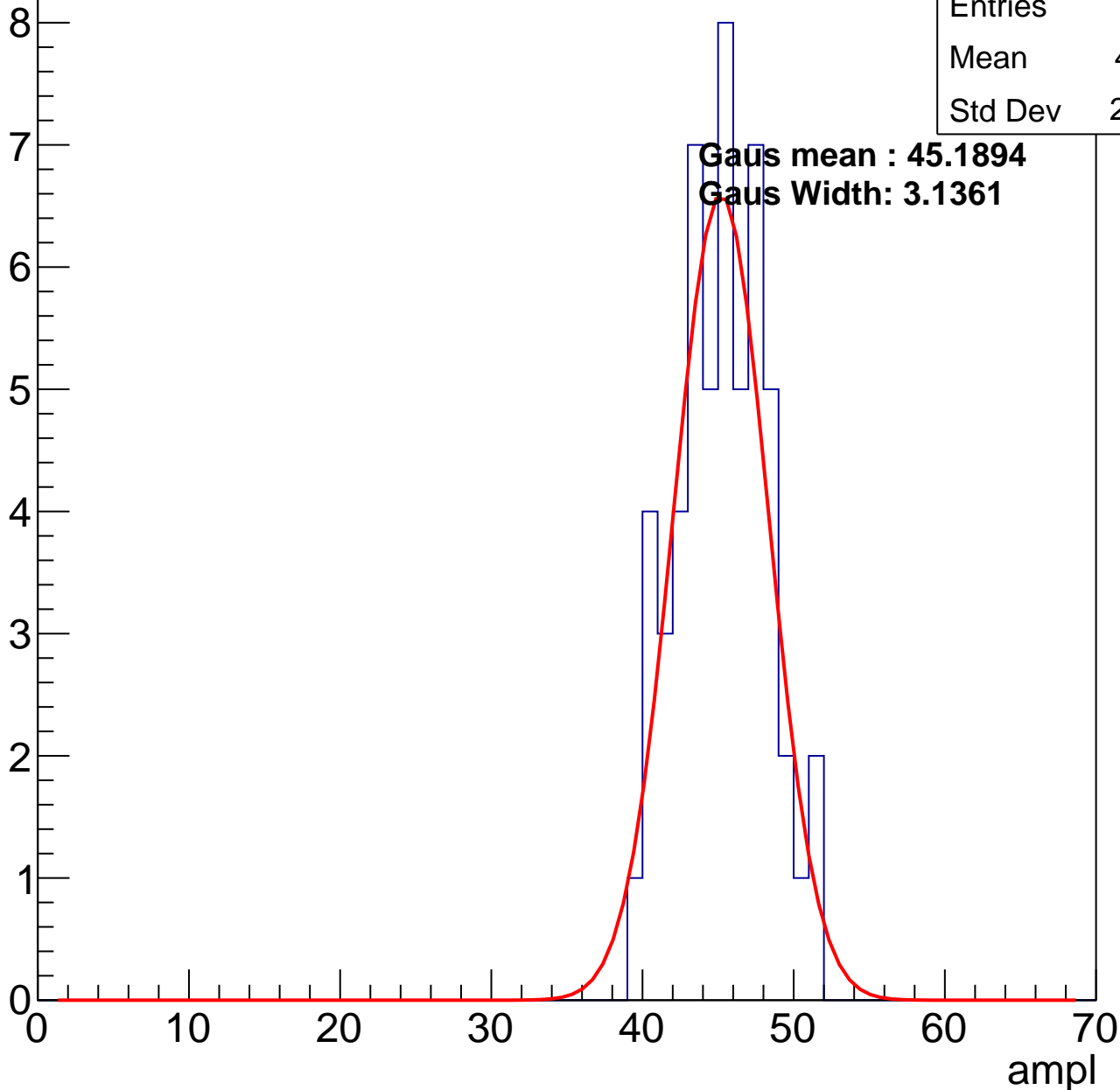
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	44.81
Std Dev	2.913

Gaus mean : 45.1894

Gaus Width: 3.1361

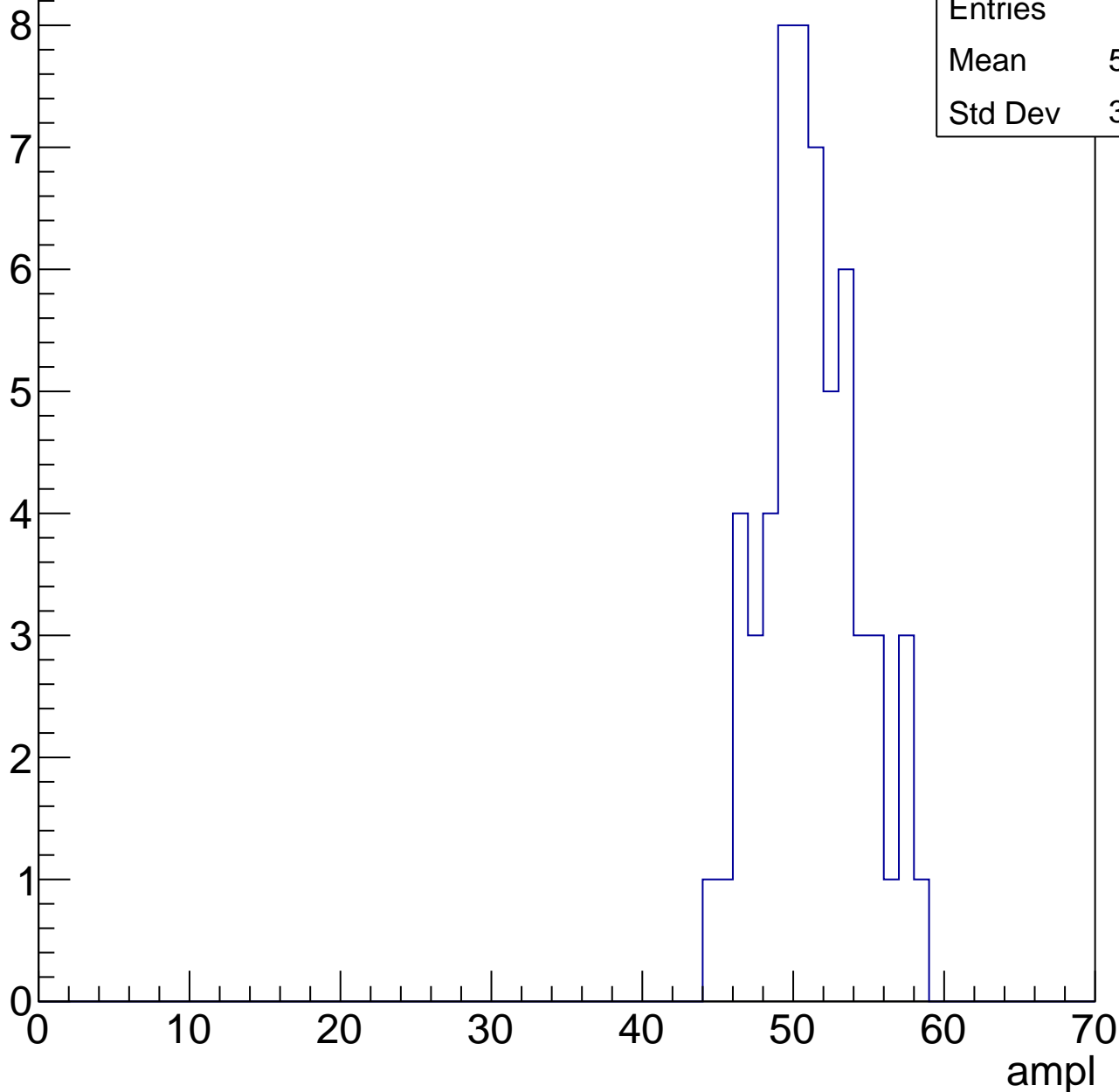


B0L000S, U4-ch79, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

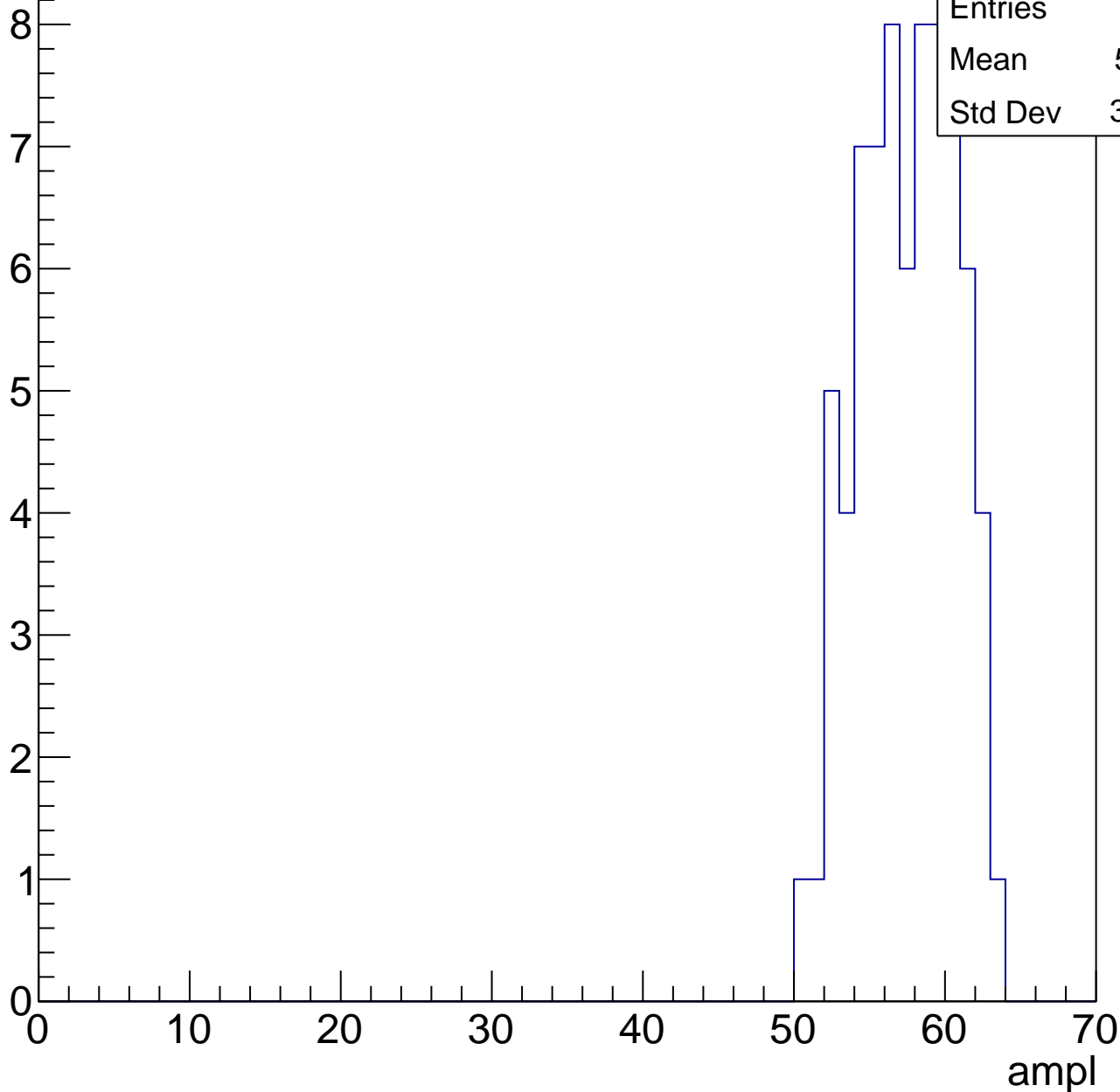
Entries	58
Mean	50.78
Std Dev	3.195



B0L000S, U4-ch79, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

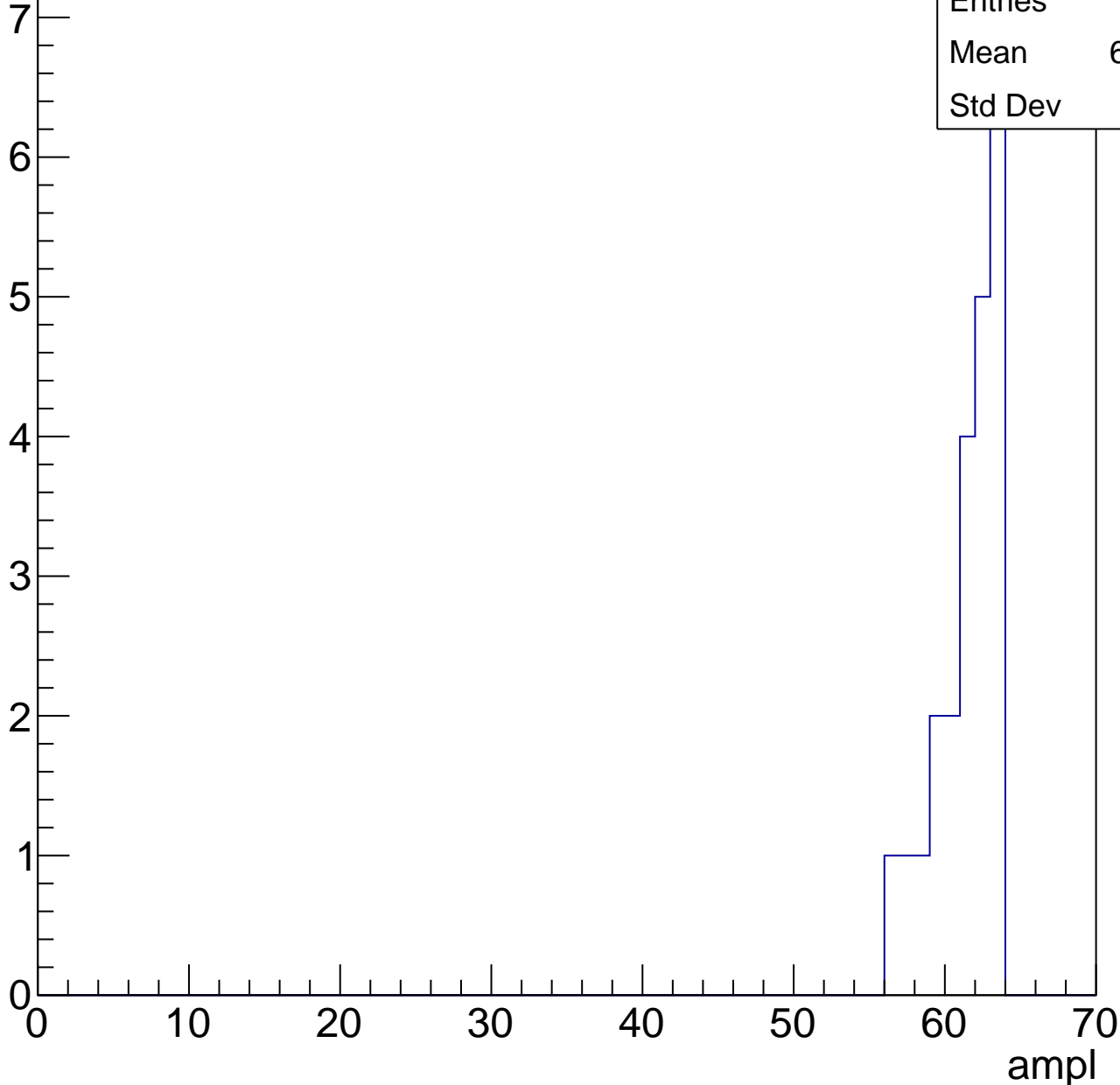


B0L000S, U4-ch79, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	23
Mean	61.04
Std Dev	2.01



B0L000S, U4-ch79, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch79, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch80, adc0

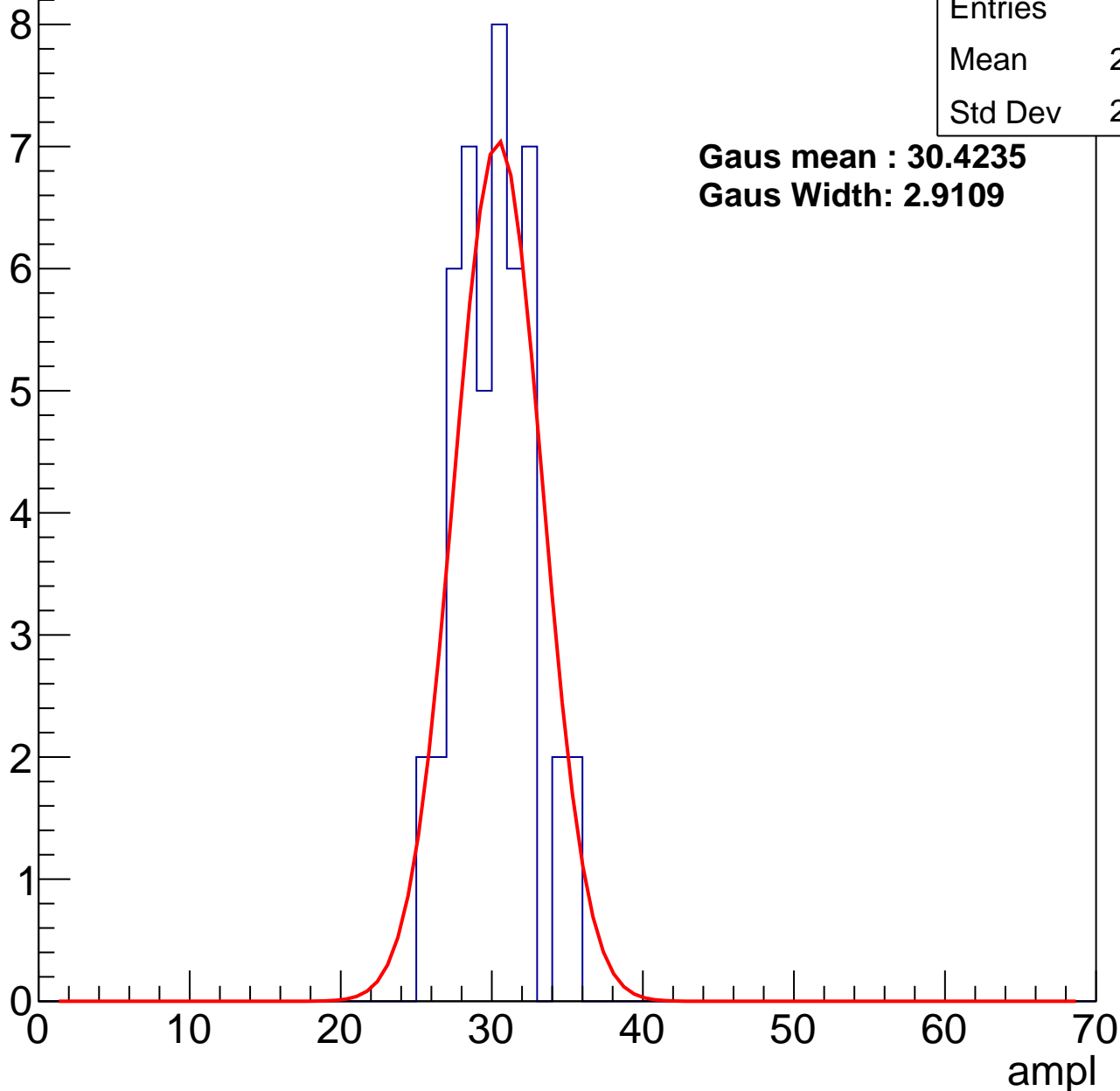
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	29.64
Std Dev	2.436

Gaus mean : 30.4235

Gaus Width: 2.9109



B0L000S, U4-ch80, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	73
Mean	35.86
Std Dev	2.83

Gaus mean : 36.4411

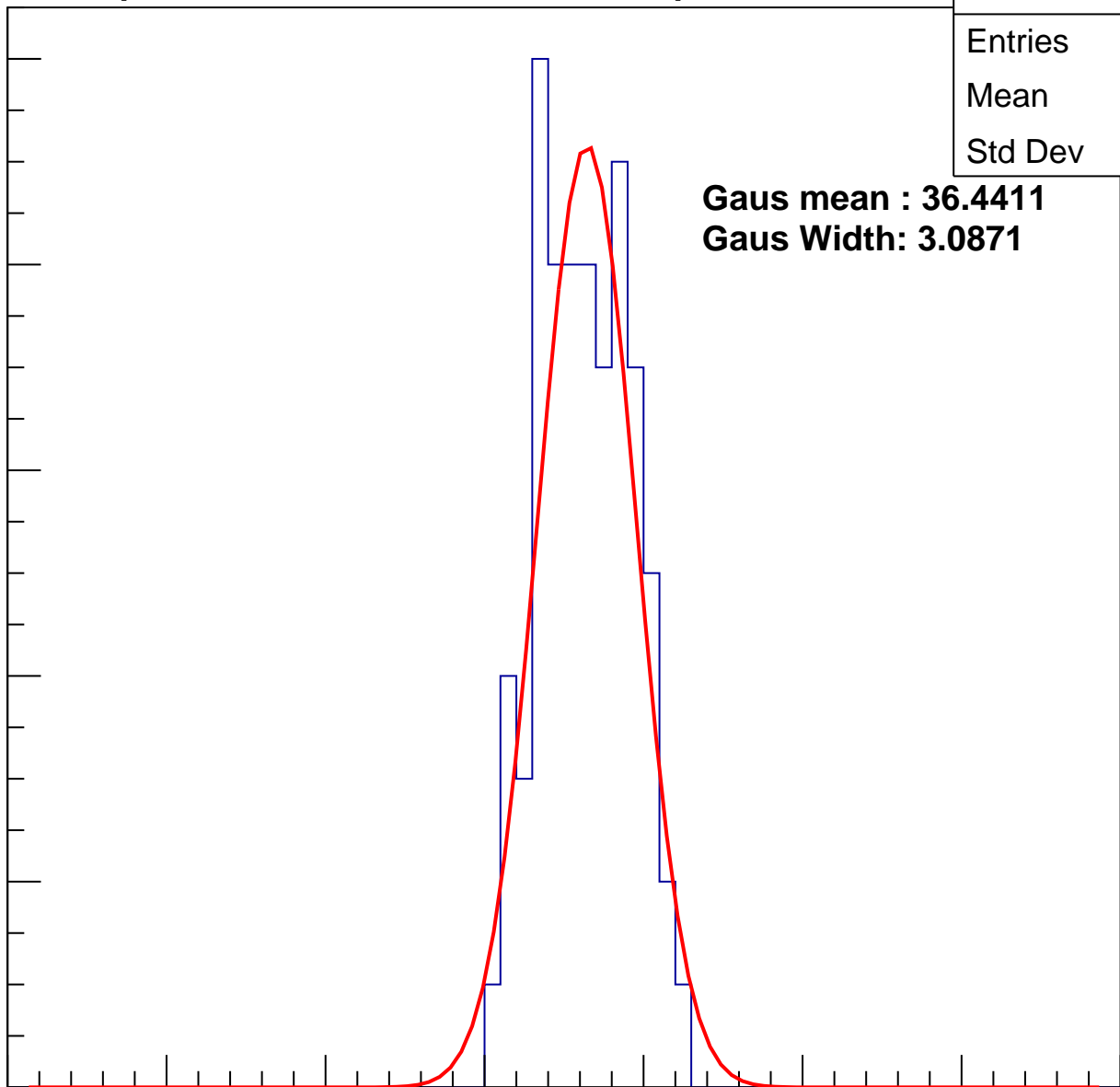
Gaus Width: 3.0871

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch80, adc2

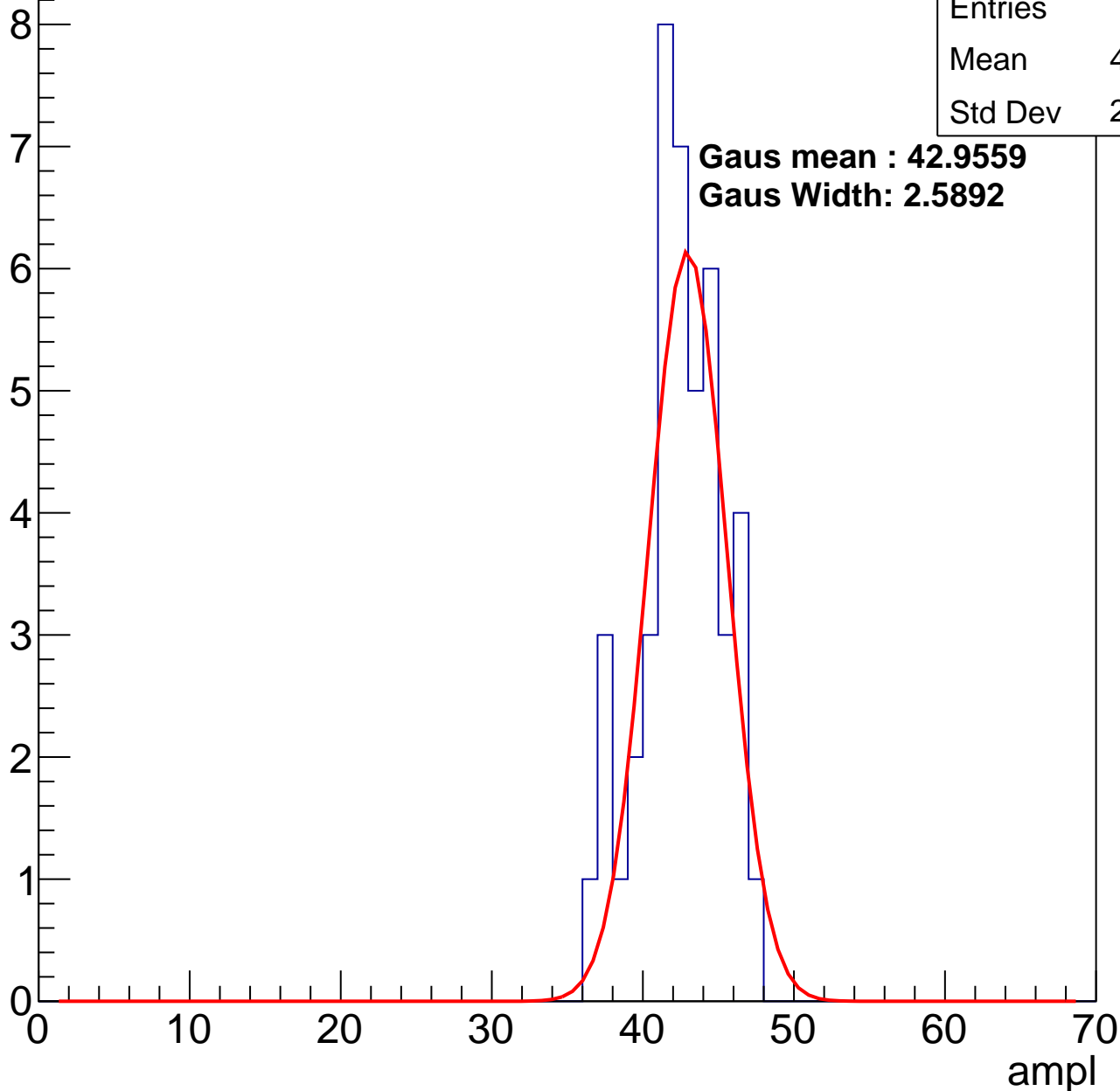
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	42.05
Std Dev	2.654

Gaus mean : 42.9559

Gaus Width: 2.5892

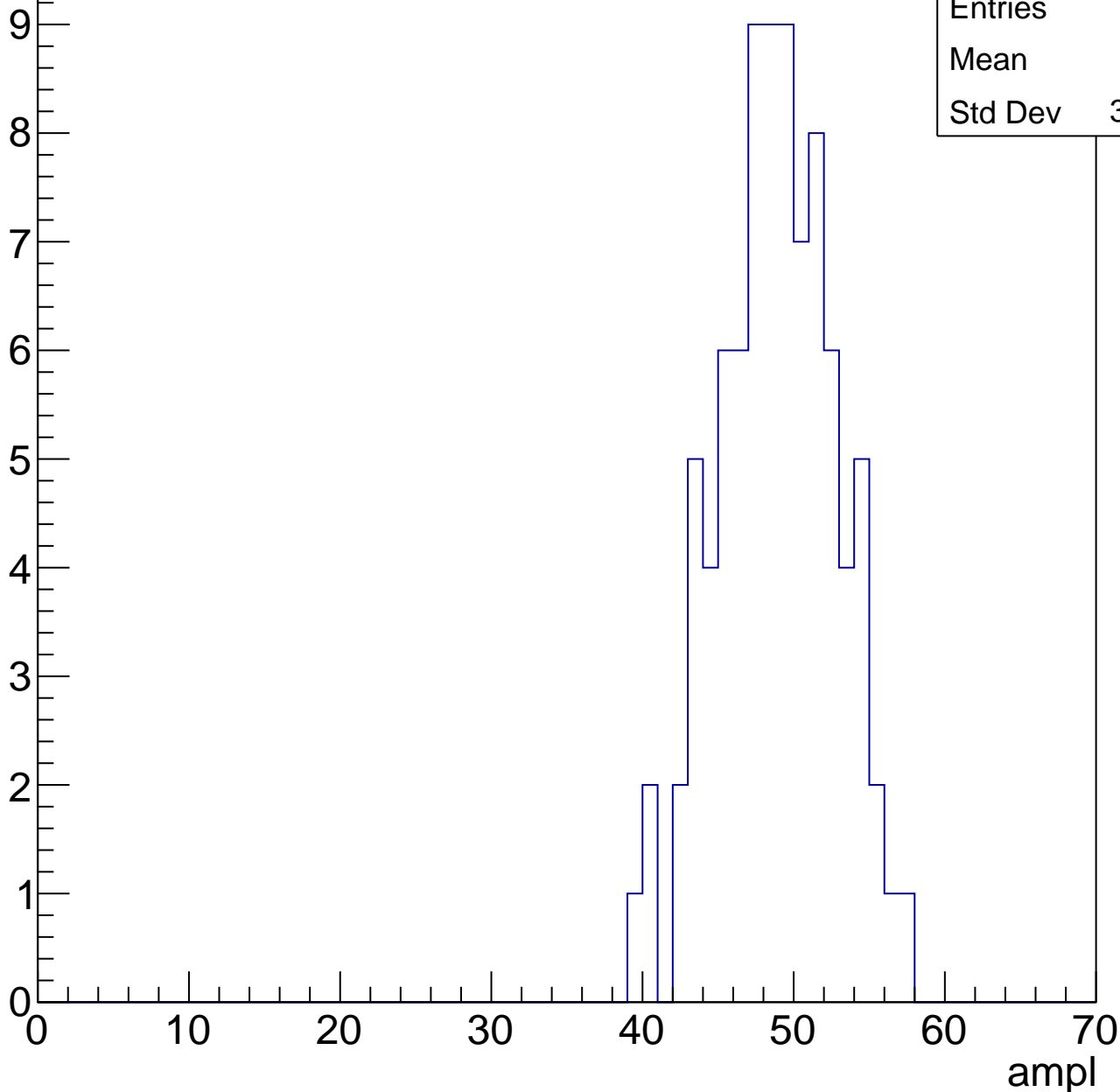


B0L000S, U4-ch80, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	87
Mean	48.4
Std Dev	3.822

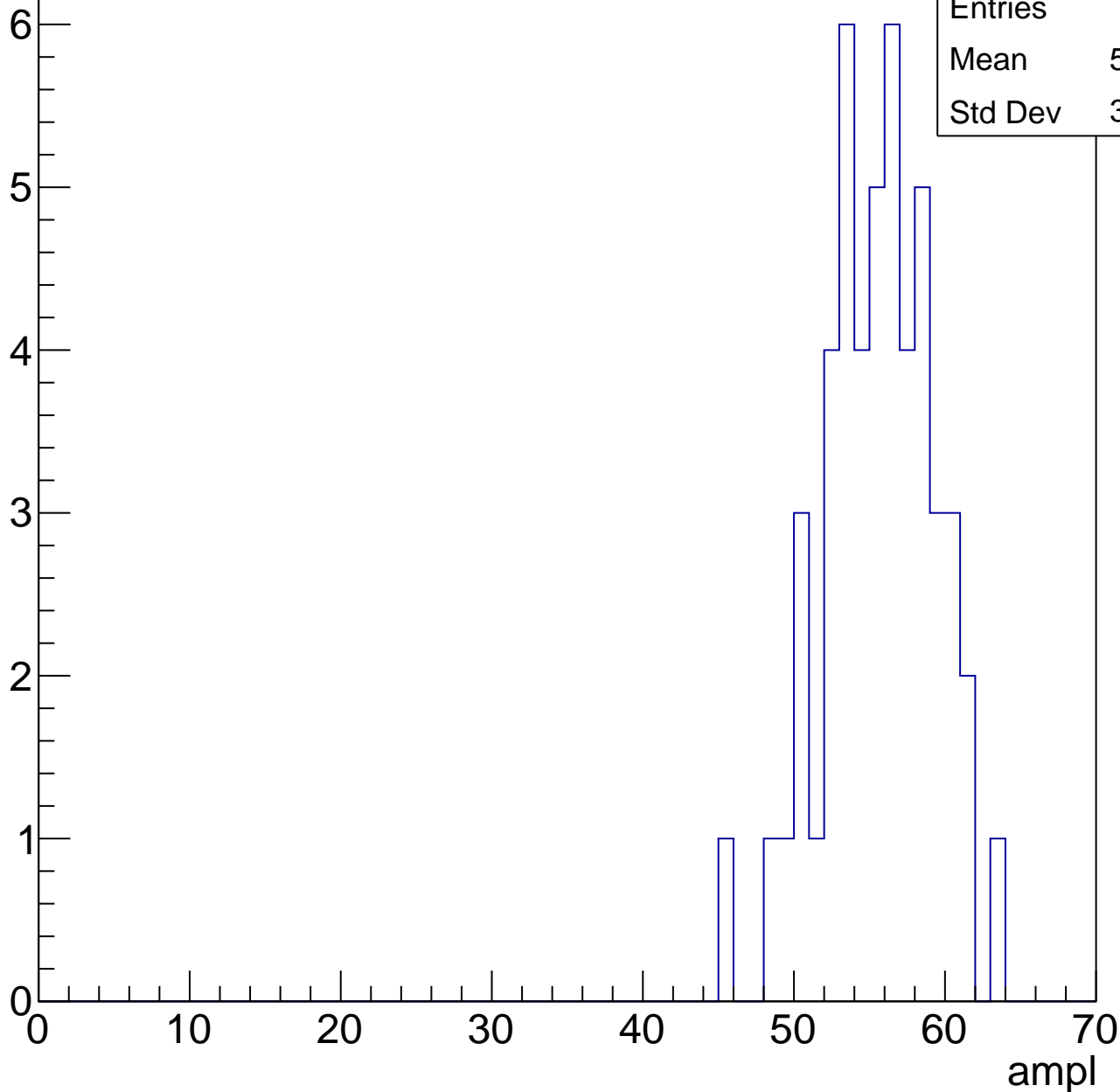


B0L000S, U4-ch80, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	55.12
Std Dev	3.648

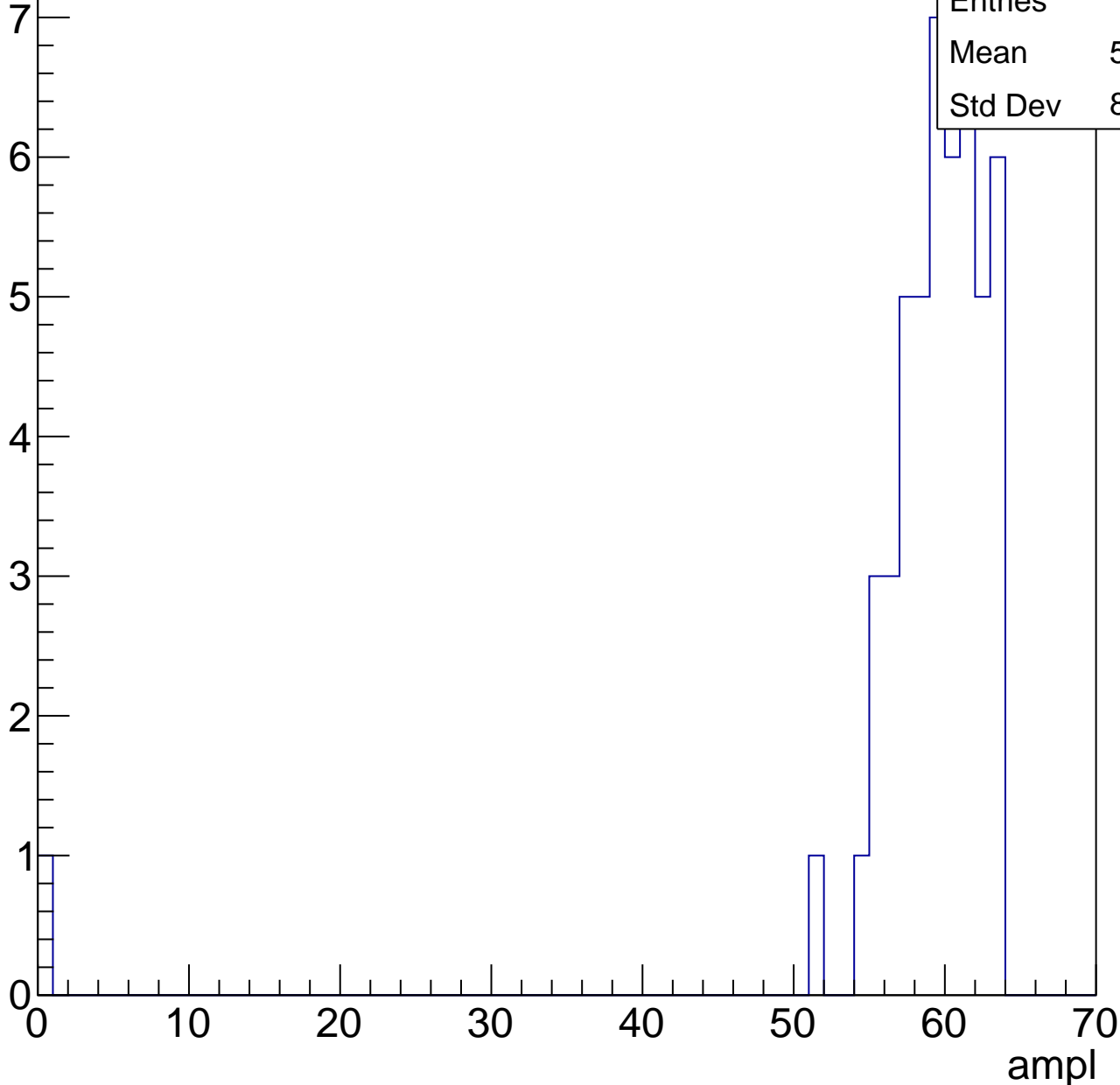


B0L000S, U4-ch80, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	58.02
Std Dev	8.714

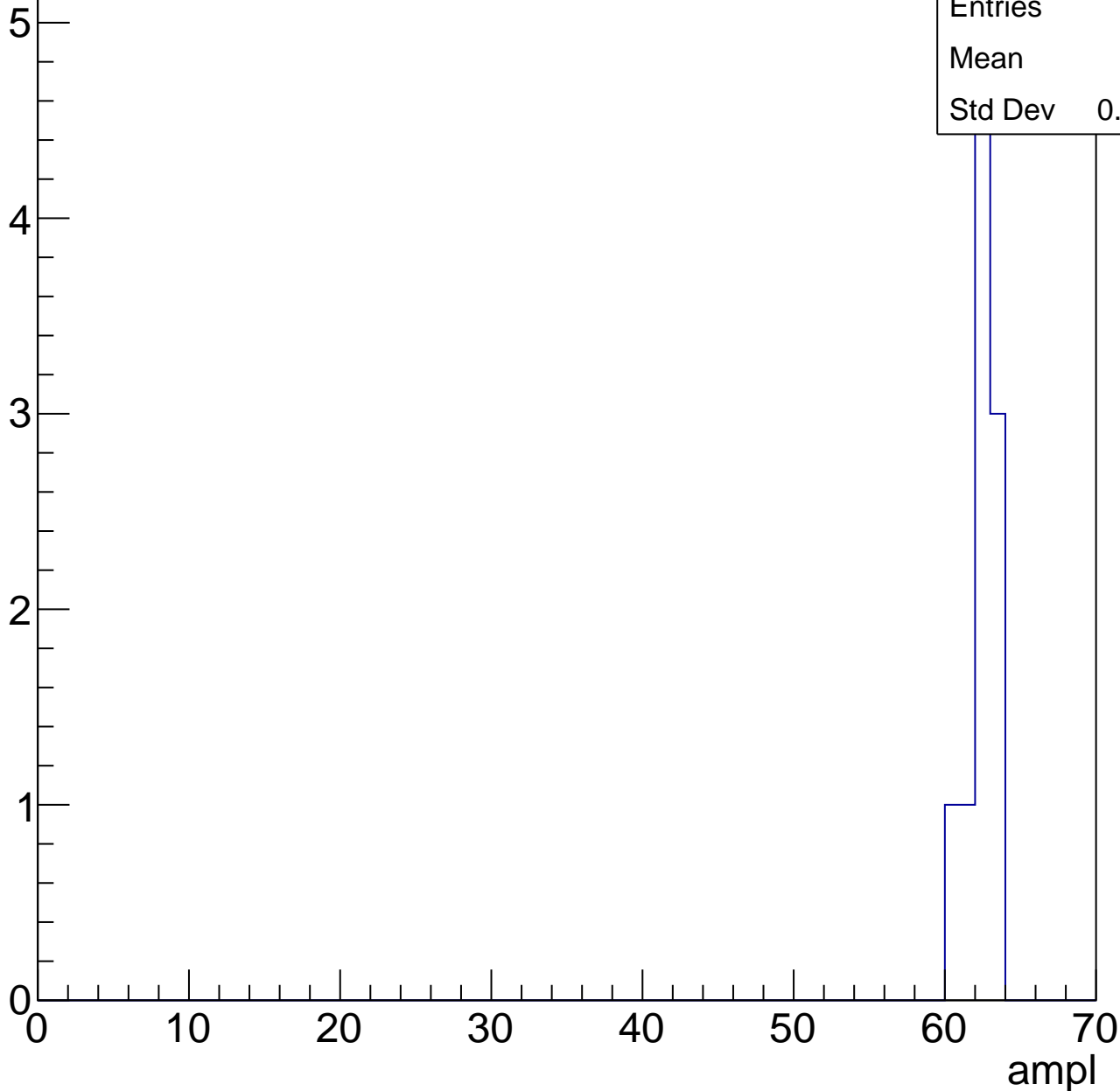


B0L000S, U4-ch80, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62
Std Dev	0.8944



B0L000S, U4-ch80, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L000S, U4-ch81, adc0

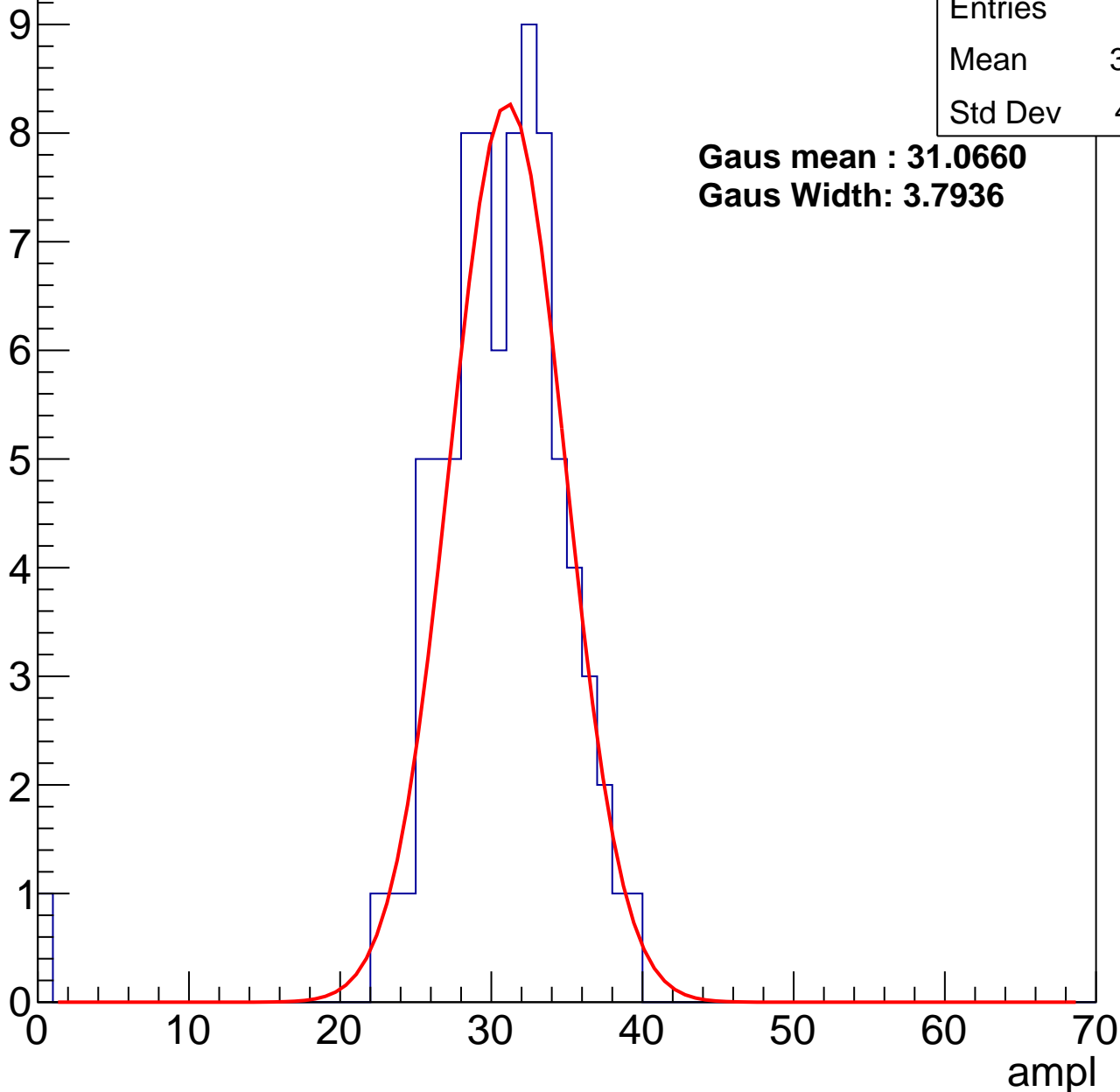
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	30.05
Std Dev	4.921

Gaus mean : 31.0660

Gaus Width: 3.7936



B0L000S, U4-ch81, adc1

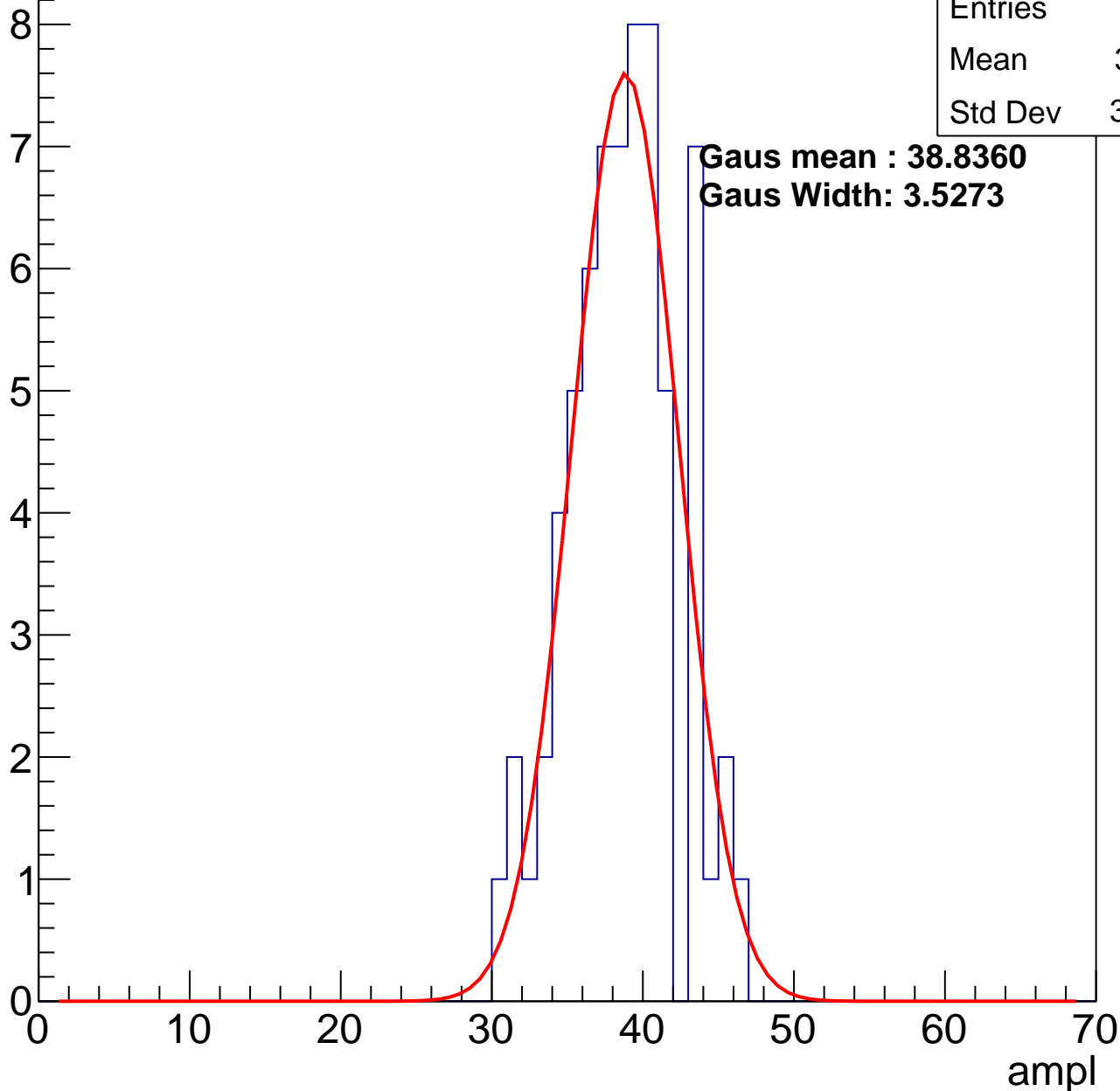
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	38.21
Std Dev	3.547

Gaus mean : 38.8360

Gaus Width: 3.5273



B0L000S, U4-ch81, adc2

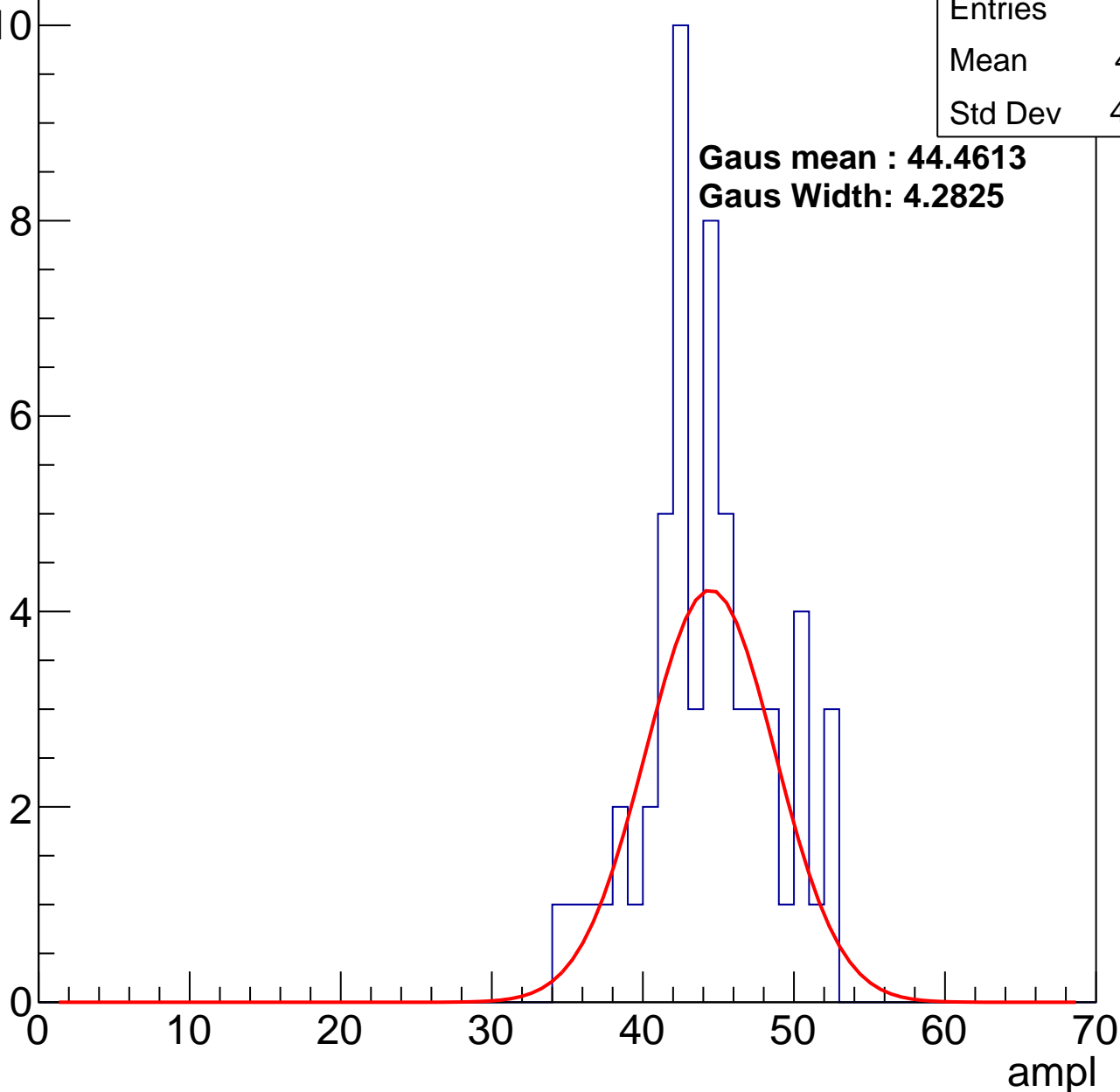
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.91
Std Dev	4.178

Gaus mean : 44.4613

Gaus Width: 4.2825

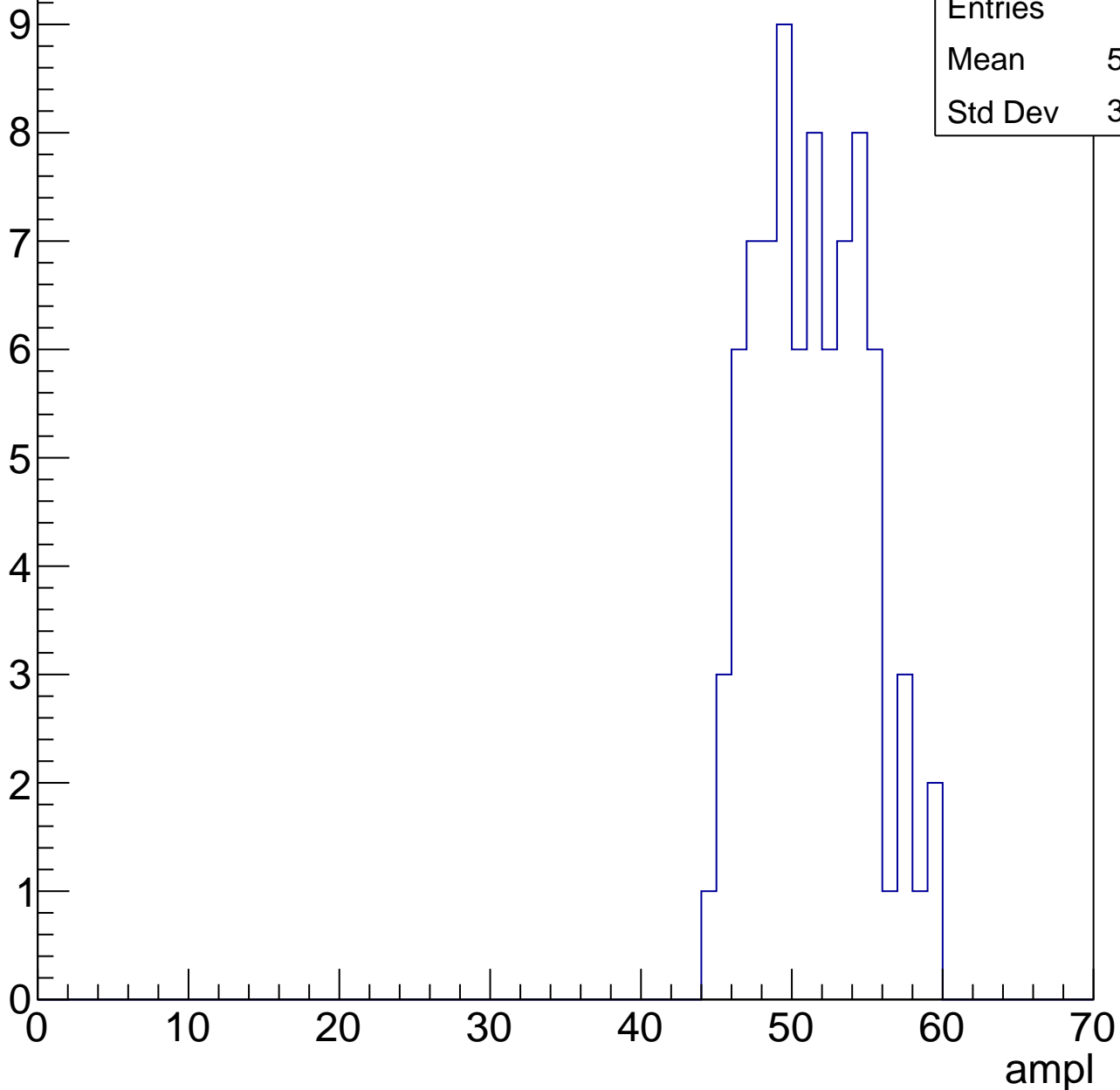


B0L000S, U4-ch81, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

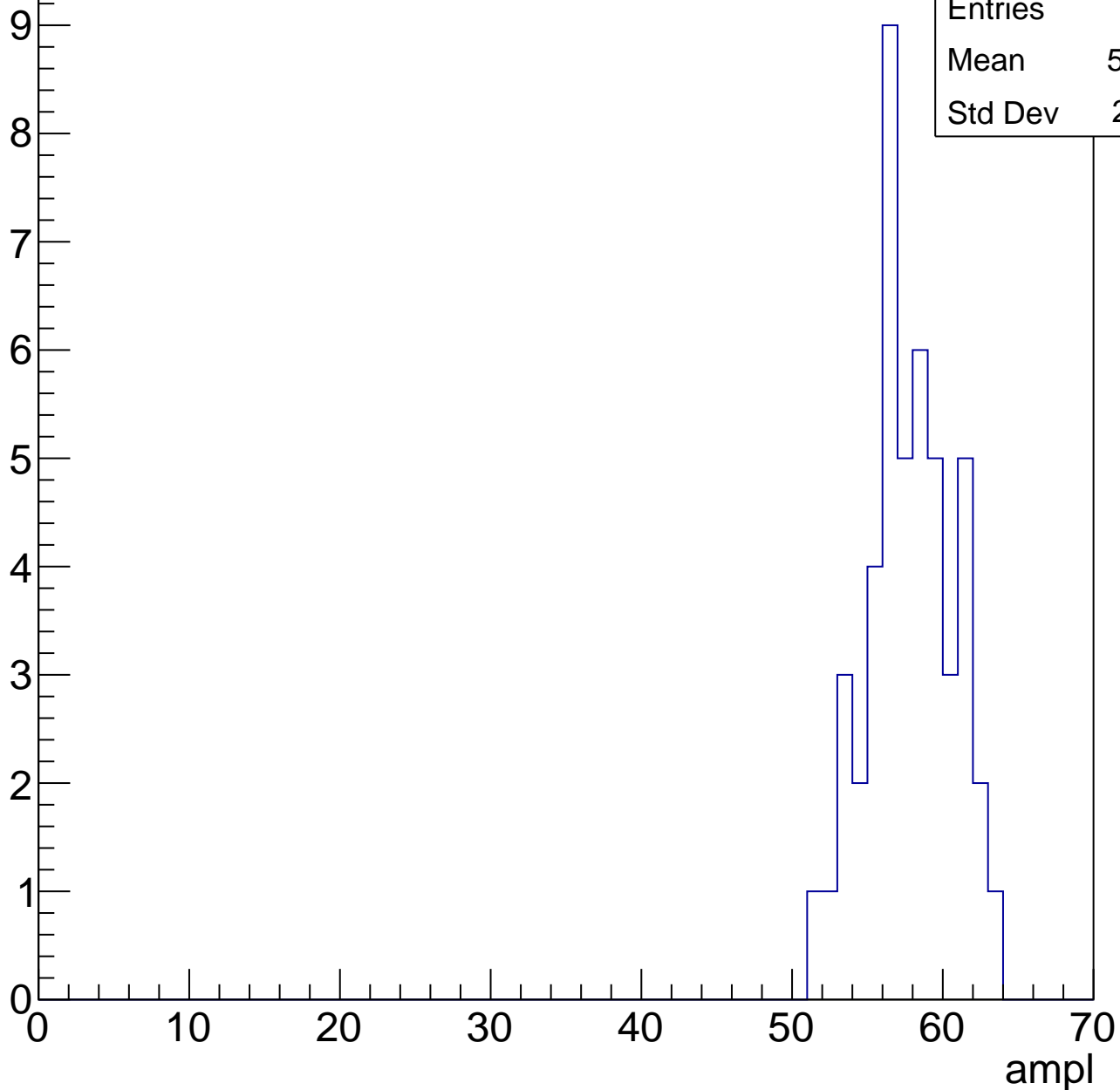
Entries	81
Mean	50.83
Std Dev	3.572



B0L000S, U4-ch81, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

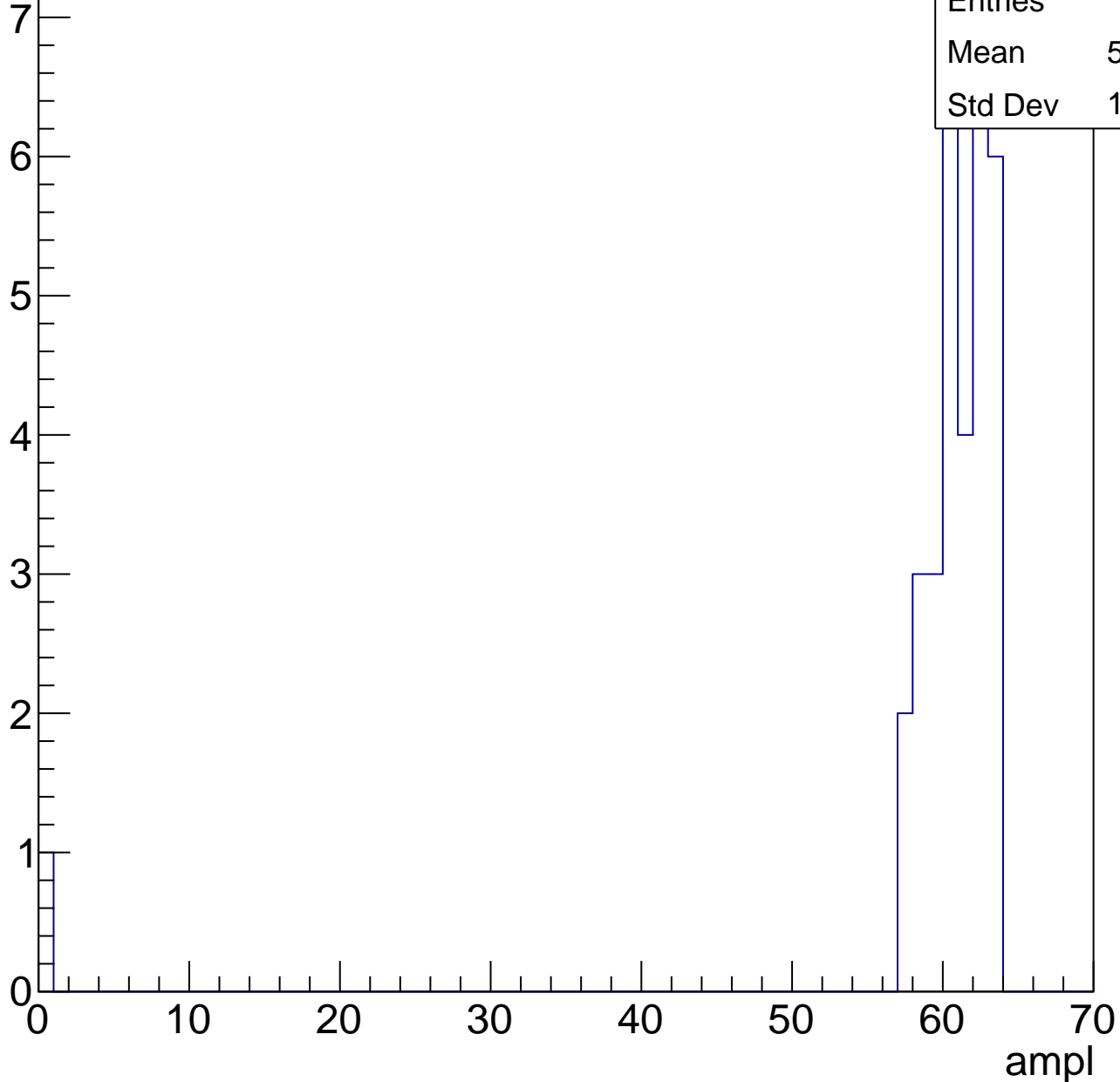


Entries	47
Mean	57.32
Std Dev	2.791

B0L000S, U4-ch81, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	33
Mean	58.82
Std Dev	10.55

B0L000S, U4-ch81, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch81, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch82, adc0

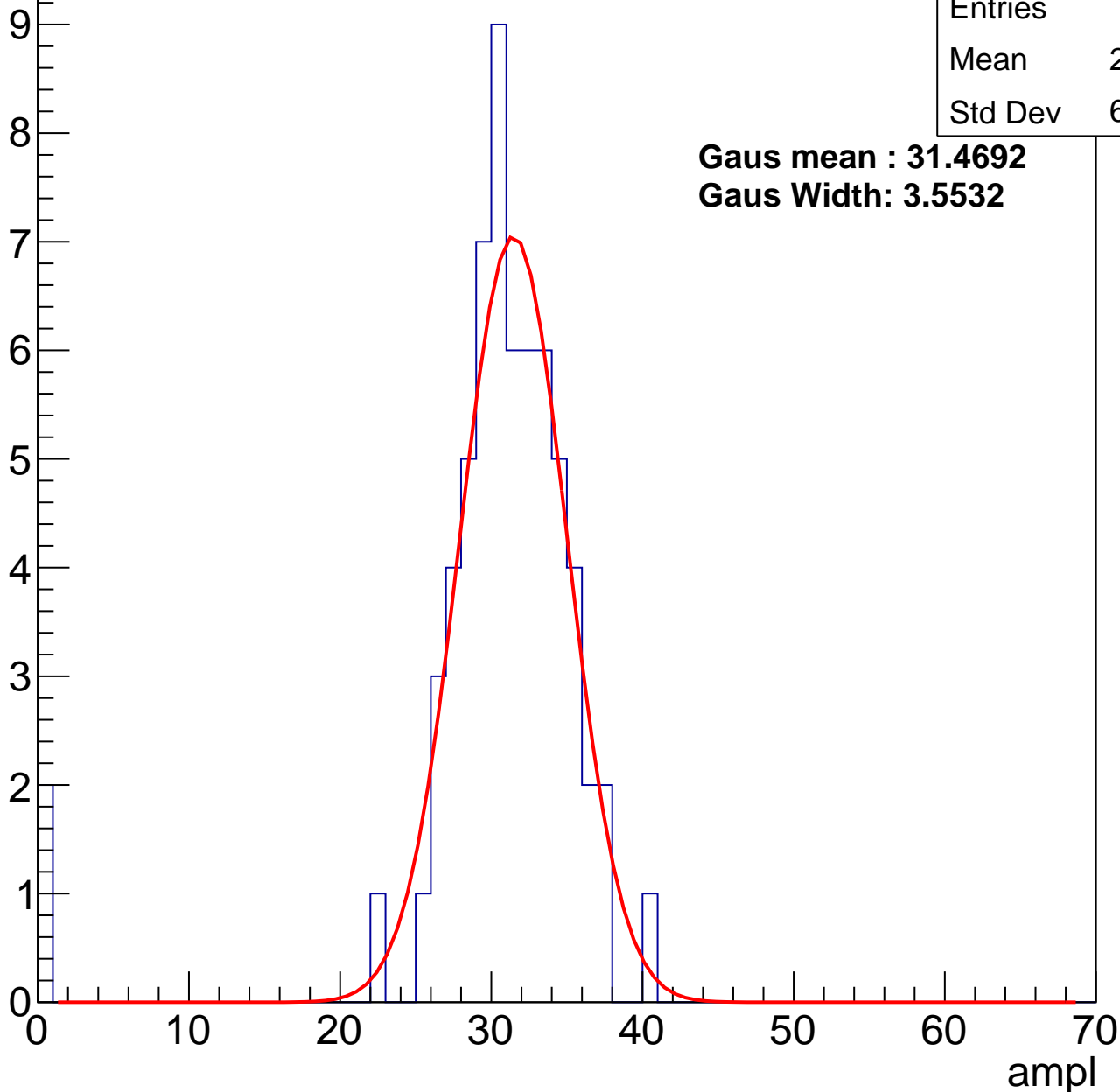
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.97
Std Dev	6.292

Gaus mean : 31.4692

Gaus Width: 3.5532



B0L000S, U4-ch82, adc1

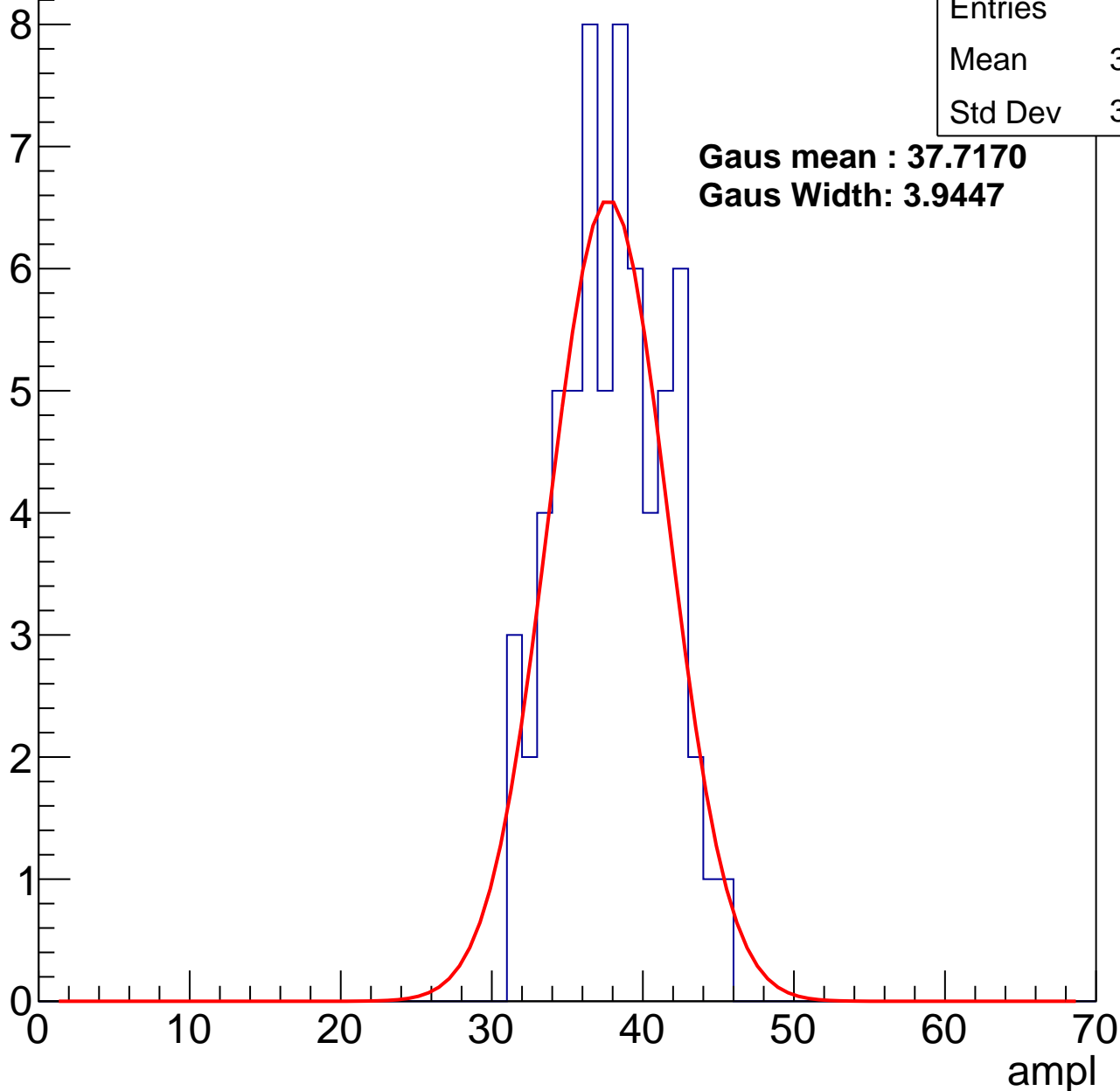
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.49
Std Dev	3.424

Gaus mean : 37.7170

Gaus Width: 3.9447



B0L000S, U4-ch82, adc2

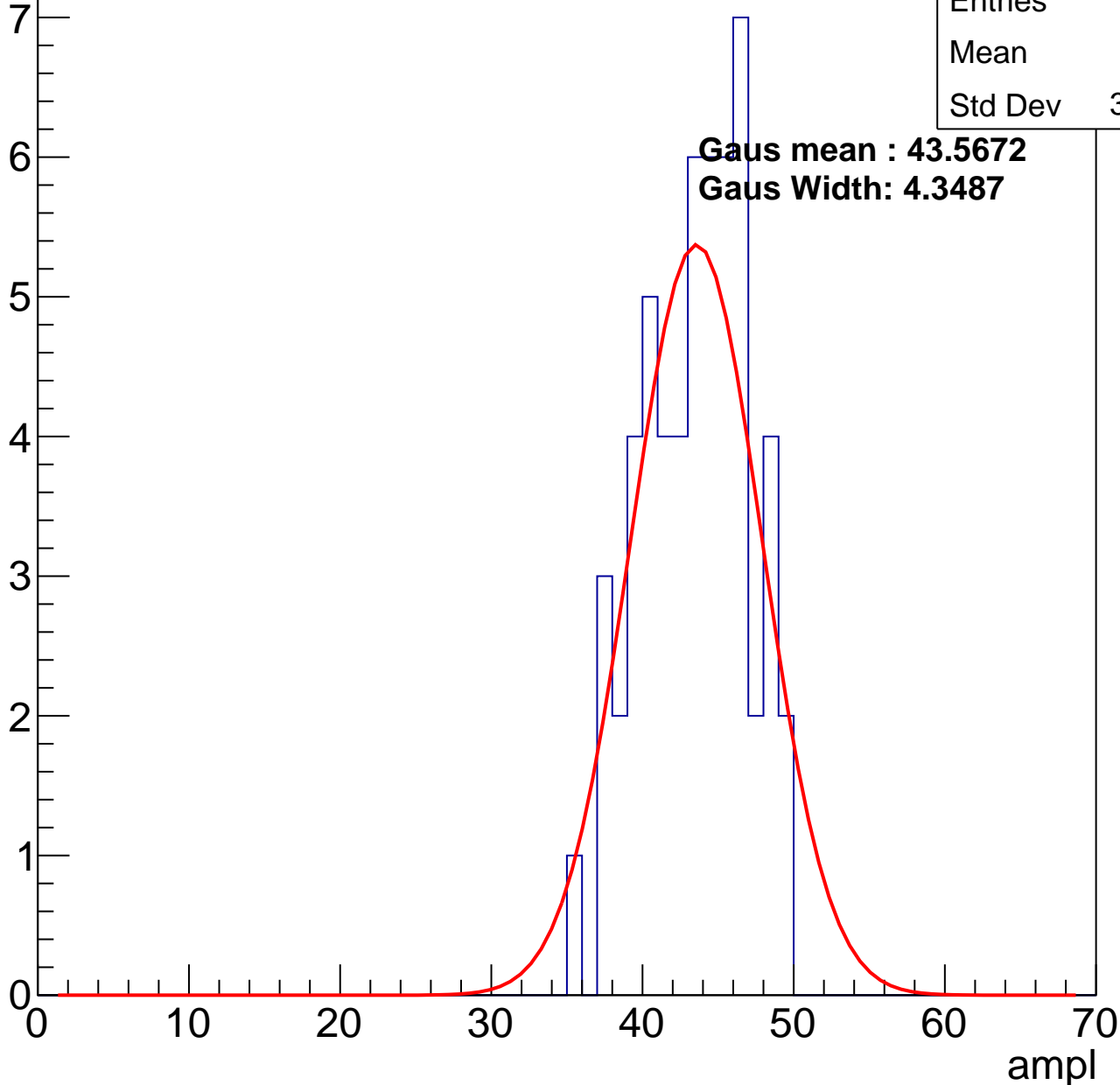
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43
Std Dev	3.402

Gaus mean : 43.5672

Gaus Width: 4.3487

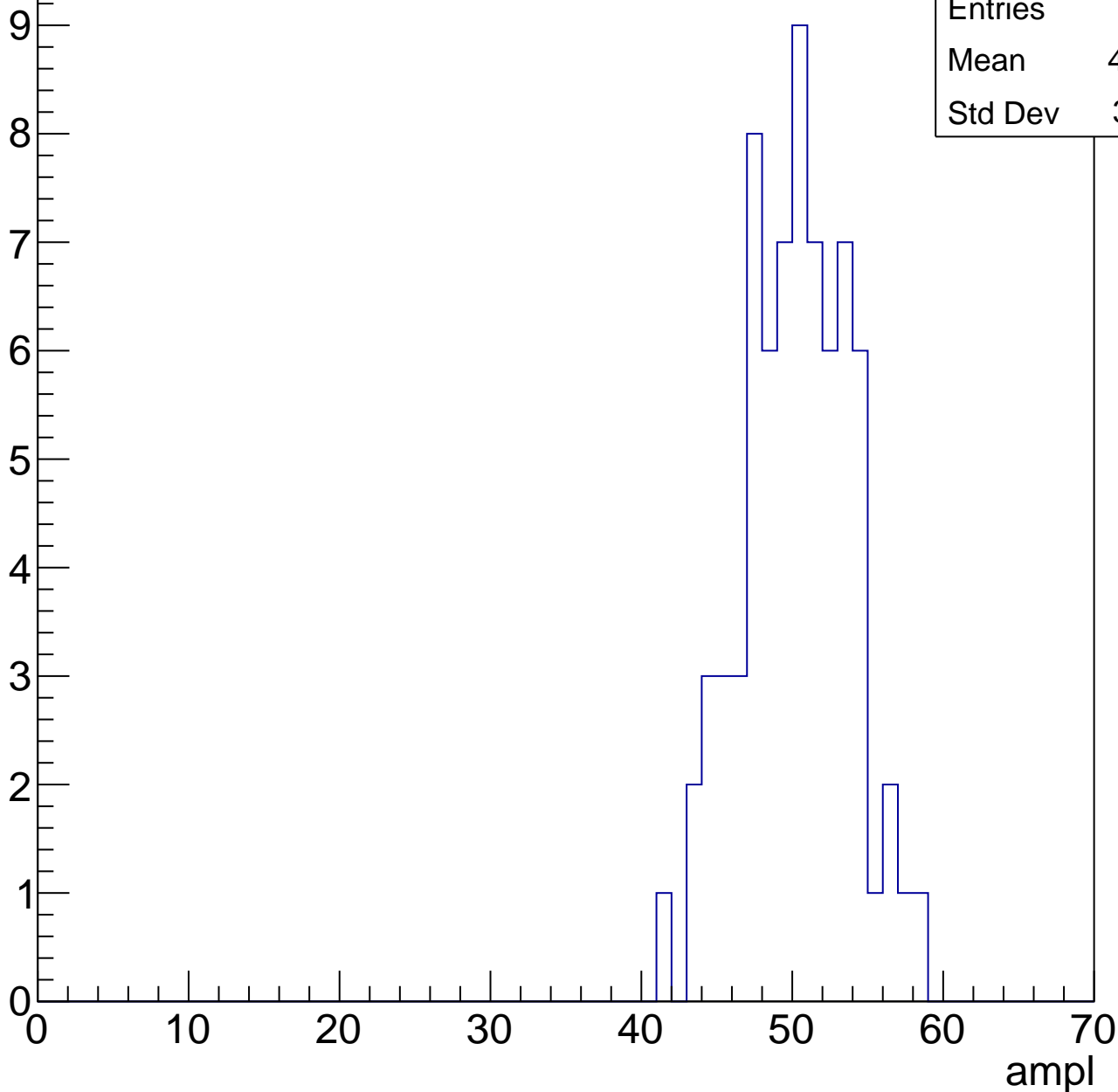


B0L000S, U4-ch82, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	49.79
Std Dev	3.531

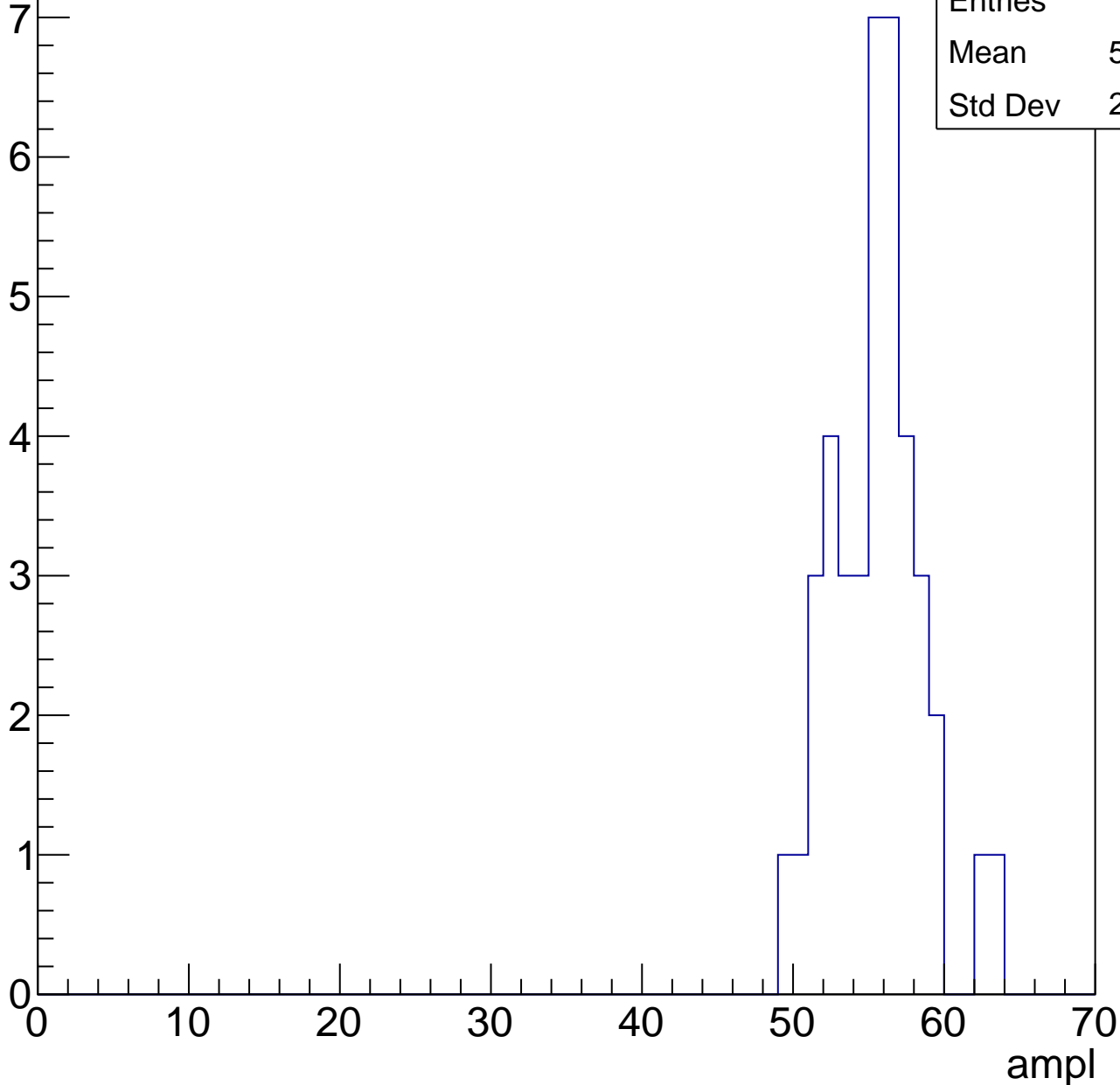


B0L000S, U4-ch82, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	40
Mean	55.08
Std Dev	2.978



B0L000S, U4-ch82, adc5

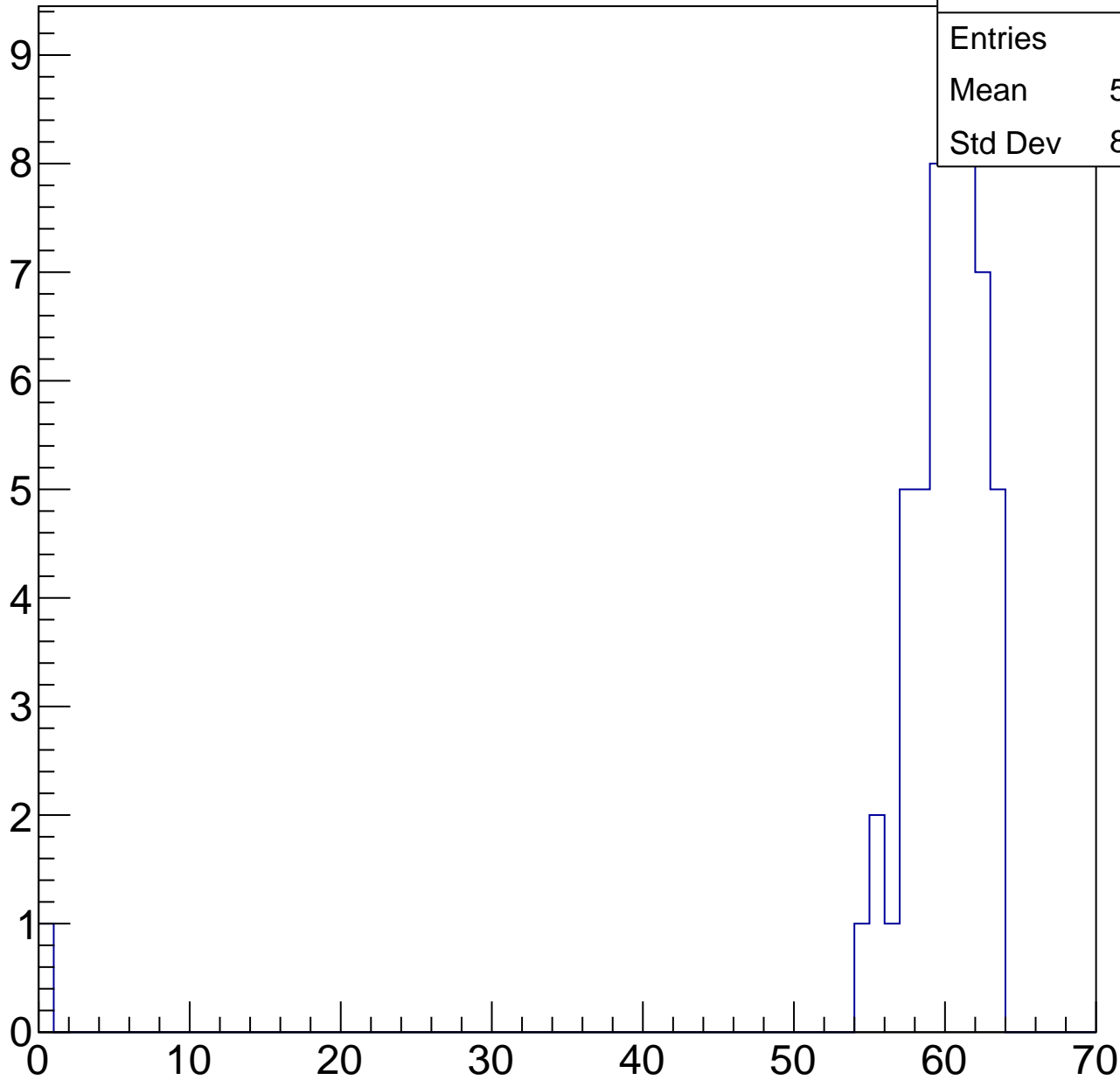
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.58
Std Dev	8.413

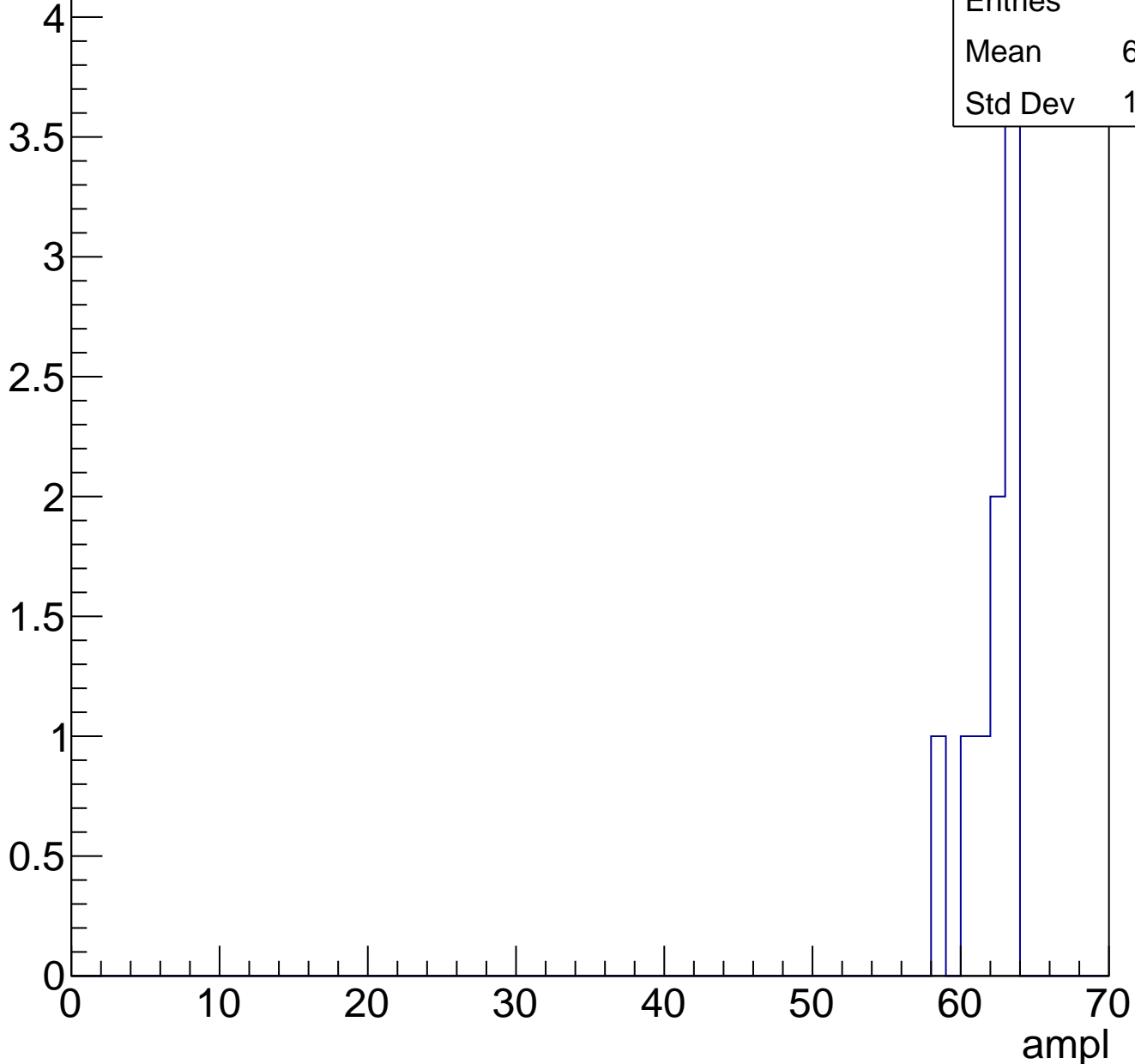
ampl



B0L000S, U4-ch82, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	9
Mean	61.67
Std Dev	1.633

B0L000S, U4-ch82, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch83, adc0

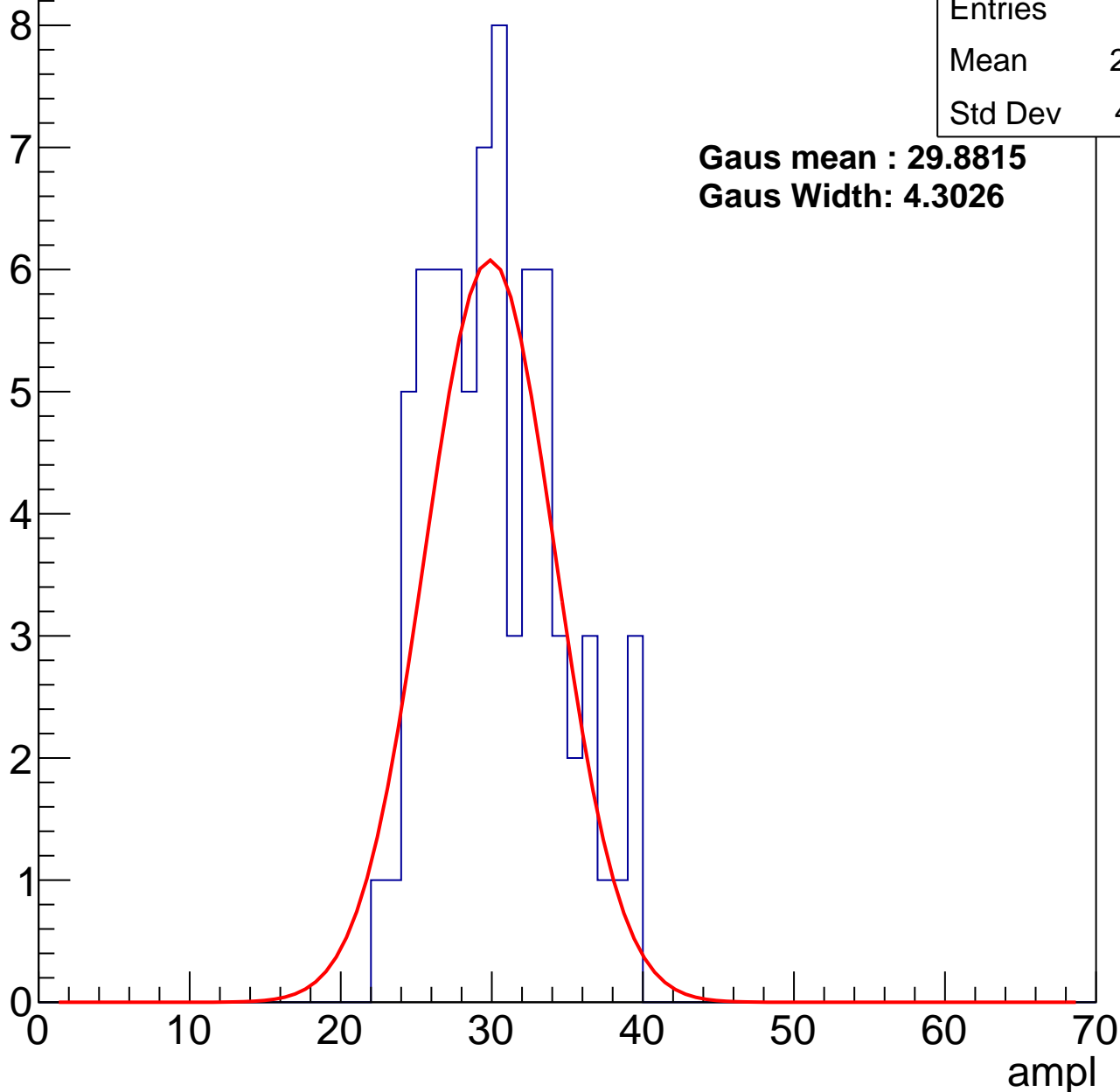
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.74
Std Dev	4.151

Gaus mean : 29.8815

Gaus Width: 4.3026



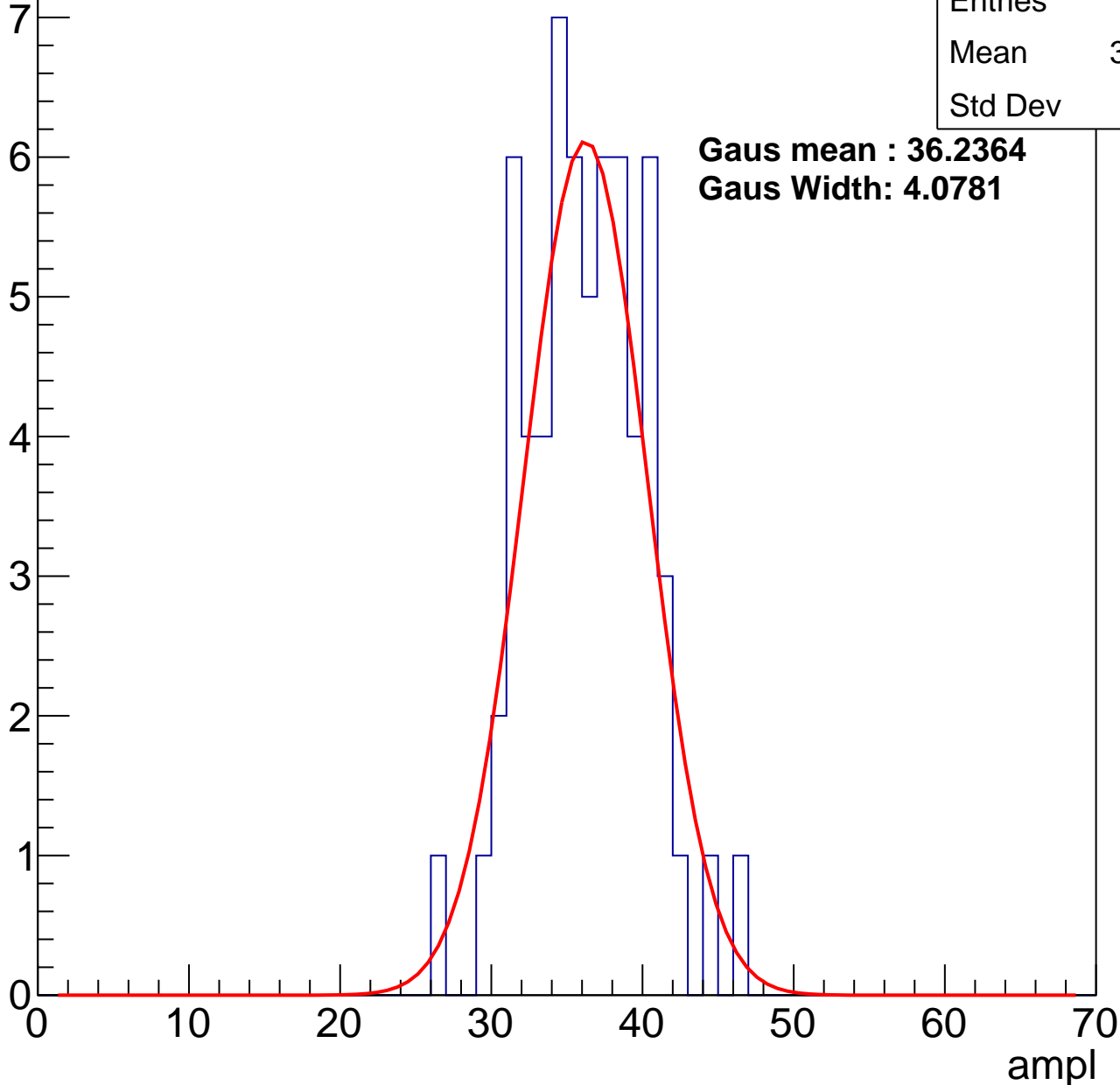
B0L000S, U4-ch83, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	35.78
Std Dev	3.83

Gaus mean : 36.2364
Gaus Width: 4.0781



B0L000S, U4-ch83, adc2

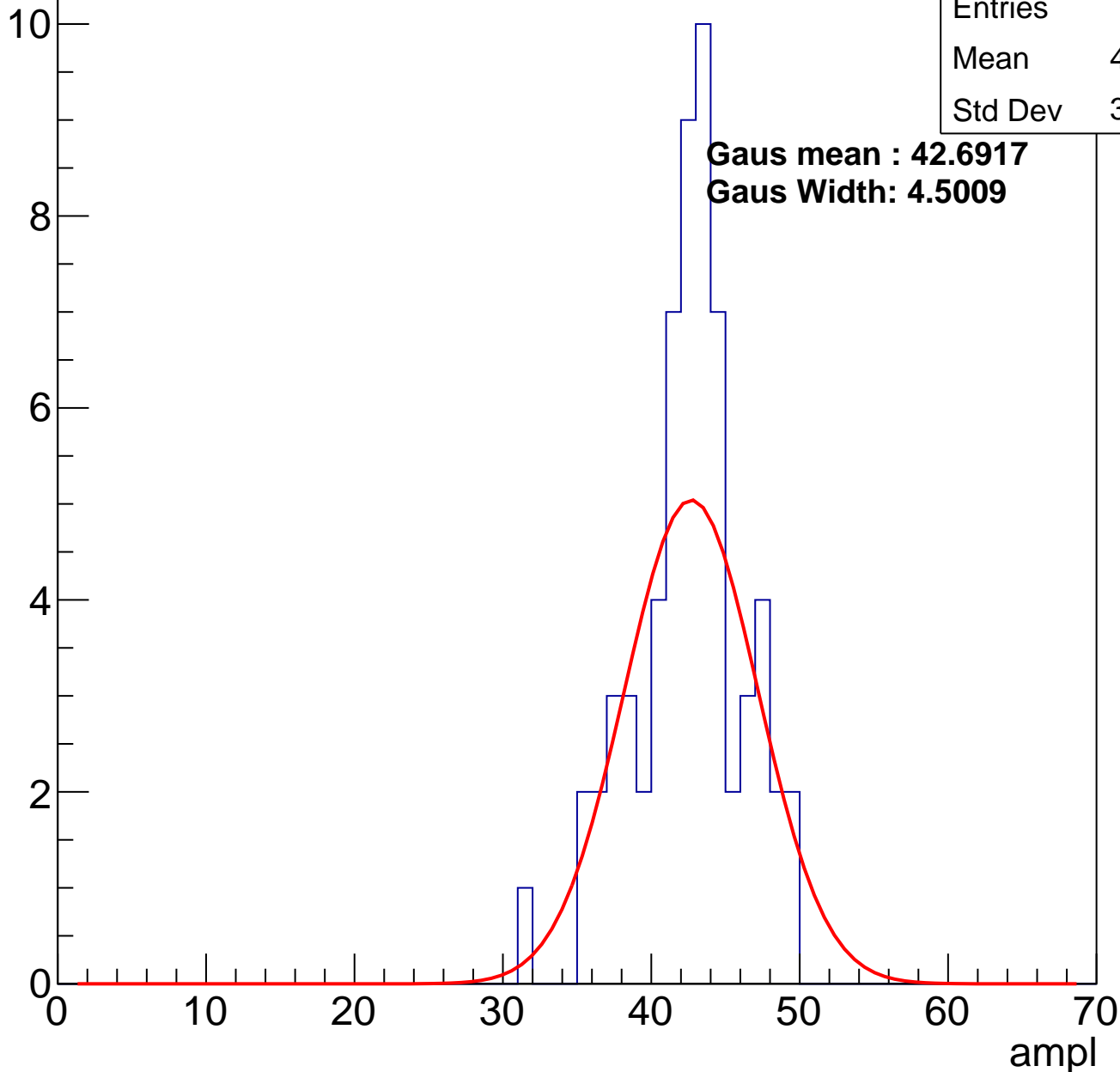
calib_packv5_042523_0143.root, FC#5, port B1

Entries	63
Mean	42.05
Std Dev	3.627

Gaus mean : 42.6917

Gaus Width: 4.5009

Entry

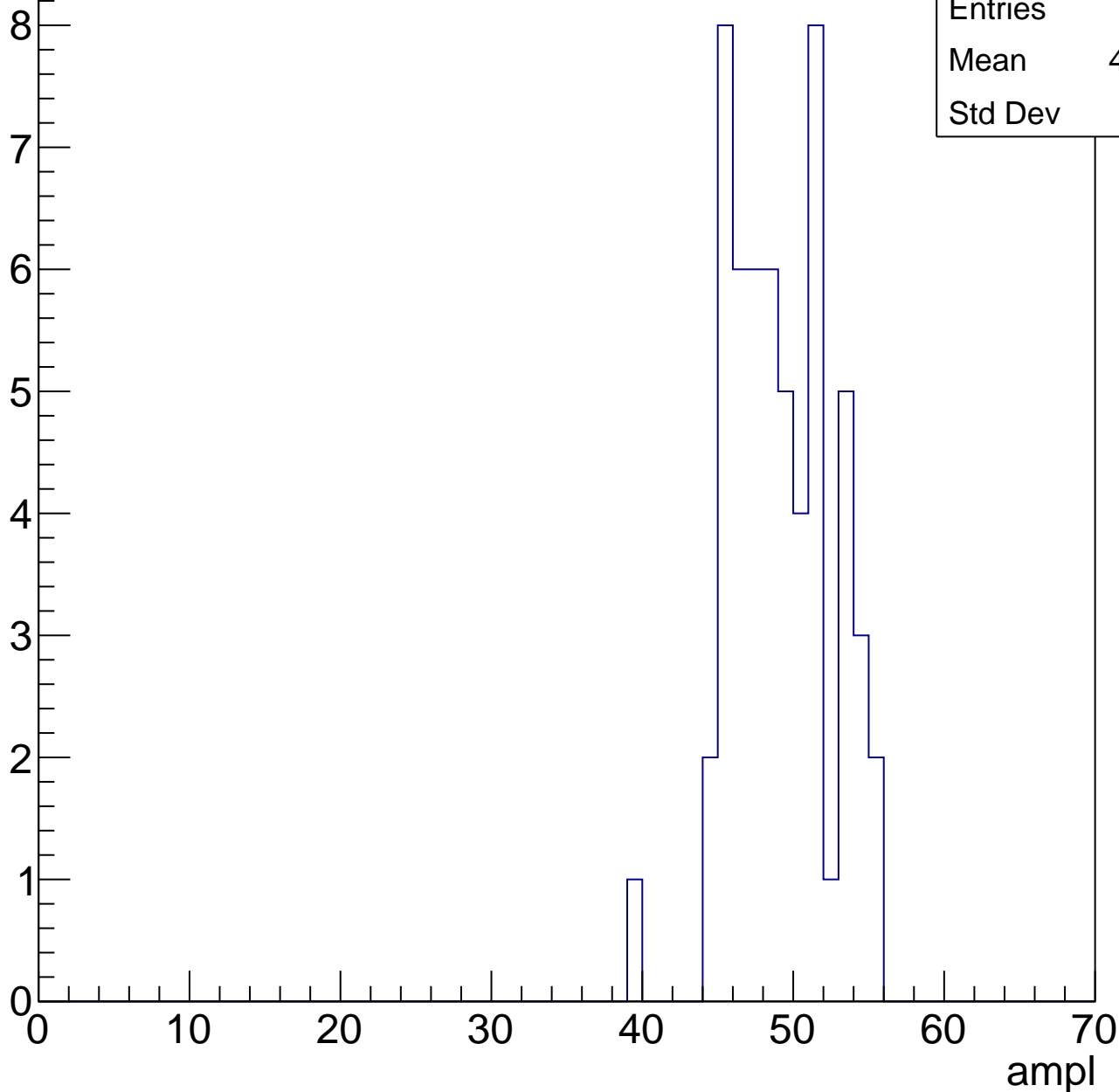


B0L000S, U4-ch83, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	48.68
Std Dev	3.32

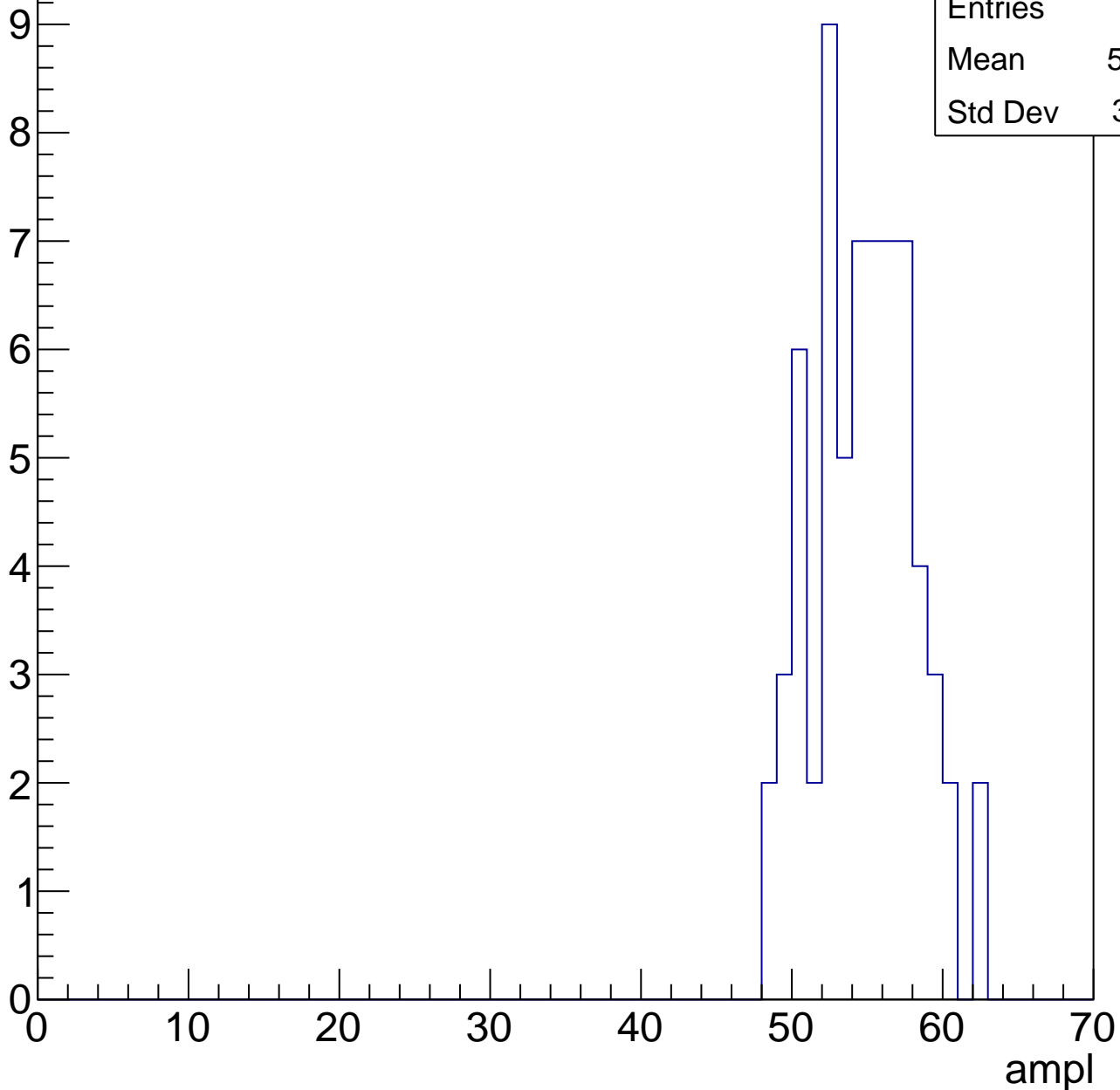


B0L000S, U4-ch83, adc4

calib_packv5_042523_0143.root, FC#5, port B1

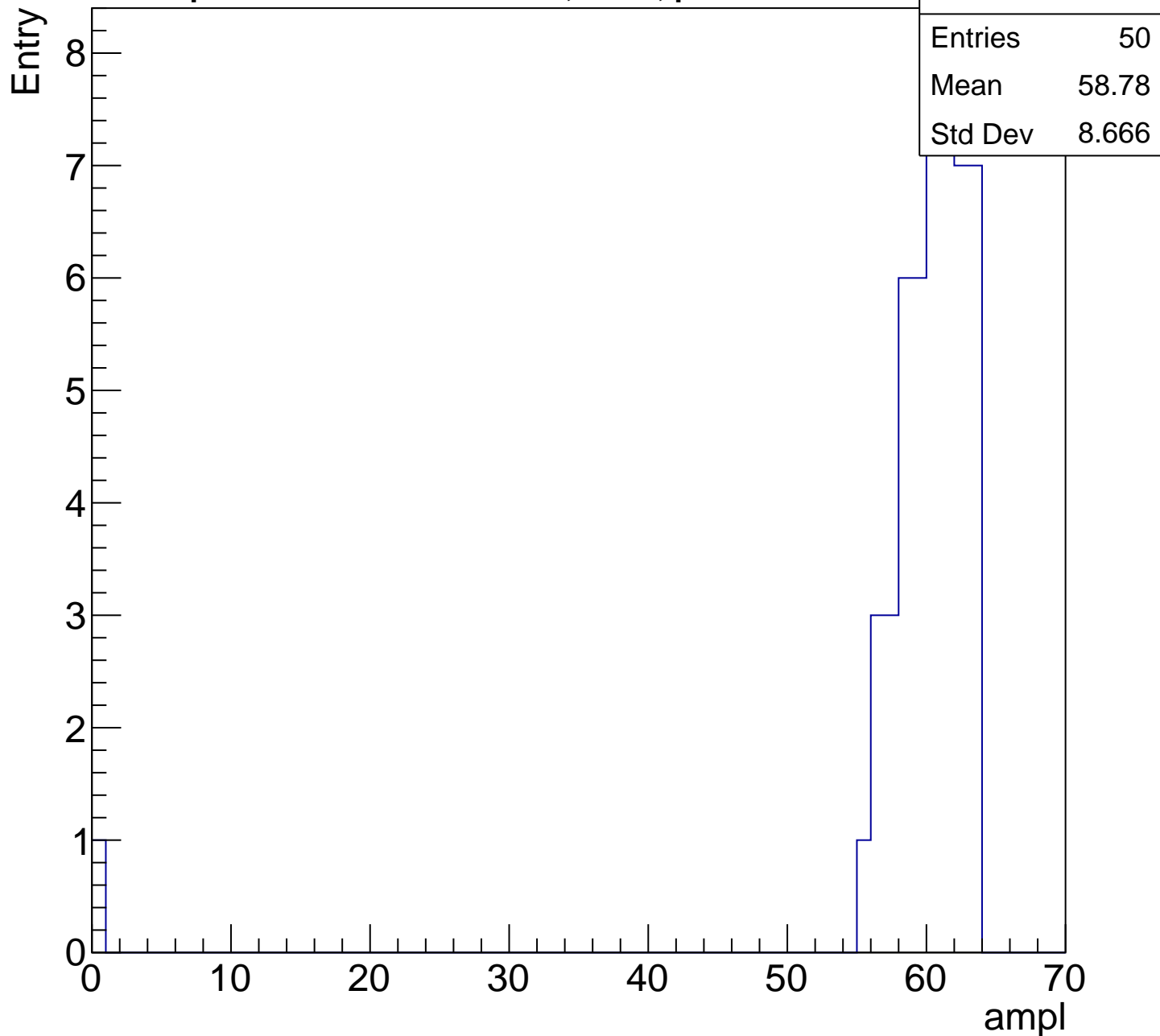
Entry

Entries	66
Mean	54.32
Std Dev	3.331



B0L000S, U4-ch83, adc5

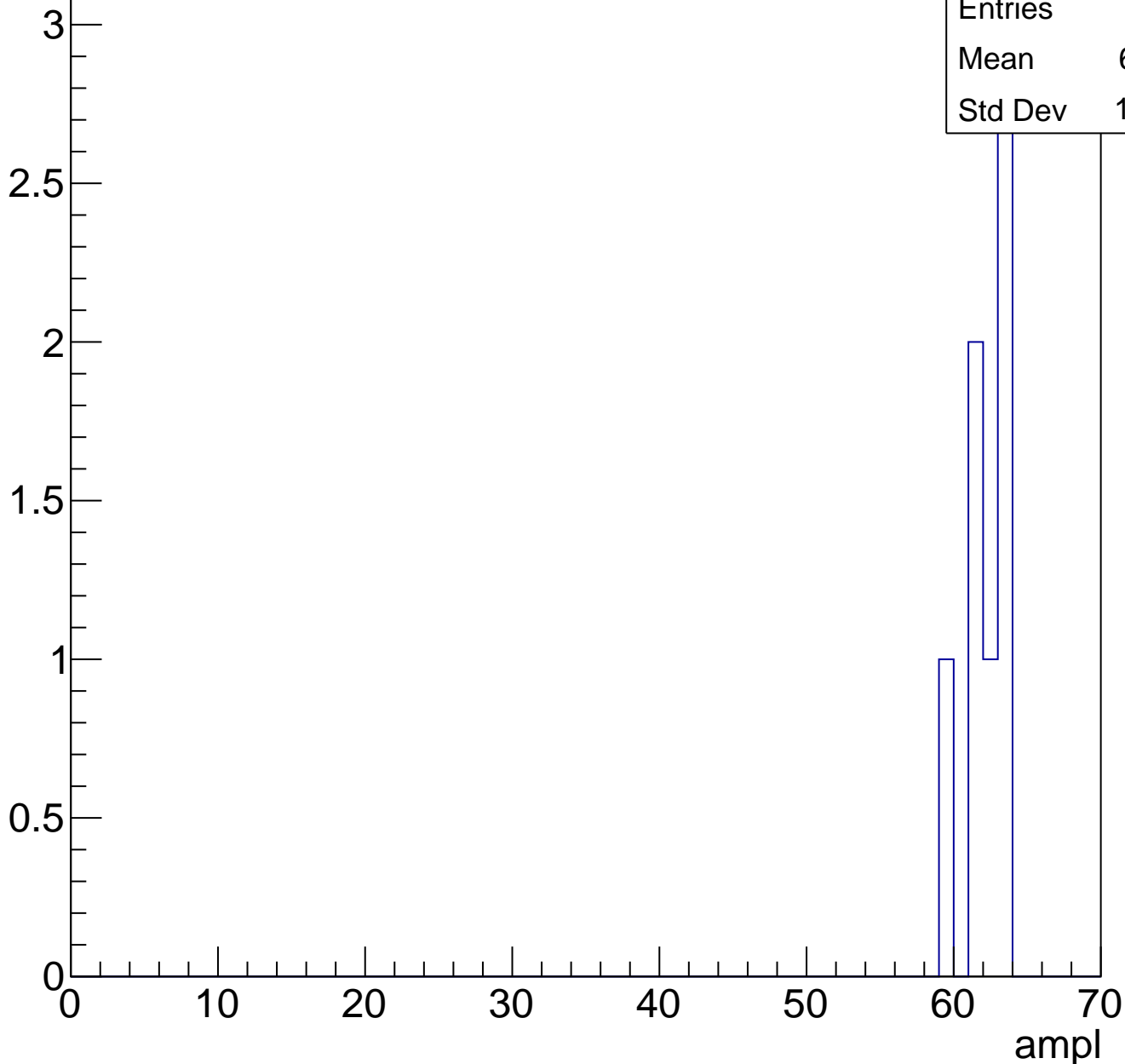
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch83, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch83, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch84, adc0

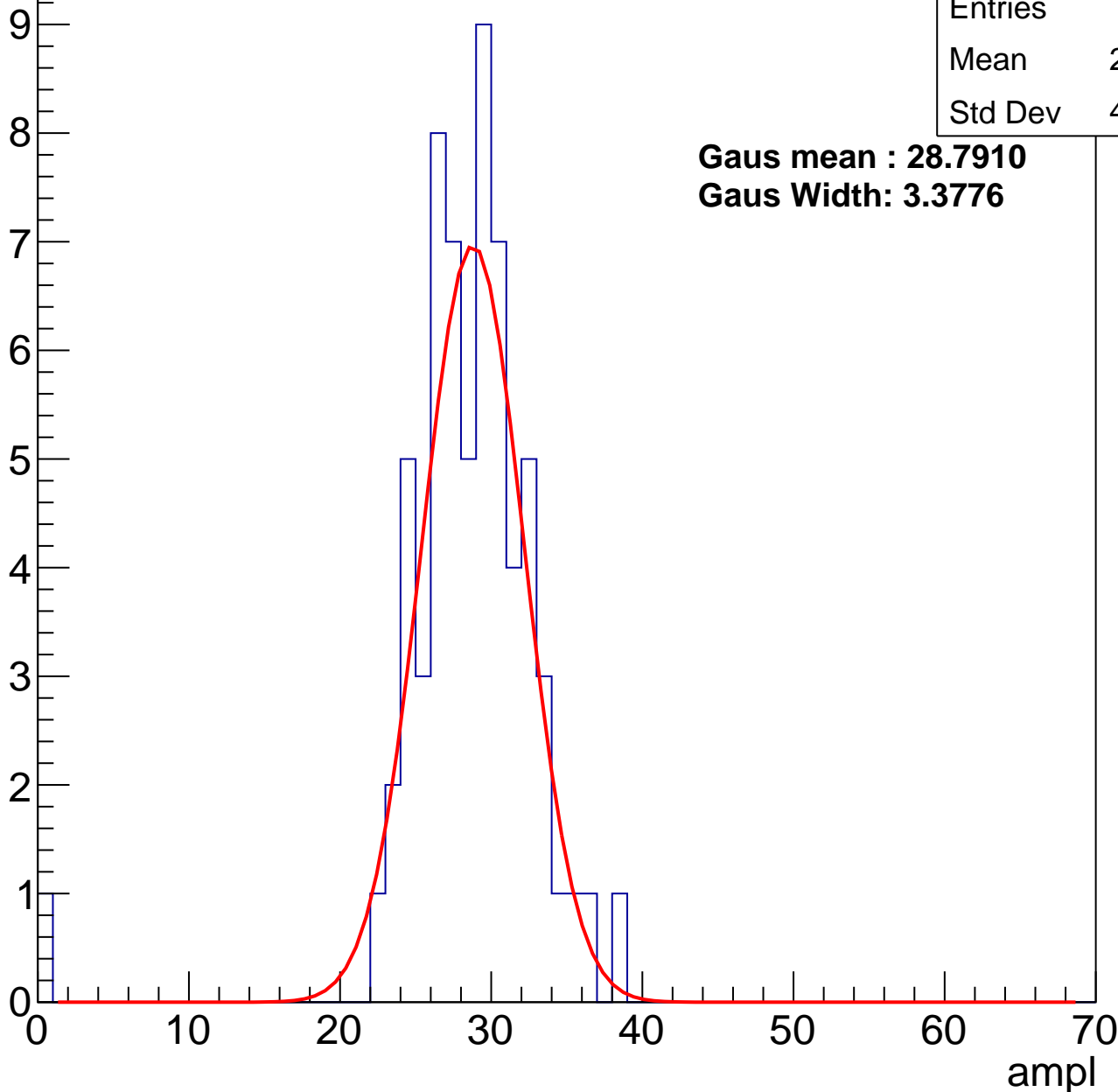
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	28.08
Std Dev	4.829

Gaus mean : 28.7910

Gaus Width: 3.3776



B0L000S, U4-ch84, adc1

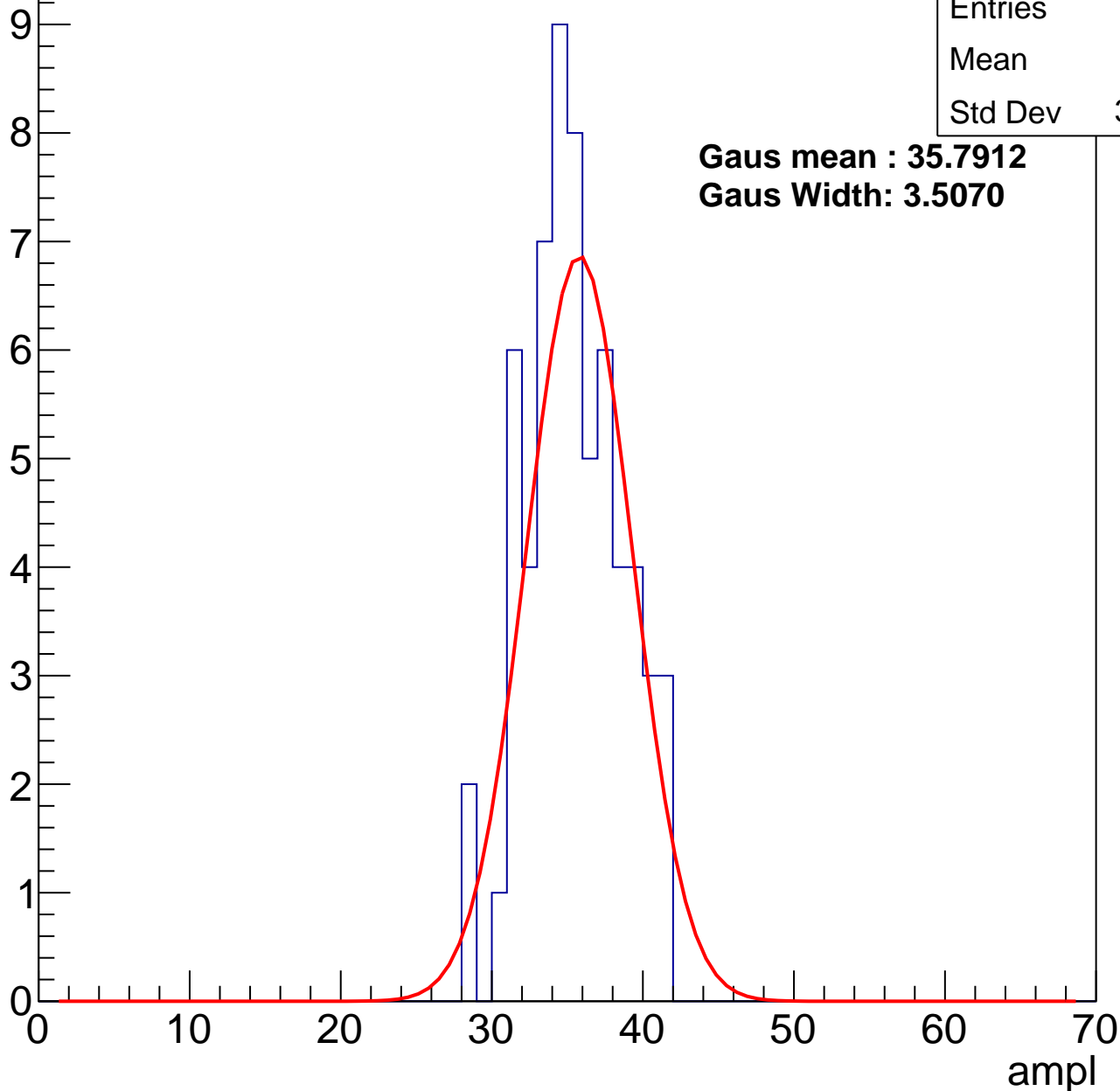
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	35
Std Dev	3.121

Gaus mean : 35.7912

Gaus Width: 3.5070



B0L000S, U4-ch84, adc2

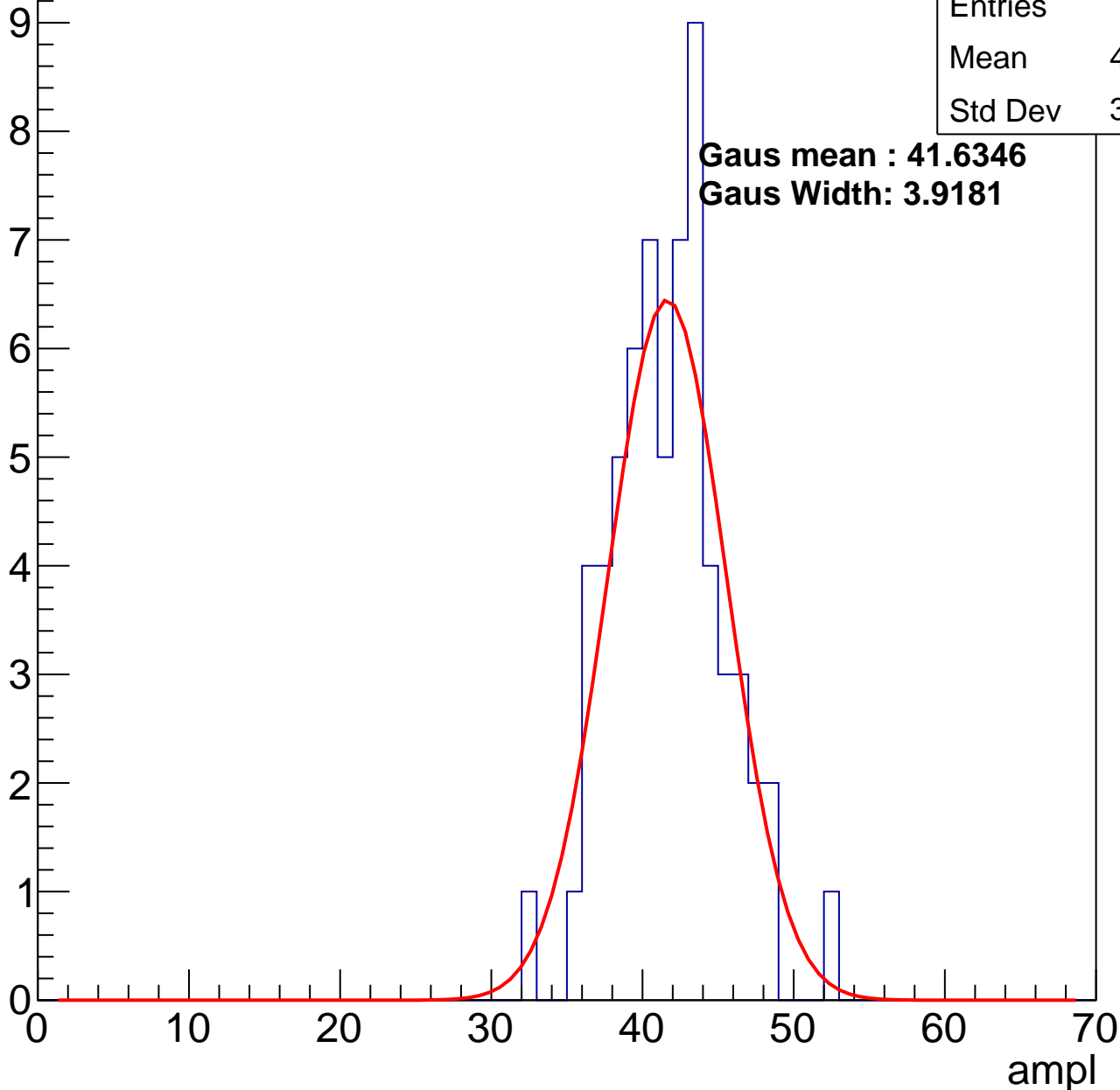
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.25
Std Dev	3.636

Gaus mean : 41.6346

Gaus Width: 3.9181

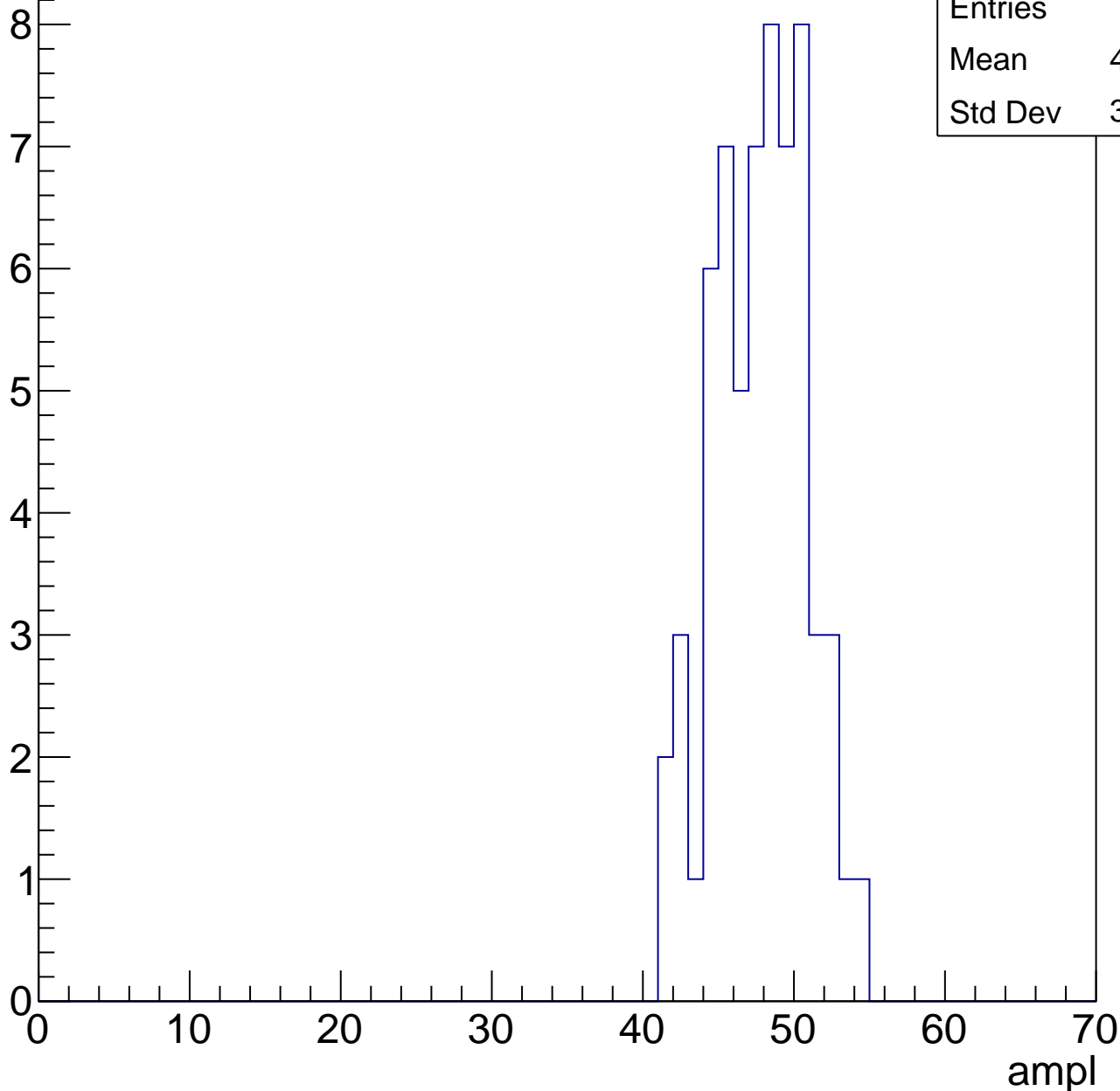


B0L000S, U4-ch84, adc3

calib_packv5_042523_0143.root, FC#5, port B1

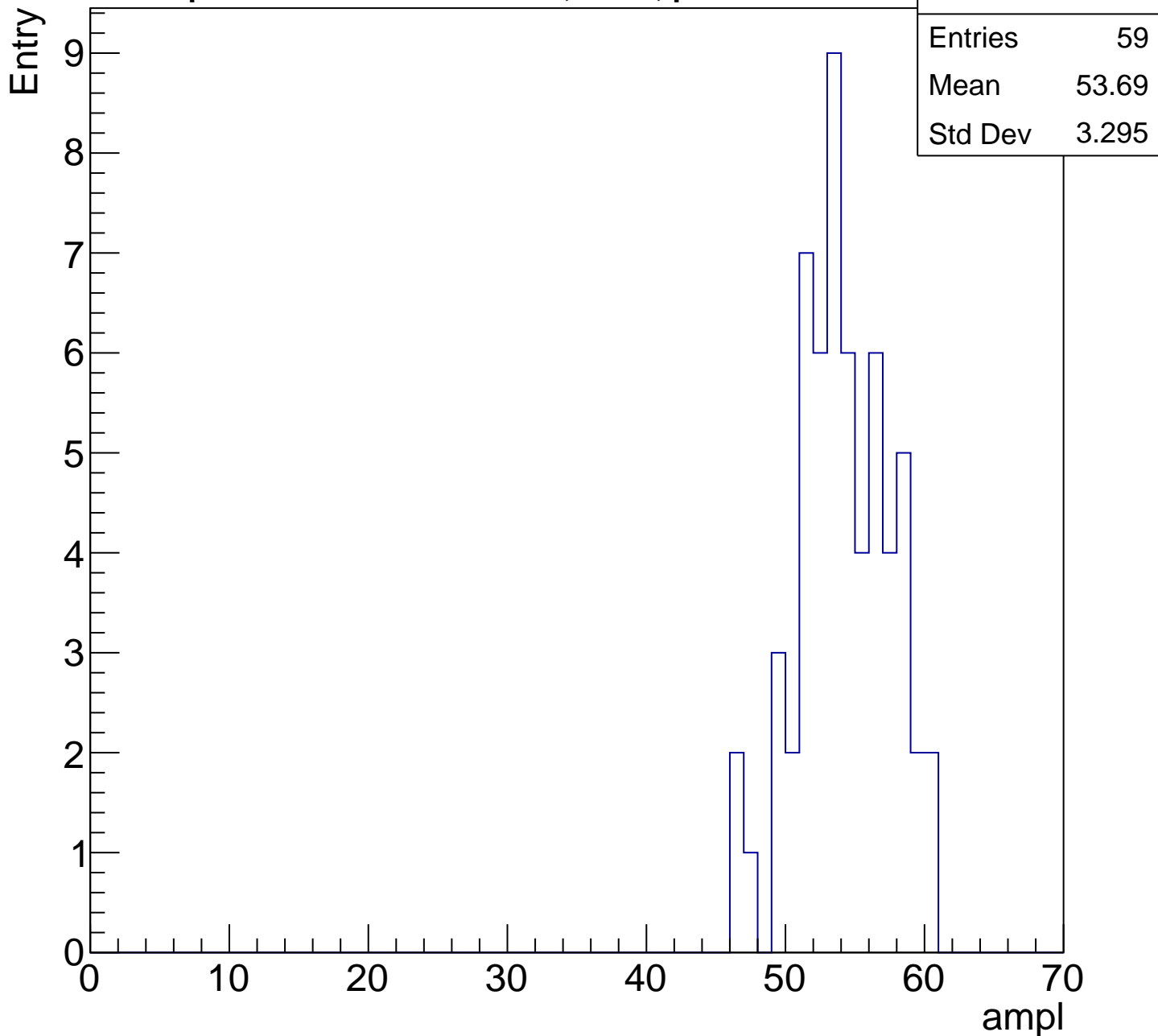
Entry

Entries	62
Mean	47.29
Std Dev	3.007



B0L000S, U4-ch84, adc4

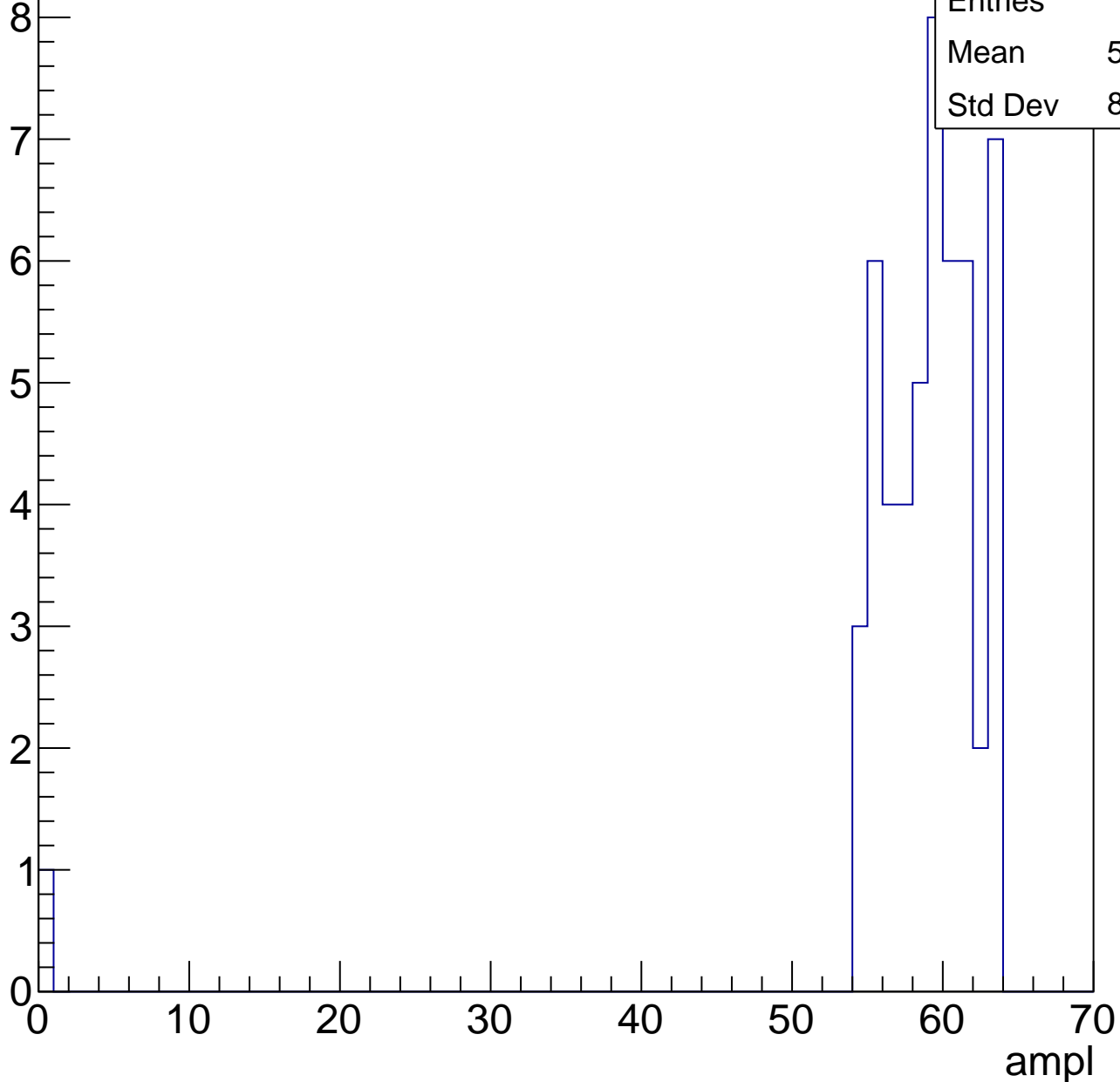
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch84, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



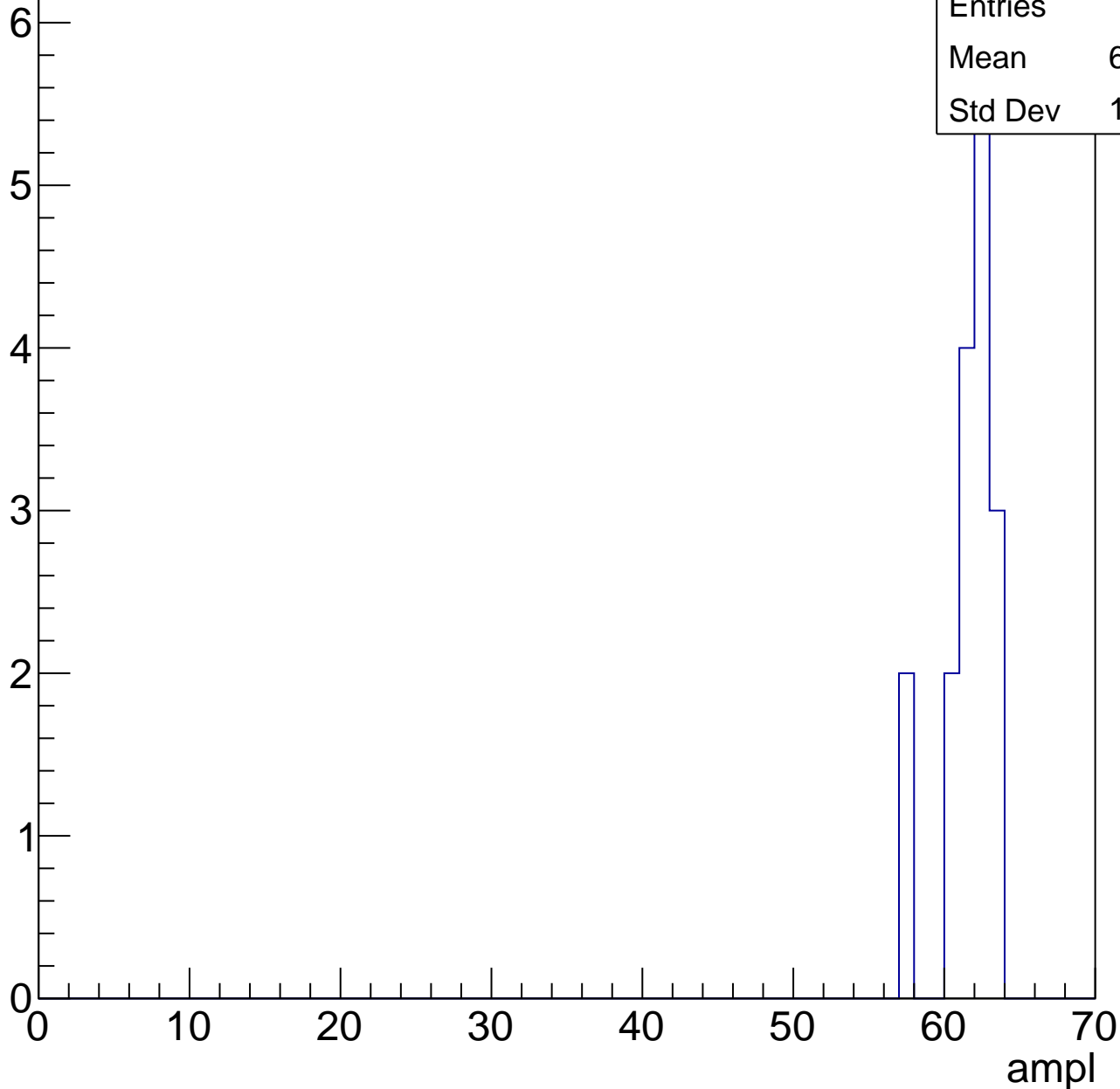
Entries	52
Mean	57.63
Std Dev	8.517

B0L000S, U4-ch84, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

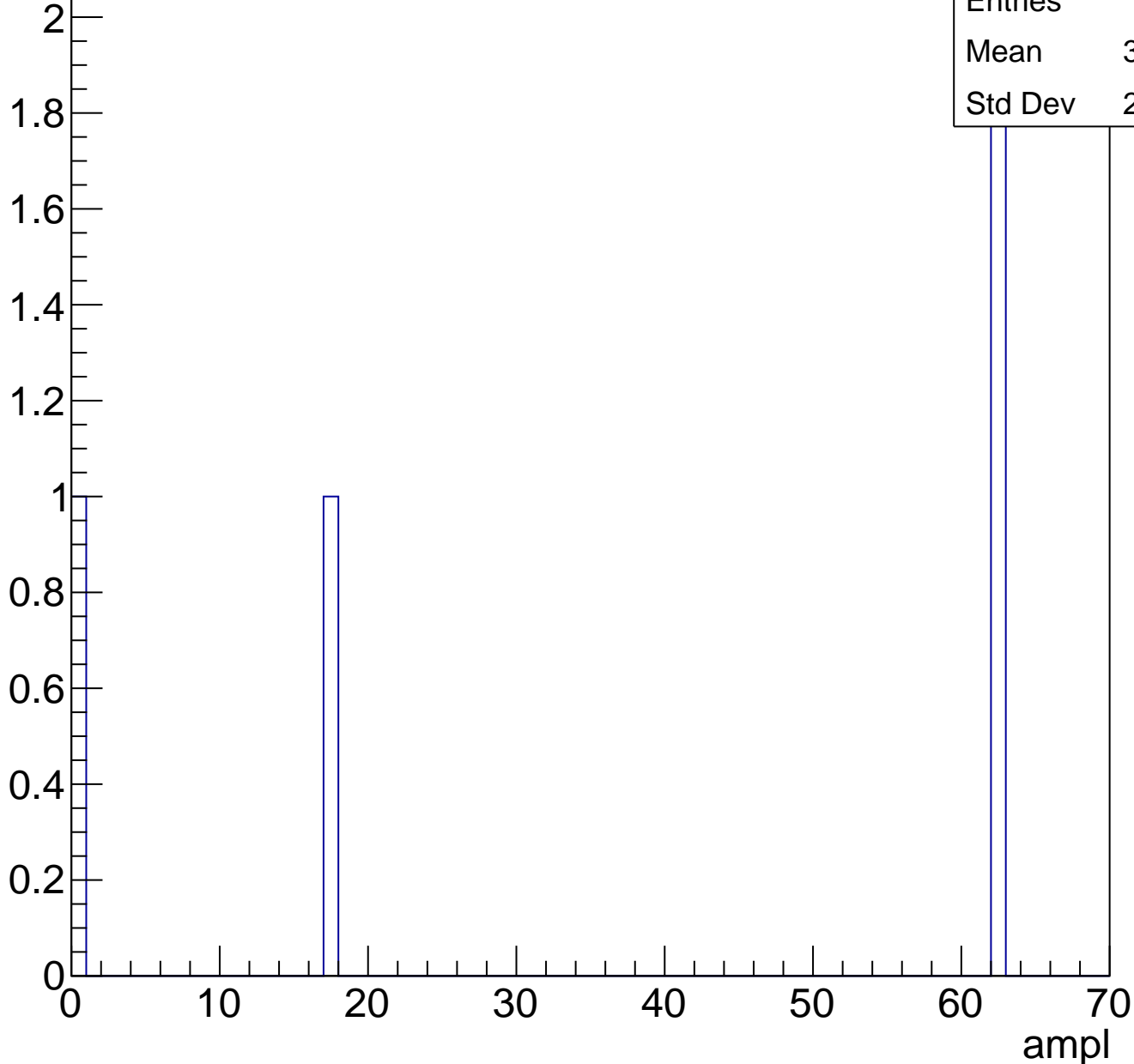
Entries	17
Mean	61.12
Std Dev	1.745



B0L000S, U4-ch84, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	4
Mean	35.25
Std Dev	27.42

B0L000S, U4-ch85, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entry

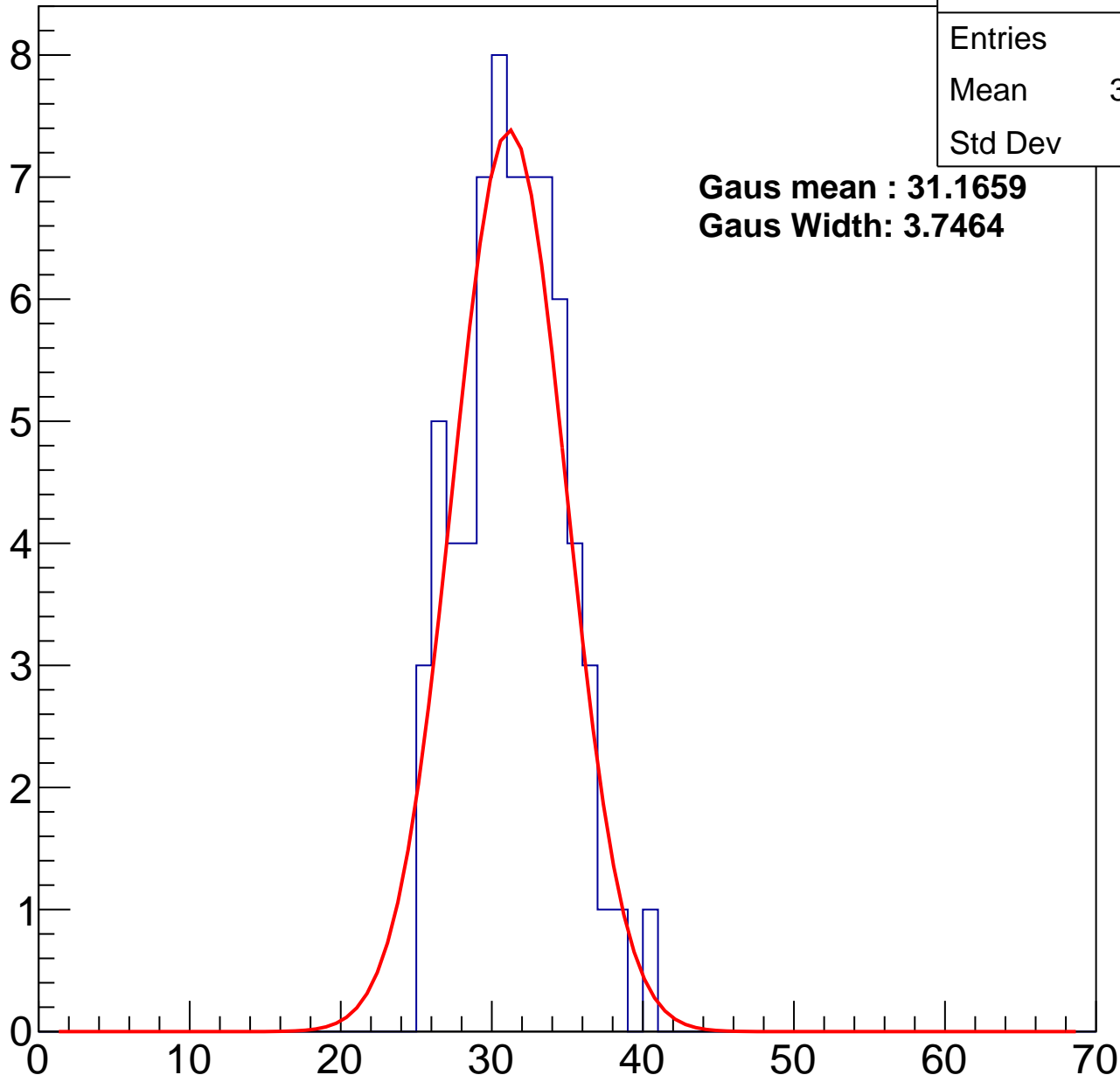
8
7
6
5
4
3
2
1
0

Entries	68
Mean	30.99
Std Dev	3.35

Gaus mean : 31.1659

Gaus Width: 3.7464

ampl



B0L000S, U4-ch85, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	76
Mean	37.97
Std Dev	3.22

Gaus mean : 38.4808

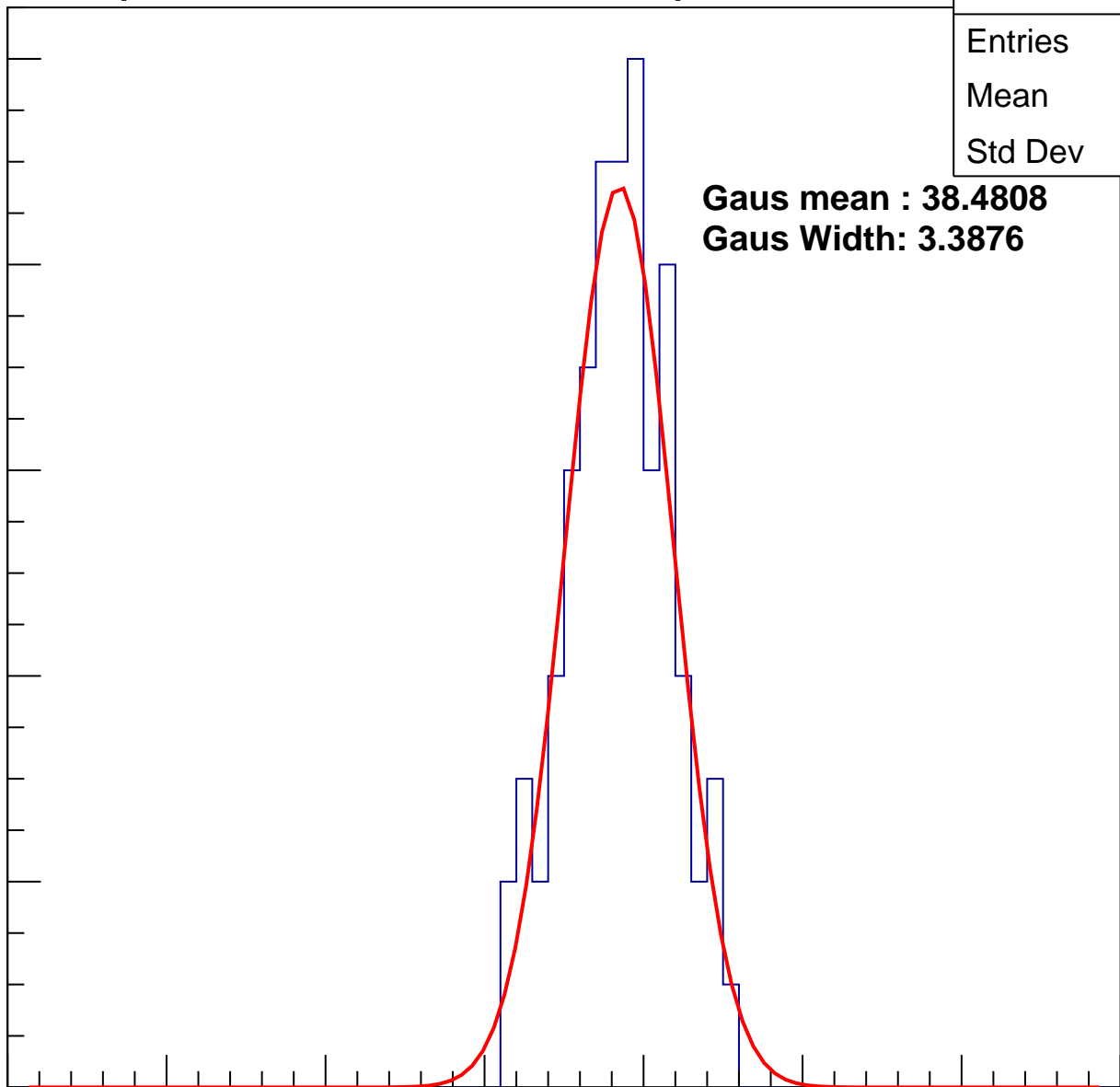
Gaus Width: 3.3876

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch85, adc2

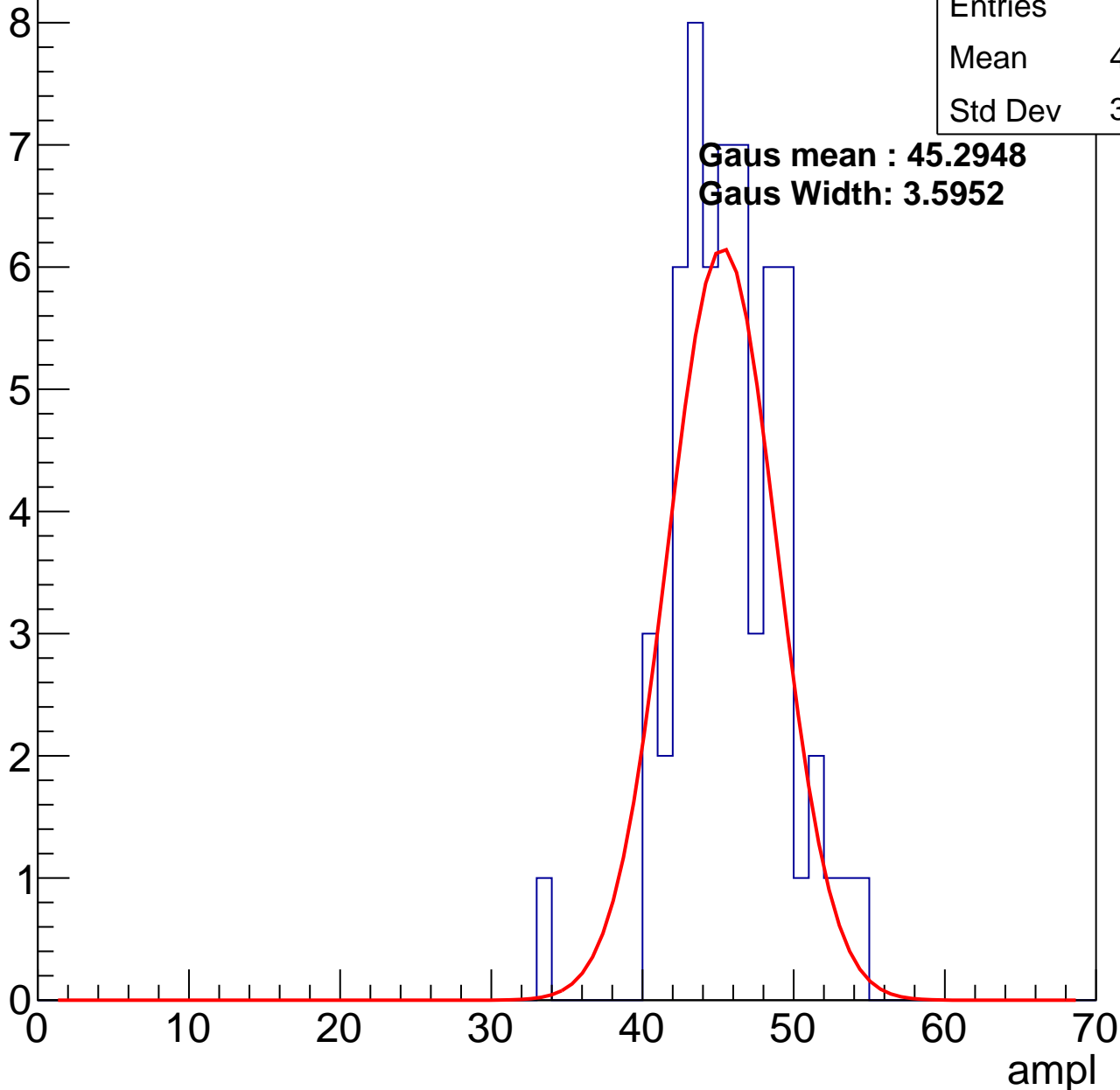
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	45.34
Std Dev	3.612

Gaus mean : 45.2948

Gaus Width: 3.5952

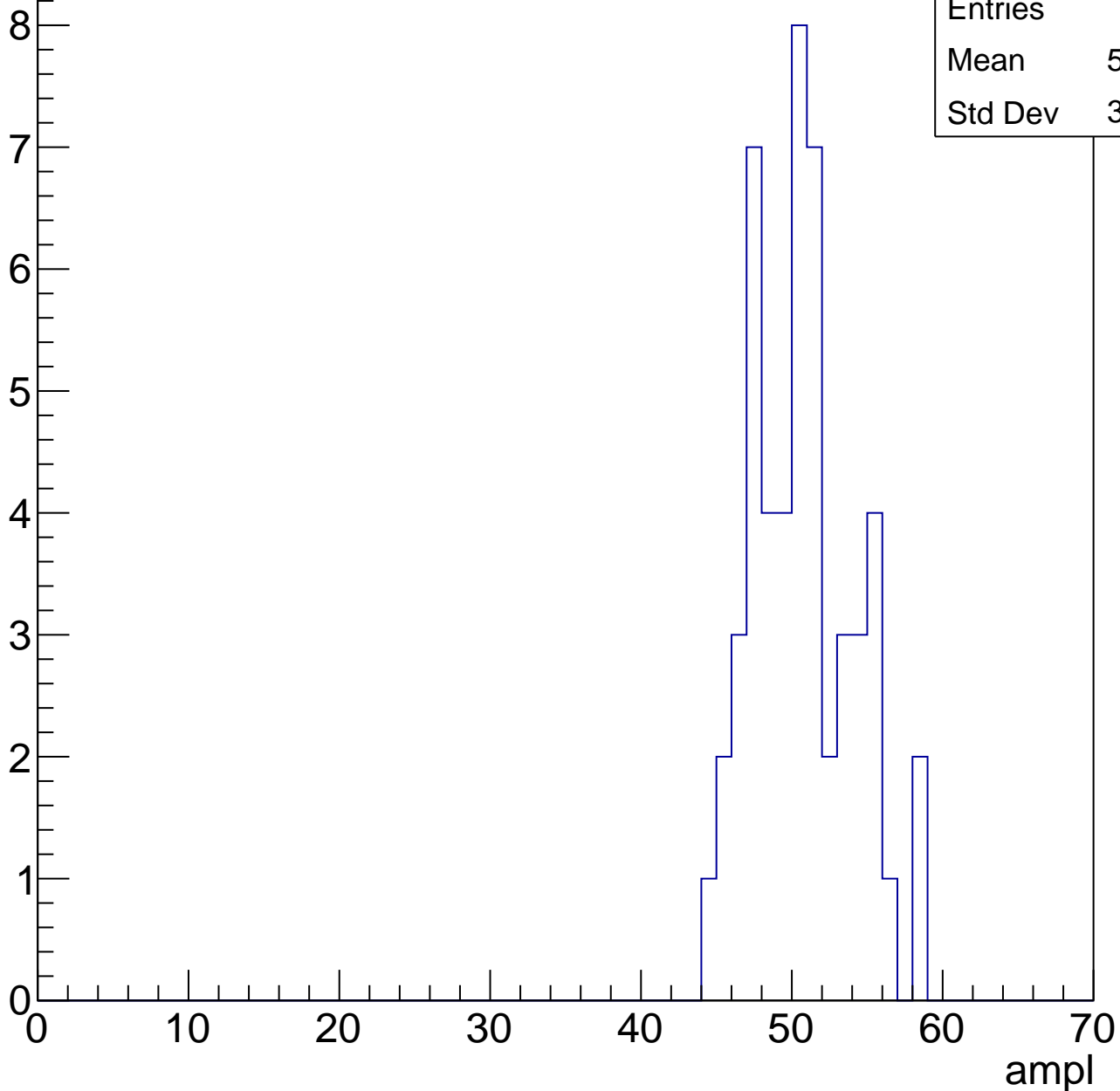


B0L000S, U4-ch85, adc3

calib_packv5_042523_0143.root, FC#5, port B1

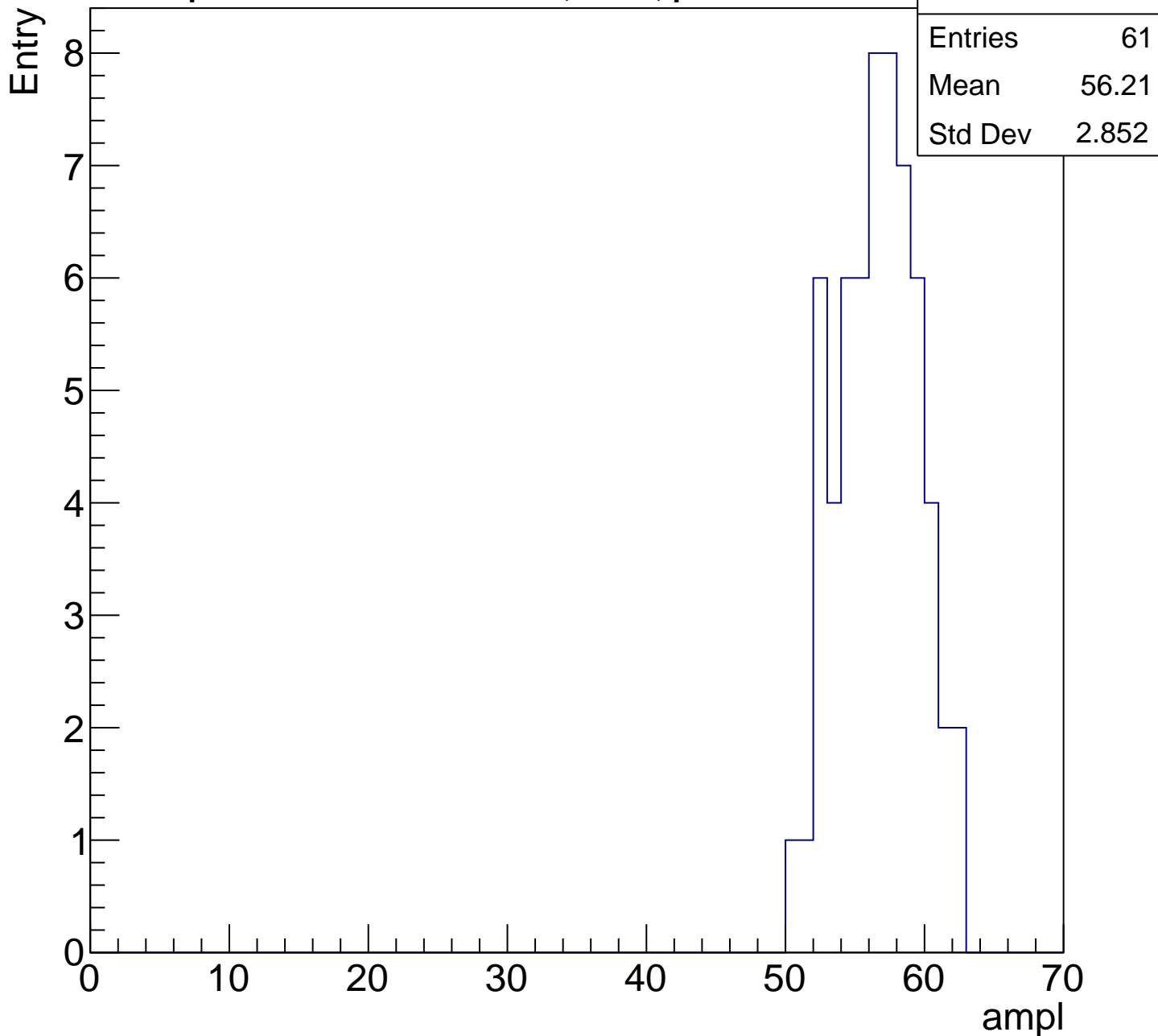
Entry

Entries	51
Mean	50.25
Std Dev	3.336



B0L000S, U4-ch85, adc4

calib_packv5_042523_0143.root, FC#5, port B1

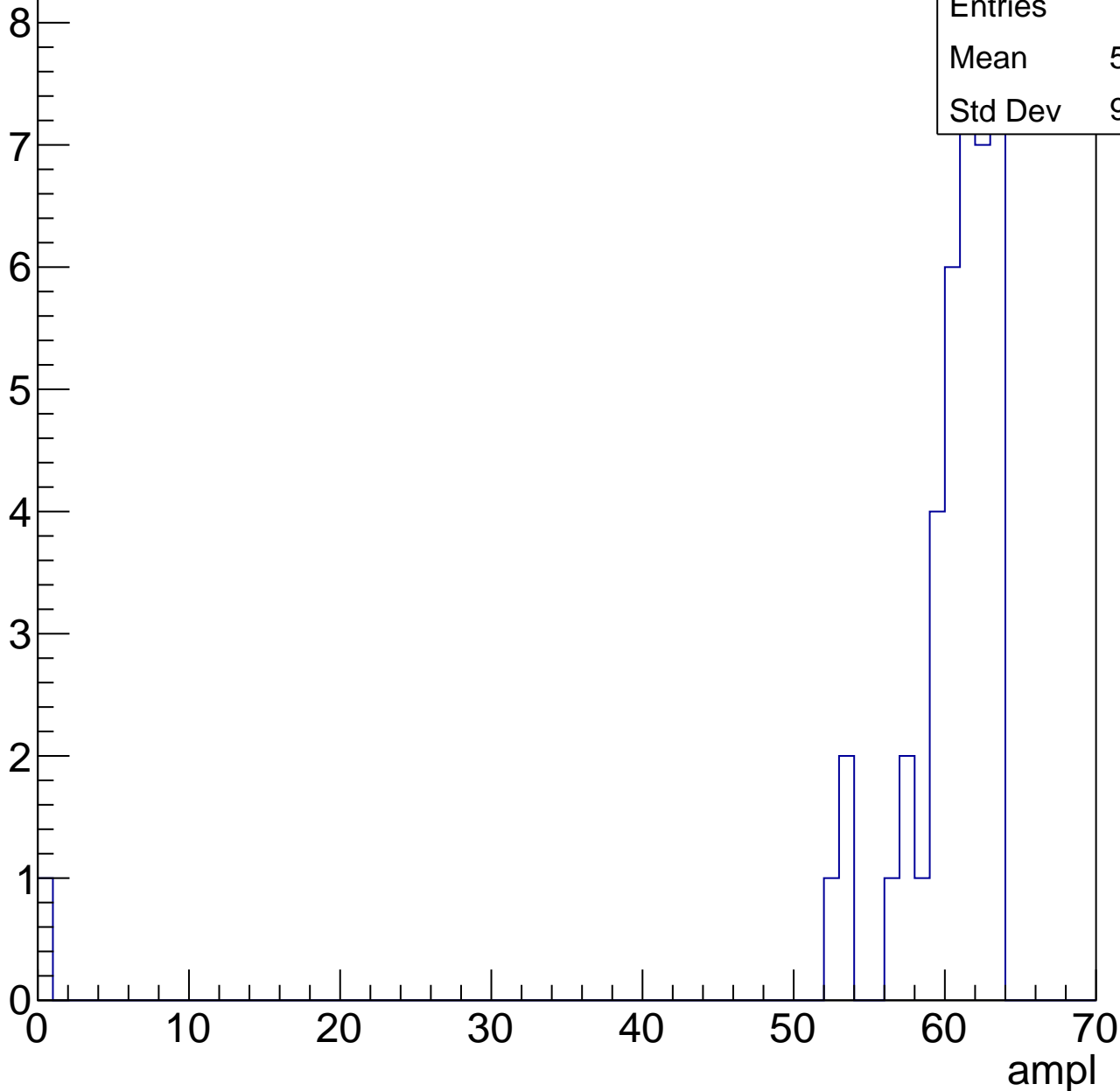


B0L000S, U4-ch85, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.73
Std Dev	9.685



B0L000S, U4-ch85, adc6

calib_packv5_042523_0143.root, FC#5, port B1

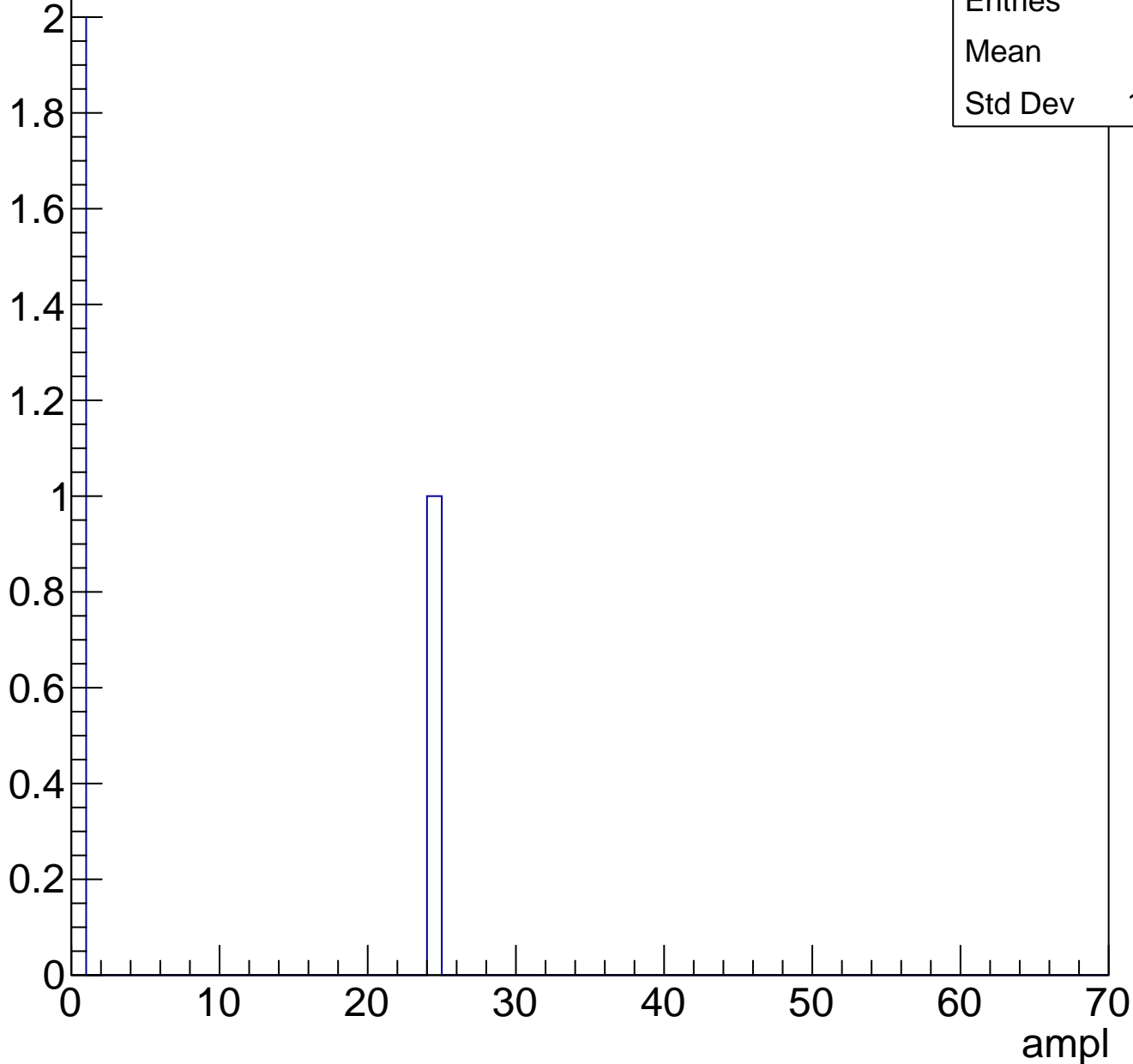
Entry



B0L000S, U4-ch85, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch86, adc0

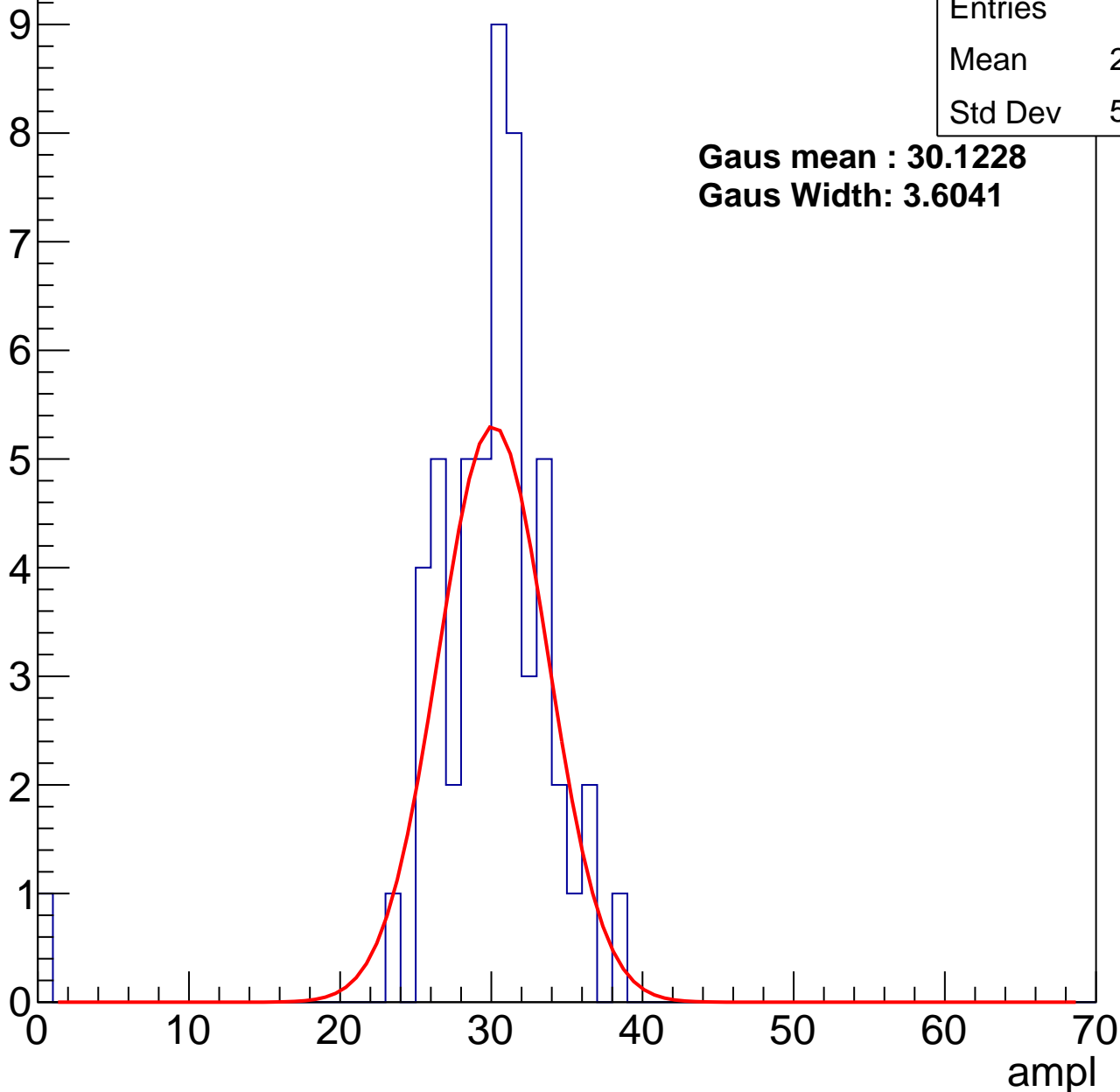
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	29.33
Std Dev	5.103

Gaus mean : 30.1228

Gaus Width: 3.6041



B0L000S, U4-ch86, adc1

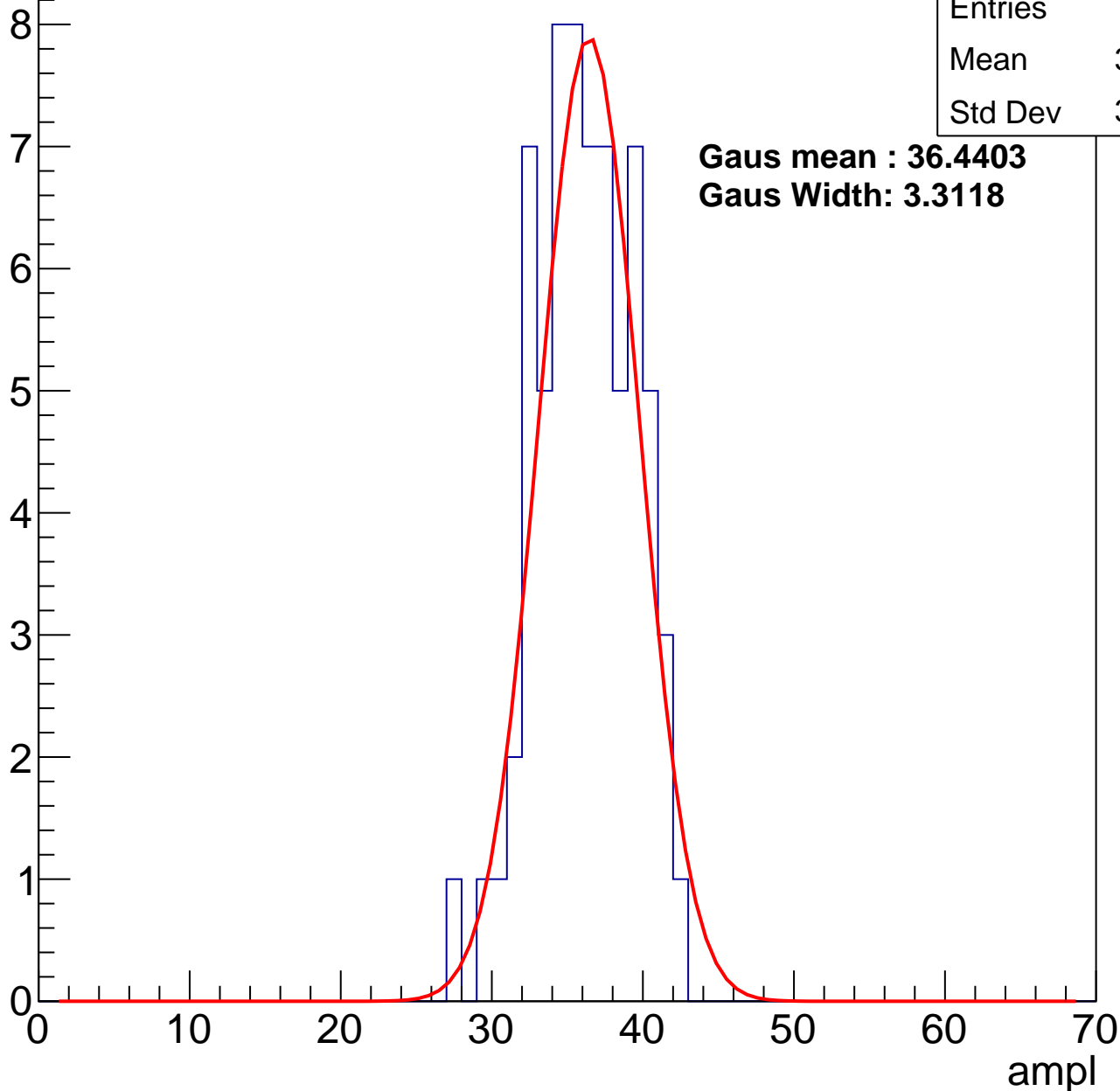
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	35.71
Std Dev	3.181

Gaus mean : 36.4403

Gaus Width: 3.3118



B0L000S, U4-ch86, adc2

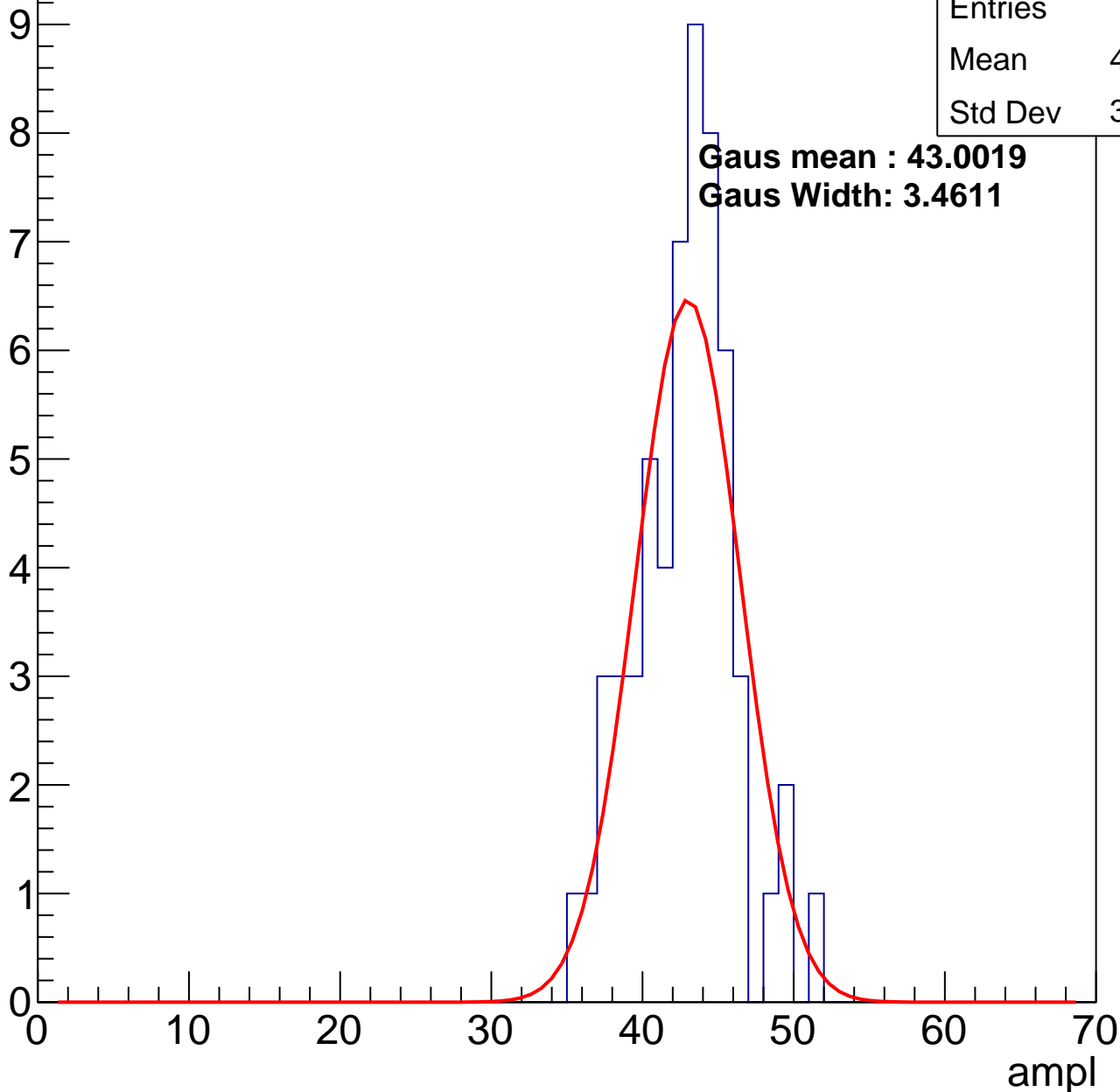
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.37
Std Dev	3.269

Gaus mean : 43.0019

Gaus Width: 3.4611

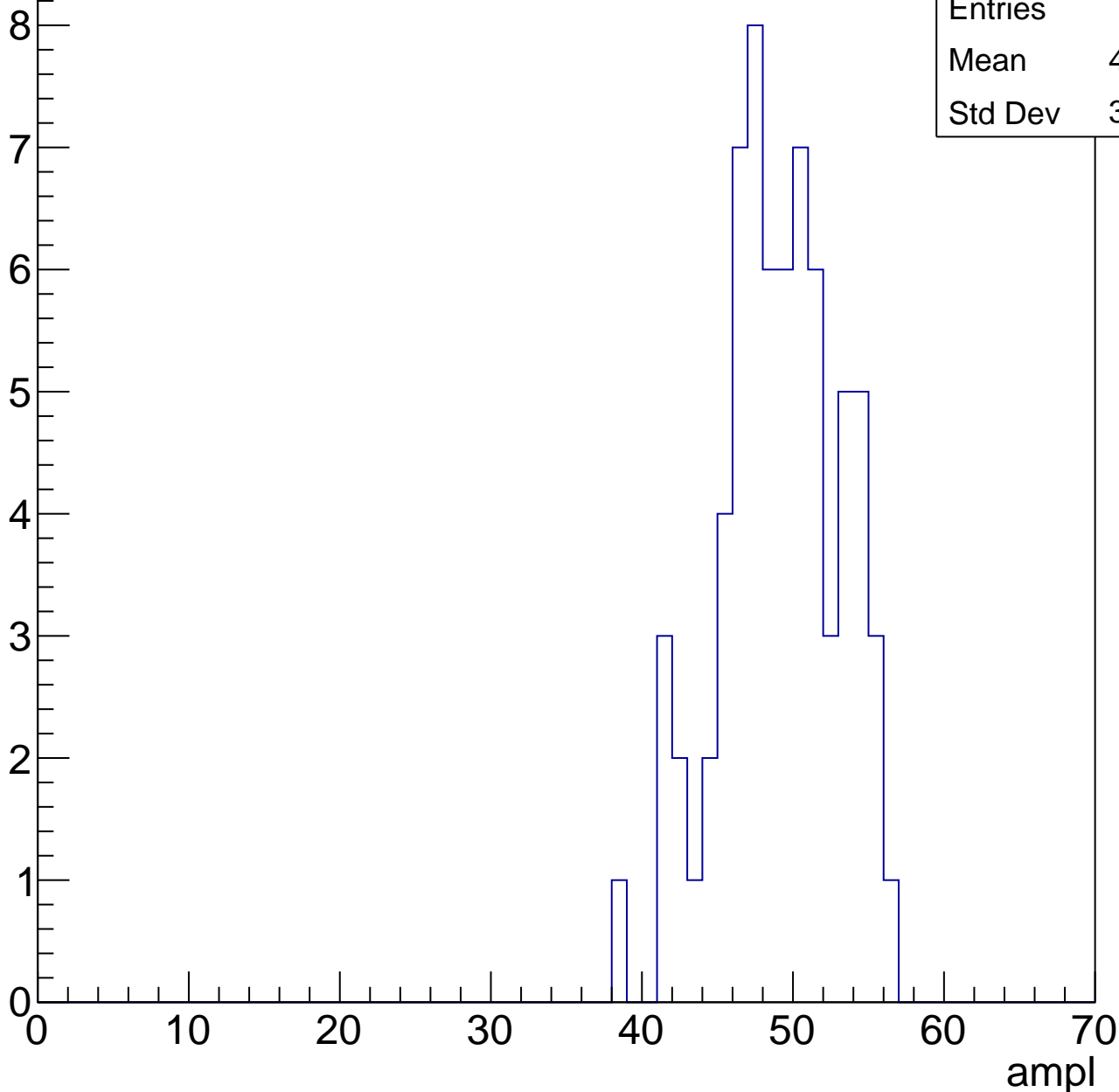


B0L000S, U4-ch86, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	48.63
Std Dev	3.914

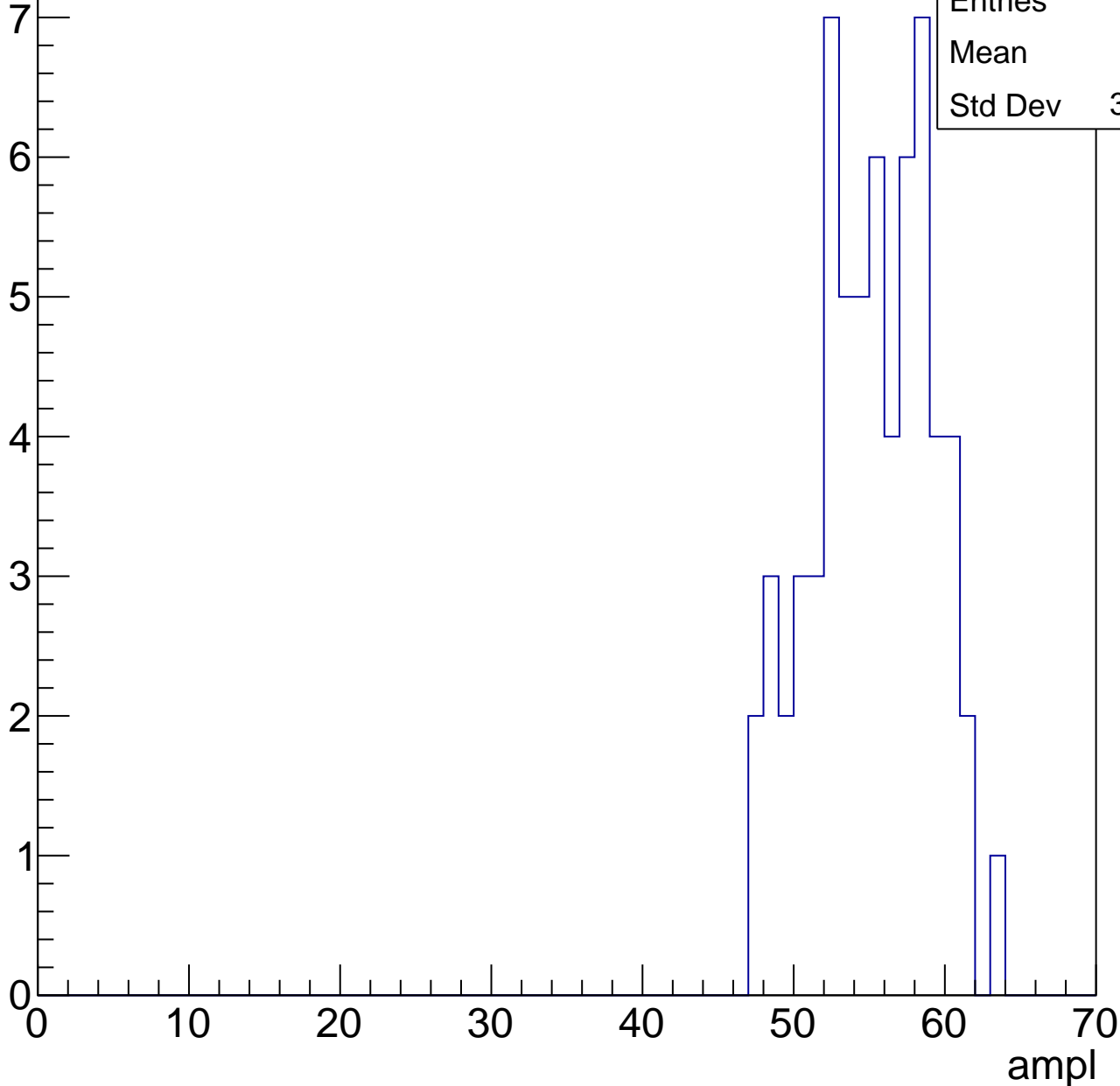


B0L000S, U4-ch86, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.7
Std Dev	3.839

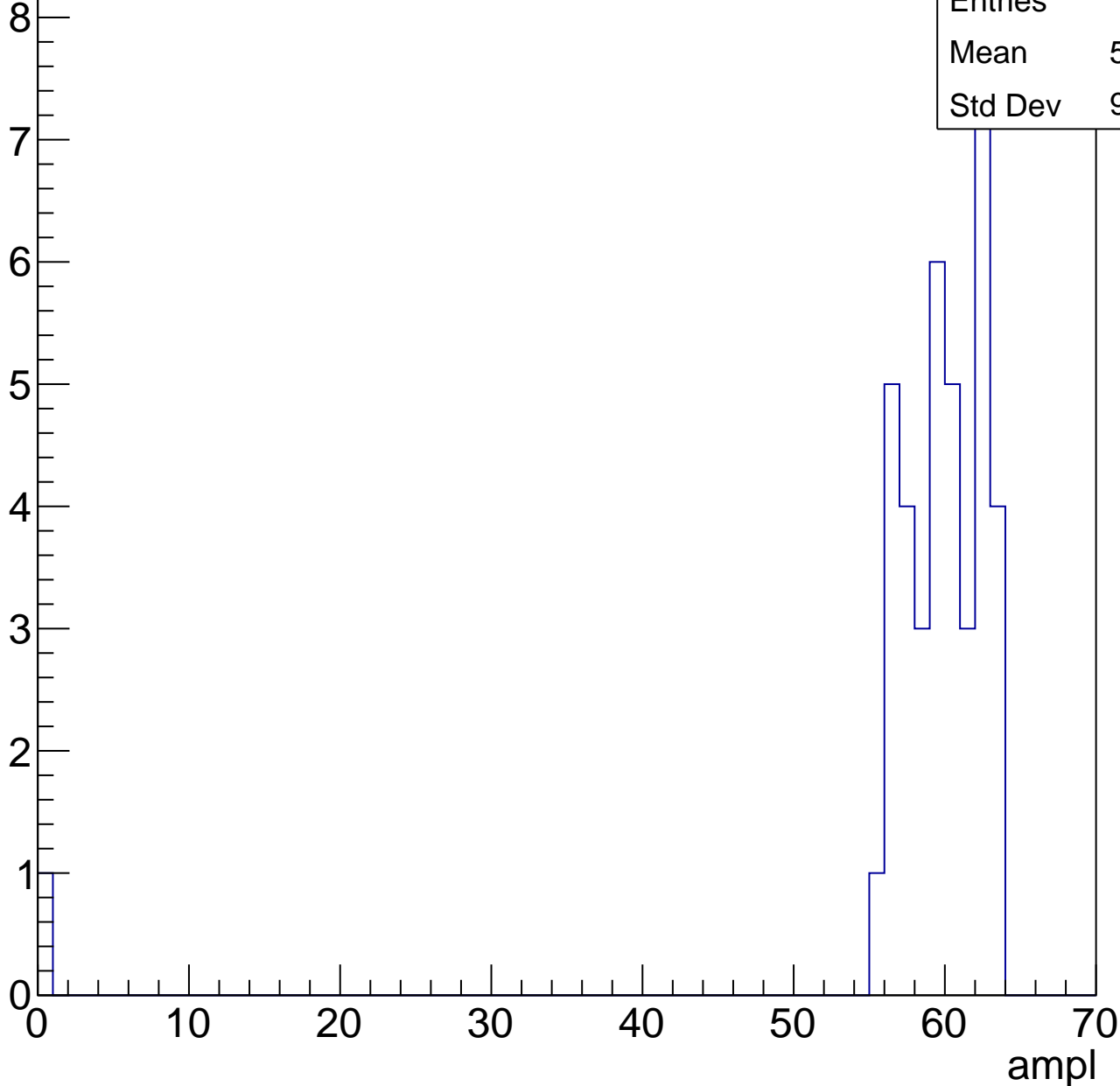


B0L000S, U4-ch86, adc5

calib_packv5_042523_0143.root, FC#5, port B1

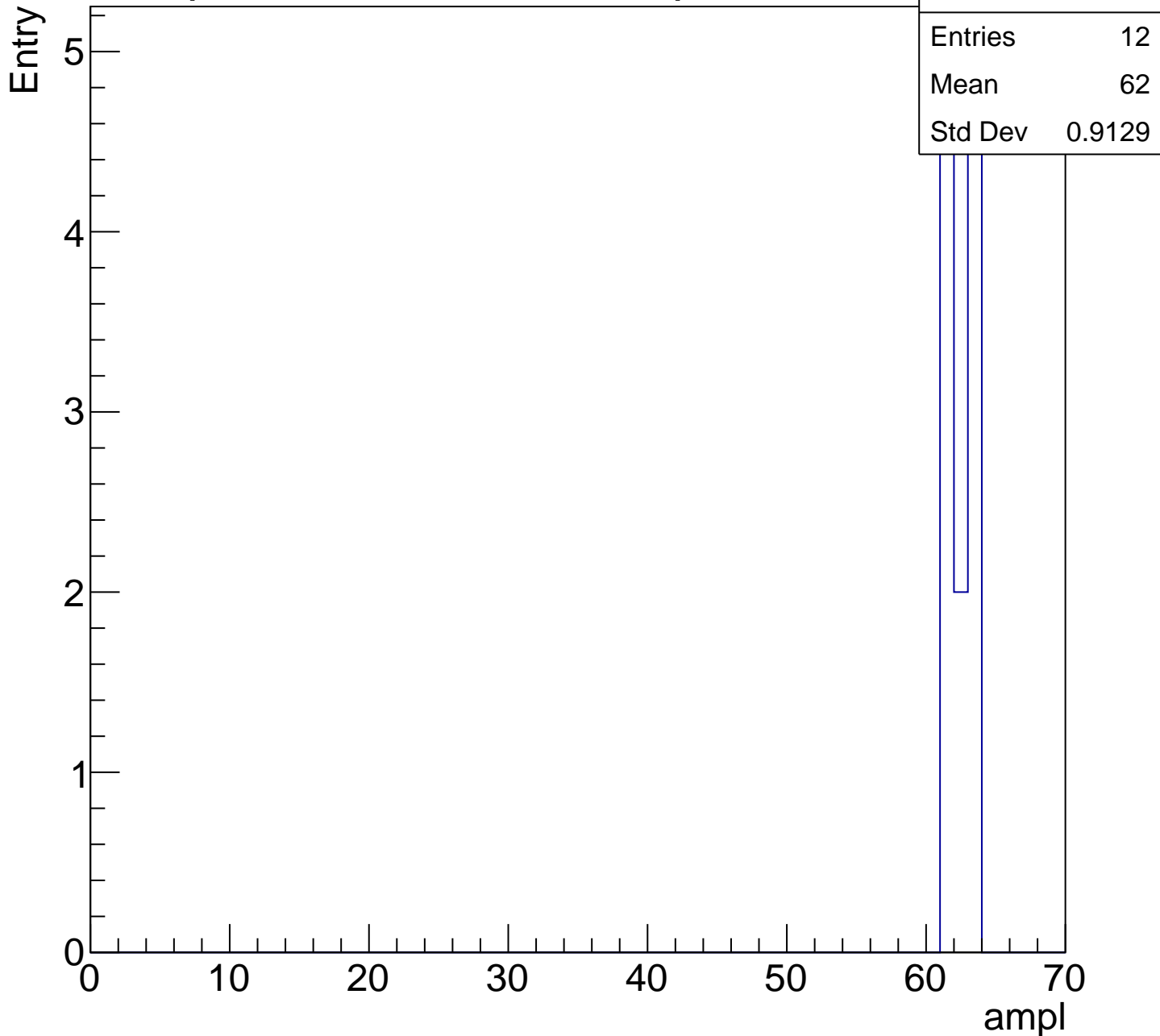
Entry

Entries	40
Mean	58.05
Std Dev	9.589



B0L000S, U4-ch86, adc6

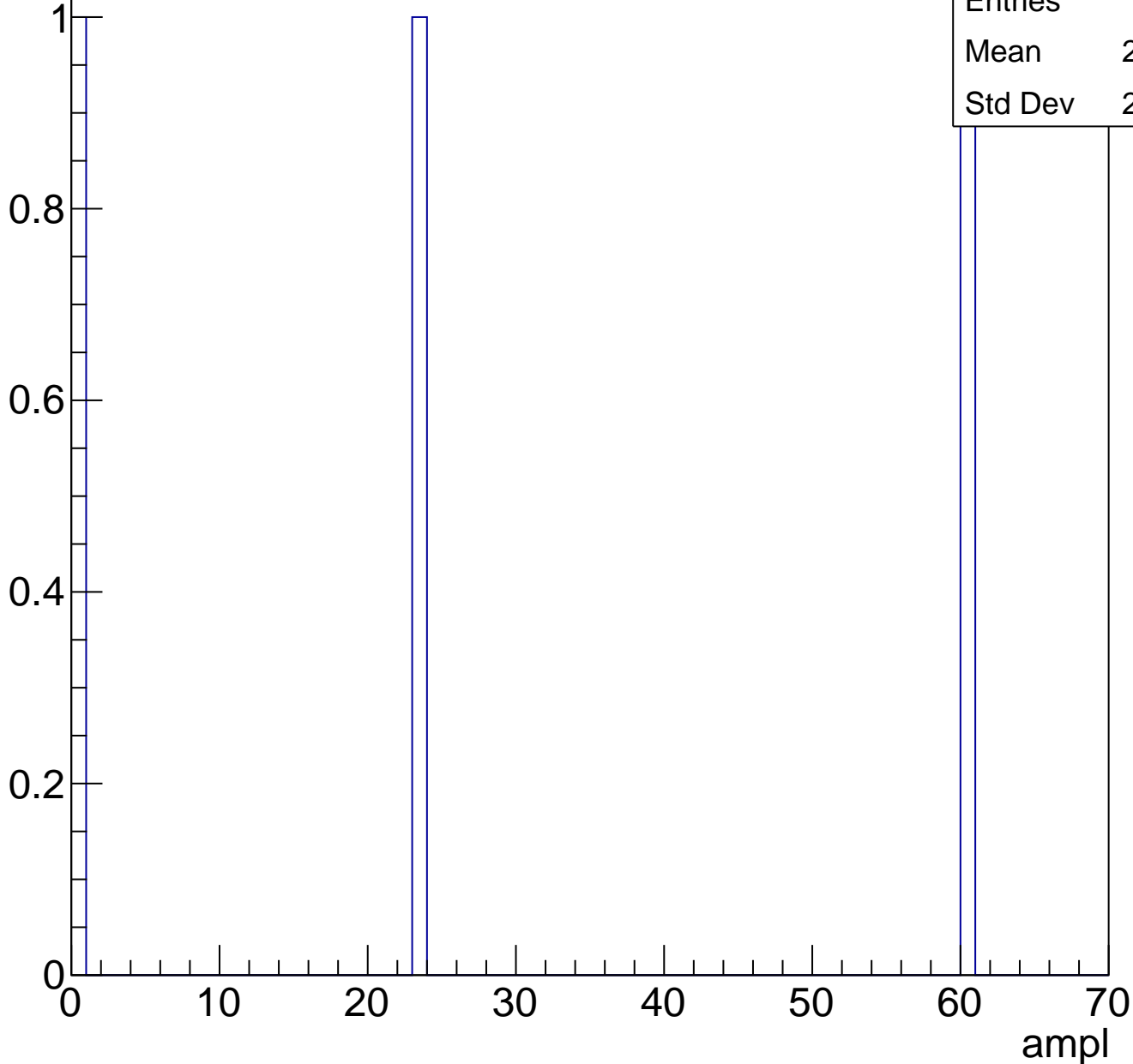
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch86, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	27.67
Std Dev	24.72

B0L000S, U4-ch87, adc0

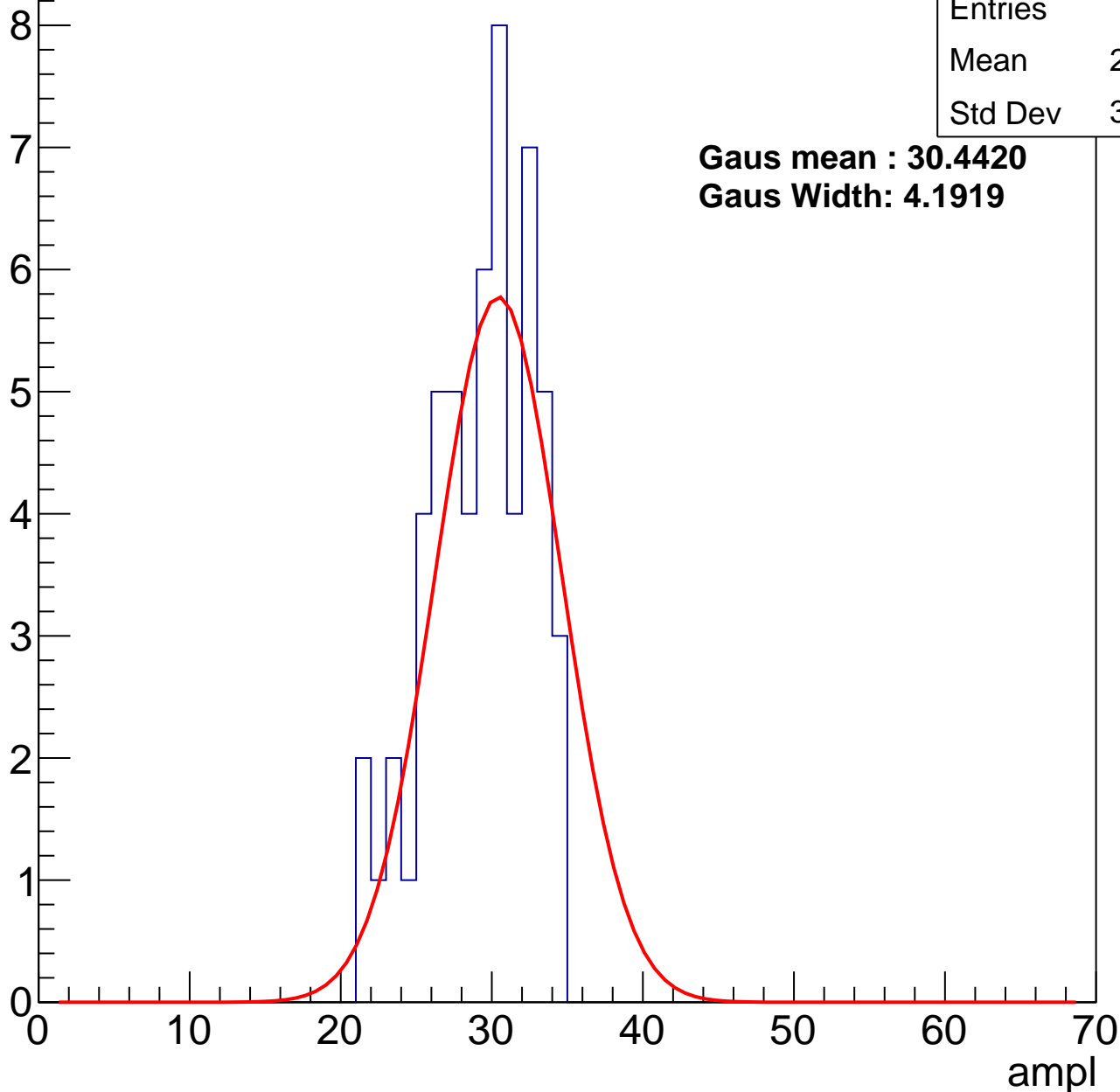
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	28.77
Std Dev	3.367

Gaus mean : 30.4420

Gaus Width: 4.1919



B0L000S, U4-ch87, adc1

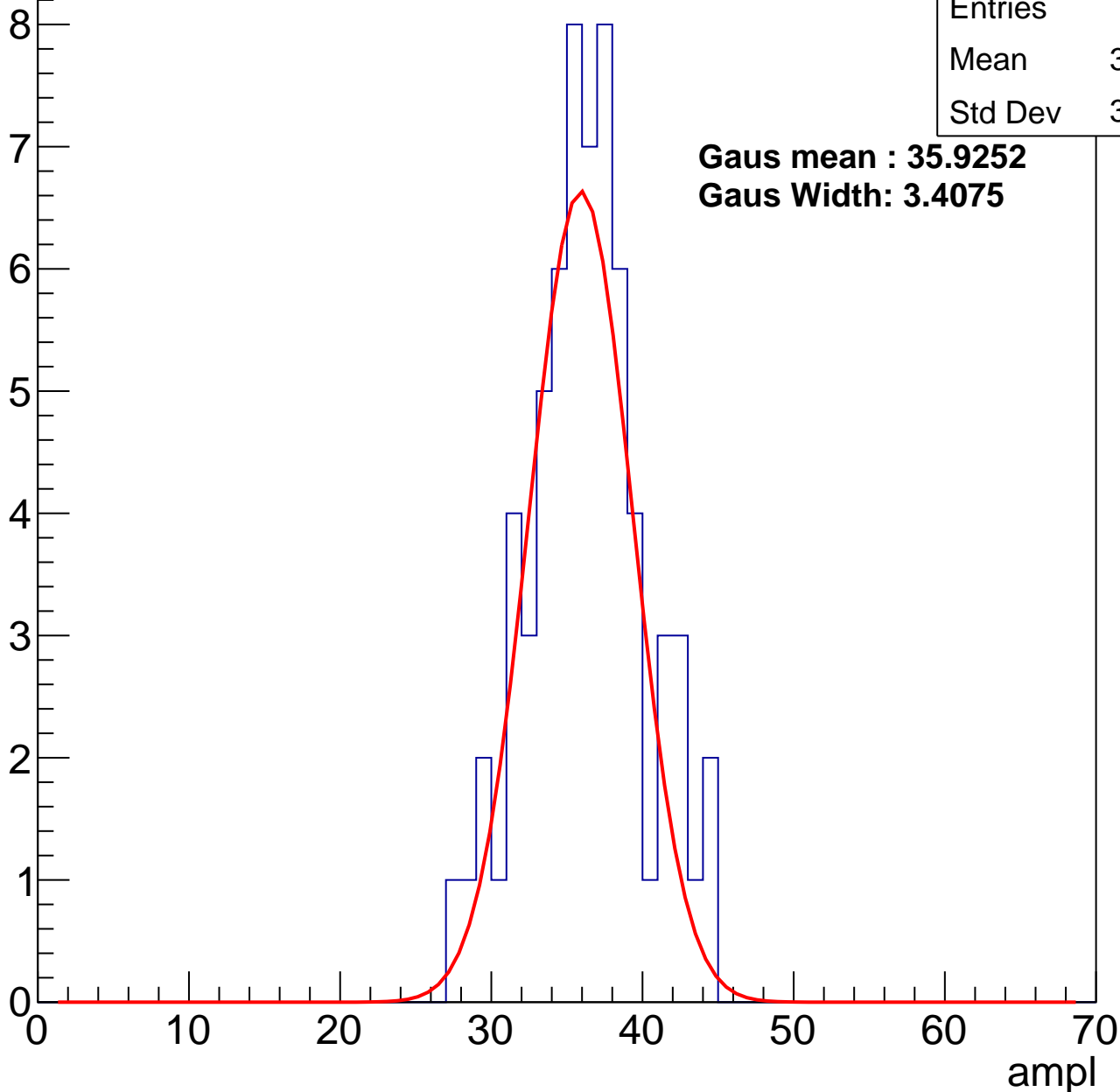
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	35.82
Std Dev	3.786

Gaus mean : 35.9252

Gaus Width: 3.4075



B0L000S, U4-ch87, adc2

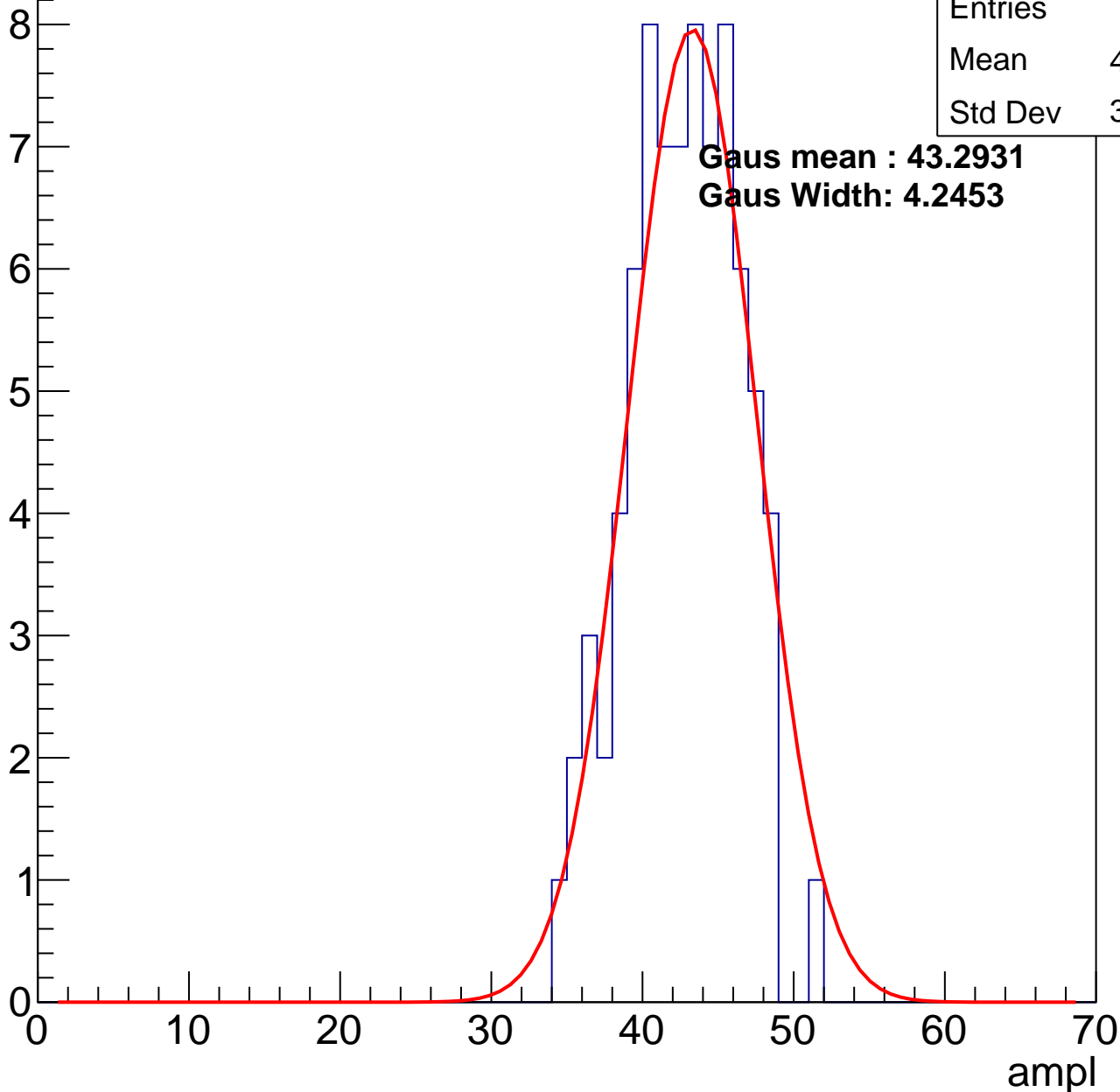
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	79
Mean	42.27
Std Dev	3.603

Gaus mean : 43.2931

Gaus Width: 4.2453

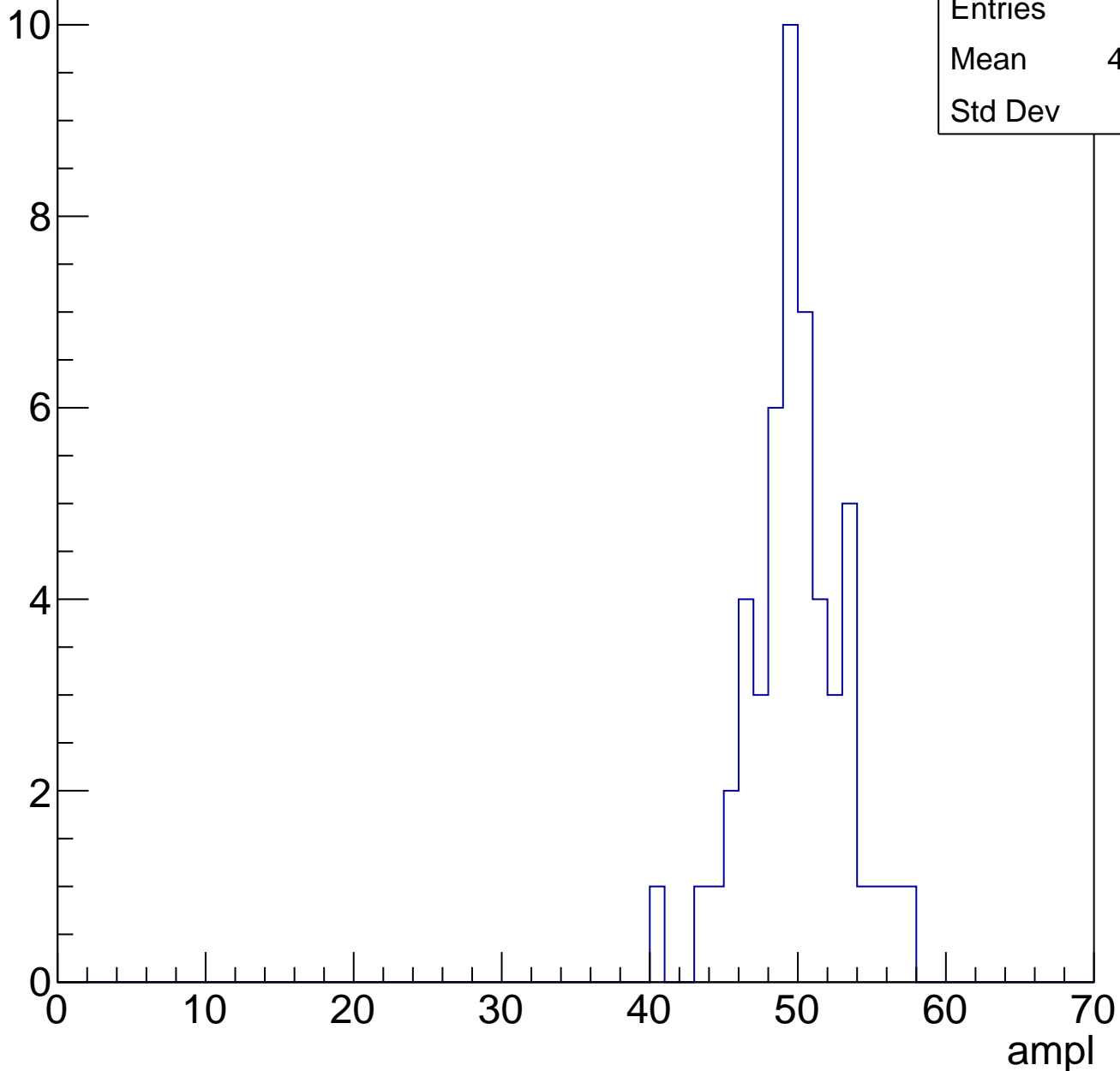


B0L000S, U4-ch87, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	49.35
Std Dev	3.21

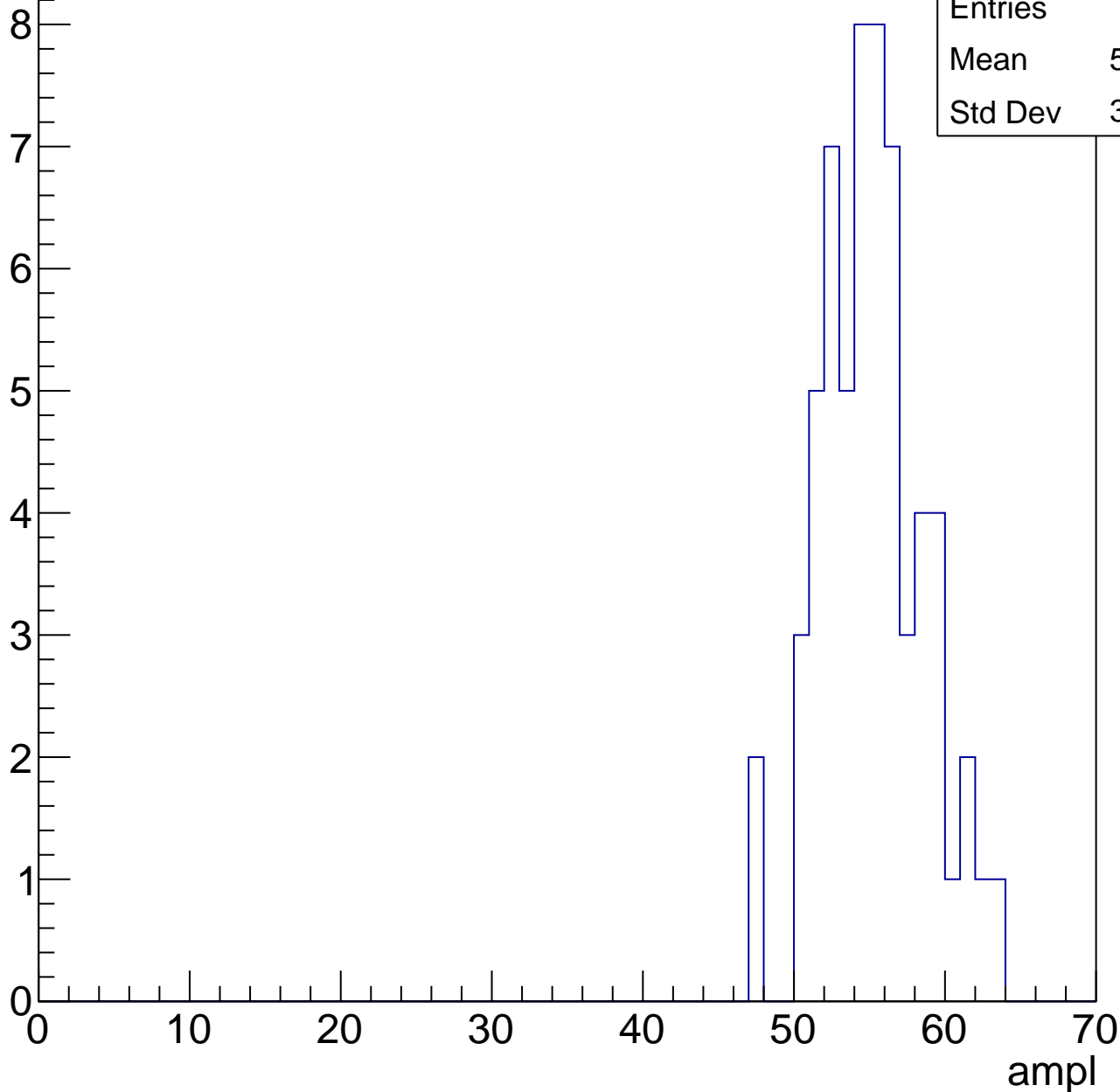


B0L000S, U4-ch87, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	54.72
Std Dev	3.383



B0L000S, U4-ch87, adc5

calib_packv5_042523_0143.root, FC#5, port B1

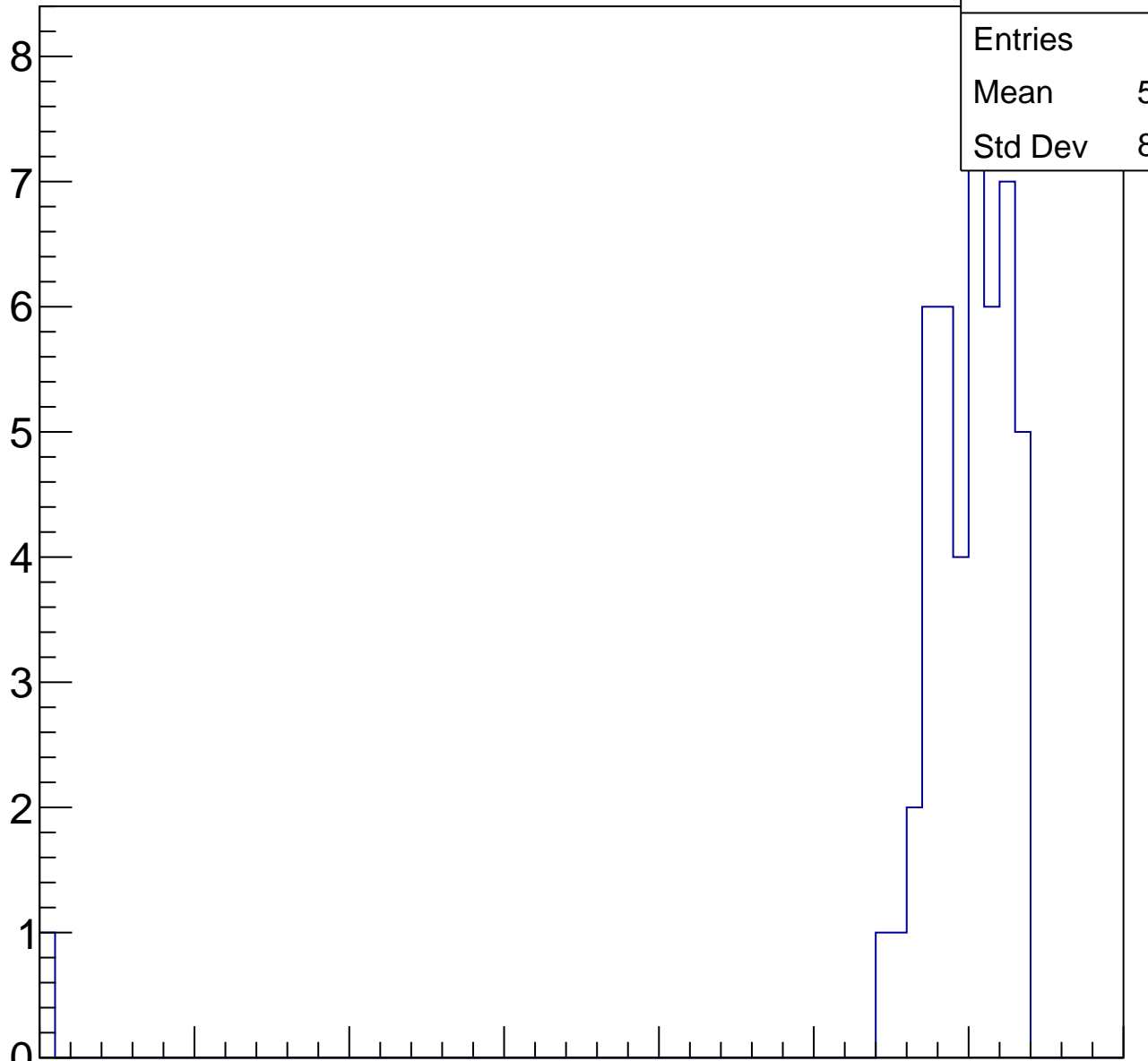
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.34
Std Dev	8.902

ampl

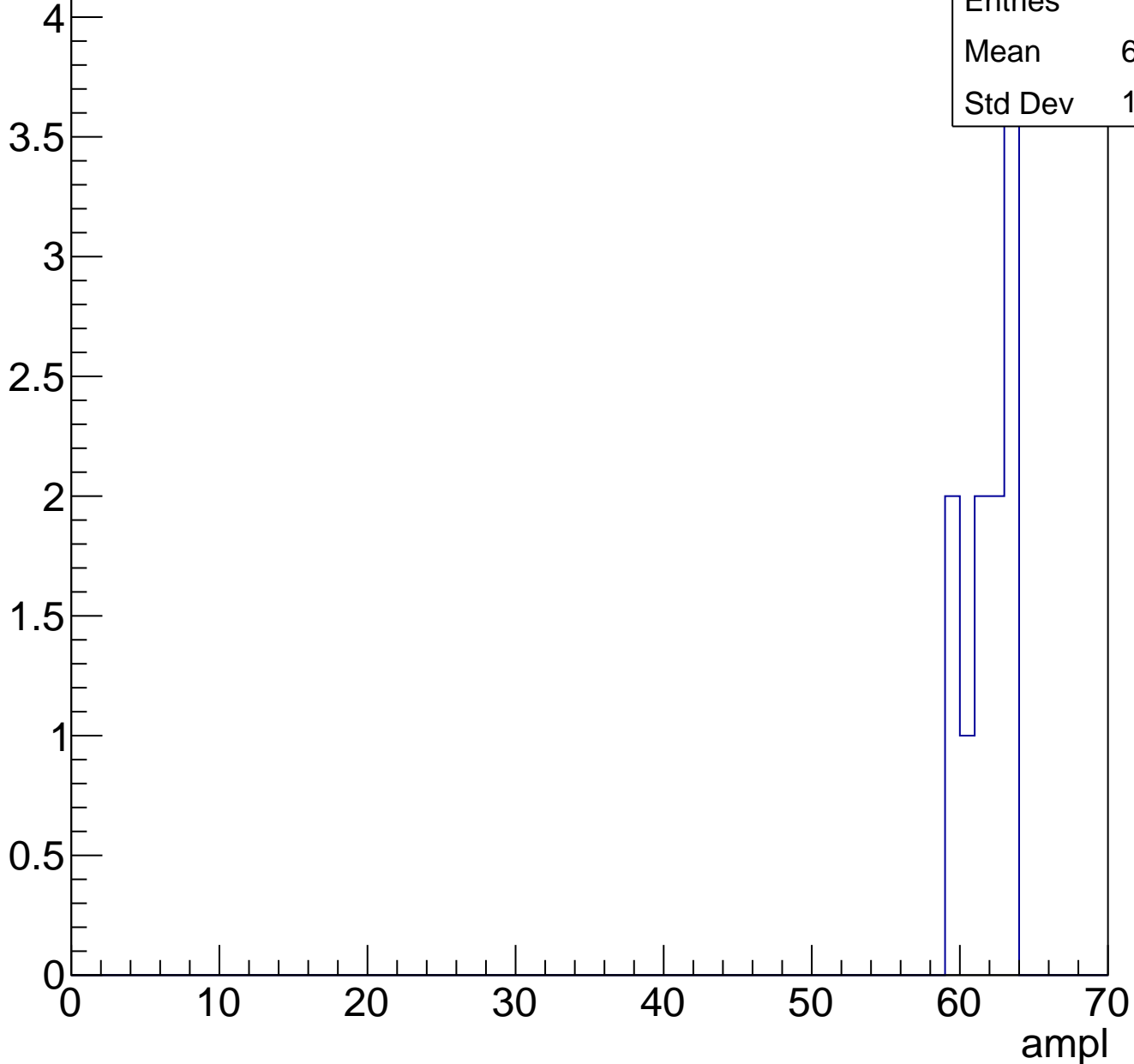
0 10 20 30 40 50 60 70



B0L000S, U4-ch87, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch87, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch88, adc0

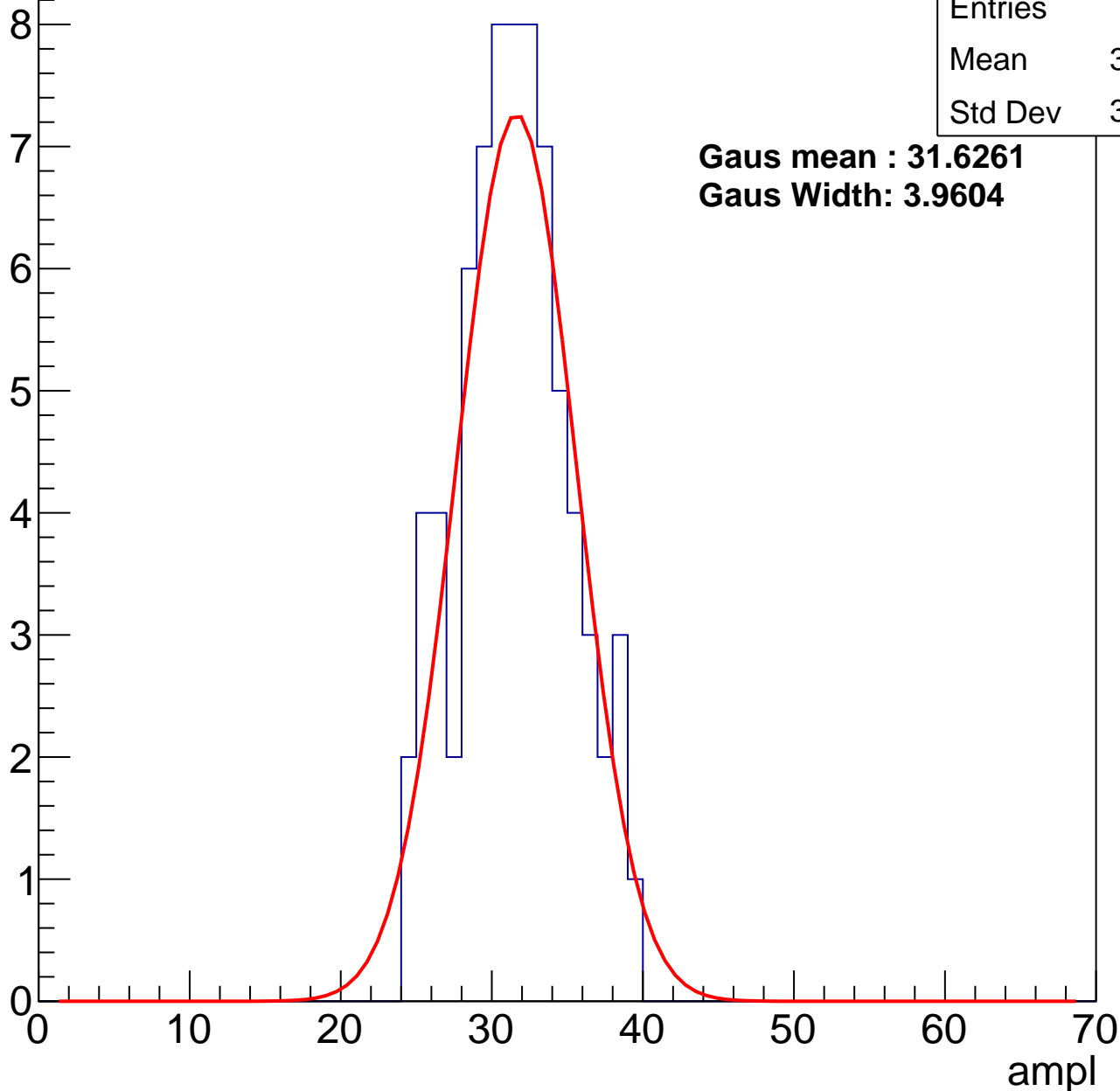
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	31.04
Std Dev	3.615

Gaus mean : 31.6261

Gaus Width: 3.9604



B0L000S, U4-ch88, adc1

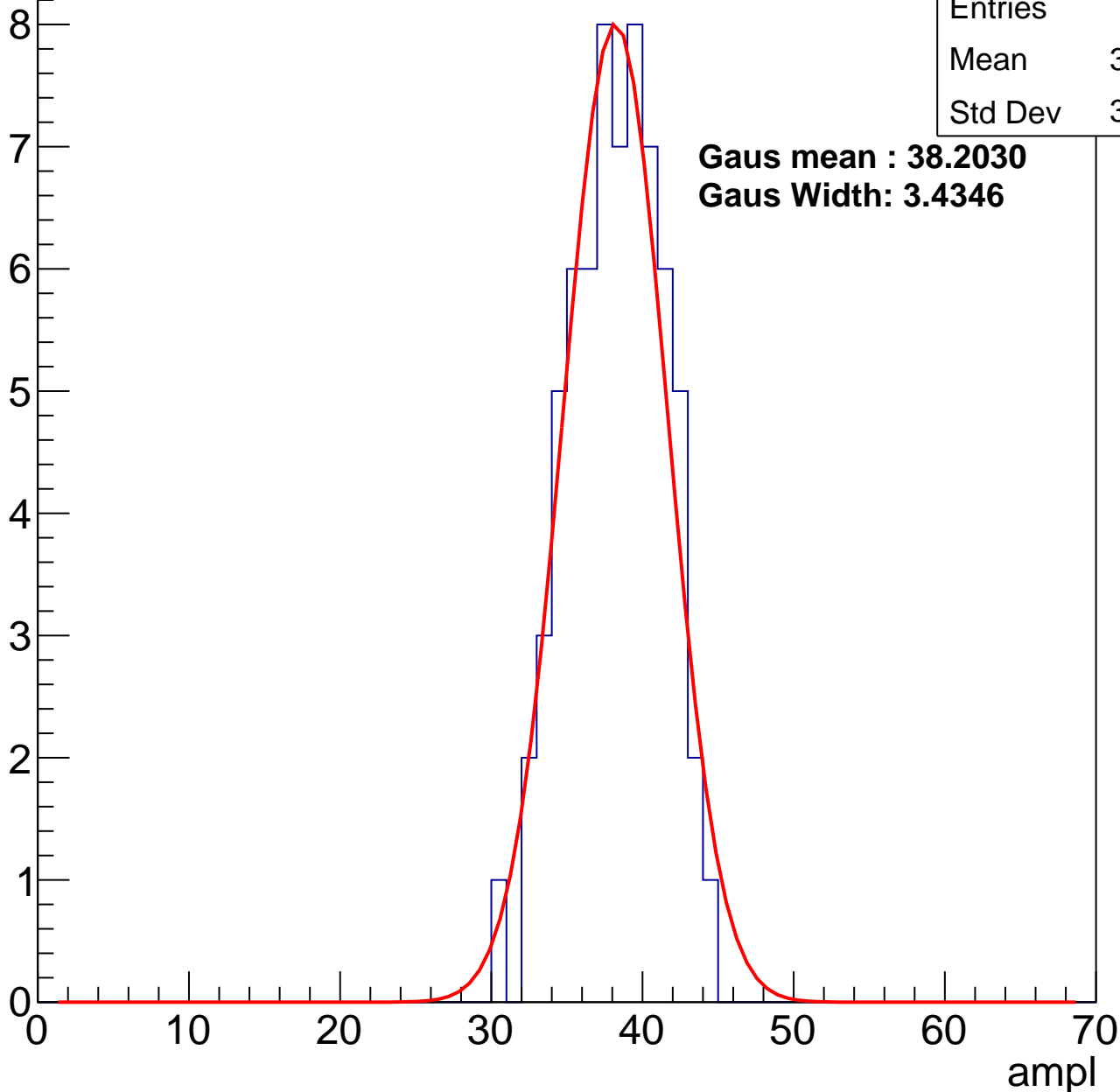
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.75
Std Dev	3.063

Gaus mean : 38.2030

Gaus Width: 3.4346



B0L000S, U4-ch88, adc2

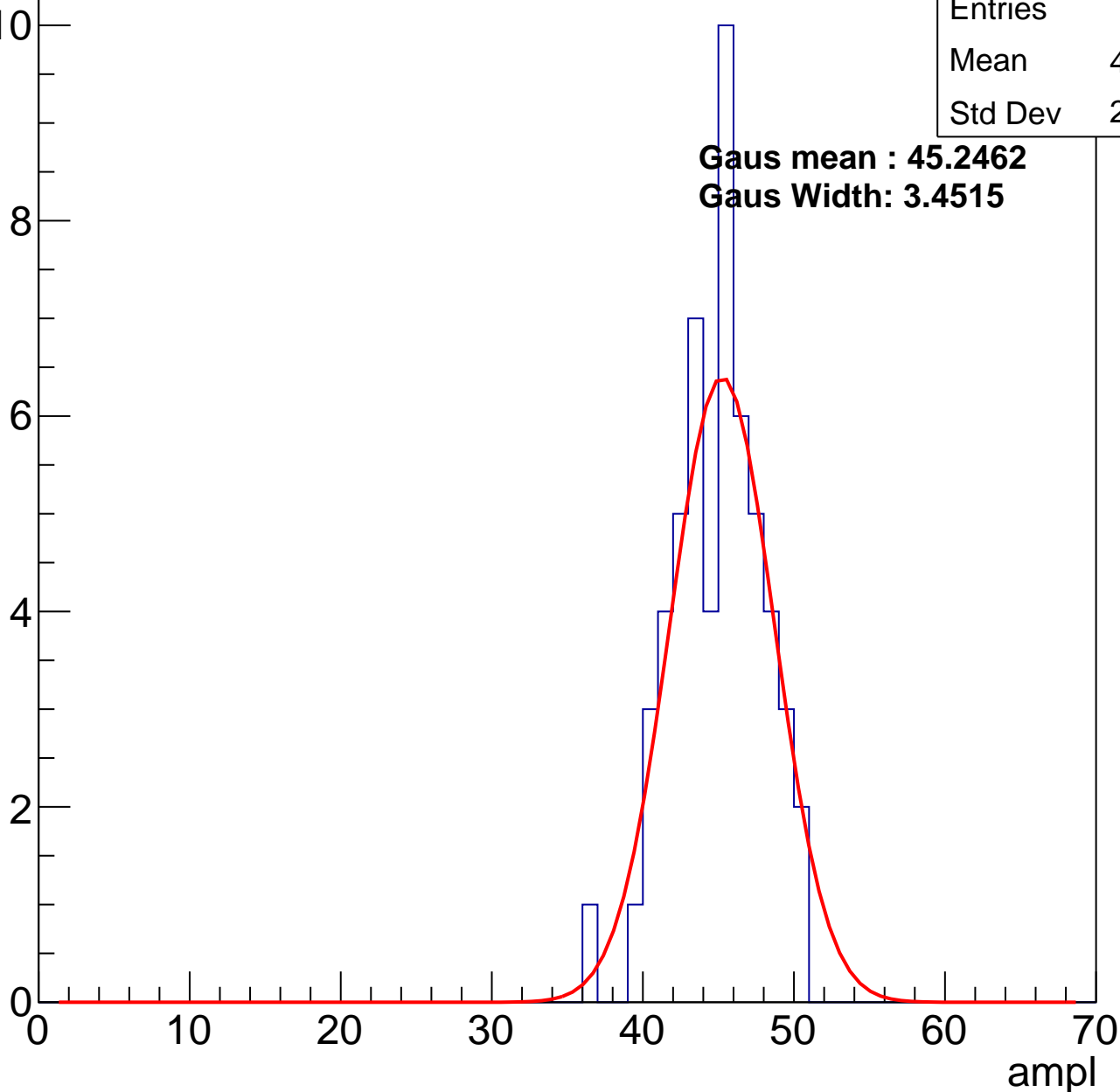
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	44.47
Std Dev	2.947

Gaus mean : 45.2462

Gaus Width: 3.4515

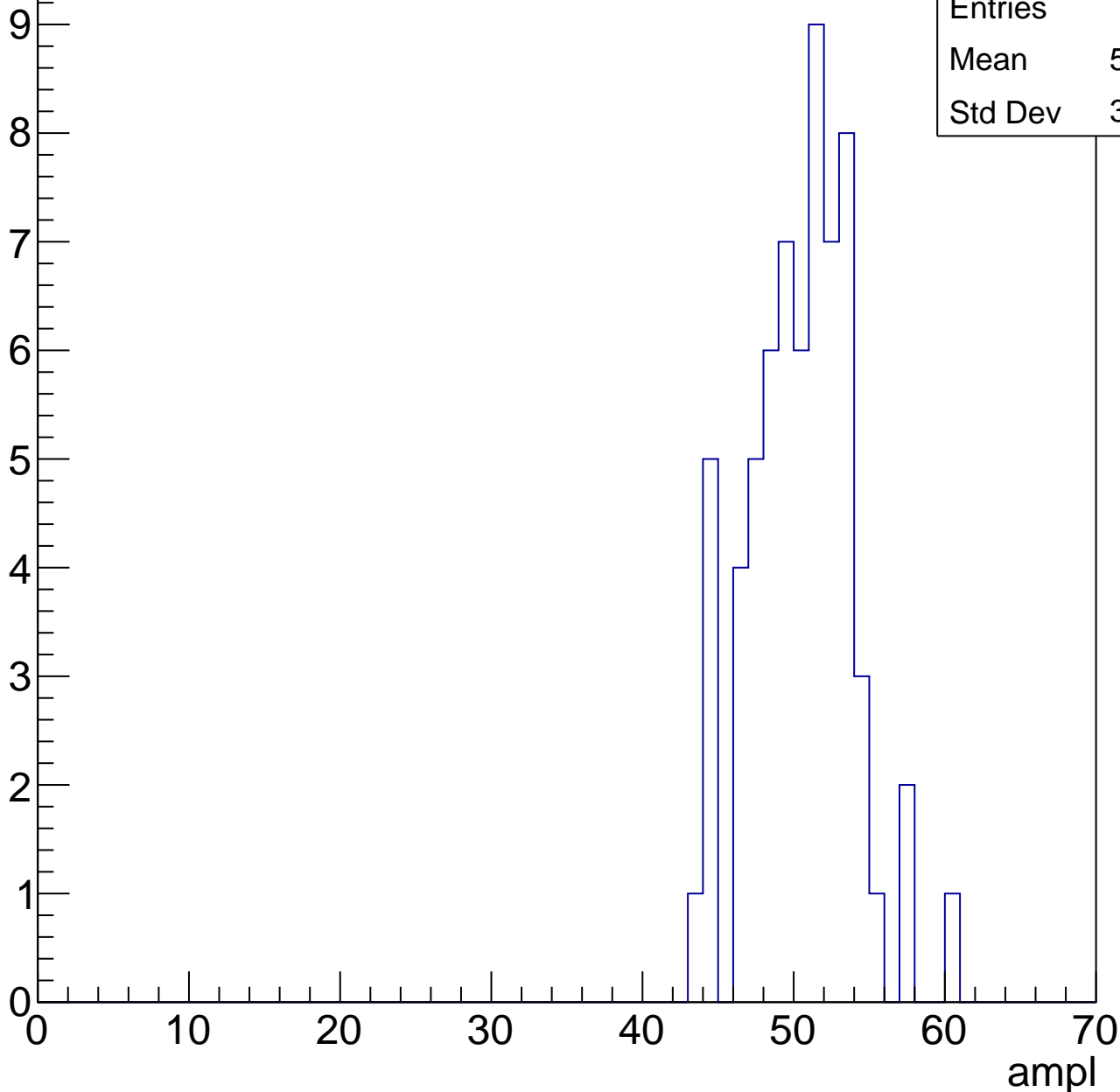


B0L000S, U4-ch88, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

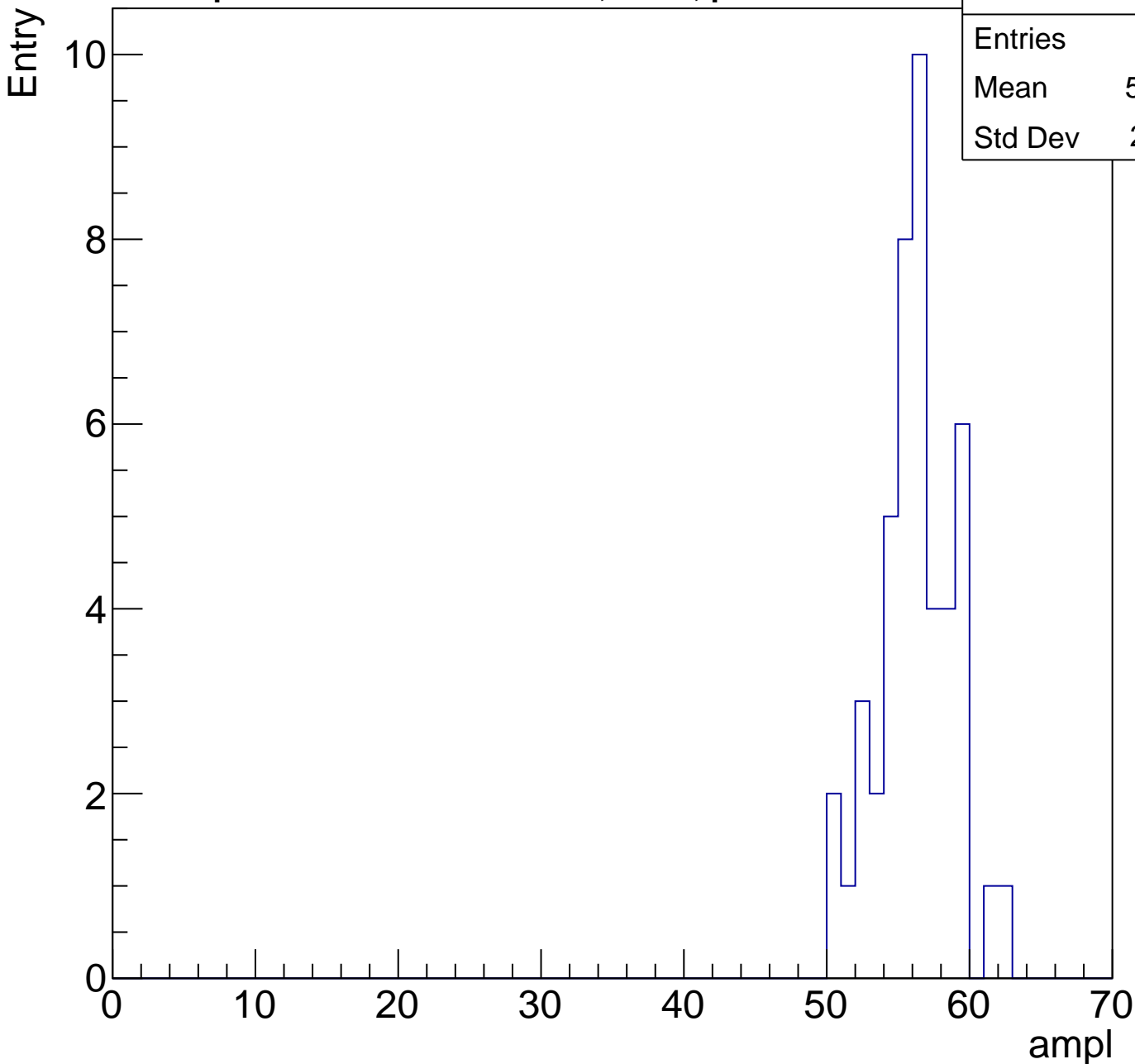
Entries	65
Mean	50.02
Std Dev	3.395



B0L000S, U4-ch88, adc4

calib_packv5_042523_0143.root, FC#5, port B1

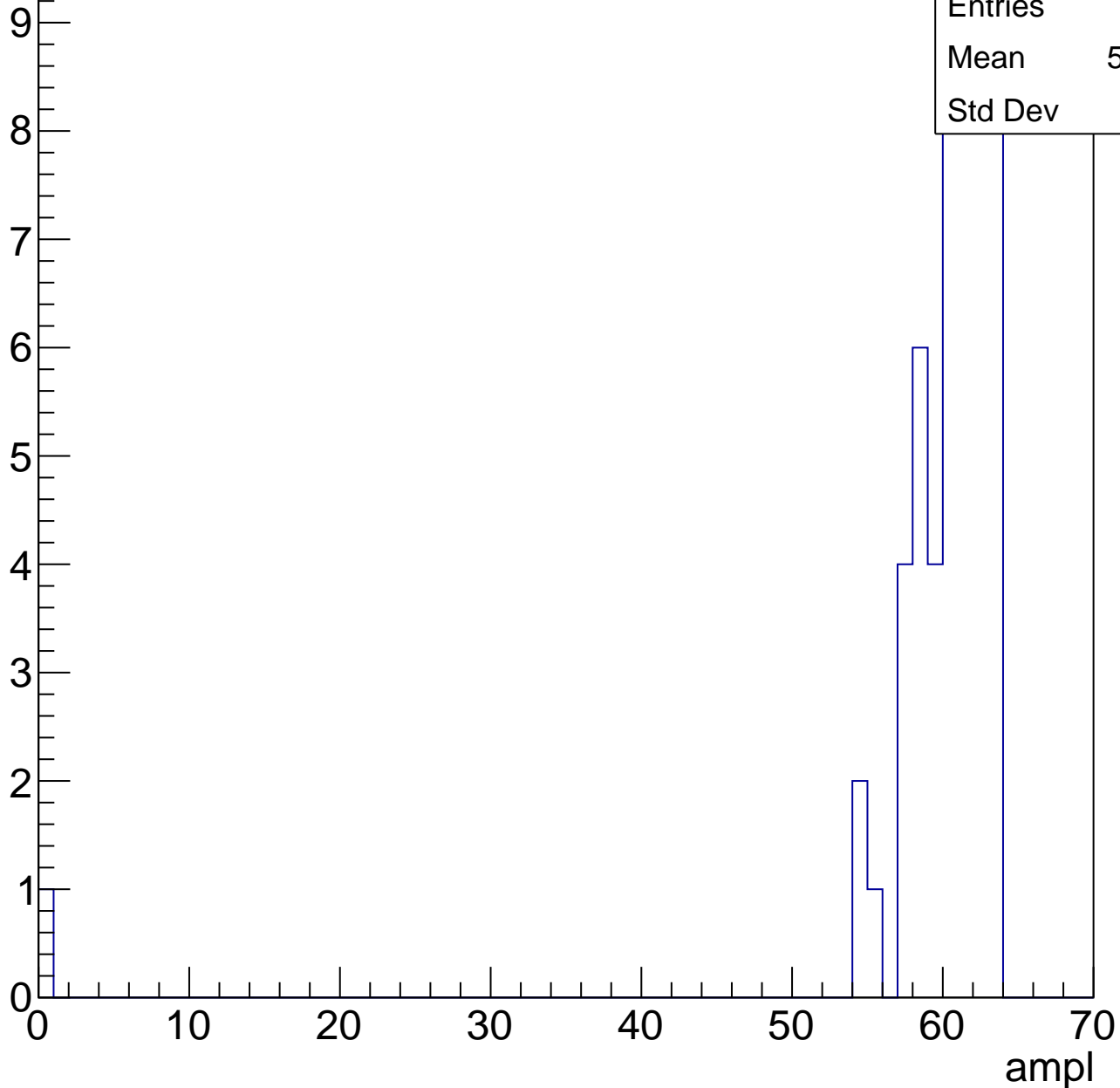
Entries	47
Mean	55.74
Std Dev	2.621



B0L000S, U4-ch88, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch88, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch88, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch89, adc0

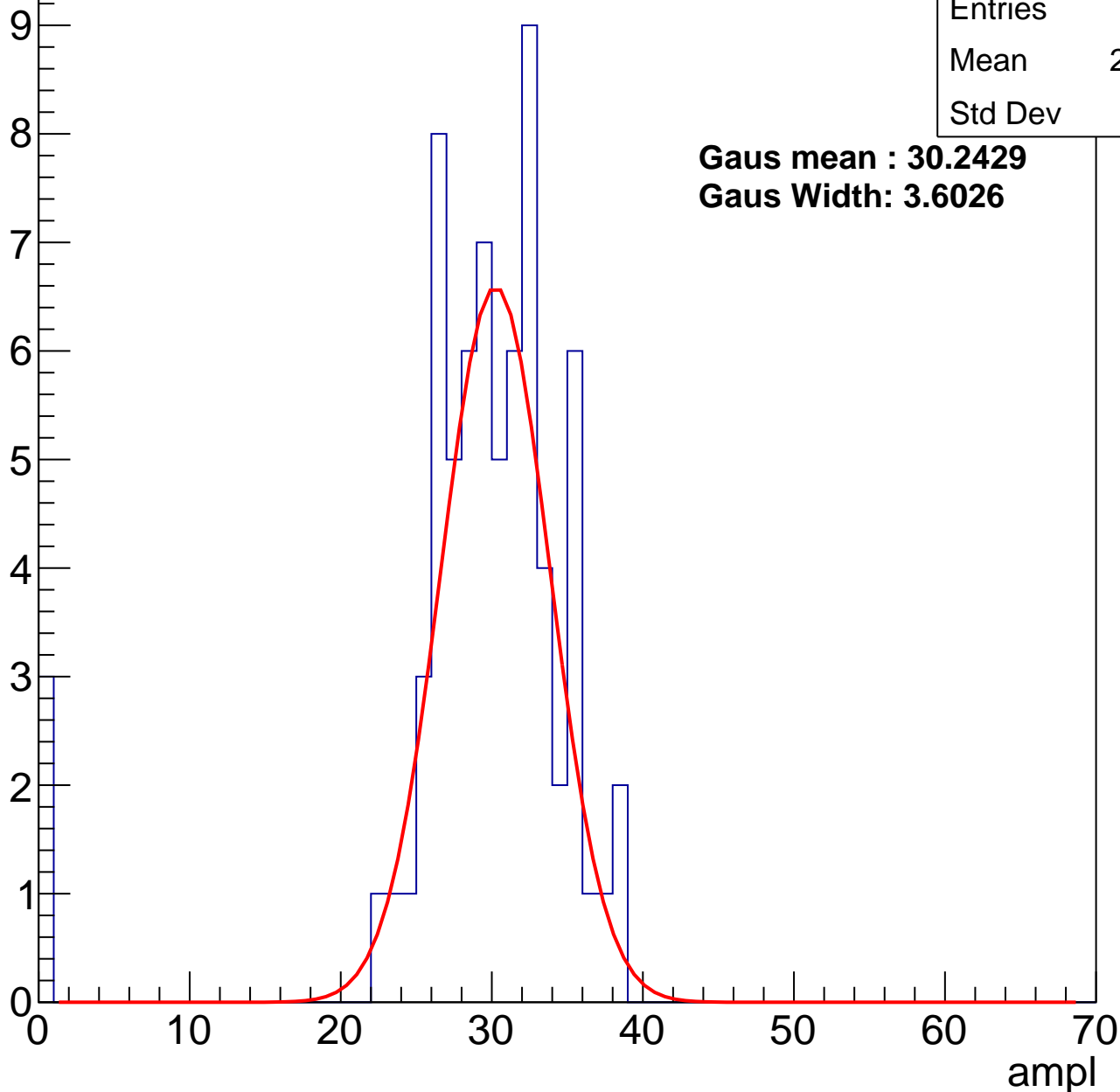
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	28.75
Std Dev	7.01

Gaus mean : 30.2429

Gaus Width: 3.6026



B0L000S, U4-ch89, adc1

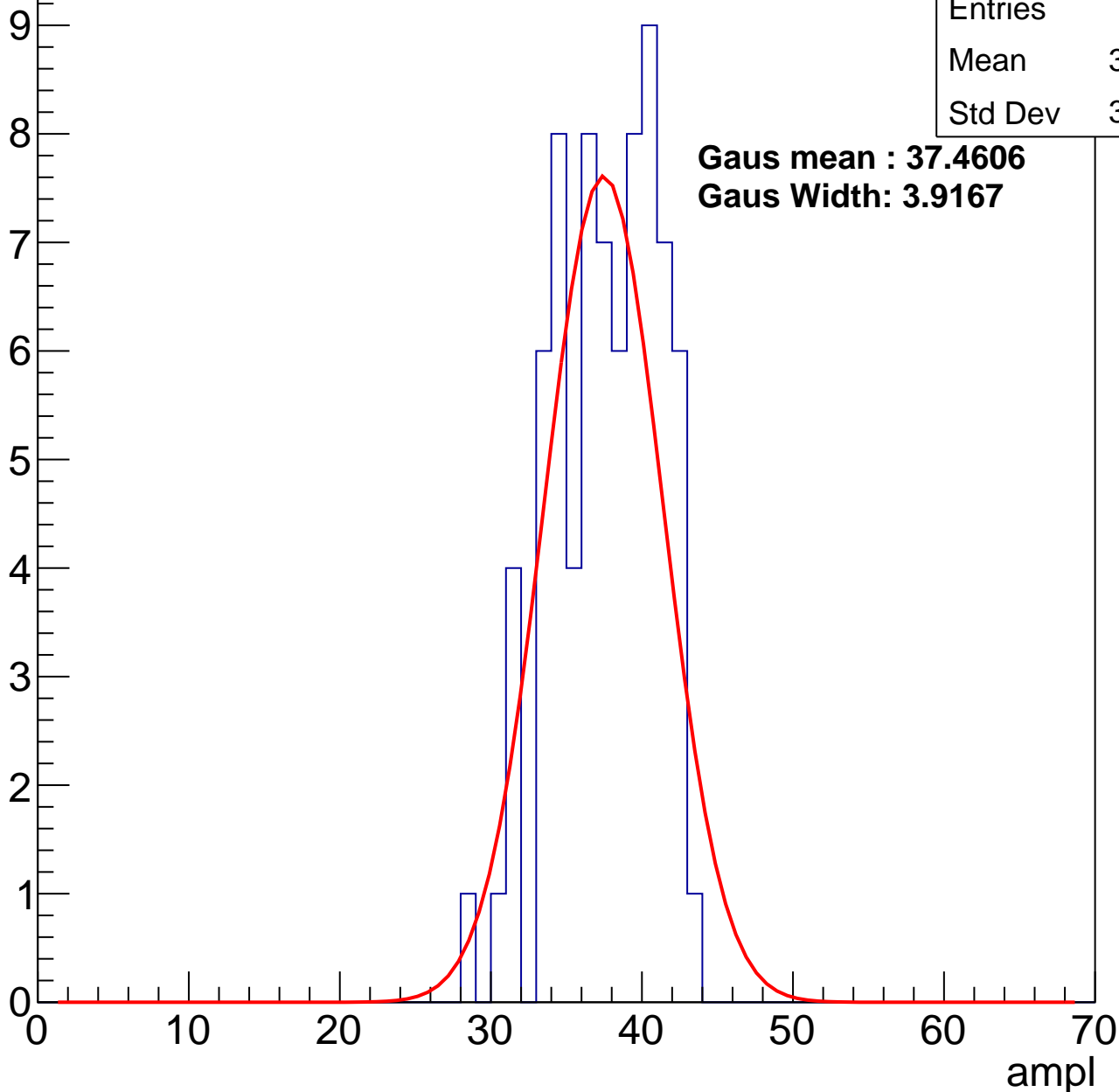
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	37.12
Std Dev	3.407

Gaus mean : 37.4606

Gaus Width: 3.9167



B0L000S, U4-ch89, adc2

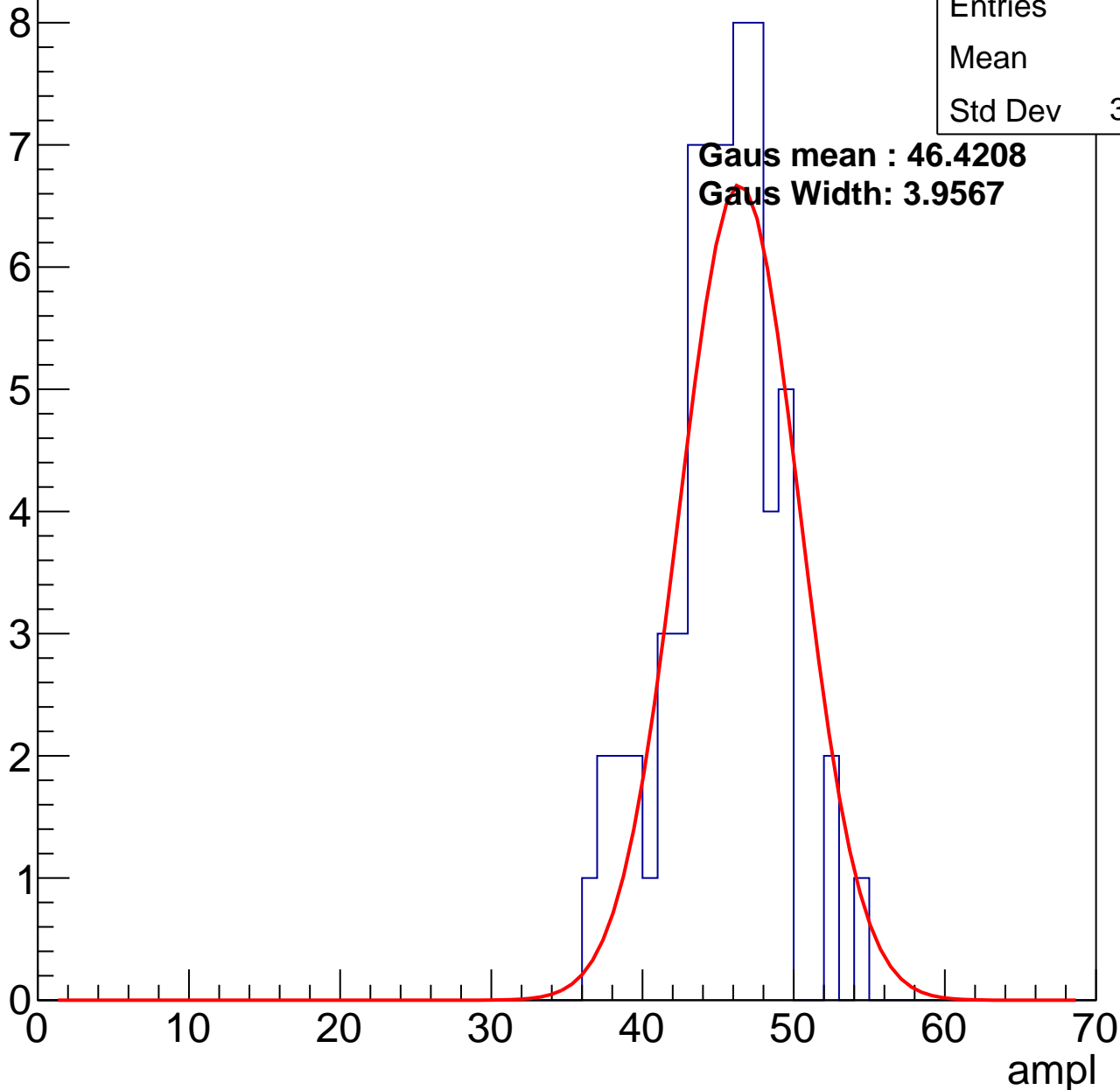
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	44.7
Std Dev	3.659

Gaus mean : 46.4208

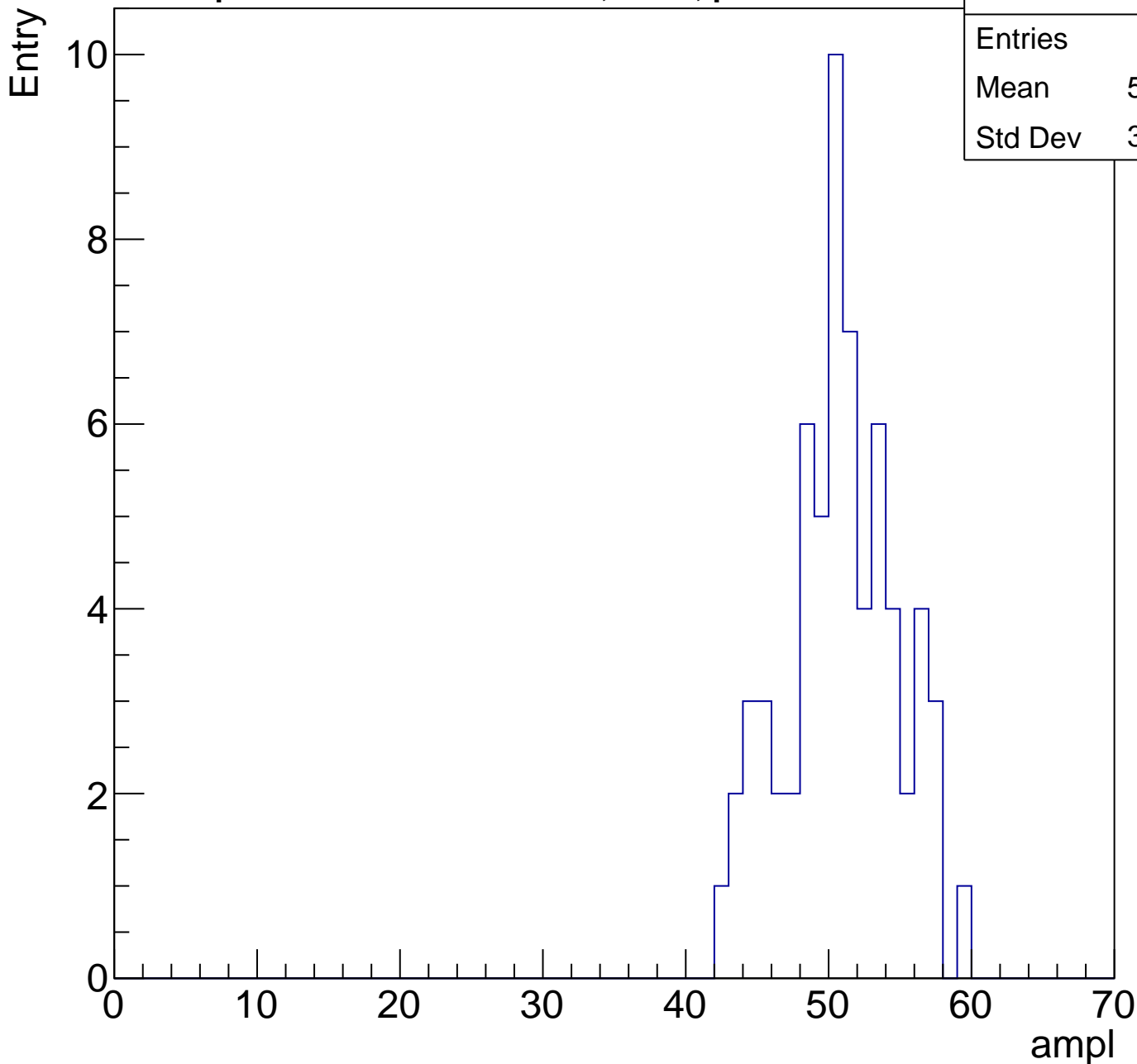
Gaus Width: 3.9567



B0L000S, U4-ch89, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	65
Mean	50.42
Std Dev	3.875

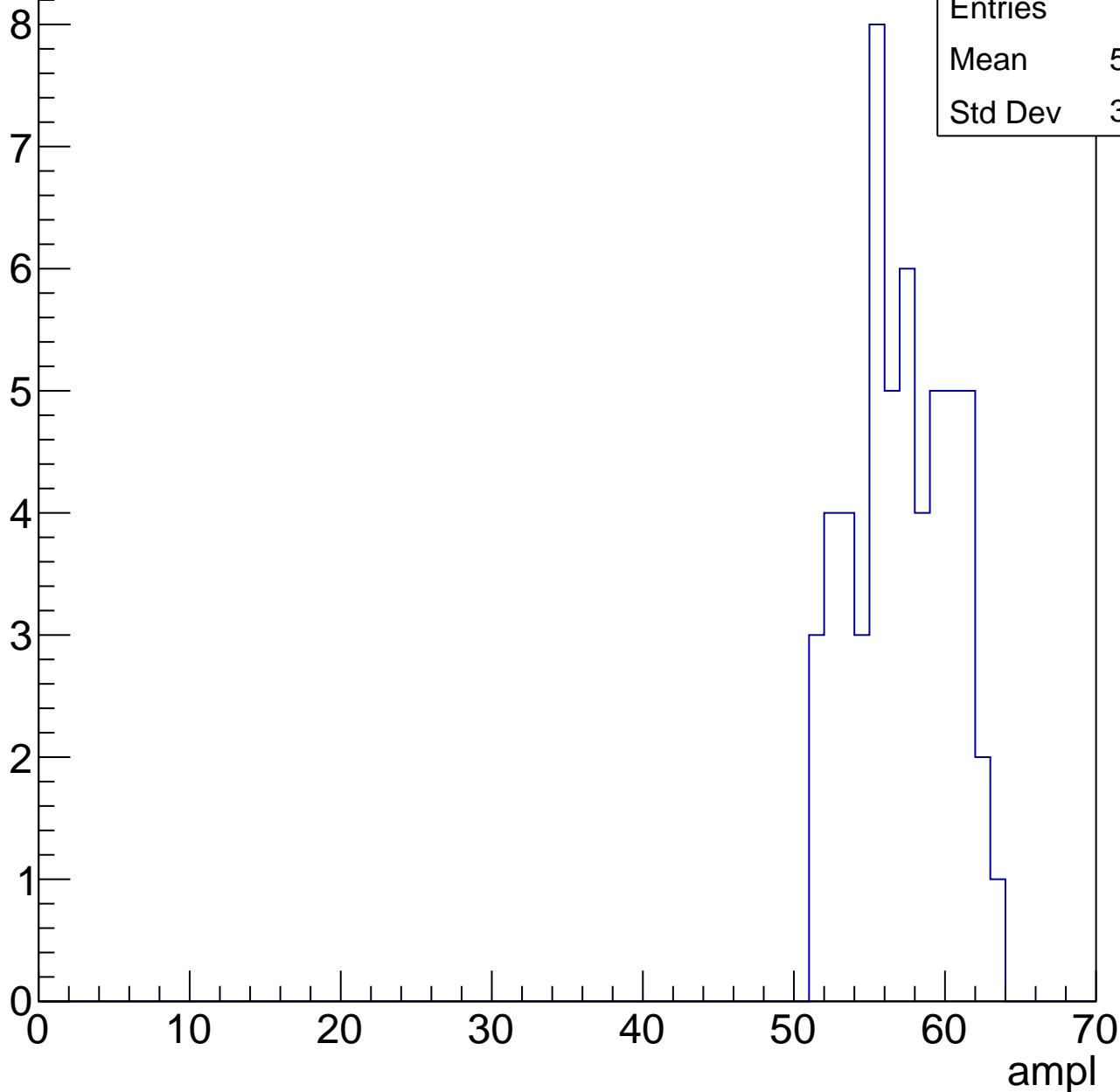


B0L000S, U4-ch89, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.65
Std Dev	3.204

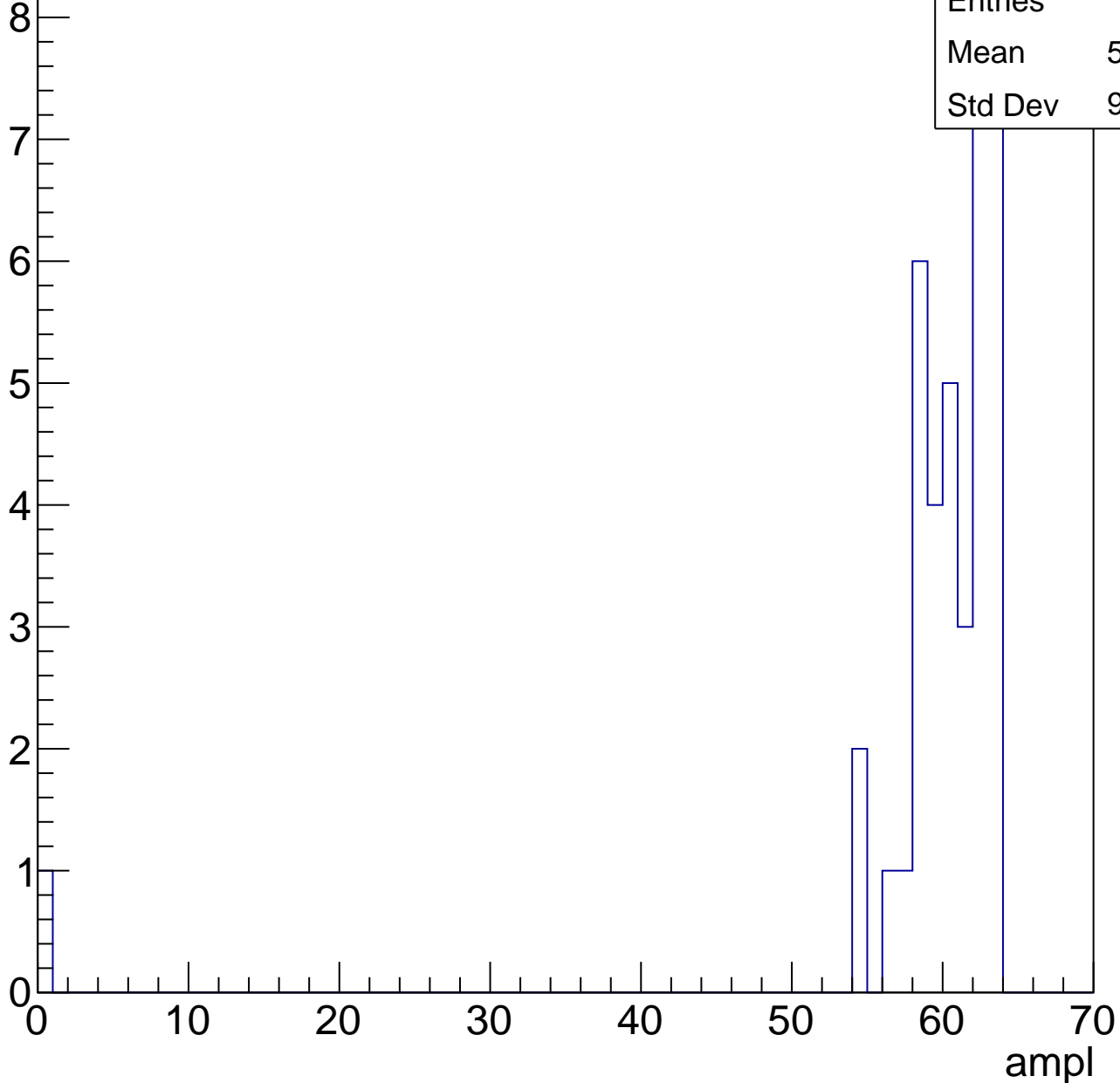


B0L000S, U4-ch89, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.67
Std Dev	9.822



B0L000S, U4-ch89, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	61.67
Std Dev	0.9428

B0L000S, U4-ch89, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U4-ch90, adc0

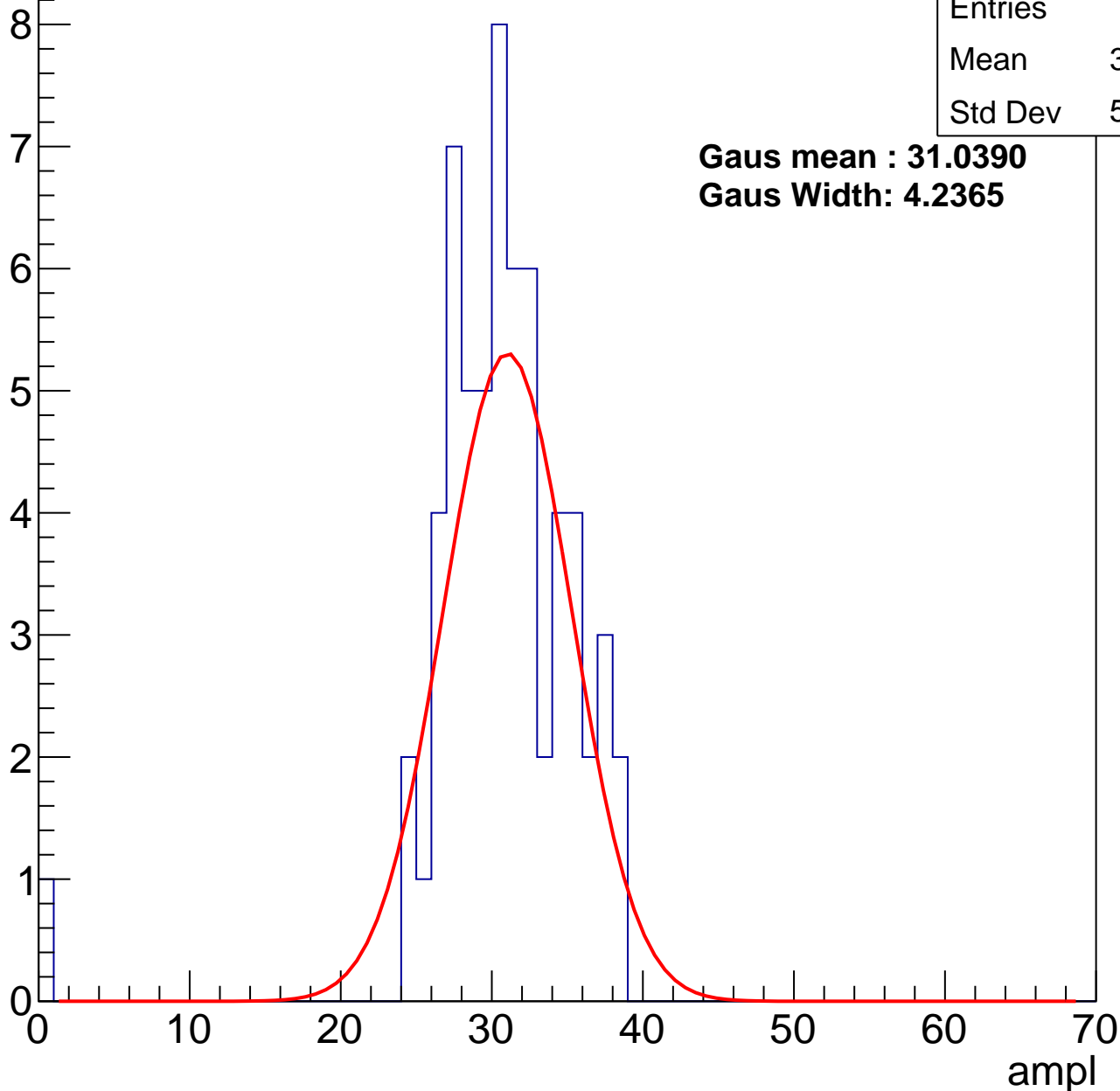
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.16
Std Dev	5.246

Gaus mean : 31.0390

Gaus Width: 4.2365



B0L000S, U4-ch90, adc1

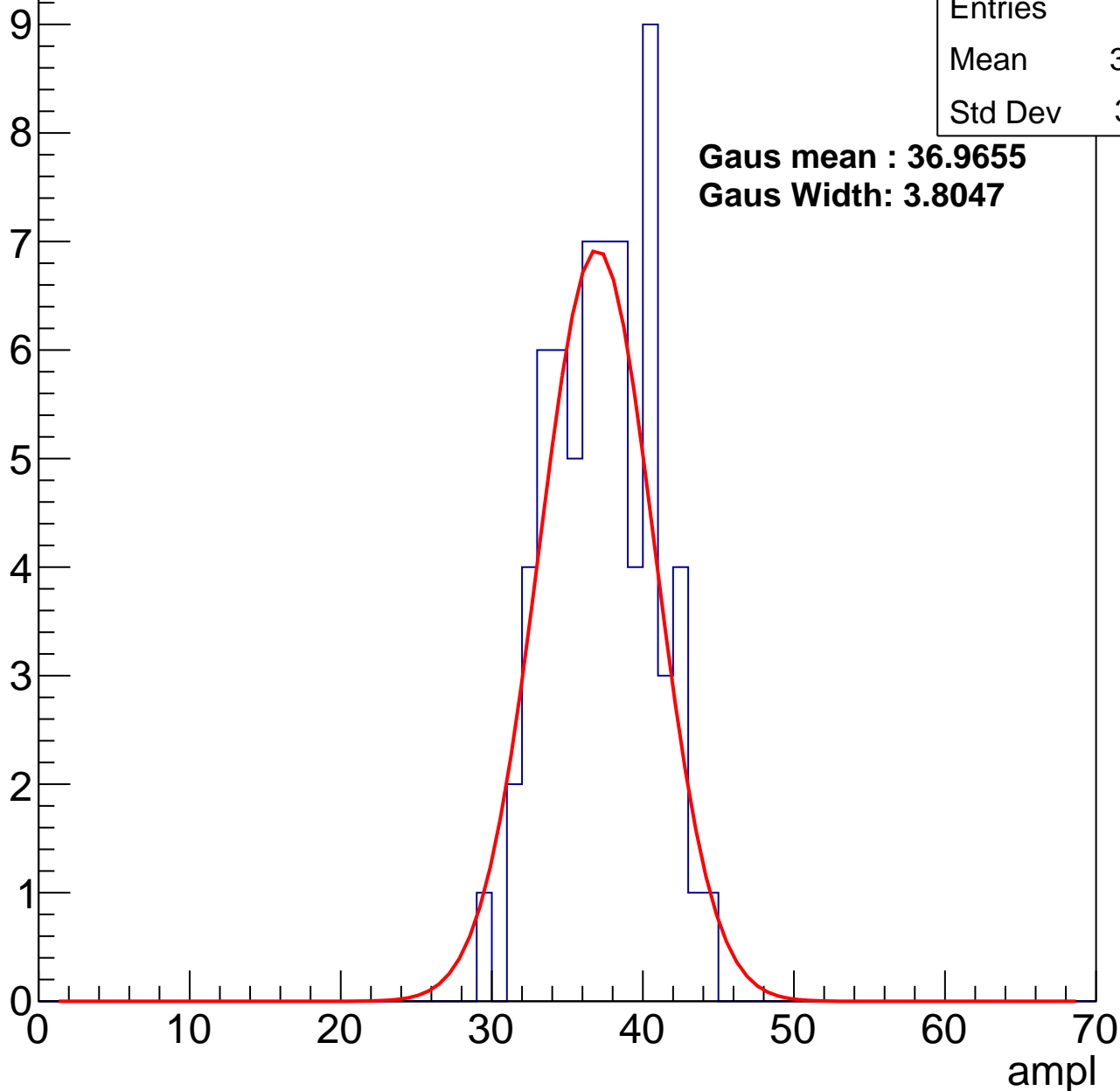
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.82
Std Dev	3.341

Gaus mean : 36.9655

Gaus Width: 3.8047



B0L000S, U4-ch90, adc2

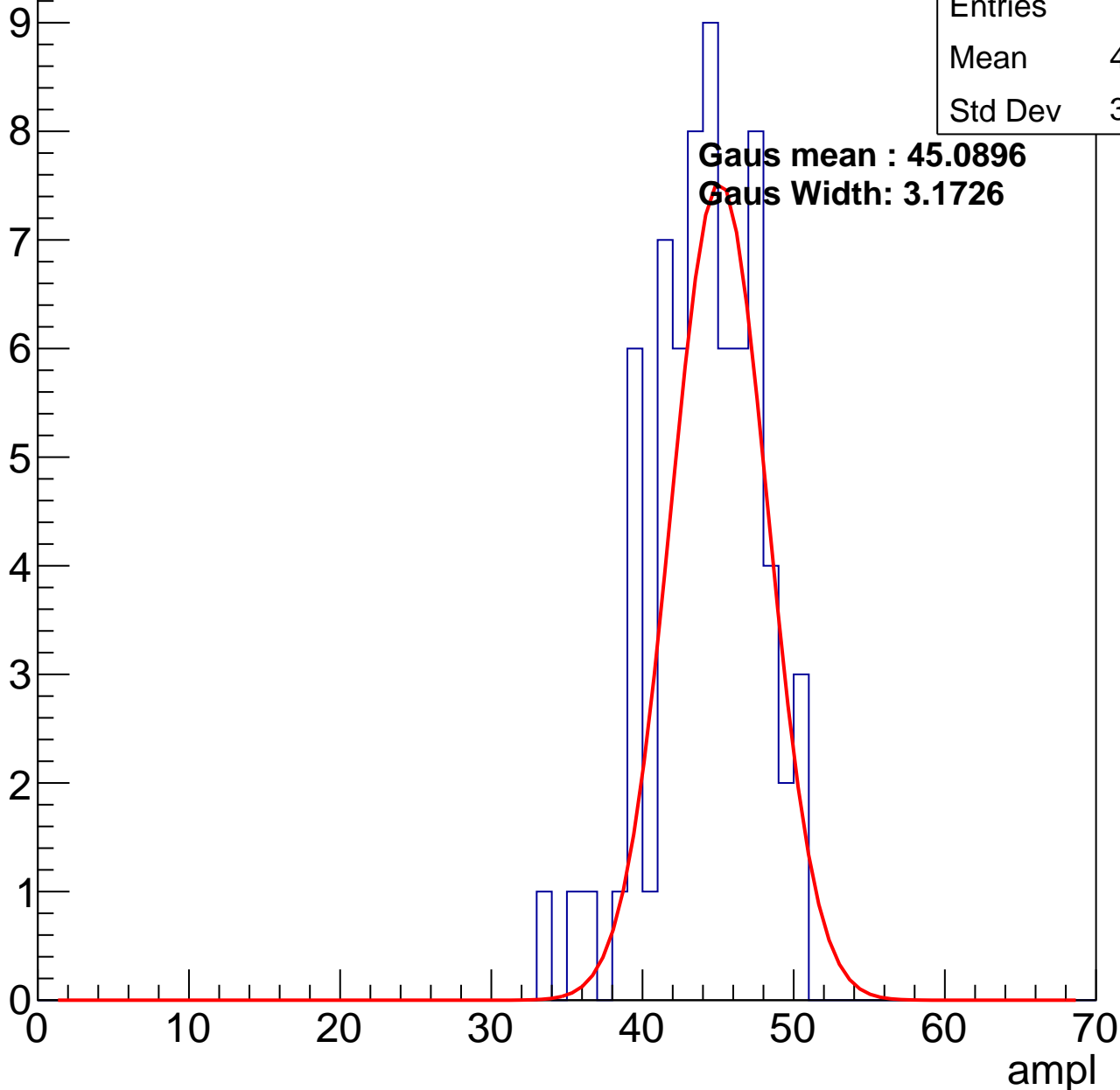
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.67
Std Dev	3.544

Gaus mean : 45.0896

Gaus Width: 3.1726

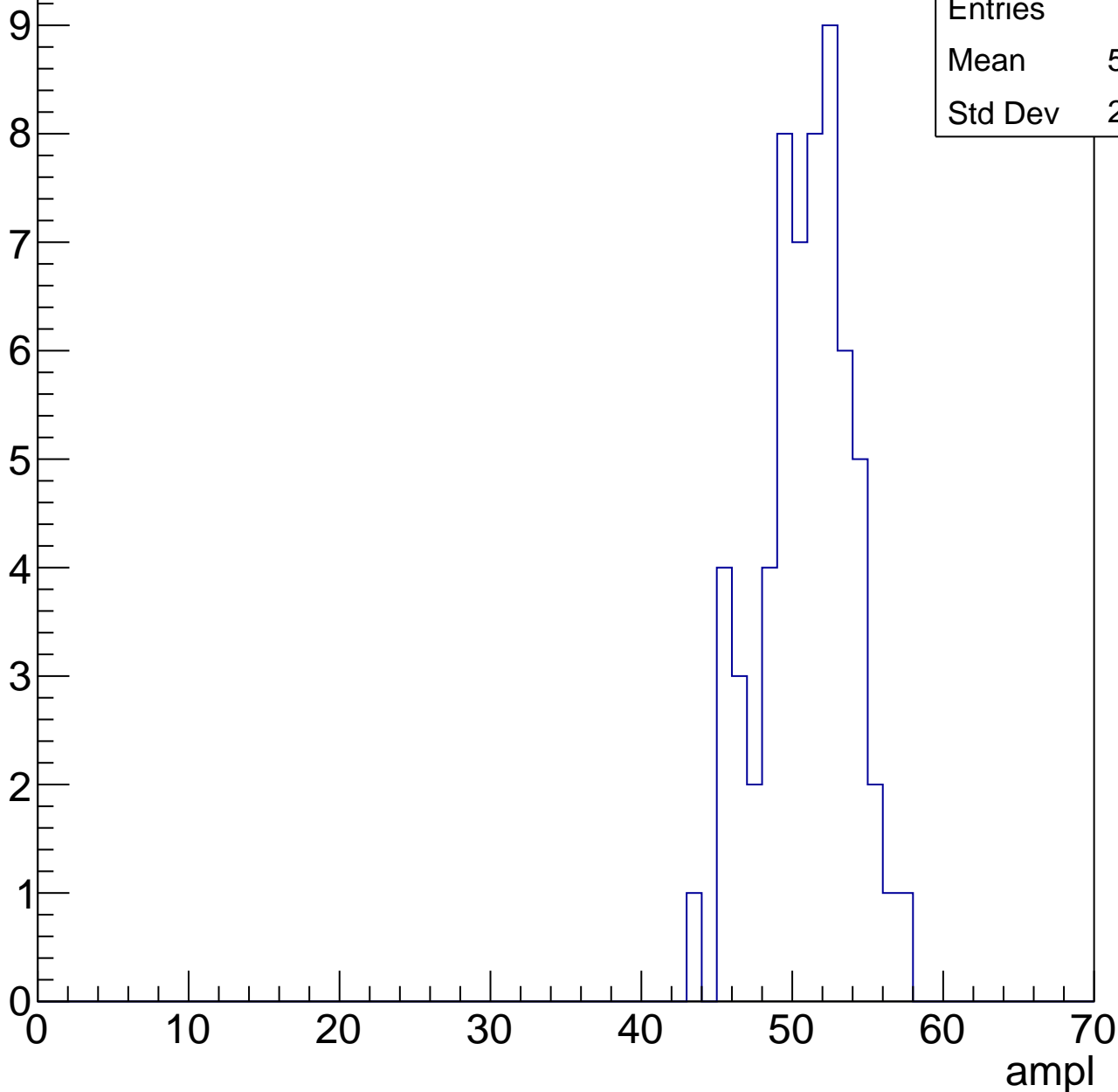


B0L000S, U4-ch90, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

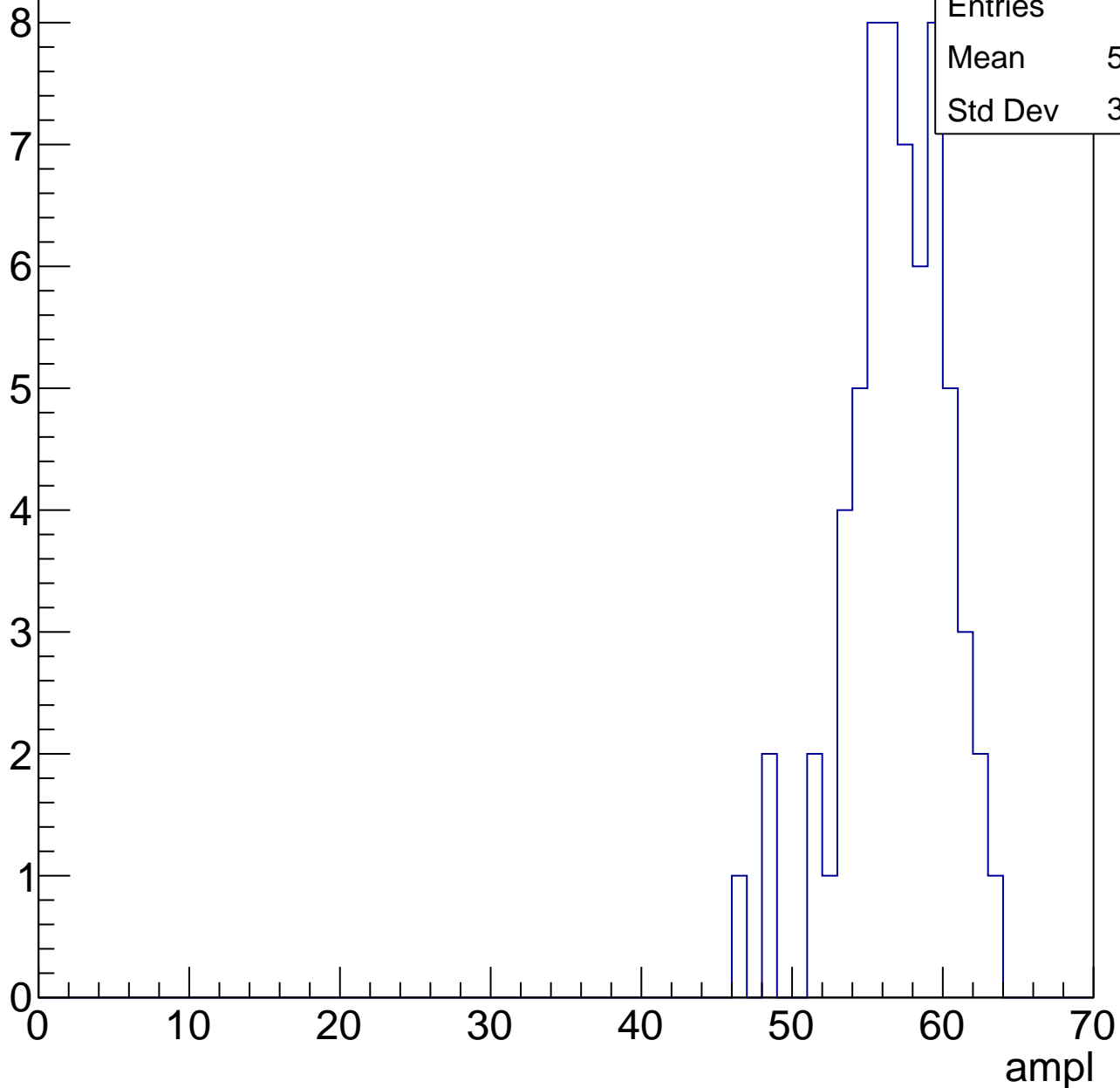
Entries	61
Mean	50.43
Std Dev	2.978



B0L000S, U4-ch90, adc4

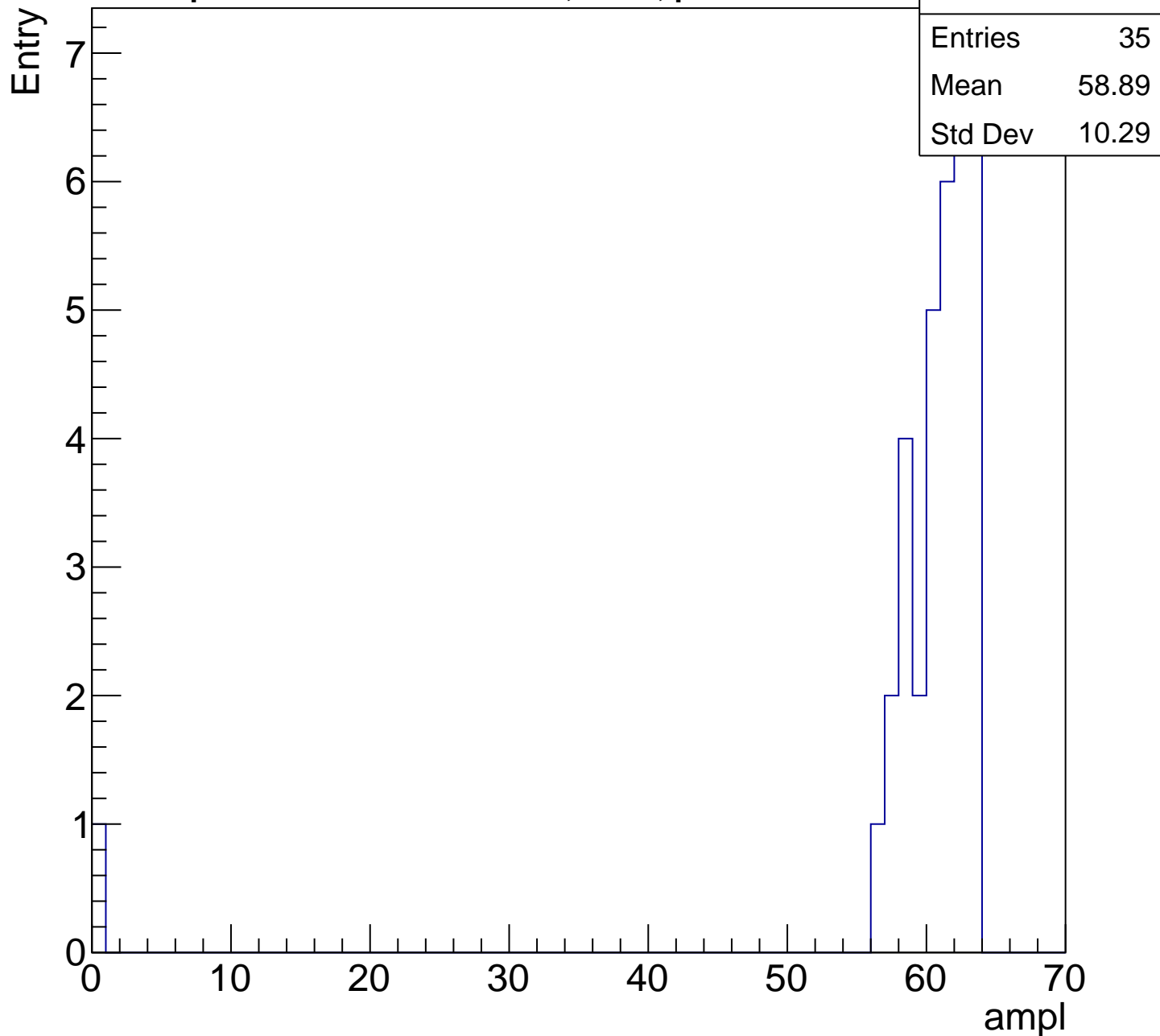
calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch90, adc5

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U4-ch90, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch90, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch91, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	89
Mean	28.85
Std Dev	6.023

Gaus mean : 30.1307

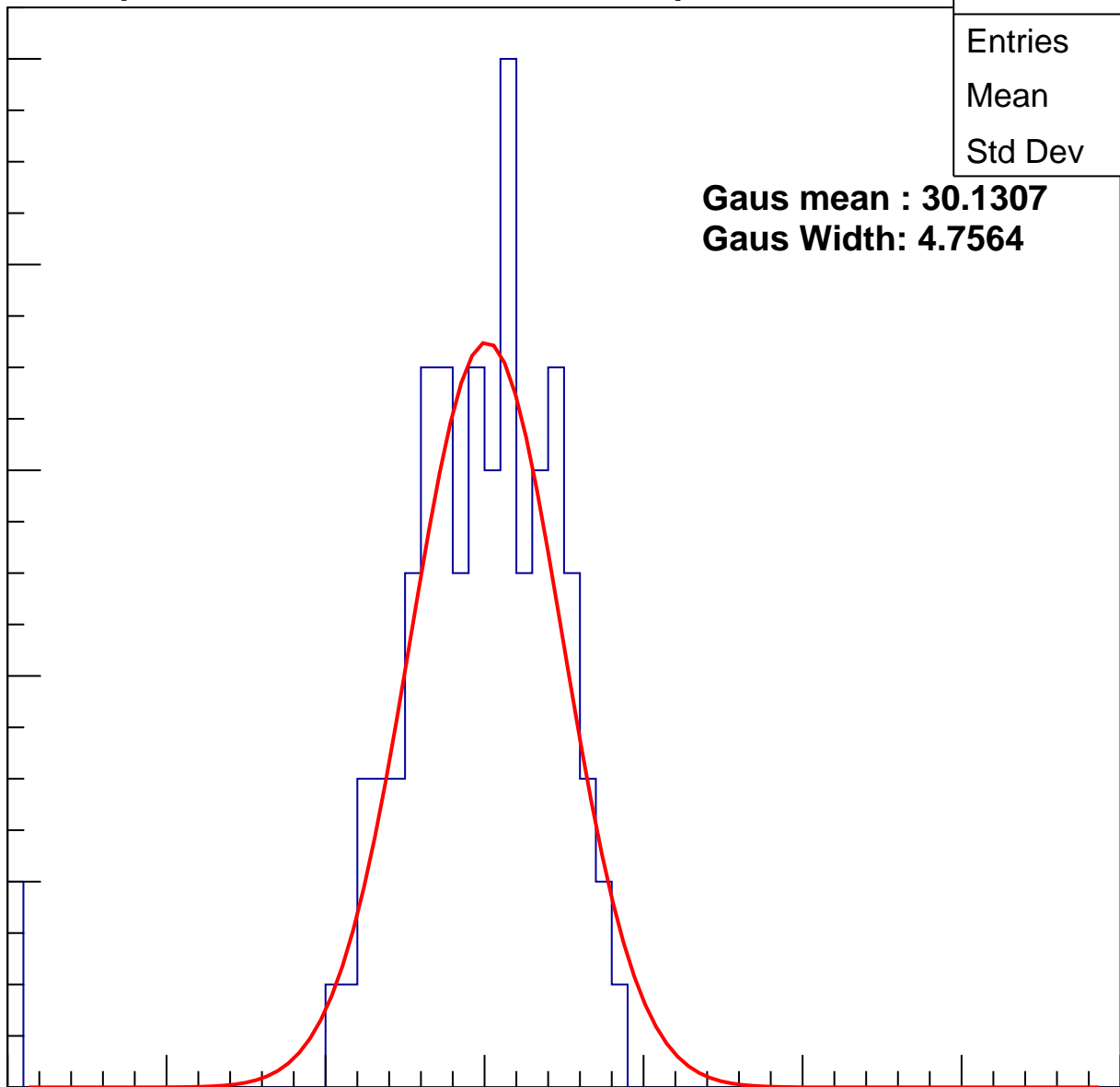
Gaus Width: 4.7564

Entry

10
8
6
4
2
0

ampl

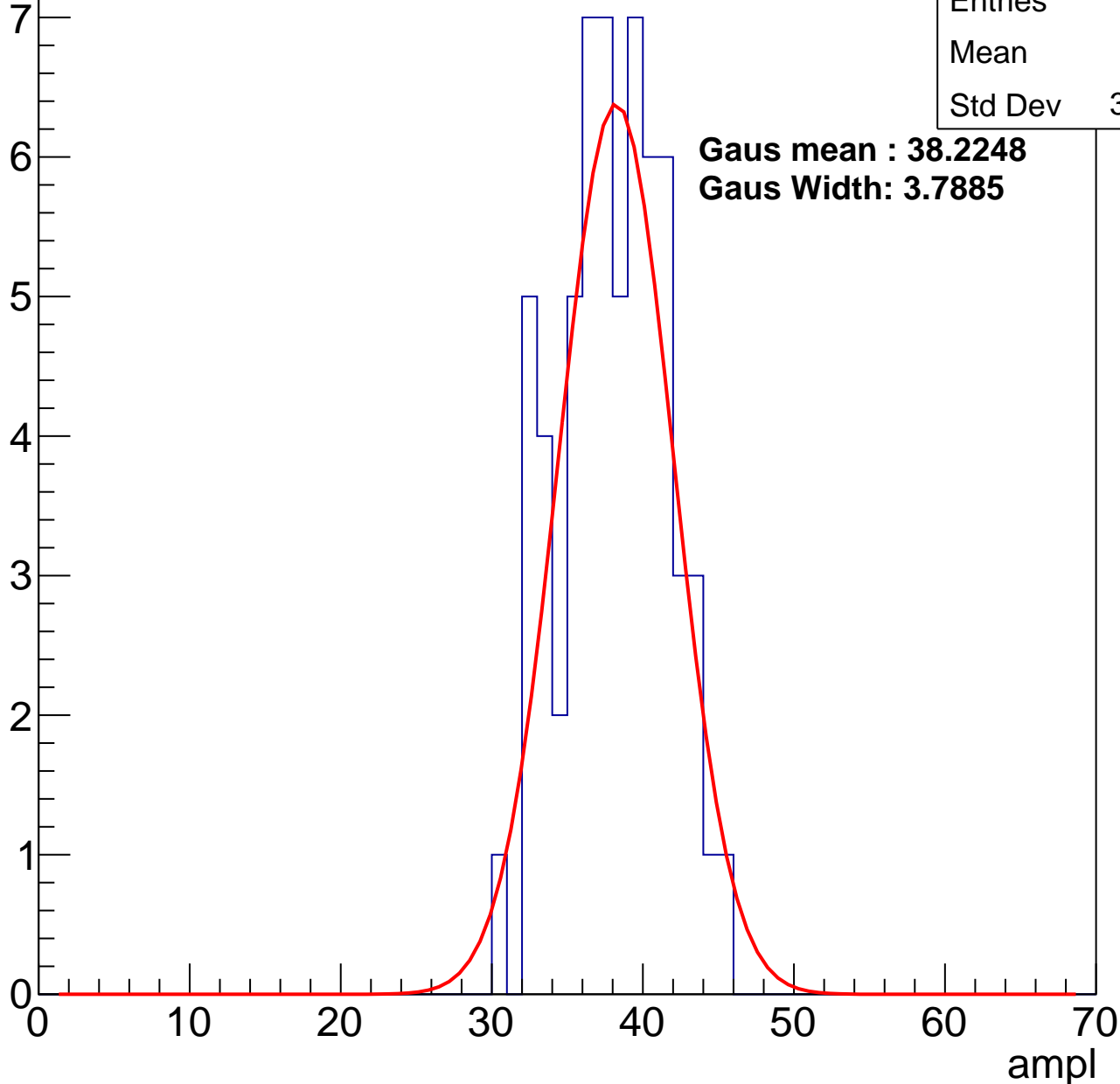
0 10 20 30 40 50 60 70



B0L000S, U4-ch91, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch91, adc2

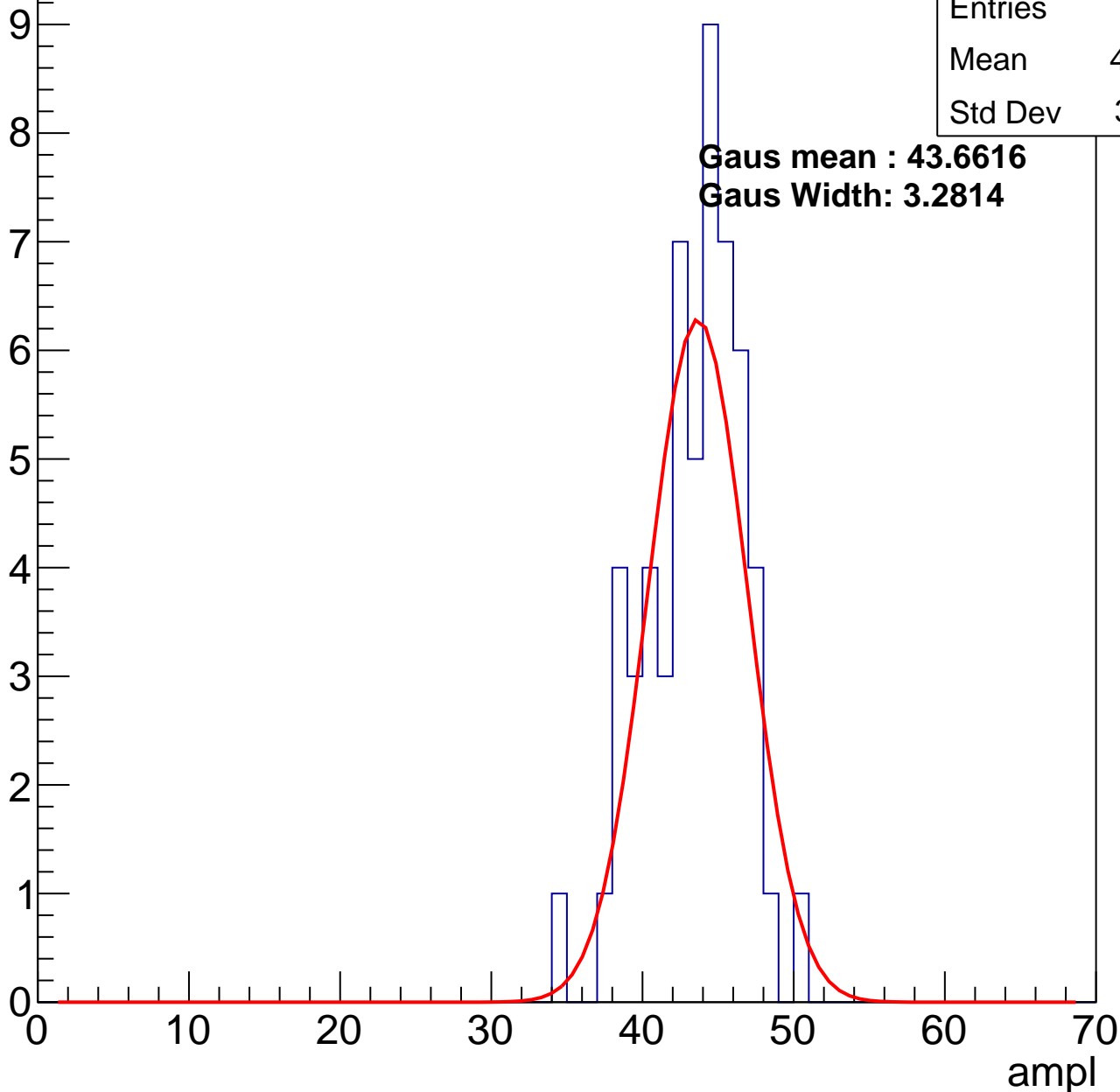
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.95
Std Dev	3.131

Gaus mean : 43.6616

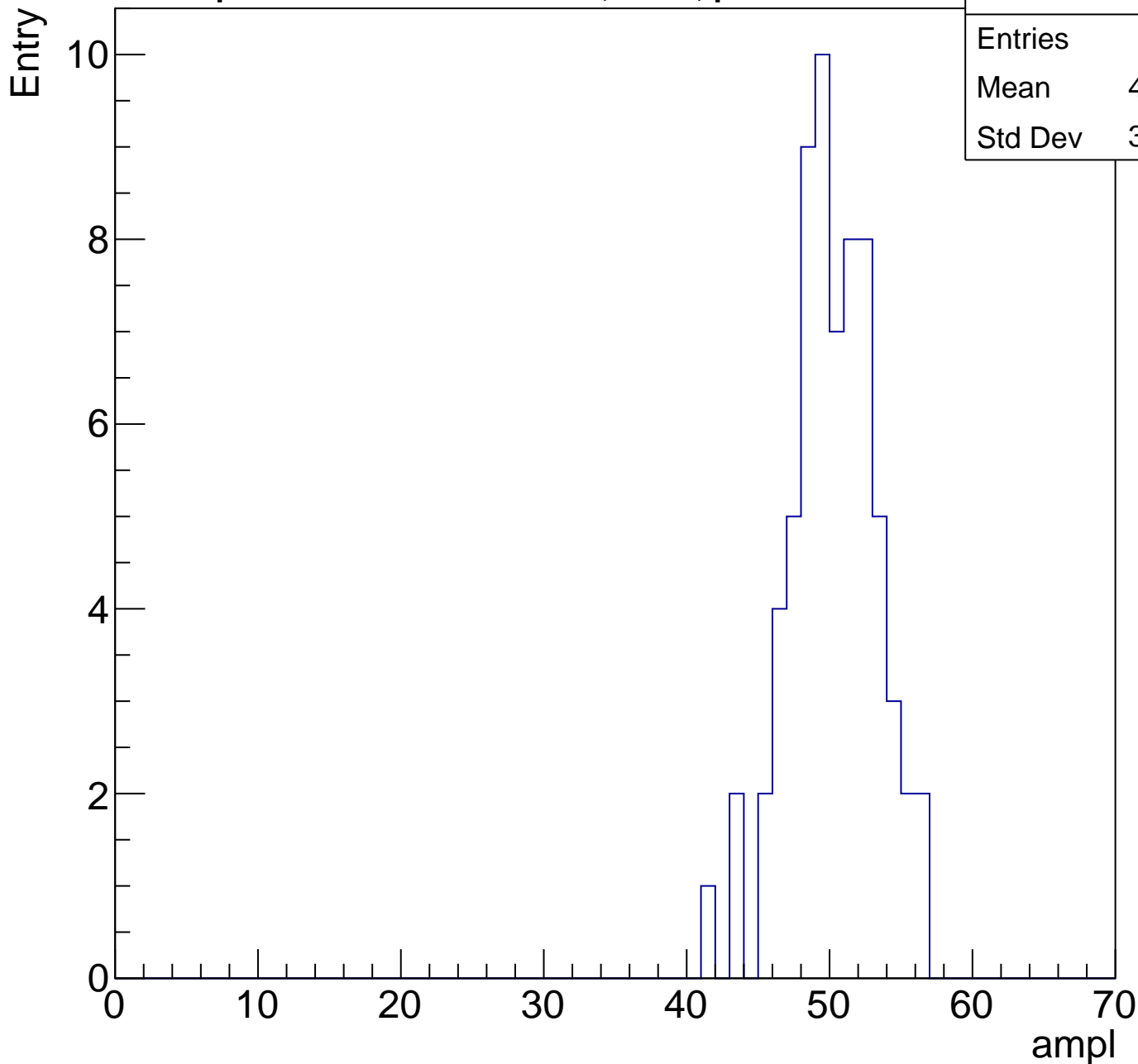
Gaus Width: 3.2814



B0L000S, U4-ch91, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	68
Mean	49.72
Std Dev	3.053

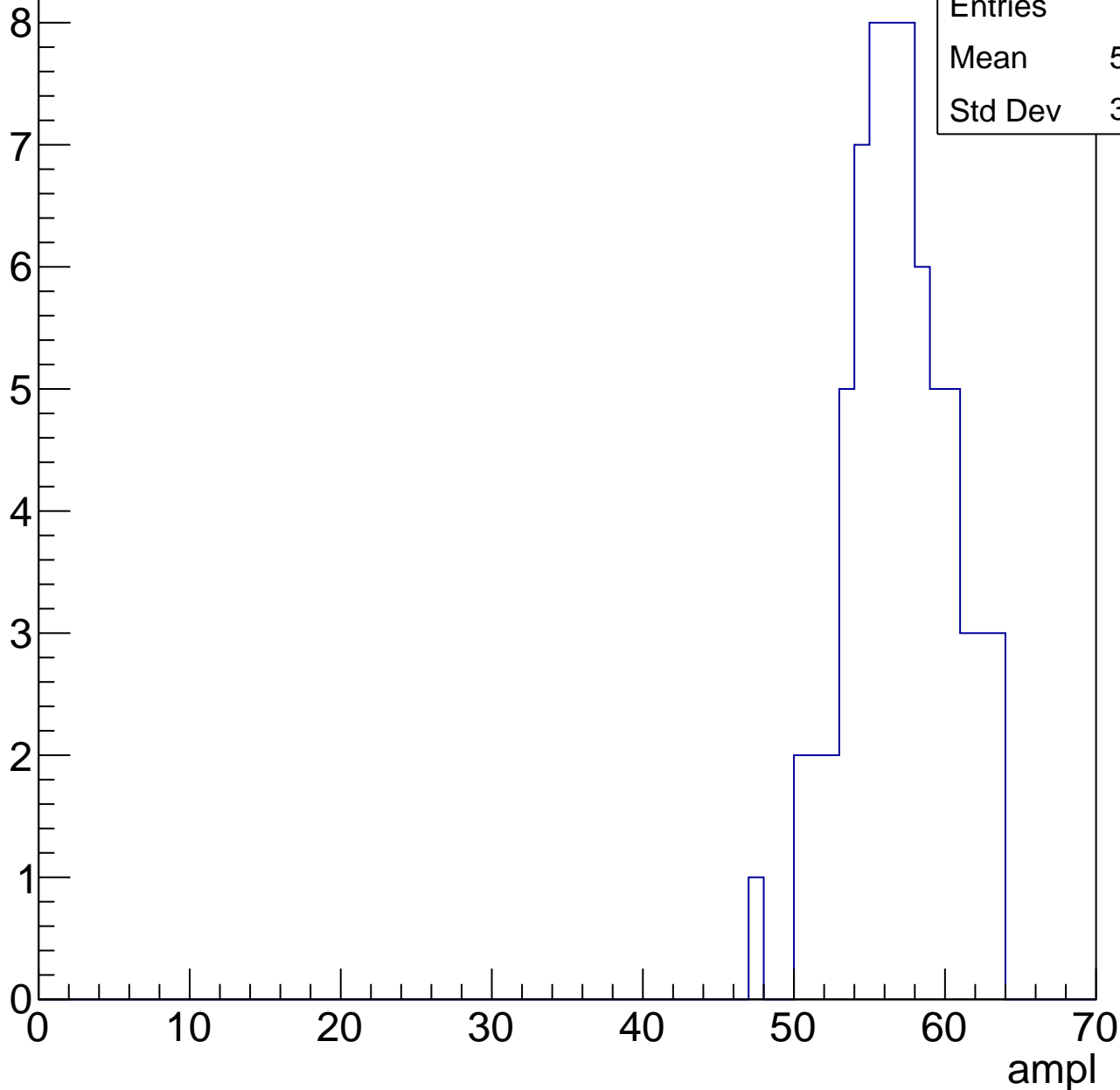


B0L000S, U4-ch91, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	56.49
Std Dev	3.402

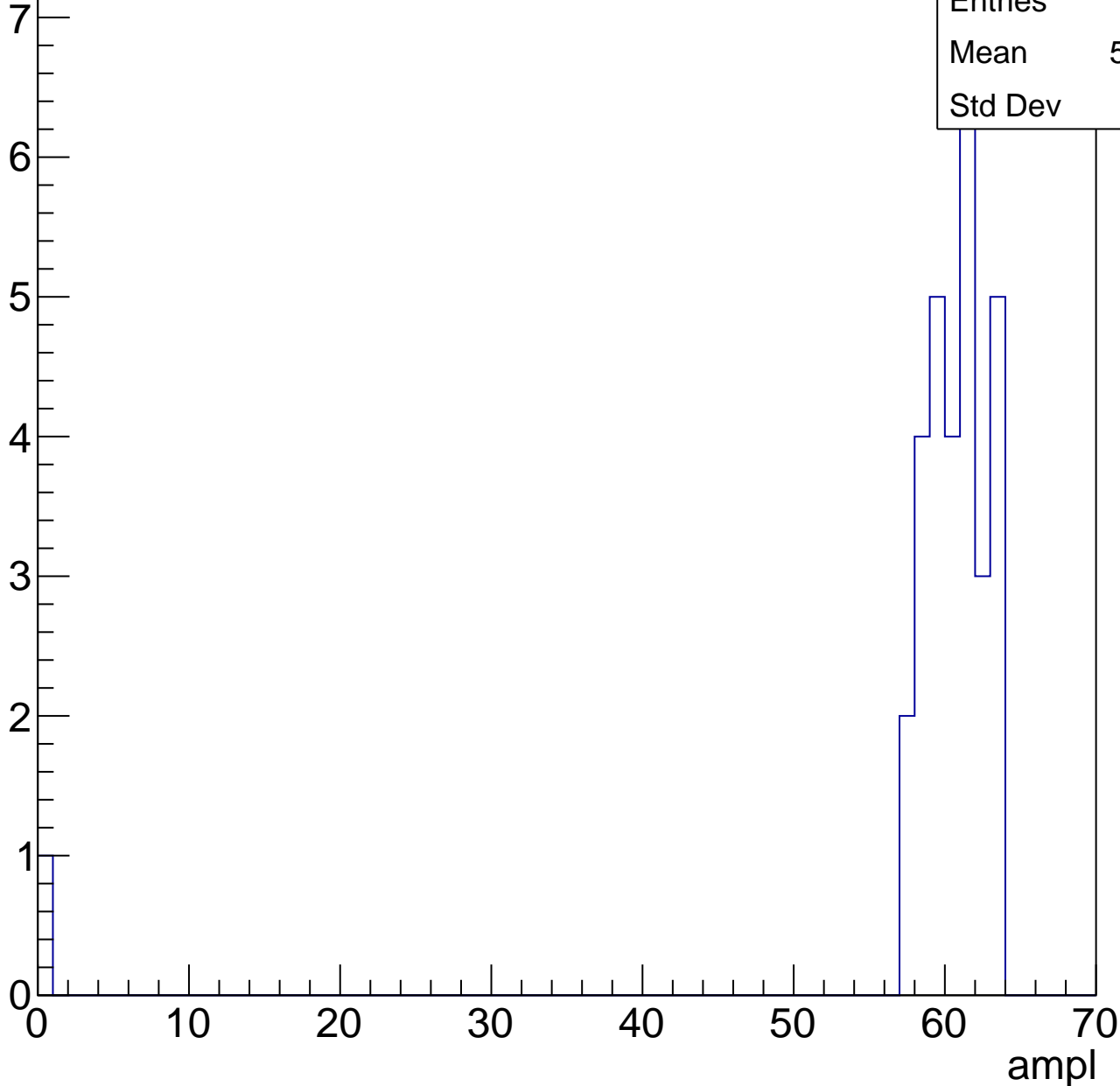


B0L000S, U4-ch91, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

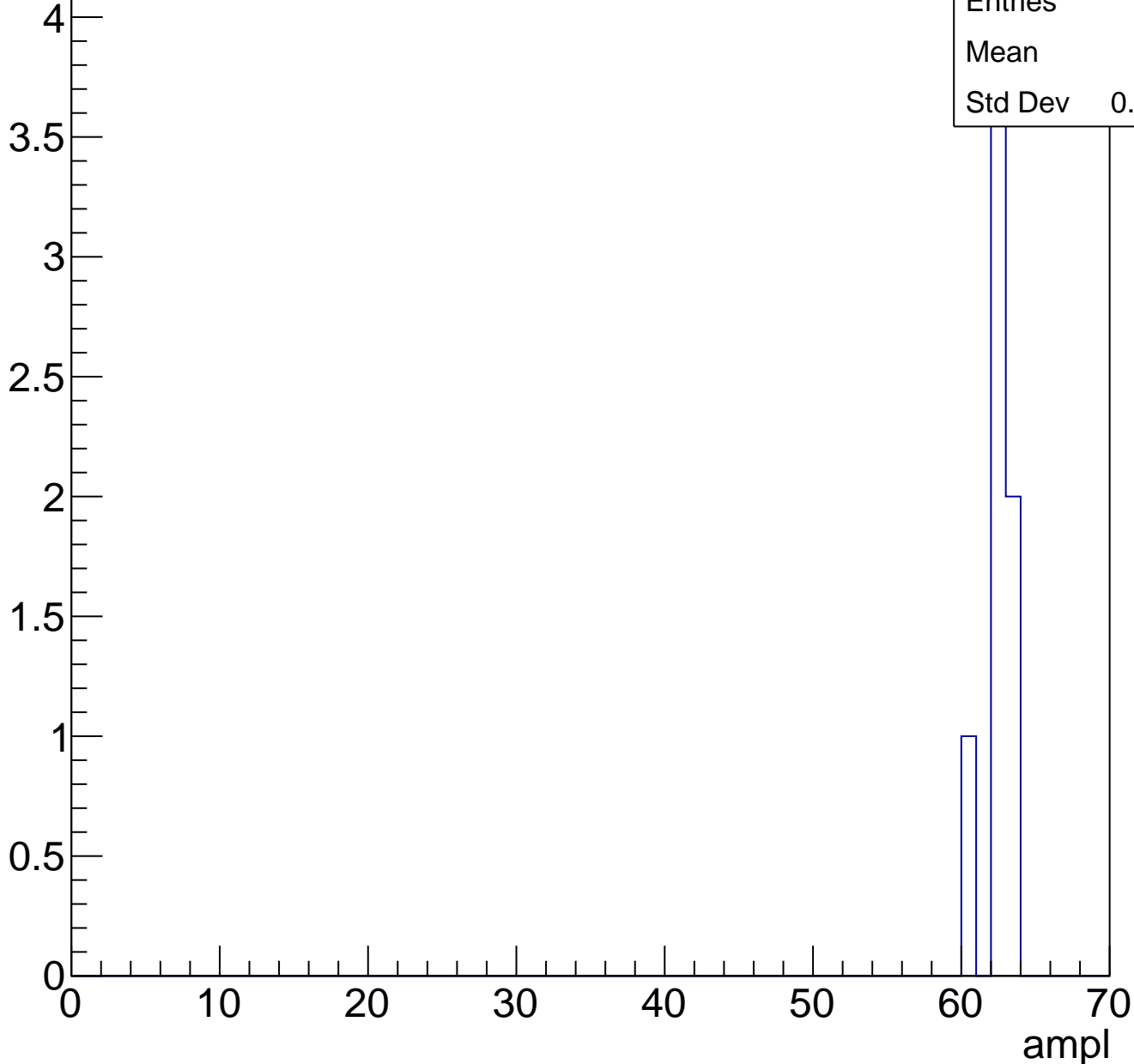
Entries	31
Mean	58.35
Std Dev	10.8



B0L000S, U4-ch91, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch91, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch92, adc0

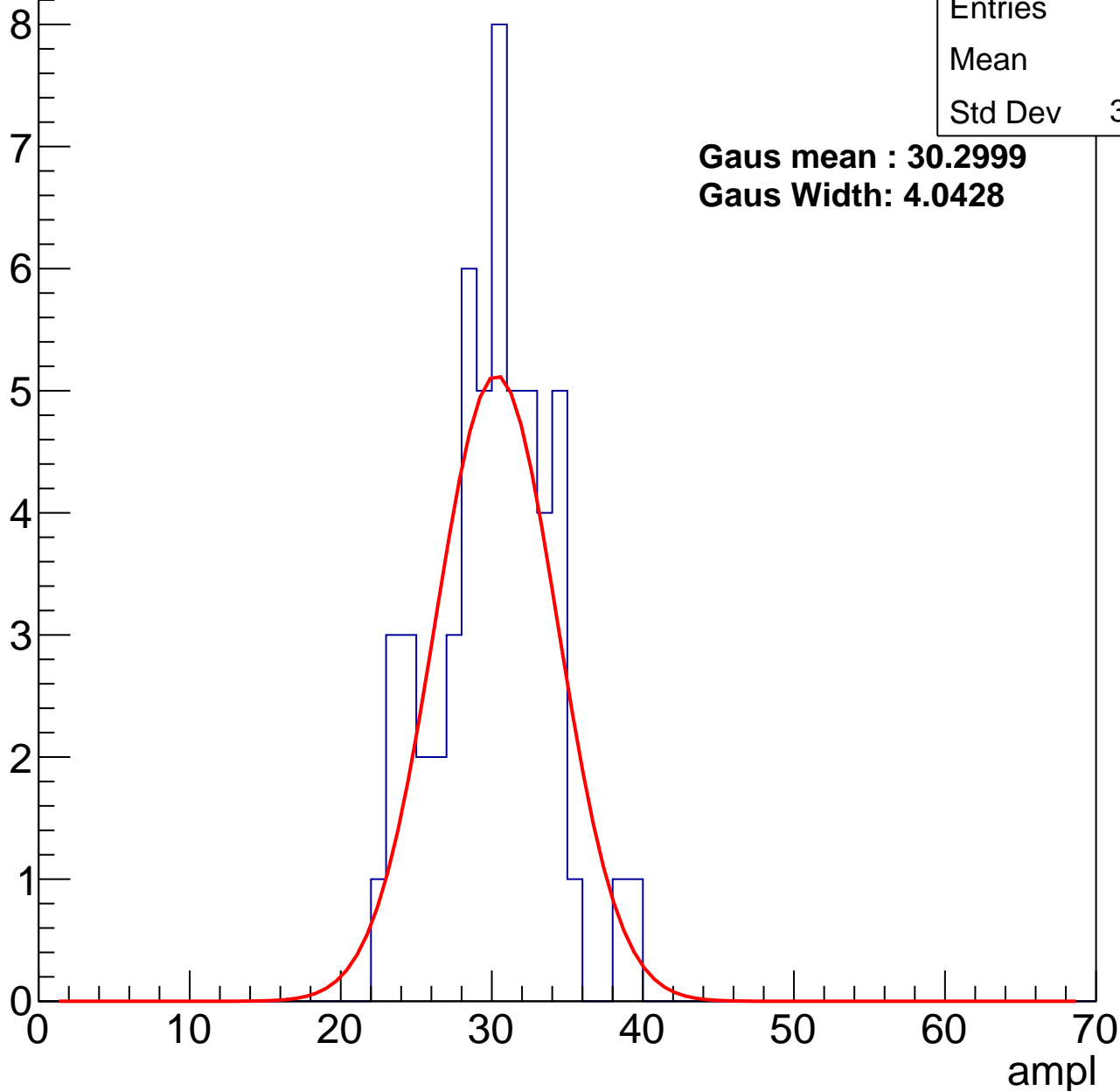
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	29.6
Std Dev	3.715

Gaus mean : 30.2999

Gaus Width: 4.0428



B0L000S, U4-ch92, adc1

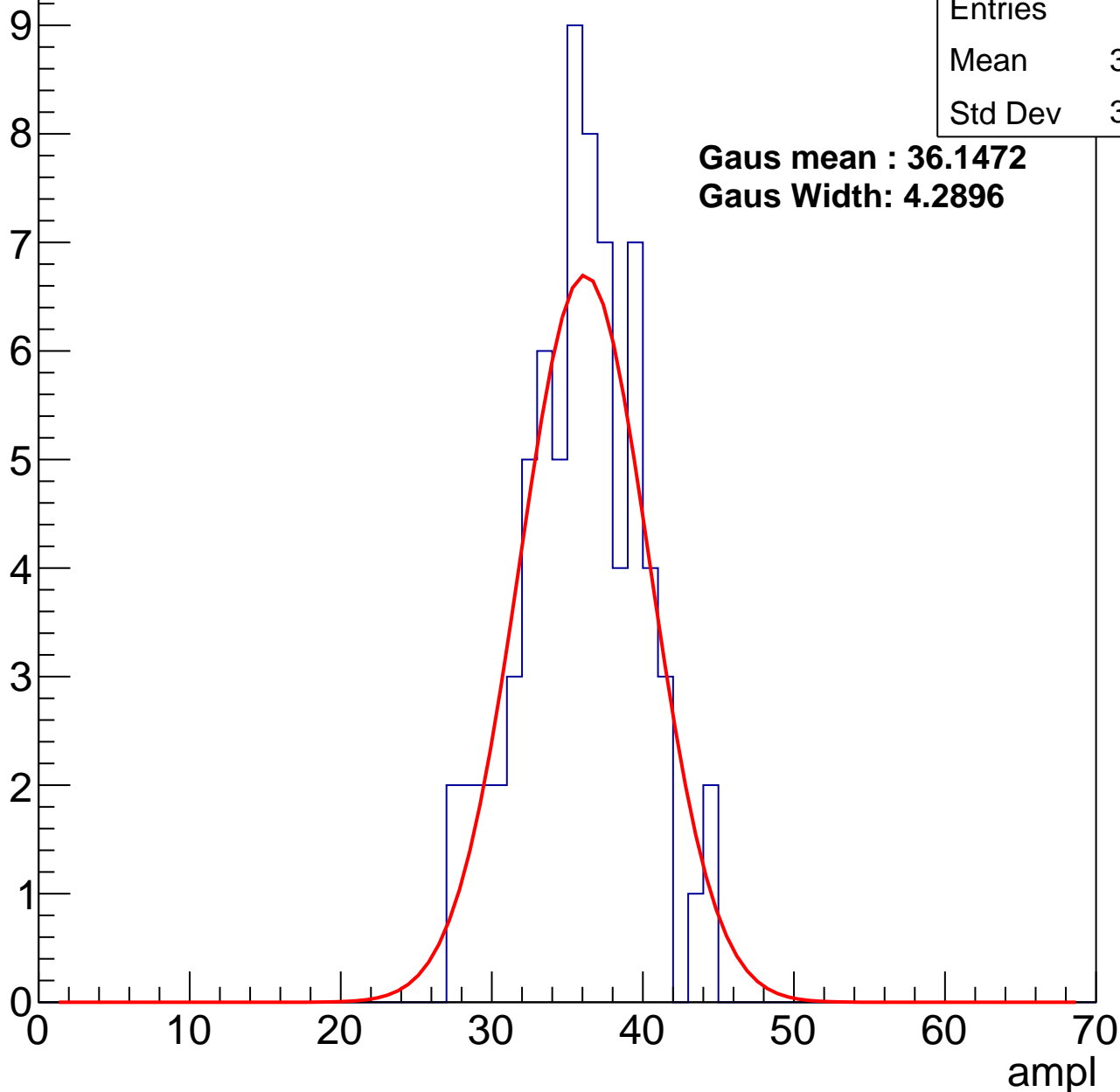
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	35.42
Std Dev	3.865

Gaus mean : 36.1472

Gaus Width: 4.2896



B0L000S, U4-ch92, adc2

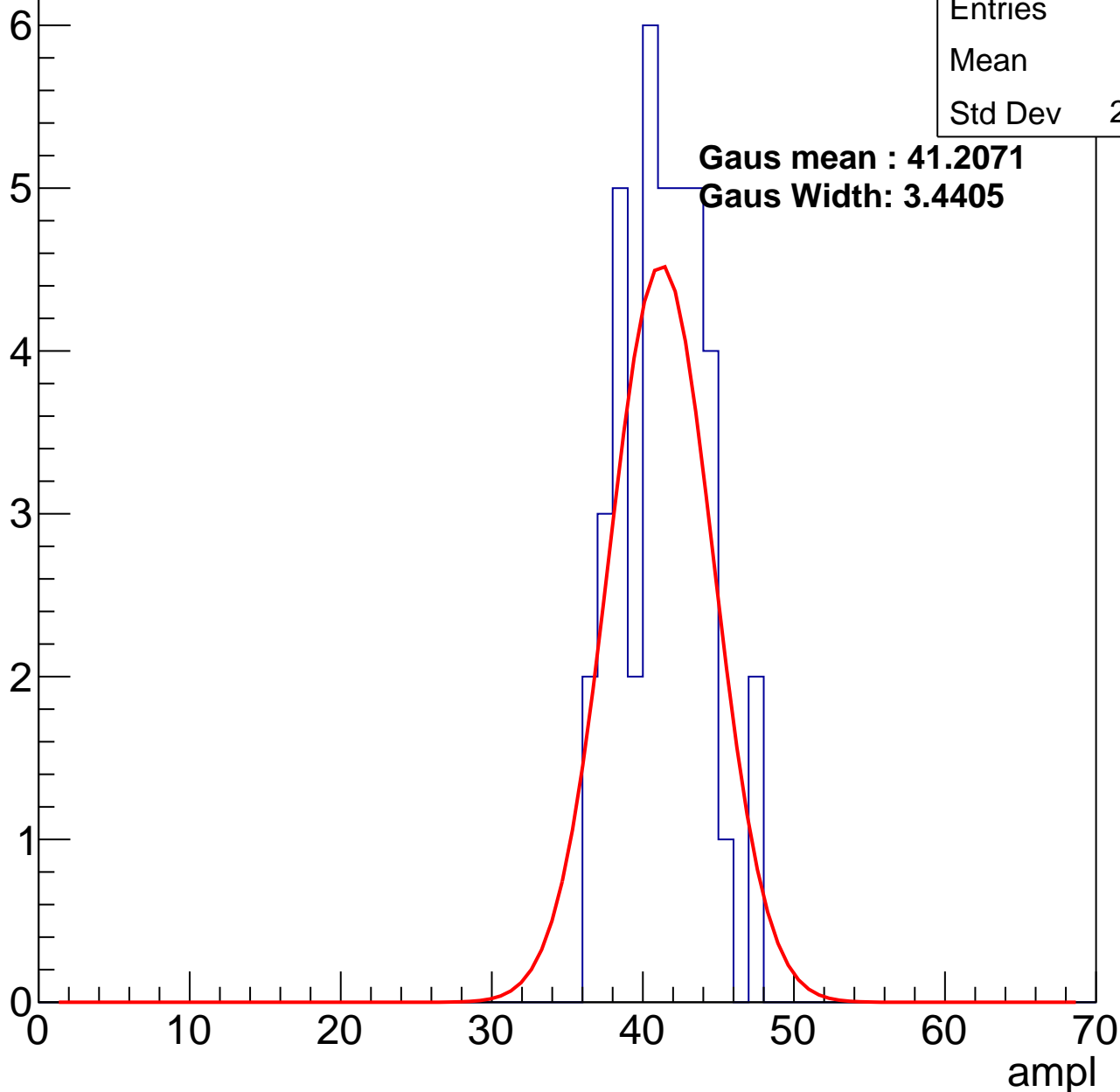
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	40
Mean	40.9
Std Dev	2.764

Gaus mean : 41.2071

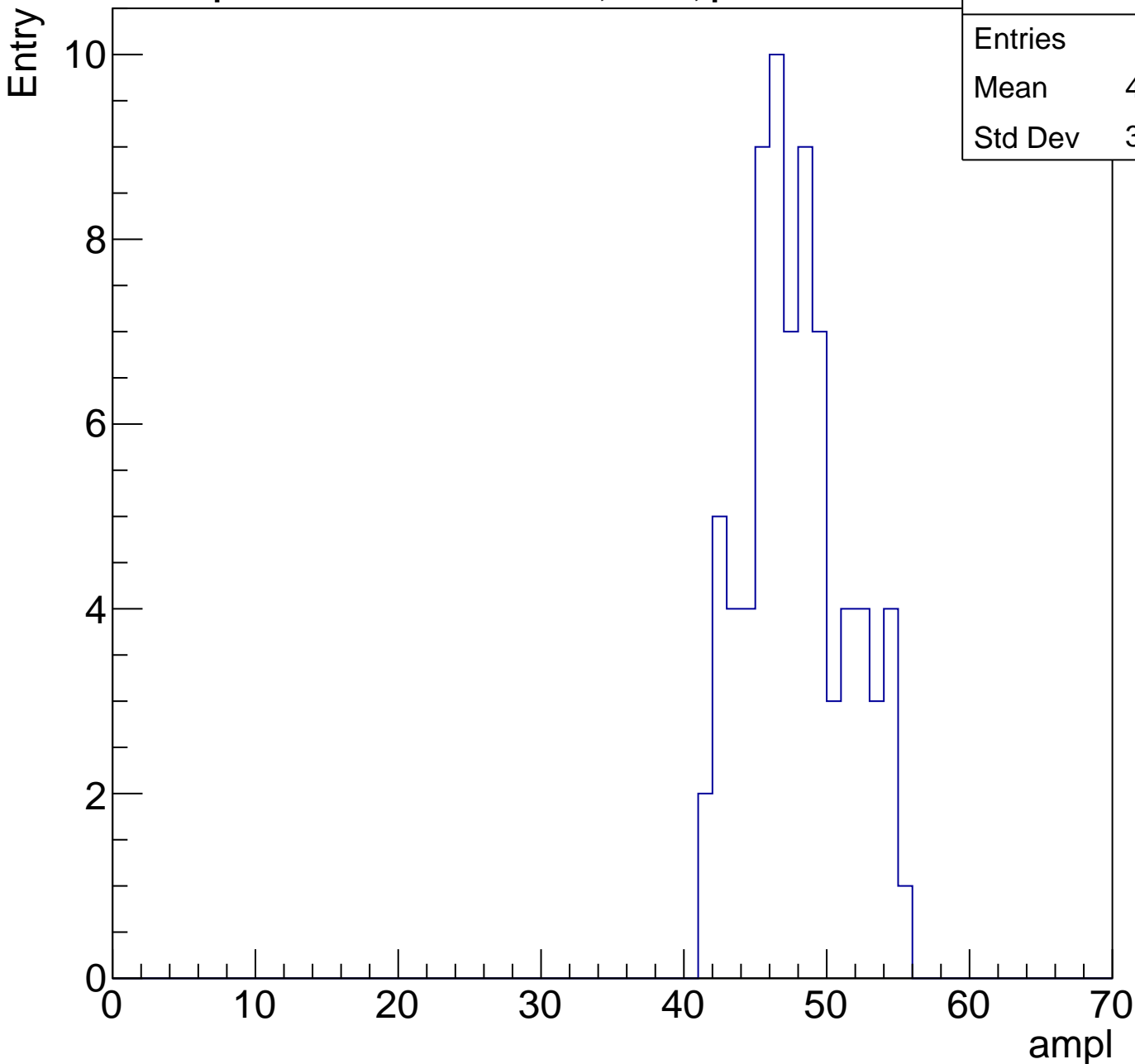
Gaus Width: 3.4405



B0L000S, U4-ch92, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	76
Mean	47.38
Std Dev	3.494

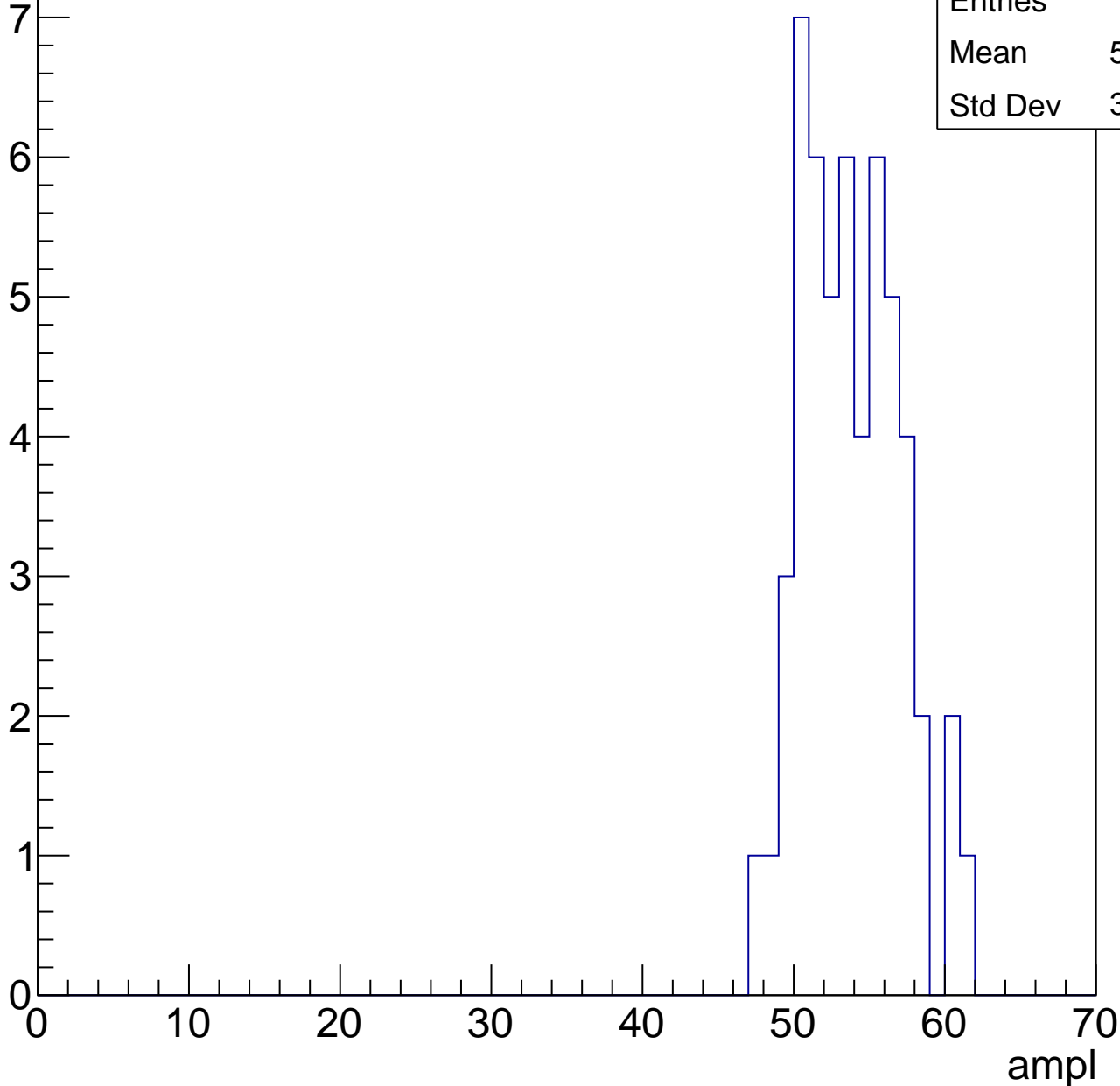


B0L000S, U4-ch92, adc4

calib_packv5_042523_0143.root, FC#5, port B1

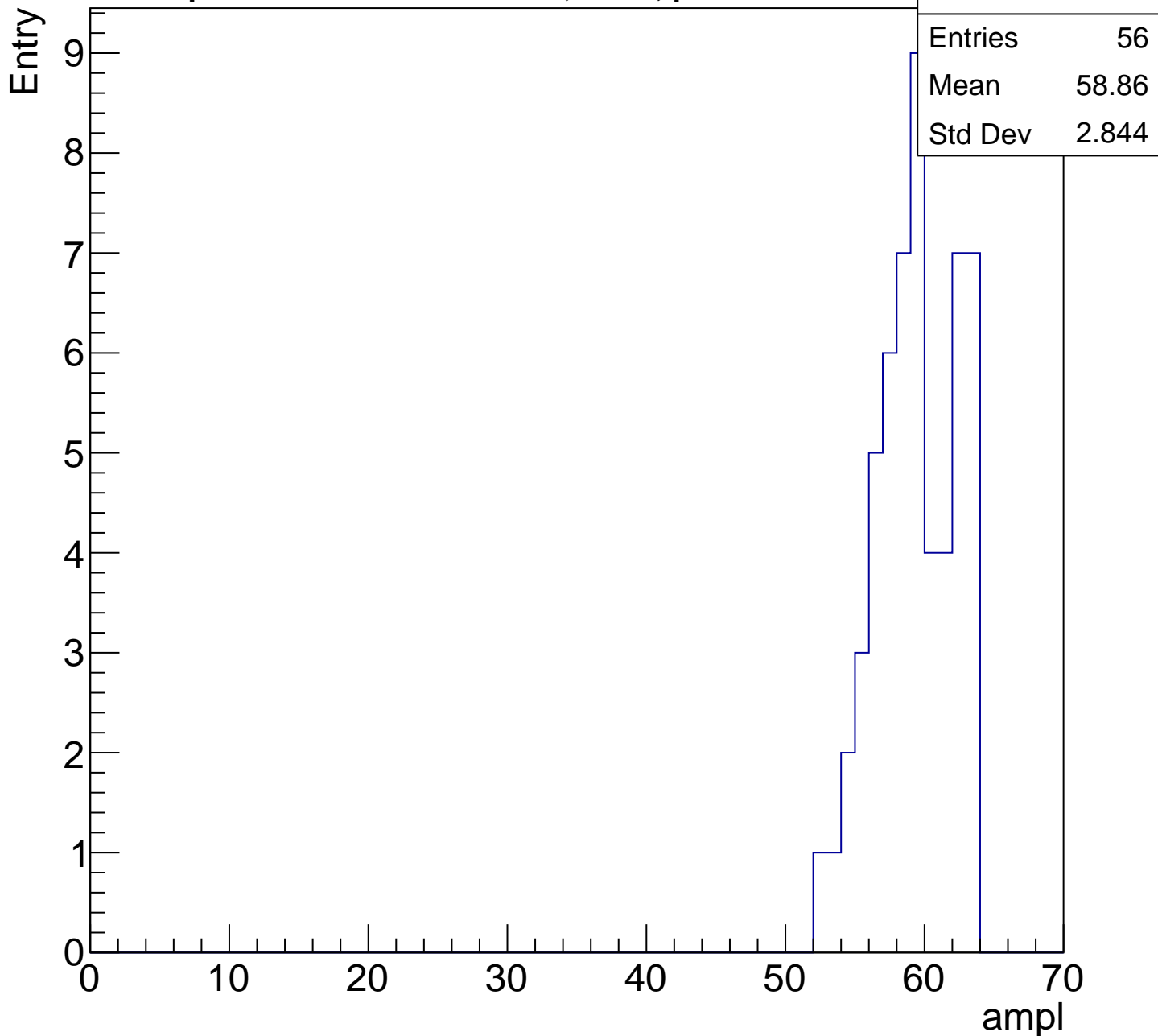
Entry

Entries	53
Mean	53.34
Std Dev	3.203



B0L000S, U4-ch92, adc5

calib_packv5_042523_0143.root, FC#5, port B1

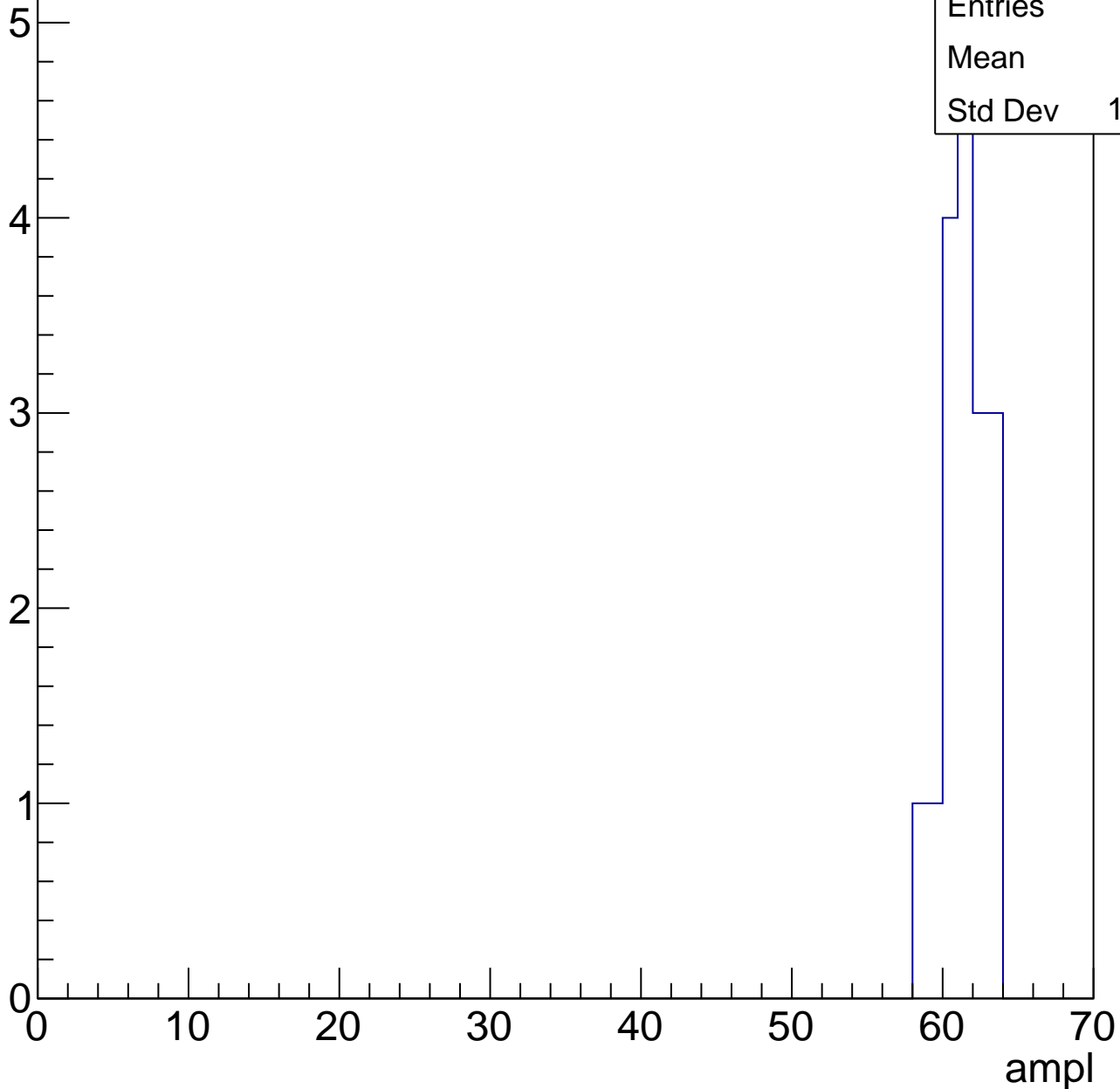


B0L000S, U4-ch92, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61
Std Dev	1.372



B0L000S, U4-ch92, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U4-ch93, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	75
Mean	30.37
Std Dev	3.595

Gaus mean : 30.7227

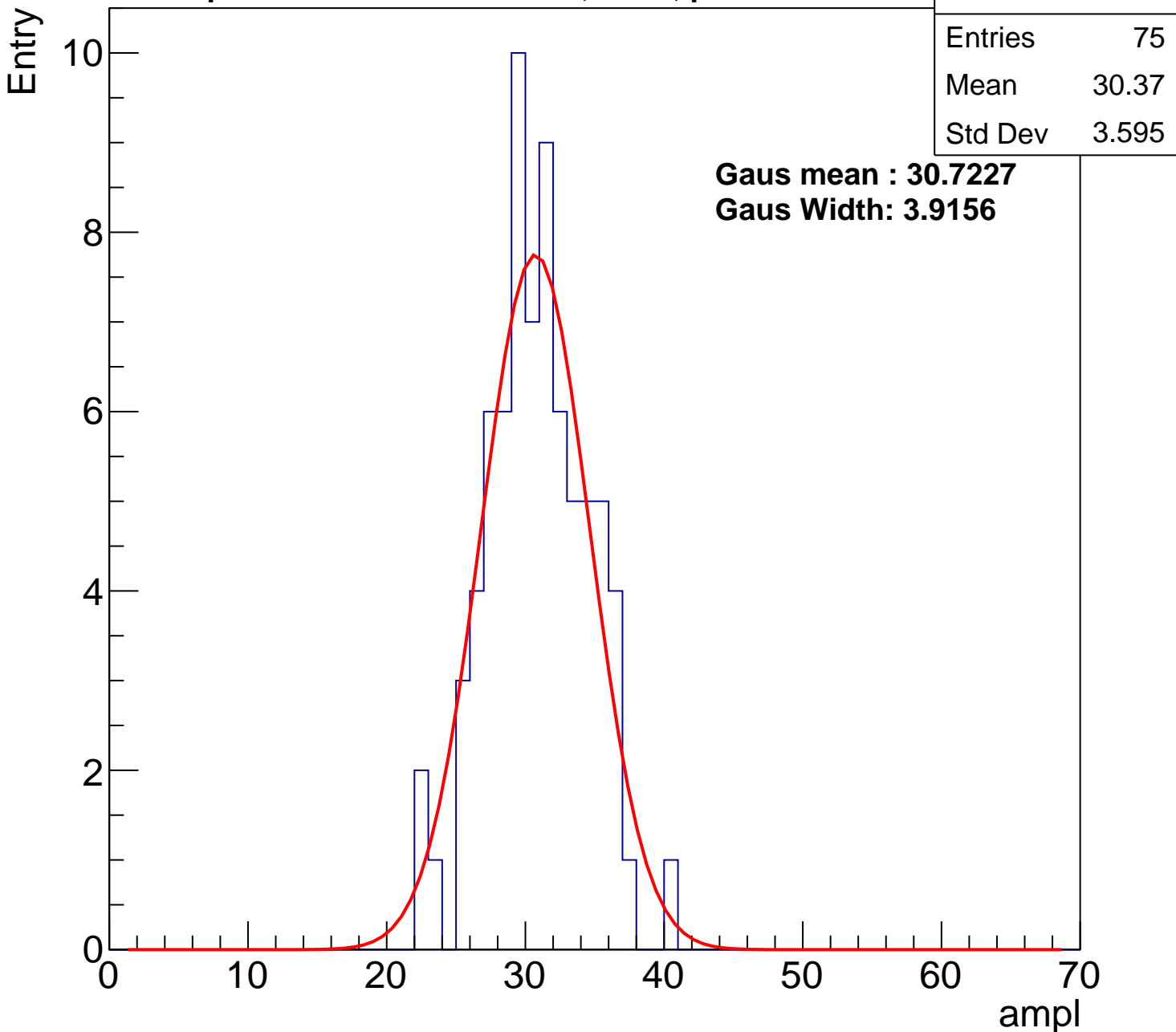
Gaus Width: 3.9156

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch93, adc1

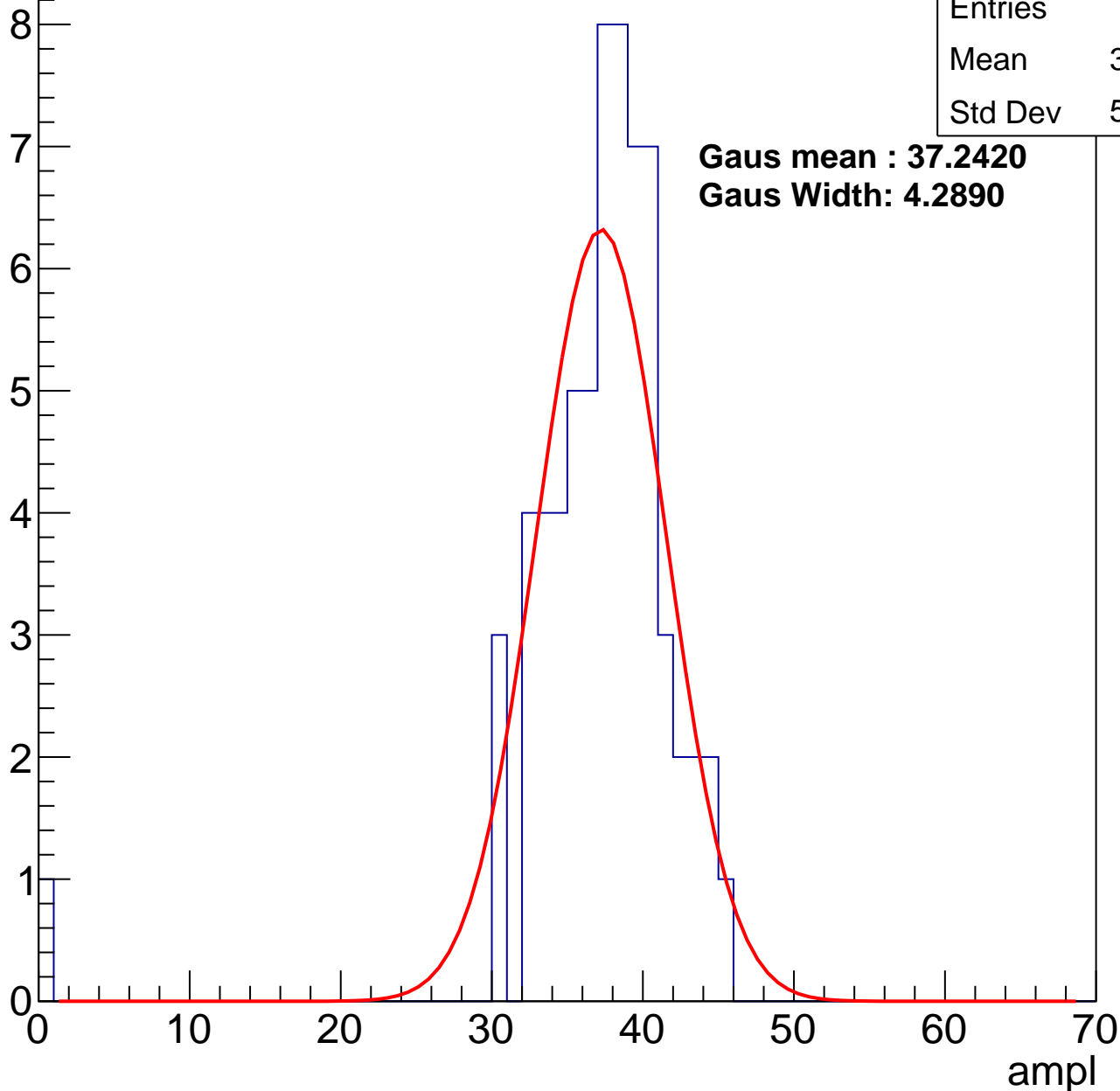
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.67
Std Dev	5.727

Gaus mean : 37.2420

Gaus Width: 4.2890



B0L000S, U4-ch93, adc2

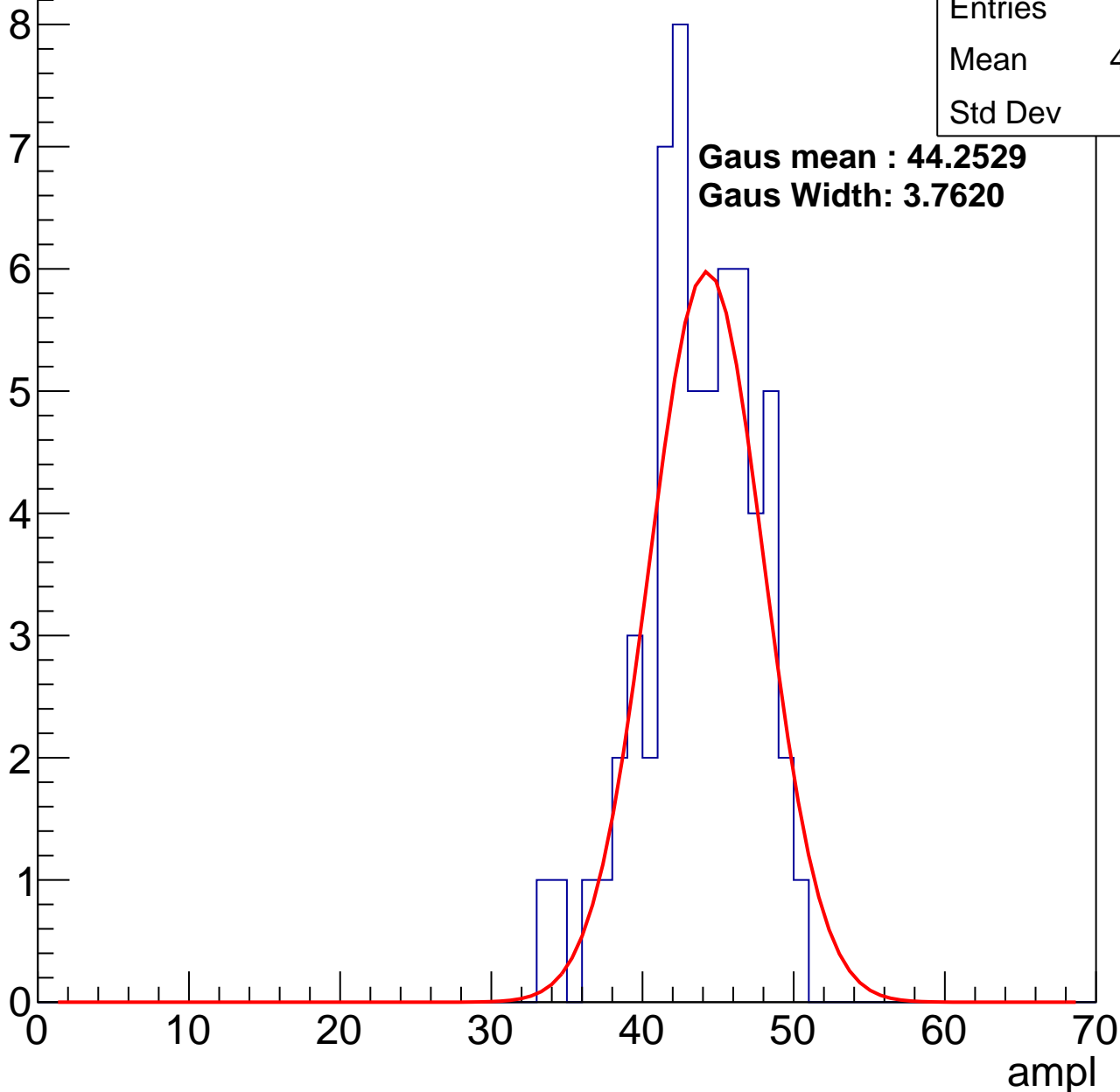
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.22
Std Dev	3.67

Gaus mean : 44.2529

Gaus Width: 3.7620

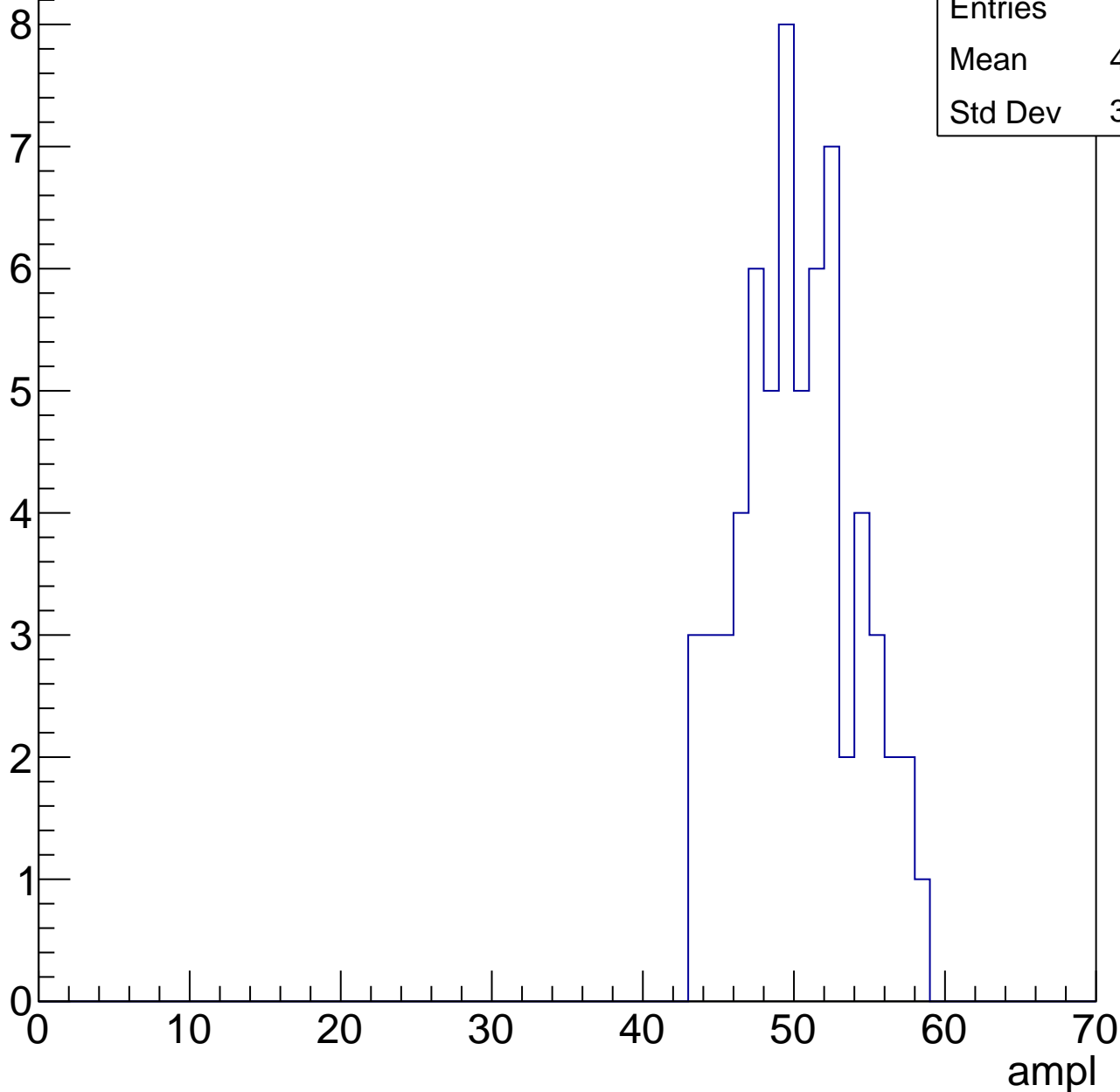


B0L000S, U4-ch93, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.77
Std Dev	3.745



B0L000S, U4-ch93, adc4

calib_packv5_042523_0143.root, FC#5, port B1

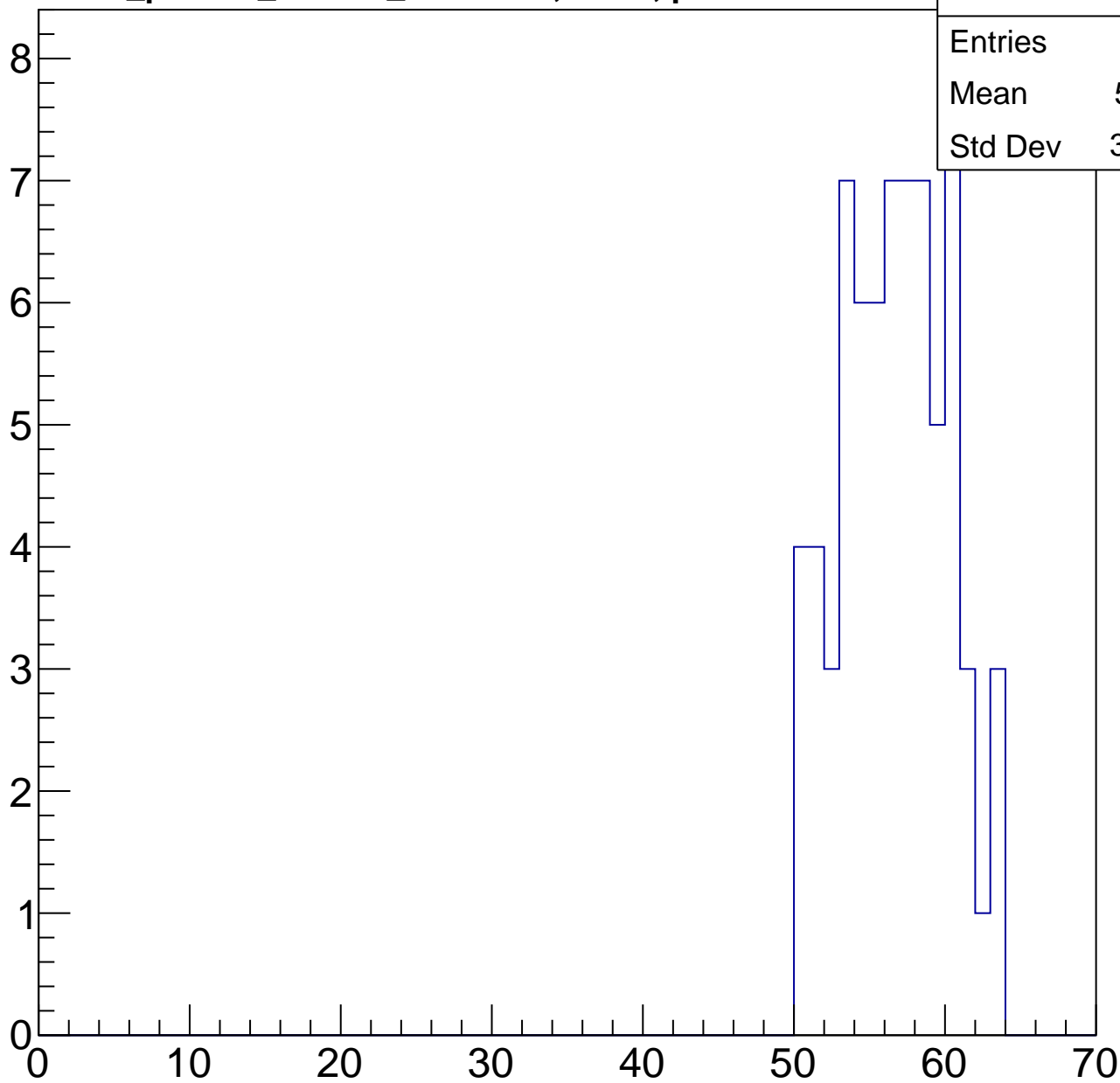
Entry

8
7
6
5
4
3
2
1
0

Entries	71
Mean	56.21
Std Dev	3.456

ampl

0 10 20 30 40 50 60 70

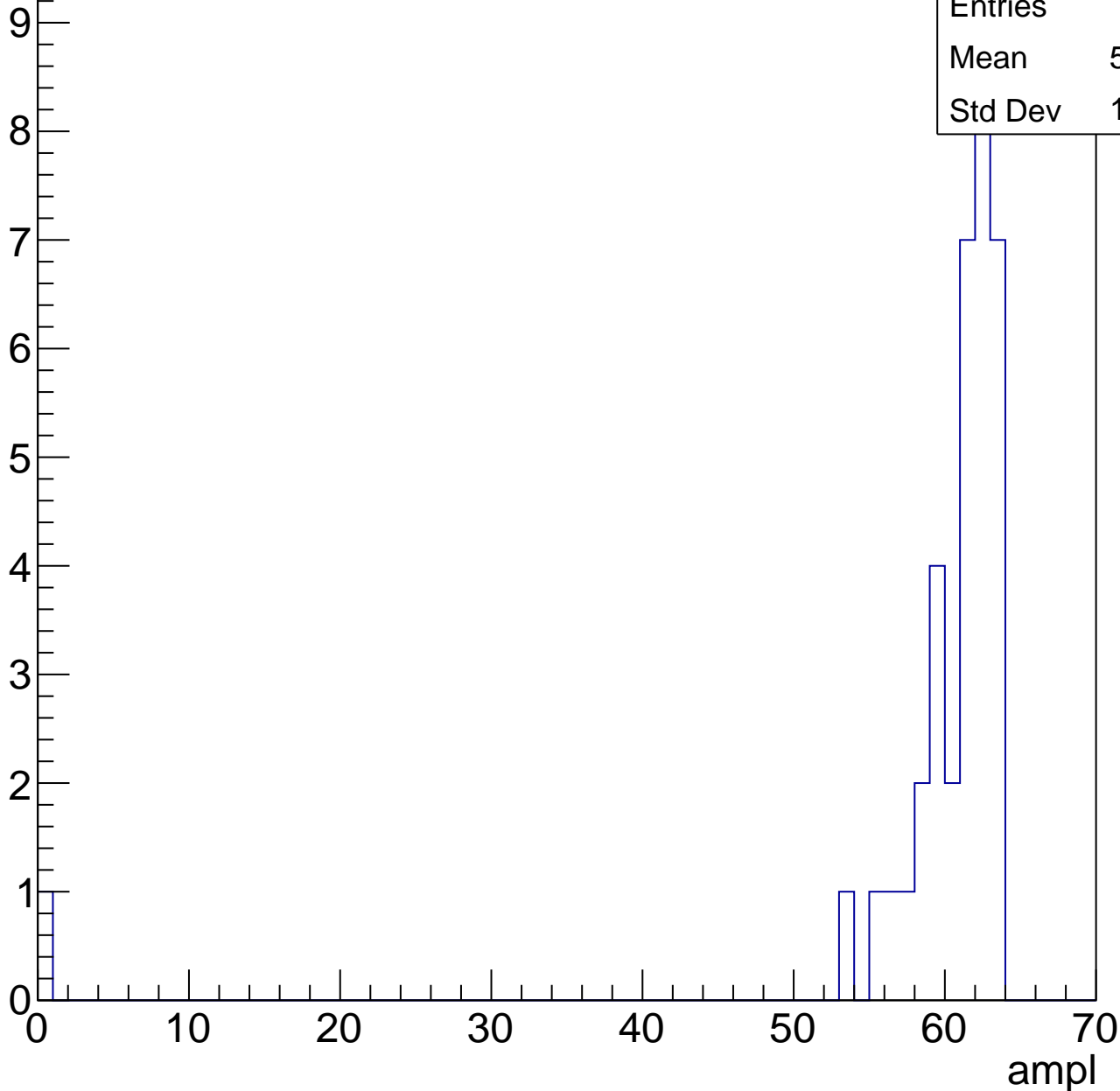


B0L000S, U4-ch93, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

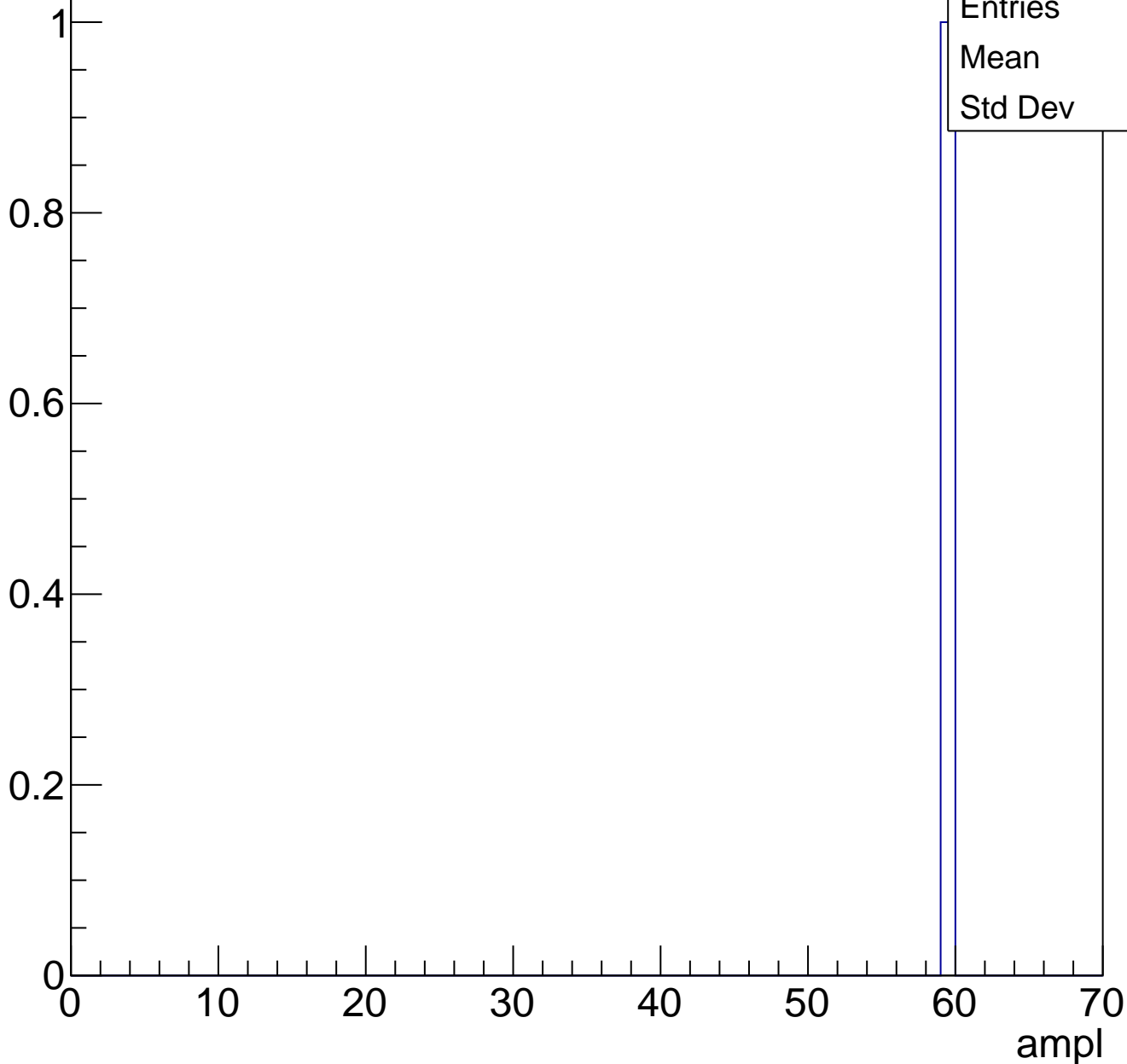
Entries	36
Mean	58.86
Std Dev	10.23



B0L000S, U4-ch93, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch93, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U4-ch94, adc0

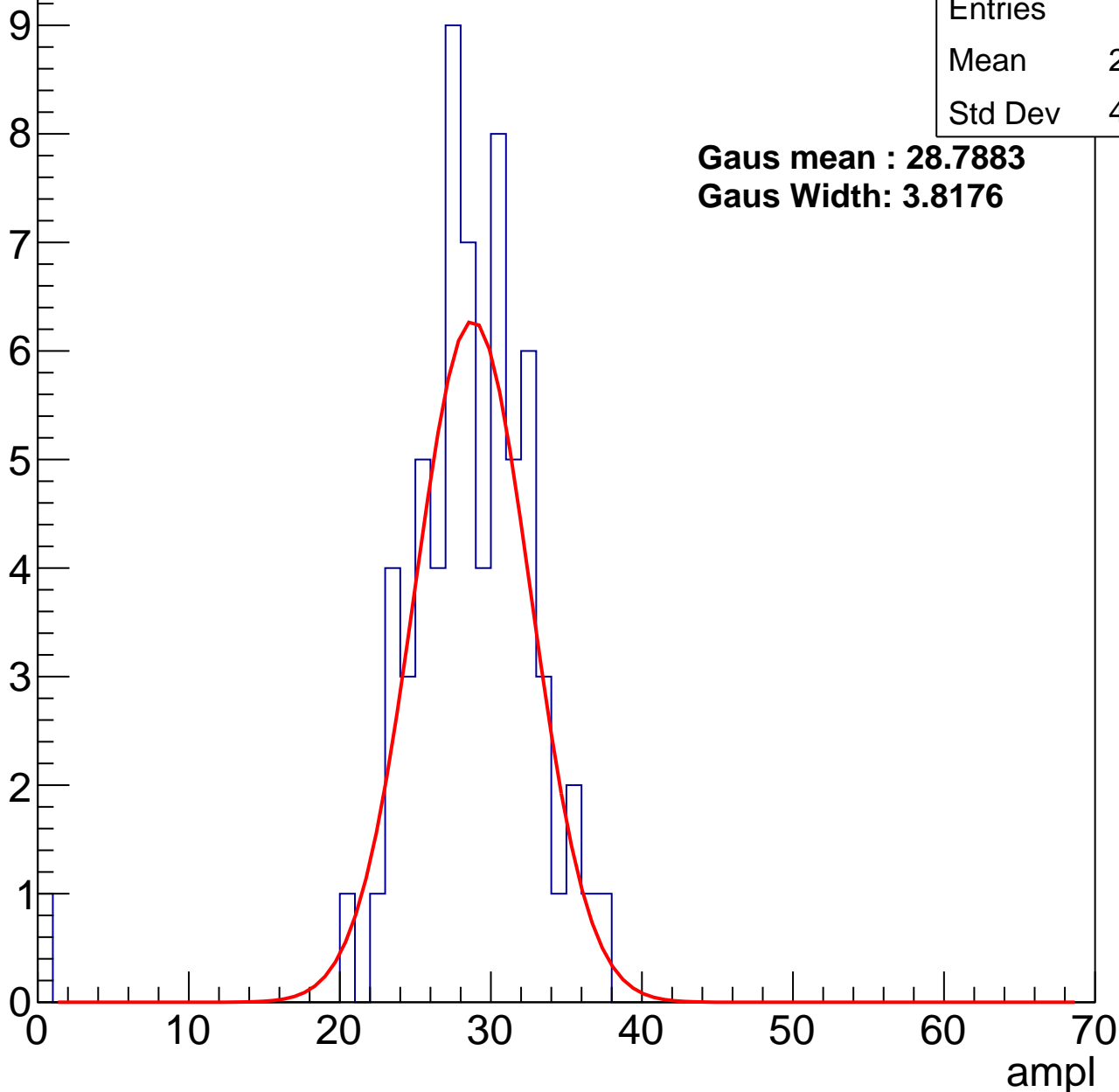
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.08
Std Dev	4.974

Gaus mean : 28.7883

Gaus Width: 3.8176



B0L000S, U4-ch94, adc1

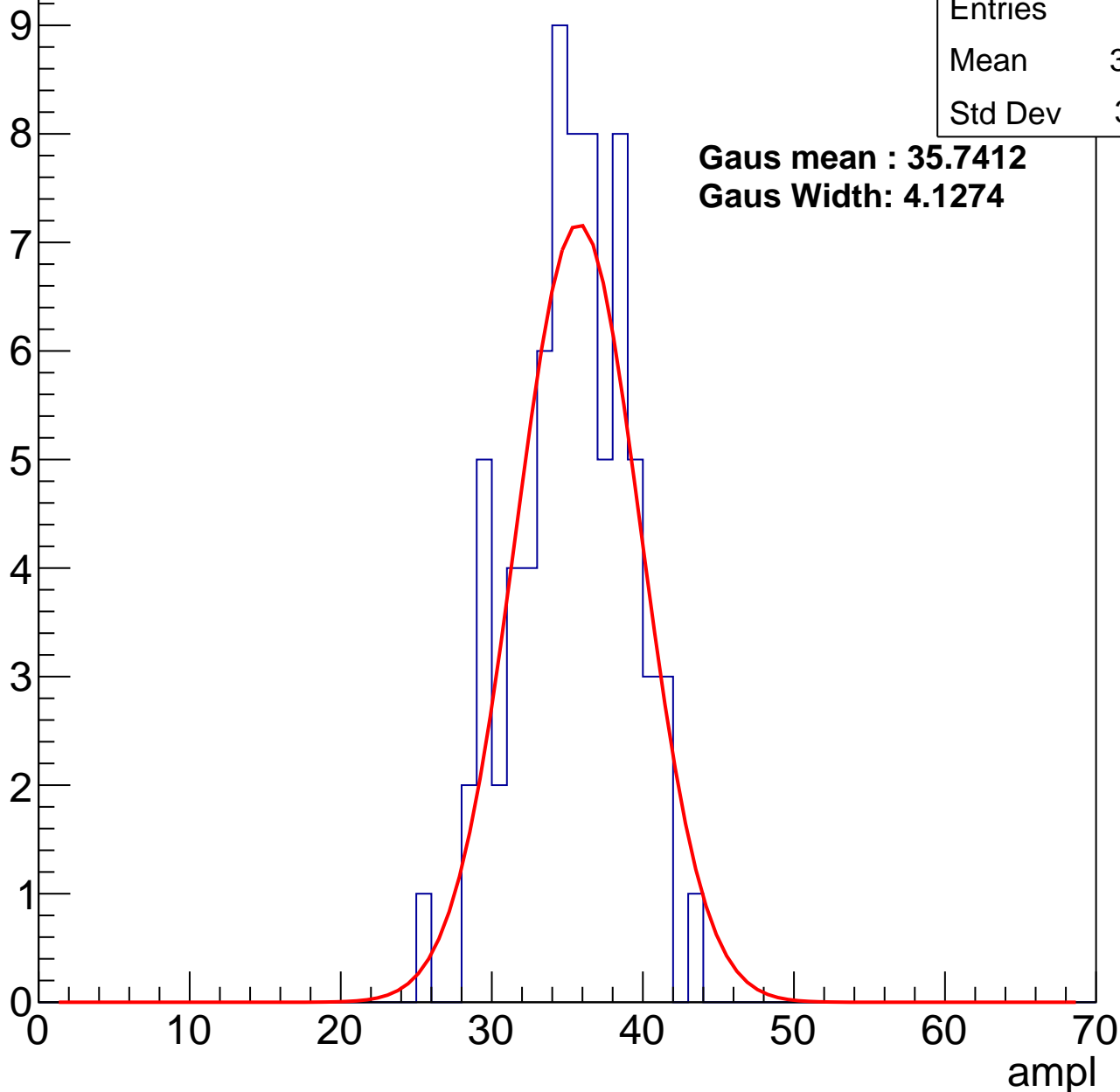
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	34.86
Std Dev	3.651

Gaus mean : 35.7412

Gaus Width: 4.1274



B0L000S, U4-ch94, adc2

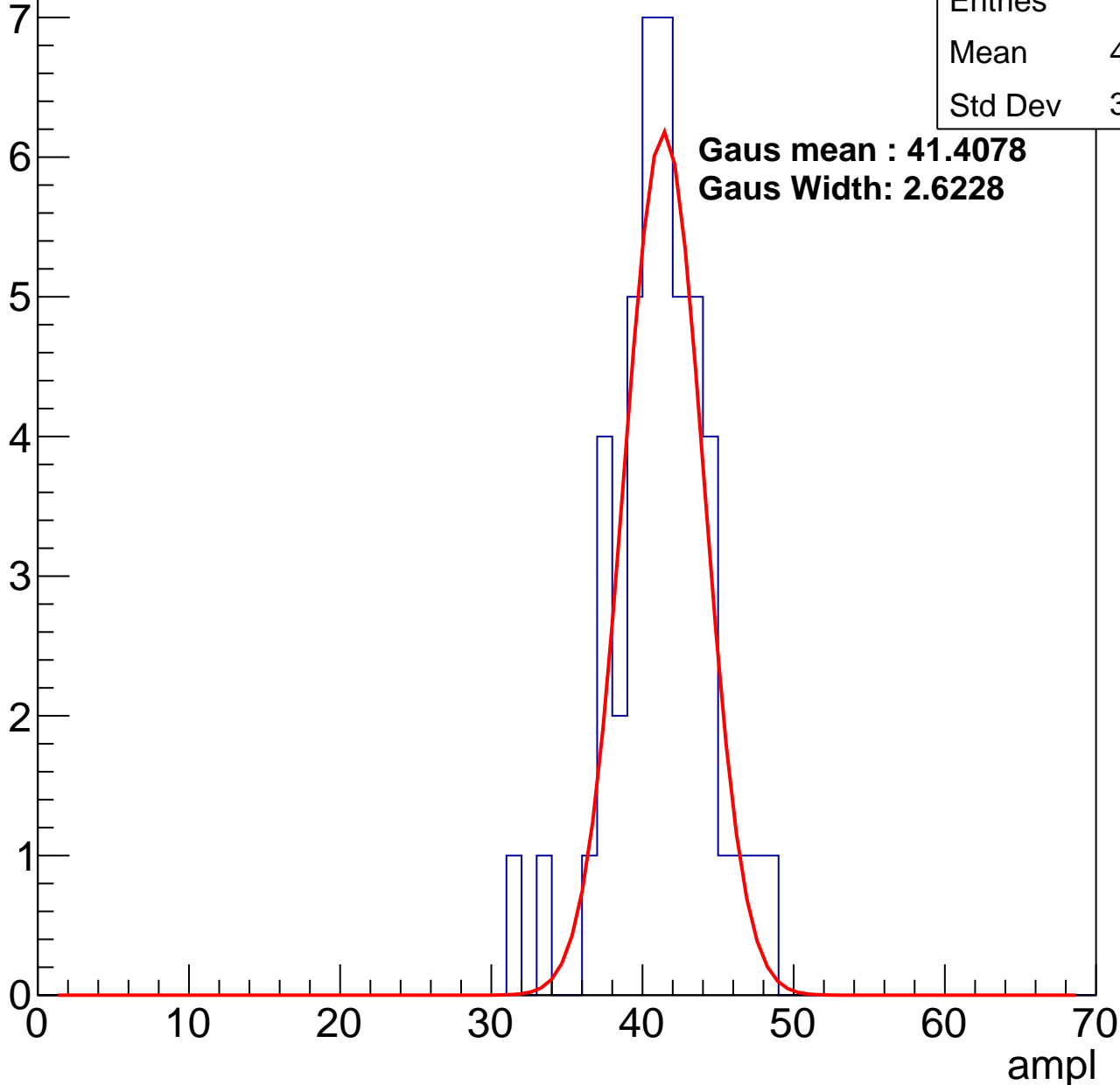
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	40.72
Std Dev	3.235

Gaus mean : 41.4078

Gaus Width: 2.6228



B0L000S, U4-ch94, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	84
Mean	47.74
Std Dev	3.367

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

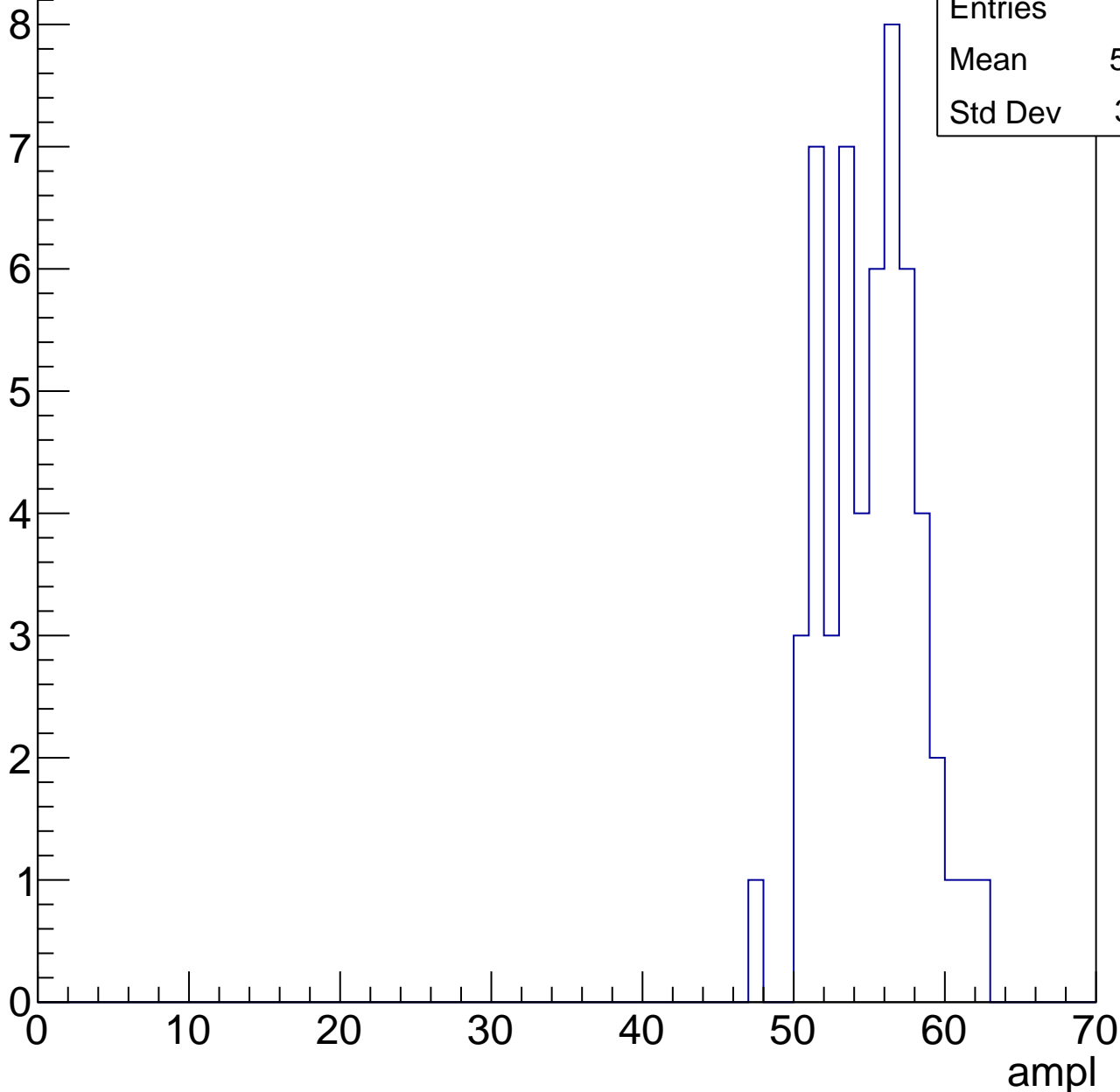


B0L000S, U4-ch94, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	54.63
Std Dev	3.081

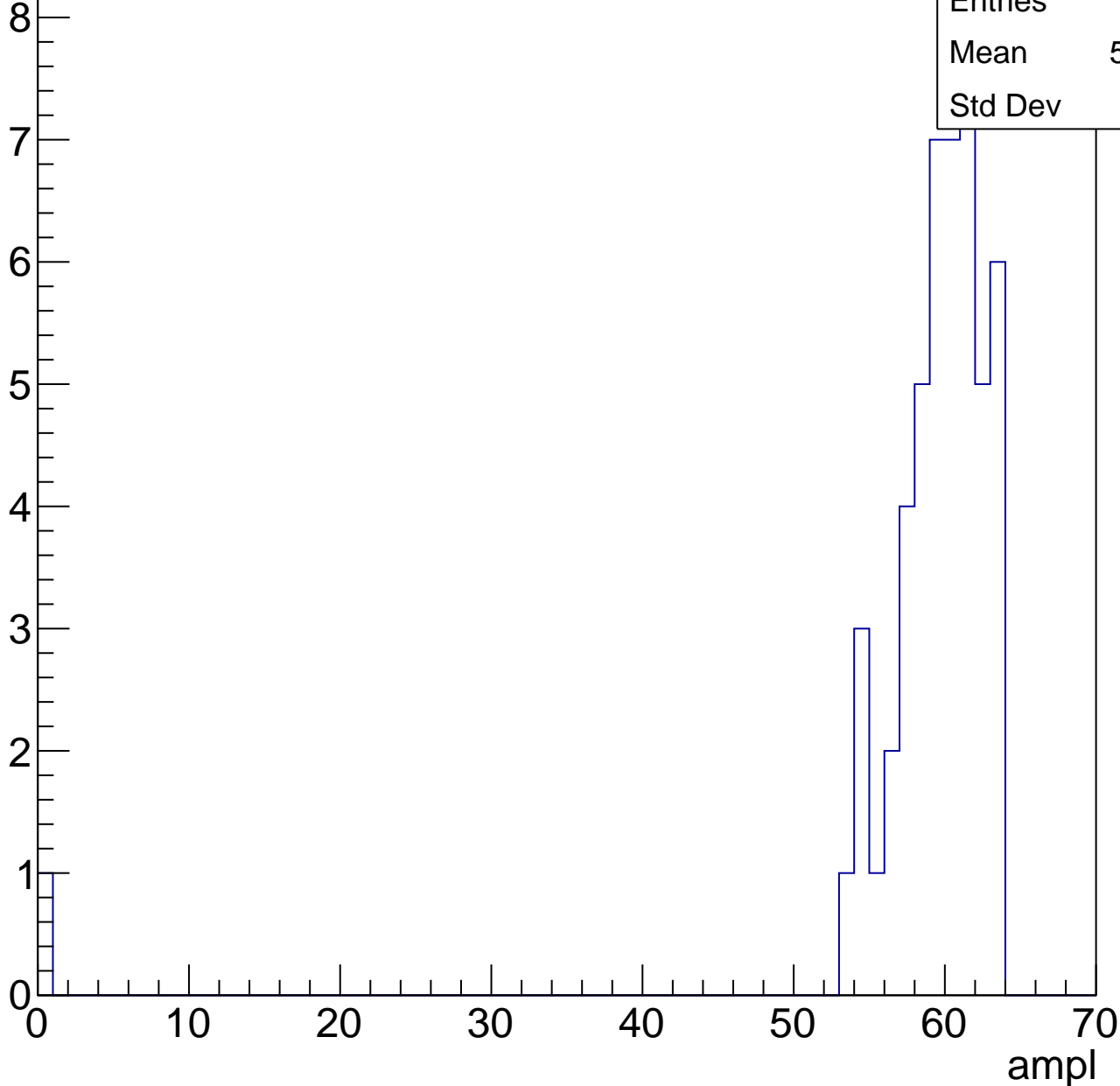


B0L000S, U4-ch94, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

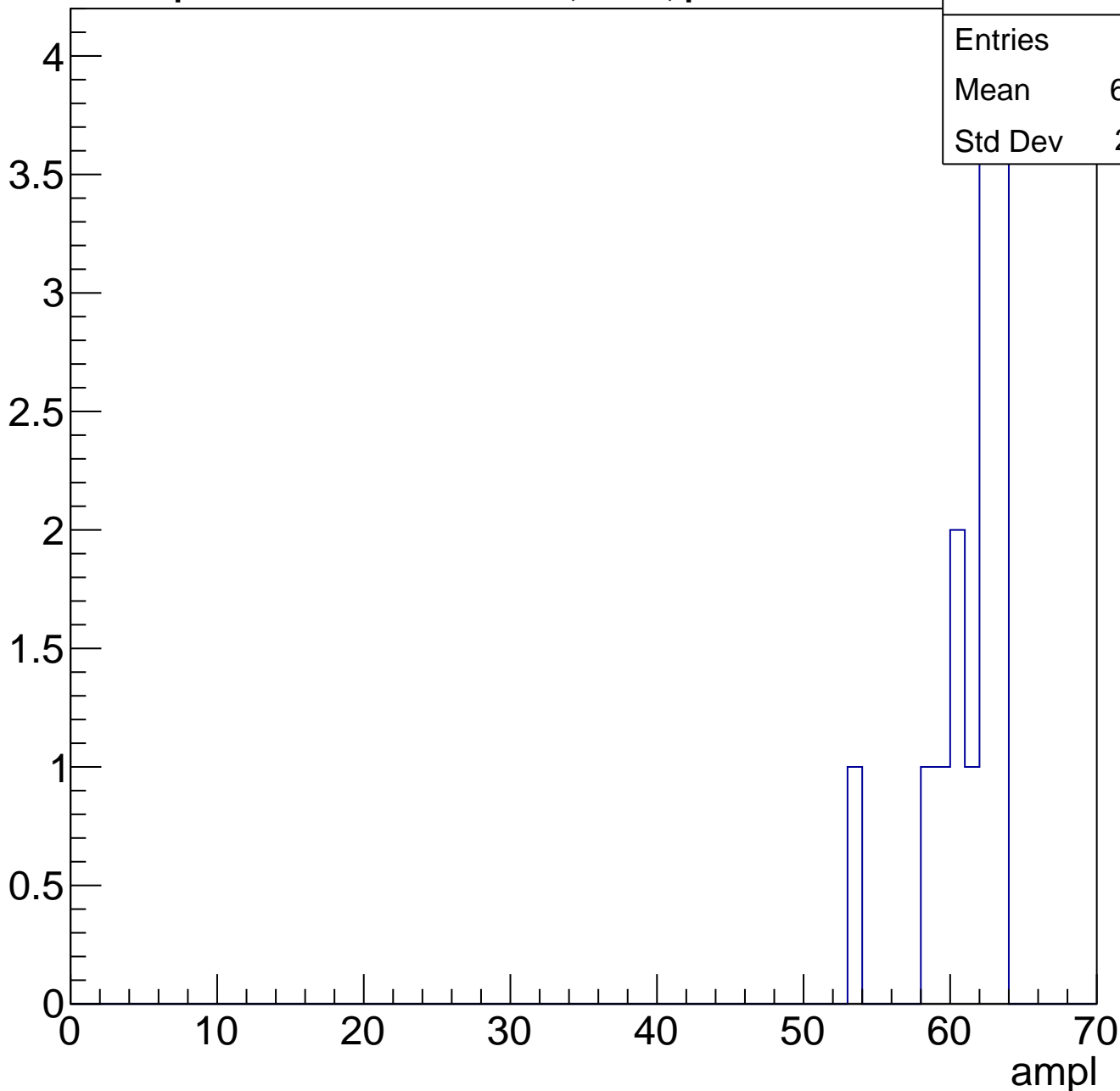
Entries	50
Mean	58.18
Std Dev	8.71



B0L000S, U4-ch94, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch94, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch95, adc0

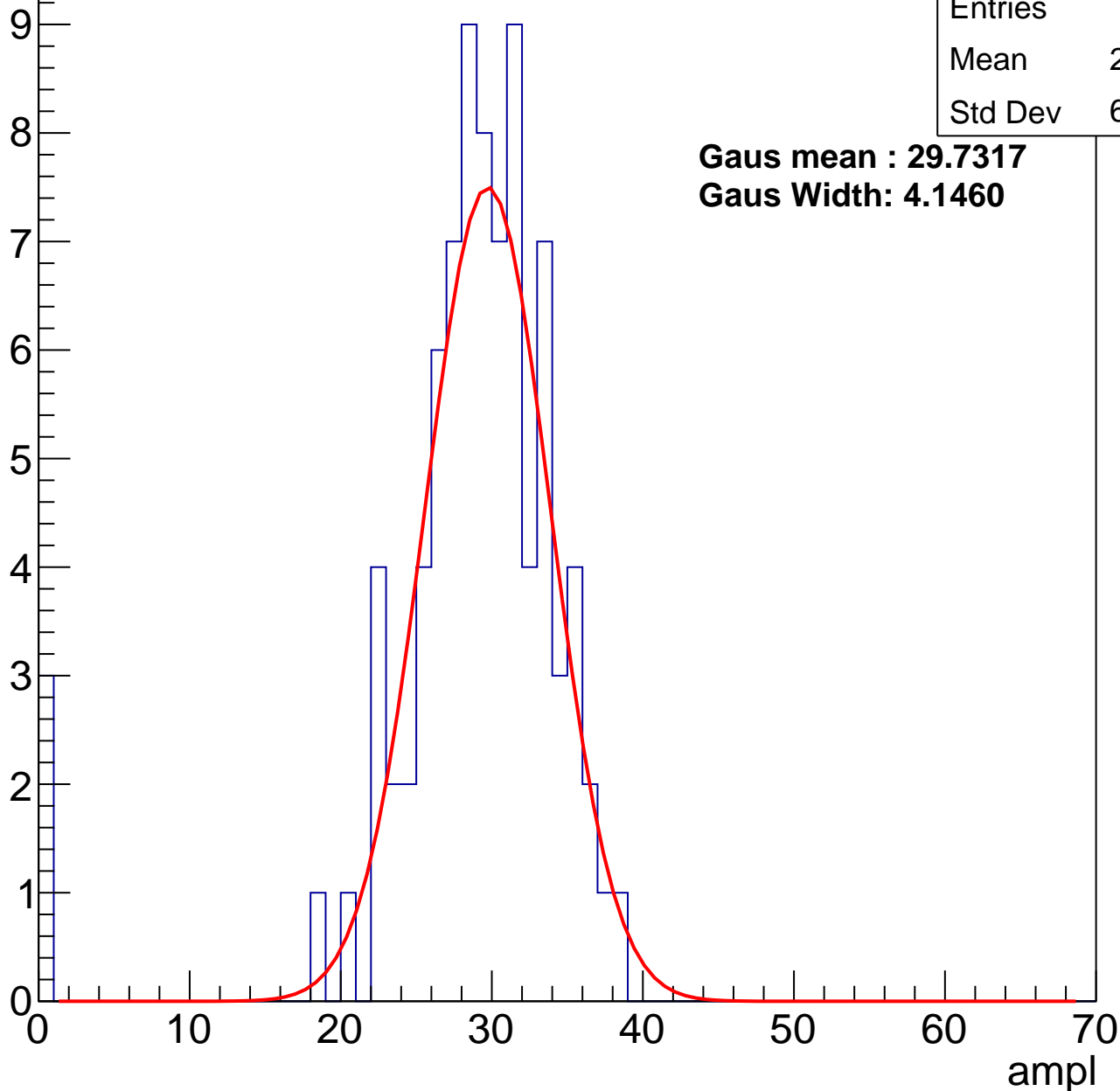
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	85
Mean	28.07
Std Dev	6.666

Gaus mean : 29.7317

Gaus Width: 4.1460



B0L000S, U4-ch95, adc1

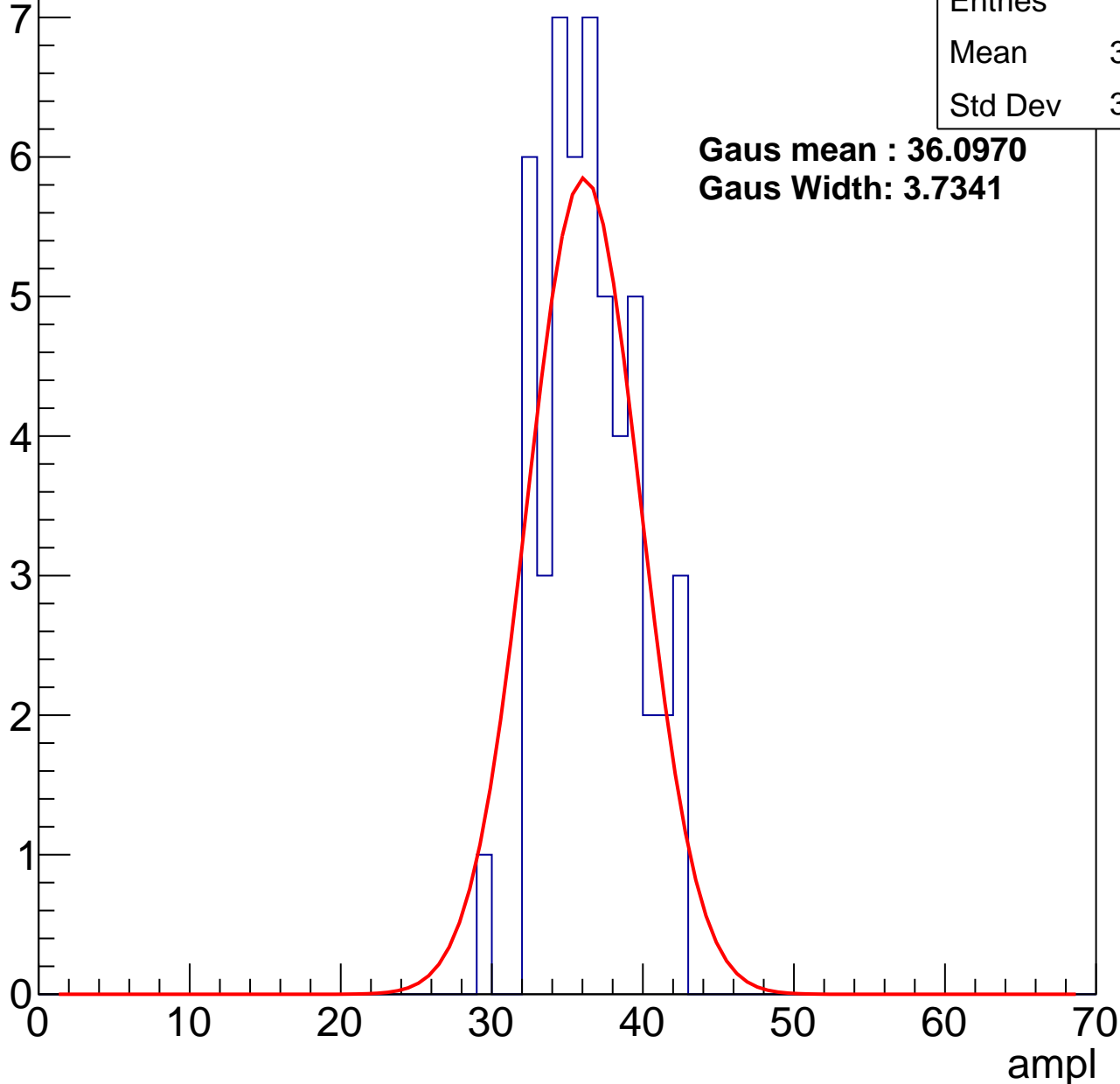
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	36.08
Std Dev	3.009

Gaus mean : 36.0970

Gaus Width: 3.7341



B0L000S, U4-ch95, adc2

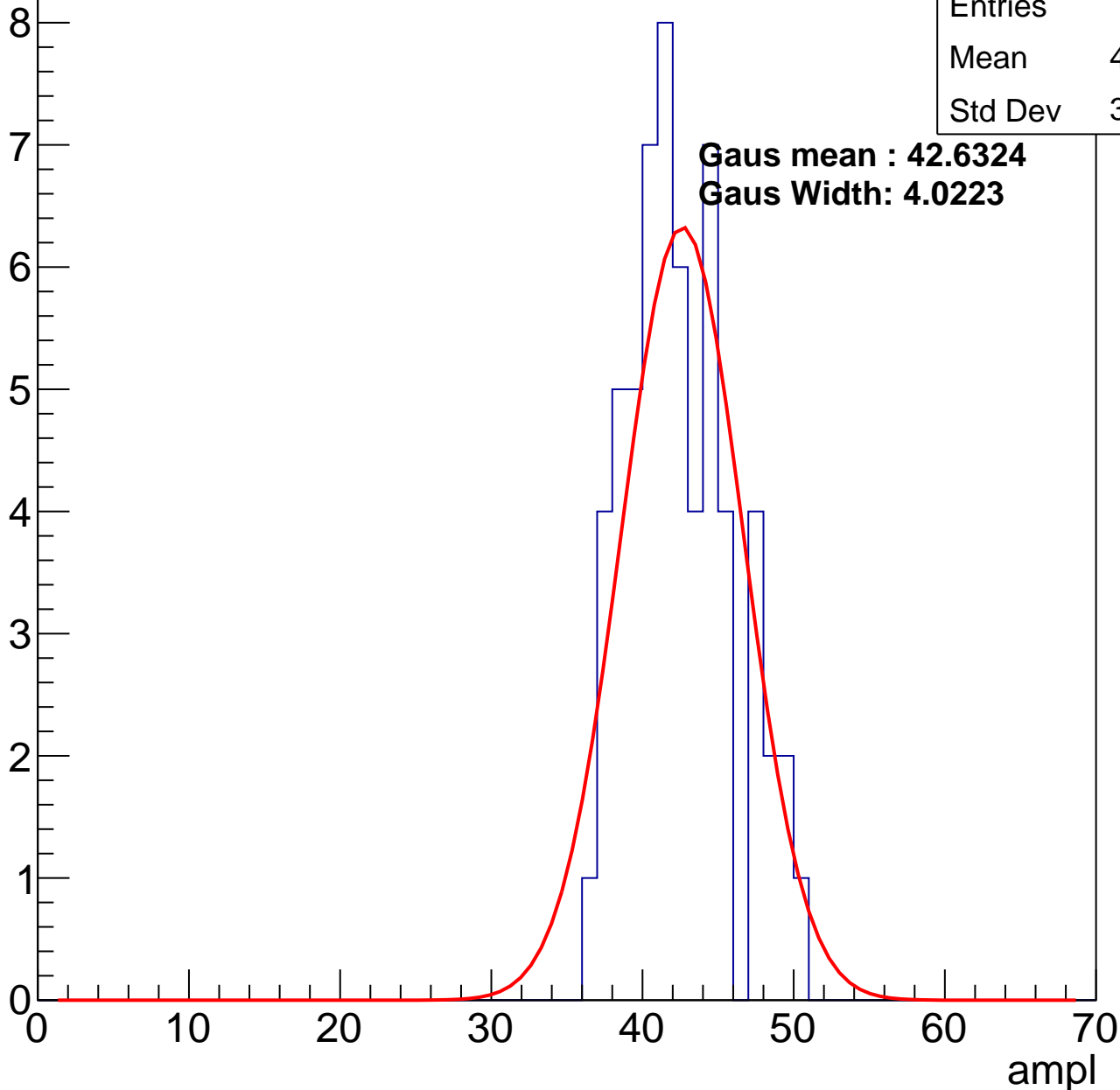
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.02
Std Dev	3.413

Gaus mean : 42.6324

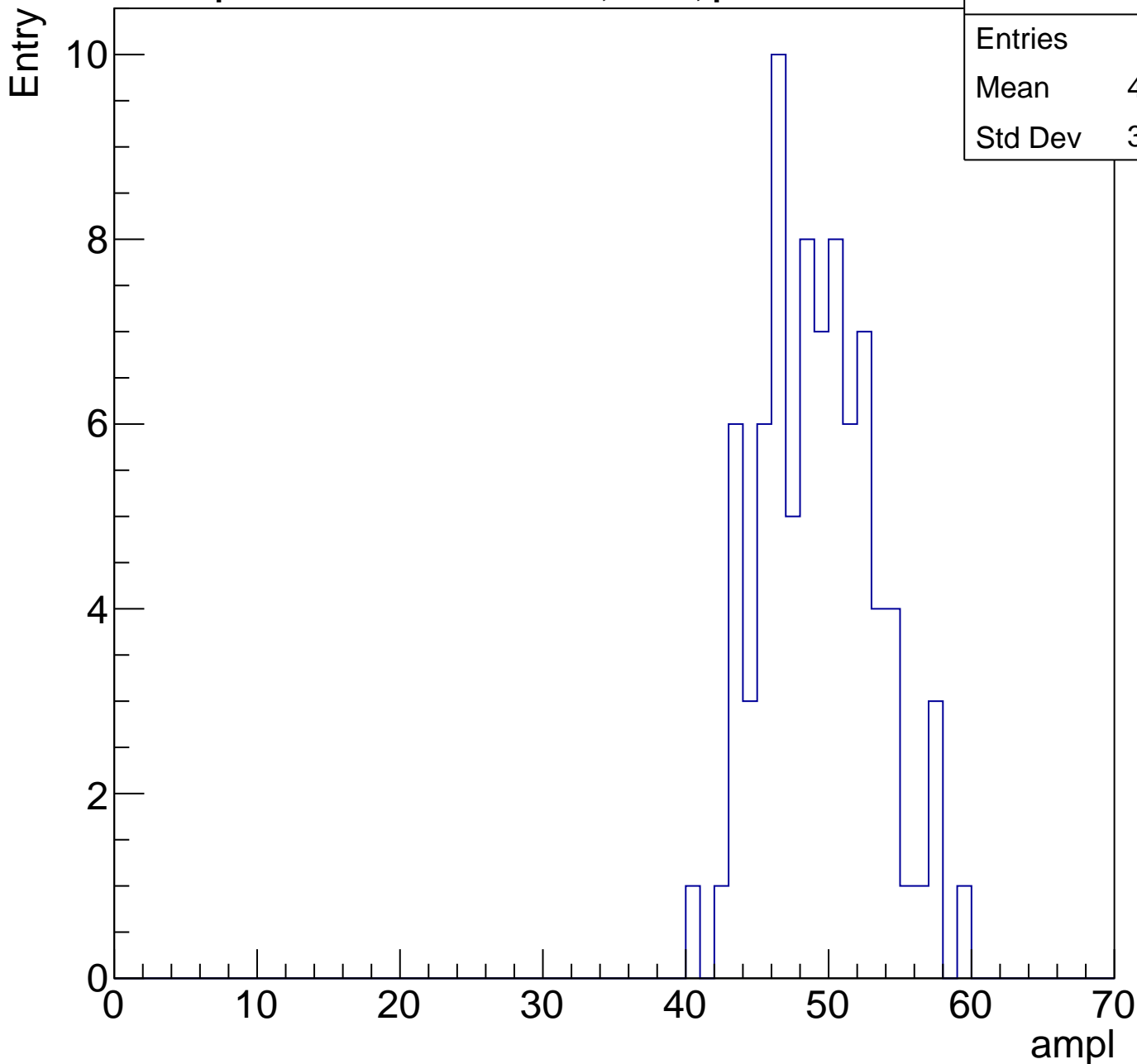
Gaus Width: 4.0223



B0L000S, U4-ch95, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	82
Mean	48.82
Std Dev	3.933

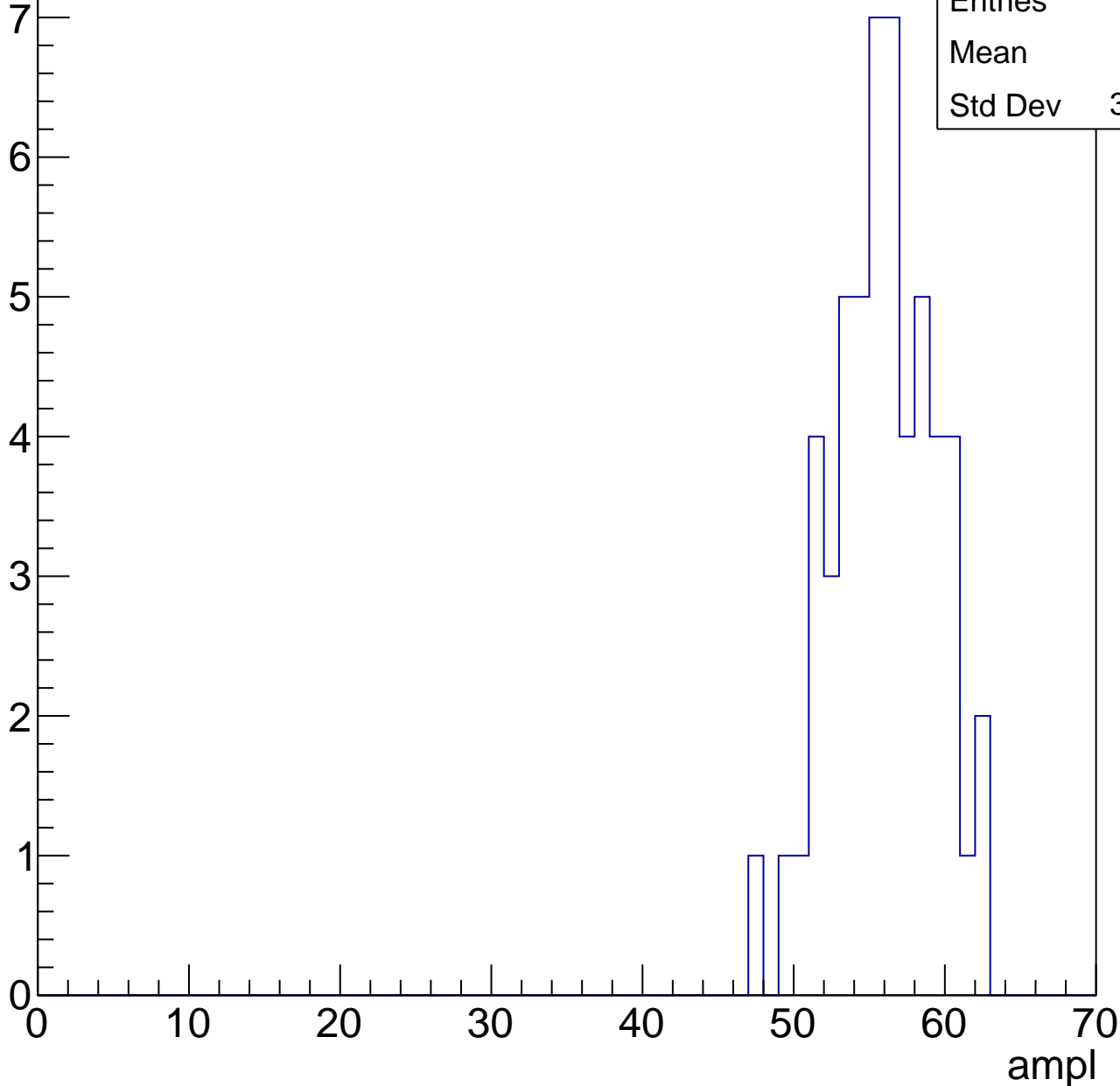


B0L000S, U4-ch95, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.5
Std Dev	3.315



B0L000S, U4-ch95, adc5

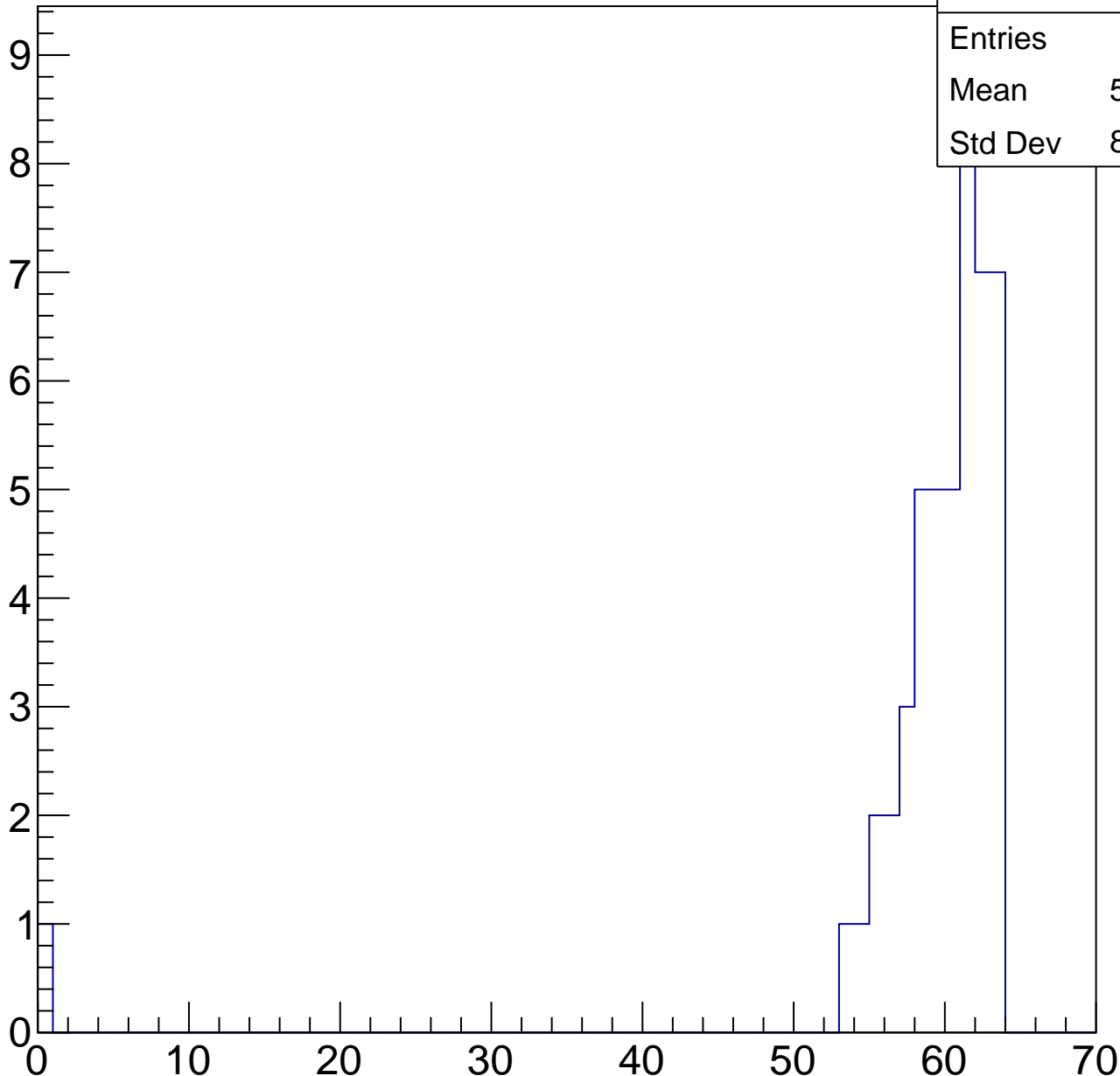
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.52
Std Dev	8.914

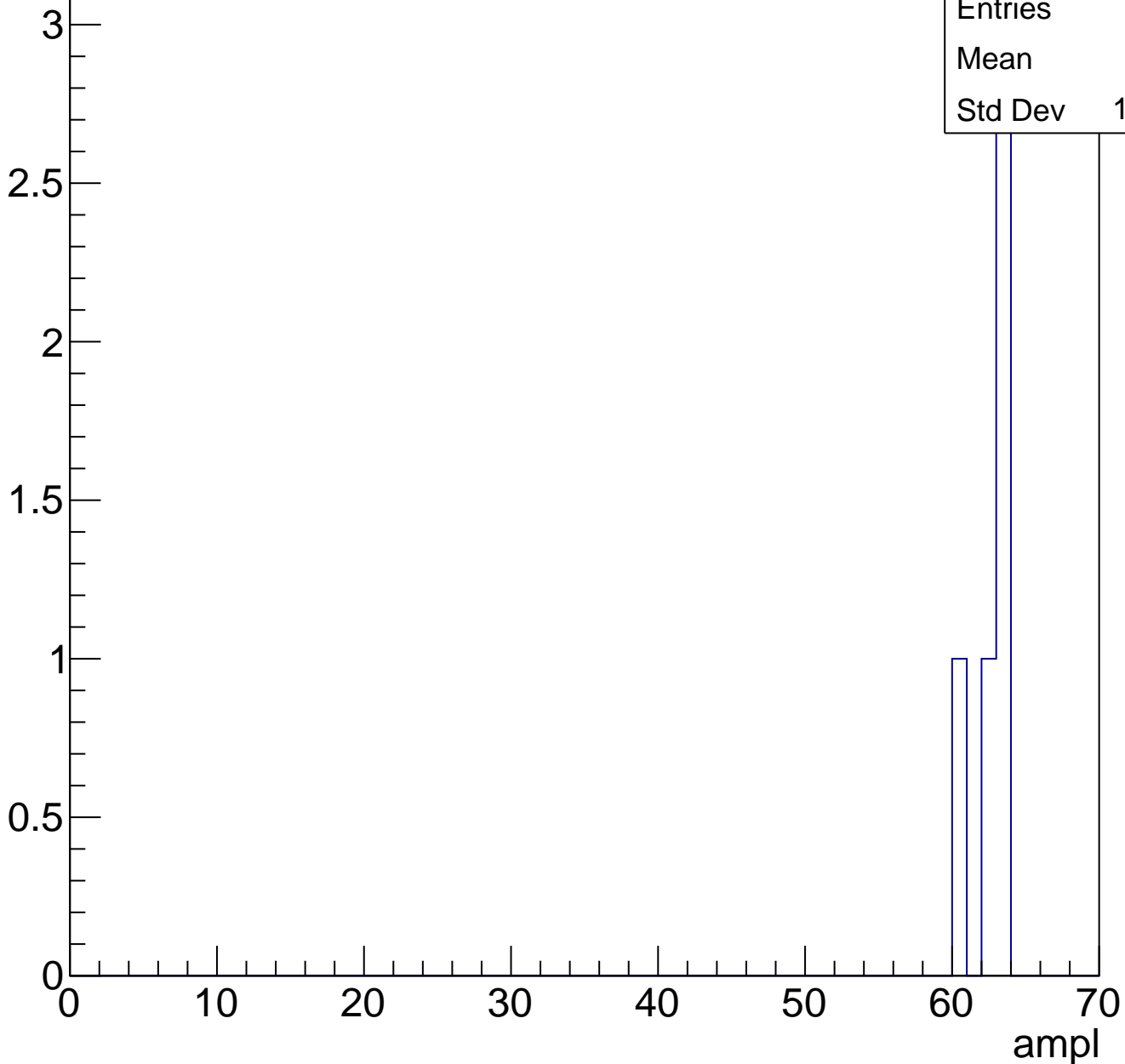
ampl



B0L000S, U4-ch95, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch95, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch96, adc0

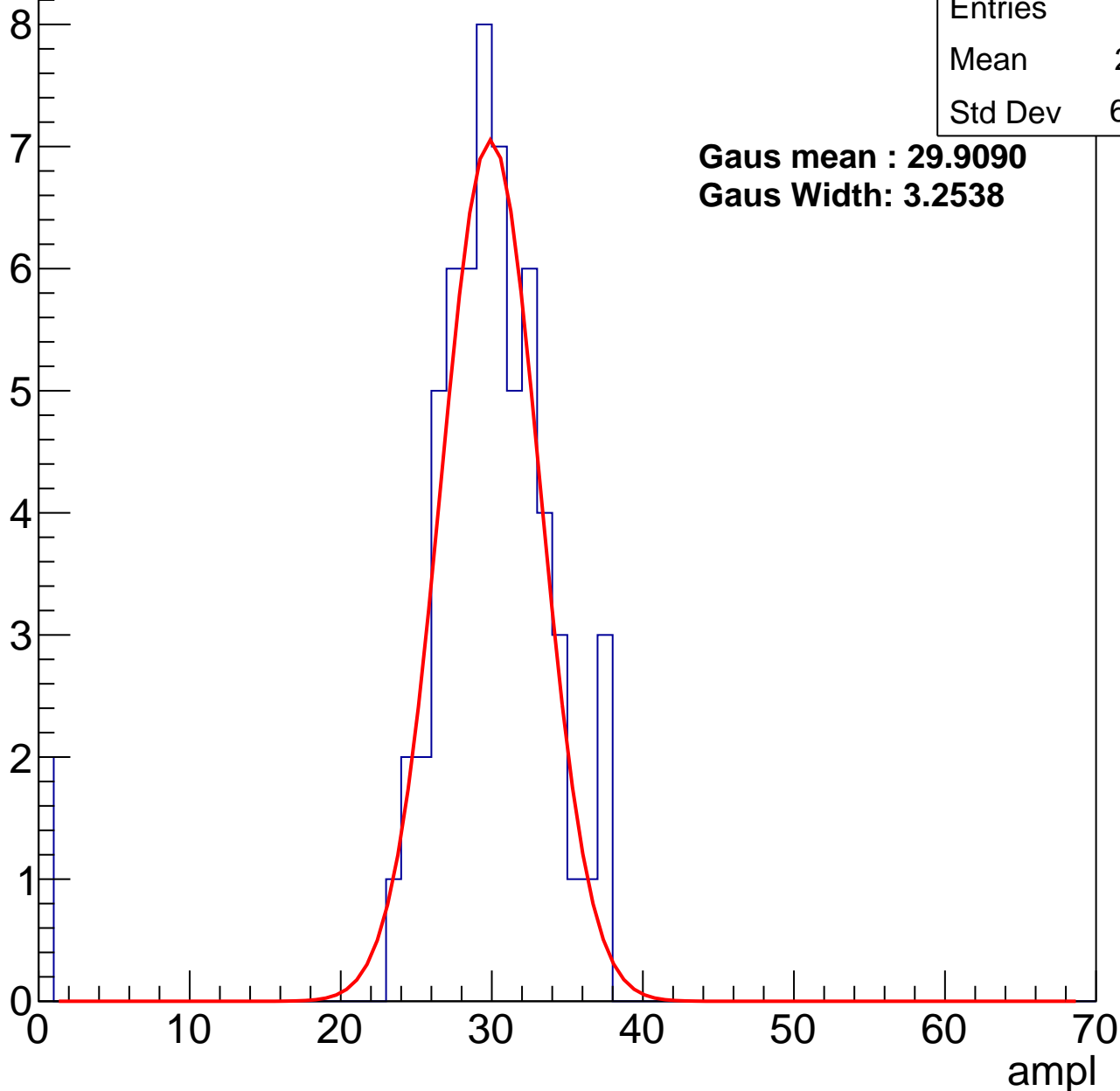
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	28.81
Std Dev	6.182

Gaus mean : 29.9090

Gaus Width: 3.2538



B0L000S, U4-ch96, adc1

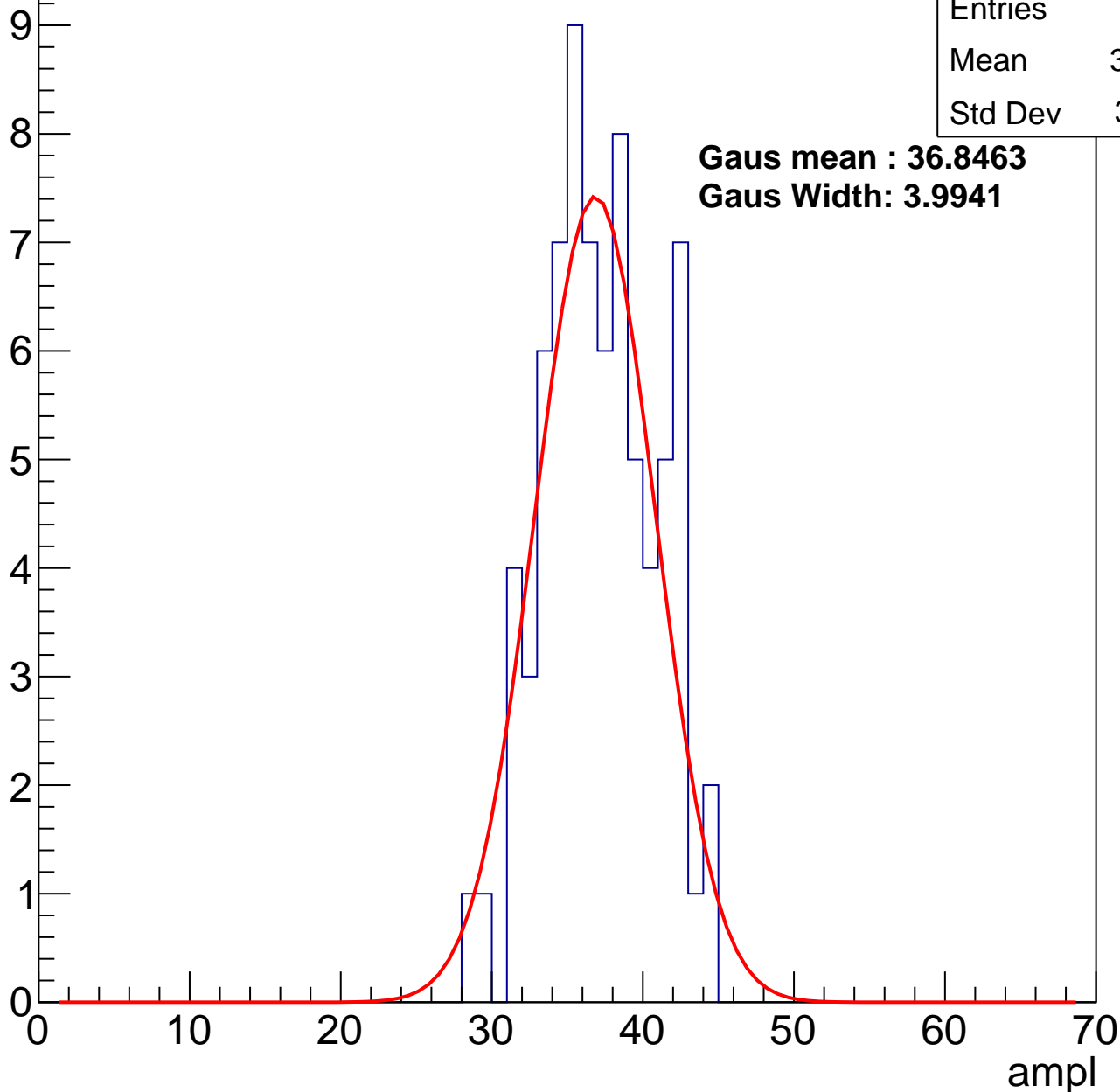
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	36.72
Std Dev	3.651

Gaus mean : 36.8463

Gaus Width: 3.9941



B0L000S, U4-ch96, adc2

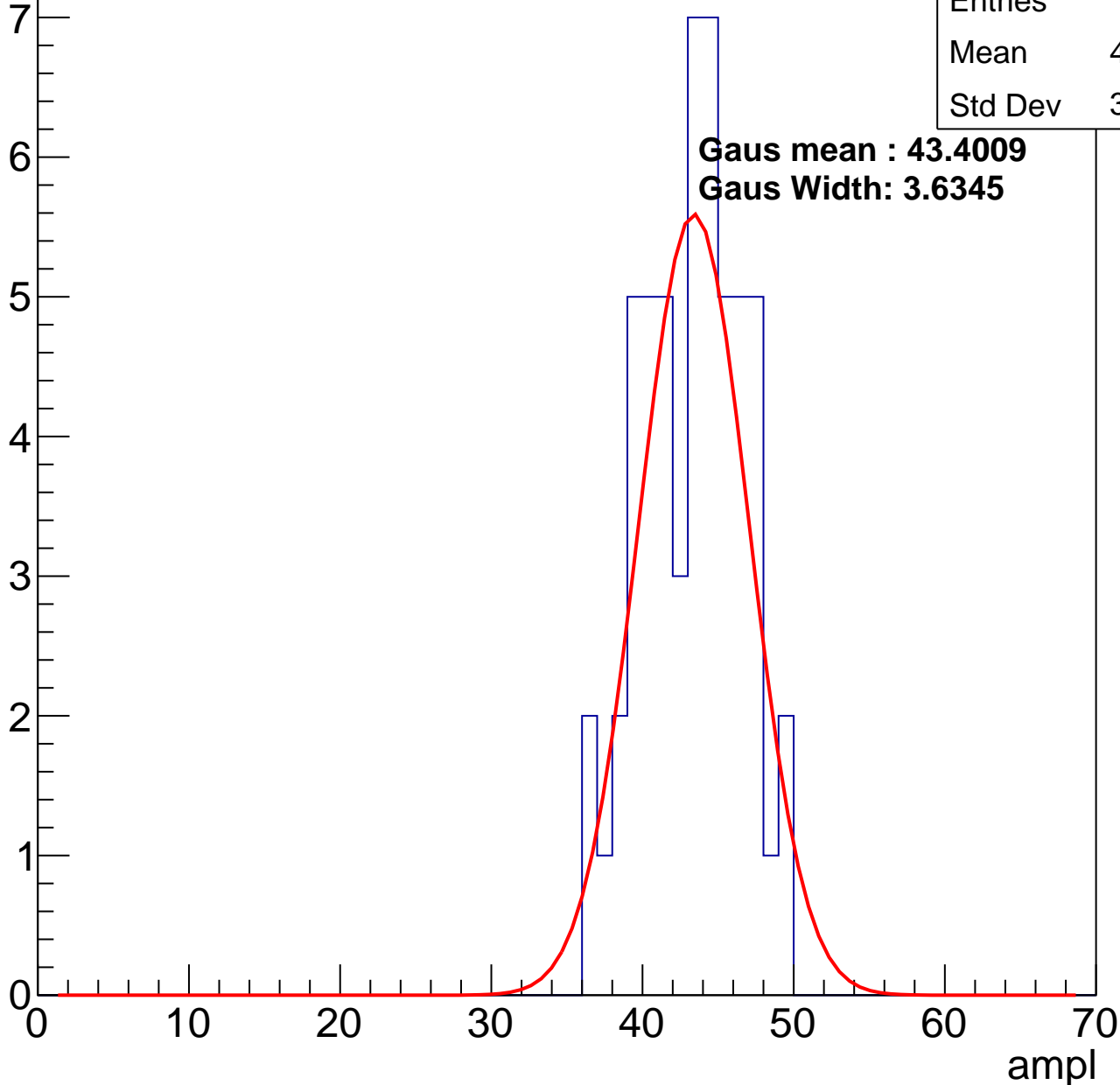
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	42.84
Std Dev	3.246

Gaus mean : 43.4009

Gaus Width: 3.6345

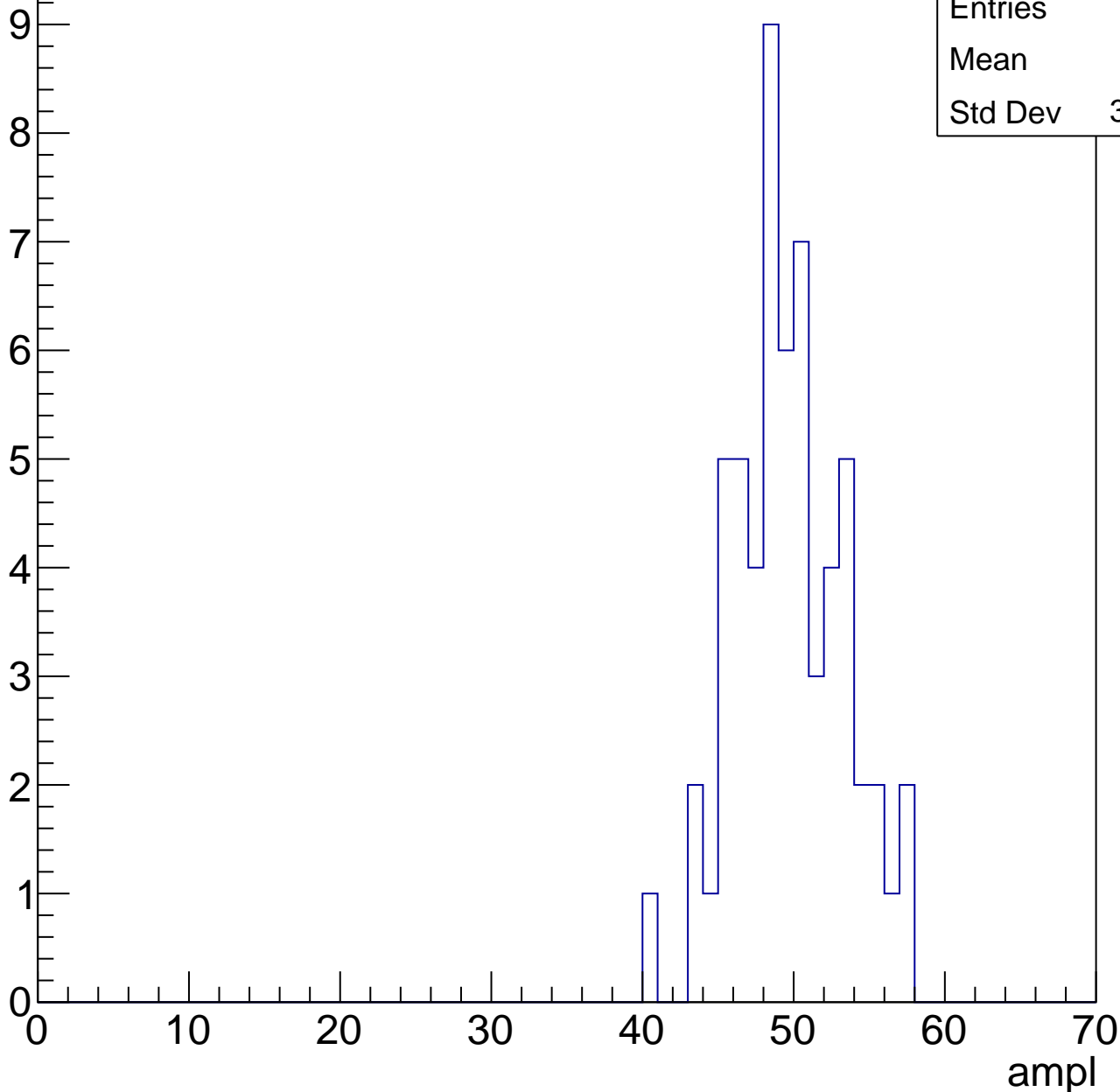


B0L000S, U4-ch96, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.2
Std Dev	3.588

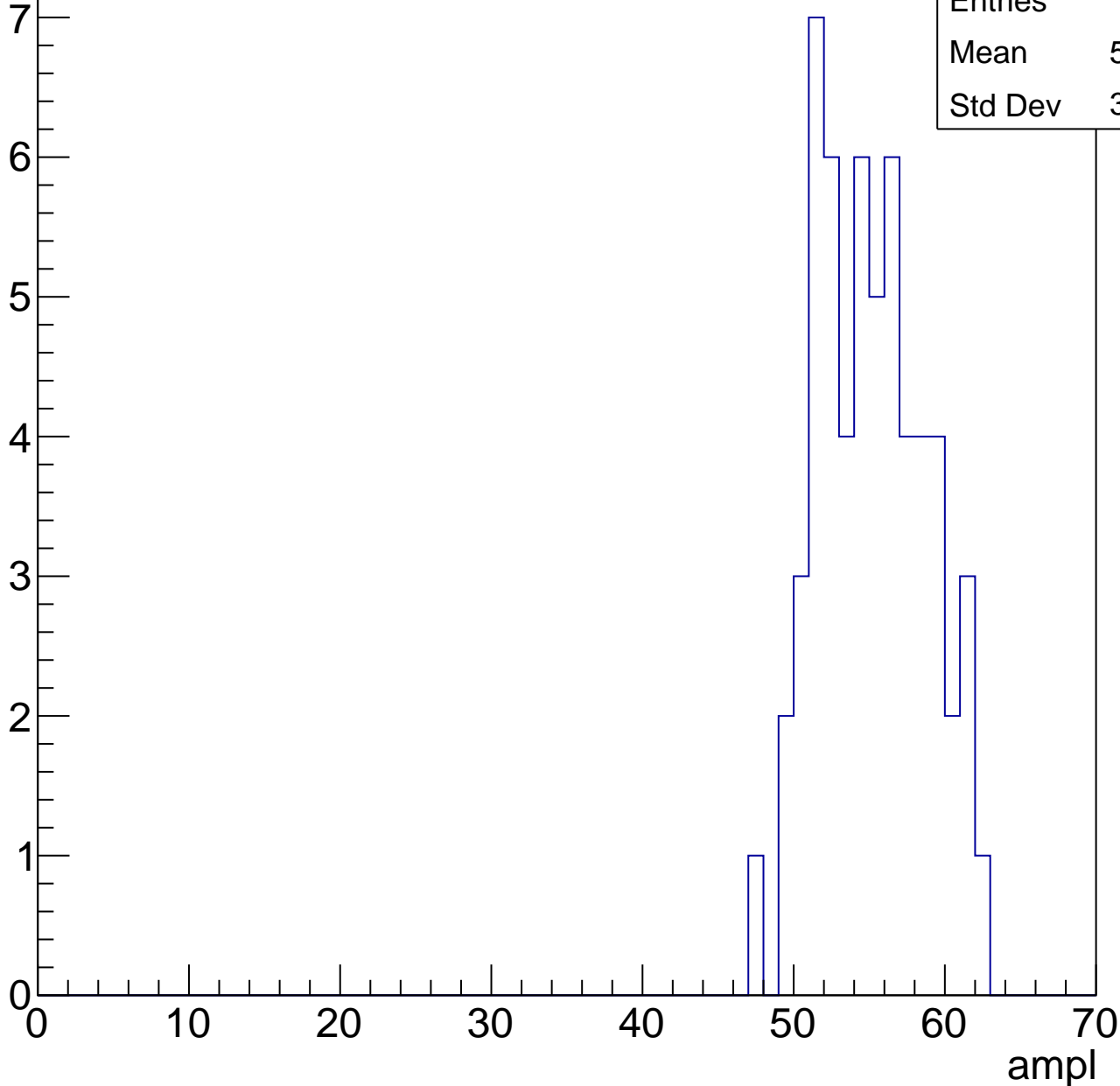


B0L000S, U4-ch96, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	54.69
Std Dev	3.529



B0L000S, U4-ch96, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries	45
Mean	58.96
Std Dev	2.616

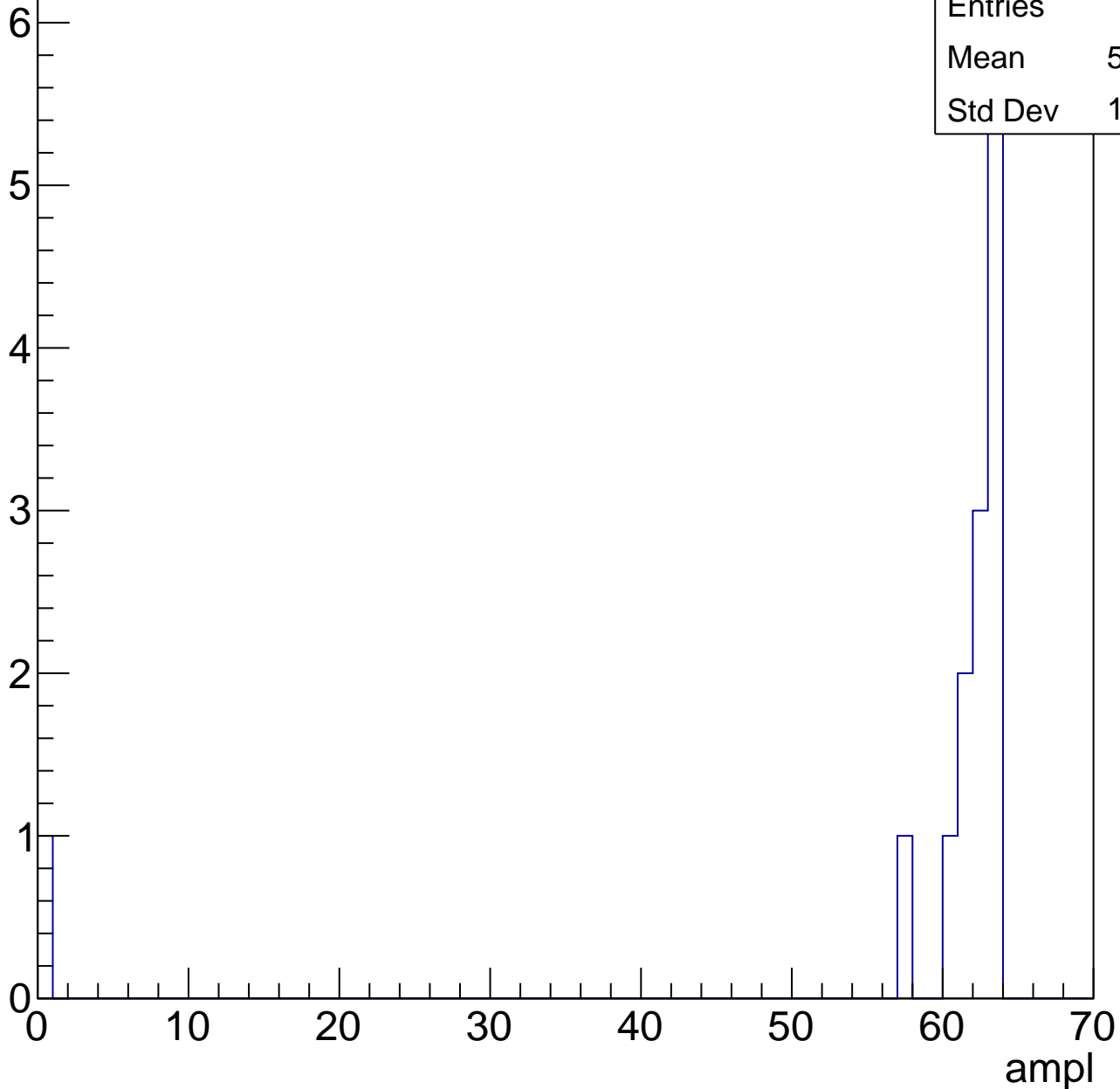
ampl

B0L000S, U4-ch96, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

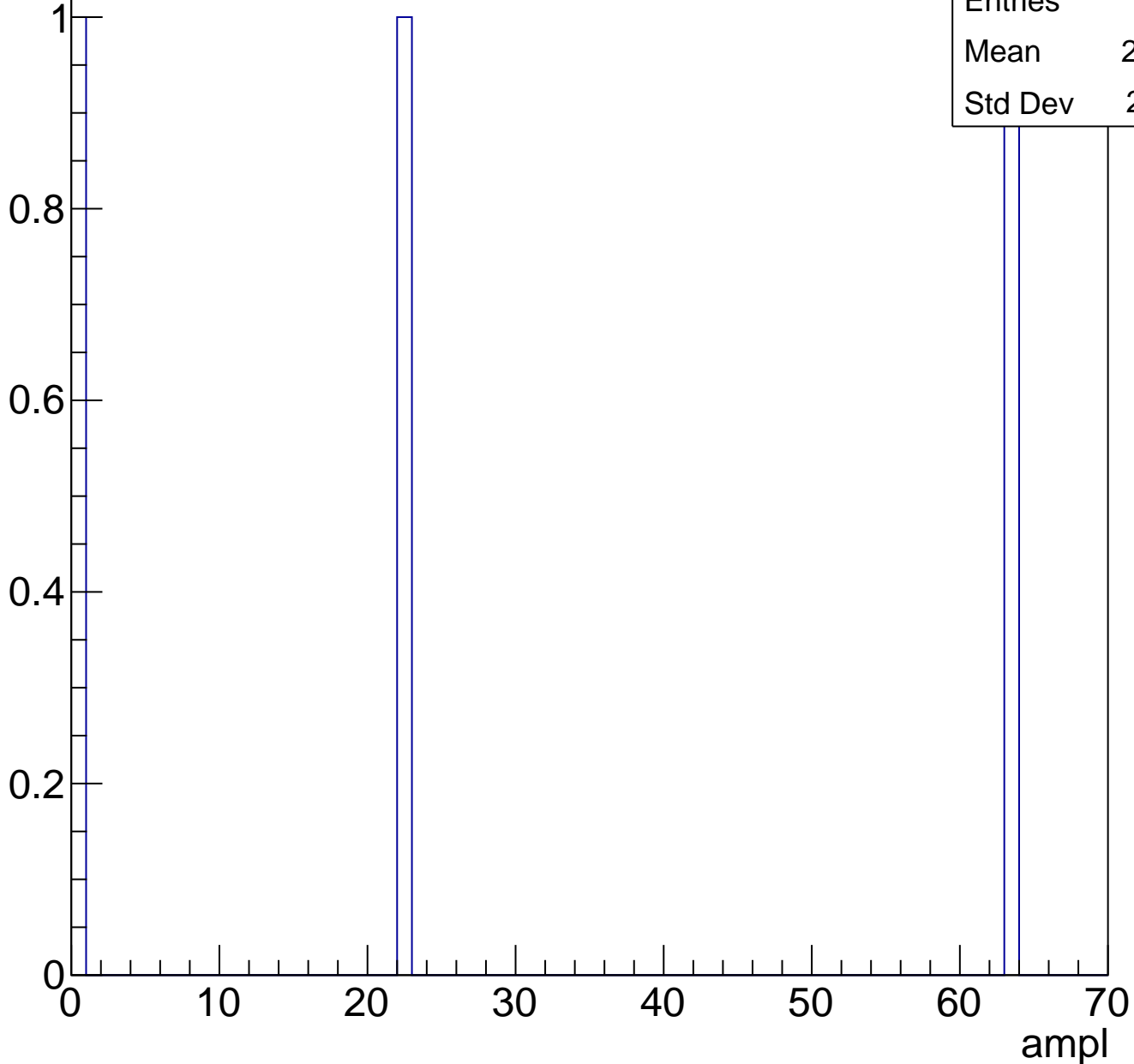
Entries	14
Mean	57.36
Std Dev	15.99



B0L000S, U4-ch96, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B0L000S, U4-ch97, adc0

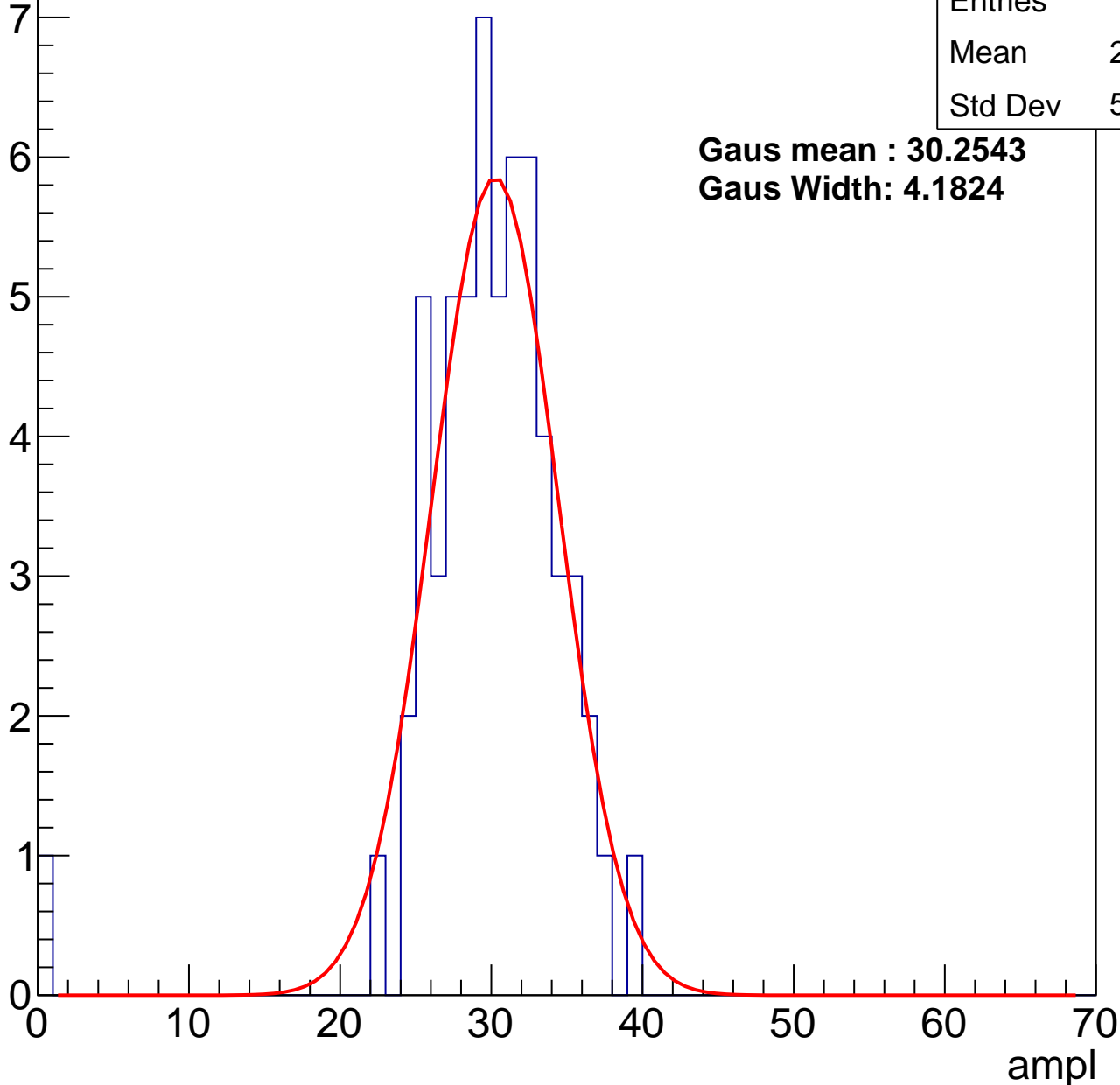
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.43
Std Dev	5.248

Gaus mean : 30.2543

Gaus Width: 4.1824



B0L000S, U4-ch97, adc1

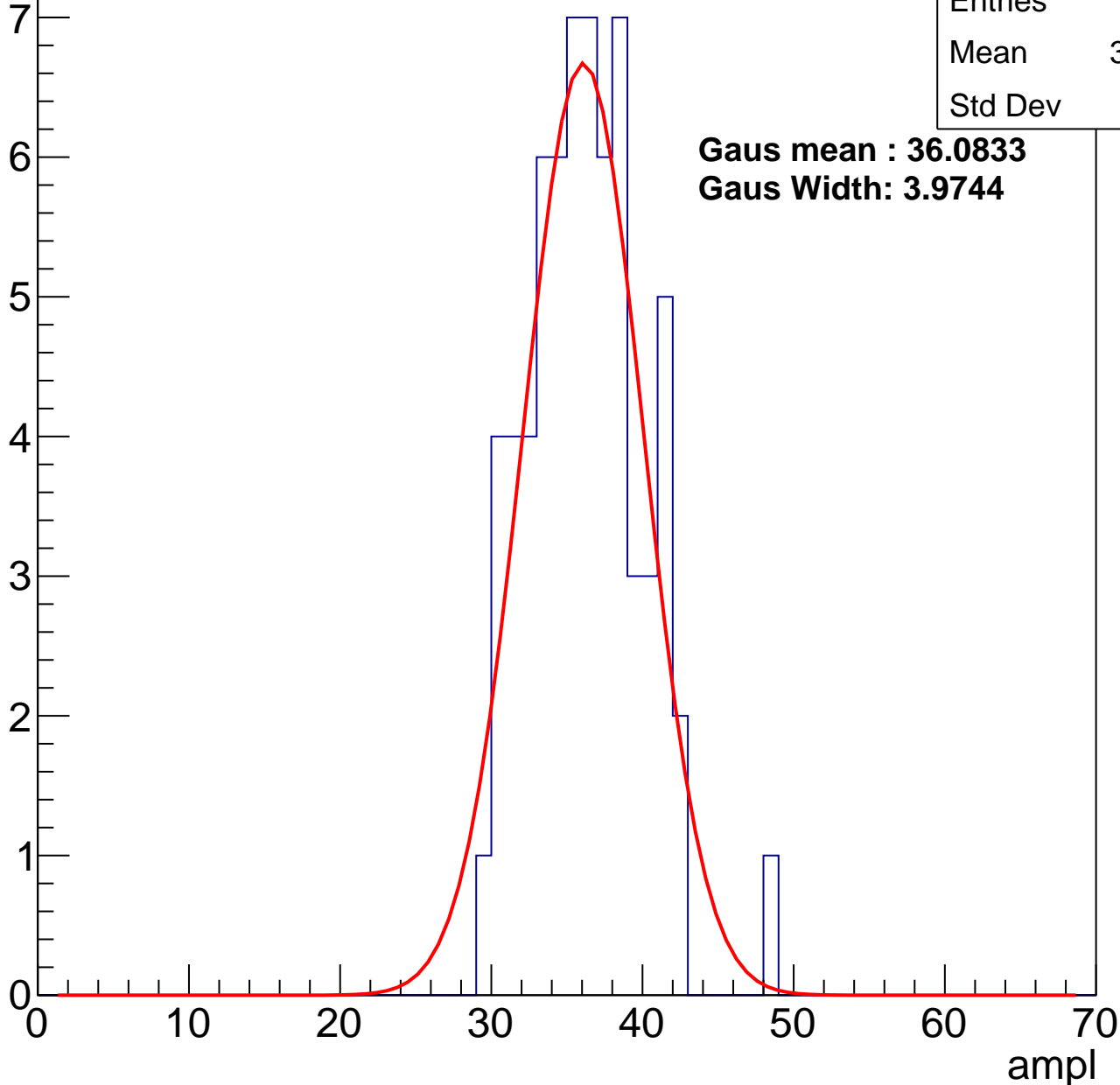
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	35.79
Std Dev	3.67

Gaus mean : 36.0833

Gaus Width: 3.9744



B0L000S, U4-ch97, adc2

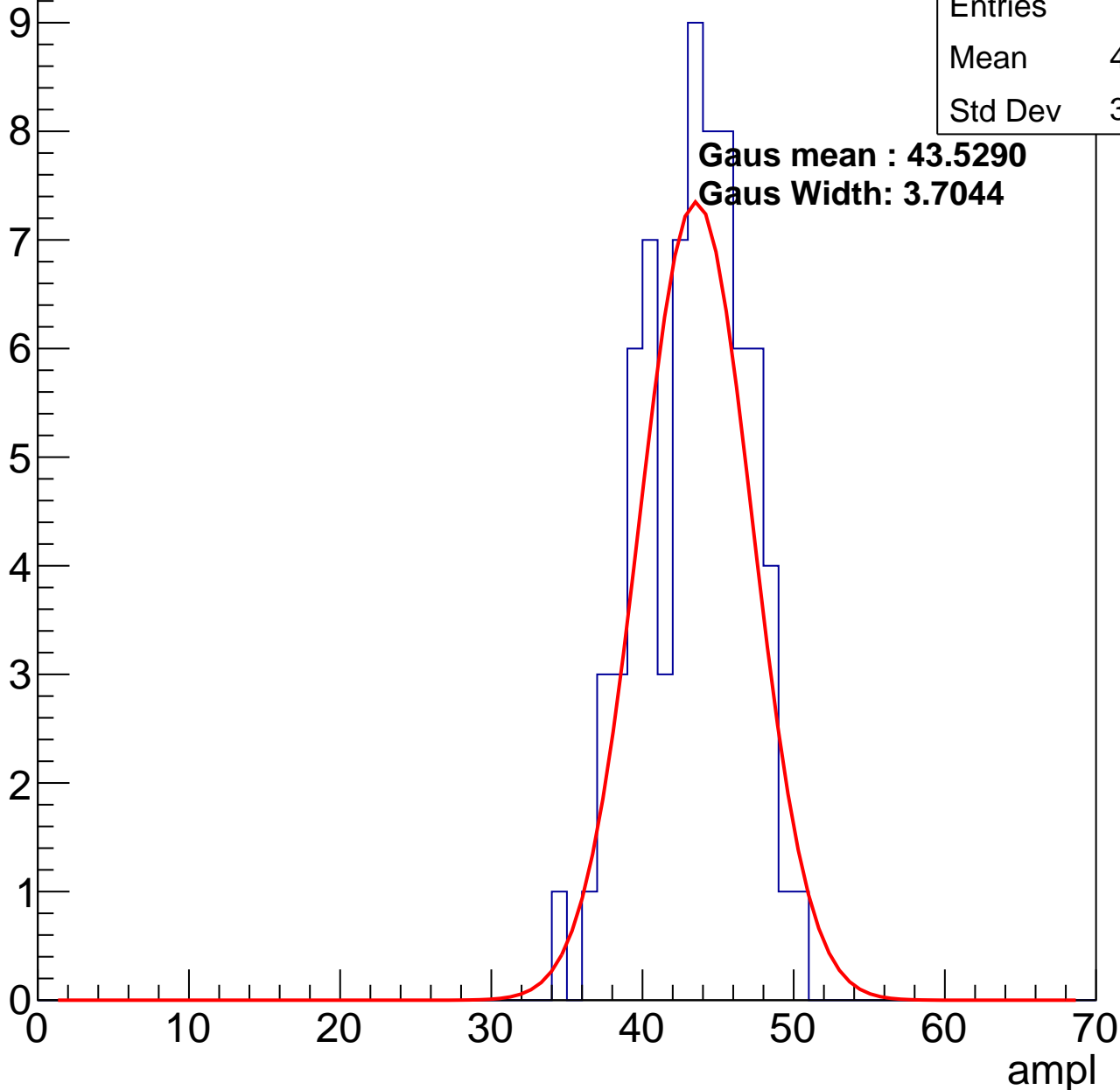
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.89
Std Dev	3.419

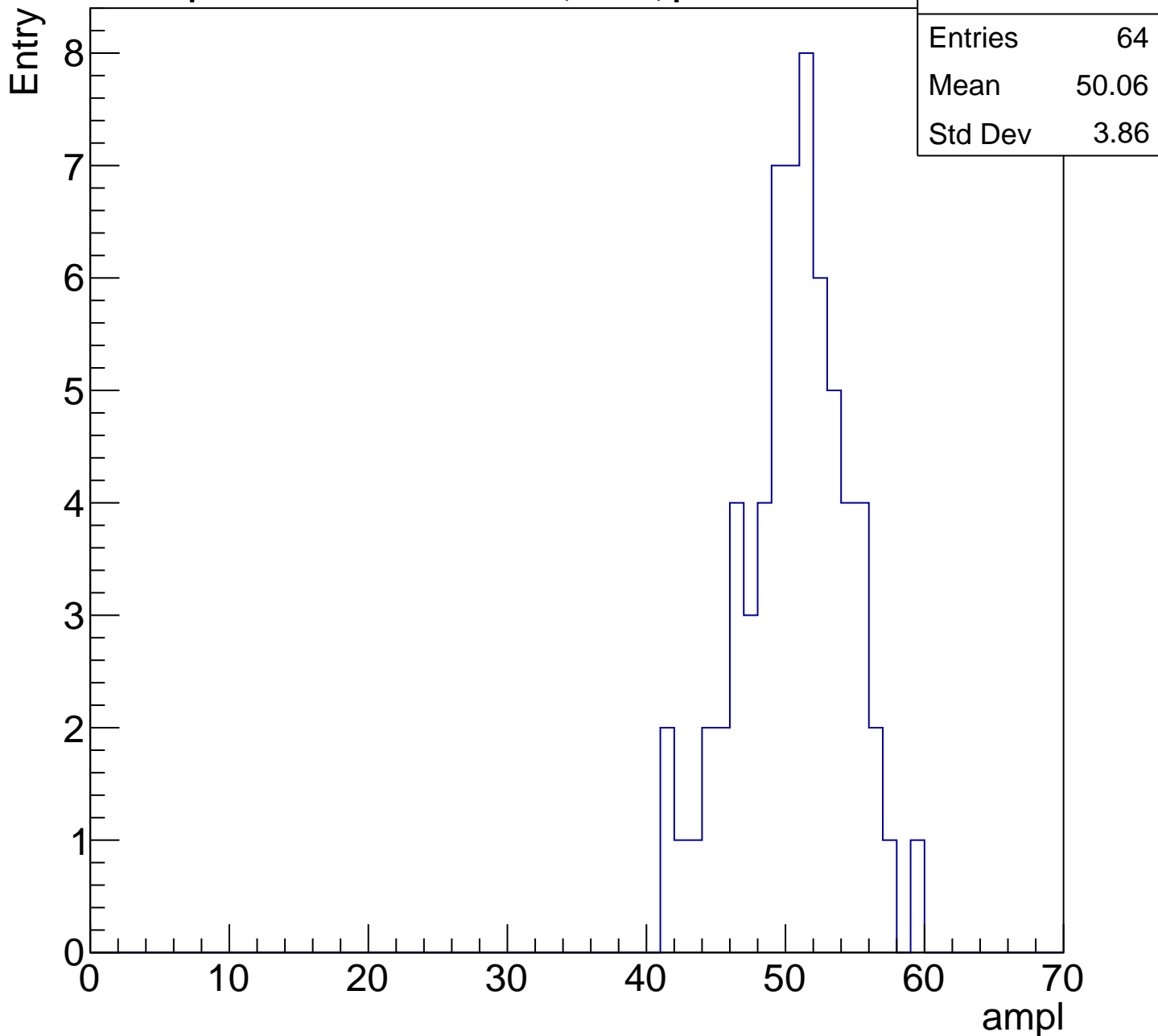
Gaus mean : 43.5290

Gaus Width: 3.7044



B0L000S, U4-ch97, adc3

calib_packv5_042523_0143.root, FC#5, port B1

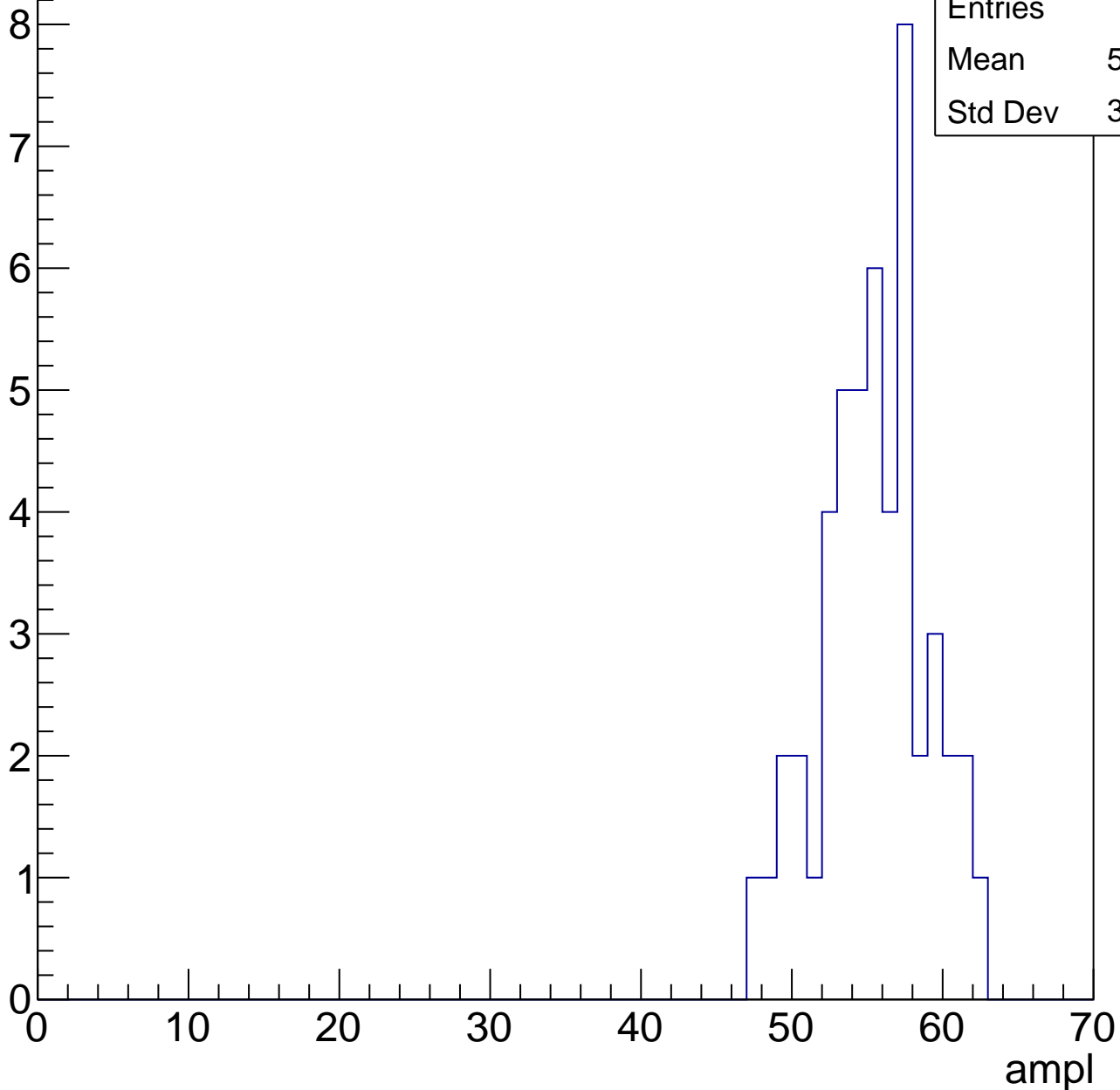


B0L000S, U4-ch97, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

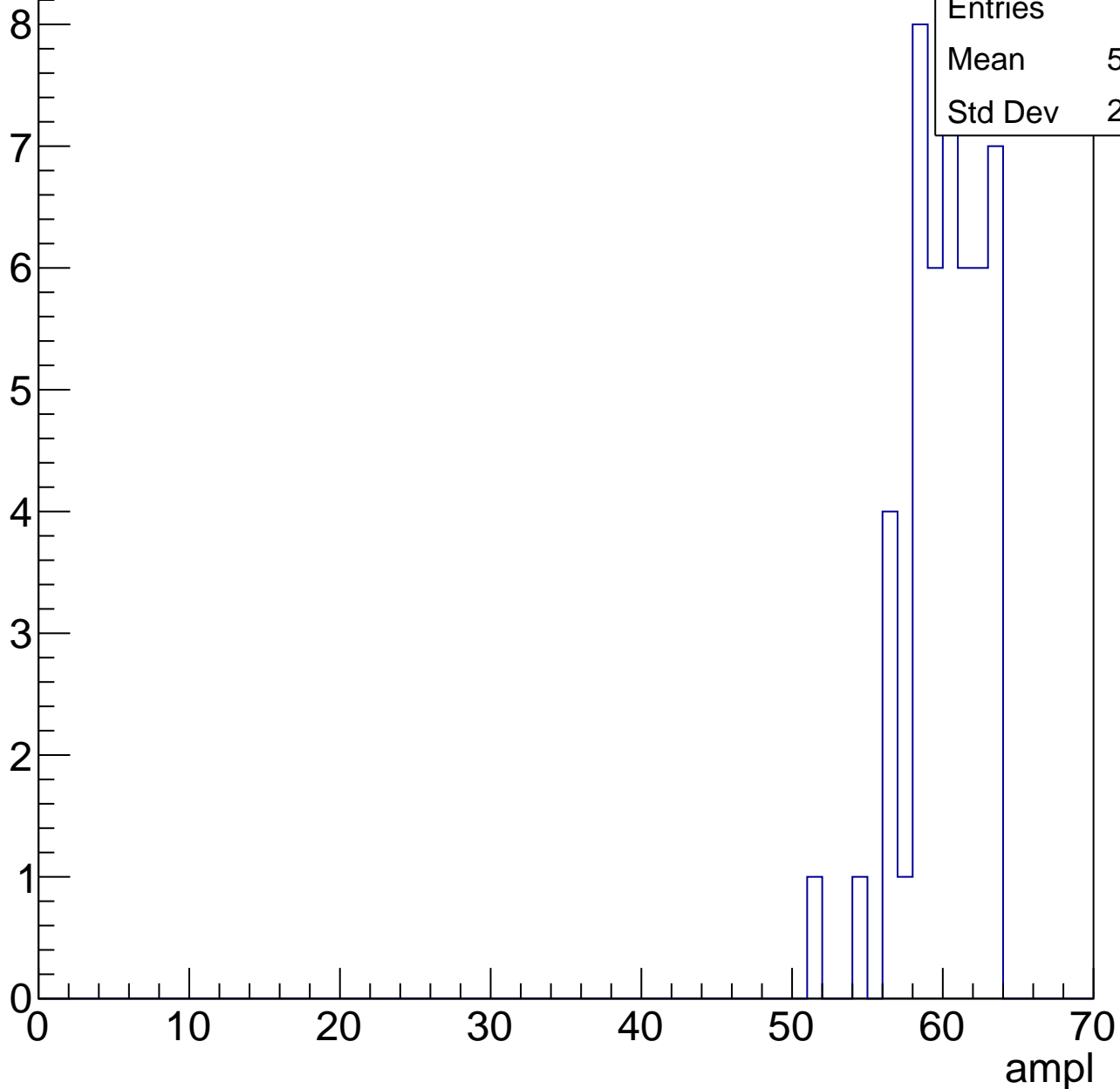
Entries	49
Mean	54.98
Std Dev	3.455



B0L000S, U4-ch97, adc5

calib_packv5_042523_0143.root, FC#5, port B1

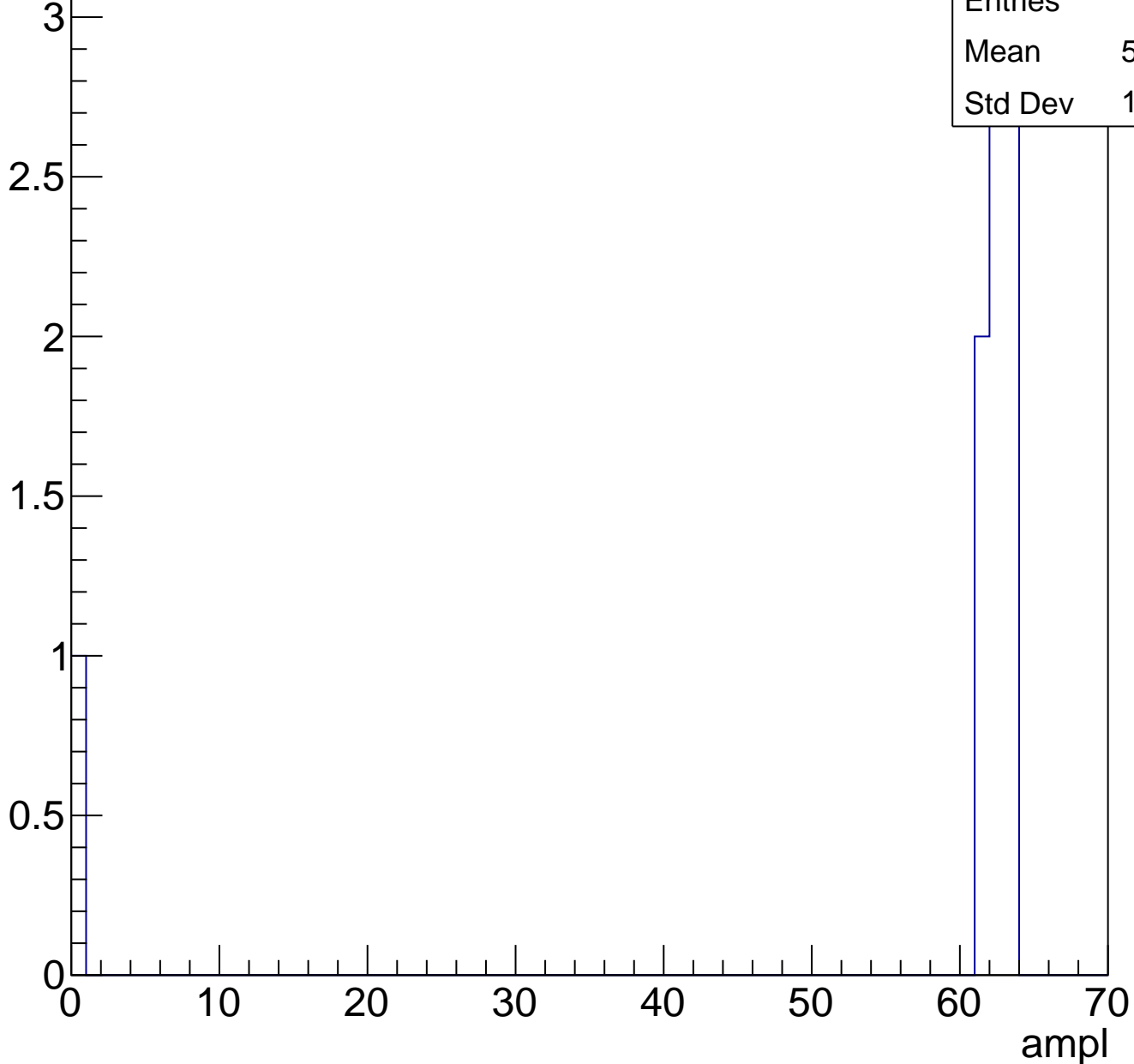
Entry



B0L000S, U4-ch97, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	9
Mean	55.22
Std Dev	19.54

B0L000S, U4-ch97, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch98, adc0

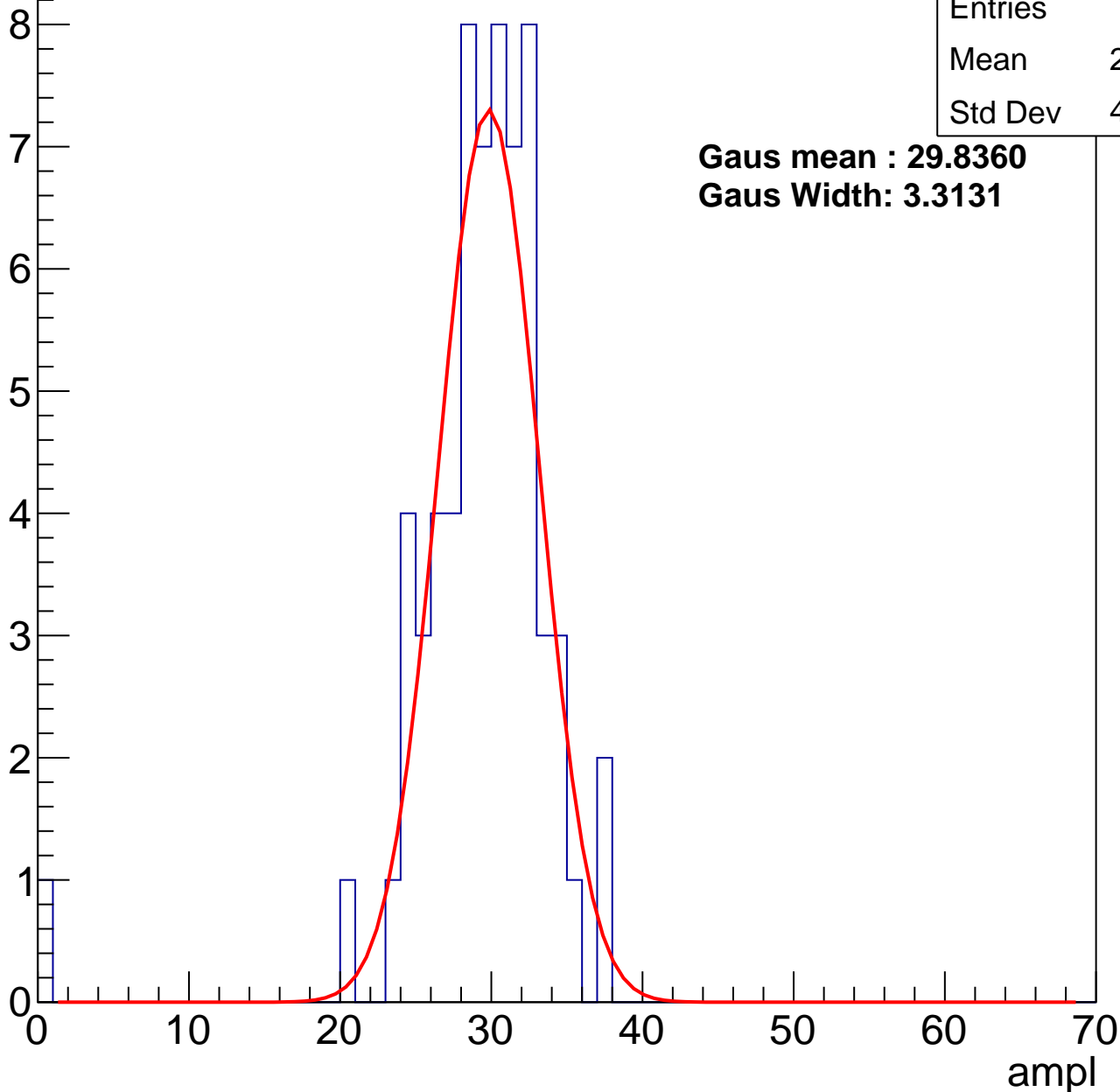
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	28.86
Std Dev	4.899

Gaus mean : 29.8360

Gaus Width: 3.3131



B0L000S, U4-ch98, adc1

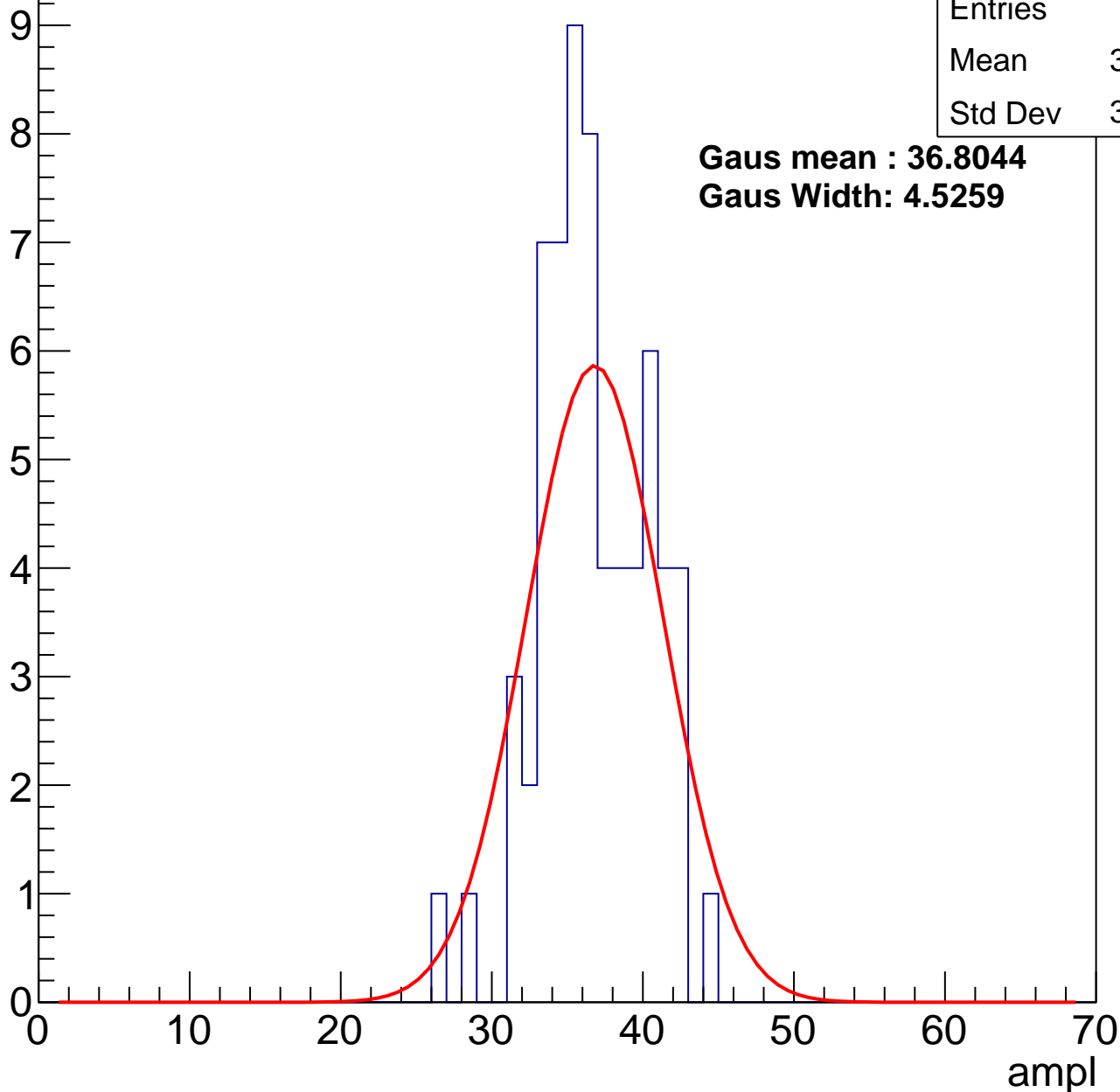
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.23
Std Dev	3.568

Gaus mean : 36.8044

Gaus Width: 4.5259



B0L000S, U4-ch98, adc2

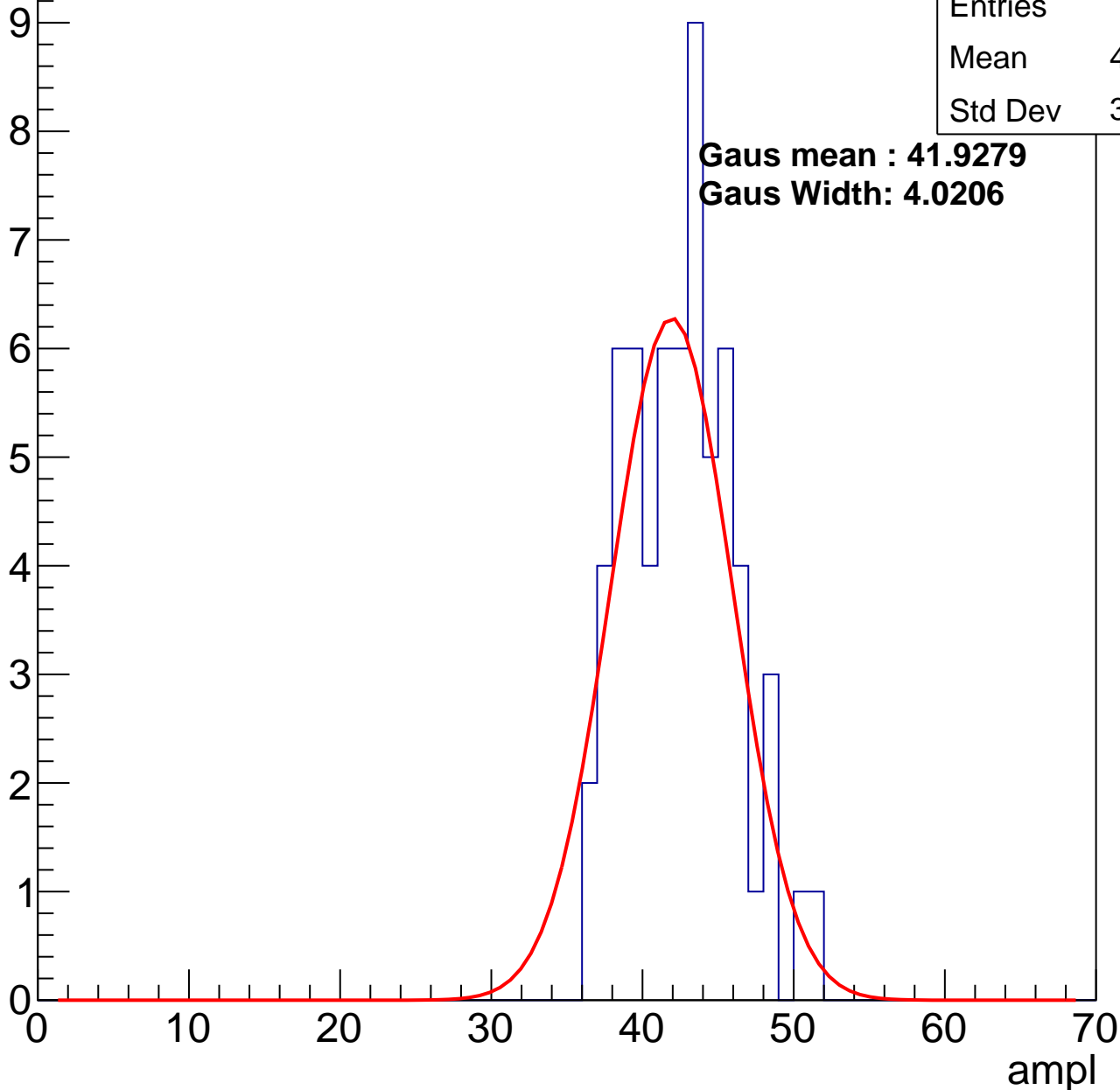
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	42.08
Std Dev	3.465

Gaus mean : 41.9279

Gaus Width: 4.0206

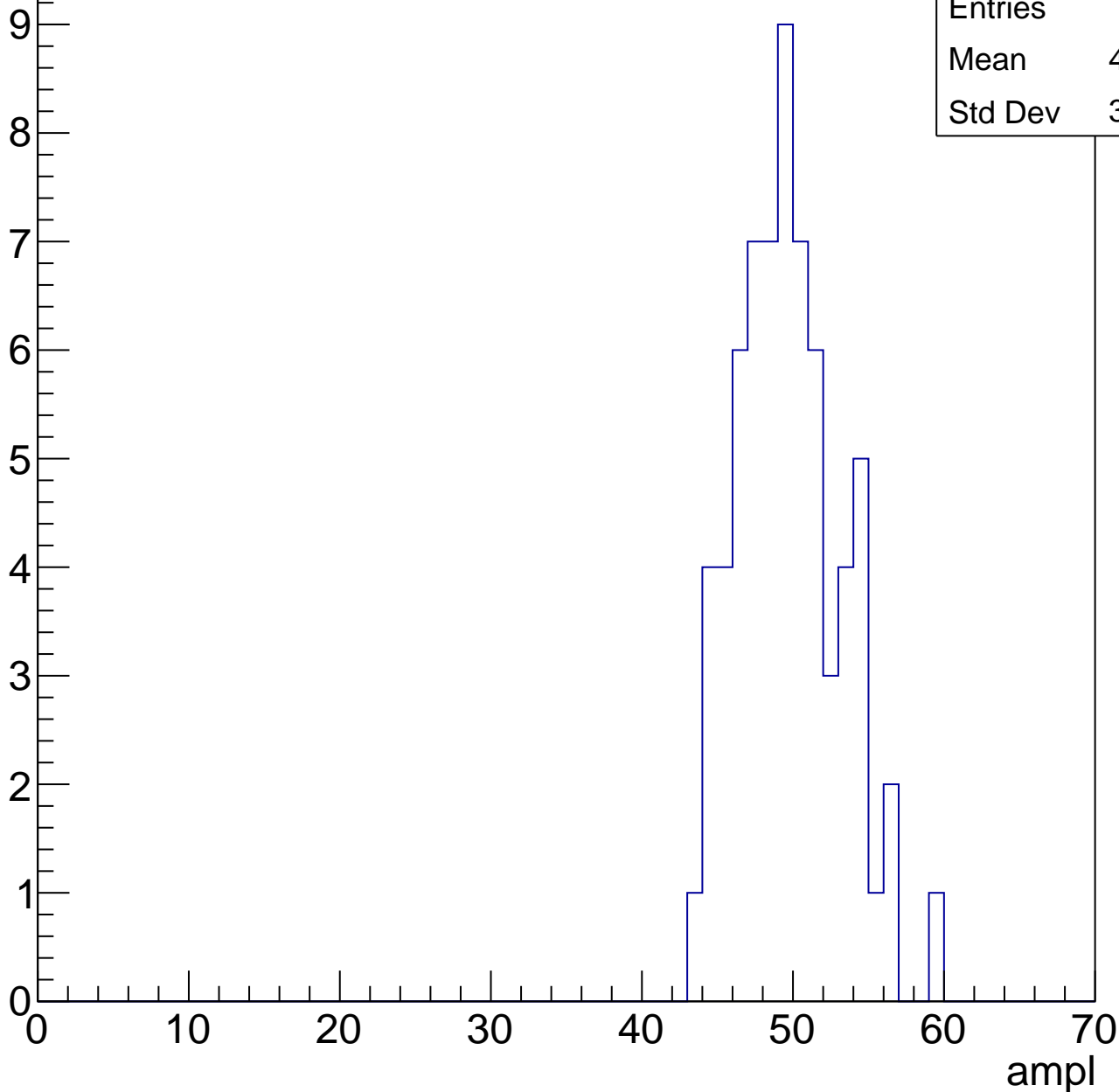


B0L000S, U4-ch98, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.27
Std Dev	3.379

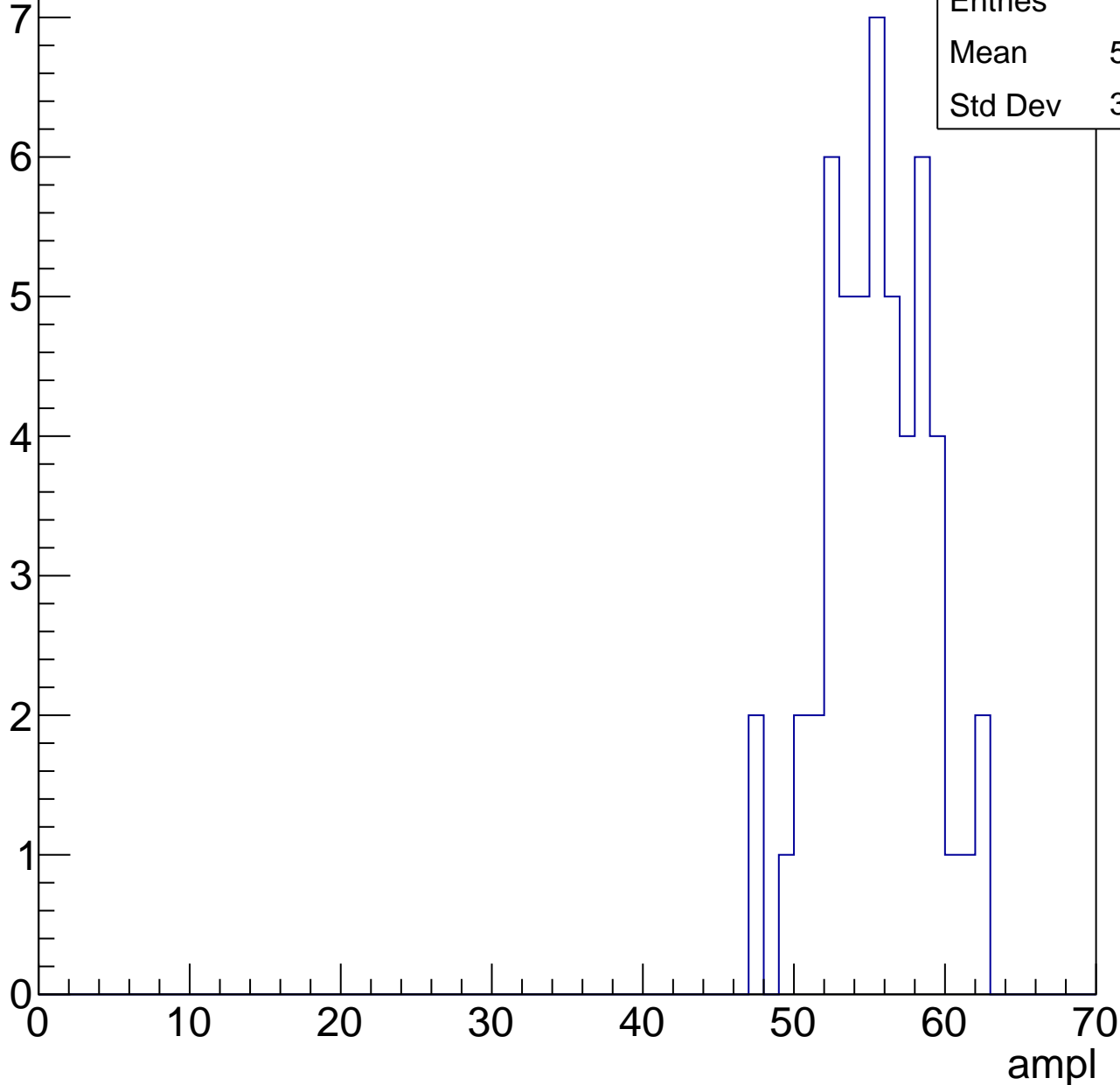


B0L000S, U4-ch98, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	54.98
Std Dev	3.428

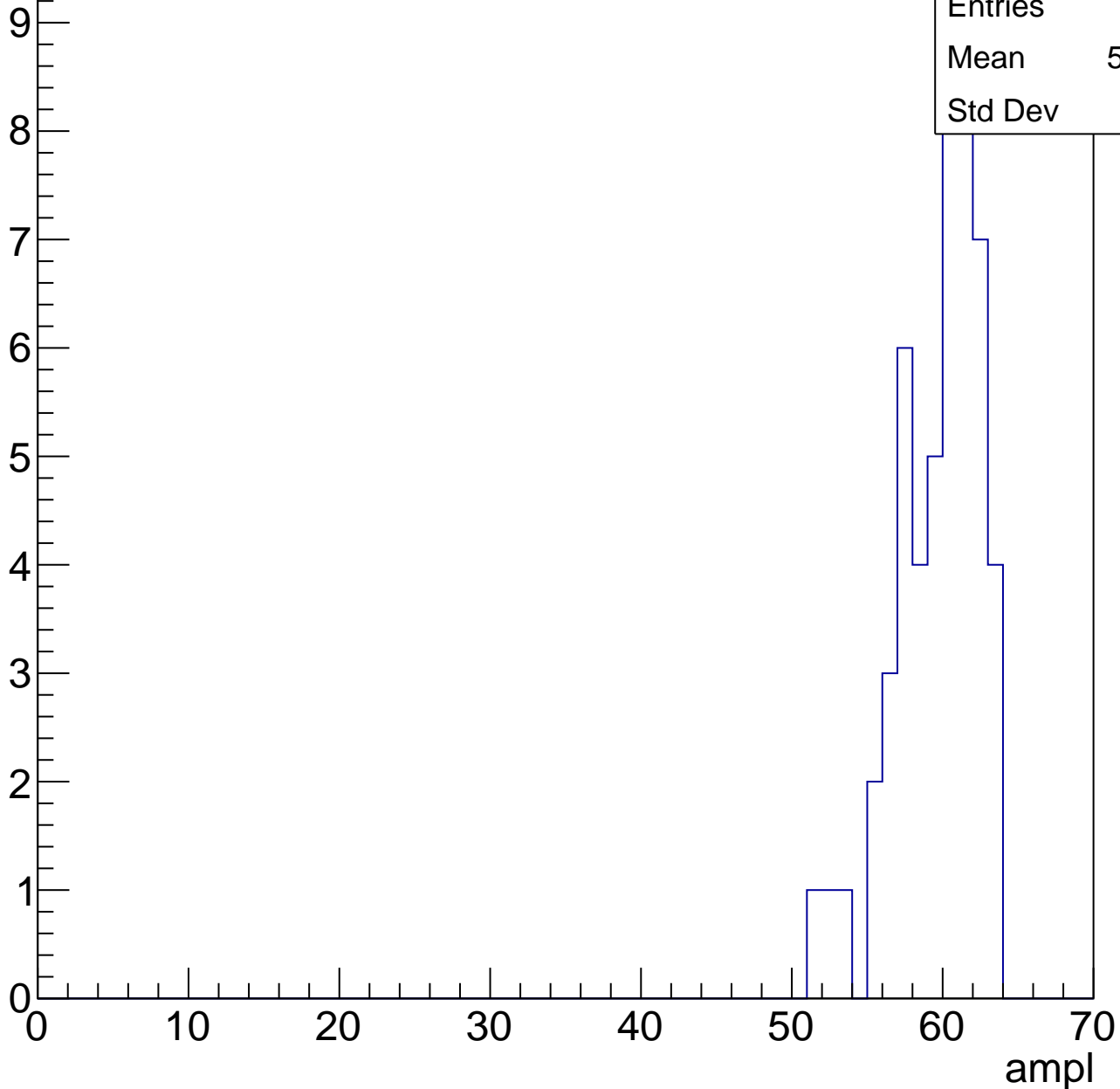


B0L000S, U4-ch98, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	59.16
Std Dev	2.81

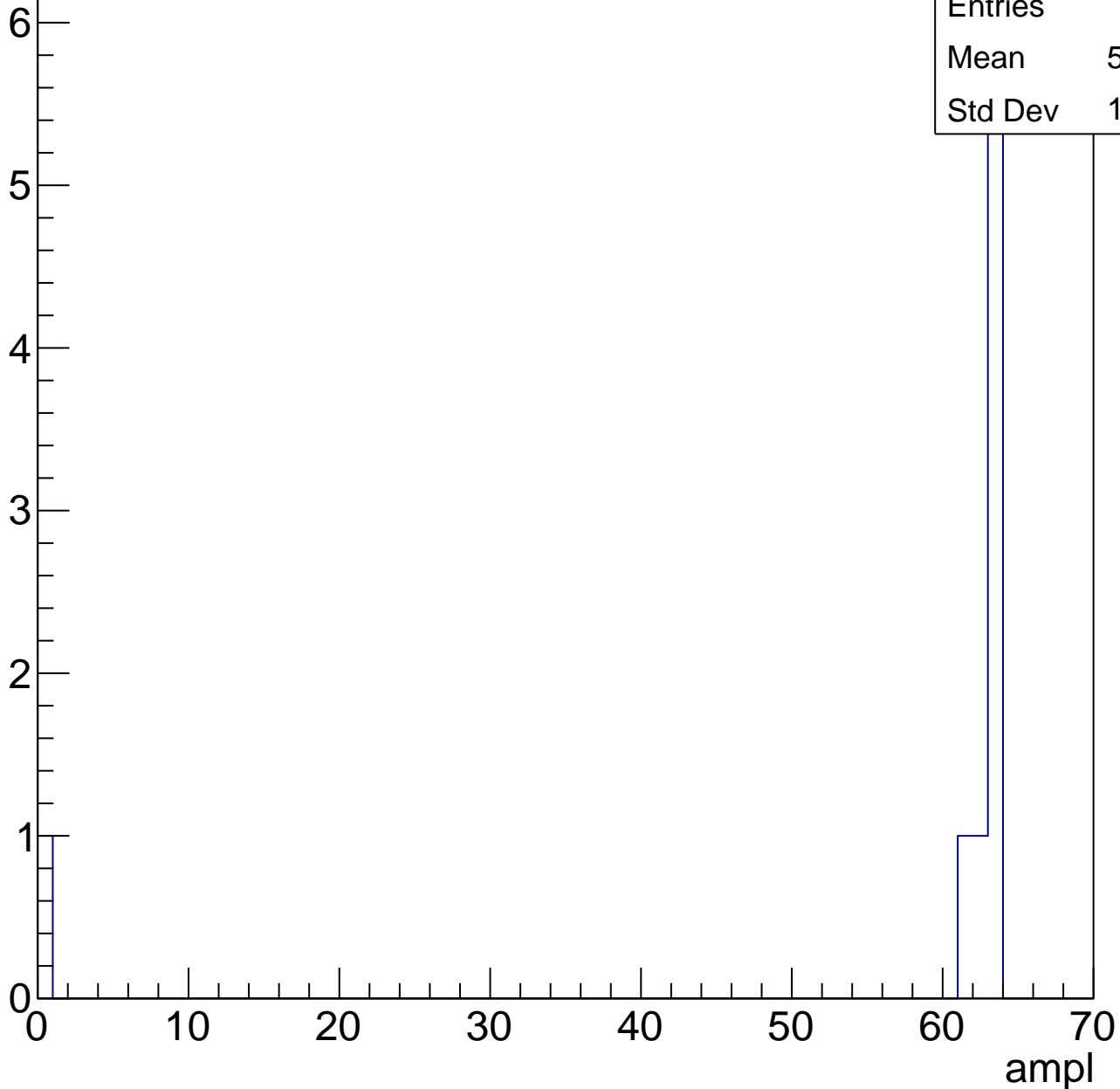


B0L000S, U4-ch98, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	9
Mean	55.67
Std Dev	19.69



B0L000S, U4-ch98, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U4-ch99, adc0

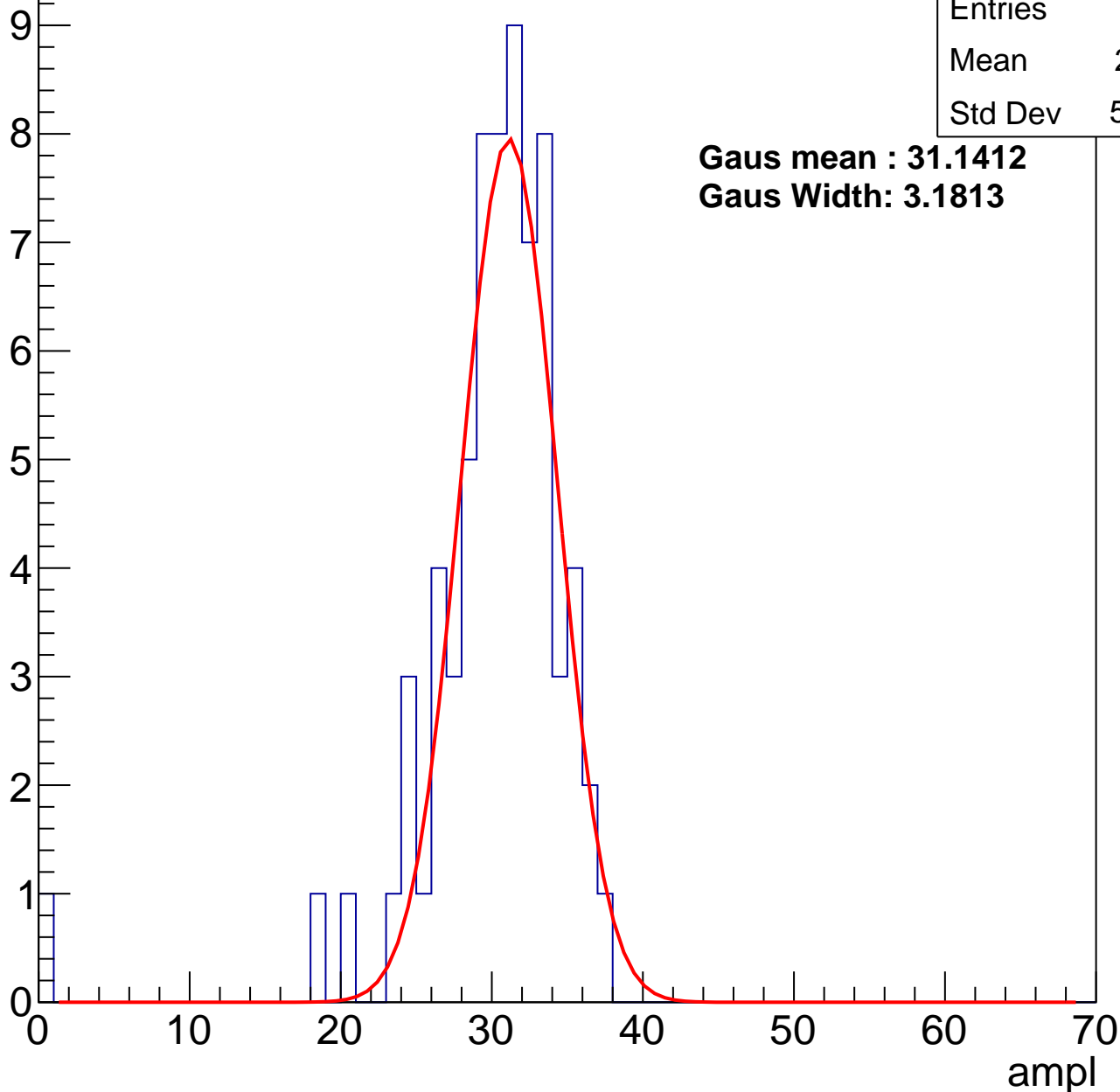
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.61
Std Dev	5.089

Gaus mean : 31.1412

Gaus Width: 3.1813



B0L000S, U4-ch99, adc1

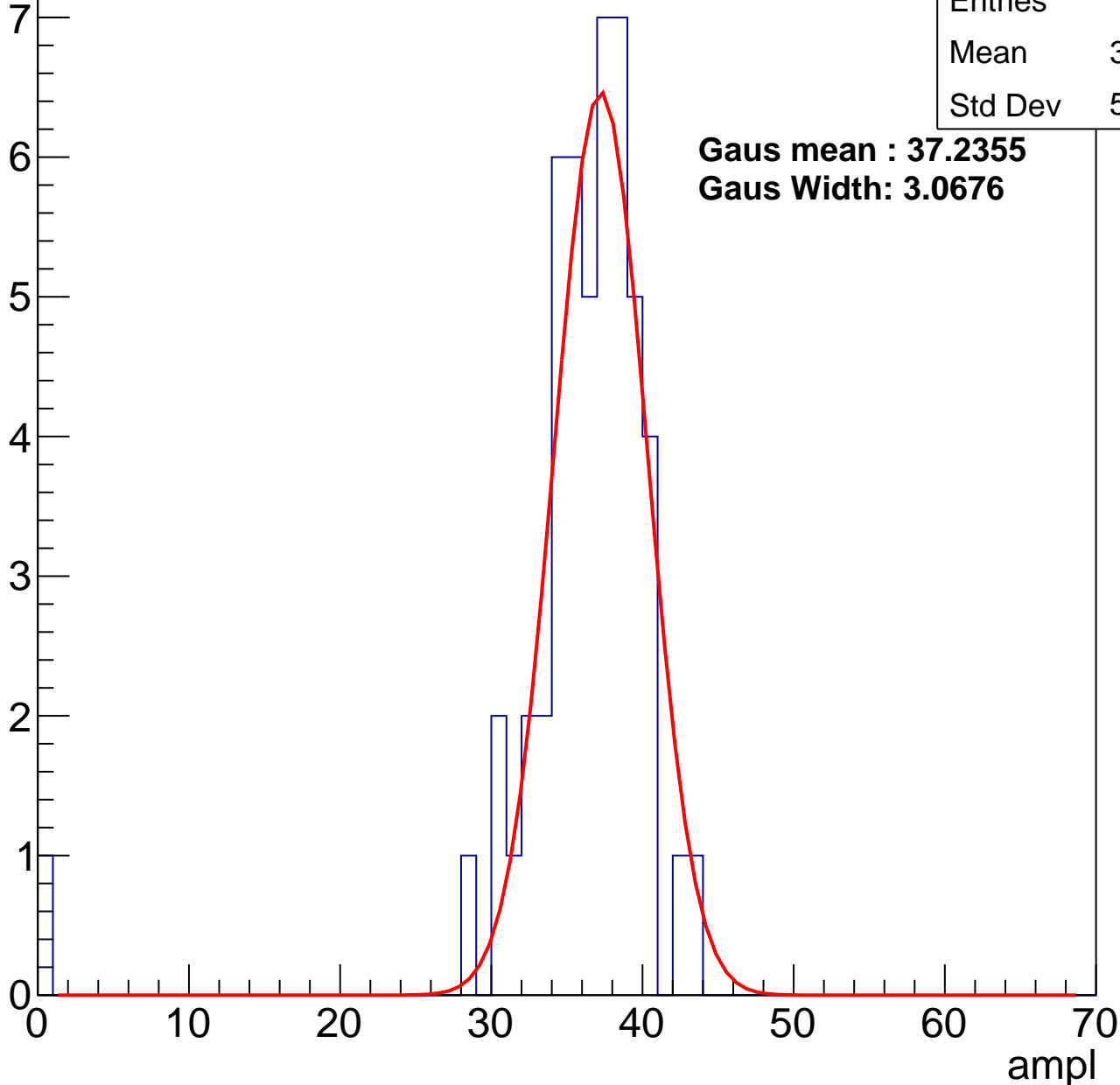
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	35.45
Std Dev	5.859

Gaus mean : 37.2355

Gaus Width: 3.0676



B0L000S, U4-ch99, adc2

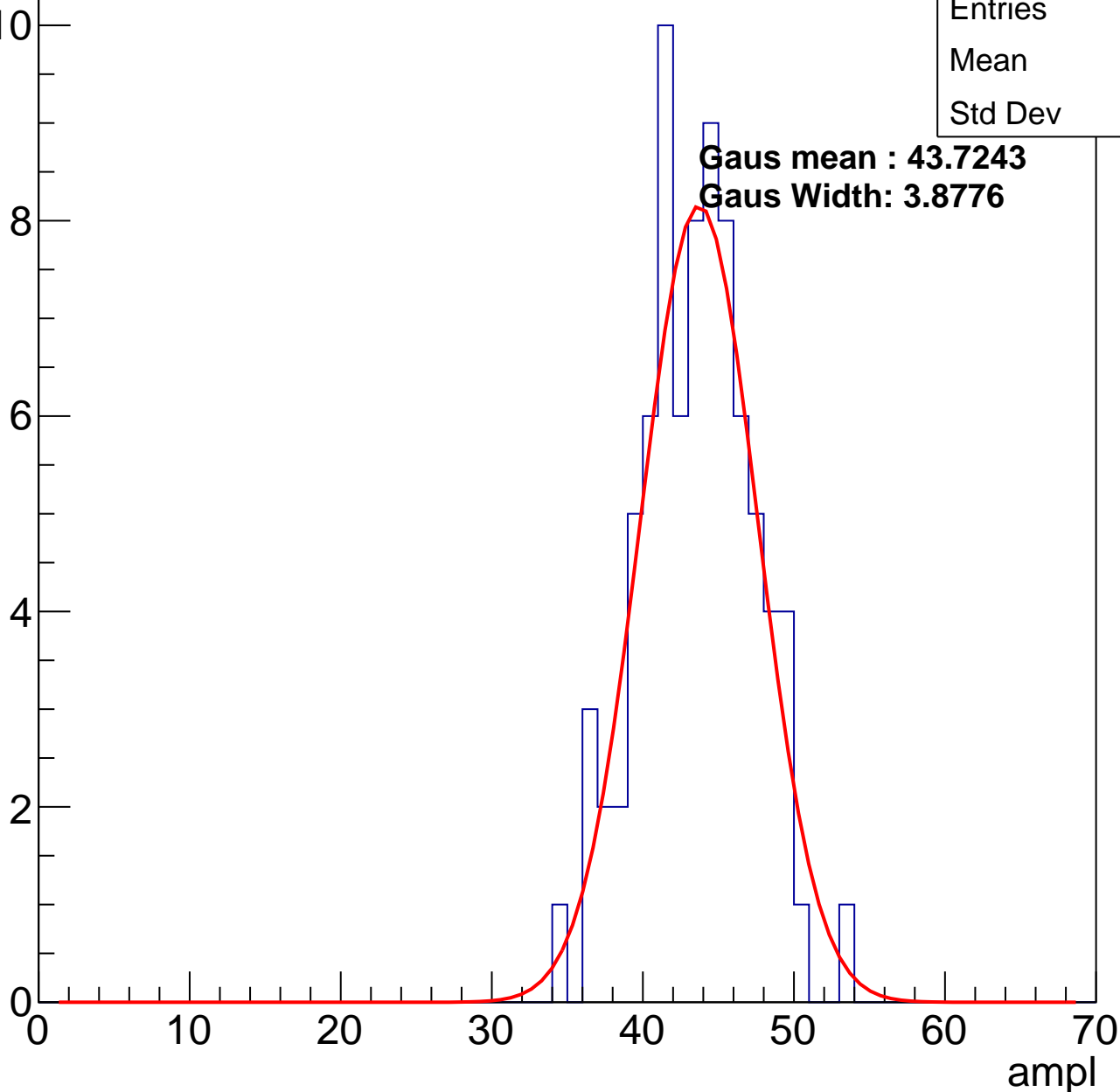
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	81
Mean	43.1
Std Dev	3.68

Gaus mean : 43.7243

Gaus Width: 3.8776

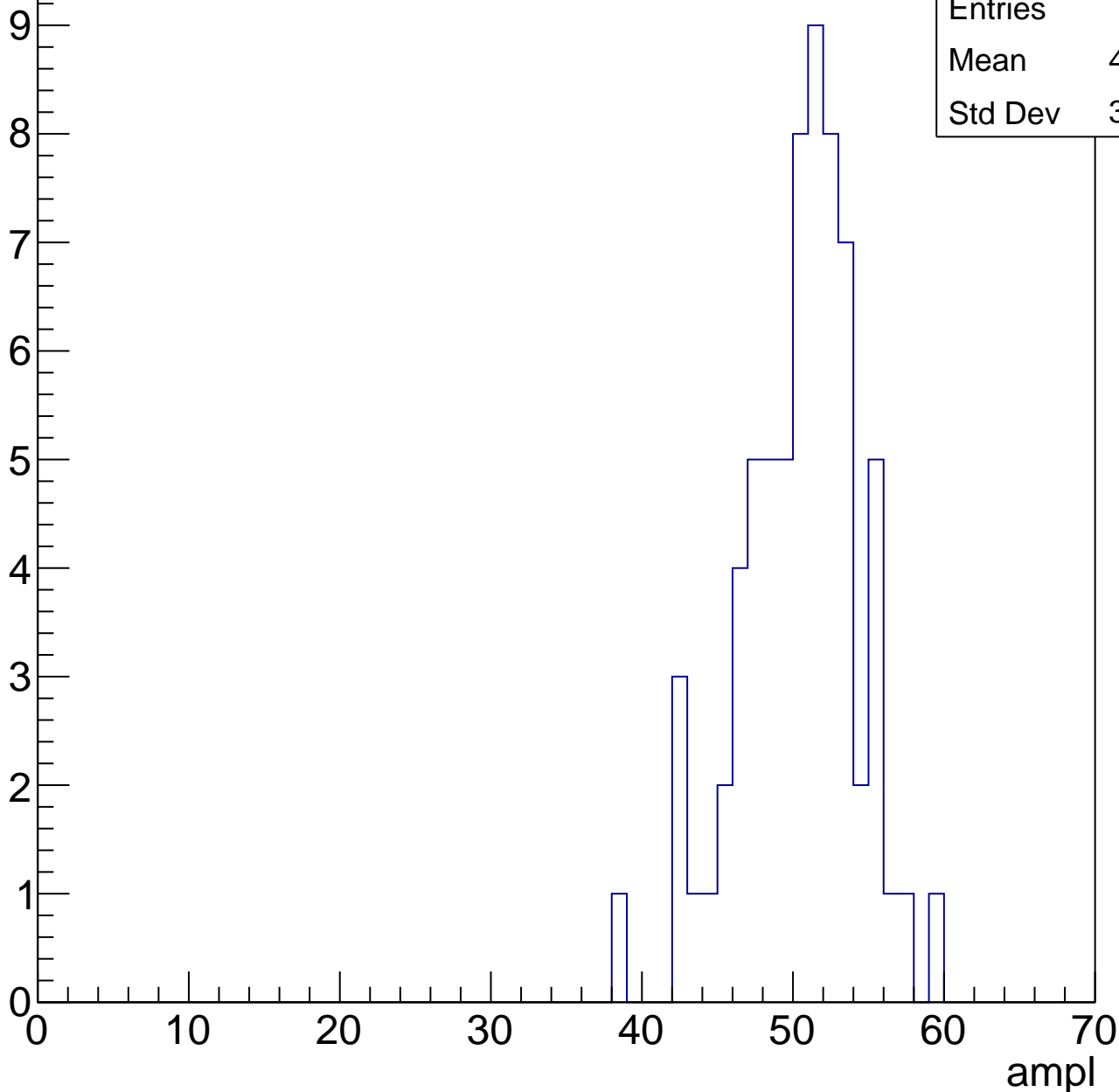


B0L000S, U4-ch99, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

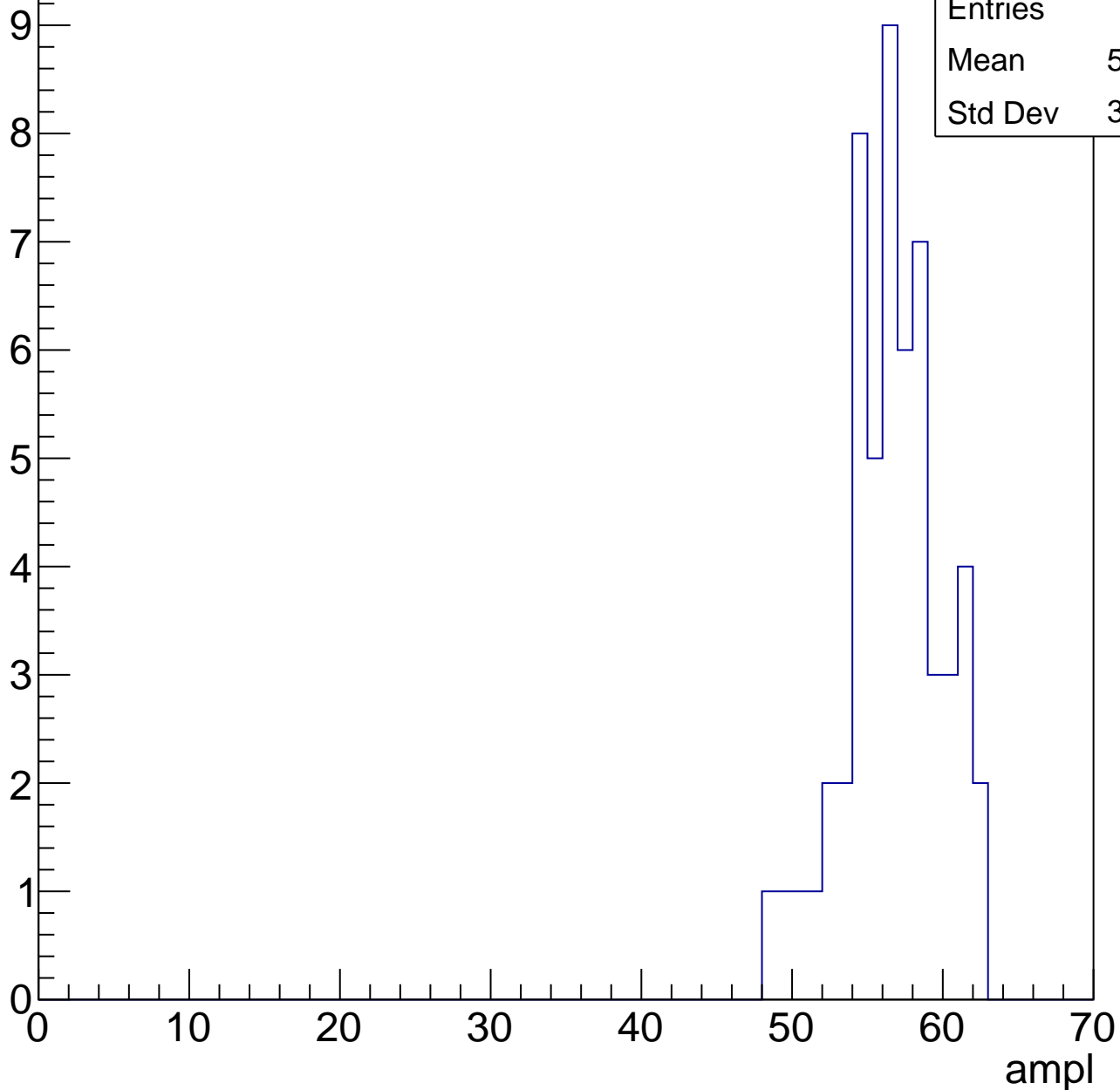
Entries	69
Mean	49.94
Std Dev	3.867



B0L000S, U4-ch99, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



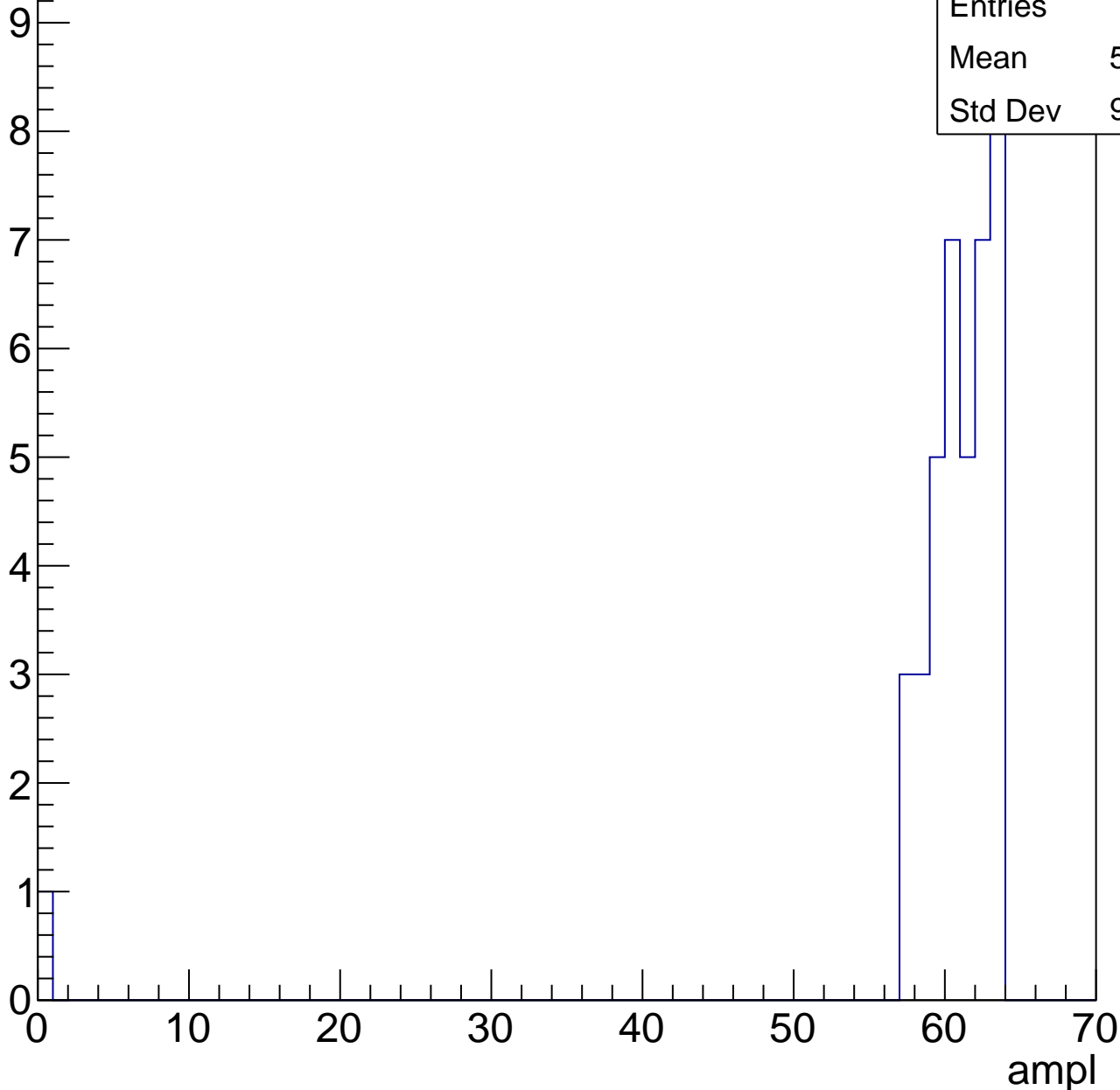
Entries	55
Mean	56.22
Std Dev	3.132

B0L000S, U4-ch99, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

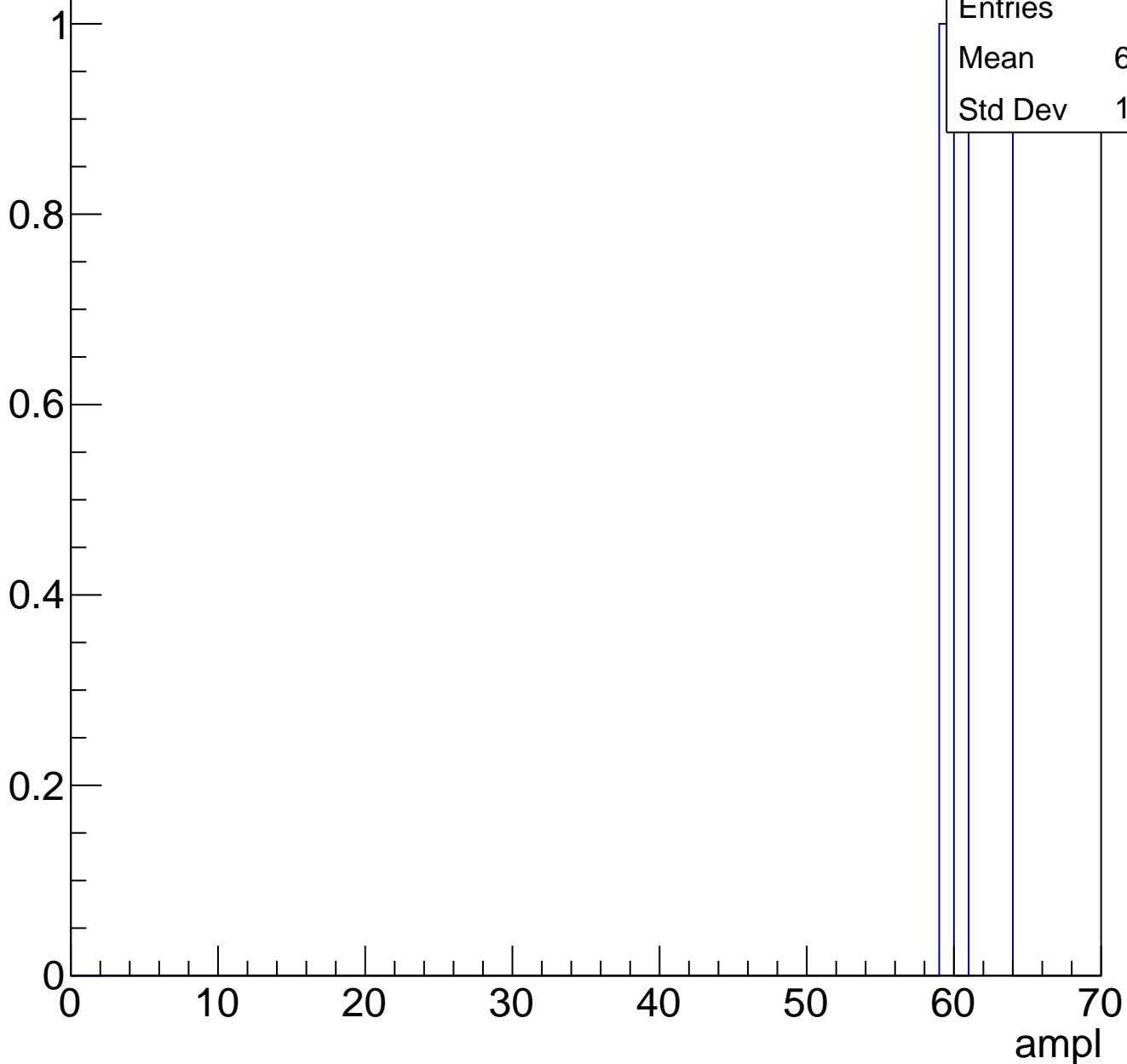
Entries	40
Mean	59.15
Std Dev	9.655



B0L000S, U4-ch99, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch99, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch100, adc0

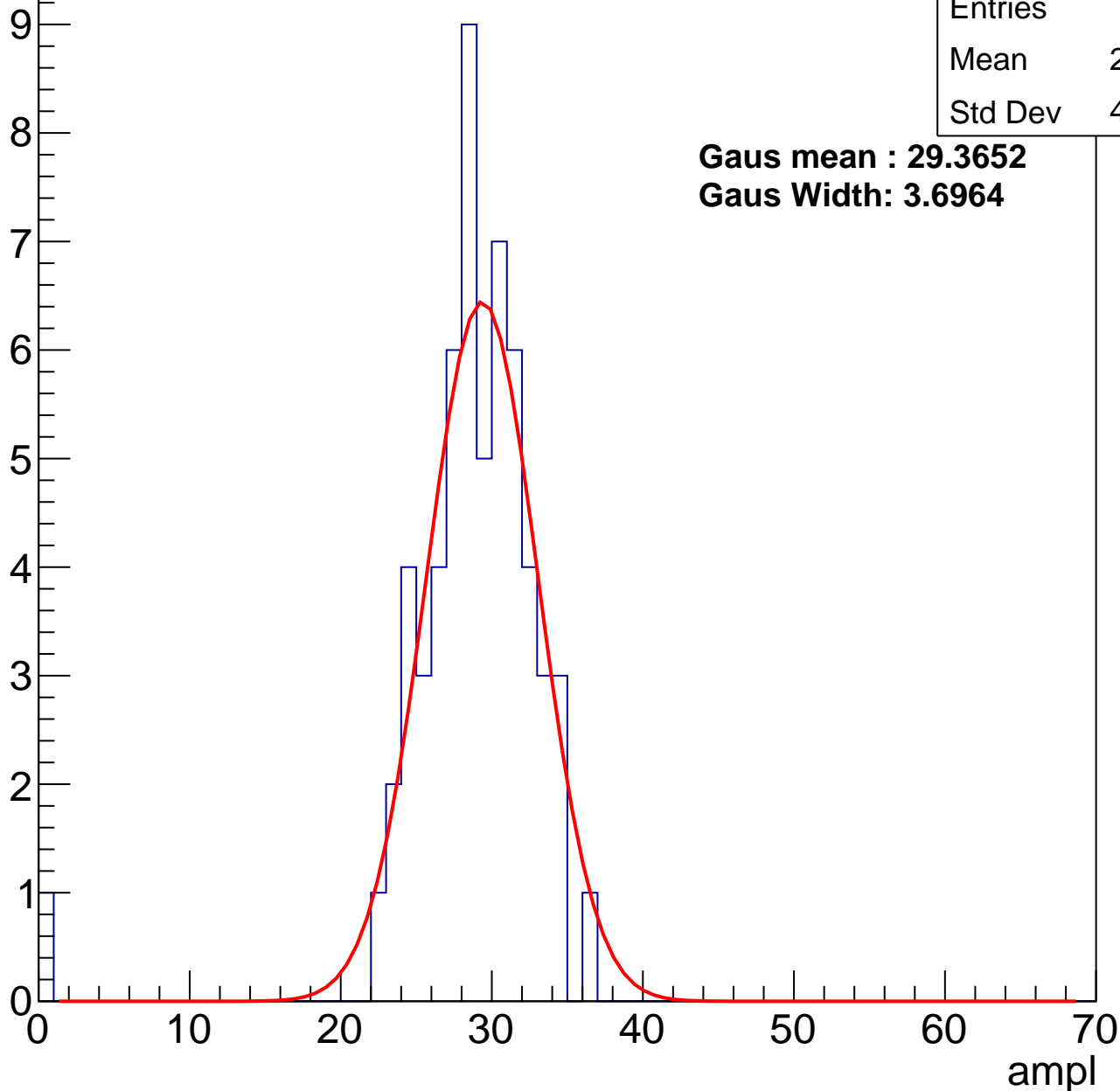
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	28.19
Std Dev	4.834

Gaus mean : 29.3652

Gaus Width: 3.6964



B0L000S, U4-ch100, adc1

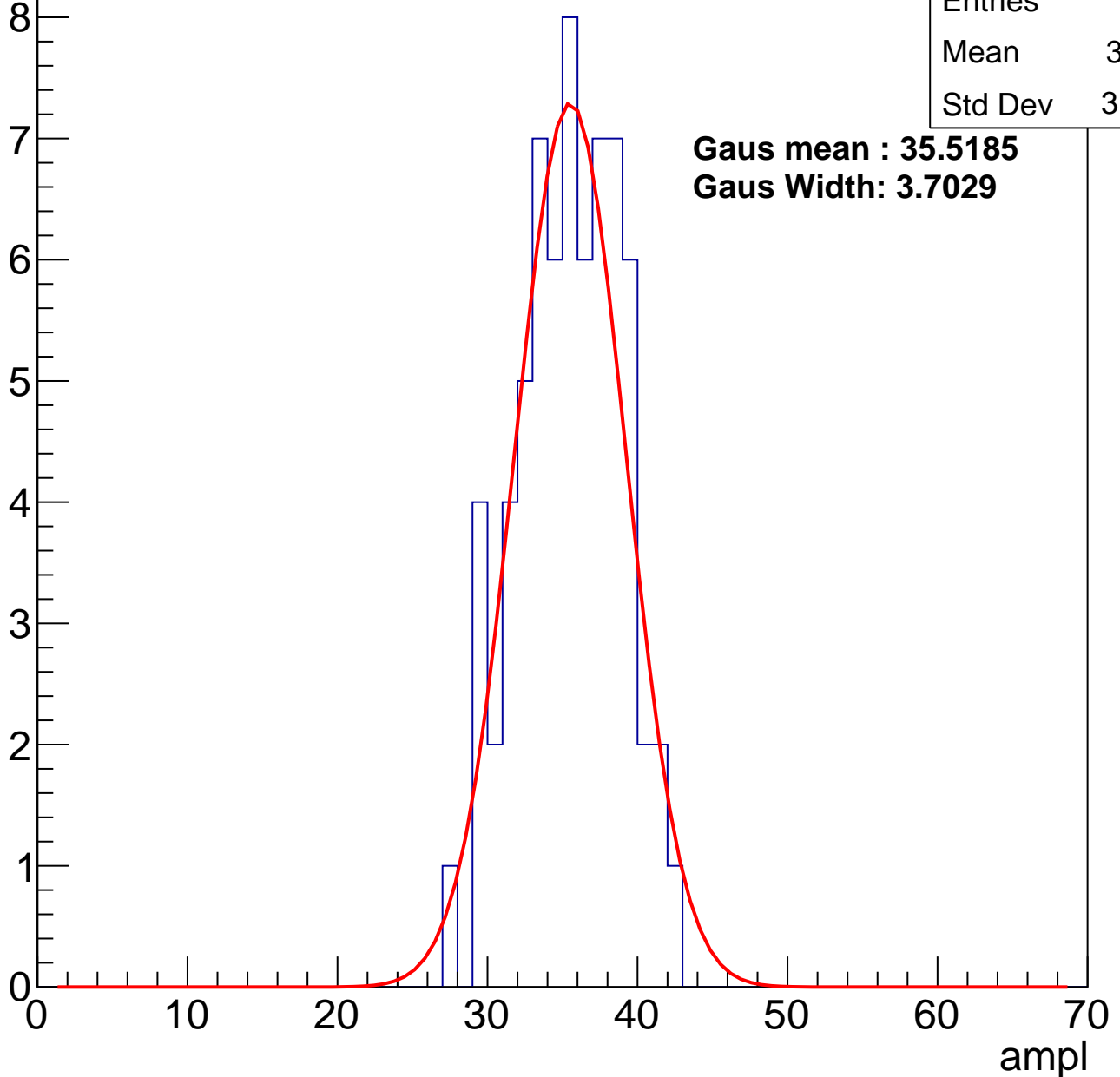
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	35.01
Std Dev	3.354

Gaus mean : 35.5185

Gaus Width: 3.7029



B0L000S, U4-ch100, adc2

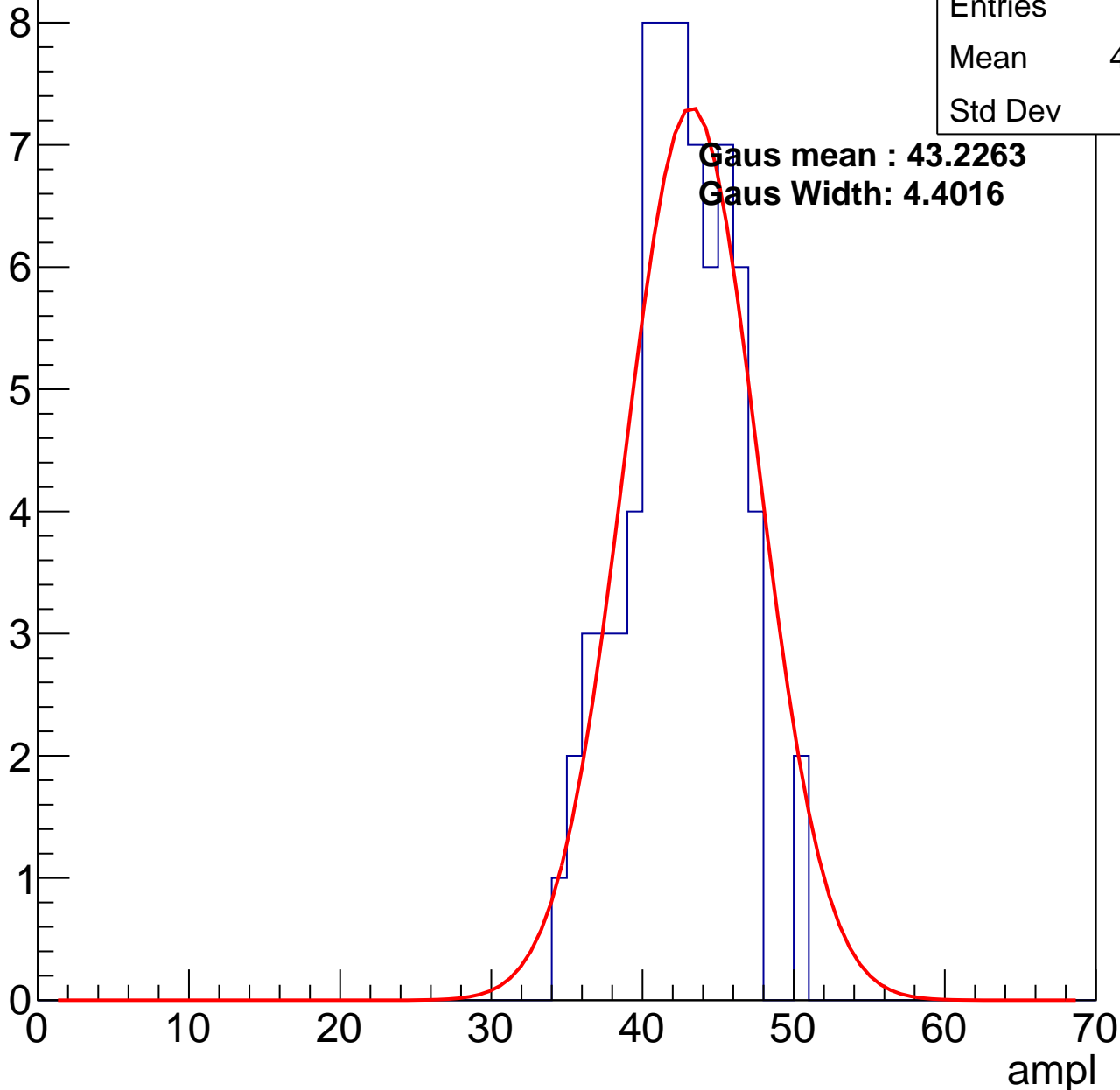
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	41.96
Std Dev	3.51

Gaus mean : 43.2263

Gaus Width: 4.4016



B0L000S, U4-ch100, adc3

calib_packv5_042523_0143.root, FC#5, port B1

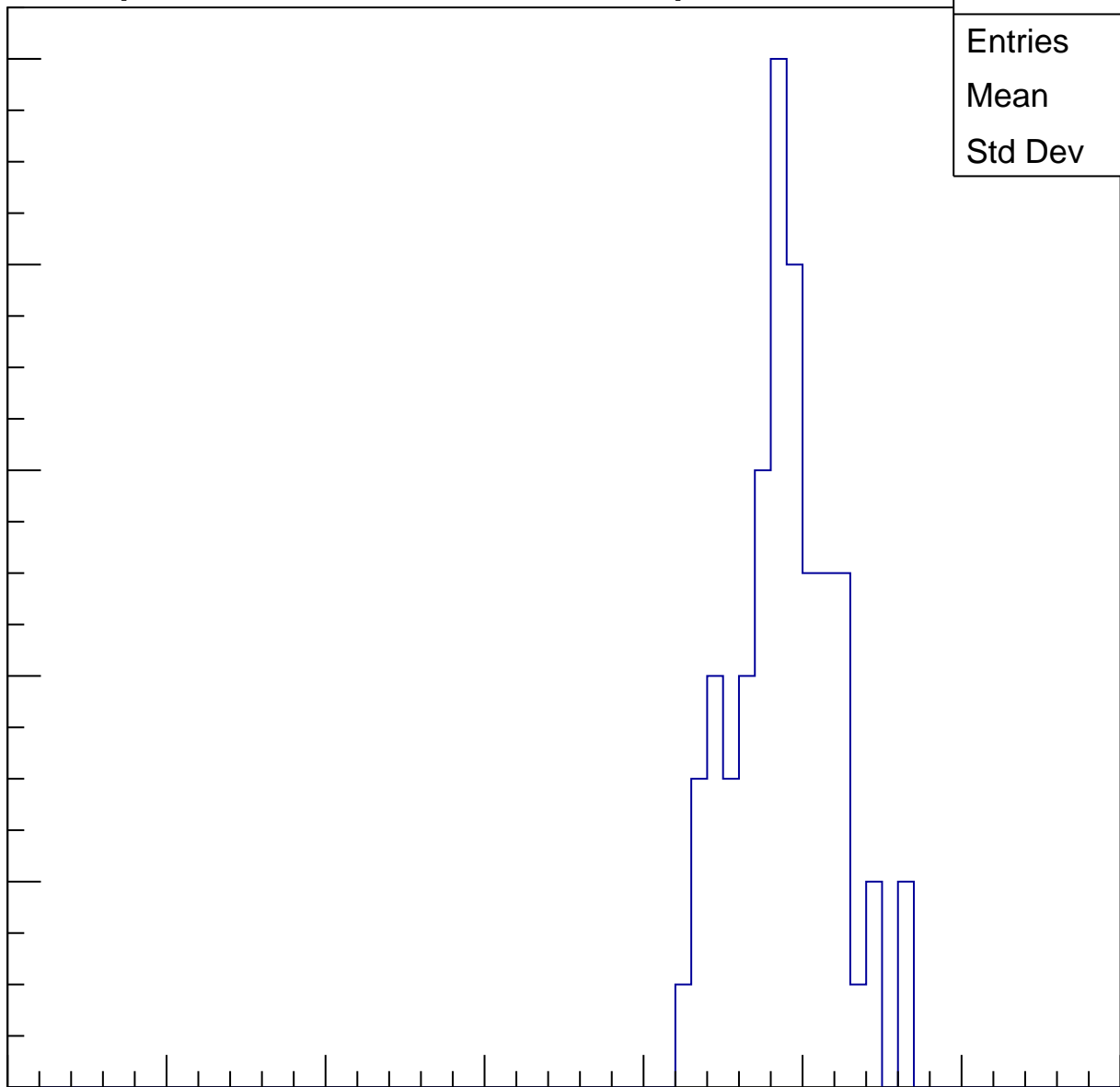
Entries	59
Mean	48.44
Std Dev	3.164

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

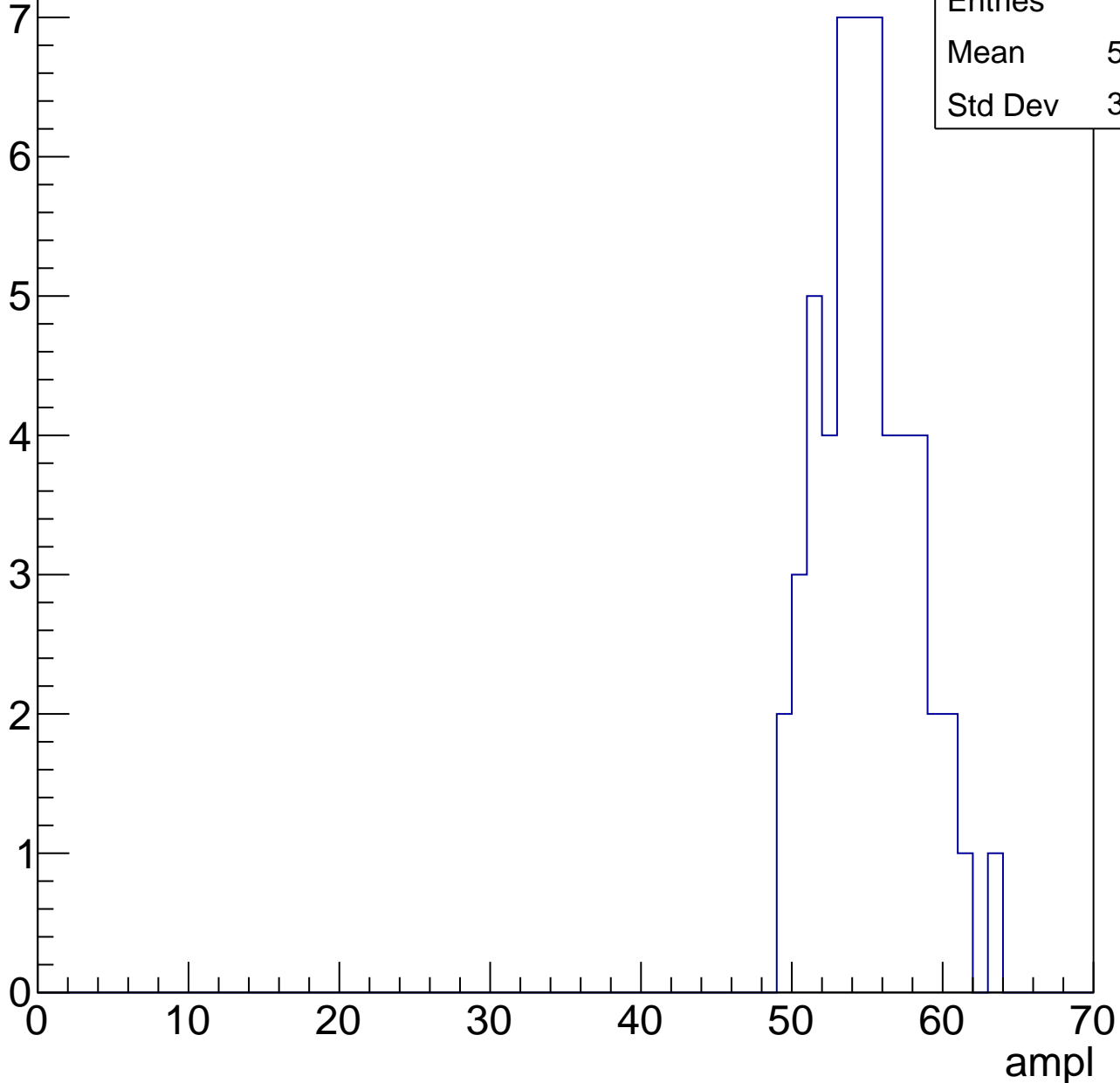


B0L000S, U4-ch100, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	54.55
Std Dev	3.148

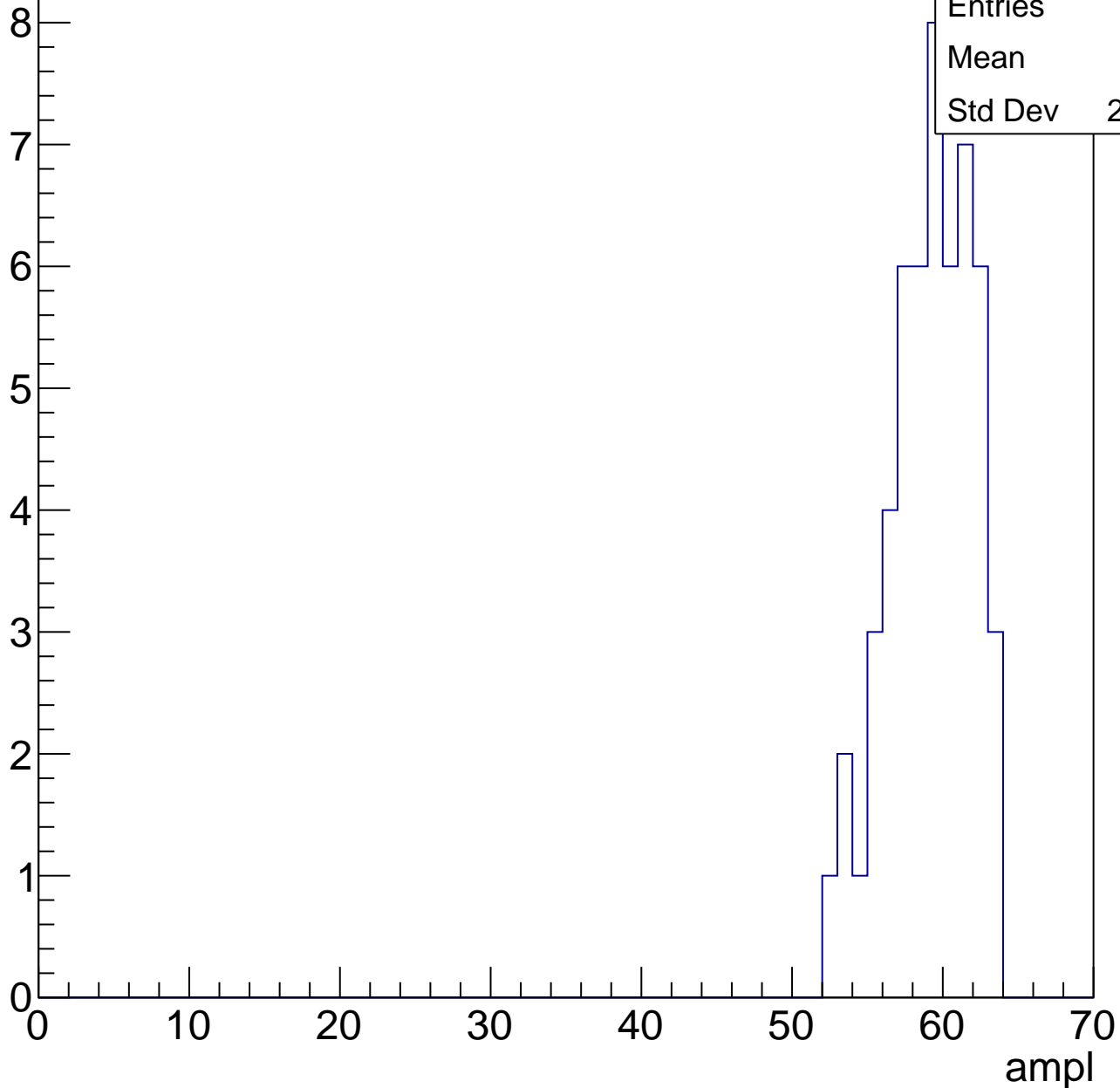


B0L000S, U4-ch100, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	58.7
Std Dev	2.717

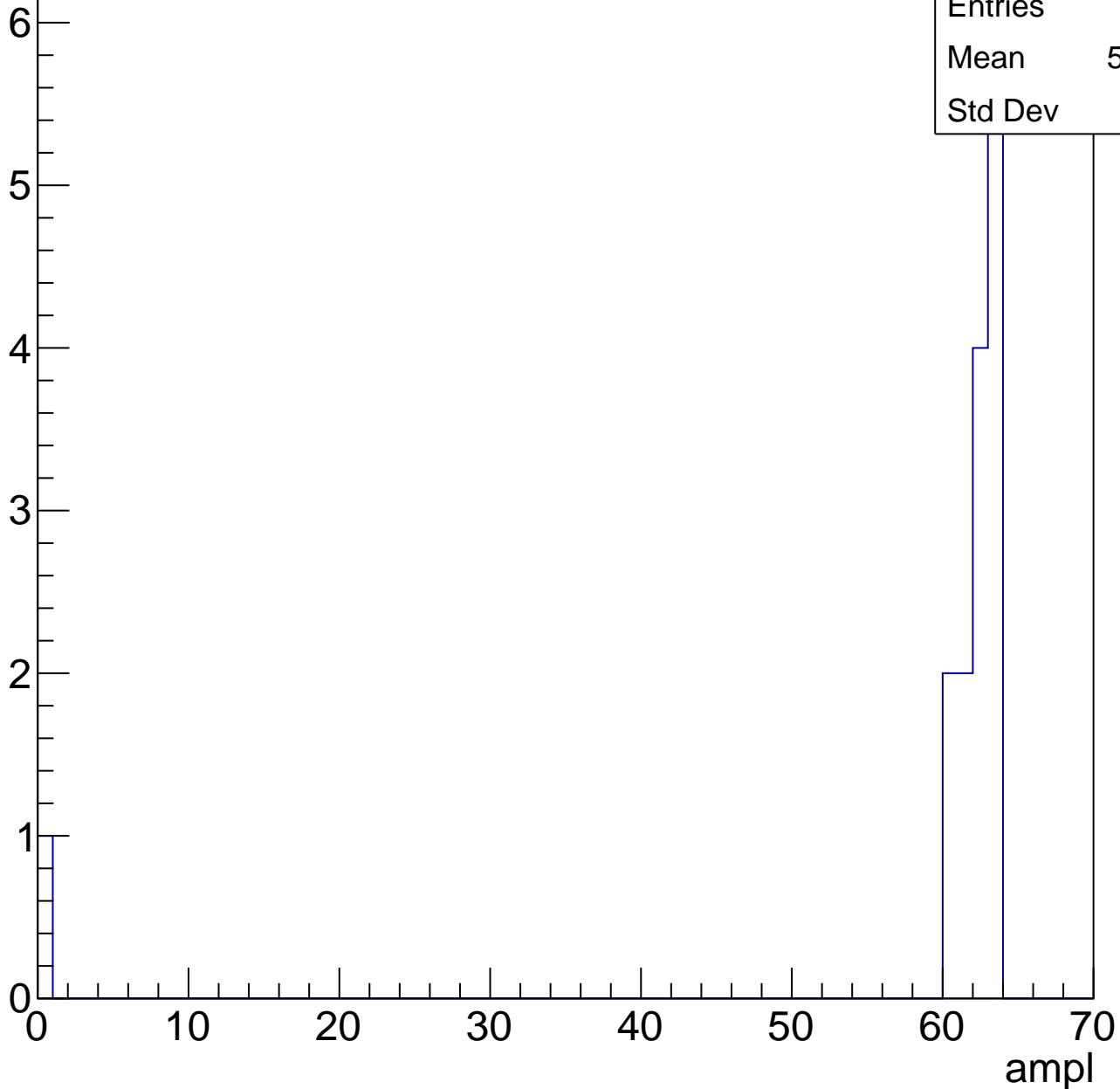


B0L000S, U4-ch100, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	15
Mean	57.87
Std Dev	15.5



B0L000S, U4-ch100, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch101, adc0

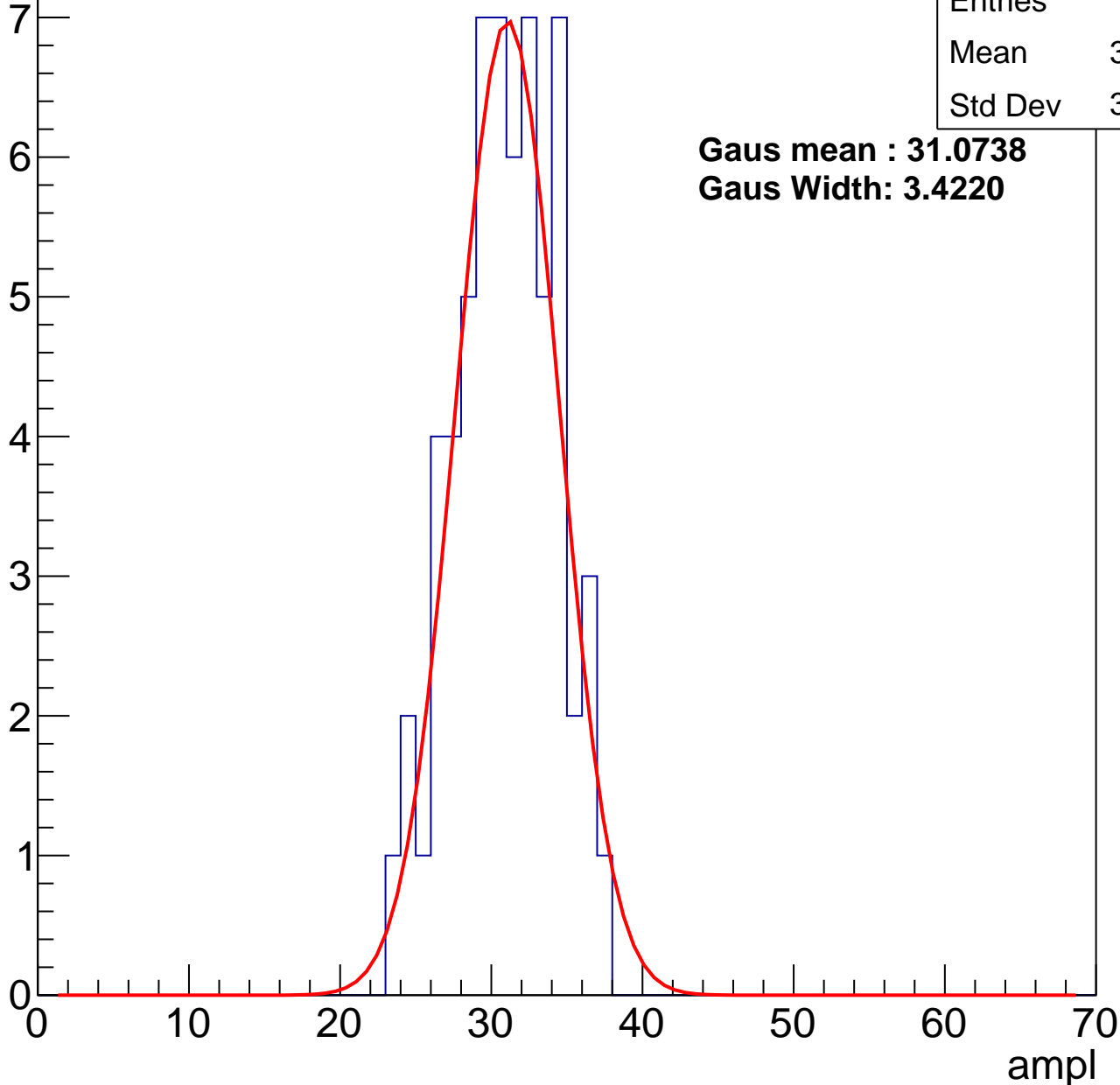
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.47
Std Dev	3.256

Gaus mean : 31.0738

Gaus Width: 3.4220



B0L000S, U4-ch101, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	73
Mean	37.68
Std Dev	3.561

Gaus mean : 39.0334

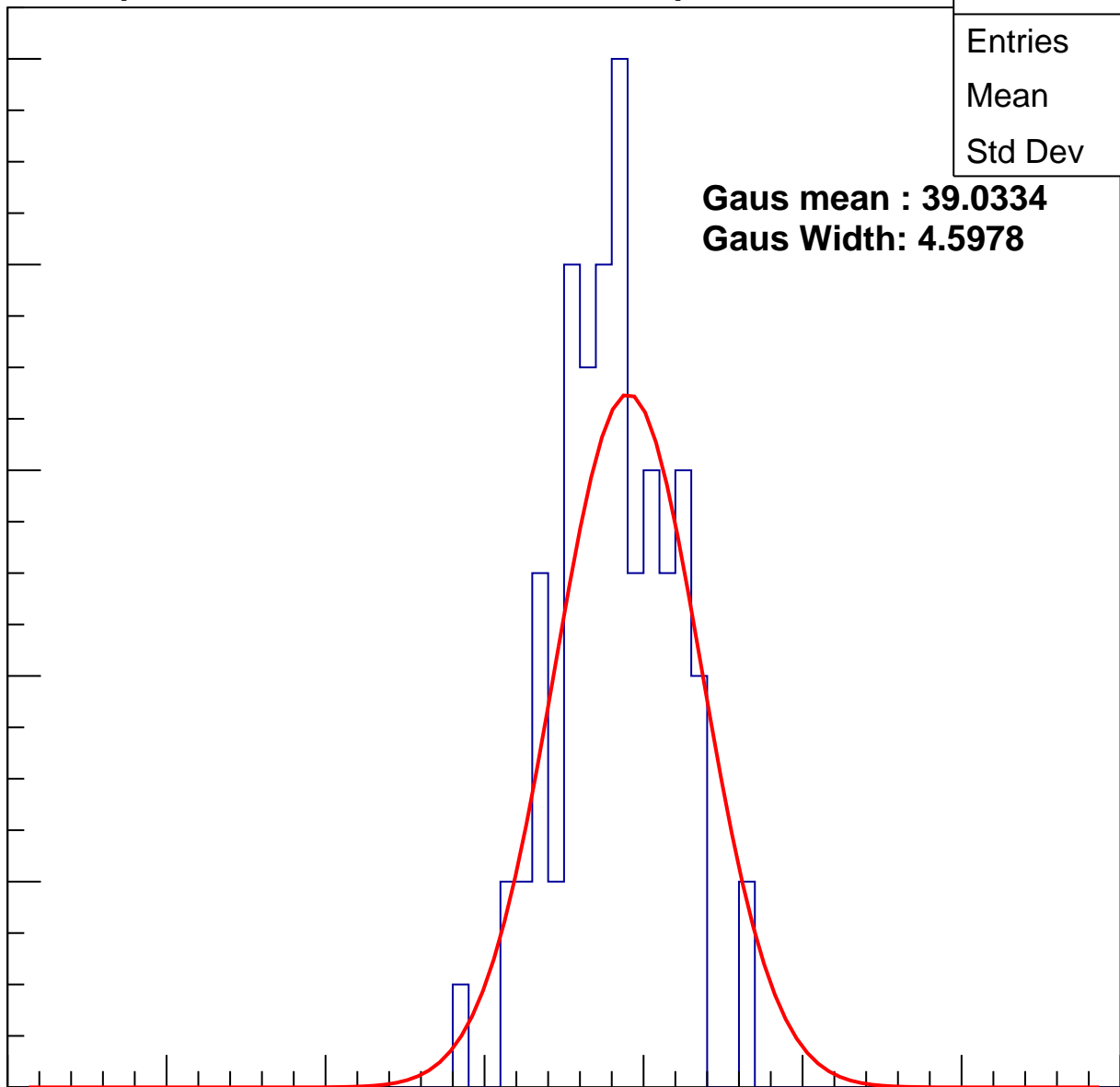
Gaus Width: 4.5978

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch101, adc2

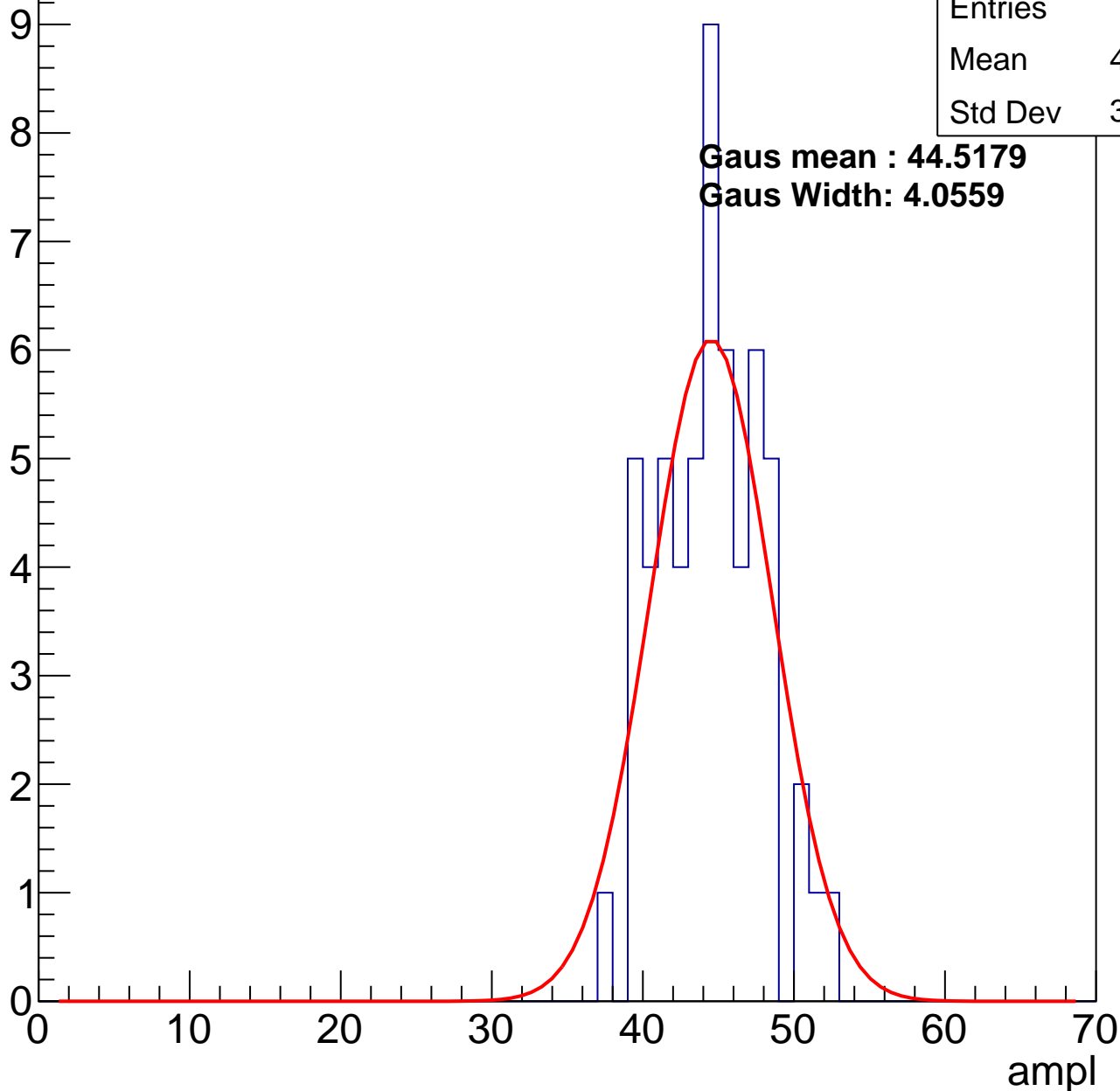
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.05
Std Dev	3.334

Gaus mean : 44.5179

Gaus Width: 4.0559

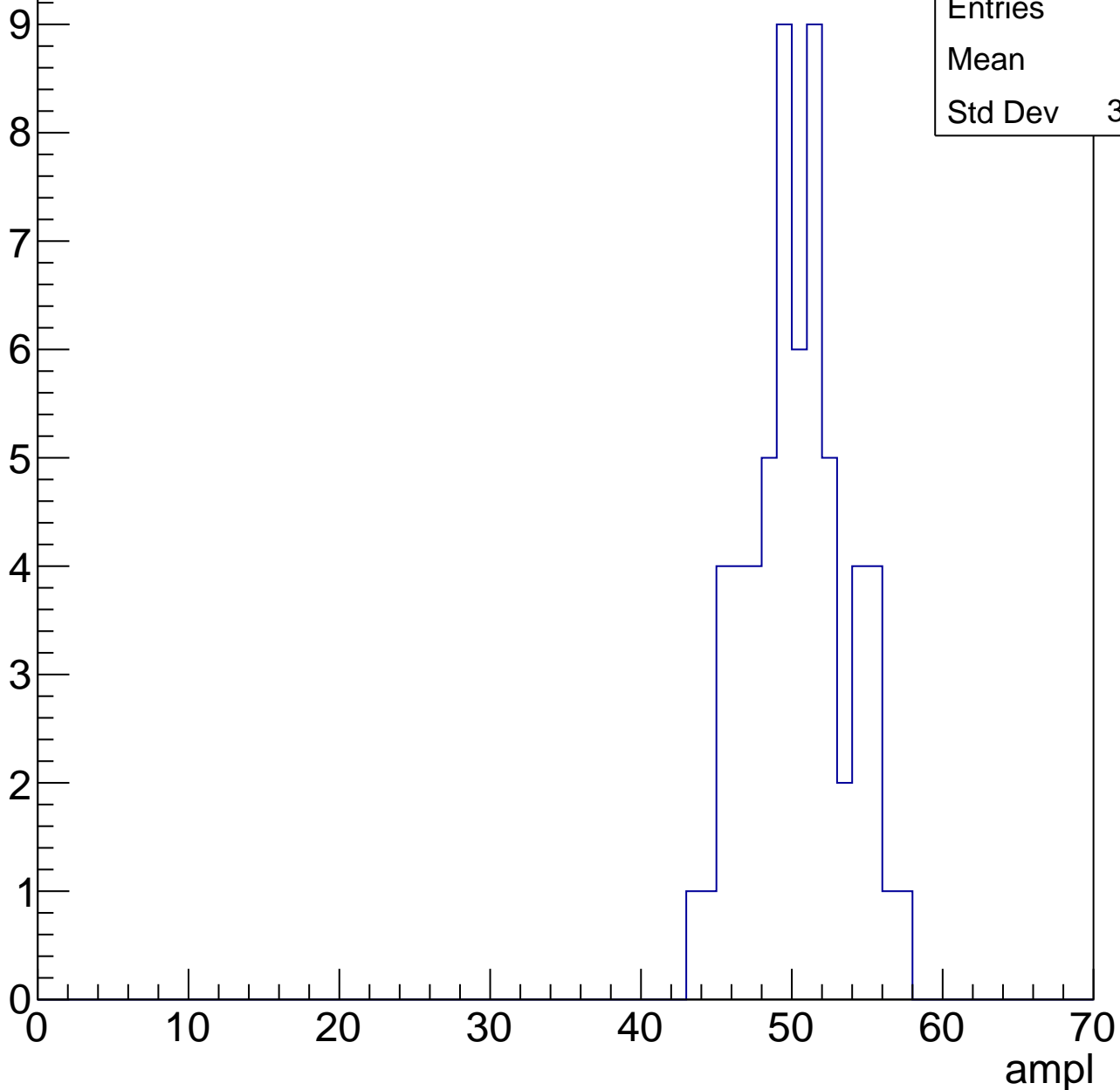


B0L000S, U4-ch101, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	49.9
Std Dev	3.187

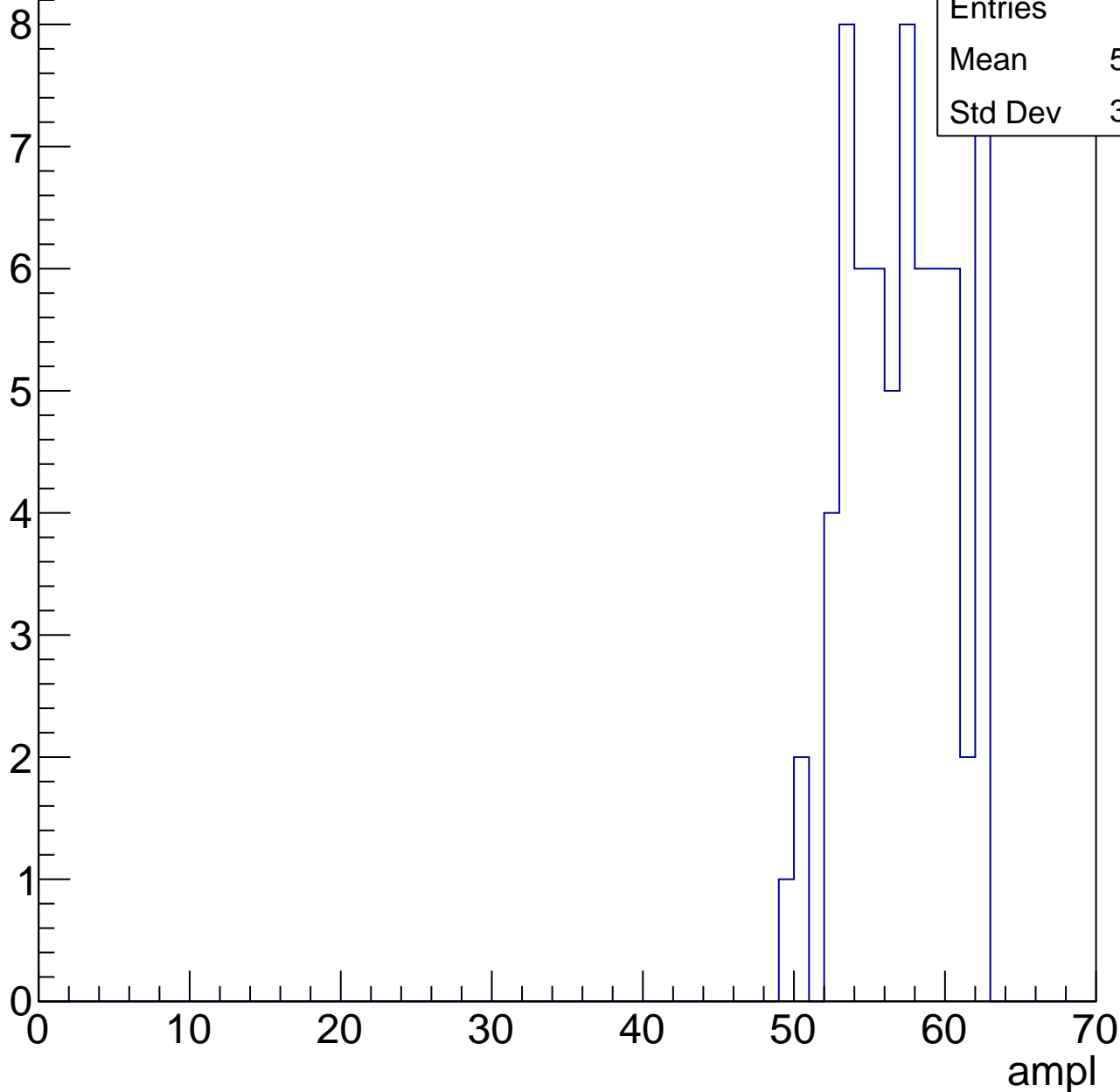


B0L000S, U4-ch101, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	56.63
Std Dev	3.386

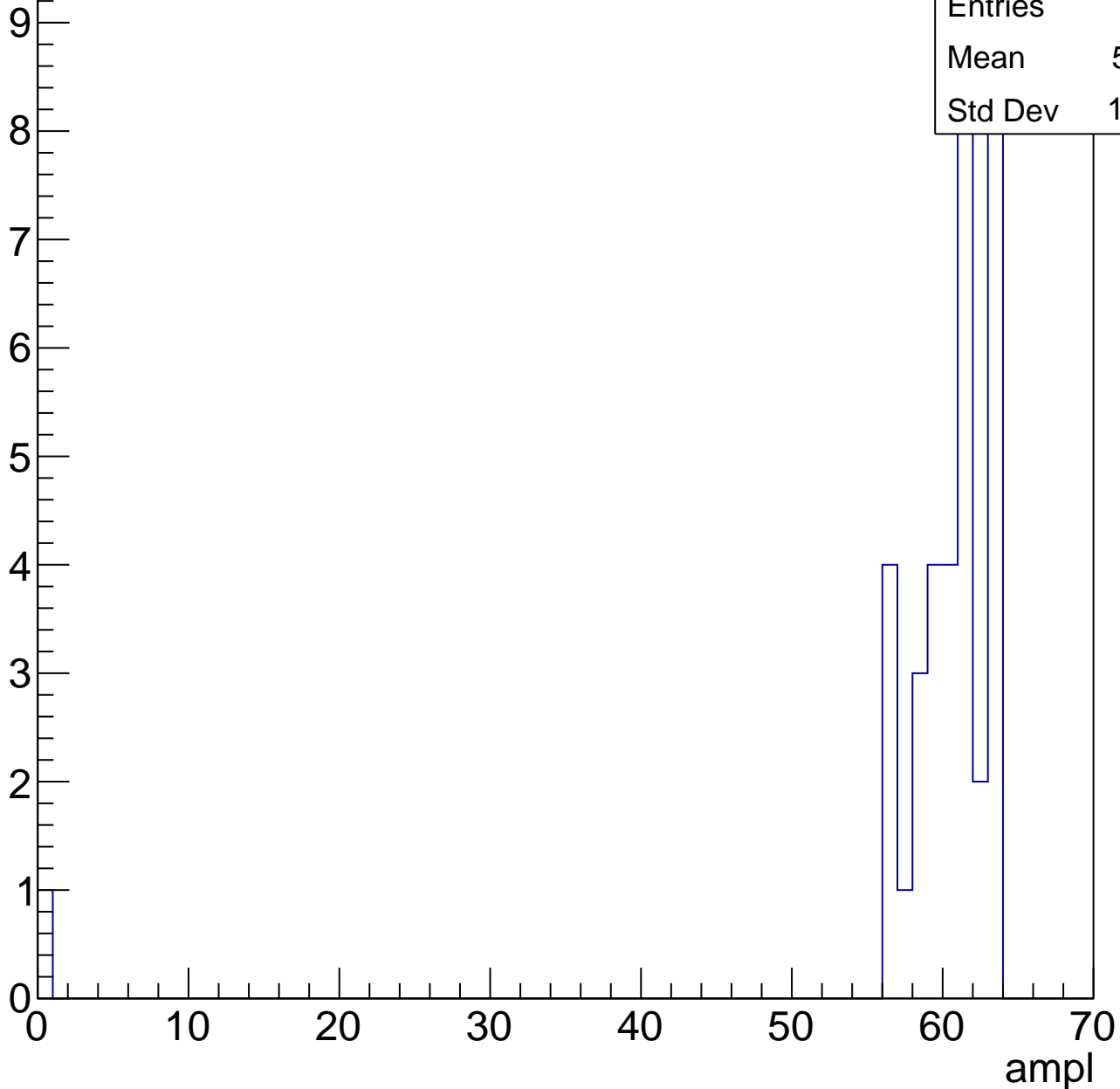


B0L000S, U4-ch101, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

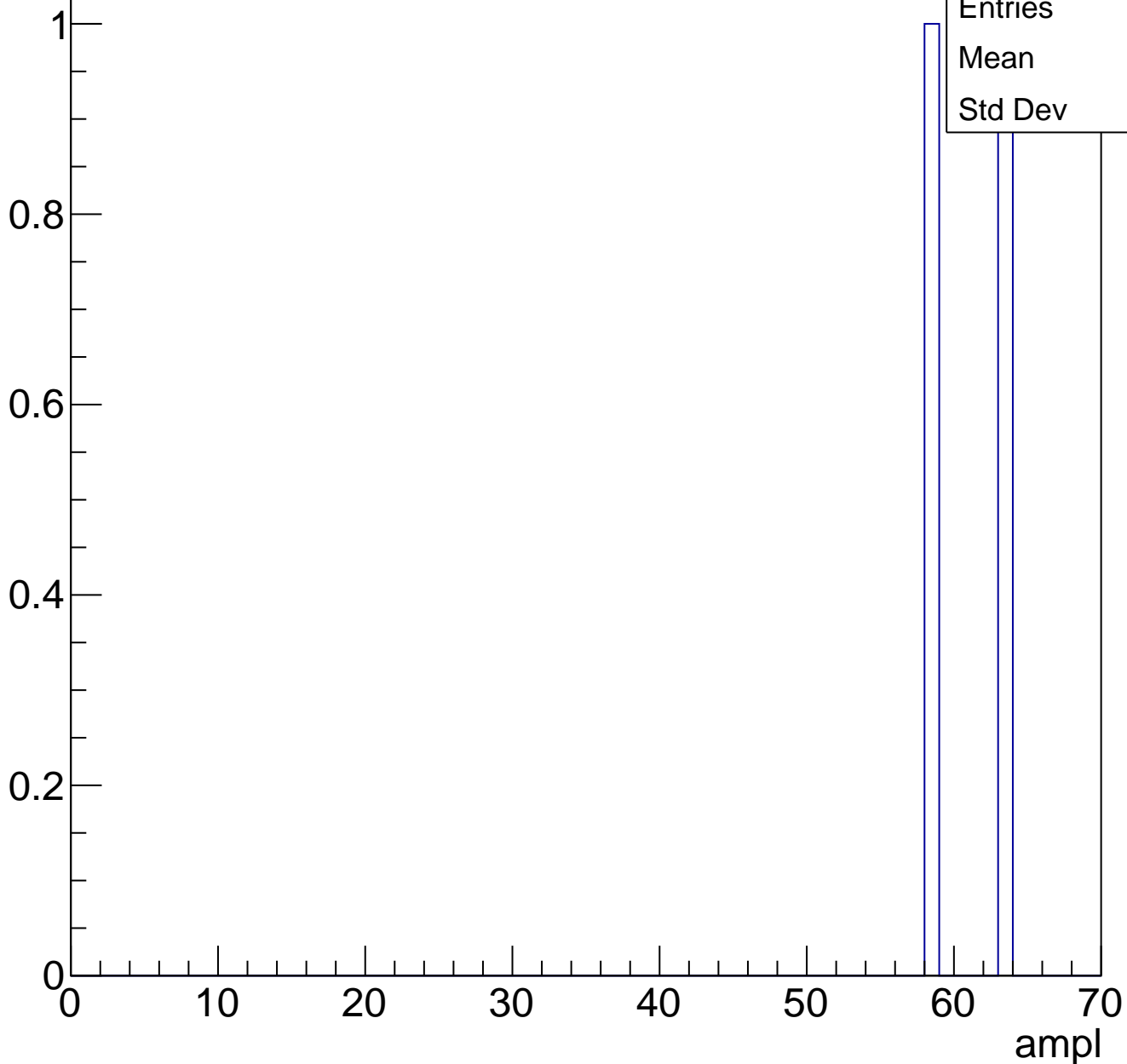
Entries	36
Mean	58.61
Std Dev	10.16



B0L000S, U4-ch101, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch101, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch102, adc0

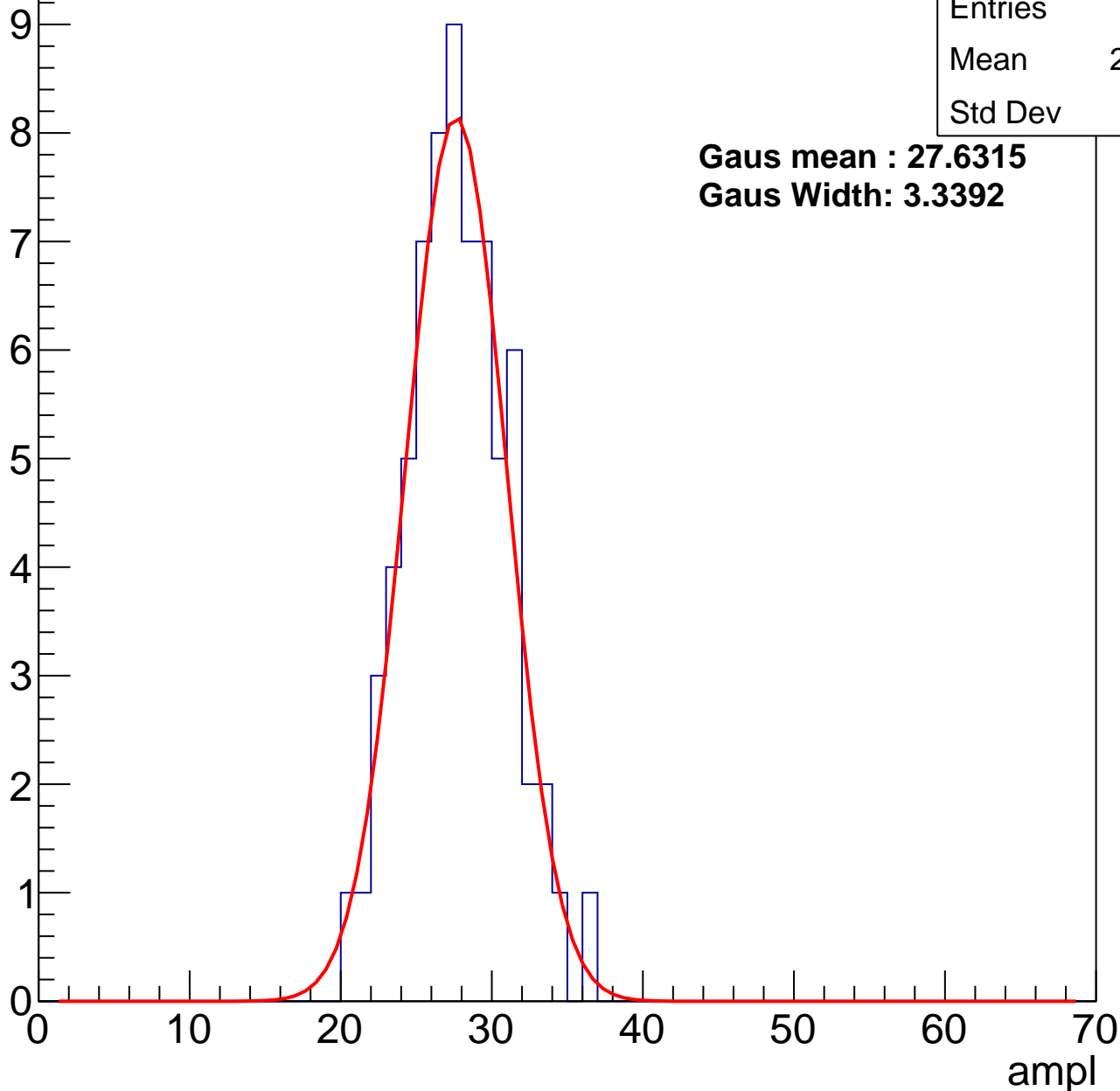
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	27.25
Std Dev	3.25

Gaus mean : 27.6315

Gaus Width: 3.3392



B0L000S, U4-ch102, adc1

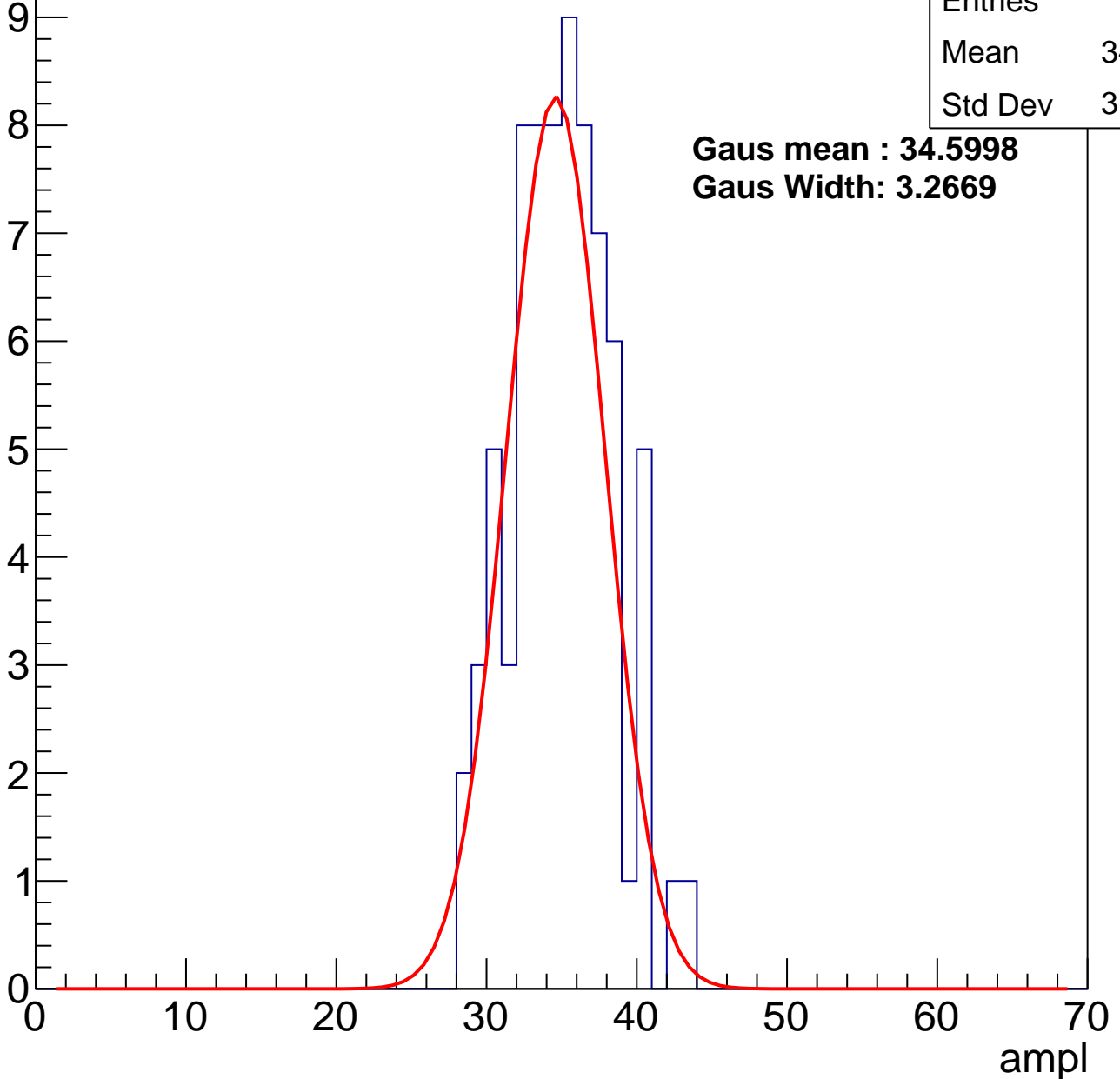
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	34.56
Std Dev	3.308

Gaus mean : 34.5998

Gaus Width: 3.2669



B0L000S, U4-ch102, adc2

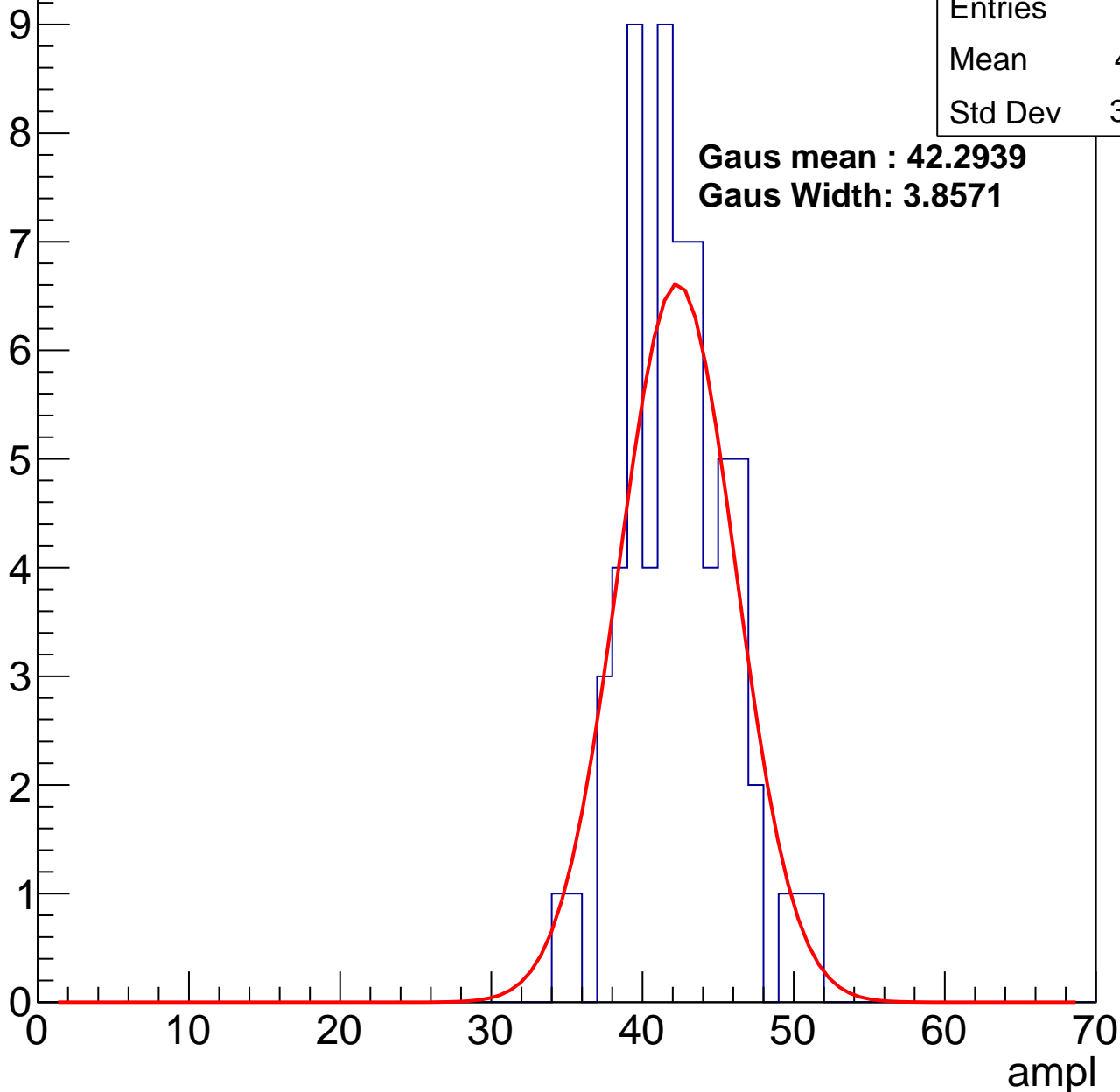
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.91
Std Dev	3.422

Gaus mean : 42.2939

Gaus Width: 3.8571

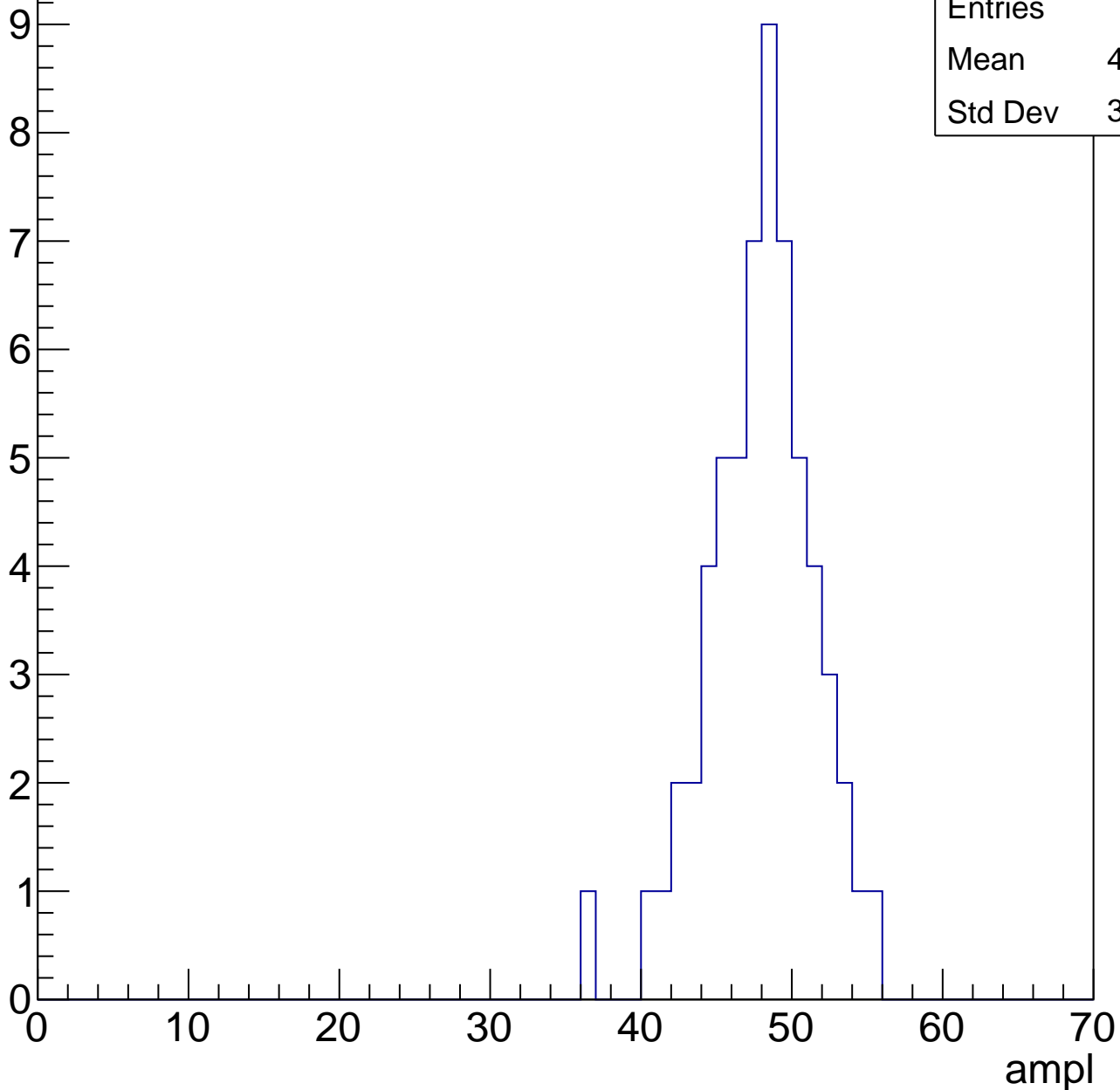


B0L000S, U4-ch102, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	47.45
Std Dev	3.519

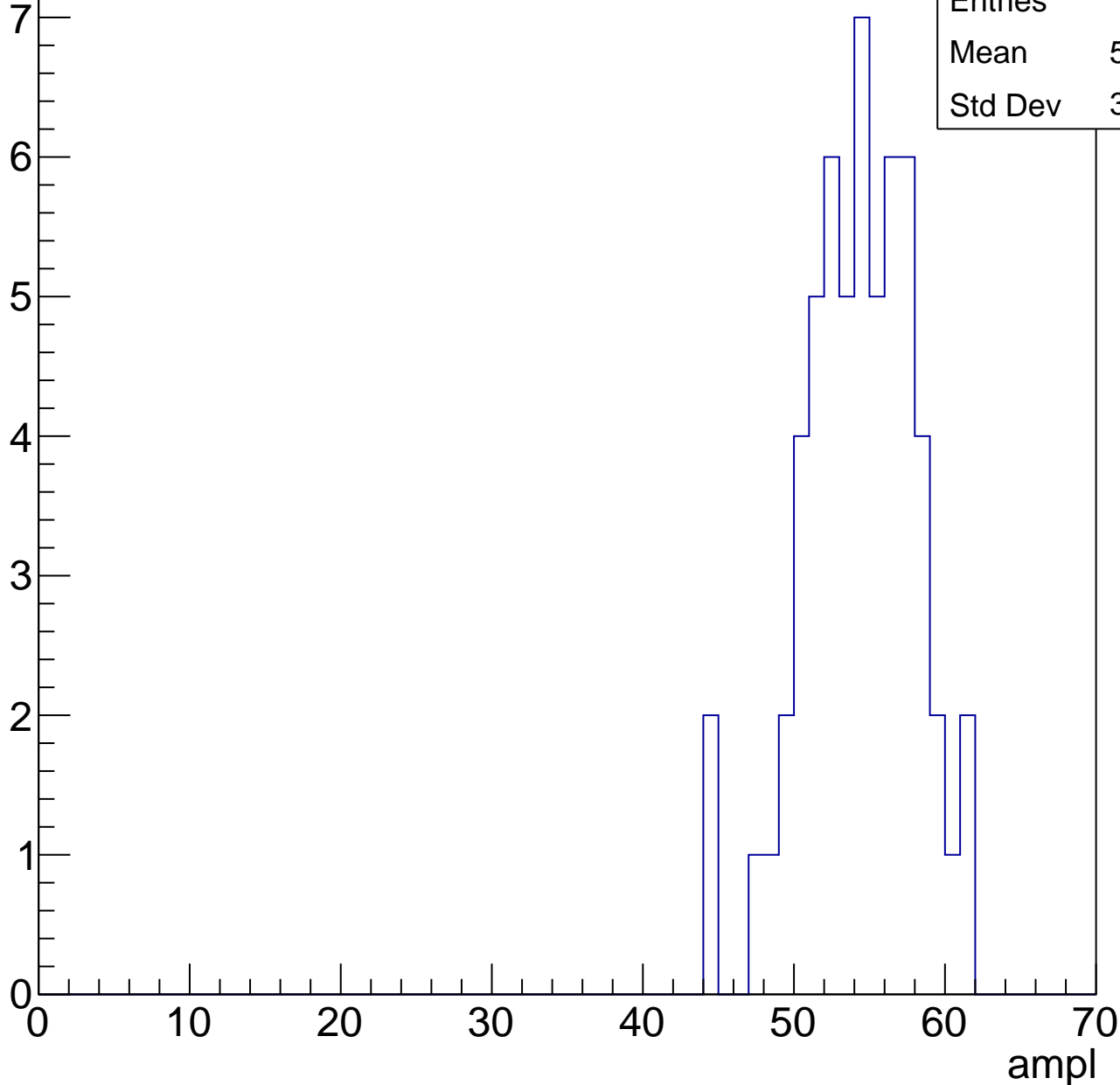


B0L000S, U4-ch102, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	53.83
Std Dev	3.688



B0L000S, U4-ch102, adc5

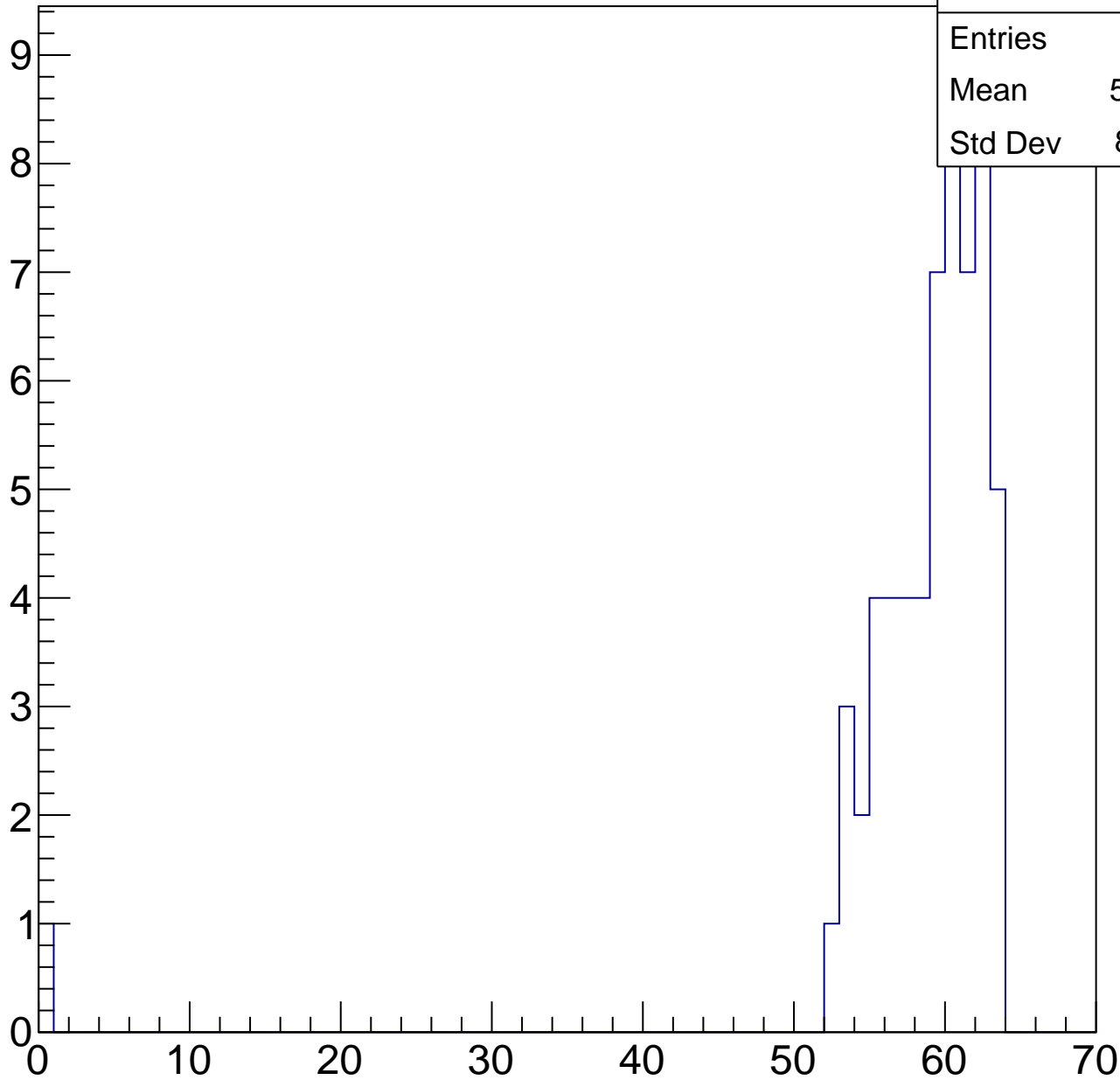
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	57.93
Std Dev	8.101

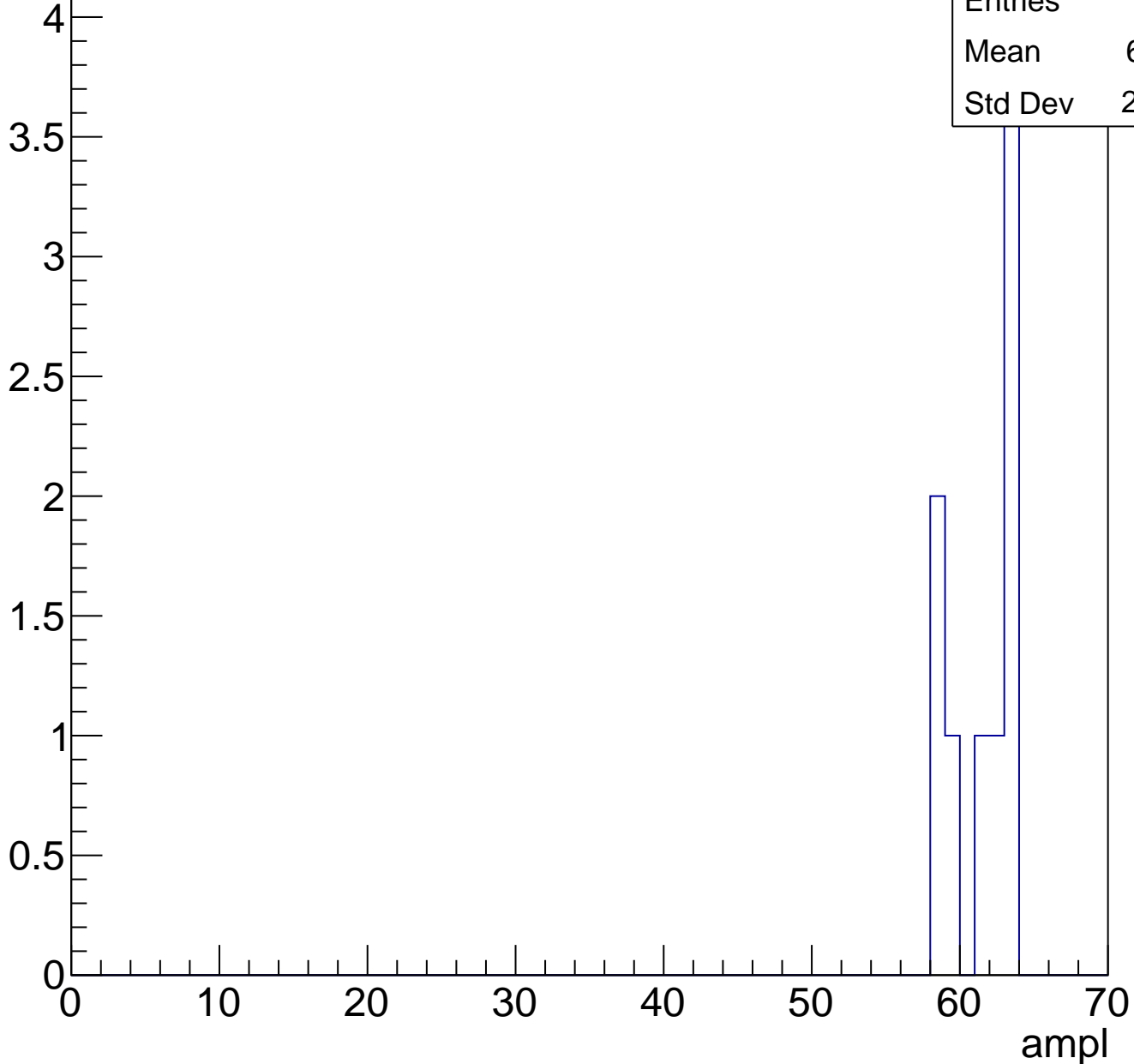
ampl



B0L000S, U4-ch102, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch102, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch103, adc0

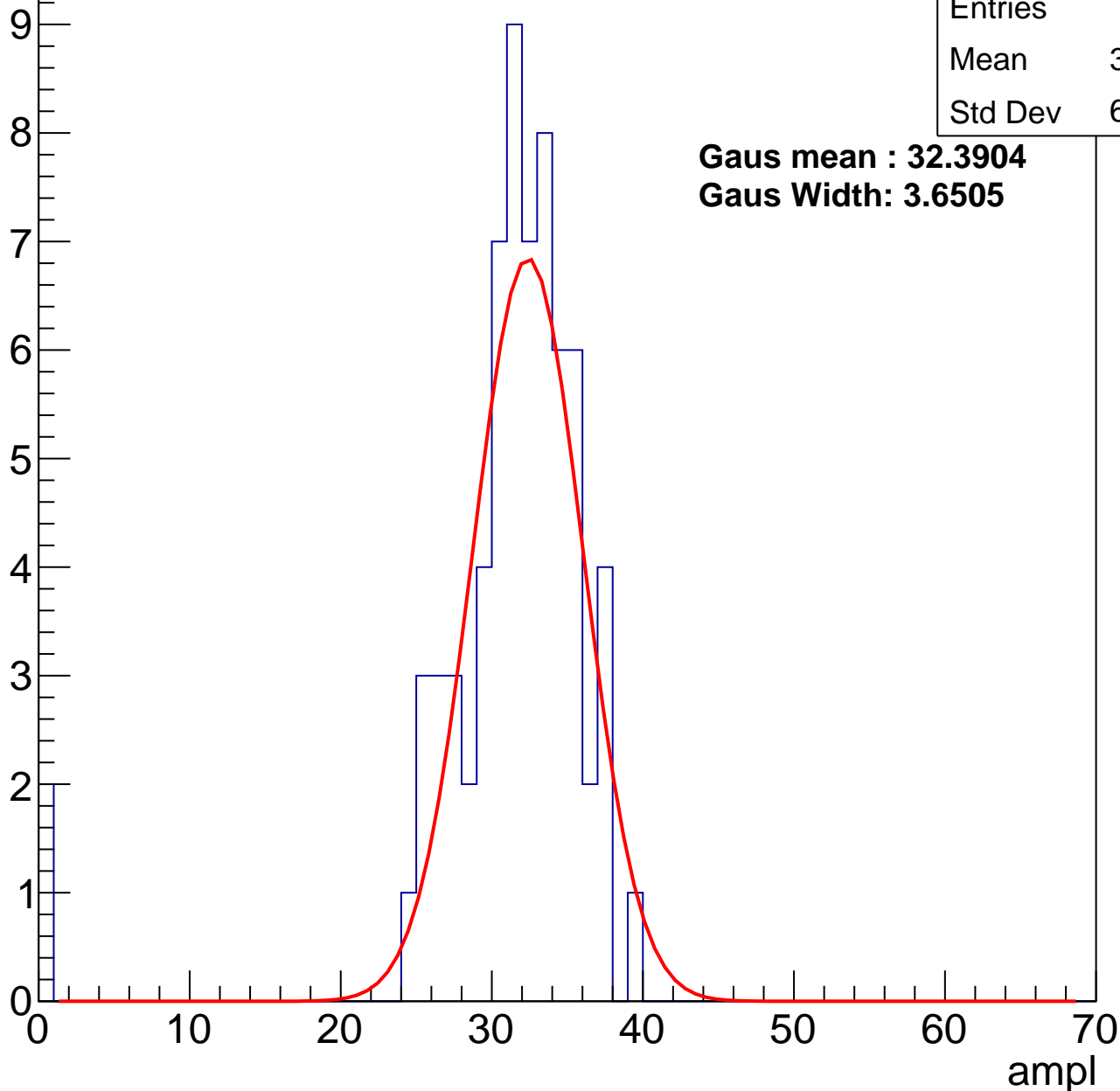
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.59
Std Dev	6.276

Gaus mean : 32.3904

Gaus Width: 3.6505



B0L000S, U4-ch103, adc1

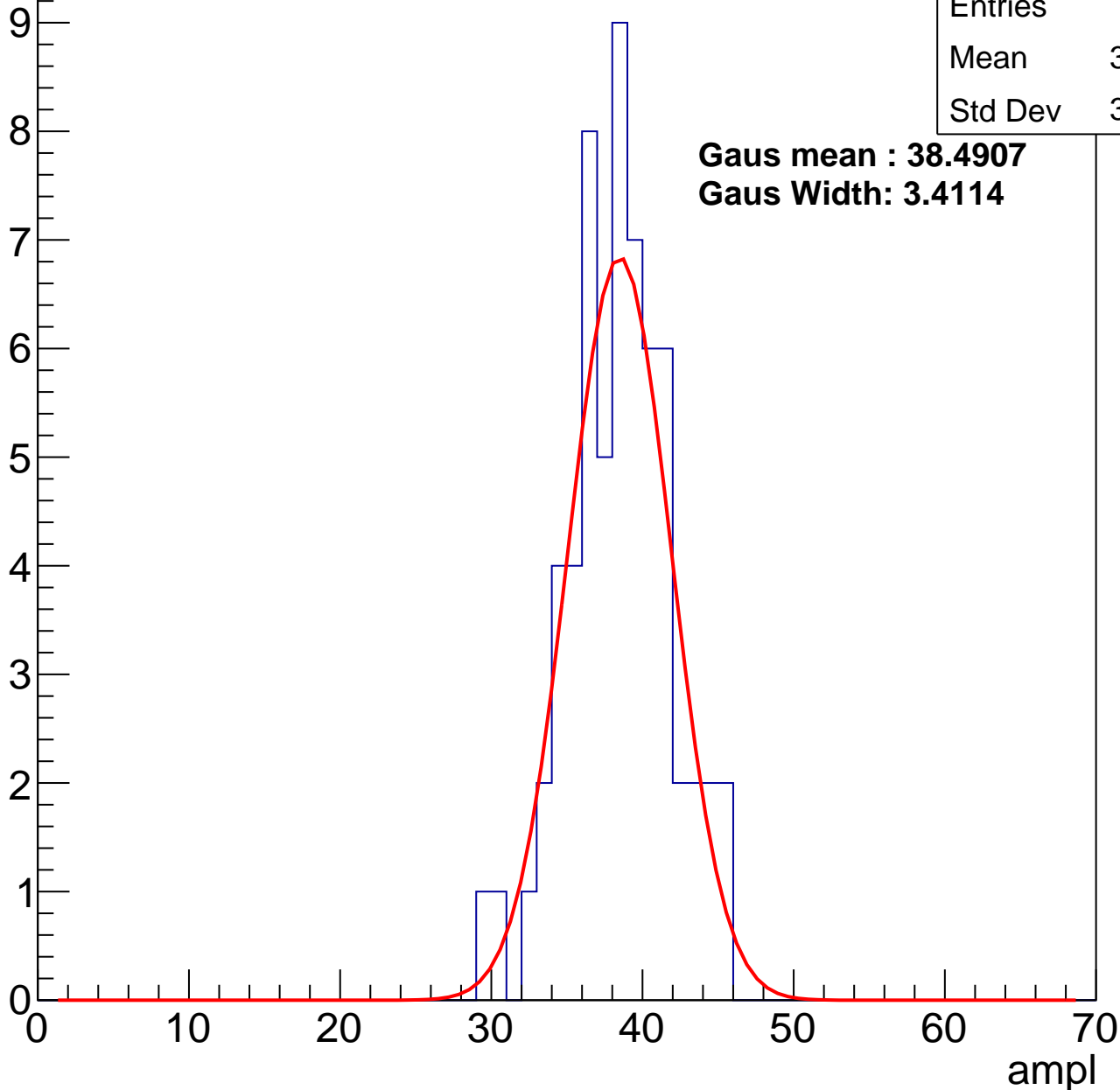
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.98
Std Dev	3.372

Gaus mean : 38.4907

Gaus Width: 3.4114



B0L000S, U4-ch103, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	44.83
Std Dev	3.258

Gaus mean : 45.3515

Gaus Width: 3.5656

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

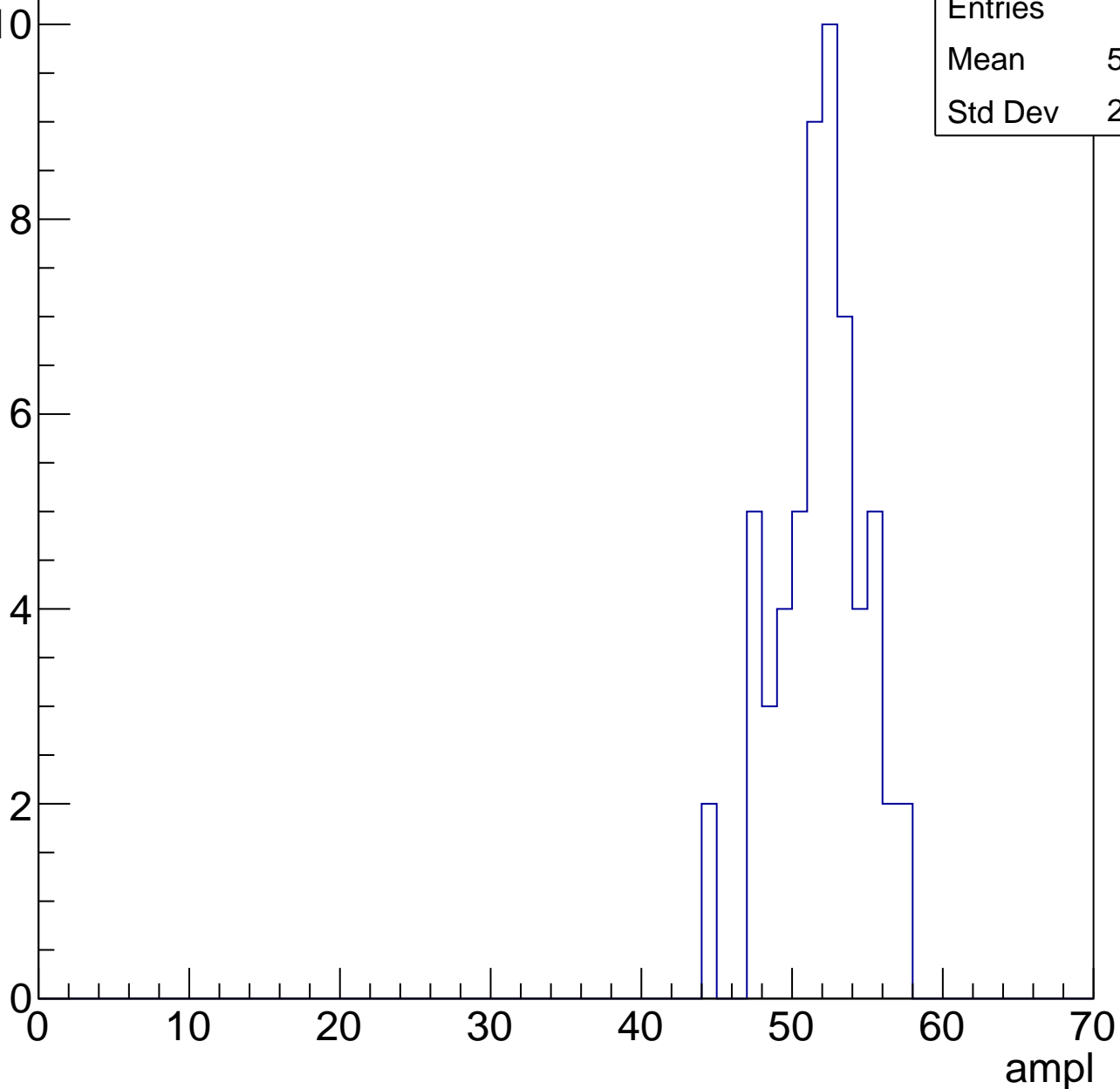
70

B0L000S, U4-ch103, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	51.38
Std Dev	2.917

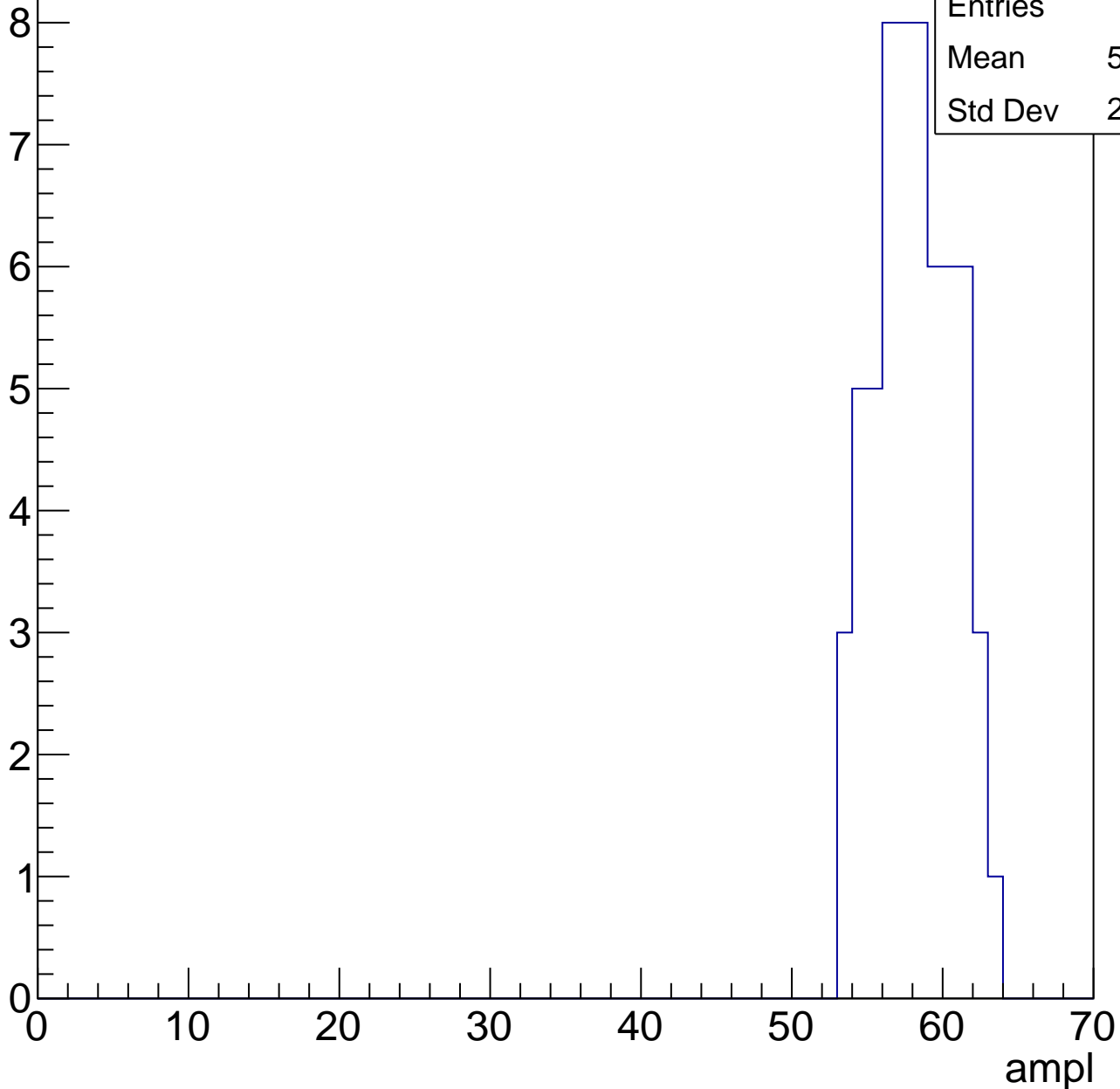


B0L000S, U4-ch103, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	57.64
Std Dev	2.569

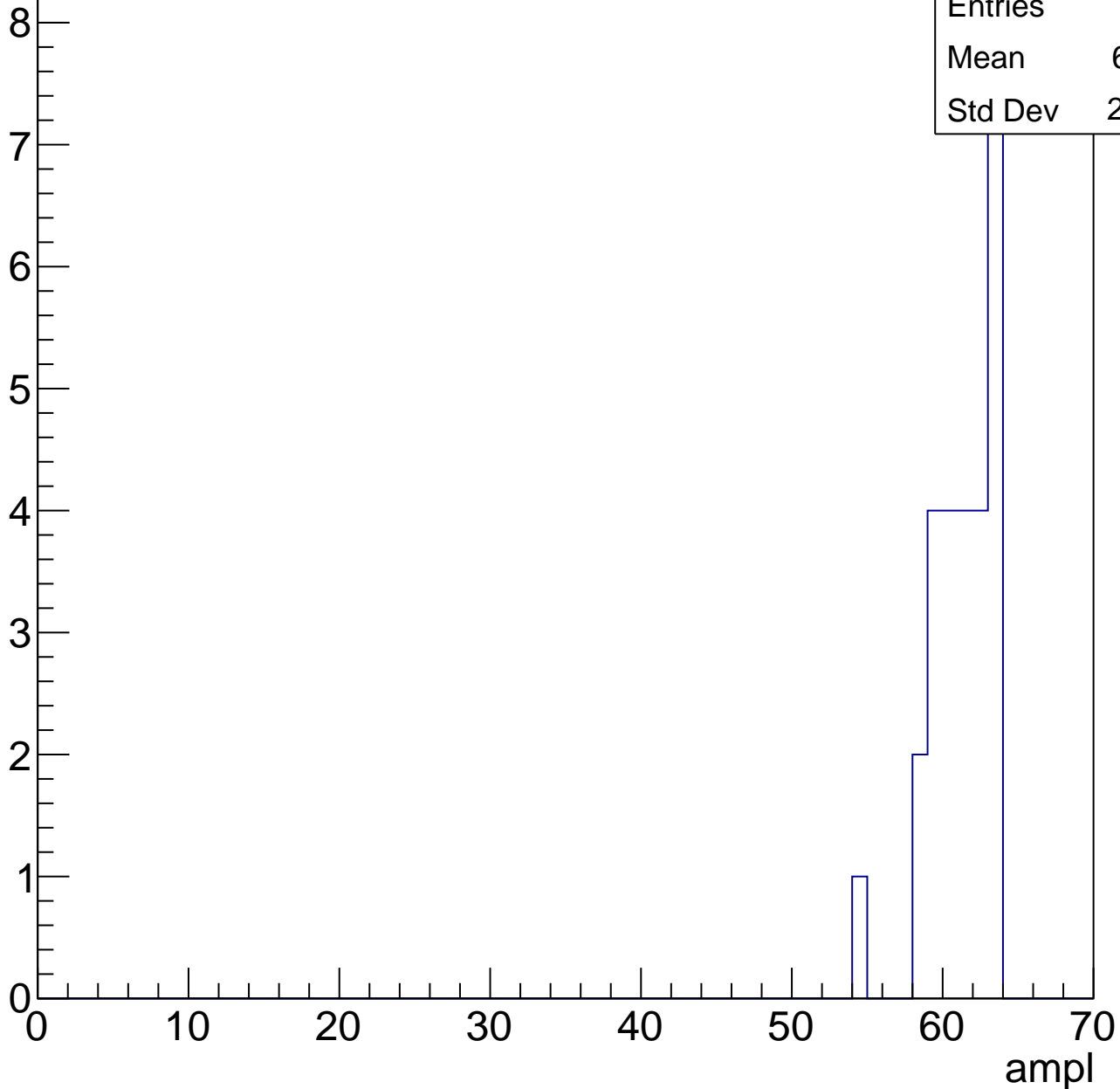


B0L000S, U4-ch103, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

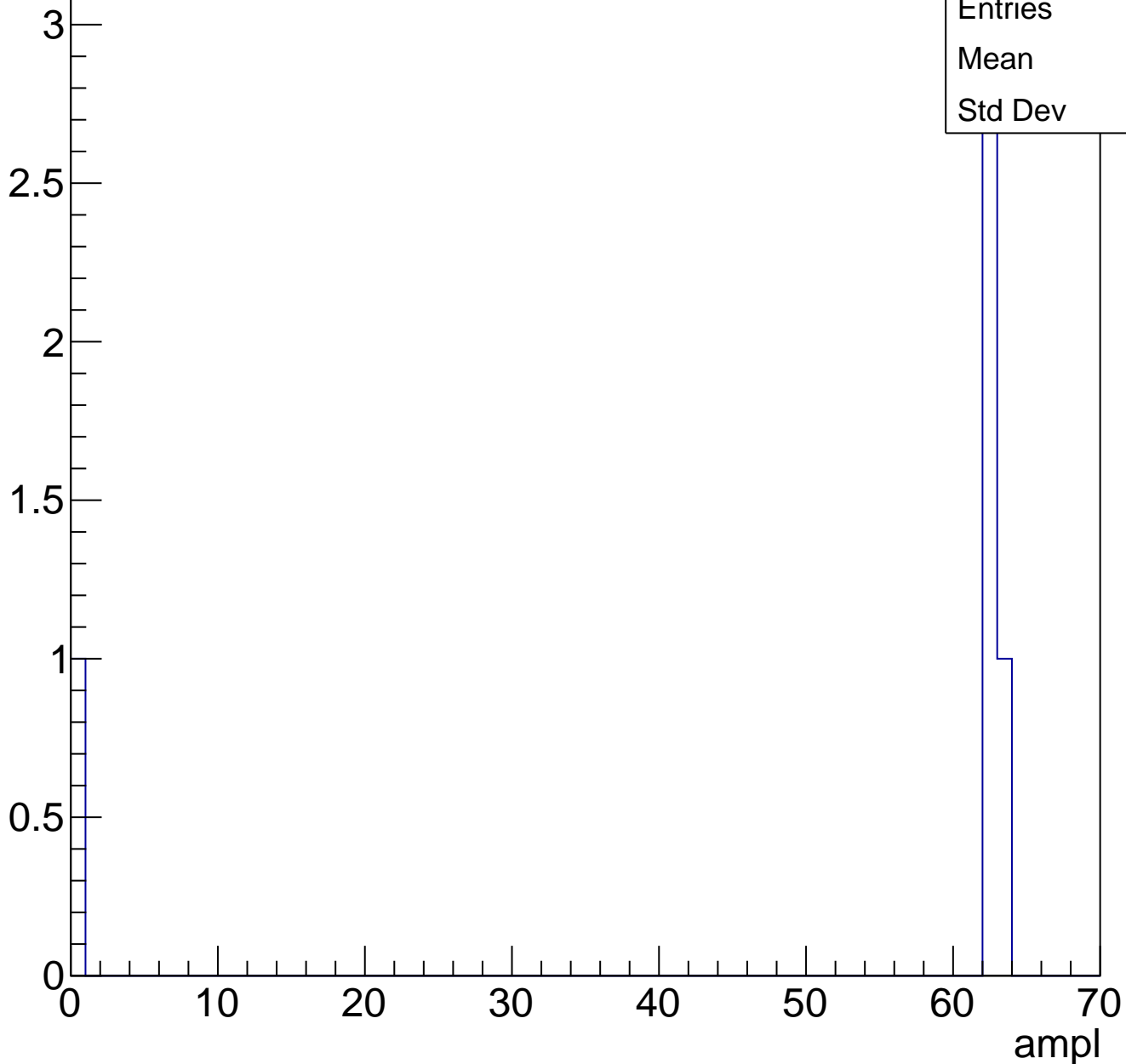
Entries	27
Mean	60.81
Std Dev	2.126



B0L000S, U4-ch103, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch103, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L000S, U4-ch104, adc0

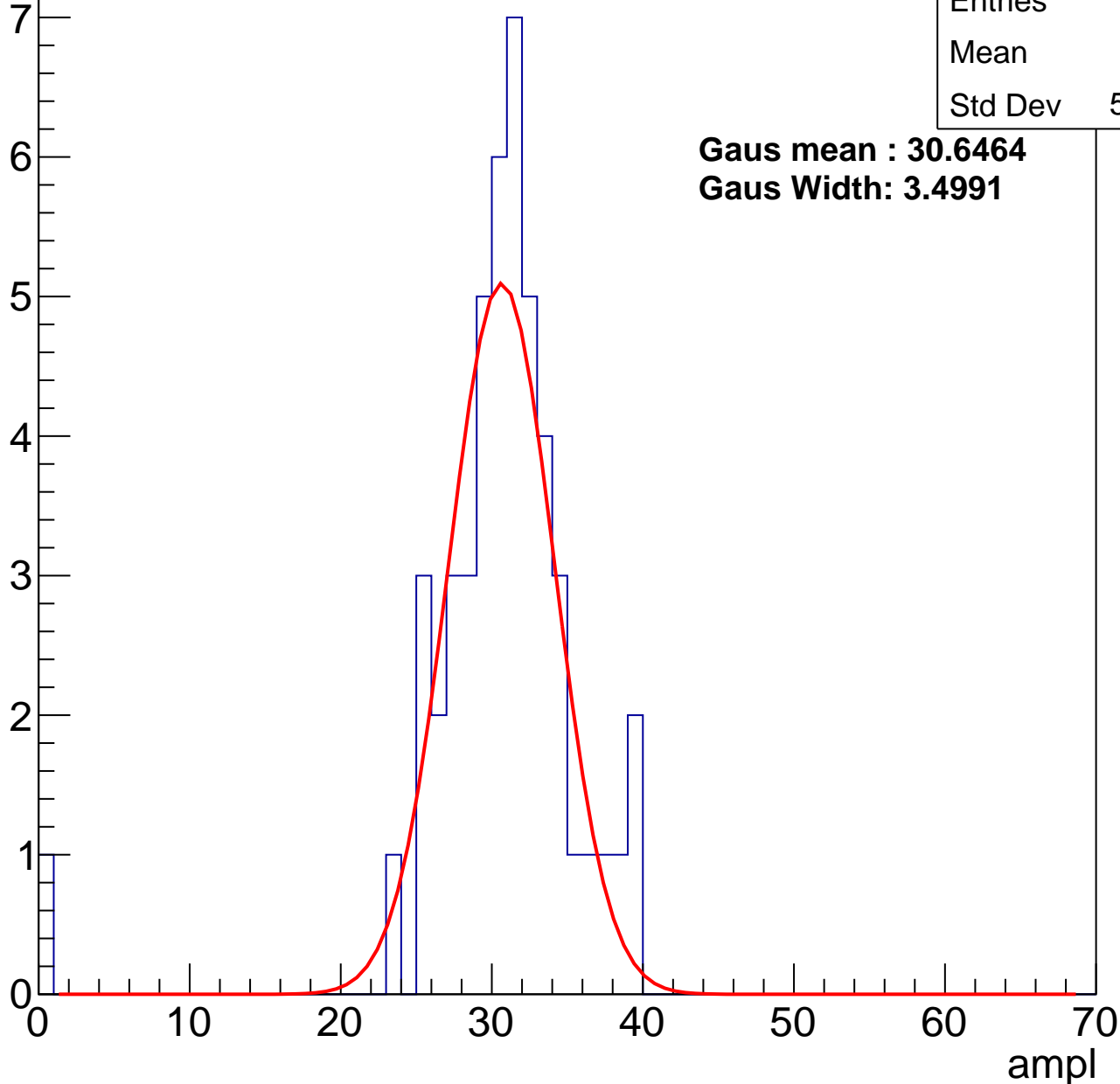
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	30.1
Std Dev	5.618

Gaus mean : 30.6464

Gaus Width: 3.4991



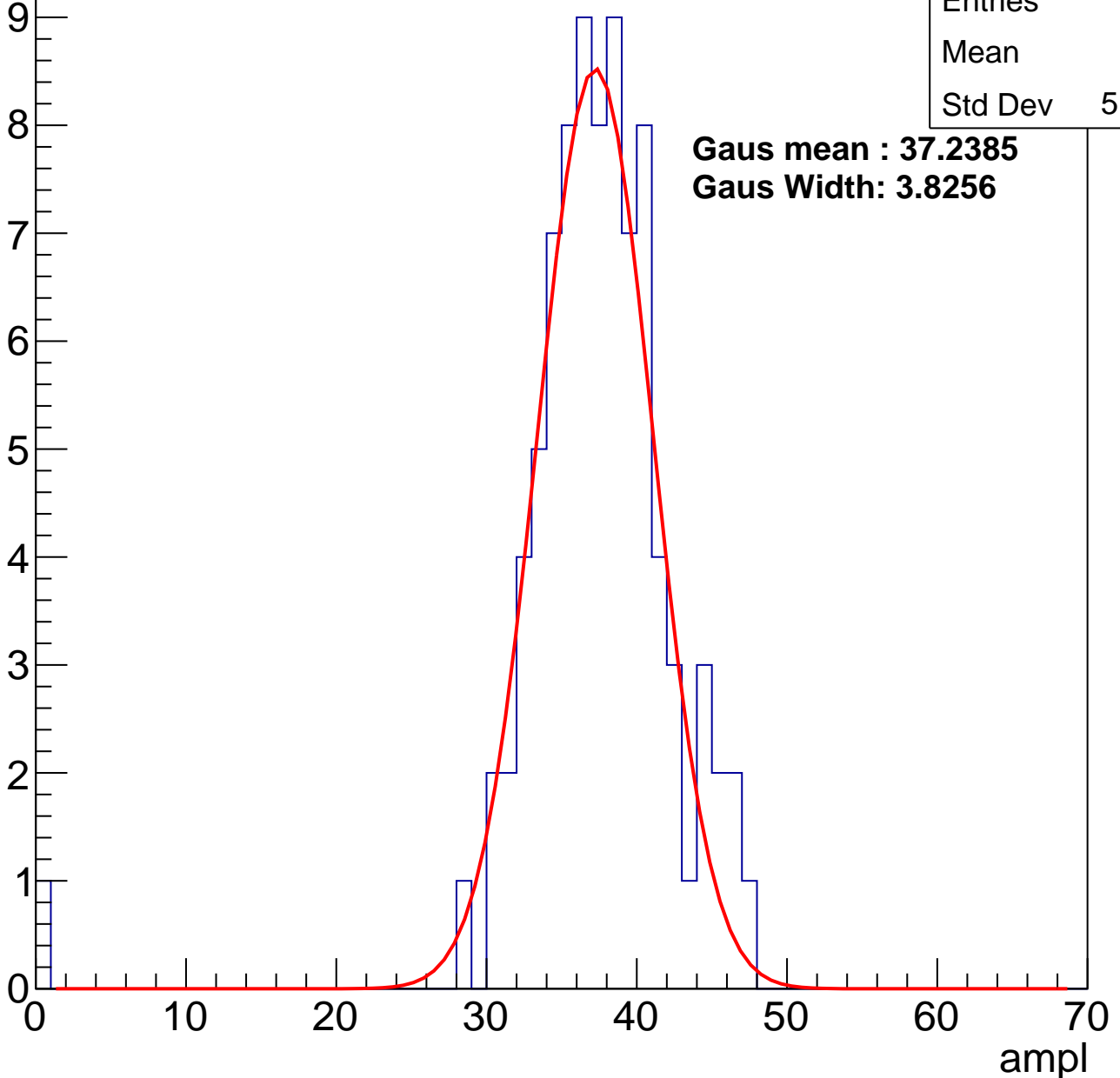
B0L000S, U4-ch104, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	87
Mean	36.9
Std Dev	5.596

Gaus mean : 37.2385
Gaus Width: 3.8256



B0L000S, U4-ch104, adc2

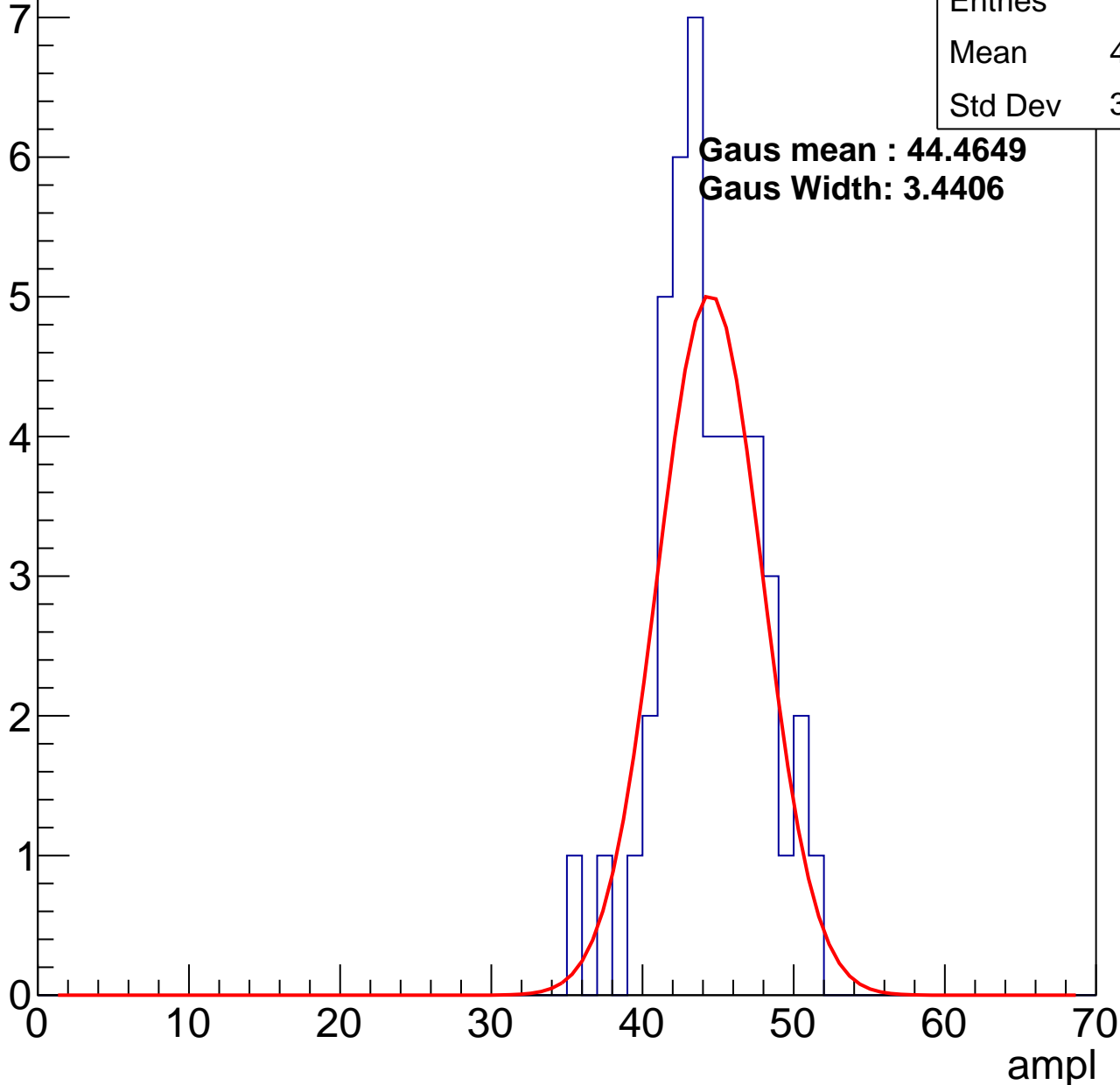
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	43.93
Std Dev	3.339

Gaus mean : 44.4649

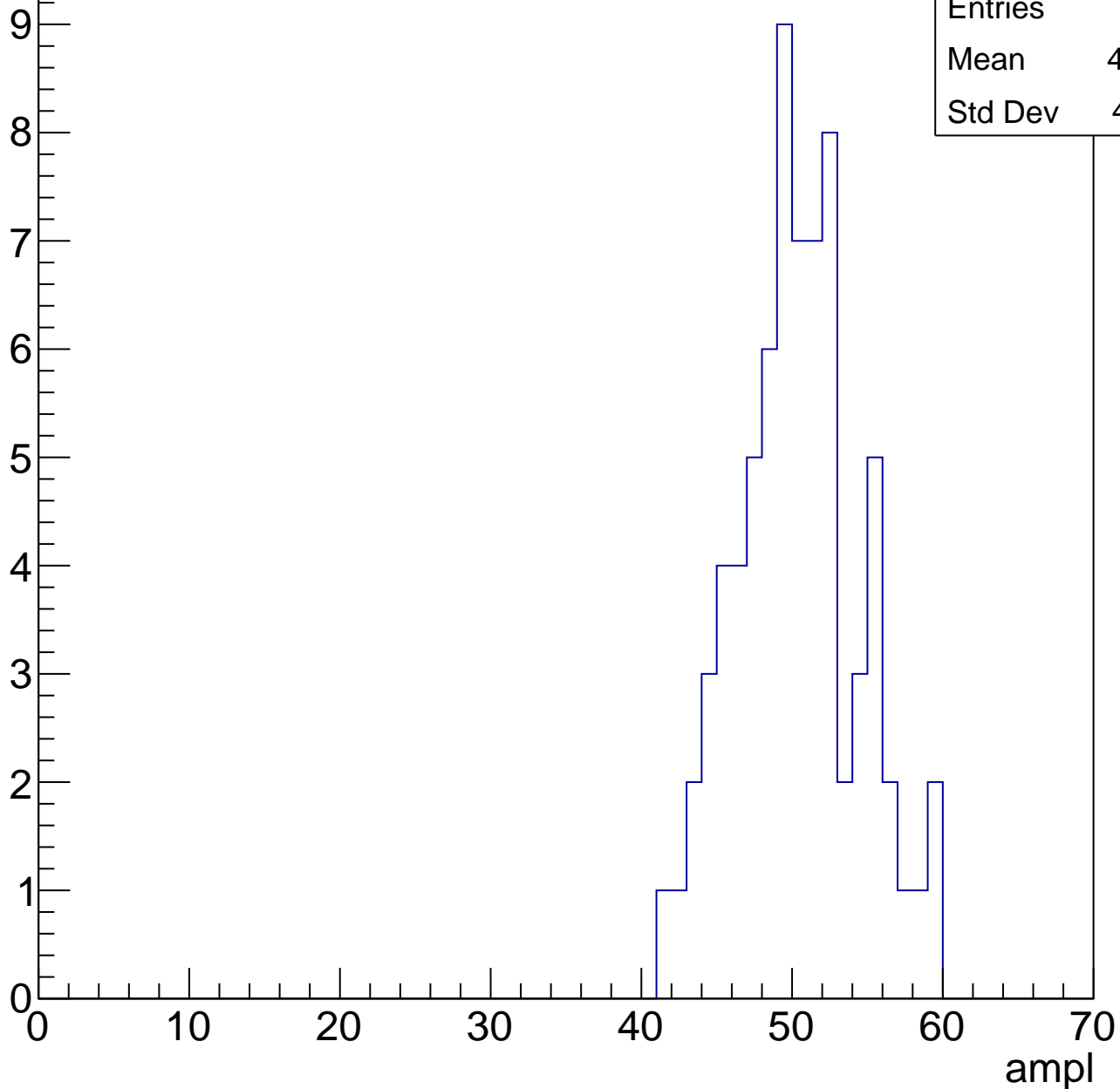
Gaus Width: 3.4406



B0L000S, U4-ch104, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

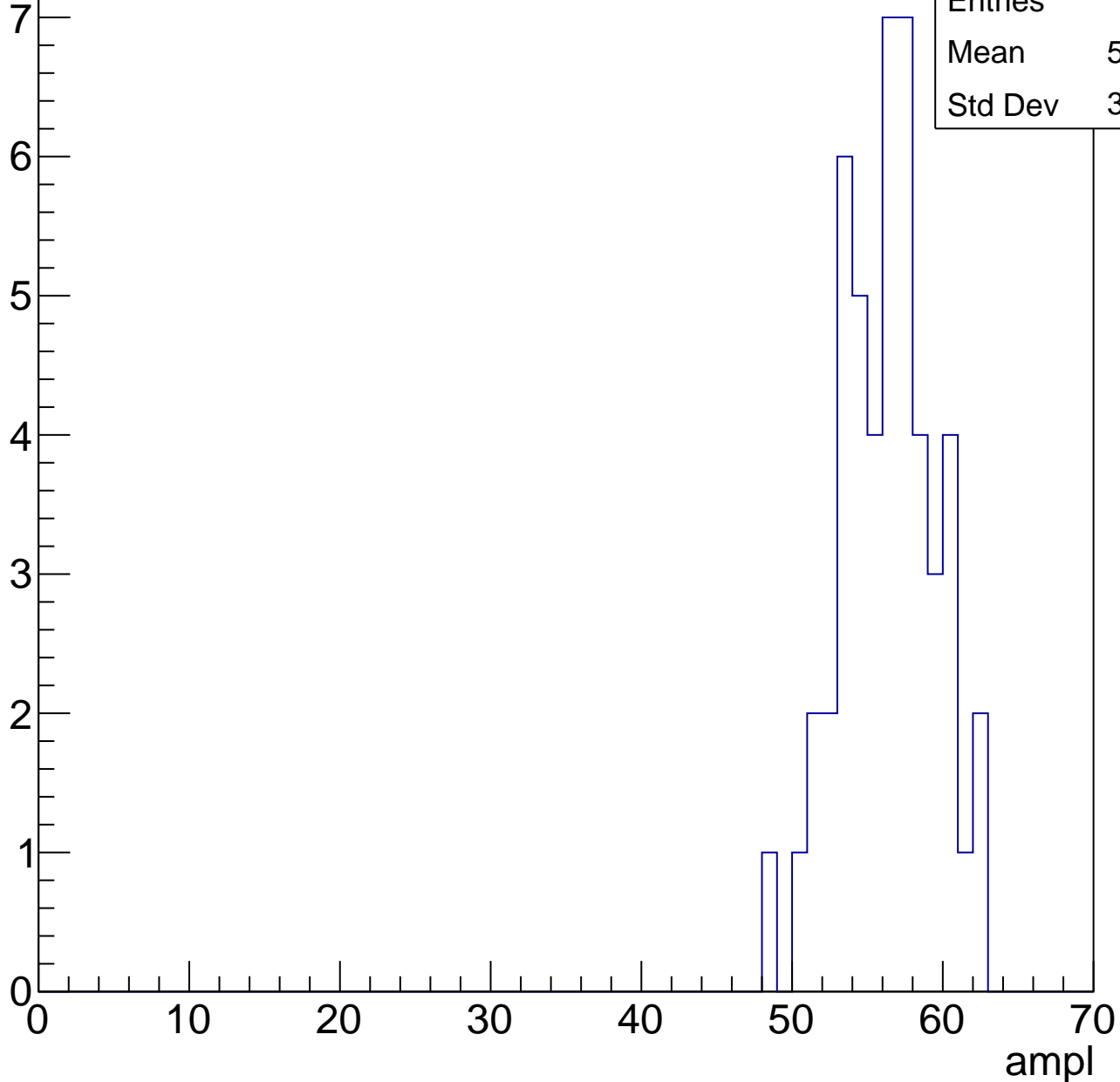


B0L000S, U4-ch104, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.86
Std Dev	3.097

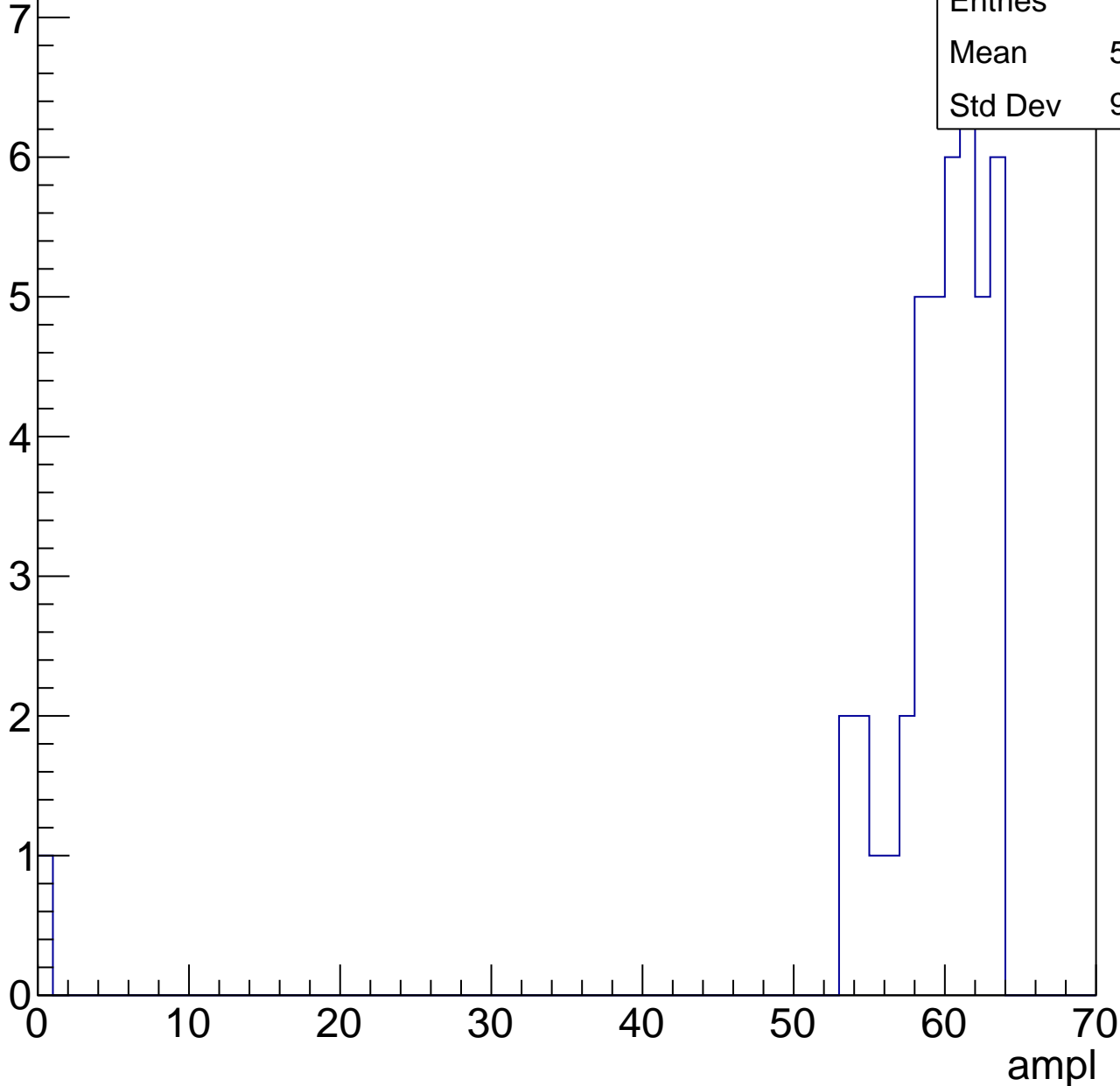


B0L000S, U4-ch104, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.12
Std Dev	9.379



B0L000S, U4-ch104, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch104, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U4-ch105, adc0

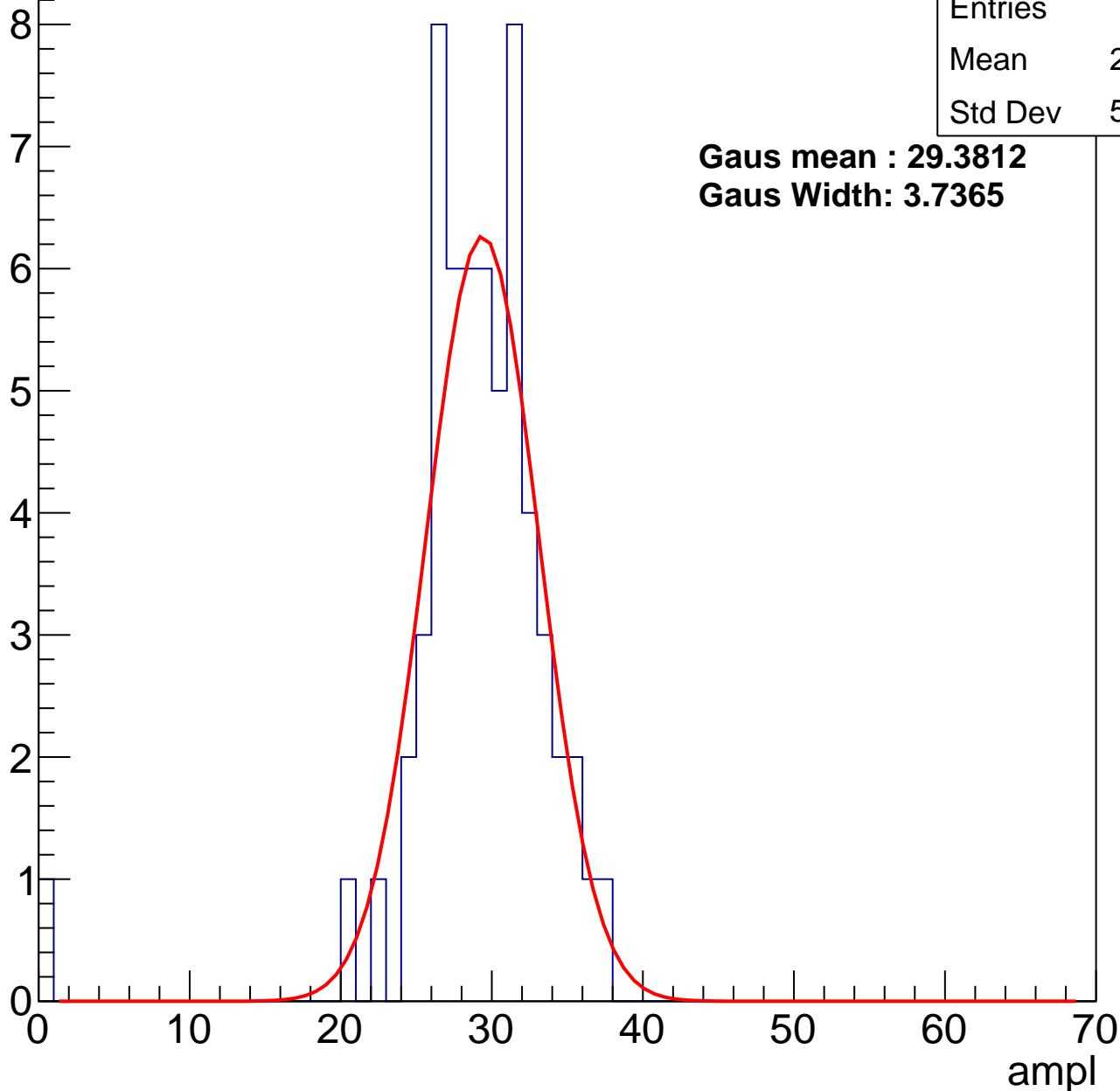
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	28.55
Std Dev	5.025

Gaus mean : 29.3812

Gaus Width: 3.7365



B0L000S, U4-ch105, adc1

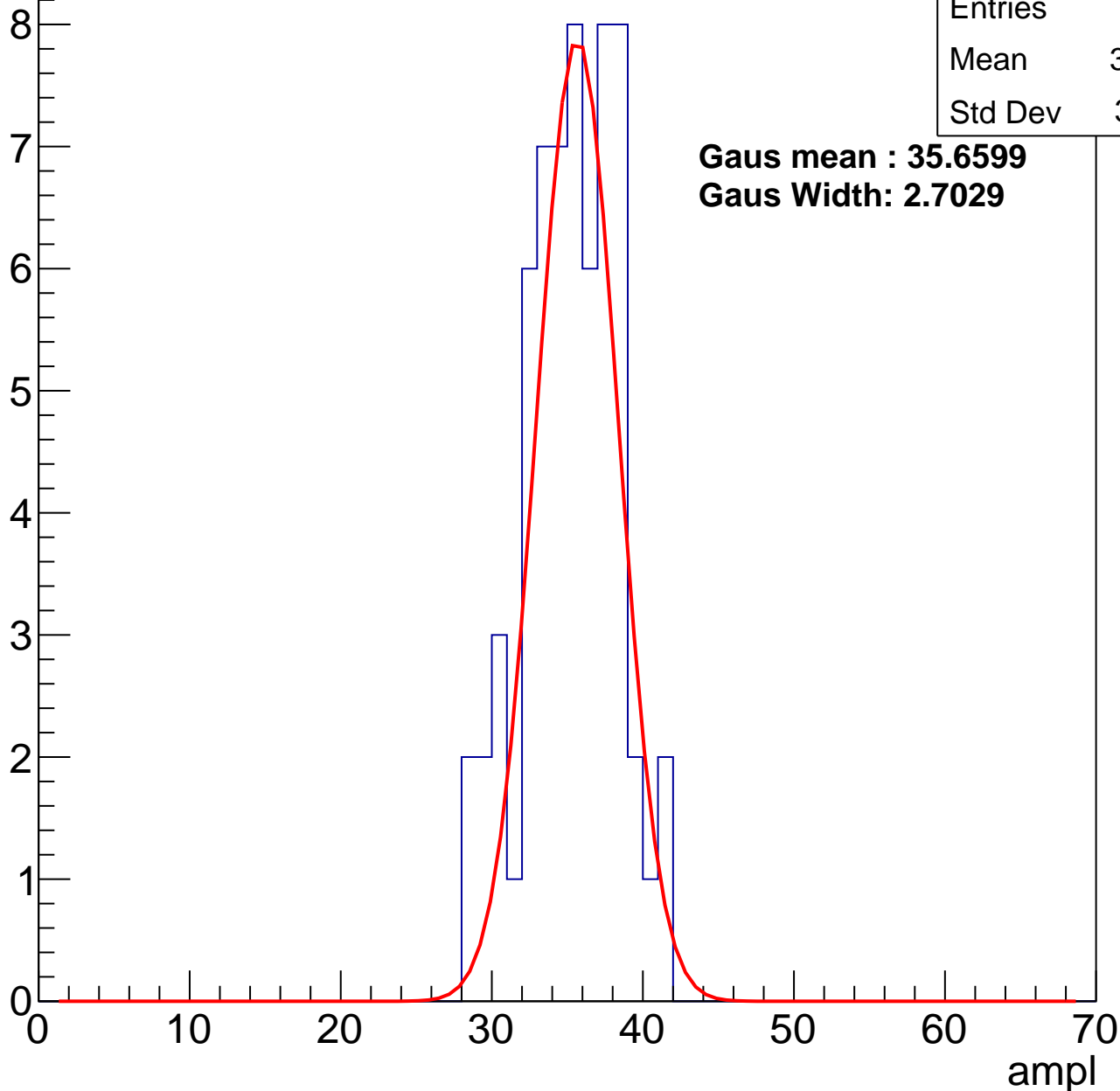
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	34.79
Std Dev	3.051

Gaus mean : 35.6599

Gaus Width: 2.7029



B0L000S, U4-ch105, adc2

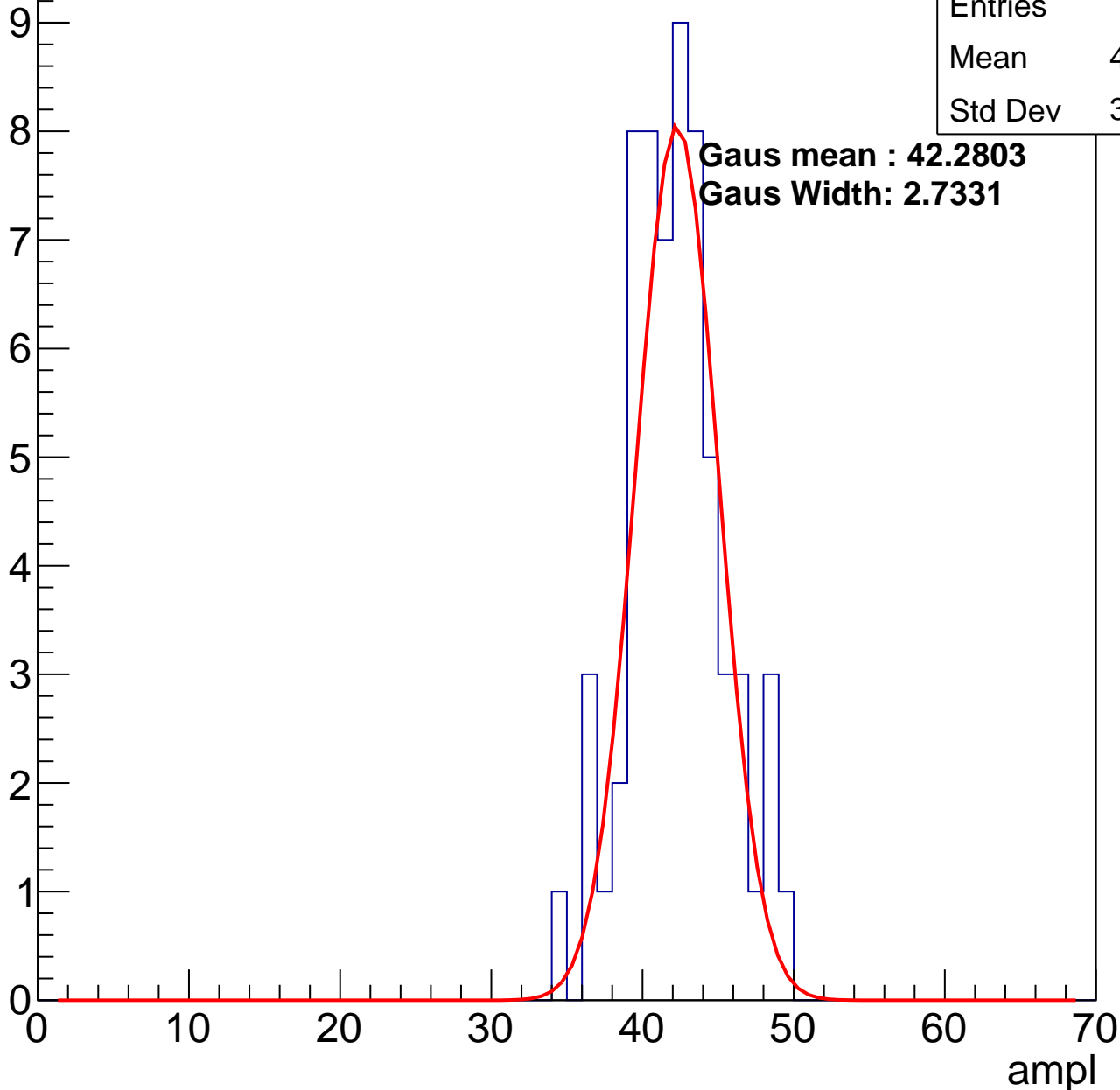
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	41.73
Std Dev	3.138

Gaus mean : 42.2803

Gaus Width: 2.7331

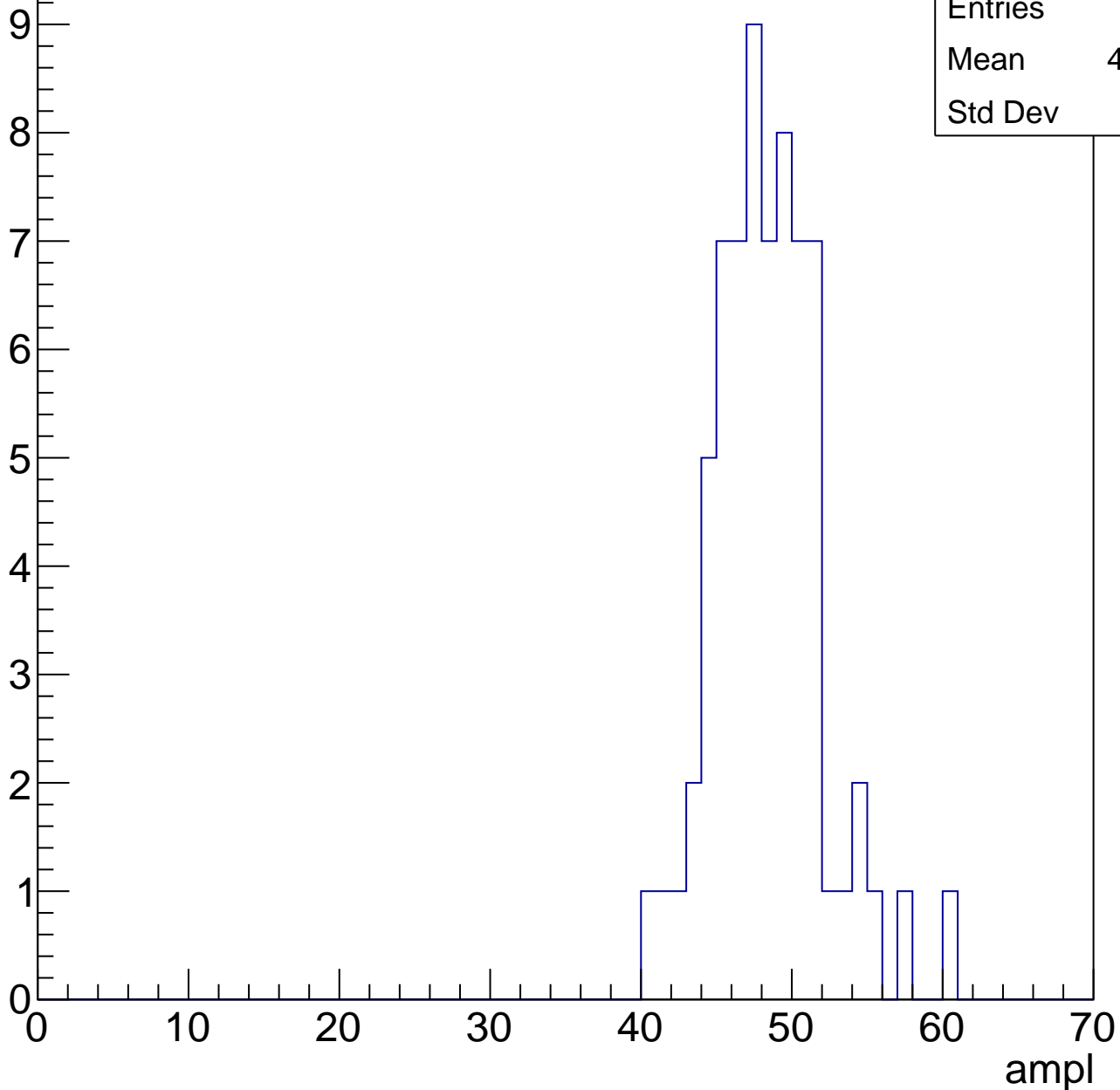


B0L000S, U4-ch105, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

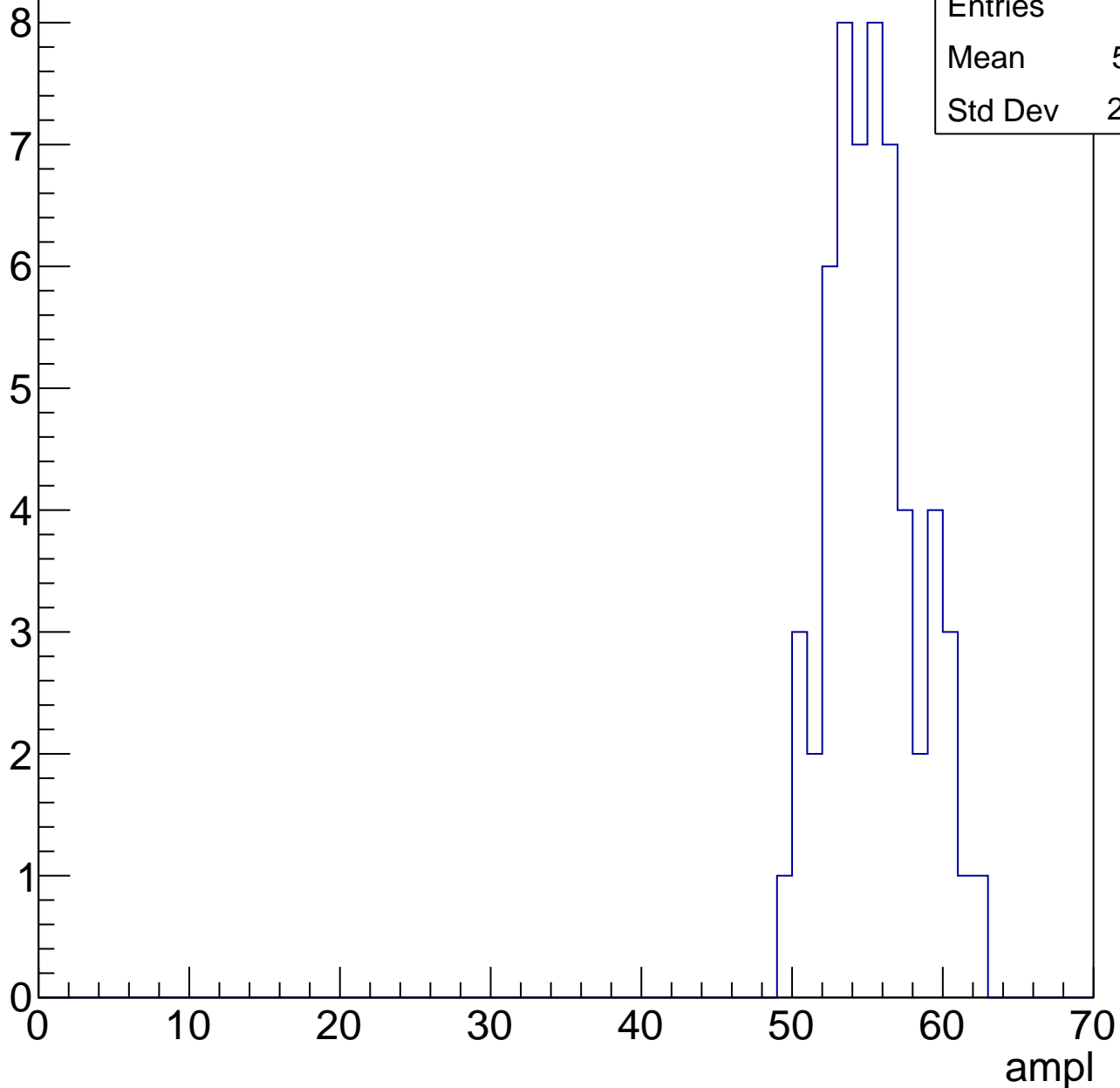
Entries	69
Mean	47.96
Std Dev	3.54



B0L000S, U4-ch105, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

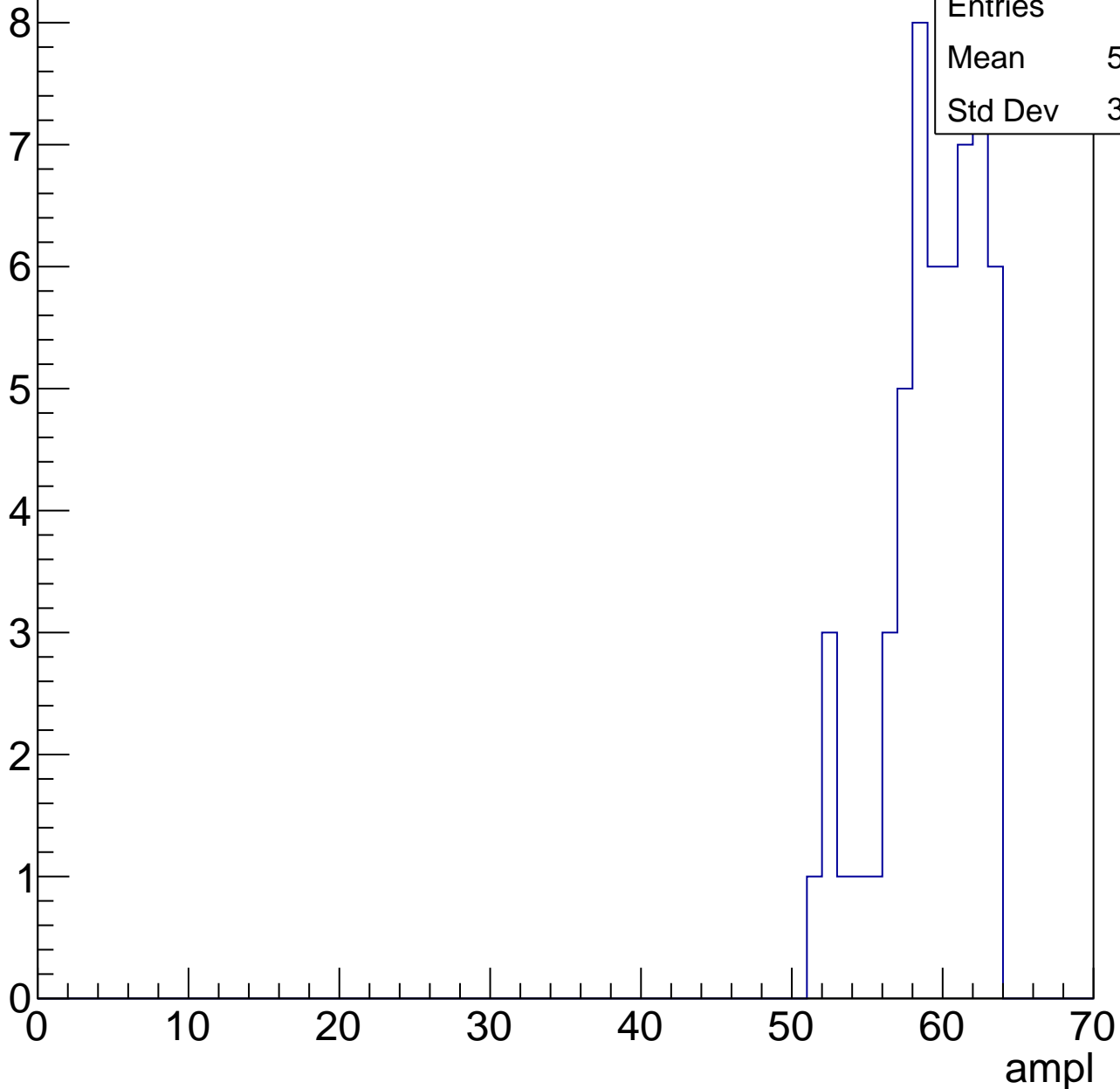


B0L000S, U4-ch105, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

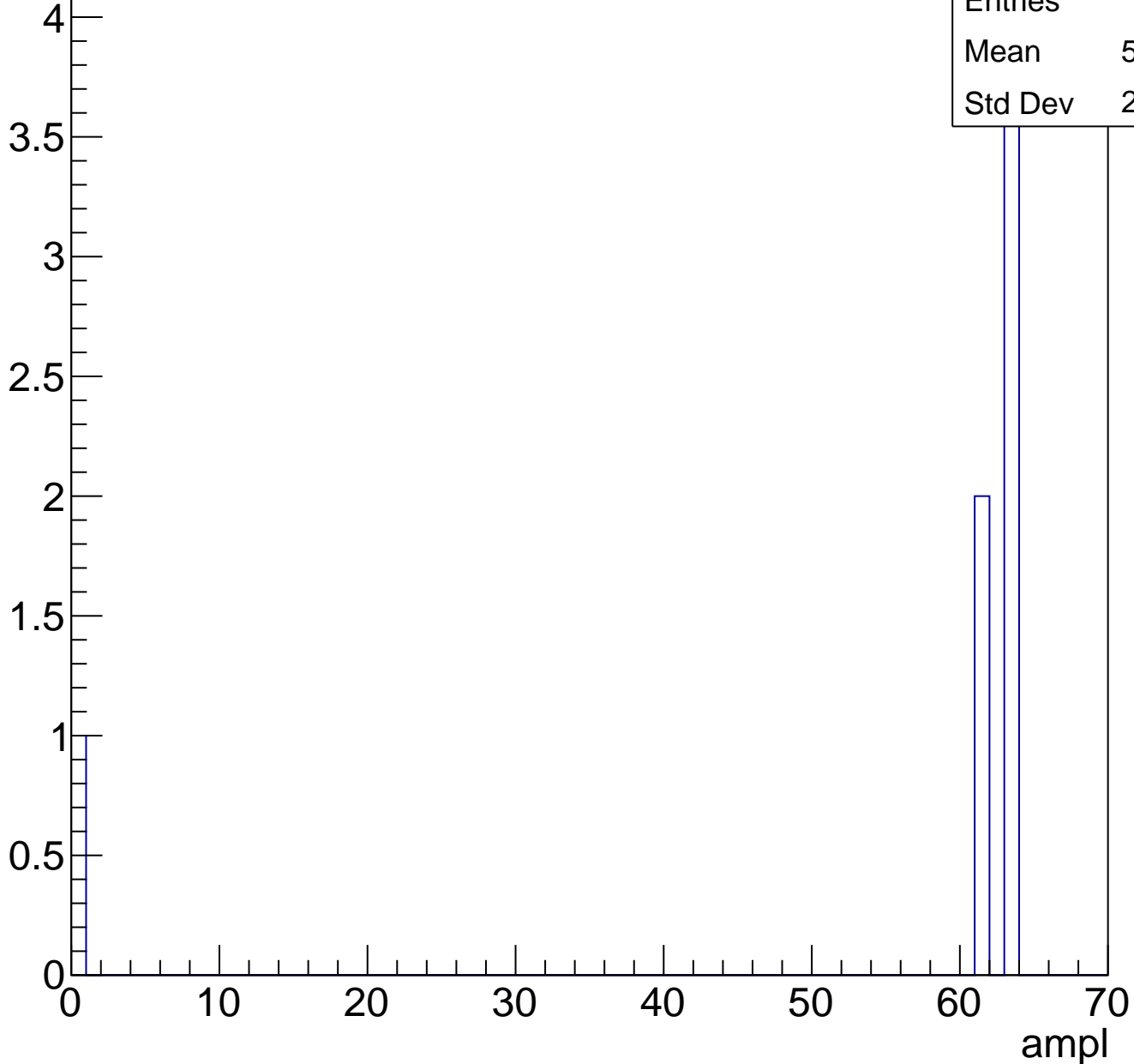
Entries	56
Mean	58.95
Std Dev	3.119



B0L000S, U4-ch105, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch105, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch106, adc0

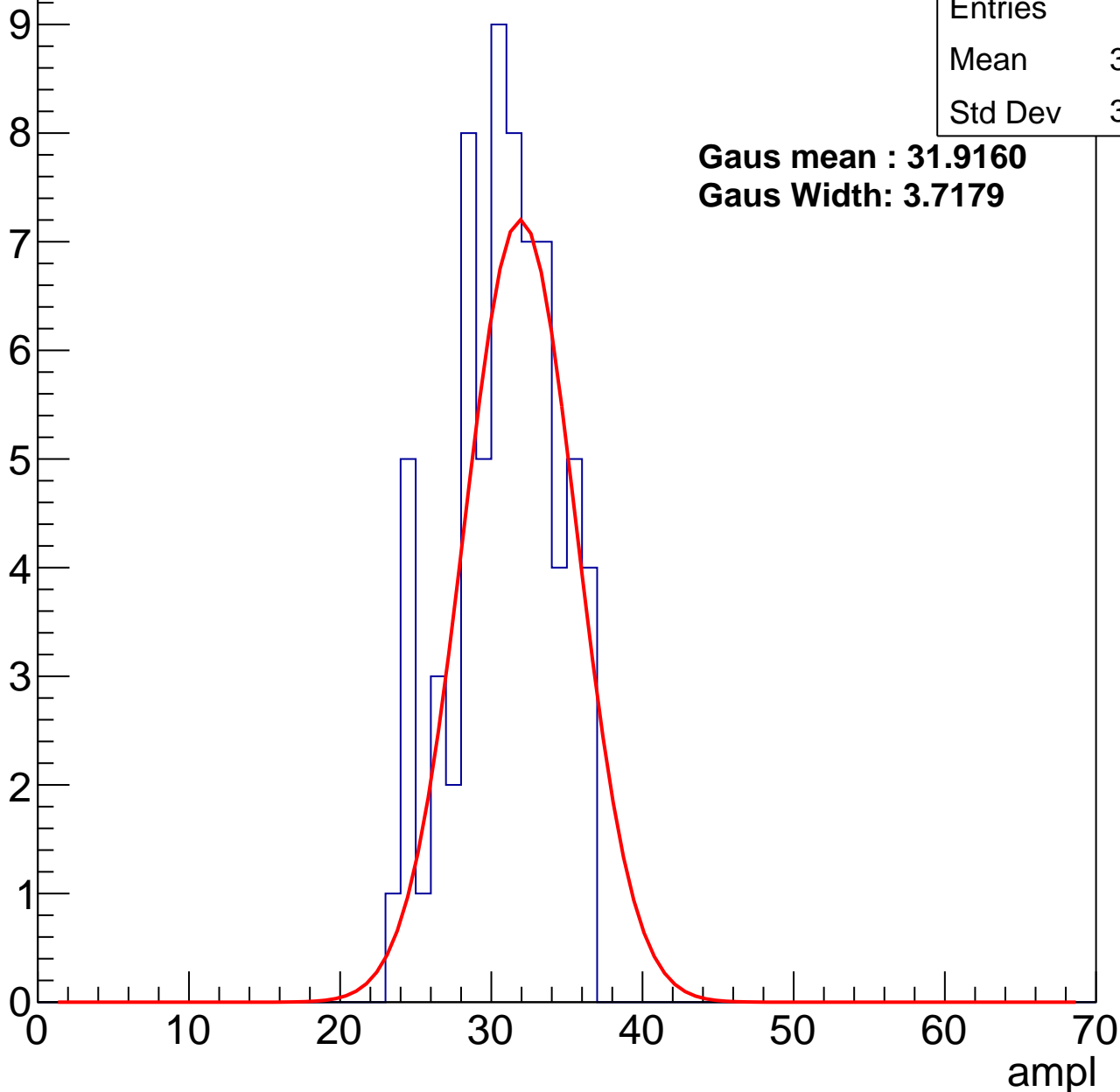
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.39
Std Dev	3.359

Gaus mean : 31.9160

Gaus Width: 3.7179



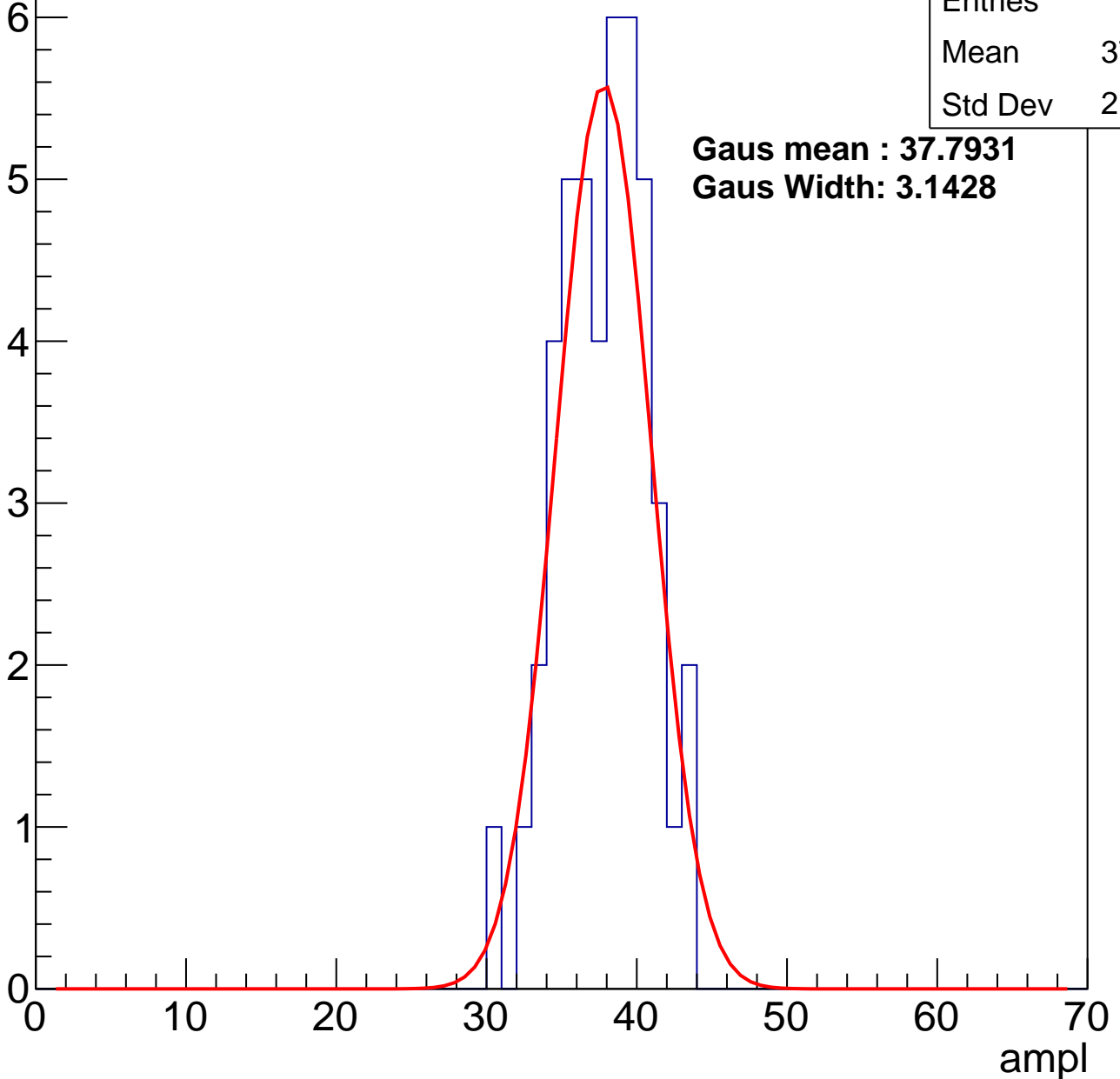
B0L000S, U4-ch106, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	45
Mean	37.33
Std Dev	2.914

Gaus mean : 37.7931
Gaus Width: 3.1428



B0L000S, U4-ch106, adc2

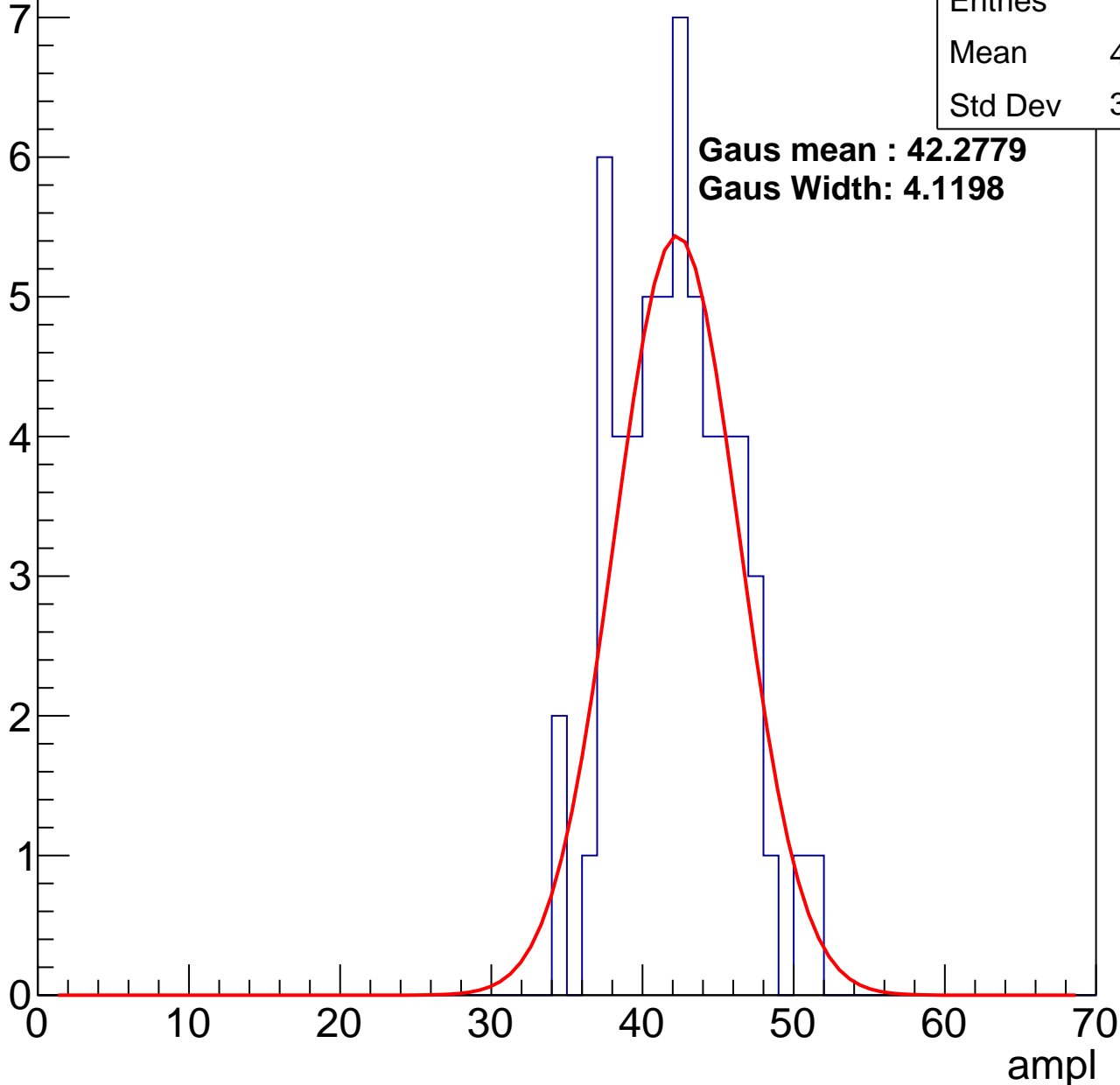
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	41.72
Std Dev	3.773

Gaus mean : 42.2779

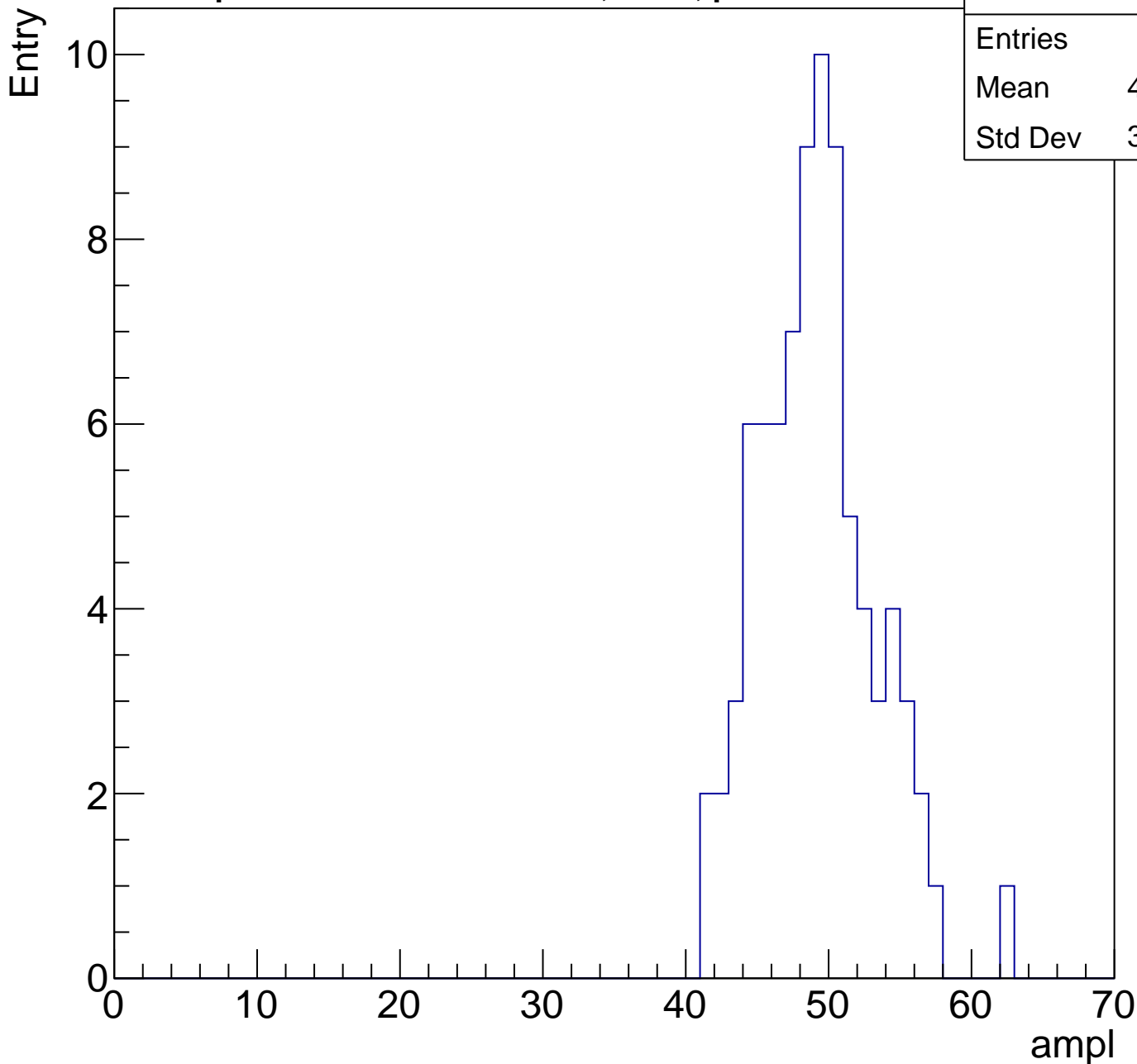
Gaus Width: 4.1198



B0L000S, U4-ch106, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	83
Mean	48.67
Std Dev	3.979

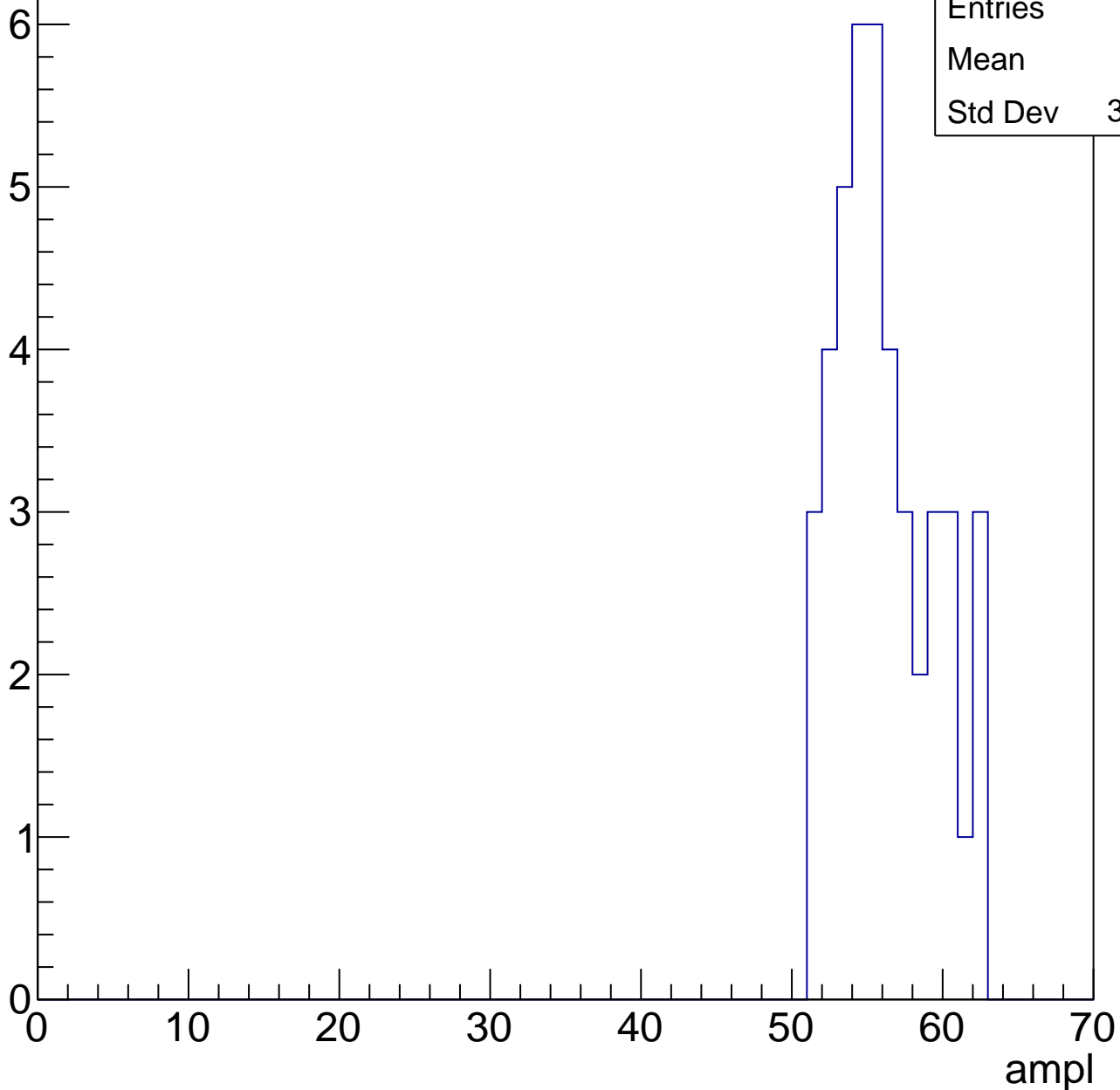


B0L000S, U4-ch106, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	43
Mean	55.7
Std Dev	3.159

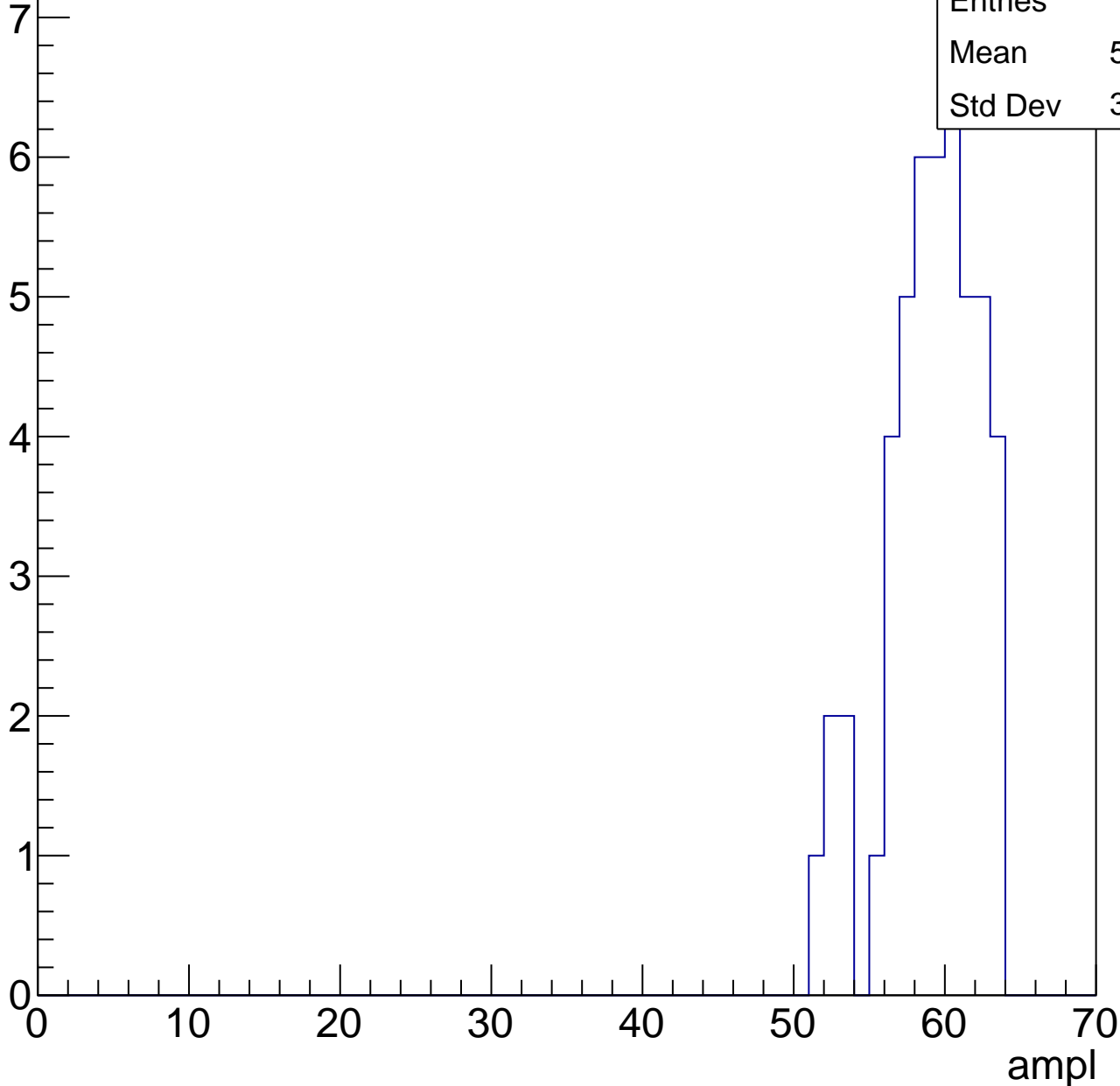


B0L000S, U4-ch106, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	58.62
Std Dev	3.032

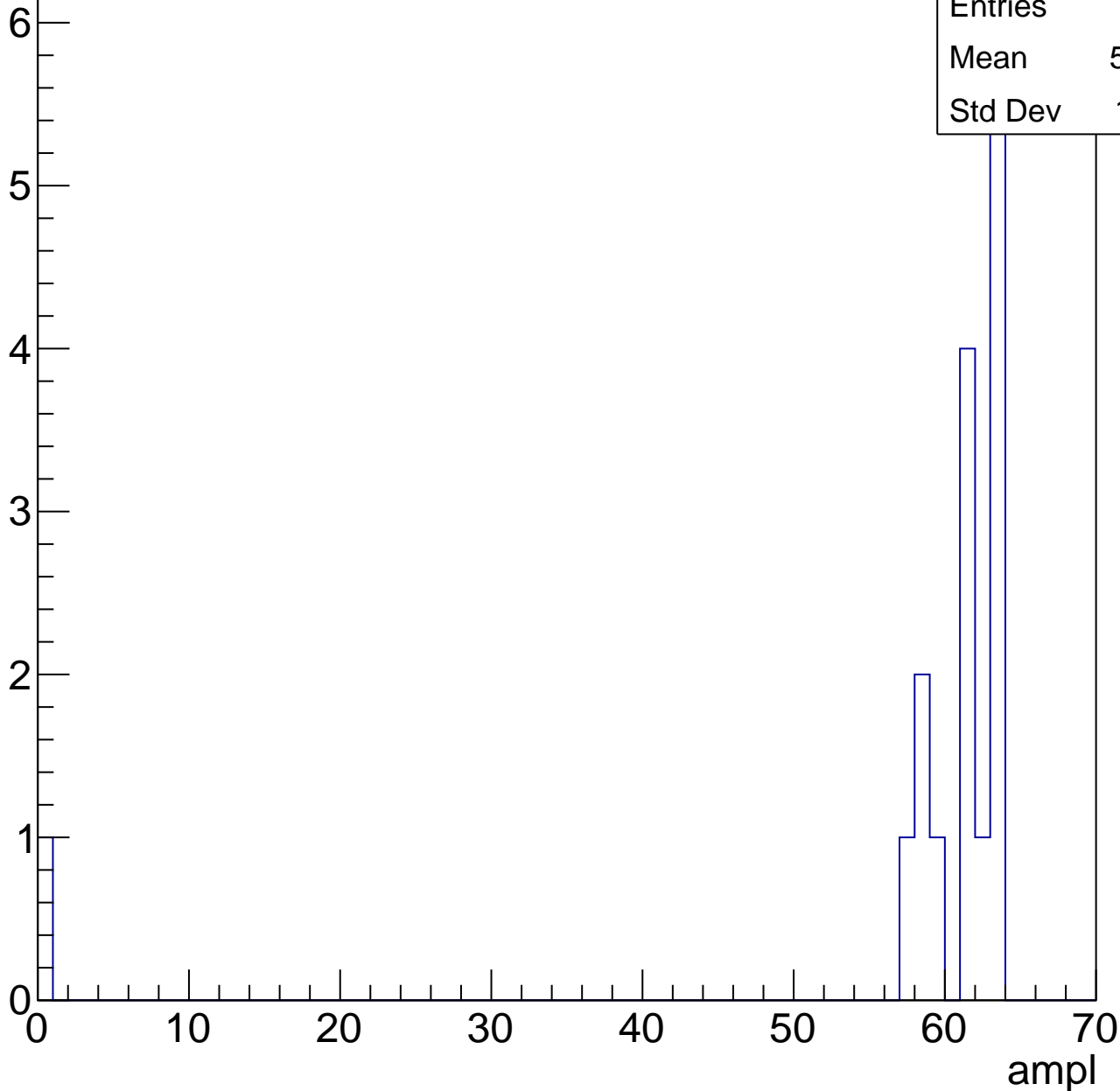


B0L000S, U4-ch106, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.25
Std Dev	14.91



B0L000S, U4-ch106, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch107, adc0

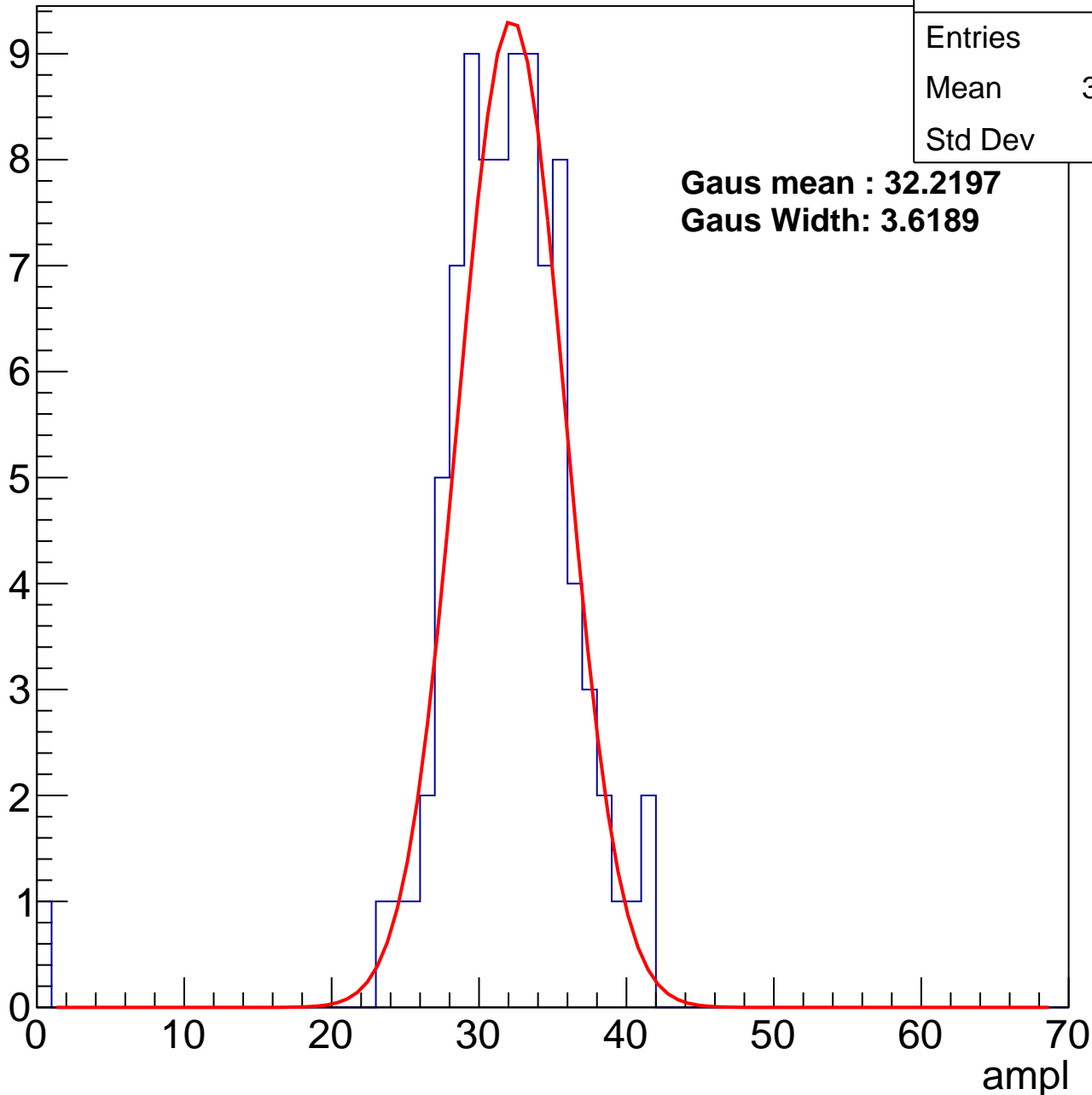
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	89
Mean	31.45
Std Dev	4.99

Gaus mean : 32.2197

Gaus Width: 3.6189



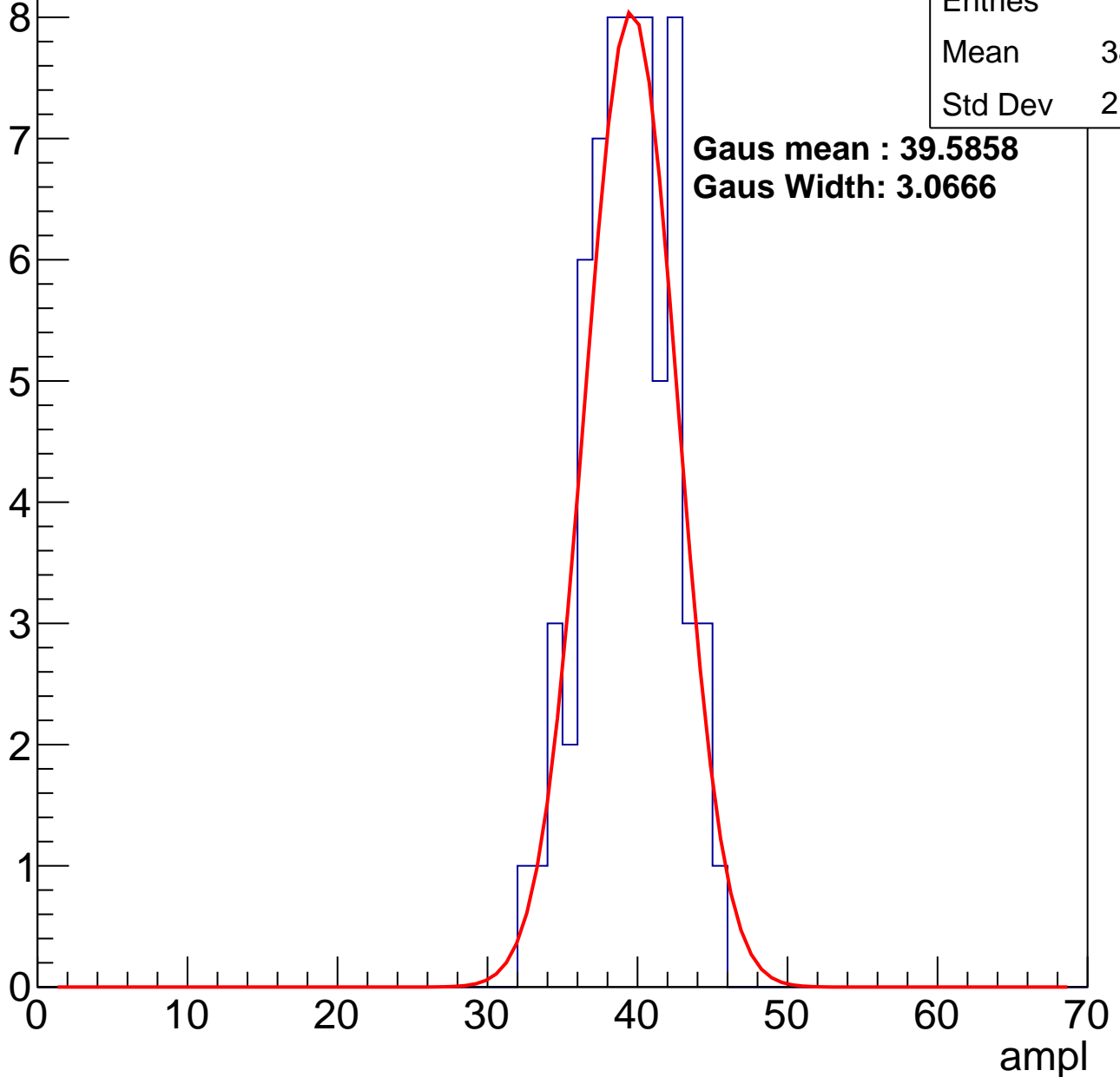
B0L000S, U4-ch107, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	38.98
Std Dev	2.907

Gaus mean : 39.5858
Gaus Width: 3.0666



B0L000S, U4-ch107, adc2

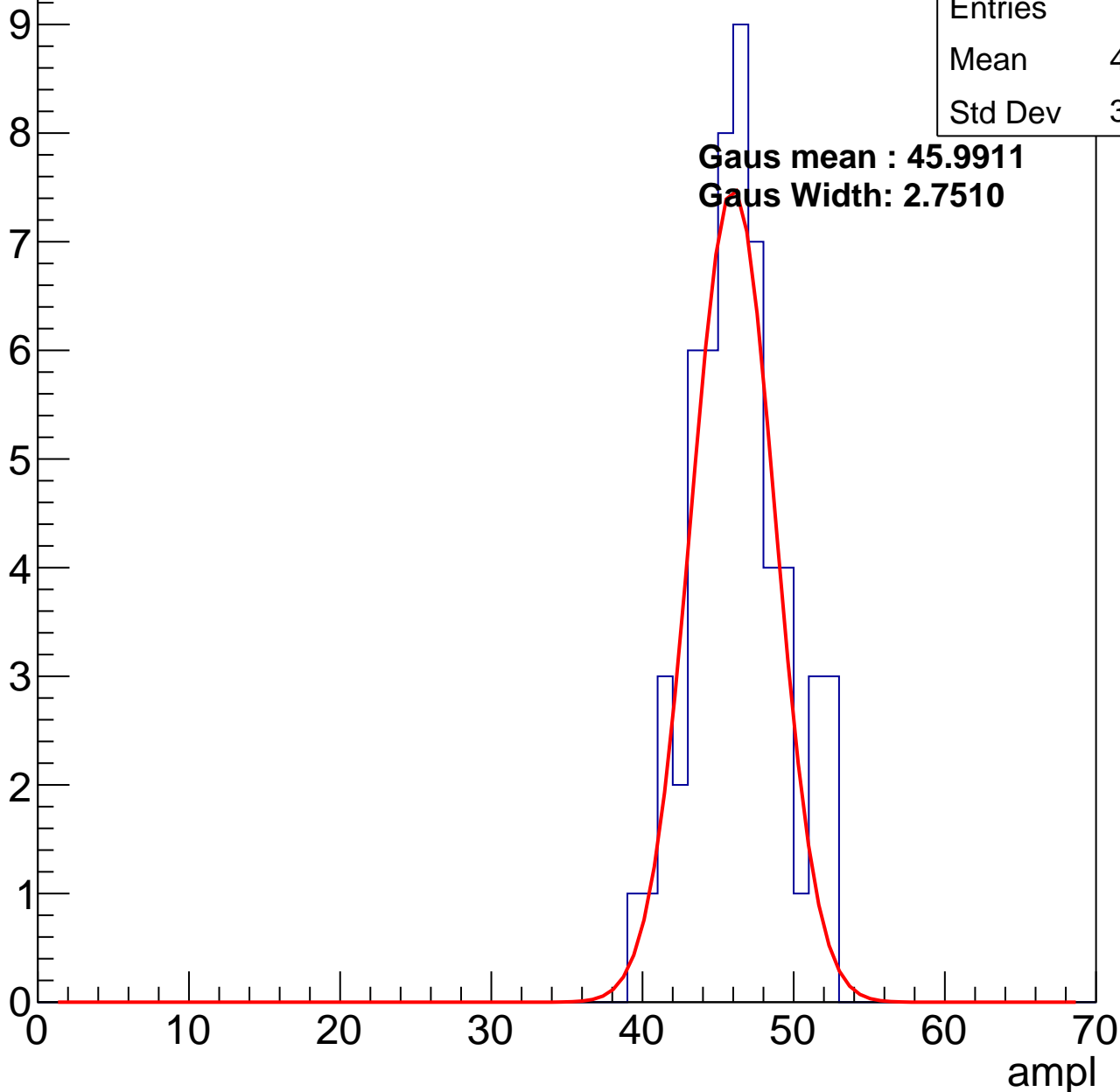
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	45.83
Std Dev	3.035

Gaus mean : 45.9911

Gaus Width: 2.7510

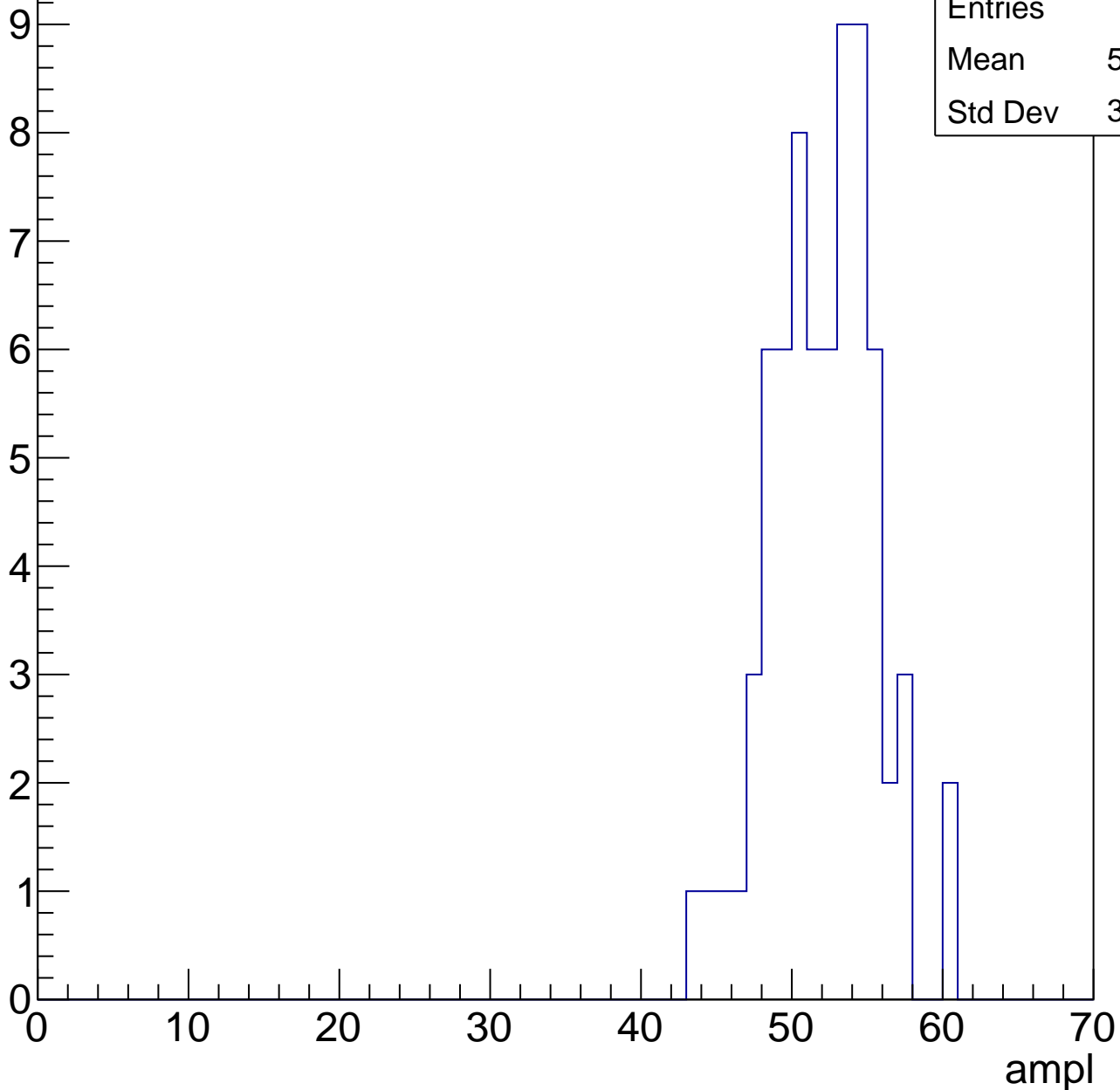


B0L000S, U4-ch107, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

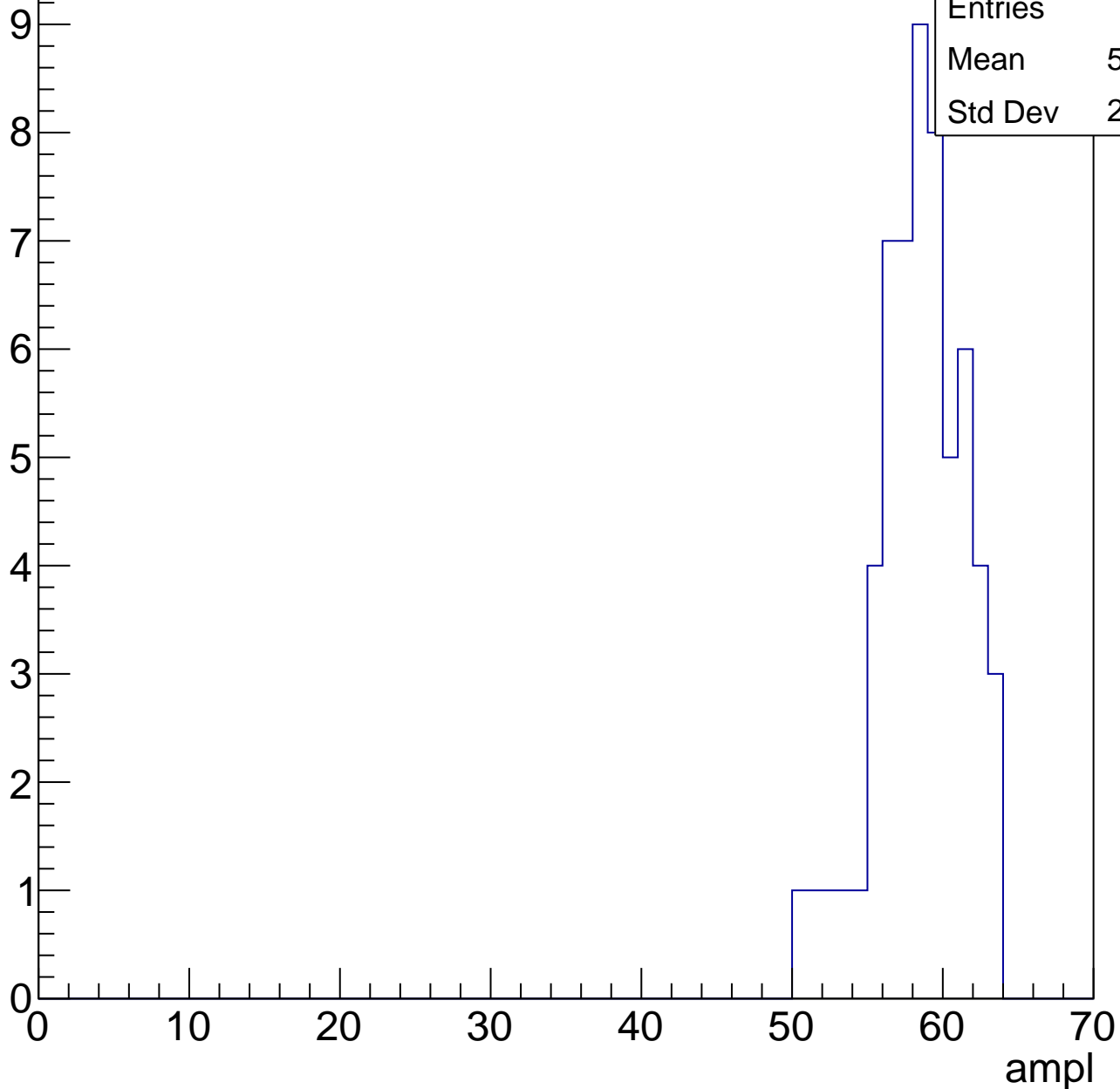
Entries	70
Mean	51.64
Std Dev	3.419



B0L000S, U4-ch107, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



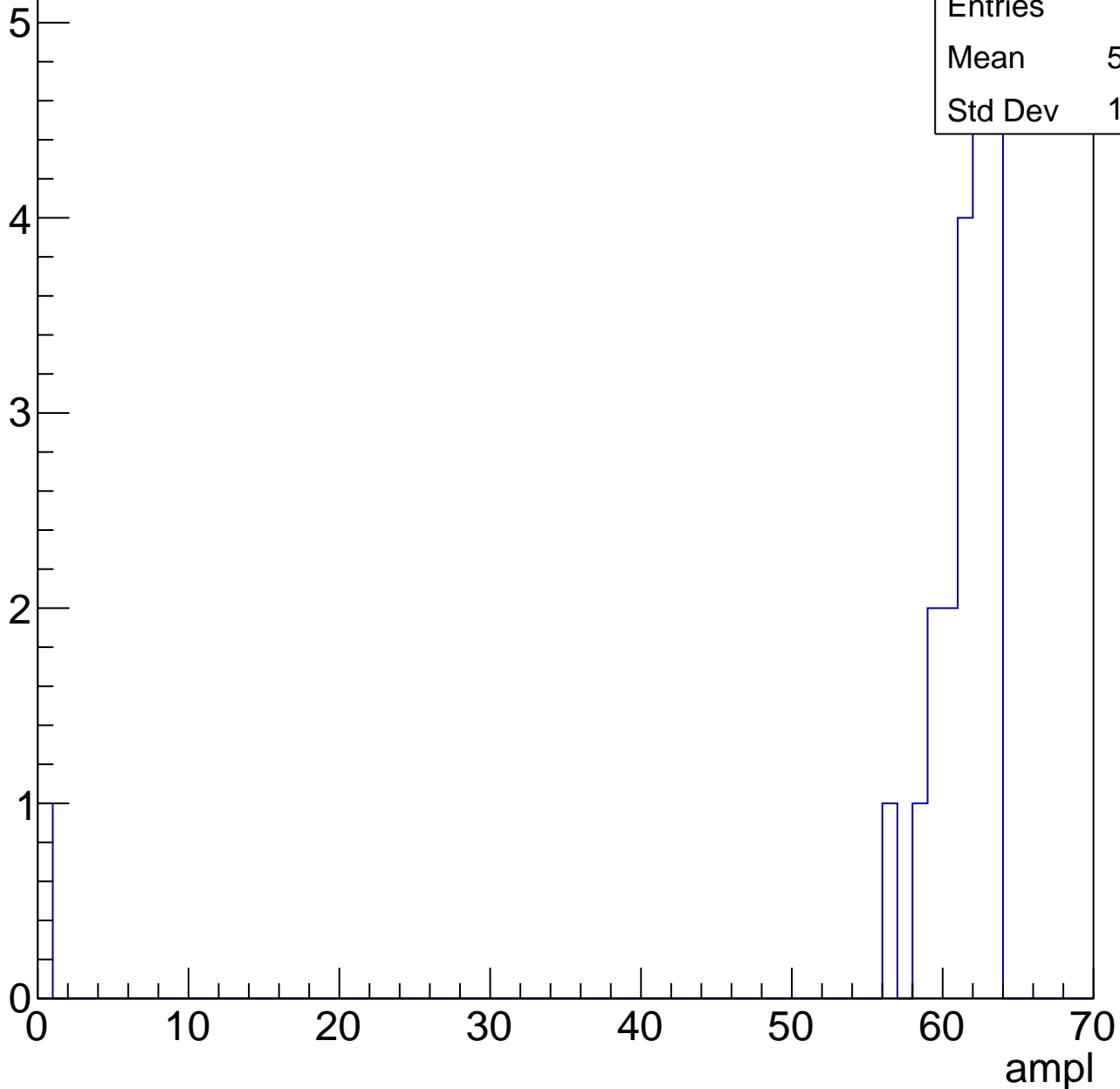
Entries	58
Mean	58.07
Std Dev	2.882

B0L000S, U4-ch107, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	21
Mean	58.14
Std Dev	13.13



B0L000S, U4-ch107, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch107, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch108, adc0

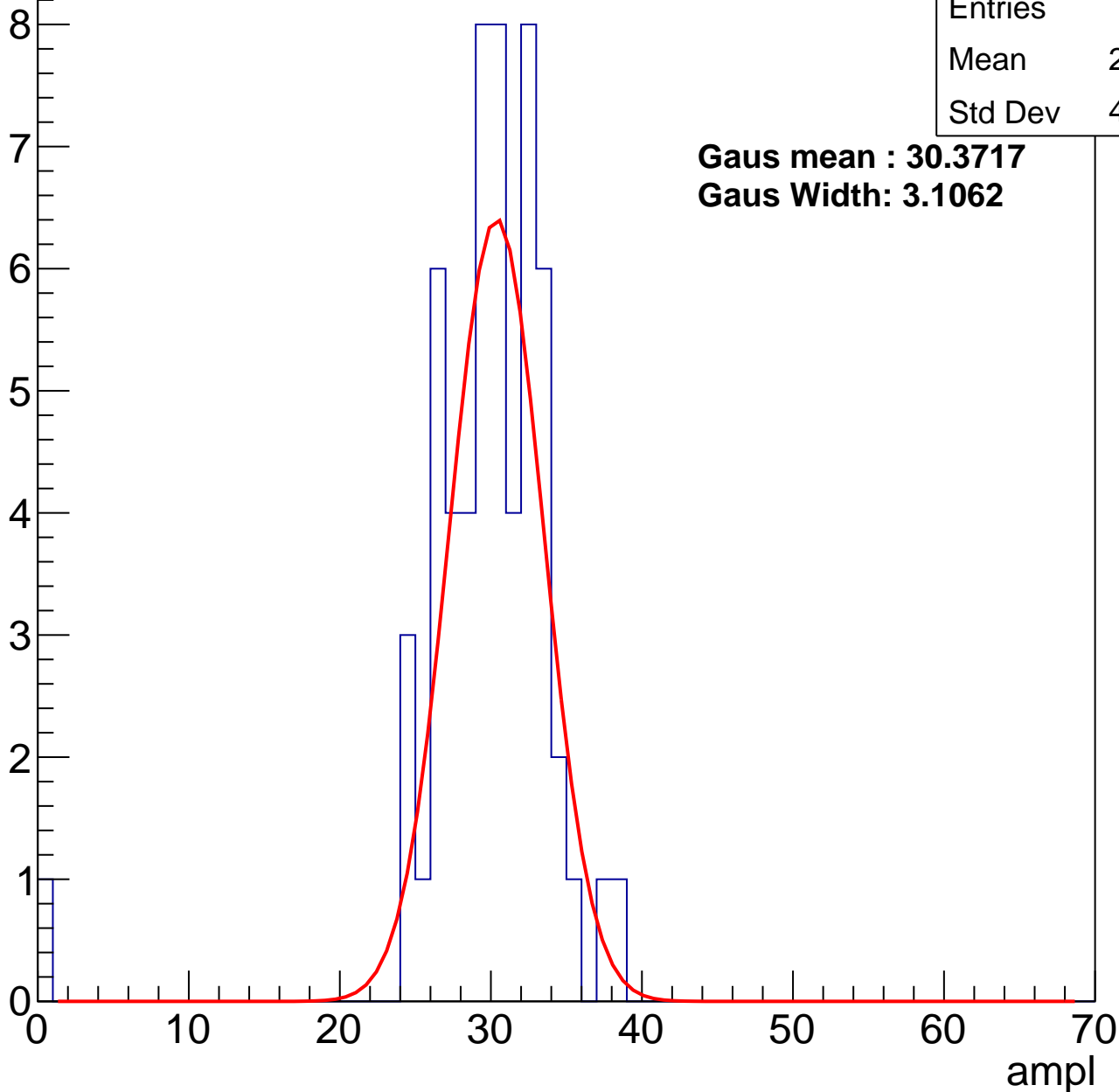
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.33
Std Dev	4.953

Gaus mean : 30.3717

Gaus Width: 3.1062



B0L000S, U4-ch108, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	69
Mean	36.26
Std Dev	3.247

Gaus mean : 36.5406

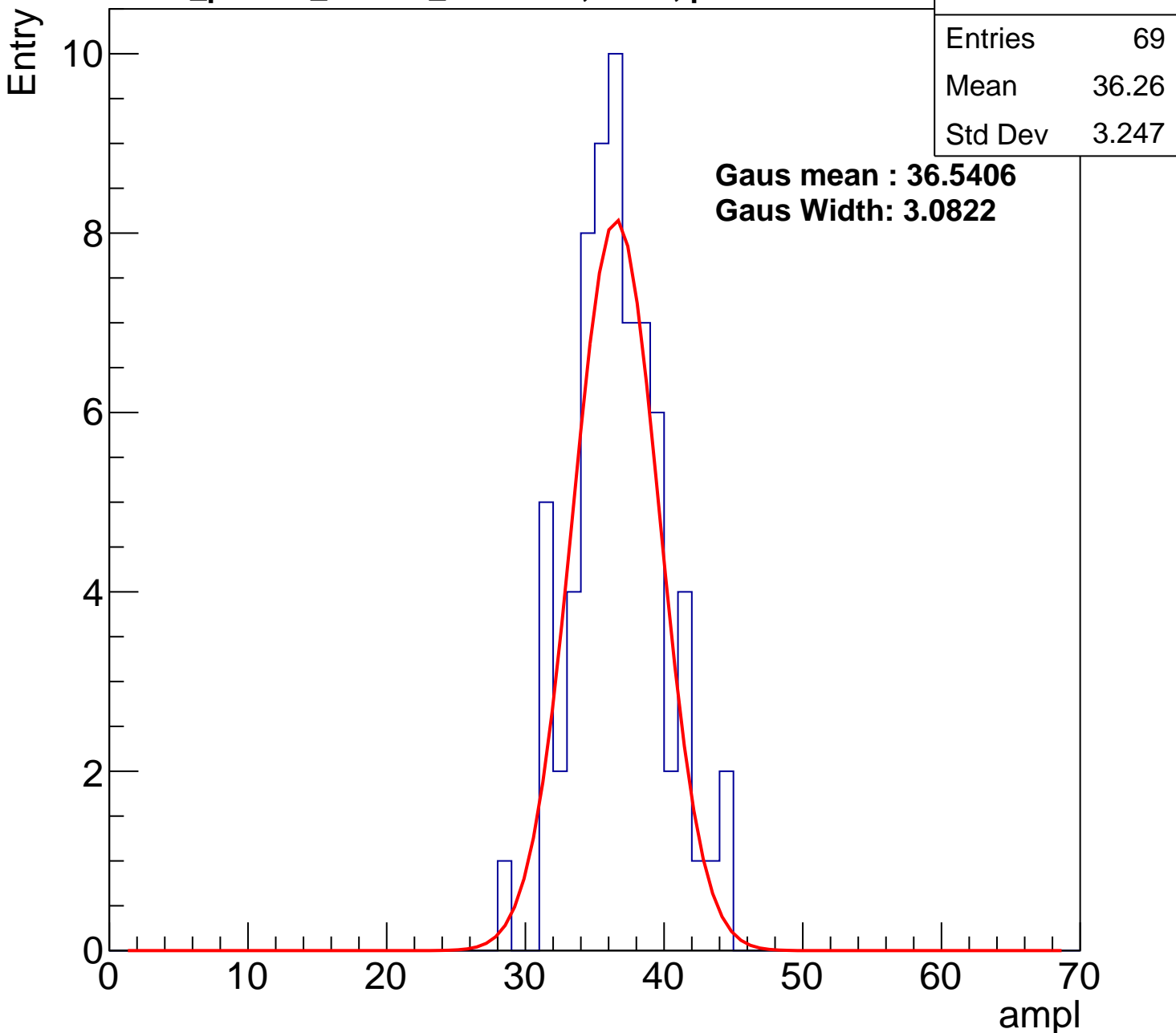
Gaus Width: 3.0822

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

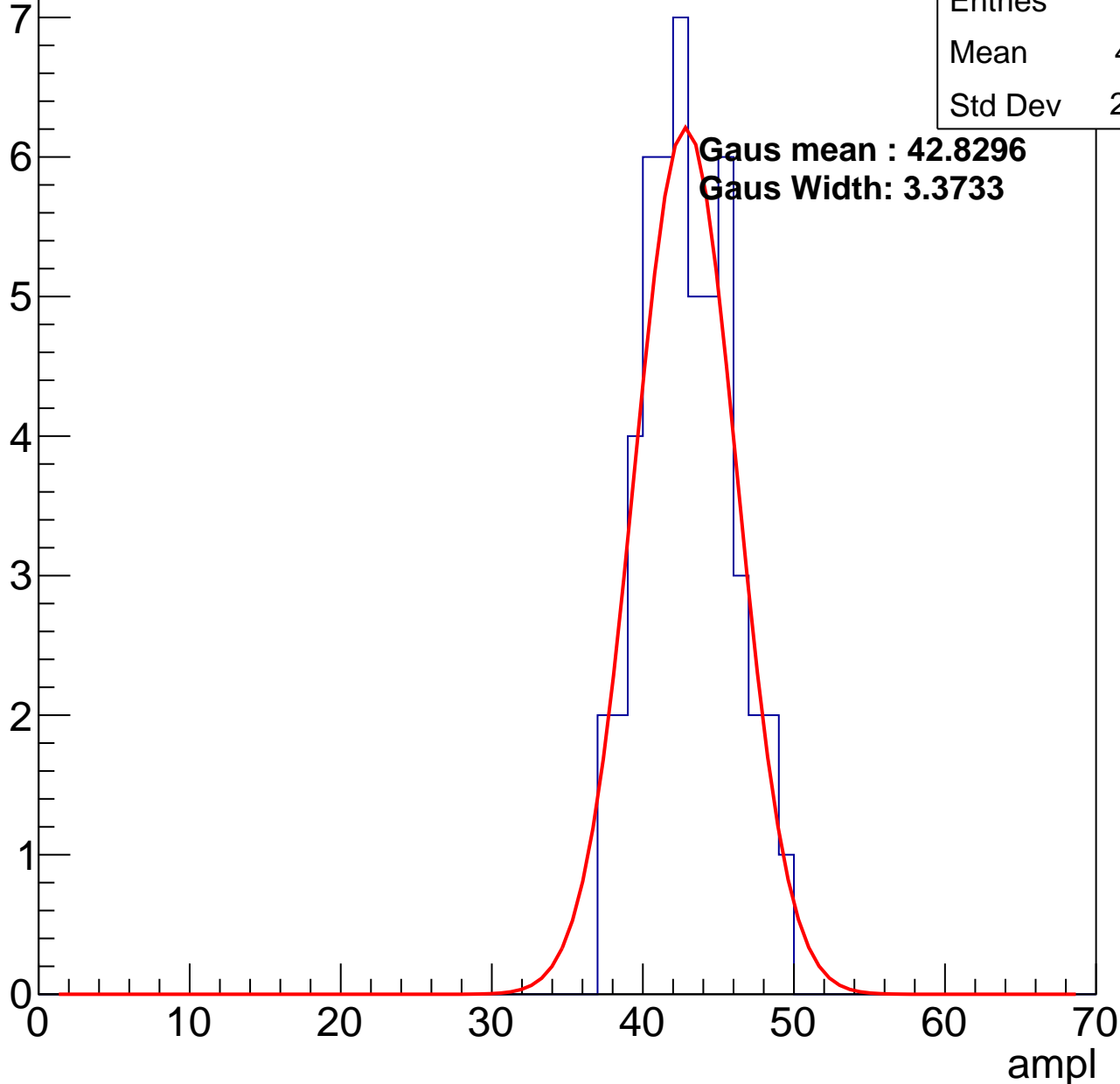


B0L000S, U4-ch108, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	42.51
Std Dev	2.913

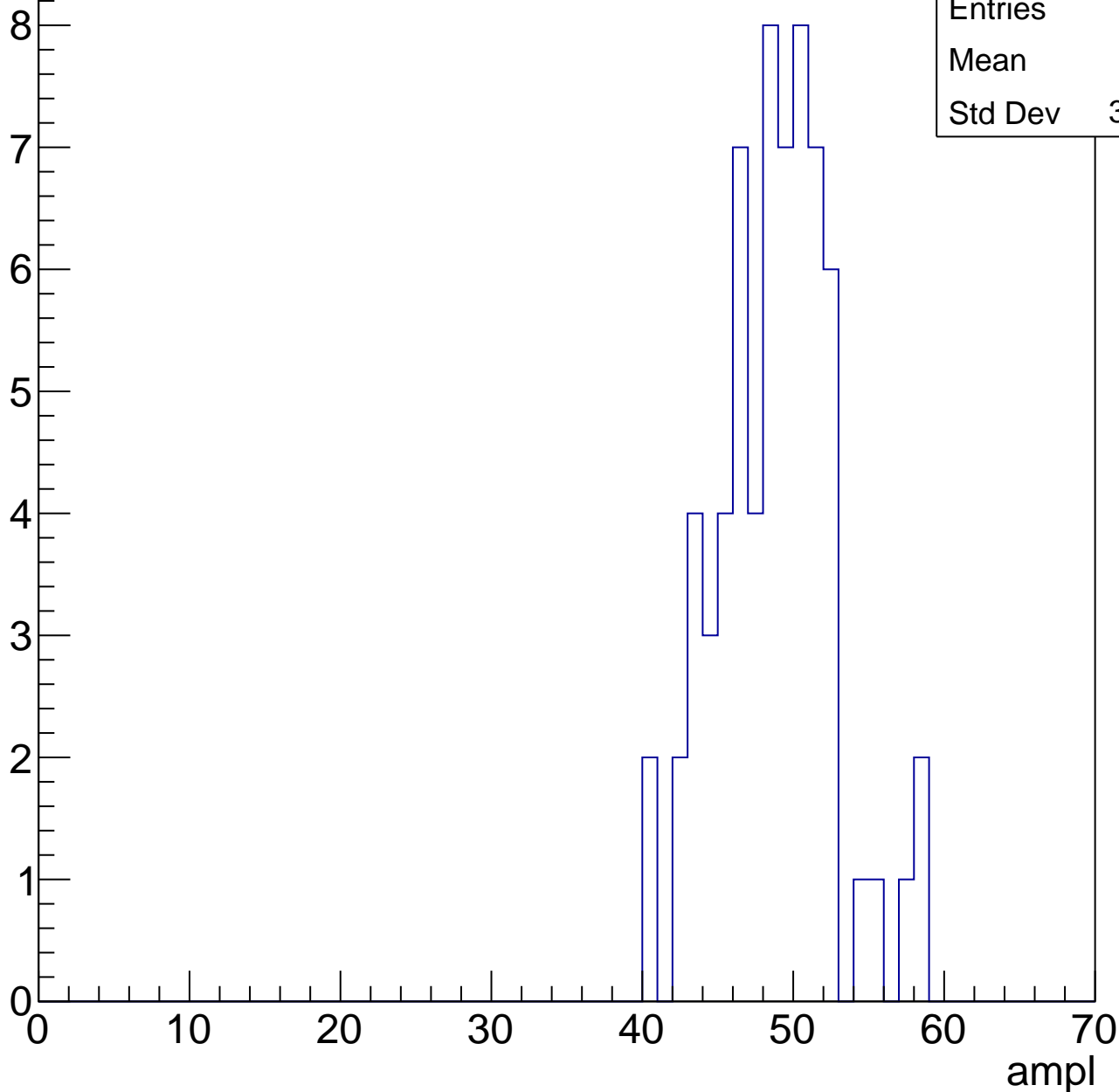


B0L000S, U4-ch108, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.3
Std Dev	3.813

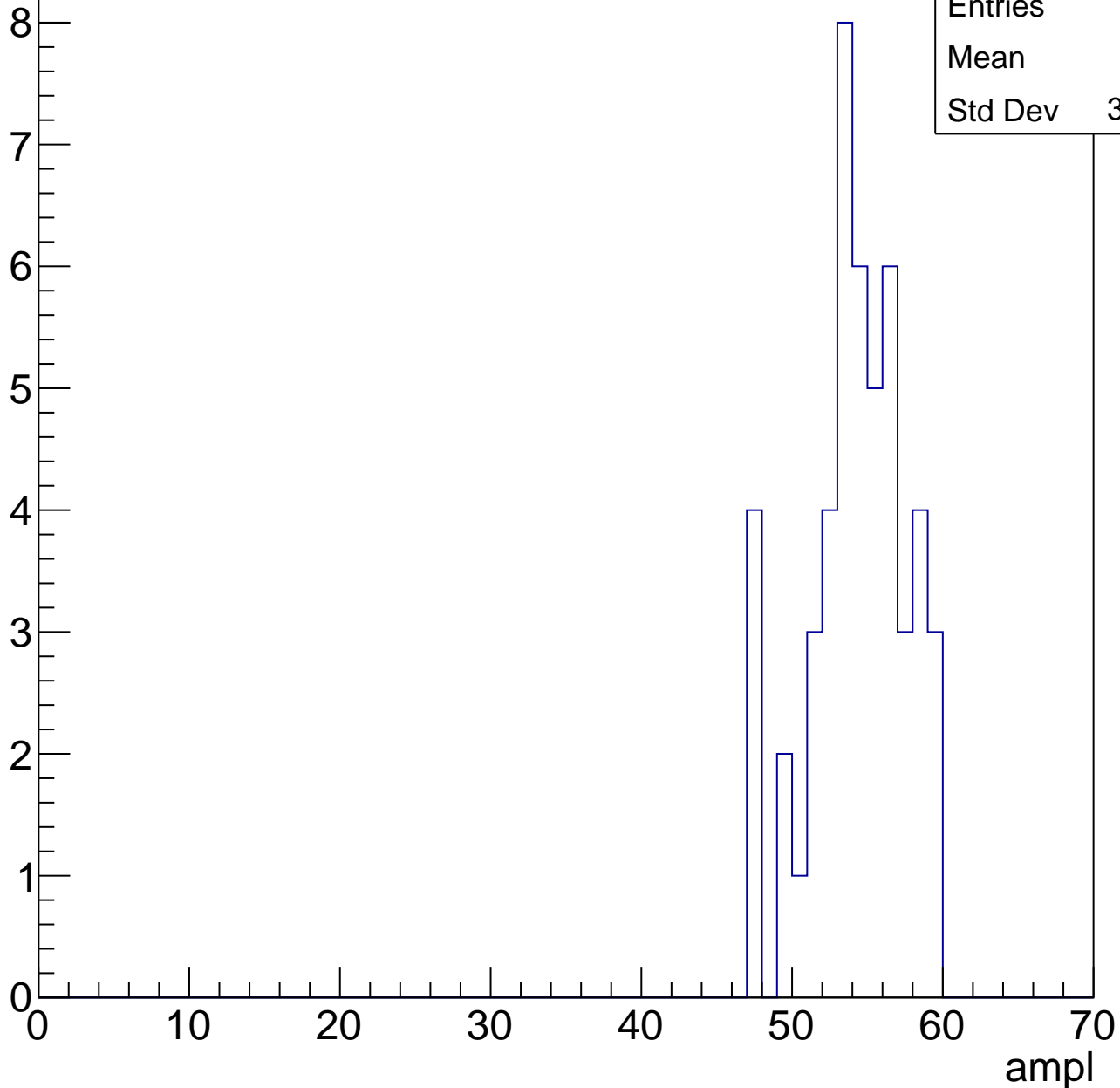


B0L000S, U4-ch108, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	53.8
Std Dev	3.213

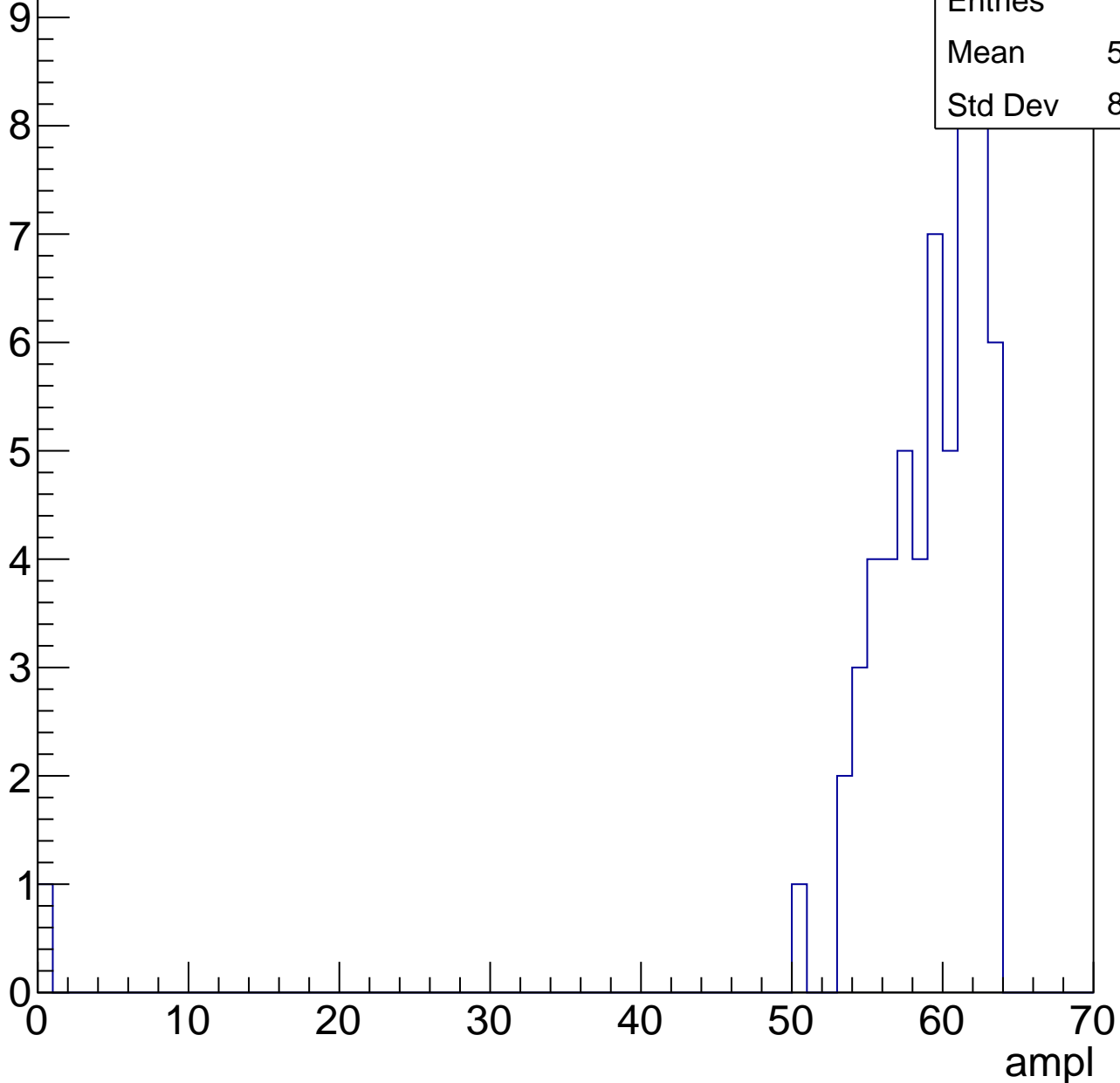


B0L000S, U4-ch108, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

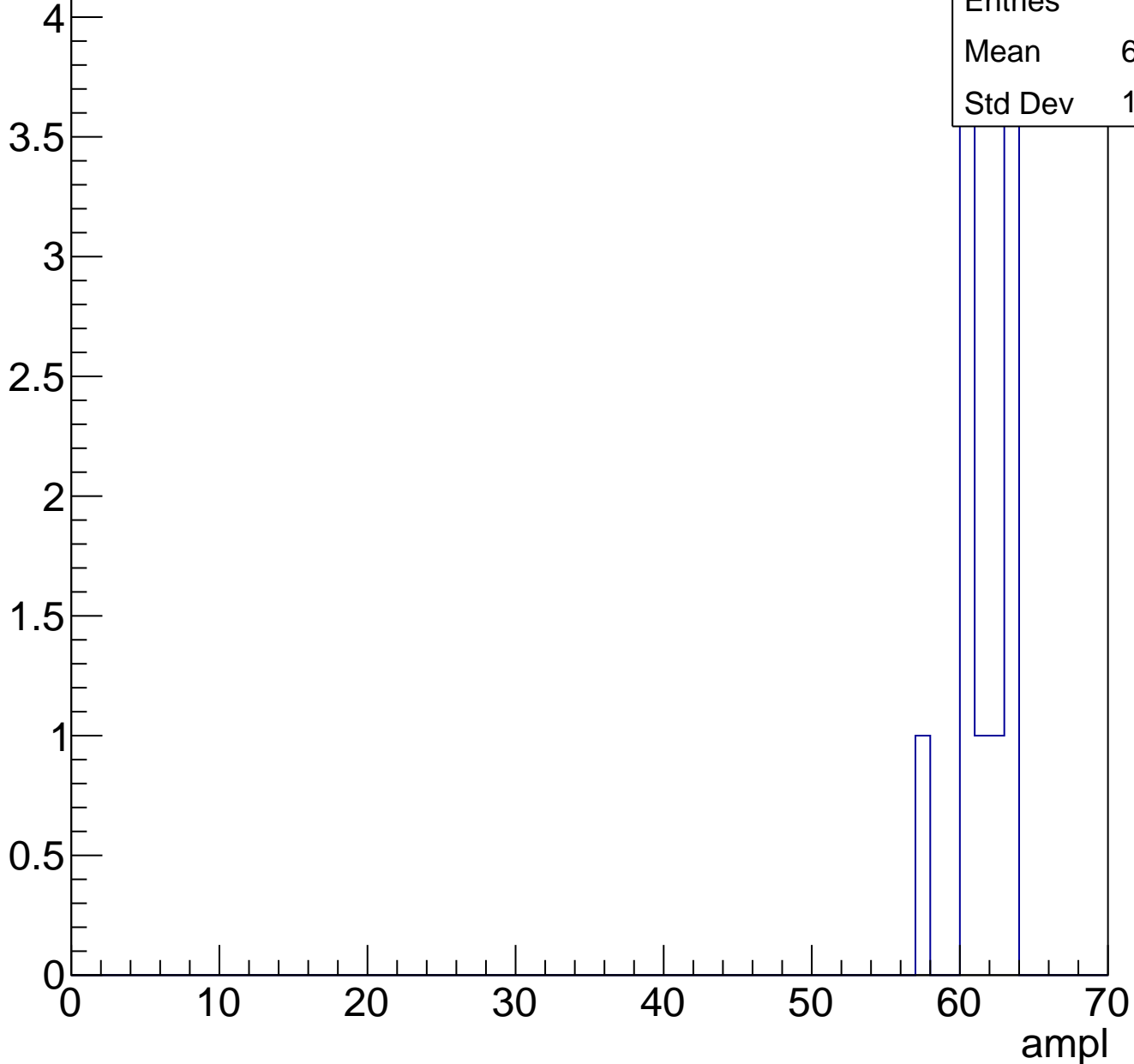
Entries	60
Mean	57.95
Std Dev	8.149



B0L000S, U4-ch108, adc6

calib_packv5_042523_0143.root, FC#5, port B1

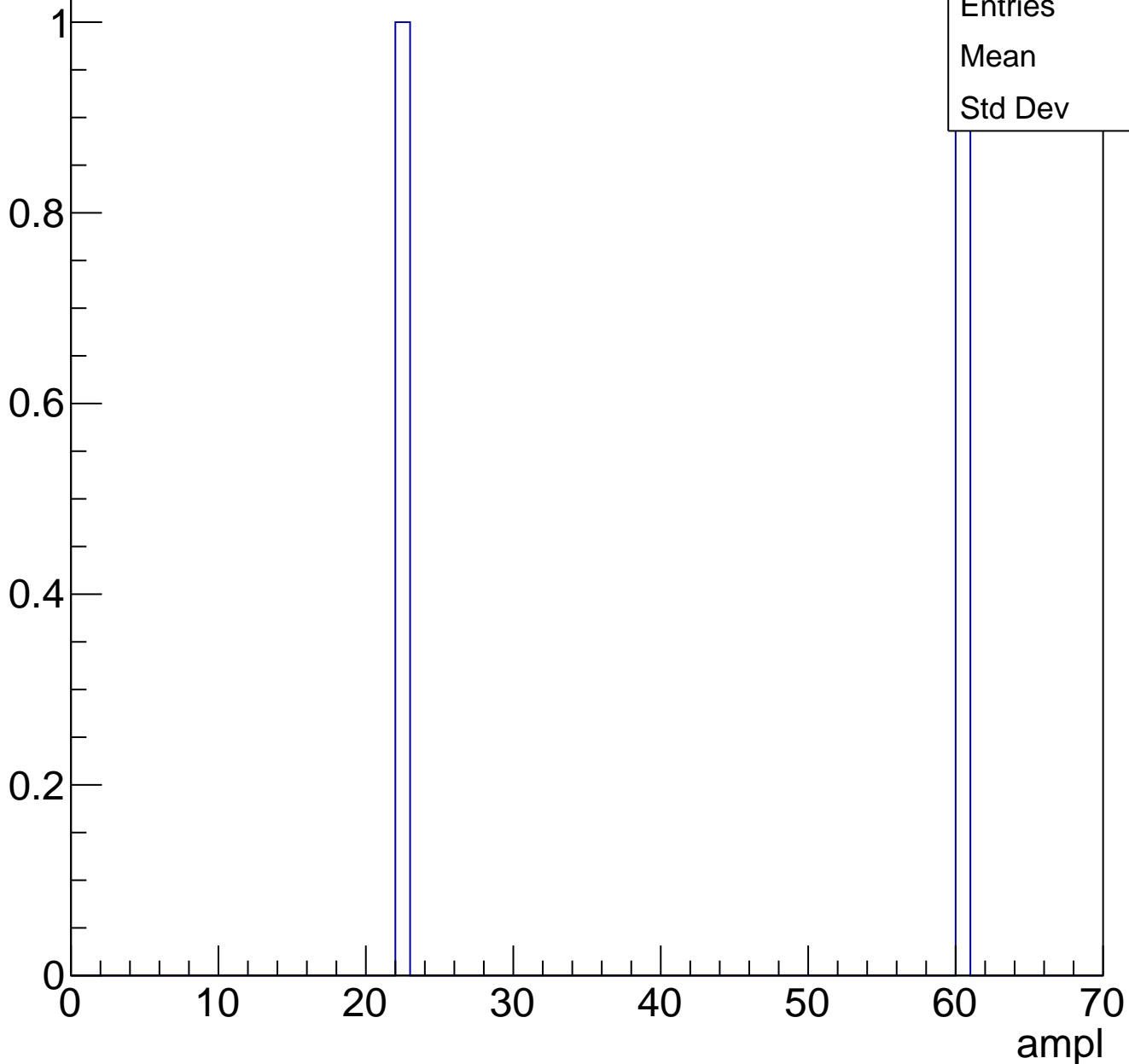
Entry



B0L000S, U4-ch108, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch109, adc0

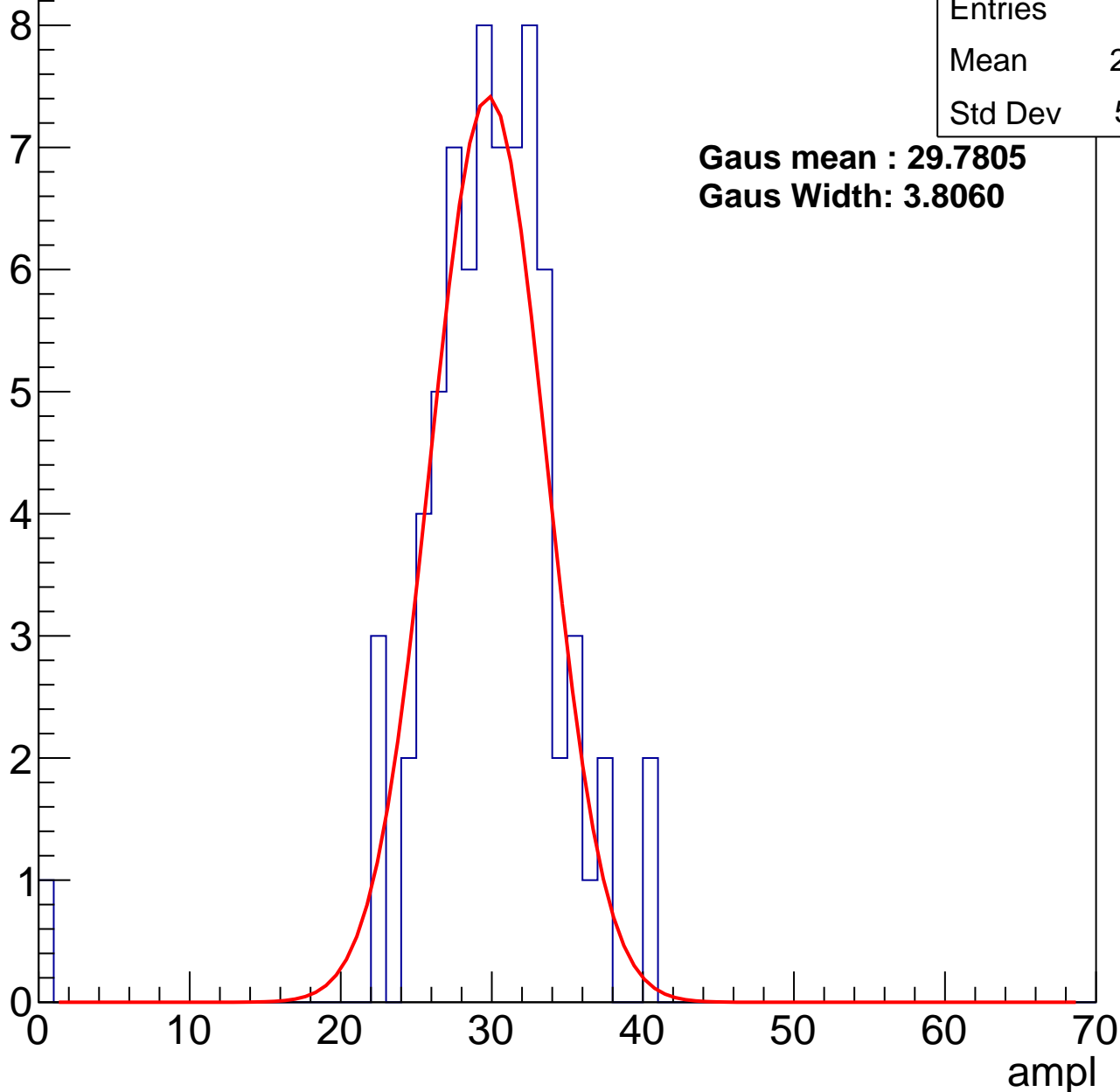
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	29.42
Std Dev	5.131

Gaus mean : 29.7805

Gaus Width: 3.8060

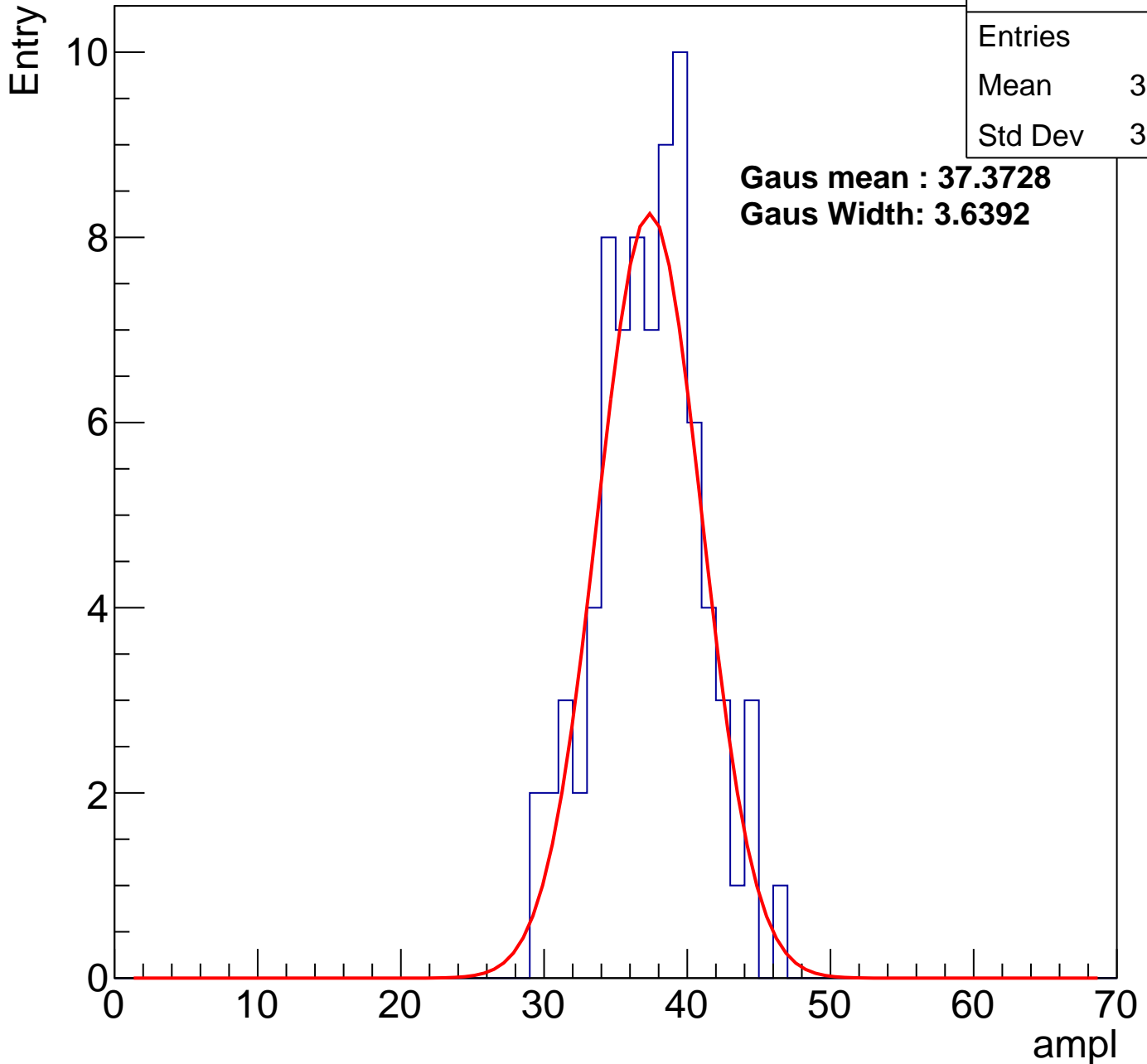


B0L000S, U4-ch109, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	36.92
Std Dev	3.643

Gaus mean : 37.3728
Gaus Width: 3.6392



B0L000S, U4-ch109, adc2

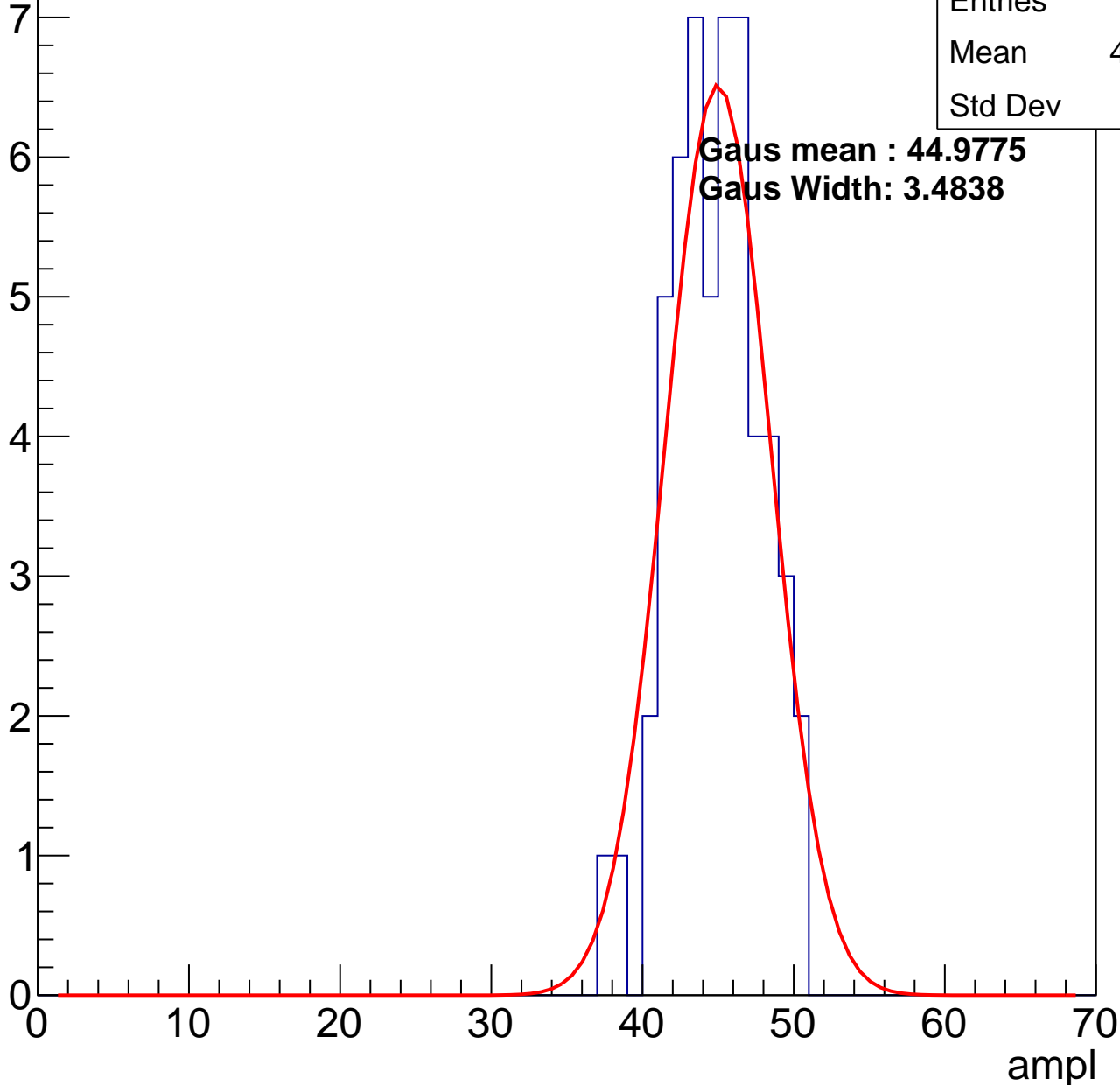
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	44.39
Std Dev	2.94

Gaus mean : 44.9775

Gaus Width: 3.4838

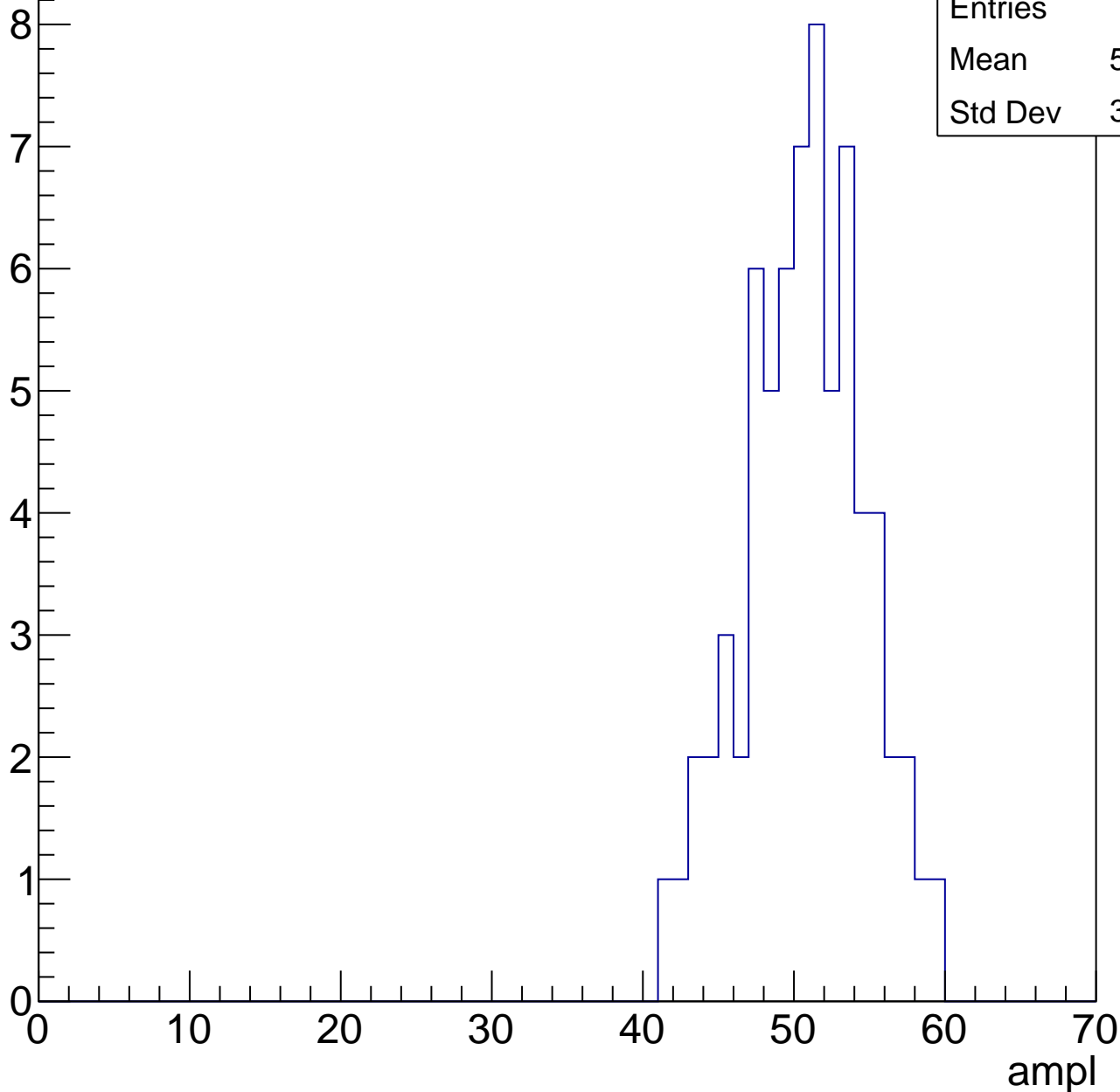


B0L000S, U4-ch109, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

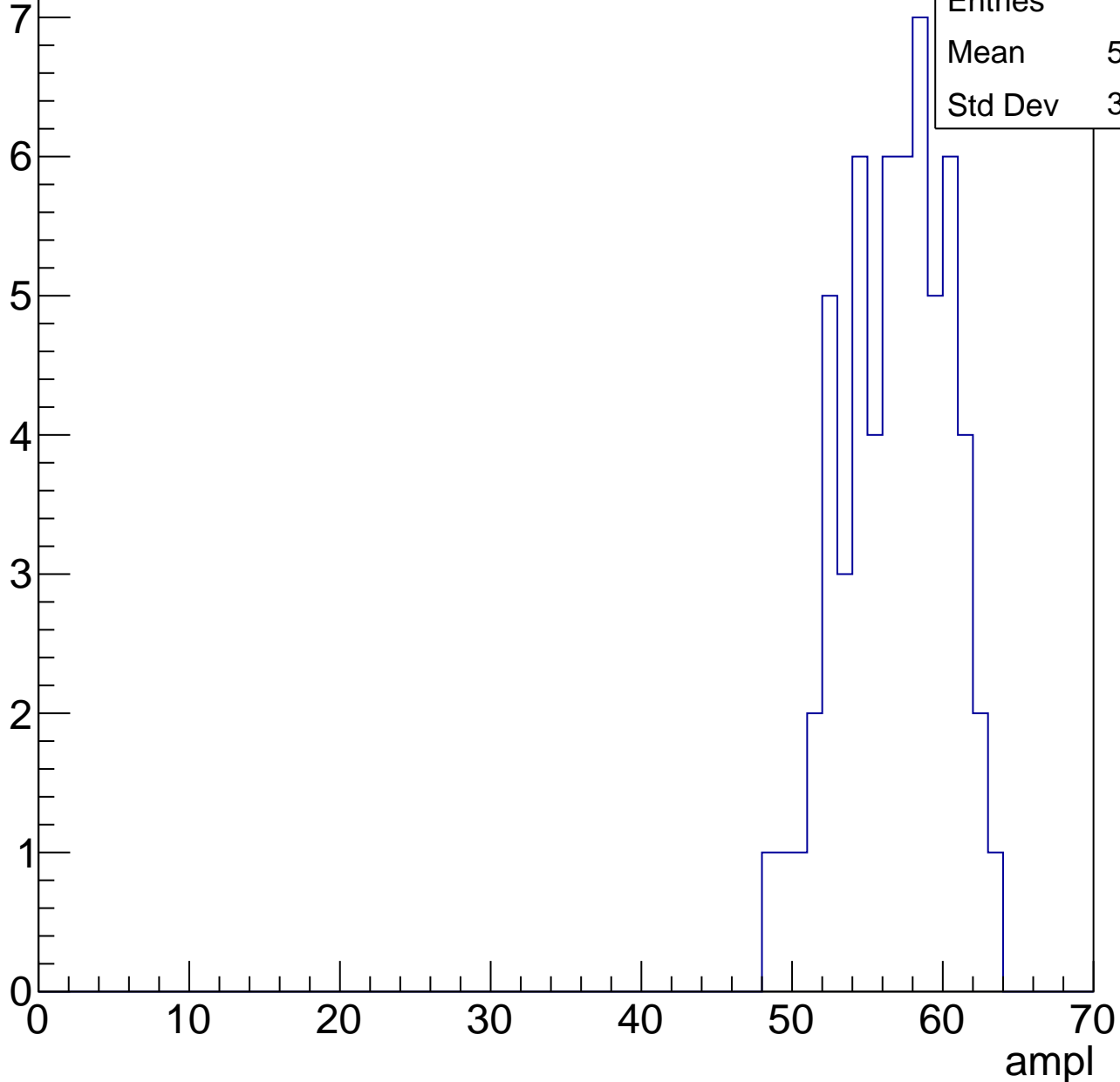
Entries	69
Mean	50.26
Std Dev	3.933



B0L000S, U4-ch109, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



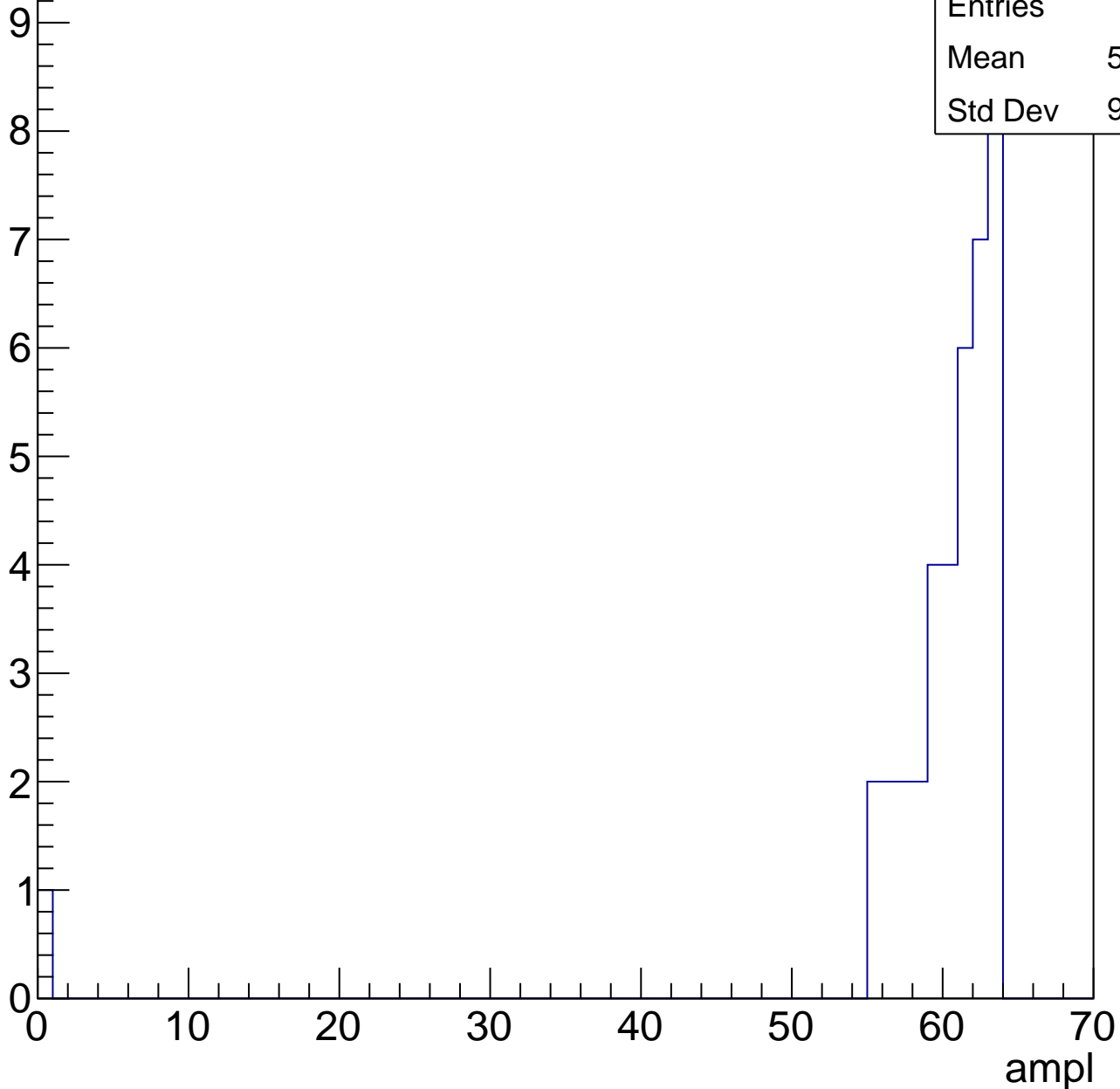
Entries	60
Mean	56.37
Std Dev	3.469

B0L000S, U4-ch109, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.85
Std Dev	9.839



B0L000S, U4-ch109, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch109, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch110, adc0

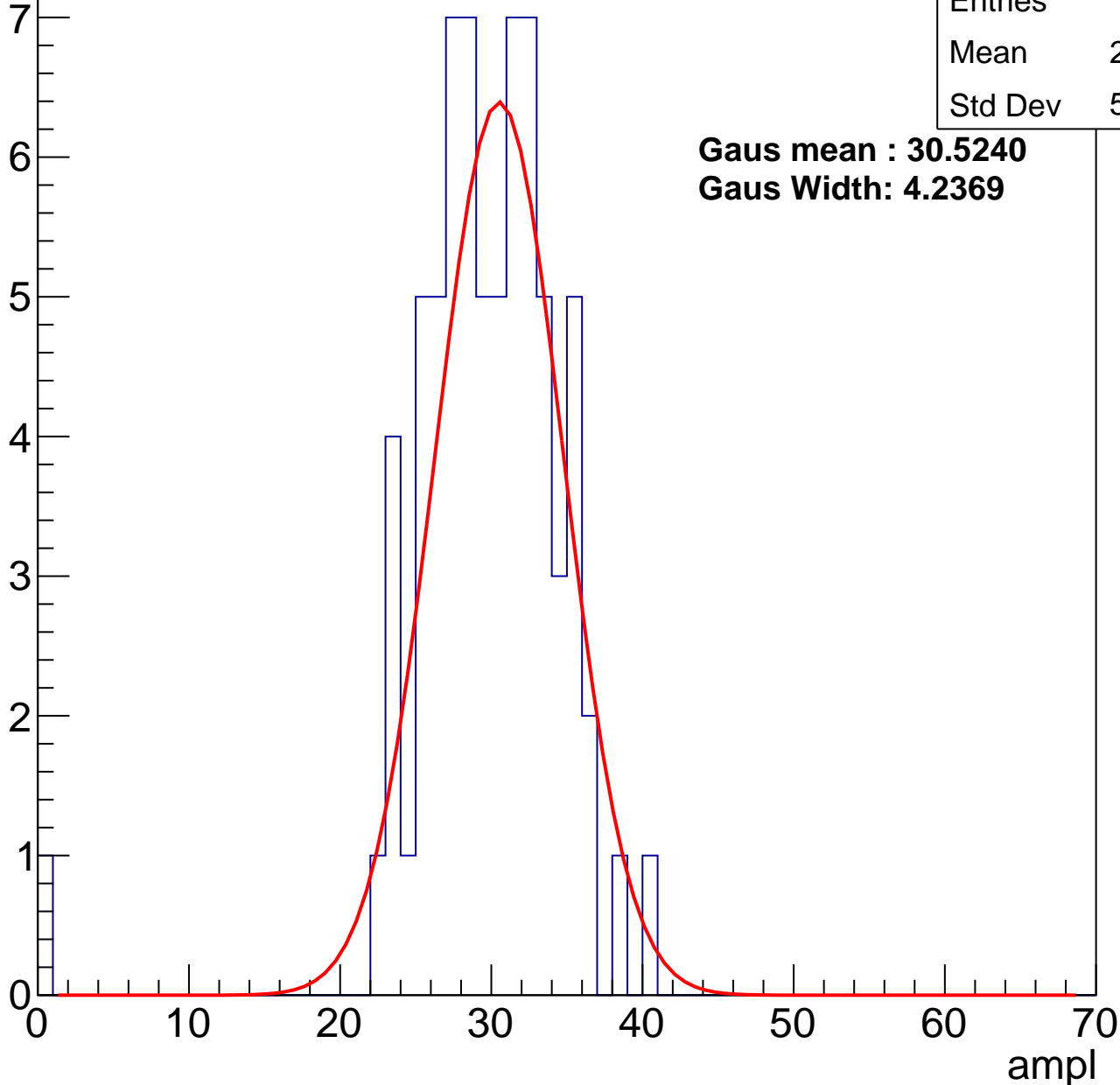
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	29.25
Std Dev	5.198

Gaus mean : 30.5240

Gaus Width: 4.2369



B0L000S, U4-ch110, adc1

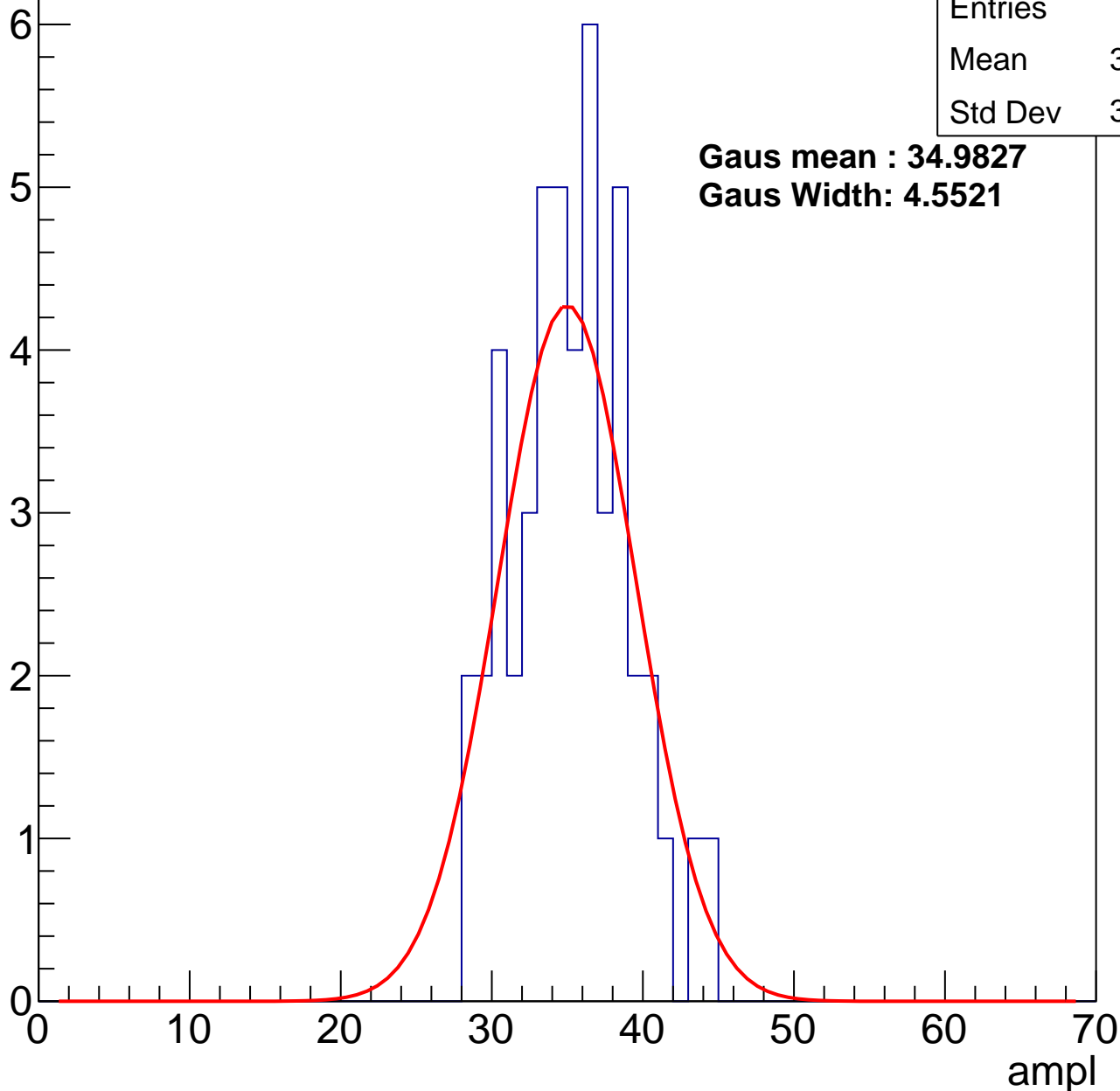
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	34.79
Std Dev	3.758

Gaus mean : 34.9827

Gaus Width: 4.5521



B0L000S, U4-ch110, adc2

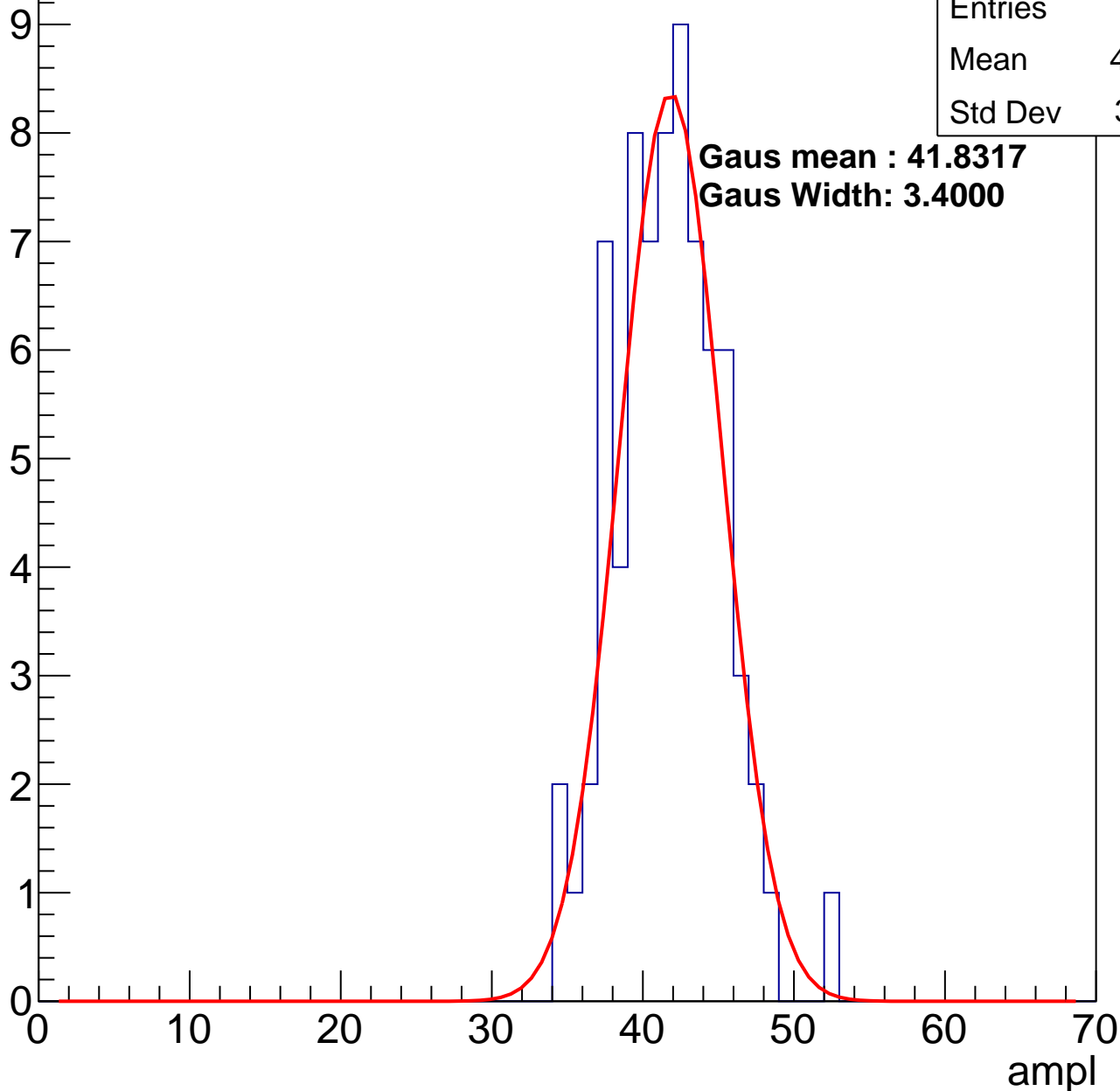
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	41.23
Std Dev	3.431

Gaus mean : 41.8317

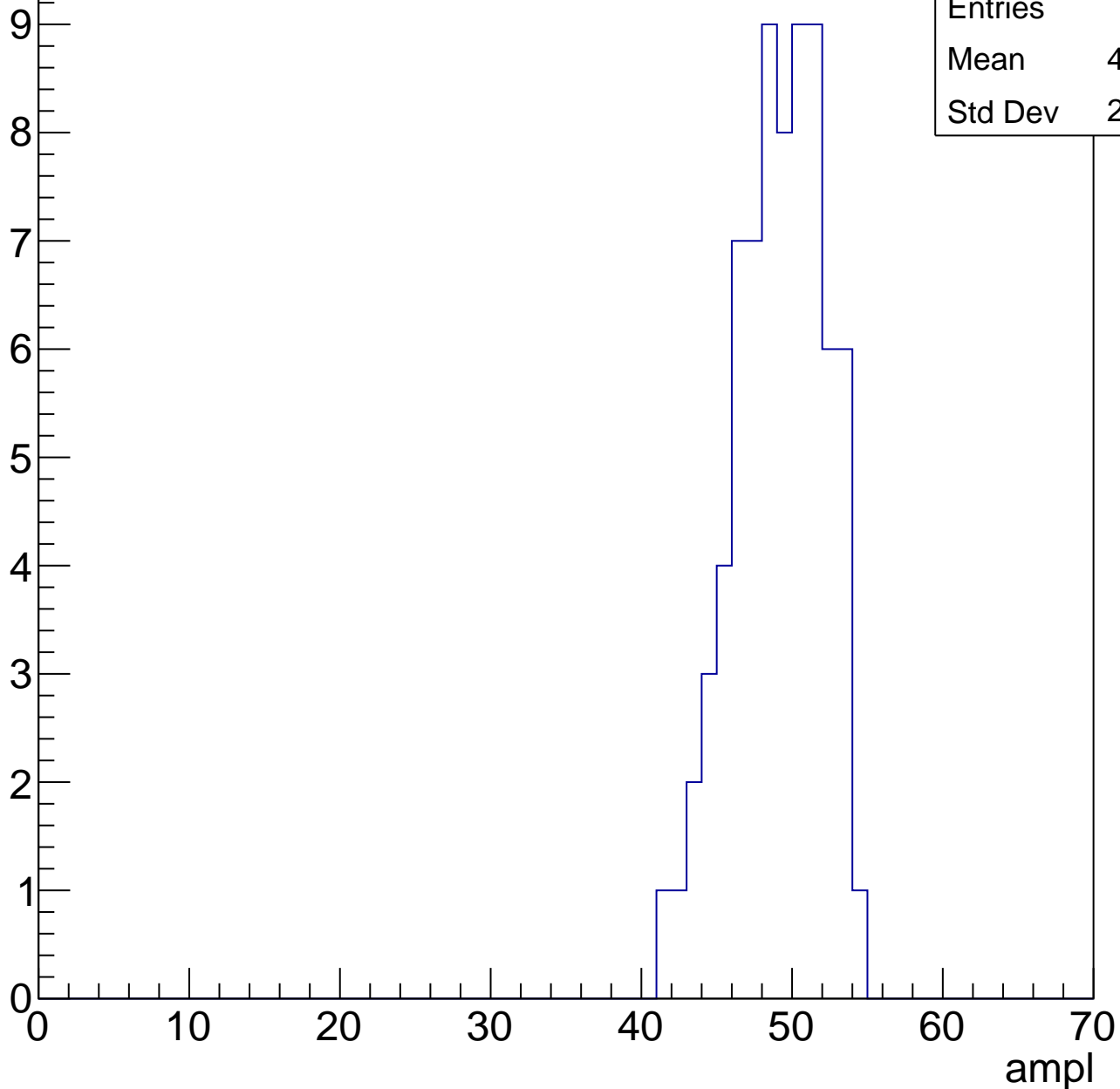
Gaus Width: 3.4000



B0L000S, U4-ch110, adc3

calib_packv5_042523_0143.root, FC#5, port B1

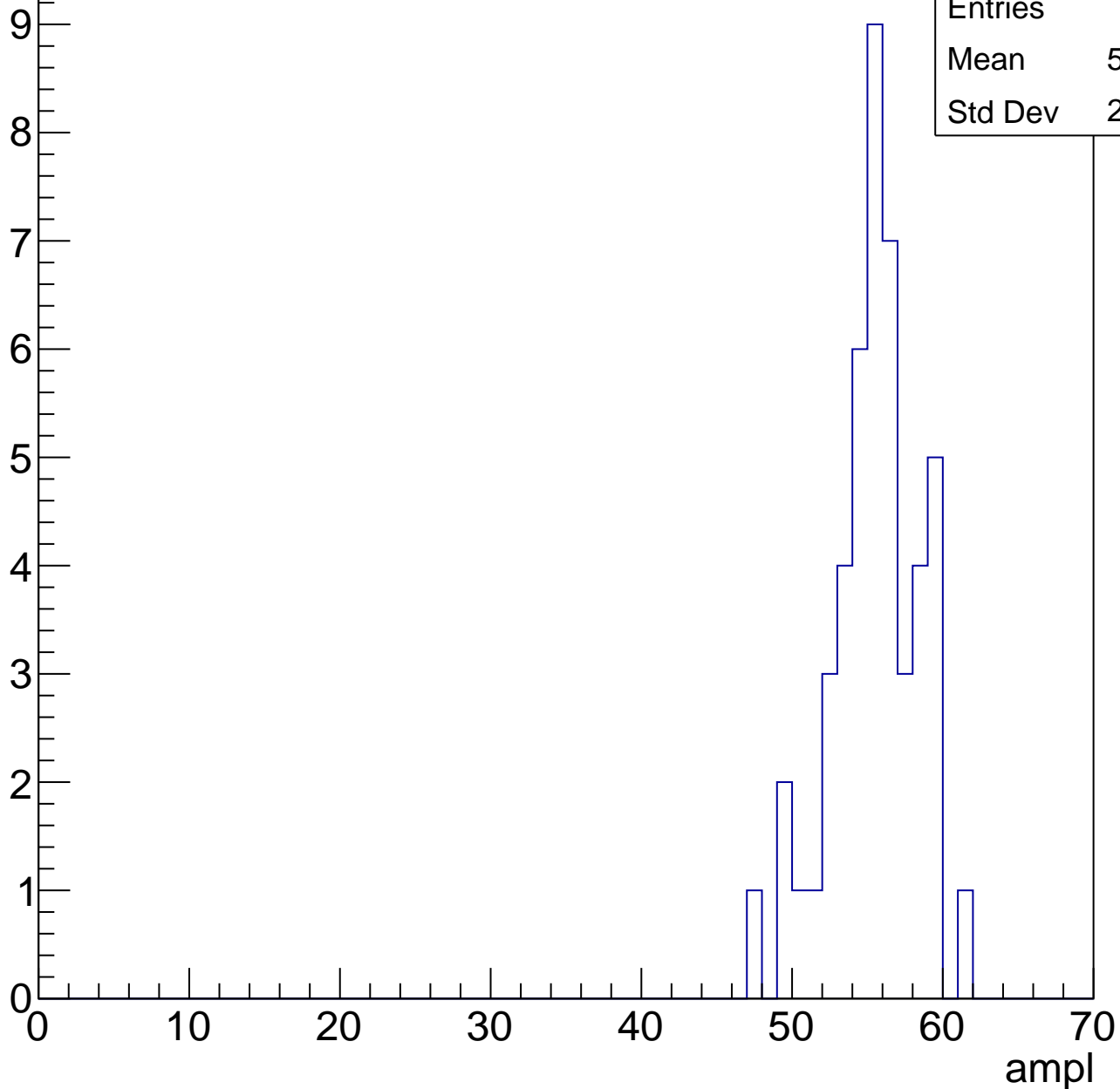
Entry



B0L000S, U4-ch110, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	47
Mean	54.98
Std Dev	2.906

B0L000S, U4-ch110, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	54
Mean	58.35
Std Dev	8.409

10

8

6

4

2

0

0

10

20

30

40

50

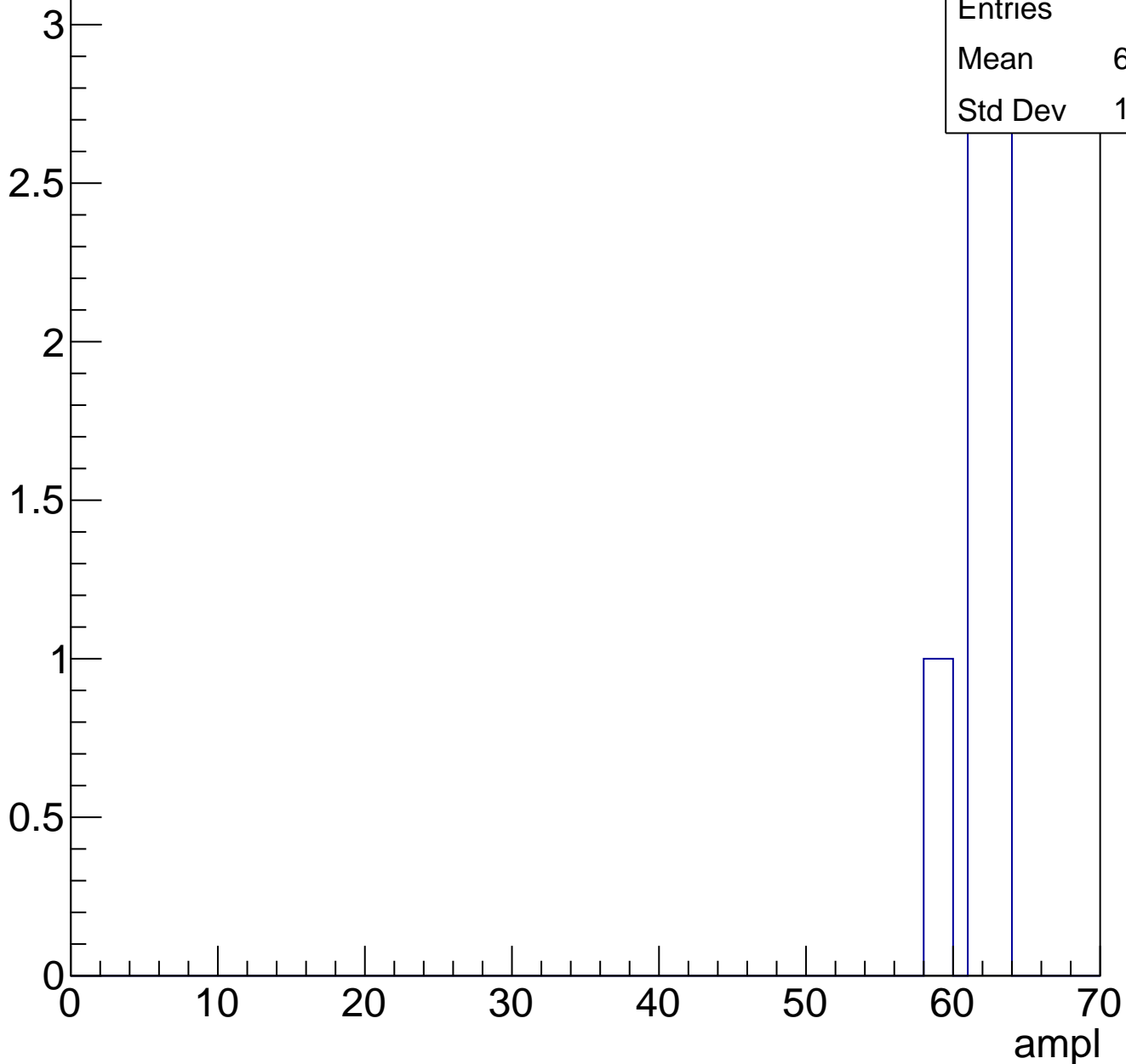
60

ampl

B0L000S, U4-ch110, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch110, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch111, adc0

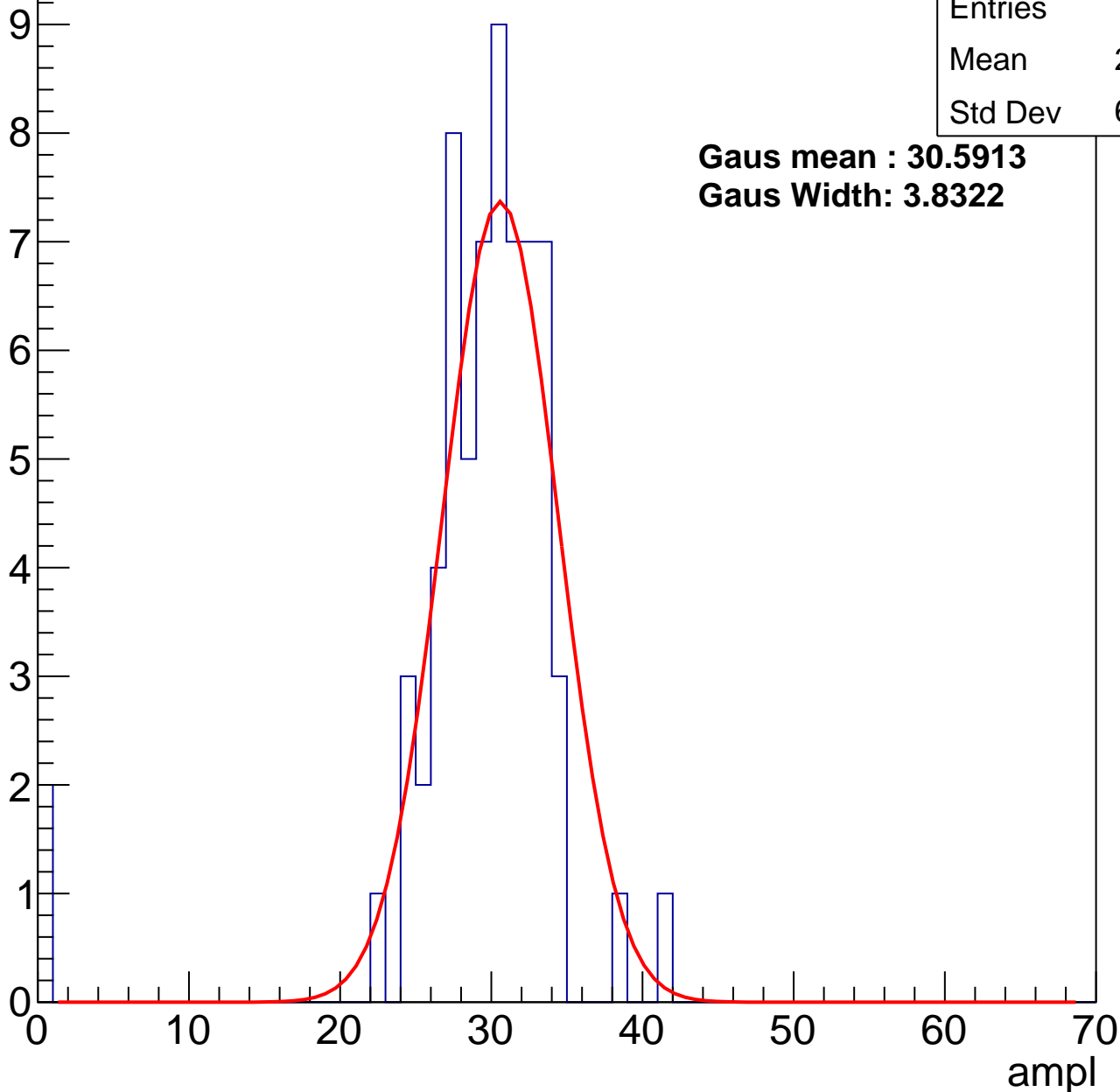
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.81
Std Dev	6.011

Gaus mean : 30.5913

Gaus Width: 3.8322



B0L000S, U4-ch111, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	70
Mean	36.89
Std Dev	3.205

Gaus mean : 37.1836

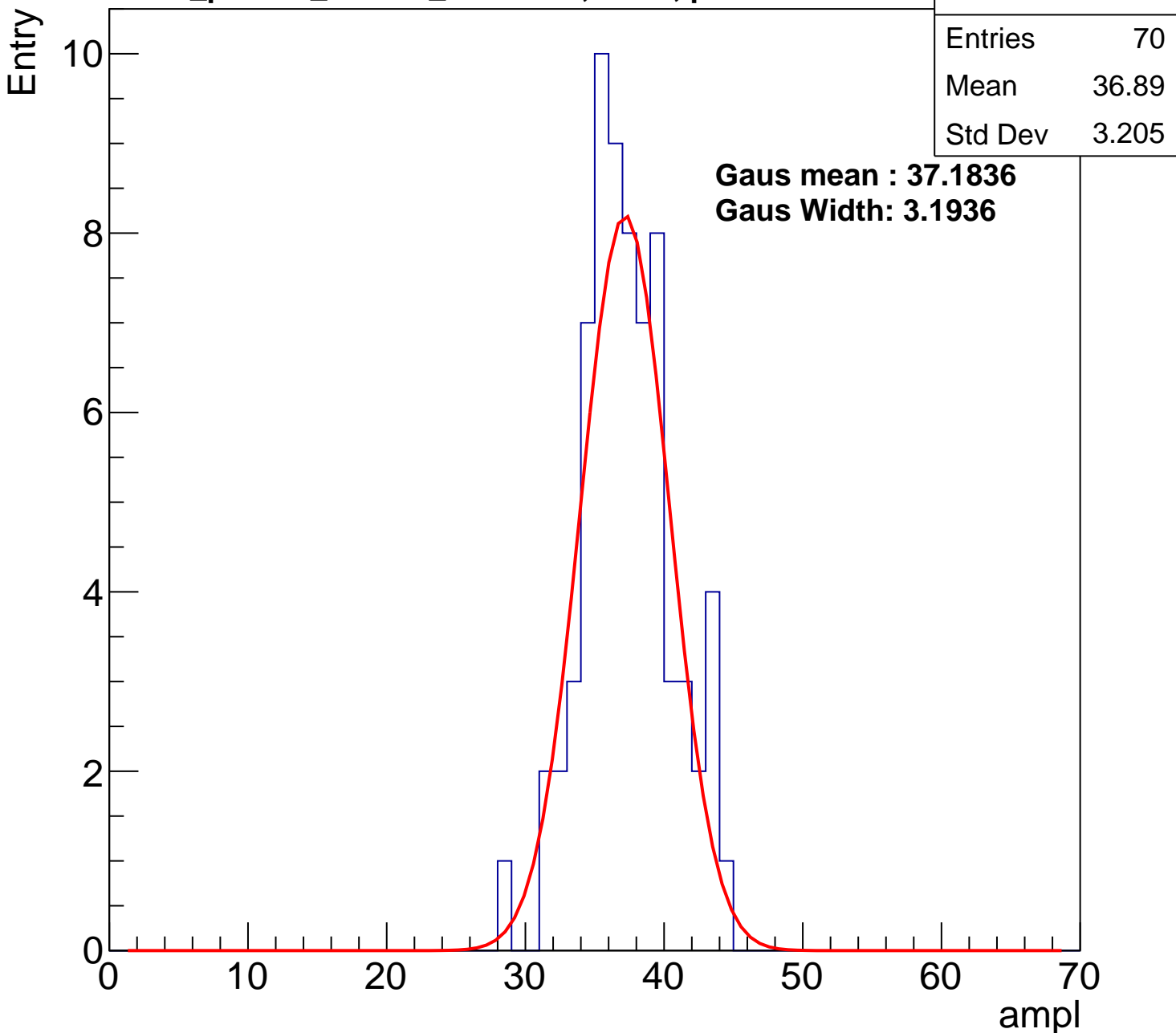
Gaus Width: 3.1936

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch111, adc2

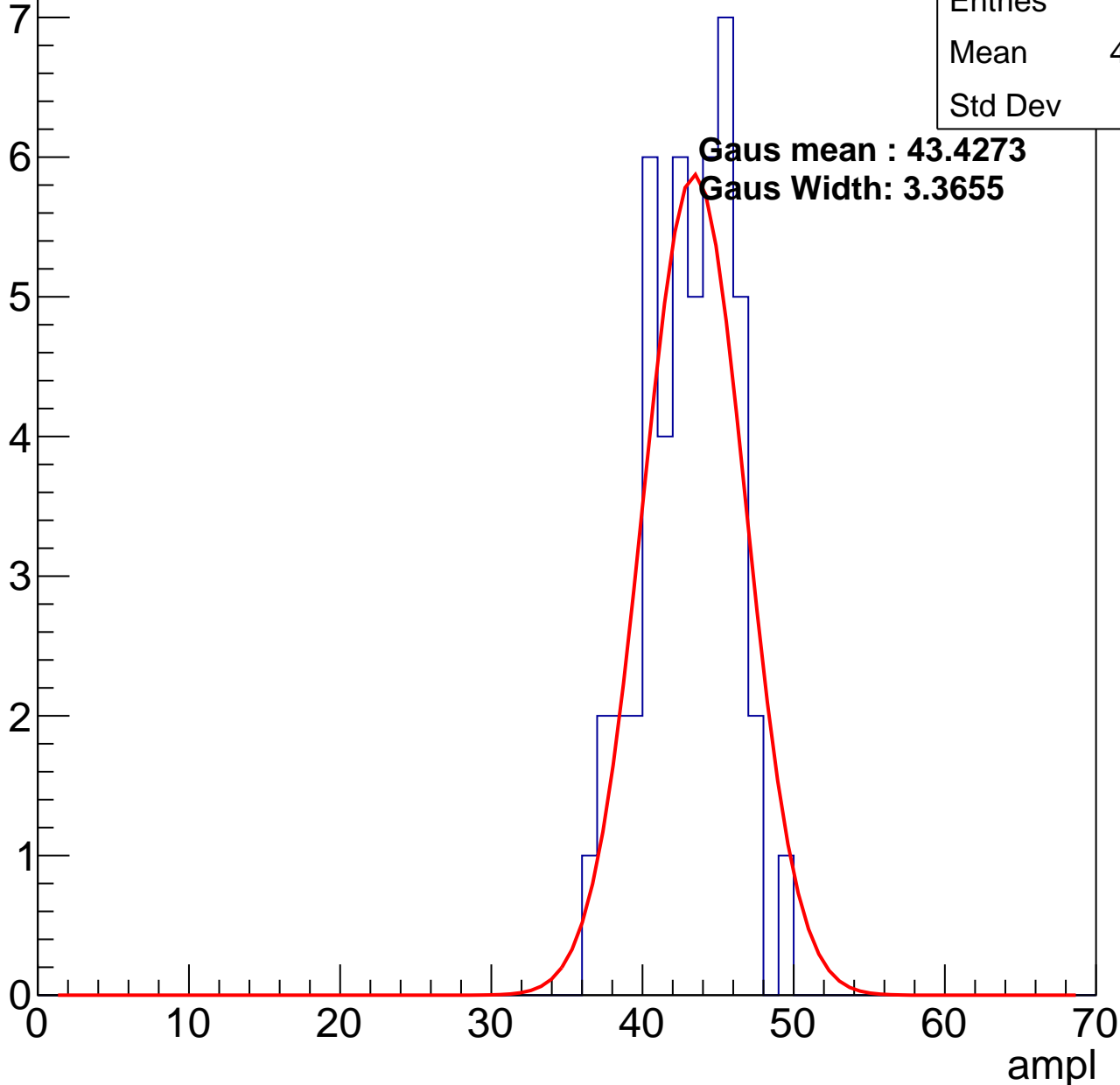
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	42.59
Std Dev	2.92

Gaus mean : 43.4273

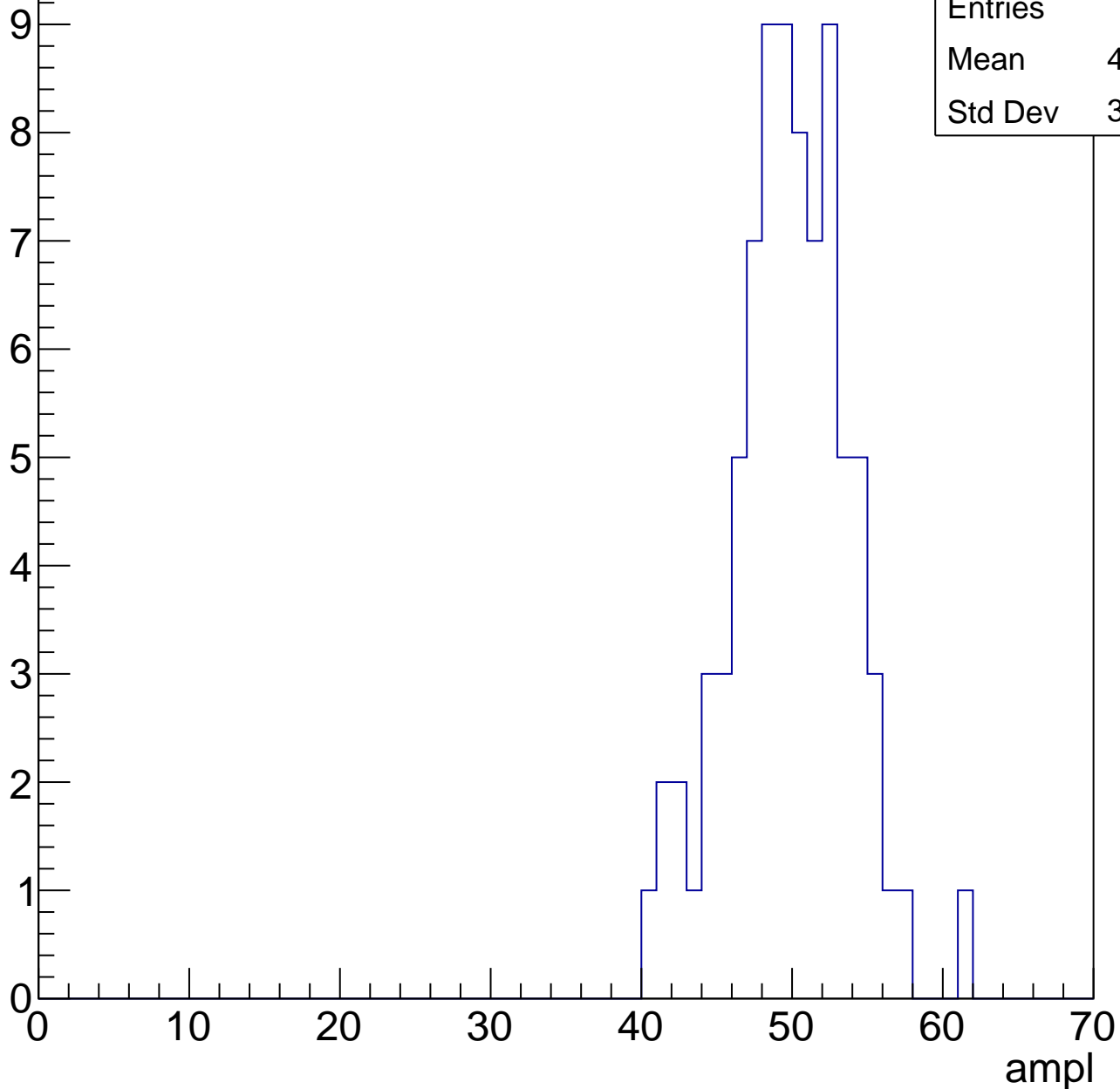
Gaus Width: 3.3655



B0L000S, U4-ch111, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



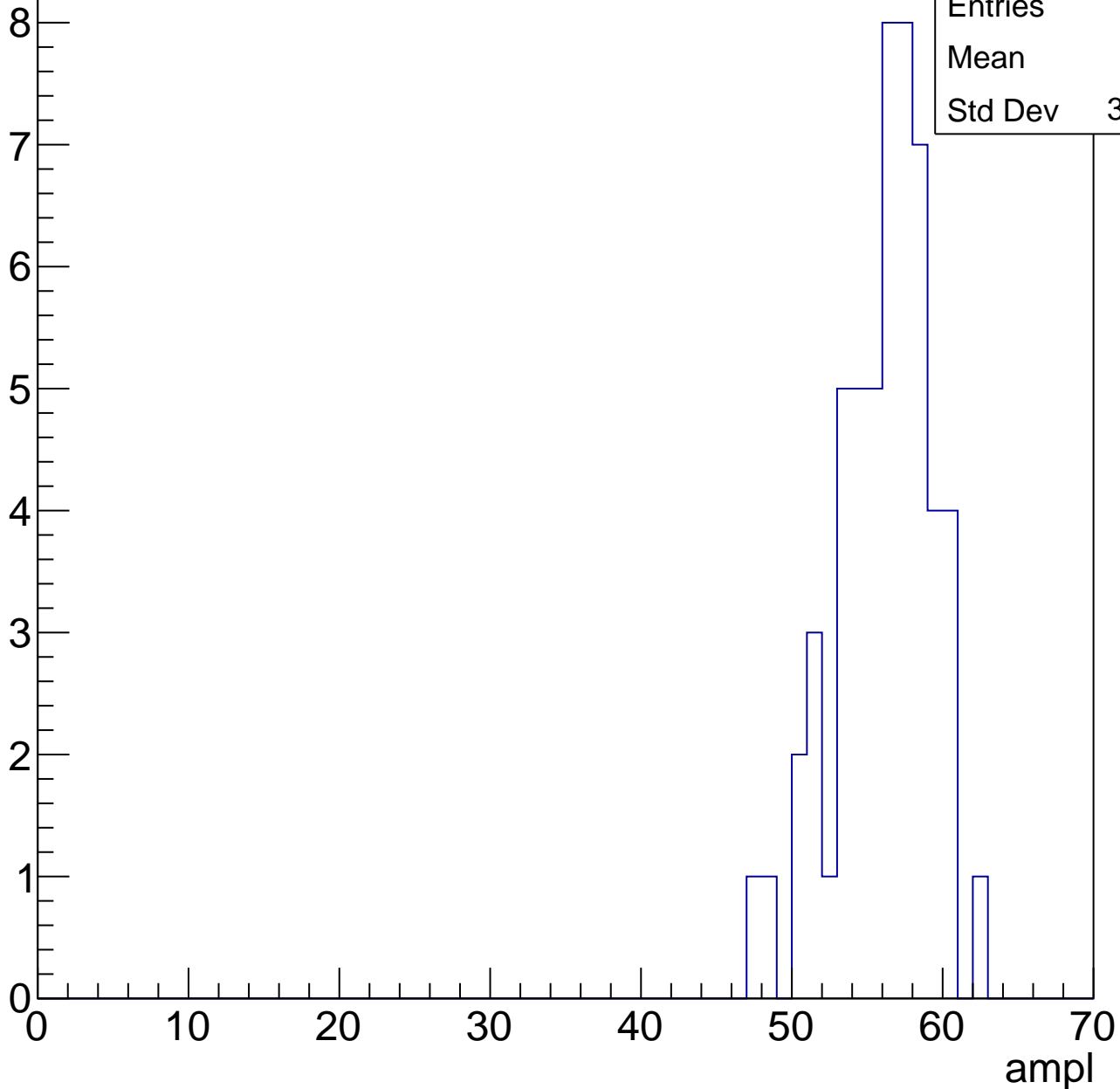
Entries	82
Mean	49.35
Std Dev	3.865

B0L000S, U4-ch111, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.6
Std Dev	3.137

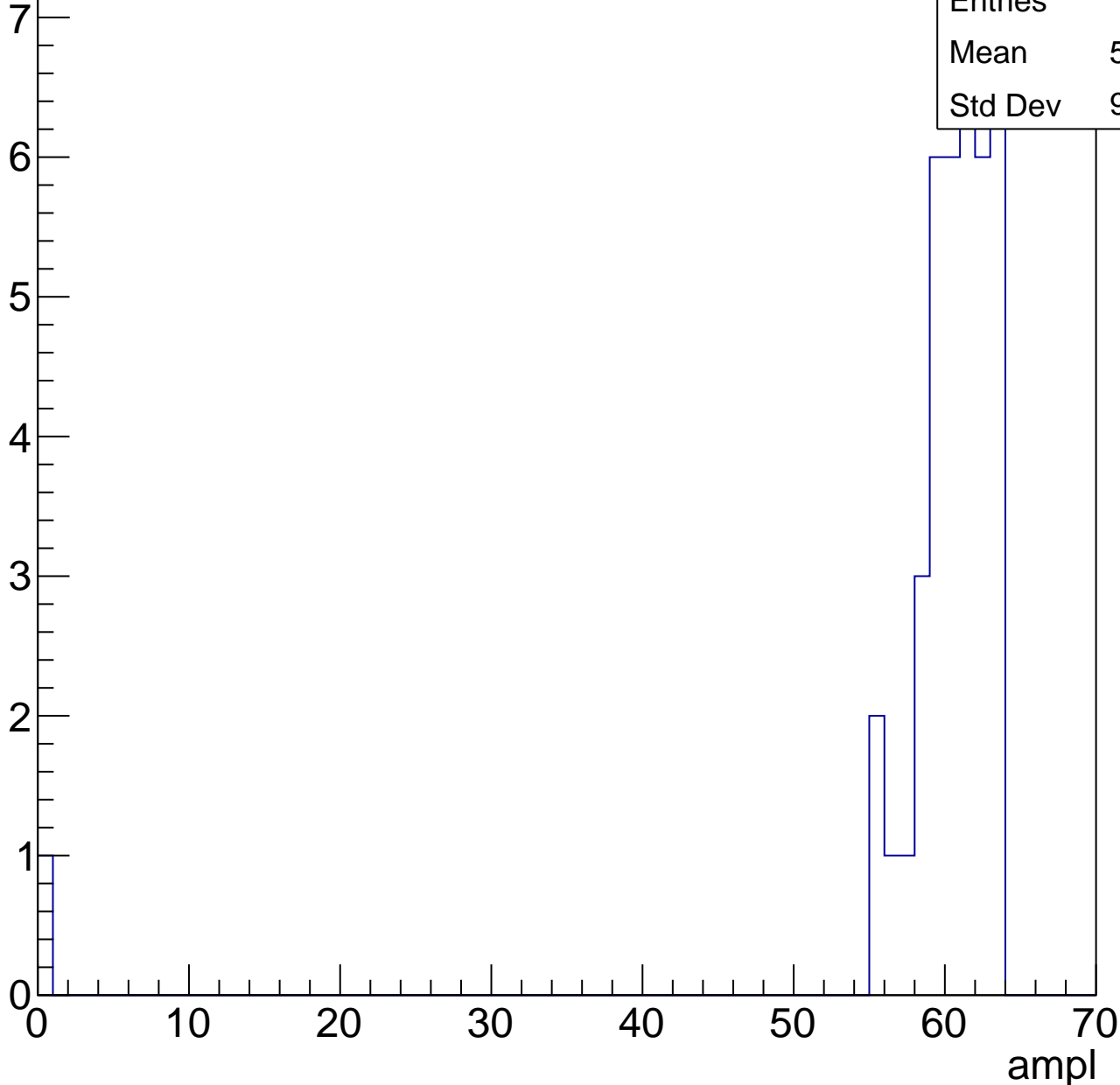


B0L000S, U4-ch111, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

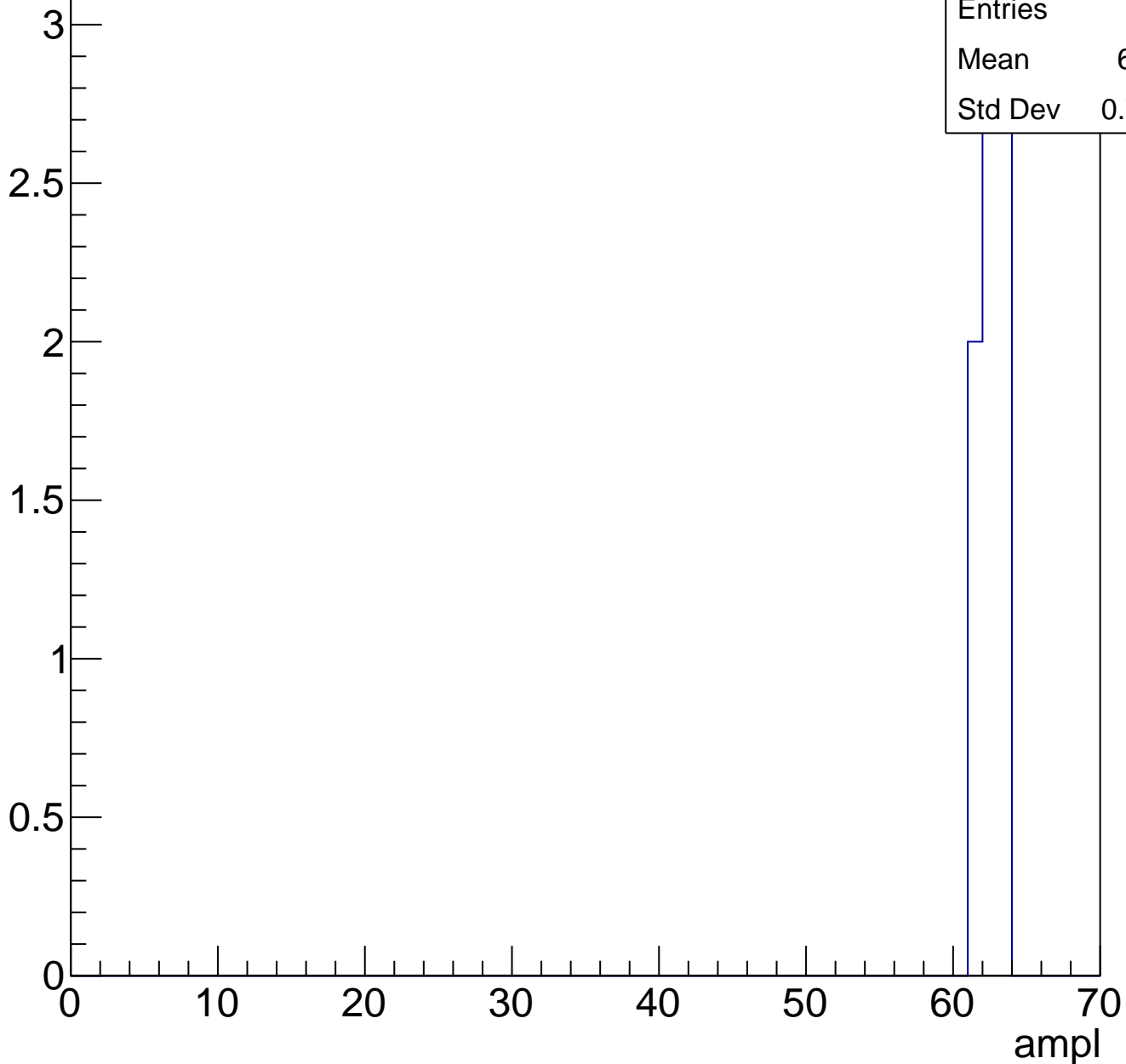
Entries	40
Mean	58.77
Std Dev	9.653



B0L000S, U4-ch111, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch111, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch112, adc0

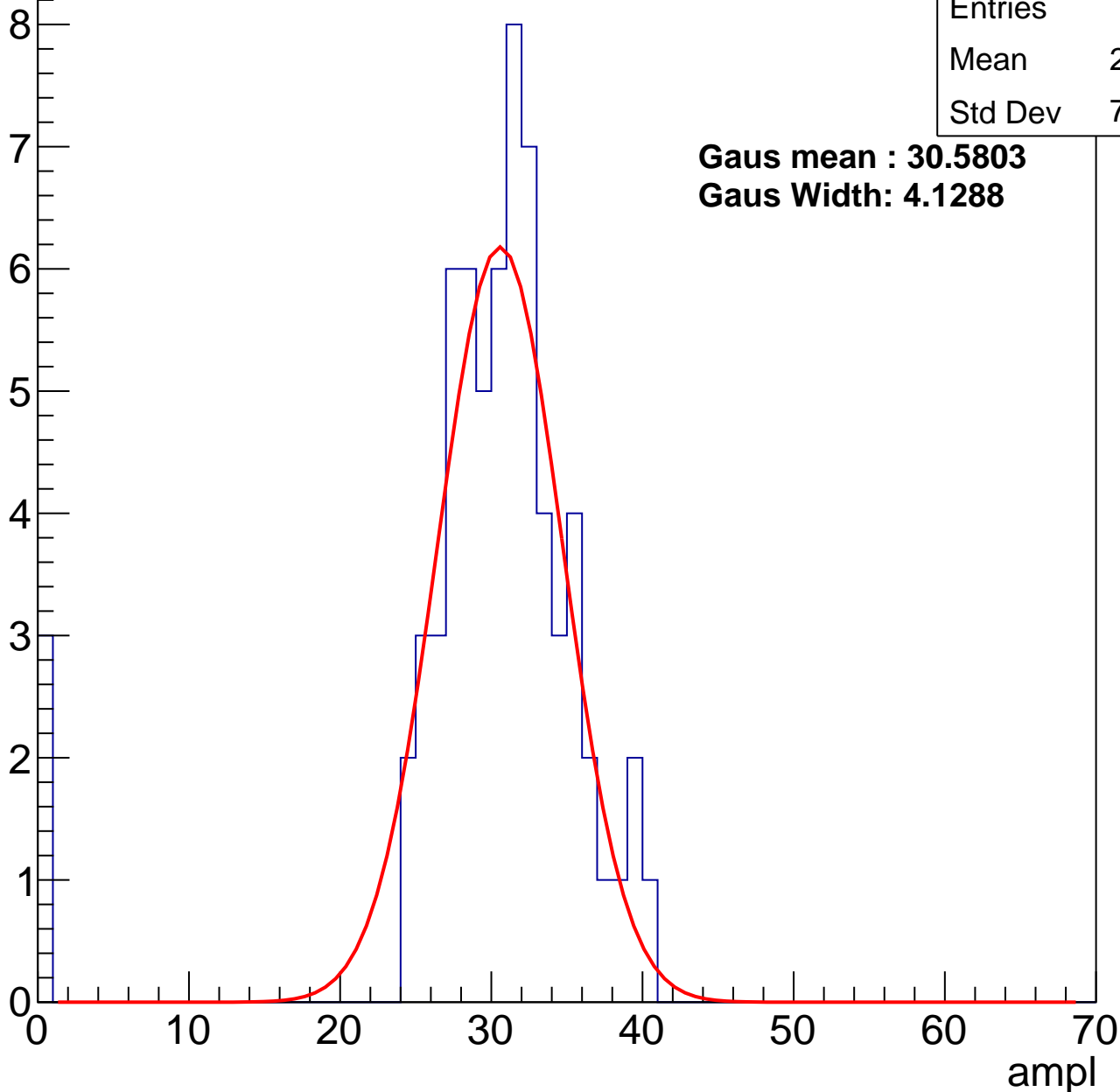
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.36
Std Dev	7.348

Gaus mean : 30.5803

Gaus Width: 4.1288



B0L000S, U4-ch112, adc1

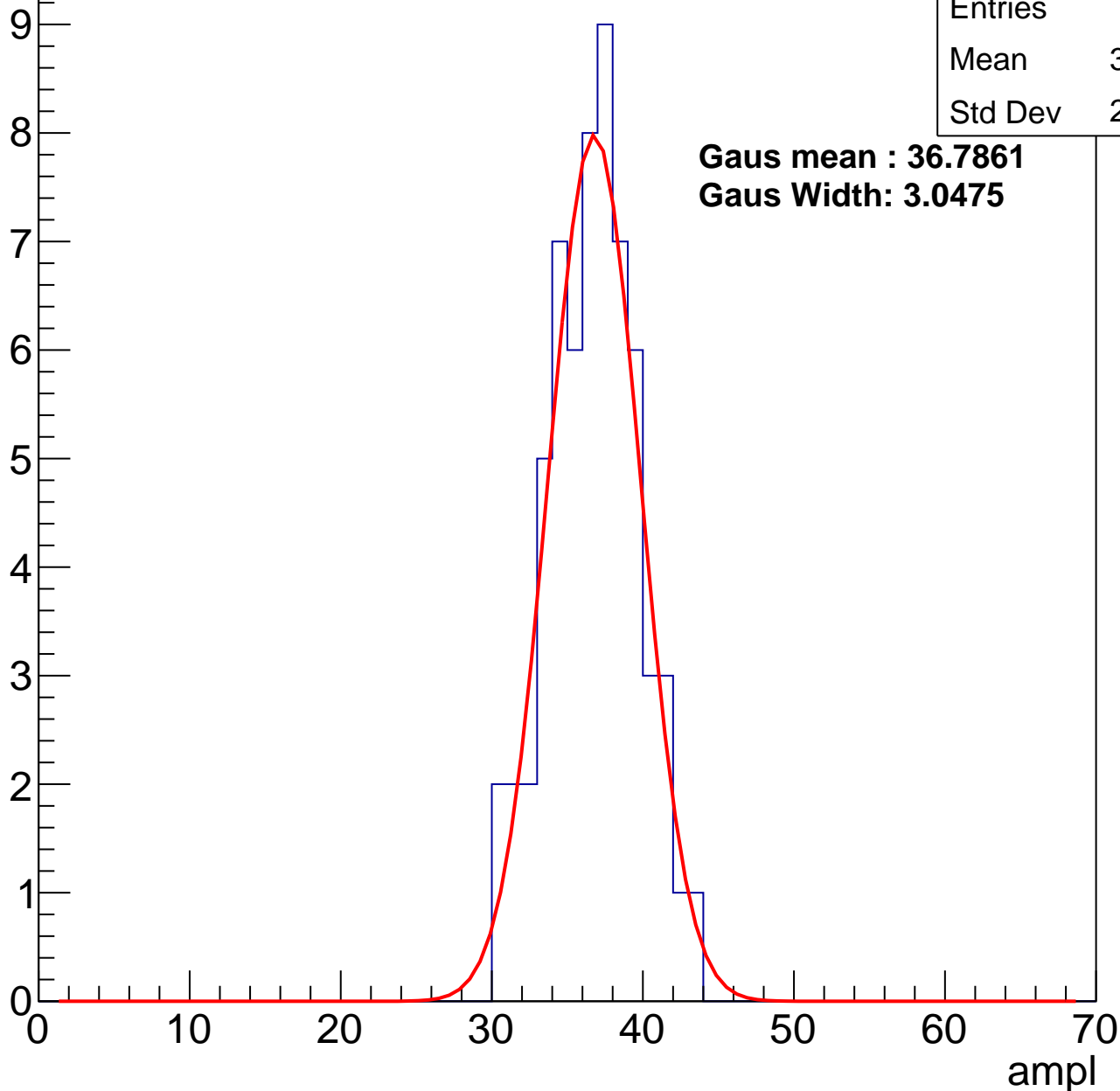
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.26
Std Dev	2.918

Gaus mean : 36.7861

Gaus Width: 3.0475



B0L000S, U4-ch112, adc2

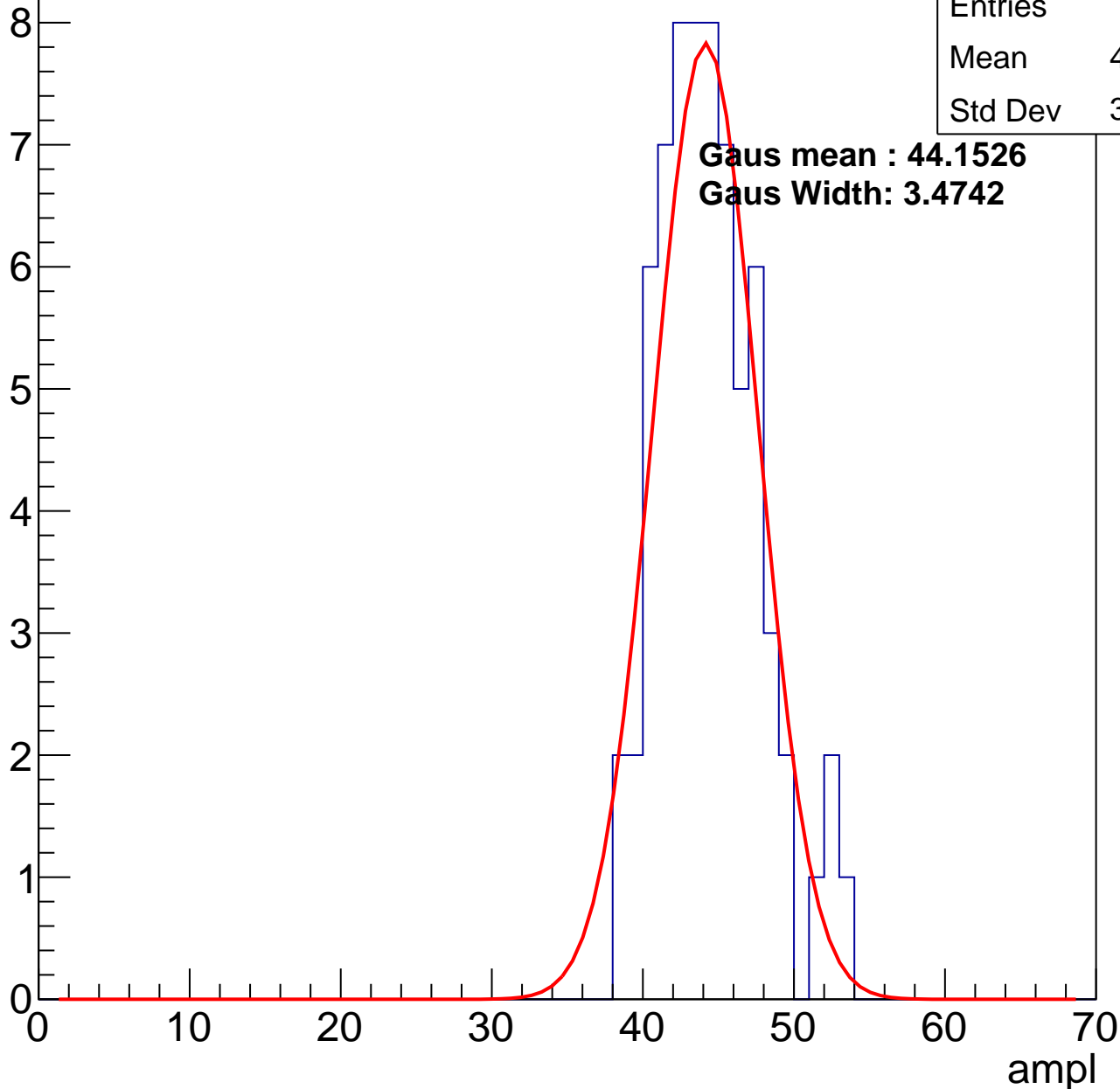
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.97
Std Dev	3.339

Gaus mean : 44.1526

Gaus Width: 3.4742

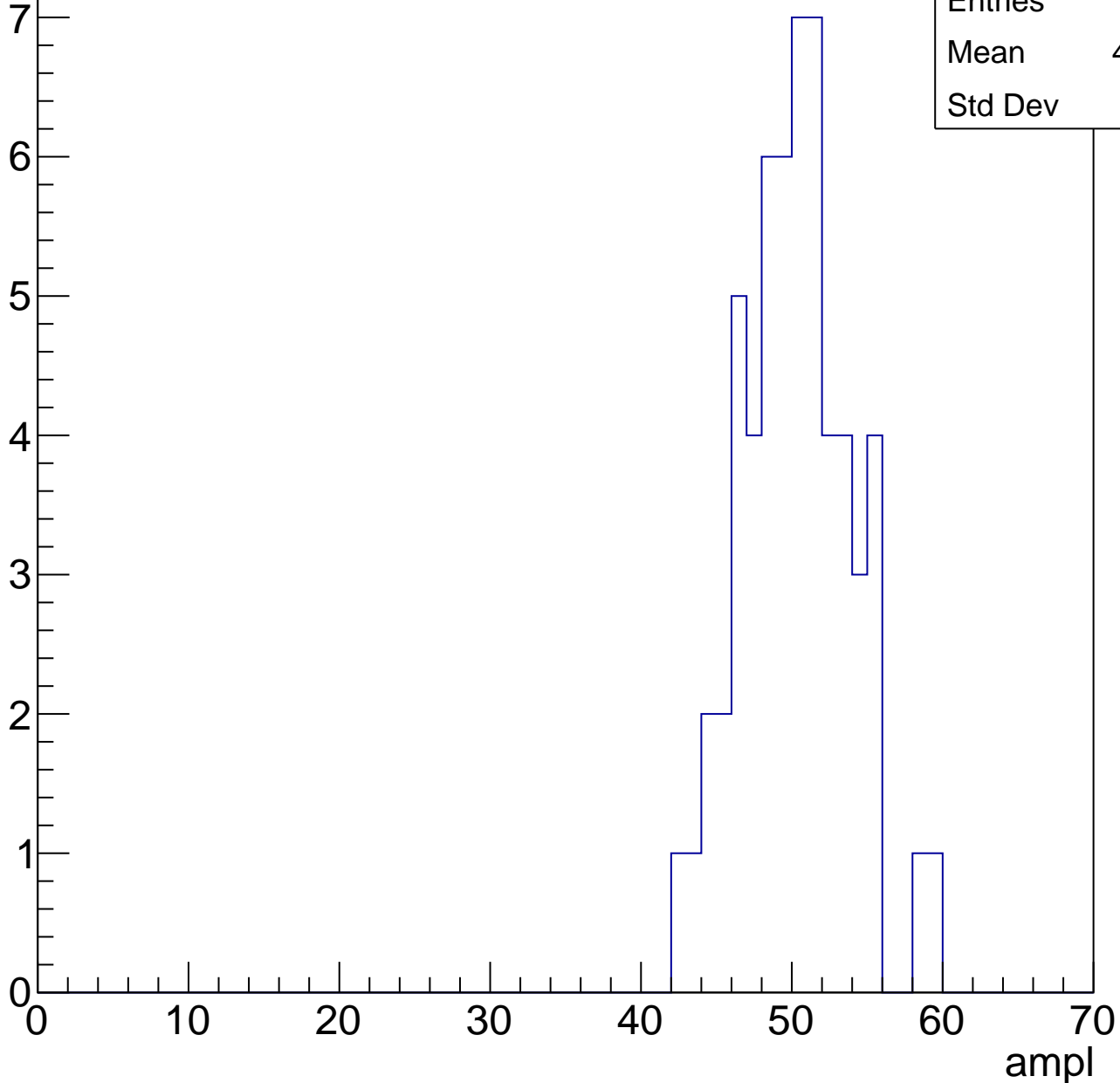


B0L000S, U4-ch112, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	49.81
Std Dev	3.55

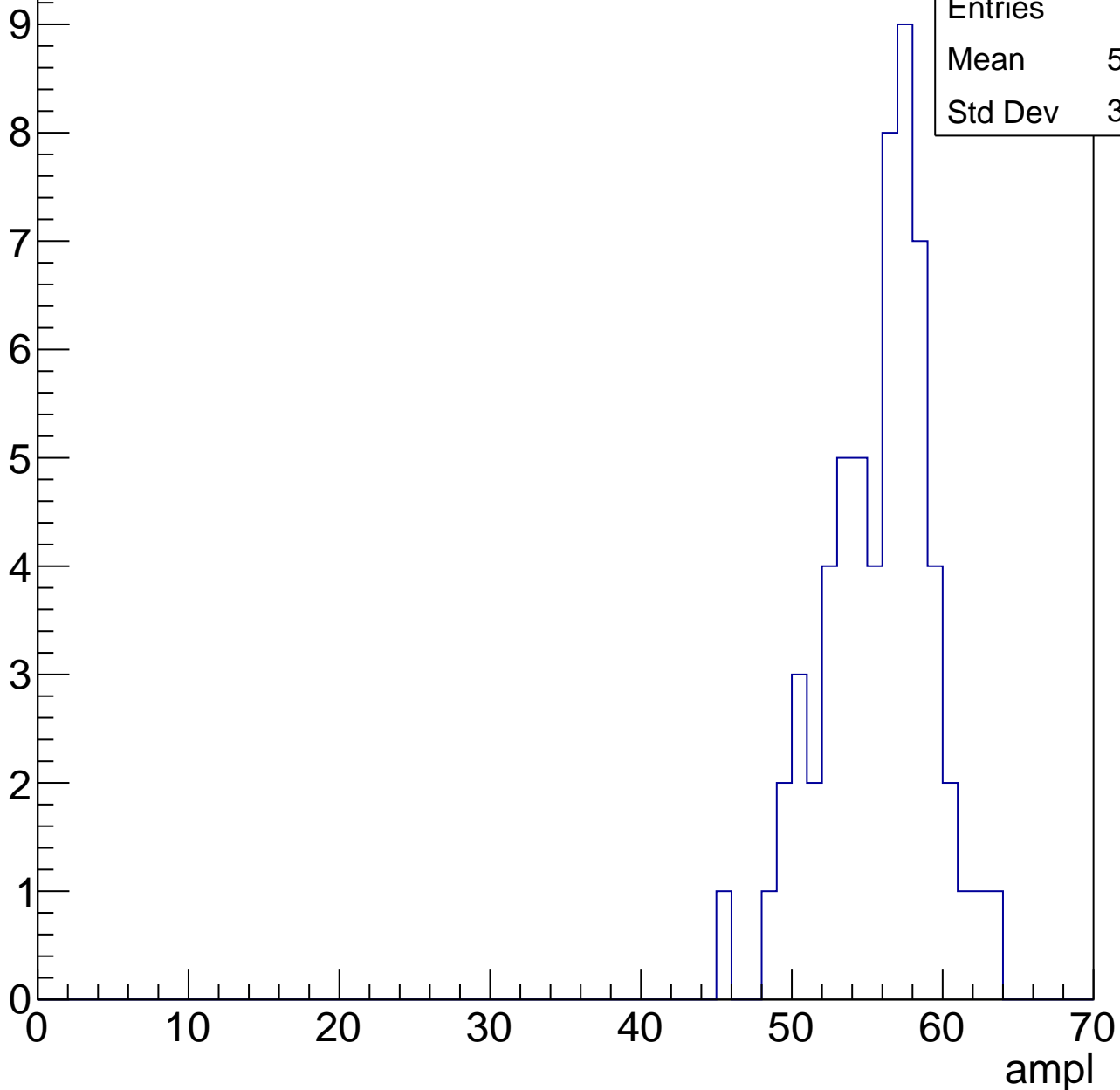


B0L000S, U4-ch112, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	55.25
Std Dev	3.548

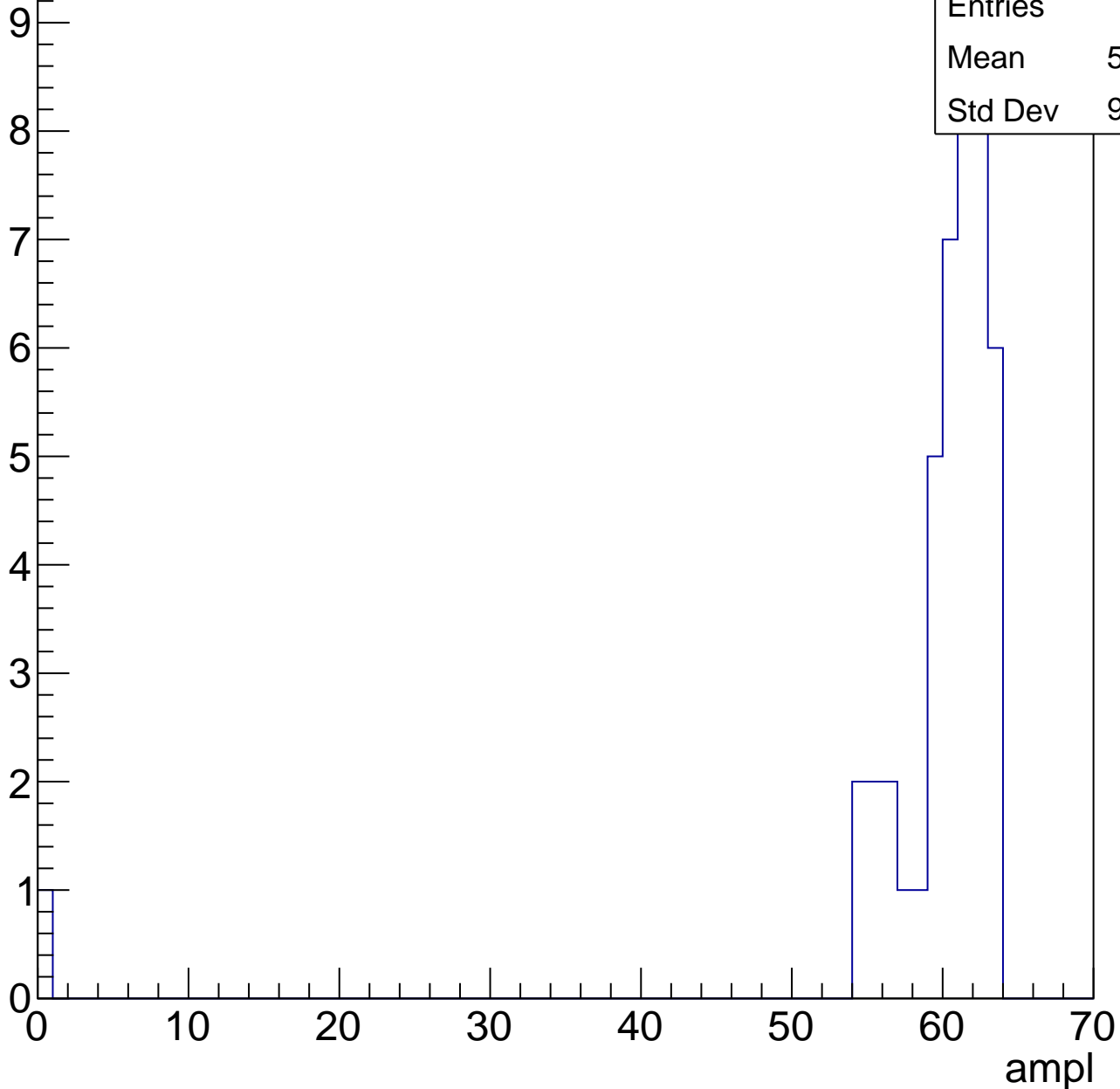


B0L000S, U4-ch112, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

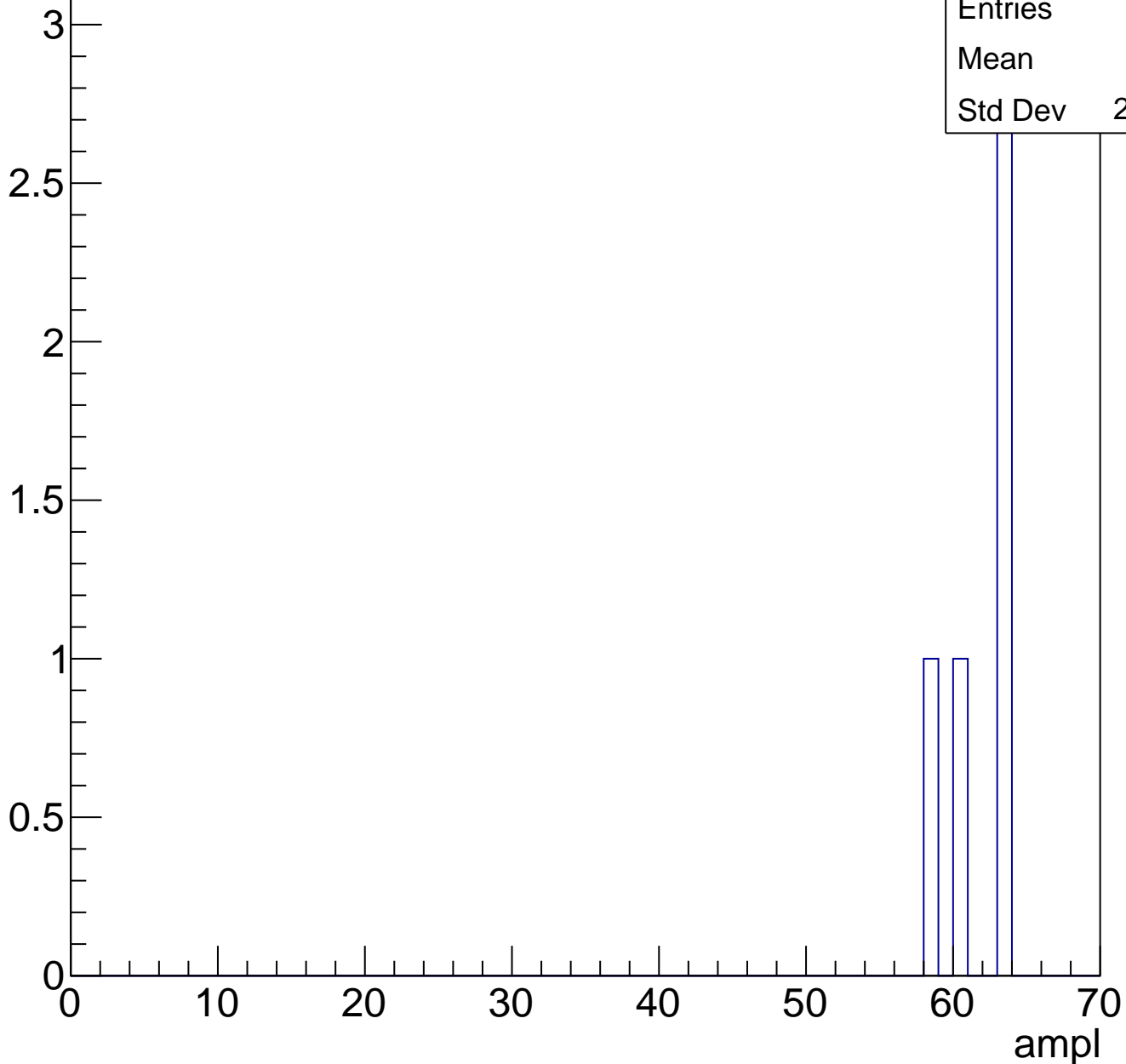
Entries	45
Mean	58.78
Std Dev	9.194



B0L000S, U4-ch112, adc6

calib_packv5_042523_0143.root, FC#5, port B1

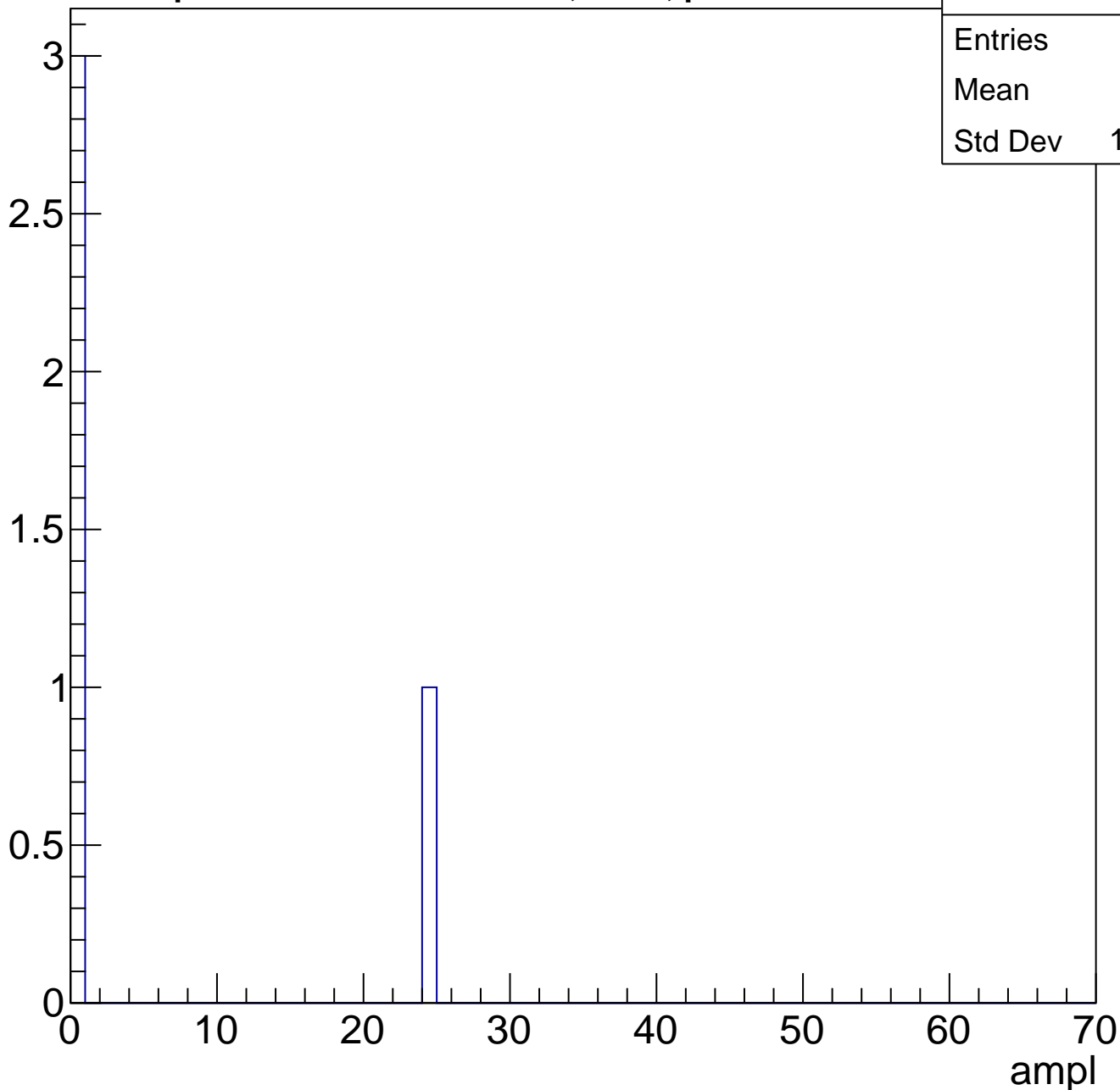
Entry



B0L000S, U4-ch112, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch113, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	87
Mean	30.57
Std Dev	4.891

Gaus mean : 31.6637

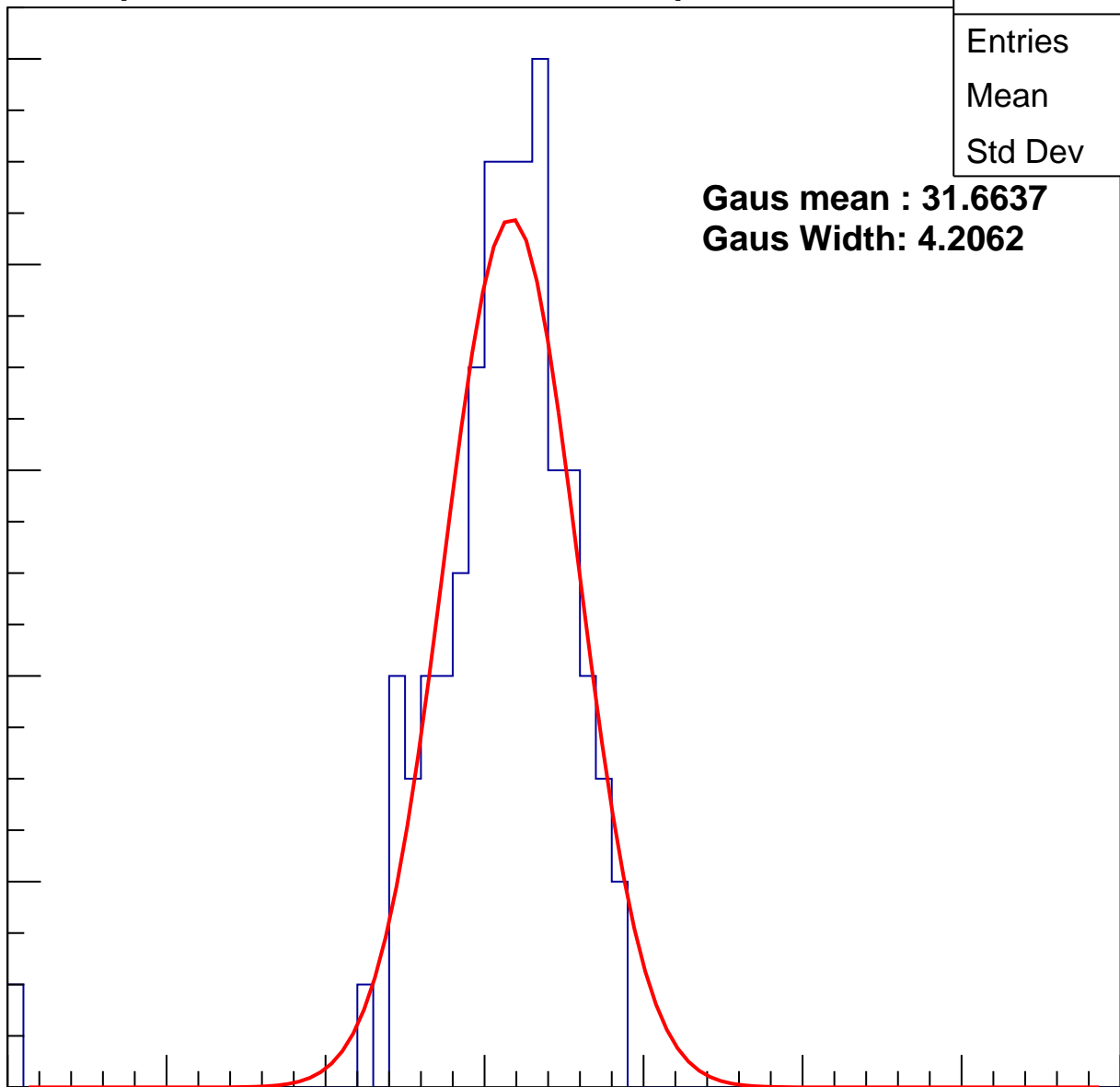
Gaus Width: 4.2062

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch113, adc1

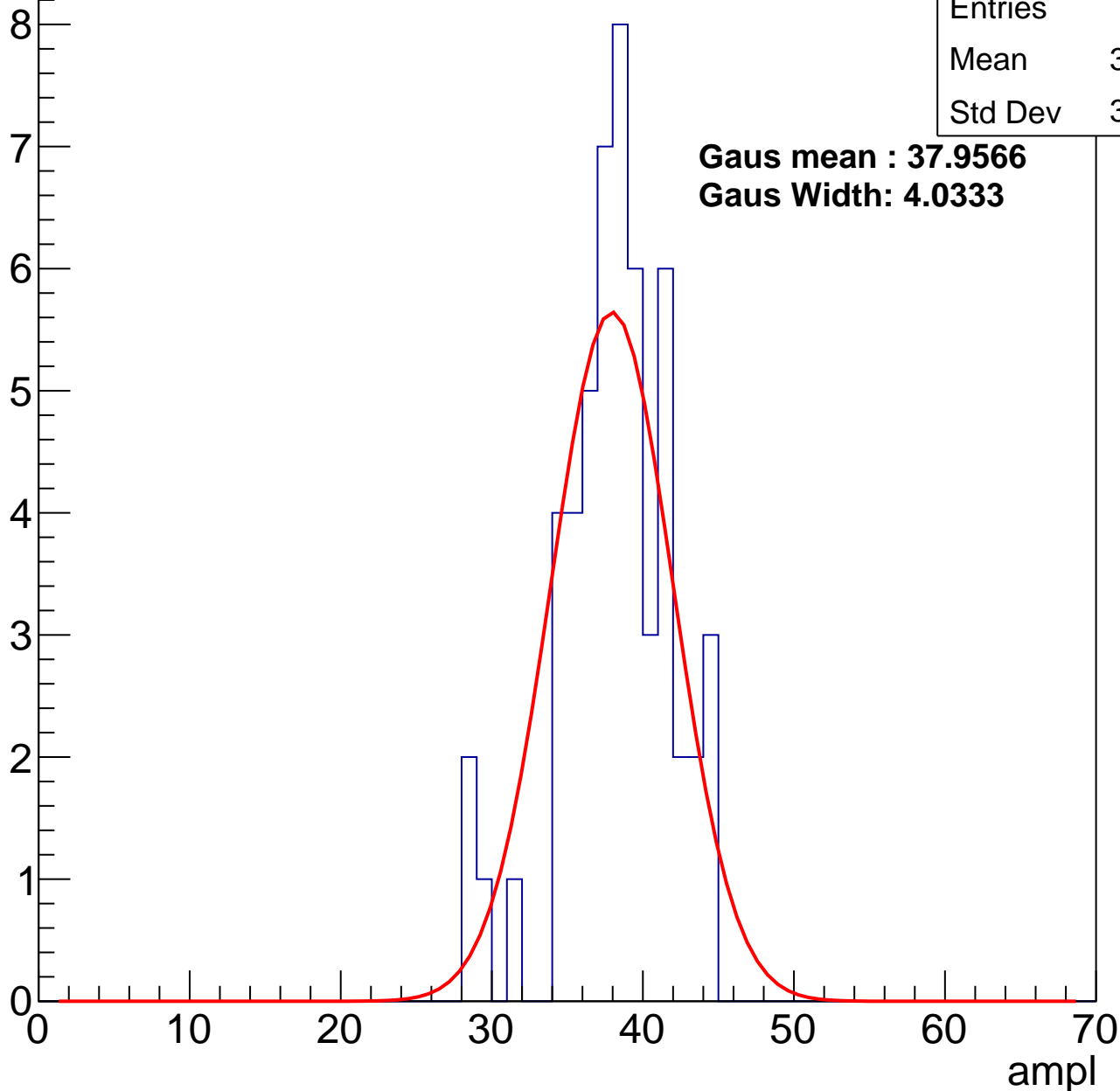
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	37.72
Std Dev	3.638

Gaus mean : 37.9566

Gaus Width: 4.0333



B0L000S, U4-ch113, adc2

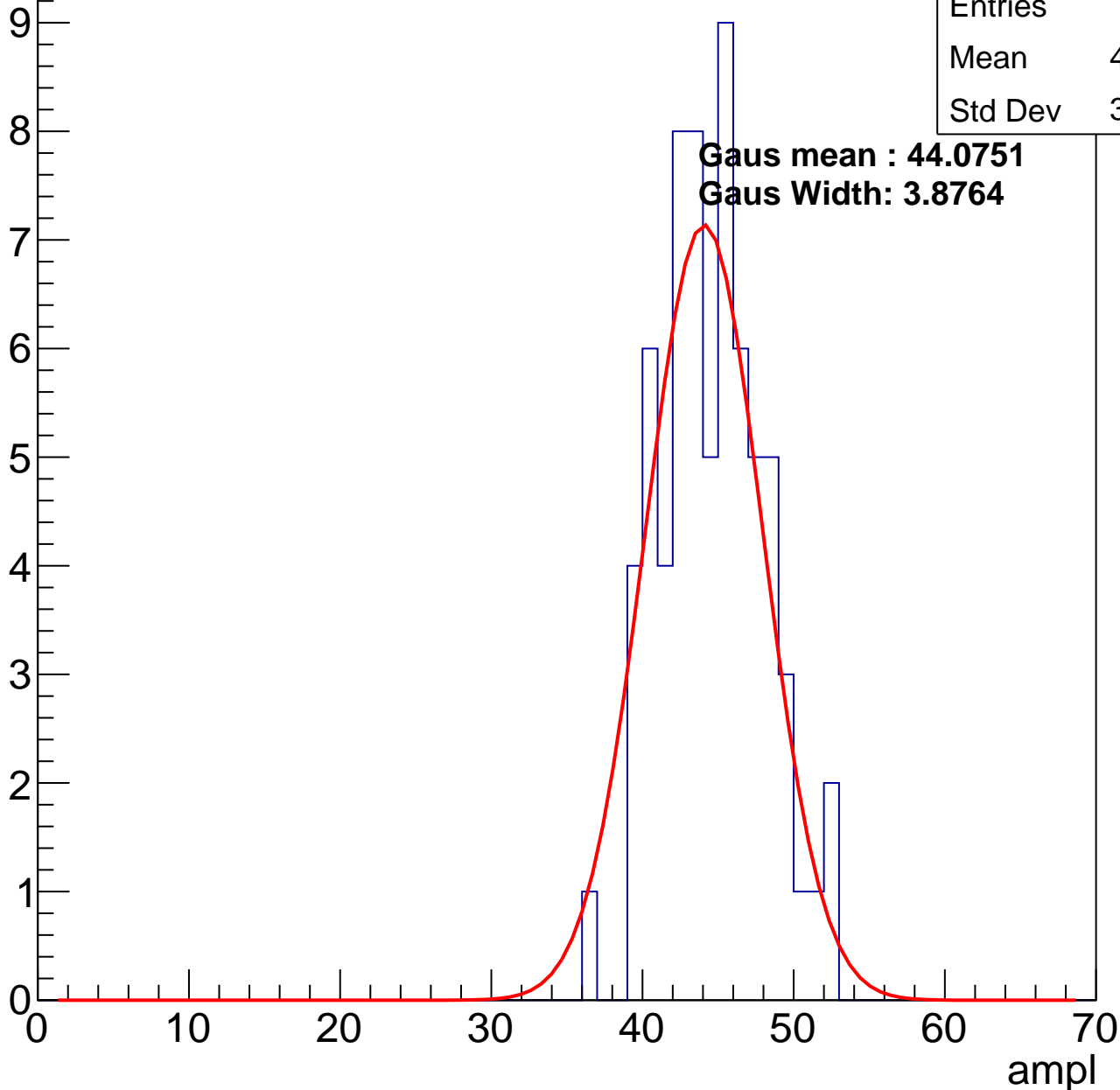
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	44.18
Std Dev	3.387

Gaus mean : 44.0751

Gaus Width: 3.8764

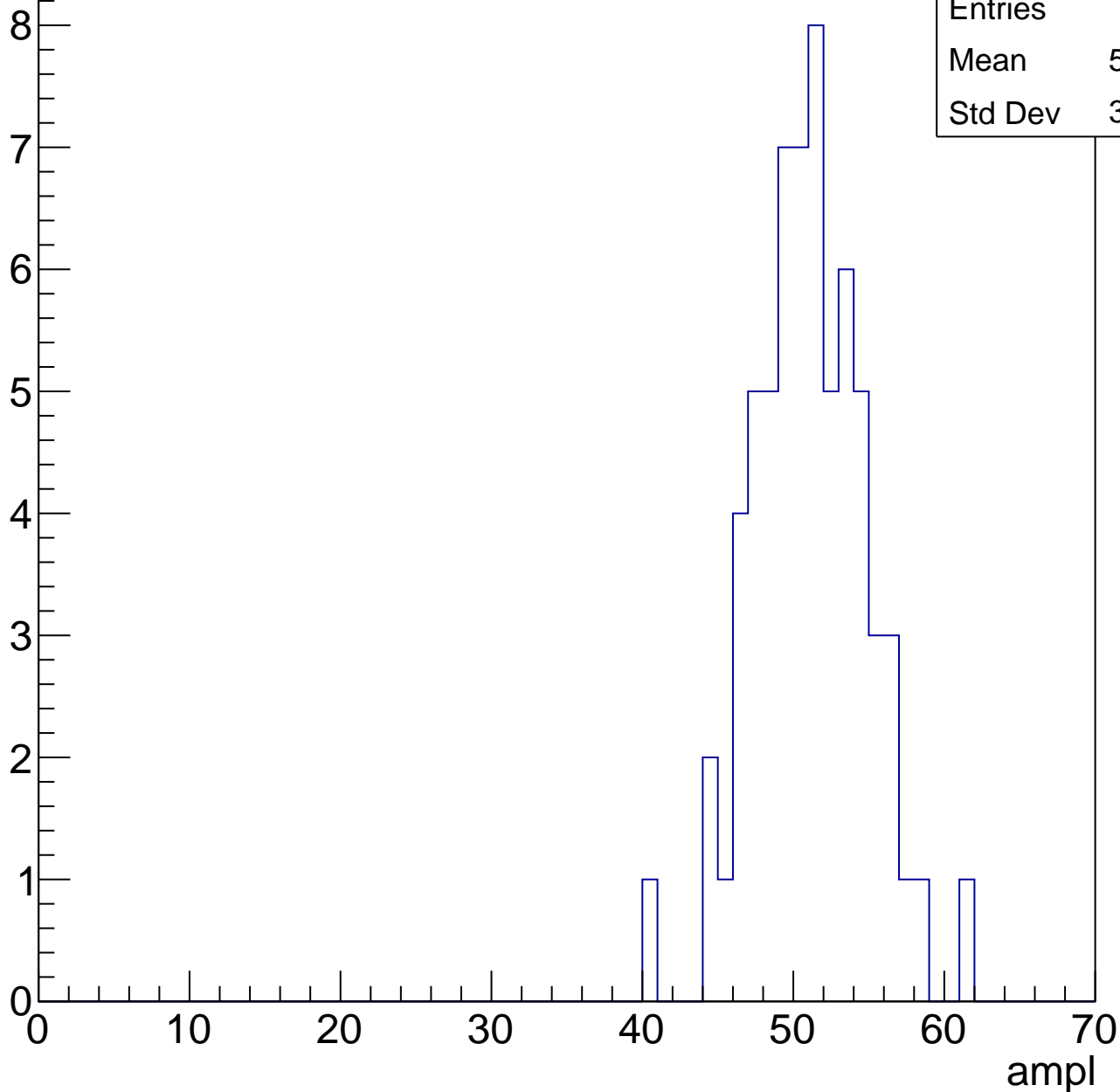


B0L000S, U4-ch113, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	50.62
Std Dev	3.695

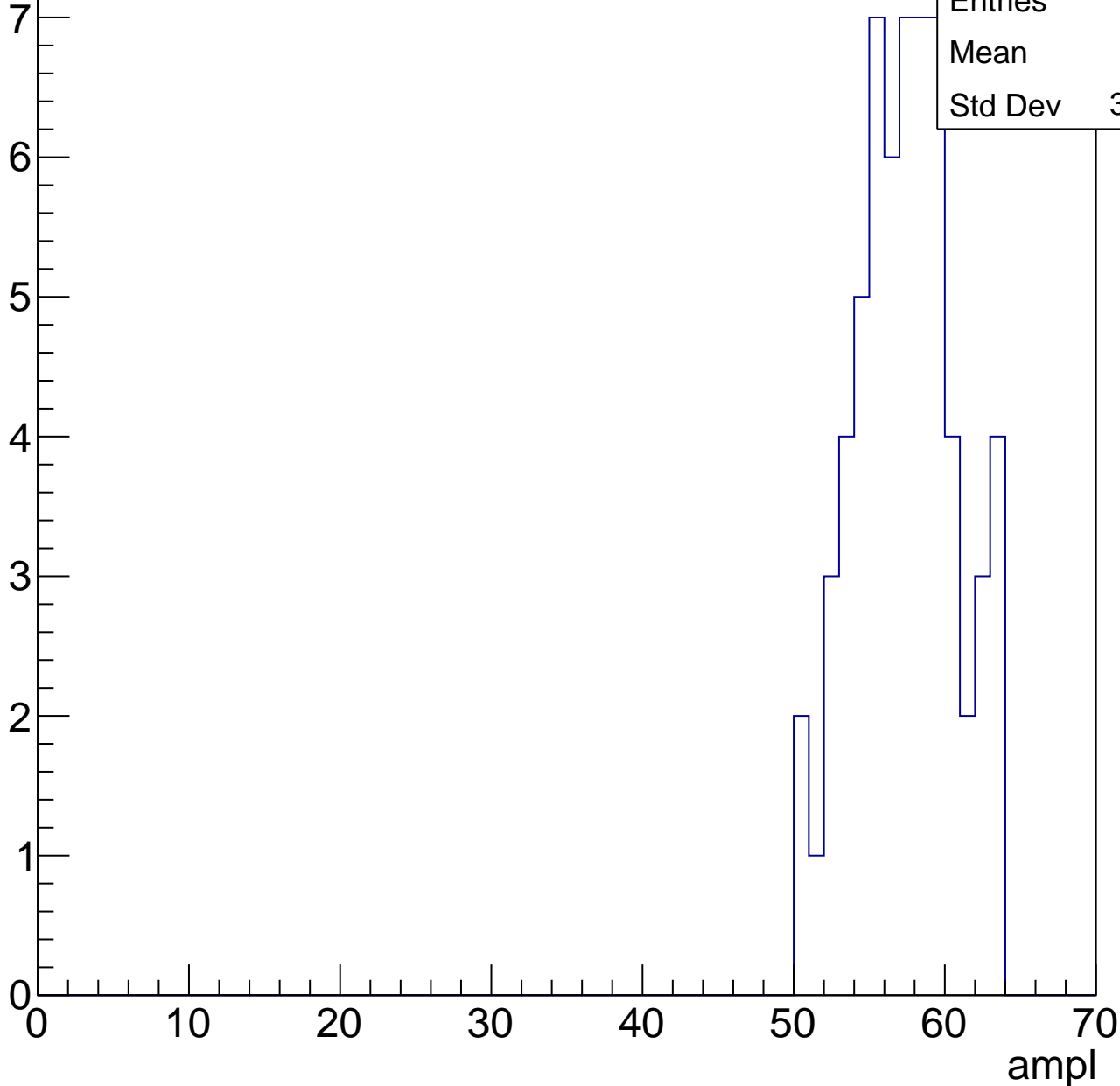


B0L000S, U4-ch113, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	56.9
Std Dev	3.296

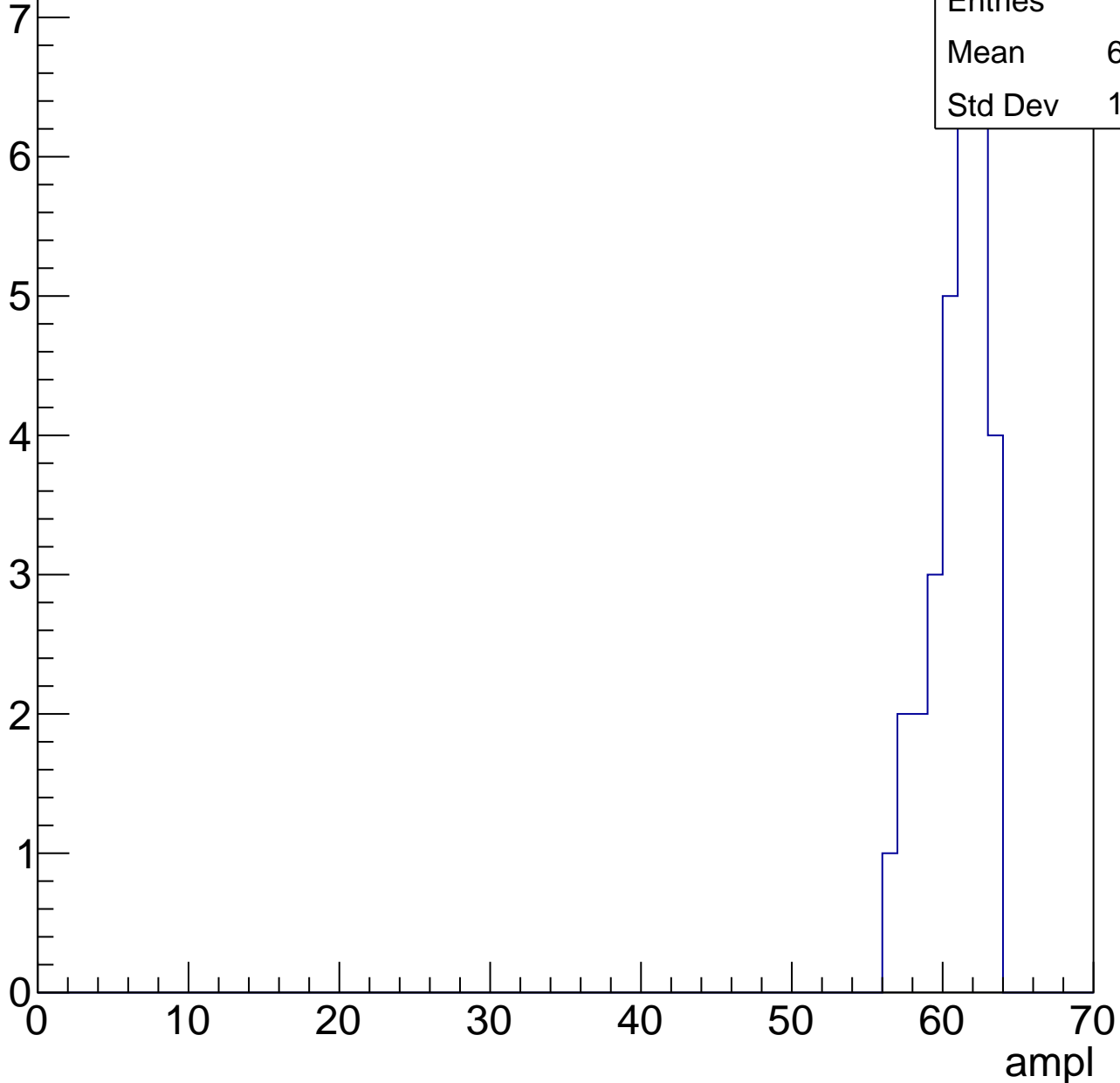


B0L000S, U4-ch113, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

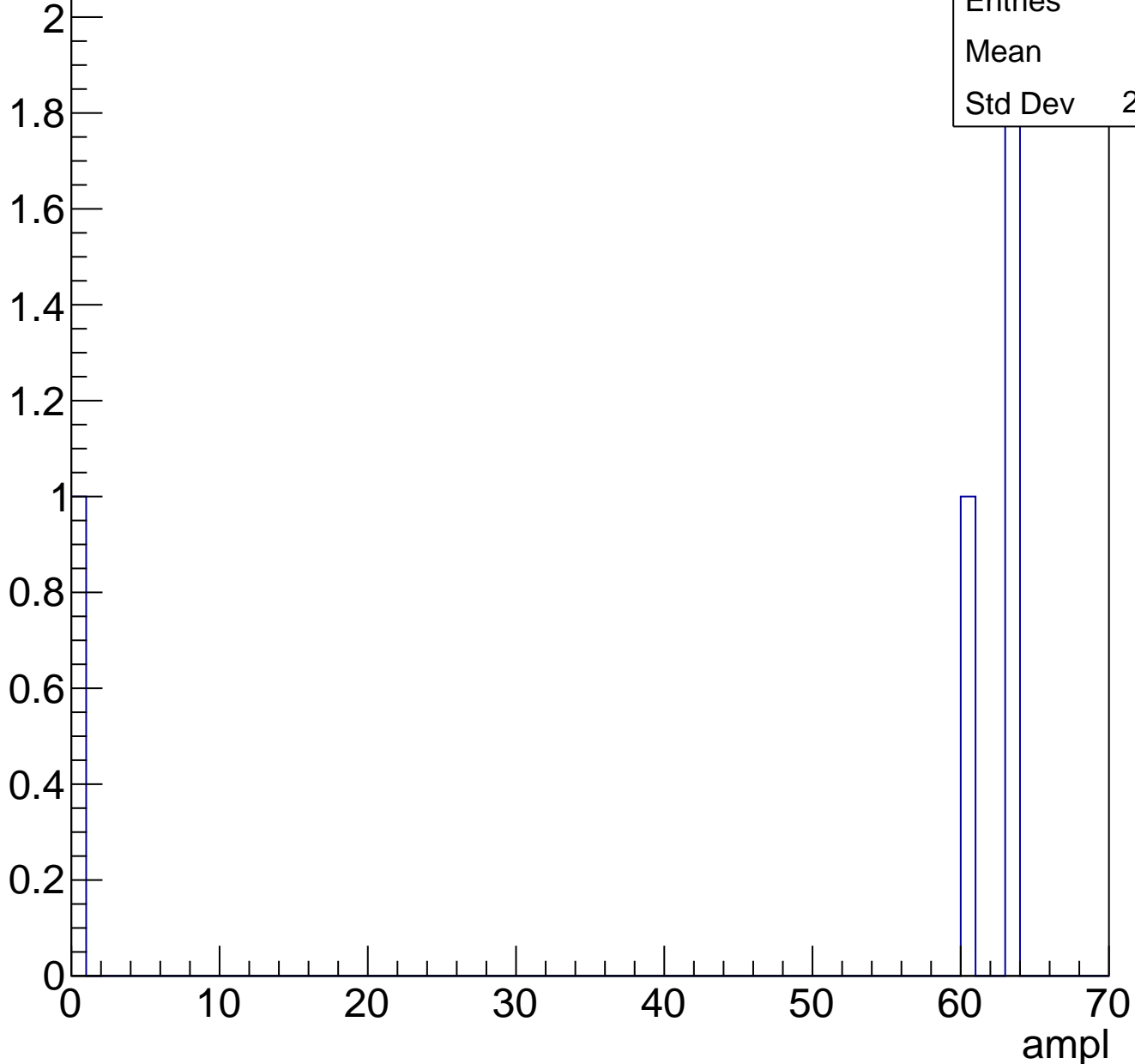
Entries	31
Mean	60.52
Std Dev	1.864



B0L000S, U4-ch113, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch113, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch114, adc0

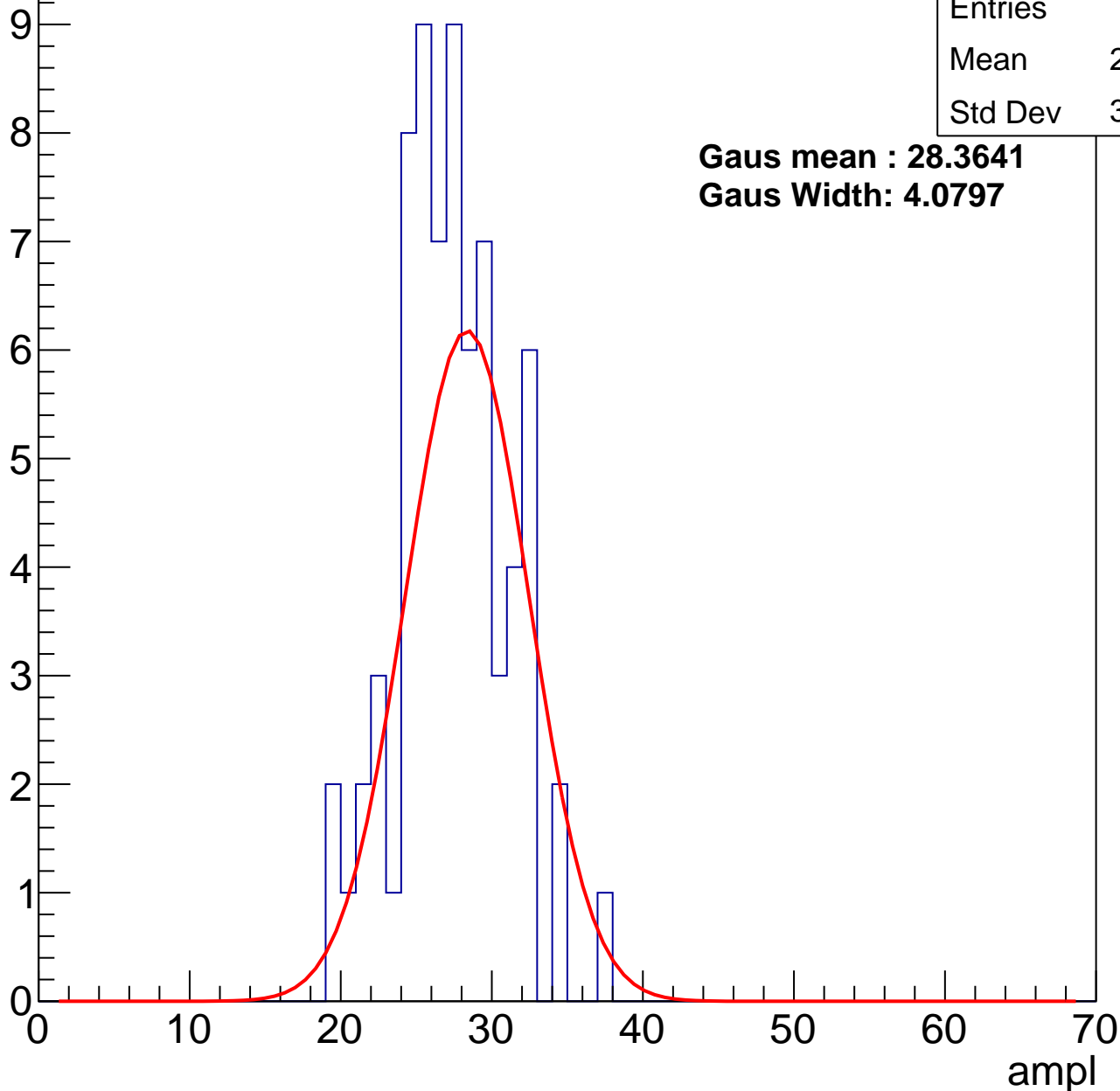
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	26.94
Std Dev	3.615

Gaus mean : 28.3641

Gaus Width: 4.0797



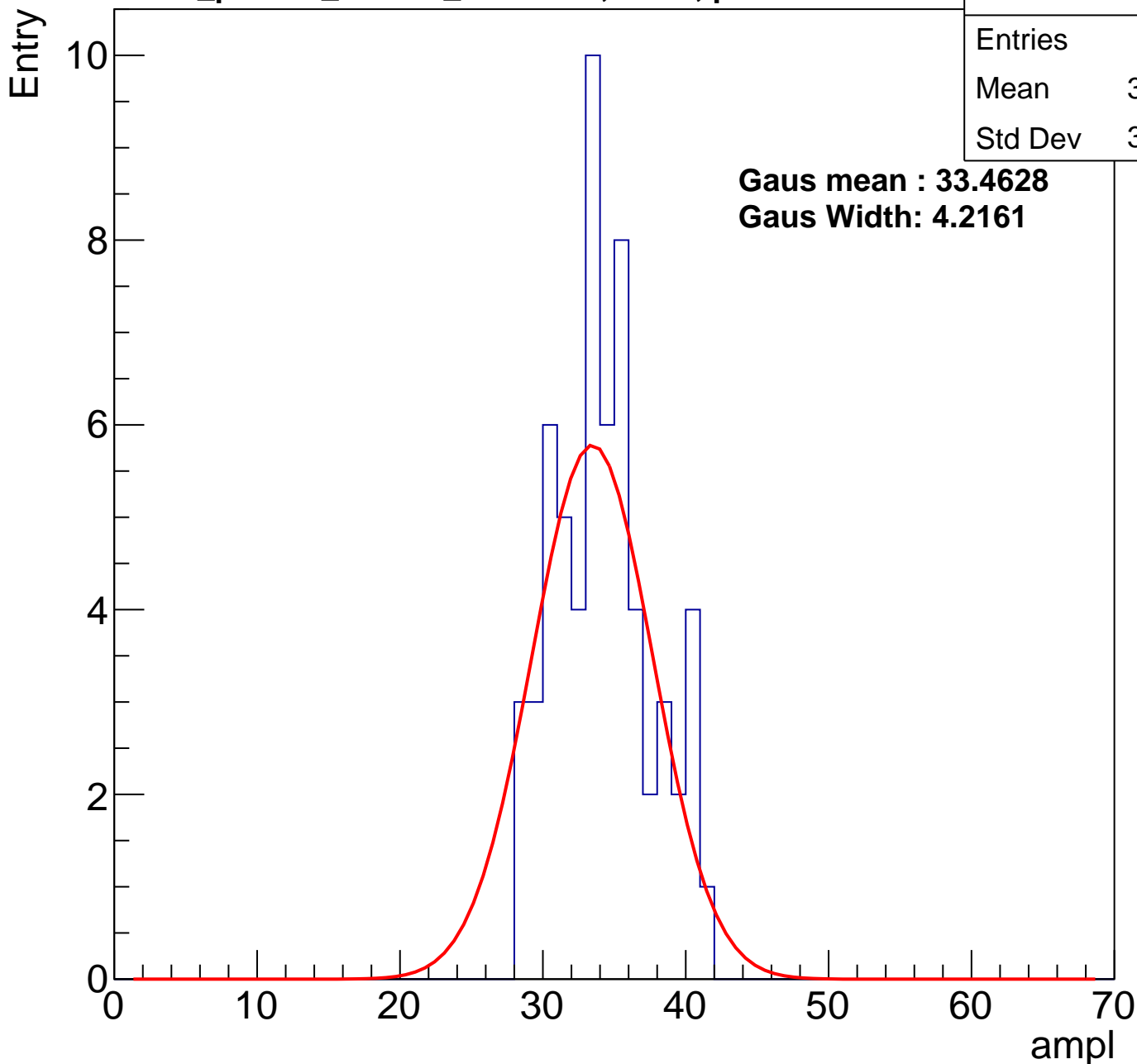
B0L000S, U4-ch114, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	33.75
Std Dev	3.337

Gaus mean : 33.4628

Gaus Width: 4.2161



B0L000S, U4-ch114, adc2

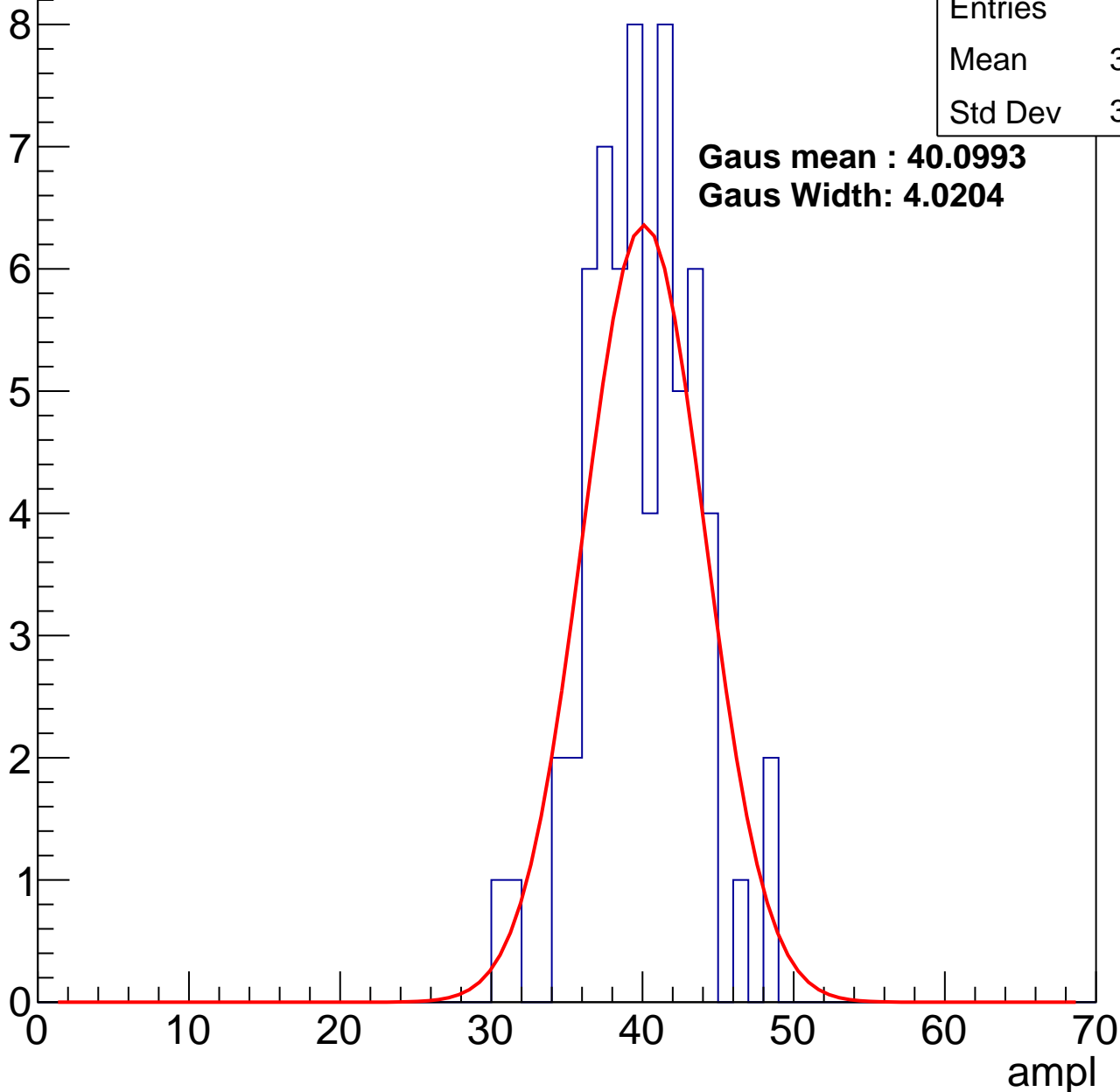
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	39.49
Std Dev	3.536

Gaus mean : 40.0993

Gaus Width: 4.0204

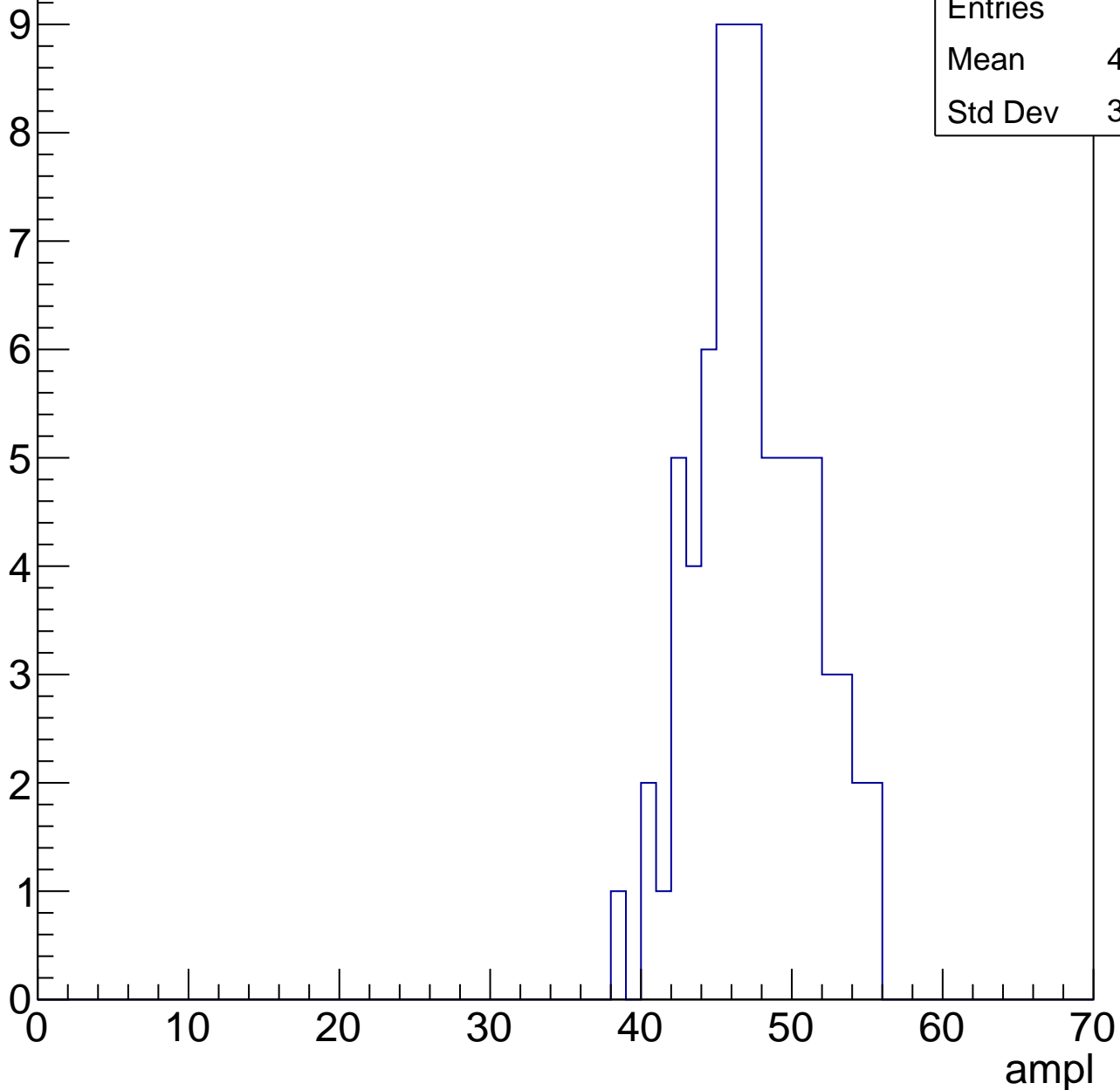


B0L000S, U4-ch114, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	46.97
Std Dev	3.738

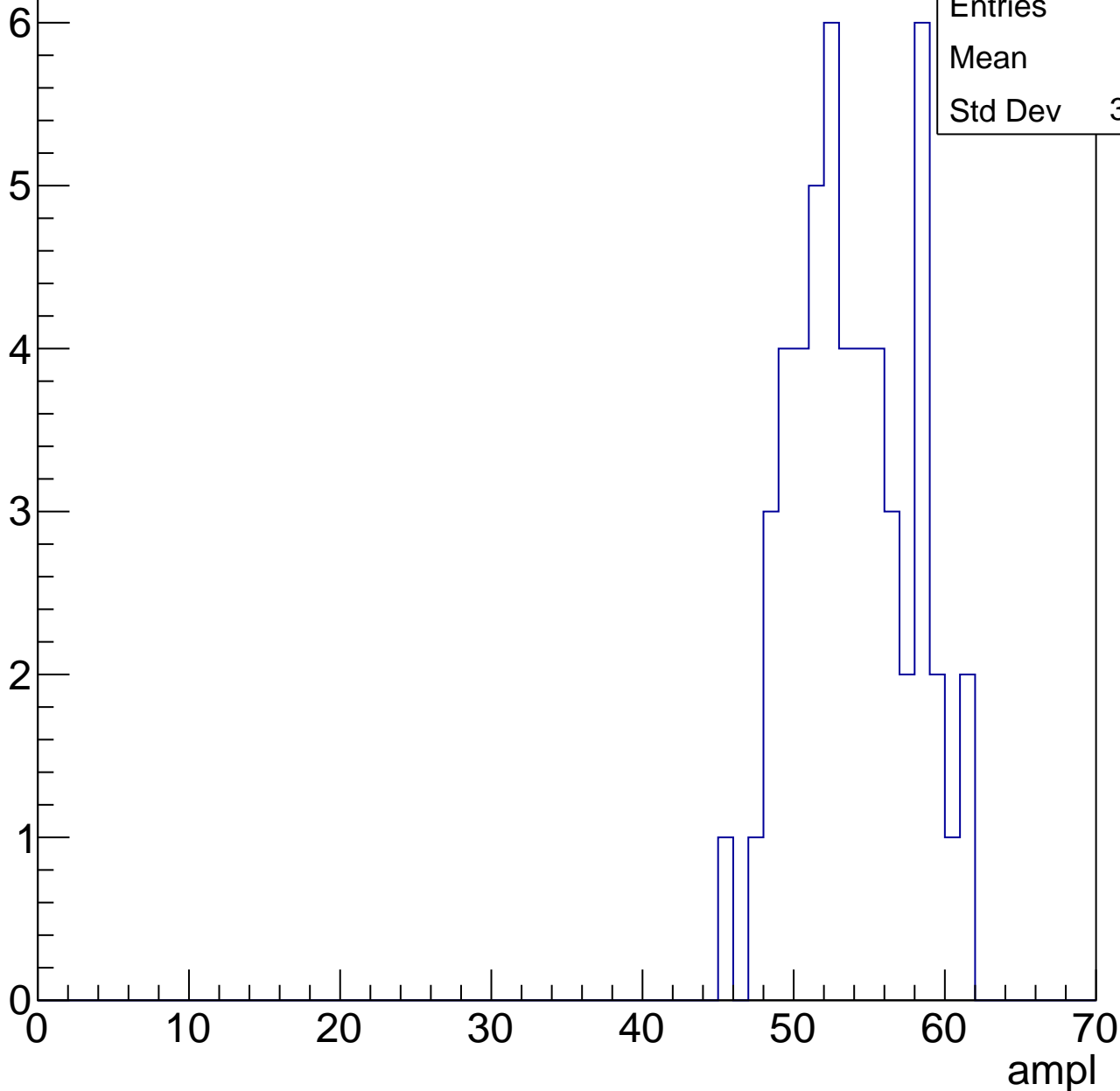


B0L000S, U4-ch114, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

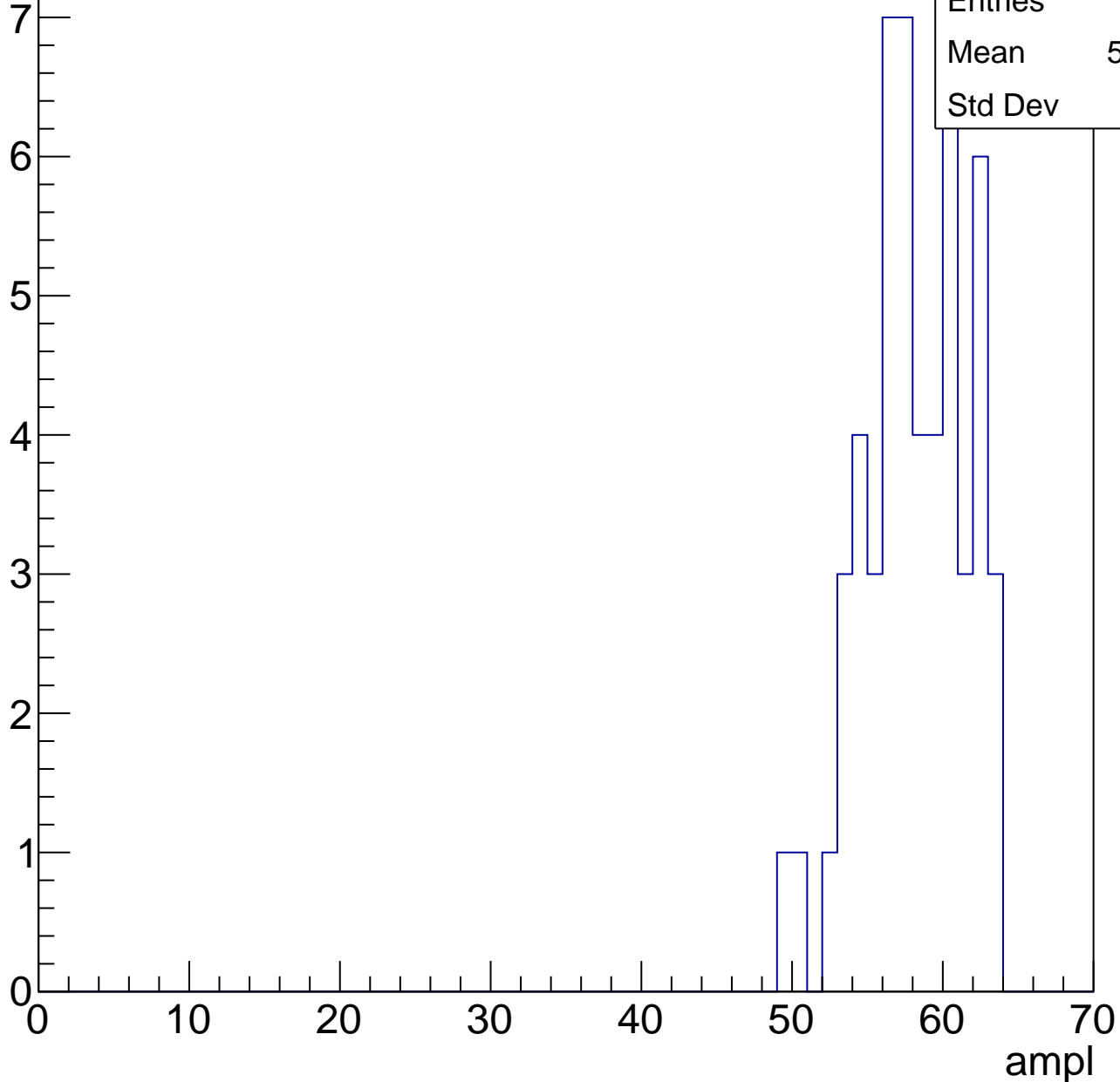
Entries	52
Mean	53.4
Std Dev	3.849



B0L000S, U4-ch114, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



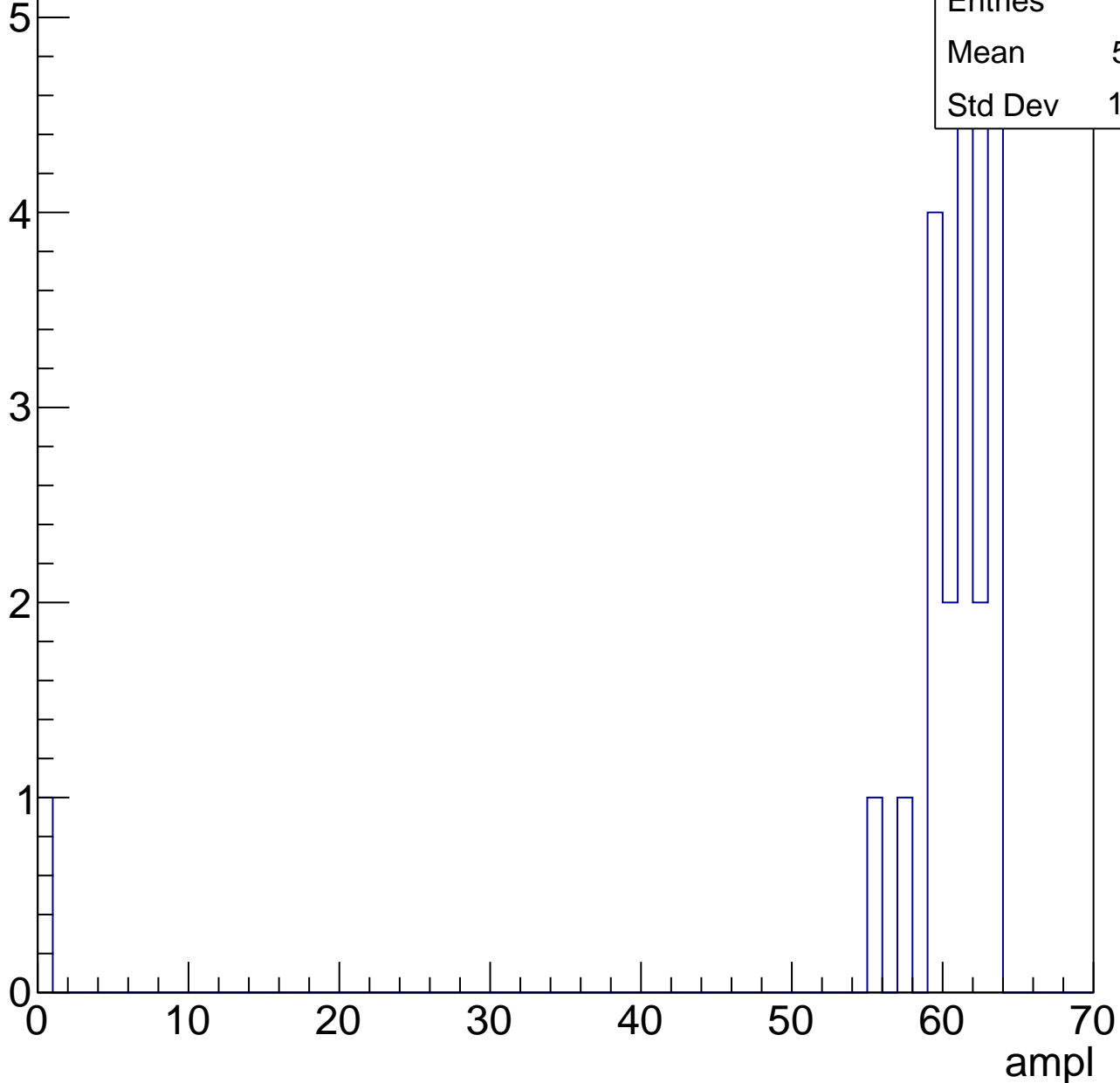
Entries	54
Mean	57.67
Std Dev	3.35

B0L000S, U4-ch114, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

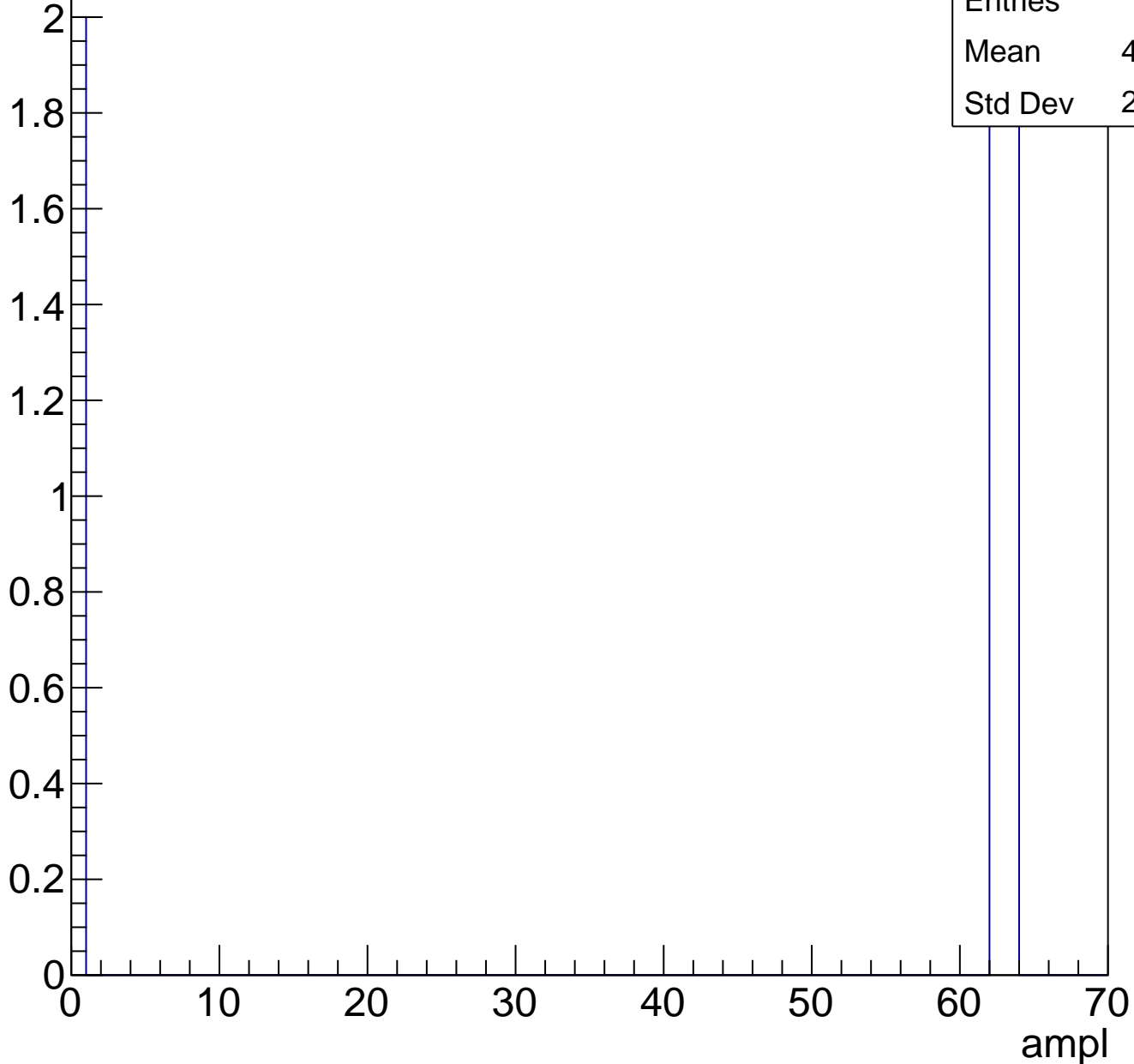
Entries	21
Mean	57.71
Std Dev	13.07



B0L000S, U4-ch114, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch115, adc0

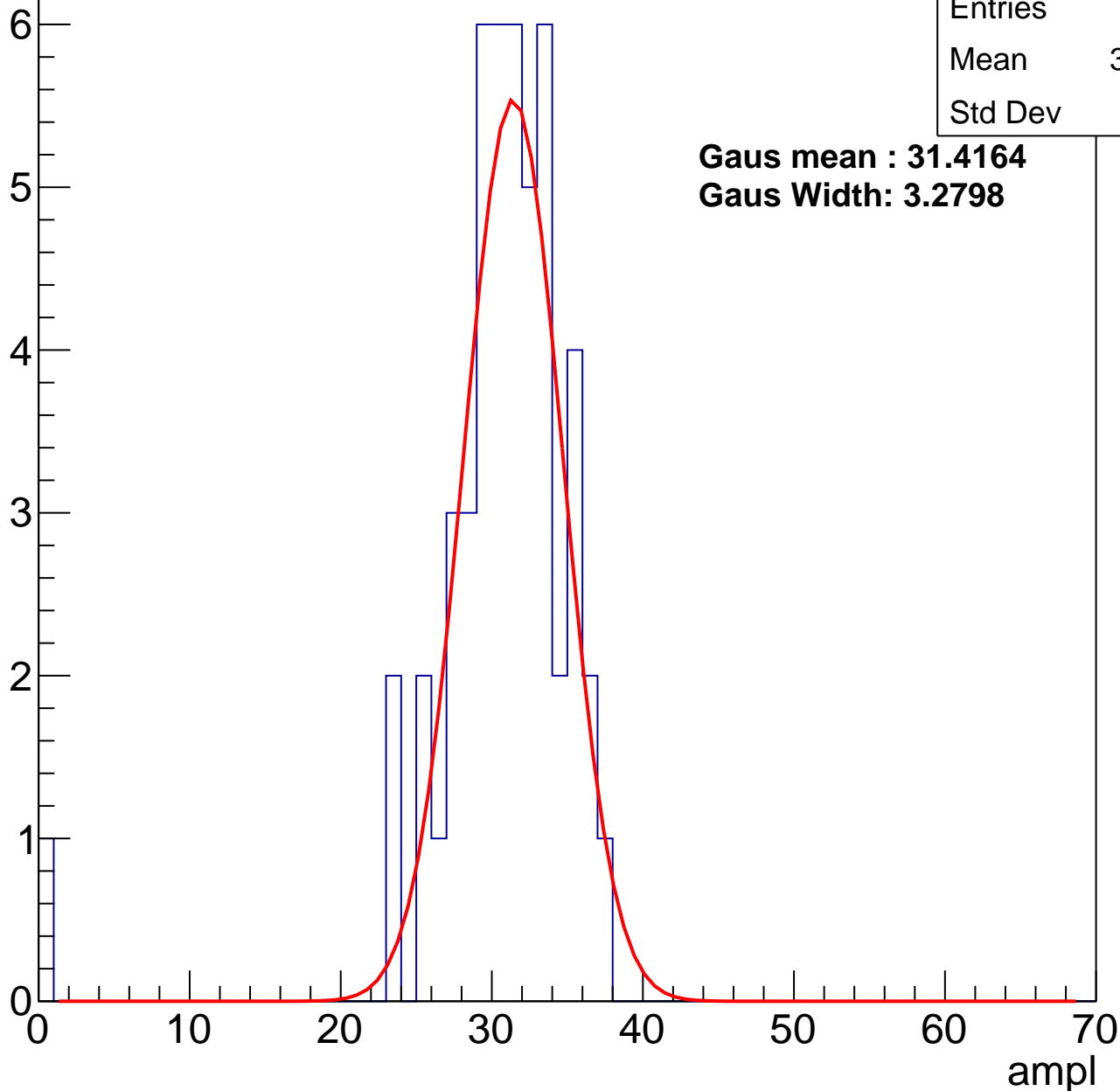
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	30.04
Std Dev	5.37

Gaus mean : 31.4164

Gaus Width: 3.2798



B0L000S, U4-ch115, adc1

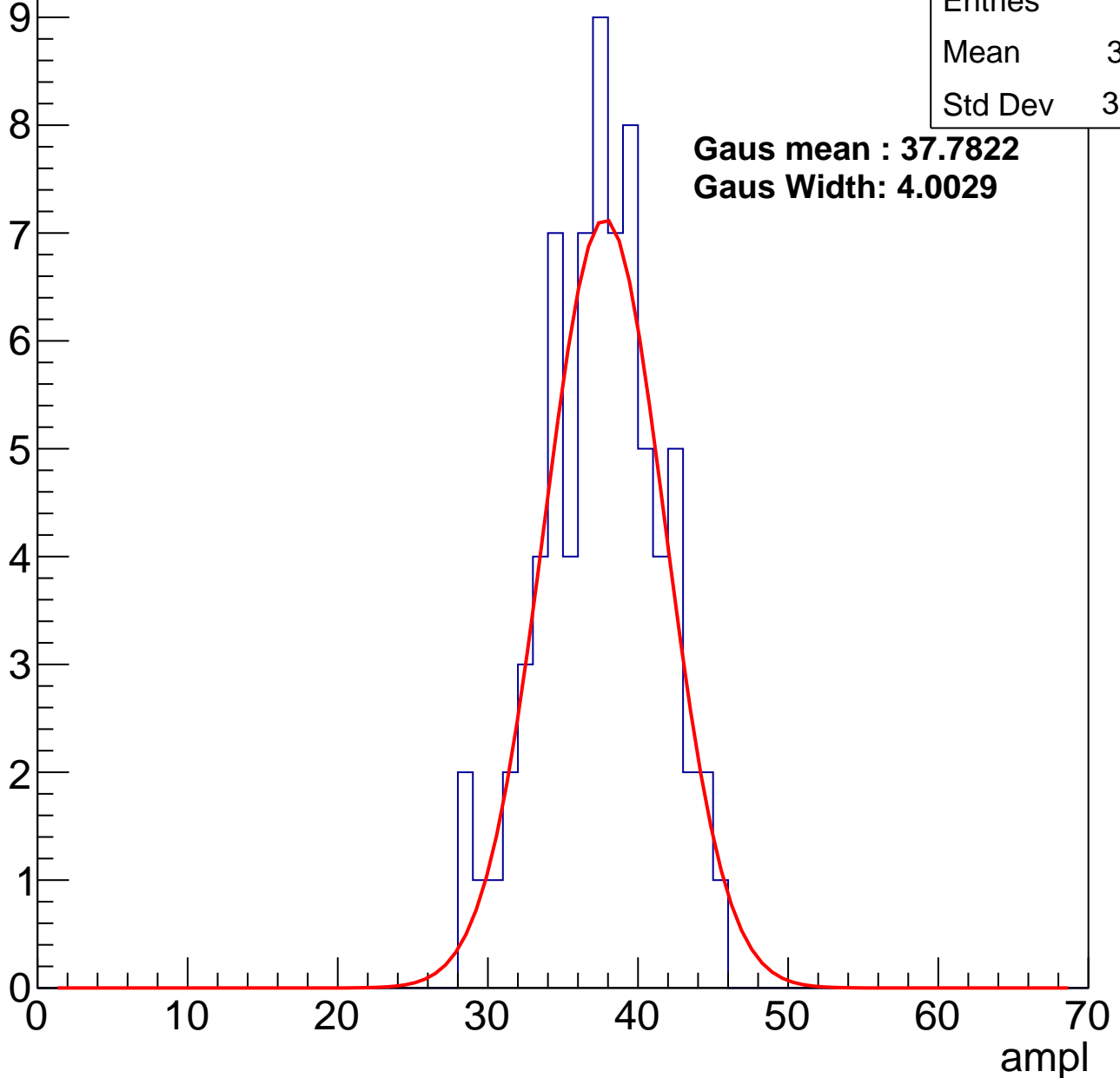
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.01
Std Dev	3.818

Gaus mean : 37.7822

Gaus Width: 4.0029



B0L000S, U4-ch115, adc2

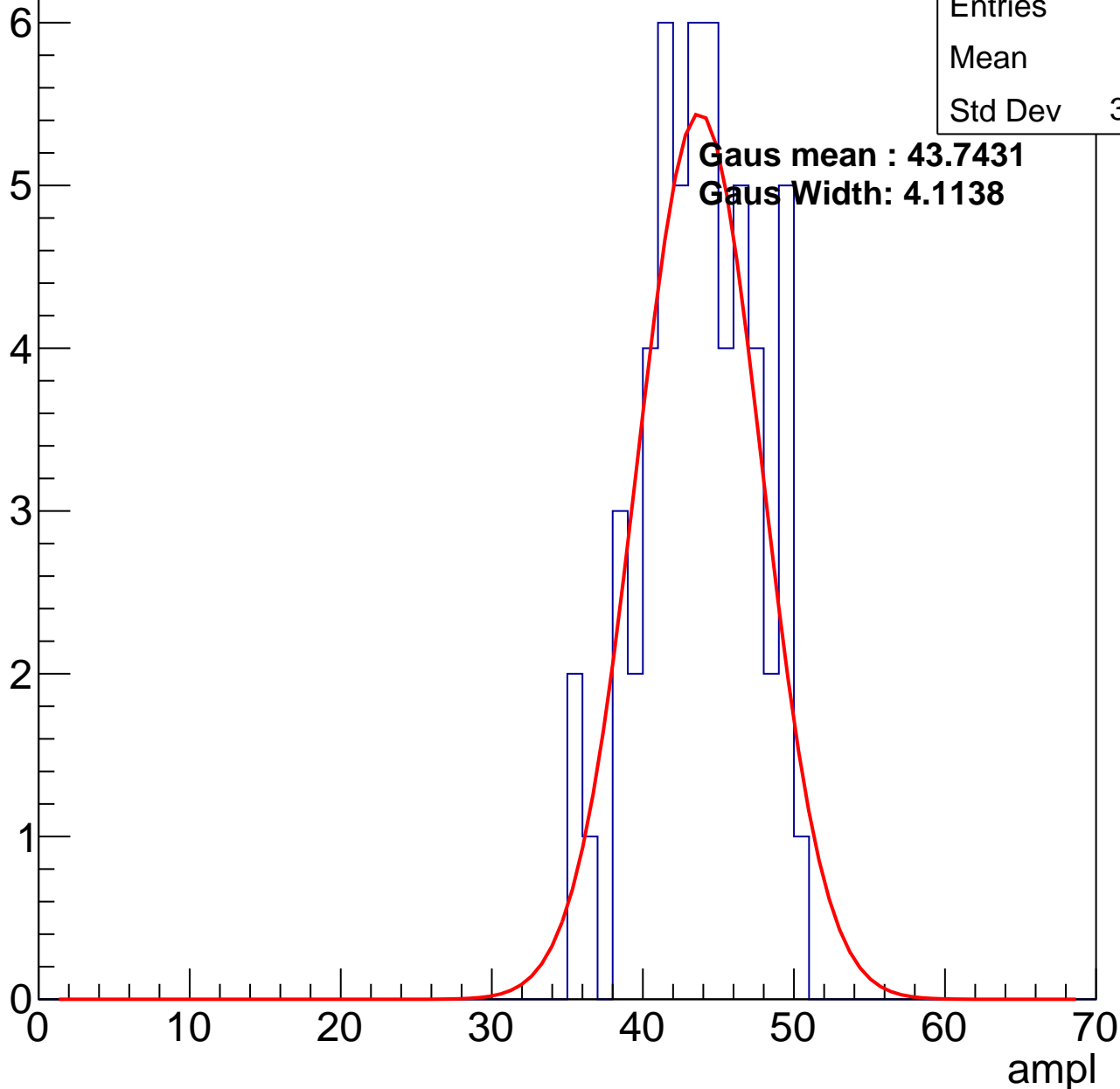
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43.3
Std Dev	3.669

Gaus mean : 43.7431

Gaus Width: 4.1138

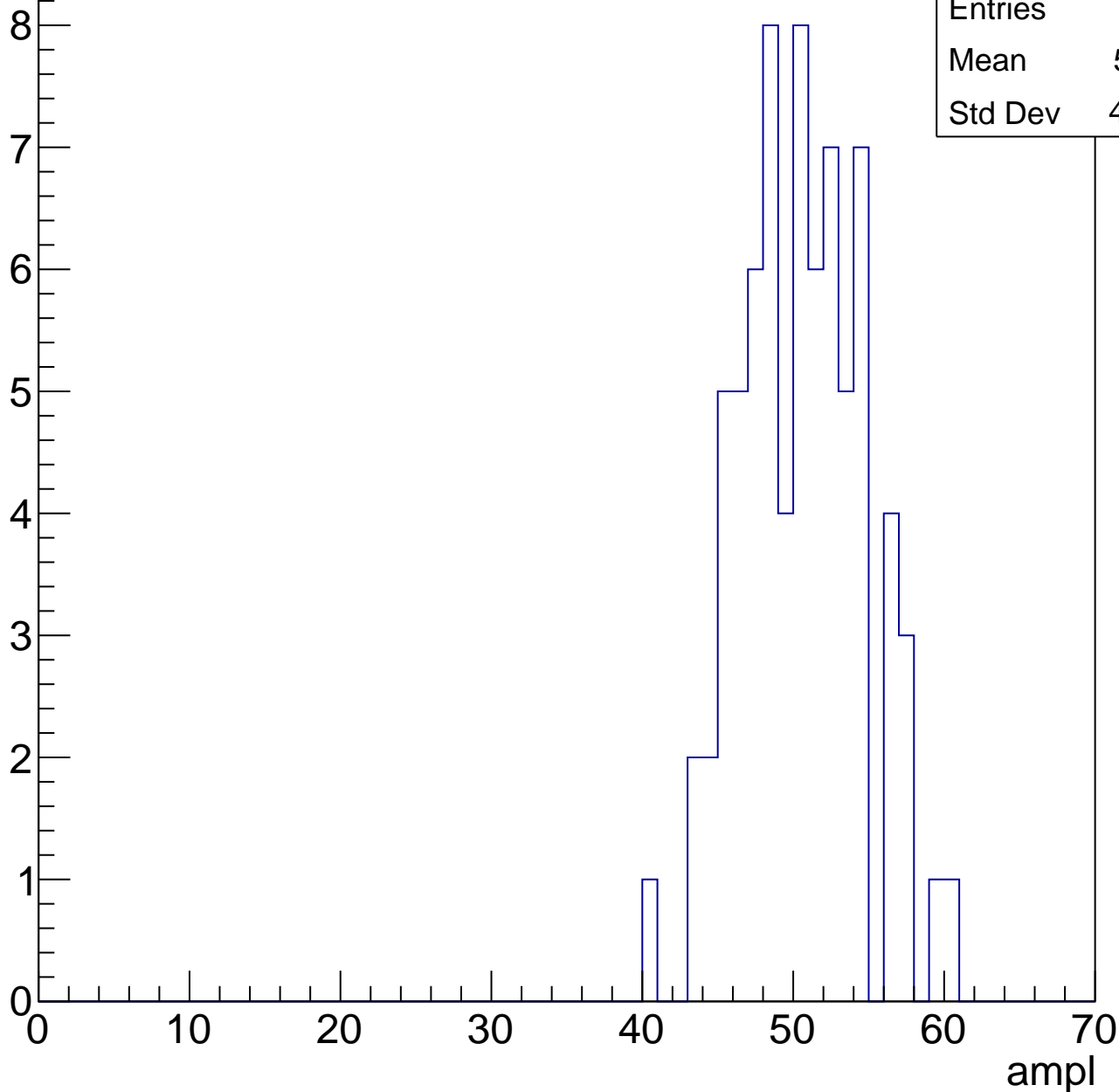


B0L000S, U4-ch115, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

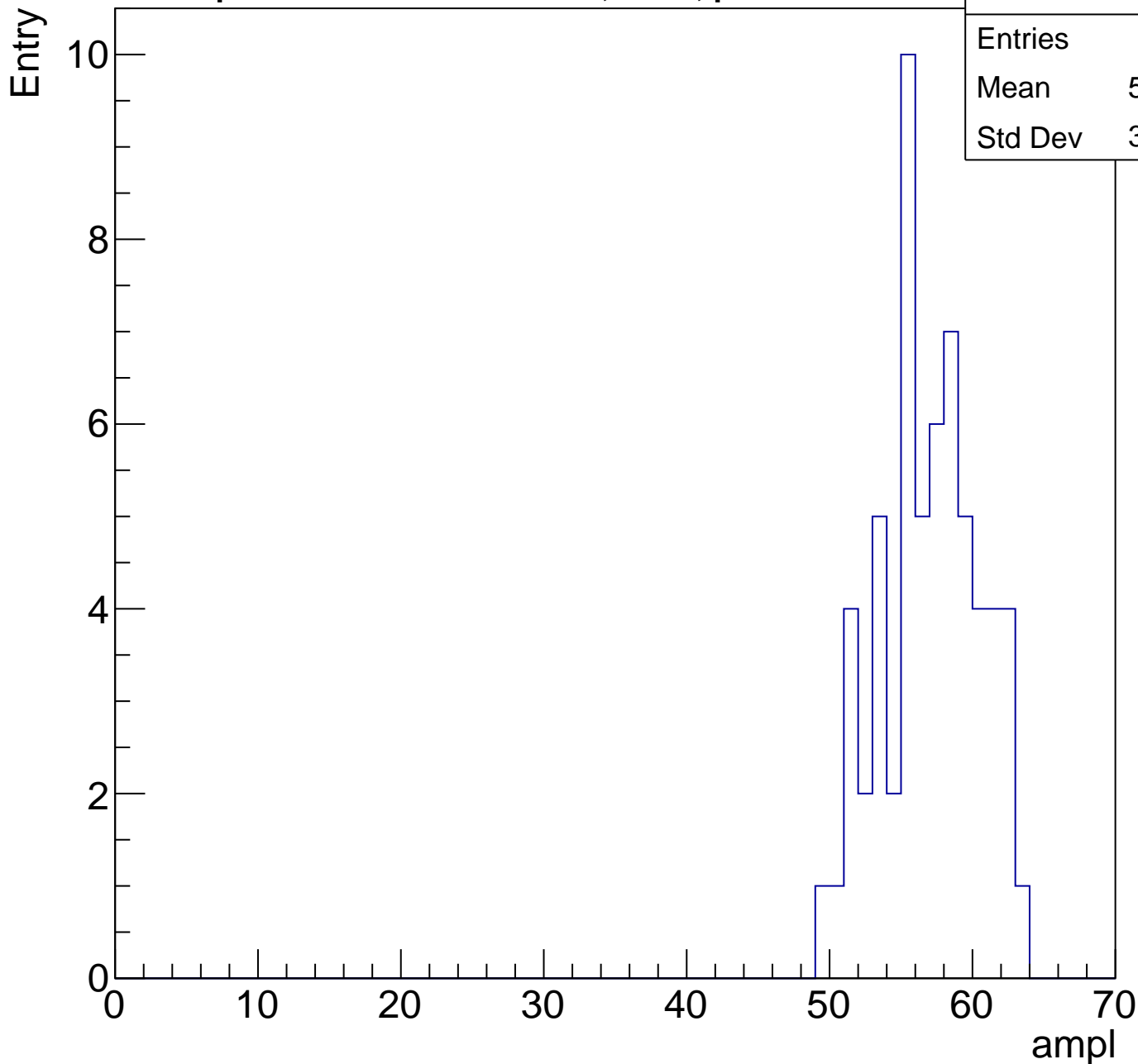
Entries	75
Mean	50.11
Std Dev	4.045



B0L000S, U4-ch115, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	56.52
Std Dev	3.386

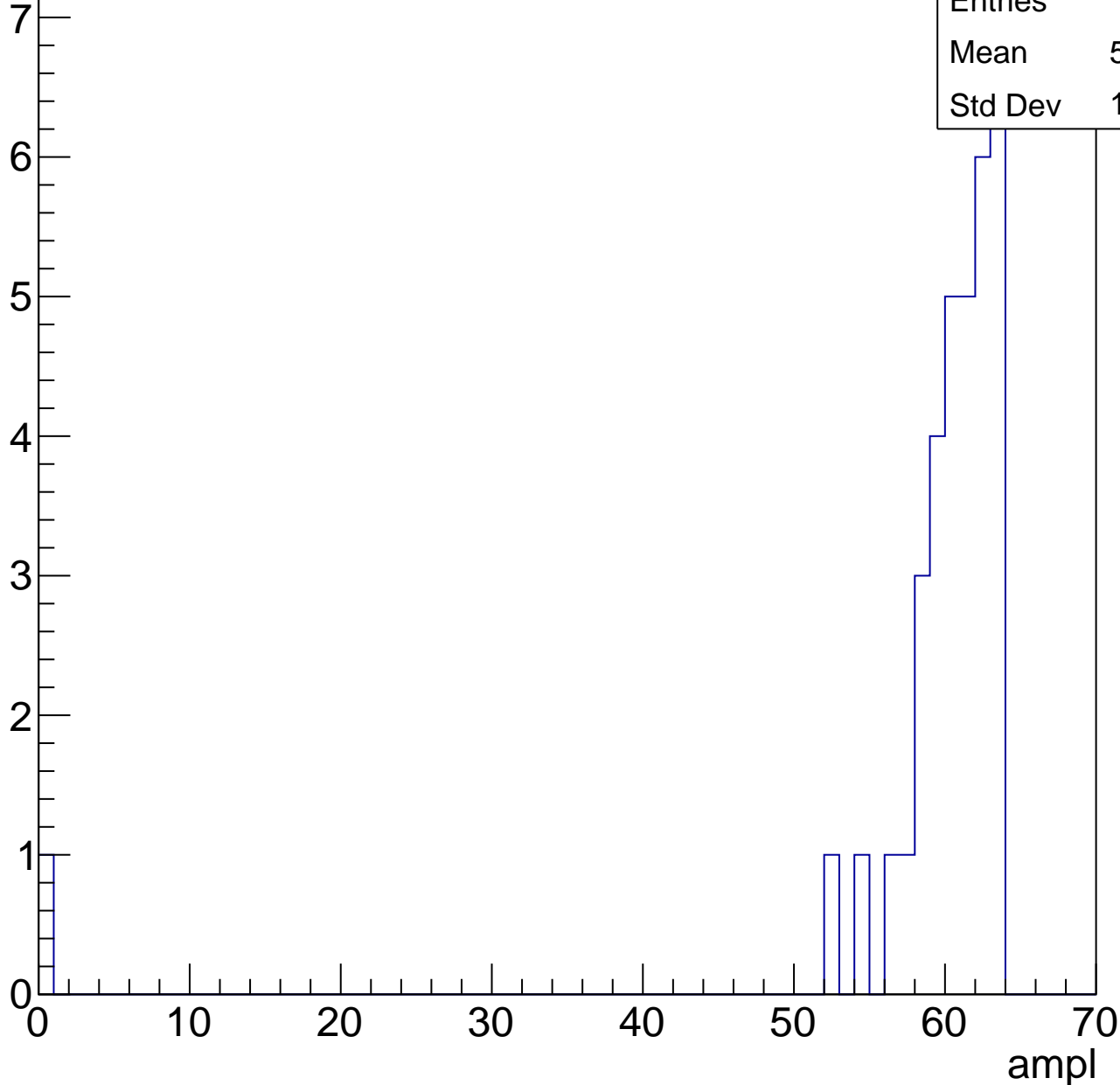


B0L000S, U4-ch115, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.49
Std Dev	10.35



B0L000S, U4-ch115, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch115, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch116, adc0

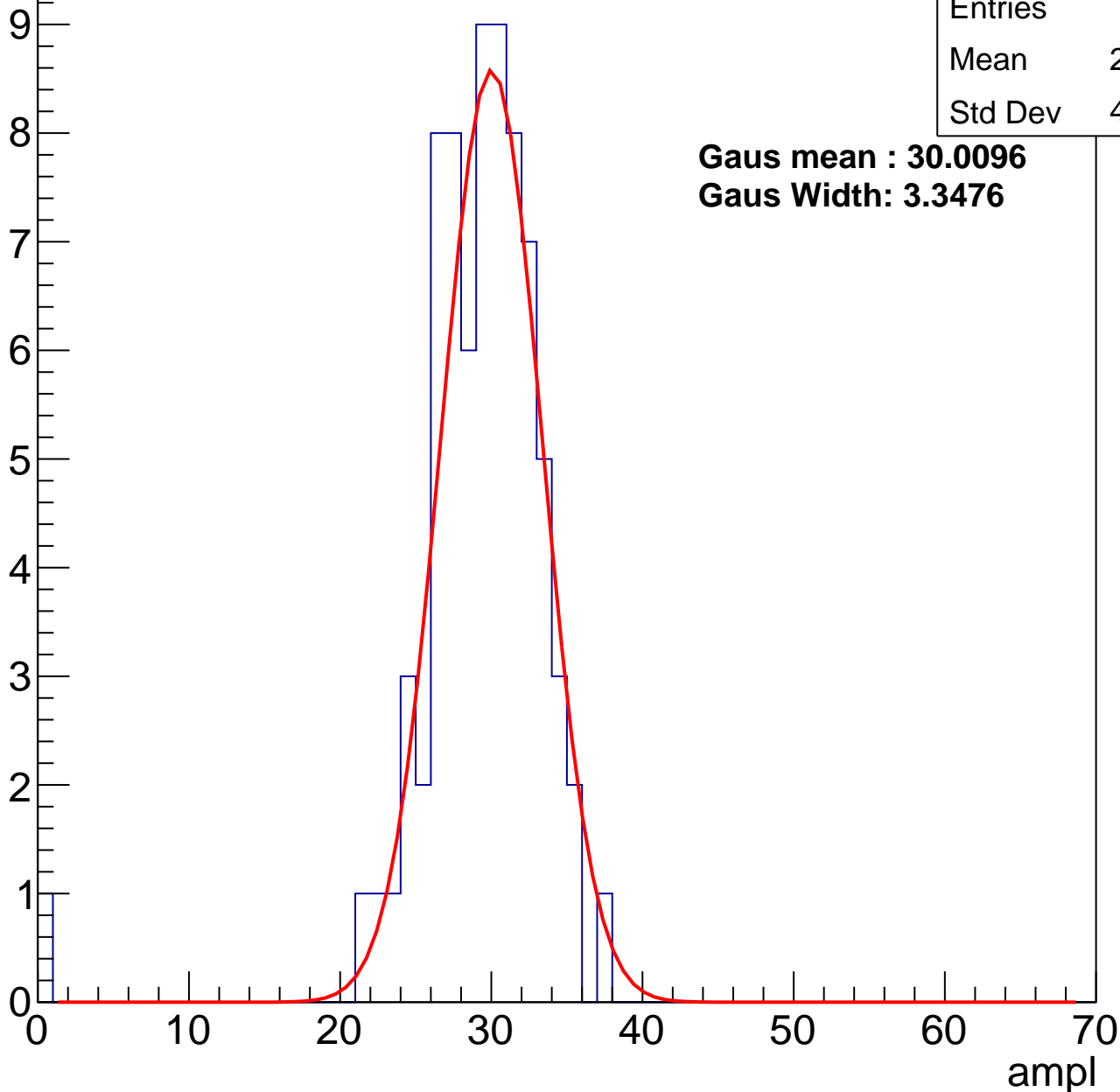
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	28.76
Std Dev	4.615

Gaus mean : 30.0096

Gaus Width: 3.3476



B0L000S, U4-ch116, adc1

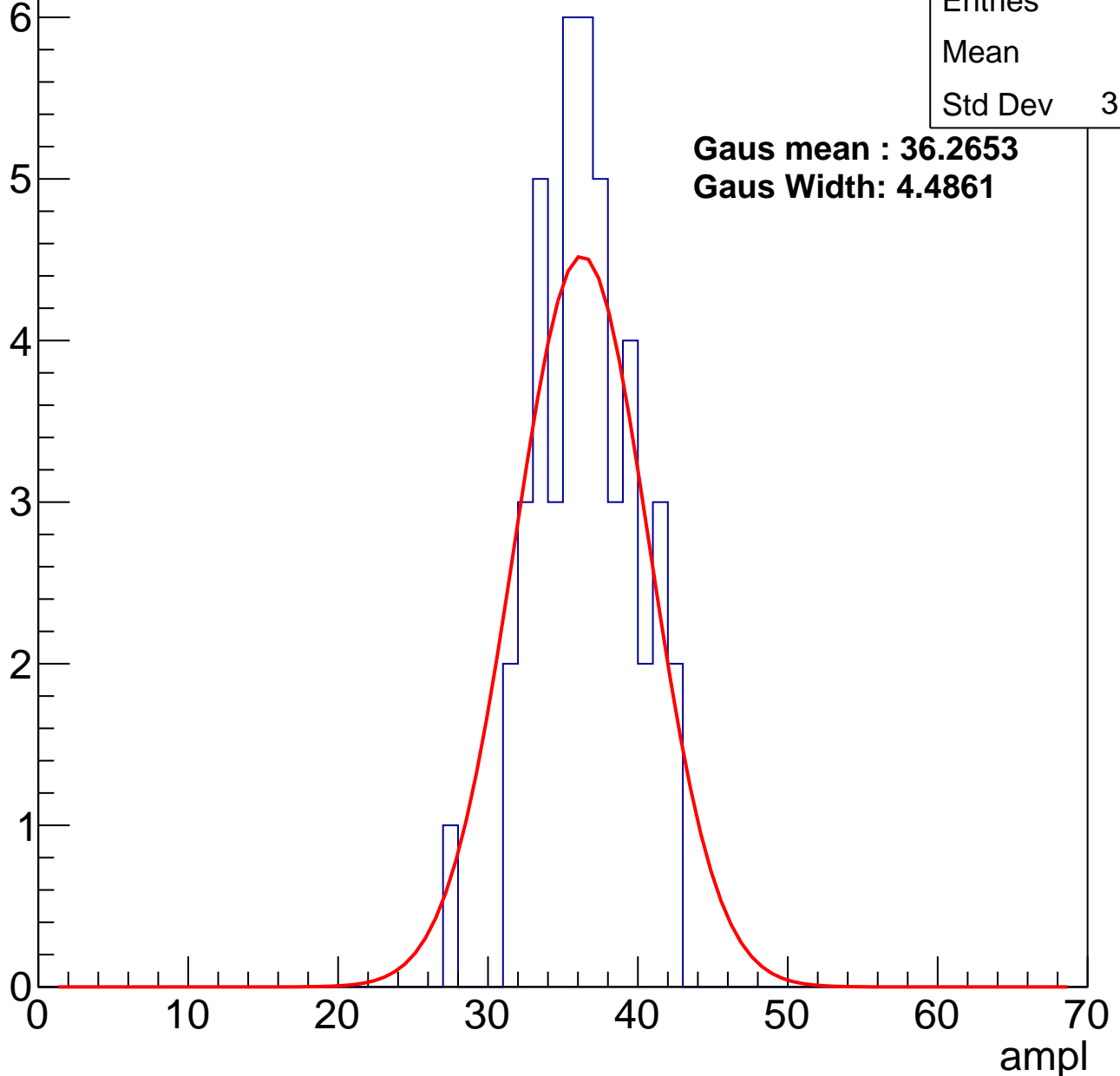
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	45
Mean	36
Std Dev	3.246

Gaus mean : 36.2653

Gaus Width: 4.4861



B0L000S, U4-ch116, adc2

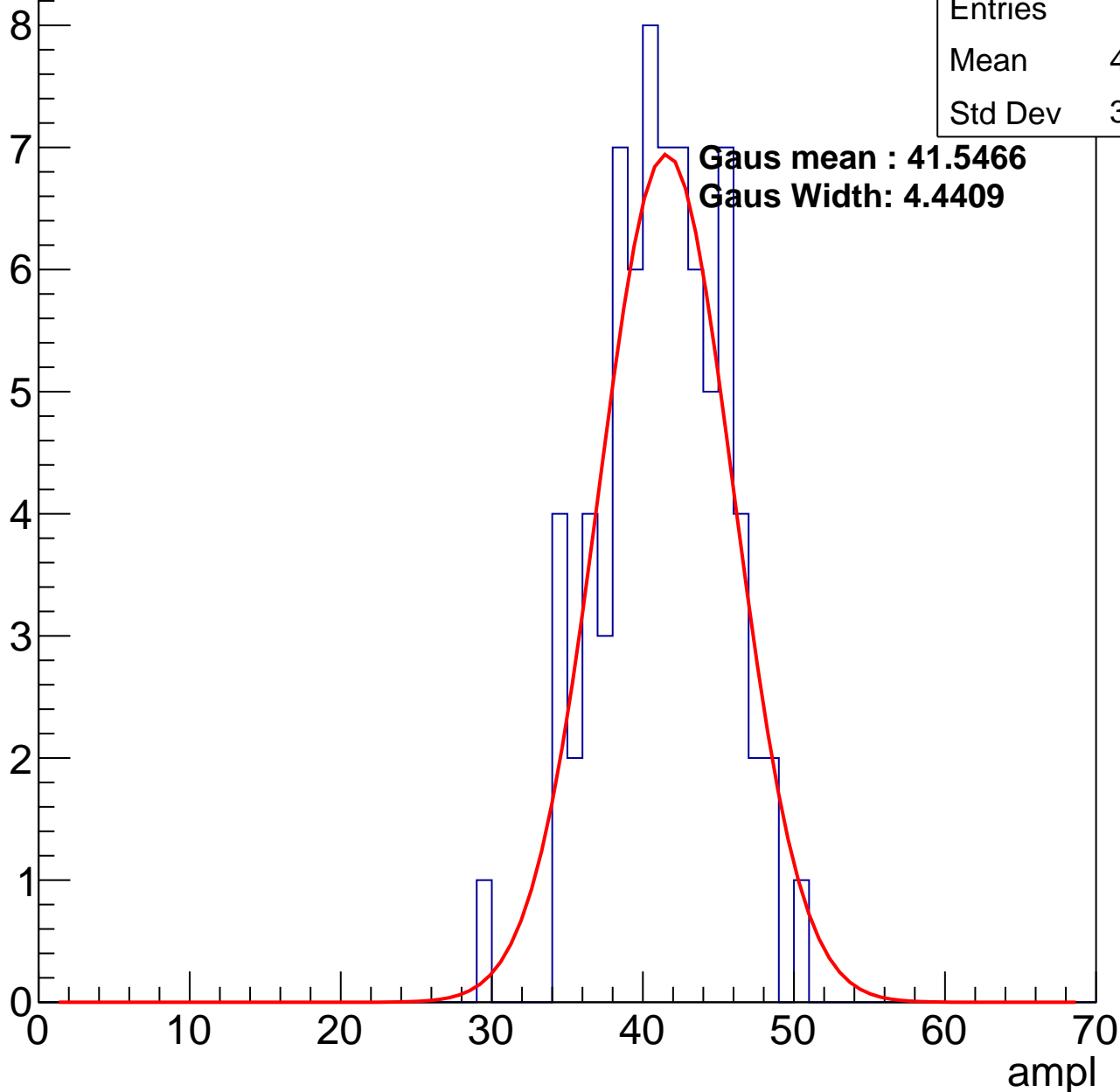
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	40.89
Std Dev	3.962

Gaus mean : 41.5466

Gaus Width: 4.4409

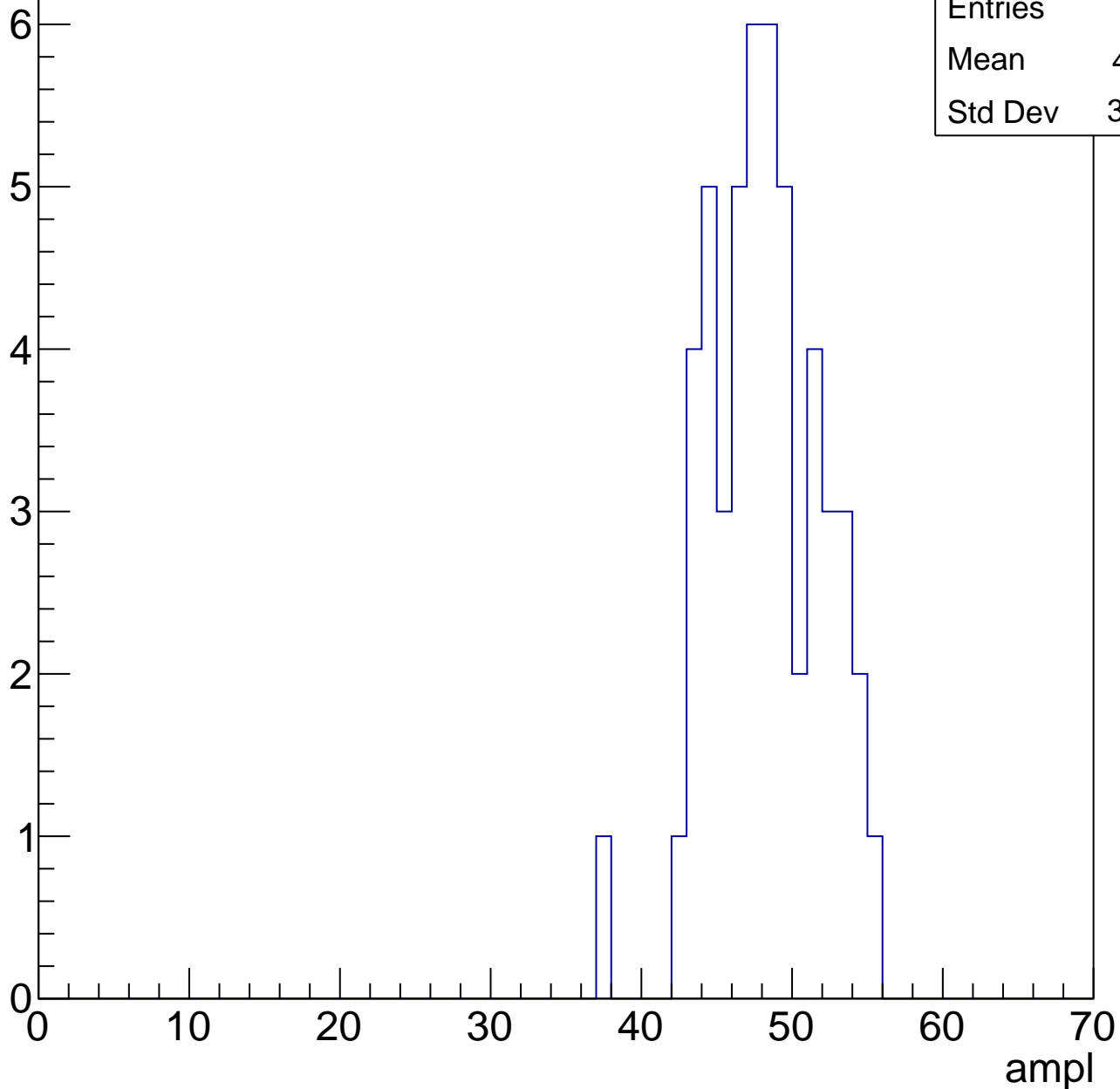


B0L000S, U4-ch116, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	47.71
Std Dev	3.648

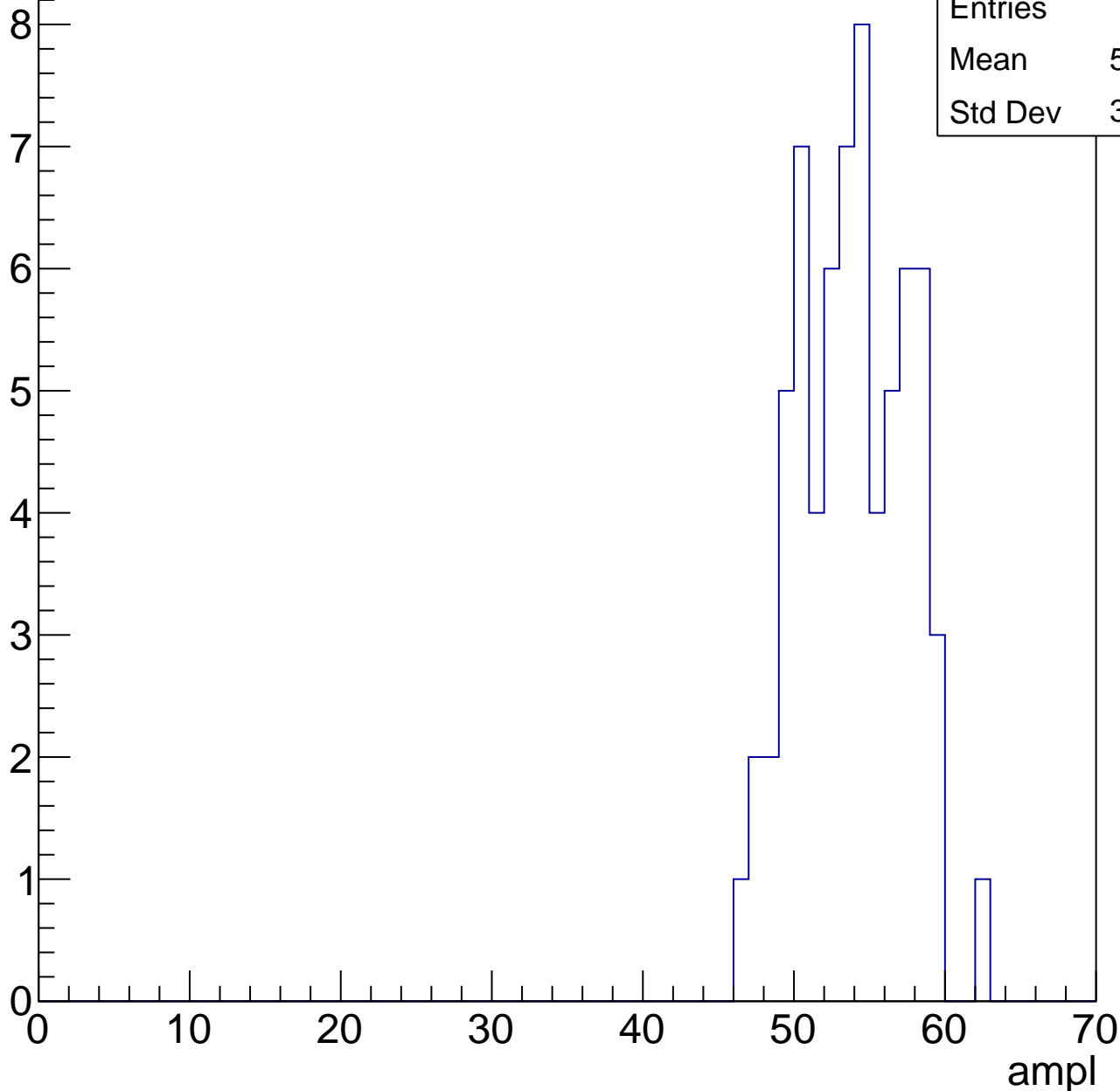


B0L000S, U4-ch116, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	53.42
Std Dev	3.516

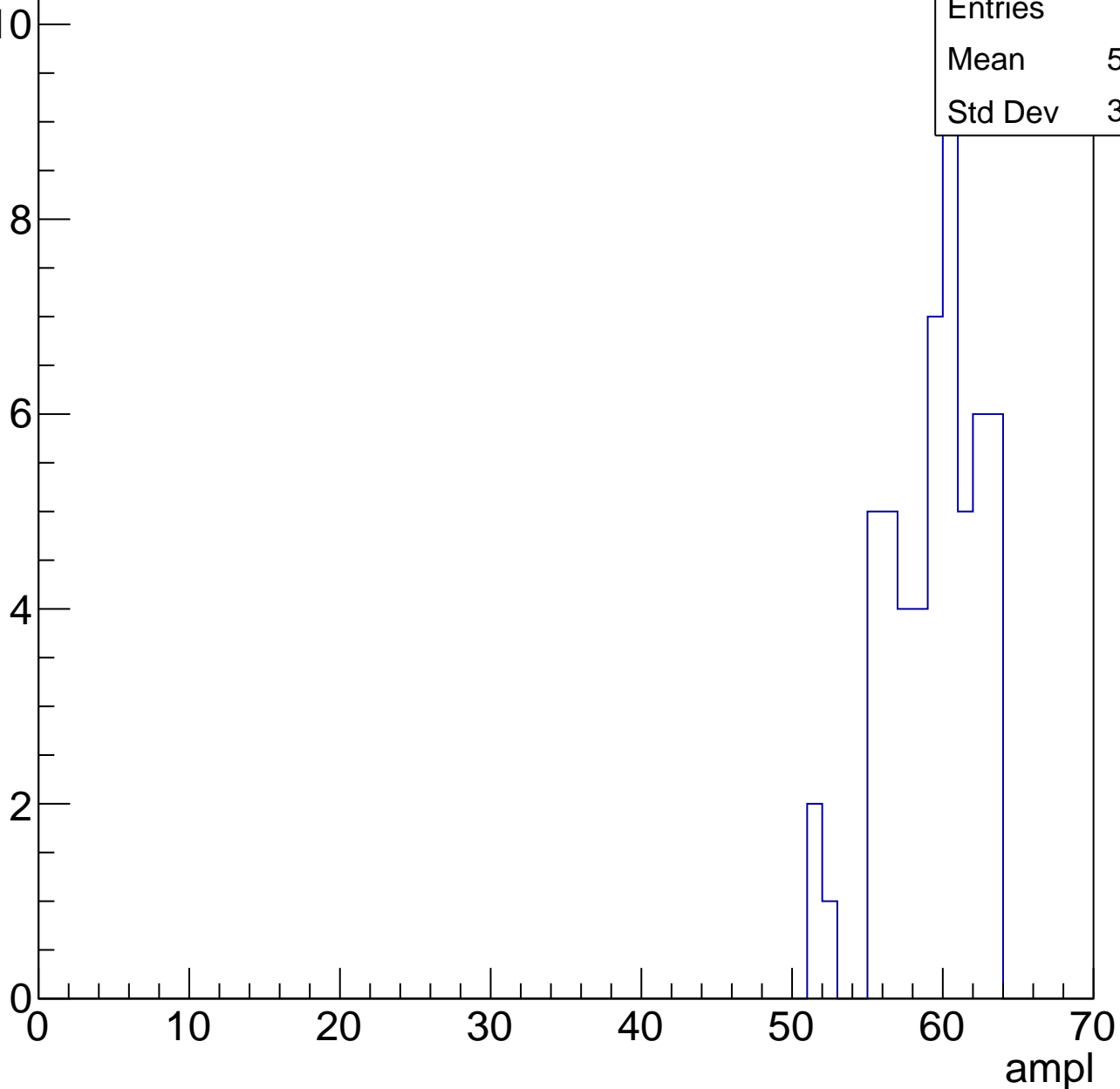


B0L000S, U4-ch116, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.85
Std Dev	3.018

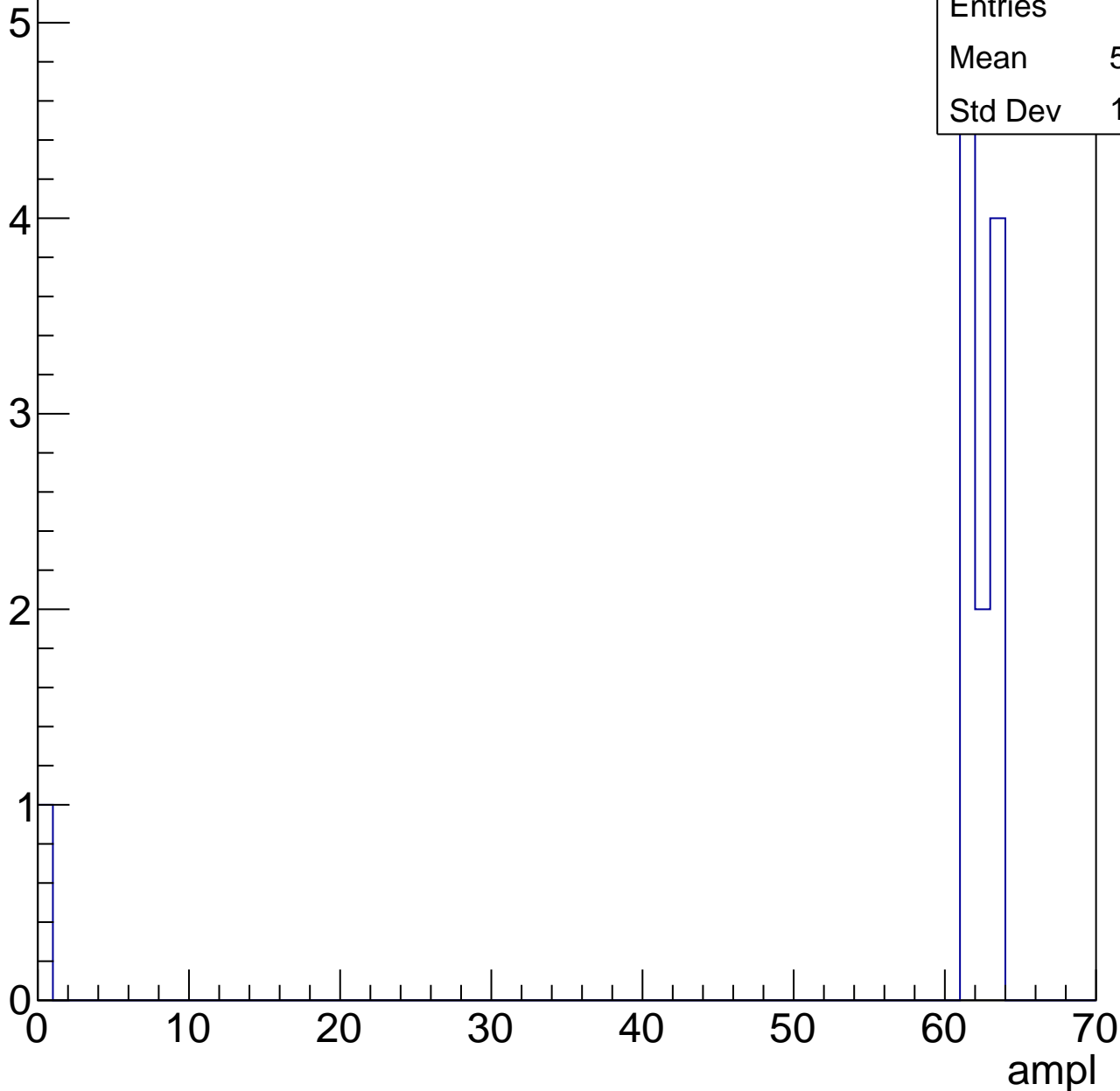


B0L000S, U4-ch116, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	12
Mean	56.75
Std Dev	17.13



B0L000S, U4-ch116, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch117, adc0

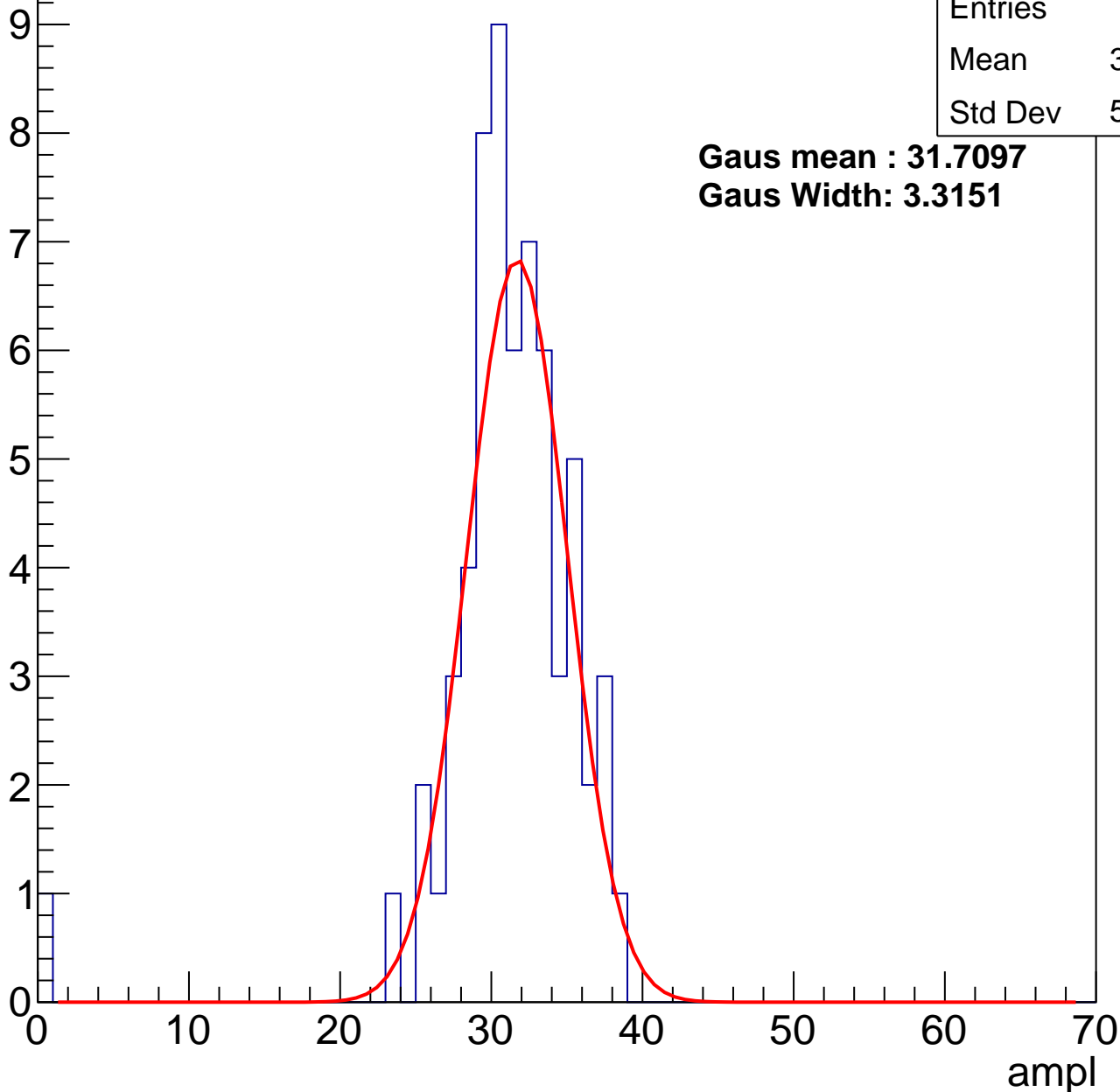
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.65
Std Dev	5.055

Gaus mean : 31.7097

Gaus Width: 3.3151



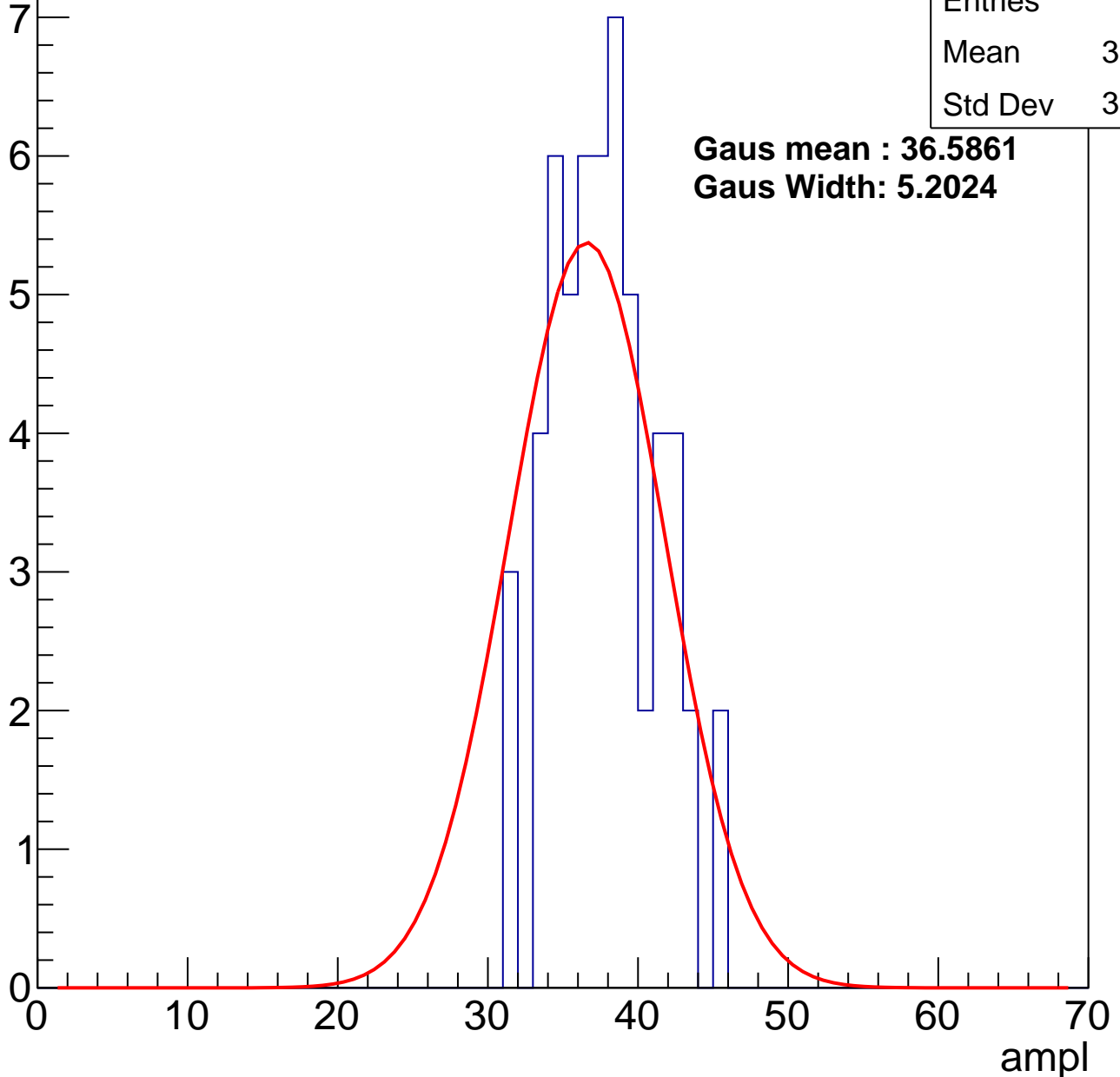
B0L000S, U4-ch117, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	37.34
Std Dev	3.419

Gaus mean : 36.5861
Gaus Width: 5.2024



B0L000S, U4-ch117, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	73
Mean	43.27
Std Dev	3.75

Gaus mean : 44.0309

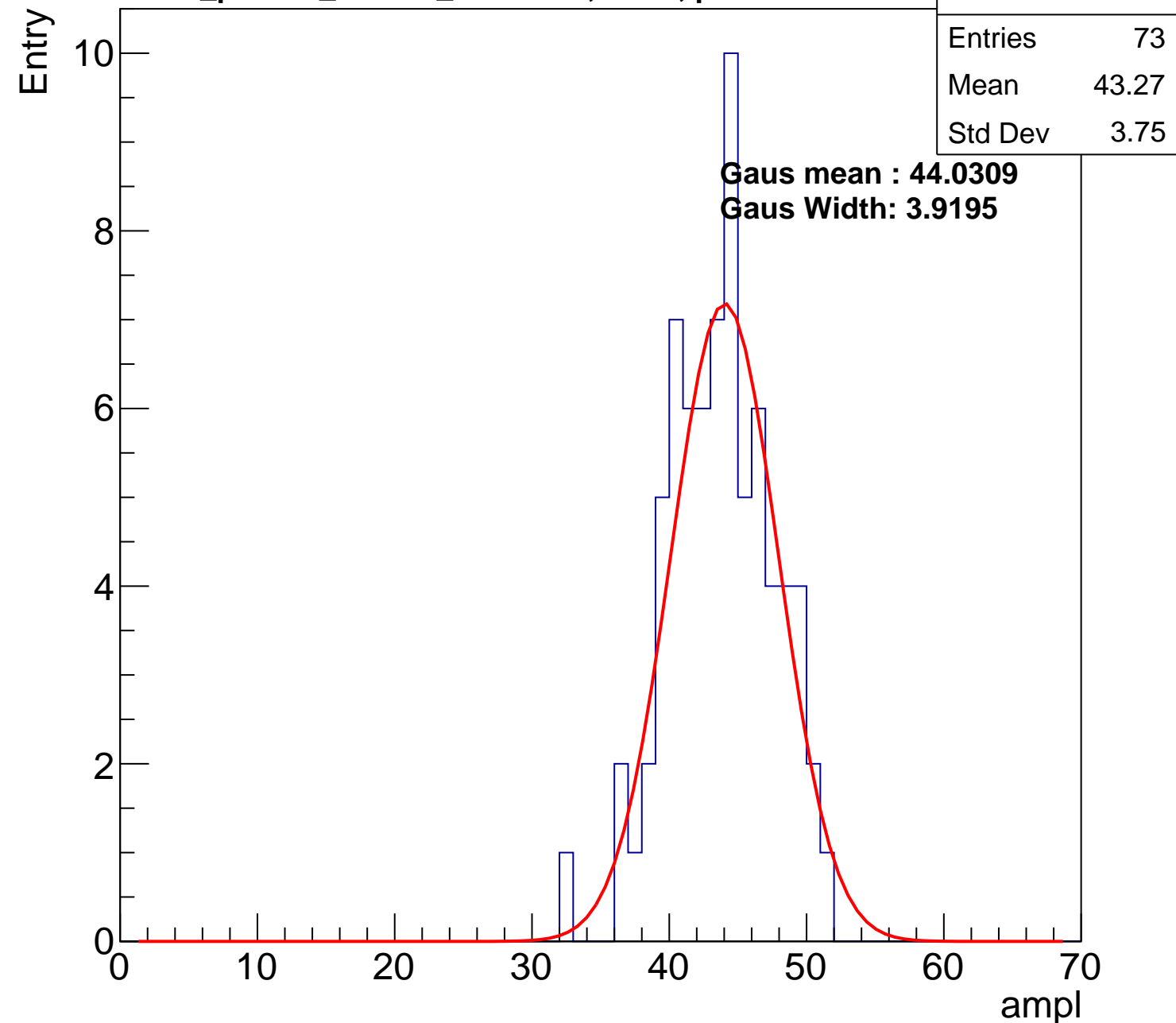
Gaus Width: 3.9195

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

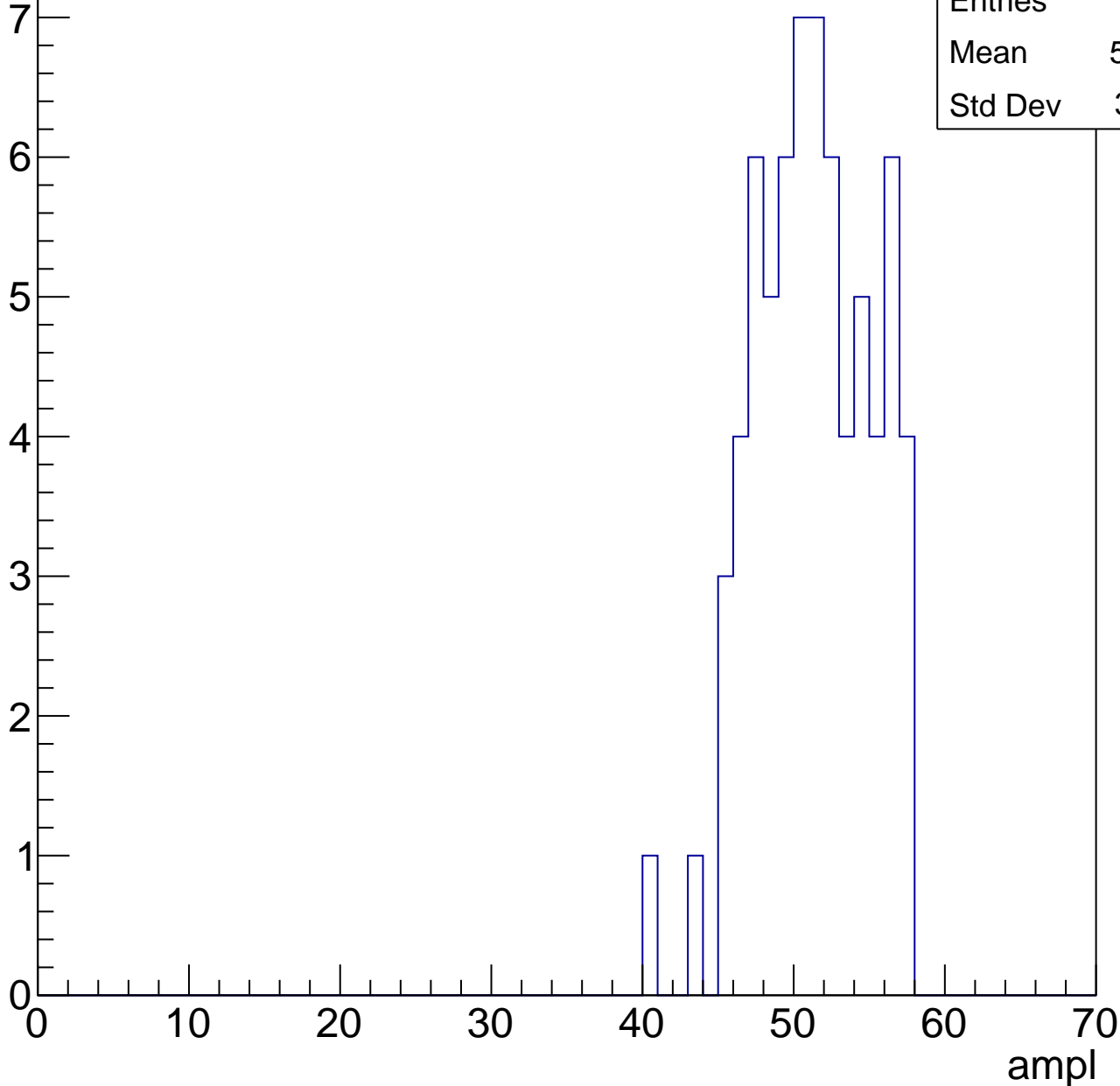


B0L000S, U4-ch117, adc3

calib_packv5_042523_0143.root, FC#5, port B1

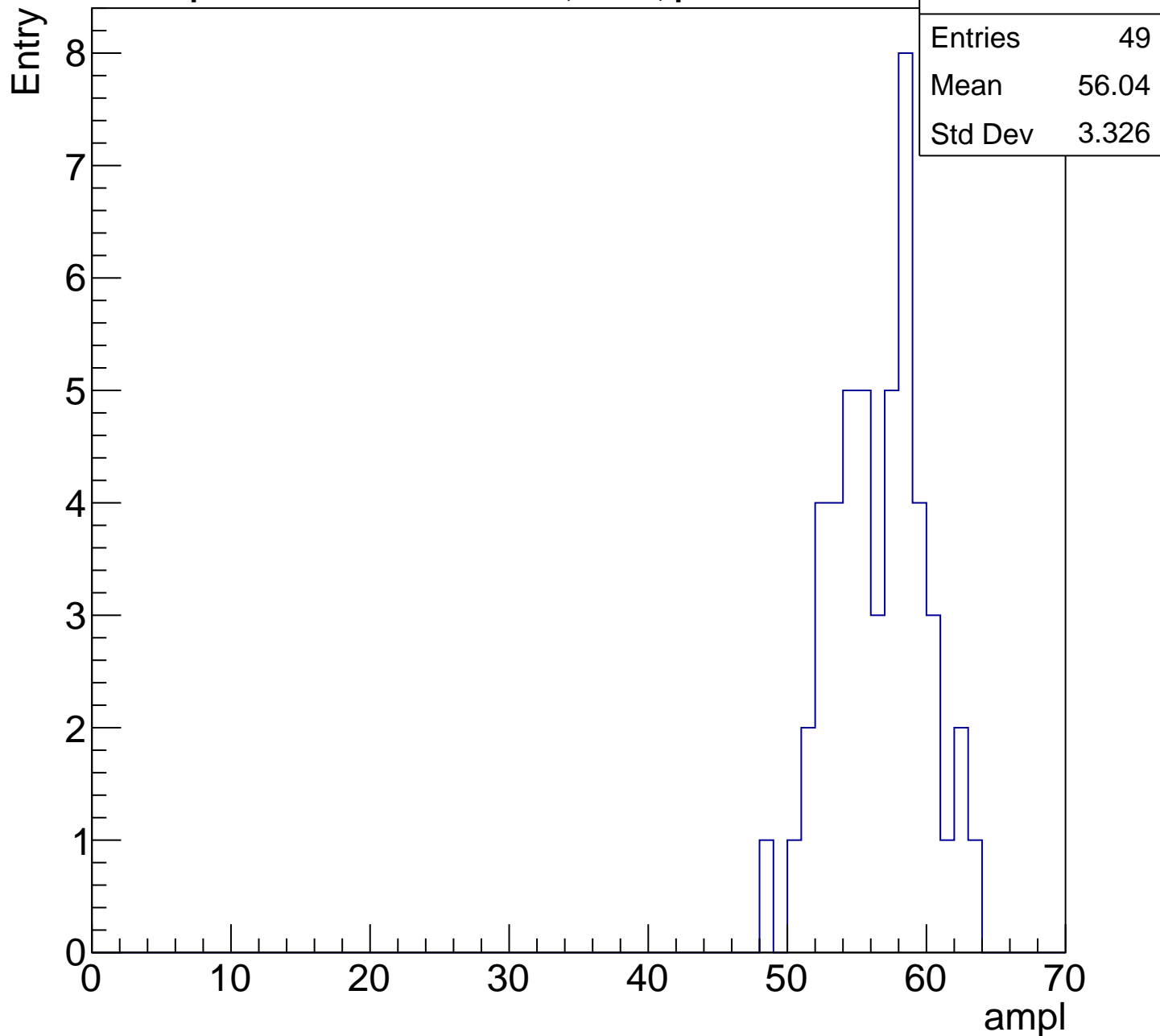
Entry

Entries	69
Mean	50.77
Std Dev	3.781



B0L000S, U4-ch117, adc4

calib_packv5_042523_0143.root, FC#5, port B1

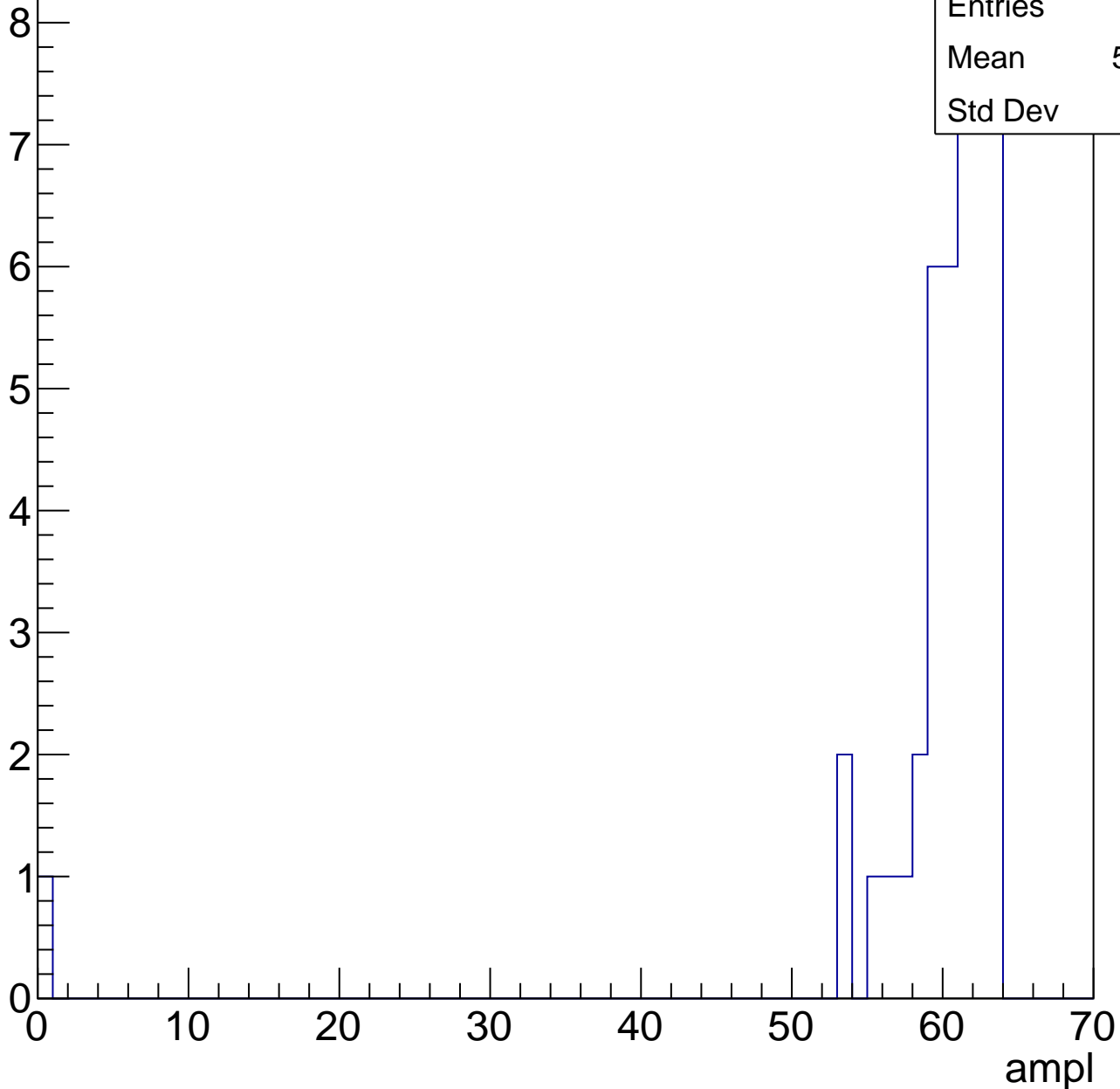


B0L000S, U4-ch117, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

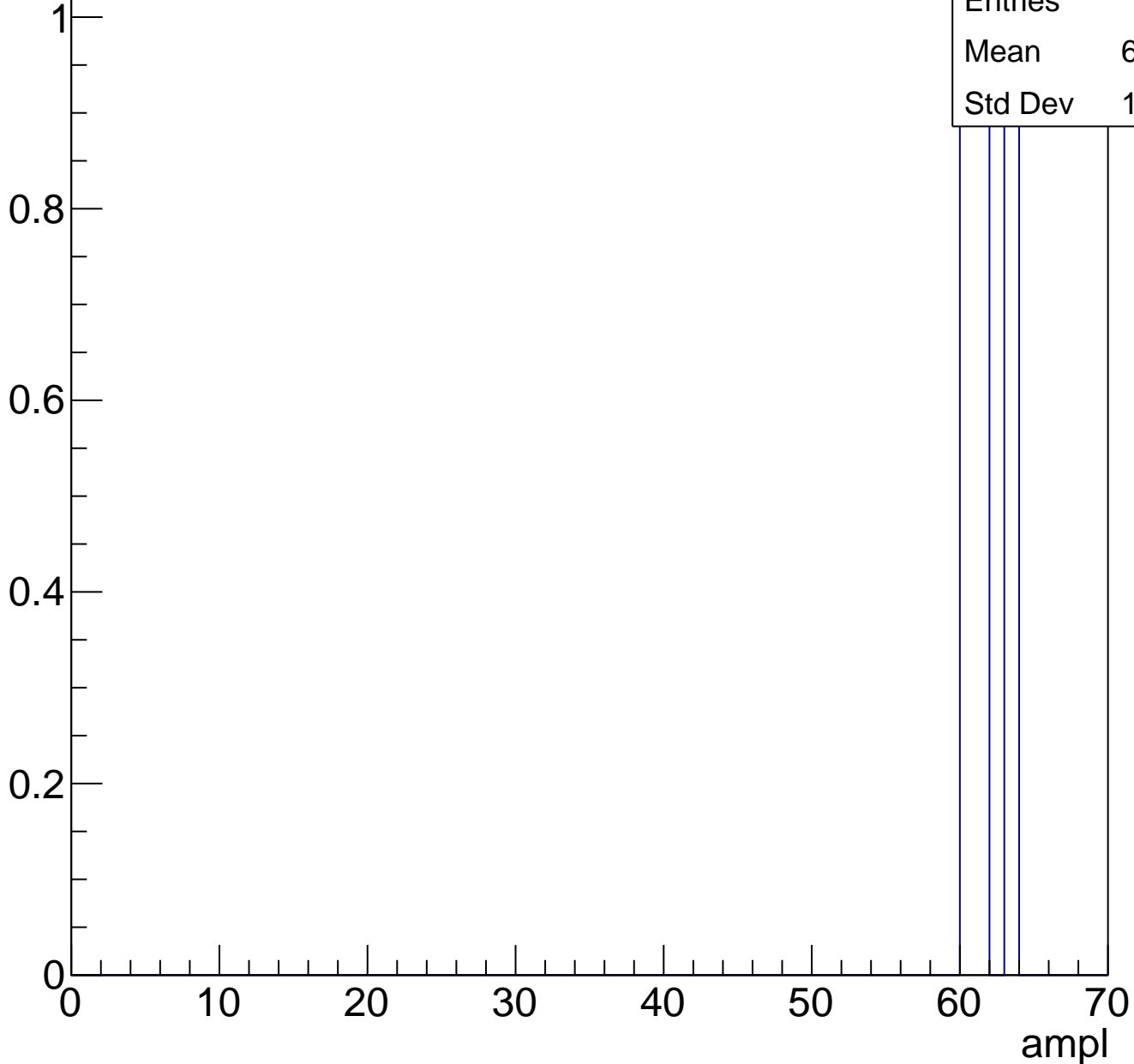
Entries	44
Mean	58.91
Std Dev	9.32



B0L000S, U4-ch117, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch117, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch118, adc0

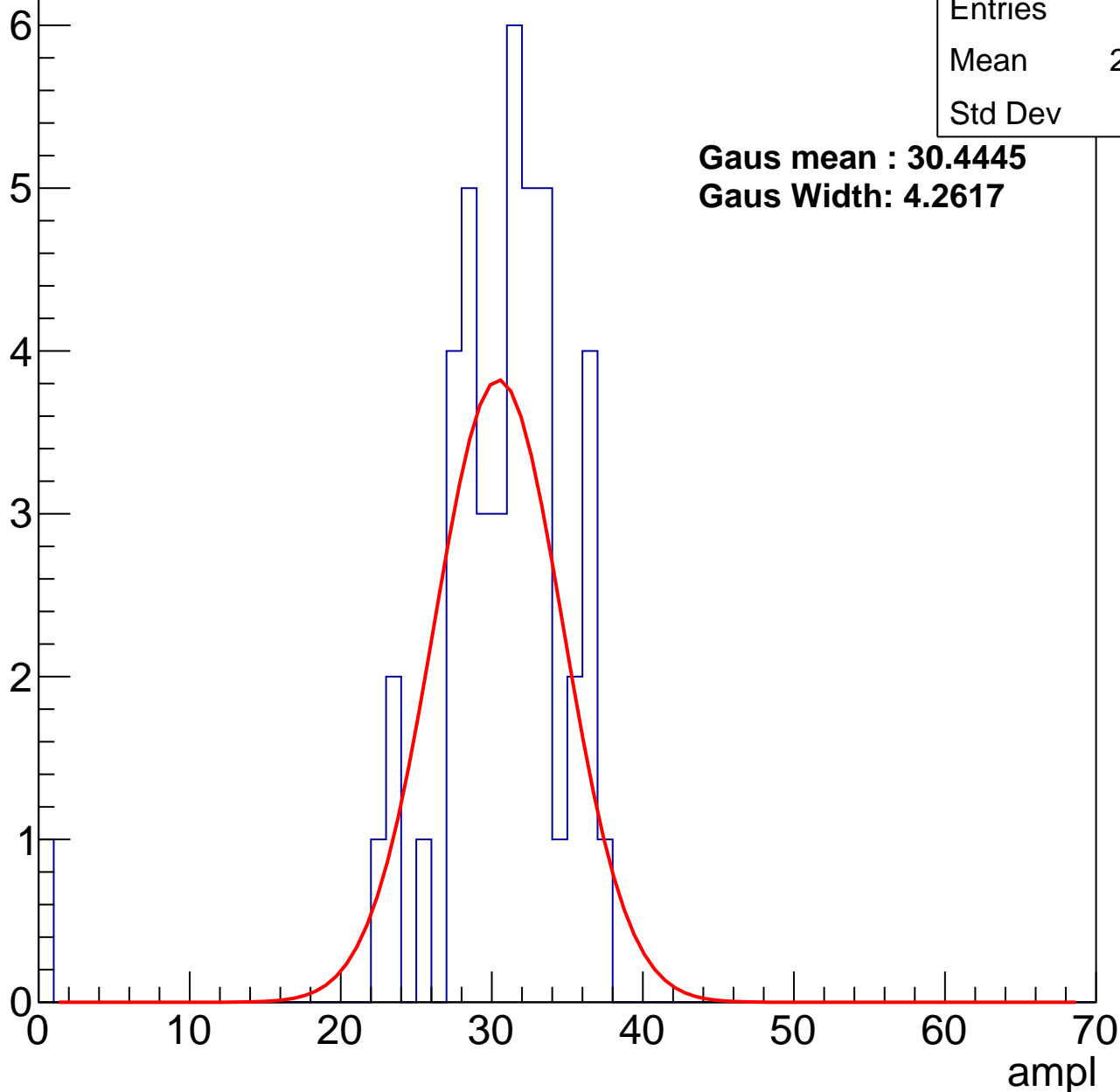
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	29.86
Std Dev	5.79

Gaus mean : 30.4445

Gaus Width: 4.2617



B0L000S, U4-ch118, adc1

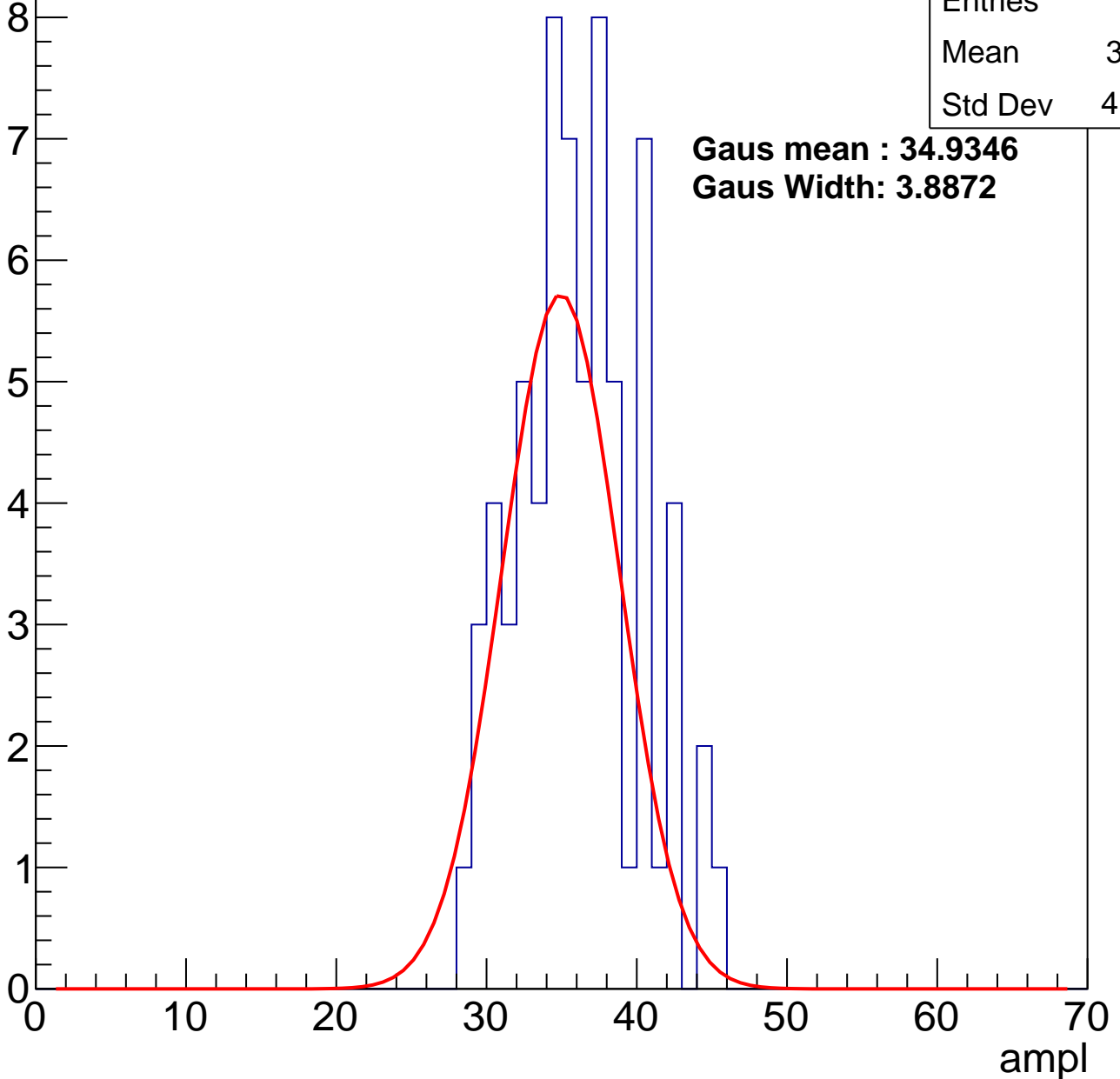
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	35.71
Std Dev	4.004

Gaus mean : 34.9346

Gaus Width: 3.8872



B0L000S, U4-ch118, adc2

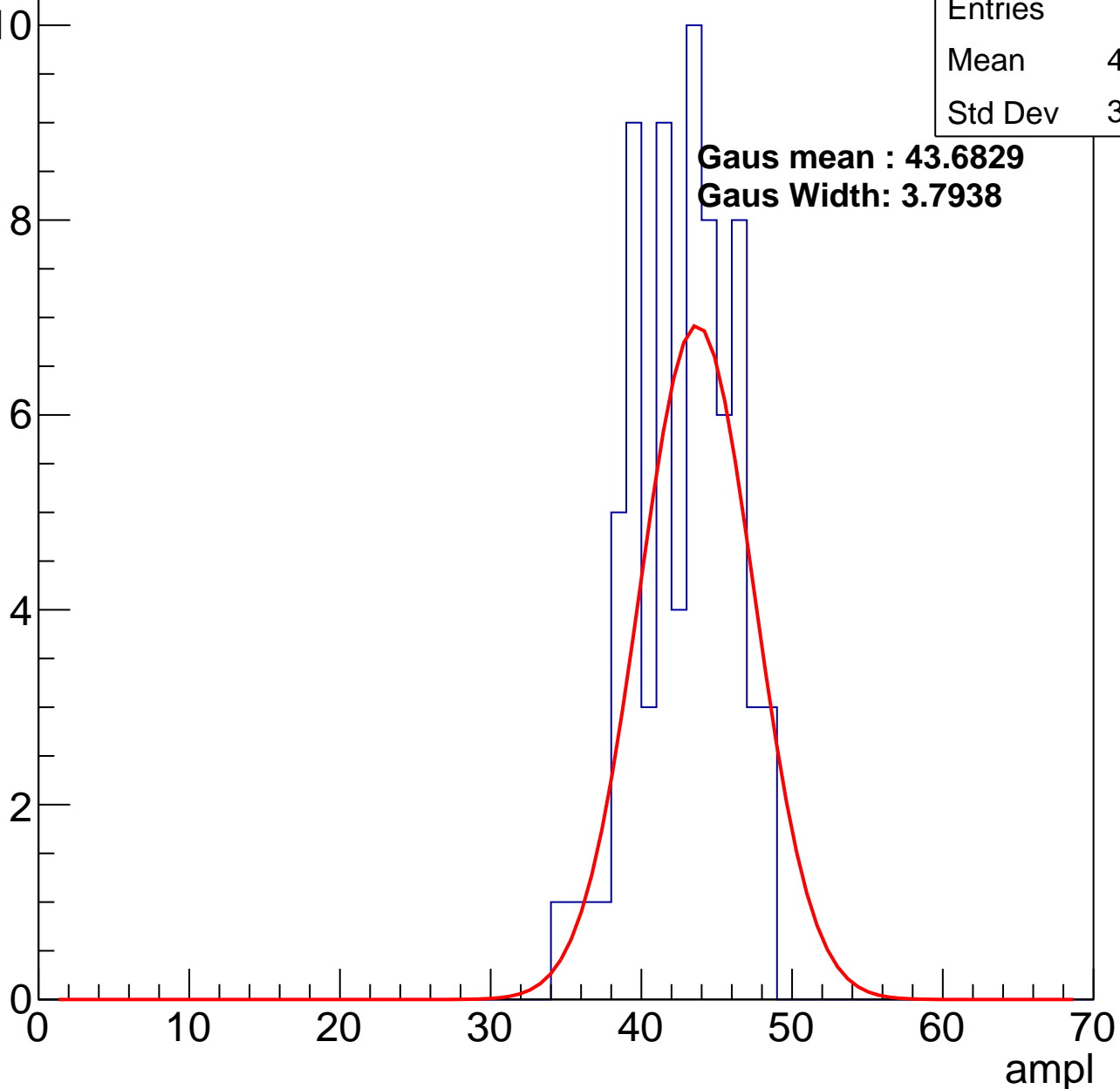
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	42.29
Std Dev	3.242

Gaus mean : 43.6829

Gaus Width: 3.7938

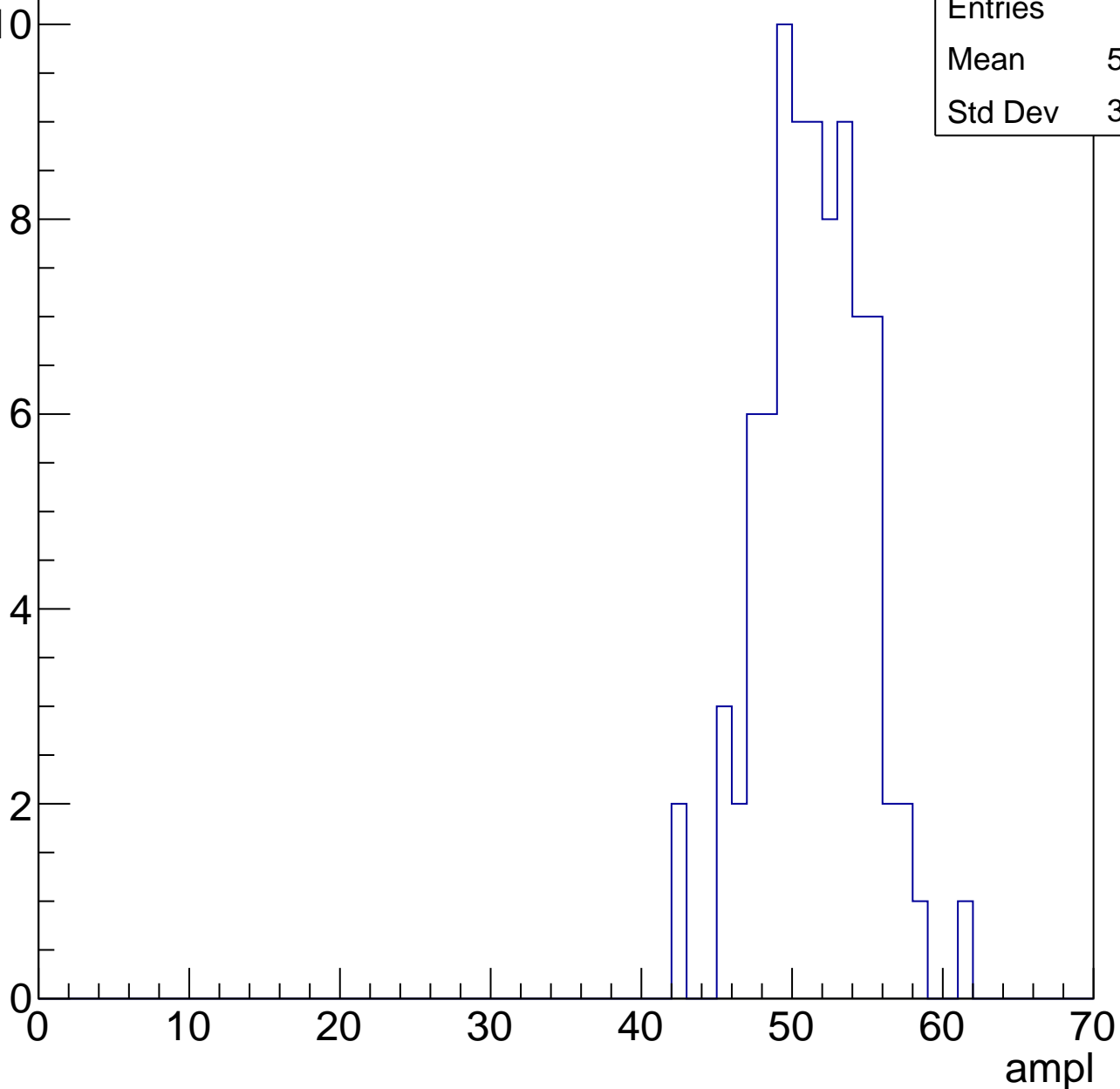


B0L000S, U4-ch118, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	84
Mean	50.96
Std Dev	3.466

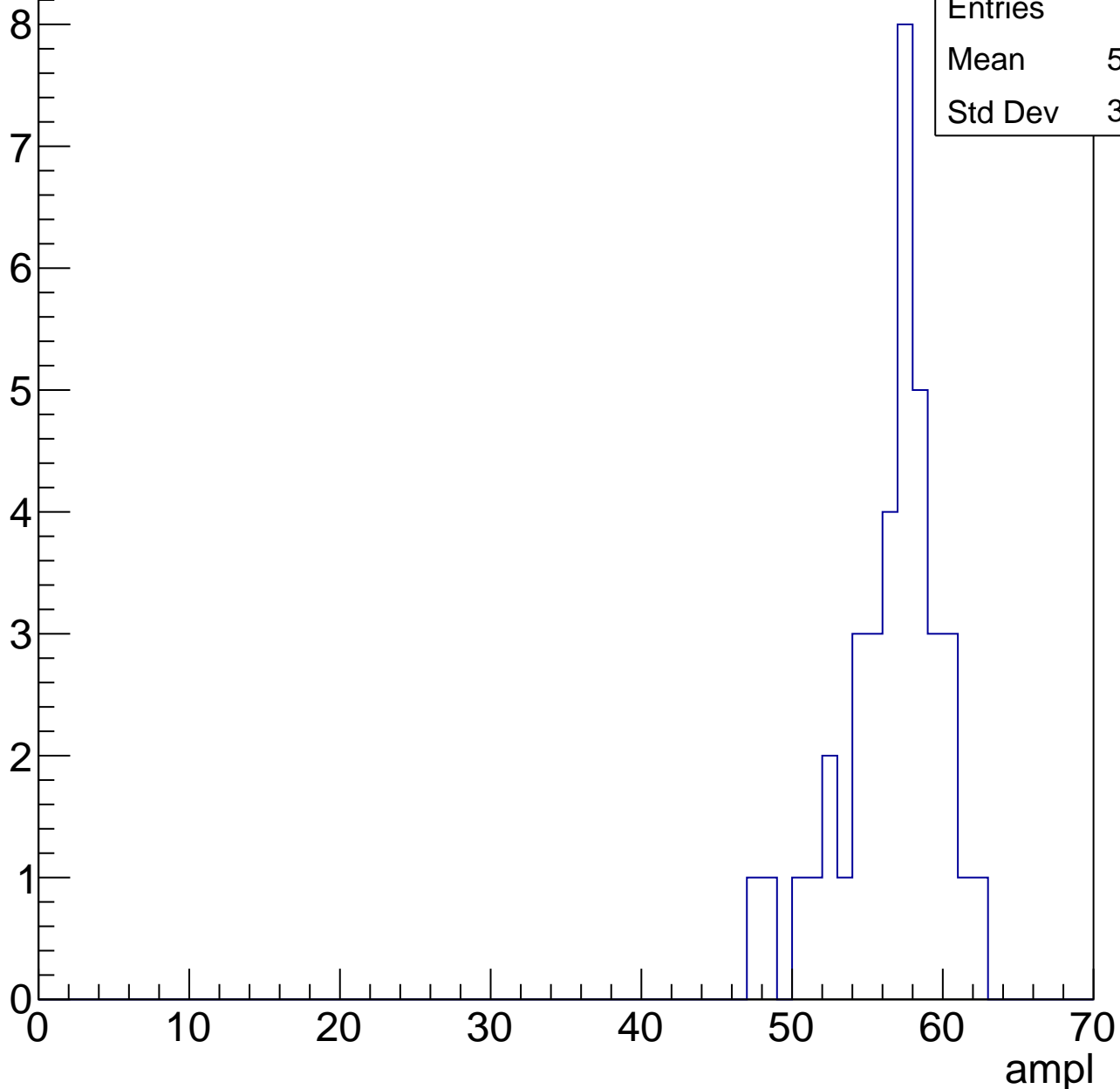


B0L000S, U4-ch118, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	38
Mean	56.05
Std Dev	3.348

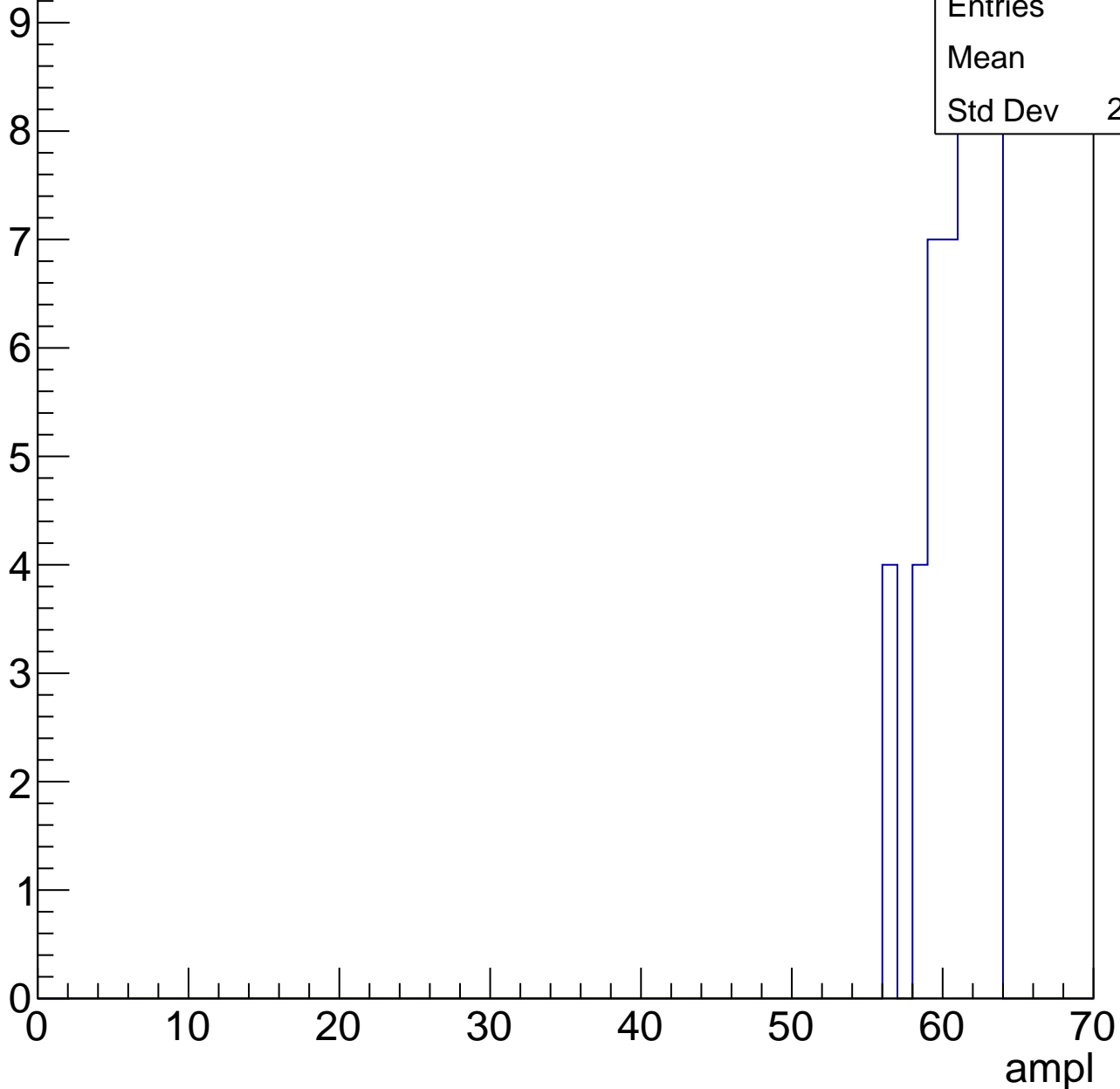


B0L000S, U4-ch118, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	60.4
Std Dev	2.039



B0L000S, U4-ch118, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch118, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch119, adc0

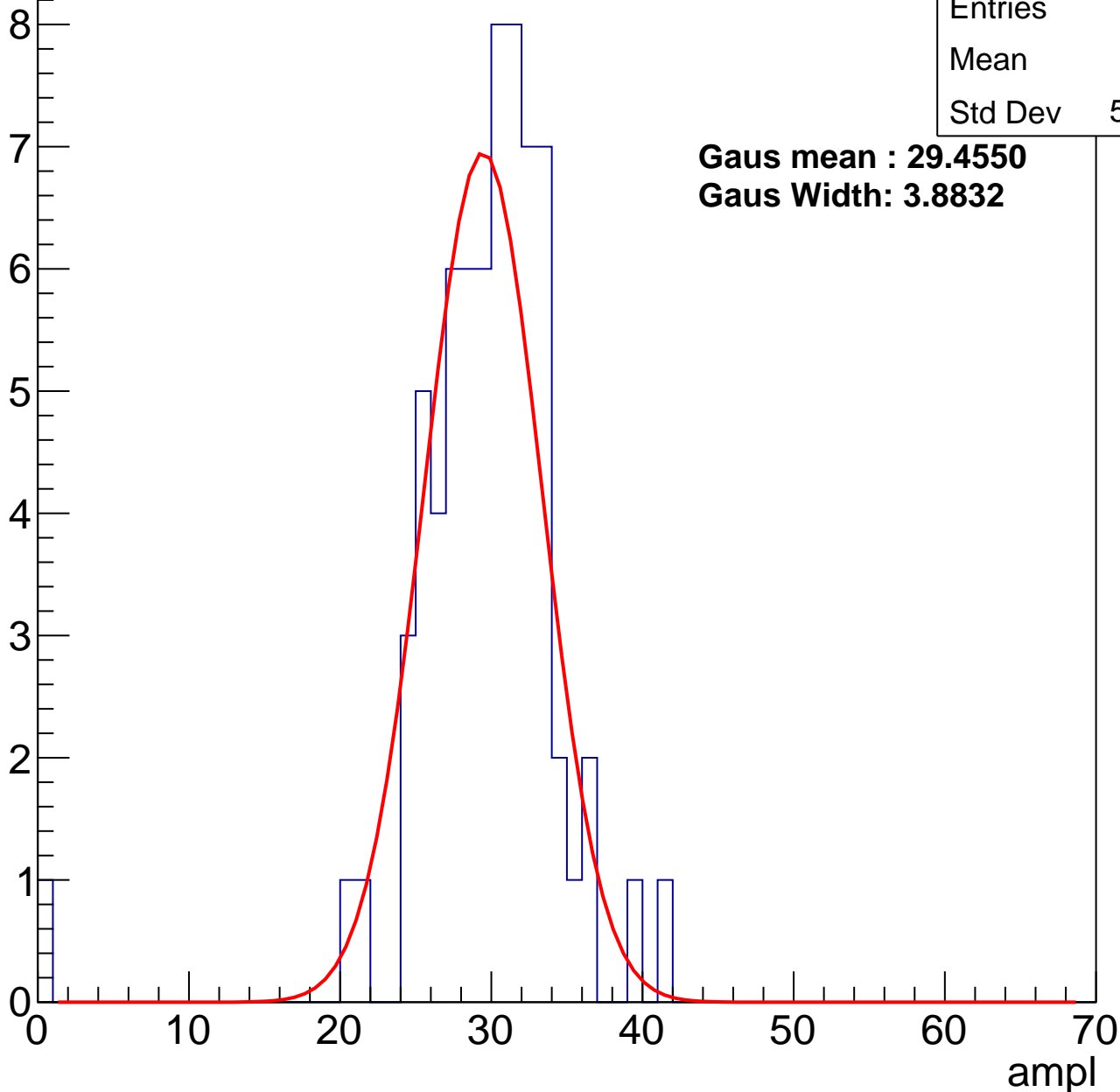
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.2
Std Dev	5.137

Gaus mean : 29.4550

Gaus Width: 3.8832



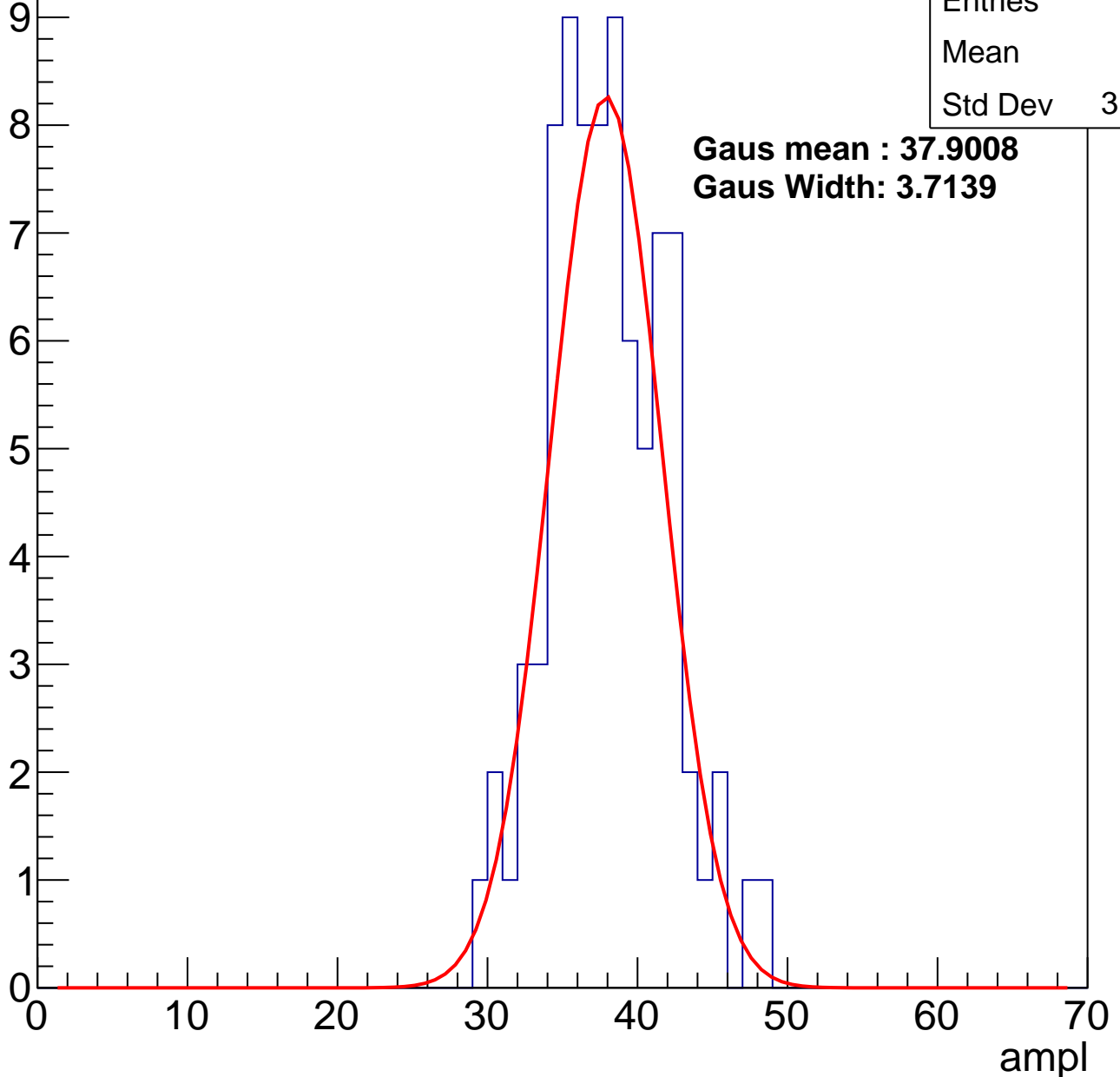
B0L000S, U4-ch119, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	84
Mean	37.6
Std Dev	3.846

Gaus mean : 37.9008
Gaus Width: 3.7139



B0L000S, U4-ch119, adc2

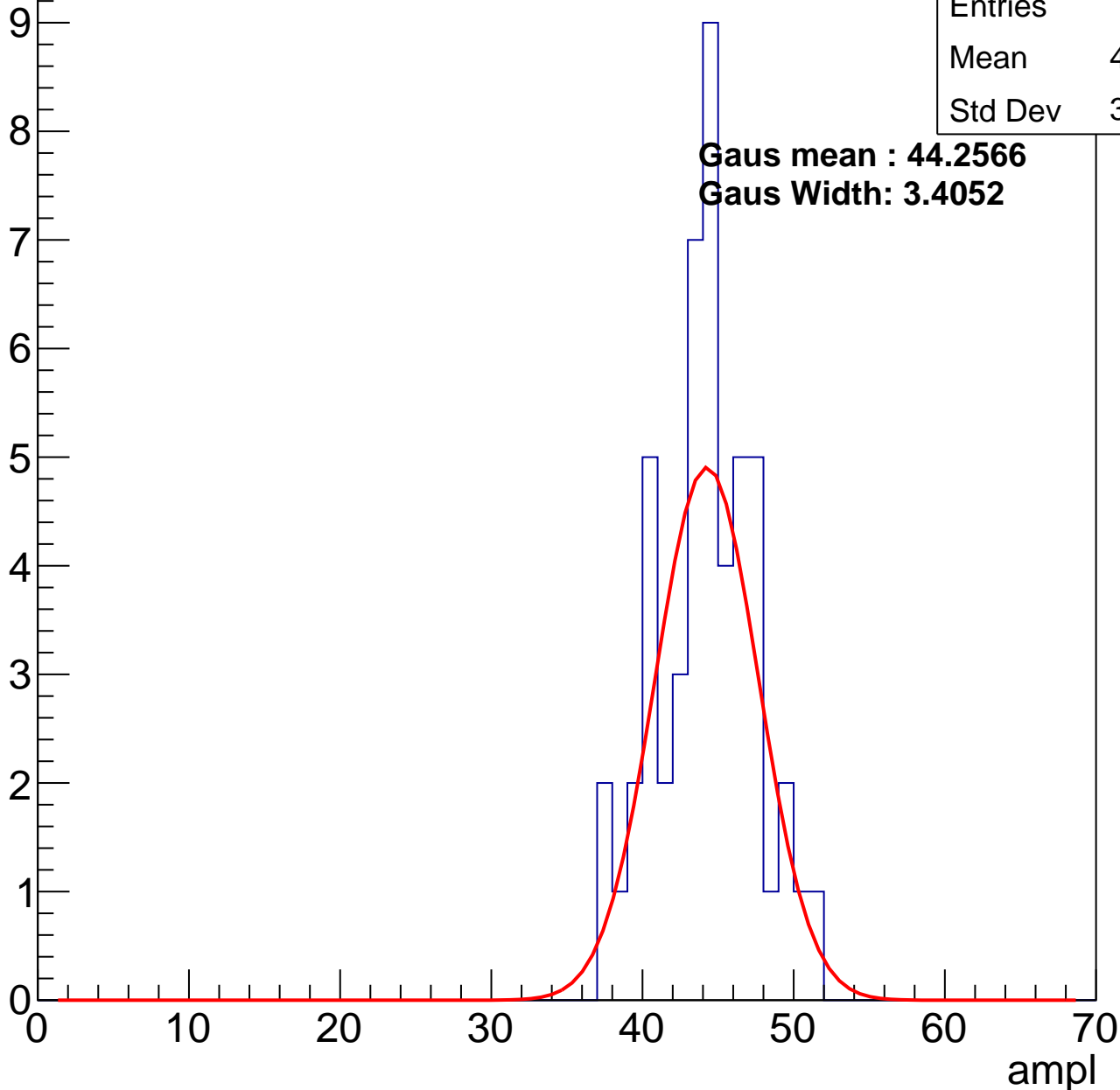
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	43.74
Std Dev	3.218

Gaus mean : 44.2566

Gaus Width: 3.4052

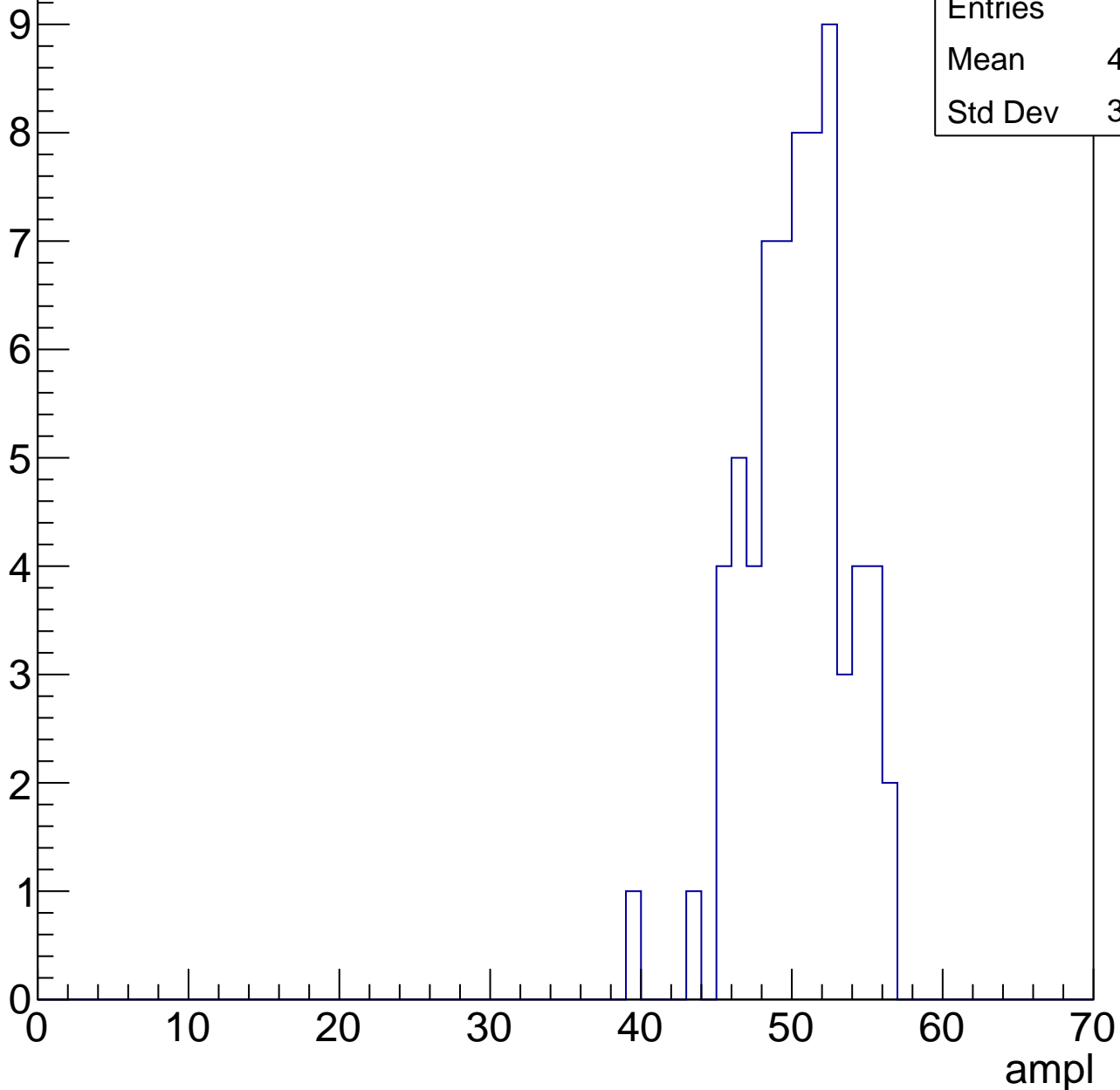


B0L000S, U4-ch119, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.88
Std Dev	3.294

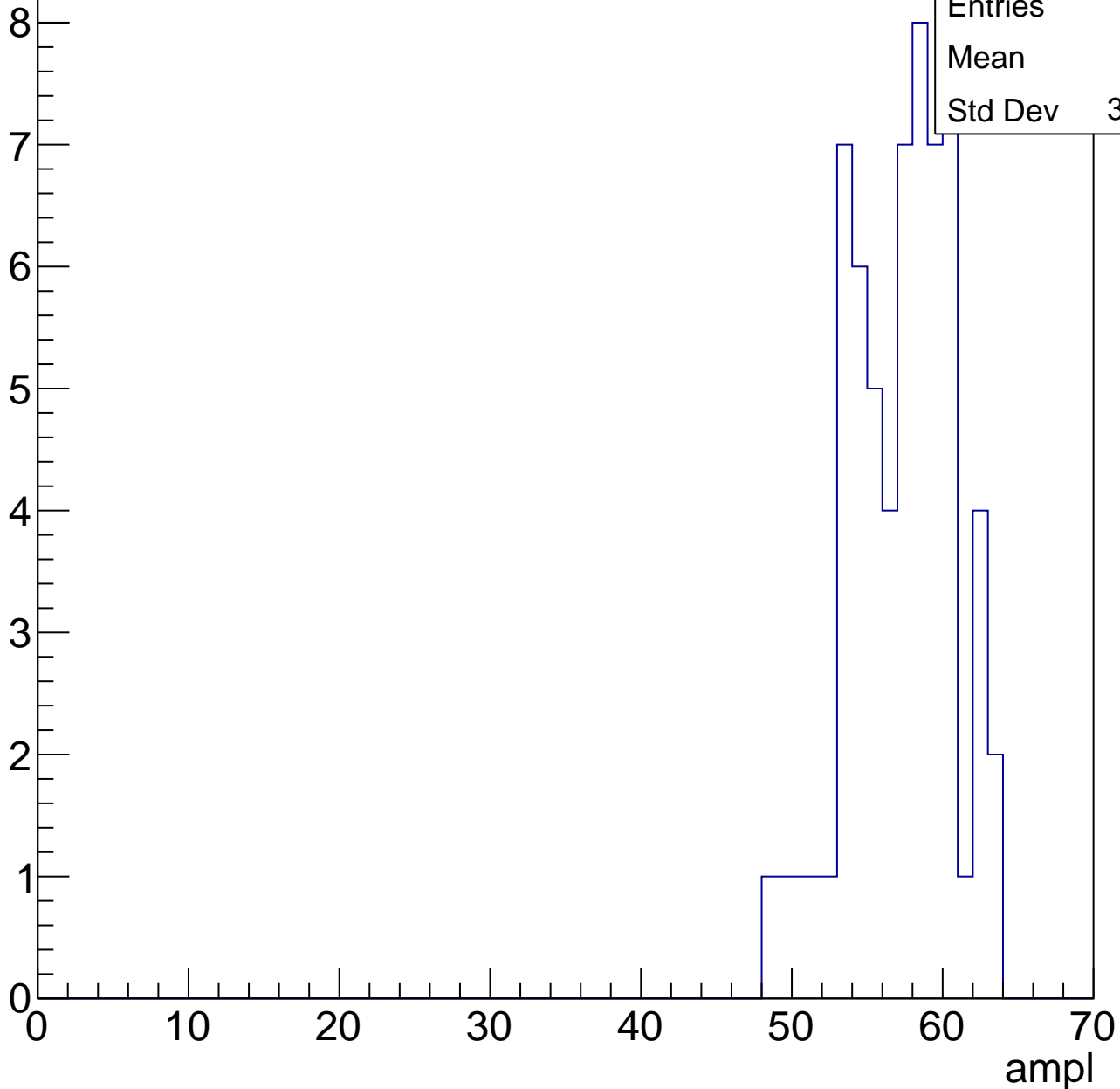


B0L000S, U4-ch119, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	56.8
Std Dev	3.397

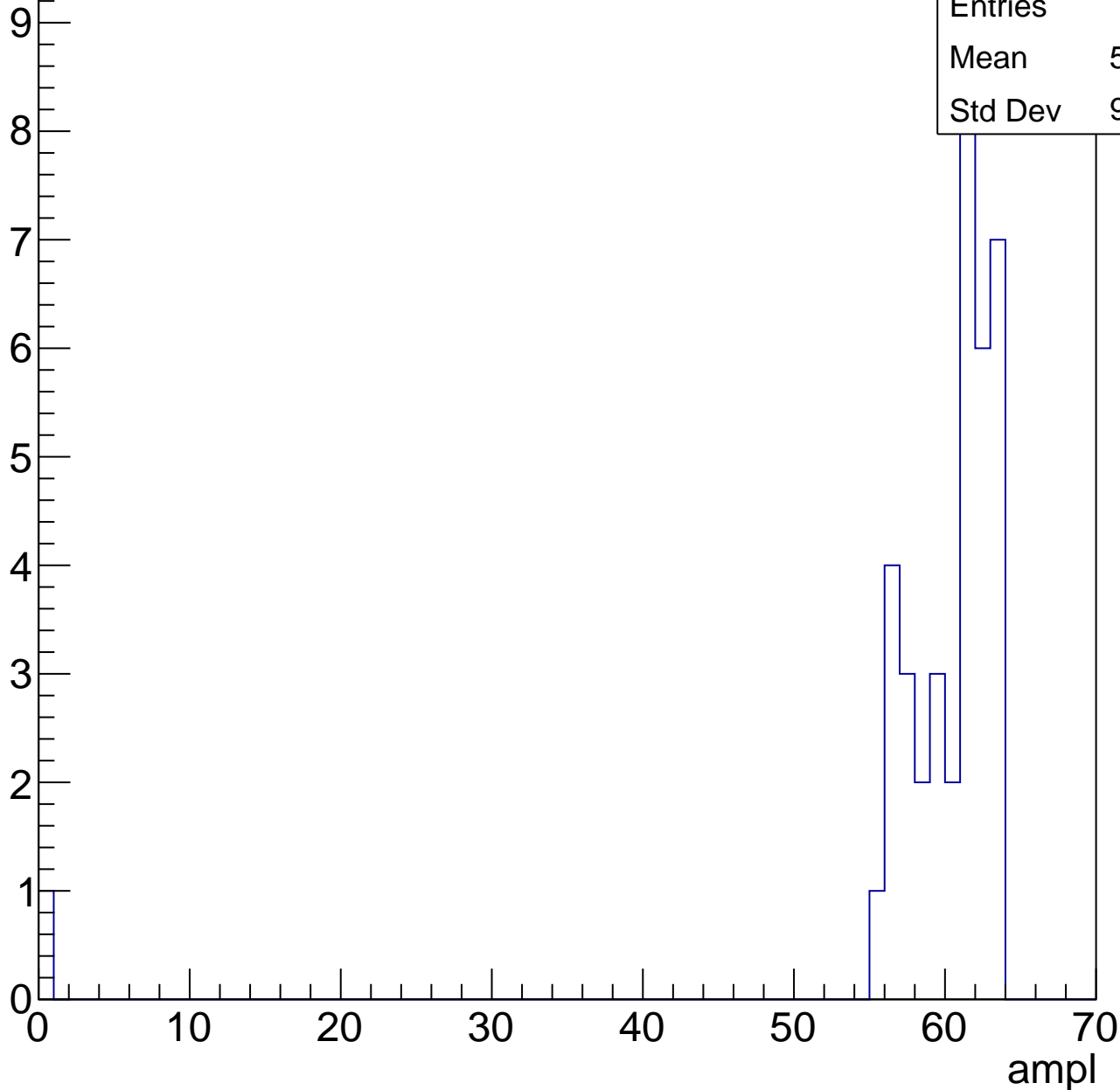


B0L000S, U4-ch119, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.55
Std Dev	9.925



B0L000S, U4-ch119, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch119, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch120, adc0

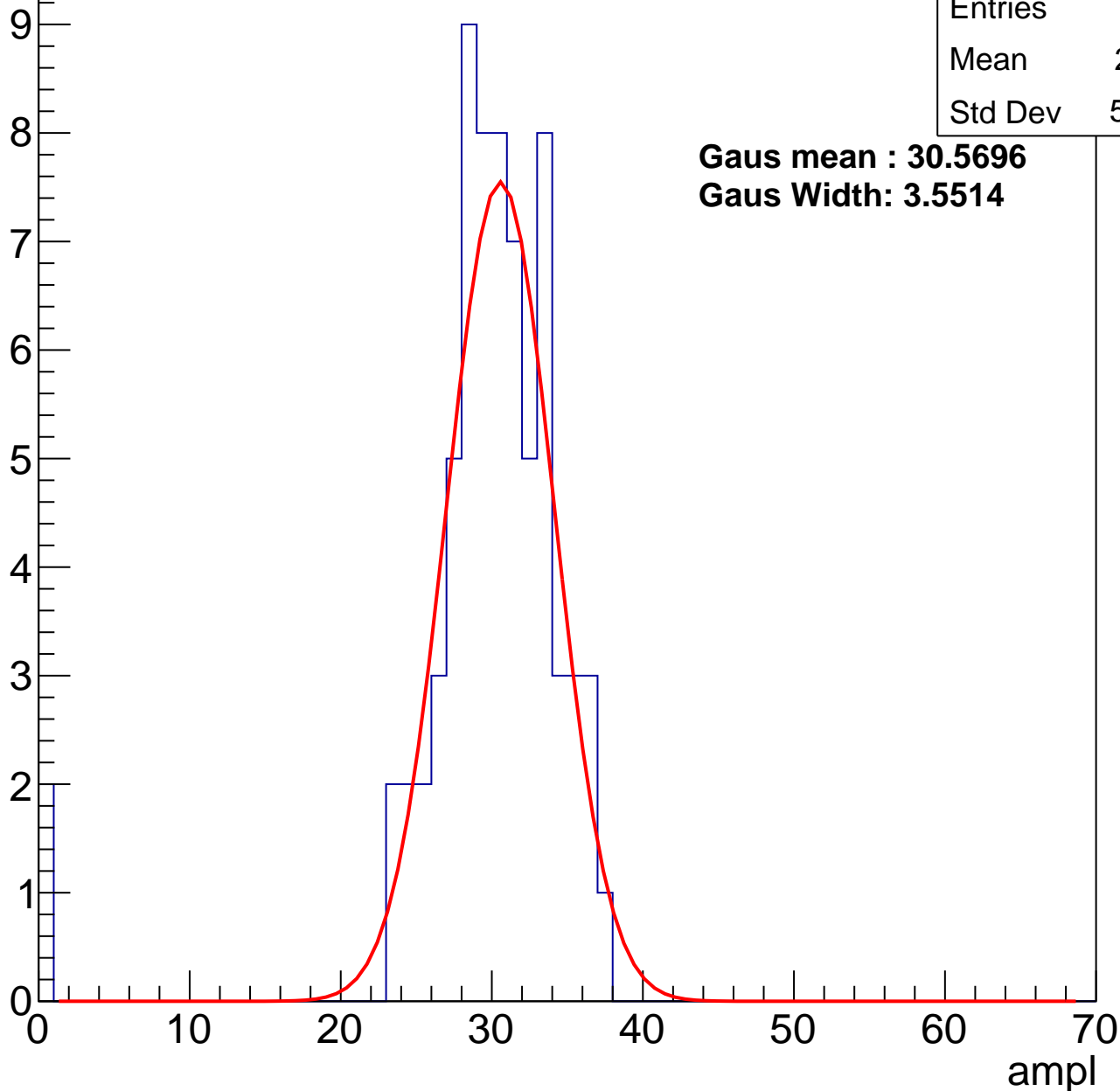
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	29.21
Std Dev	5.924

Gaus mean : 30.5696

Gaus Width: 3.5514



B0L000S, U4-ch120, adc1

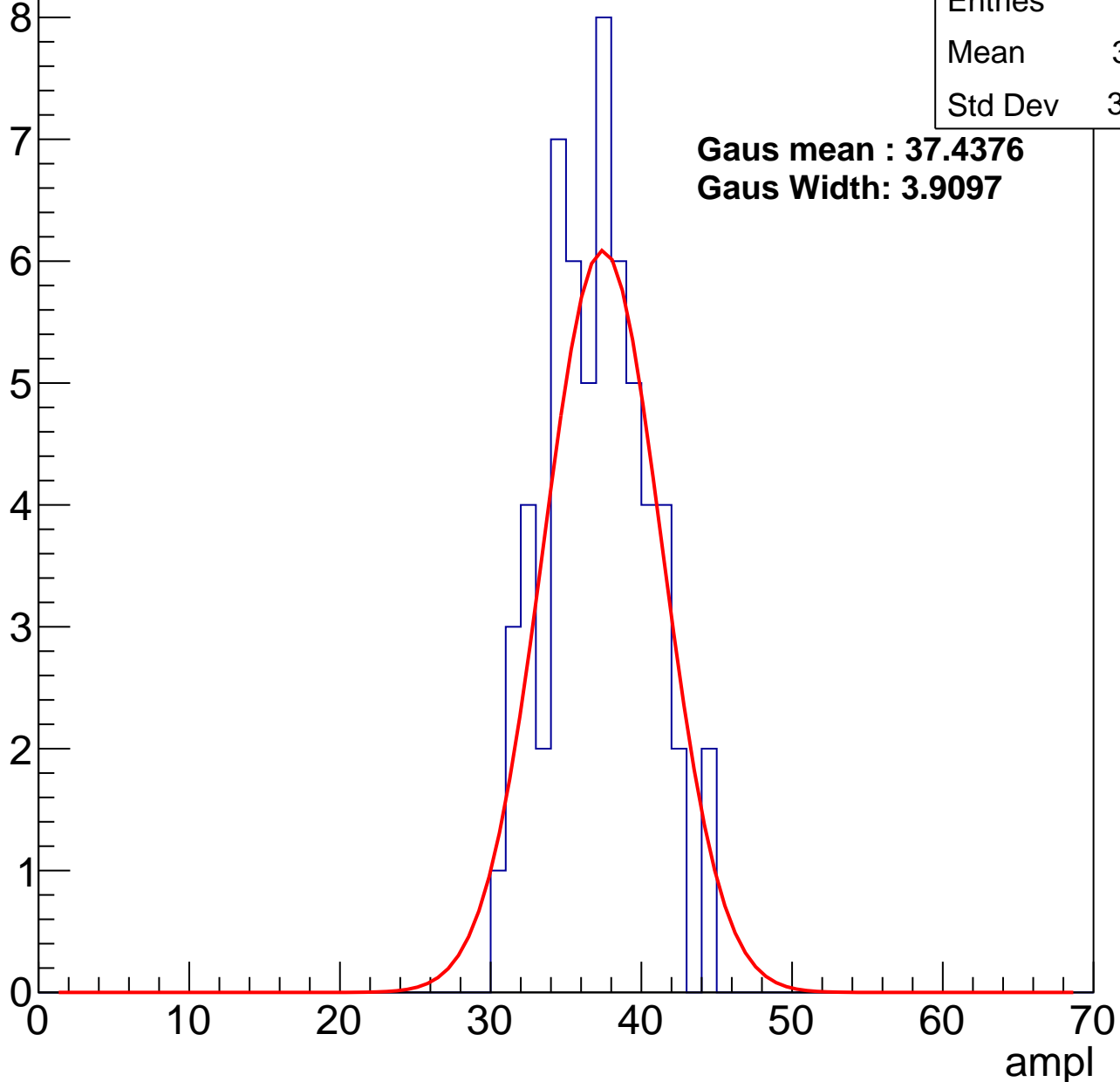
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.61
Std Dev	3.309

Gaus mean : 37.4376

Gaus Width: 3.9097



B0L000S, U4-ch120, adc2

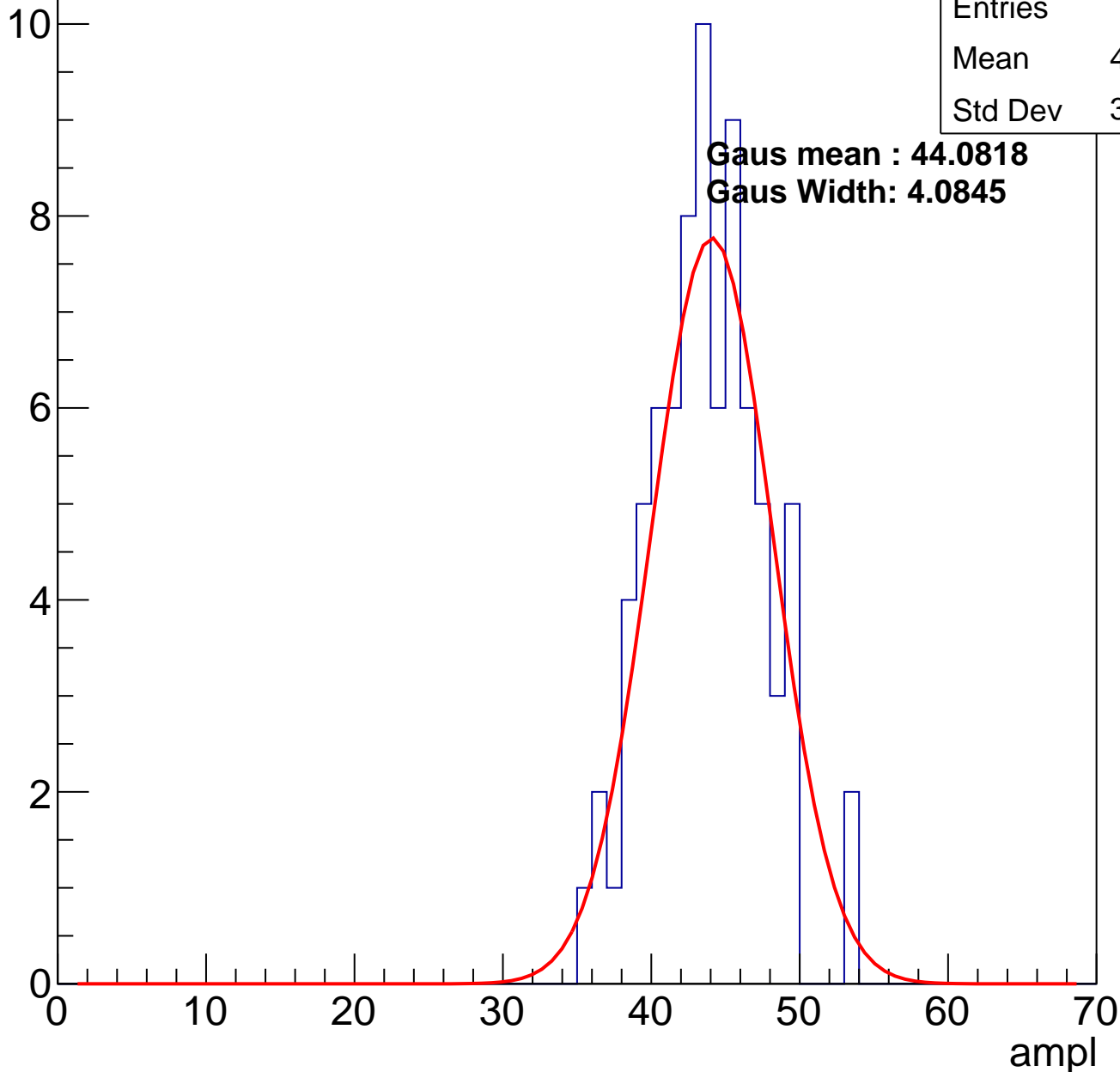
calib_packv5_042523_0143.root, FC#5, port B1

Entries	79
Mean	43.27
Std Dev	3.714

Gaus mean : 44.0818

Gaus Width: 4.0845

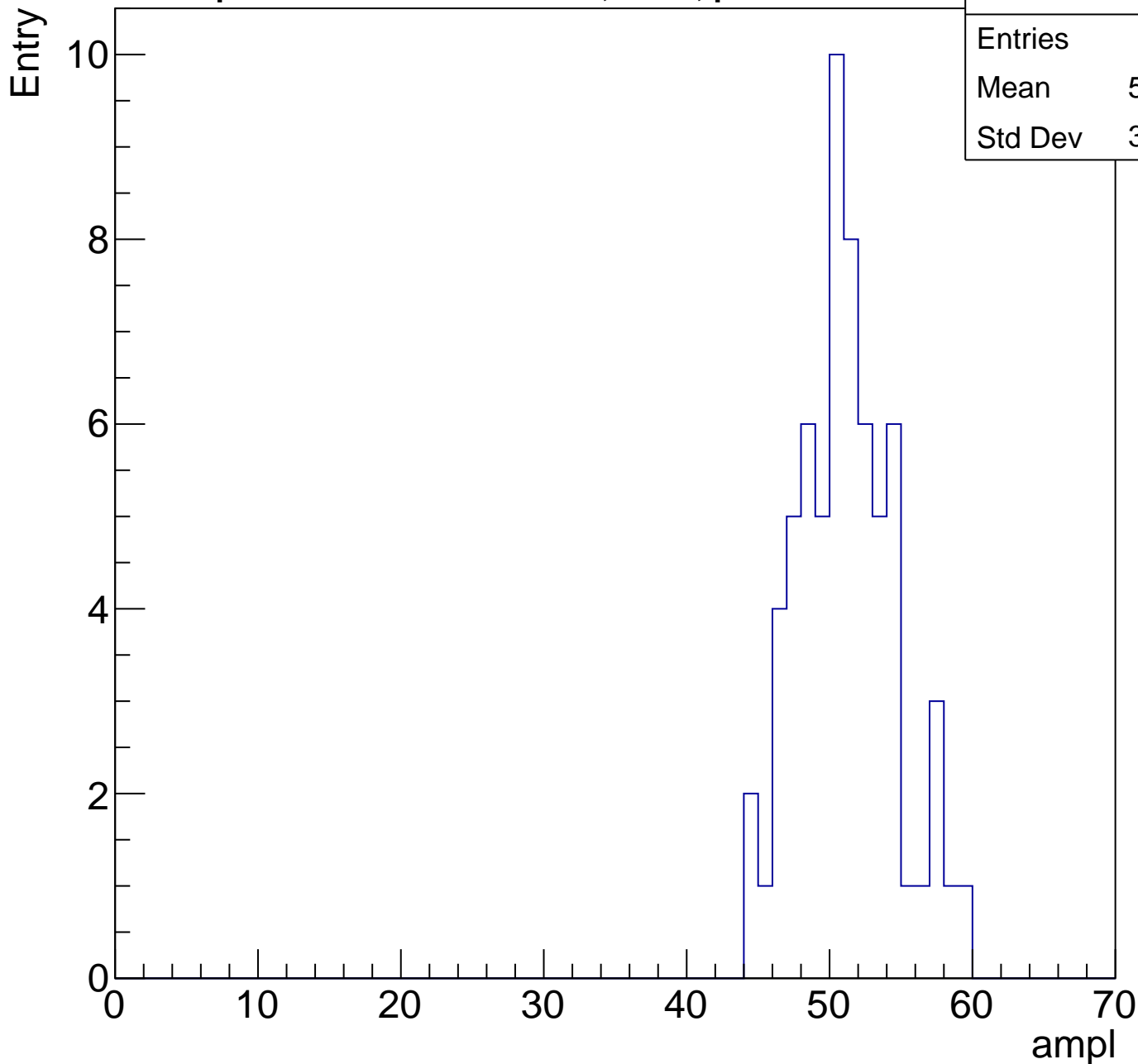
Entry



B0L000S, U4-ch120, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	65
Mean	50.66
Std Dev	3.357

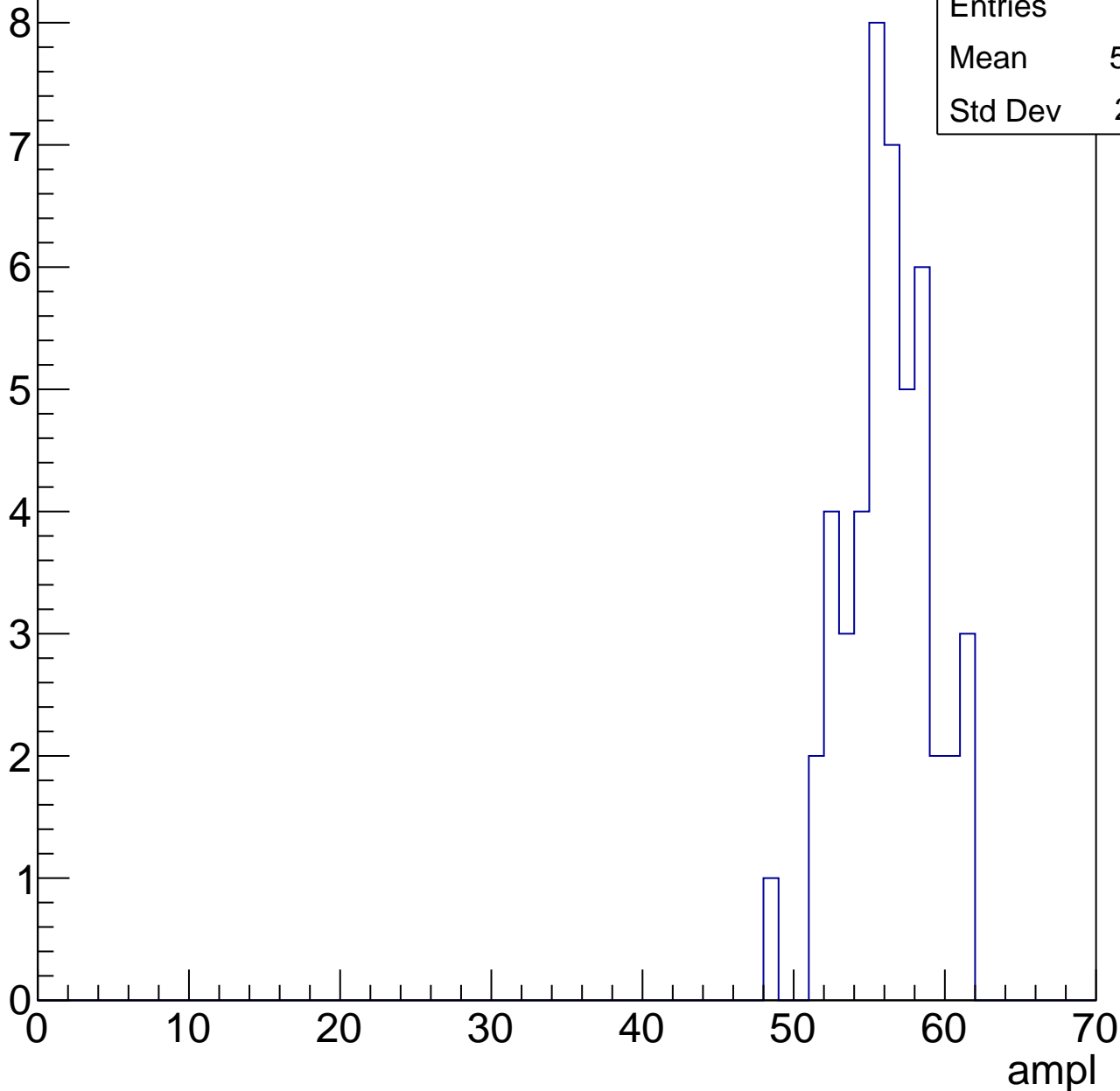


B0L000S, U4-ch120, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	55.72
Std Dev	2.841



B0L000S, U4-ch120, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	43
Mean	58.81
Std Dev	9.314

0

B0L000S, U4-ch120, adc6

calib_packv5_042523_0143.root, FC#5, port B1

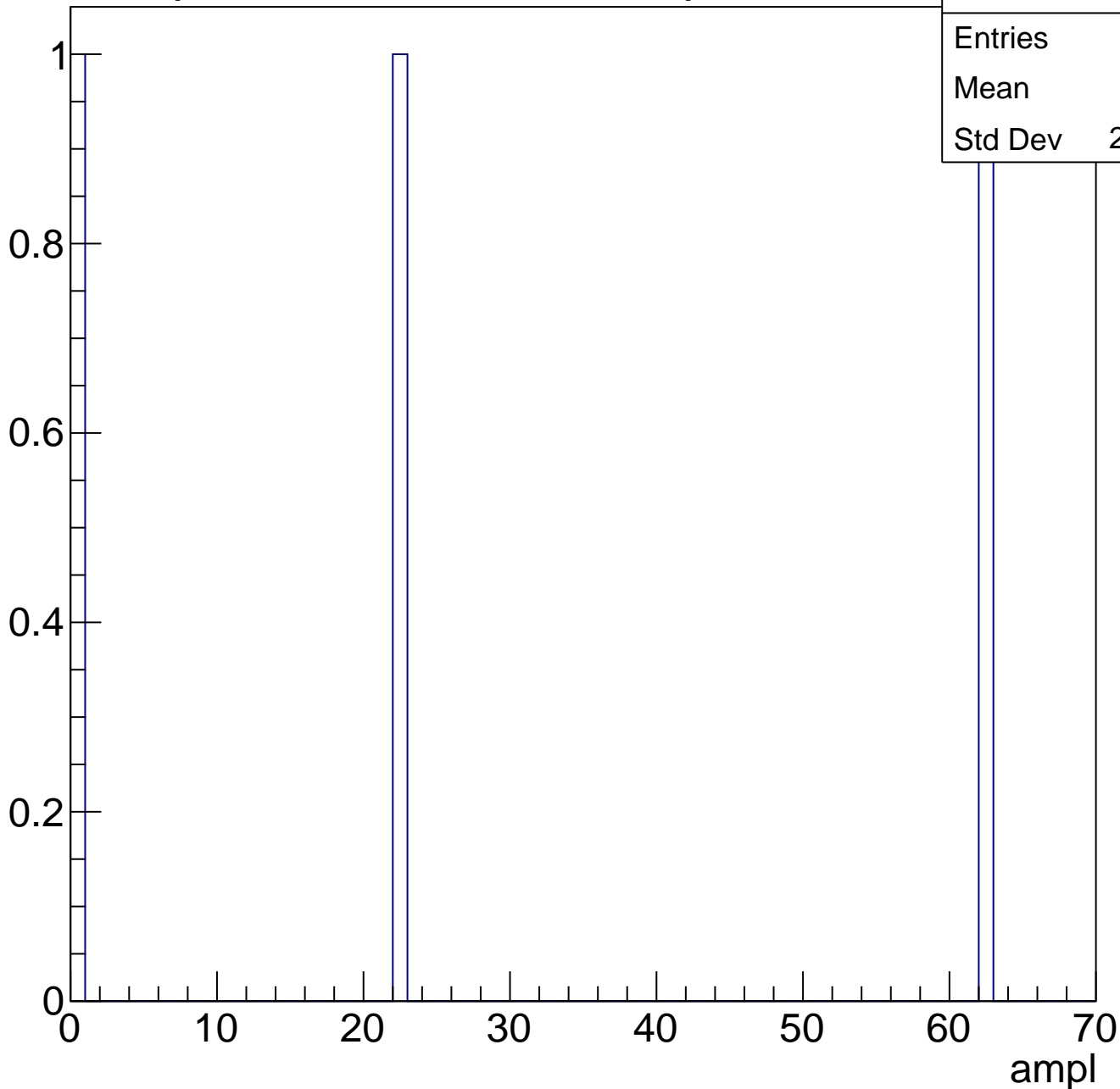
Entry



B0L000S, U4-ch120, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28
Std Dev	25.66

B0L000S, U4-ch121, adc0

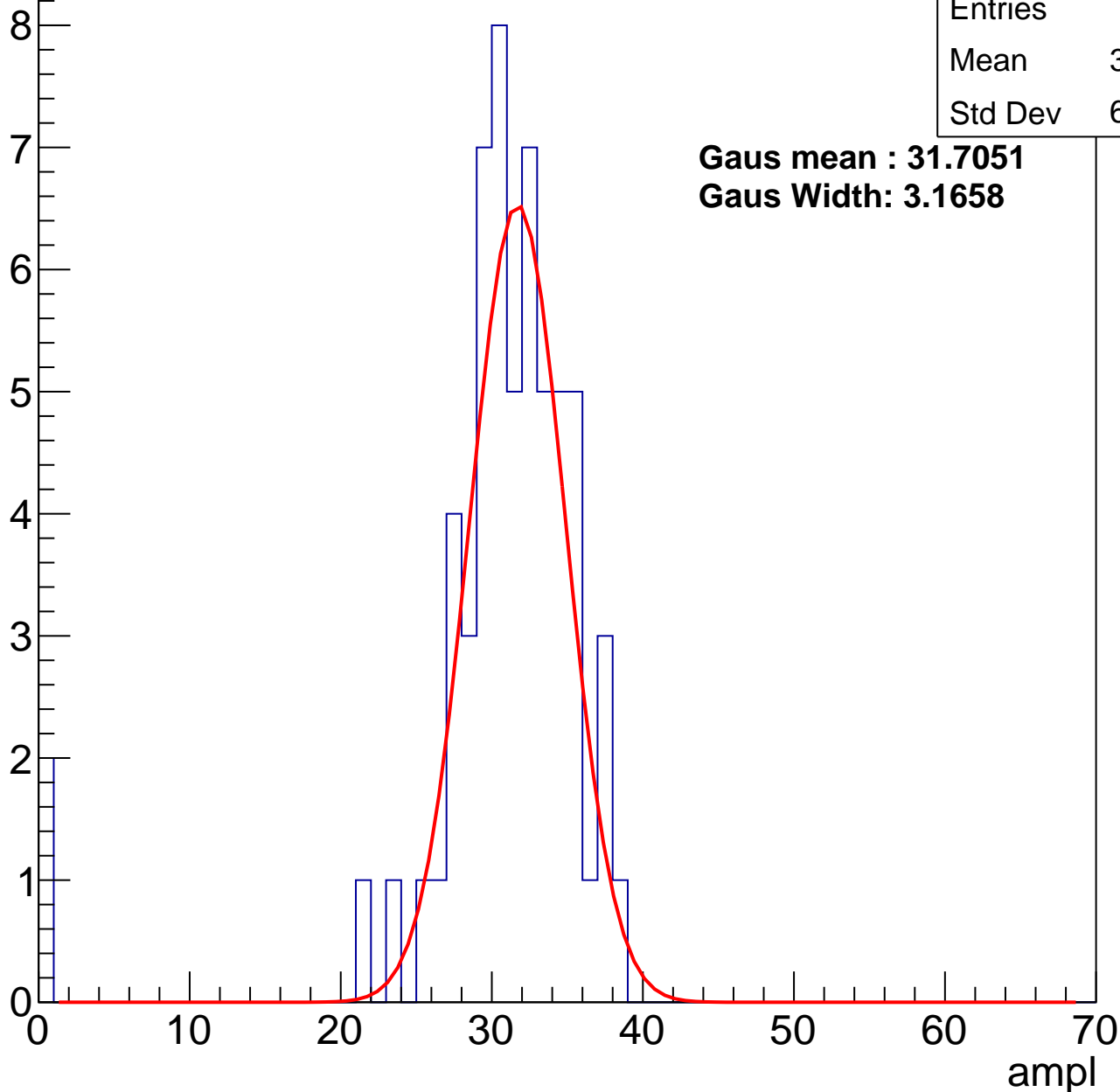
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.07
Std Dev	6.527

Gaus mean : 31.7051

Gaus Width: 3.1658



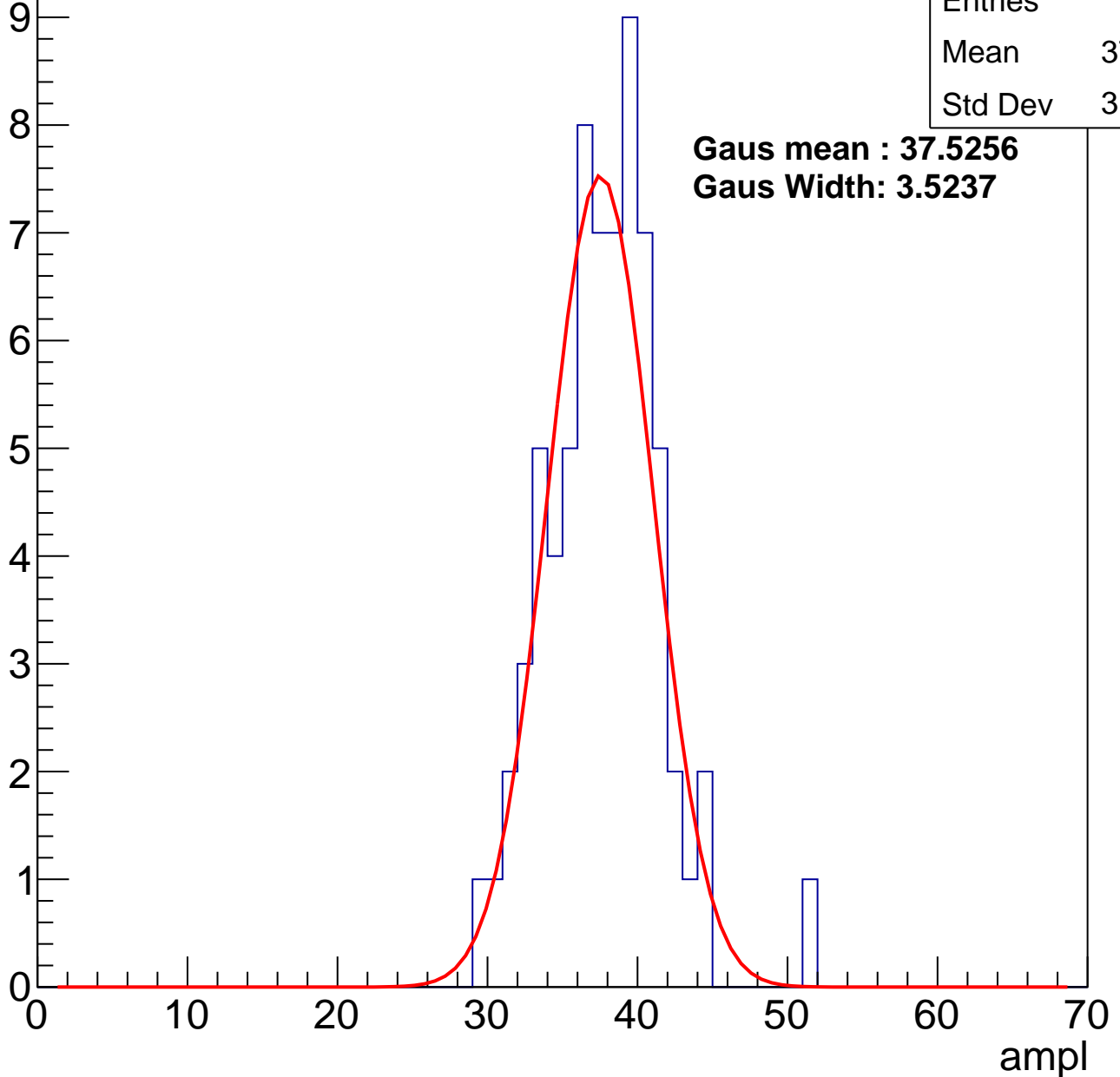
B0L000S, U4-ch121, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.26
Std Dev	3.714

Gaus mean : 37.5256
Gaus Width: 3.5237



B0L000S, U4-ch121, adc2

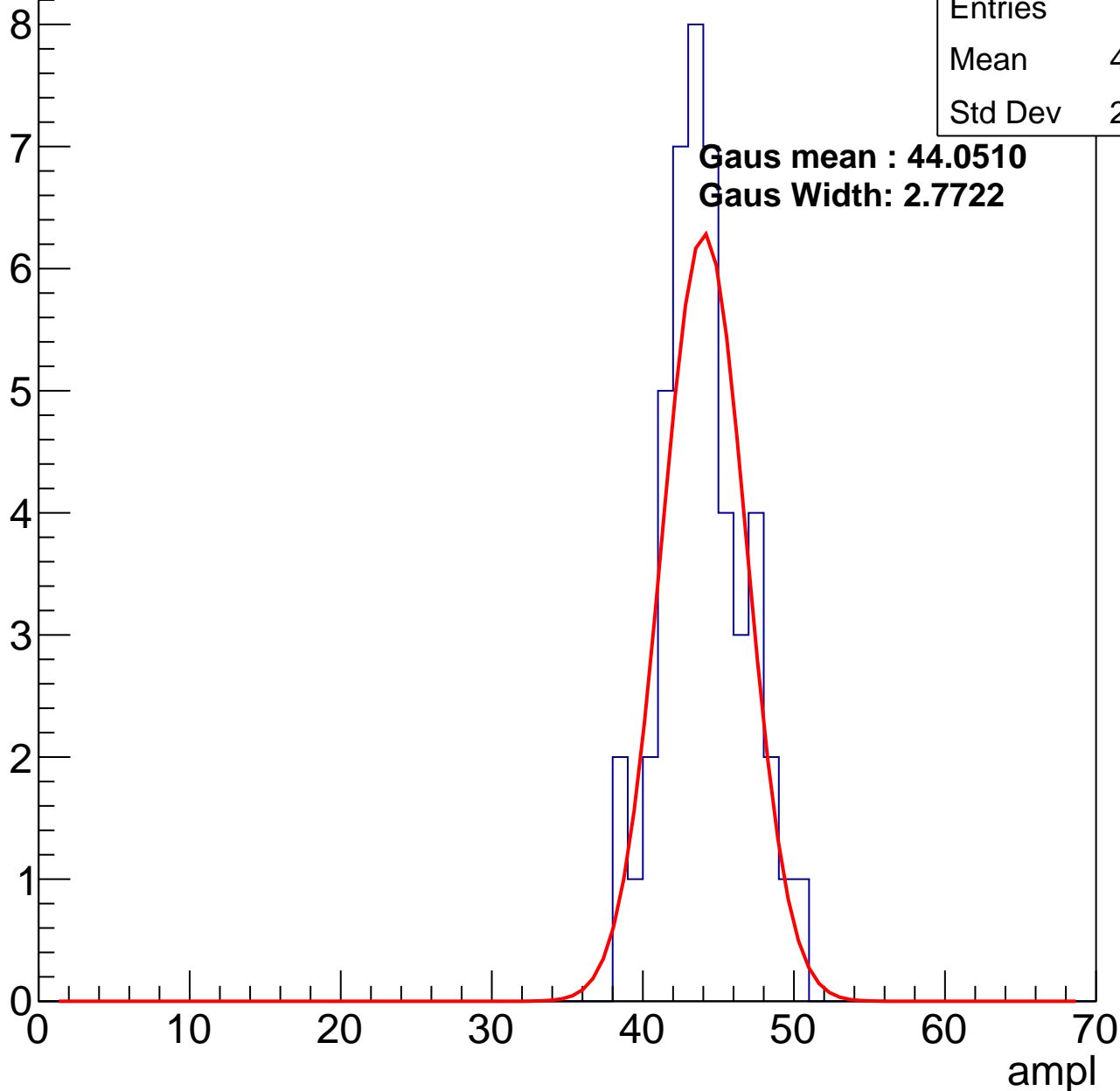
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	43.55
Std Dev	2.712

Gaus mean : 44.0510

Gaus Width: 2.7722

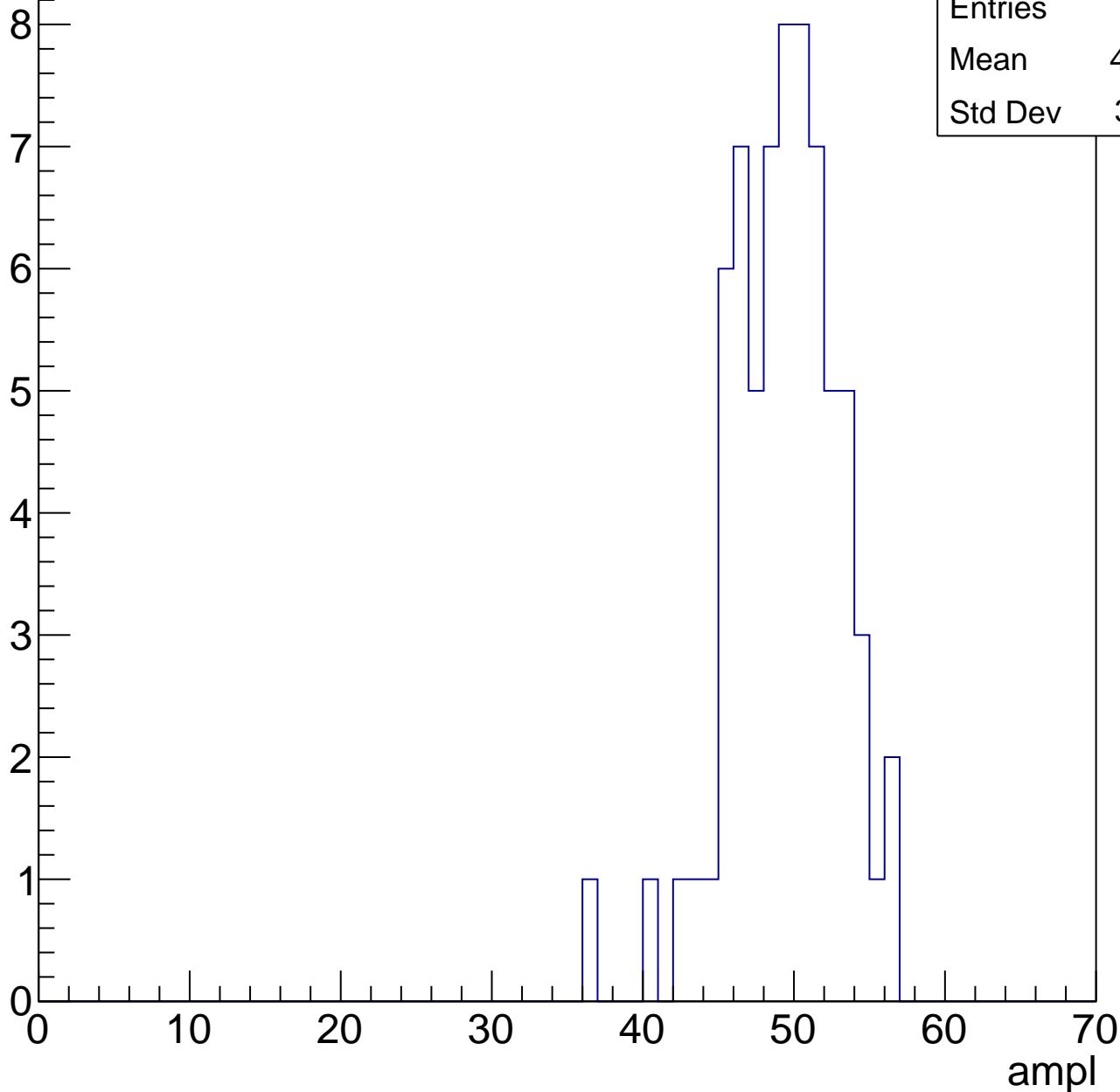


B0L000S, U4-ch121, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	48.86
Std Dev	3.641

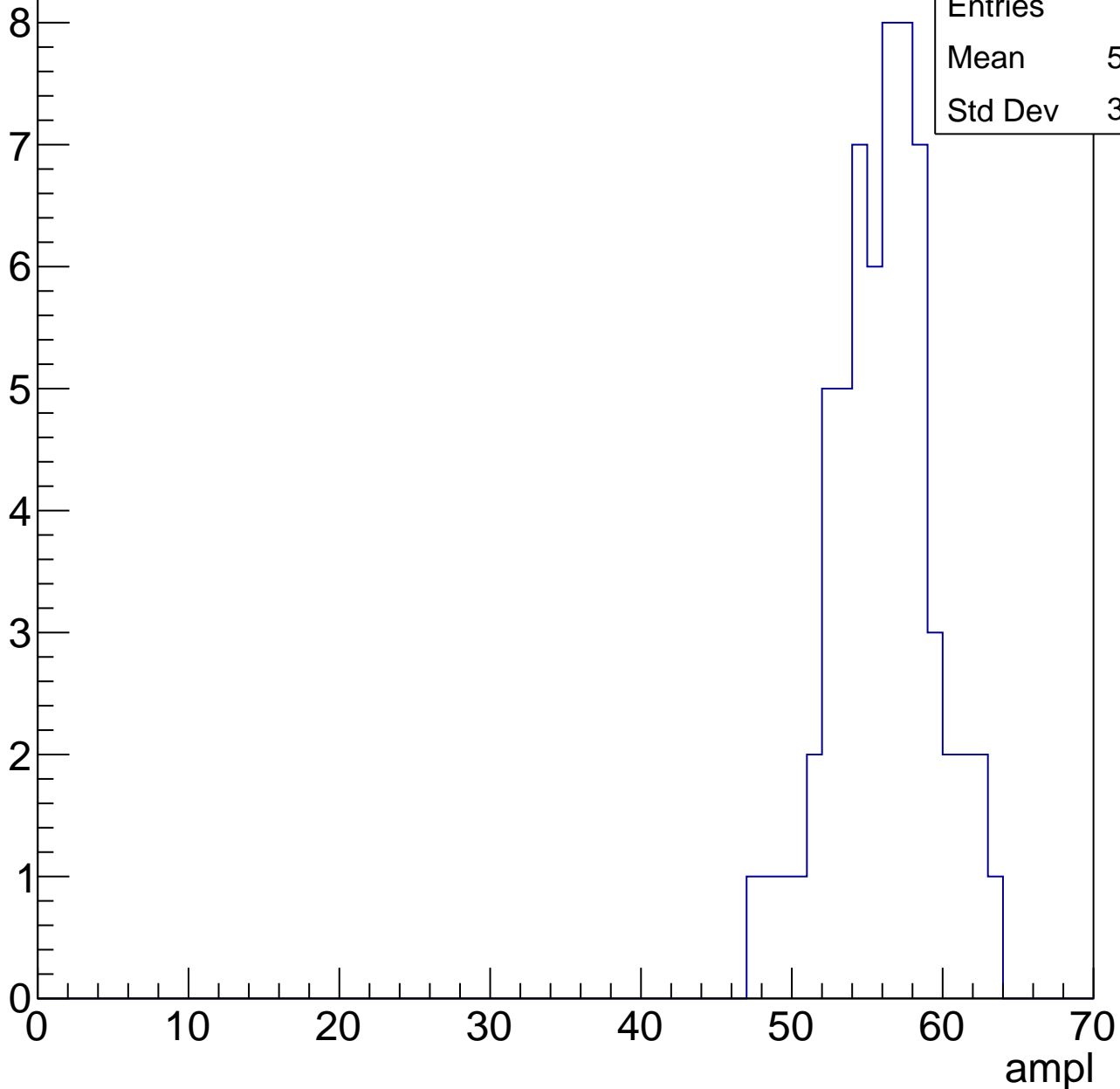


B0L000S, U4-ch121, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.56
Std Dev	3.344

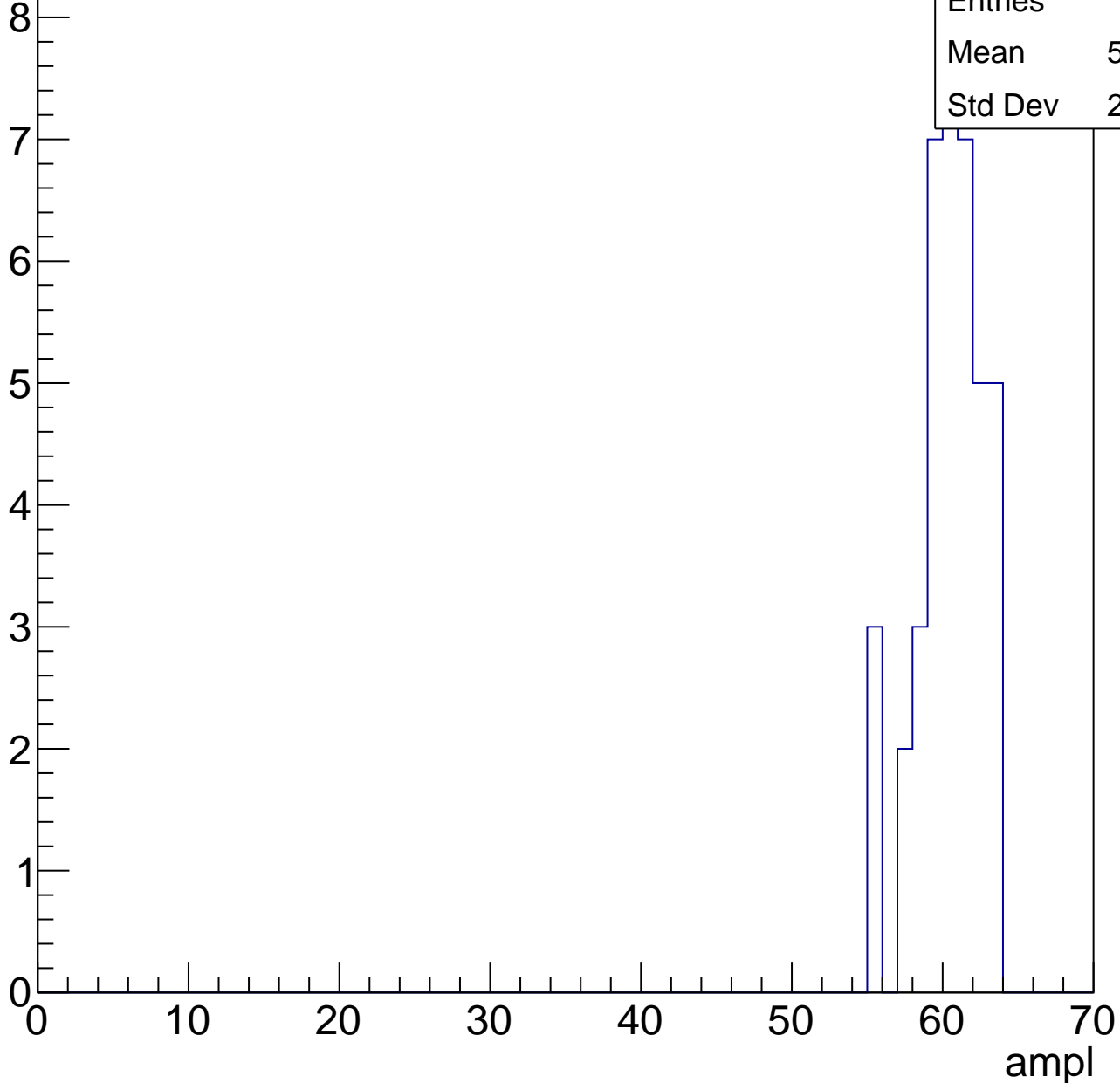


B0L000S, U4-ch121, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

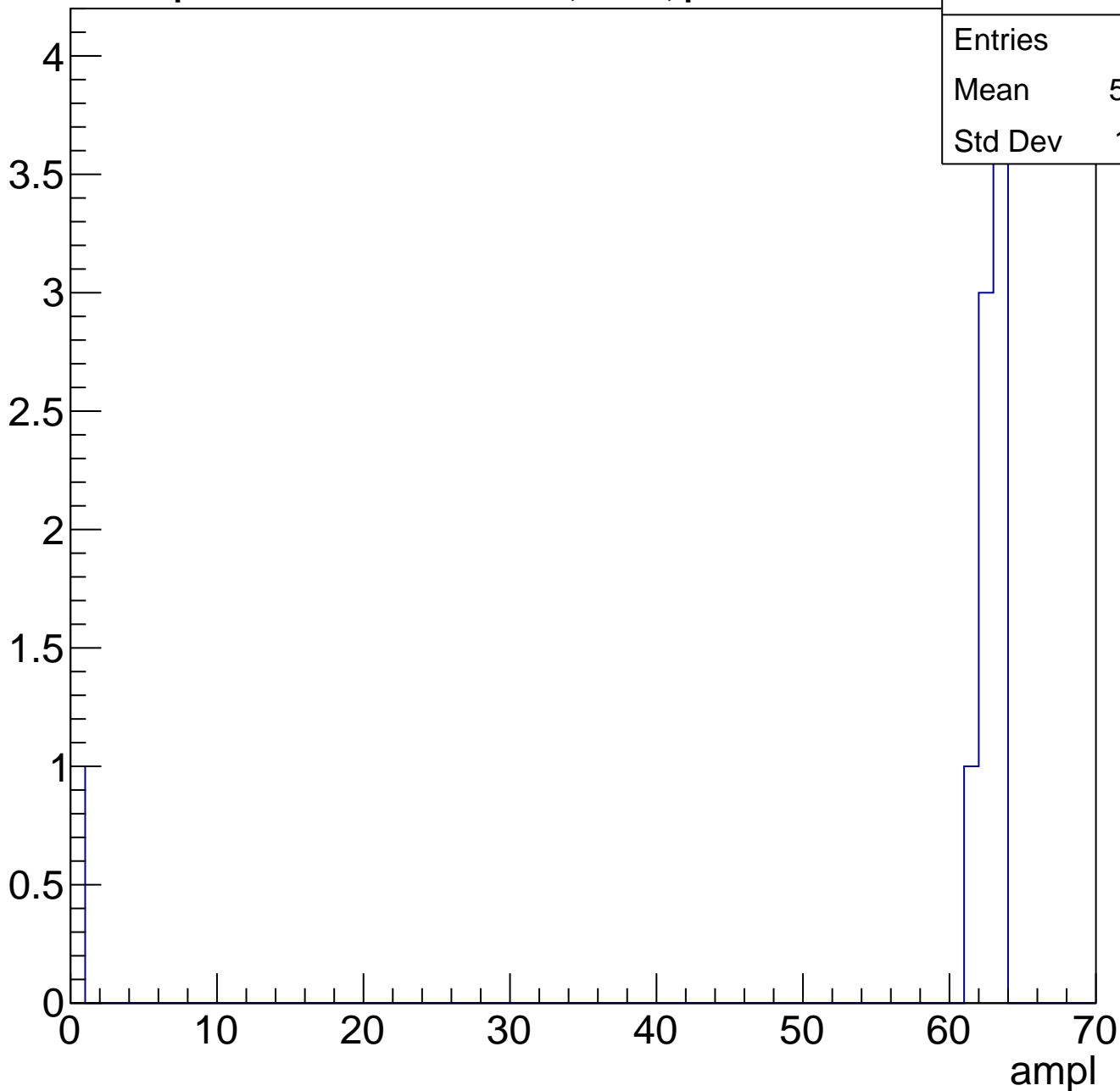
Entries	40
Mean	59.95
Std Dev	2.144



B0L000S, U4-ch121, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch121, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch122, adc0

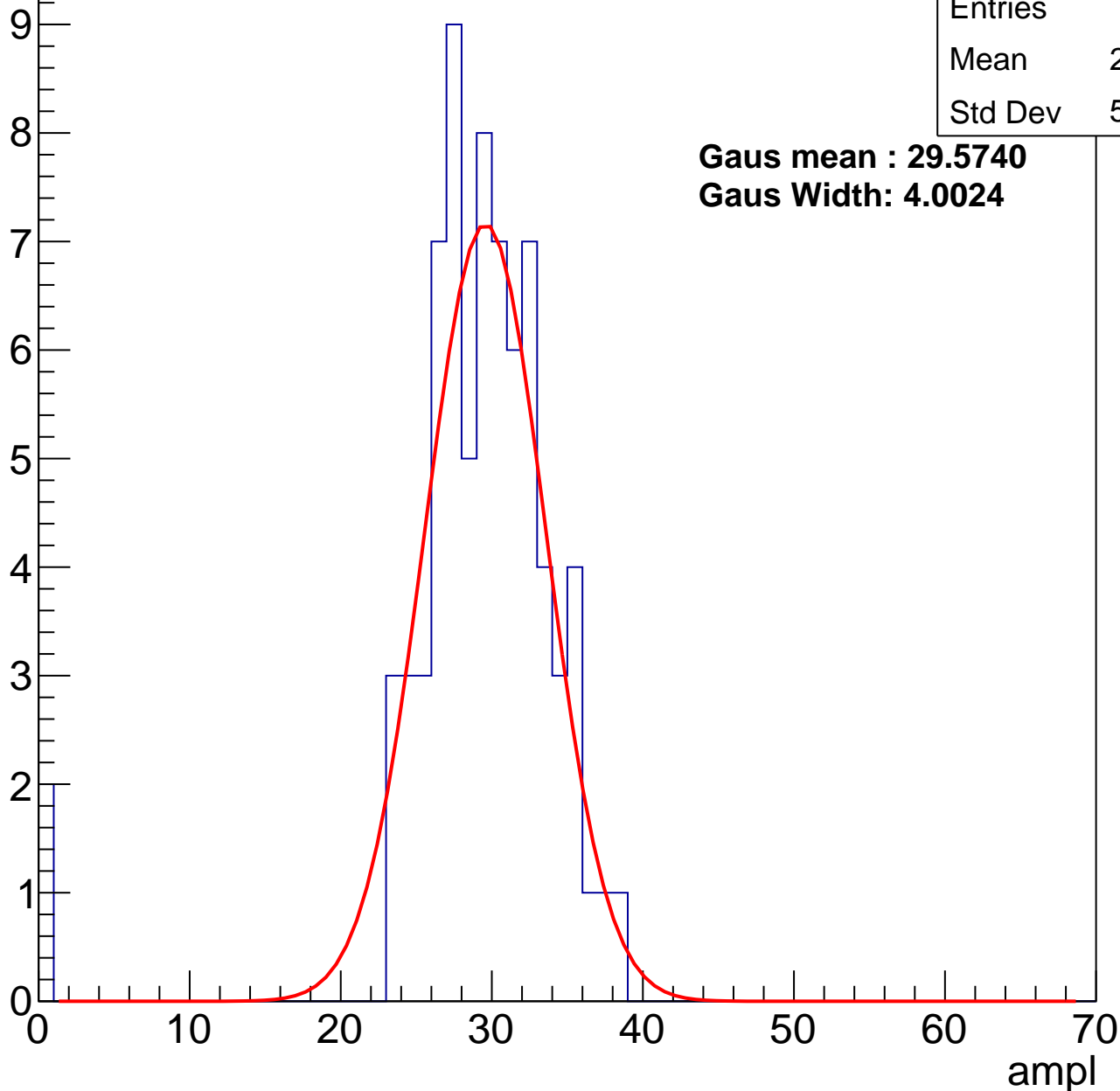
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	28.62
Std Dev	5.902

Gaus mean : 29.5740

Gaus Width: 4.0024



B0L000S, U4-ch122, adc1

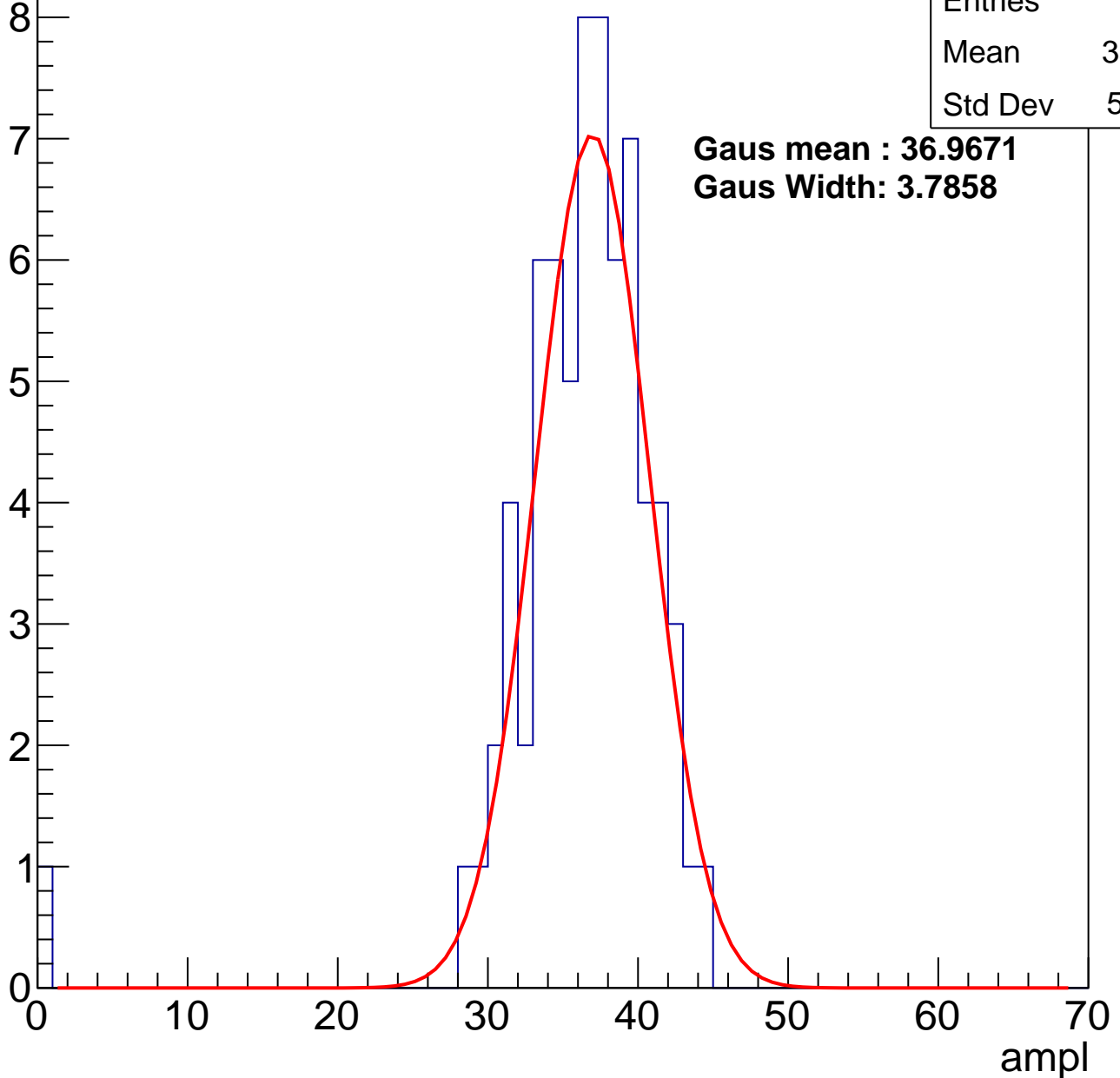
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.77
Std Dev	5.571

Gaus mean : 36.9671

Gaus Width: 3.7858



B0L000S, U4-ch122, adc2

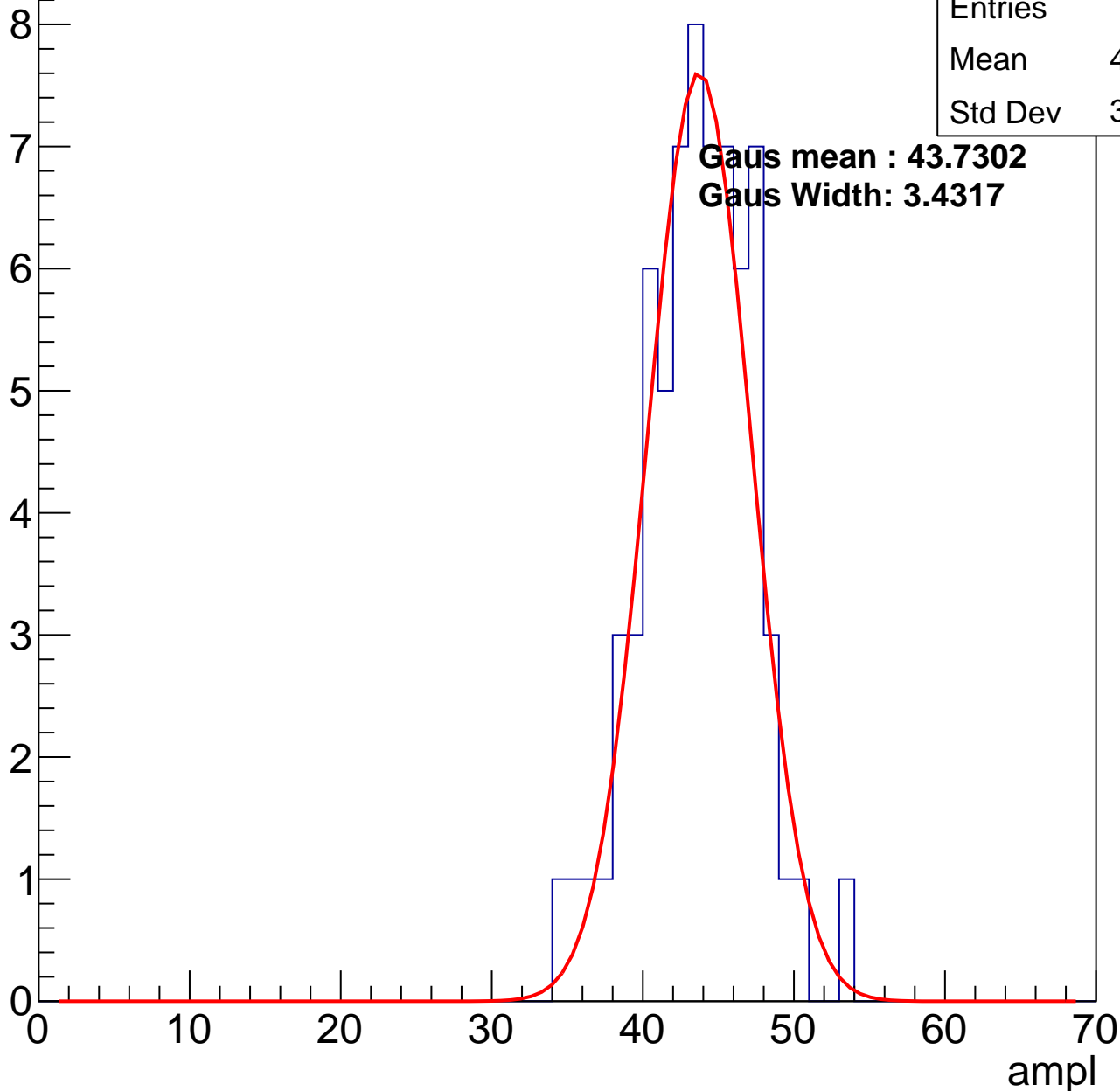
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.19
Std Dev	3.597

Gaus mean : 43.7302

Gaus Width: 3.4317

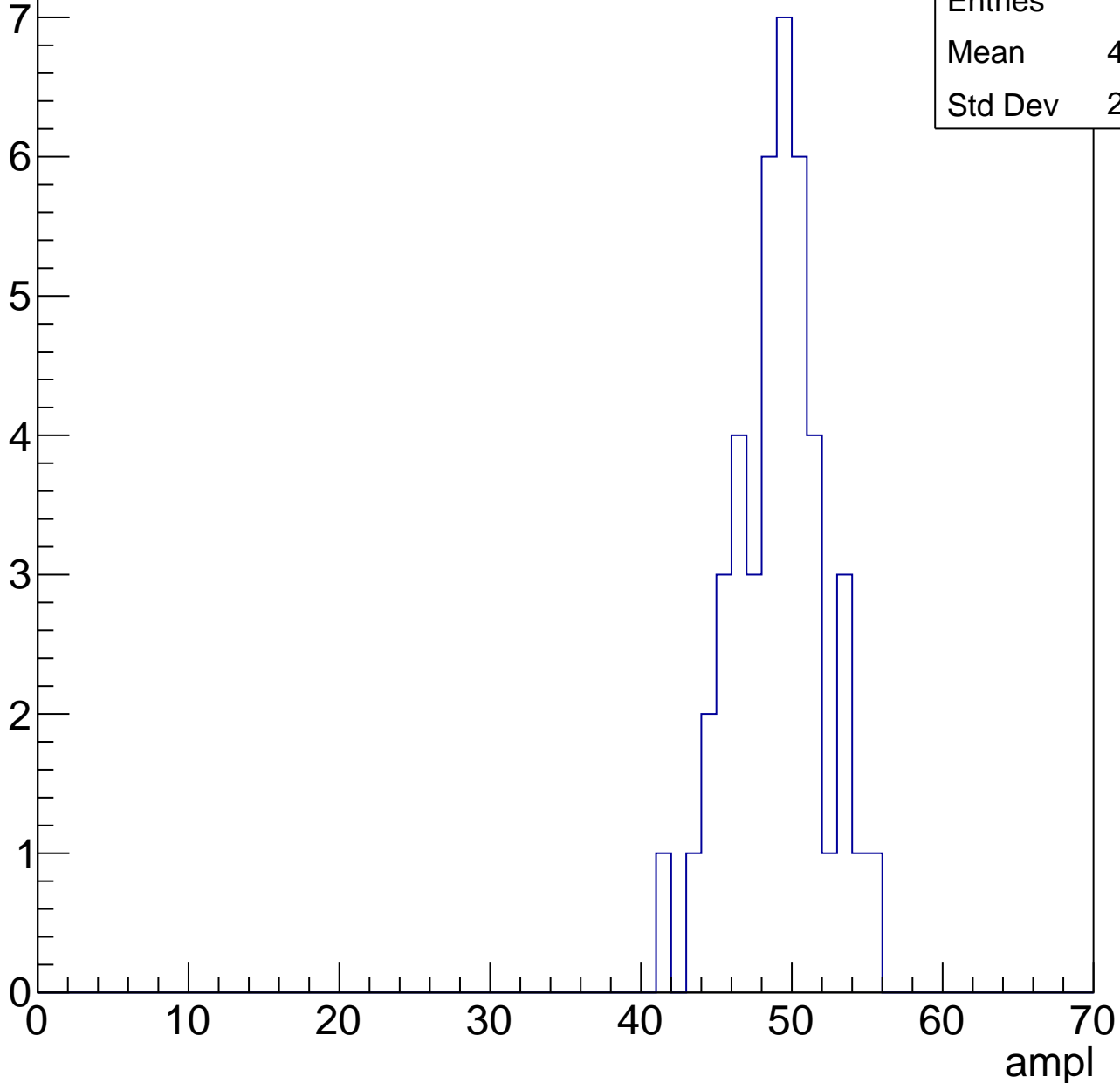


B0L000S, U4-ch122, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

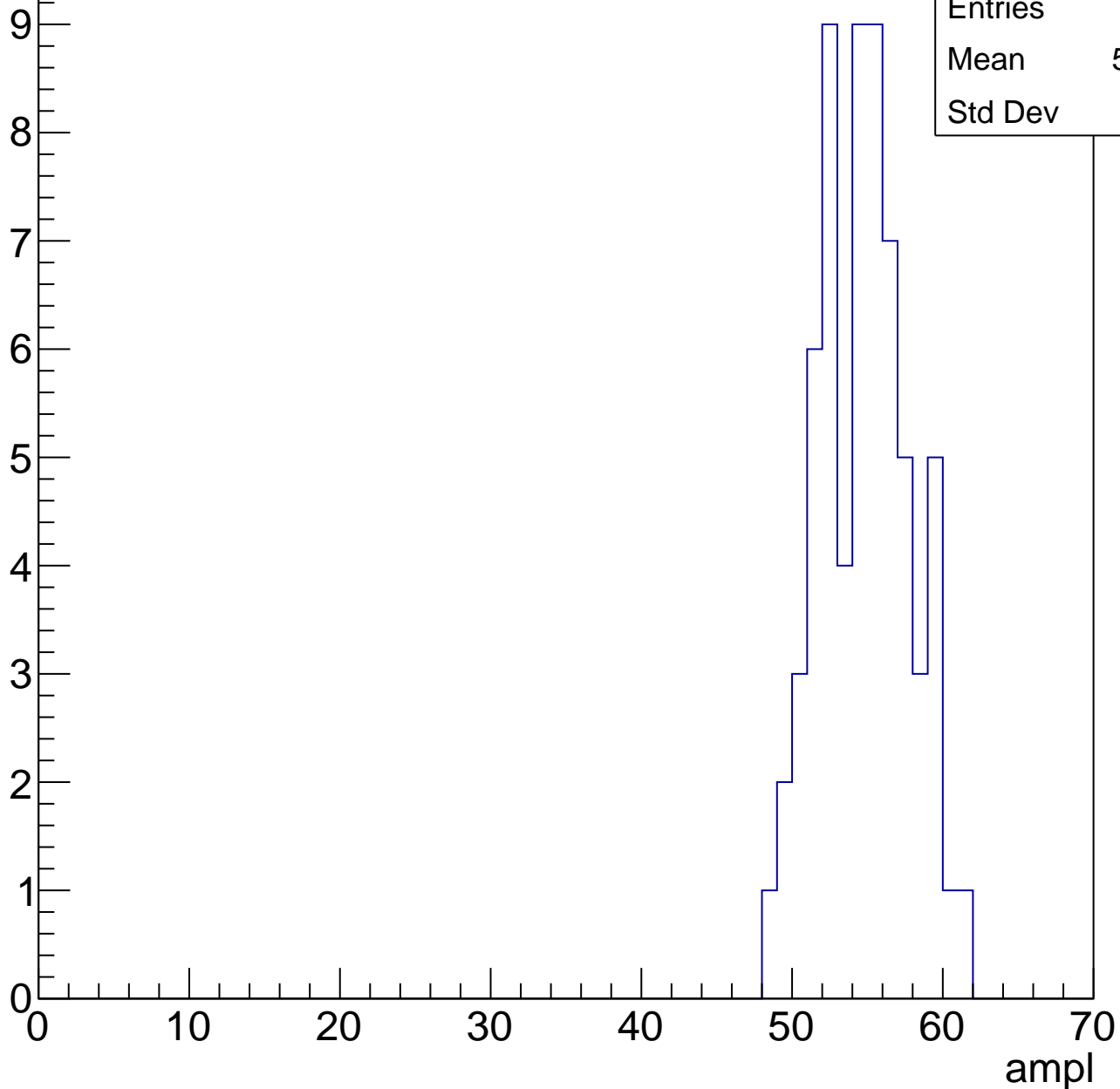
Entries	43
Mean	48.53
Std Dev	2.983



B0L000S, U4-ch122, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	65
Mean	54.31
Std Dev	2.94

B0L000S, U4-ch122, adc5

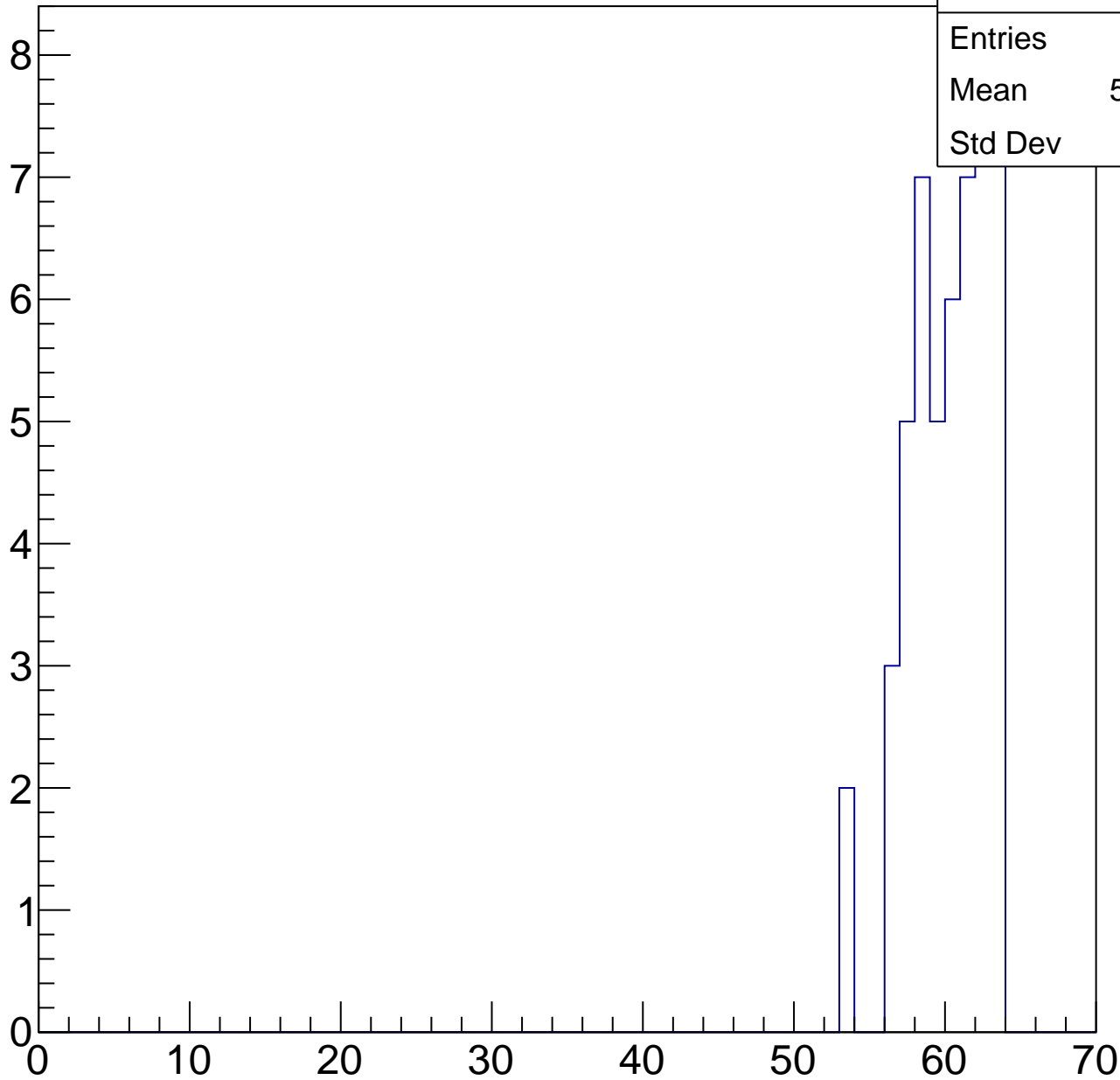
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.75
Std Dev	2.55

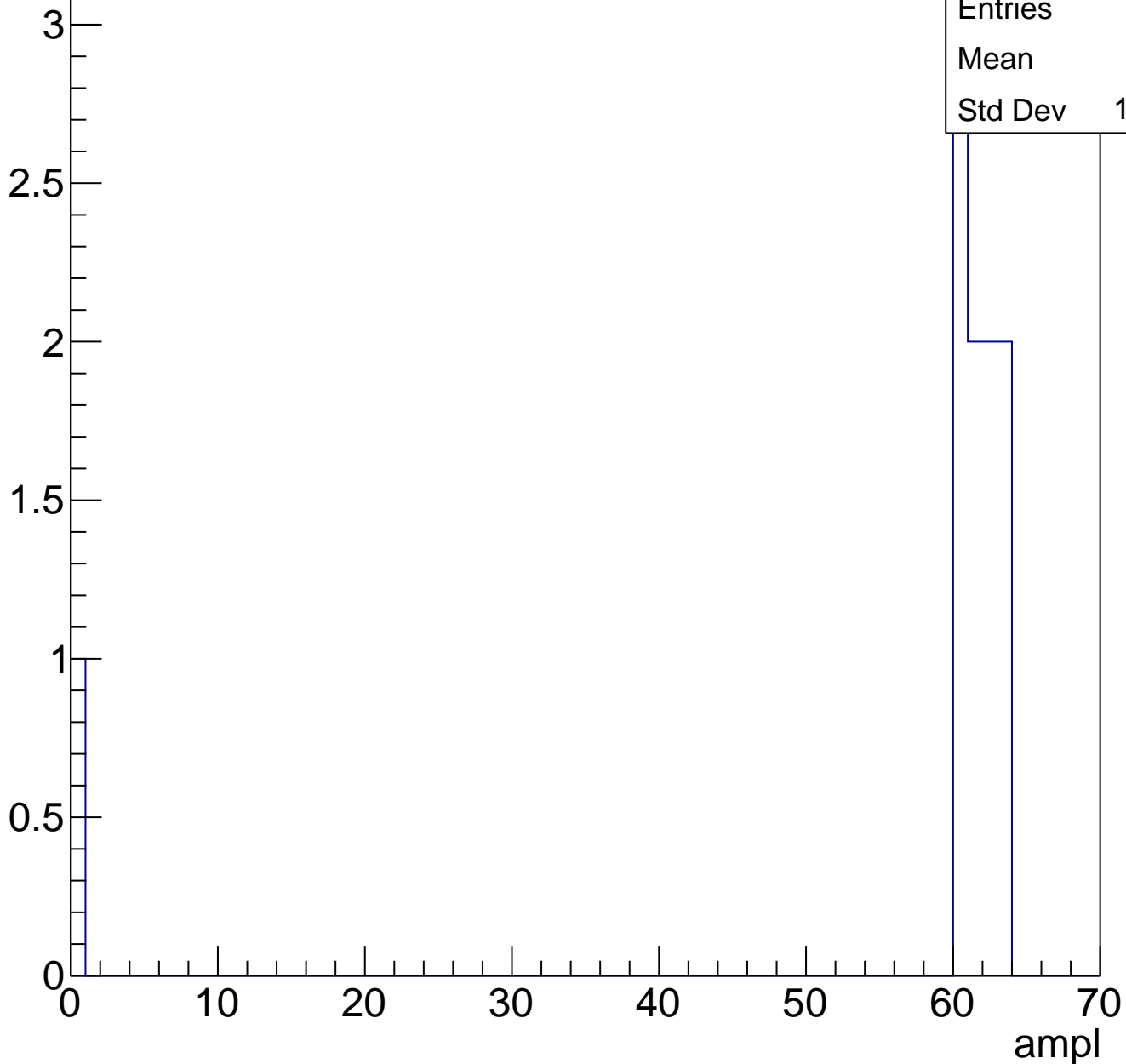
ampl



B0L000S, U4-ch122, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch122, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch123, adc0

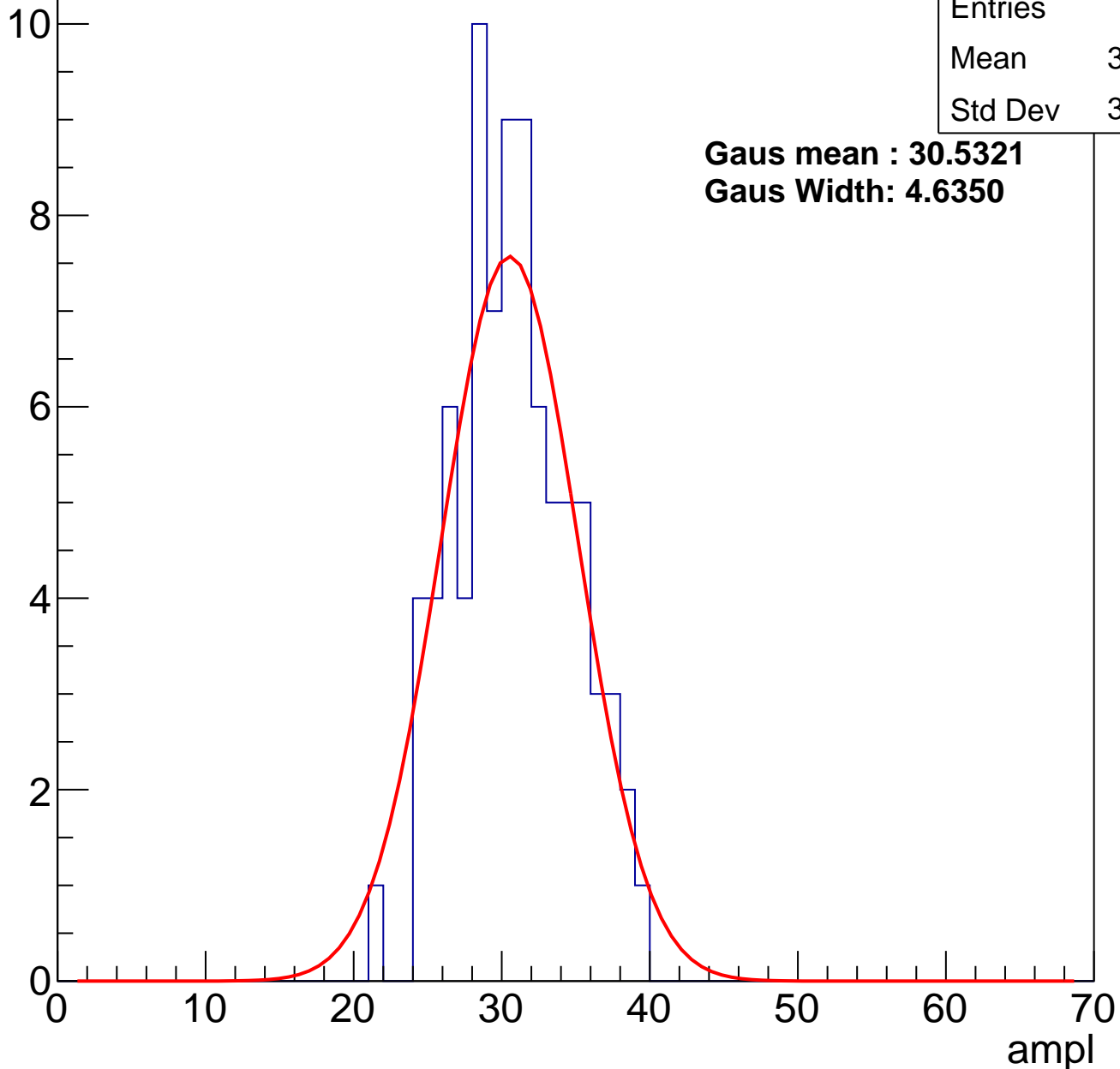
calib_packv5_042523_0143.root, FC#5, port B1

Entries	84
Mean	30.35
Std Dev	3.844

Gaus mean : 30.5321

Gaus Width: 4.6350

Entry



B0L000S, U4-ch123, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	38.2
Std Dev	3.881

Gaus mean : 38.7550

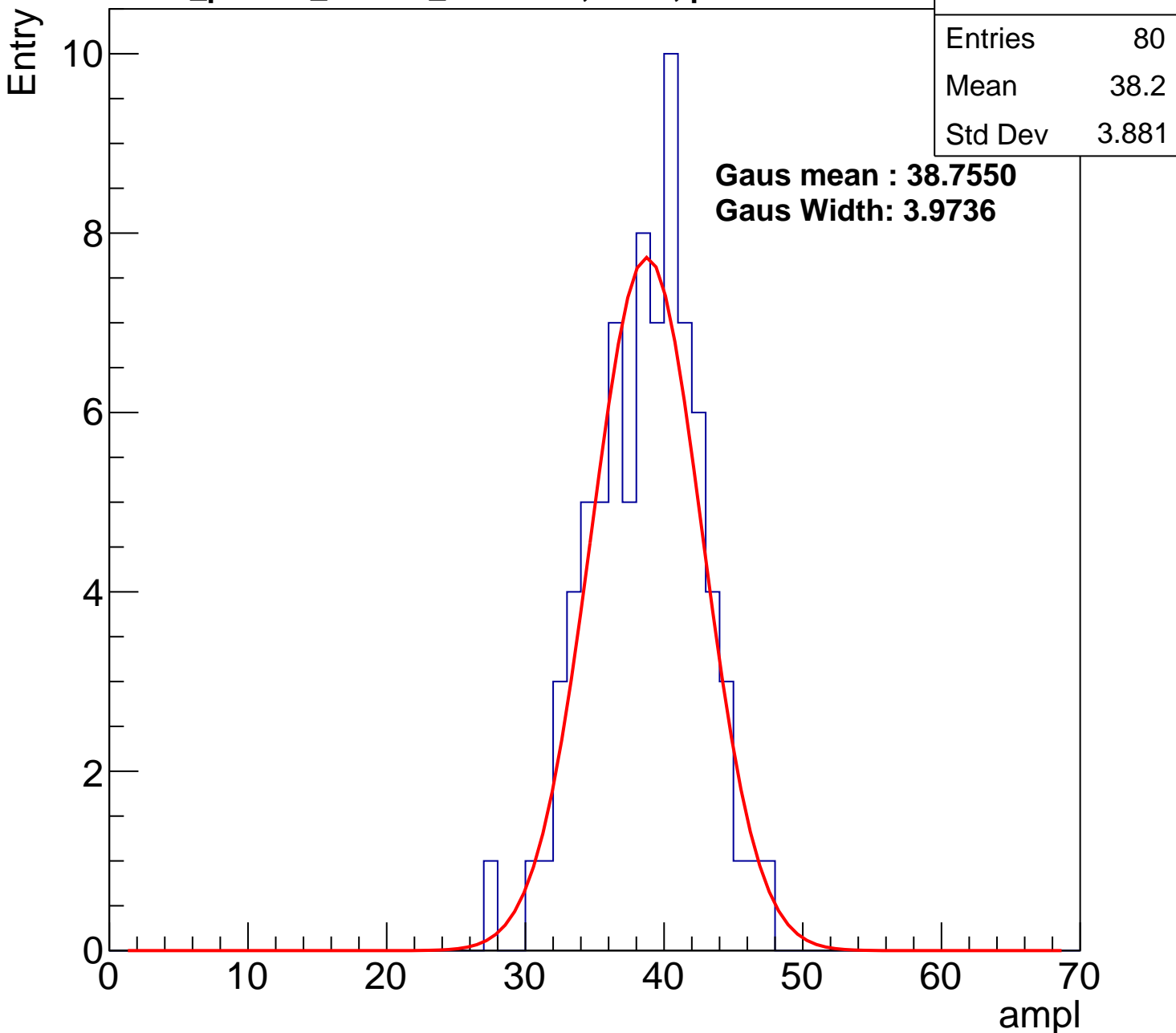
Gaus Width: 3.9736

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch123, adc2

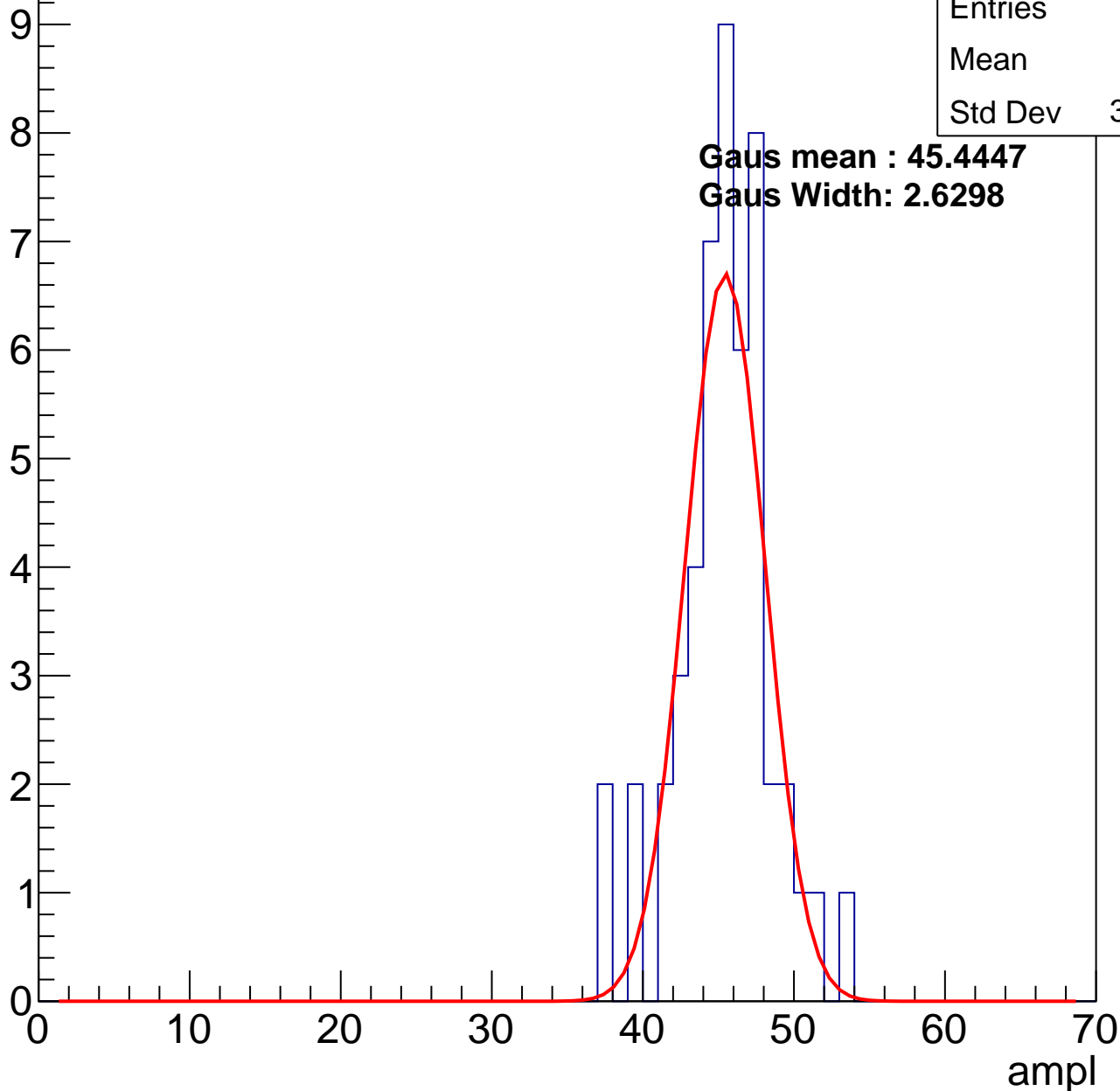
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	44.9
Std Dev	3.145

Gaus mean : 45.4447

Gaus Width: 2.6298

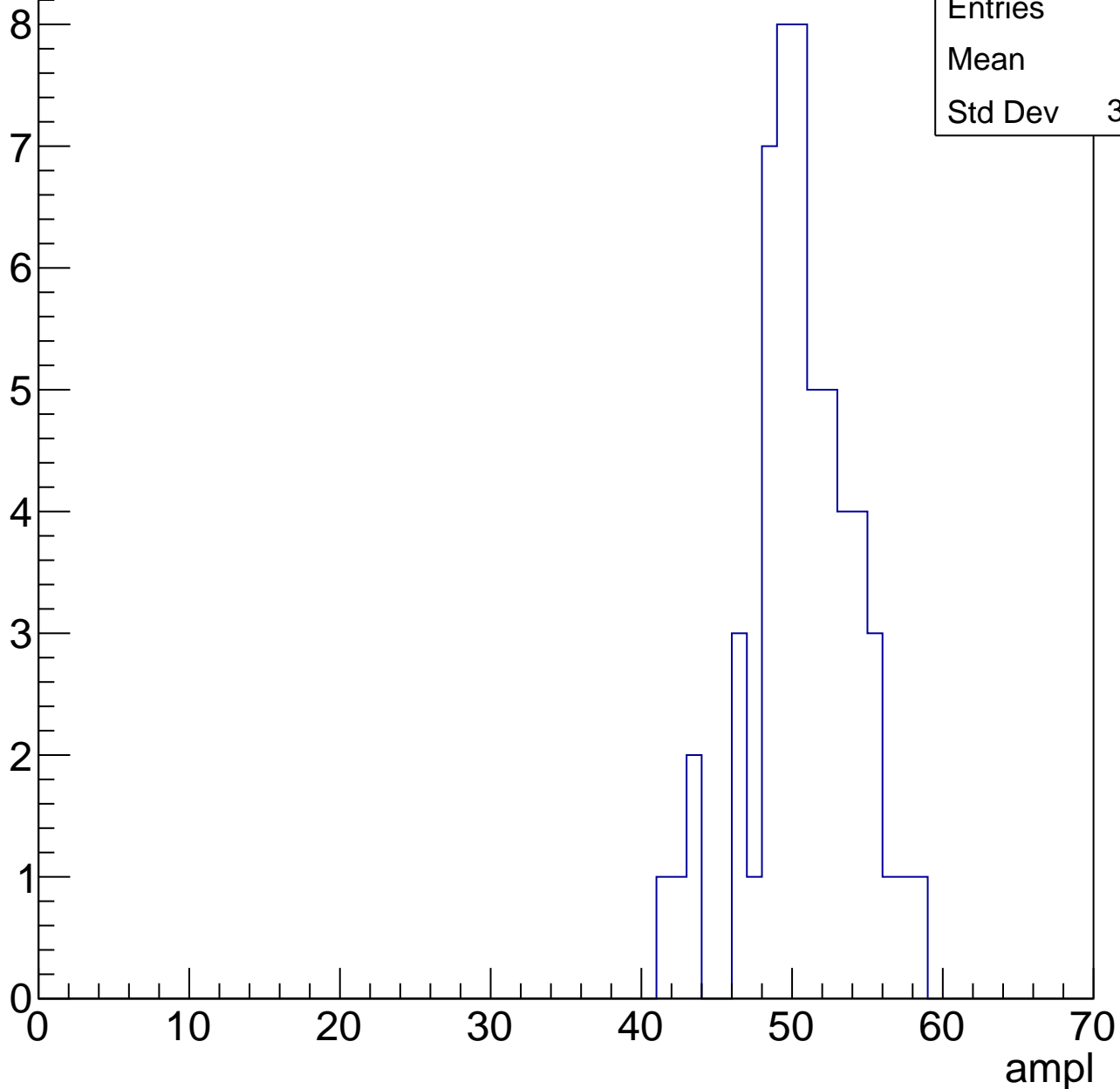


B0L000S, U4-ch123, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

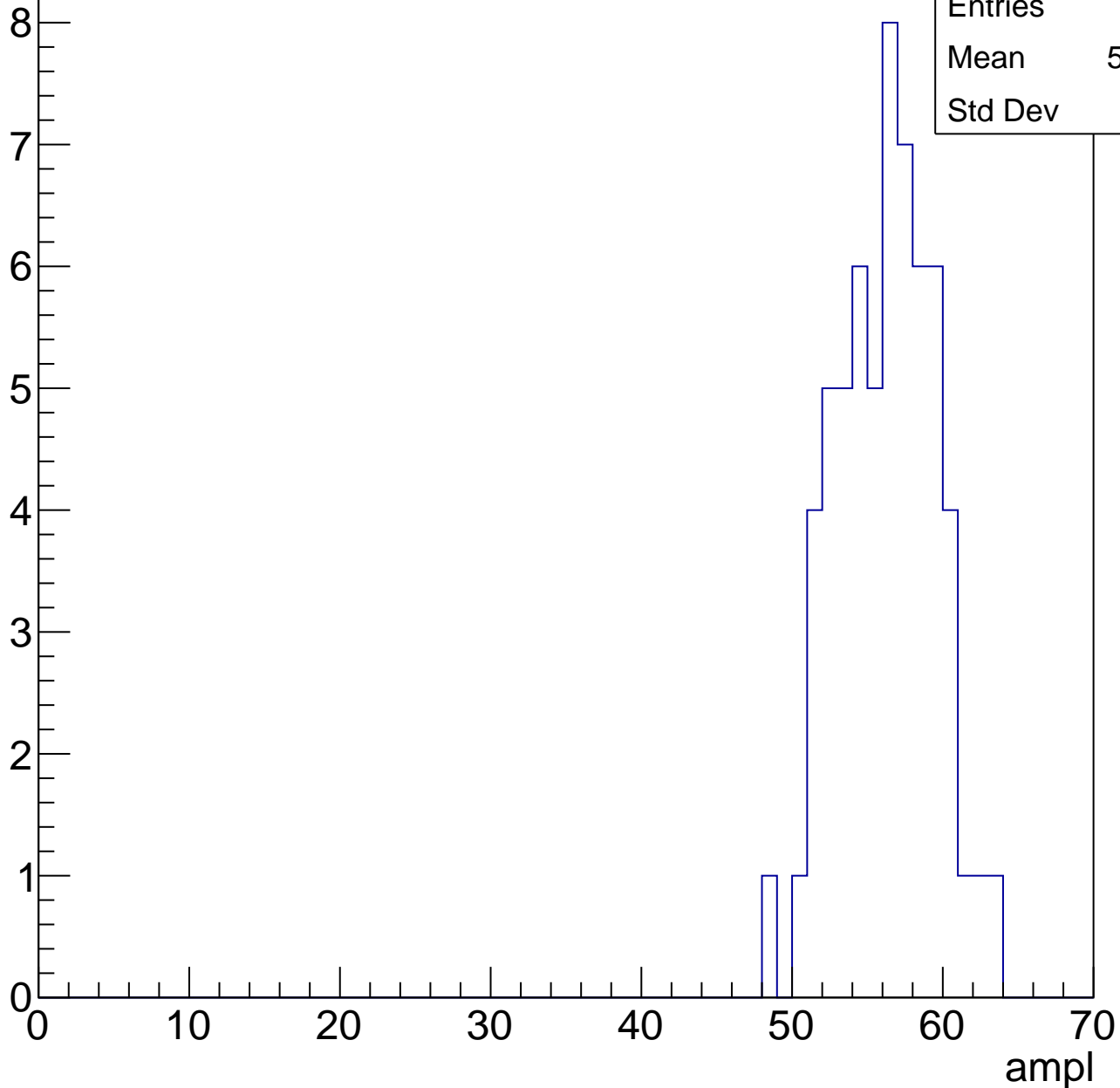
Entries	55
Mean	50.2
Std Dev	3.523



B0L000S, U4-ch123, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

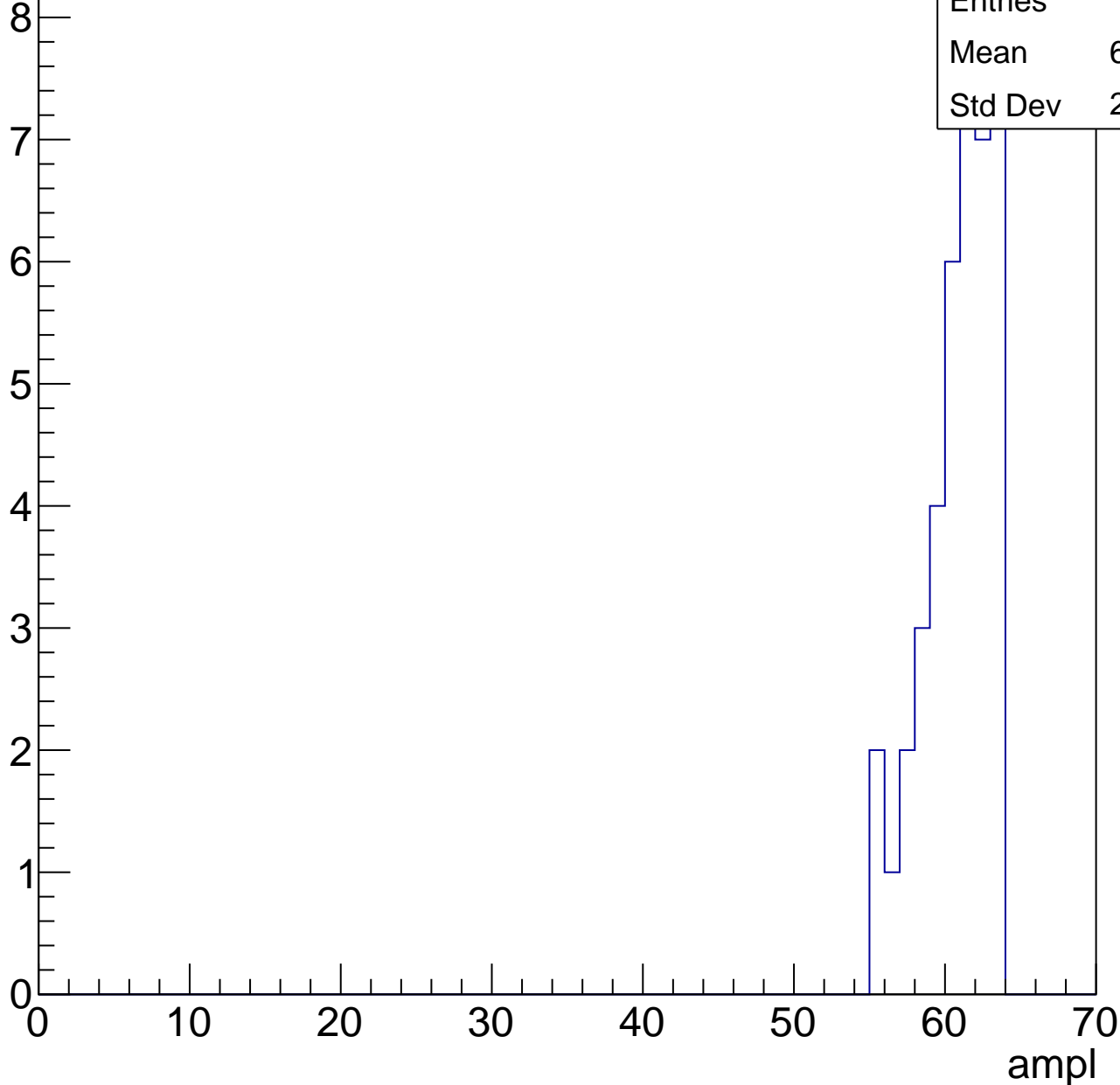


B0L000S, U4-ch123, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

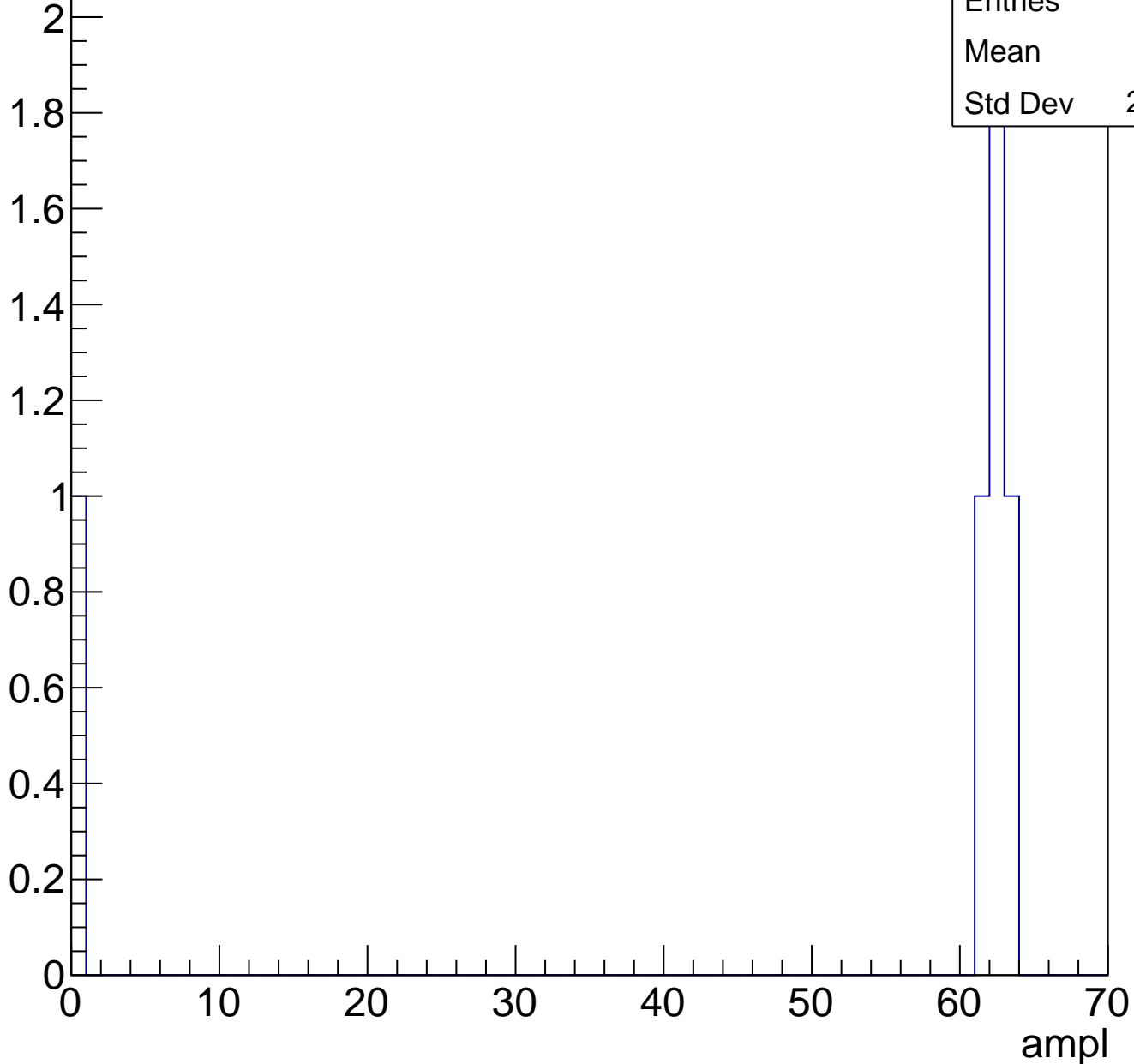
Entries	41
Mean	60.39
Std Dev	2.218



B0L000S, U4-ch123, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch123, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U4-ch124, adc0

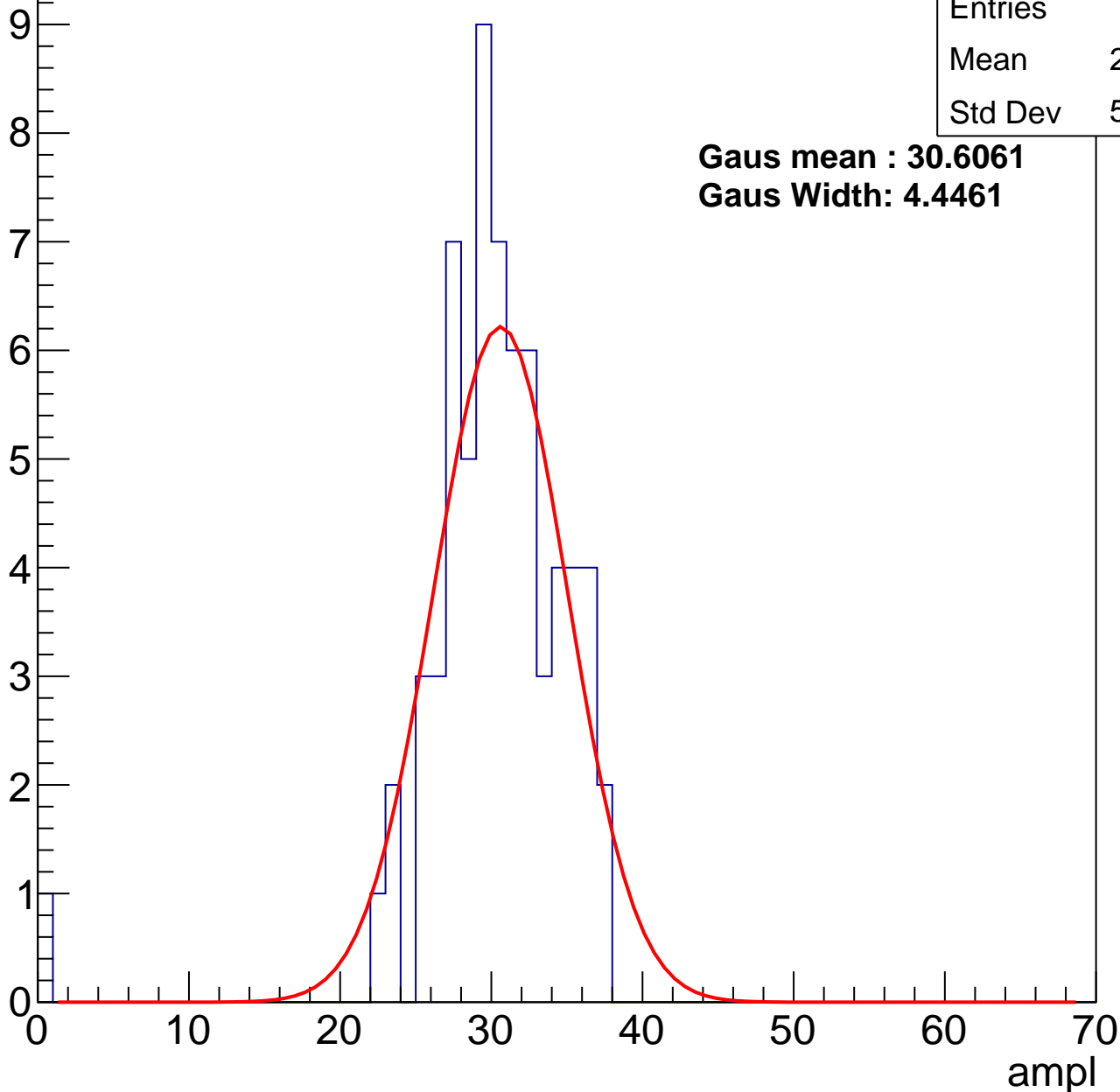
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.73
Std Dev	5.089

Gaus mean : 30.6061

Gaus Width: 4.4461



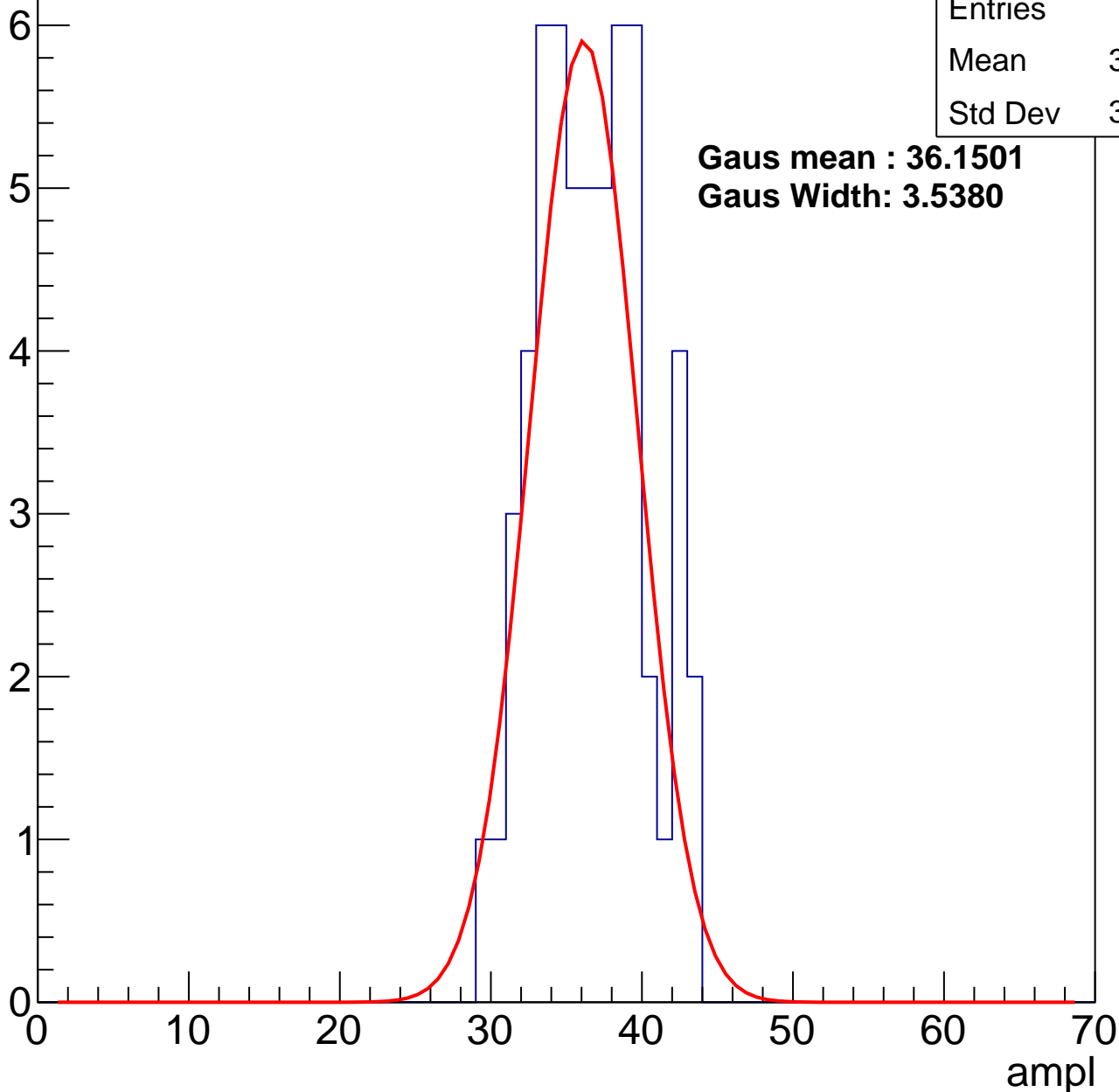
B0L000S, U4-ch124, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	36.12
Std Dev	3.475

Gaus mean : 36.1501
Gaus Width: 3.5380



B0L000S, U4-ch124, adc2

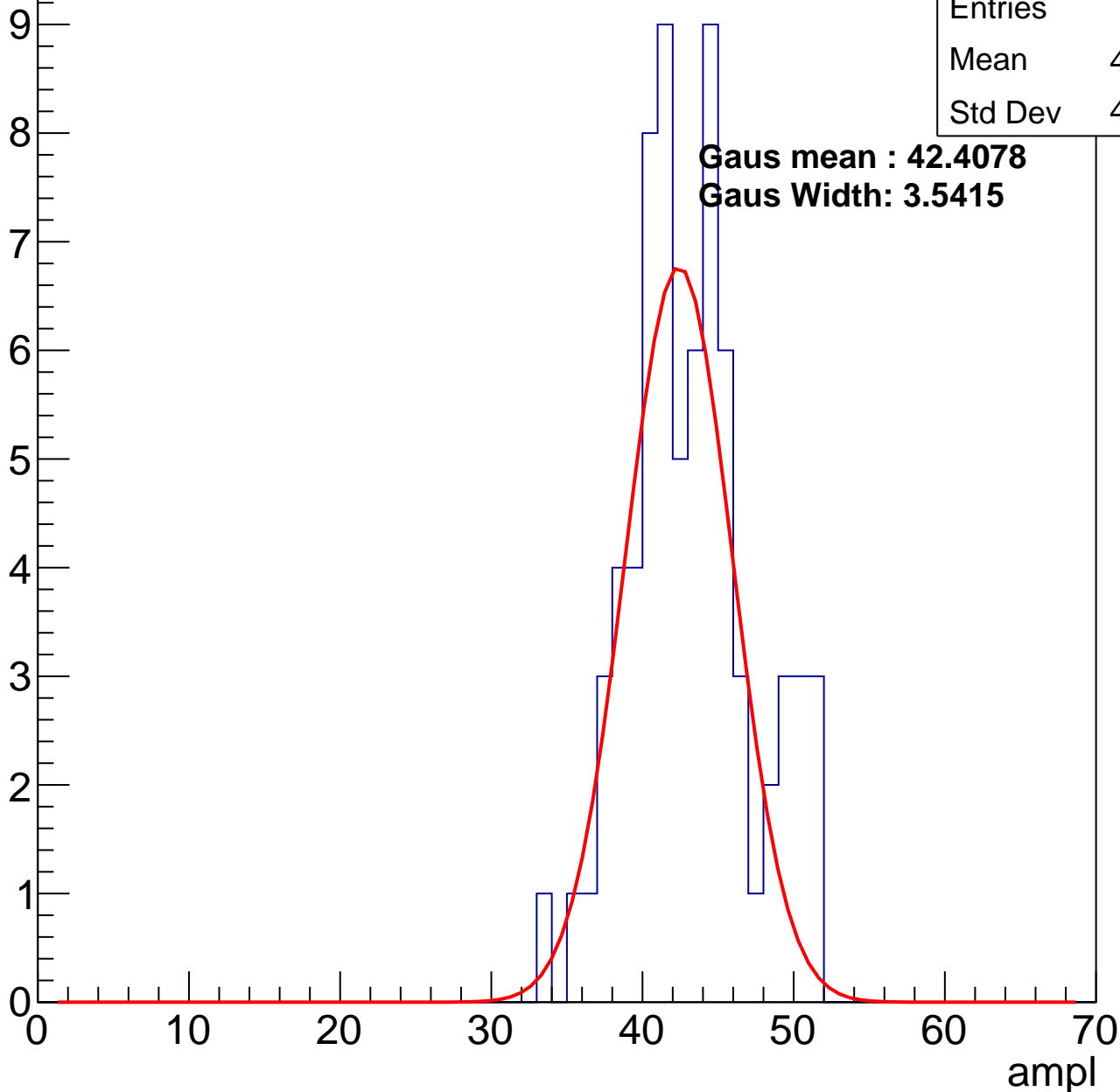
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	42.74
Std Dev	4.024

Gaus mean : 42.4078

Gaus Width: 3.5415

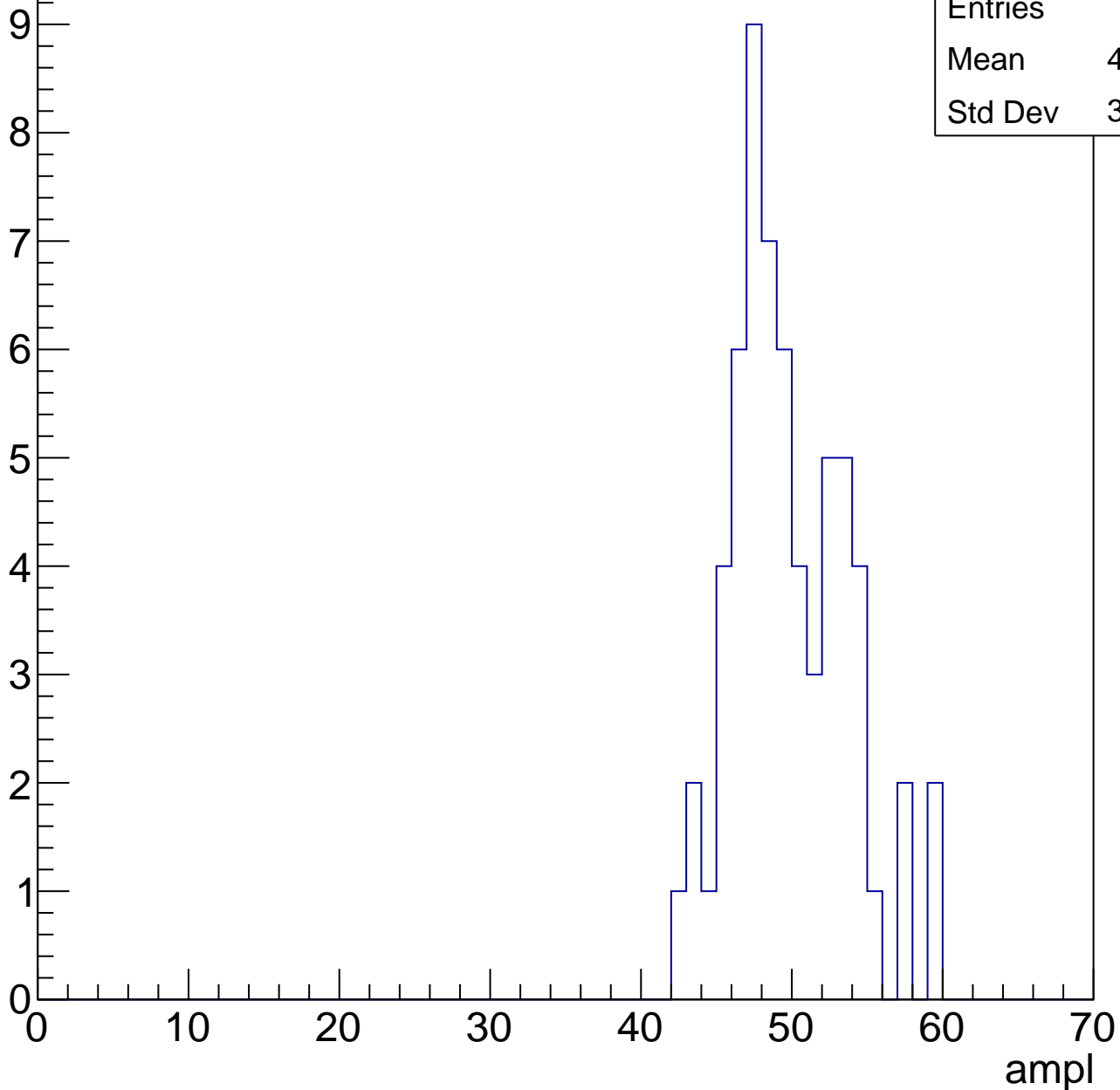


B0L000S, U4-ch124, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	49.39
Std Dev	3.816

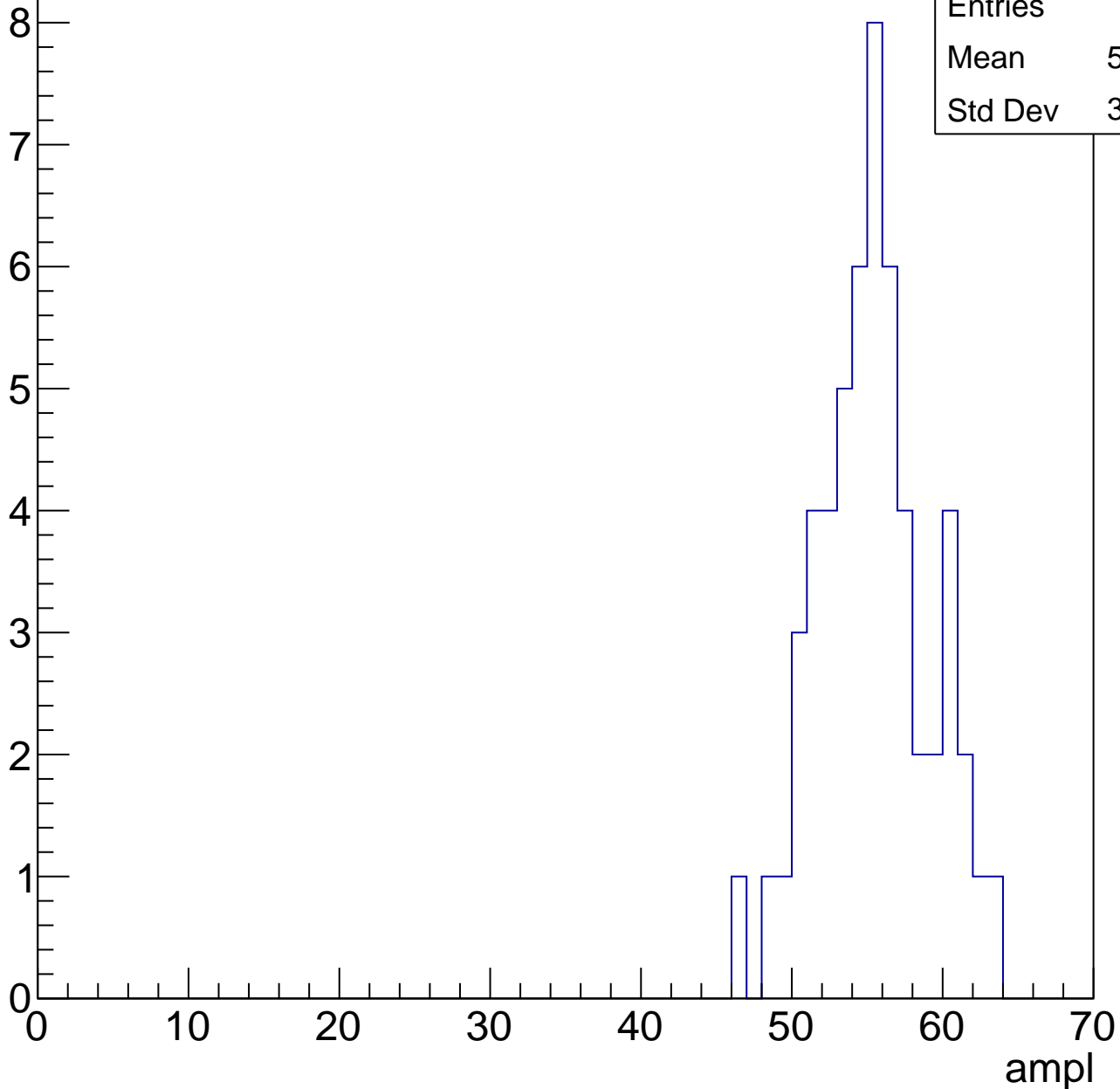


B0L000S, U4-ch124, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	54.89
Std Dev	3.627

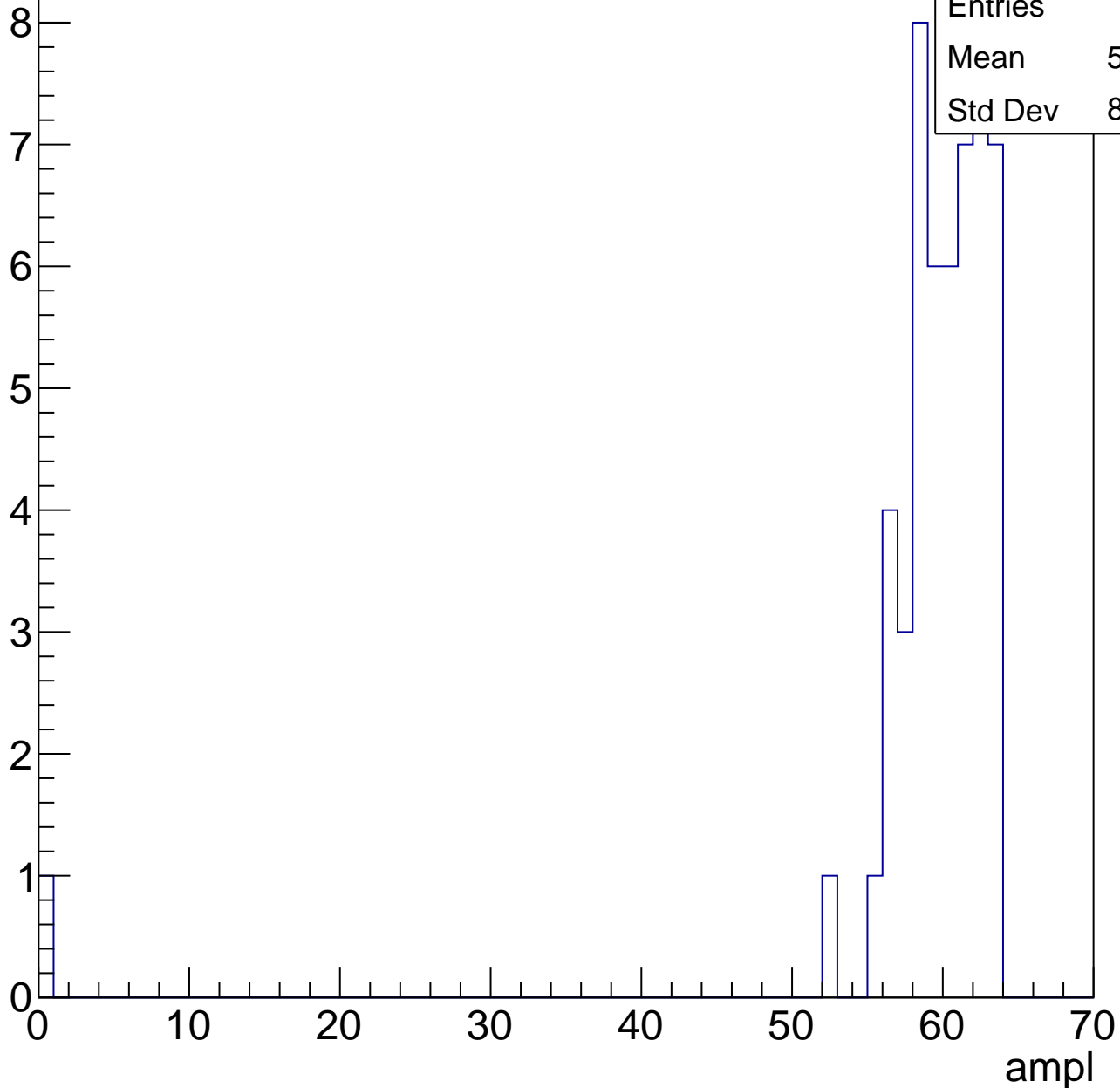


B0L000S, U4-ch124, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.54
Std Dev	8.559



B0L000S, U4-ch124, adc6

calib_packv5_042523_0143.root, FC#5, port B1

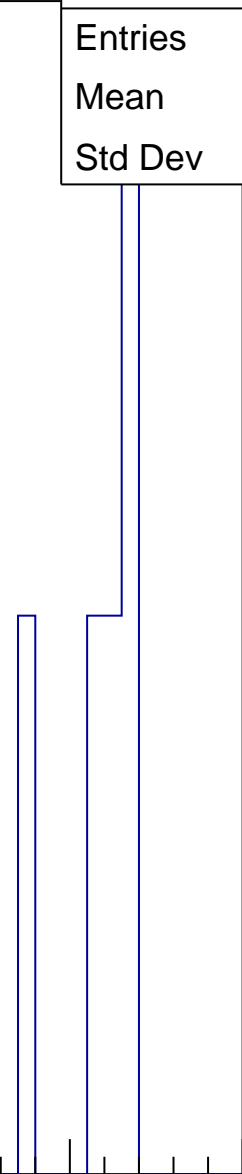
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	2.227

0 10 20 30 40 50 60 70

ampl



B0L000S, U4-ch124, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch125, adc0

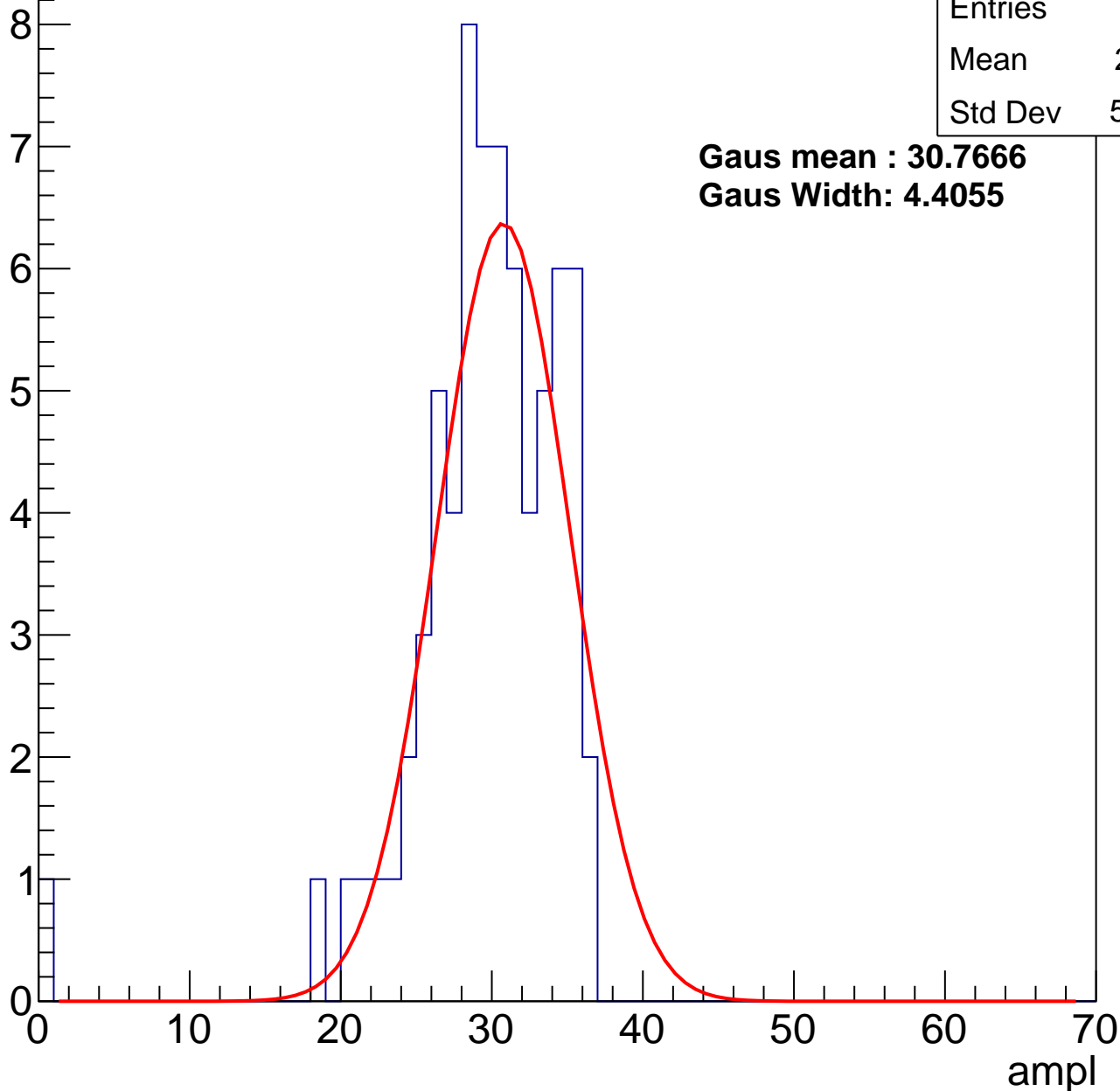
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	29.11
Std Dev	5.266

Gaus mean : 30.7666

Gaus Width: 4.4055



B0L000S, U4-ch125, adc1

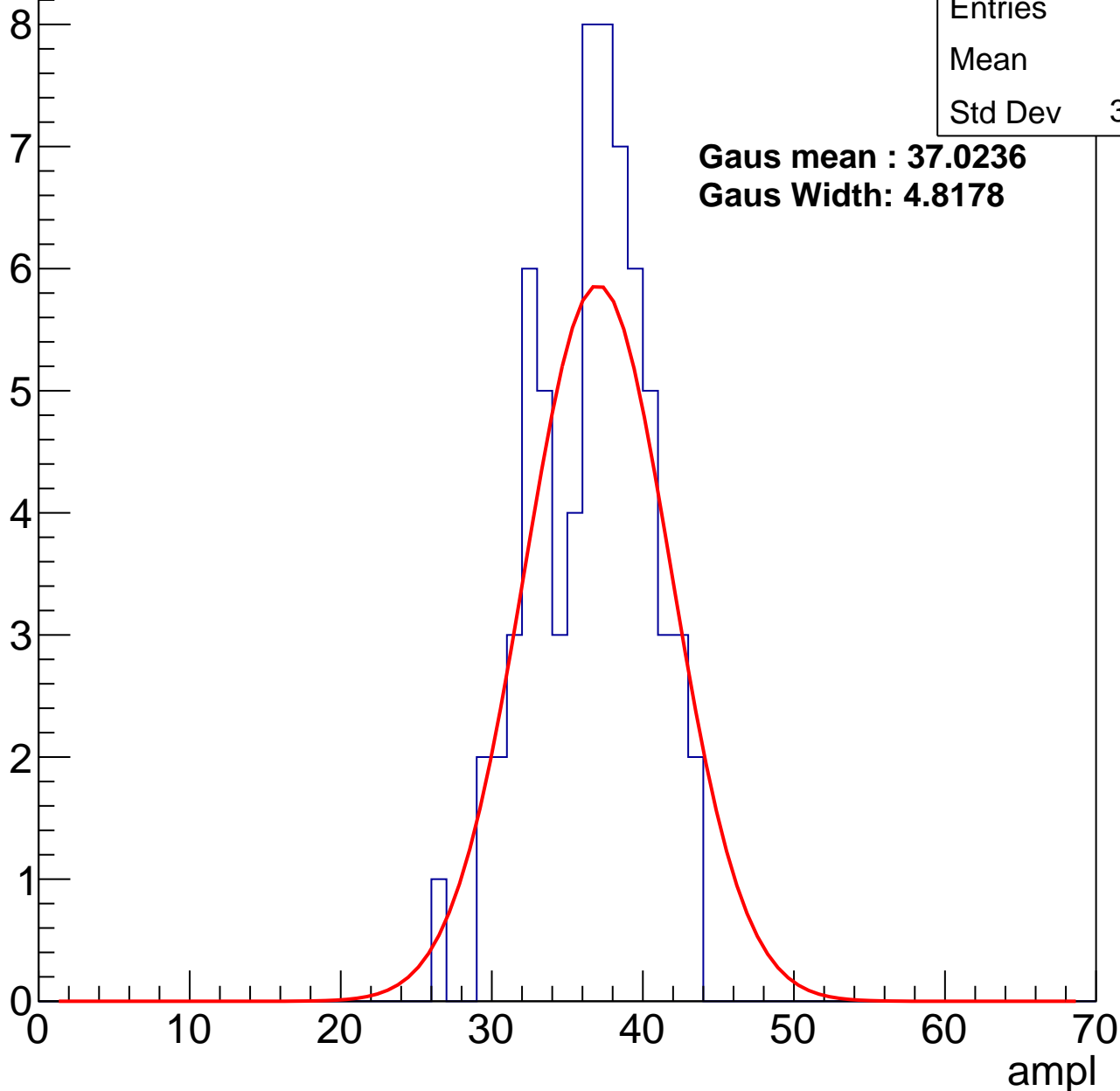
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.1
Std Dev	3.742

Gaus mean : 37.0236

Gaus Width: 4.8178



B0L000S, U4-ch125, adc2

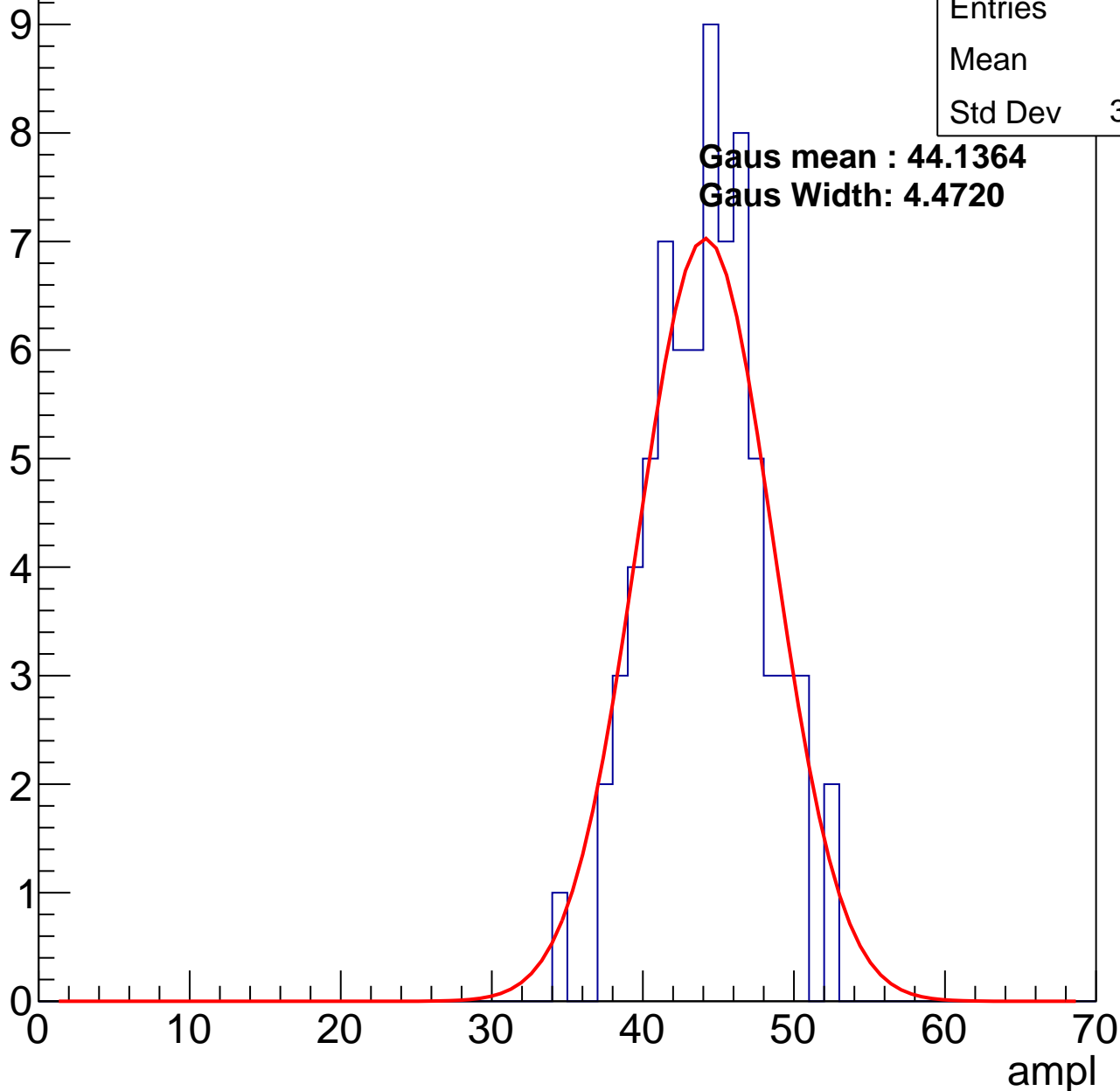
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.7
Std Dev	3.704

Gaus mean : 44.1364

Gaus Width: 4.4720

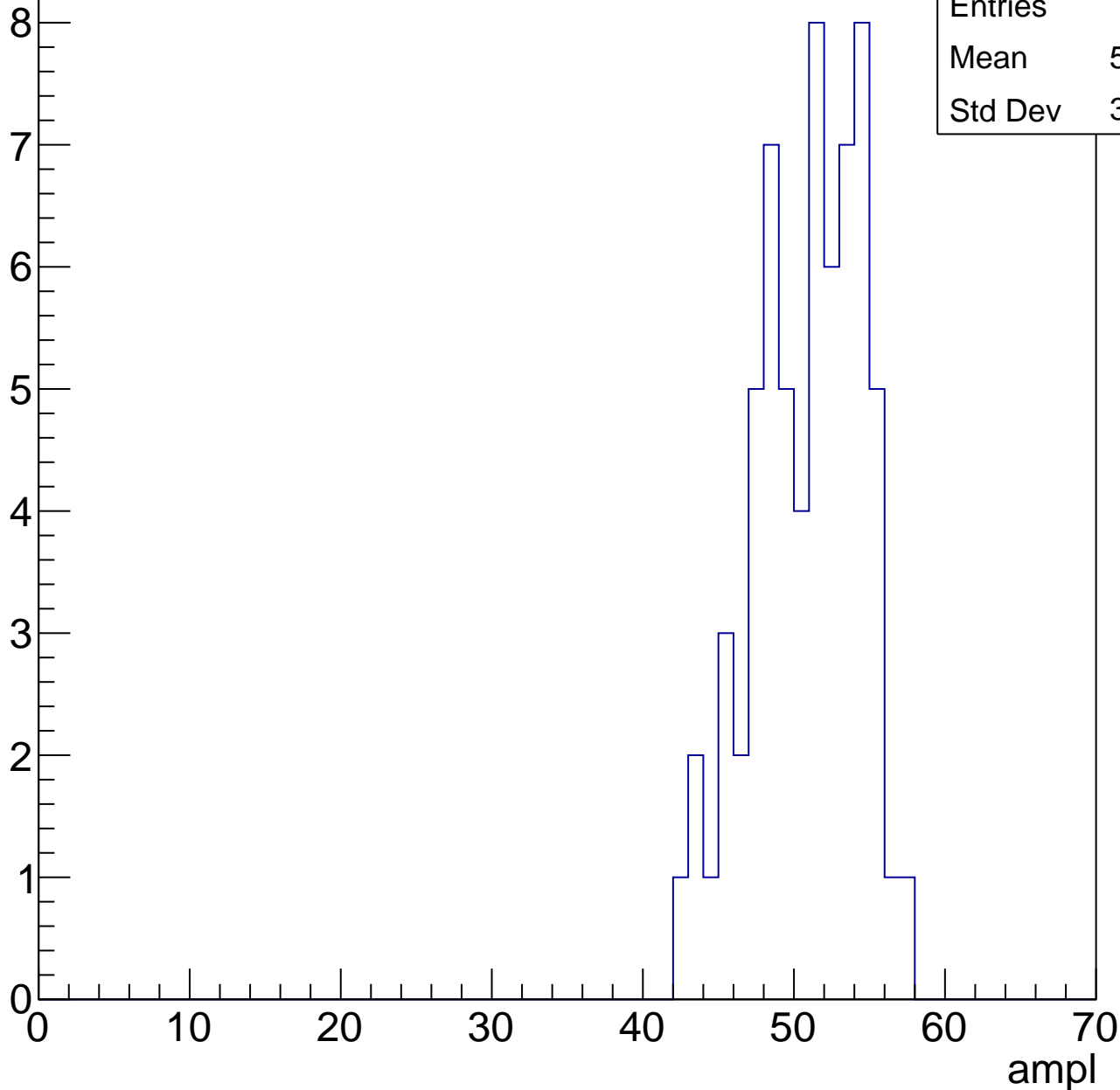


B0L000S, U4-ch125, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.39
Std Dev	3.494

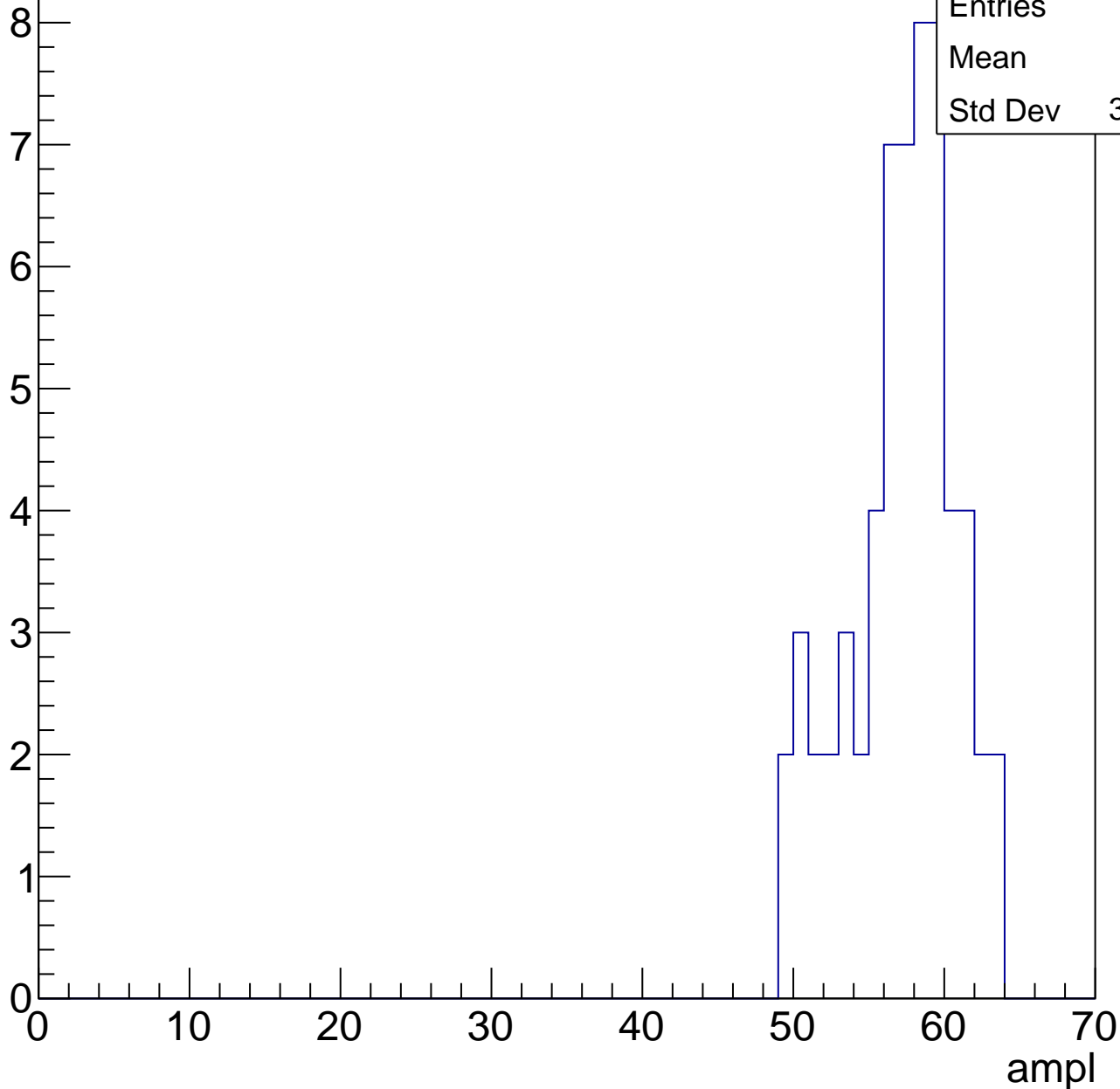


B0L000S, U4-ch125, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	56.7
Std Dev	3.518

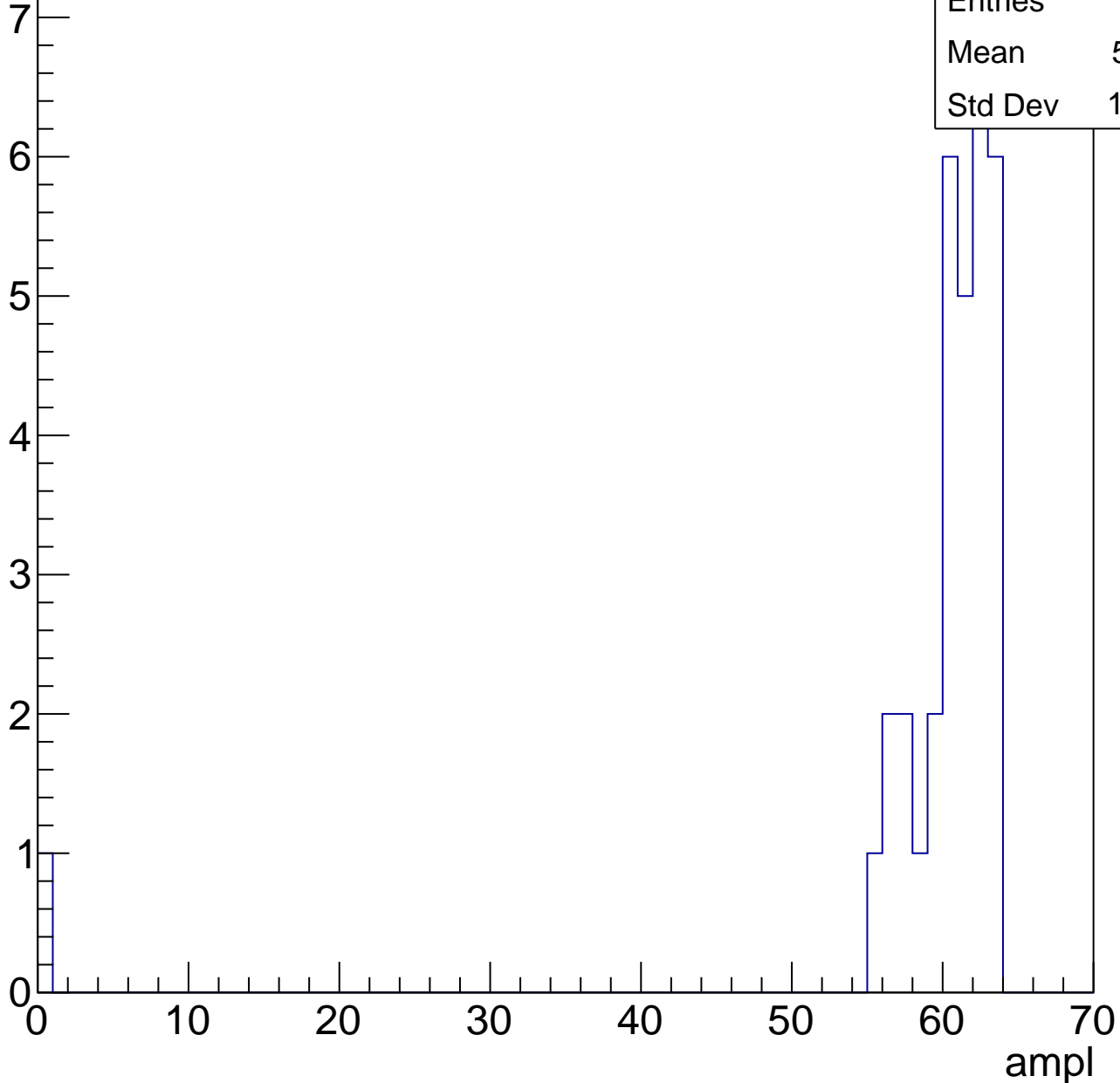


B0L000S, U4-ch125, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

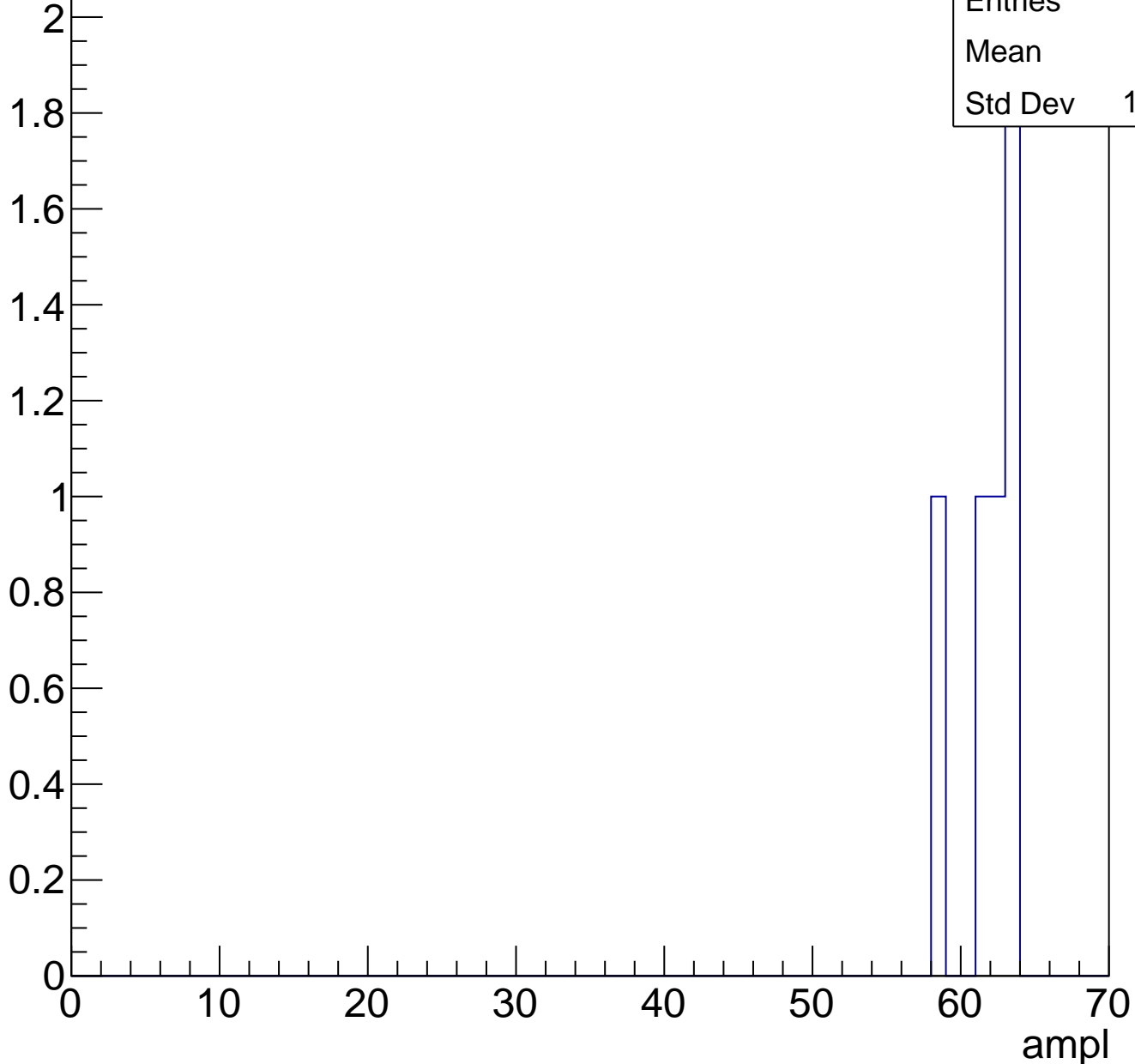
Entries	33
Mean	58.61
Std Dev	10.59



B0L000S, U4-ch125, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	5
Mean	61.4
Std Dev	1.855

B0L000S, U4-ch125, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U4-ch126, adc0

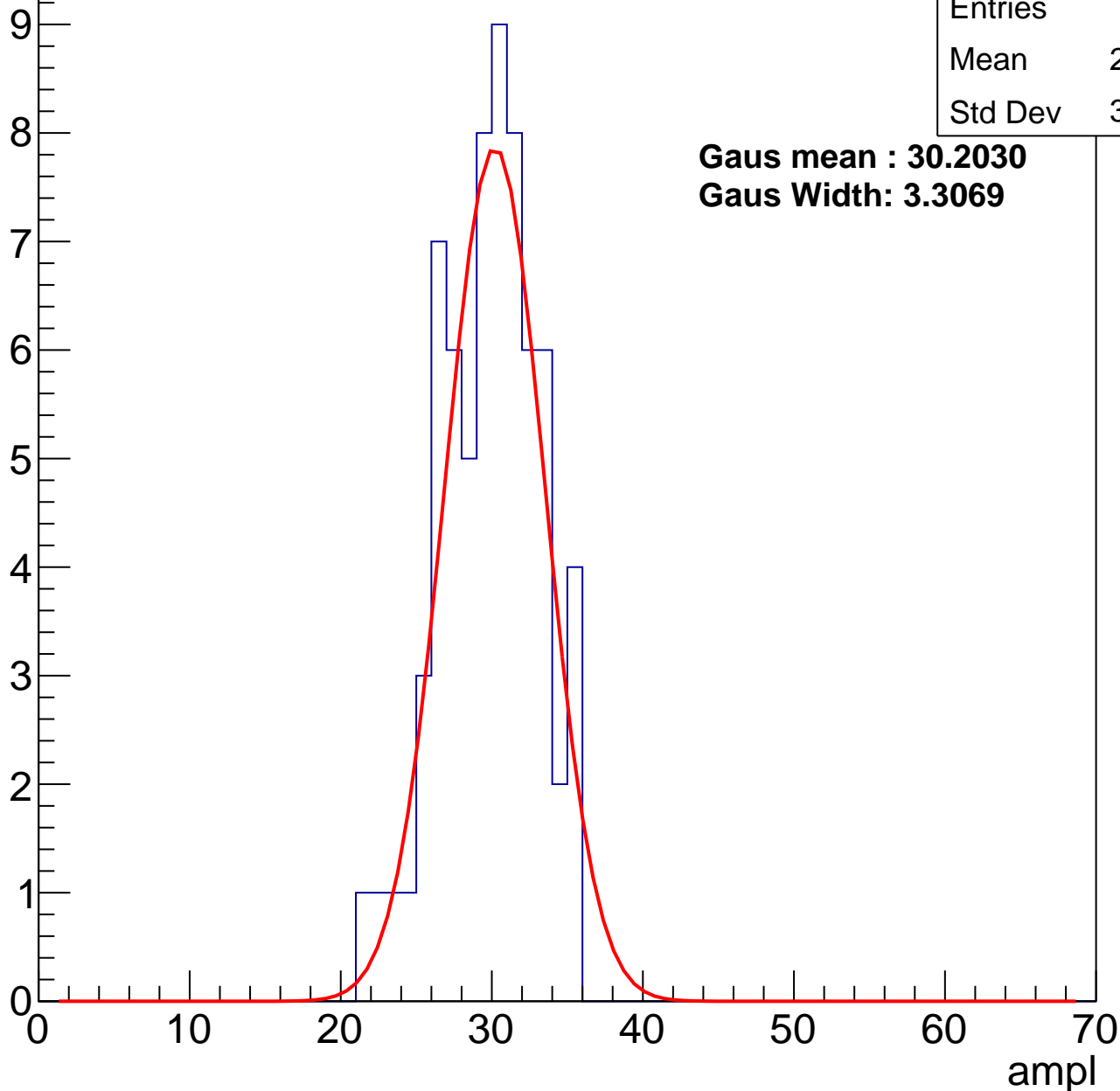
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	29.37
Std Dev	3.185

Gaus mean : 30.2030

Gaus Width: 3.3069



B0L000S, U4-ch126, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	36.27
Std Dev	3.572

Gaus mean : 37.0788

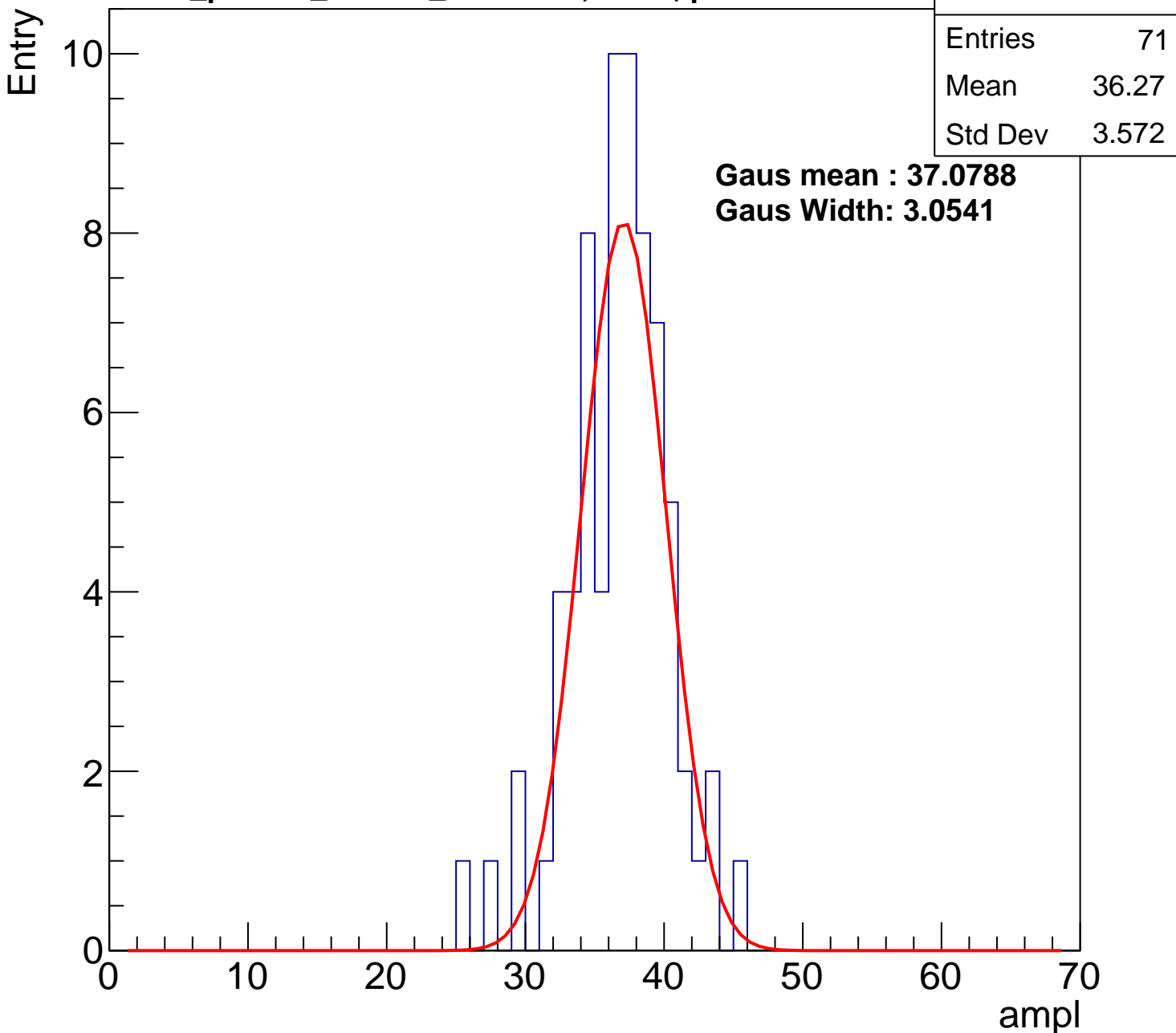
Gaus Width: 3.0541

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch126, adc2

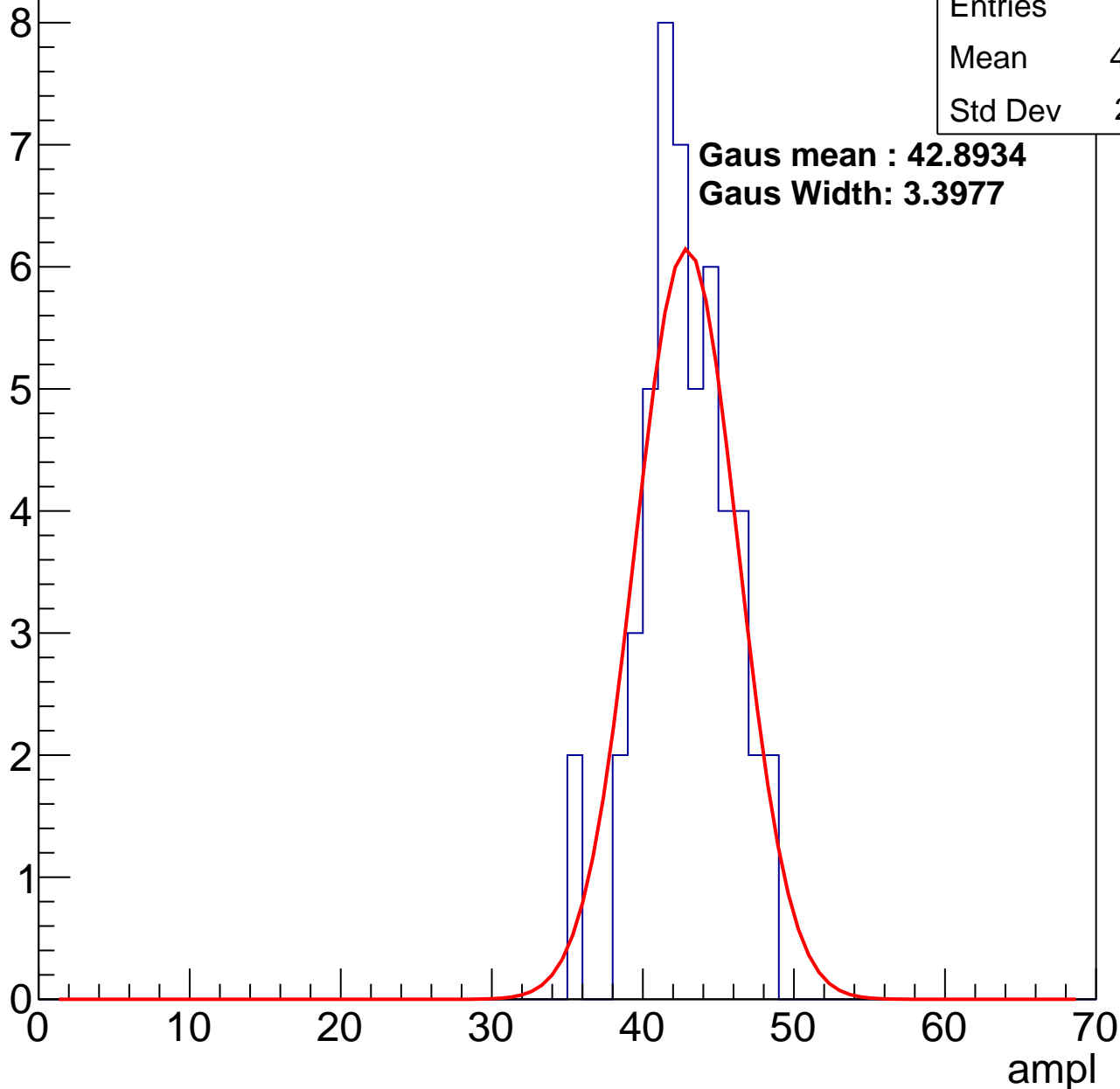
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	42.36
Std Dev	2.931

Gaus mean : 42.8934

Gaus Width: 3.3977

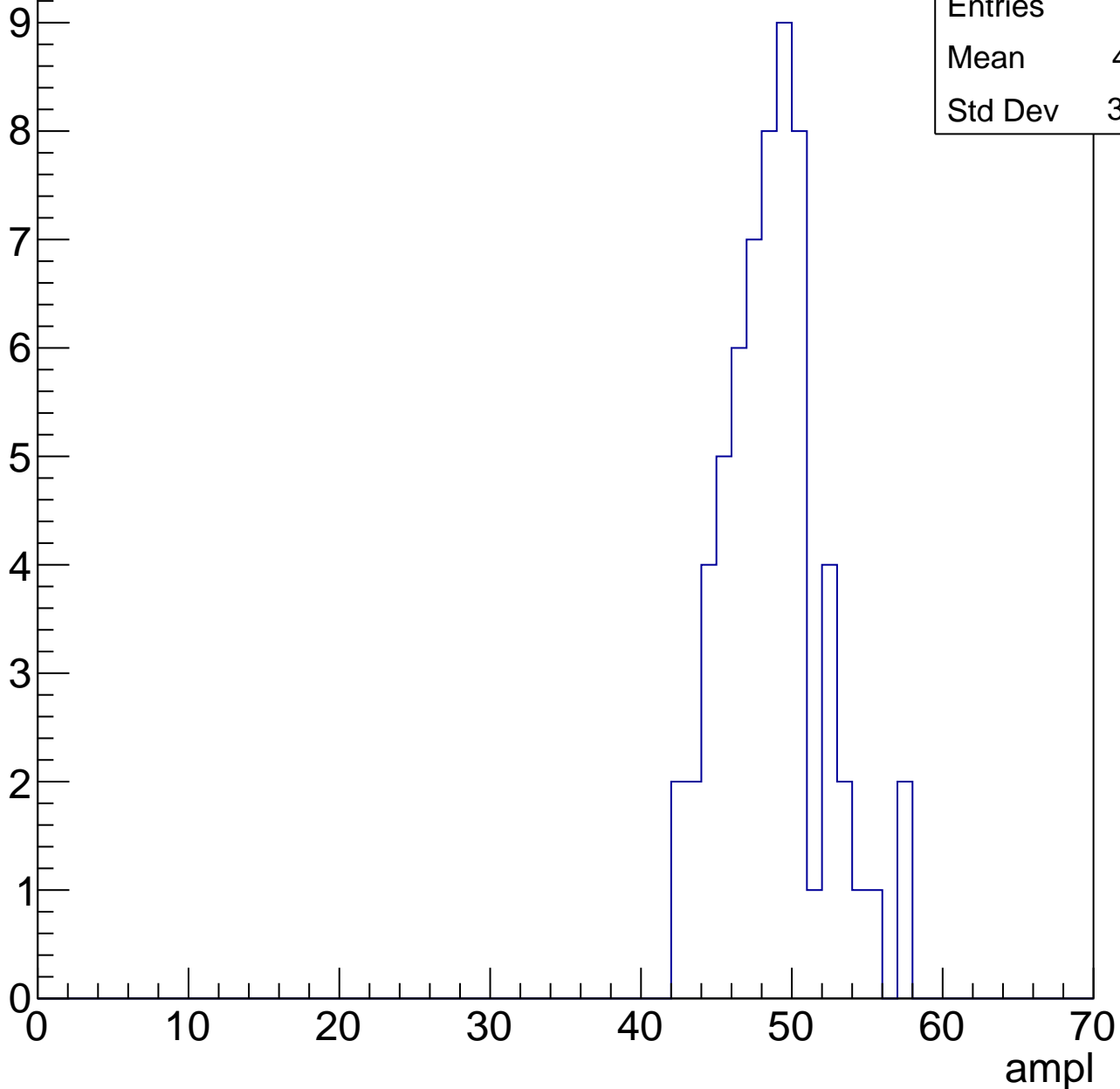


B0L000S, U4-ch126, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	48.21
Std Dev	3.288

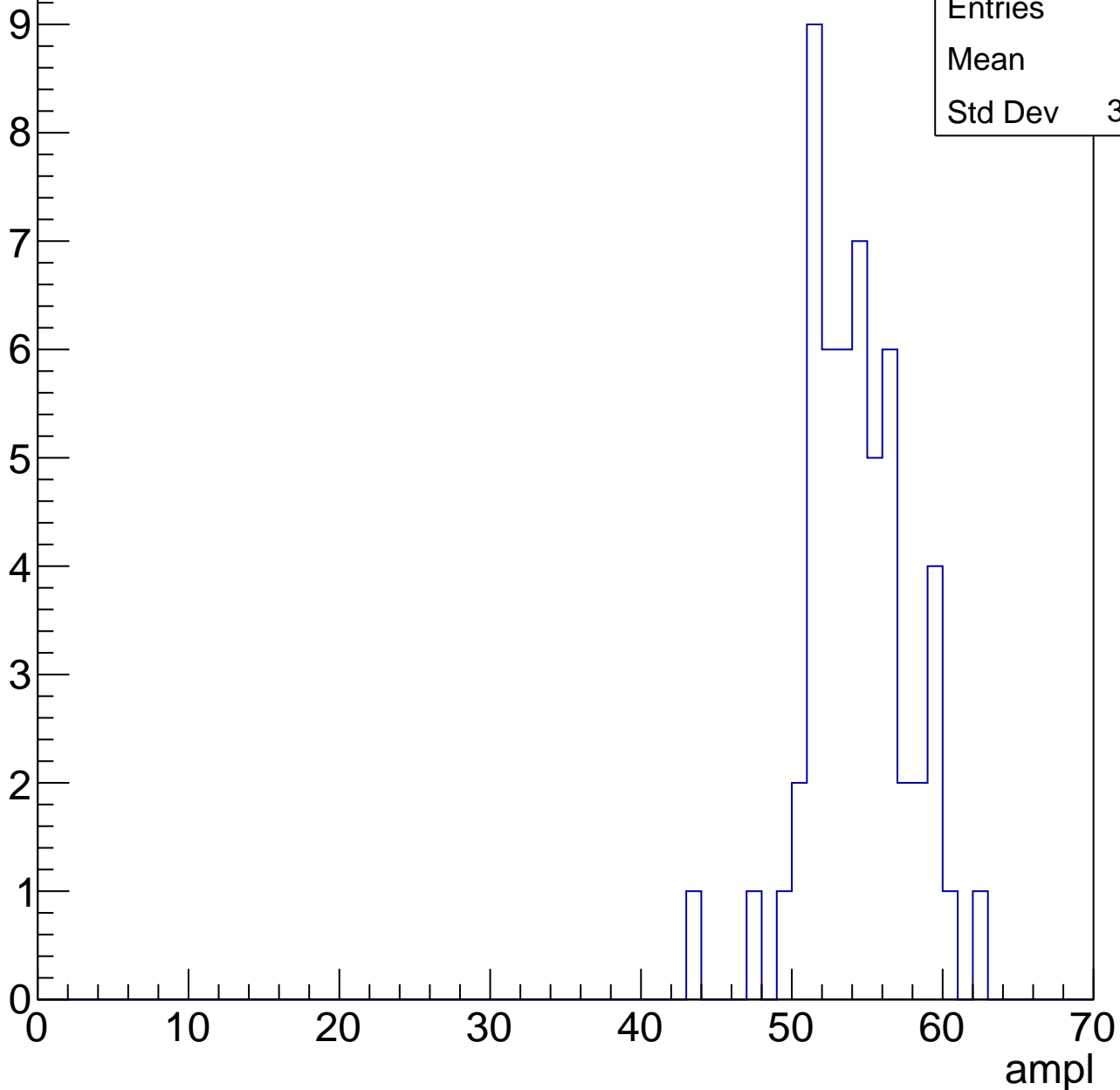


B0L000S, U4-ch126, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	53.8
Std Dev	3.374

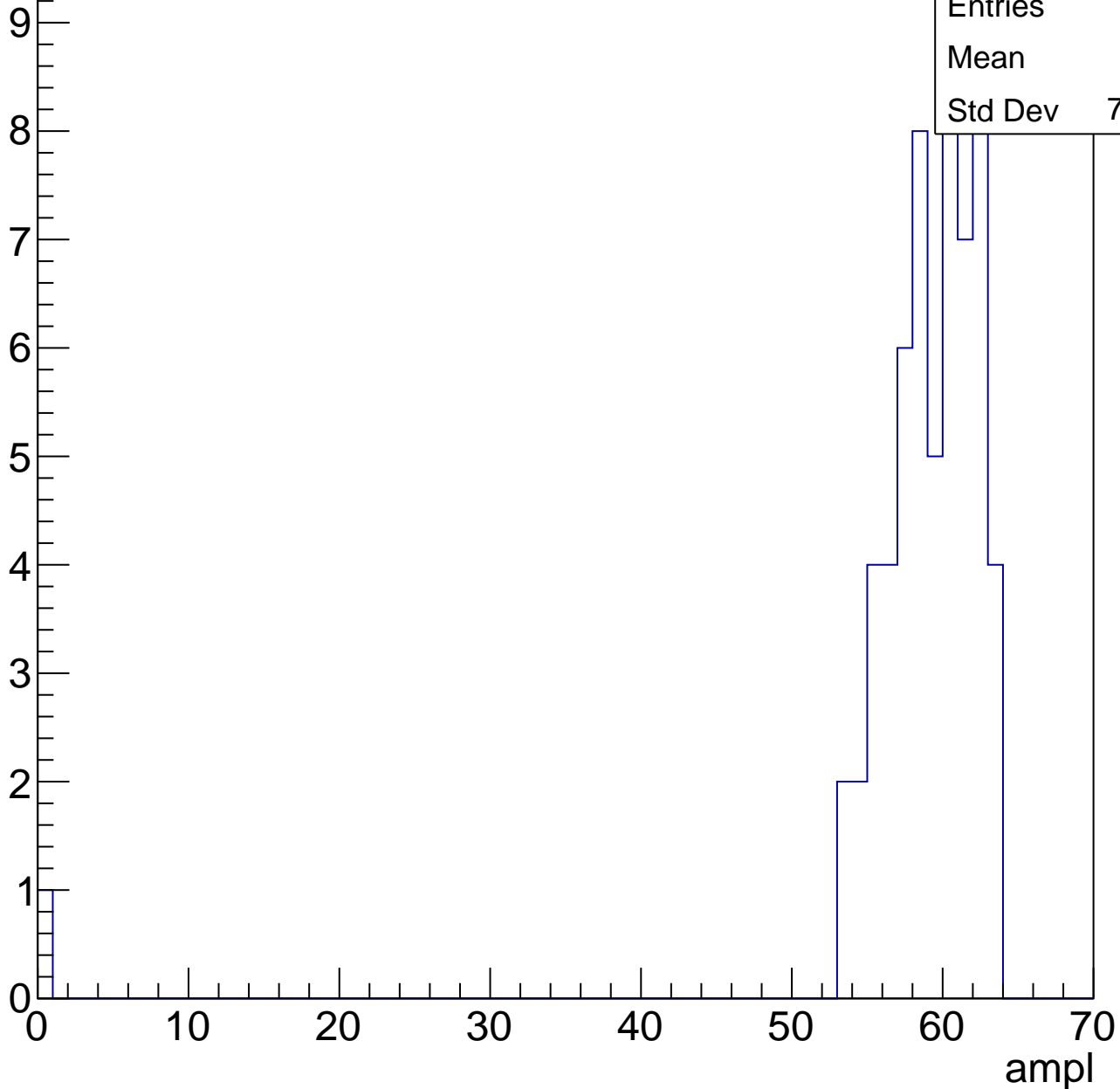


B0L000S, U4-ch126, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57.9
Std Dev	7.995

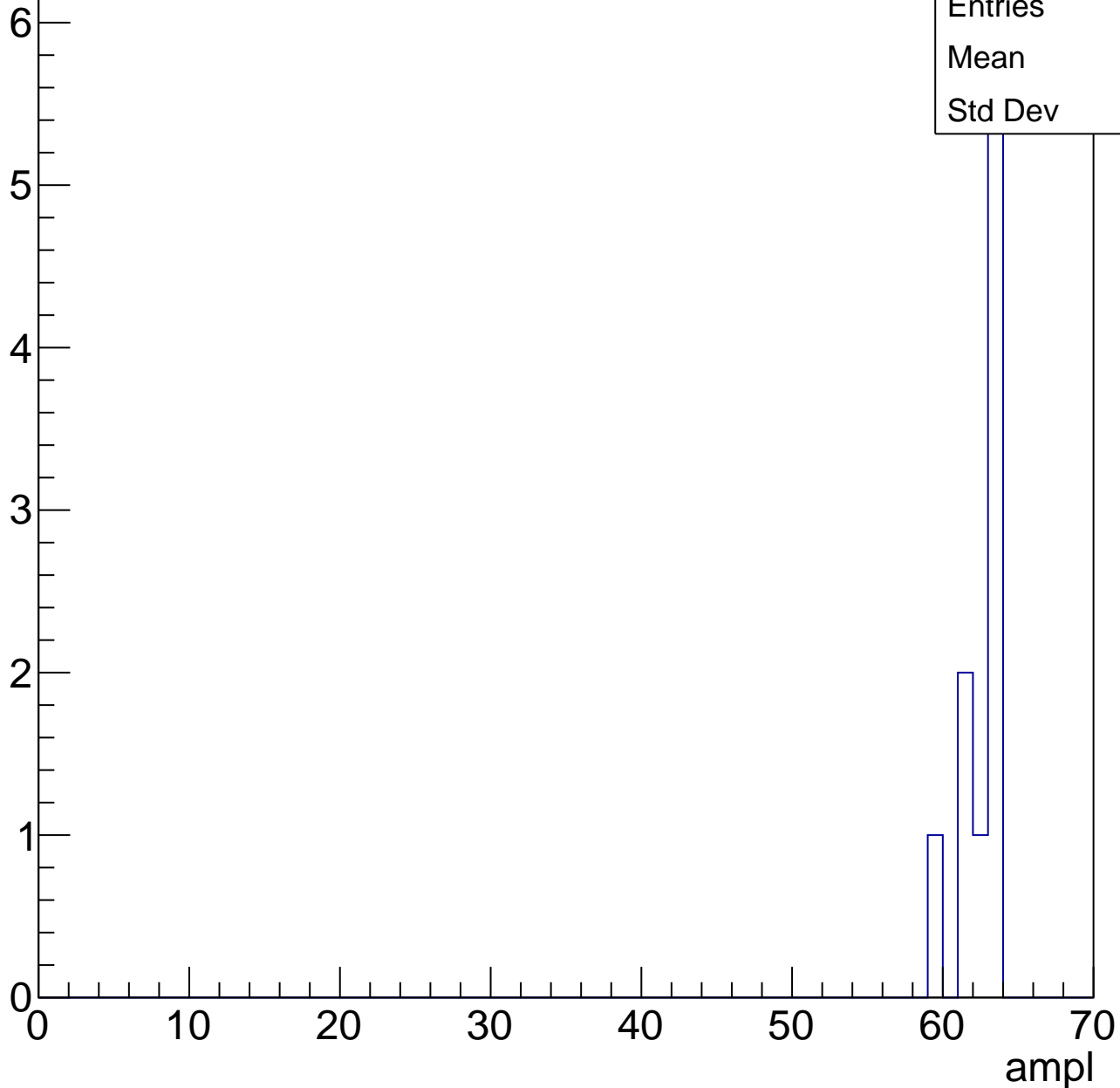


B0L000S, U4-ch126, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62.1
Std Dev	1.3



B0L000S, U4-ch126, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch127, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	83
Mean	27.55
Std Dev	6.972

Gaus mean : 29.0959

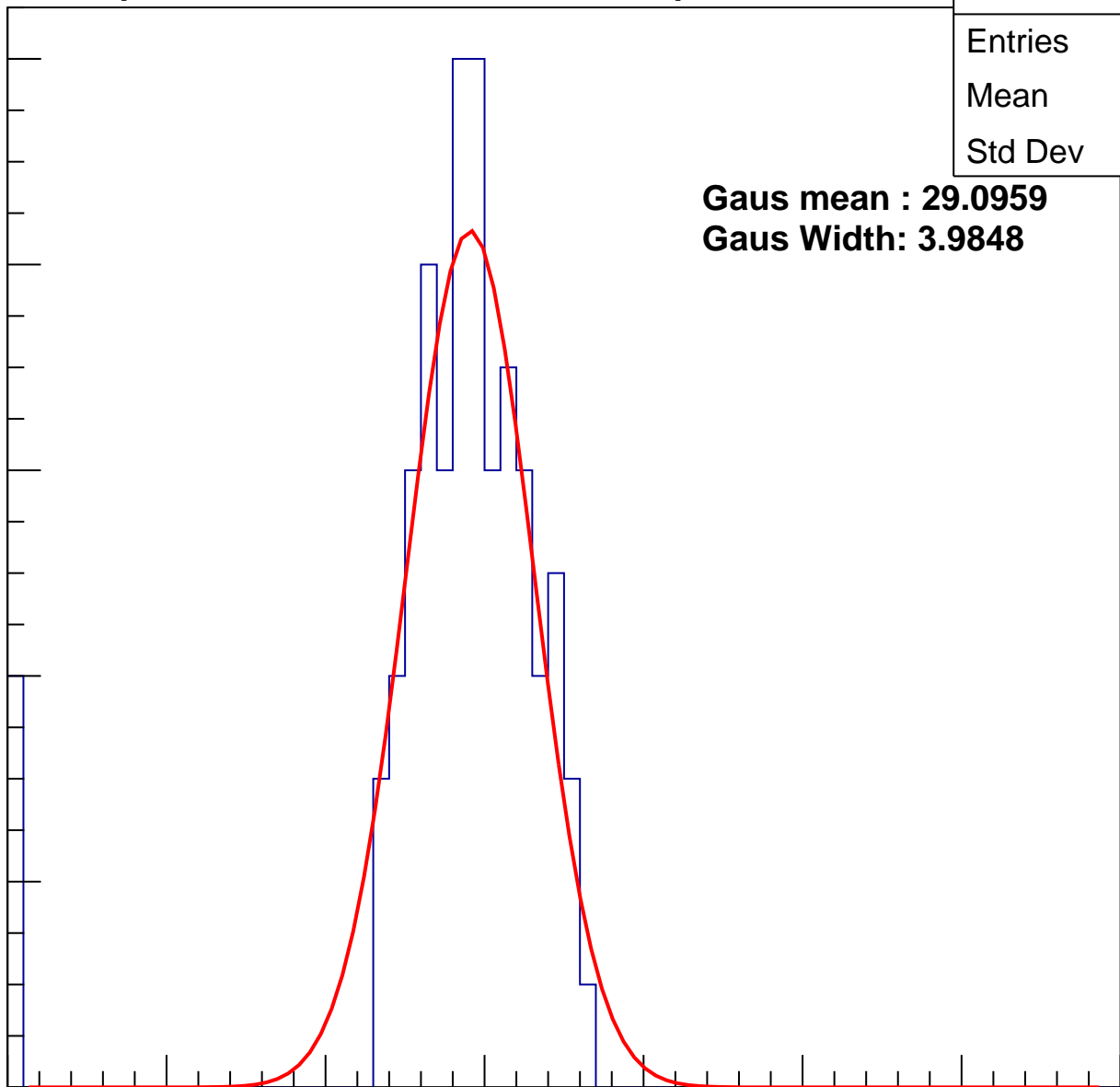
Gaus Width: 3.9848

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U4-ch127, adc1

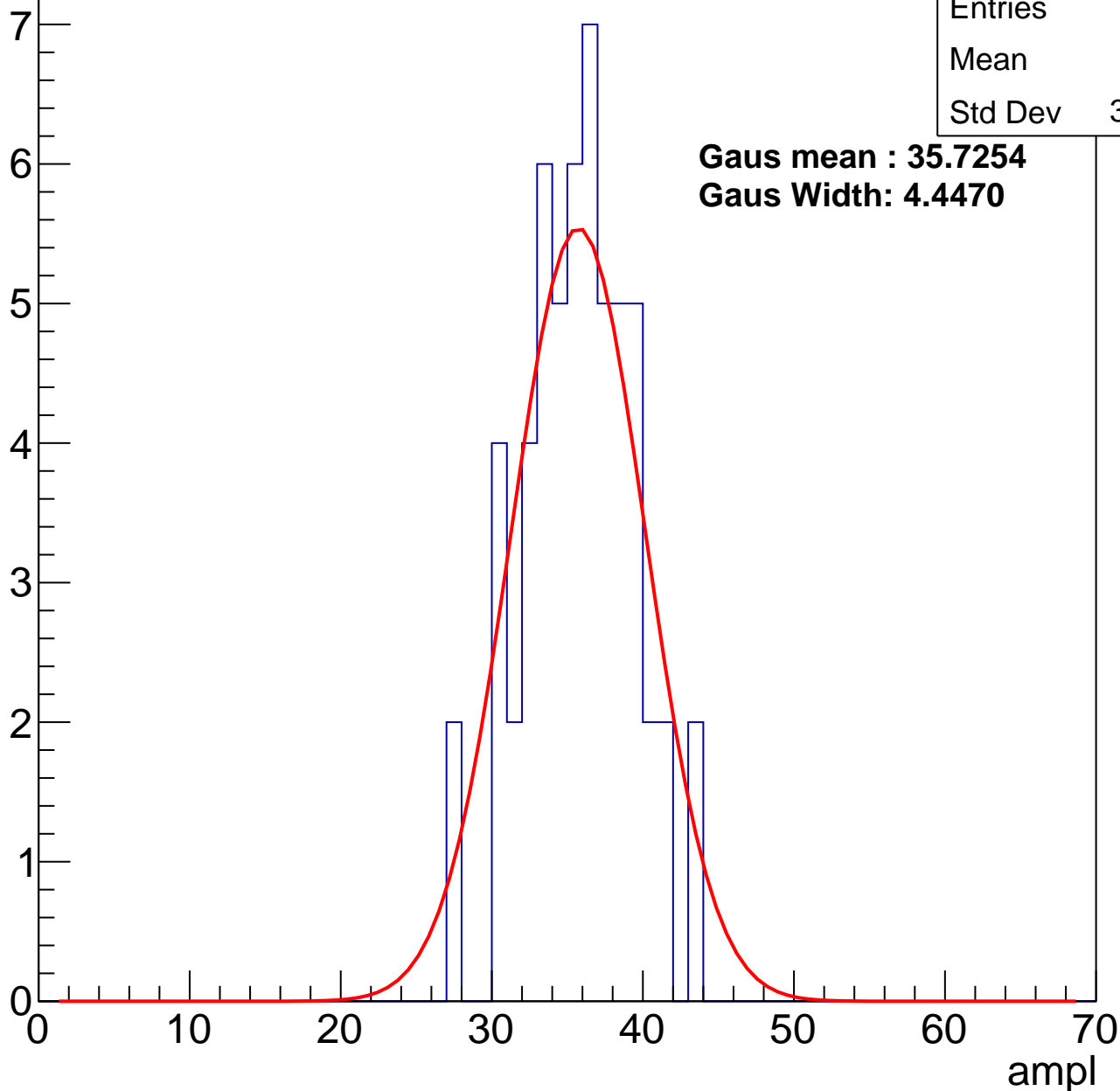
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.3
Std Dev	3.559

Gaus mean : 35.7254

Gaus Width: 4.4470



B0L000S, U4-ch127, adc2

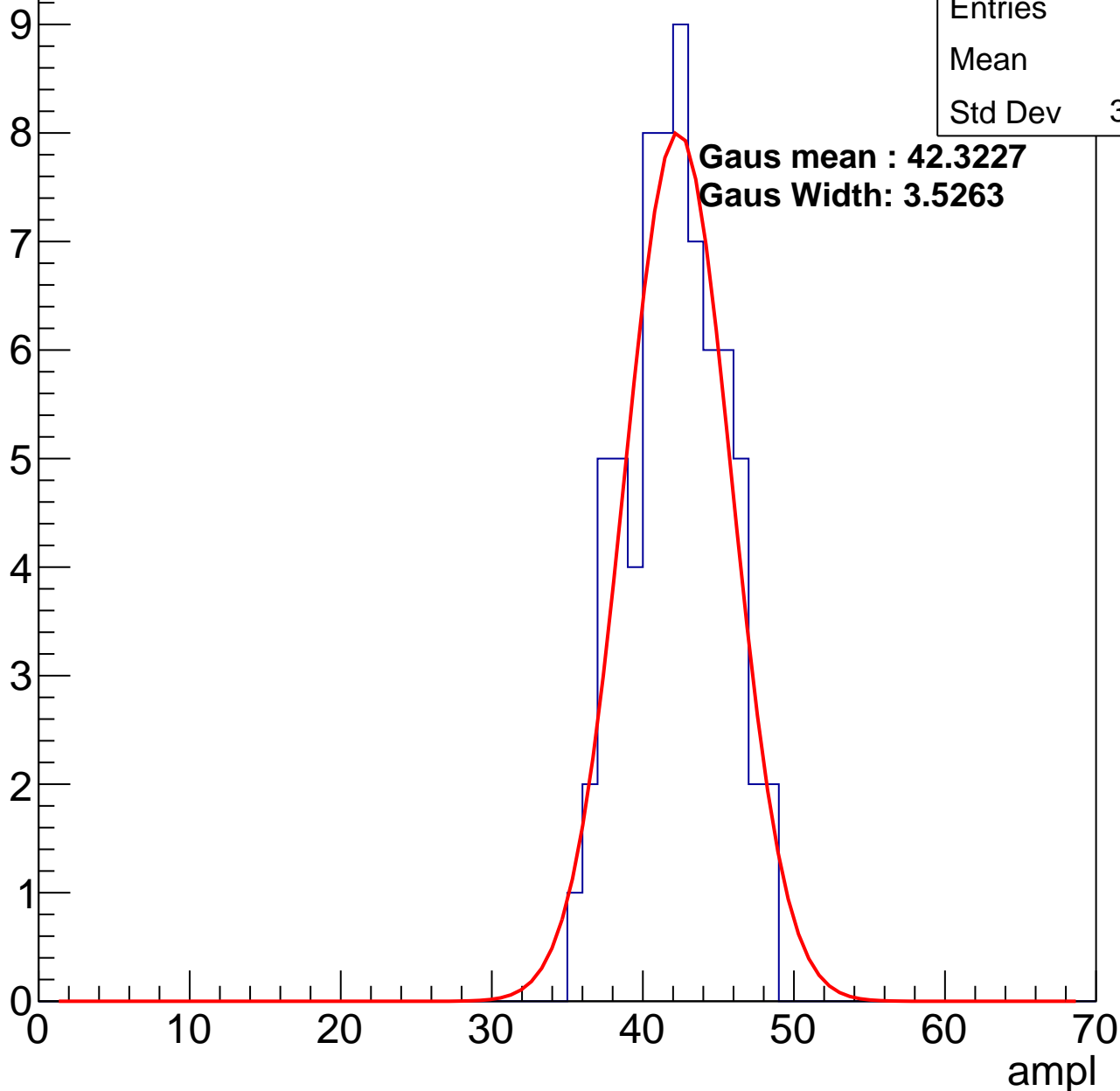
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.7
Std Dev	3.123

Gaus mean : 42.3227

Gaus Width: 3.5263

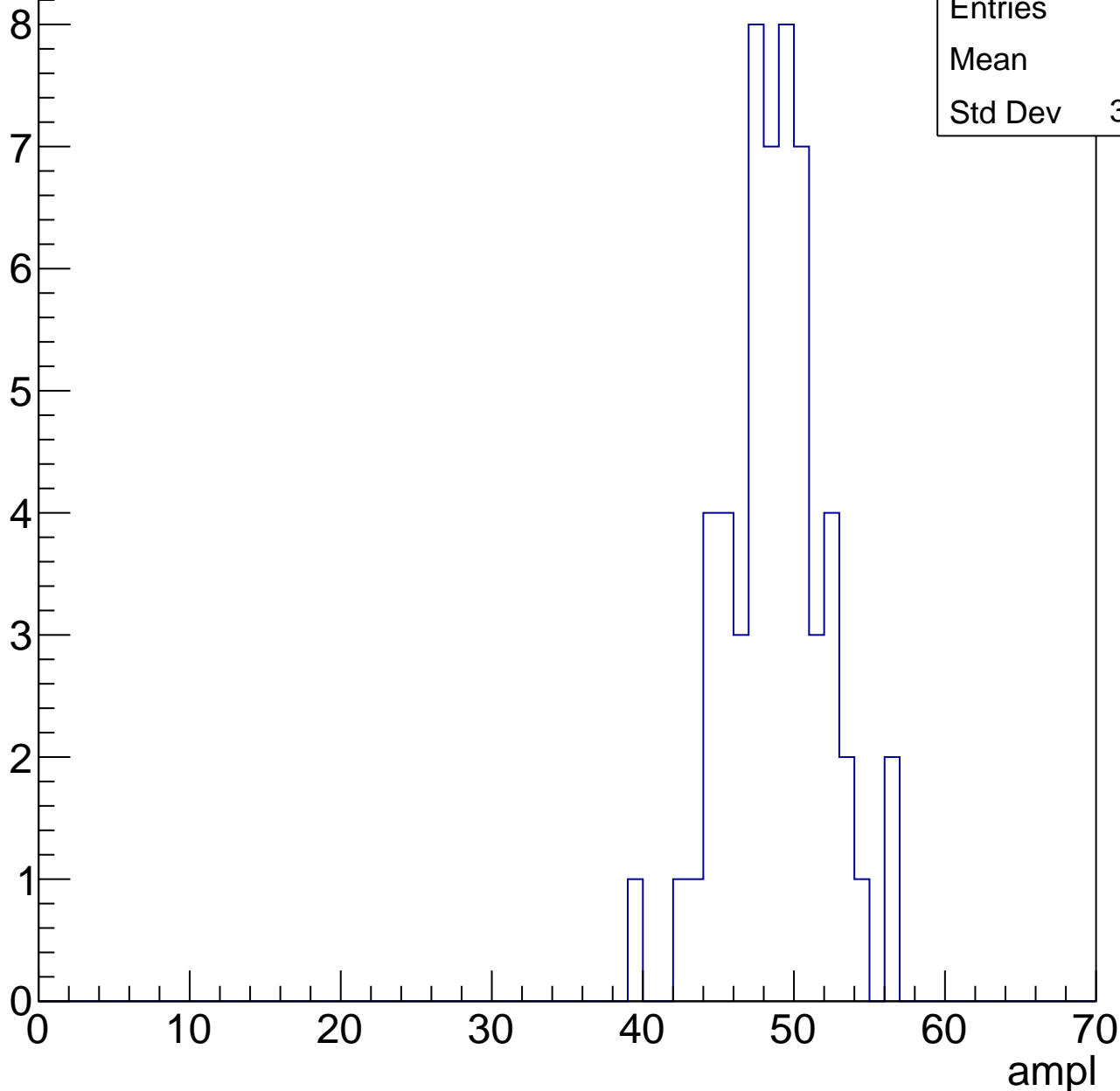


B0L000S, U4-ch127, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	48.3
Std Dev	3.267

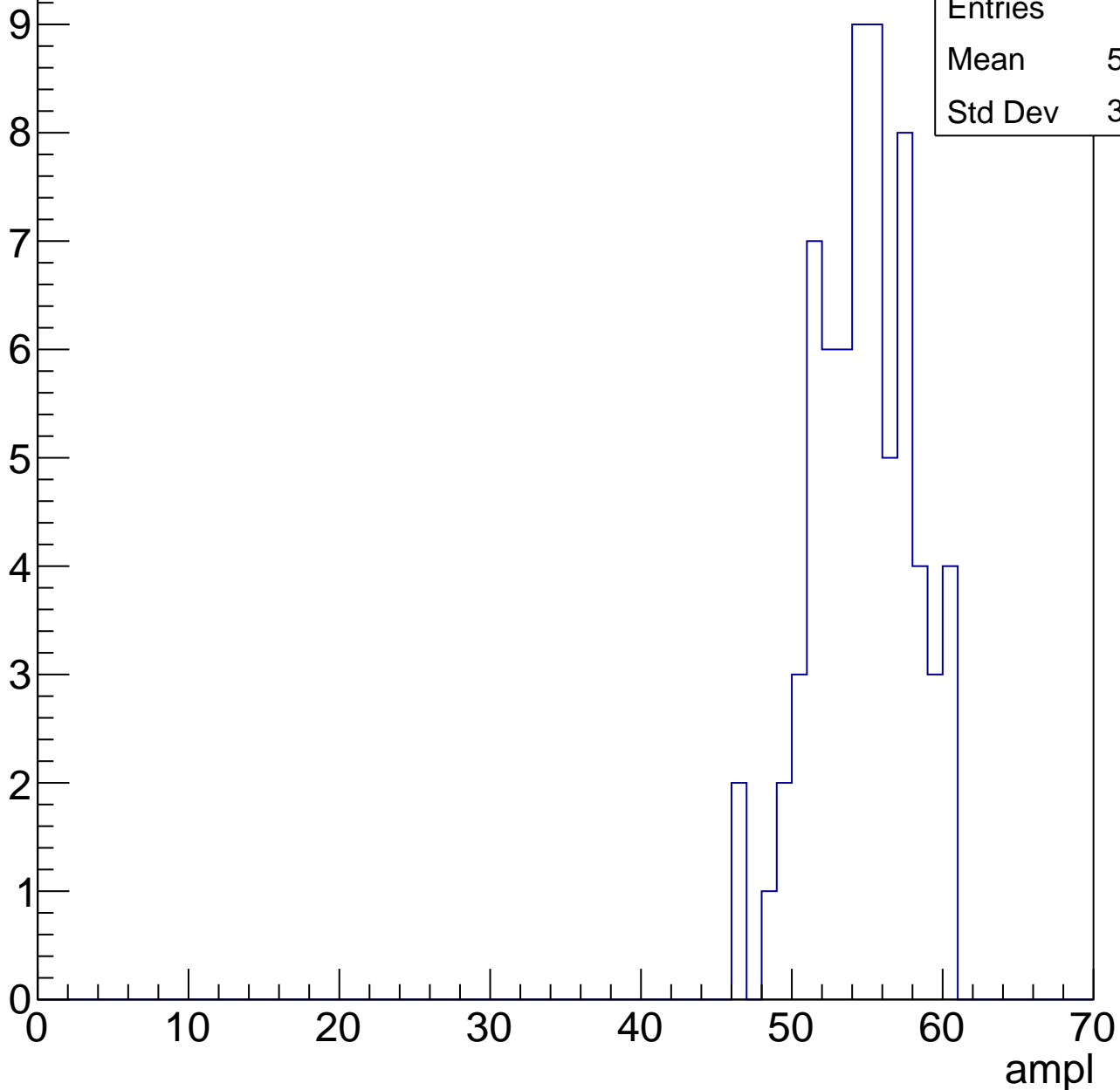


B0L000S, U4-ch127, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

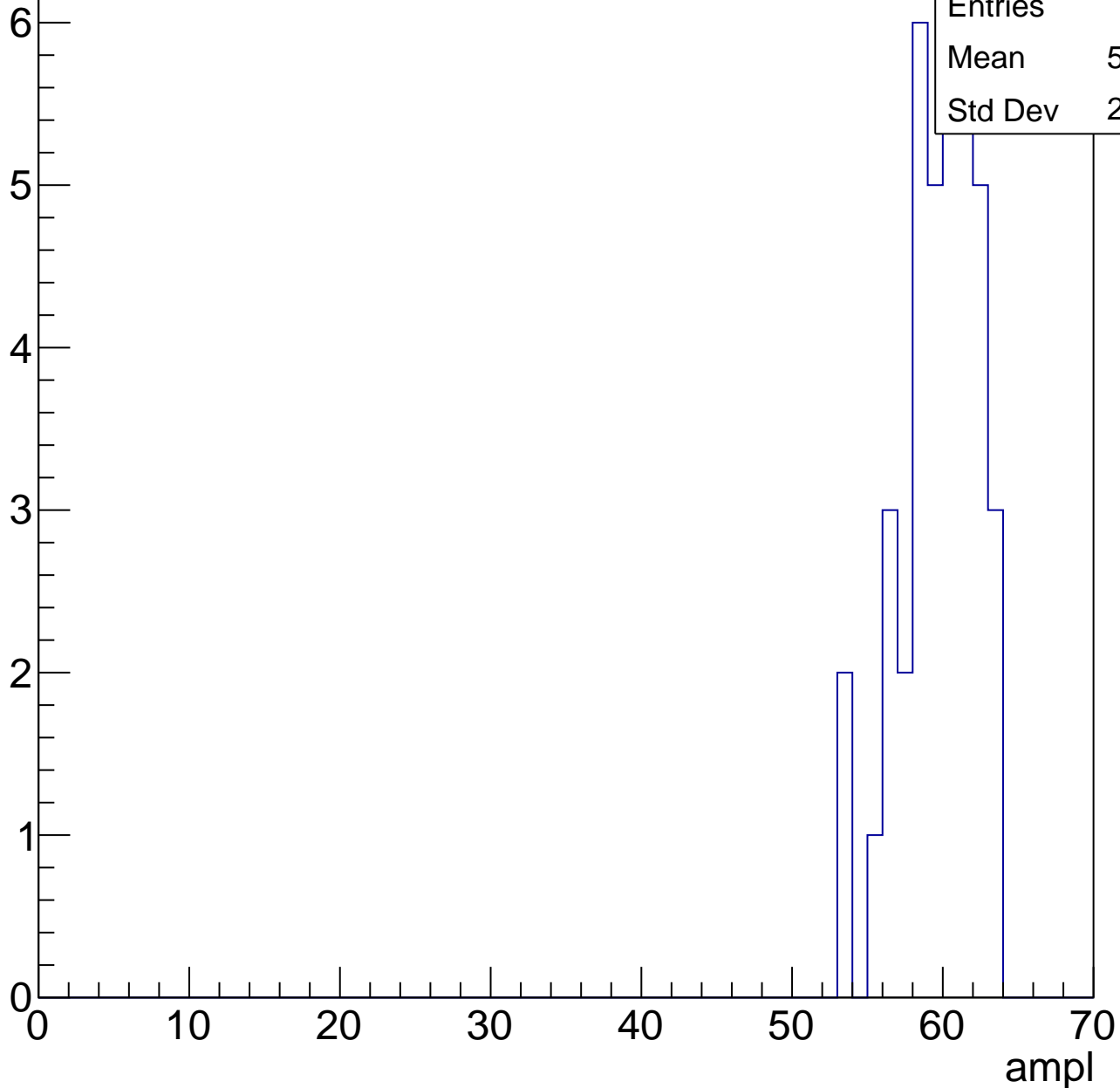
Entries	69
Mean	54.22
Std Dev	3.265



B0L000S, U4-ch127, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

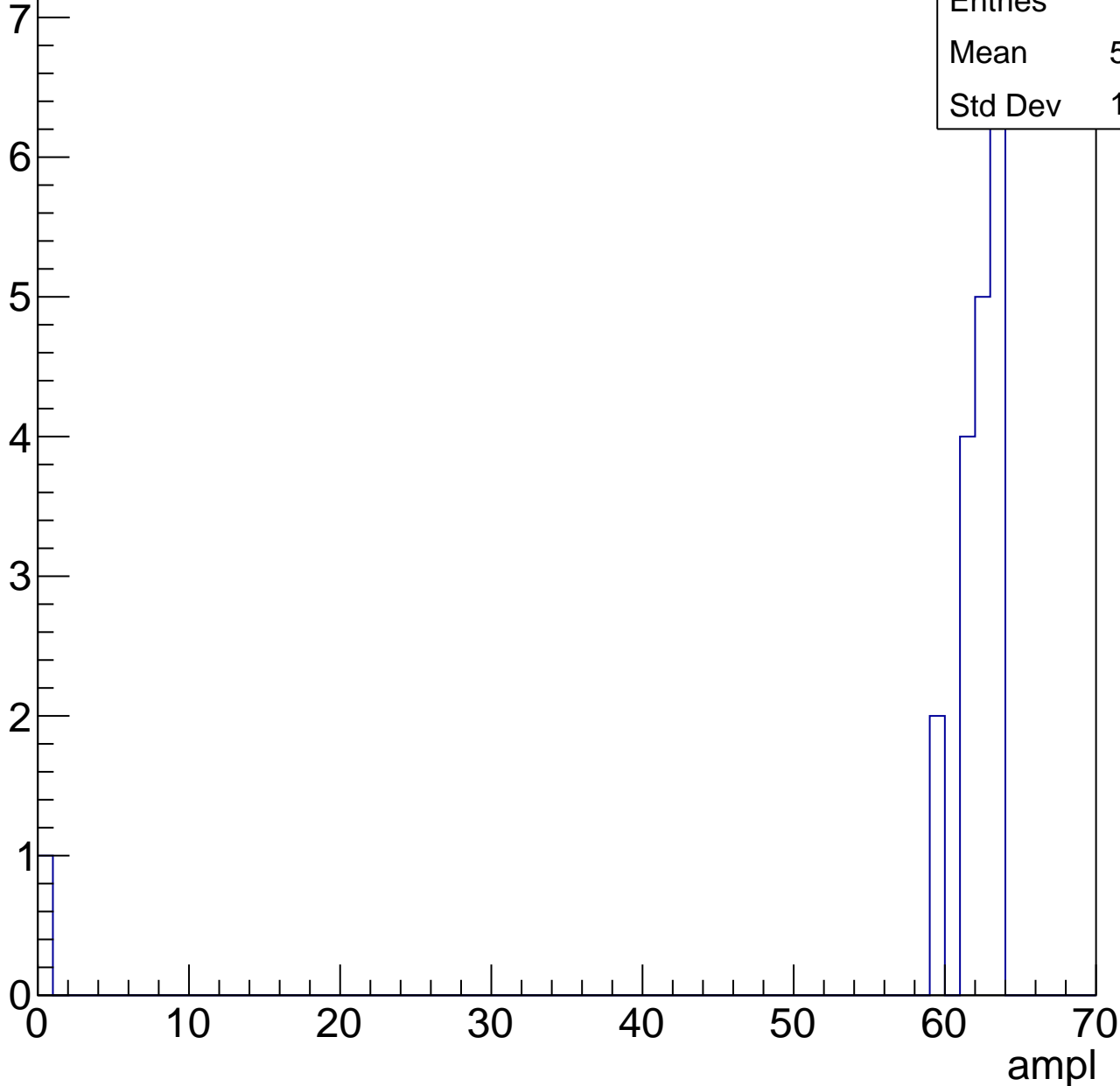


B0L000S, U4-ch127, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	19
Mean	58.58
Std Dev	13.86



B0L000S, U4-ch127, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U4-ch127, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0