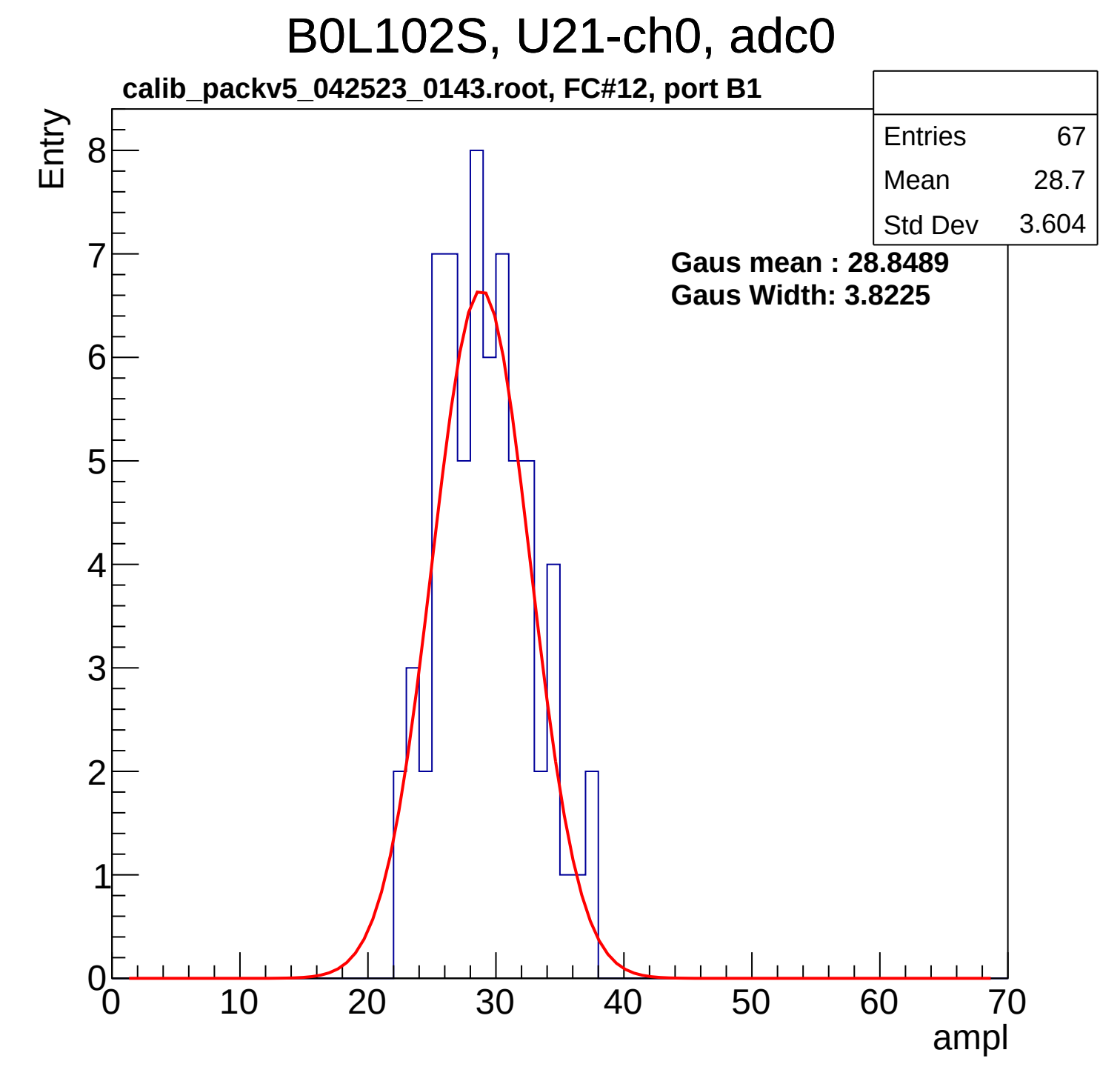
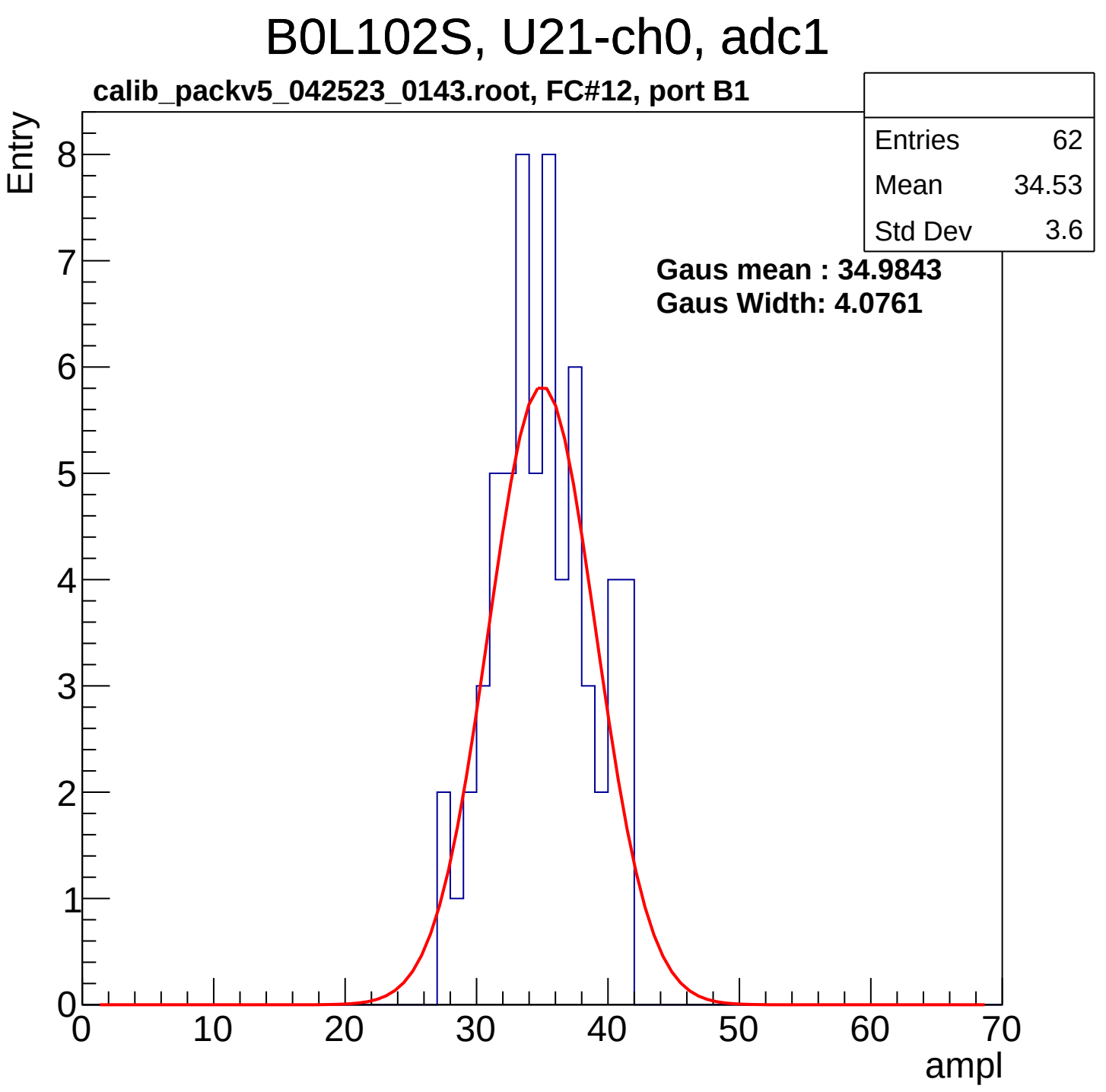

B0L102S, U21-ch0, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.7
Std Dev 3.604
Gaus mean : 28.8489
Gaus Width: 3.8225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch0, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 34.53
Std Dev 3.6
Gaus mean : 34.9843
Gaus Width: 4.0761
Entry
ampl
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1
2
3
4
5
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7
8
0
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40
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60
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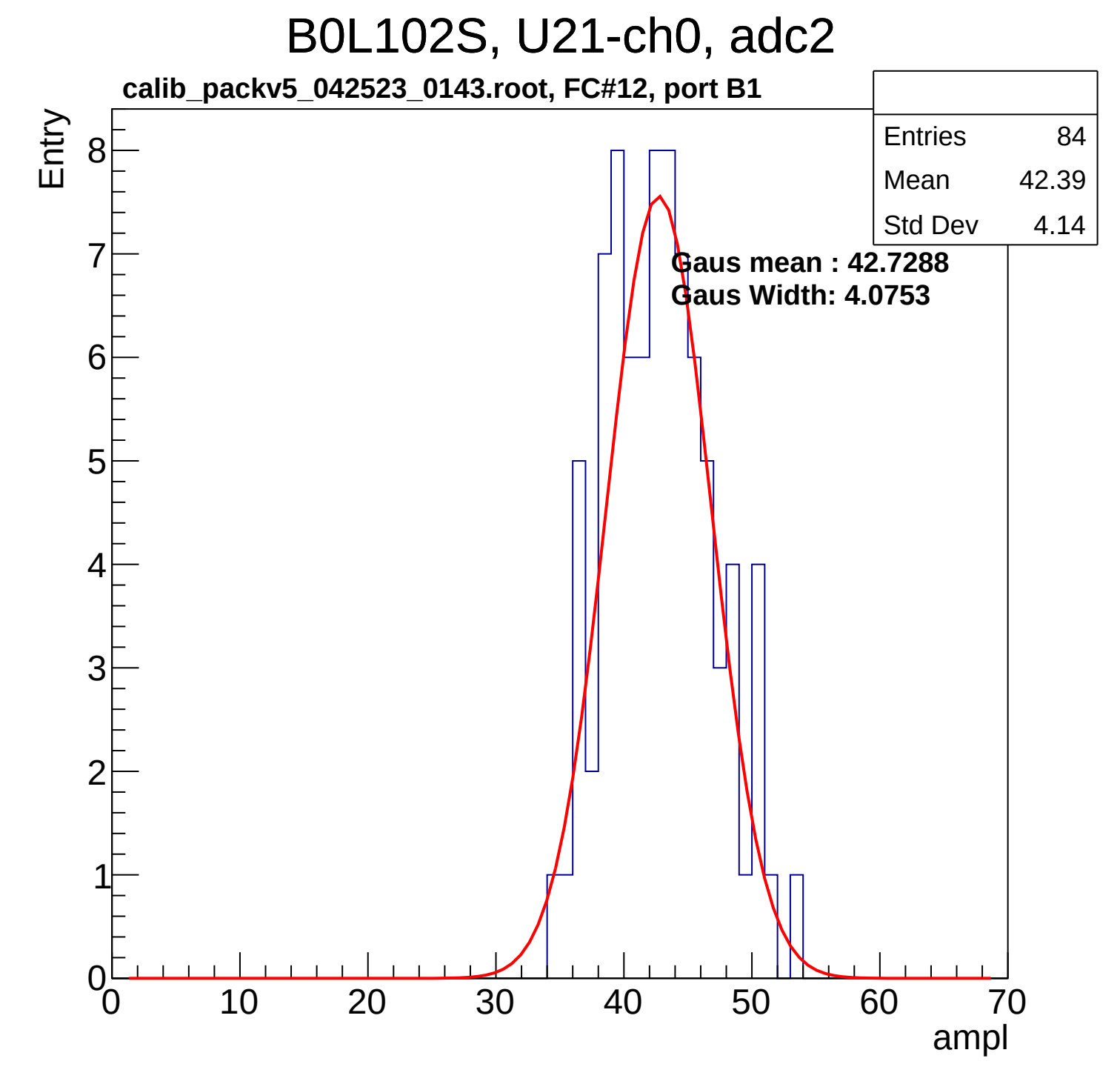
B0L102S, U21-ch0, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 42.39
Std Dev 4.14
Gaus mean : 42.7288
Gaus Width: 4.0753
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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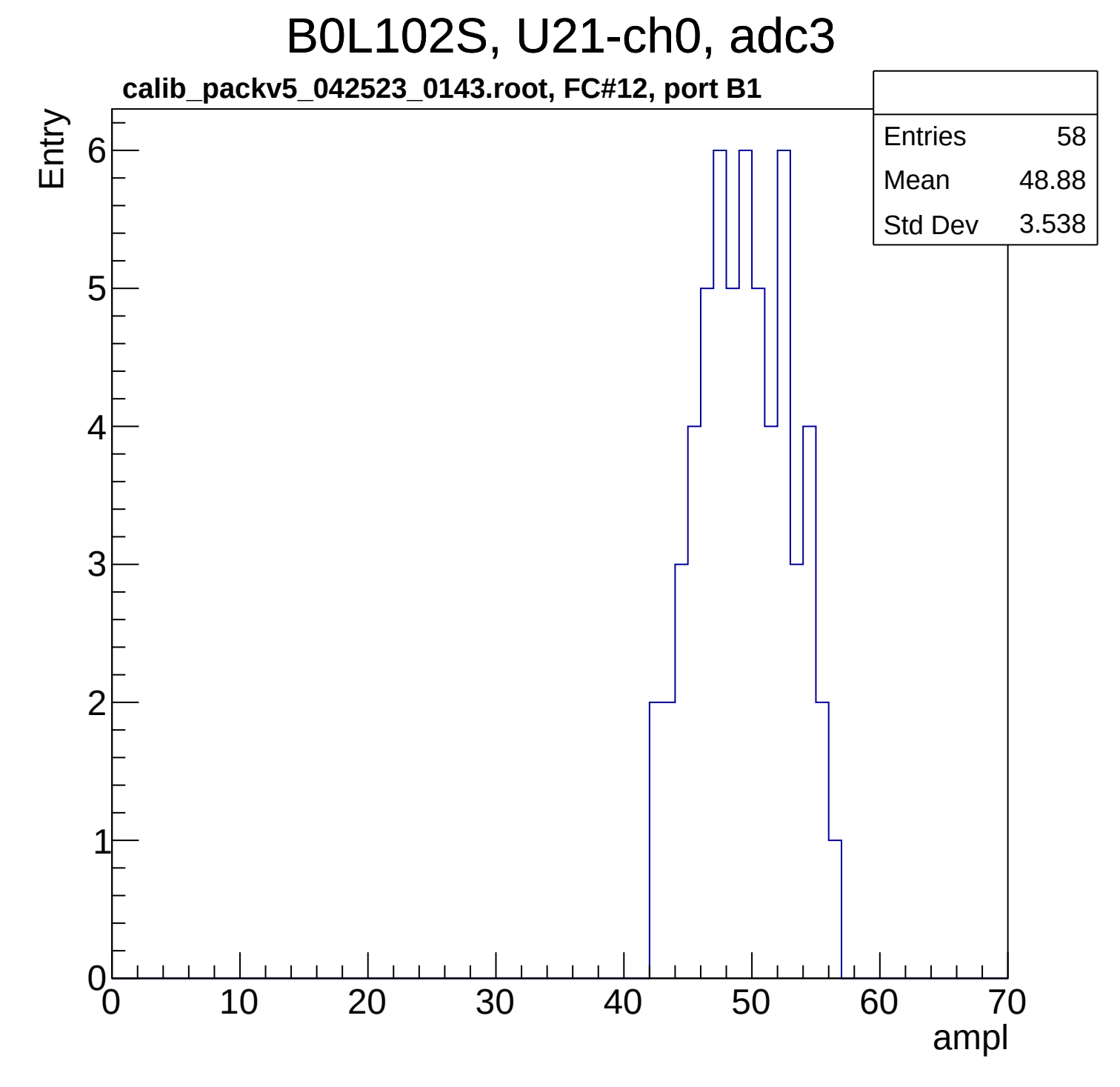
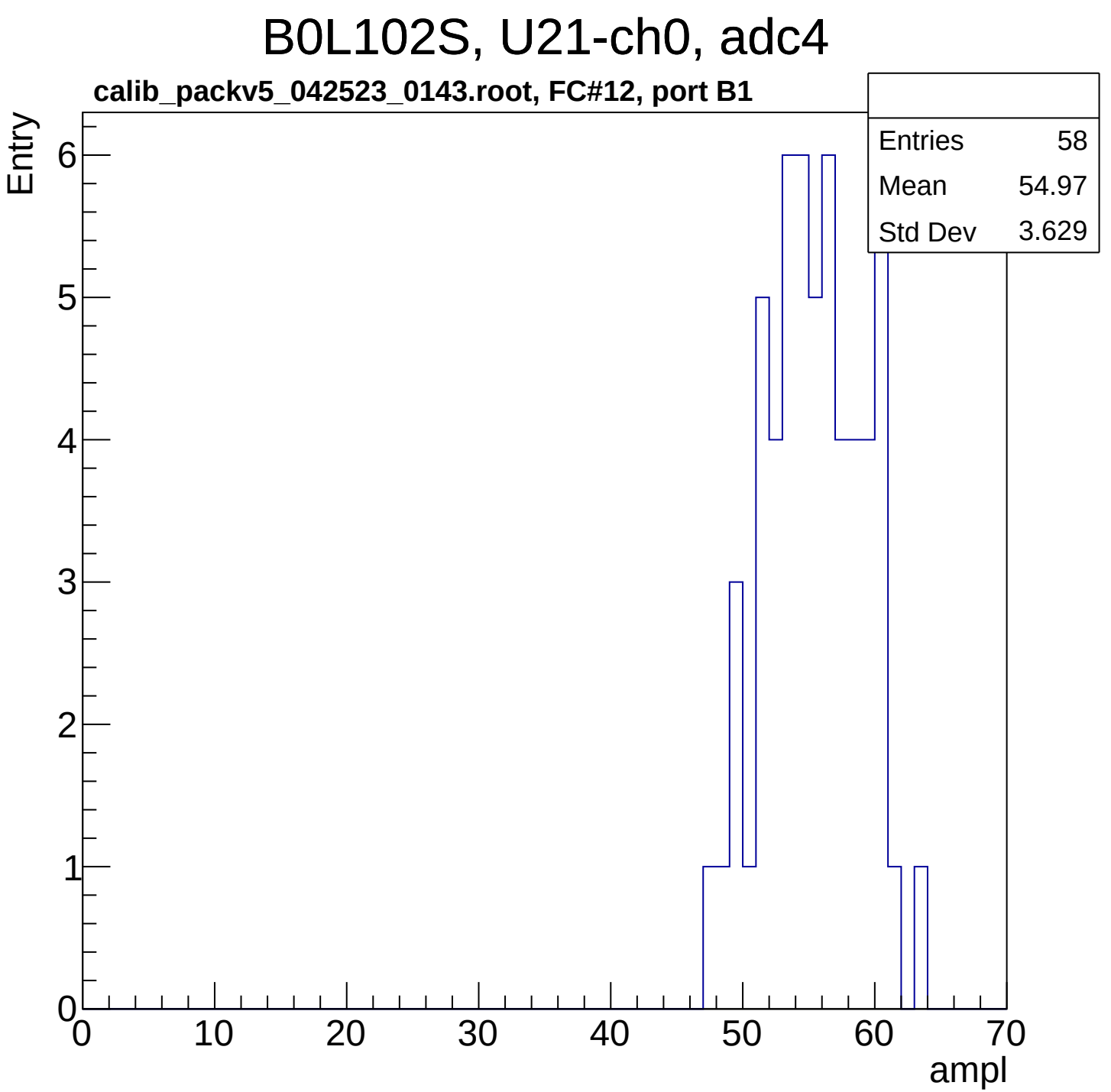
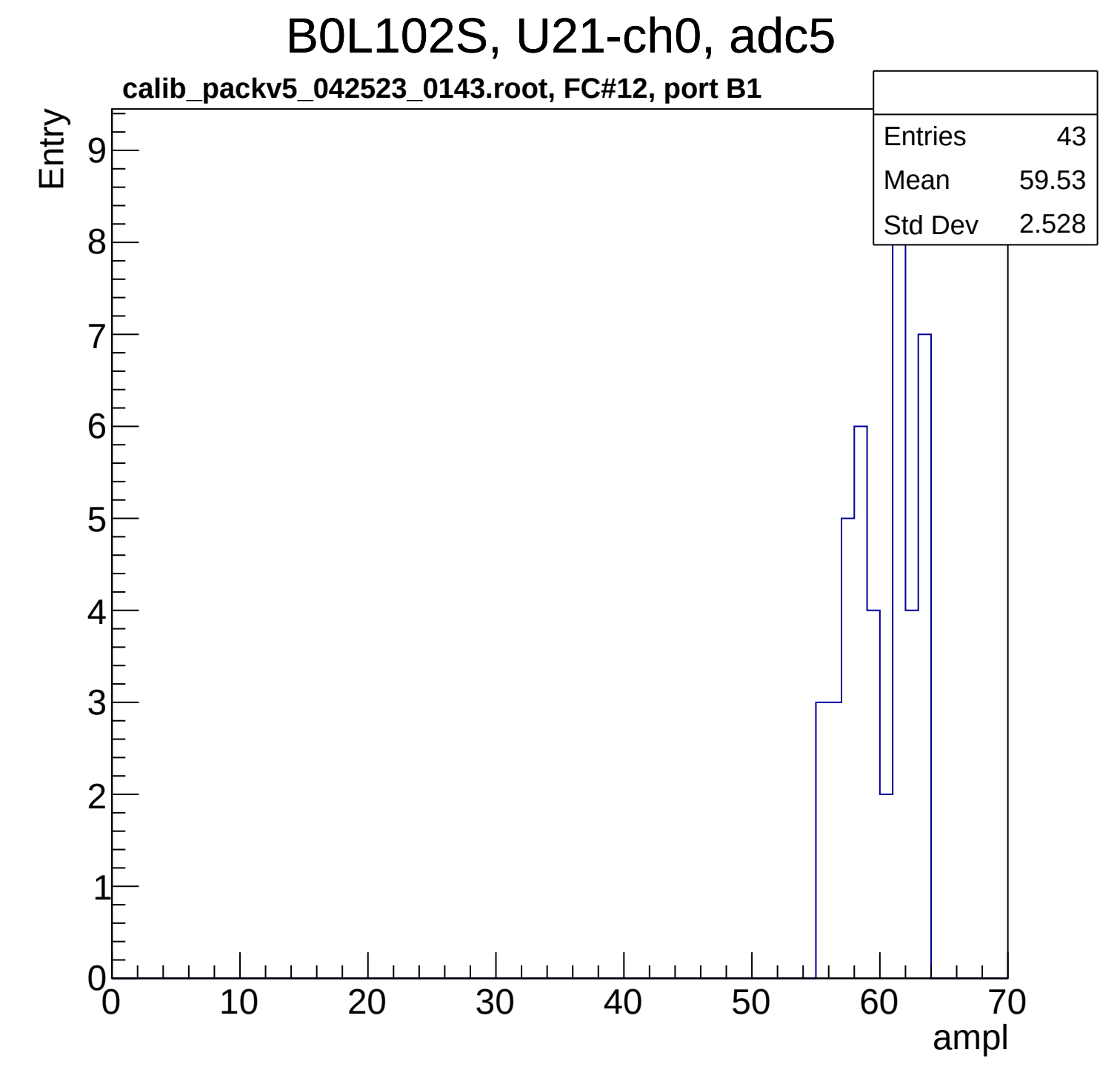

B0L102S, U21-ch0, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 48.88
Std Dev 3.538
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch0, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 54.97
Std Dev 3.629
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch0, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.53
Std Dev 2.528
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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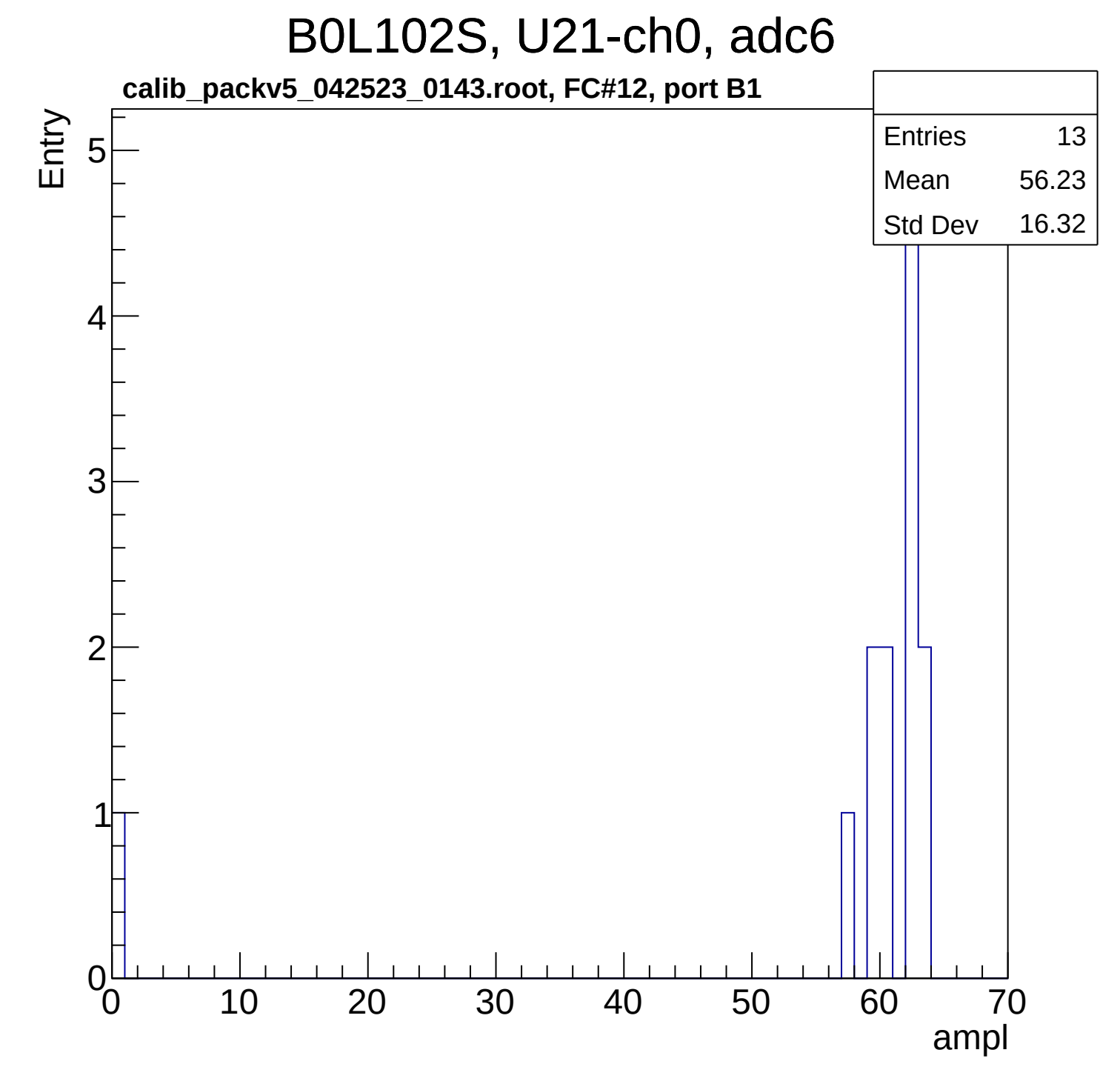
B0L102S, U21-ch0, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.23
Std Dev 16.32
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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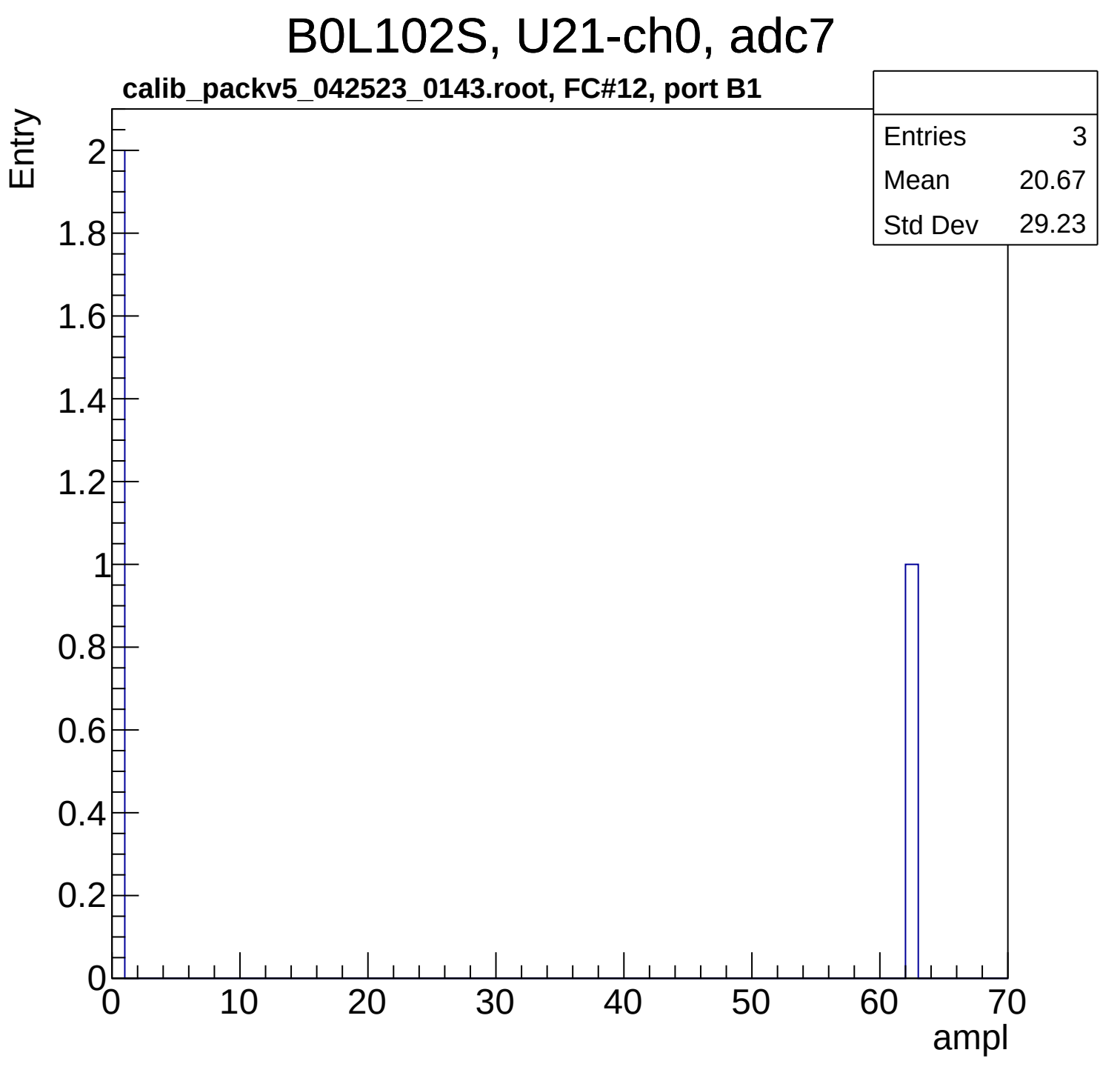
B0L102S, U21-ch0, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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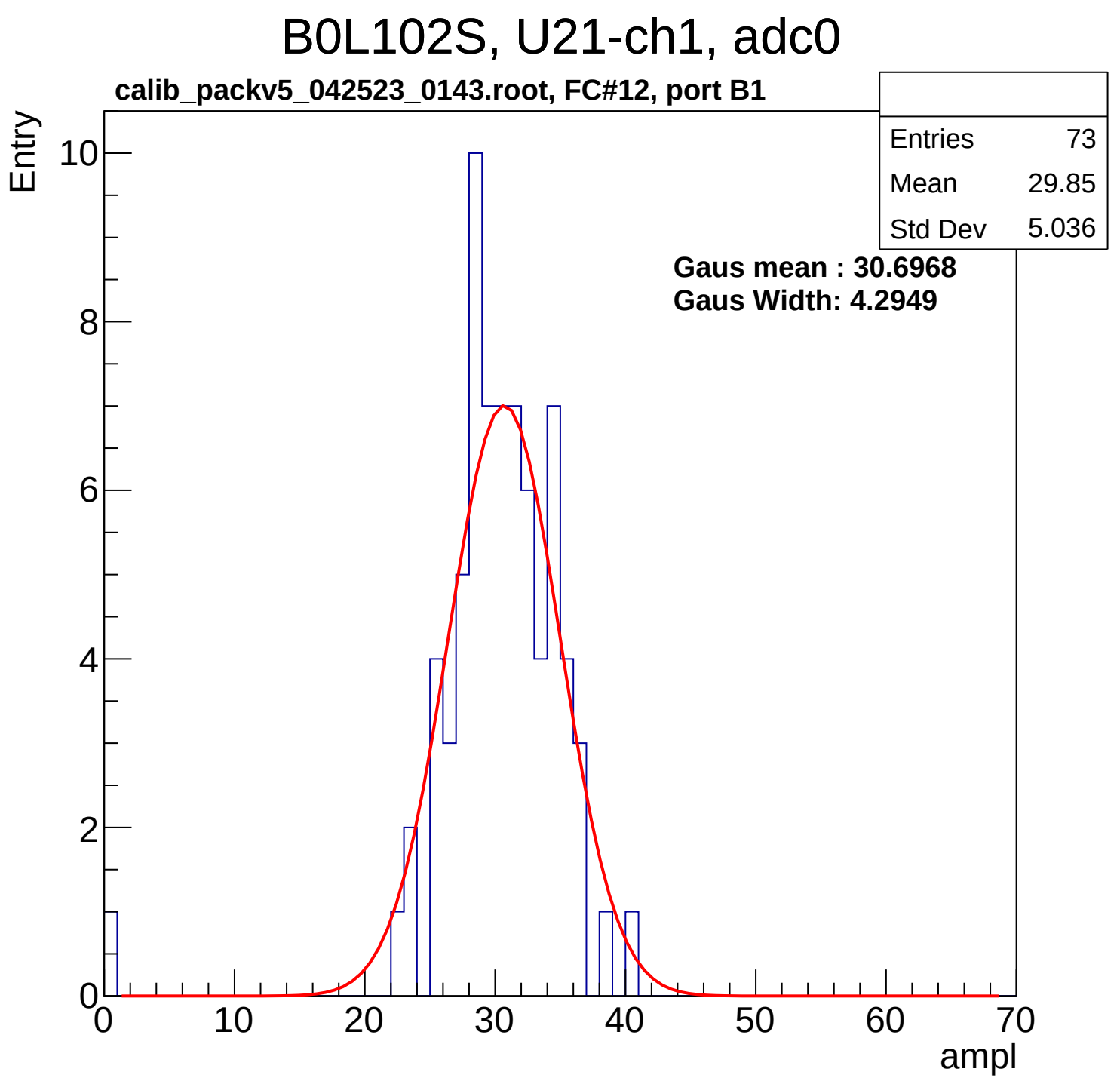
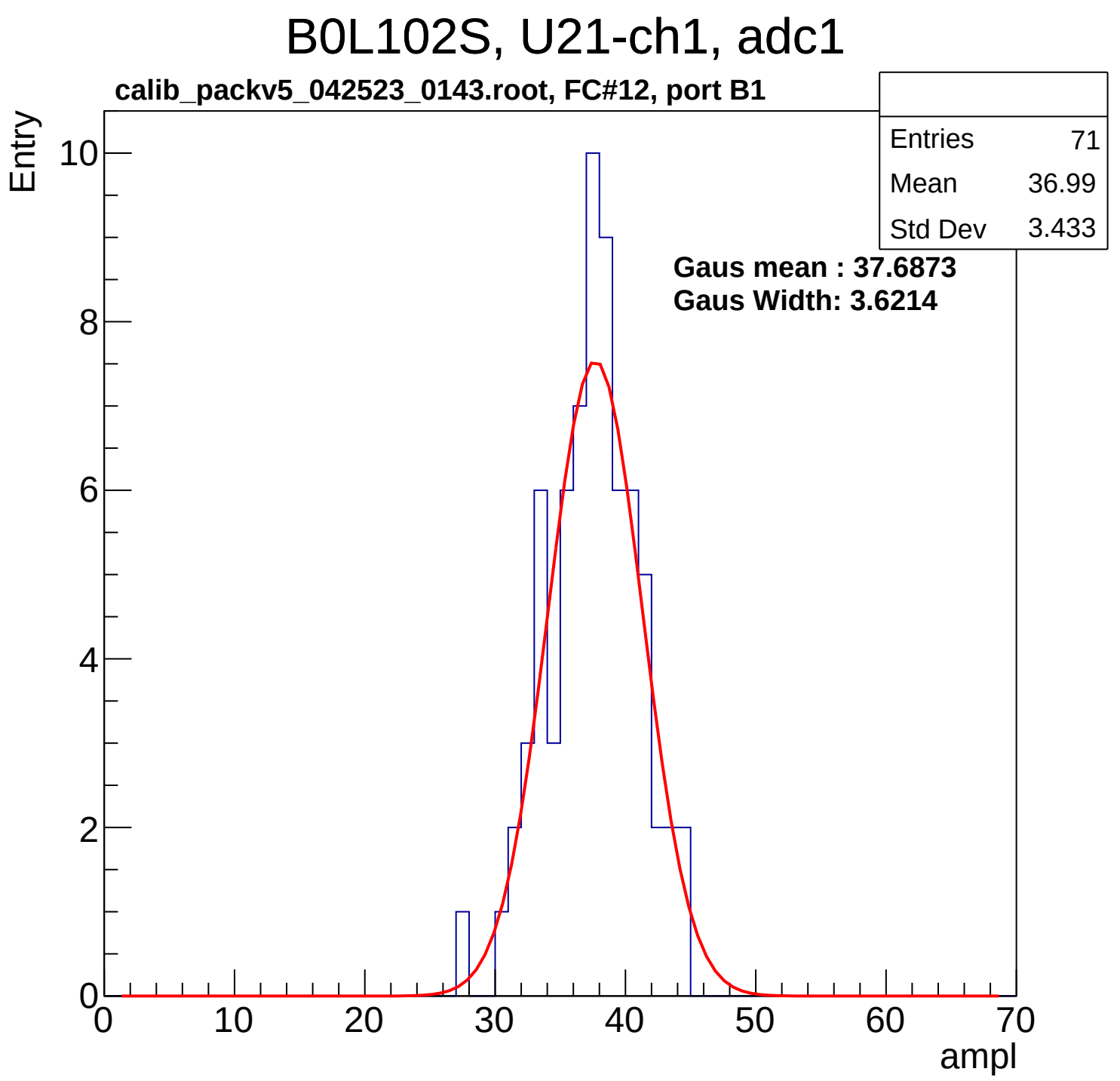
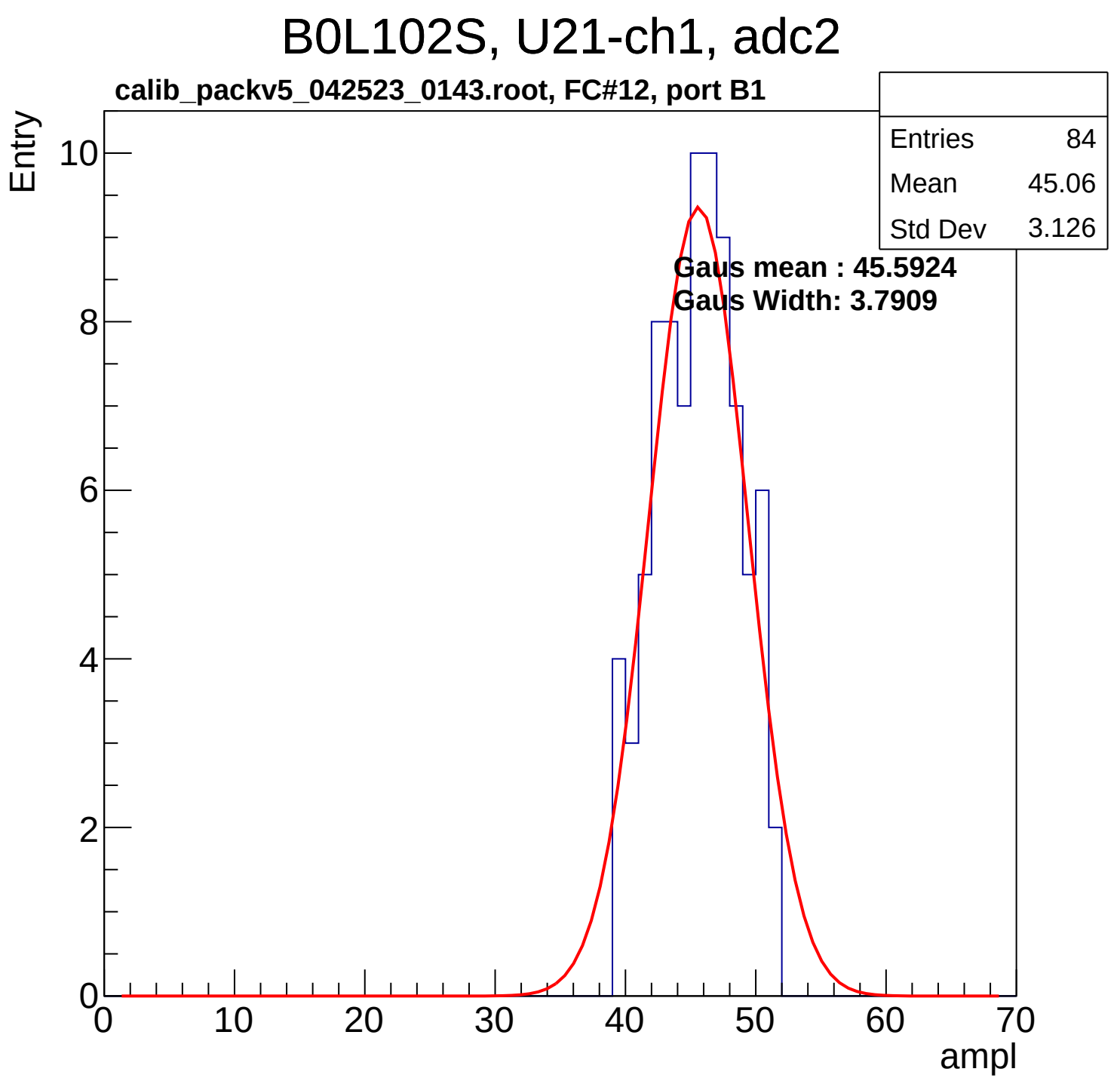
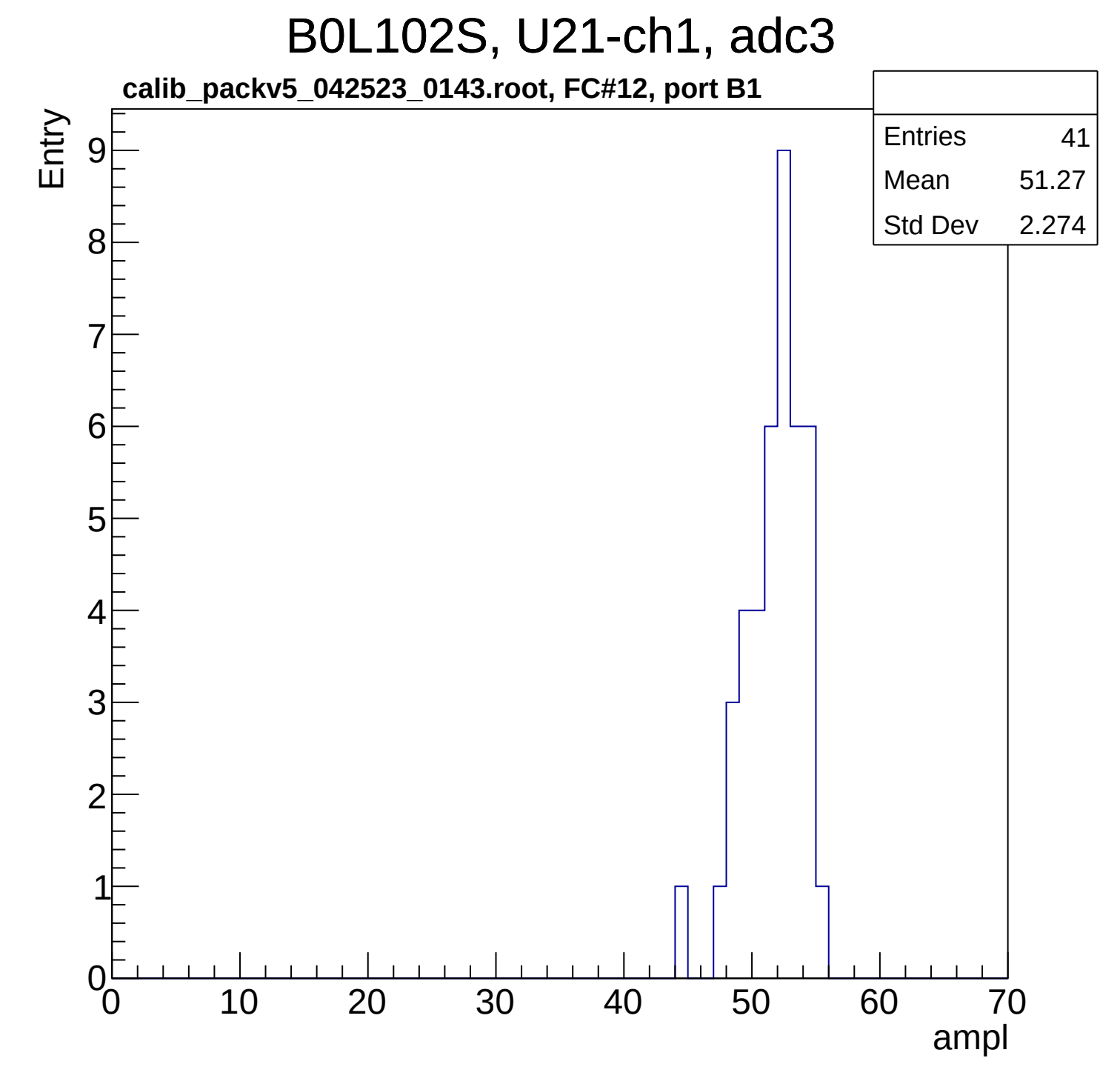
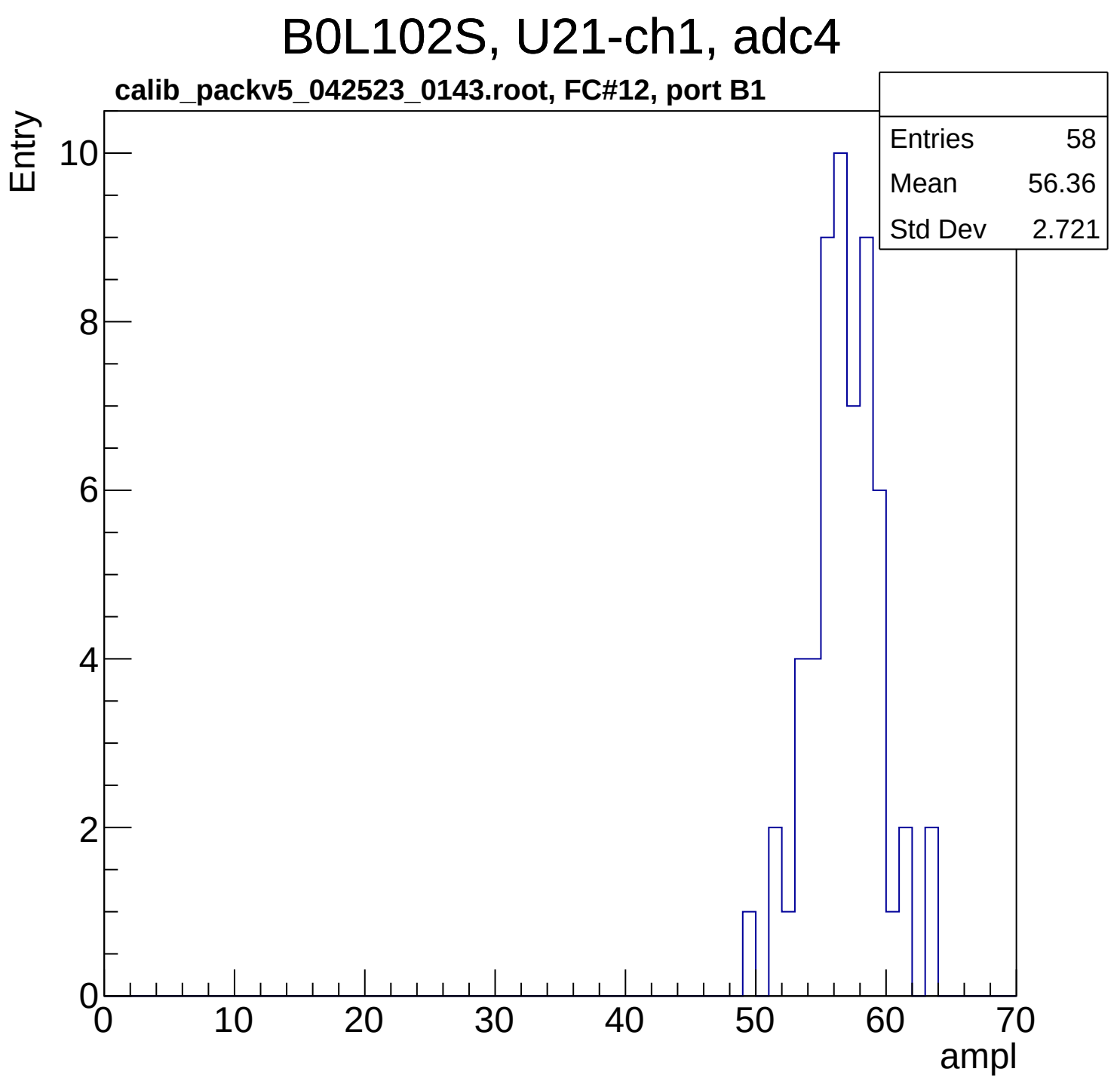
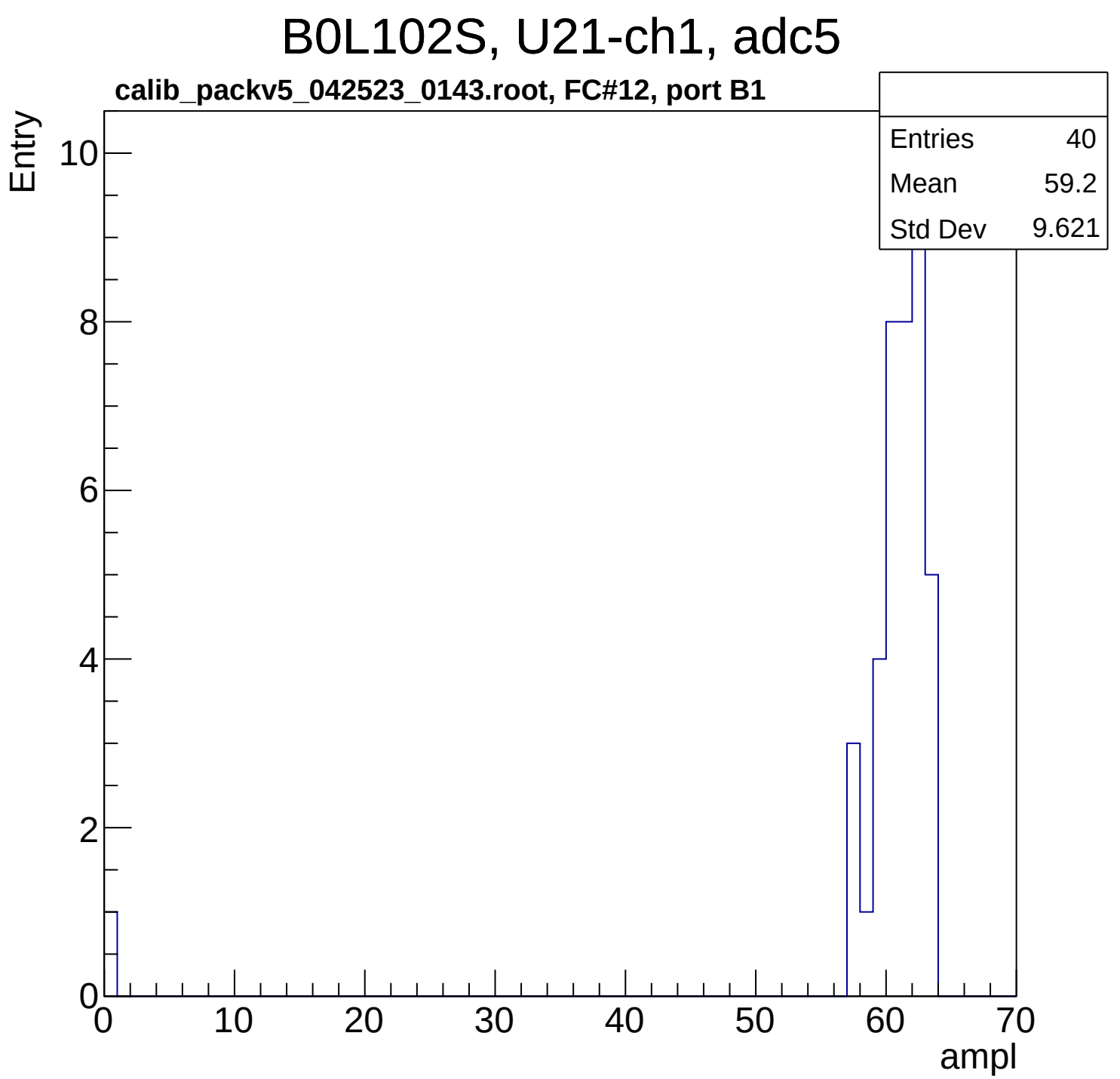

B0L102S, U21-ch1, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.85
Std Dev 5.036
Gaus mean : 30.6968
Gaus Width: 4.2949
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

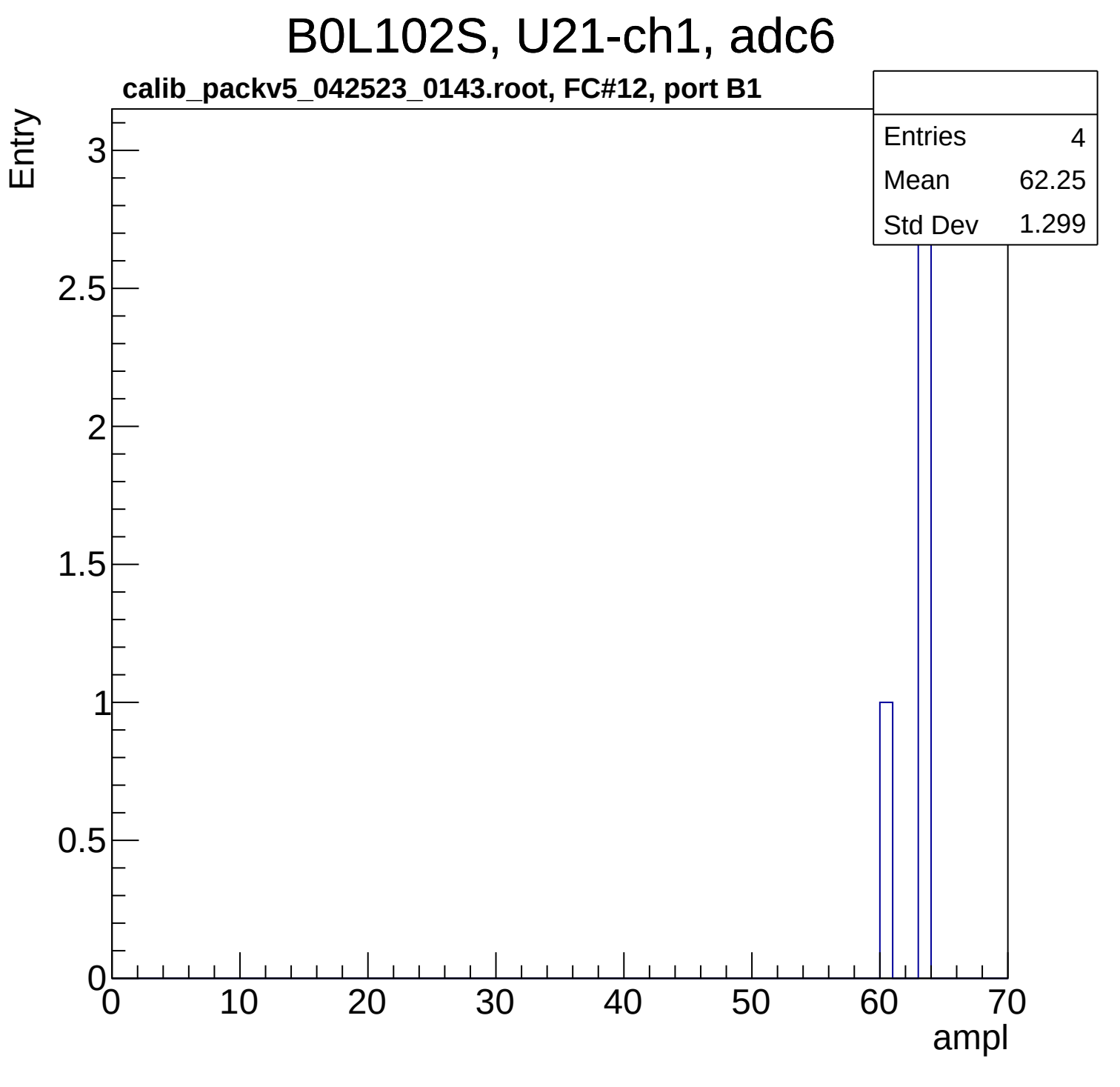

B0L102S, U21-ch1, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 36.99
Std Dev 3.433
Gaus mean : 37.6873
Gaus Width: 3.6214
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

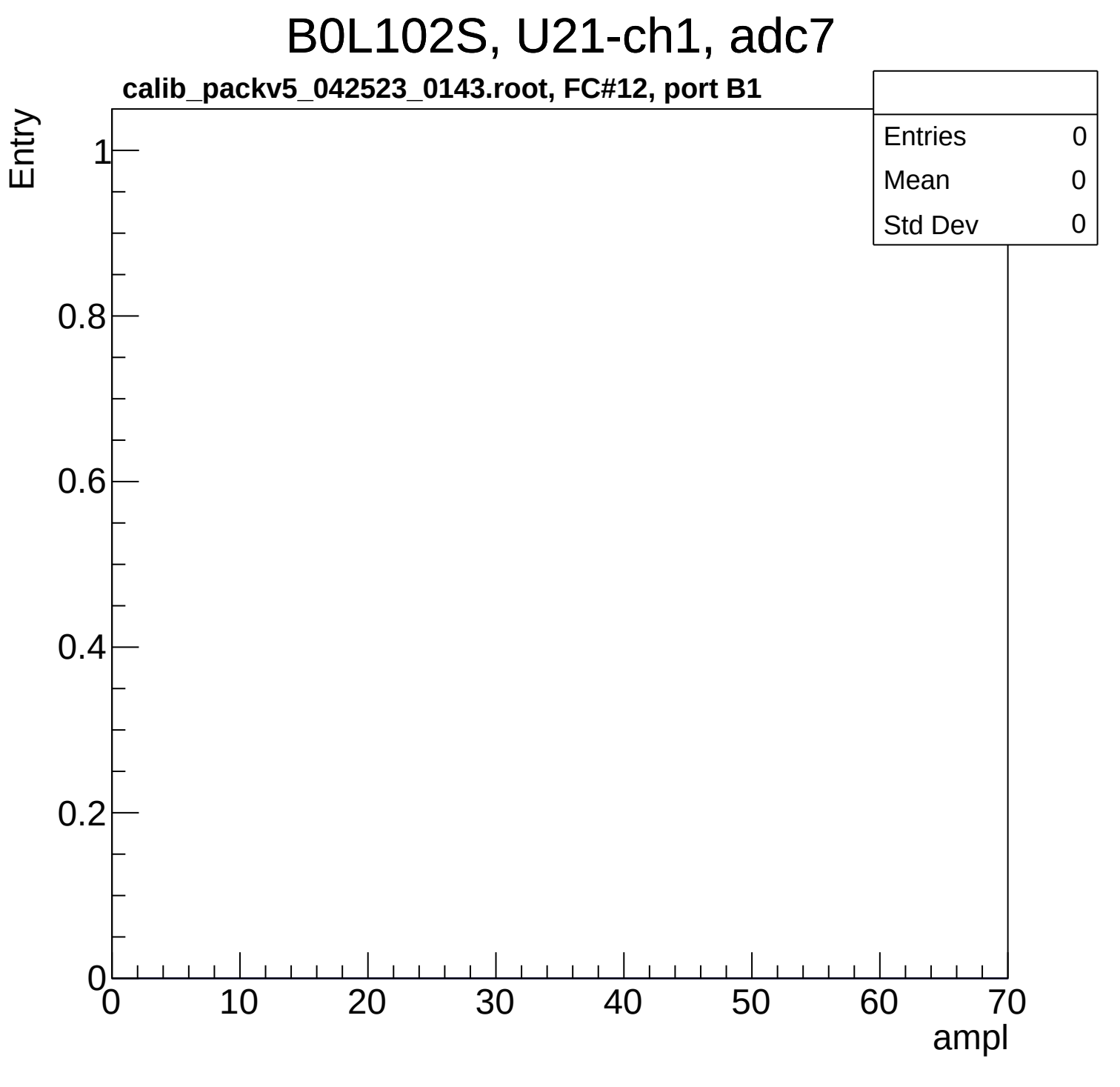

B0L102S, U21-ch1, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 45.06
Std Dev 3.126
Gaus mean : 45.5924
Gaus Width: 3.7909
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

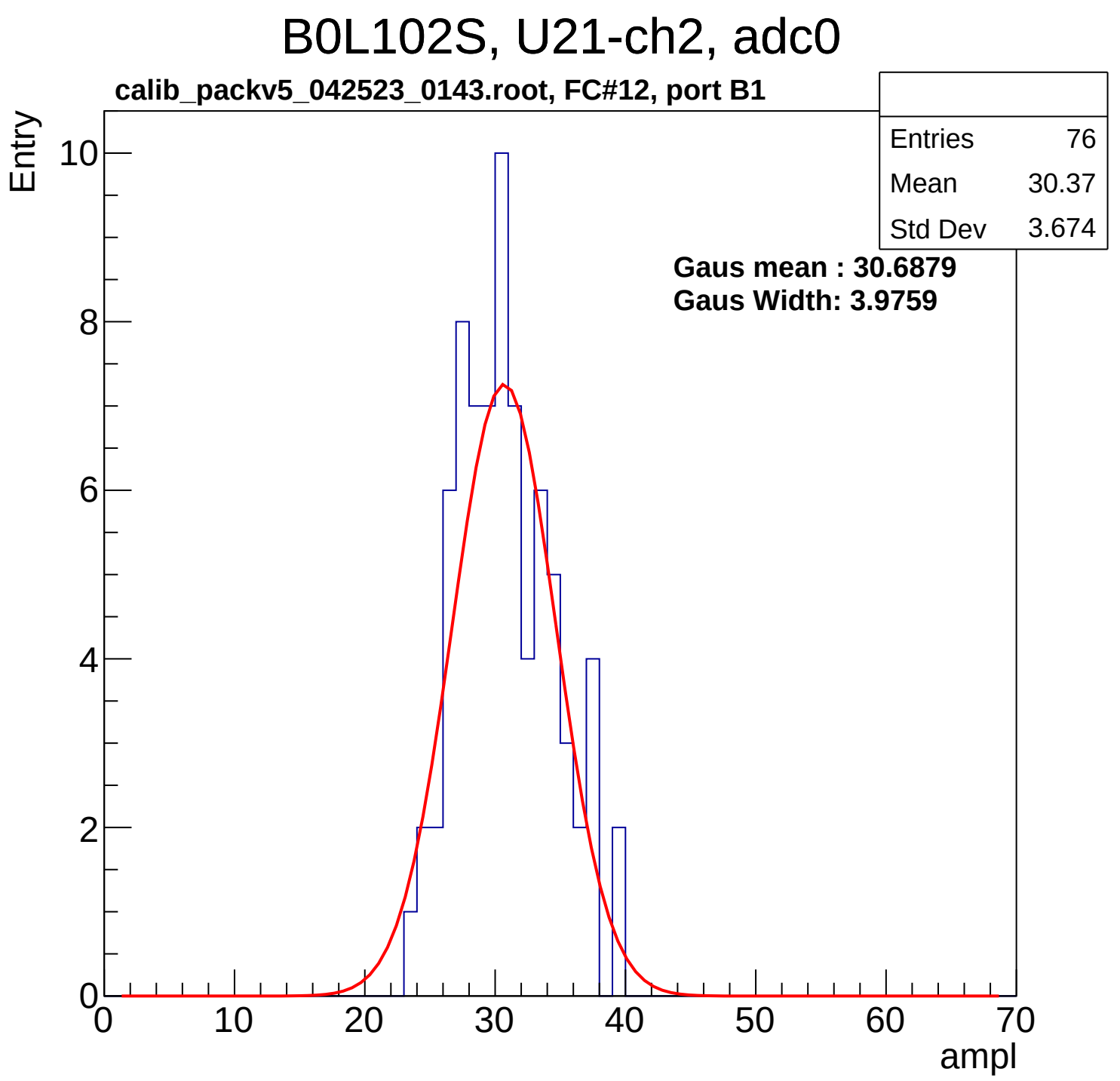

B0L102S, U21-ch1, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 51.27
Std Dev 2.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

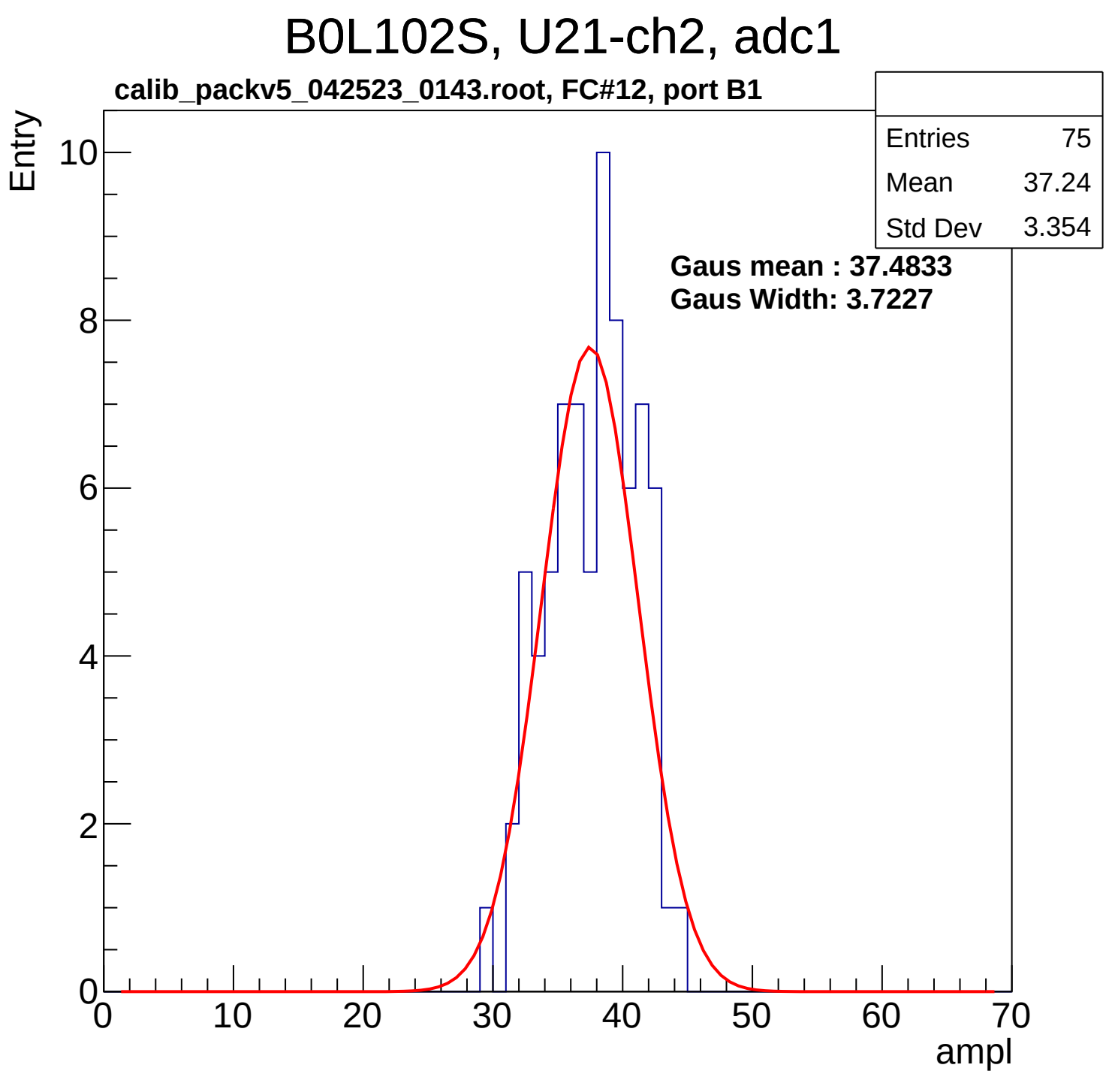

B0L102S, U21-ch1, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 56.36
Std Dev 2.721
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

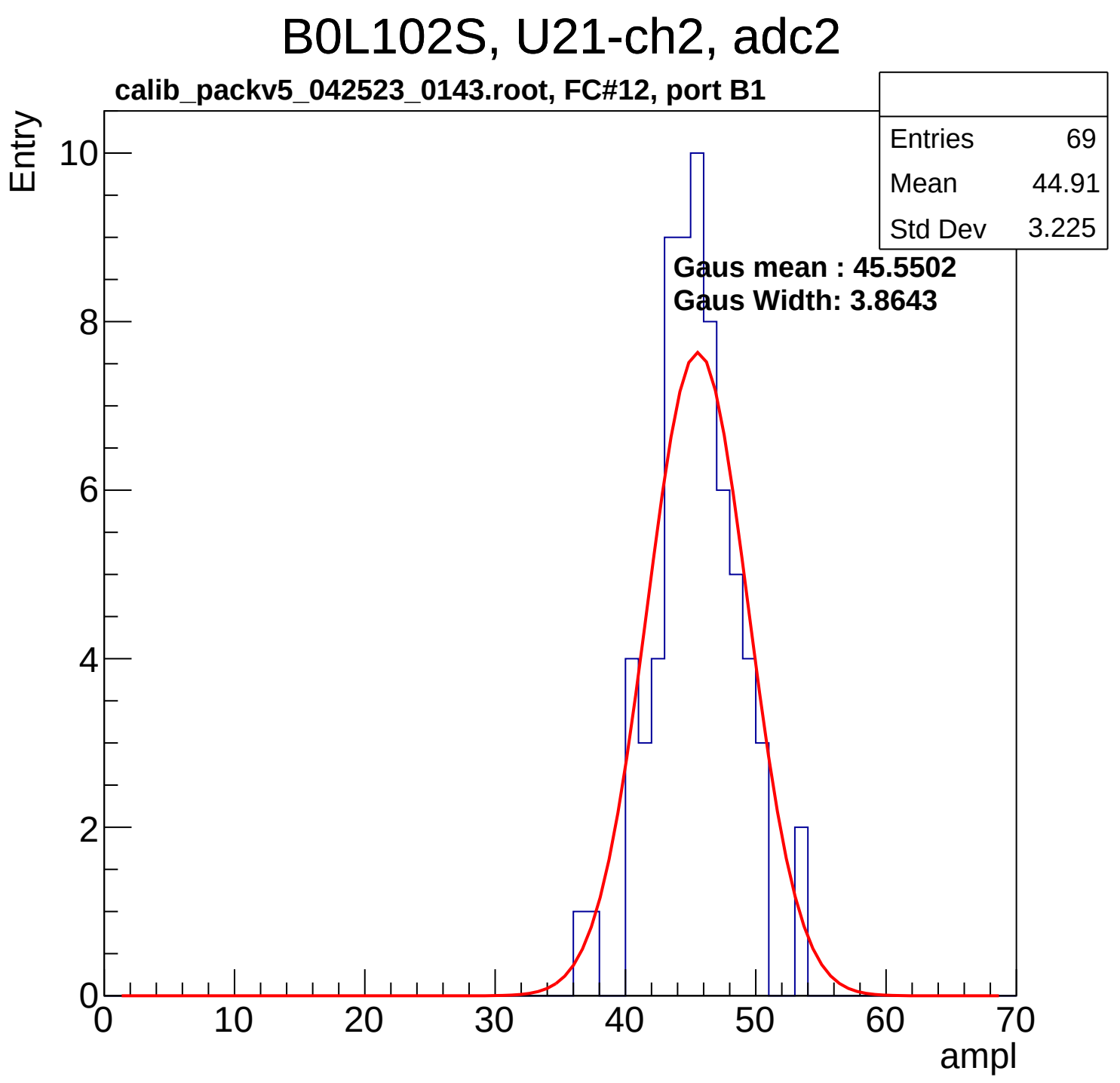

B0L102S, U21-ch1, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 59.2
Std Dev 9.621
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch1, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch1, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch2, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 30.37
Std Dev 3.674
Gaus mean : 30.6879
Gaus Width: 3.9759
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch2, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.24
Std Dev 3.354
Gaus mean : 37.4833
Gaus Width: 3.7227
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch2, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 44.91
Std Dev 3.225
Gaus mean : 45.5502
Gaus Width: 3.8643
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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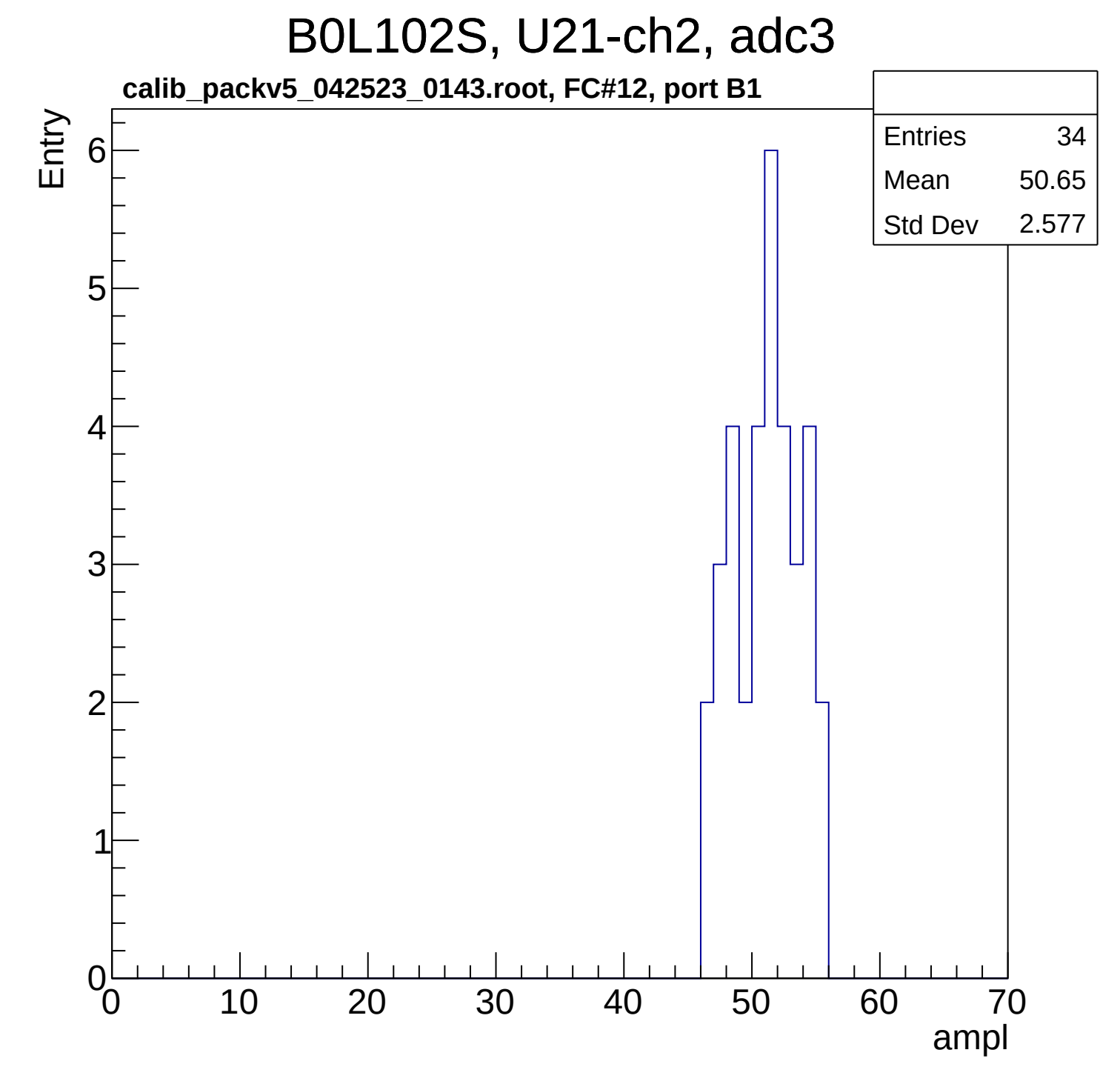
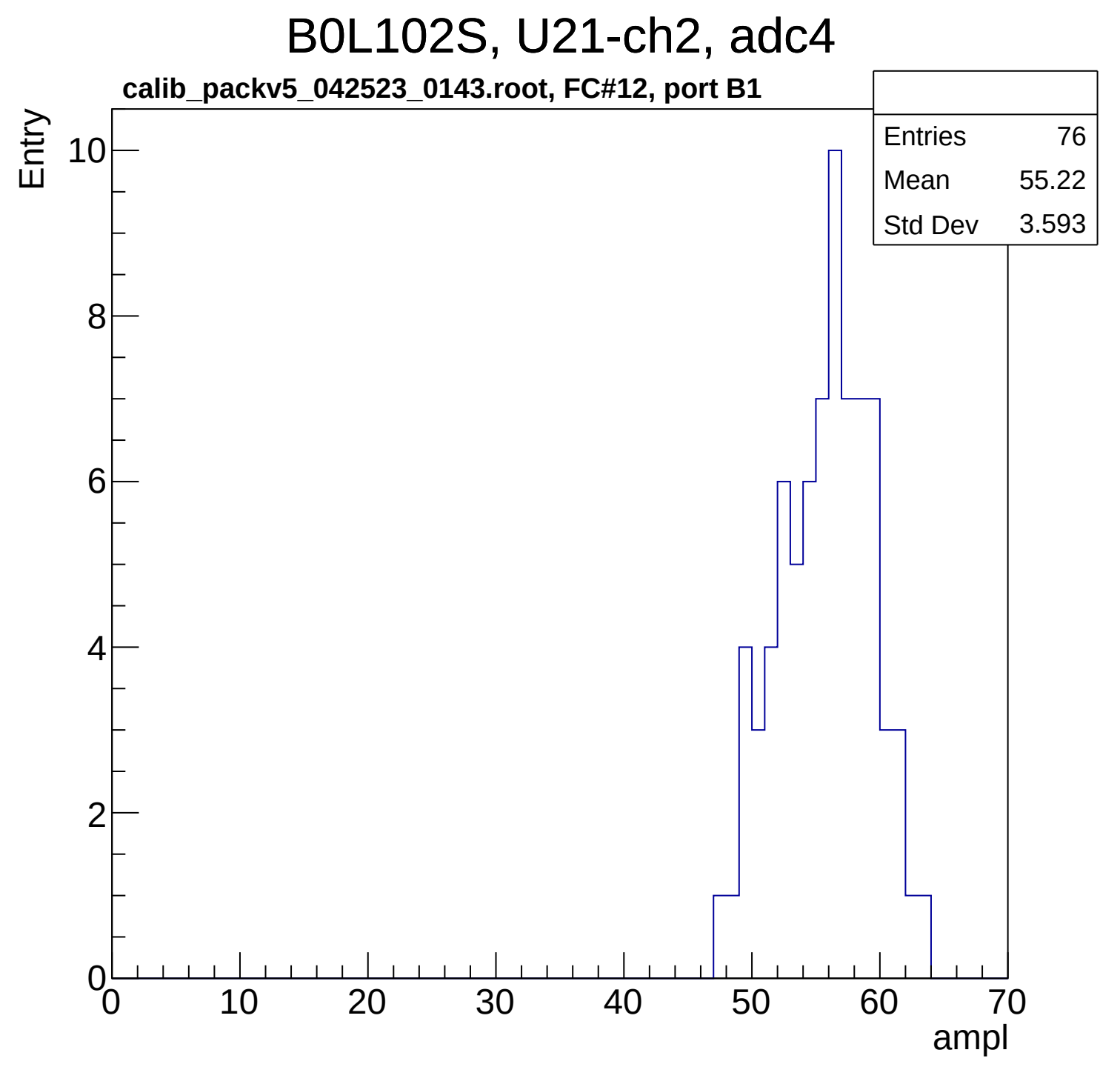

B0L102S, U21-ch2, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 50.65
Std Dev 2.577
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch2, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 55.22
Std Dev 3.593
Entry
ampl
0
2
4
6
8
10
0
10
20
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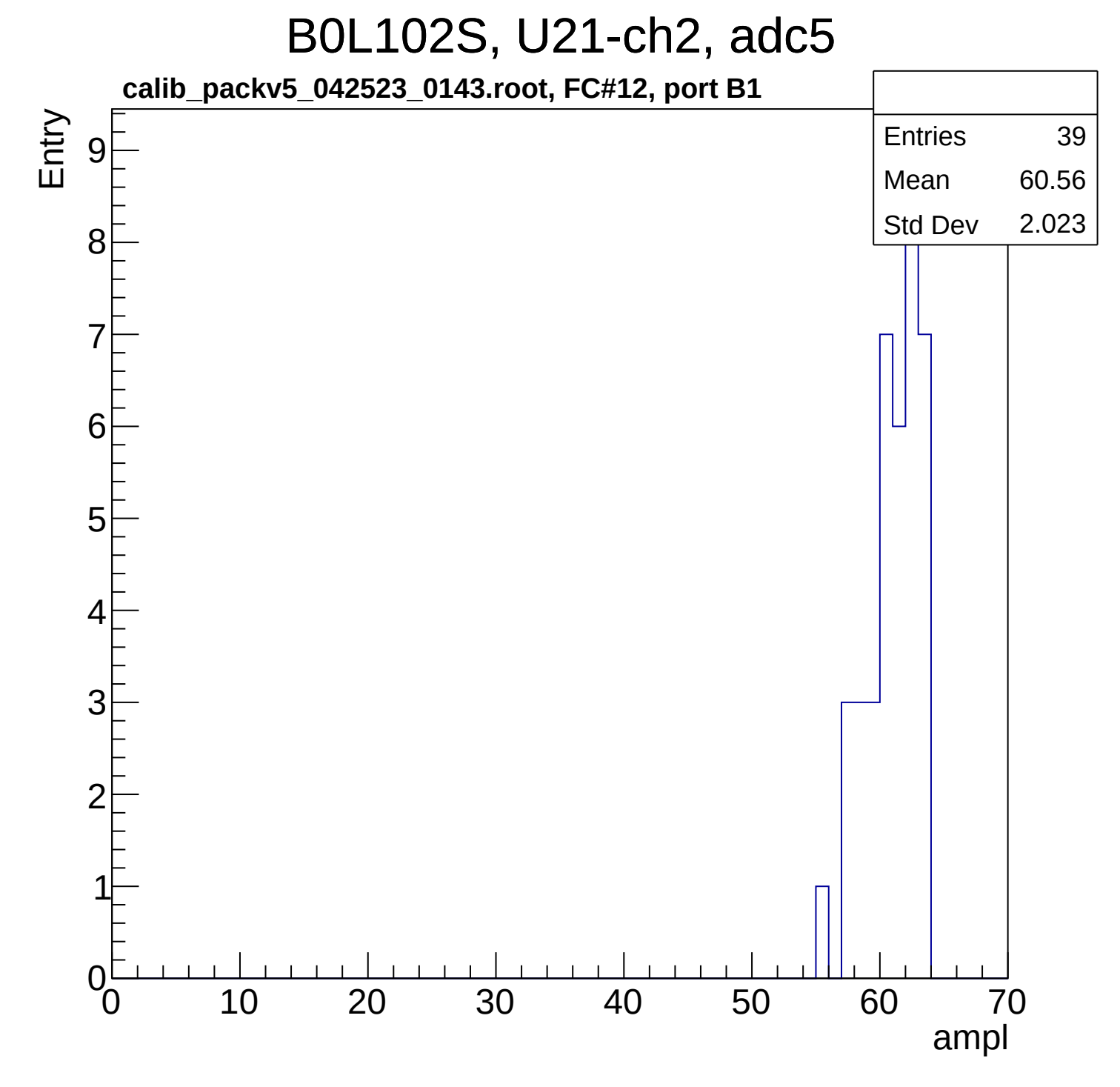
40
50
60
70


B0L102S, U21-ch2, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 60.56
Std Dev 2.023
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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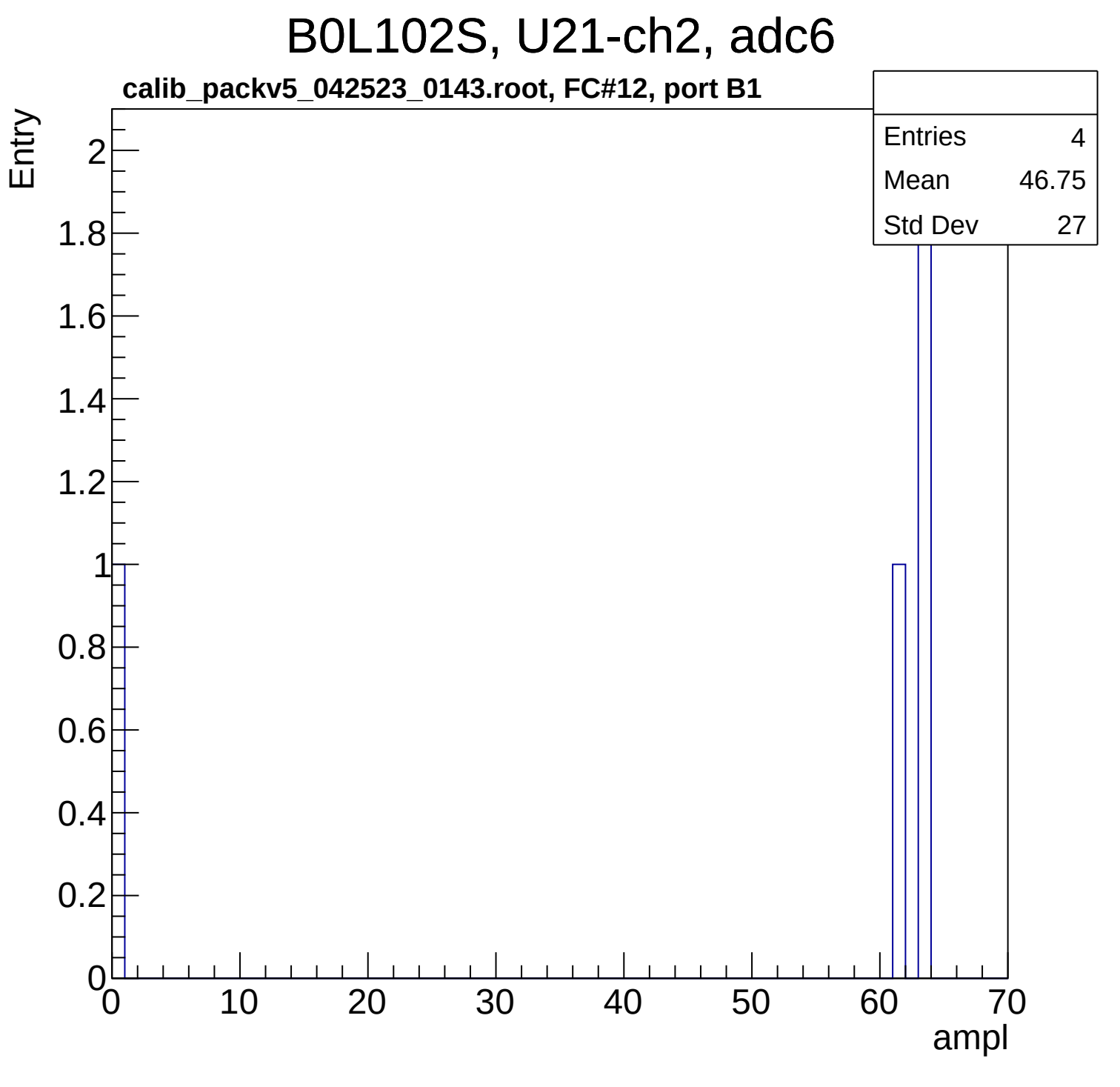
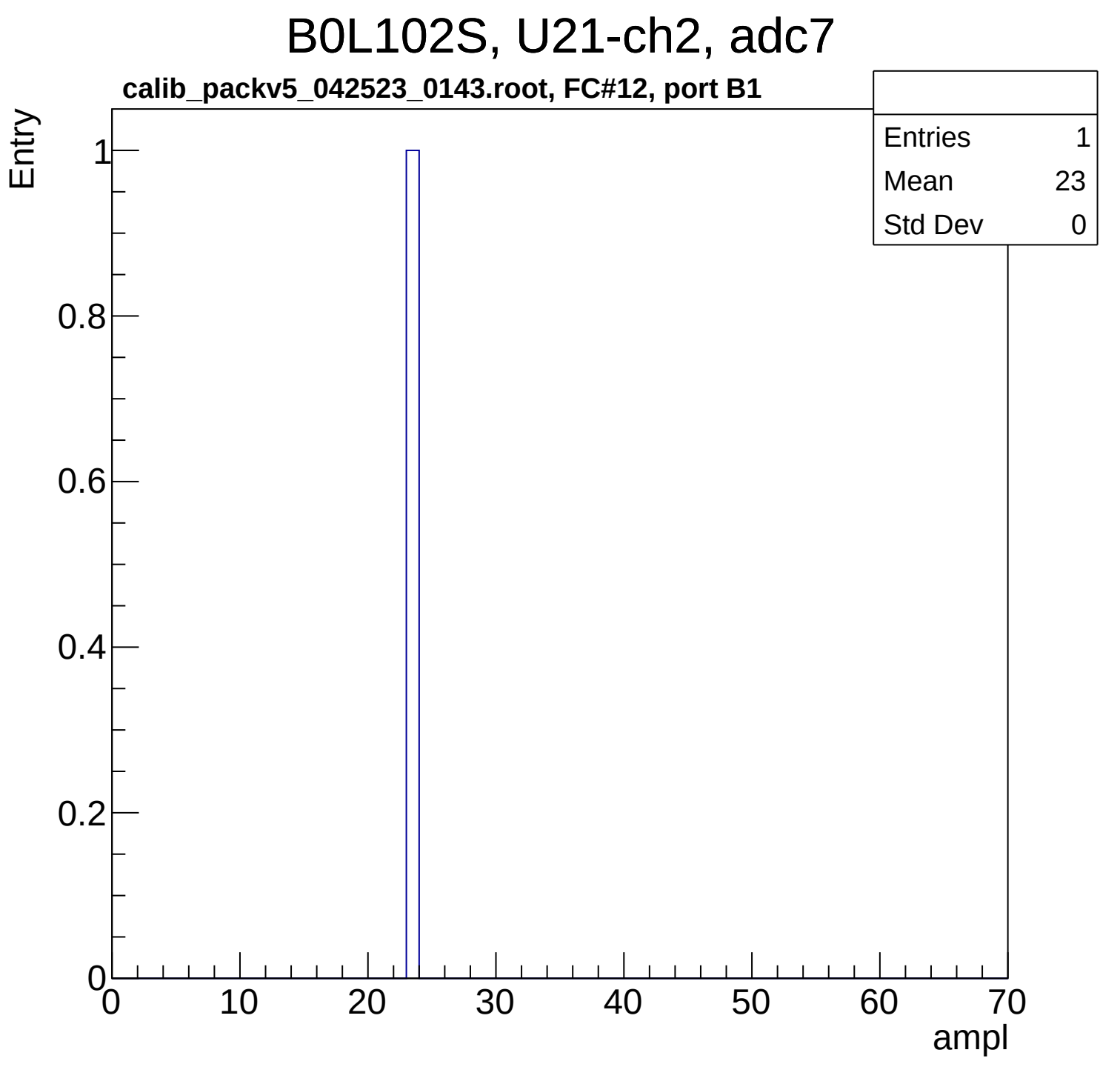

B0L102S, U21-ch2, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch2, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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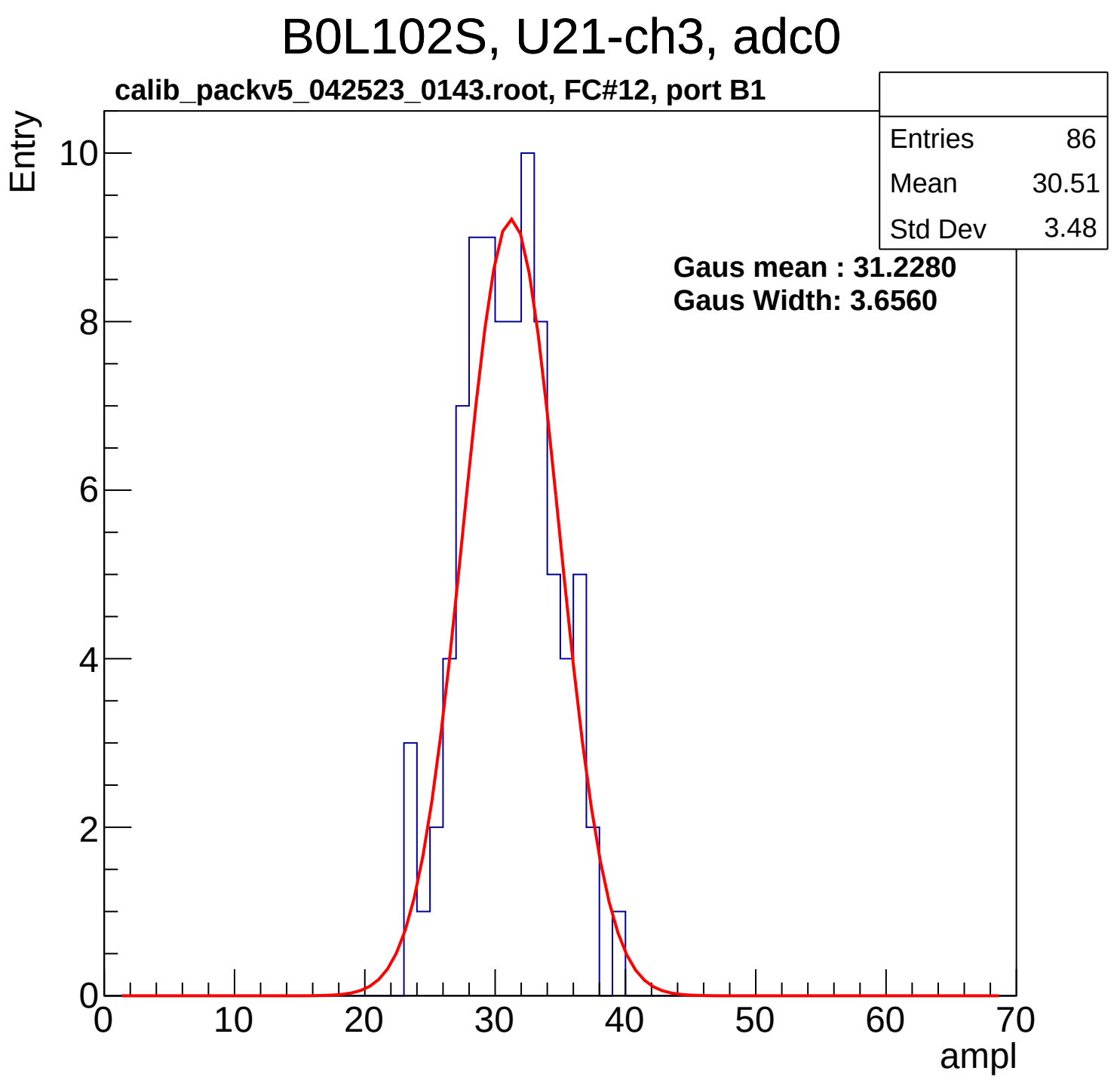
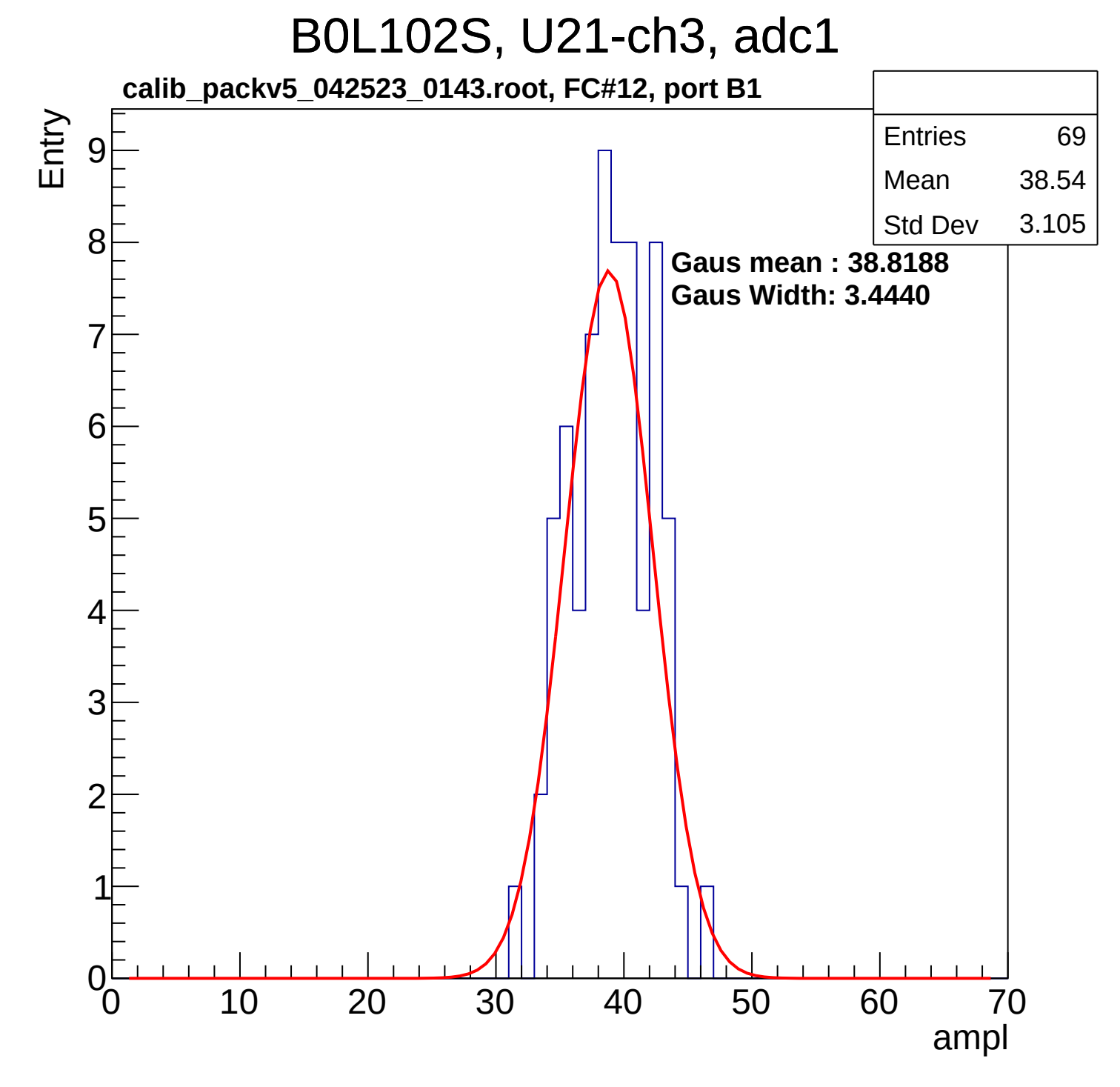
50
60
70
ampl


B0L102S, U21-ch3, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 30.51
Std Dev 3.48
Gaus mean : 31.2280
Gaus Width: 3.6560
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch3, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 38.54
Std Dev 3.105
Gaus mean : 38.8188
Gaus Width: 3.4440
Entry
ampl
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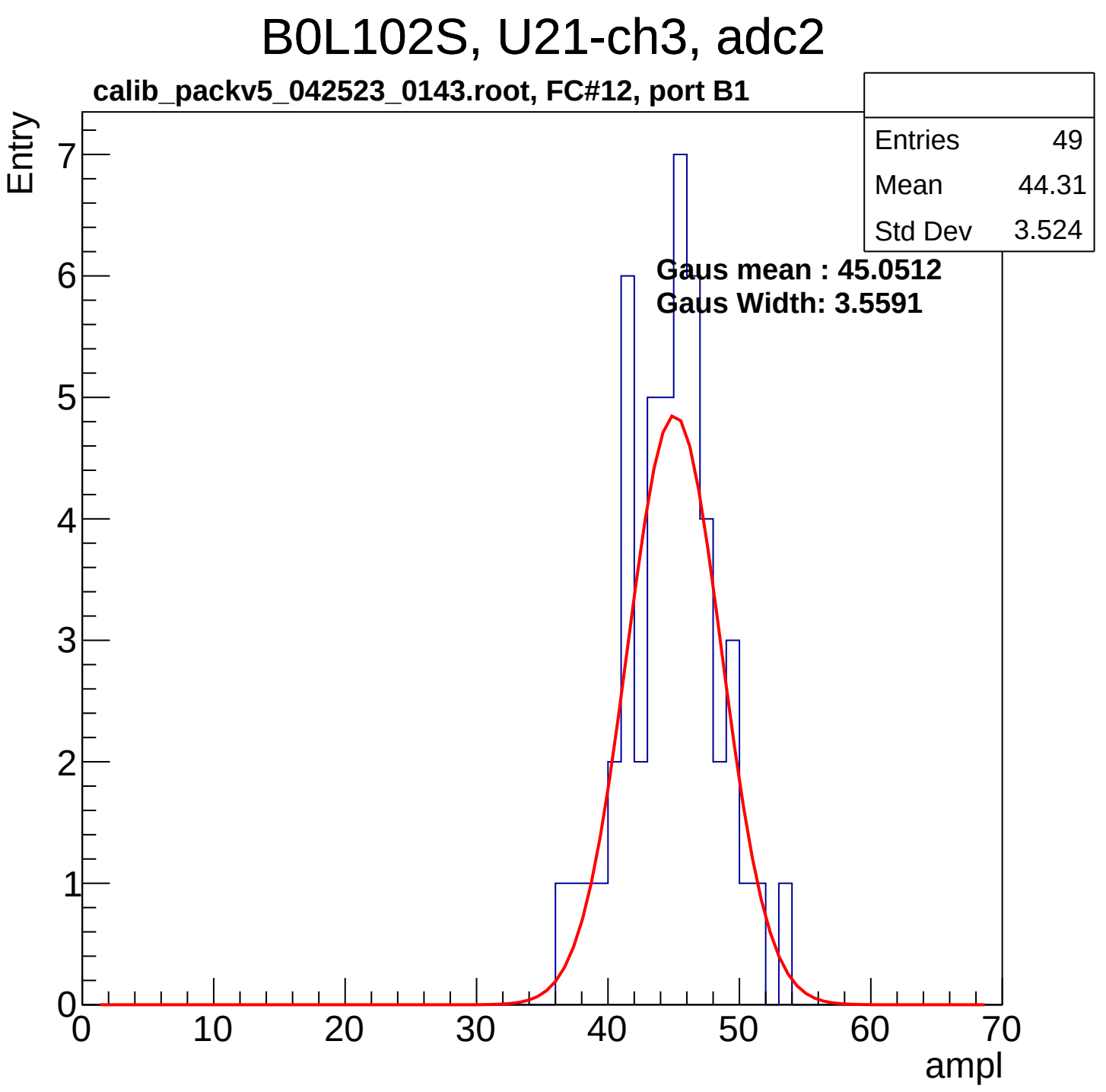
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch3, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 44.31
Std Dev 3.524
Gaus mean : 45.0512
Gaus Width: 3.5591
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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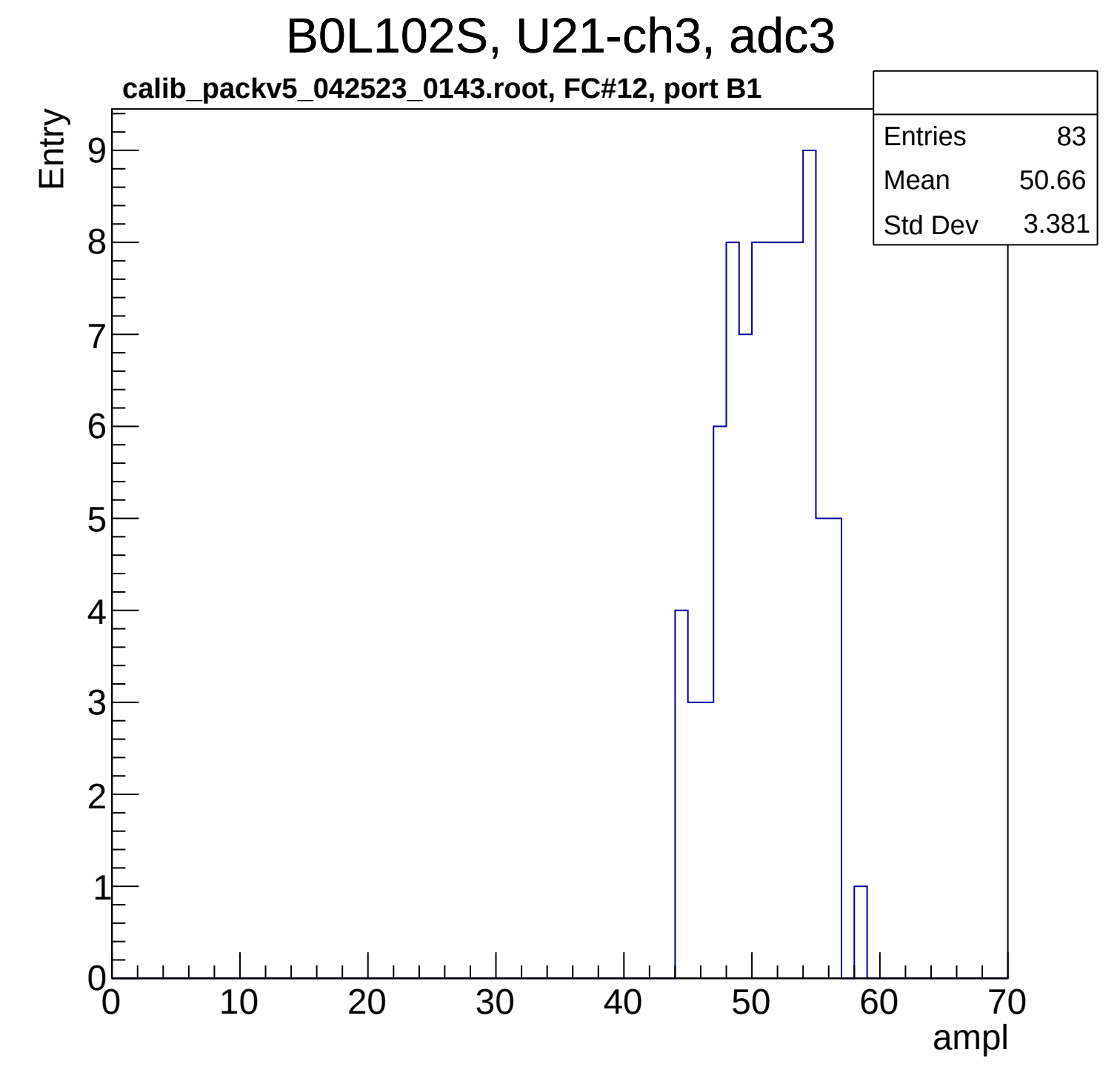
B0L102S, U21-ch3, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 50.66
Std Dev 3.381
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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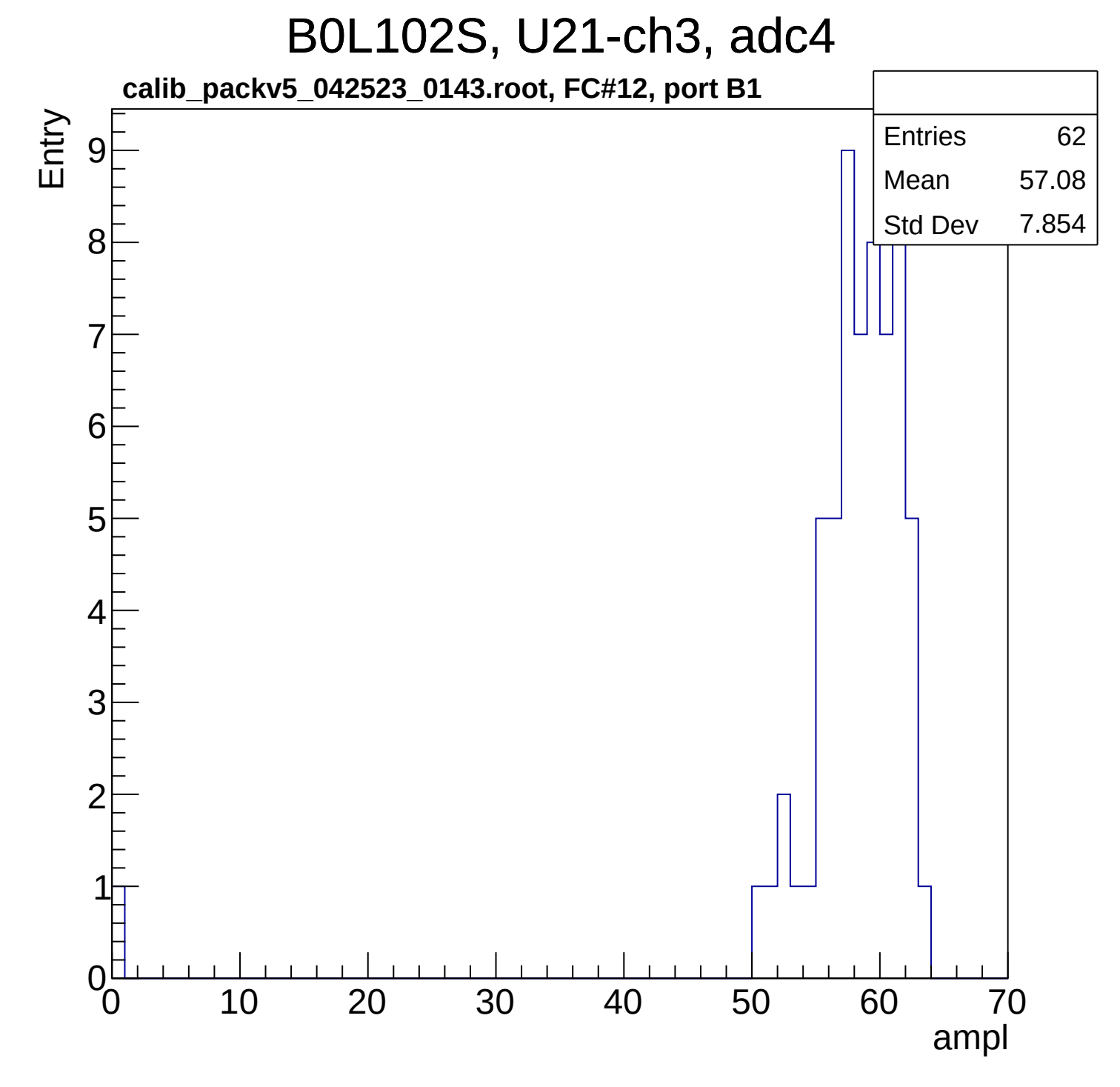
B0L102S, U21-ch3, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 57.08
Std Dev 7.854
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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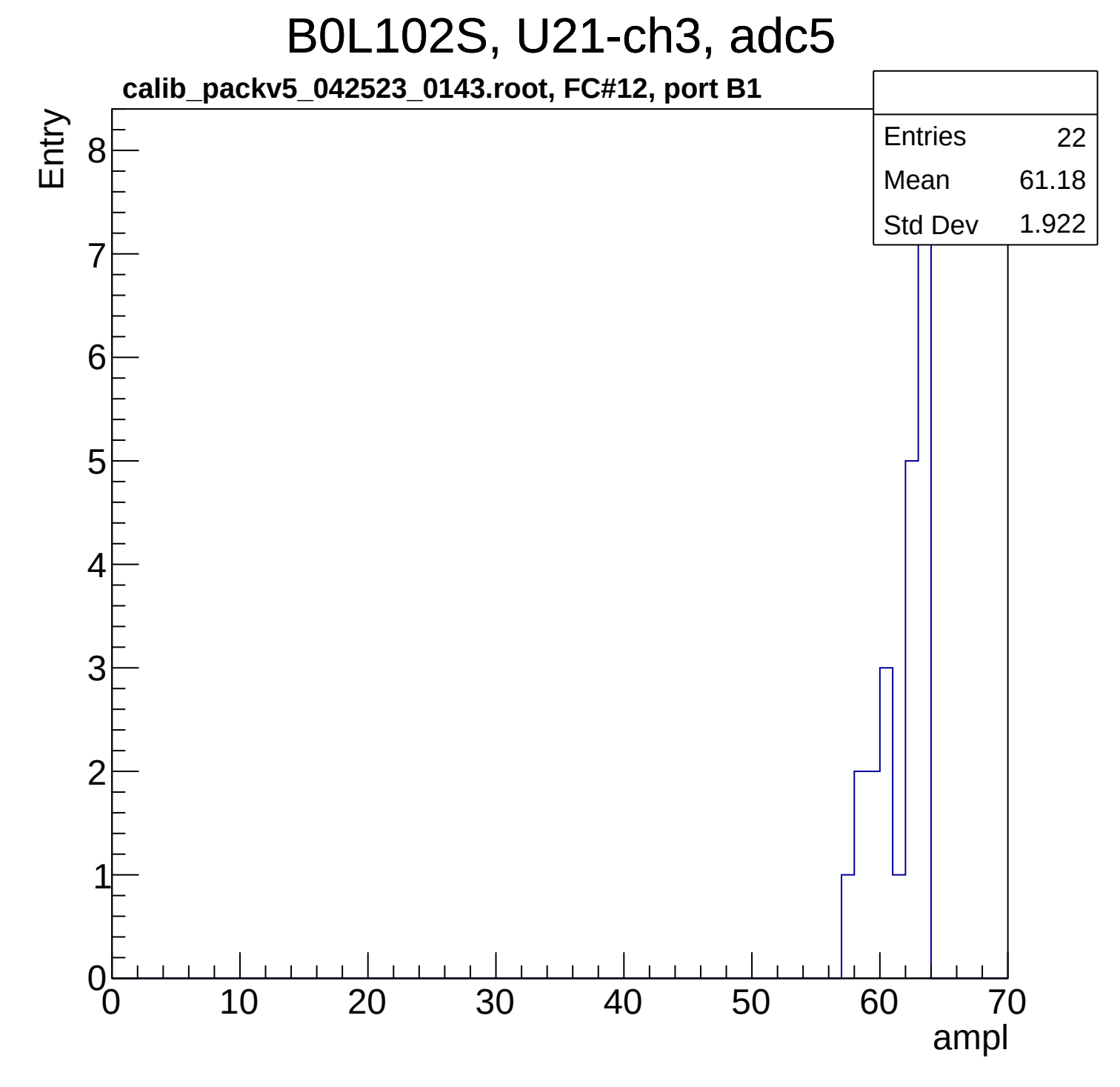
B0L102S, U21-ch3, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 22
Mean 61.18
Std Dev 1.922
Entry
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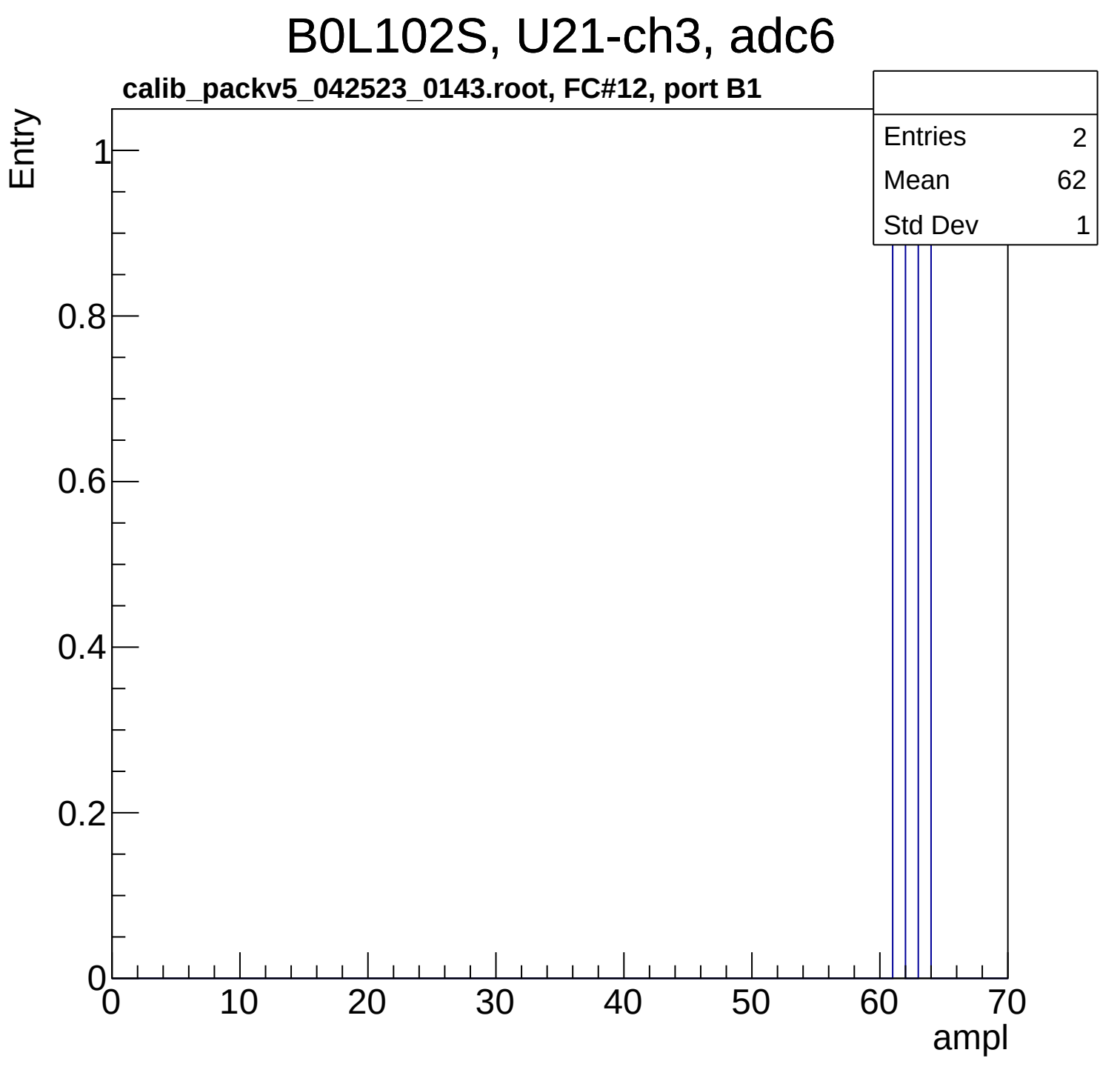
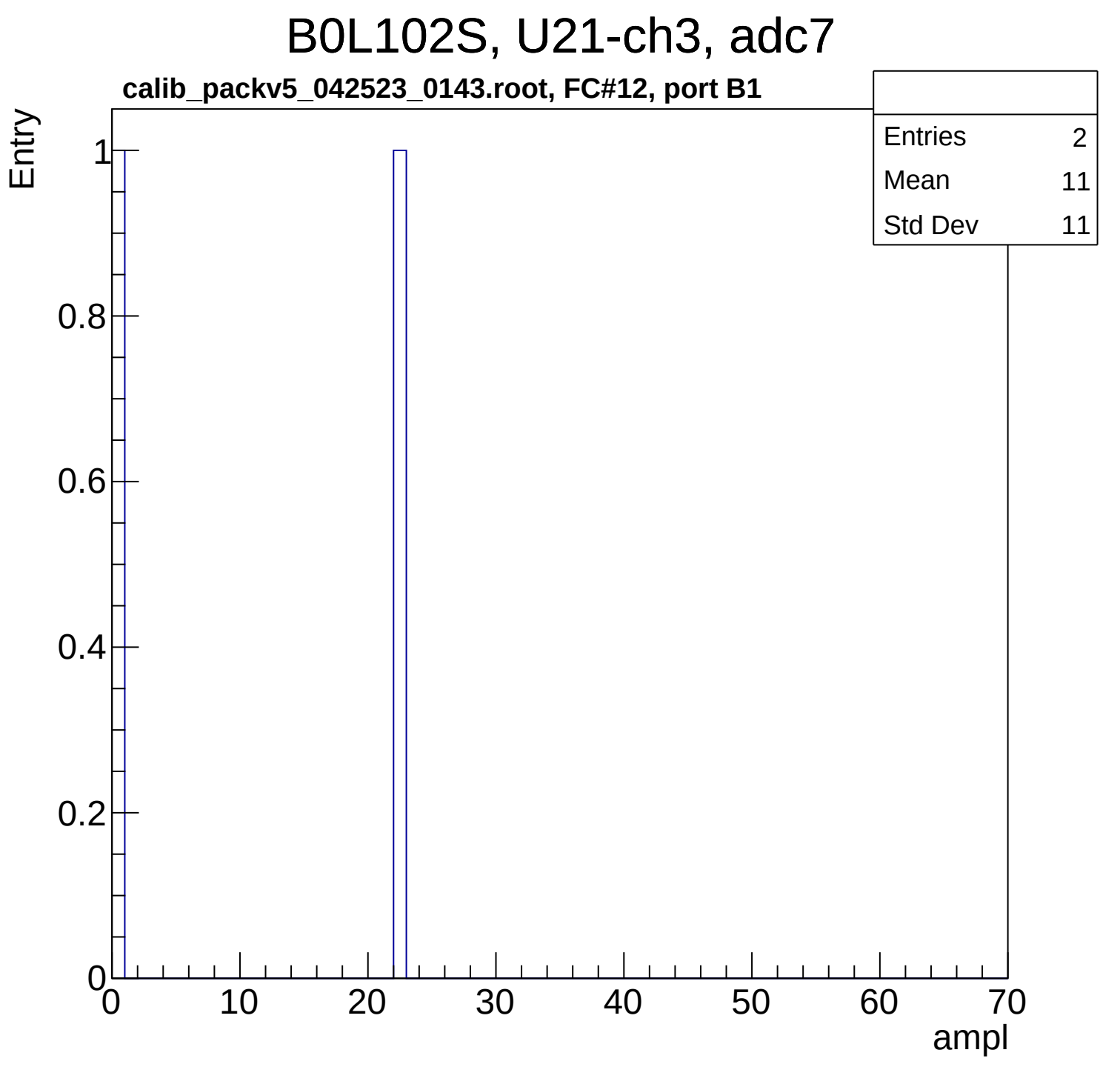
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch3, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch3, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
0.2
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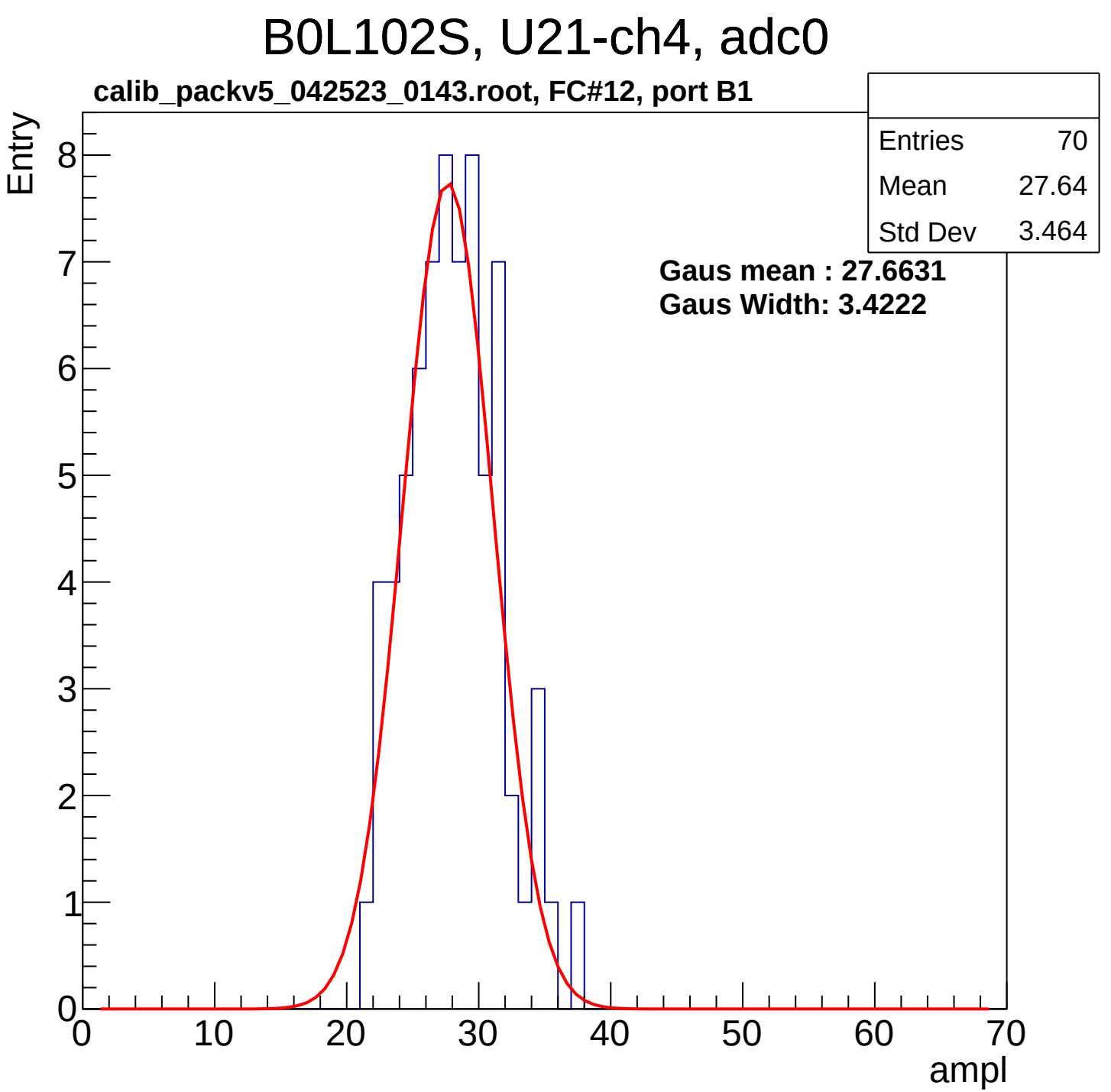
ampl


B0L102S, U21-ch4, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 27.64
Std Dev 3.464
Gaus mean : 27.6631
Gaus Width: 3.4222
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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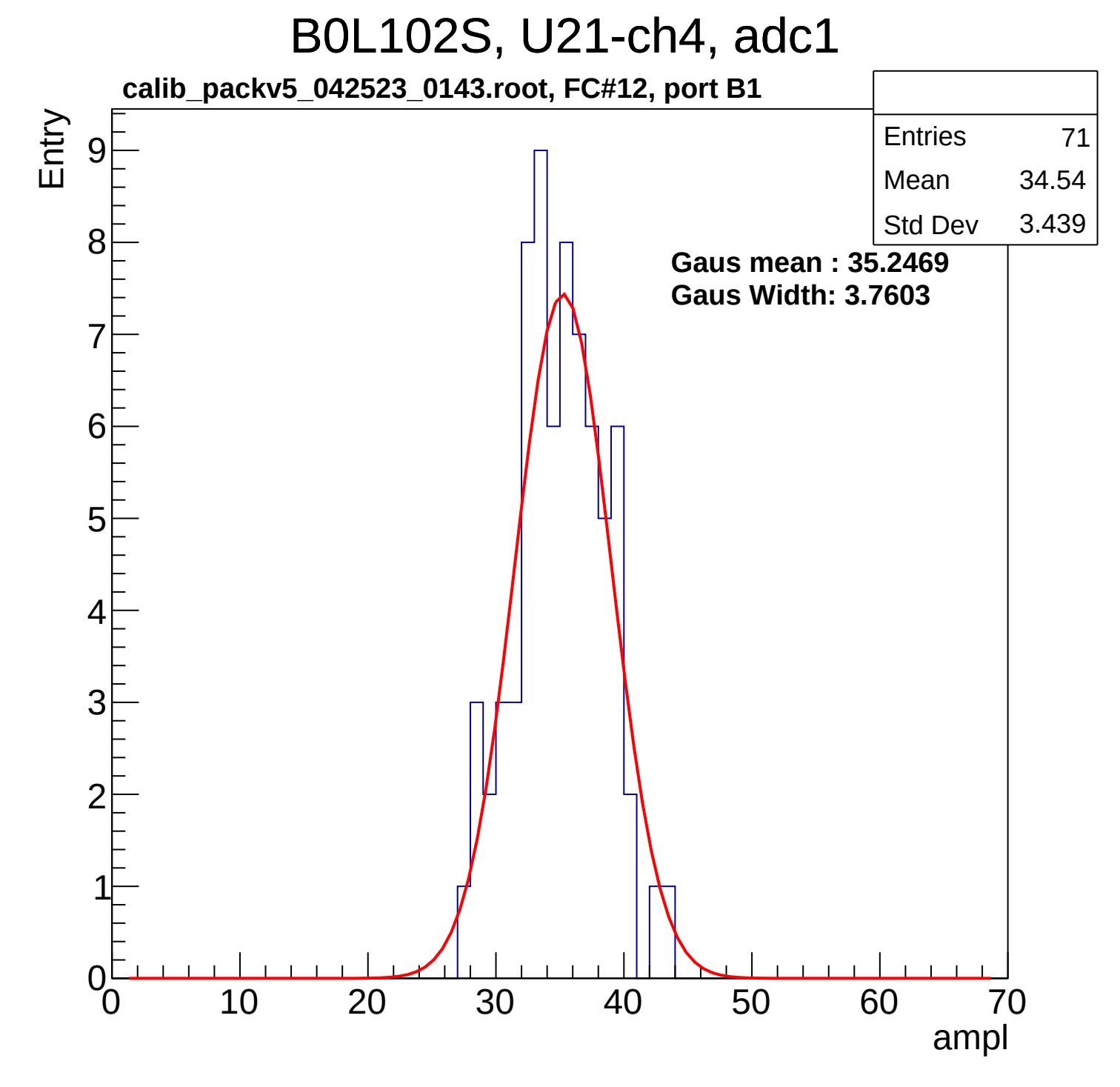
B0L102S, U21-ch4, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 34.54
Std Dev 3.439
Gaus mean : 35.2469
Gaus Width: 3.7603
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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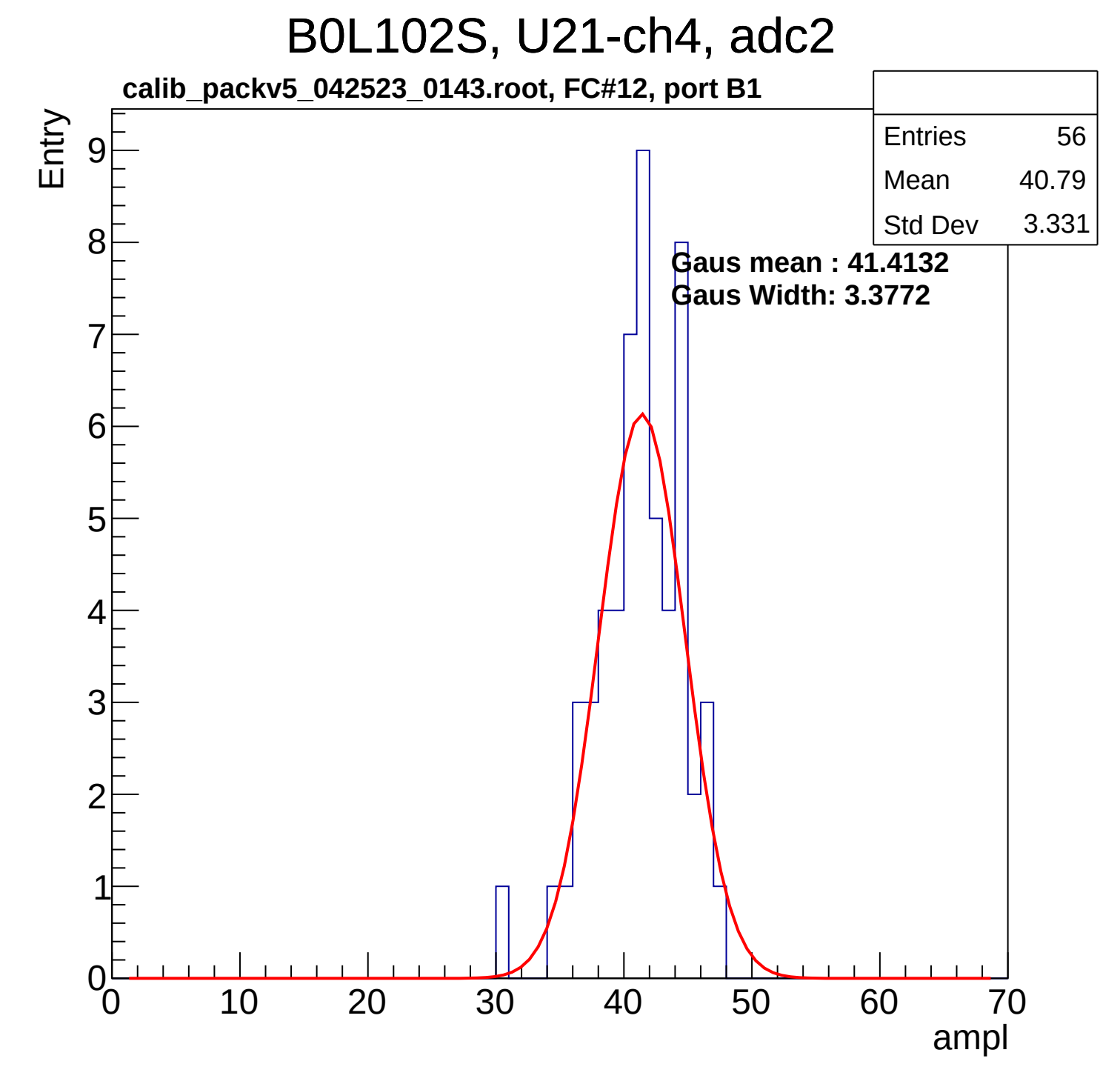
B0L102S, U21-ch4, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 40.79
Std Dev 3.331
Gaus mean : 41.4132
Gaus Width: 3.3772
Entry
ampl
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3
4
5
6
7
8
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0
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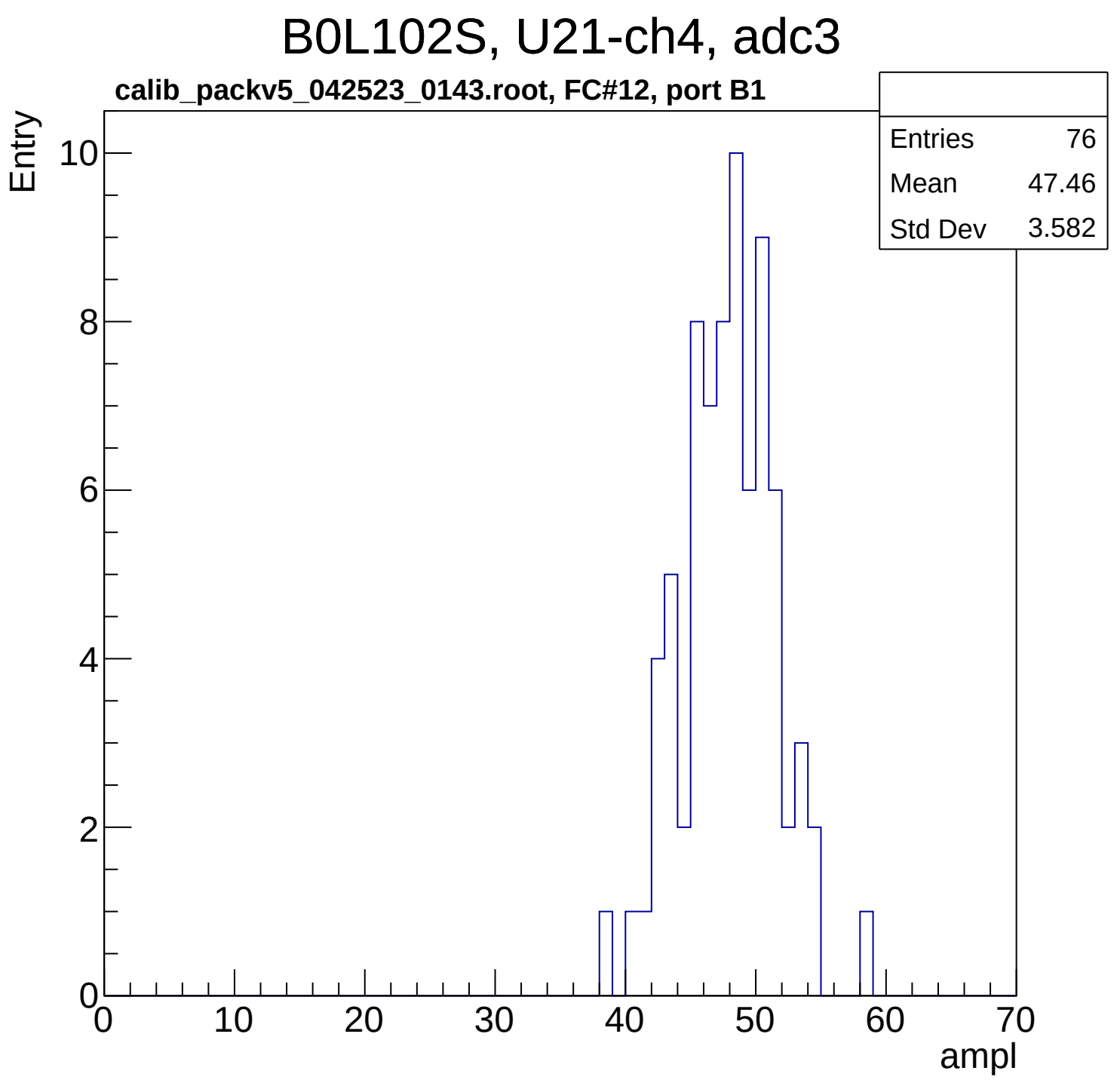
B0L102S, U21-ch4, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 47.46
Std Dev 3.582
Entry
0
2
4
6
8
10
0
10
20
30
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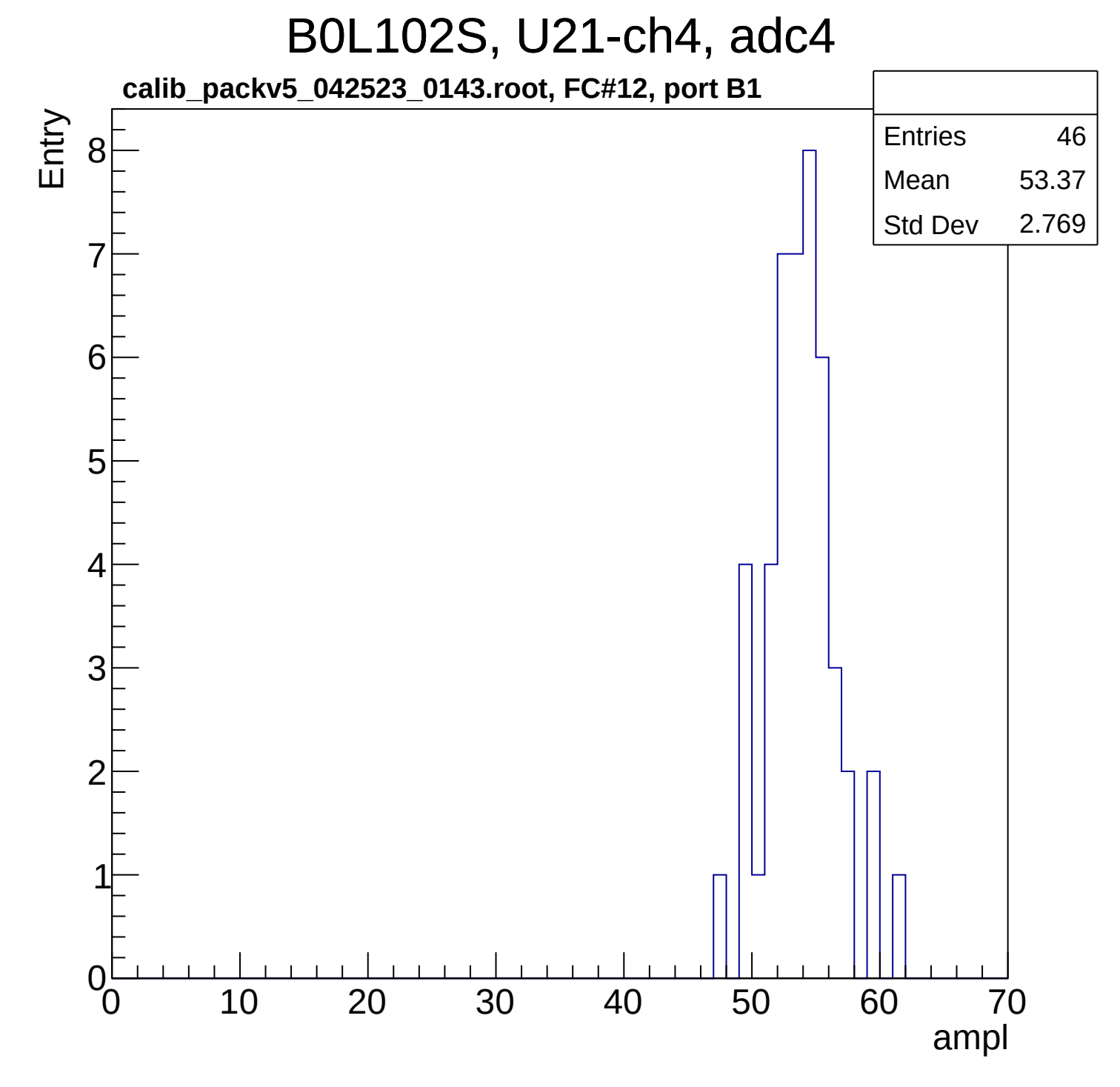
50
60
70
ampl


B0L102S, U21-ch4, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 53.37
Std Dev 2.769
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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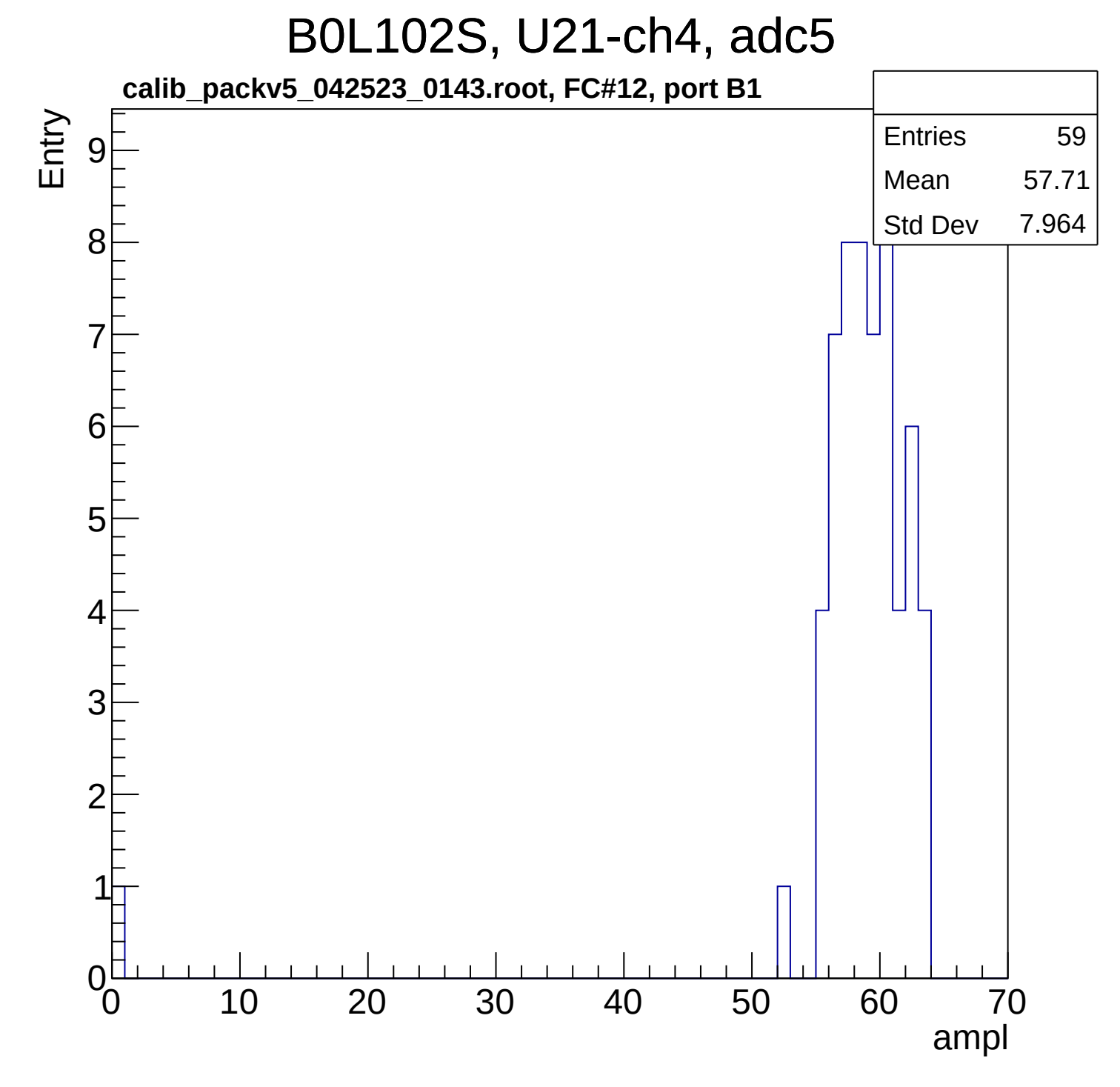
B0L102S, U21-ch4, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 57.71
Std Dev 7.964
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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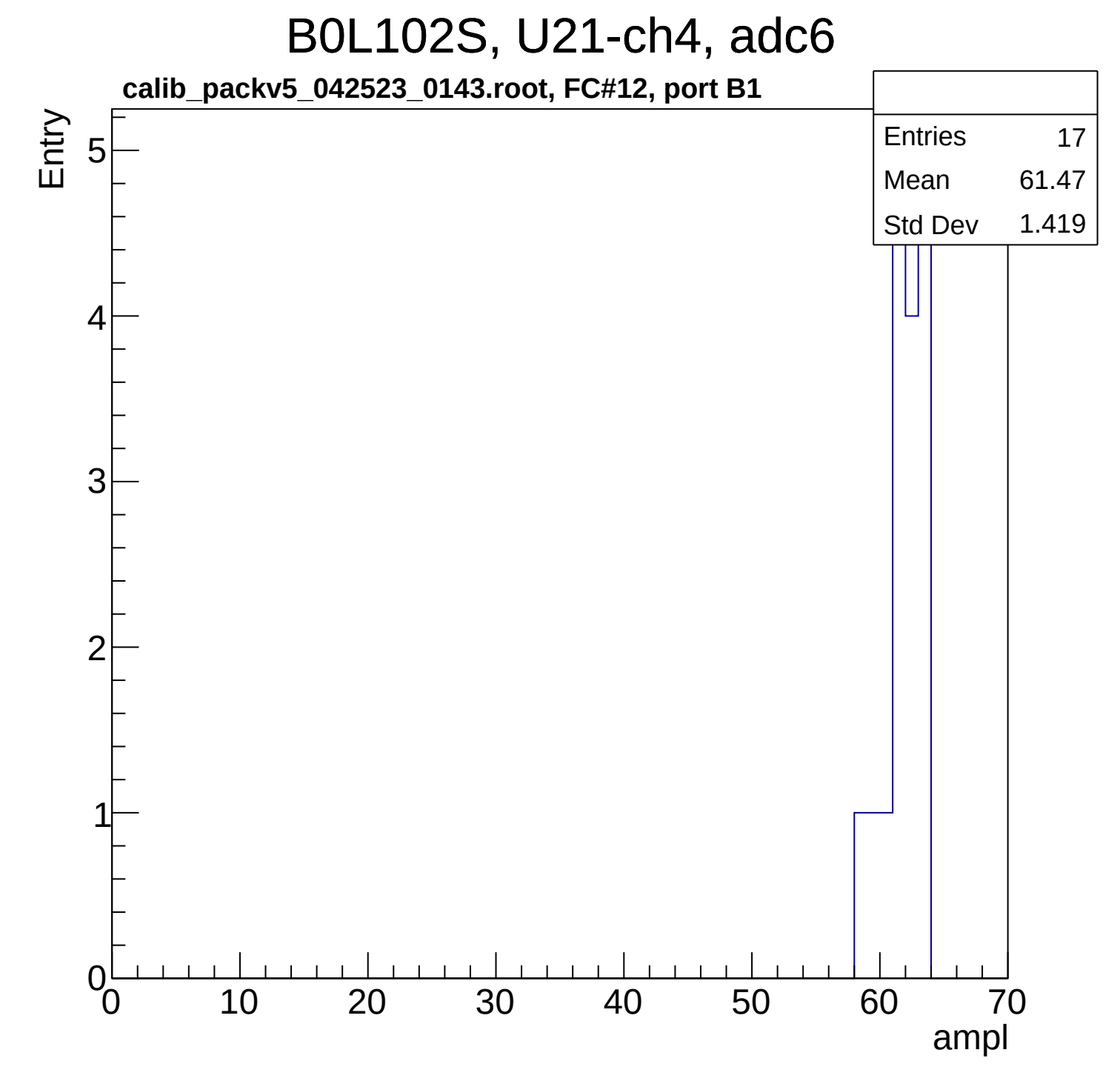
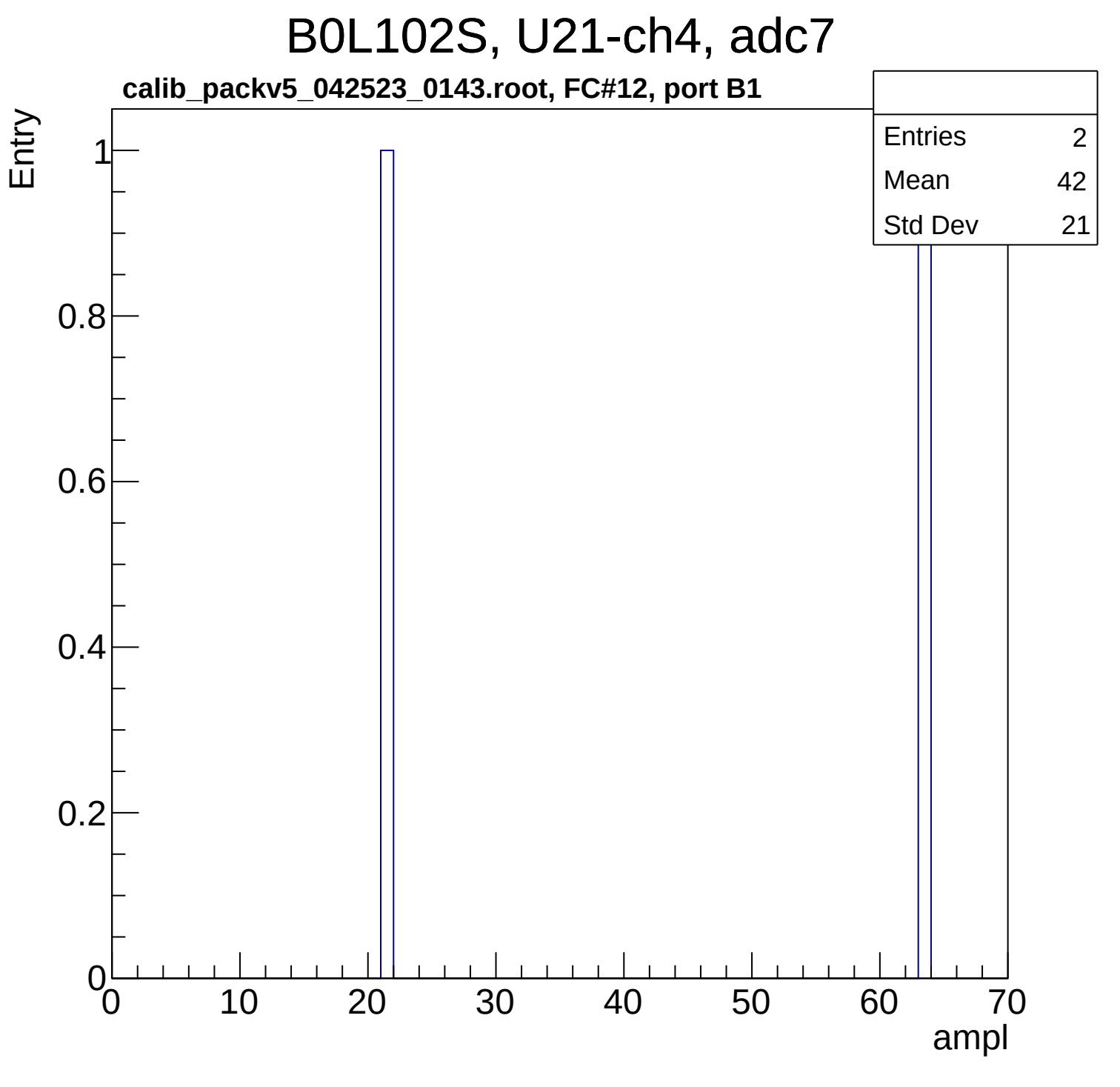

B0L102S, U21-ch4, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 61.47
Std Dev 1.419
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U21-ch4, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 42
Std Dev 21
Entry
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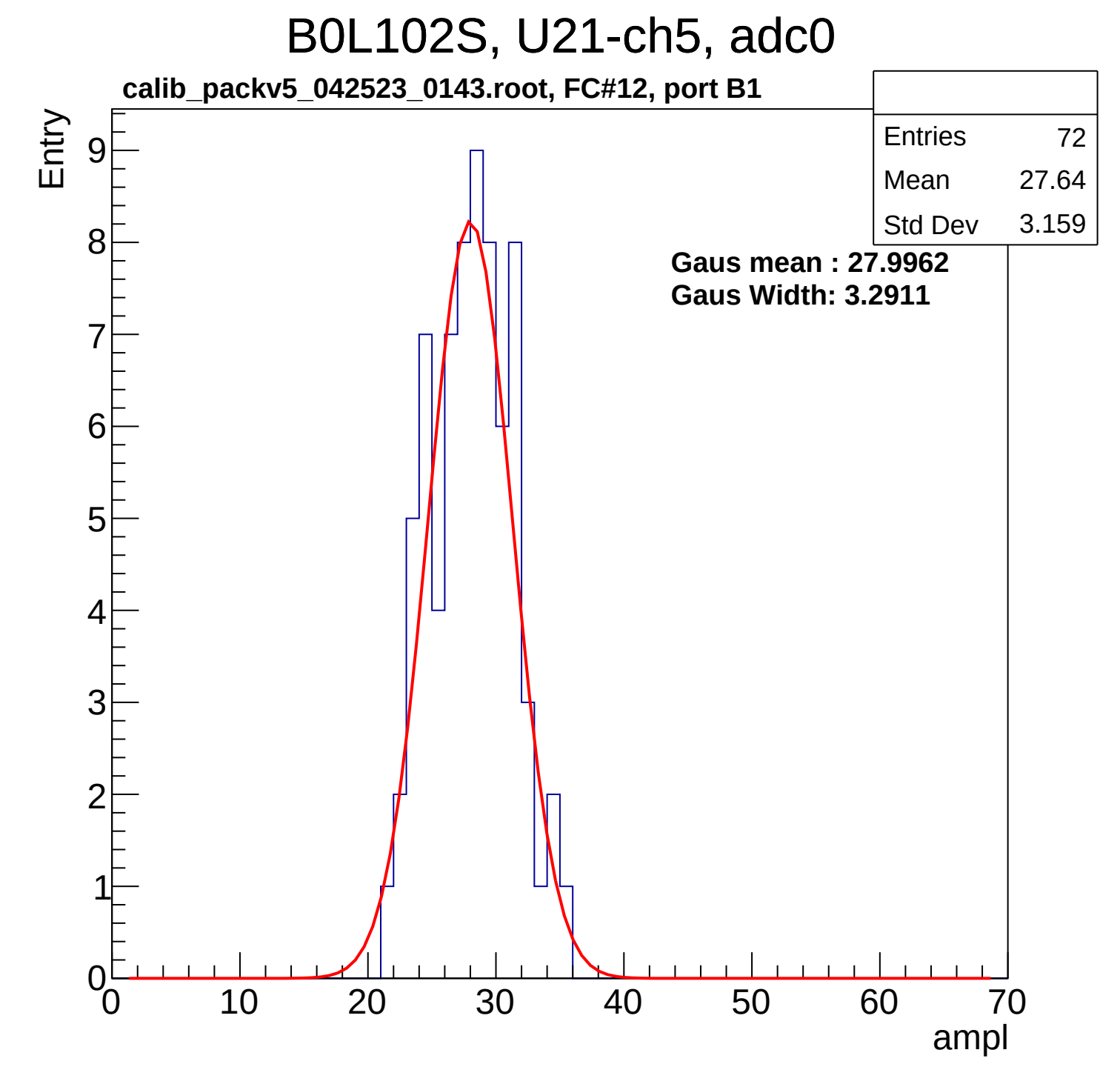
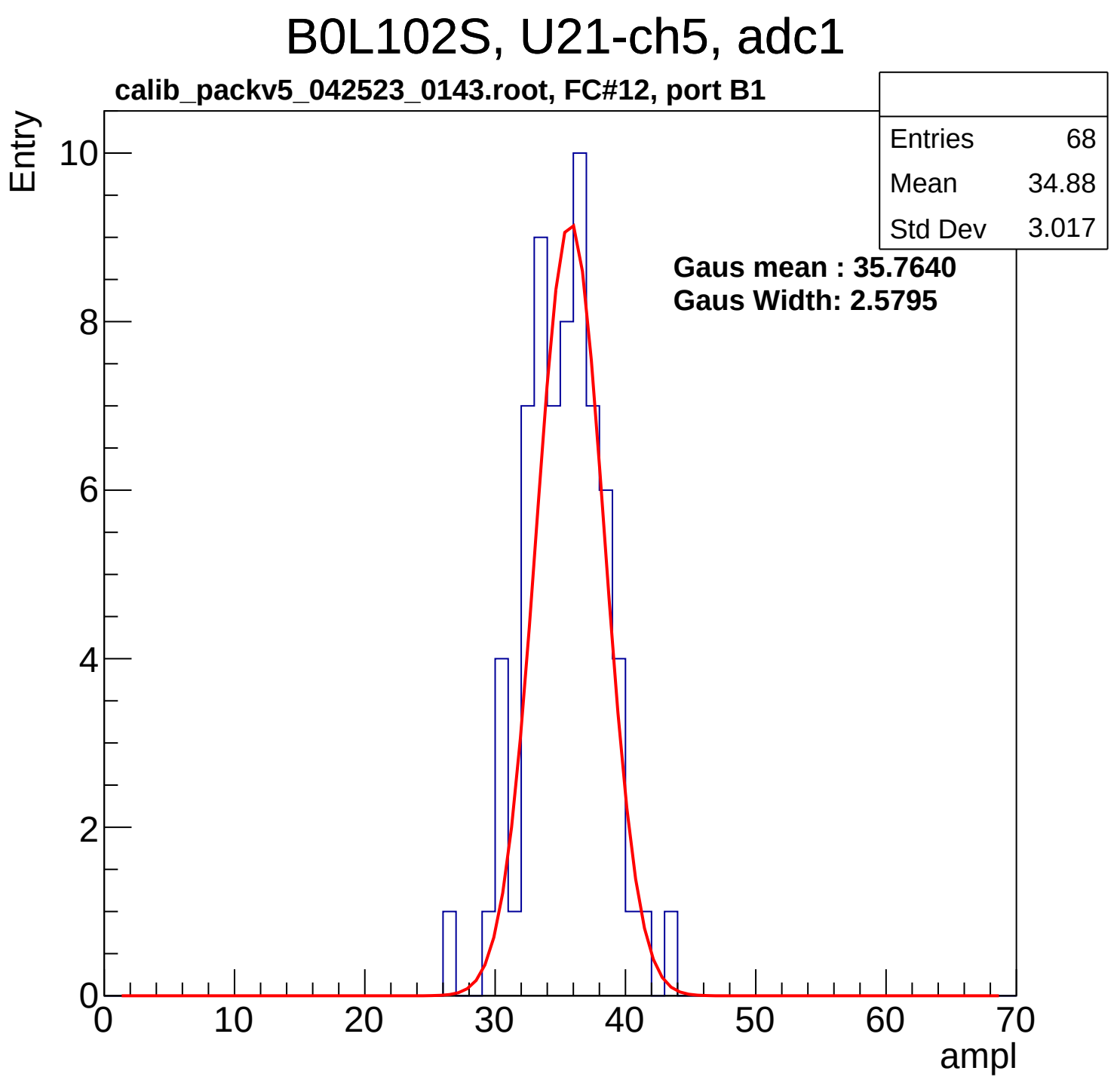
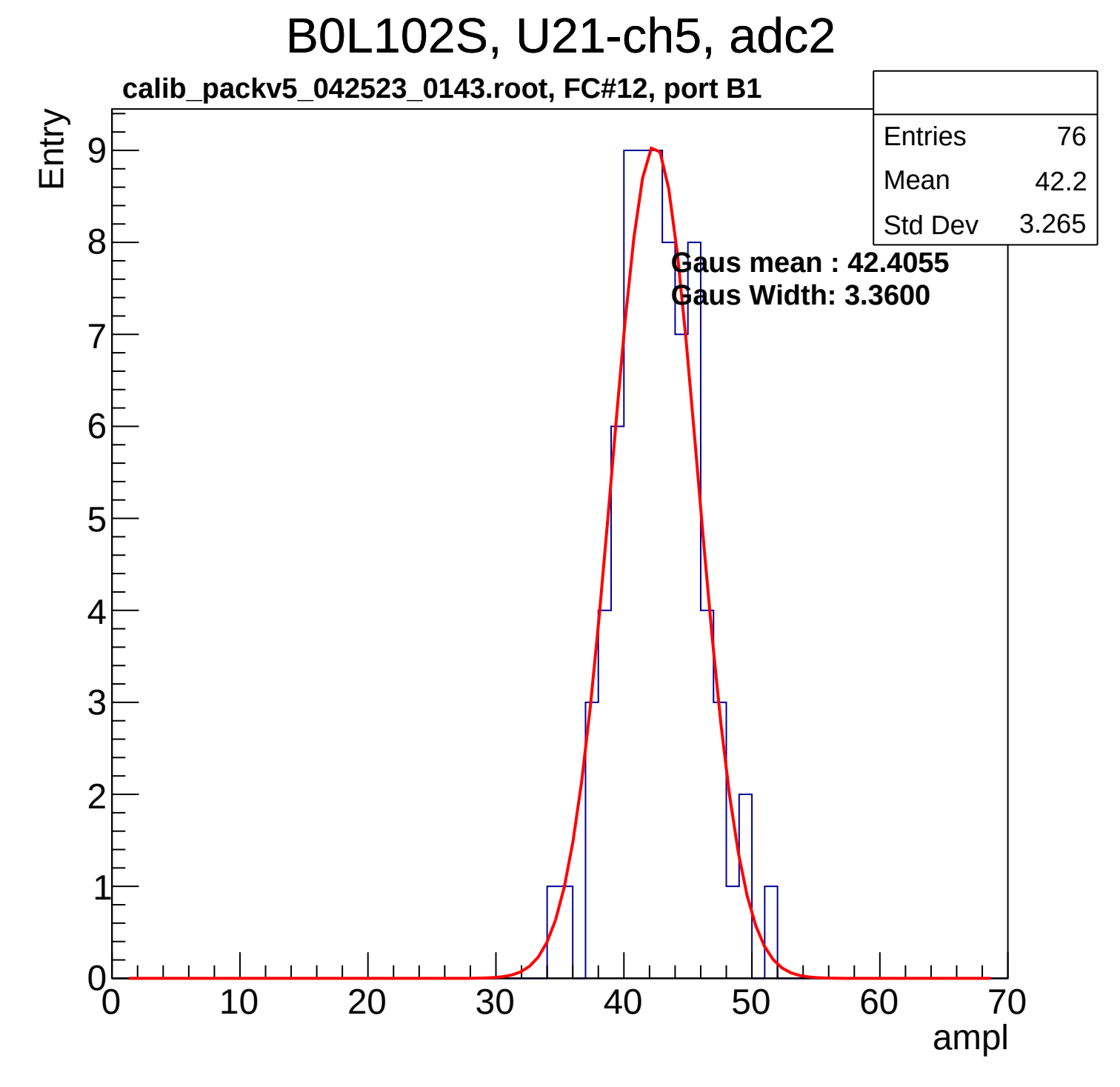
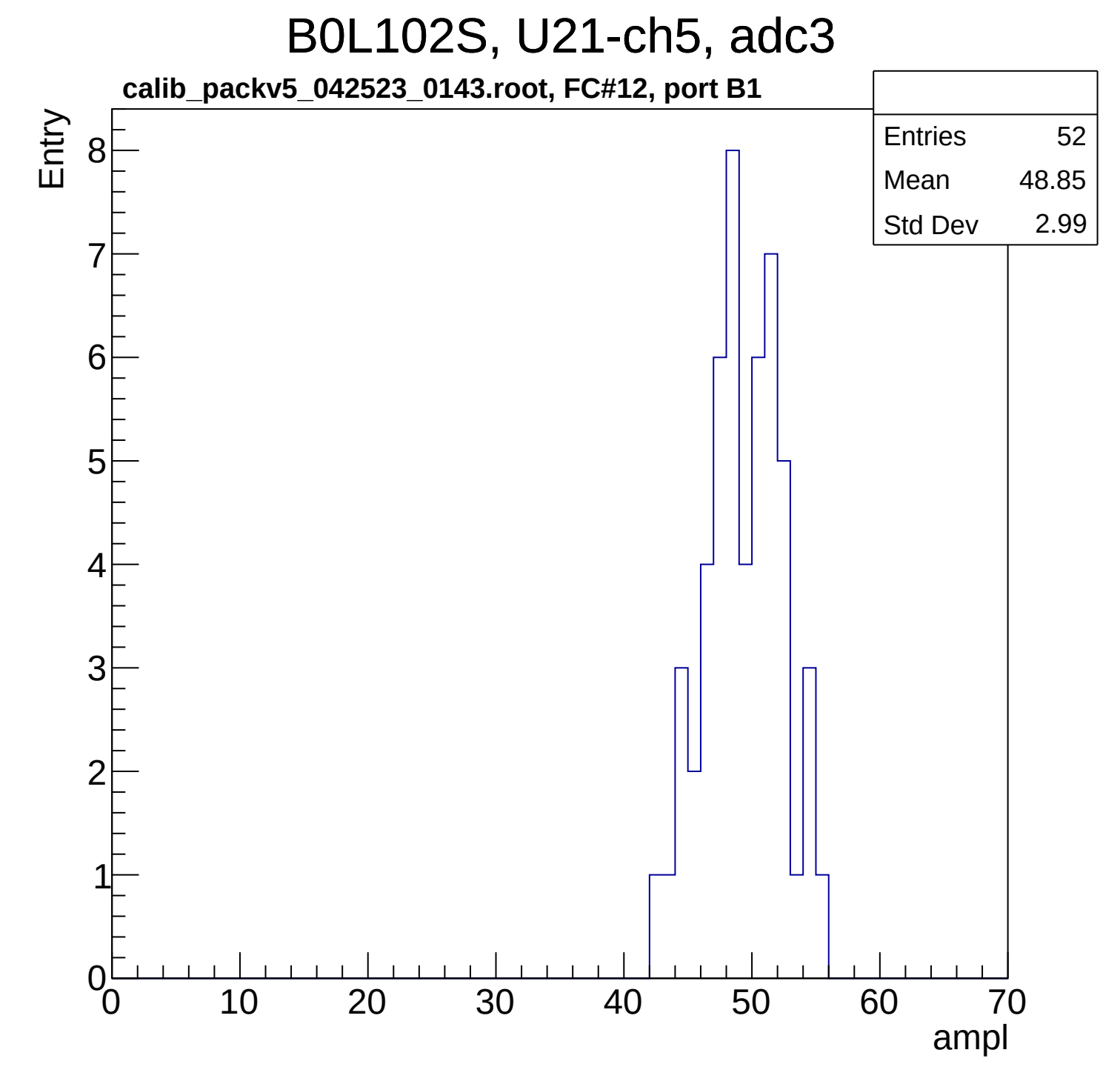
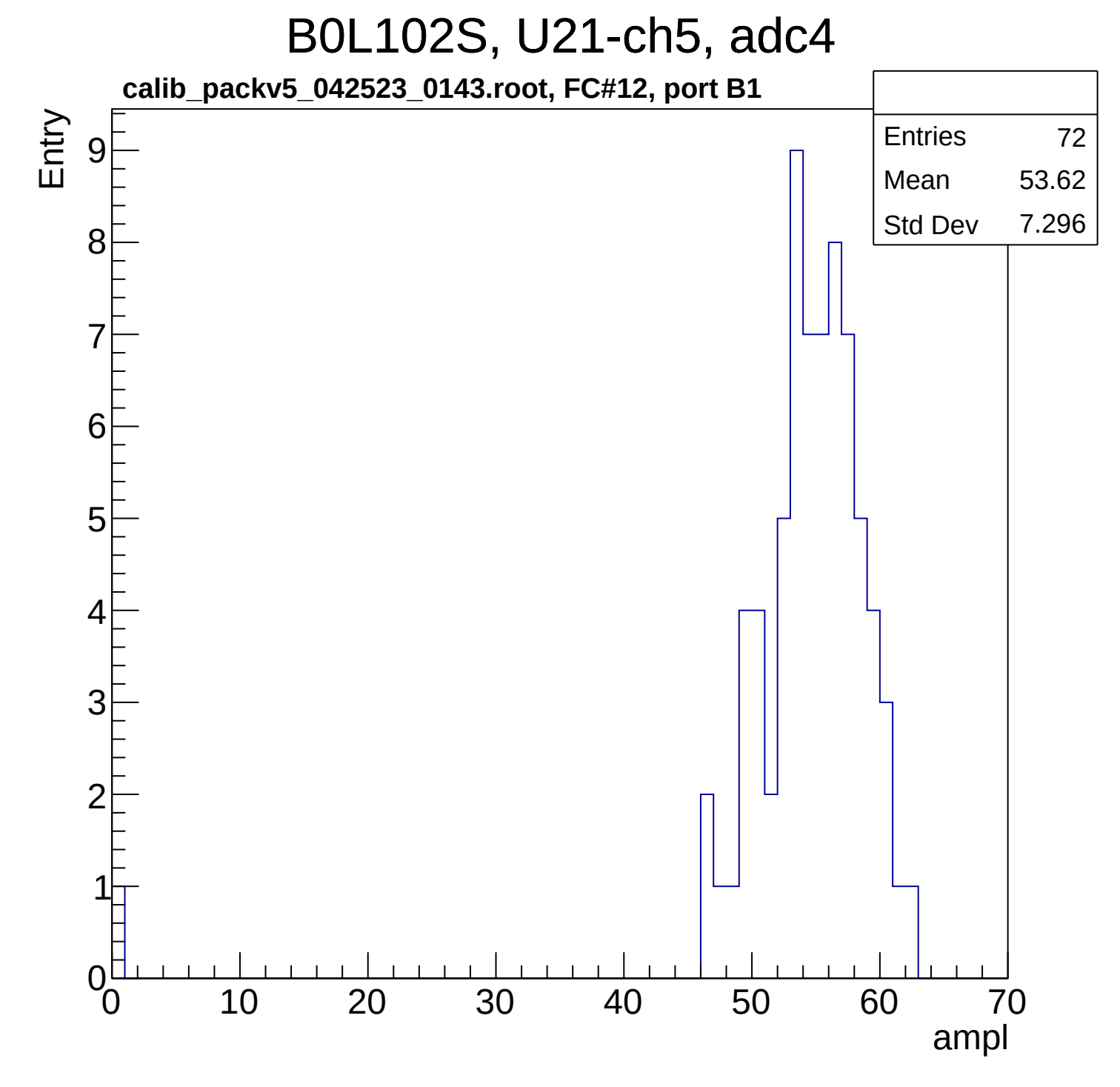
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

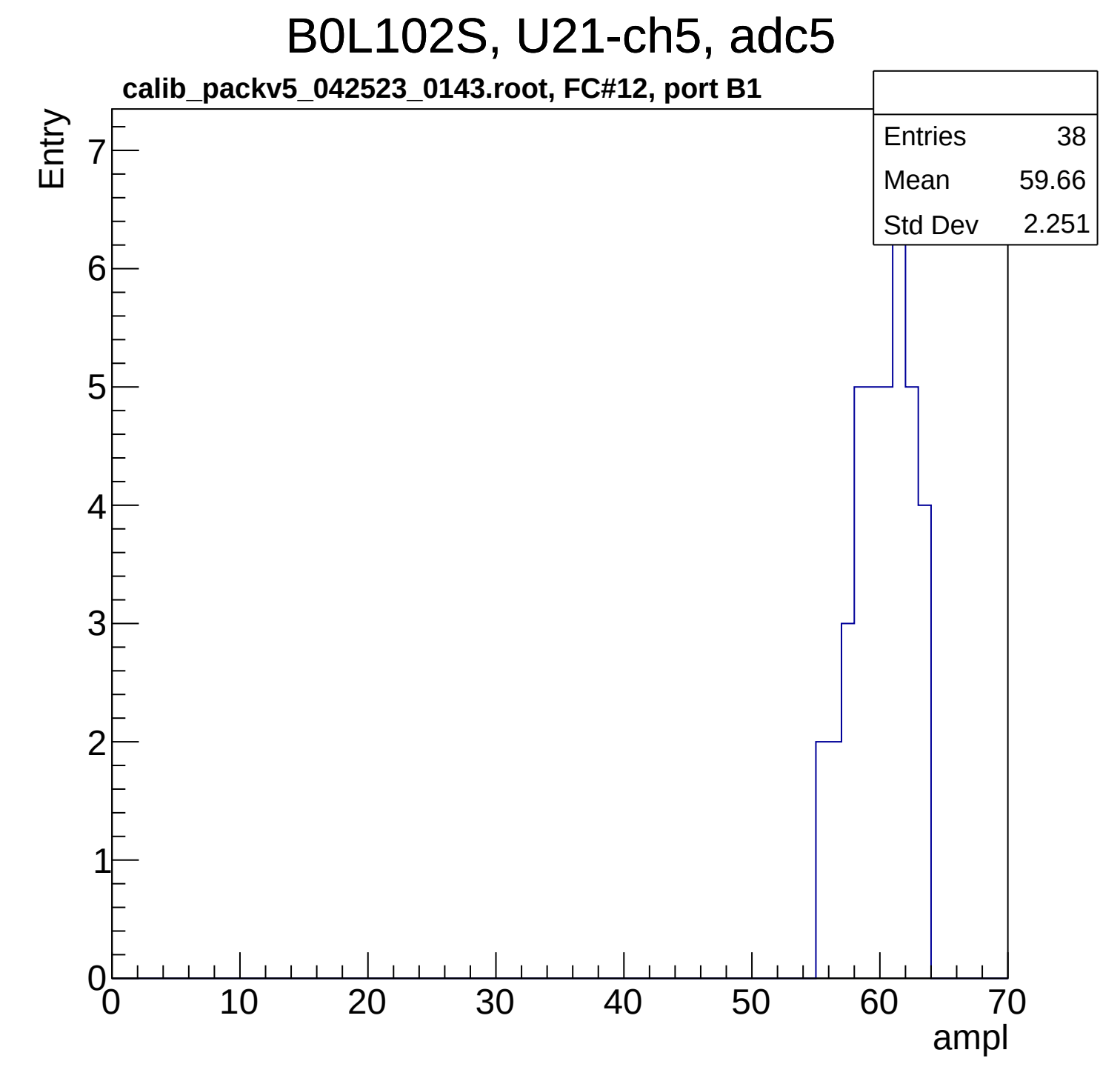

B0L102S, U21-ch5, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 27.64
Std Dev 3.159
Gaus mean : 27.9962
Gaus Width: 3.2911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

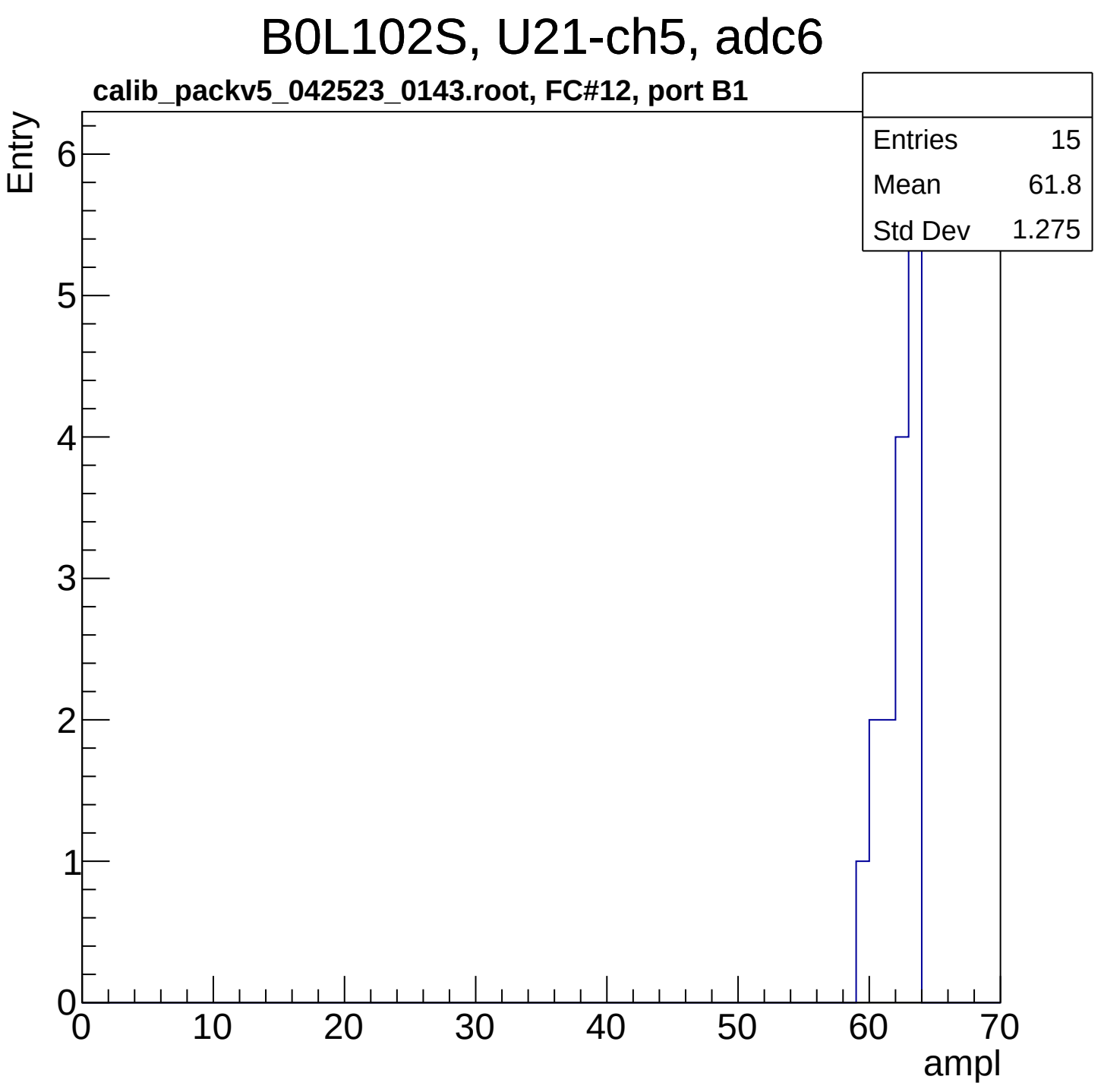

B0L102S, U21-ch5, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 34.88
Std Dev 3.017
Gaus mean : 35.7640
Gaus Width: 2.5795
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

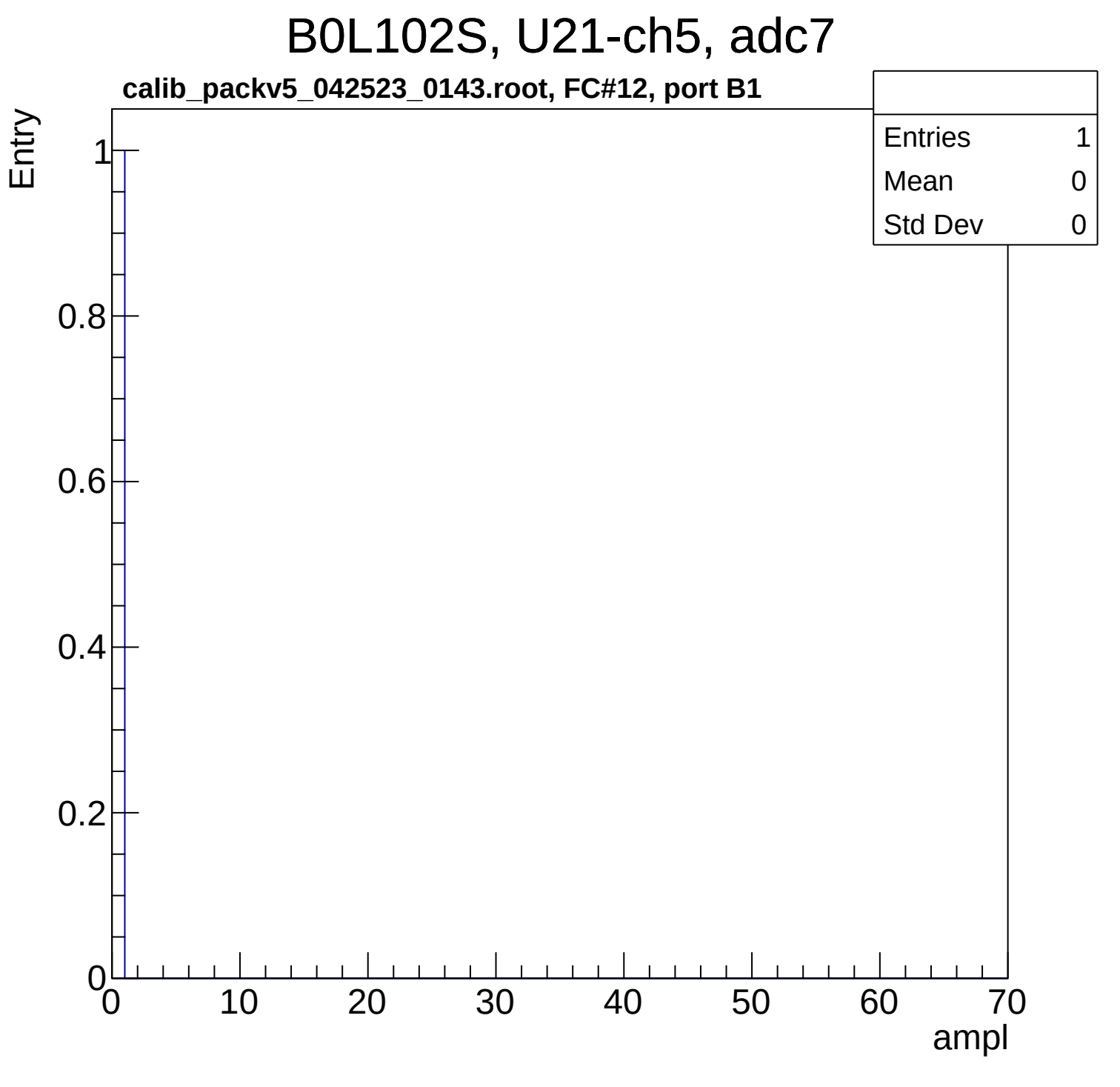

B0L102S, U21-ch5, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 42.2
Std Dev 3.265
Gaus mean : 42.4055
Gaus Width: 3.3600
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

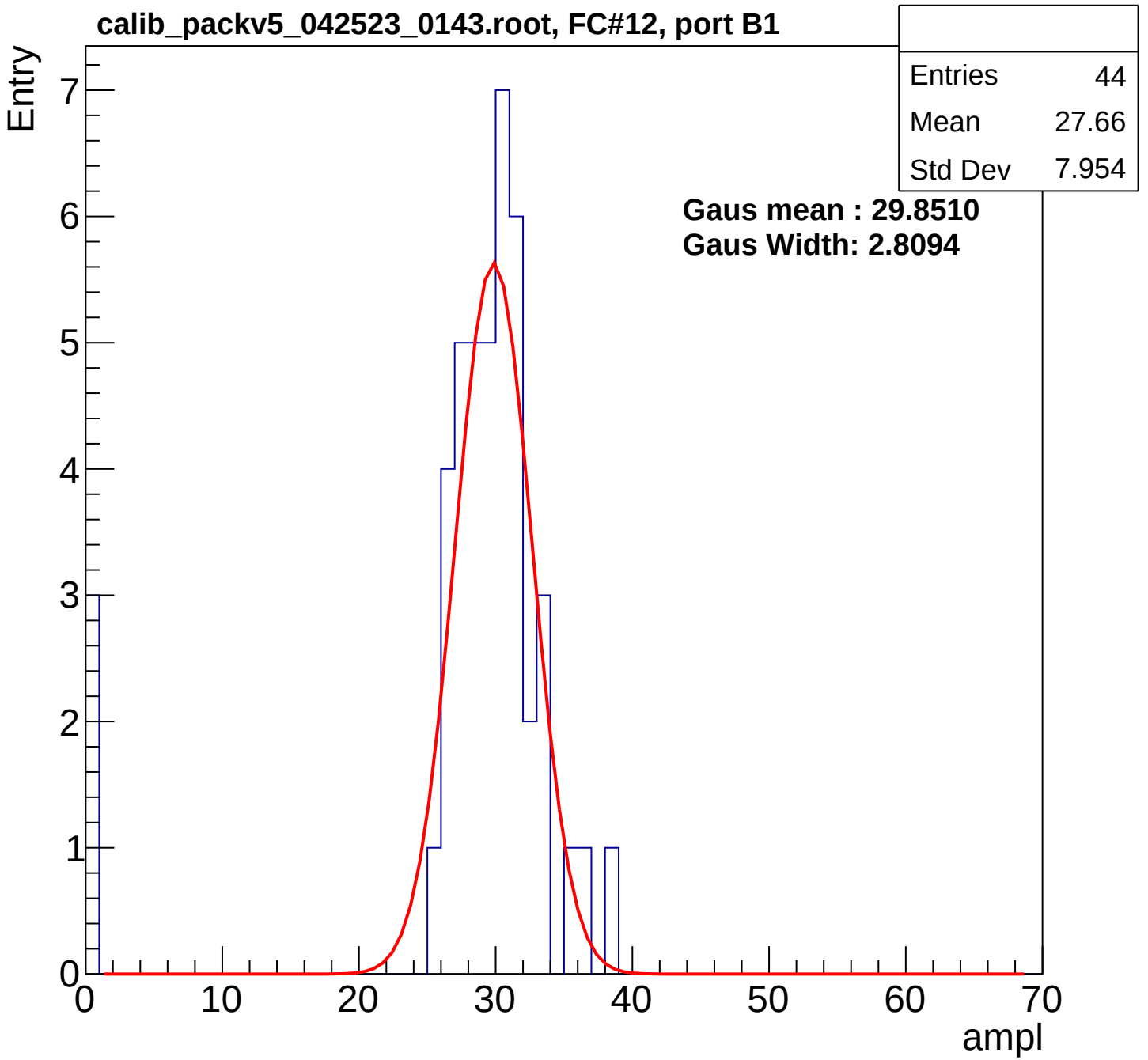

B0L102S, U21-ch5, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 48.85
Std Dev 2.99
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch5, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 53.62
Std Dev 7.296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch5, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 59.66
Std Dev 2.251
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch5, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 61.8
Std Dev 1.275
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch5, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch6, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 27.66
Std Dev 7.954
Gaus mean : 29.8510
Gaus Width: 2.8094
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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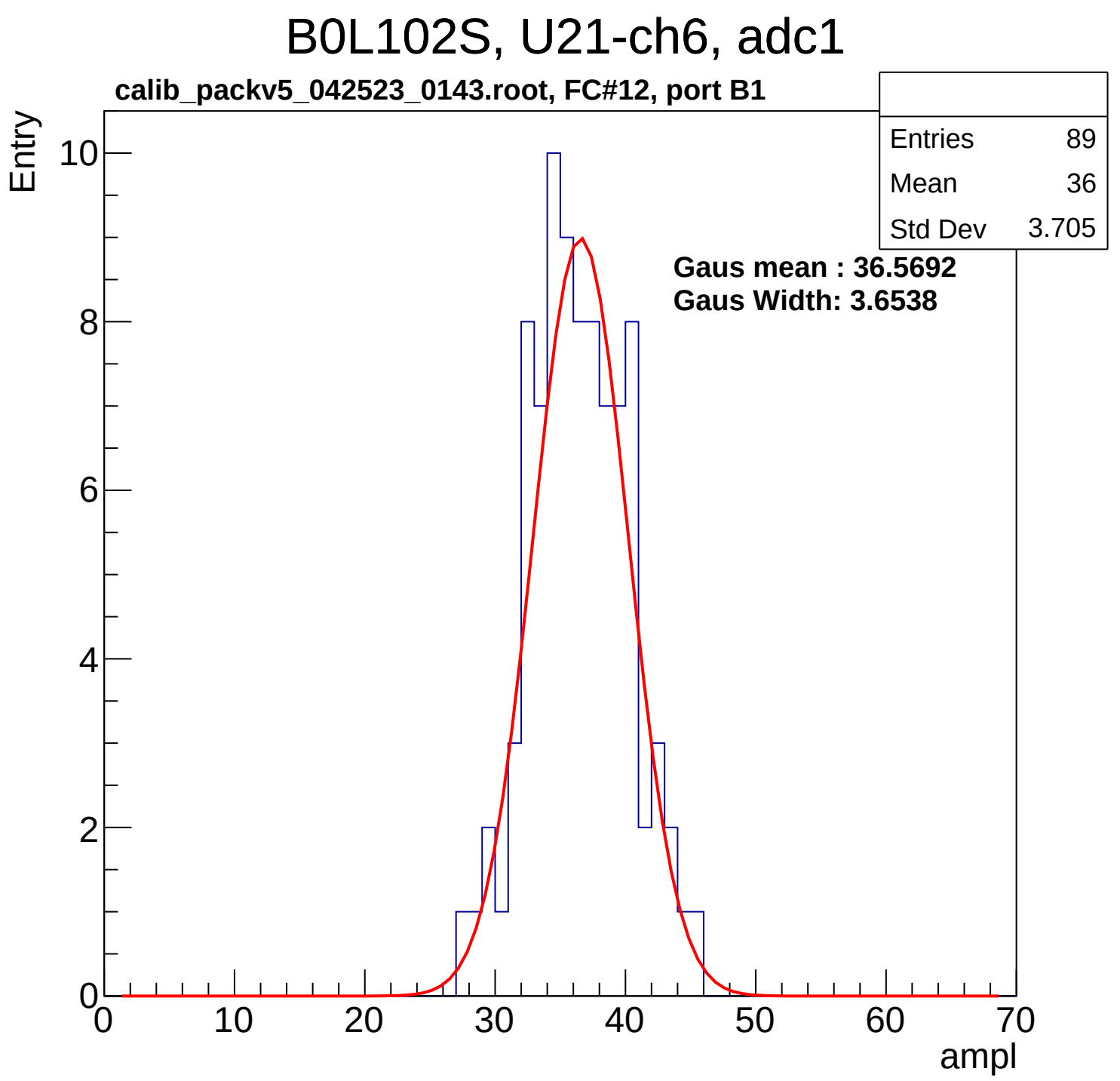
B0L102S, U21-ch6, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 36
Std Dev 3.705
Gaus mean : 36.5692
Gaus Width: 3.6538
Entry
ampl
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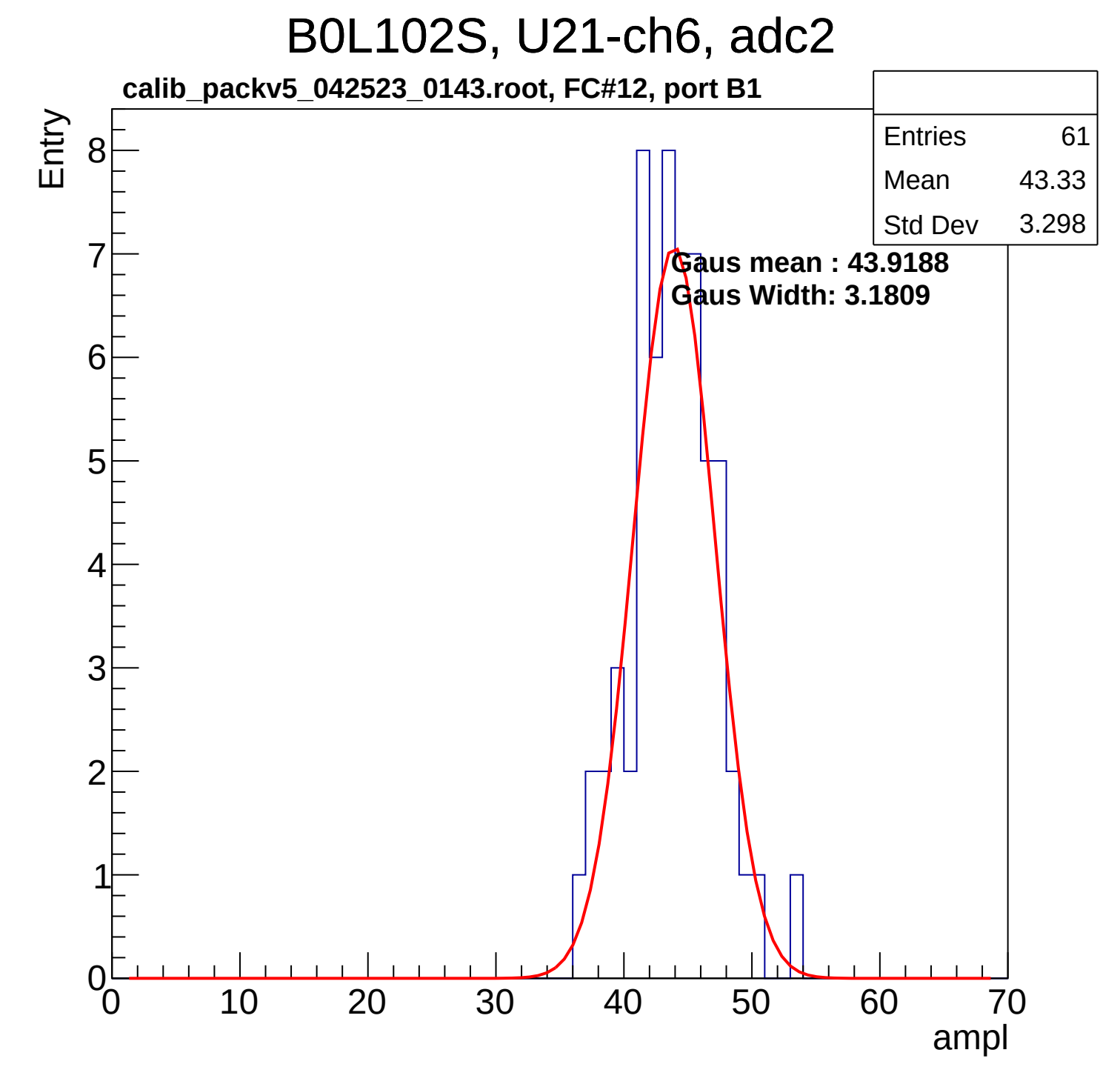
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch6, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 43.33
Std Dev 3.298
Gaus mean : 43.9188
Gaus Width: 3.1809
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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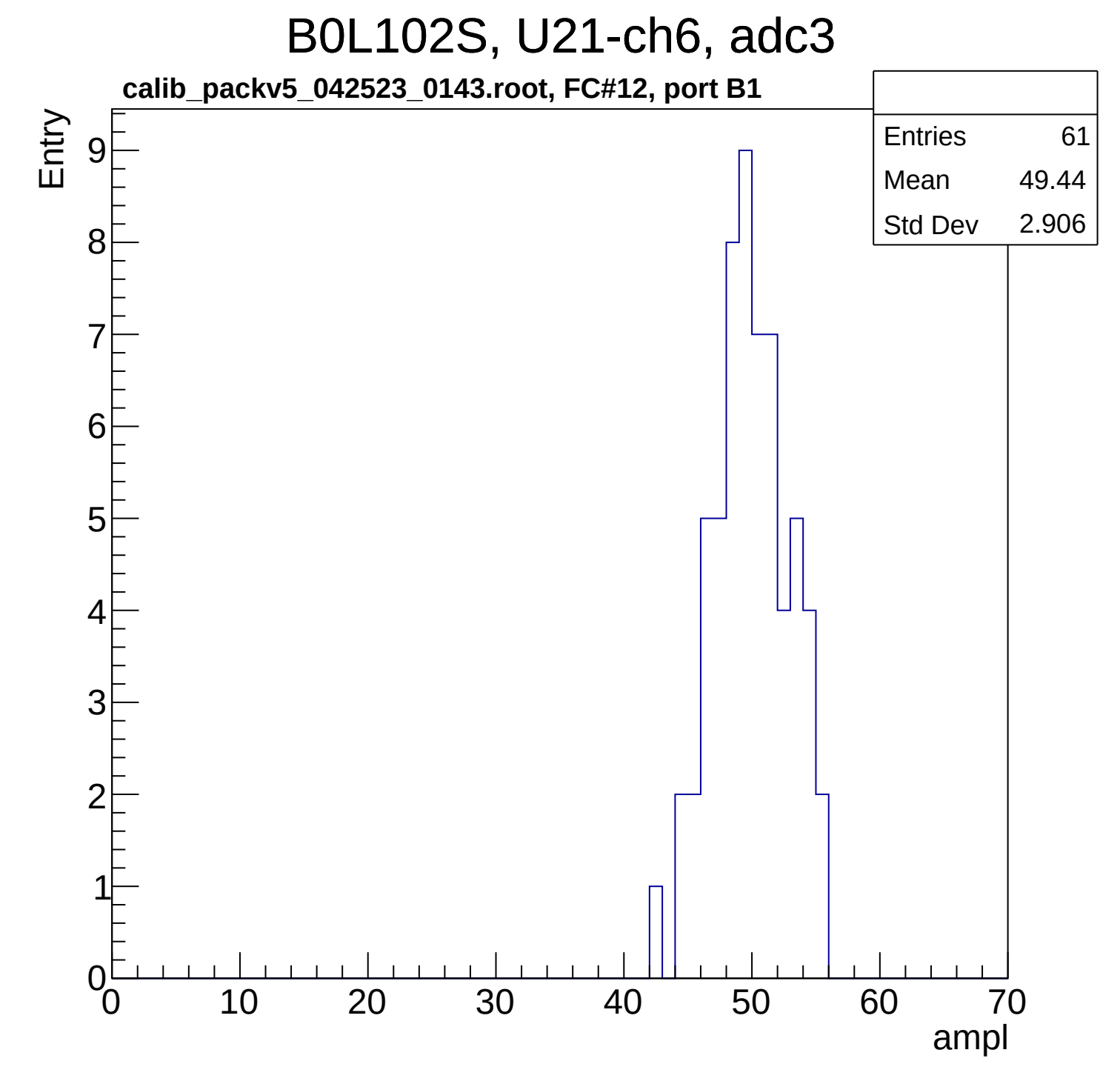
B0L102S, U21-ch6, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 49.44
Std Dev 2.906
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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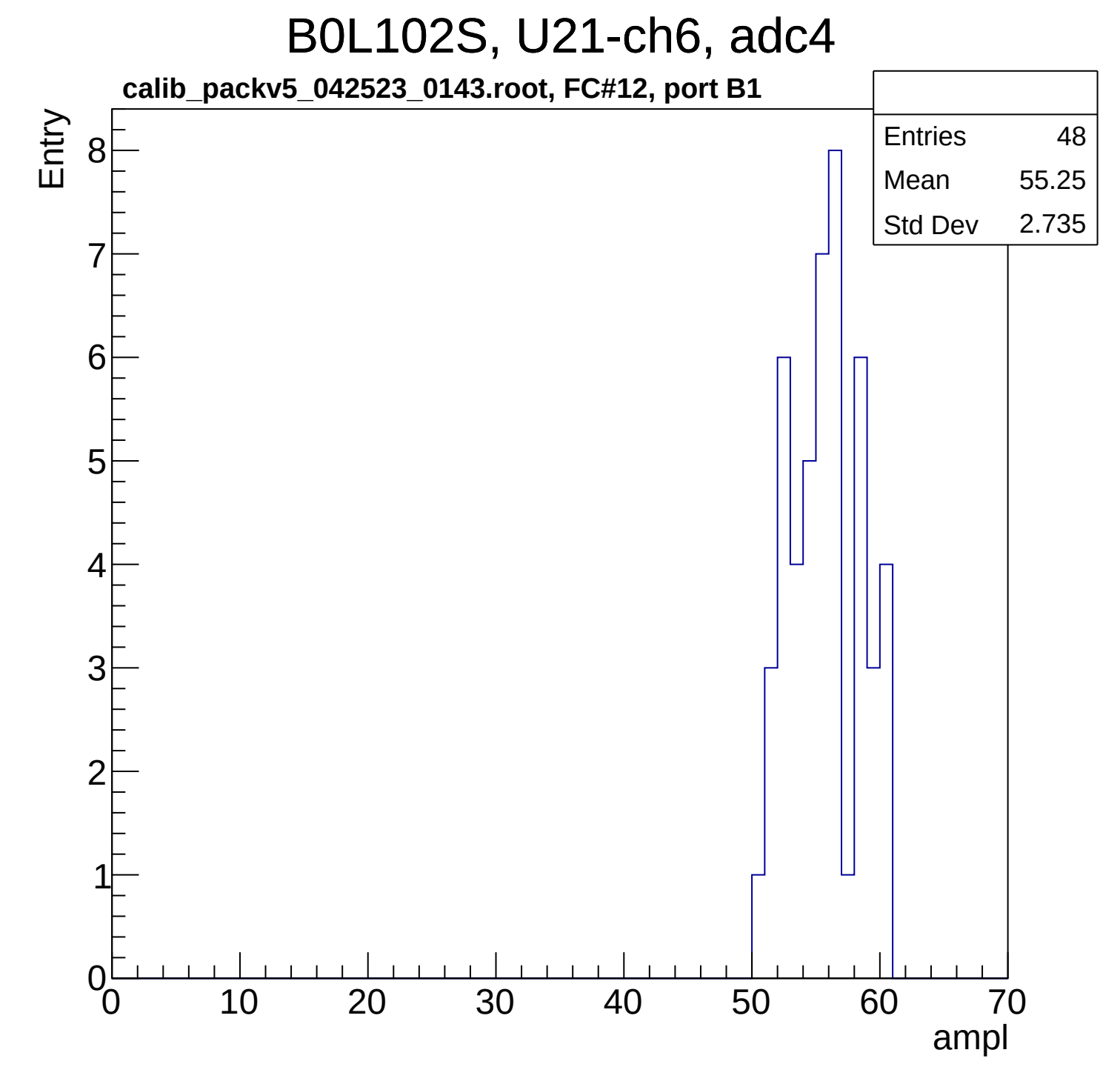
B0L102S, U21-ch6, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 55.25
Std Dev 2.735
Entry
ampl
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3
4
5
6
7
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0
10
20
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40
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60
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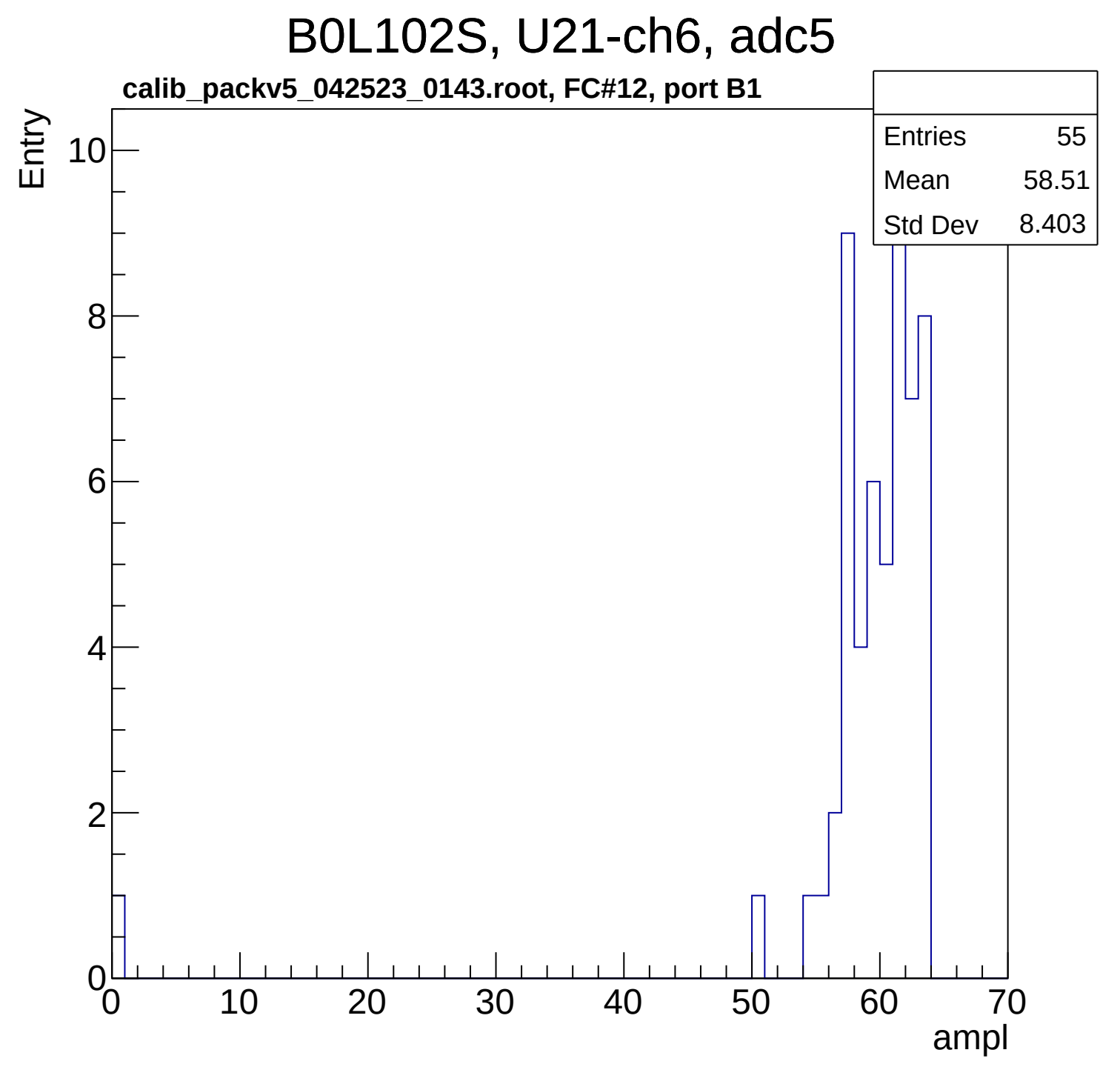
B0L102S, U21-ch6, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 58.51
Std Dev 8.403
Entry
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6
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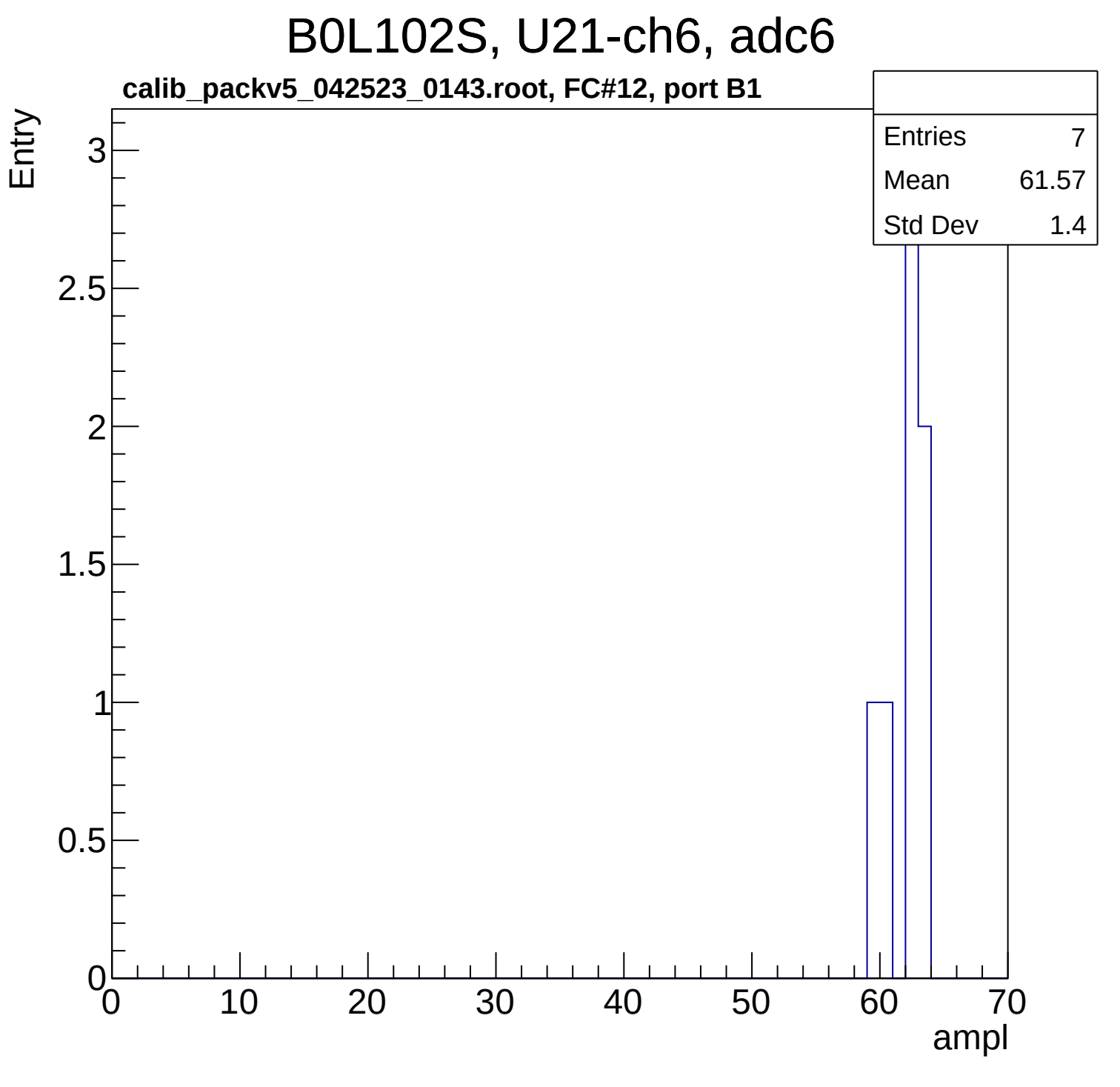
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch6, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.57
Std Dev 1.4
Entry
ampl
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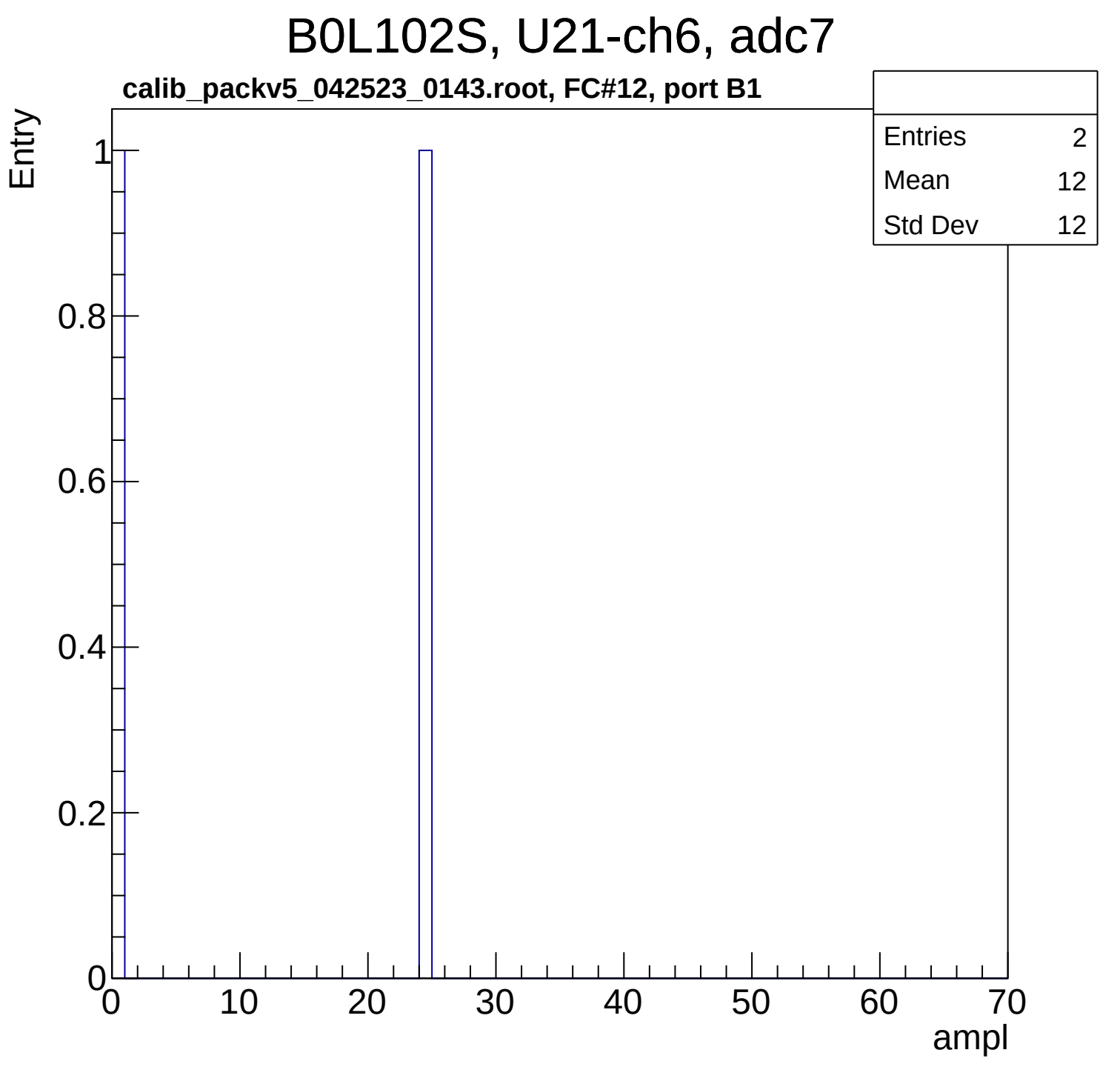
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch6, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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0.4
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10
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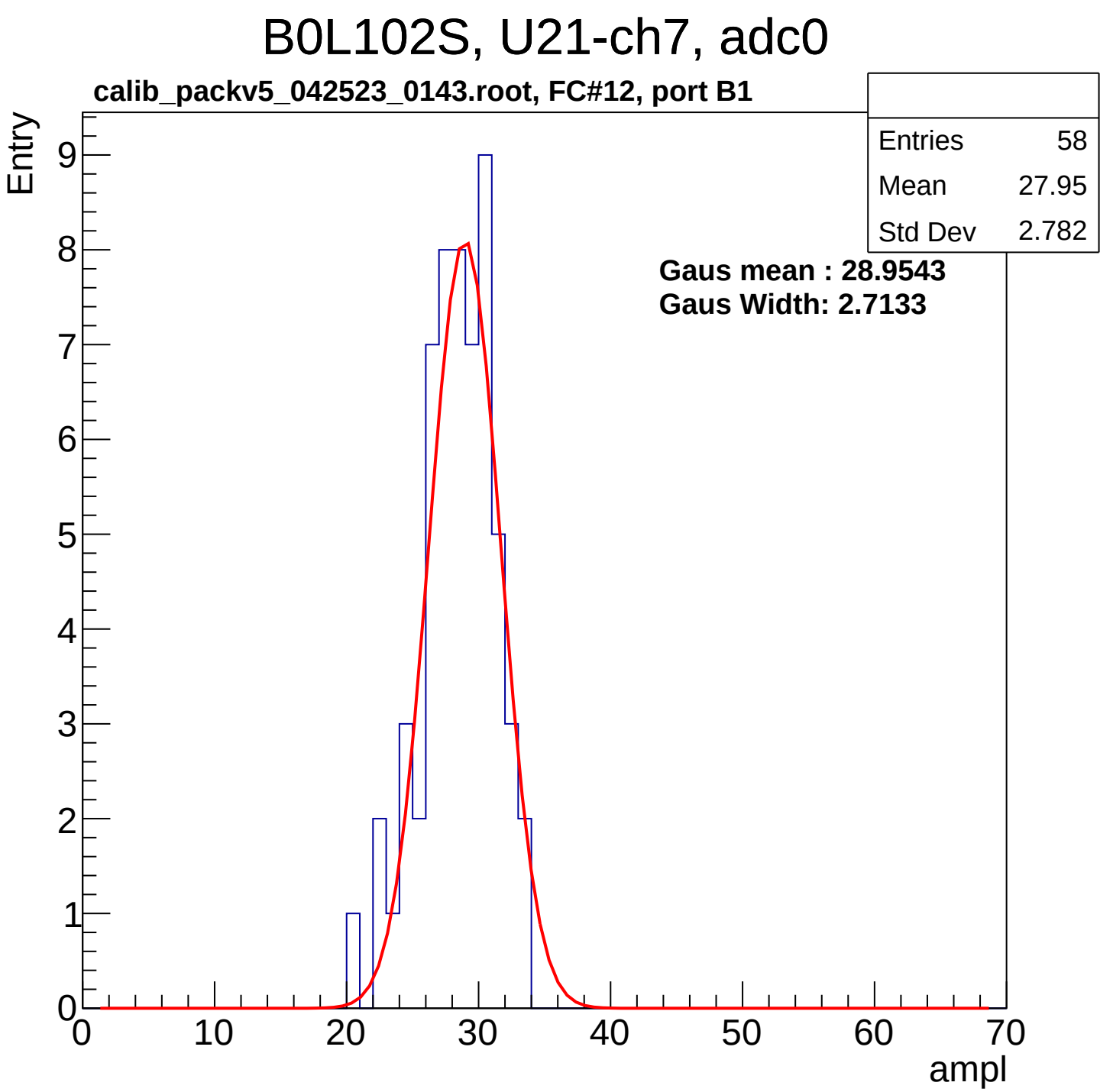
40
50
60
70
ampl


B0L102S, U21-ch7, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 27.95
Std Dev 2.782
Gaus mean : 28.9543
Gaus Width: 2.7133
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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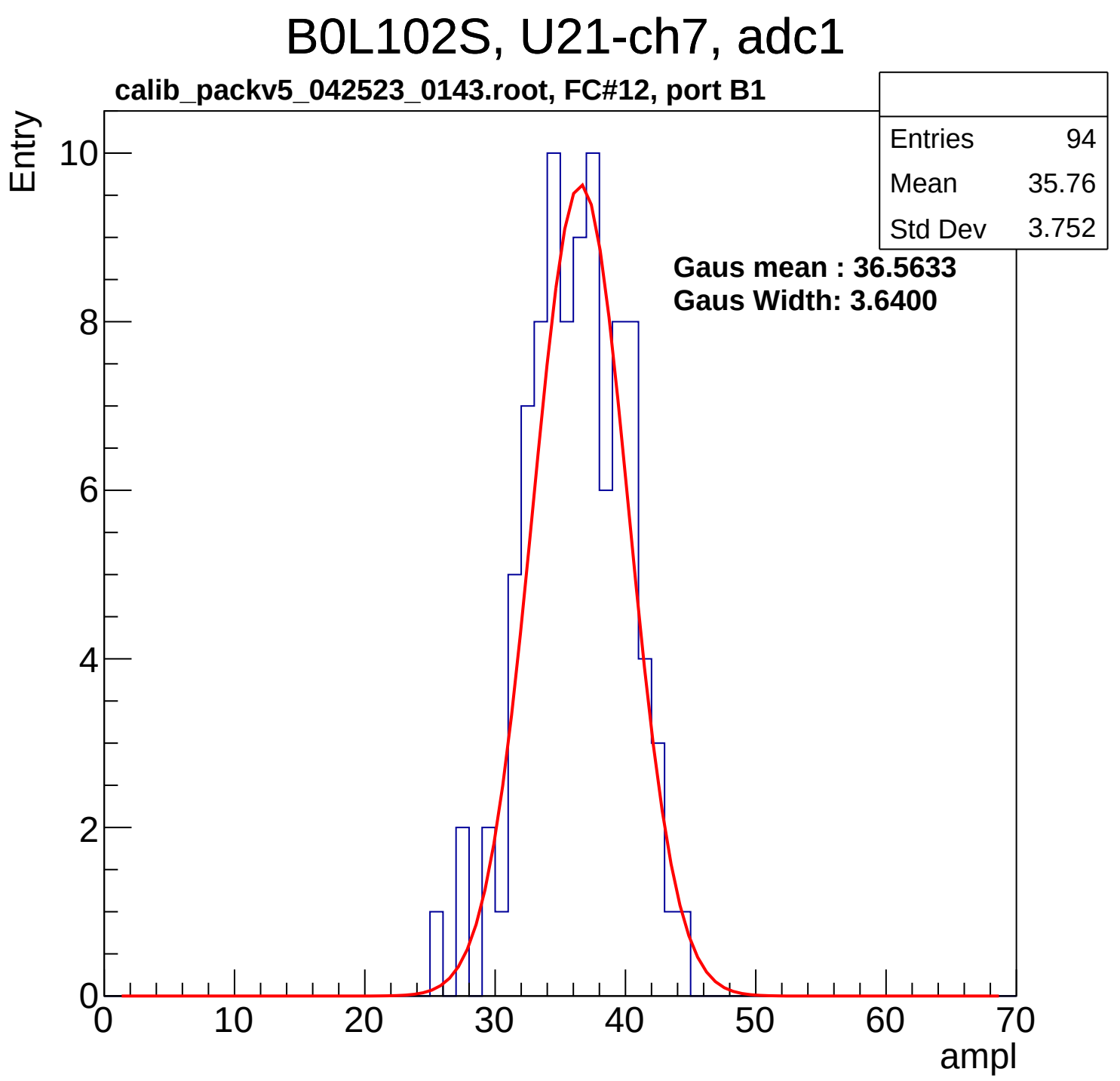
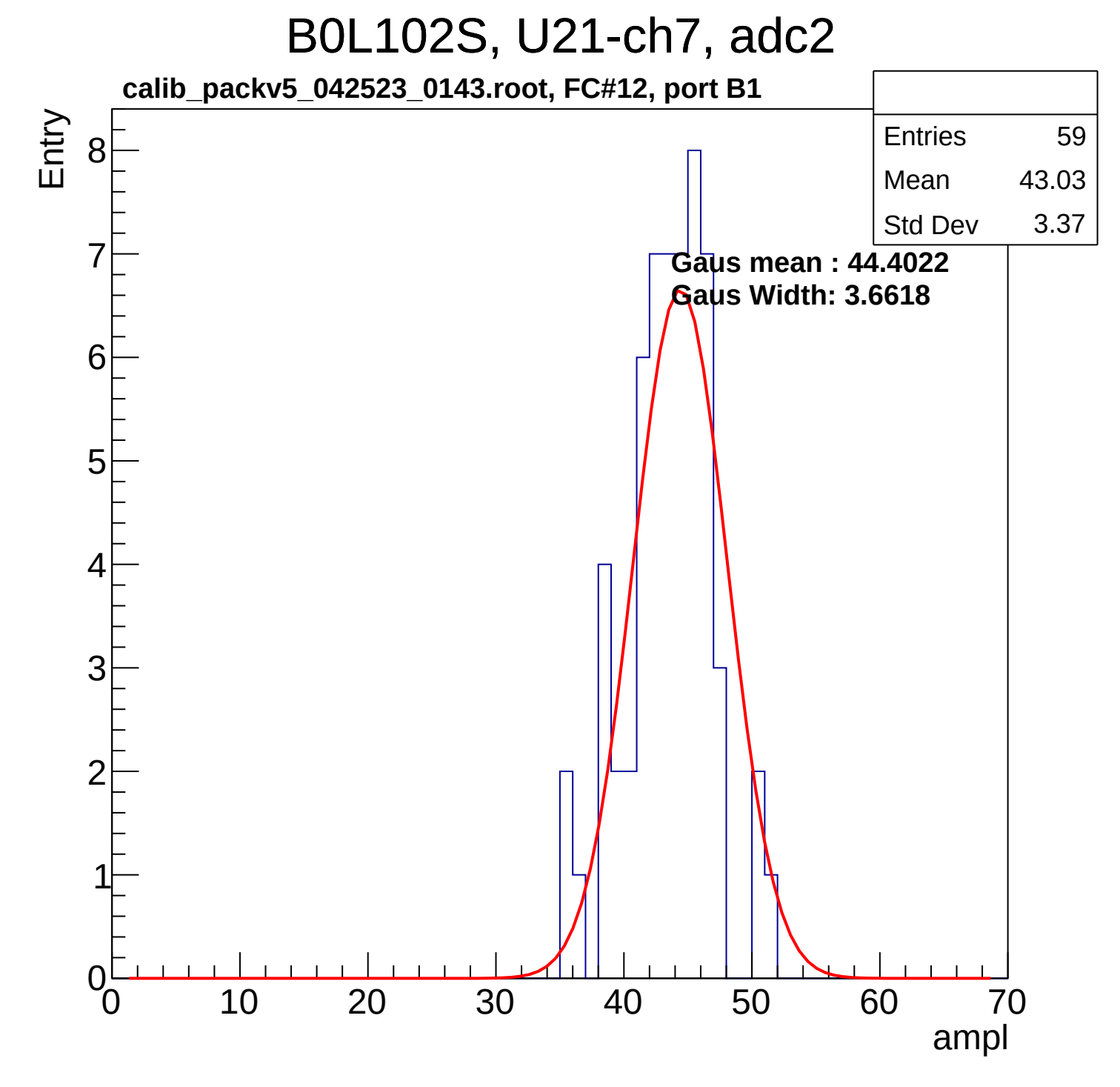

B0L102S, U21-ch7, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 94
Mean 35.76
Std Dev 3.752
Gaus mean : 36.5633
Gaus Width: 3.6400
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch7, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 43.03
Std Dev 3.37
Gaus mean : 44.4022
Gaus Width: 3.6618
Entry
ampl
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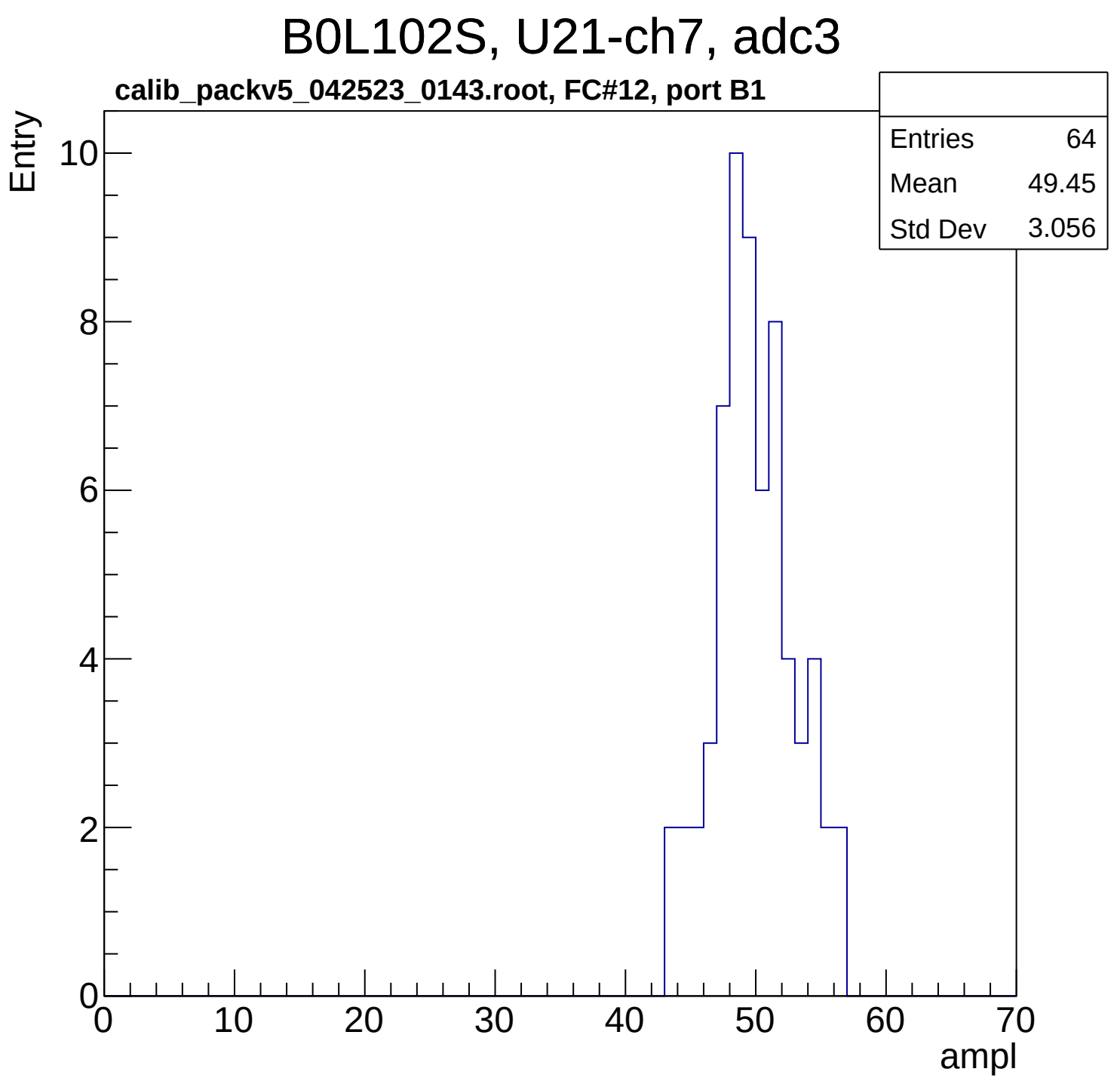
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch7, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.45
Std Dev 3.056
Entry
ampl
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60
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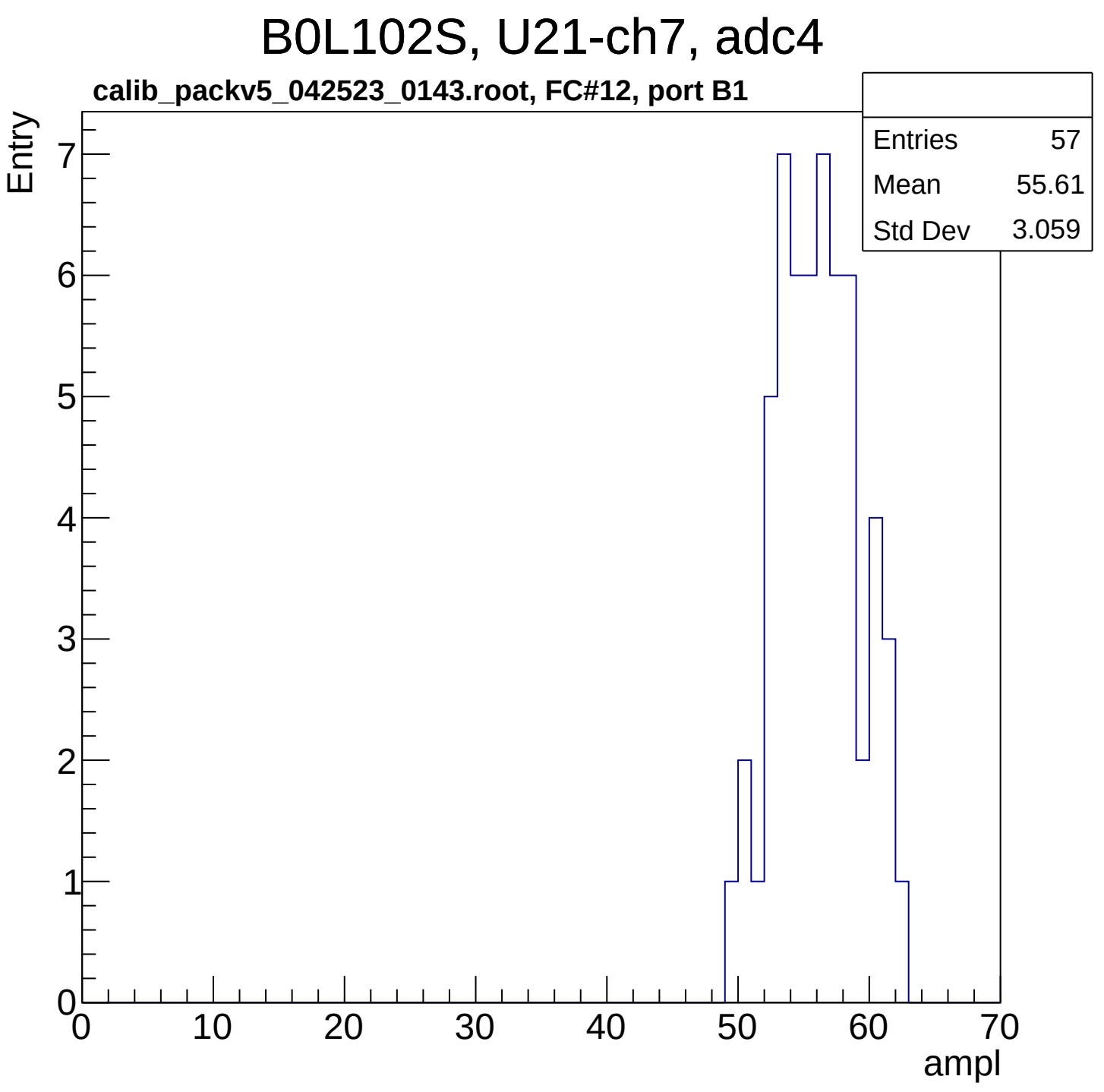
B0L102S, U21-ch7, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.61
Std Dev 3.059
Entry
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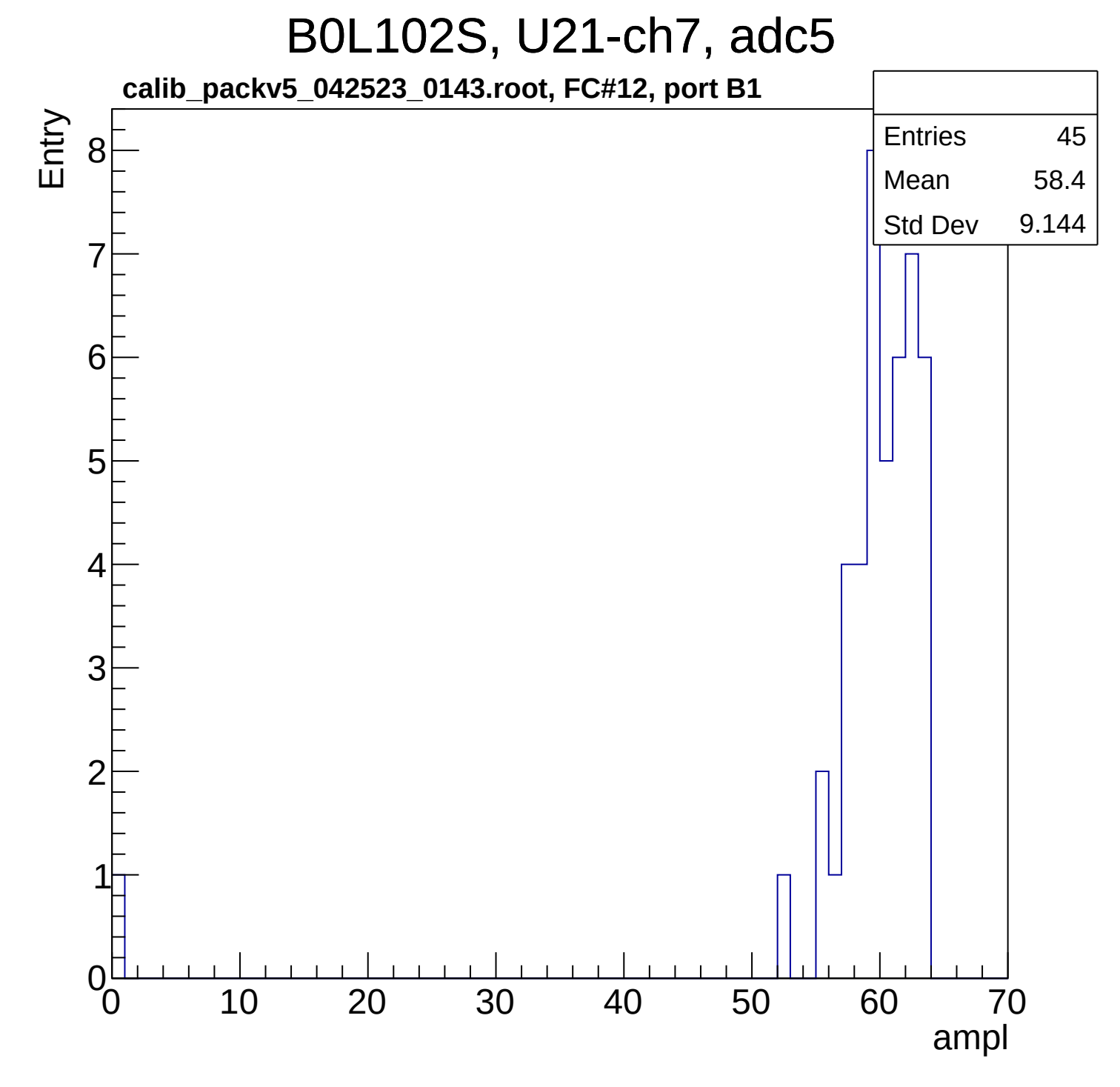
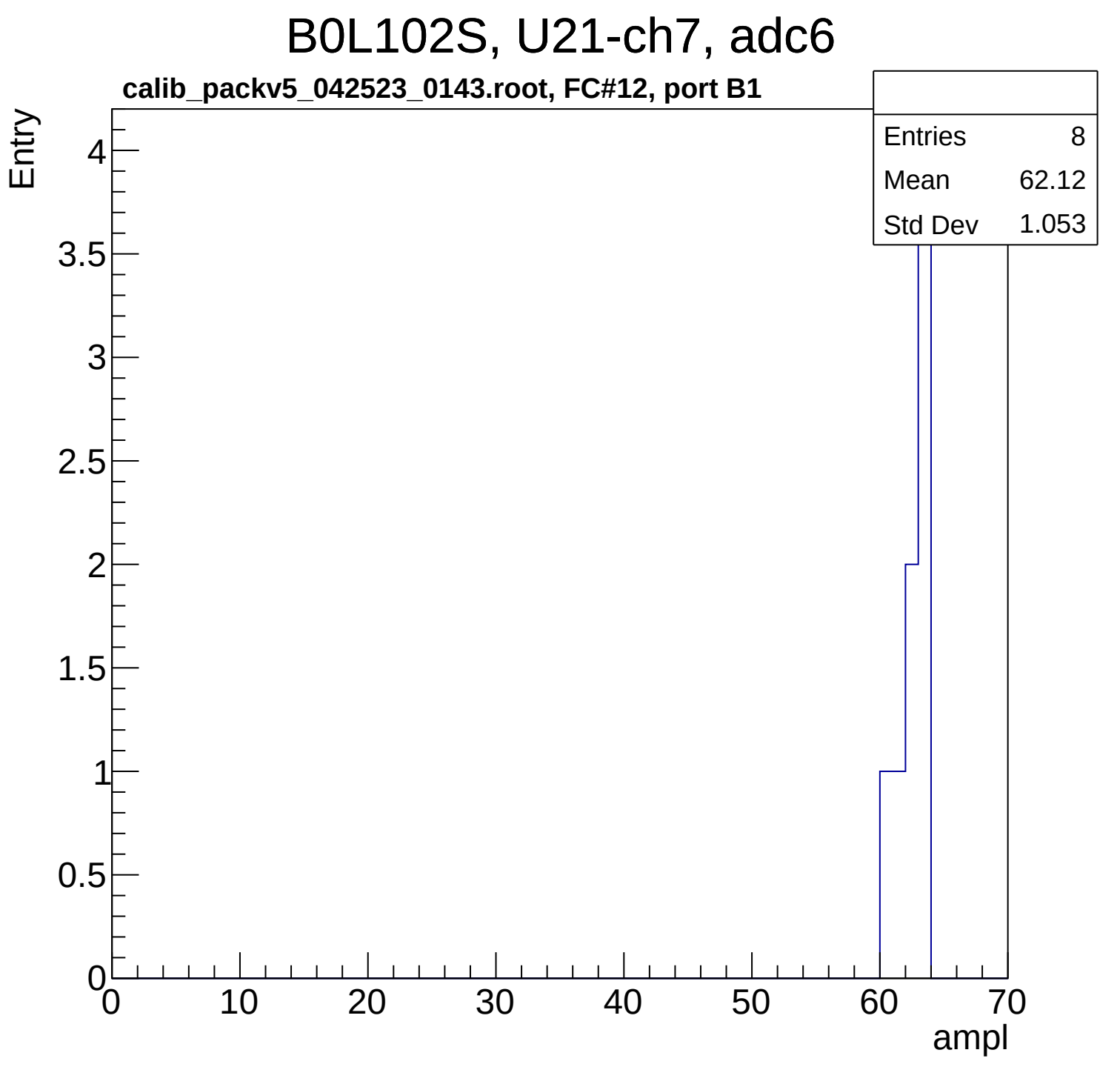
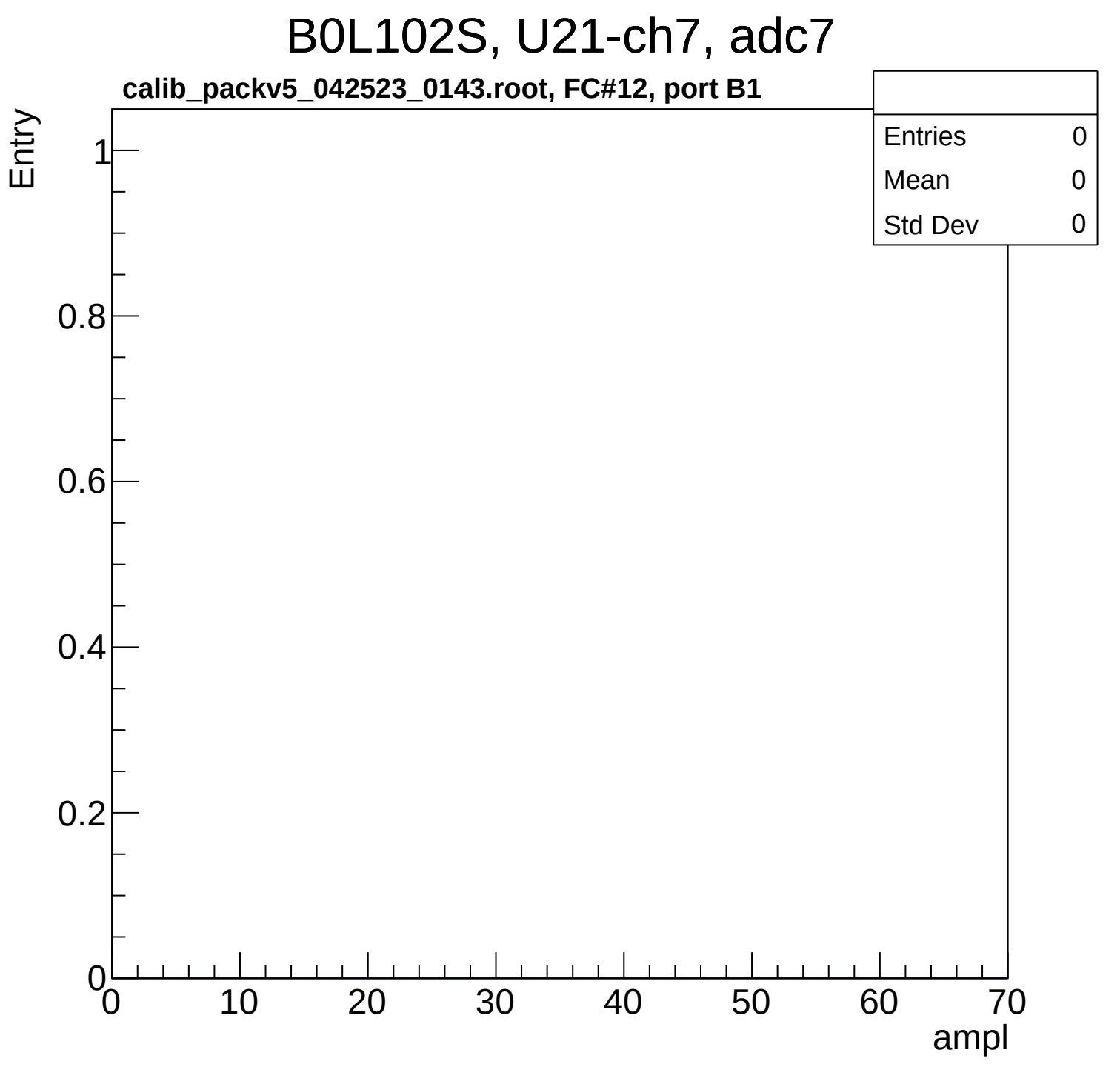
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch7, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.4
Std Dev 9.144
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch7, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch7, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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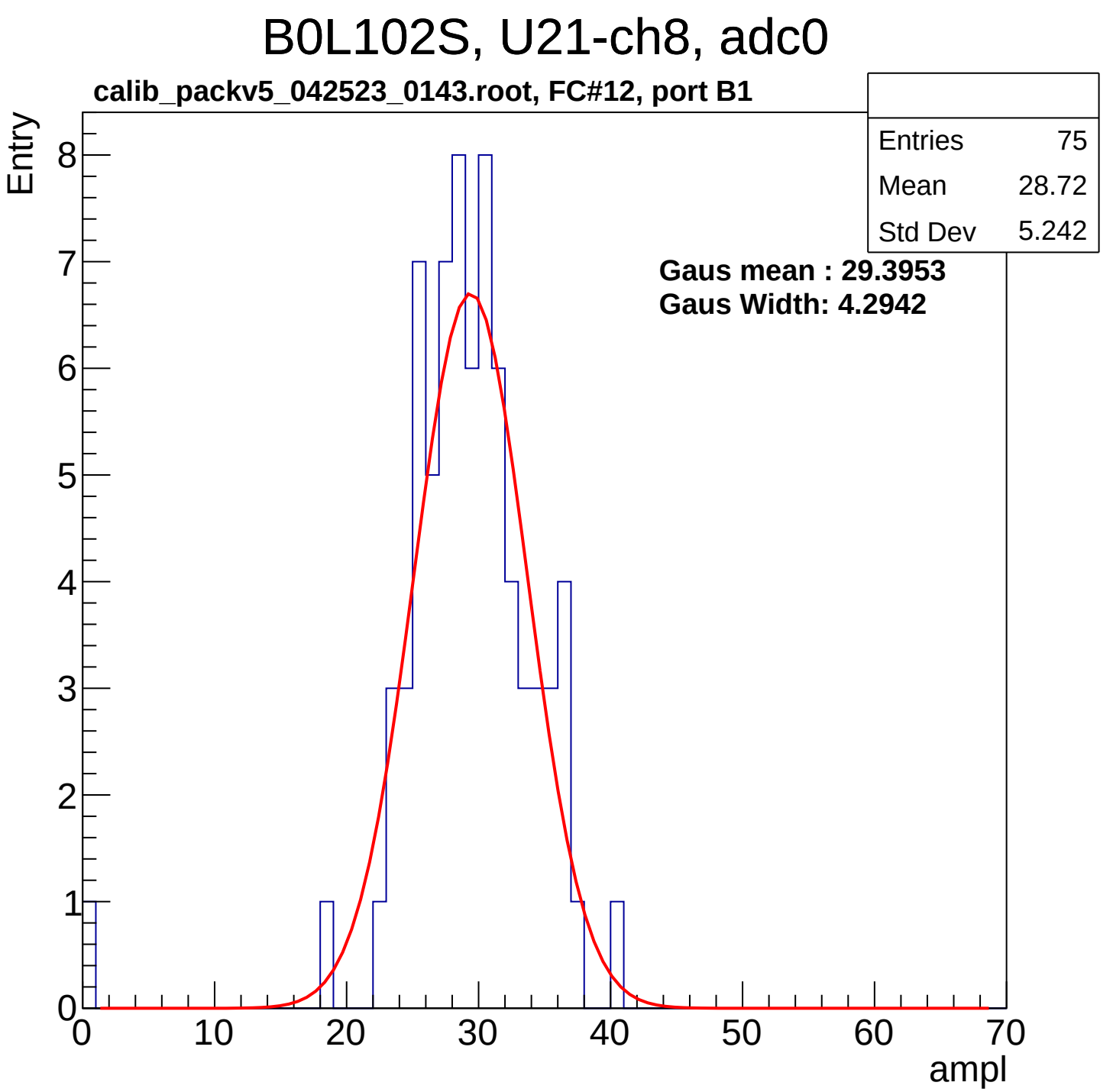
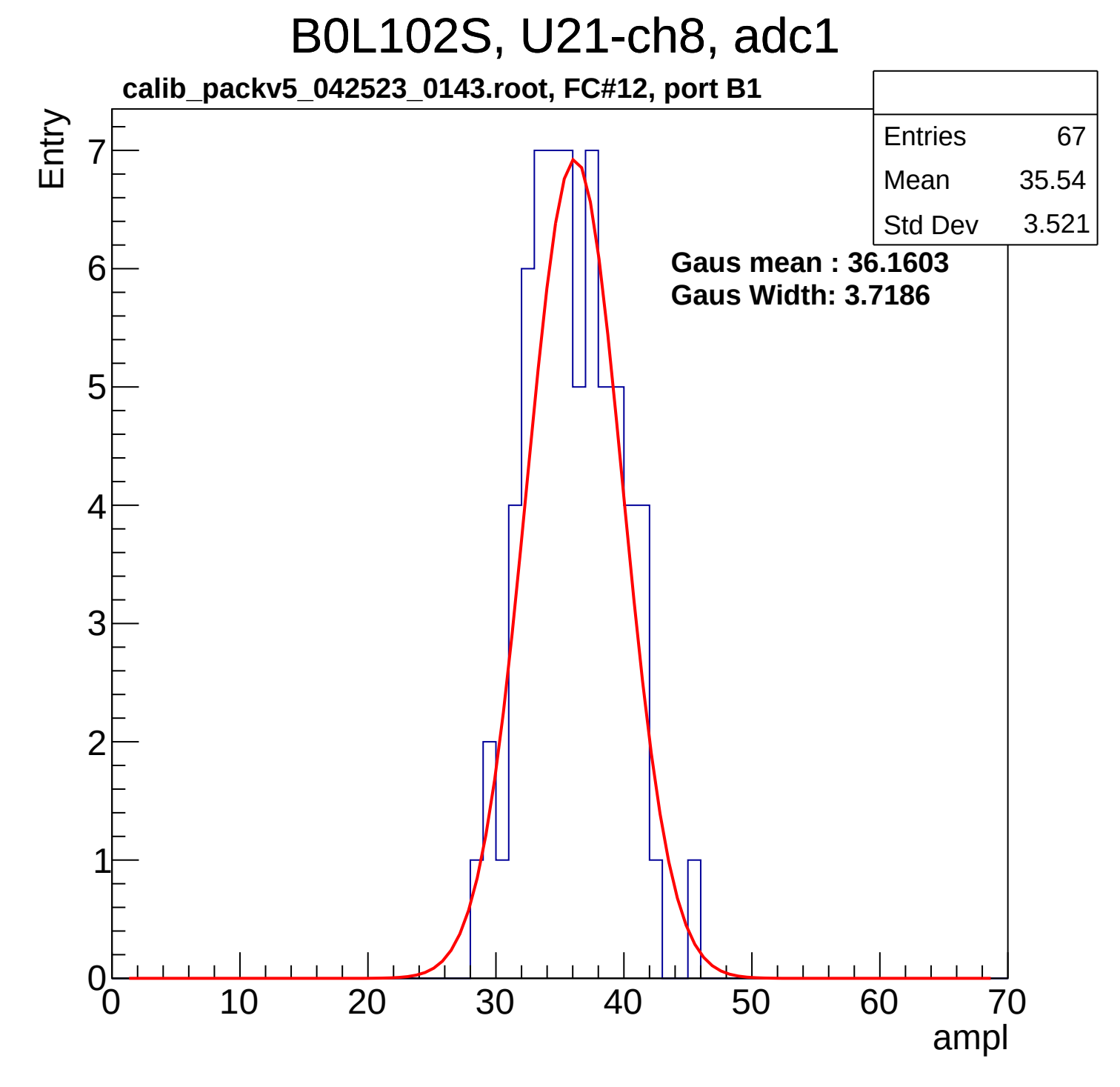
40
50
60
70
ampl


B0L102S, U21-ch8, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 28.72
Std Dev 5.242
Gaus mean : 29.3953
Gaus Width: 4.2942
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch8, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.54
Std Dev 3.521
Gaus mean : 36.1603
Gaus Width: 3.7186
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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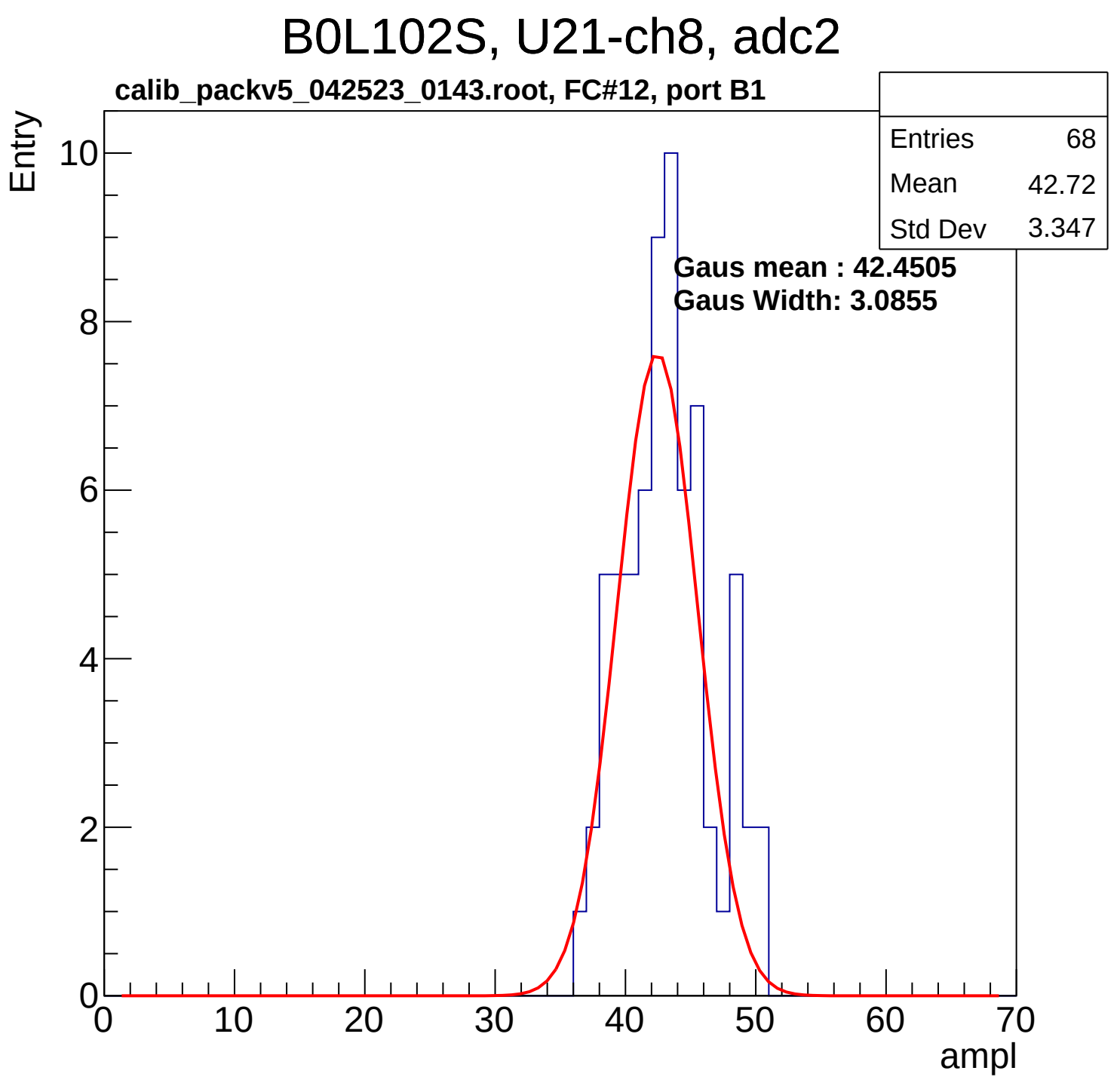
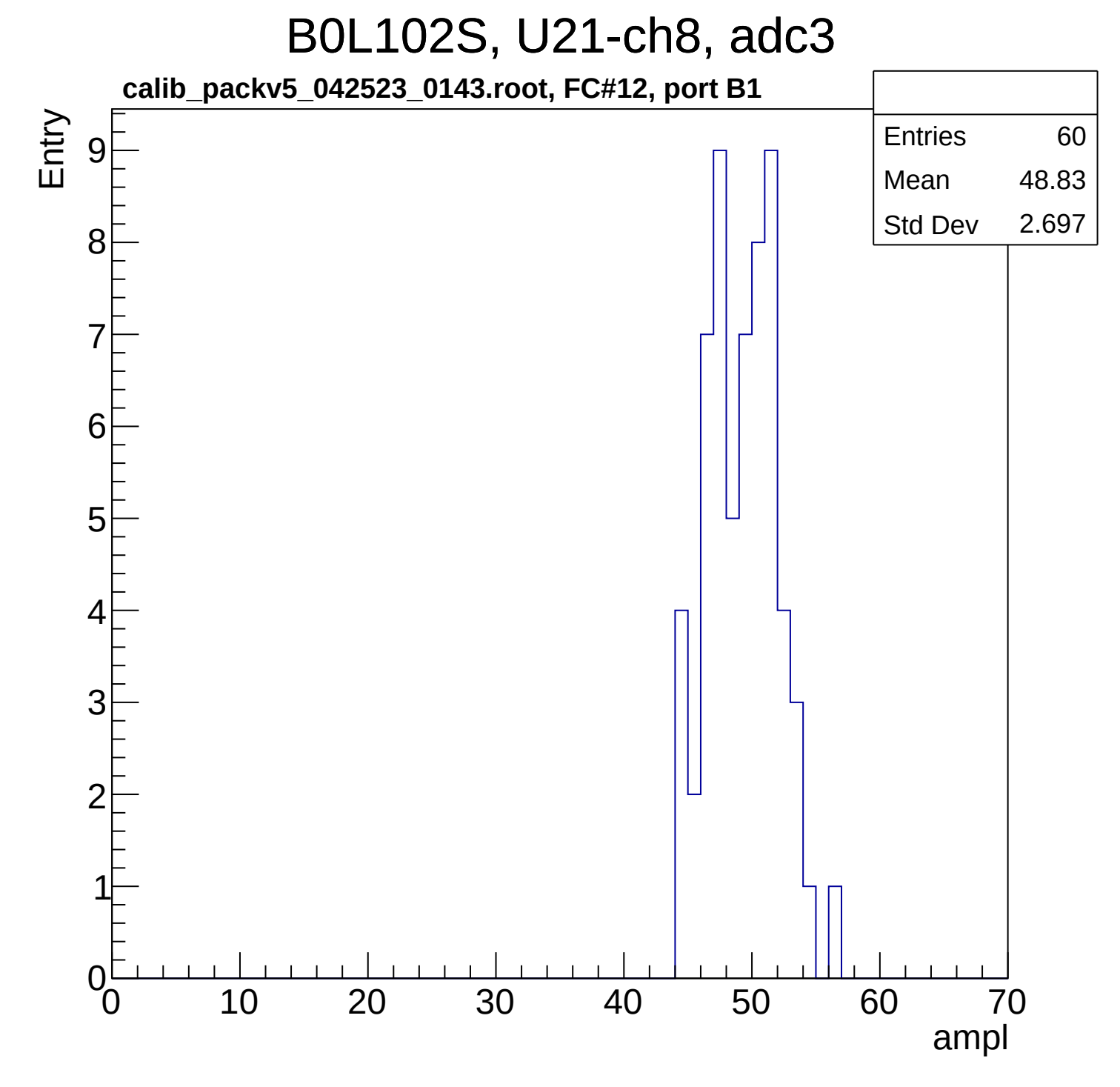

B0L102S, U21-ch8, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 42.72
Std Dev 3.347
Gaus mean : 42.4505
Gaus Width: 3.0855
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch8, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 48.83
Std Dev 2.697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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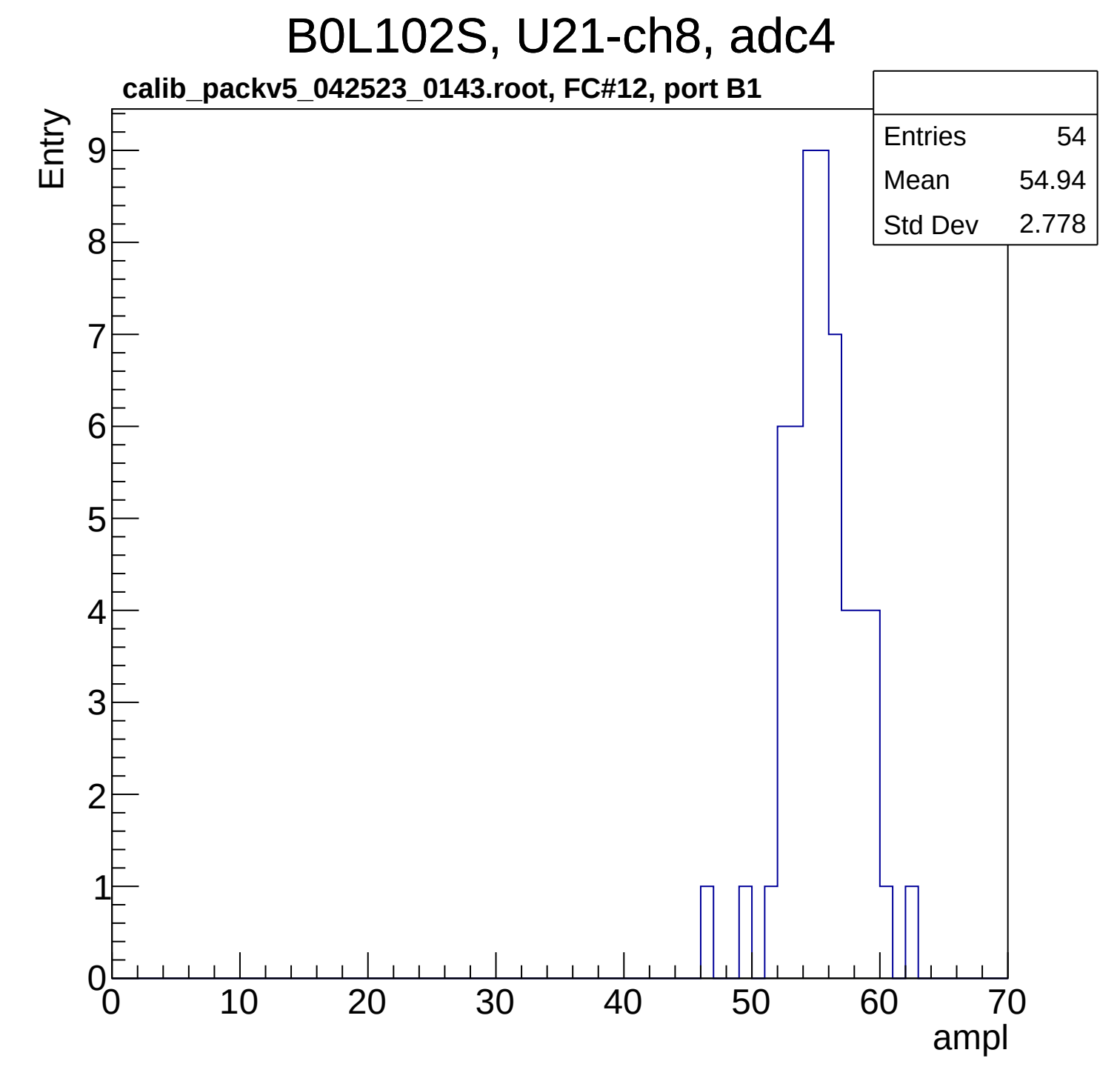
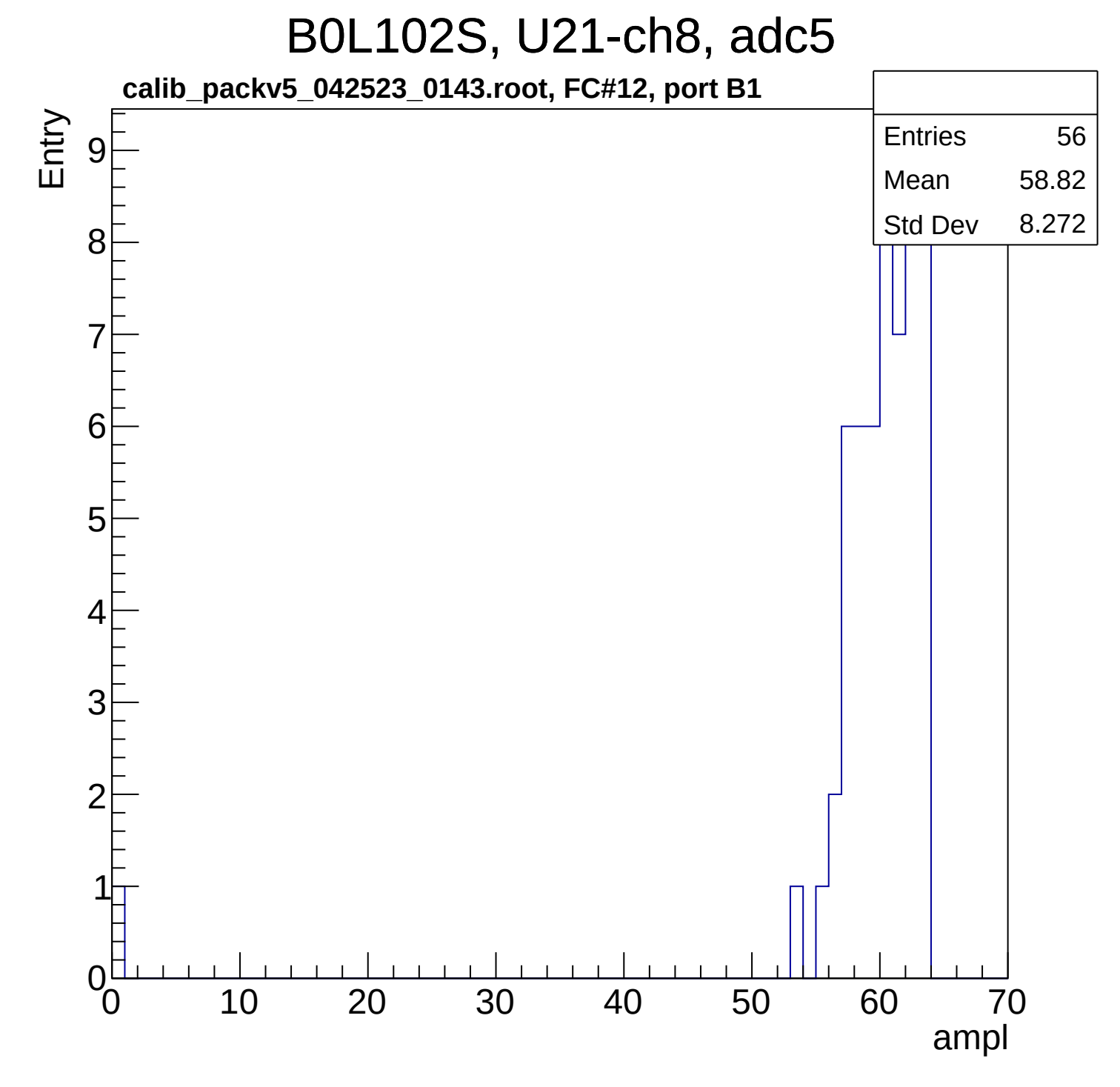

B0L102S, U21-ch8, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.94
Std Dev 2.778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch8, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 58.82
Std Dev 8.272
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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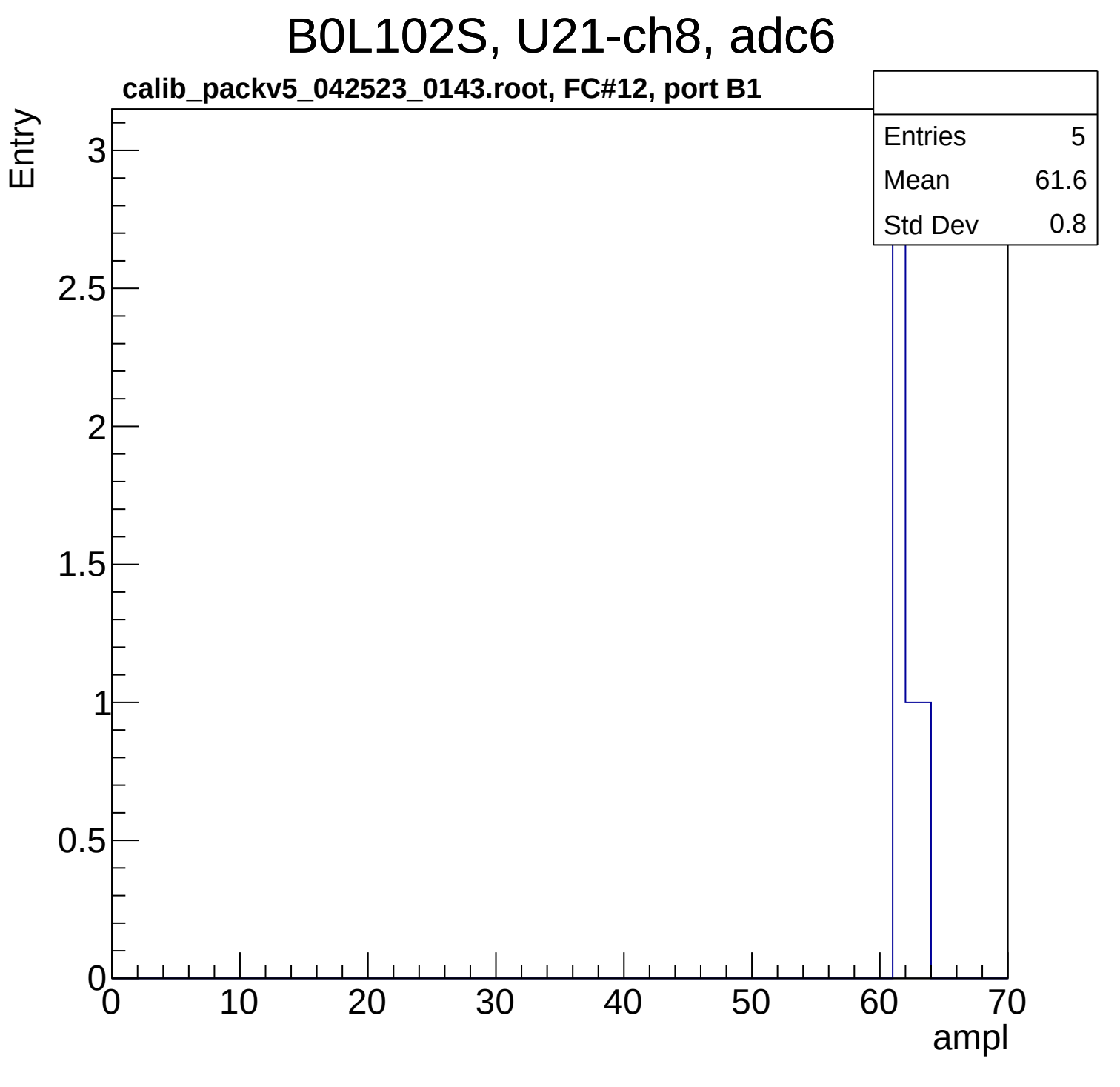
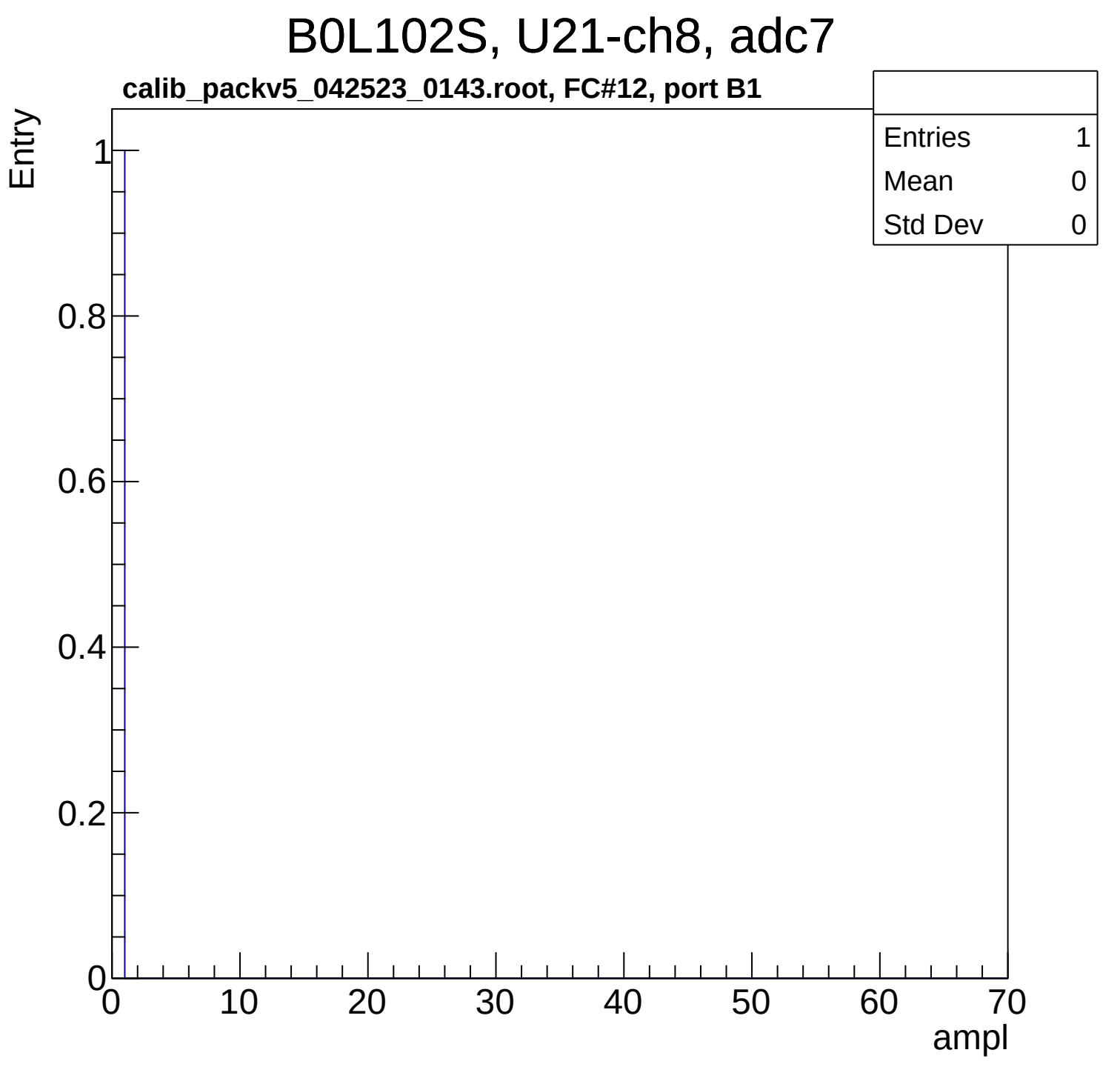
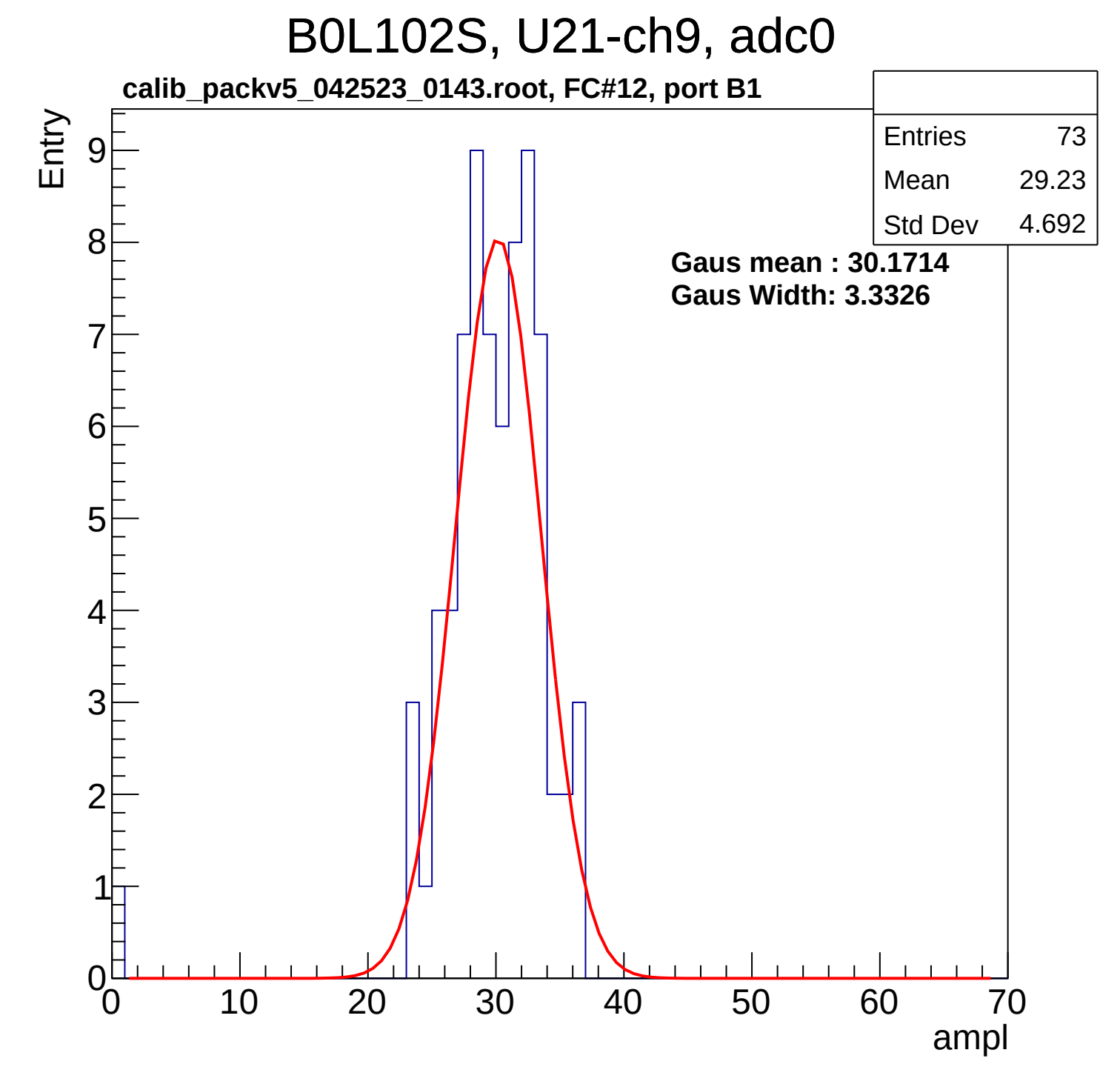
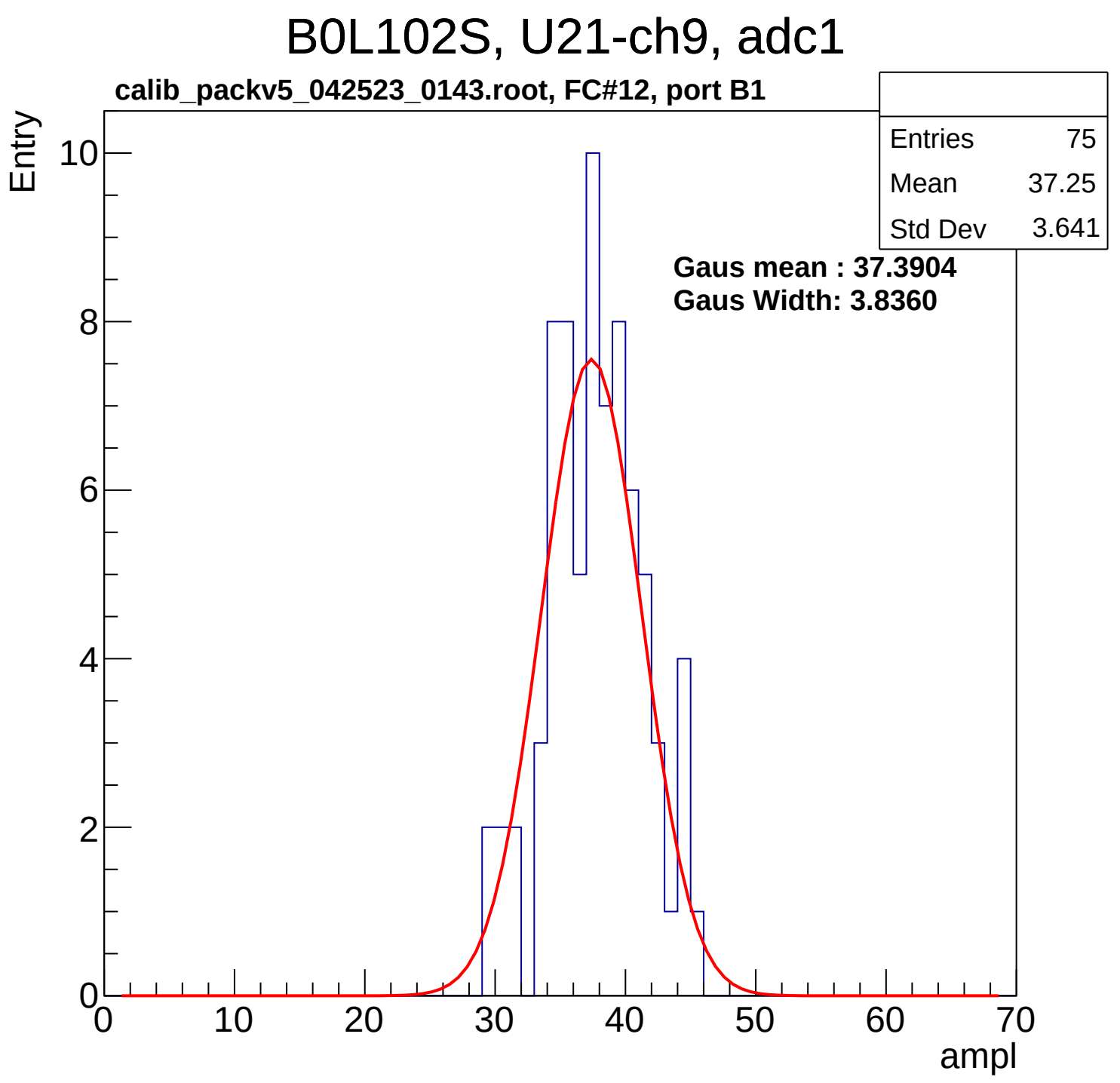
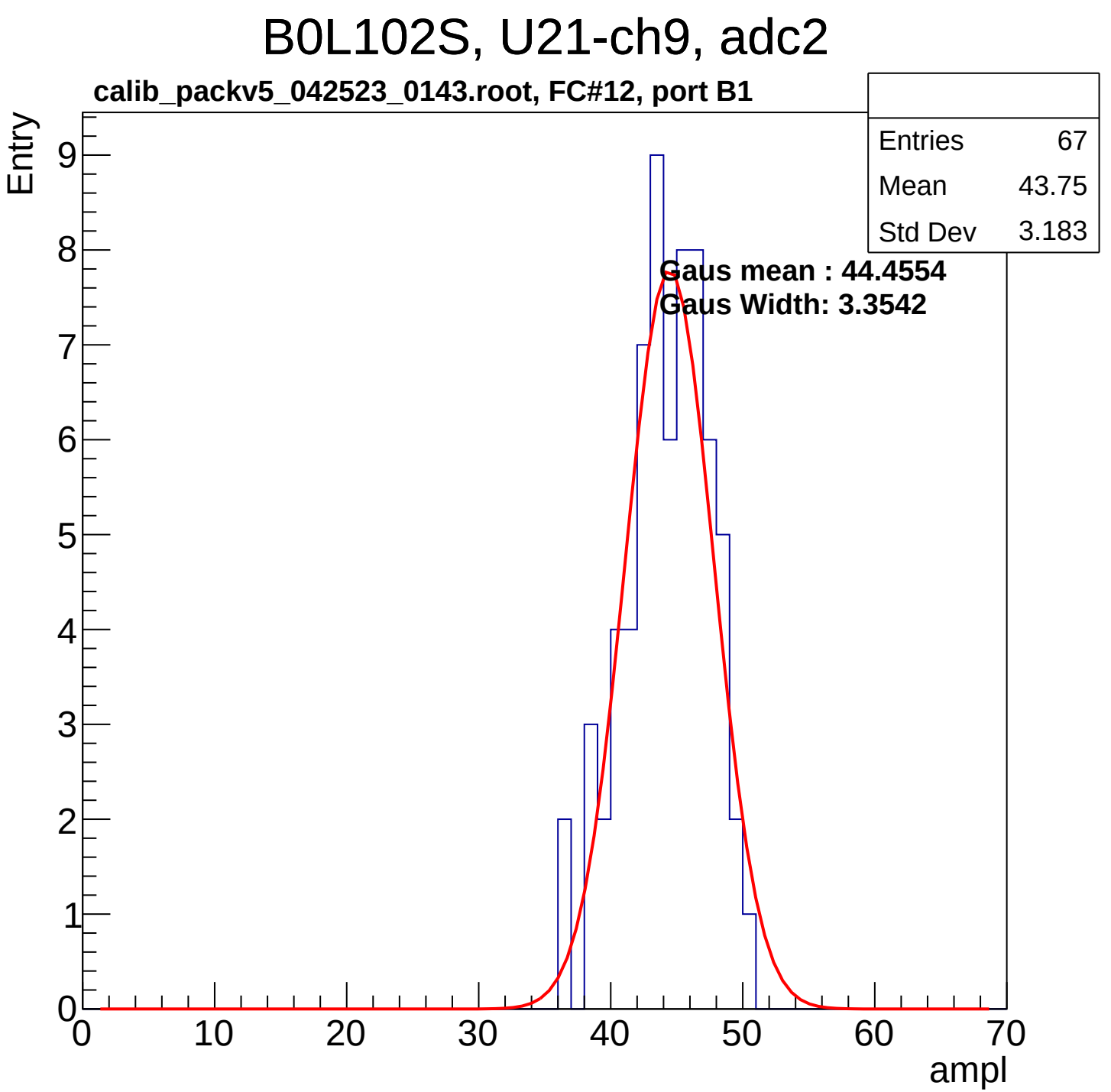

B0L102S, U21-ch8, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.6
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch8, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch9, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.23
Std Dev 4.692
Gaus mean : 30.1714
Gaus Width: 3.3326
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch9, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.25
Std Dev 3.641
Gaus mean : 37.3904
Gaus Width: 3.8360
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch9, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 43.75
Std Dev 3.183
Gaus mean : 44.4554
Gaus Width: 3.3542
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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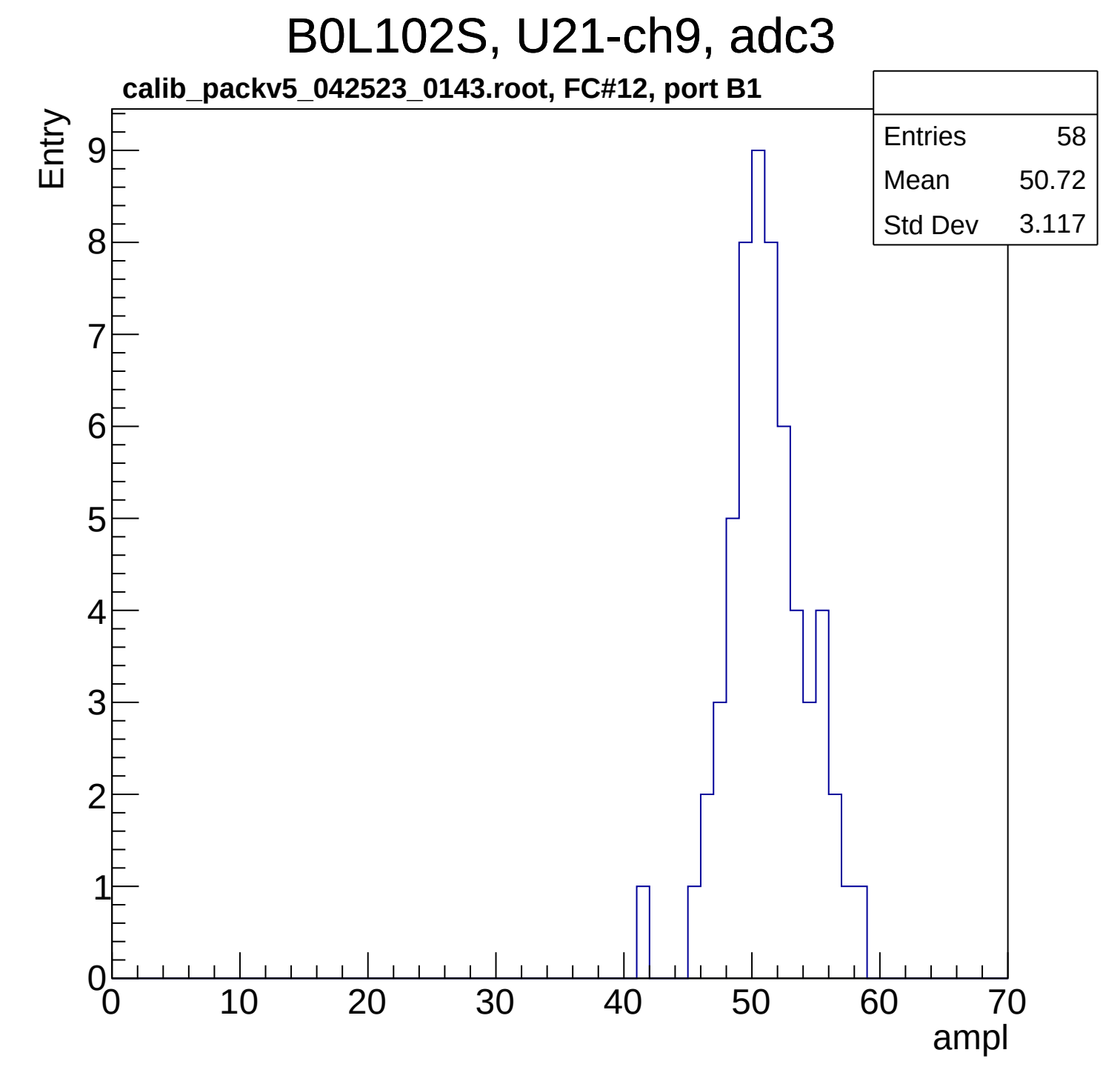
B0L102S, U21-ch9, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 50.72
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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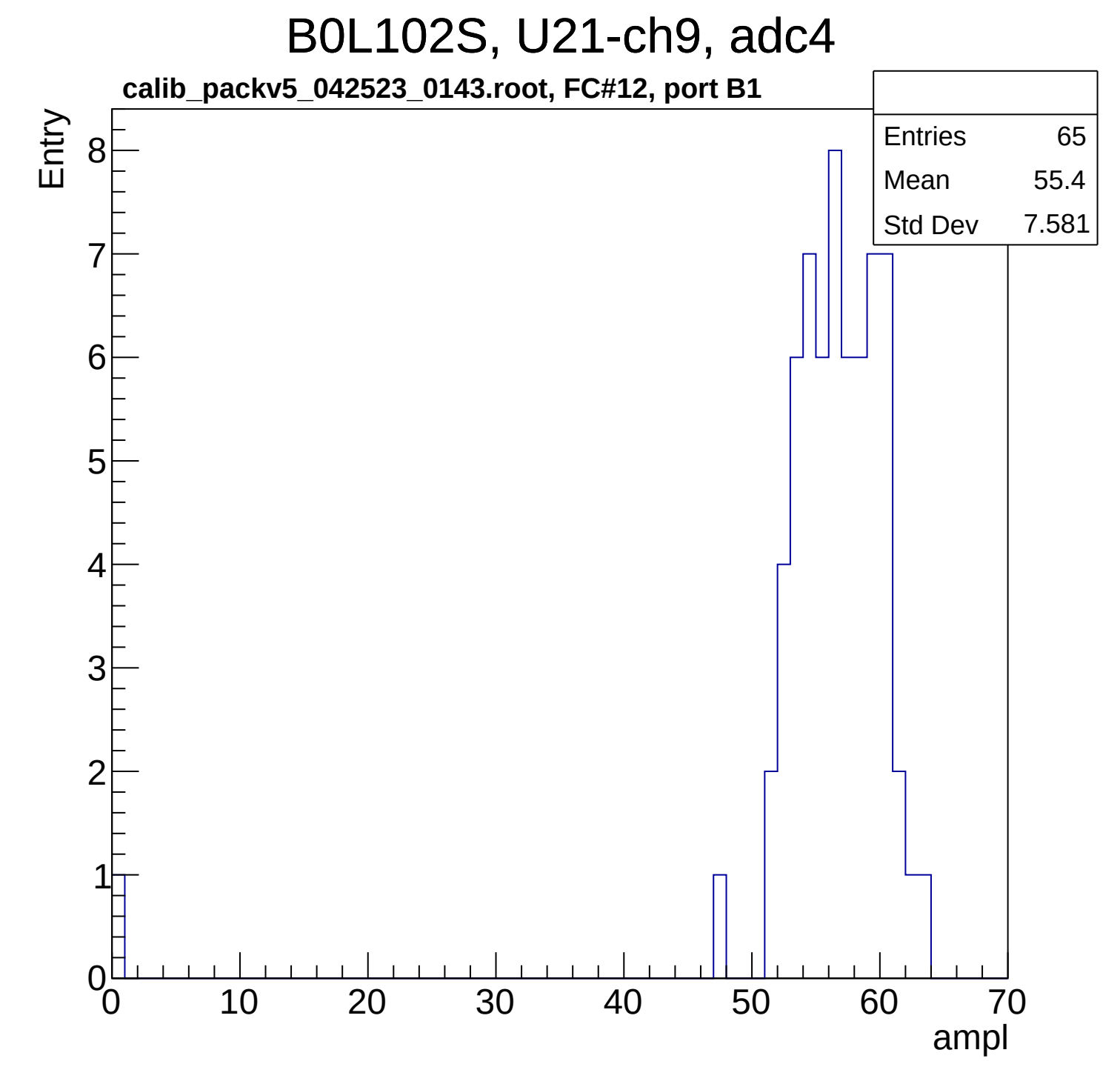
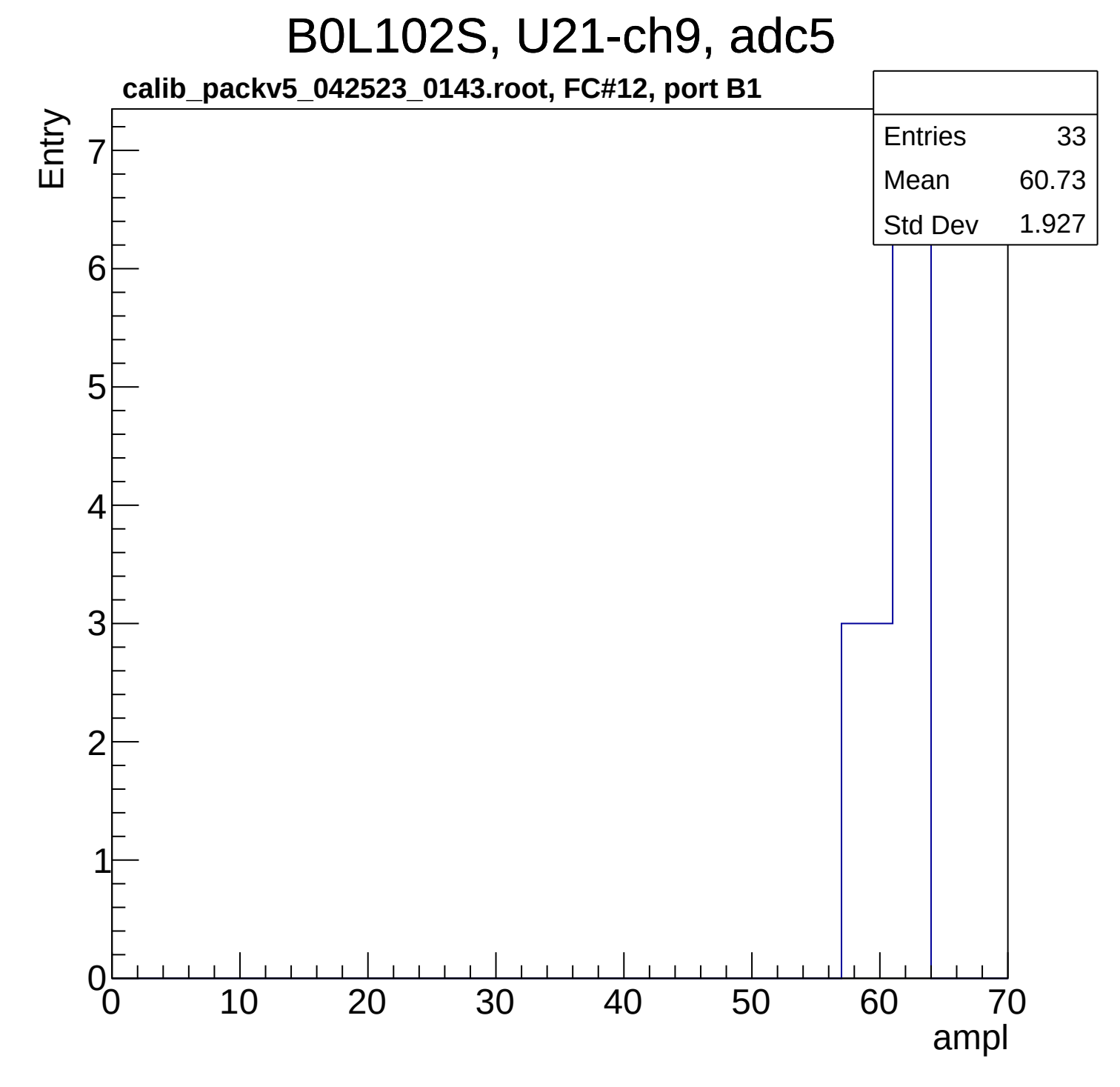
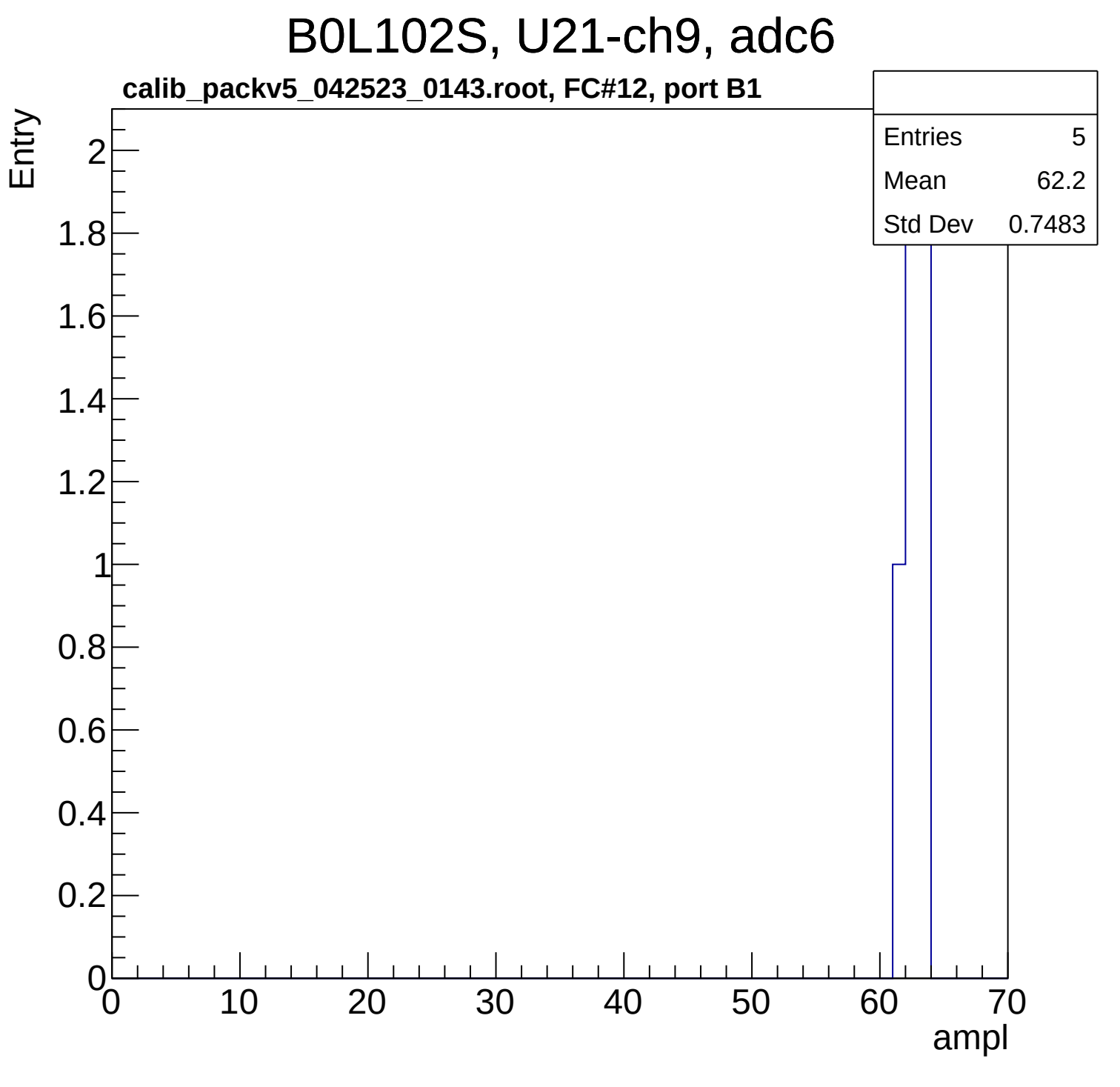

B0L102S, U21-ch9, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 55.4
Std Dev 7.581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch9, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 60.73
Std Dev 1.927
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch9, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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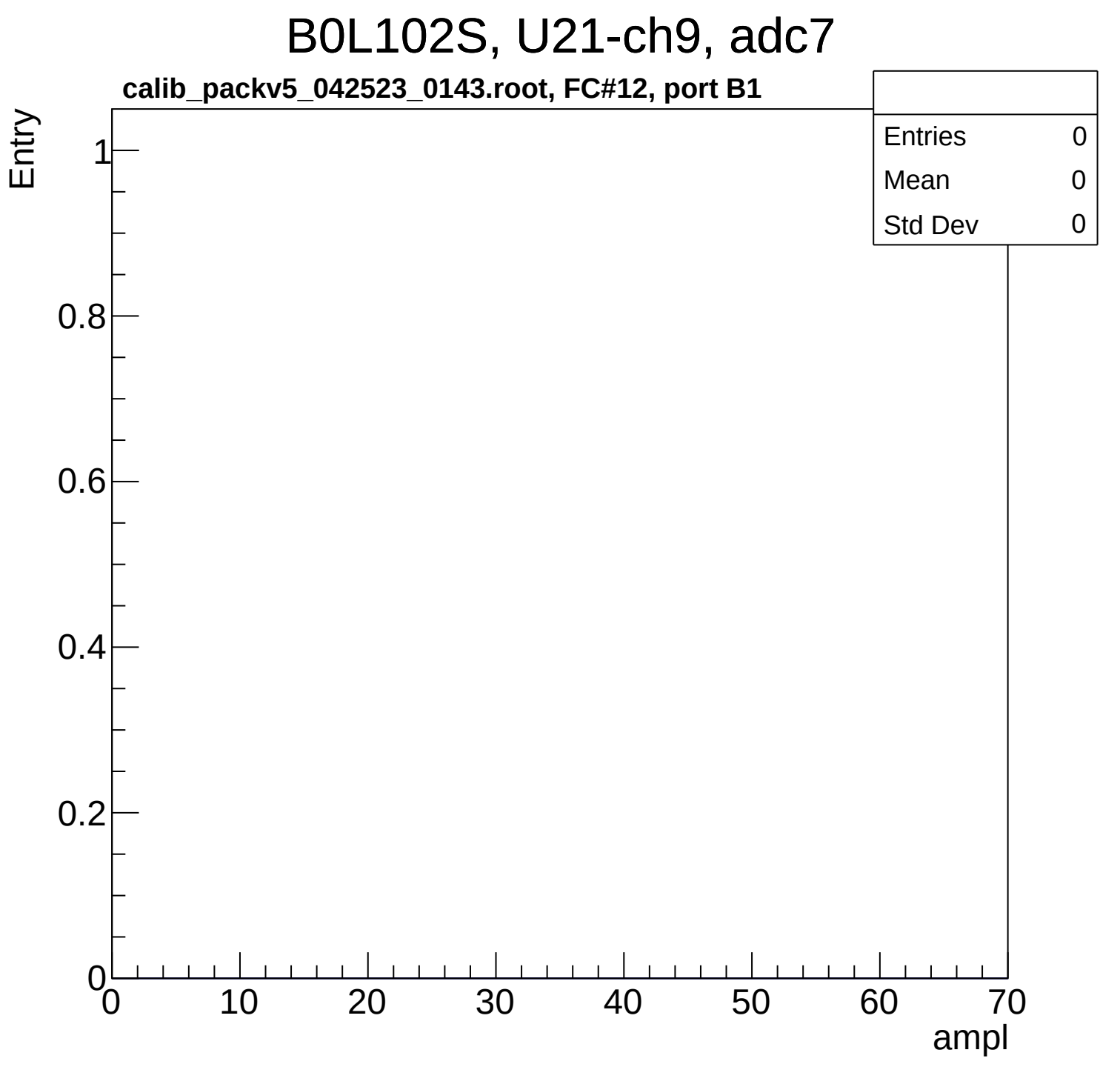
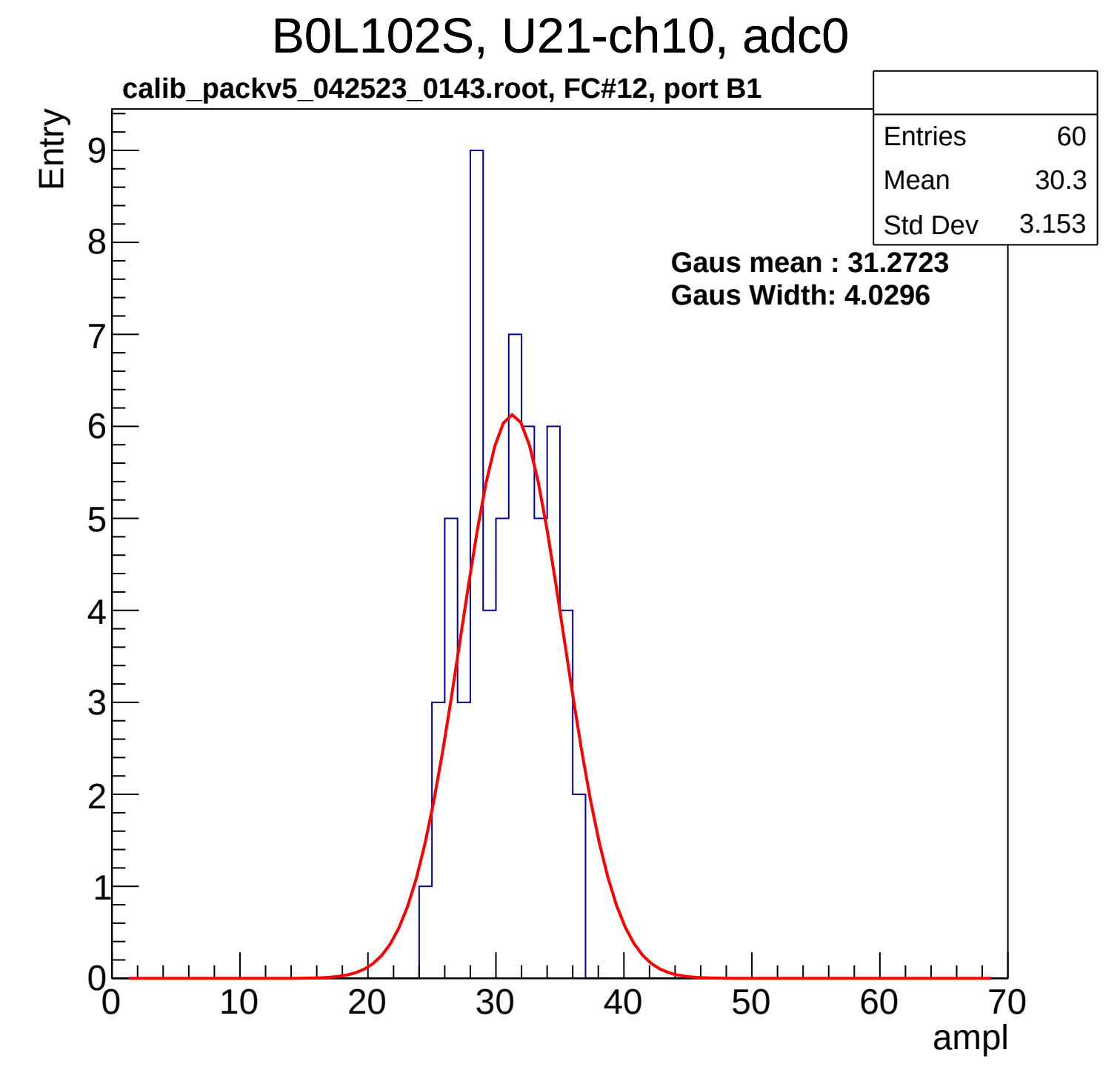
20
30
40
50
60
70


B0L102S, U21-ch9, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch10, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 30.3
Std Dev 3.153
Gaus mean : 31.2723
Gaus Width: 4.0296
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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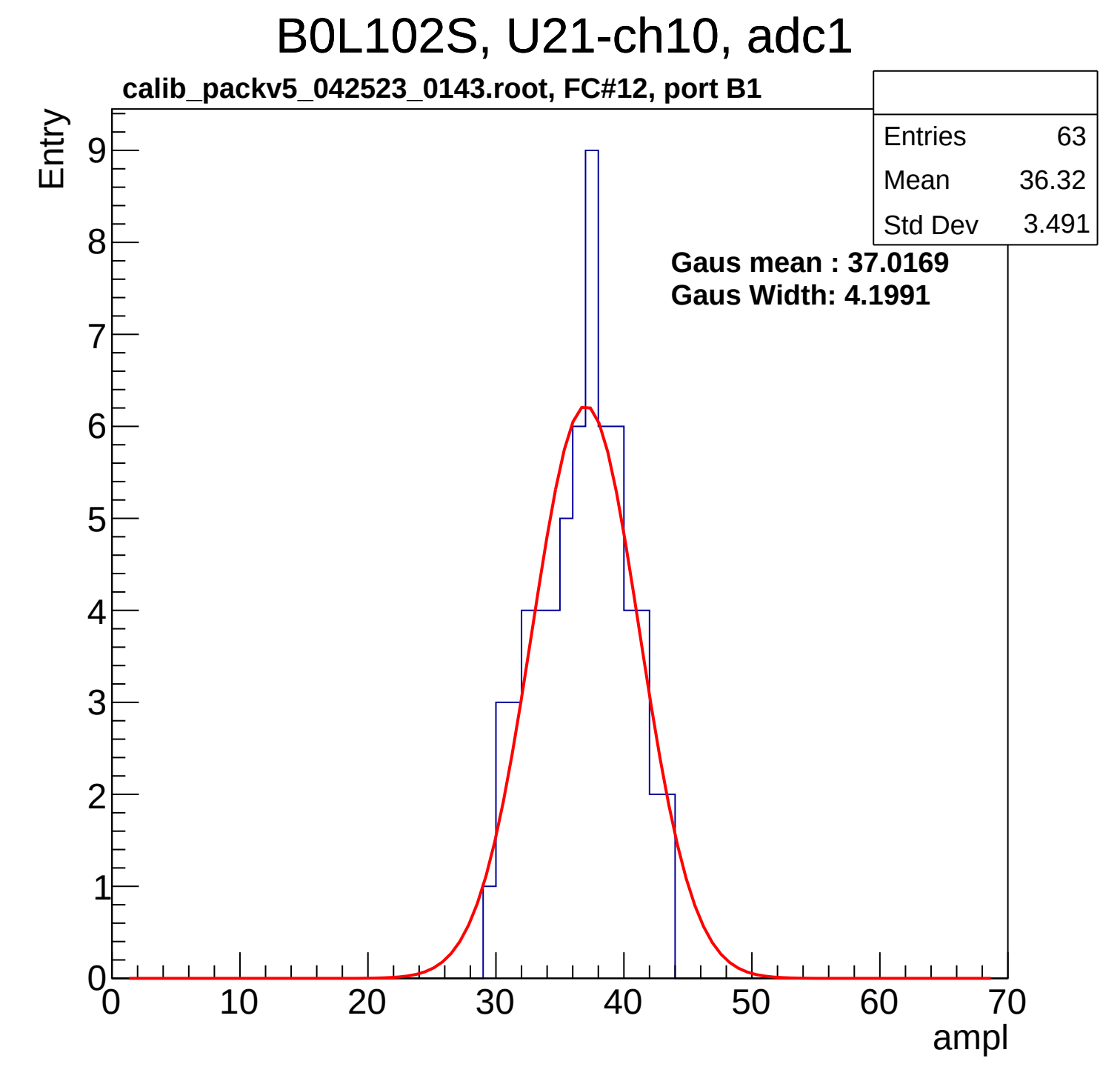
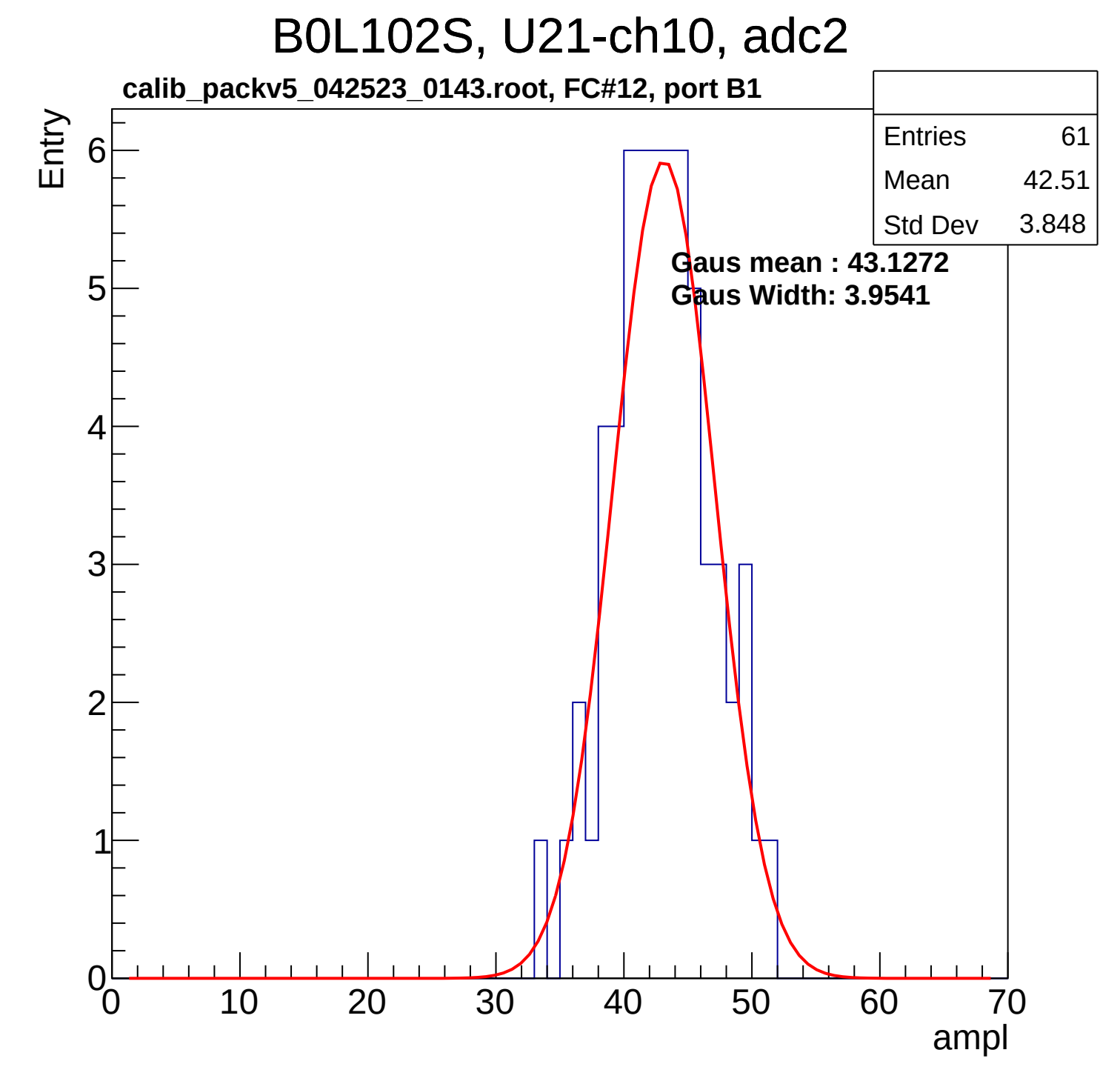

B0L102S, U21-ch10, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 36.32
Std Dev 3.491
Gaus mean : 37.0169
Gaus Width: 4.1991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch10, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 42.51
Std Dev 3.848
Gaus mean : 43.1272
Gaus Width: 3.9541
Entry
ampl
0
1
2
3
4
5
6
0
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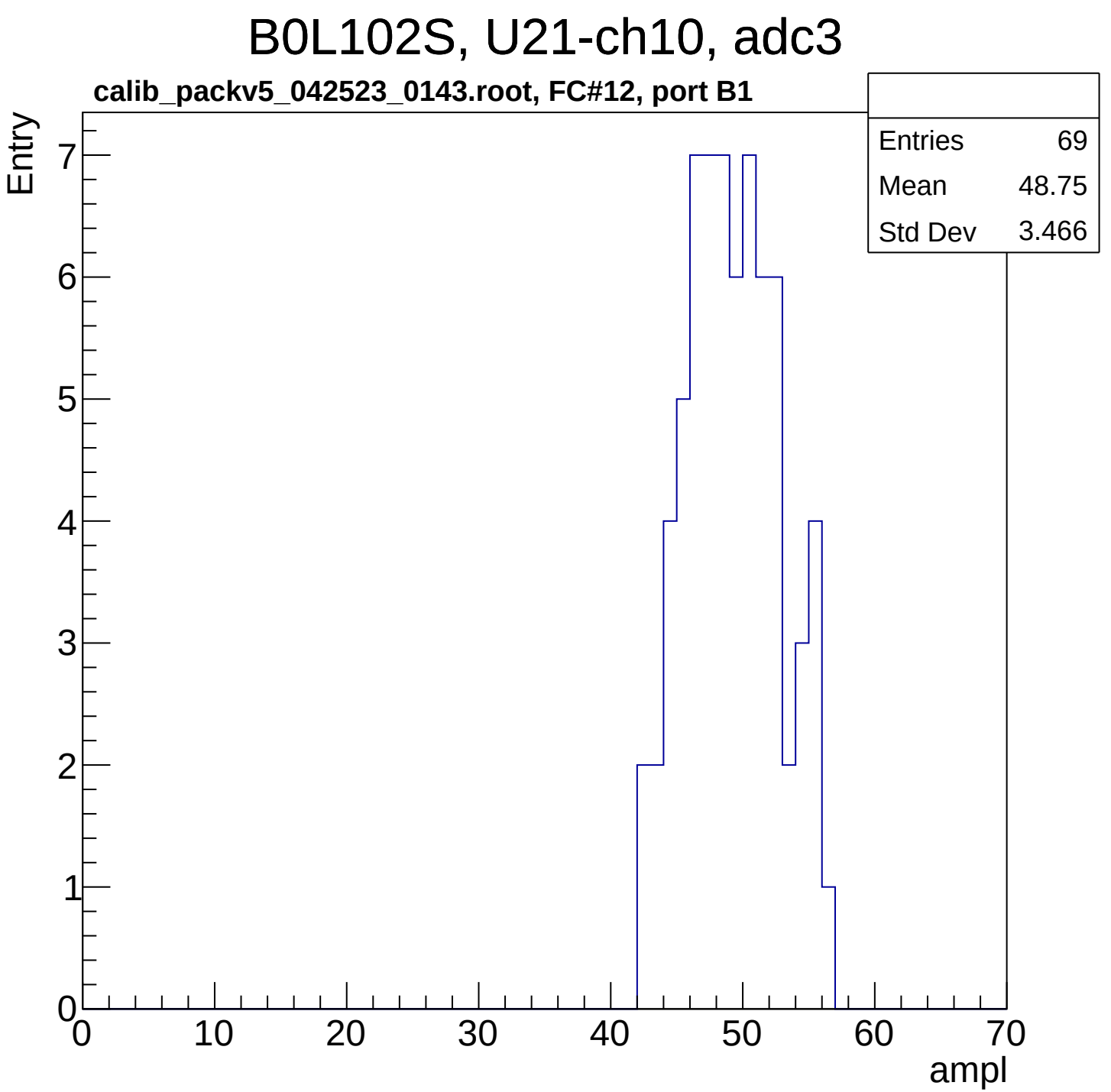
20
30
40
50
60
70


B0L102S, U21-ch10, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 48.75
Std Dev 3.466
Entry
ampl
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4
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6
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0
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60
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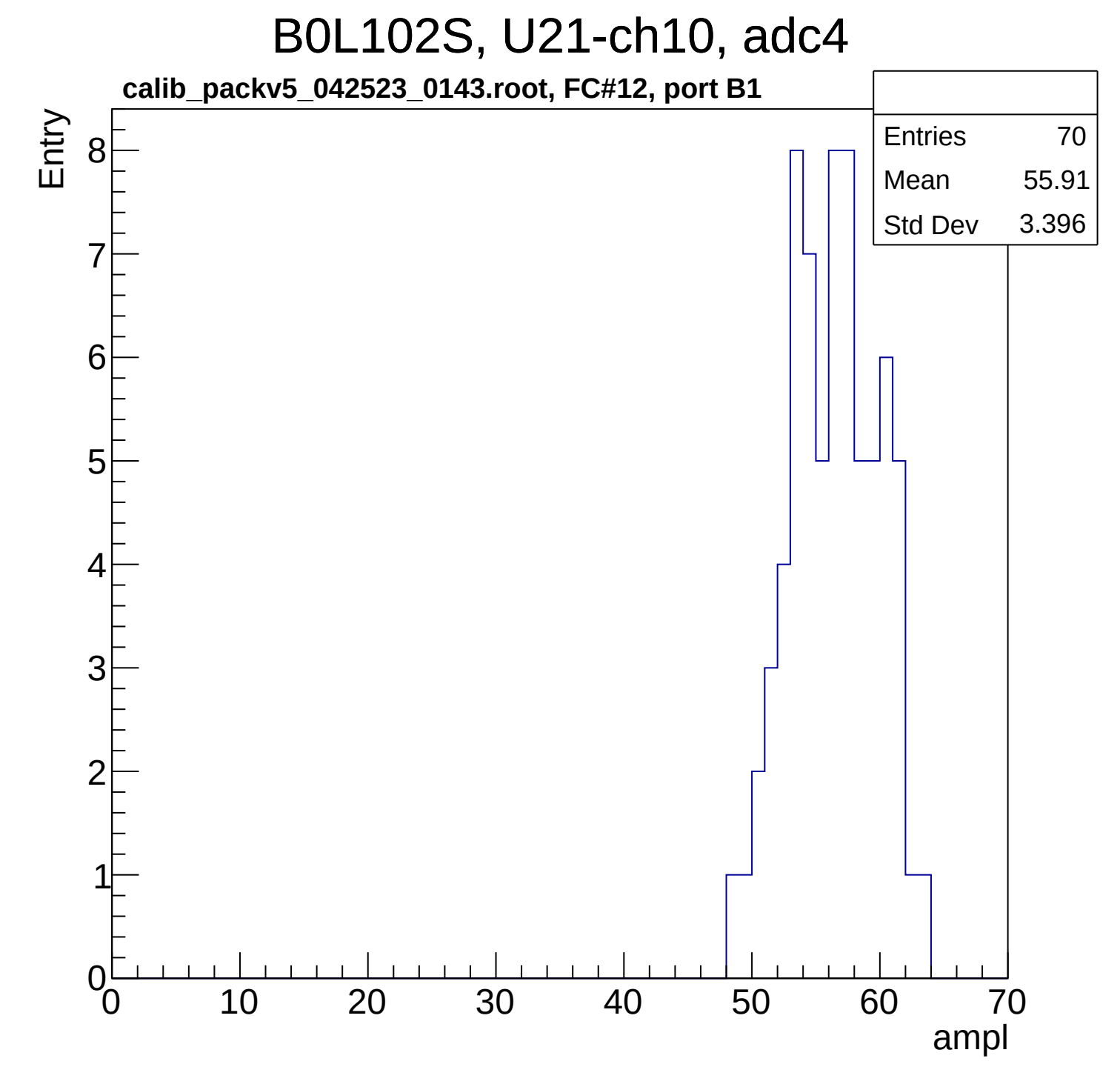
B0L102S, U21-ch10, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 55.91
Std Dev 3.396
Entry
ampl
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1
2
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4
5
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7
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0
10
20
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40
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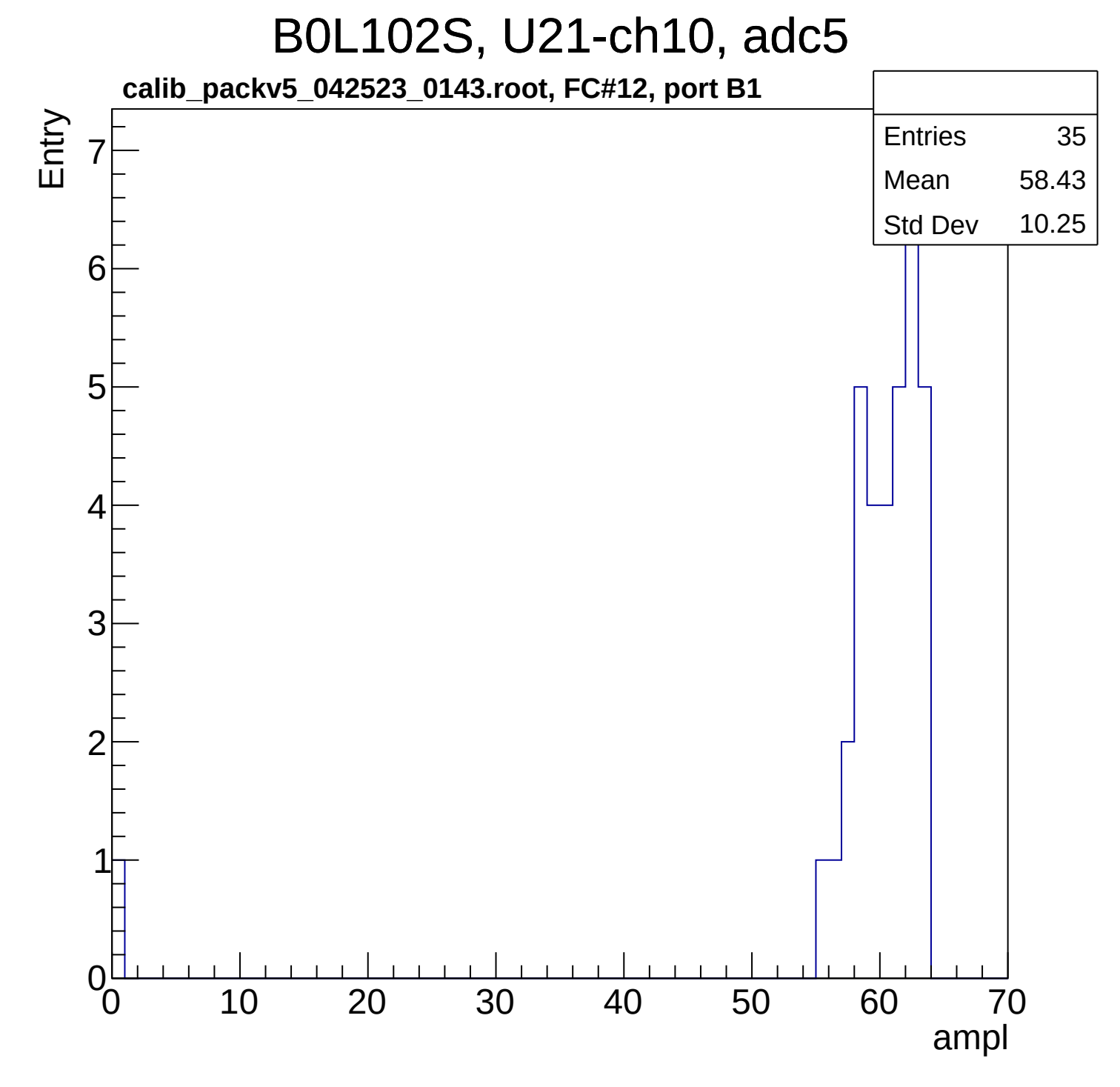
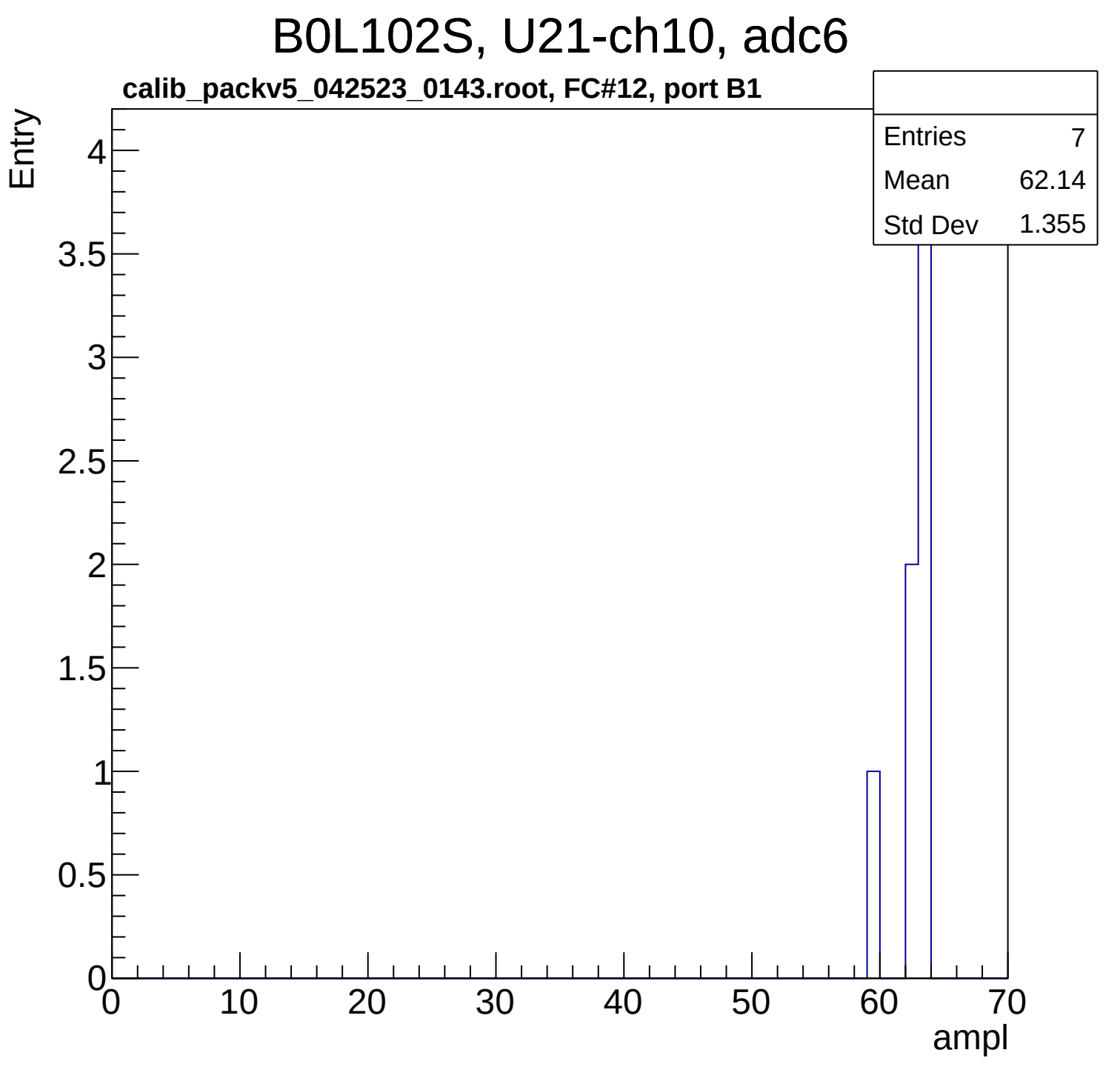
60
70


B0L102S, U21-ch10, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 58.43
Std Dev 10.25
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch10, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.14
Std Dev 1.355
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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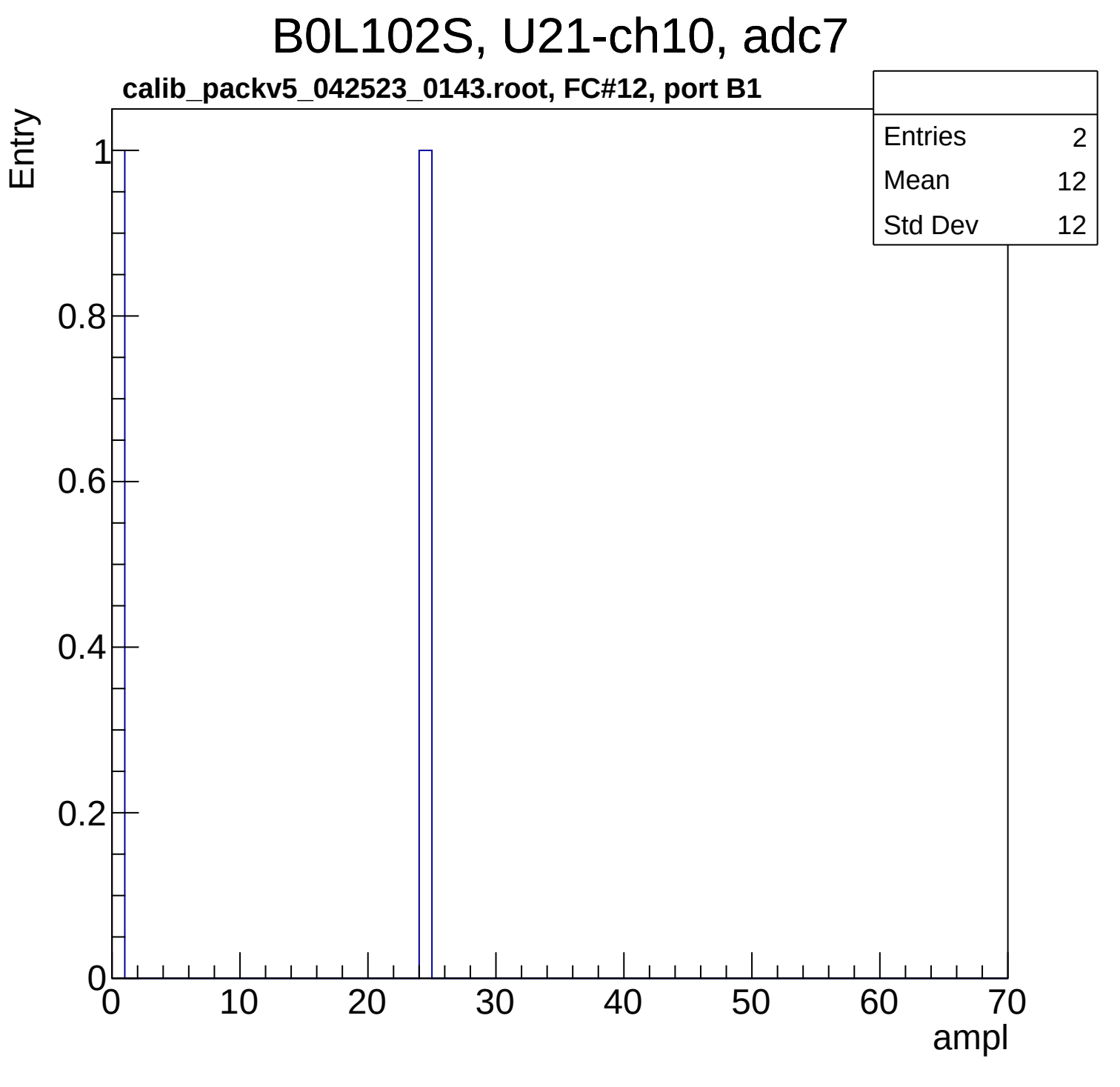
50
60
70


B0L102S, U21-ch10, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
0.2
0
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10
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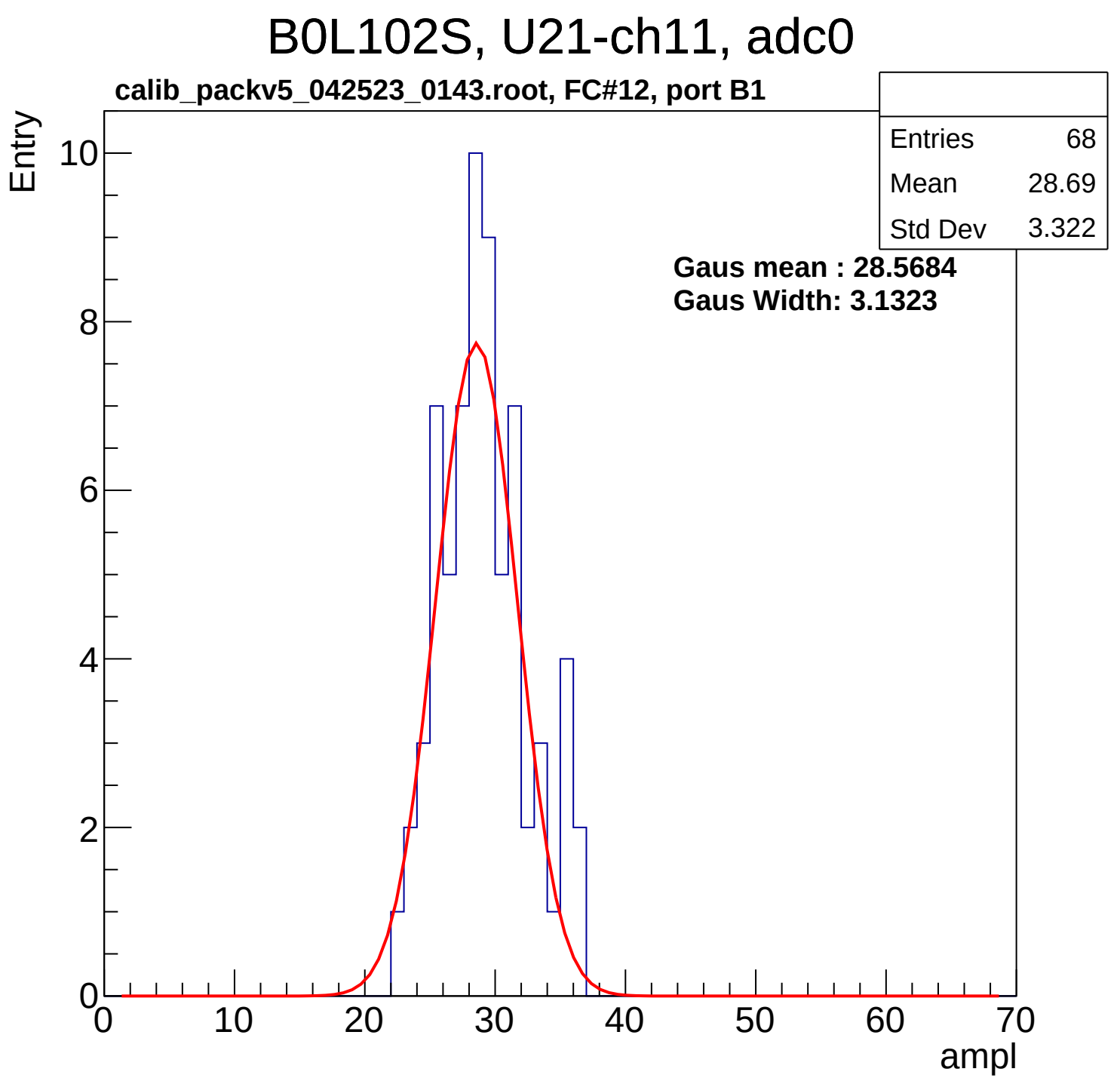
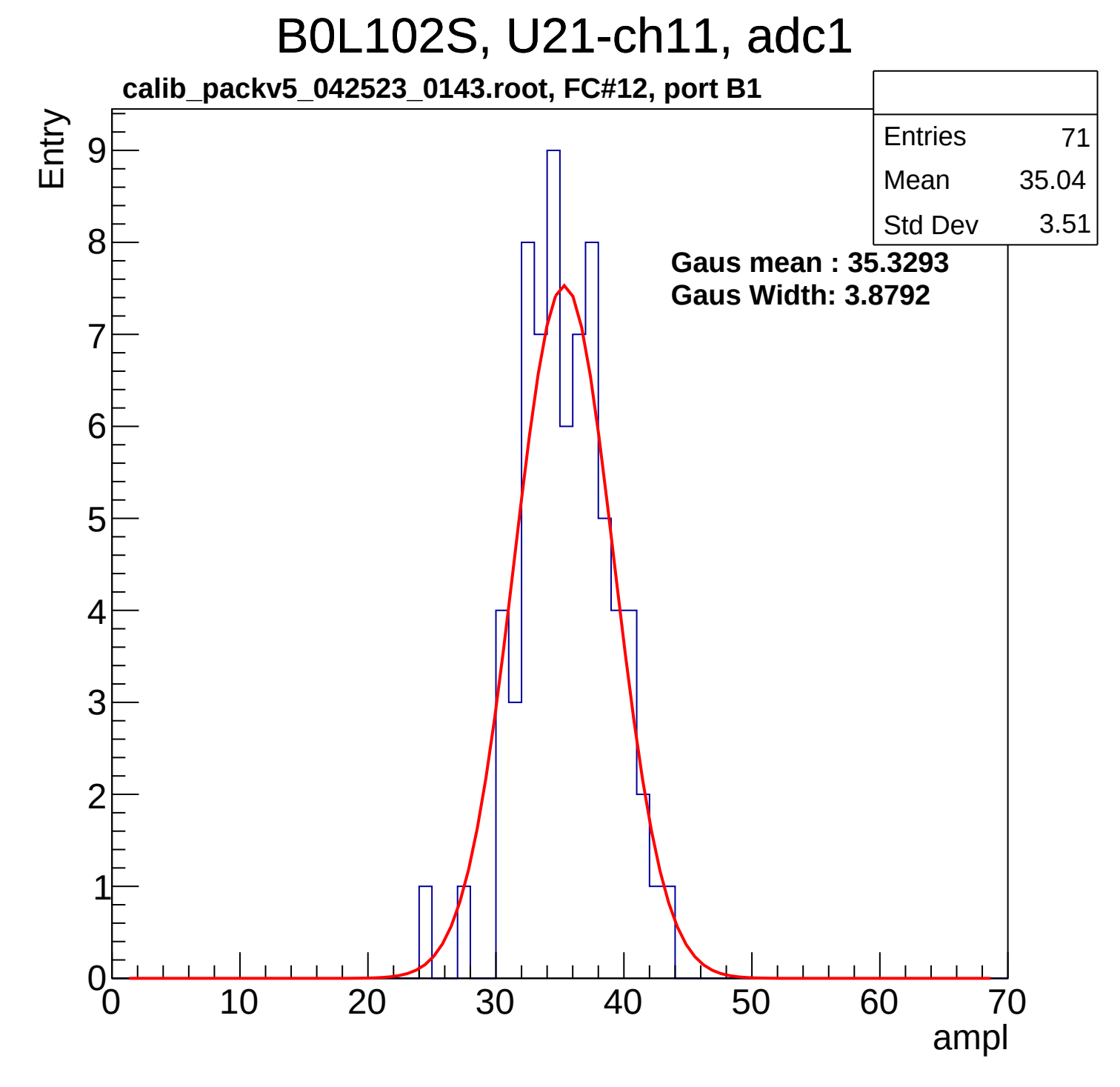
30
40
50
60
70
ampl


B0L102S, U21-ch11, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 28.69
Std Dev 3.322
Gaus mean : 28.5684
Gaus Width: 3.1323
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch11, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 35.04
Std Dev 3.51
Gaus mean : 35.3293
Gaus Width: 3.8792
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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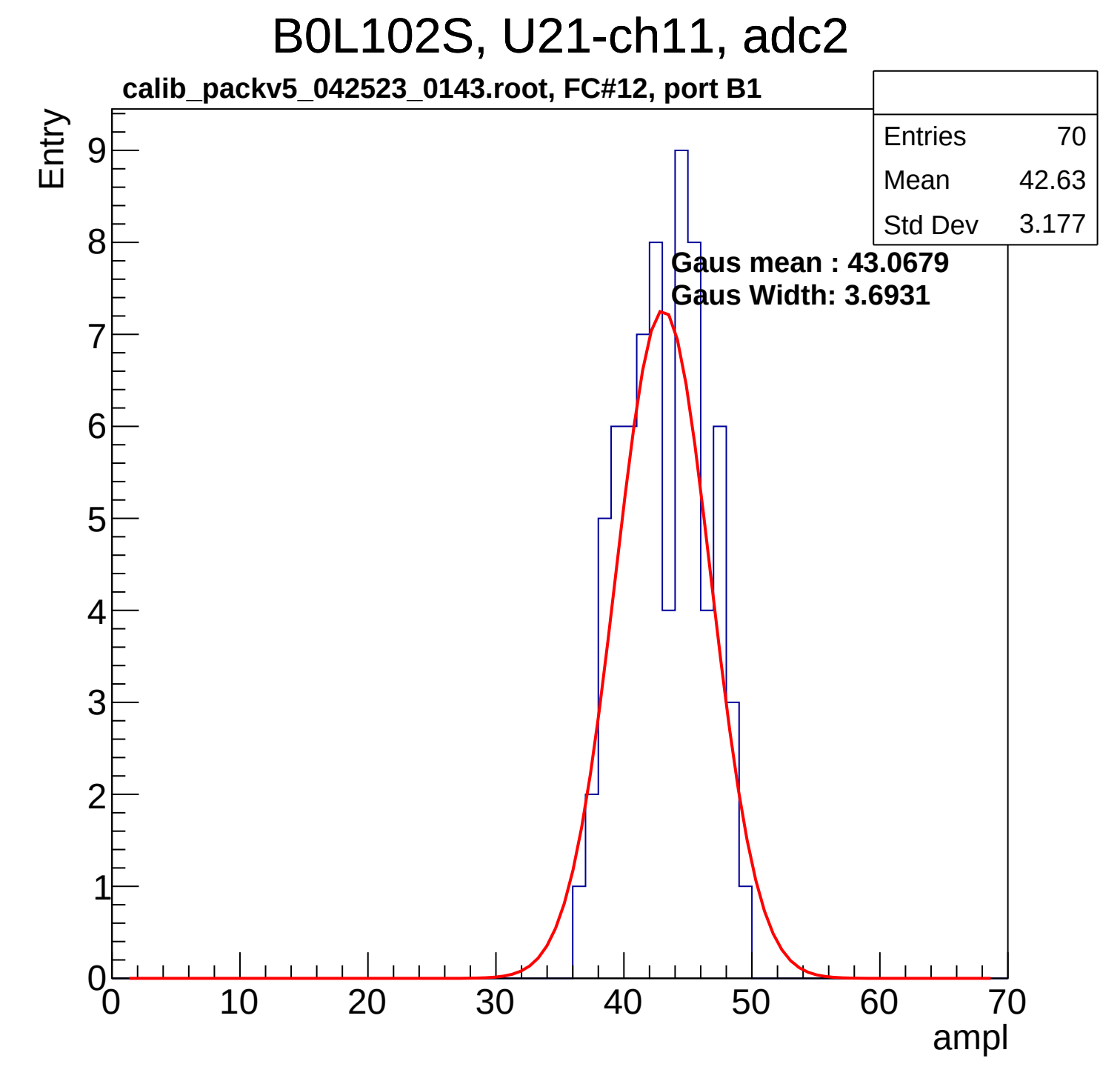
B0L102S, U21-ch11, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 42.63
Std Dev 3.177
Gaus mean : 43.0679
Gaus Width: 3.6931
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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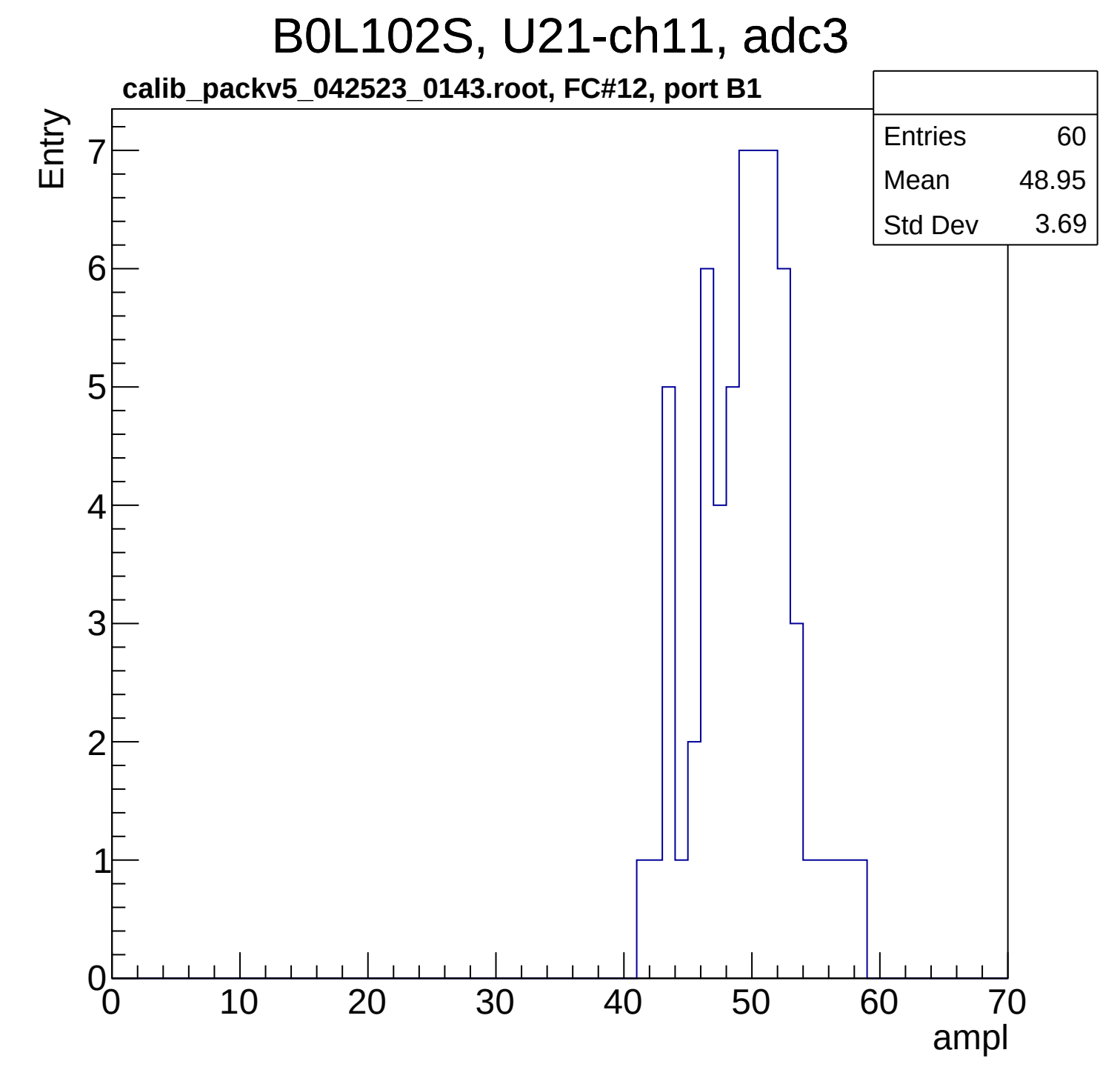
B0L102S, U21-ch11, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 48.95
Std Dev 3.69
Entry
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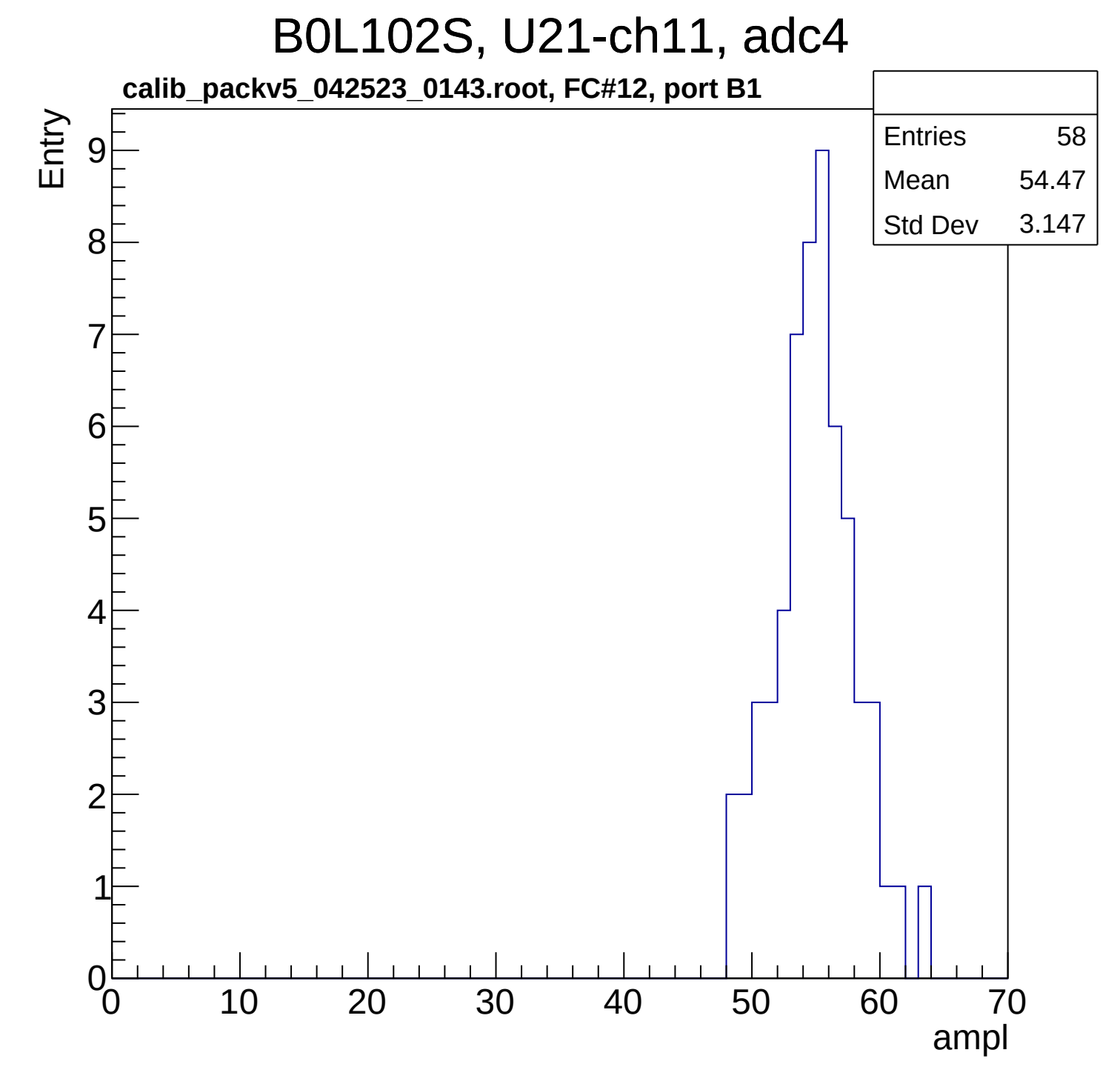
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch11, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 54.47
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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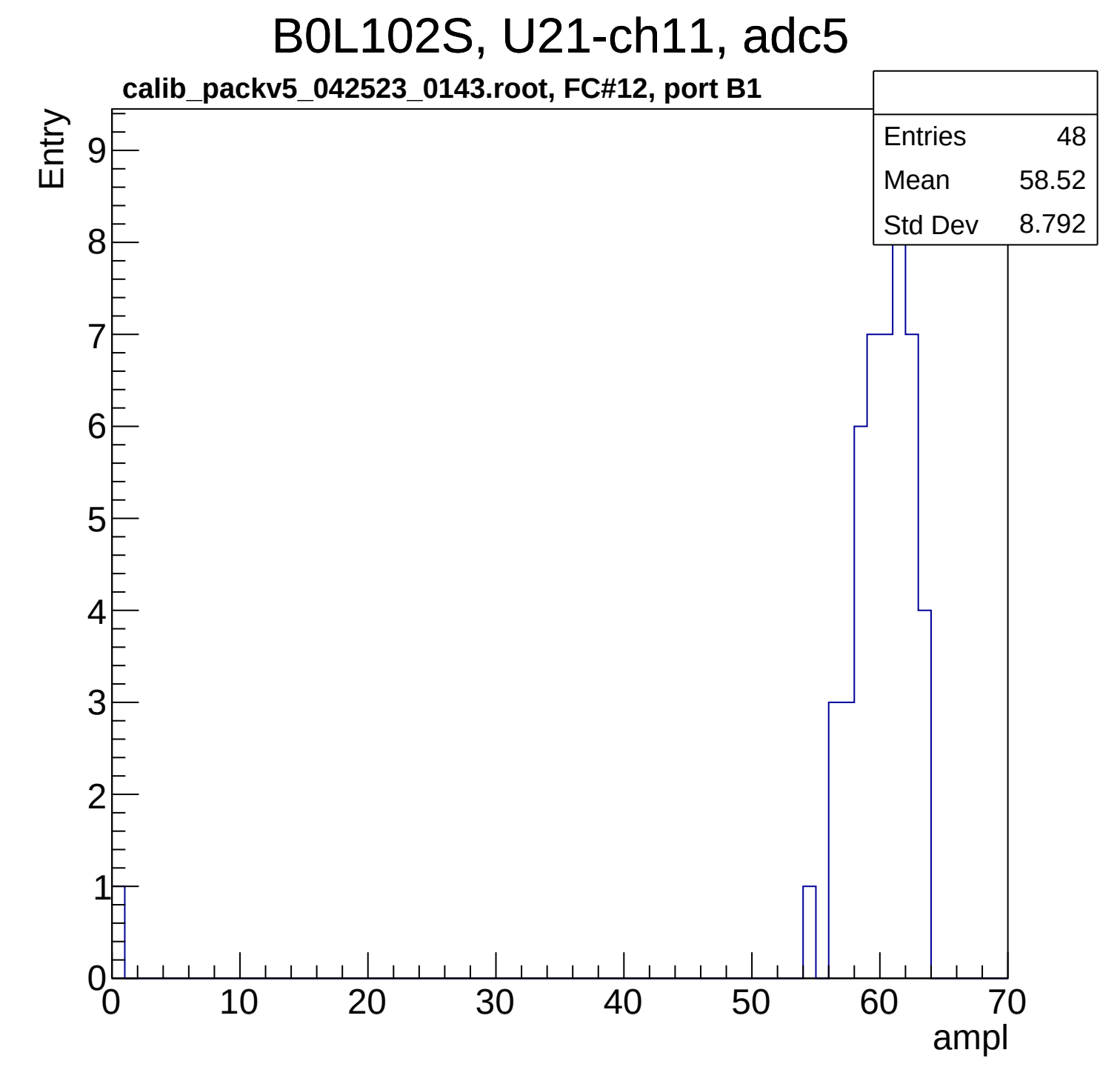
60
70


B0L102S, U21-ch11, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.52
Std Dev 8.792
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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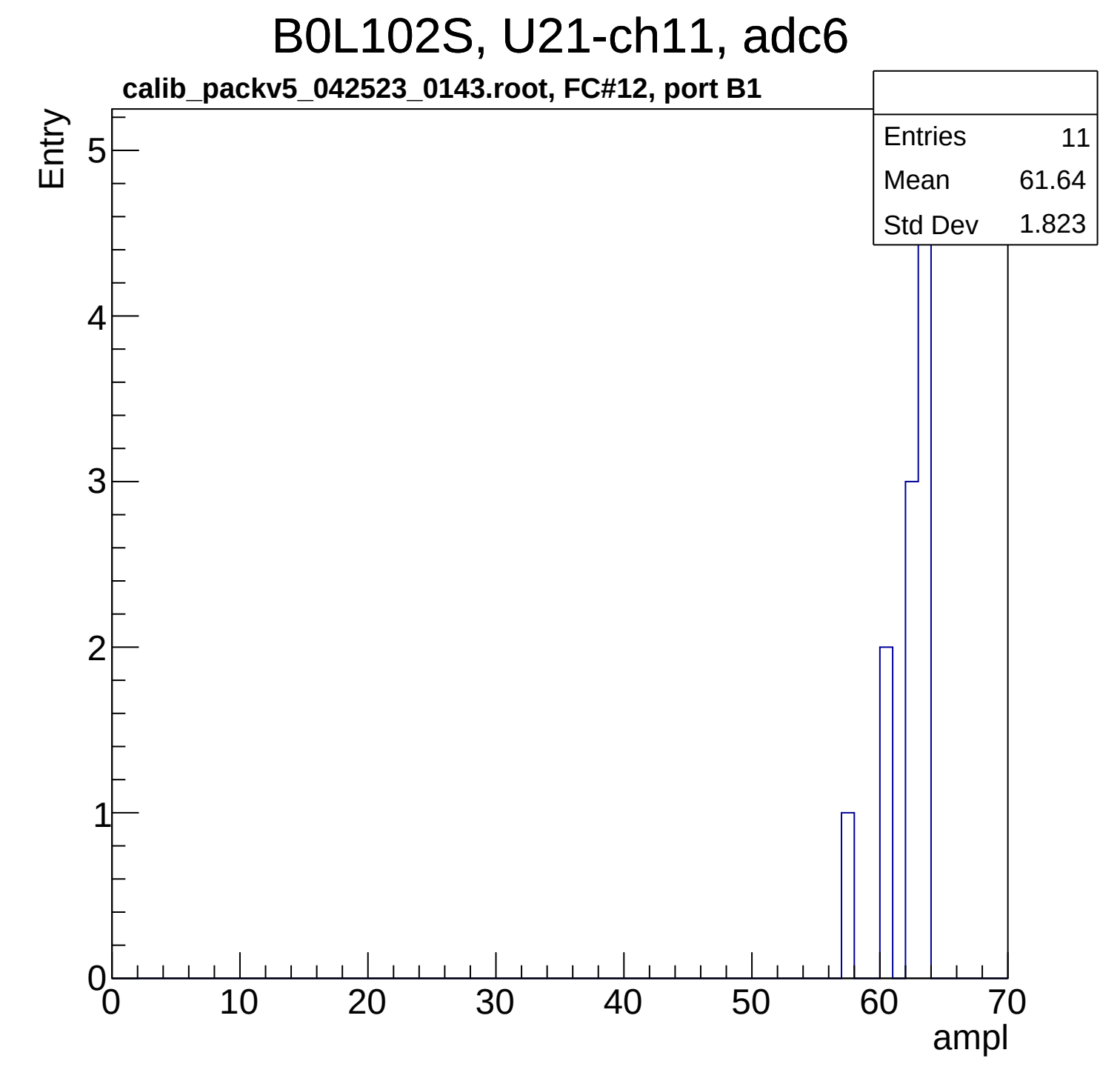
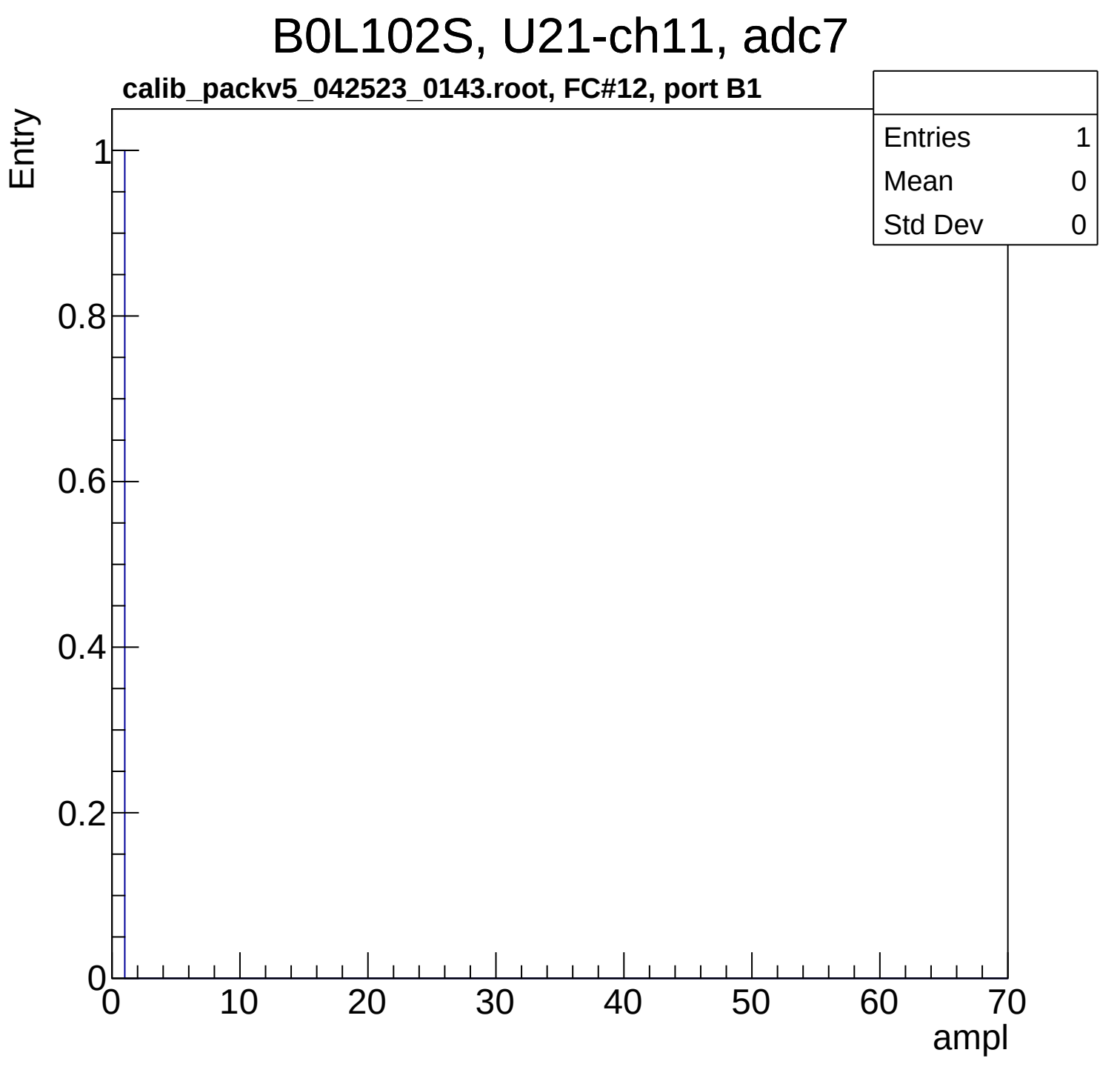

B0L102S, U21-ch11, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61.64
Std Dev 1.823
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch11, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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30
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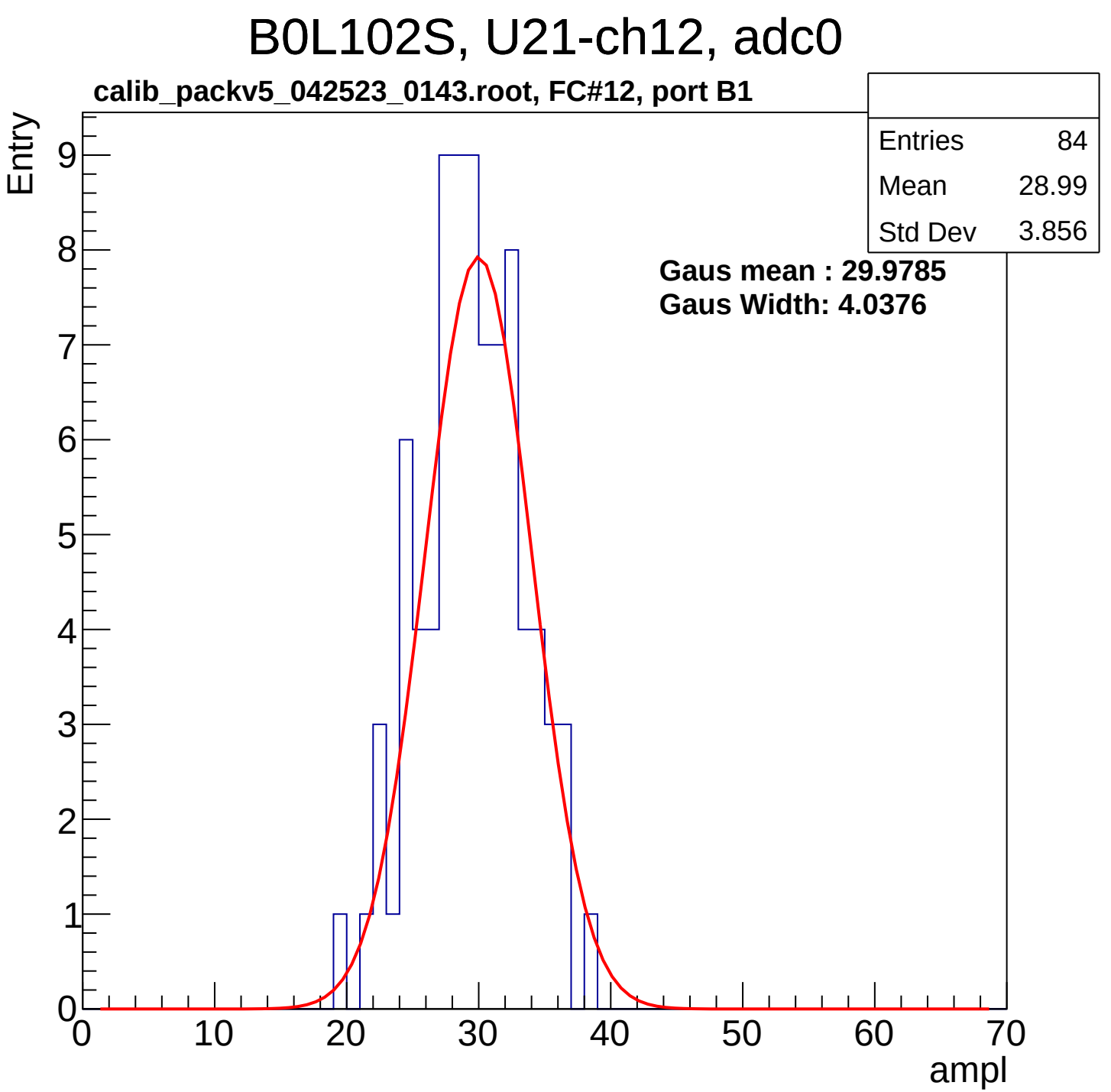
50
60
70
ampl


B0L102S, U21-ch12, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 28.99
Std Dev 3.856
Gaus mean : 29.9785
Gaus Width: 4.0376
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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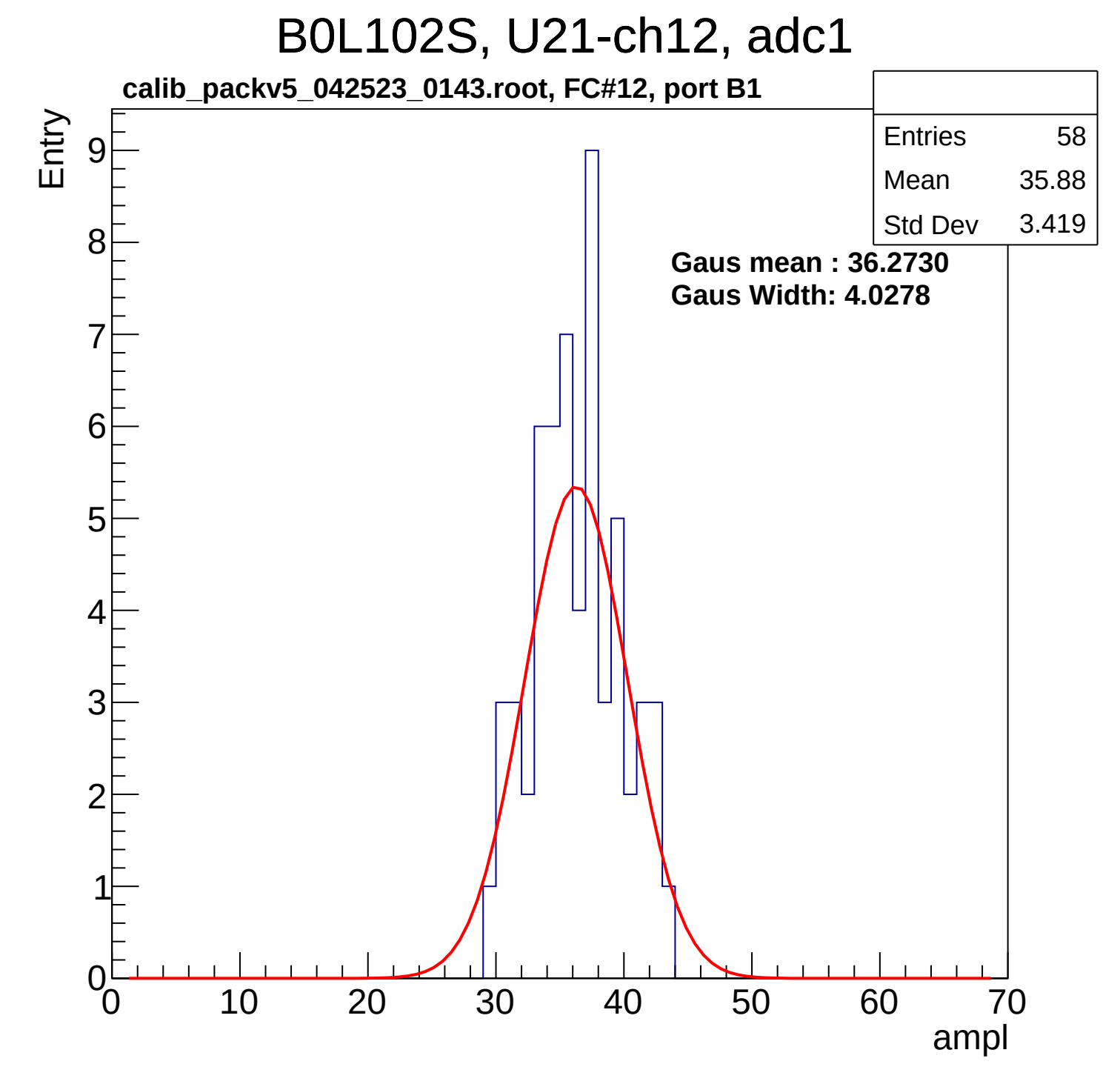
B0L102S, U21-ch12, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 35.88
Std Dev 3.419
Gaus mean : 36.2730
Gaus Width: 4.0278
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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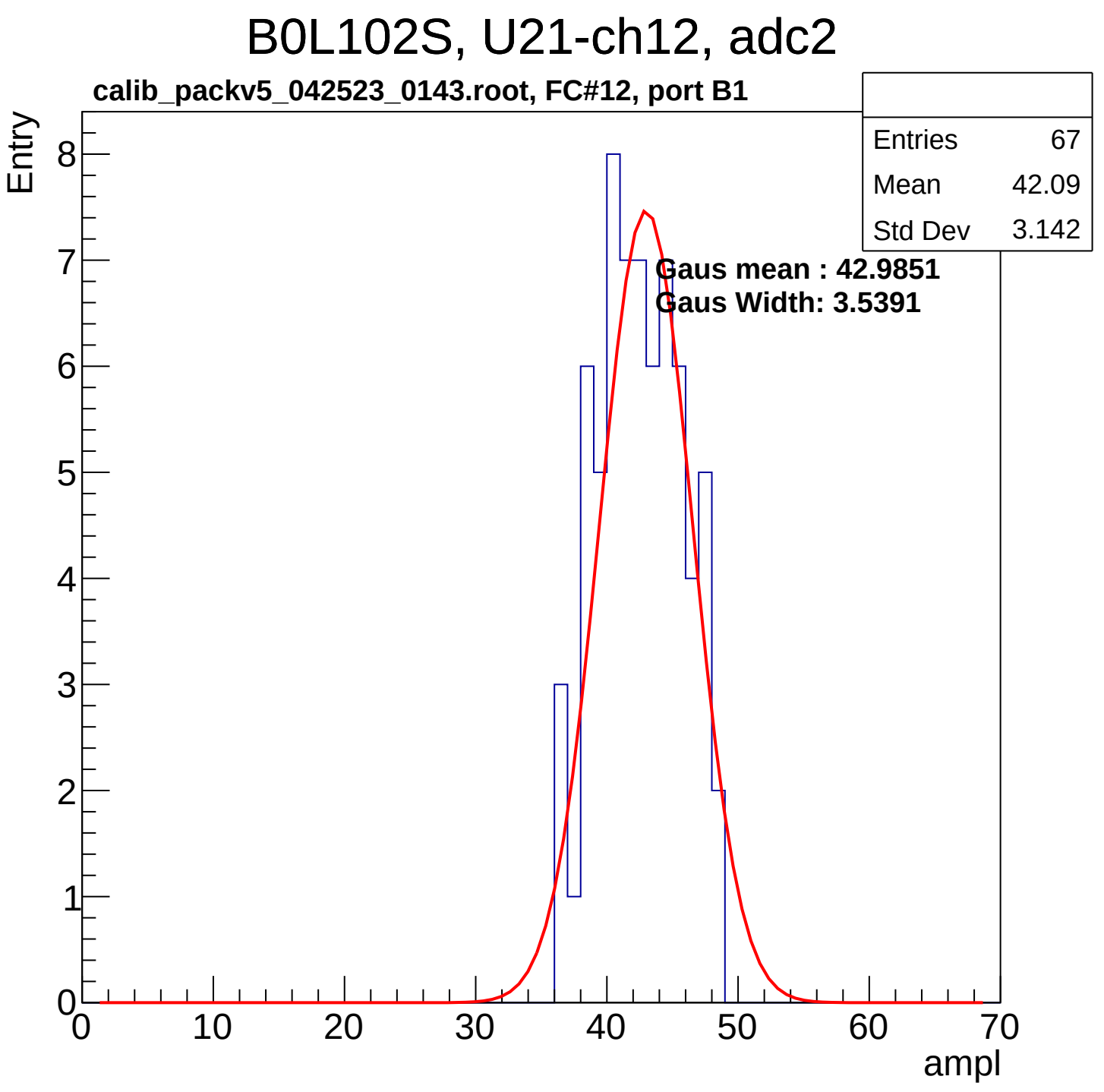
B0L102S, U21-ch12, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 42.09
Std Dev 3.142
Gaus mean : 42.9851
Gaus Width: 3.5391
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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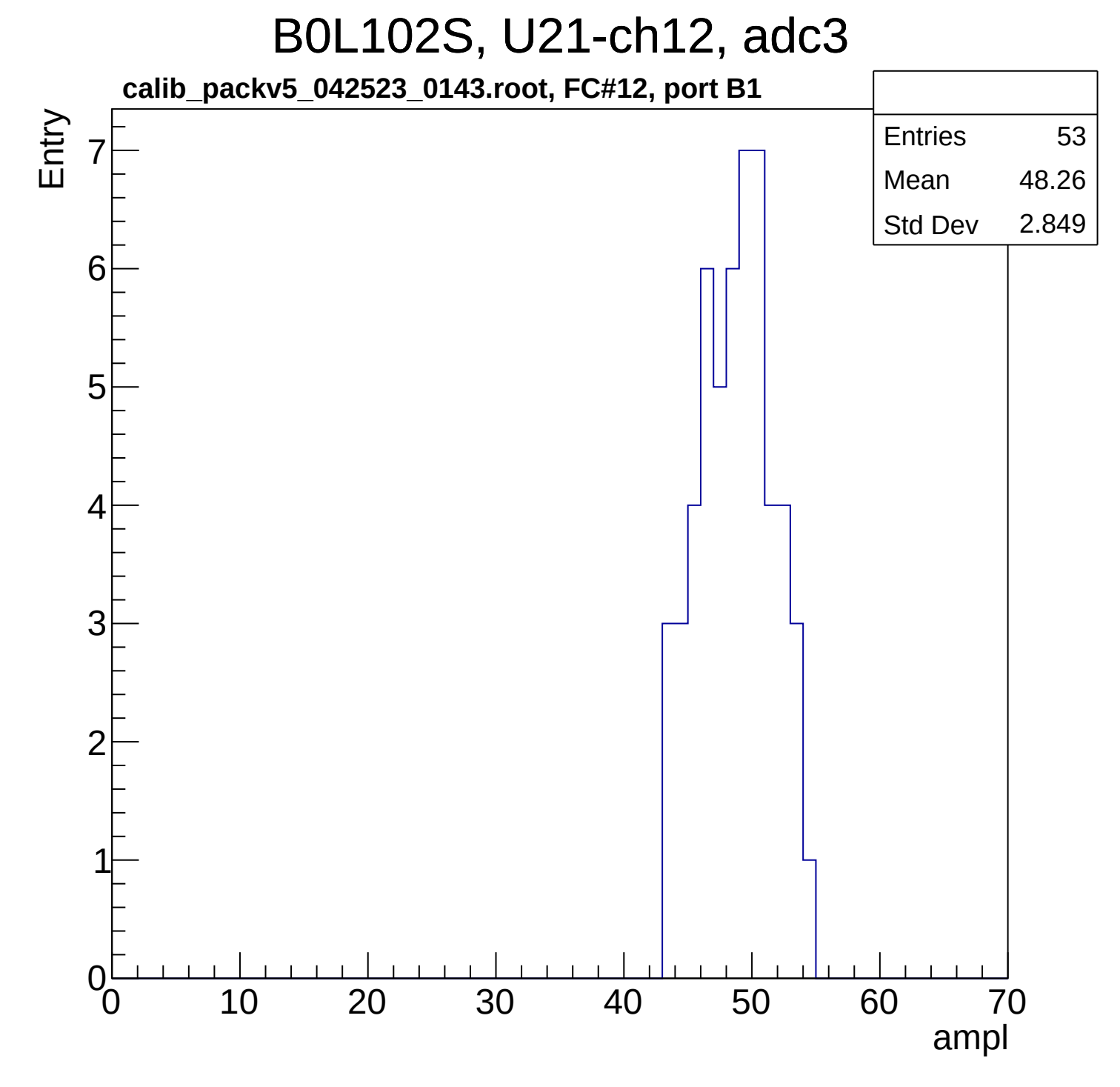
B0L102S, U21-ch12, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 48.26
Std Dev 2.849
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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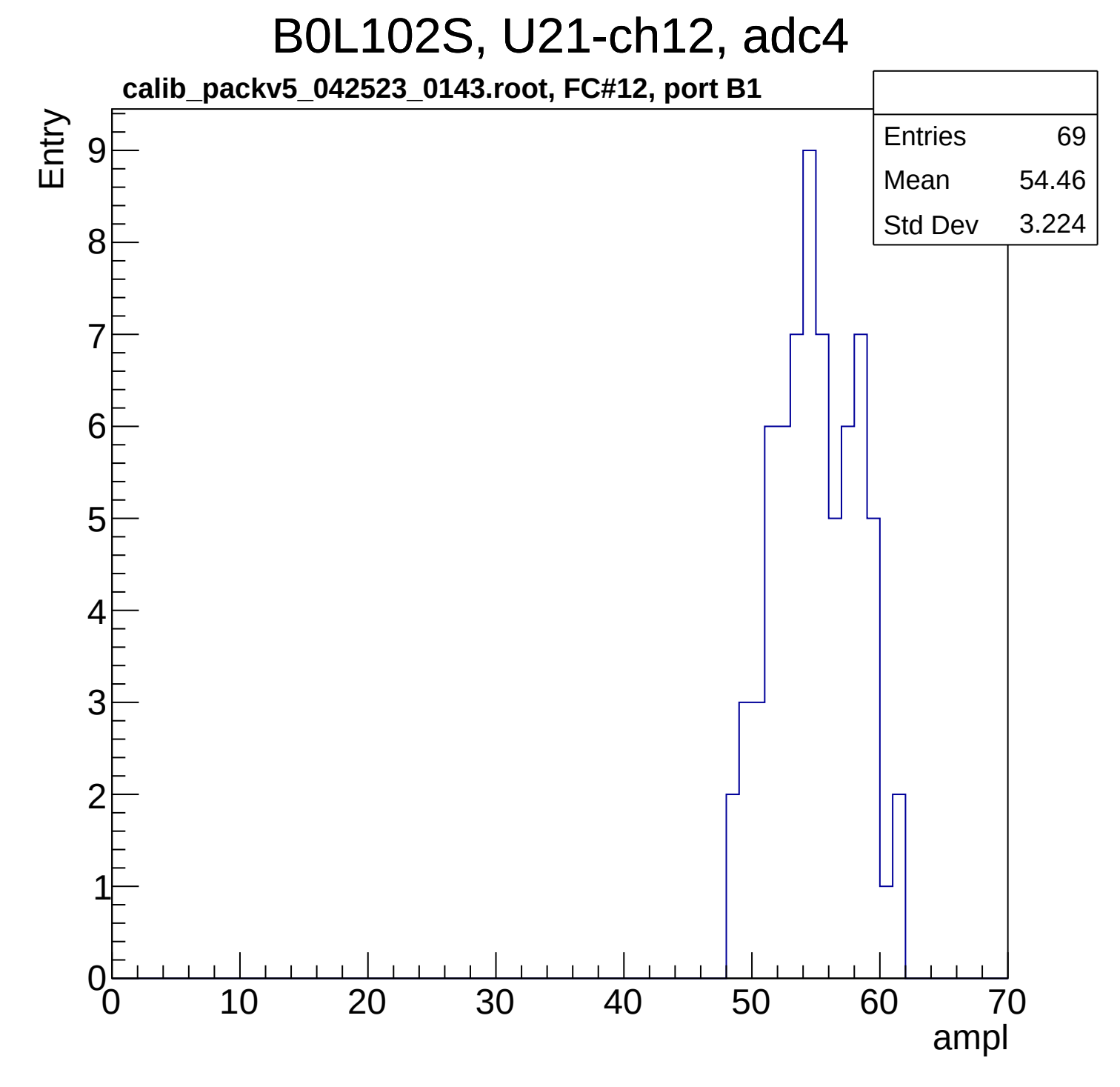
B0L102S, U21-ch12, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 54.46
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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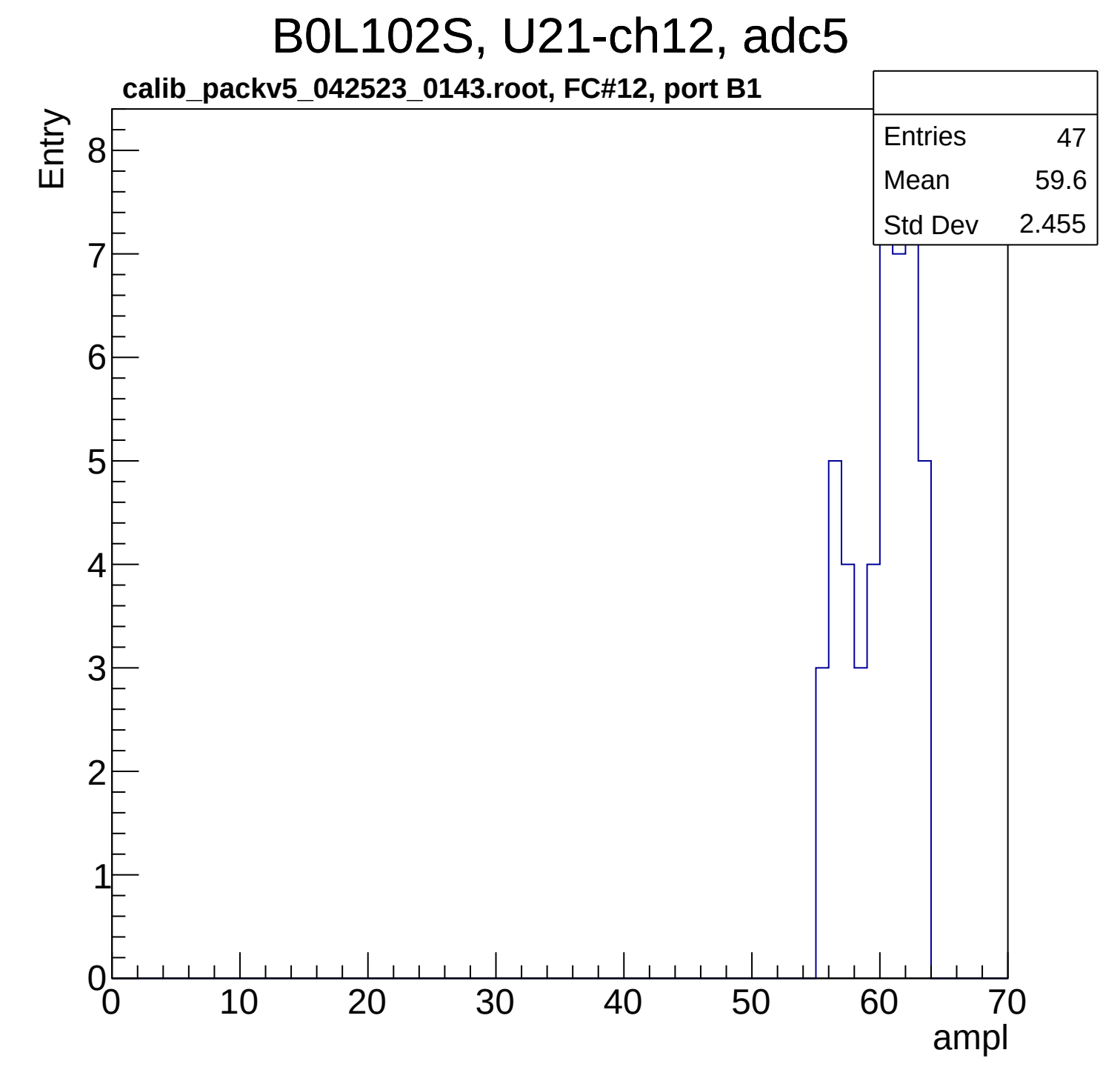
B0L102S, U21-ch12, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.6
Std Dev 2.455
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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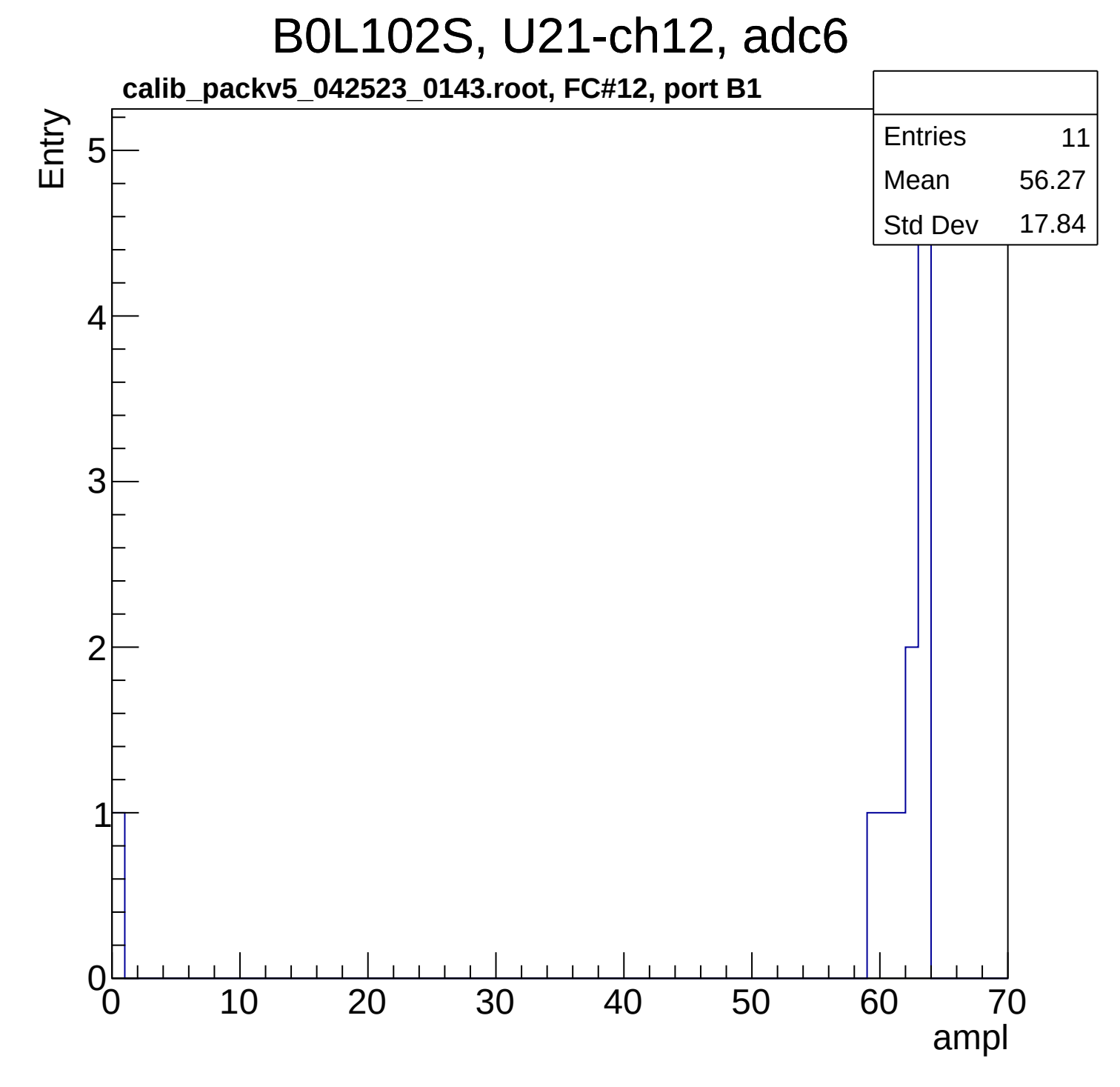
B0L102S, U21-ch12, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 56.27
Std Dev 17.84
Entry
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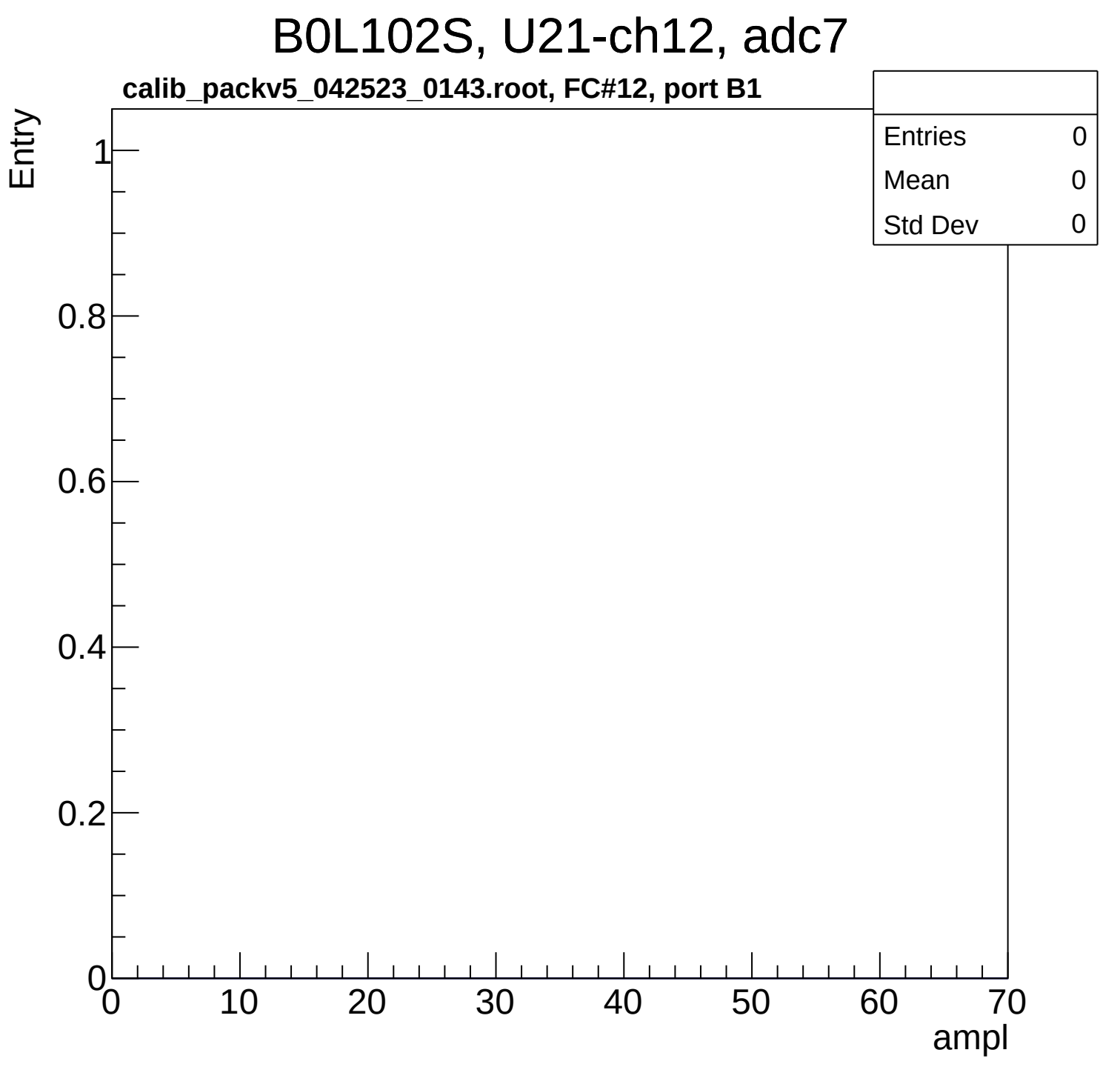
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch12, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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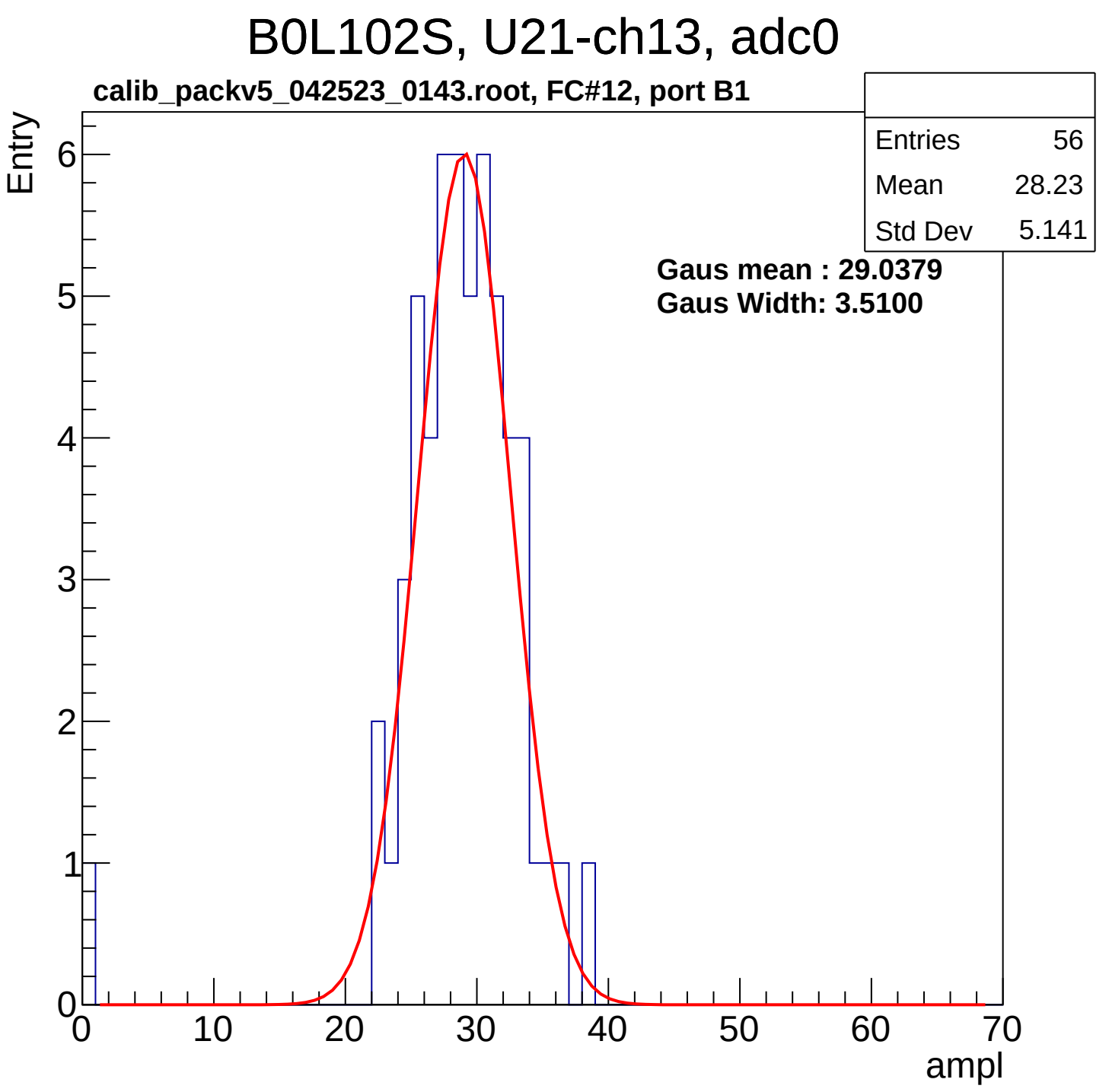
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch13, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 28.23
Std Dev 5.141
Gaus mean : 29.0379
Gaus Width: 3.5100
Entry
ampl
0
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2
3
4
5
6
0
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60
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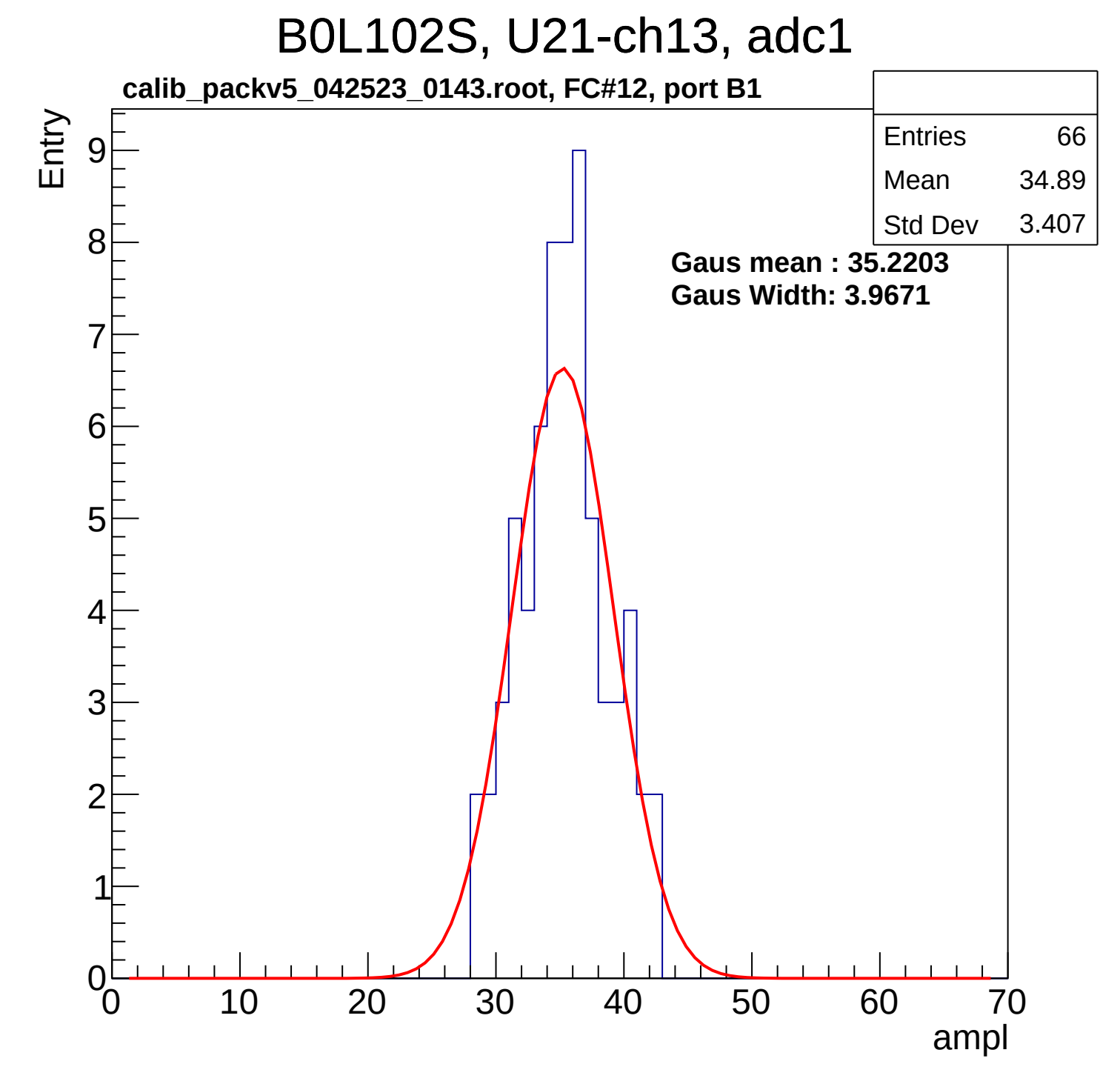
B0L102S, U21-ch13, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 34.89
Std Dev 3.407
Gaus mean : 35.2203
Gaus Width: 3.9671
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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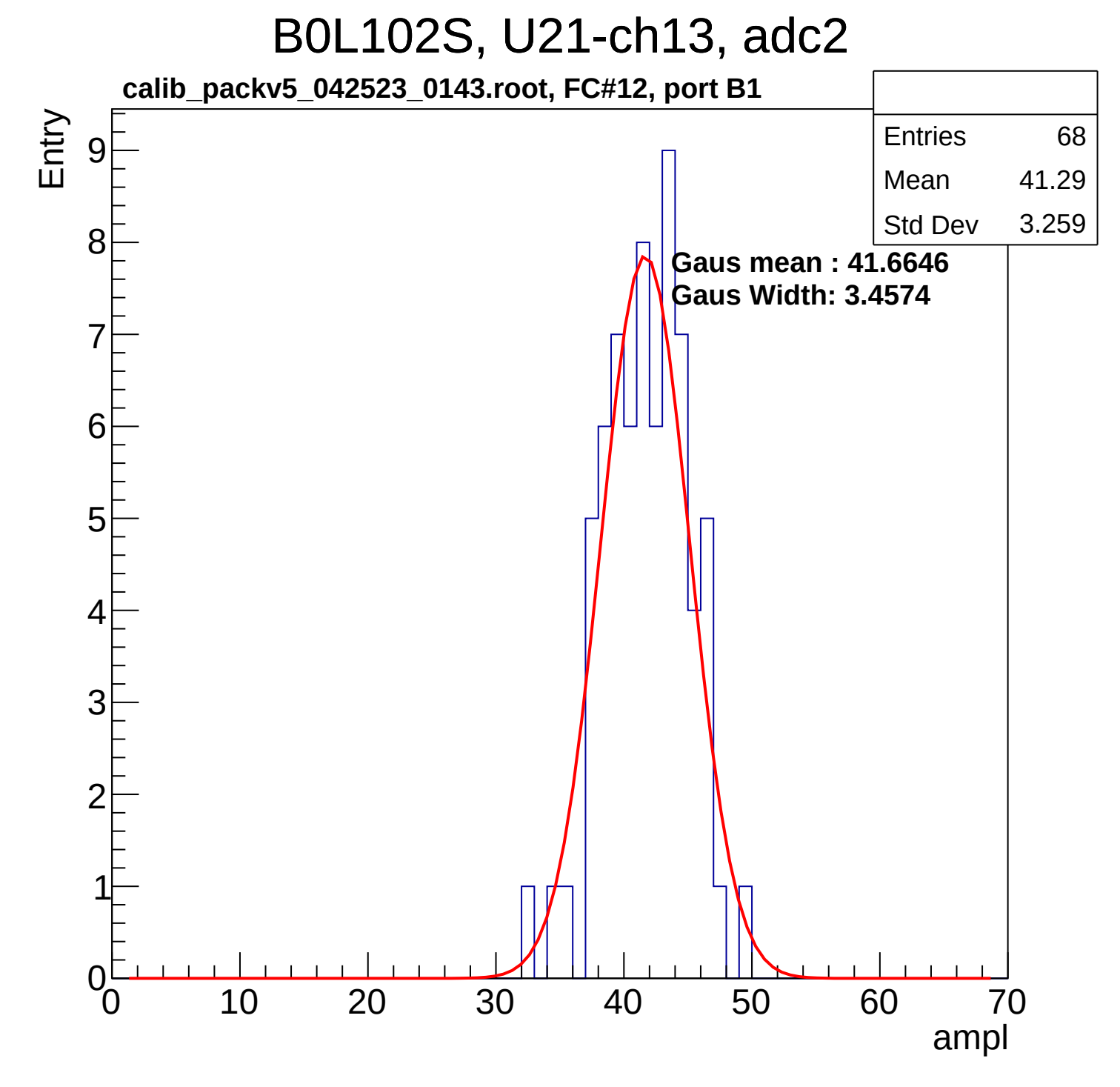
B0L102S, U21-ch13, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.29
Std Dev 3.259
Gaus mean : 41.6646
Gaus Width: 3.4574
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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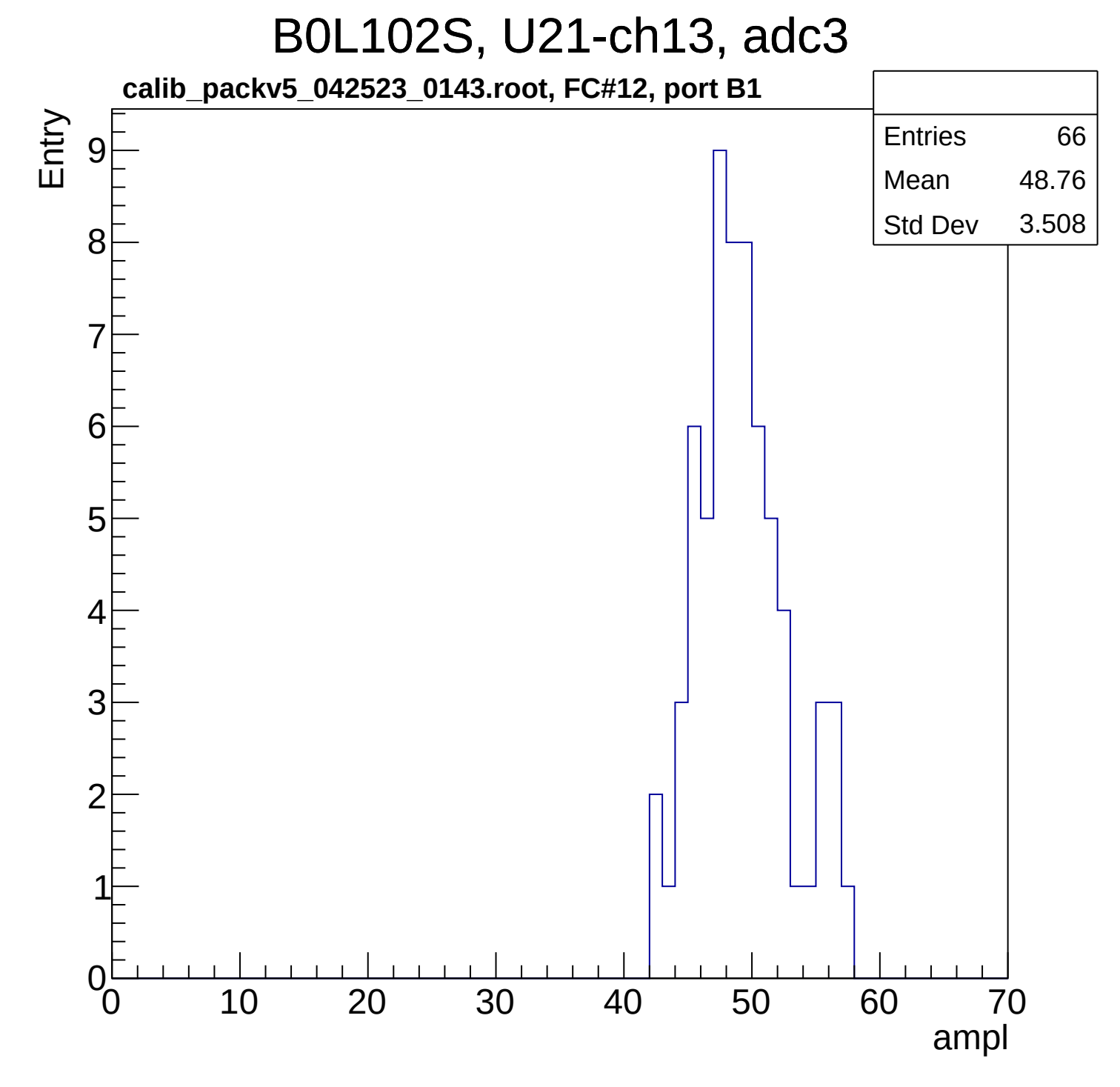
B0L102S, U21-ch13, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 48.76
Std Dev 3.508
Entry
ampl
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2
3
4
5
6
7
8
9
0
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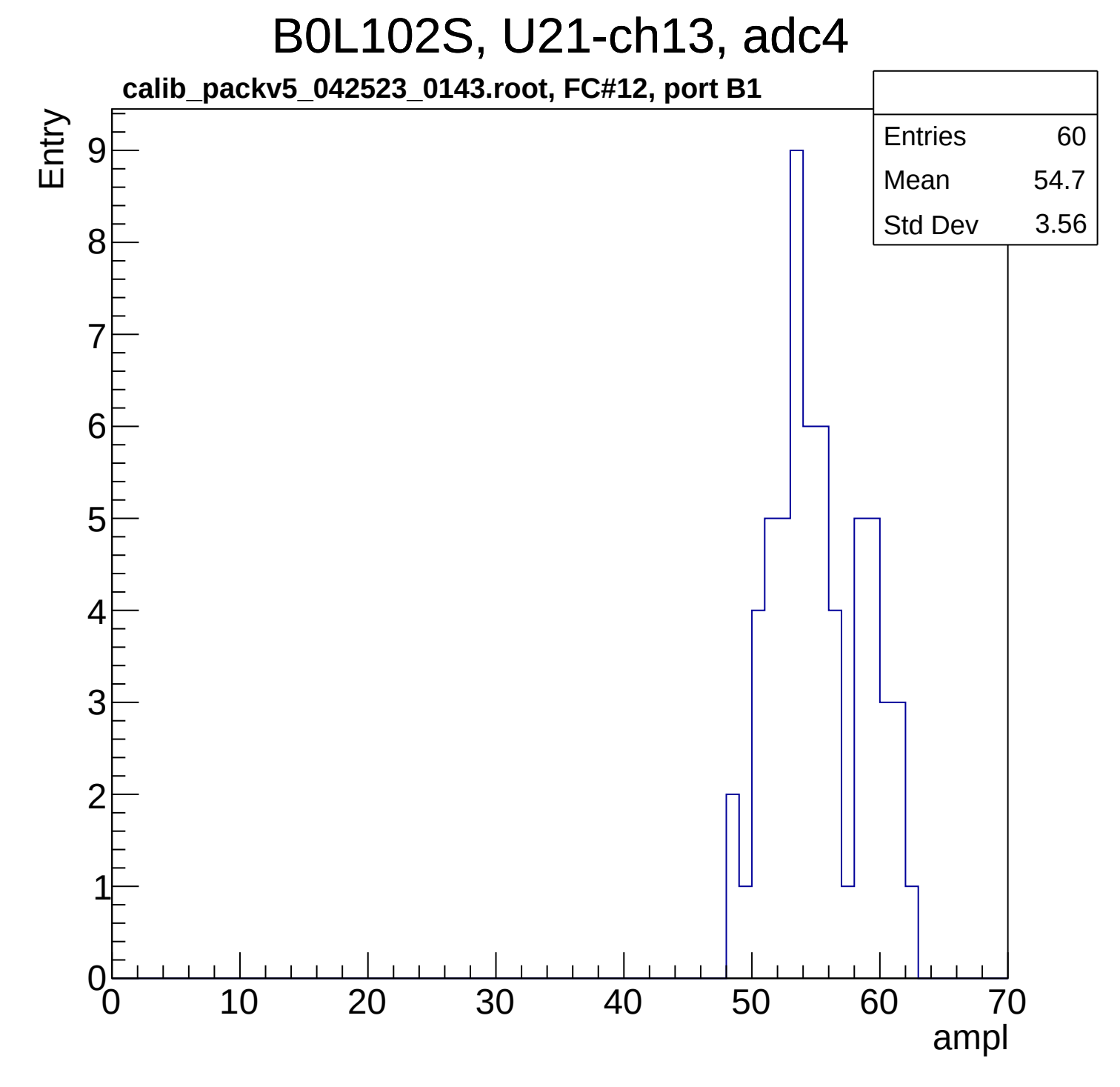
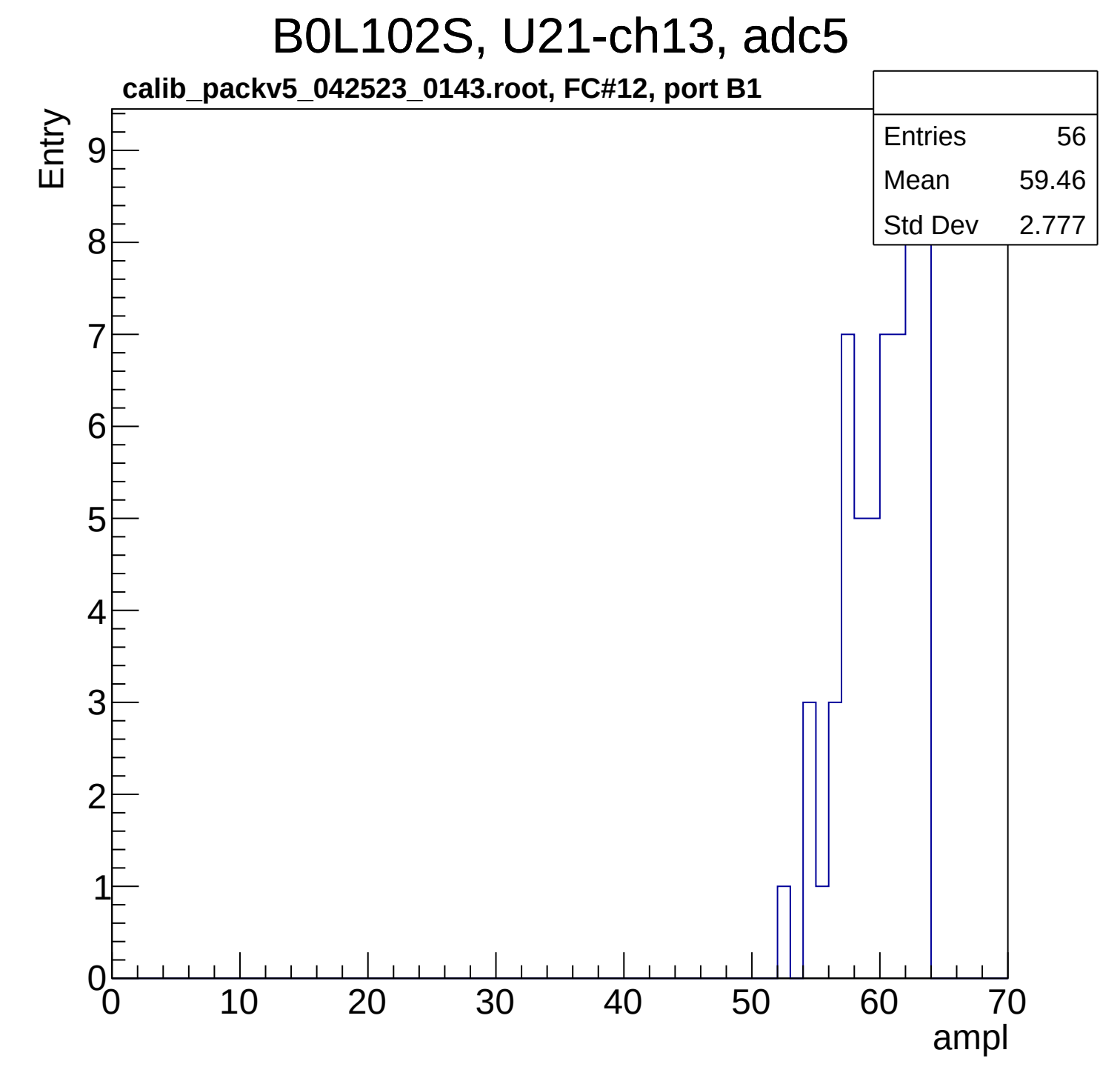

B0L102S, U21-ch13, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 54.7
Std Dev 3.56
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch13, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 59.46
Std Dev 2.777
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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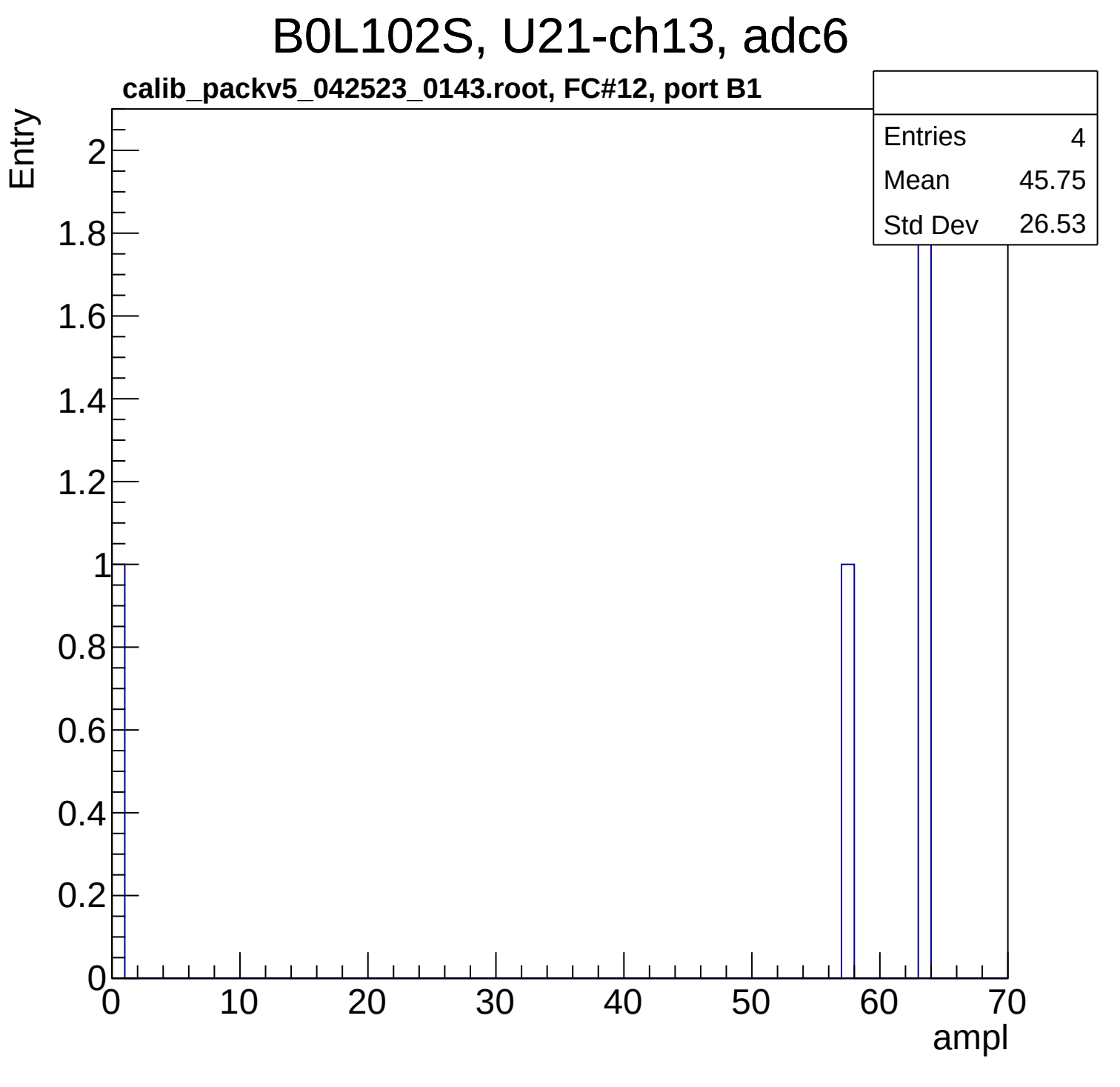
B0L102S, U21-ch13, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 45.75
Std Dev 26.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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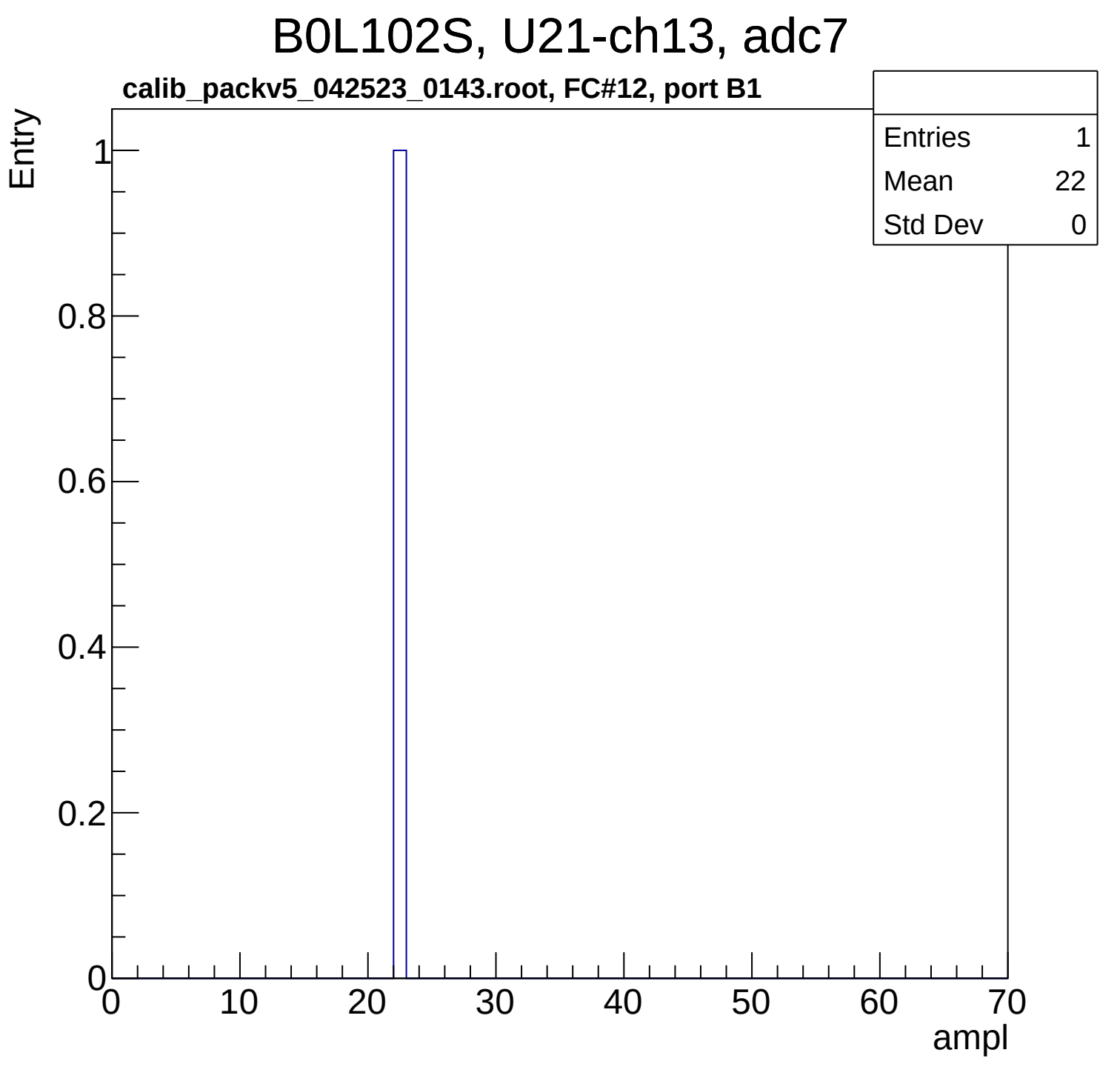
B0L102S, U21-ch13, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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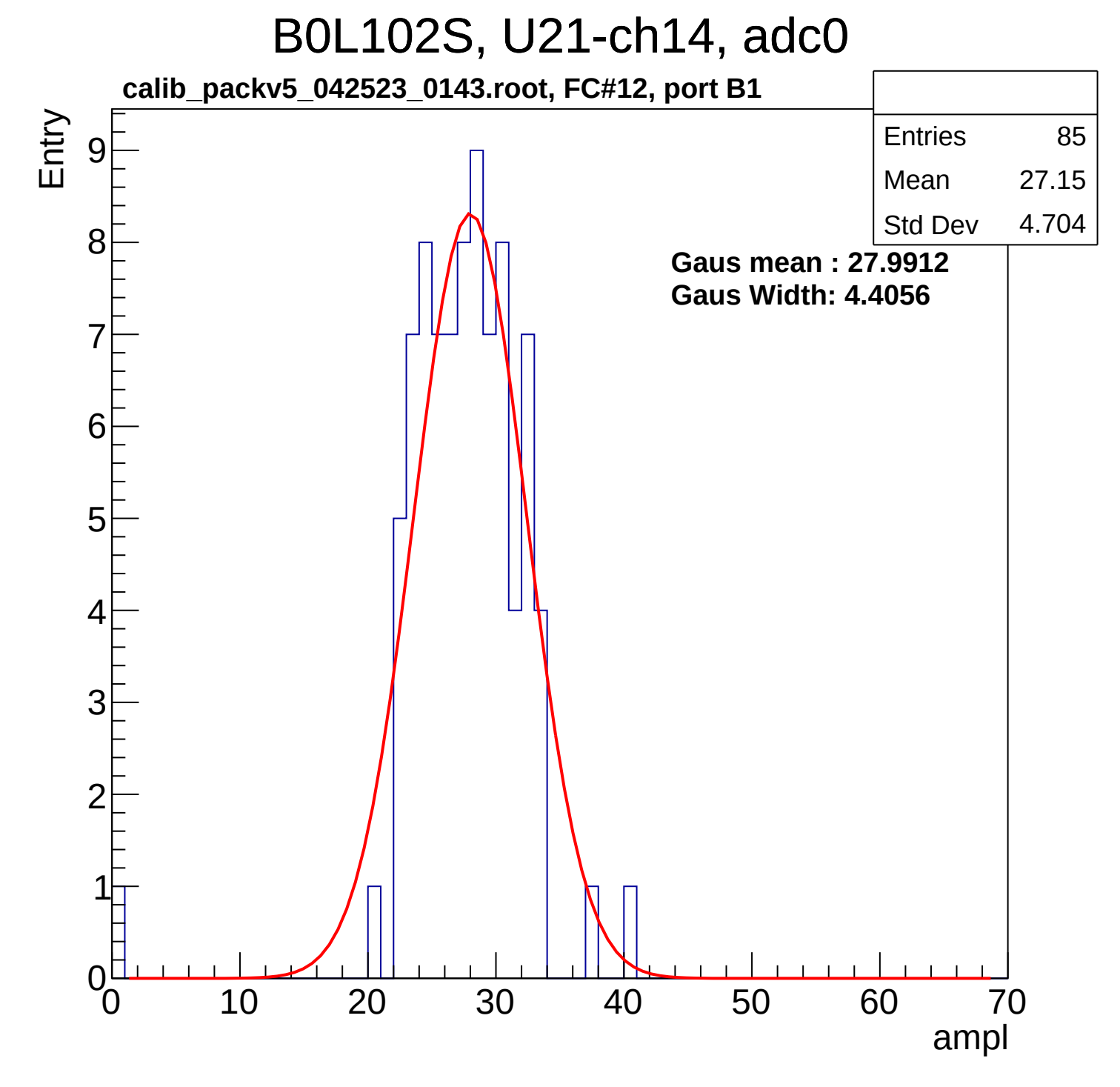
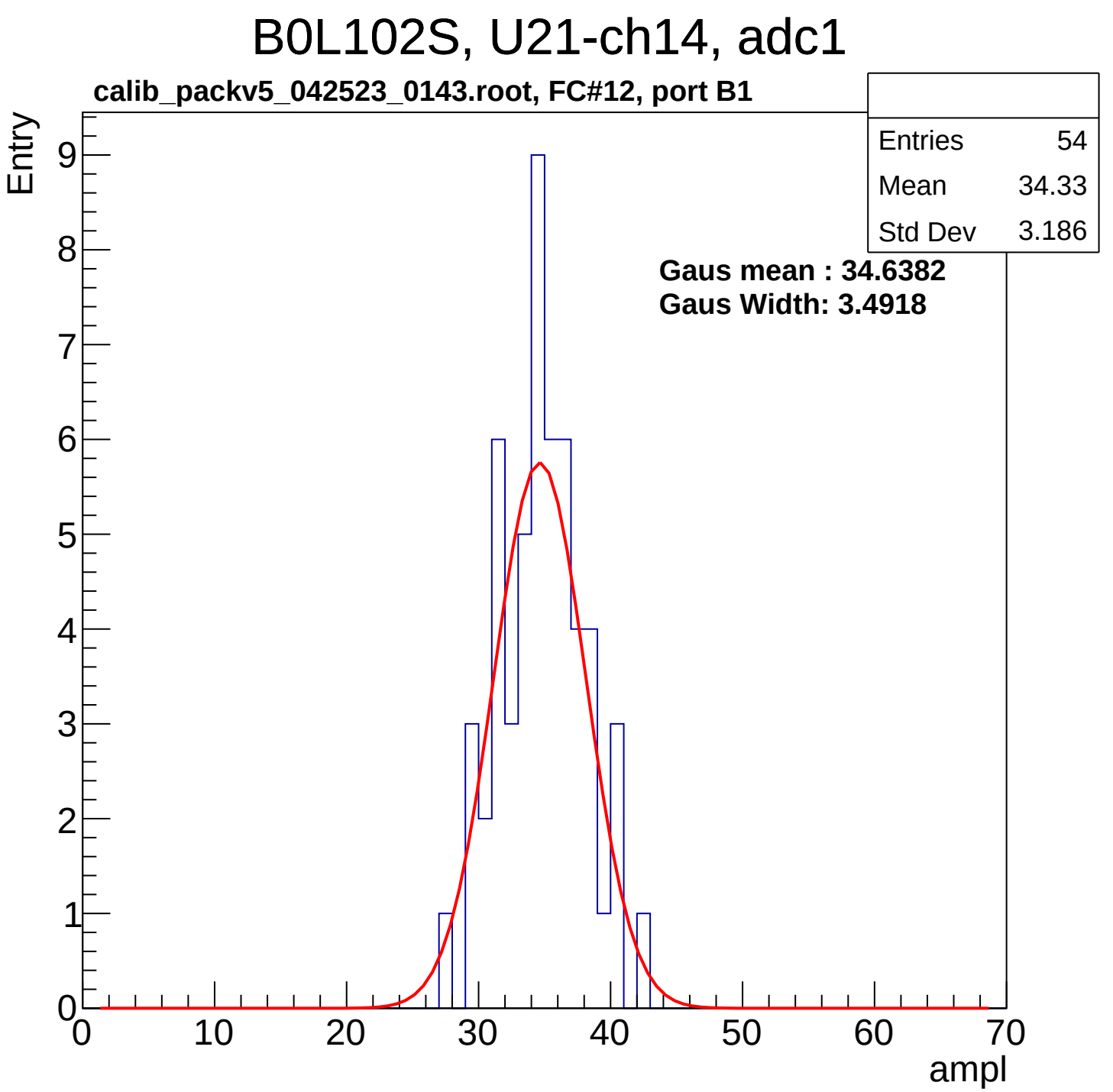
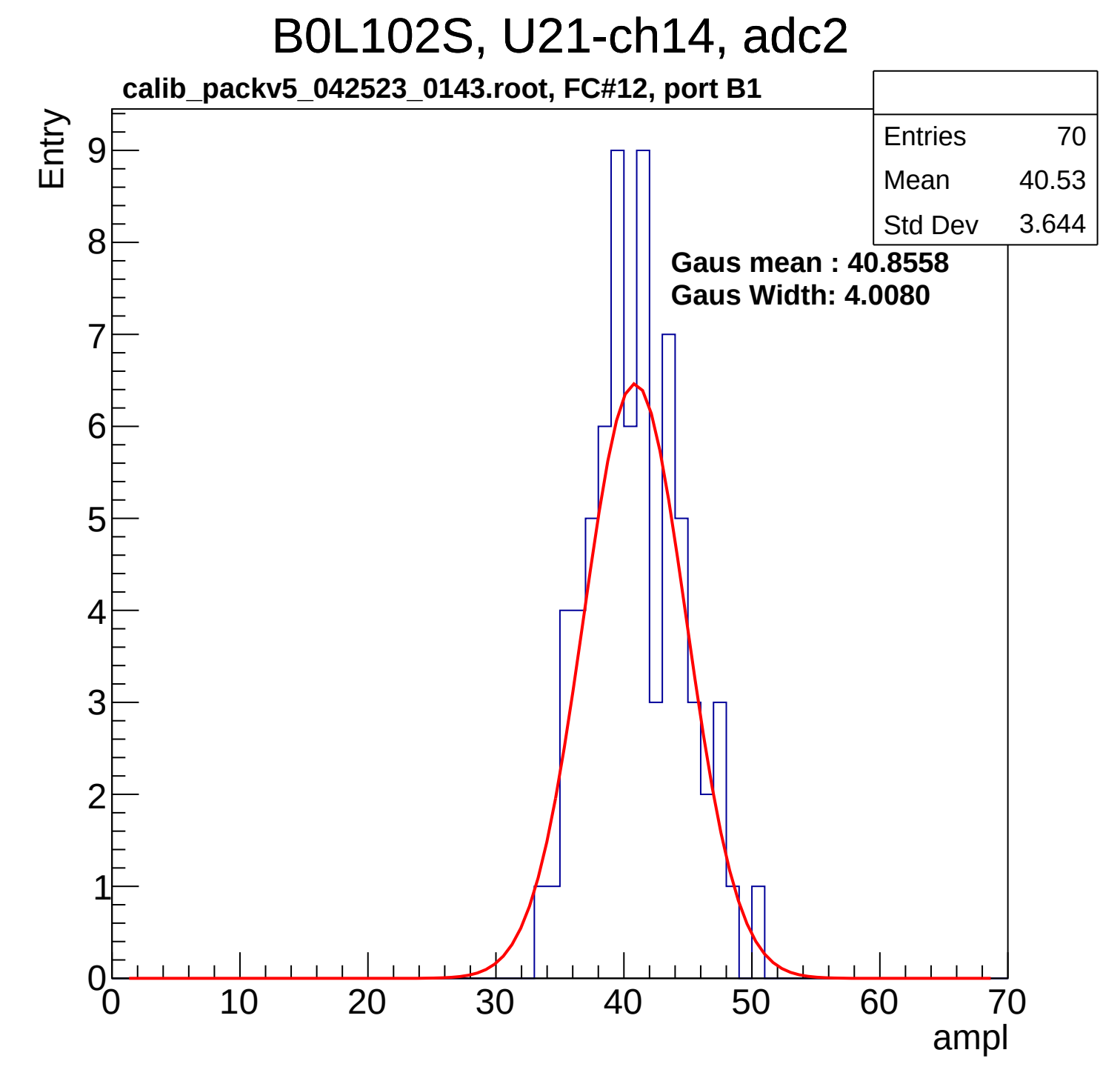
50
60
70
ampl


B0L102S, U21-ch14, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 27.15
Std Dev 4.704
Gaus mean : 27.9912
Gaus Width: 4.4056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch14, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 34.33
Std Dev 3.186
Gaus mean : 34.6382
Gaus Width: 3.4918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch14, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 40.53
Std Dev 3.644
Gaus mean : 40.8558
Gaus Width: 4.0080
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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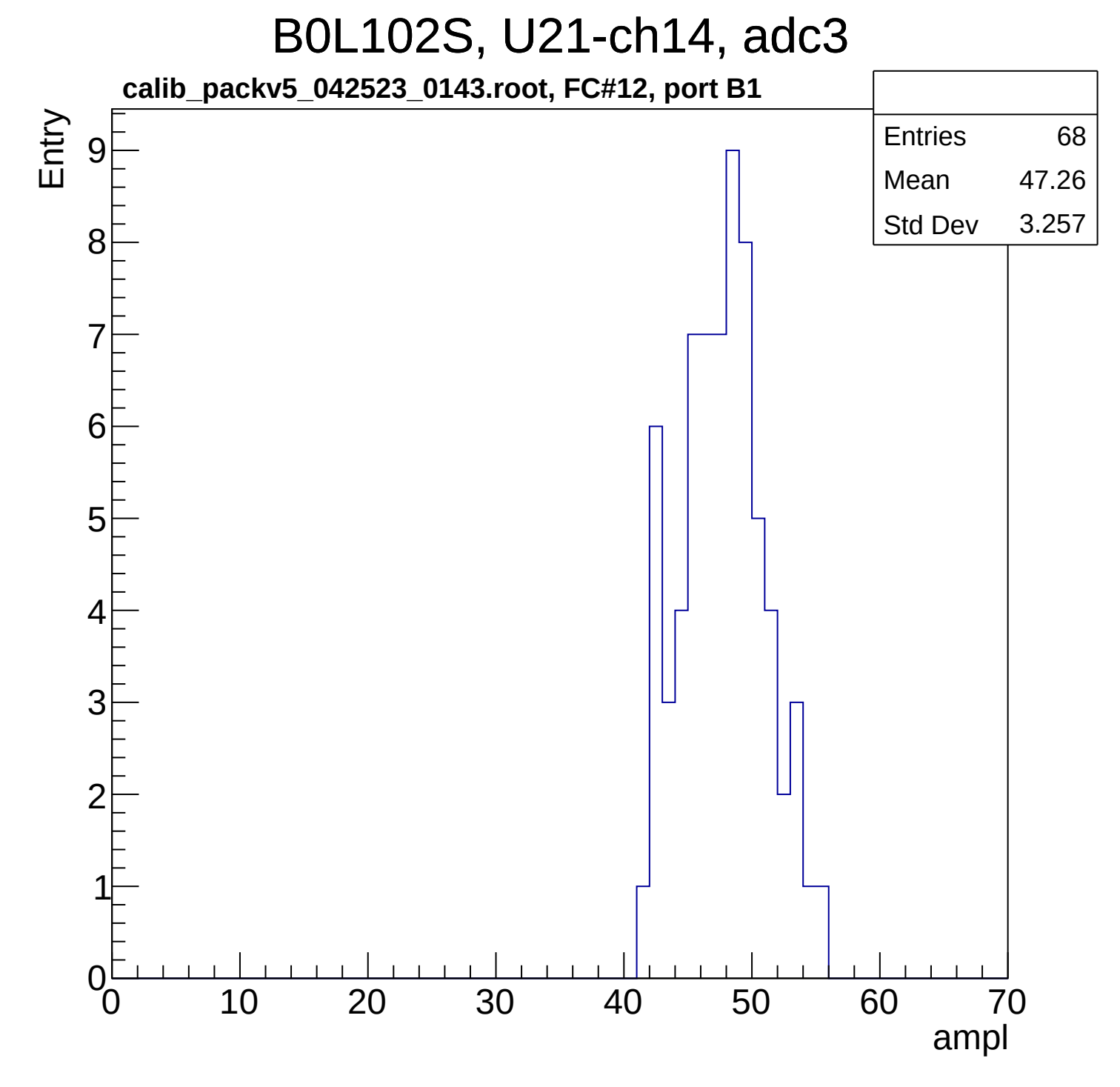
B0L102S, U21-ch14, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 47.26
Std Dev 3.257
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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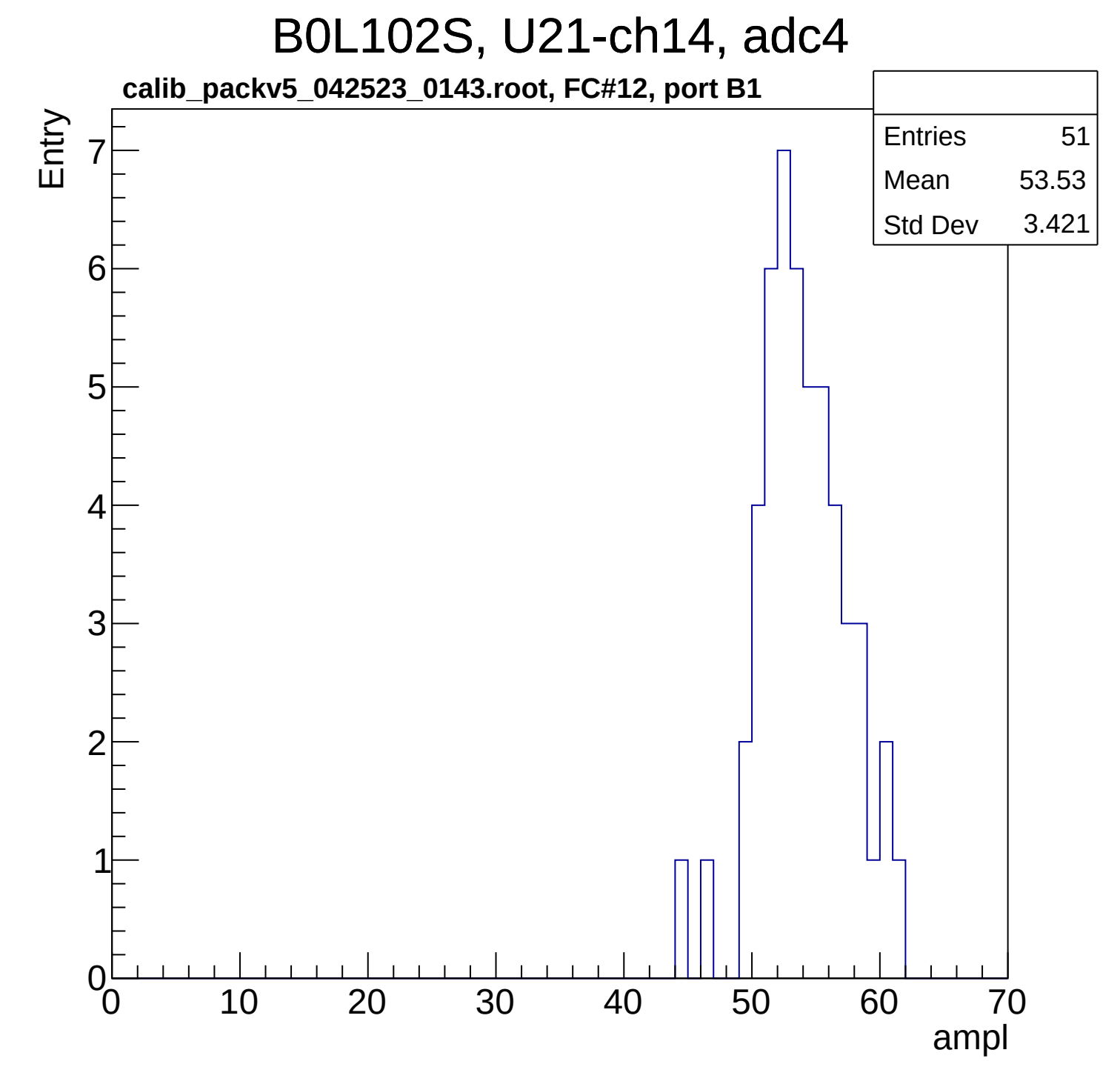
B0L102S, U21-ch14, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 53.53
Std Dev 3.421
Entry
ampl
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3
4
5
6
7
0
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20
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50
60
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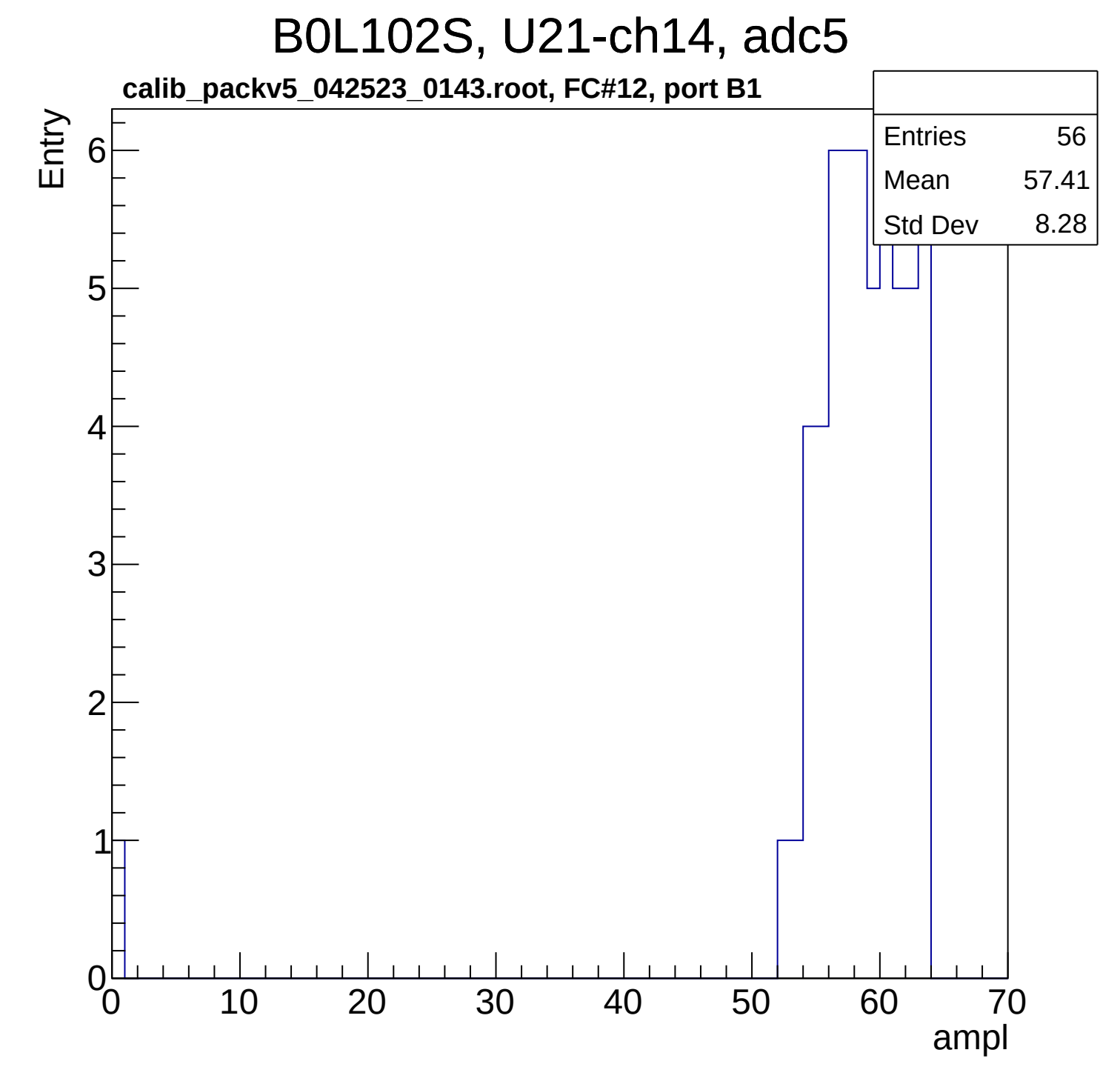
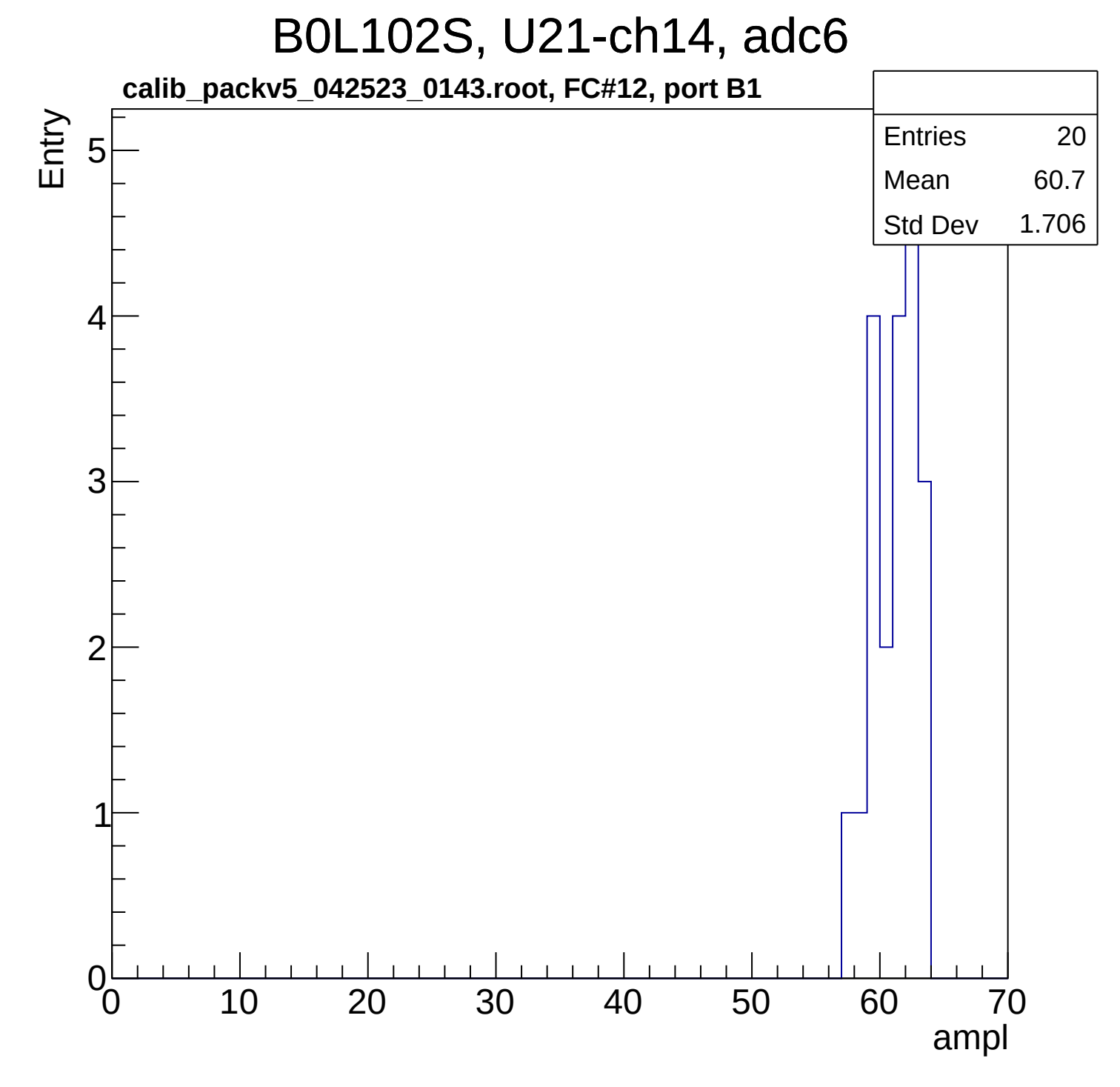

B0L102S, U21-ch14, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 57.41
Std Dev 8.28
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U21-ch14, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 20
Mean 60.7
Std Dev 1.706
Entry
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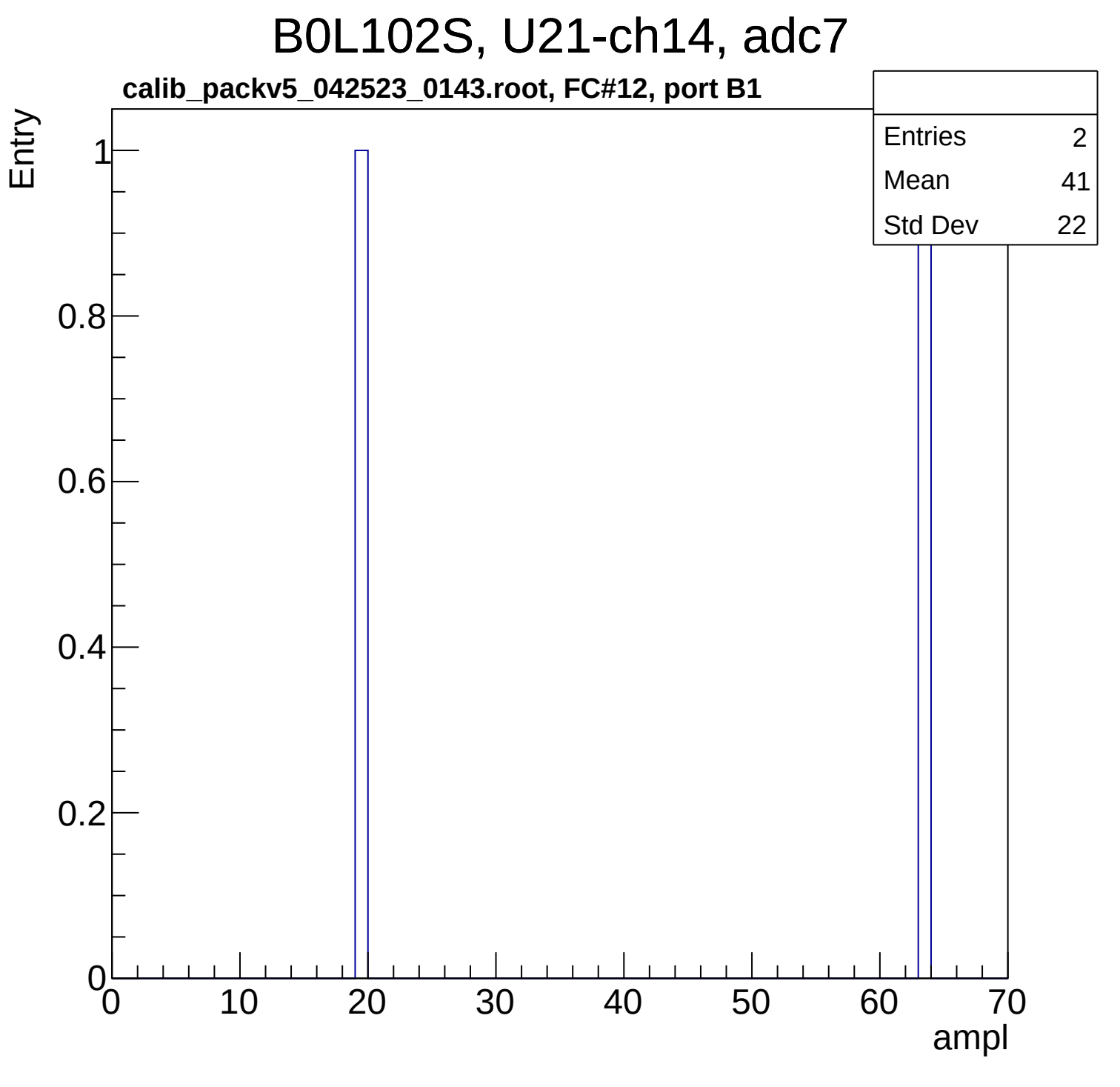
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch14, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 41
Std Dev 22
Entry
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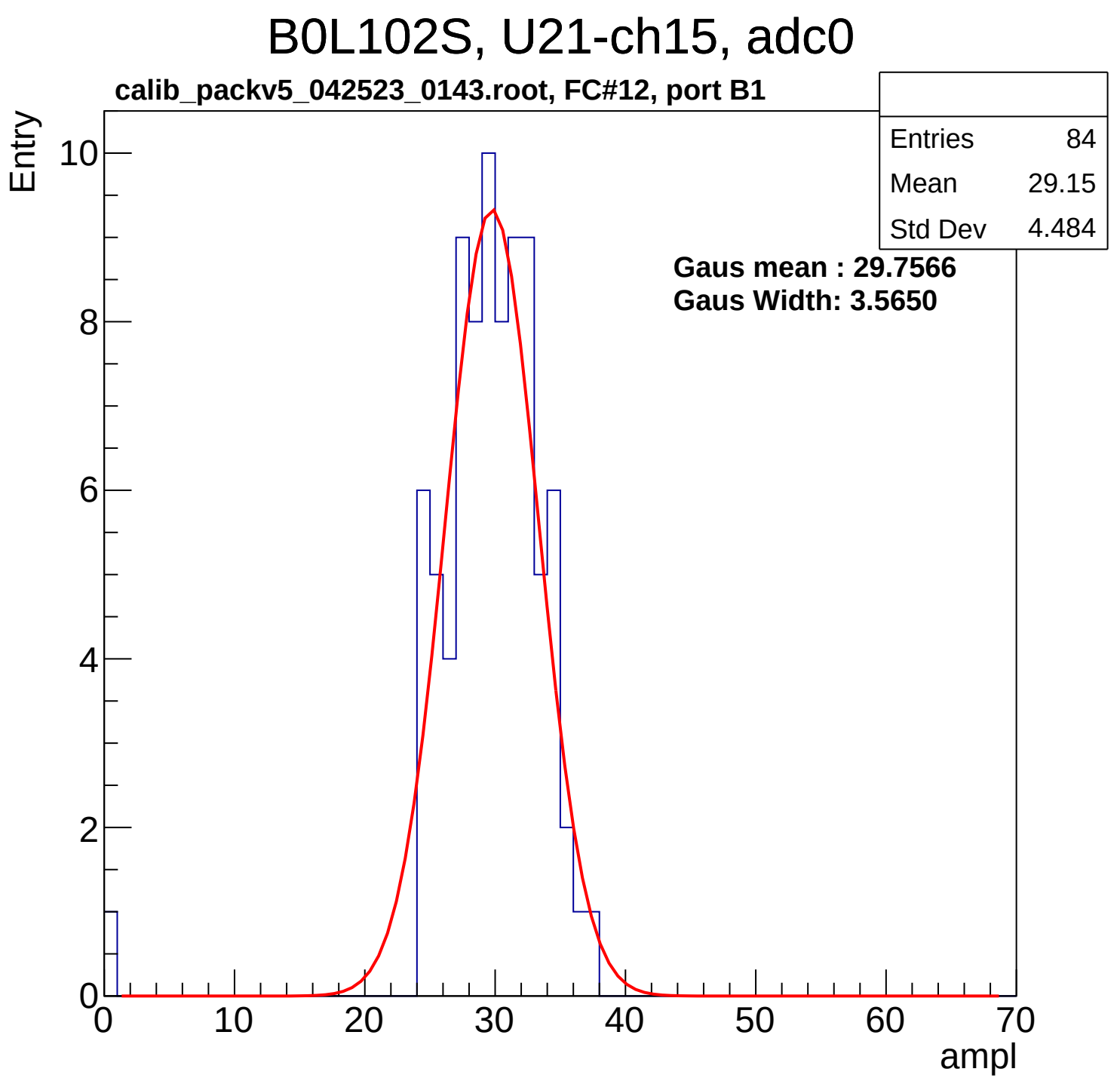
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch15, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 29.15
Std Dev 4.484
Gaus mean : 29.7566
Gaus Width: 3.5650
Entry
ampl
0
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4
6
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0
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50
60
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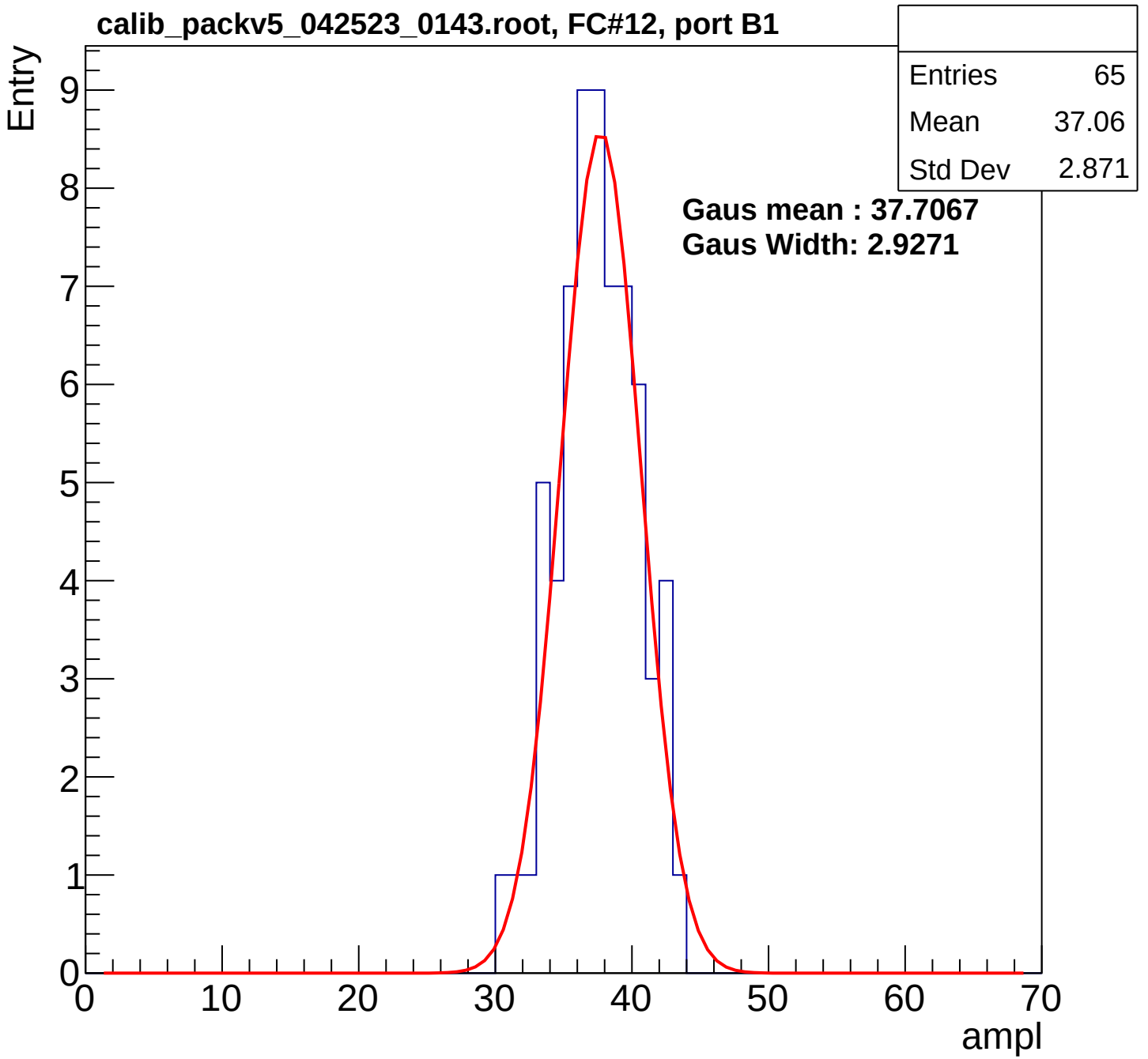
B0L102S, U21-ch15, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 37.06
Std Dev 2.871
Gaus mean : 37.7067
Gaus Width: 2.9271
Entry
ampl
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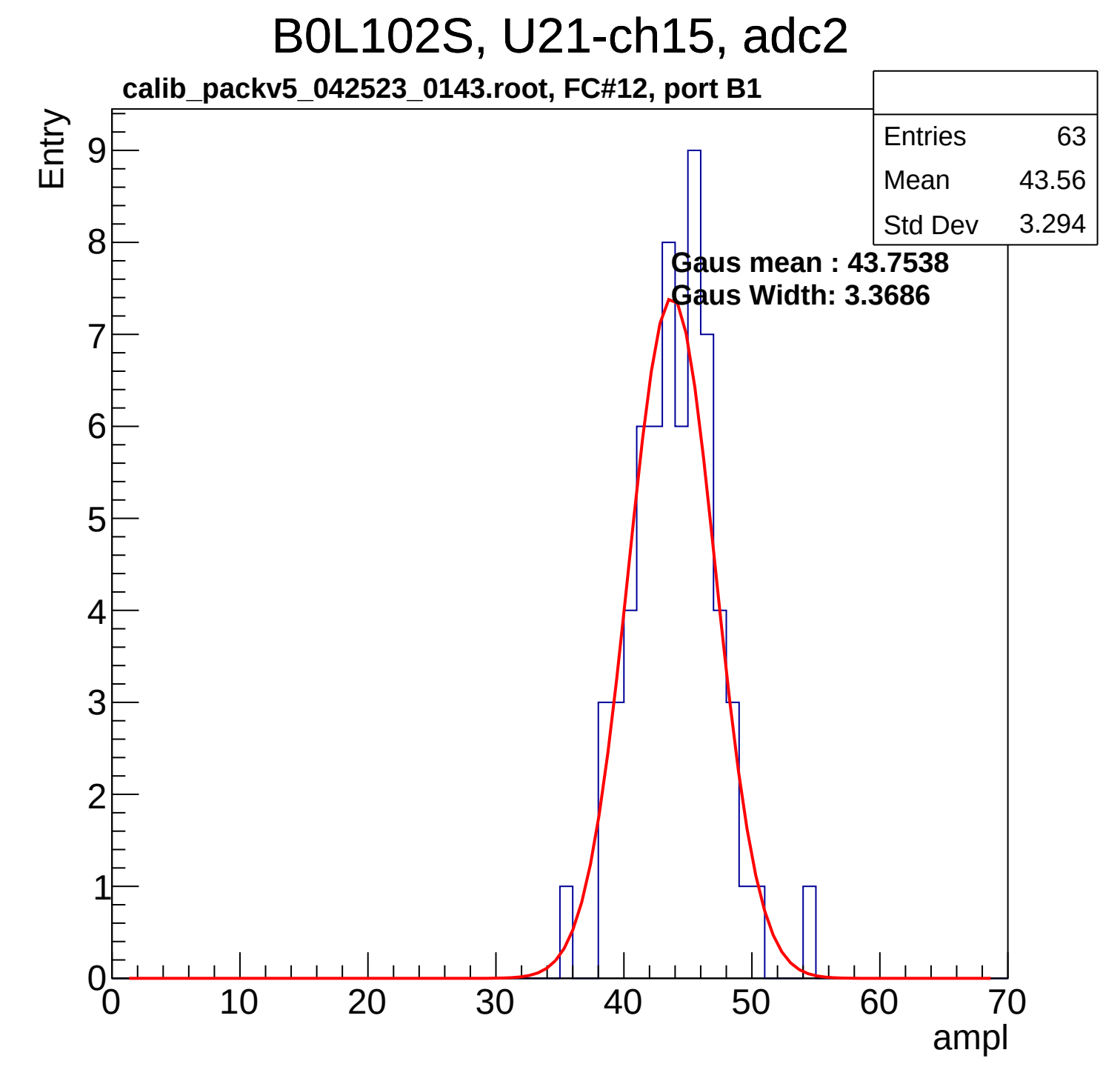
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch15, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 43.56
Std Dev 3.294
Gaus mean : 43.7538
Gaus Width: 3.3686
Entry
ampl
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2
3
4
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6
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0
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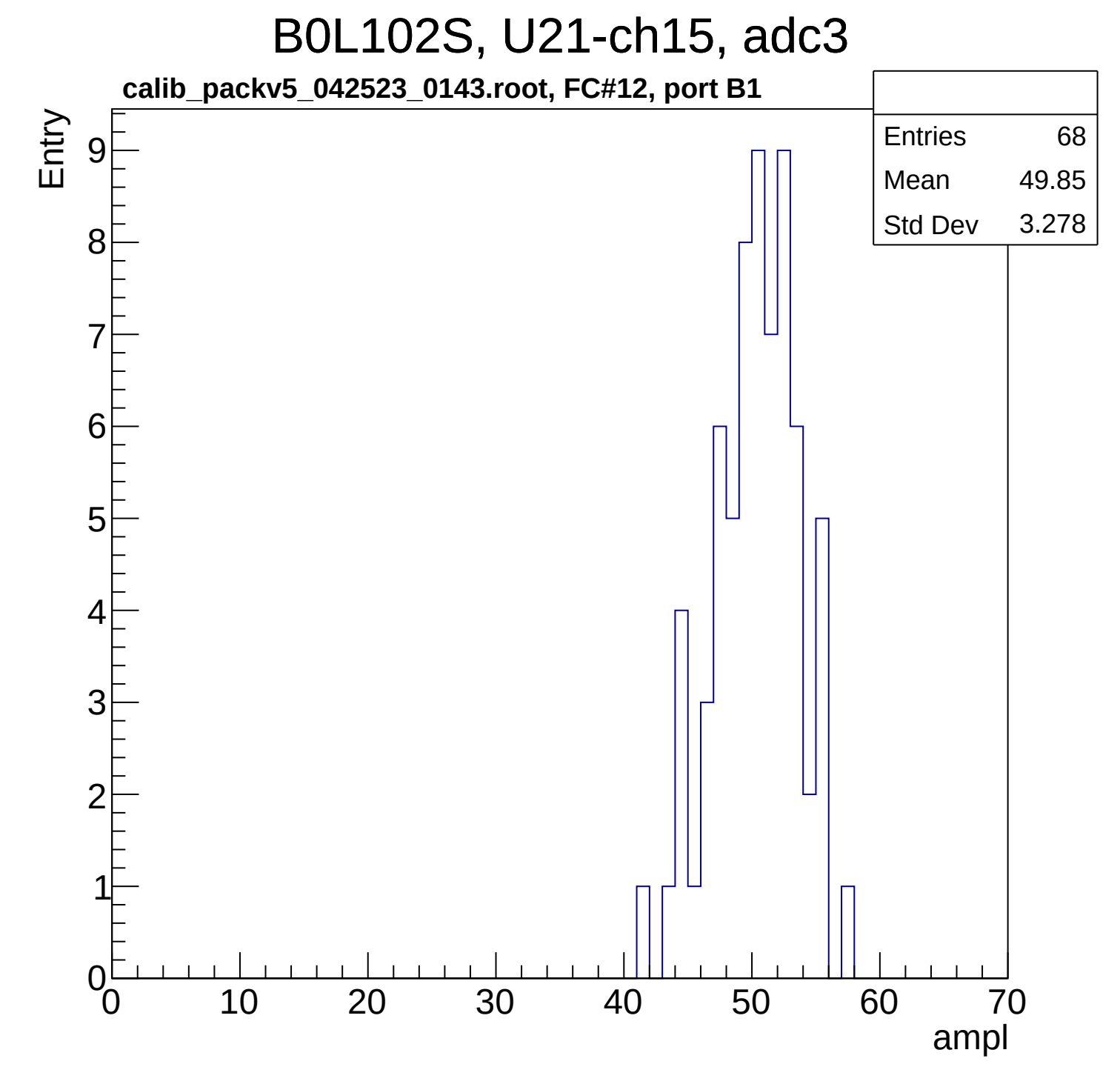
B0L102S, U21-ch15, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.85
Std Dev 3.278
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
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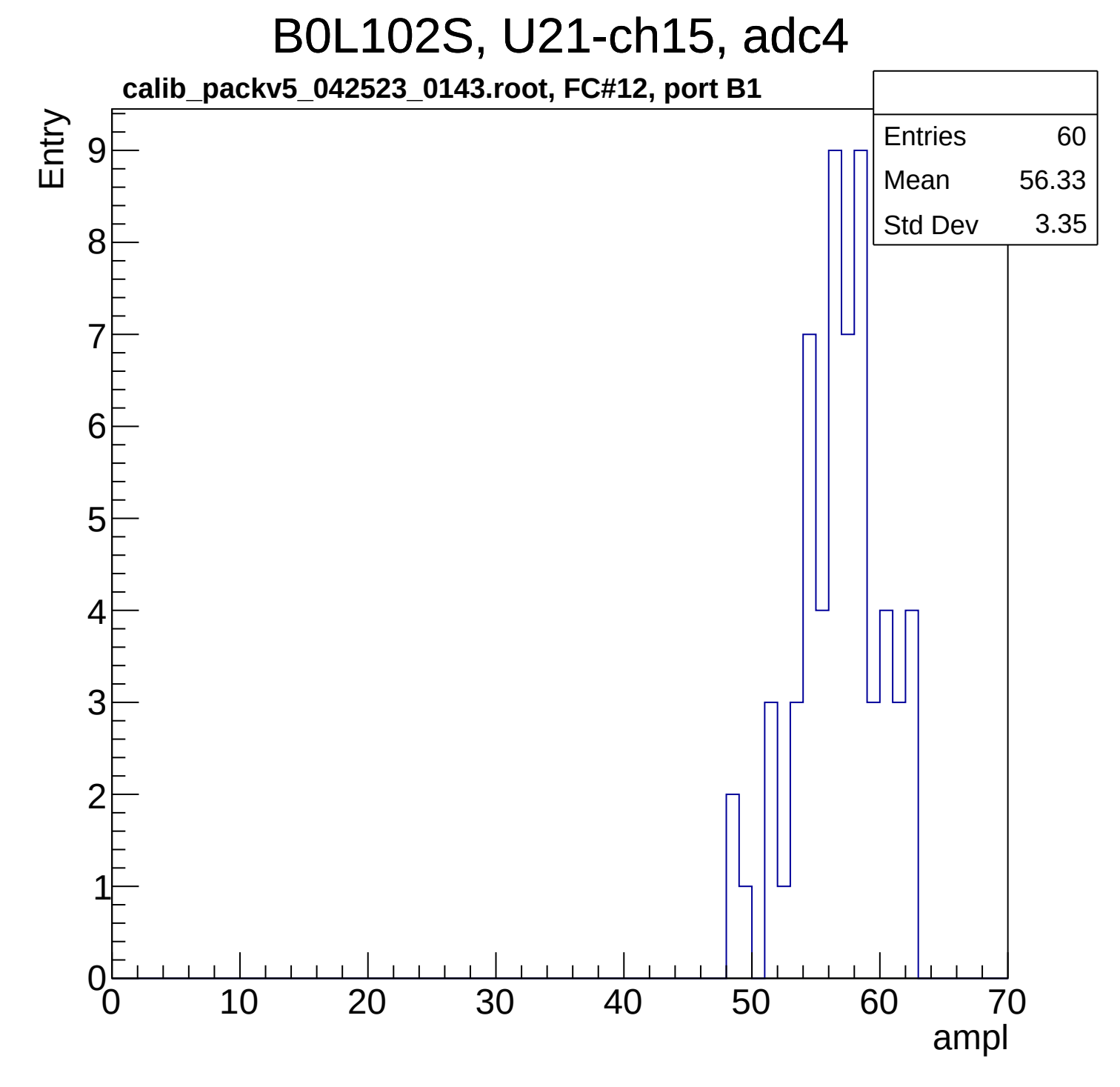
40
50
60
70


B0L102S, U21-ch15, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.33
Std Dev 3.35
Entry
ampl
0
1
2
3
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6
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60
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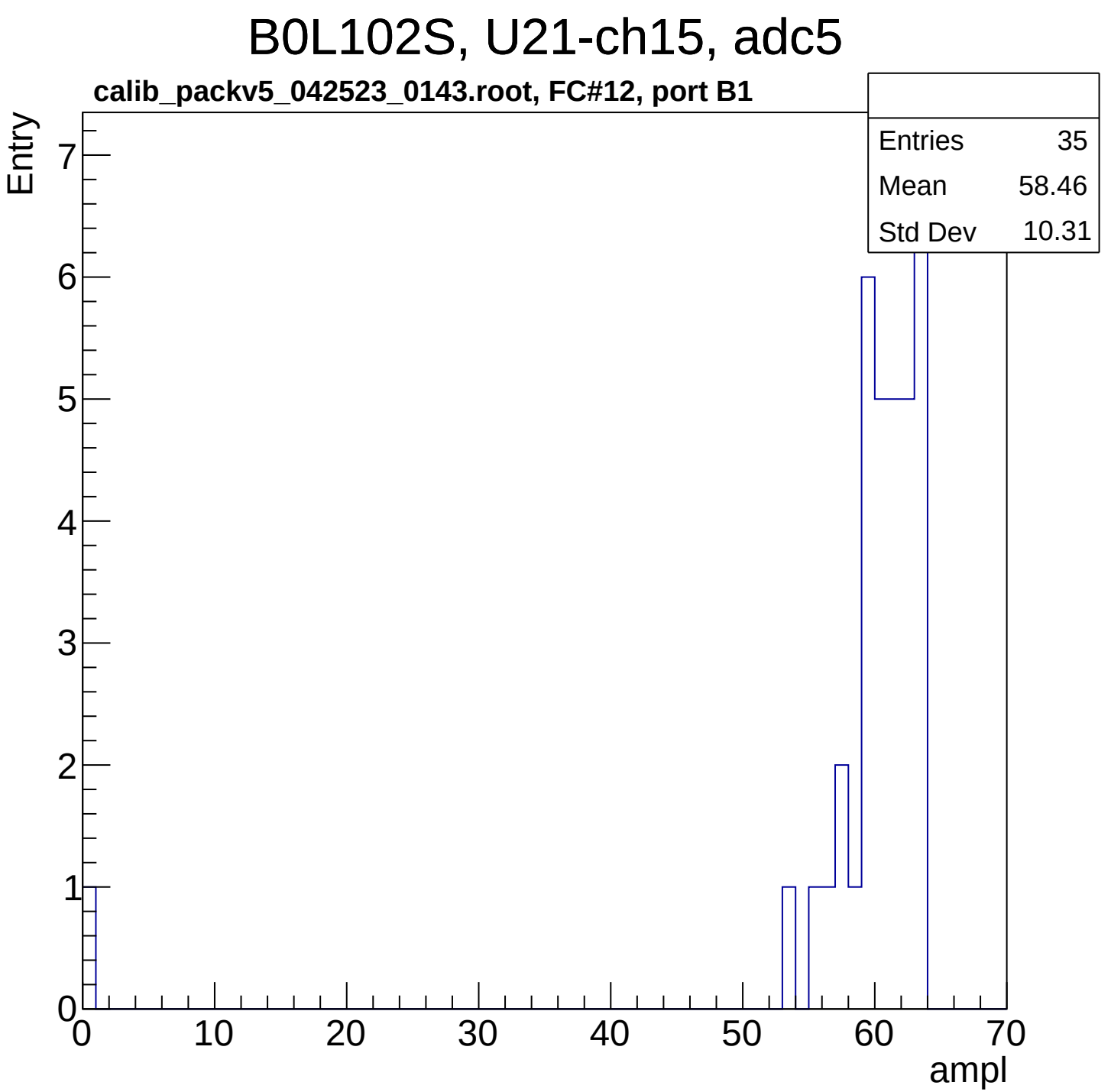
B0L102S, U21-ch15, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 58.46
Std Dev 10.31
Entry
0
1
2
3
4
5
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7
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10
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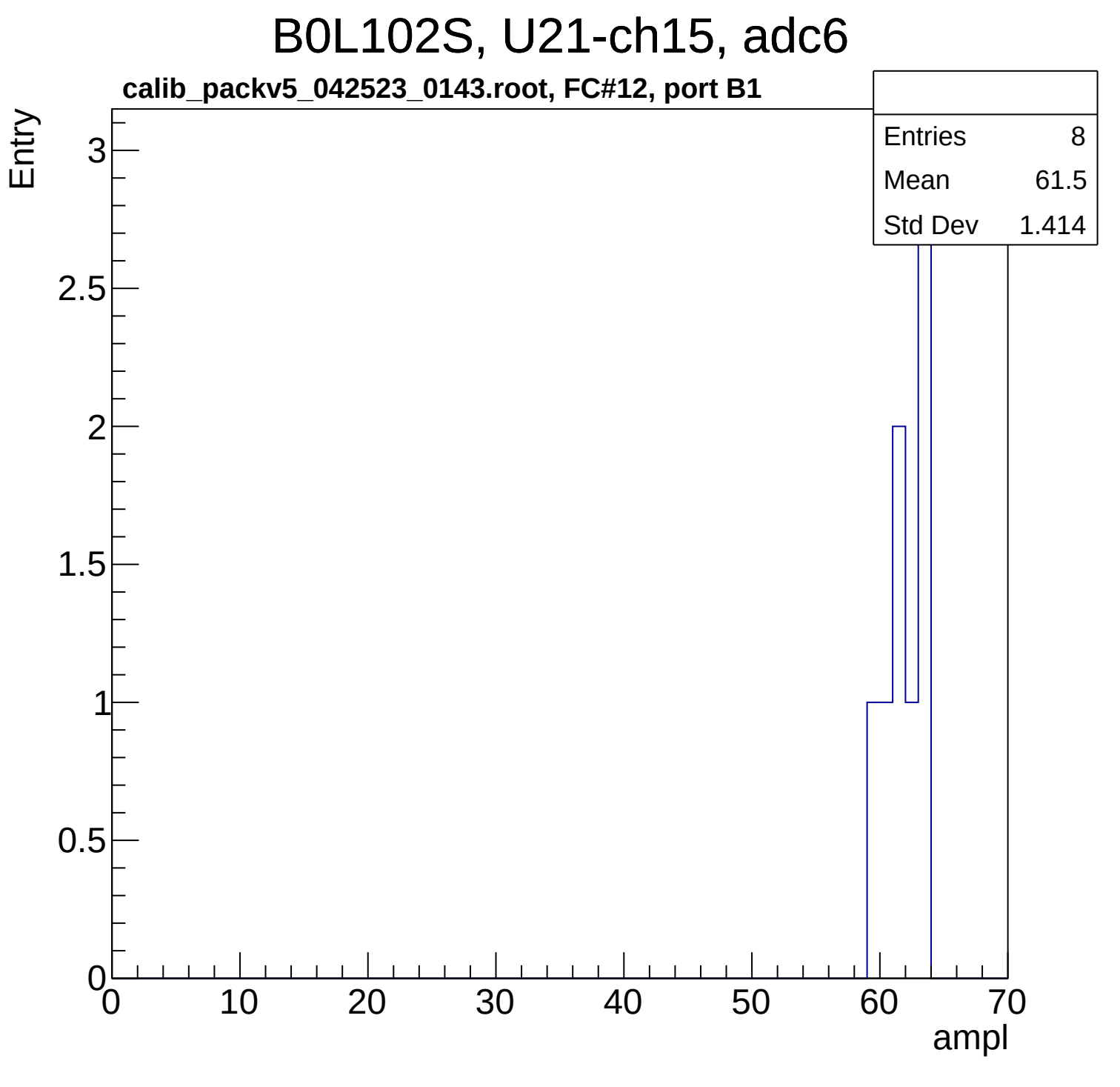
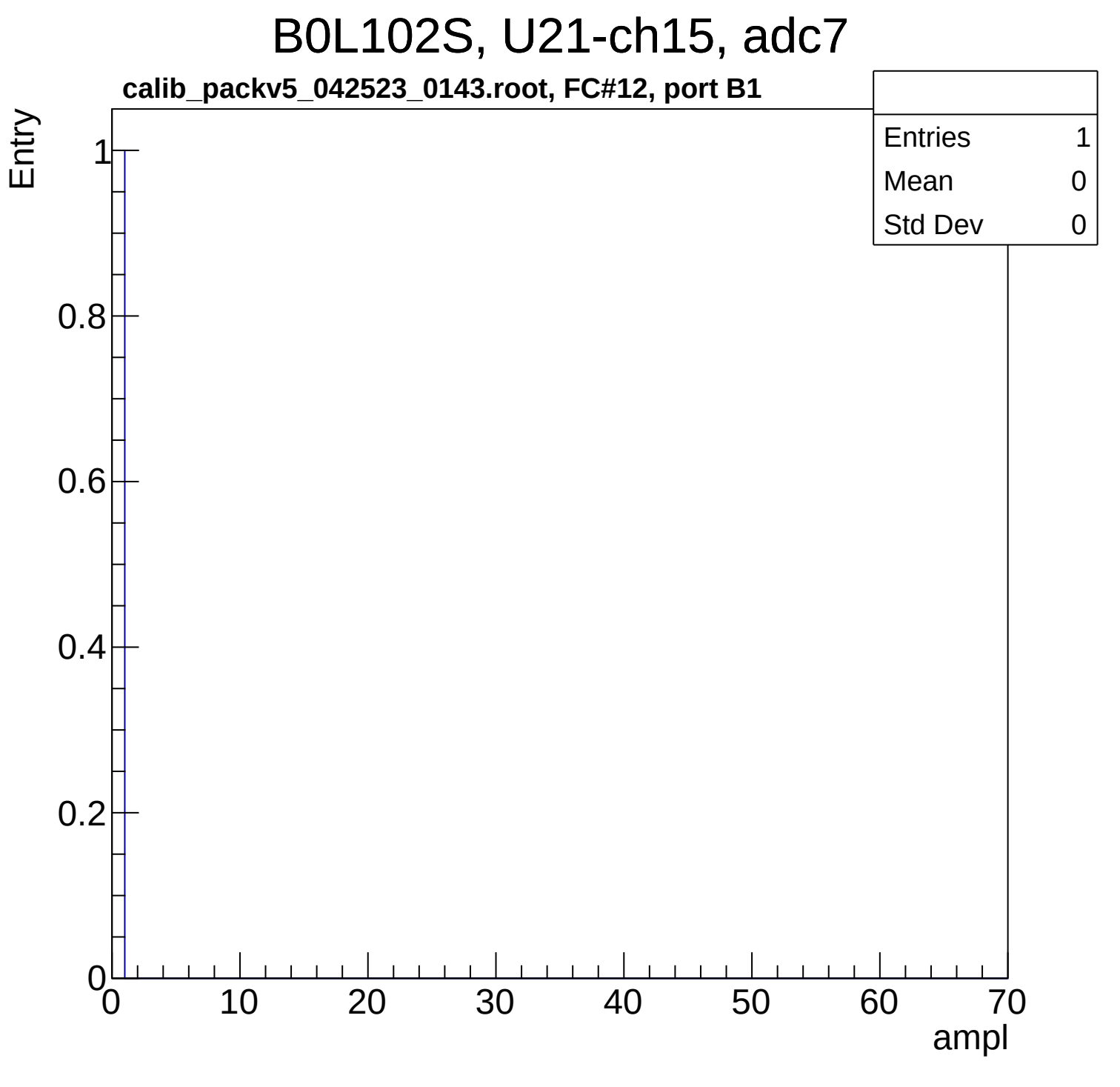
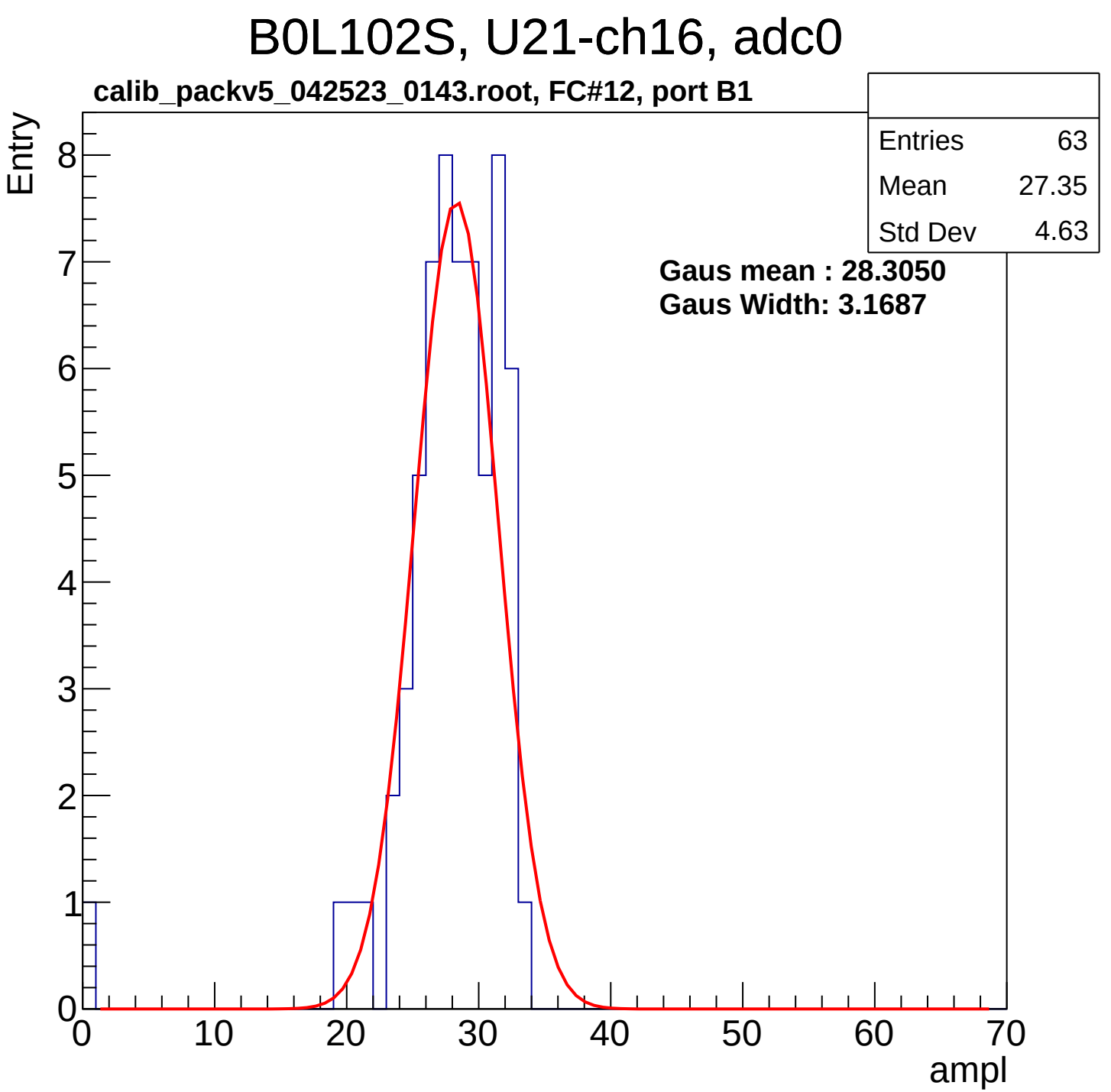
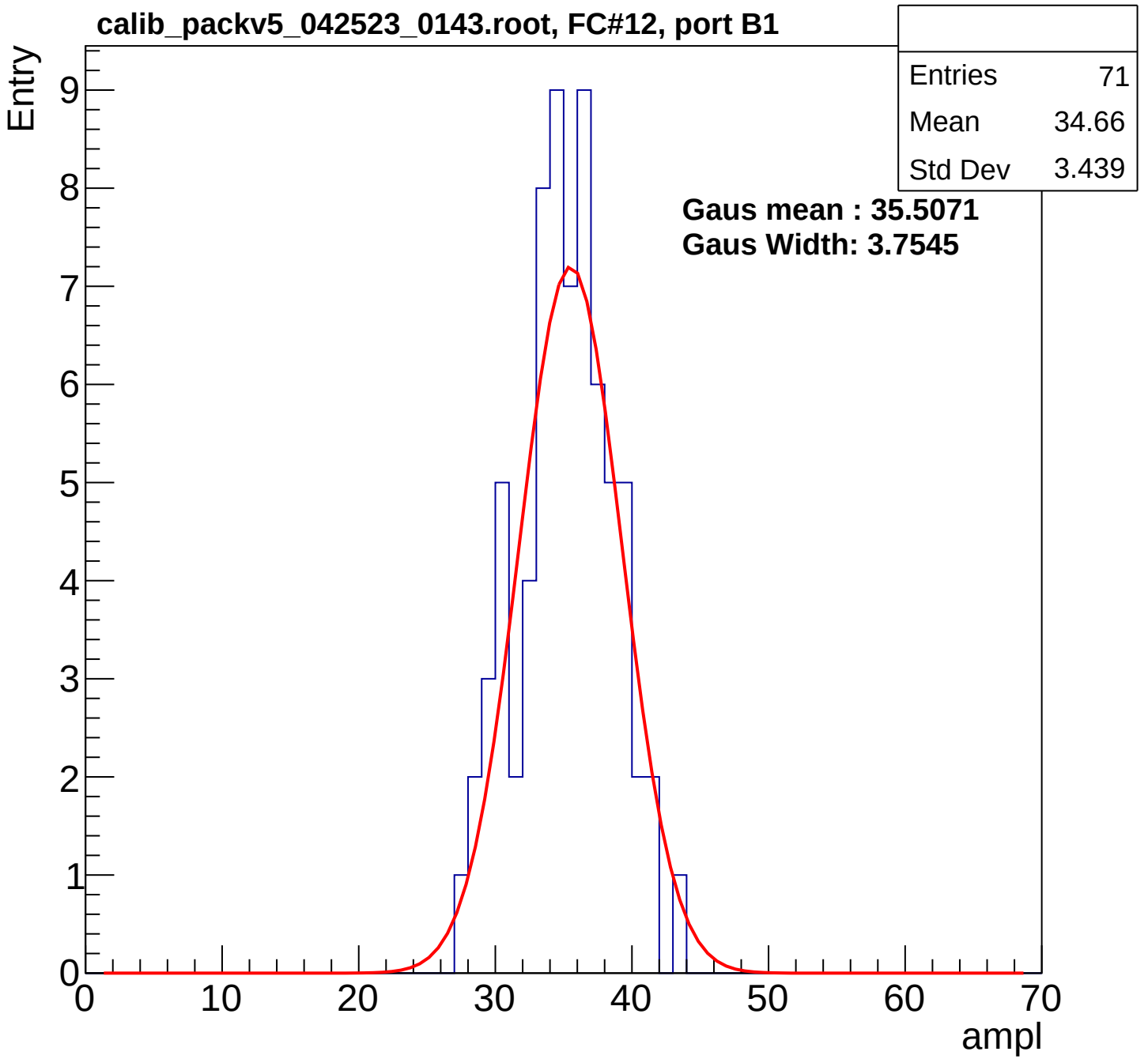
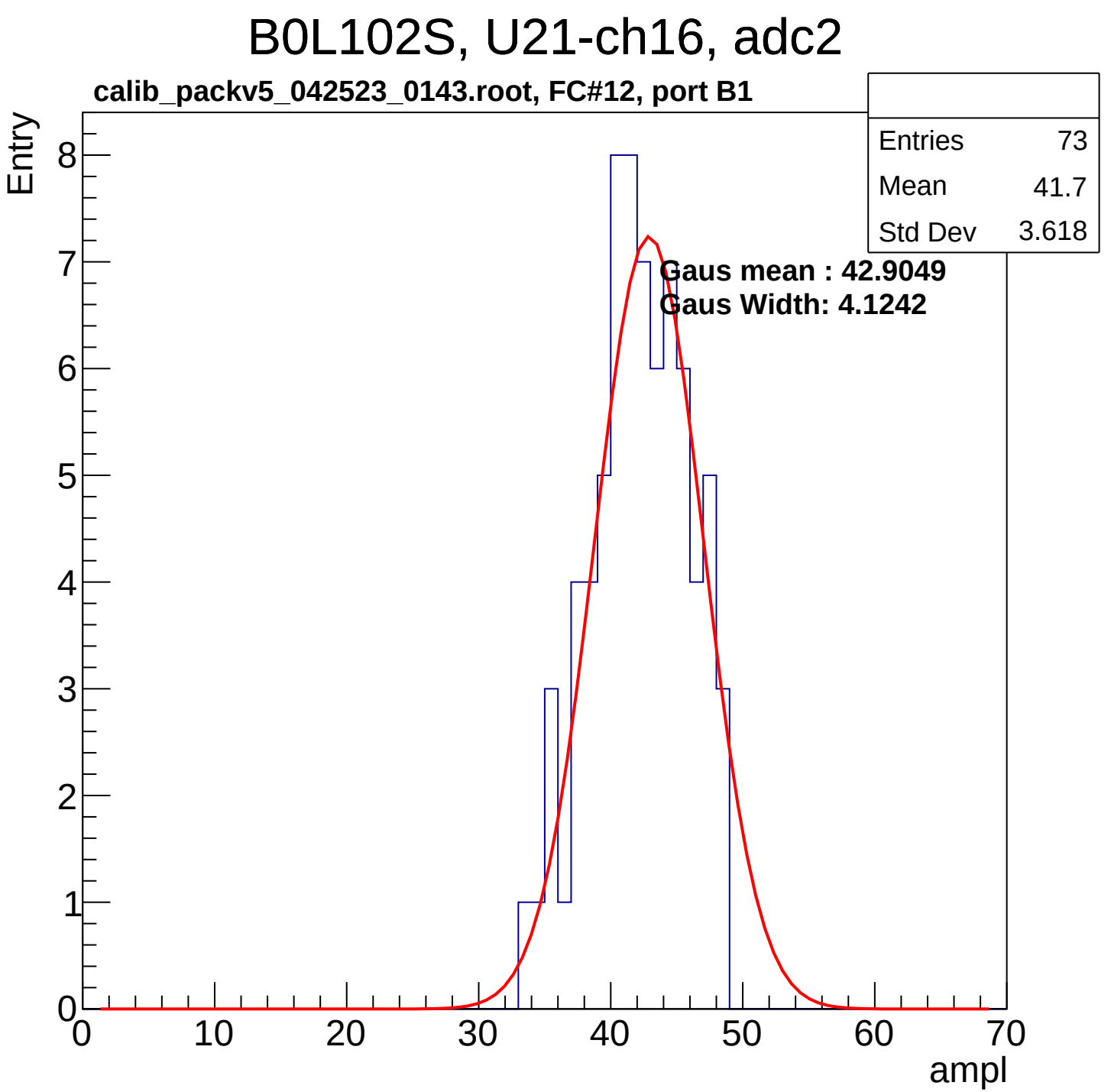
50
60
70
ampl


B0L102S, U21-ch15, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.5
Std Dev 1.414
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch15, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch16, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 27.35
Std Dev 4.63
Gaus mean : 28.3050
Gaus Width: 3.1687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch16, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 34.66
Std Dev 3.439
Gaus mean : 35.5071
Gaus Width: 3.7545
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch16, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 41.7
Std Dev 3.618
Gaus mean : 42.9049
Gaus Width: 4.1242
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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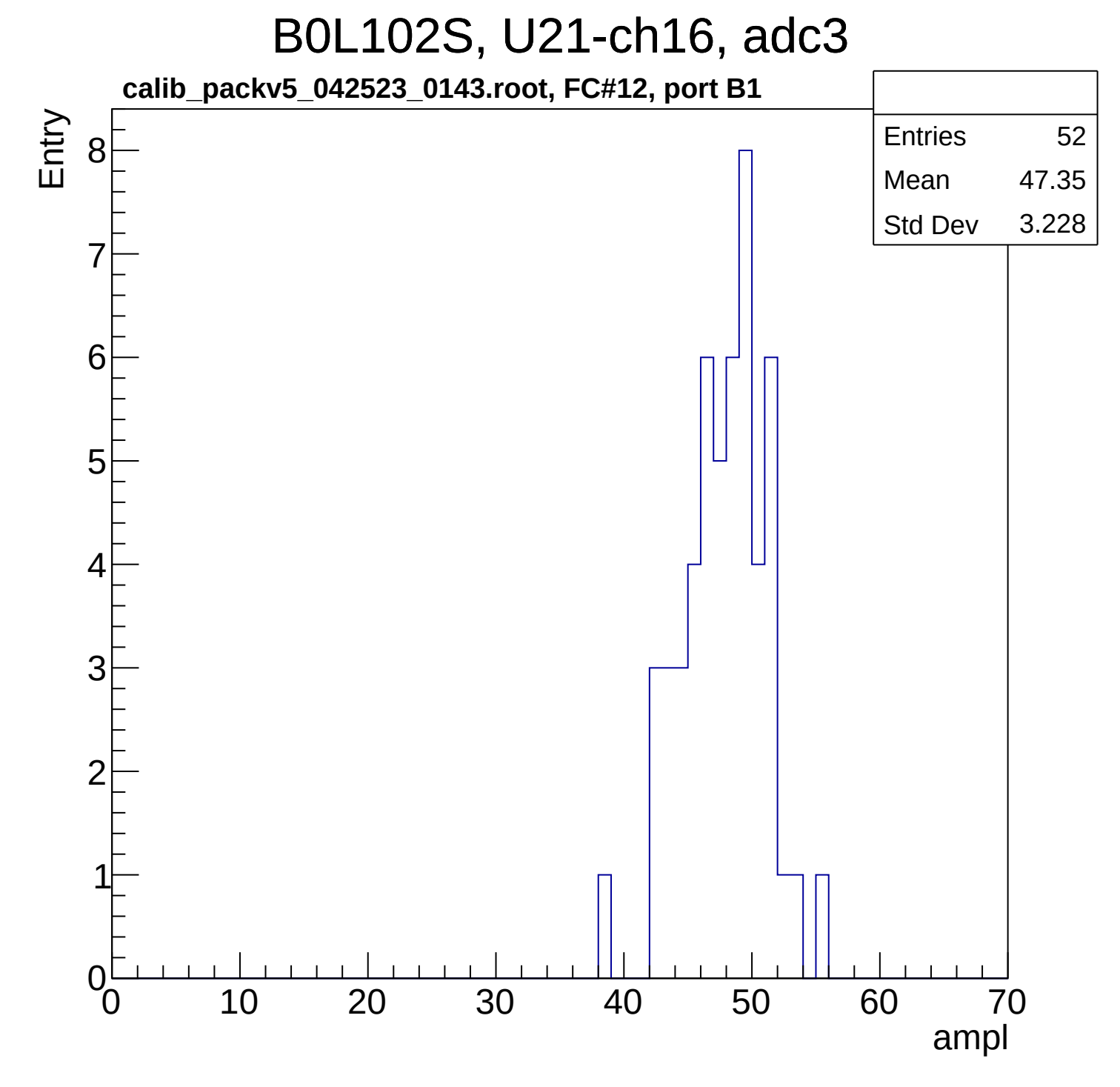
B0L102S, U21-ch16, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 47.35
Std Dev 3.228
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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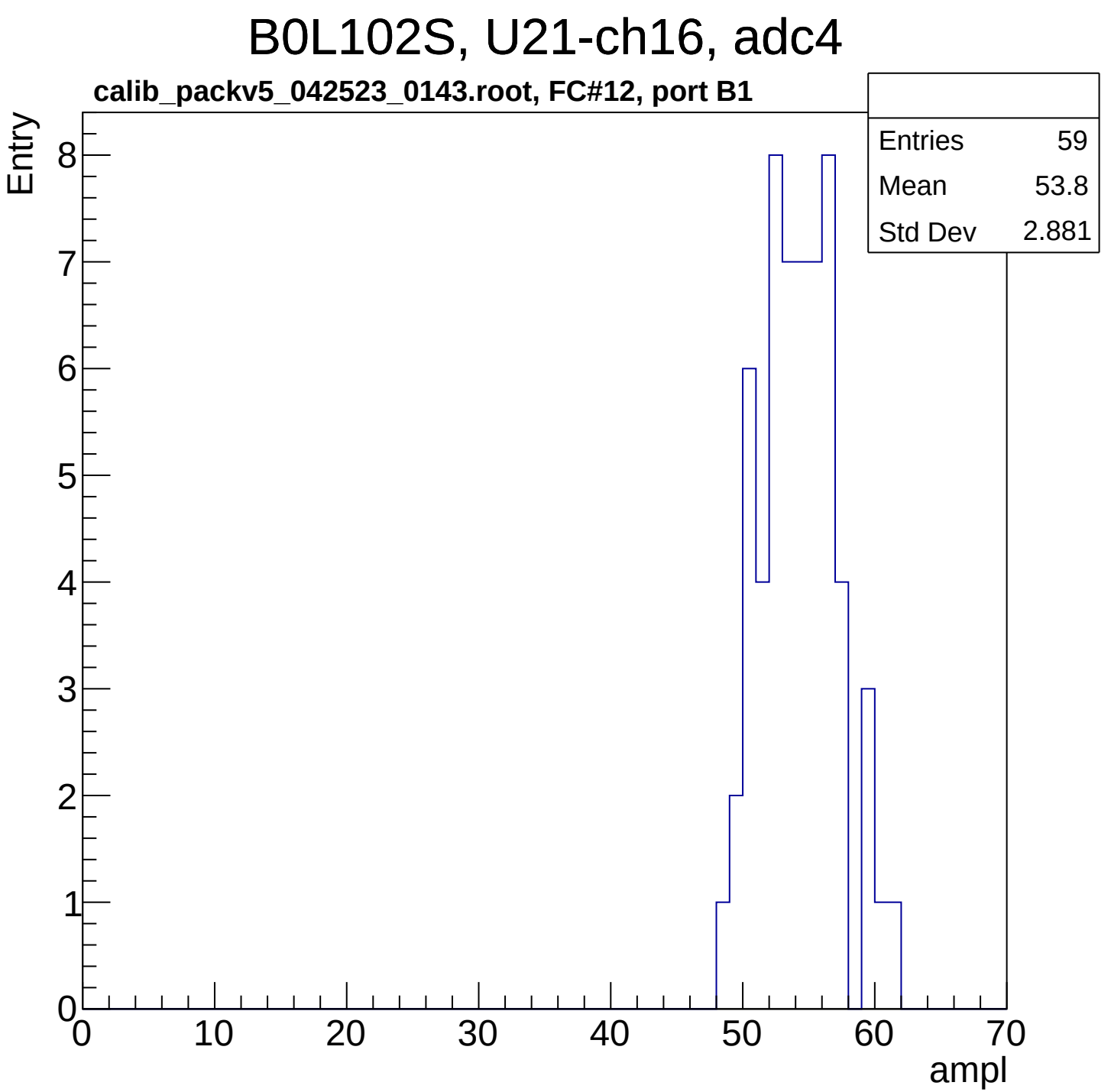
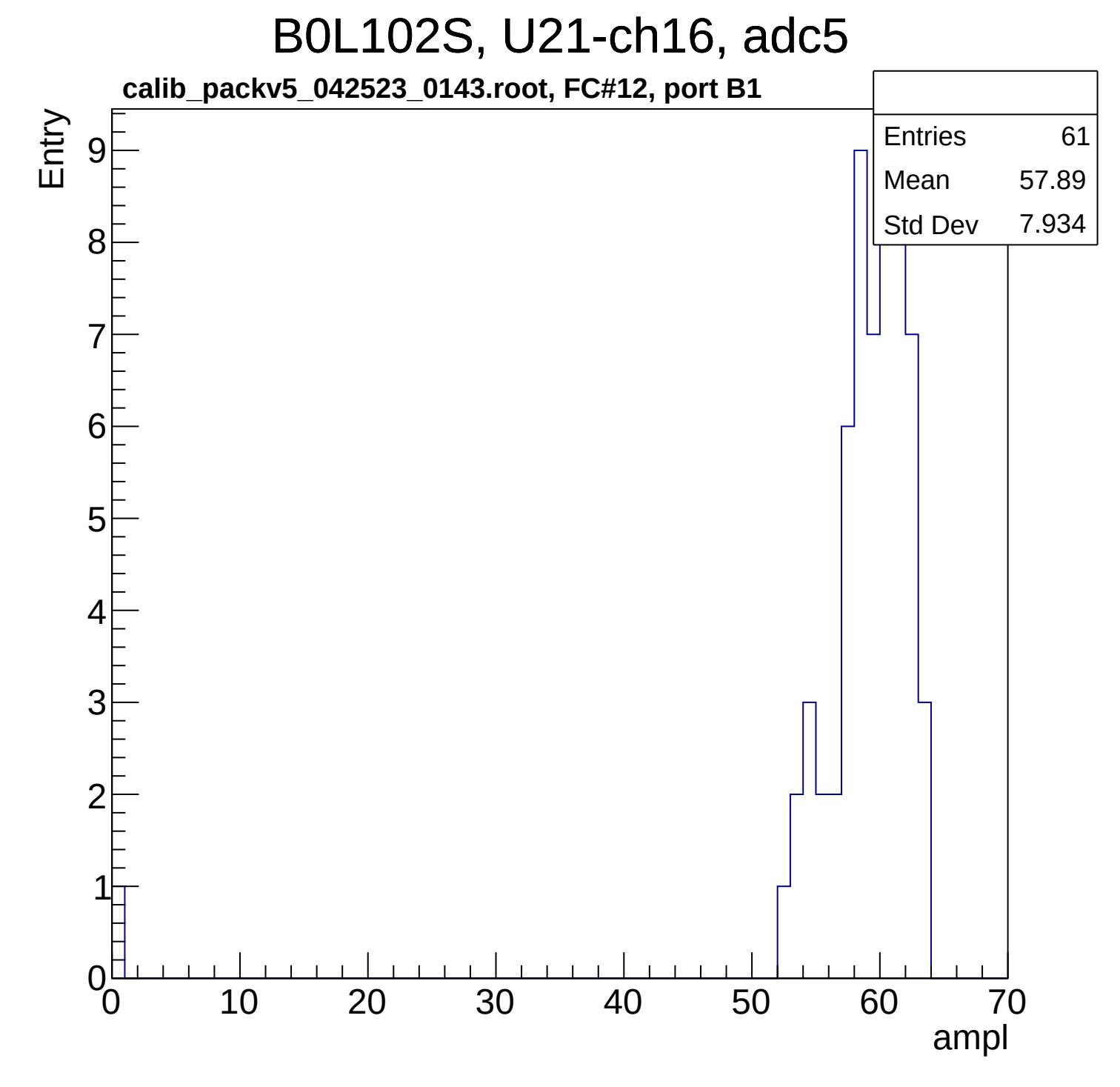

B0L102S, U21-ch16, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 53.8
Std Dev 2.881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch16, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 57.89
Std Dev 7.934
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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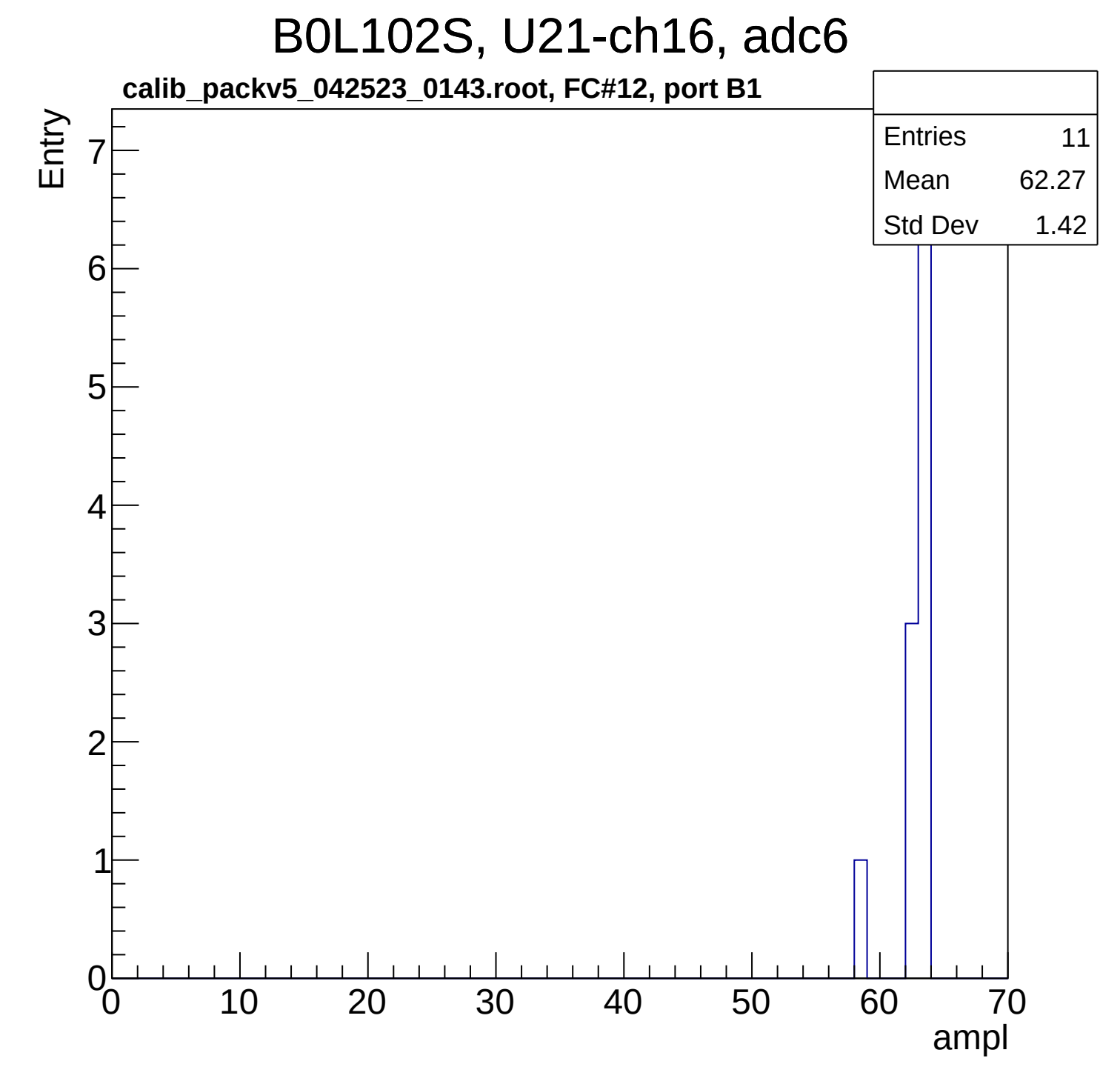
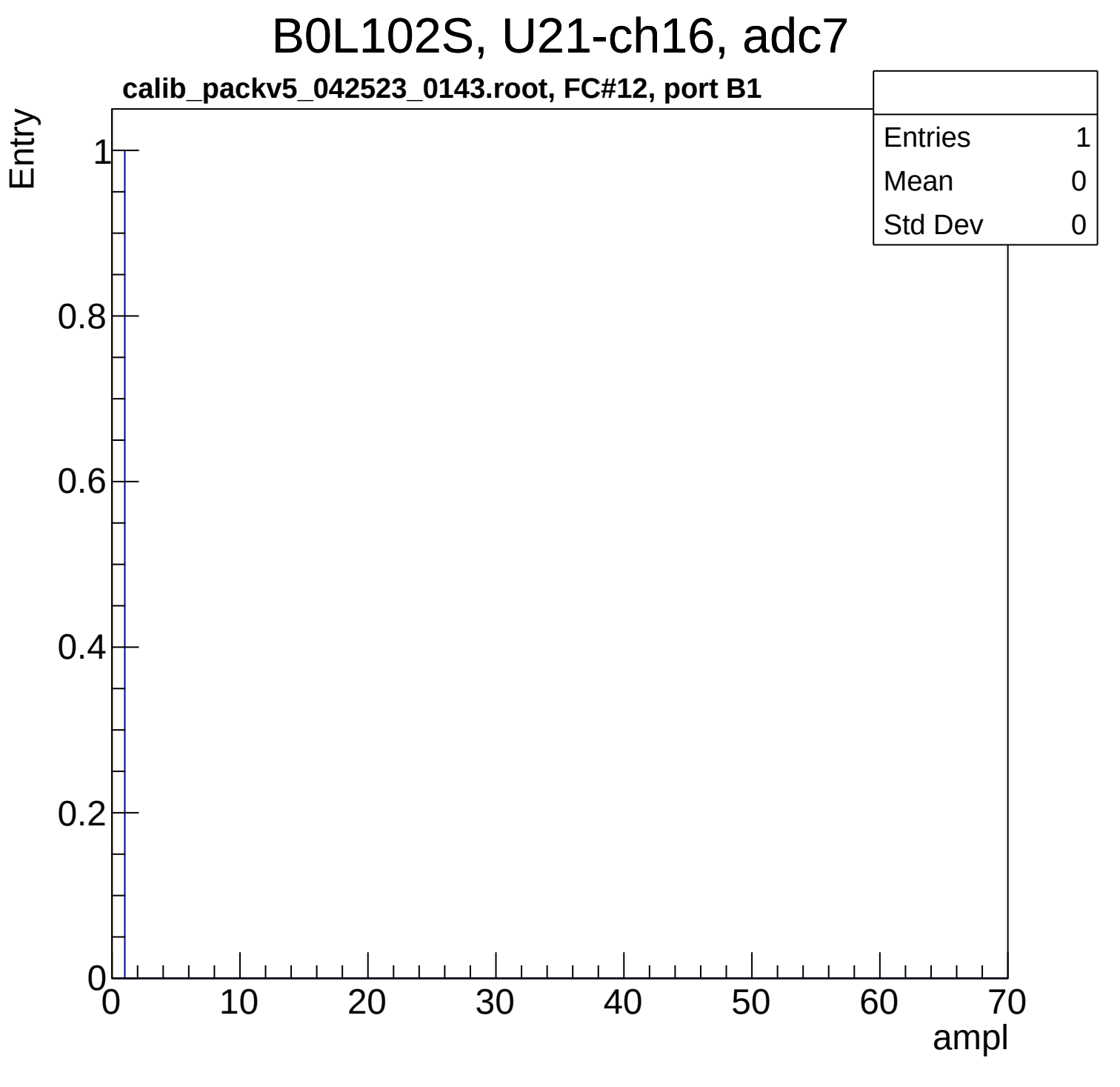
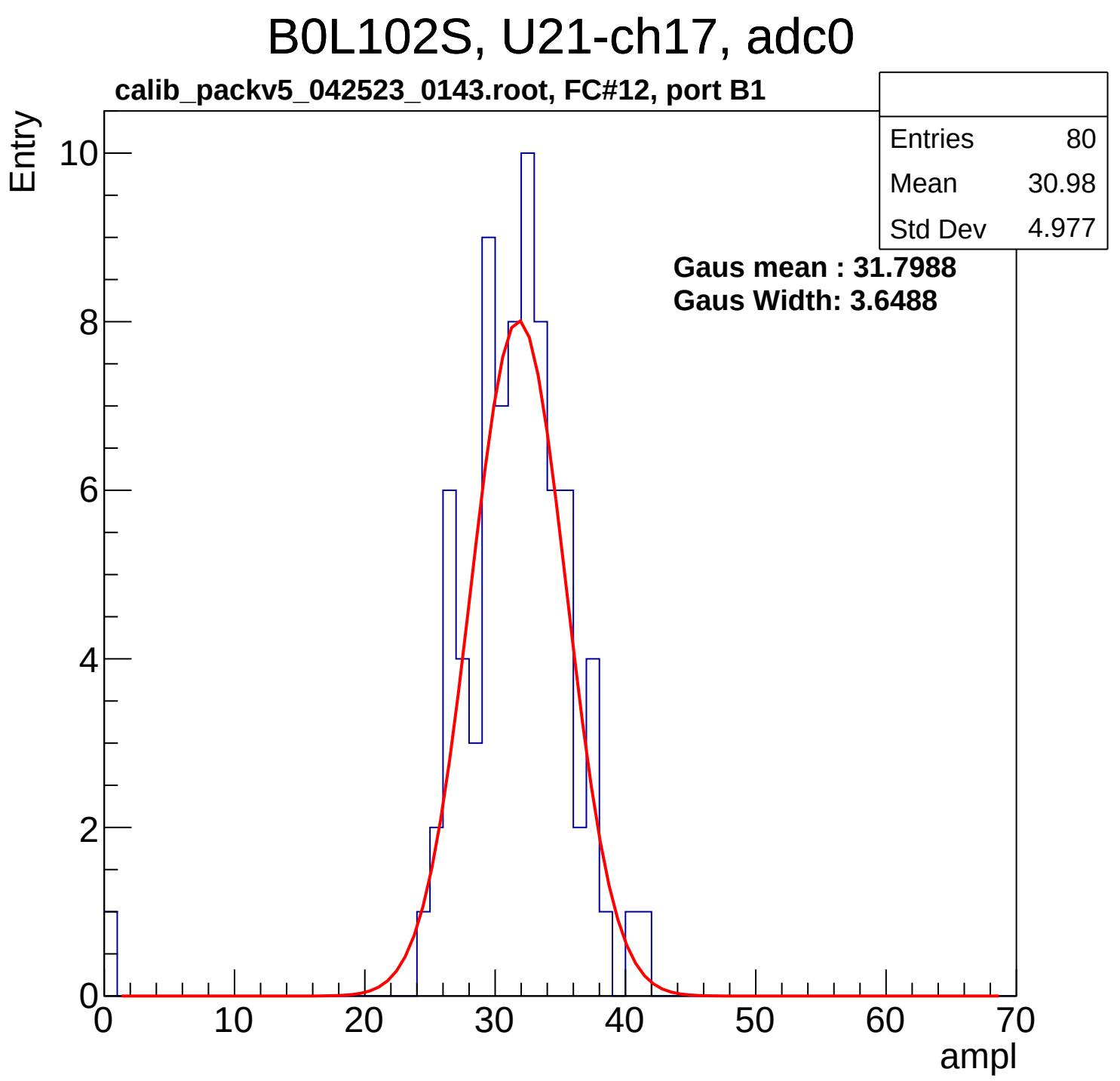
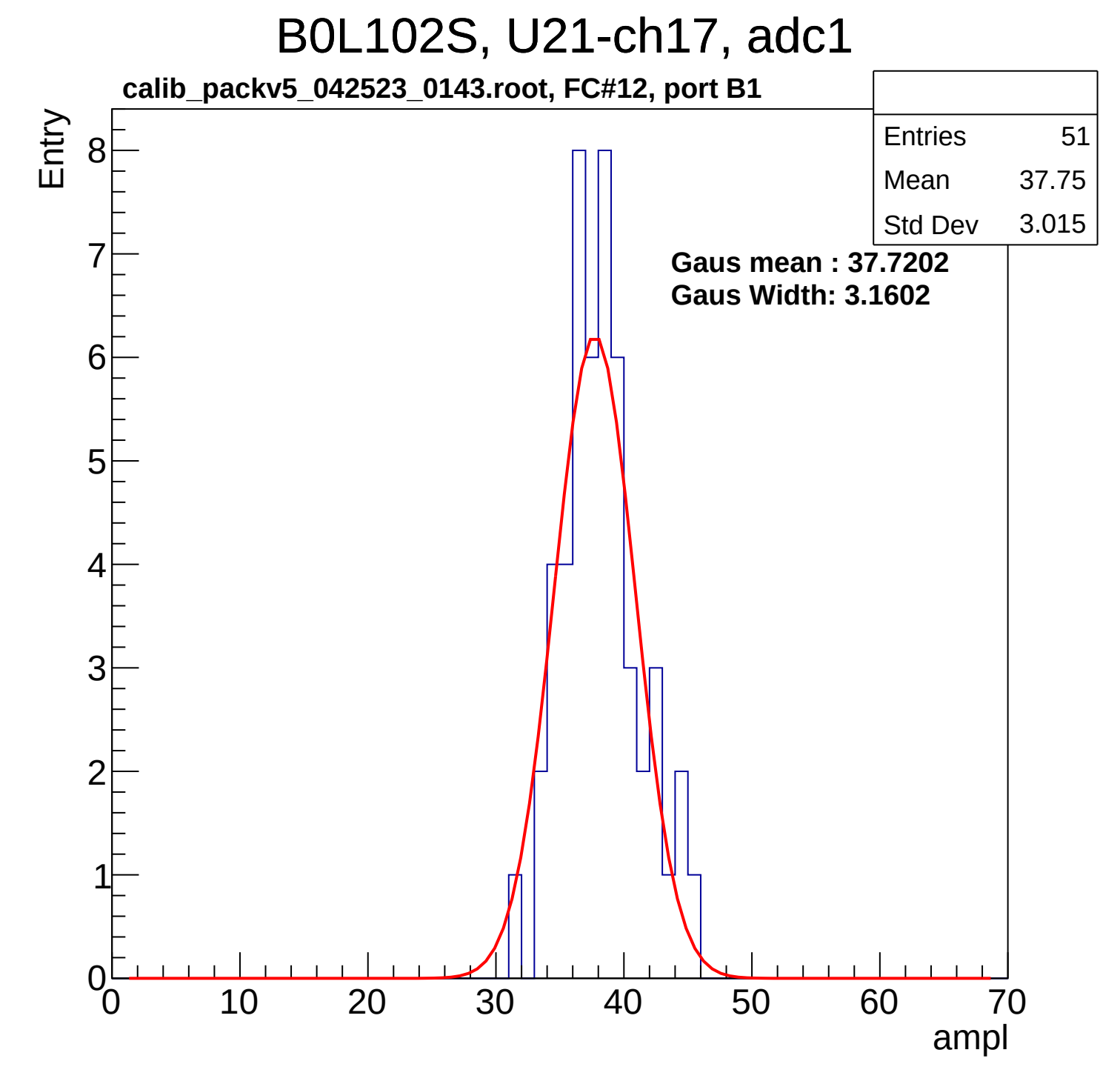
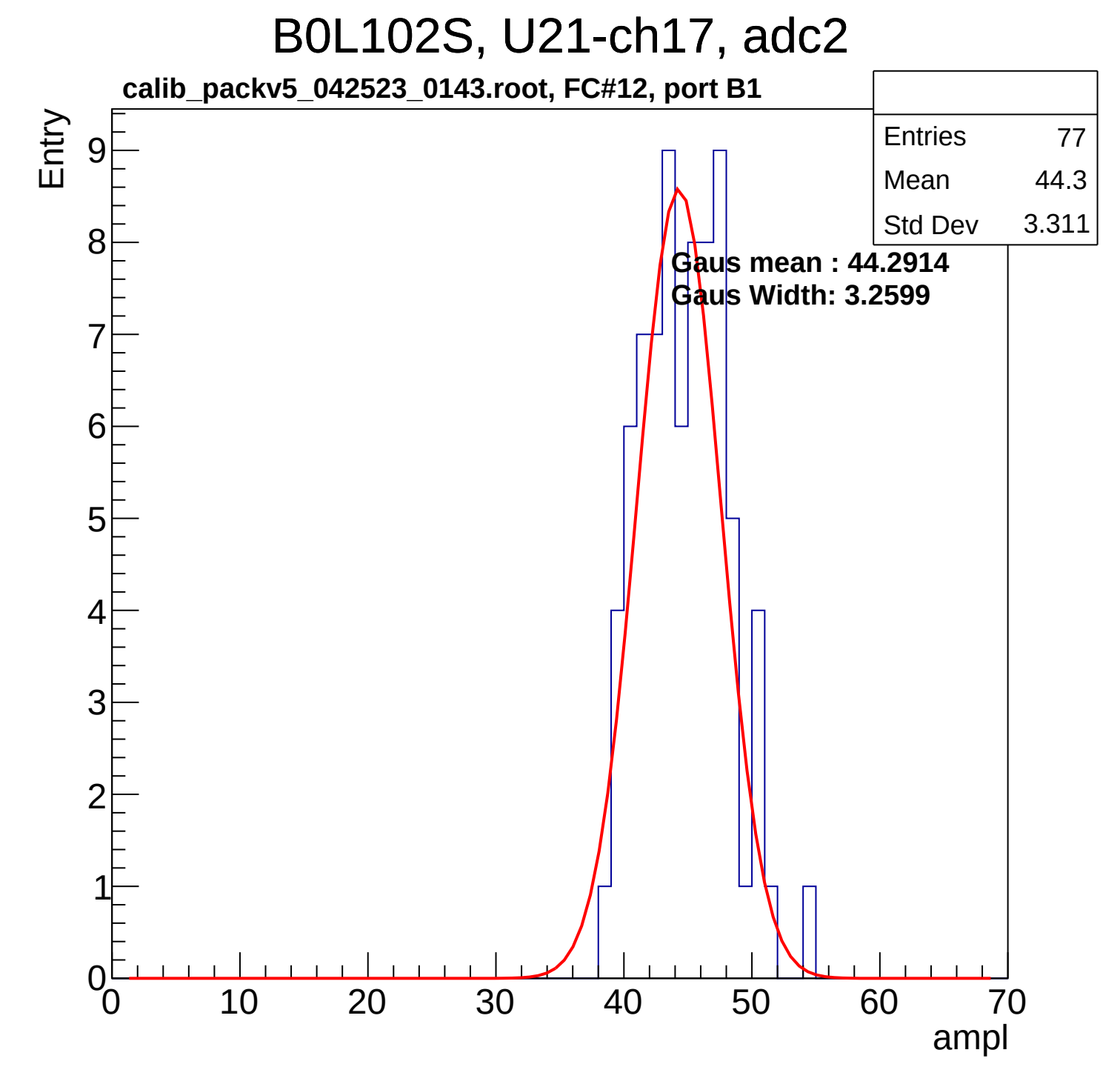
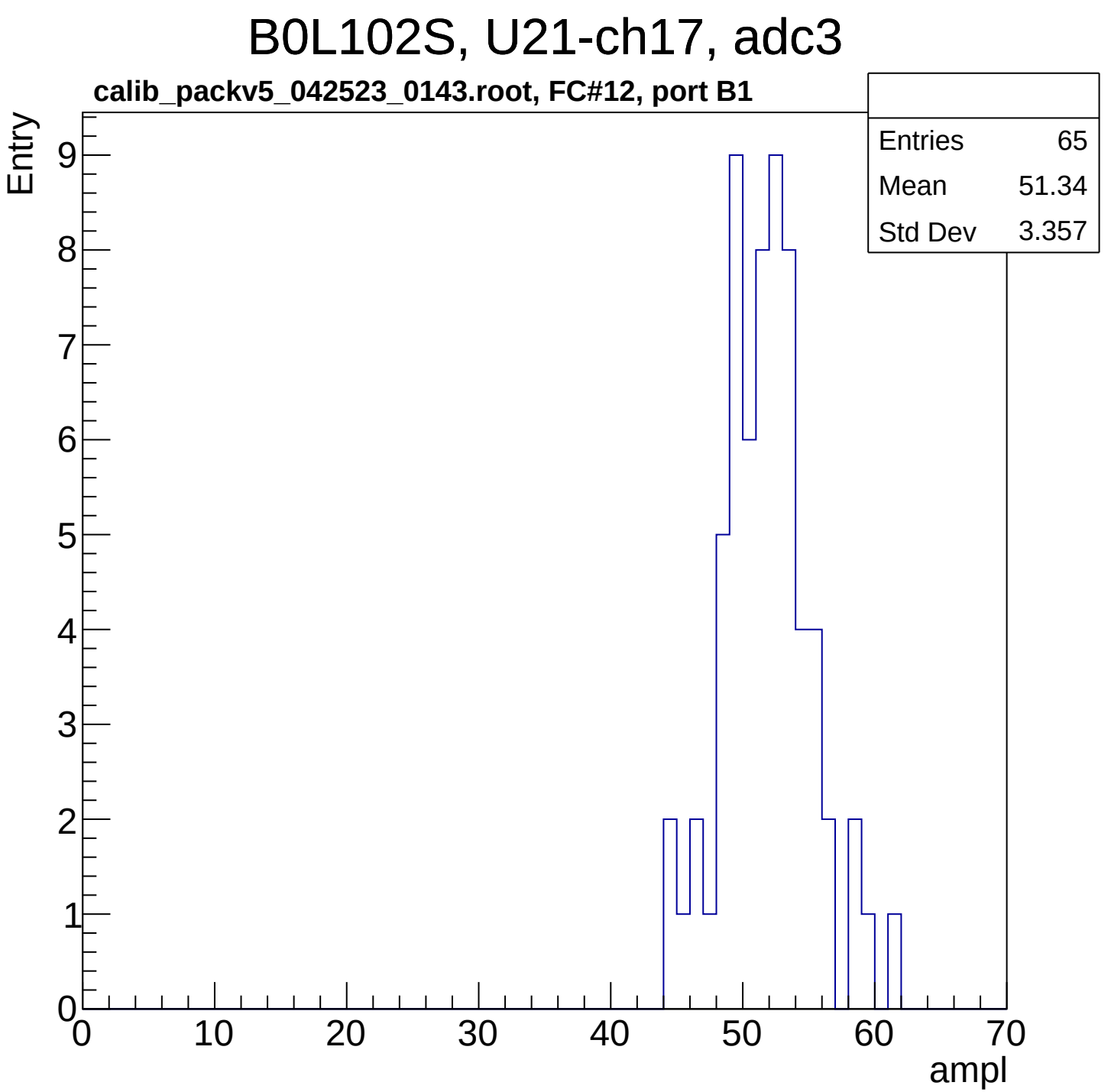

B0L102S, U21-ch16, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 62.27
Std Dev 1.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

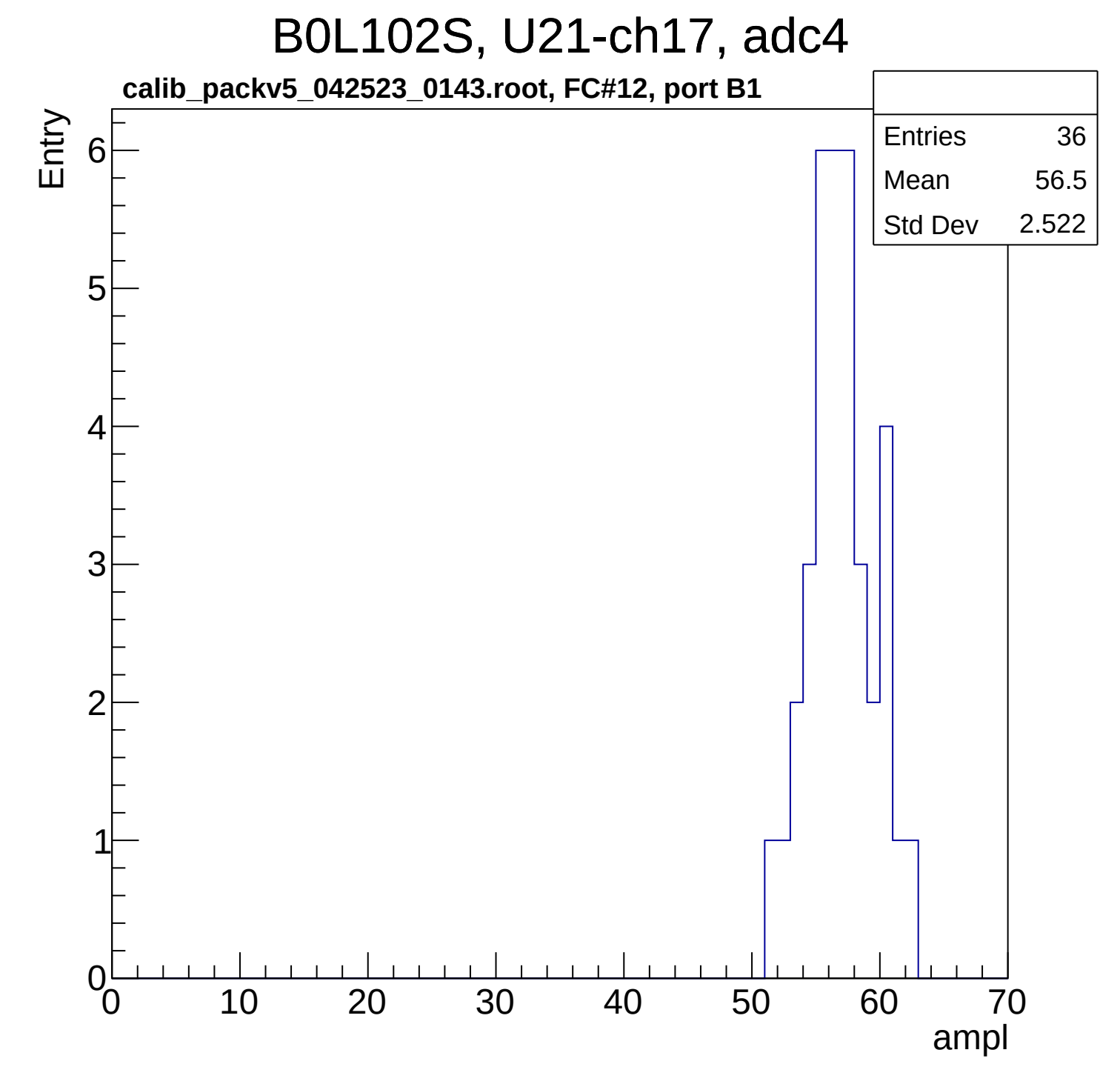

B0L102S, U21-ch16, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

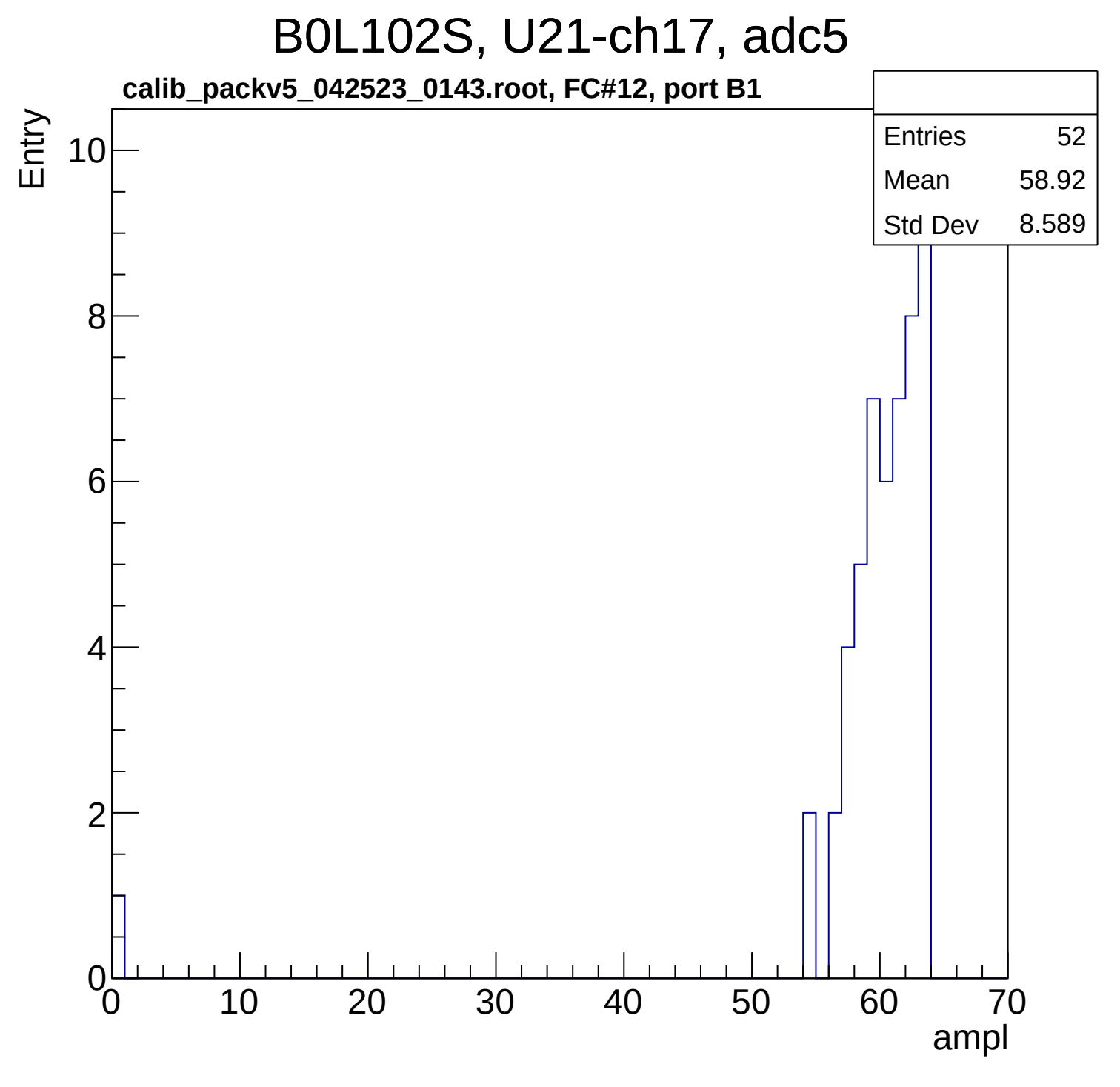

B0L102S, U21-ch17, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 30.98
Std Dev 4.977
Gaus mean : 31.7988
Gaus Width: 3.6488
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

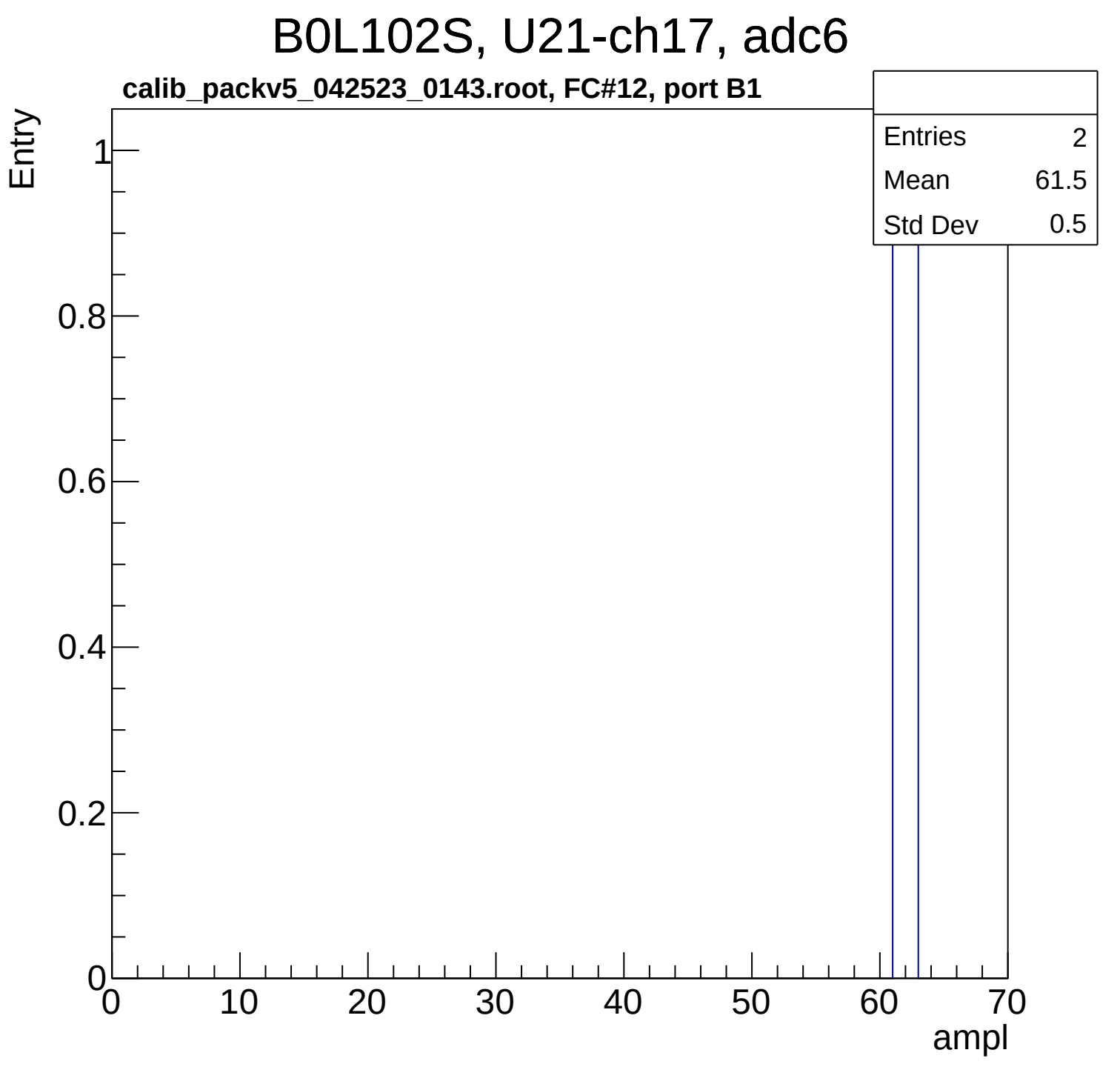

B0L102S, U21-ch17, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 37.75
Std Dev 3.015
Gaus mean : 37.7202
Gaus Width: 3.1602
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

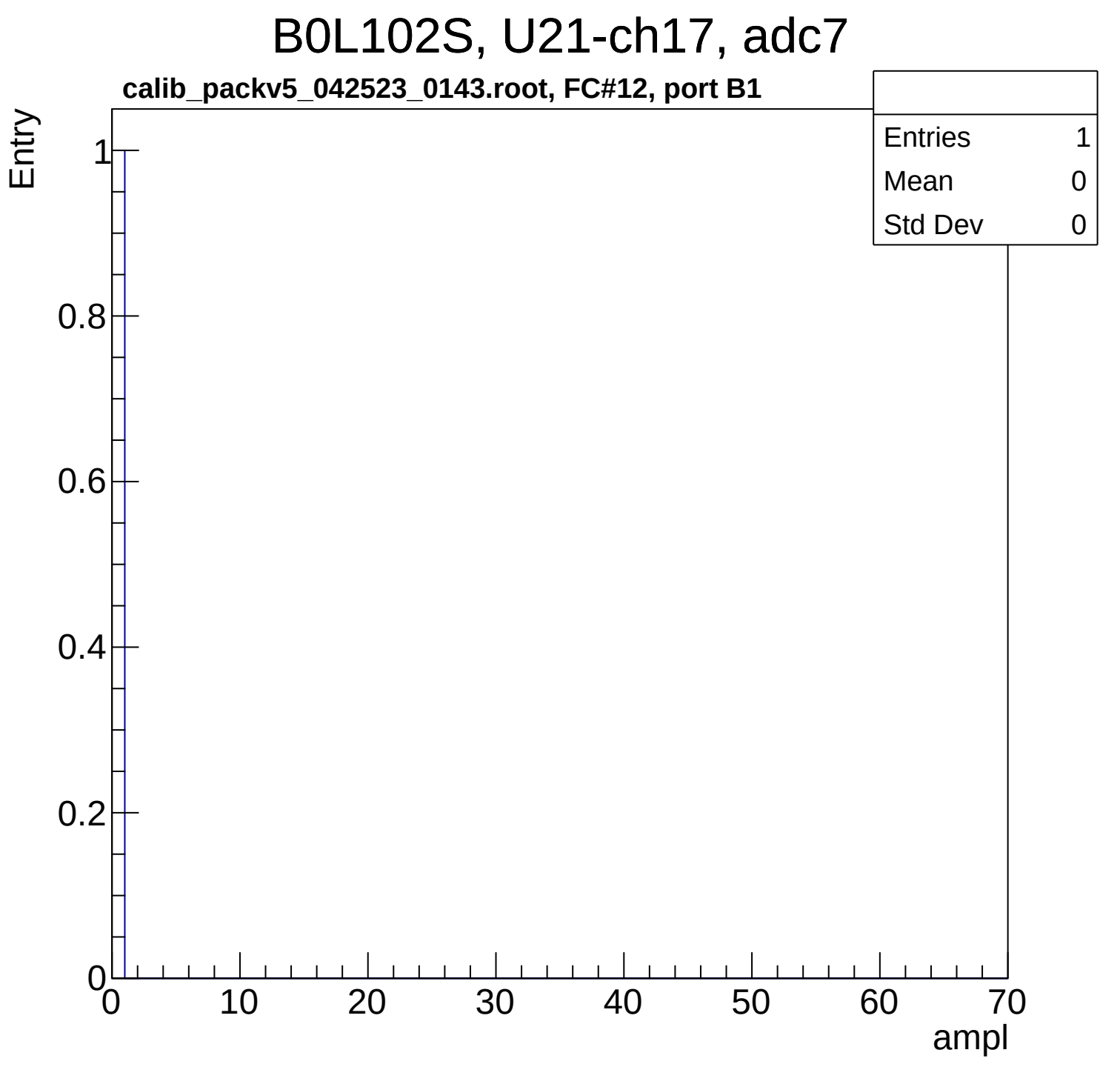

B0L102S, U21-ch17, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 44.3
Std Dev 3.311
Gaus mean : 44.2914
Gaus Width: 3.2599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

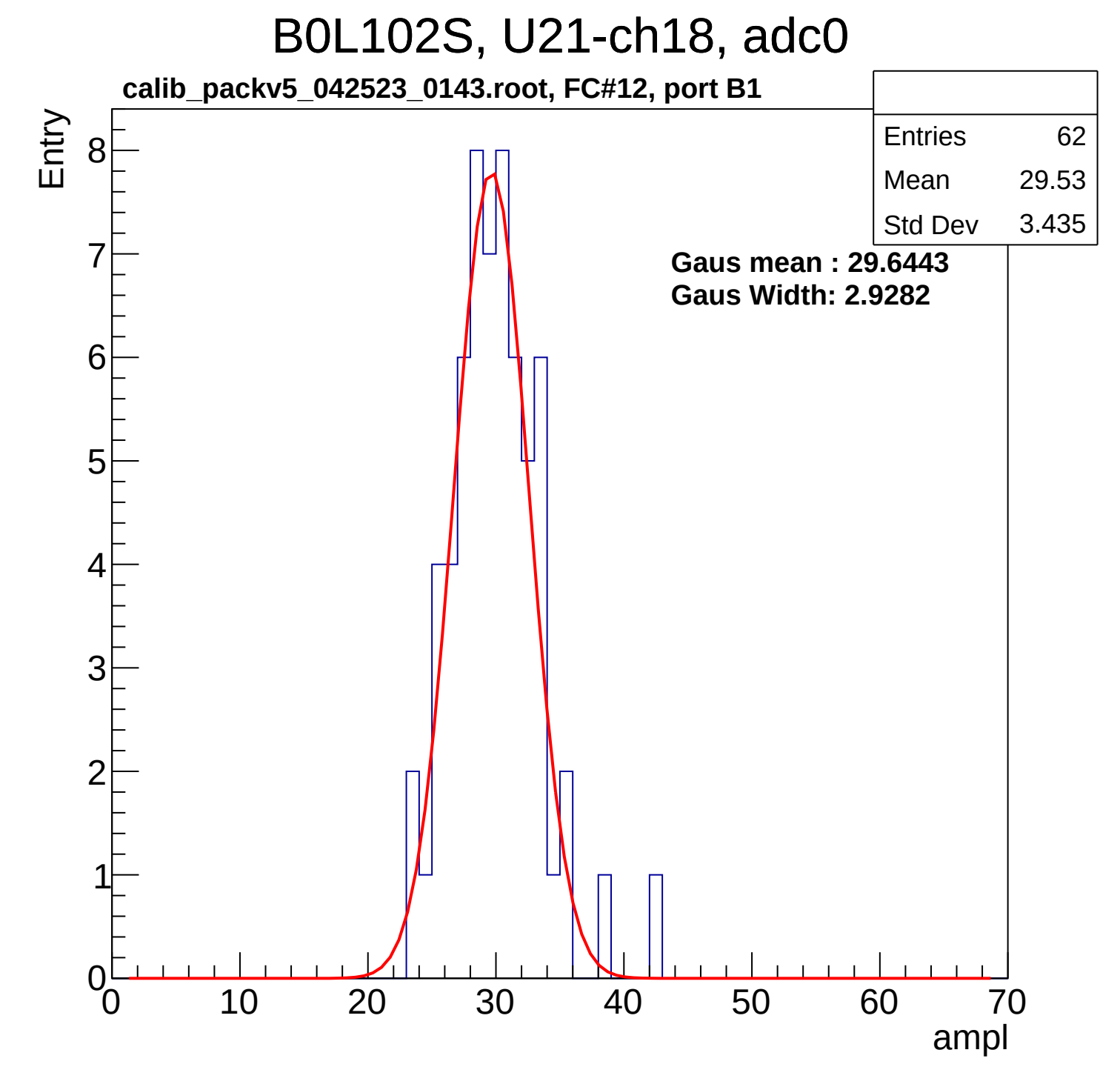

B0L102S, U21-ch17, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 51.34
Std Dev 3.357
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

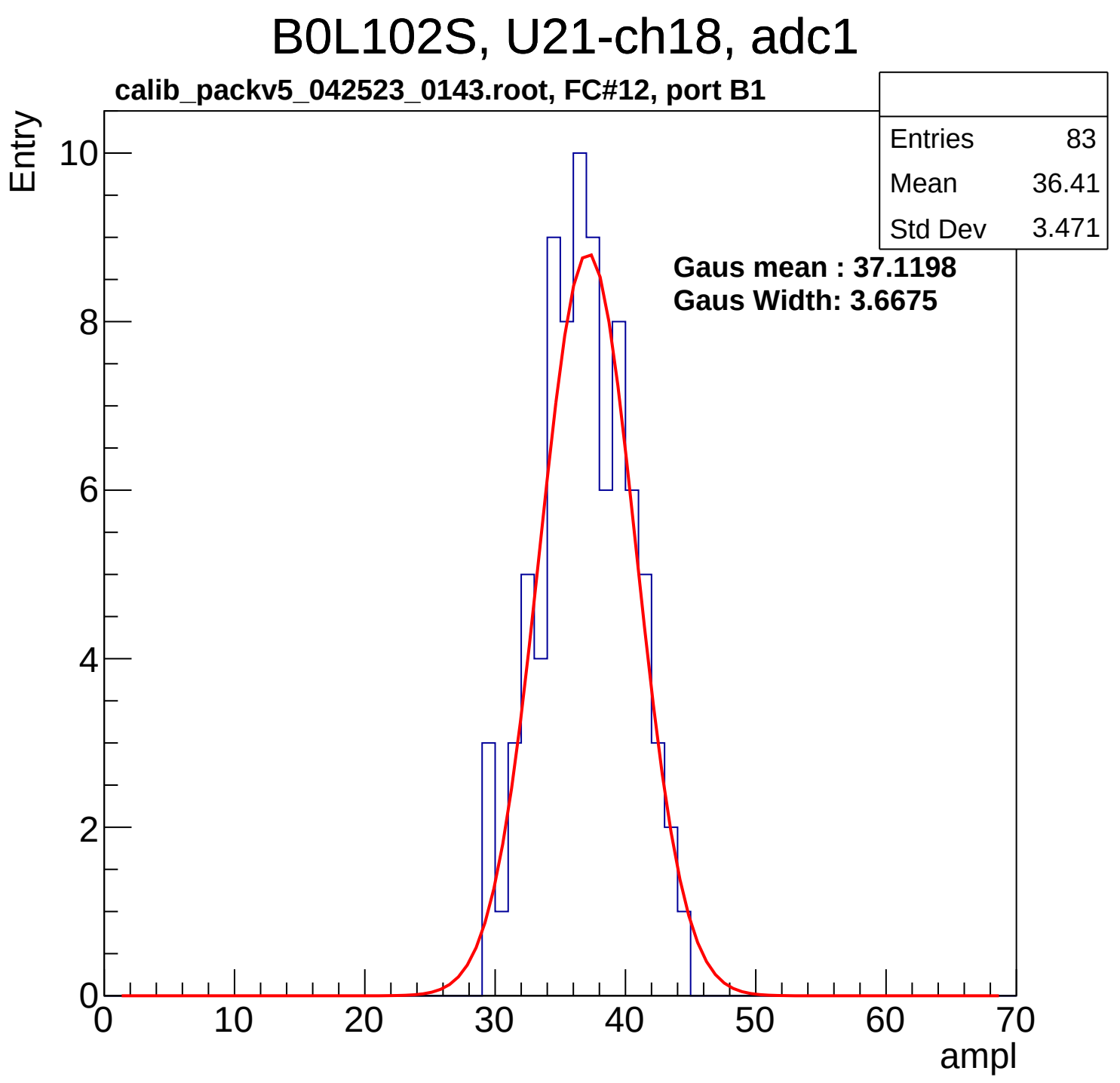

B0L102S, U21-ch17, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 56.5
Std Dev 2.522
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

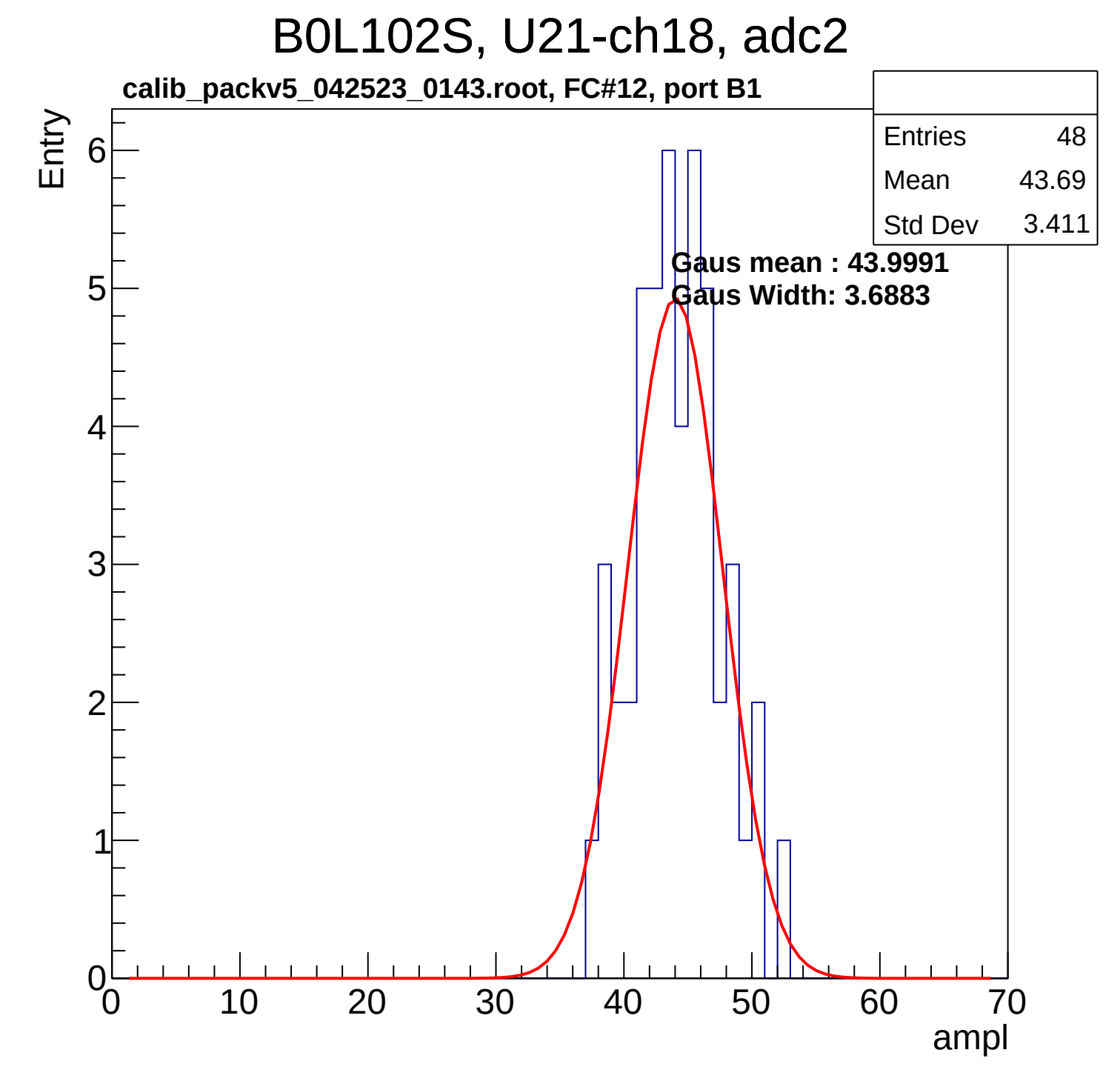

B0L102S, U21-ch17, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.92
Std Dev 8.589
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch17, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch17, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch18, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 29.53
Std Dev 3.435
Gaus mean : 29.6443
Gaus Width: 2.9282
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch18, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 36.41
Std Dev 3.471
Gaus mean : 37.1198
Gaus Width: 3.6675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch18, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 43.69
Std Dev 3.411
Gaus mean : 43.9991
Gaus Width: 3.6883
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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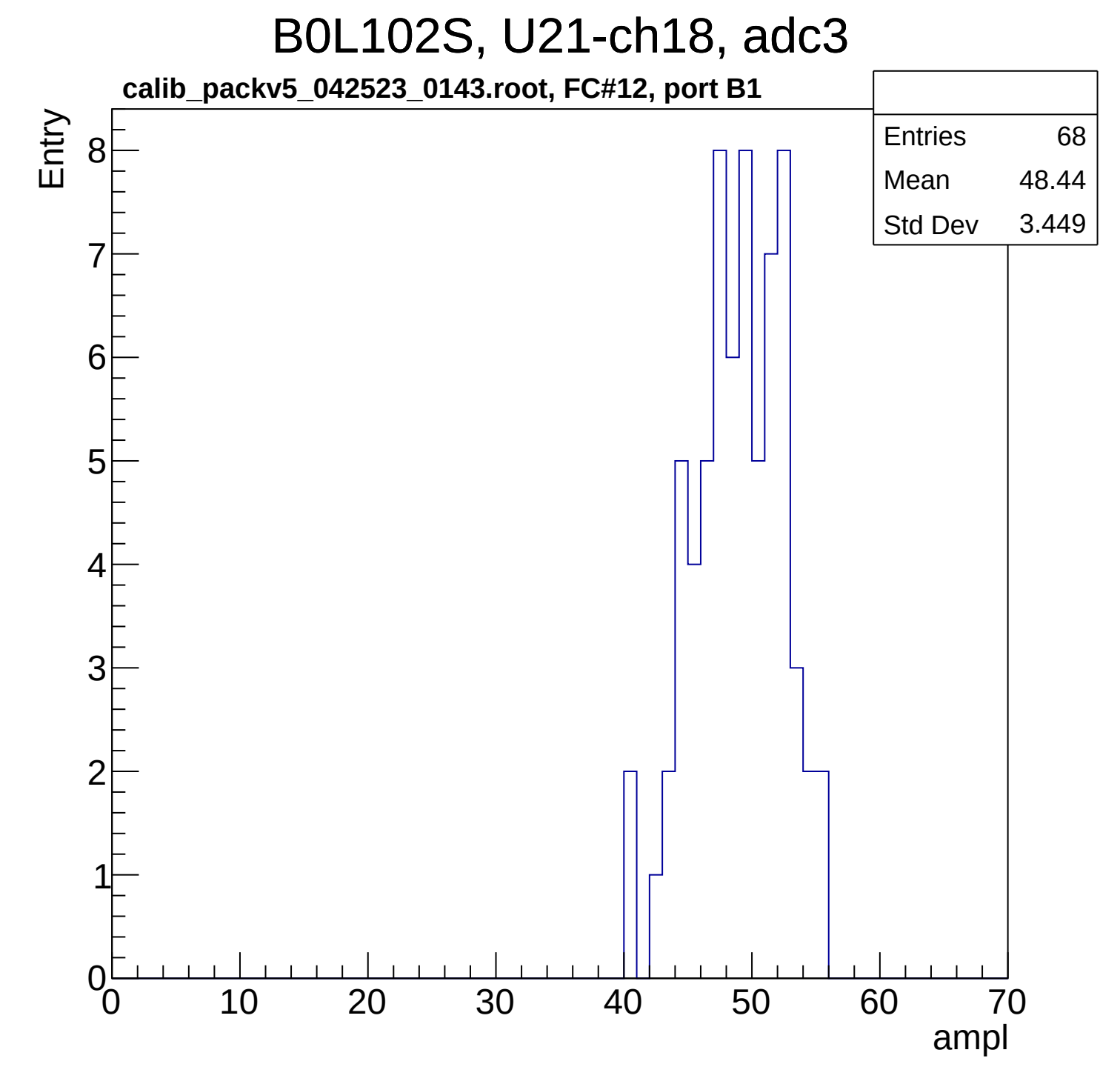
B0L102S, U21-ch18, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.44
Std Dev 3.449
Entry
ampl
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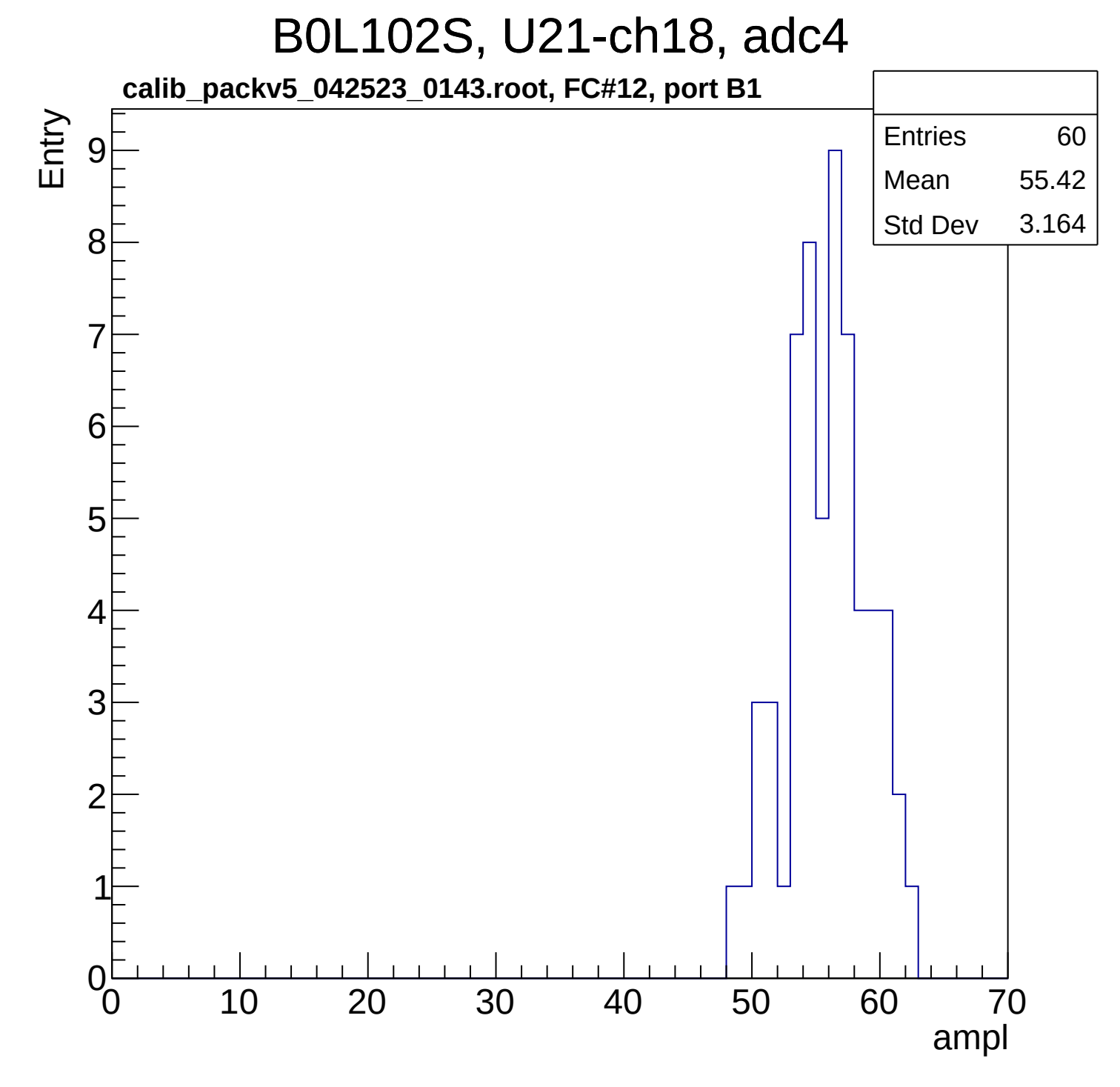
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch18, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 55.42
Std Dev 3.164
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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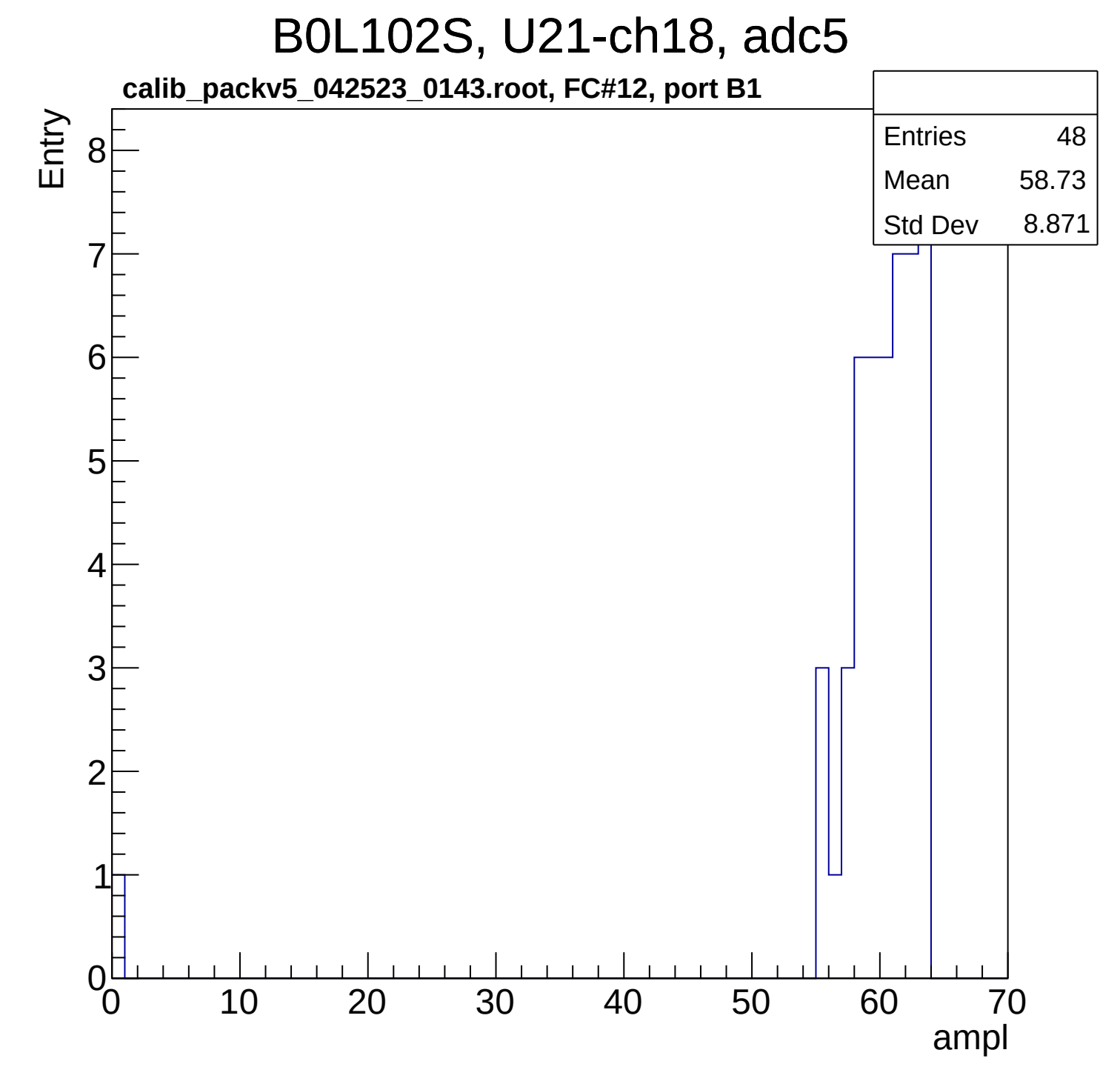
B0L102S, U21-ch18, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.73
Std Dev 8.871
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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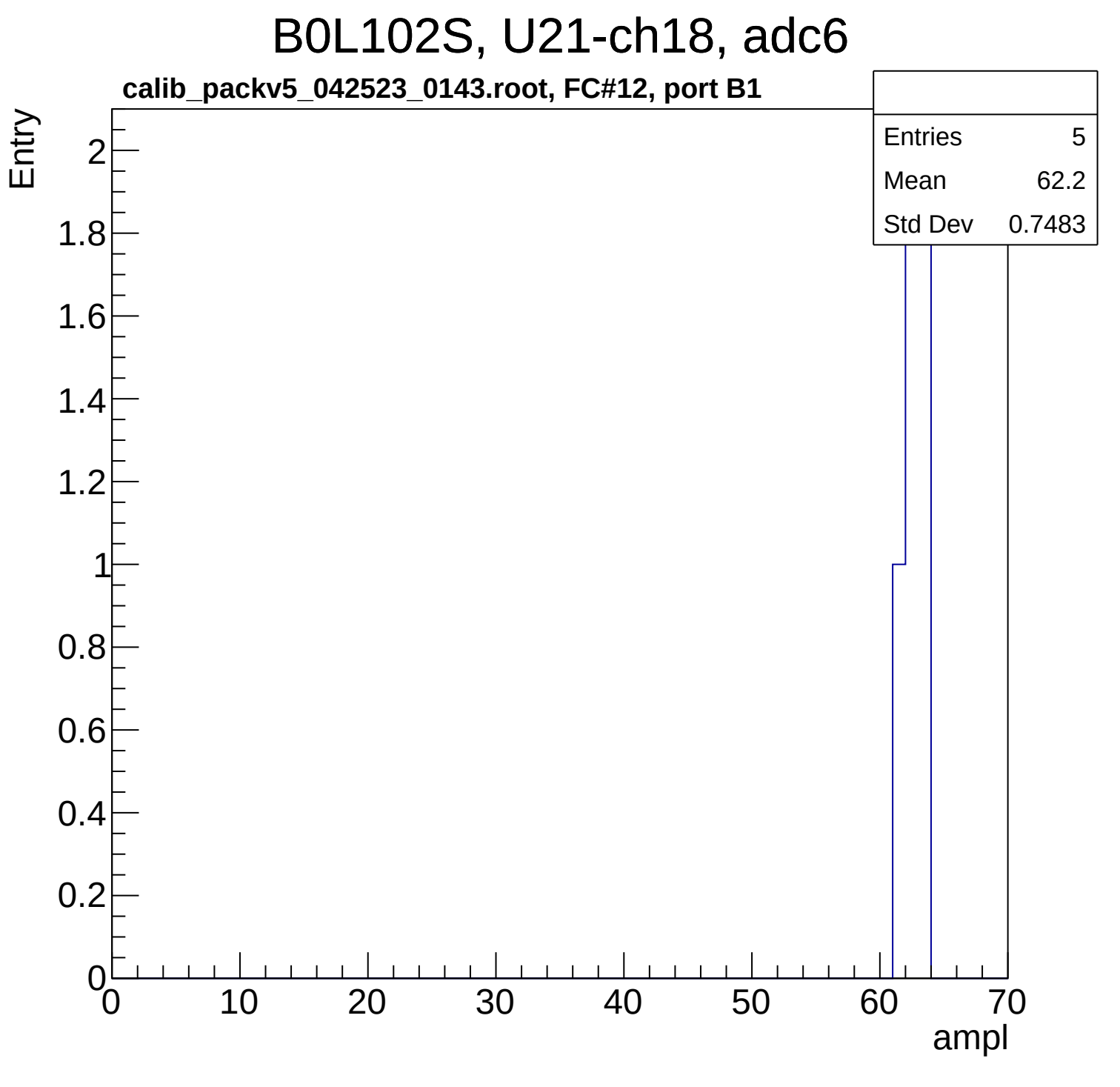
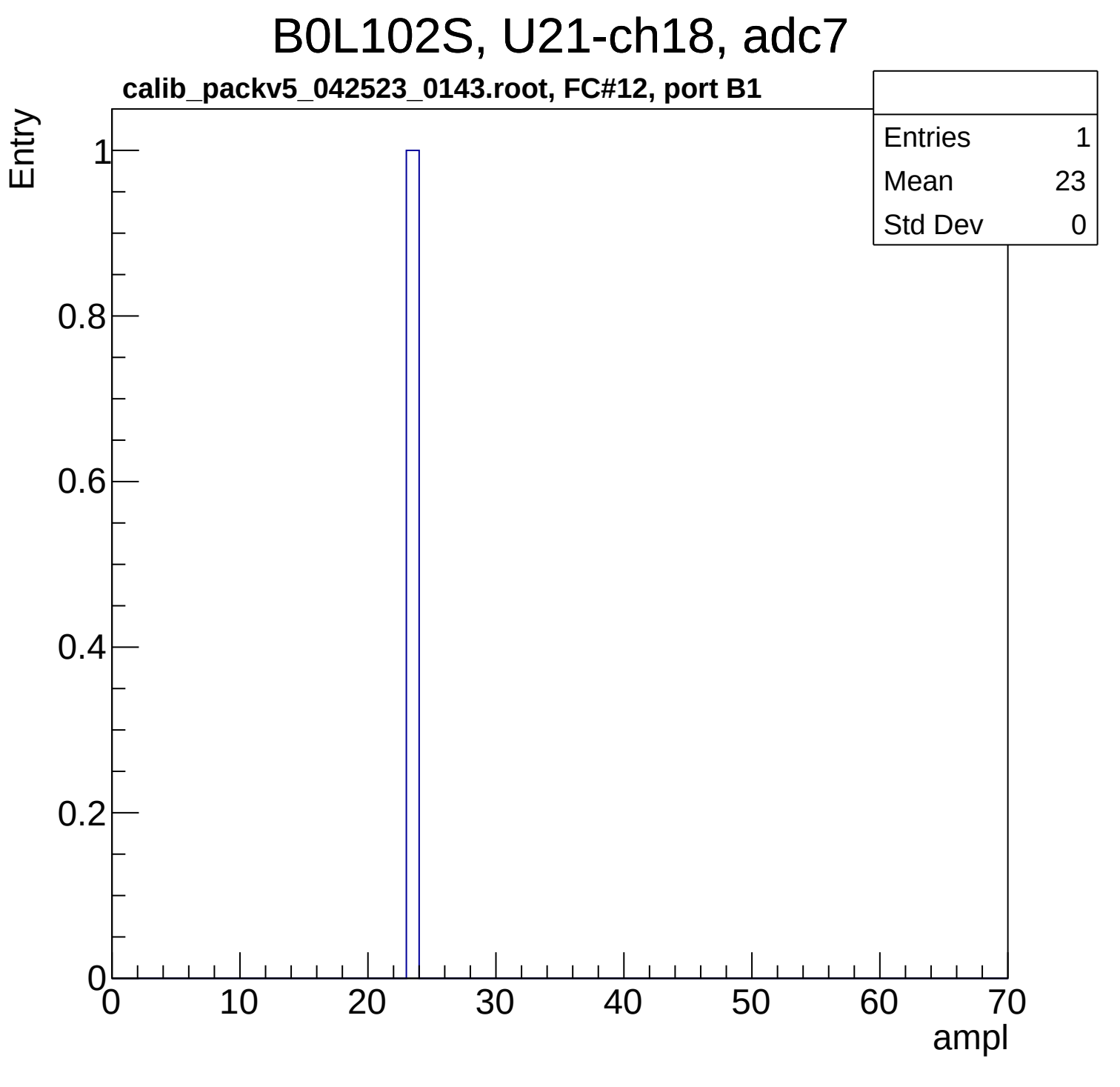
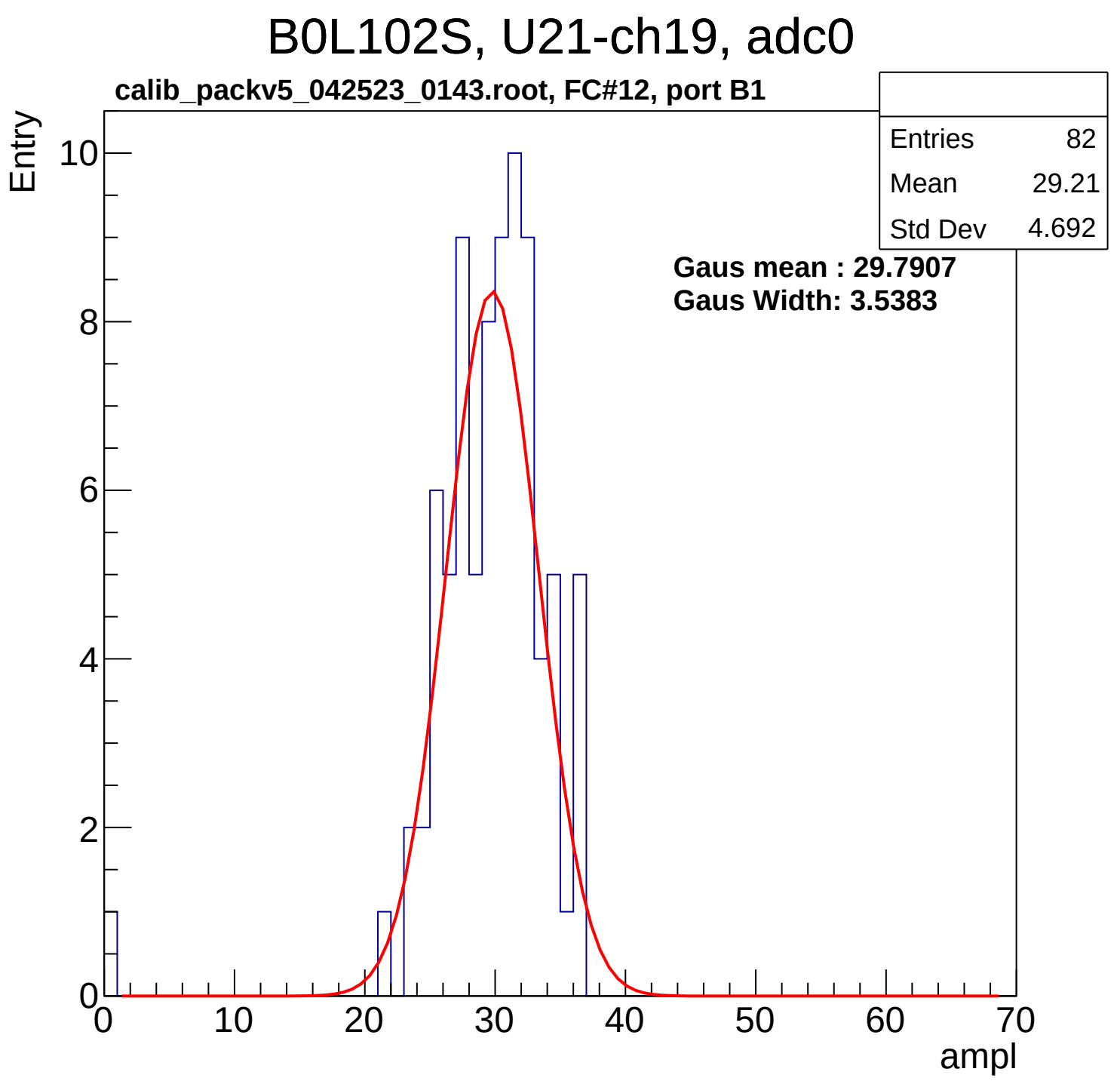
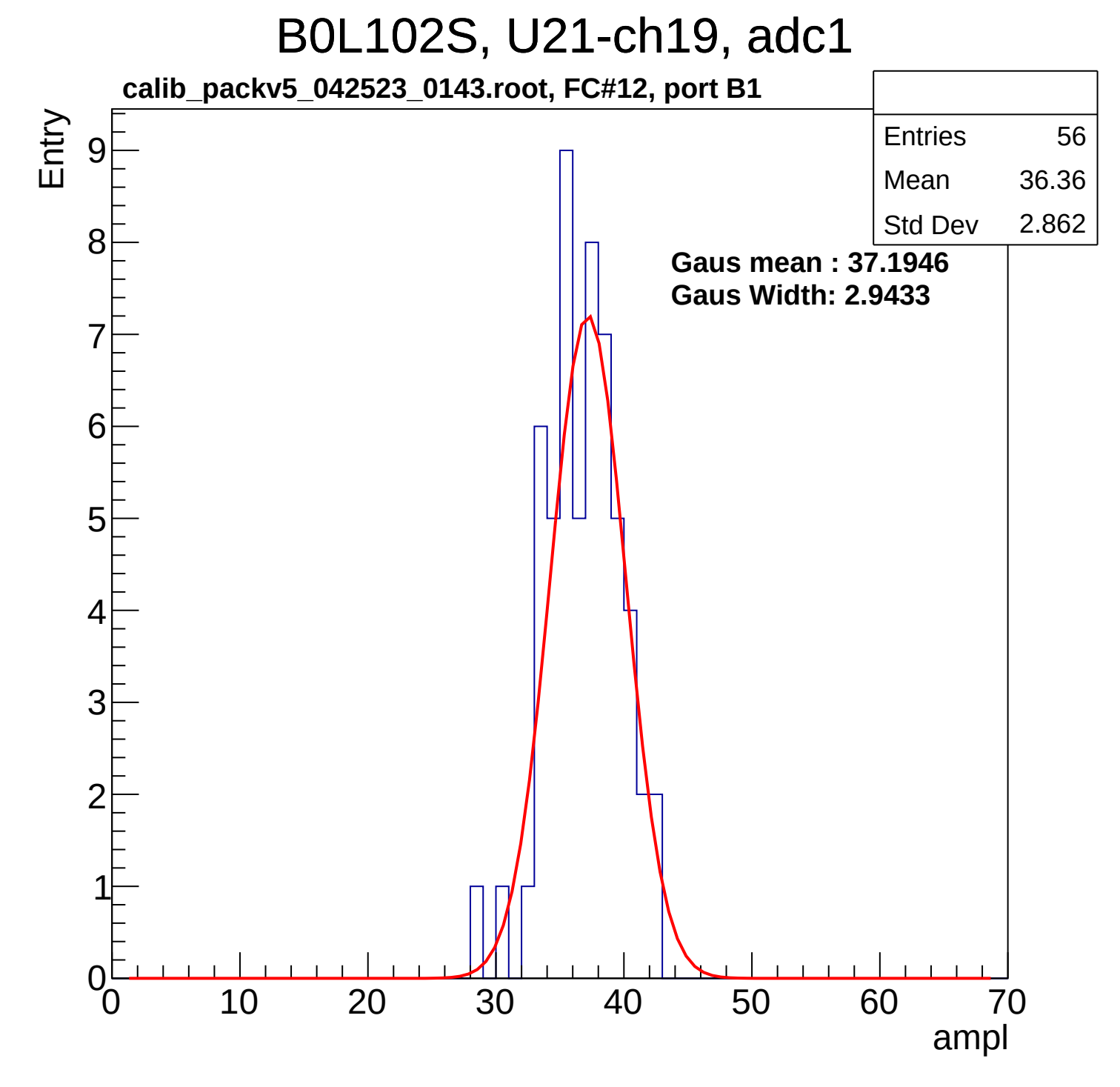
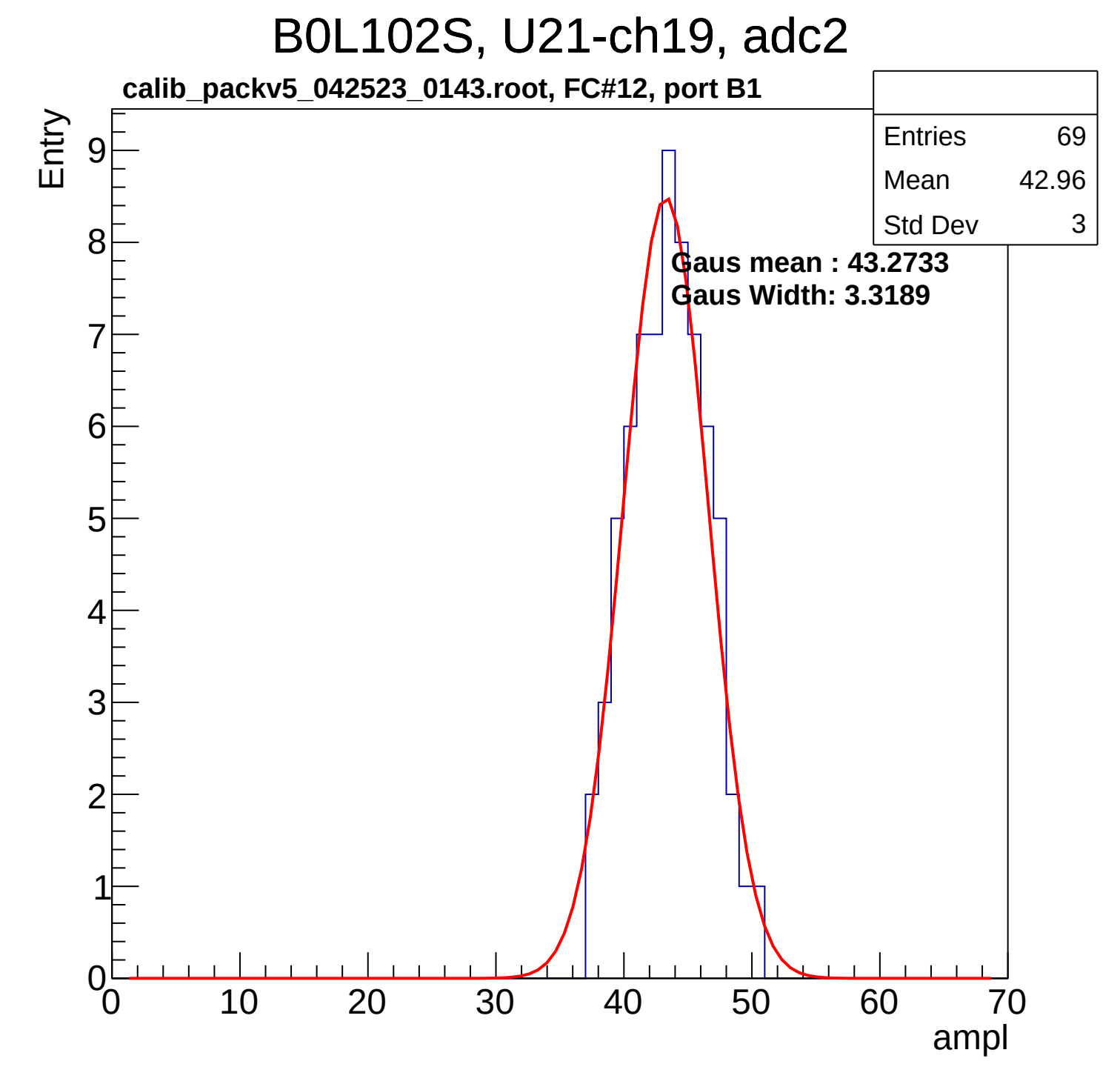
40
50
60
70


B0L102S, U21-ch18, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch18, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch19, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 29.21
Std Dev 4.692
Gaus mean : 29.7907
Gaus Width: 3.5383
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch19, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 36.36
Std Dev 2.862
Gaus mean : 37.1946
Gaus Width: 2.9433
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch19, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 42.96
Std Dev 3
Gaus mean : 43.2733
Gaus Width: 3.3189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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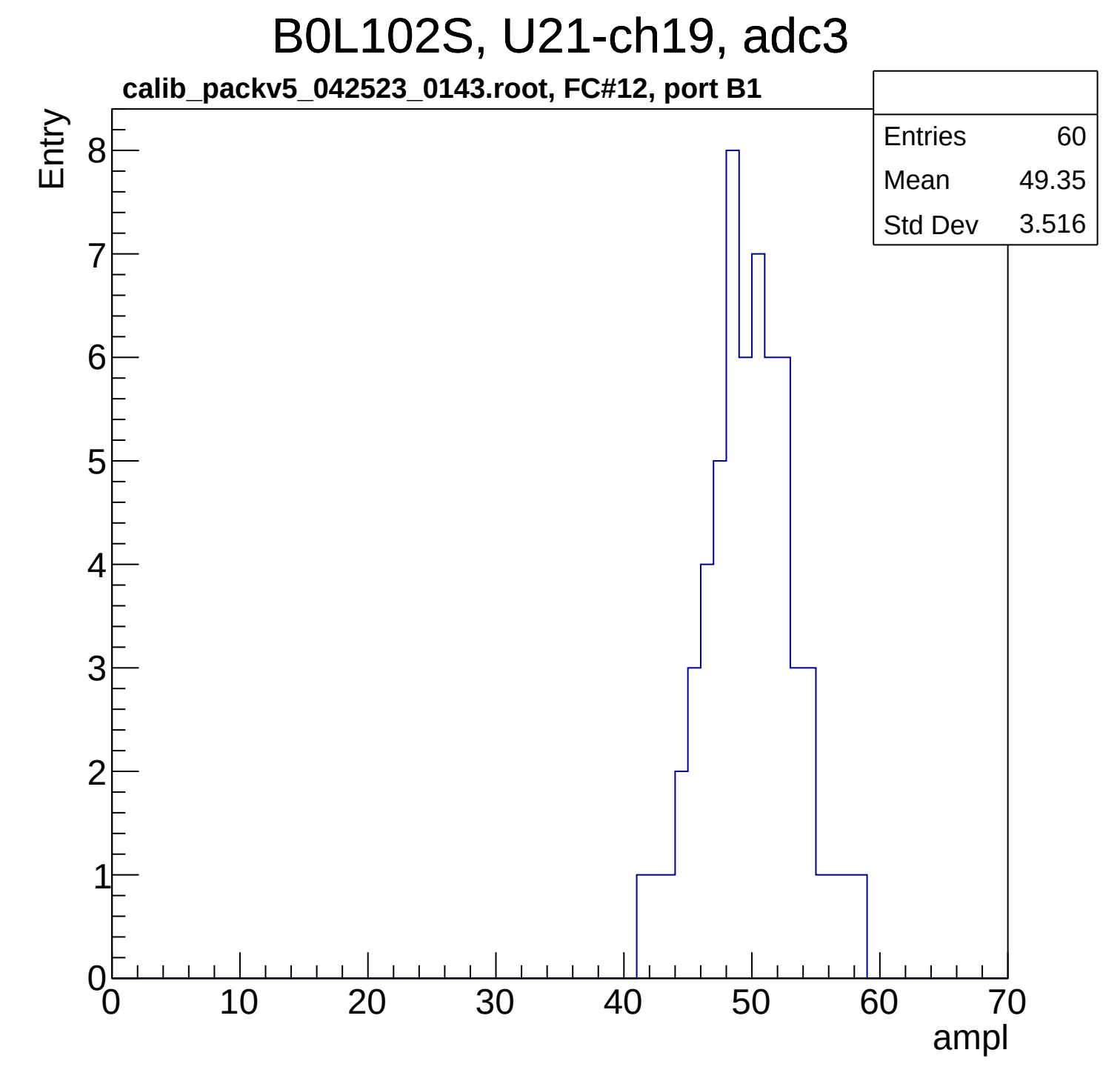
B0L102S, U21-ch19, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 49.35
Std Dev 3.516
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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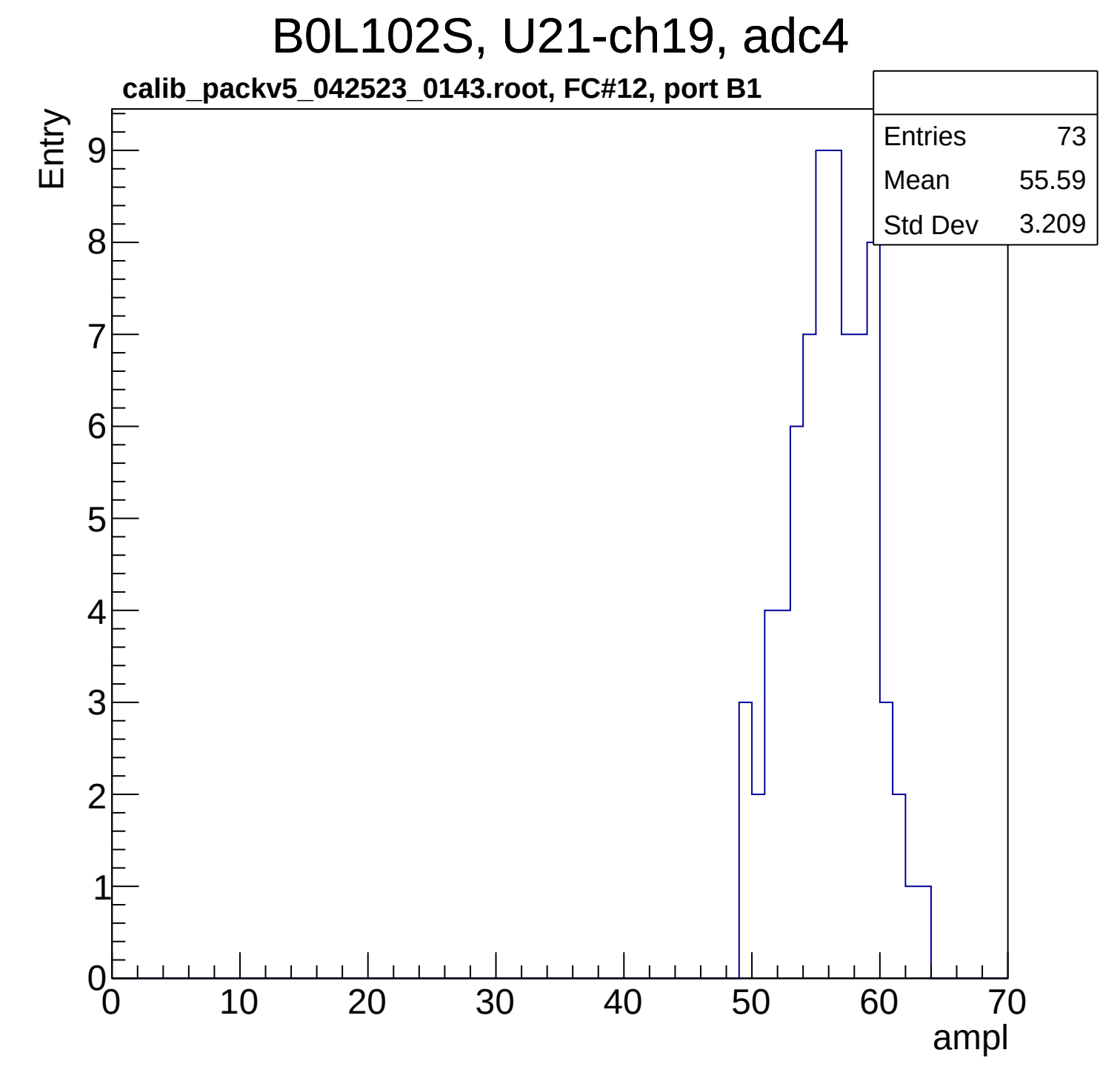
B0L102S, U21-ch19, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 55.59
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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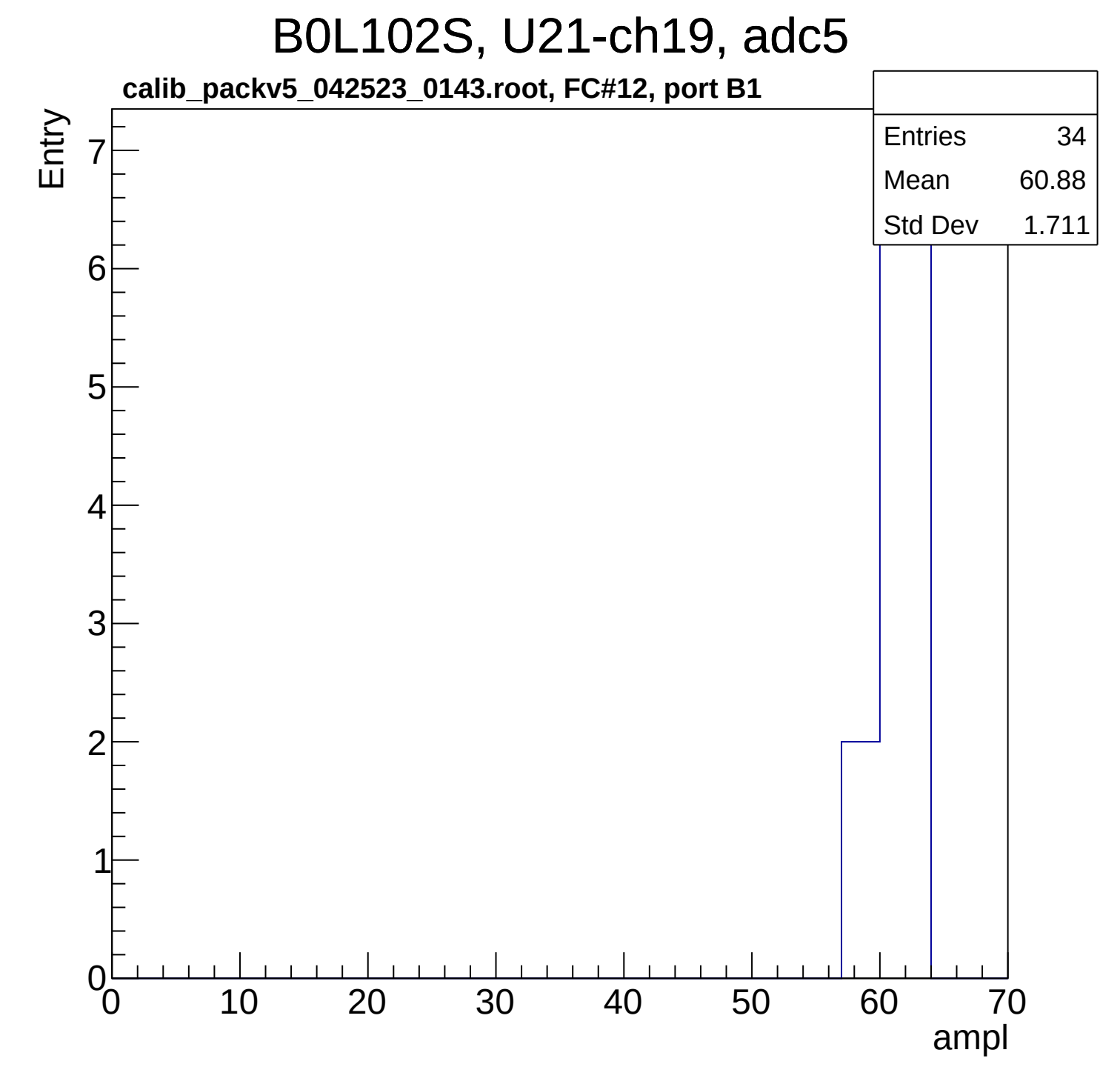
B0L102S, U21-ch19, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 60.88
Std Dev 1.711
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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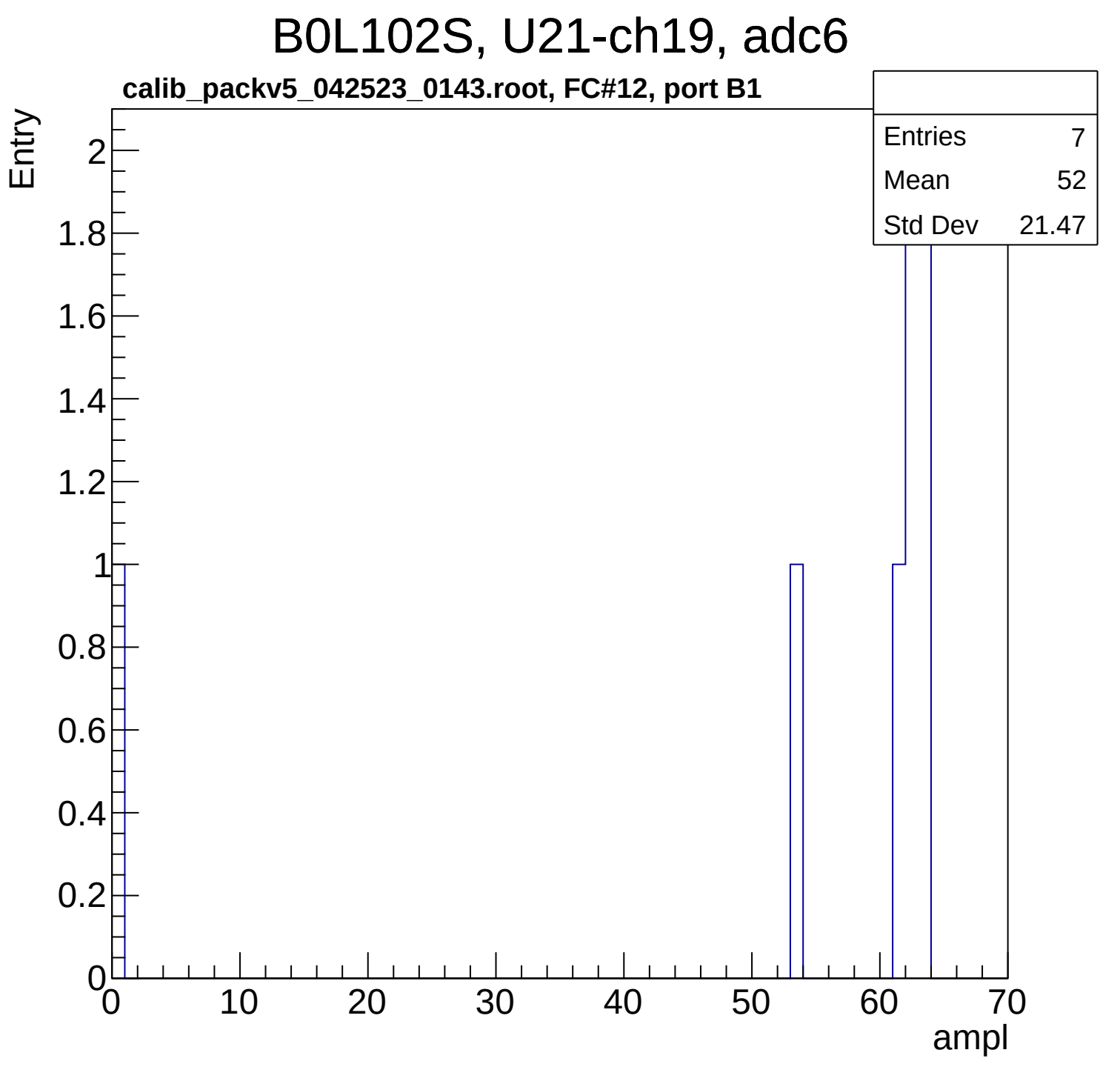
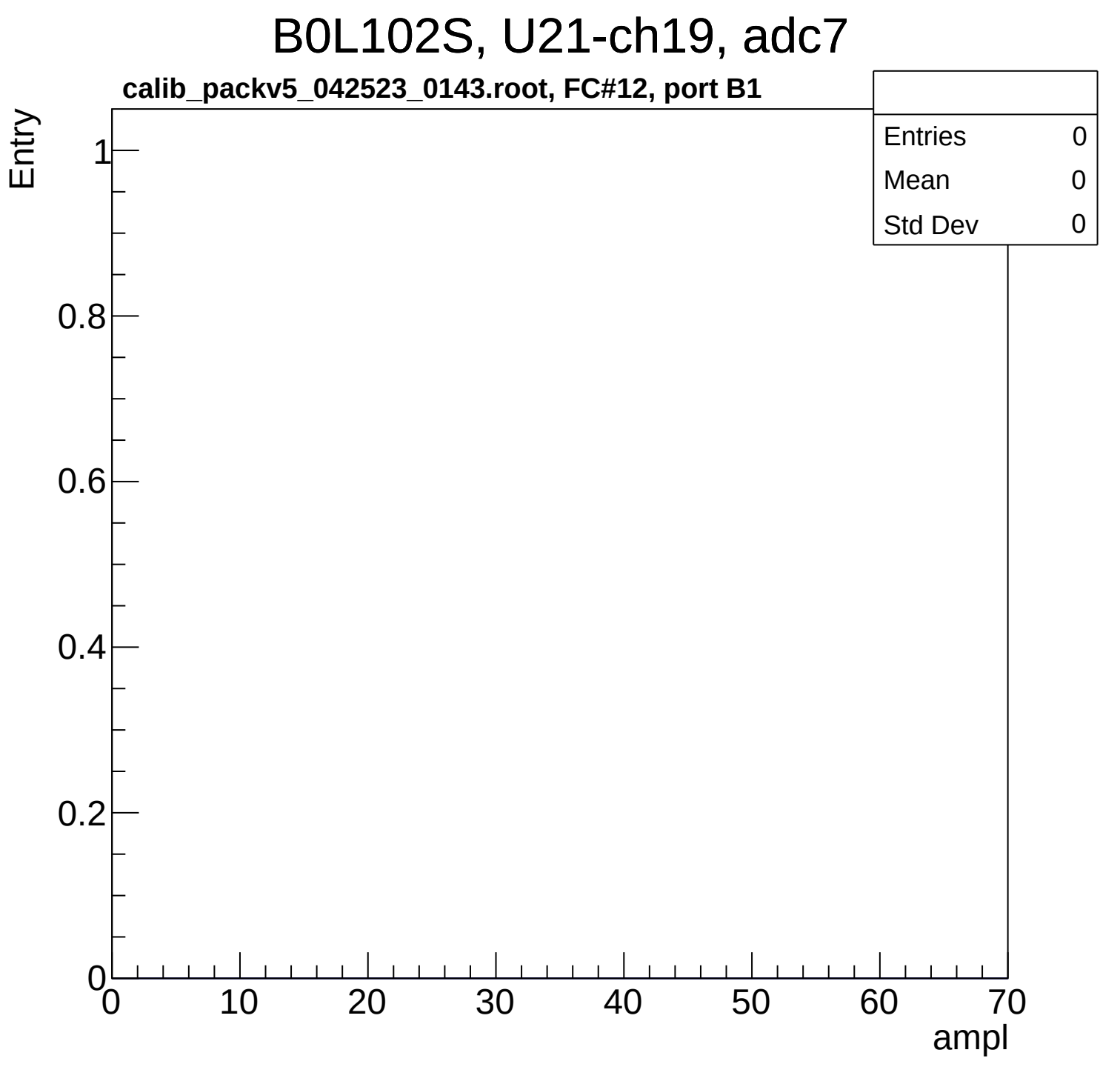
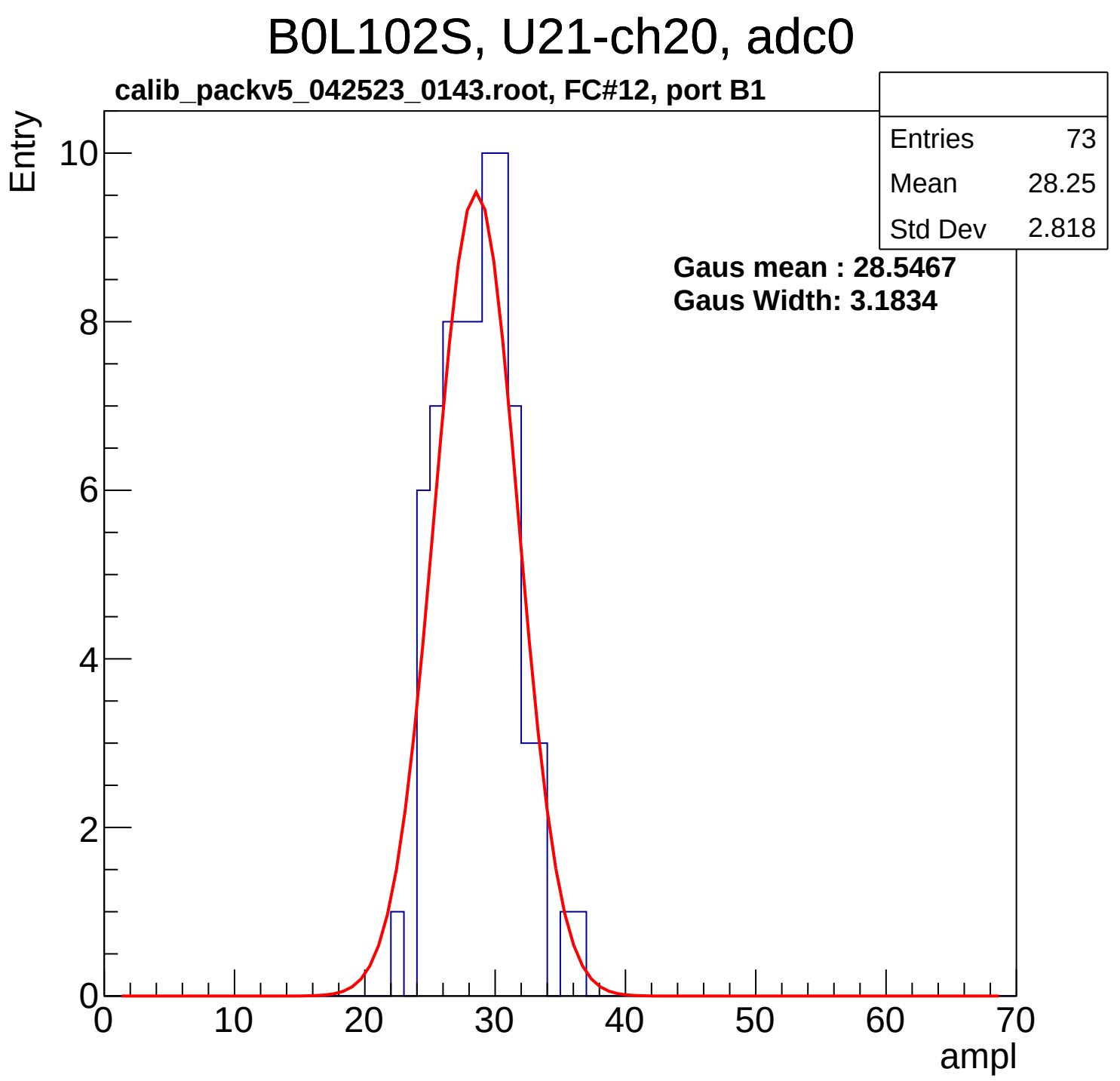
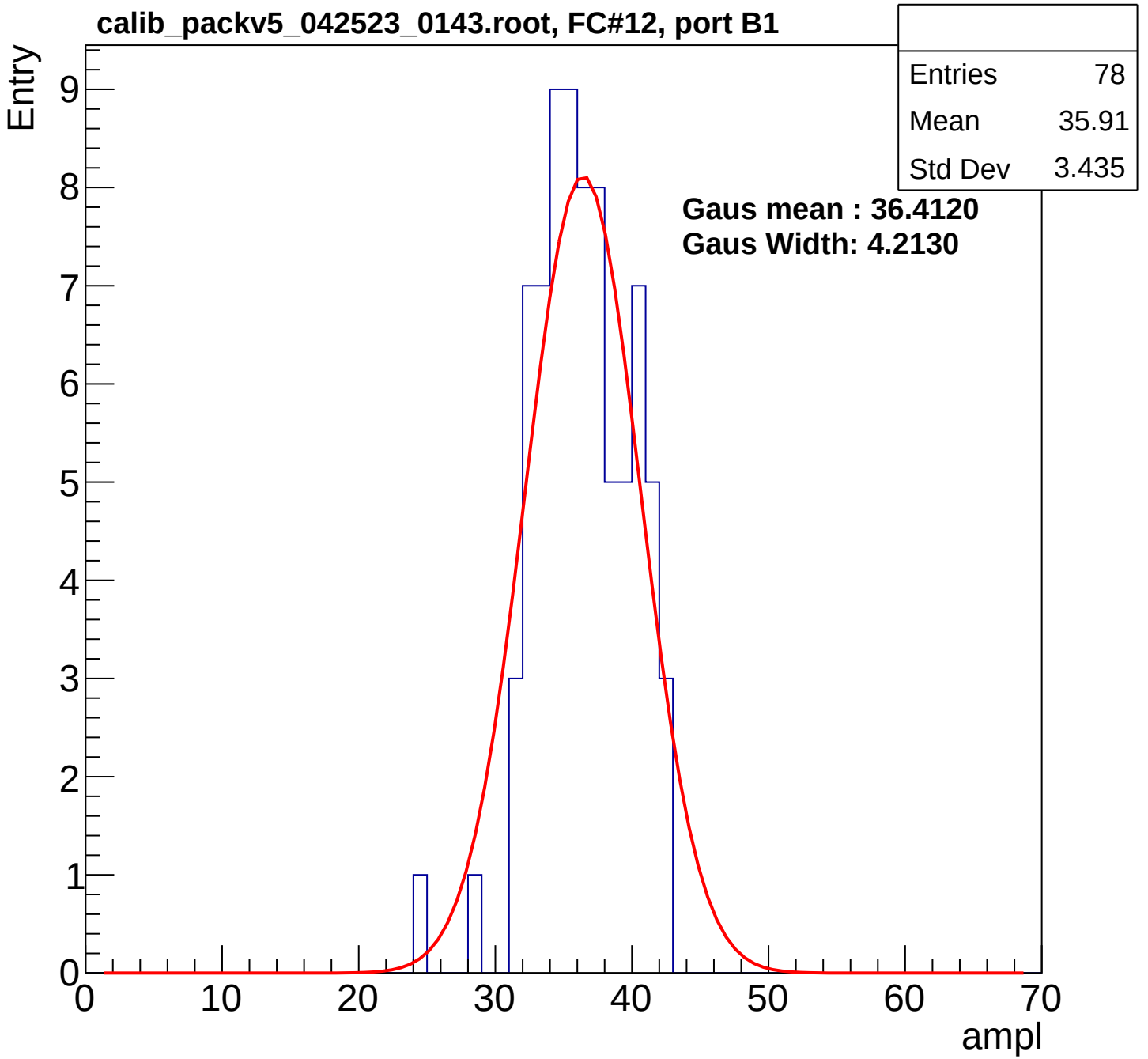
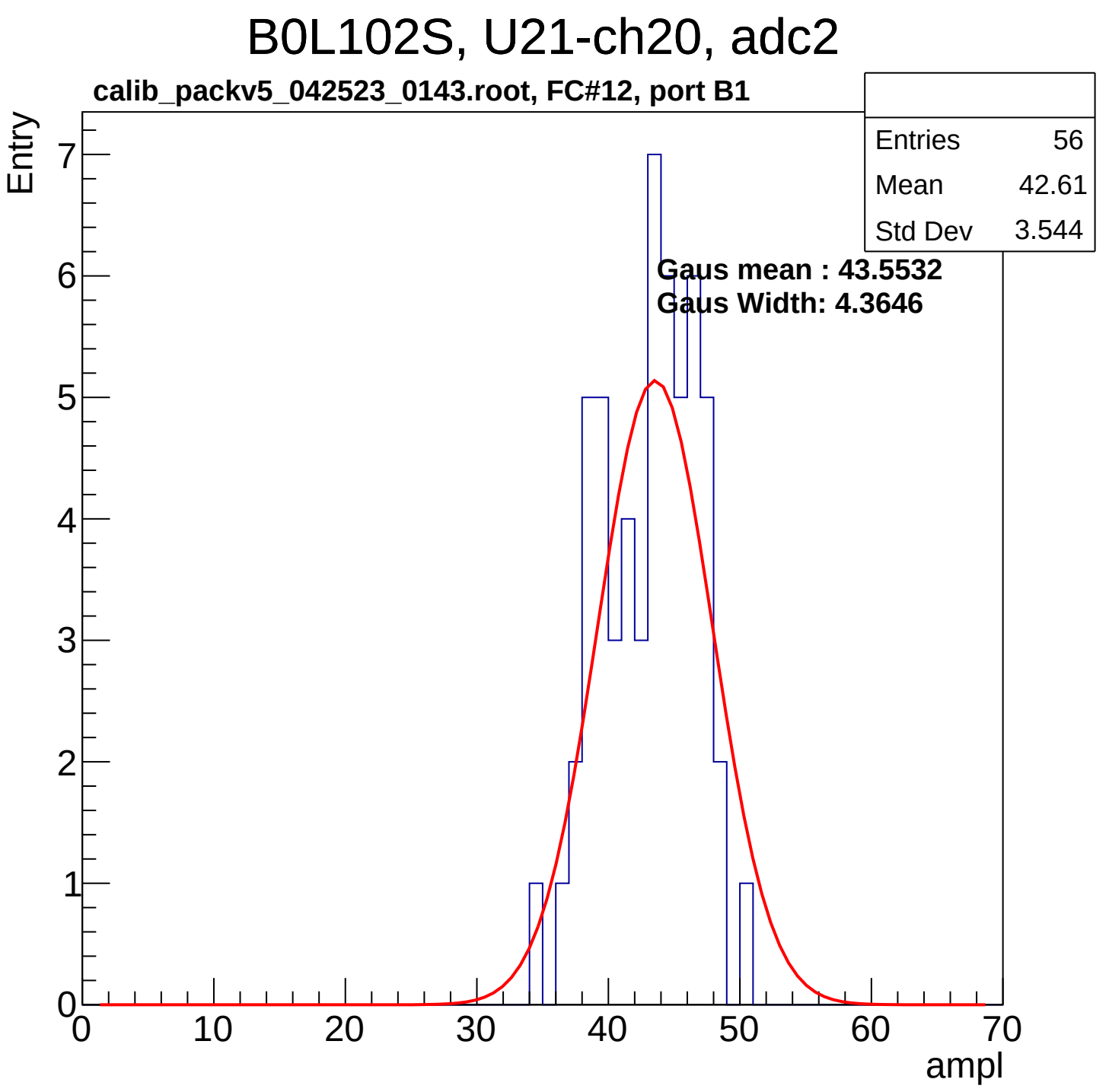
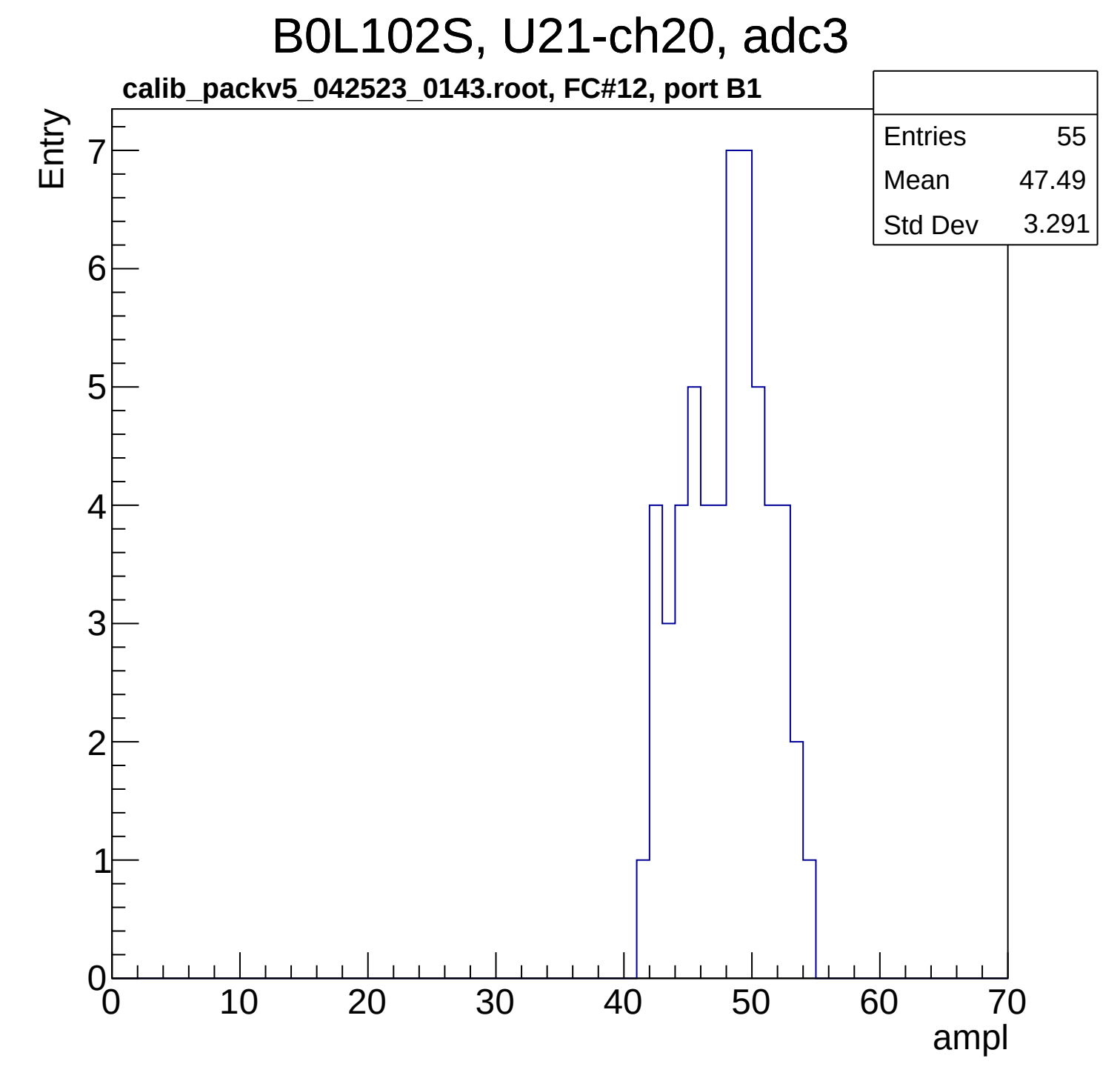

B0L102S, U21-ch19, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 52
Std Dev 21.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

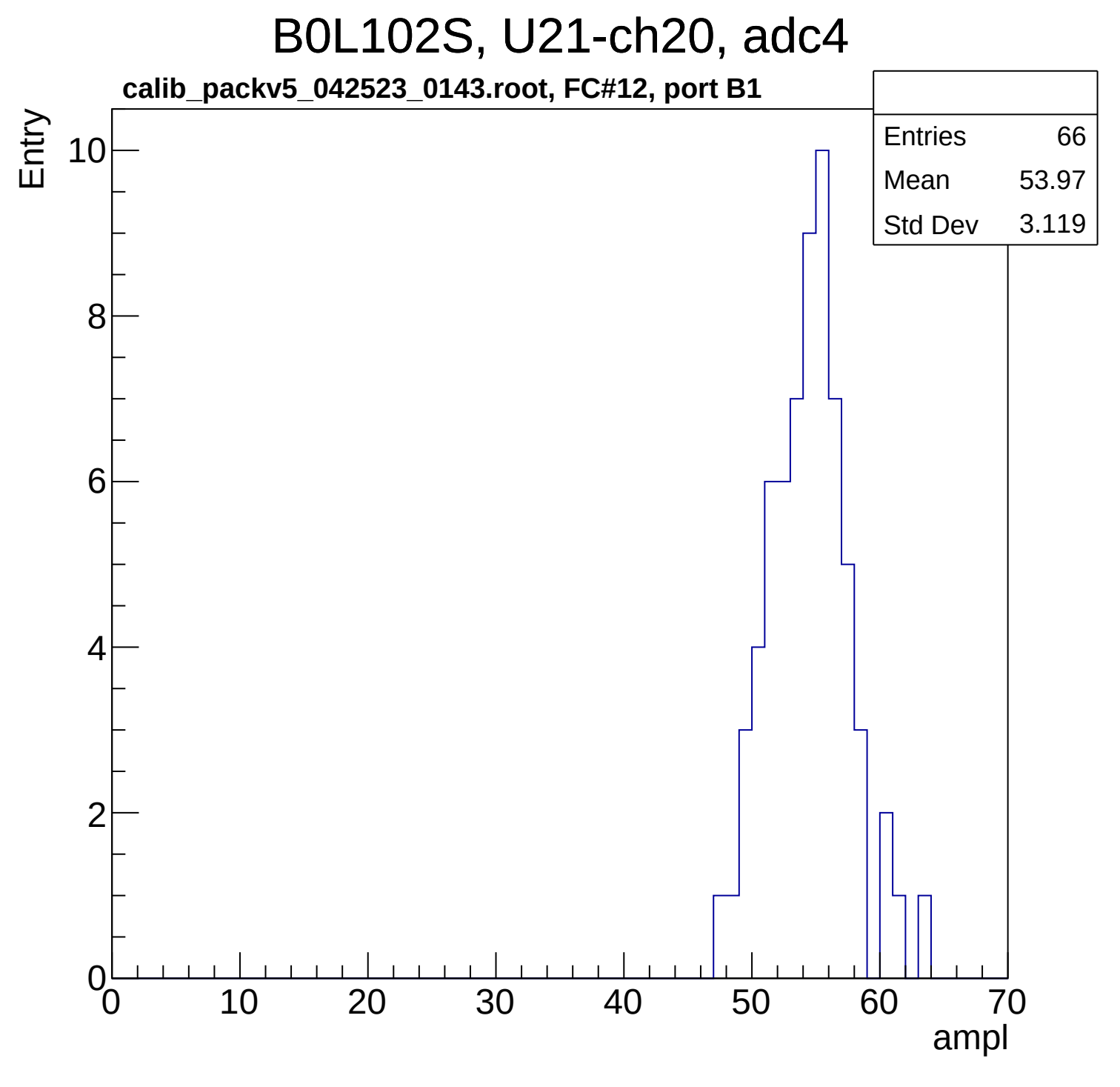

B0L102S, U21-ch19, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

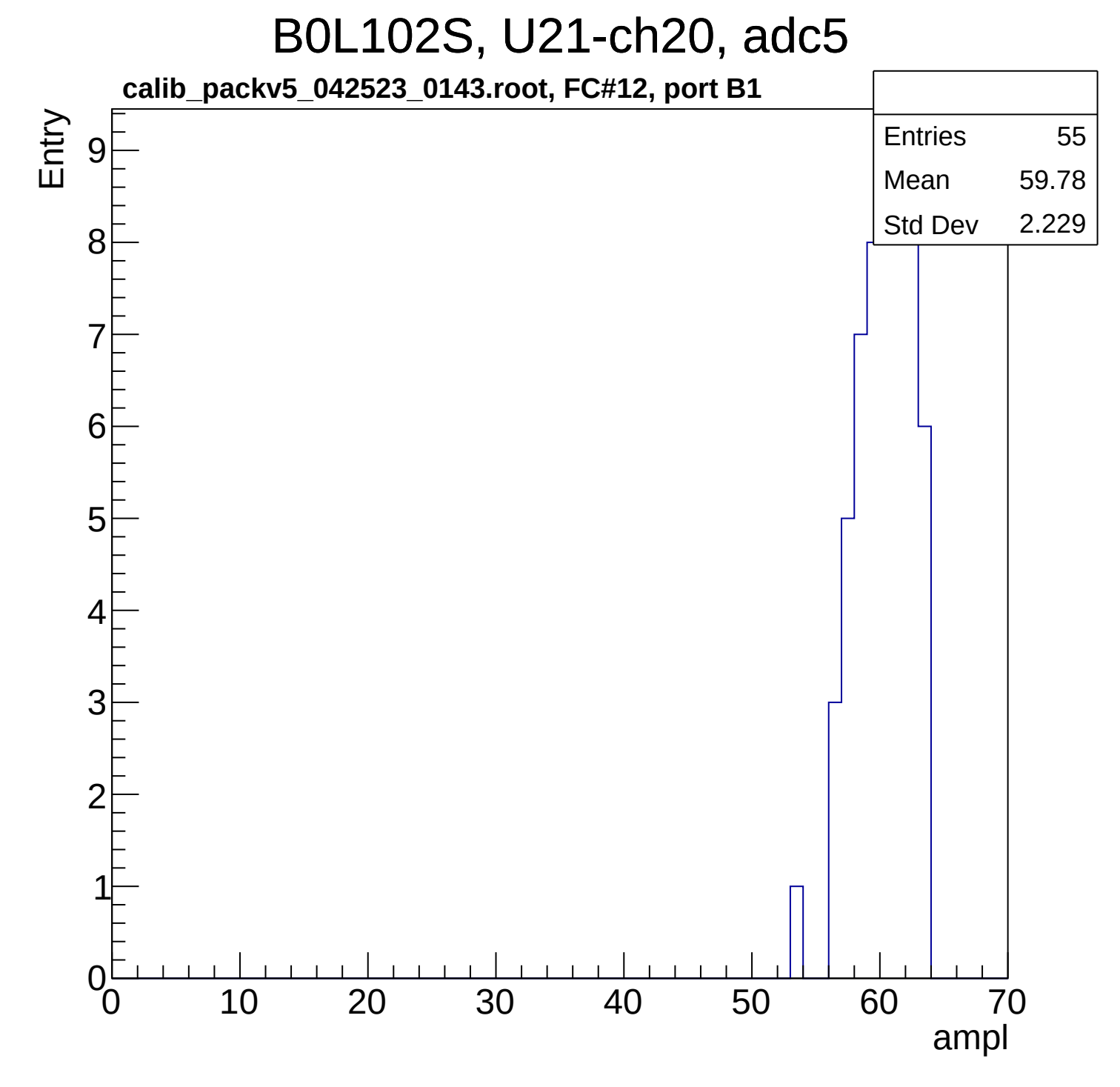

B0L102S, U21-ch20, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 28.25
Std Dev 2.818
Gaus mean : 28.5467
Gaus Width: 3.1834
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch20, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 35.91
Std Dev 3.435
Gaus mean : 36.4120
Gaus Width: 4.2130
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch20, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 42.61
Std Dev 3.544
Gaus mean : 43.5532
Gaus Width: 4.3646
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch20, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 47.49
Std Dev 3.291
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch20, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 53.97
Std Dev 3.119
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch20, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 59.78
Std Dev 2.229
Entry
ampl
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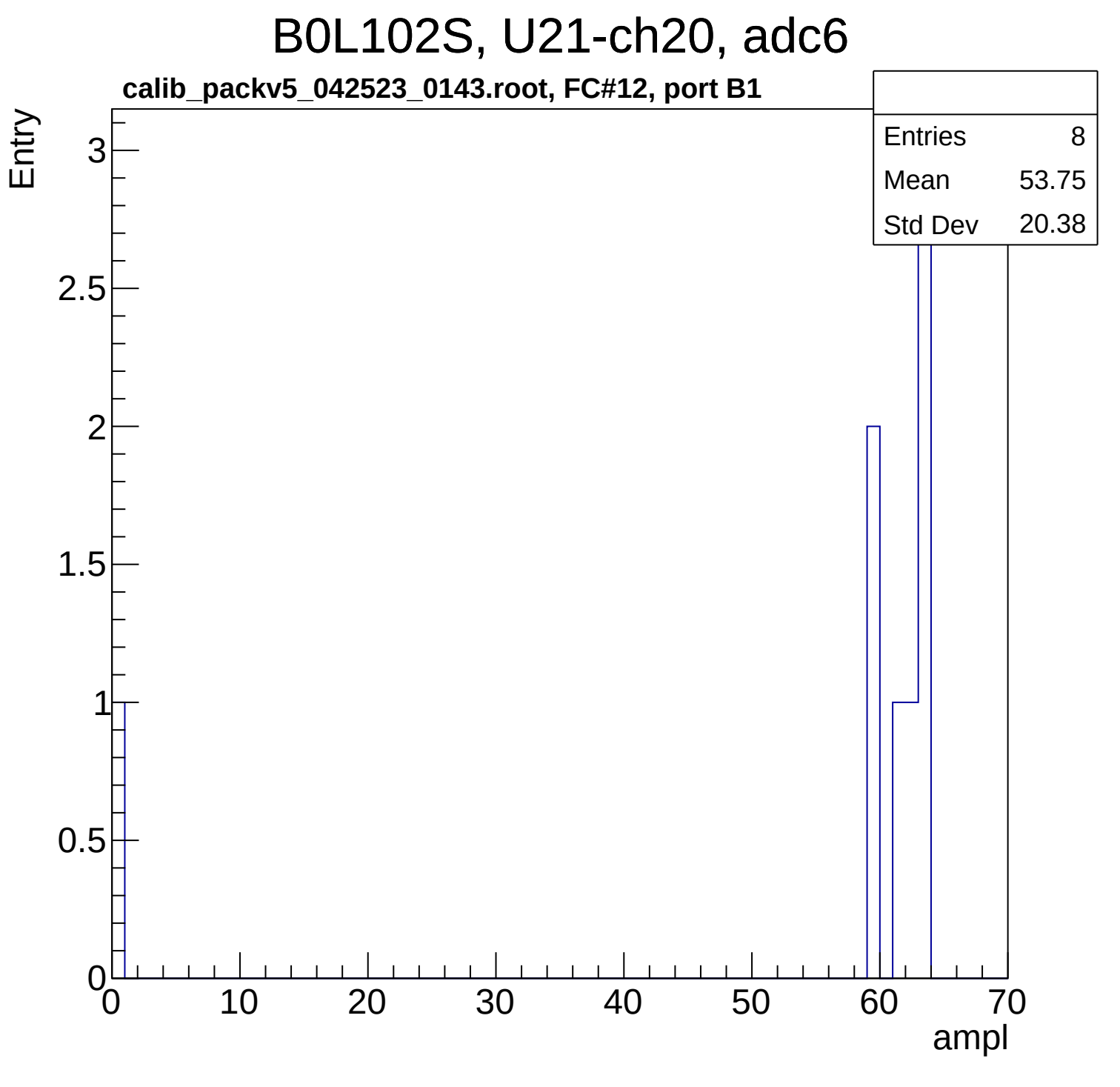
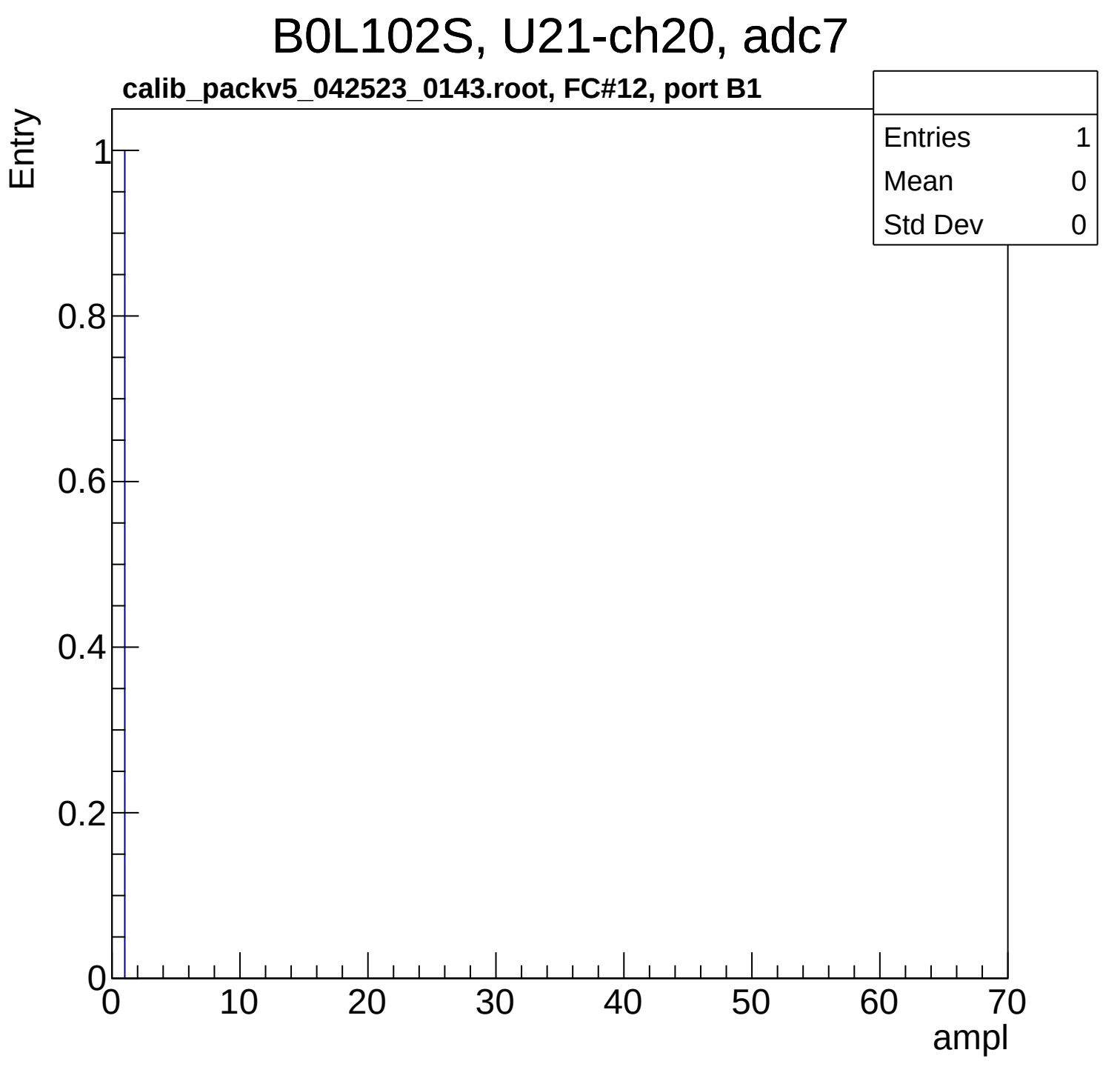
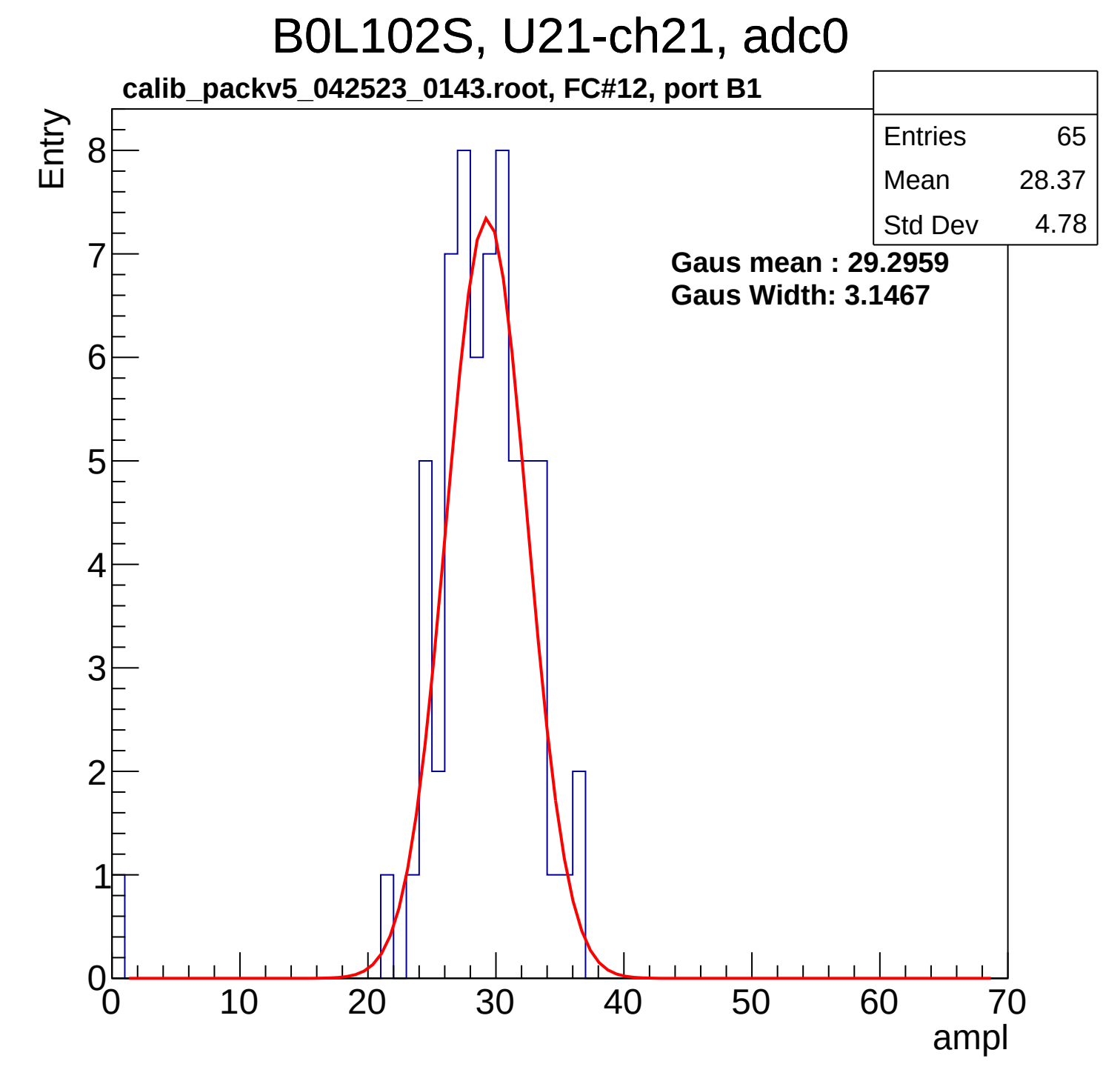
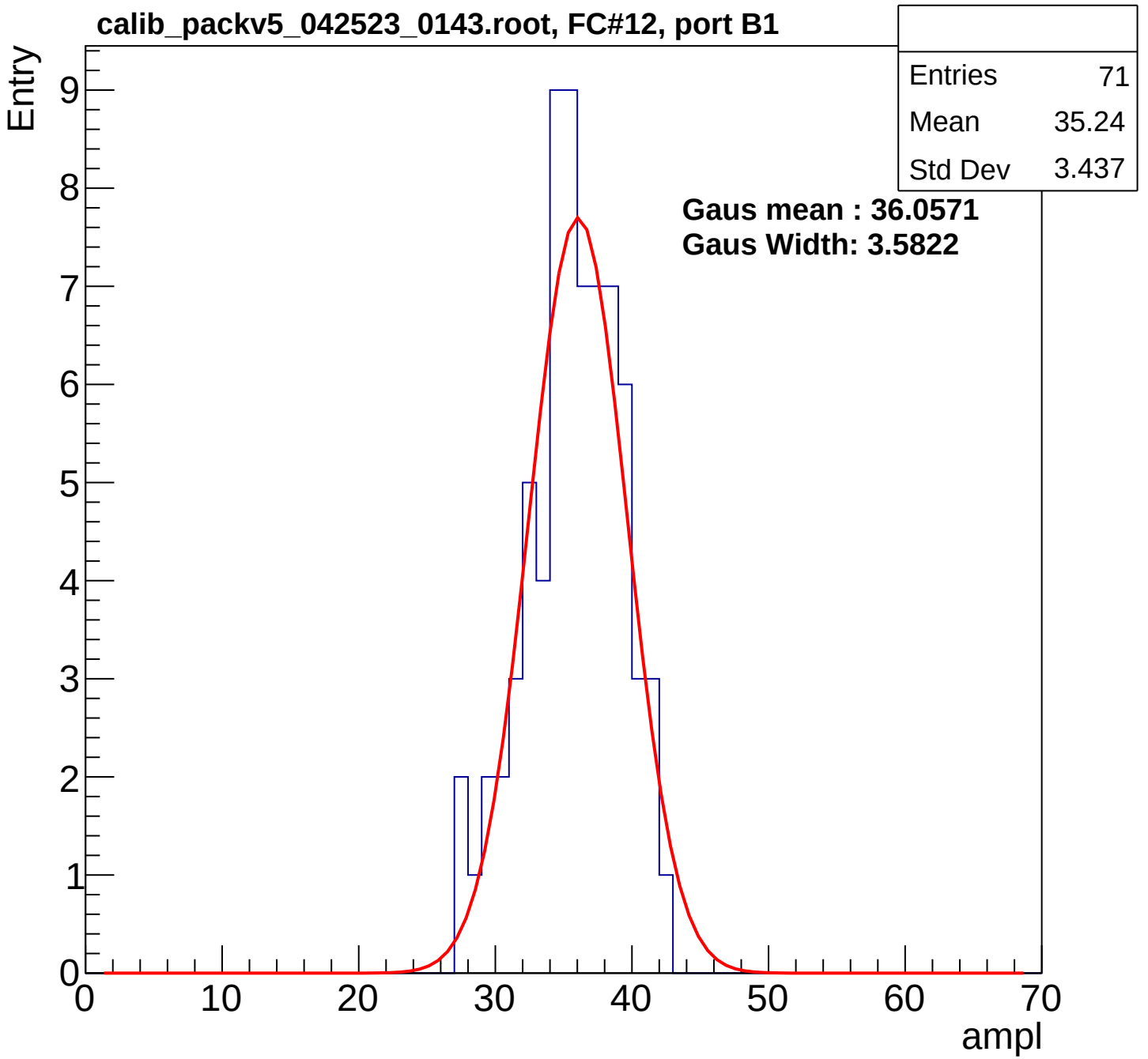
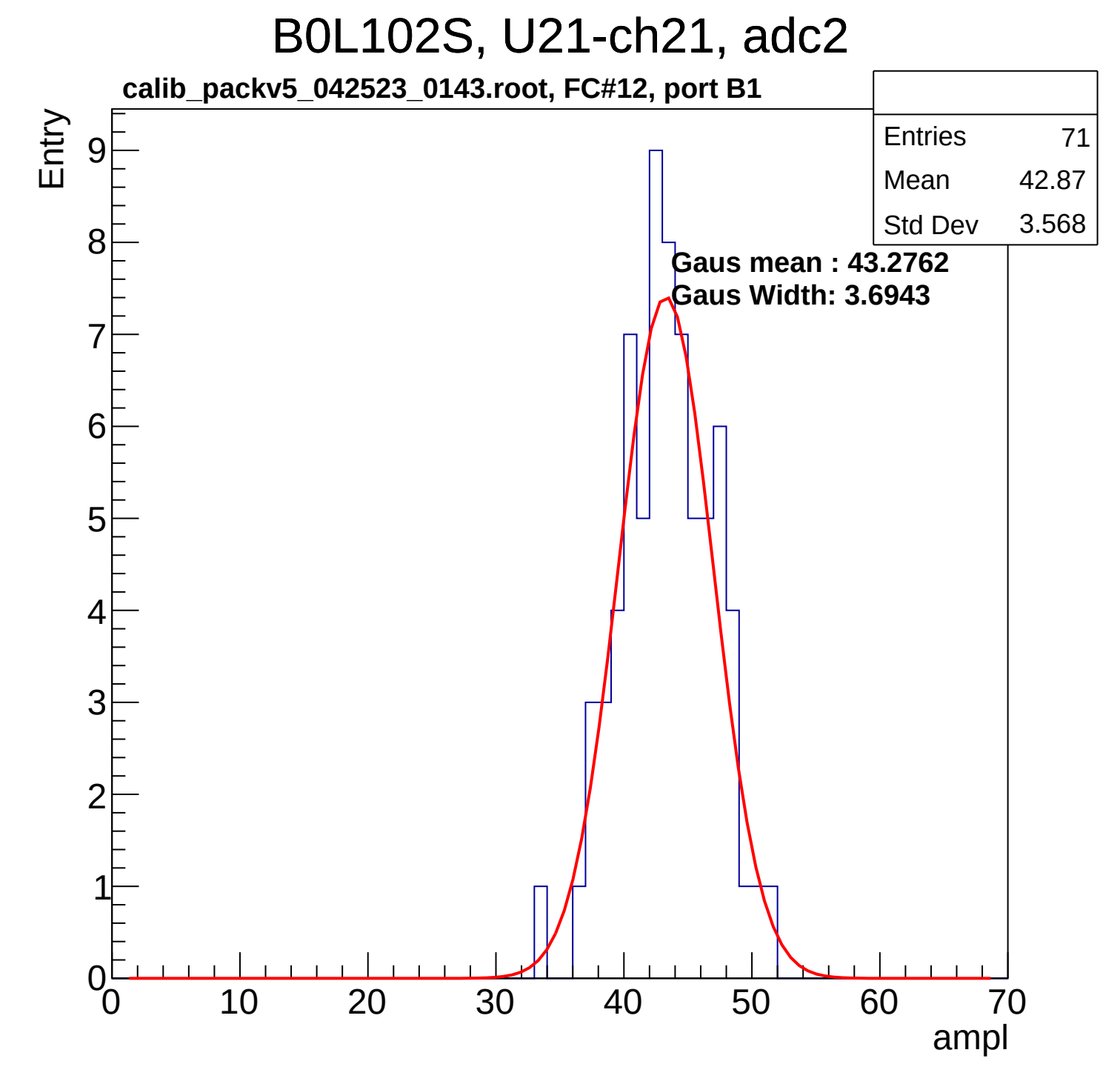
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch20, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 53.75
Std Dev 20.38
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch20, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch21, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 28.37
Std Dev 4.78
Gaus mean : 29.2959
Gaus Width: 3.1467
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch21, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 35.24
Std Dev 3.437
Gaus mean : 36.0571
Gaus Width: 3.5822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch21, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 42.87
Std Dev 3.568
Gaus mean : 43.2762
Gaus Width: 3.6943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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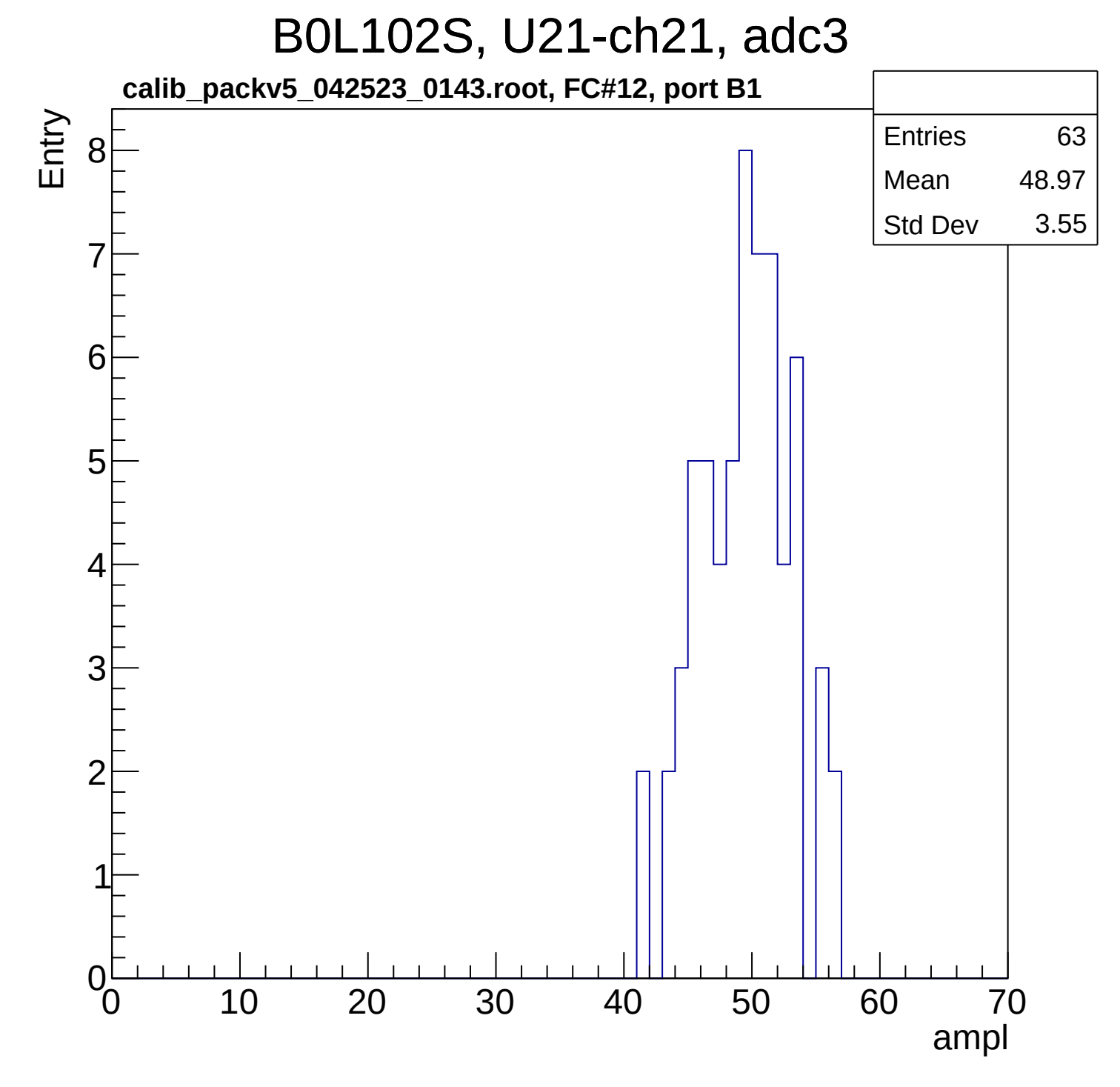
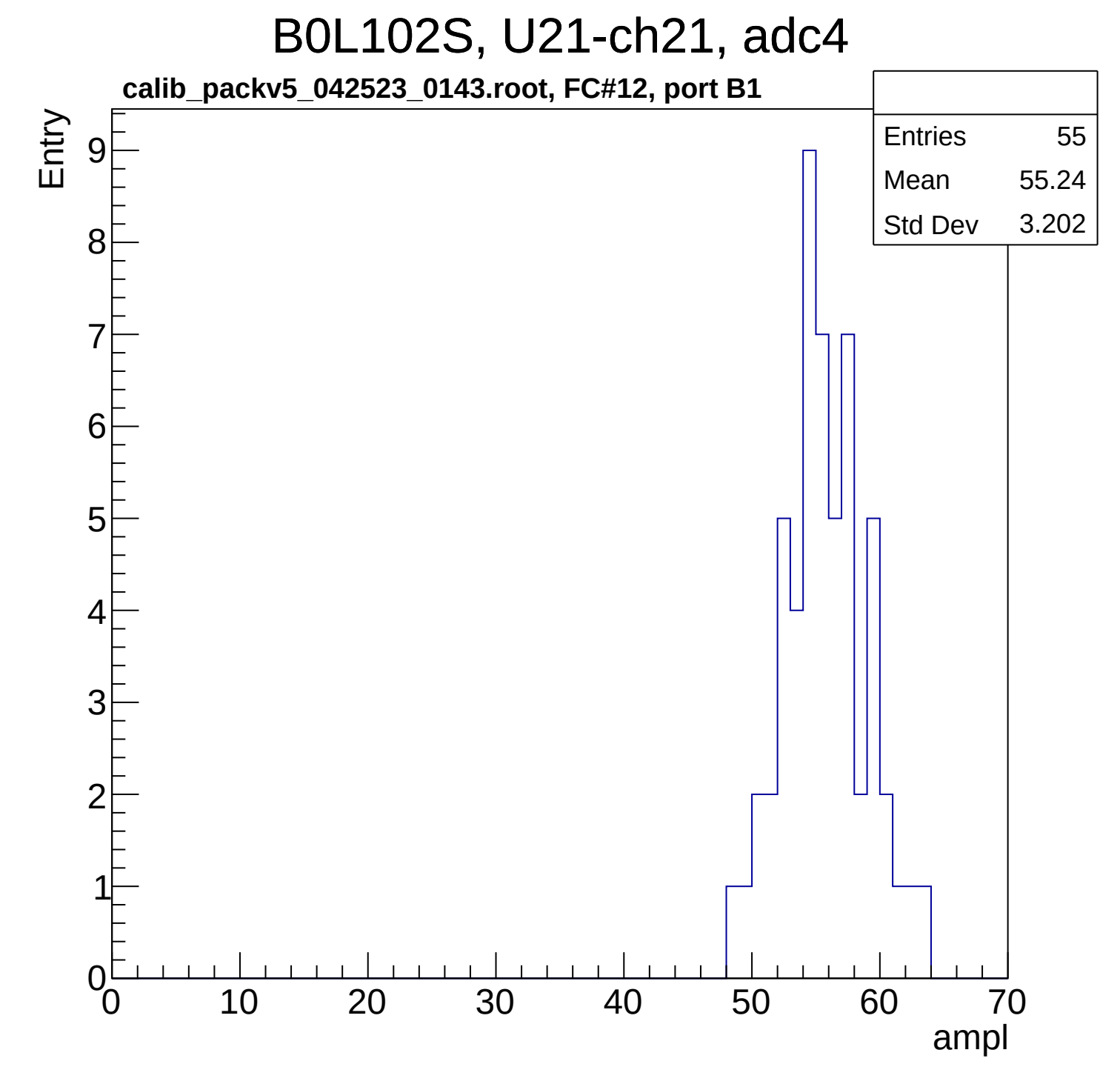

B0L102S, U21-ch21, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 48.97
Std Dev 3.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch21, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.24
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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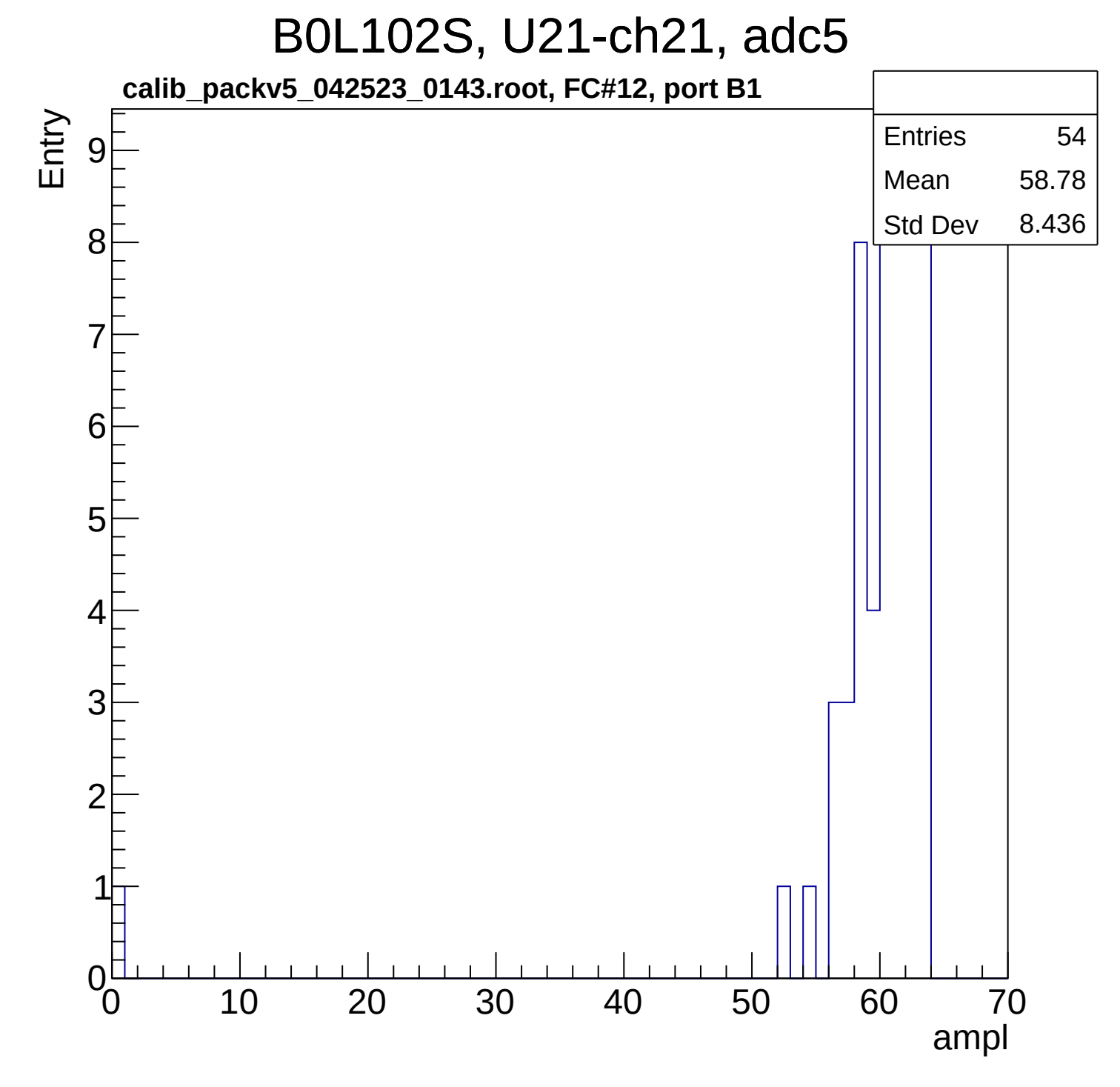
B0L102S, U21-ch21, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 58.78
Std Dev 8.436
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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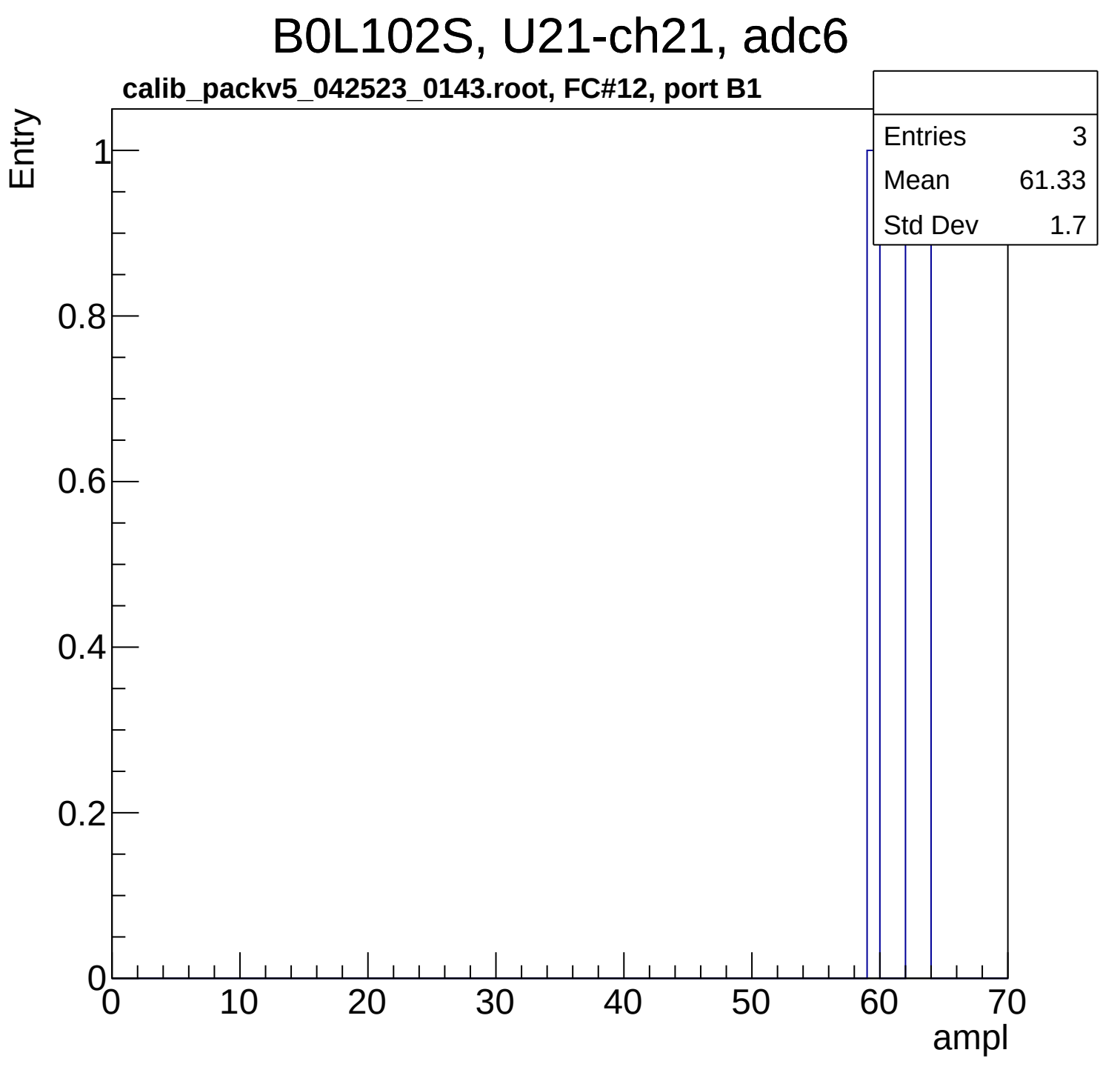
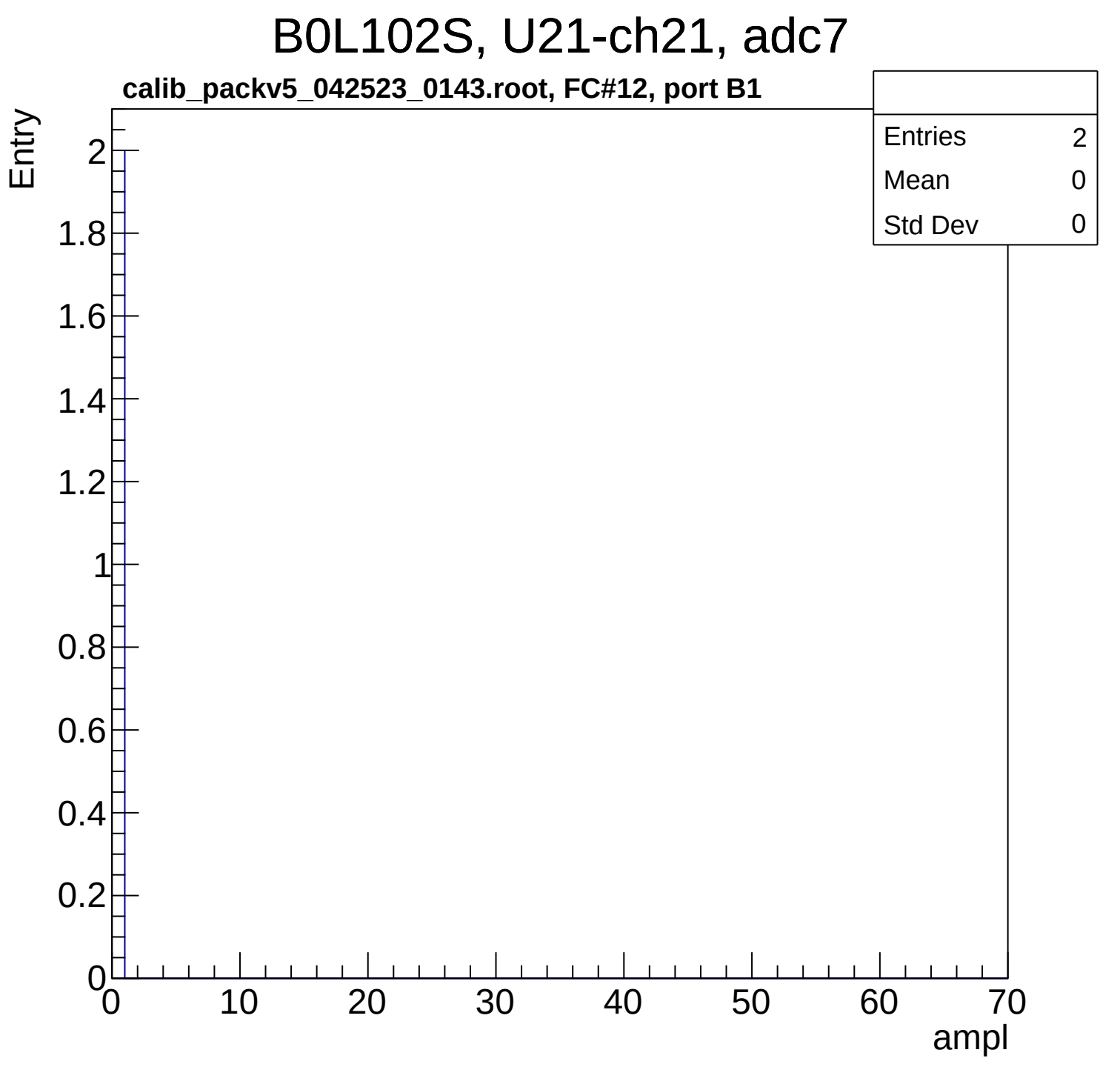

B0L102S, U21-ch21, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.33
Std Dev 1.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U21-ch21, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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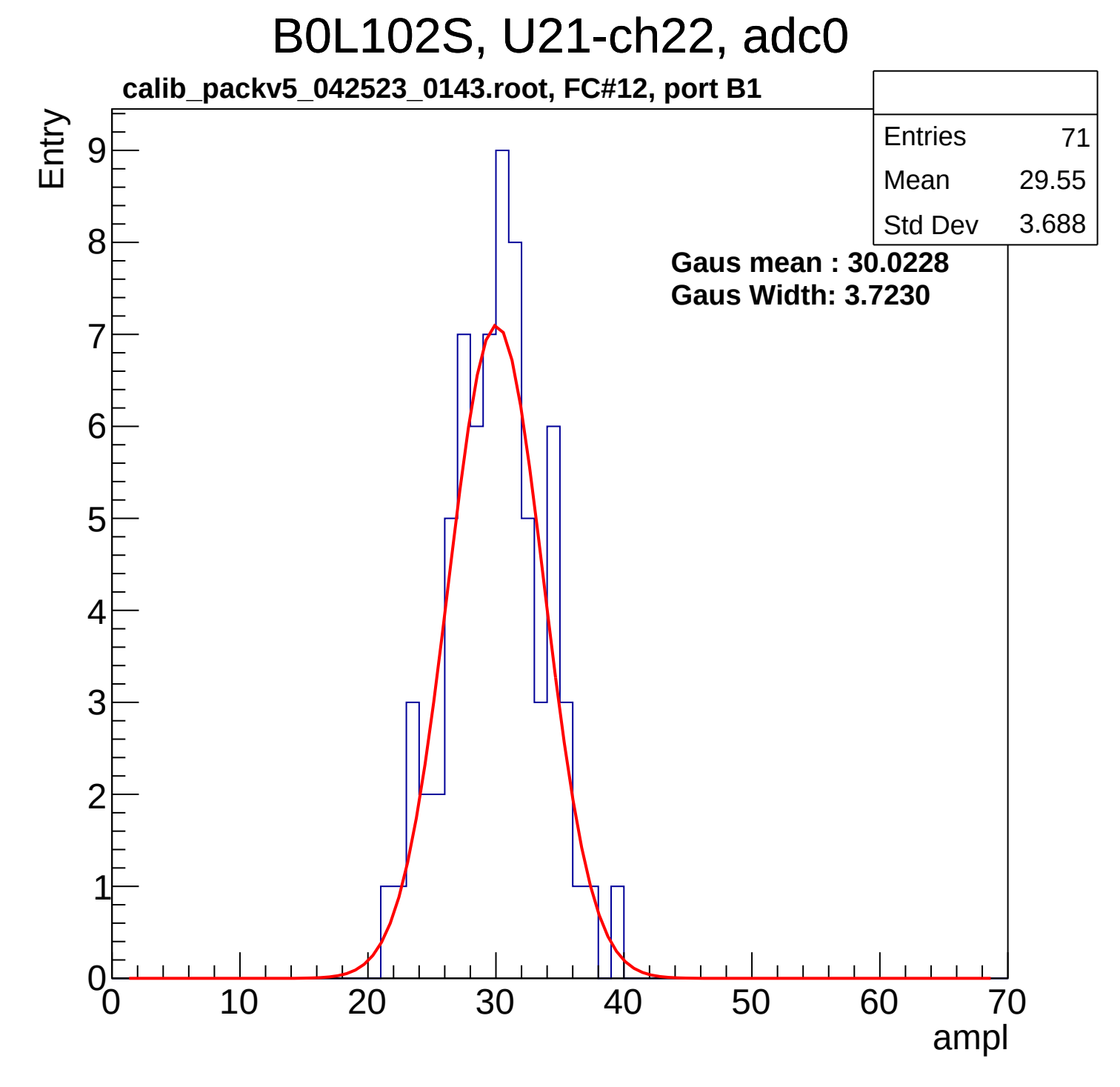
B0L102S, U21-ch22, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 29.55
Std Dev 3.688
Gaus mean : 30.0228
Gaus Width: 3.7230
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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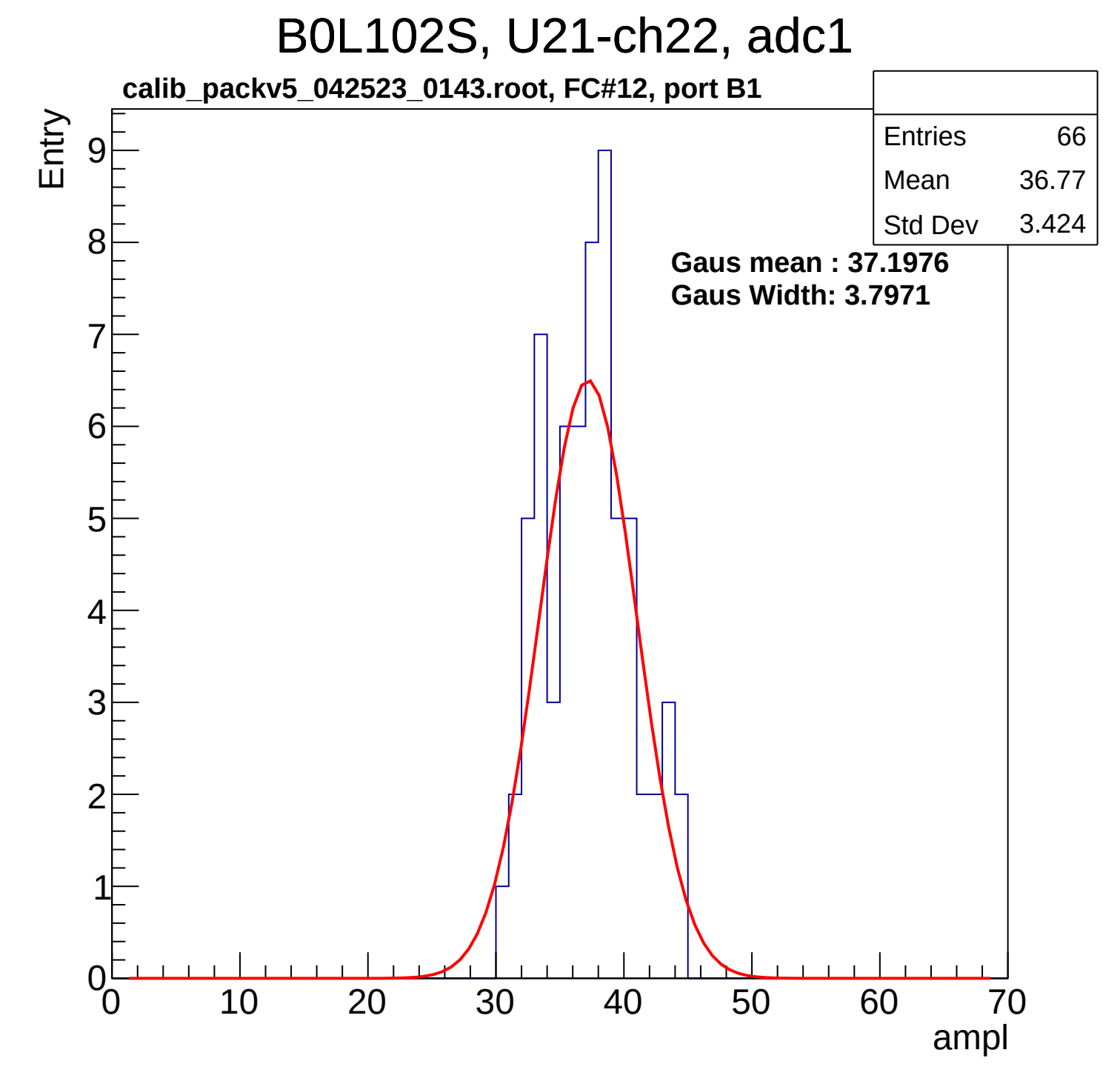
20
30
40
50
60
70


B0L102S, U21-ch22, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.77
Std Dev 3.424
Gaus mean : 37.1976
Gaus Width: 3.7971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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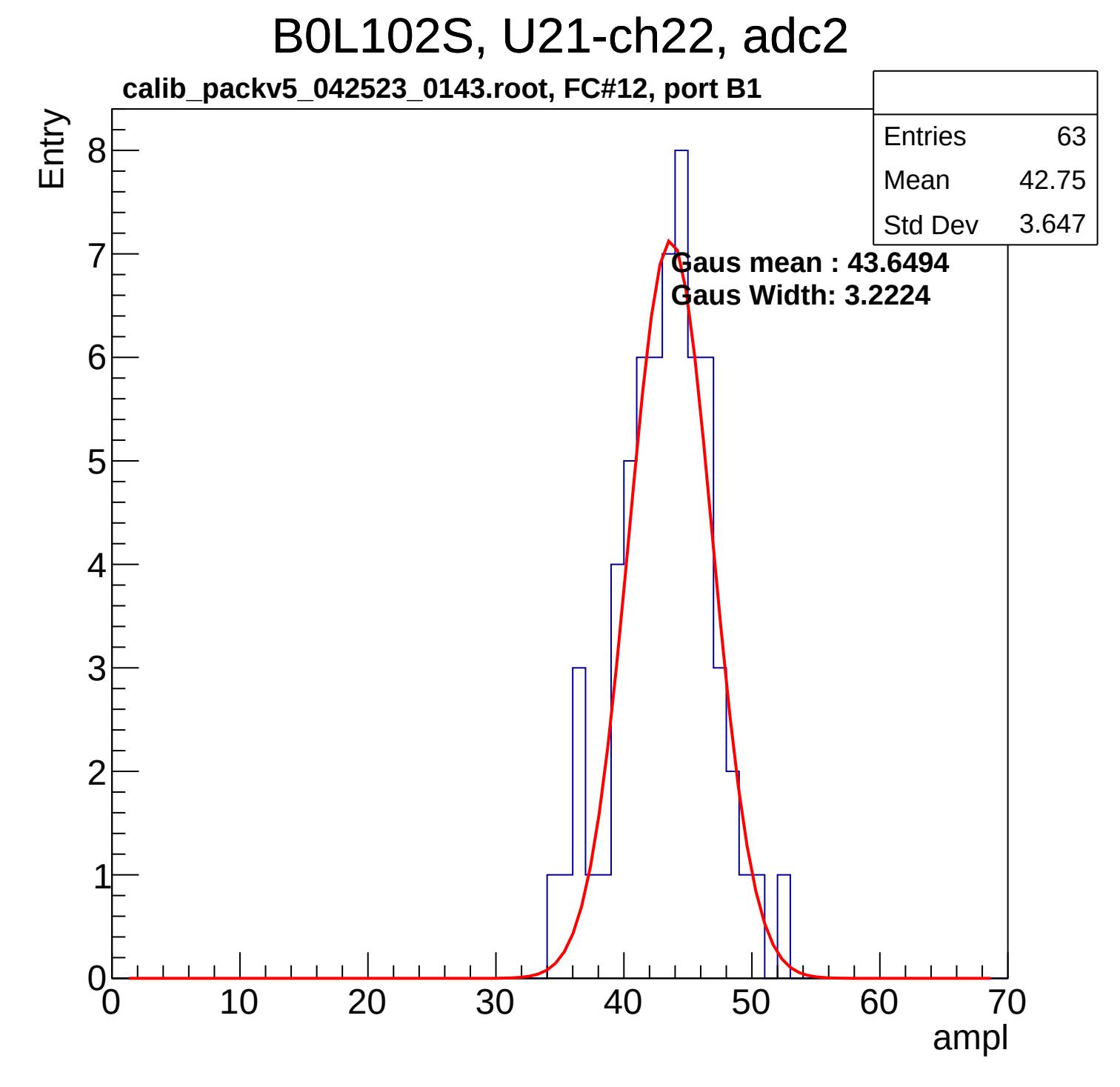
B0L102S, U21-ch22, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 42.75
Std Dev 3.647
Gaus mean : 43.6494
Gaus Width: 3.2224
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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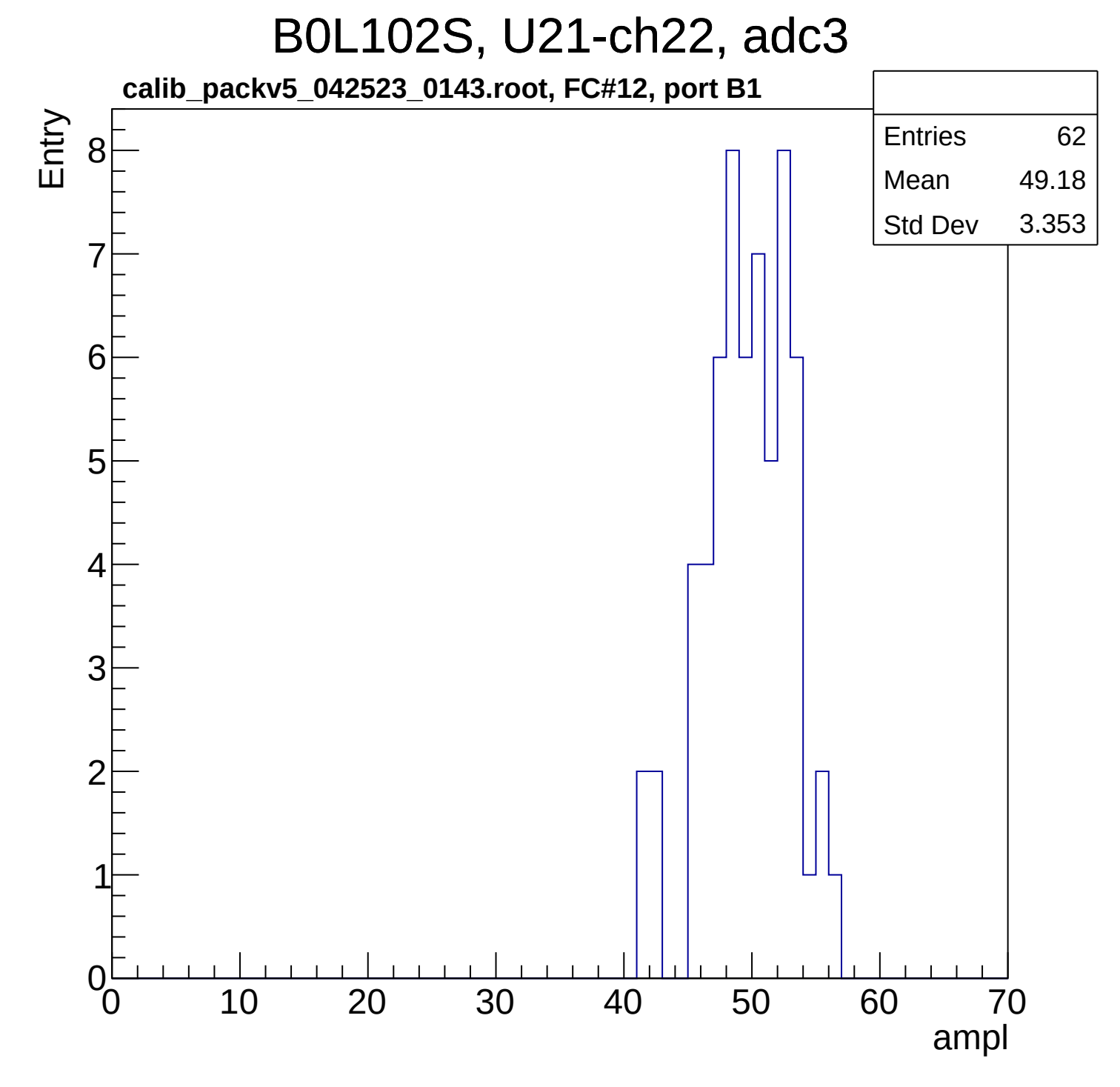
B0L102S, U21-ch22, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 49.18
Std Dev 3.353
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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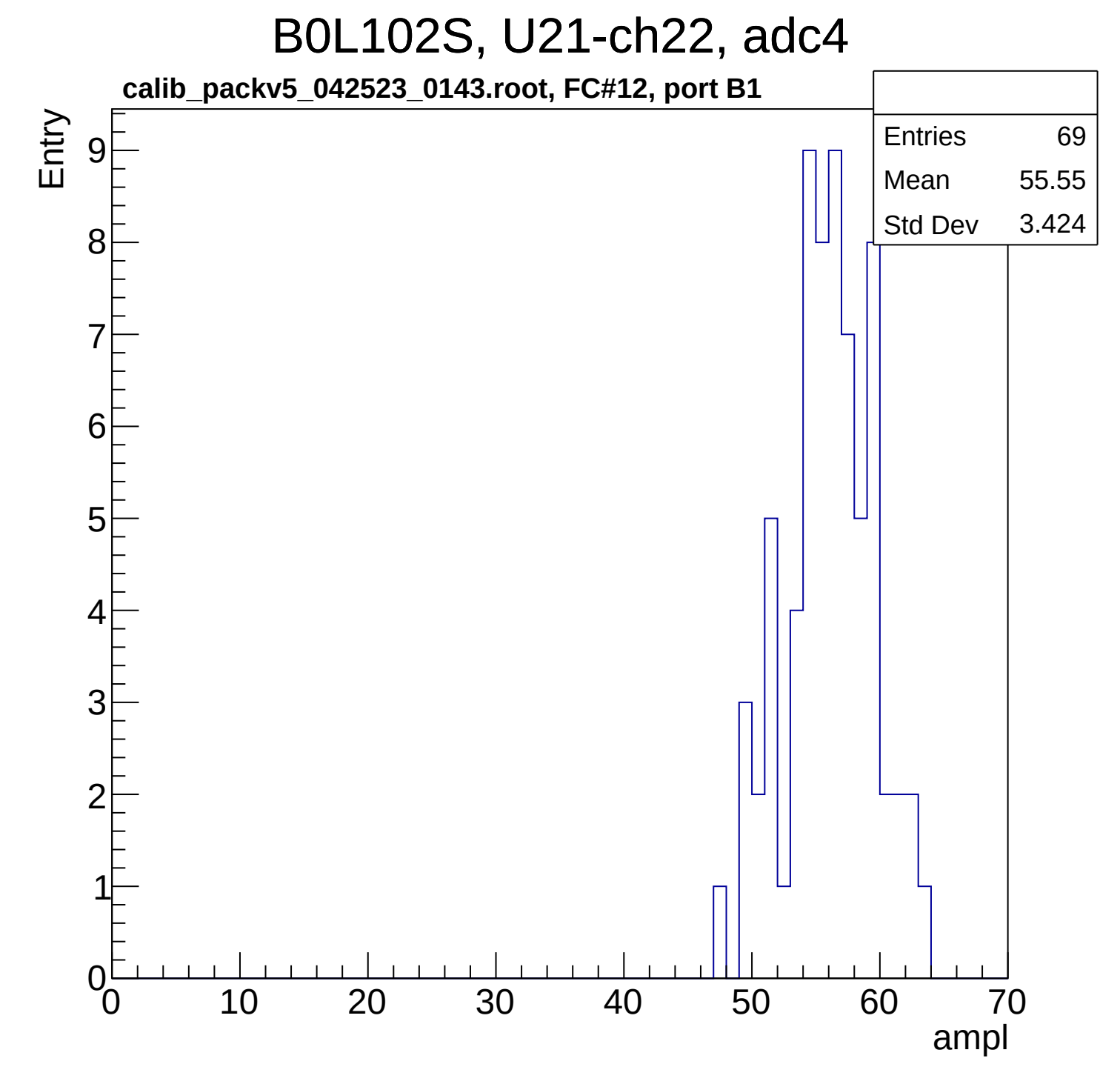
B0L102S, U21-ch22, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 55.55
Std Dev 3.424
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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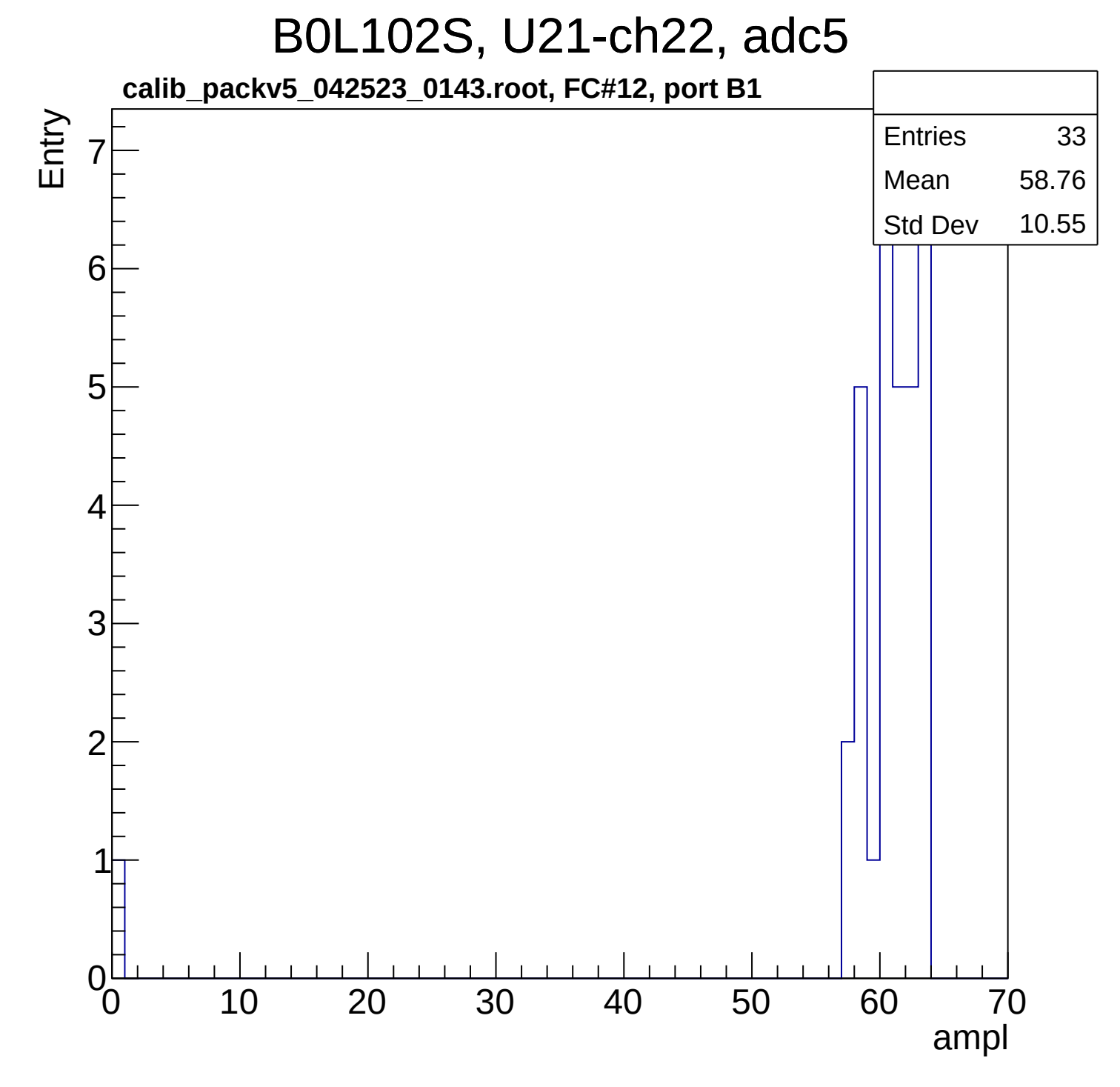
B0L102S, U21-ch22, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 58.76
Std Dev 10.55
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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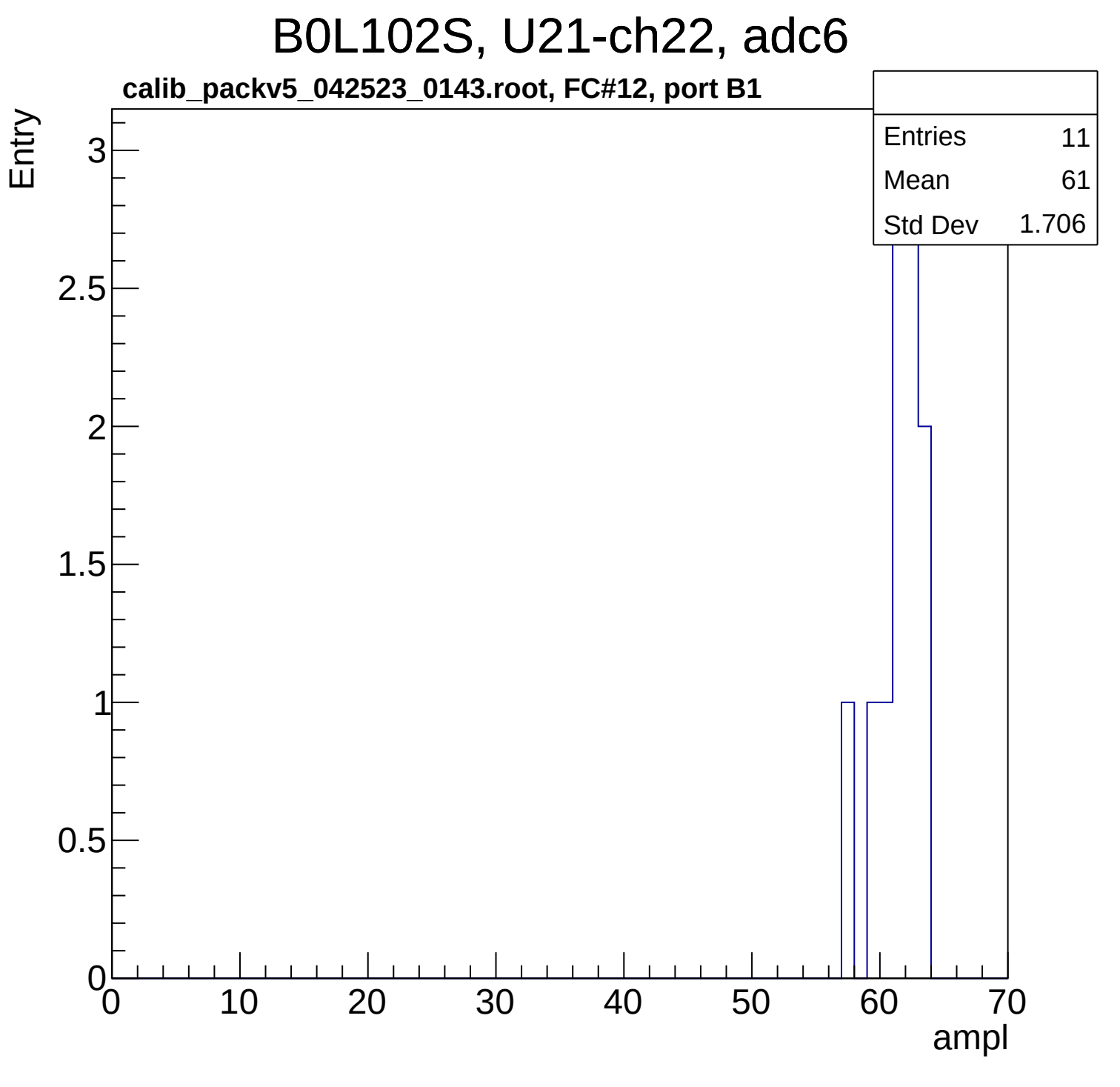
B0L102S, U21-ch22, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61
Std Dev 1.706
Entry
ampl
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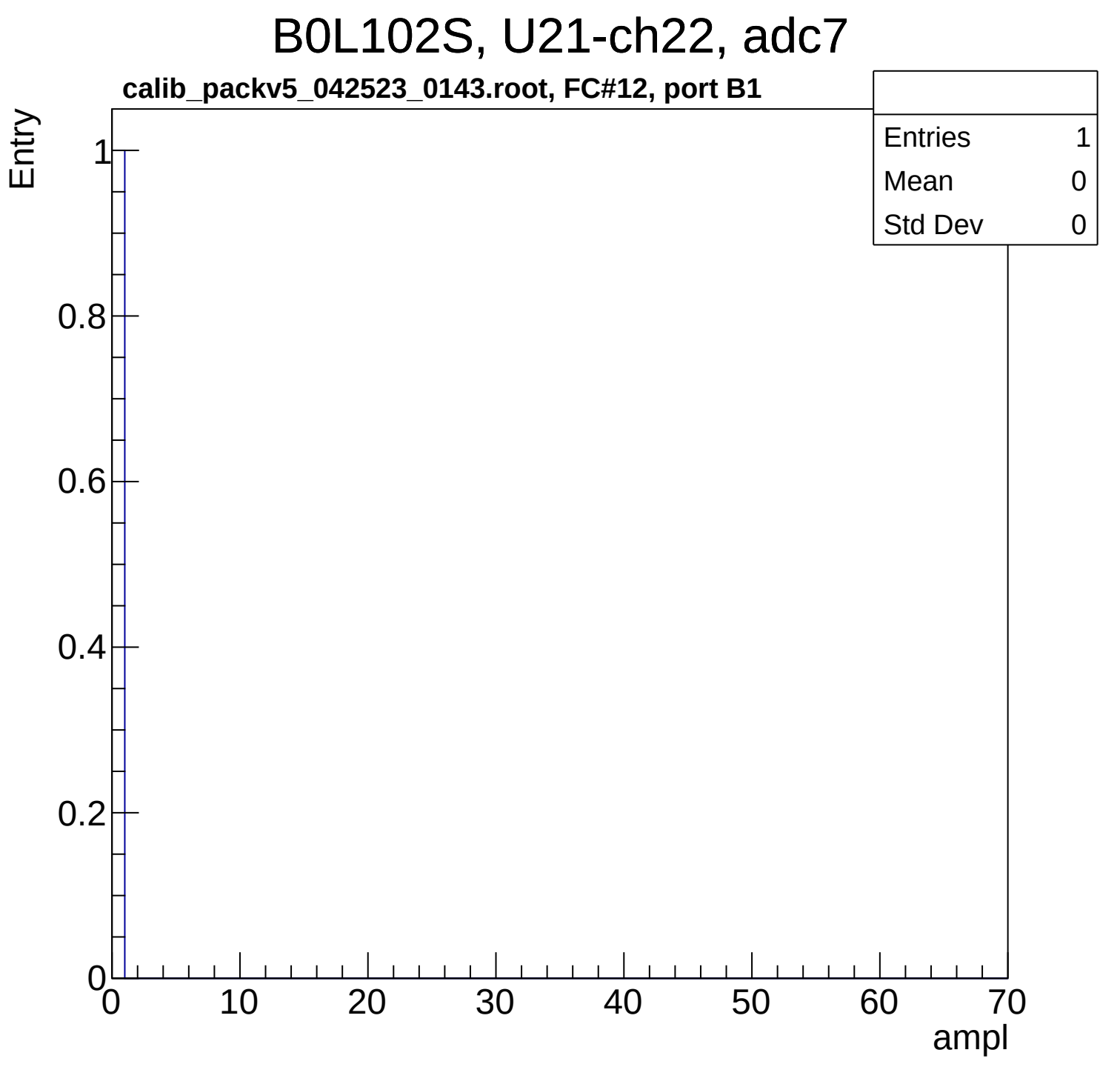
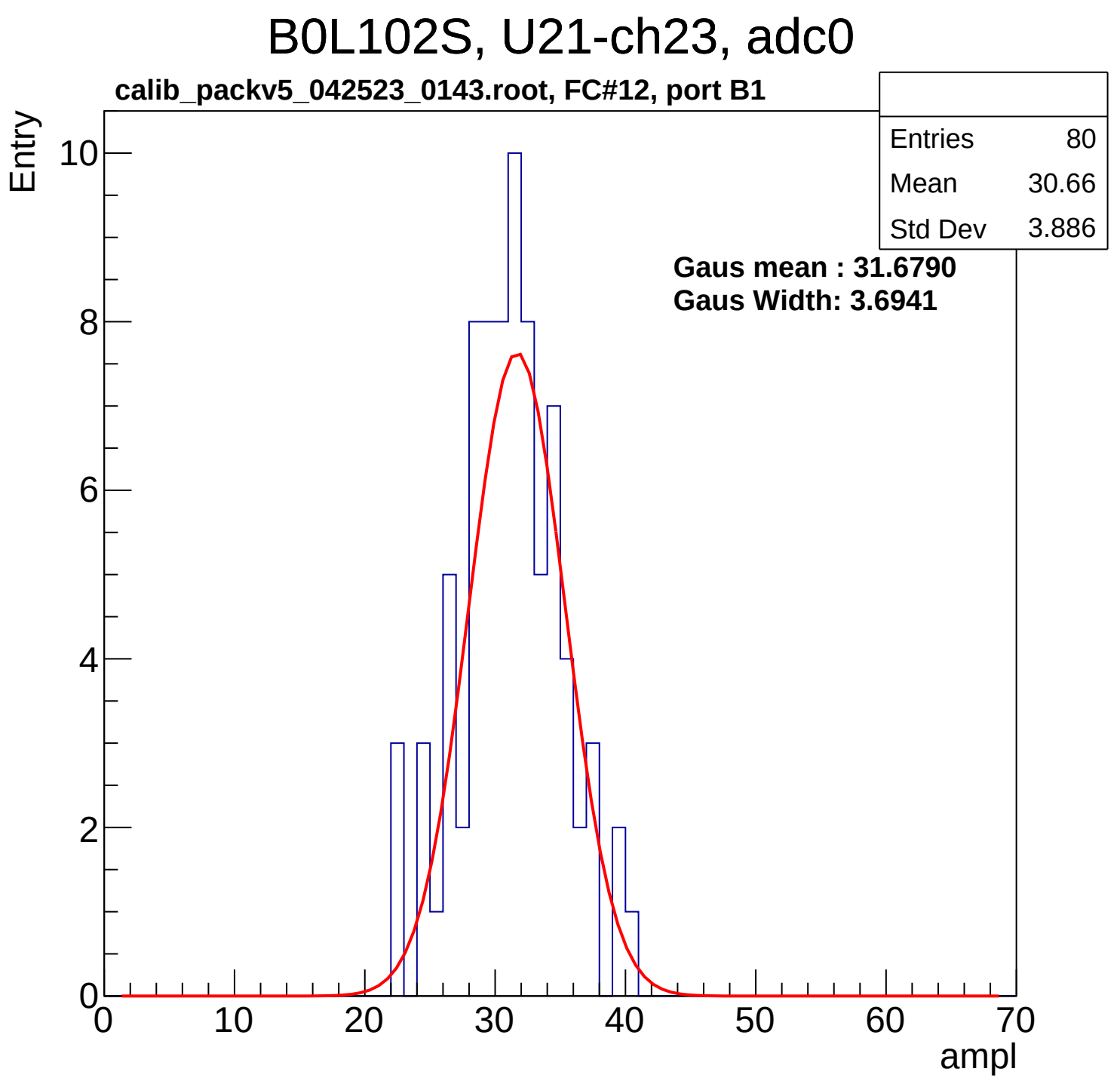
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch22, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch23, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 30.66
Std Dev 3.886
Gaus mean : 31.6790
Gaus Width: 3.6941
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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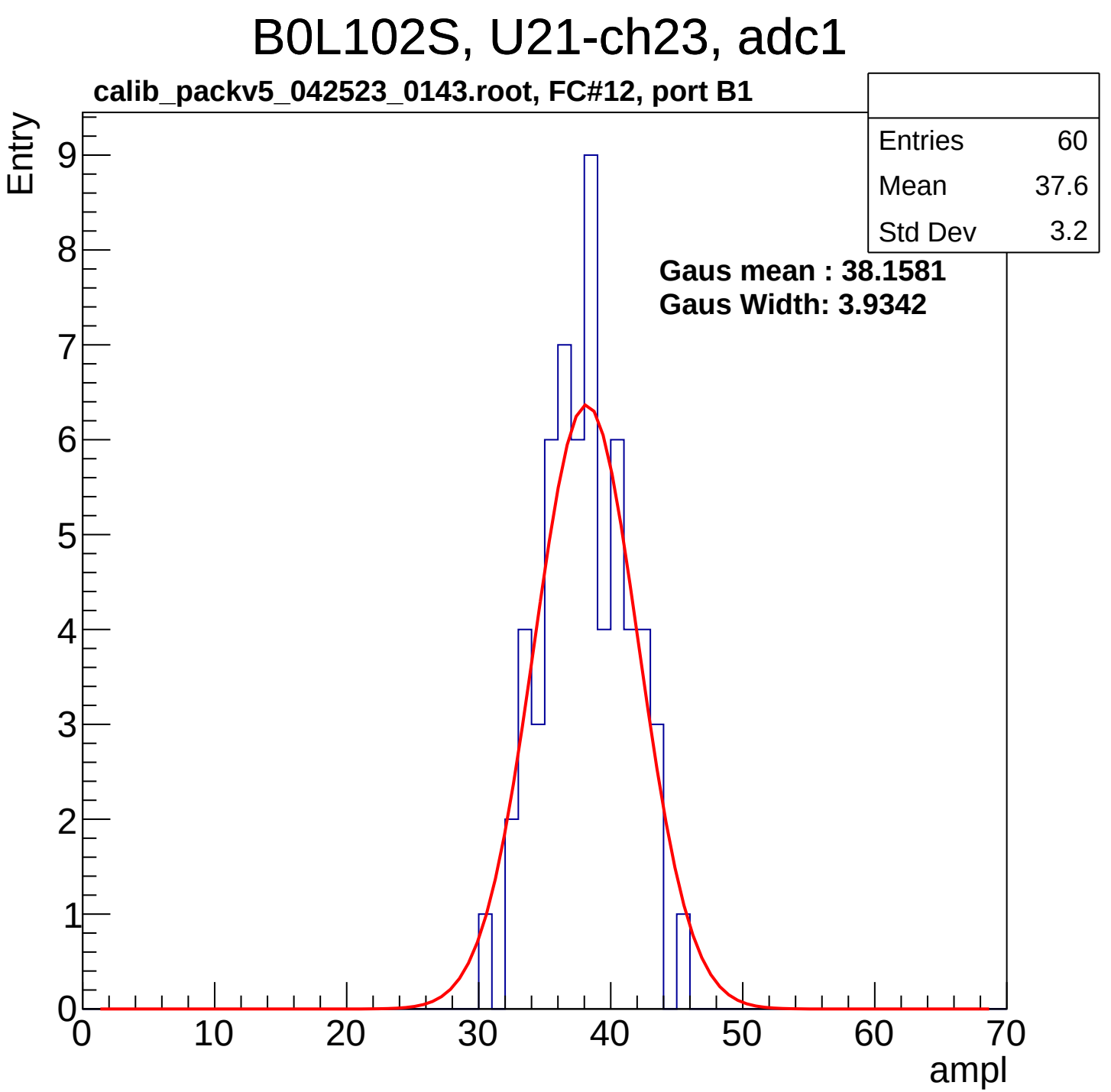
B0L102S, U21-ch23, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 37.6
Std Dev 3.2
Gaus mean : 38.1581
Gaus Width: 3.9342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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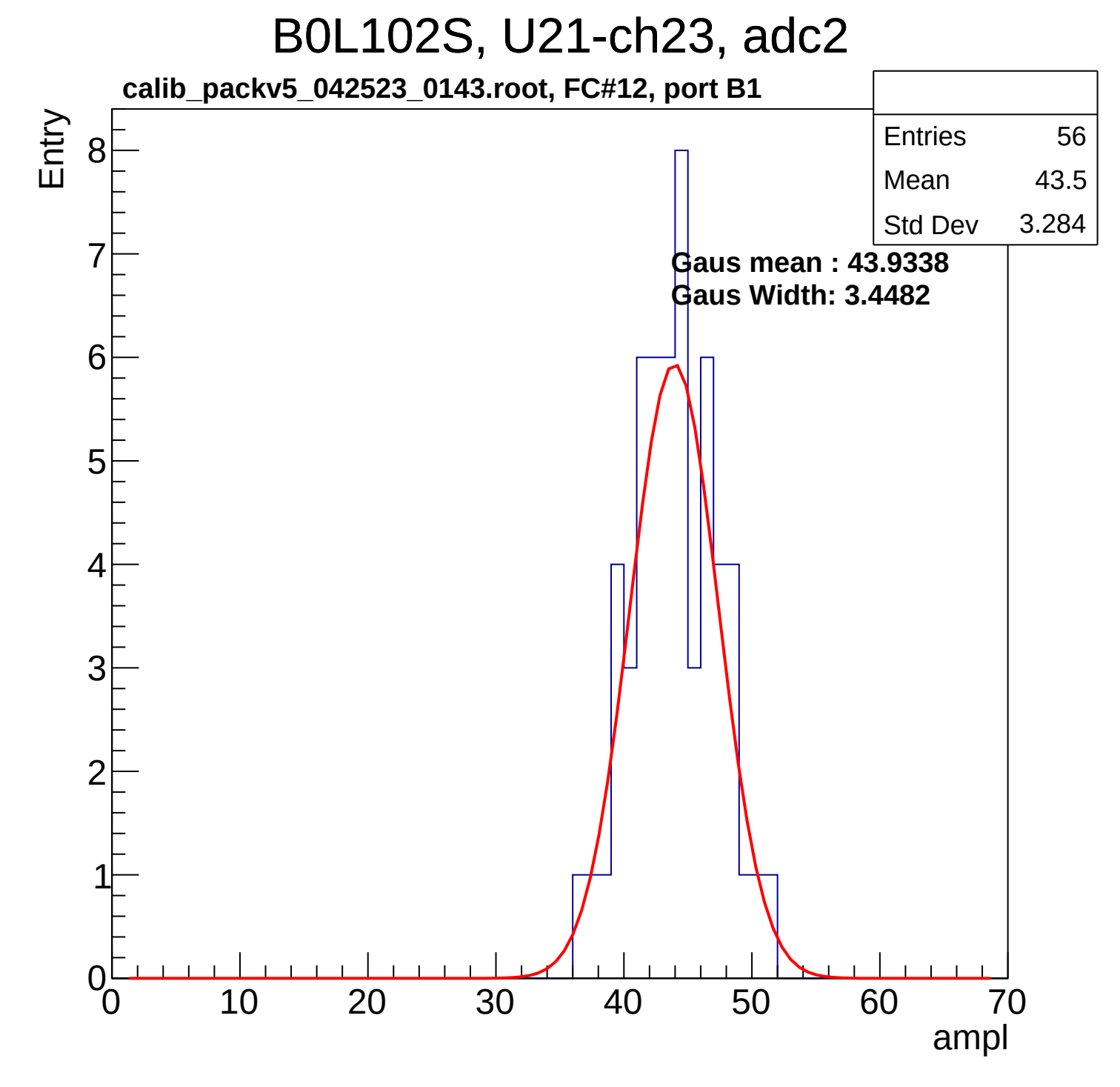
60
70


B0L102S, U21-ch23, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 43.5
Std Dev 3.284
Gaus mean : 43.9338
Gaus Width: 3.4482
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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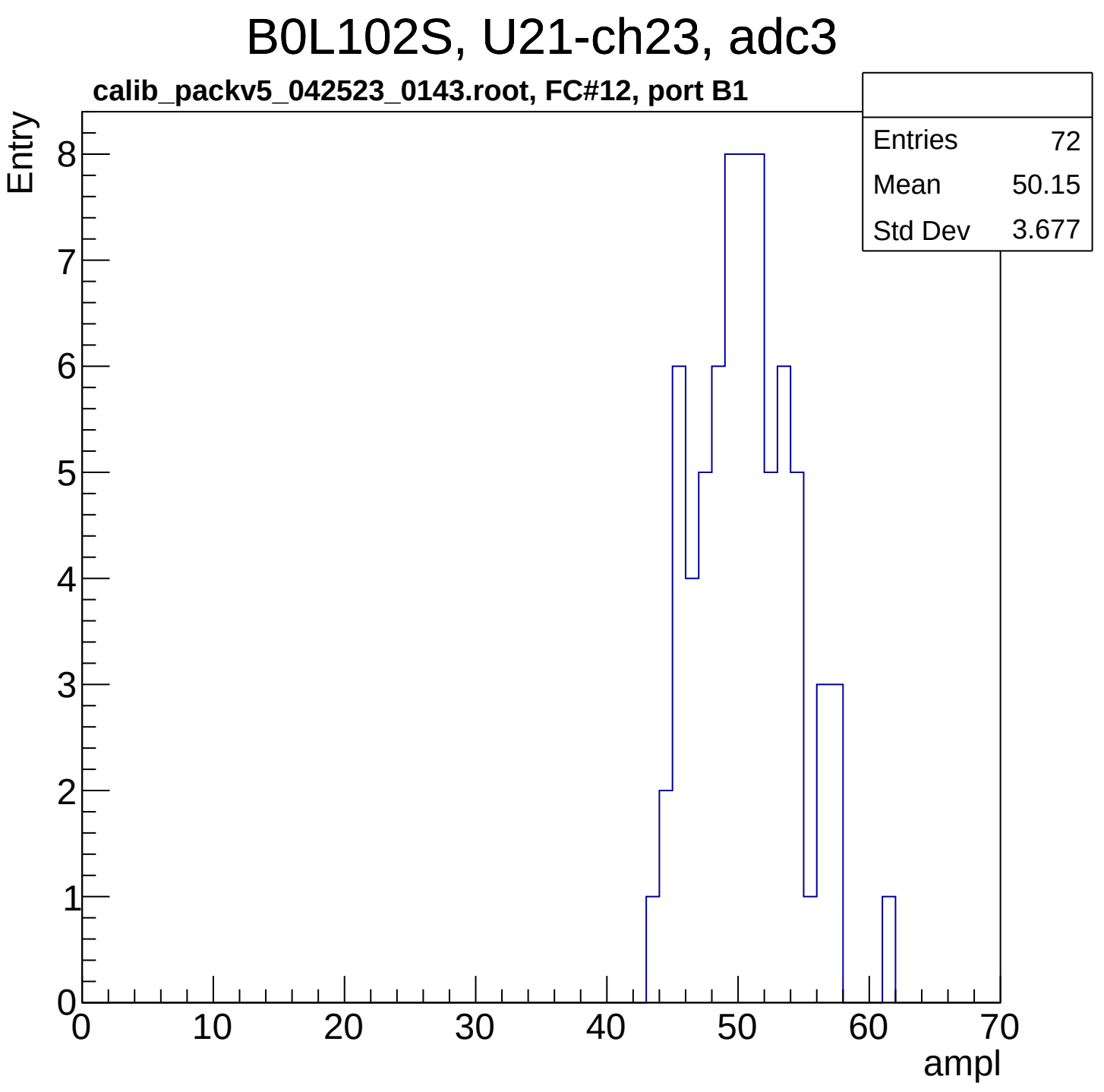
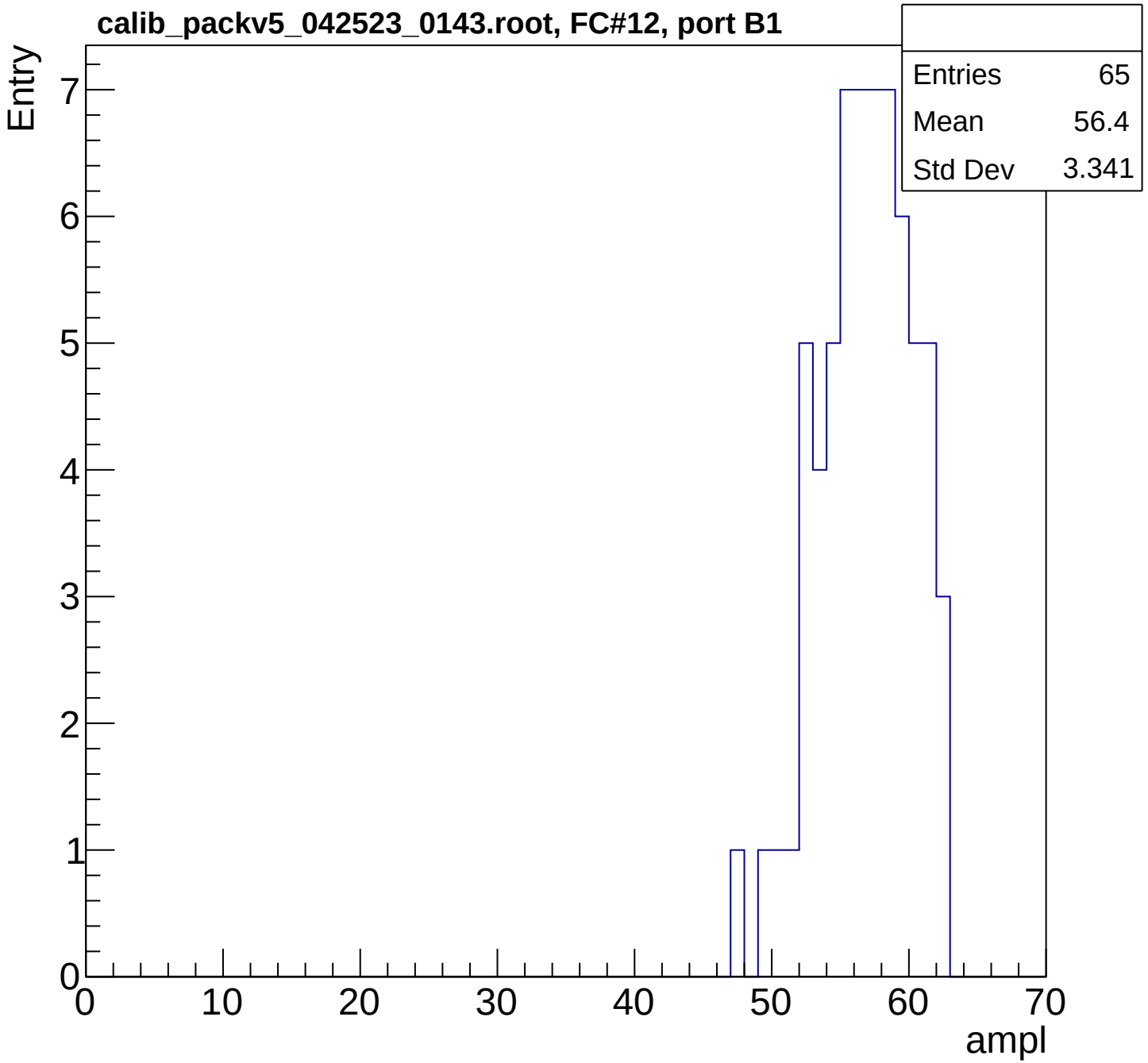
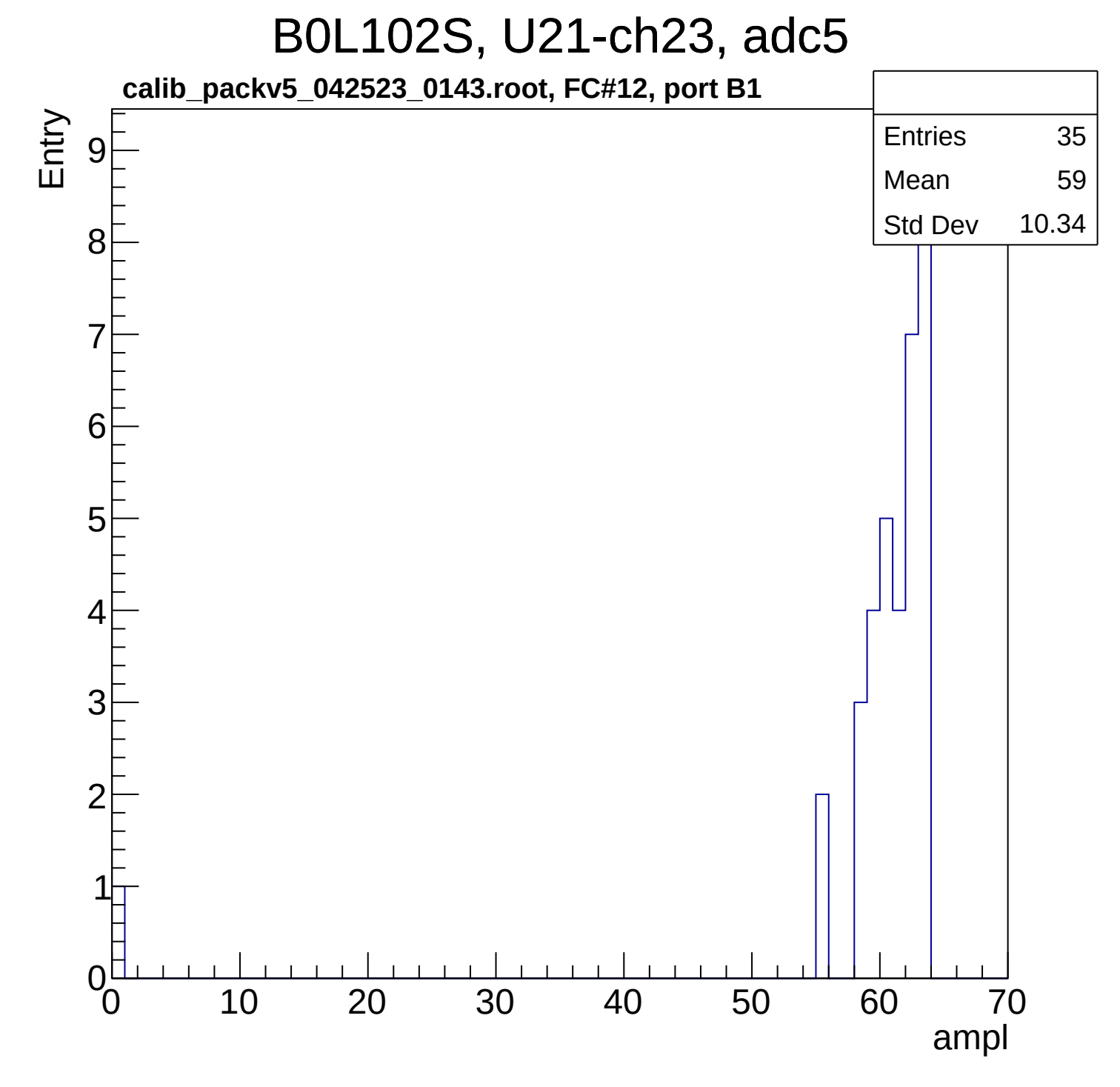
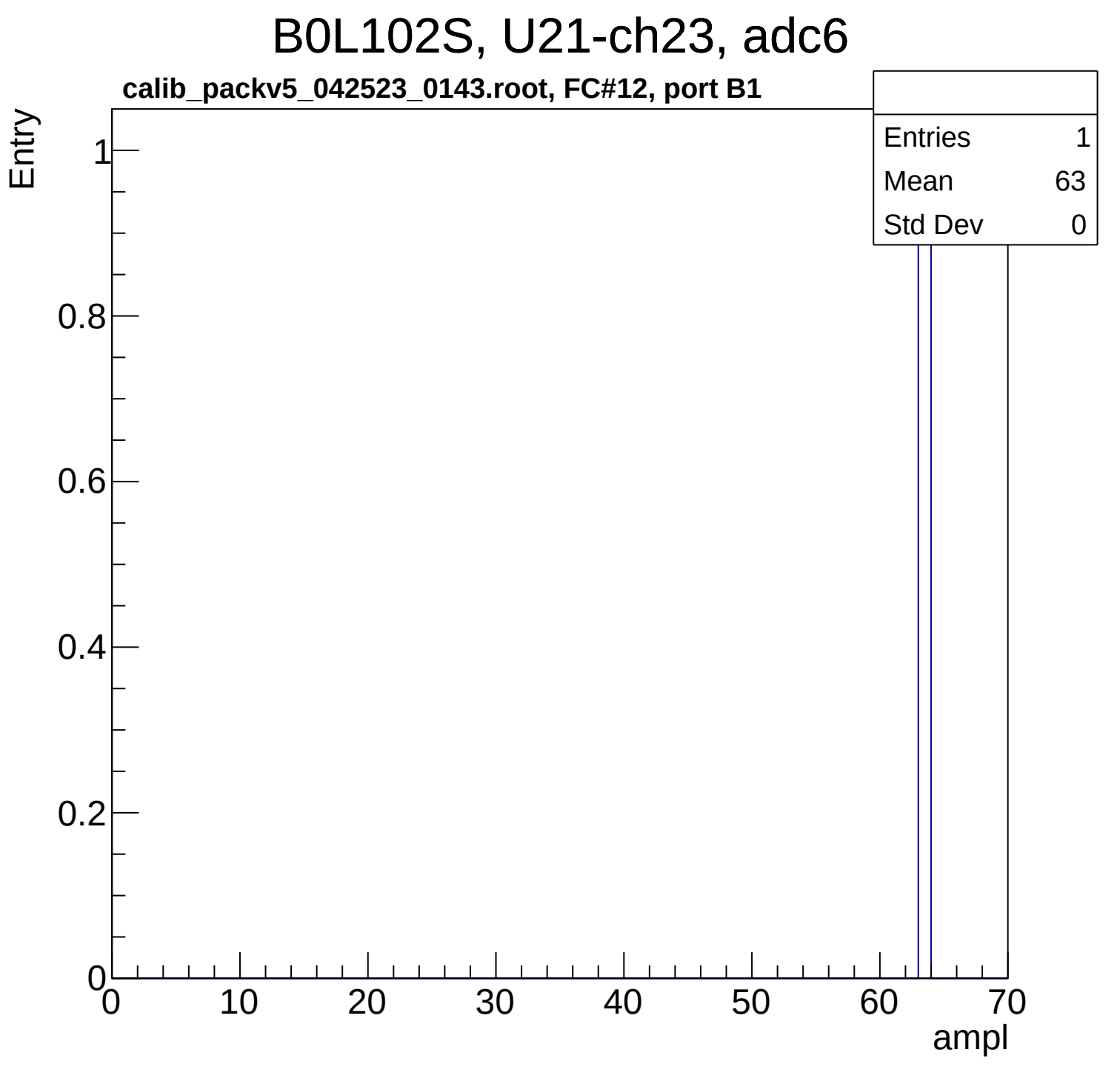
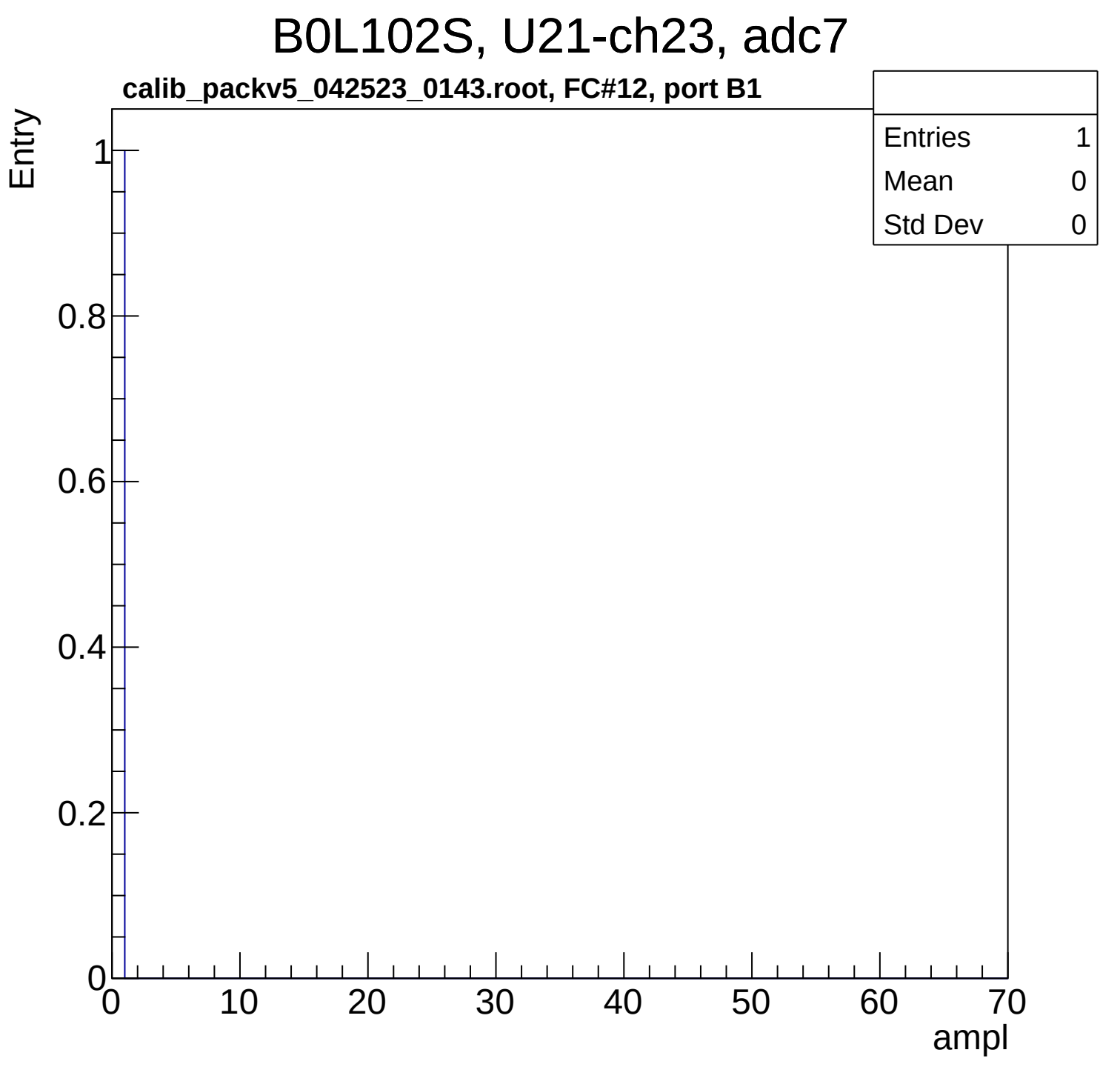
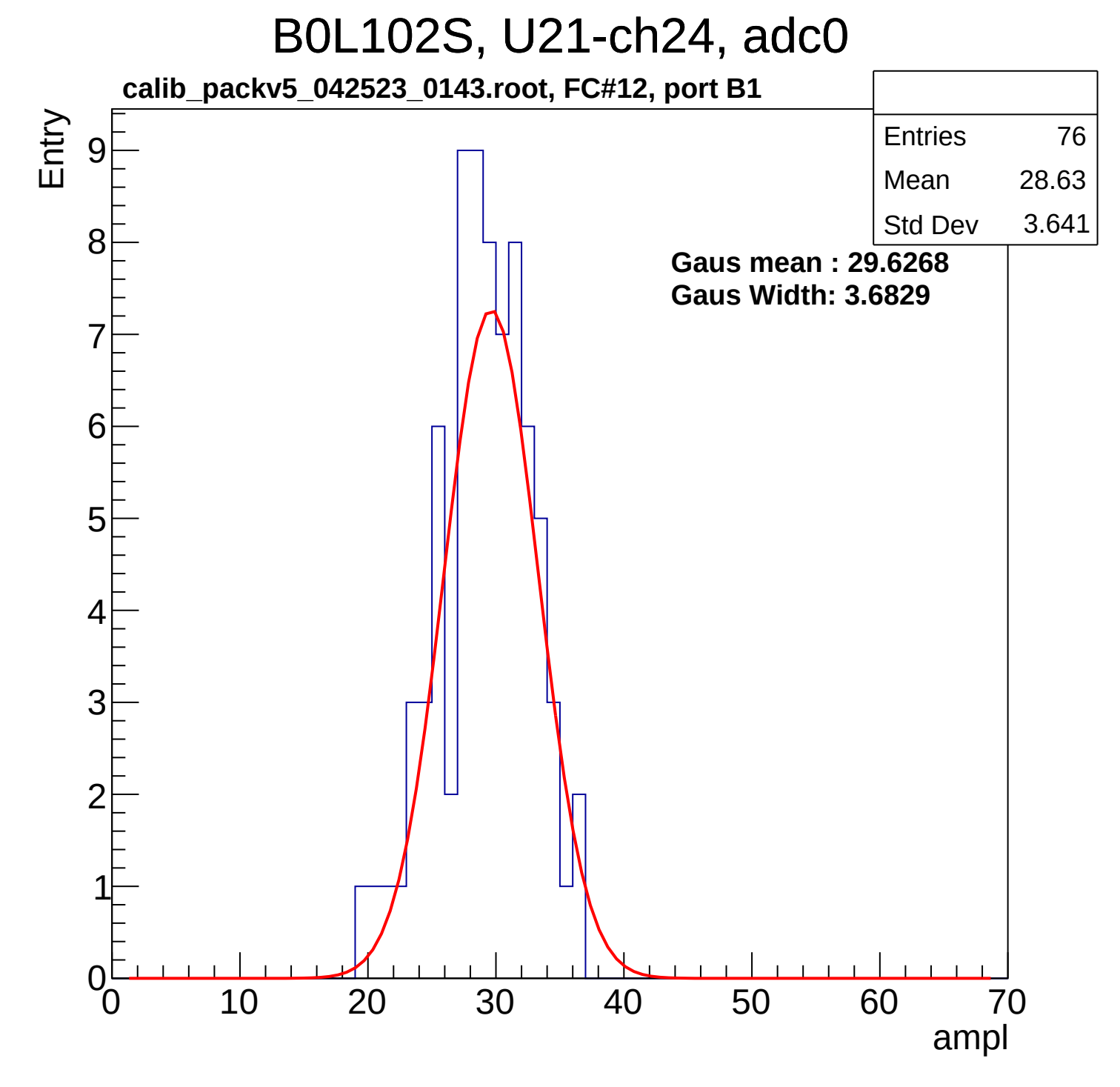

B0L102S, U21-ch23, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 50.15
Std Dev 3.677
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

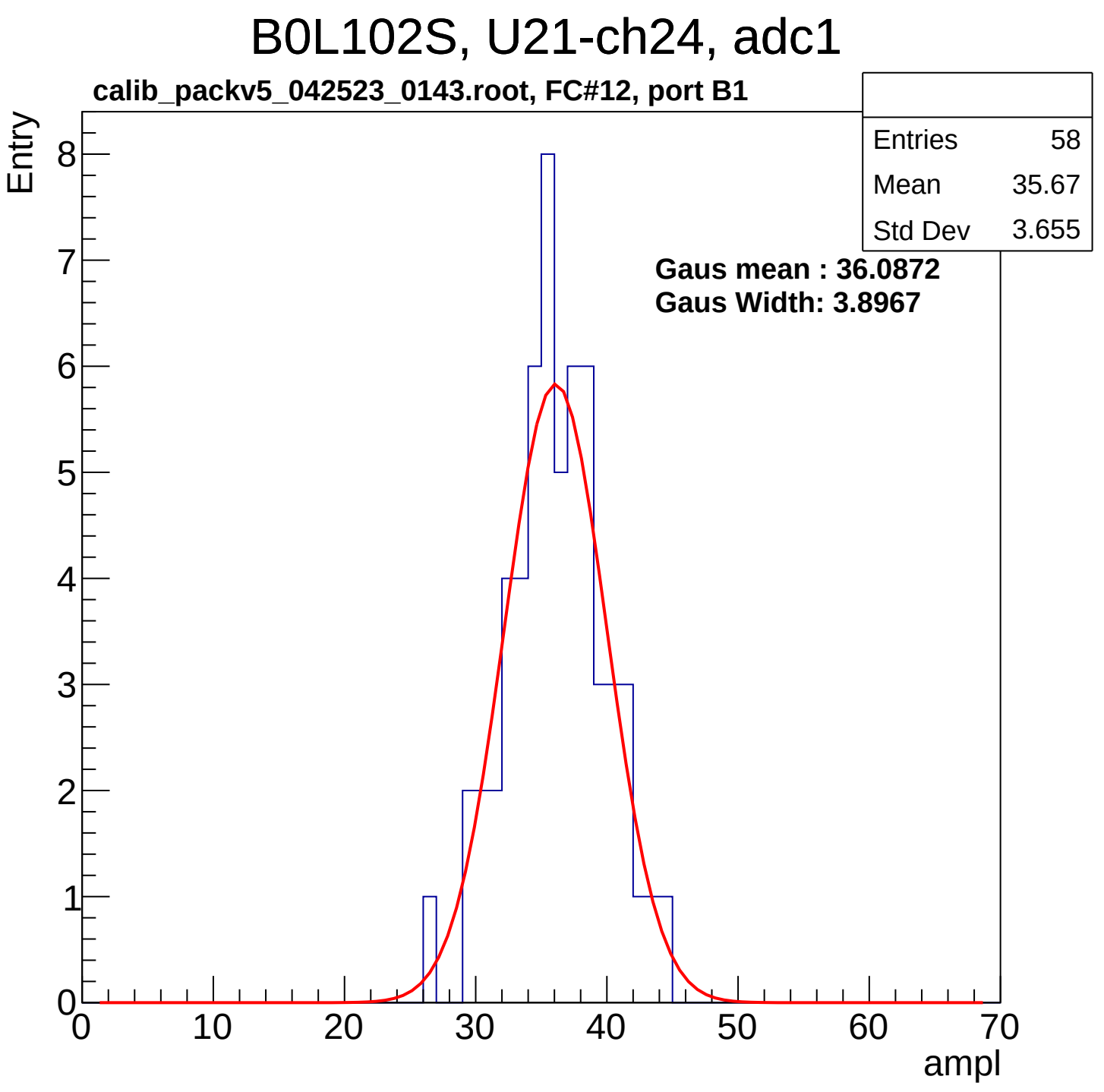

B0L102S, U21-ch23, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 56.4
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

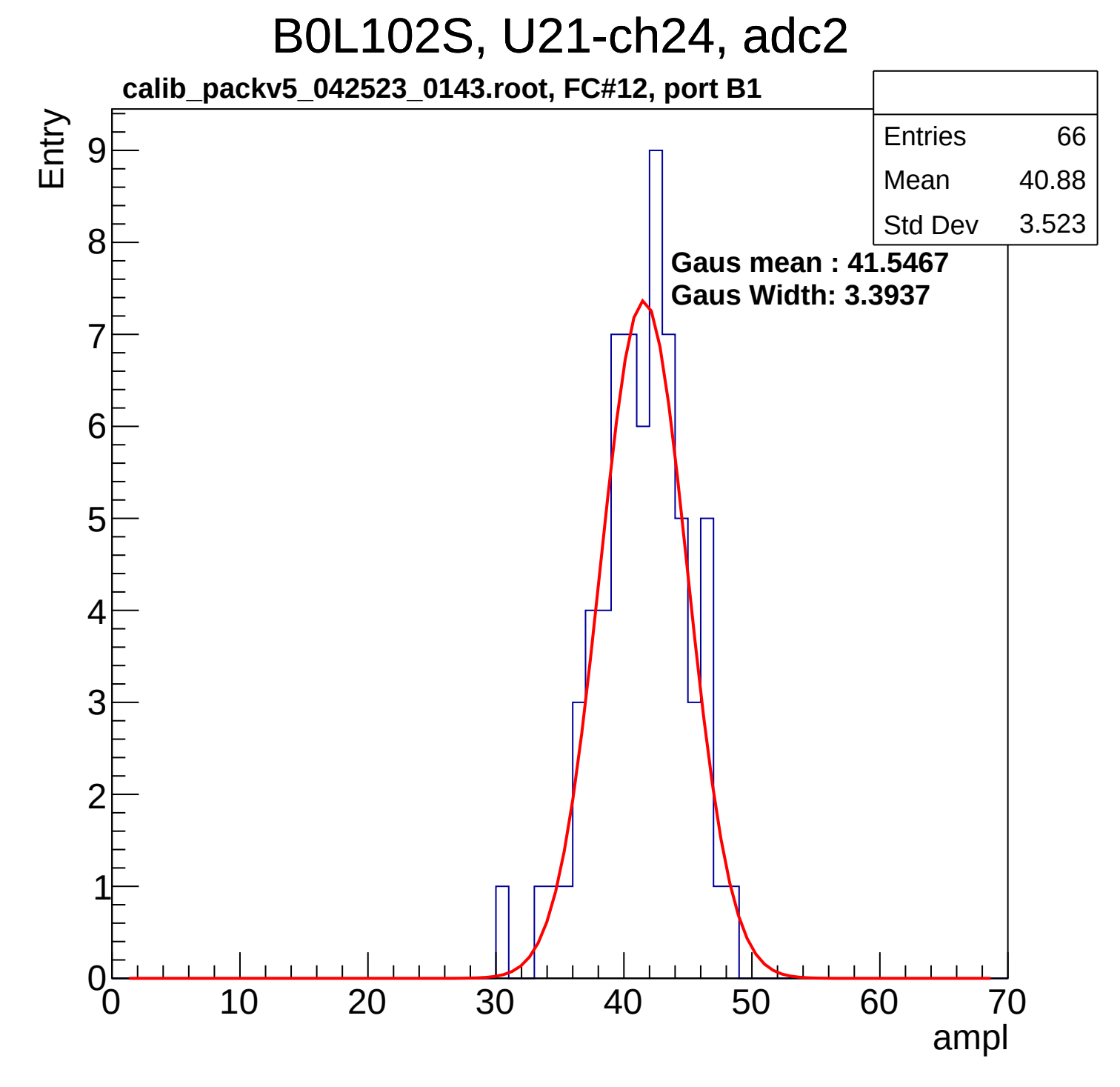

B0L102S, U21-ch23, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 59
Std Dev 10.34
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

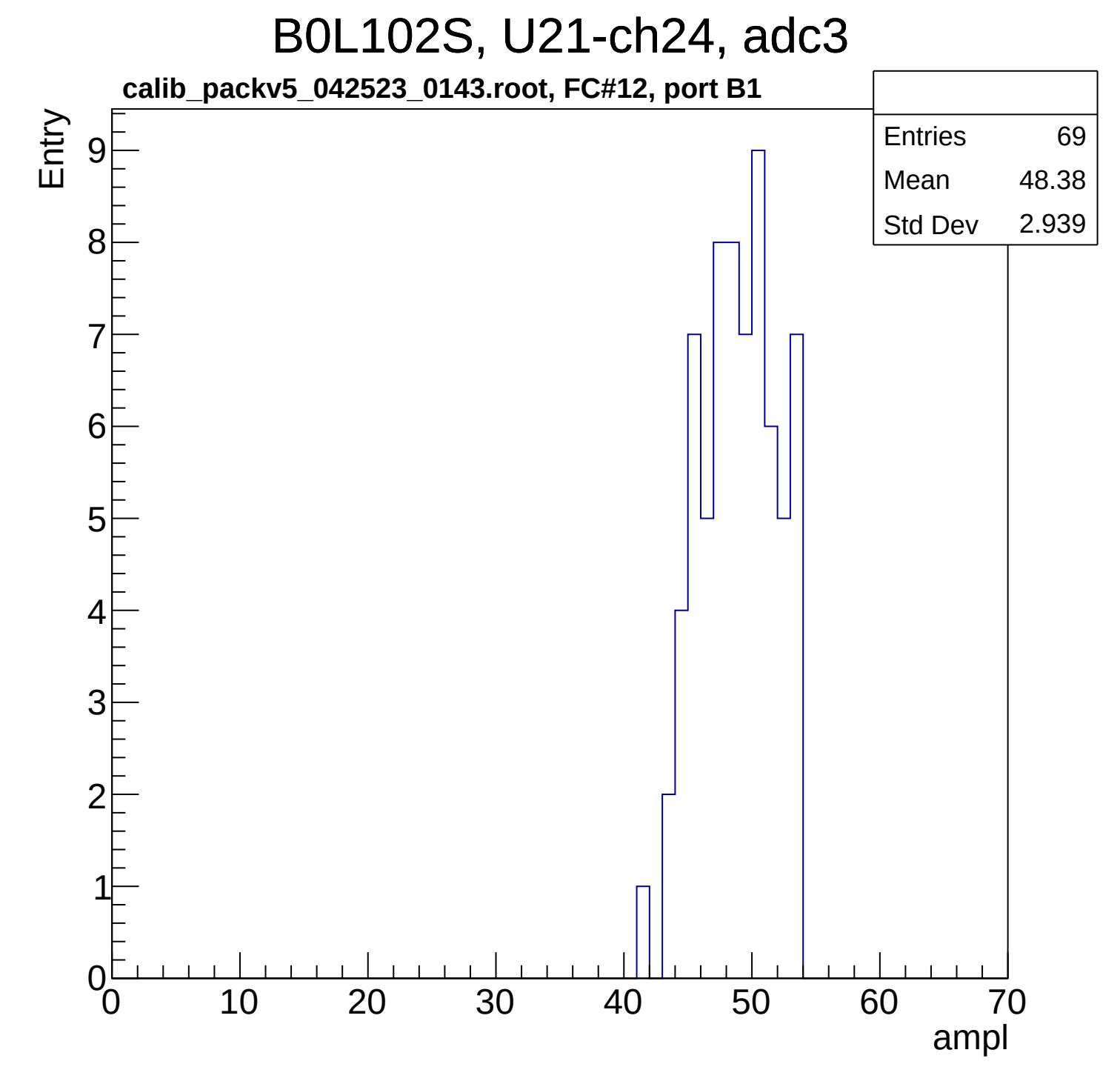

B0L102S, U21-ch23, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

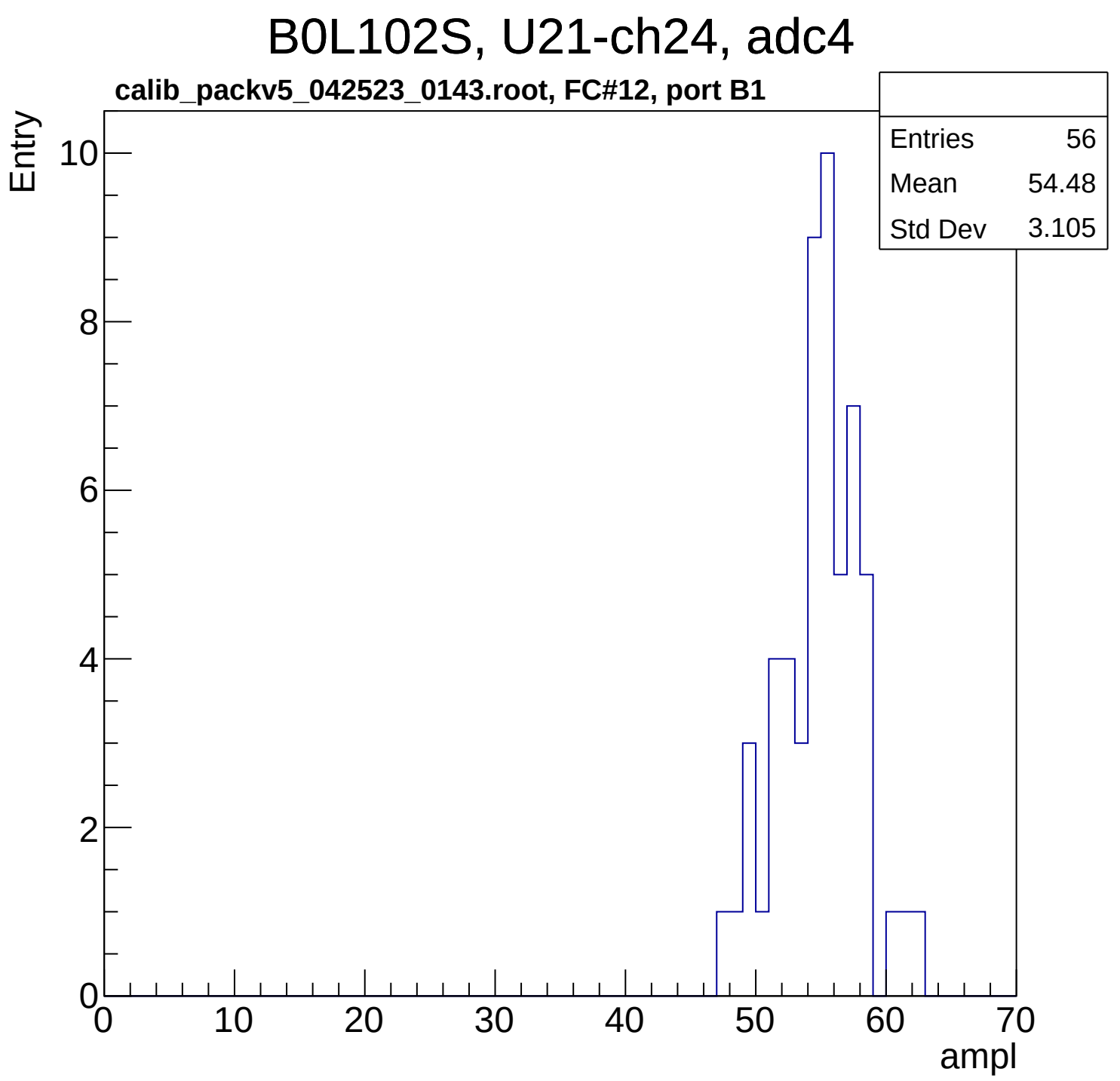

B0L102S, U21-ch23, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

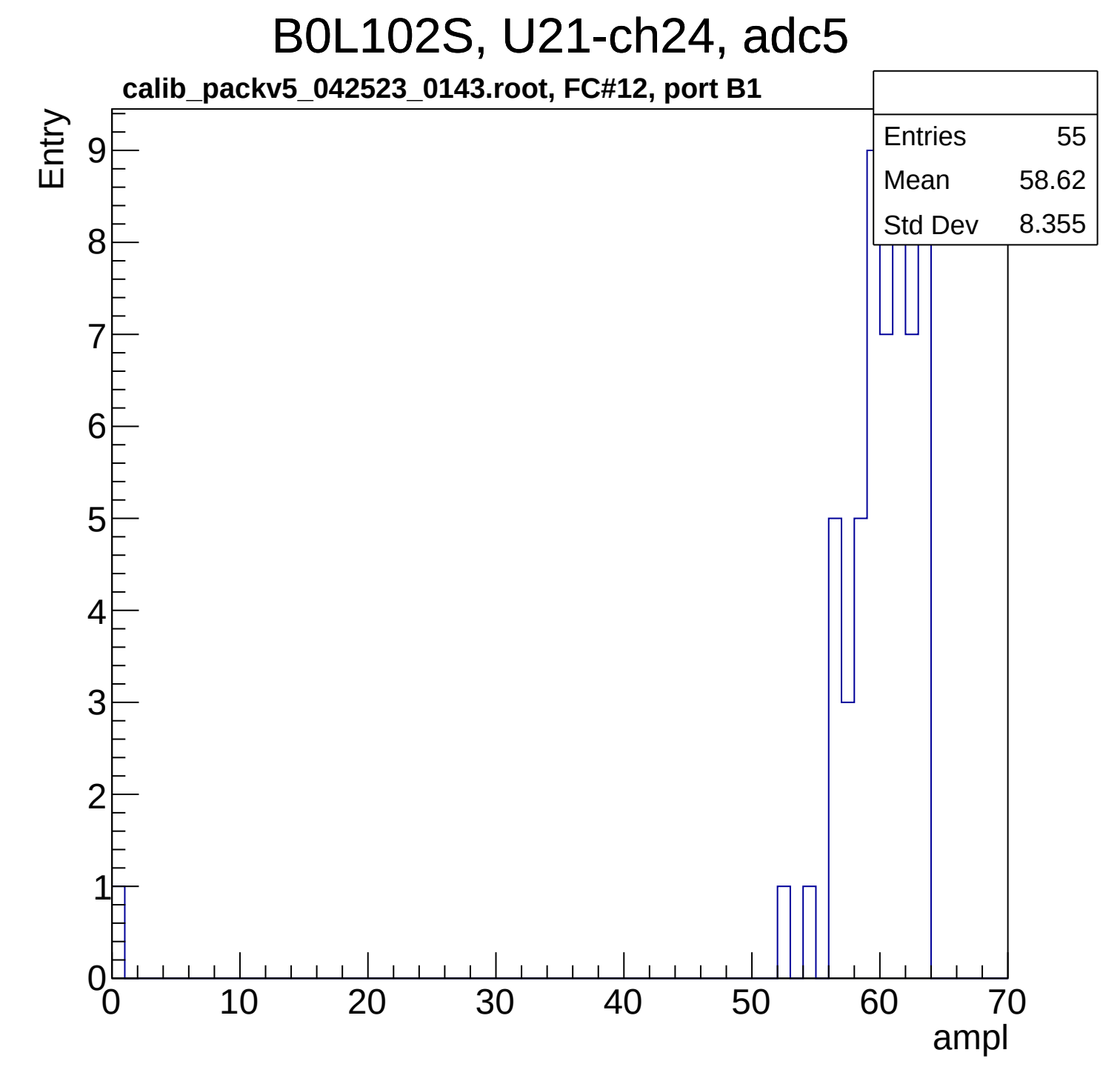

B0L102S, U21-ch24, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 28.63
Std Dev 3.641
Gaus mean : 29.6268
Gaus Width: 3.6829
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

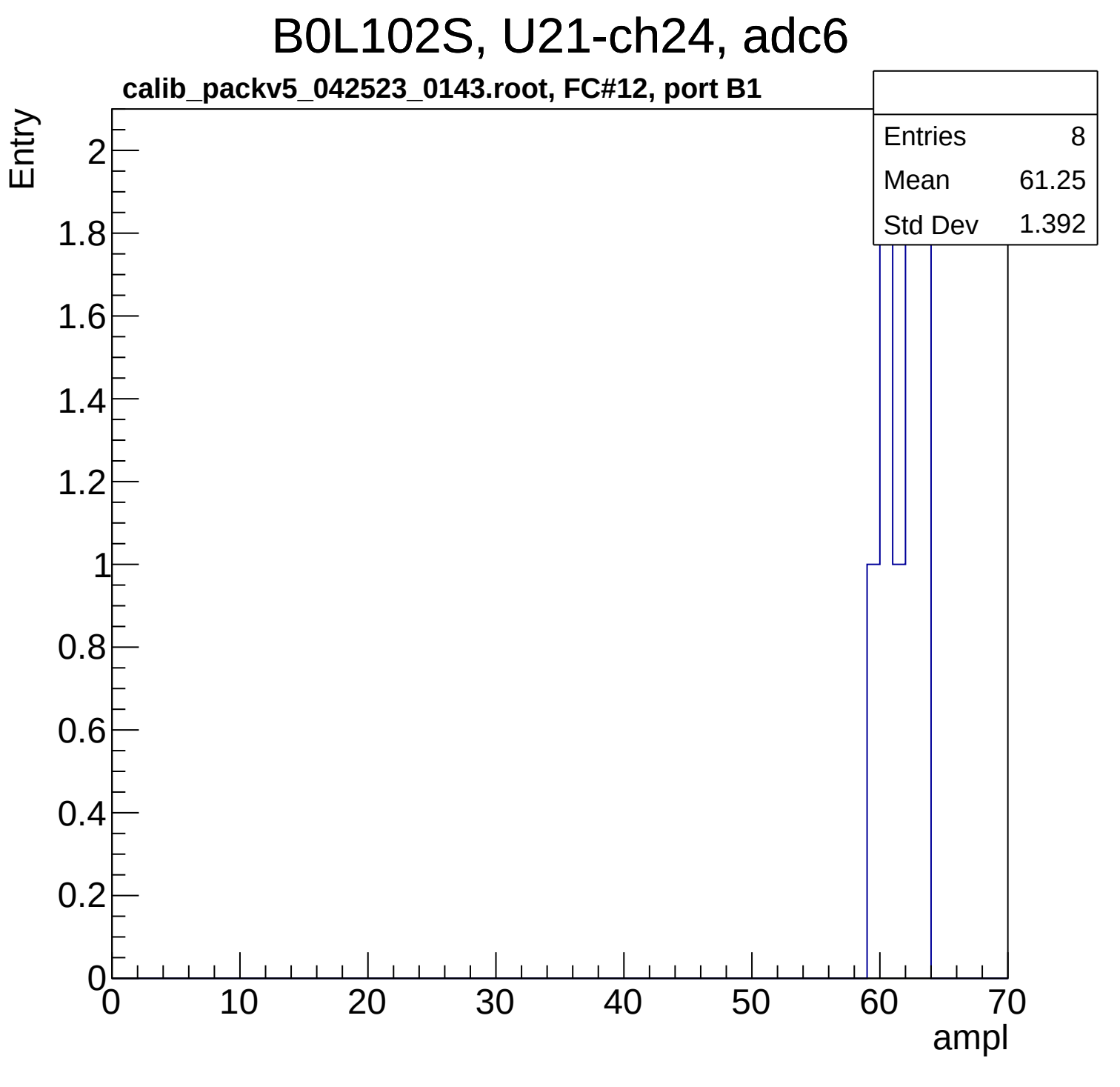

B0L102S, U21-ch24, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 35.67
Std Dev 3.655
Gaus mean : 36.0872
Gaus Width: 3.8967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

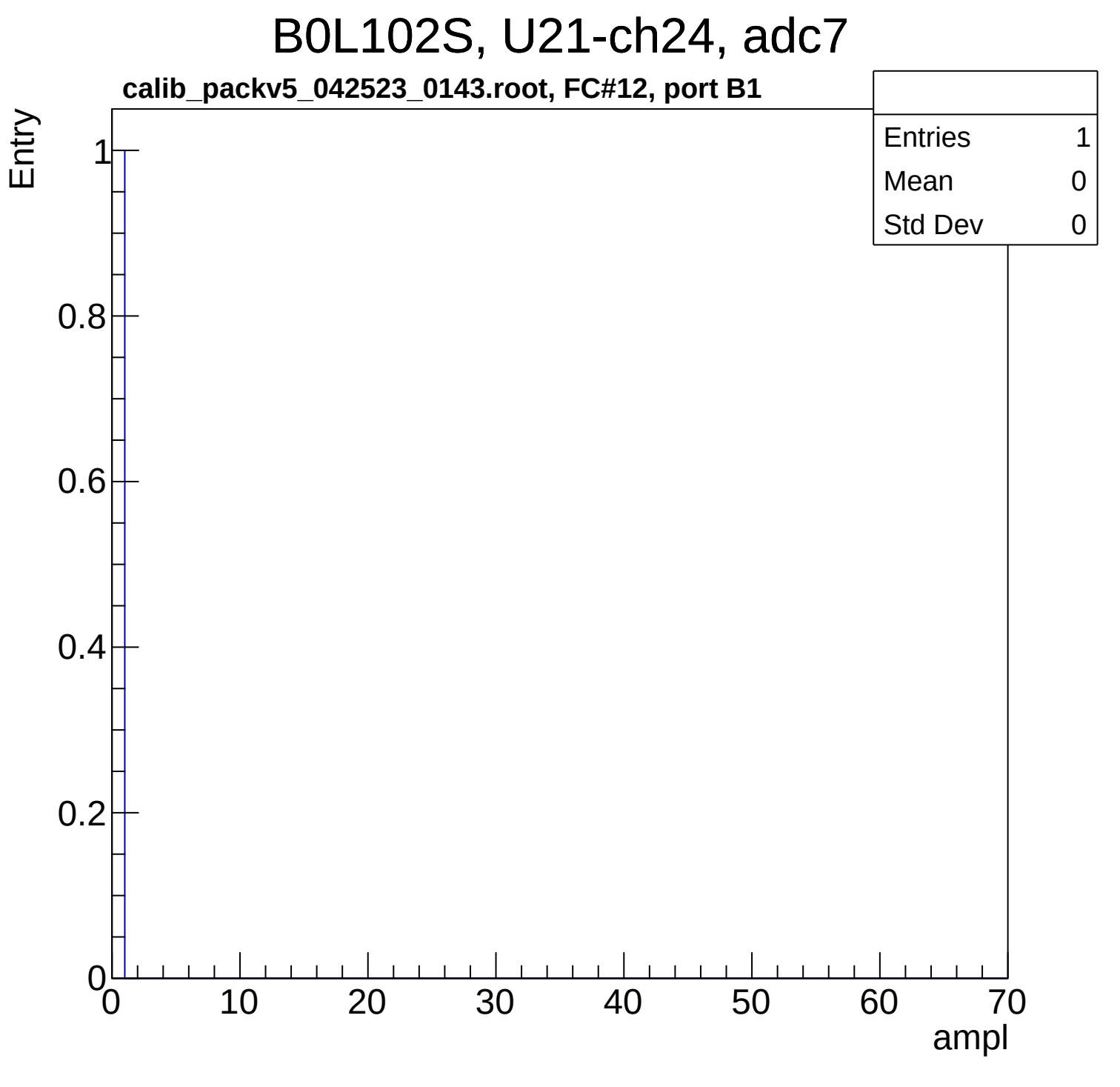

B0L102S, U21-ch24, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 40.88
Std Dev 3.523
Gaus mean : 41.5467
Gaus Width: 3.3937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

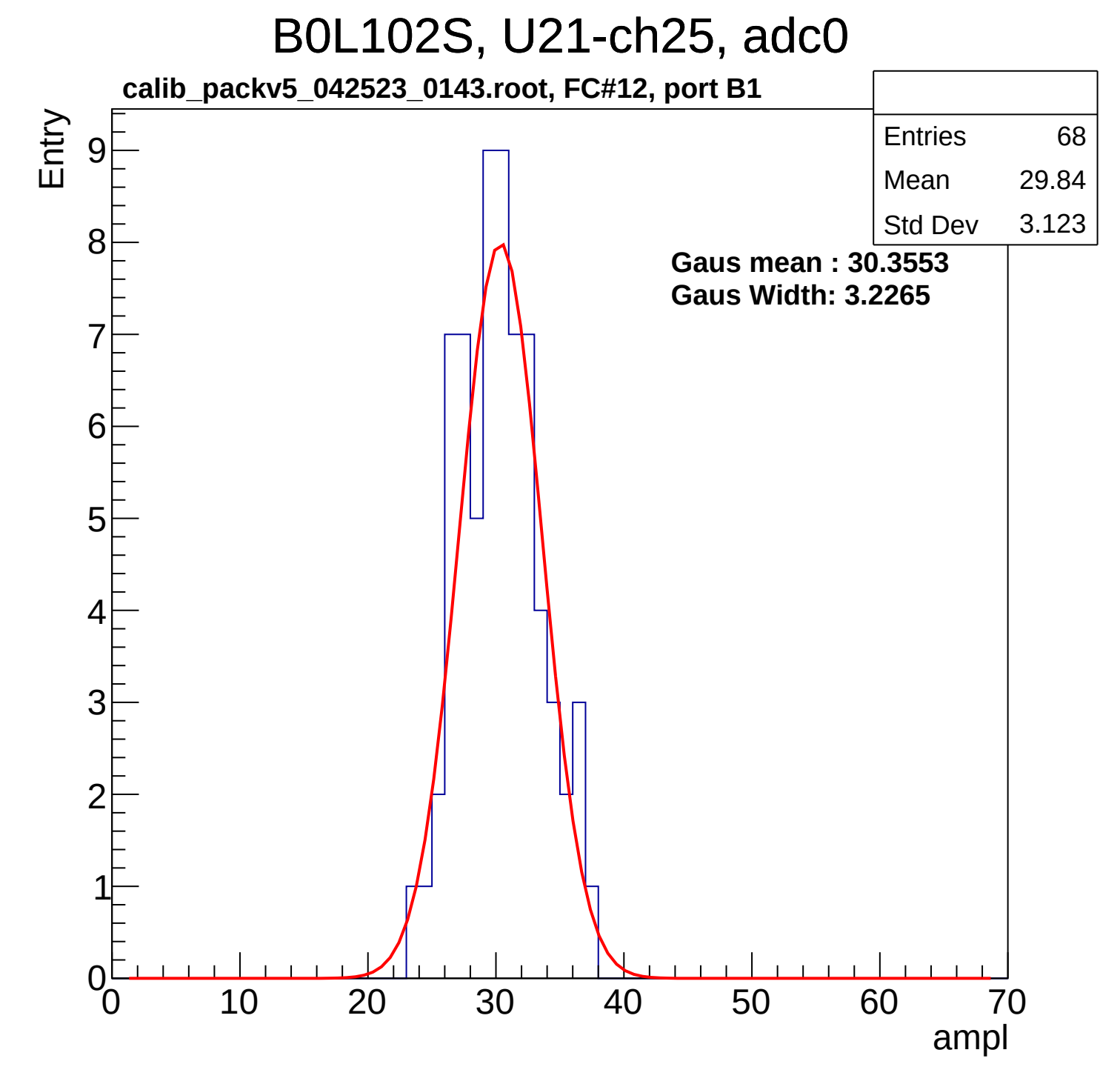

B0L102S, U21-ch24, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 48.38
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

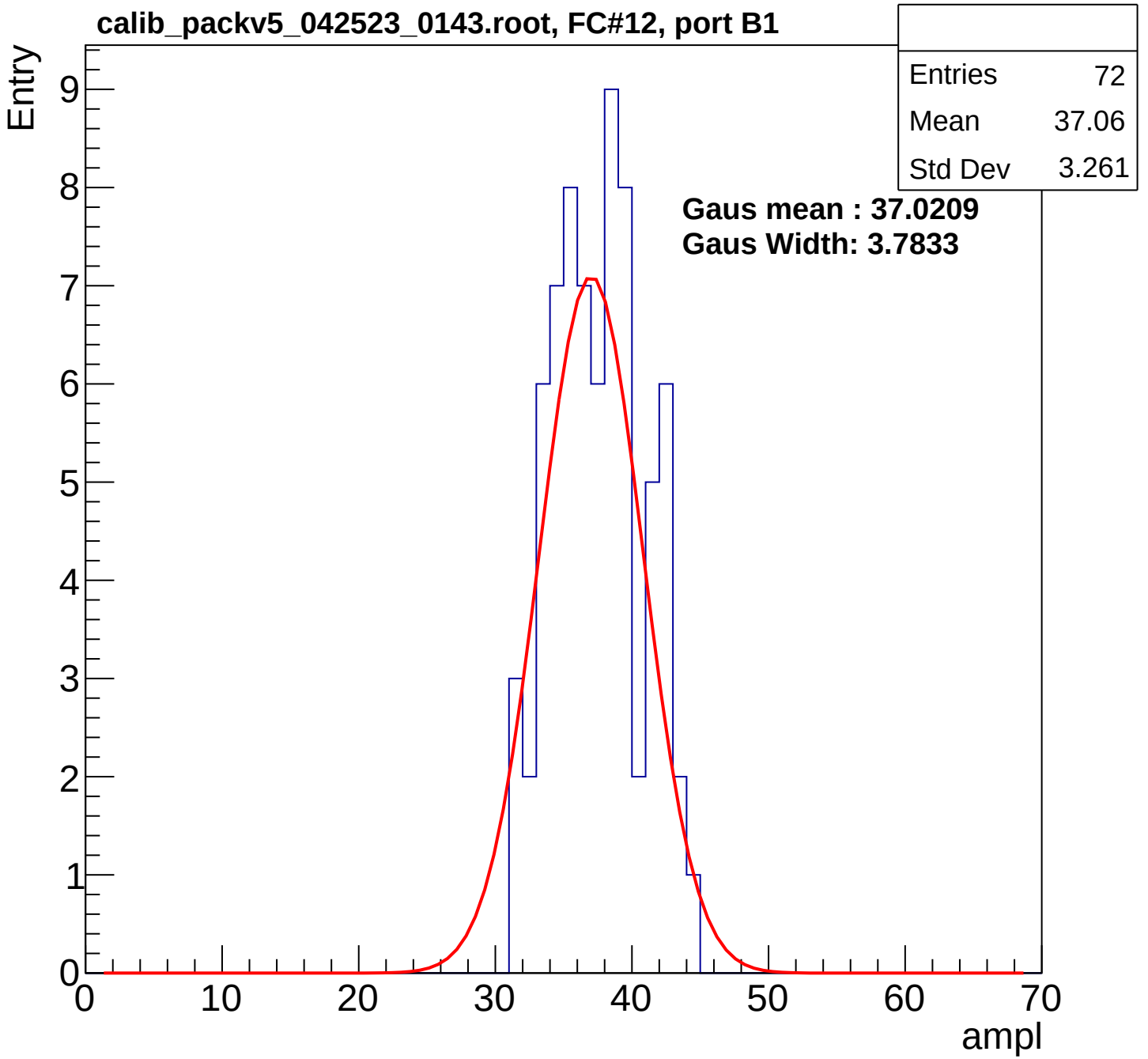

B0L102S, U21-ch24, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 54.48
Std Dev 3.105
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

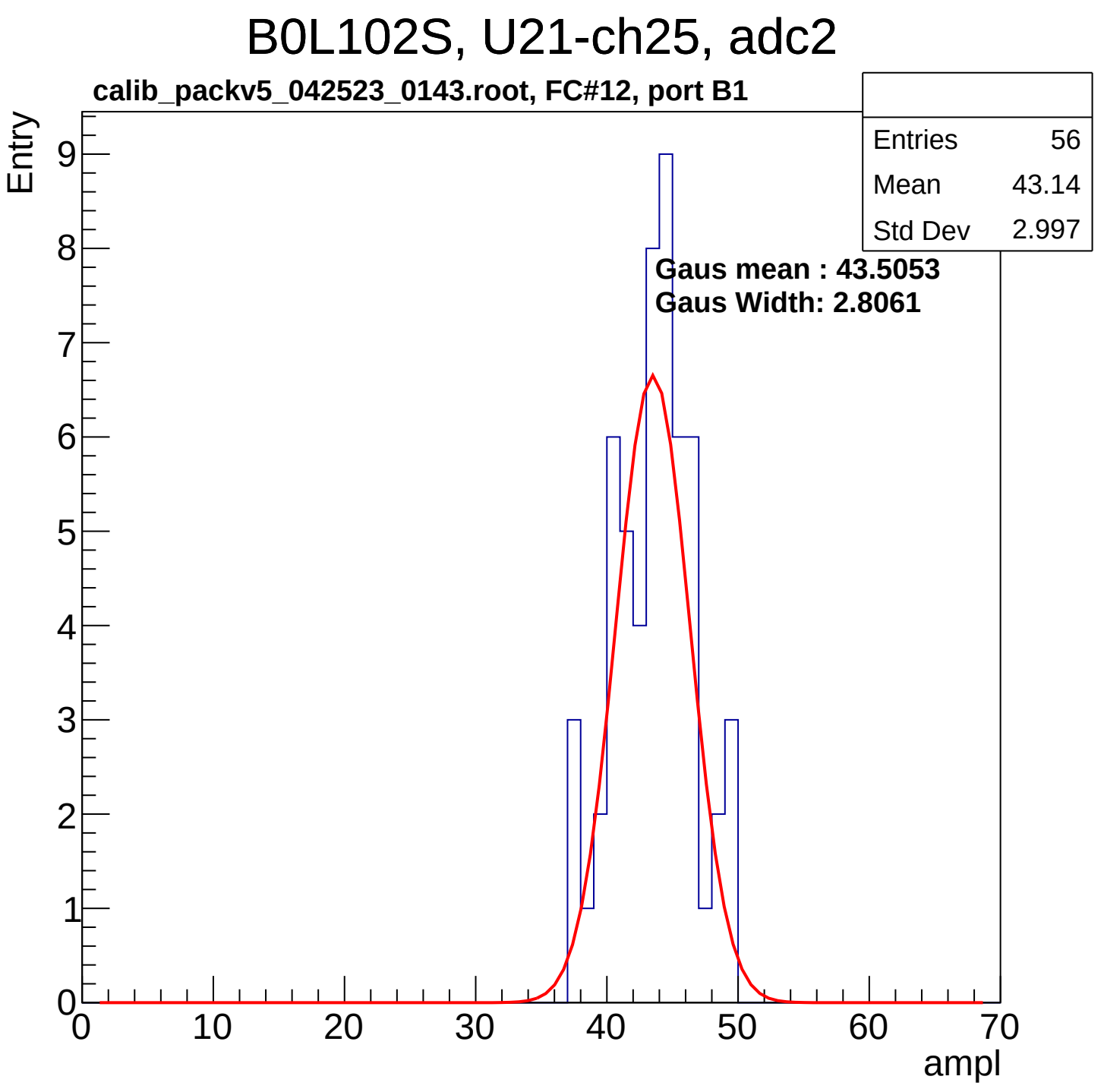

B0L102S, U21-ch24, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 58.62
Std Dev 8.355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

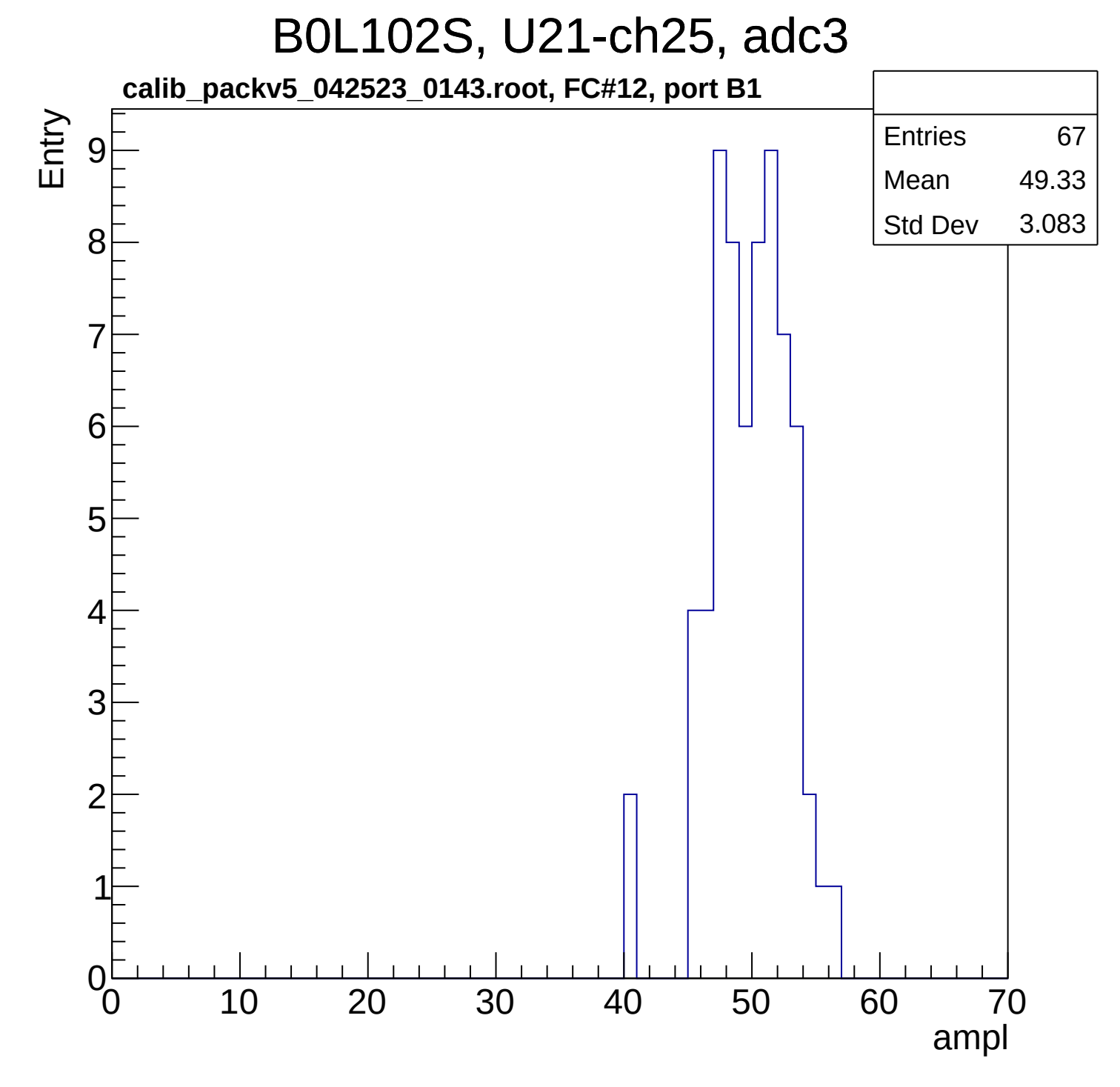

B0L102S, U21-ch24, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.25
Std Dev 1.392
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch24, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch25, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 29.84
Std Dev 3.123
Gaus mean : 30.3553
Gaus Width: 3.2265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch25, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 37.06
Std Dev 3.261
Gaus mean : 37.0209
Gaus Width: 3.7833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch25, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 43.14
Std Dev 2.997
Gaus mean : 43.5053
Gaus Width: 2.8061
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch25, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 49.33
Std Dev 3.083
Entry
ampl
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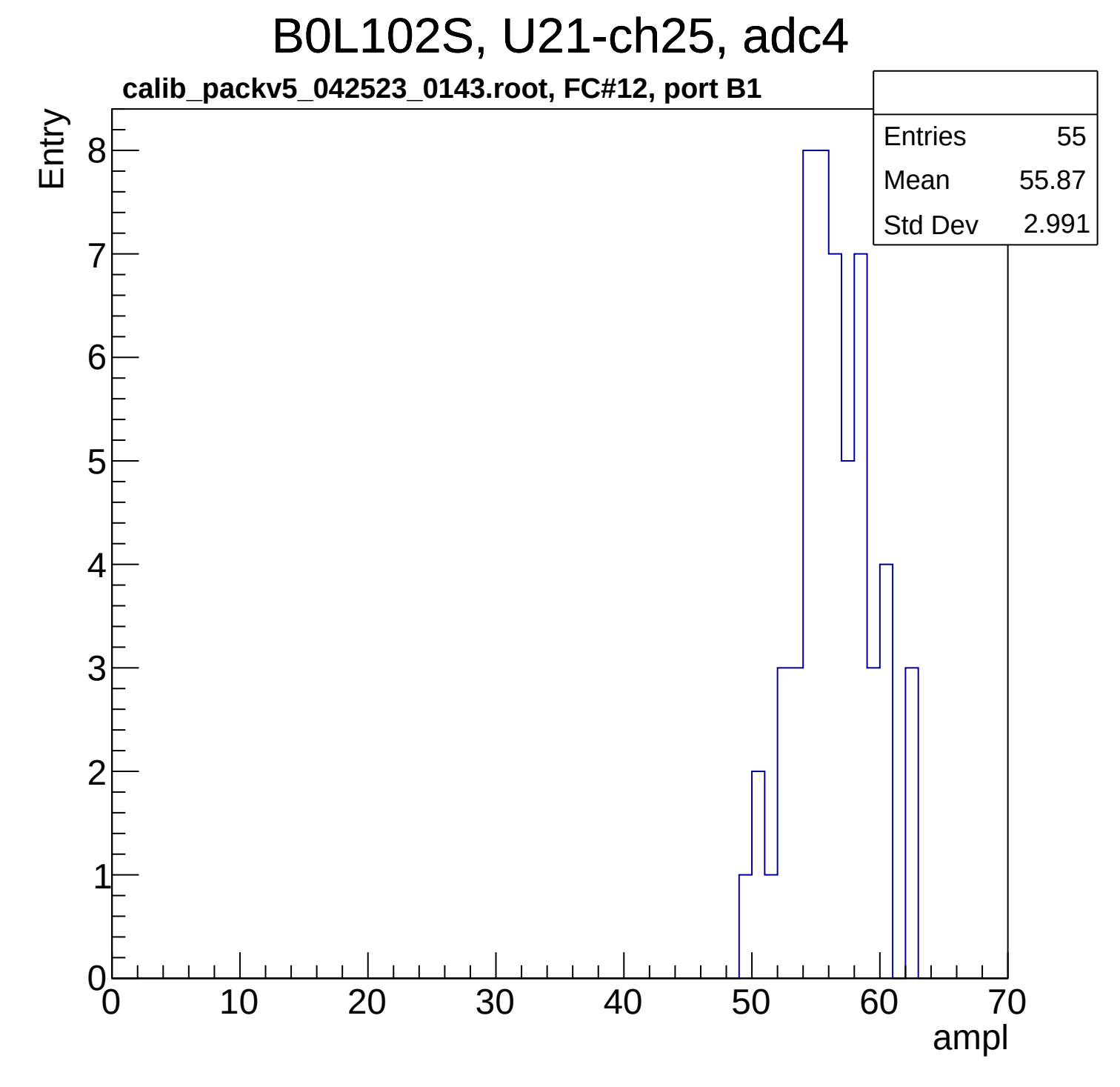
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch25, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.87
Std Dev 2.991
Entry
ampl
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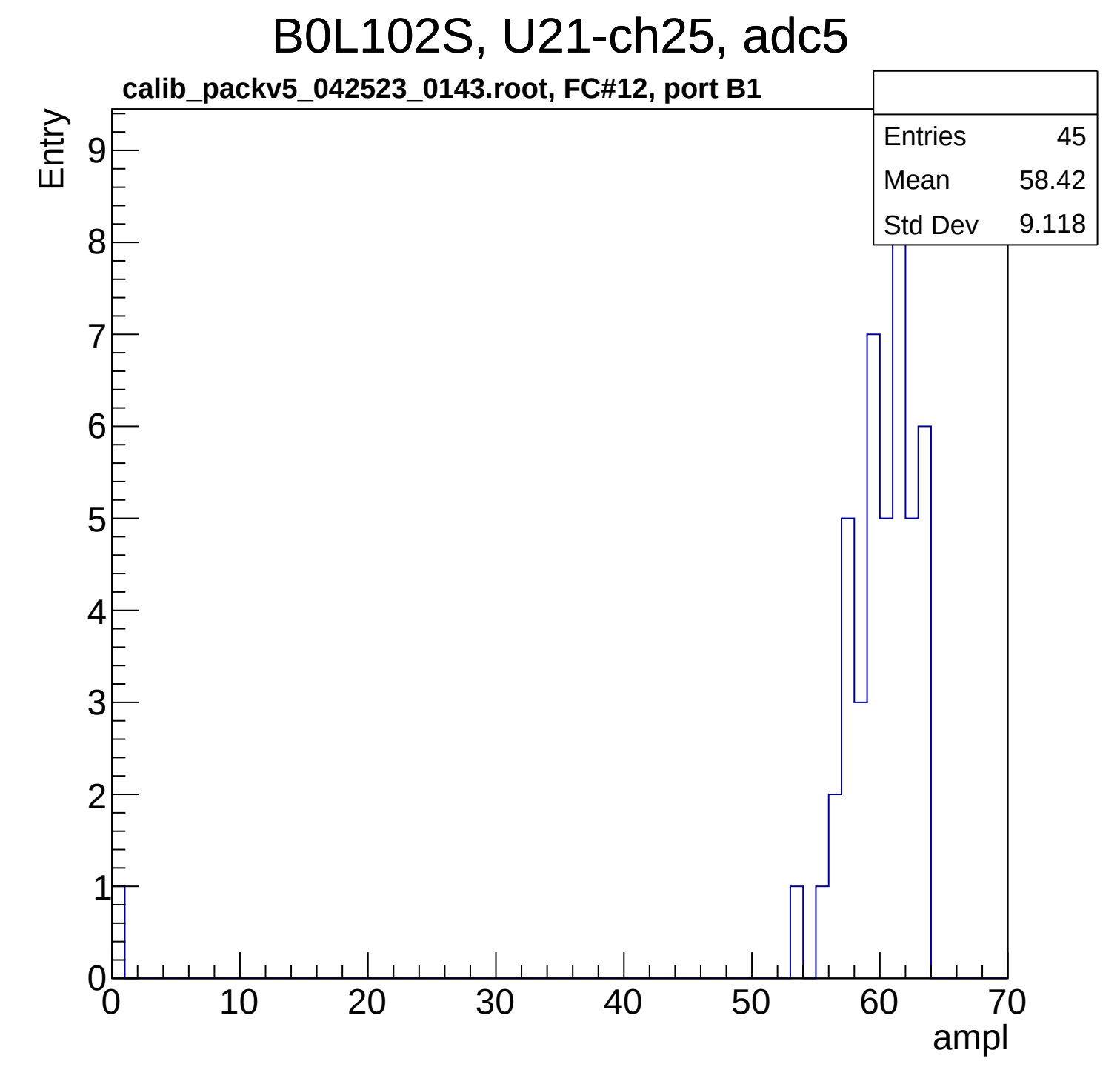
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch25, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.42
Std Dev 9.118
Entry
ampl
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5
6
7
8
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0
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60
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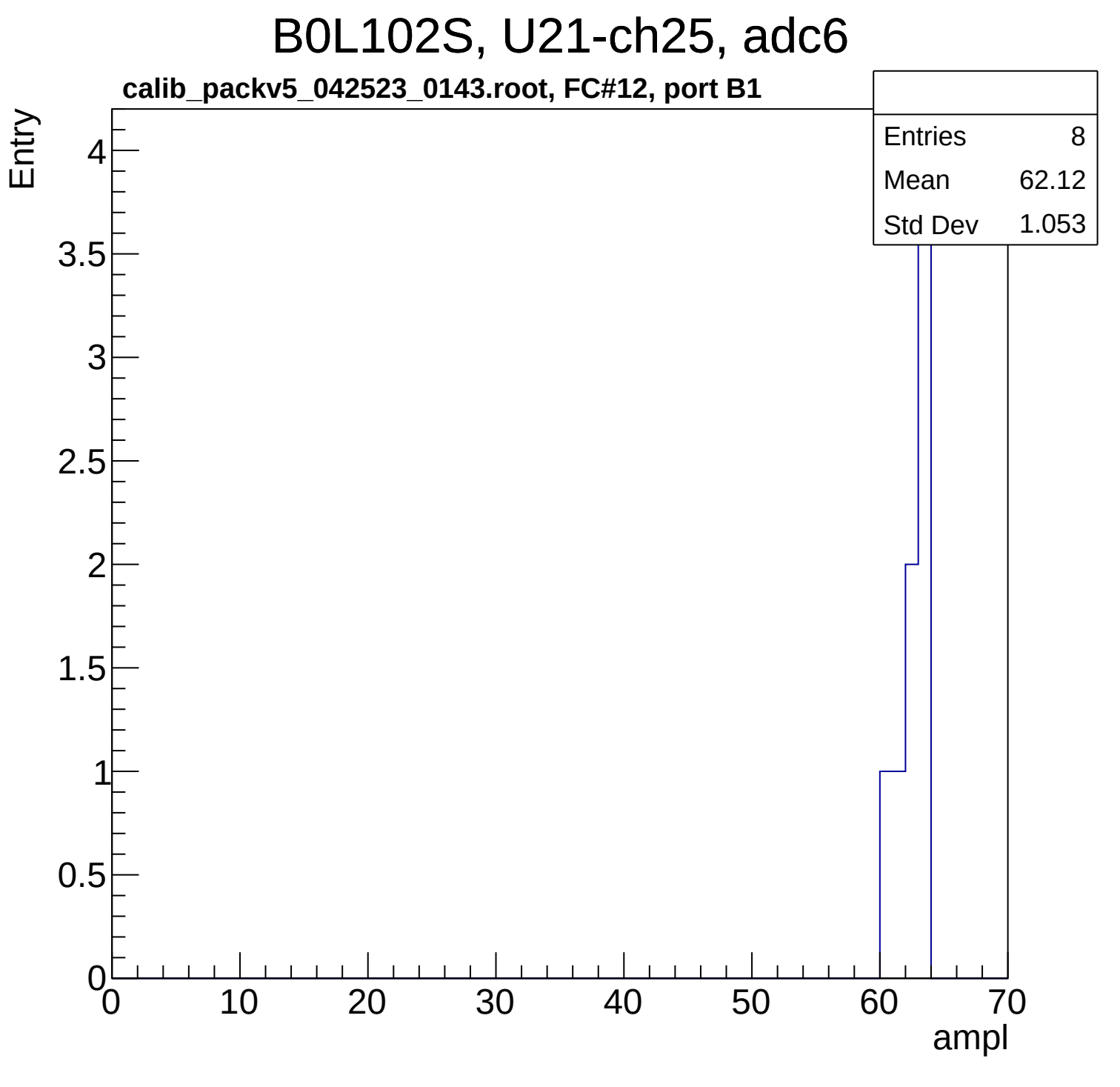
B0L102S, U21-ch25, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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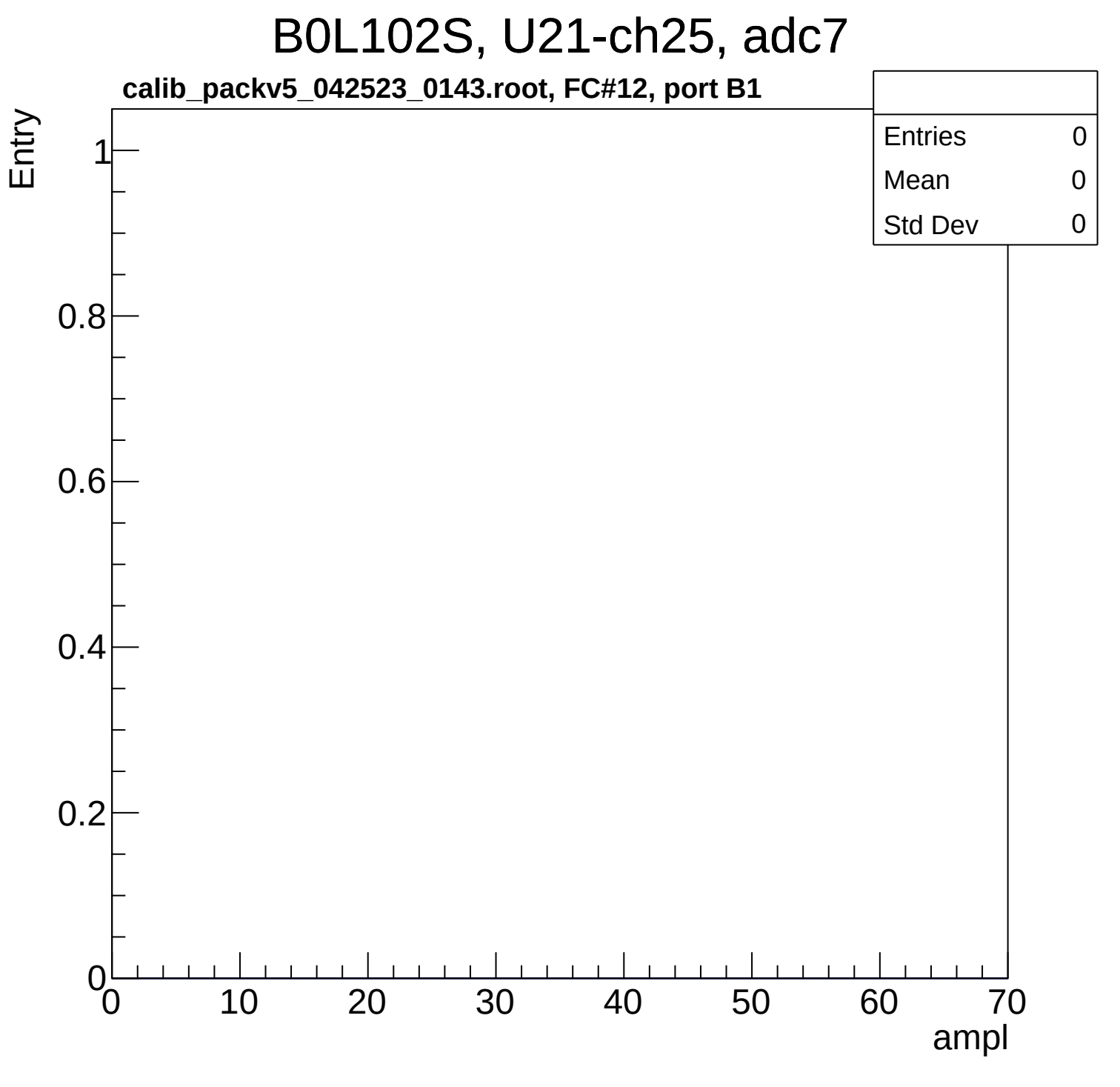
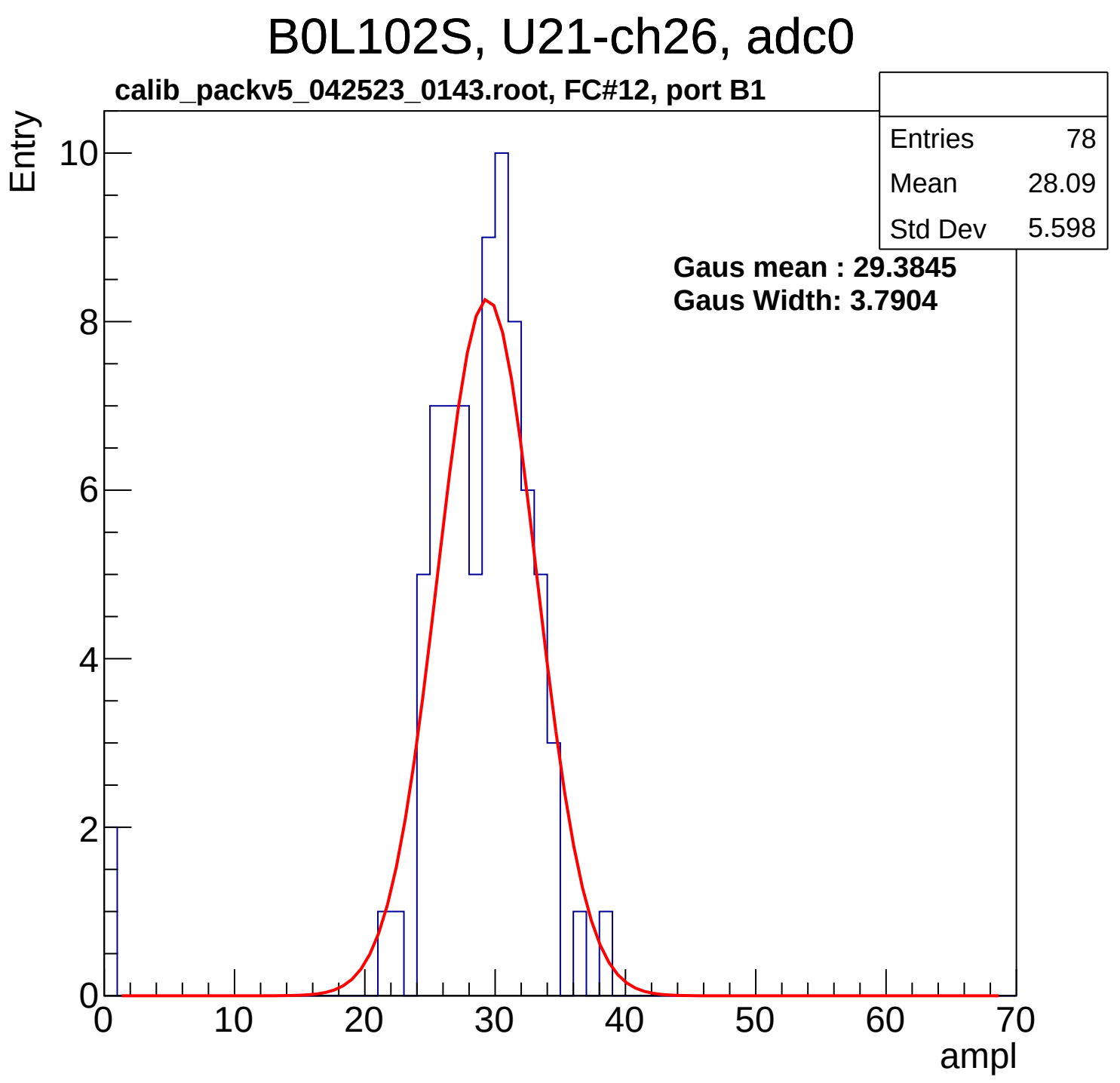
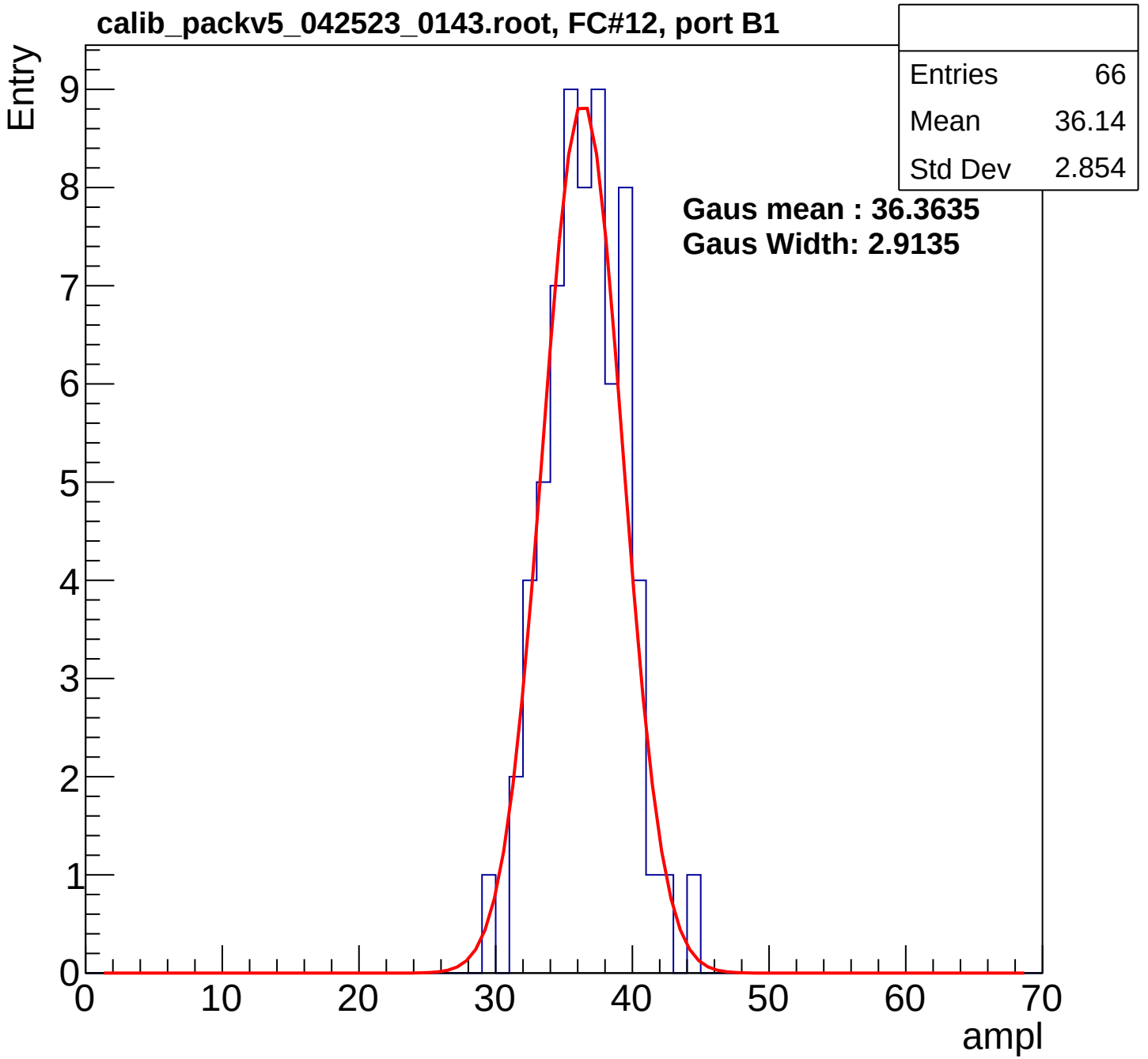
40
50
60
70


B0L102S, U21-ch25, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch26, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 28.09
Std Dev 5.598
Gaus mean : 29.3845
Gaus Width: 3.7904
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch26, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.14
Std Dev 2.854
Gaus mean : 36.3635
Gaus Width: 2.9135
Entry
ampl
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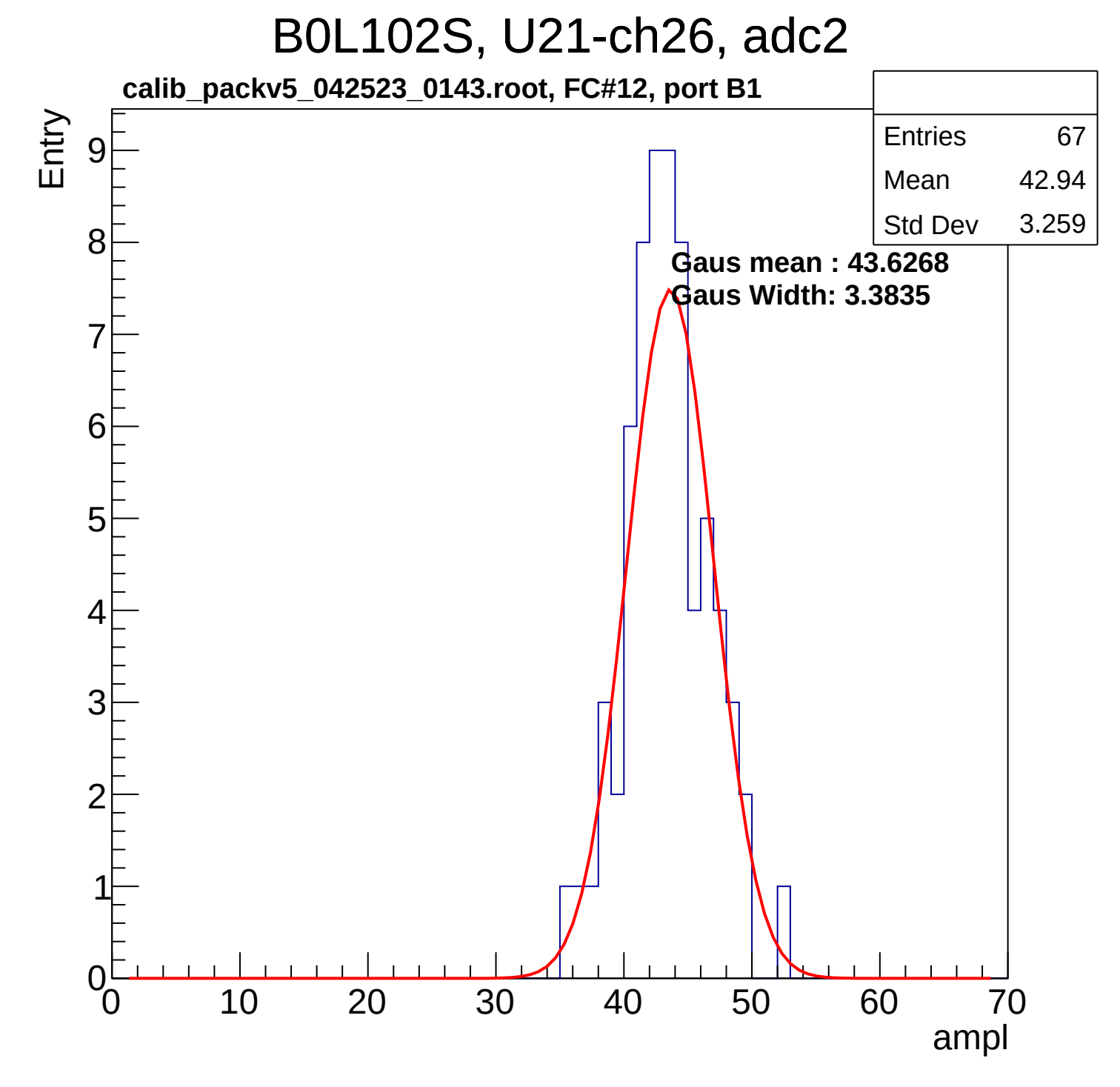
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch26, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 42.94
Std Dev 3.259
Gaus mean : 43.6268
Gaus Width: 3.3835
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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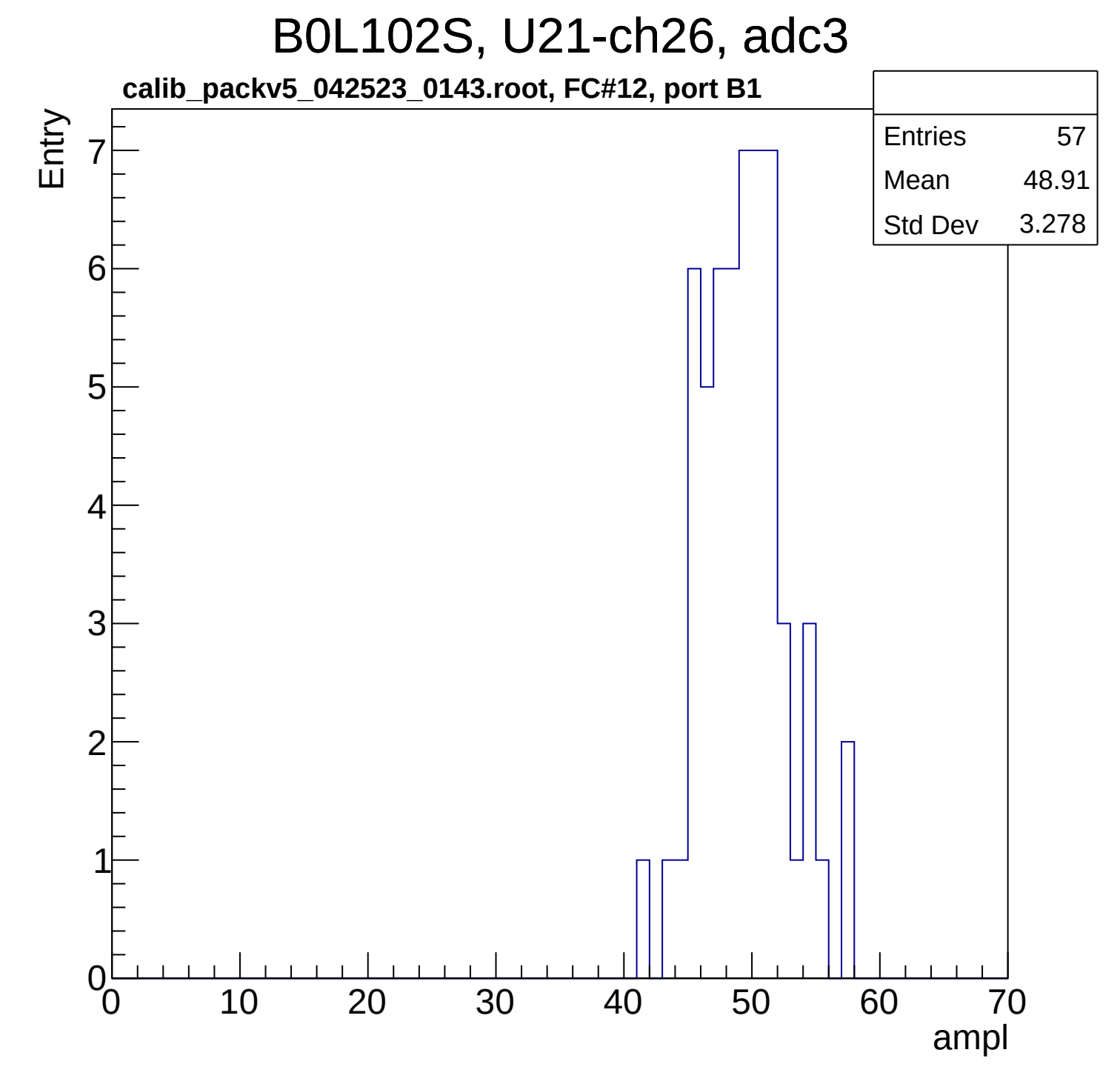
B0L102S, U21-ch26, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 48.91
Std Dev 3.278
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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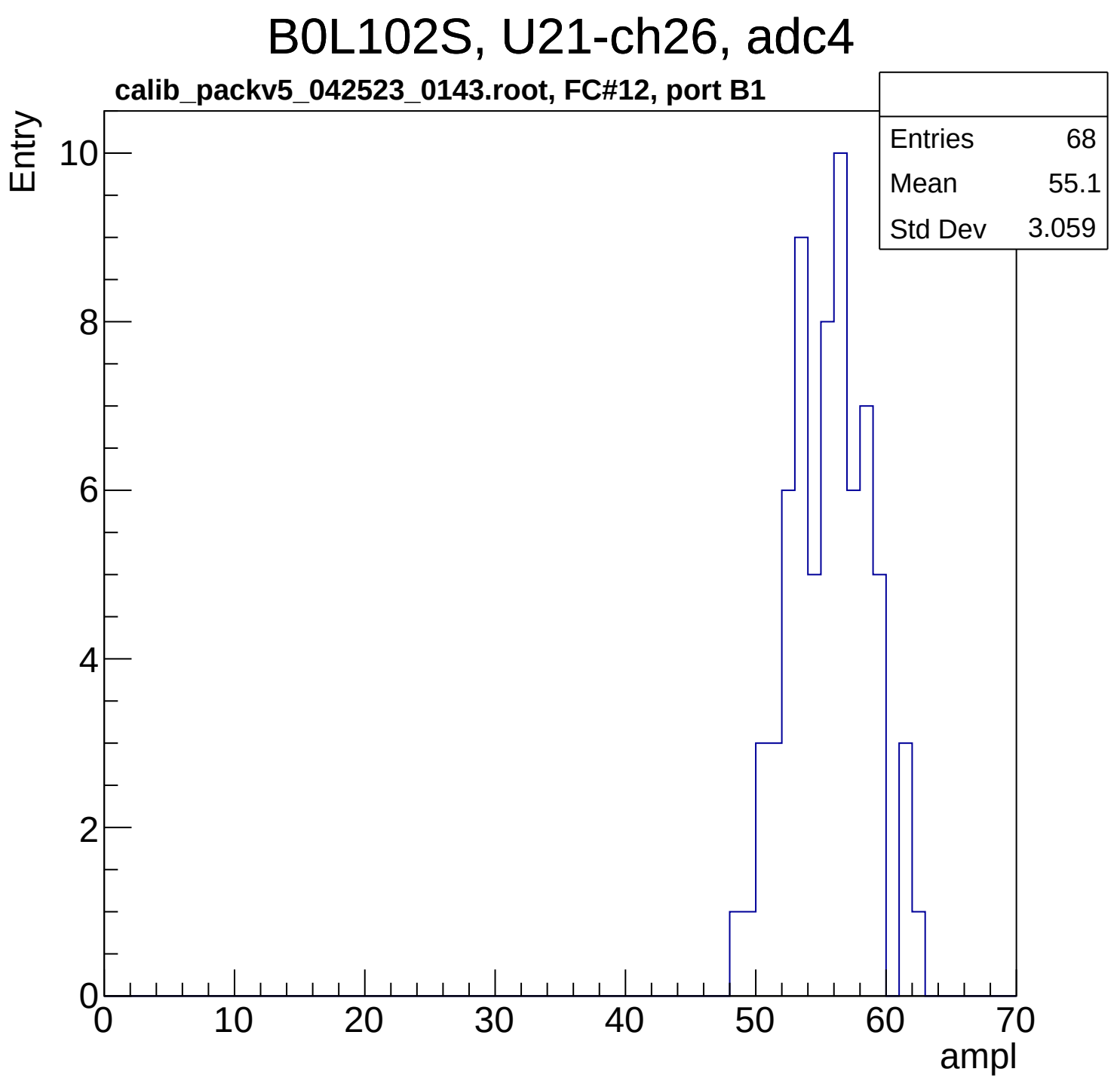
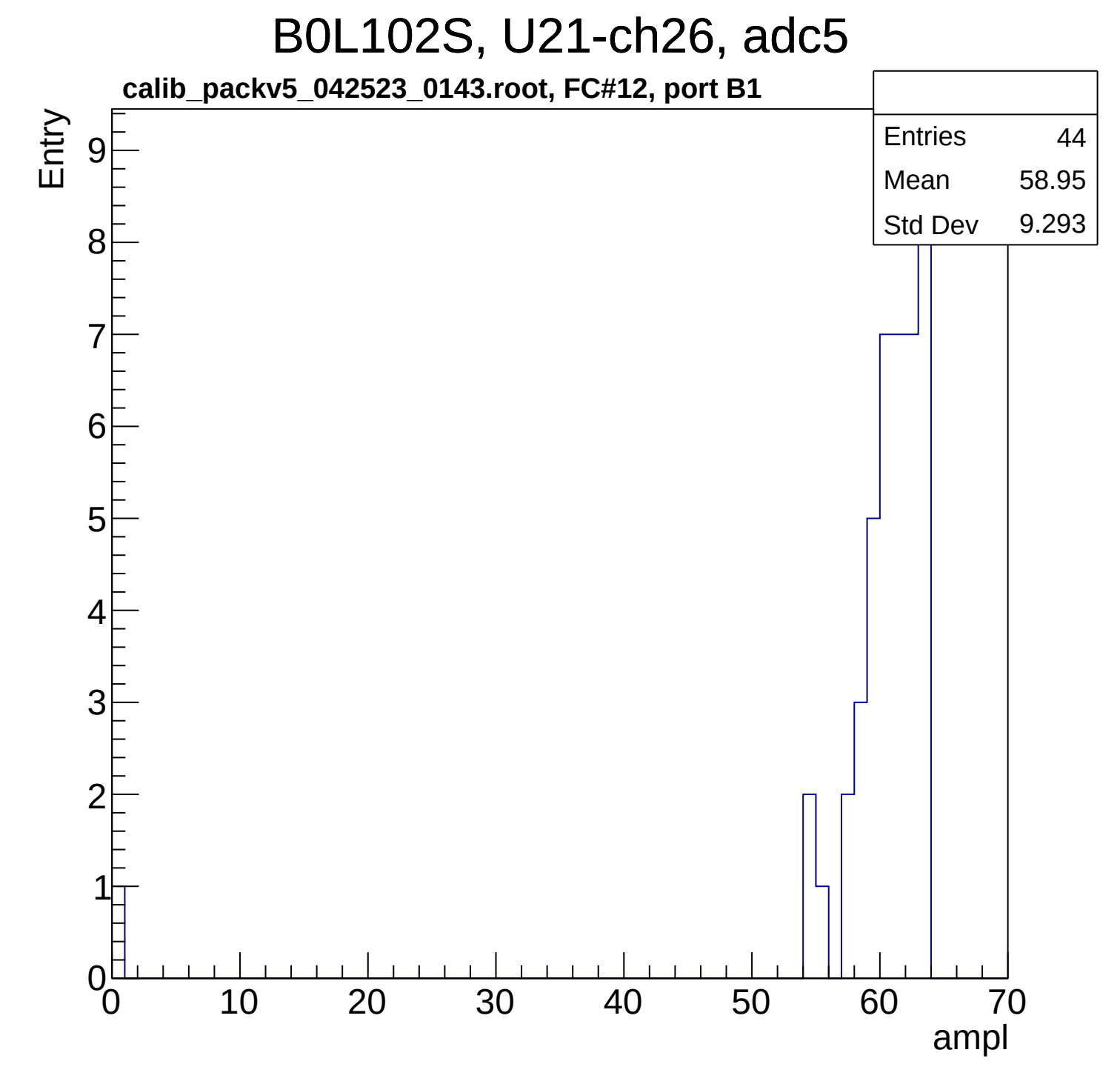

B0L102S, U21-ch26, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 55.1
Std Dev 3.059
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch26, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.95
Std Dev 9.293
Entry
ampl
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2
3
4
5
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7
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9
0
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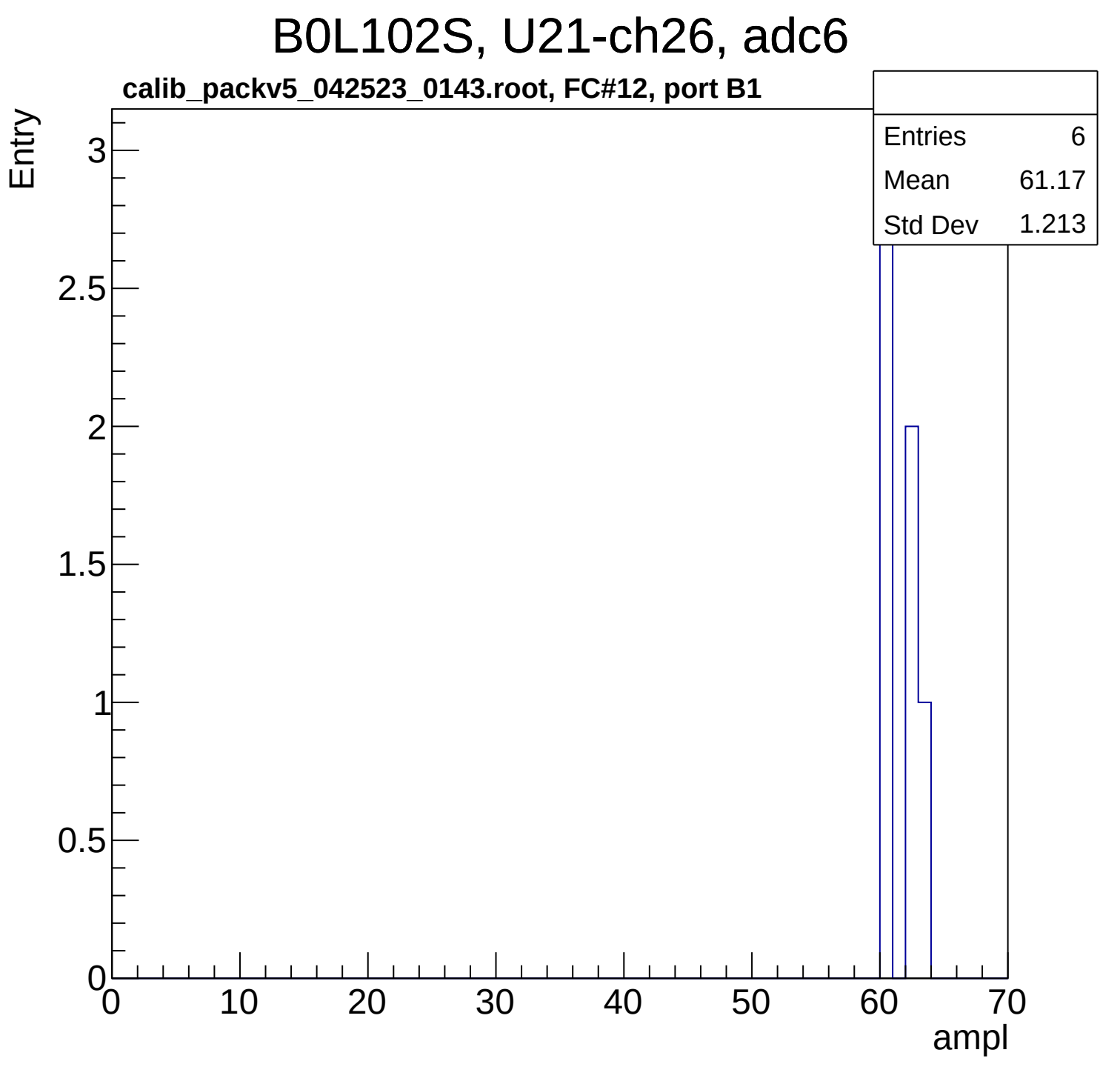
B0L102S, U21-ch26, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.17
Std Dev 1.213
Entry
ampl
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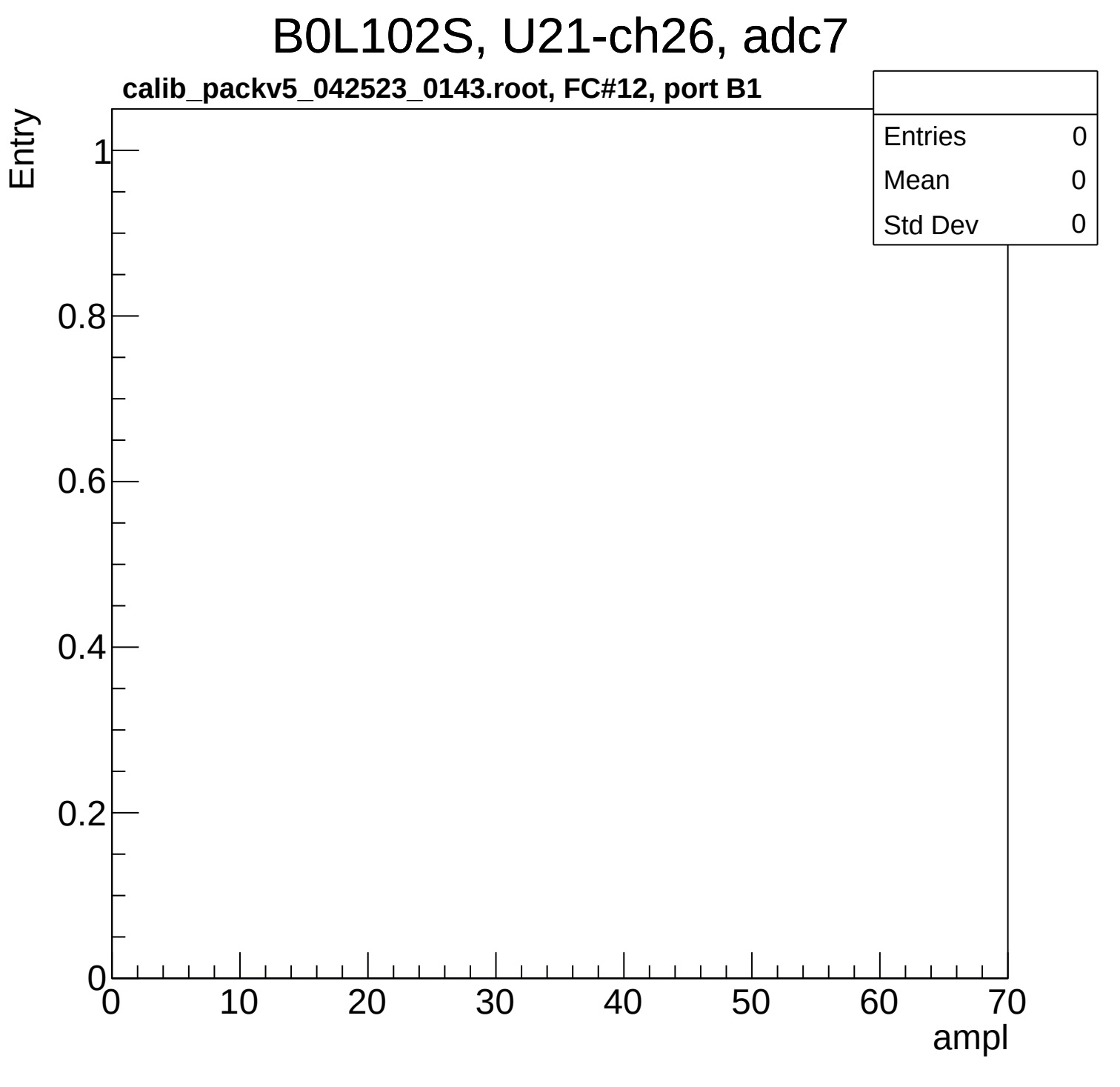
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch26, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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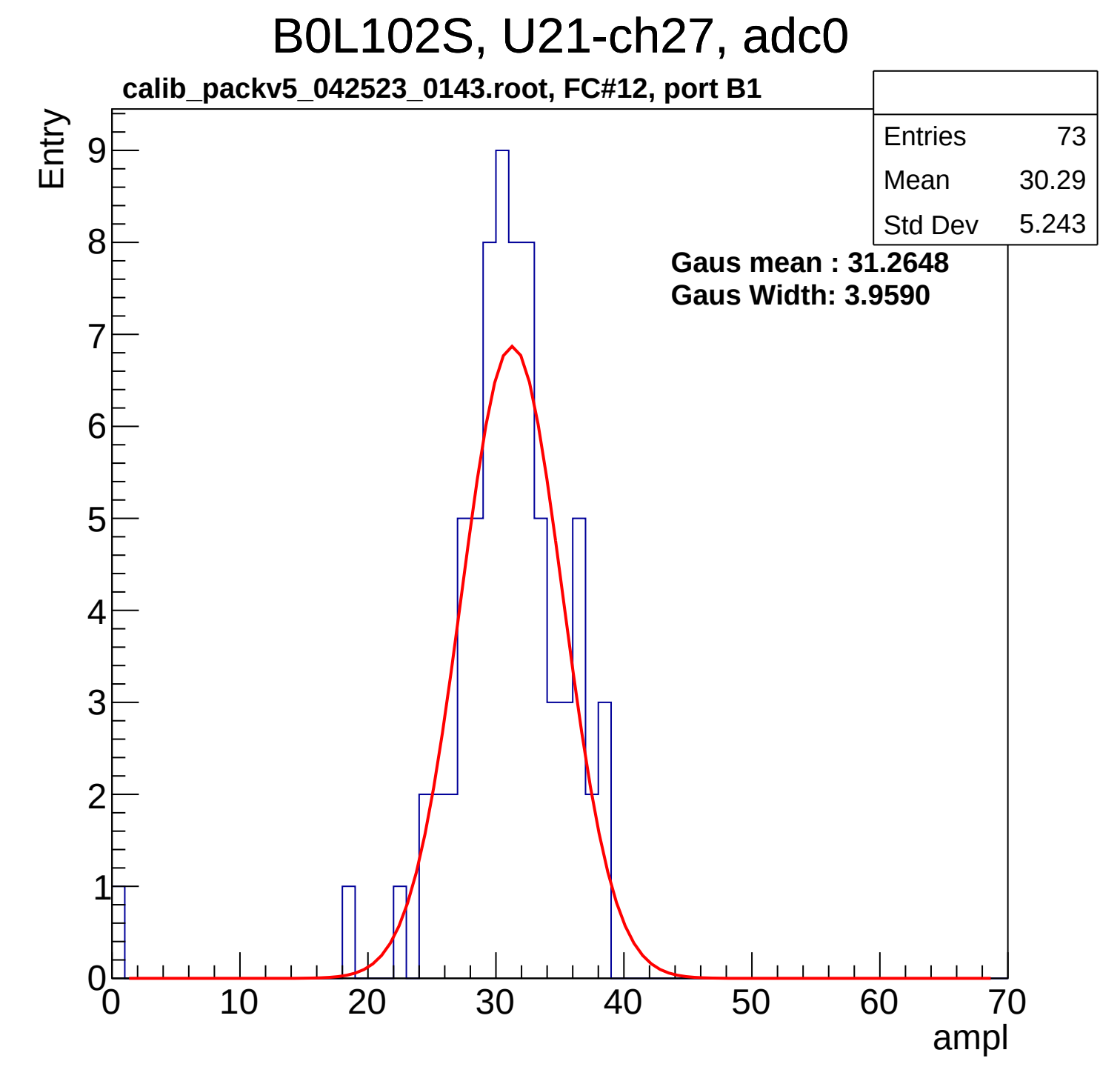
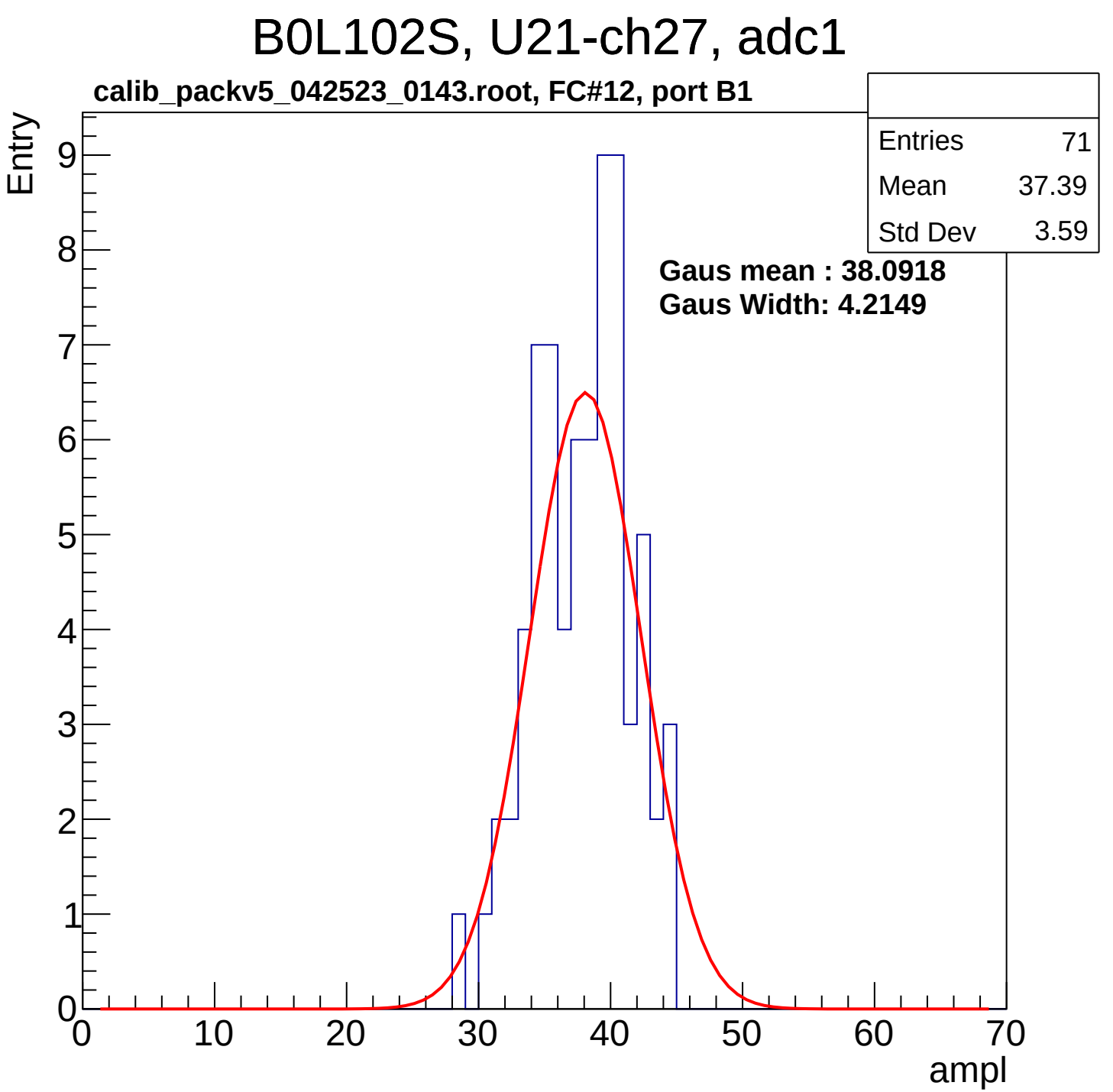
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch27, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 30.29
Std Dev 5.243
Gaus mean : 31.2648
Gaus Width: 3.9590
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch27, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 37.39
Std Dev 3.59
Gaus mean : 38.0918
Gaus Width: 4.2149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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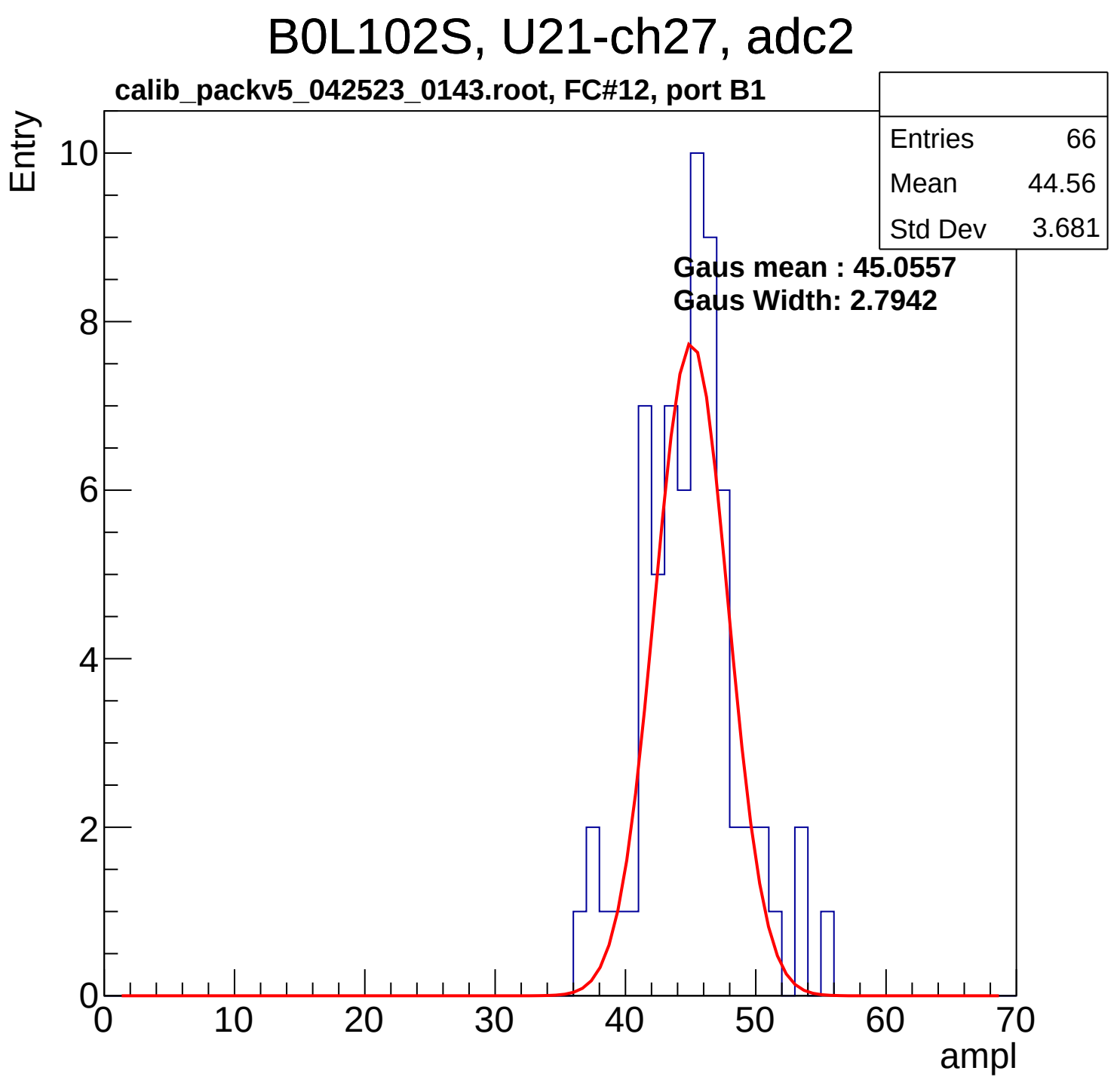
B0L102S, U21-ch27, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 44.56
Std Dev 3.681
Gaus mean : 45.0557
Gaus Width: 2.7942
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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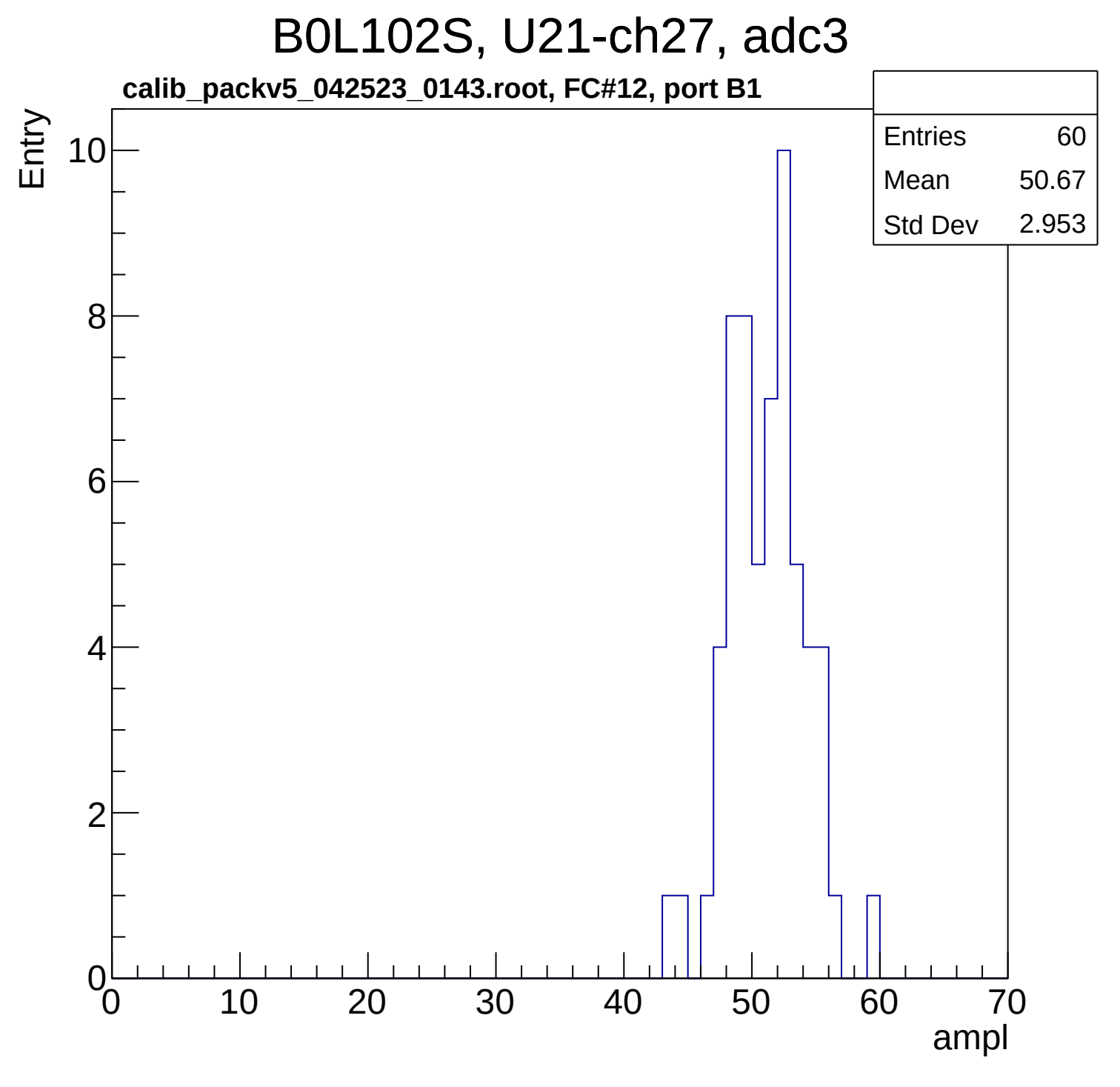
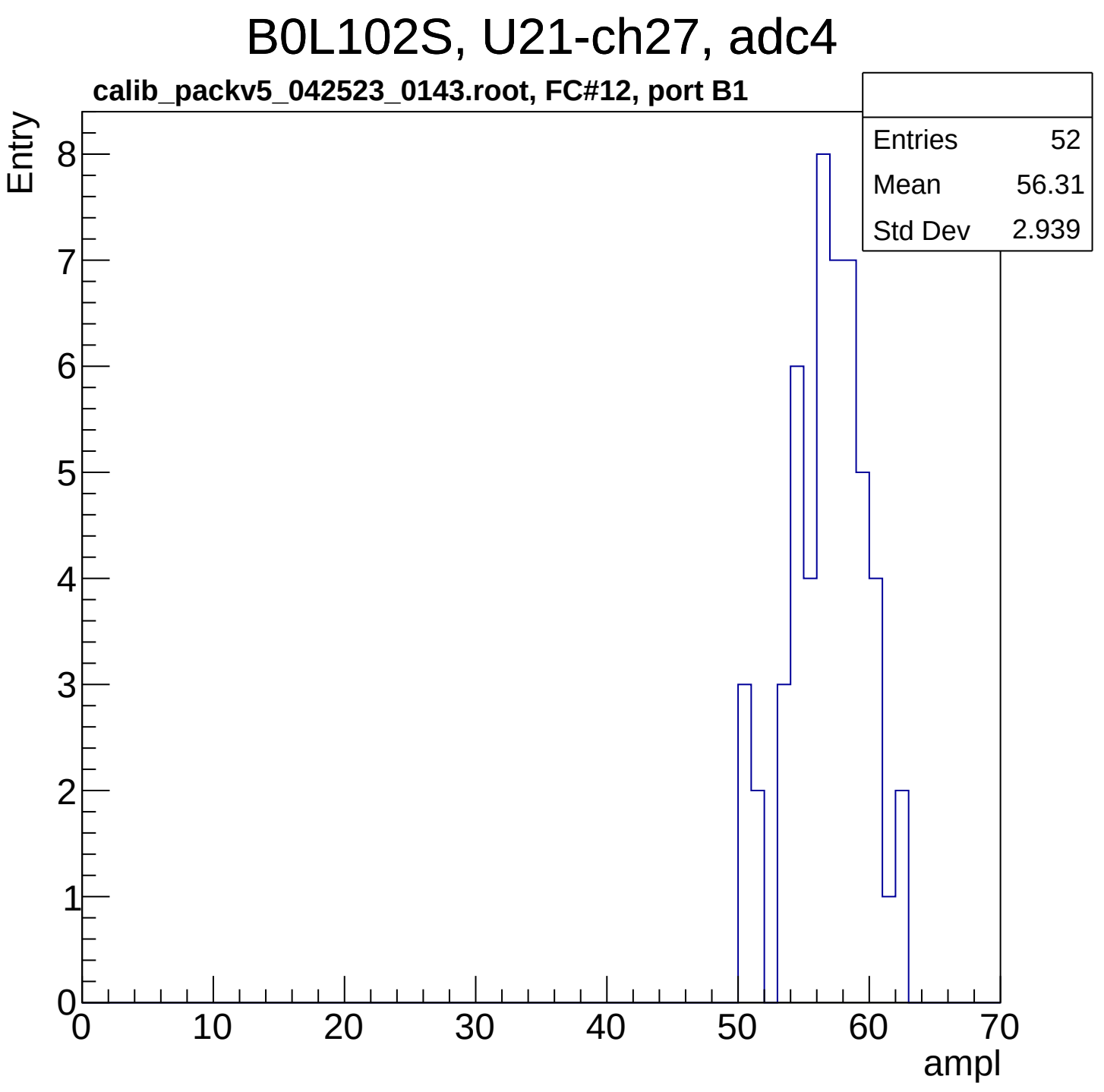
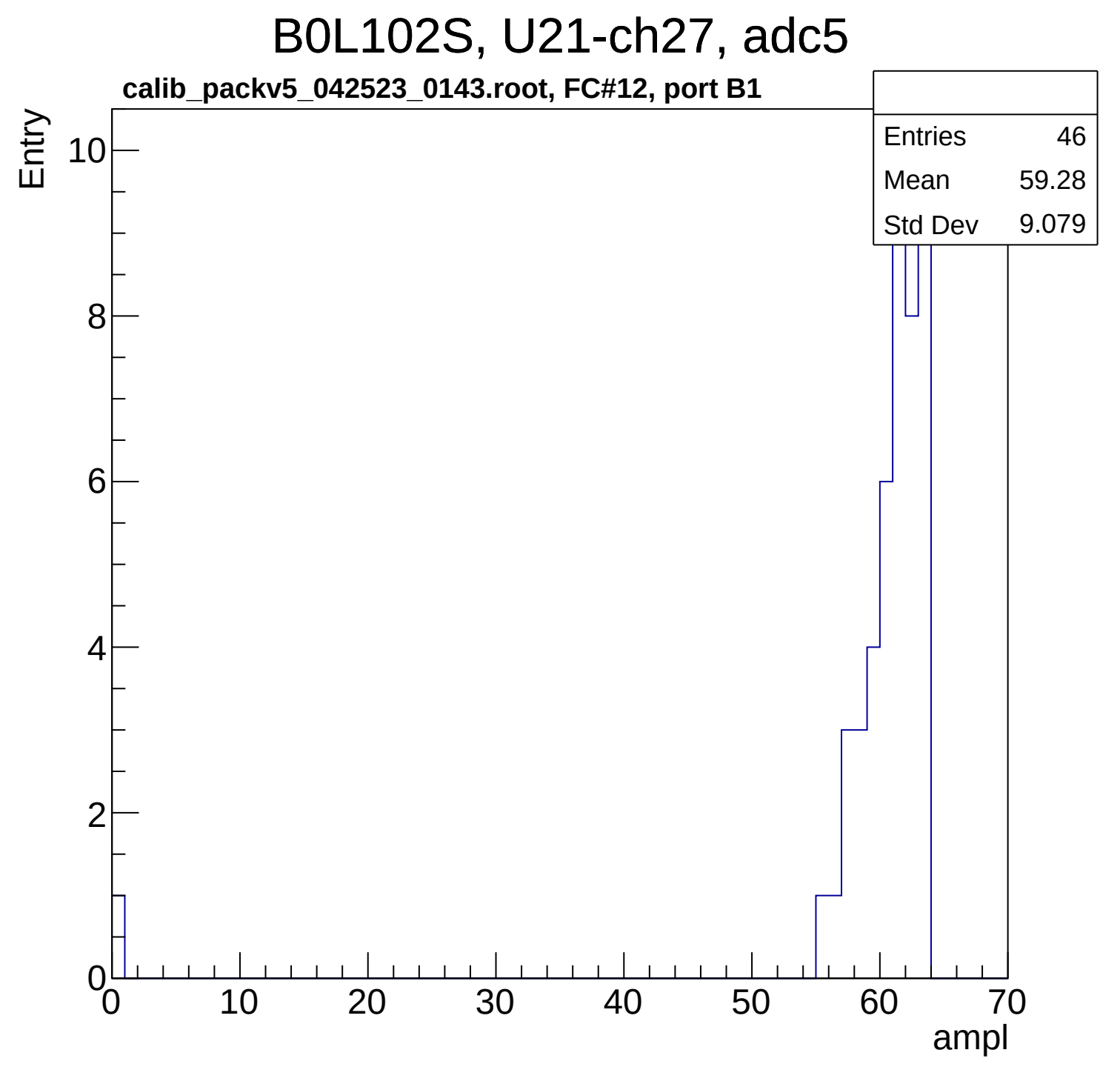
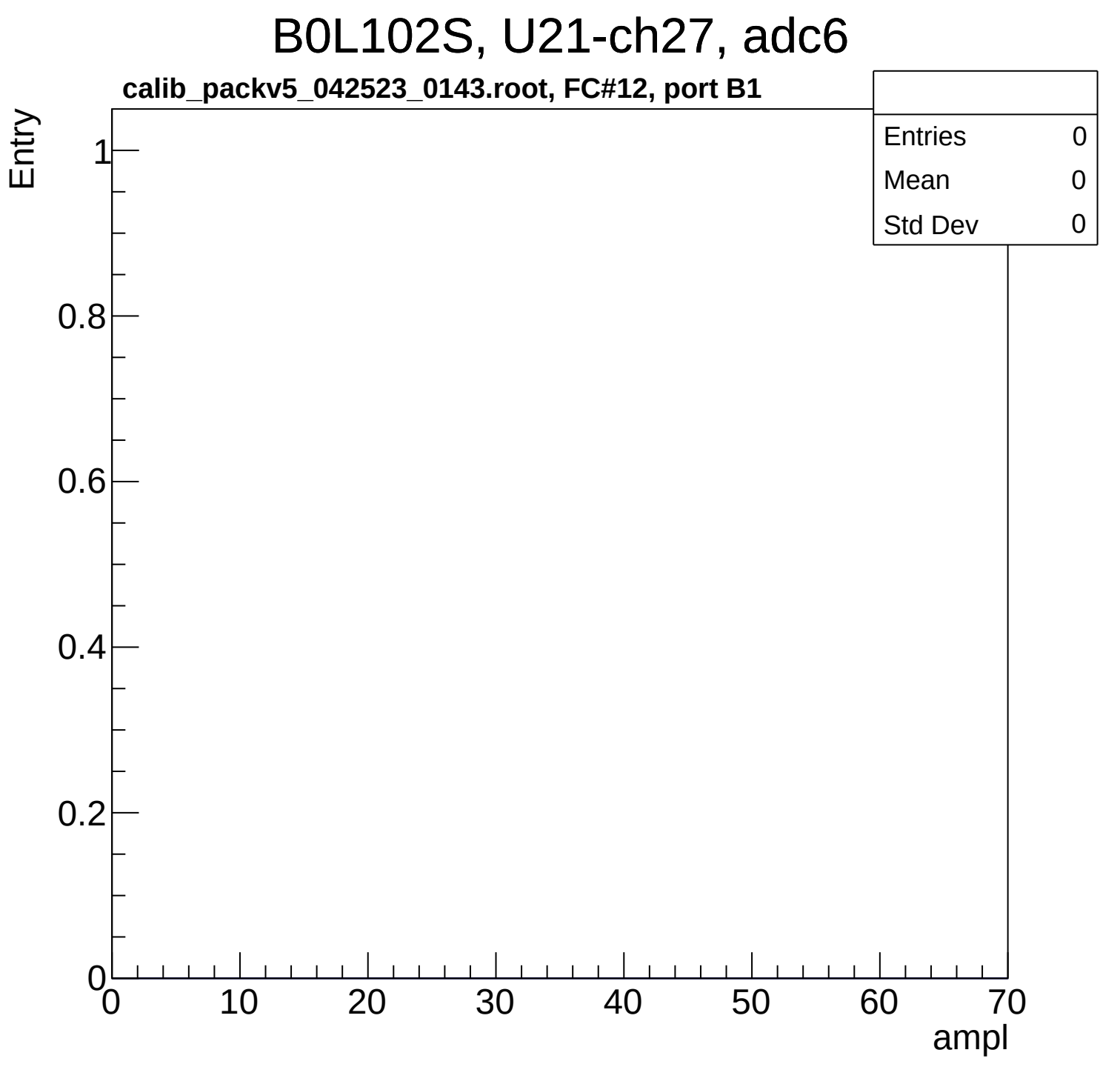
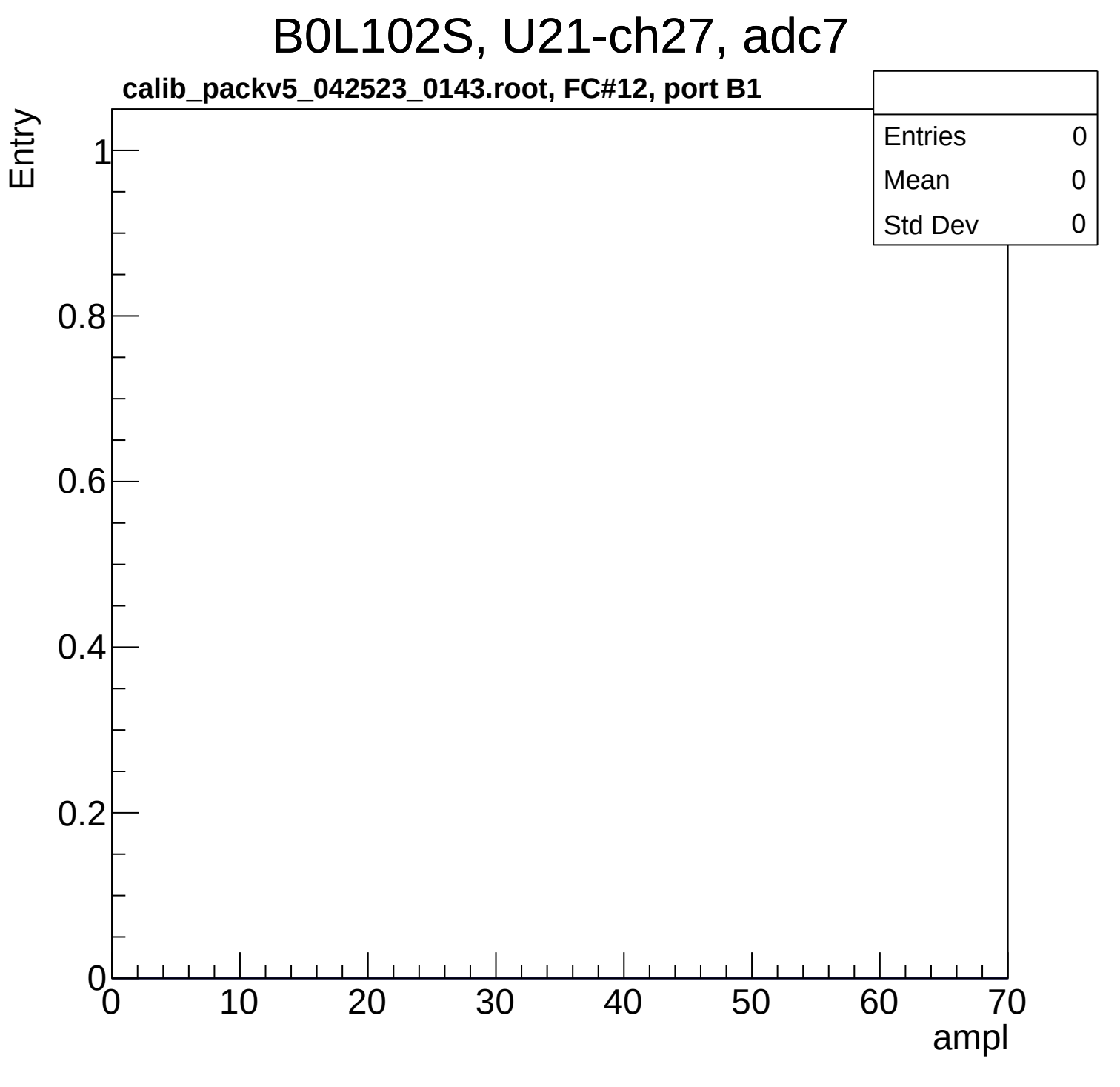
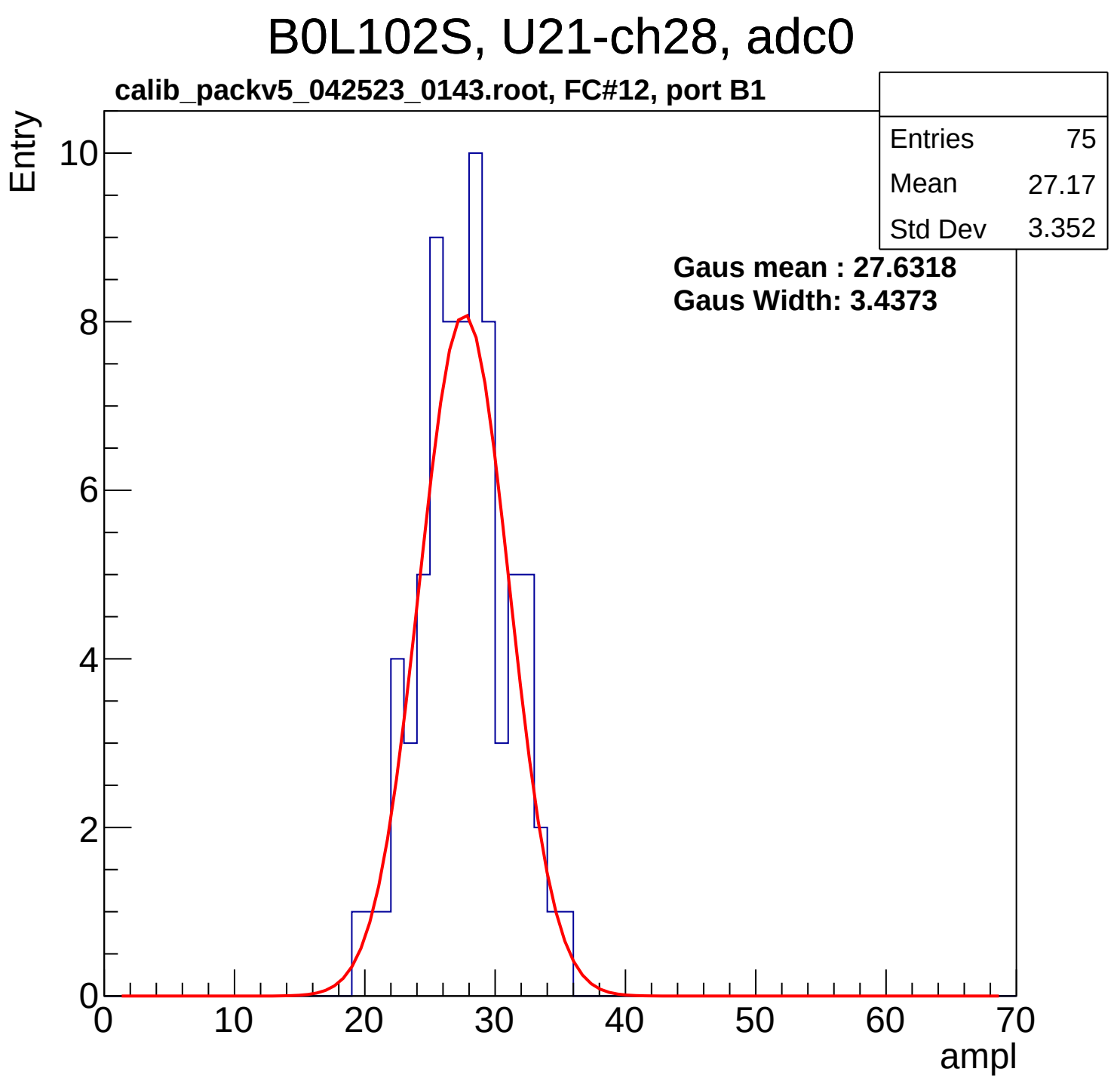

B0L102S, U21-ch27, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 50.67
Std Dev 2.953
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

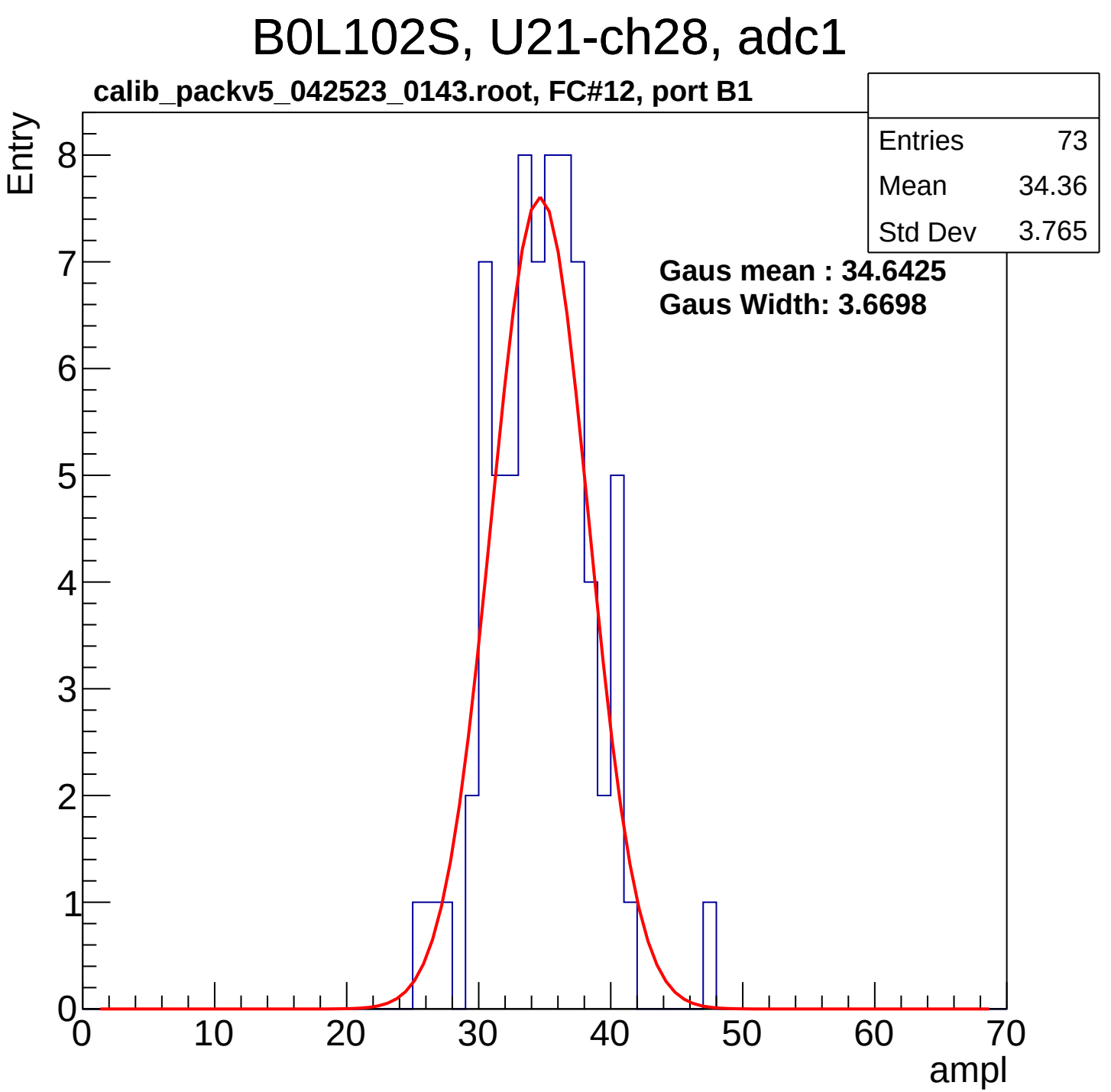

B0L102S, U21-ch27, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 56.31
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

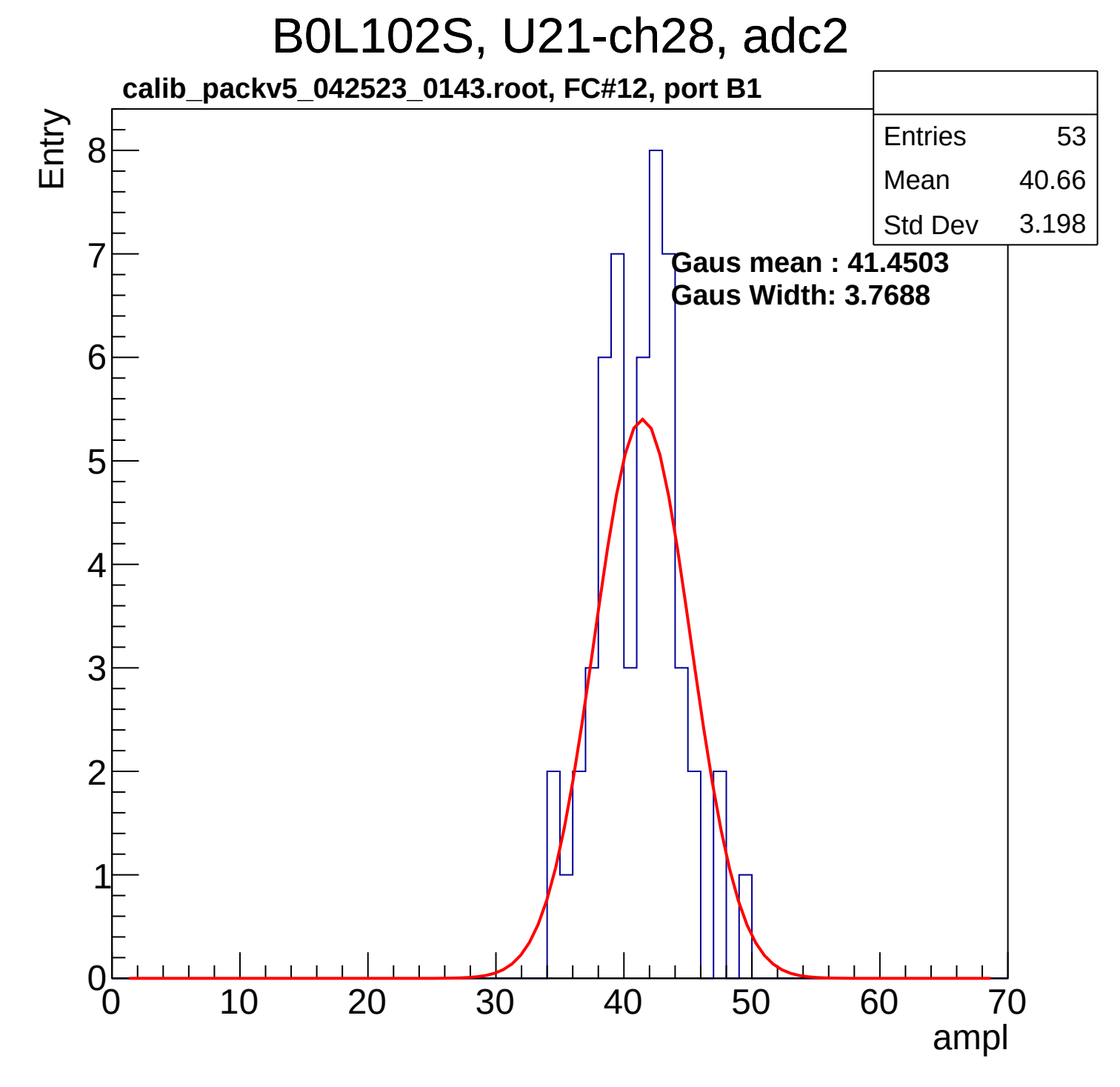

B0L102S, U21-ch27, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.28
Std Dev 9.079
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

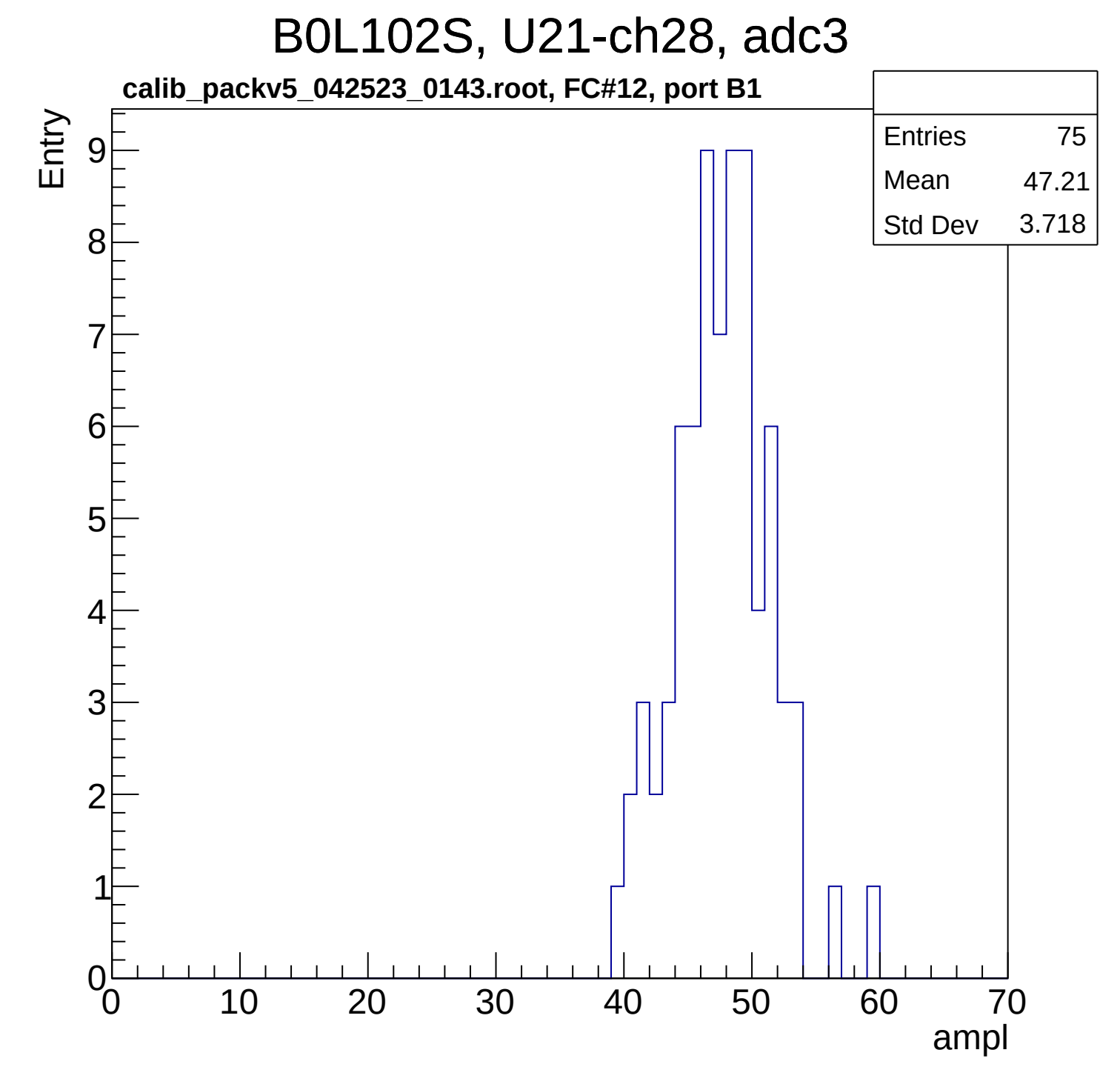

B0L102S, U21-ch27, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch27, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch28, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 27.17
Std Dev 3.352
Gaus mean : 27.6318
Gaus Width: 3.4373
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch28, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 34.36
Std Dev 3.765
Gaus mean : 34.6425
Gaus Width: 3.6698
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch28, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 40.66
Std Dev 3.198
Gaus mean : 41.4503
Gaus Width: 3.7688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch28, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 47.21
Std Dev 3.718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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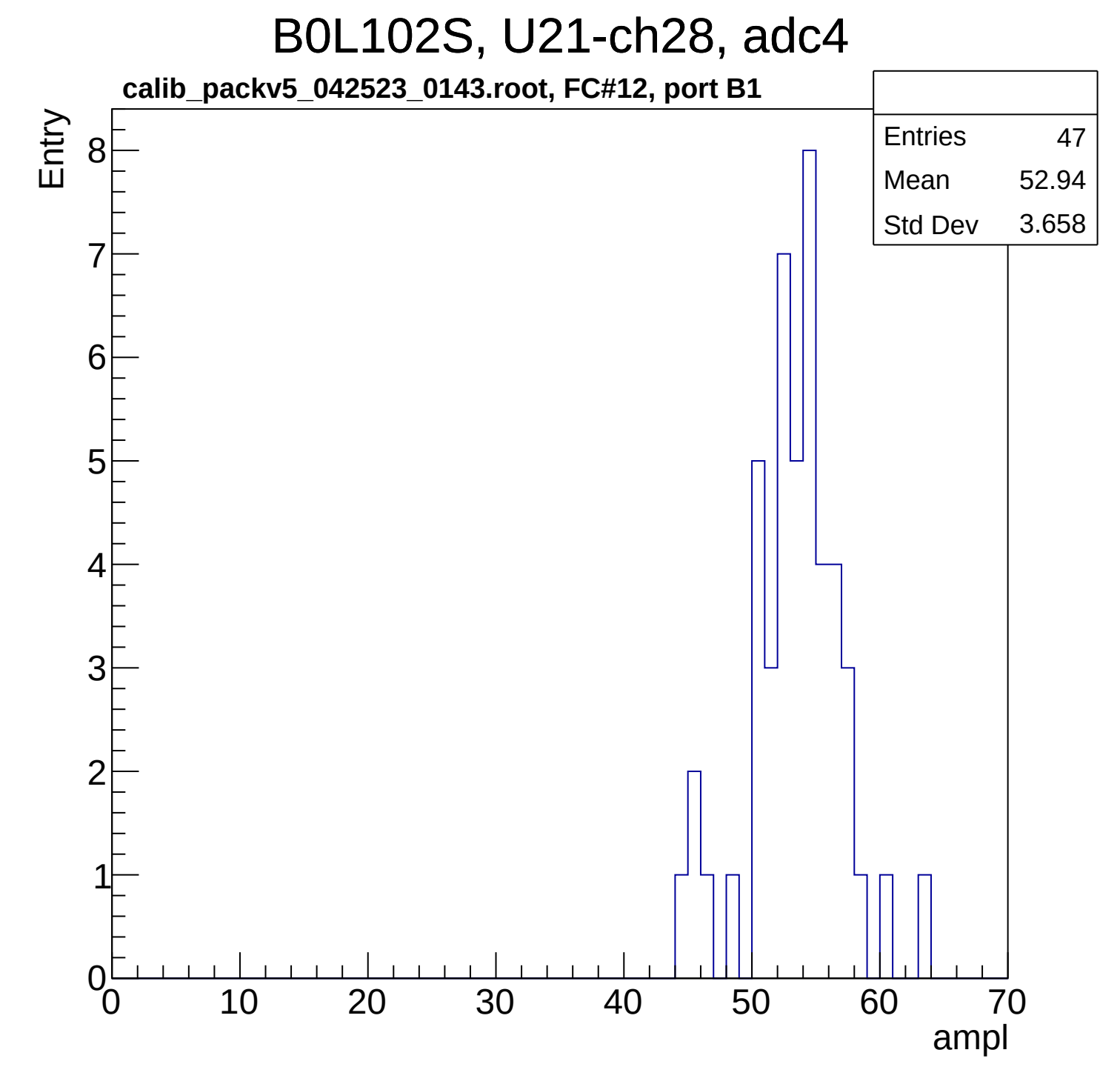
B0L102S, U21-ch28, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 52.94
Std Dev 3.658
Entry
ampl
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2
3
4
5
6
7
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0
10
20
30
40
50
60
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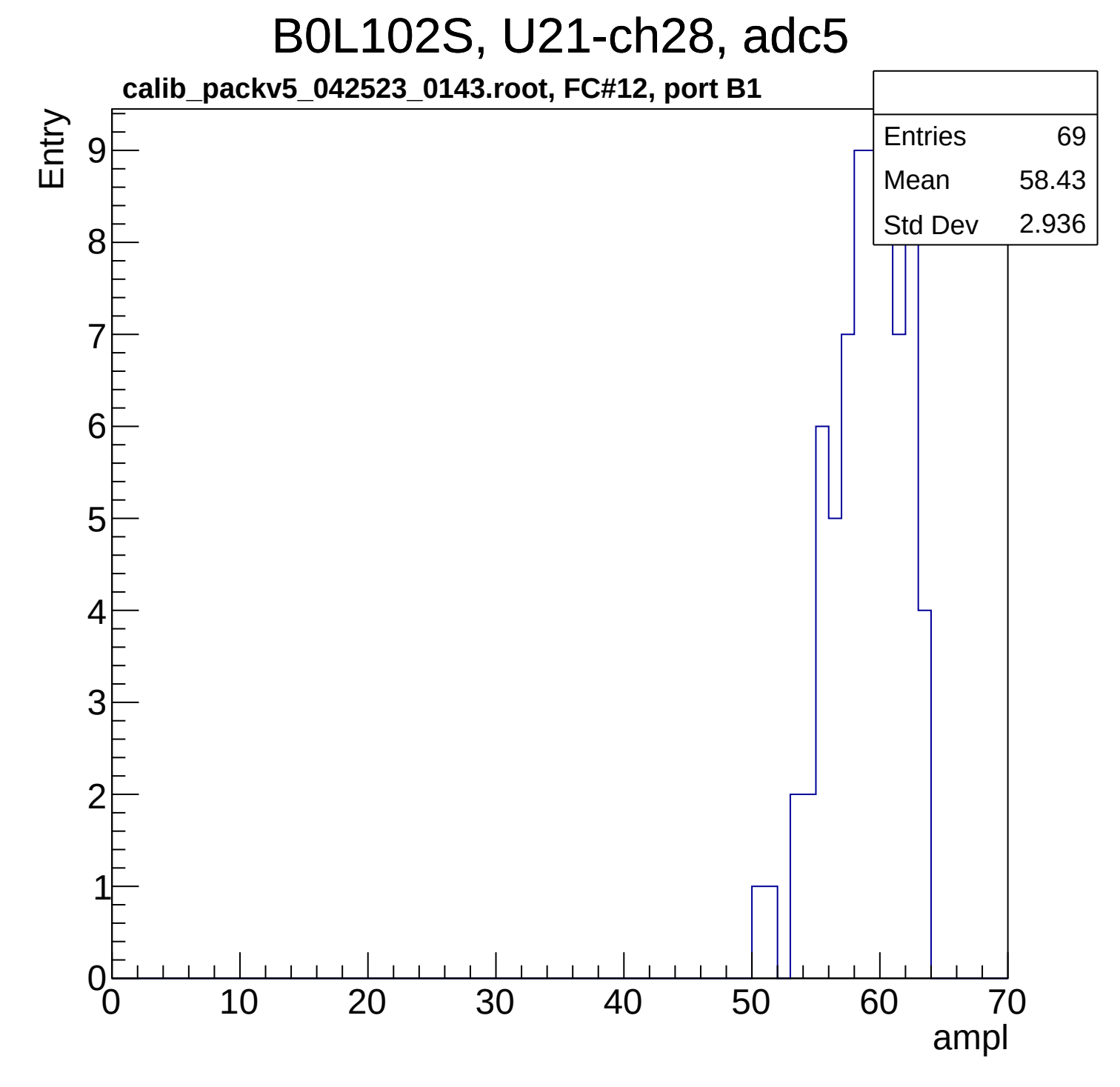
B0L102S, U21-ch28, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 58.43
Std Dev 2.936
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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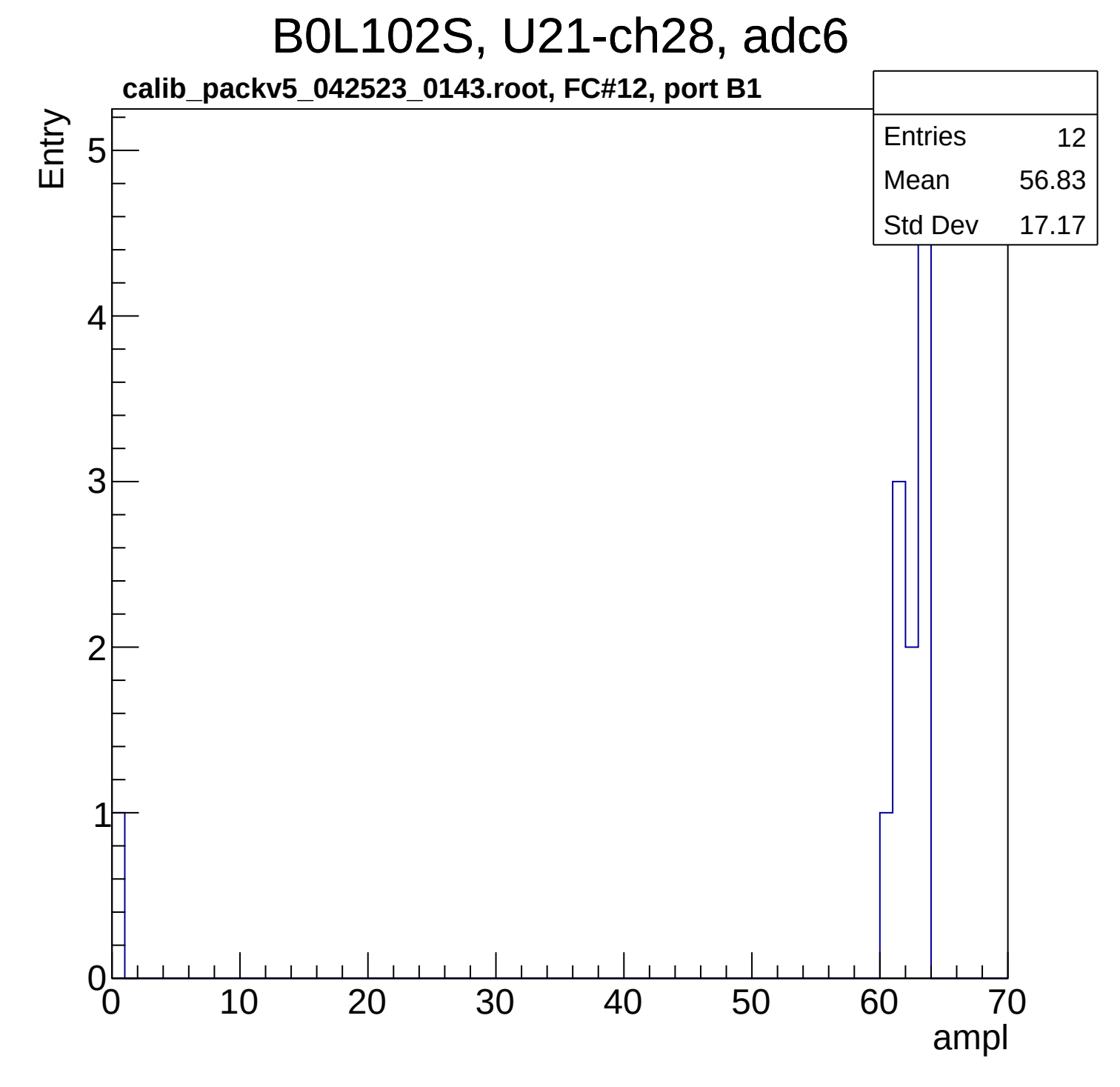
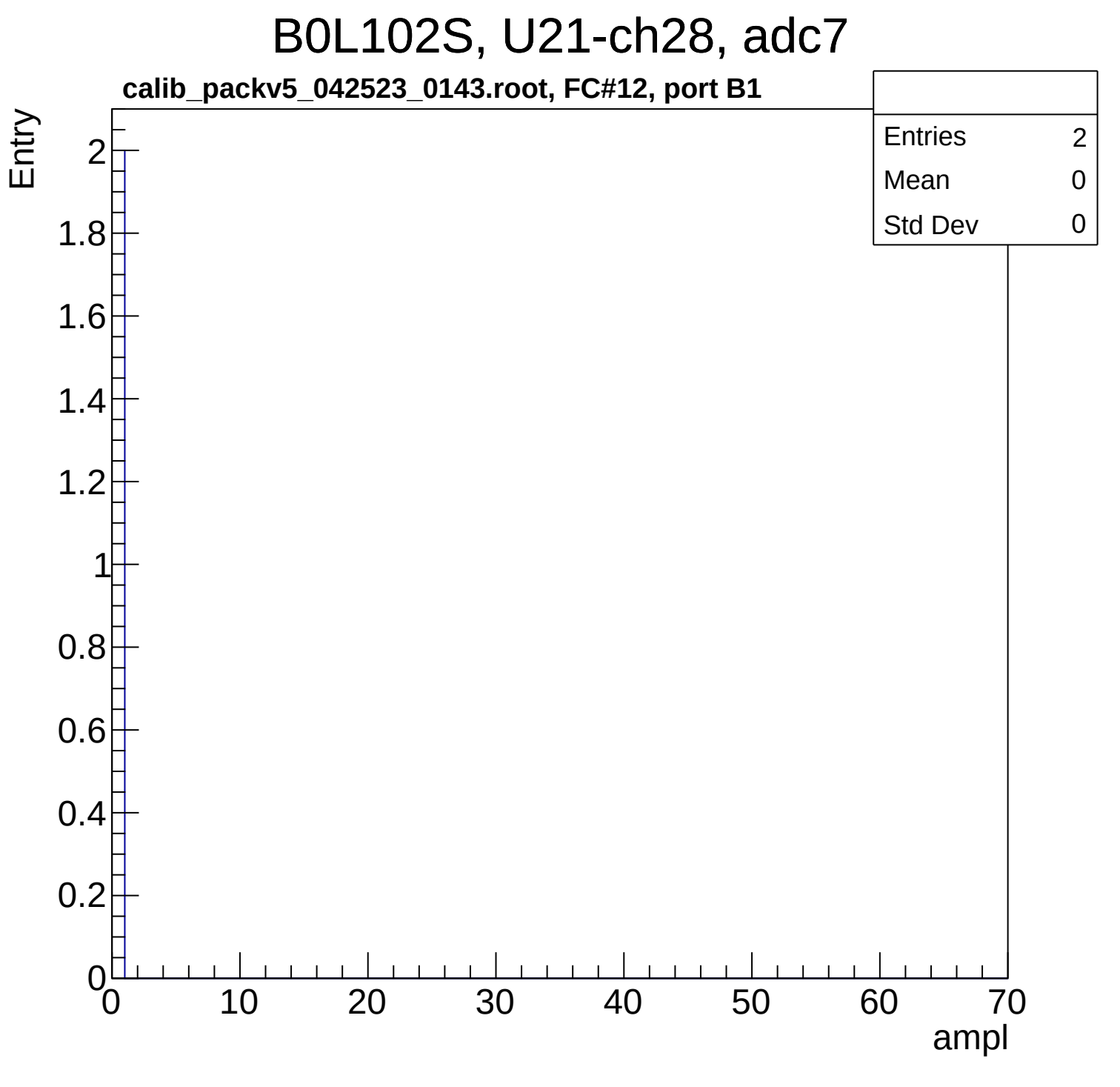
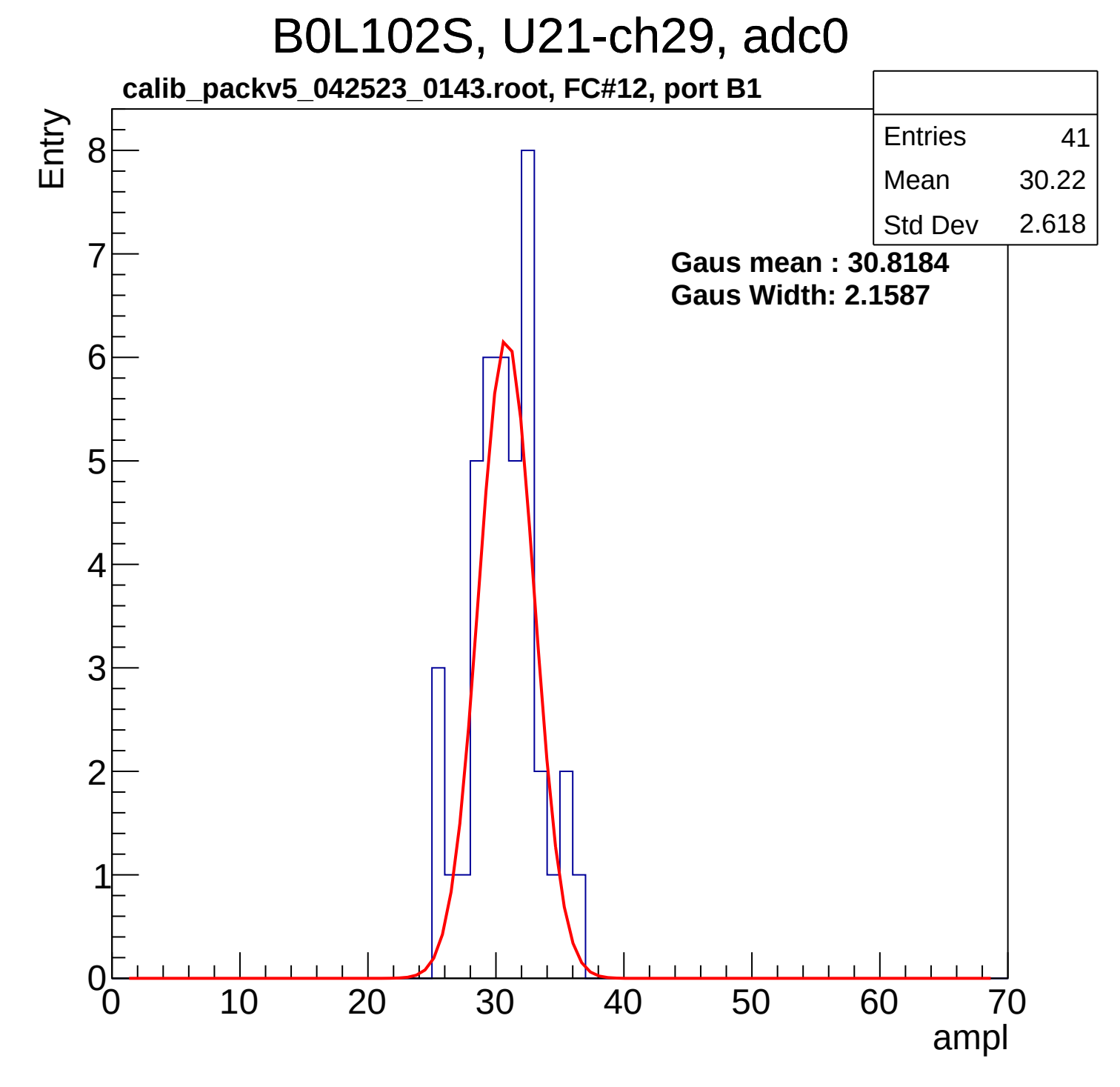
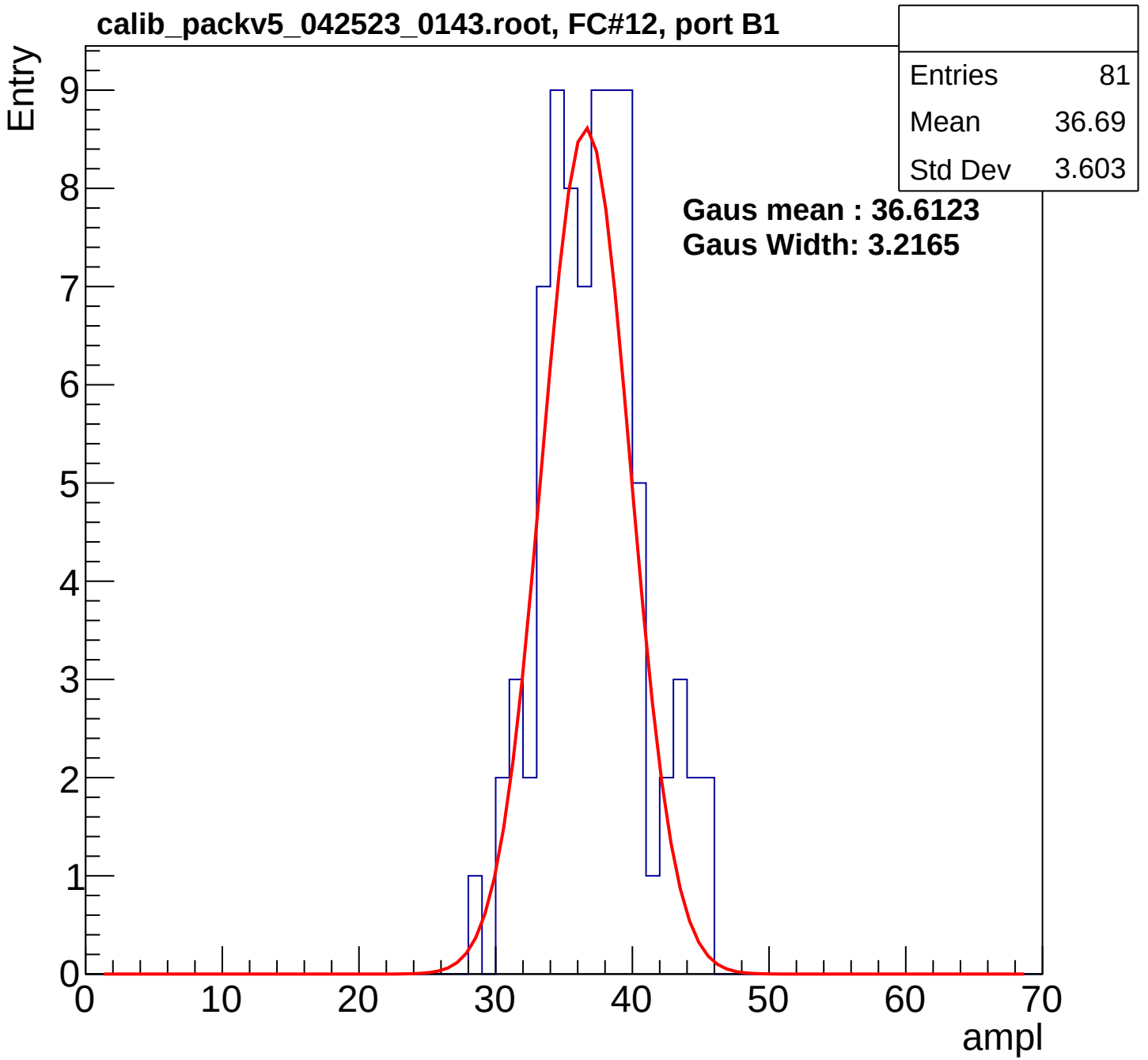

B0L102S, U21-ch28, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch28, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch29, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 30.22
Std Dev 2.618
Gaus mean : 30.8184
Gaus Width: 2.1587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch29, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 36.69
Std Dev 3.603
Gaus mean : 36.6123
Gaus Width: 3.2165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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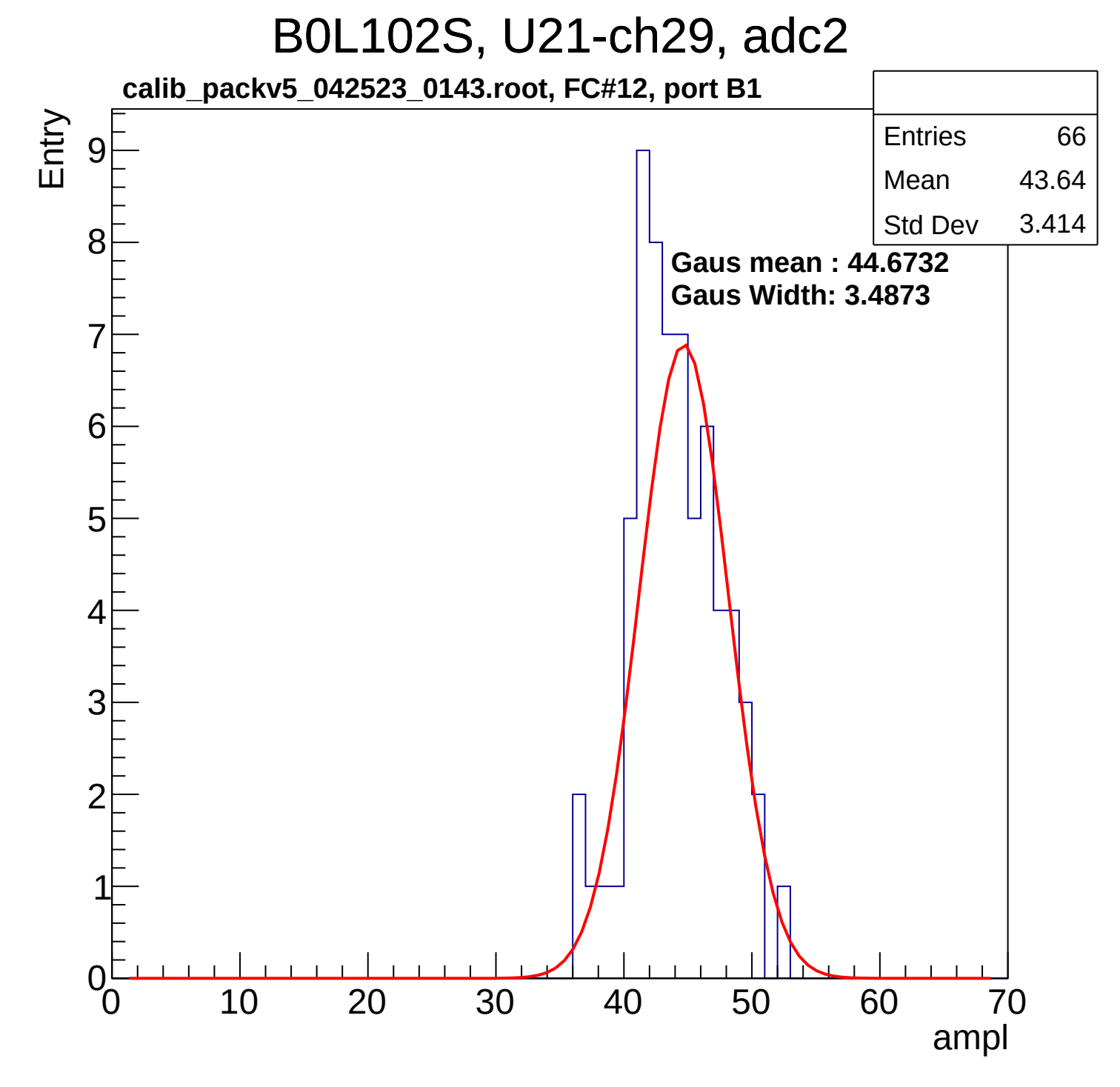
B0L102S, U21-ch29, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.64
Std Dev 3.414
Gaus mean : 44.6732
Gaus Width: 3.4873
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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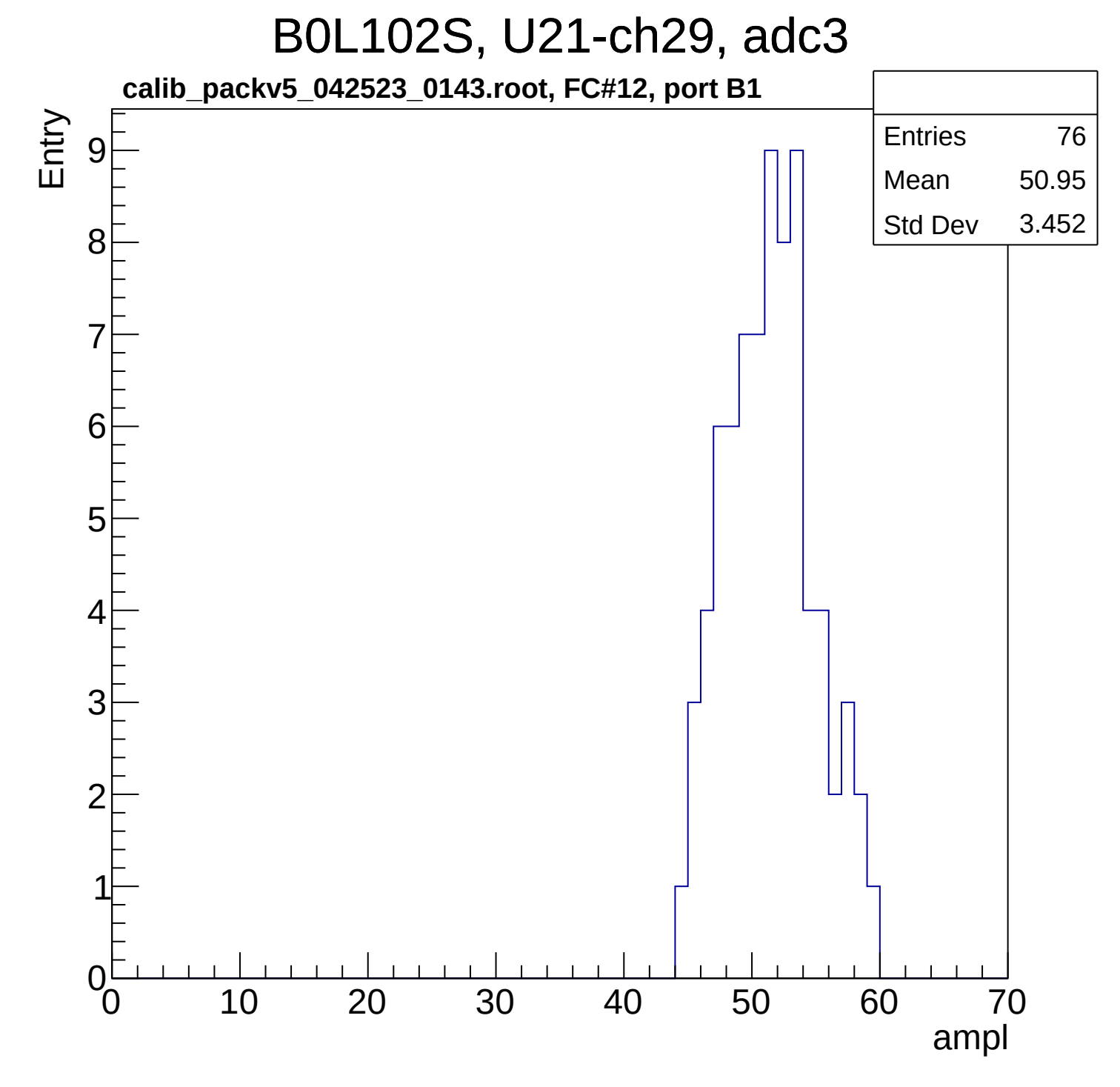
B0L102S, U21-ch29, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 50.95
Std Dev 3.452
Entry
ampl
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2
3
4
5
6
7
8
9
0
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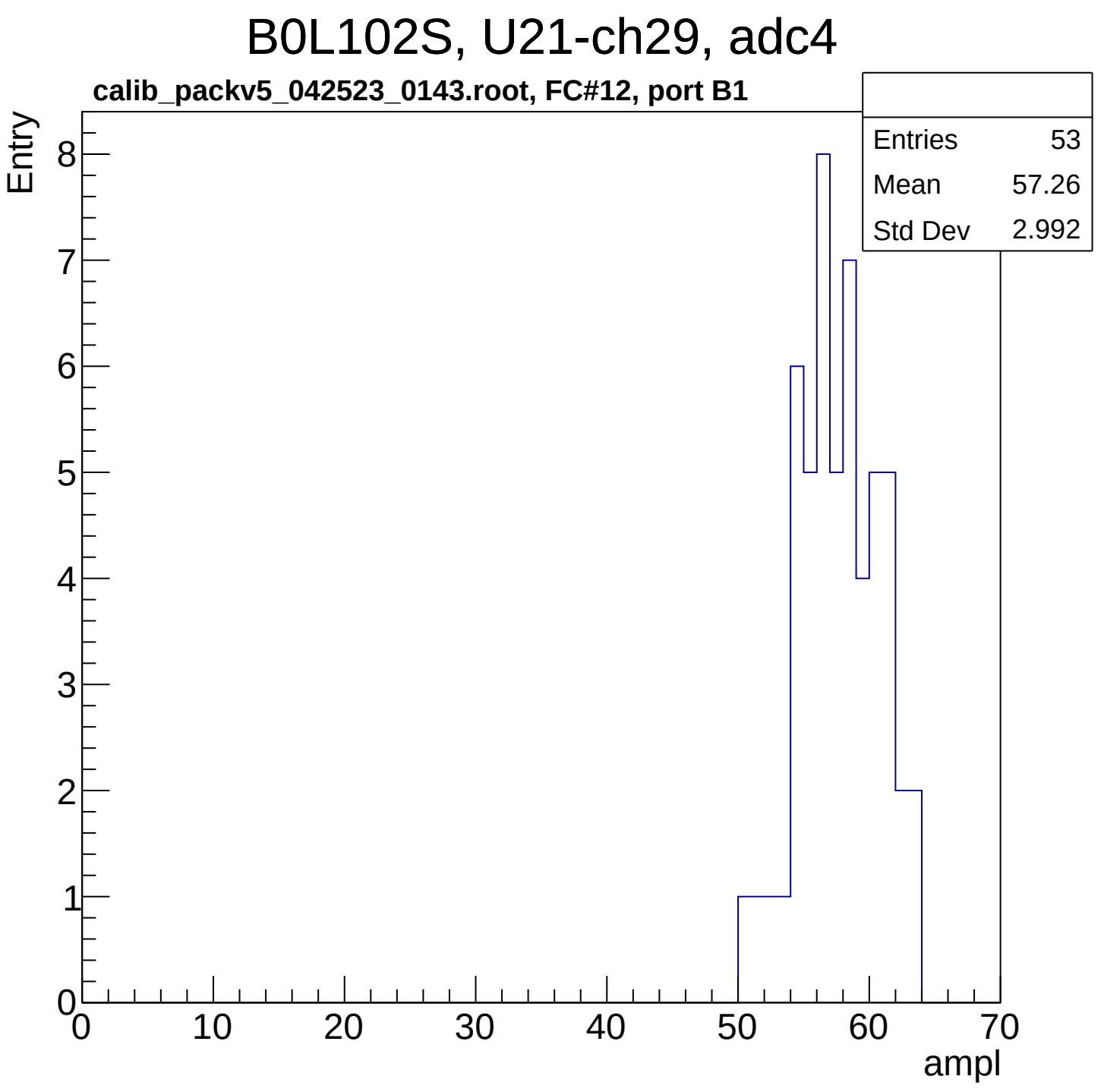
20
30
40
50
60
70


B0L102S, U21-ch29, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 57.26
Std Dev 2.992
Entry
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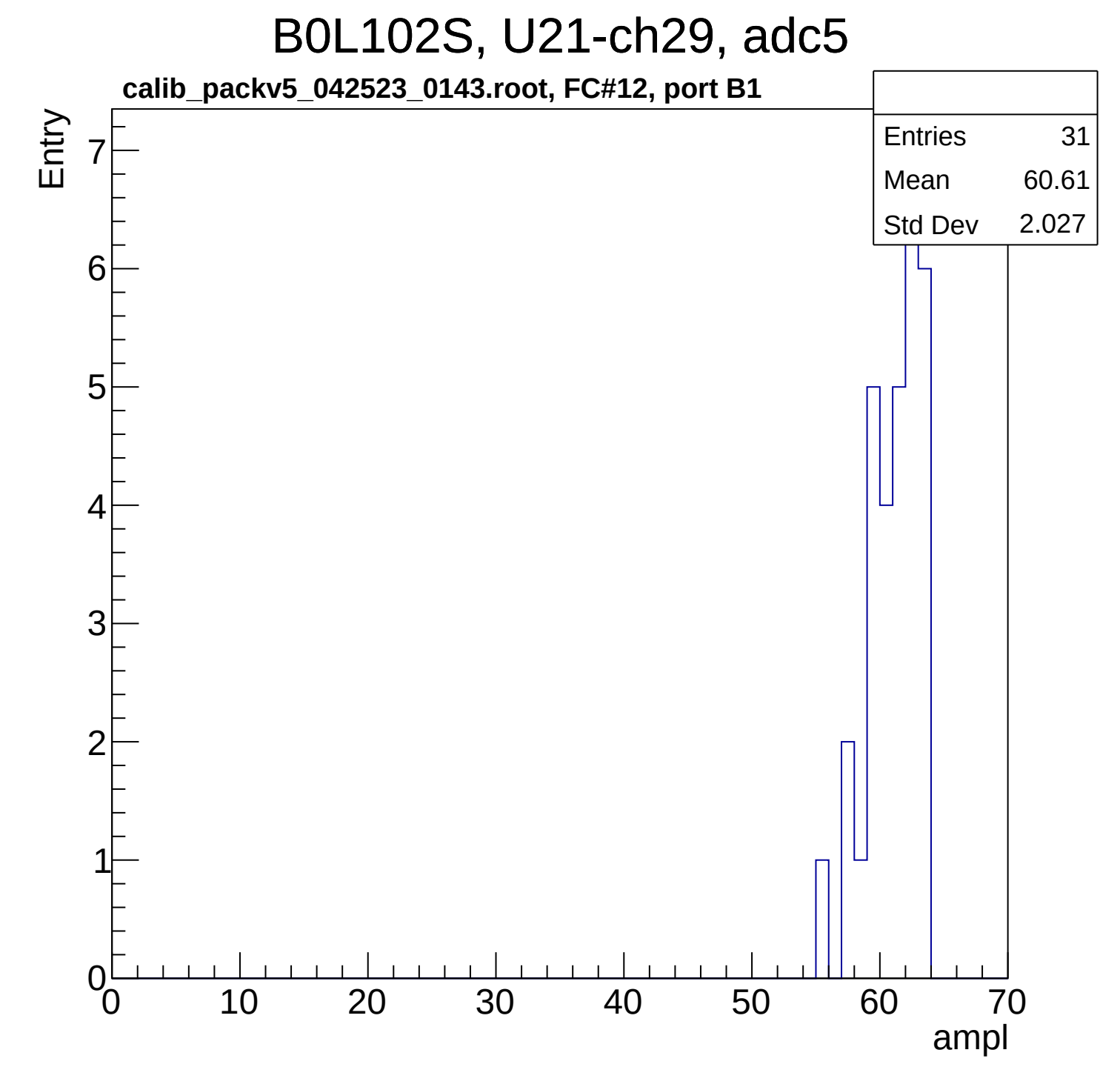
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch29, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 60.61
Std Dev 2.027
Entry
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2
3
4
5
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7
0
10
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30
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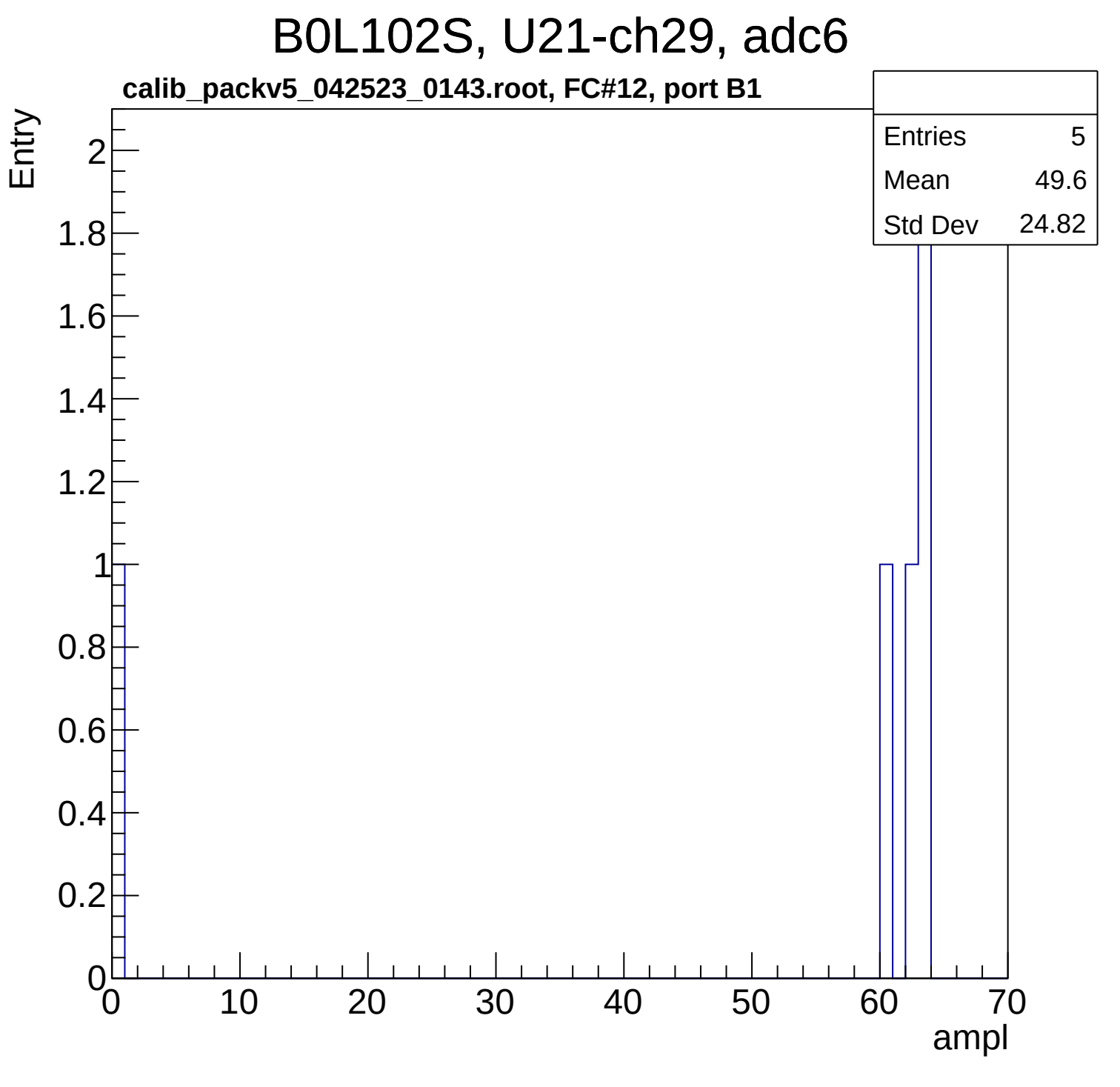
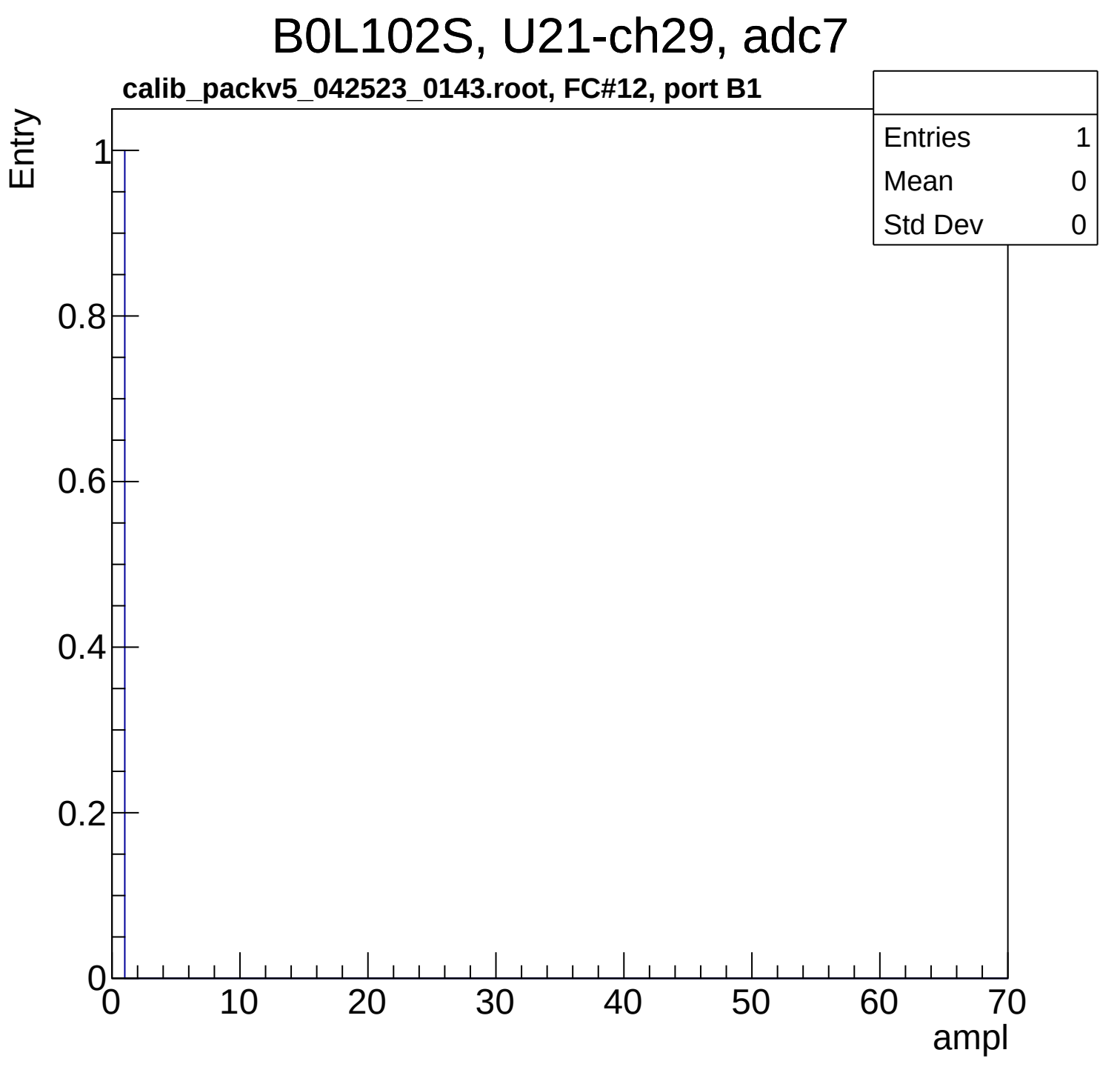
50
60
70
ampl


B0L102S, U21-ch29, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch29, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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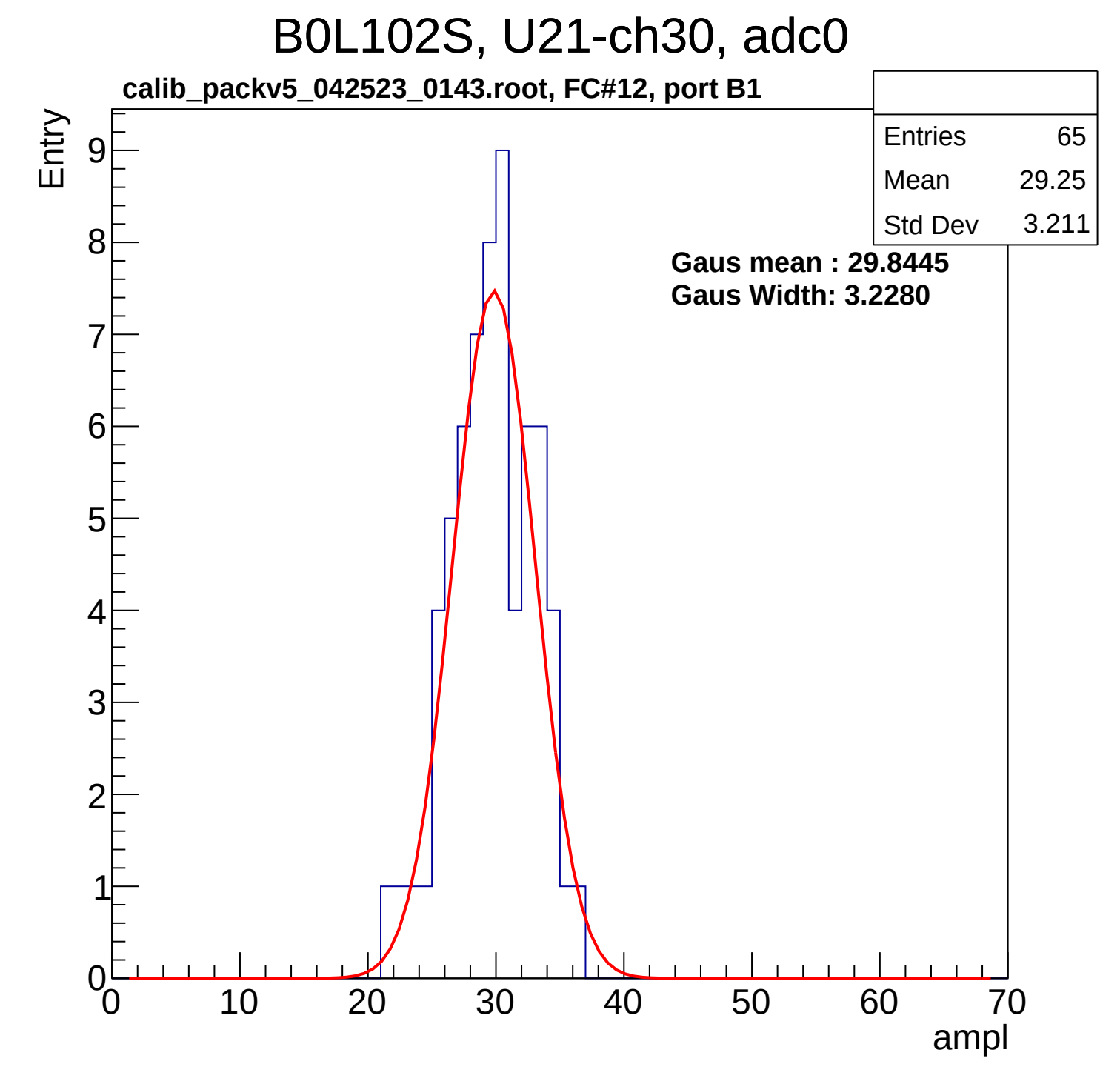
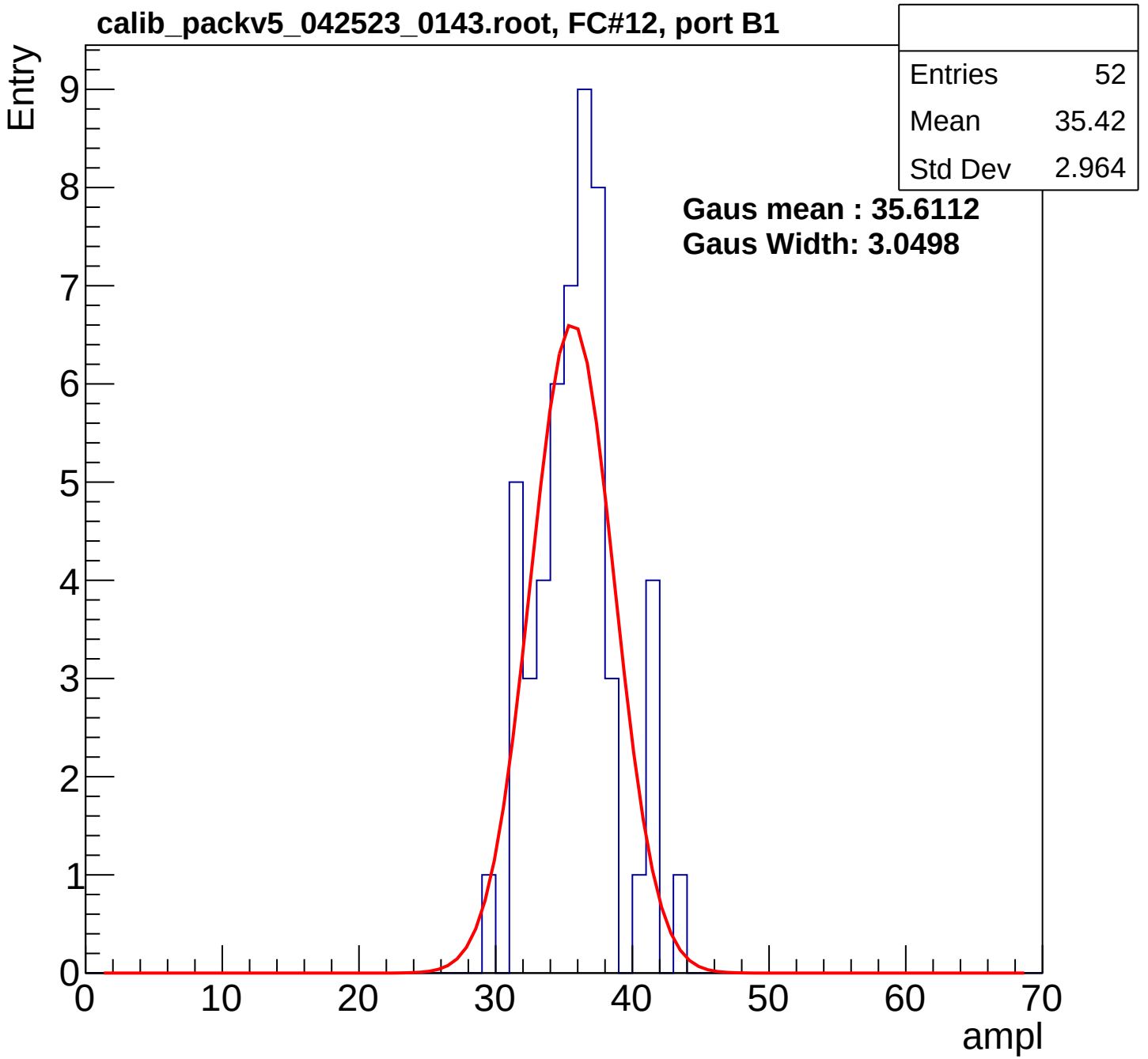
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch30, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 29.25
Std Dev 3.211
Gaus mean : 29.8445
Gaus Width: 3.2280
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch30, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 35.42
Std Dev 2.964
Gaus mean : 35.6112
Gaus Width: 3.0498
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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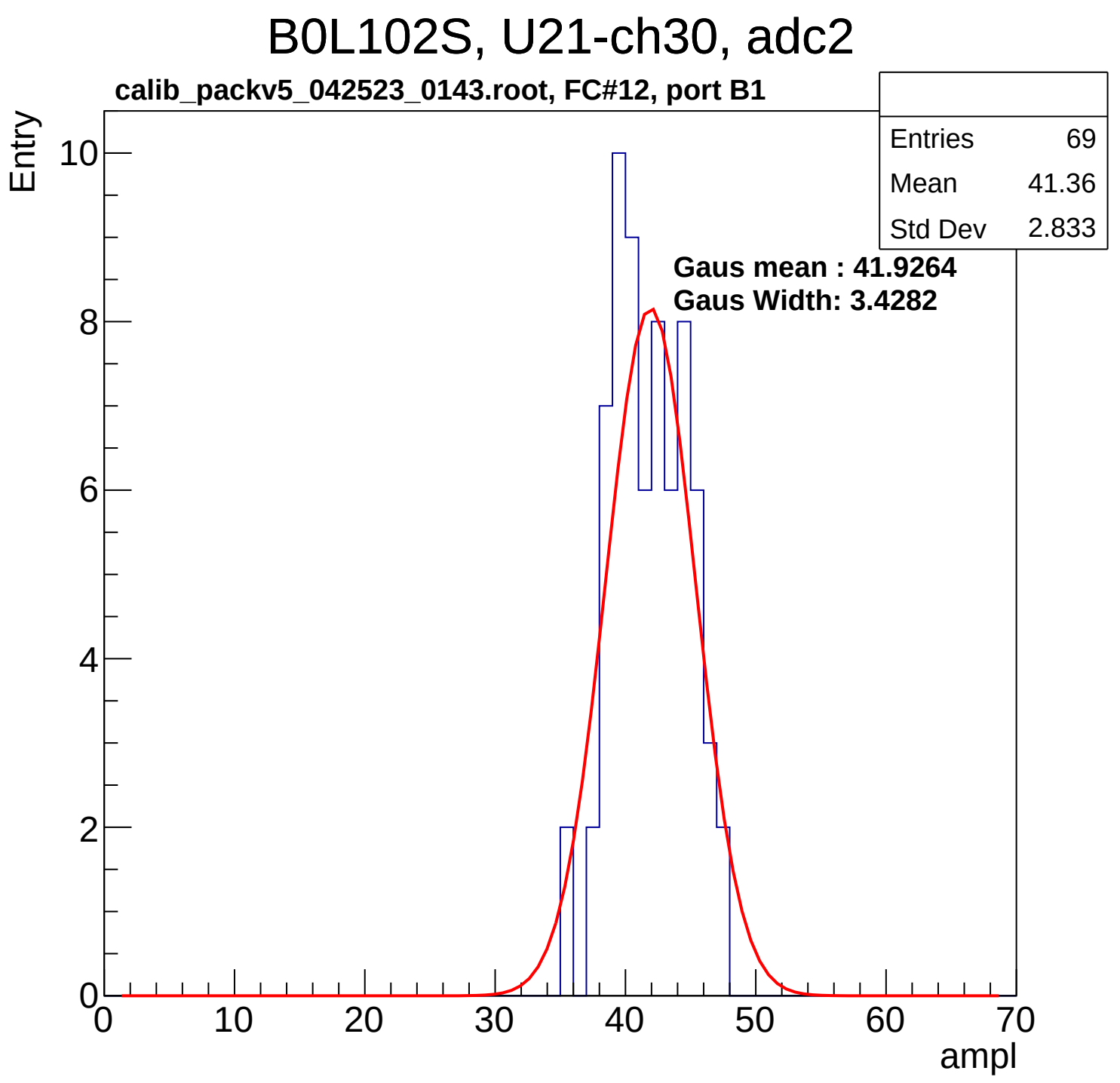
B0L102S, U21-ch30, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 41.36
Std Dev 2.833
Gaus mean : 41.9264
Gaus Width: 3.4282
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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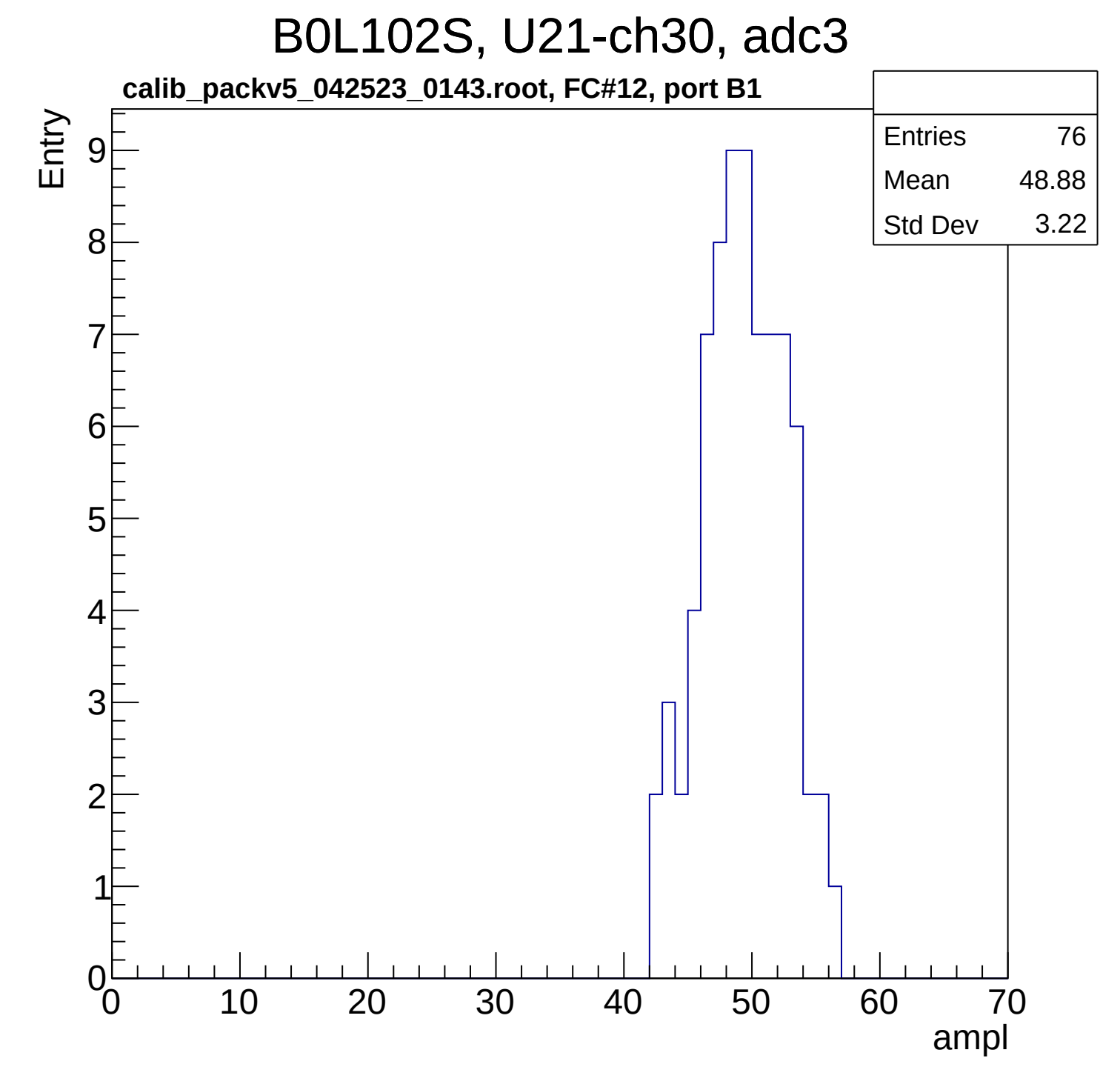
B0L102S, U21-ch30, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 48.88
Std Dev 3.22
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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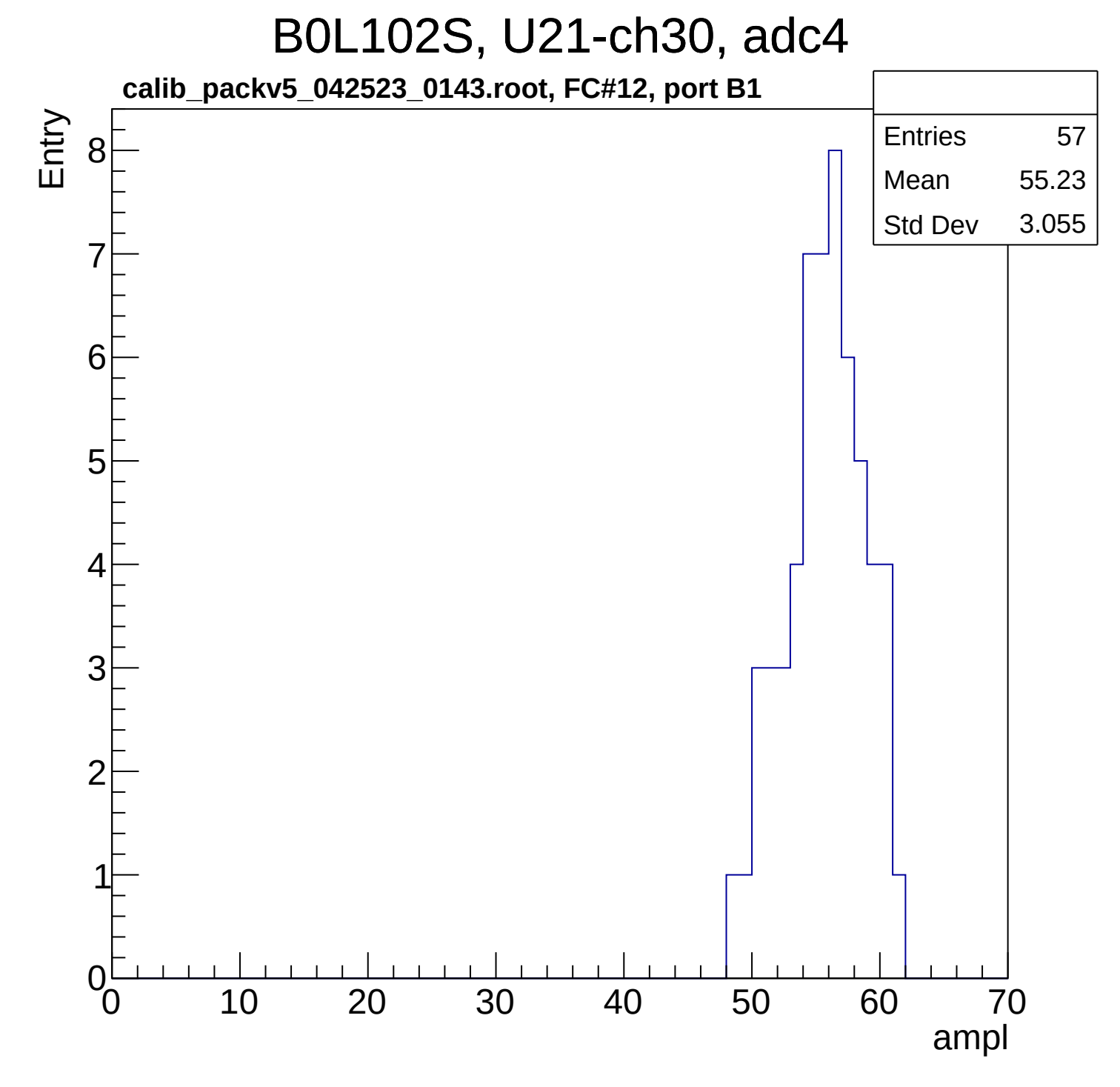
B0L102S, U21-ch30, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.23
Std Dev 3.055
Entry
ampl
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1
2
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4
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60
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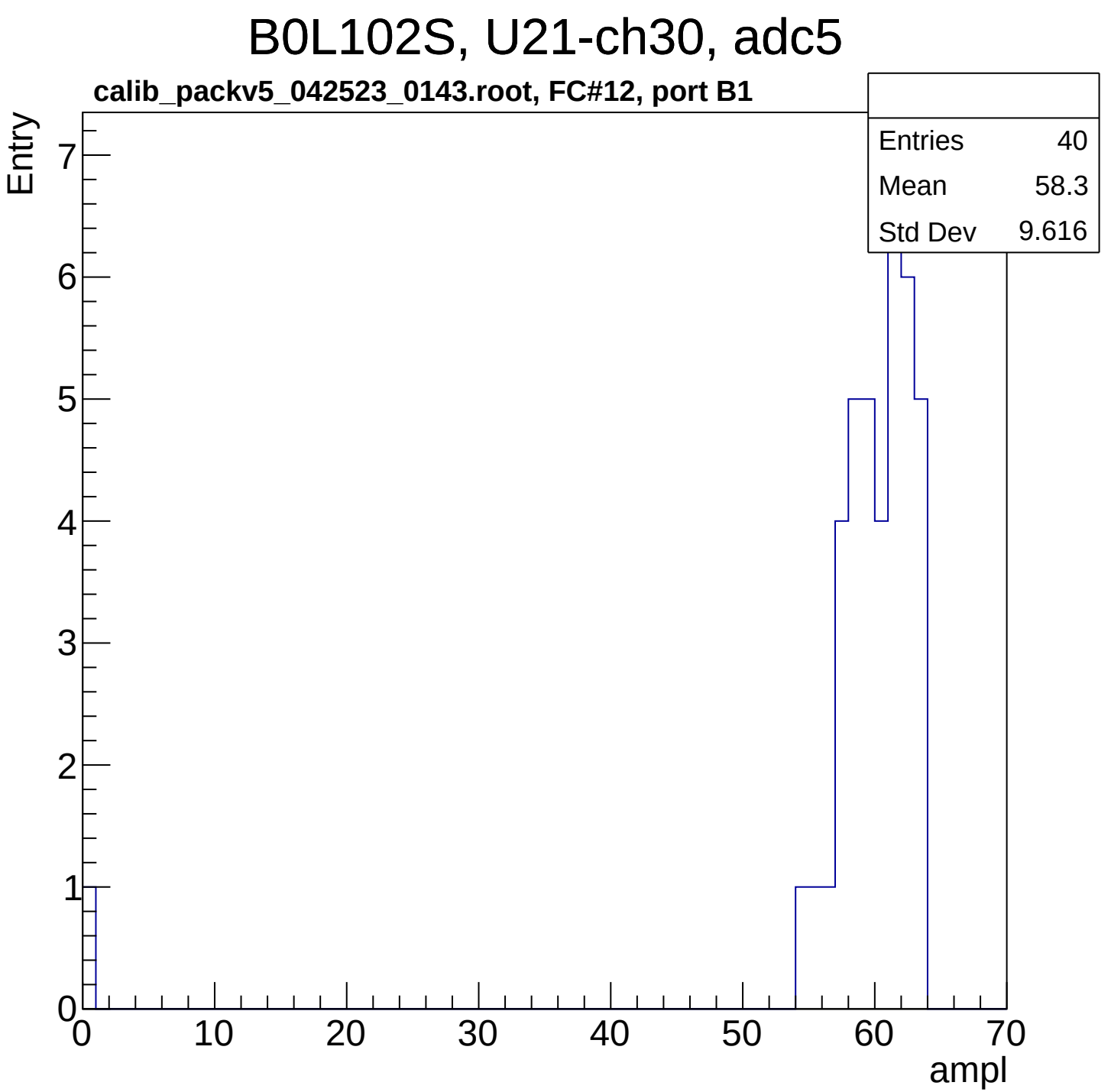
B0L102S, U21-ch30, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.3
Std Dev 9.616
Entry
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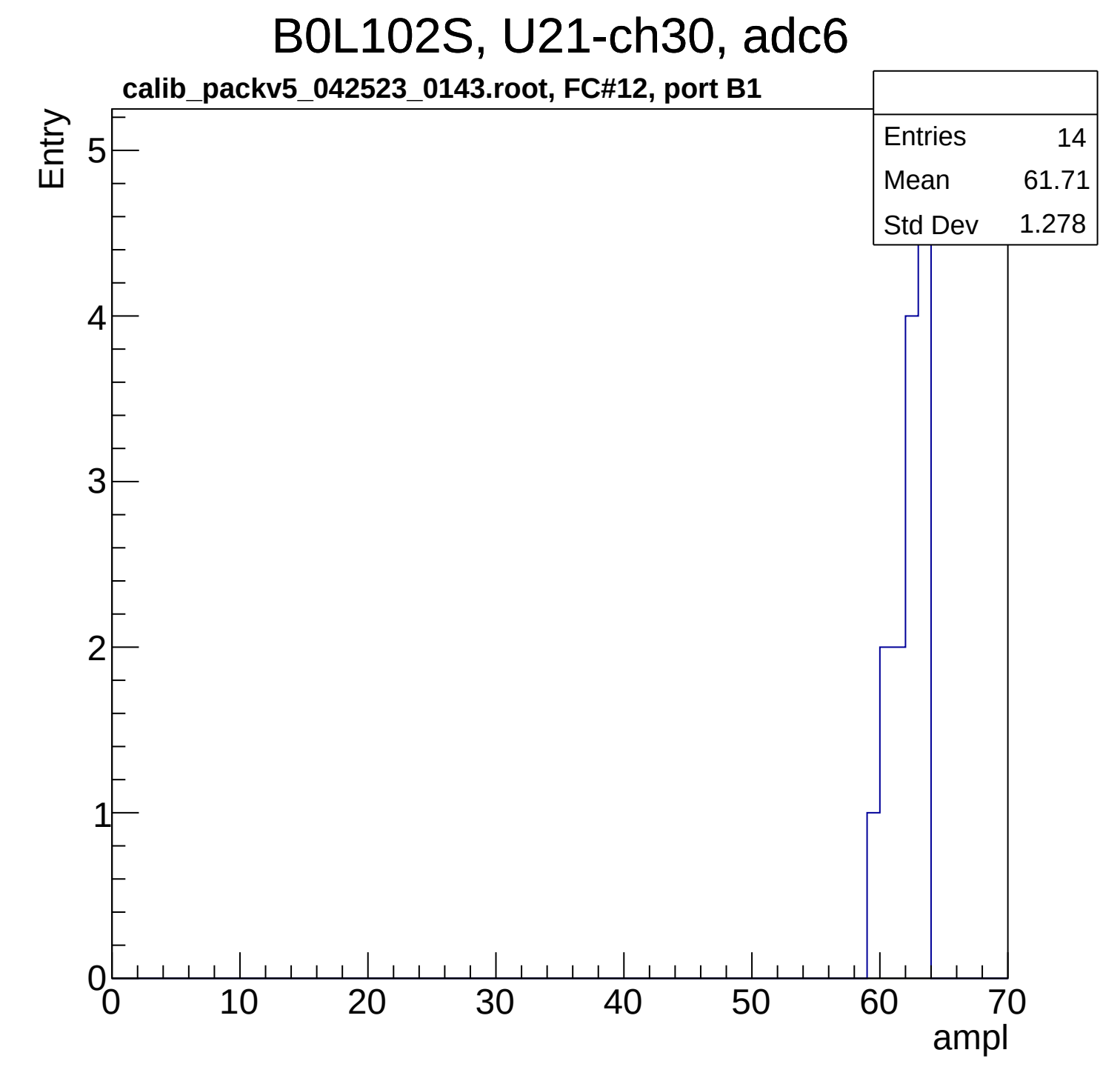
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch30, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.71
Std Dev 1.278
Entry
ampl
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2
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4
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0
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40
50
60
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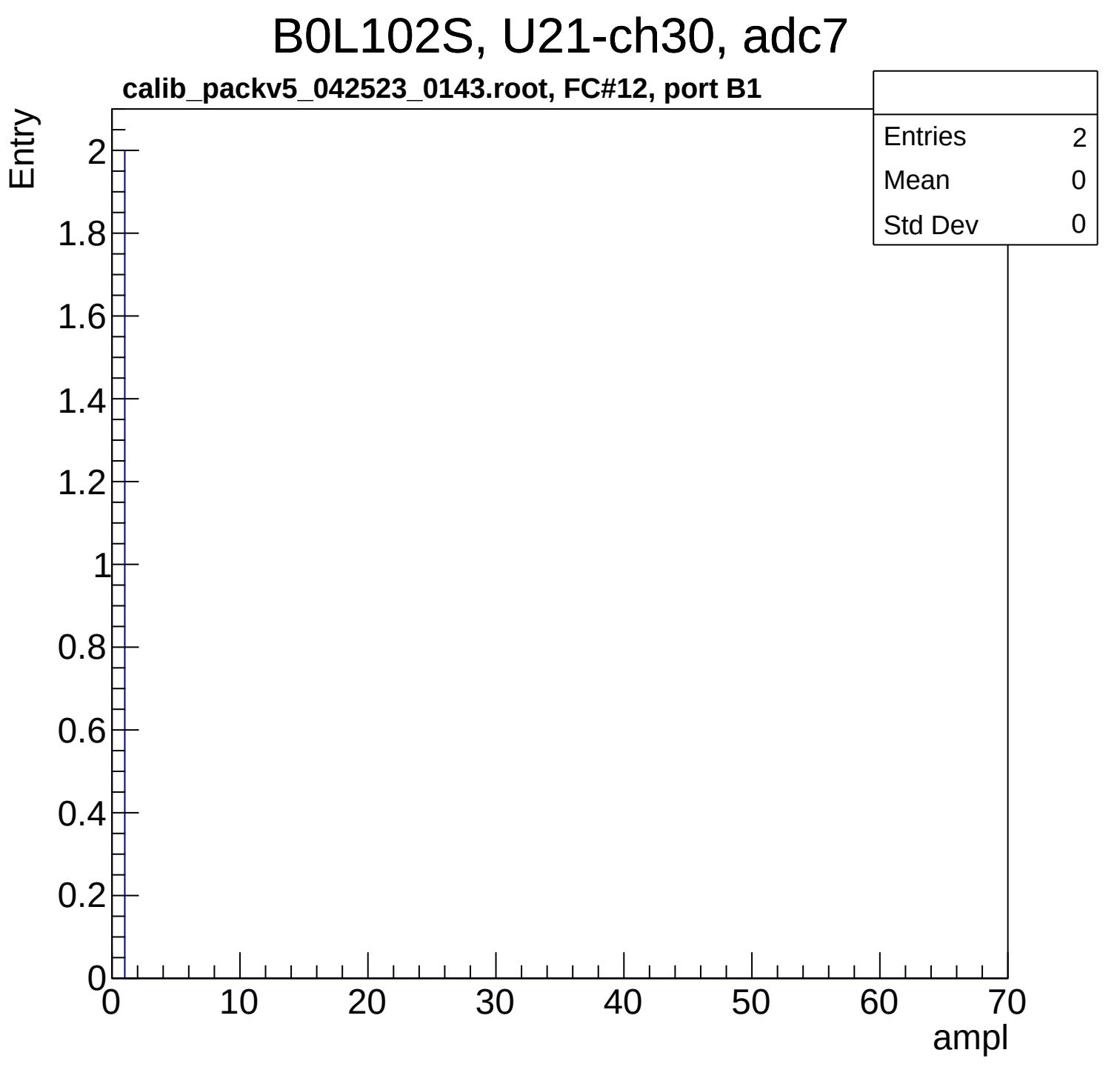
B0L102S, U21-ch30, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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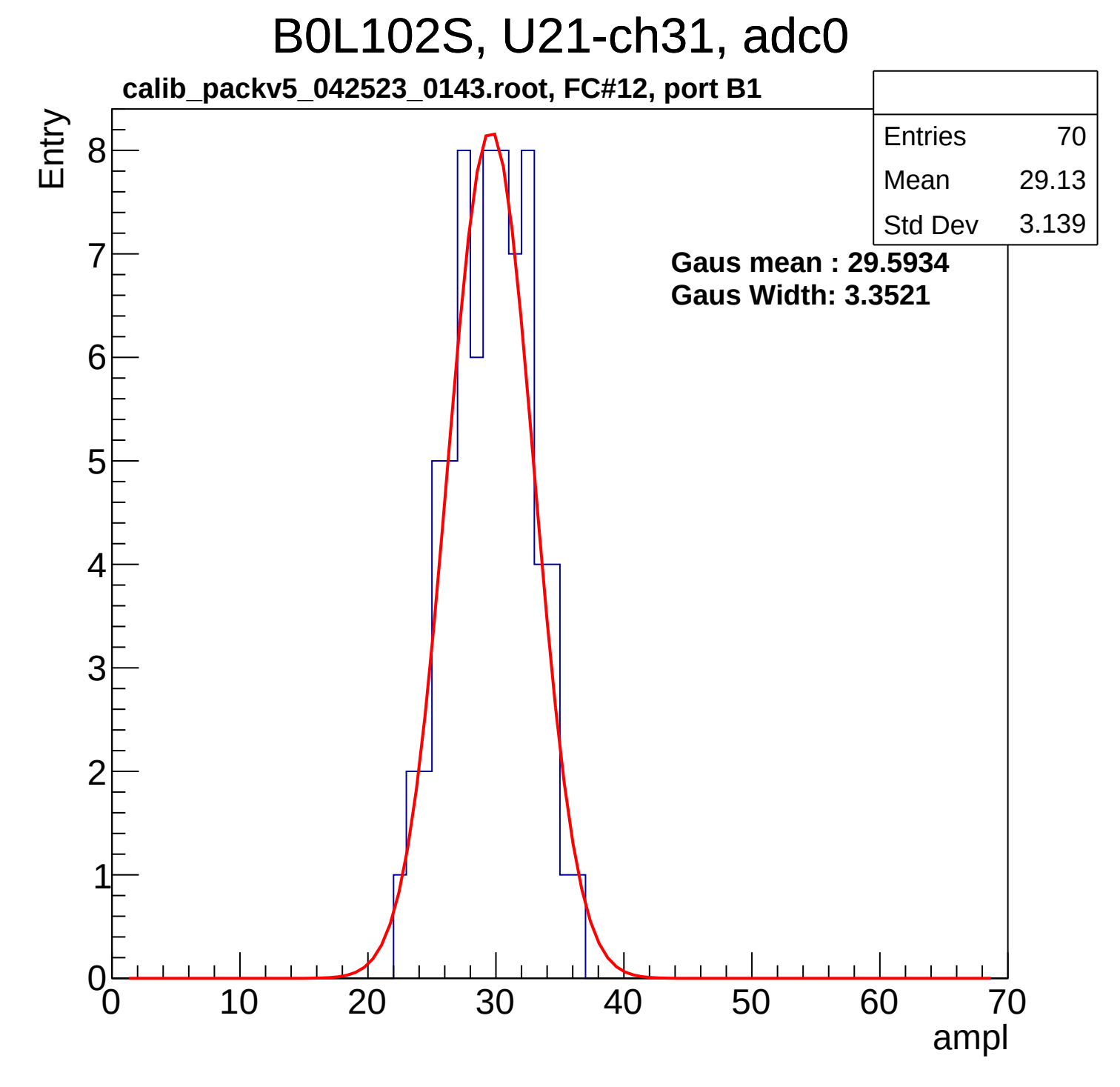
40
50
60
70


B0L102S, U21-ch31, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 29.13
Std Dev 3.139
Gaus mean : 29.5934
Gaus Width: 3.3521
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
50
60
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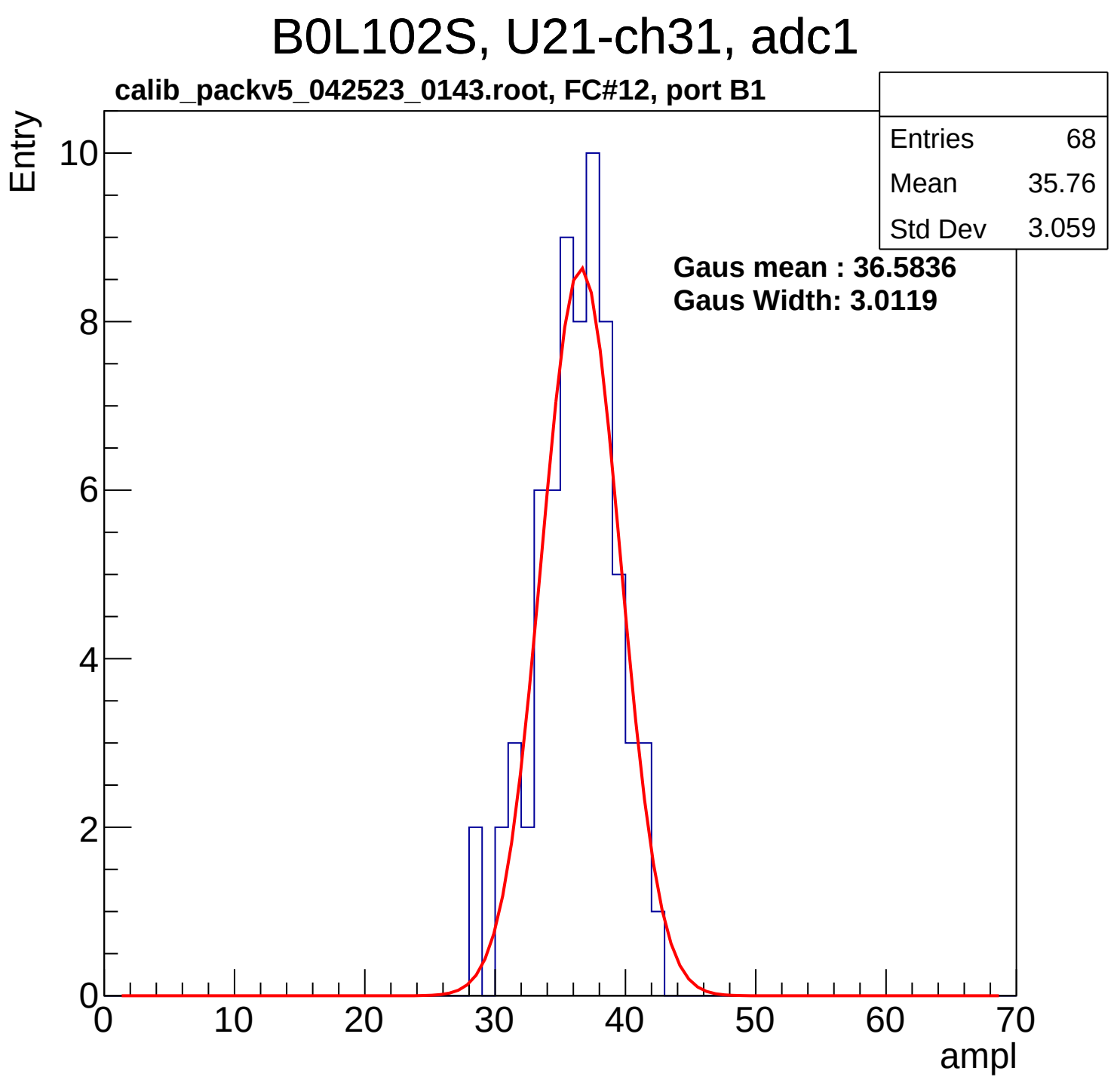
B0L102S, U21-ch31, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 35.76
Std Dev 3.059
Gaus mean : 36.5836
Gaus Width: 3.0119
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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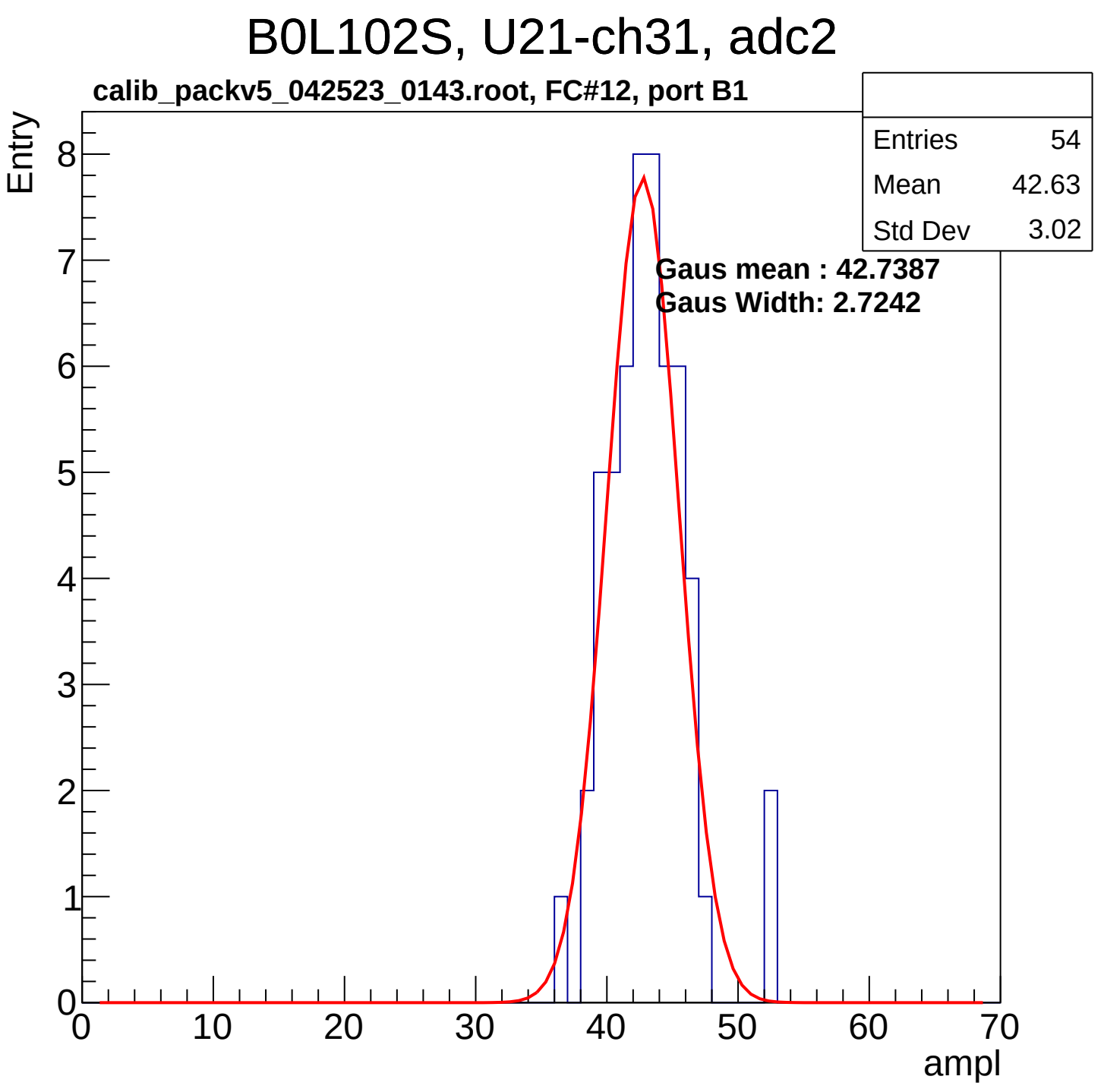
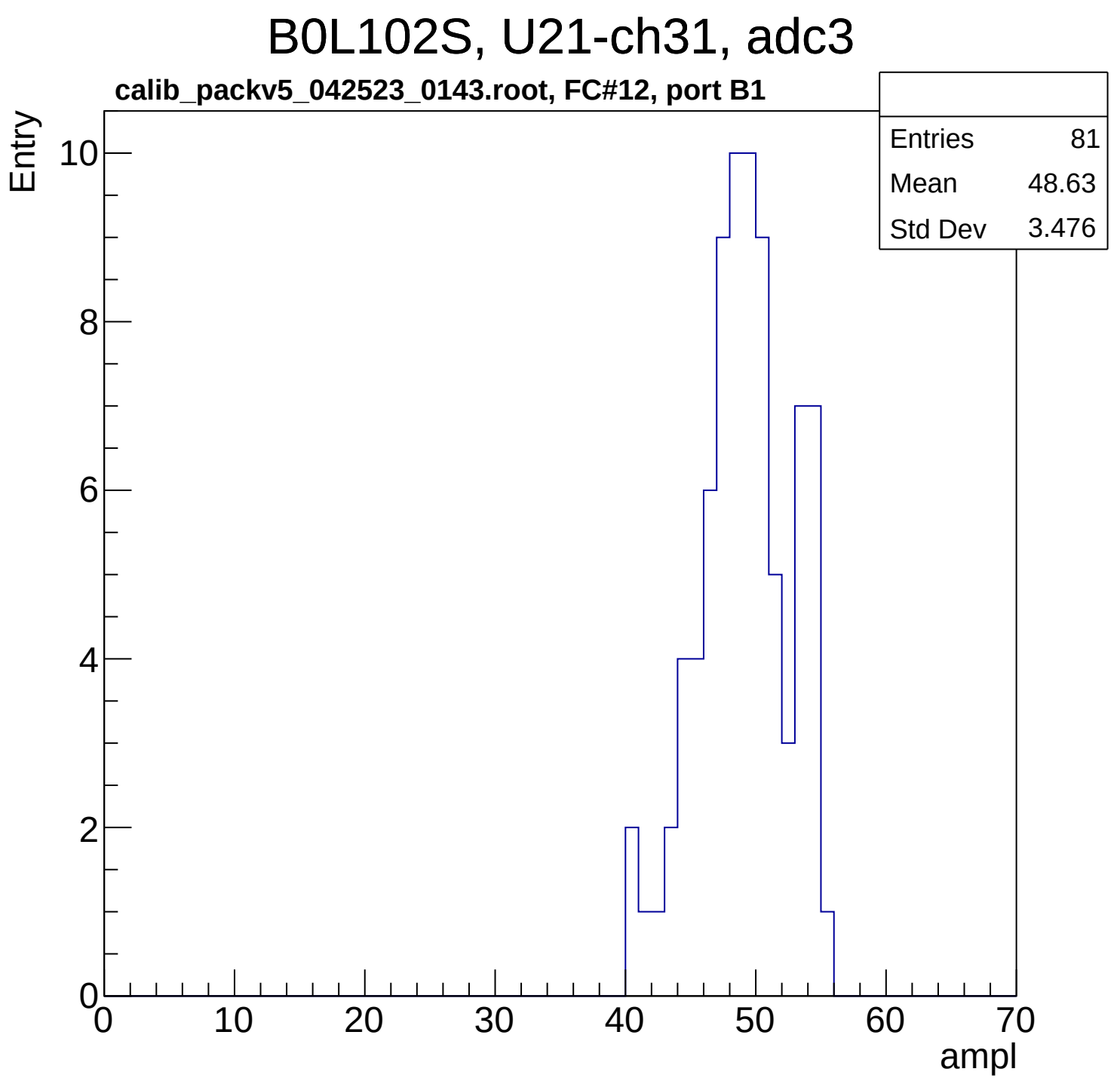
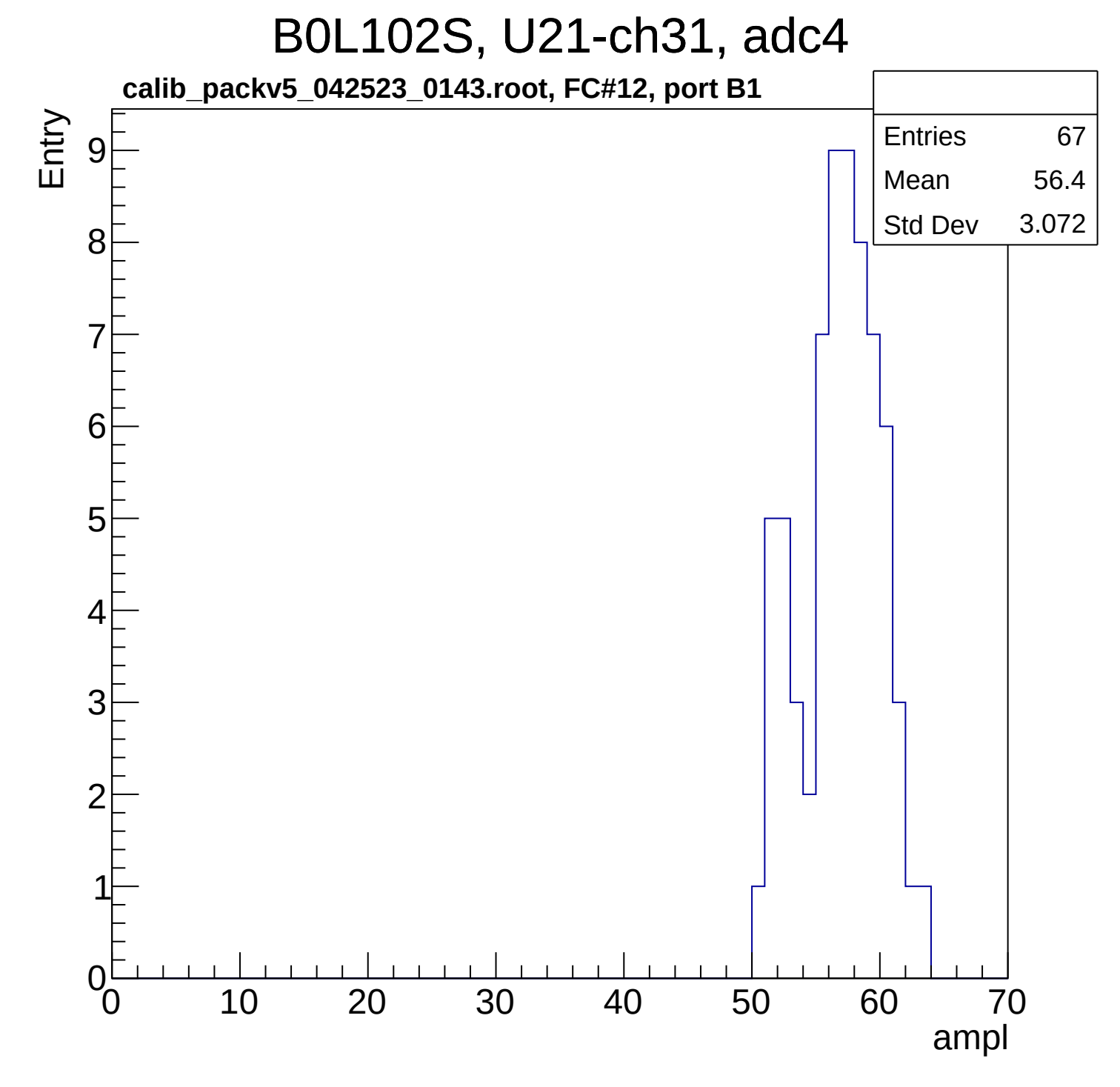

B0L102S, U21-ch31, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 42.63
Std Dev 3.02
Gaus mean : 42.7387
Gaus Width: 2.7242
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch31, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 48.63
Std Dev 3.476
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch31, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 56.4
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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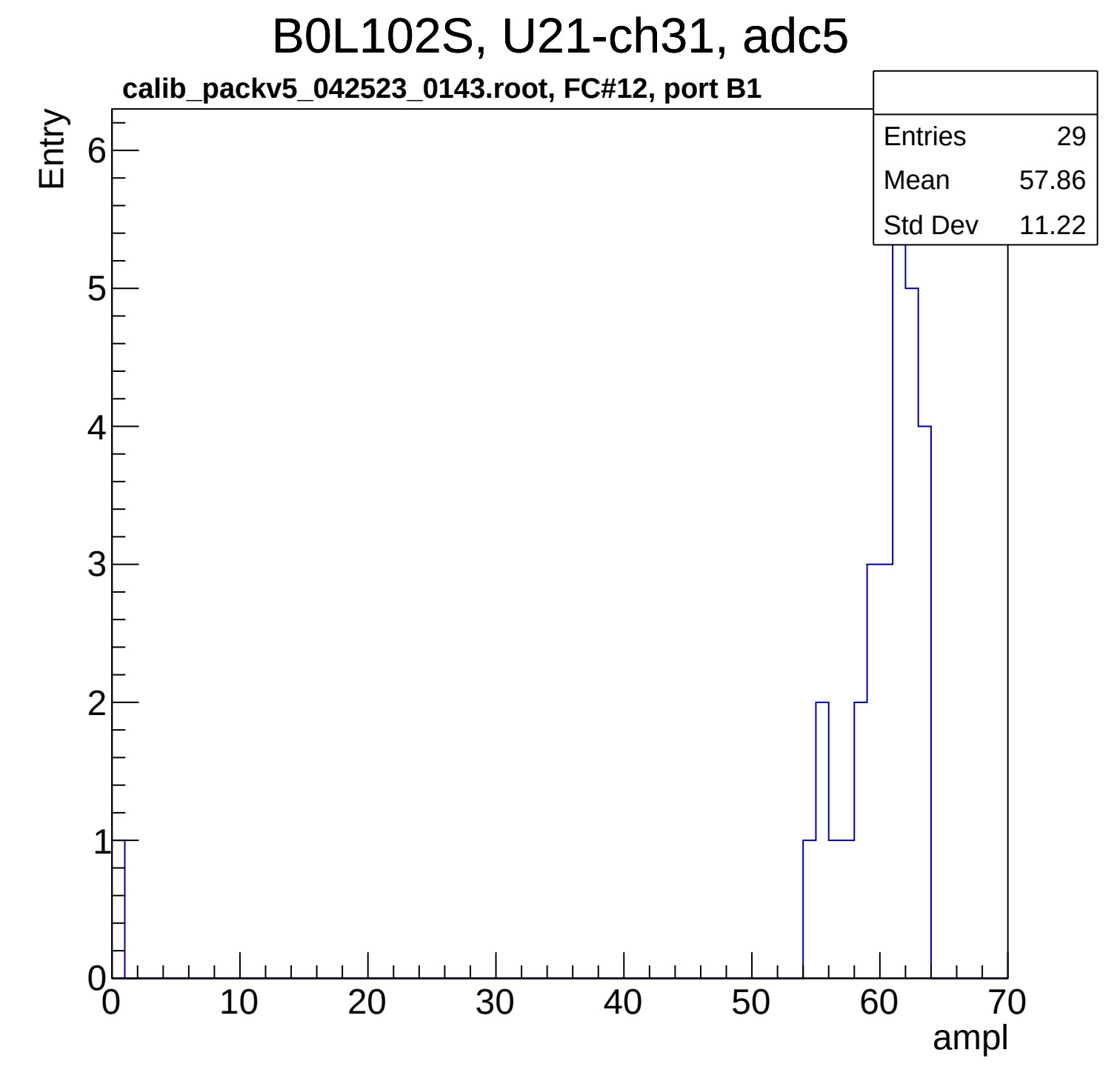
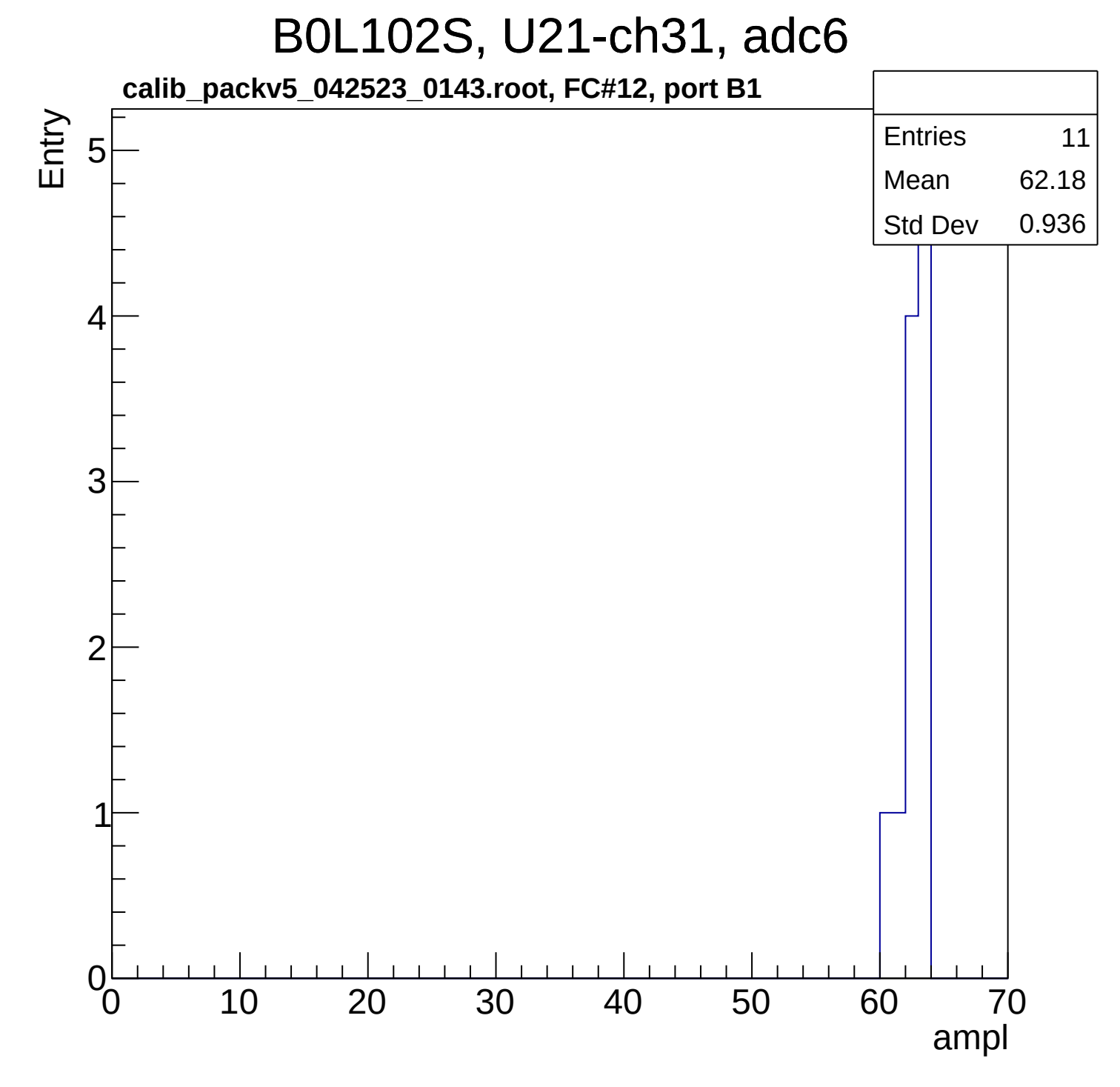
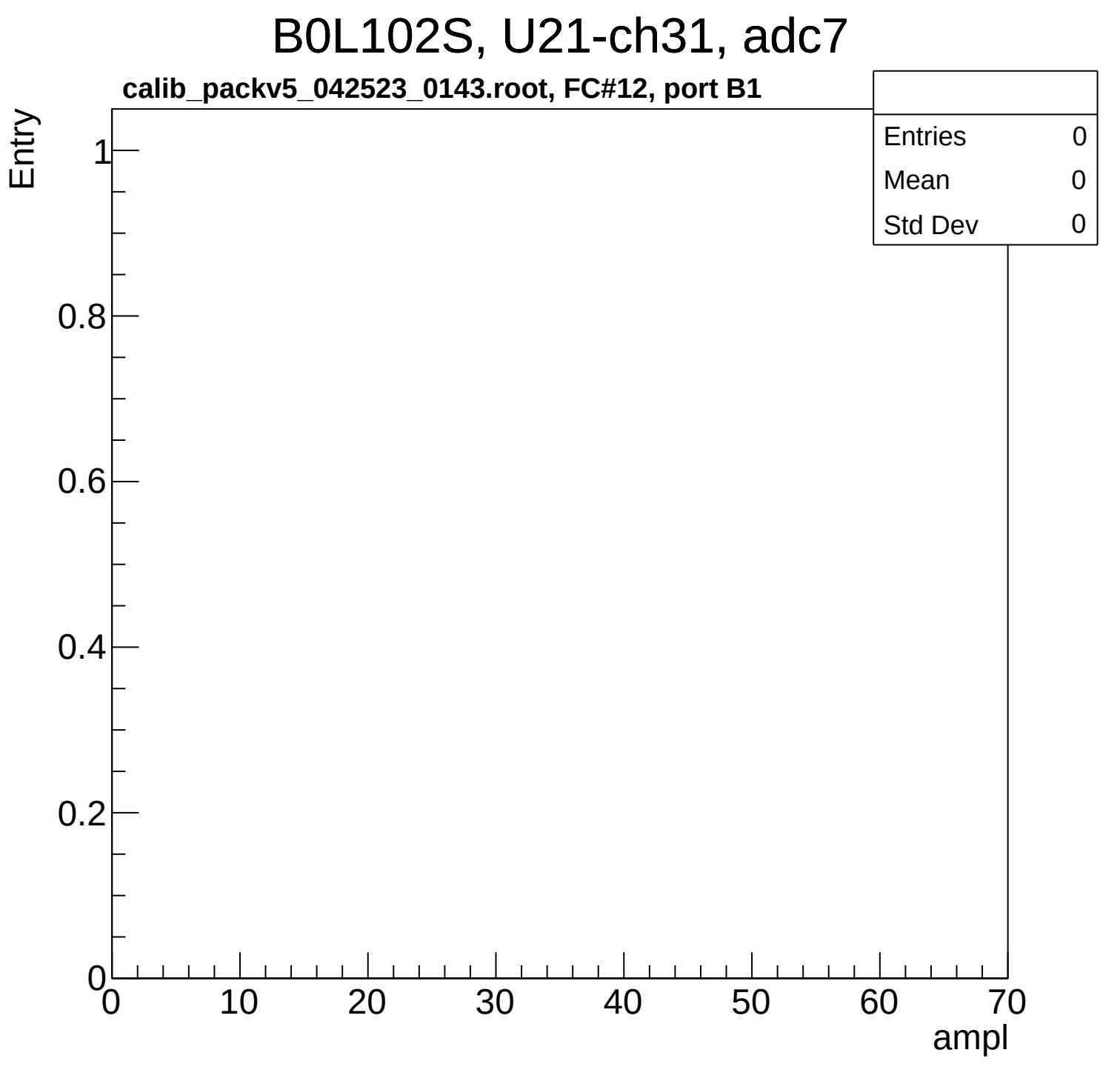
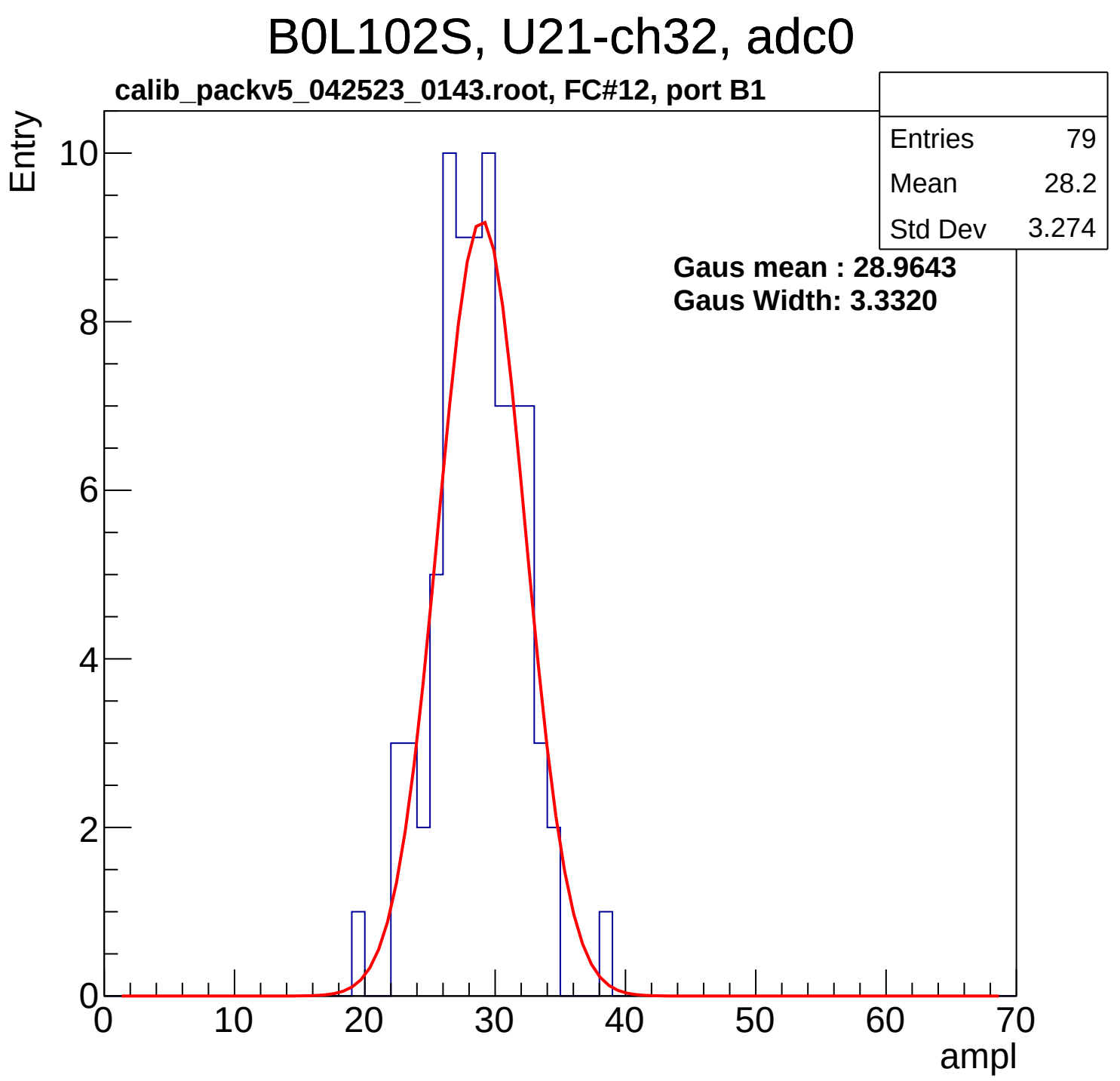
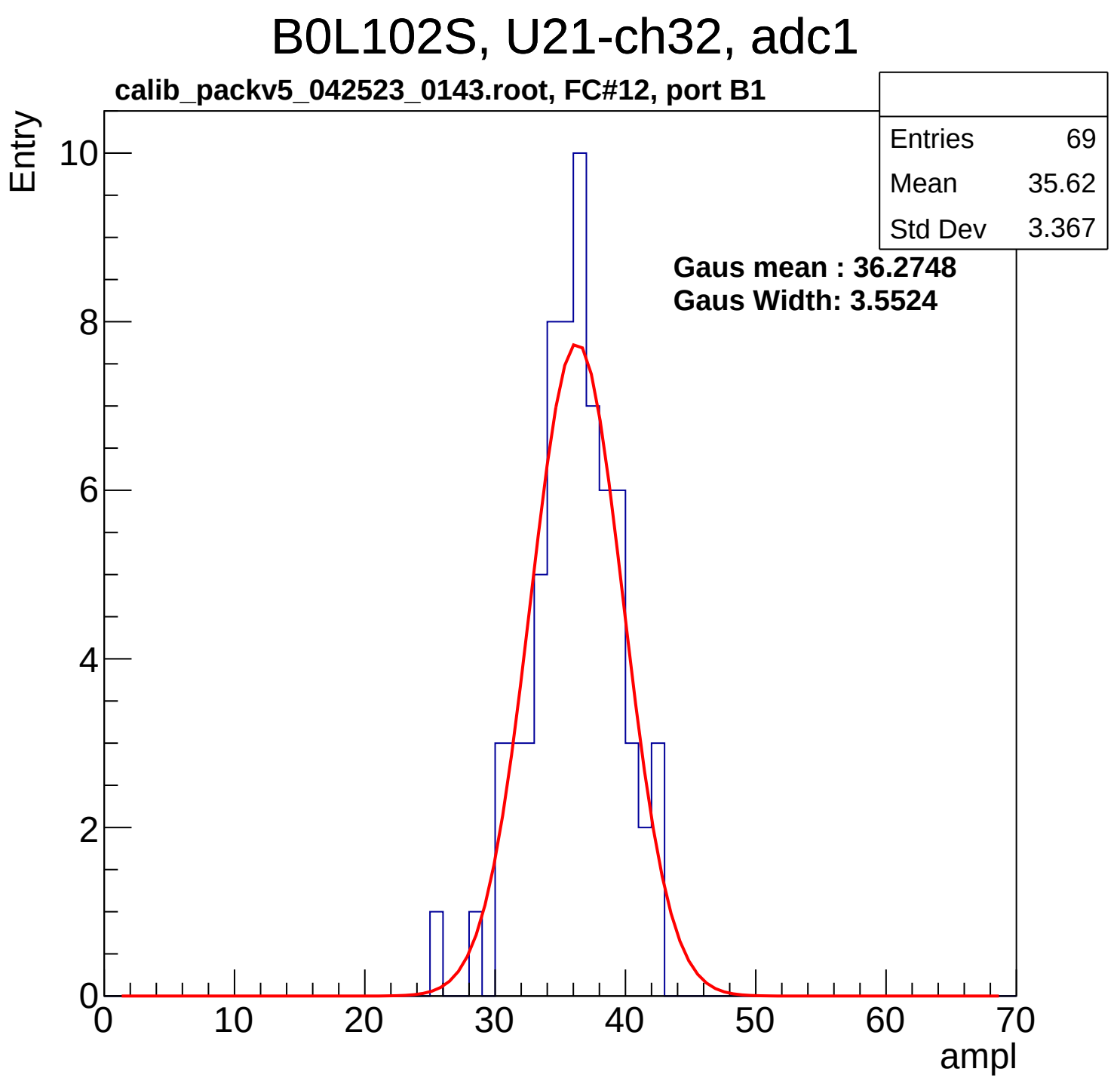
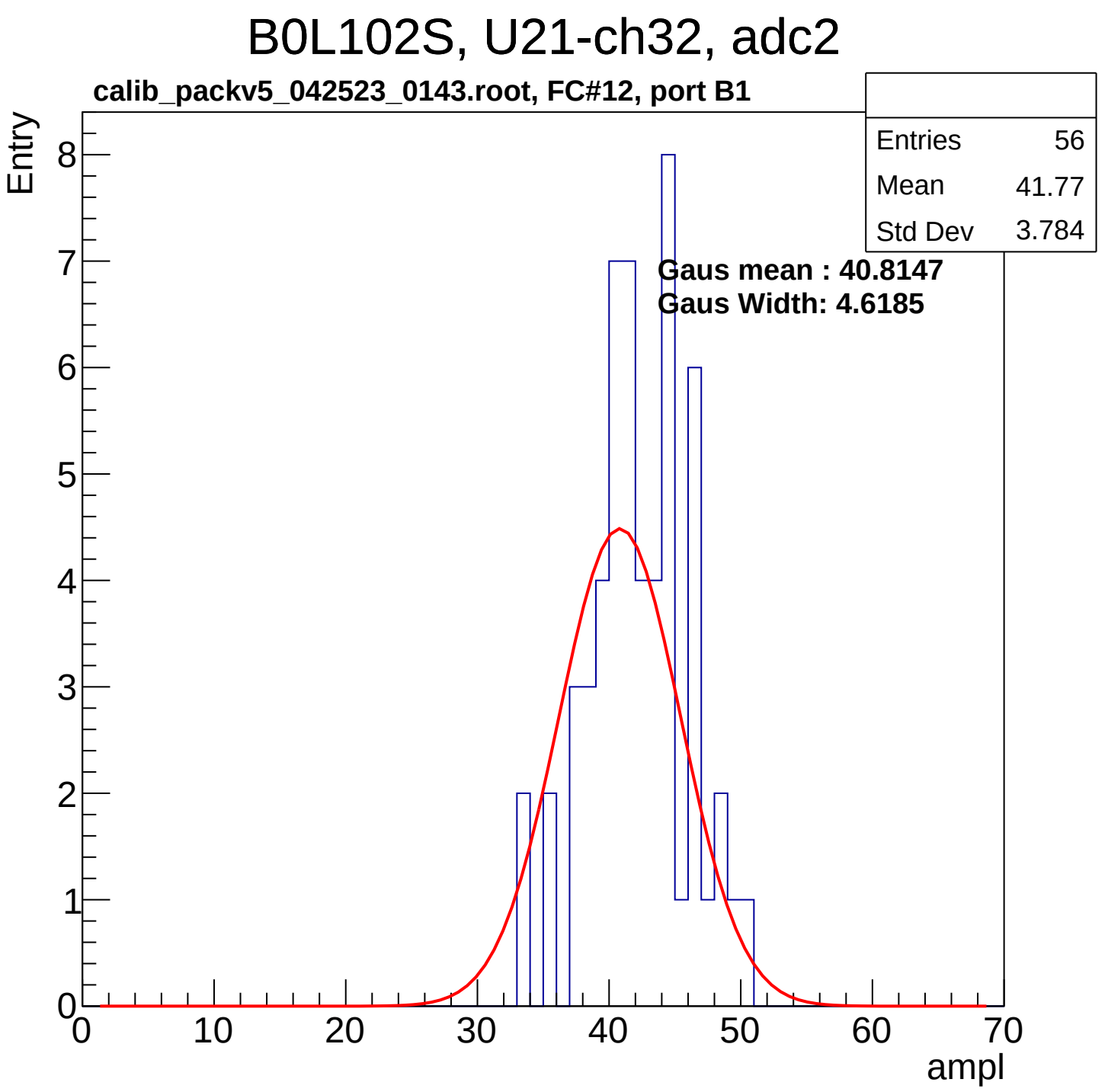

B0L102S, U21-ch31, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 29
Mean 57.86
Std Dev 11.22
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

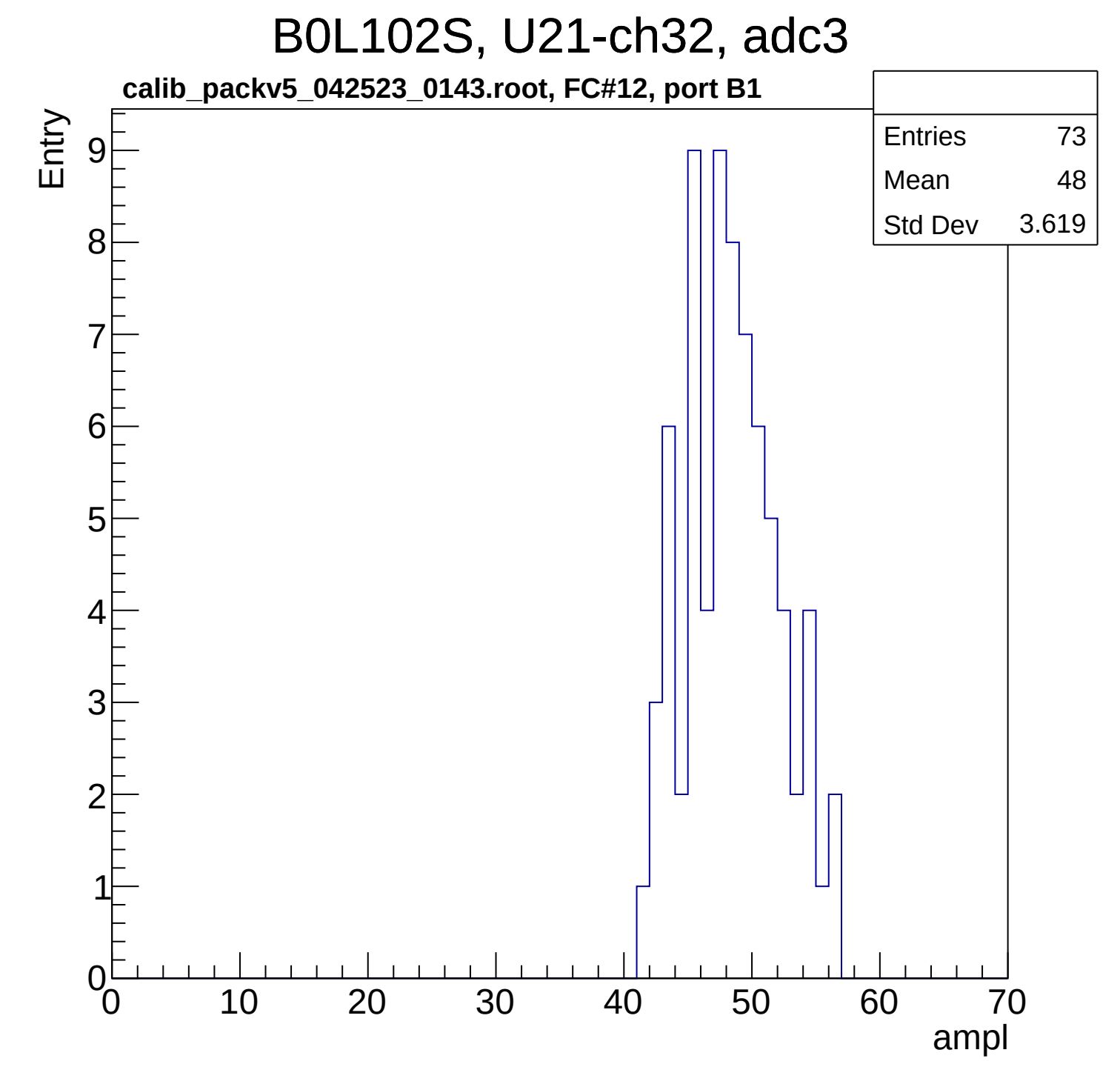

B0L102S, U21-ch31, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 62.18
Std Dev 0.936
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

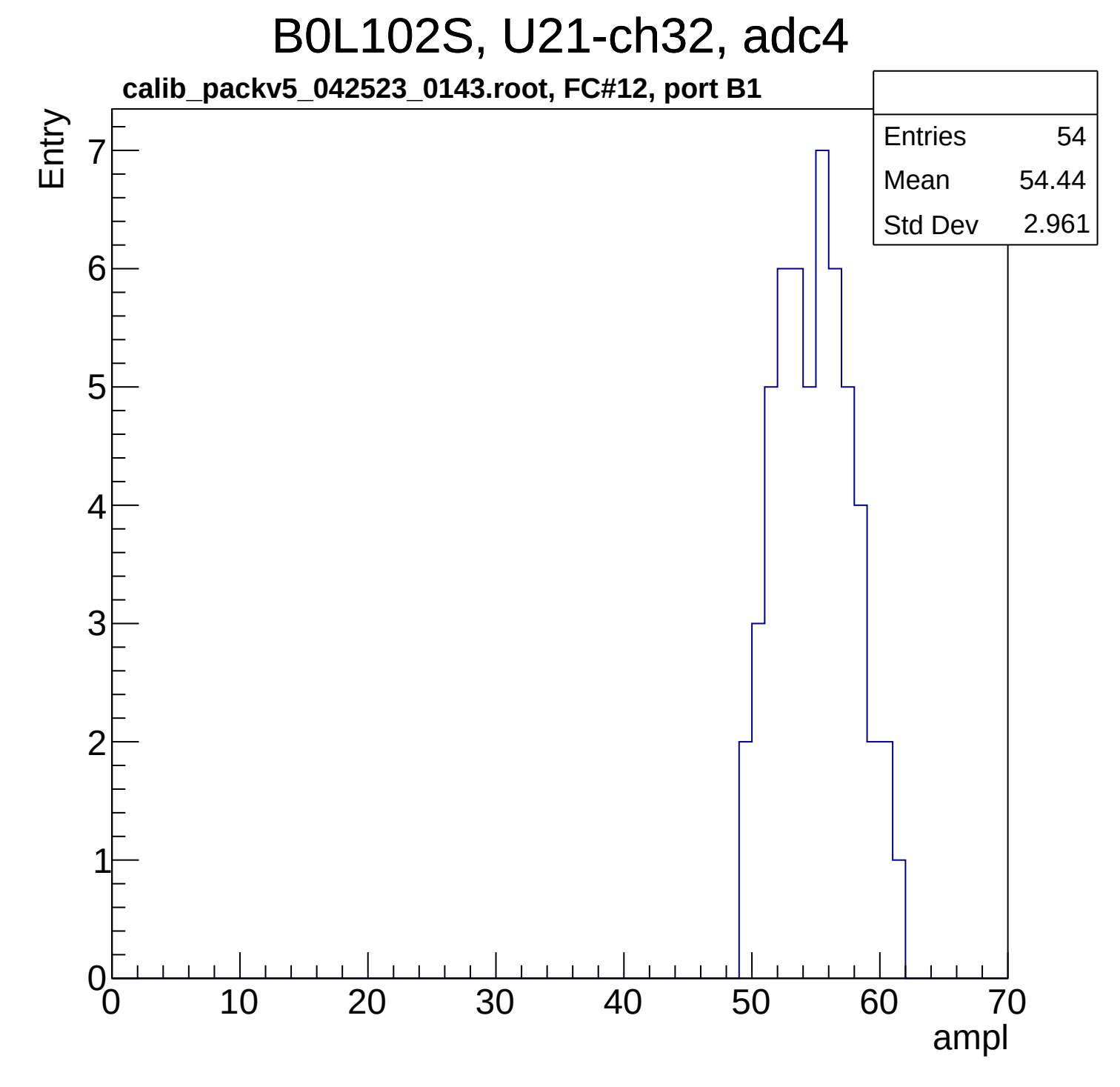

B0L102S, U21-ch31, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

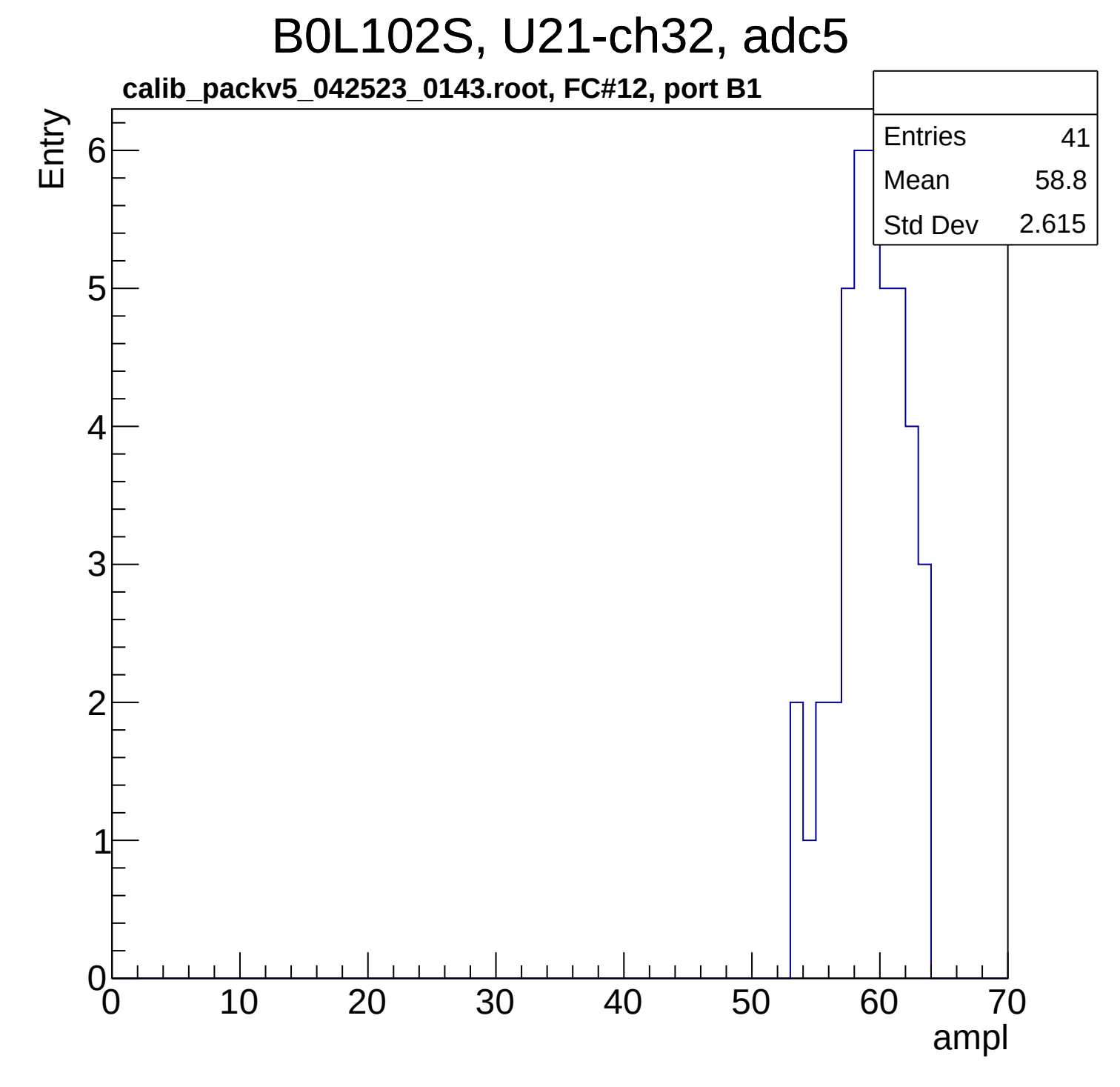

B0L102S, U21-ch32, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 28.2
Std Dev 3.274
Gaus mean : 28.9643
Gaus Width: 3.3320
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

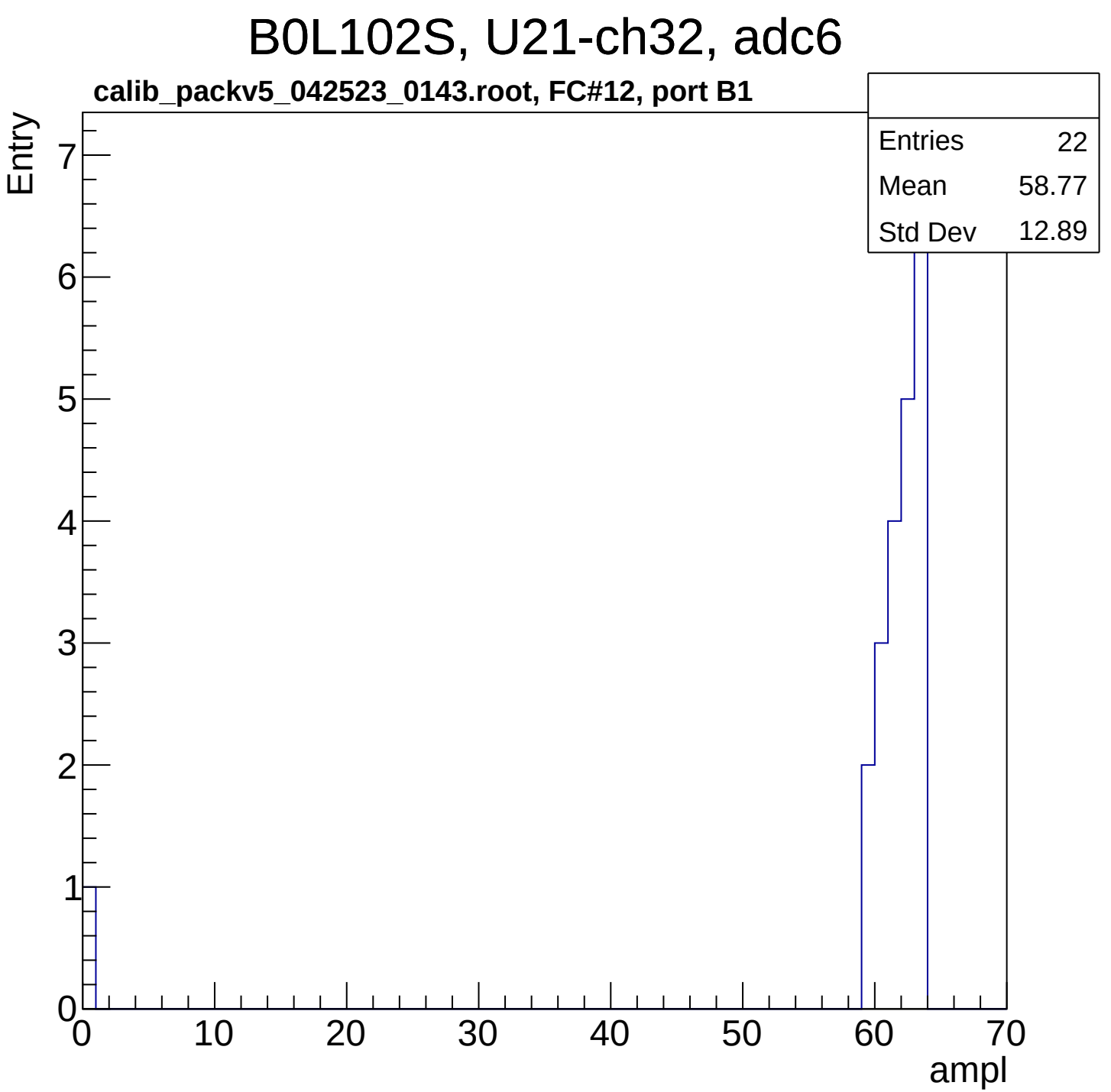

B0L102S, U21-ch32, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 35.62
Std Dev 3.367
Gaus mean : 36.2748
Gaus Width: 3.5524
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

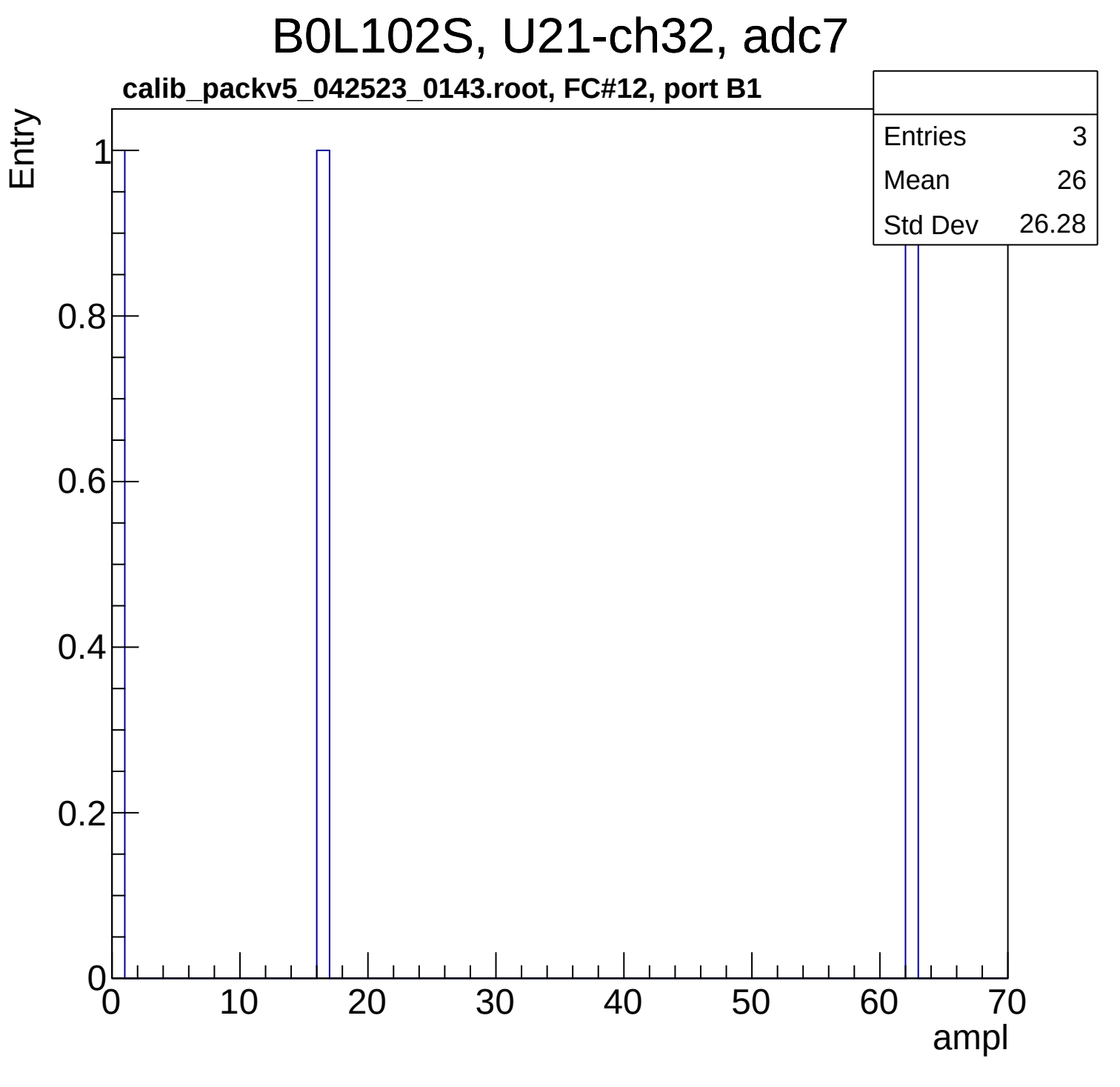

B0L102S, U21-ch32, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 41.77
Std Dev 3.784
Gaus mean : 40.8147
Gaus Width: 4.6185
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

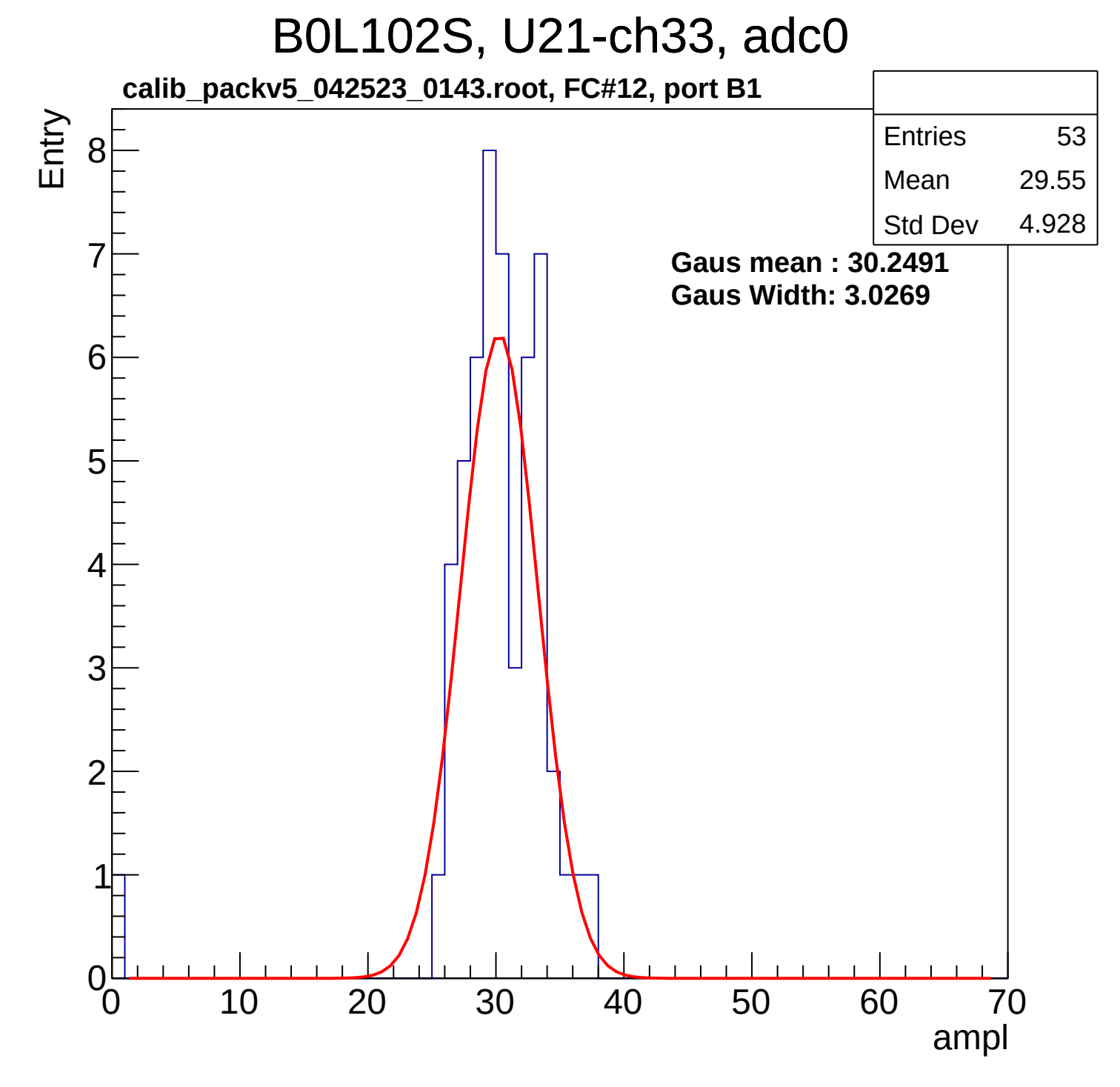

B0L102S, U21-ch32, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 48
Std Dev 3.619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

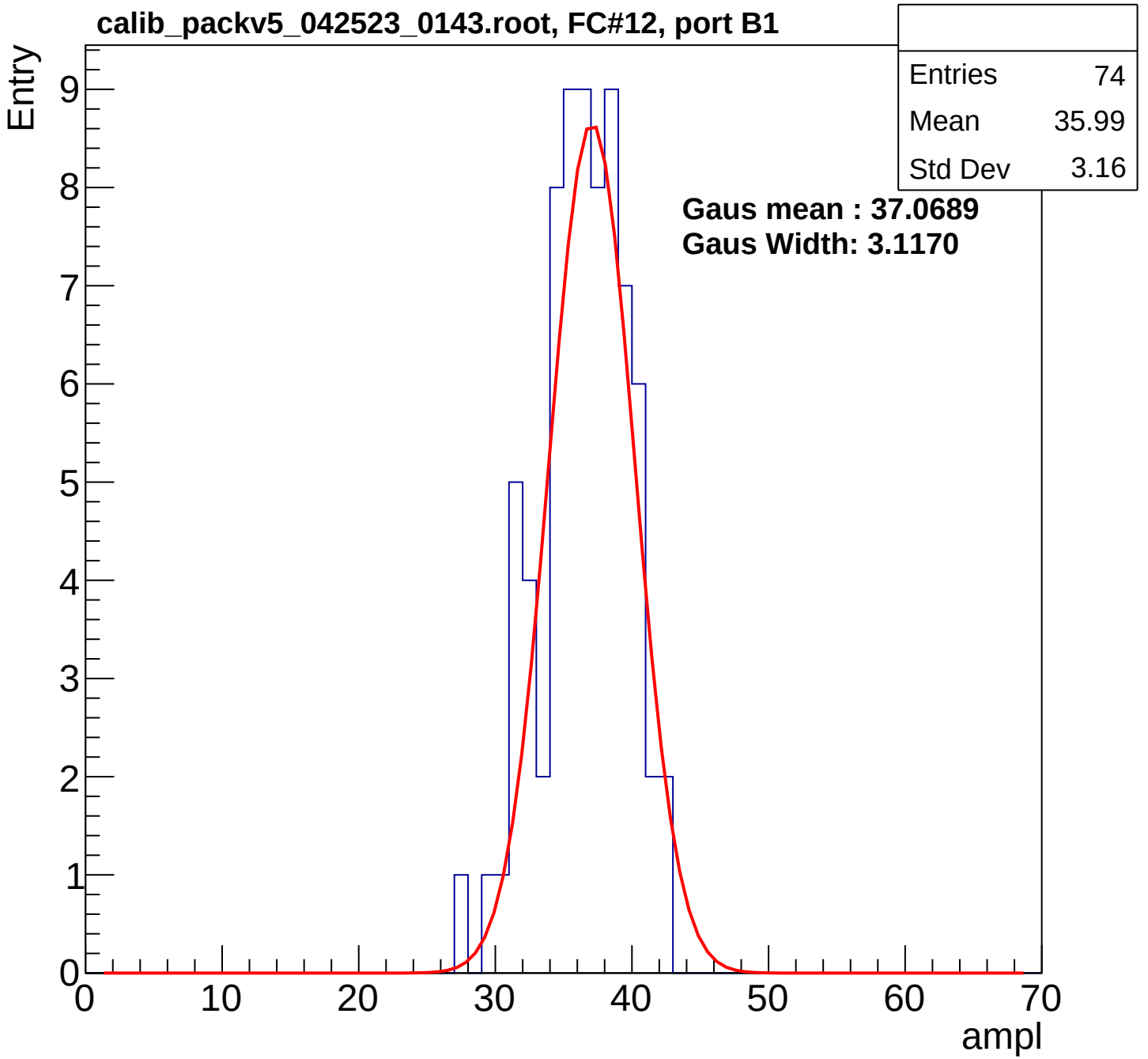

B0L102S, U21-ch32, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.44
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

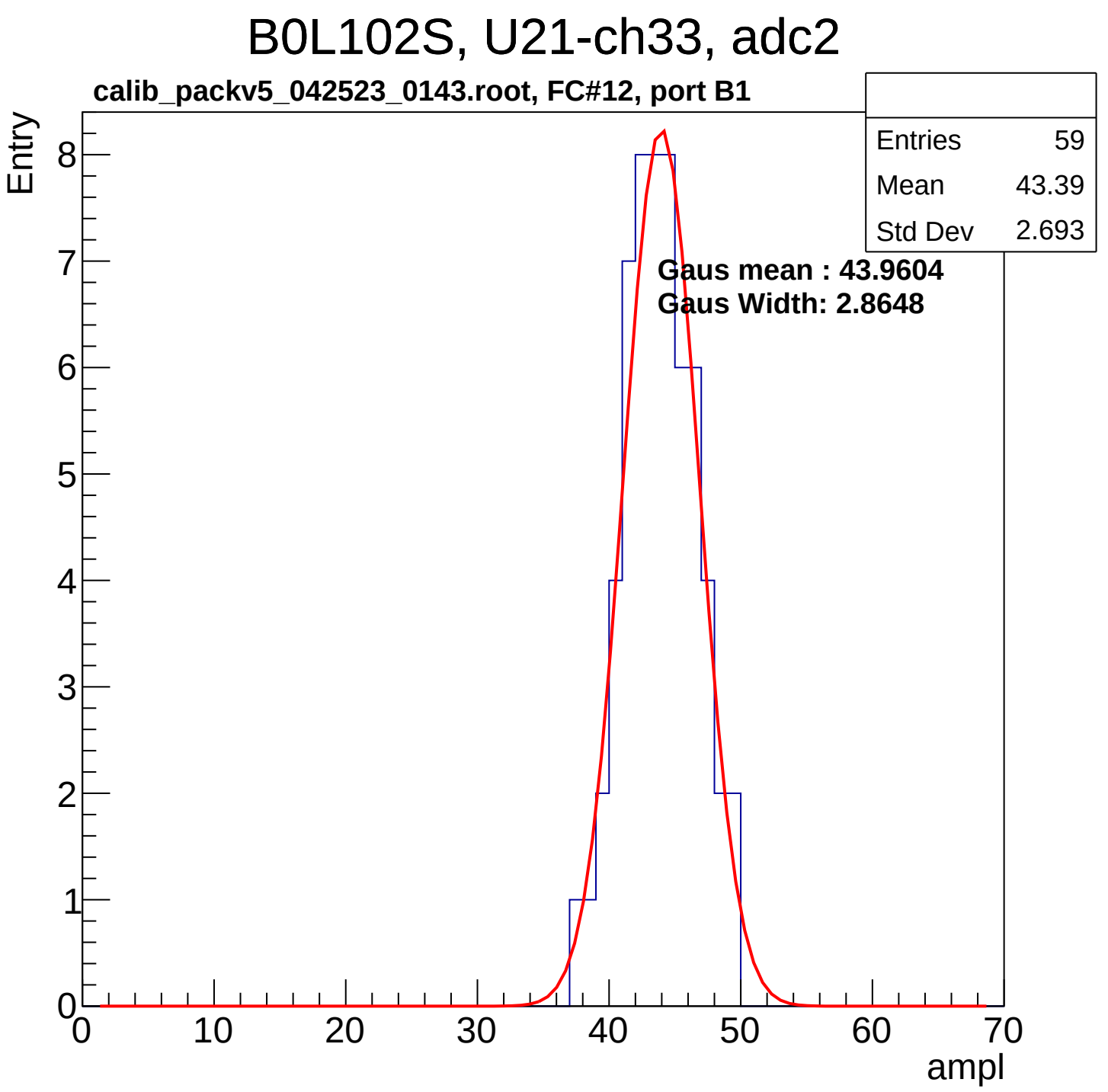

B0L102S, U21-ch32, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.8
Std Dev 2.615
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

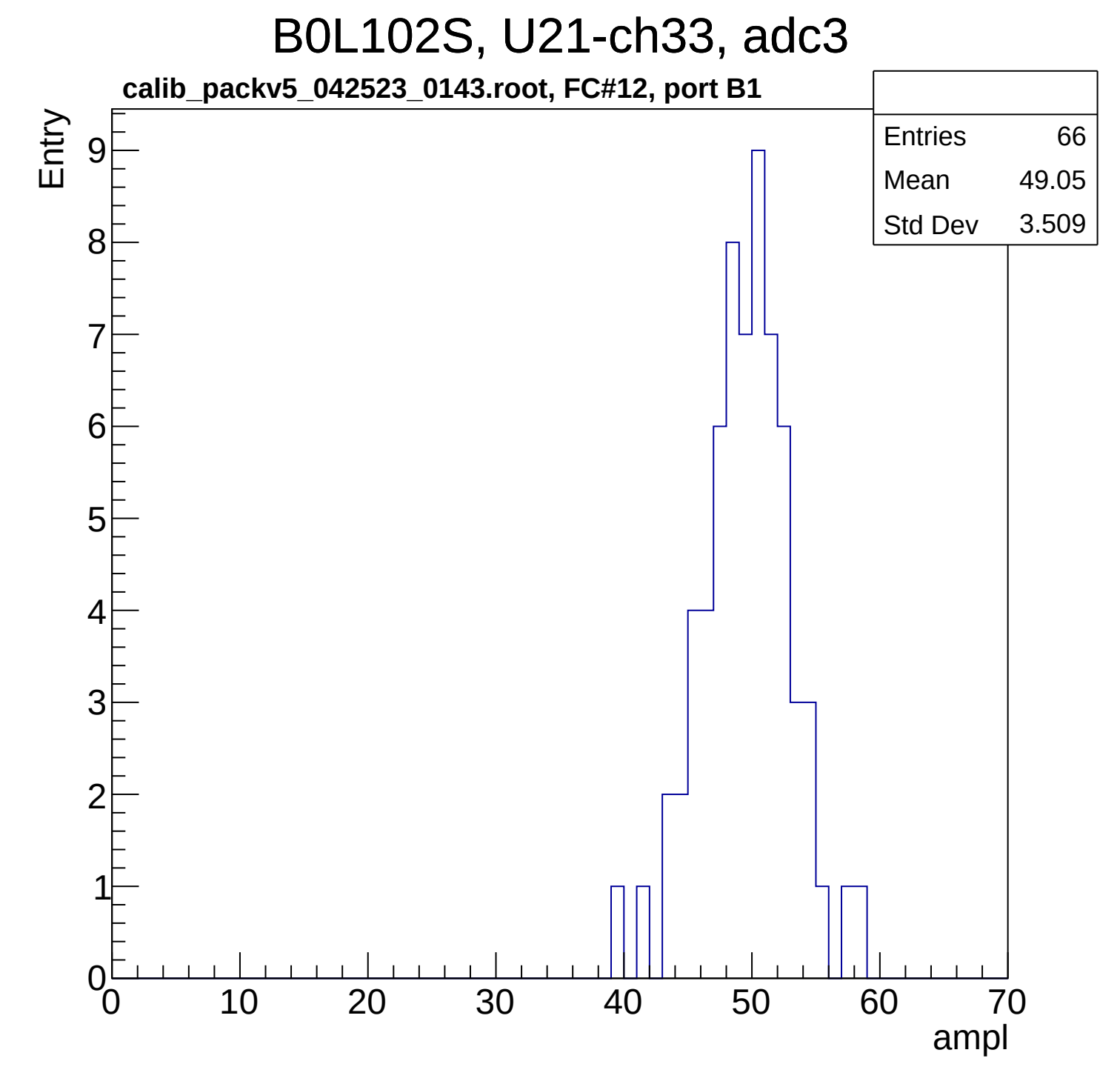

B0L102S, U21-ch32, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 22
Mean 58.77
Std Dev 12.89
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

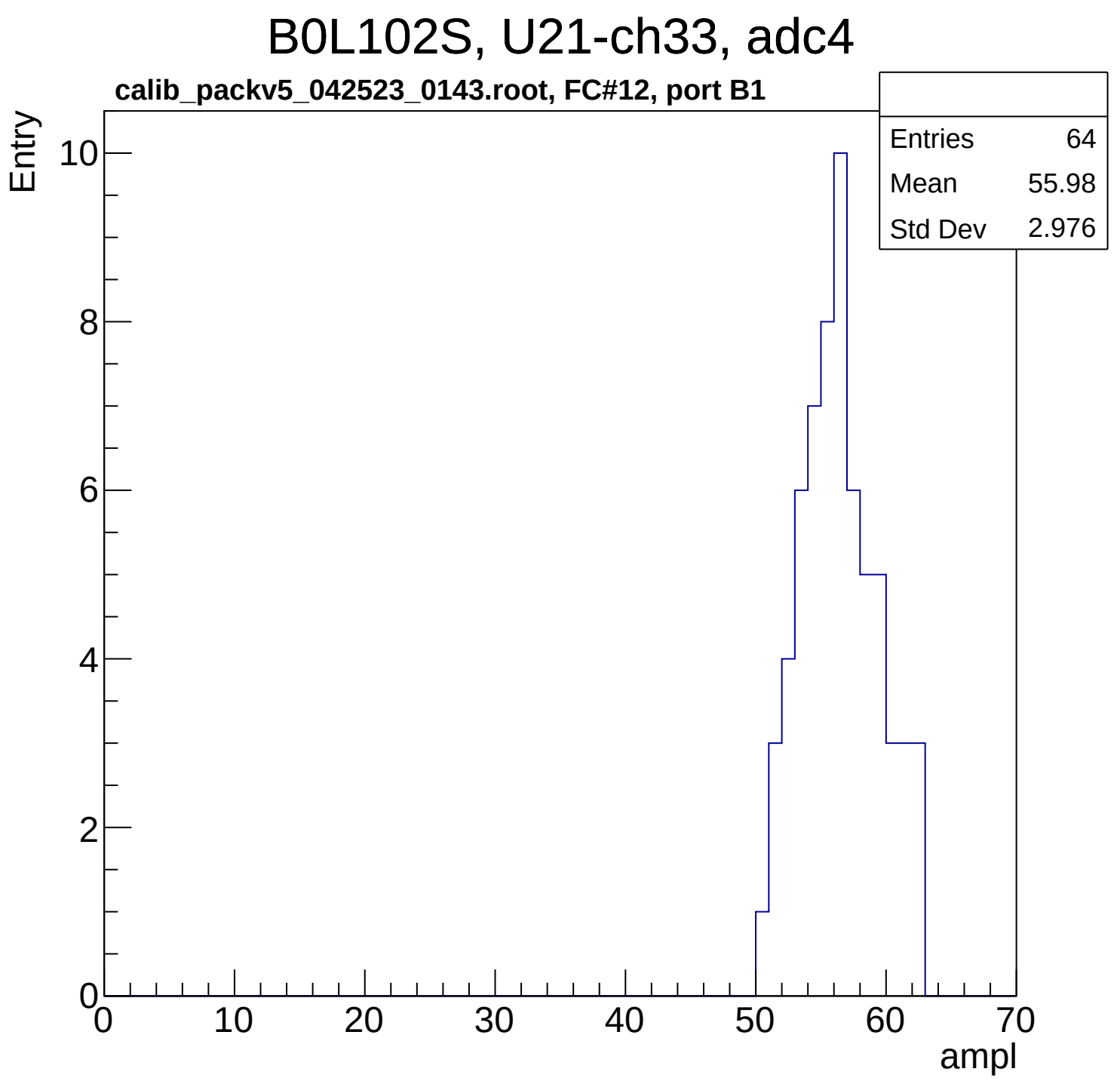

B0L102S, U21-ch32, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 26
Std Dev 26.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

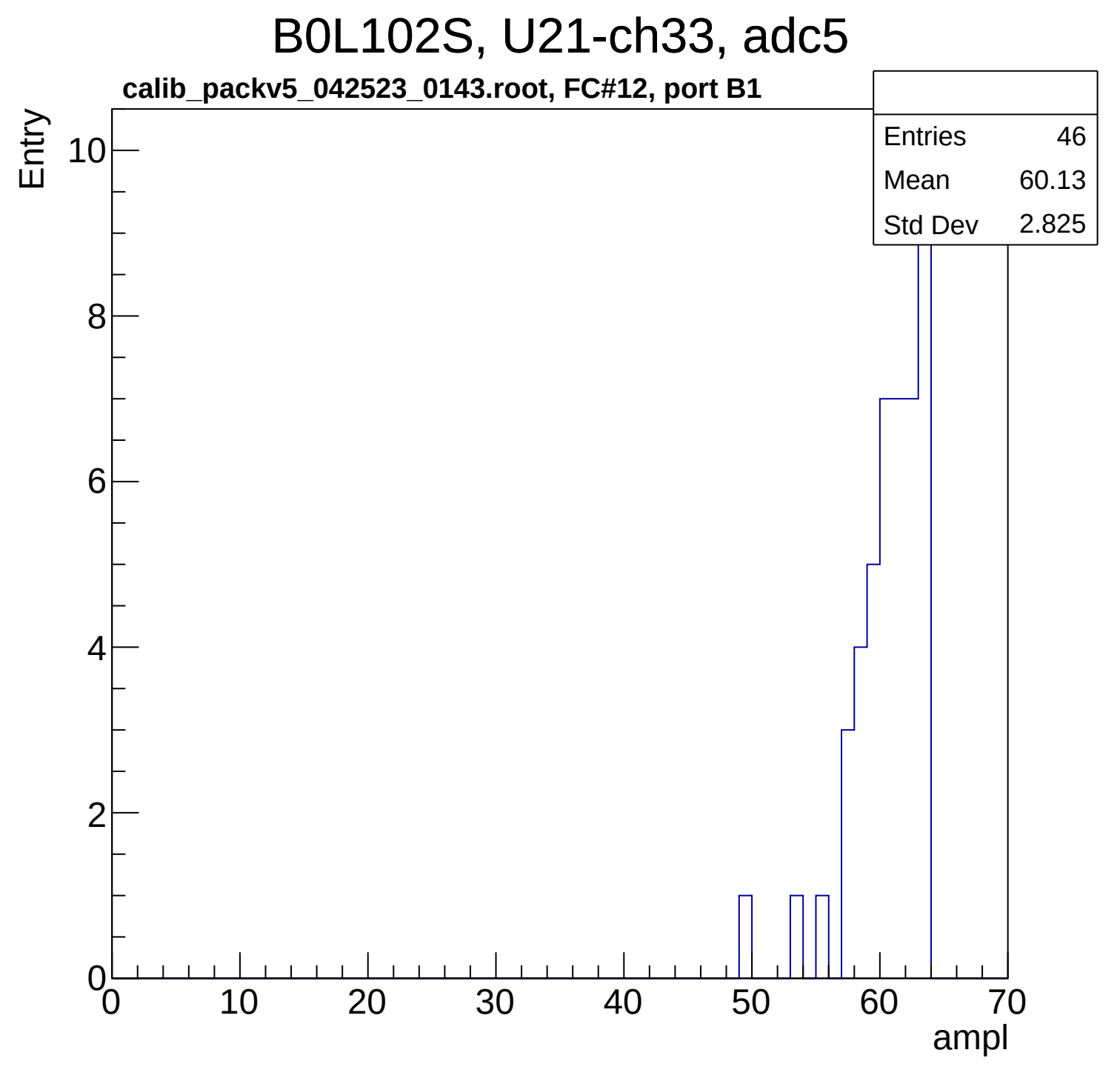

B0L102S, U21-ch33, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 29.55
Std Dev 4.928
Gaus mean : 30.2491
Gaus Width: 3.0269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

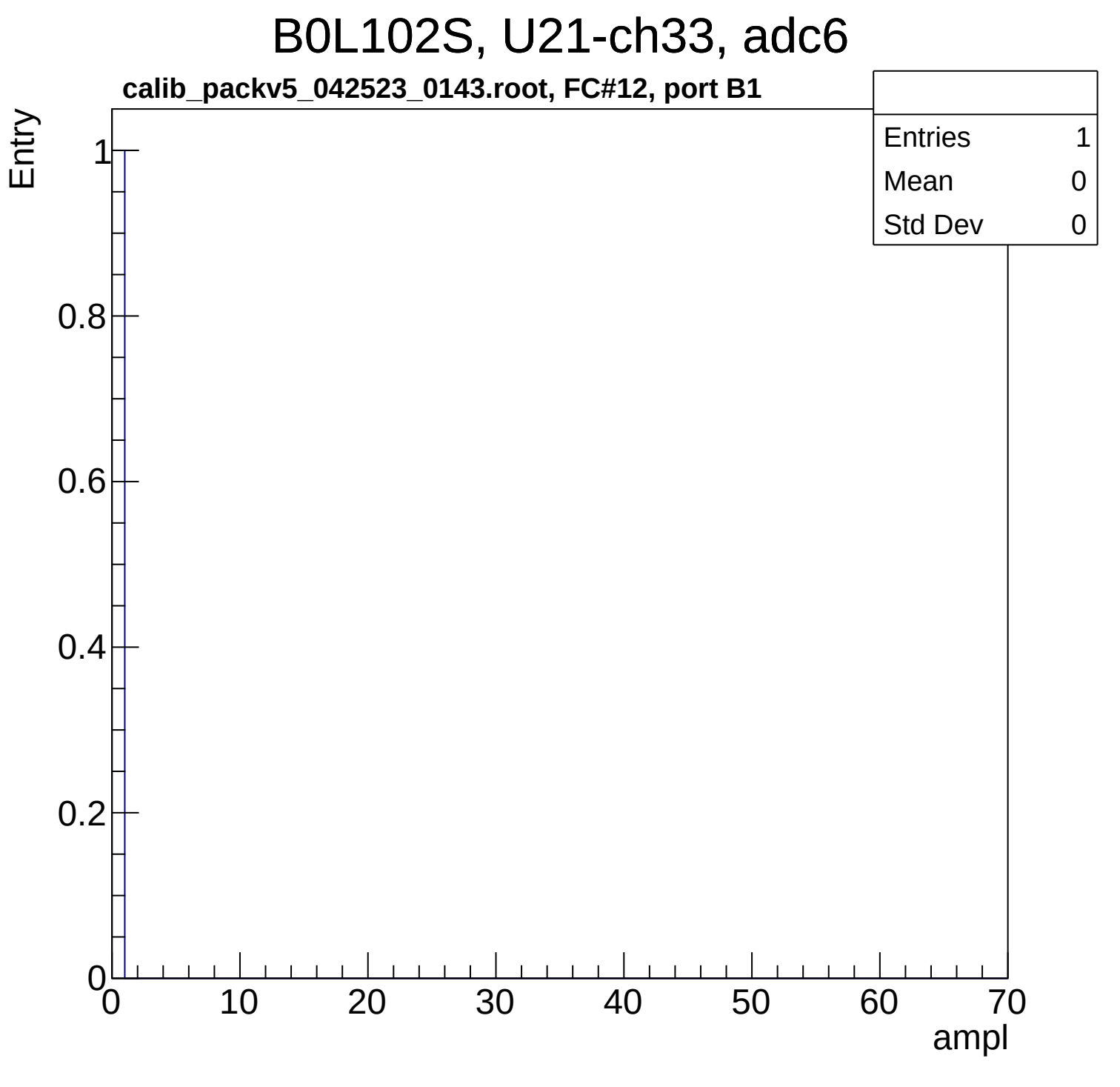

B0L102S, U21-ch33, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 35.99
Std Dev 3.16
Gaus mean : 37.0689
Gaus Width: 3.1170
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

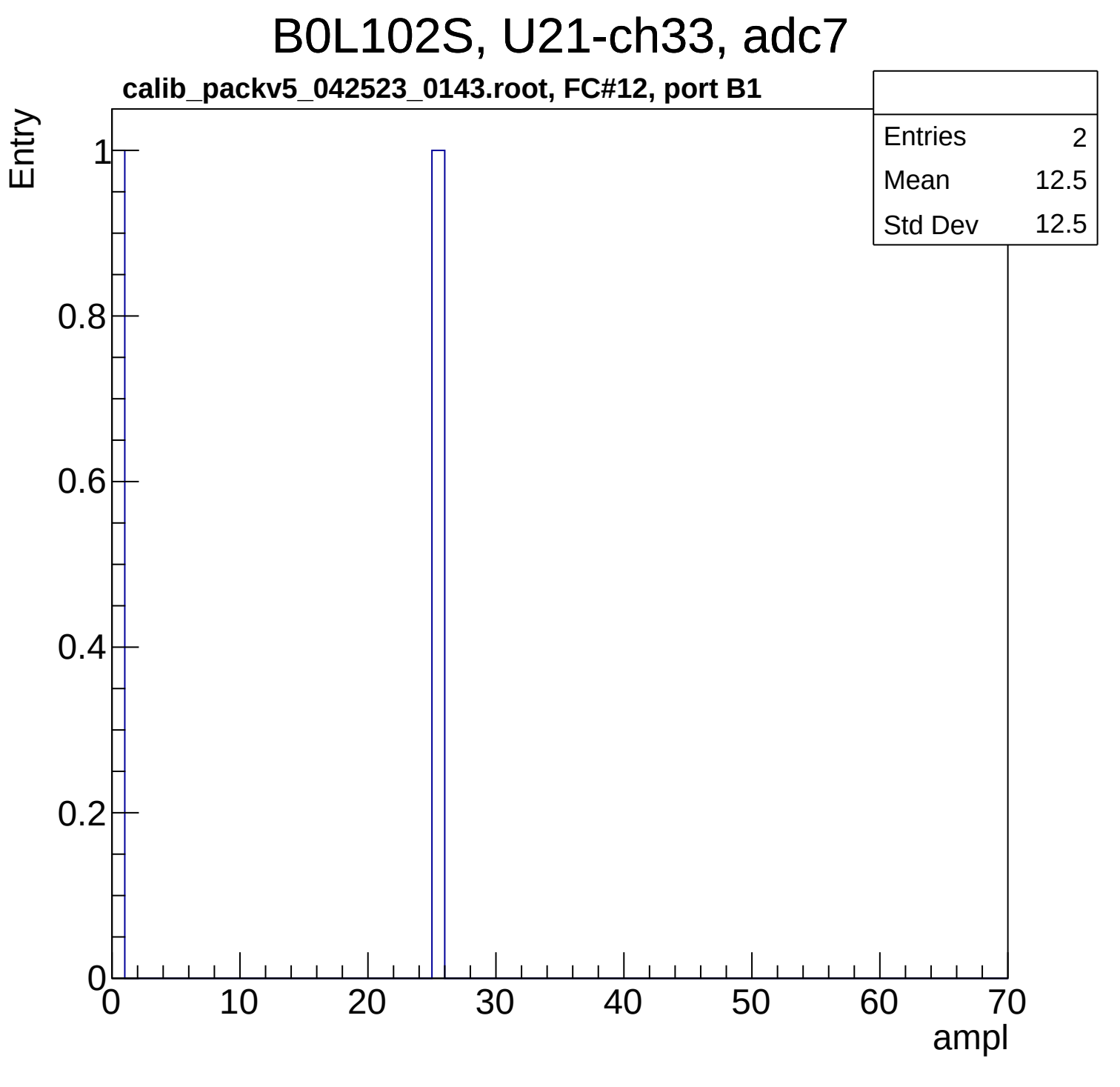

B0L102S, U21-ch33, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 43.39
Std Dev 2.693
Gaus mean : 43.9604
Gaus Width: 2.8648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch33, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.05
Std Dev 3.509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch33, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 55.98
Std Dev 2.976
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch33, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 60.13
Std Dev 2.825
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch33, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch33, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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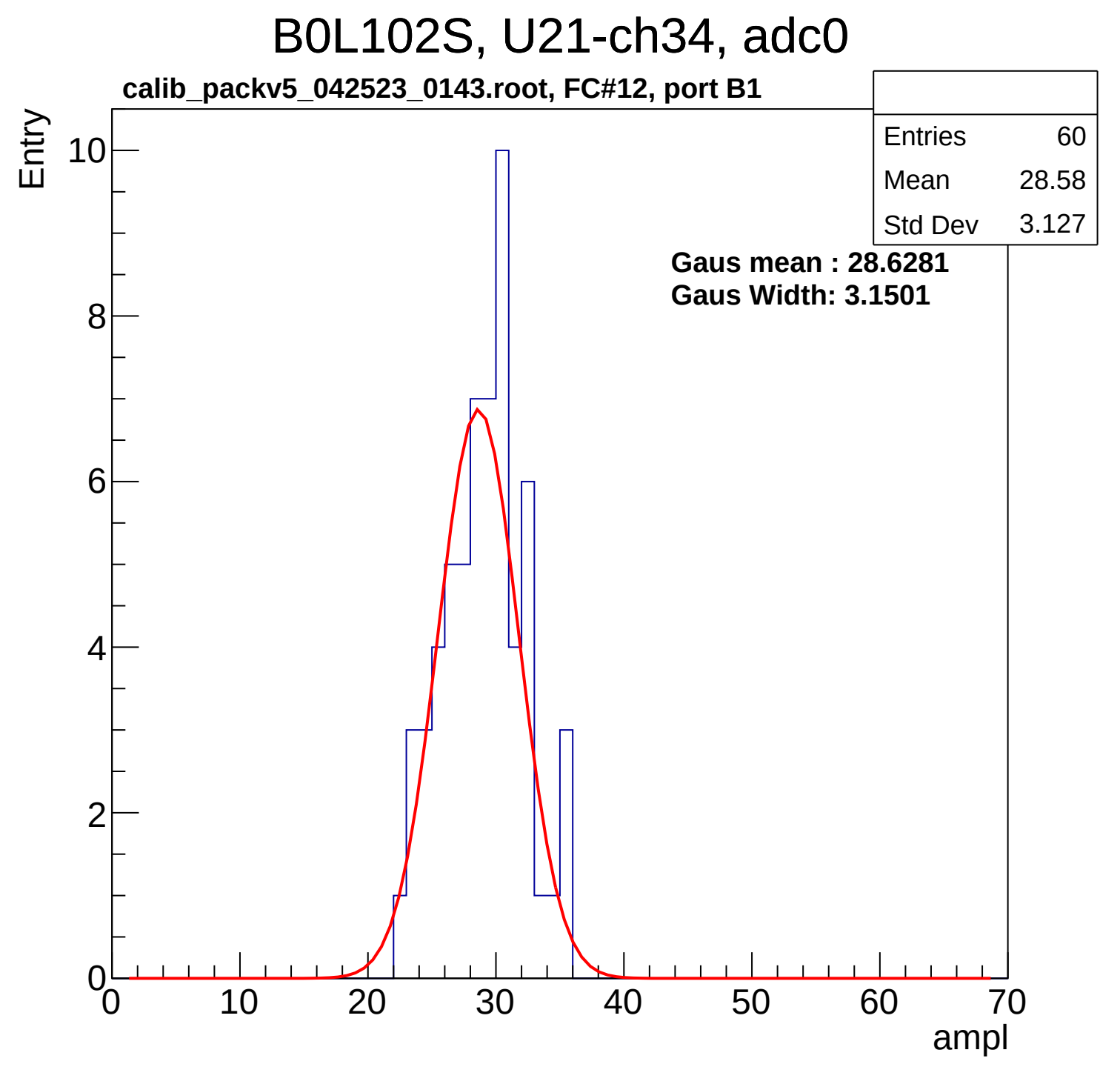
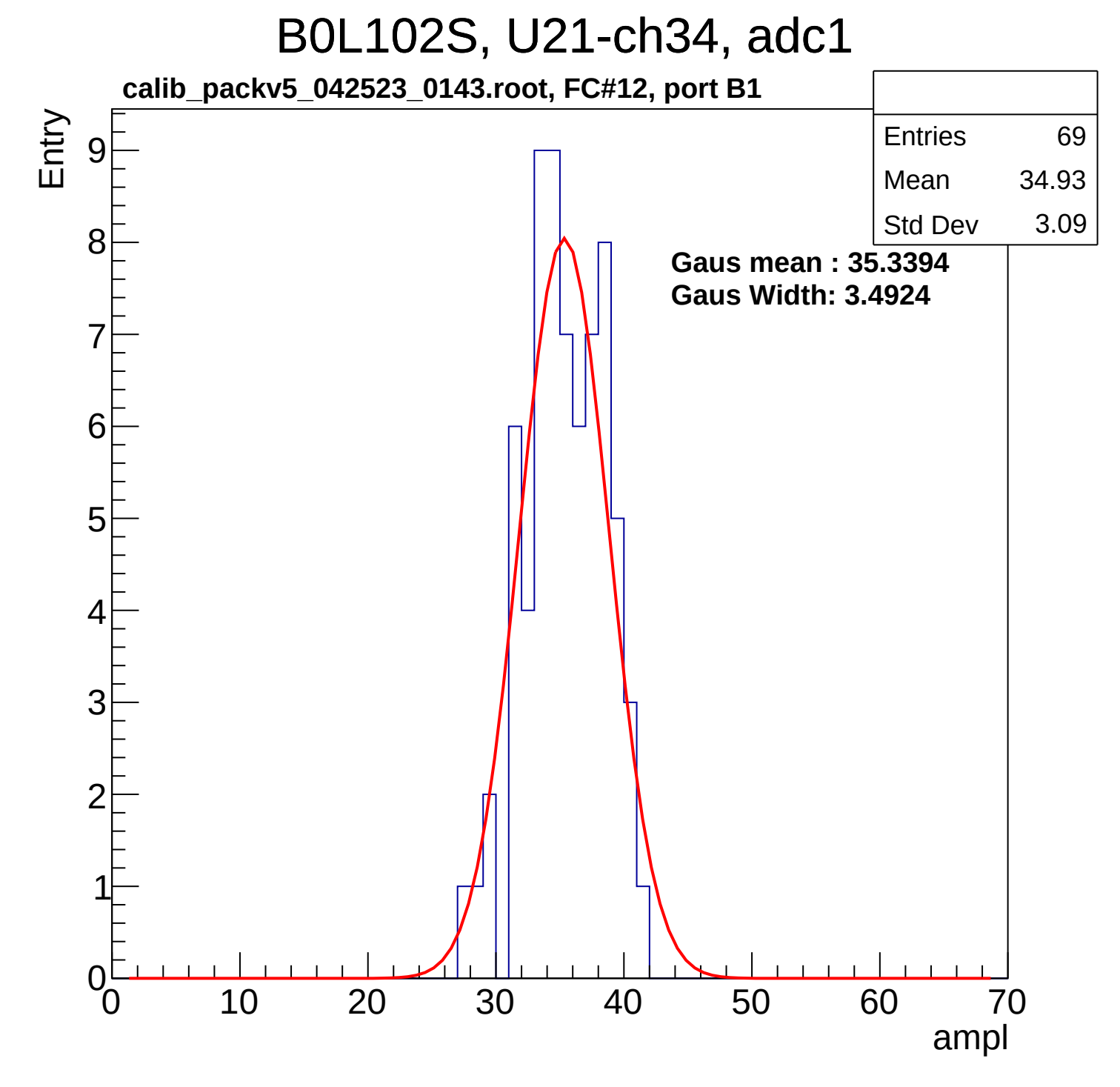
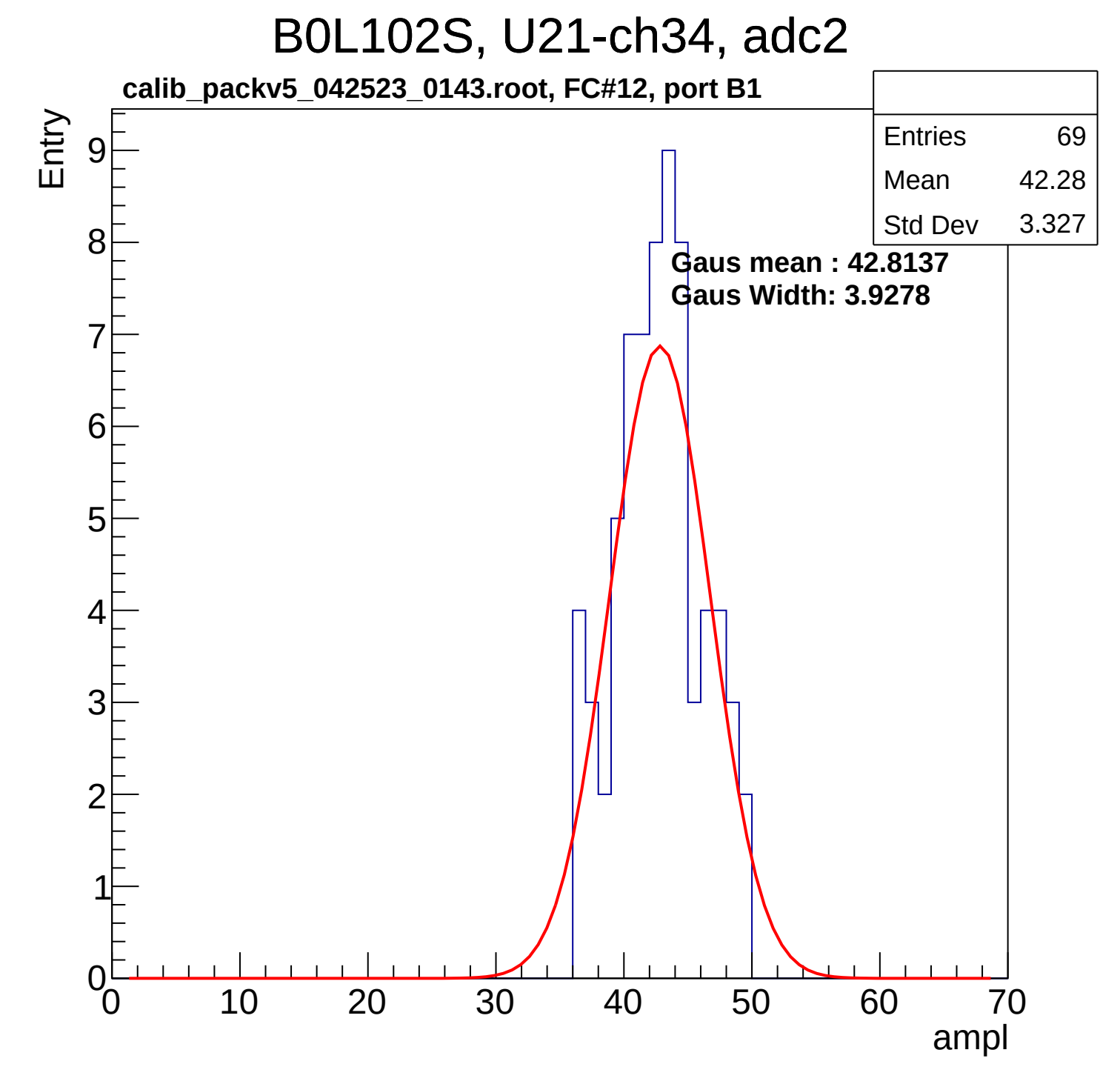
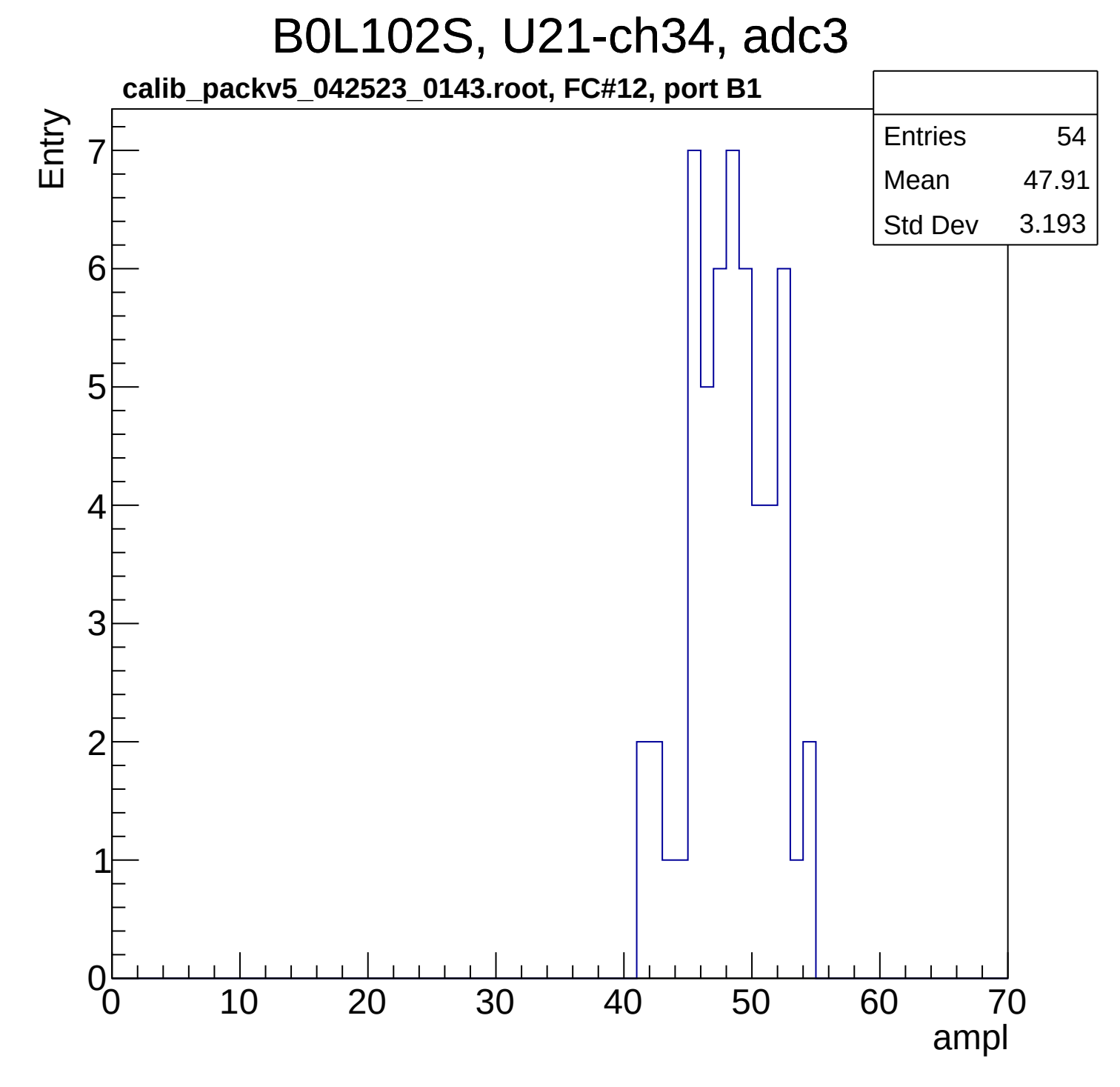
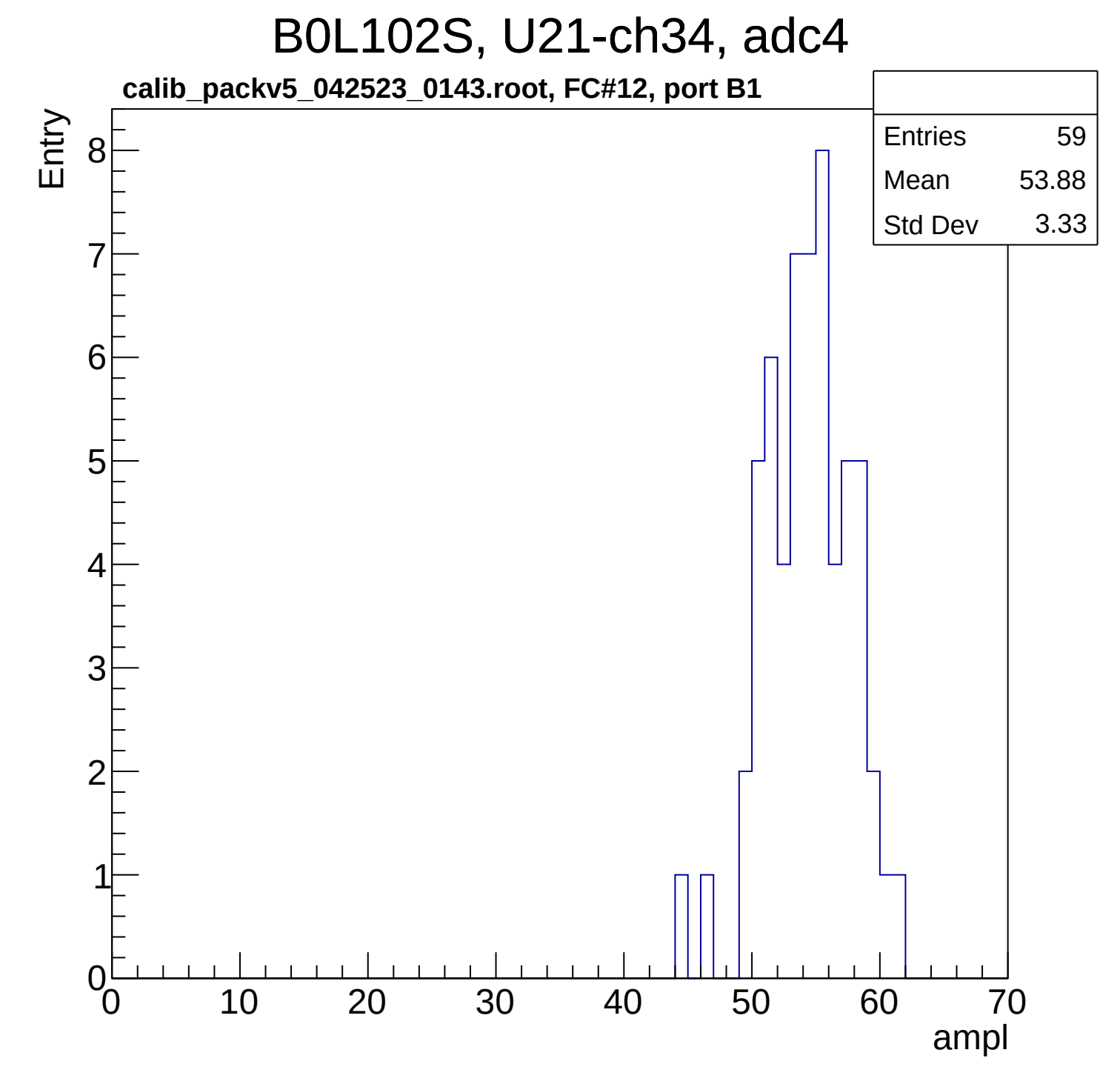
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch34, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 28.58
Std Dev 3.127
Gaus mean : 28.6281
Gaus Width: 3.1501
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch34, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 34.93
Std Dev 3.09
Gaus mean : 35.3394
Gaus Width: 3.4924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch34, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 42.28
Std Dev 3.327
Gaus mean : 42.8137
Gaus Width: 3.9278
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch34, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 47.91
Std Dev 3.193
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch34, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 53.88
Std Dev 3.33
Entry
ampl
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4
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7
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0
10
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40
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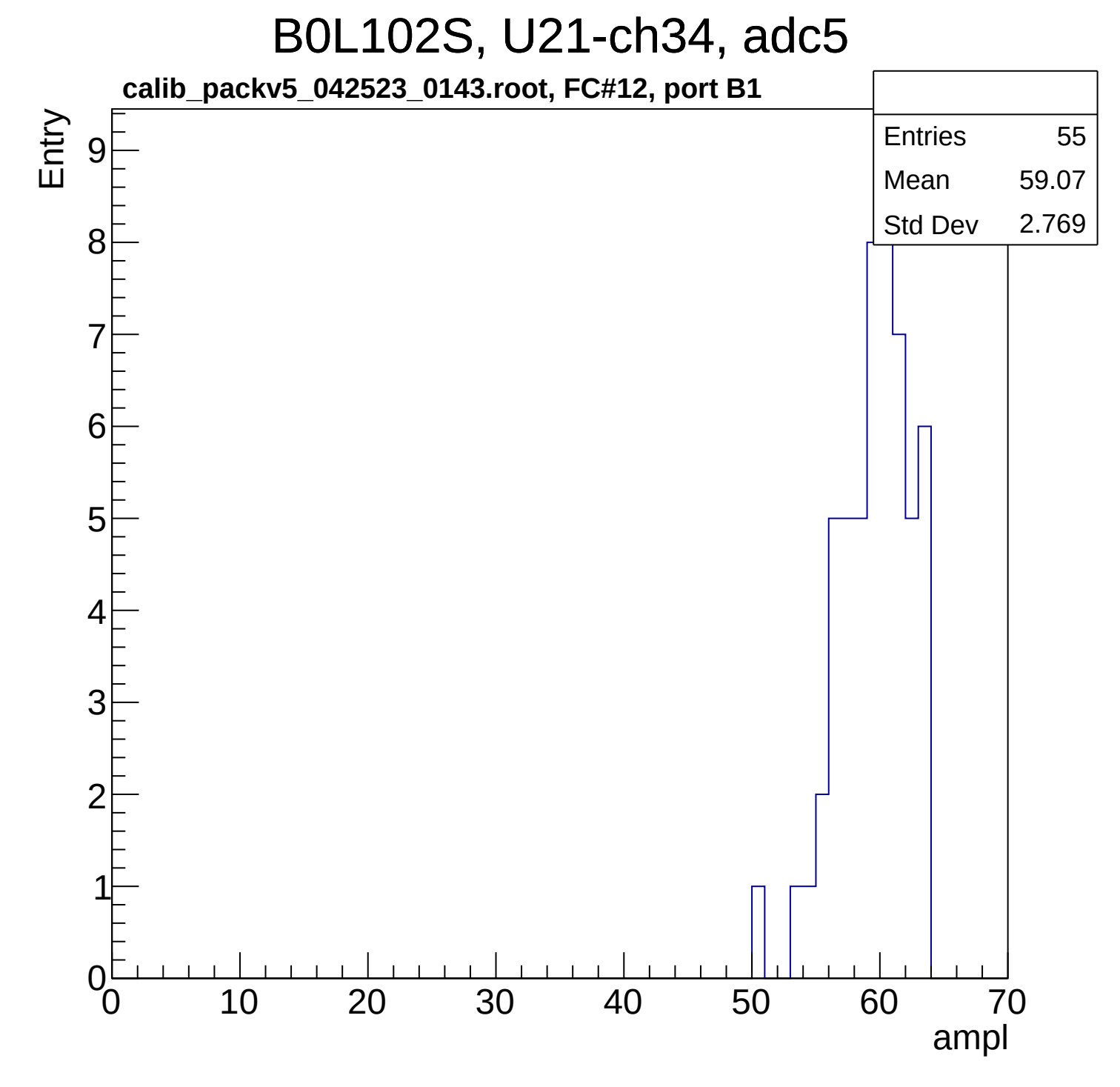
B0L102S, U21-ch34, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 59.07
Std Dev 2.769
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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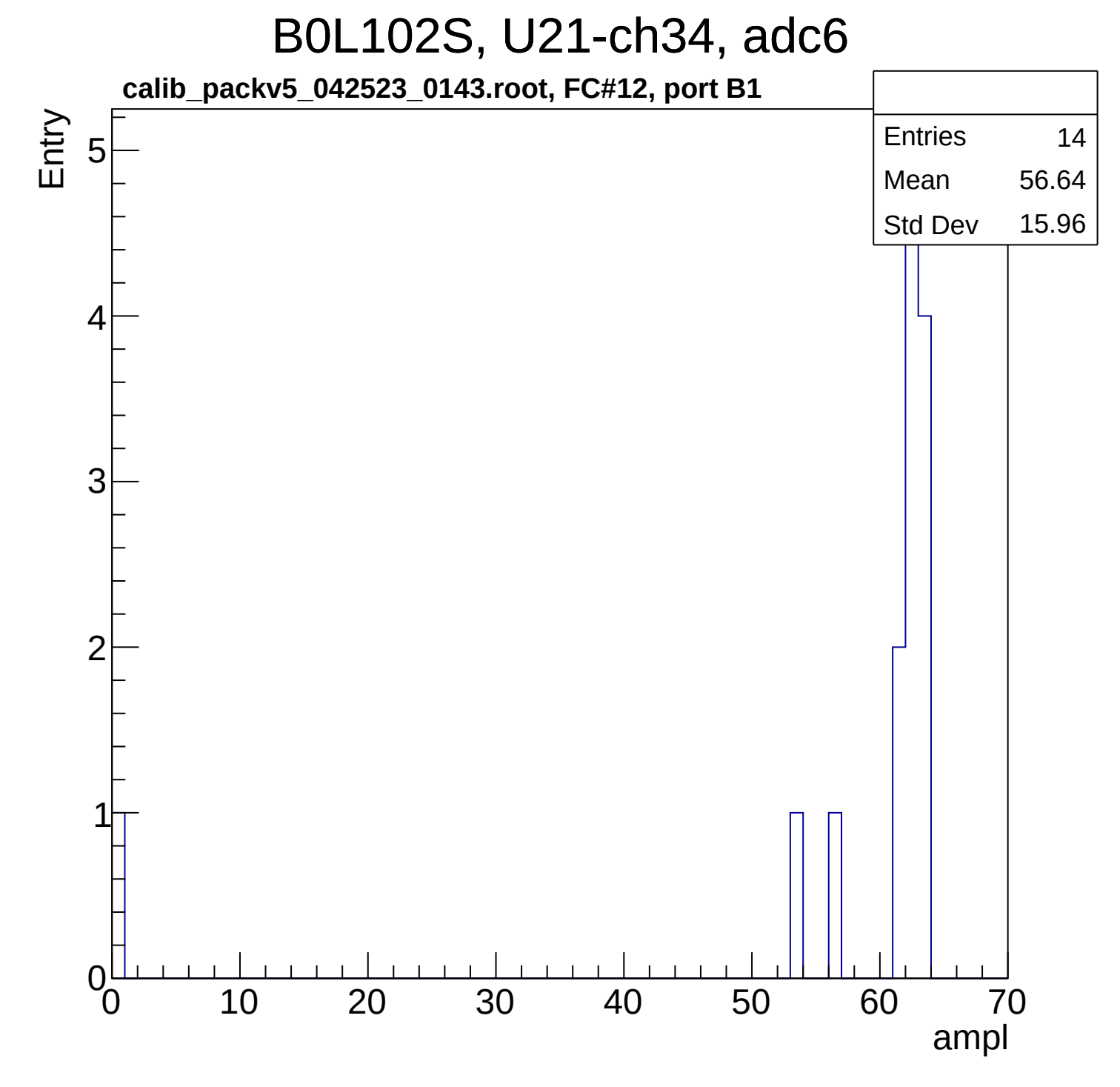
50
60
70


B0L102S, U21-ch34, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 56.64
Std Dev 15.96
Entry
ampl
0
1
2
3
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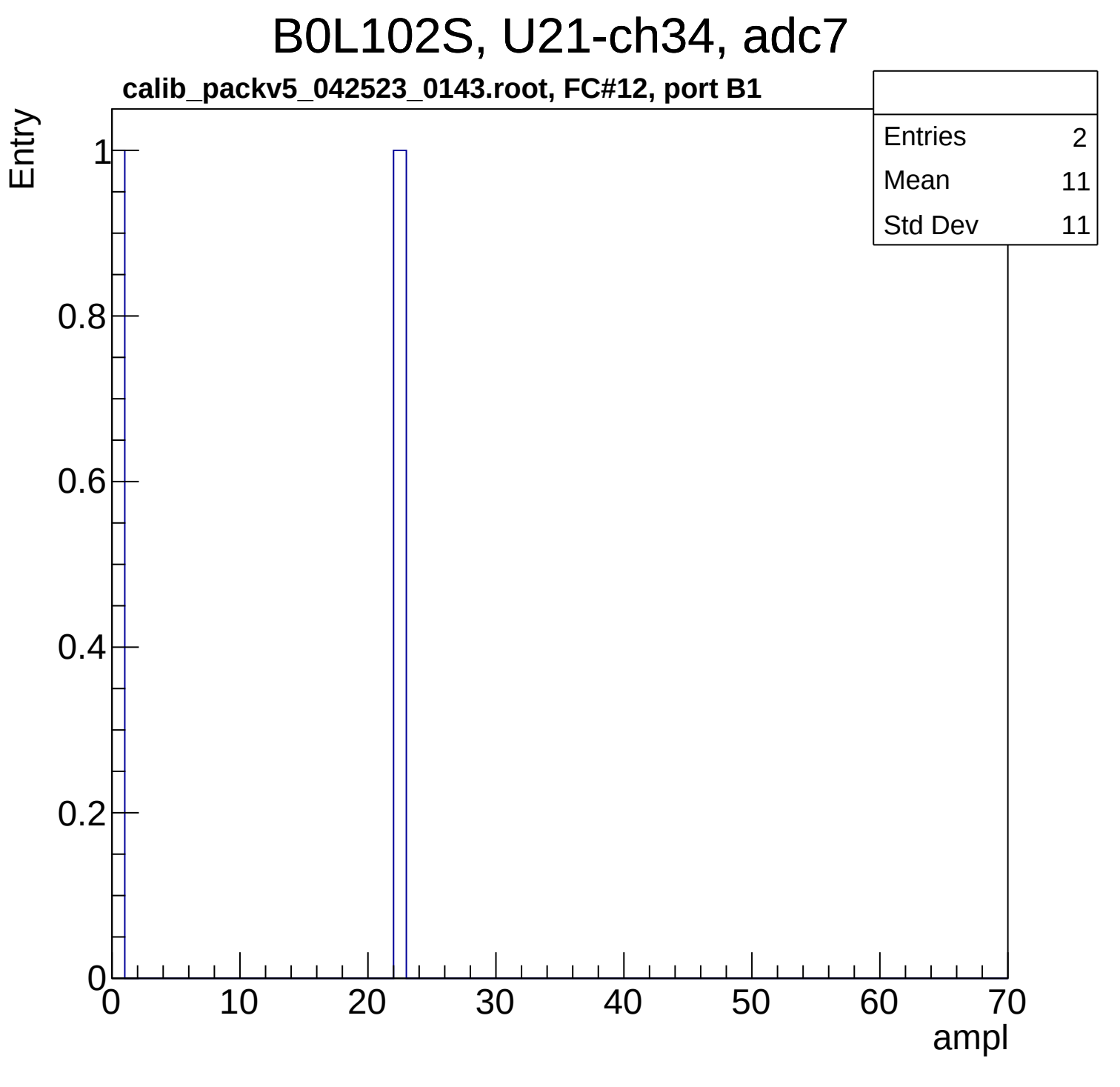
B0L102S, U21-ch34, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
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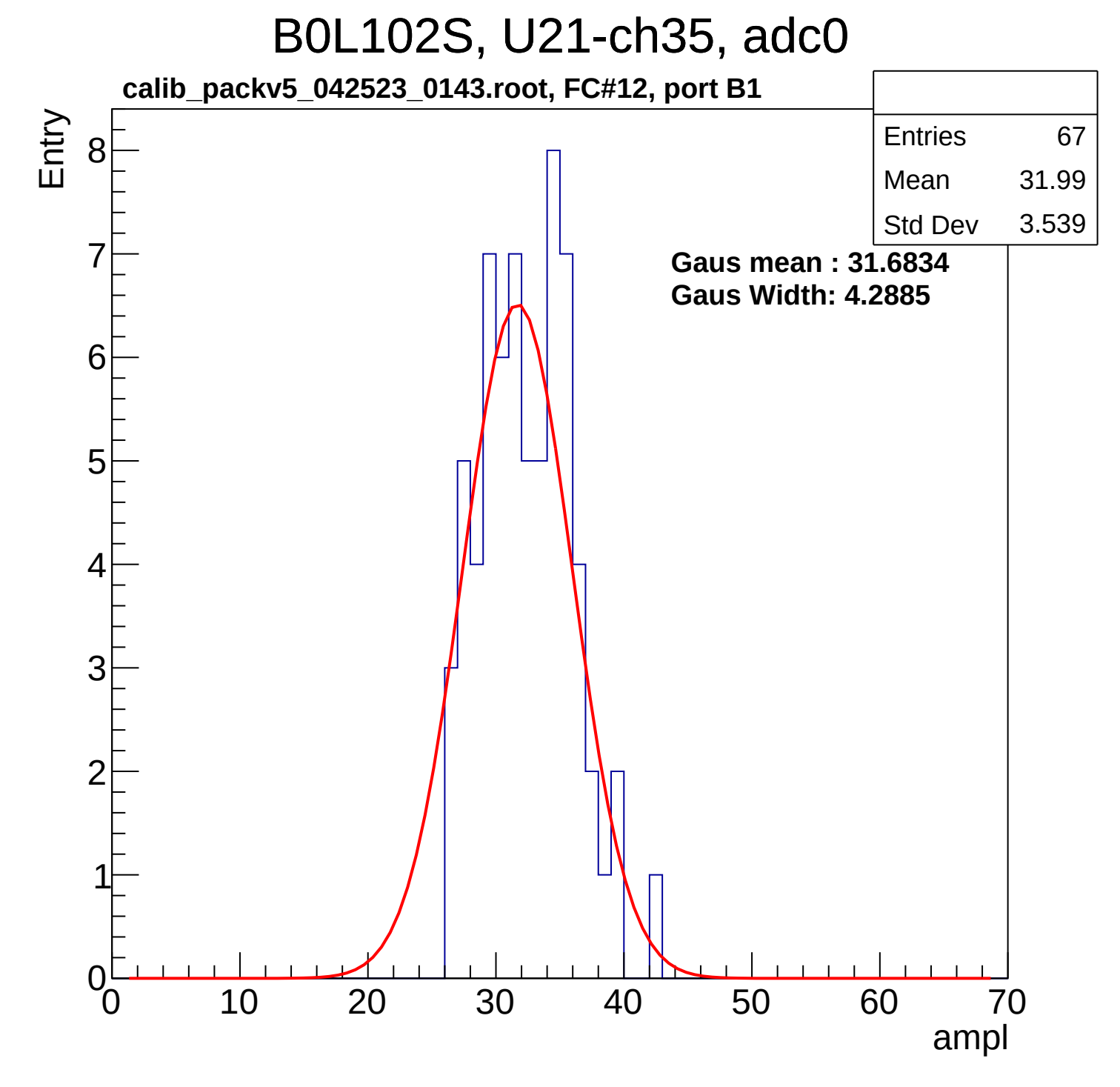
ampl


B0L102S, U21-ch35, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 31.99
Std Dev 3.539
Gaus mean : 31.6834
Gaus Width: 4.2885
Entry
ampl
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2
3
4
5
6
7
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0
10
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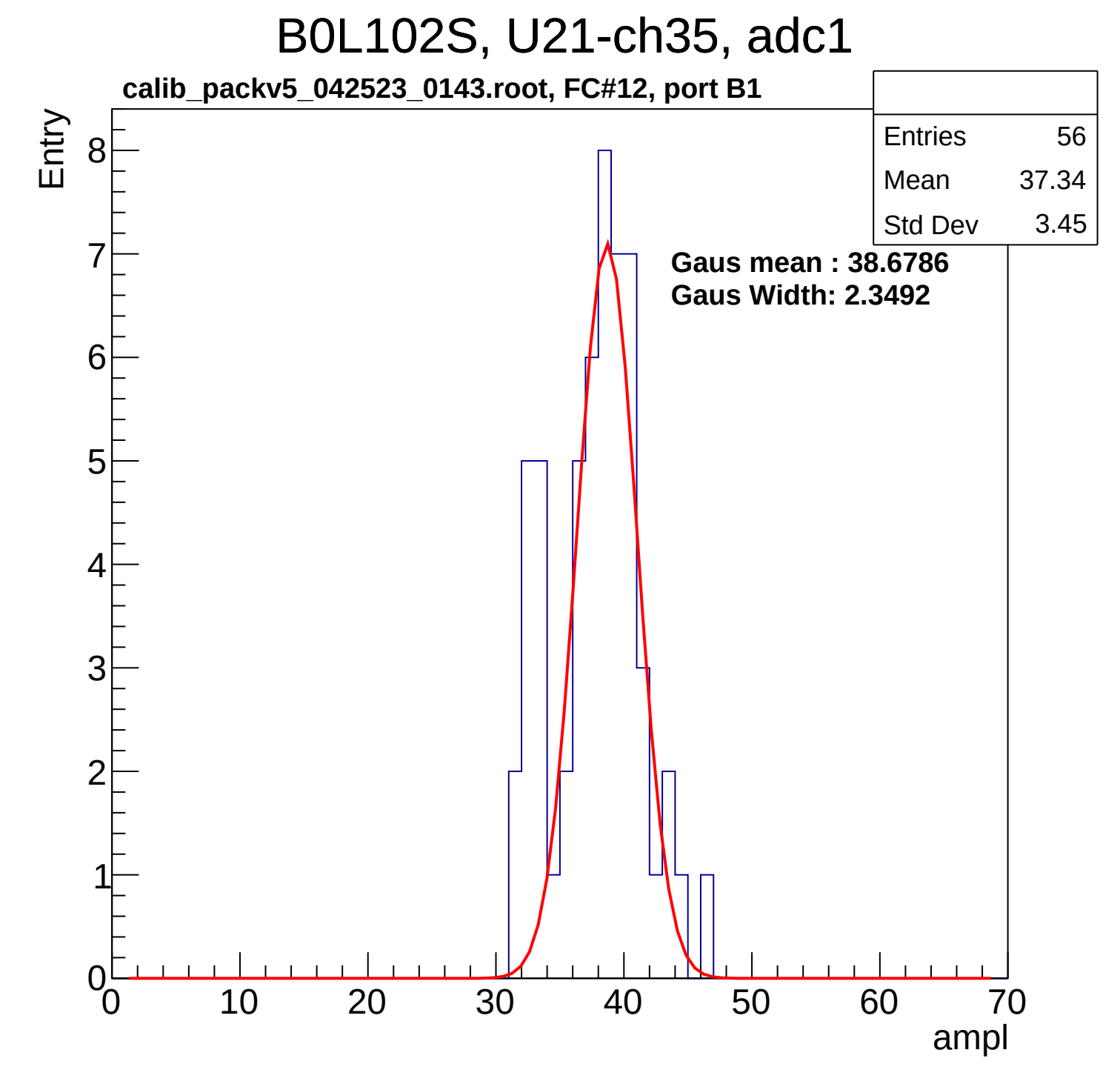
B0L102S, U21-ch35, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 37.34
Std Dev 3.45
Gaus mean : 38.6786
Gaus Width: 2.3492
Entry
ampl
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2
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4
5
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0
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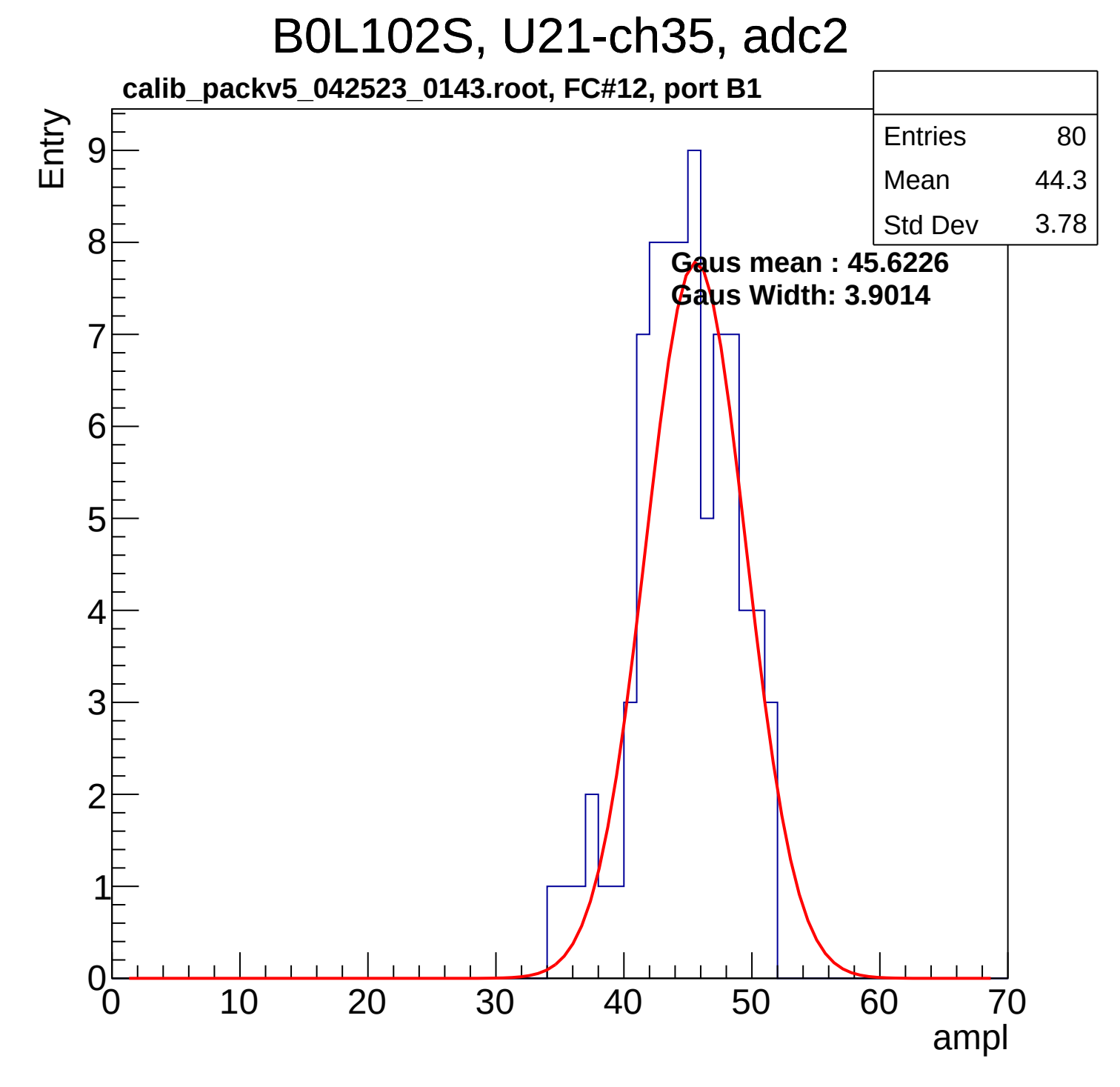
B0L102S, U21-ch35, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 44.3
Std Dev 3.78
Gaus mean : 45.6226
Gaus Width: 3.9014
Entry
ampl
0
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2
3
4
5
6
7
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0
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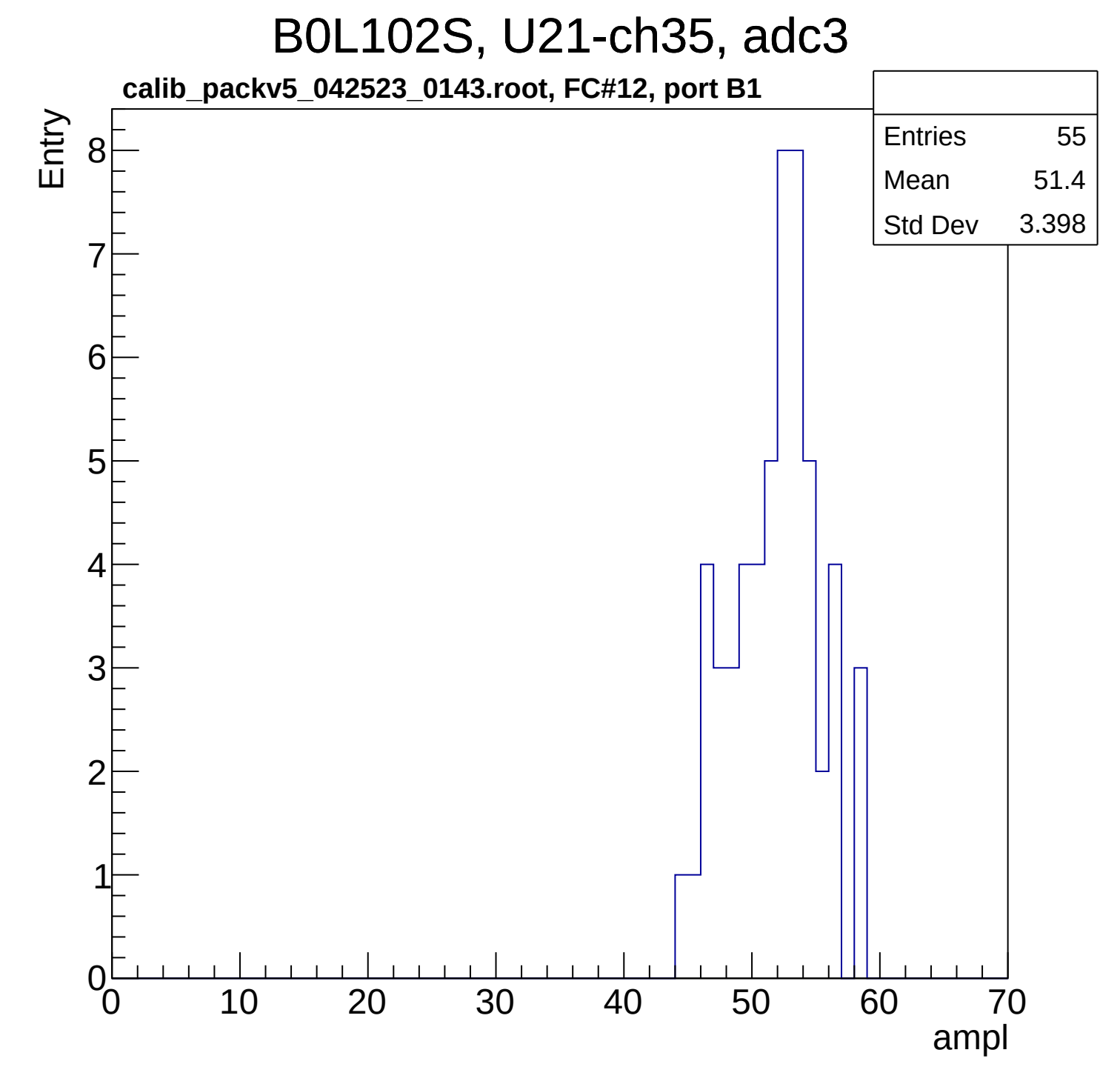
B0L102S, U21-ch35, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 51.4
Std Dev 3.398
Entry
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2
3
4
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6
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8
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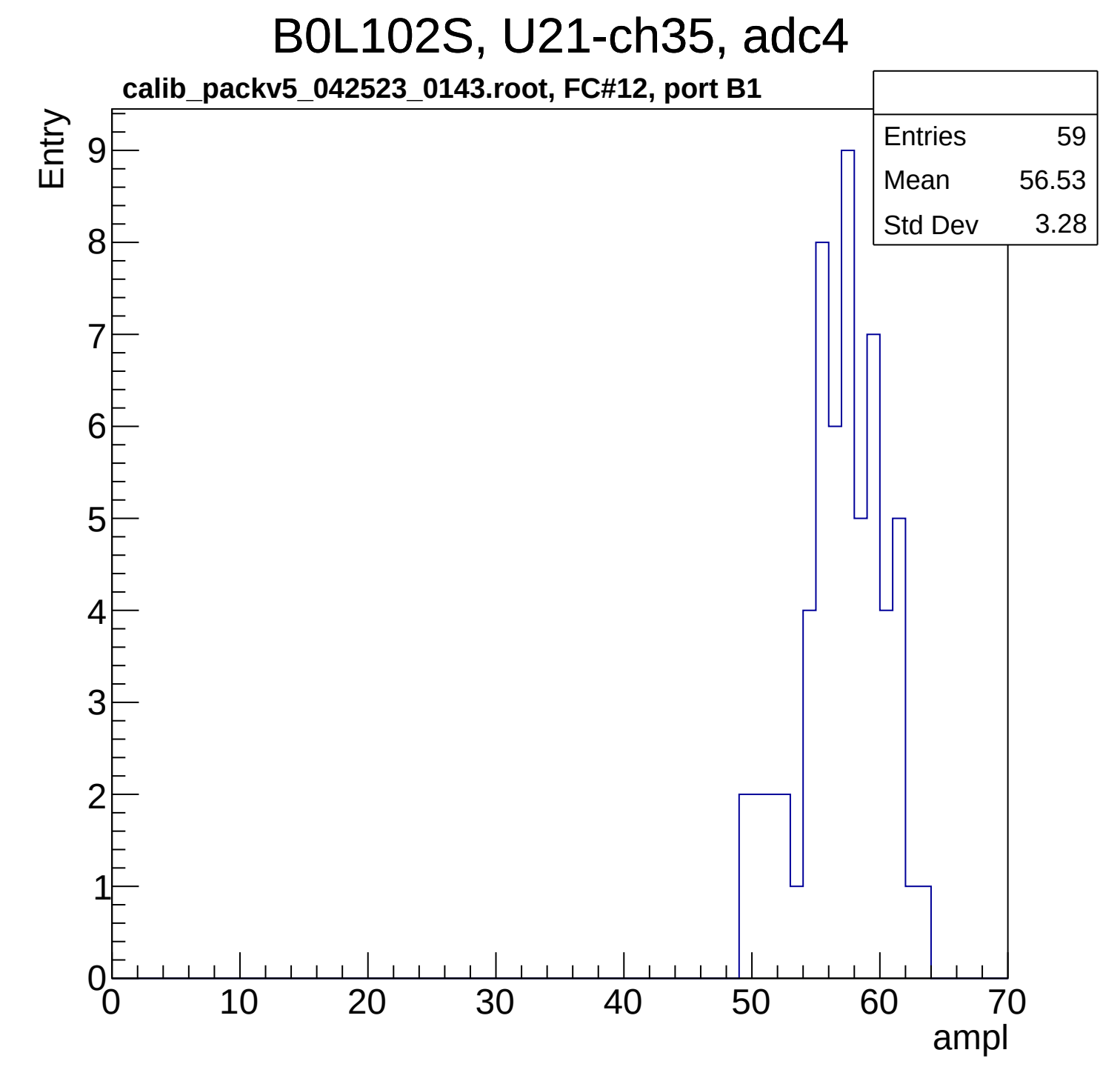
70
ampl


B0L102S, U21-ch35, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 56.53
Std Dev 3.28
Entry
ampl
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1
2
3
4
5
6
7
8
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0
10
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60
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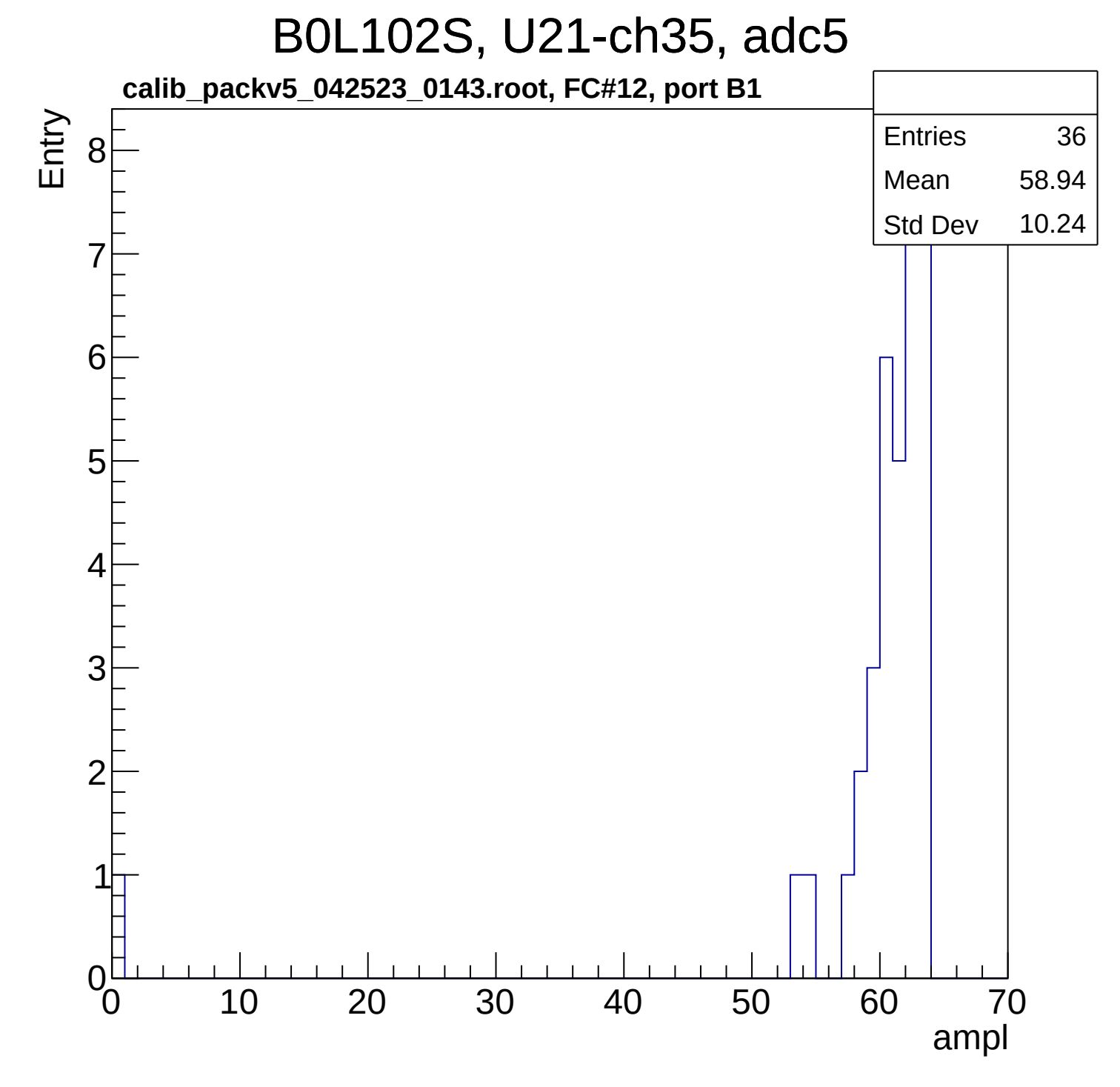
B0L102S, U21-ch35, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.94
Std Dev 10.24
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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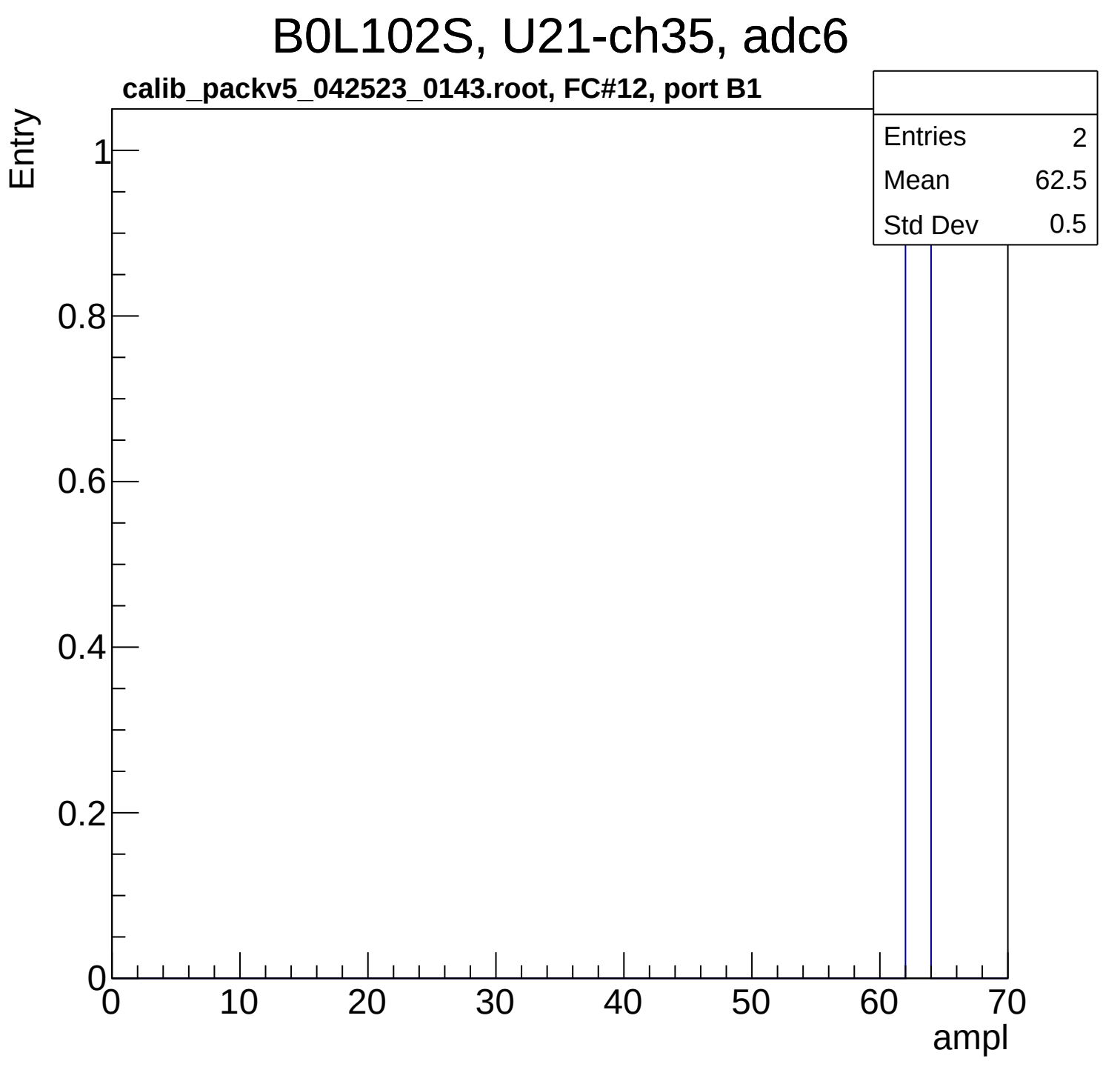
B0L102S, U21-ch35, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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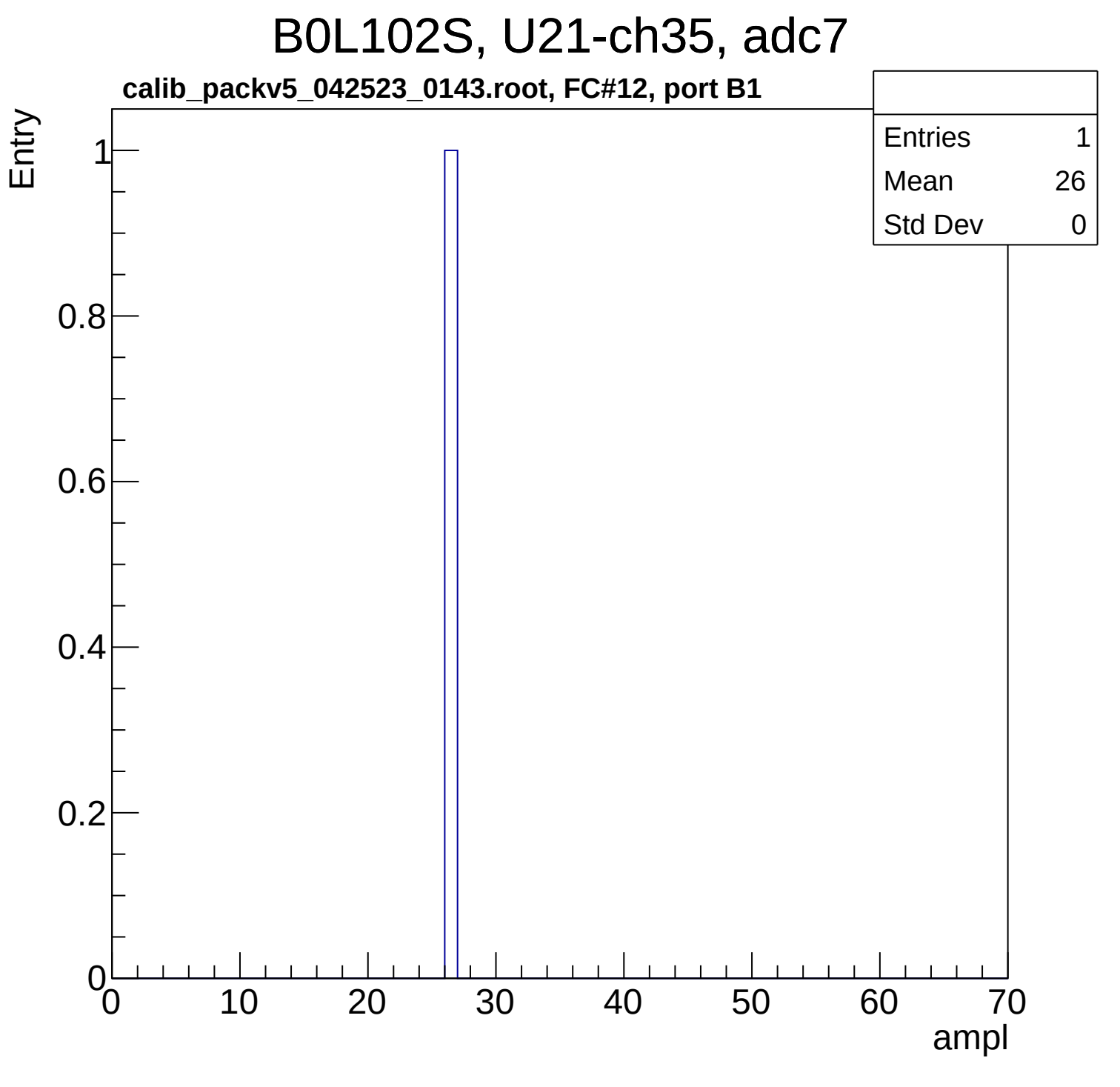
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch35, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 26
Std Dev 0
Entry
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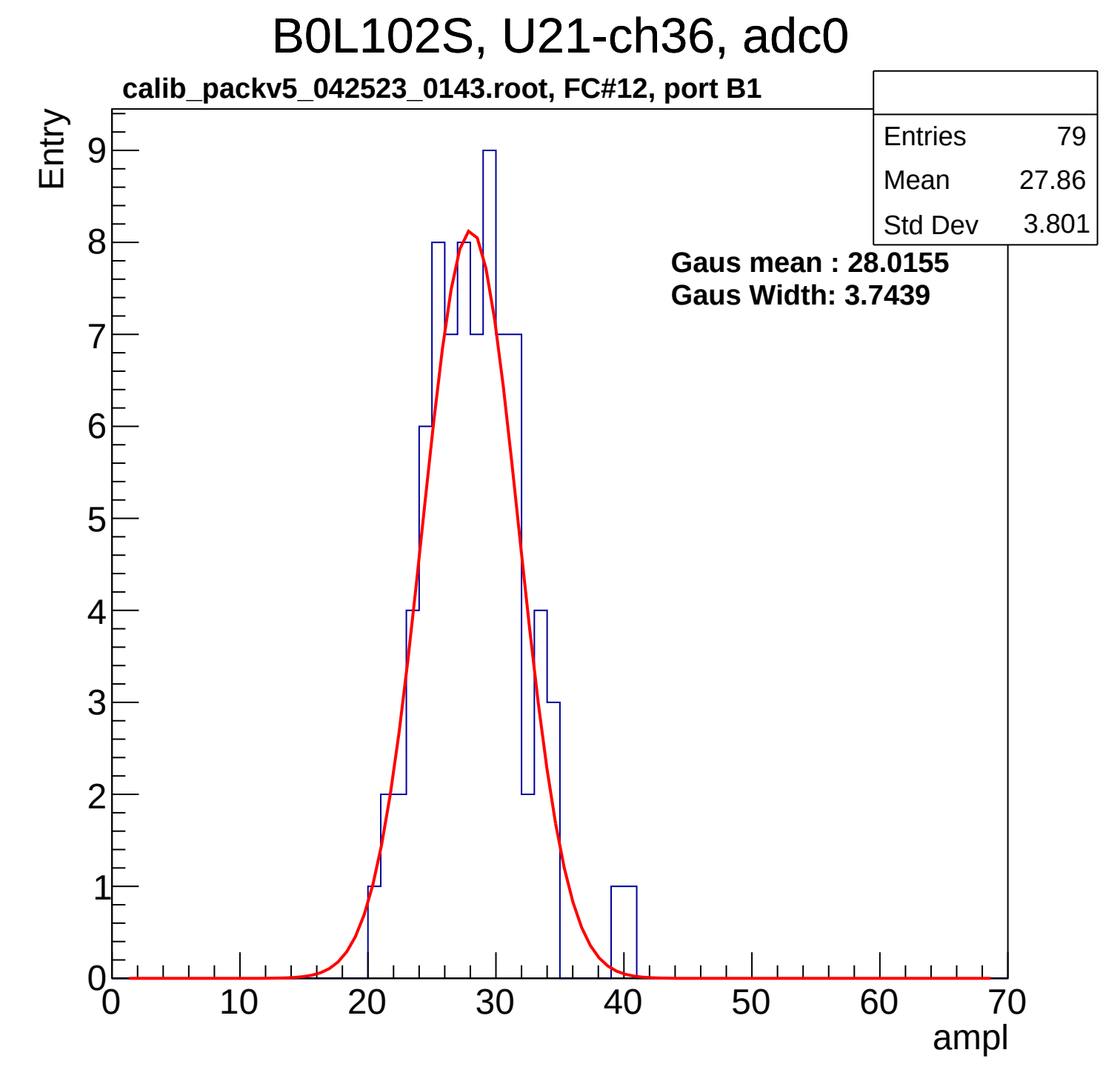
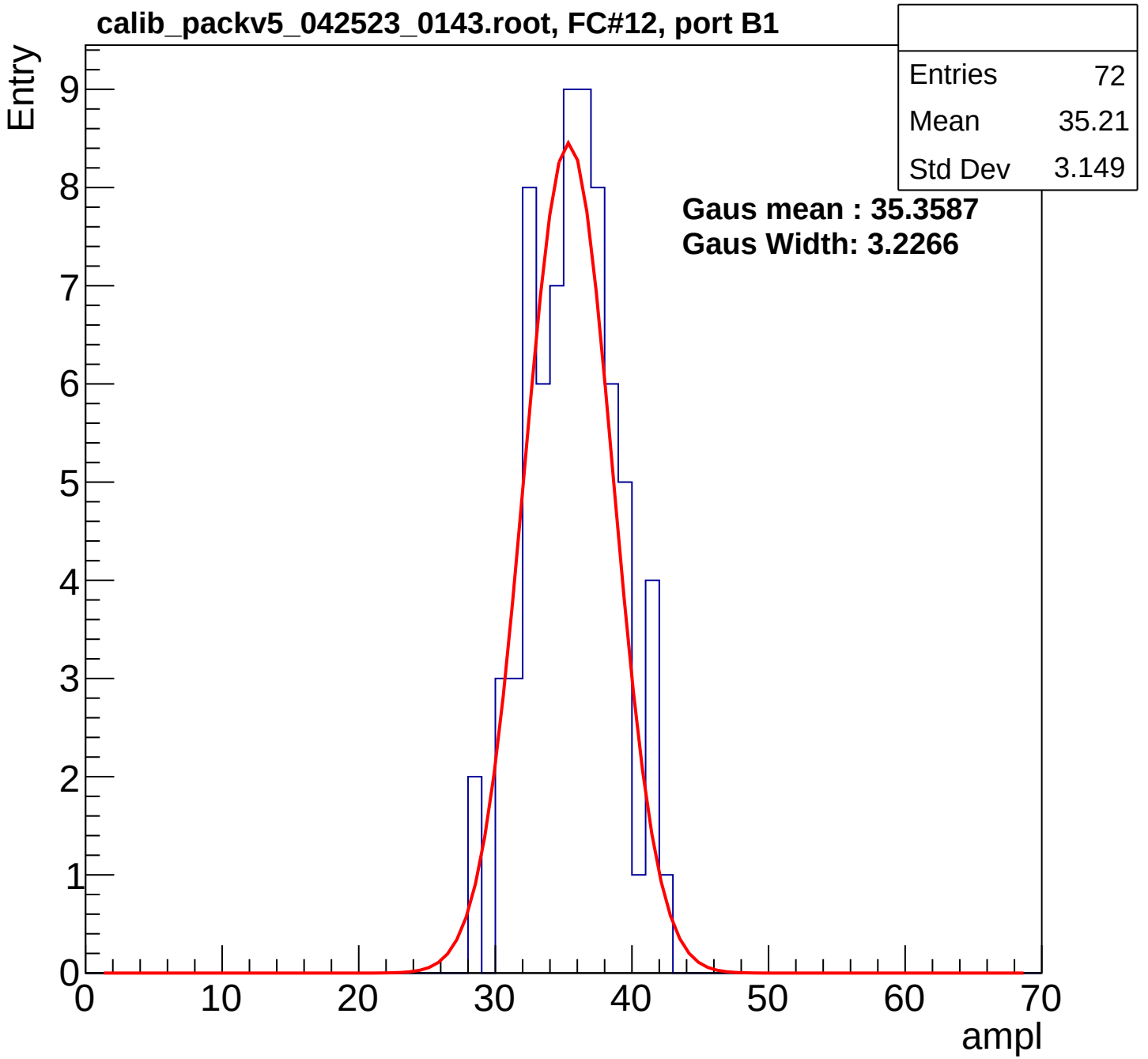
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch36, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 27.86
Std Dev 3.801
Gaus mean : 28.0155
Gaus Width: 3.7439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch36, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 35.21
Std Dev 3.149
Gaus mean : 35.3587
Gaus Width: 3.2266
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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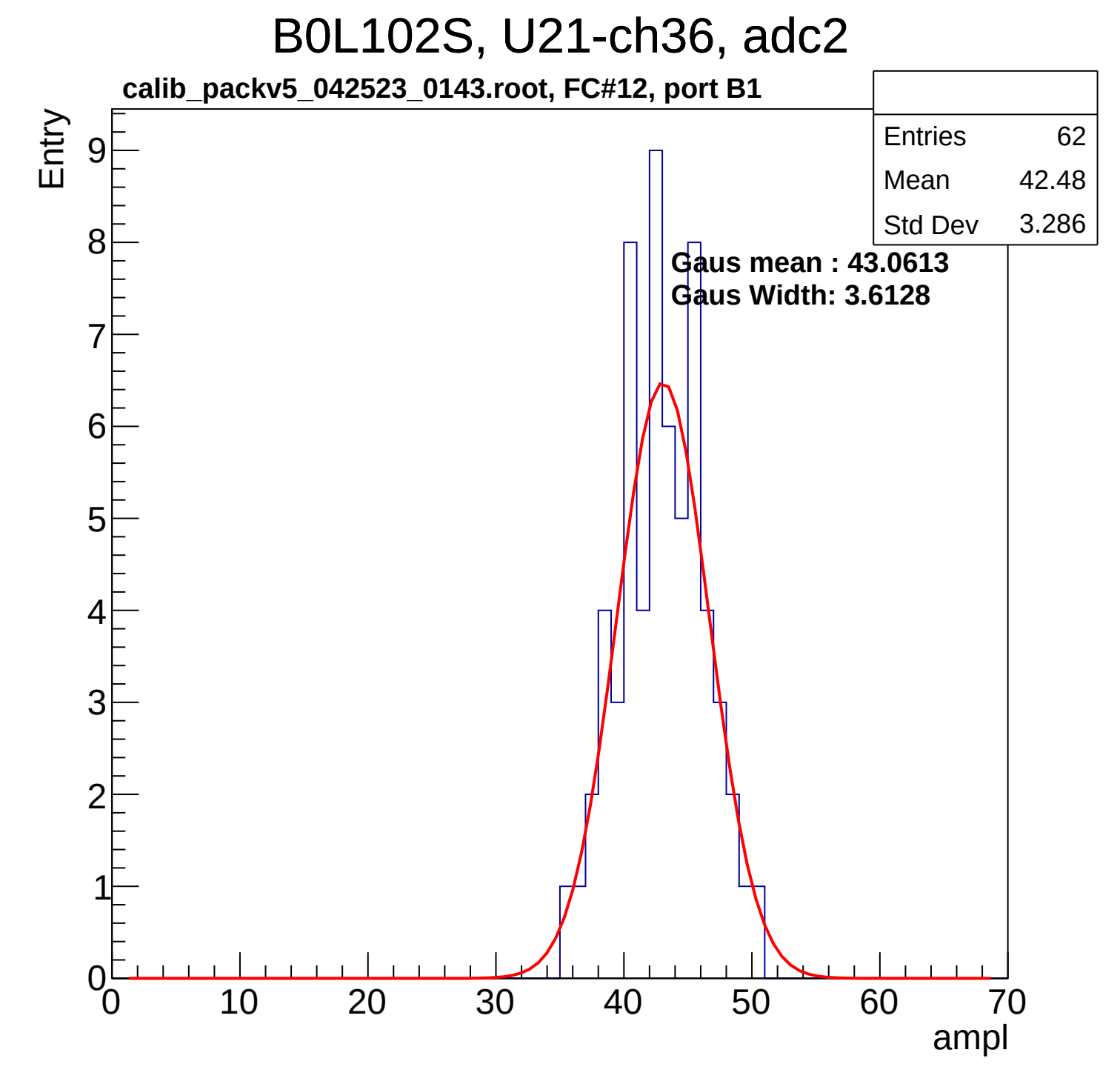
B0L102S, U21-ch36, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.48
Std Dev 3.286
Gaus mean : 43.0613
Gaus Width: 3.6128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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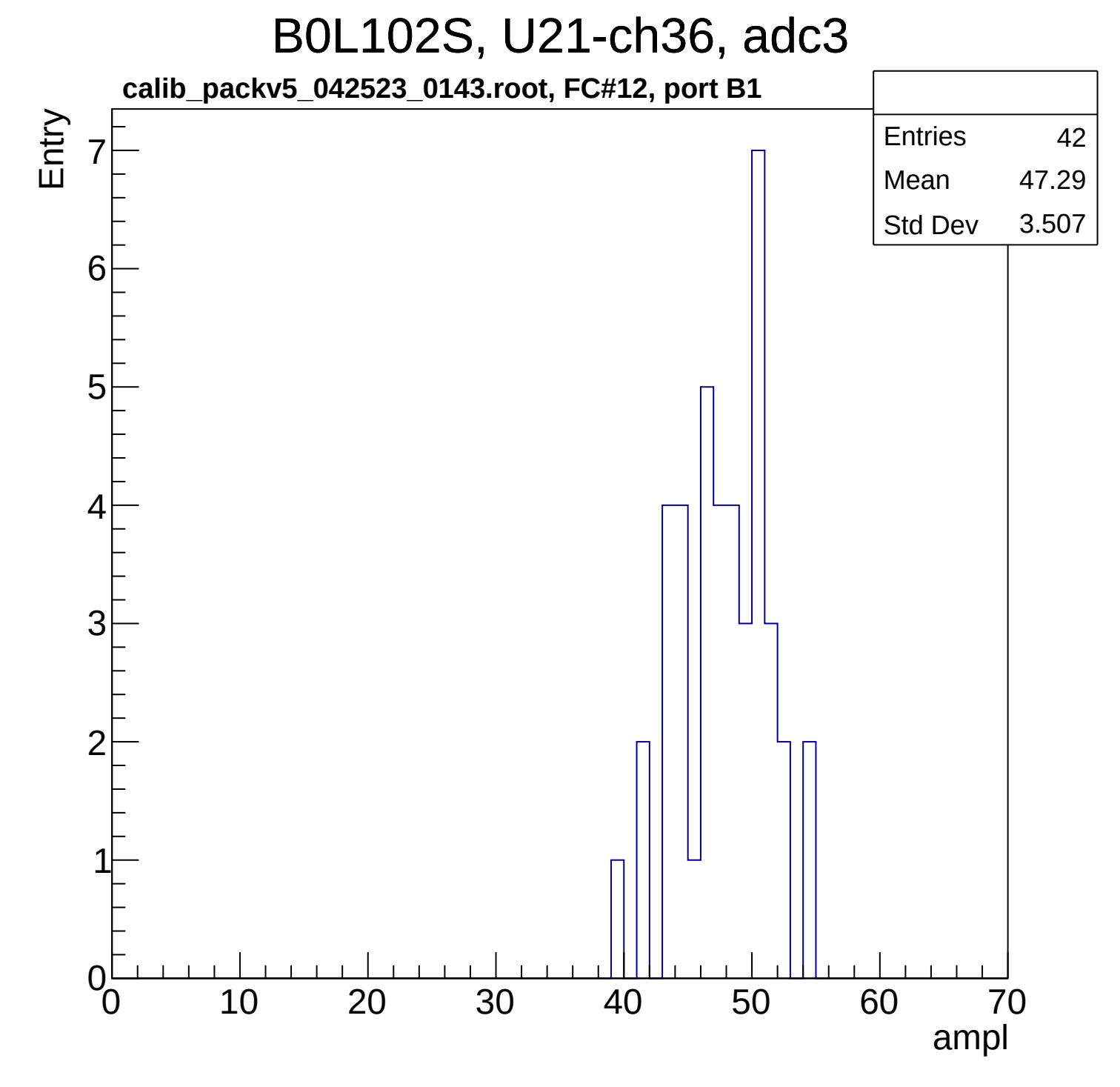
B0L102S, U21-ch36, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 47.29
Std Dev 3.507
Entry
ampl
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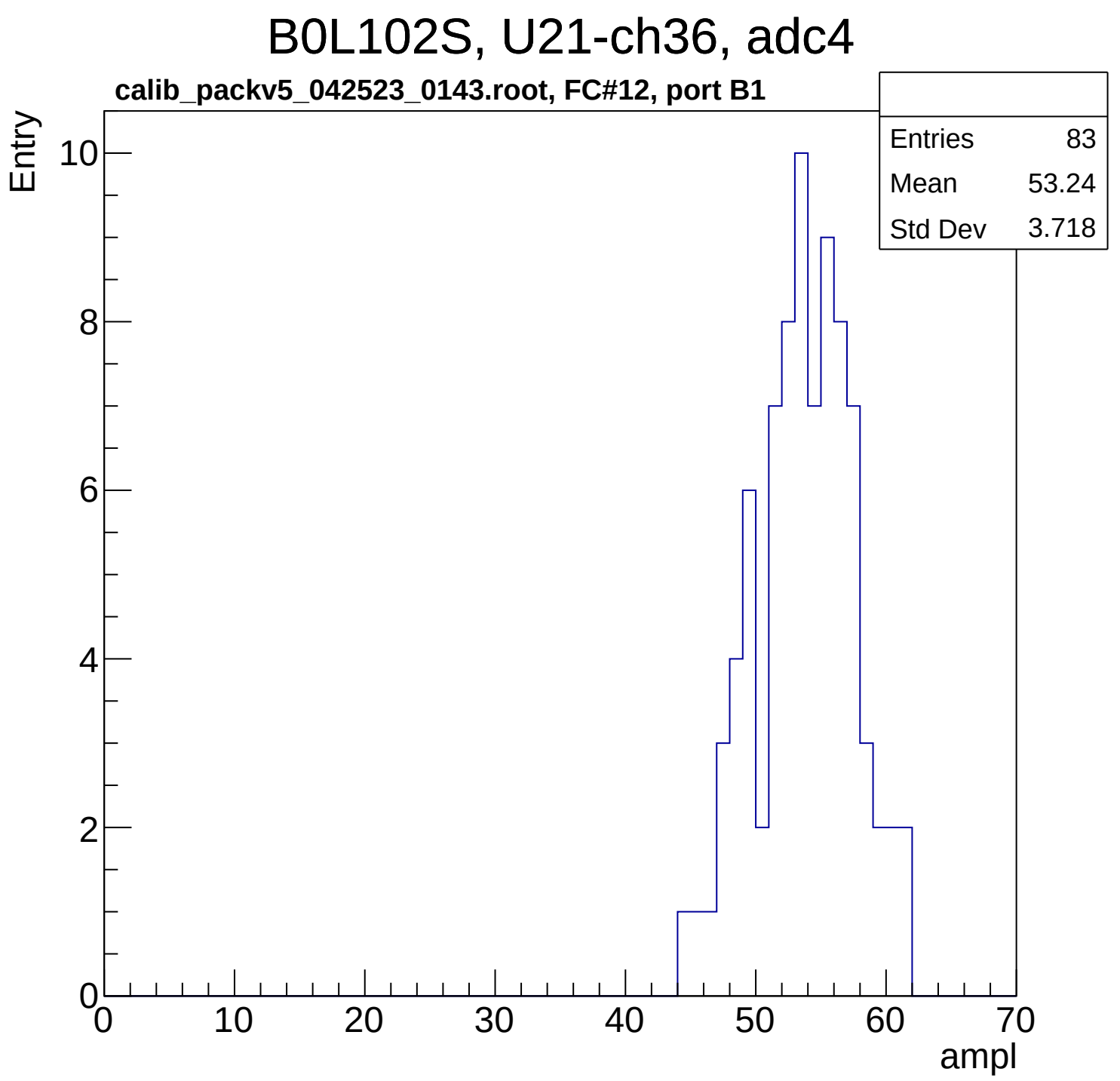
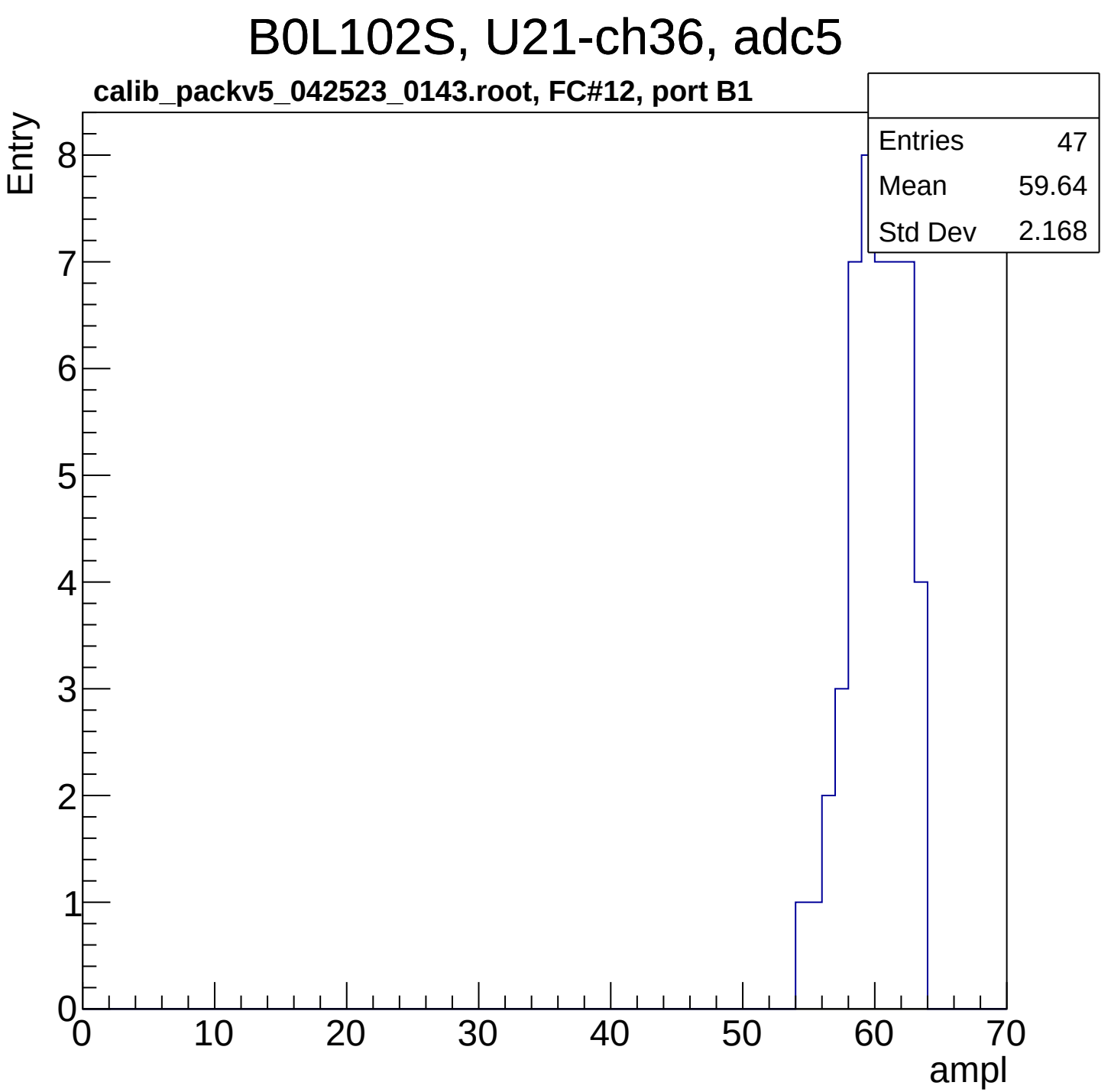
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch36, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 53.24
Std Dev 3.718
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch36, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.64
Std Dev 2.168
Entry
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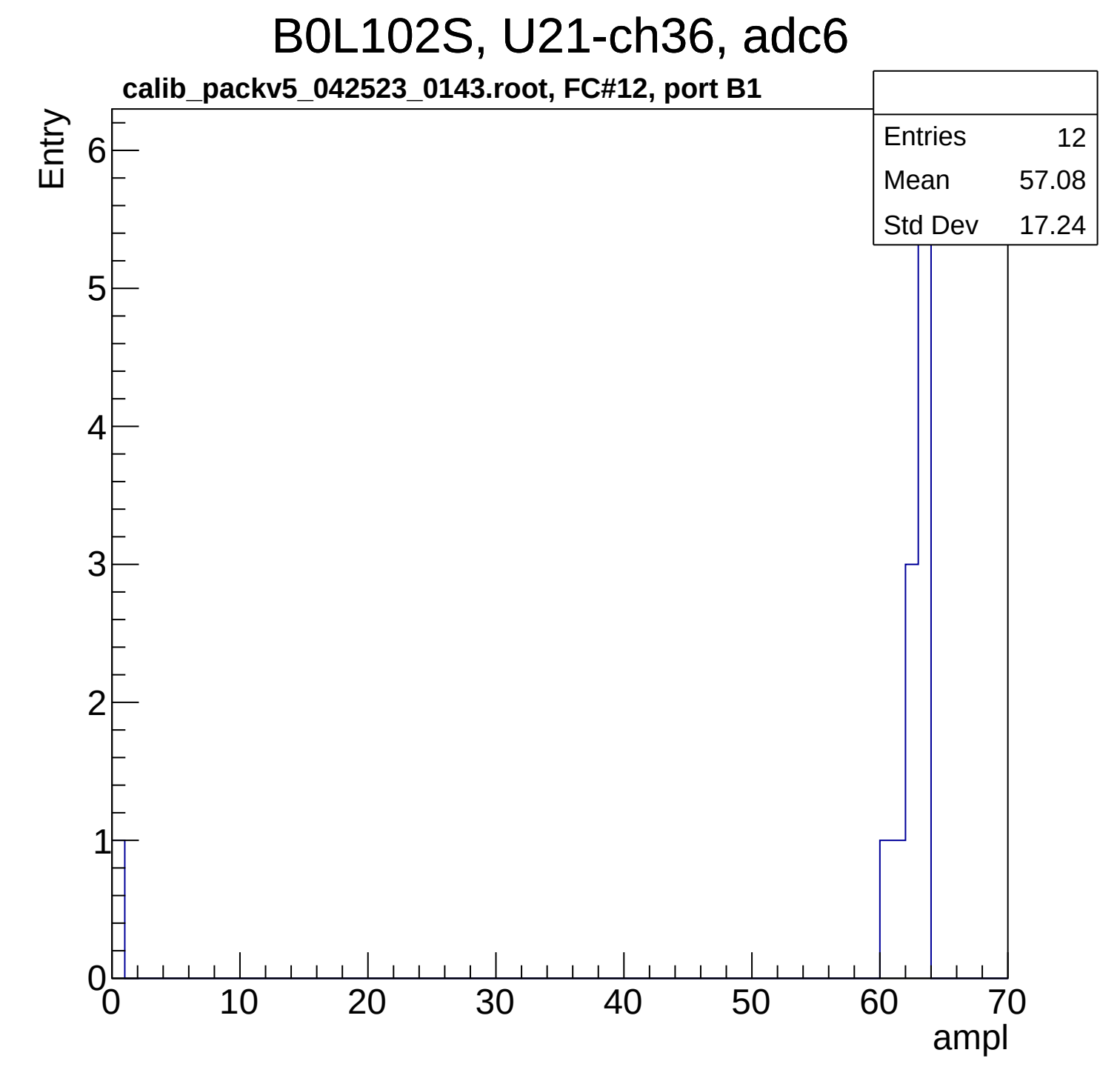
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch36, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 57.08
Std Dev 17.24
Entry
0
1
2
3
4
5
6
0
10
20
30
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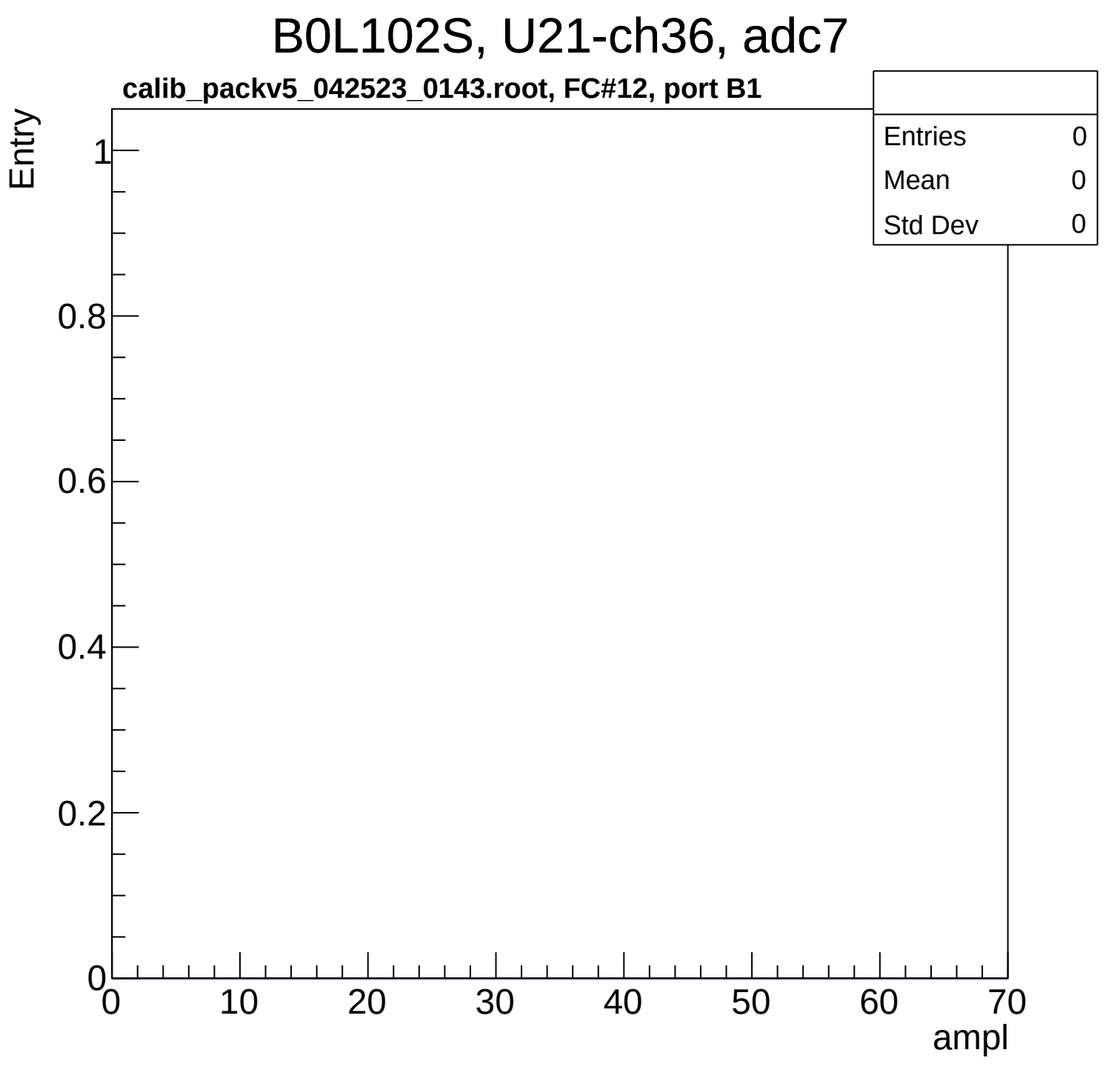
50
60
70
ampl


B0L102S, U21-ch36, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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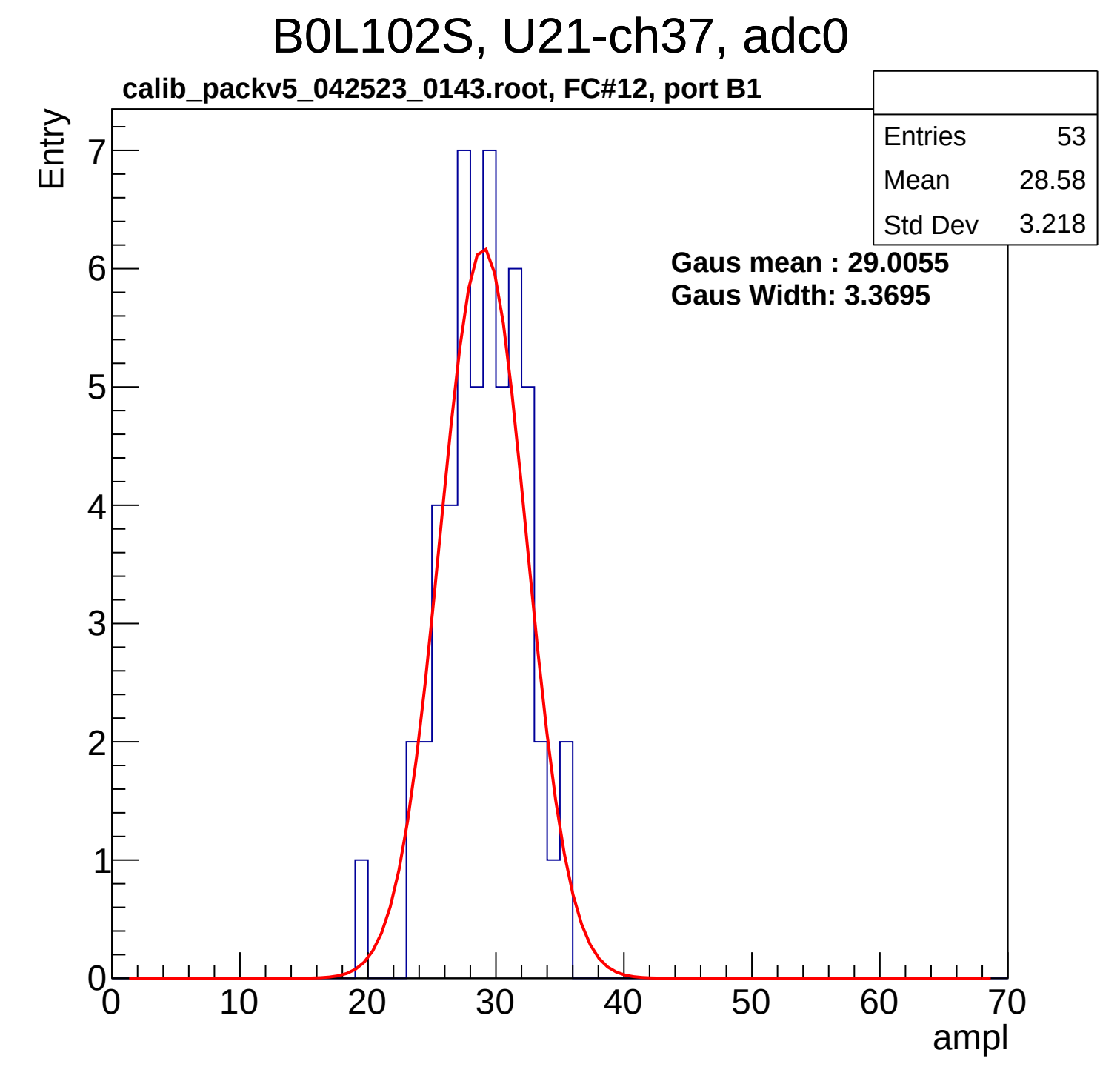
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch37, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 28.58
Std Dev 3.218
Gaus mean : 29.0055
Gaus Width: 3.3695
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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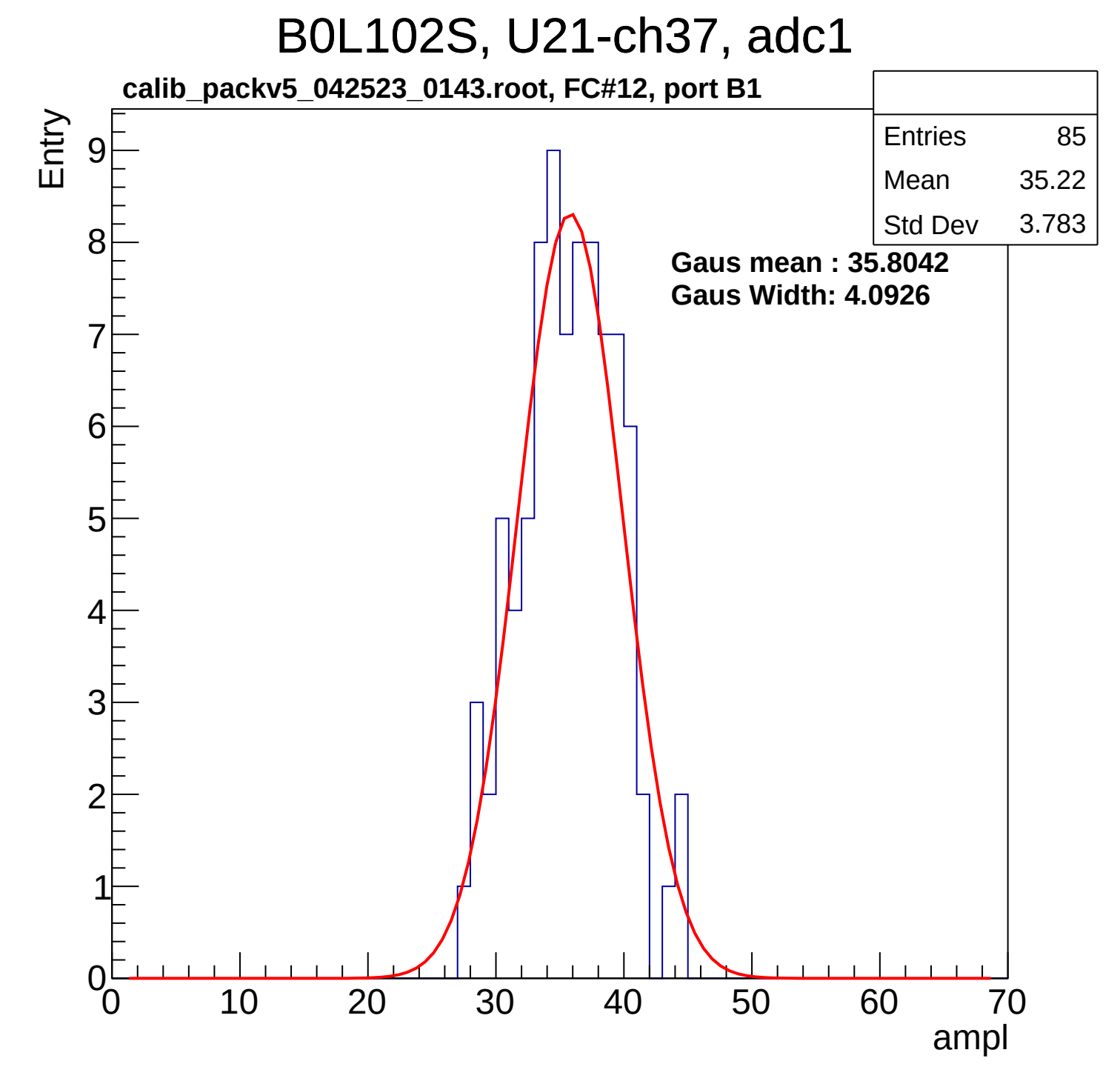
B0L102S, U21-ch37, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 35.22
Std Dev 3.783
Gaus mean : 35.8042
Gaus Width: 4.0926
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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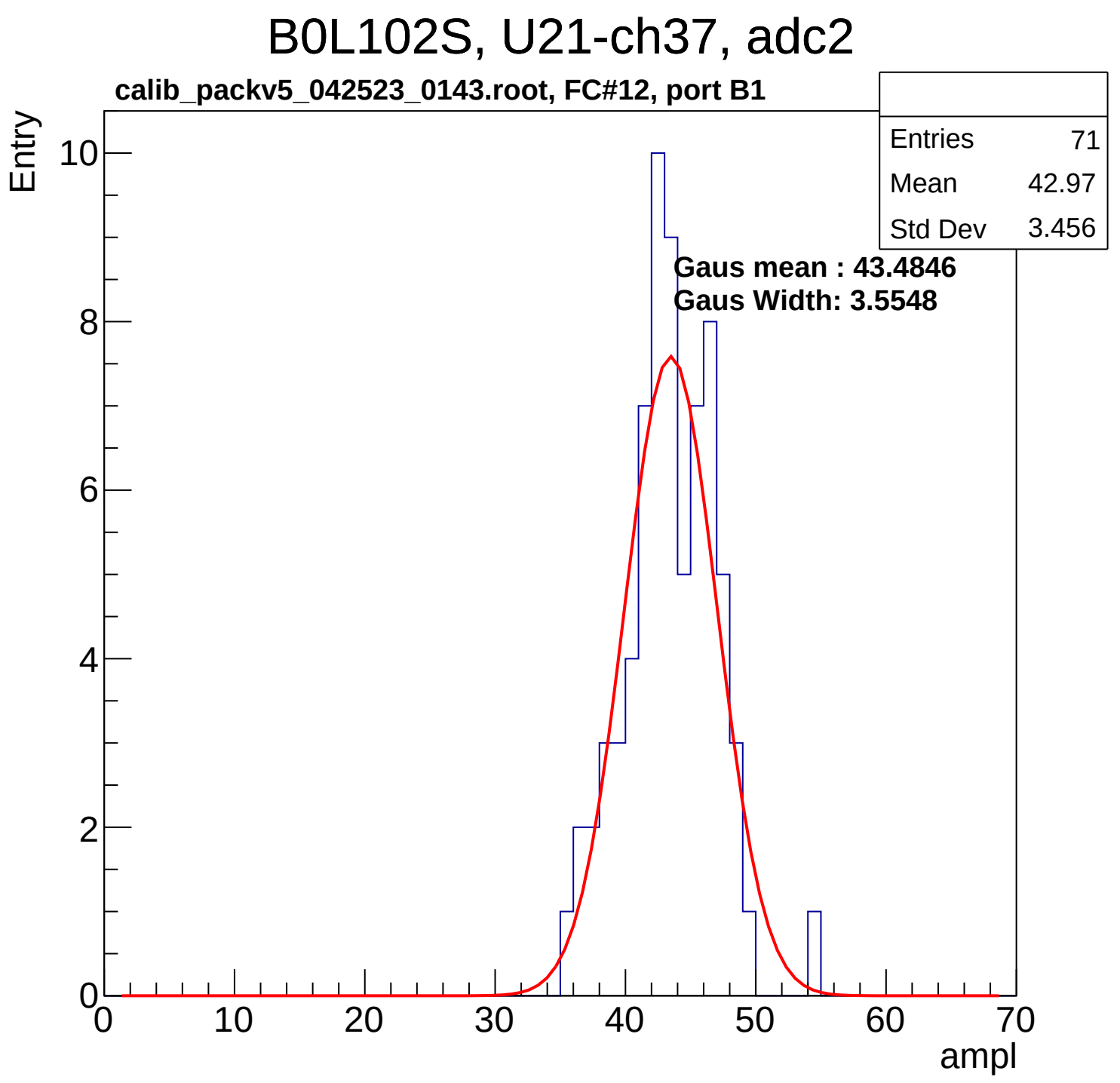
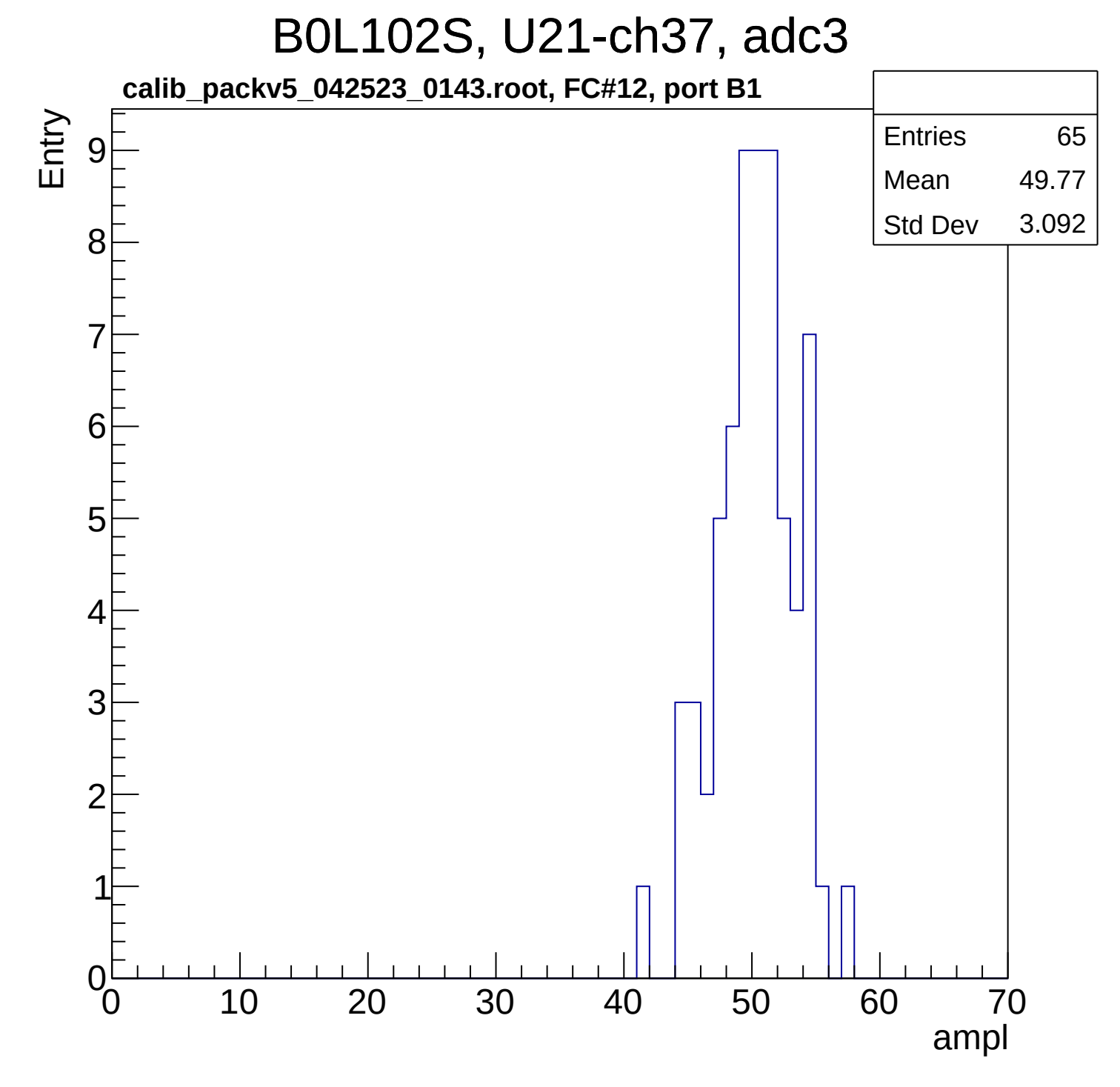
60
70


B0L102S, U21-ch37, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 42.97
Std Dev 3.456
Gaus mean : 43.4846
Gaus Width: 3.5548
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch37, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 49.77
Std Dev 3.092
Entry
ampl
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3
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5
6
7
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0
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60
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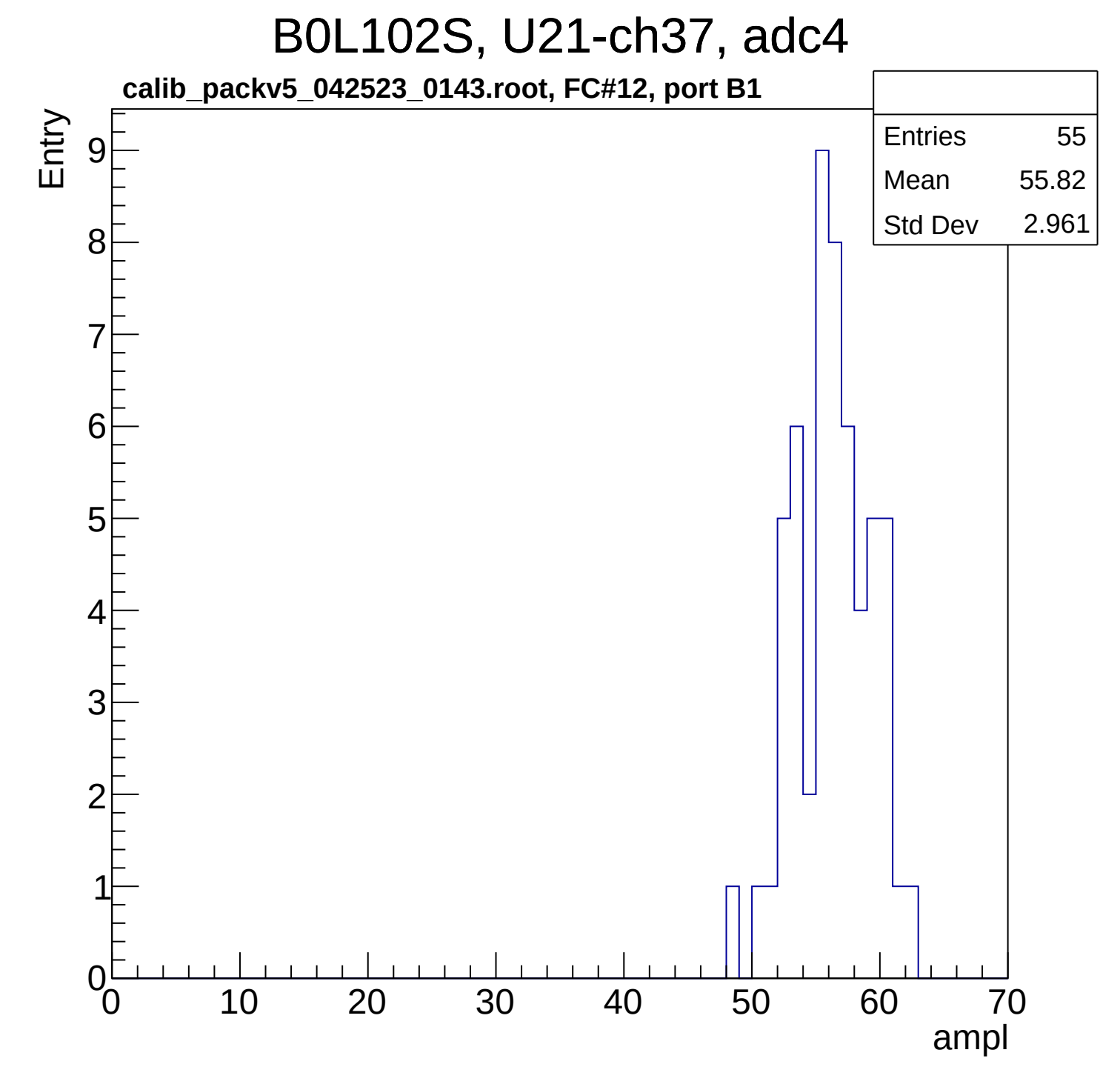
B0L102S, U21-ch37, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.82
Std Dev 2.961
Entry
ampl
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5
6
7
8
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0
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60
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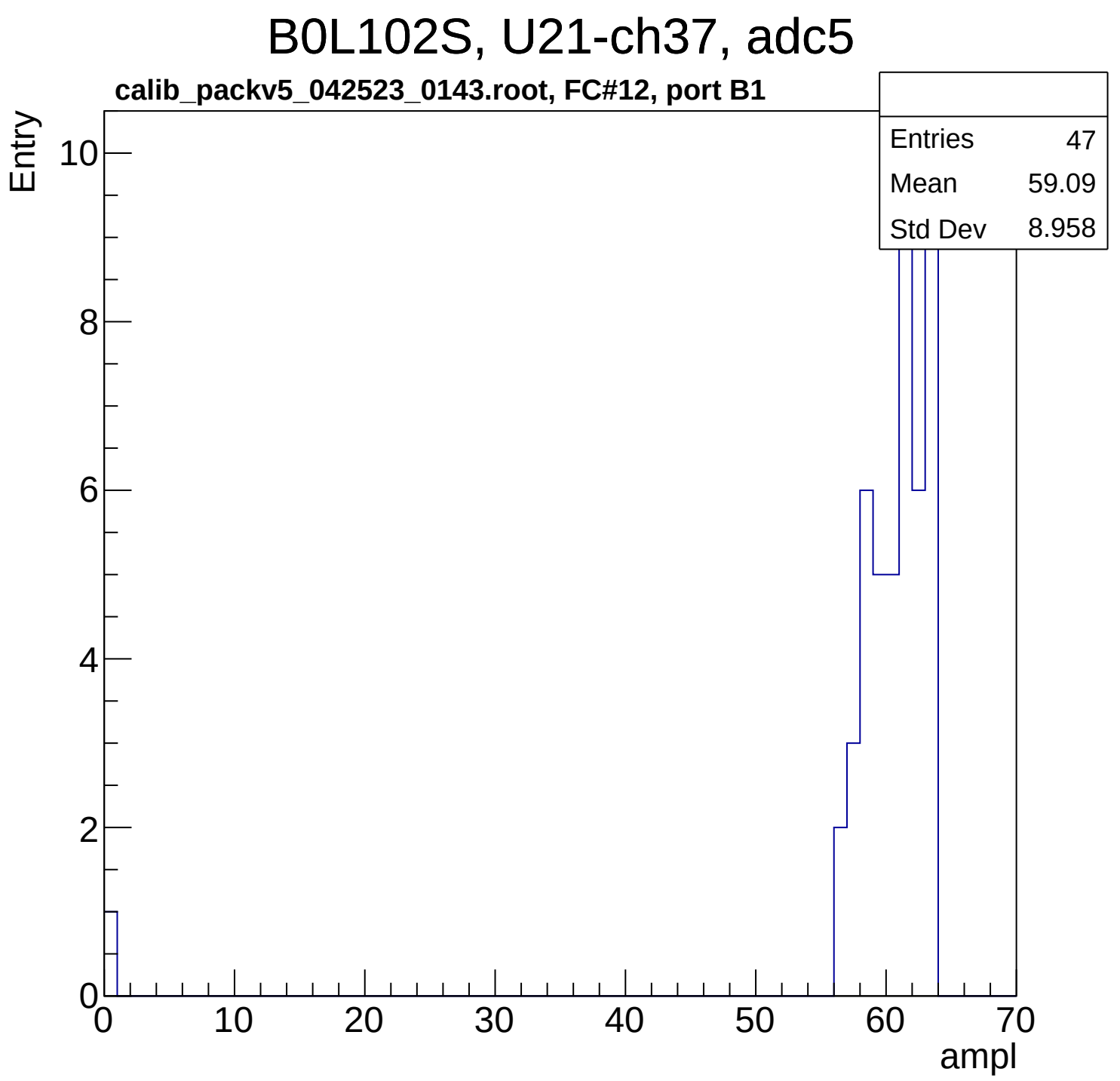
B0L102S, U21-ch37, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.09
Std Dev 8.958
Entry
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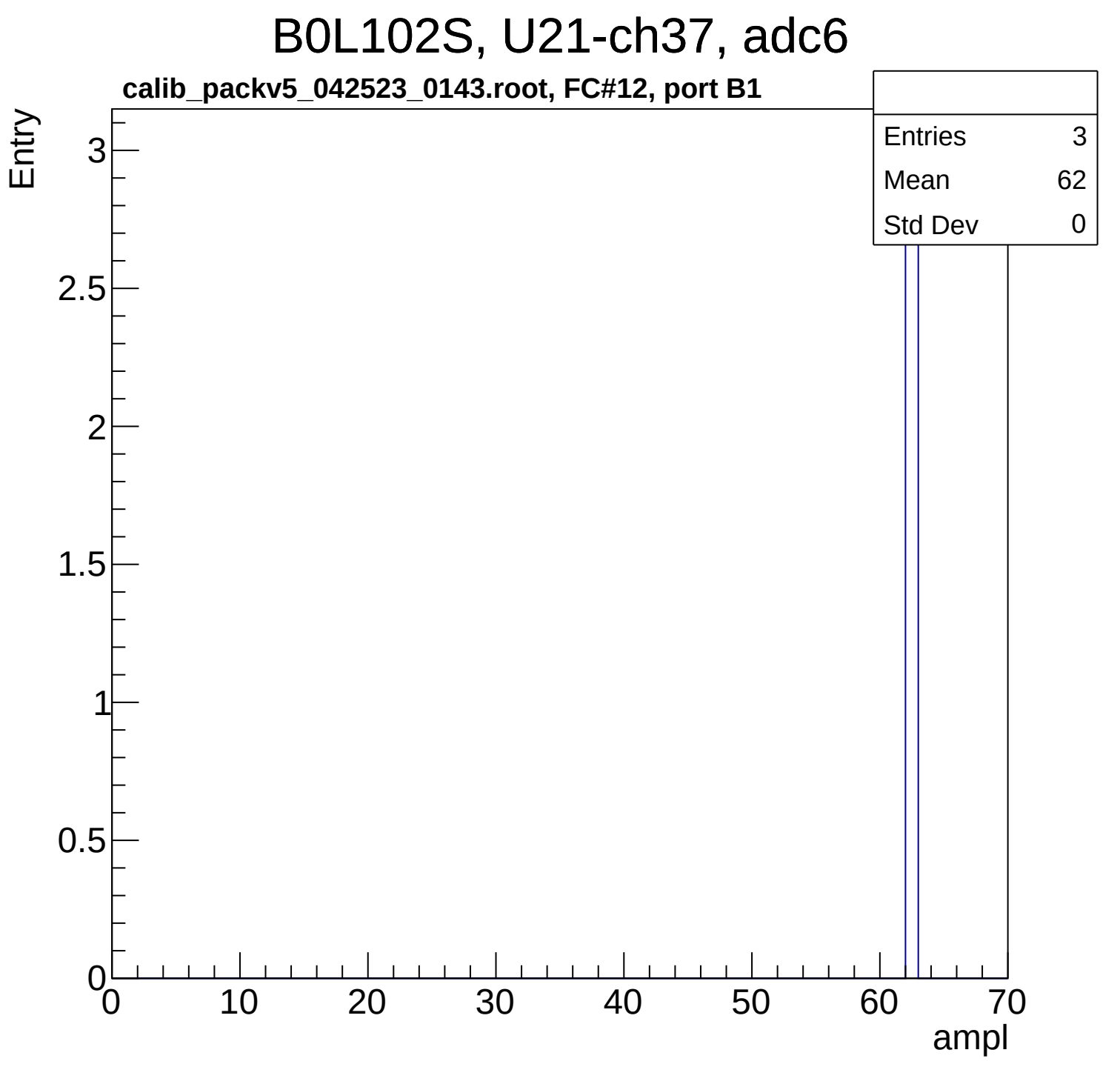
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch37, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62
Std Dev 0
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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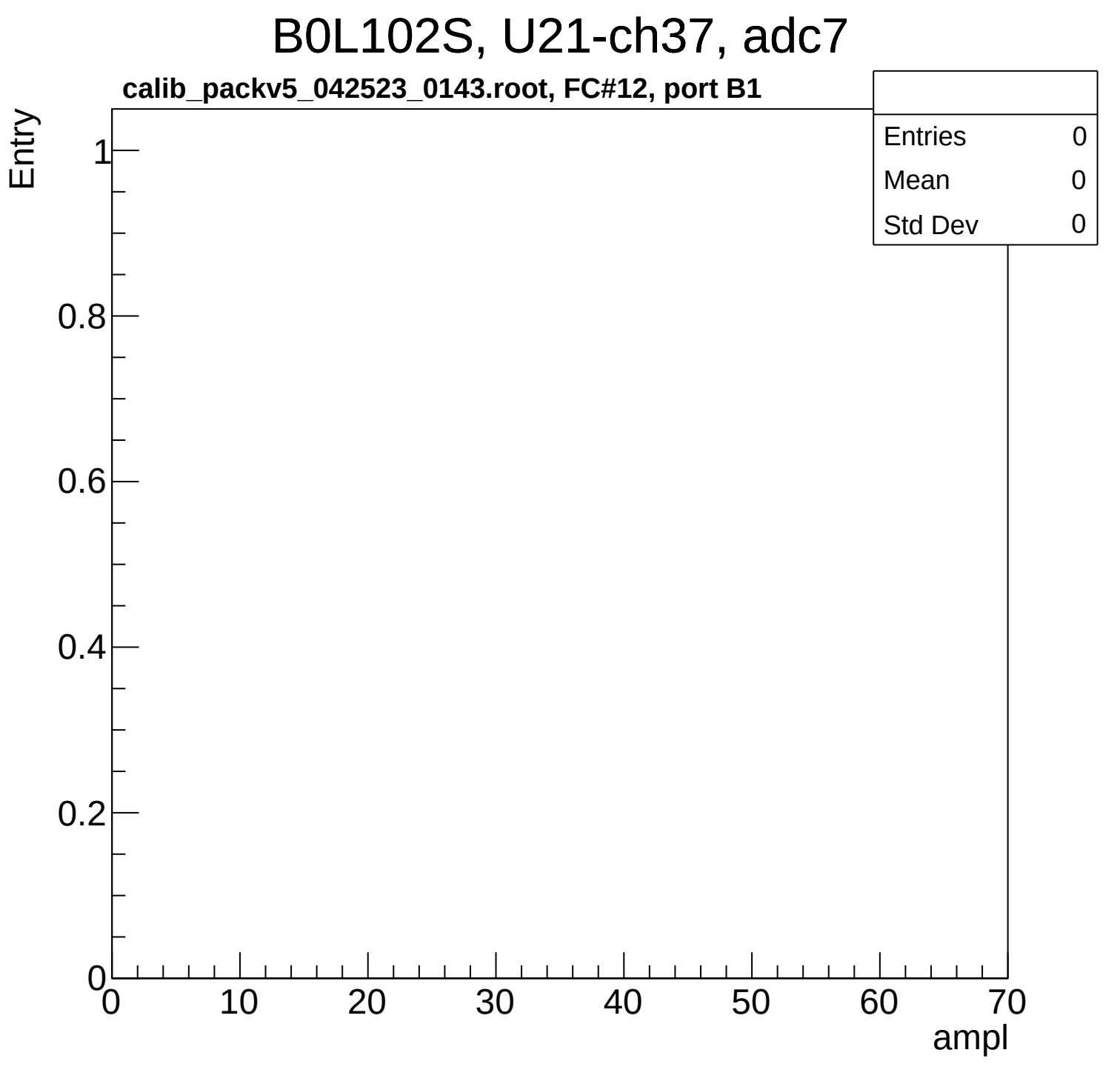
20
30
40
50
60
70


B0L102S, U21-ch37, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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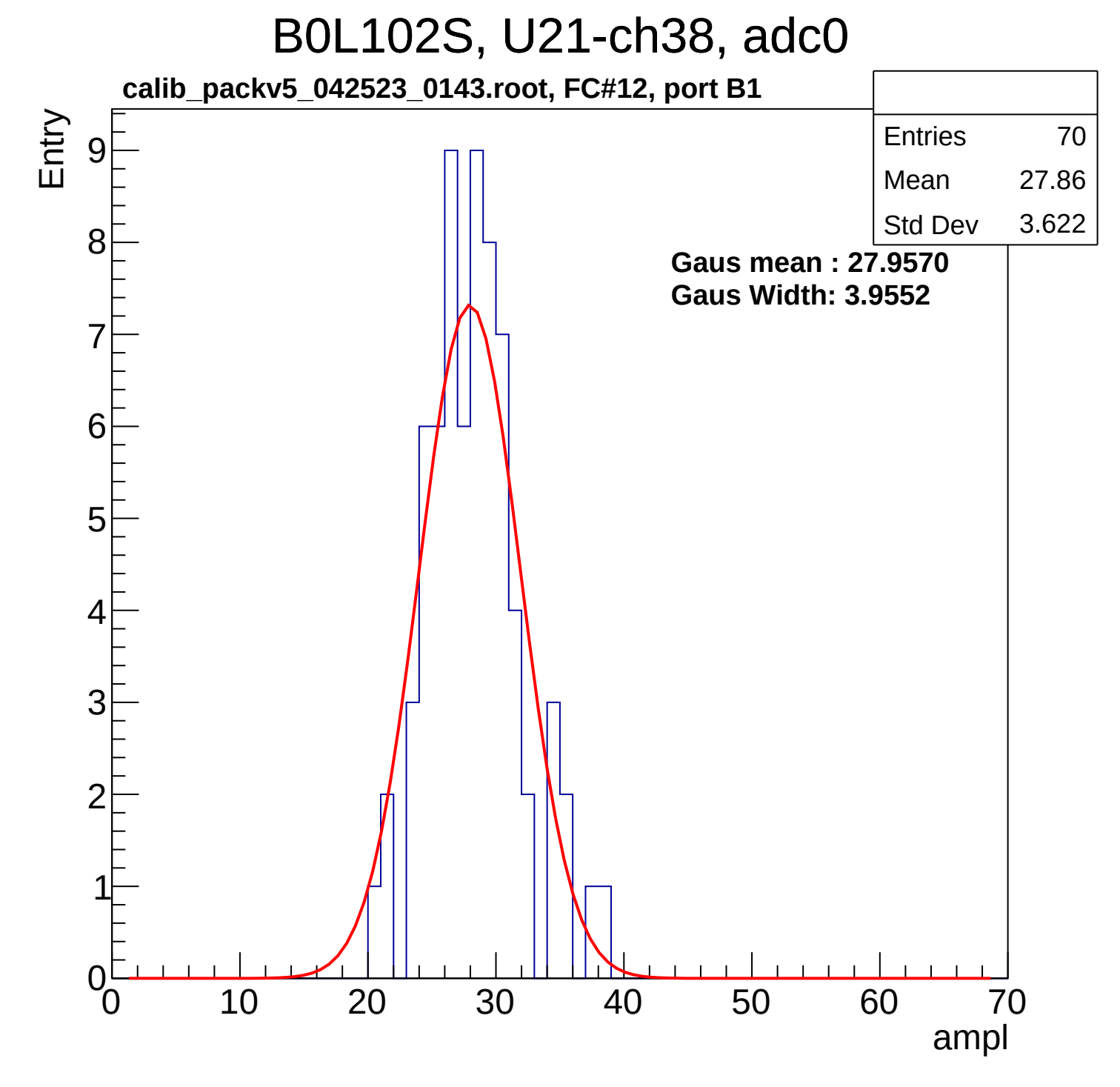
60
70
ampl


B0L102S, U21-ch38, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 27.86
Std Dev 3.622
Gaus mean : 27.9570
Gaus Width: 3.9552
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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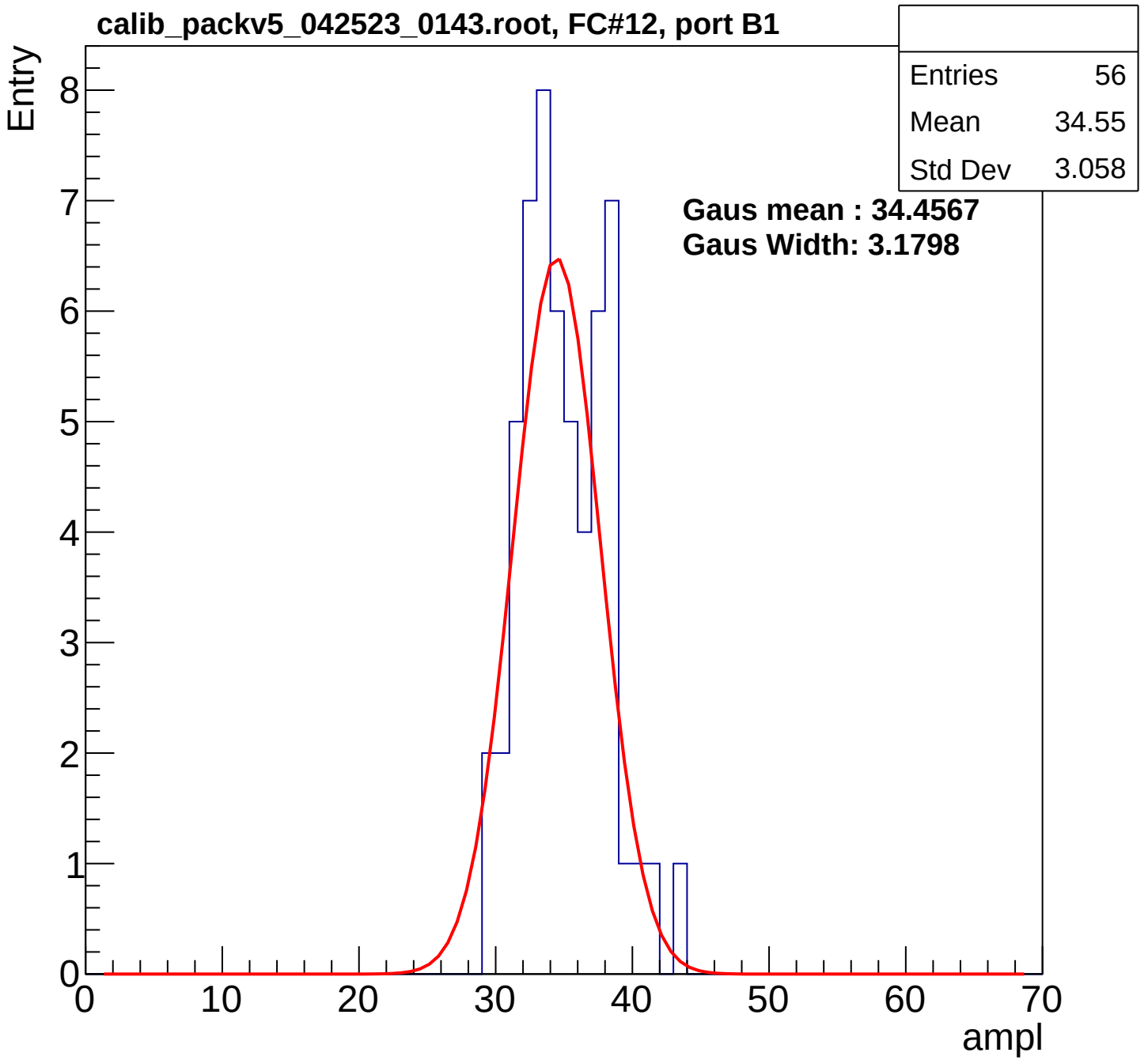
B0L102S, U21-ch38, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 34.55
Std Dev 3.058
Gaus mean : 34.4567
Gaus Width: 3.1798
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
50
60
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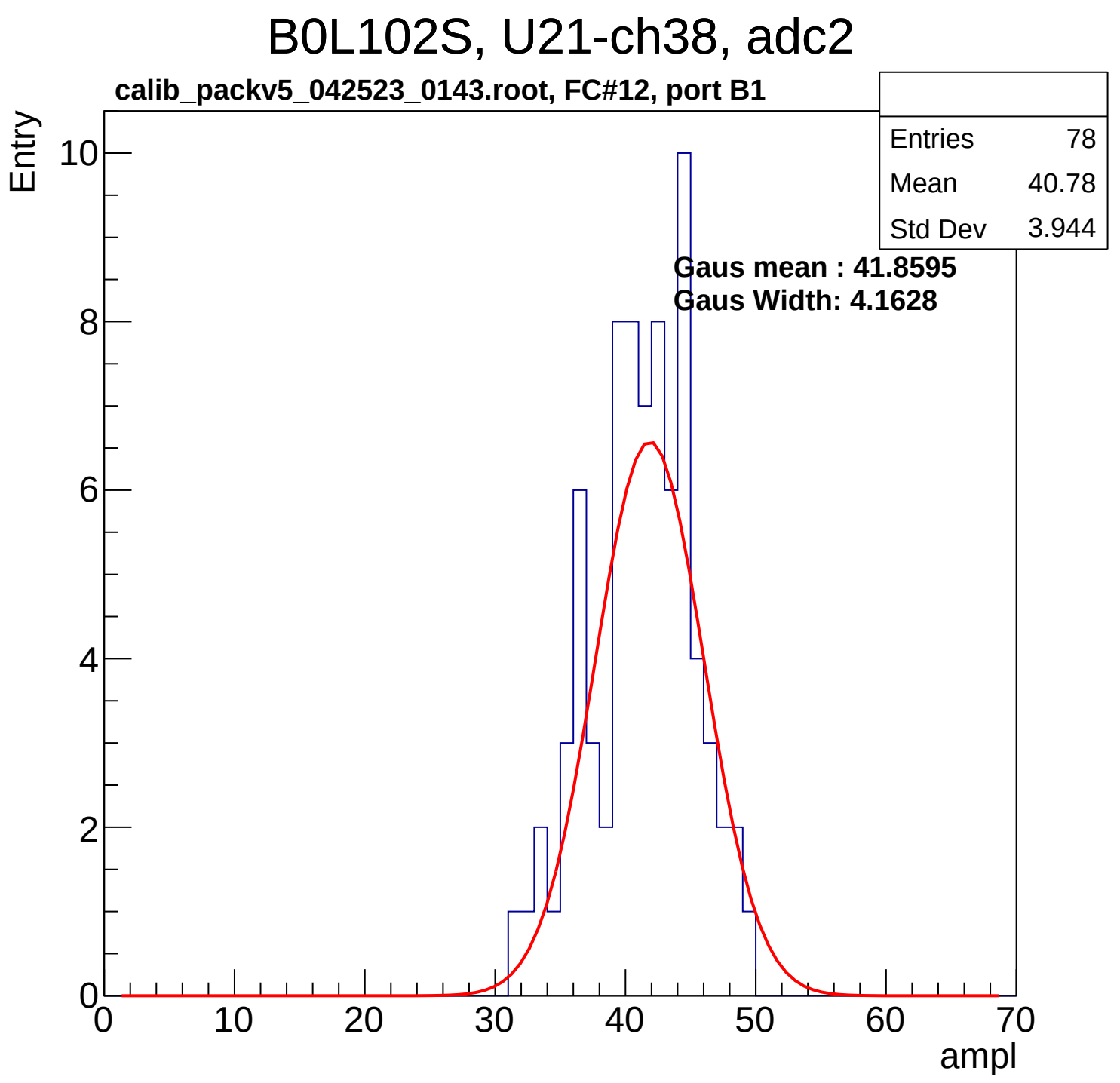
B0L102S, U21-ch38, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 40.78
Std Dev 3.944
Gaus mean : 41.8595
Gaus Width: 4.1628
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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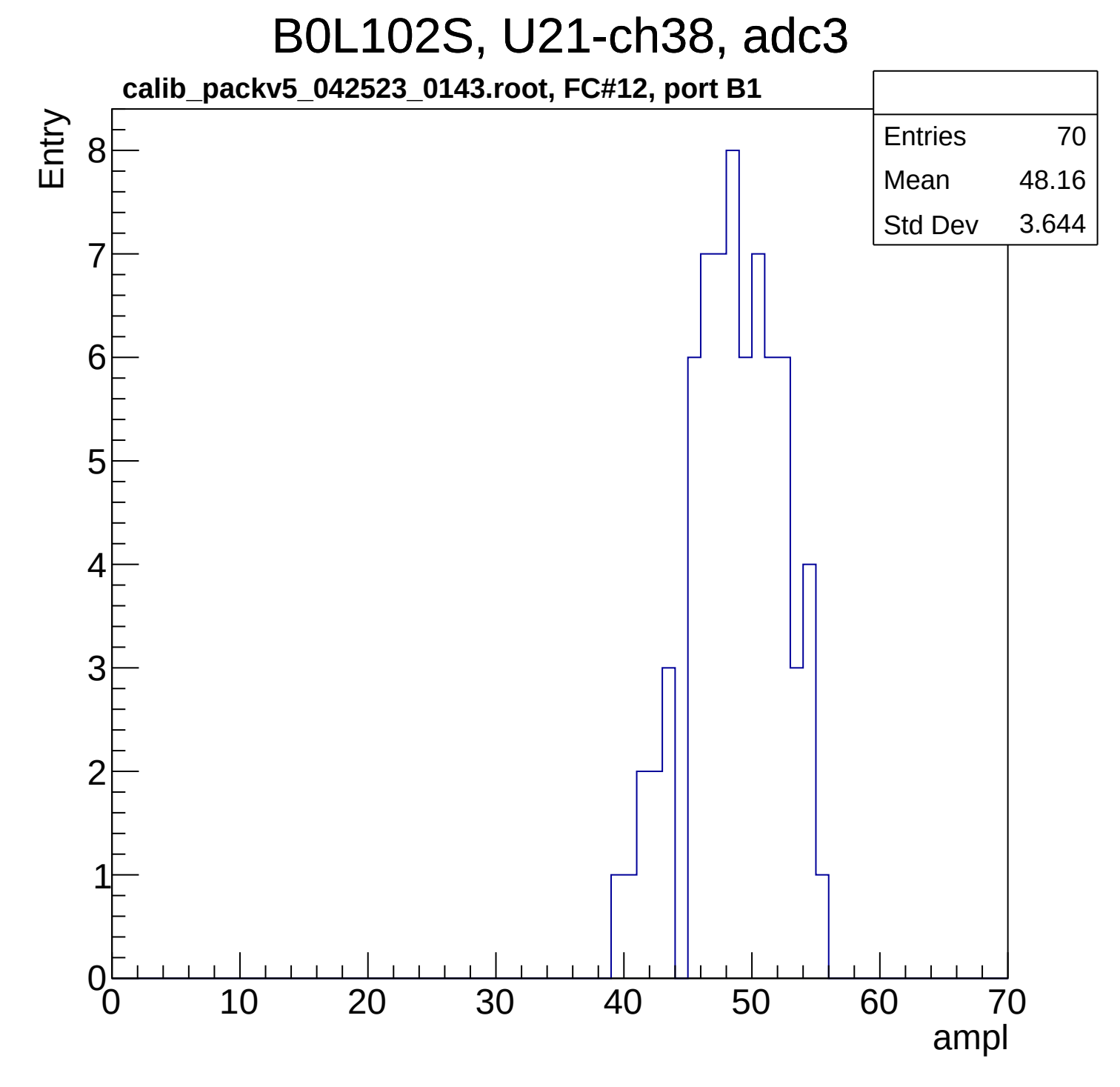
B0L102S, U21-ch38, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 48.16
Std Dev 3.644
Entry
ampl
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3
4
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7
8
0
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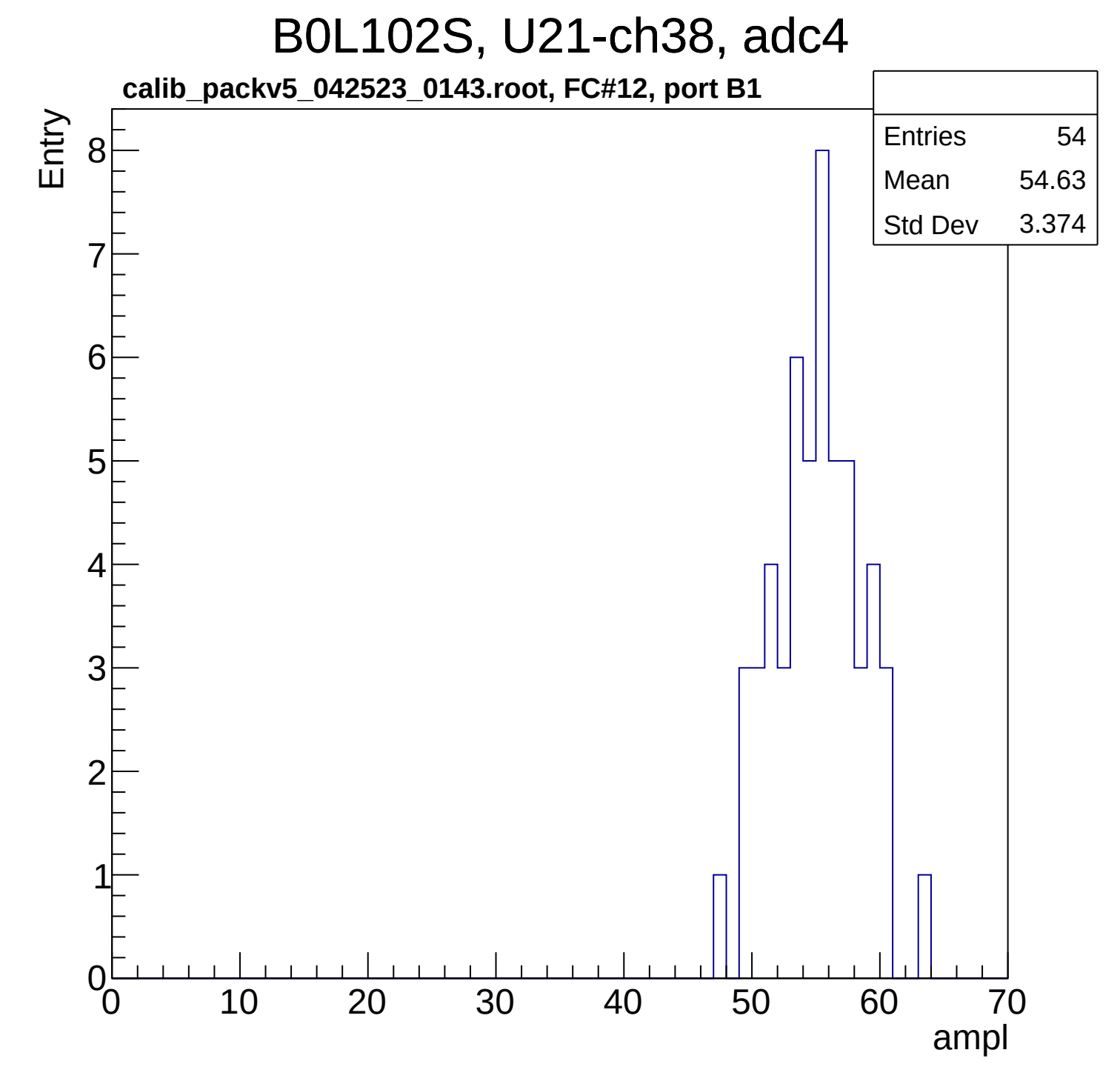
B0L102S, U21-ch38, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.63
Std Dev 3.374
Entry
ampl
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0
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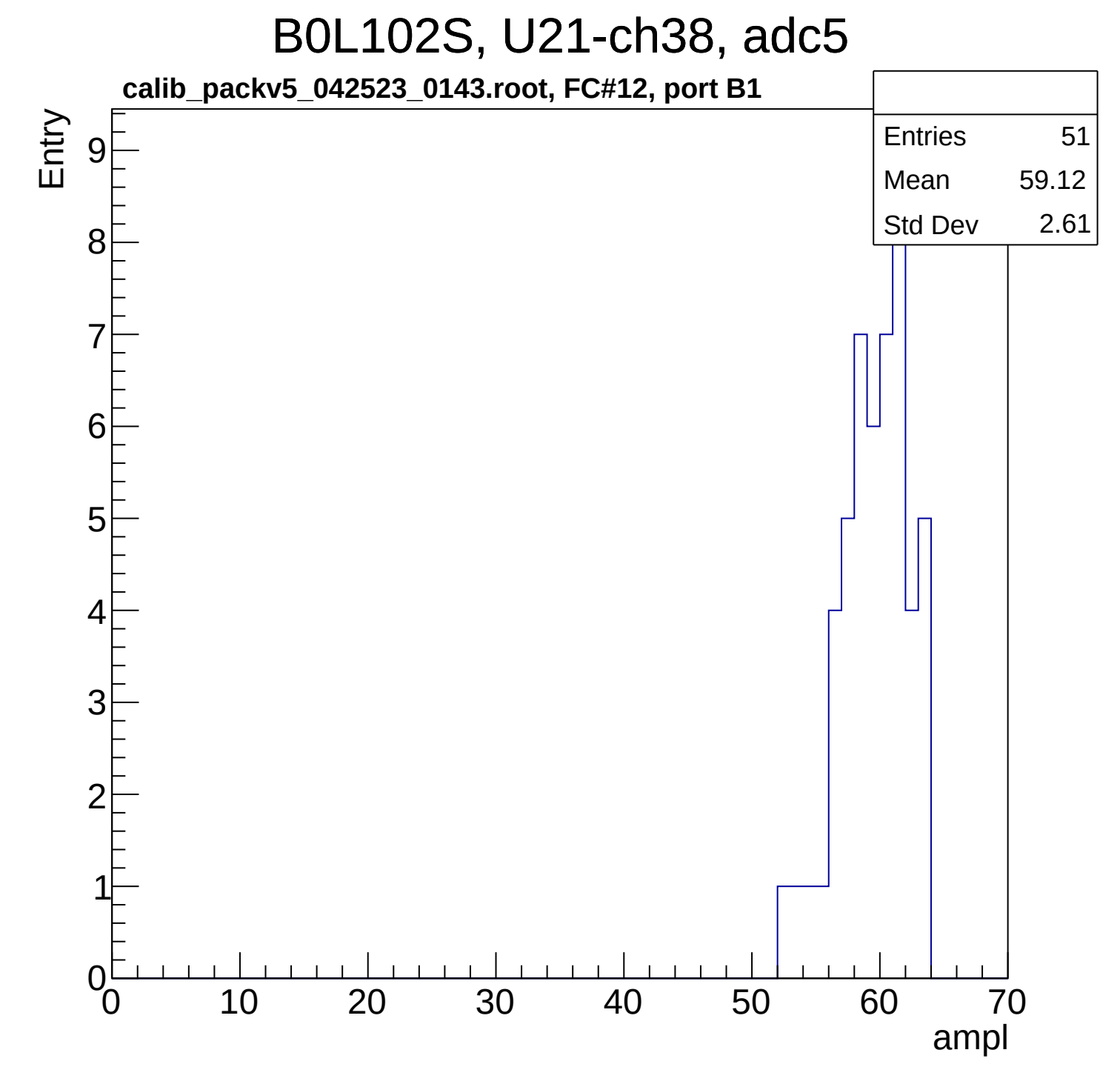
B0L102S, U21-ch38, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 59.12
Std Dev 2.61
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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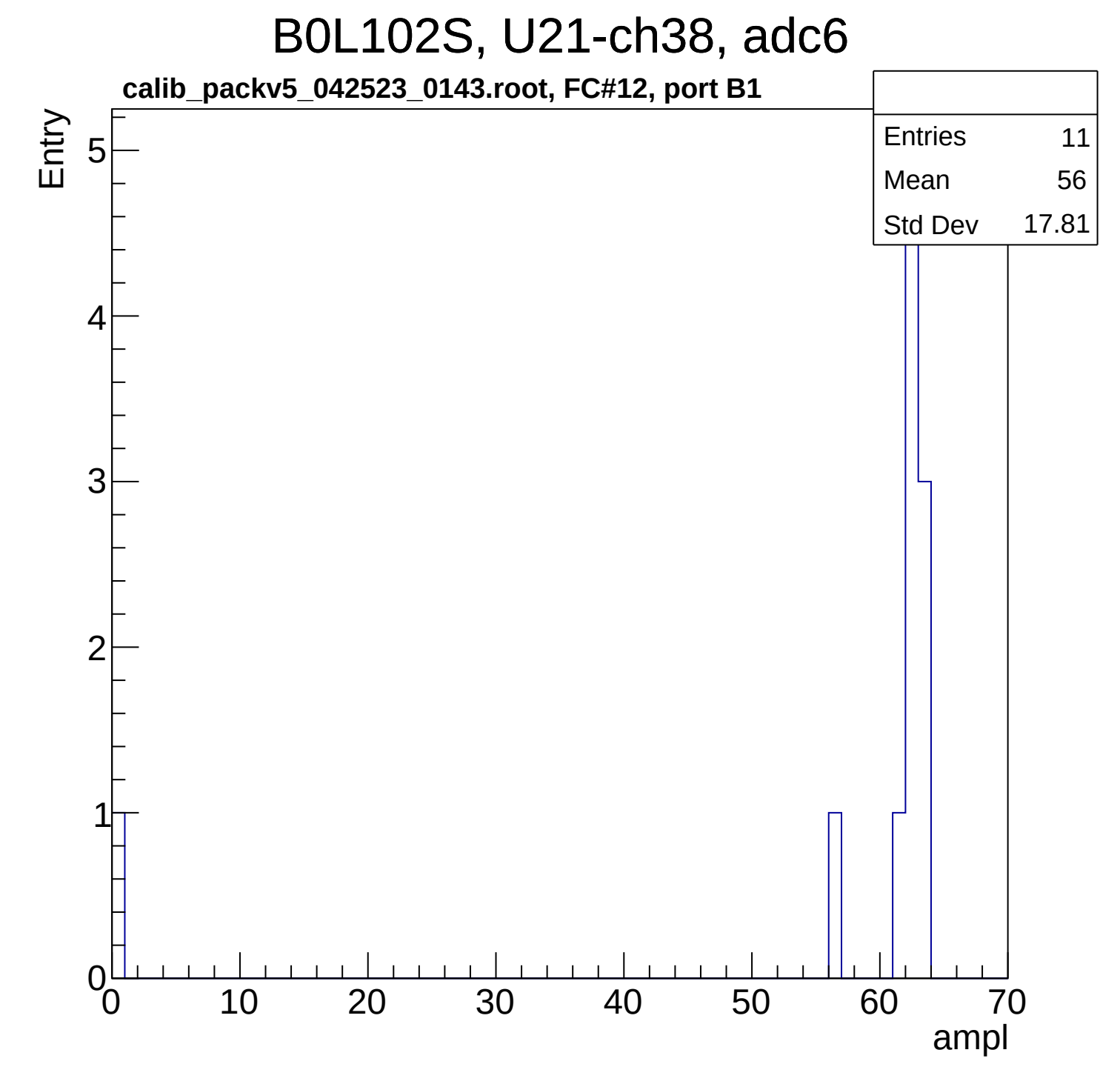
B0L102S, U21-ch38, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 56
Std Dev 17.81
Entry
0
1
2
3
4
5
0
10
20
30
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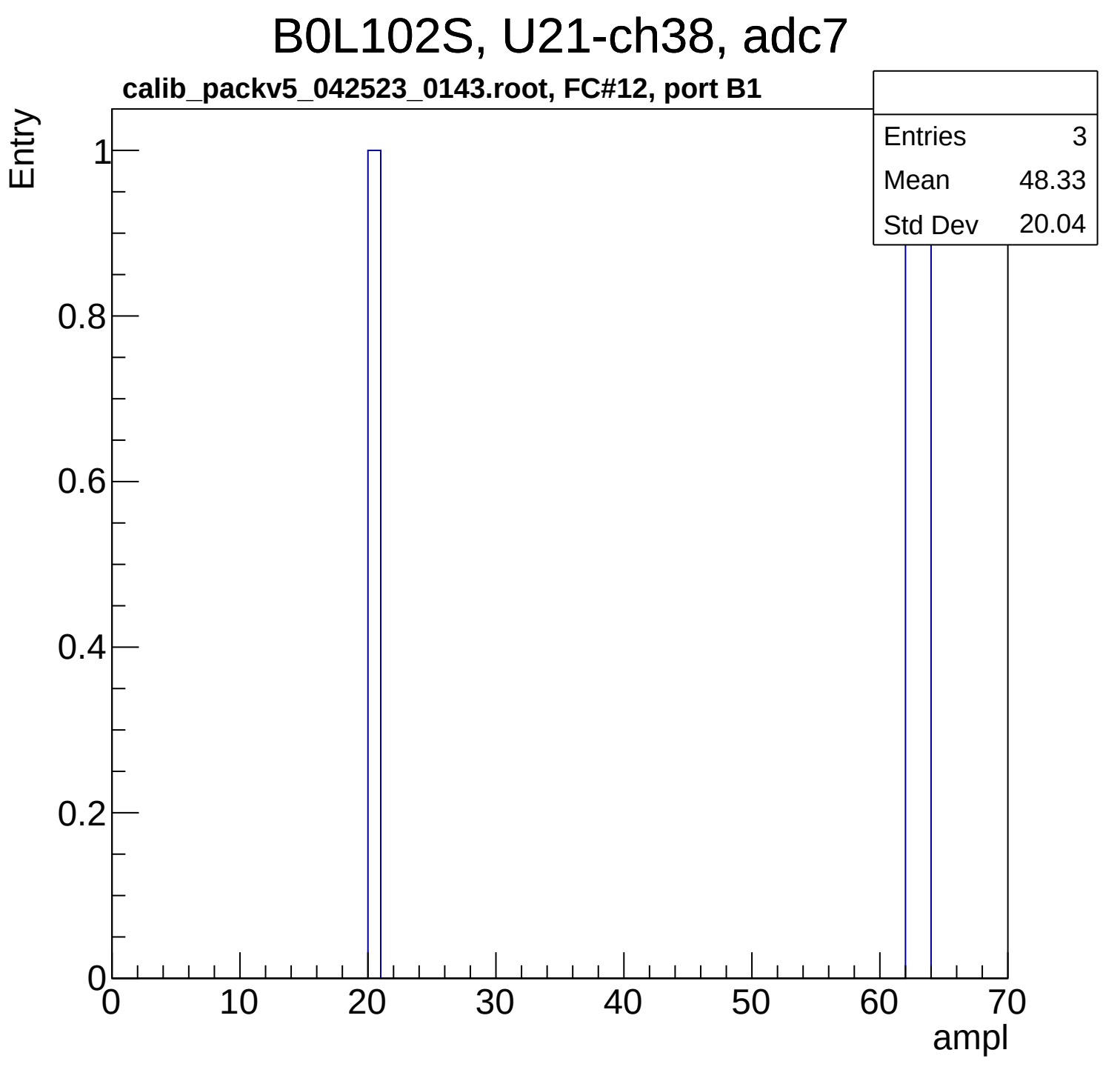
50
60
70
ampl


B0L102S, U21-ch38, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 48.33
Std Dev 20.04
Entry
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0.8
0.6
0.4
0.2
0
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10
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50
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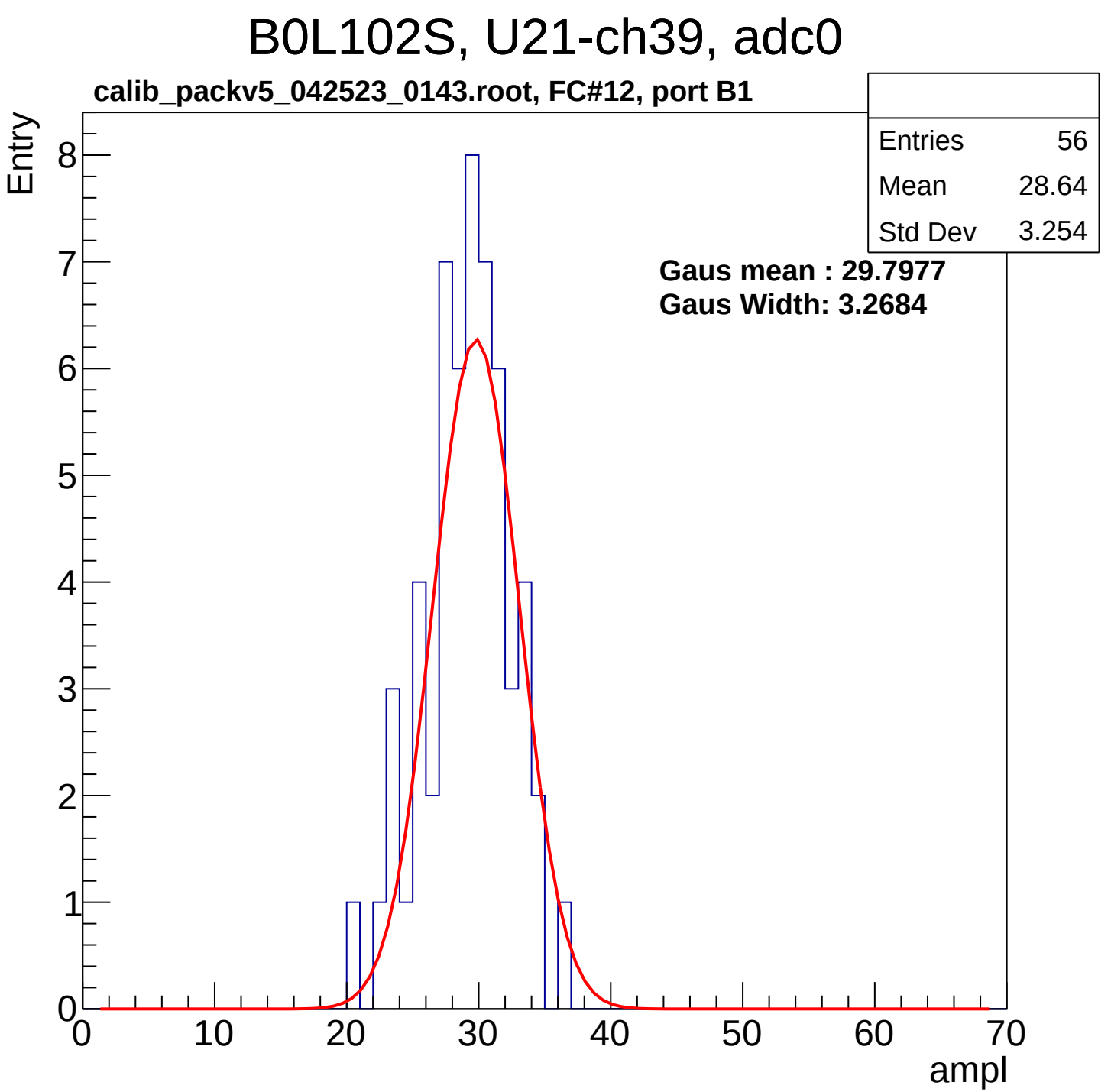
70
ampl


B0L102S, U21-ch39, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 28.64
Std Dev 3.254
Gaus mean : 29.7977
Gaus Width: 3.2684
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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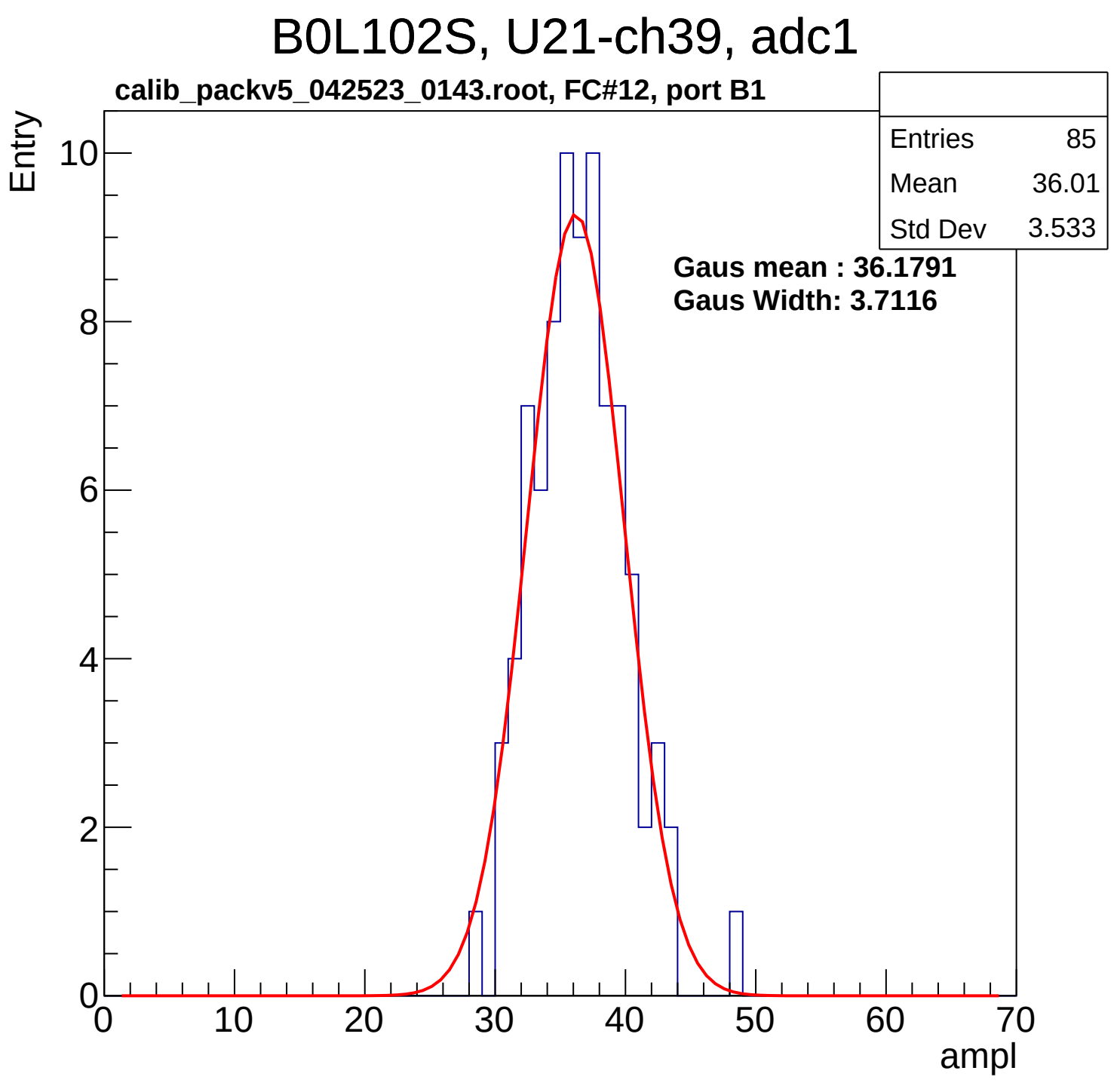
B0L102S, U21-ch39, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 36.01
Std Dev 3.533
Gaus mean : 36.1791
Gaus Width: 3.7116
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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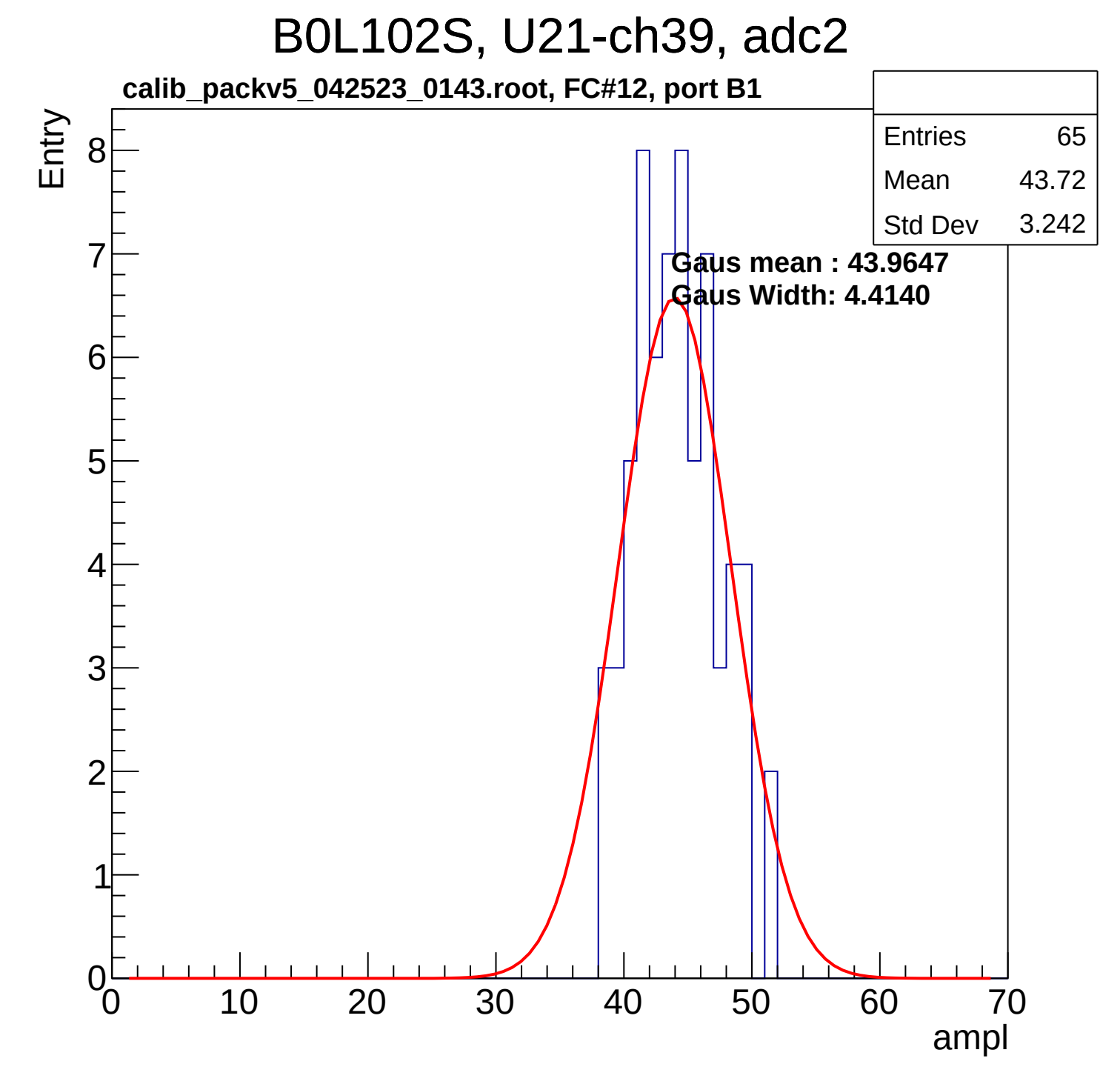
B0L102S, U21-ch39, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.72
Std Dev 3.242
Gaus mean : 43.9647
Gaus Width: 4.4140
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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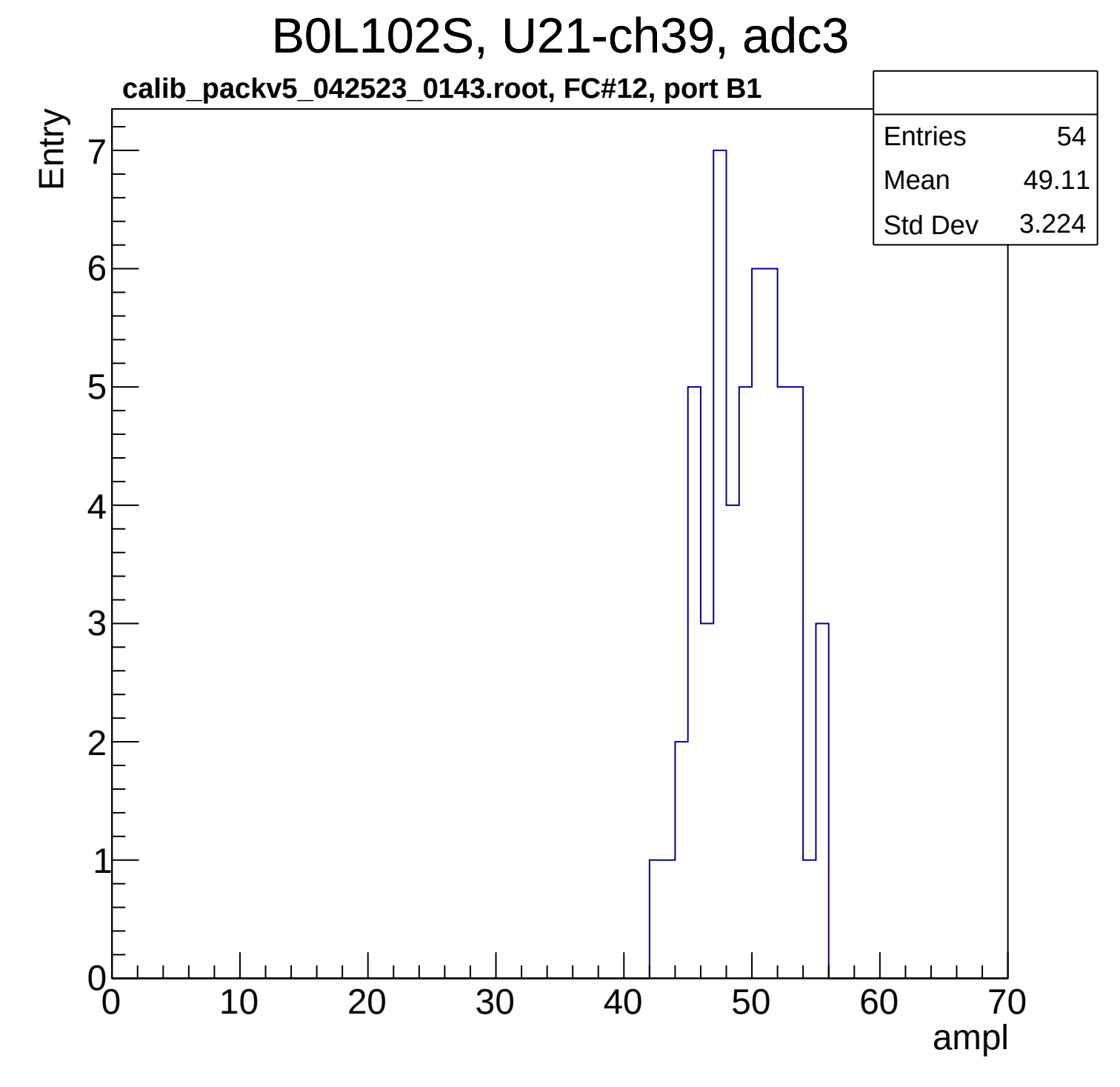
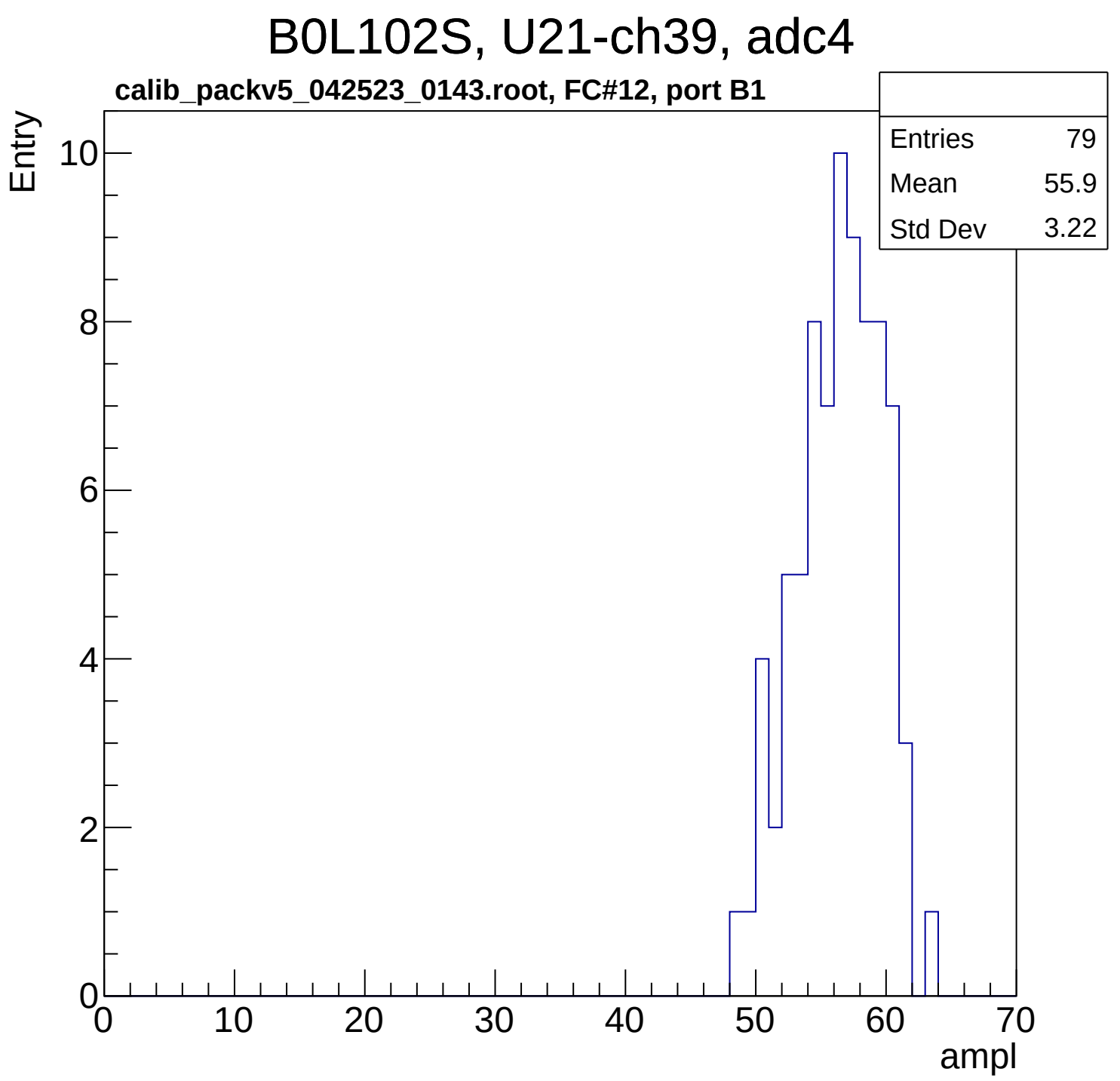
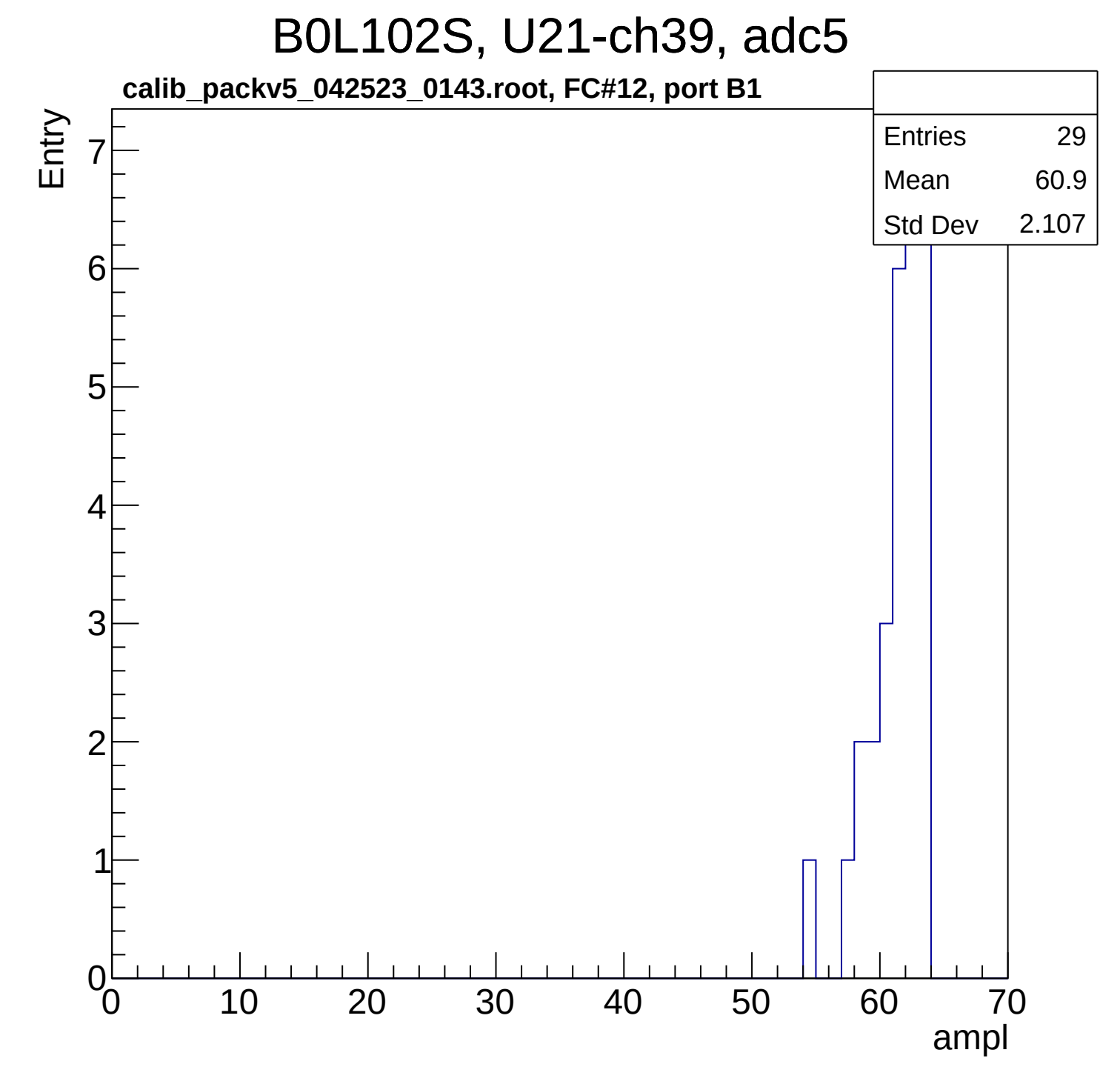
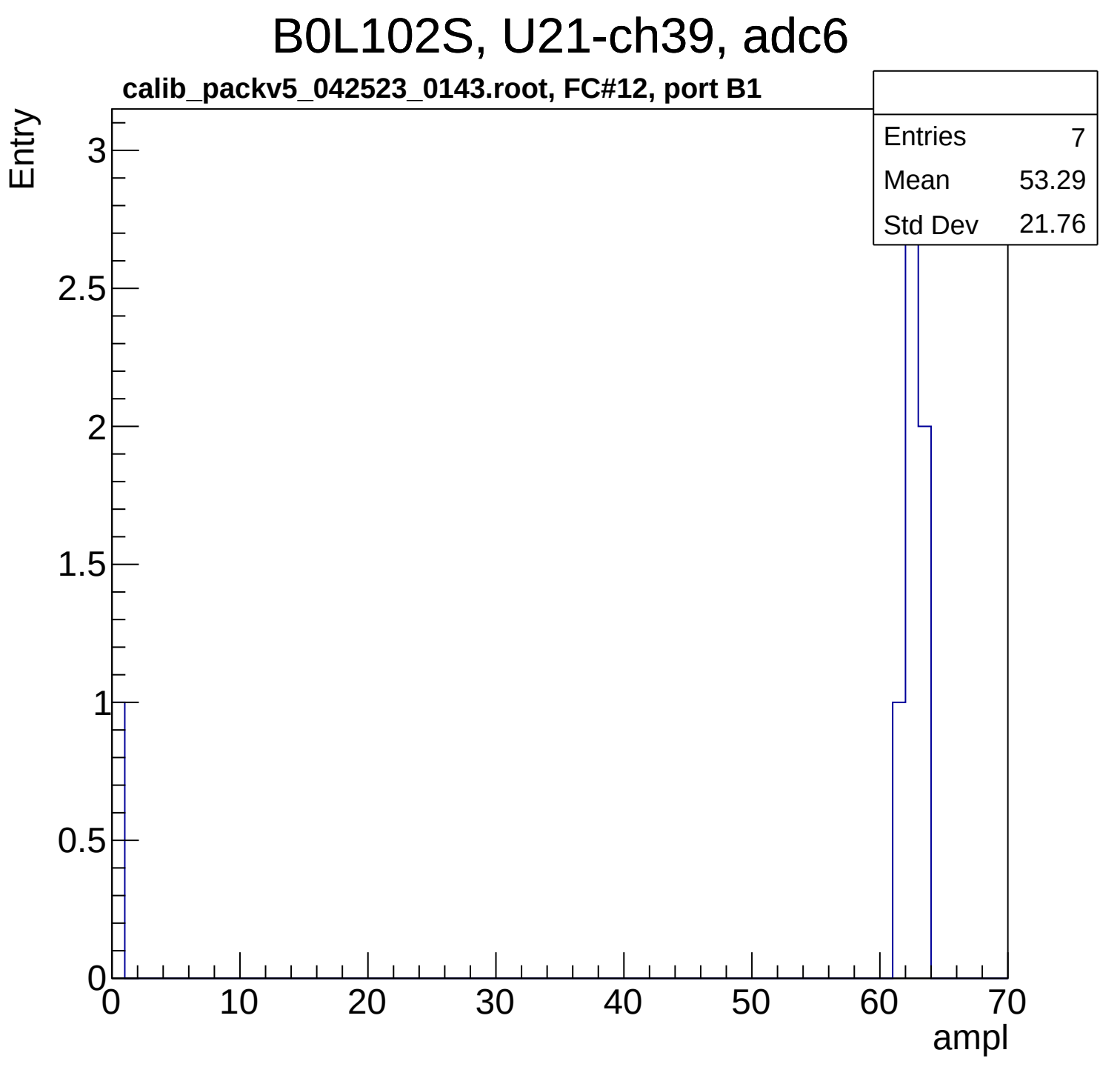
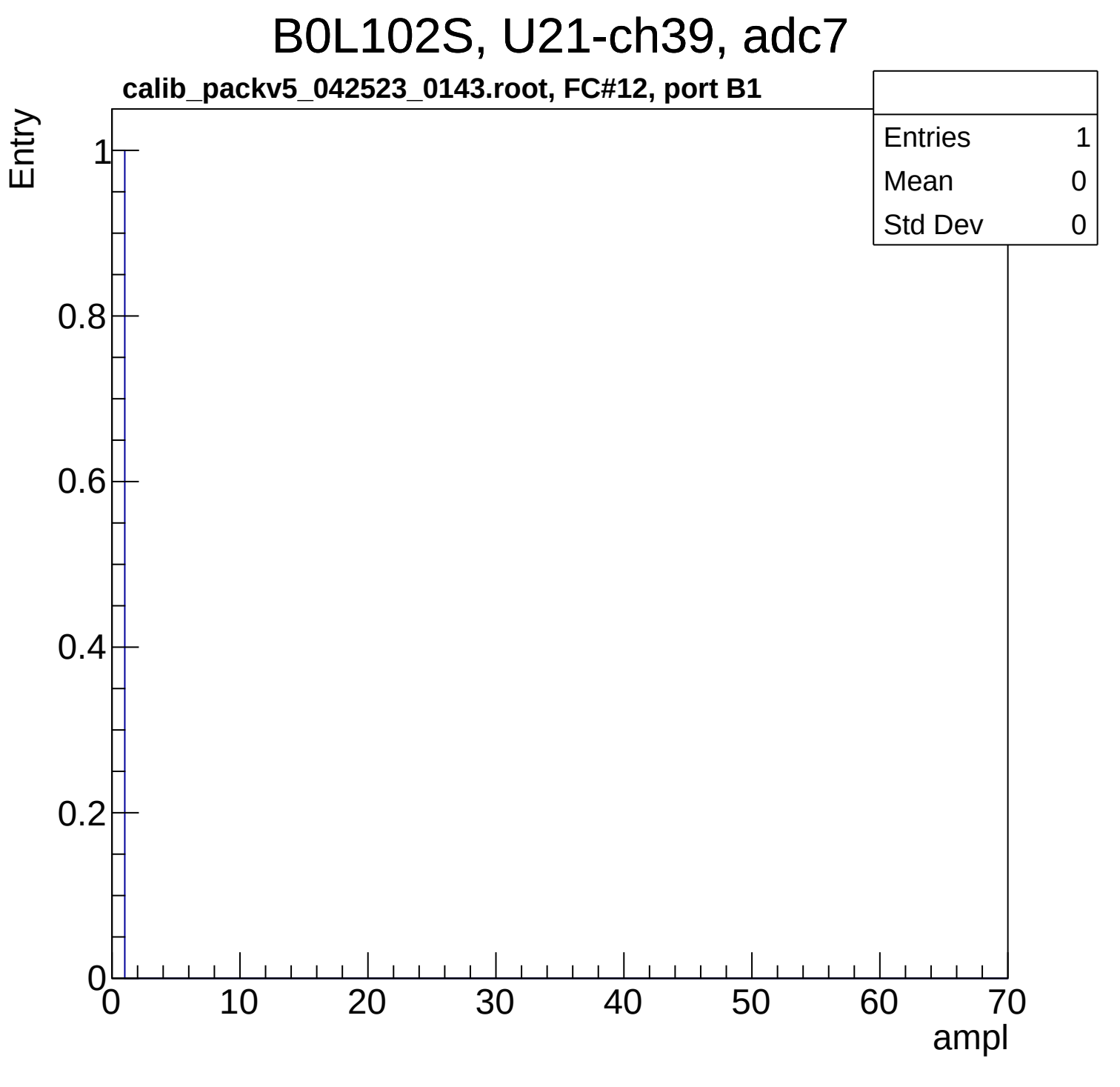
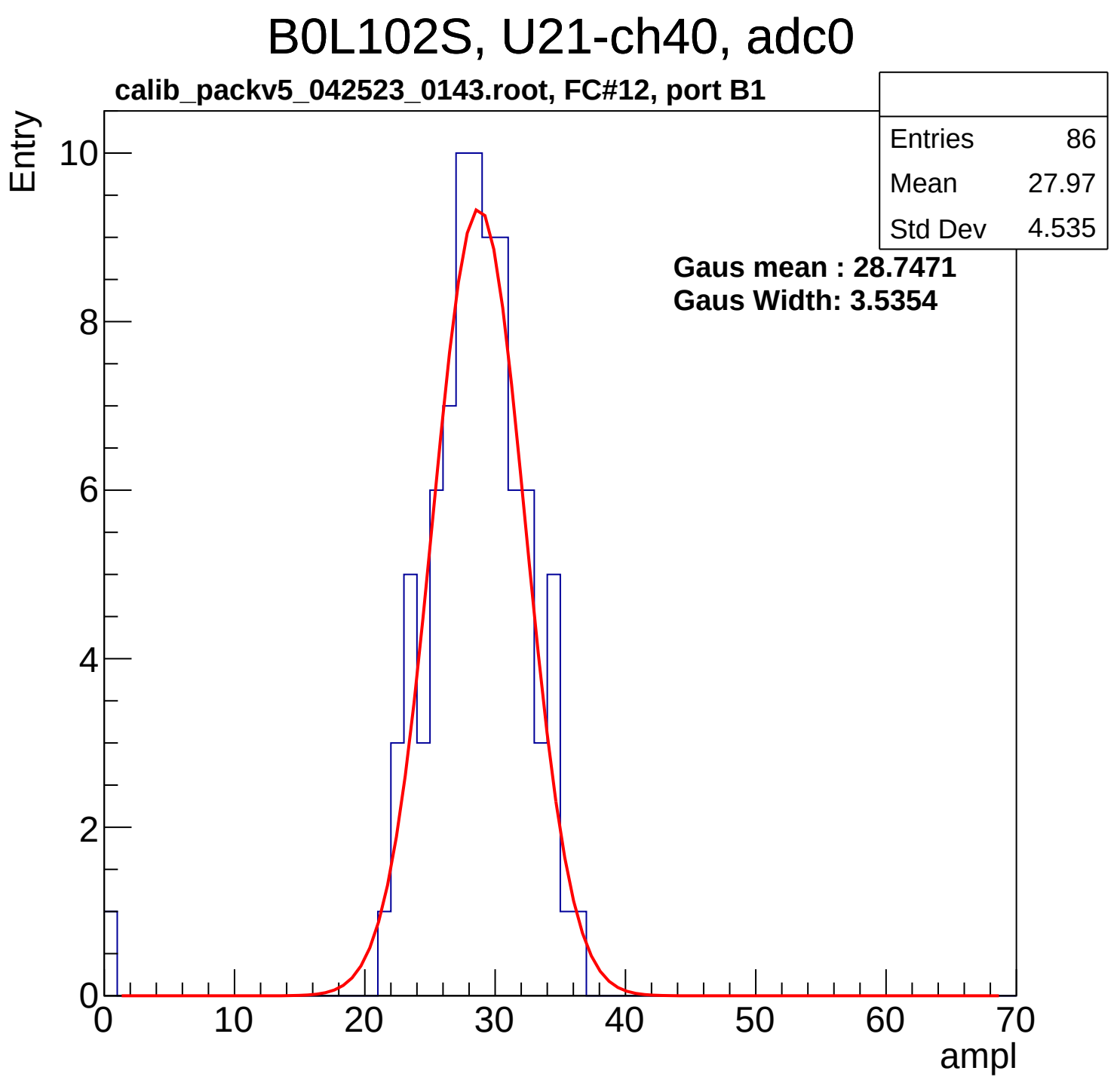

B0L102S, U21-ch39, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 49.11
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

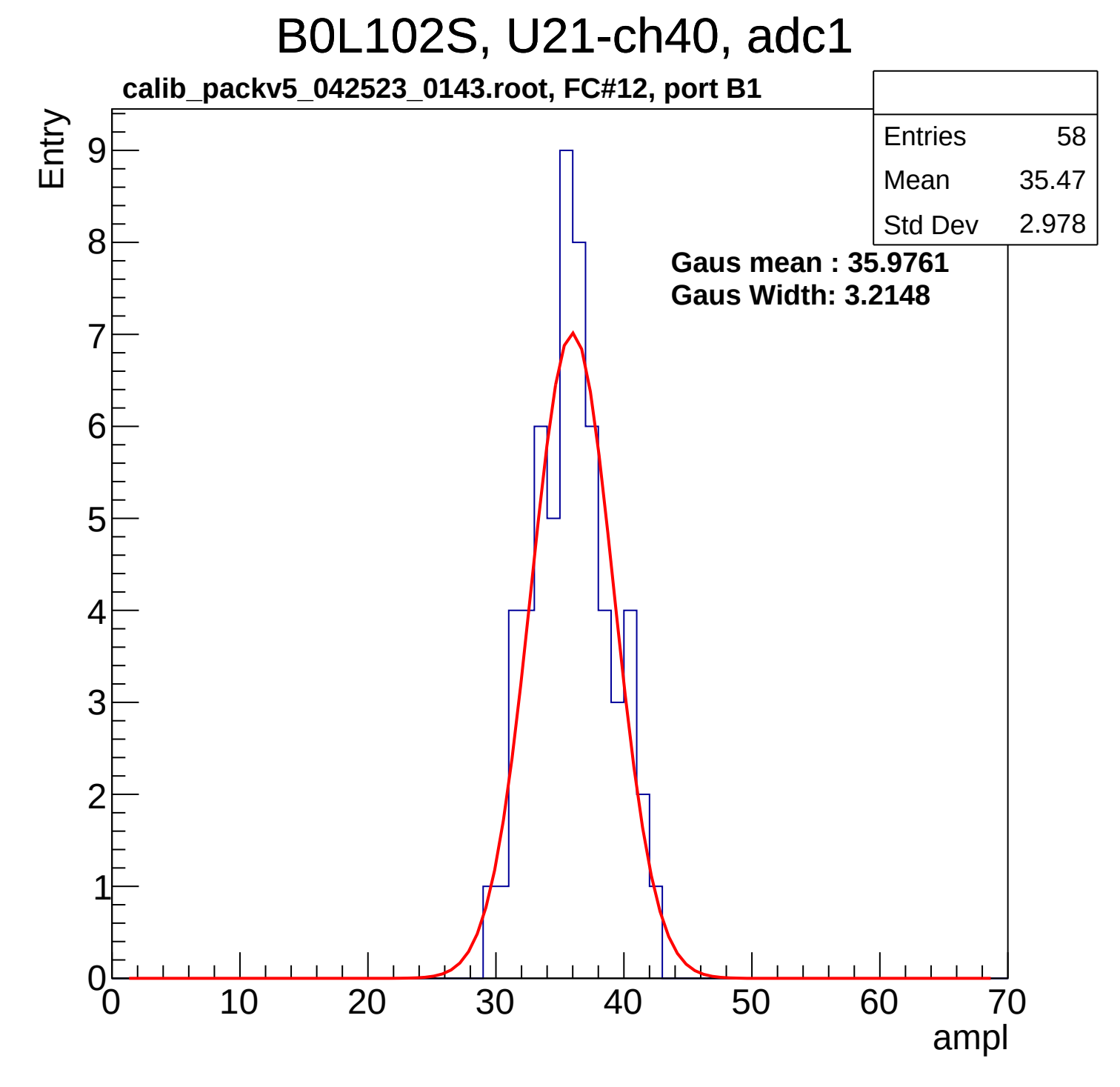

B0L102S, U21-ch39, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 55.9
Std Dev 3.22
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

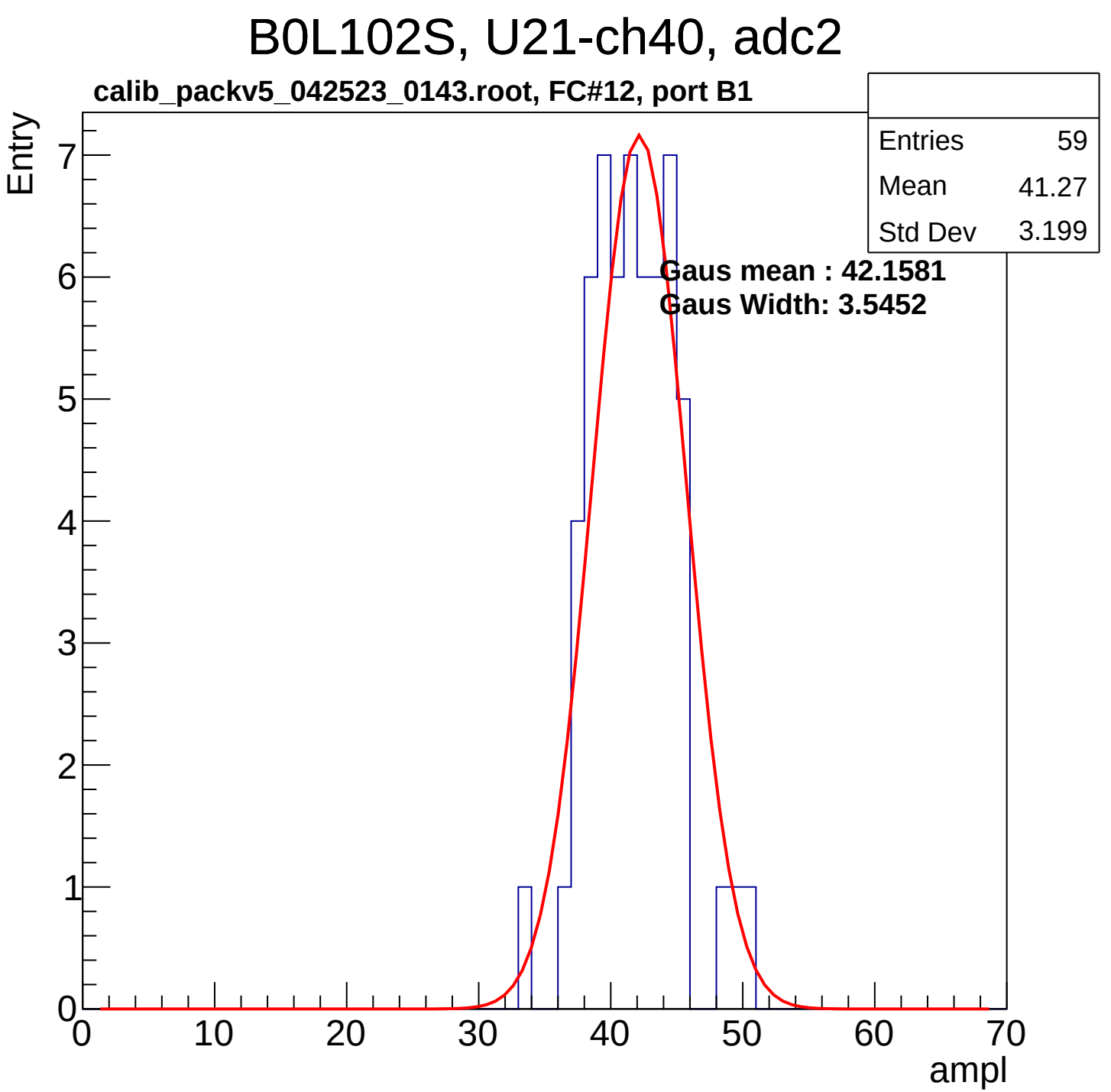

B0L102S, U21-ch39, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 29
Mean 60.9
Std Dev 2.107
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

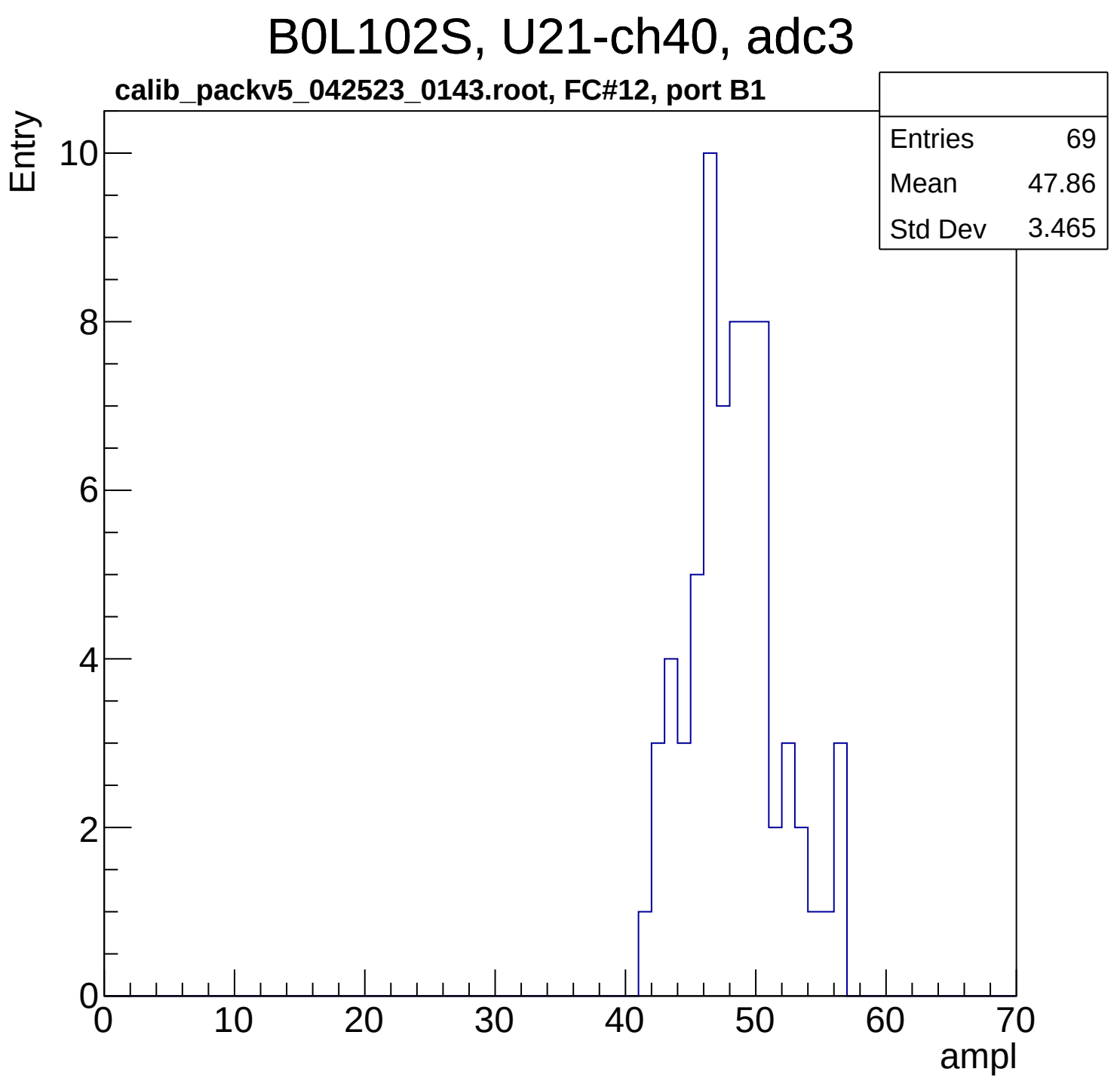

B0L102S, U21-ch39, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.29
Std Dev 21.76
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

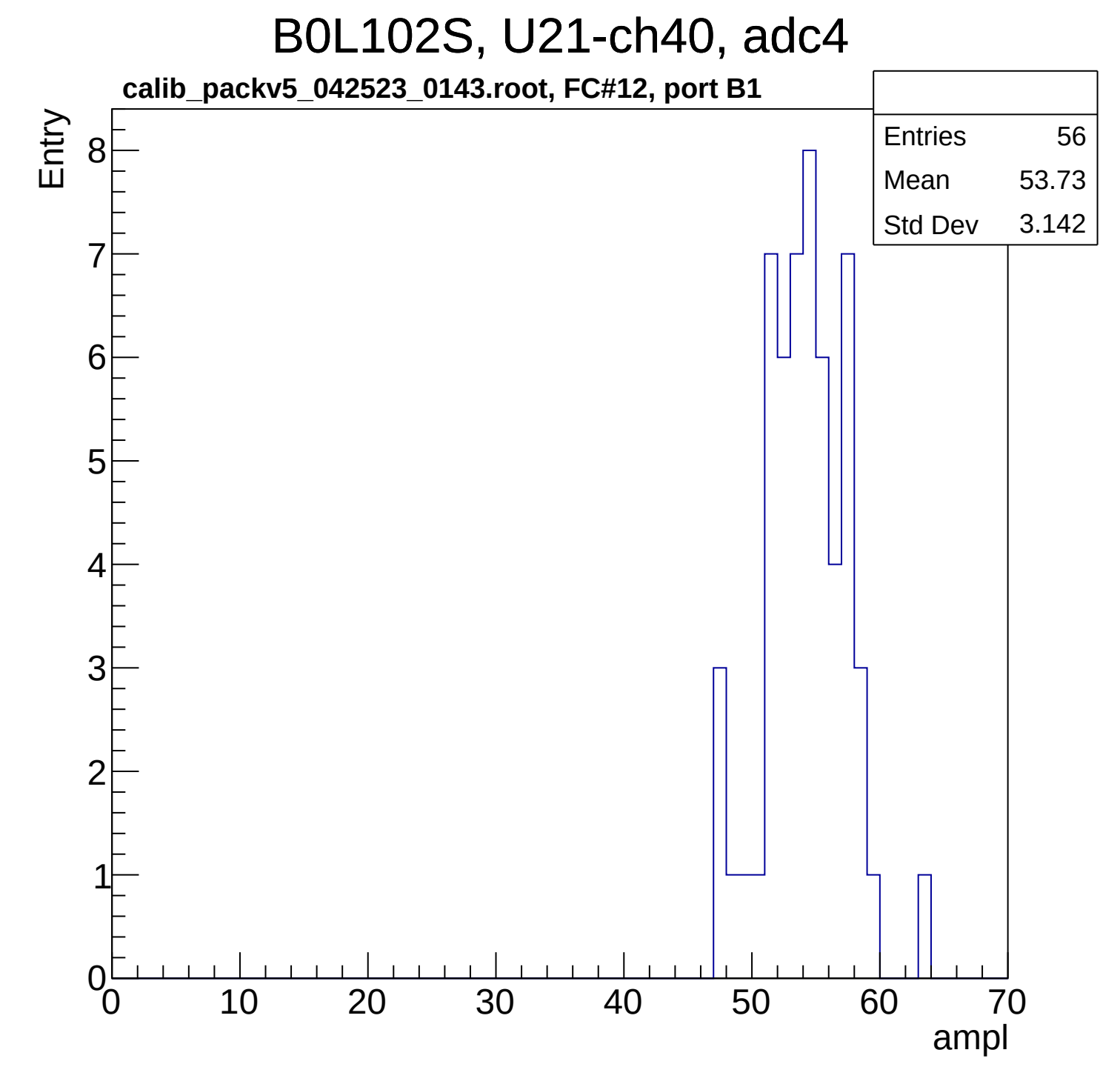

B0L102S, U21-ch39, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

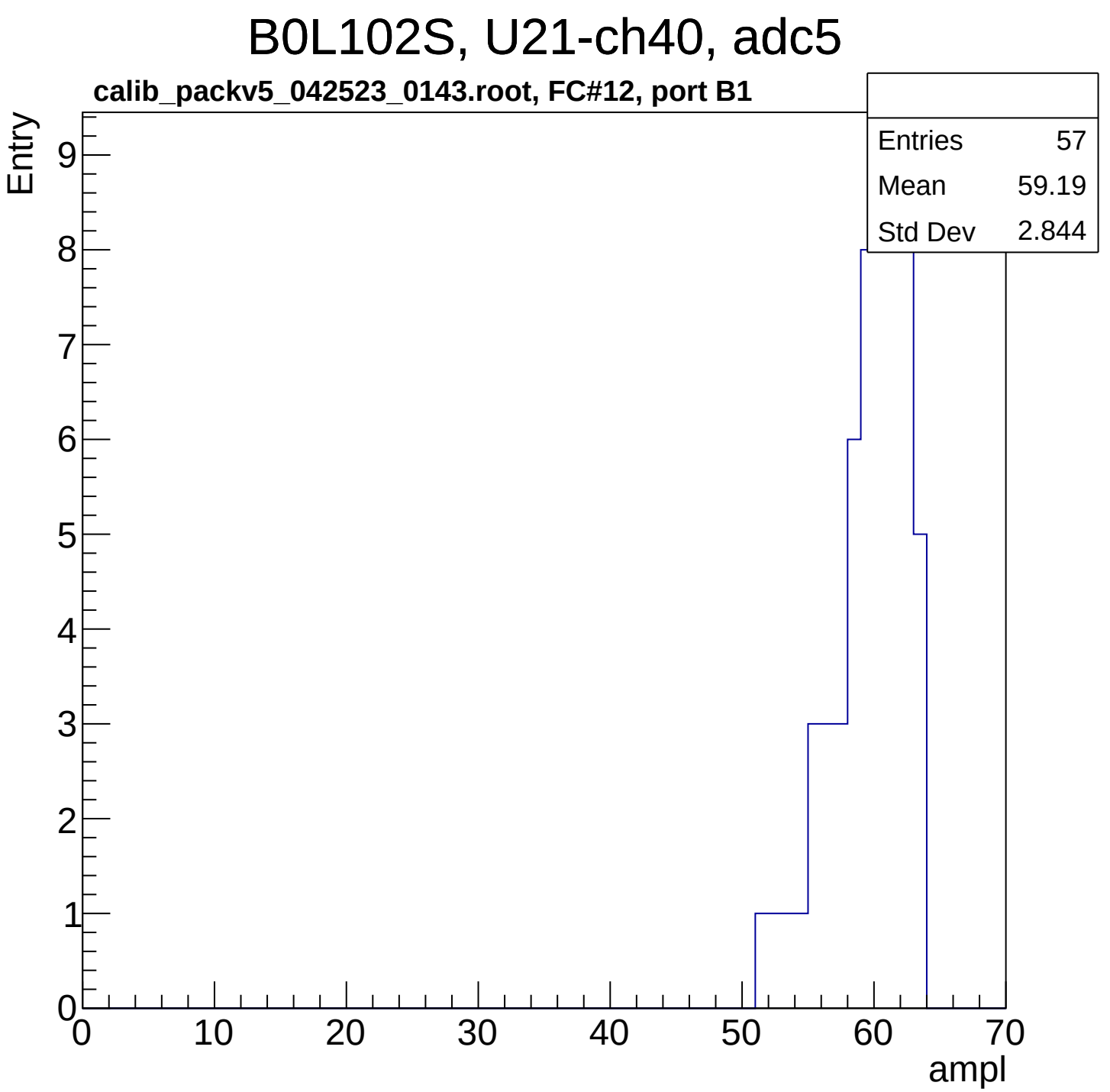

B0L102S, U21-ch40, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 27.97
Std Dev 4.535
Gaus mean : 28.7471
Gaus Width: 3.5354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch40, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 35.47
Std Dev 2.978
Gaus mean : 35.9761
Gaus Width: 3.2148
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch40, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 41.27
Std Dev 3.199
Gaus mean : 42.1581
Gaus Width: 3.5452
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch40, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 47.86
Std Dev 3.465
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch40, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 53.73
Std Dev 3.142
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch40, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 59.19
Std Dev 2.844
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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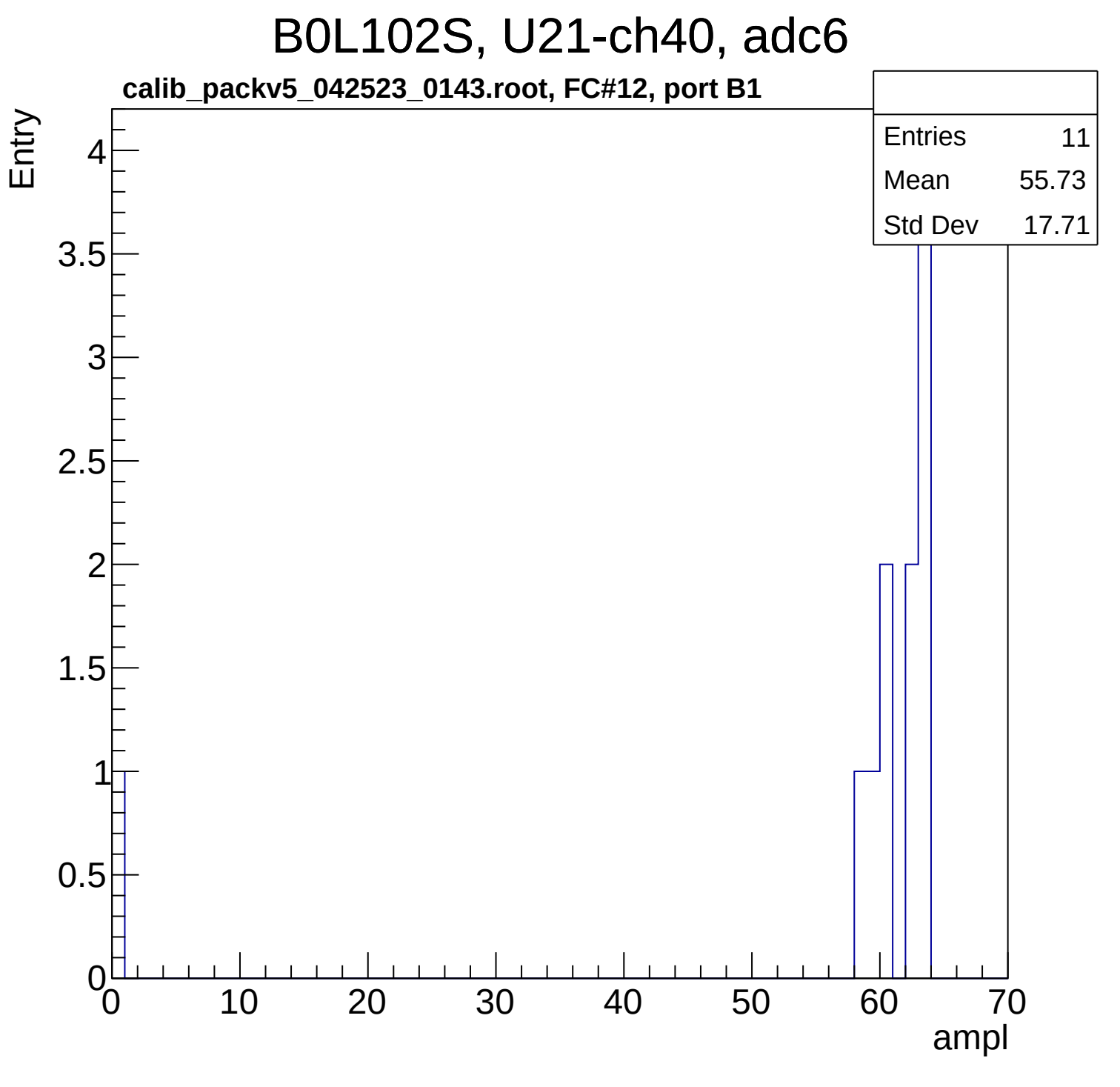
60
70


B0L102S, U21-ch40, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 55.73
Std Dev 17.71
Entry
ampl
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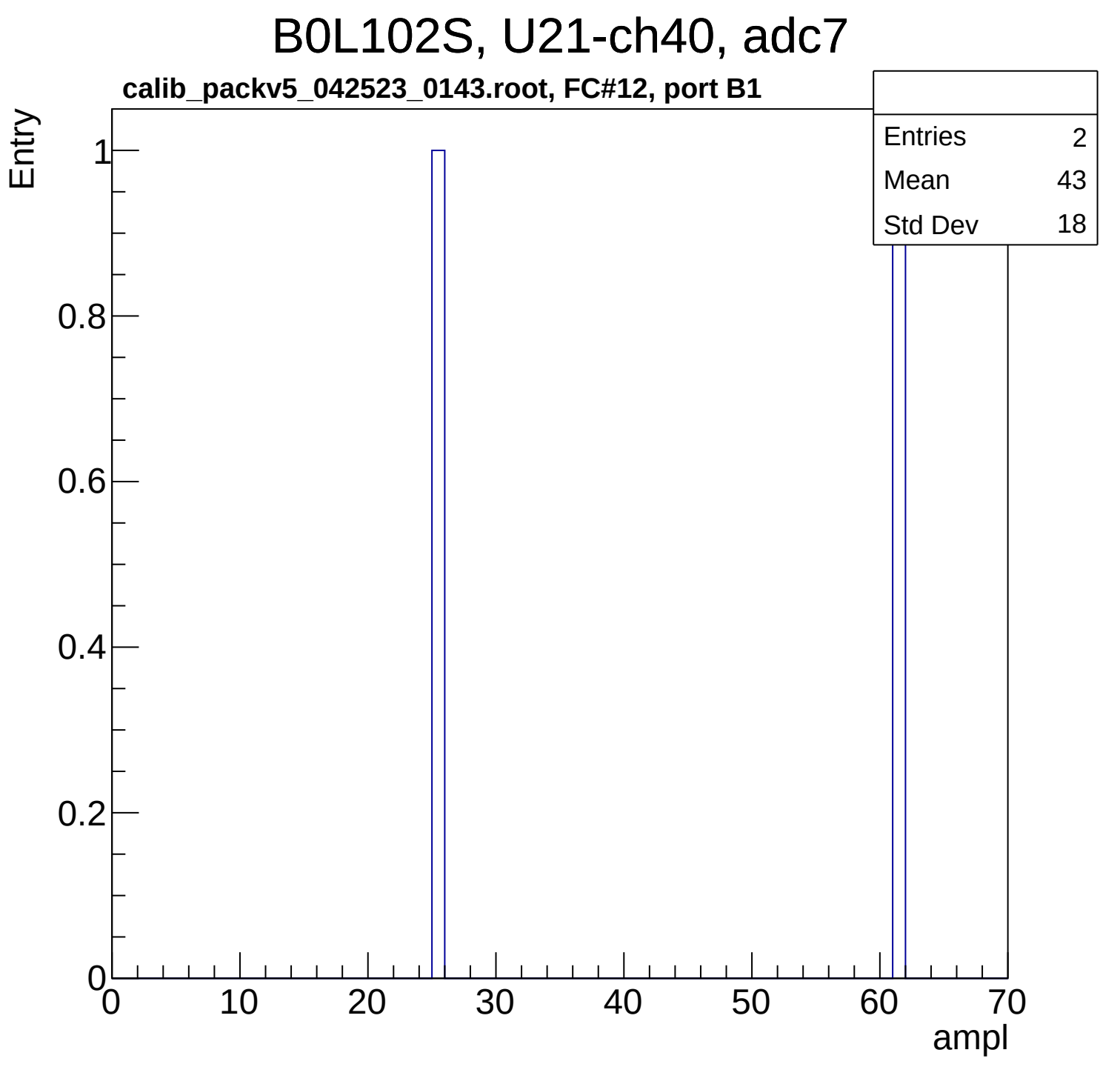
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch40, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 43
Std Dev 18
Entry
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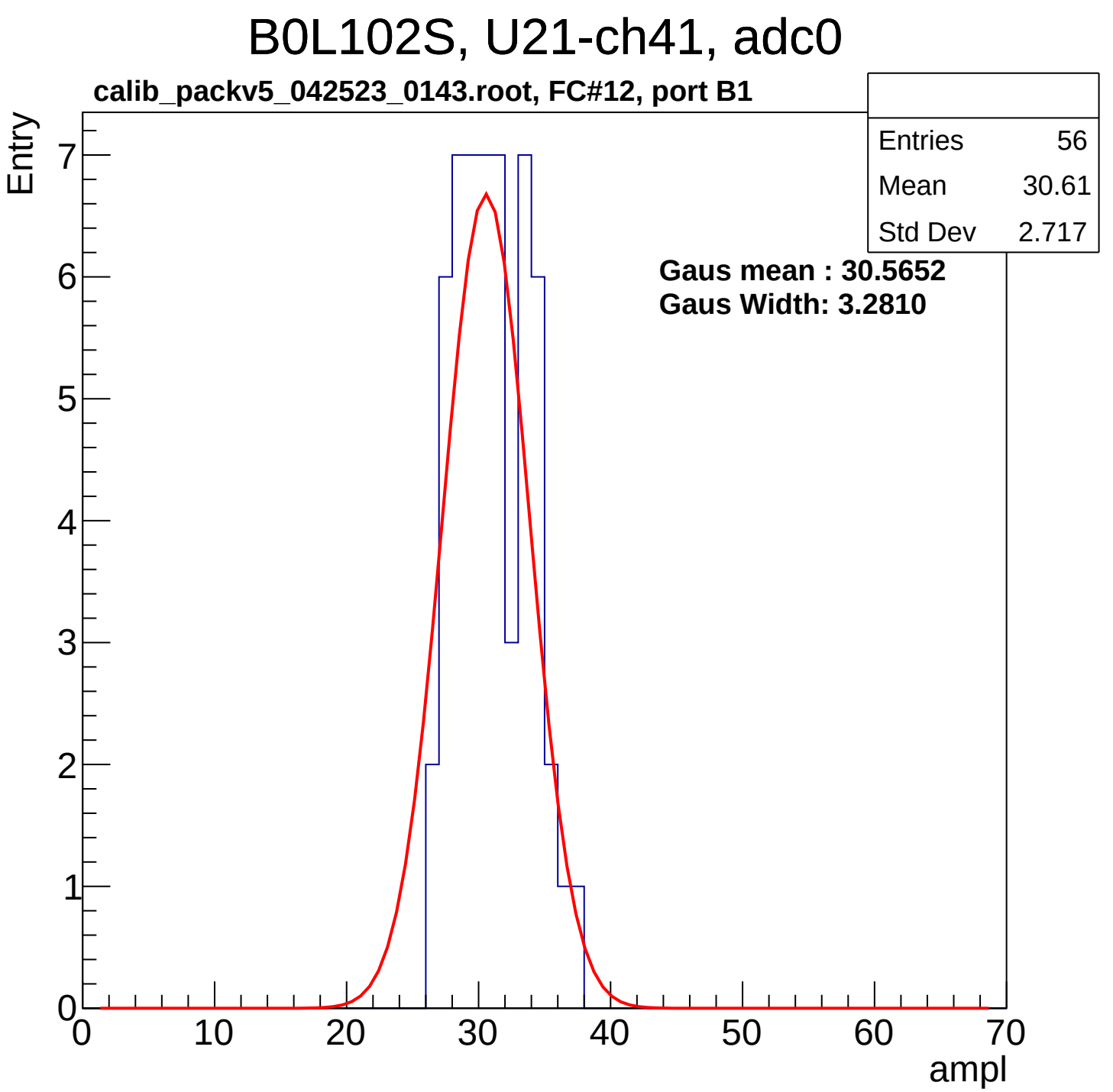
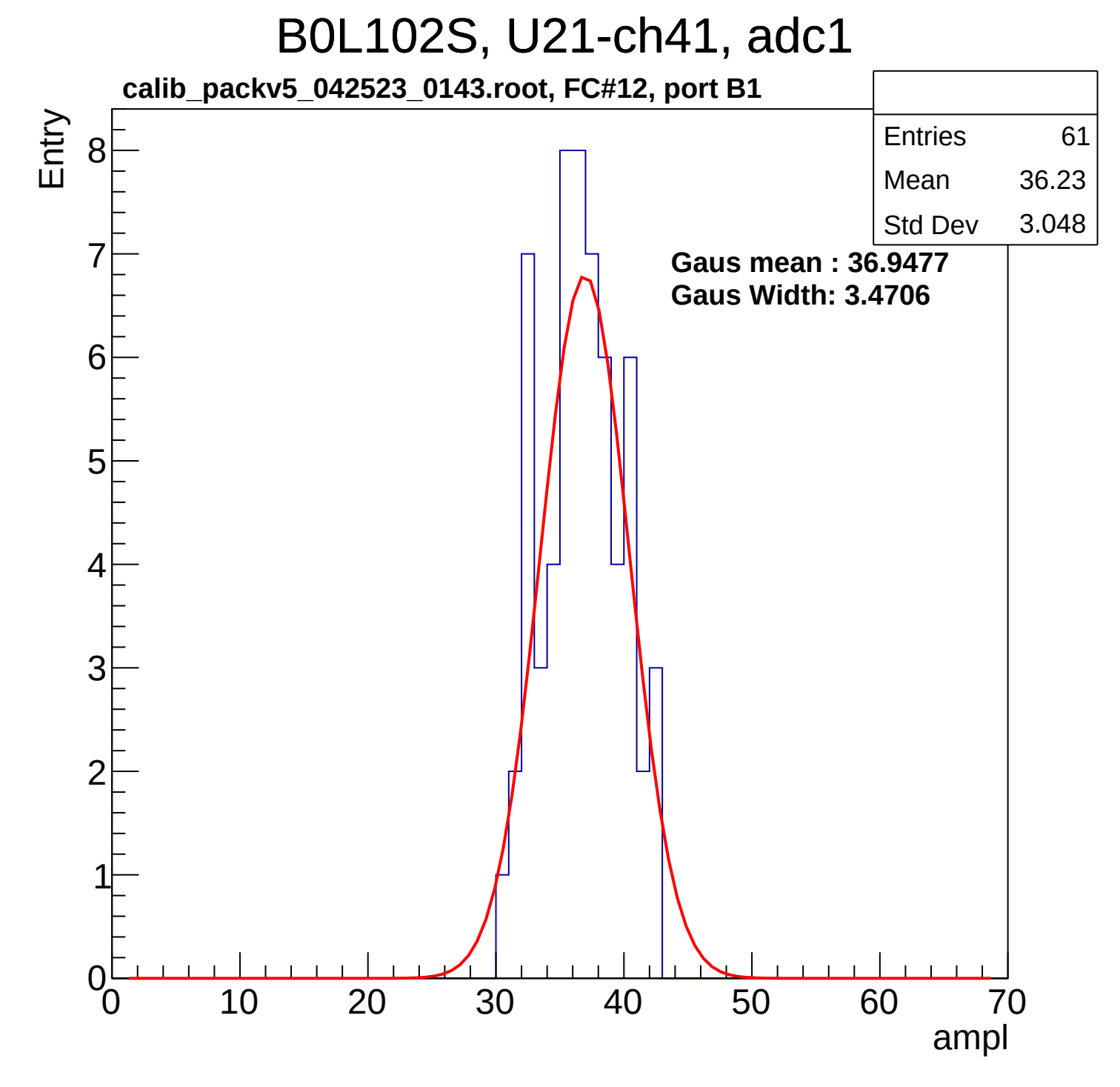
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch41, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 30.61
Std Dev 2.717
Gaus mean : 30.5652
Gaus Width: 3.2810
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch41, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 36.23
Std Dev 3.048
Gaus mean : 36.9477
Gaus Width: 3.4706
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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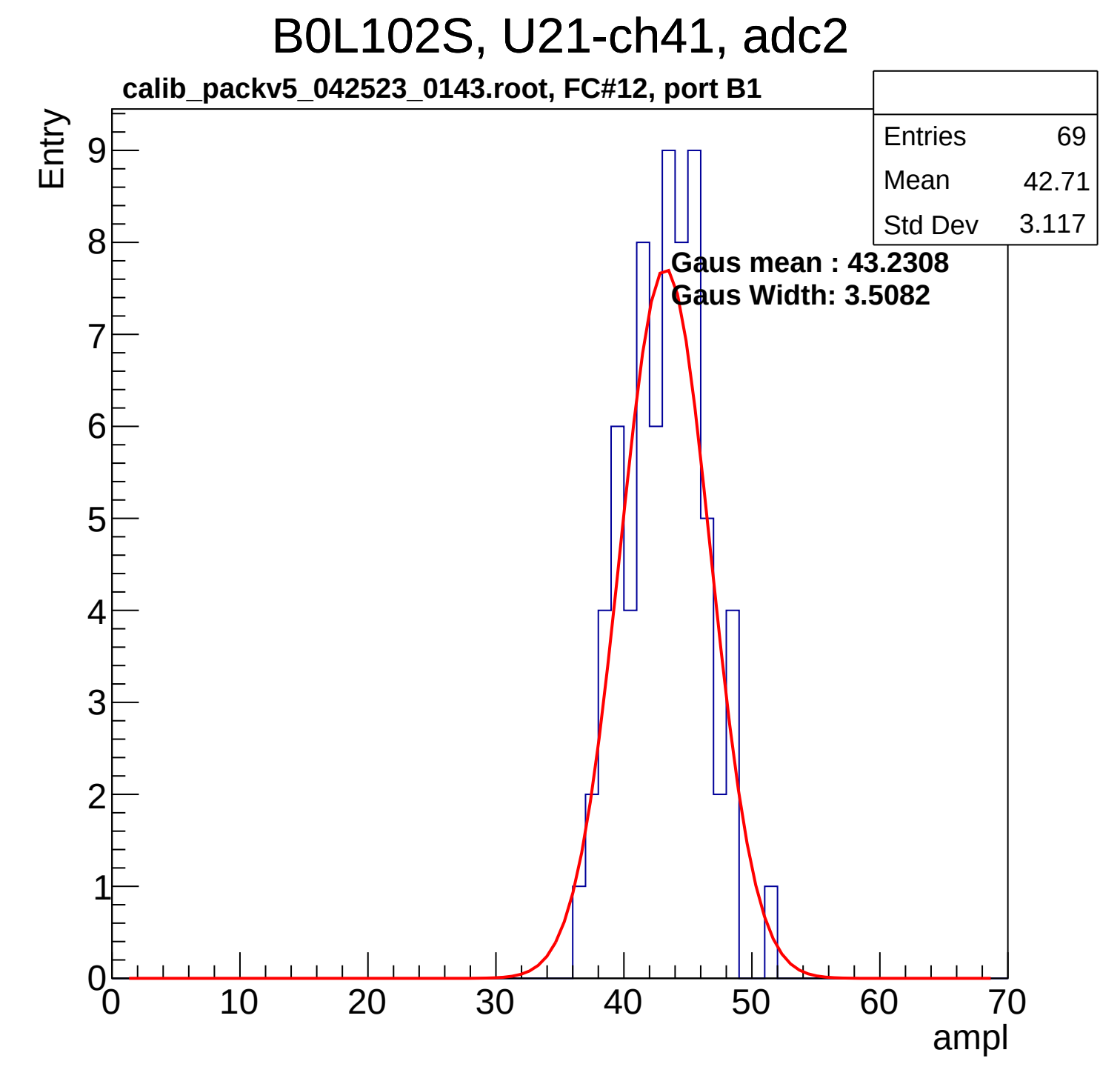
60
70


B0L102S, U21-ch41, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 42.71
Std Dev 3.117
Gaus mean : 43.2308
Gaus Width: 3.5082
Entry
ampl
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3
4
5
6
7
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9
0
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60
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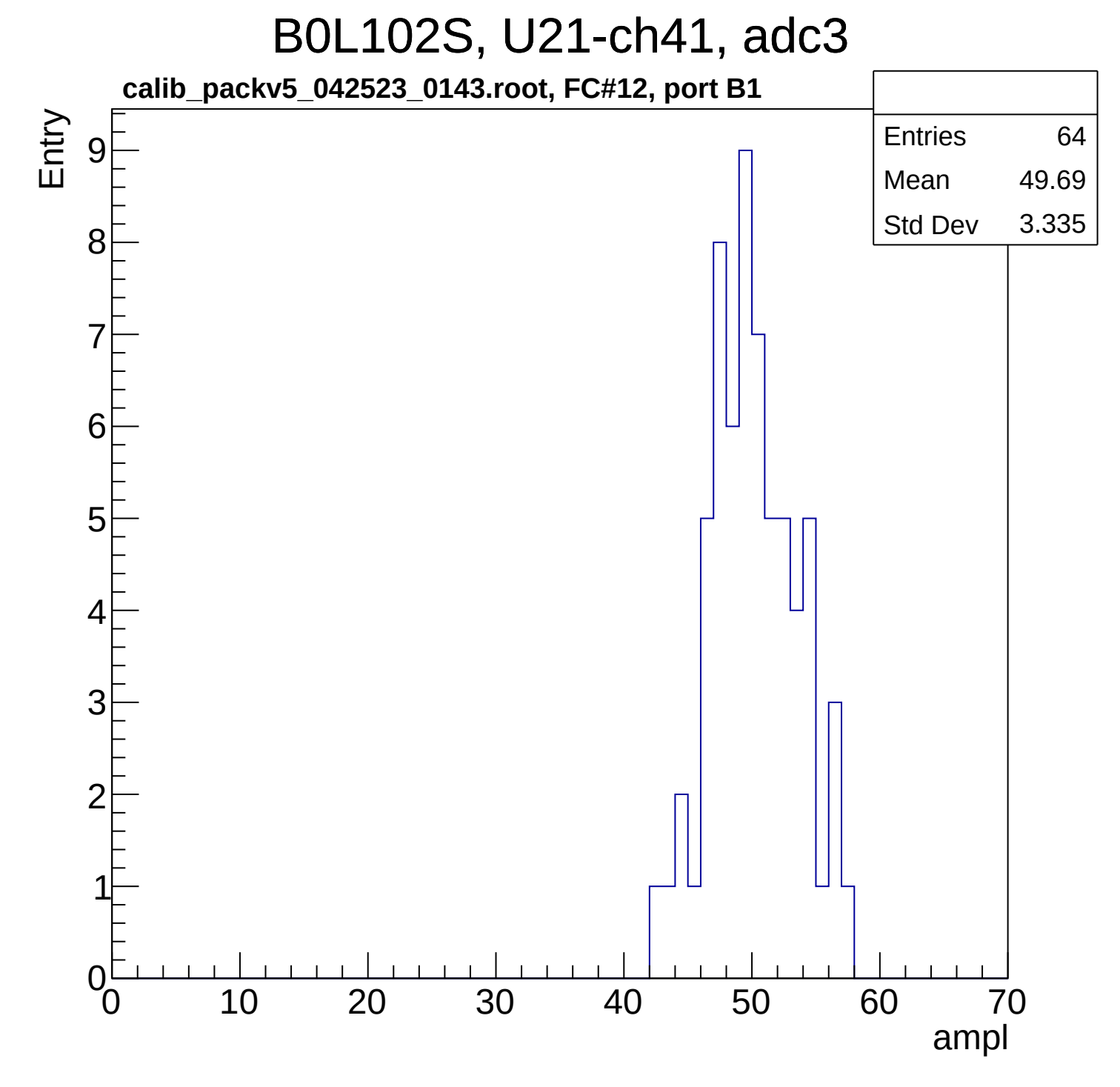
B0L102S, U21-ch41, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.69
Std Dev 3.335
Entry
ampl
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5
6
7
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0
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60
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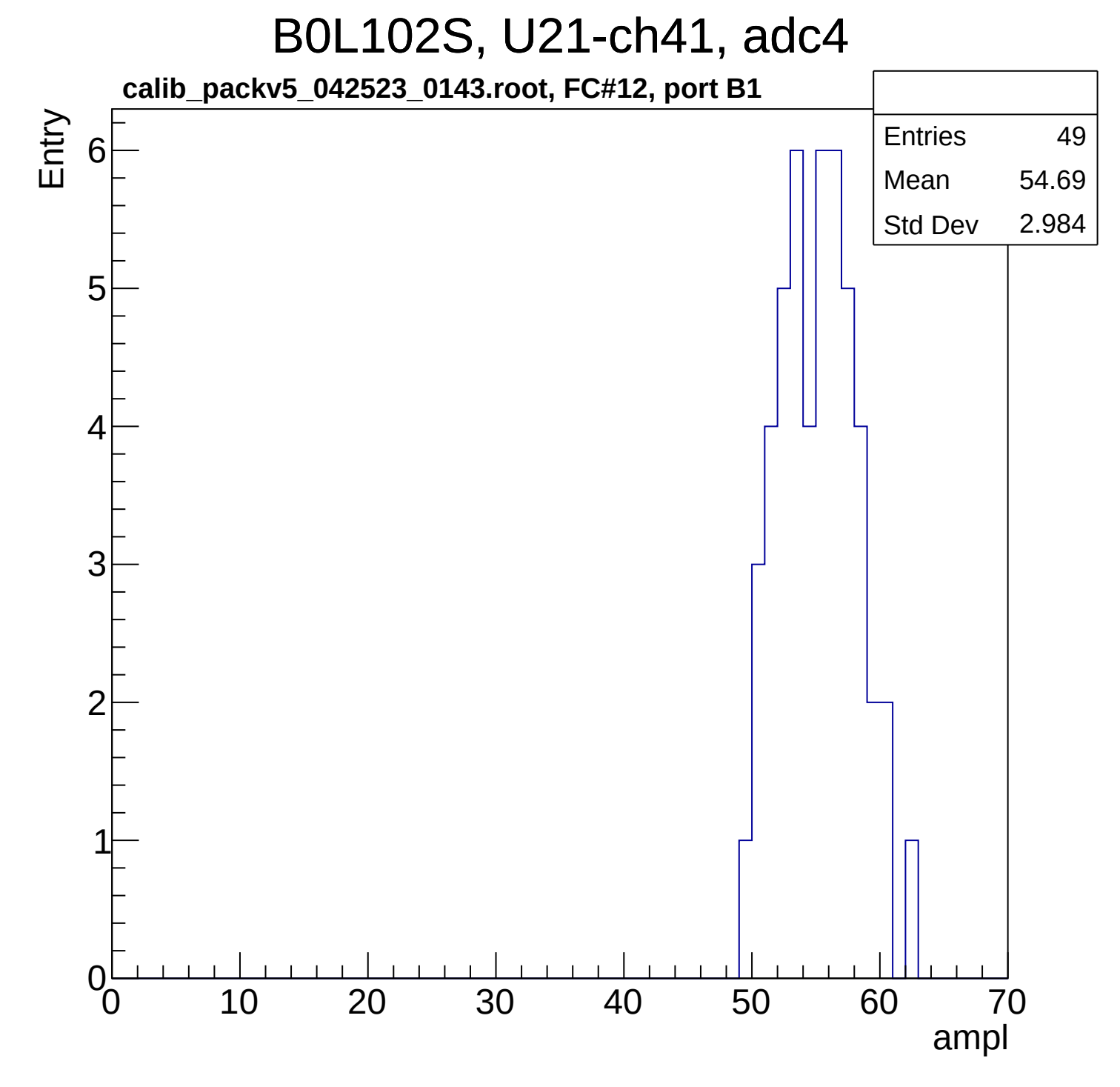
B0L102S, U21-ch41, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 54.69
Std Dev 2.984
Entry
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2
3
4
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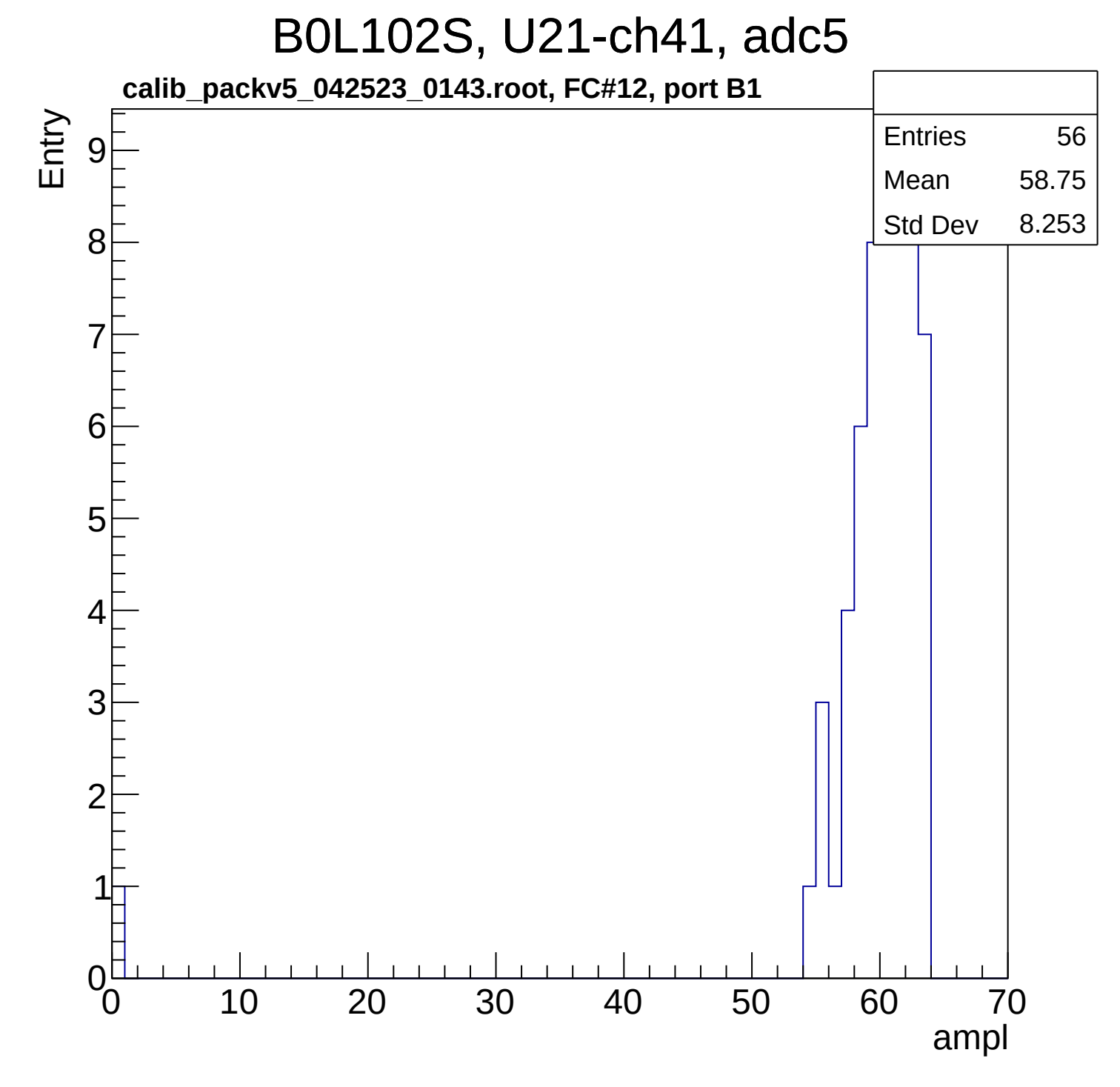
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch41, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 58.75
Std Dev 8.253
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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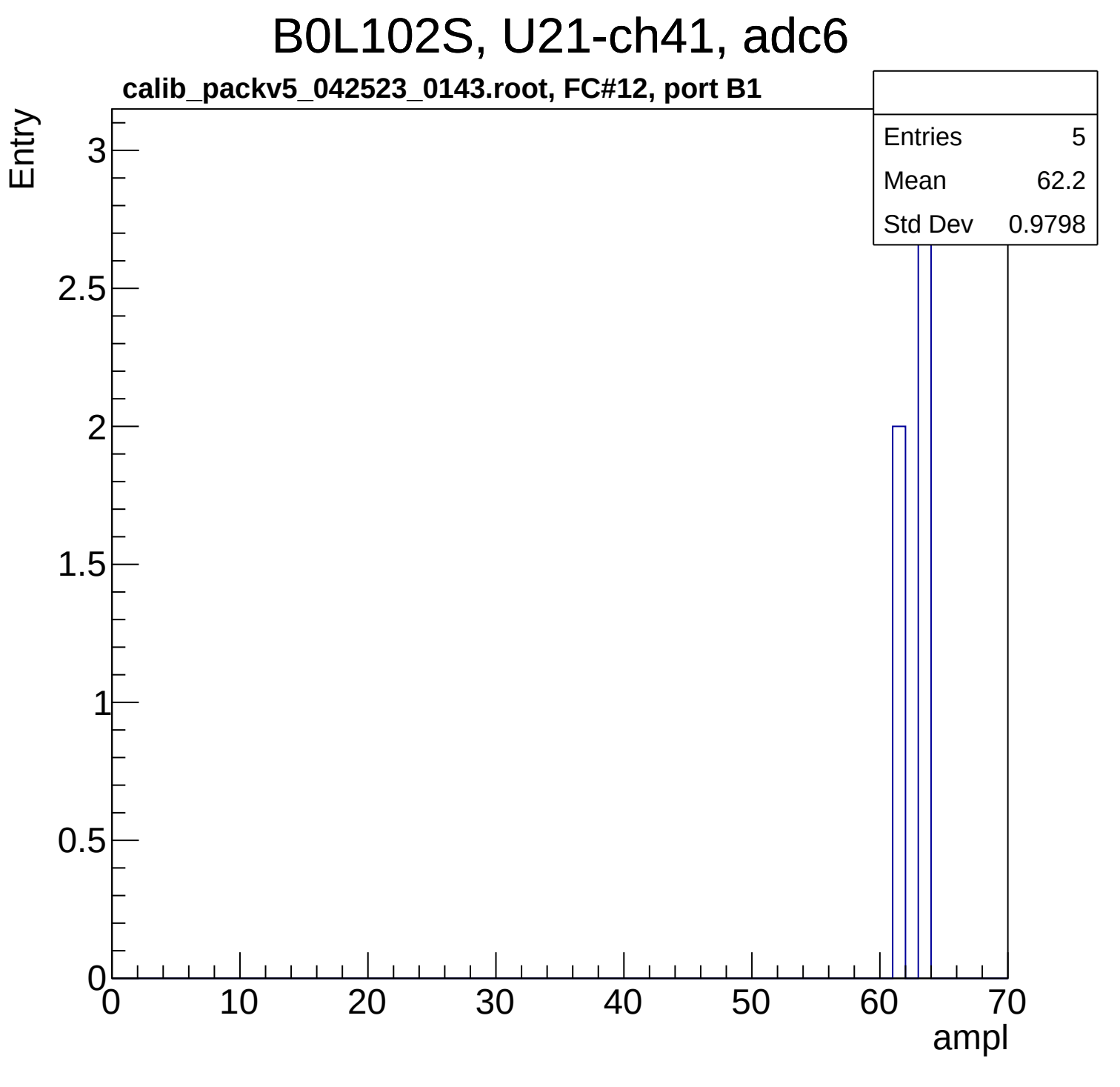
B0L102S, U21-ch41, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.2
Std Dev 0.9798
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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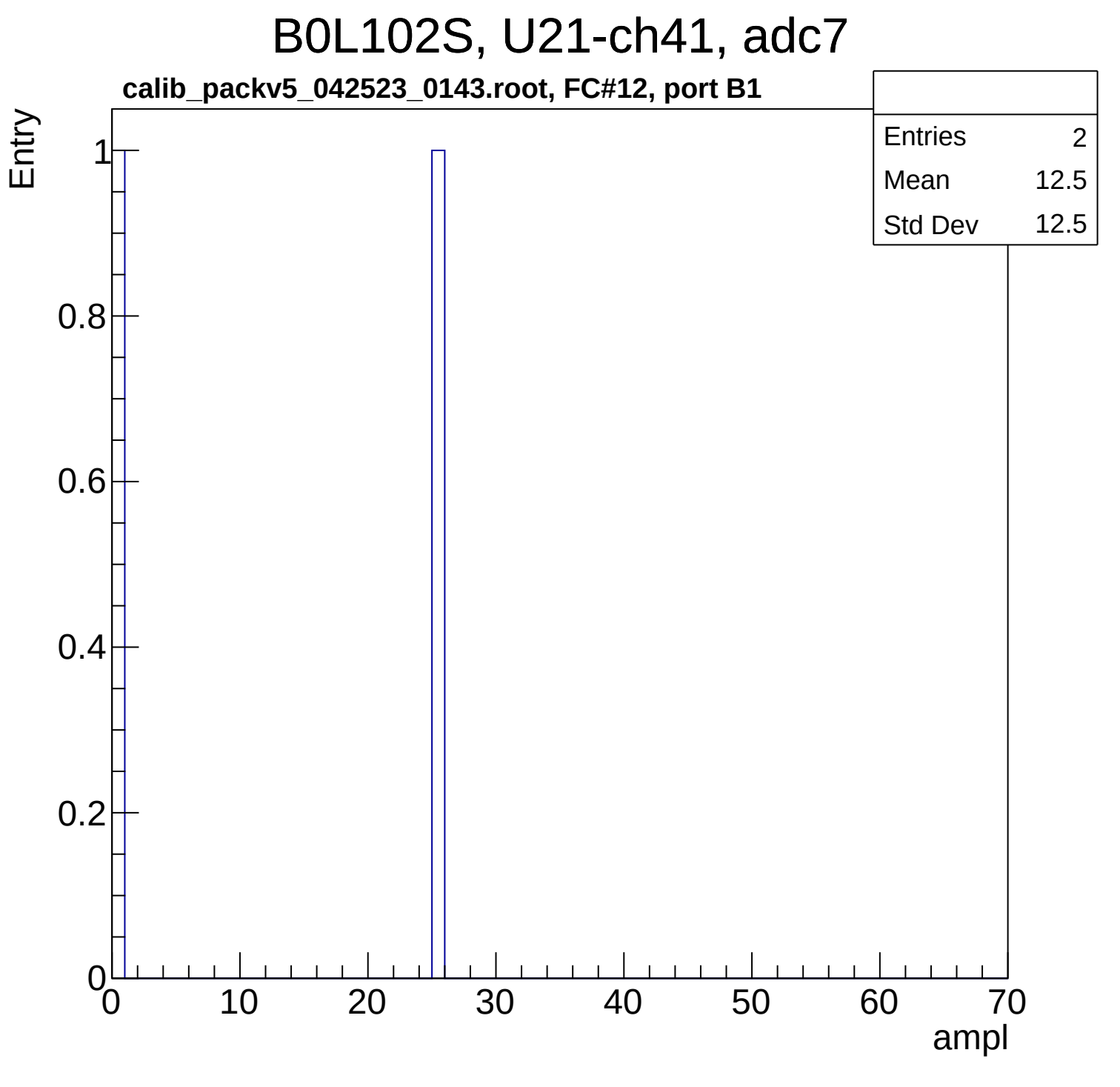
B0L102S, U21-ch41, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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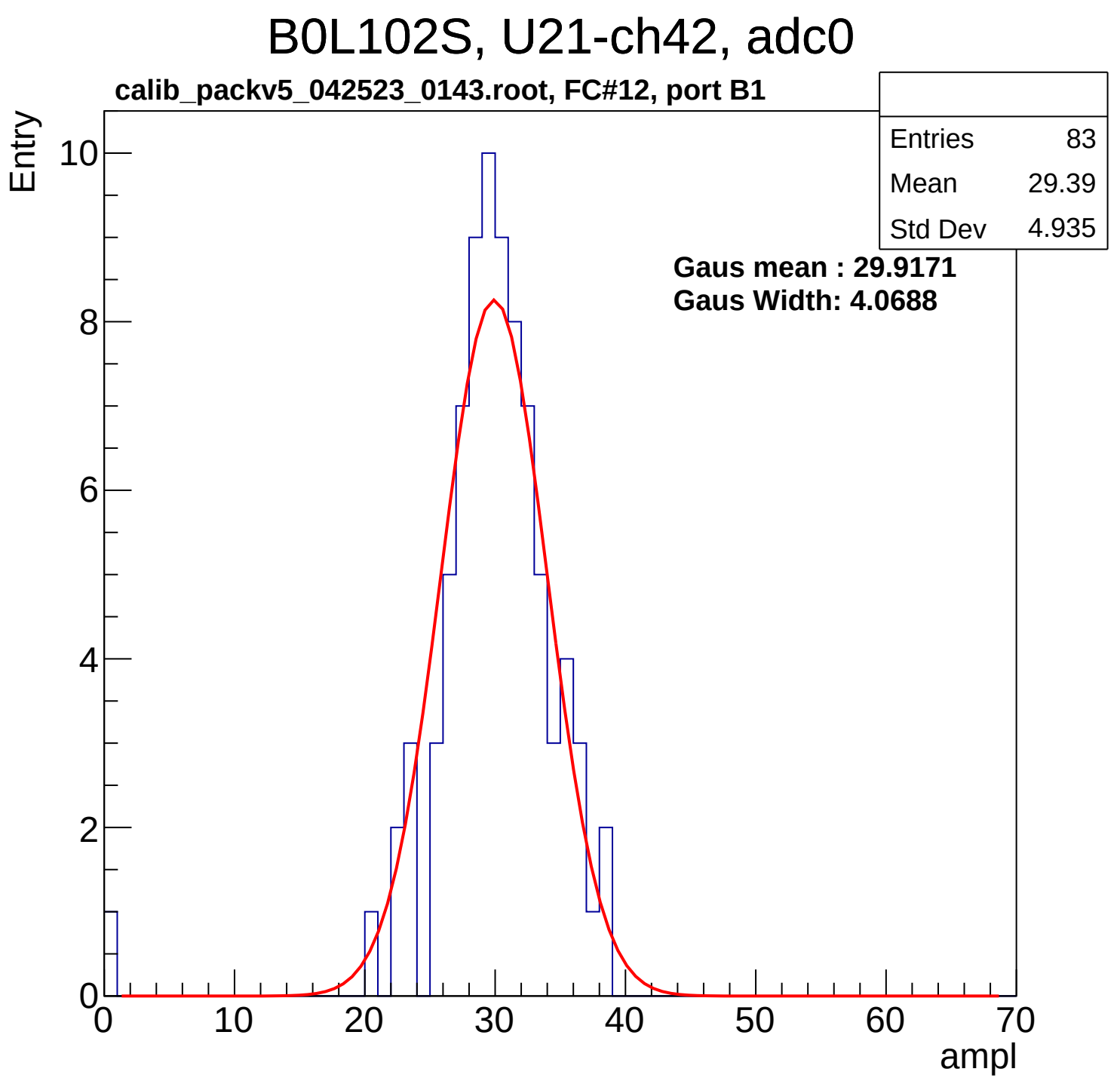
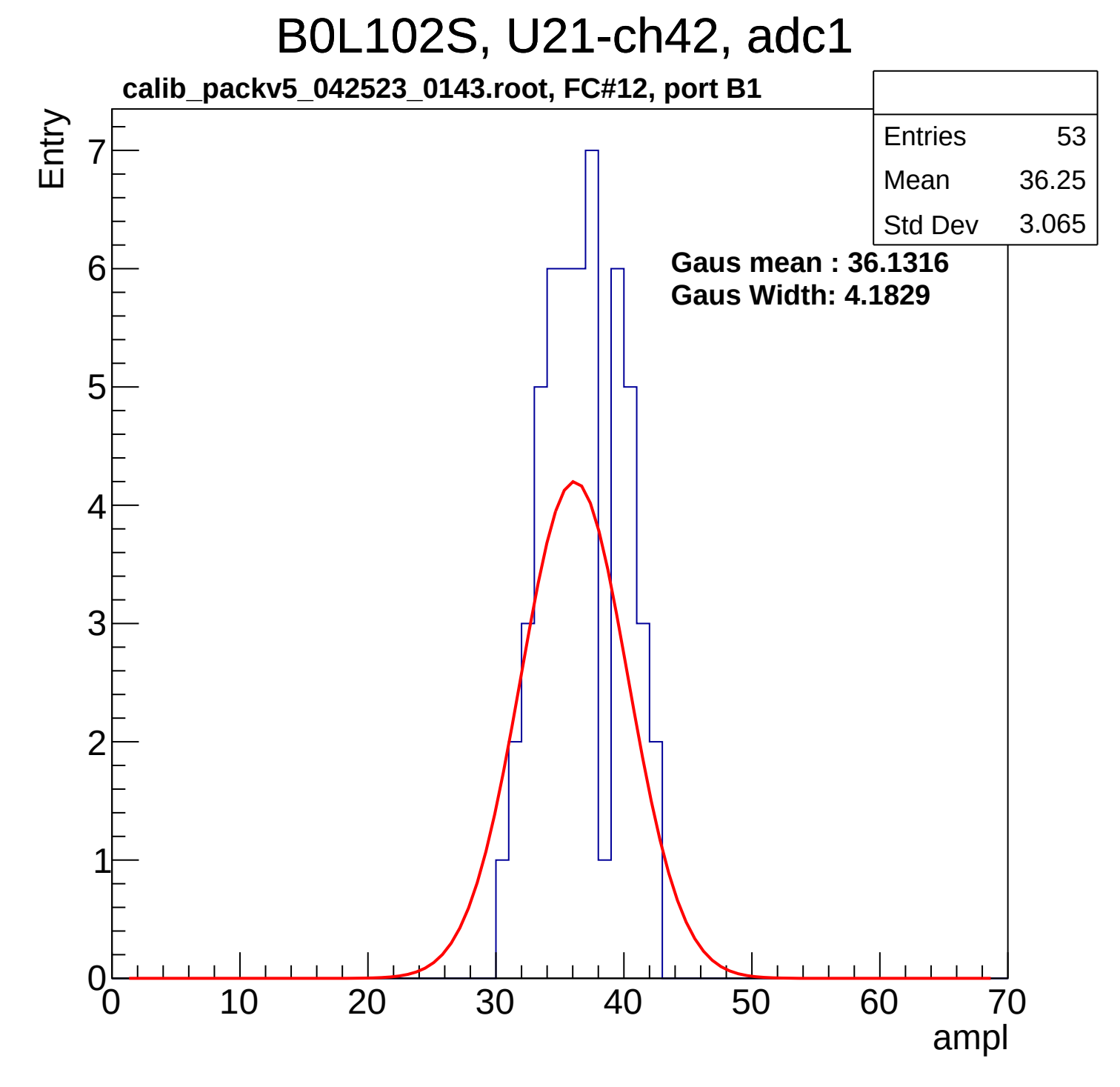
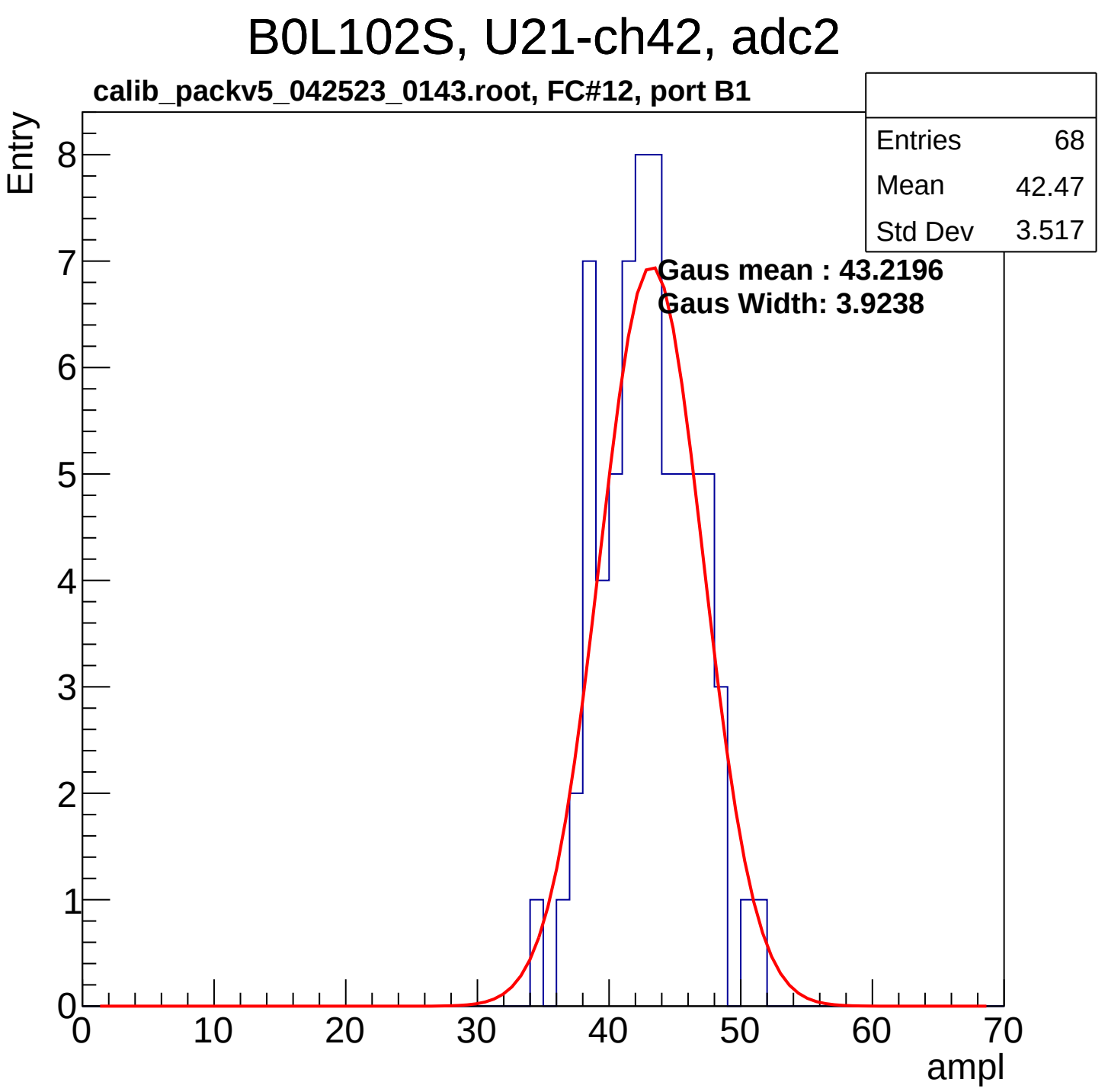
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch42, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 29.39
Std Dev 4.935
Gaus mean : 29.9171
Gaus Width: 4.0688
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch42, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 36.25
Std Dev 3.065
Gaus mean : 36.1316
Gaus Width: 4.1829
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch42, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 42.47
Std Dev 3.517
Gaus mean : 43.2196
Gaus Width: 3.9238
Entry
ampl
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2
3
4
5
6
7
8
0
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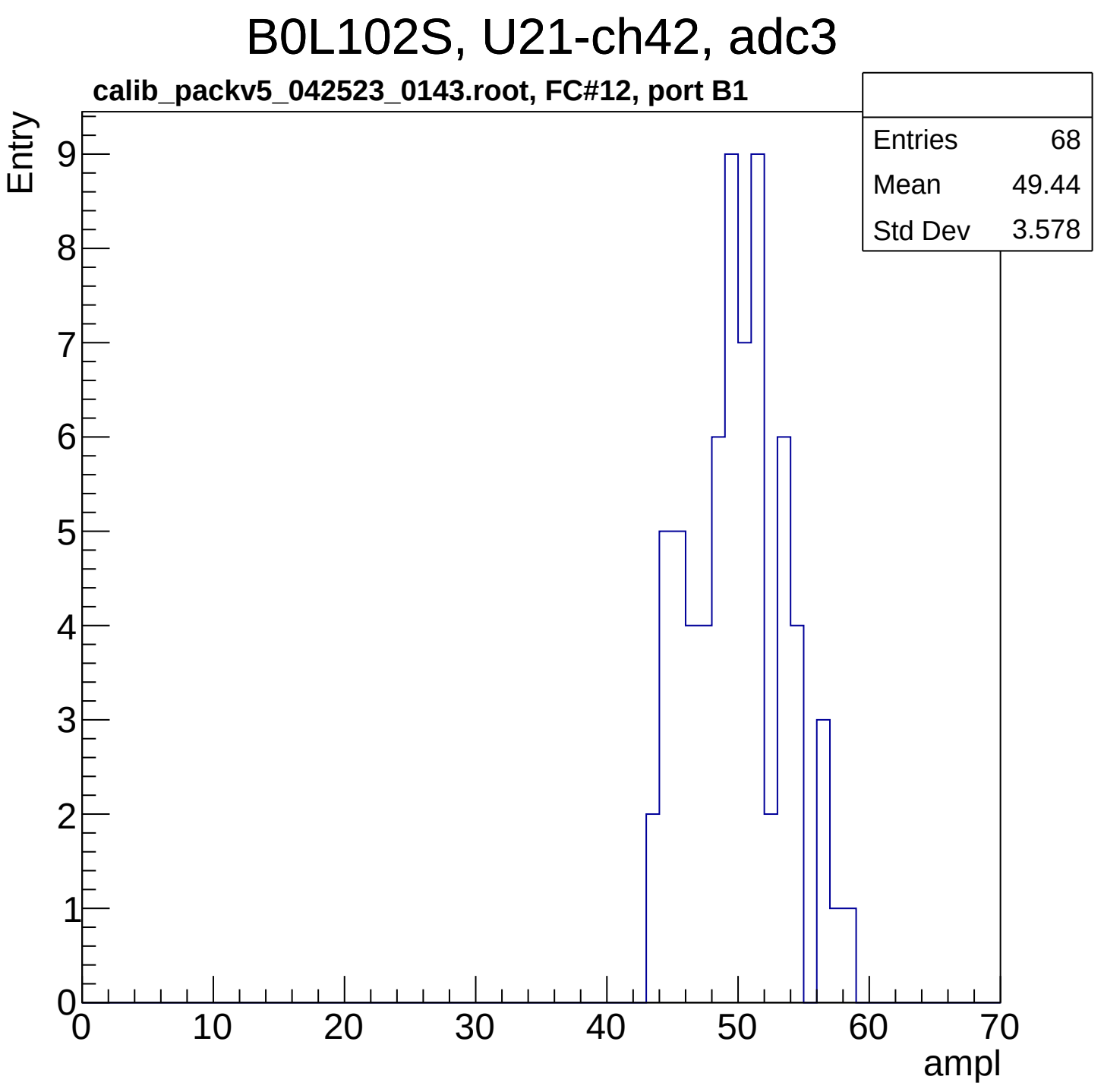
B0L102S, U21-ch42, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.44
Std Dev 3.578
Entry
ampl
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3
4
5
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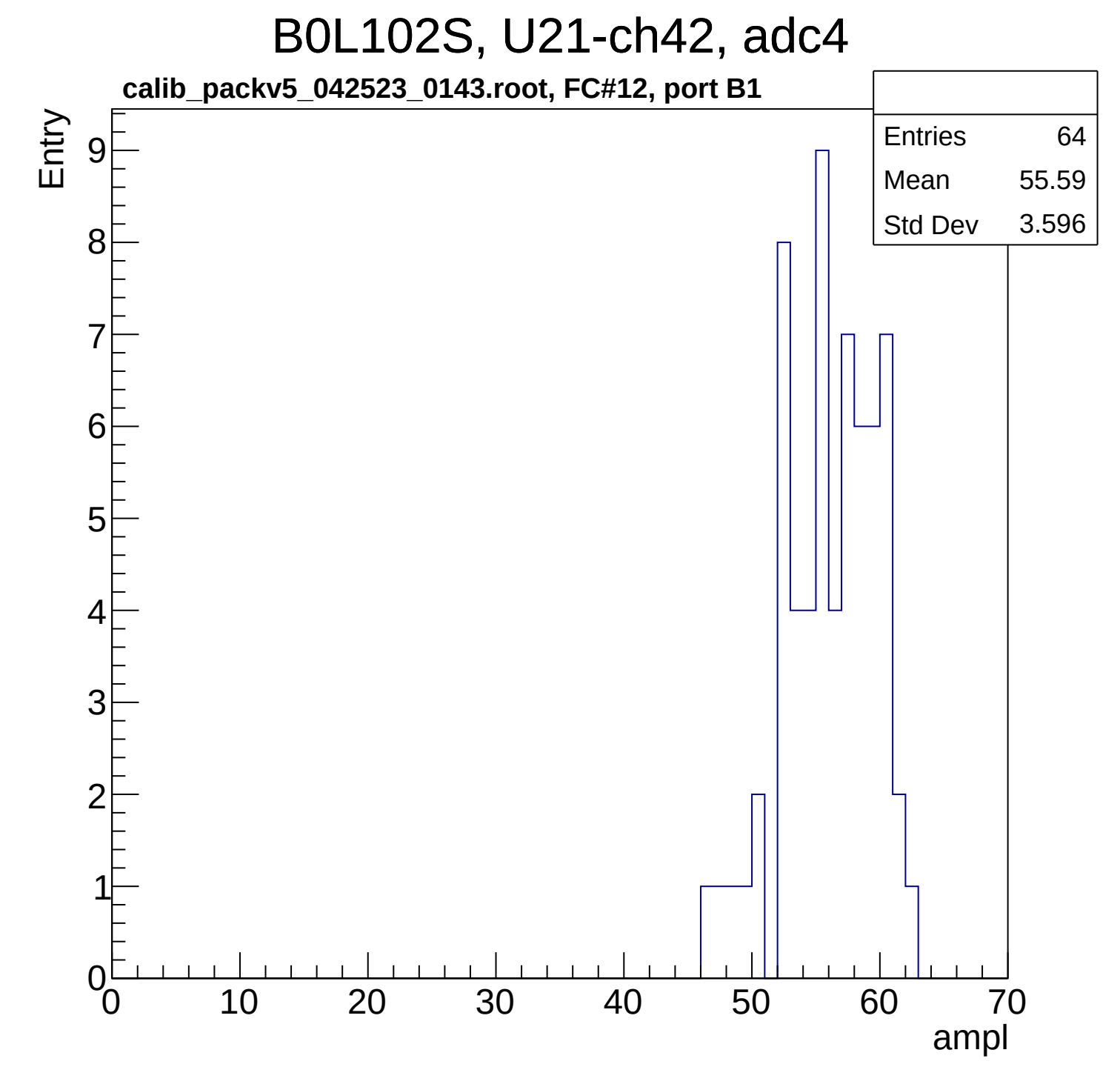
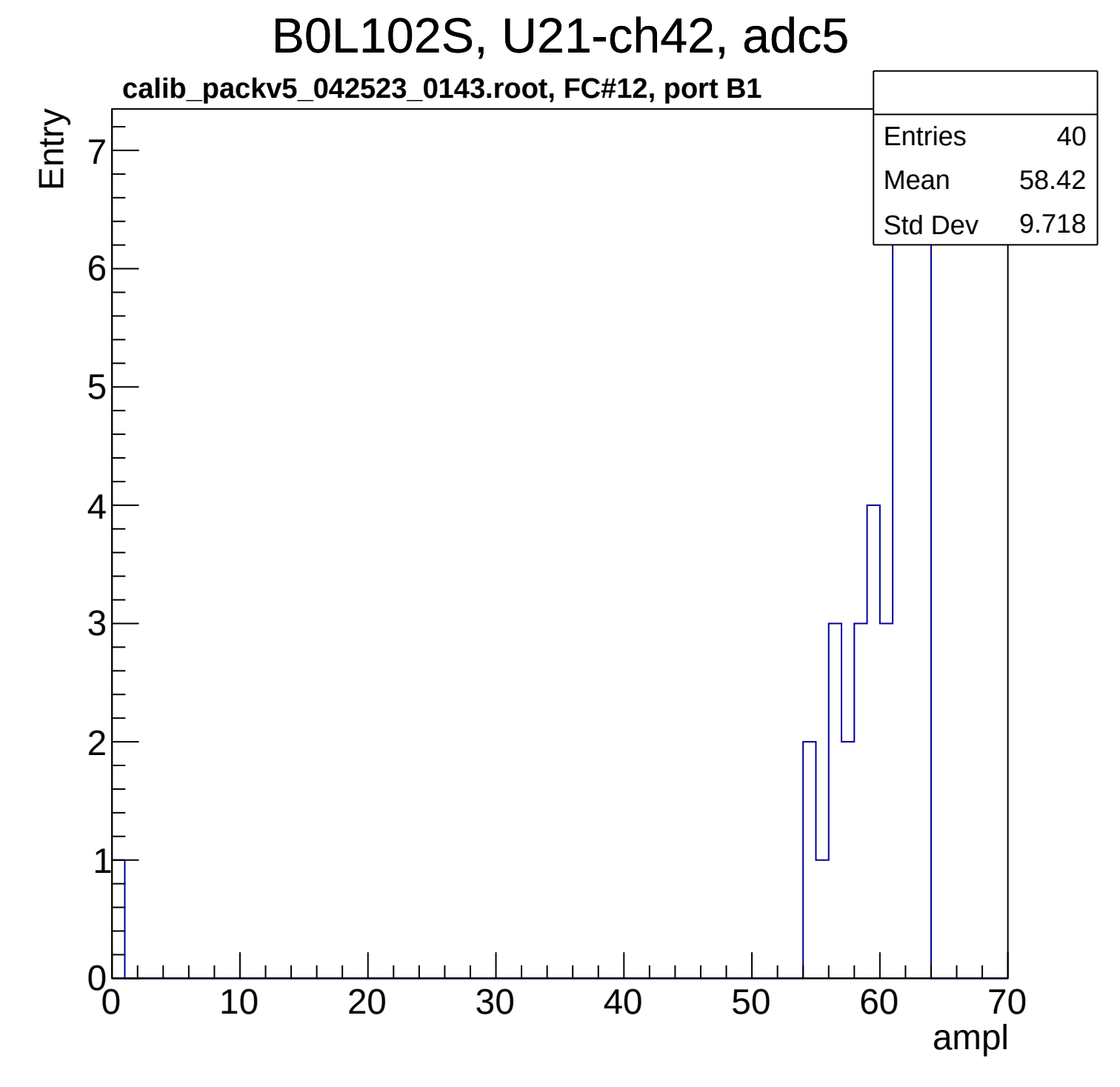

B0L102S, U21-ch42, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 55.59
Std Dev 3.596
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch42, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.42
Std Dev 9.718
Entry
ampl
0
1
2
3
4
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6
7
0
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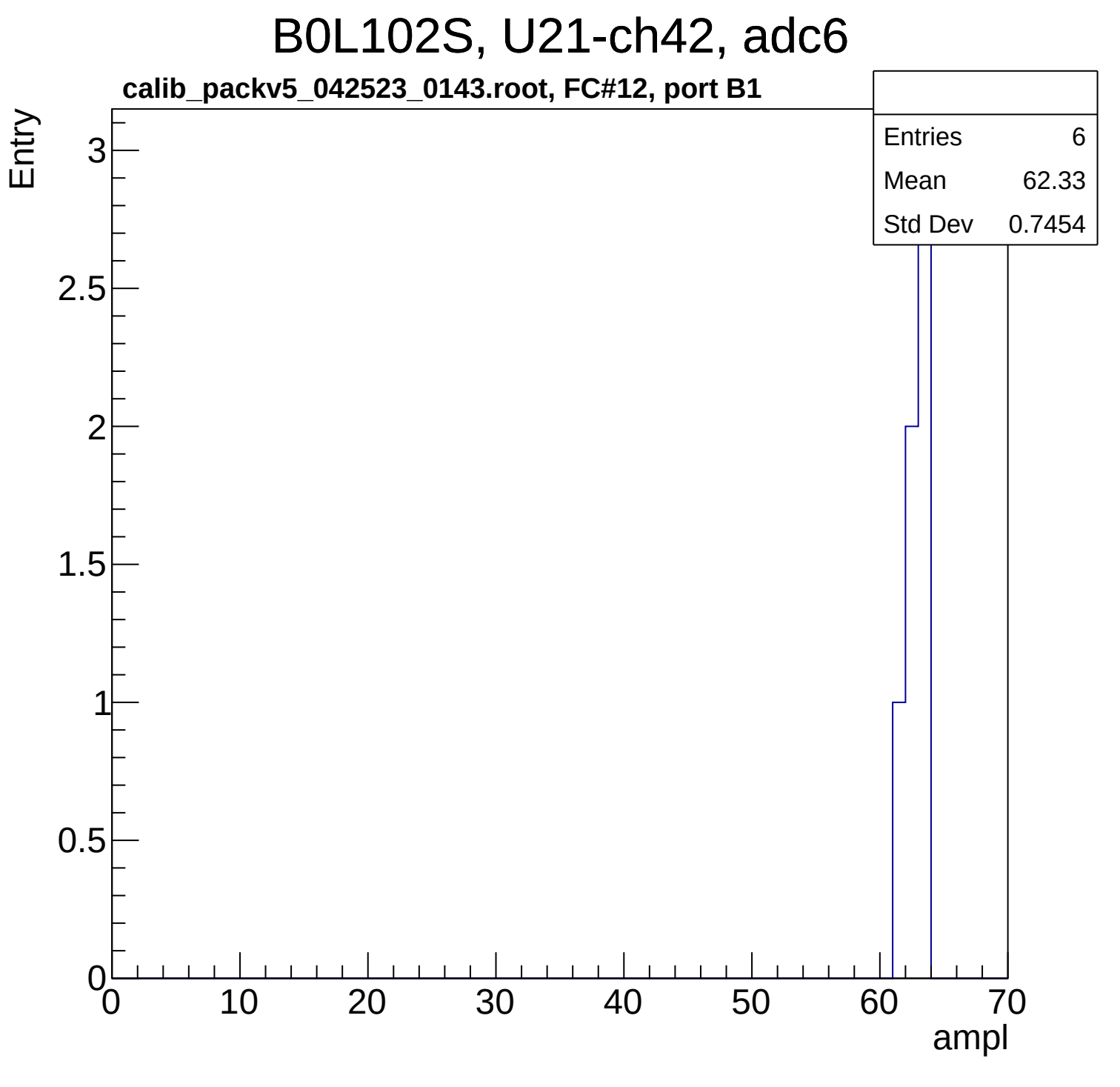
B0L102S, U21-ch42, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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0.5
1
1.5
2
2.5
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0
10
20
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40
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60
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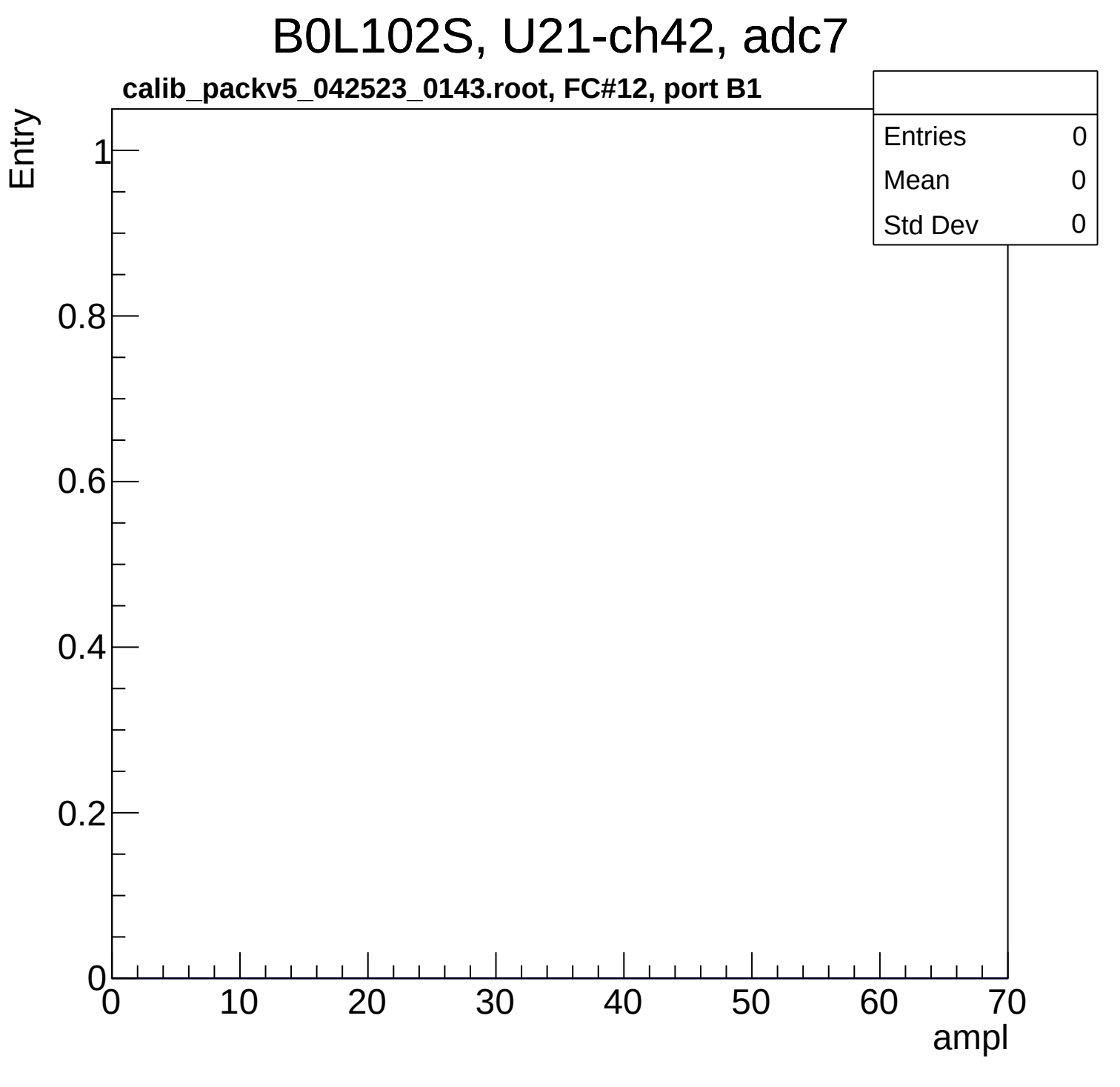
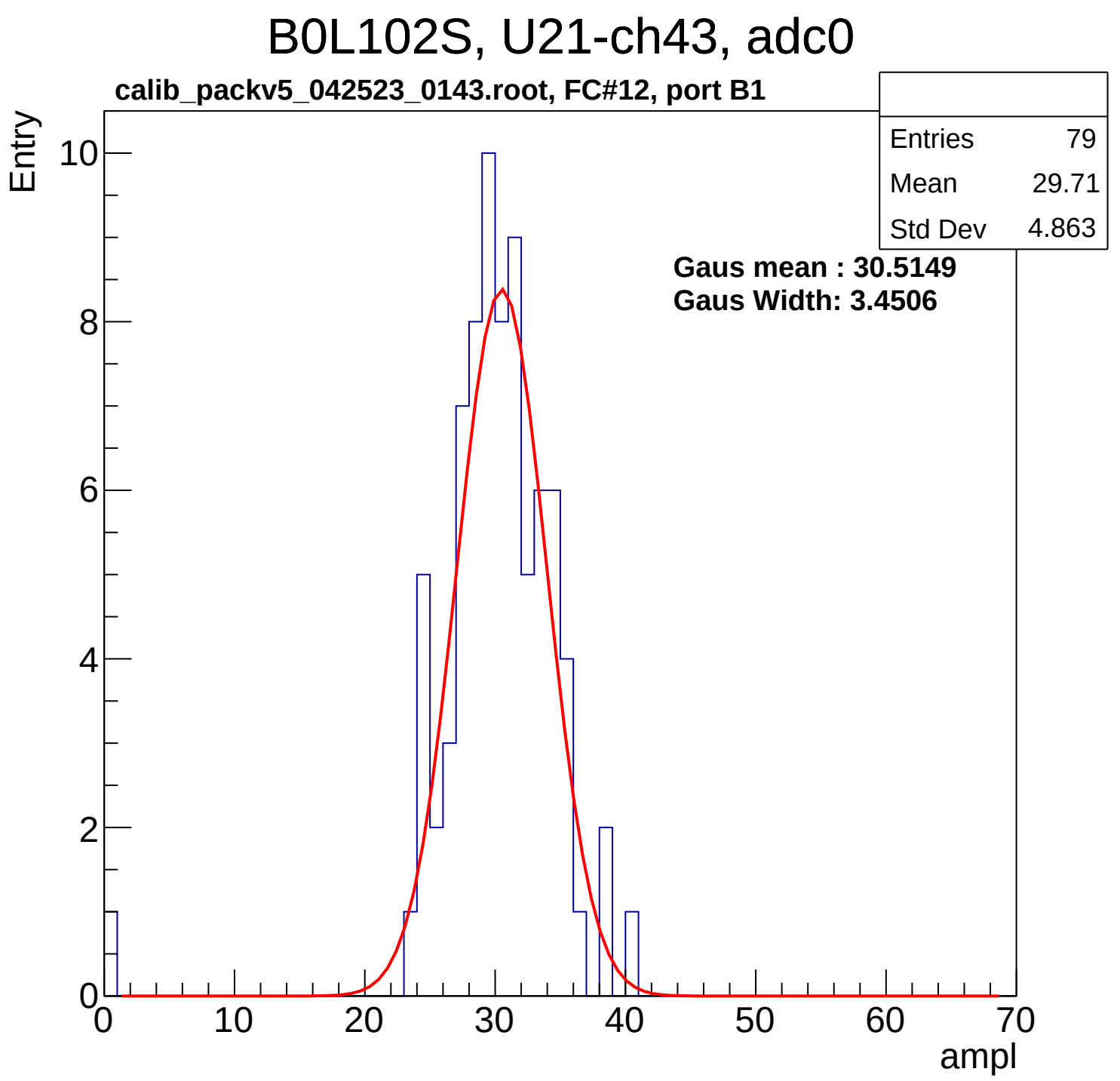

B0L102S, U21-ch42, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch43, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 29.71
Std Dev 4.863
Gaus mean : 30.5149
Gaus Width: 3.4506
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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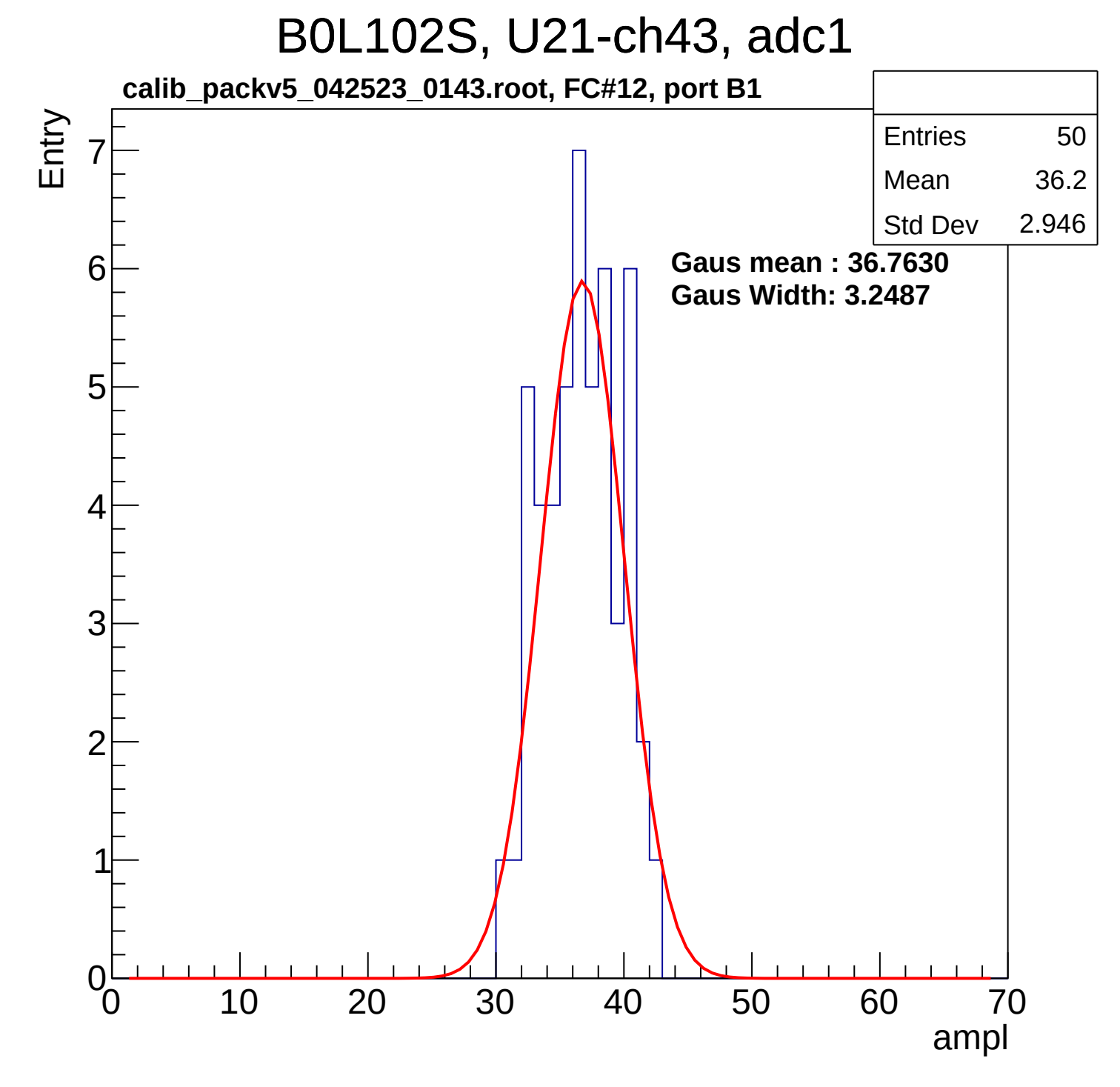
B0L102S, U21-ch43, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 36.2
Std Dev 2.946
Gaus mean : 36.7630
Gaus Width: 3.2487
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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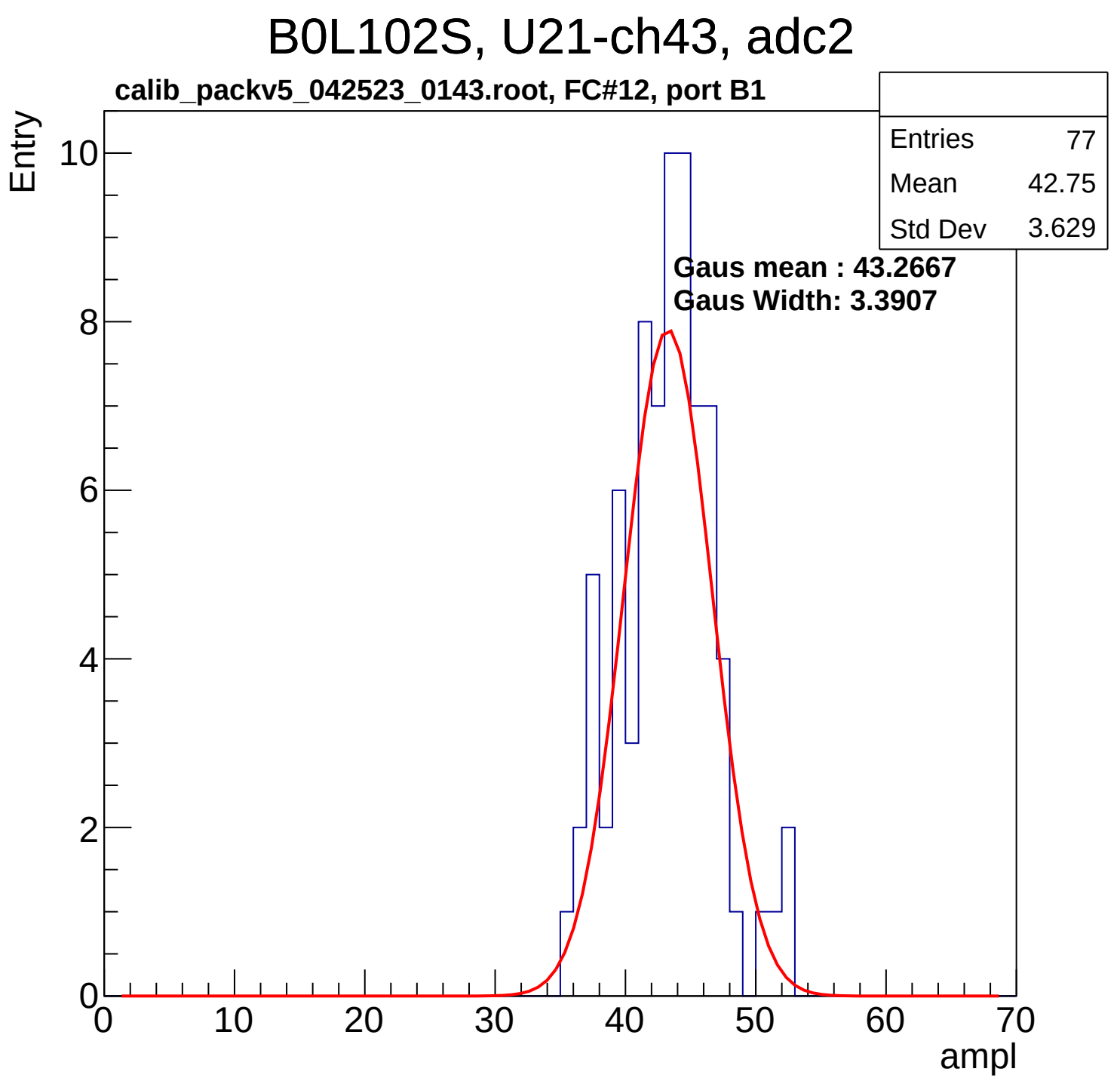
B0L102S, U21-ch43, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 42.75
Std Dev 3.629
Gaus mean : 43.2667
Gaus Width: 3.3907
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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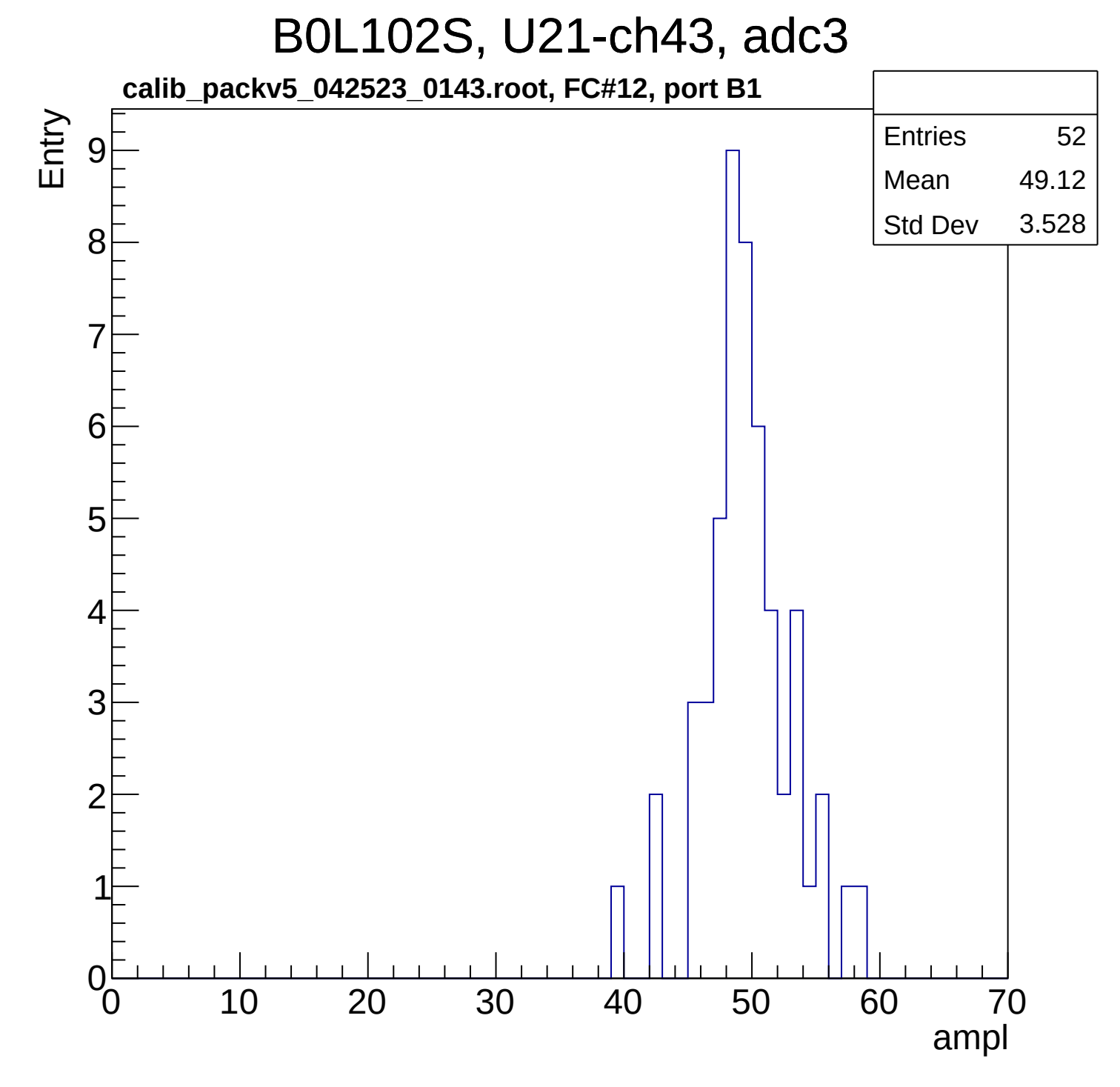
B0L102S, U21-ch43, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 49.12
Std Dev 3.528
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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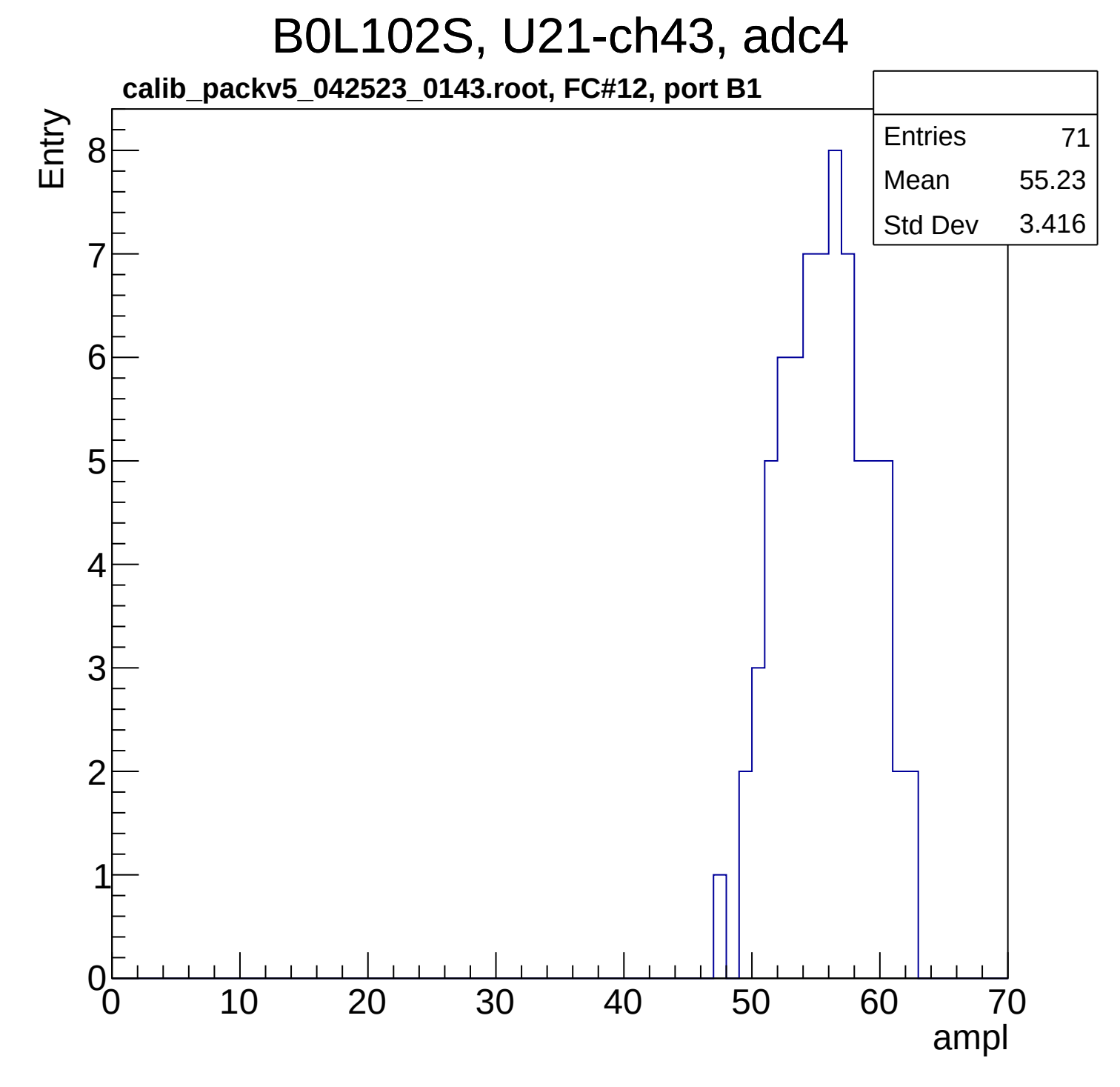
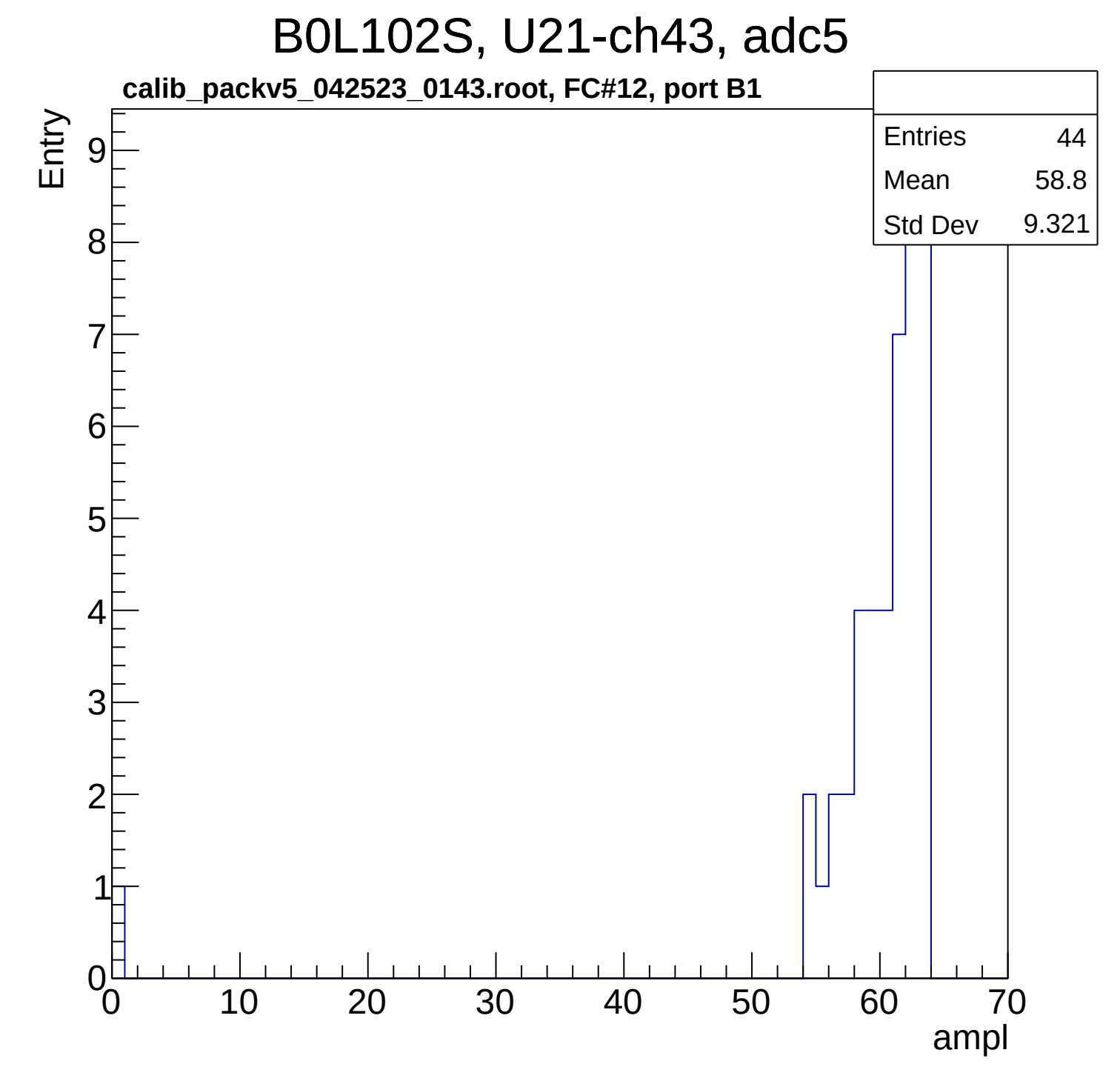

B0L102S, U21-ch43, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 55.23
Std Dev 3.416
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch43, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.8
Std Dev 9.321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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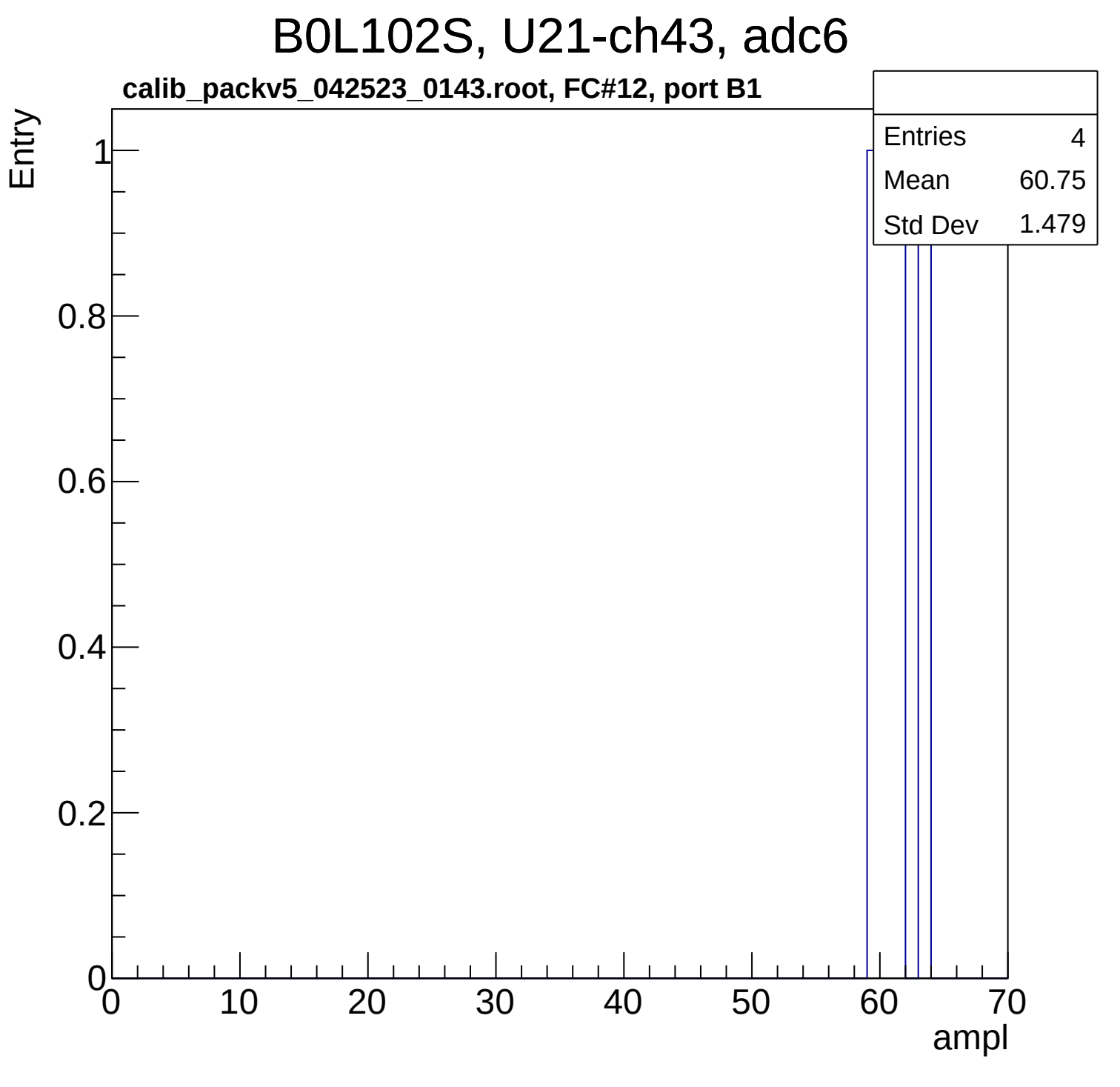
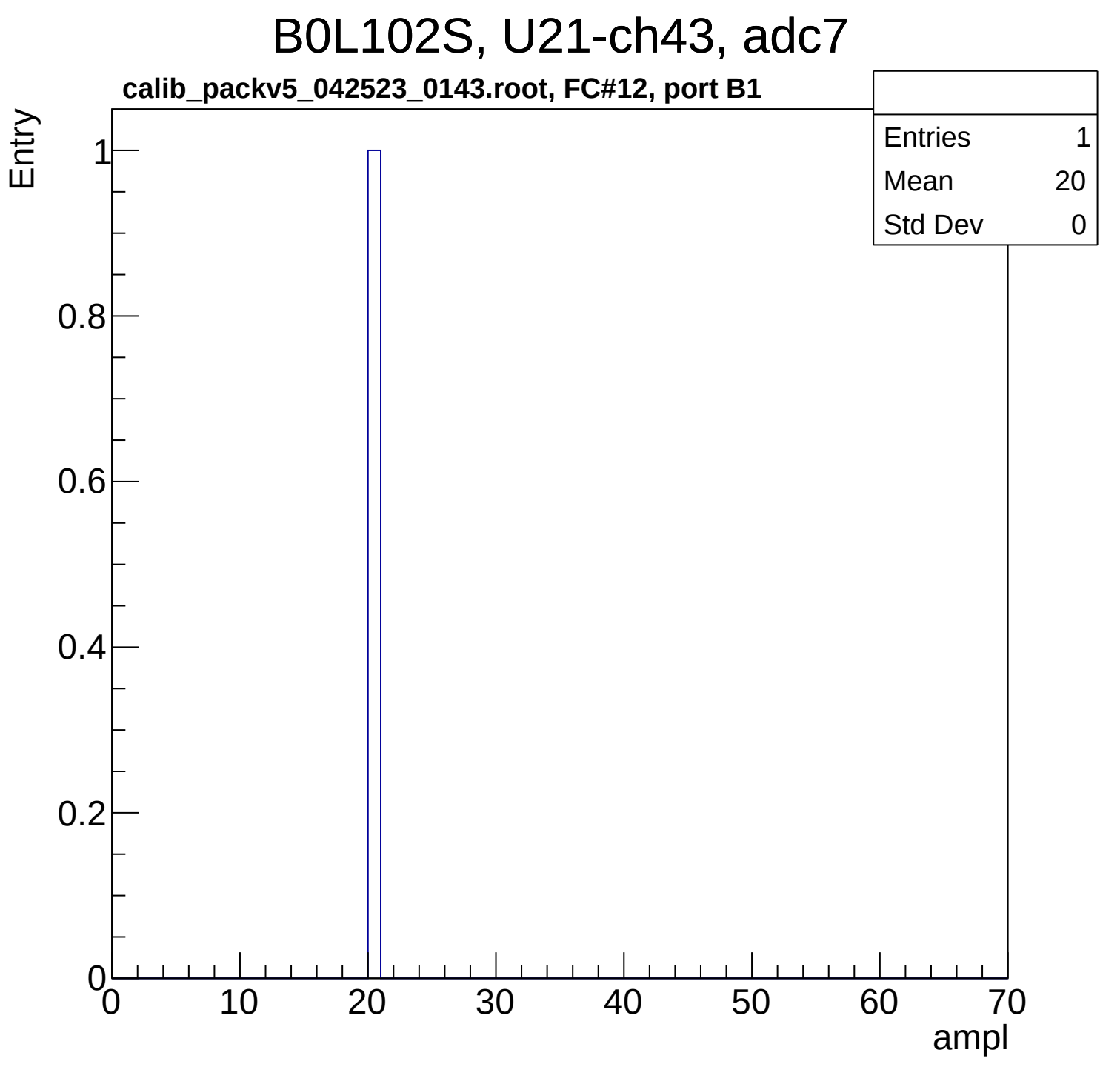
60
70


B0L102S, U21-ch43, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 60.75
Std Dev 1.479
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch43, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
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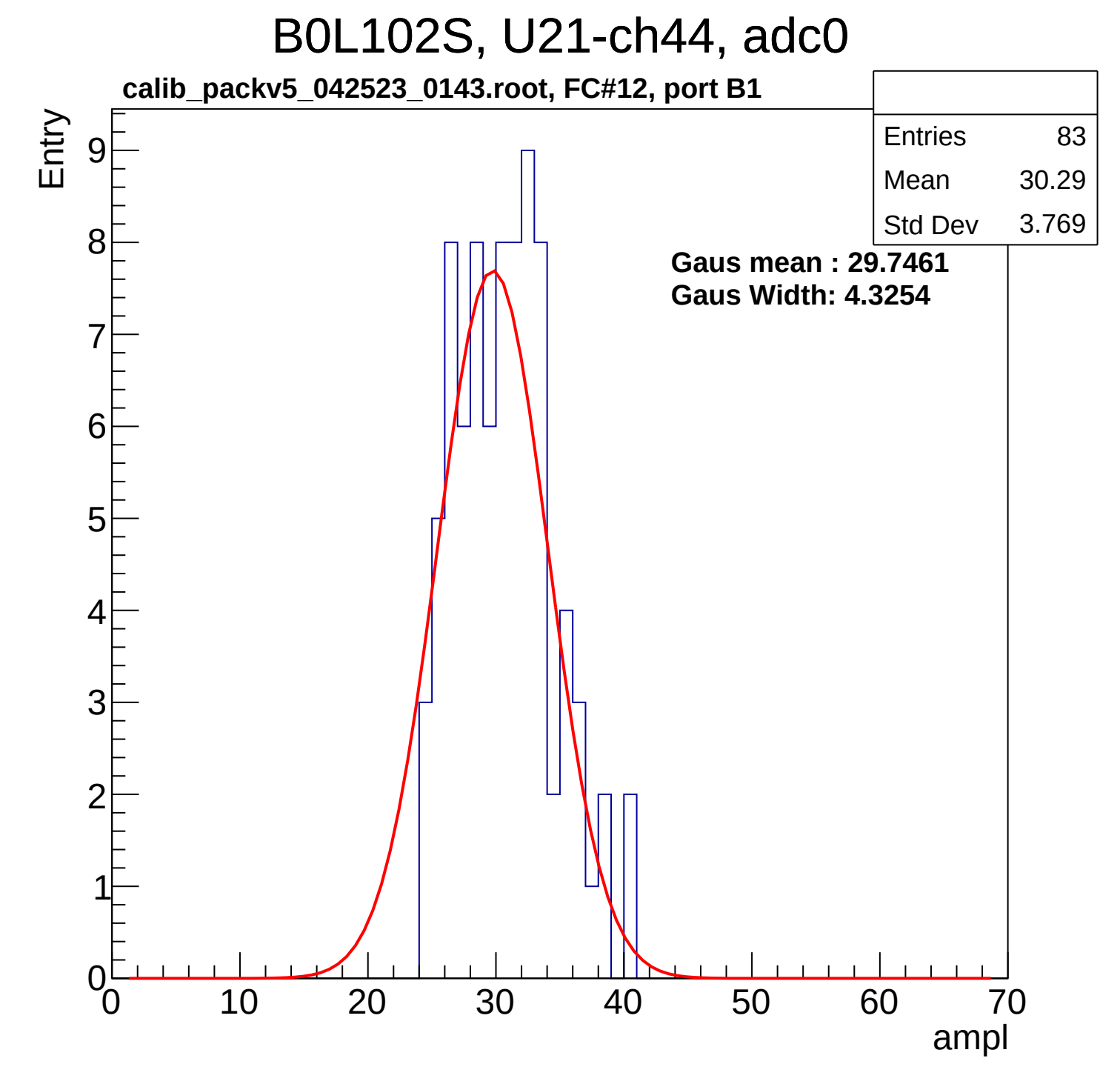
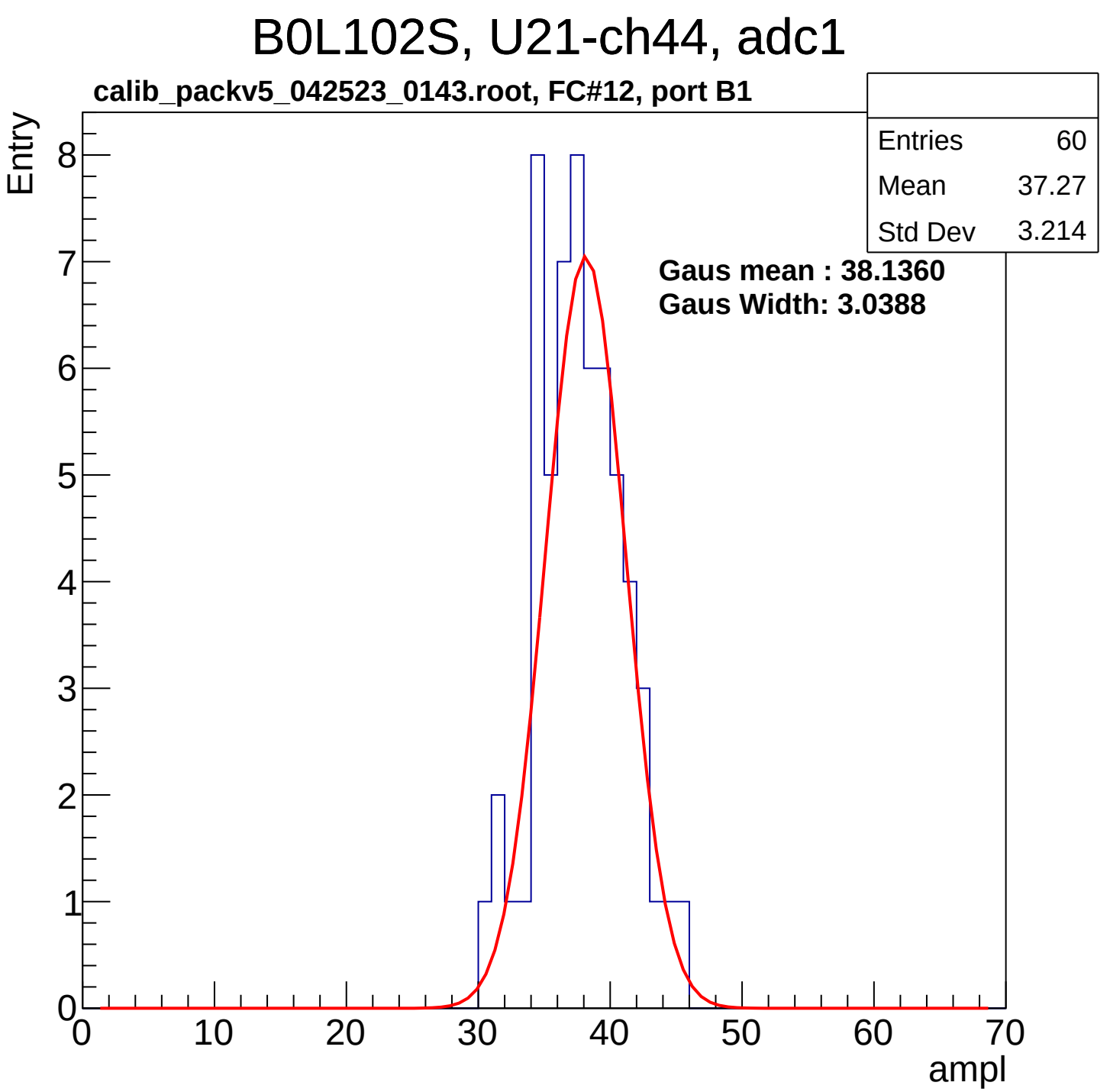
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch44, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 30.29
Std Dev 3.769
Gaus mean : 29.7461
Gaus Width: 4.3254
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch44, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 37.27
Std Dev 3.214
Gaus mean : 38.1360
Gaus Width: 3.0388
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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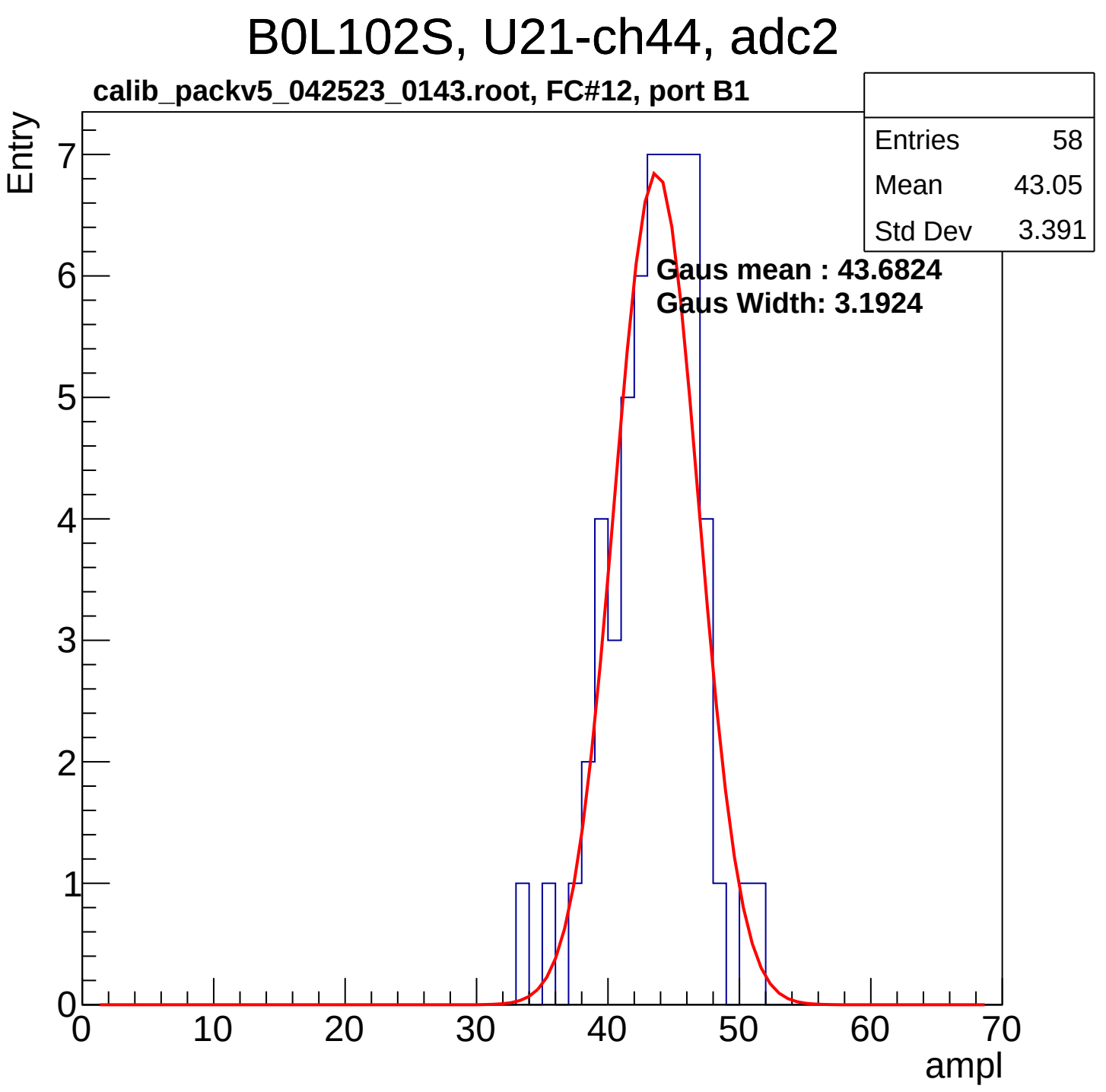
40
50
60
70


B0L102S, U21-ch44, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 43.05
Std Dev 3.391
Gaus mean : 43.6824
Gaus Width: 3.1924
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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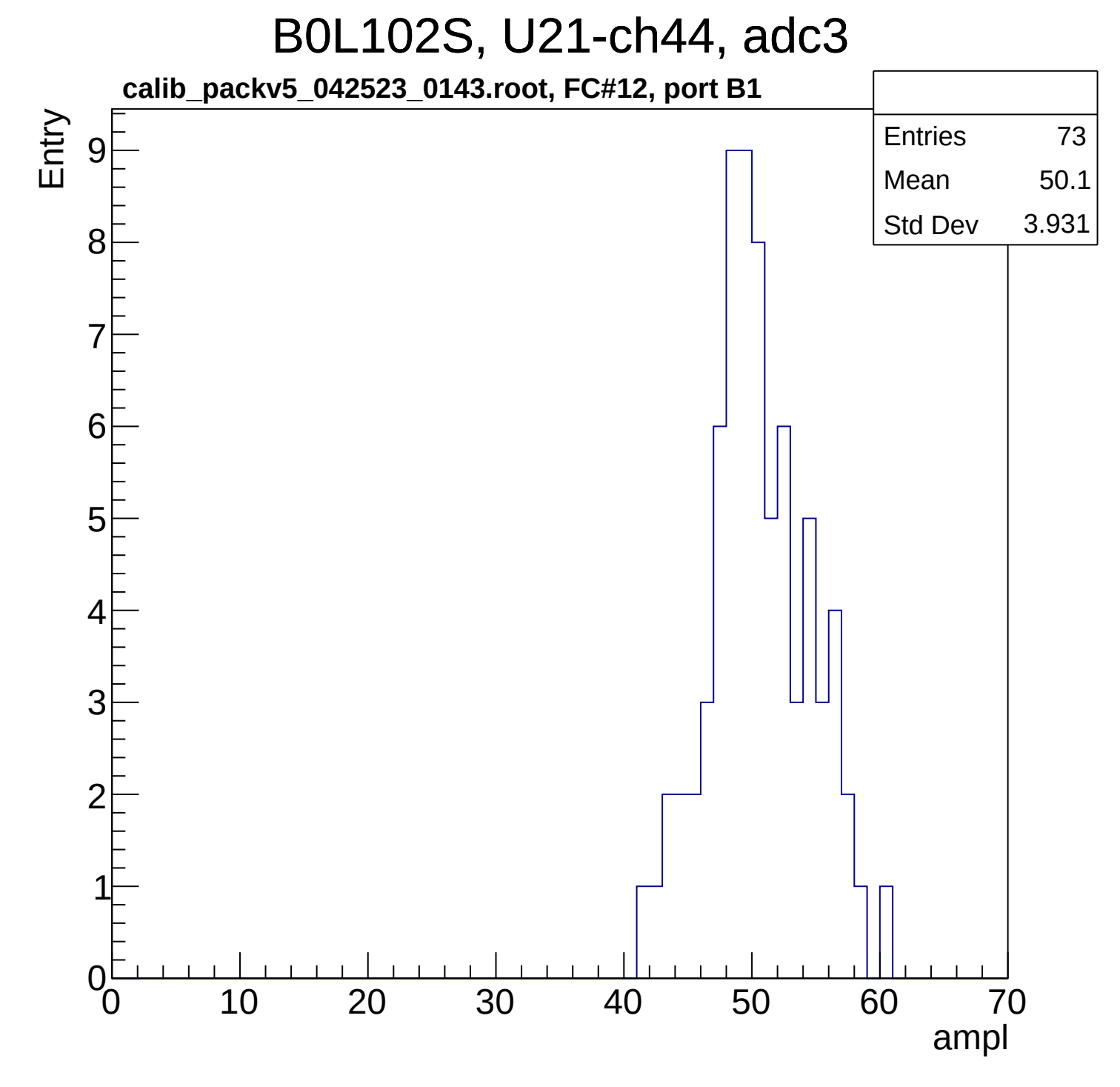
B0L102S, U21-ch44, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 50.1
Std Dev 3.931
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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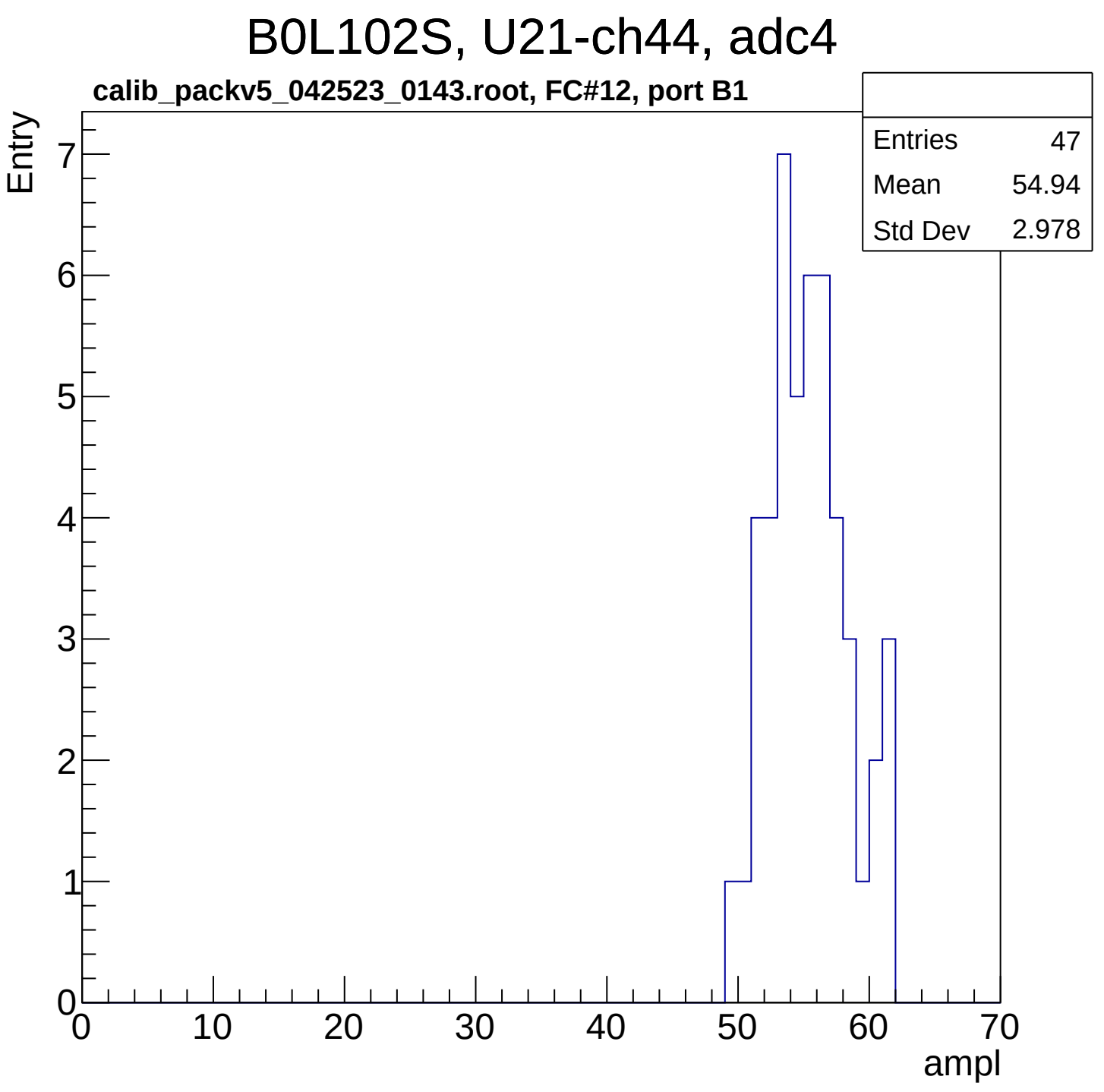
60
70


B0L102S, U21-ch44, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 54.94
Std Dev 2.978
Entry
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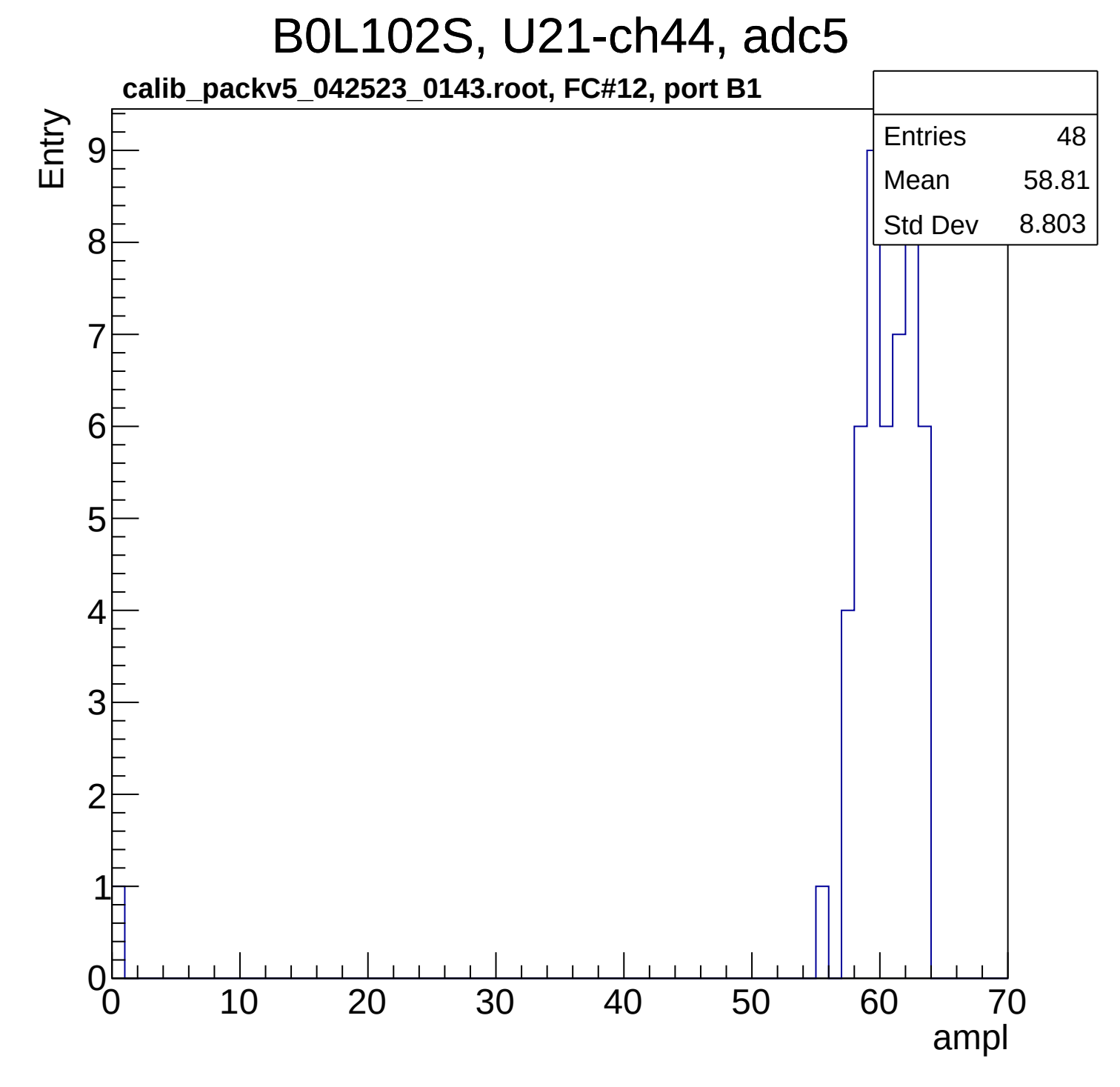
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch44, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.81
Std Dev 8.803
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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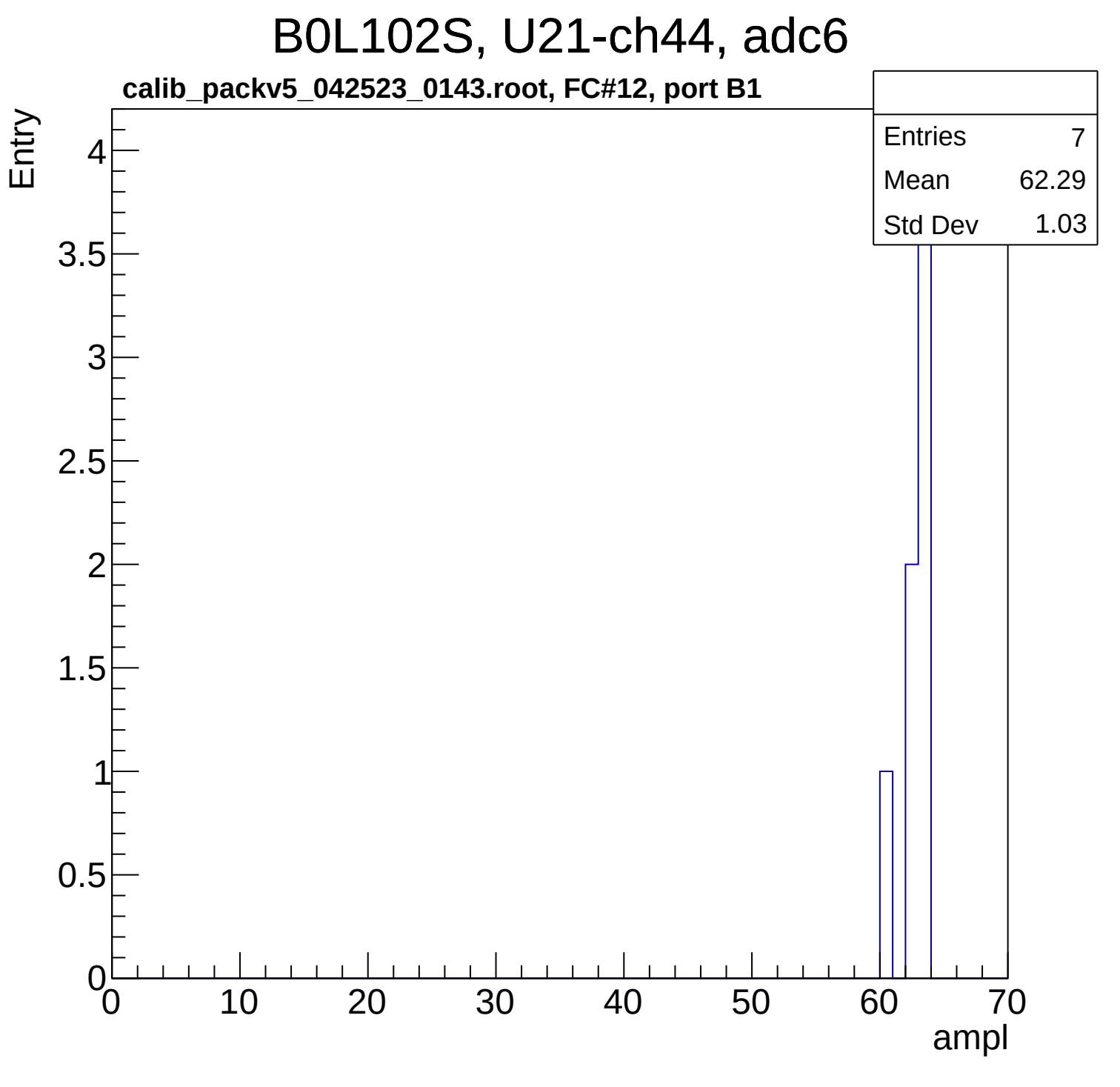
B0L102S, U21-ch44, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.29
Std Dev 1.03
Entry
ampl
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1.5
2
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3.5
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0
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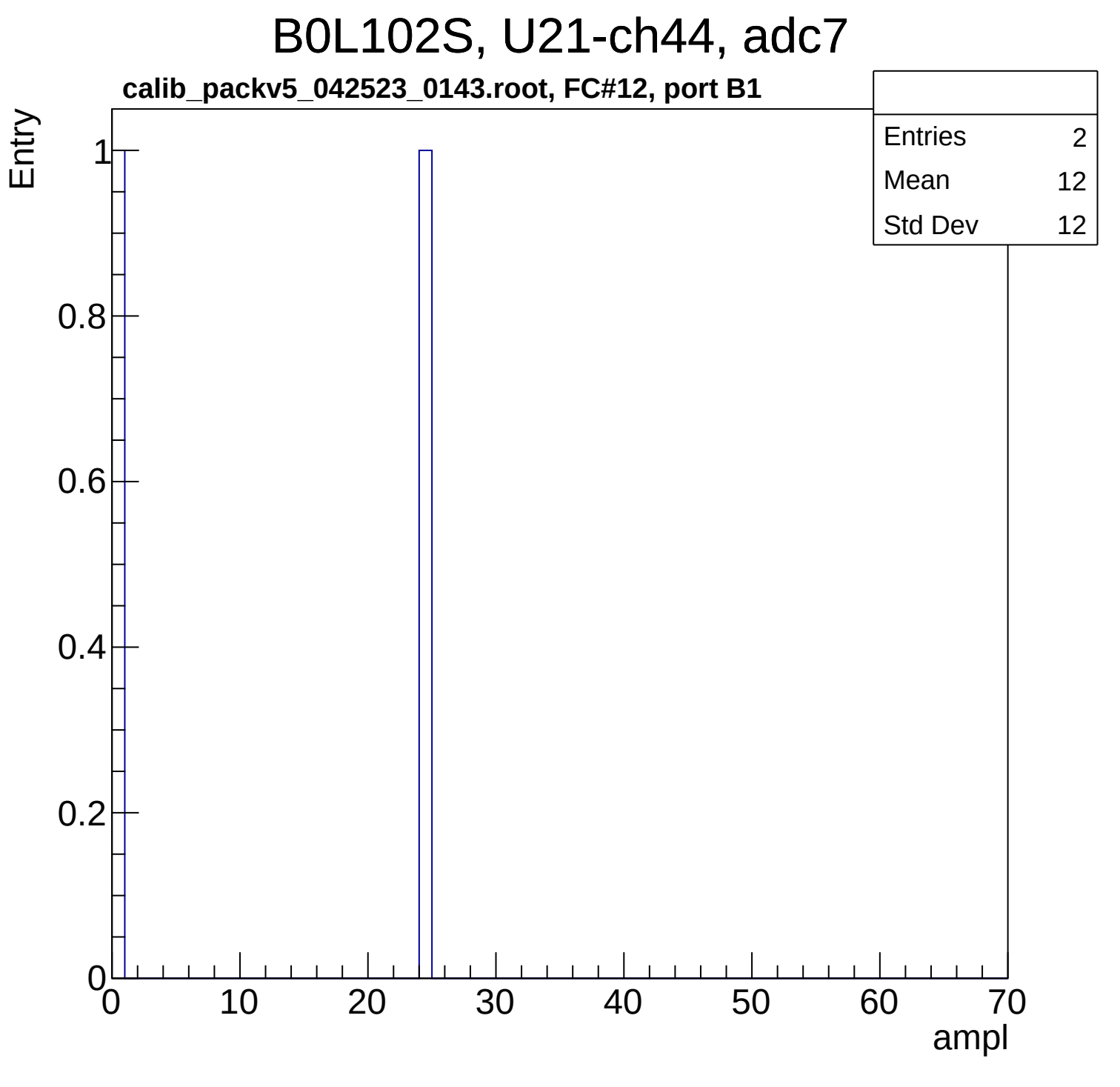
60
70


B0L102S, U21-ch44, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
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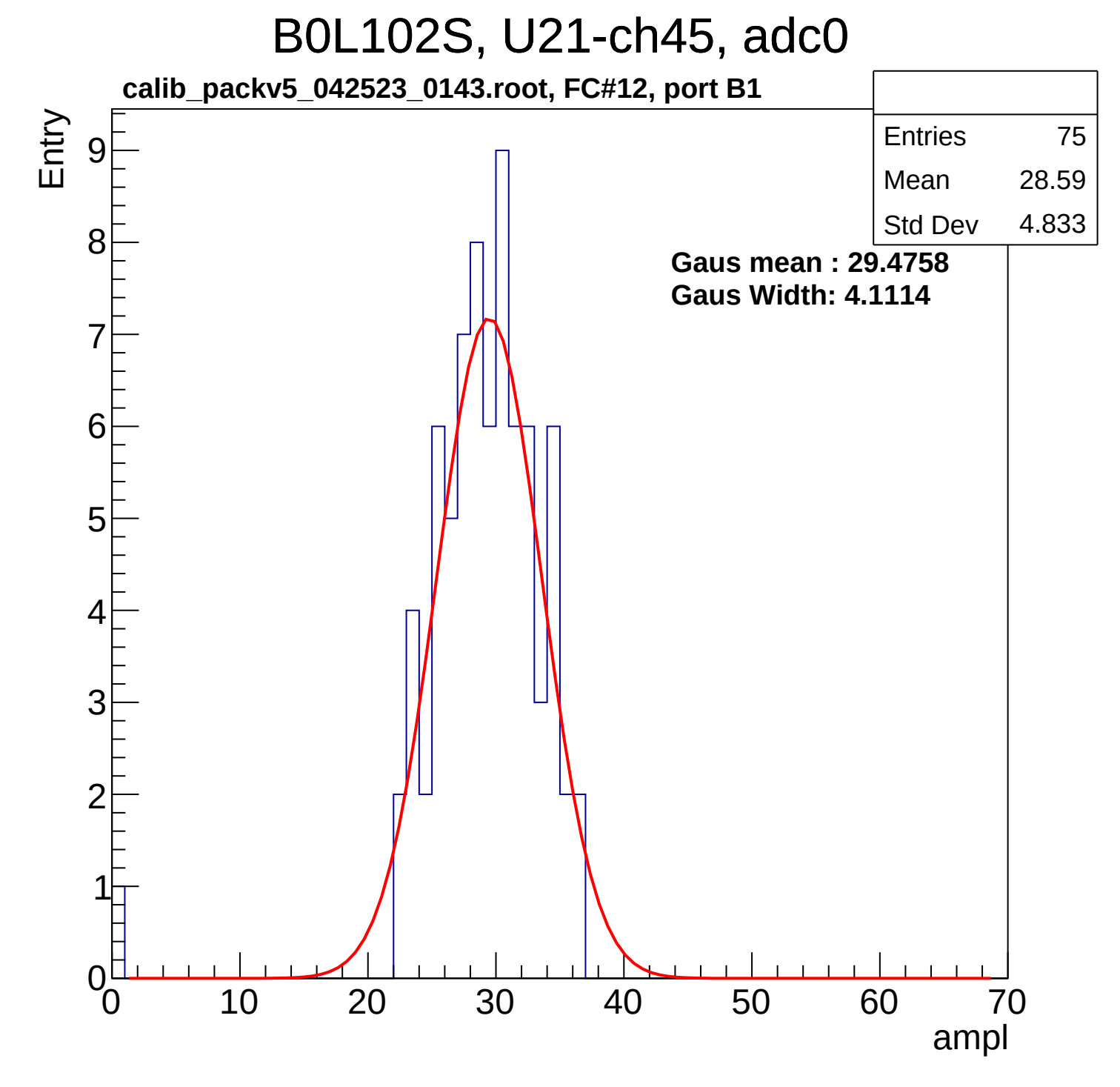
50
60
70
ampl


B0L102S, U21-ch45, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 28.59
Std Dev 4.833
Gaus mean : 29.4758
Gaus Width: 4.1114
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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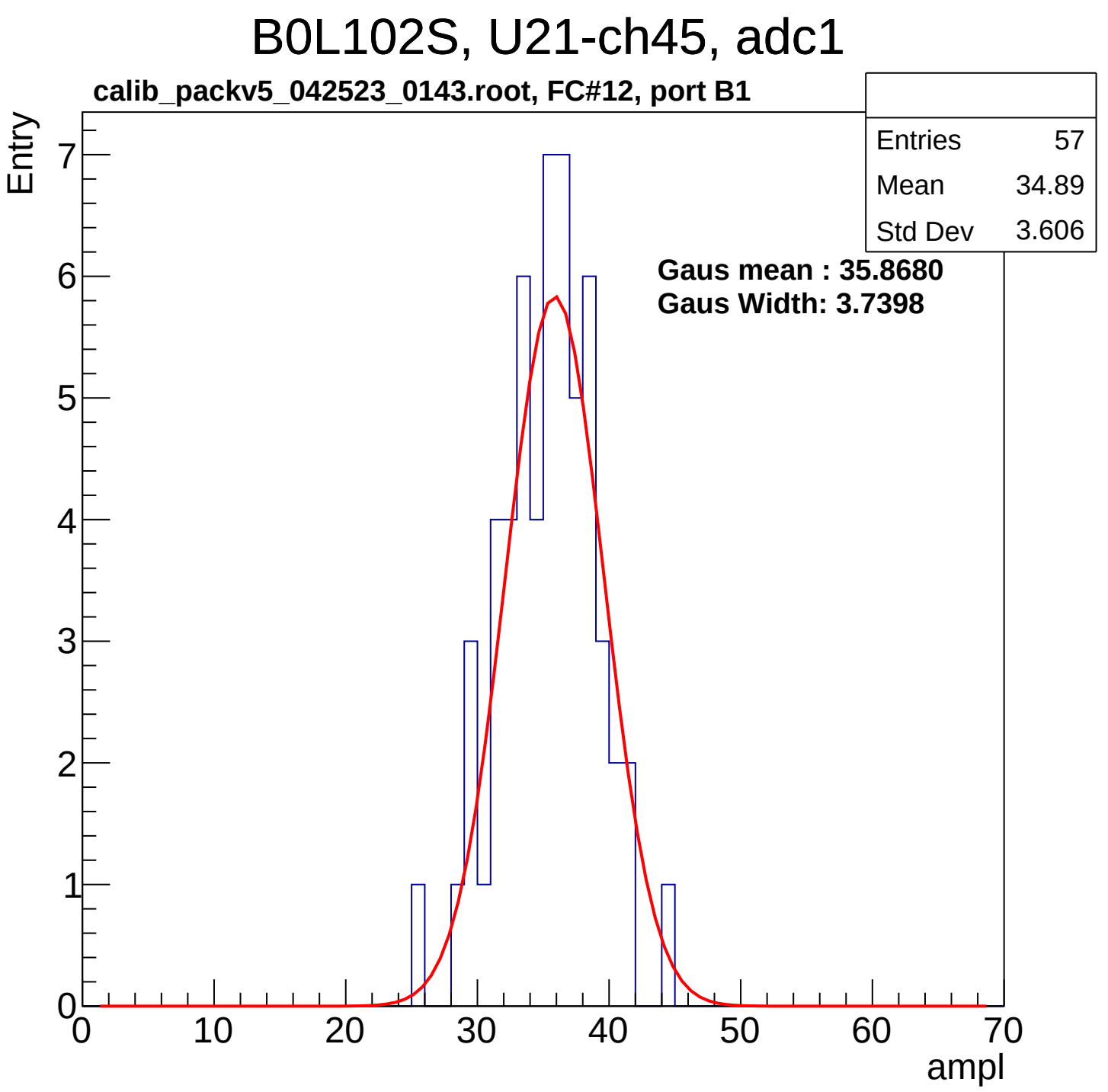
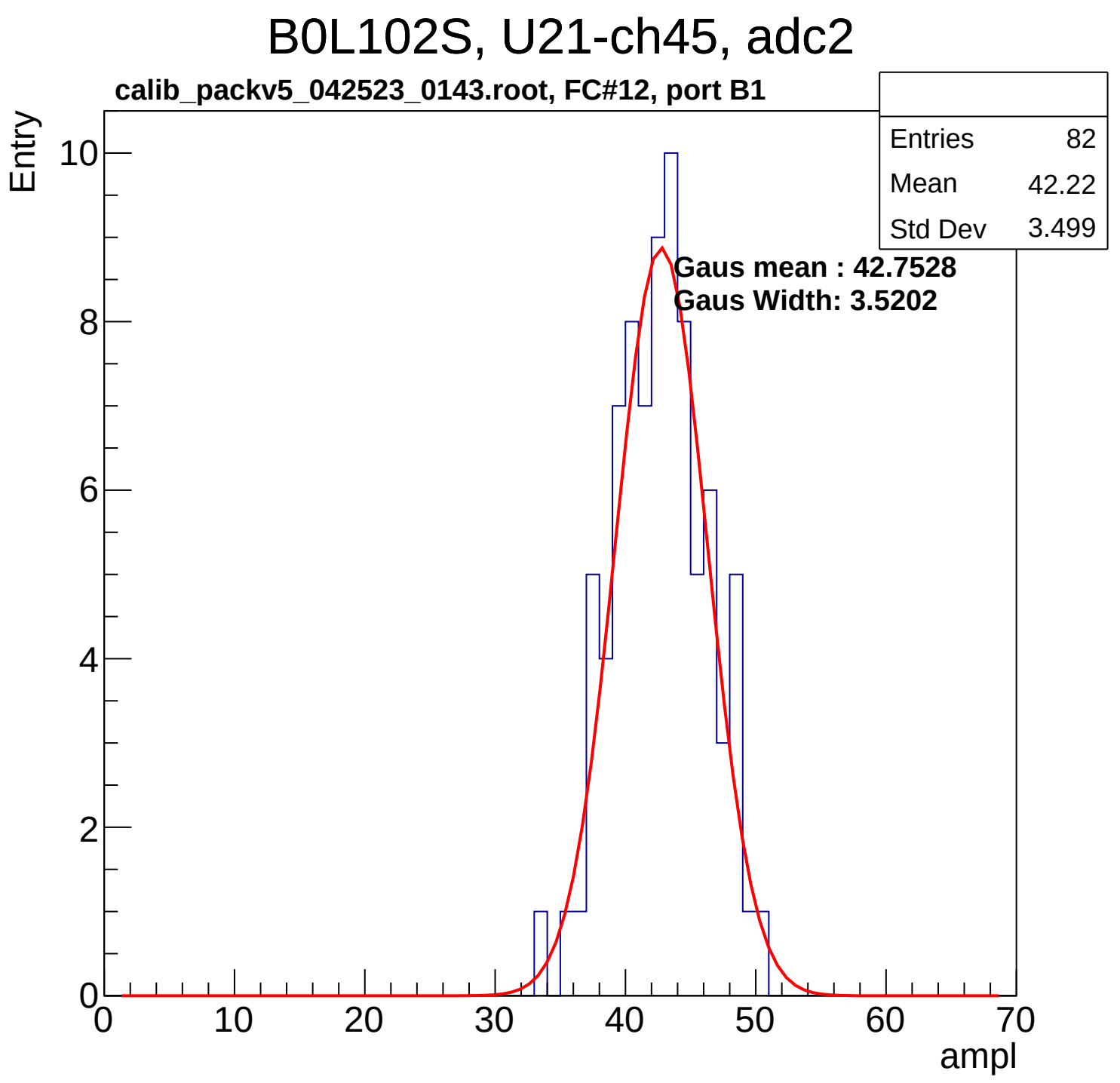

B0L102S, U21-ch45, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 34.89
Std Dev 3.606
Gaus mean : 35.8680
Gaus Width: 3.7398
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch45, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 42.22
Std Dev 3.499
Gaus mean : 42.7528
Gaus Width: 3.5202
Entry
ampl
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2
4
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8
10
0
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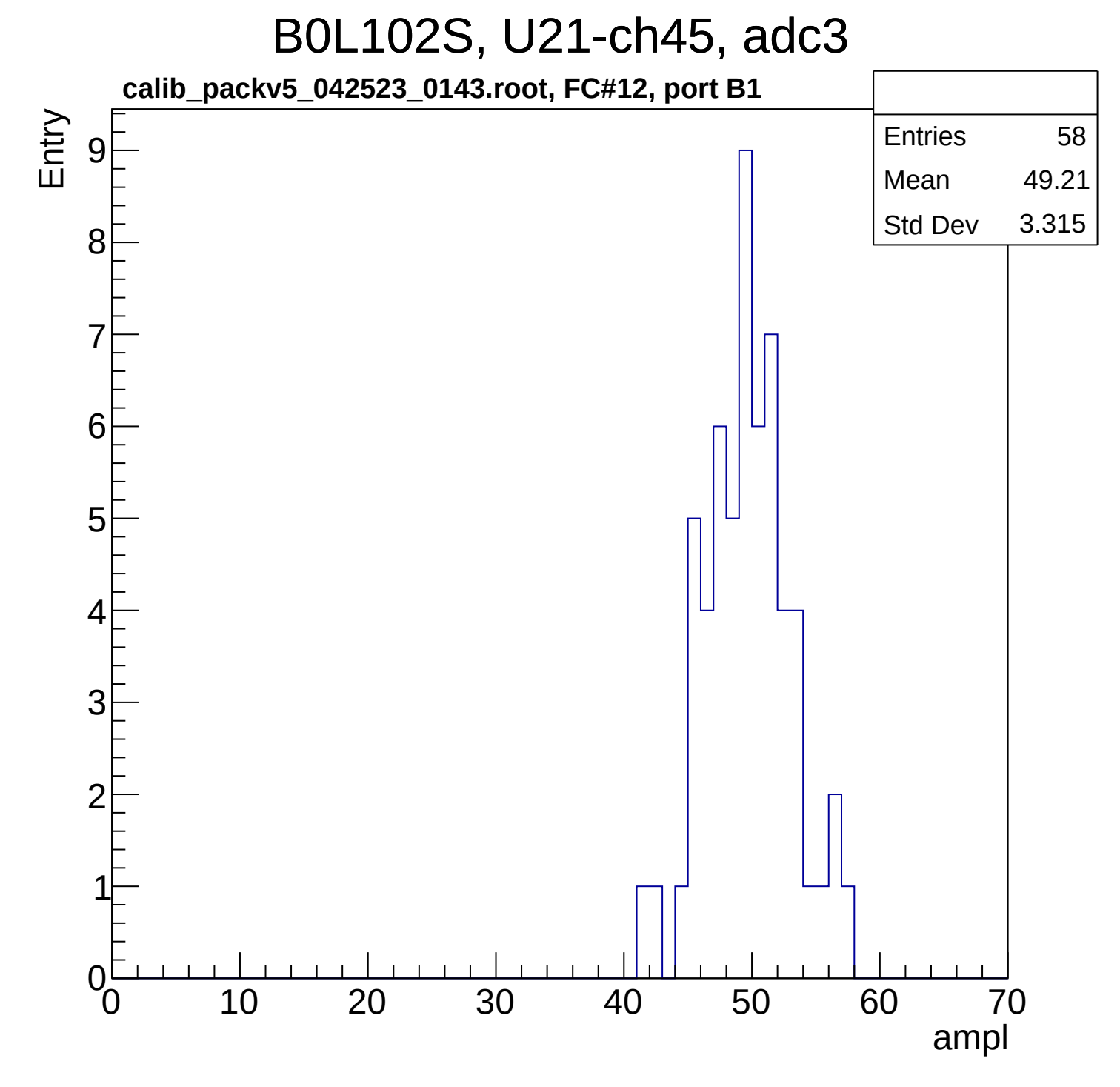
30
40
50
60
70


B0L102S, U21-ch45, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 49.21
Std Dev 3.315
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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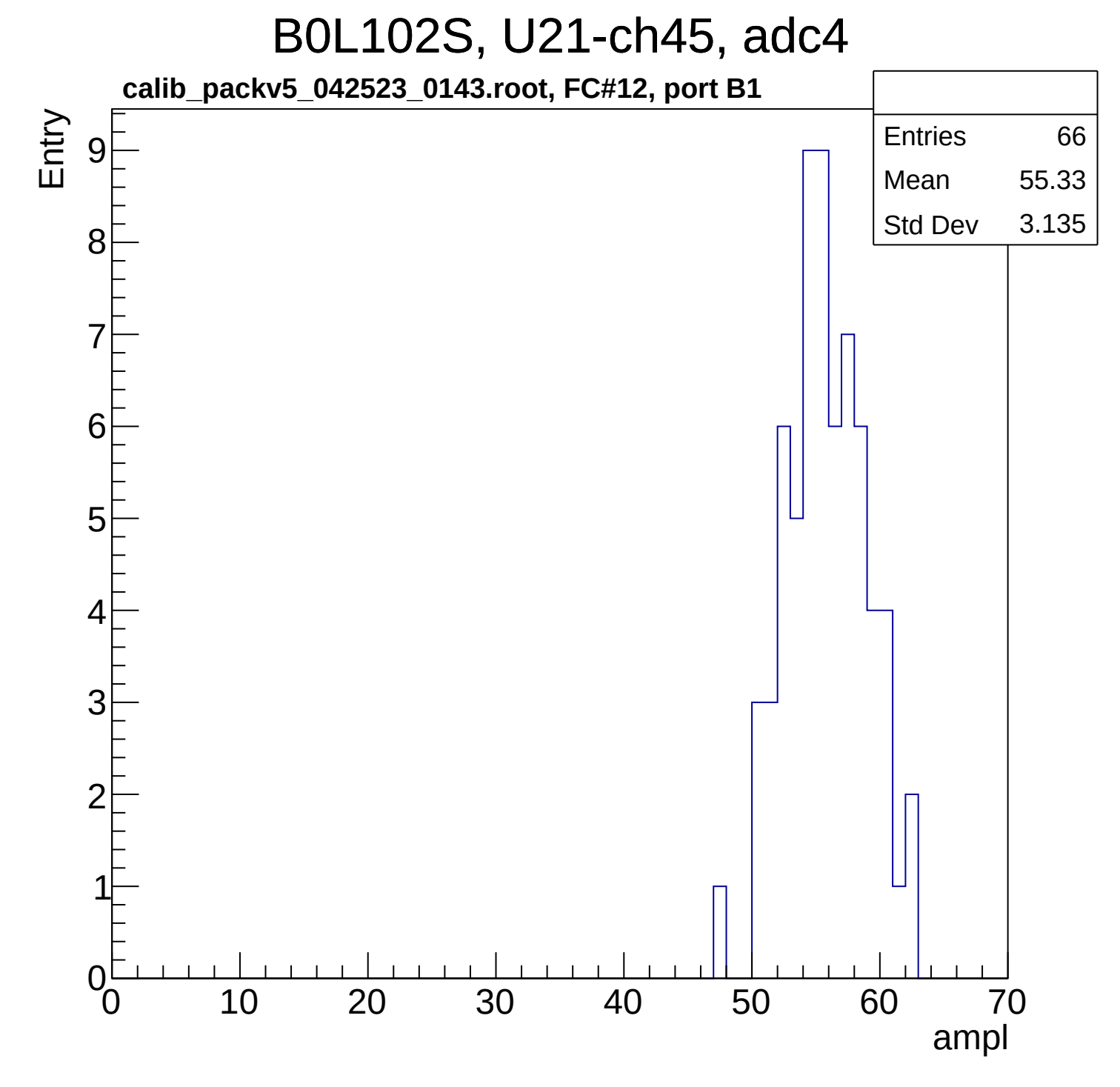
B0L102S, U21-ch45, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 55.33
Std Dev 3.135
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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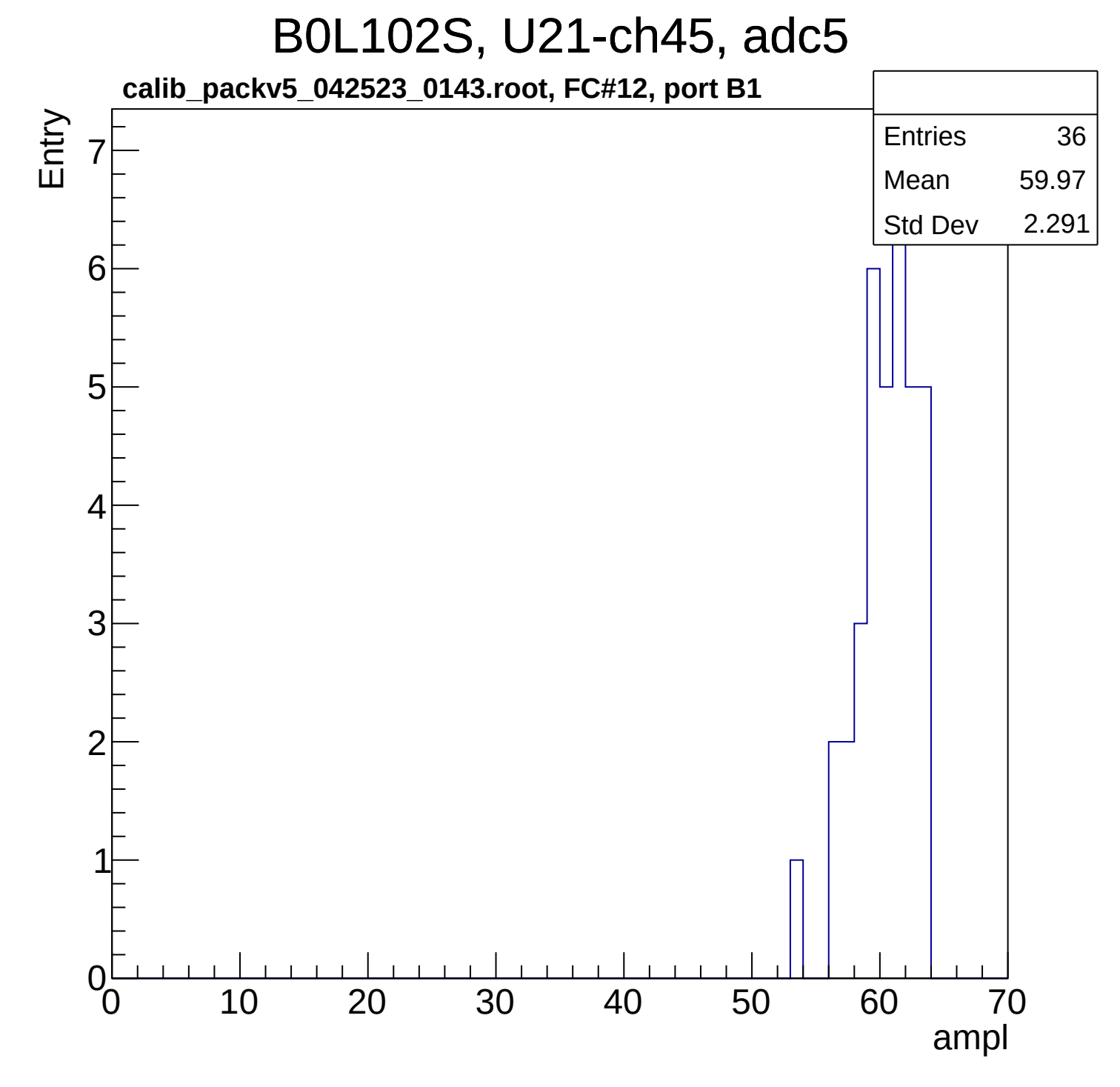
B0L102S, U21-ch45, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 59.97
Std Dev 2.291
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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60
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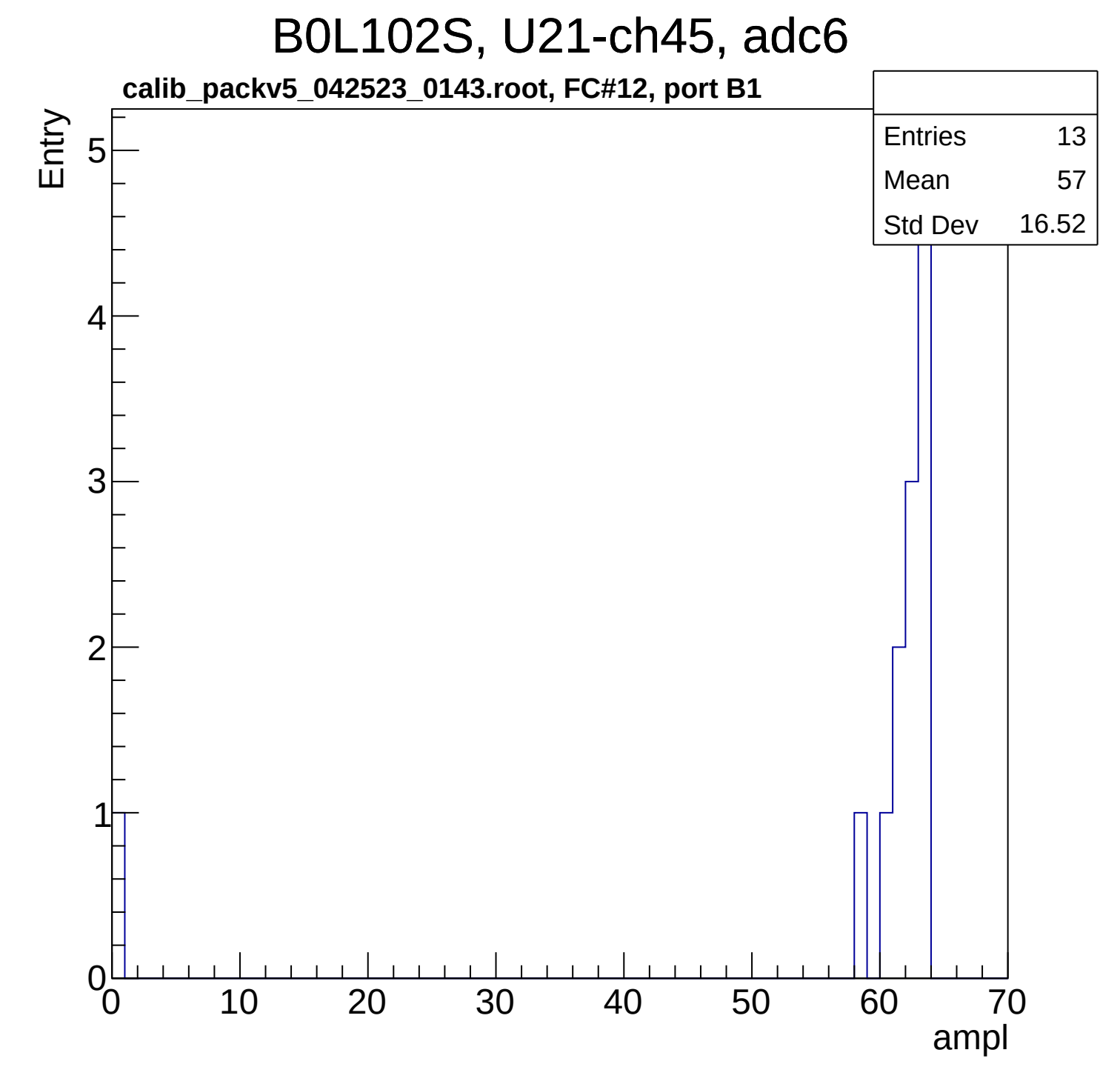
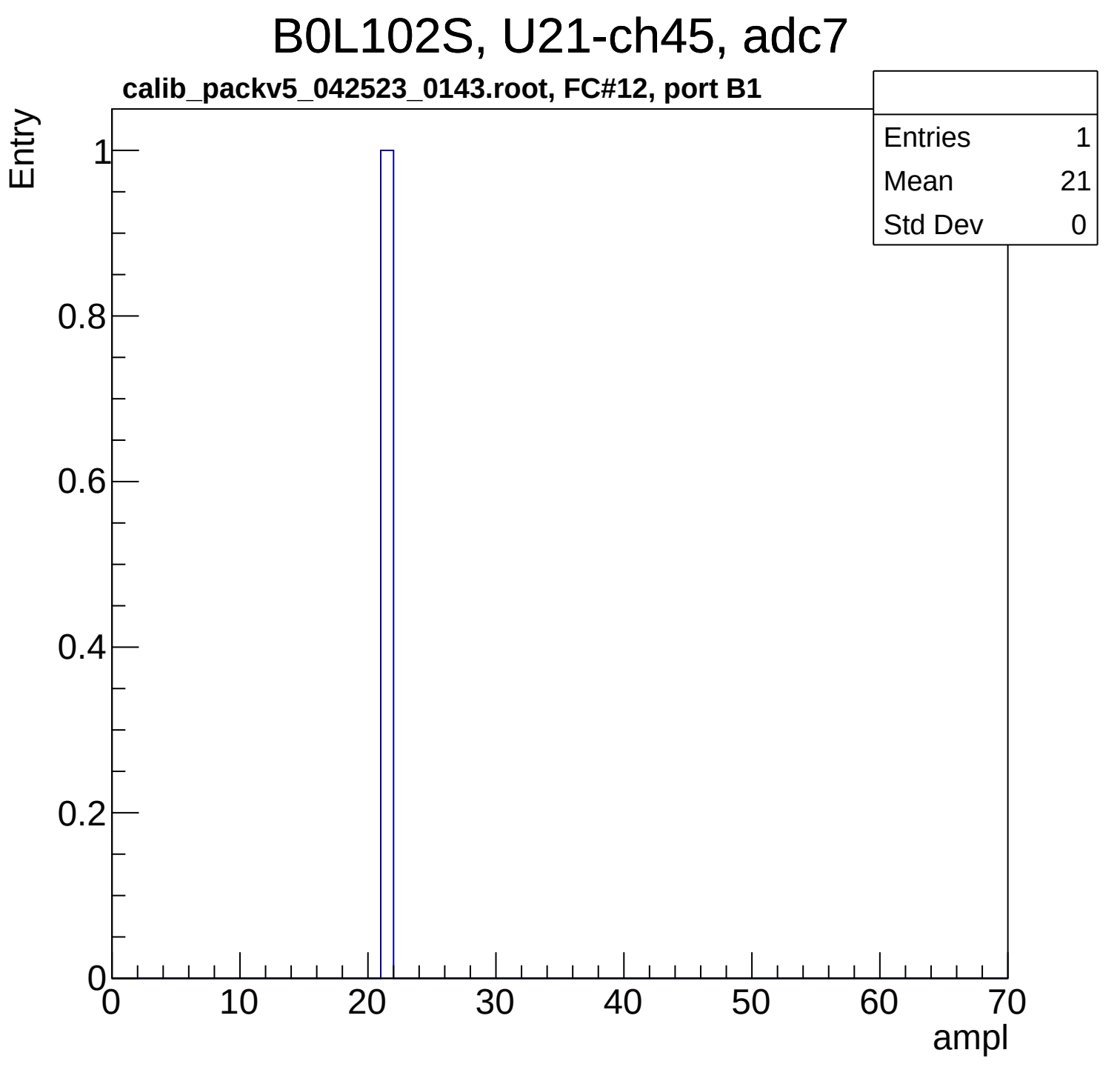

B0L102S, U21-ch45, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 57
Std Dev 16.52
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch45, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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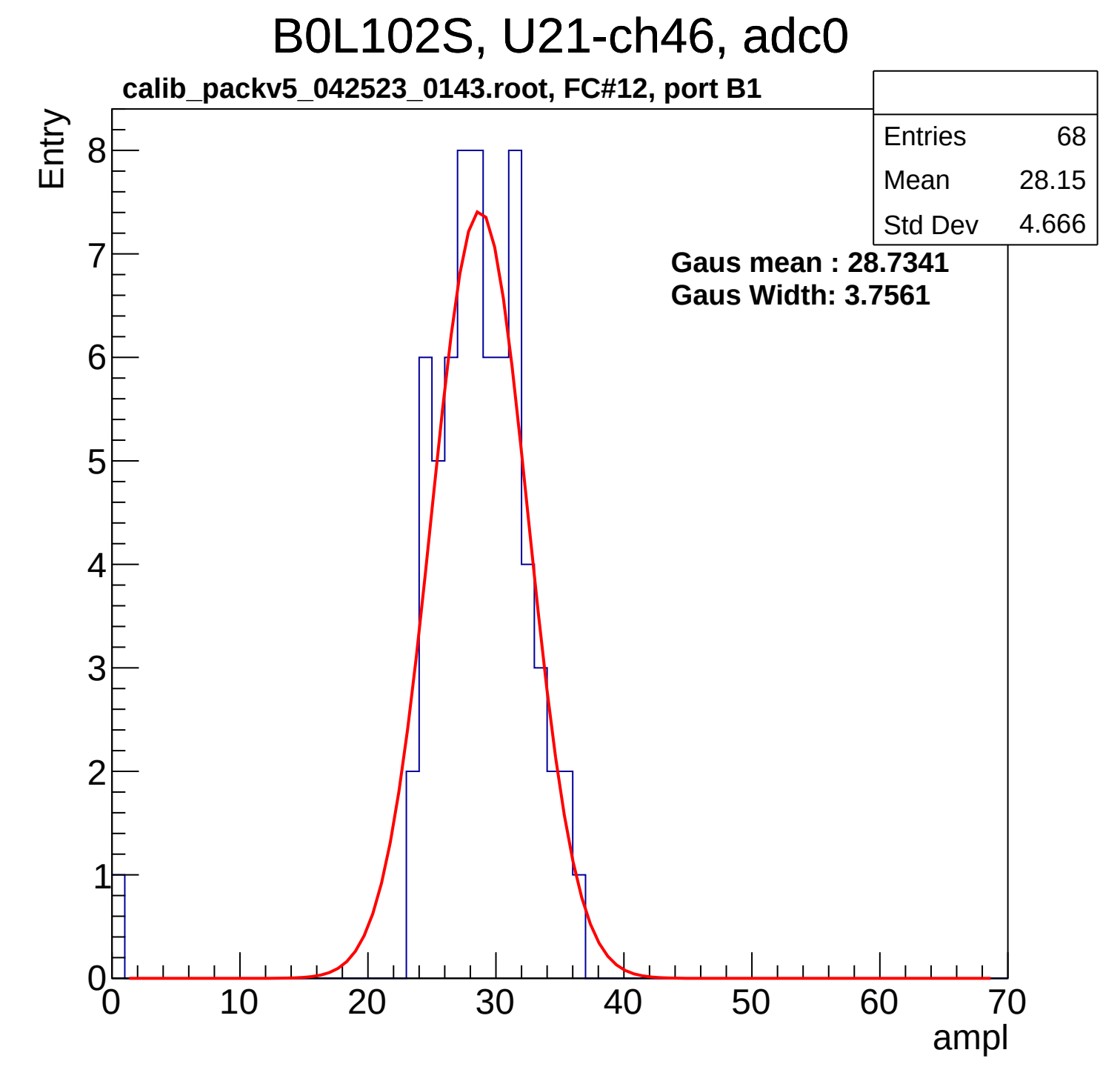
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch46, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 28.15
Std Dev 4.666
Gaus mean : 28.7341
Gaus Width: 3.7561
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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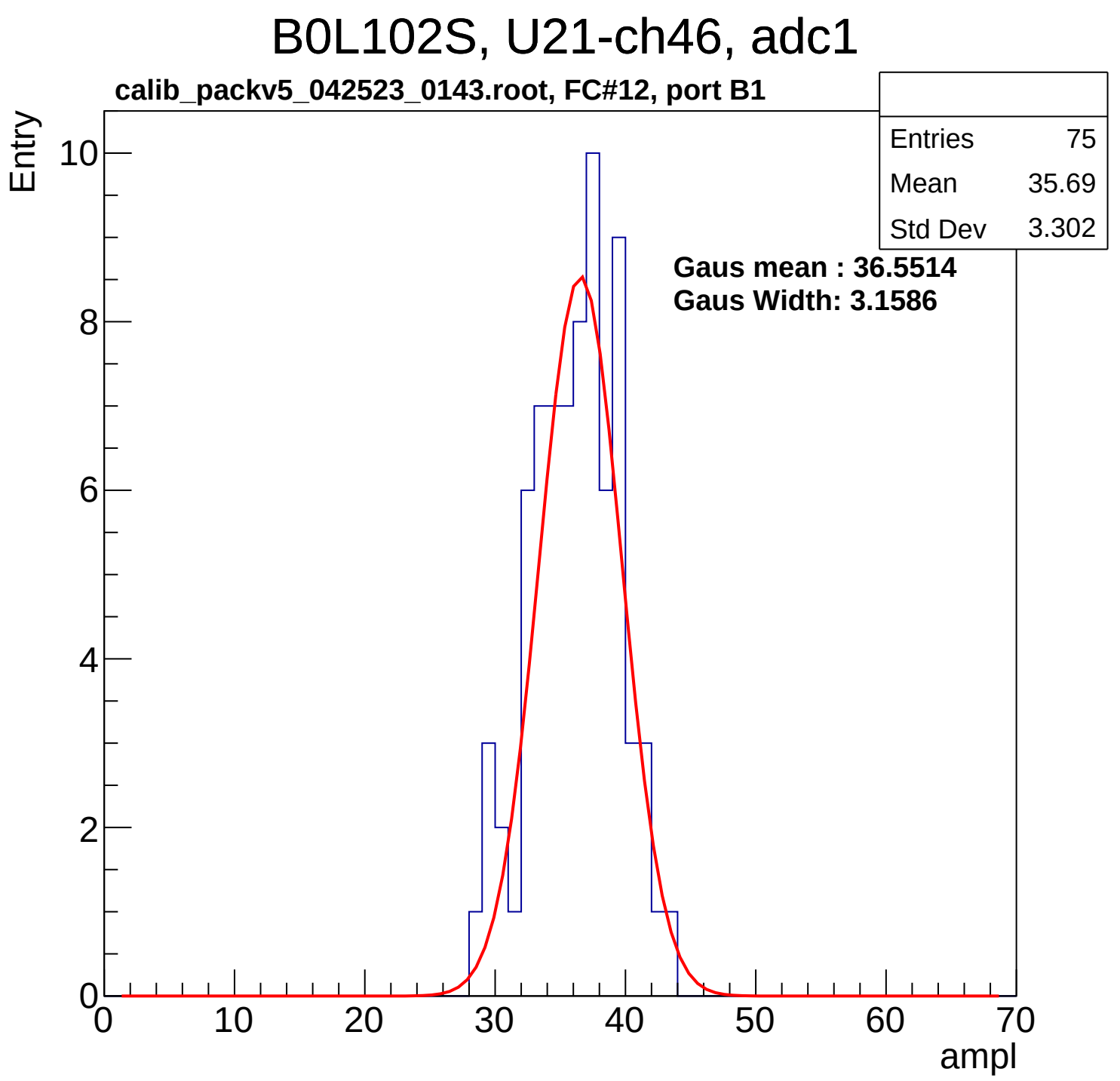
40
50
60
70


B0L102S, U21-ch46, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 35.69
Std Dev 3.302
Gaus mean : 36.5514
Gaus Width: 3.1586
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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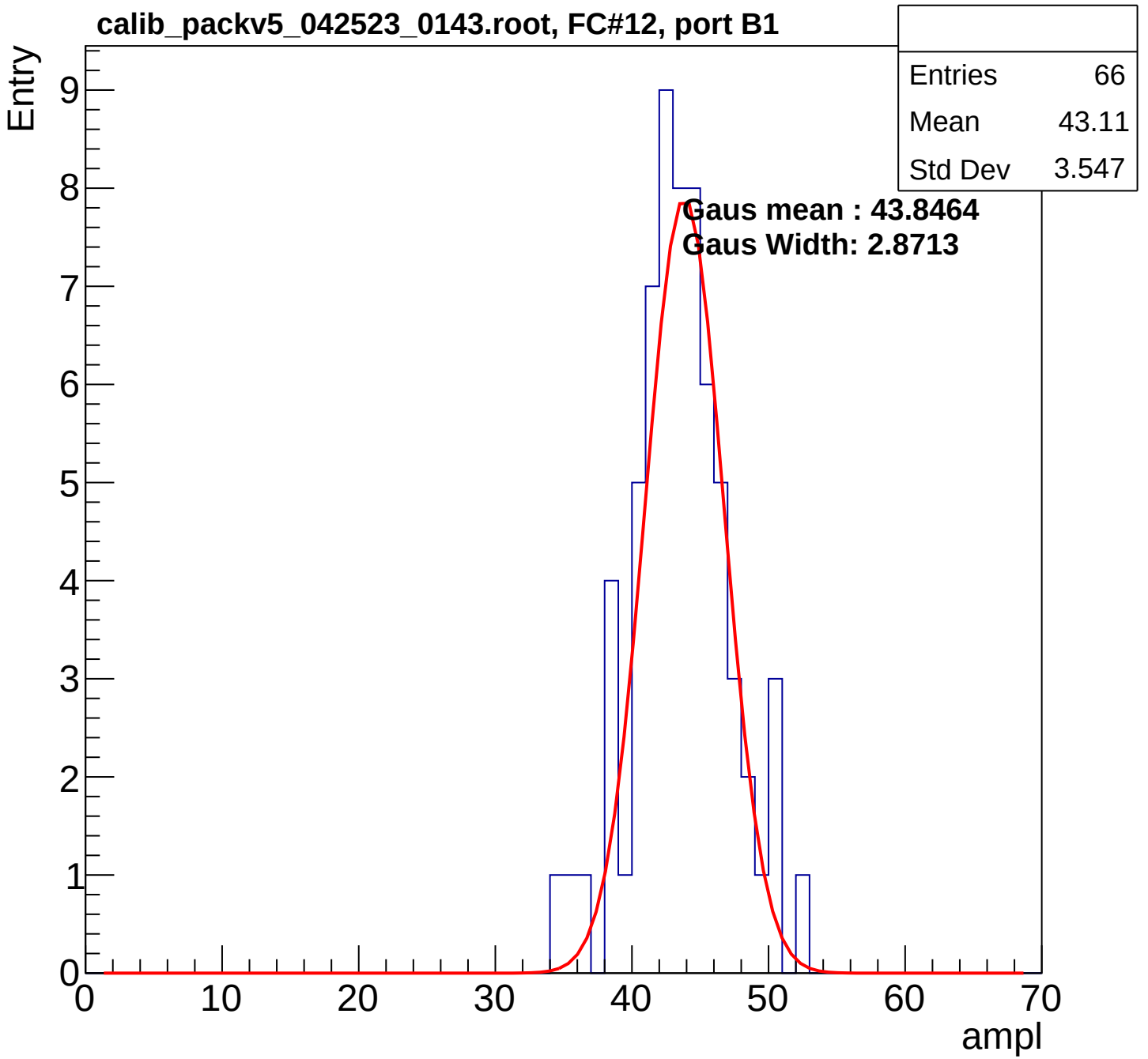
B0L102S, U21-ch46, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.11
Std Dev 3.547
Gaus mean : 43.8464
Gaus Width: 2.8713
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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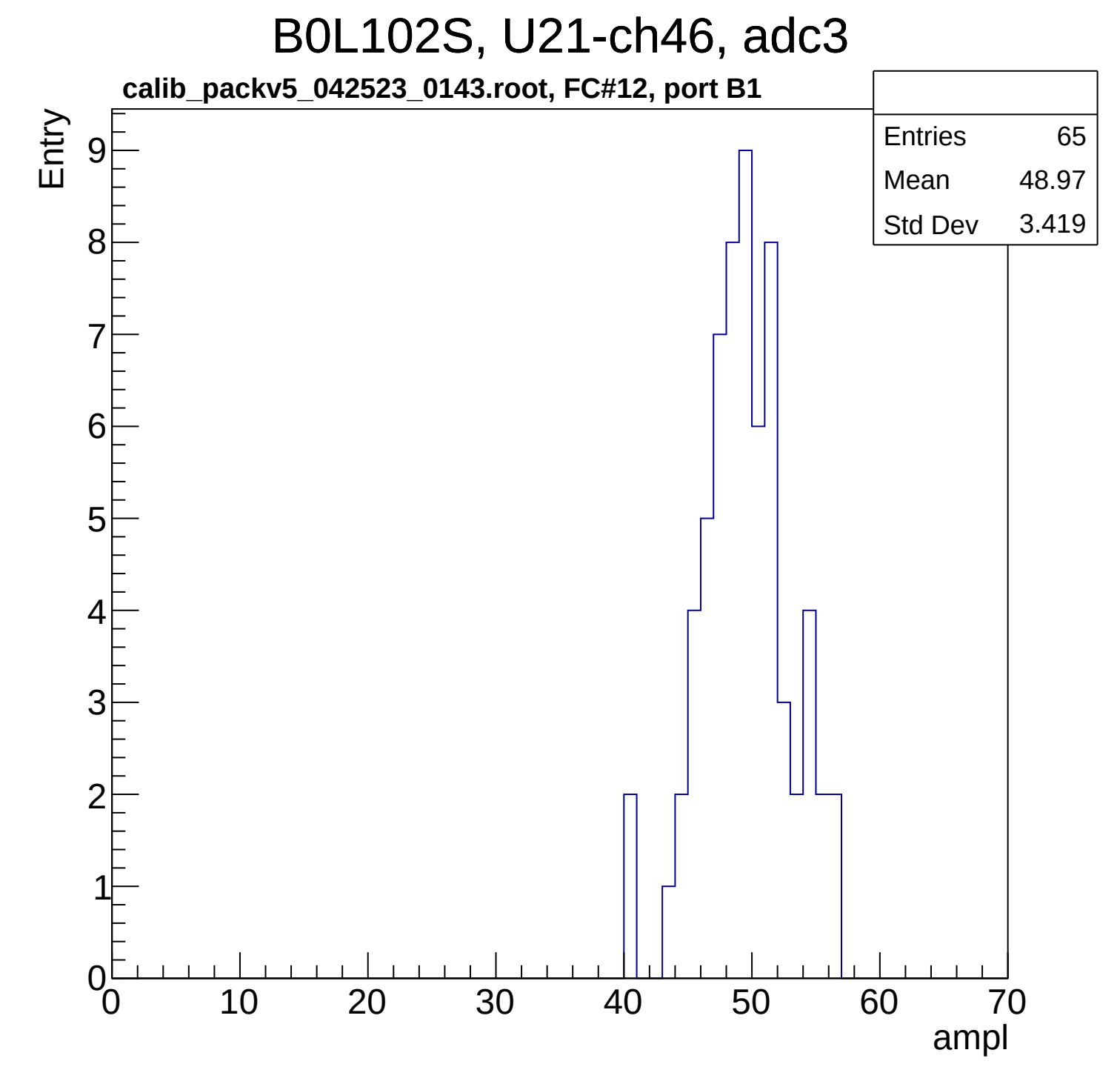
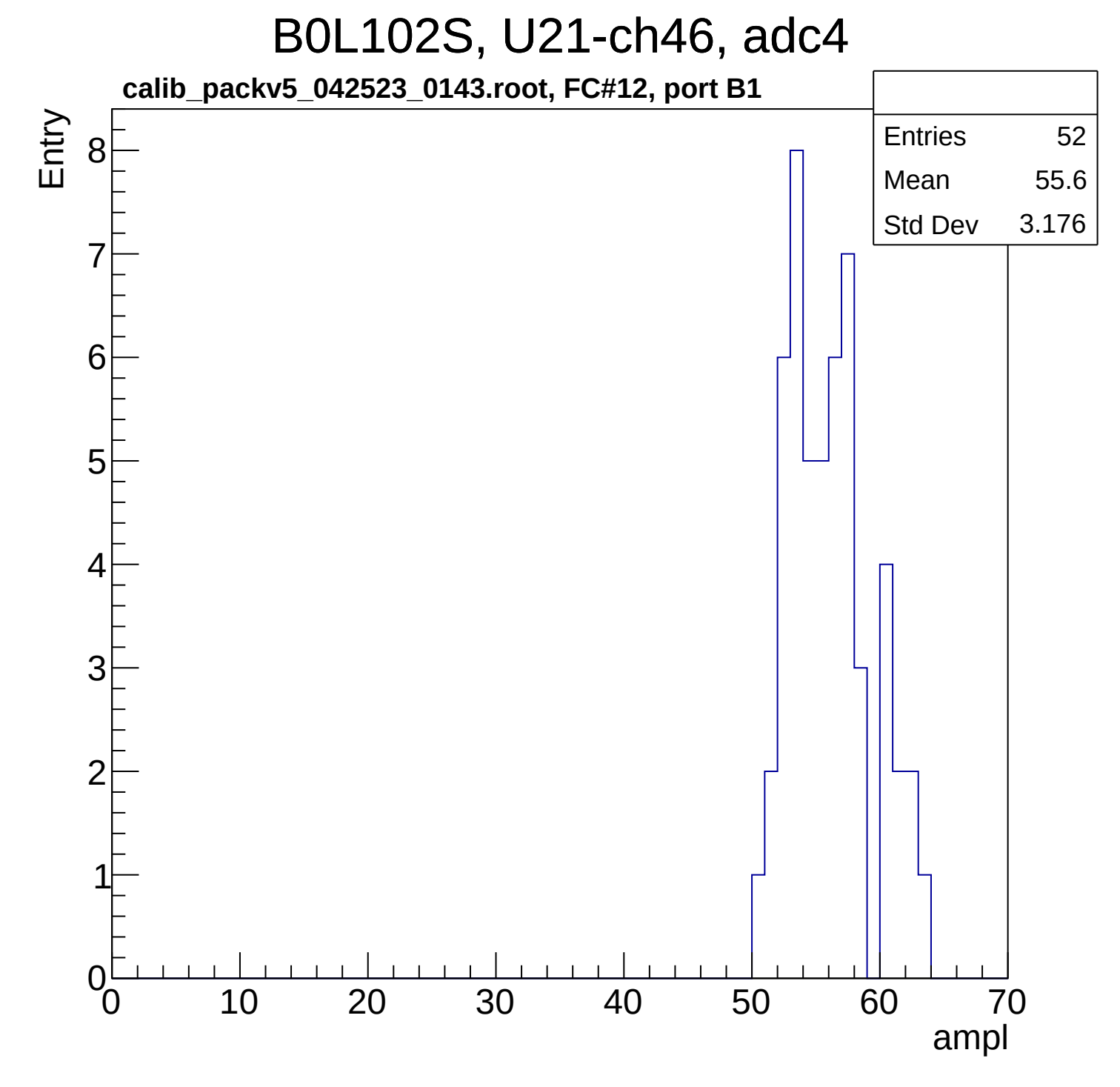

B0L102S, U21-ch46, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.97
Std Dev 3.419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch46, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 55.6
Std Dev 3.176
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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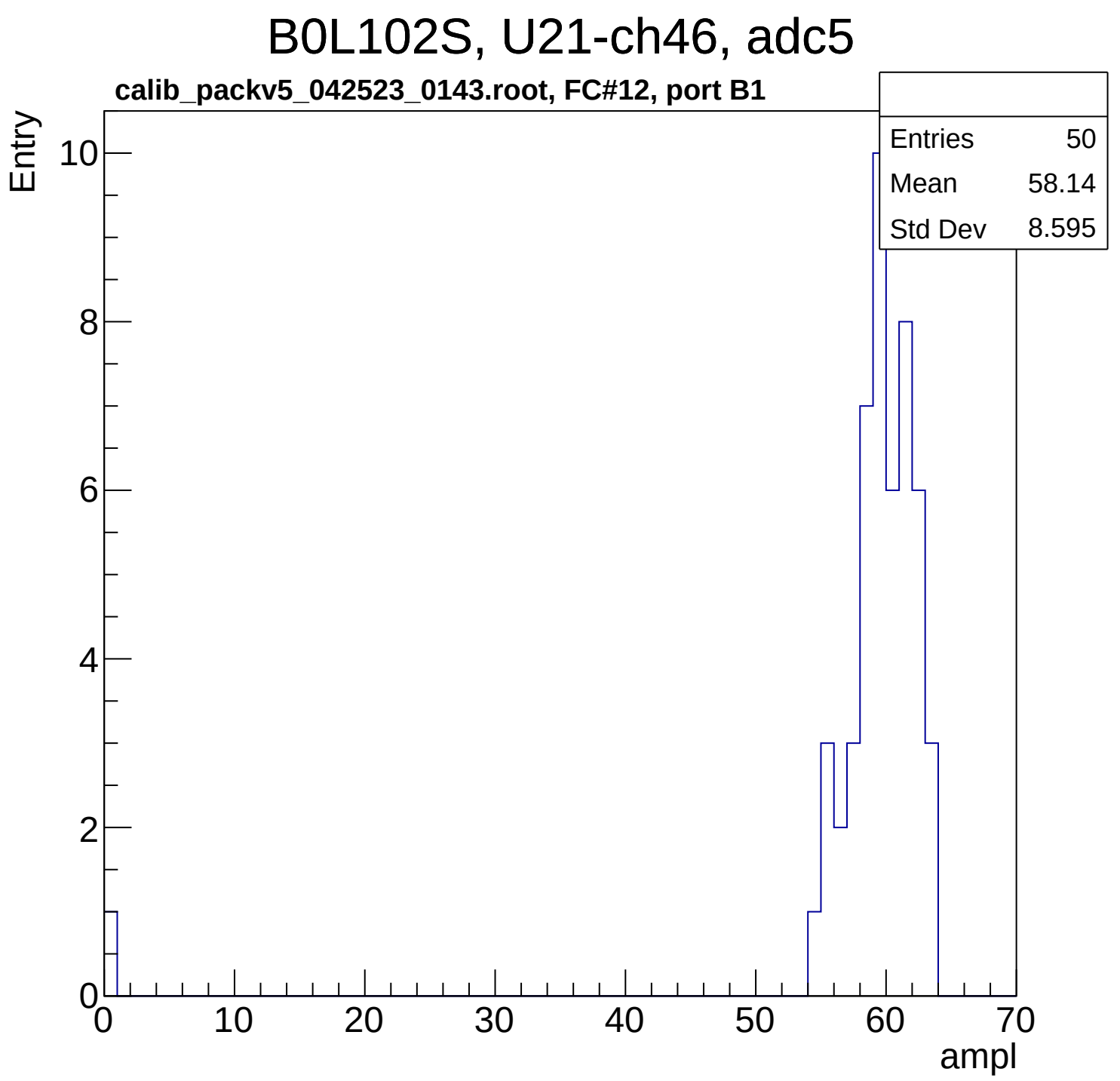
B0L102S, U21-ch46, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.14
Std Dev 8.595
Entry
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2
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6
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10
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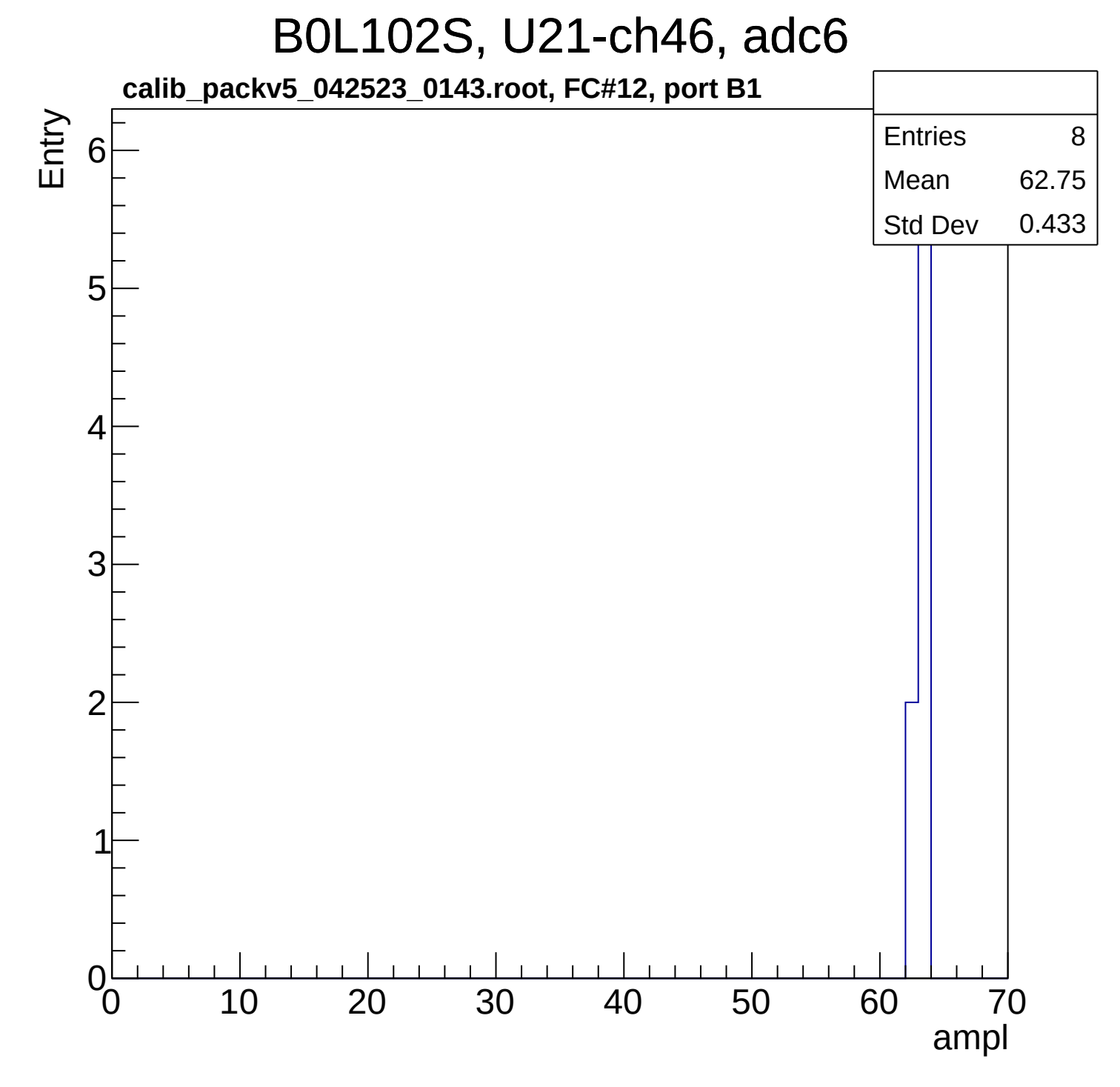
70
ampl


B0L102S, U21-ch46, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.75
Std Dev 0.433
Entry
ampl
0
1
2
3
4
5
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0
10
20
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40
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60
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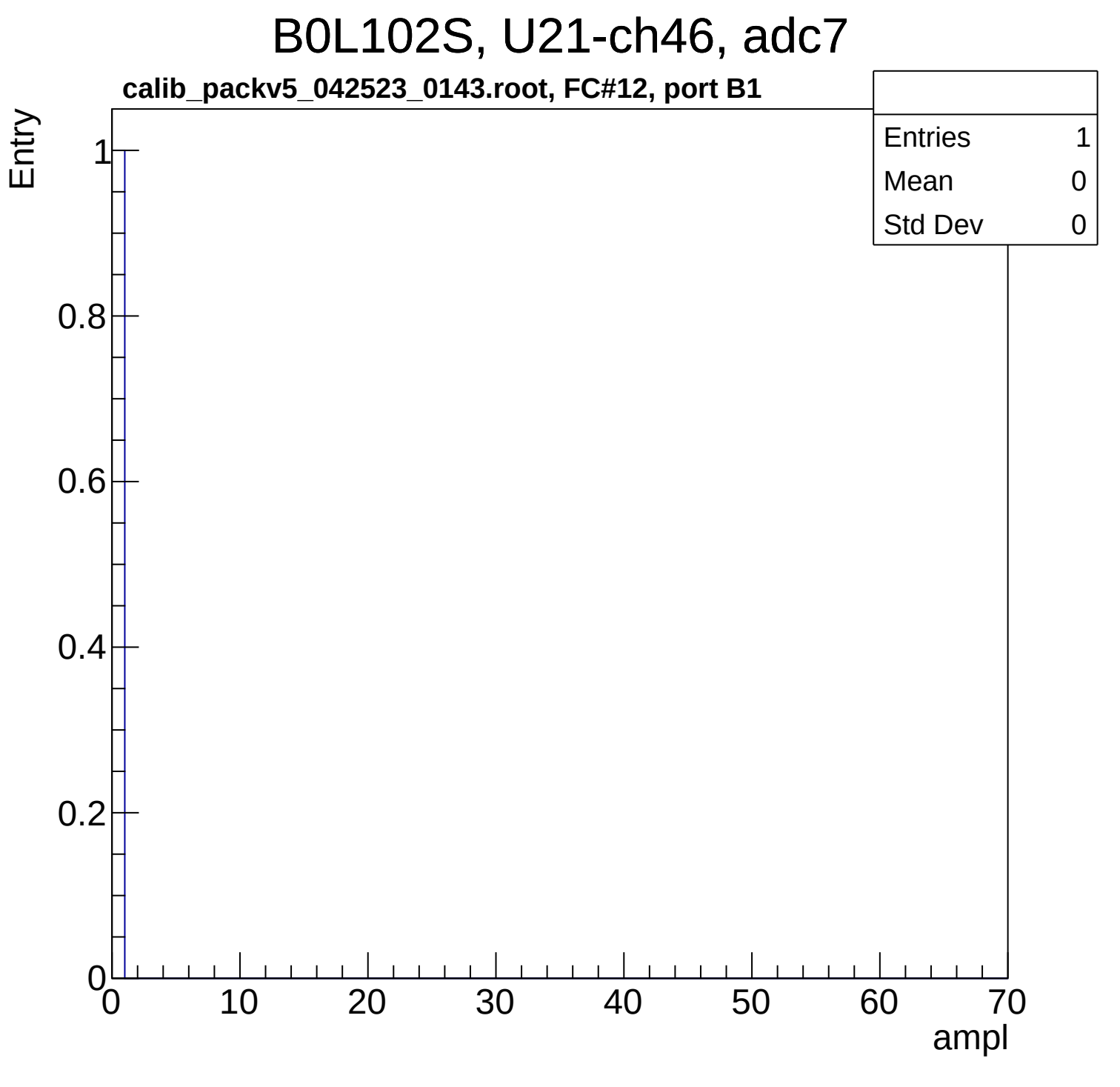
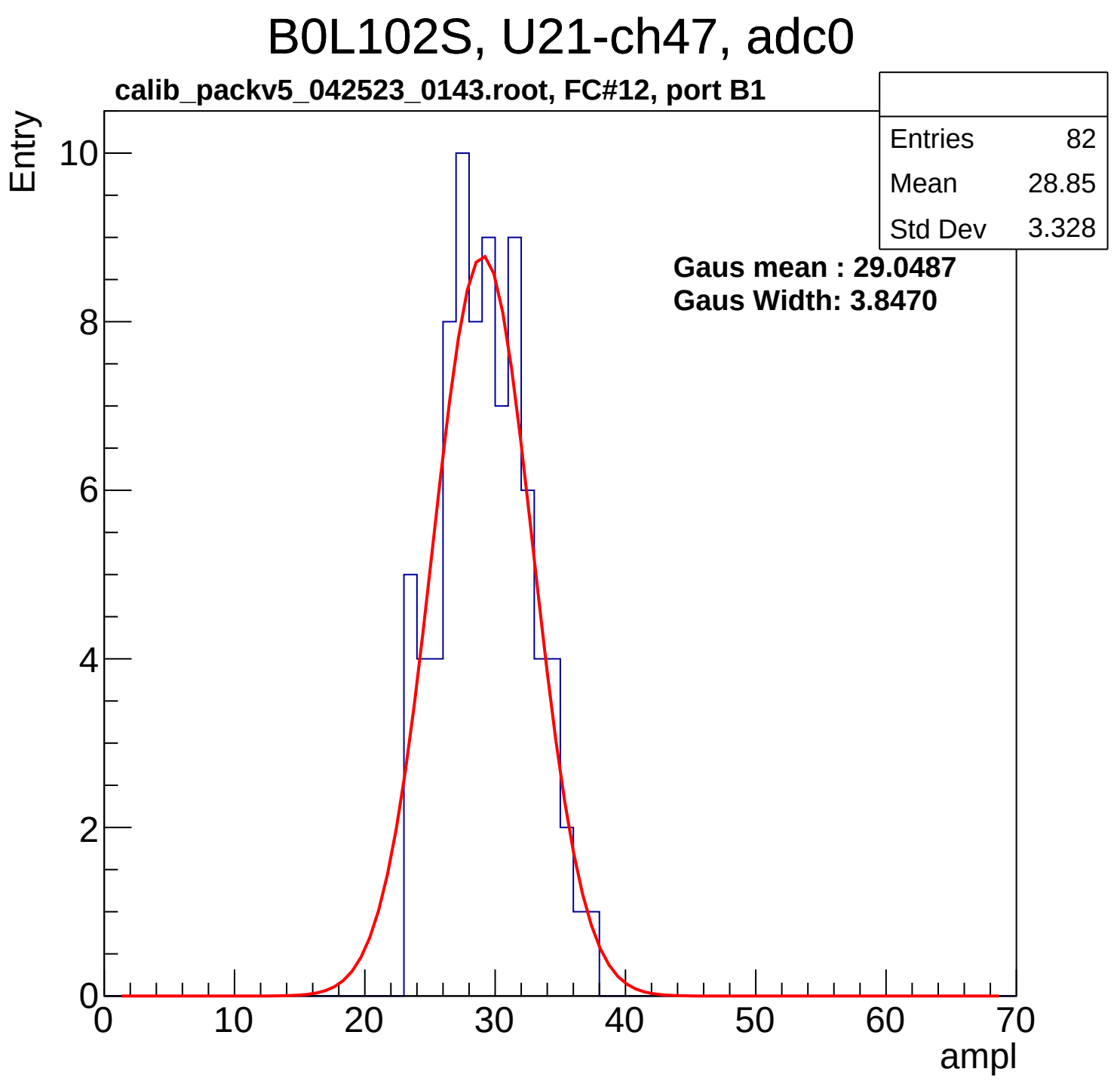

B0L102S, U21-ch46, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch47, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 28.85
Std Dev 3.328
Gaus mean : 29.0487
Gaus Width: 3.8470
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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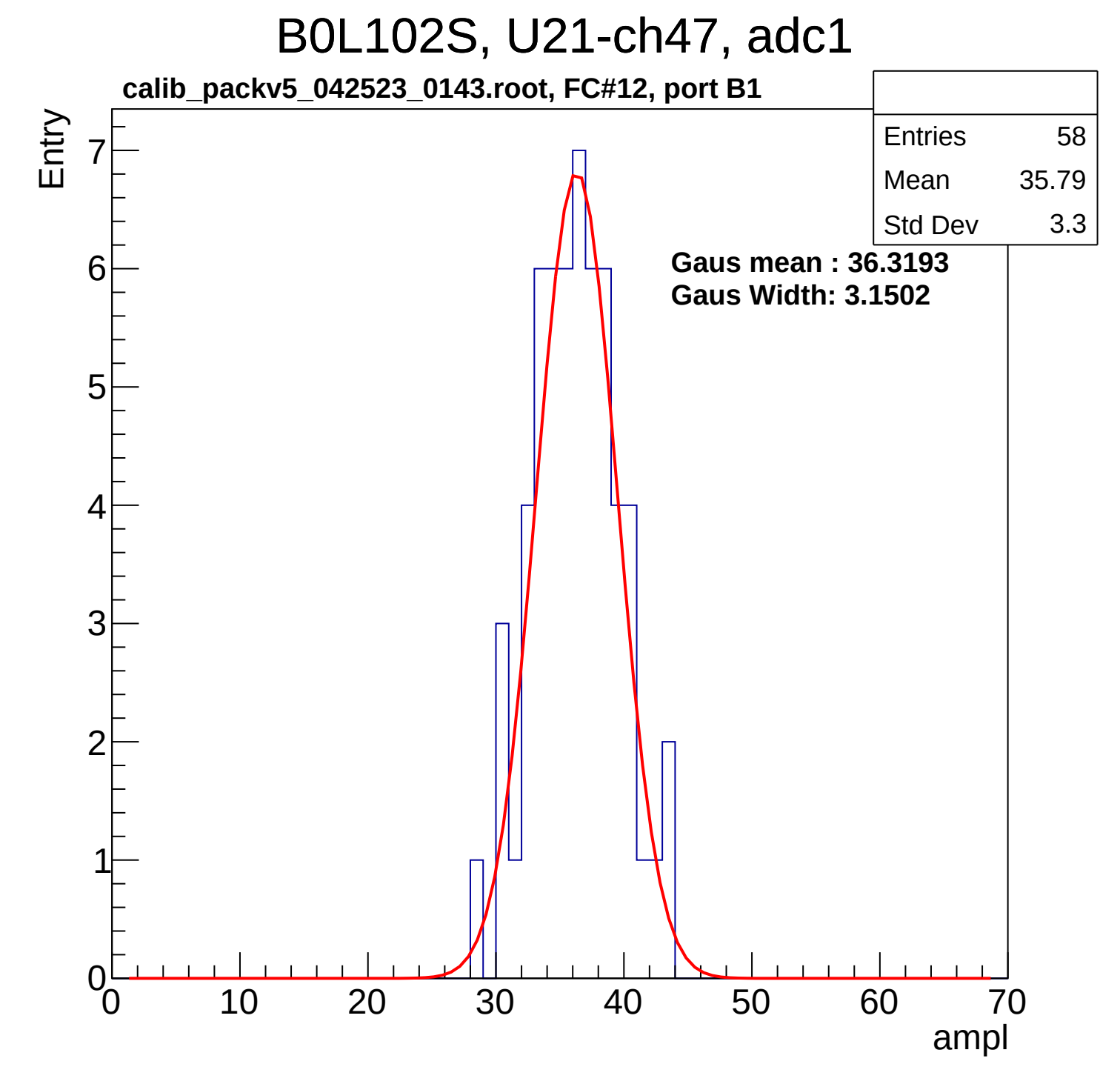
B0L102S, U21-ch47, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 35.79
Std Dev 3.3
Gaus mean : 36.3193
Gaus Width: 3.1502
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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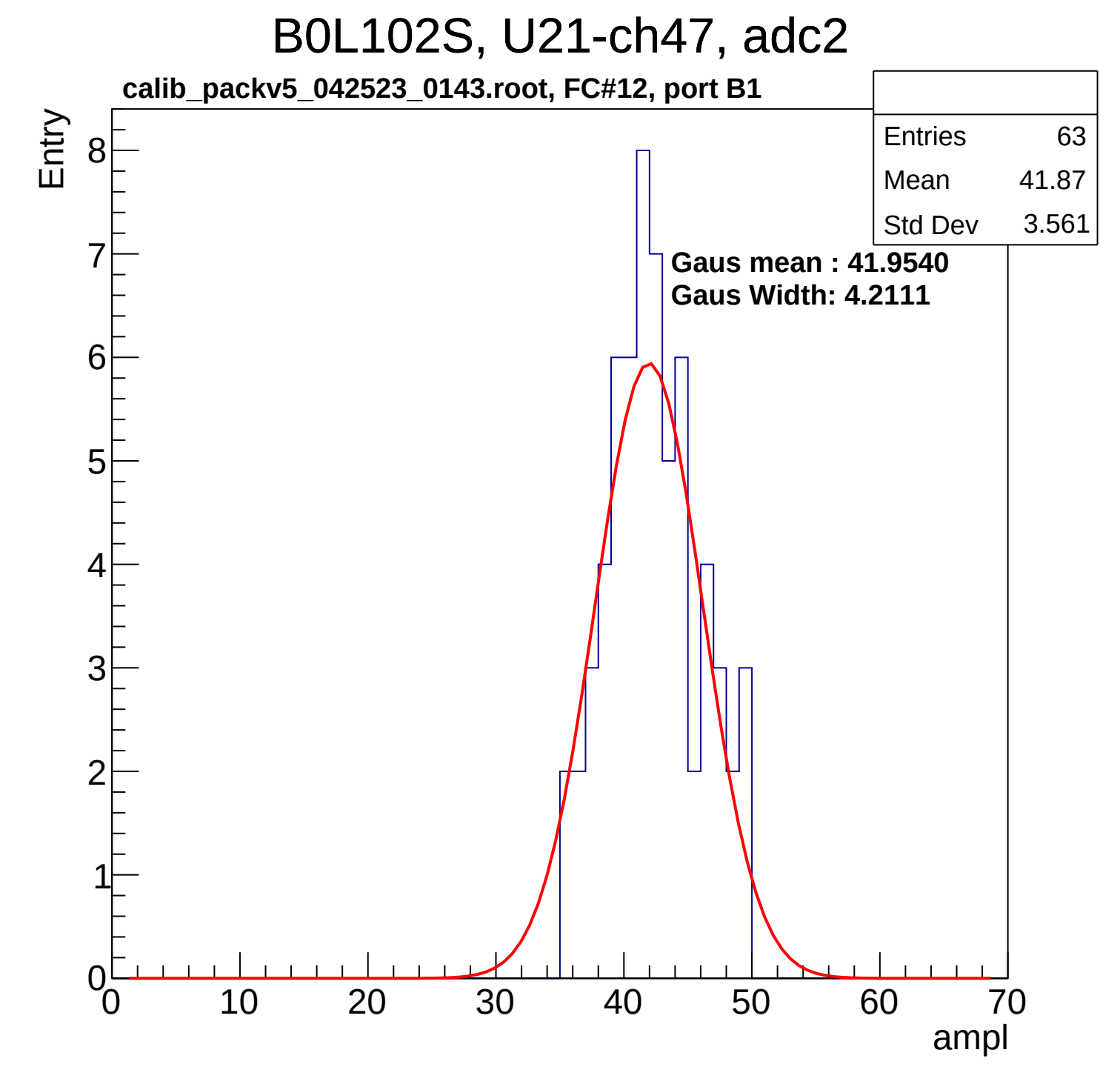
B0L102S, U21-ch47, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 41.87
Std Dev 3.561
Gaus mean : 41.9540
Gaus Width: 4.2111
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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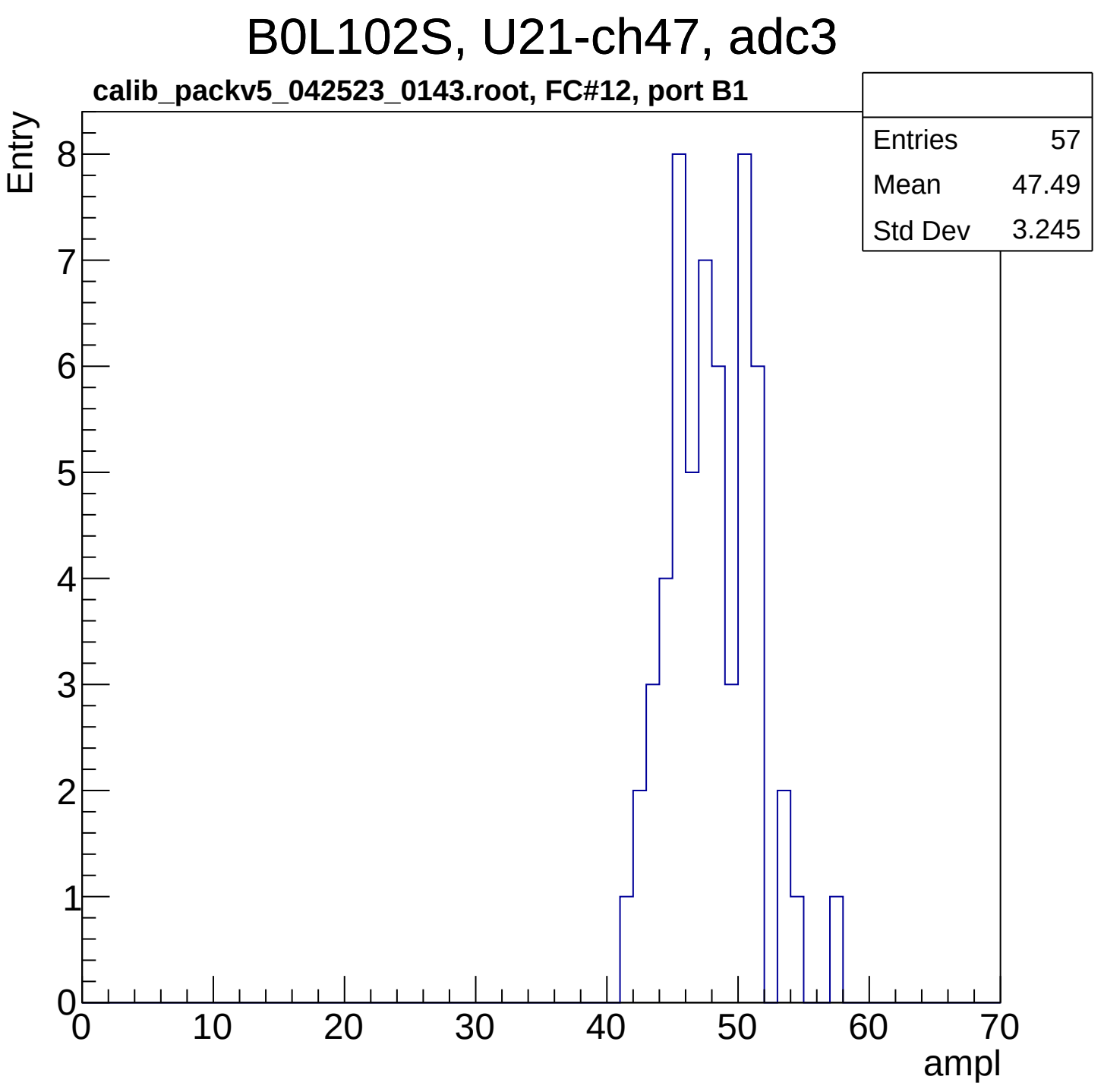
B0L102S, U21-ch47, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 47.49
Std Dev 3.245
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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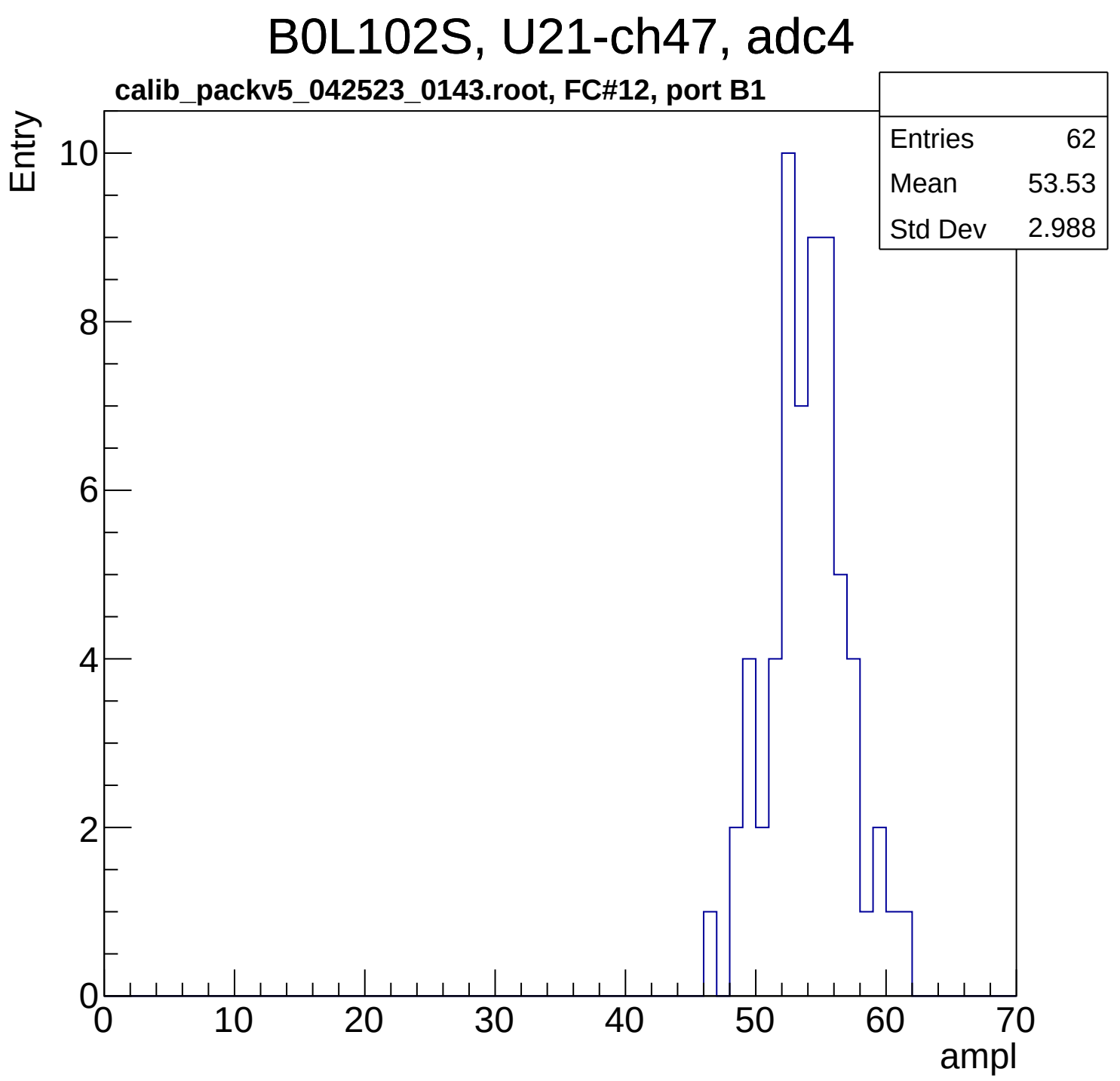
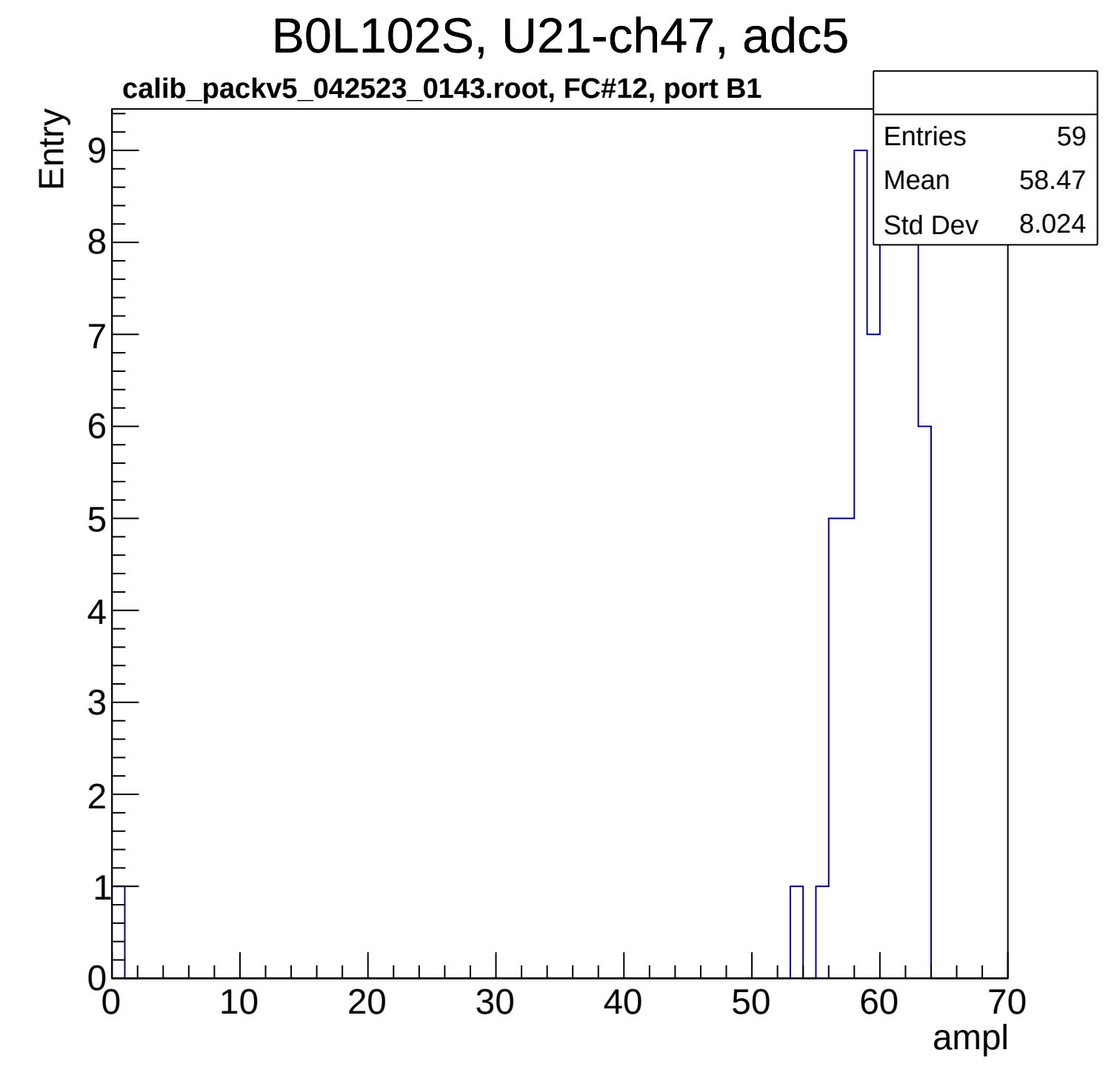

B0L102S, U21-ch47, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 53.53
Std Dev 2.988
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch47, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 58.47
Std Dev 8.024
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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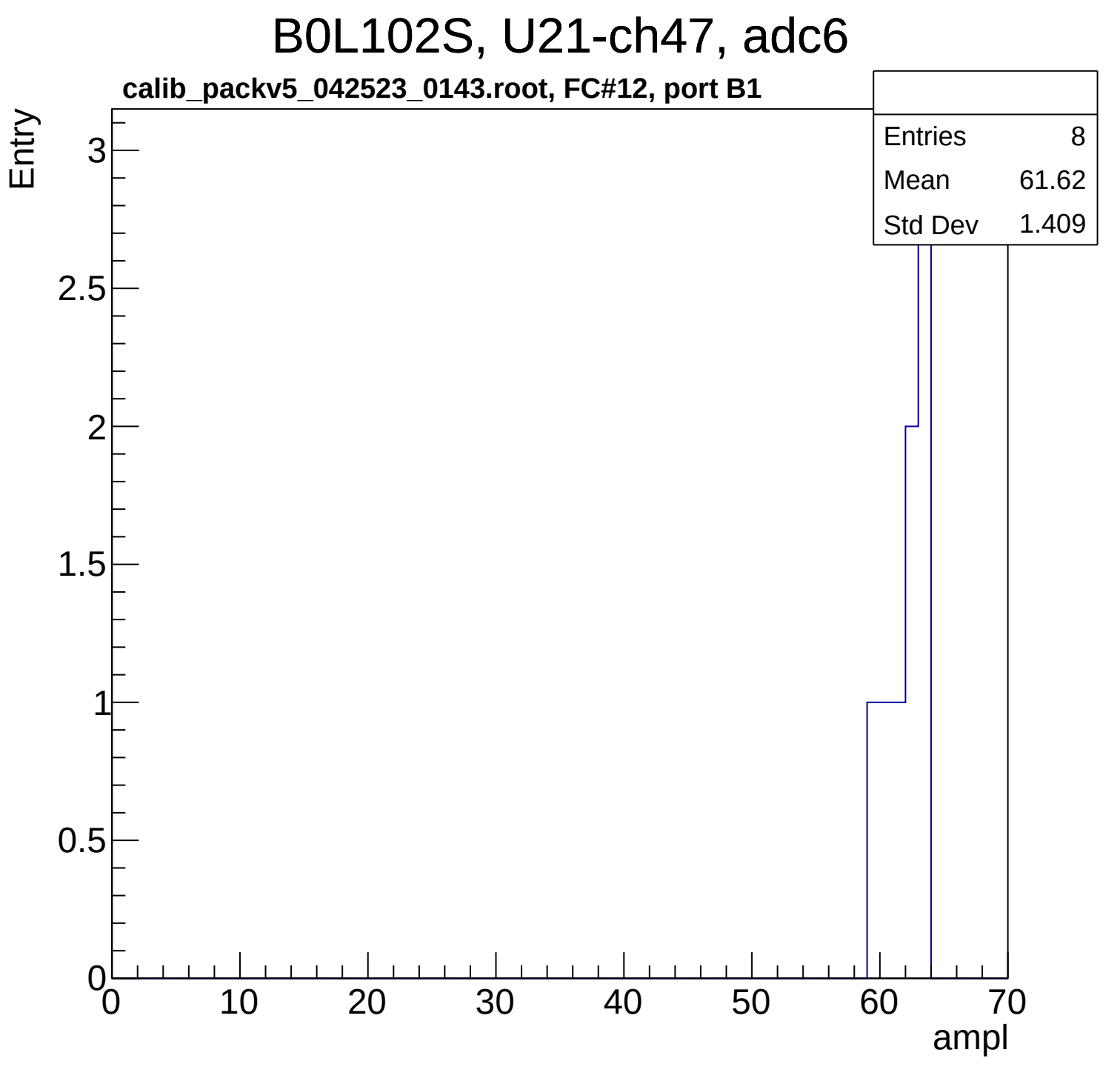
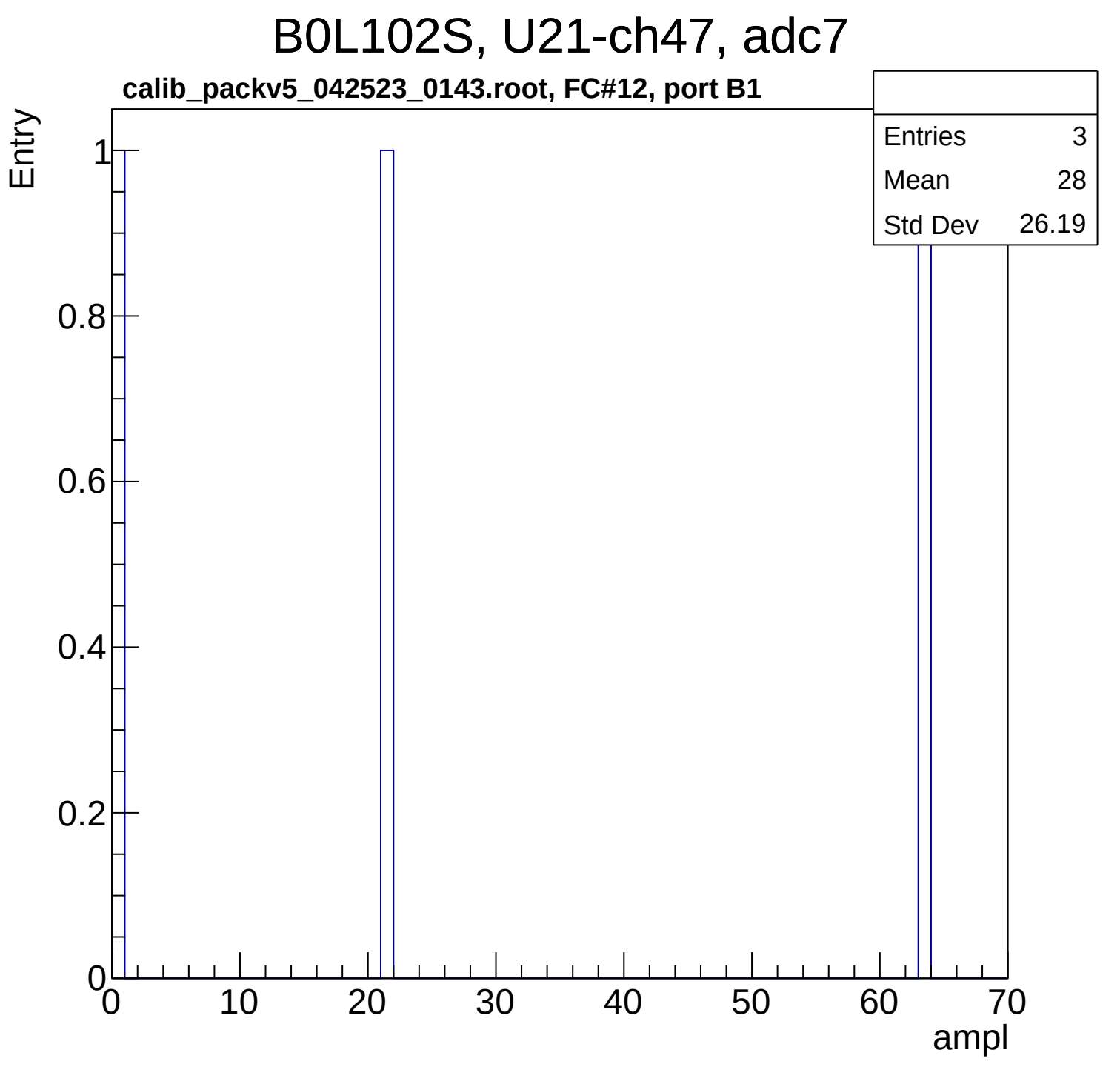

B0L102S, U21-ch47, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.62
Std Dev 1.409
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch47, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 28
Std Dev 26.19
Entry
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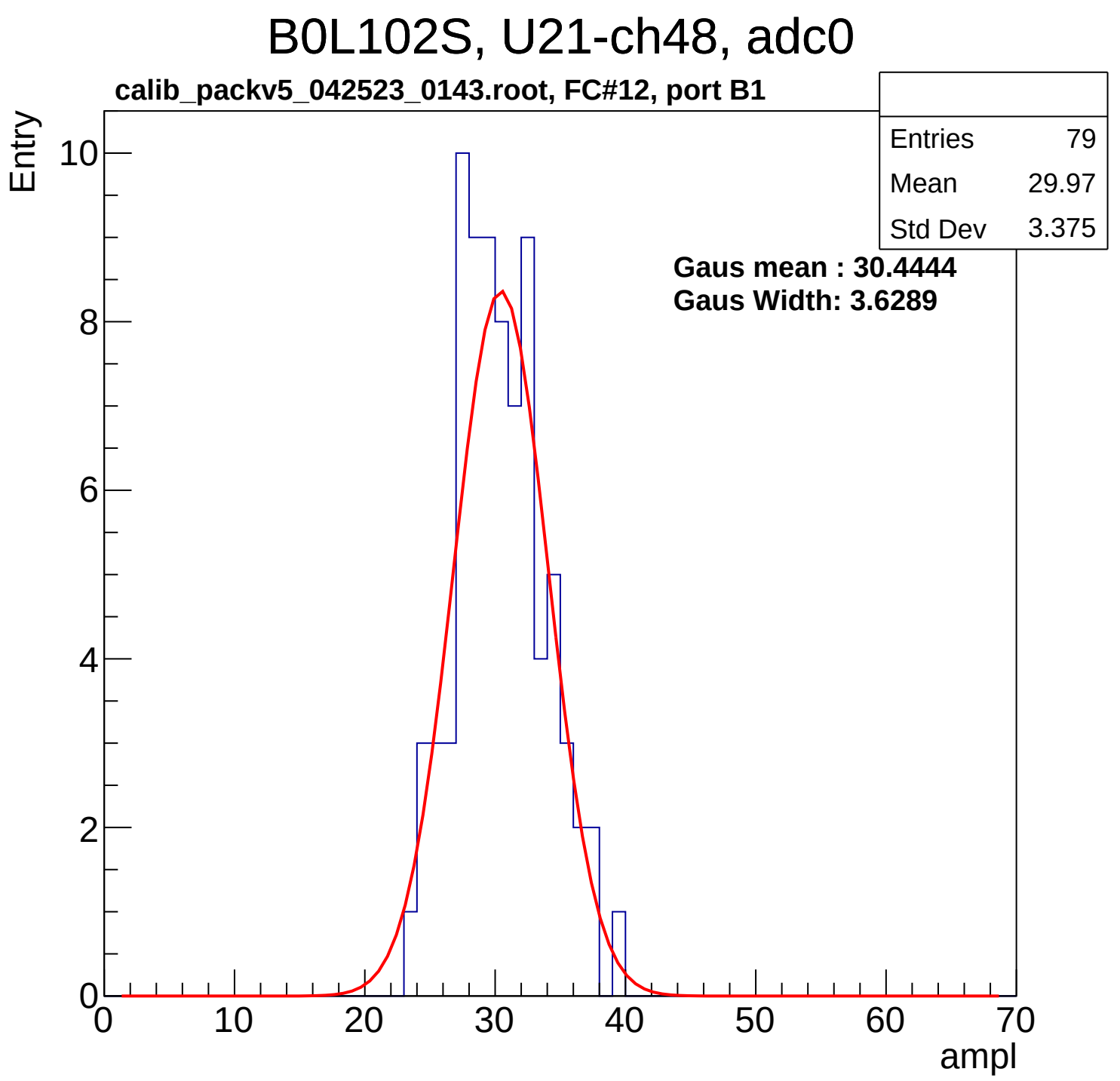
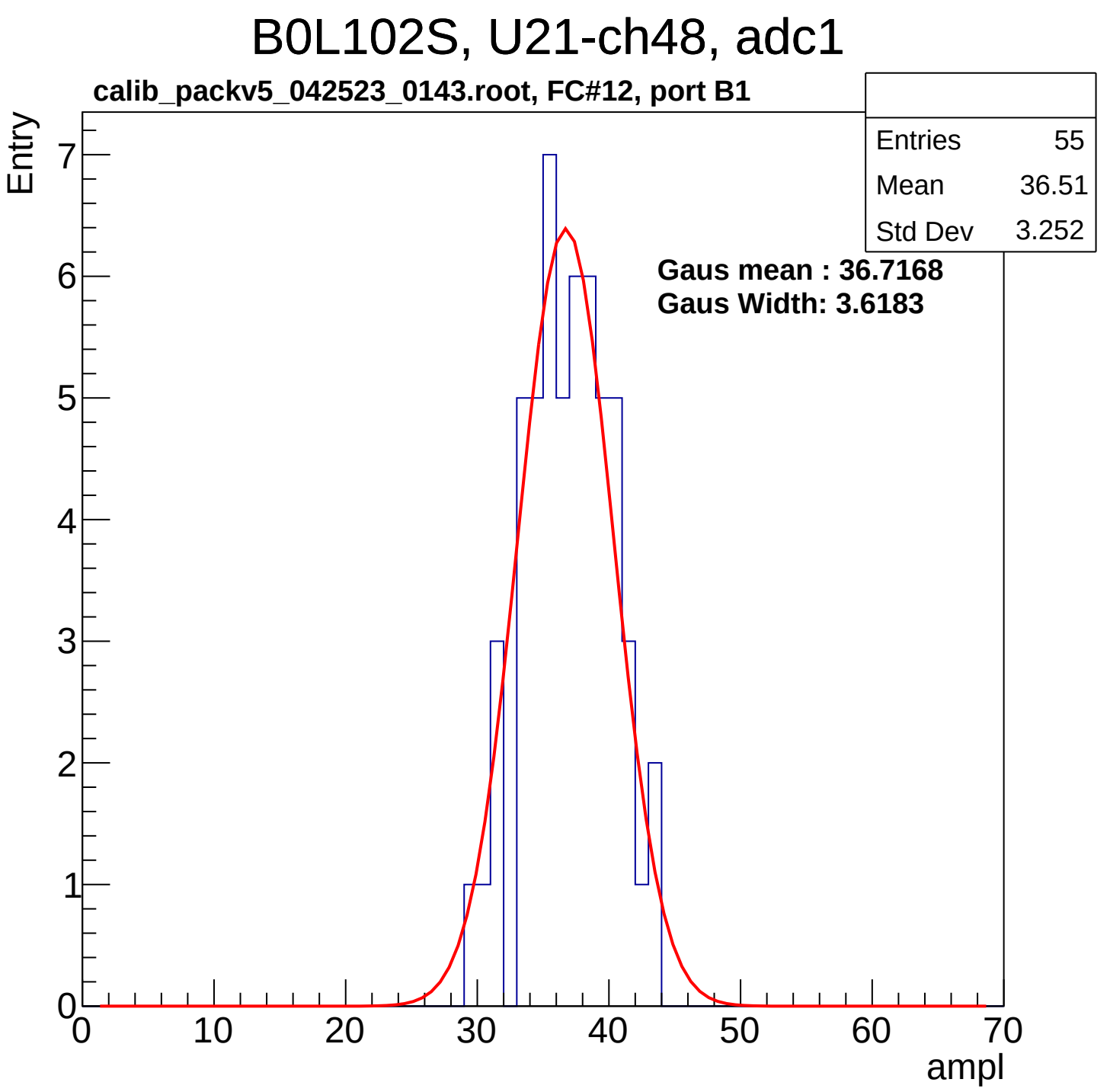
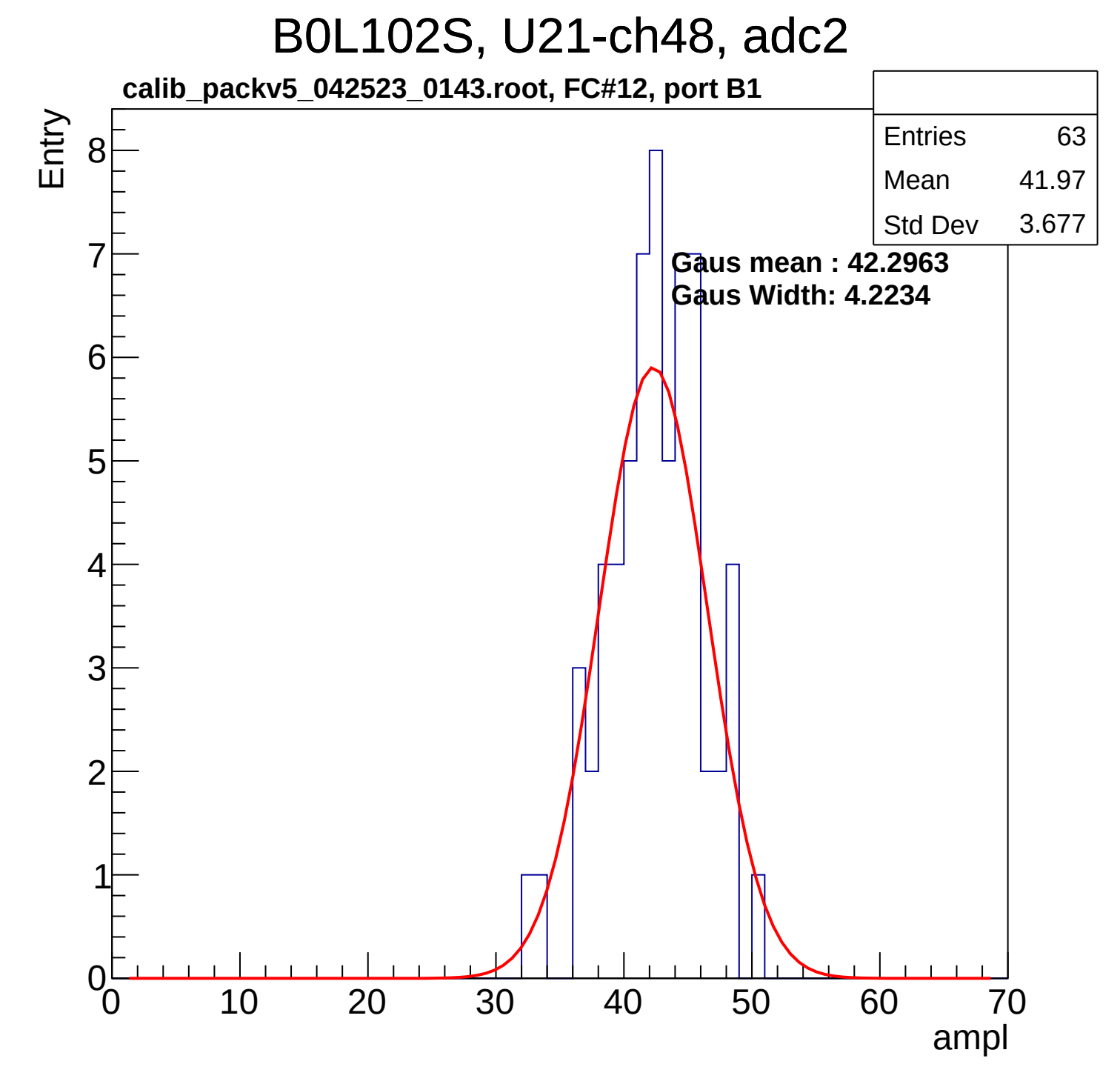
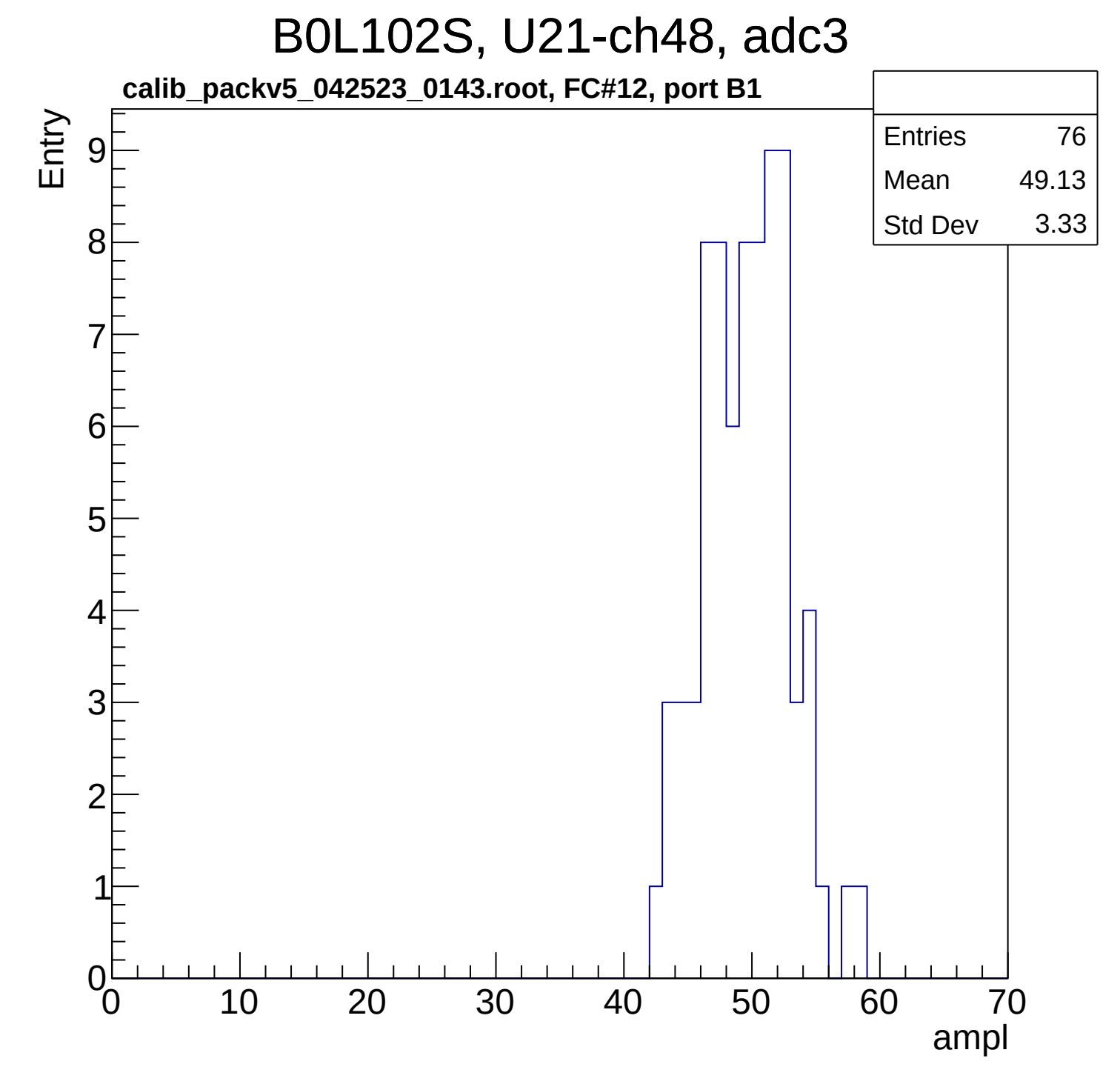
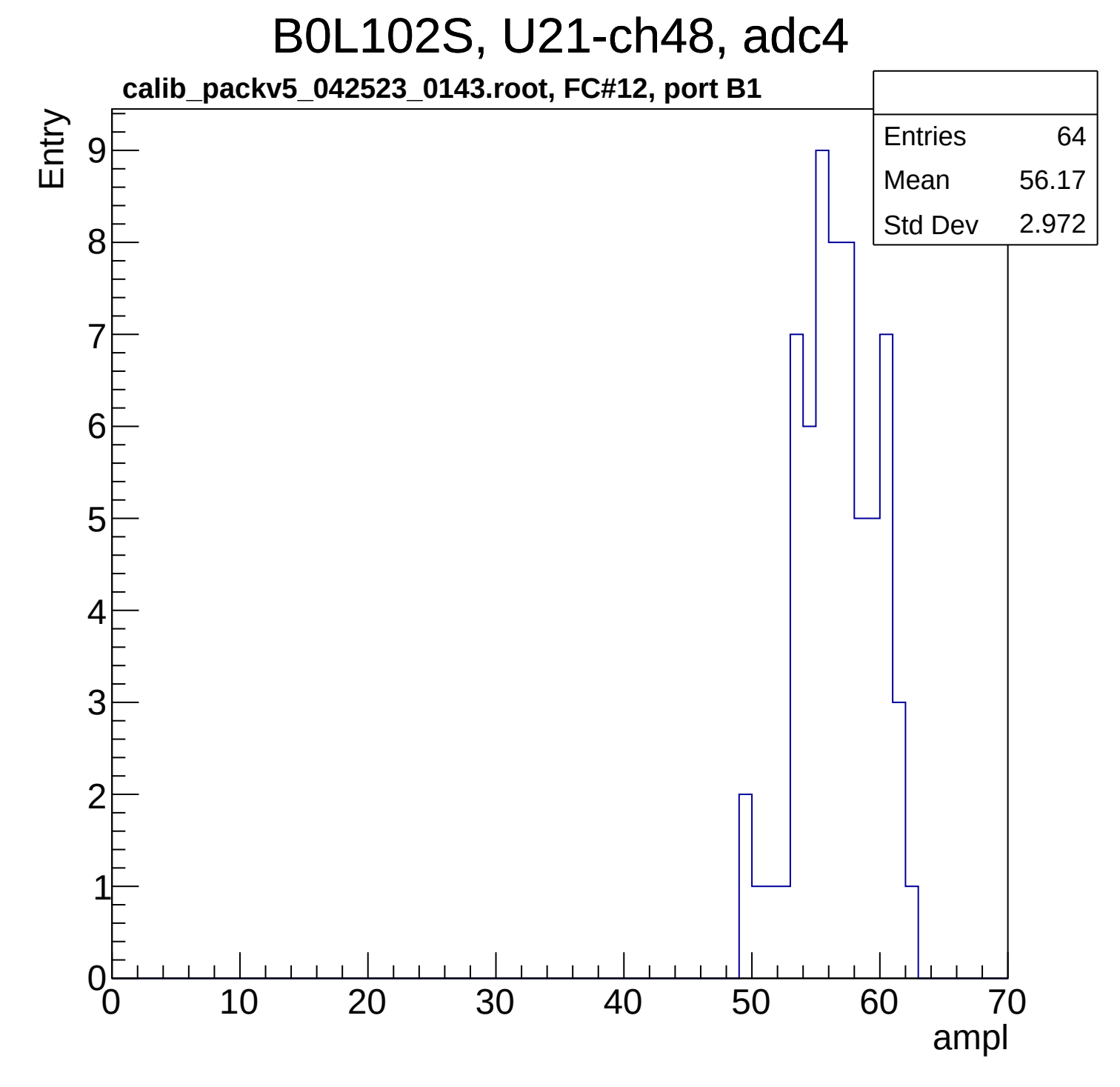
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

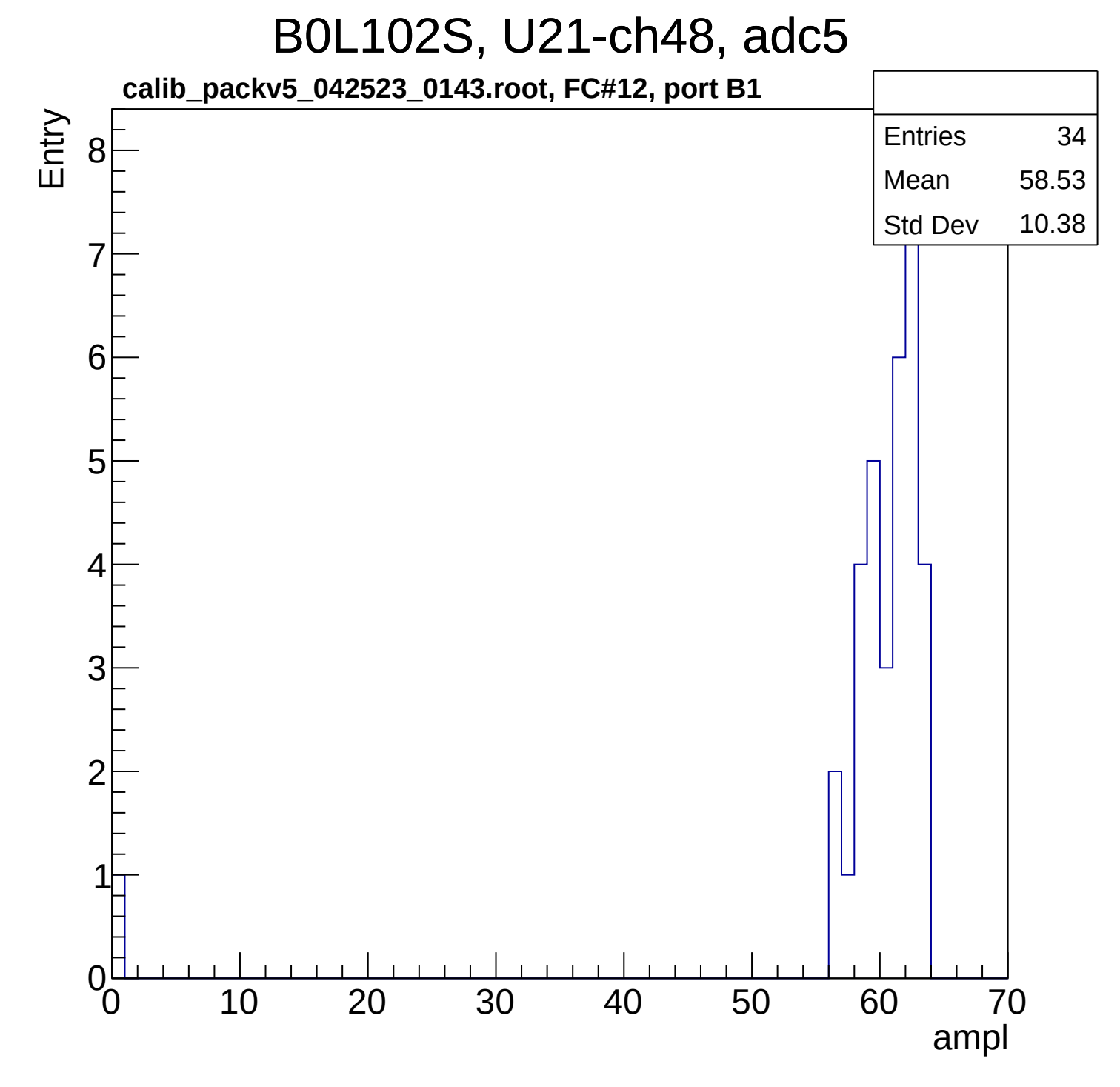

B0L102S, U21-ch48, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 29.97
Std Dev 3.375
Gaus mean : 30.4444
Gaus Width: 3.6289
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch48, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 36.51
Std Dev 3.252
Gaus mean : 36.7168
Gaus Width: 3.6183
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch48, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 41.97
Std Dev 3.677
Gaus mean : 42.2963
Gaus Width: 4.2234
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch48, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 49.13
Std Dev 3.33
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch48, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.17
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch48, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 58.53
Std Dev 10.38
Entry
ampl
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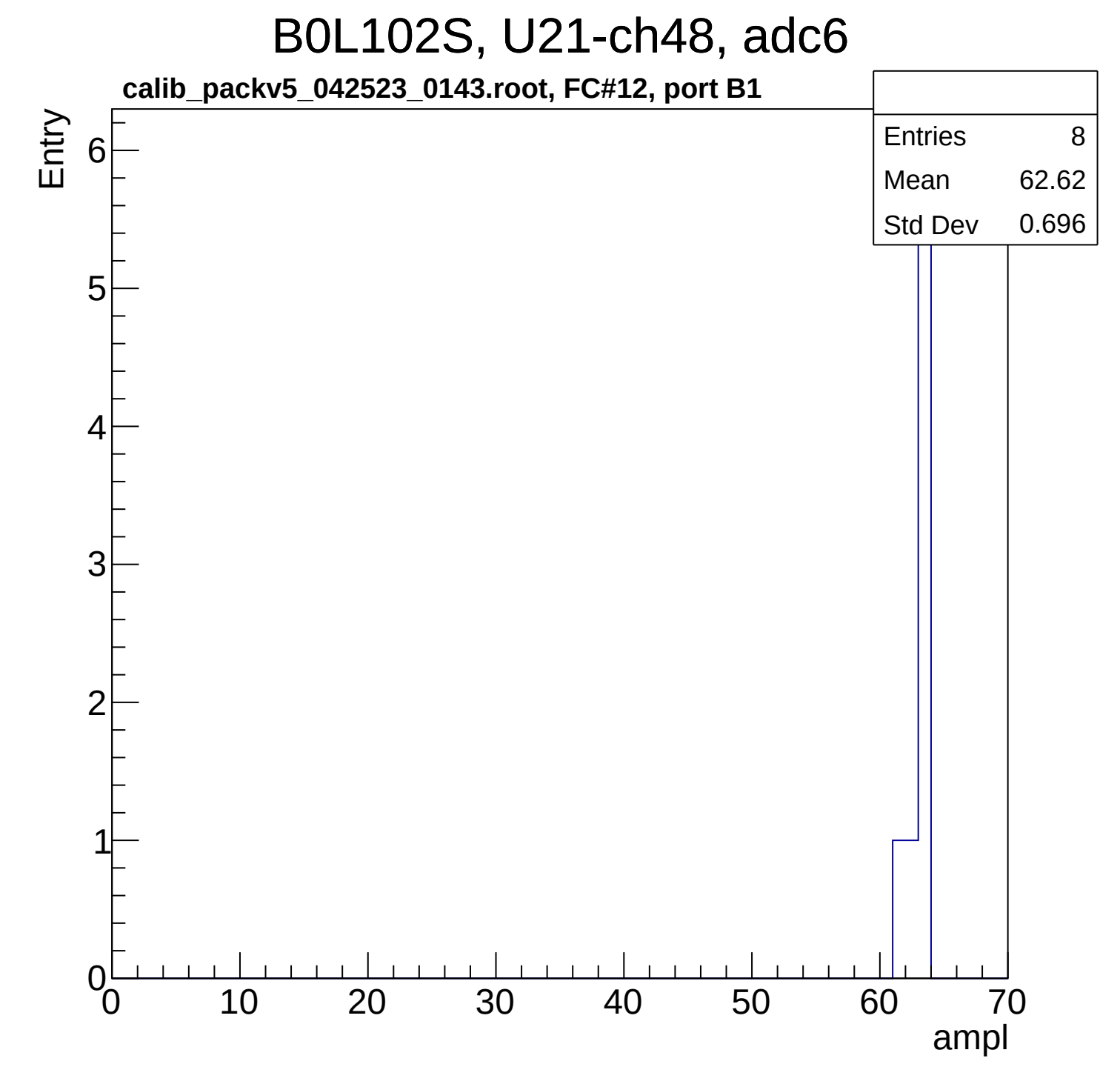
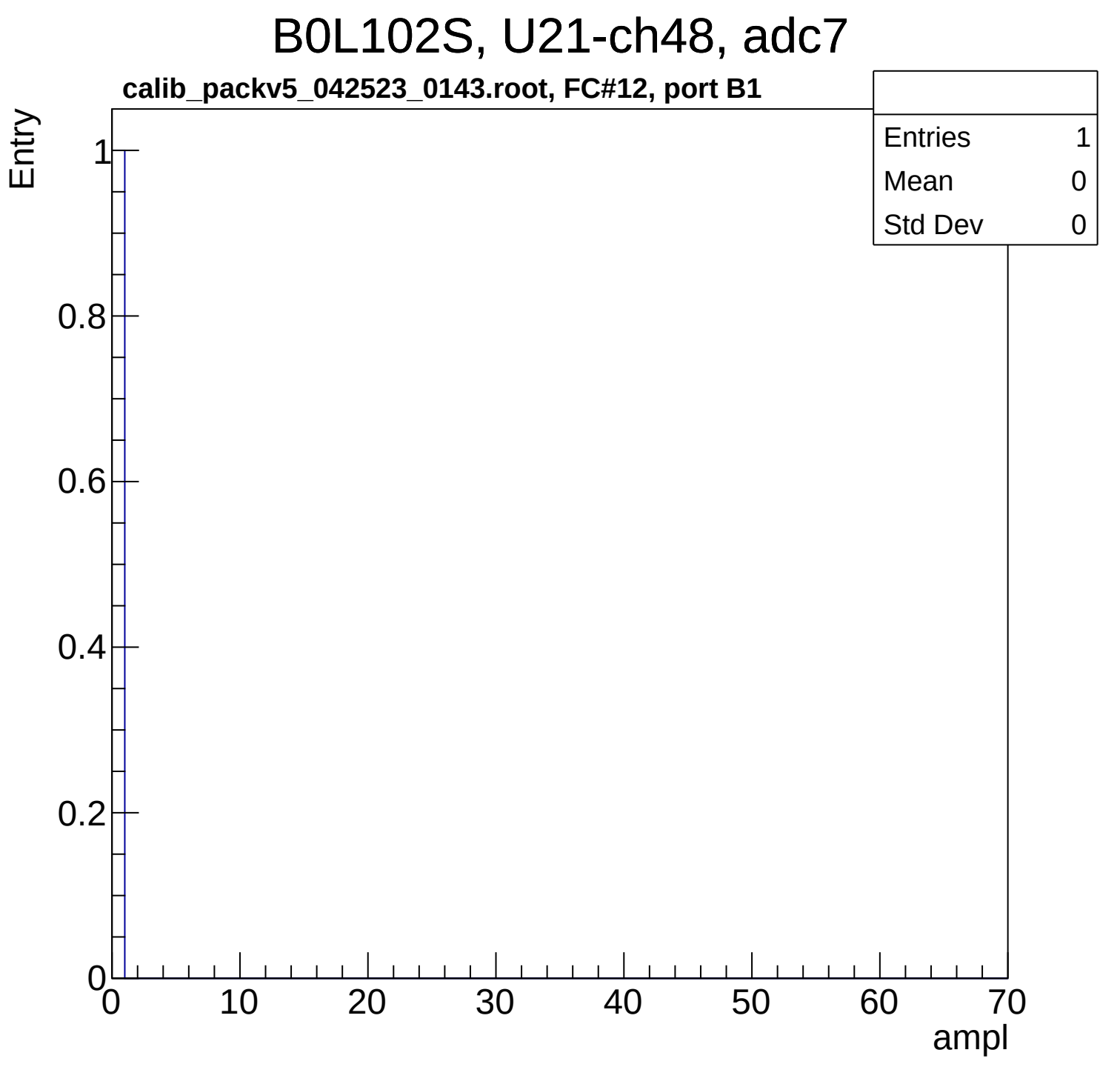
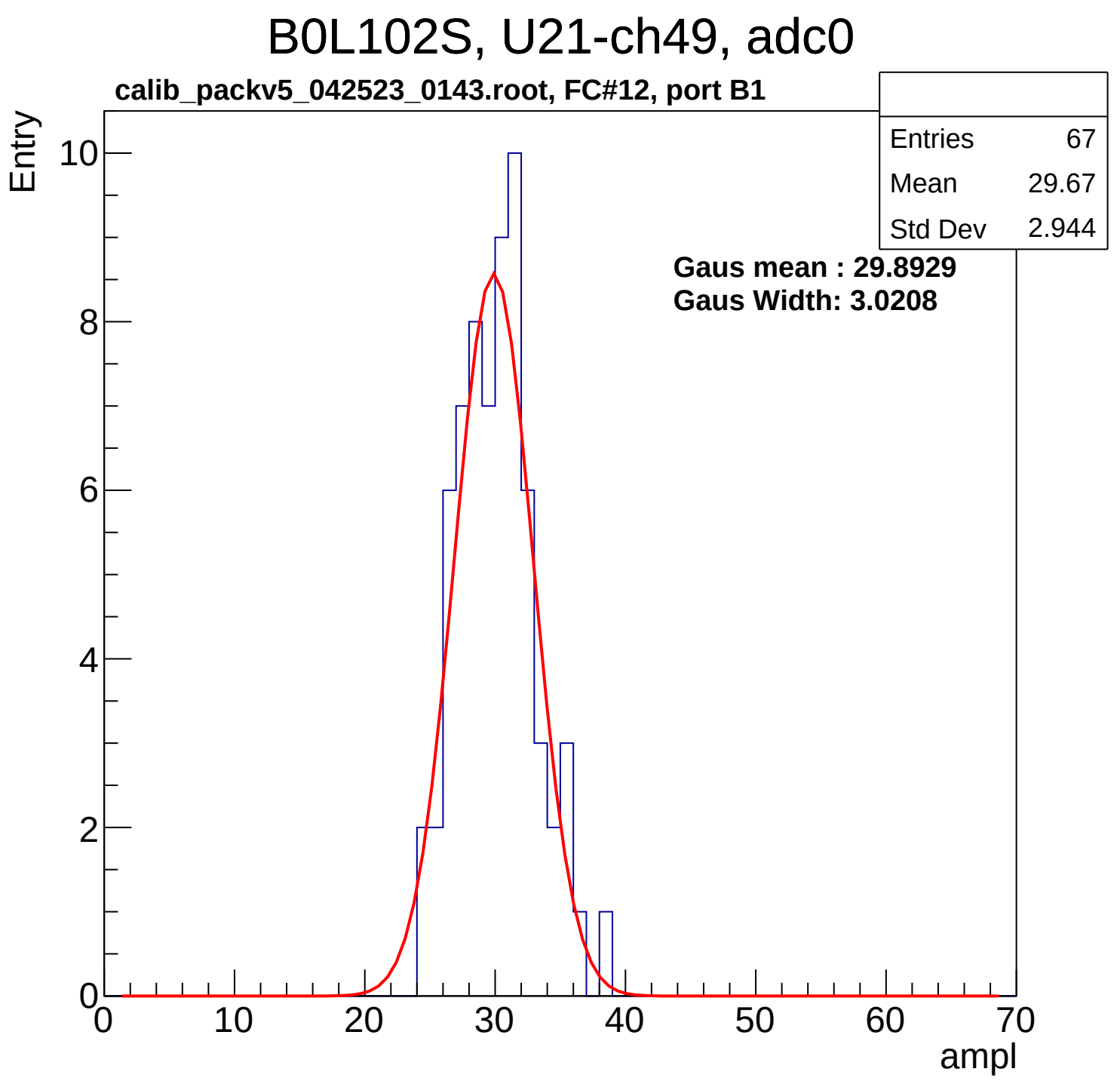
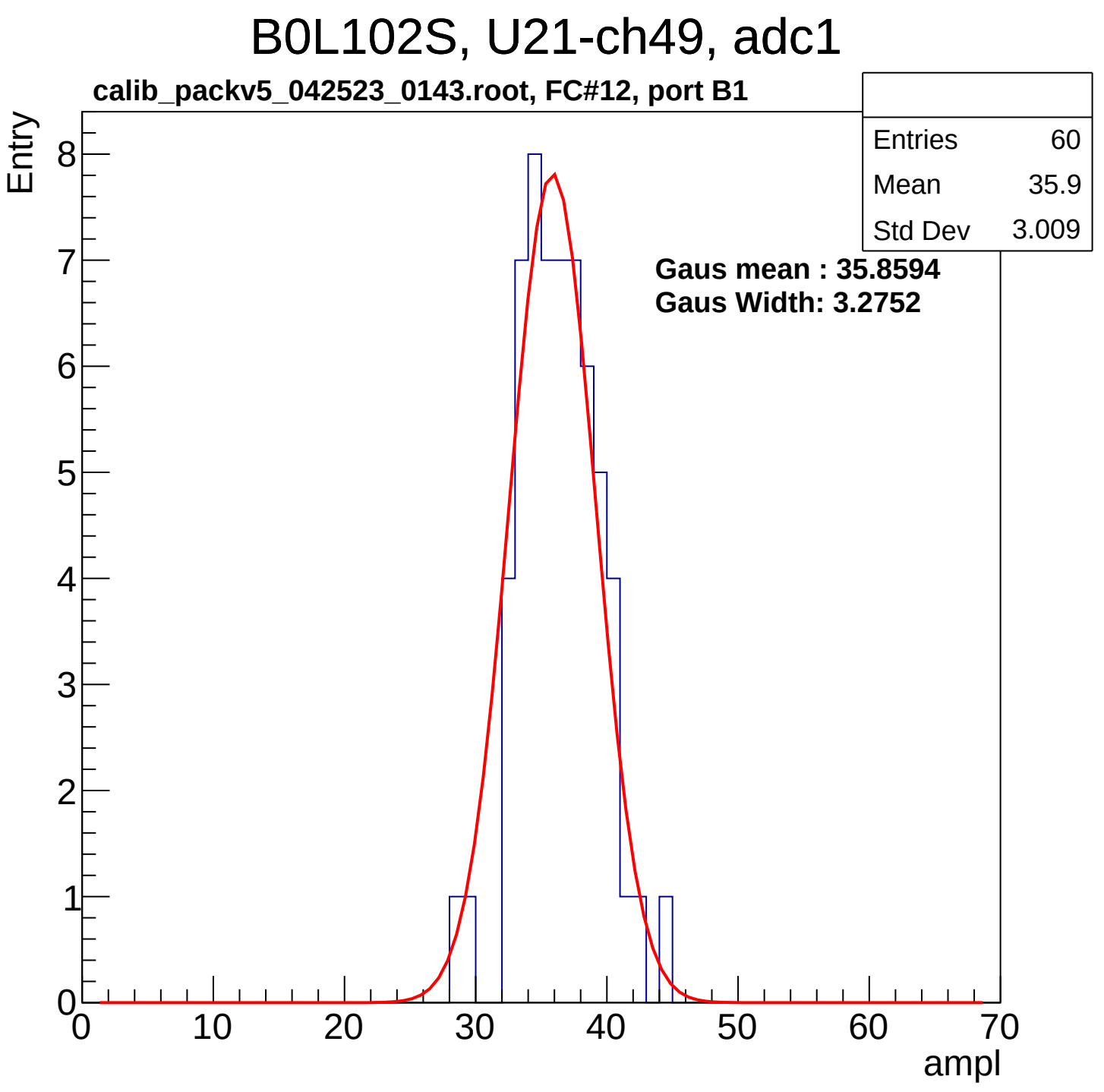
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch48, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.62
Std Dev 0.696
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U21-ch48, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch49, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.67
Std Dev 2.944
Gaus mean : 29.8929
Gaus Width: 3.0208
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch49, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 35.9
Std Dev 3.009
Gaus mean : 35.8594
Gaus Width: 3.2752
Entry
ampl
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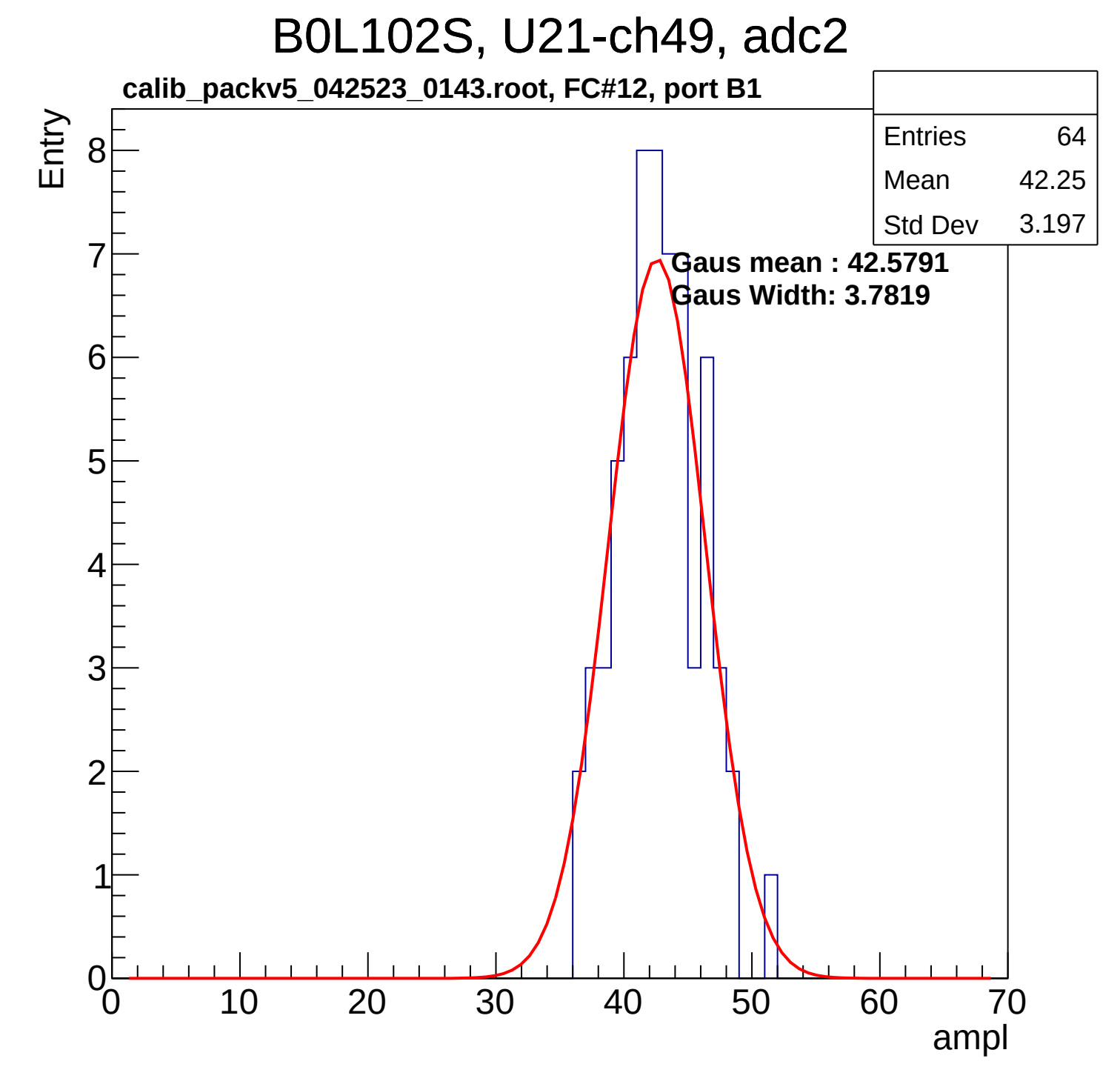
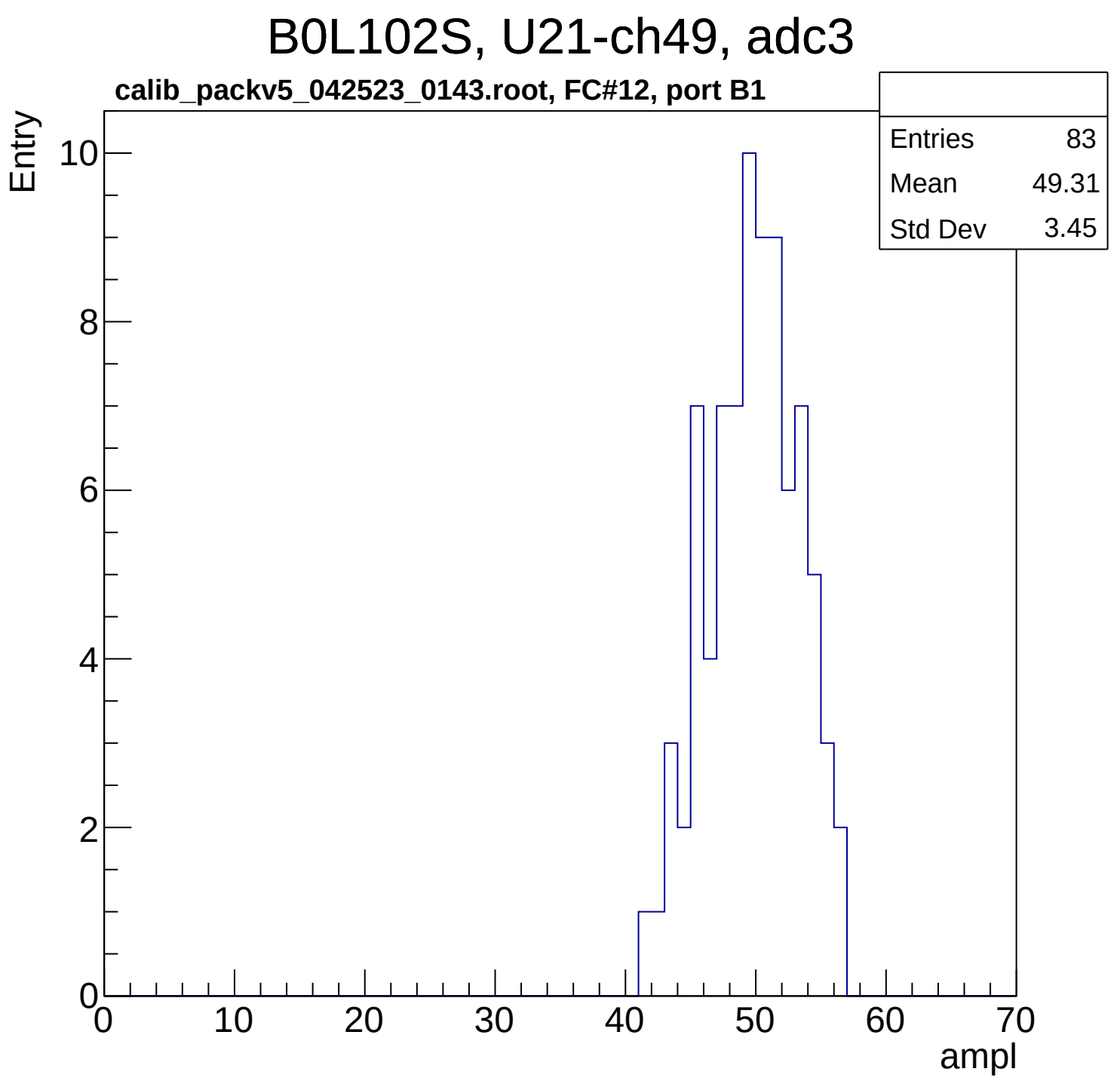
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch49, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 42.25
Std Dev 3.197
Gaus mean : 42.5791
Gaus Width: 3.7819
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch49, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 49.31
Std Dev 3.45
Entry
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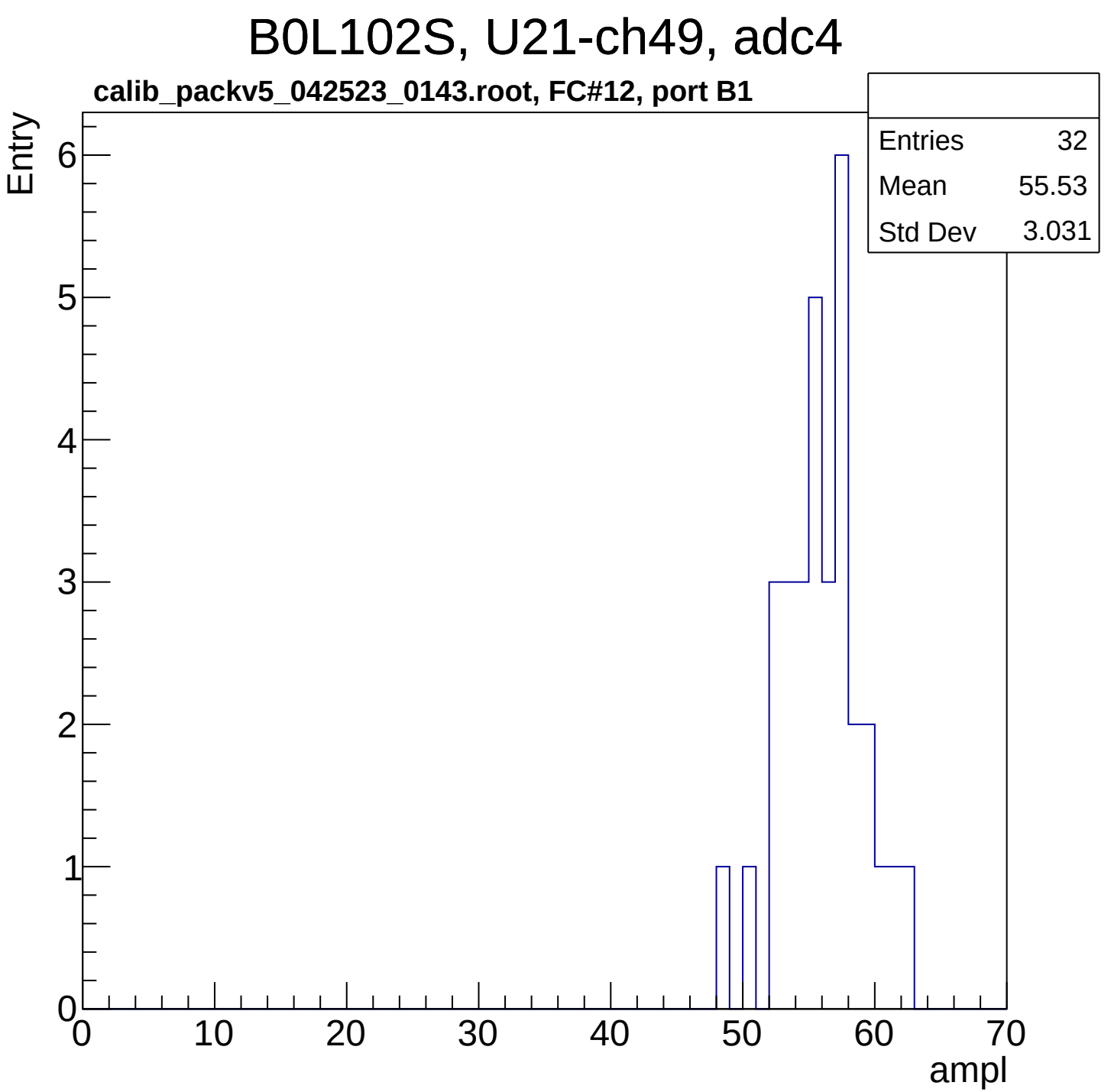
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch49, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 55.53
Std Dev 3.031
Entry
0
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4
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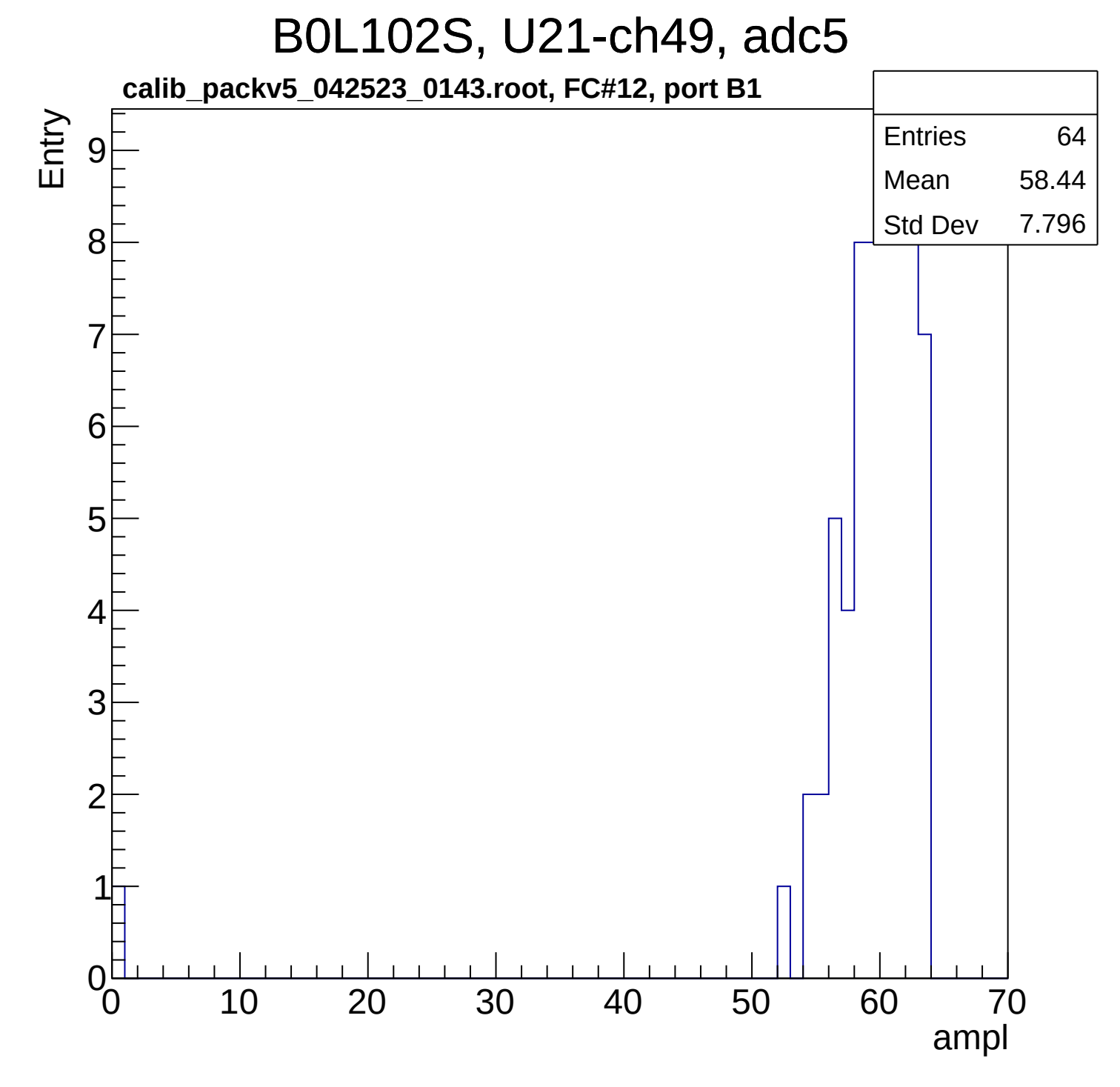
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch49, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 58.44
Std Dev 7.796
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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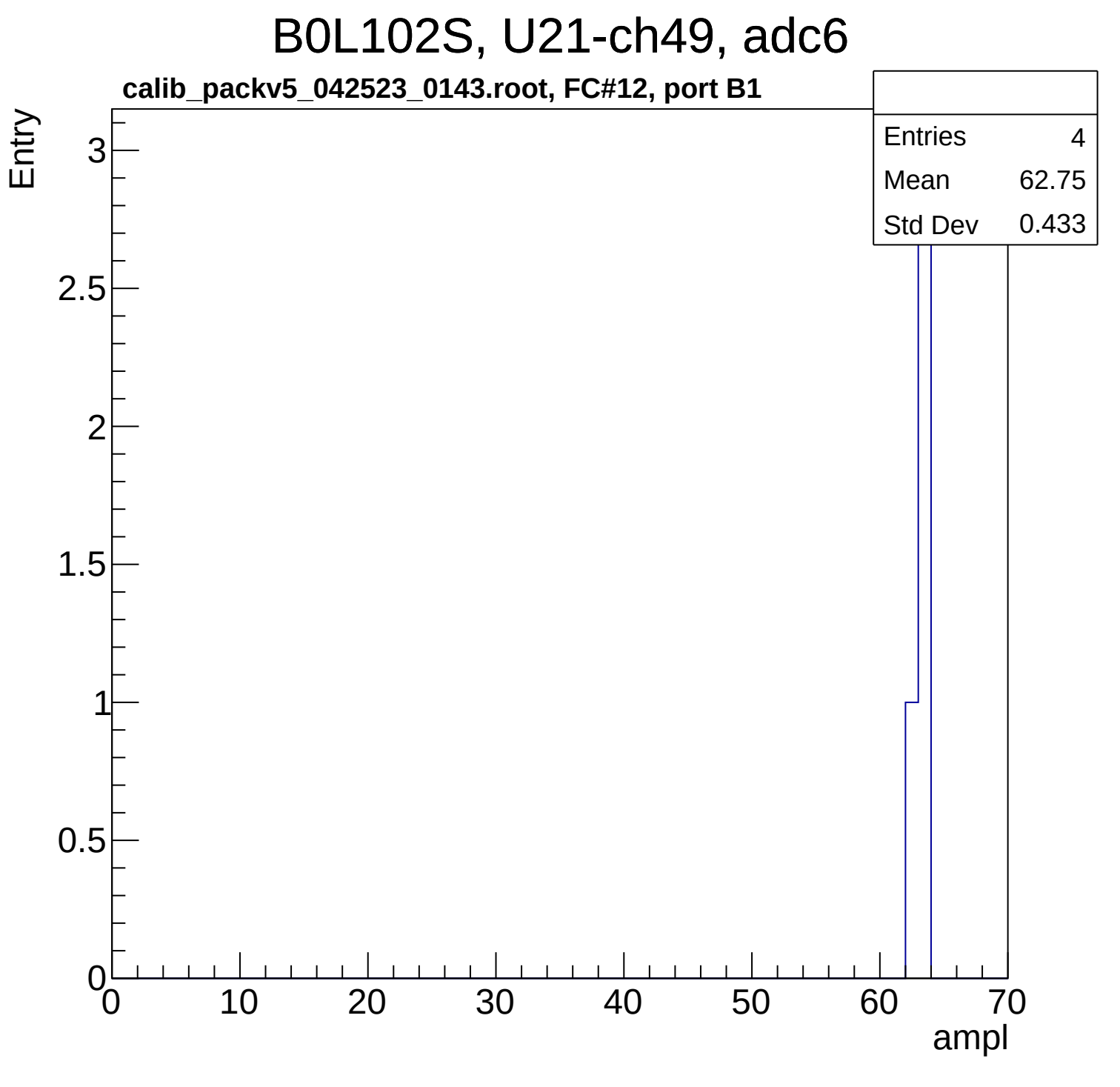
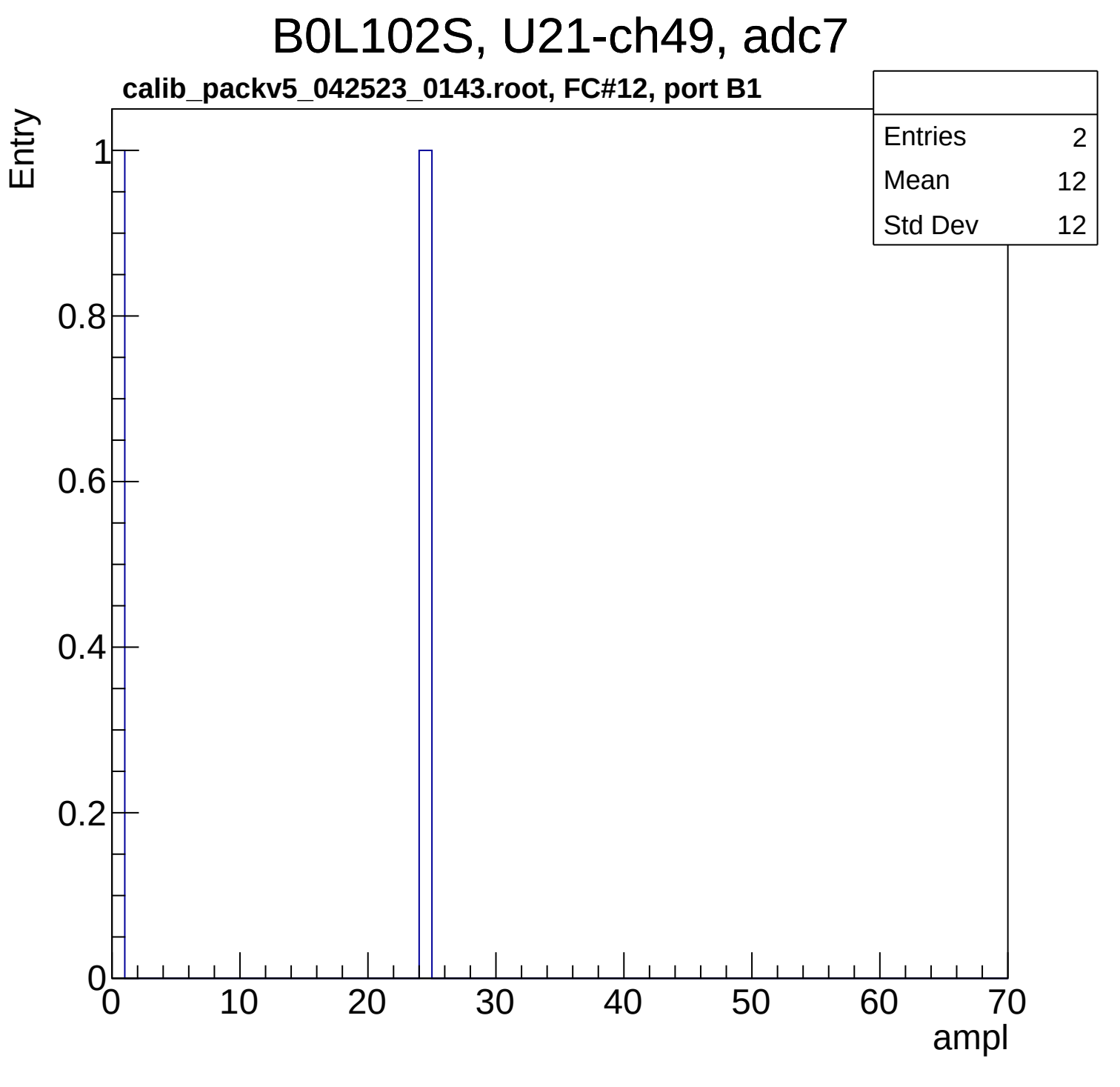

B0L102S, U21-ch49, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch49, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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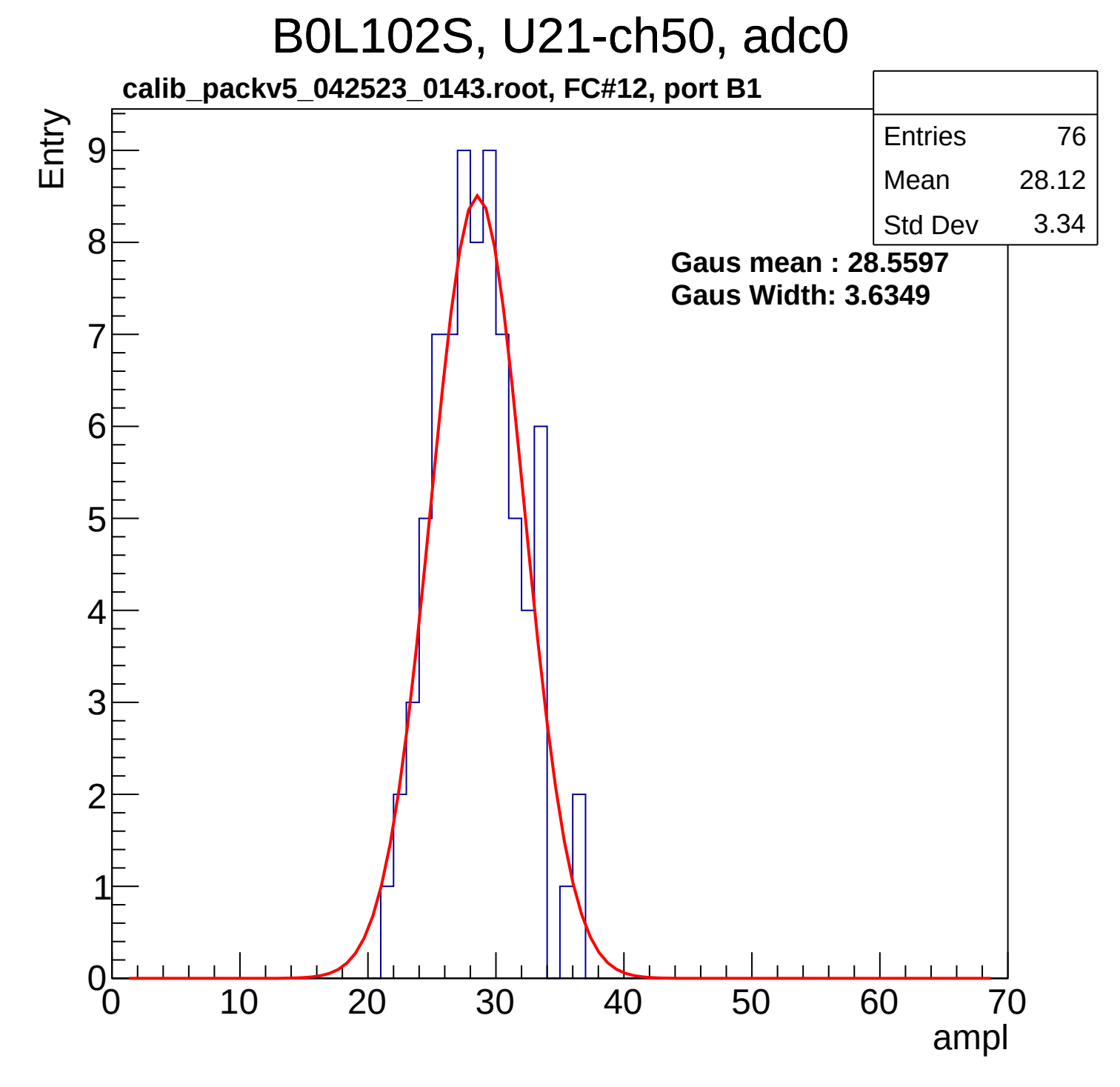
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch50, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 28.12
Std Dev 3.34
Gaus mean : 28.5597
Gaus Width: 3.6349
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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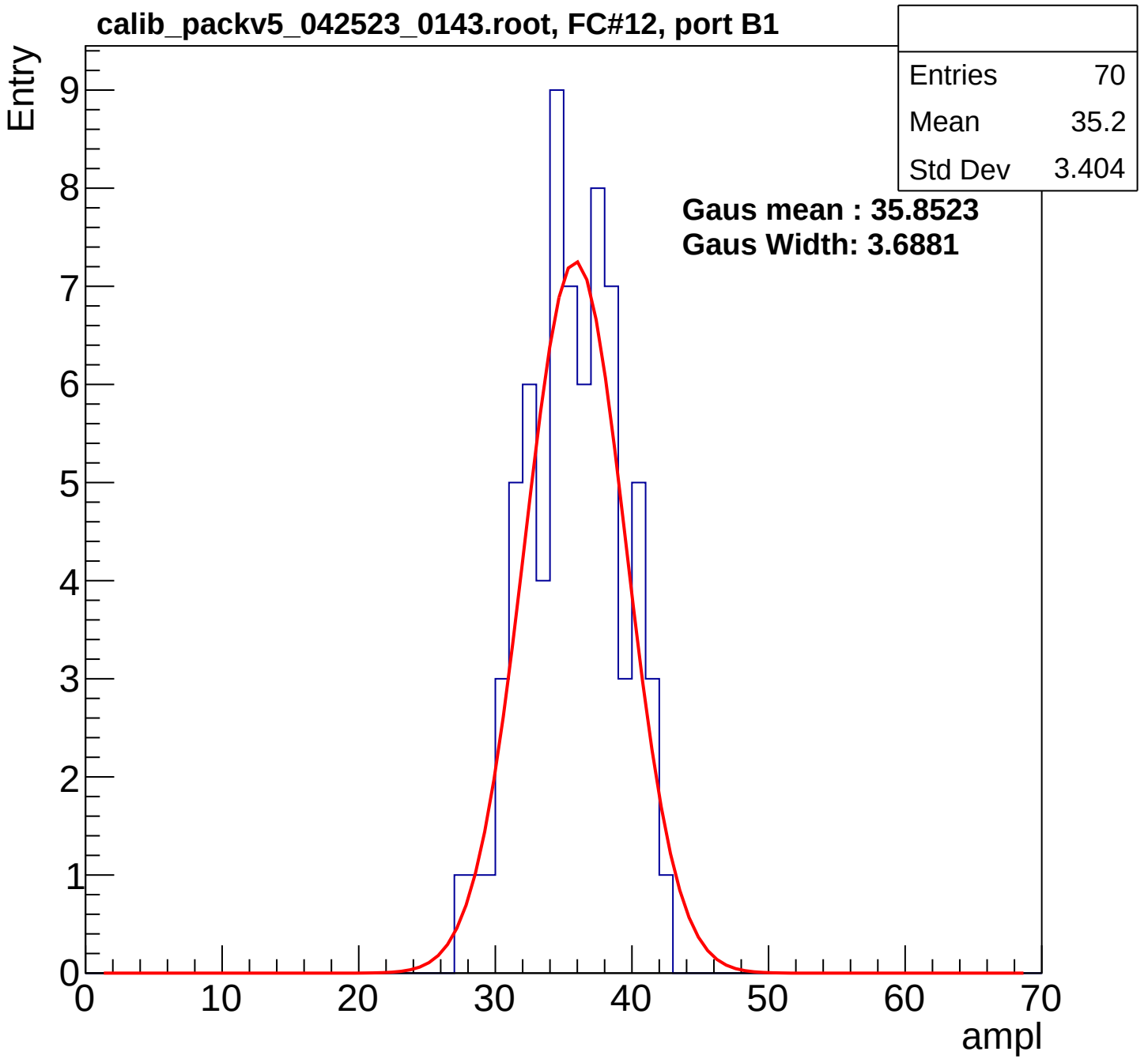
B0L102S, U21-ch50, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 35.2
Std Dev 3.404
Gaus mean : 35.8523
Gaus Width: 3.6881
Entry
ampl
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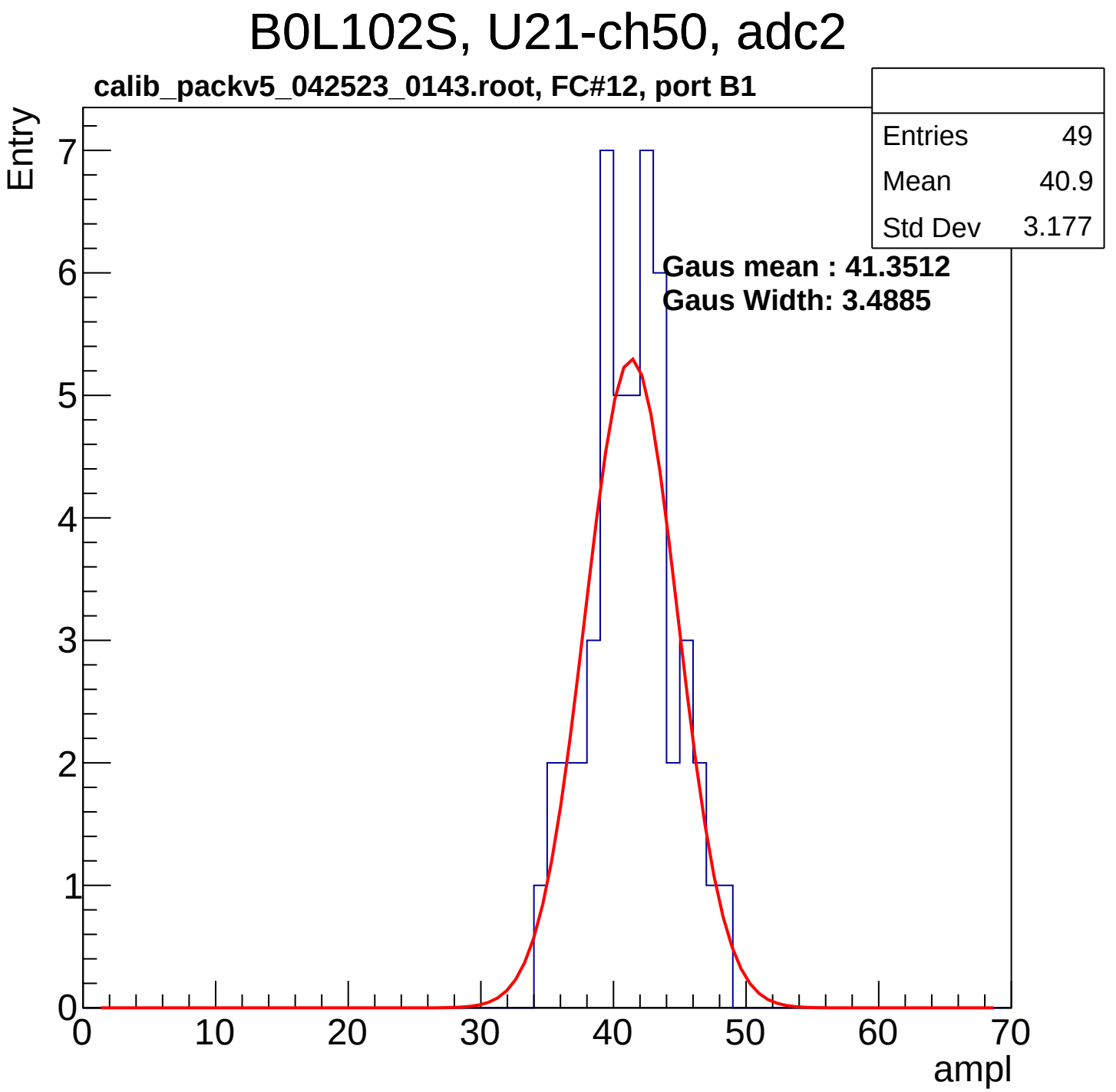
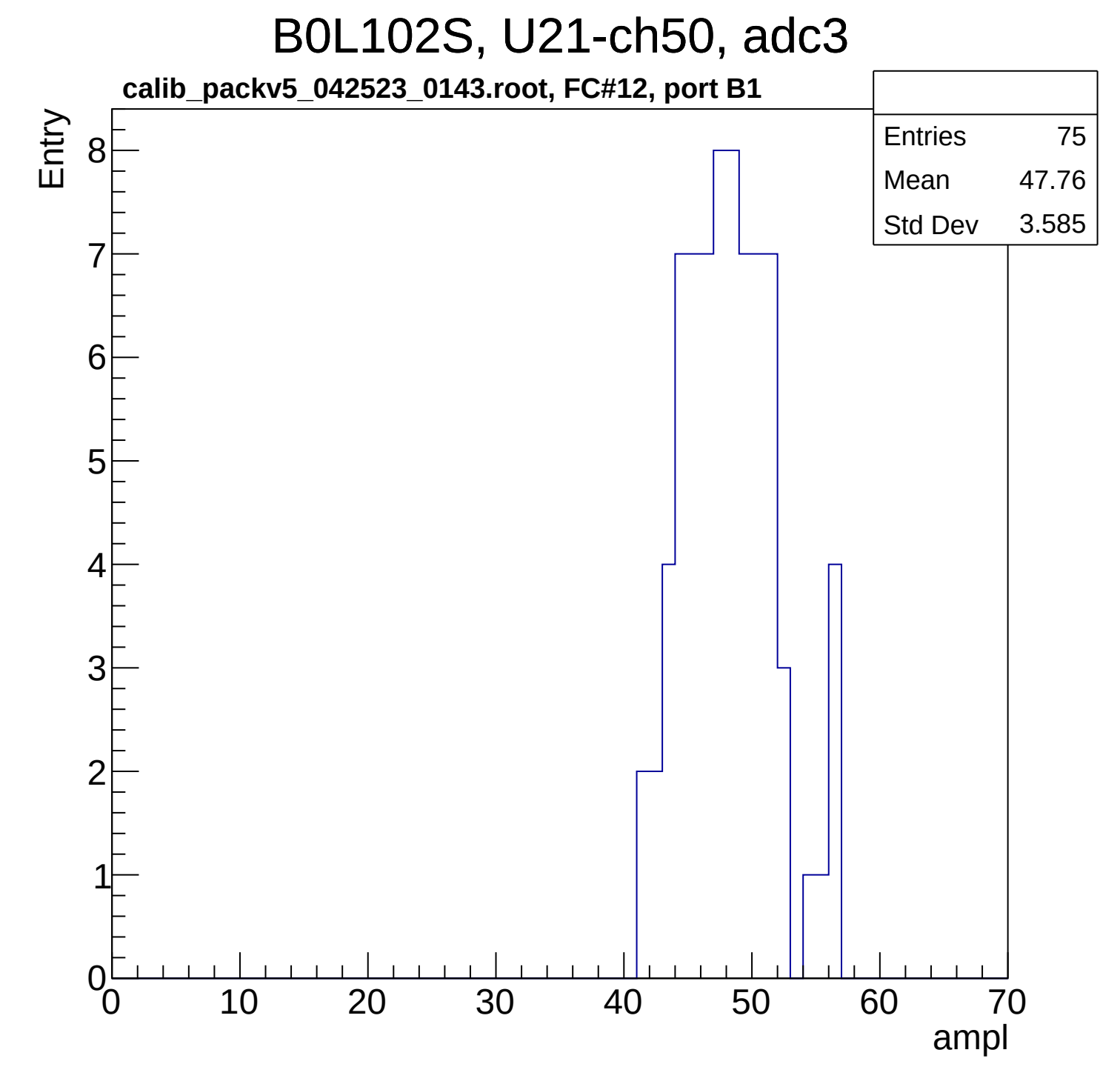
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch50, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 40.9
Std Dev 3.177
Gaus mean : 41.3512
Gaus Width: 3.4885
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch50, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 47.76
Std Dev 3.585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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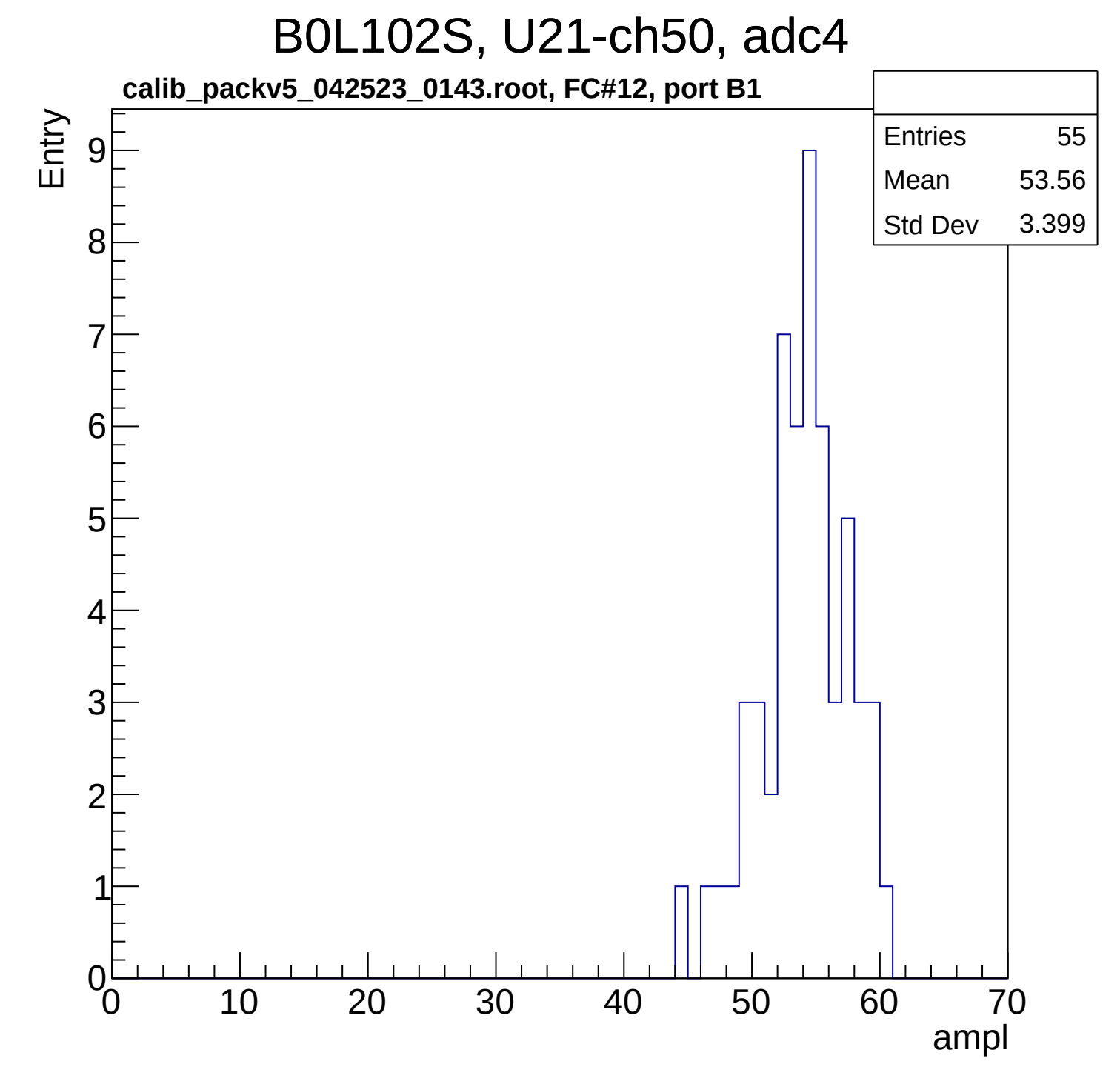
B0L102S, U21-ch50, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 53.56
Std Dev 3.399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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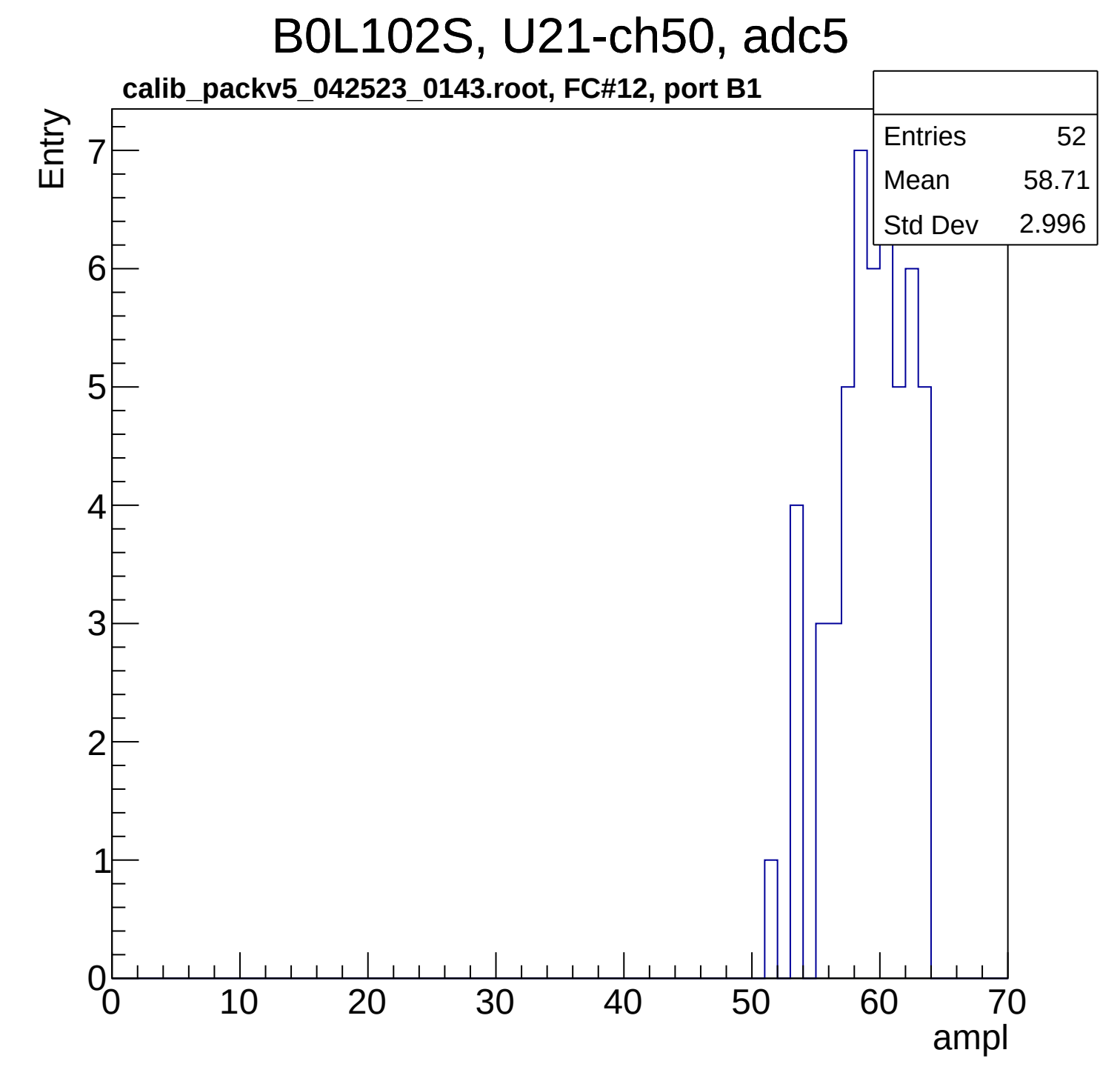
B0L102S, U21-ch50, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.71
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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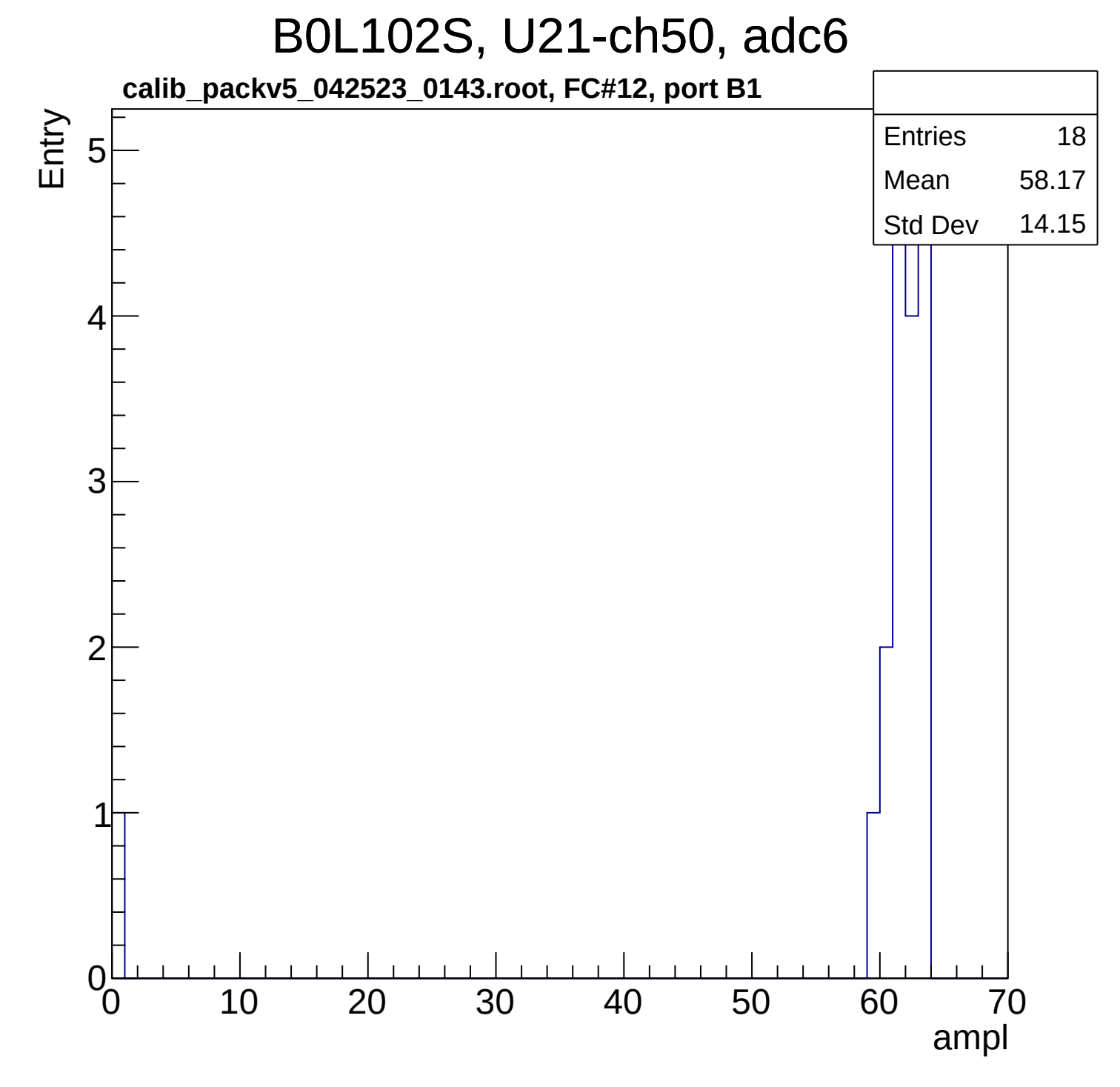
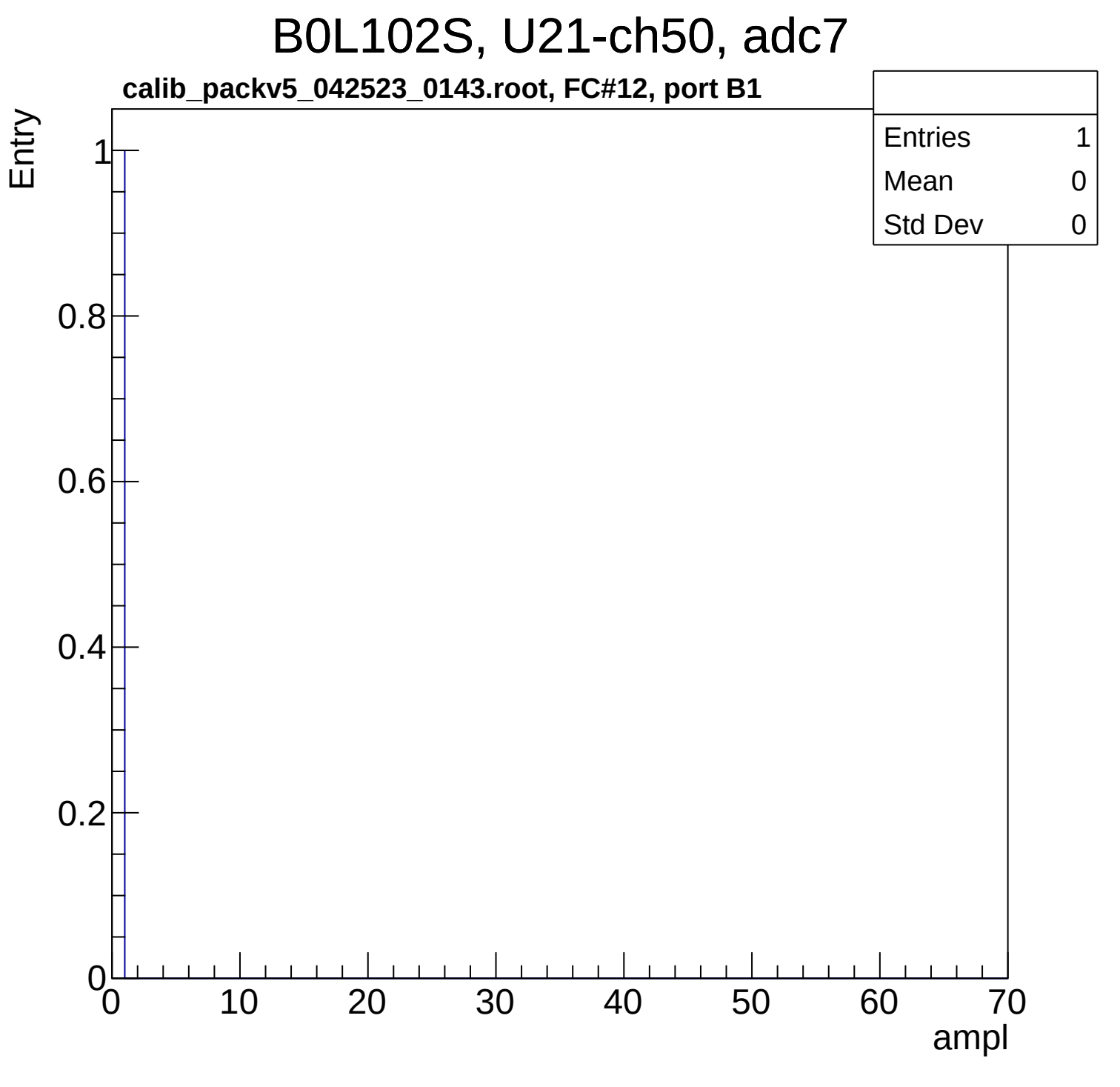

B0L102S, U21-ch50, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 18
Mean 58.17
Std Dev 14.15
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch50, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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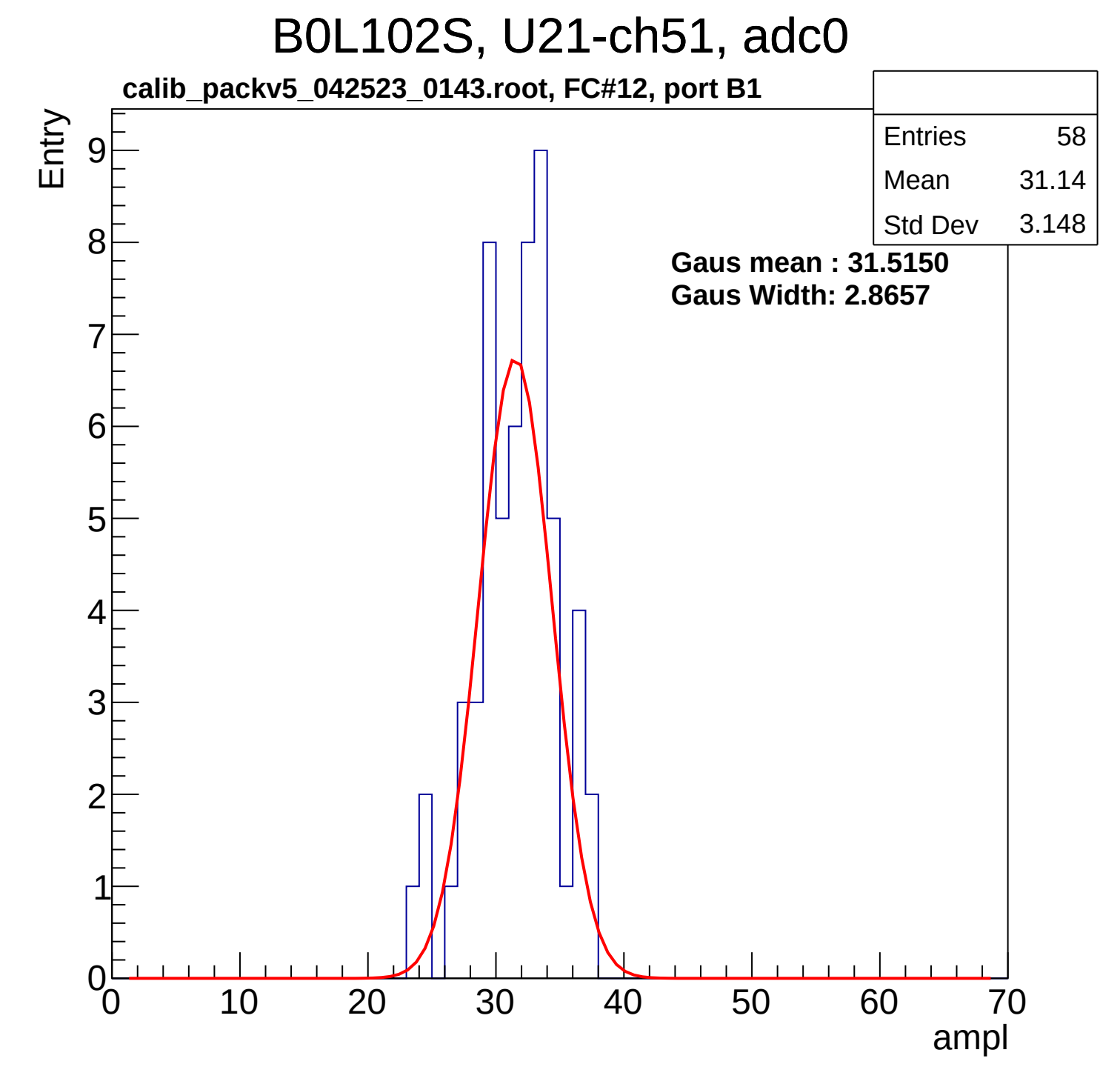
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch51, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 31.14
Std Dev 3.148
Gaus mean : 31.5150
Gaus Width: 2.8657
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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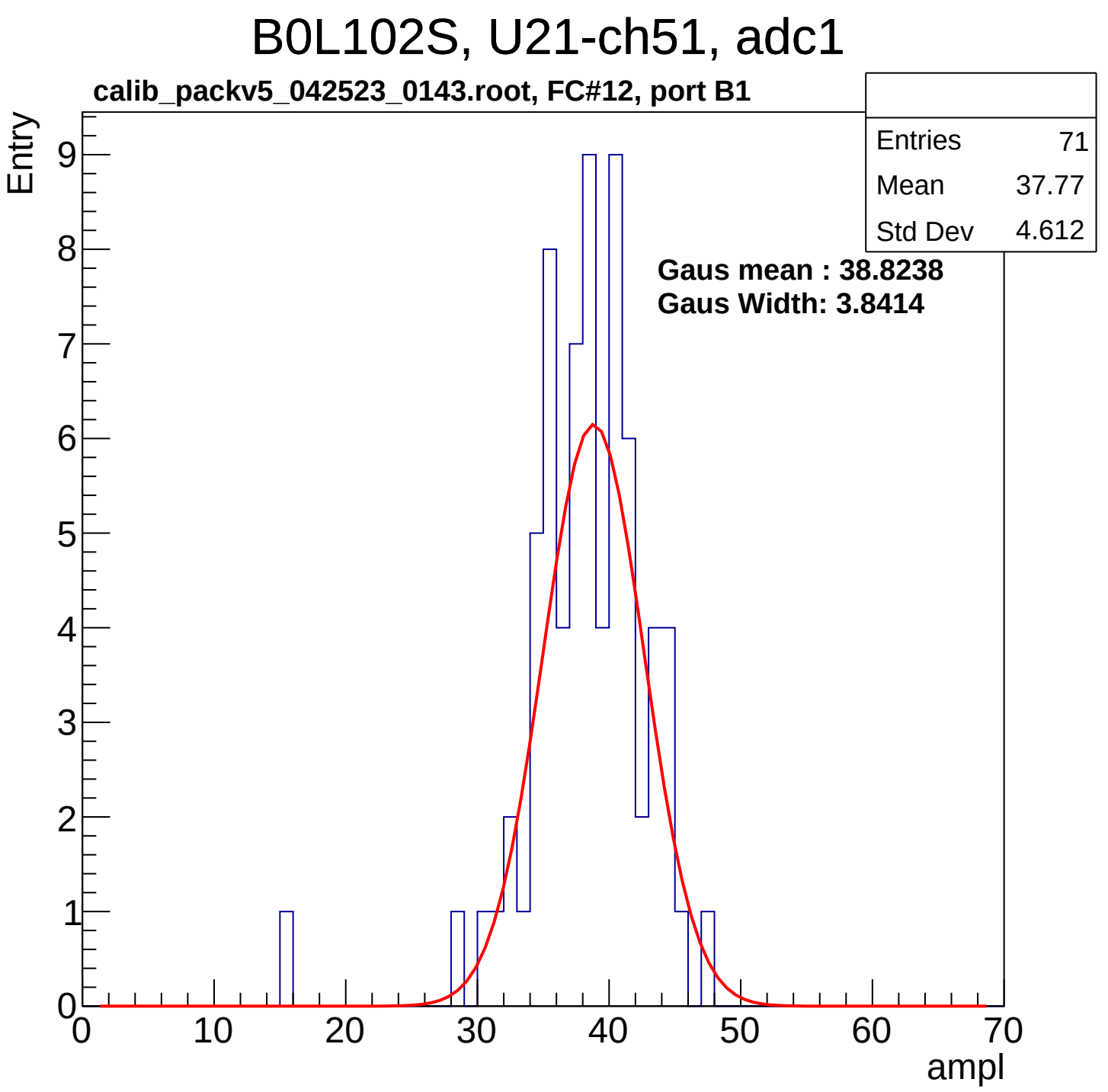
B0L102S, U21-ch51, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 37.77
Std Dev 4.612
Gaus mean : 38.8238
Gaus Width: 3.8414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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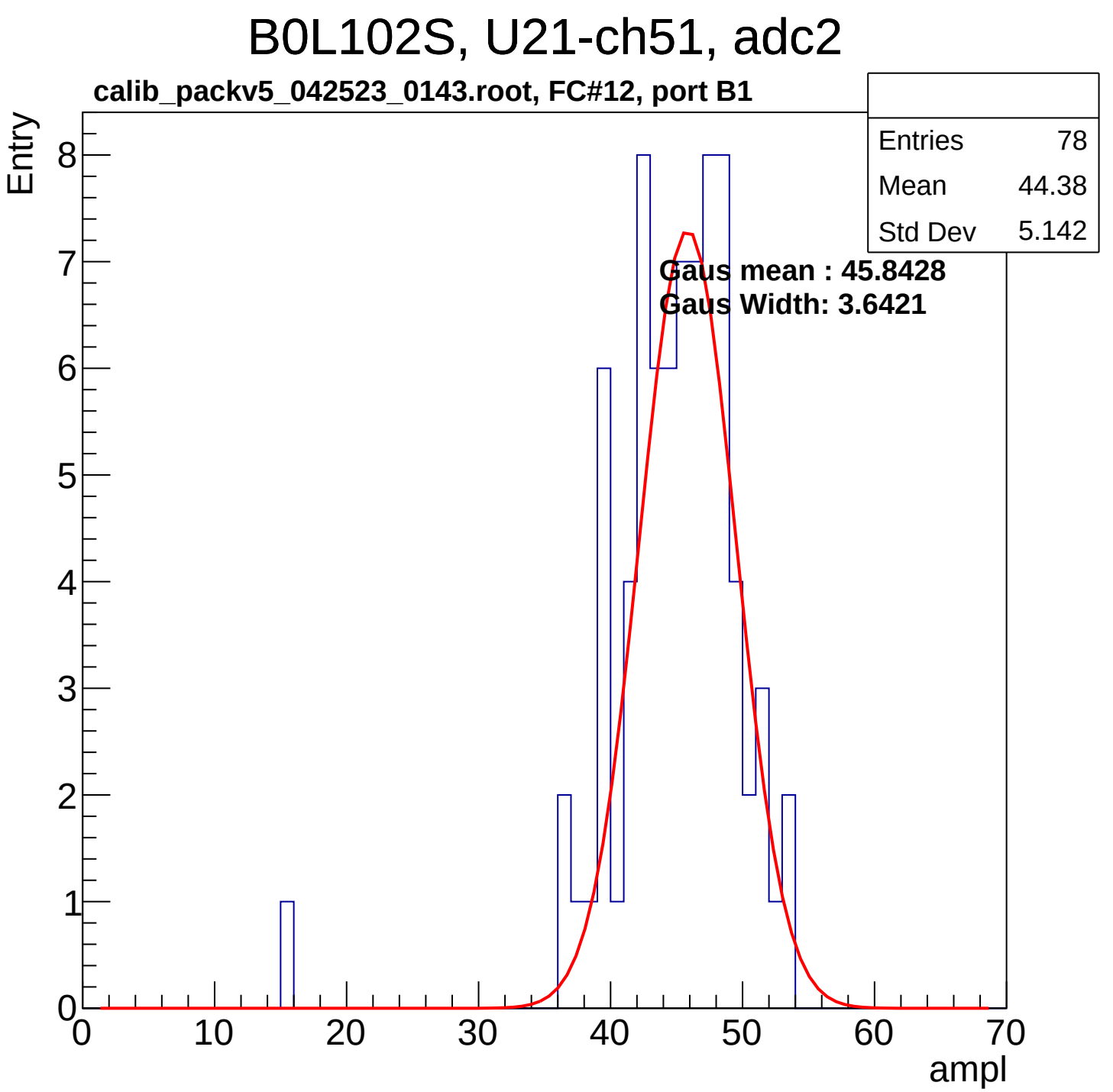
B0L102S, U21-ch51, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 44.38
Std Dev 5.142
Gaus mean : 45.8428
Gaus Width: 3.6421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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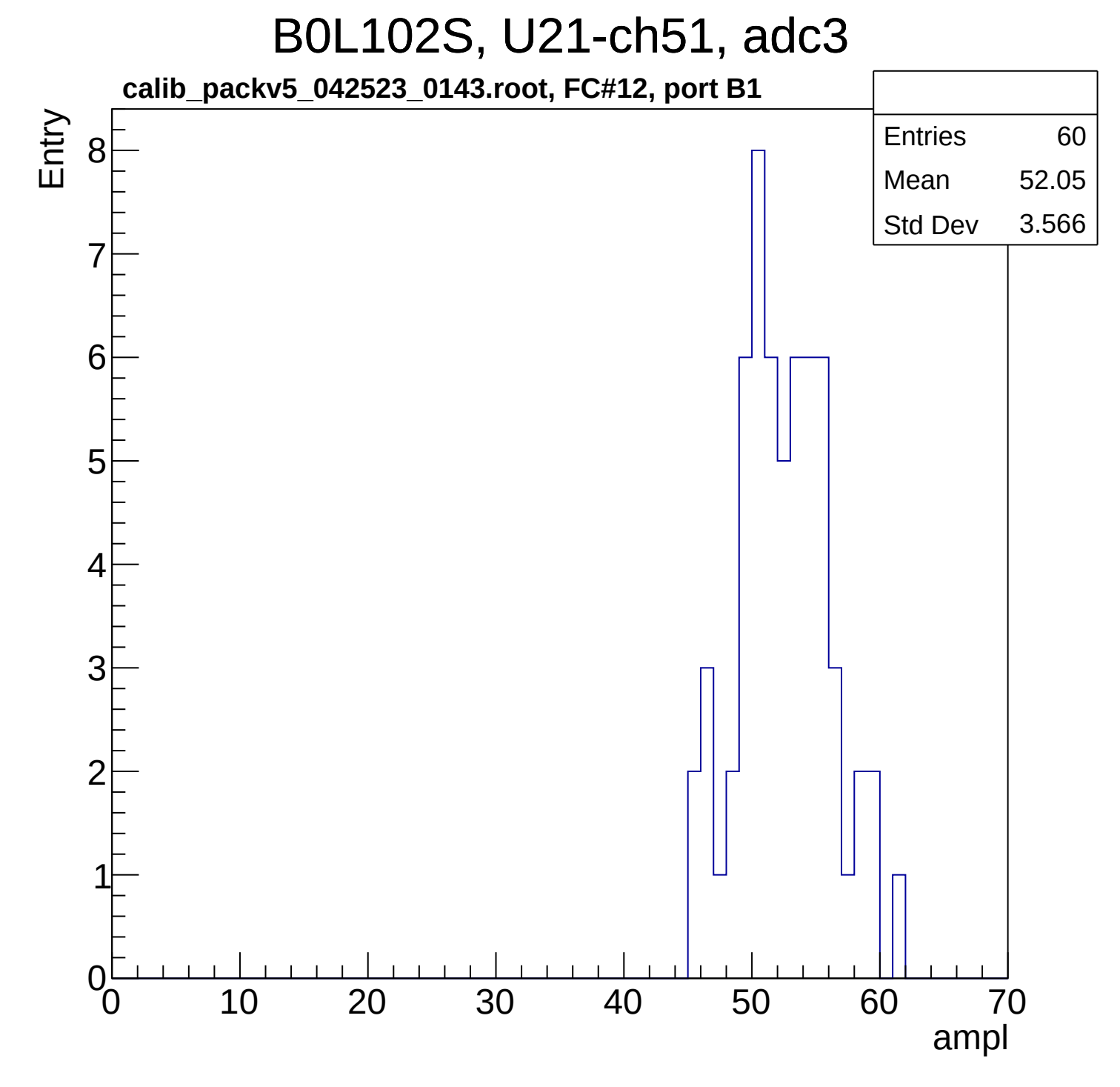
B0L102S, U21-ch51, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 52.05
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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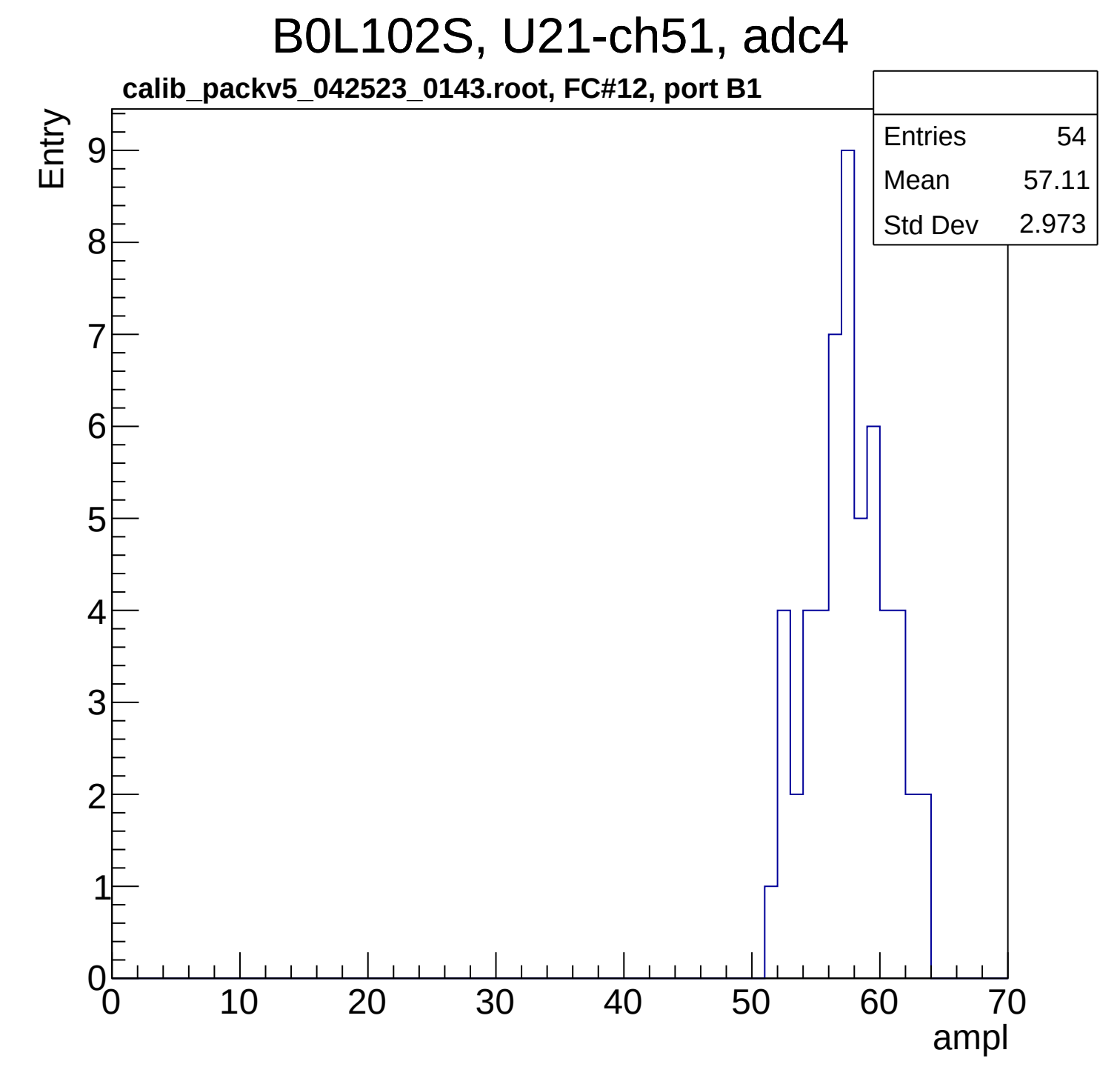
B0L102S, U21-ch51, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 57.11
Std Dev 2.973
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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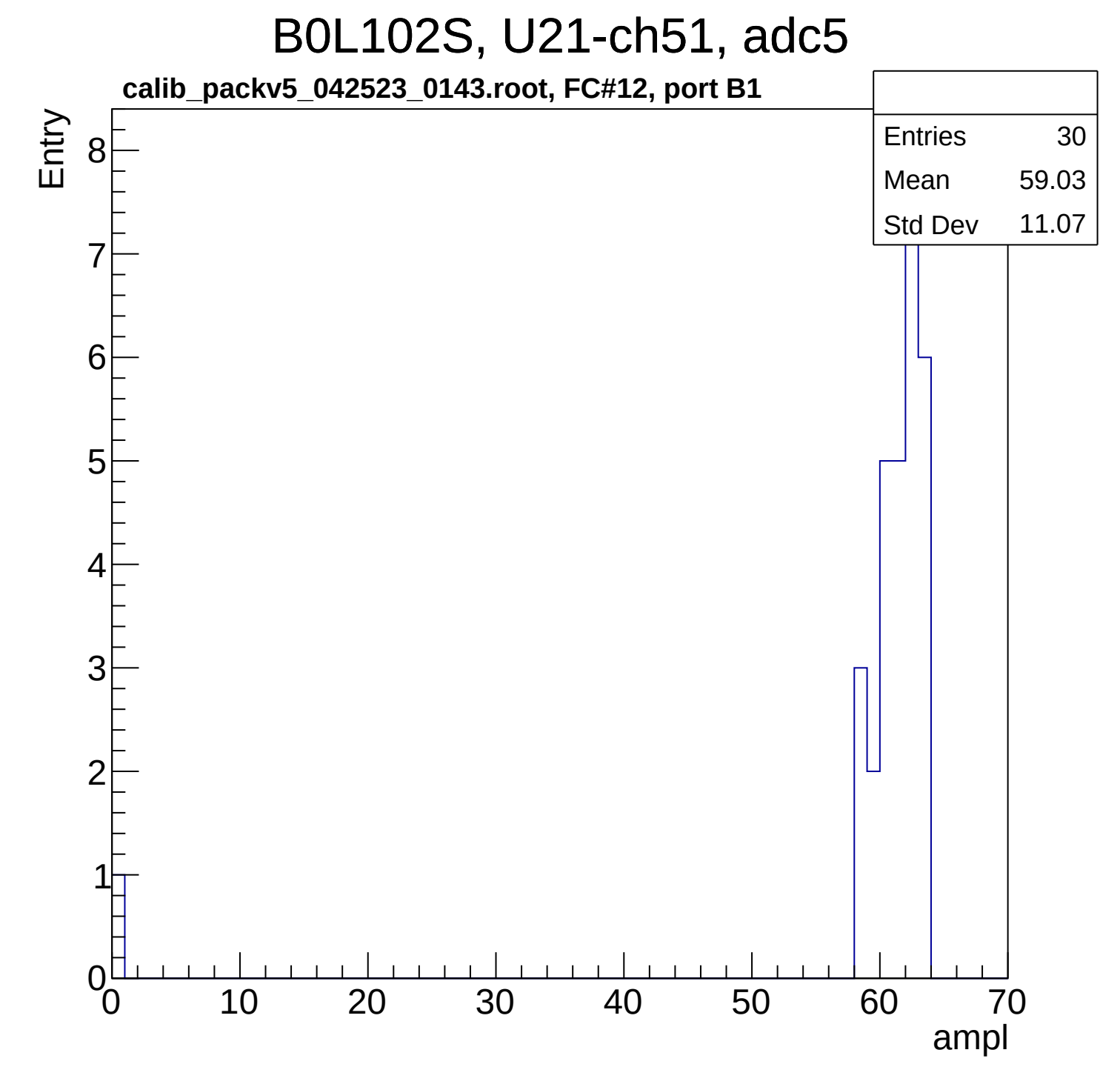
B0L102S, U21-ch51, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 30
Mean 59.03
Std Dev 11.07
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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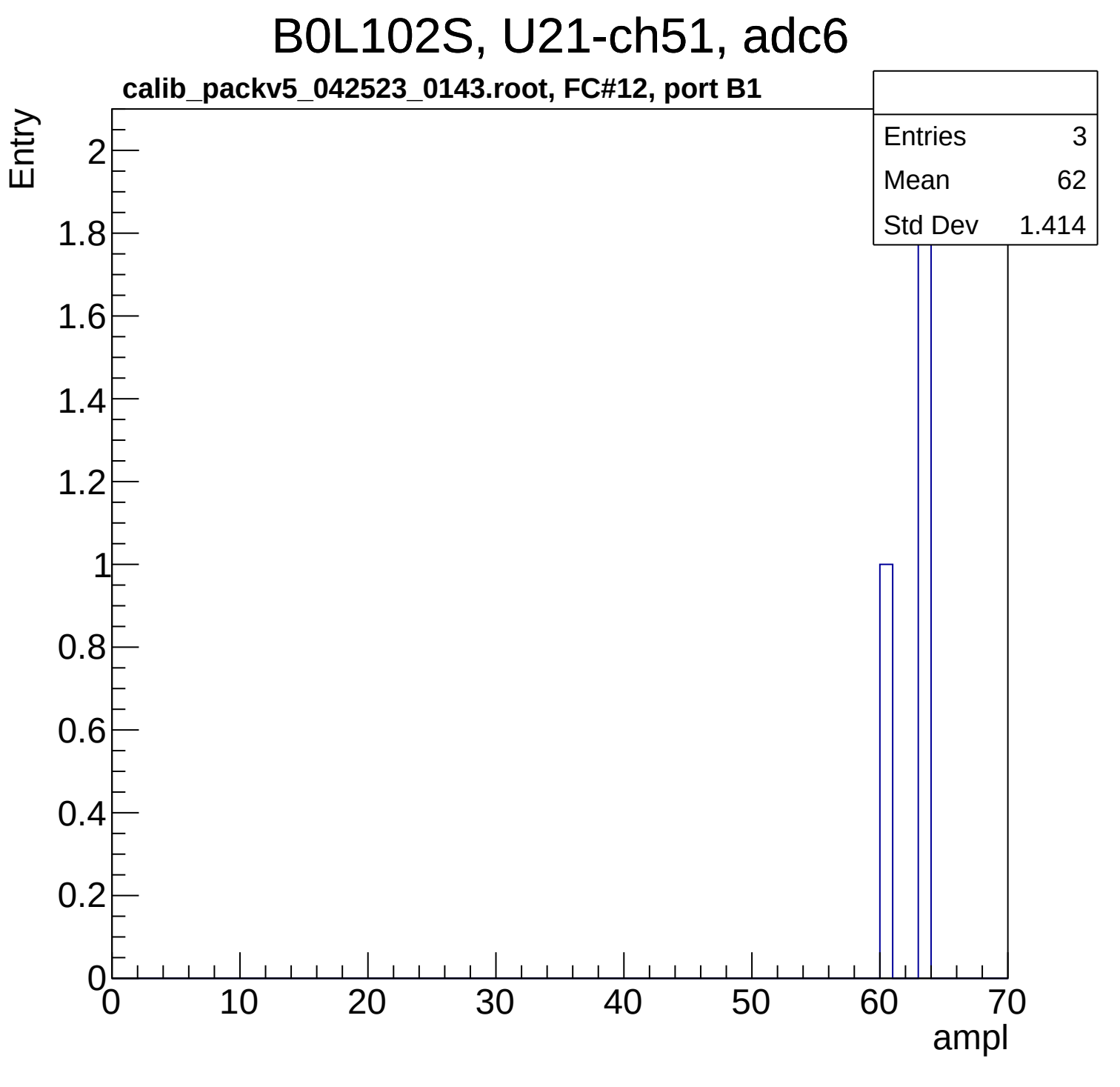
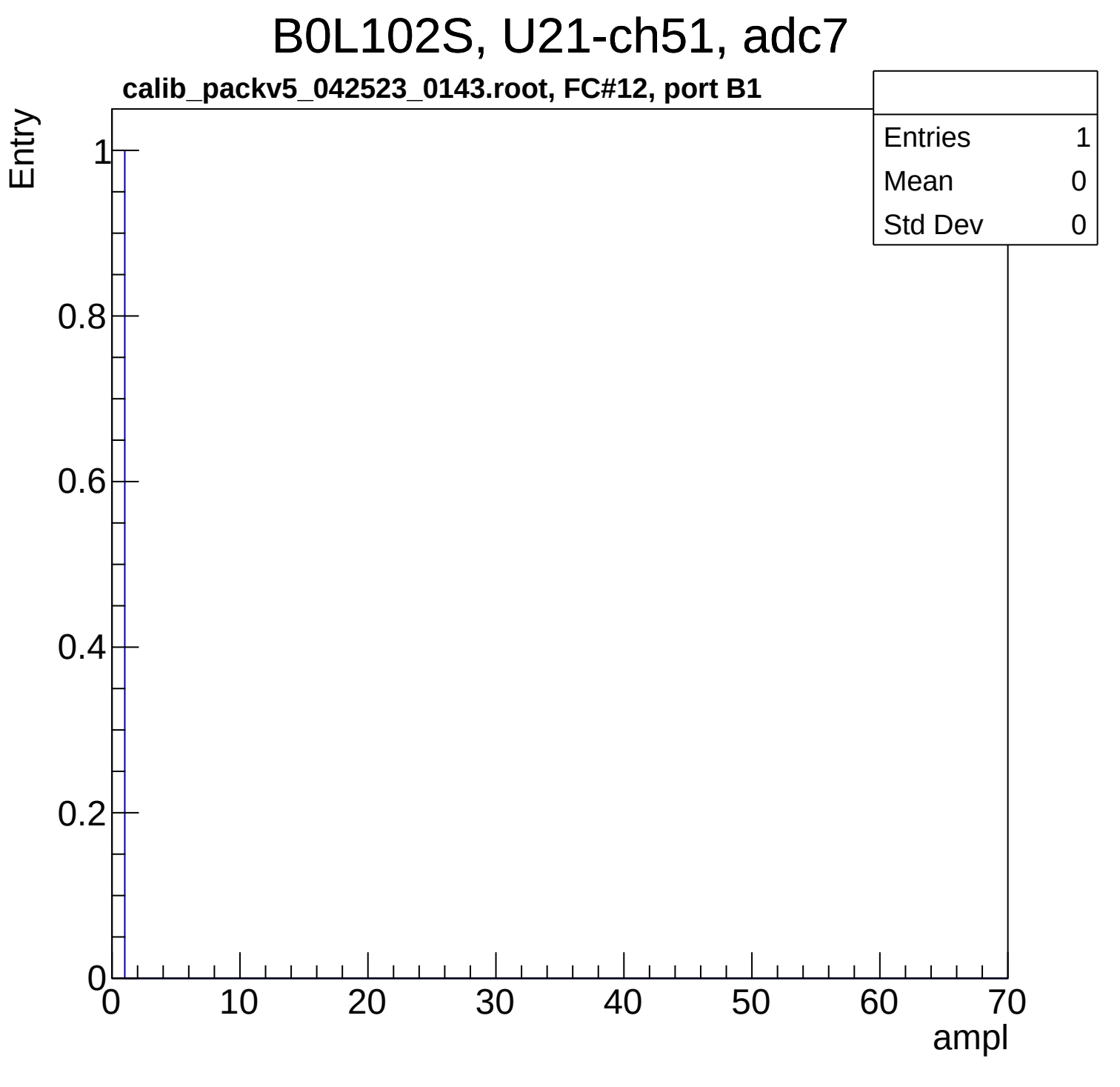
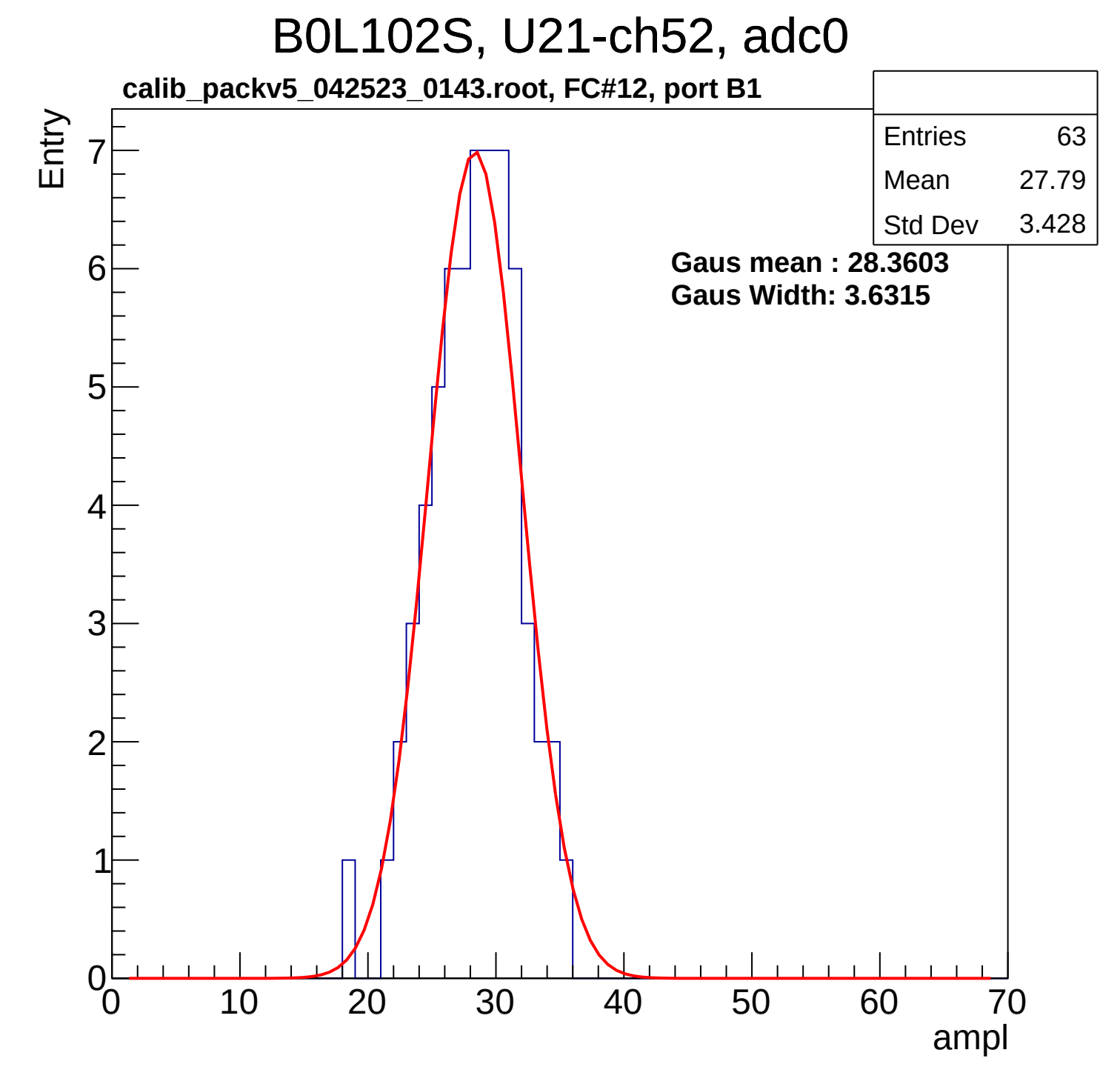
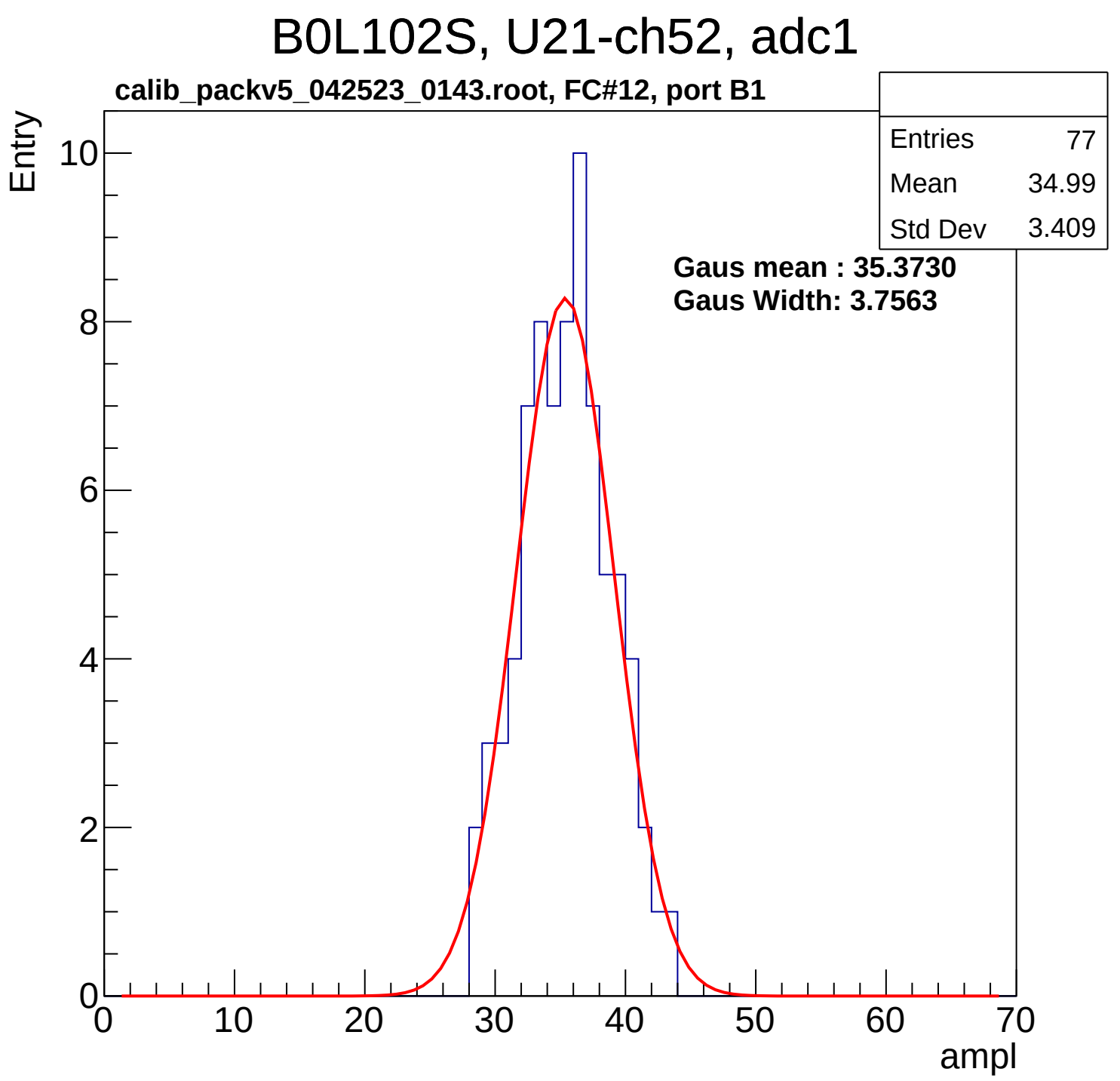
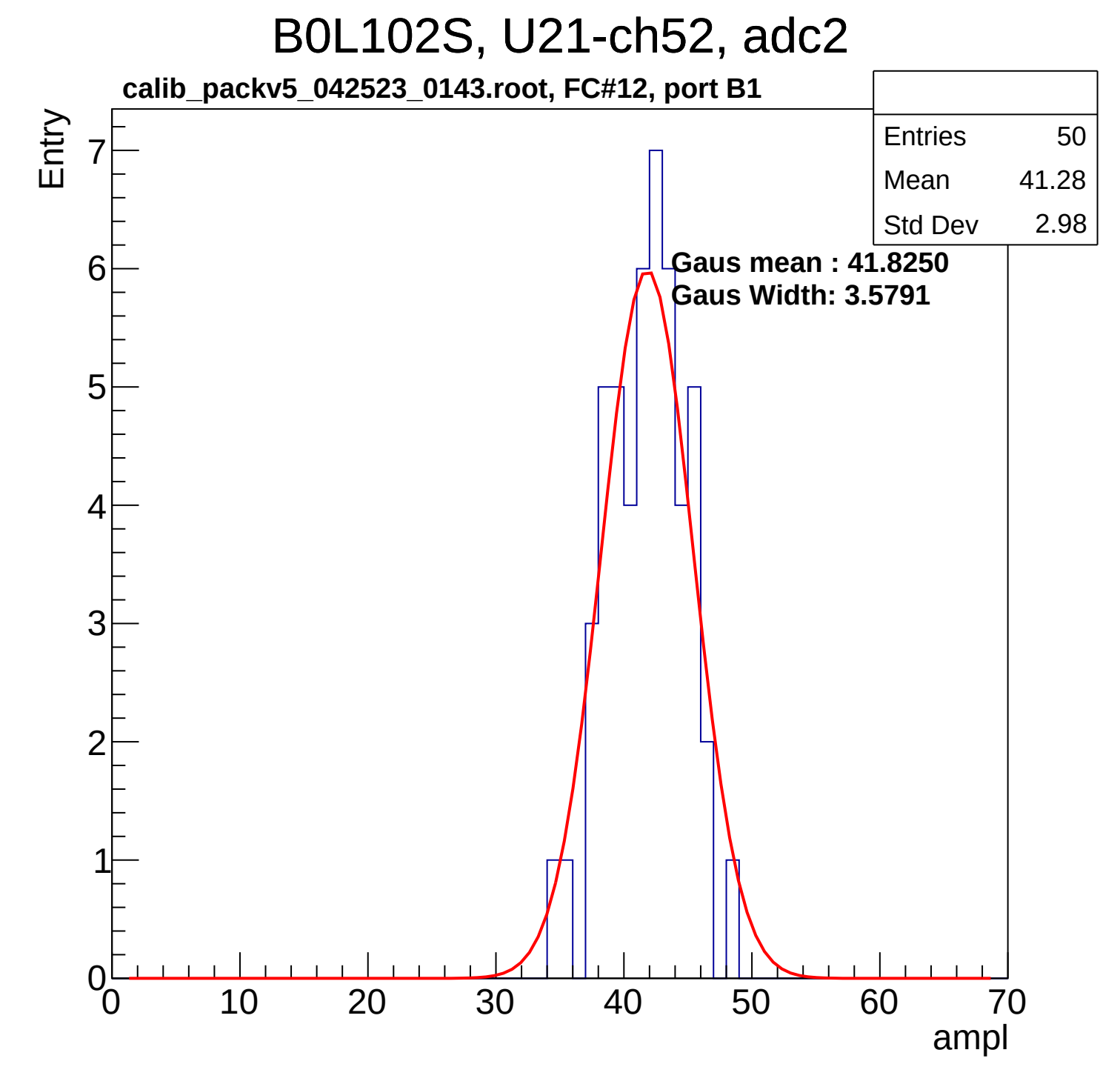

B0L102S, U21-ch51, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch51, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch52, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 27.79
Std Dev 3.428
Gaus mean : 28.3603
Gaus Width: 3.6315
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch52, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 34.99
Std Dev 3.409
Gaus mean : 35.3730
Gaus Width: 3.7563
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch52, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 41.28
Std Dev 2.98
Gaus mean : 41.8250
Gaus Width: 3.5791
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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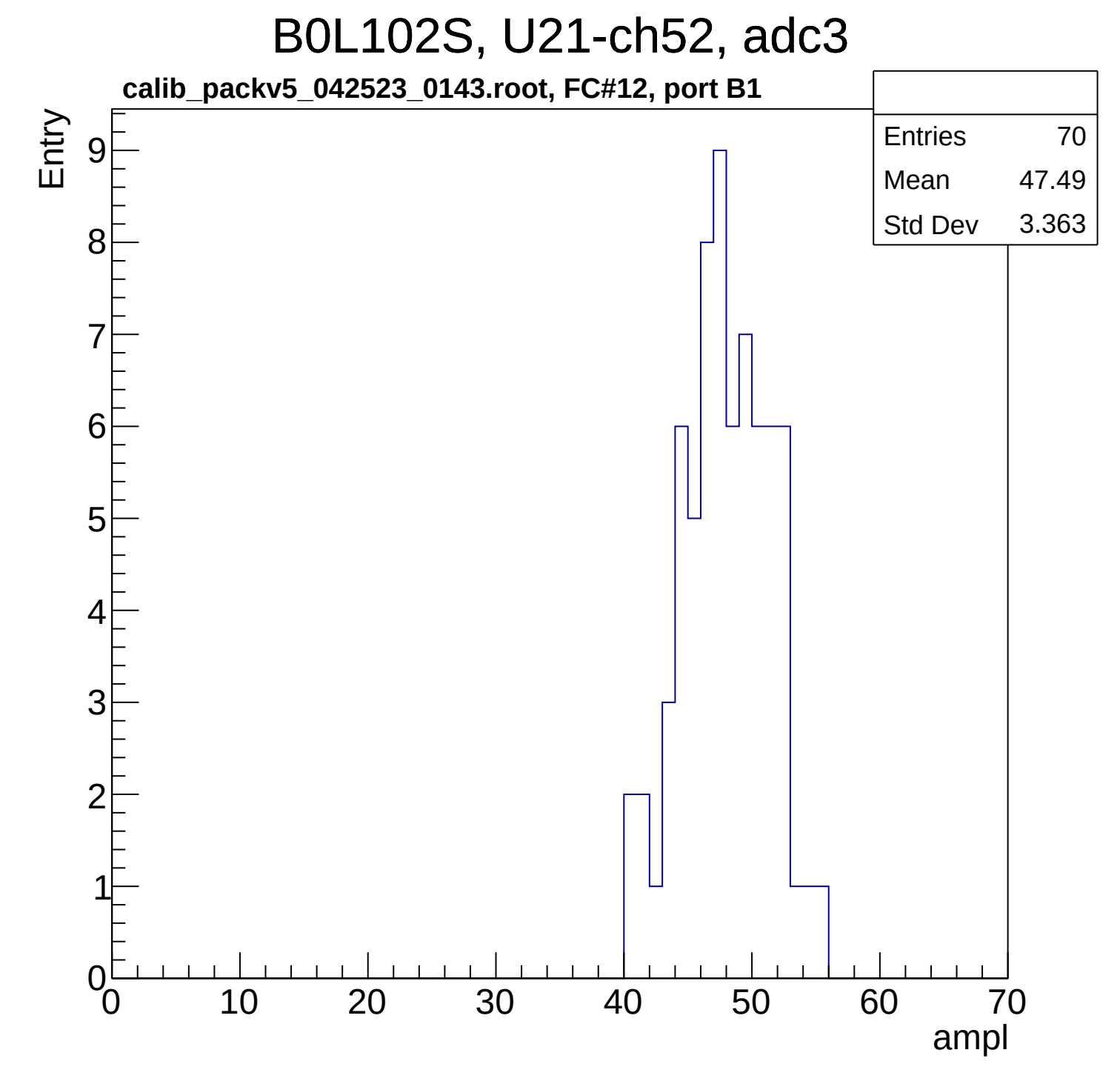
B0L102S, U21-ch52, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 47.49
Std Dev 3.363
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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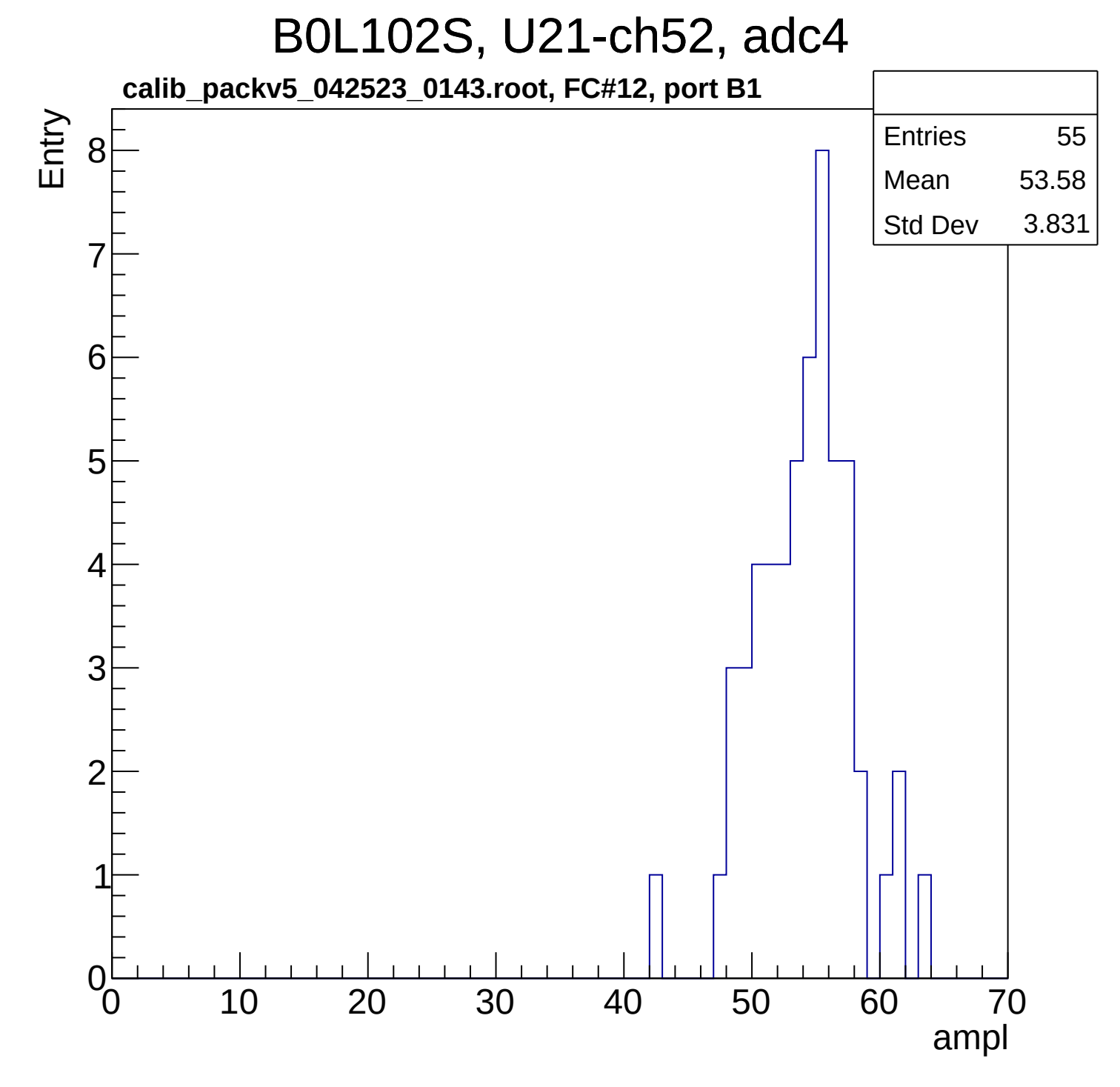
B0L102S, U21-ch52, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 53.58
Std Dev 3.831
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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40
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60
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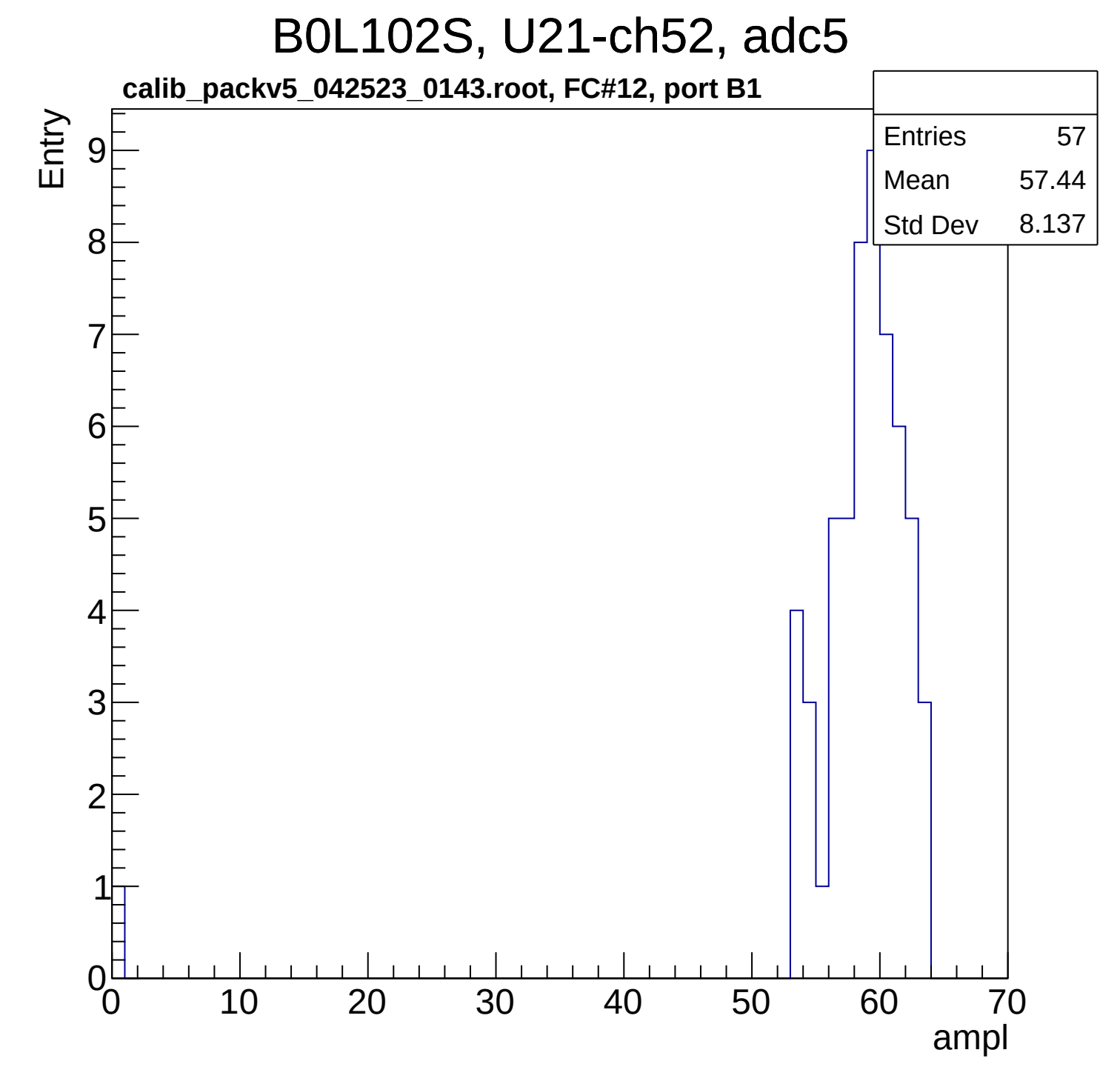
B0L102S, U21-ch52, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 57.44
Std Dev 8.137
Entry
ampl
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2
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4
5
6
7
8
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0
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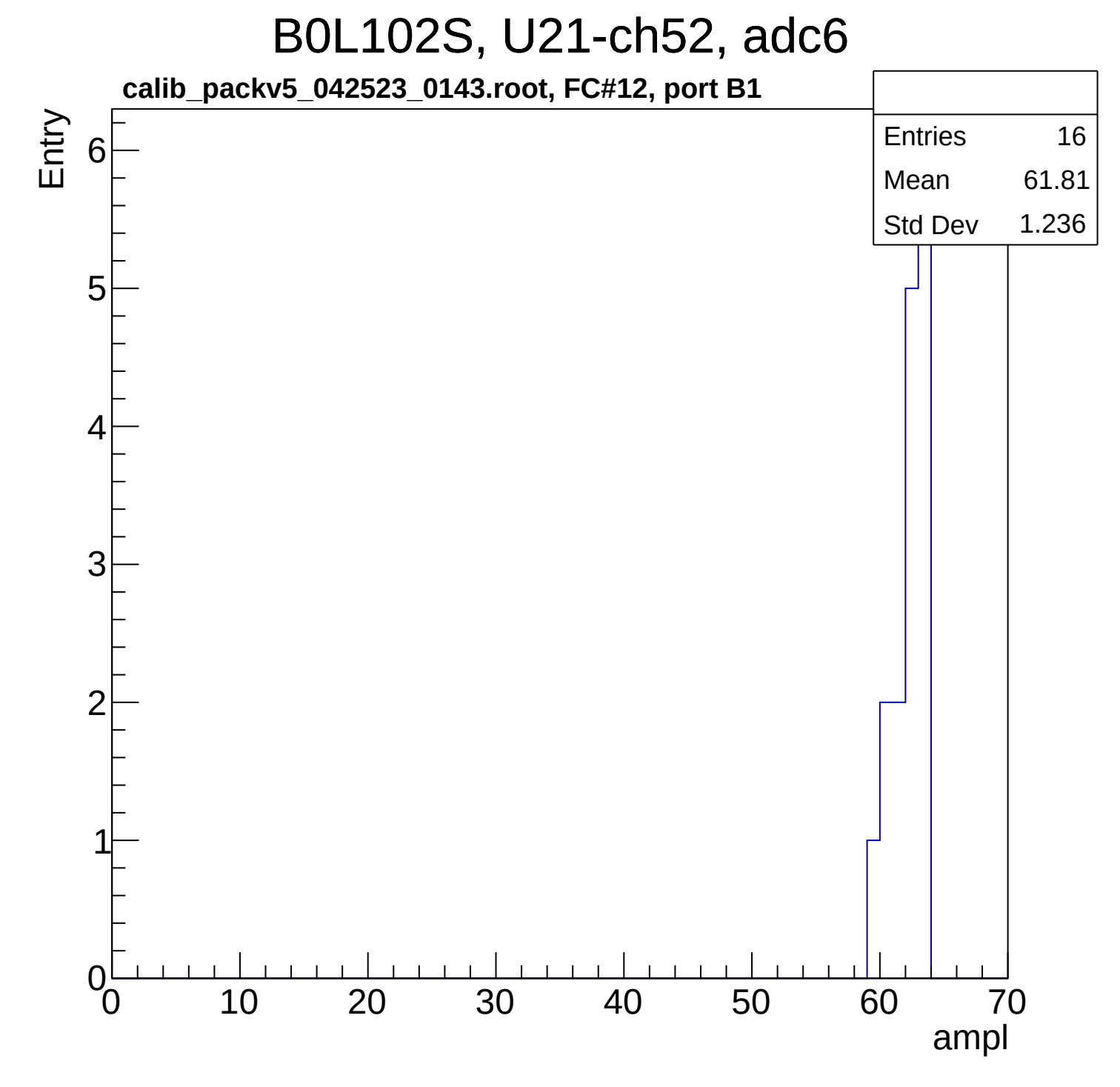
B0L102S, U21-ch52, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 61.81
Std Dev 1.236
Entry
ampl
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2
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0
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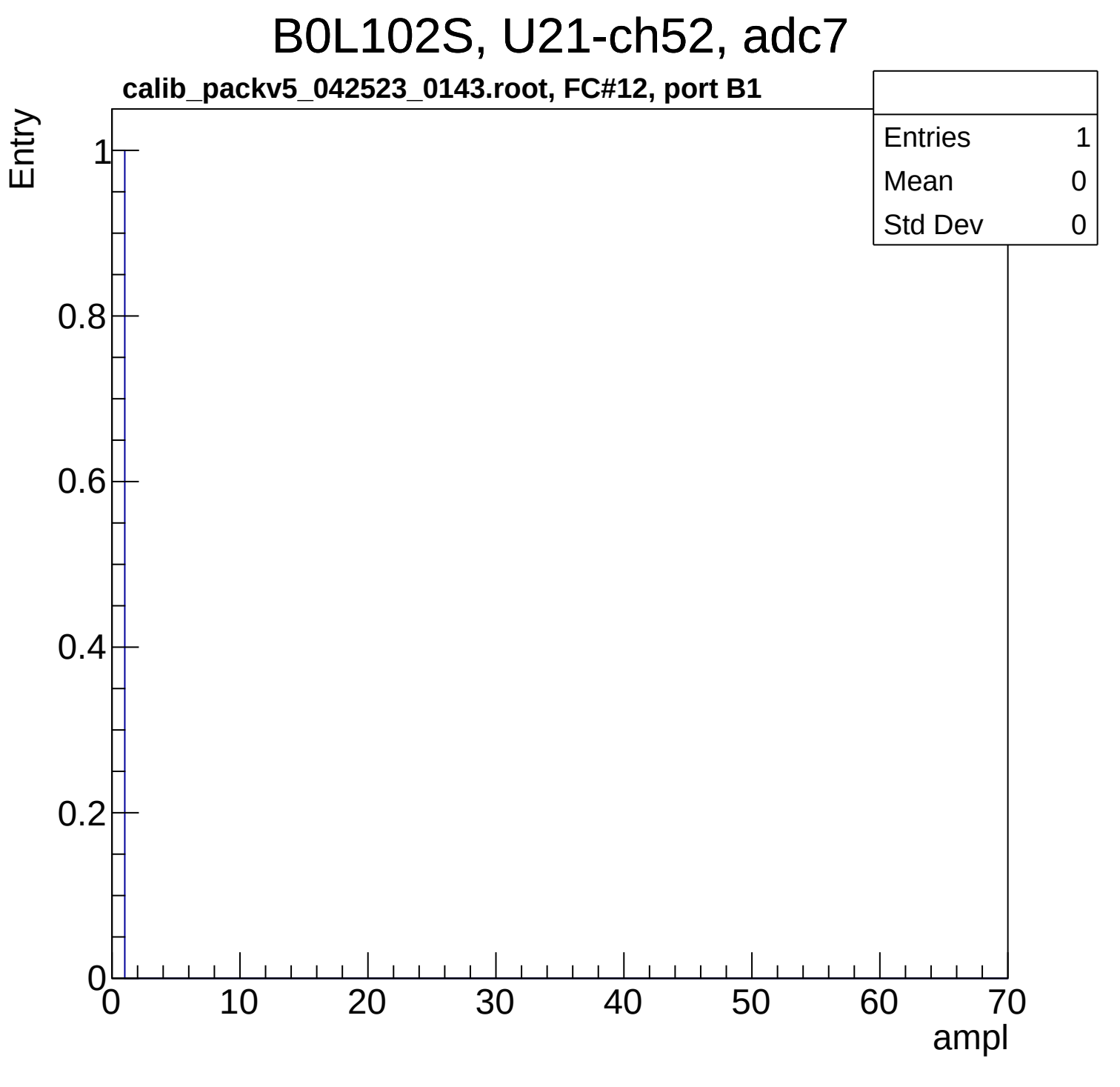
B0L102S, U21-ch52, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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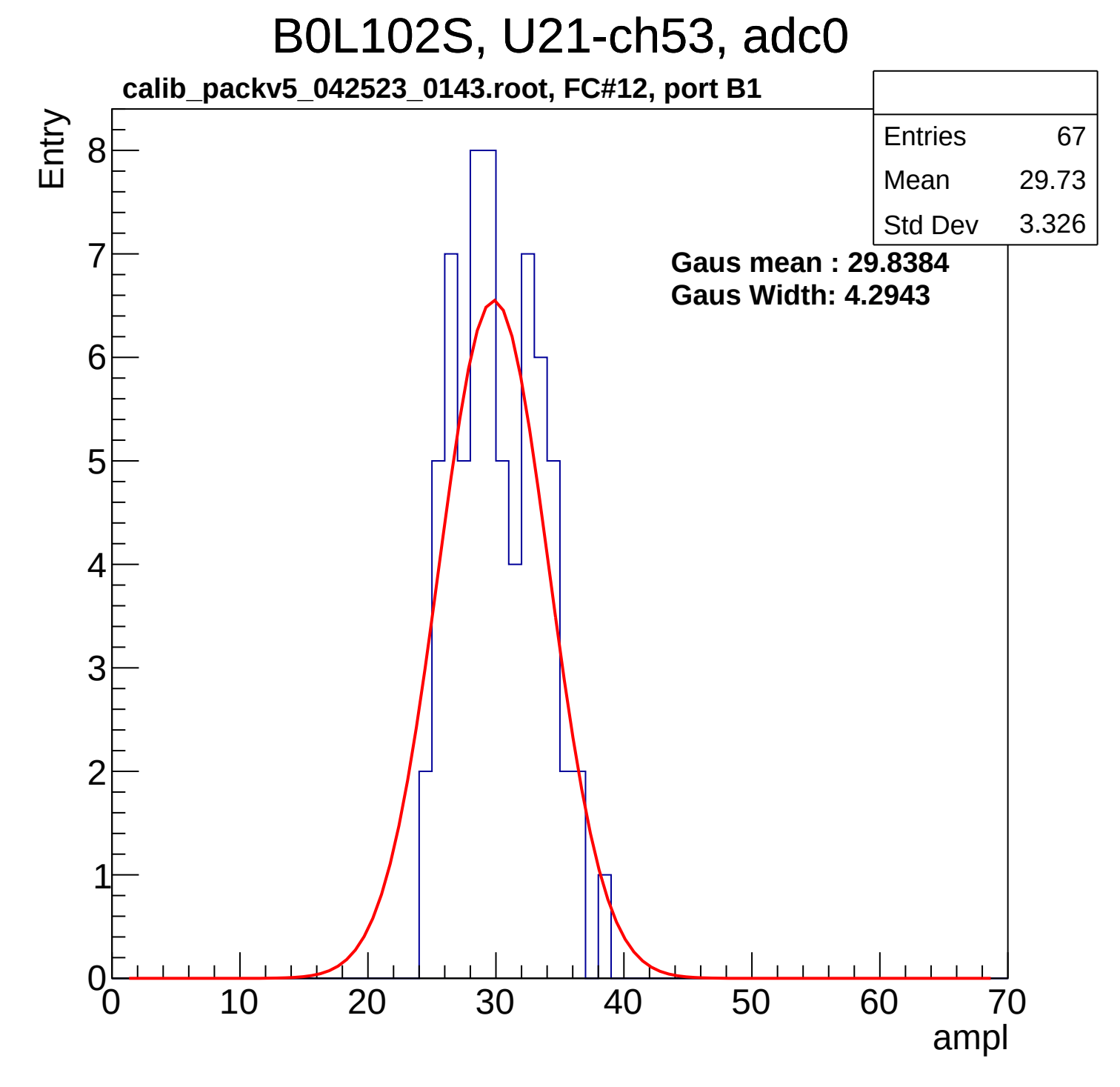
50
60
70
ampl


B0L102S, U21-ch53, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.73
Std Dev 3.326
Gaus mean : 29.8384
Gaus Width: 4.2943
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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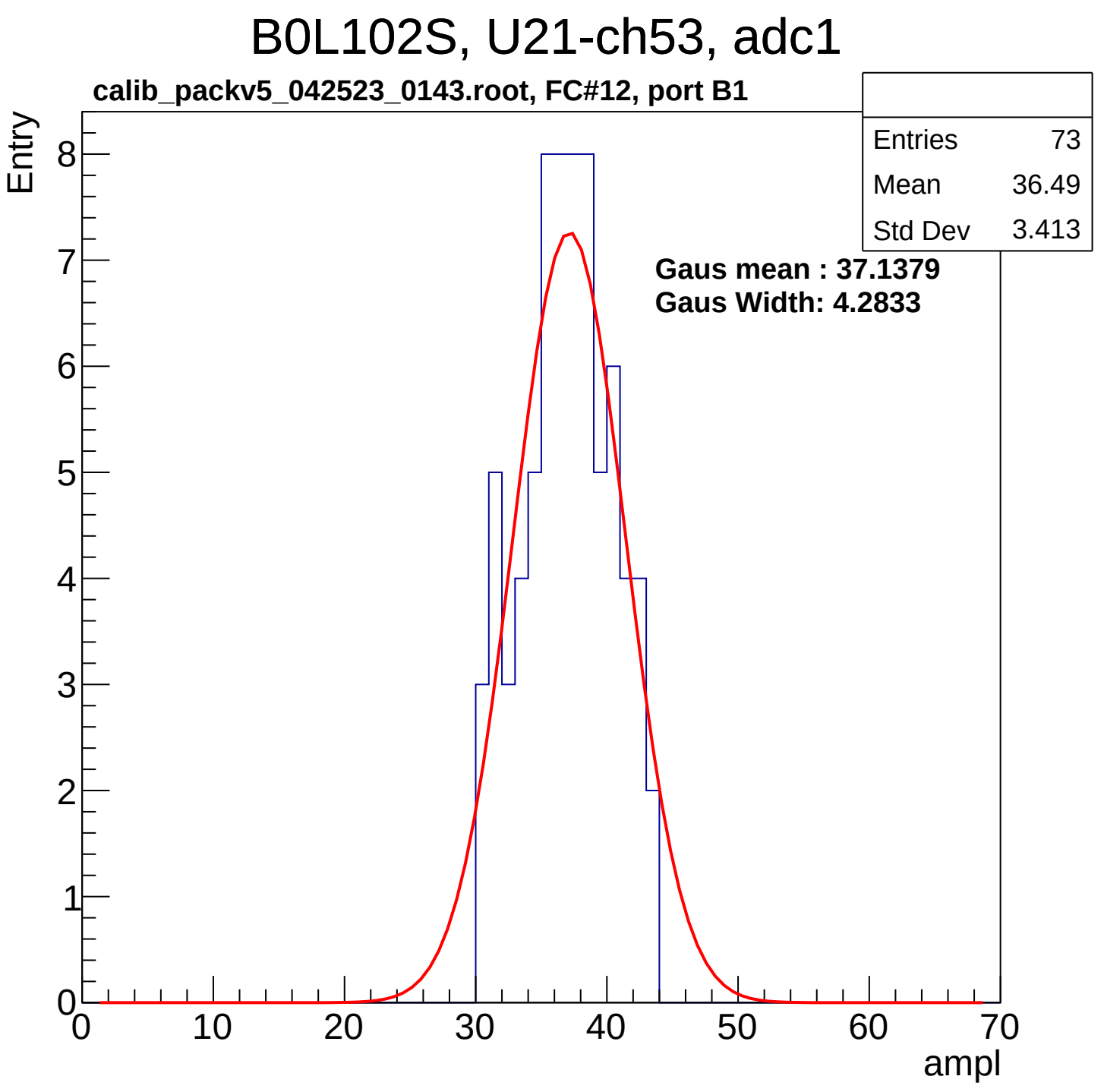
40
50
60
70


B0L102S, U21-ch53, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 36.49
Std Dev 3.413
Gaus mean : 37.1379
Gaus Width: 4.2833
Entry
ampl
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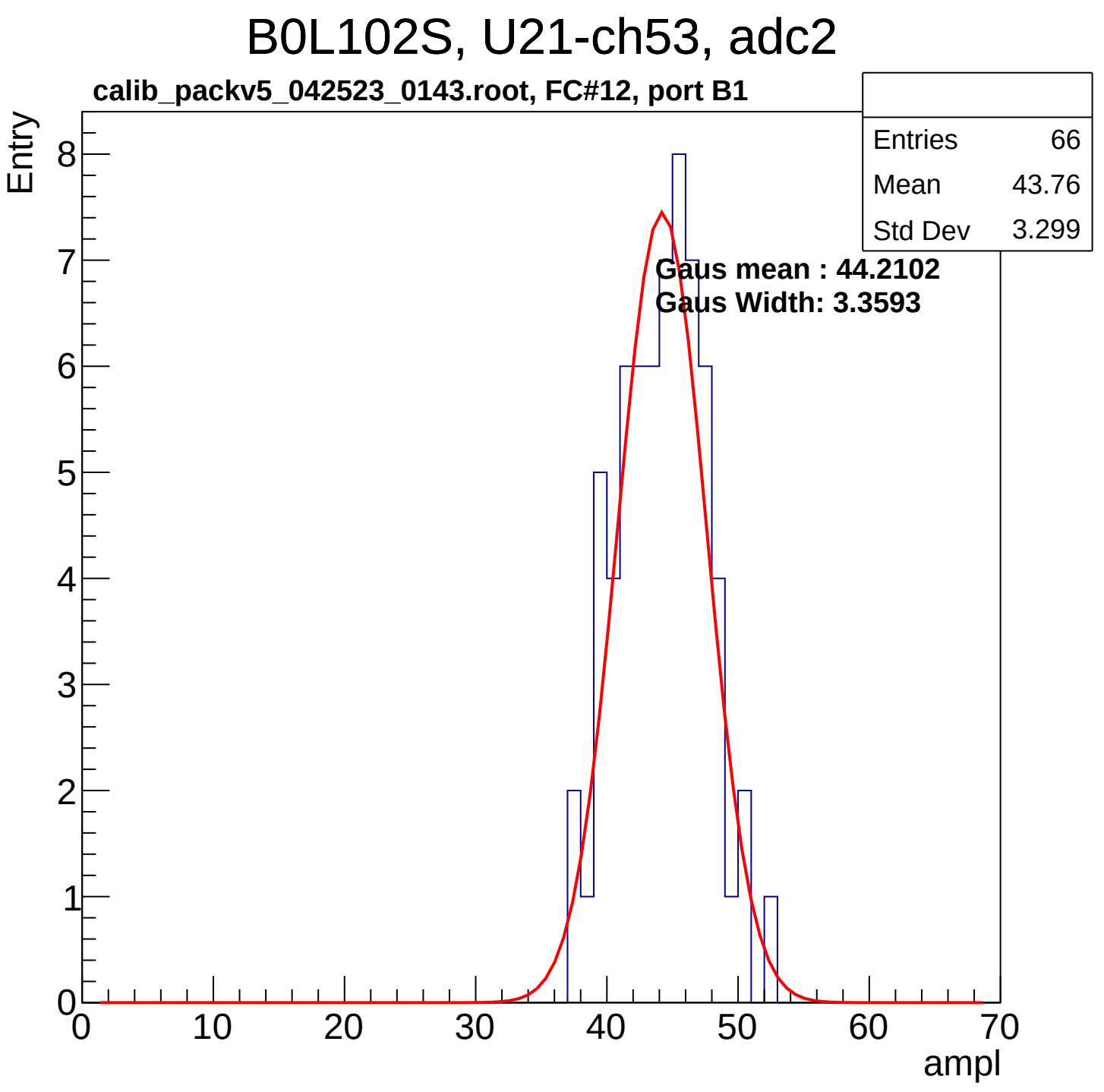
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch53, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.76
Std Dev 3.299
Gaus mean : 44.2102
Gaus Width: 3.3593
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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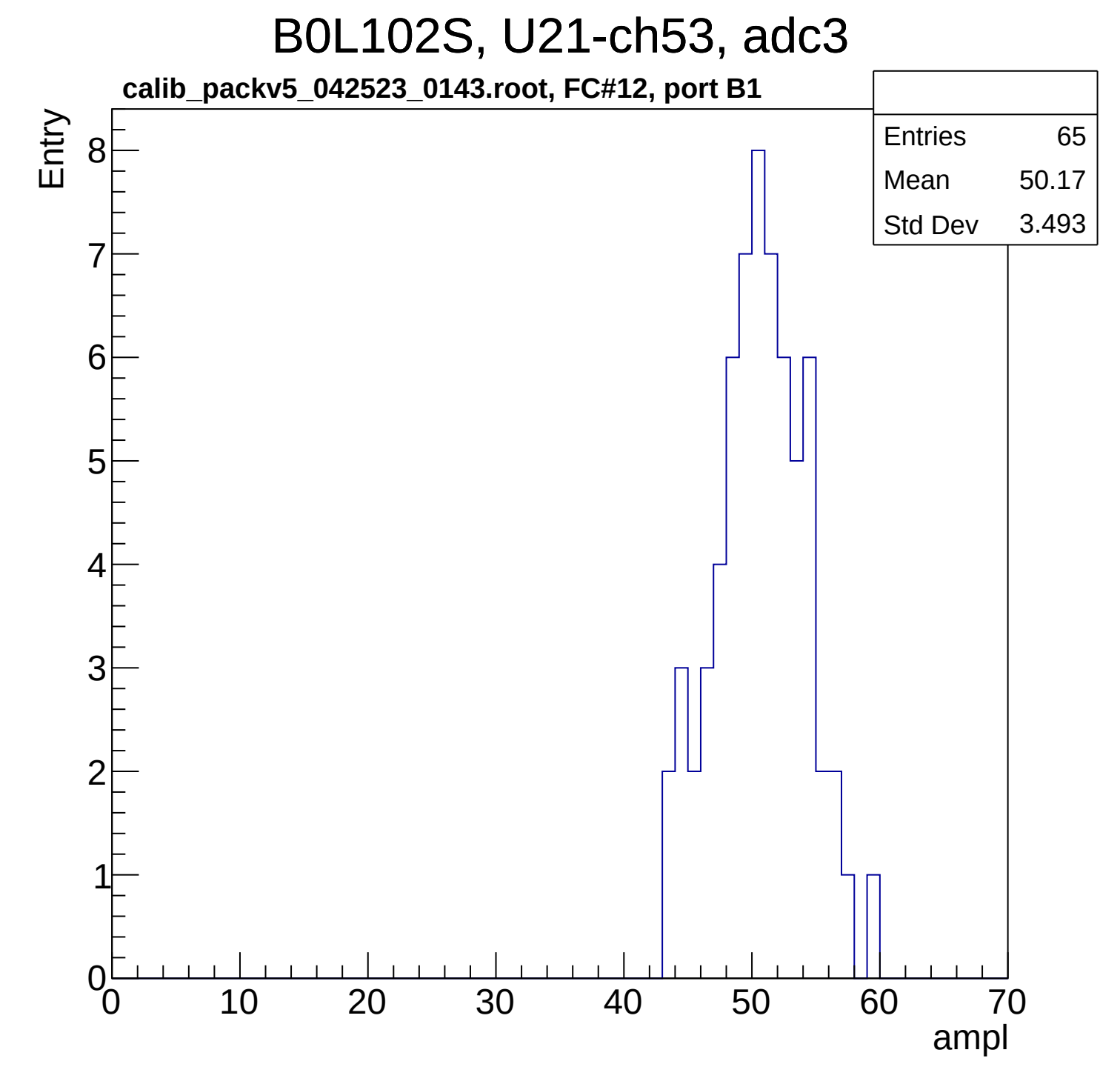
B0L102S, U21-ch53, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 50.17
Std Dev 3.493
Entry
ampl
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3
4
5
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7
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0
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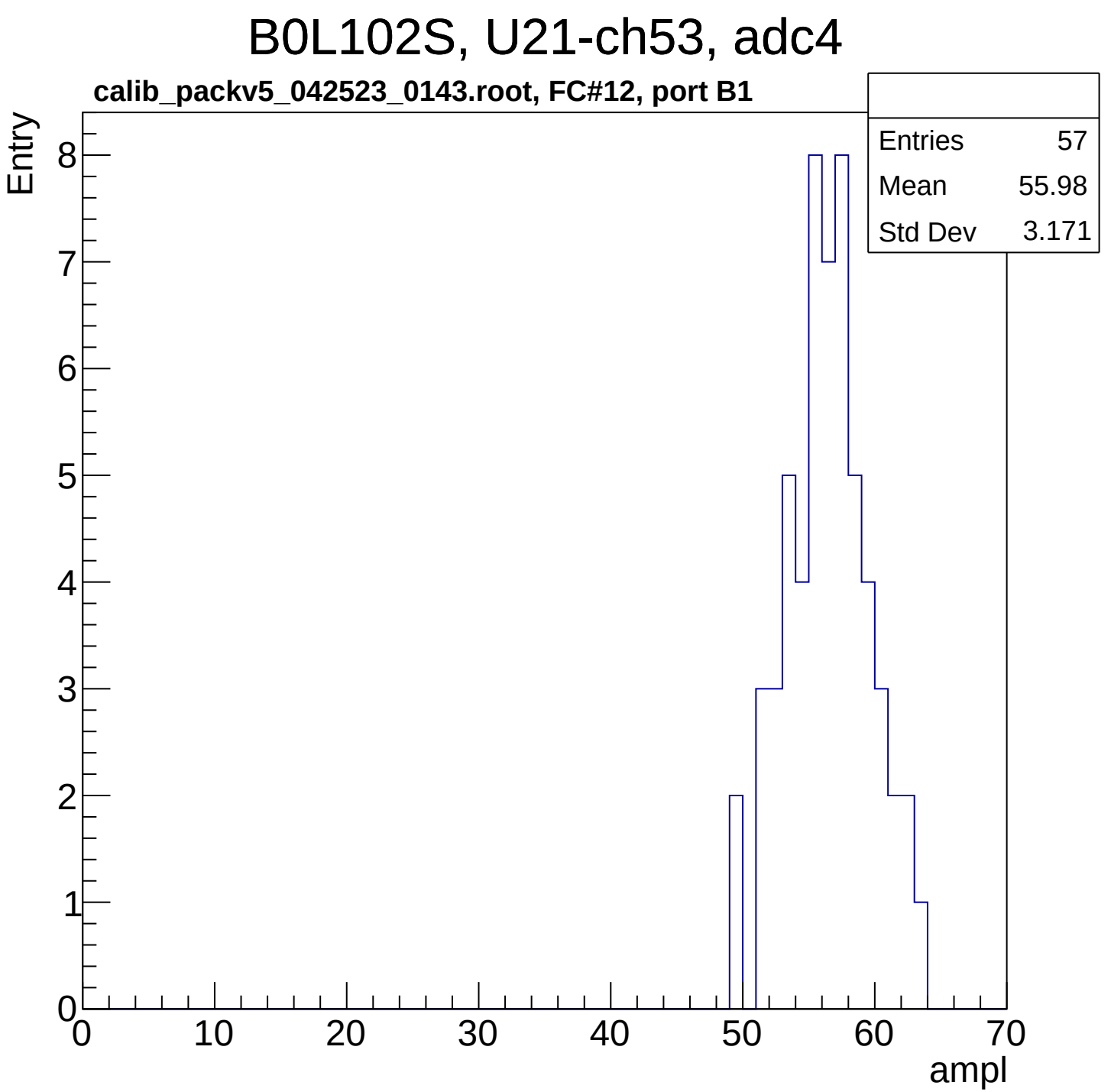
B0L102S, U21-ch53, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.98
Std Dev 3.171
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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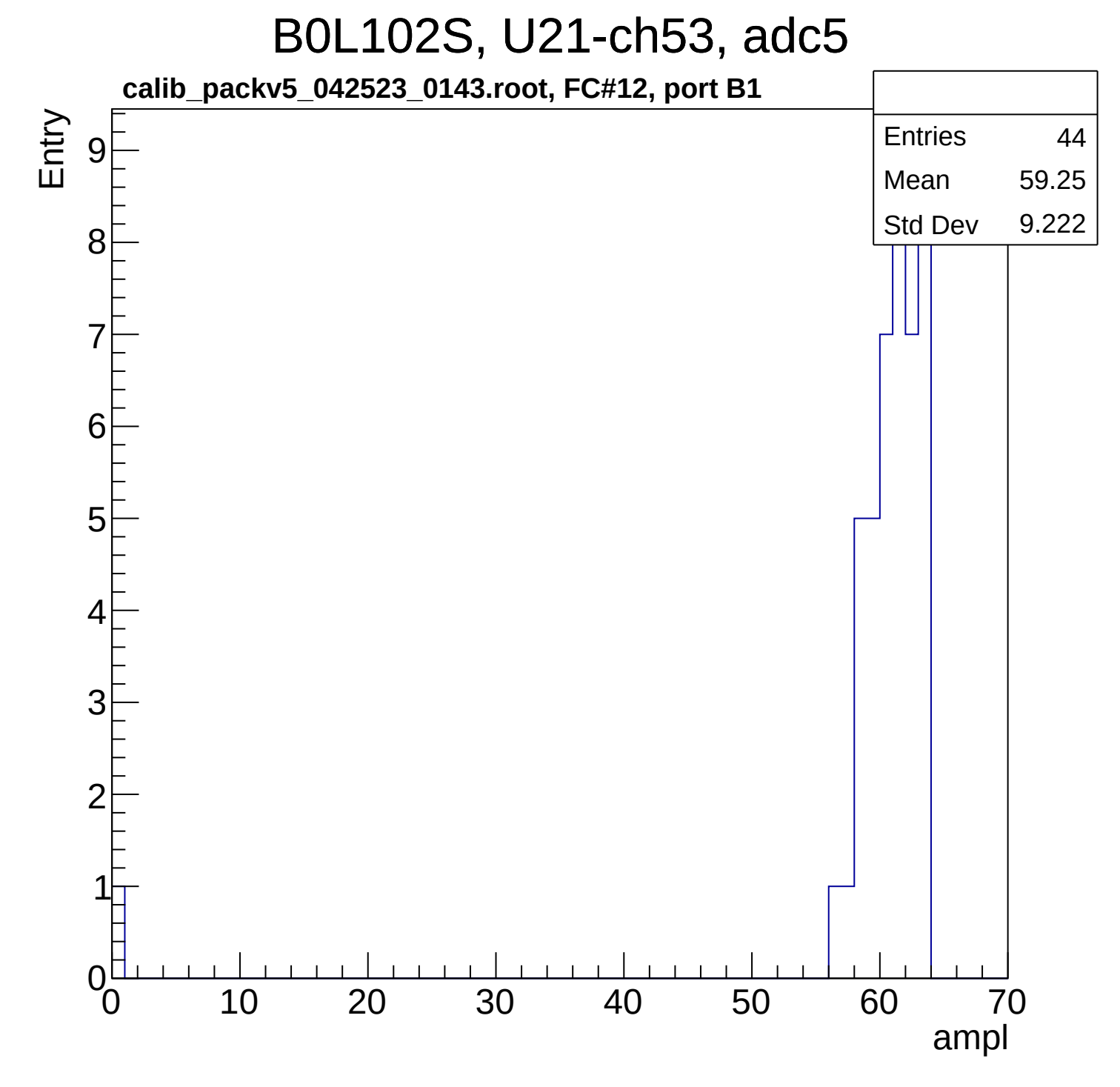
B0L102S, U21-ch53, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.25
Std Dev 9.222
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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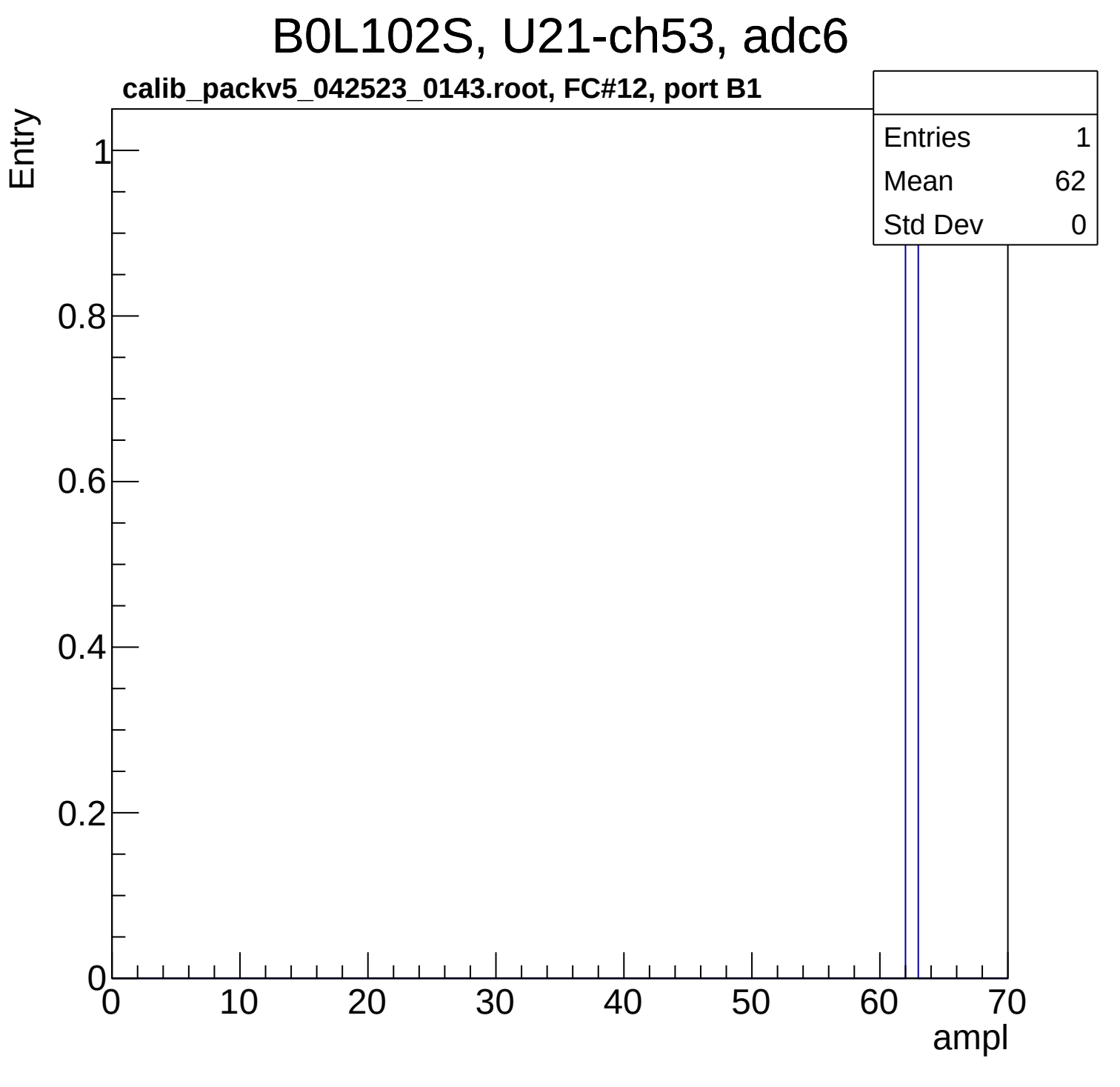
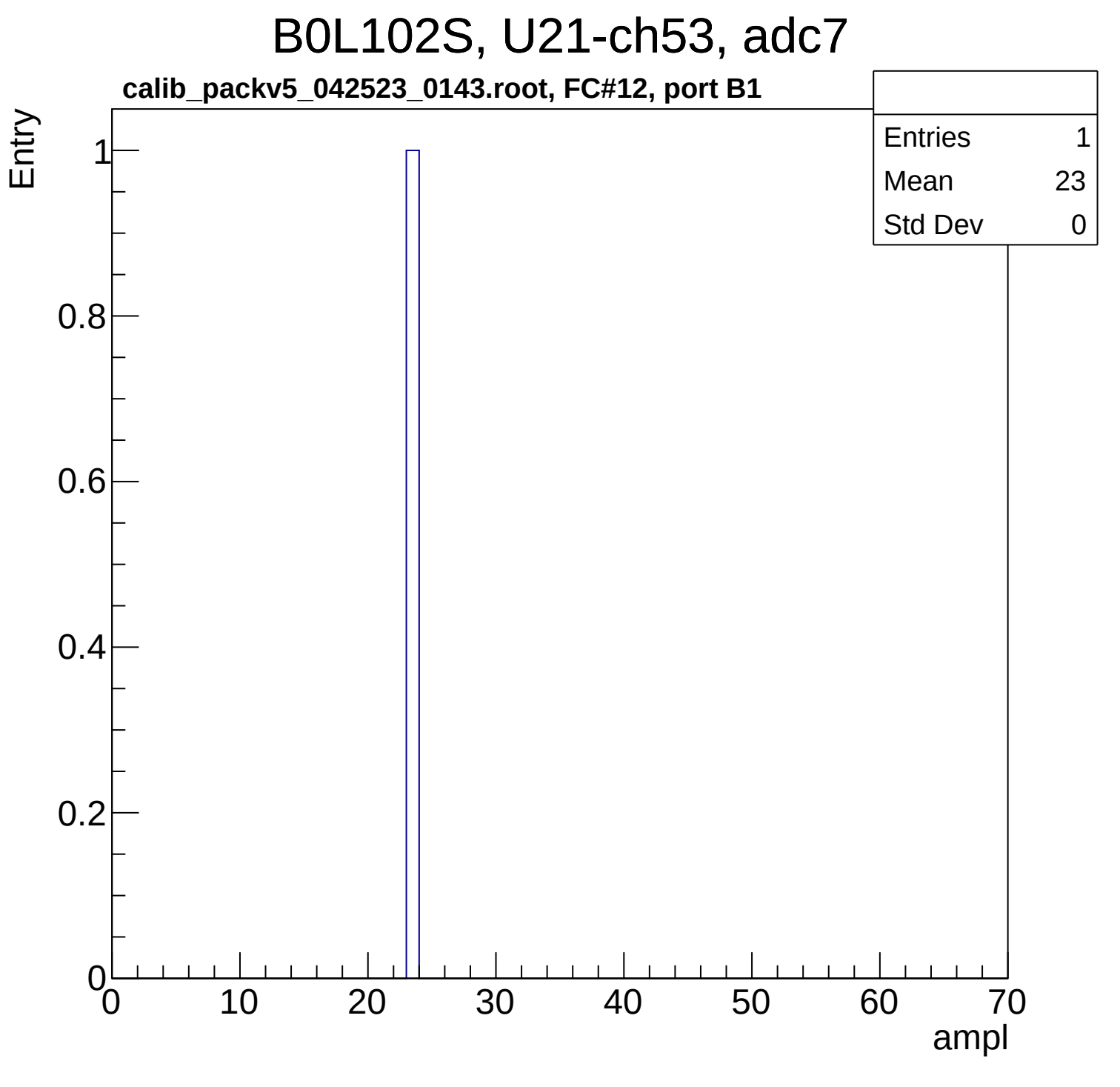

B0L102S, U21-ch53, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch53, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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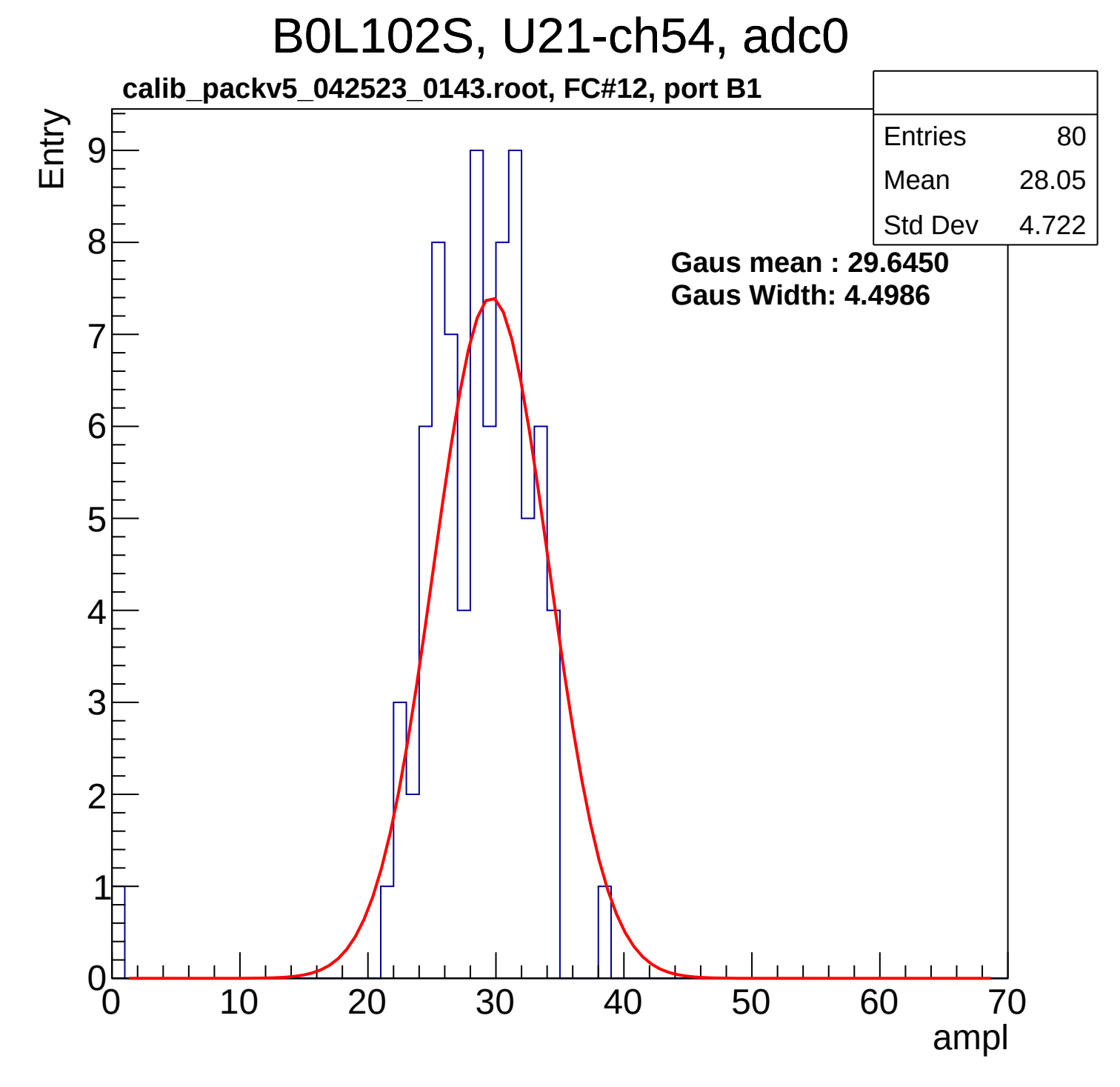
50
60
70
ampl


B0L102S, U21-ch54, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 28.05
Std Dev 4.722
Gaus mean : 29.6450
Gaus Width: 4.4986
Entry
ampl
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10
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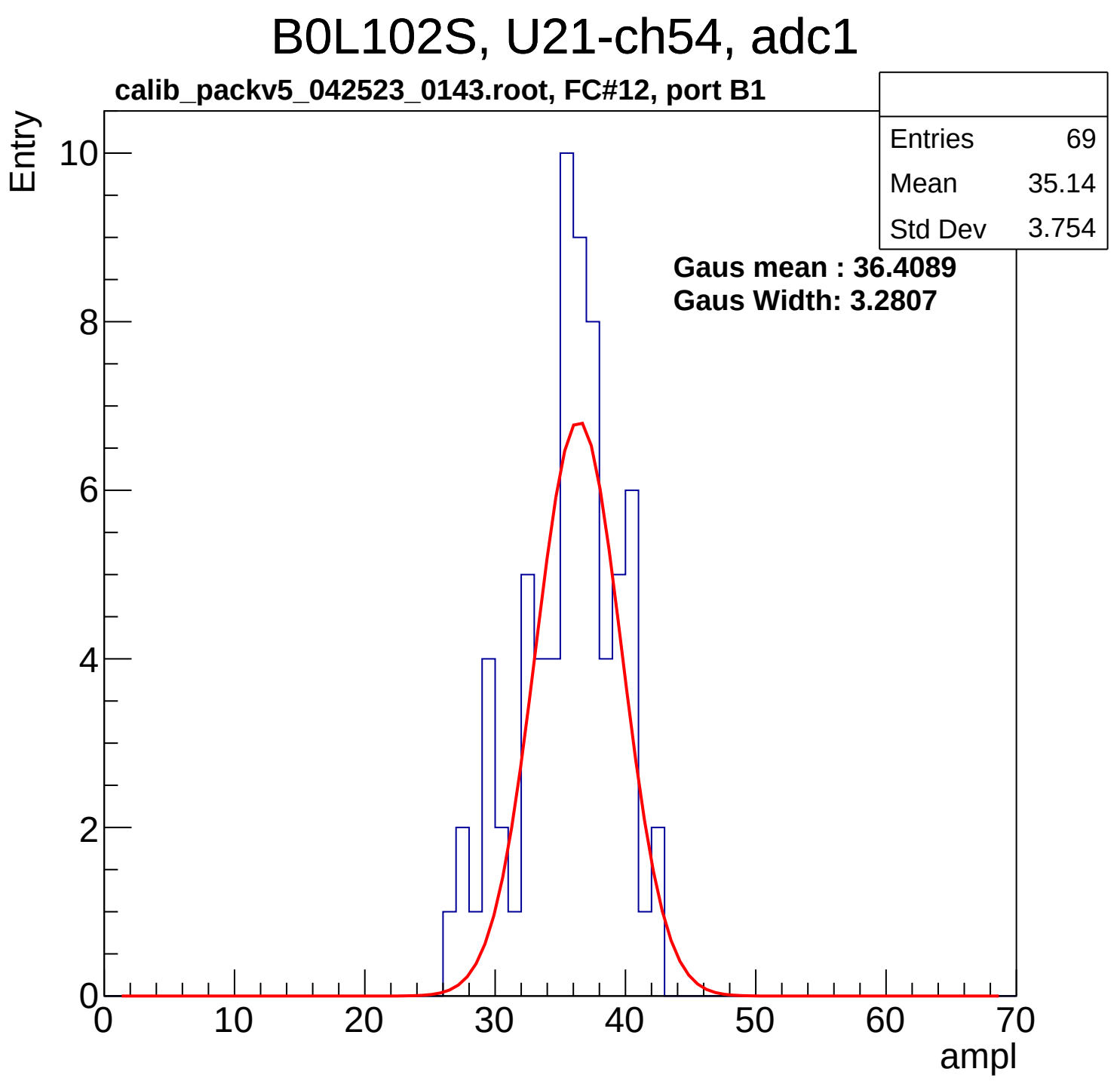
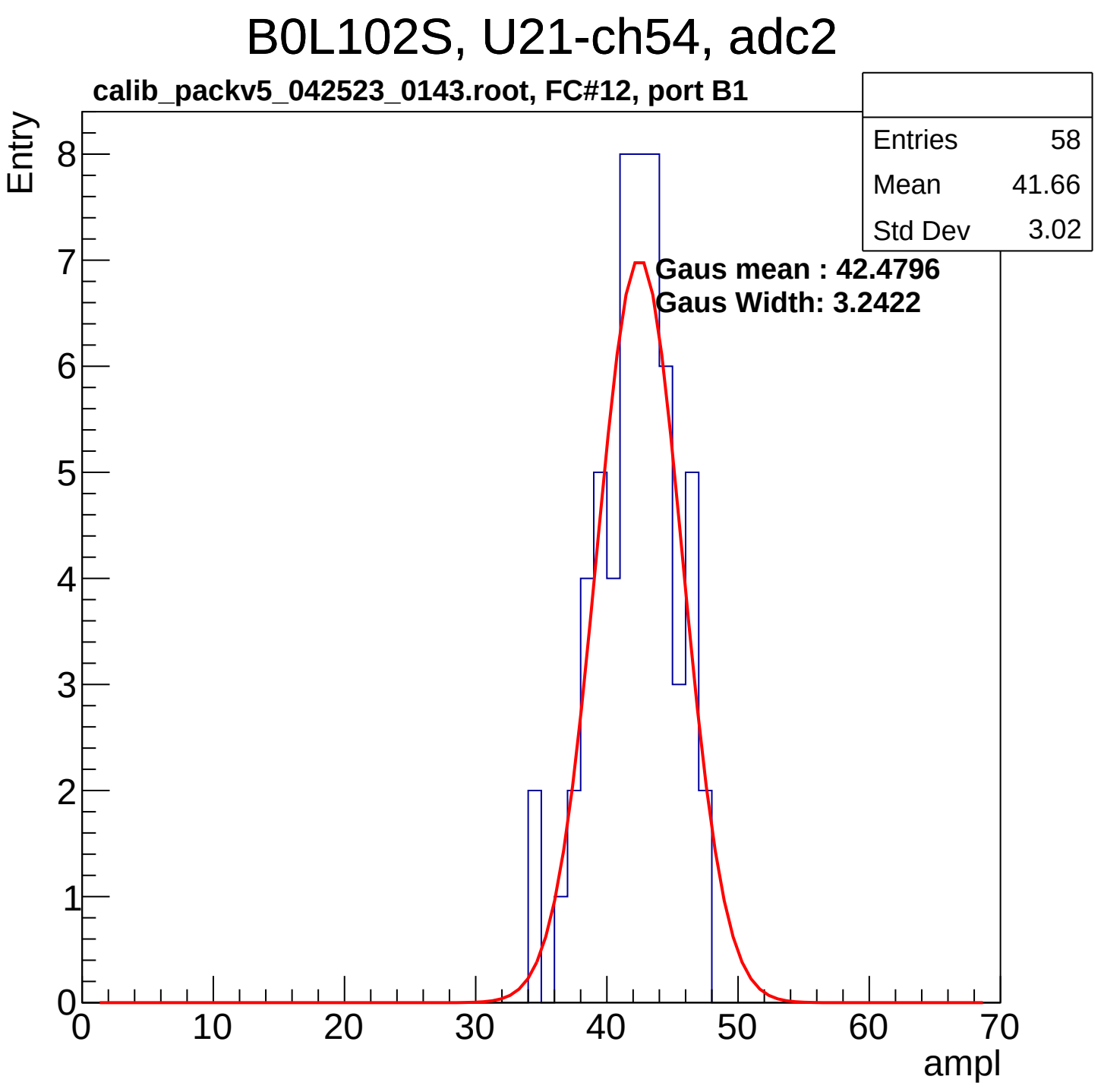
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch54, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 35.14
Std Dev 3.754
Gaus mean : 36.4089
Gaus Width: 3.2807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch54, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 41.66
Std Dev 3.02
Gaus mean : 42.4796
Gaus Width: 3.2422
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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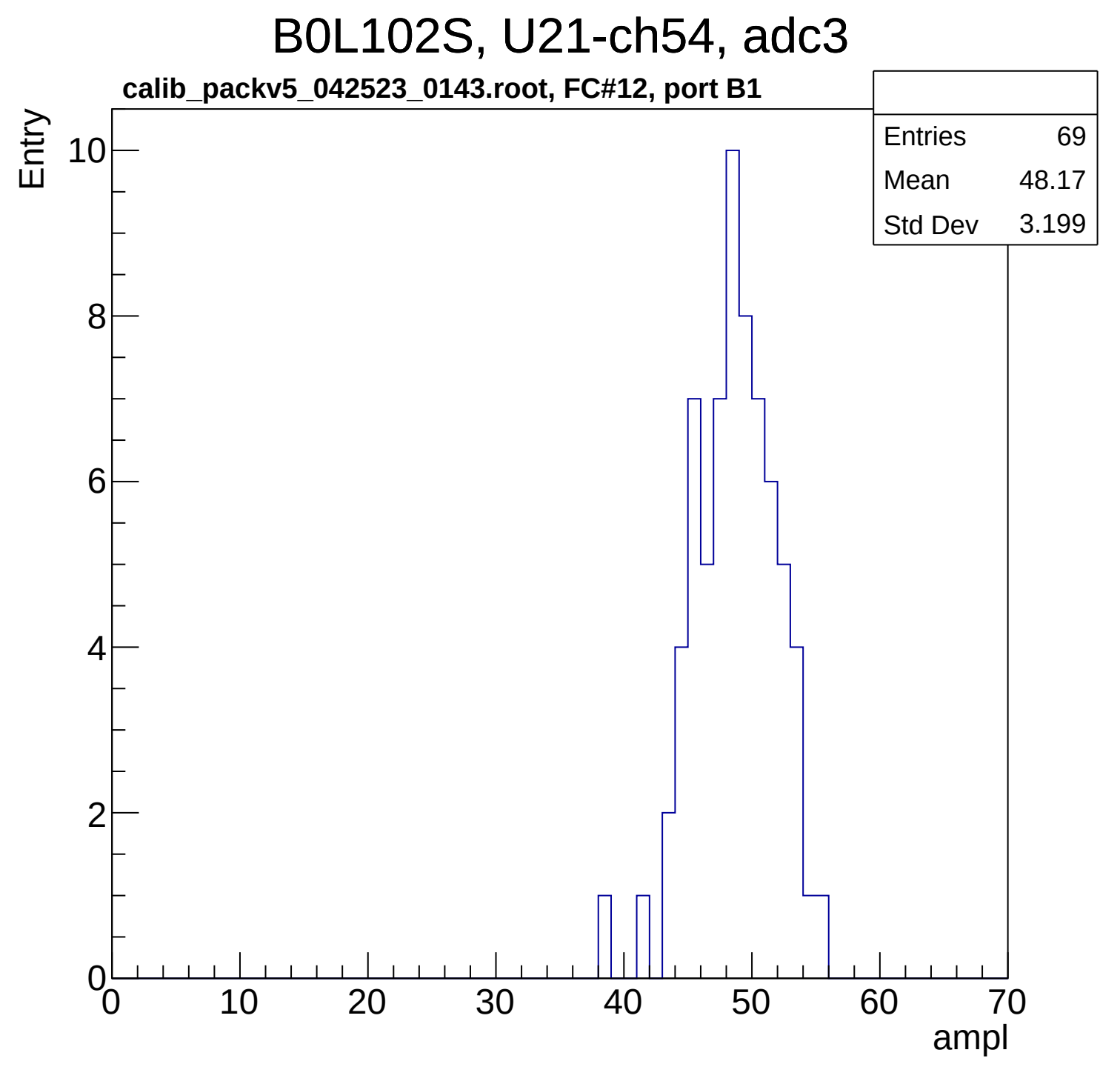
B0L102S, U21-ch54, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 48.17
Std Dev 3.199
Entry
ampl
0
2
4
6
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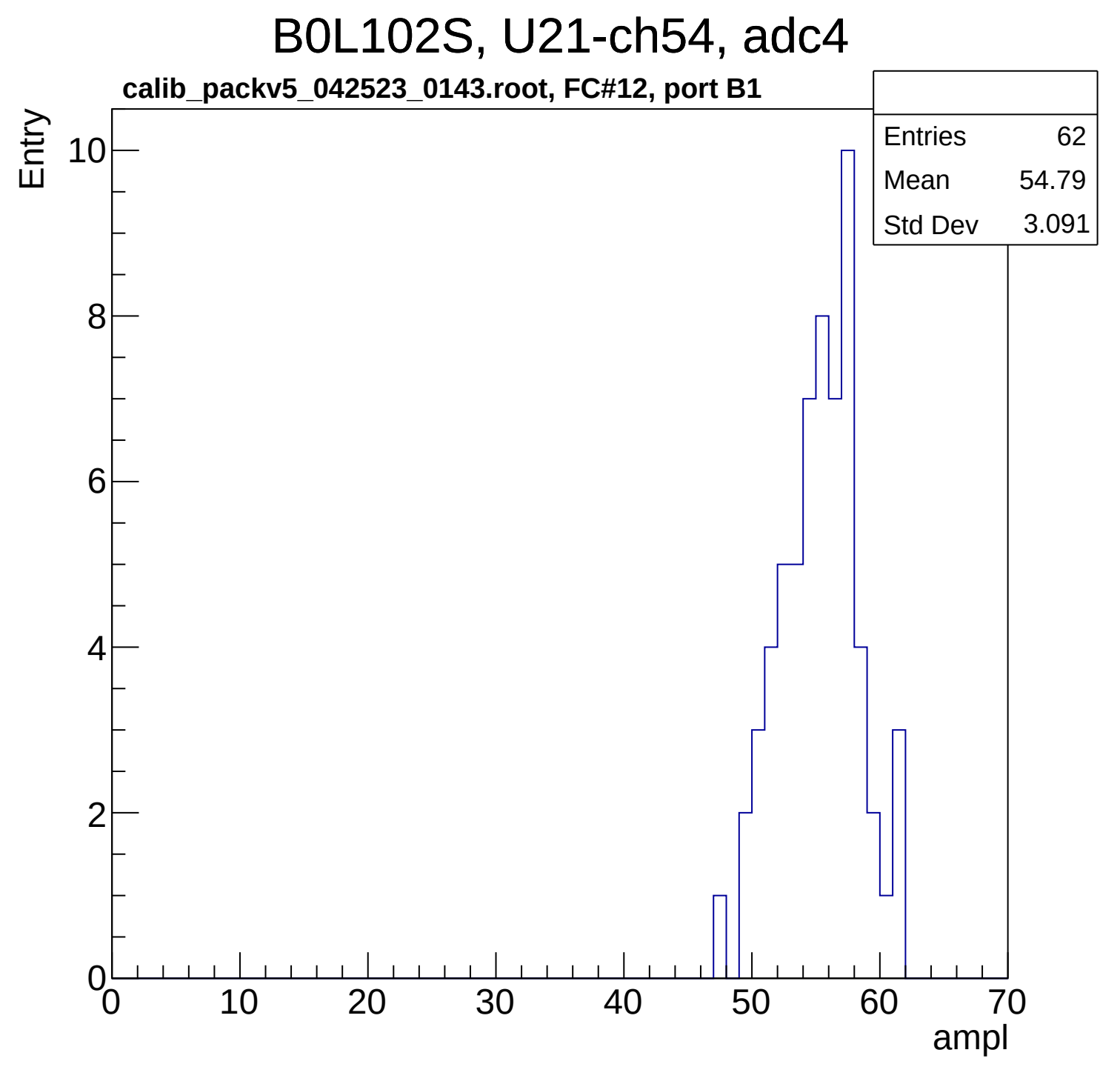
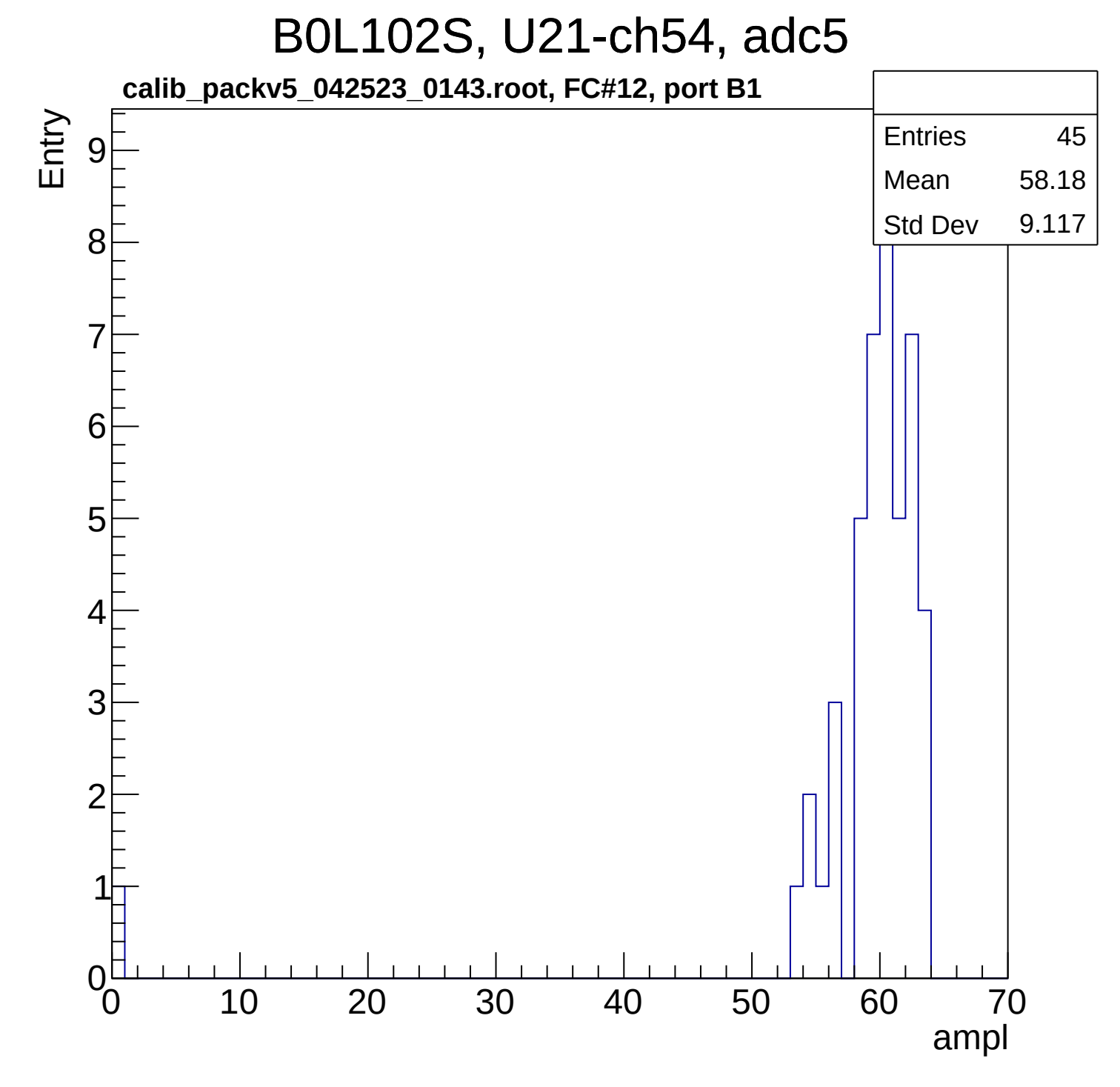

B0L102S, U21-ch54, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 54.79
Std Dev 3.091
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch54, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.18
Std Dev 9.117
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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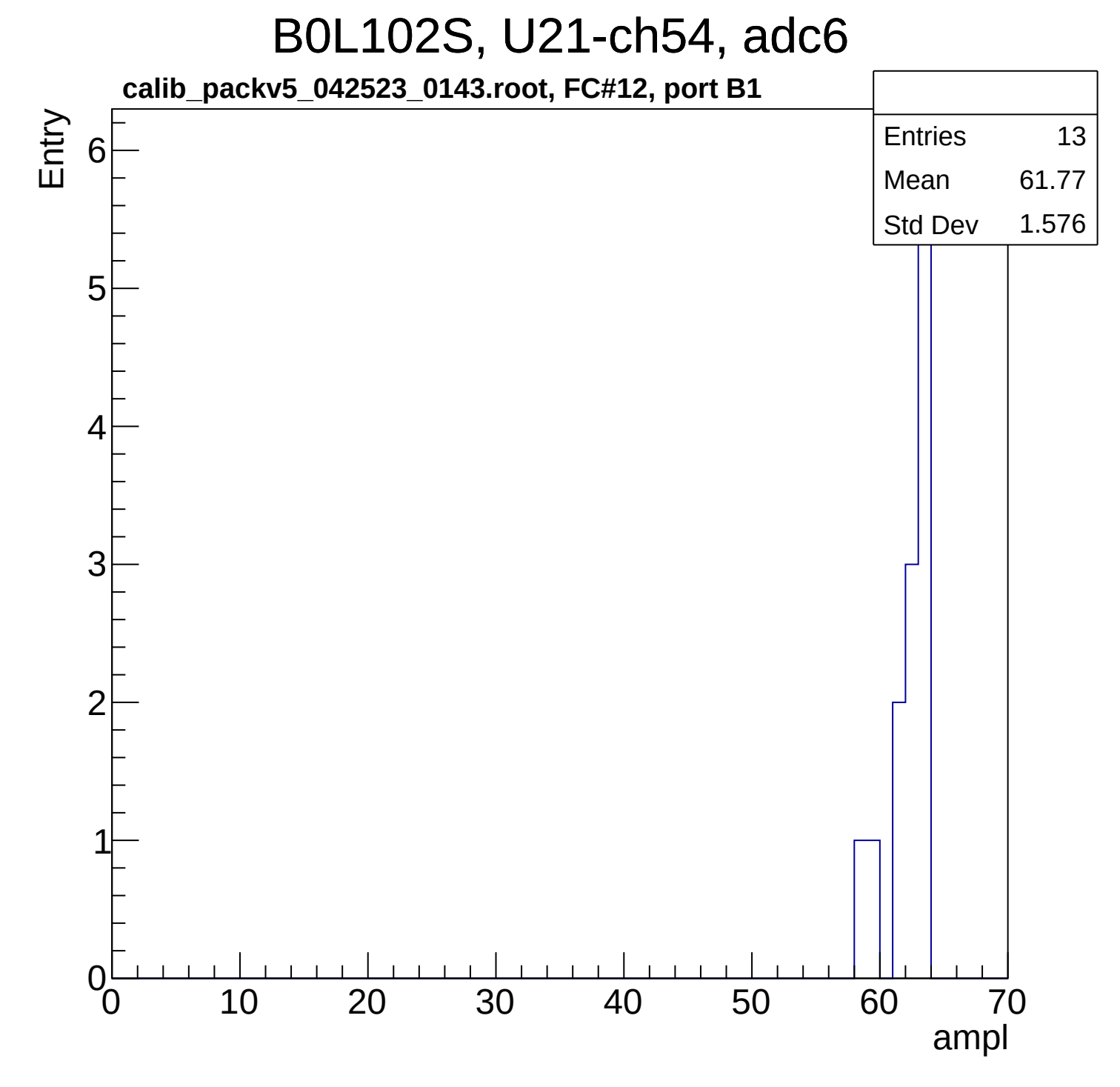
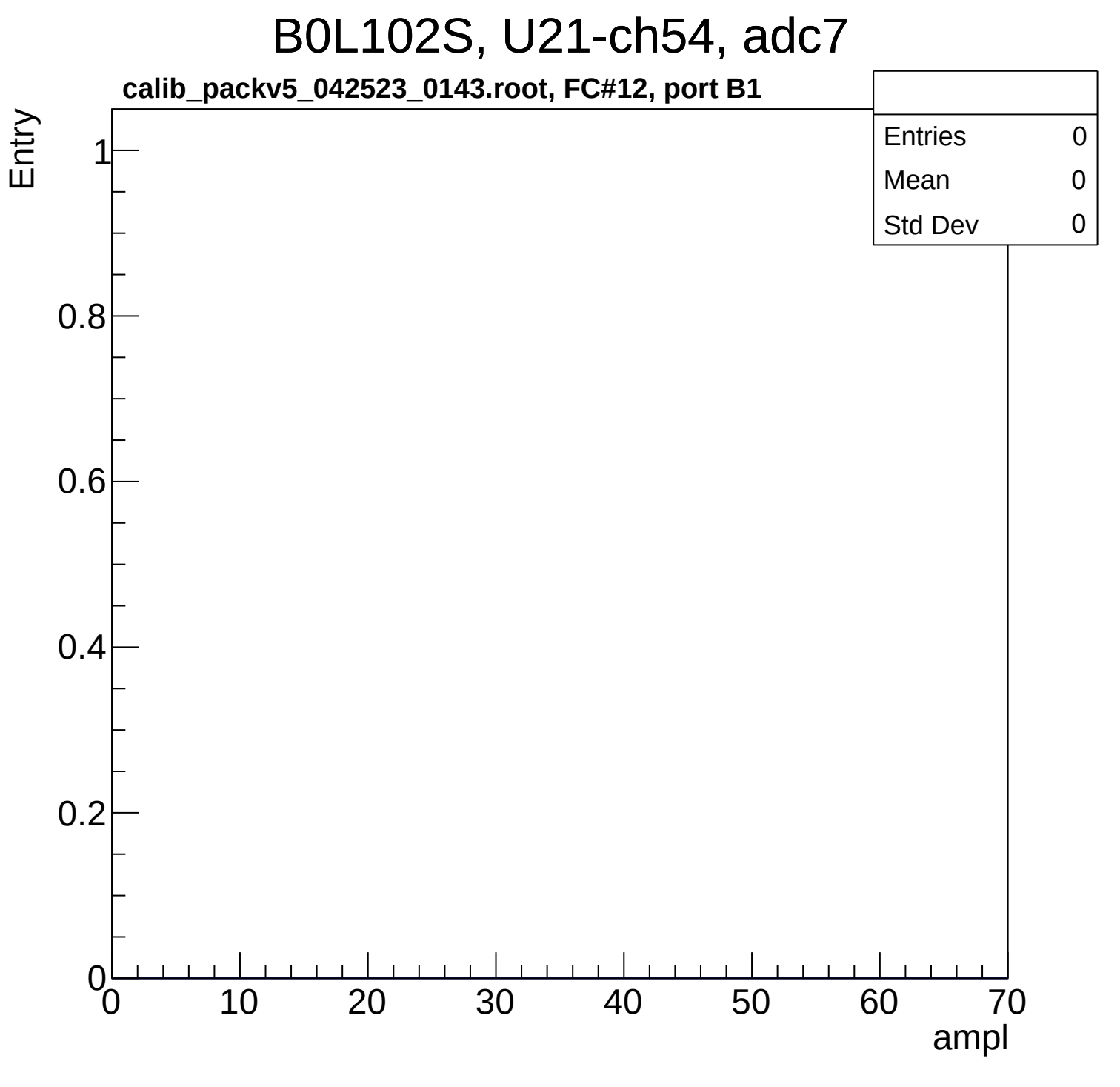
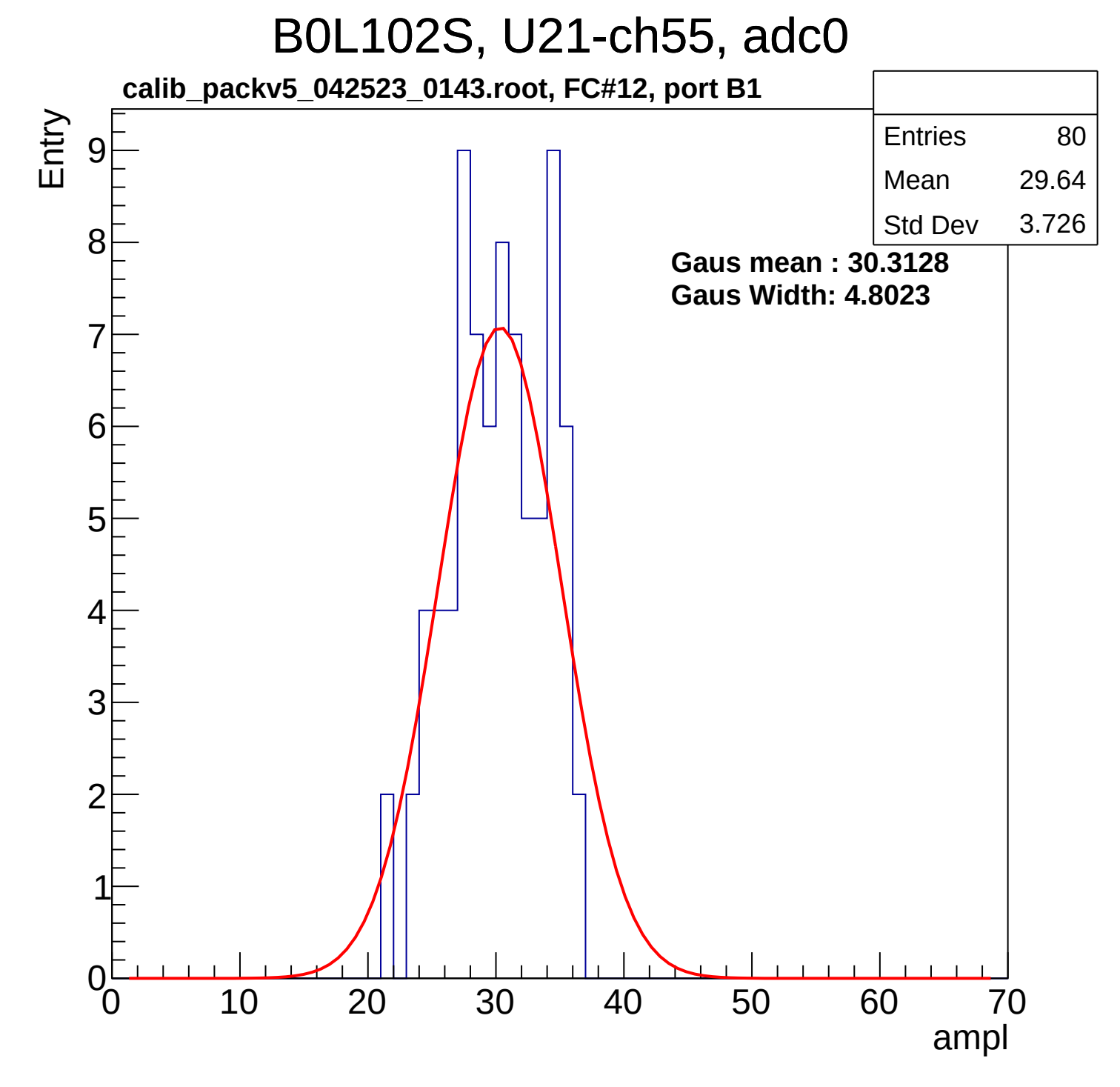

B0L102S, U21-ch54, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 61.77
Std Dev 1.576
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch54, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch55, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 29.64
Std Dev 3.726
Gaus mean : 30.3128
Gaus Width: 4.8023
Entry
ampl
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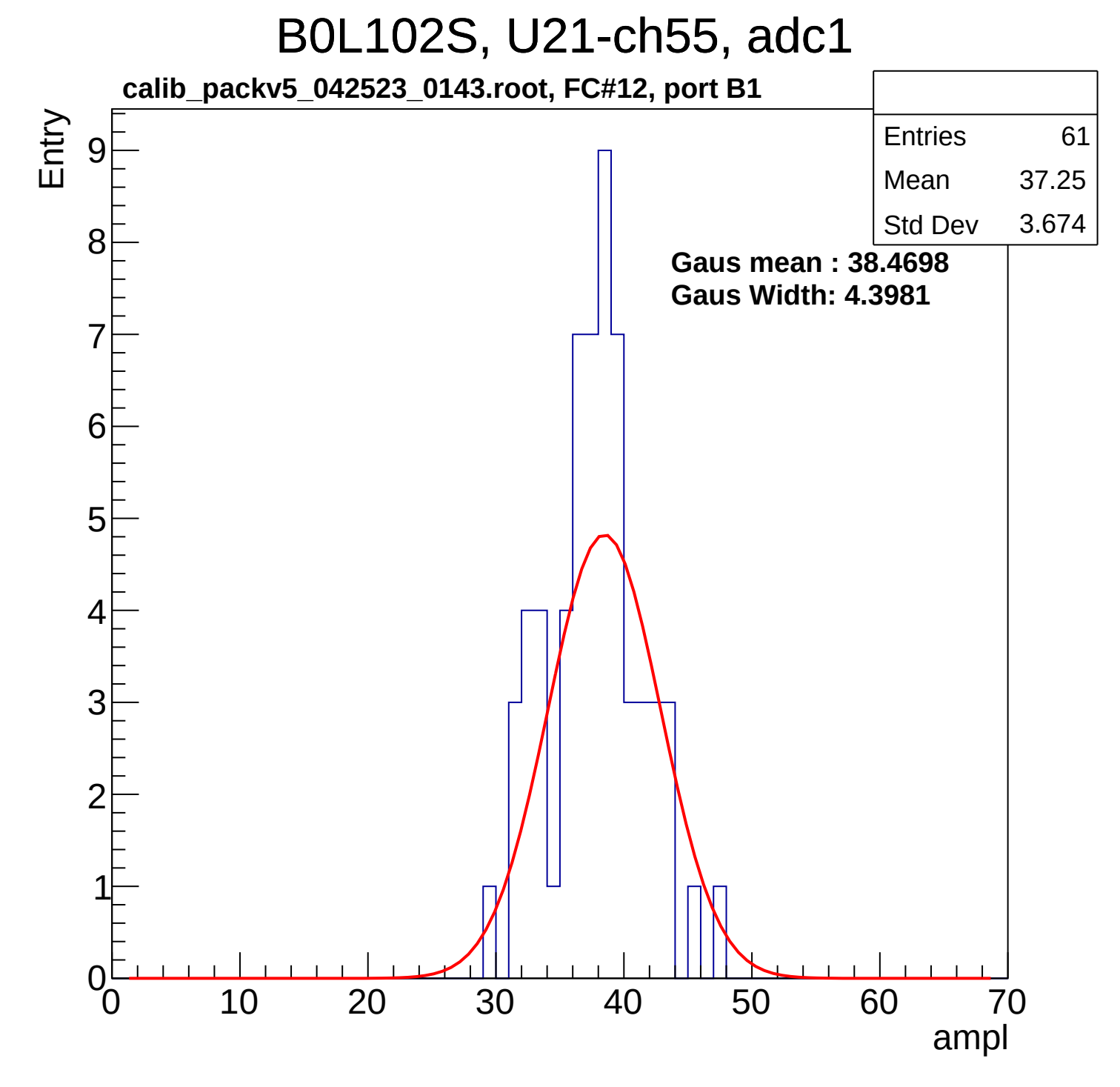
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch55, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 37.25
Std Dev 3.674
Gaus mean : 38.4698
Gaus Width: 4.3981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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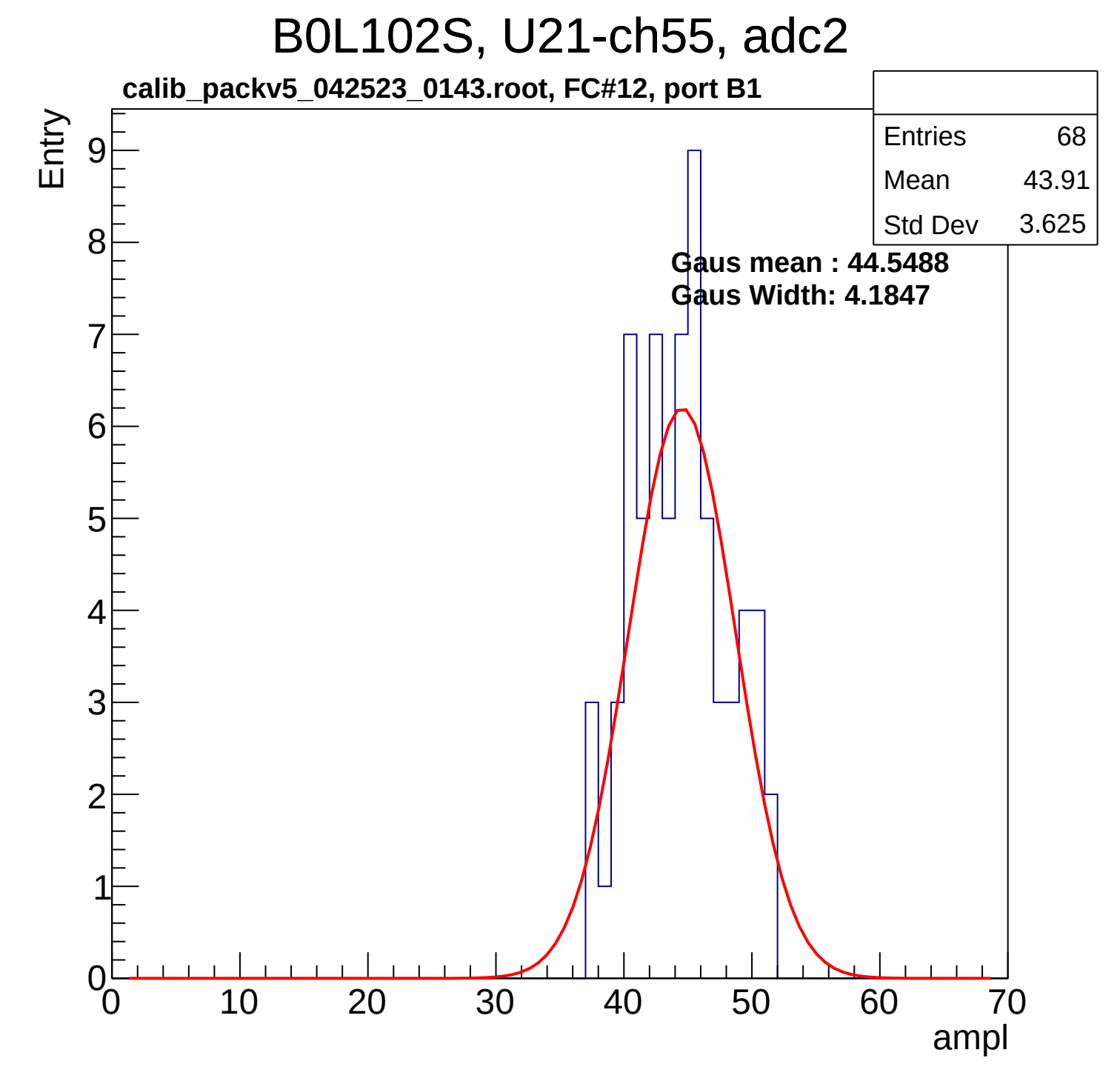
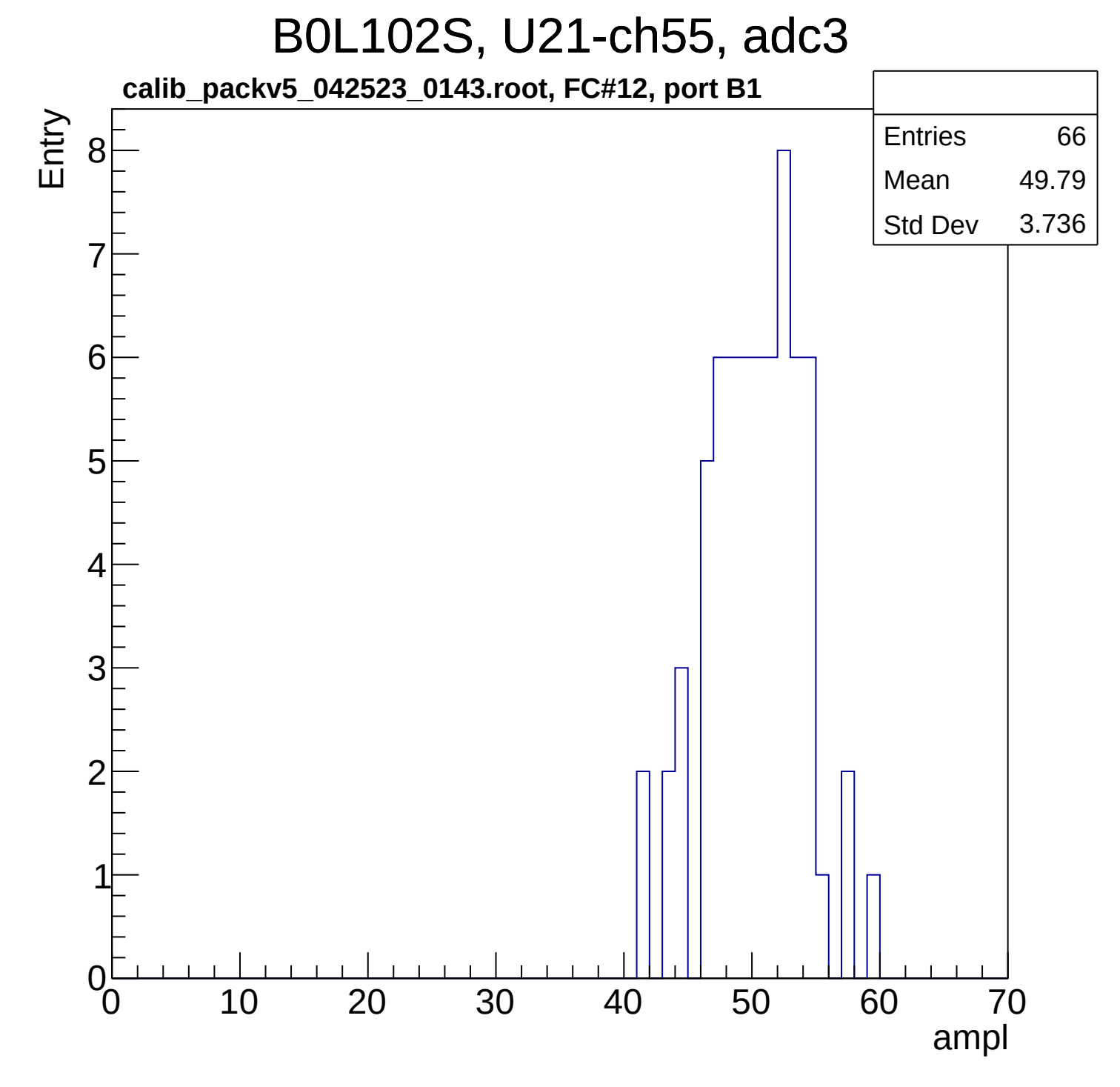
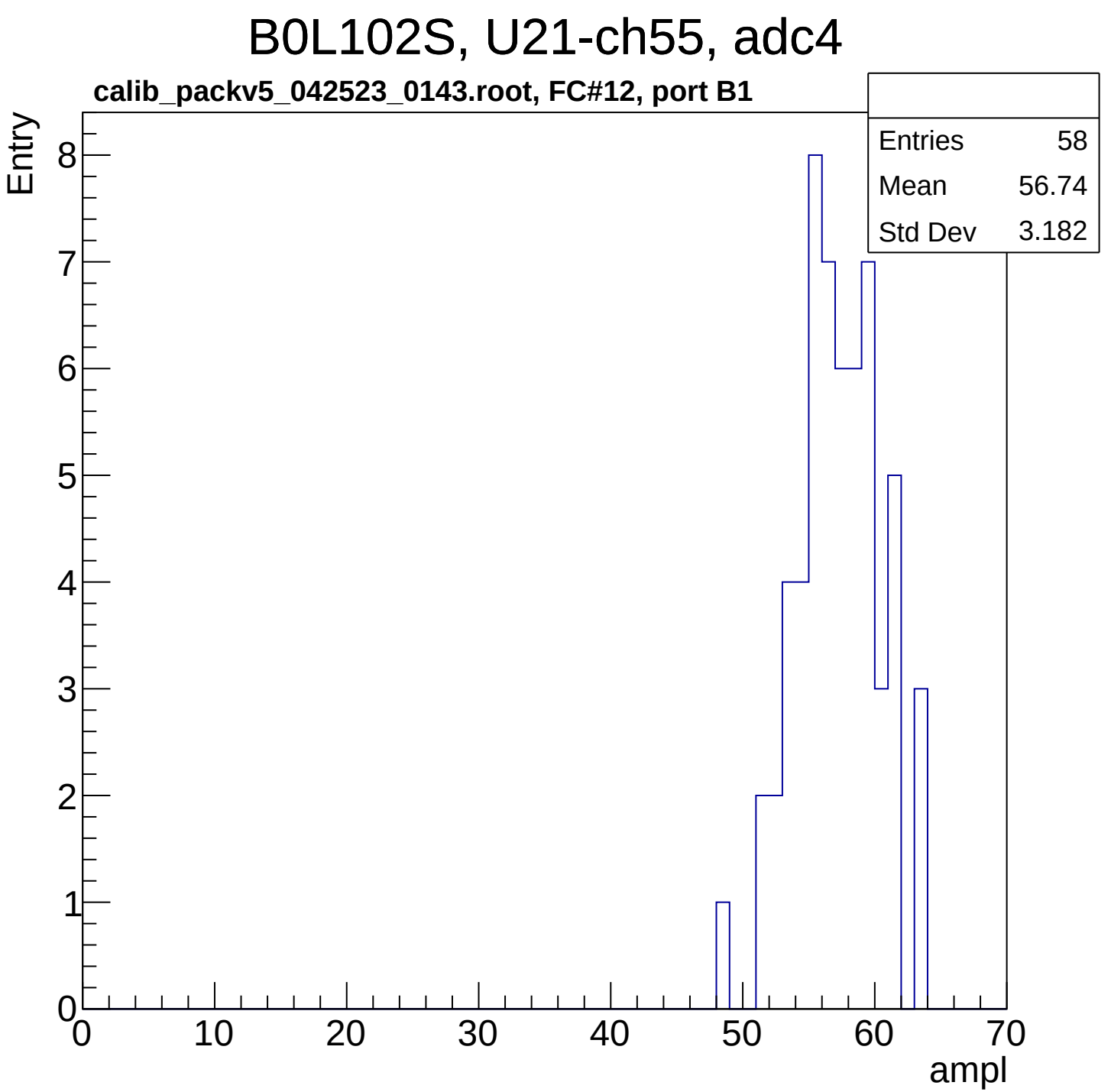
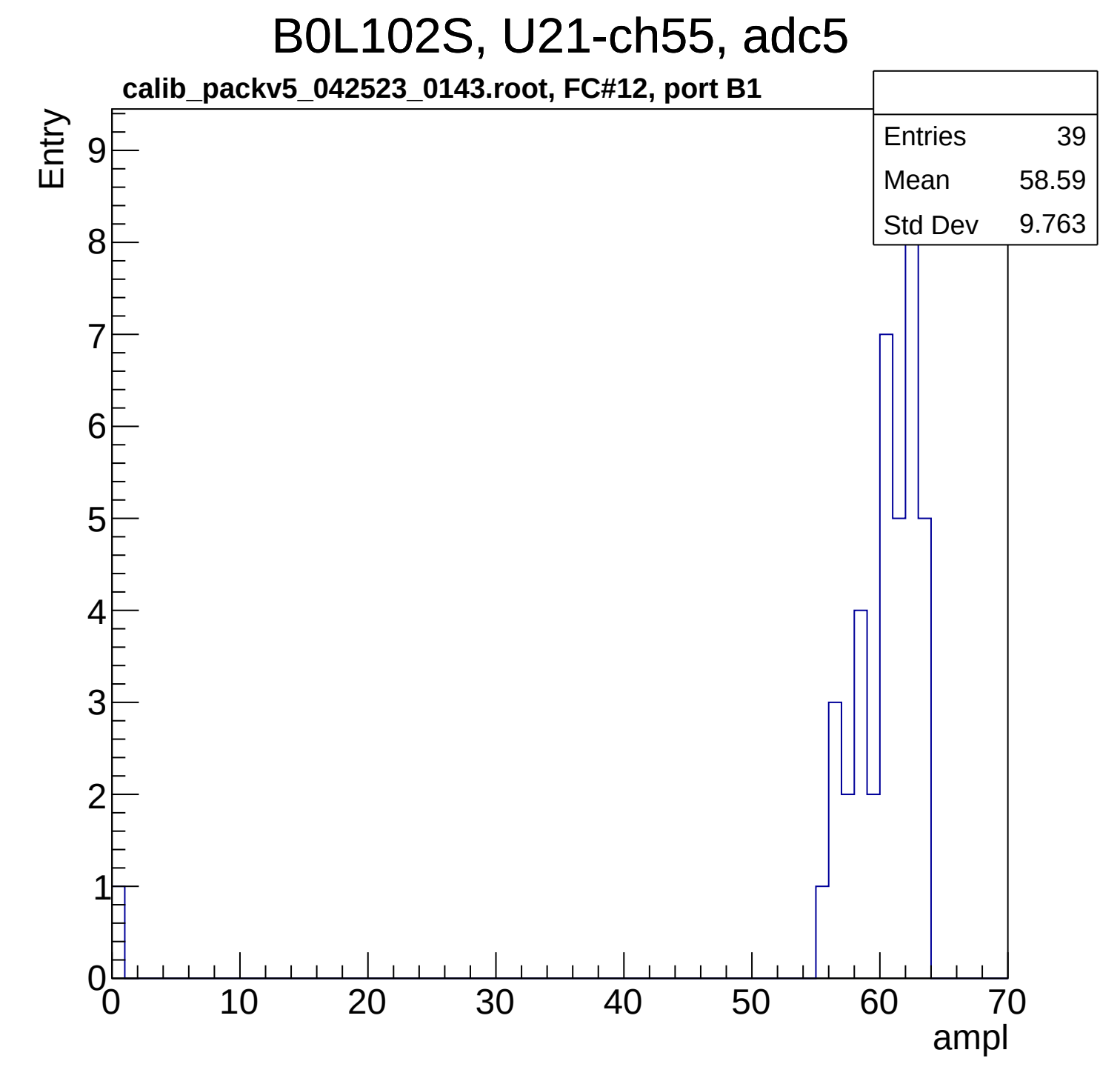

B0L102S, U21-ch55, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 43.91
Std Dev 3.625
Gaus mean : 44.5488
Gaus Width: 4.1847
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch55, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.79
Std Dev 3.736
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch55, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 56.74
Std Dev 3.182
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch55, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 58.59
Std Dev 9.763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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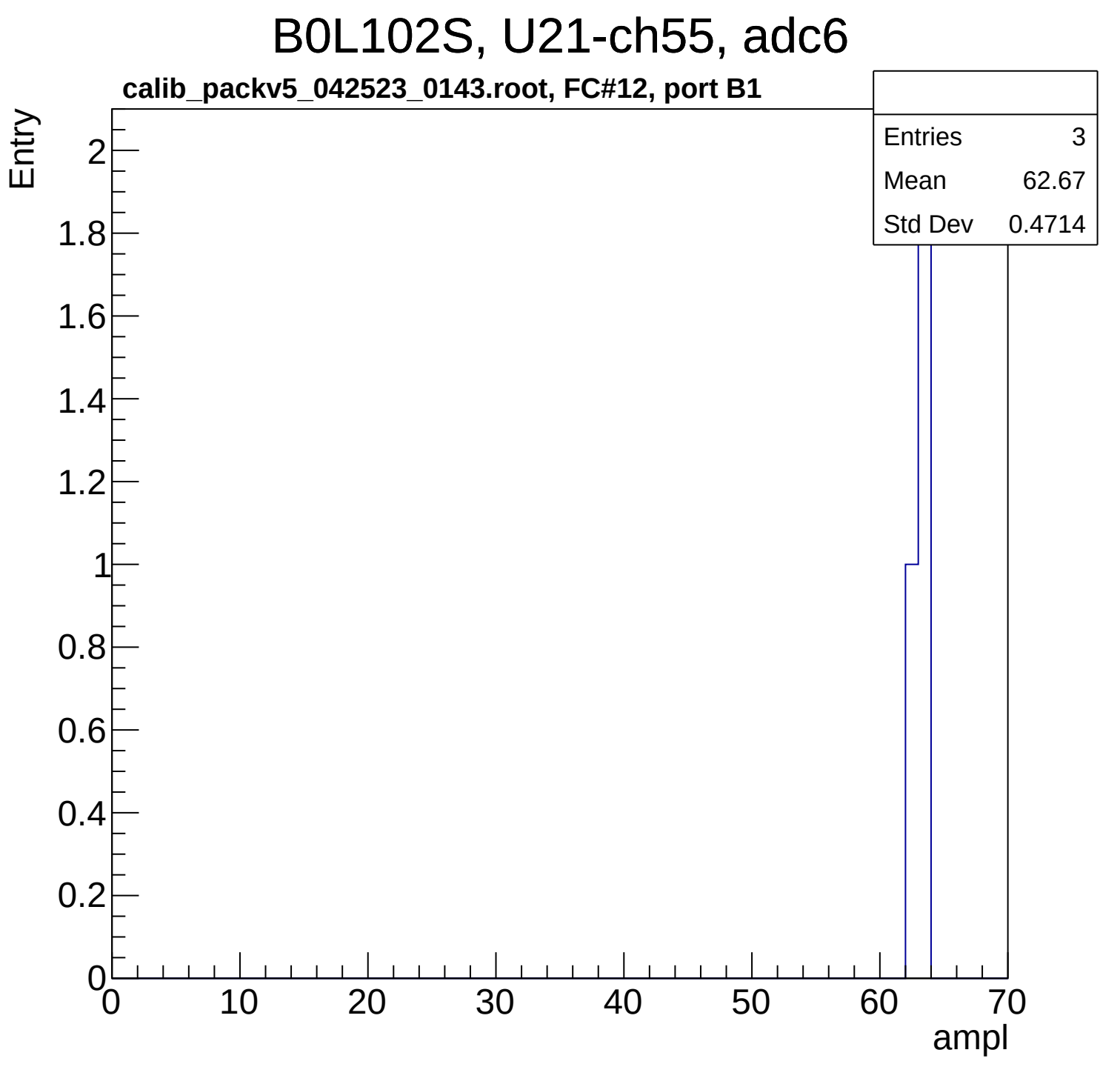
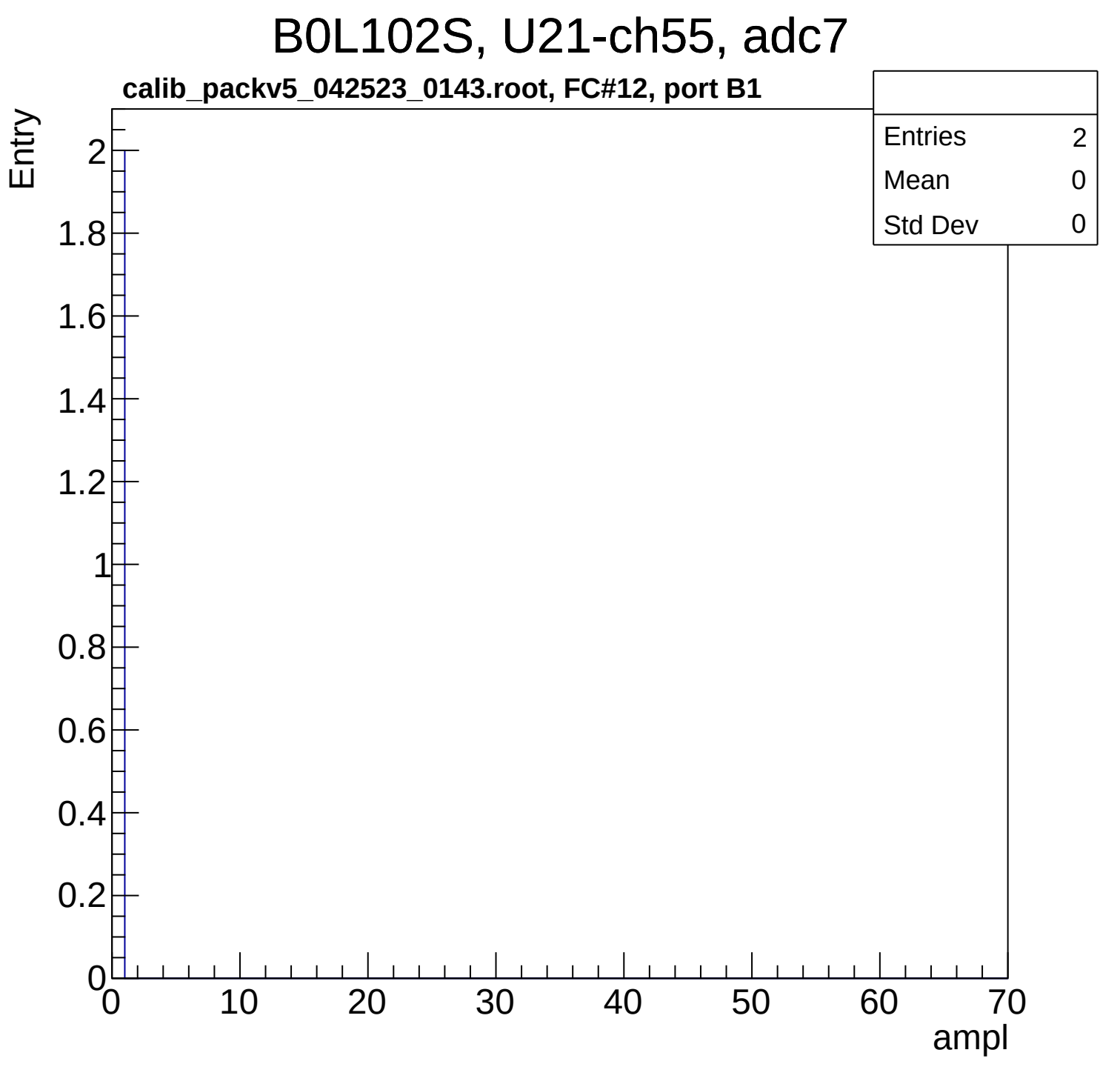
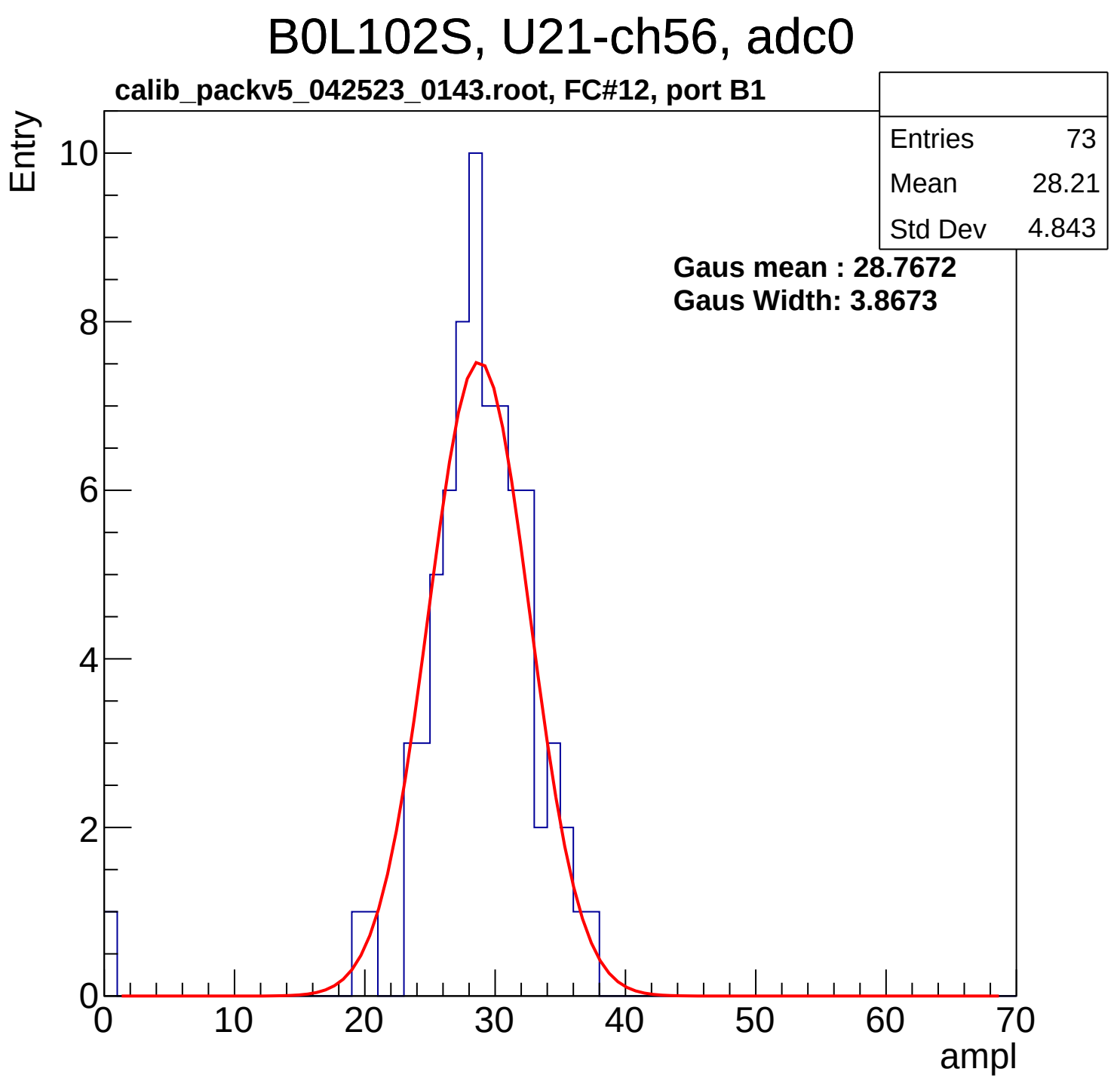

B0L102S, U21-ch55, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch55, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch56, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 28.21
Std Dev 4.843
Gaus mean : 28.7672
Gaus Width: 3.8673
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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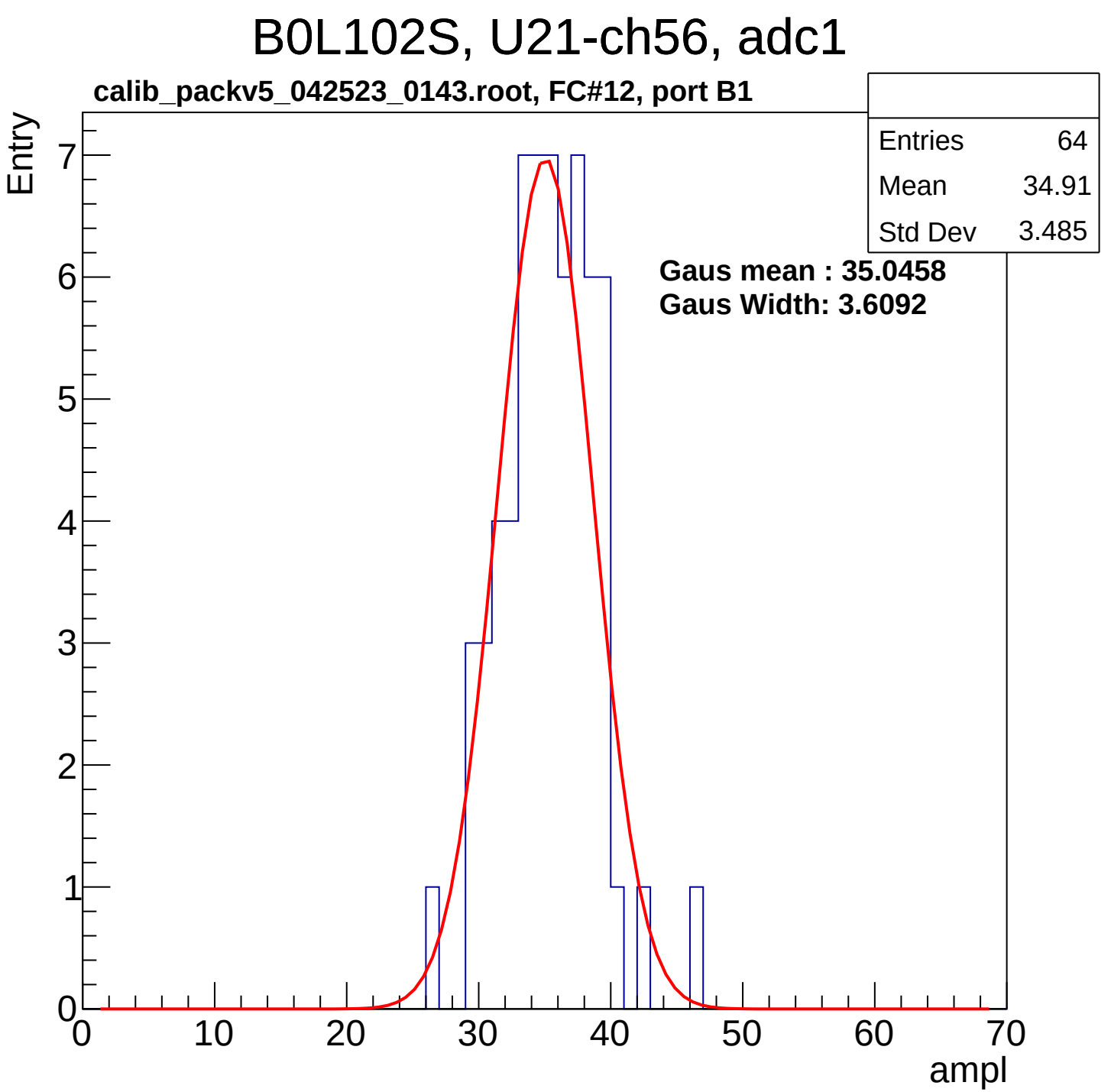
B0L102S, U21-ch56, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 34.91
Std Dev 3.485
Gaus mean : 35.0458
Gaus Width: 3.6092
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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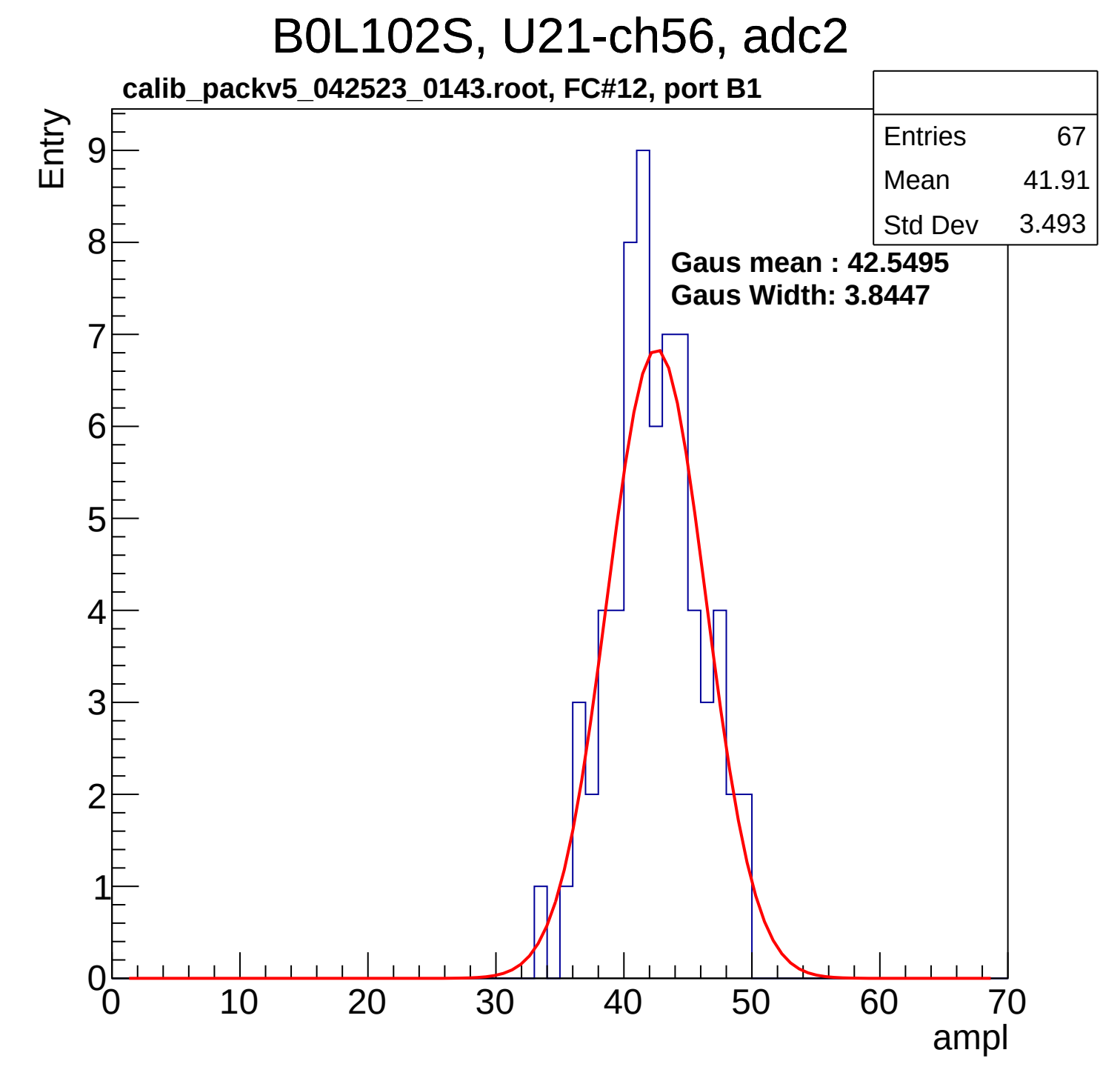
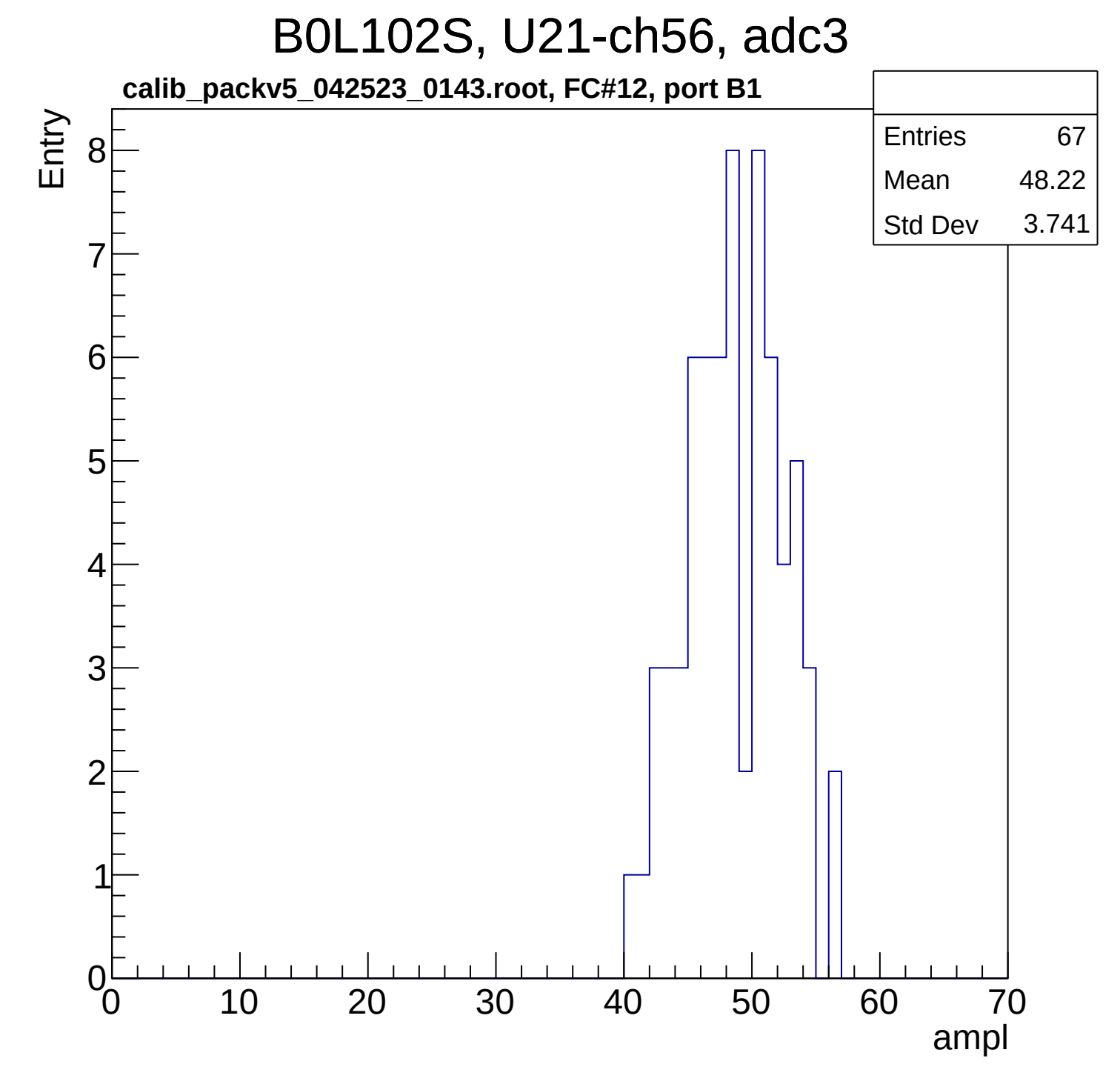

B0L102S, U21-ch56, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 41.91
Std Dev 3.493
Gaus mean : 42.5495
Gaus Width: 3.8447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch56, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 48.22
Std Dev 3.741
Entry
ampl
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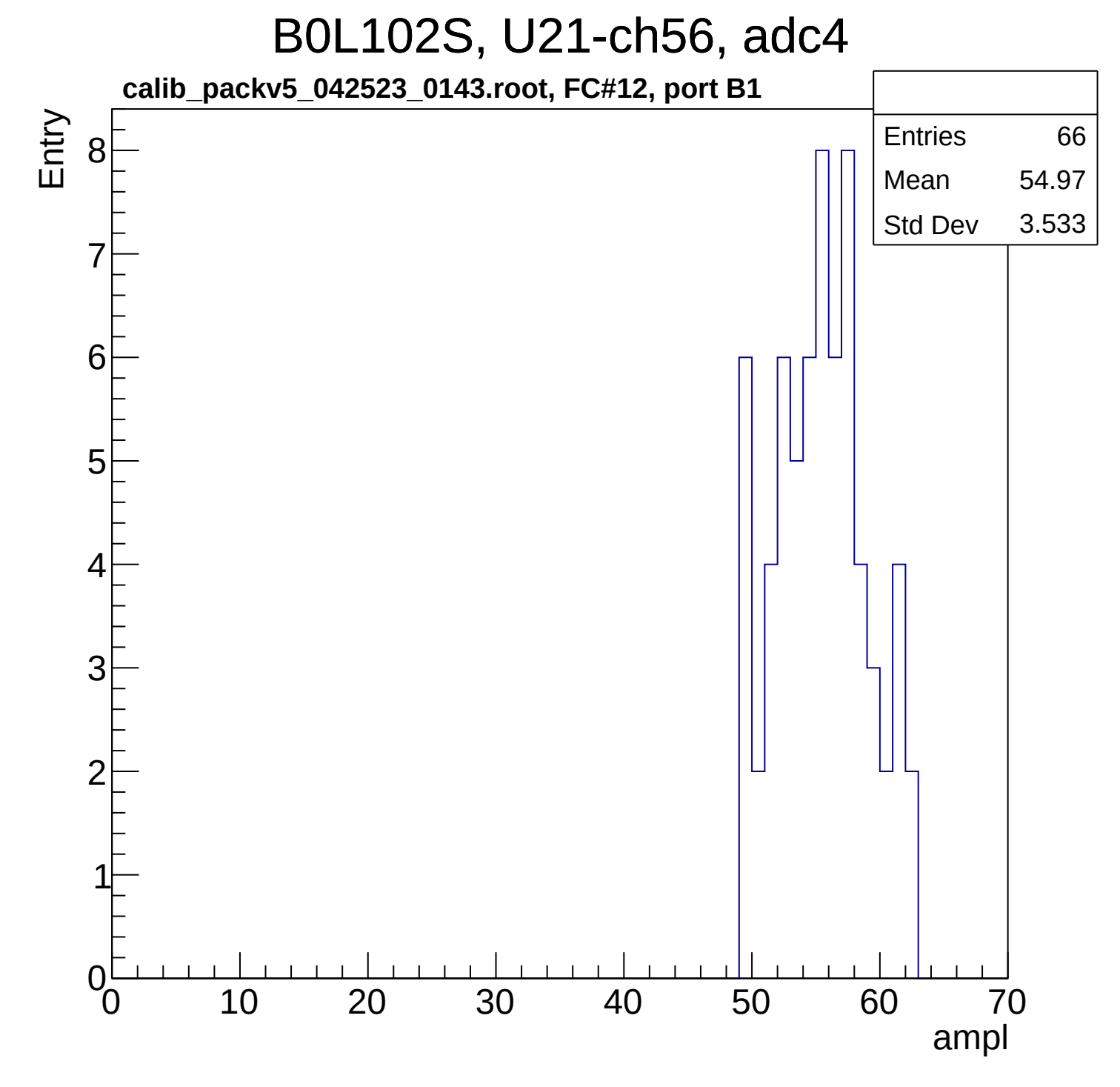
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch56, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 54.97
Std Dev 3.533
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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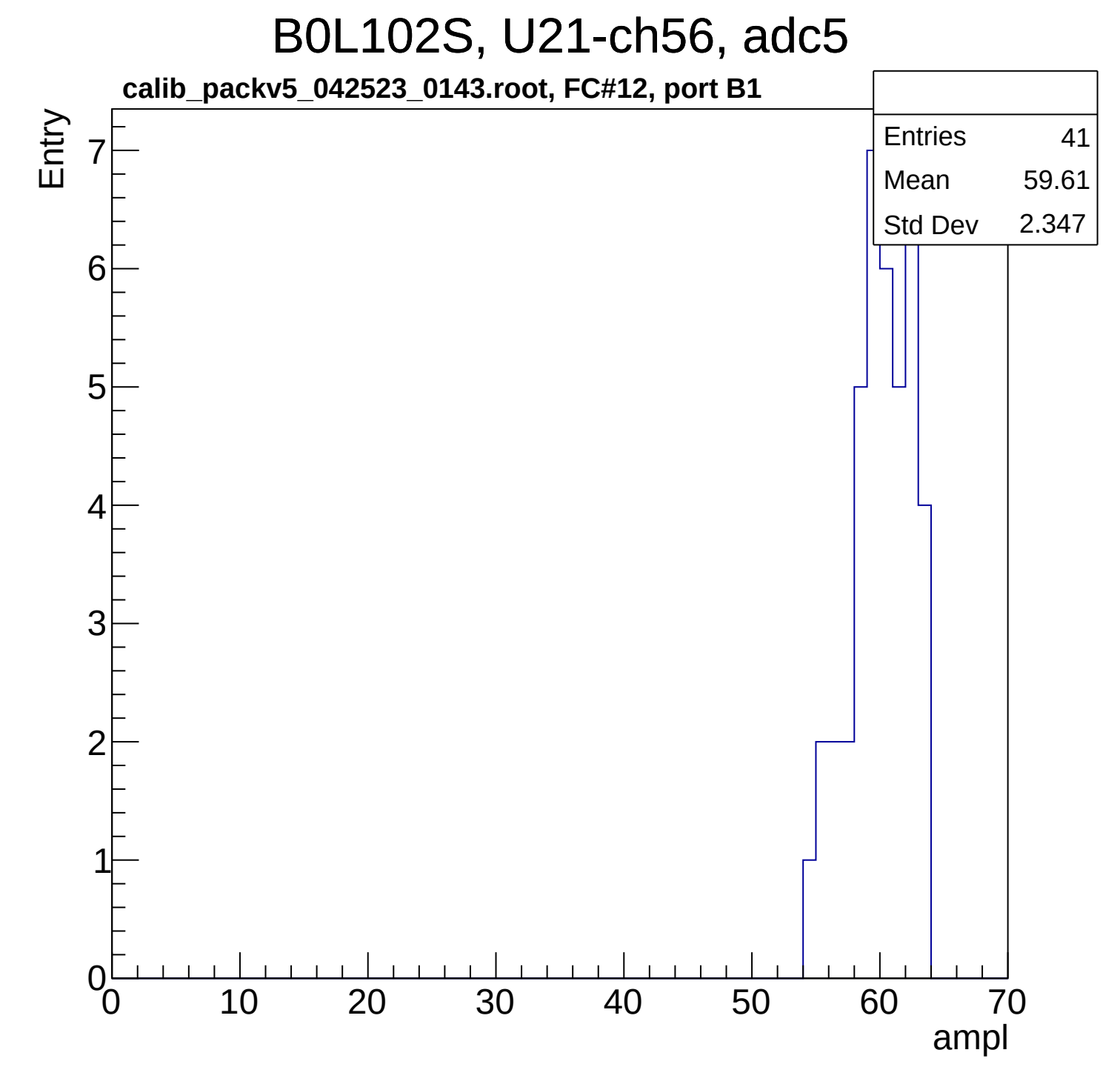
B0L102S, U21-ch56, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 59.61
Std Dev 2.347
Entry
ampl
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0
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40
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60
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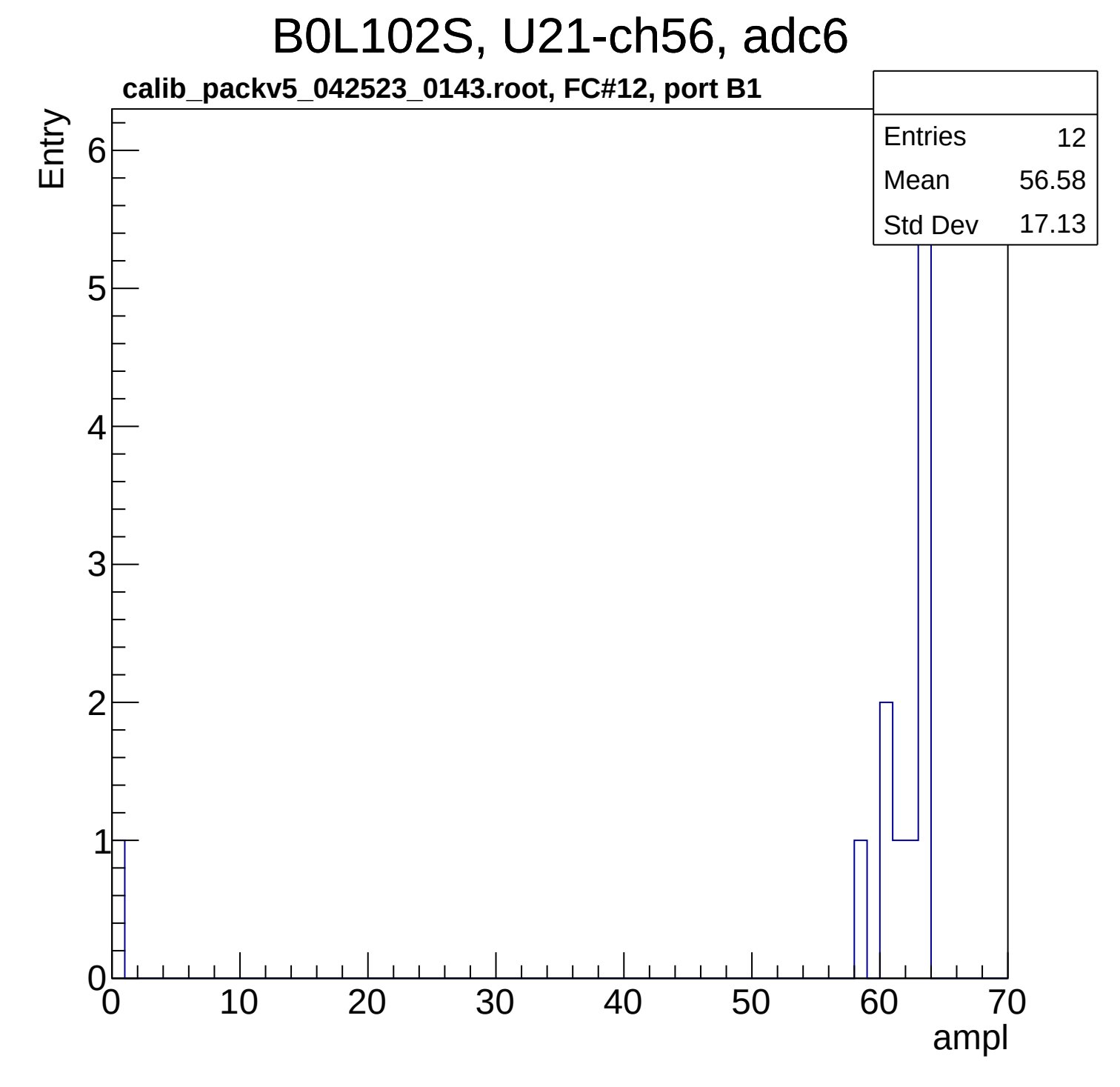
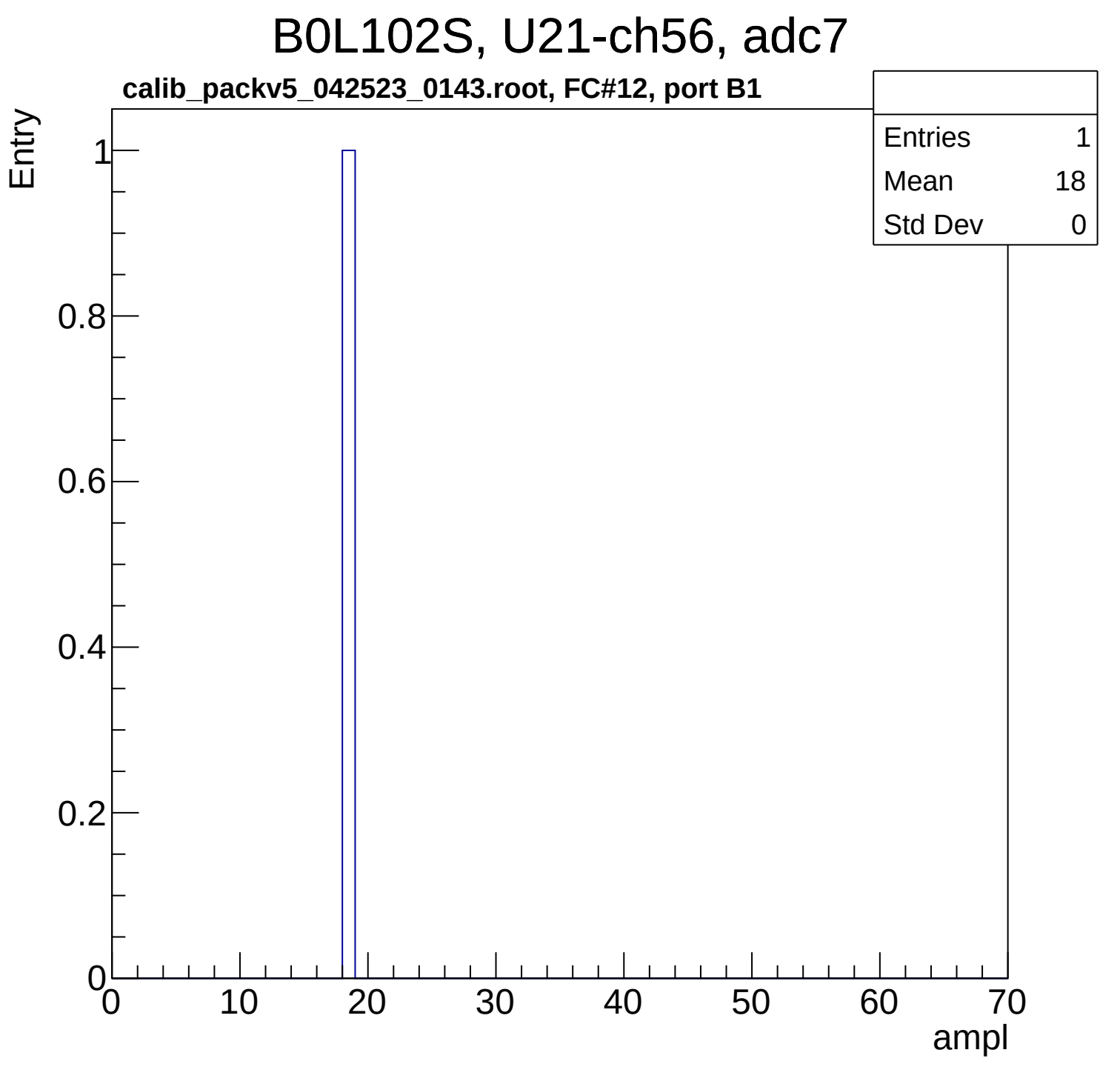

B0L102S, U21-ch56, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 56.58
Std Dev 17.13
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch56, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 18
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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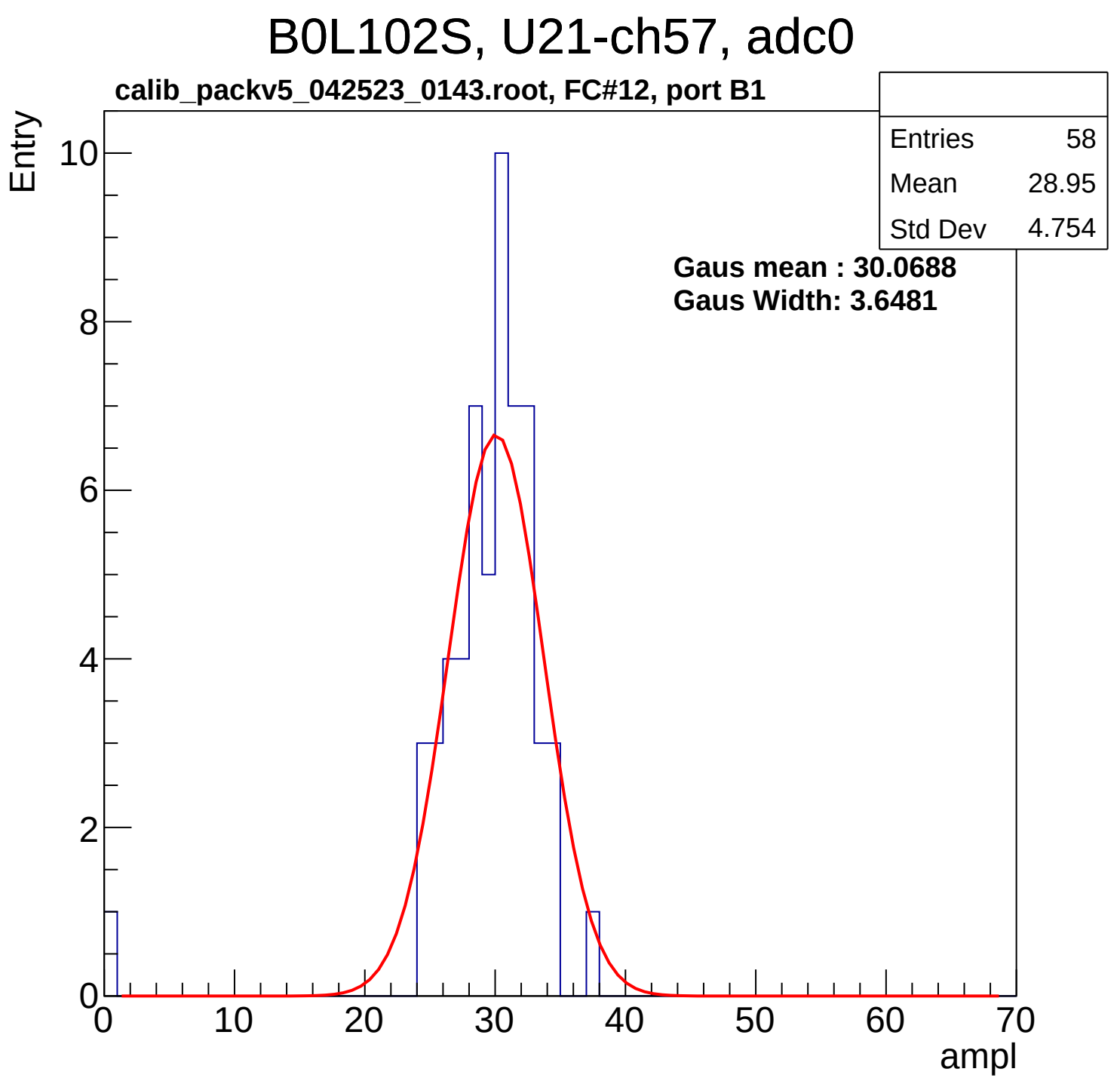
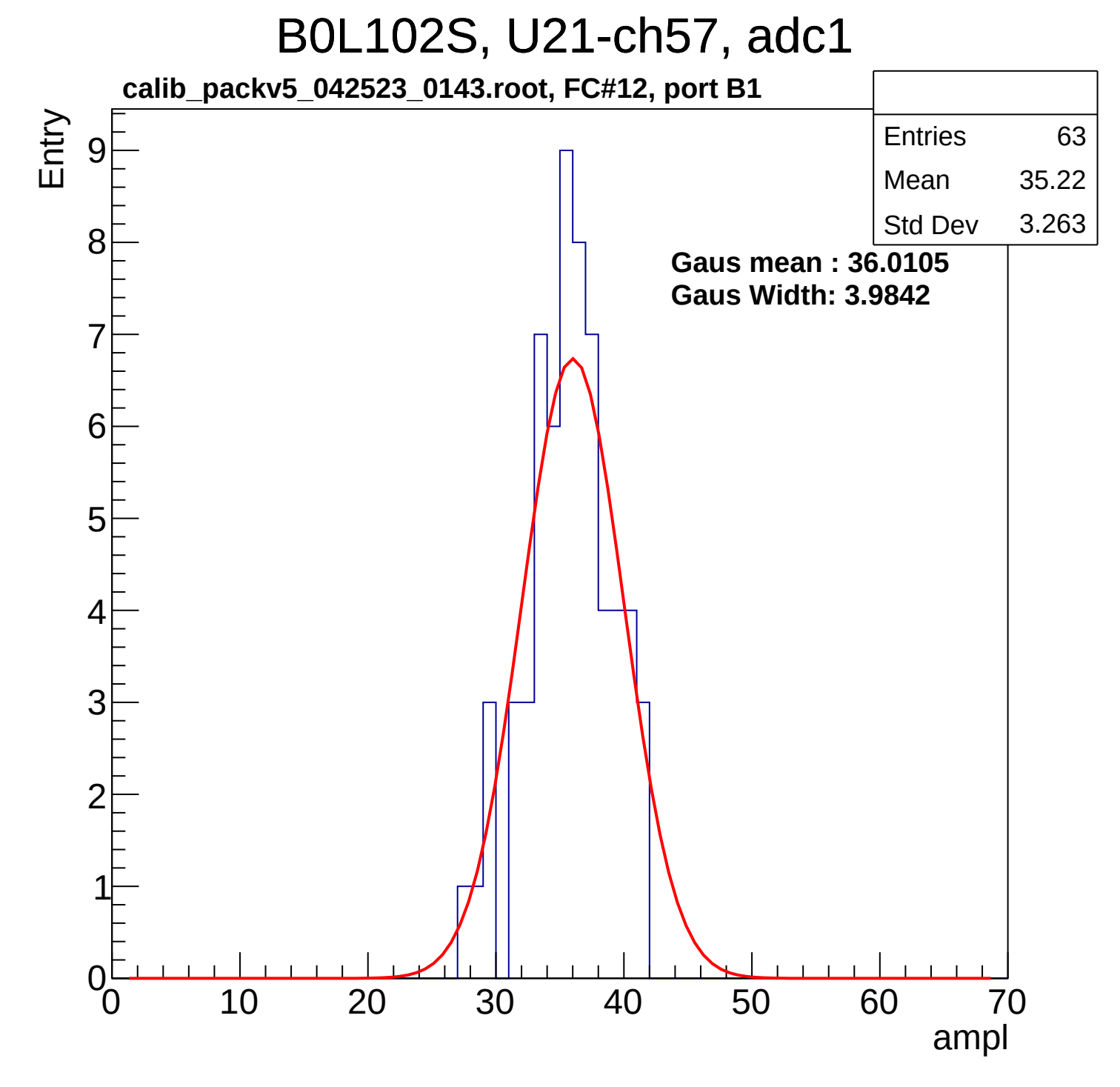
30
40
50
60
70
ampl


B0L102S, U21-ch57, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 28.95
Std Dev 4.754
Gaus mean : 30.0688
Gaus Width: 3.6481
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch57, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 35.22
Std Dev 3.263
Gaus mean : 36.0105
Gaus Width: 3.9842
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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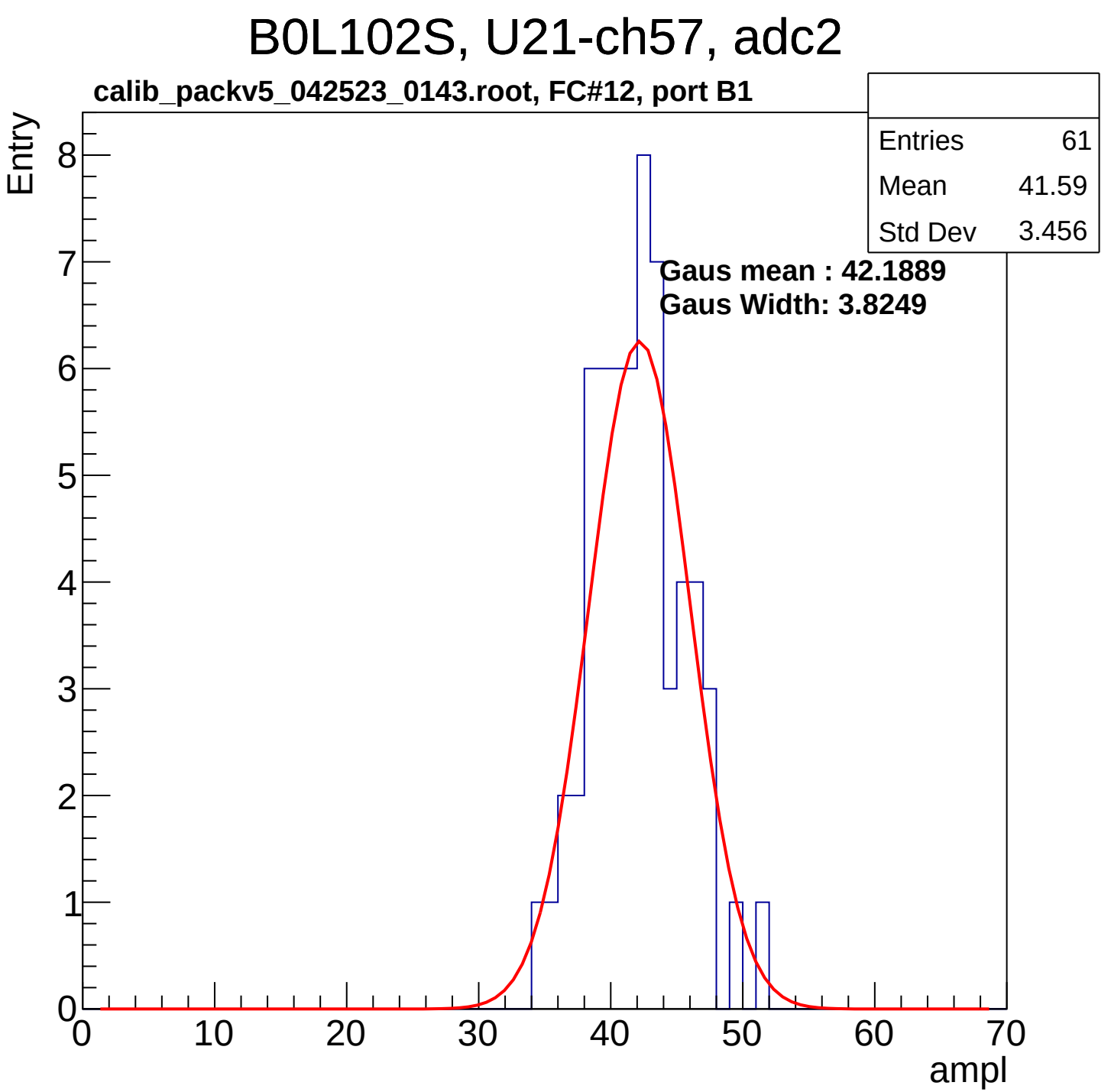
B0L102S, U21-ch57, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 41.59
Std Dev 3.456
Gaus mean : 42.1889
Gaus Width: 3.8249
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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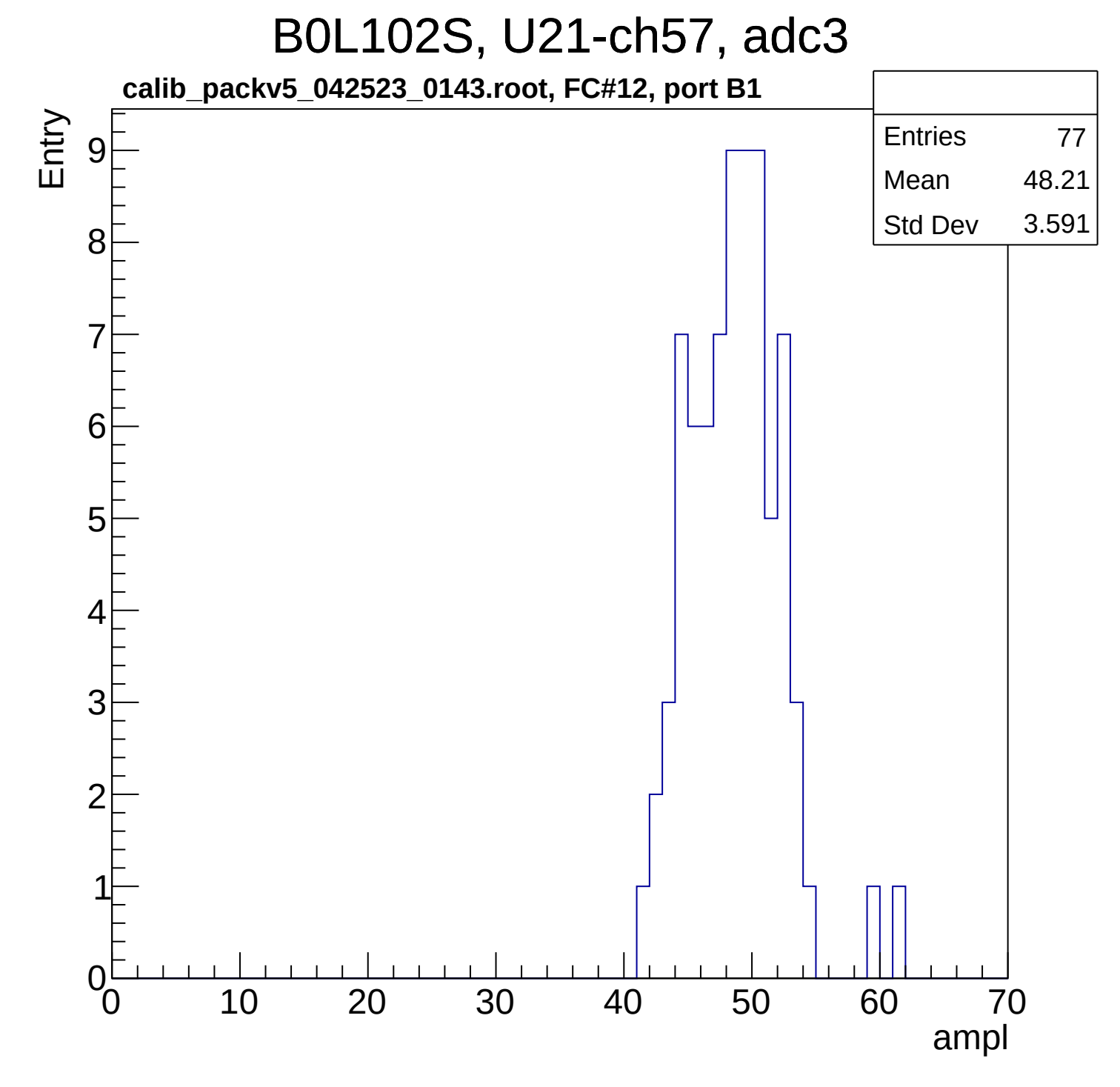
B0L102S, U21-ch57, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 48.21
Std Dev 3.591
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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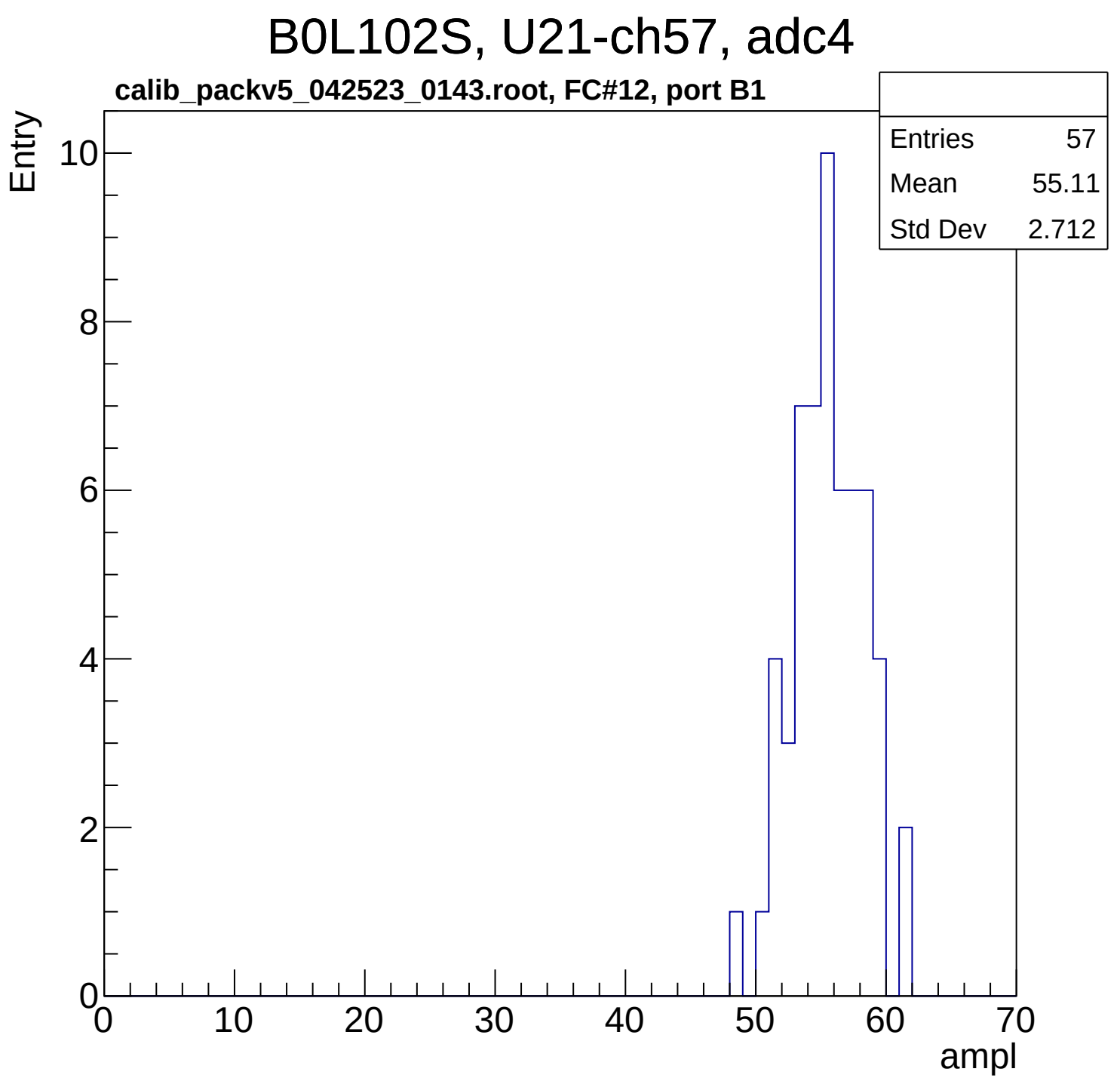
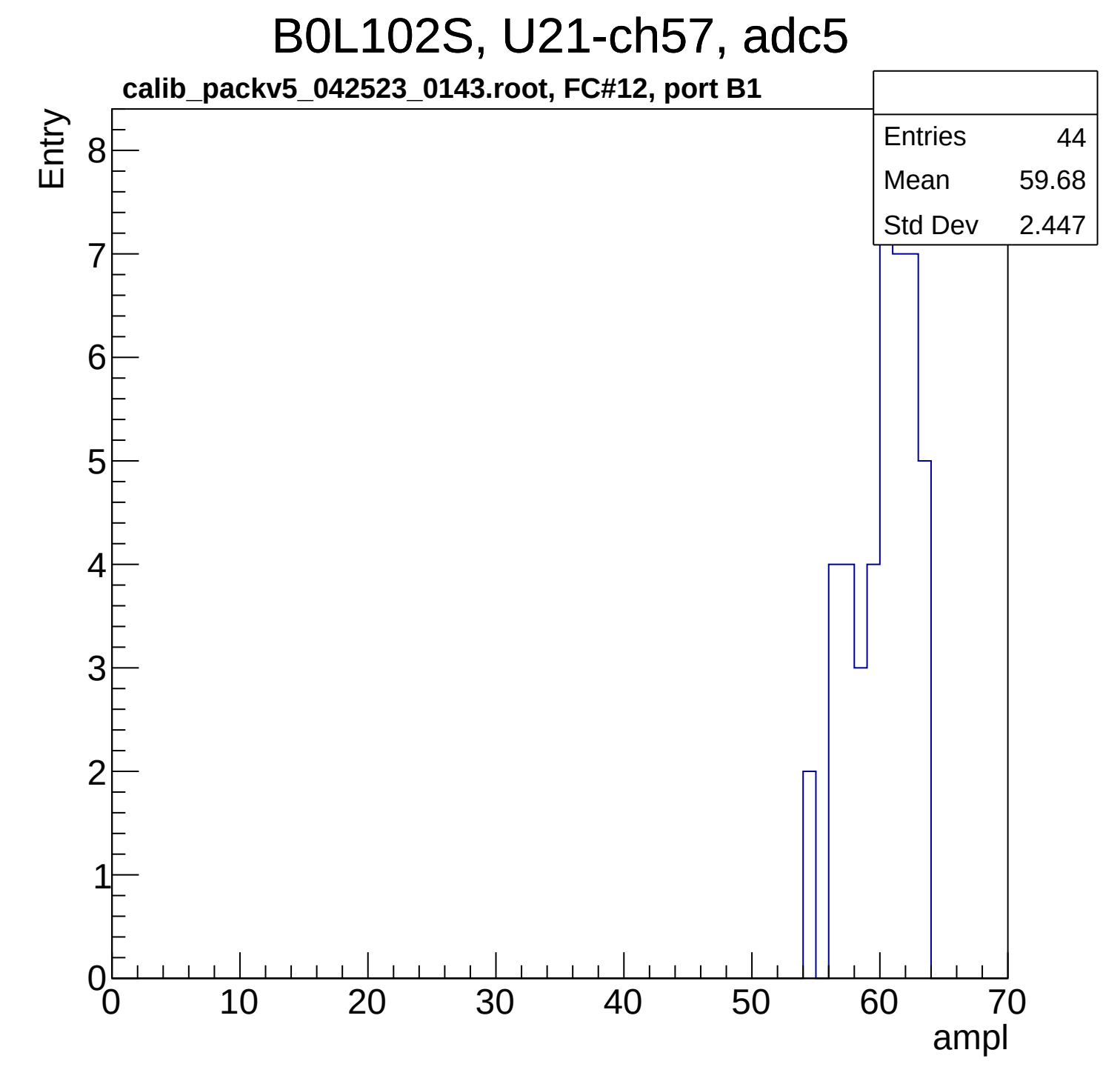

B0L102S, U21-ch57, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.11
Std Dev 2.712
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch57, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.68
Std Dev 2.447
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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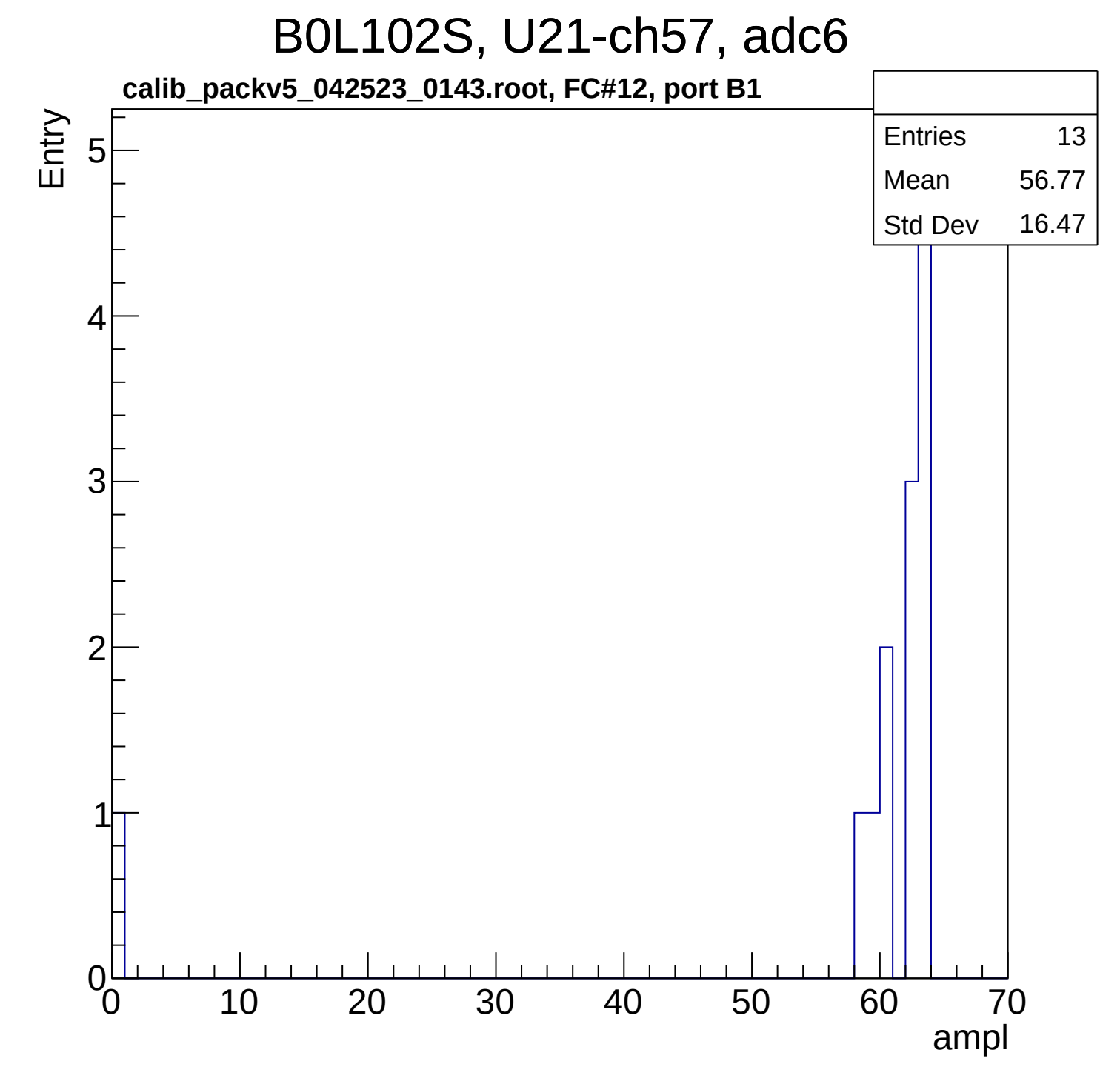
B0L102S, U21-ch57, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.77
Std Dev 16.47
Entry
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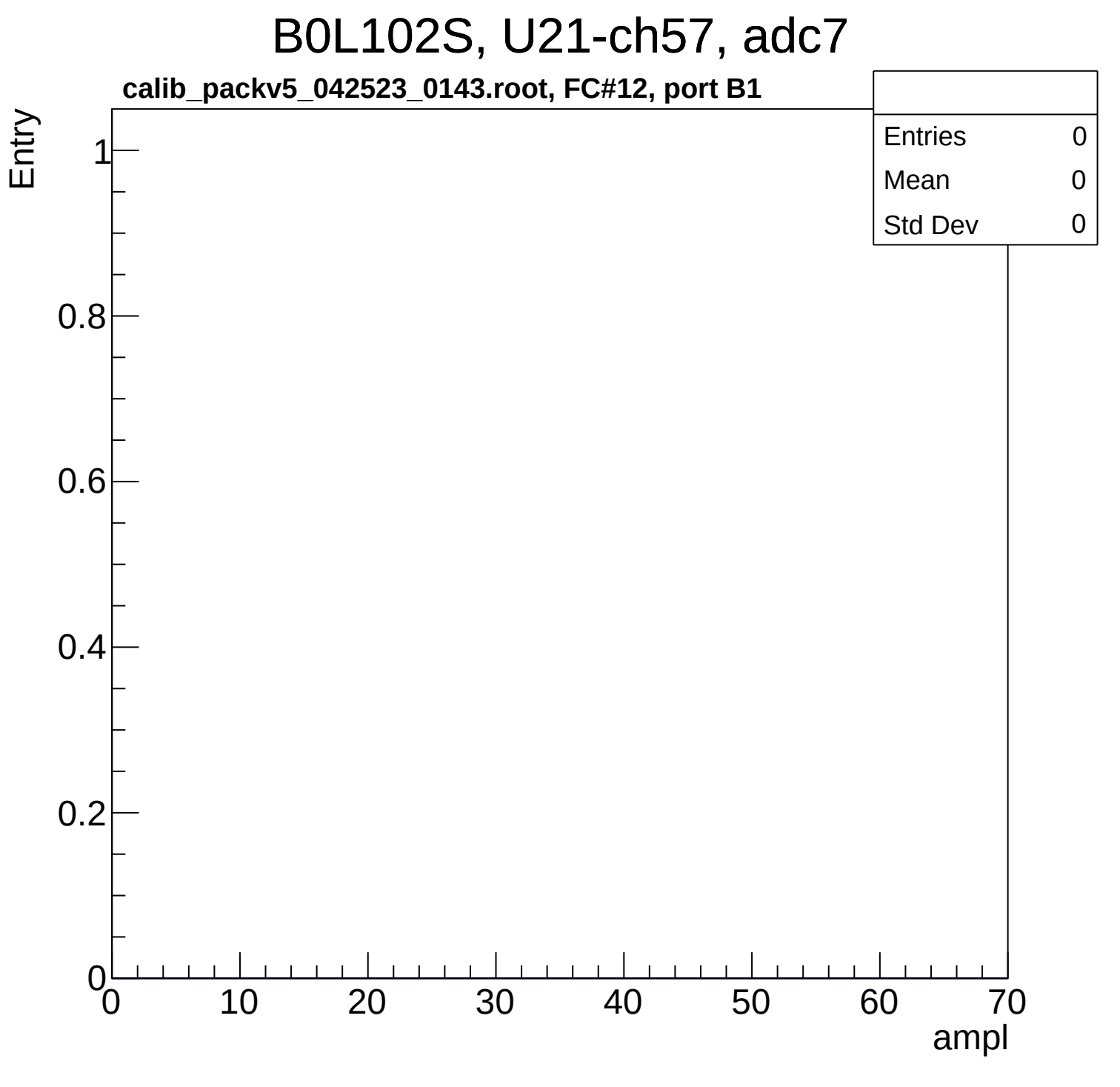
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch57, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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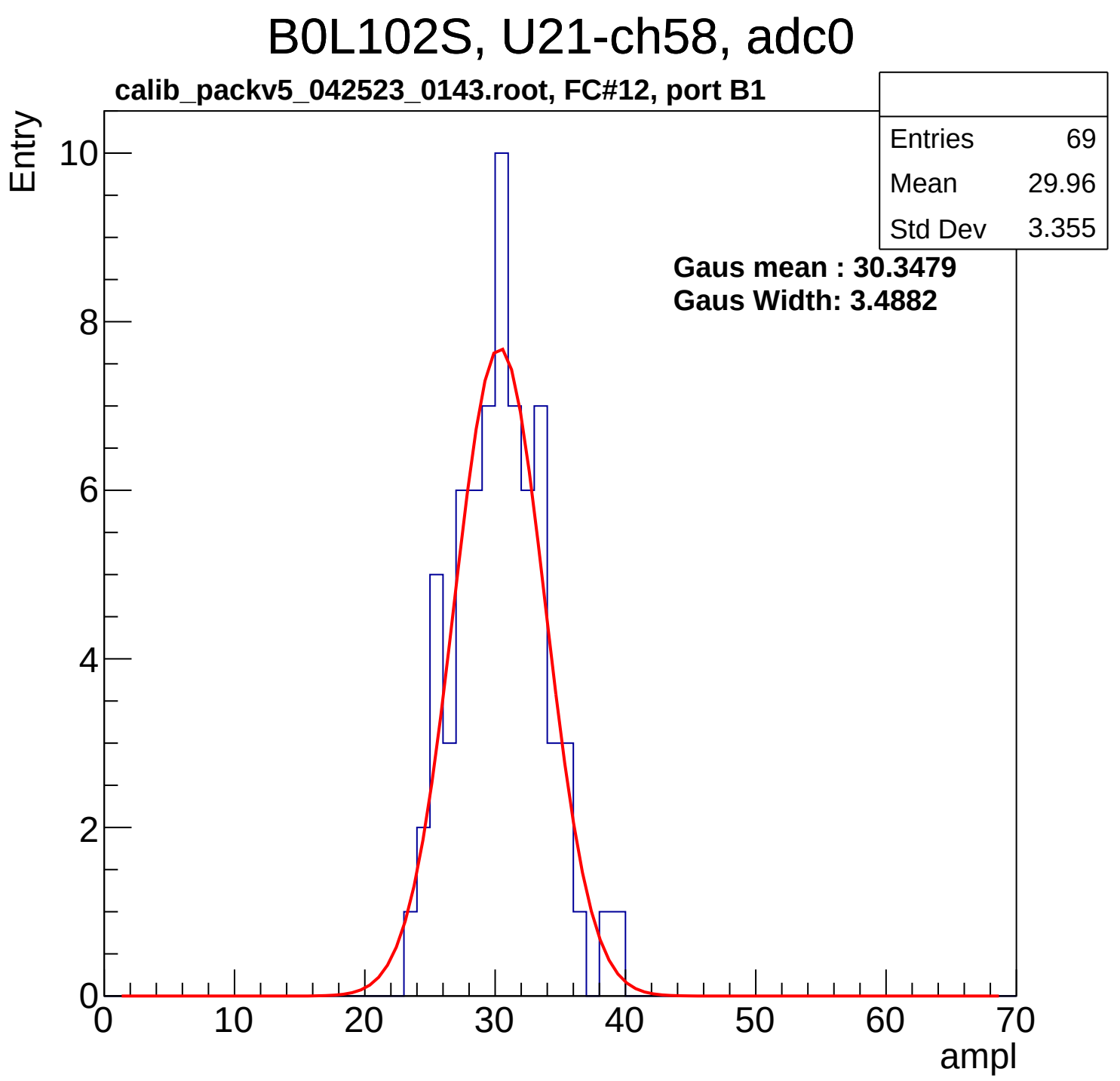
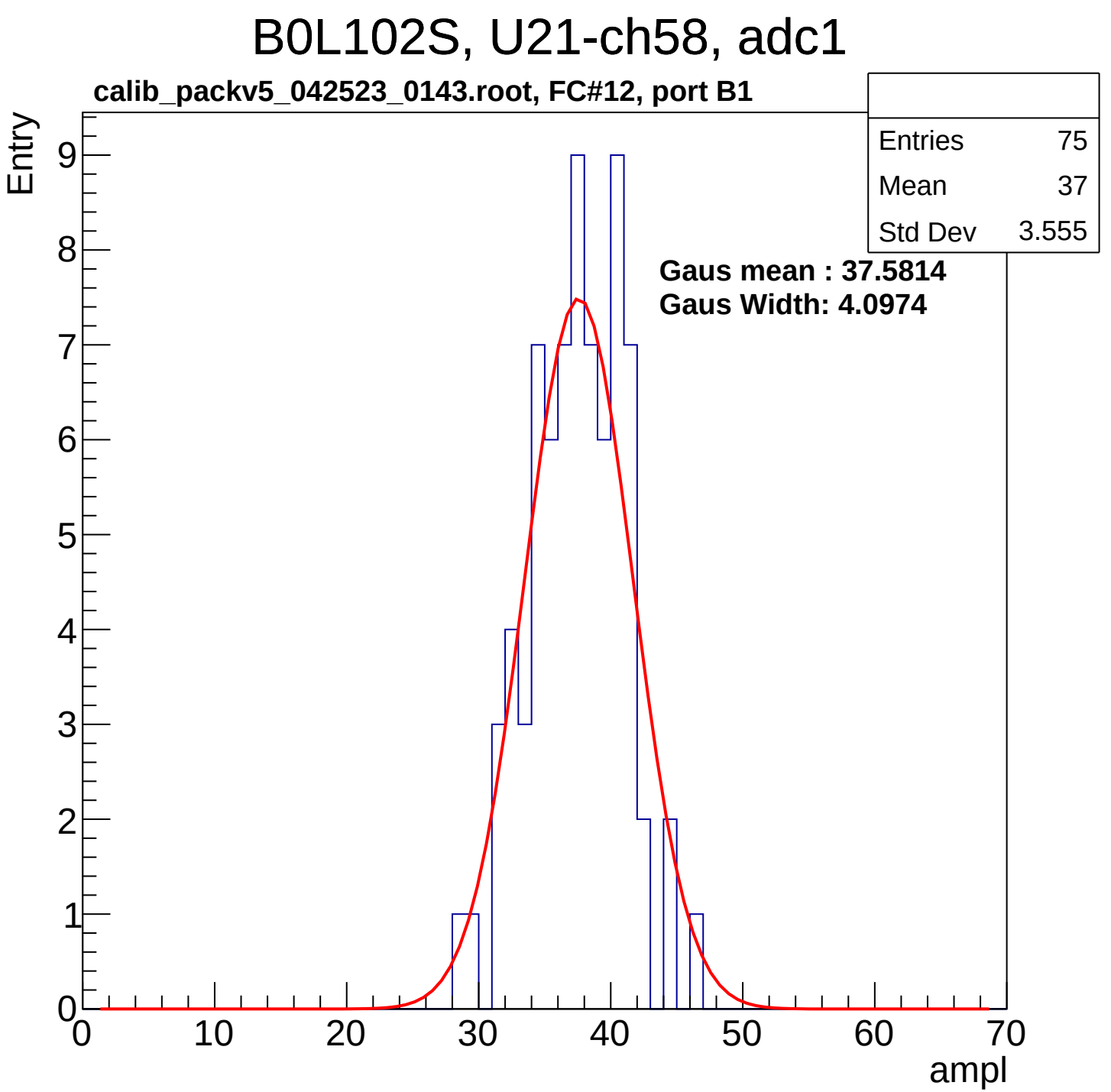
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch58, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 29.96
Std Dev 3.355
Gaus mean : 30.3479
Gaus Width: 3.4882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch58, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37
Std Dev 3.555
Gaus mean : 37.5814
Gaus Width: 4.0974
Entry
ampl
0
10
20
30
40
50
60
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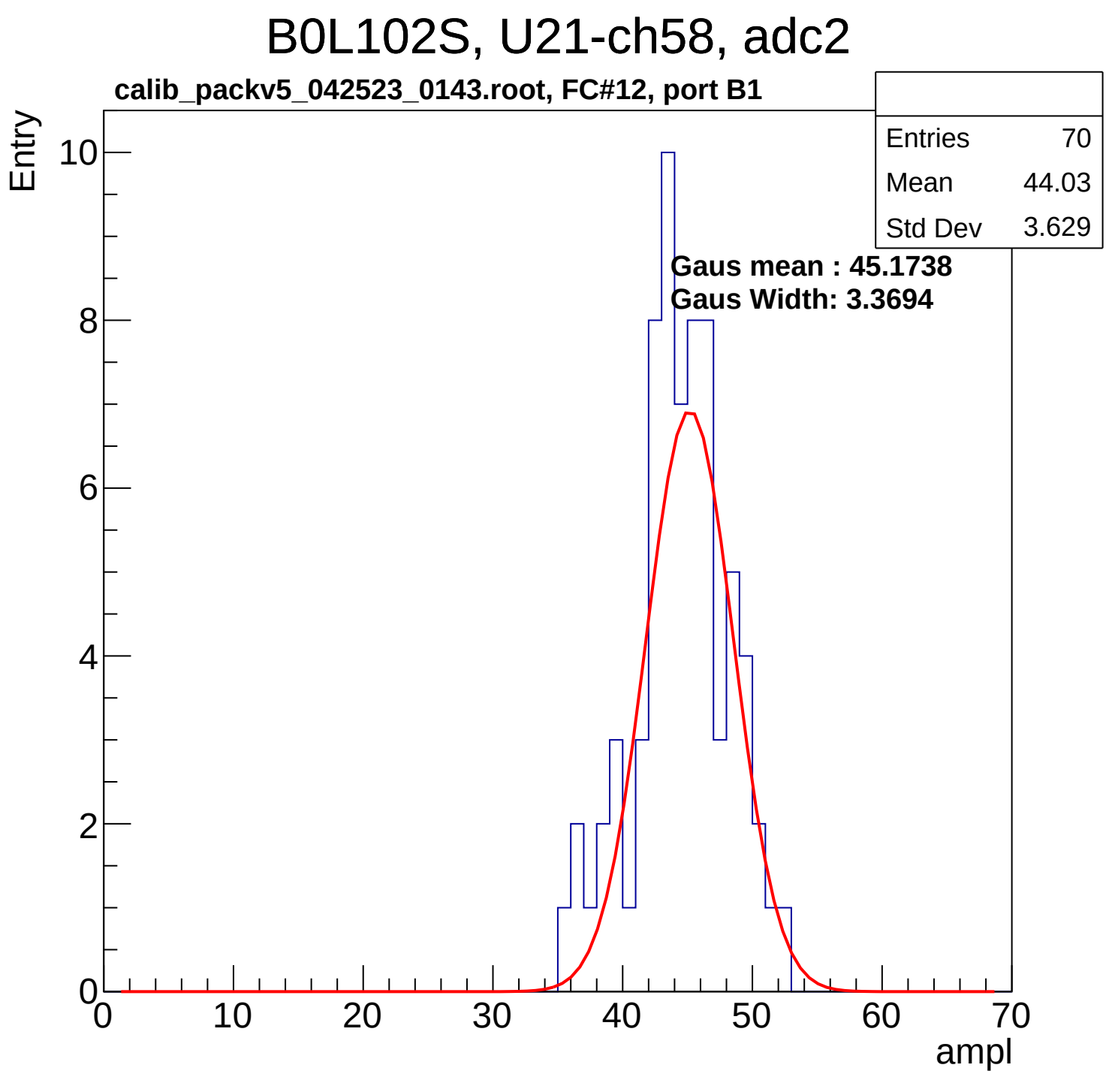
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch58, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 44.03
Std Dev 3.629
Gaus mean : 45.1738
Gaus Width: 3.3694
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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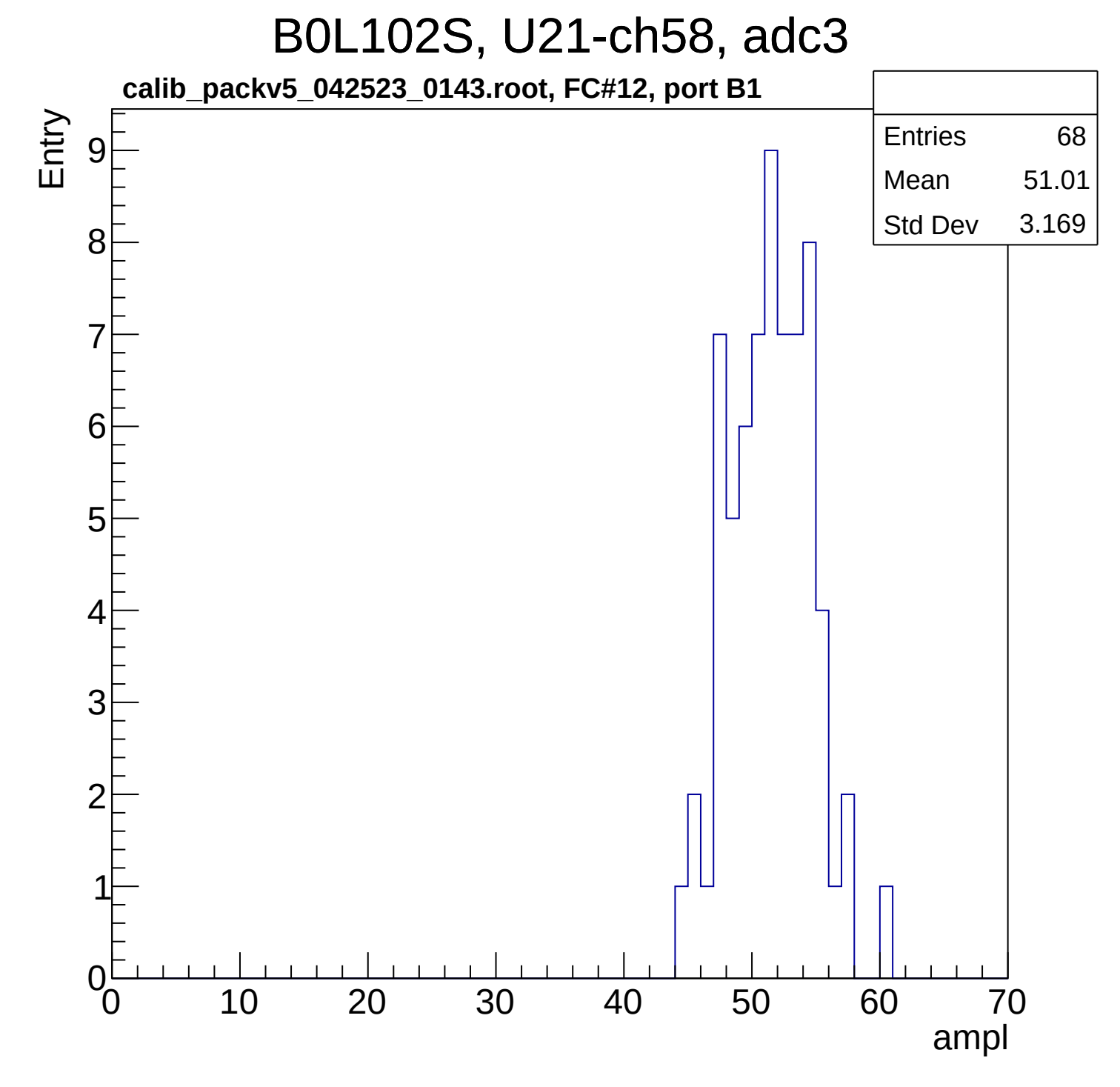
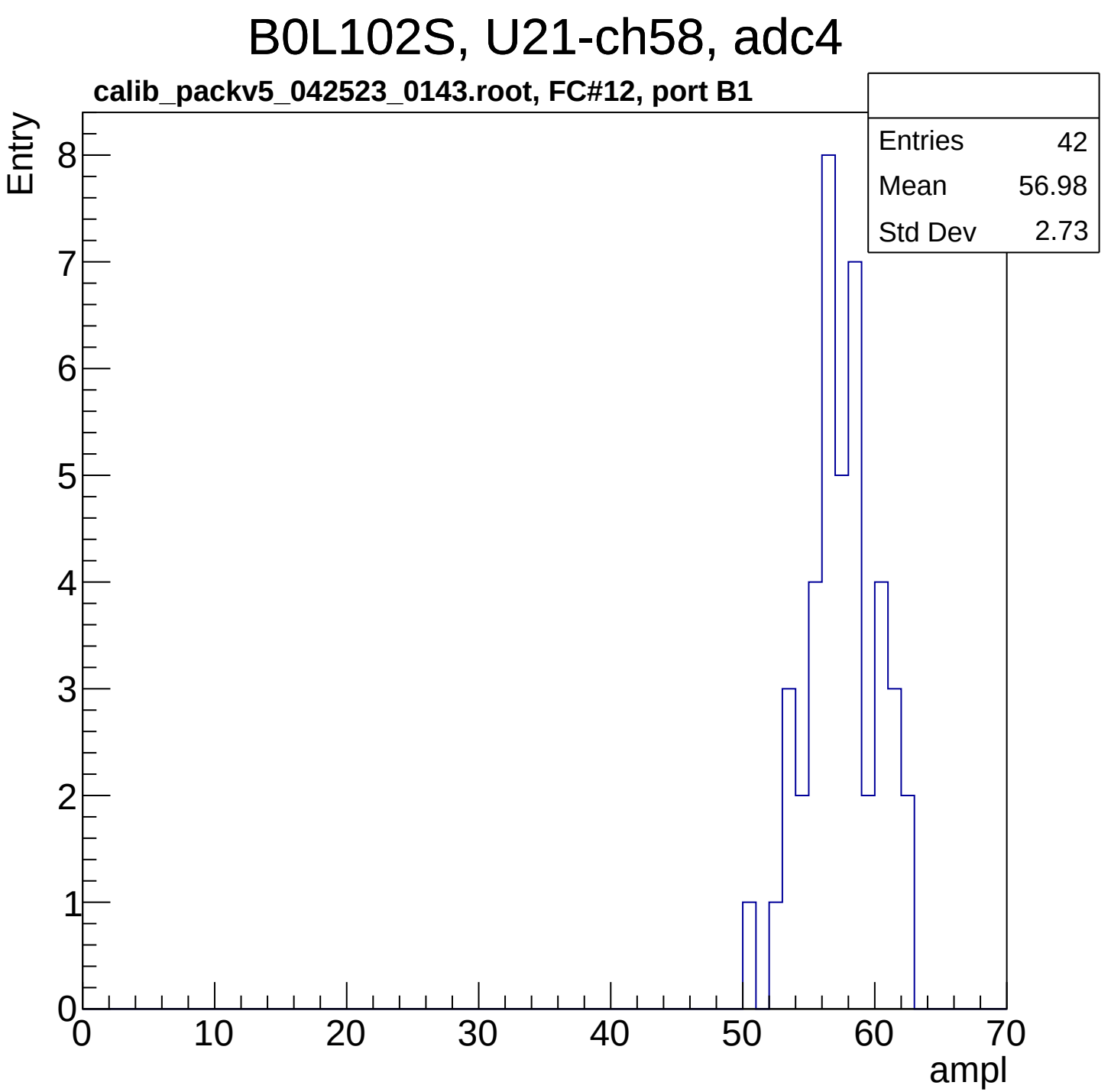
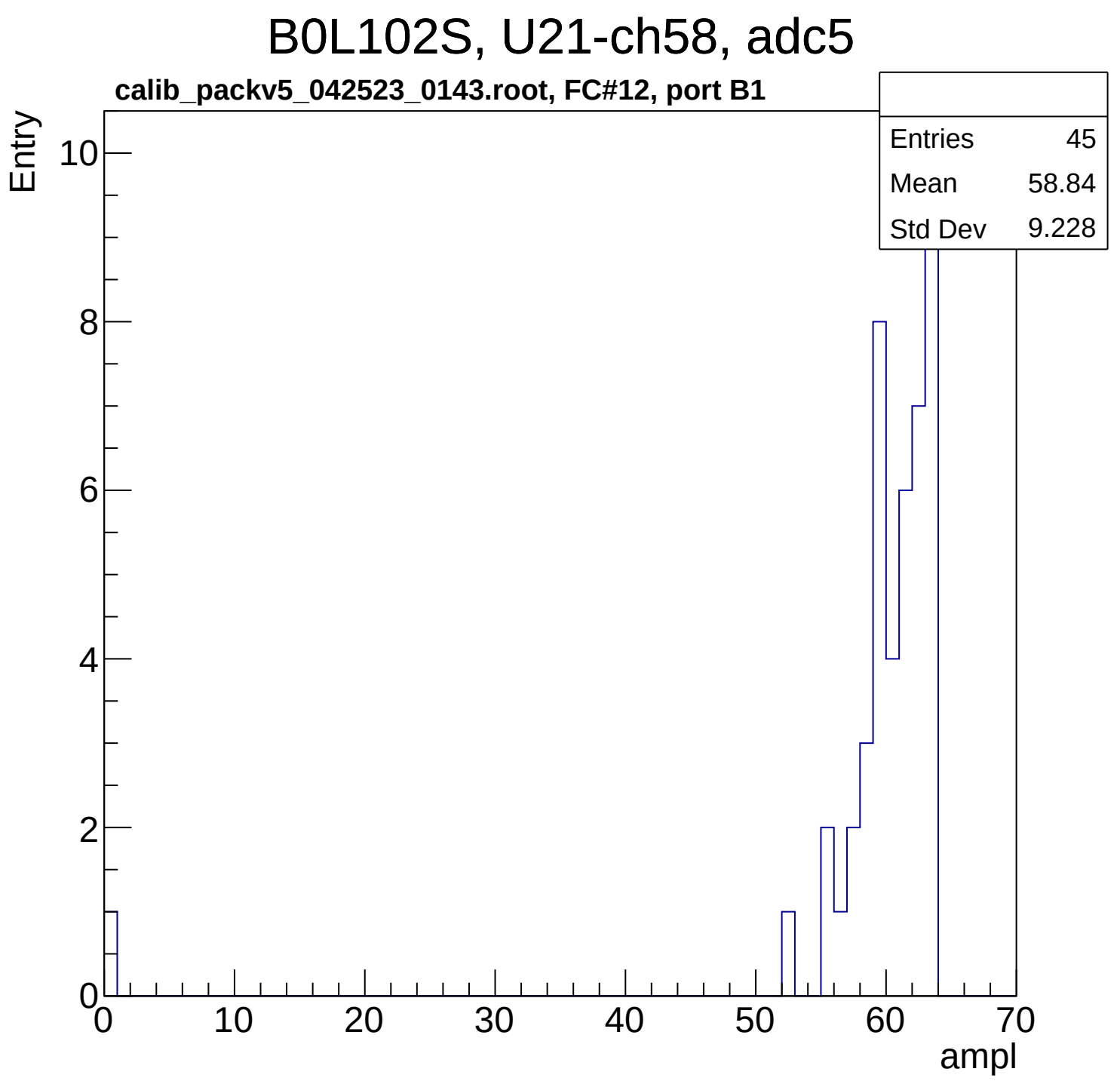
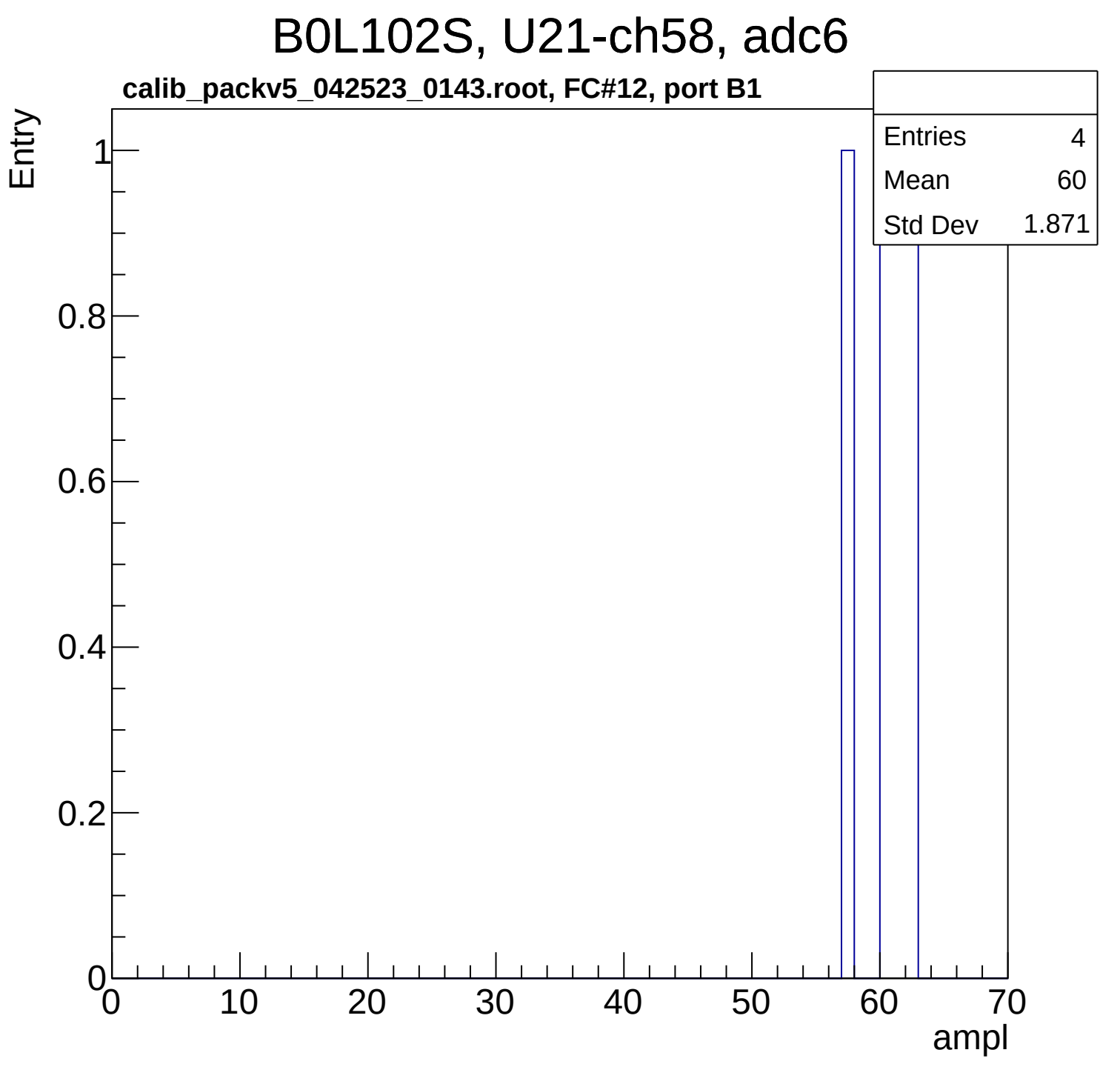
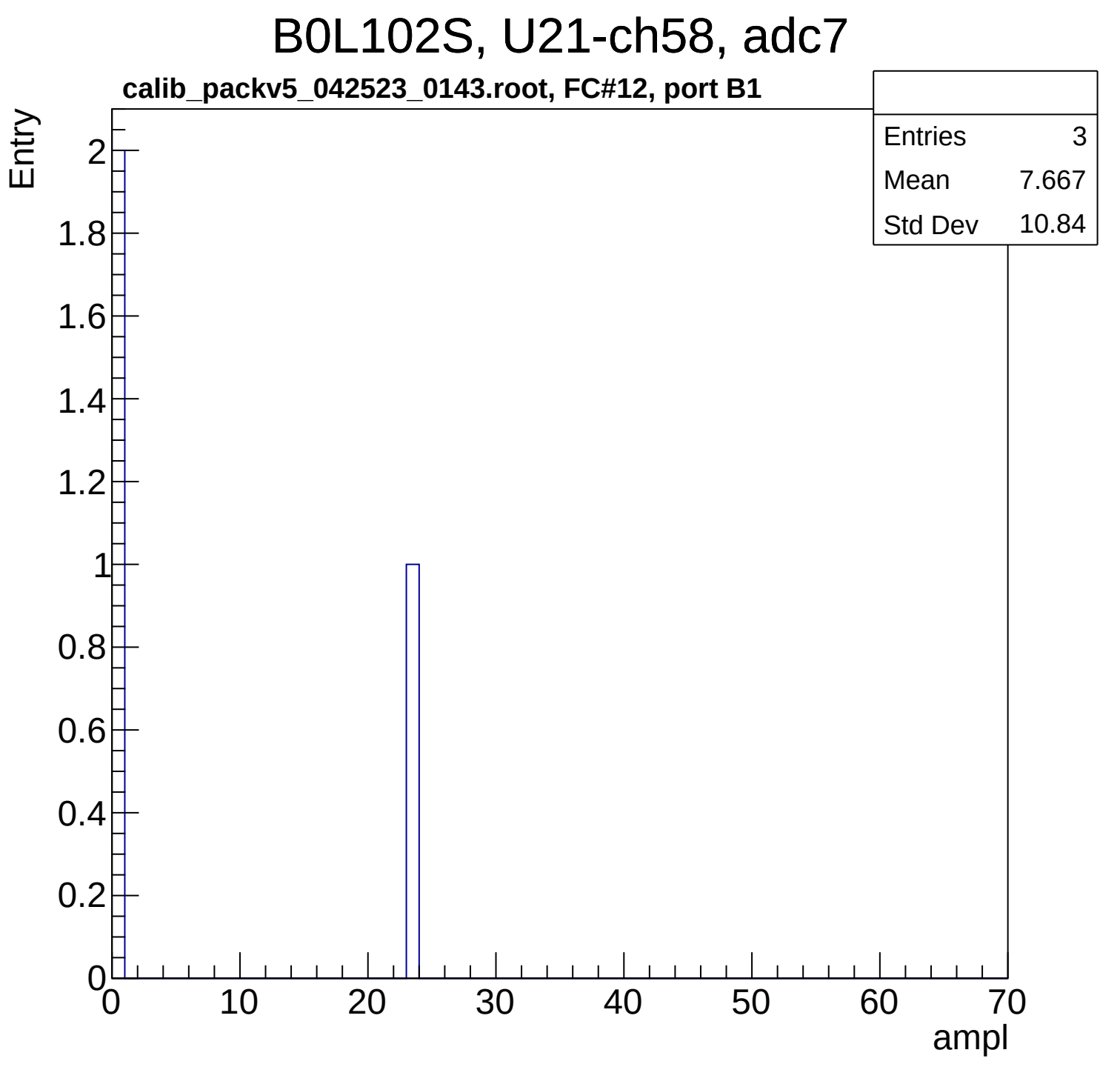
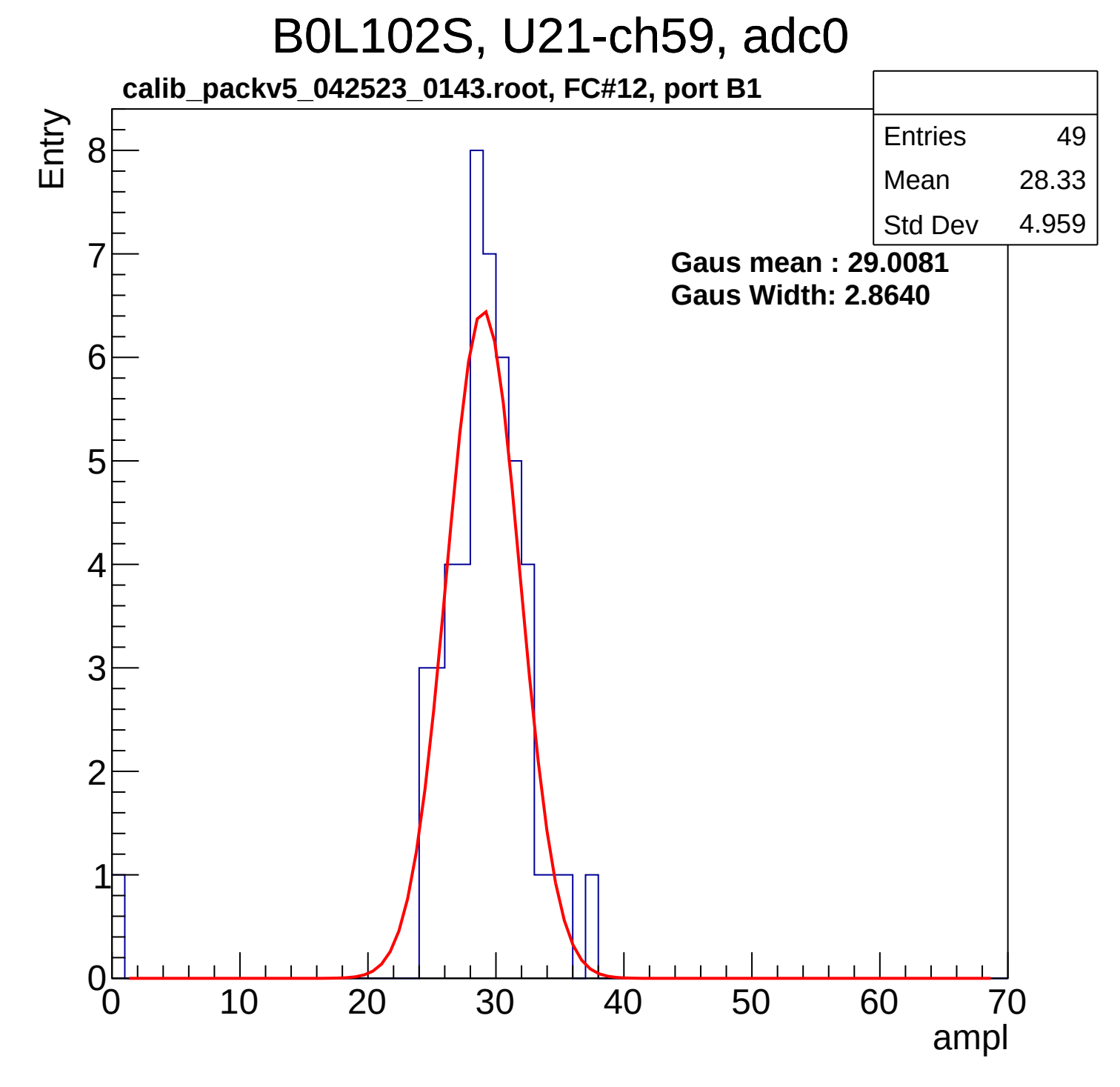

B0L102S, U21-ch58, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 51.01
Std Dev 3.169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

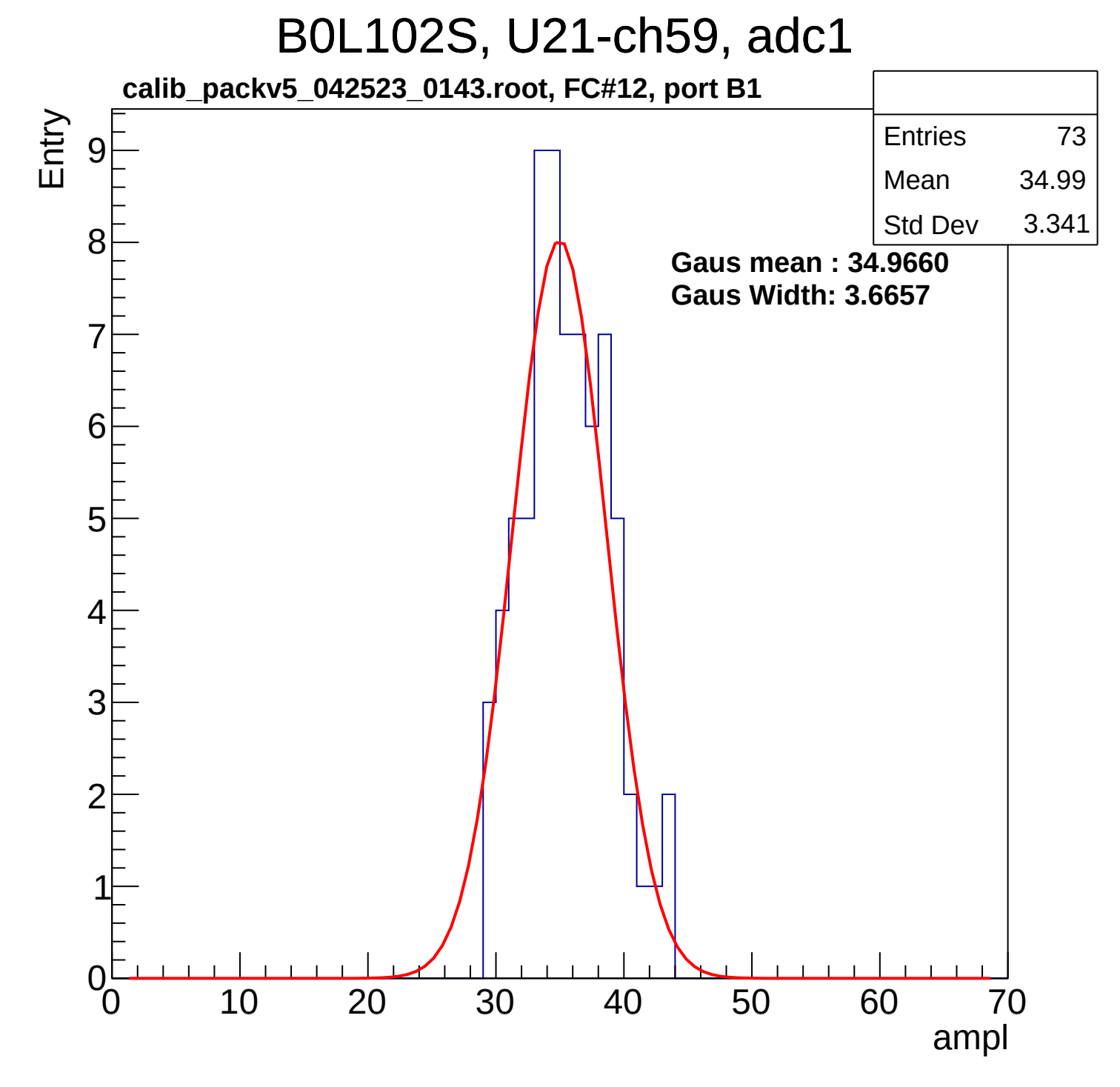

B0L102S, U21-ch58, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 56.98
Std Dev 2.73
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

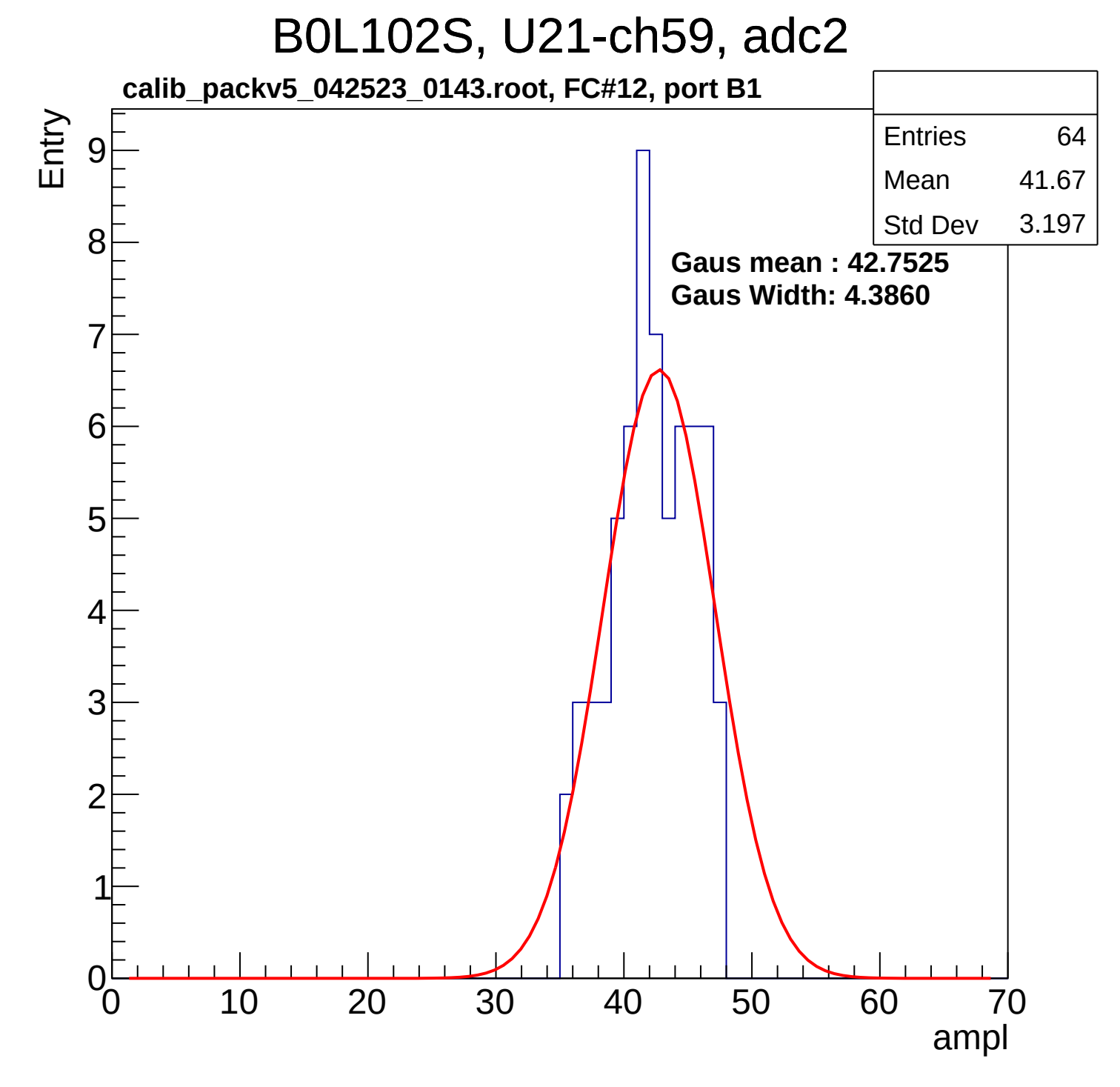

B0L102S, U21-ch58, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.84
Std Dev 9.228
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

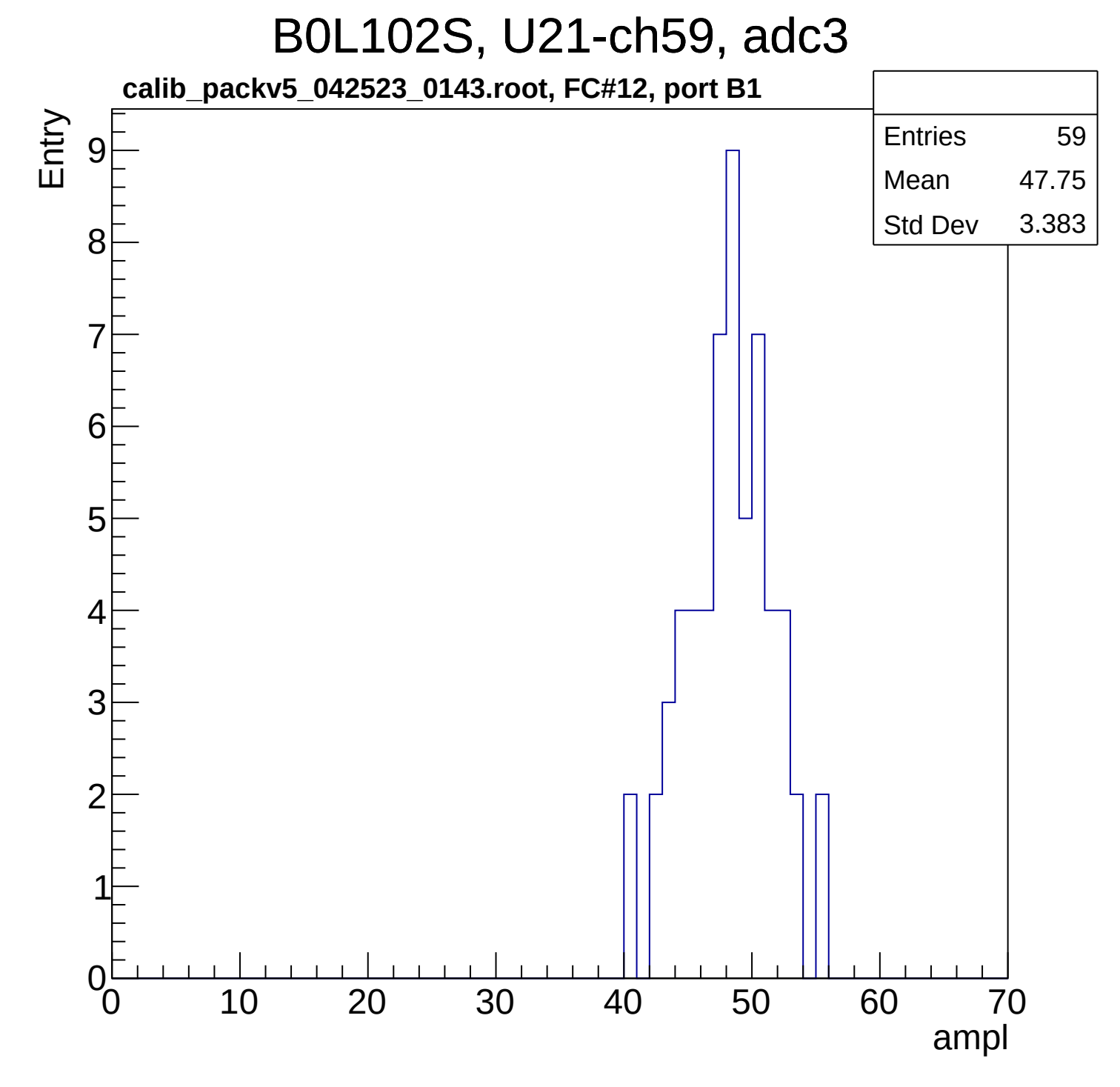

B0L102S, U21-ch58, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 60
Std Dev 1.871
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

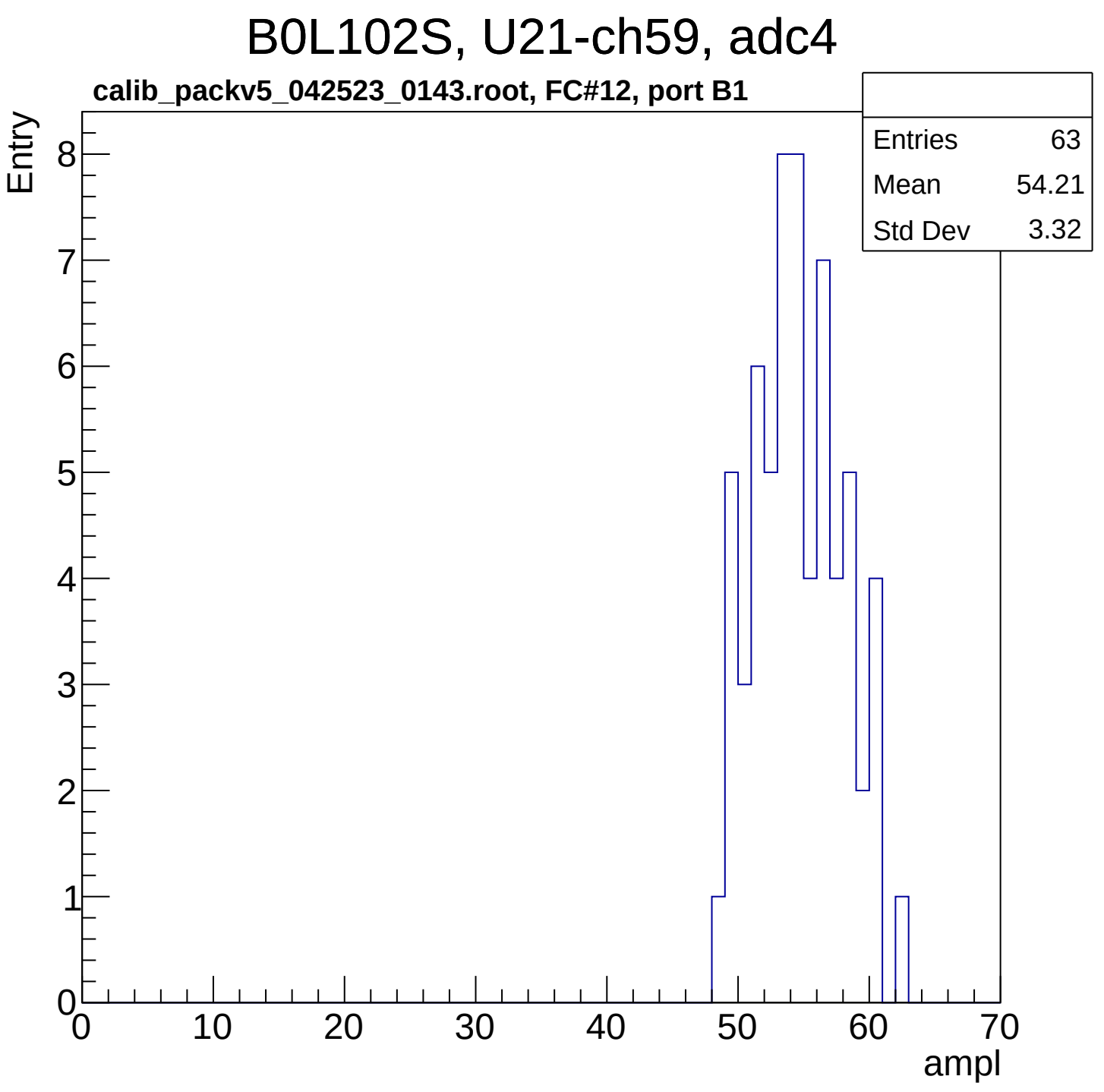

B0L102S, U21-ch58, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

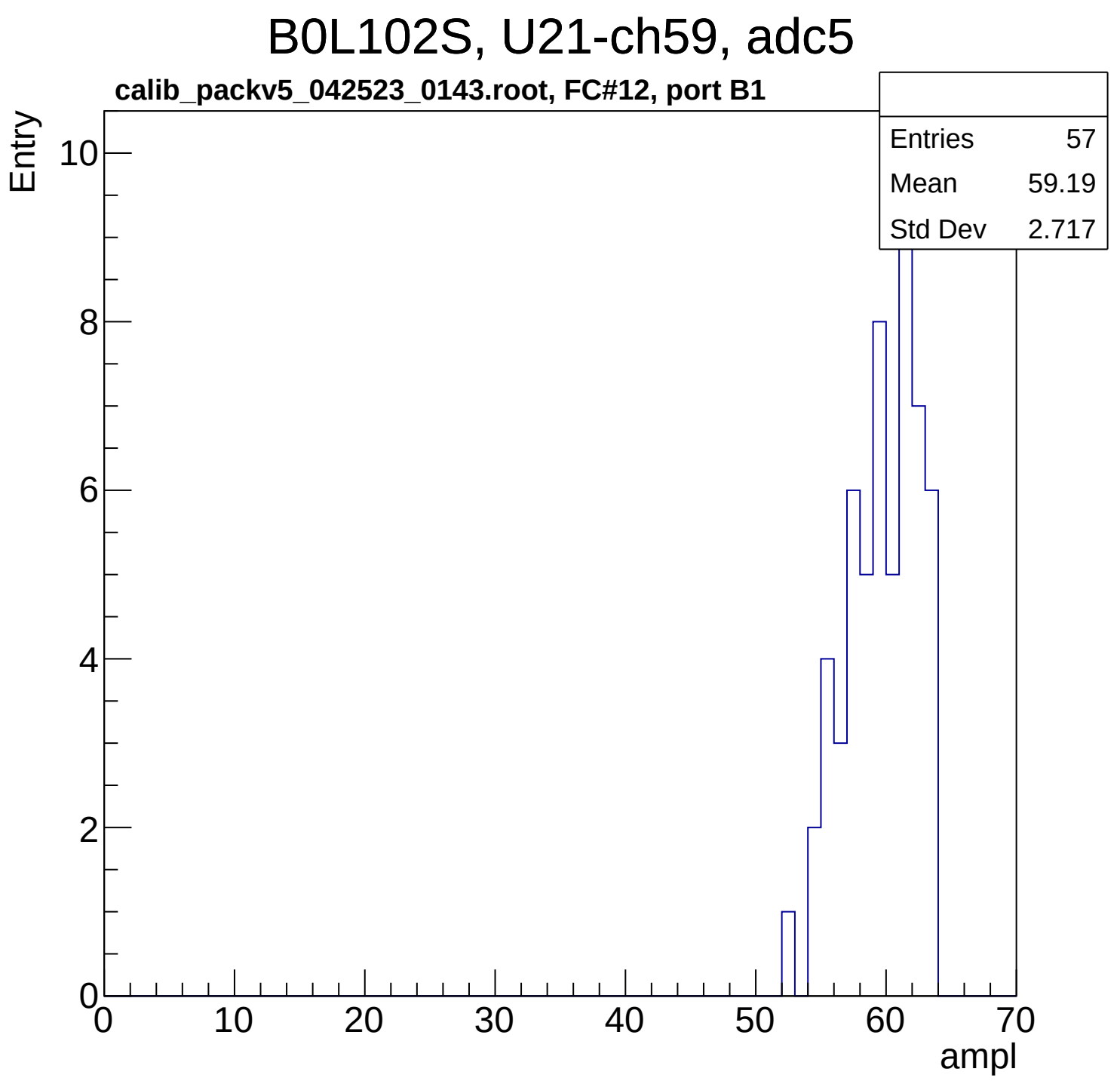

B0L102S, U21-ch59, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 28.33
Std Dev 4.959
Gaus mean : 29.0081
Gaus Width: 2.8640
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

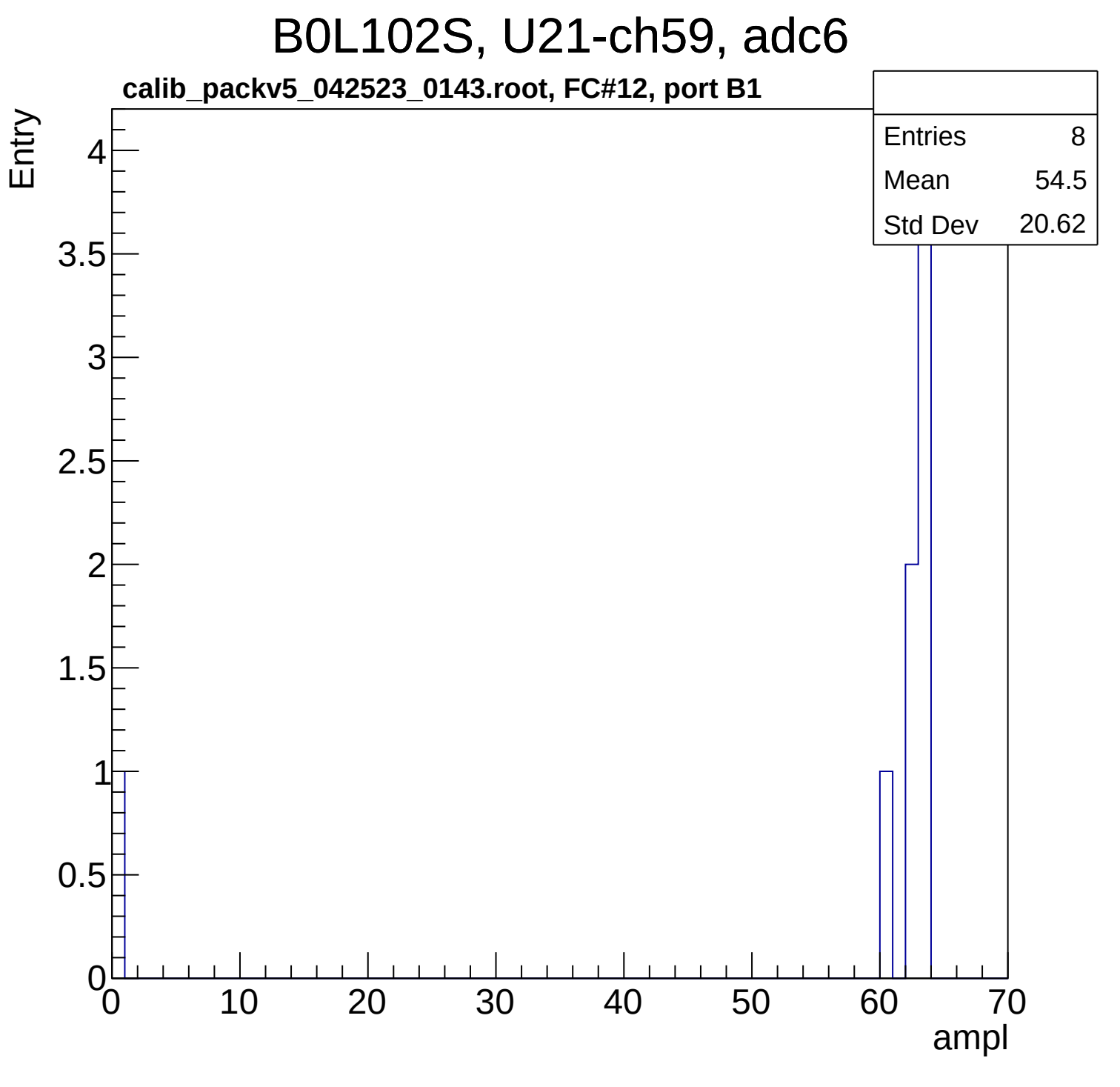

B0L102S, U21-ch59, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 34.99
Std Dev 3.341
Gaus mean : 34.9660
Gaus Width: 3.6657
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

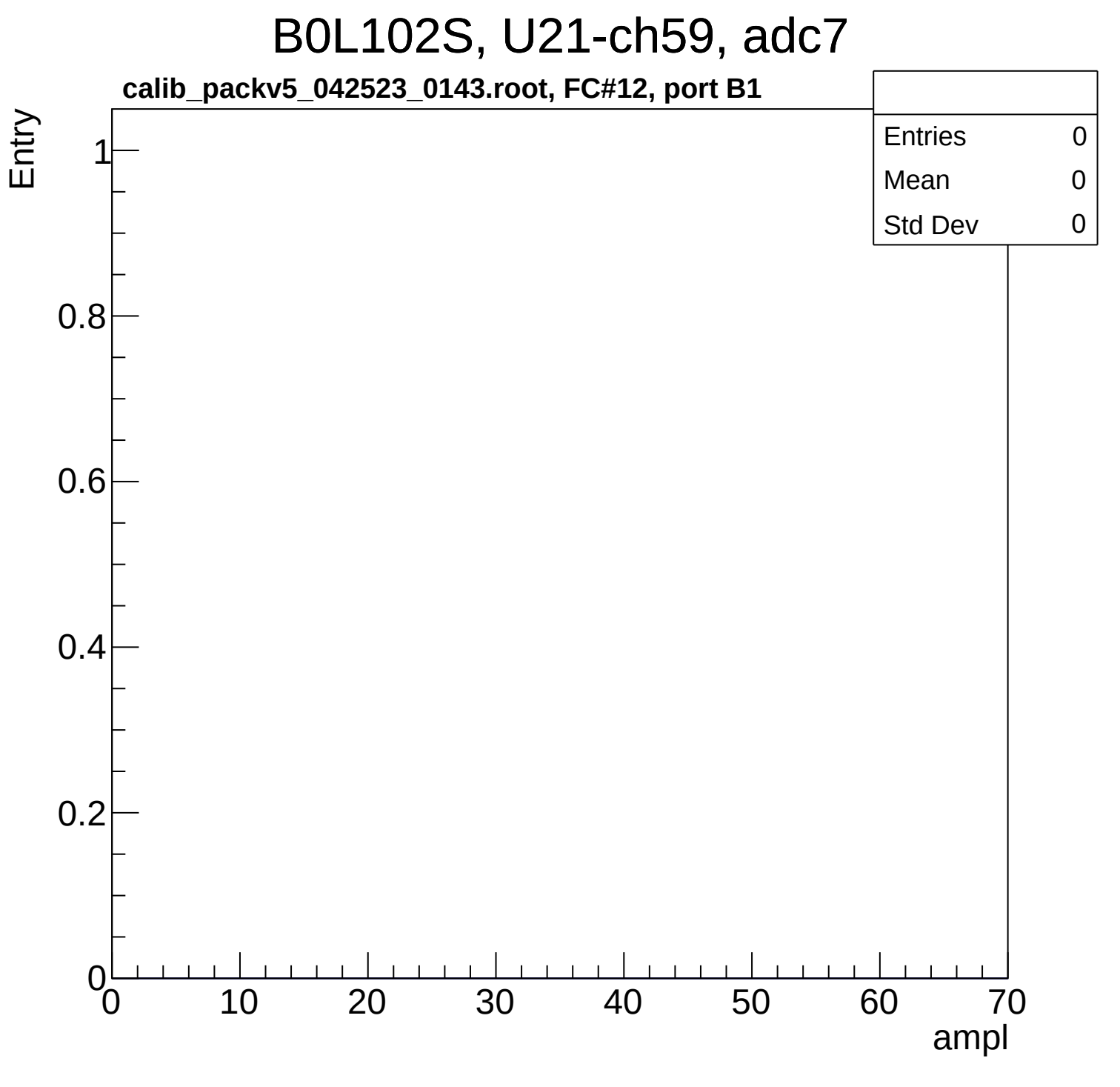

B0L102S, U21-ch59, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 41.67
Std Dev 3.197
Gaus mean : 42.7525
Gaus Width: 4.3860
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

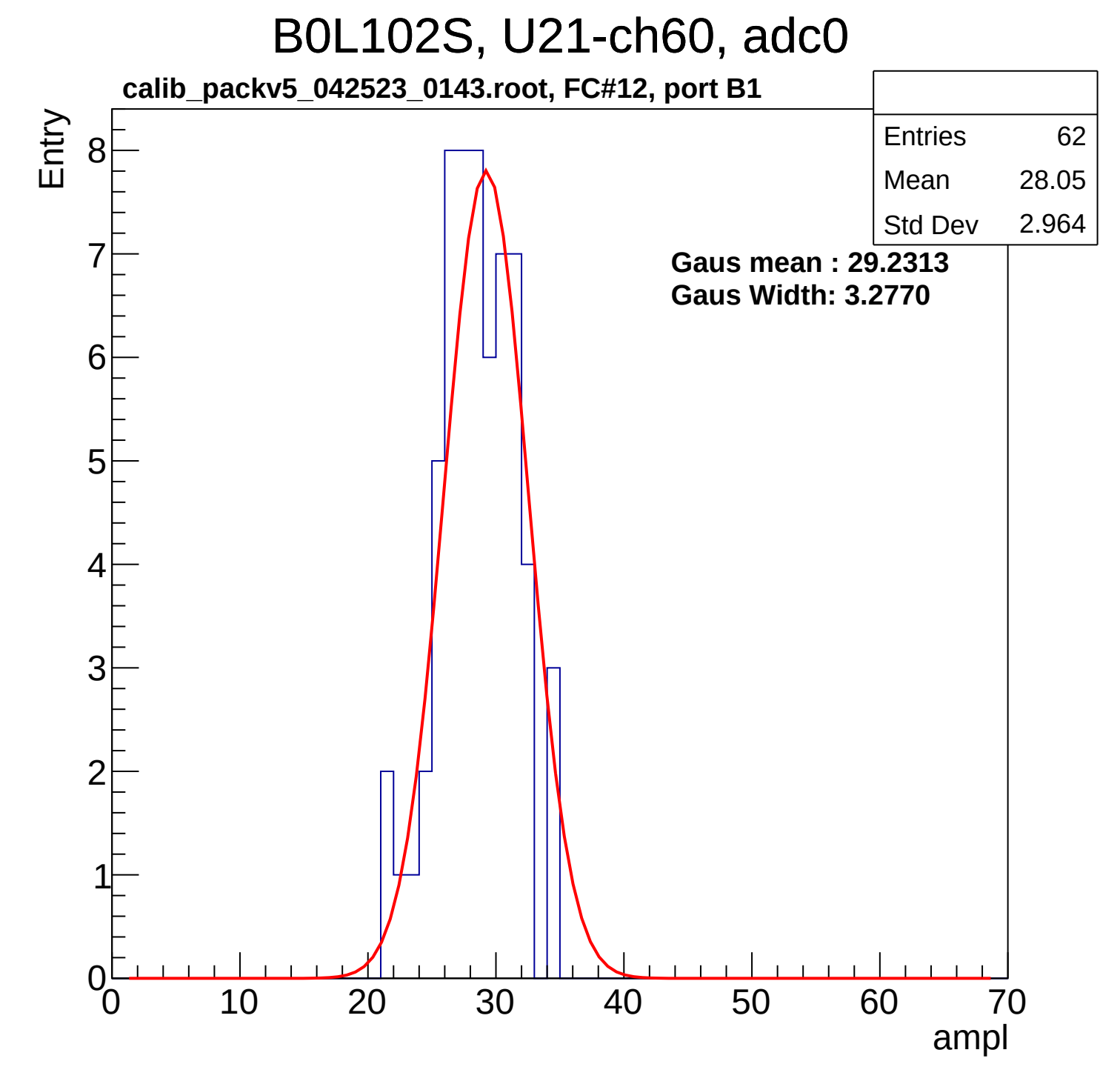

B0L102S, U21-ch59, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 47.75
Std Dev 3.383
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch59, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54.21
Std Dev 3.32
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch59, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 59.19
Std Dev 2.717
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch59, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 54.5
Std Dev 20.62
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch59, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch60, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 28.05
Std Dev 2.964
Gaus mean : 29.2313
Gaus Width: 3.2770
Entry
ampl
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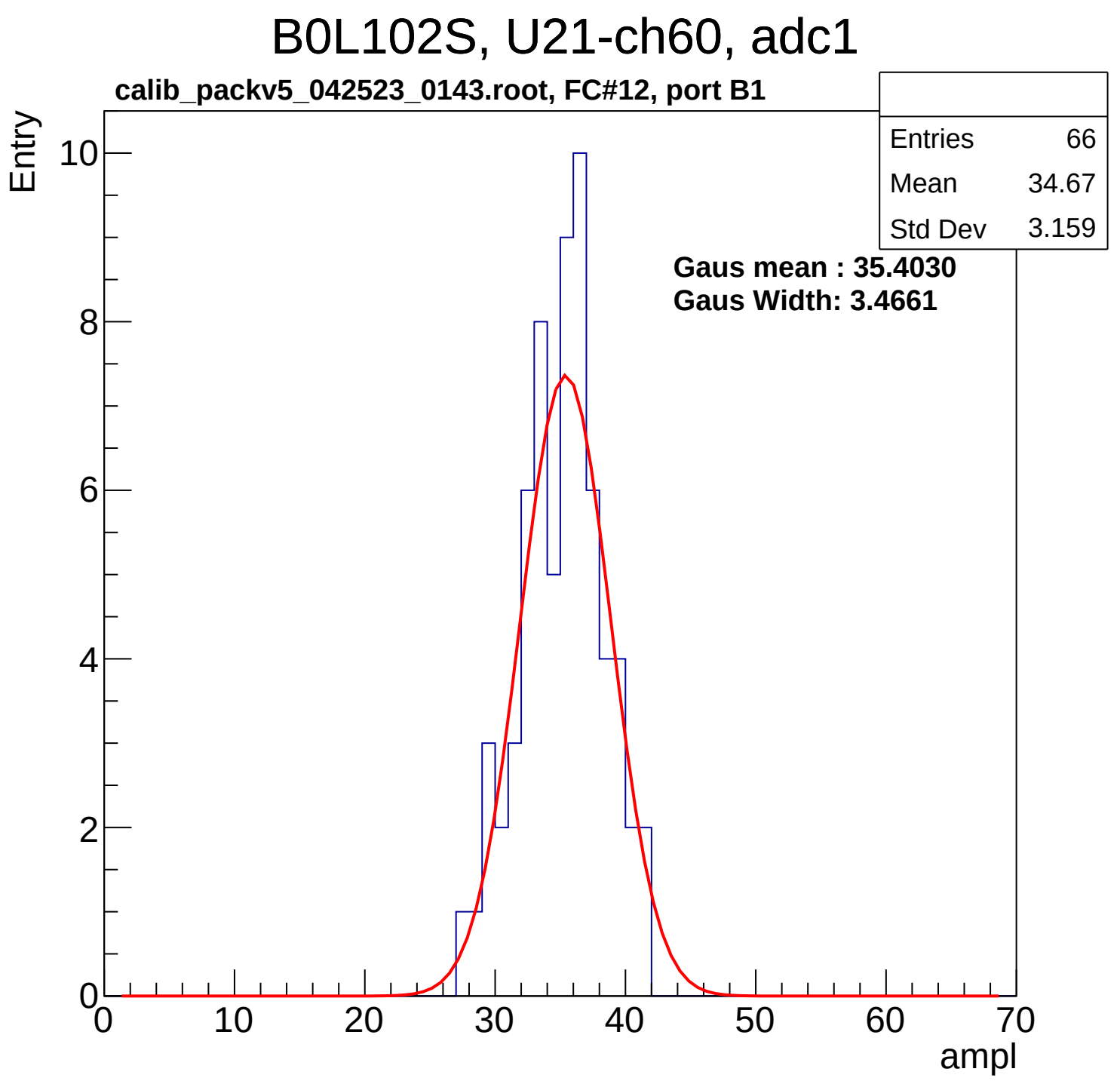
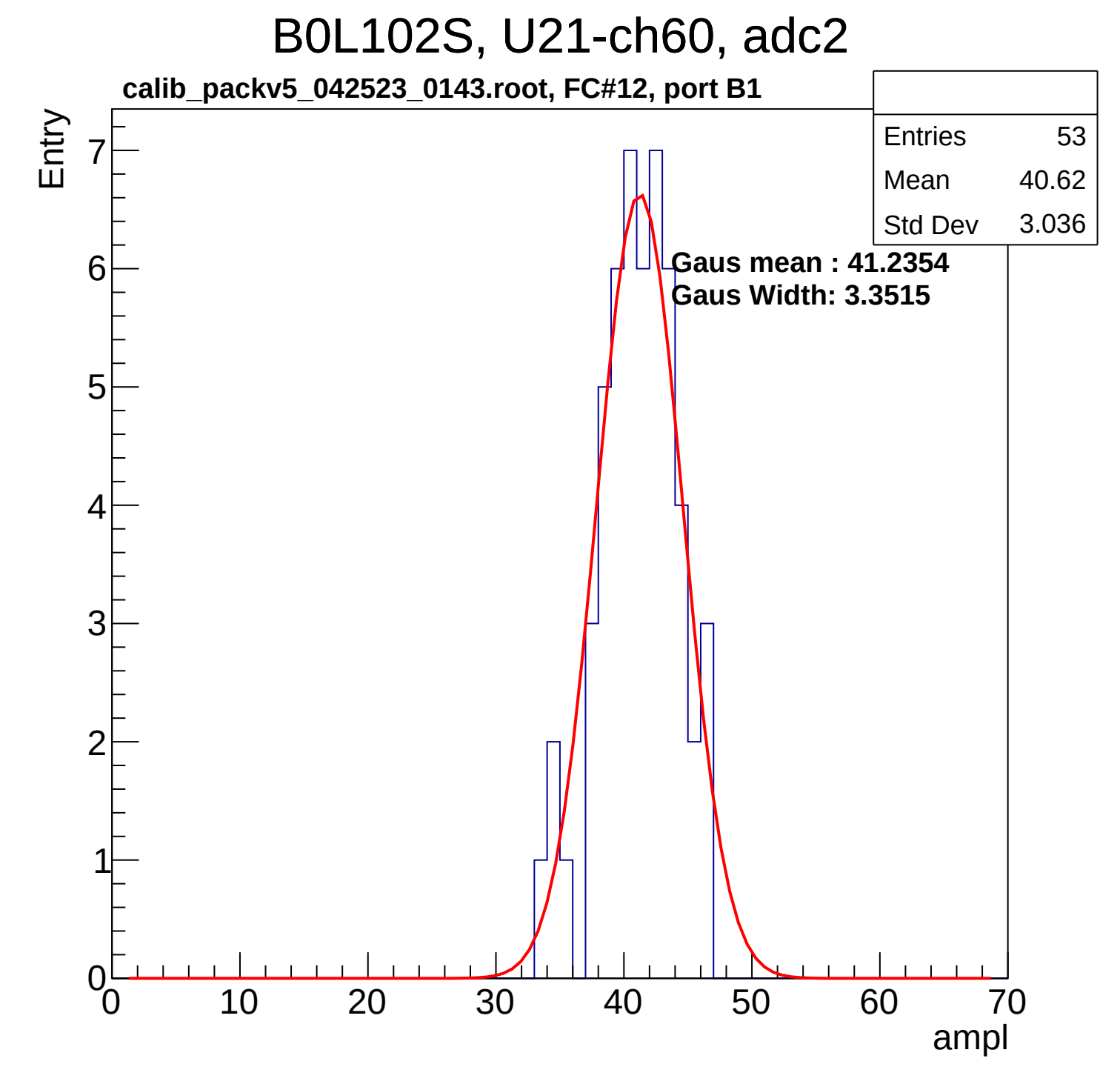
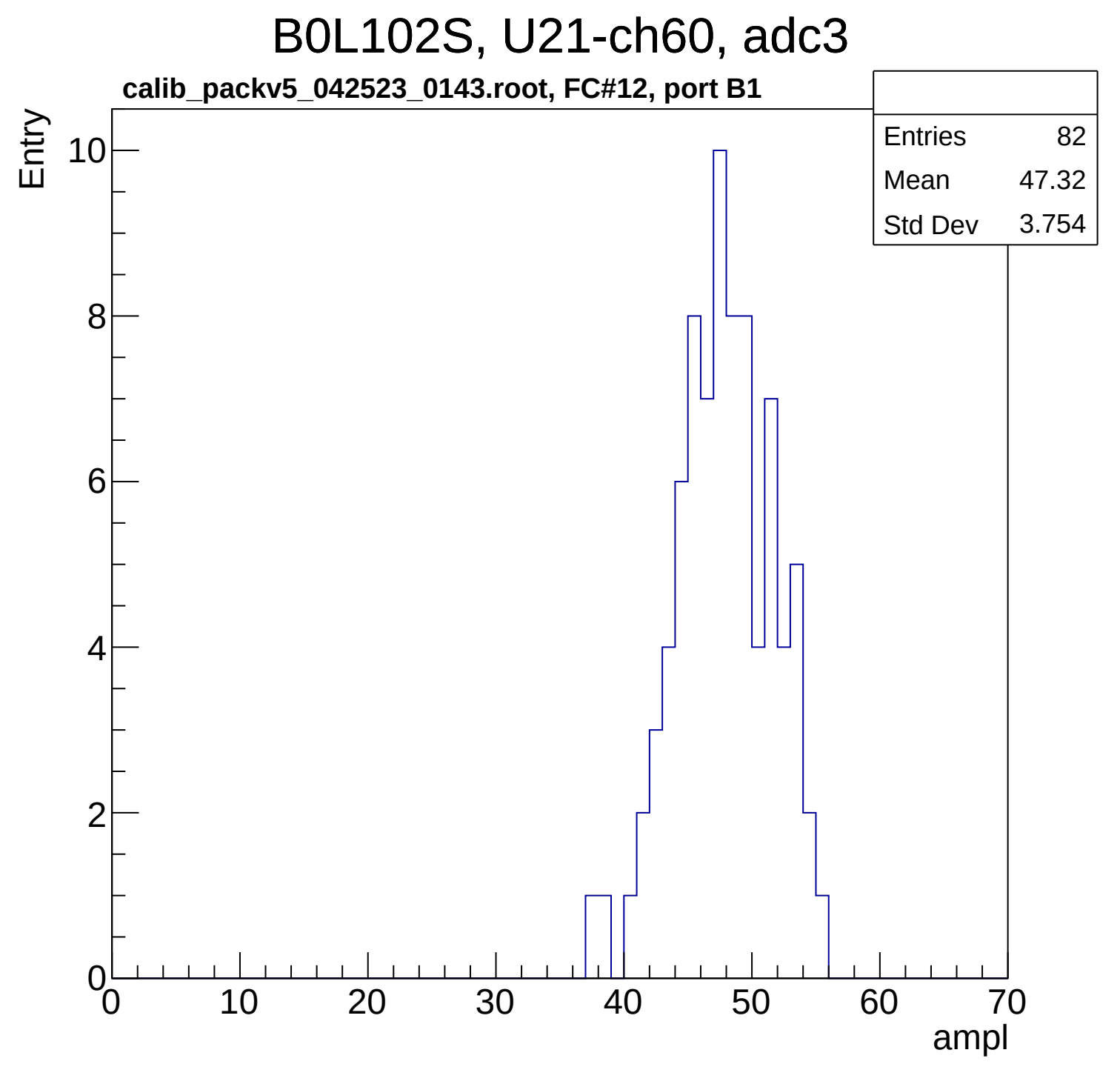
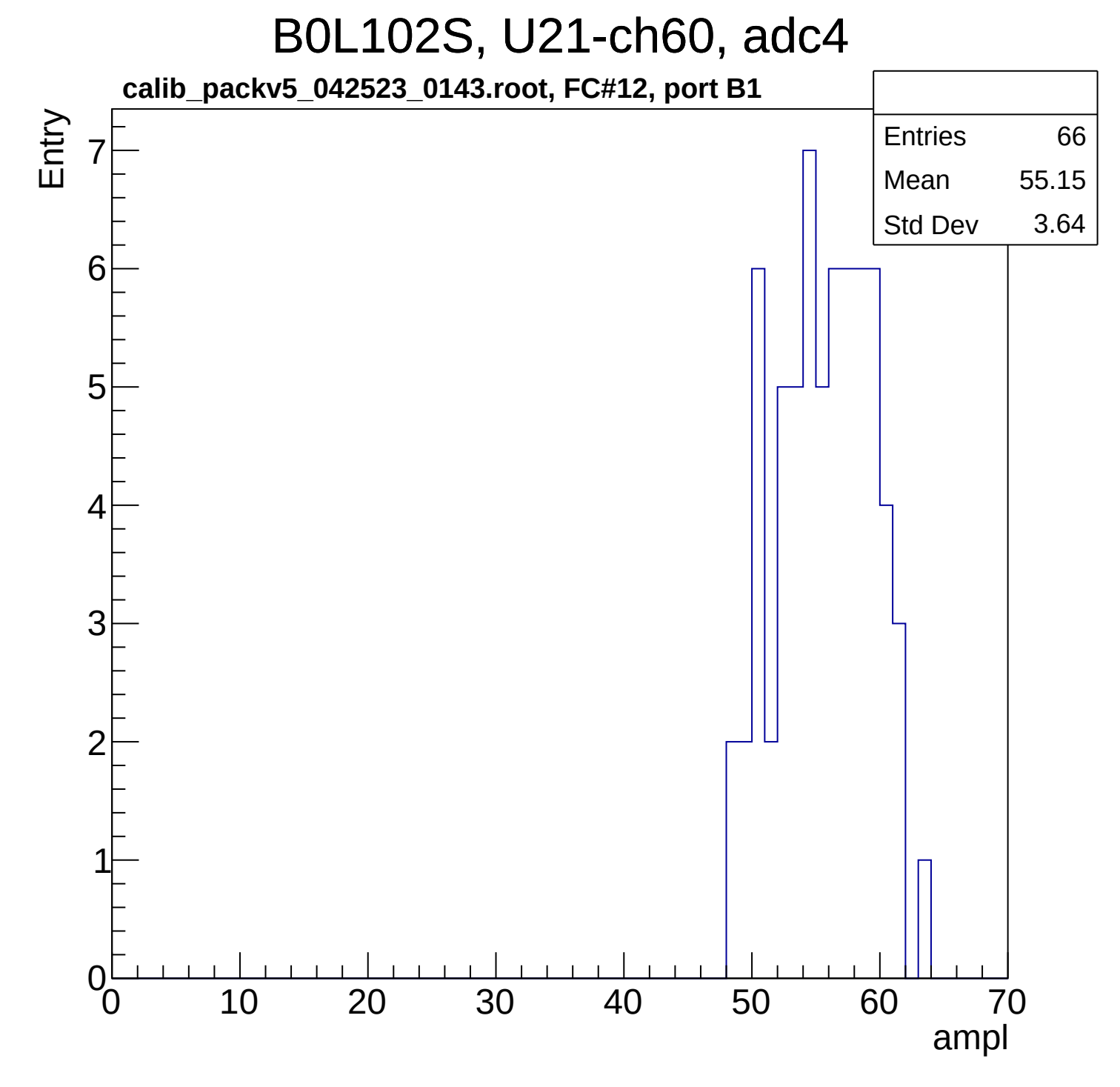
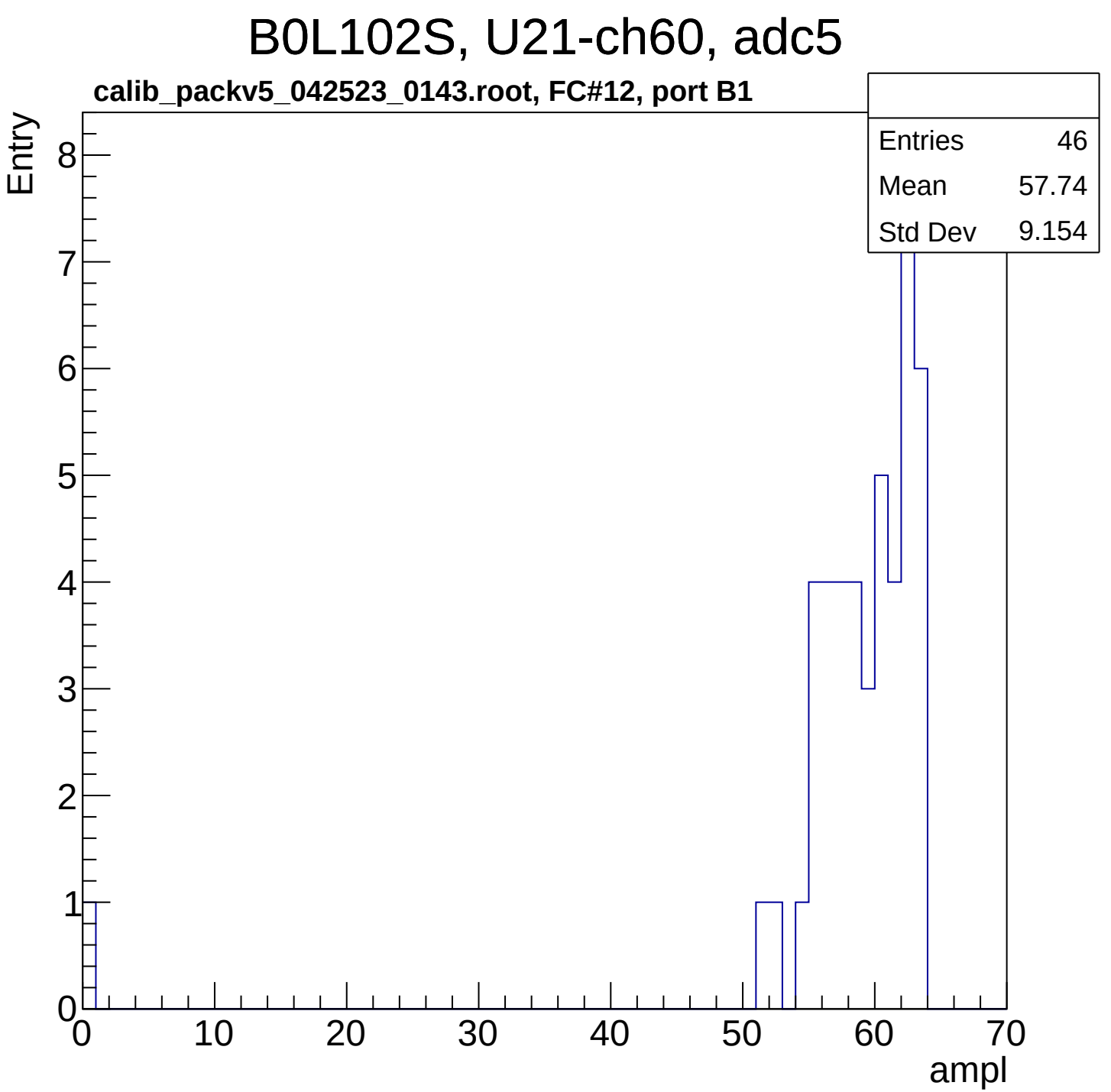
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch60, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 34.67
Std Dev 3.159
Gaus mean : 35.4030
Gaus Width: 3.4661
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch60, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 40.62
Std Dev 3.036
Gaus mean : 41.2354
Gaus Width: 3.3515
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch60, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 47.32
Std Dev 3.754
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch60, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 55.15
Std Dev 3.64
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch60, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 57.74
Std Dev 9.154
Entry
ampl
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1
2
3
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5
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7
8
0
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20
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40
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60
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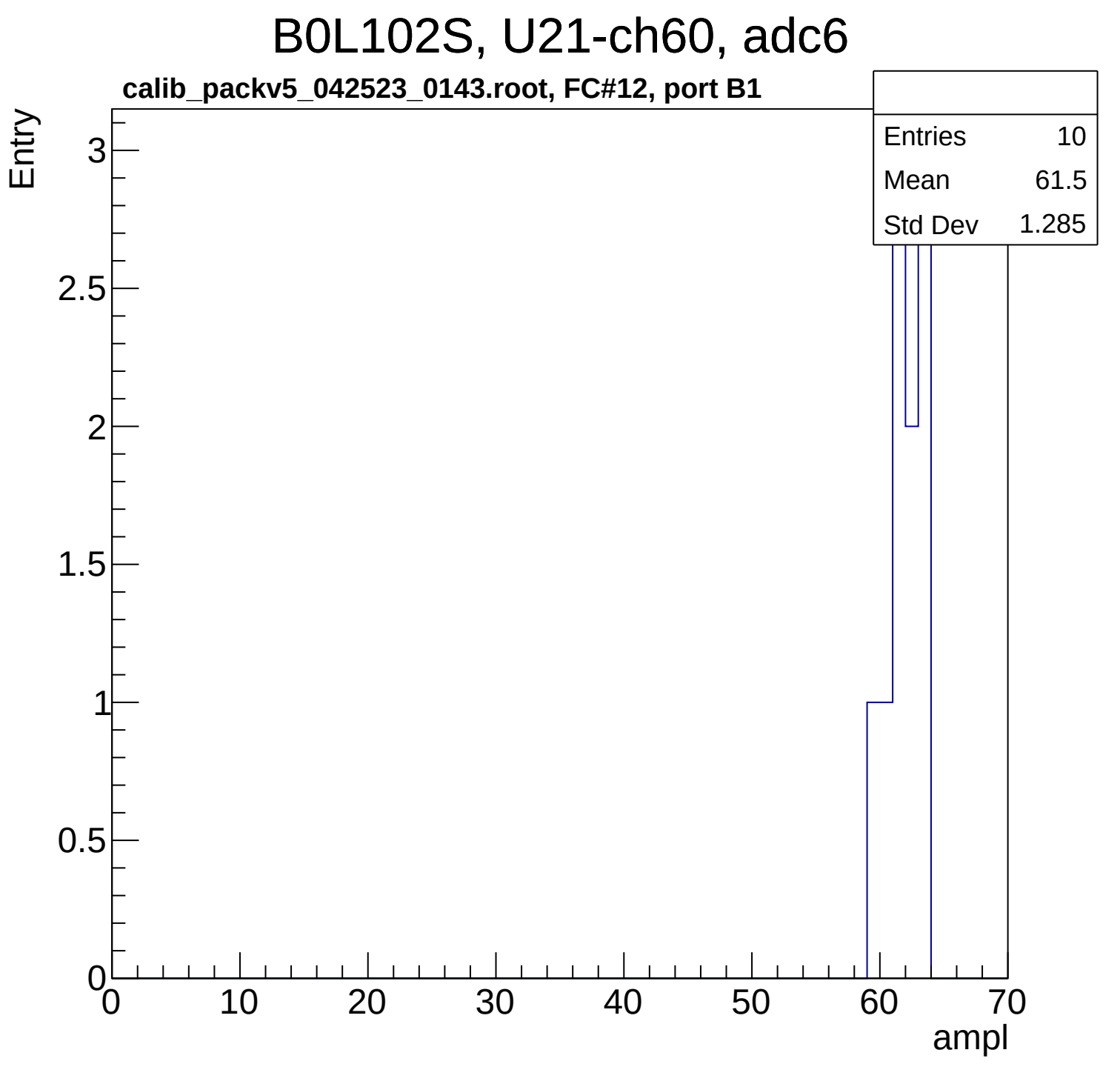
B0L102S, U21-ch60, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 61.5
Std Dev 1.285
Entry
ampl
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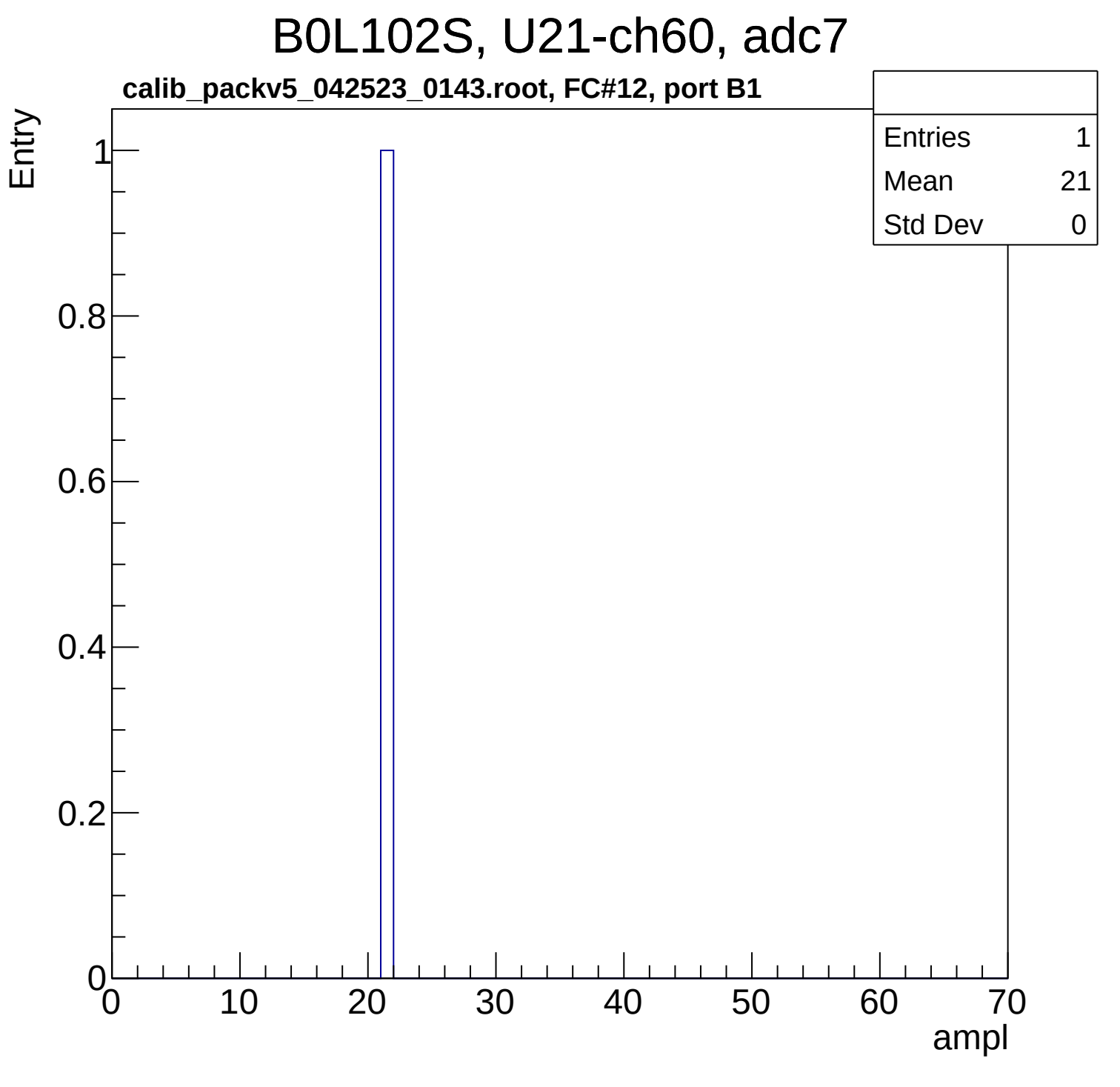
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch60, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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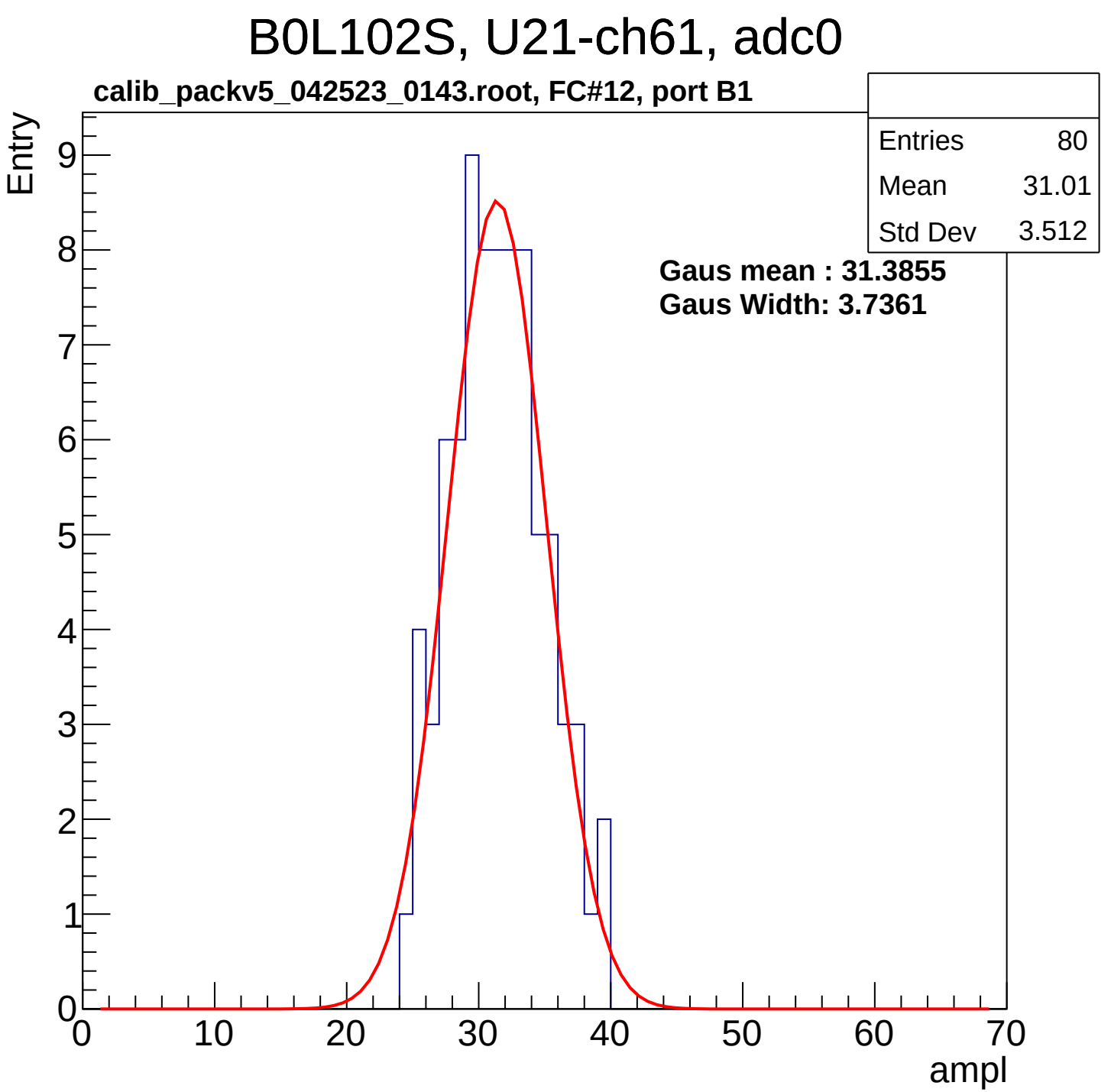
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch61, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 31.01
Std Dev 3.512
Gaus mean : 31.3855
Gaus Width: 3.7361
Entry
ampl
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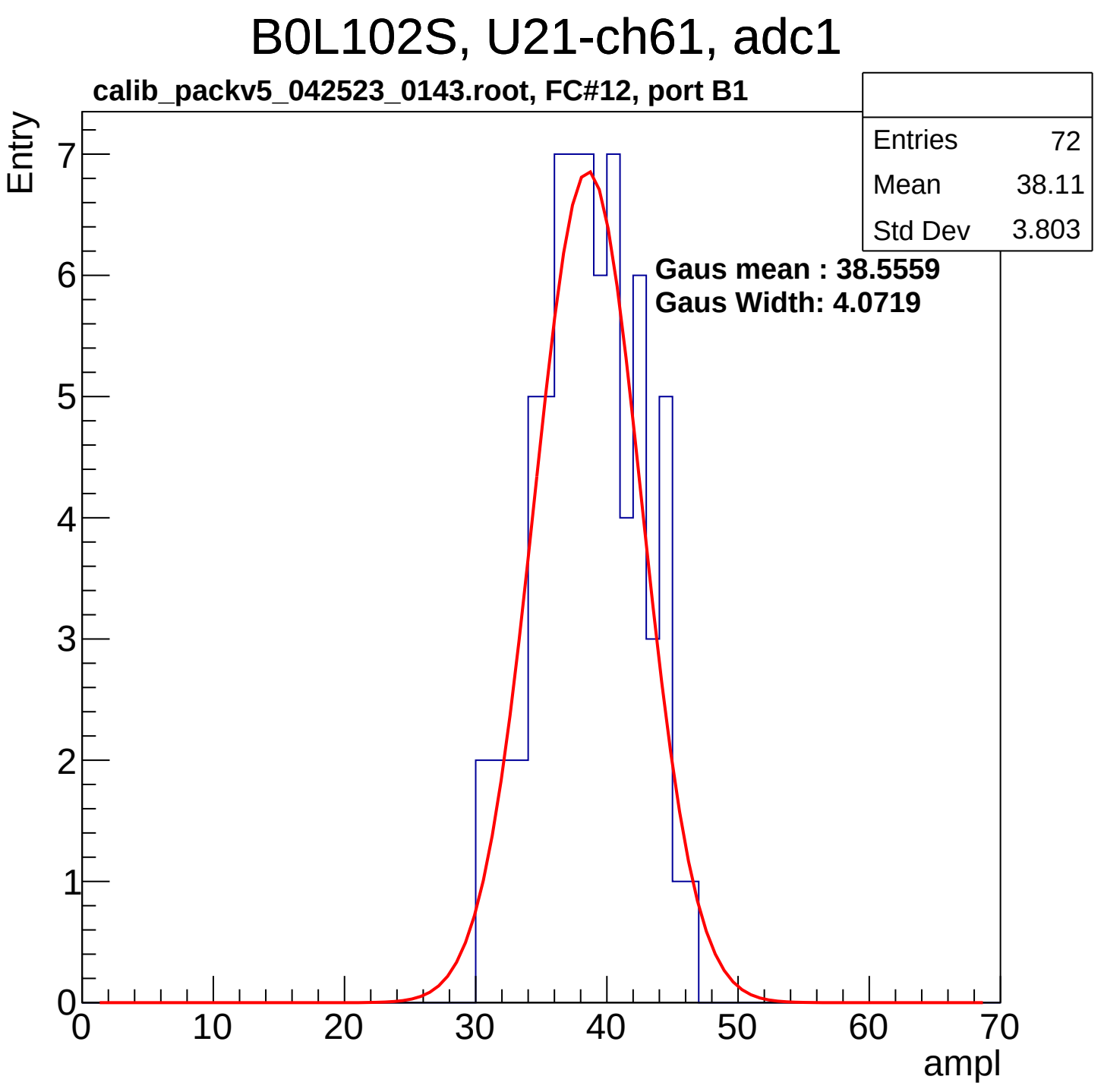
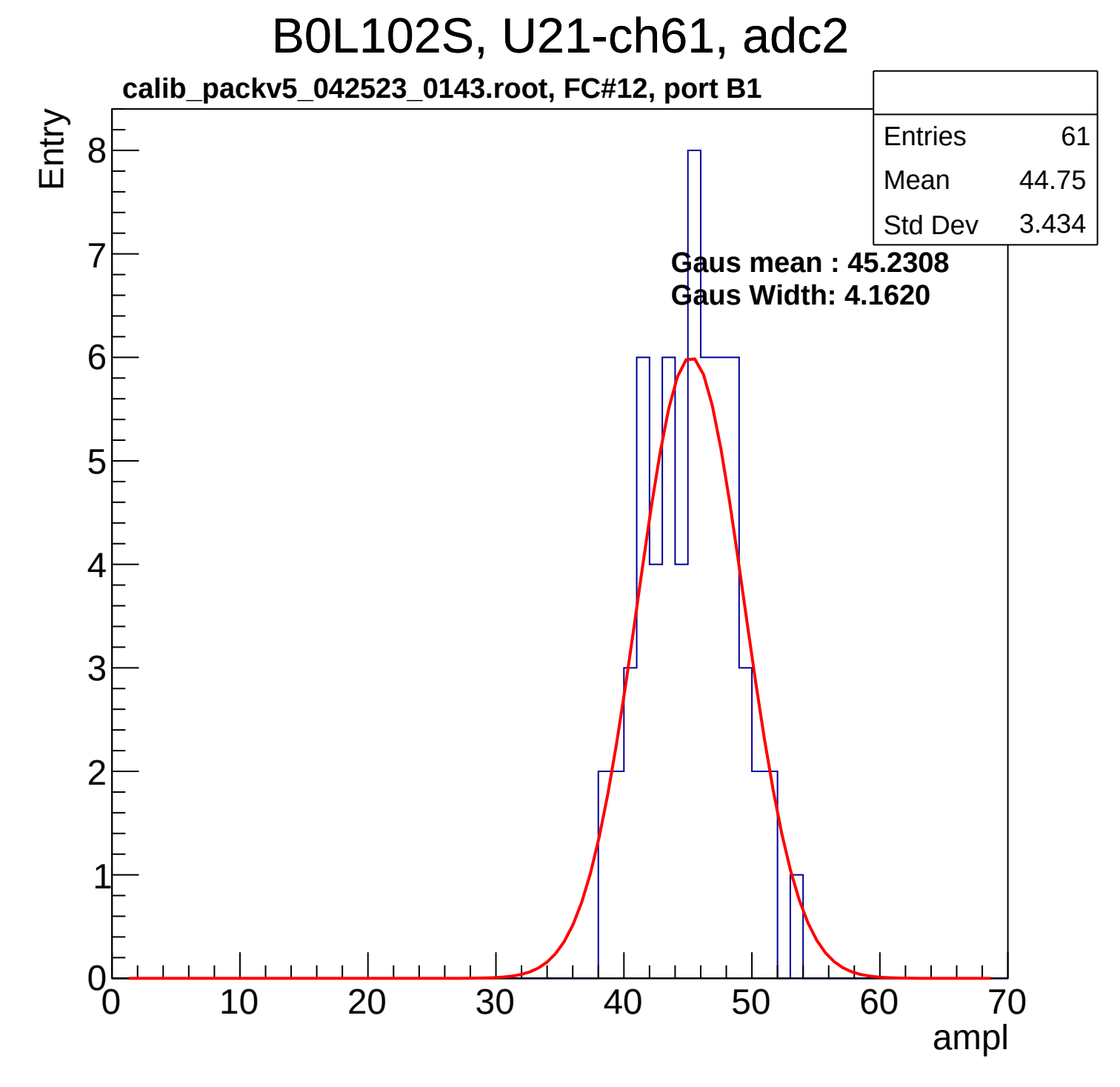
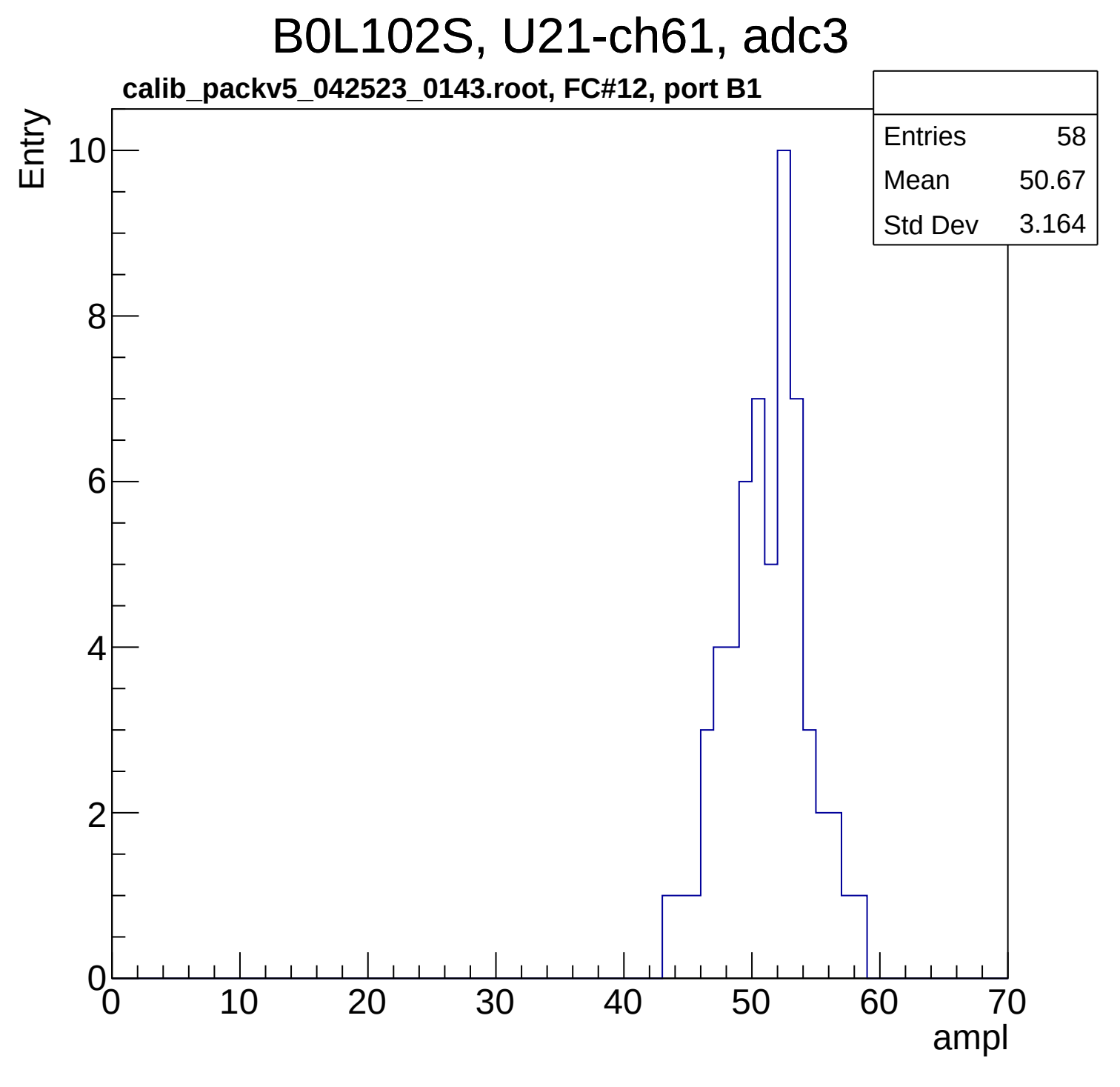
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch61, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 38.11
Std Dev 3.803
Gaus mean : 38.5559
Gaus Width: 4.0719
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch61, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 44.75
Std Dev 3.434
Gaus mean : 45.2308
Gaus Width: 4.1620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch61, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 50.67
Std Dev 3.164
Entry
0
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4
6
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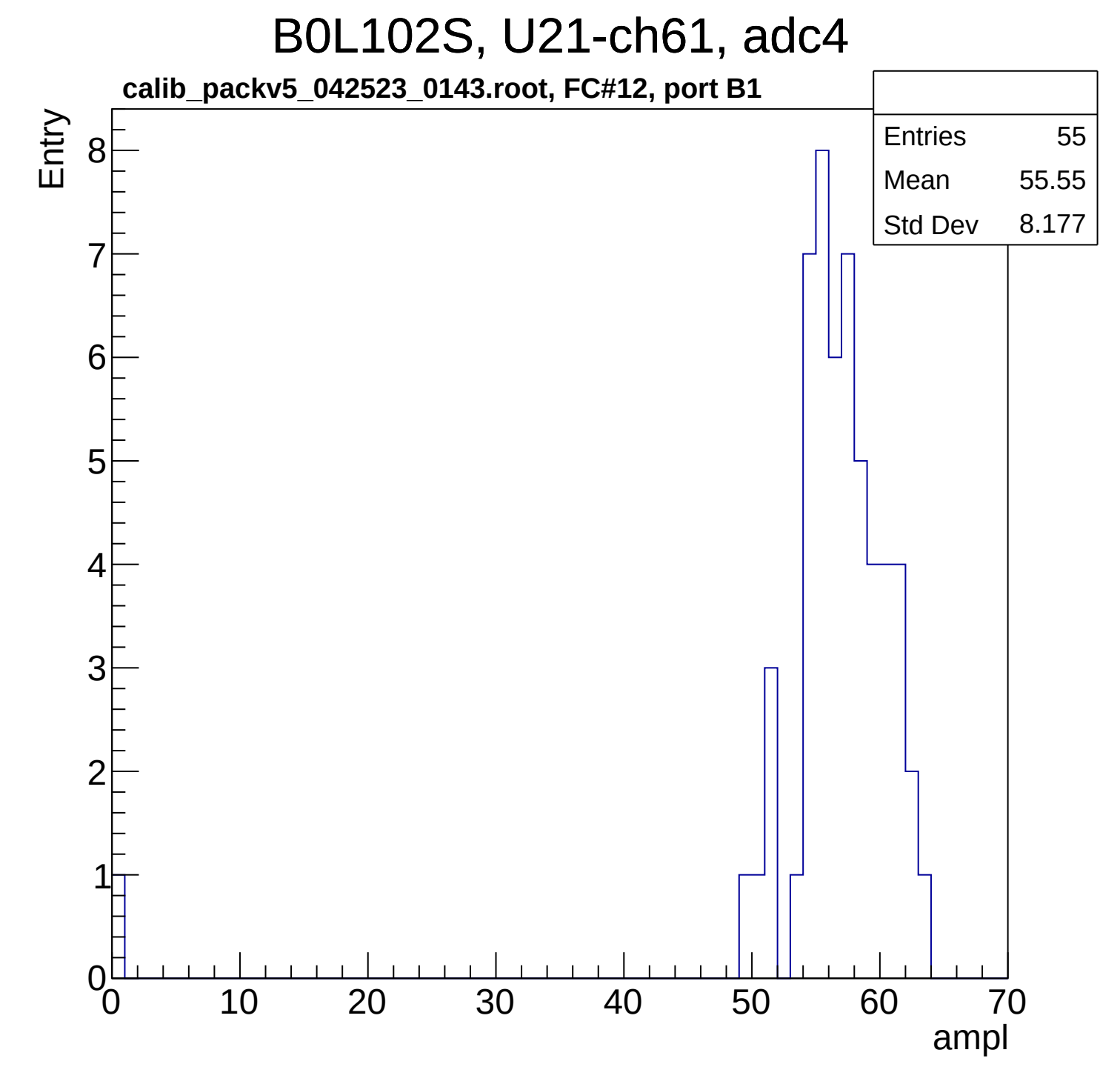
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch61, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.55
Std Dev 8.177
Entry
ampl
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1
2
3
4
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0
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40
50
60
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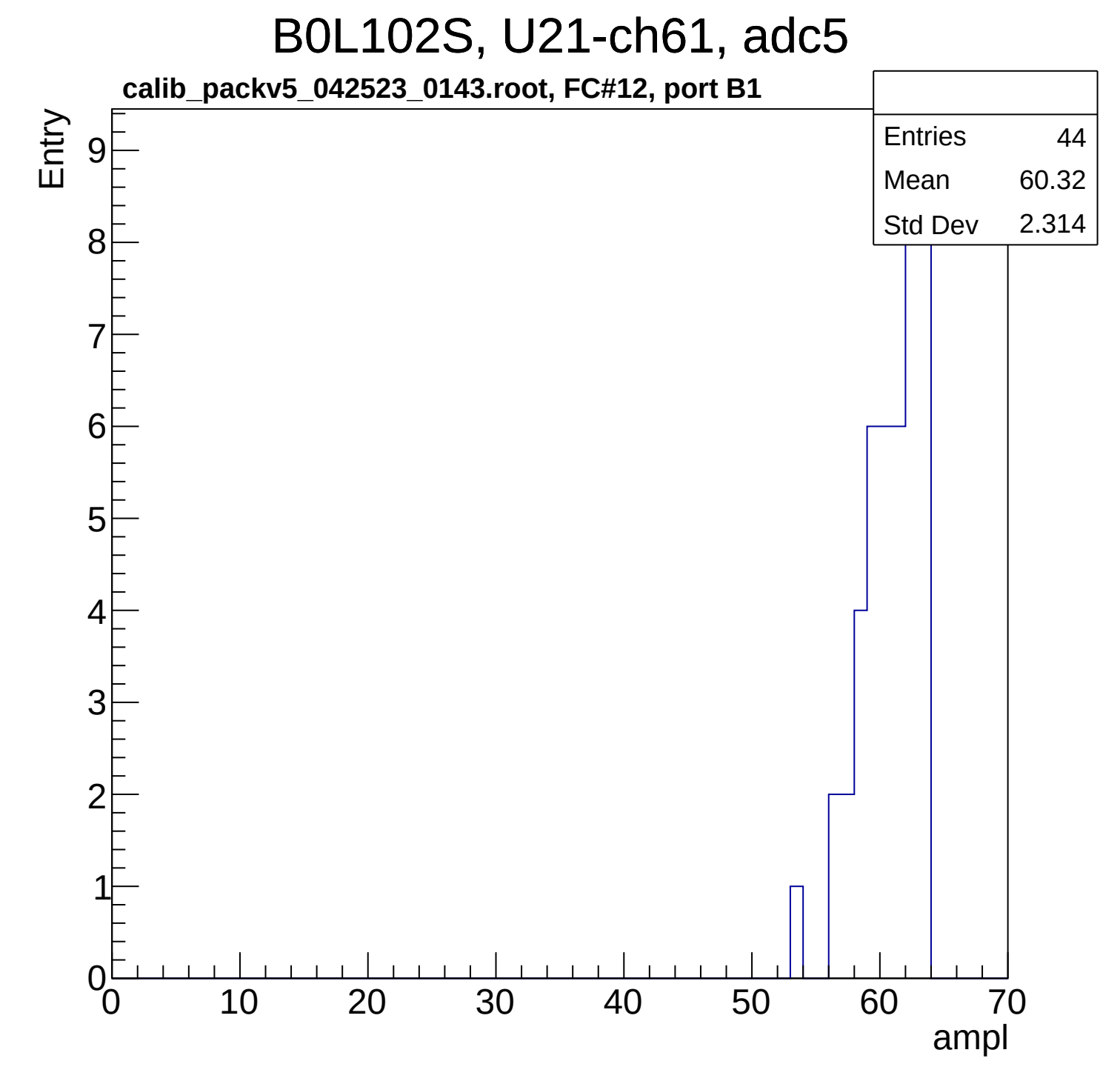
B0L102S, U21-ch61, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 60.32
Std Dev 2.314
Entry
ampl
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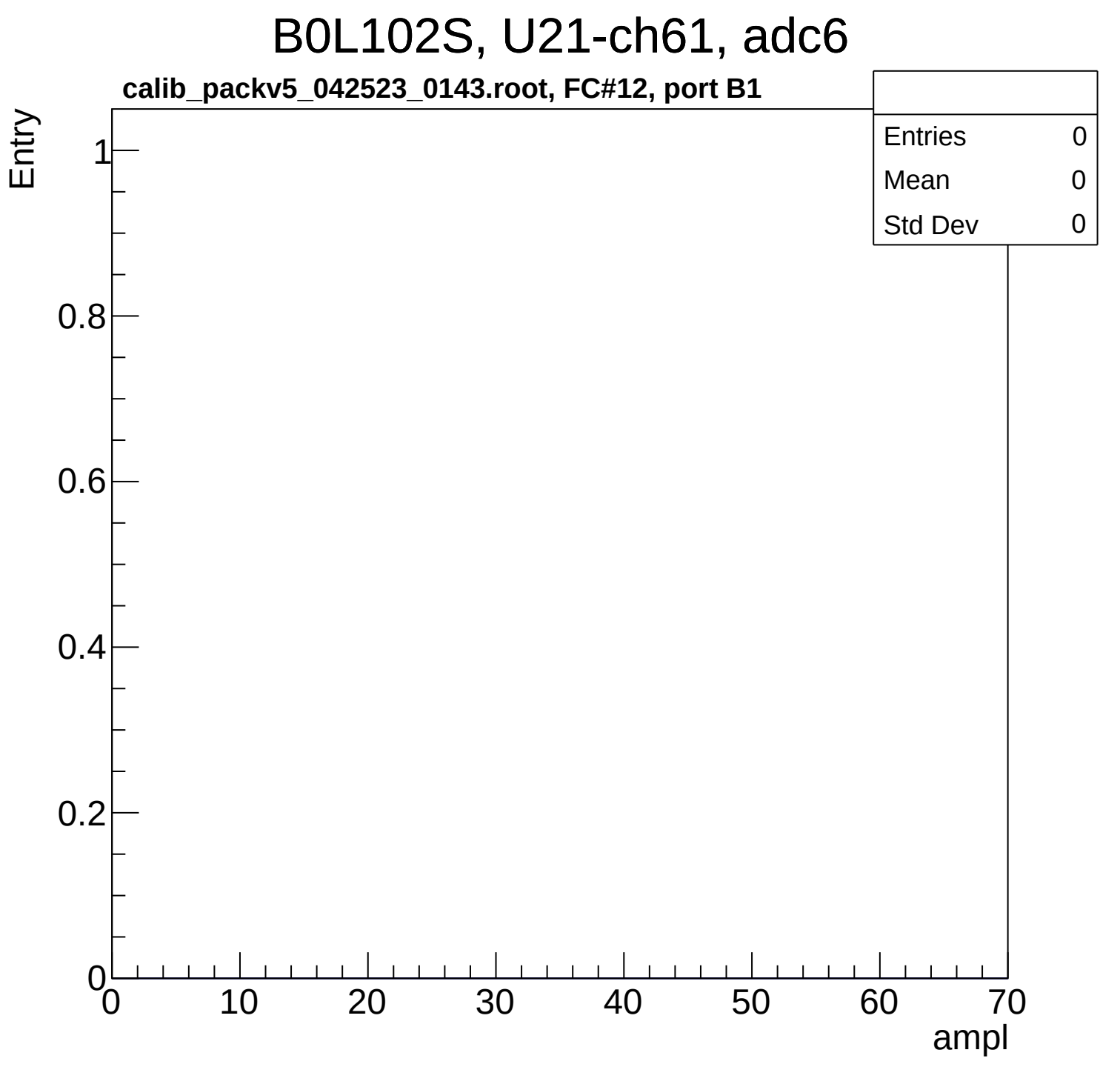
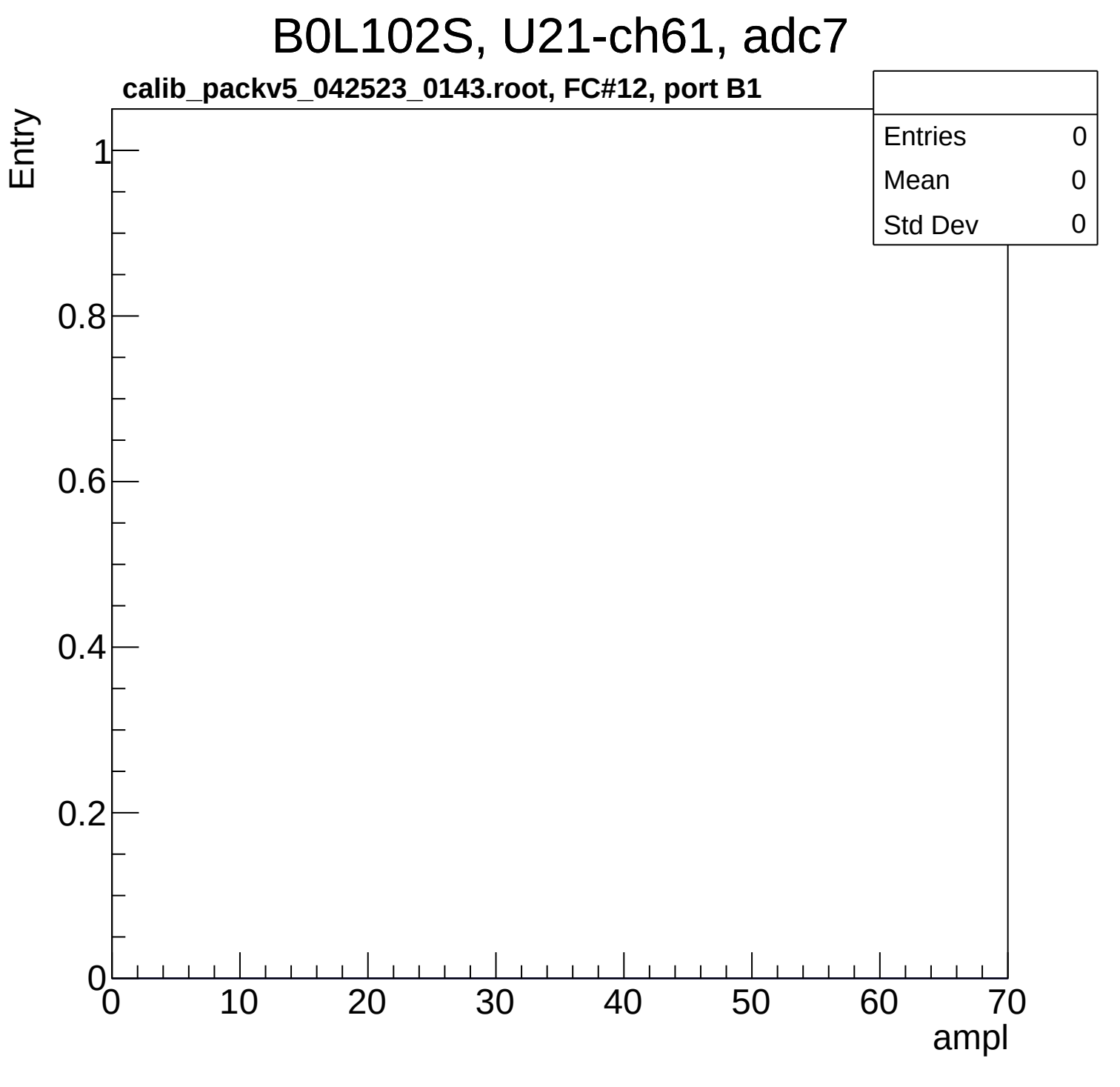
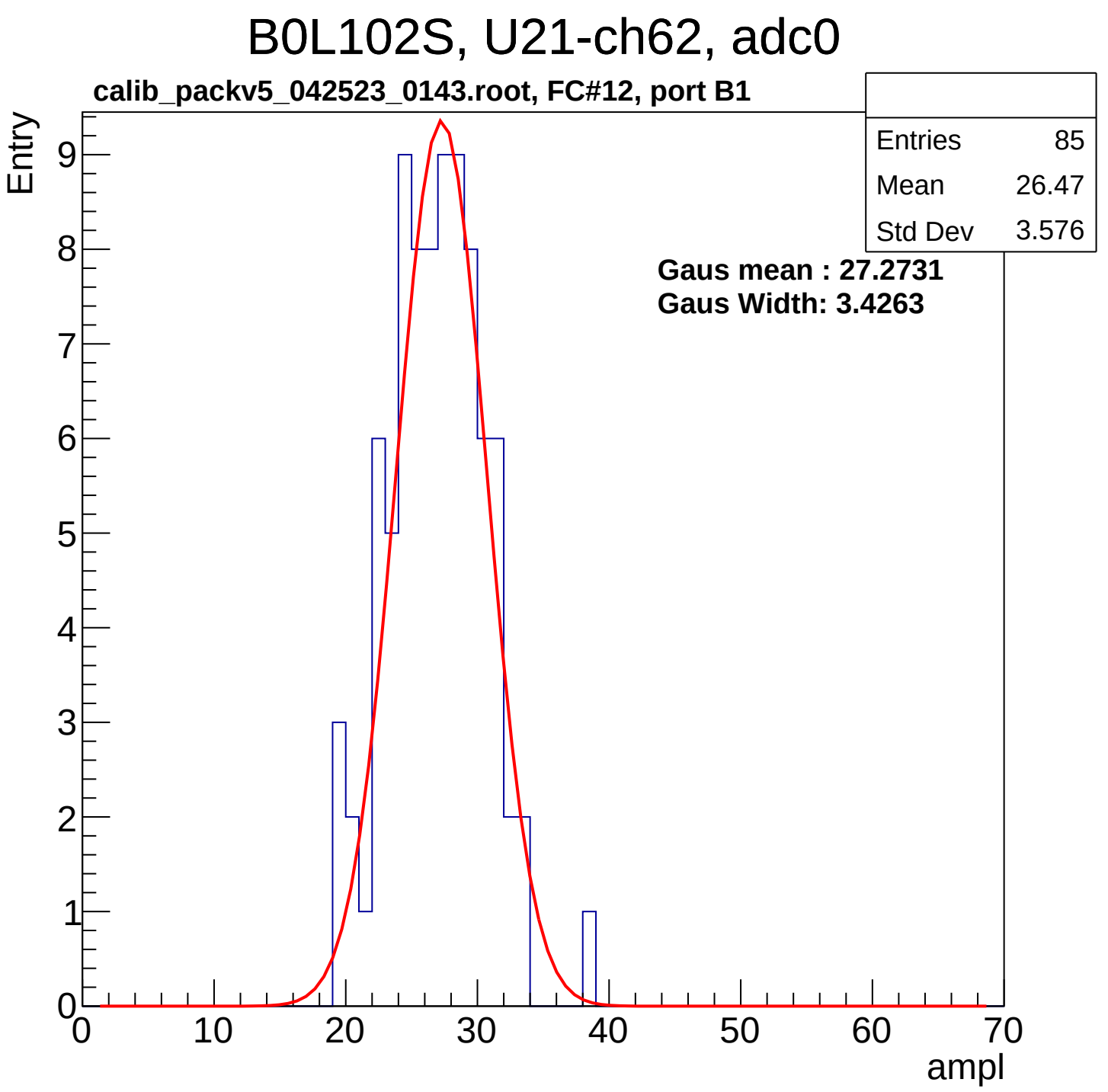

B0L102S, U21-ch61, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch61, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch62, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 26.47
Std Dev 3.576
Gaus mean : 27.2731
Gaus Width: 3.4263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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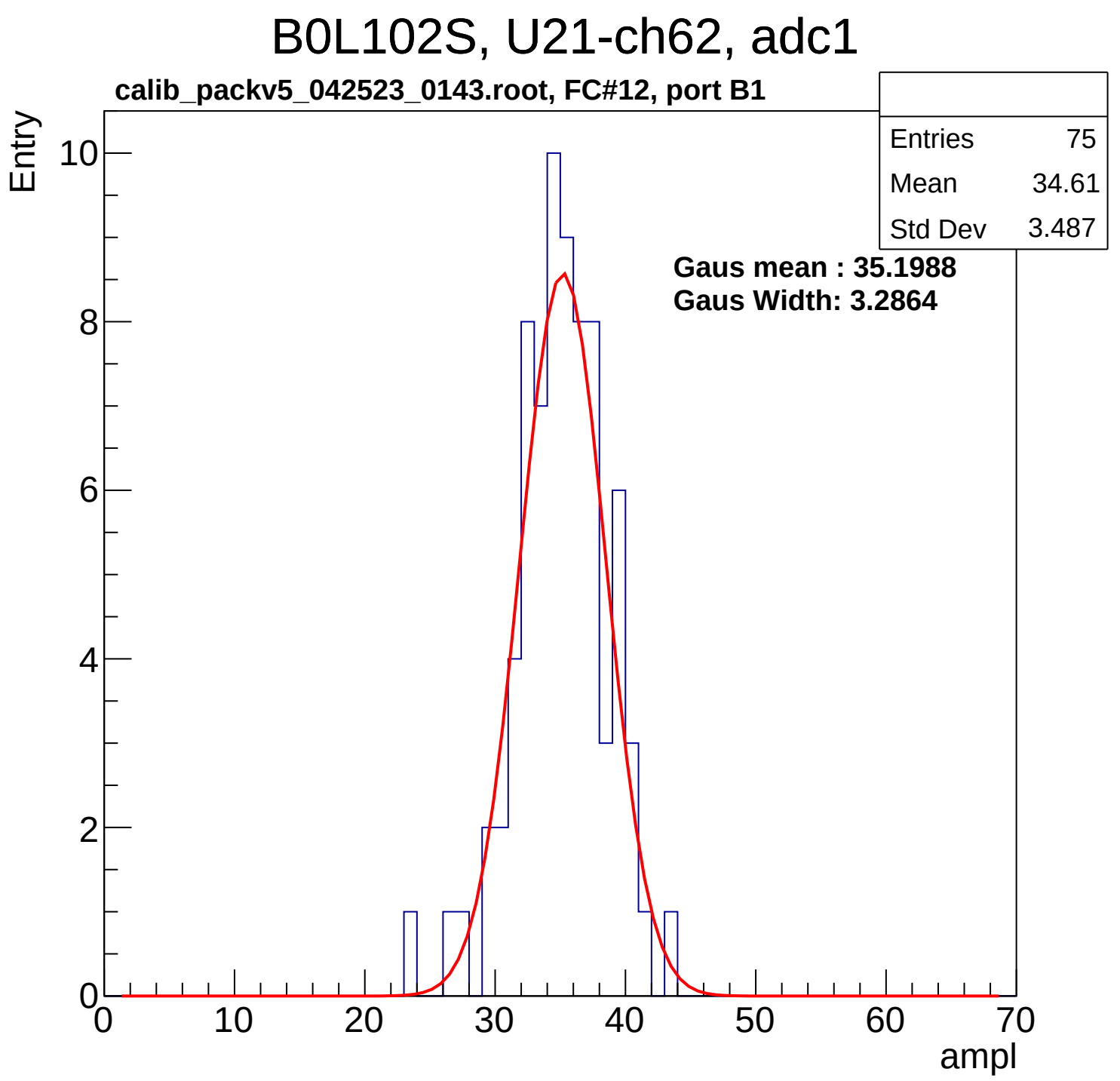
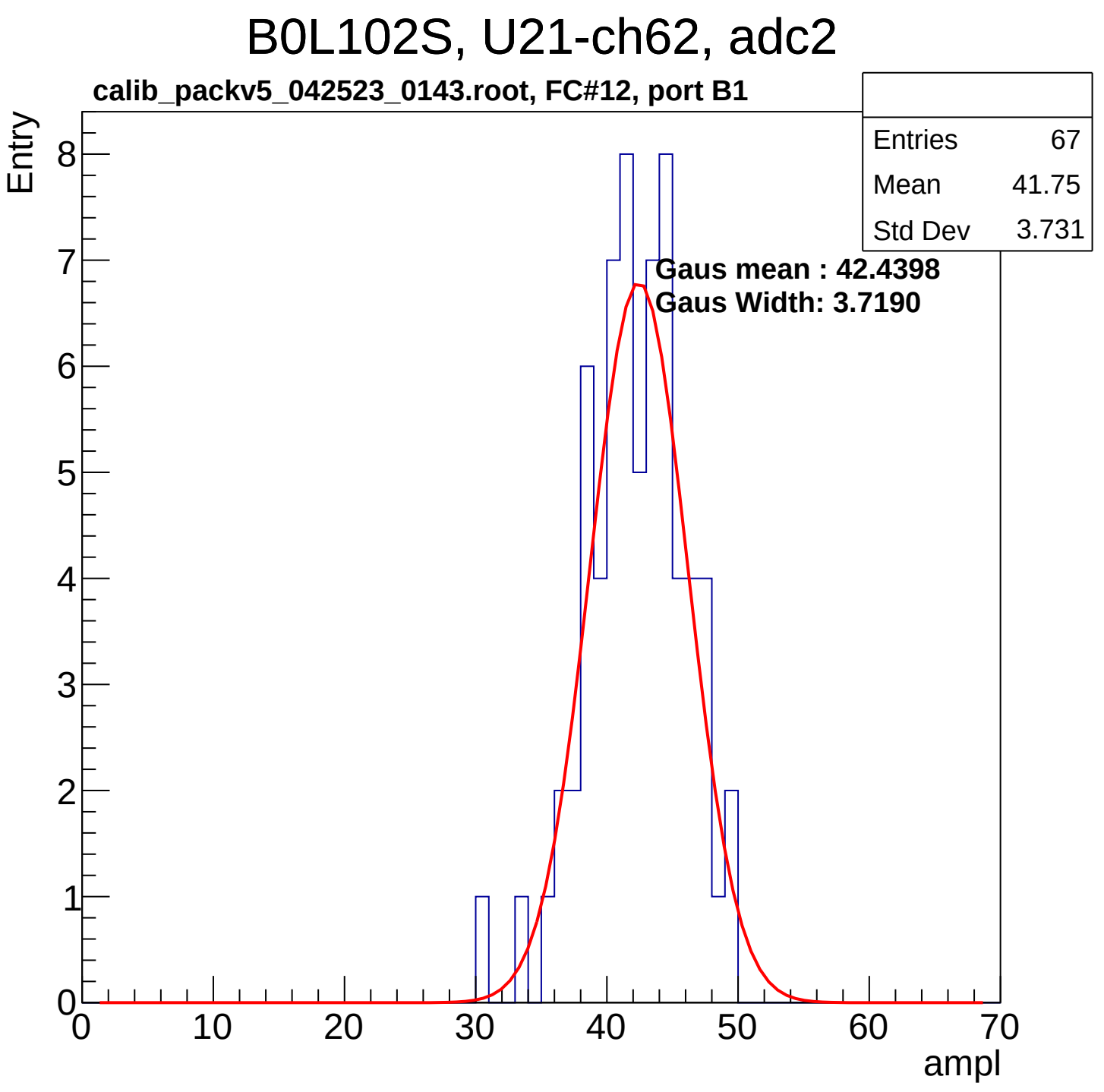
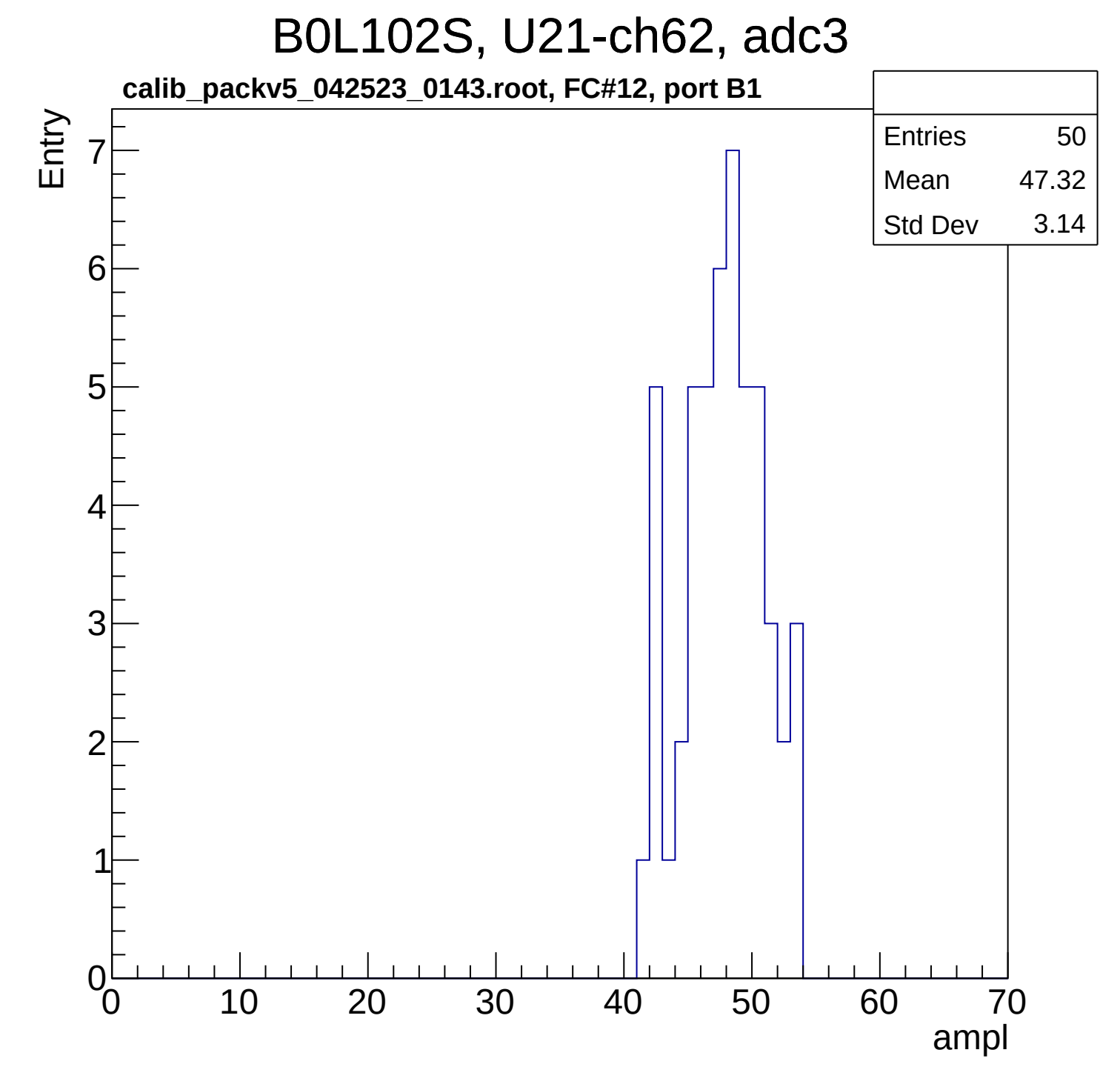
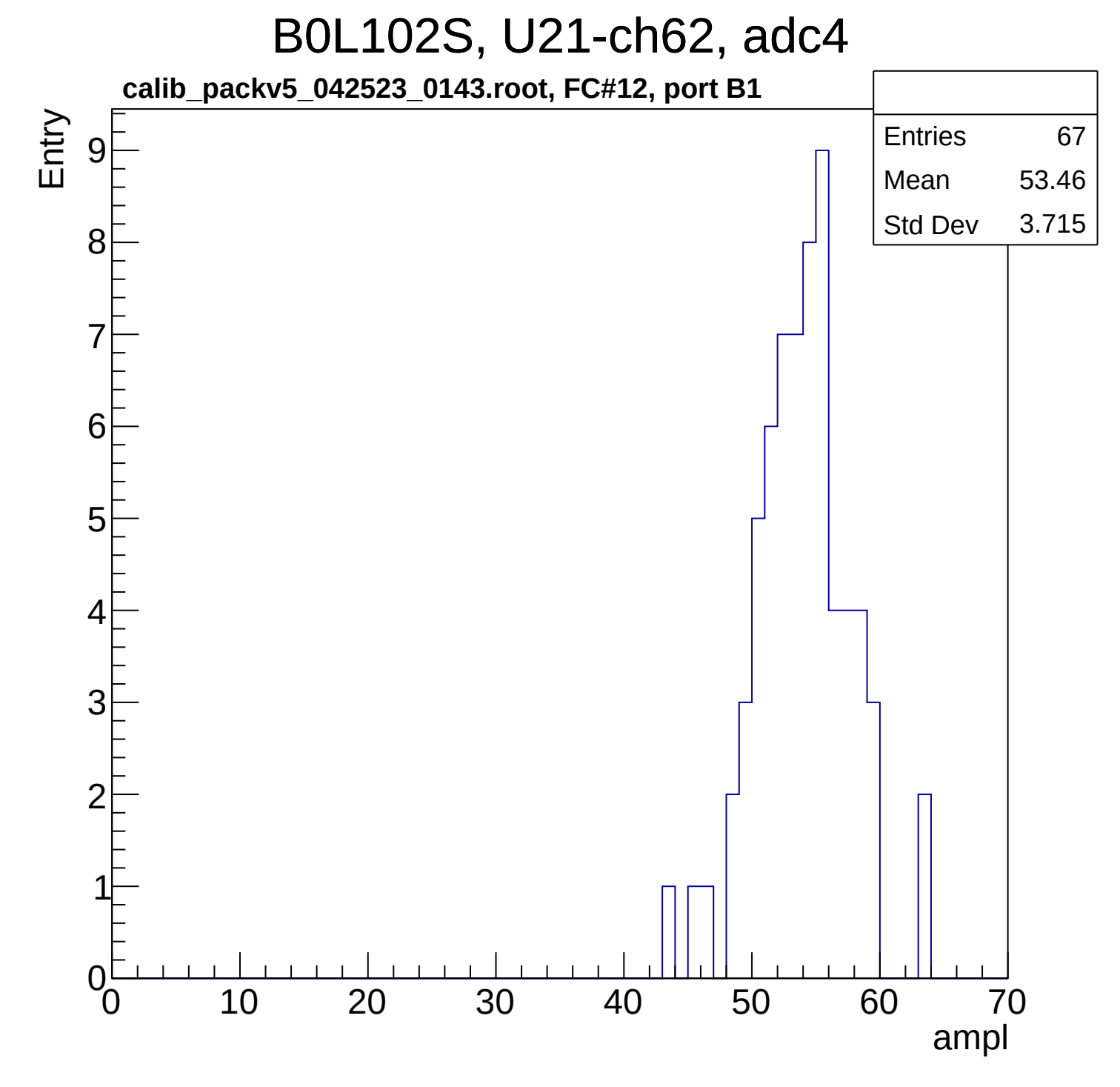
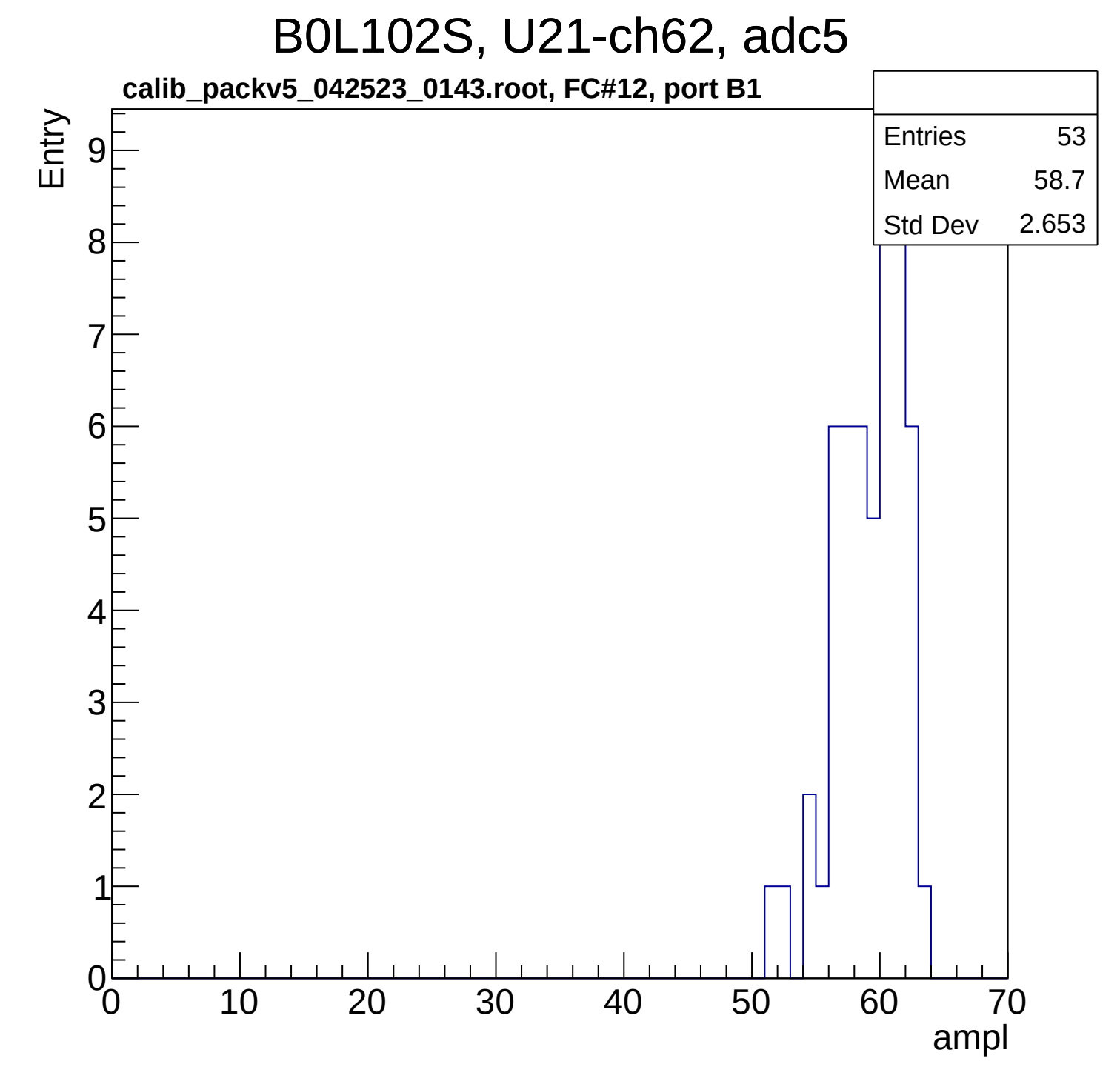
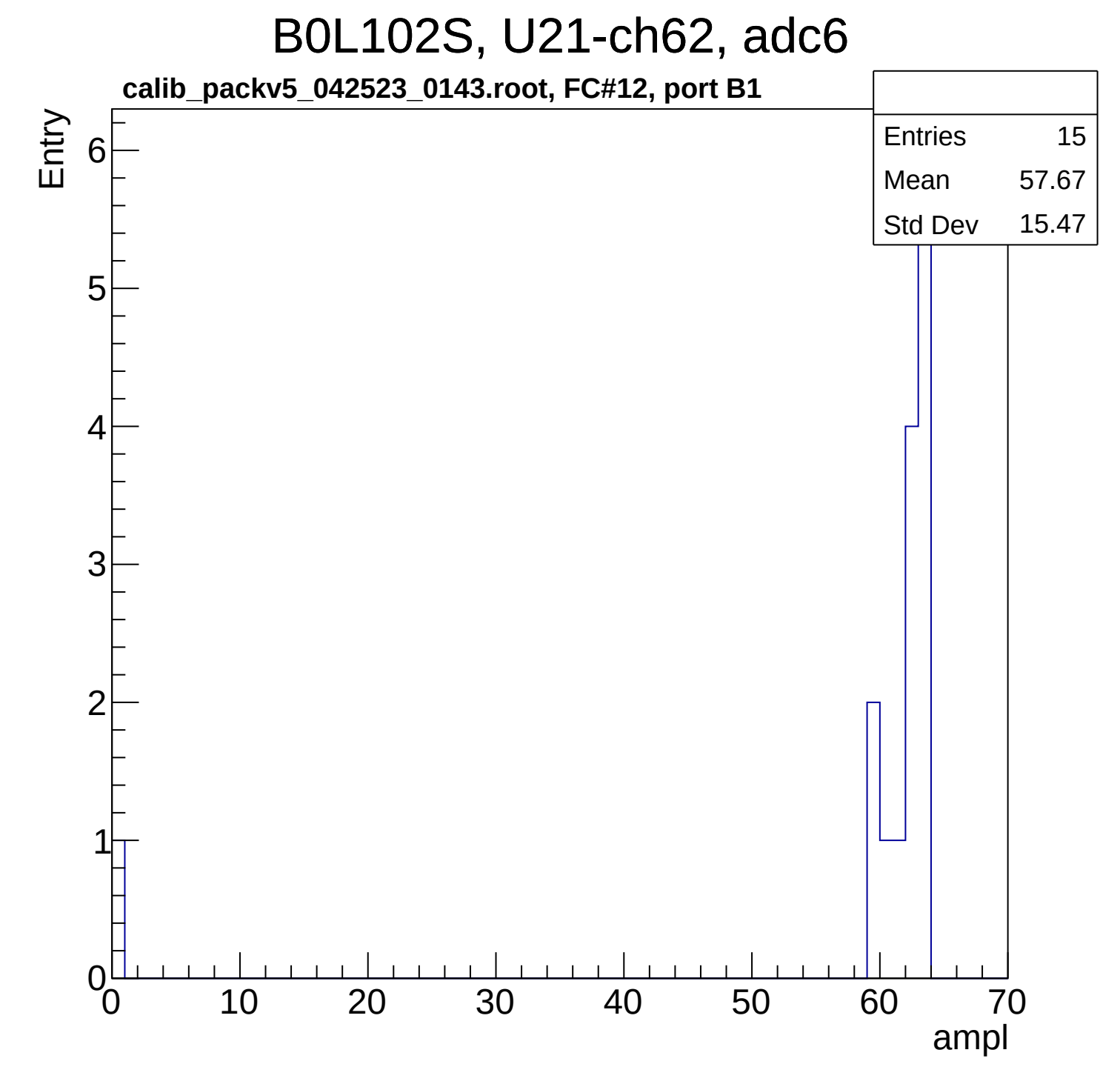

B0L102S, U21-ch62, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 34.61
Std Dev 3.487
Gaus mean : 35.1988
Gaus Width: 3.2864
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

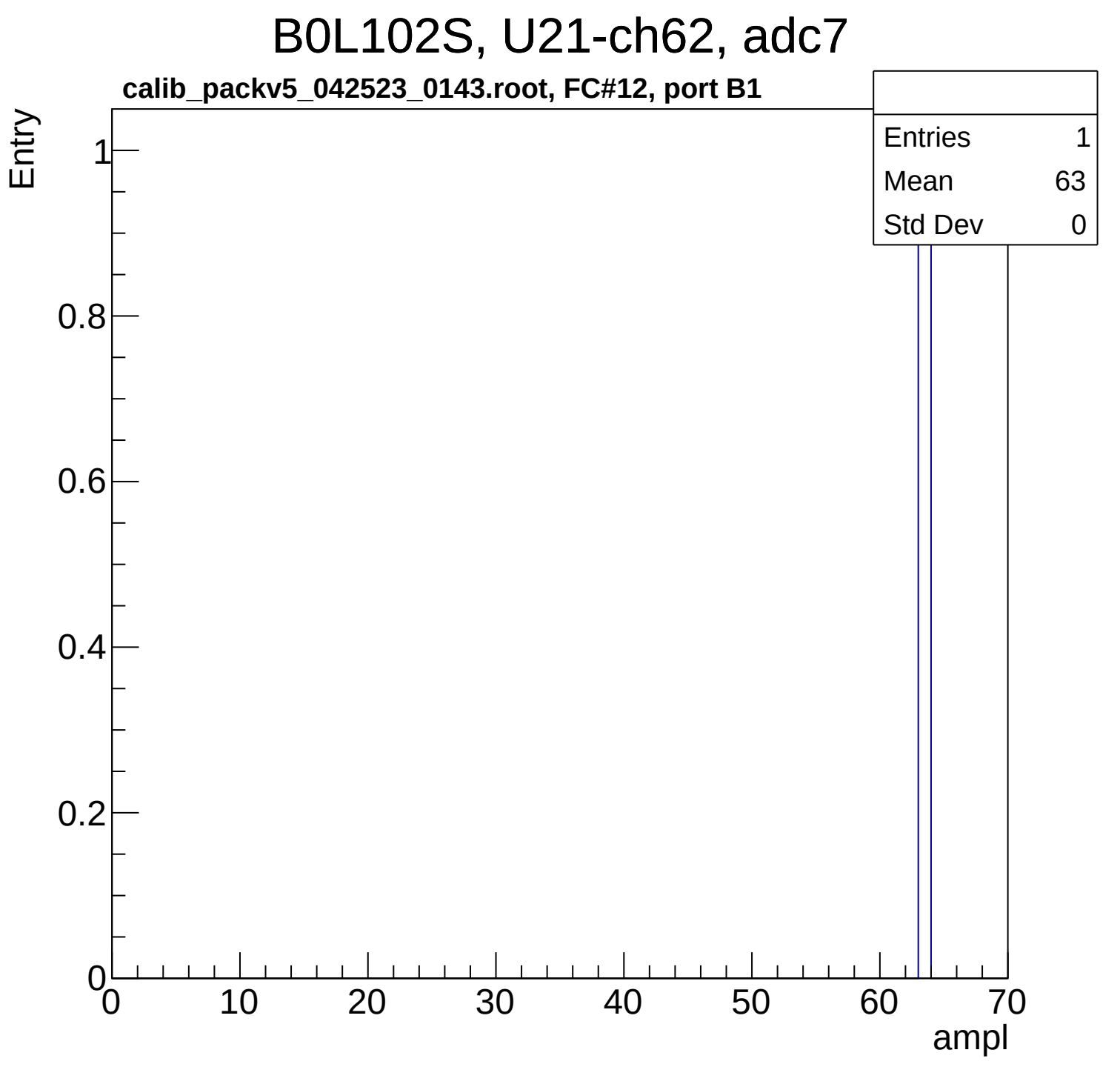

B0L102S, U21-ch62, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 41.75
Std Dev 3.731
Gaus mean : 42.4398
Gaus Width: 3.7190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

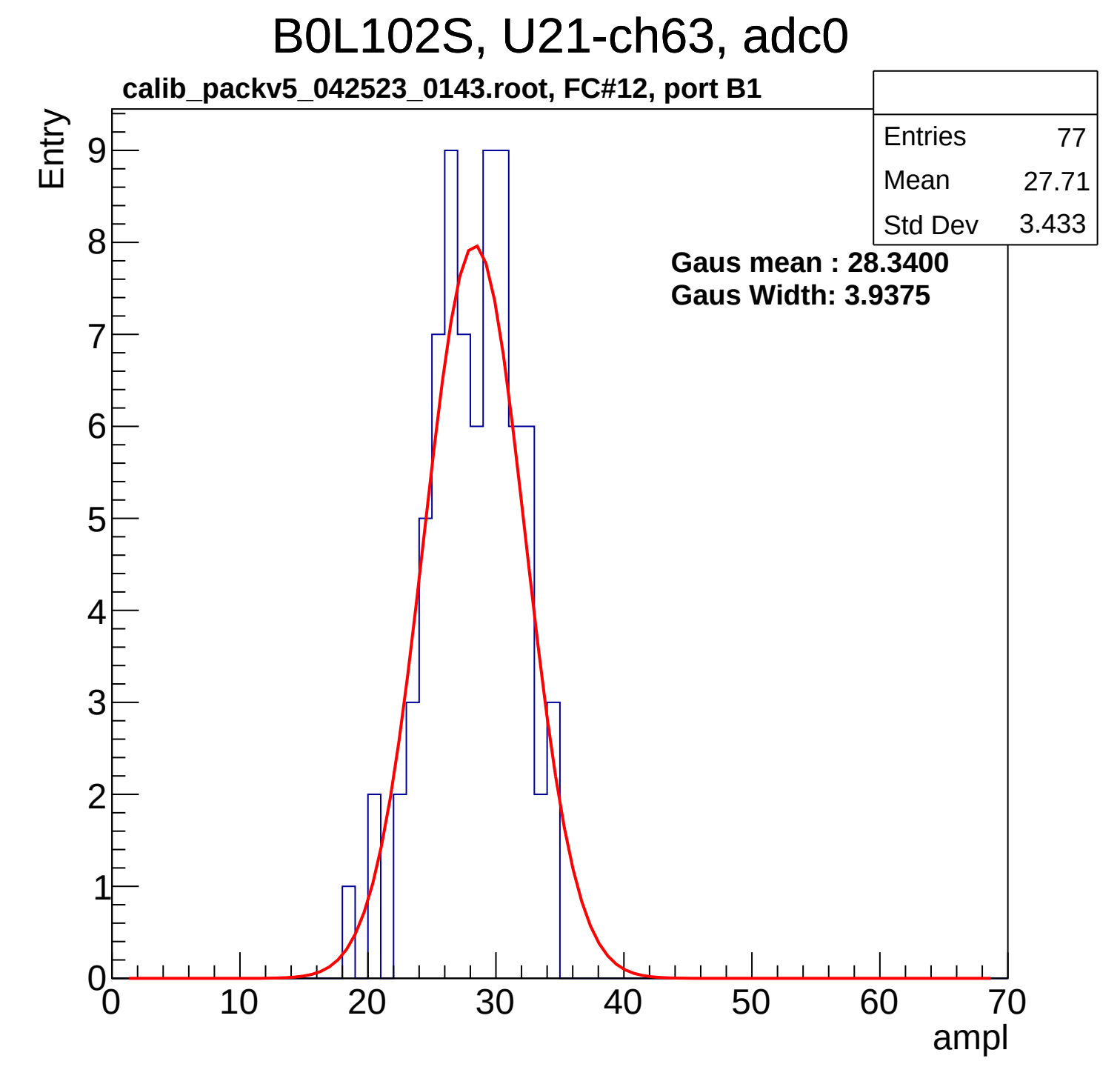

B0L102S, U21-ch62, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 47.32
Std Dev 3.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

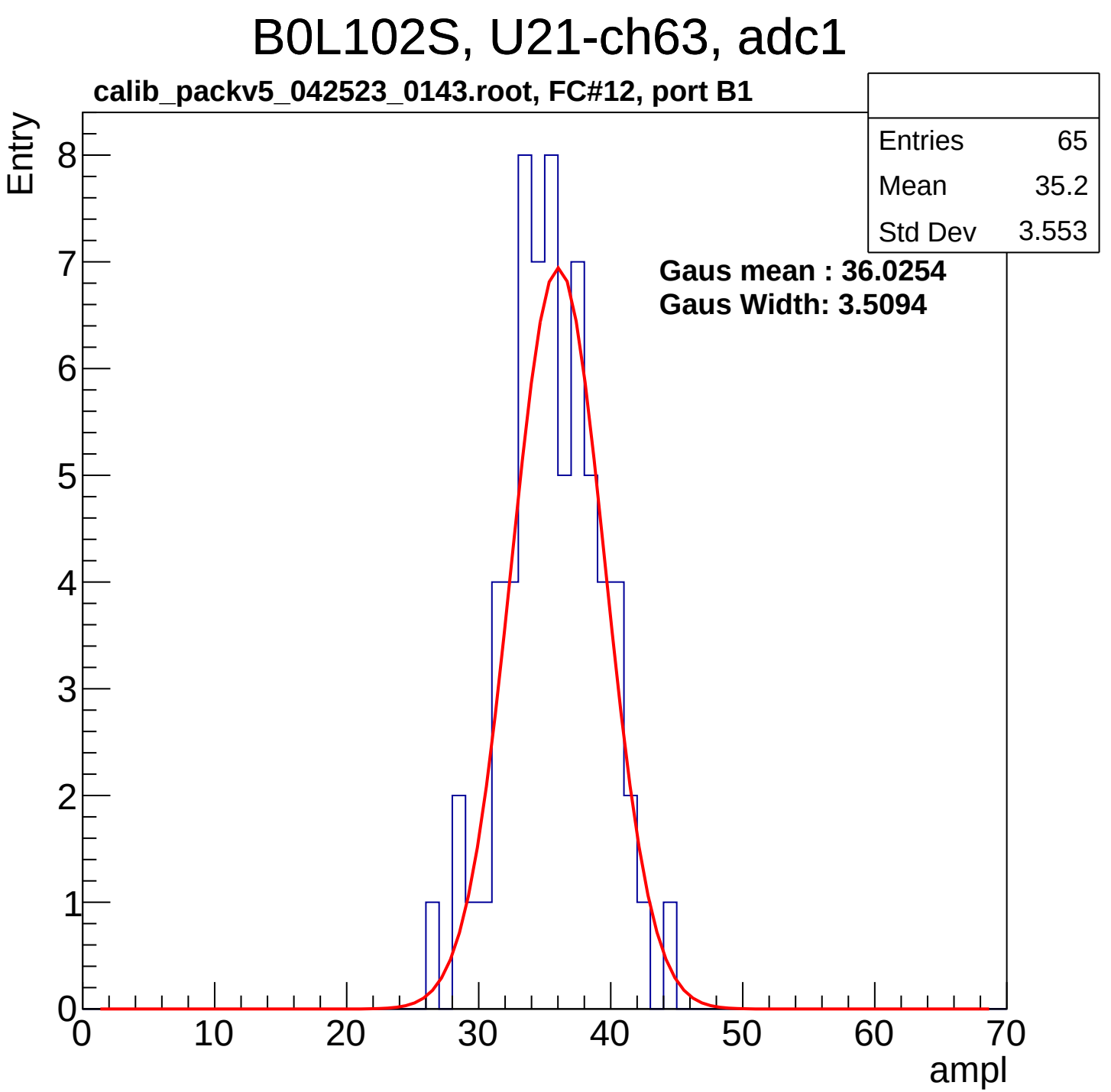

B0L102S, U21-ch62, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 53.46
Std Dev 3.715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

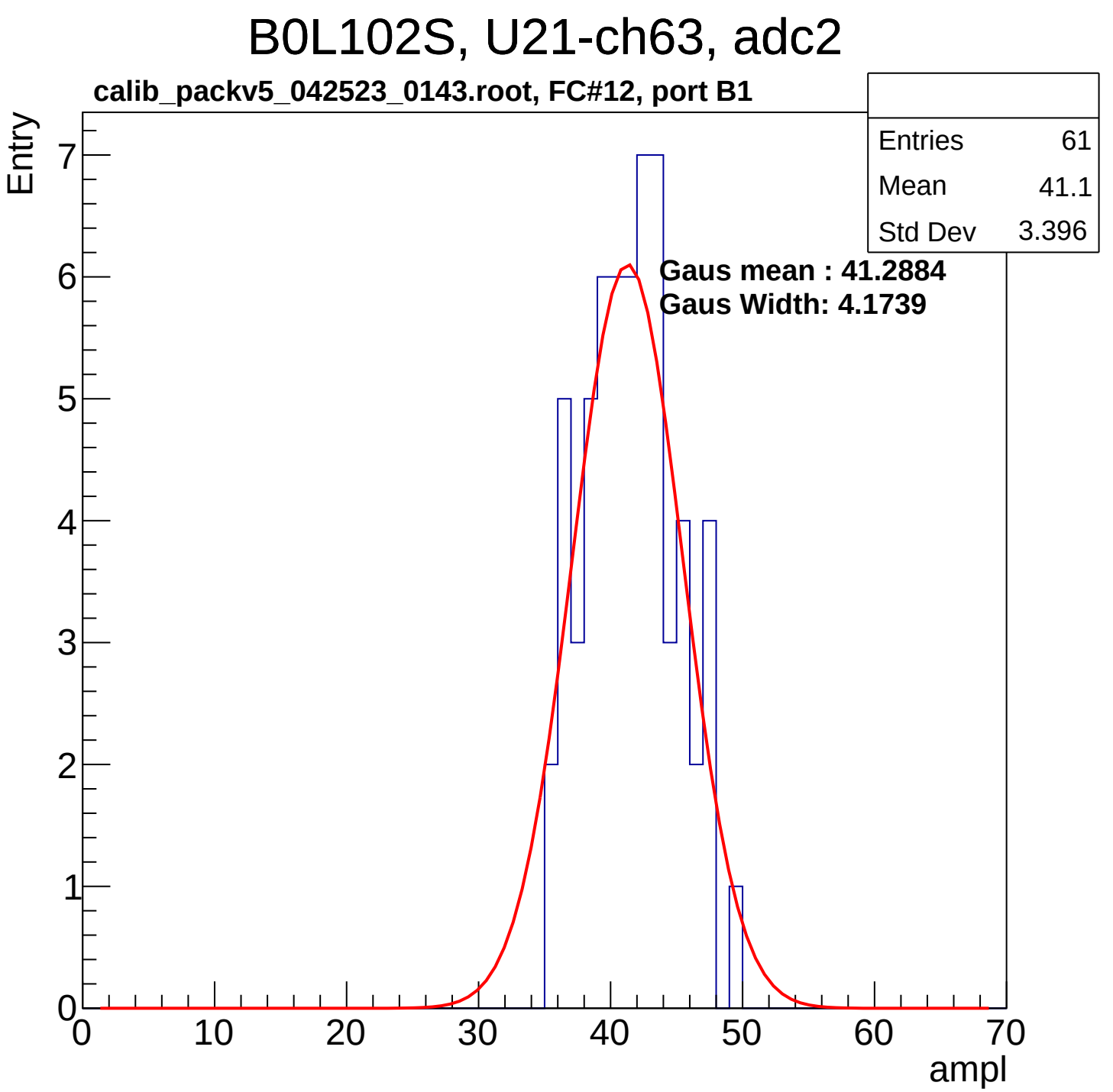

B0L102S, U21-ch62, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 58.7
Std Dev 2.653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

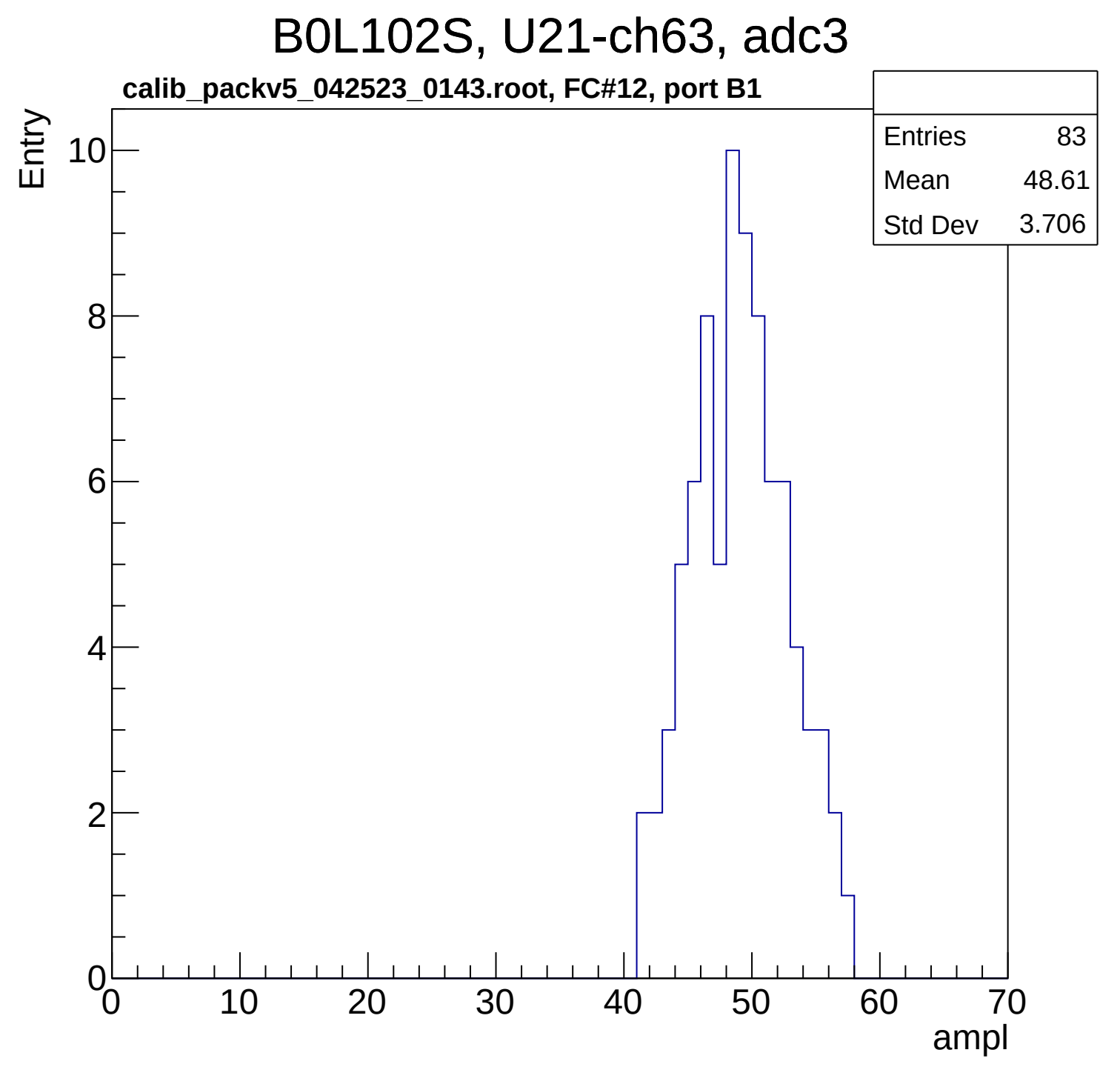

B0L102S, U21-ch62, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 57.67
Std Dev 15.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

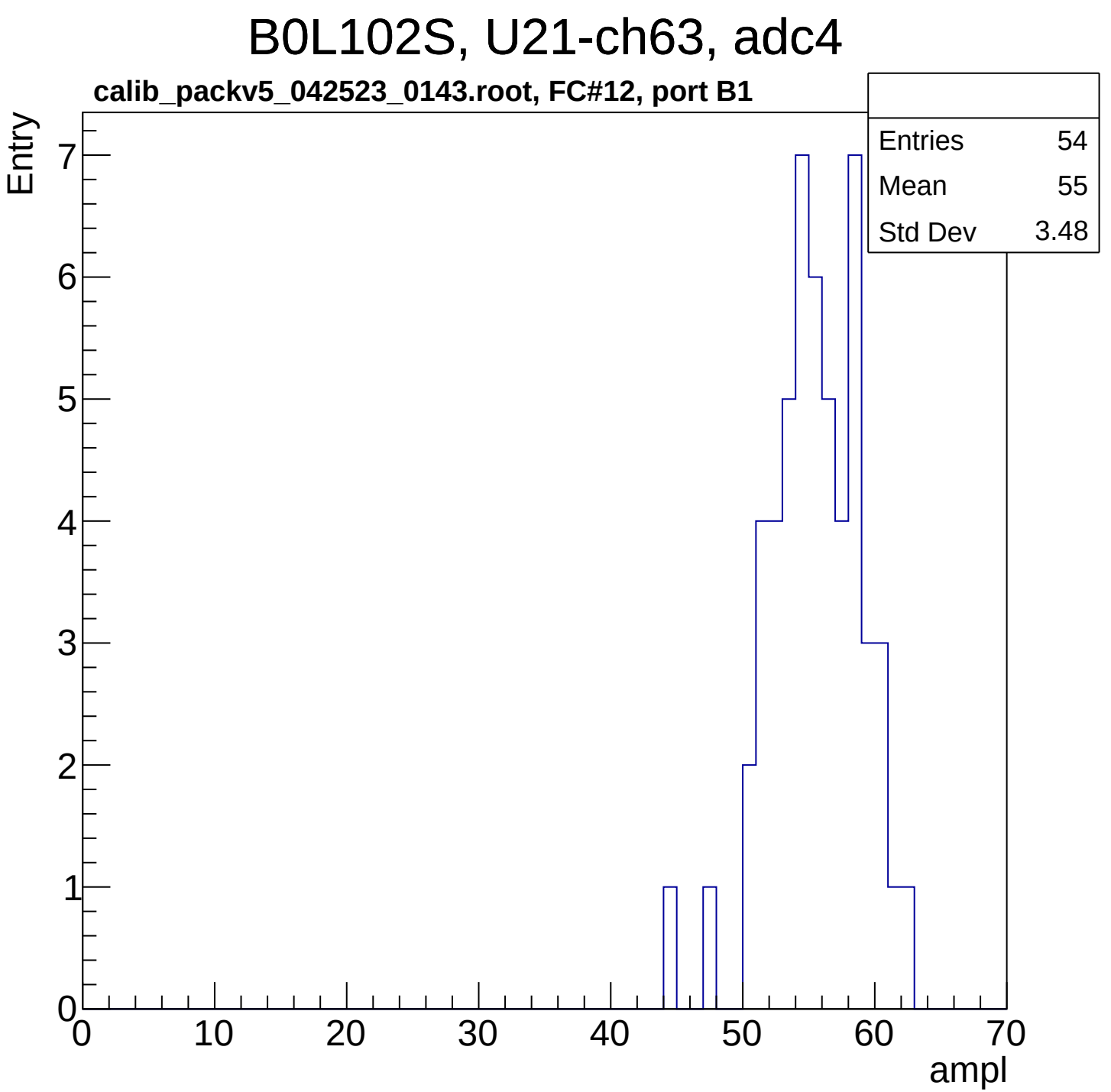

B0L102S, U21-ch62, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

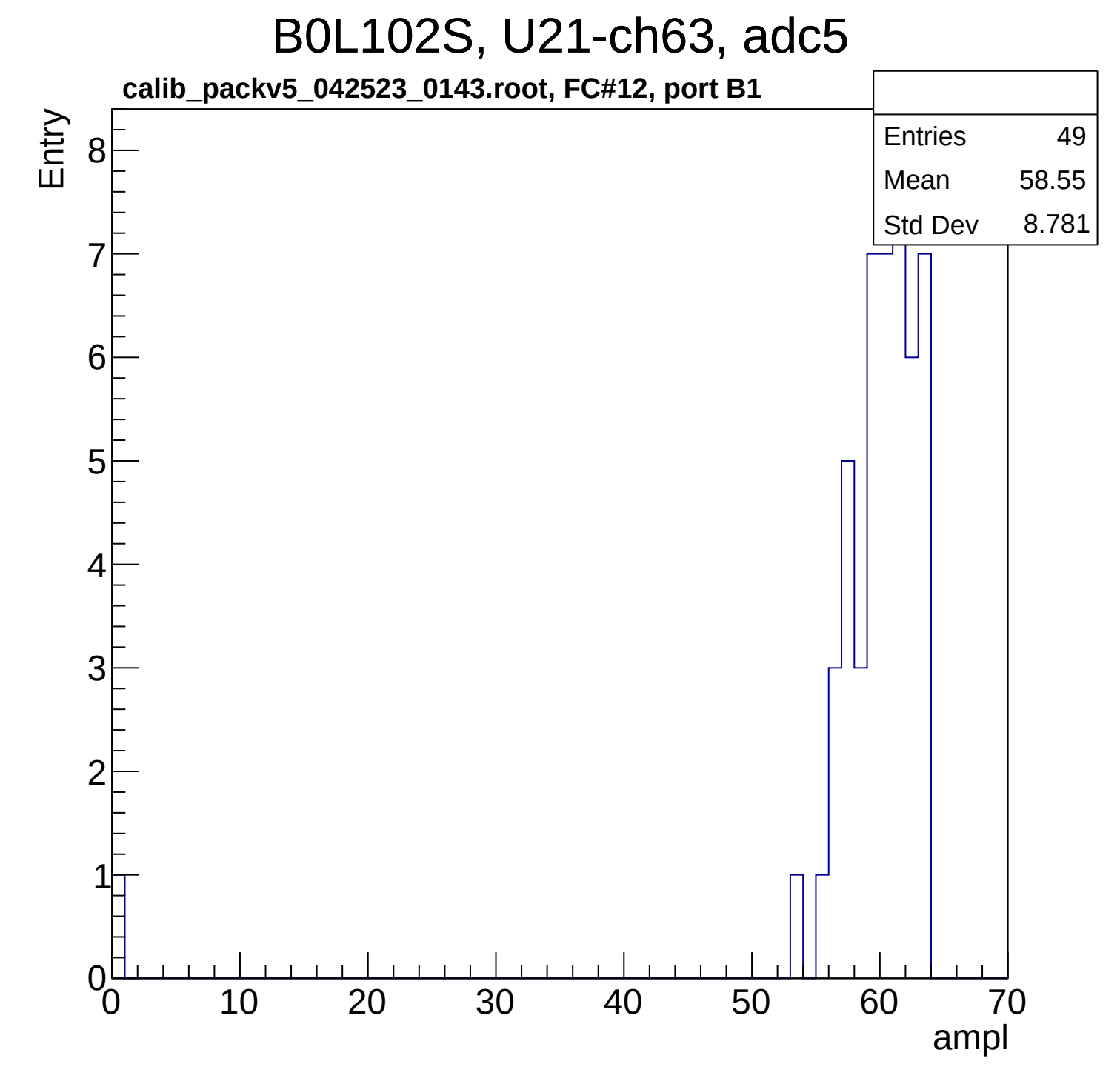

B0L102S, U21-ch63, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 27.71
Std Dev 3.433
Gaus mean : 28.3400
Gaus Width: 3.9375
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch63, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.2
Std Dev 3.553
Gaus mean : 36.0254
Gaus Width: 3.5094
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch63, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 41.1
Std Dev 3.396
Gaus mean : 41.2884
Gaus Width: 4.1739
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch63, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 48.61
Std Dev 3.706
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch63, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 55
Std Dev 3.48
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch63, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 58.55
Std Dev 8.781
Entry
ampl
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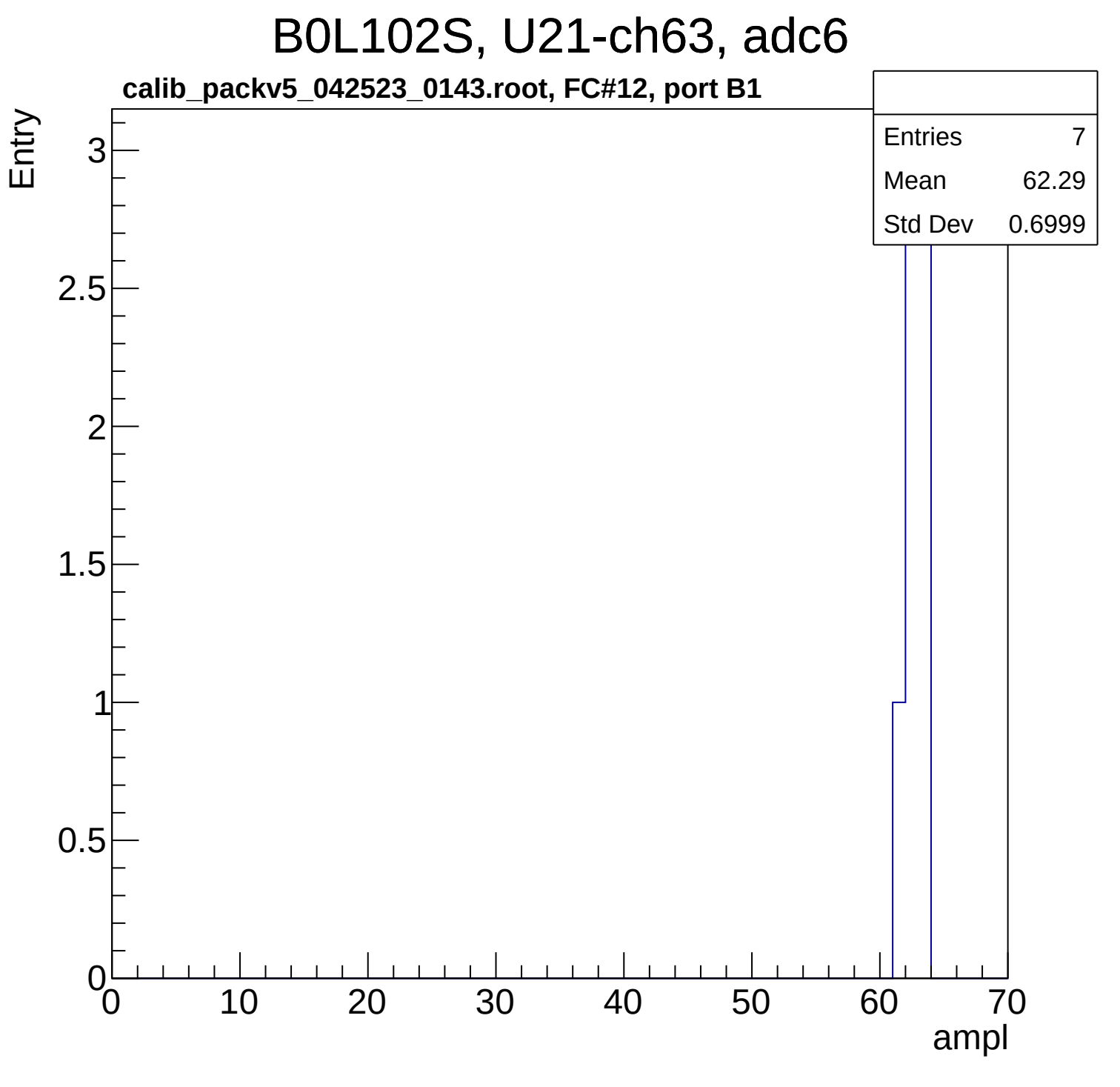
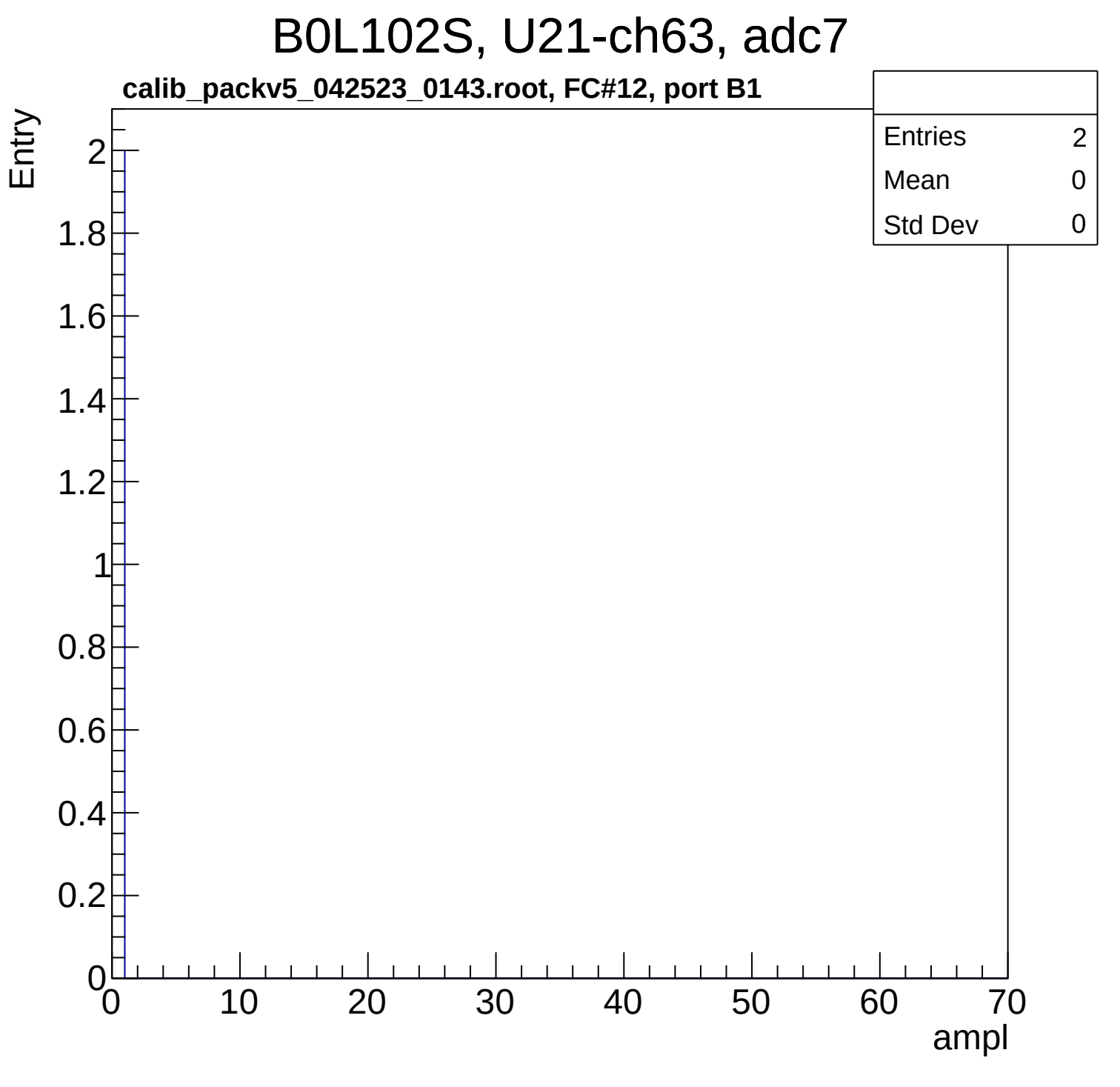
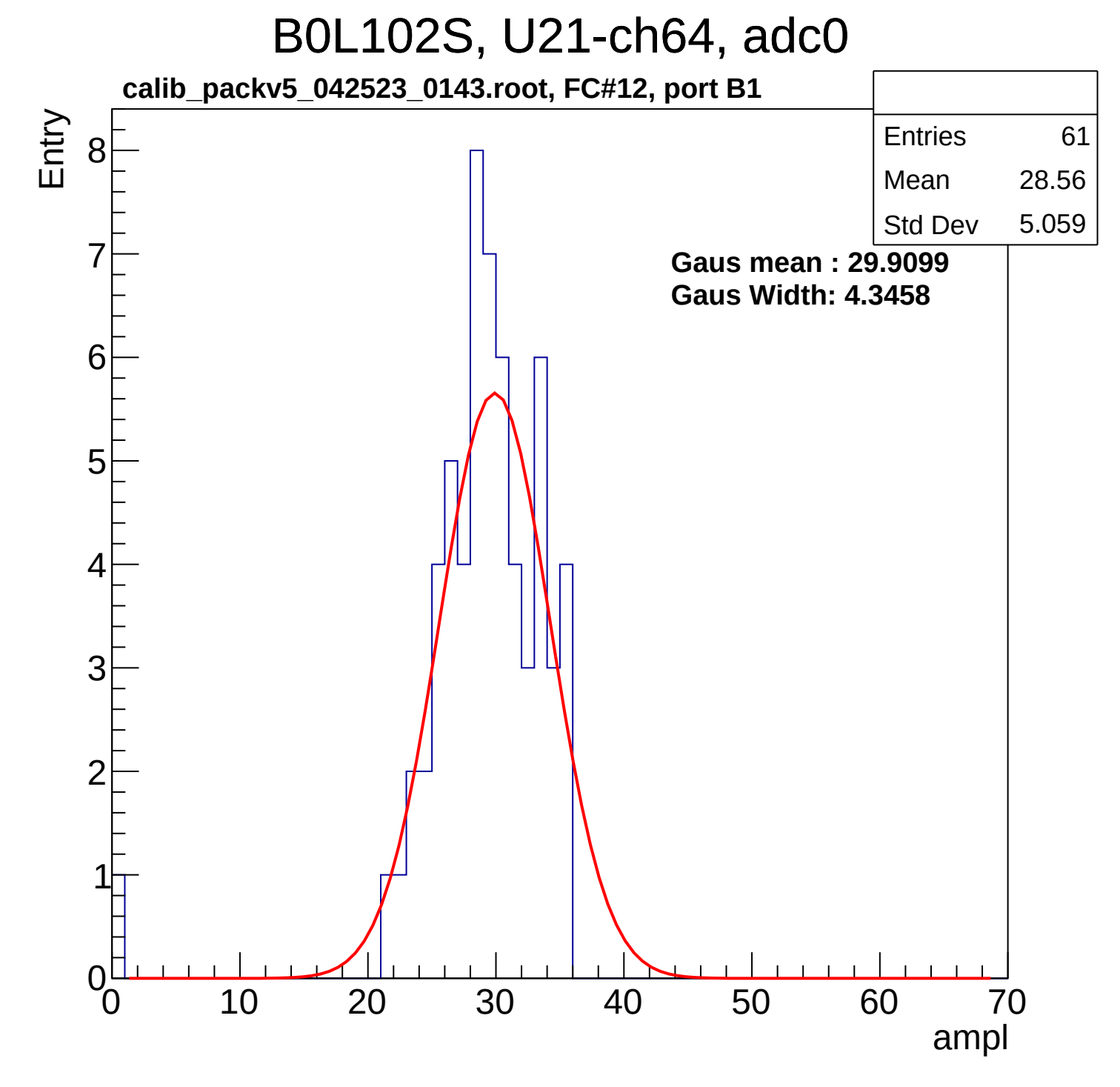
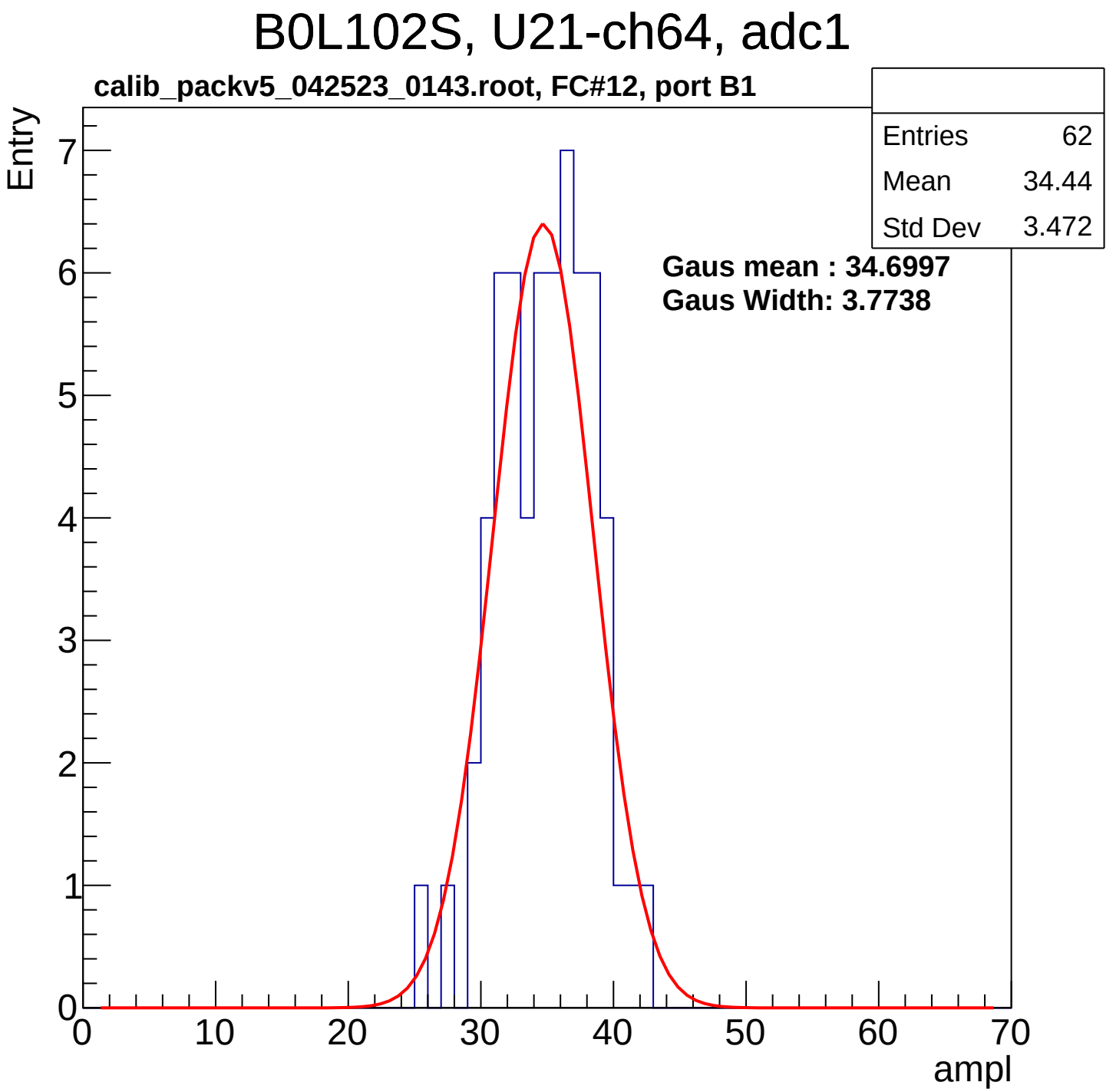
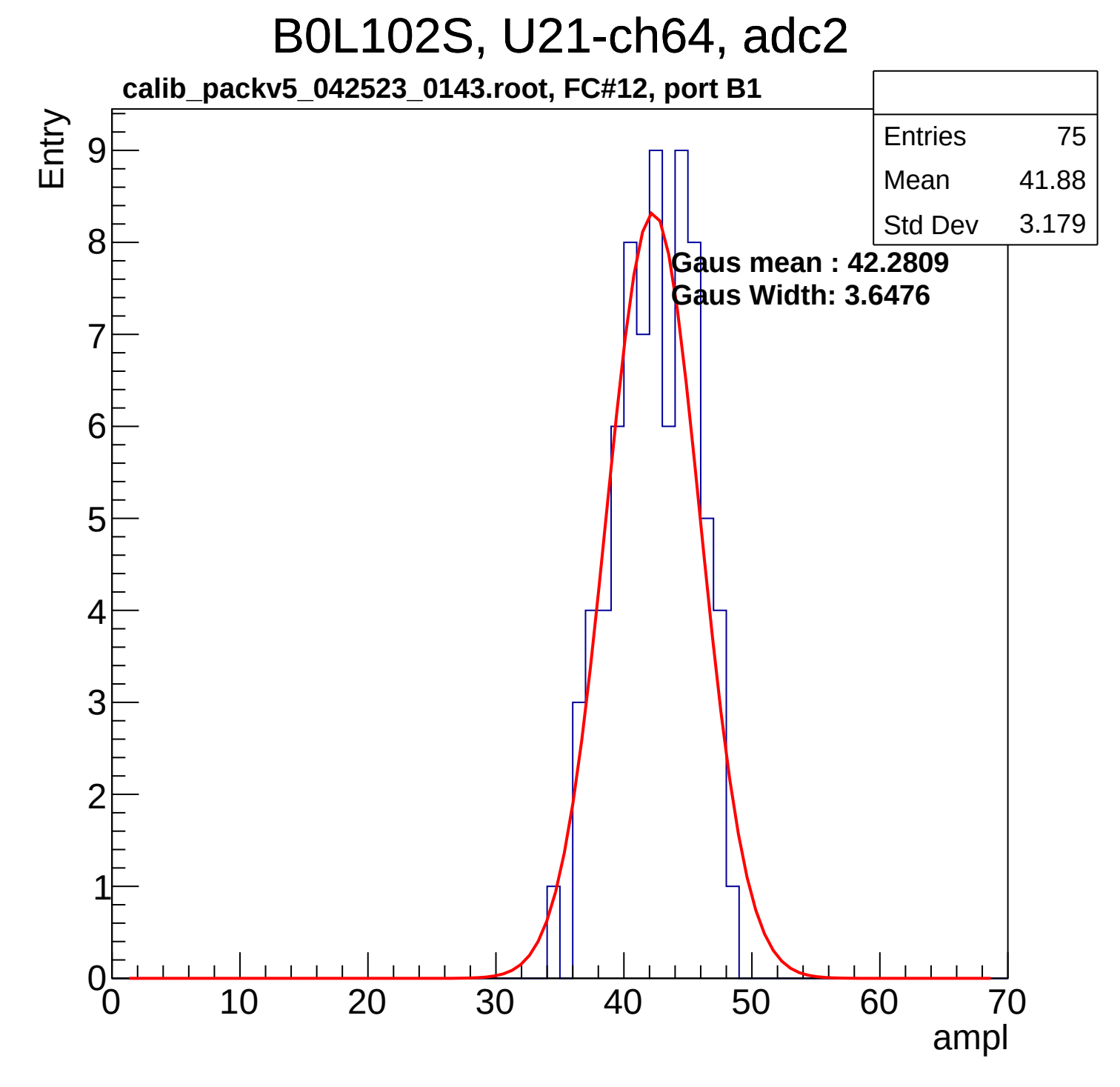
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

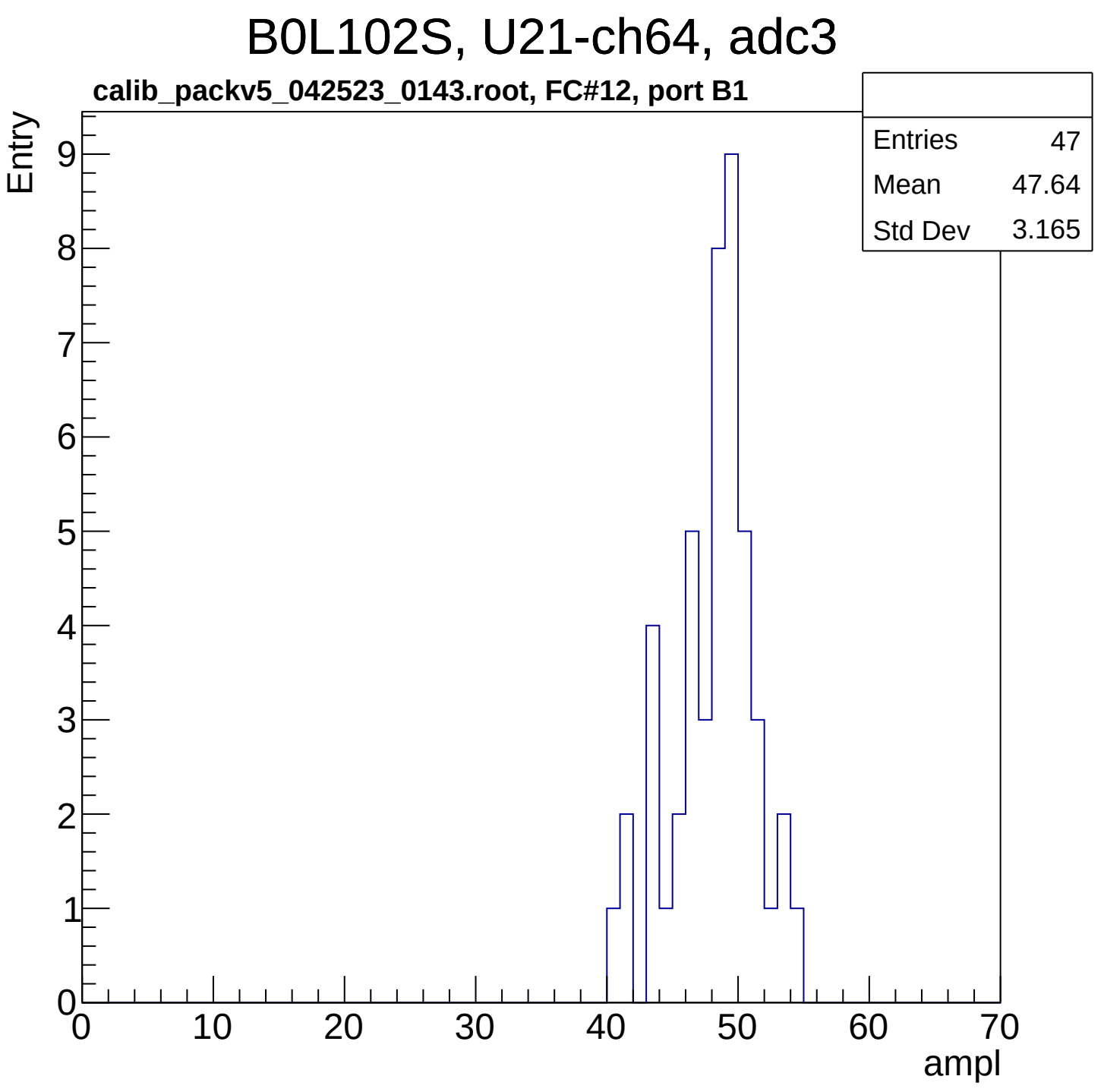

B0L102S, U21-ch63, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch63, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch64, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 28.56
Std Dev 5.059
Gaus mean : 29.9099
Gaus Width: 4.3458
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch64, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 34.44
Std Dev 3.472
Gaus mean : 34.6997
Gaus Width: 3.7738
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch64, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 41.88
Std Dev 3.179
Gaus mean : 42.2809
Gaus Width: 3.6476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch64, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 47.64
Std Dev 3.165
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
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40
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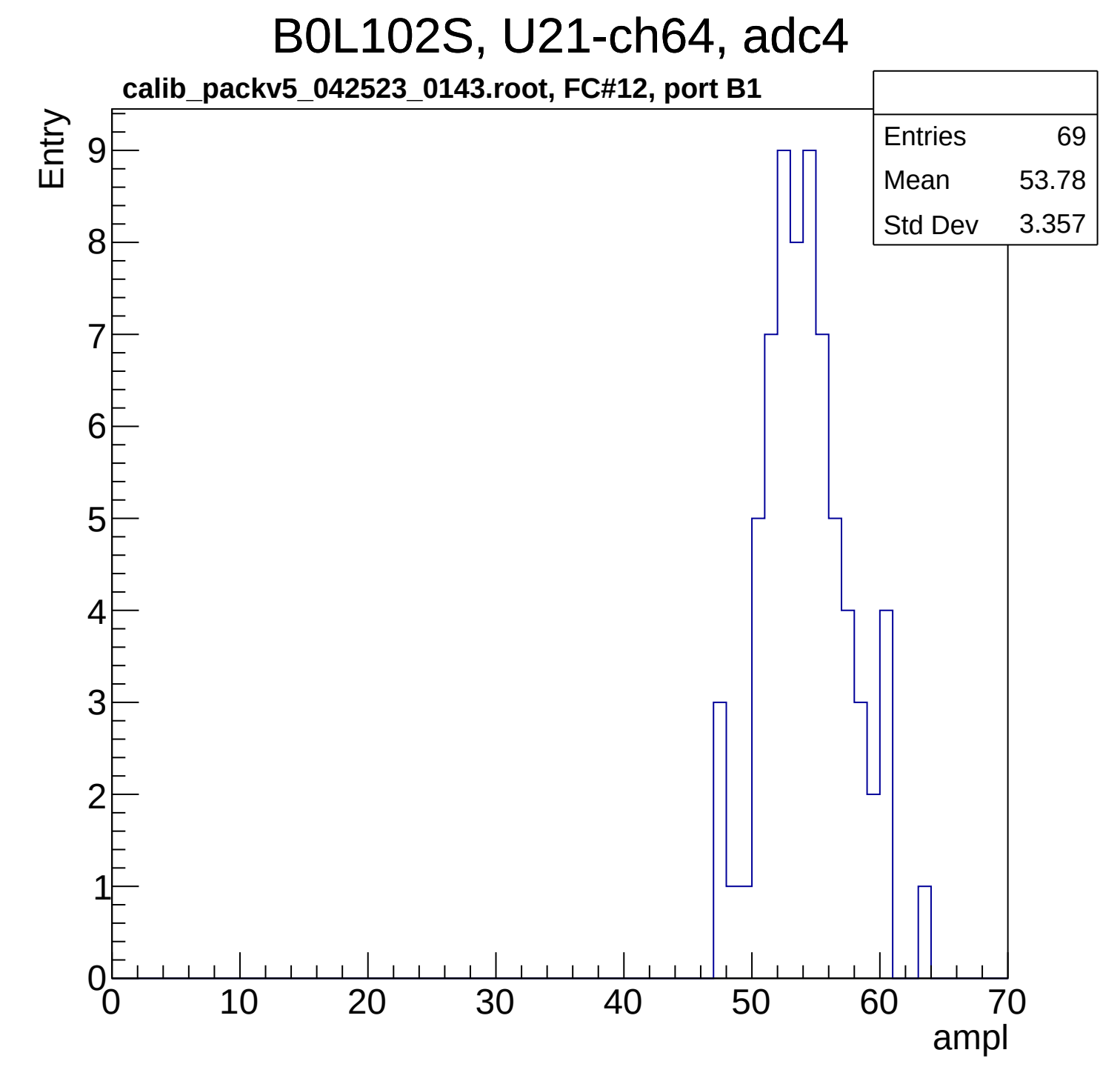
60
70


B0L102S, U21-ch64, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 53.78
Std Dev 3.357
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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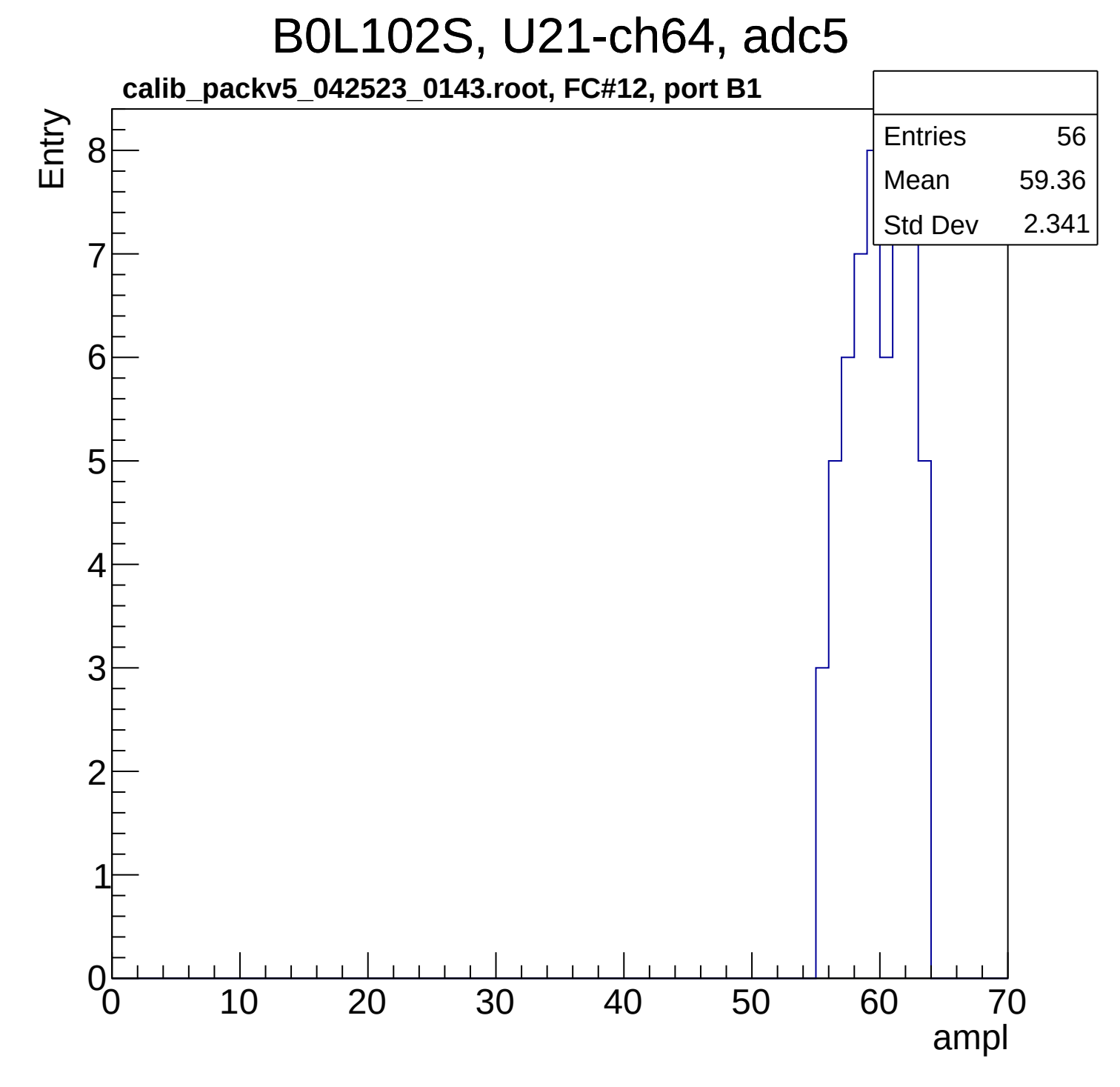
40
50
60
70


B0L102S, U21-ch64, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 59.36
Std Dev 2.341
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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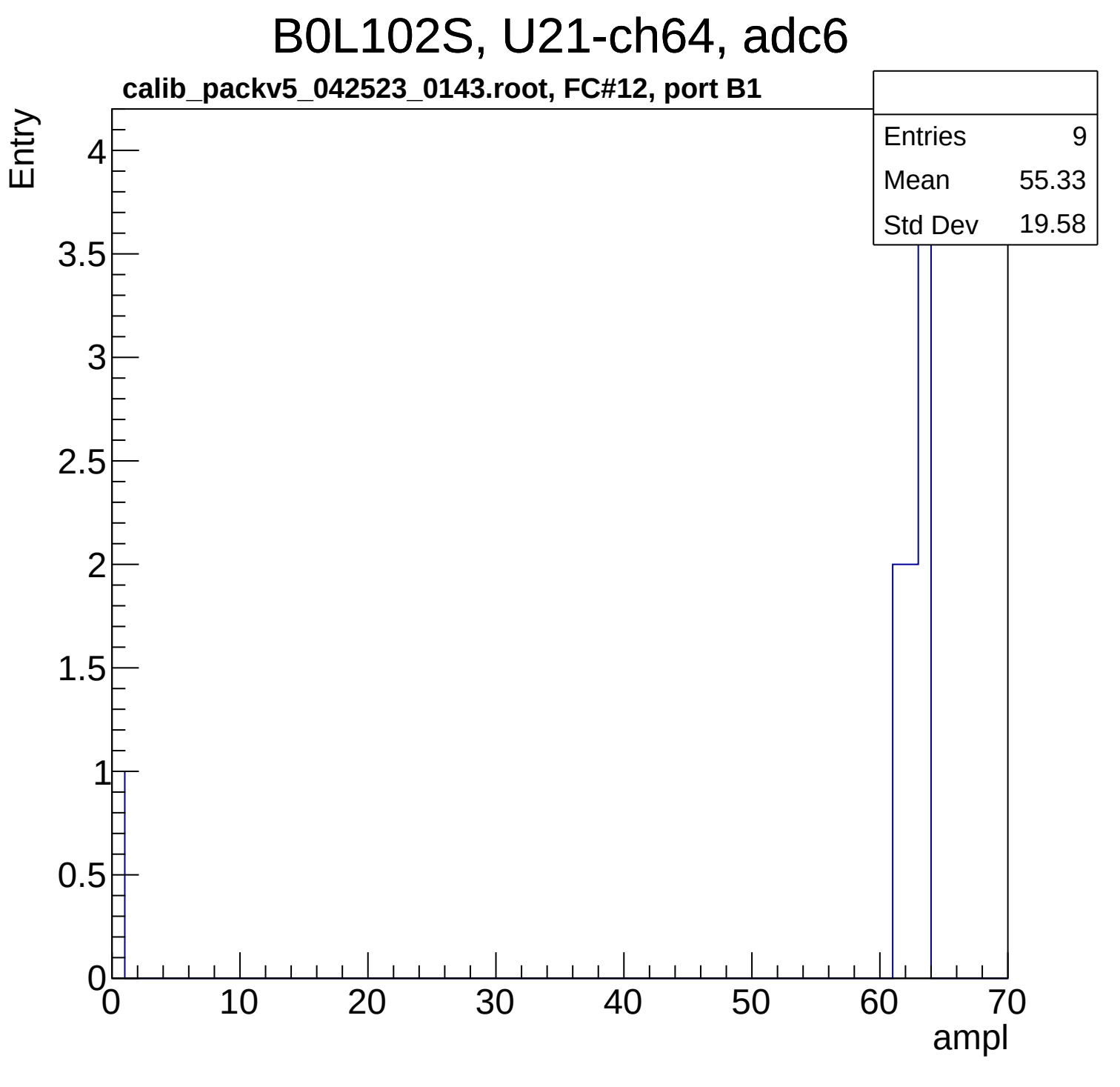
B0L102S, U21-ch64, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
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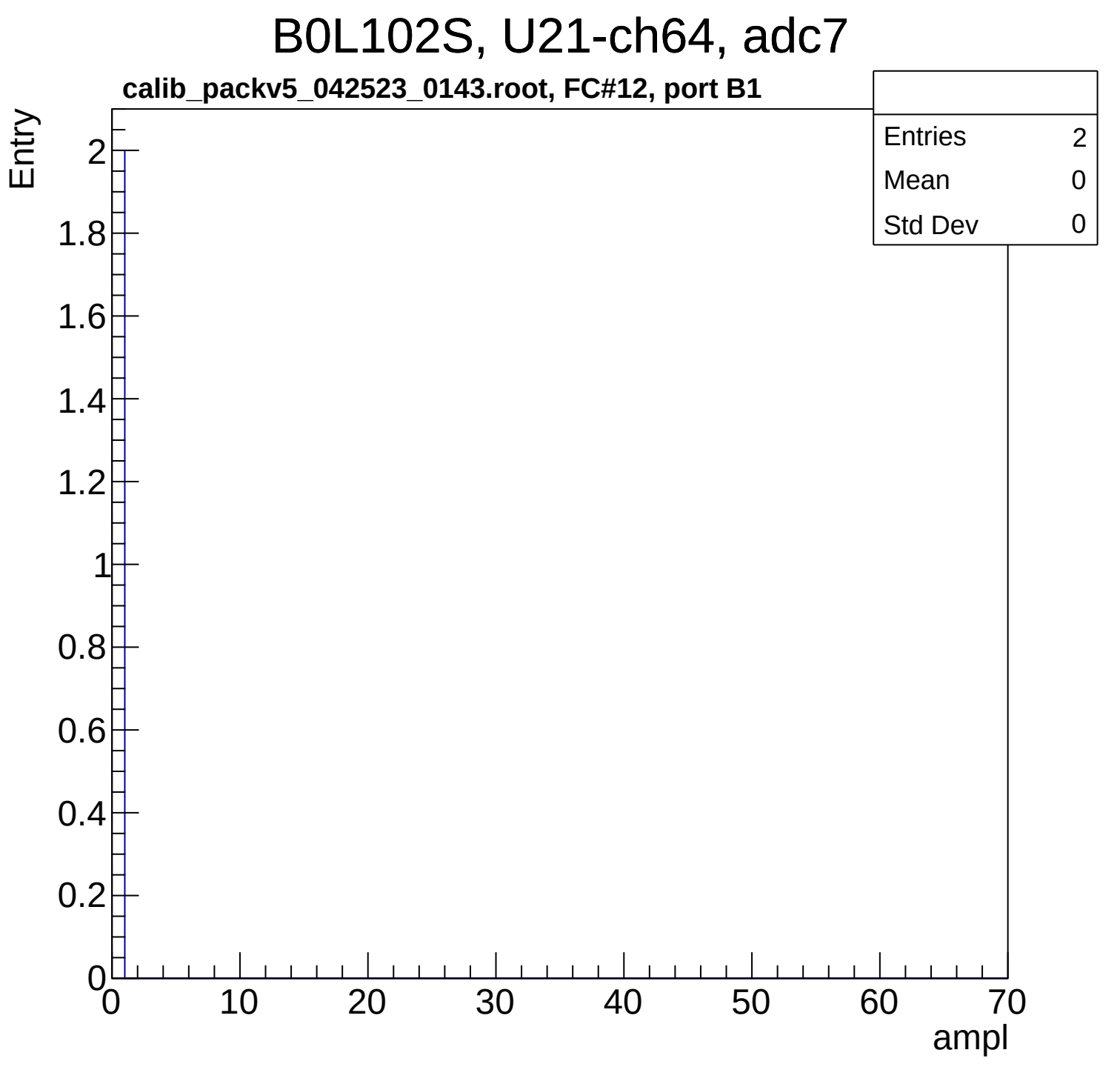
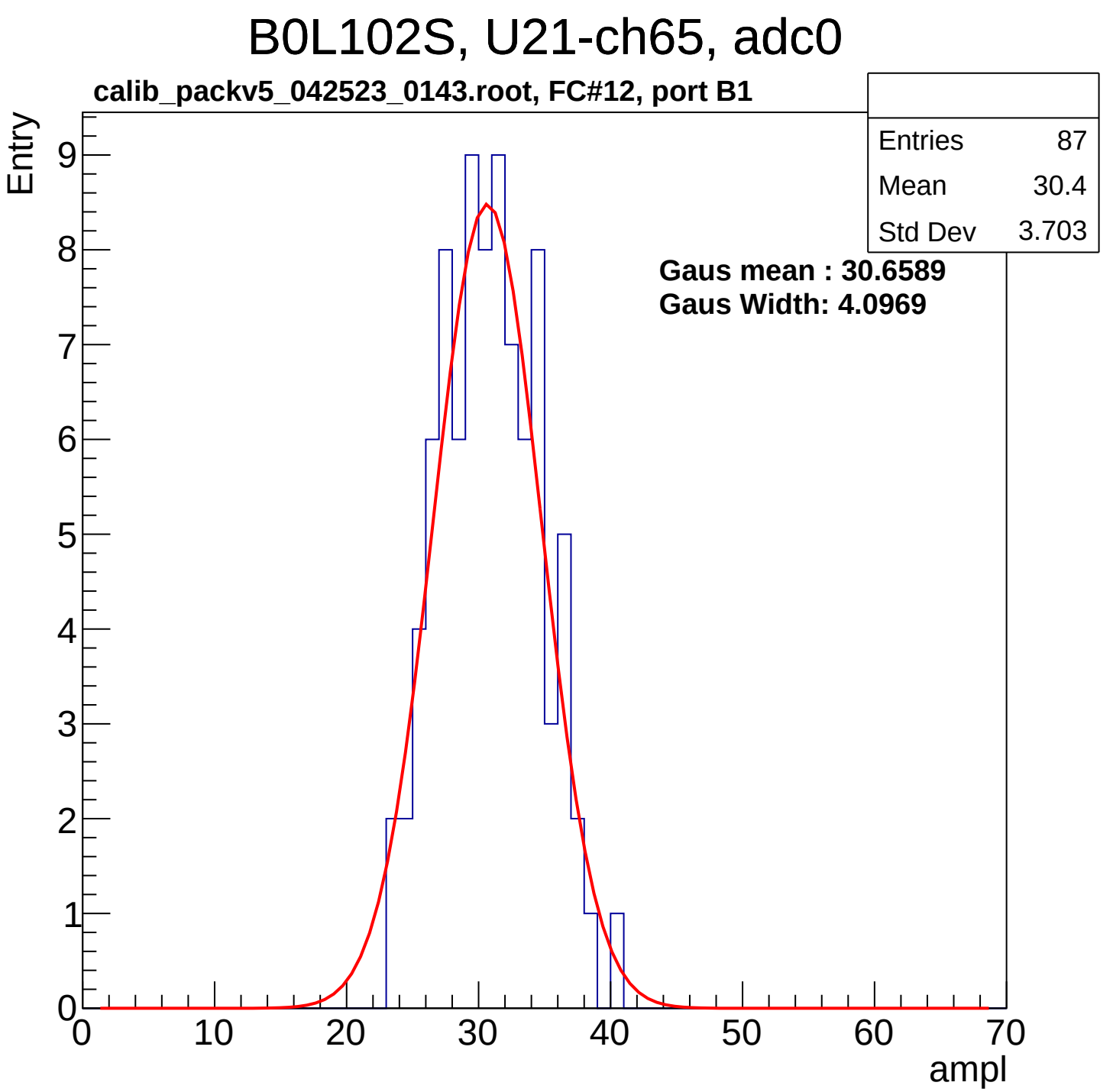
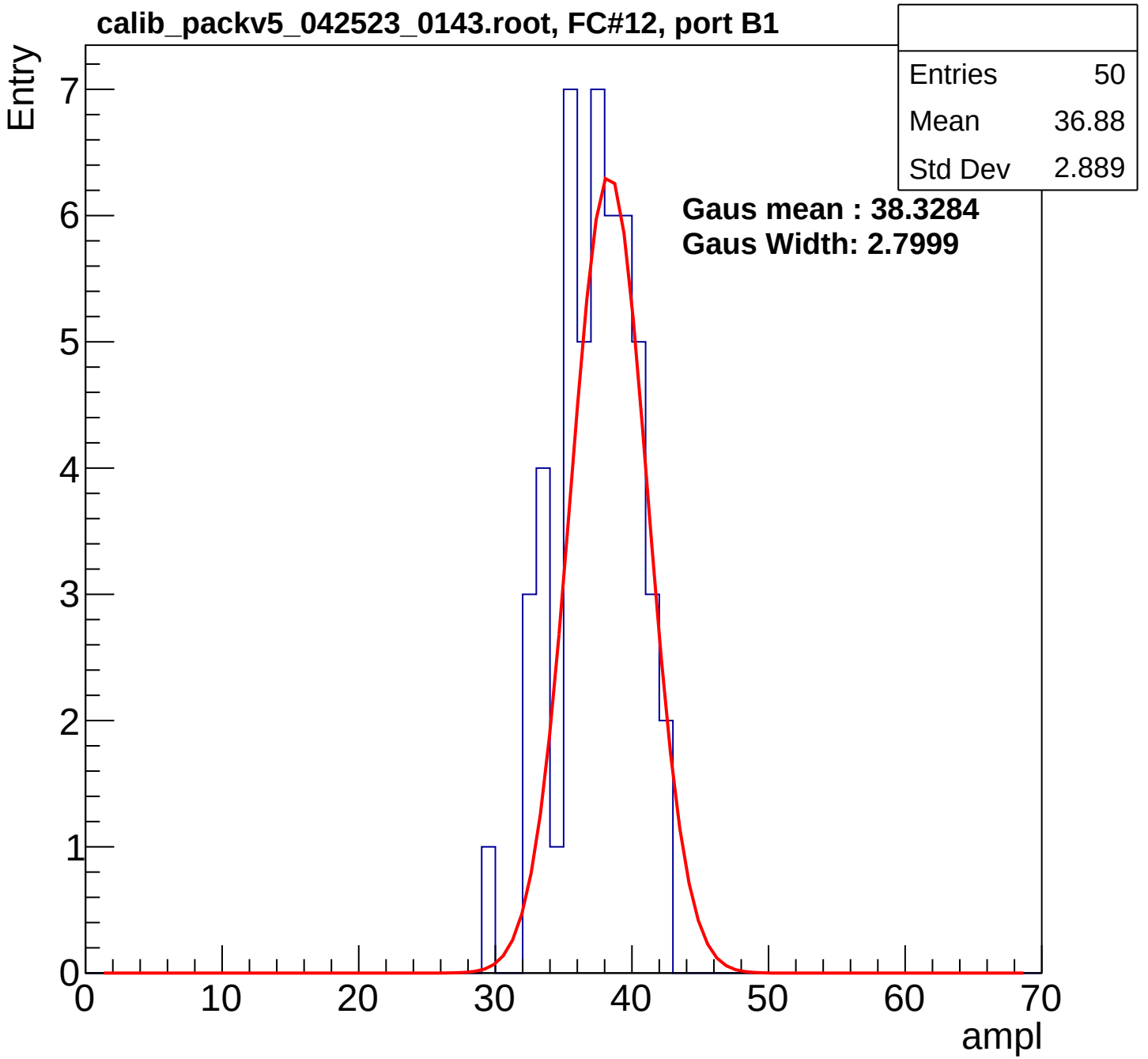
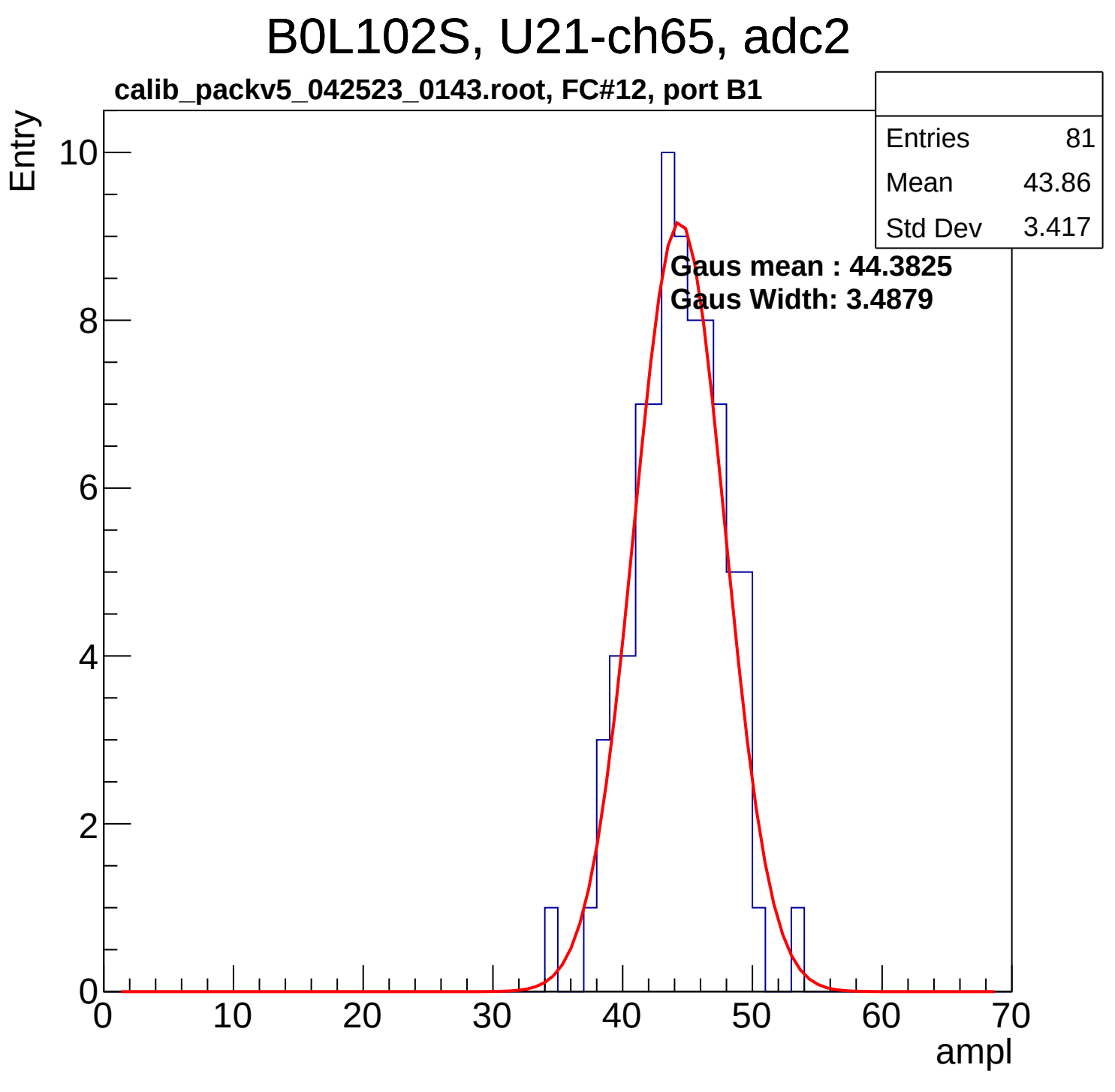
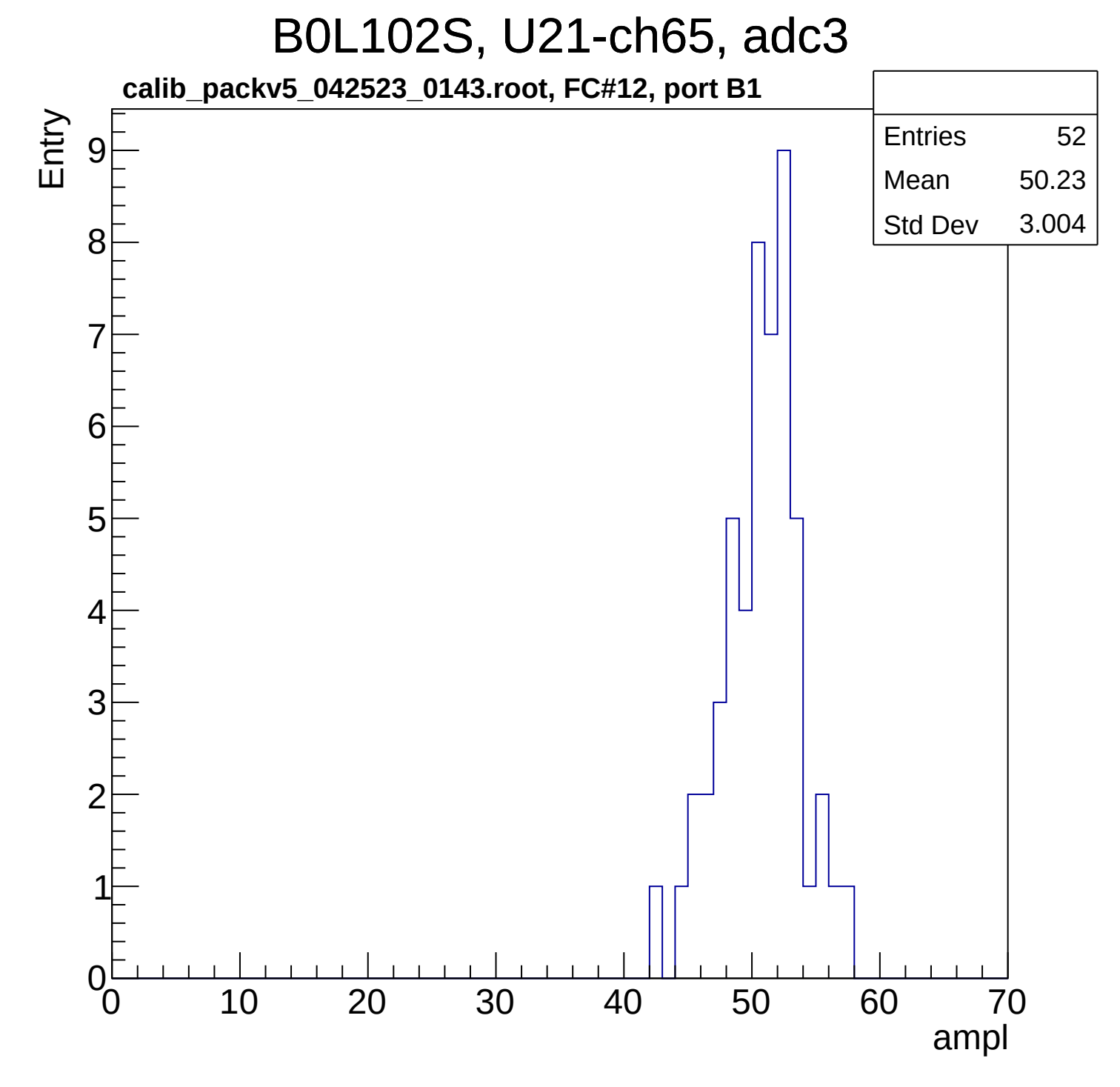
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch64, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch65, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 30.4
Std Dev 3.703
Gaus mean : 30.6589
Gaus Width: 4.0969
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch65, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 36.88
Std Dev 2.889
Gaus mean : 38.3284
Gaus Width: 2.7999
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch65, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 43.86
Std Dev 3.417
Gaus mean : 44.3825
Gaus Width: 3.4879
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch65, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 50.23
Std Dev 3.004
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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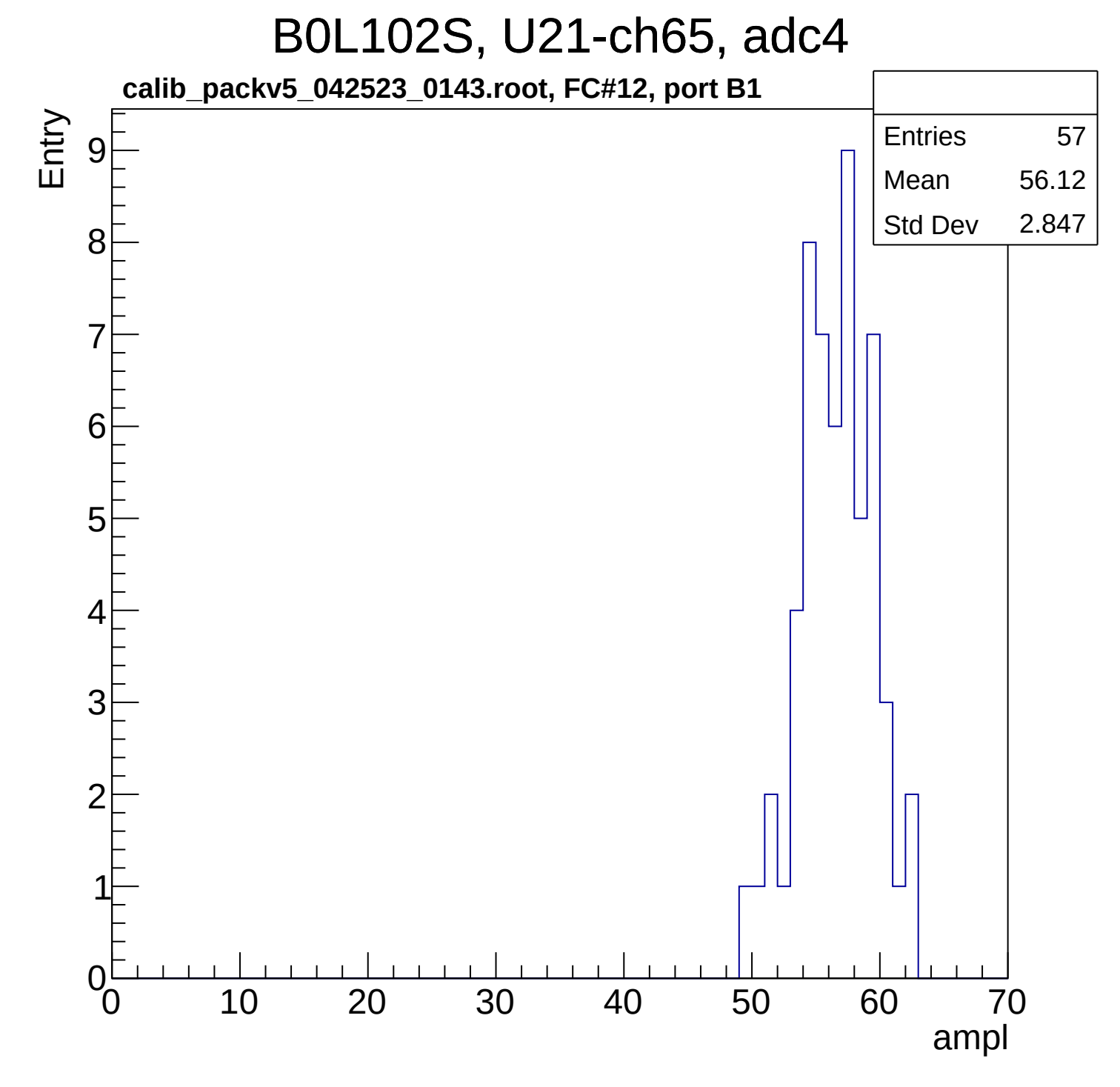
50
60
70


B0L102S, U21-ch65, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 56.12
Std Dev 2.847
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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60
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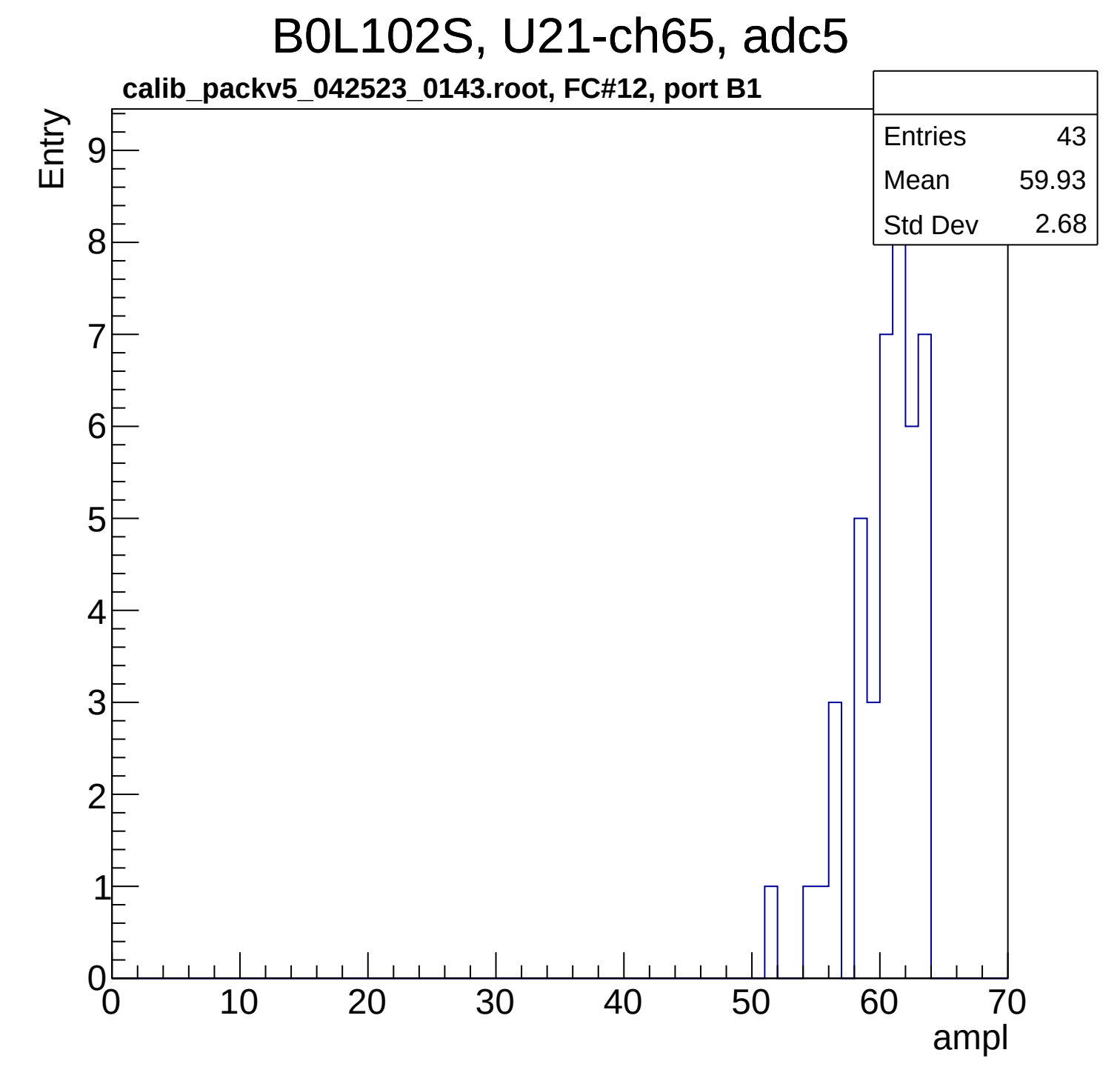
B0L102S, U21-ch65, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.93
Std Dev 2.68
Entry
ampl
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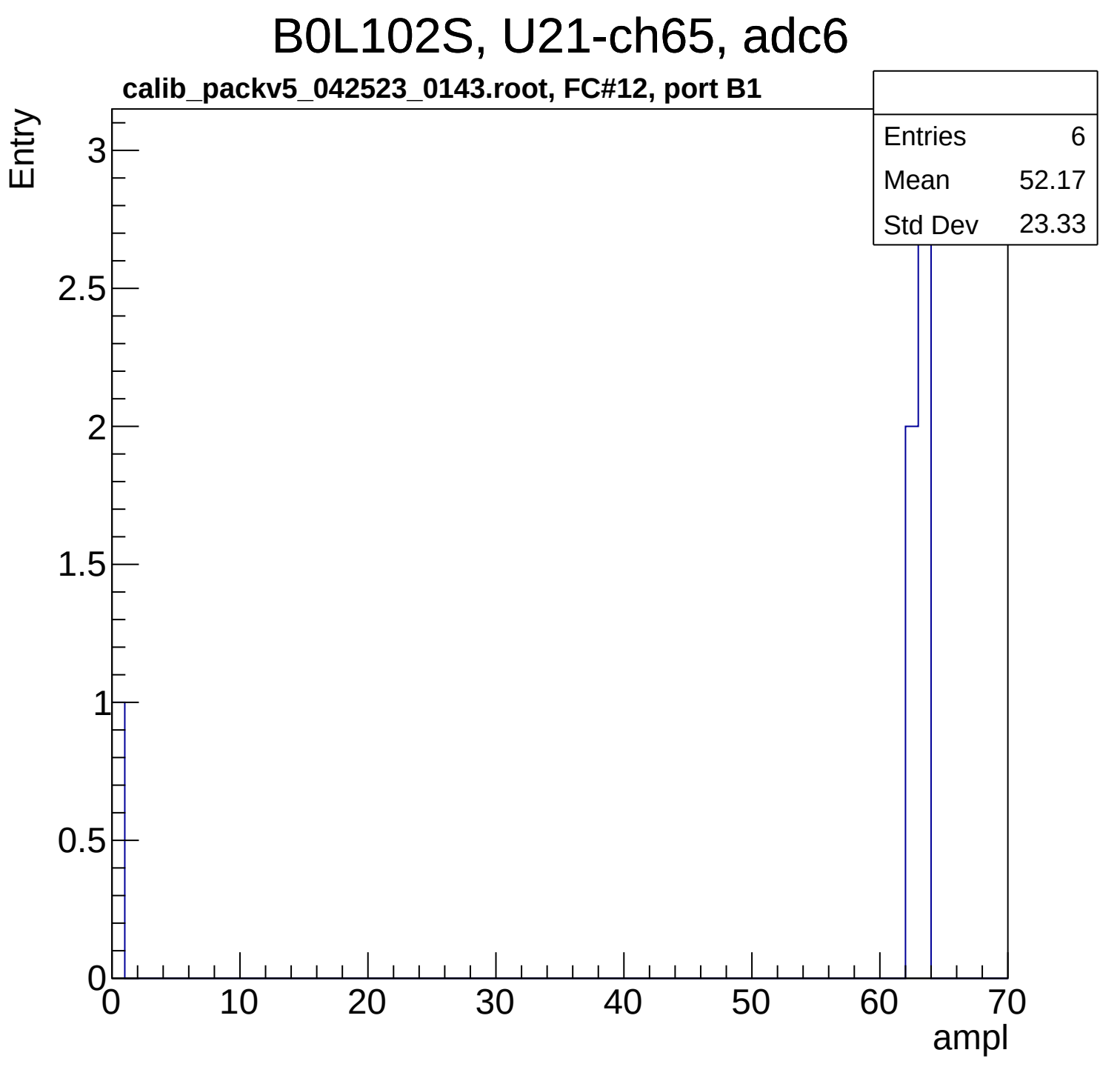
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch65, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 52.17
Std Dev 23.33
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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20
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40
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60
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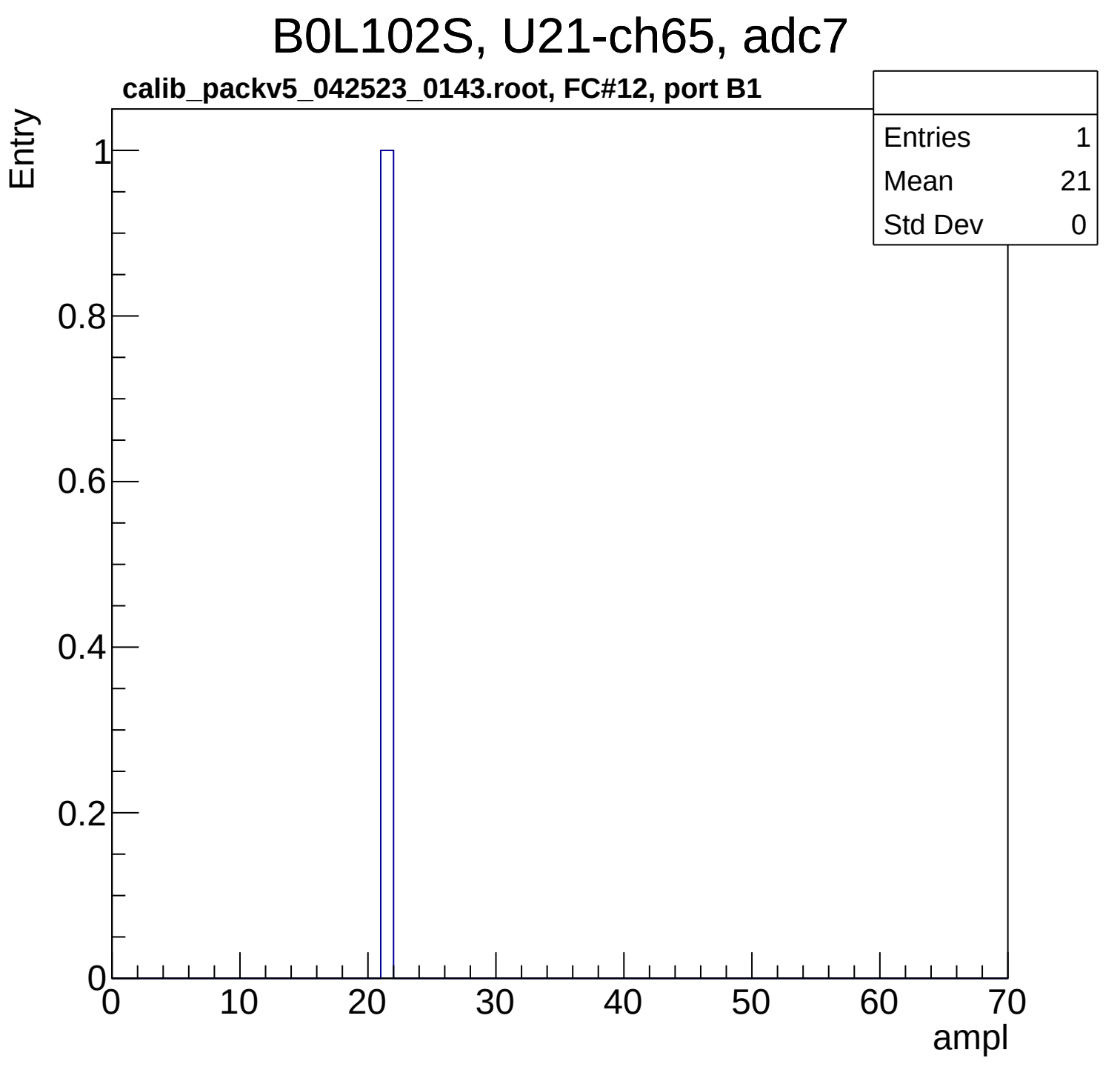
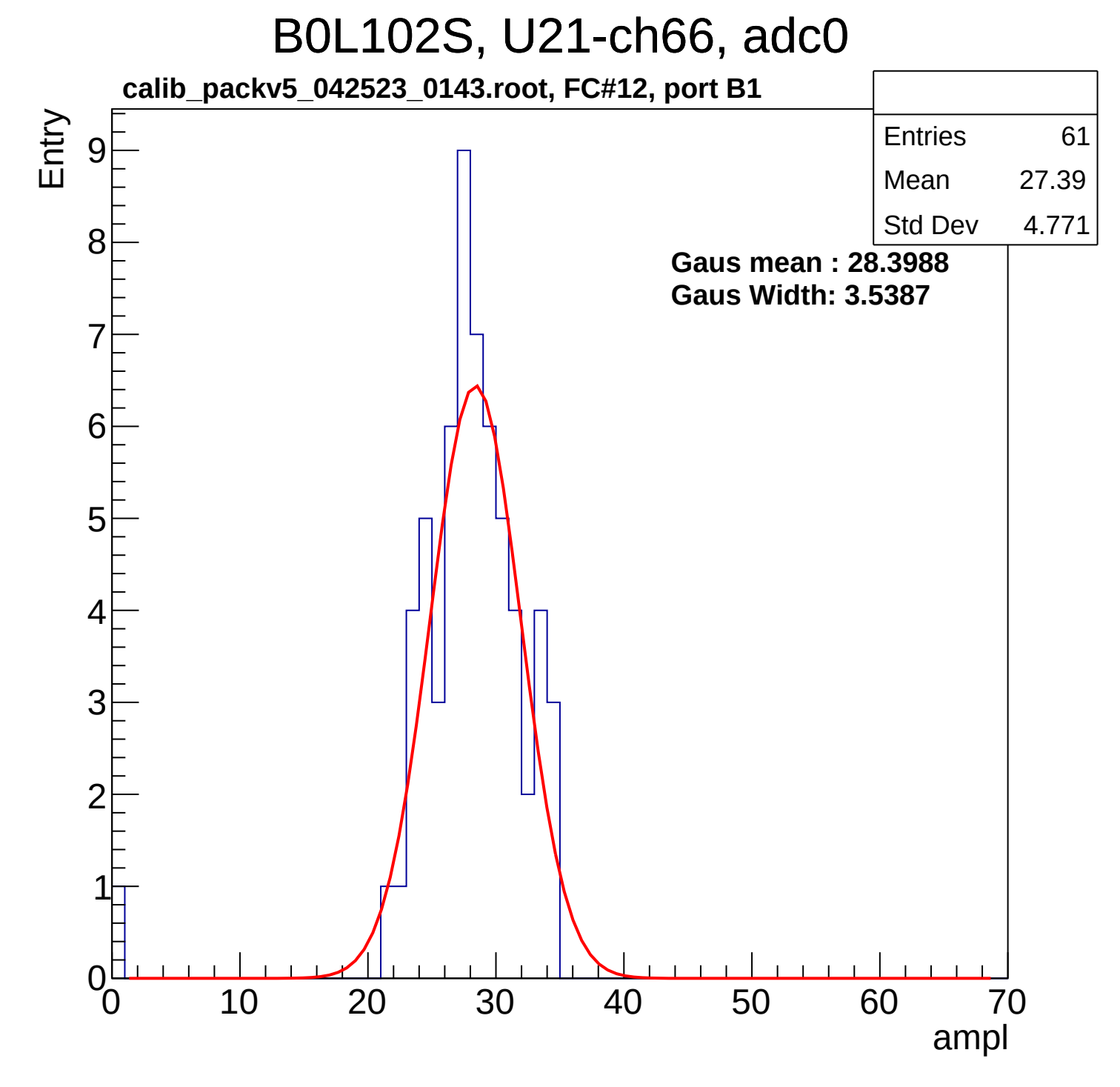

B0L102S, U21-ch65, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch66, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 27.39
Std Dev 4.771
Gaus mean : 28.3988
Gaus Width: 3.5387
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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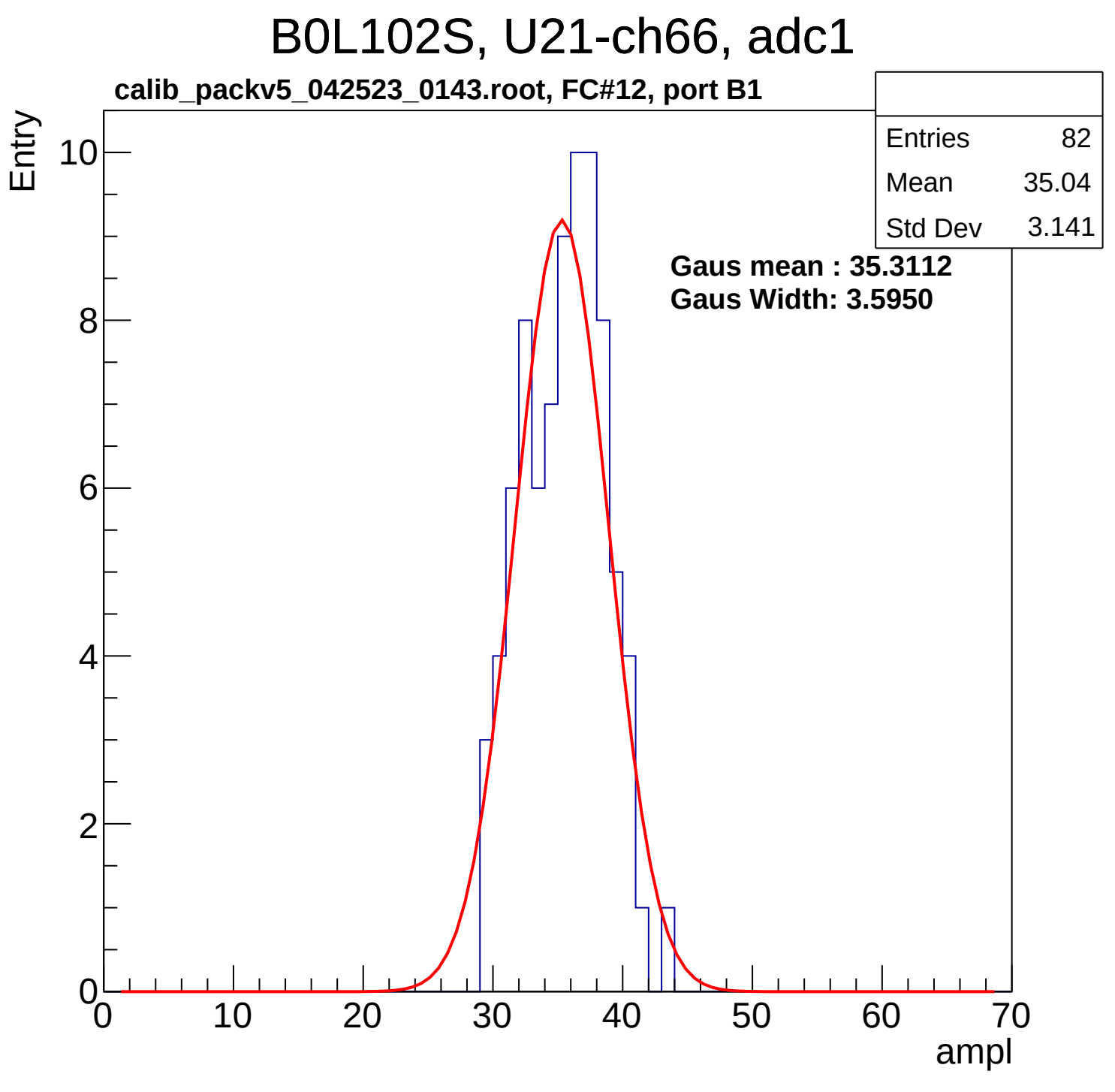
B0L102S, U21-ch66, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 35.04
Std Dev 3.141
Gaus mean : 35.3112
Gaus Width: 3.5950
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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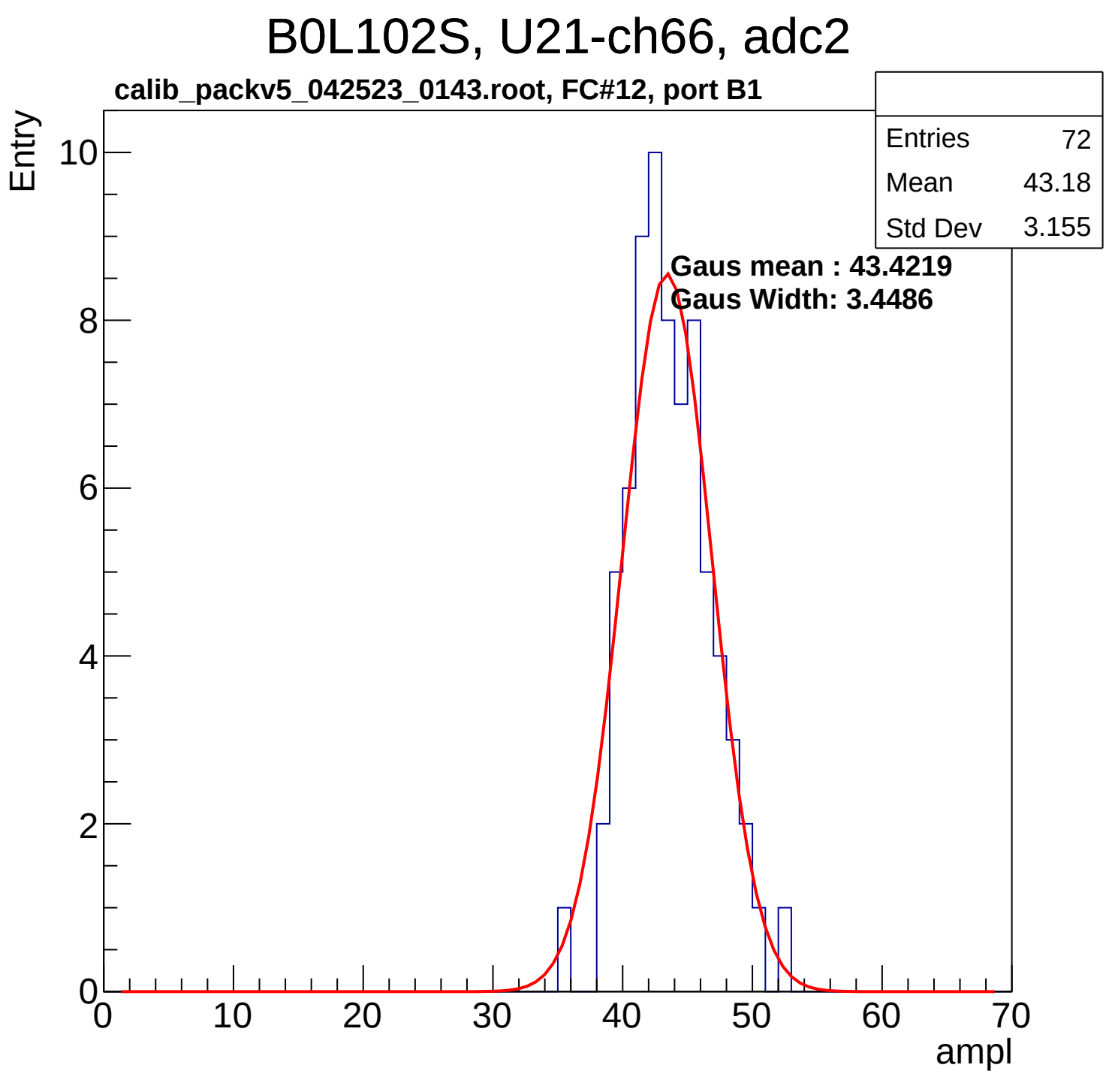
B0L102S, U21-ch66, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 43.18
Std Dev 3.155
Gaus mean : 43.4219
Gaus Width: 3.4486
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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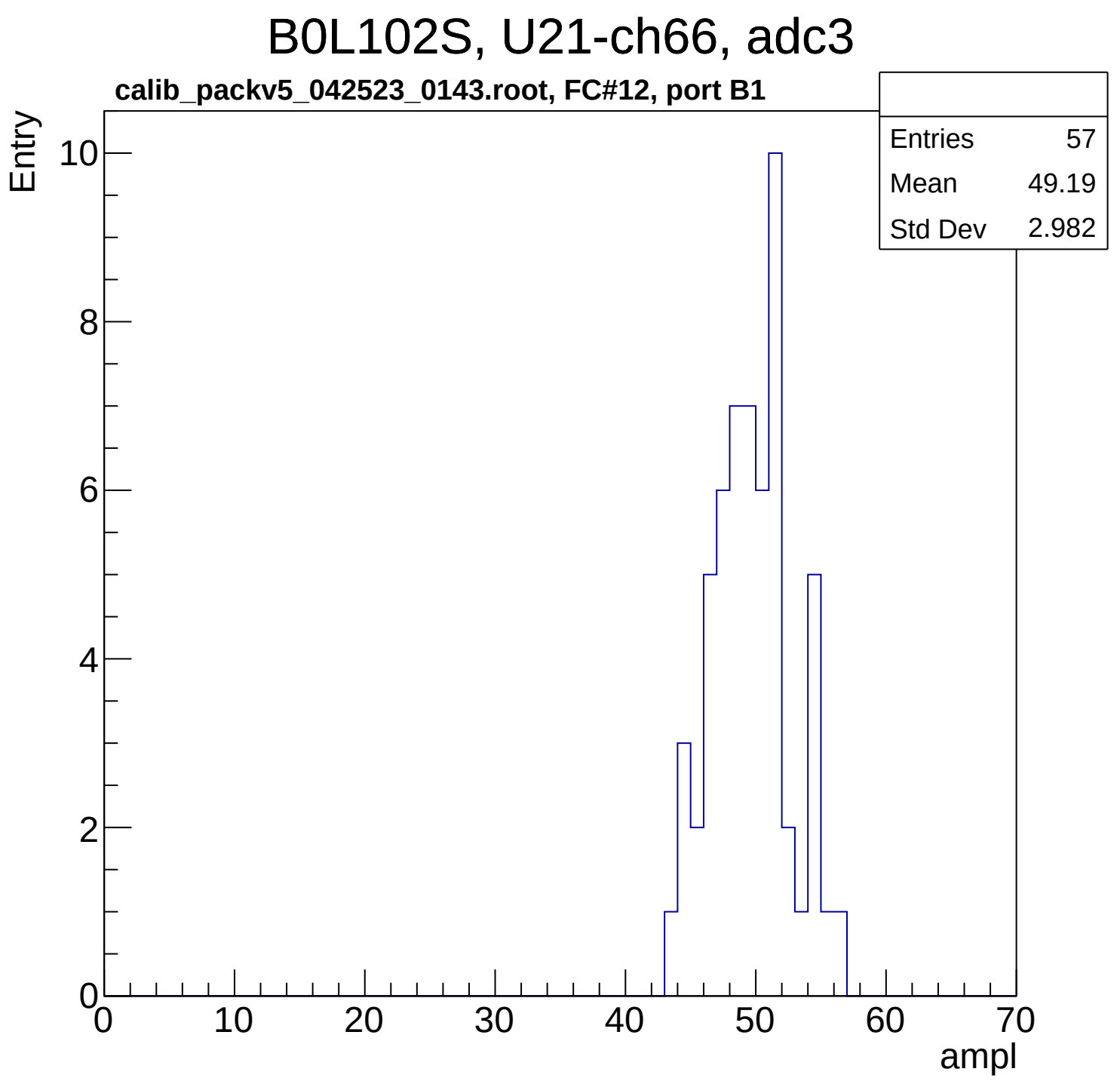
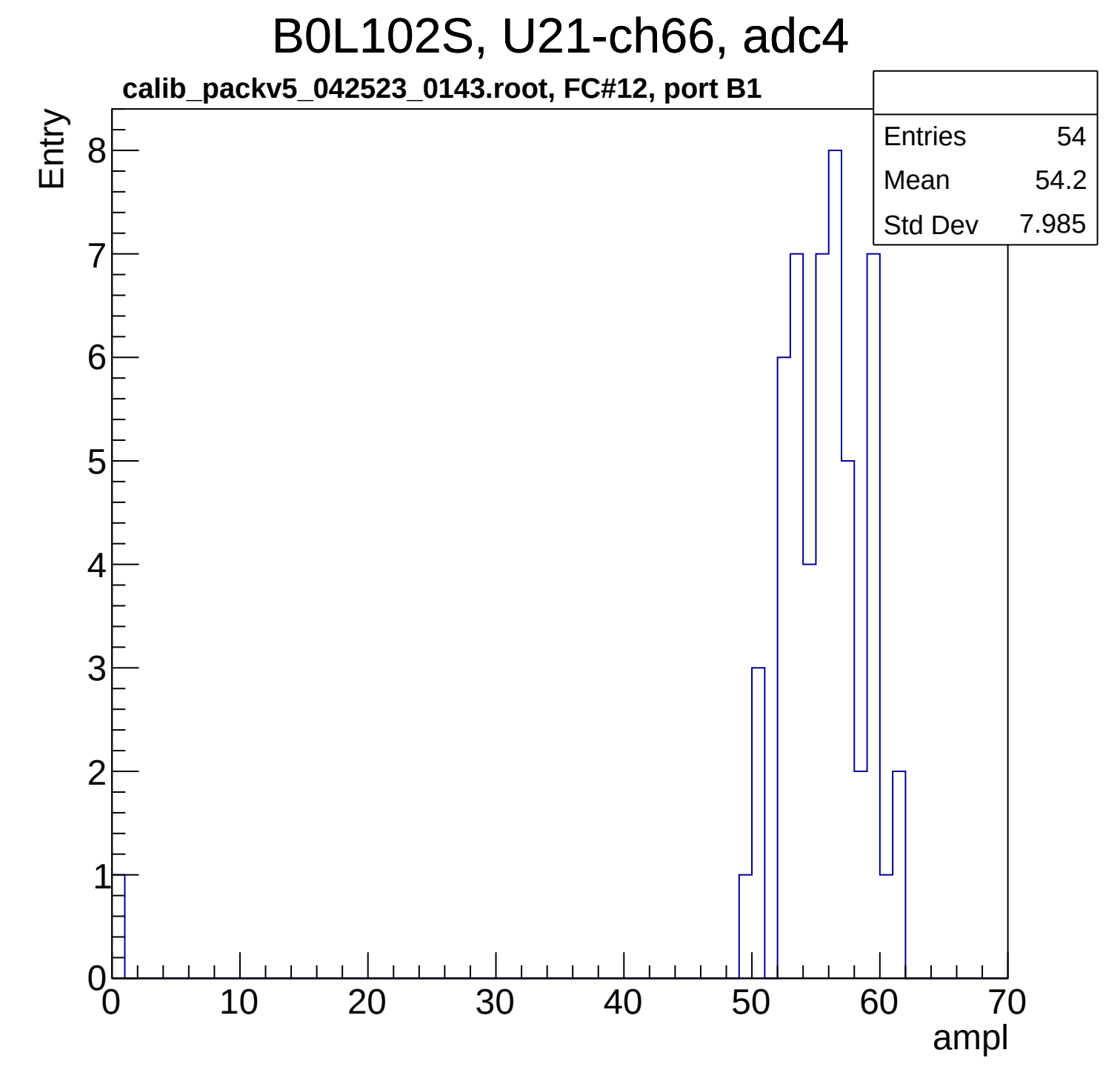

B0L102S, U21-ch66, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 49.19
Std Dev 2.982
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch66, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.2
Std Dev 7.985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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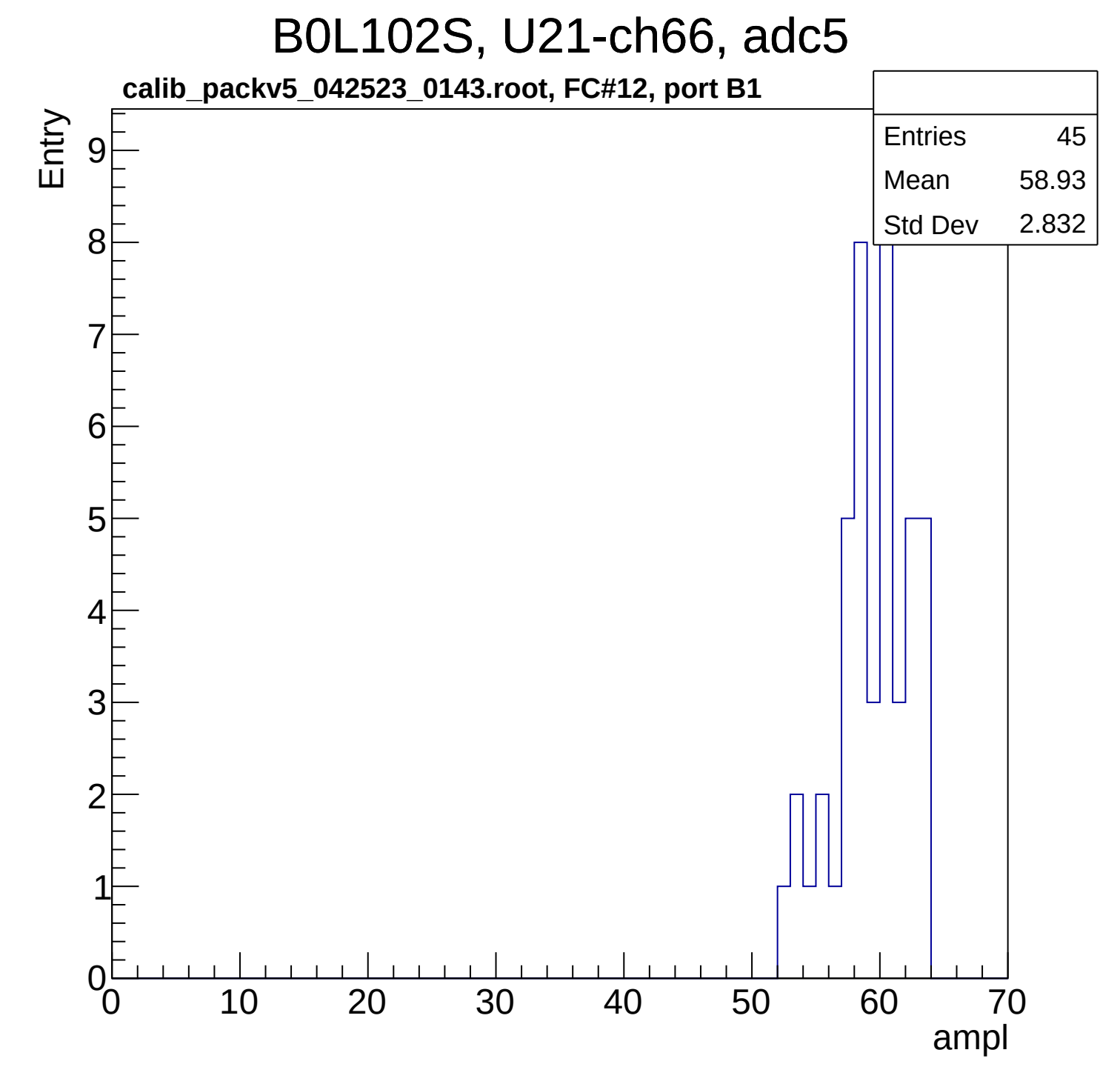
B0L102S, U21-ch66, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.93
Std Dev 2.832
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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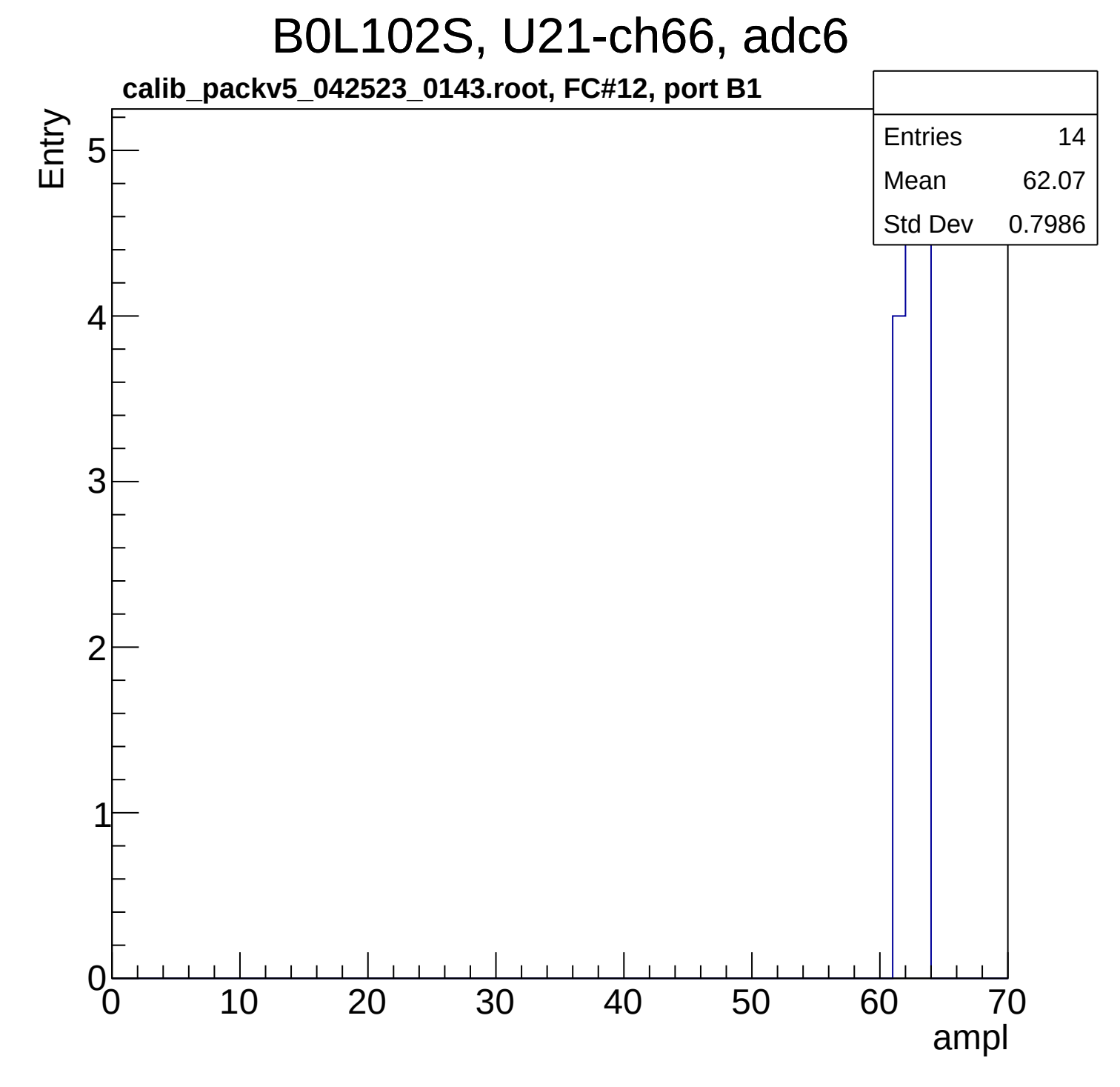
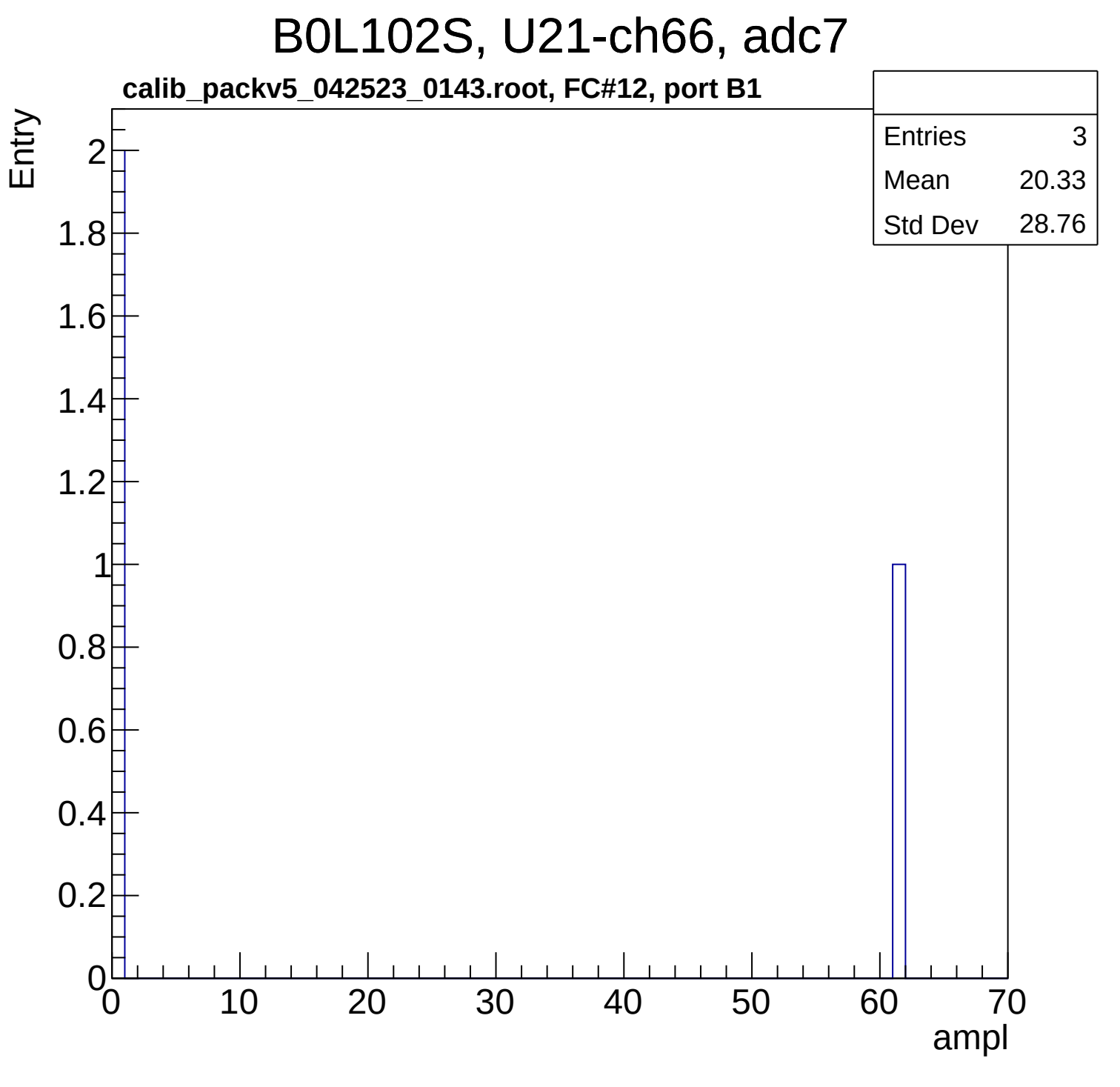

B0L102S, U21-ch66, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 62.07
Std Dev 0.7986
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U21-ch66, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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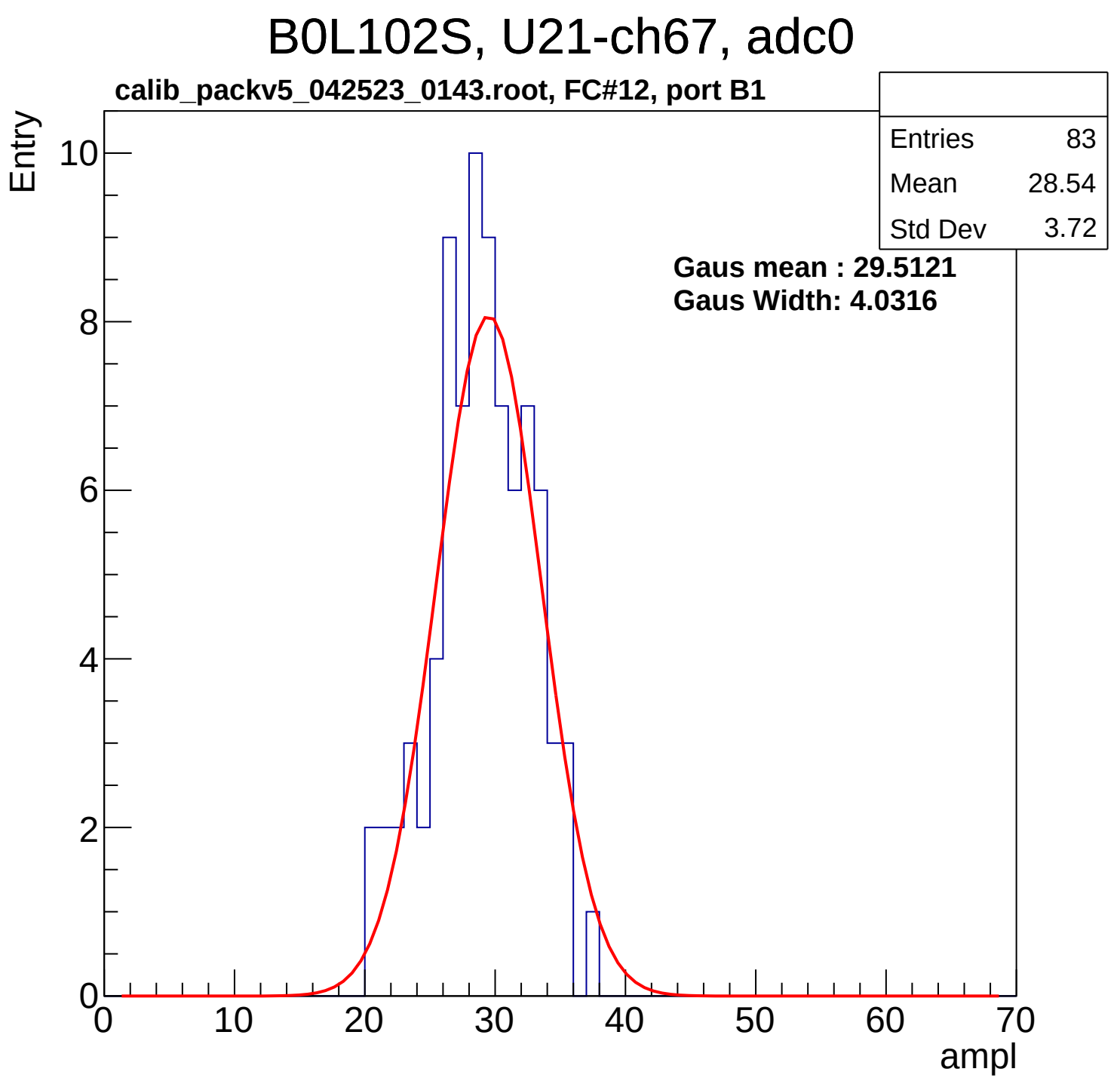
B0L102S, U21-ch67, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 28.54
Std Dev 3.72
Gaus mean : 29.5121
Gaus Width: 4.0316
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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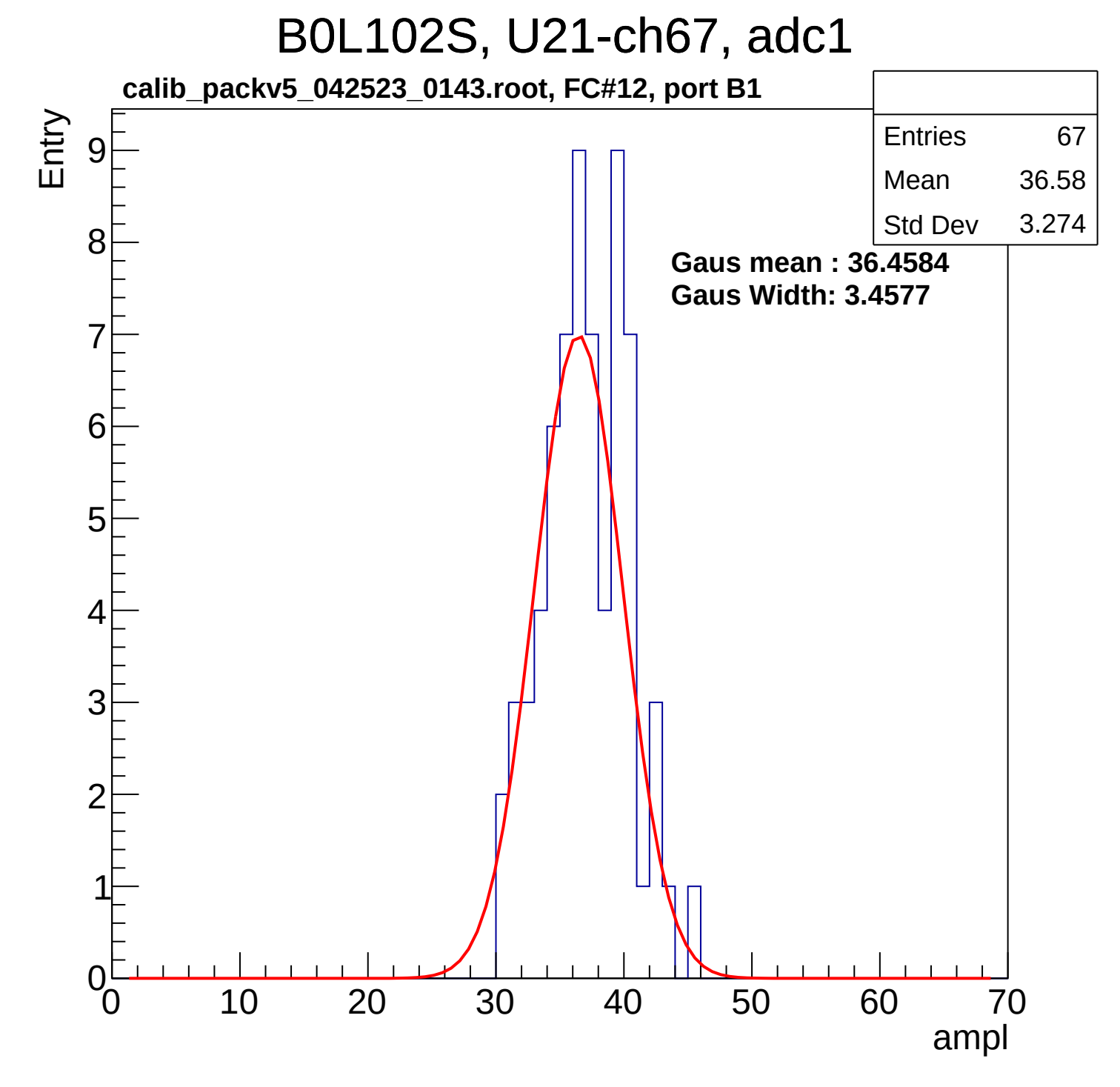
B0L102S, U21-ch67, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 36.58
Std Dev 3.274
Gaus mean : 36.4584
Gaus Width: 3.4577
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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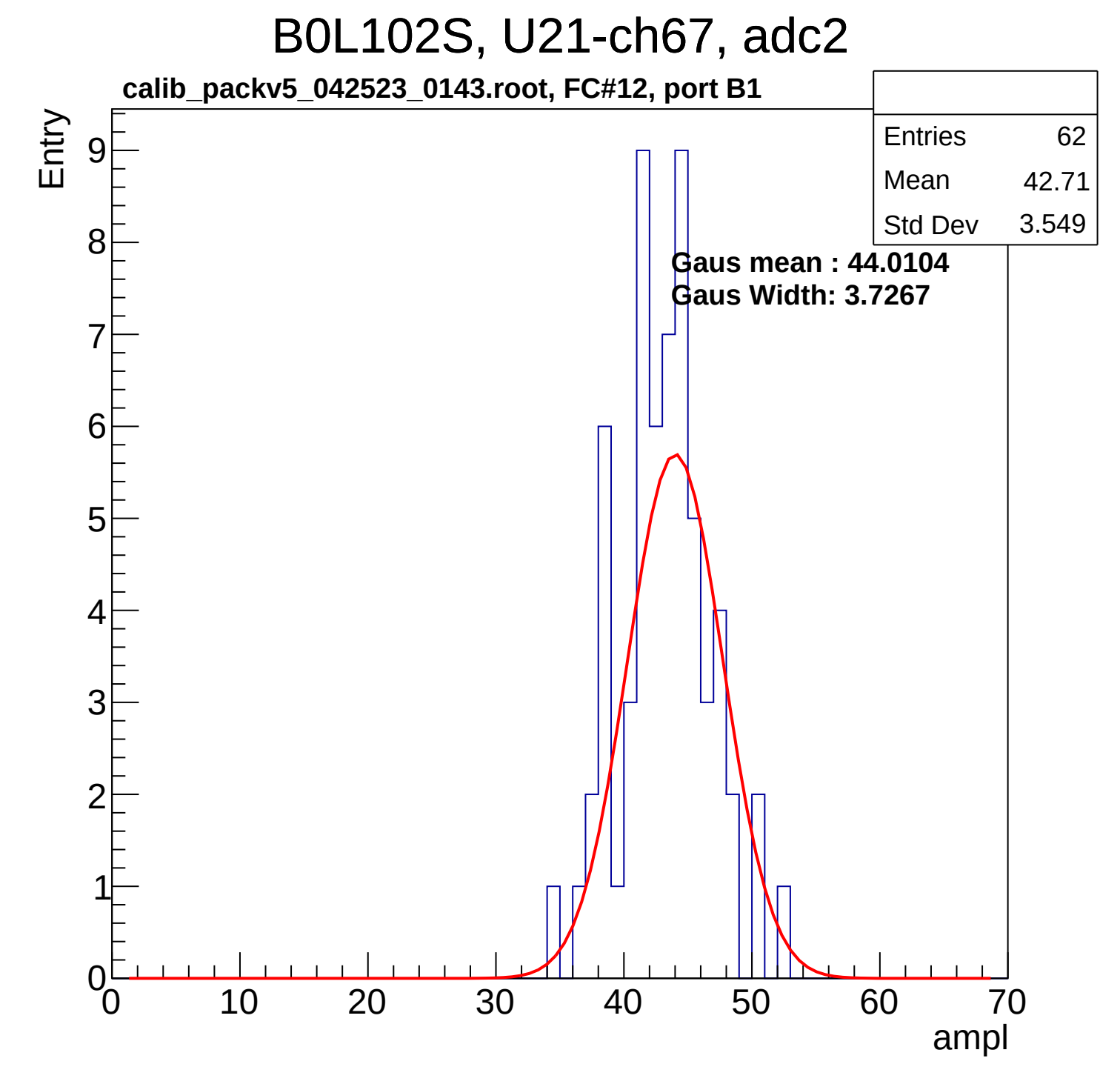
B0L102S, U21-ch67, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.71
Std Dev 3.549
Gaus mean : 44.0104
Gaus Width: 3.7267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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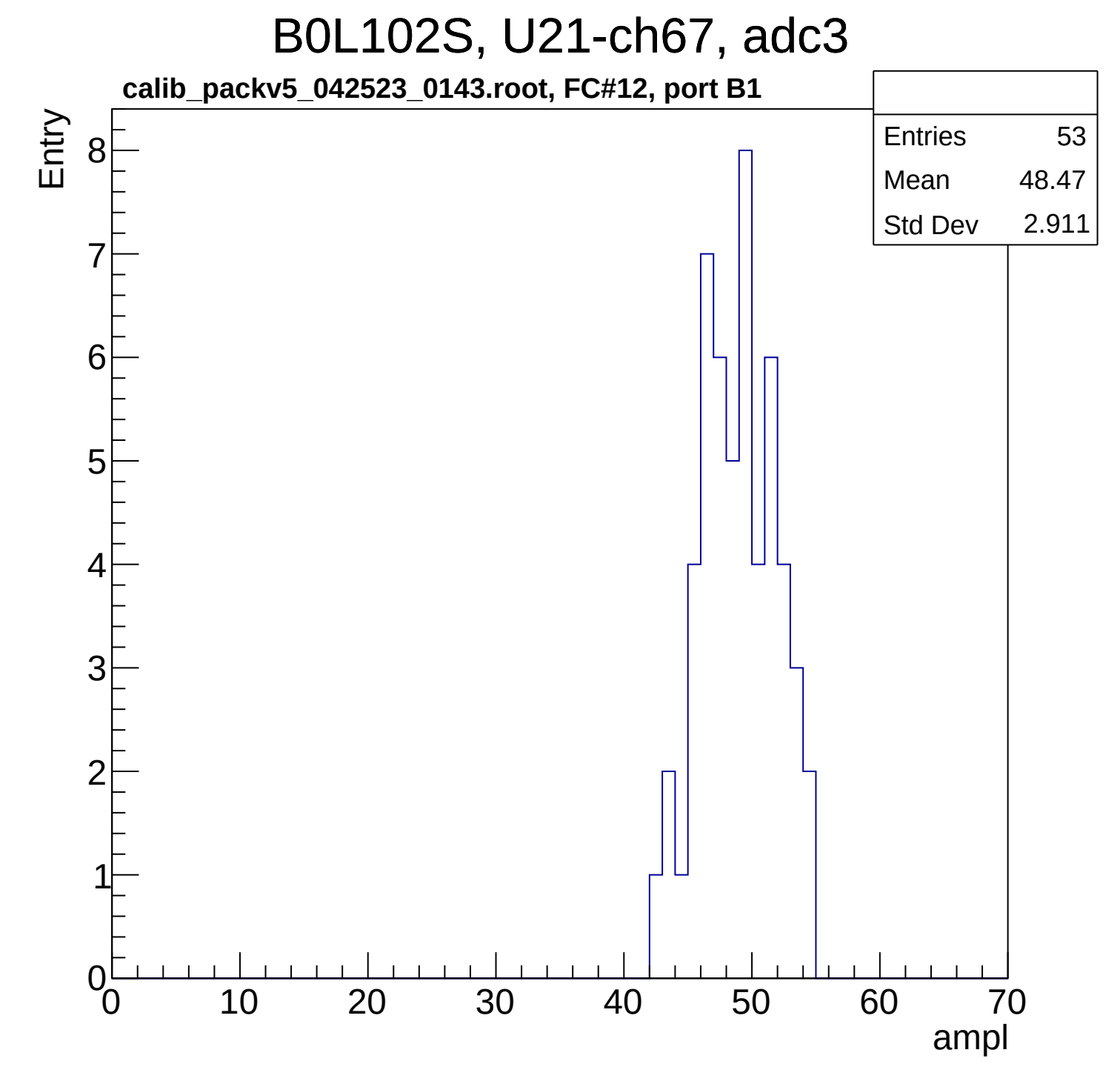
B0L102S, U21-ch67, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 48.47
Std Dev 2.911
Entry
ampl
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1
2
3
4
5
6
7
8
0
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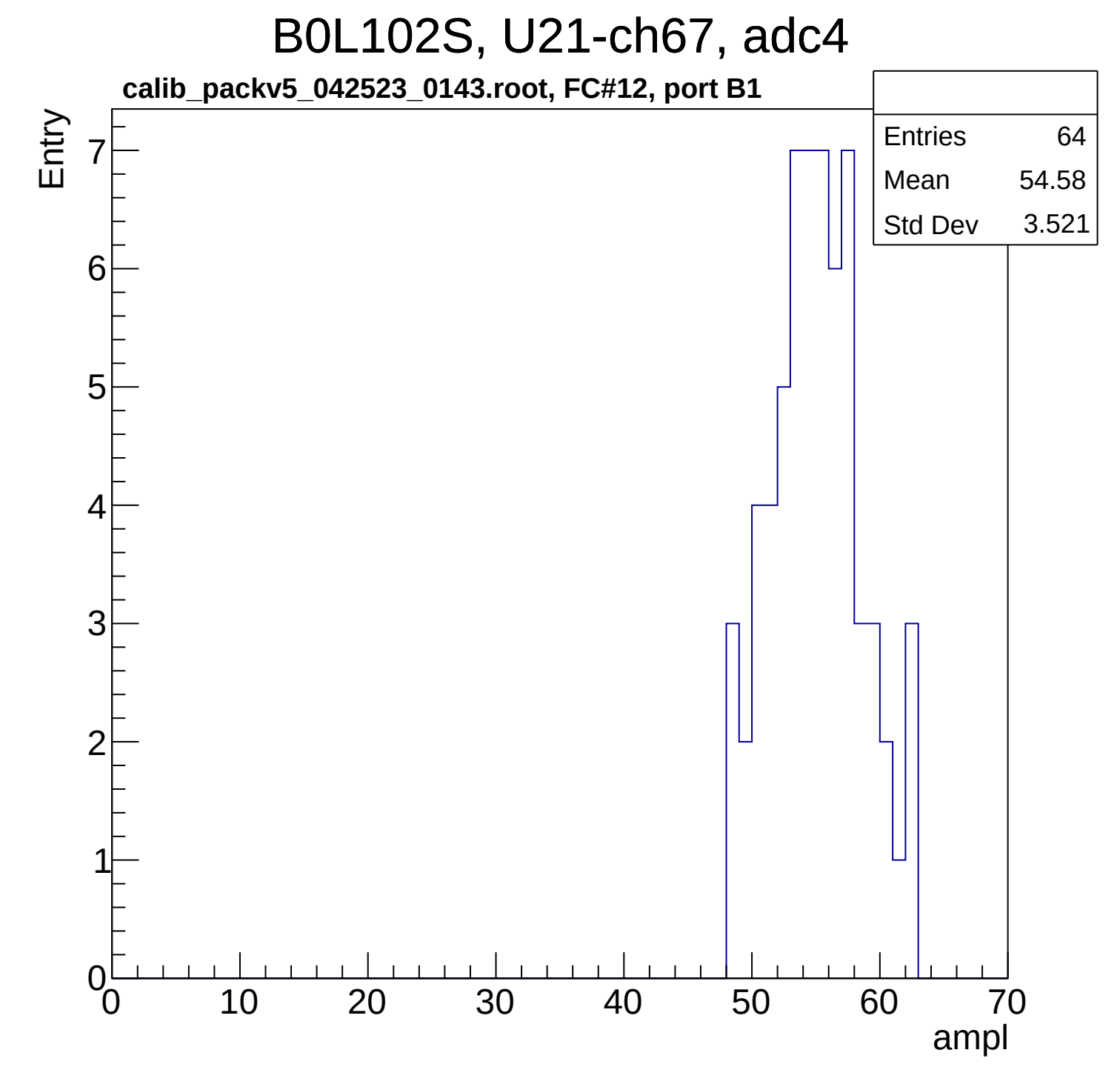
B0L102S, U21-ch67, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 54.58
Std Dev 3.521
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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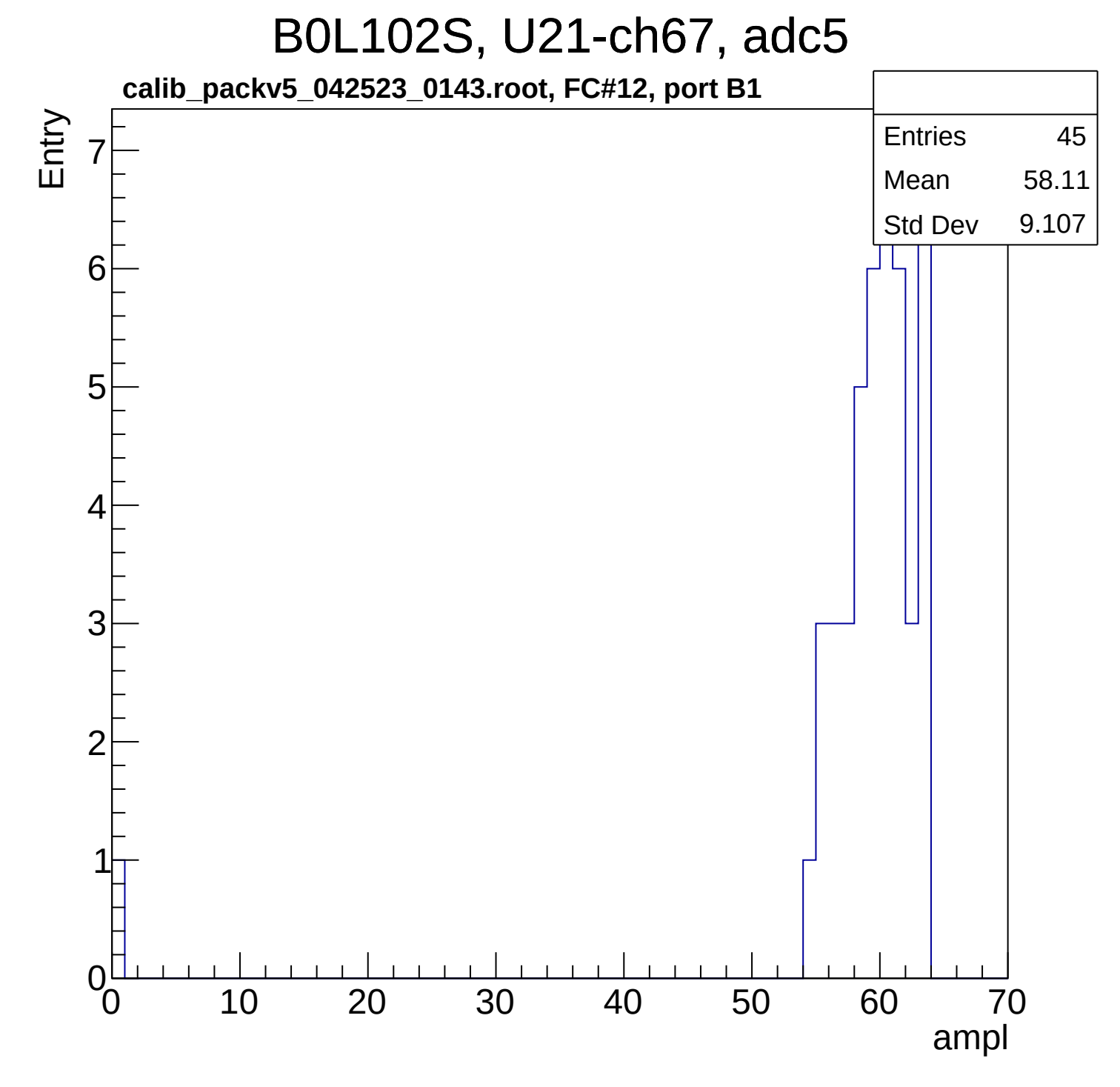
B0L102S, U21-ch67, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.11
Std Dev 9.107
Entry
ampl
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1
2
3
4
5
6
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0
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20
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60
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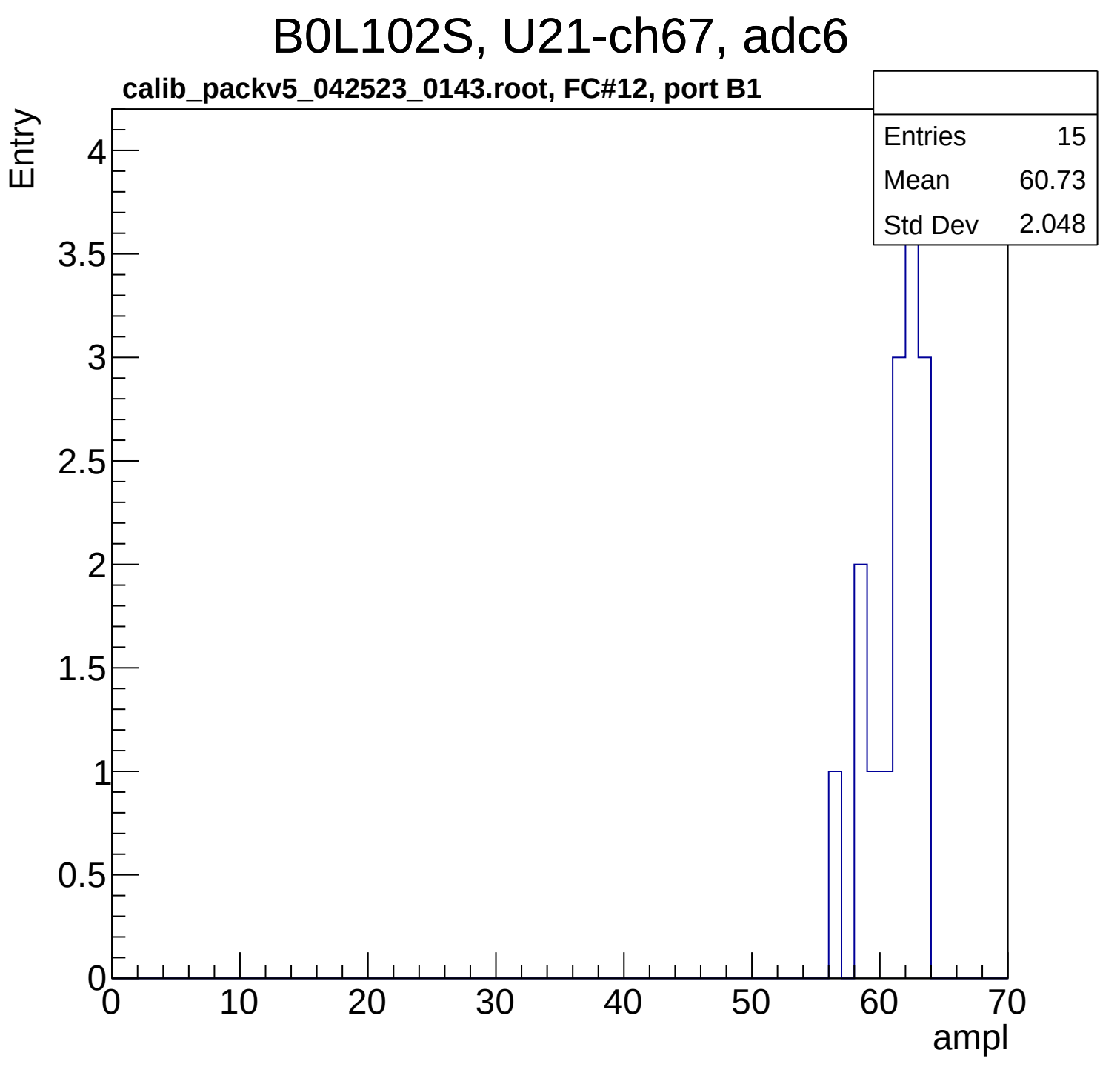
B0L102S, U21-ch67, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 60.73
Std Dev 2.048
Entry
ampl
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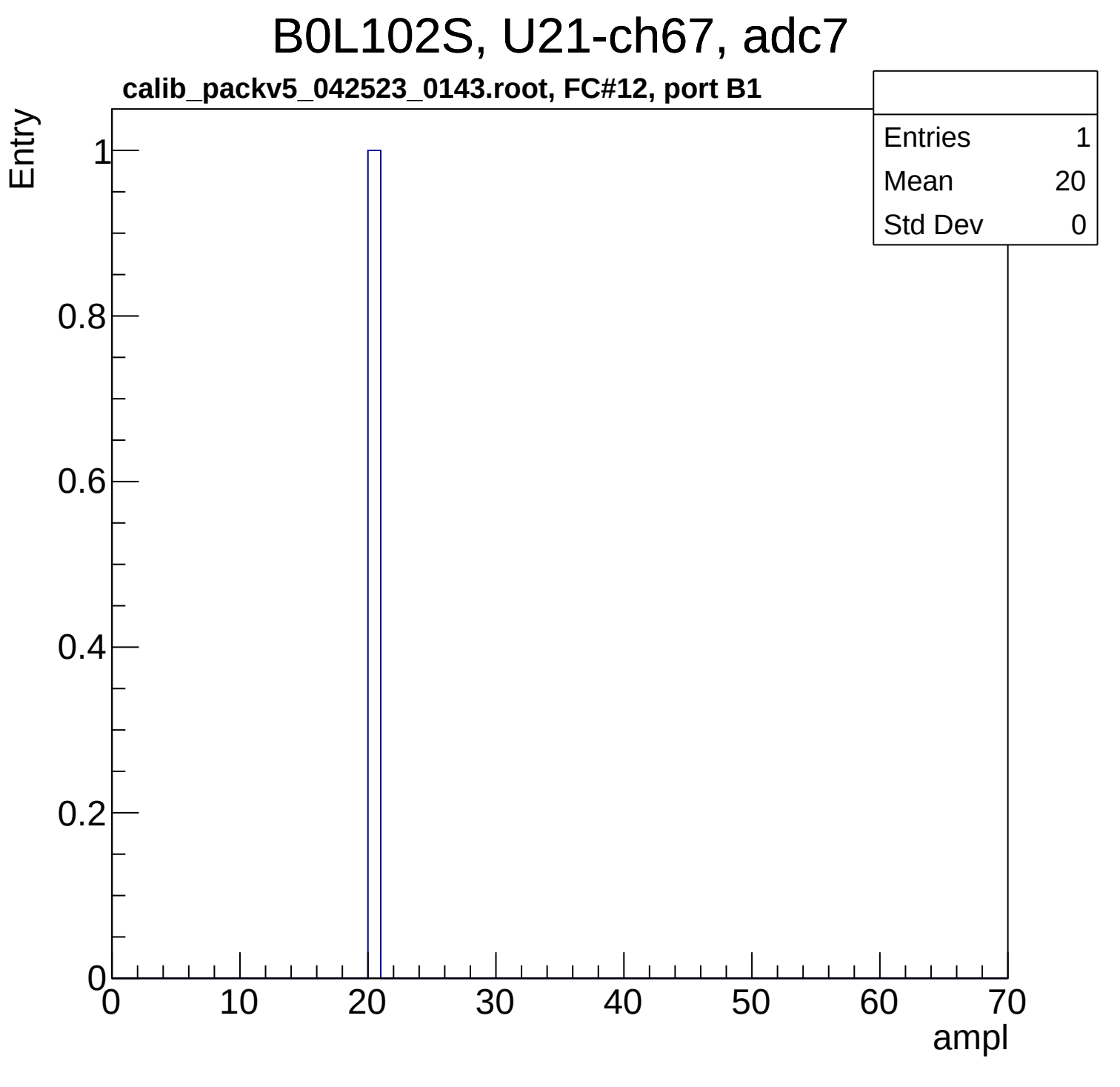
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch67, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
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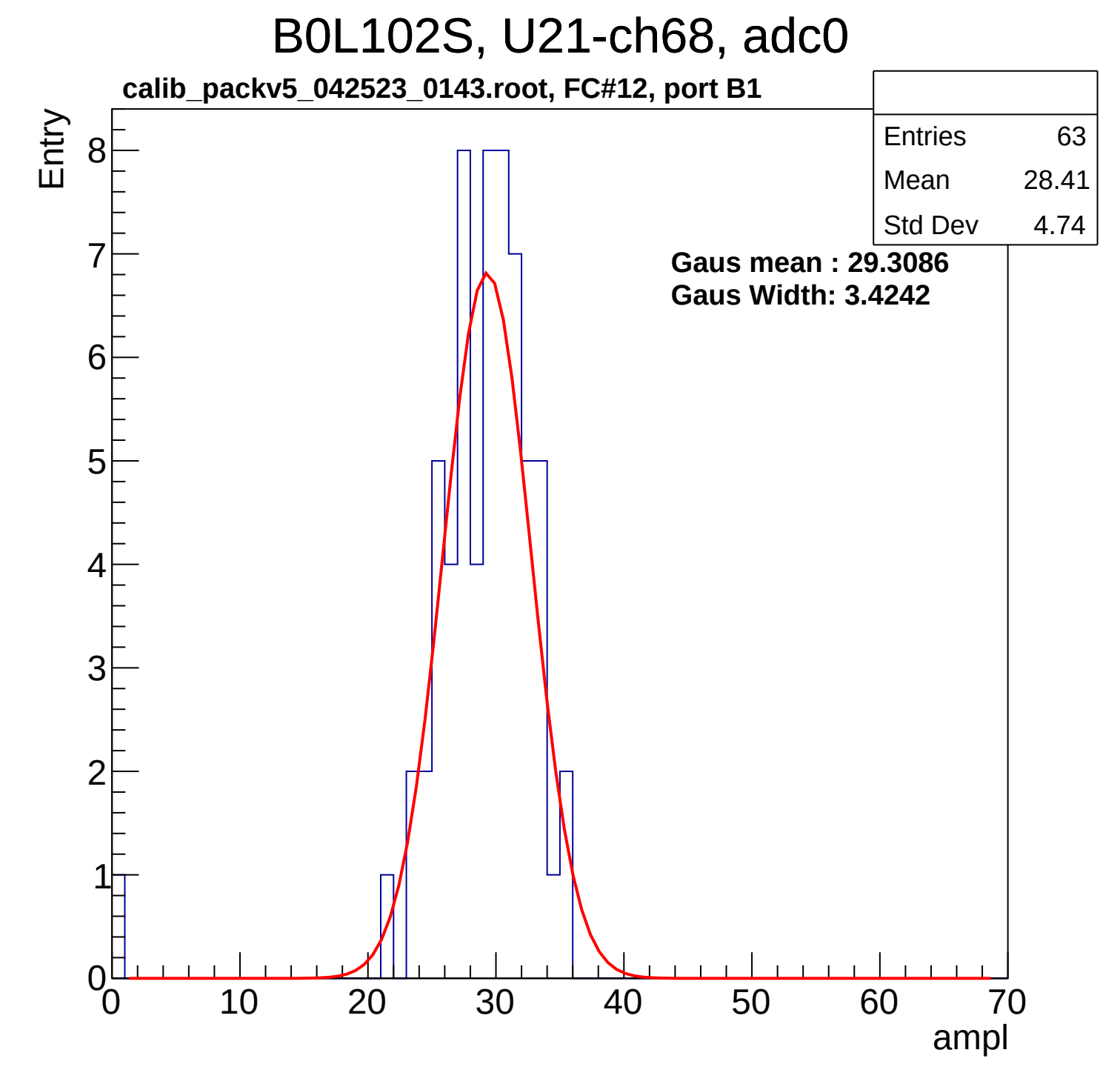
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch68, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.41
Std Dev 4.74
Gaus mean : 29.3086
Gaus Width: 3.4242
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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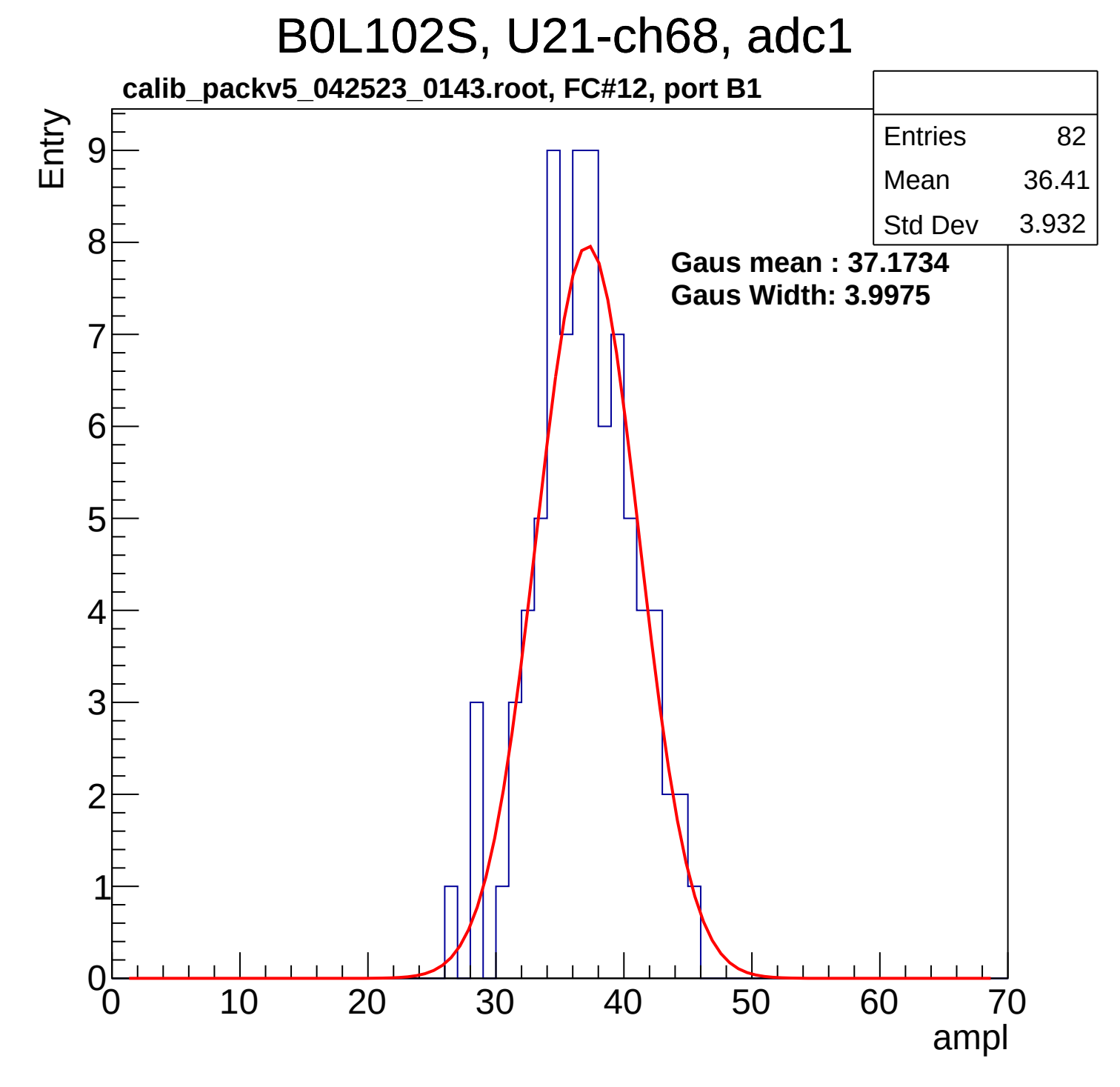
B0L102S, U21-ch68, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 36.41
Std Dev 3.932
Gaus mean : 37.1734
Gaus Width: 3.9975
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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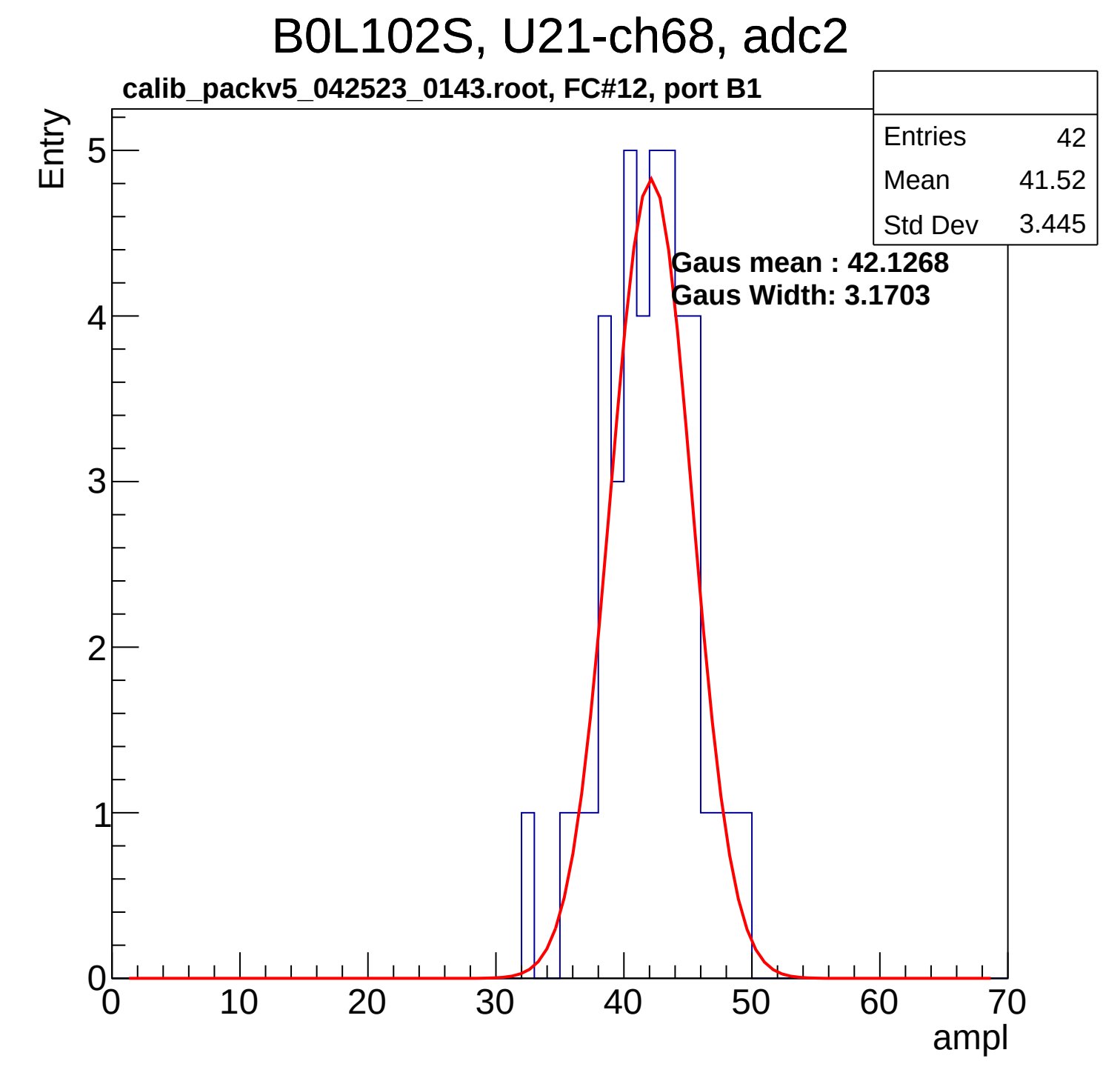
B0L102S, U21-ch68, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 41.52
Std Dev 3.445
Gaus mean : 42.1268
Gaus Width: 3.1703
Entry
ampl
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1
2
3
4
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0
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20
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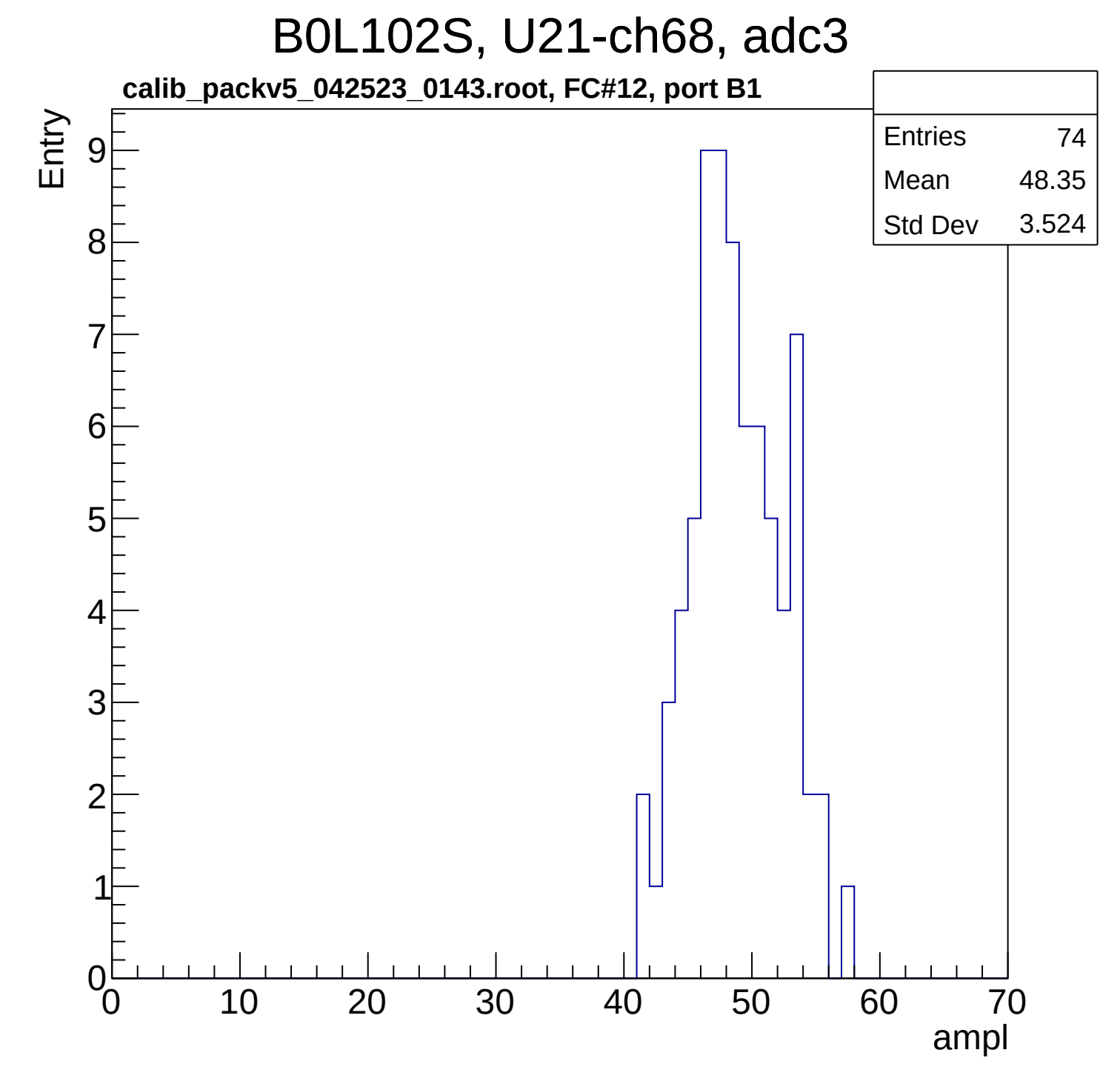
40
50
60
70


B0L102S, U21-ch68, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 48.35
Std Dev 3.524
Entry
ampl
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60
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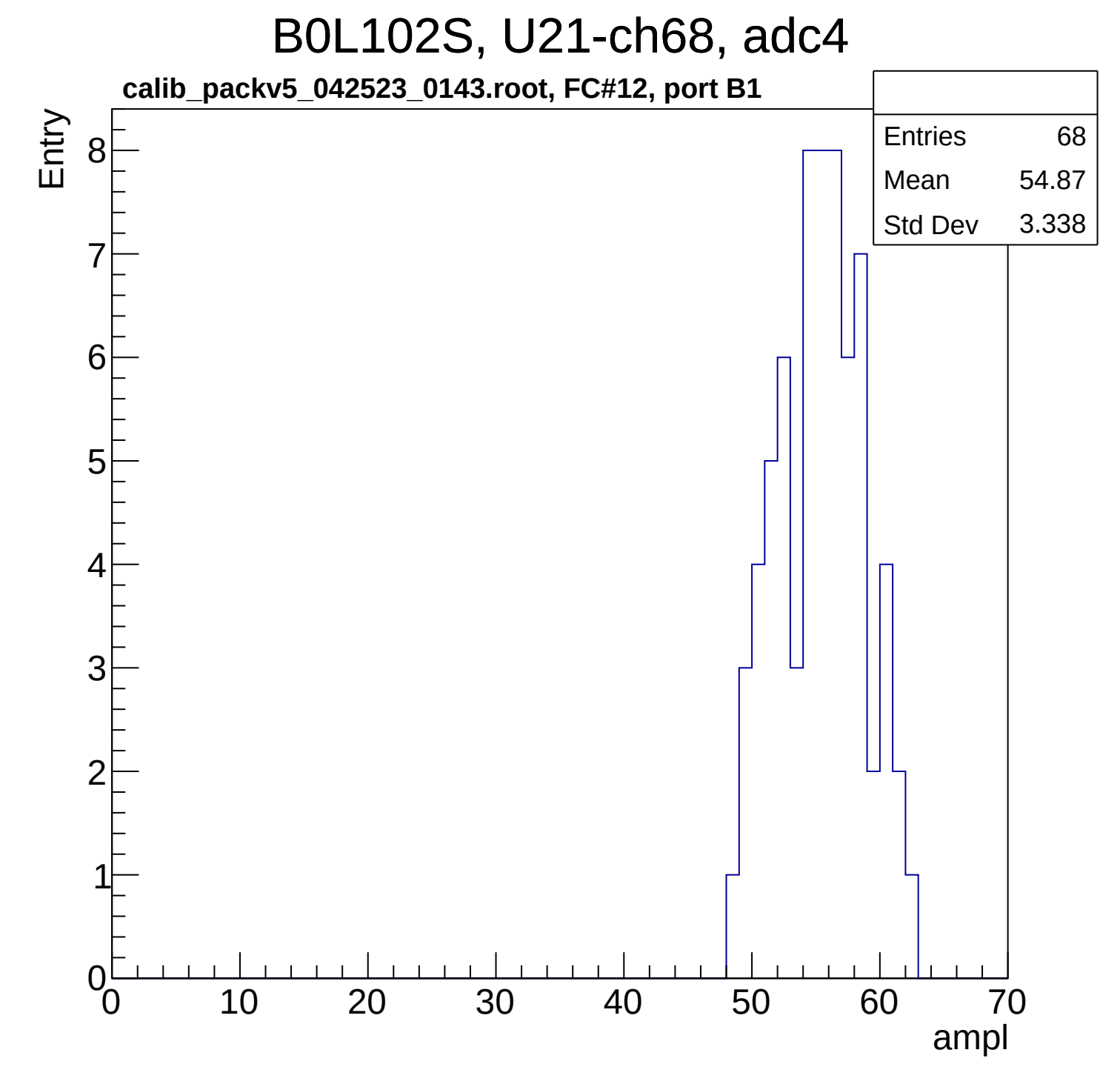
B0L102S, U21-ch68, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 54.87
Std Dev 3.338
Entry
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5
6
7
8
0
10
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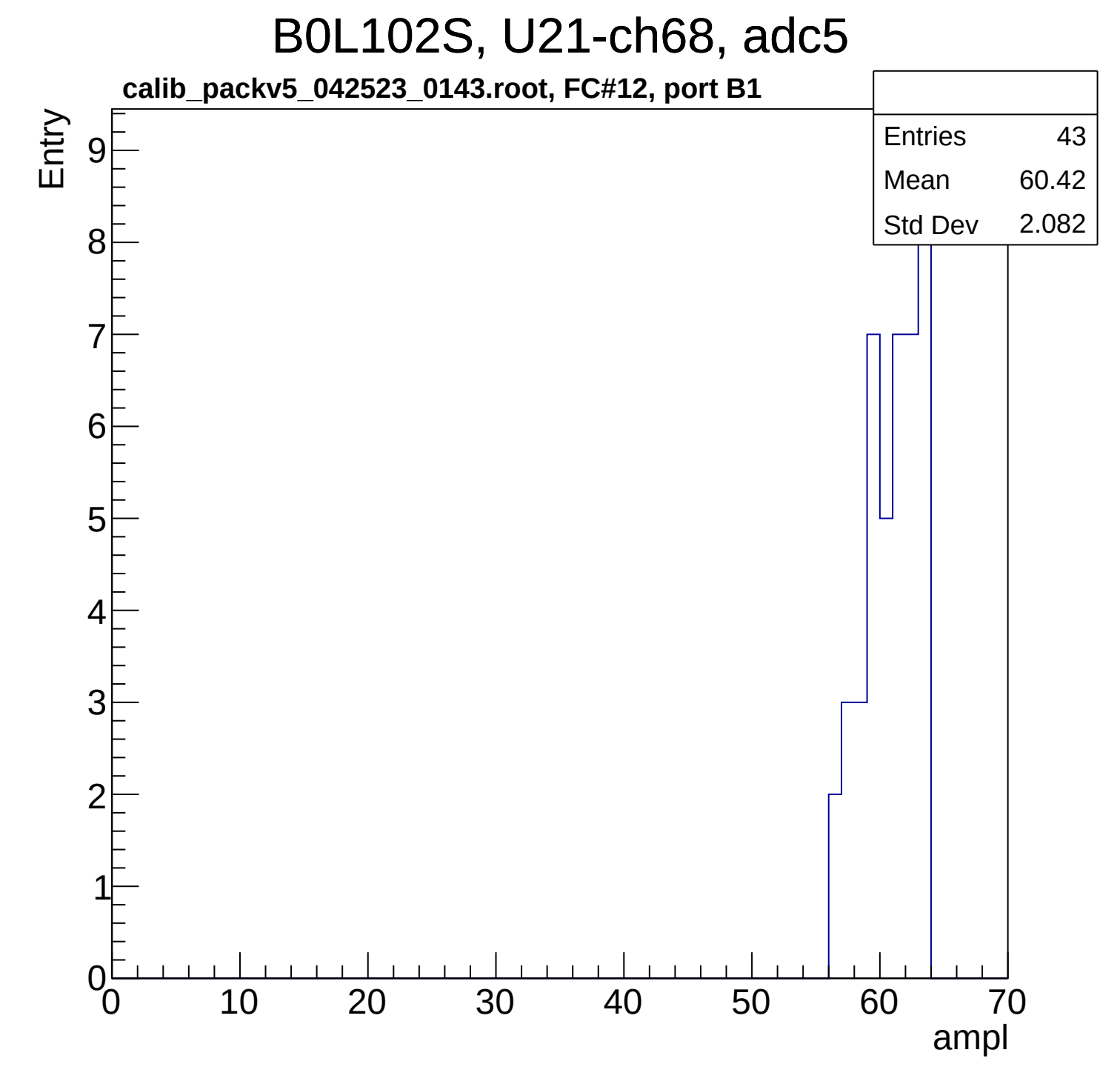
70
ampl


B0L102S, U21-ch68, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 60.42
Std Dev 2.082
Entry
ampl
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2
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4
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6
7
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0
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60
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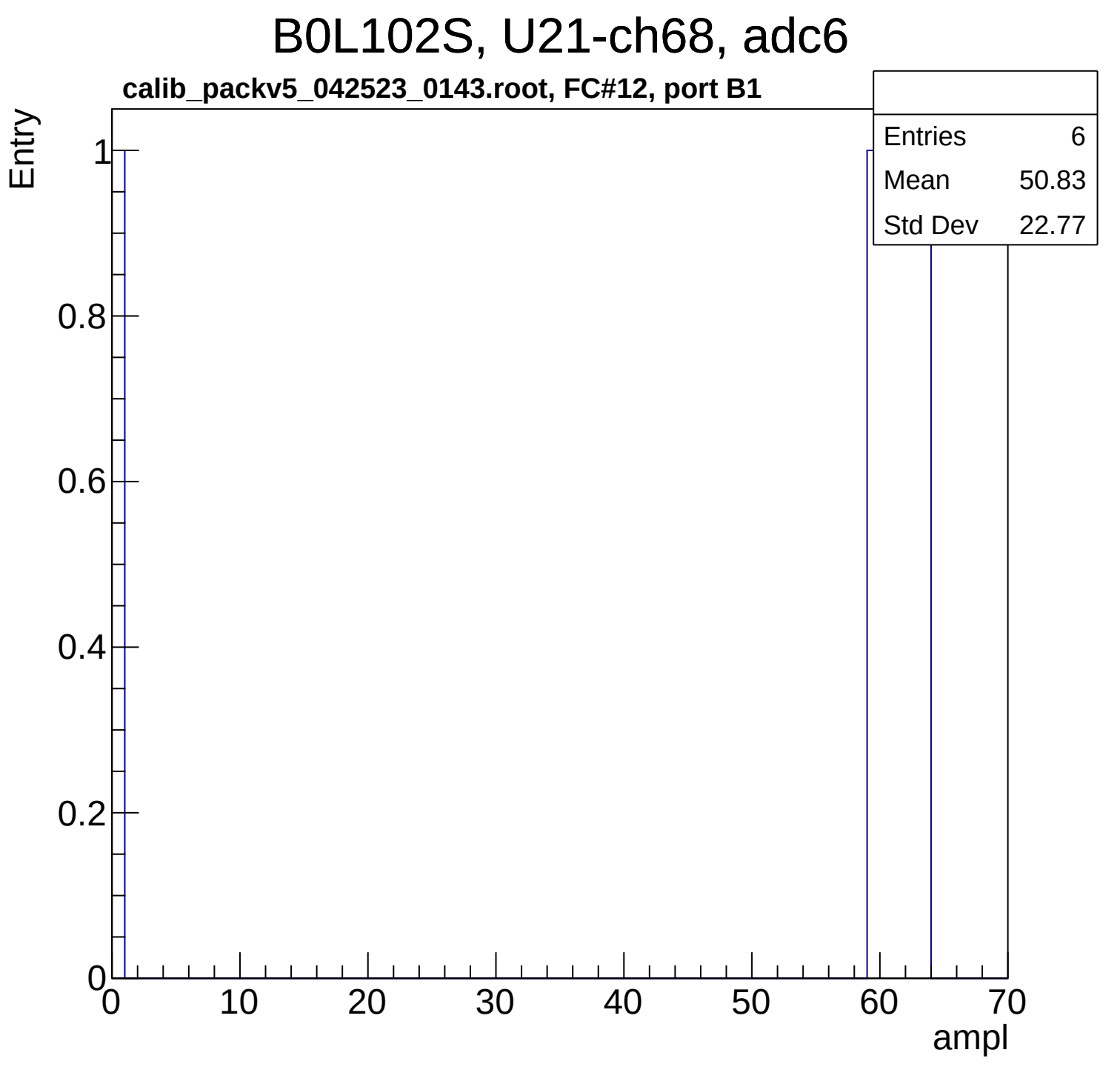
B0L102S, U21-ch68, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 50.83
Std Dev 22.77
Entry
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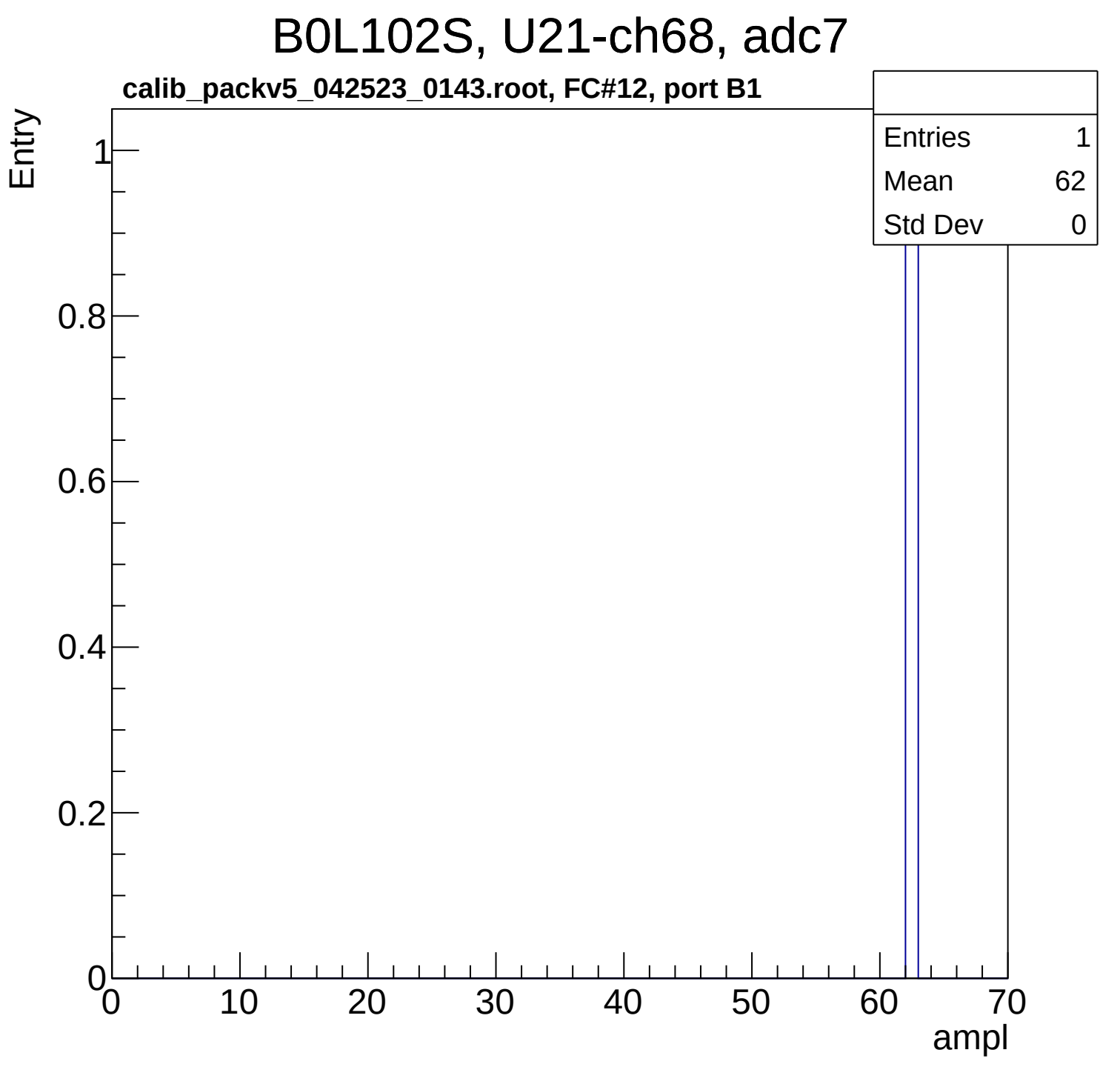
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch68, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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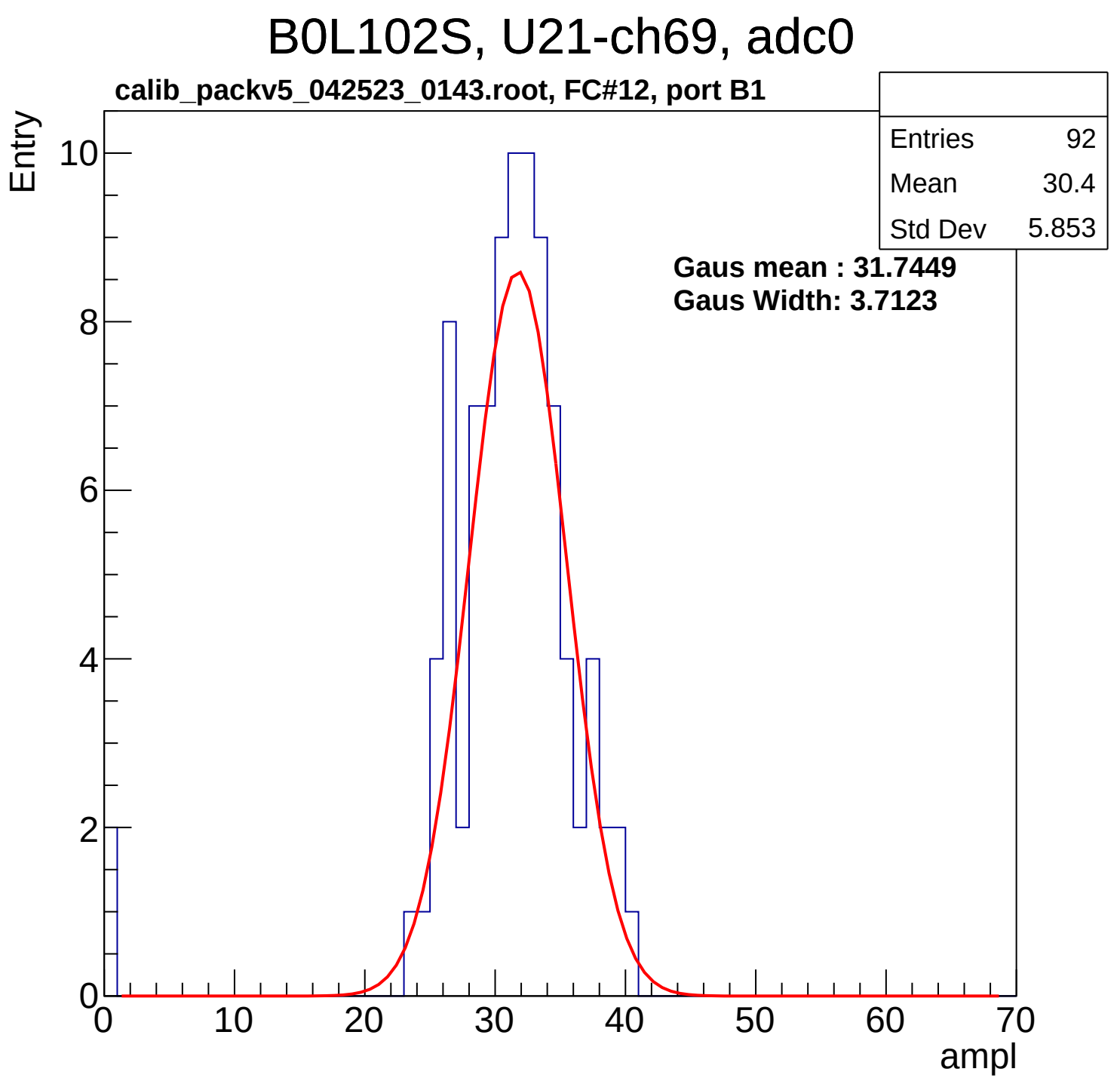
50
60
70
ampl


B0L102S, U21-ch69, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 92
Mean 30.4
Std Dev 5.853
Gaus mean : 31.7449
Gaus Width: 3.7123
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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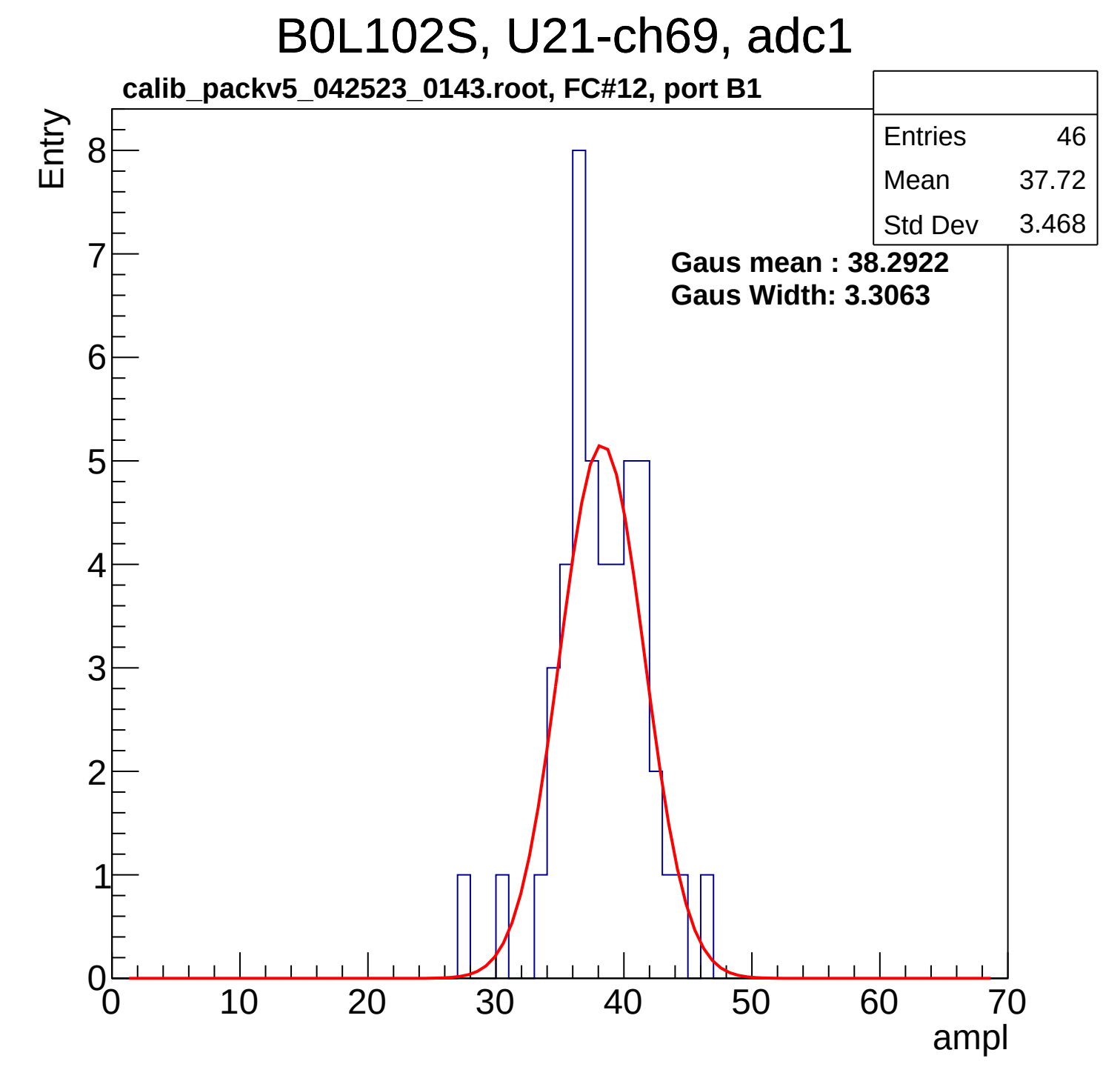
B0L102S, U21-ch69, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 37.72
Std Dev 3.468
Gaus mean : 38.2922
Gaus Width: 3.3063
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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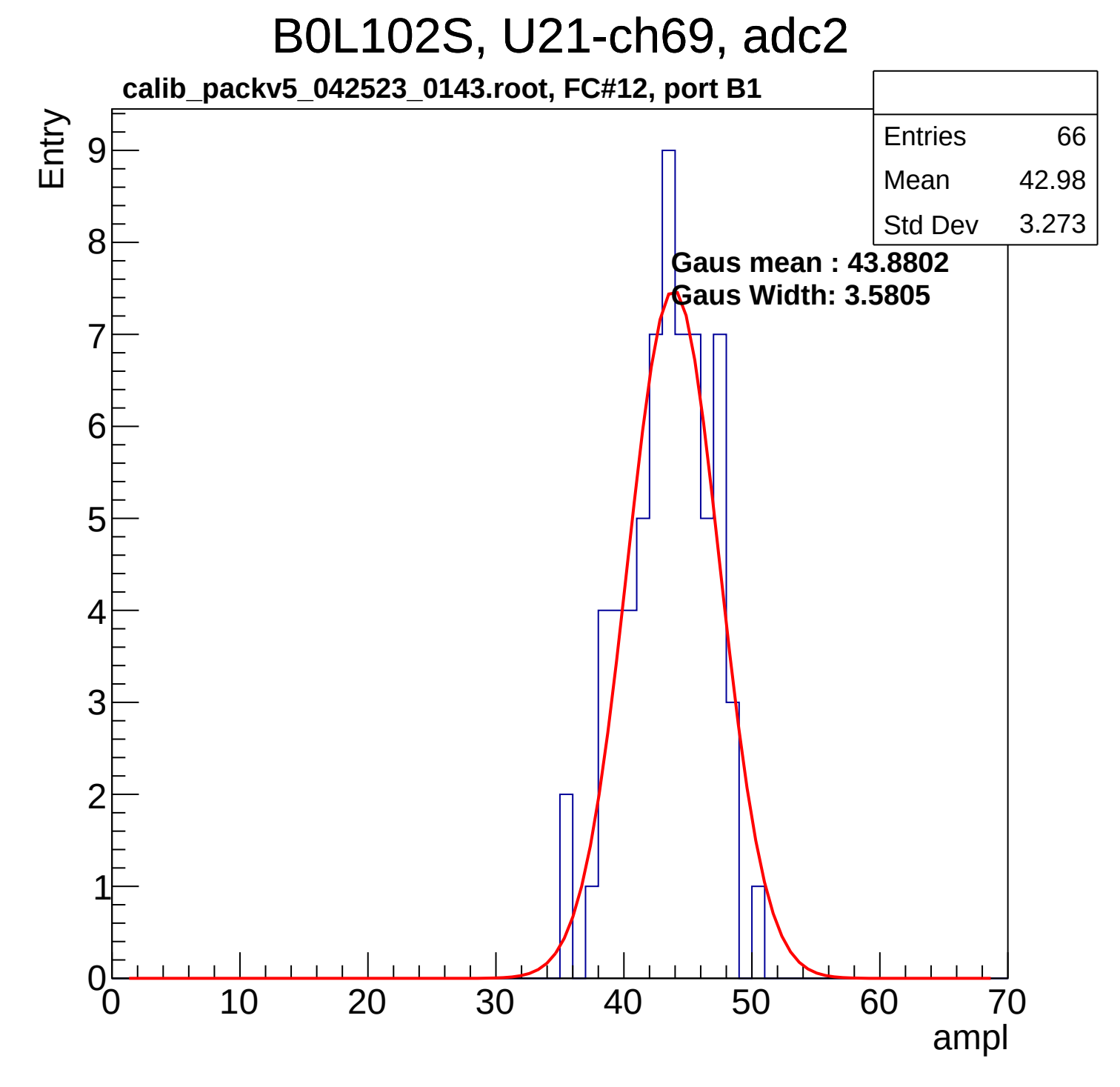
B0L102S, U21-ch69, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 42.98
Std Dev 3.273
Gaus mean : 43.8802
Gaus Width: 3.5805
Entry
ampl
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3
4
5
6
7
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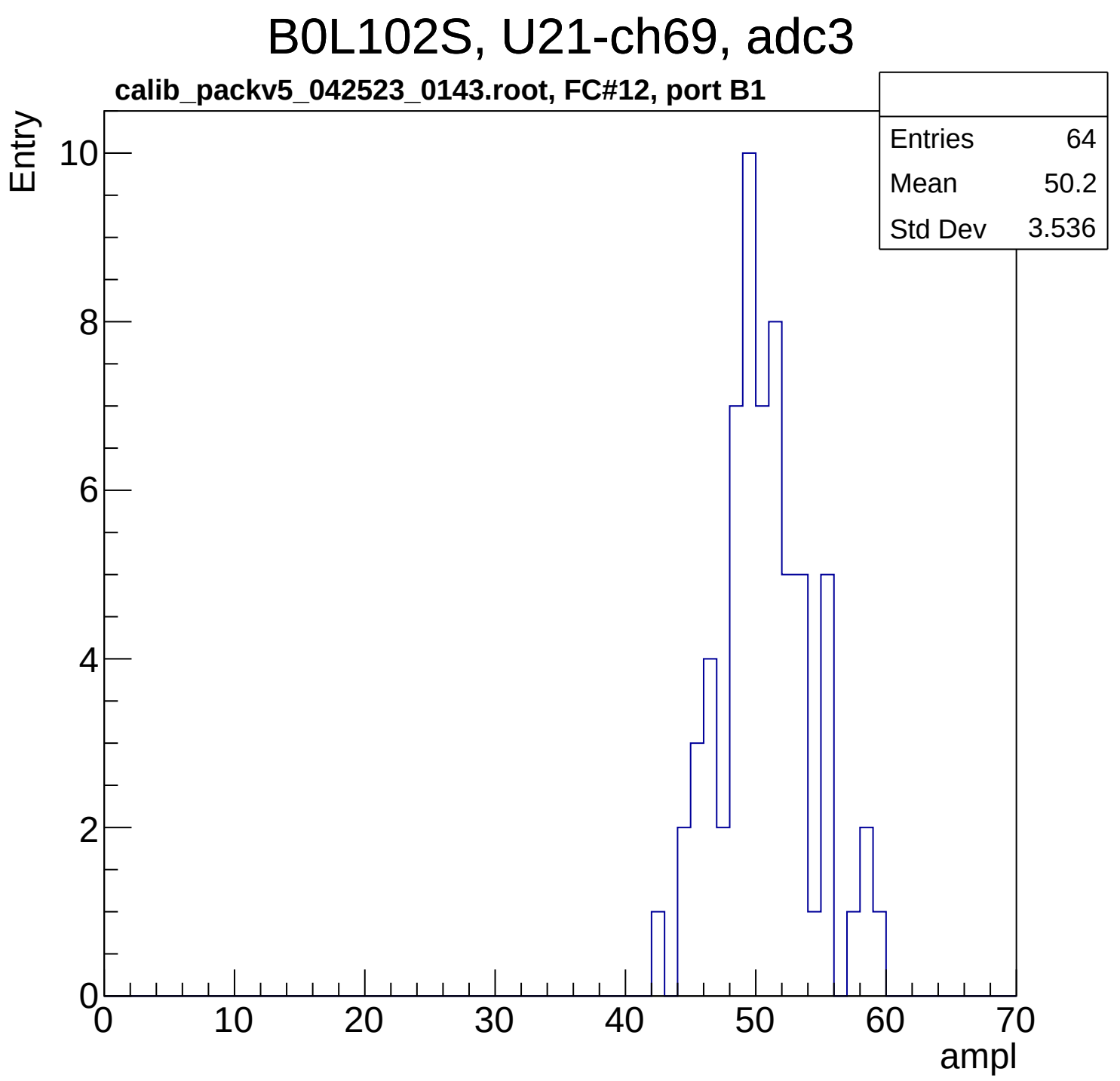
B0L102S, U21-ch69, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 50.2
Std Dev 3.536
Entry
ampl
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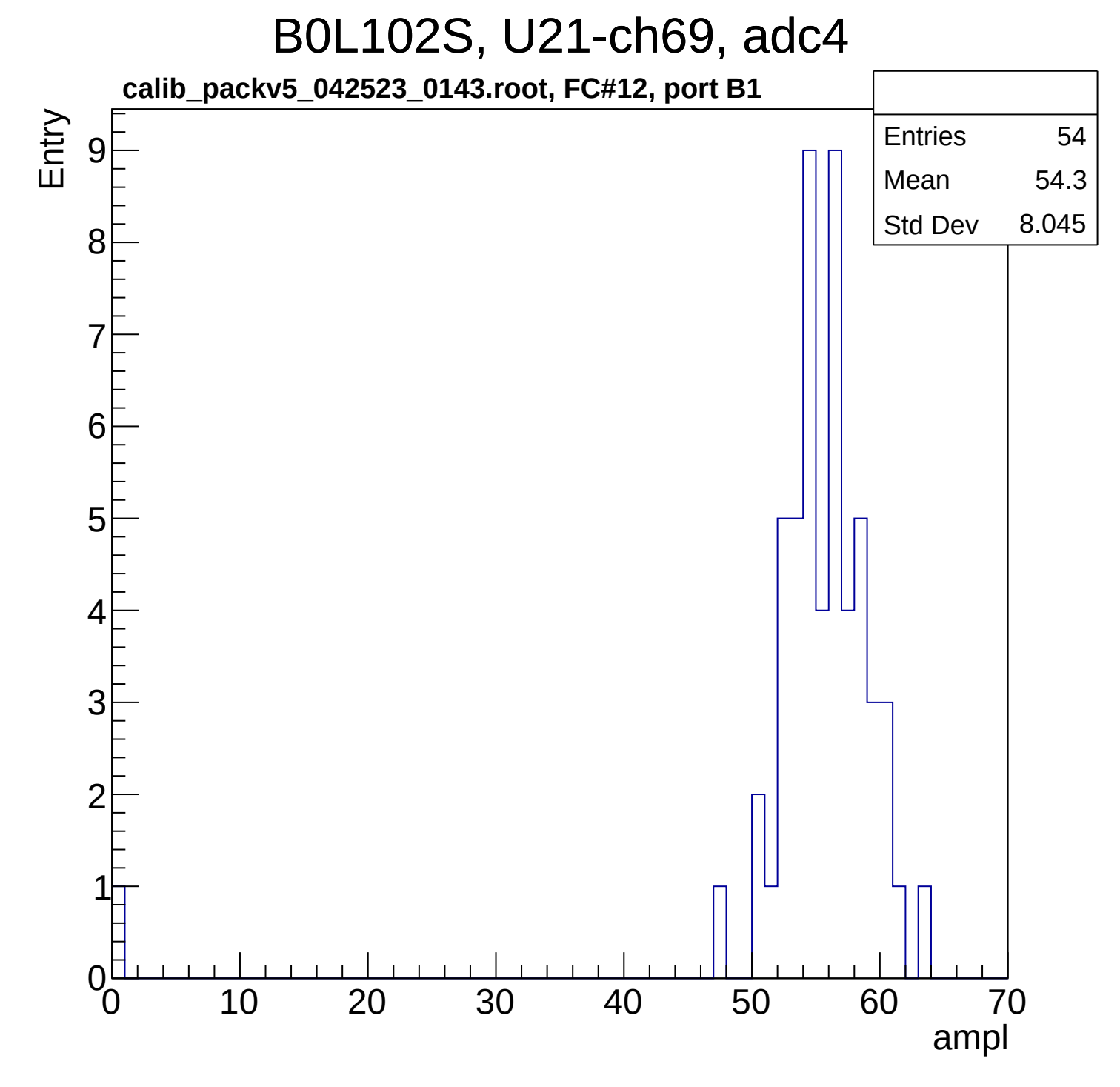
B0L102S, U21-ch69, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.3
Std Dev 8.045
Entry
ampl
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2
3
4
5
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0
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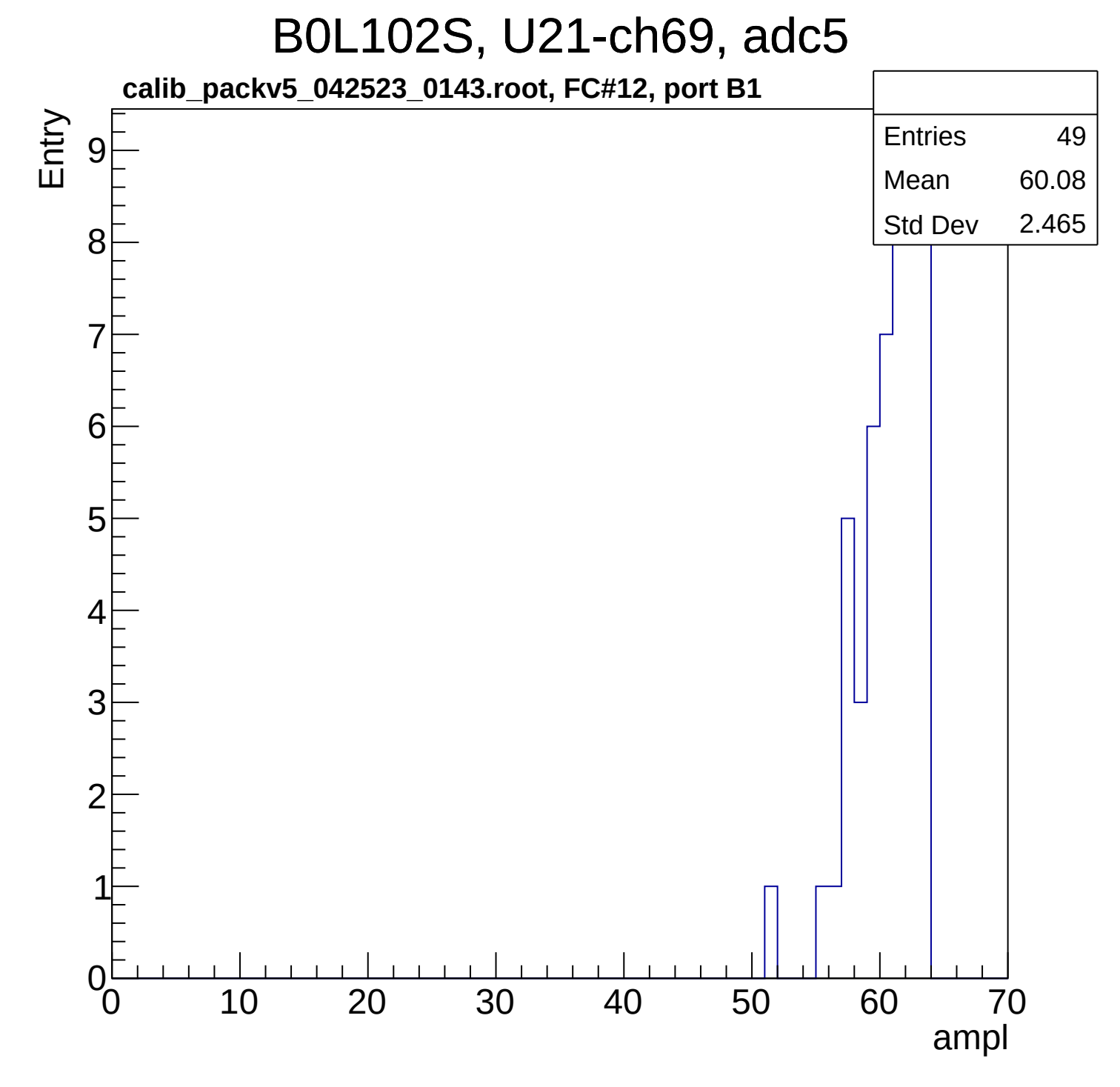
B0L102S, U21-ch69, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 60.08
Std Dev 2.465
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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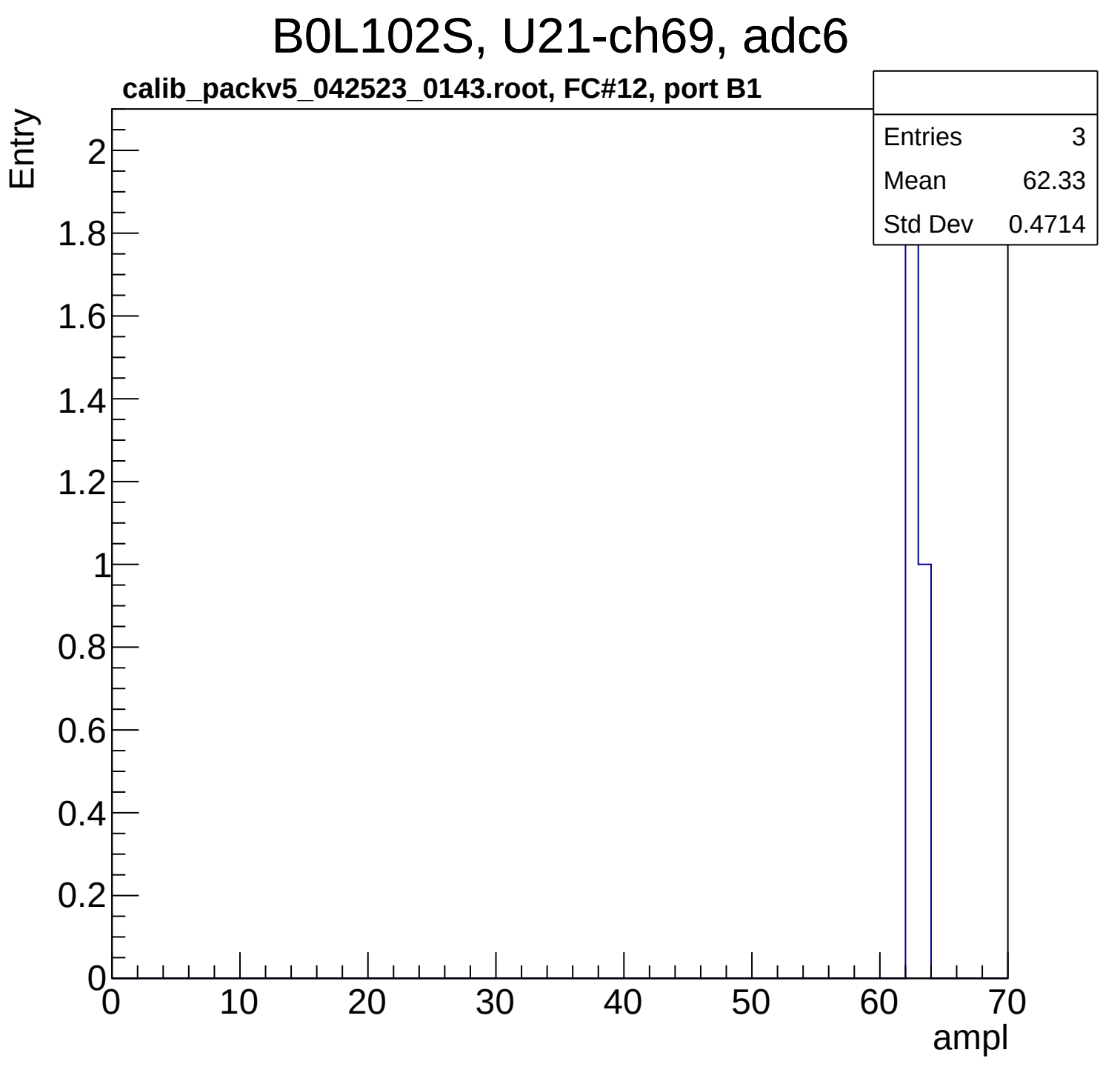
B0L102S, U21-ch69, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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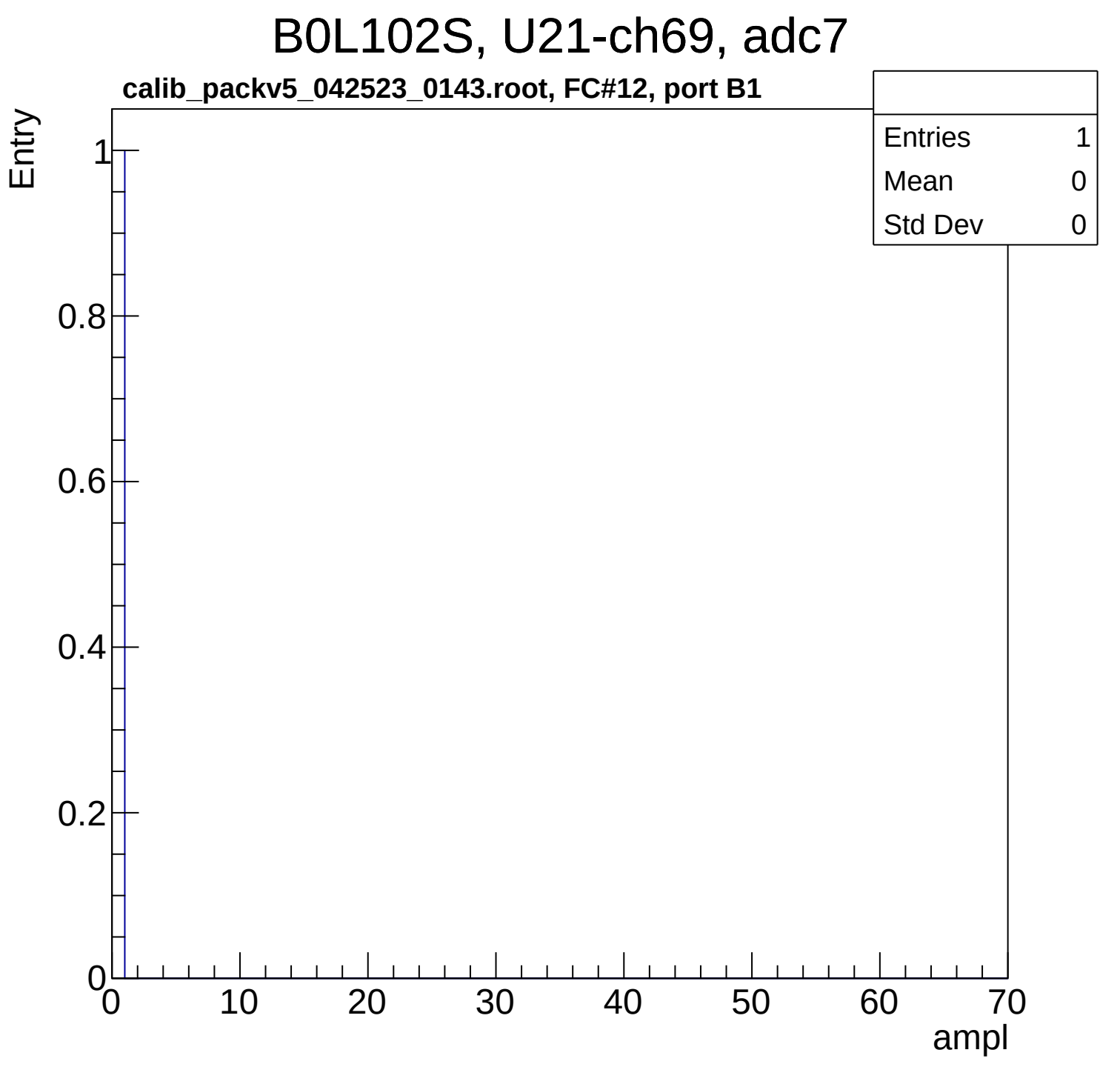
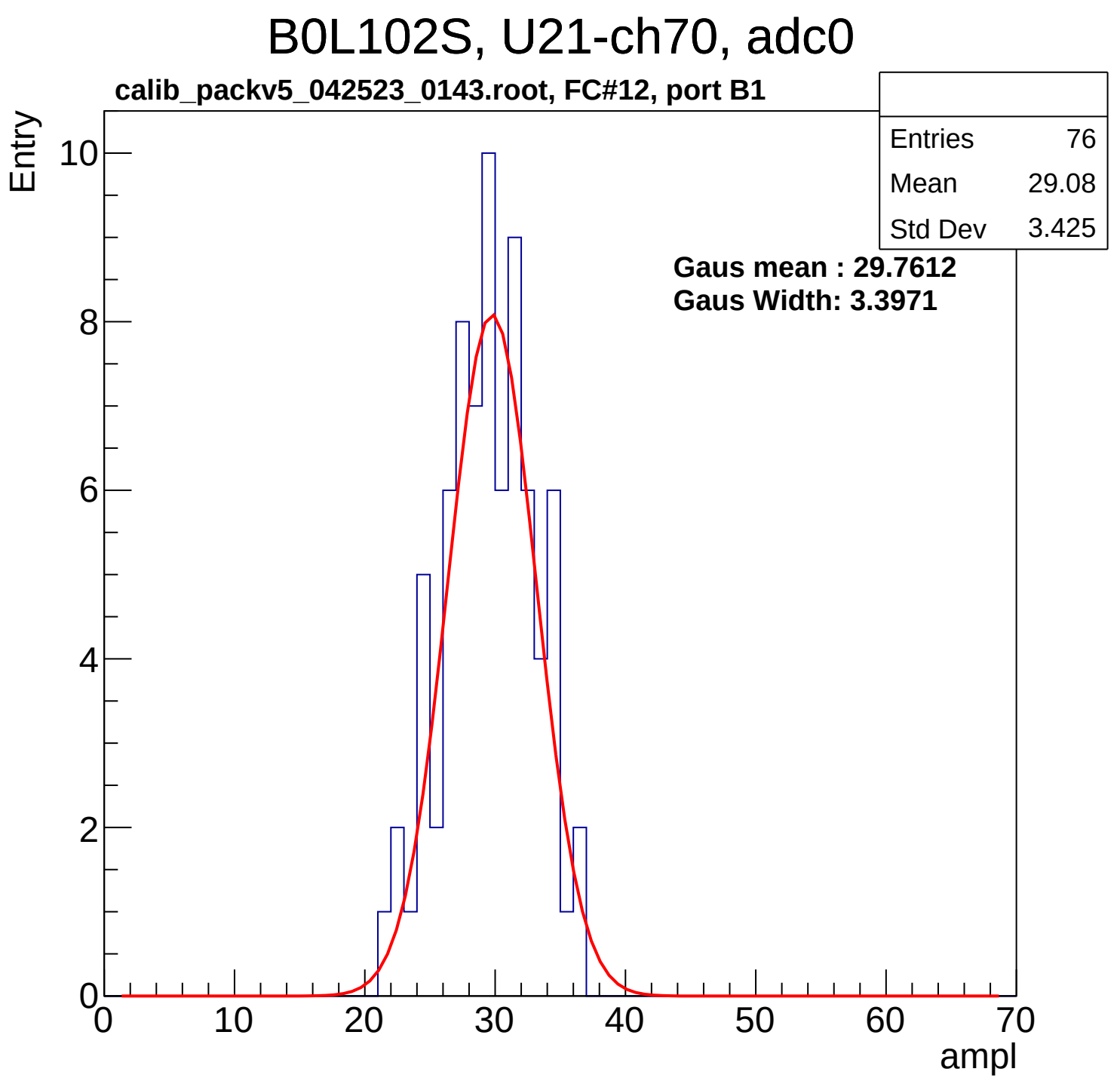

B0L102S, U21-ch69, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch70, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.08
Std Dev 3.425
Gaus mean : 29.7612
Gaus Width: 3.3971
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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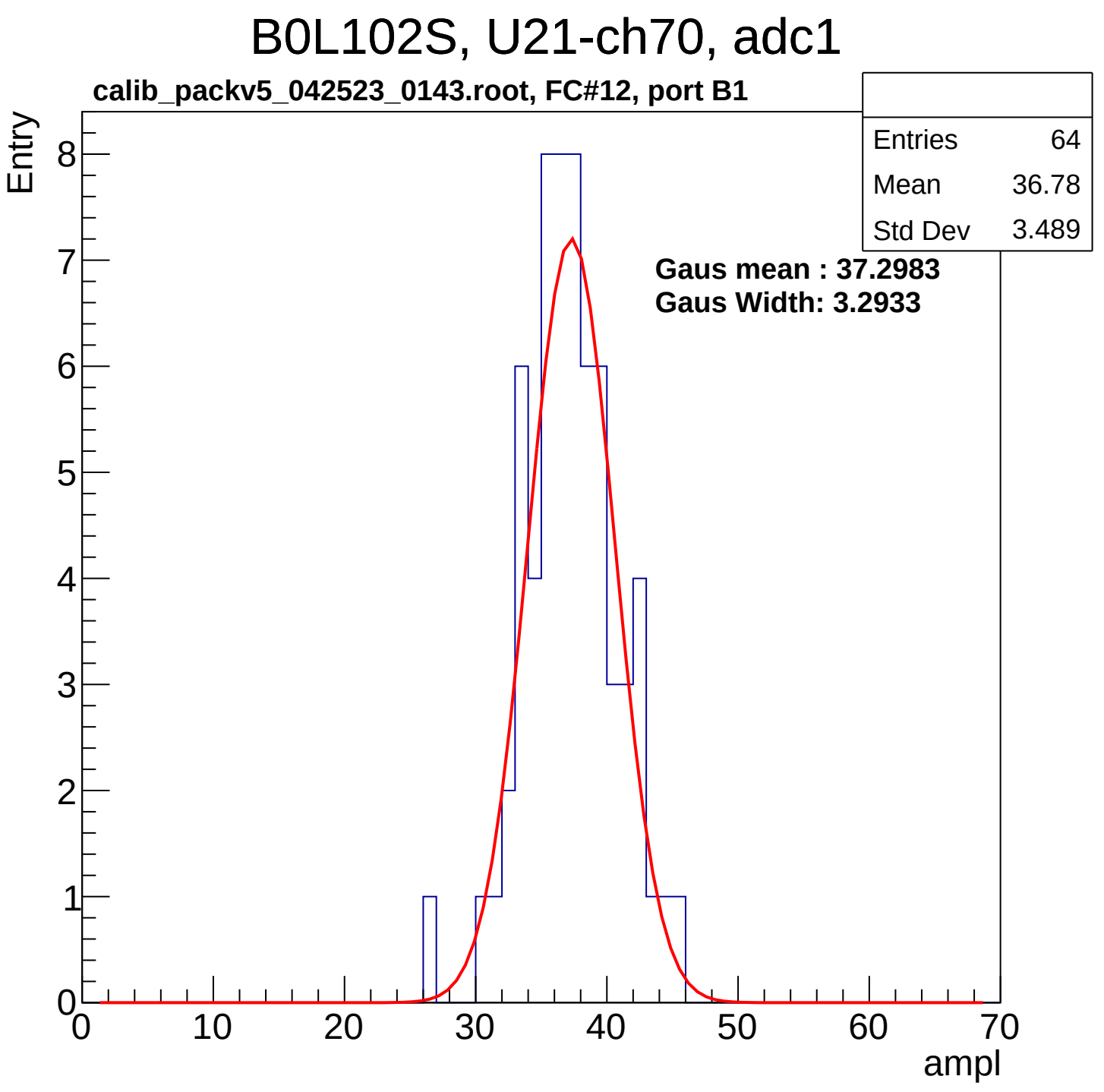
B0L102S, U21-ch70, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.78
Std Dev 3.489
Gaus mean : 37.2983
Gaus Width: 3.2933
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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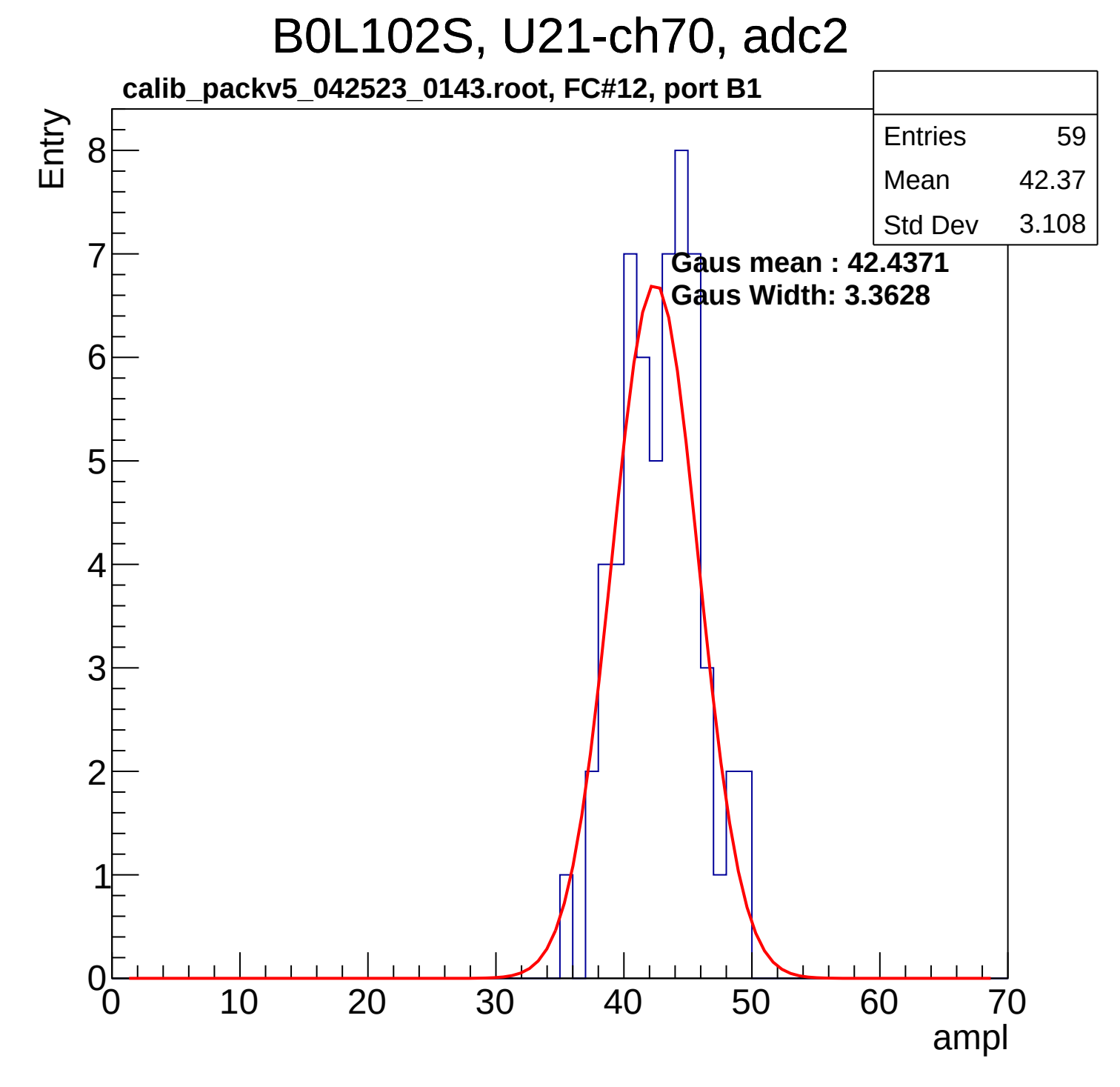
B0L102S, U21-ch70, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 42.37
Std Dev 3.108
Gaus mean : 42.4371
Gaus Width: 3.3628
Entry
ampl
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1
2
3
4
5
6
7
8
0
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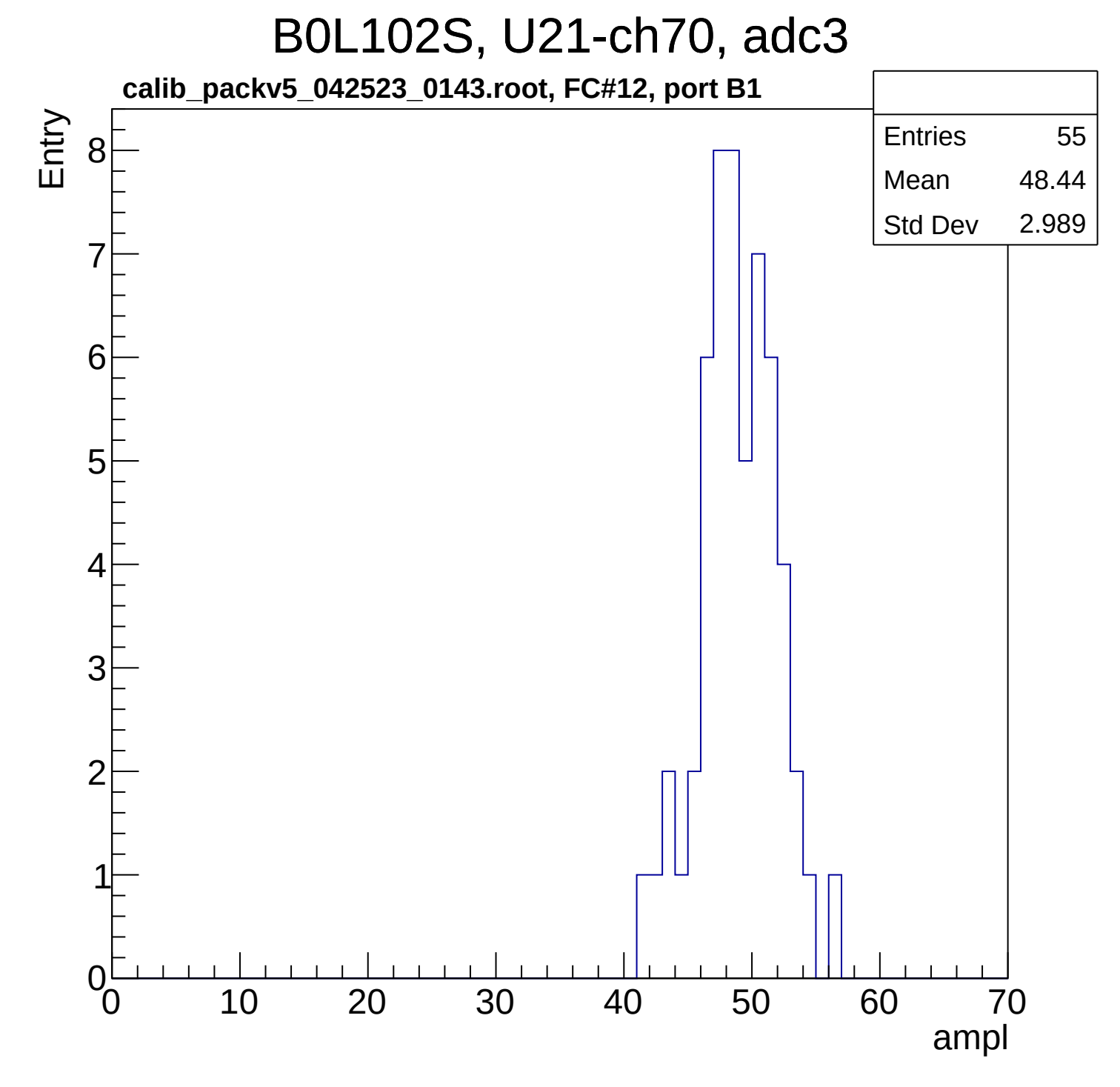
B0L102S, U21-ch70, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 48.44
Std Dev 2.989
Entry
ampl
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1
2
3
4
5
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0
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60
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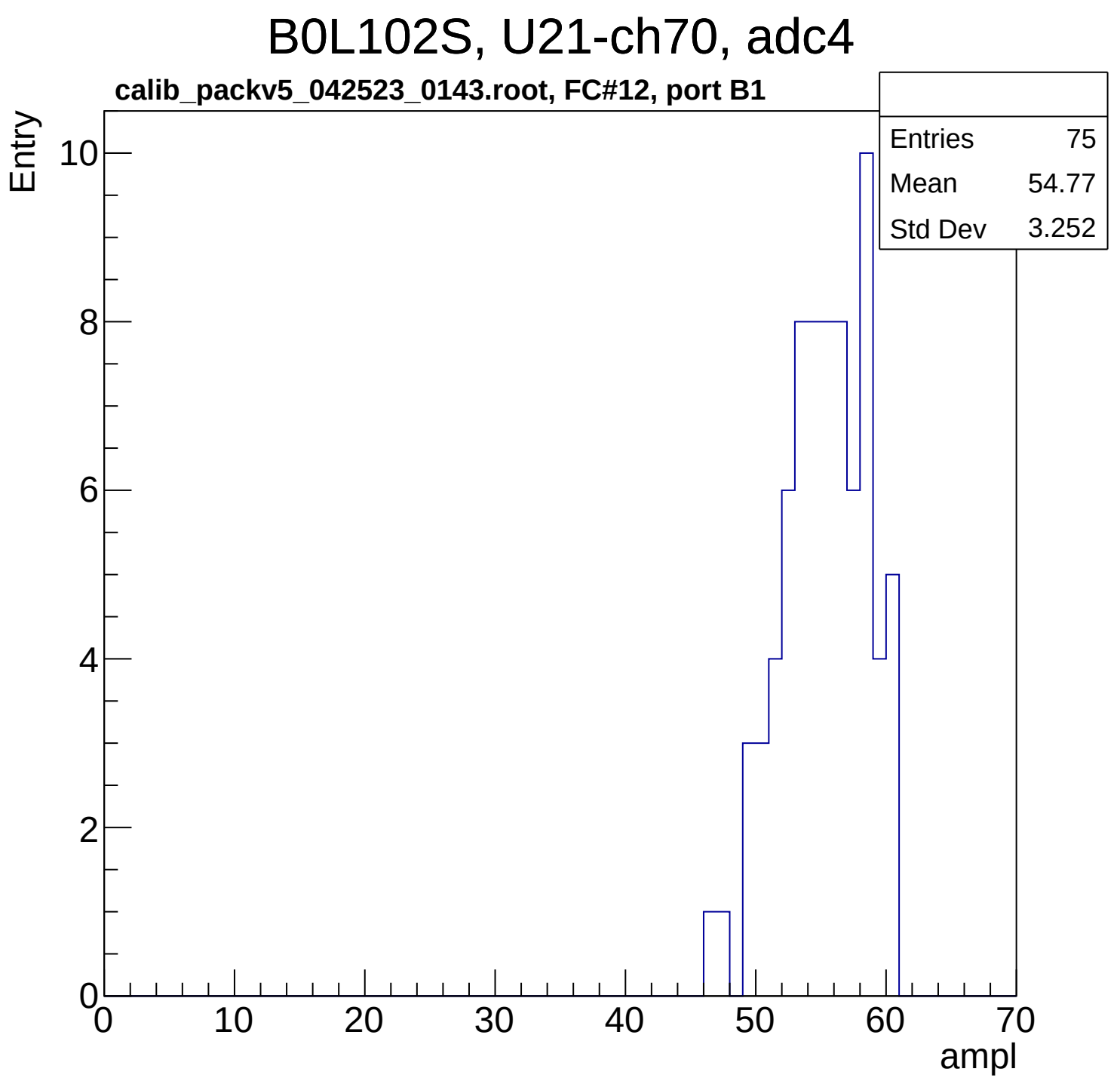
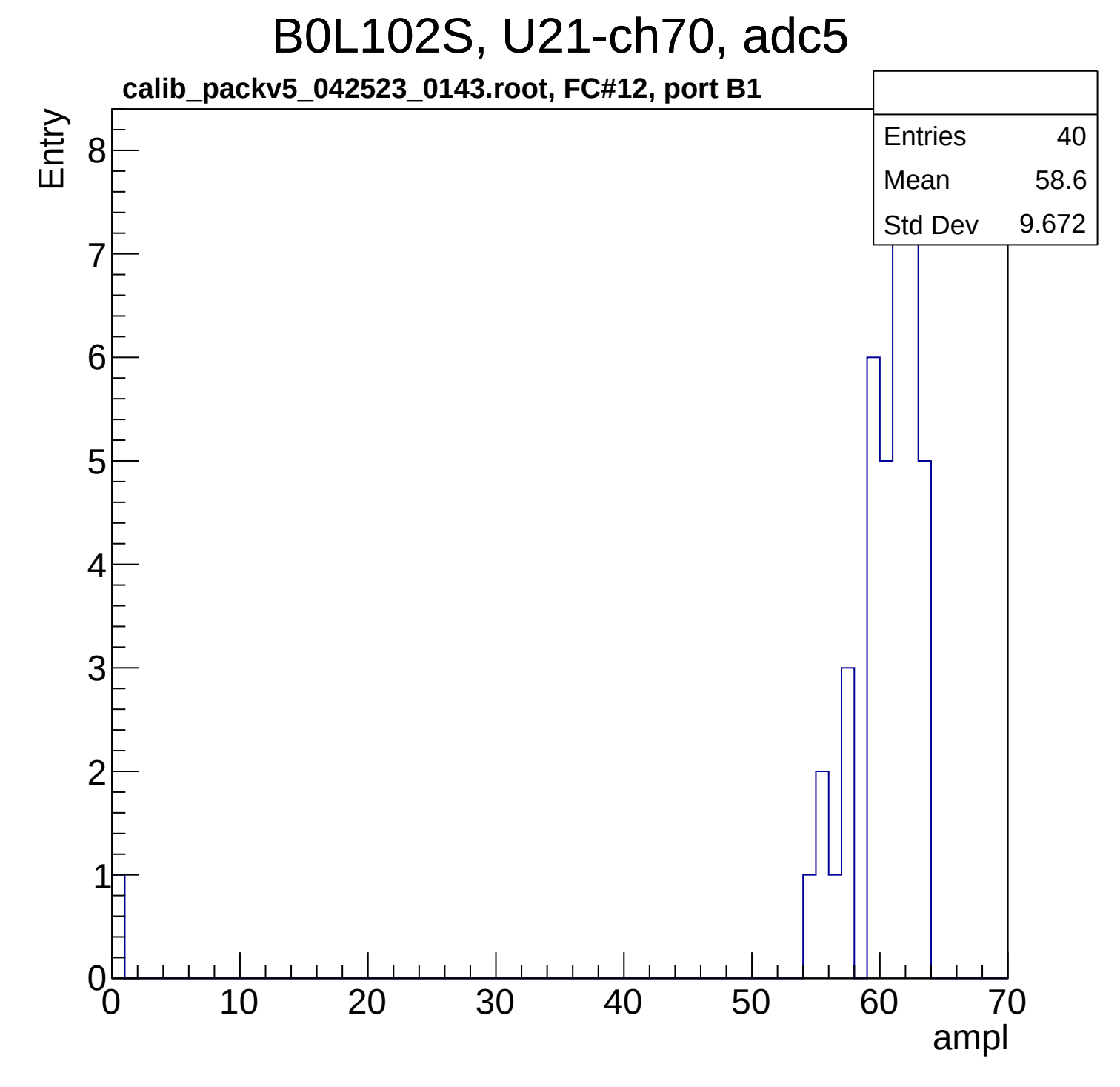

B0L102S, U21-ch70, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 54.77
Std Dev 3.252
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch70, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.6
Std Dev 9.672
Entry
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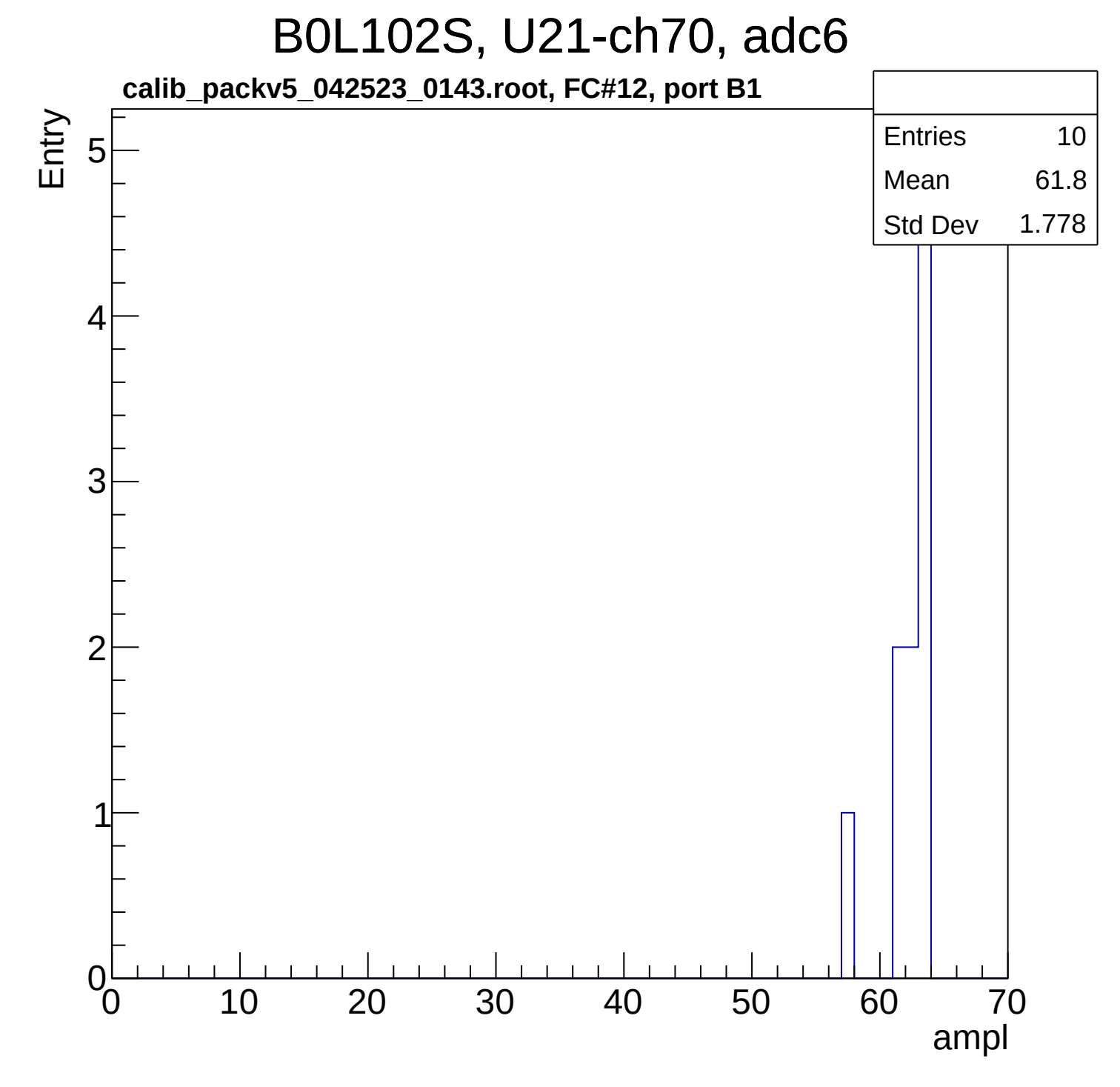
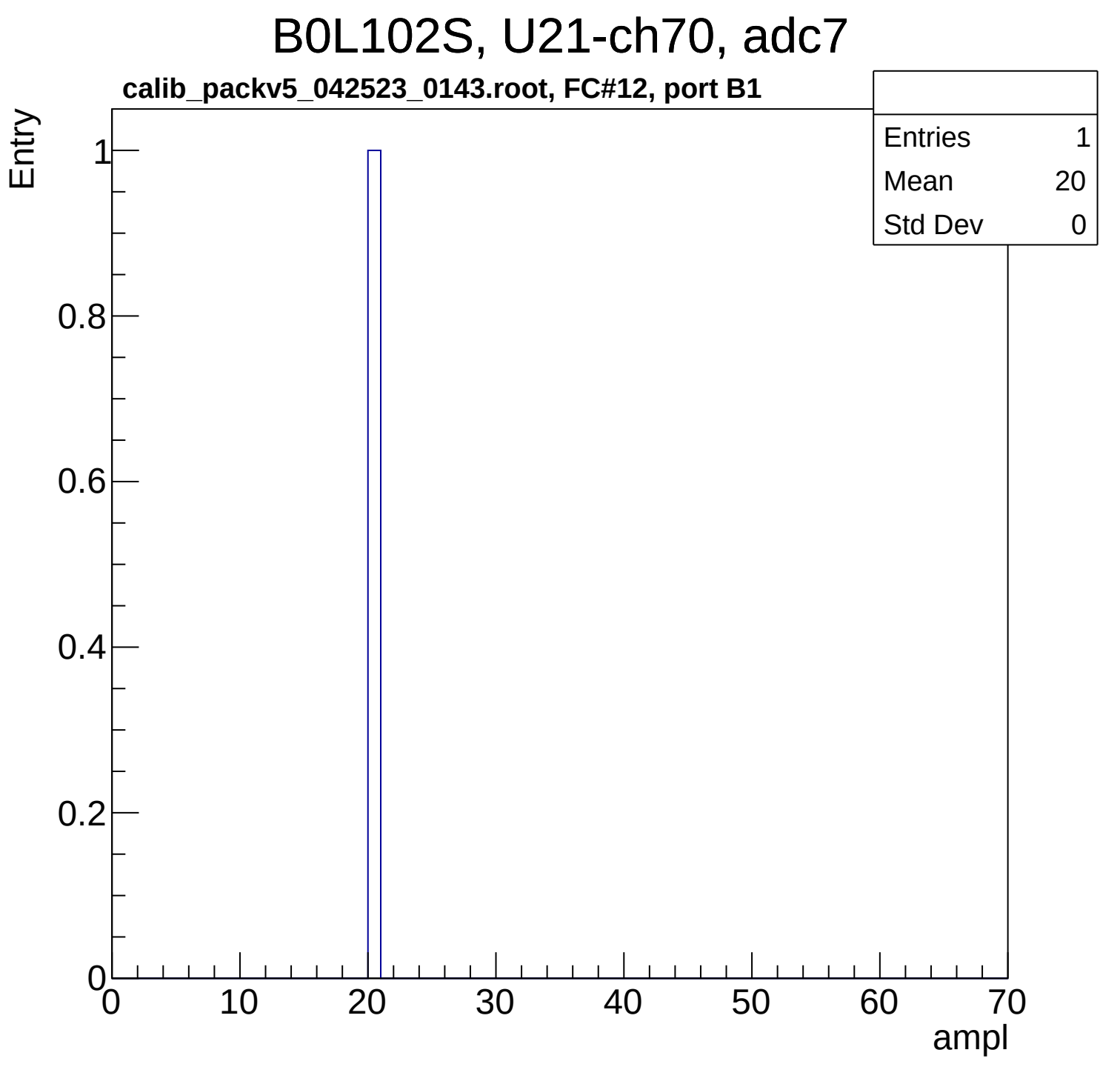
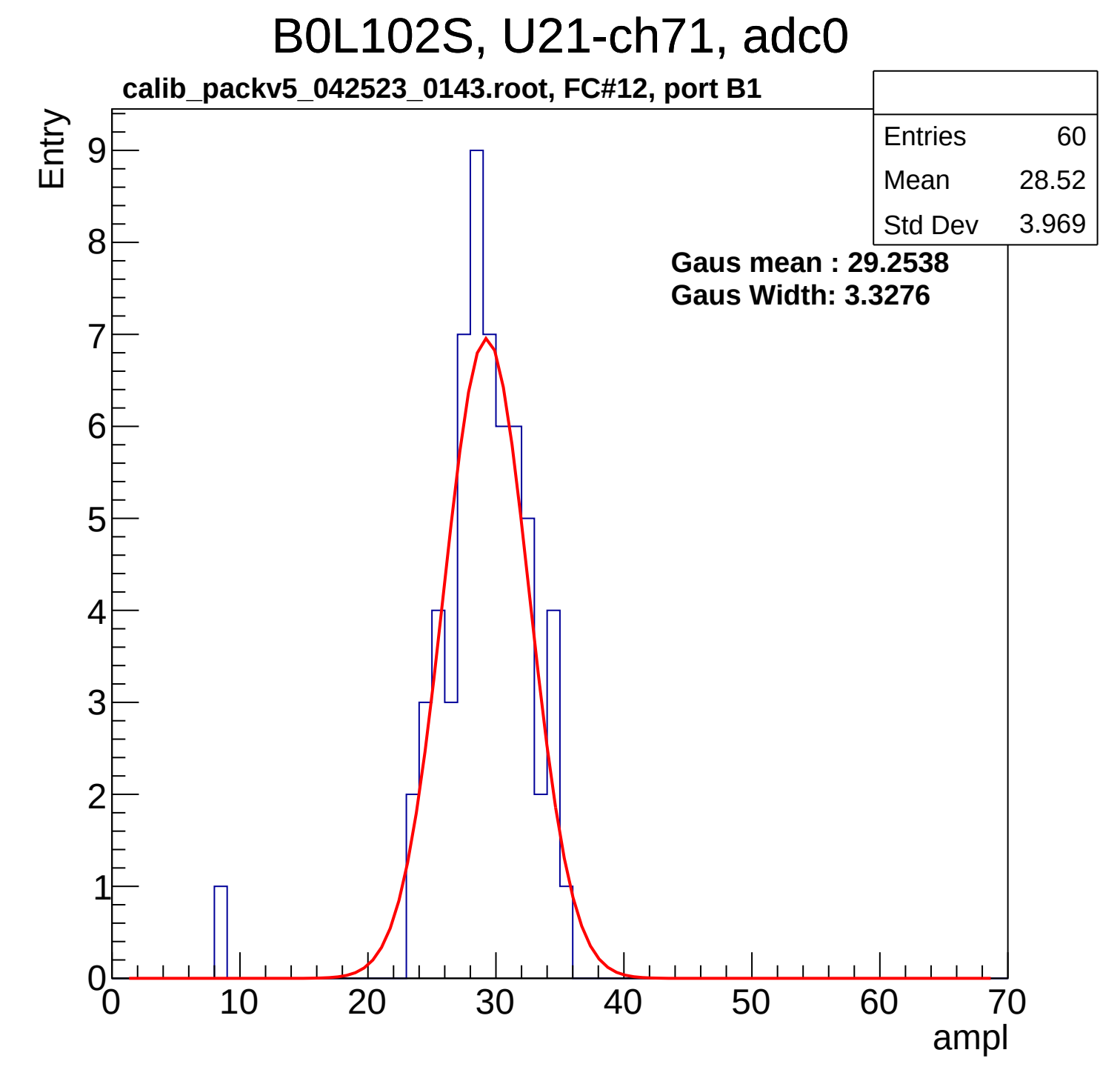
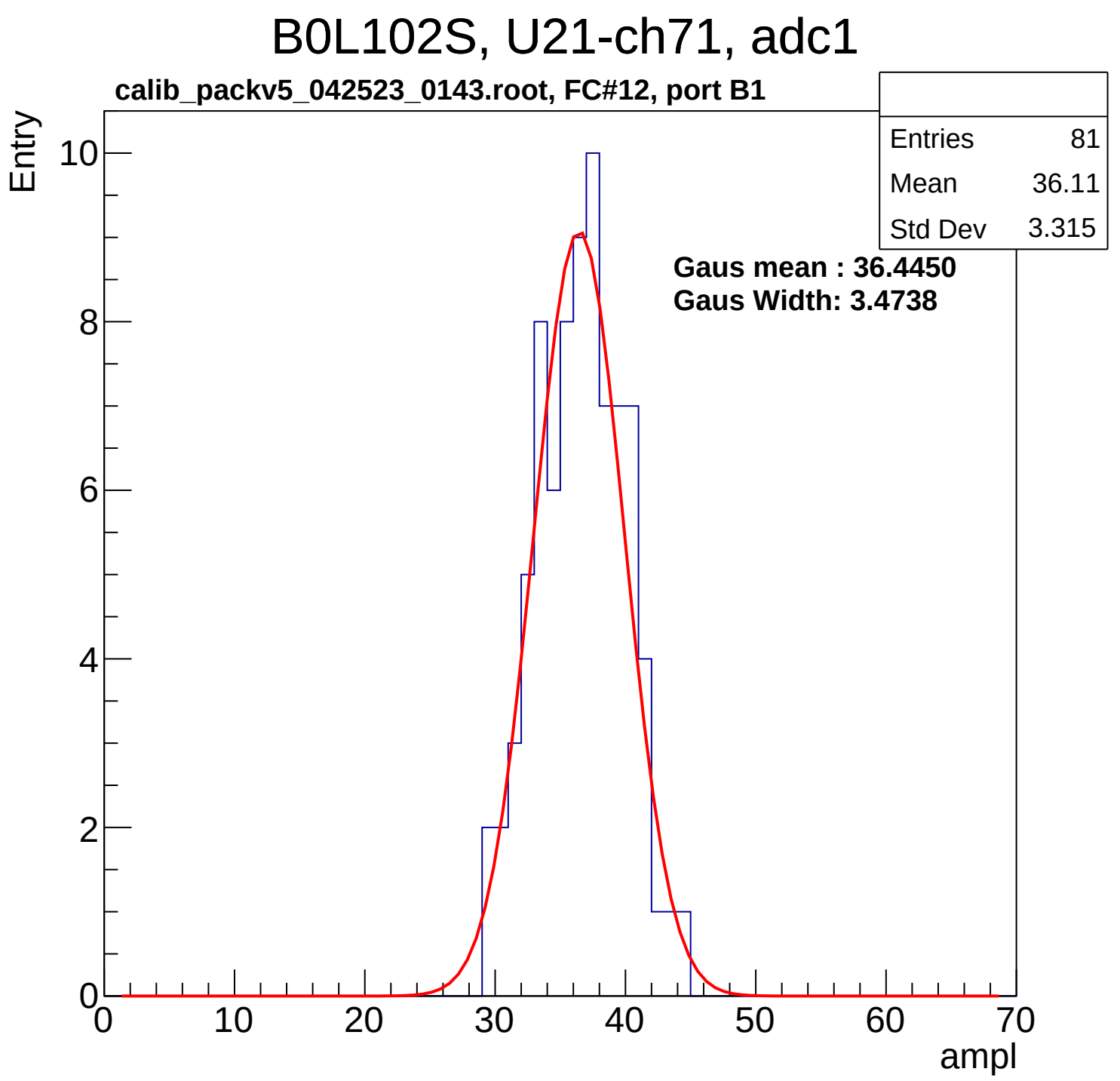
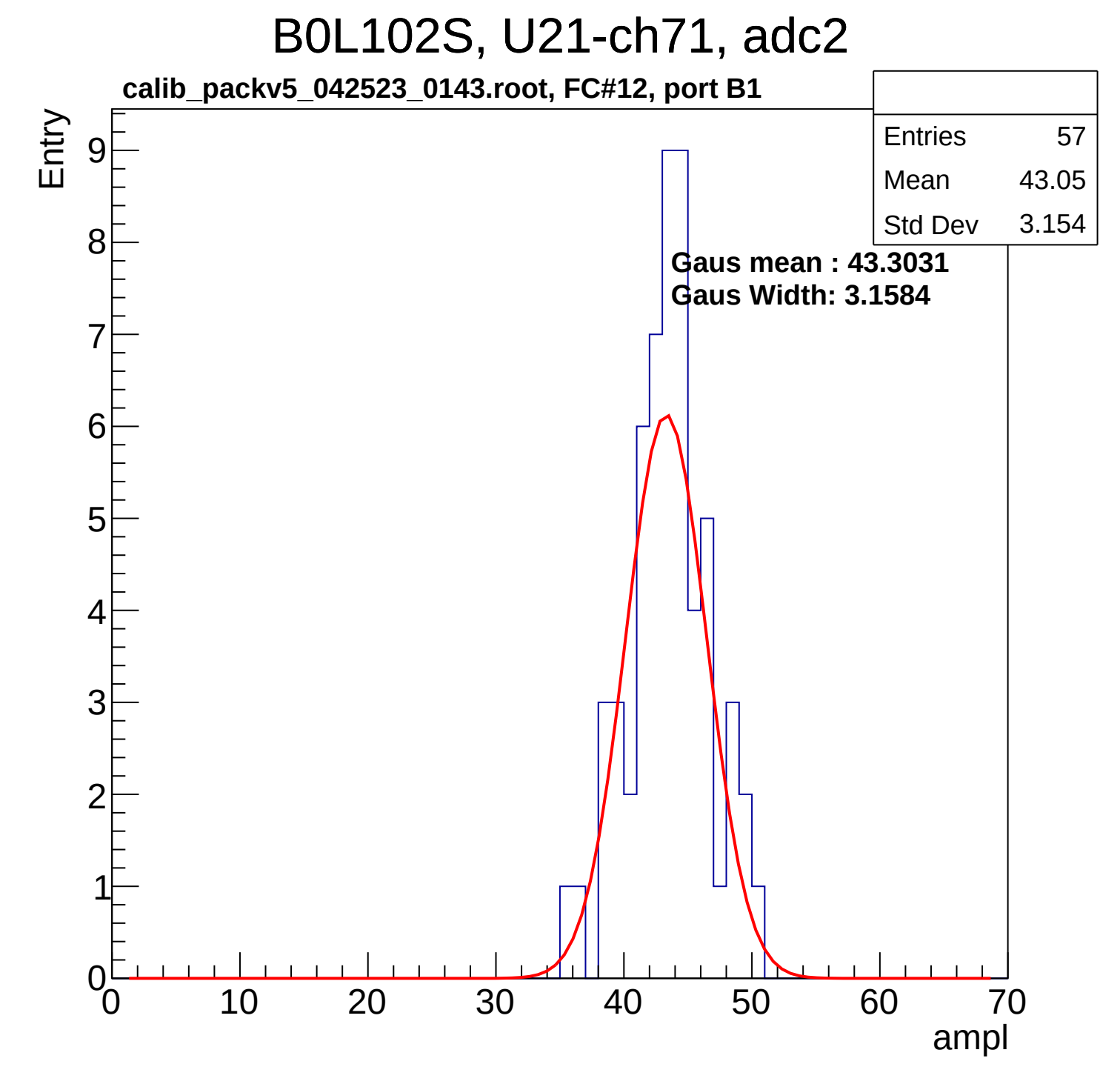
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

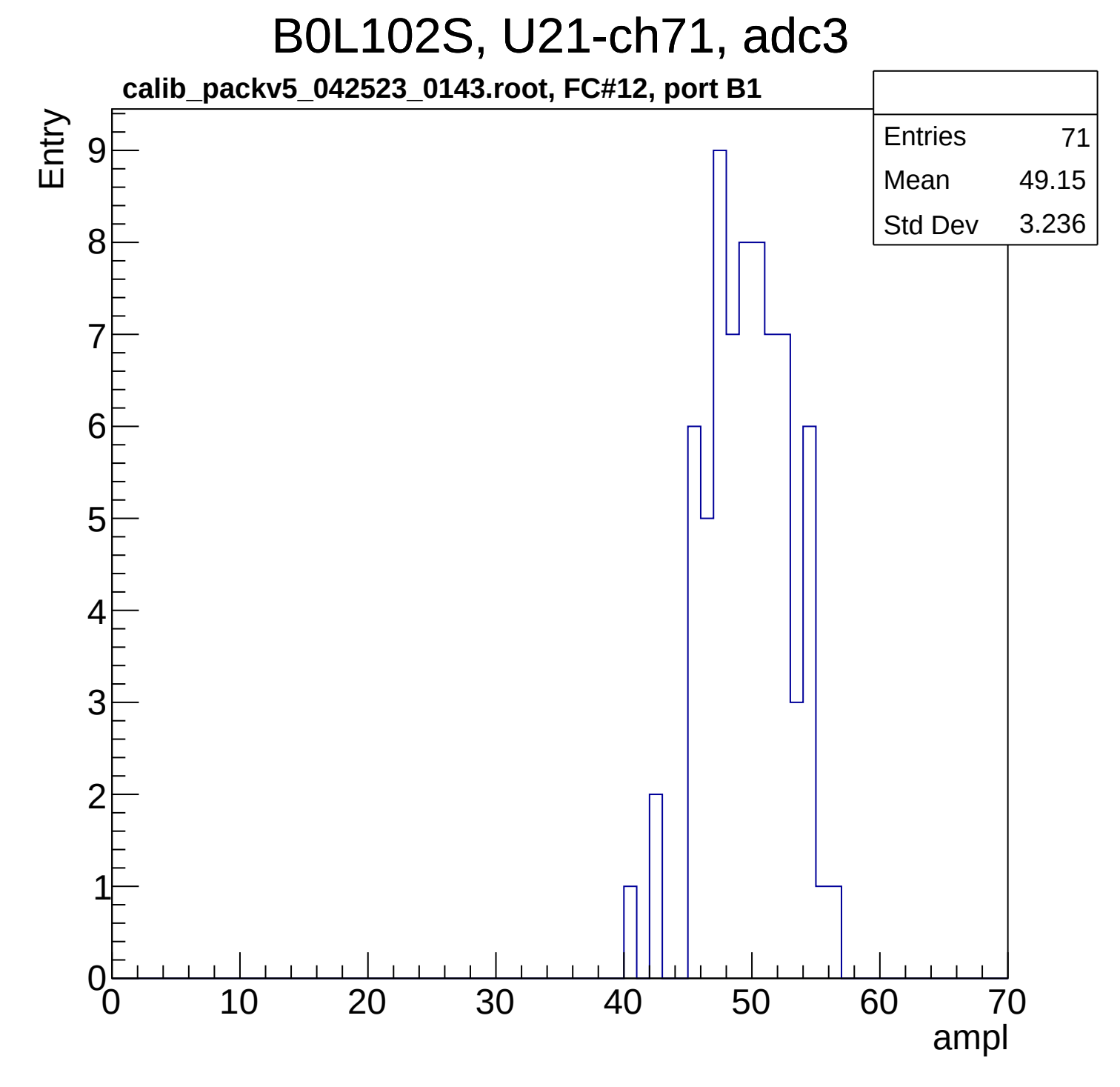

B0L102S, U21-ch70, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 61.8
Std Dev 1.778
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch70, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch71, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 28.52
Std Dev 3.969
Gaus mean : 29.2538
Gaus Width: 3.3276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch71, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 36.11
Std Dev 3.315
Gaus mean : 36.4450
Gaus Width: 3.4738
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch71, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.05
Std Dev 3.154
Gaus mean : 43.3031
Gaus Width: 3.1584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch71, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 49.15
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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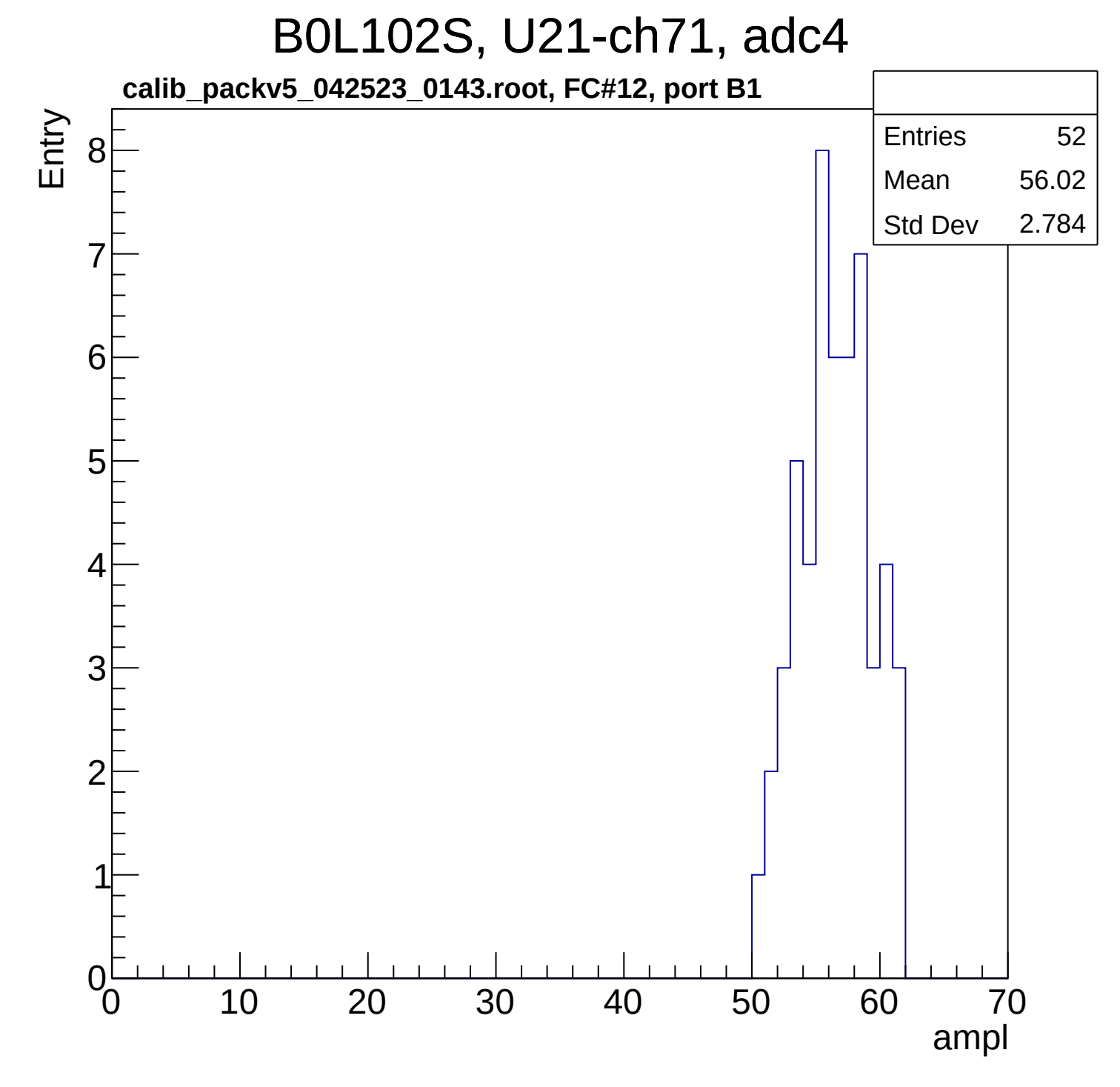
B0L102S, U21-ch71, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 56.02
Std Dev 2.784
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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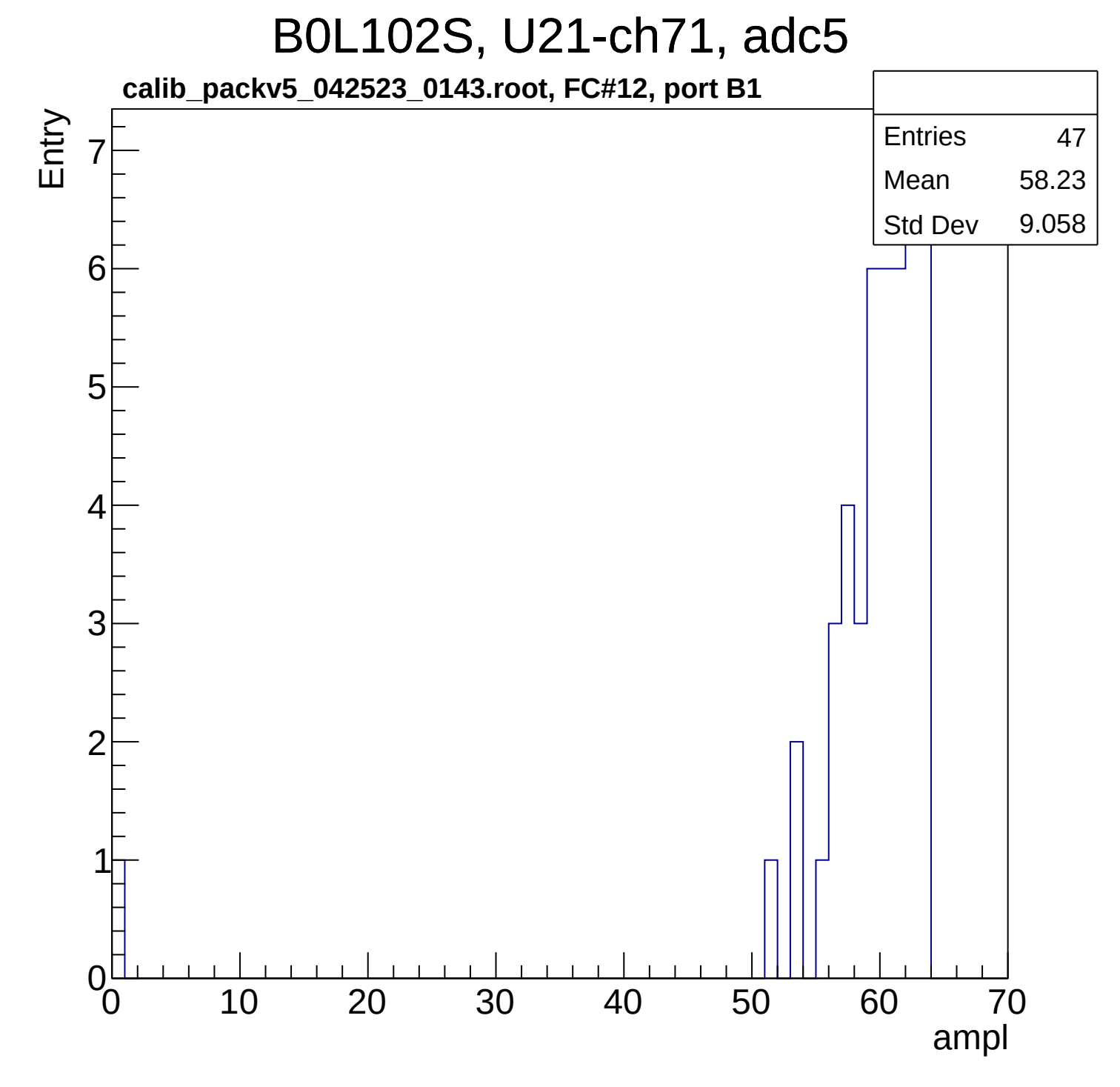
B0L102S, U21-ch71, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.23
Std Dev 9.058
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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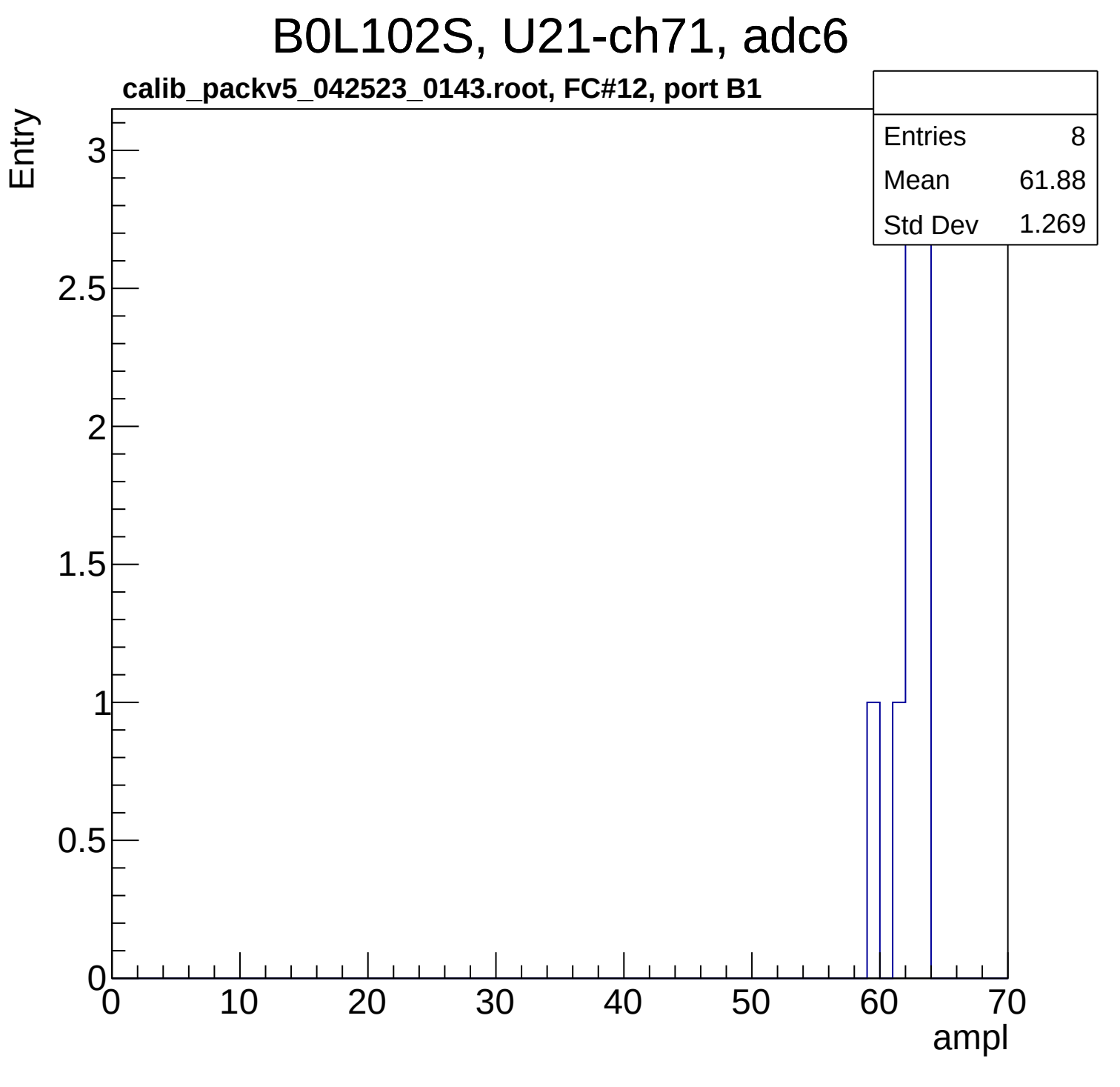
B0L102S, U21-ch71, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.88
Std Dev 1.269
Entry
ampl
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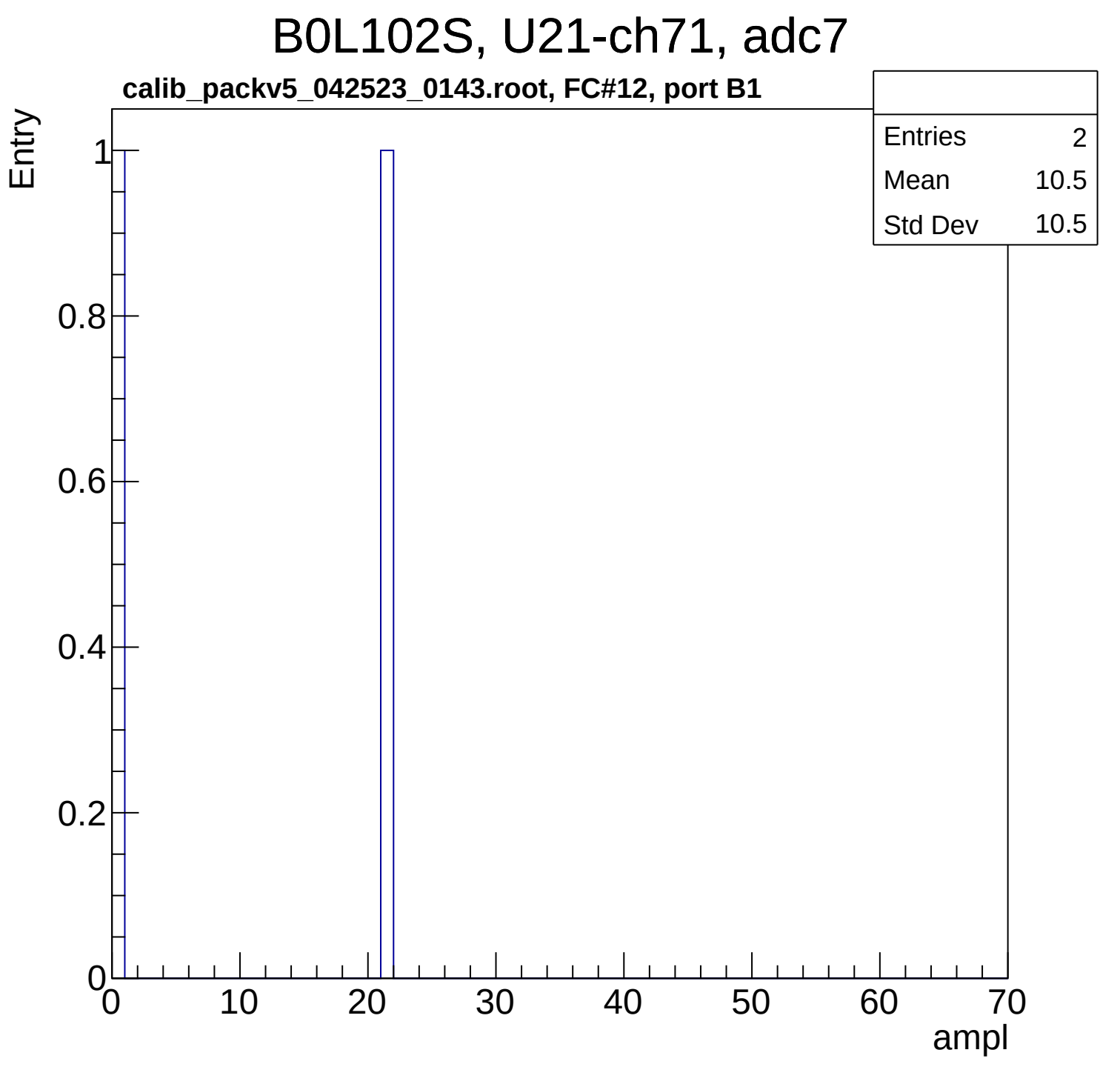
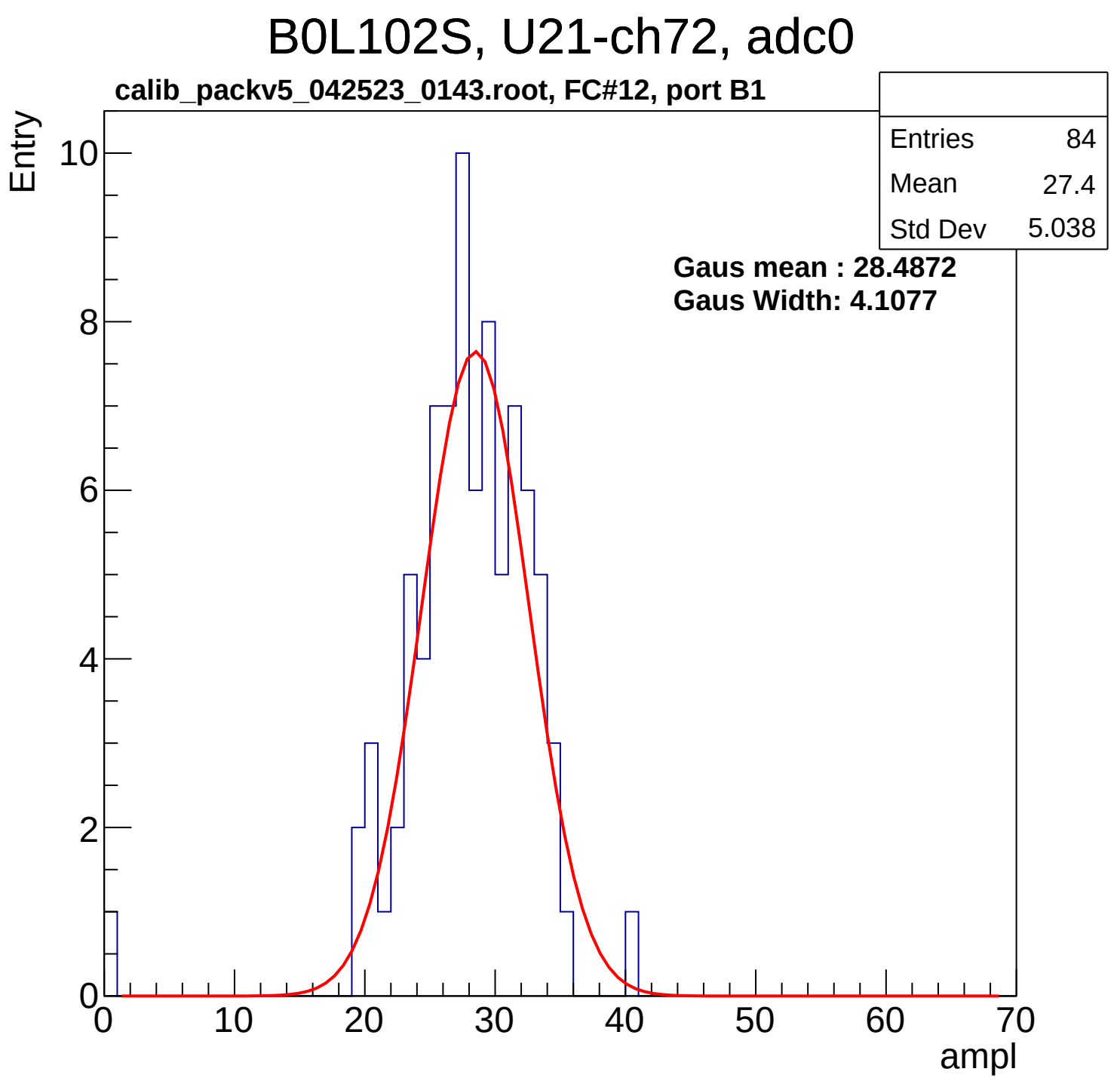
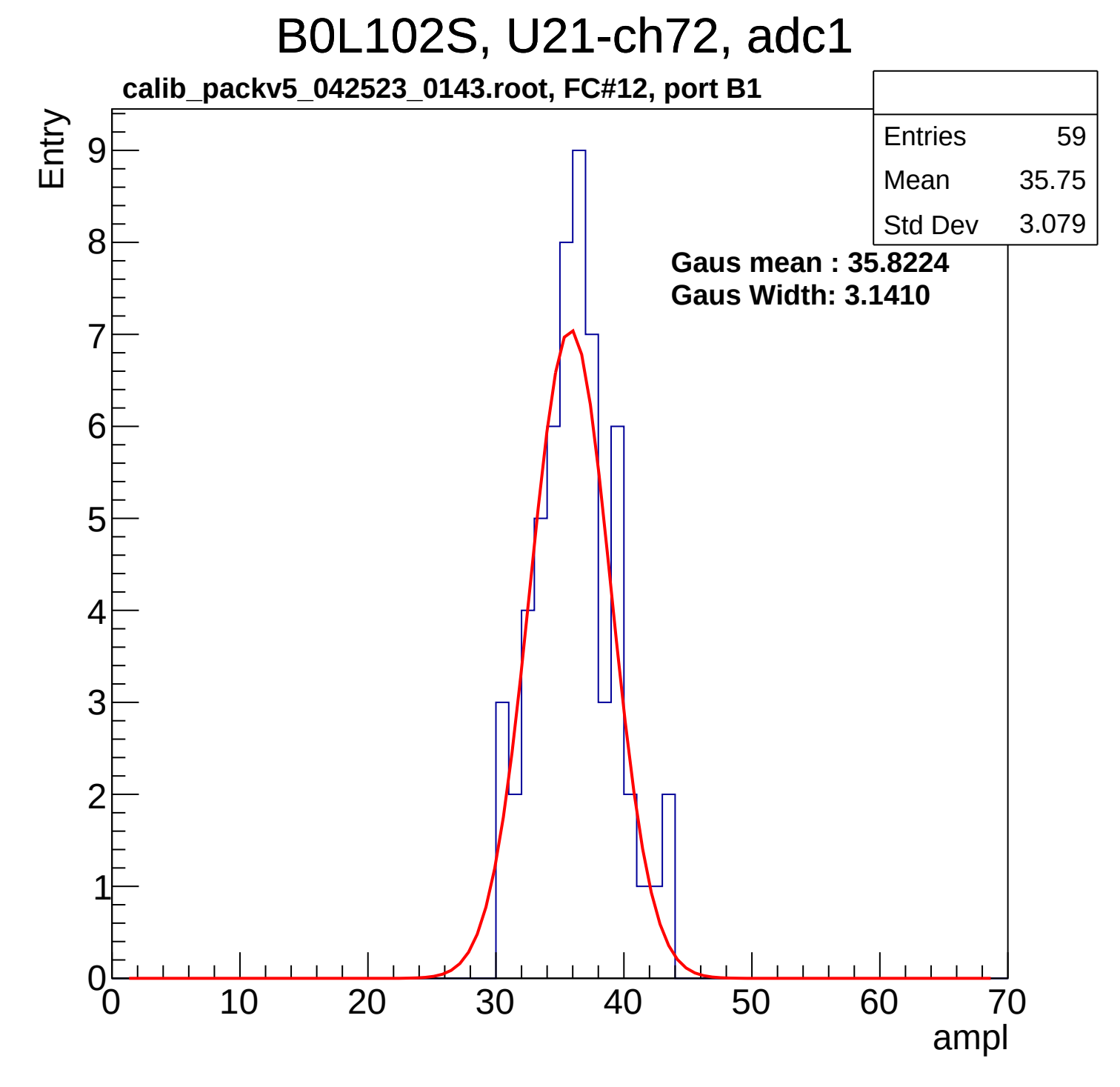
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch71, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch72, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 27.4
Std Dev 5.038
Gaus mean : 28.4872
Gaus Width: 4.1077
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch72, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 35.75
Std Dev 3.079
Gaus mean : 35.8224
Gaus Width: 3.1410
Entry
ampl
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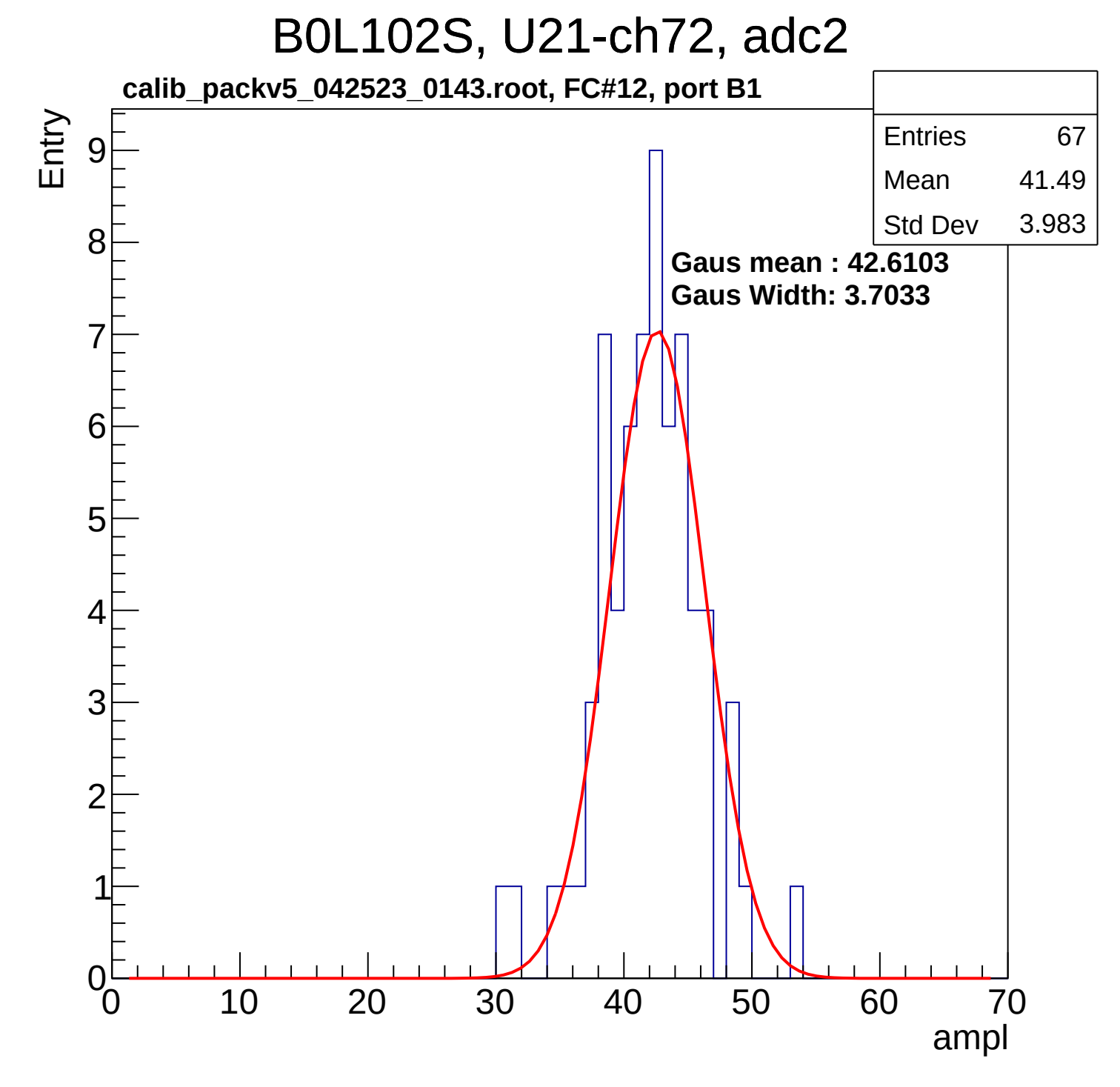
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch72, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 41.49
Std Dev 3.983
Gaus mean : 42.6103
Gaus Width: 3.7033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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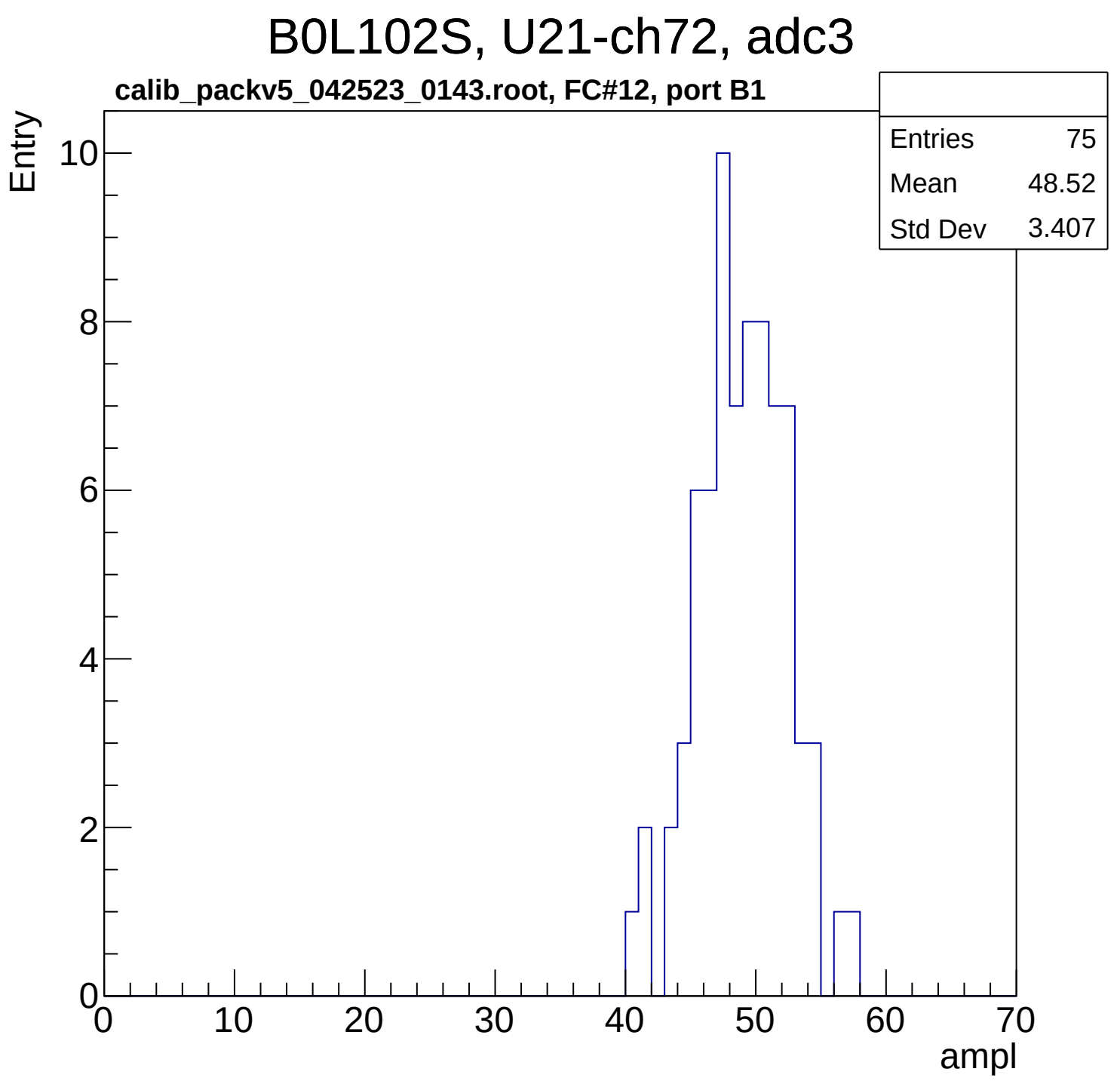
B0L102S, U21-ch72, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 48.52
Std Dev 3.407
Entry
ampl
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6
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0
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60
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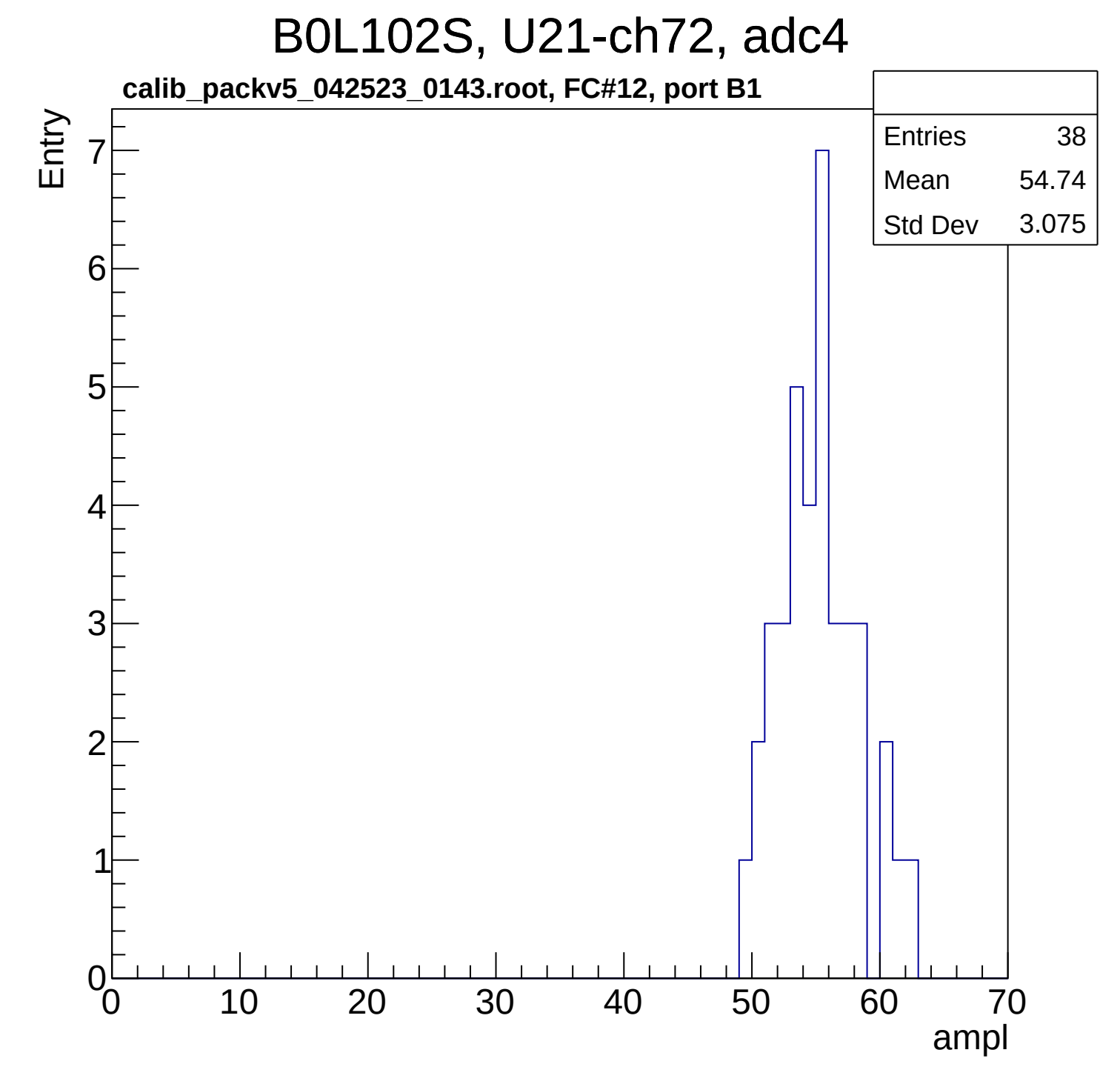
B0L102S, U21-ch72, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 54.74
Std Dev 3.075
Entry
ampl
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0
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60
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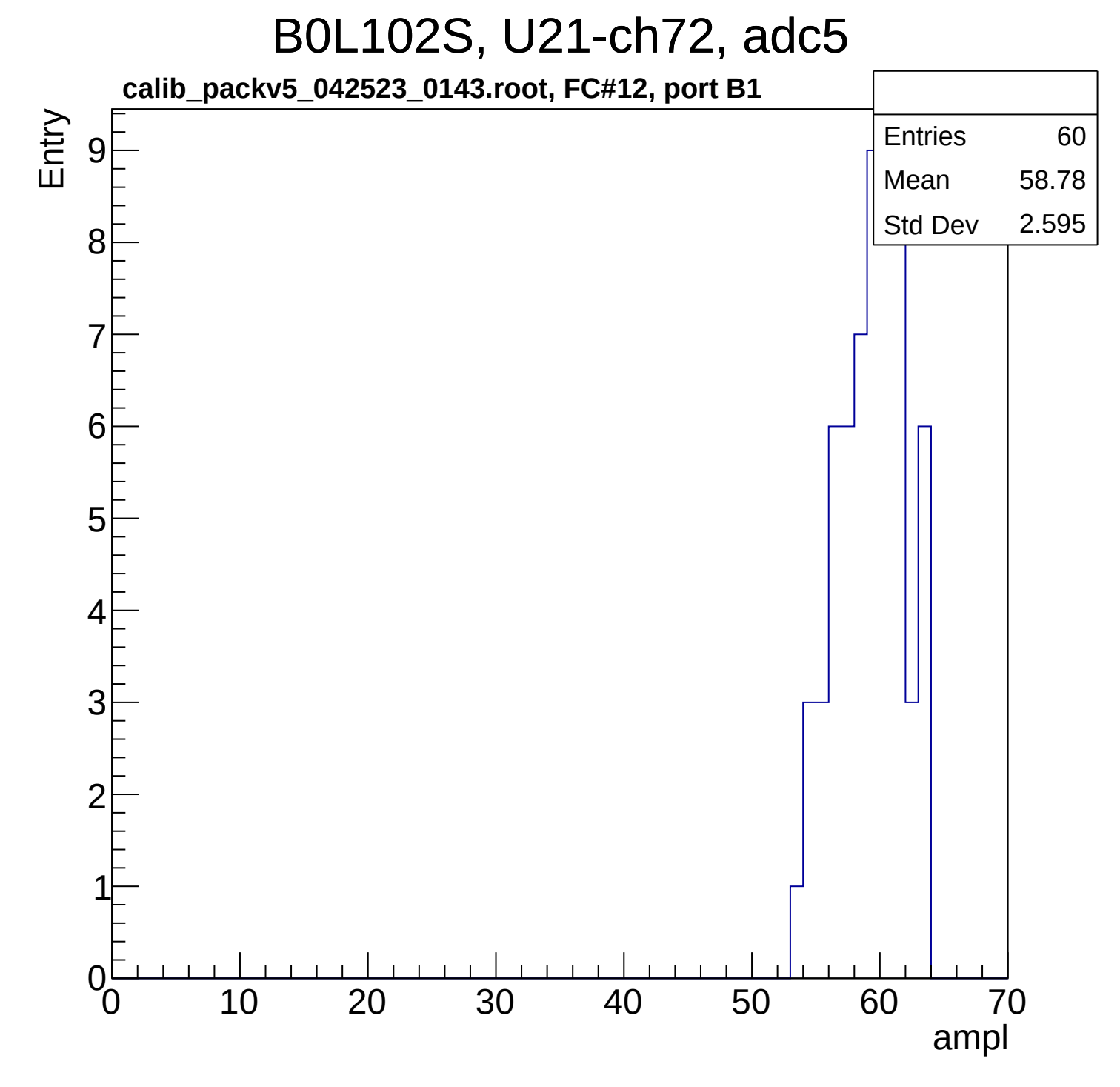
B0L102S, U21-ch72, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.78
Std Dev 2.595
Entry
ampl
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4
5
6
7
8
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0
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60
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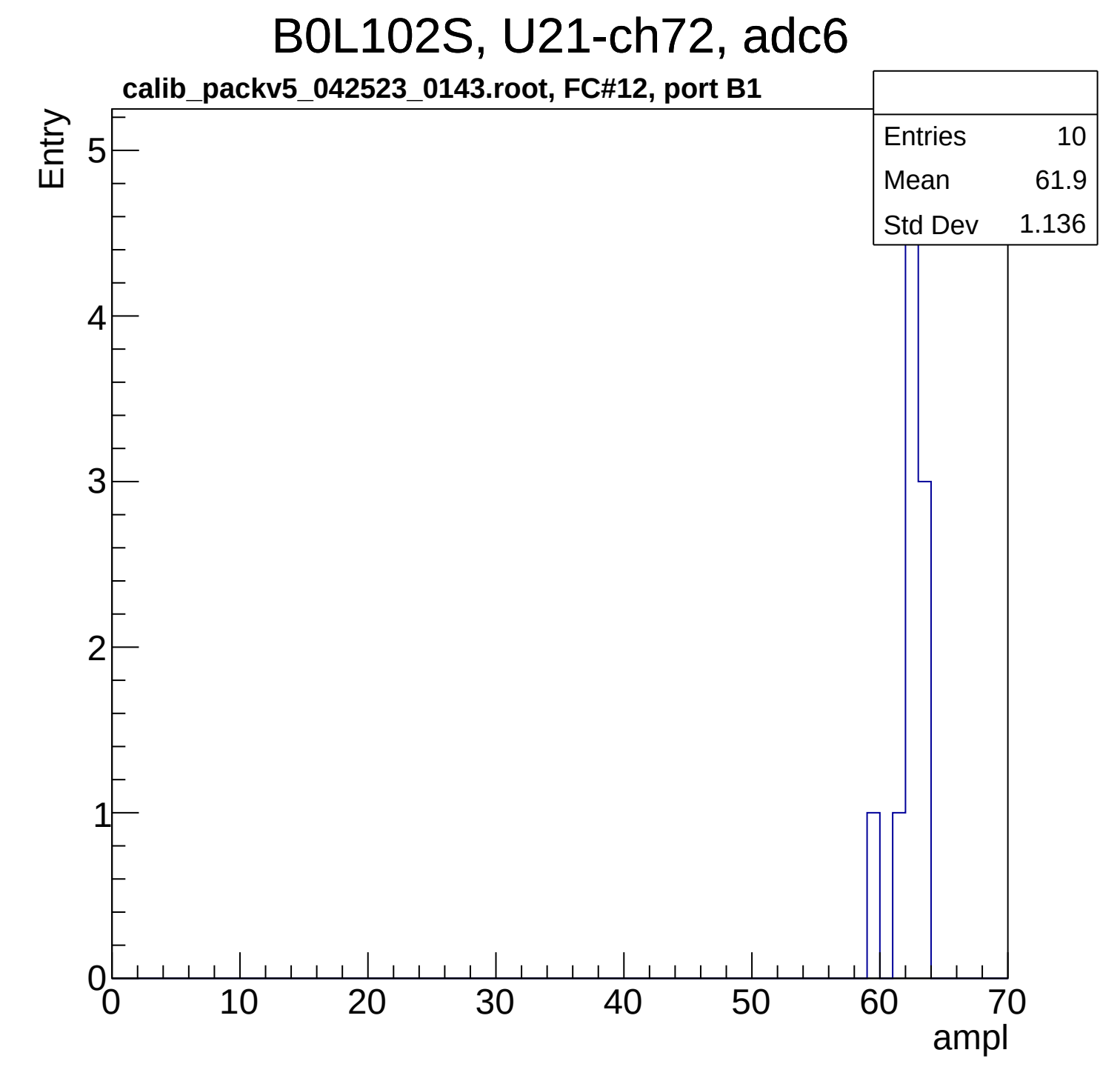
B0L102S, U21-ch72, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 61.9
Std Dev 1.136
Entry
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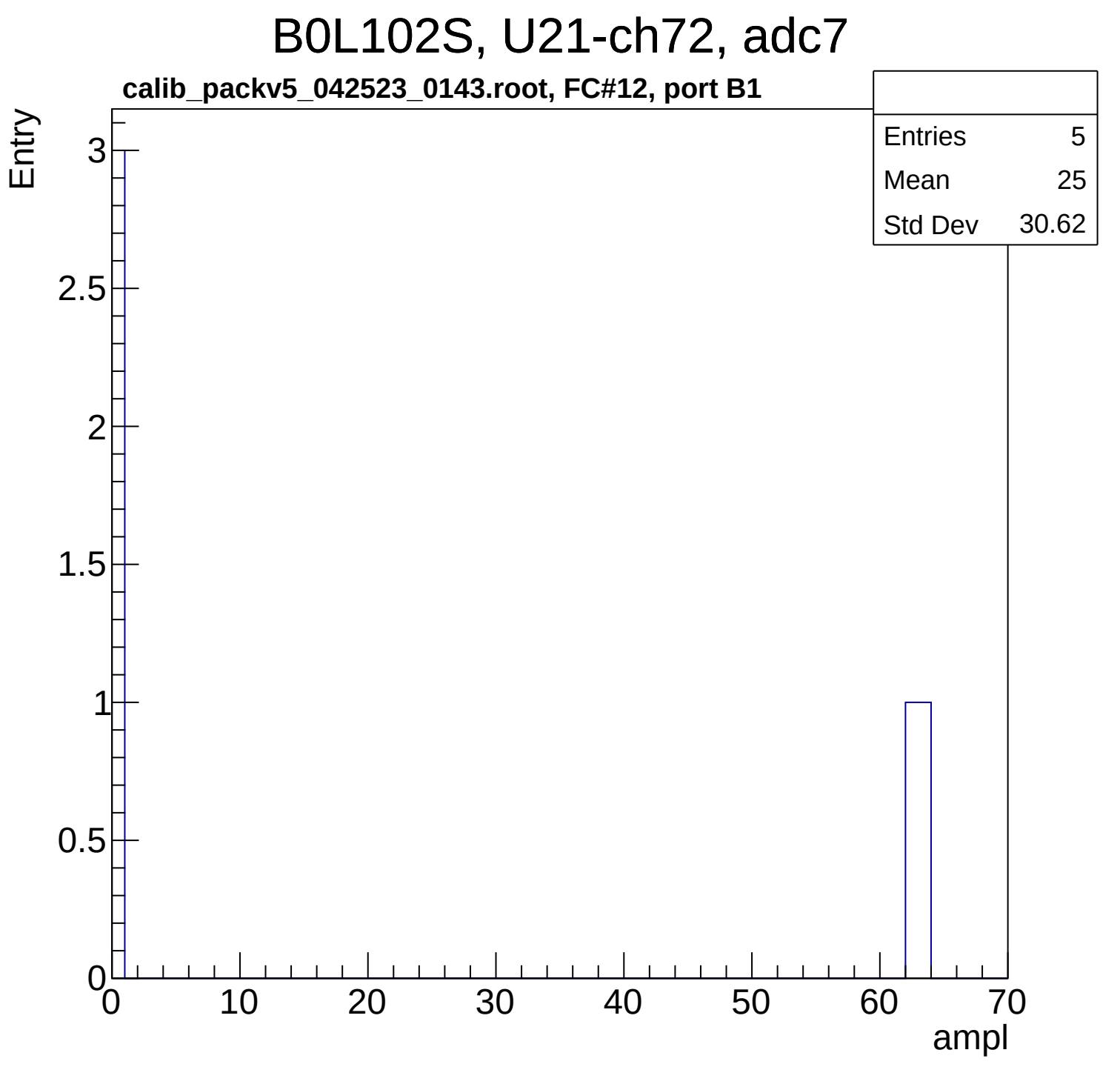
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch72, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 25
Std Dev 30.62
Entry
ampl
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0.5
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1.5
2
2.5
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0
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60
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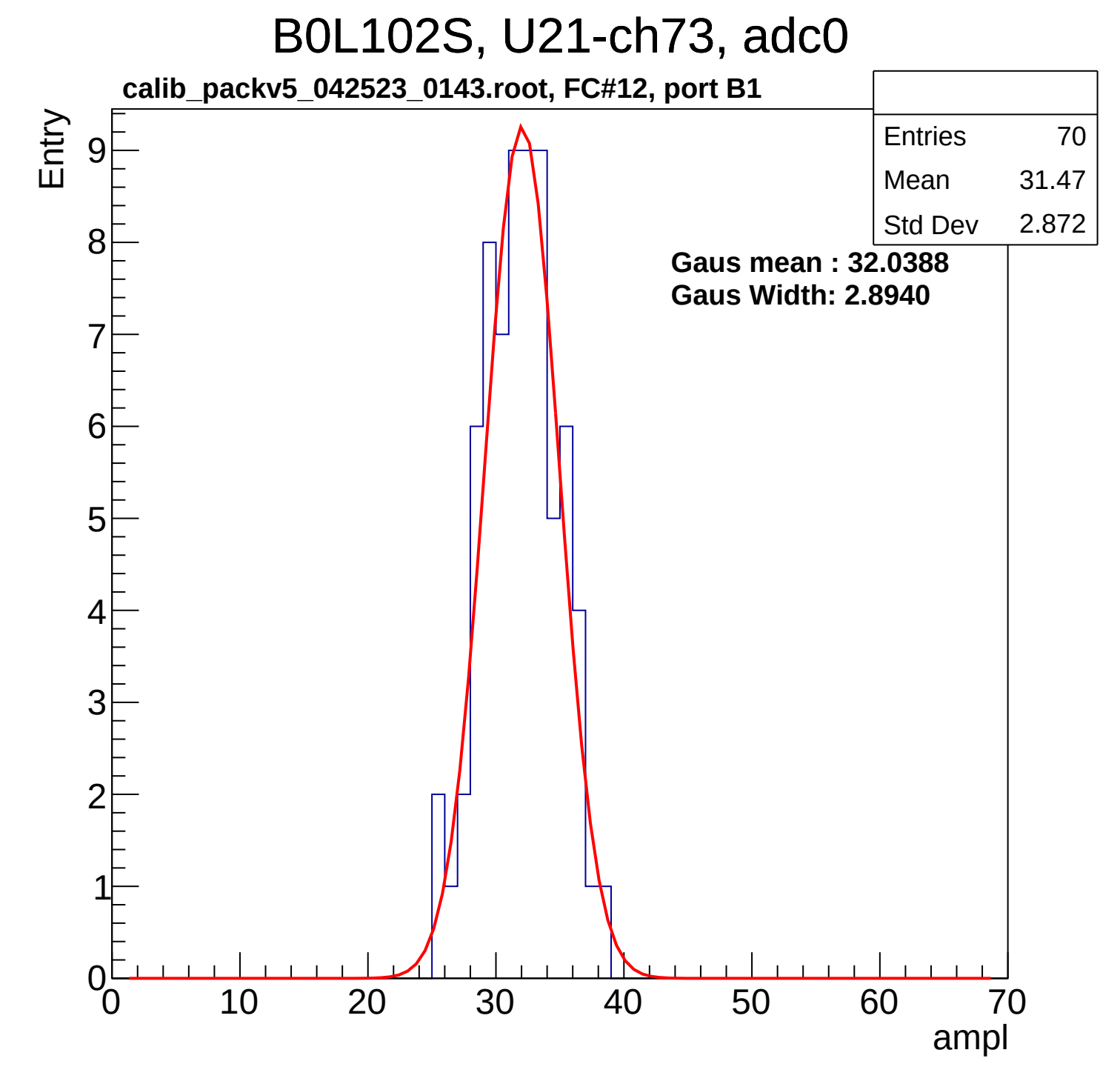
B0L102S, U21-ch73, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 31.47
Std Dev 2.872
Gaus mean : 32.0388
Gaus Width: 2.8940
Entry
ampl
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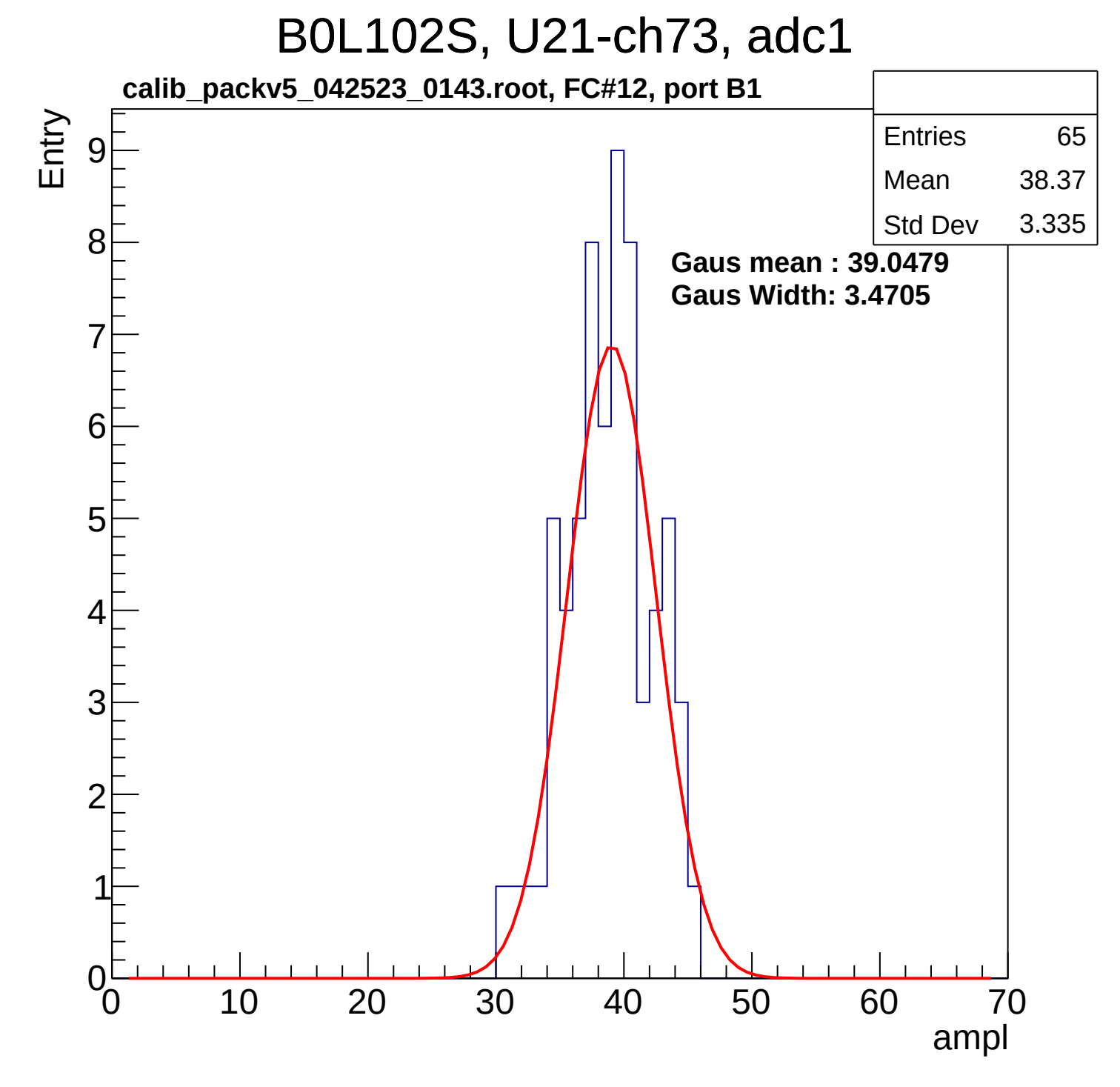
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch73, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 38.37
Std Dev 3.335
Gaus mean : 39.0479
Gaus Width: 3.4705
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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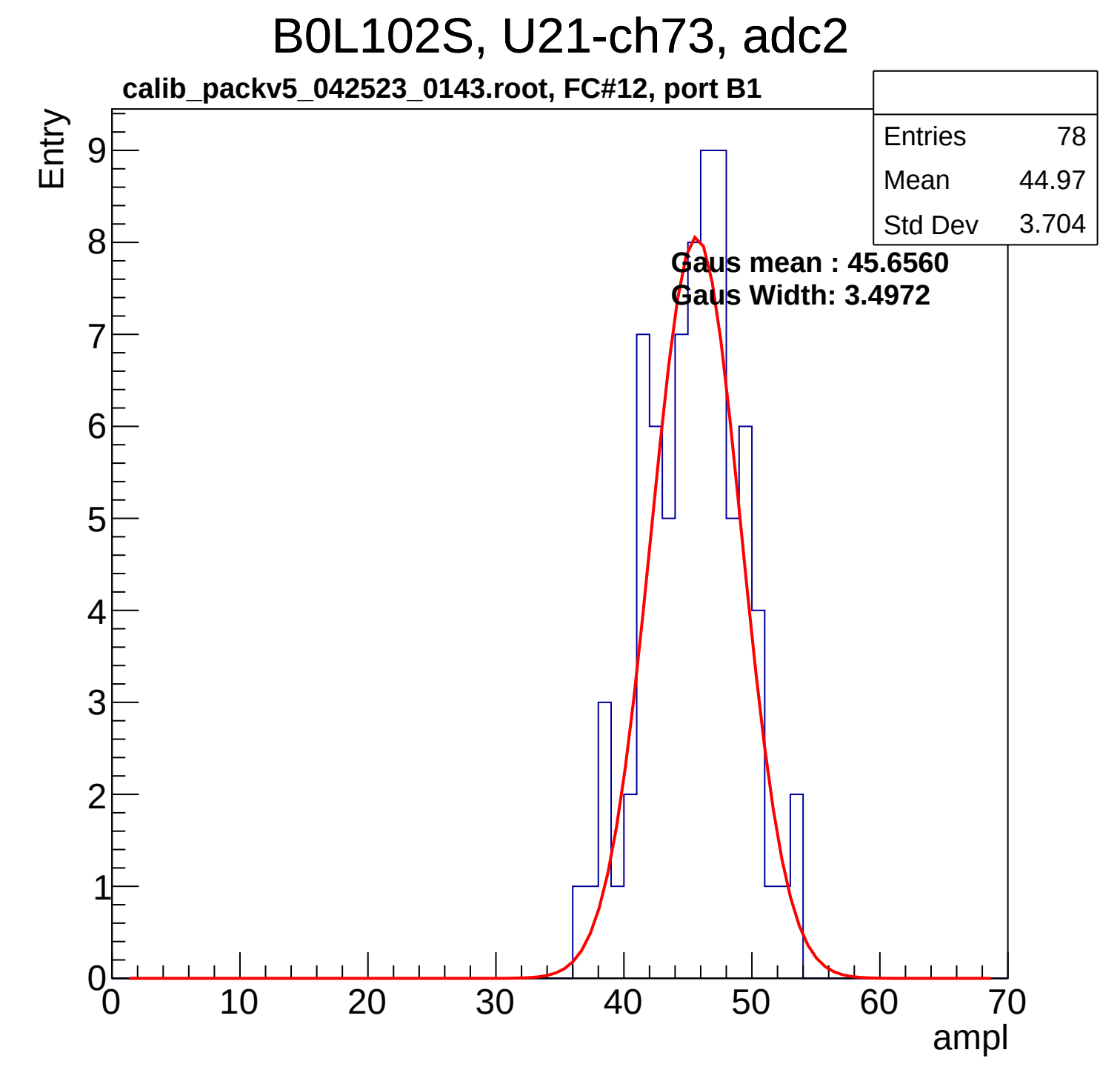
B0L102S, U21-ch73, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 44.97
Std Dev 3.704
Gaus mean : 45.6560
Gaus Width: 3.4972
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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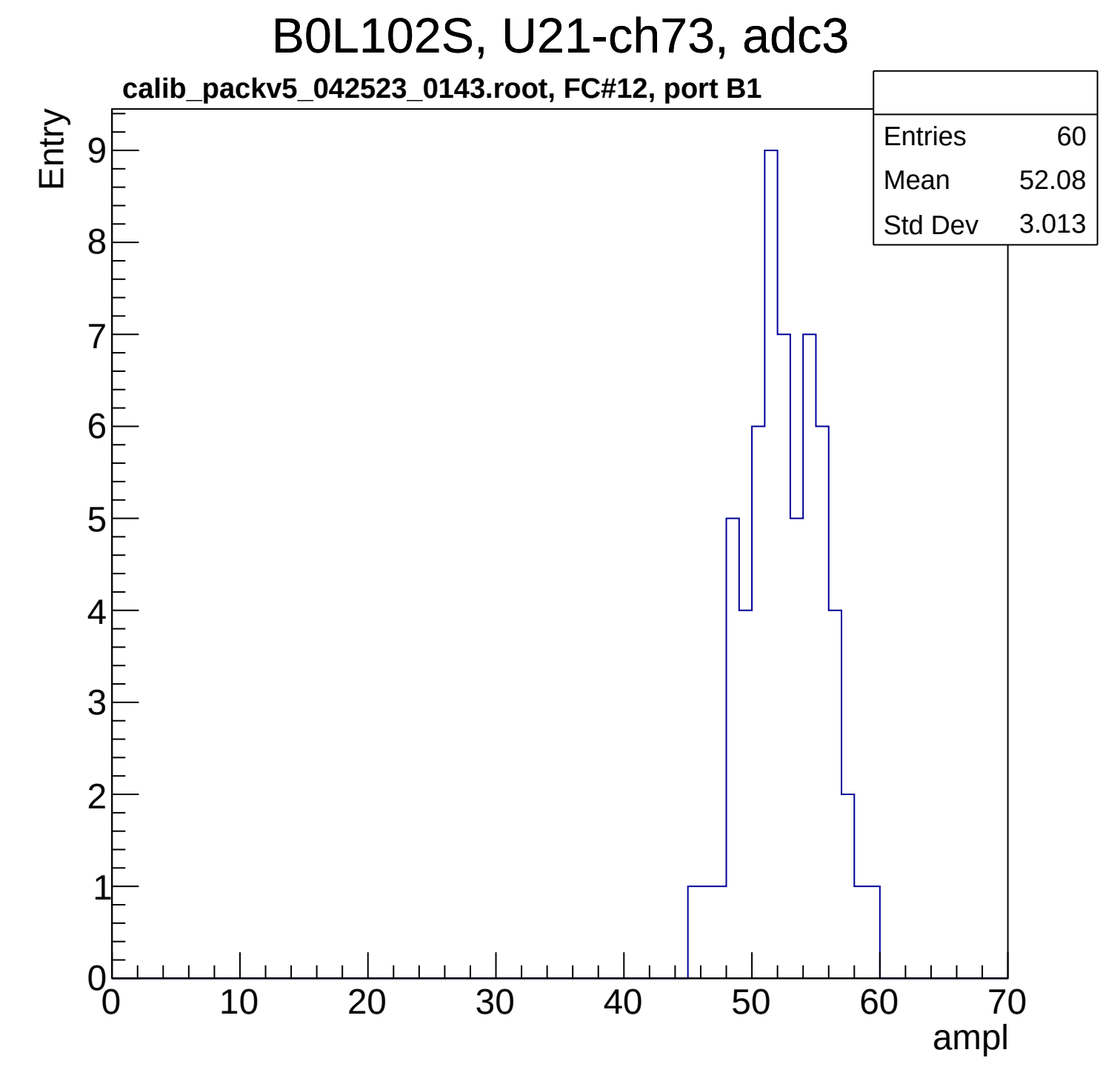
B0L102S, U21-ch73, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 52.08
Std Dev 3.013
Entry
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3
4
5
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7
8
9
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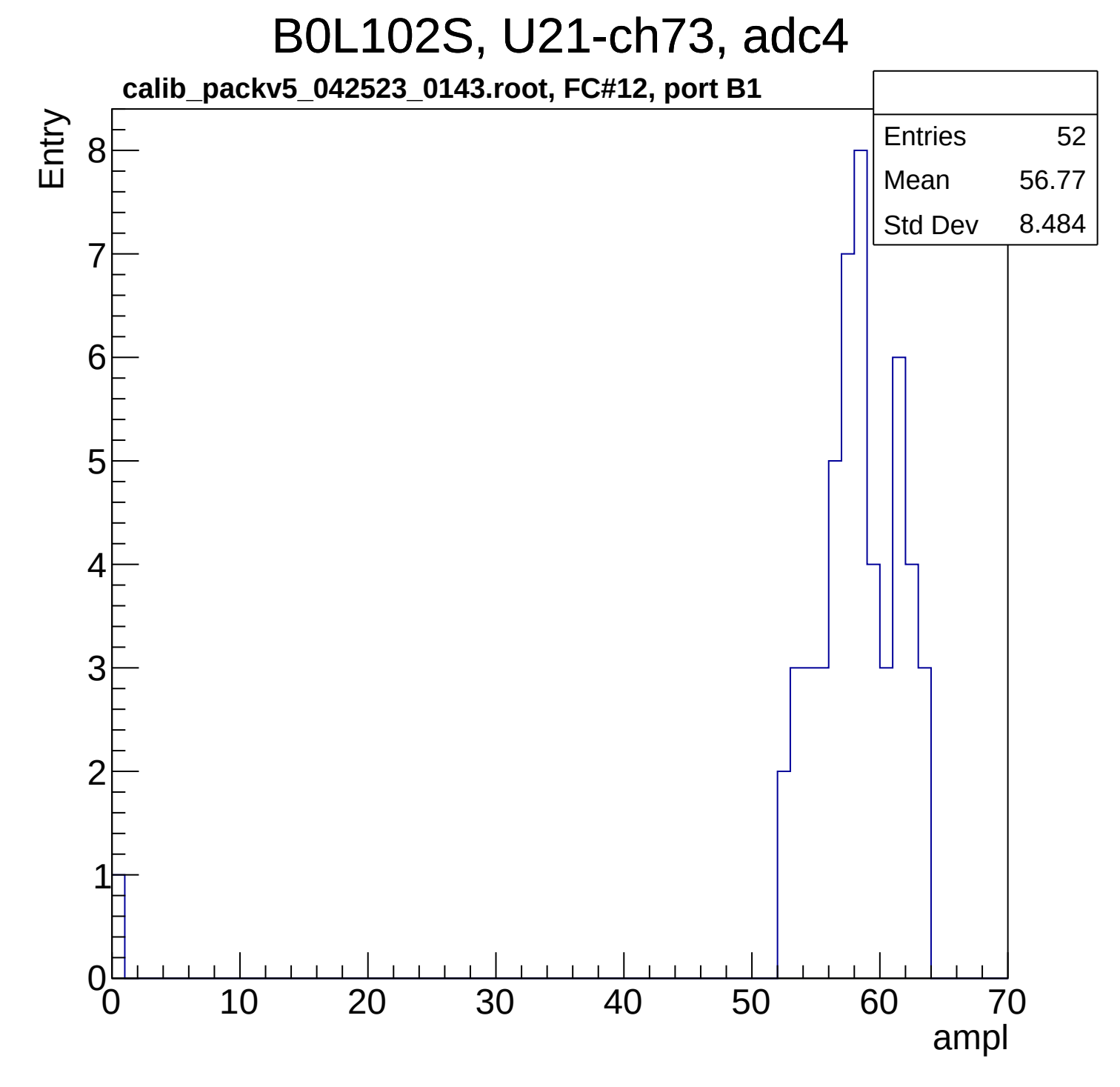
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch73, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 56.77
Std Dev 8.484
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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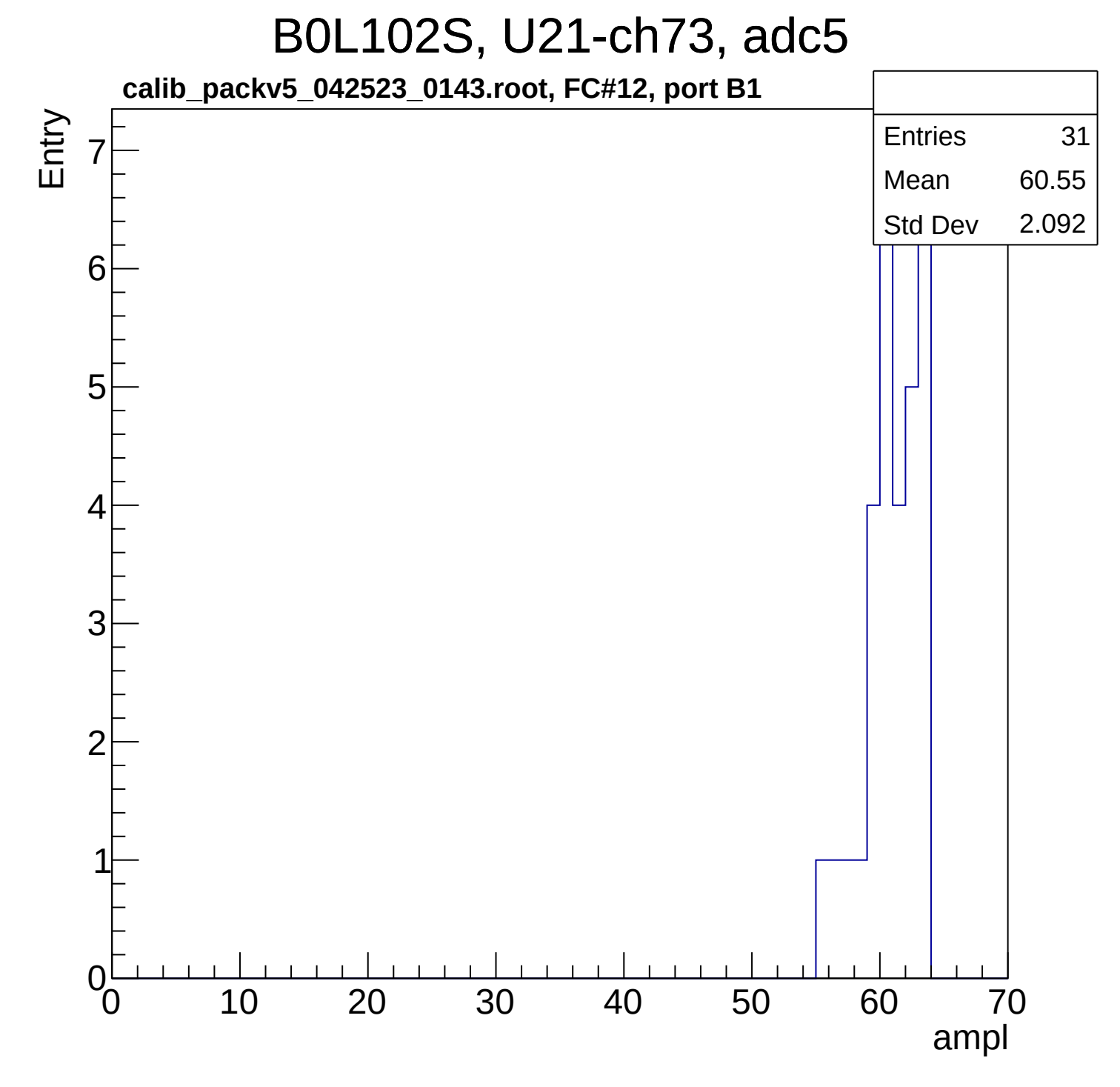
B0L102S, U21-ch73, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 60.55
Std Dev 2.092
Entry
0
1
2
3
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5
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7
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10
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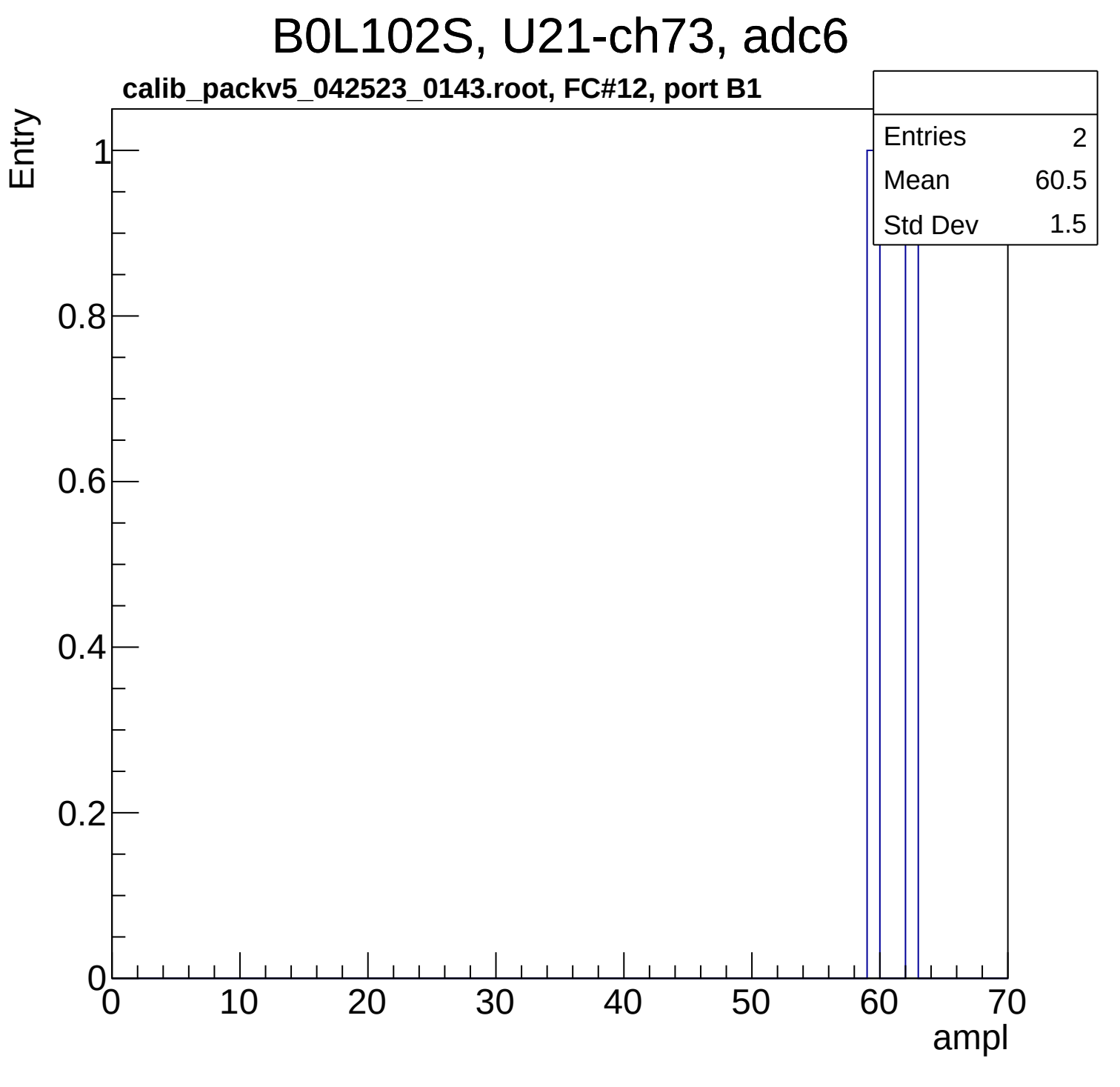
60
70
ampl


B0L102S, U21-ch73, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 60.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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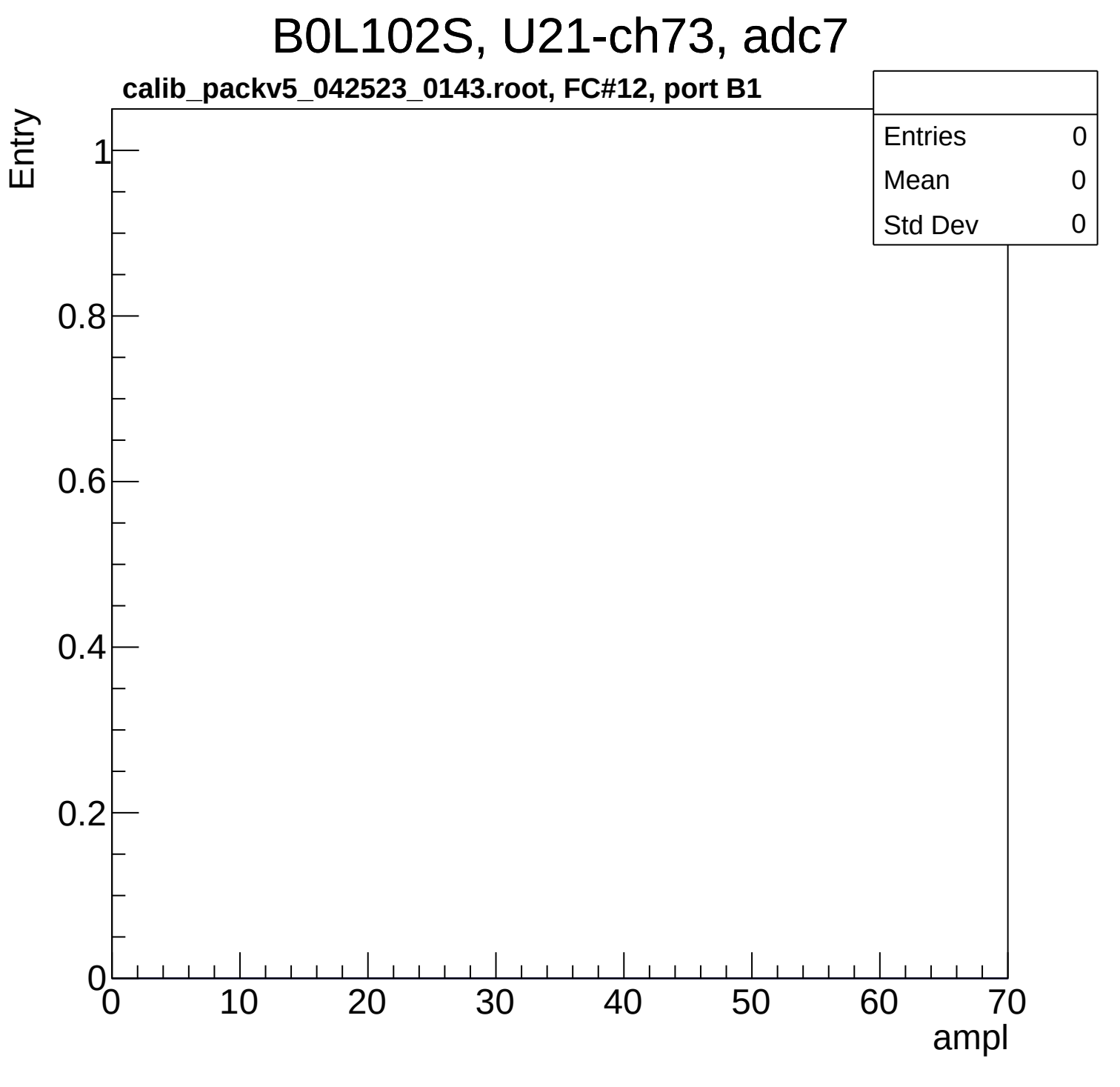
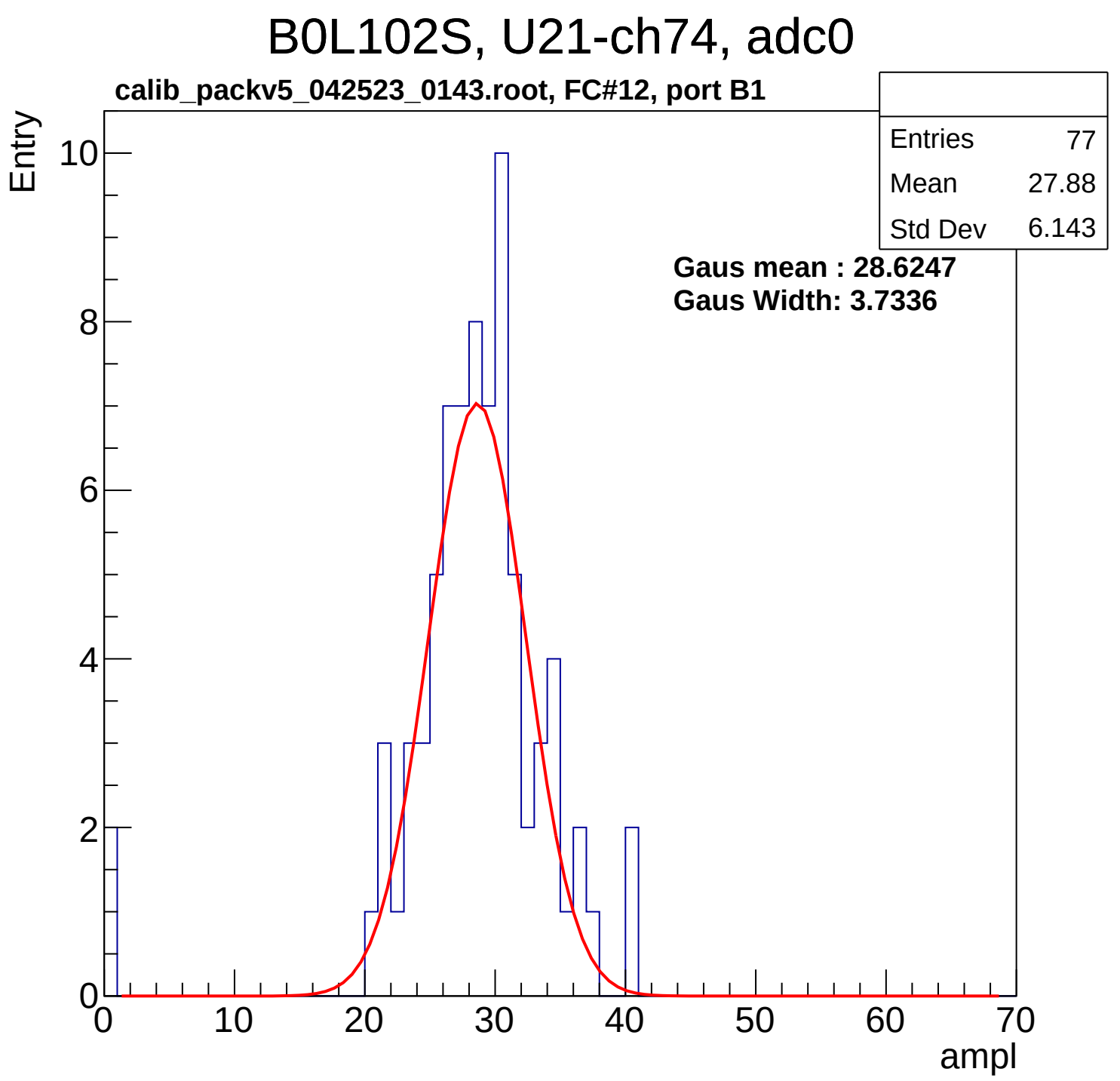
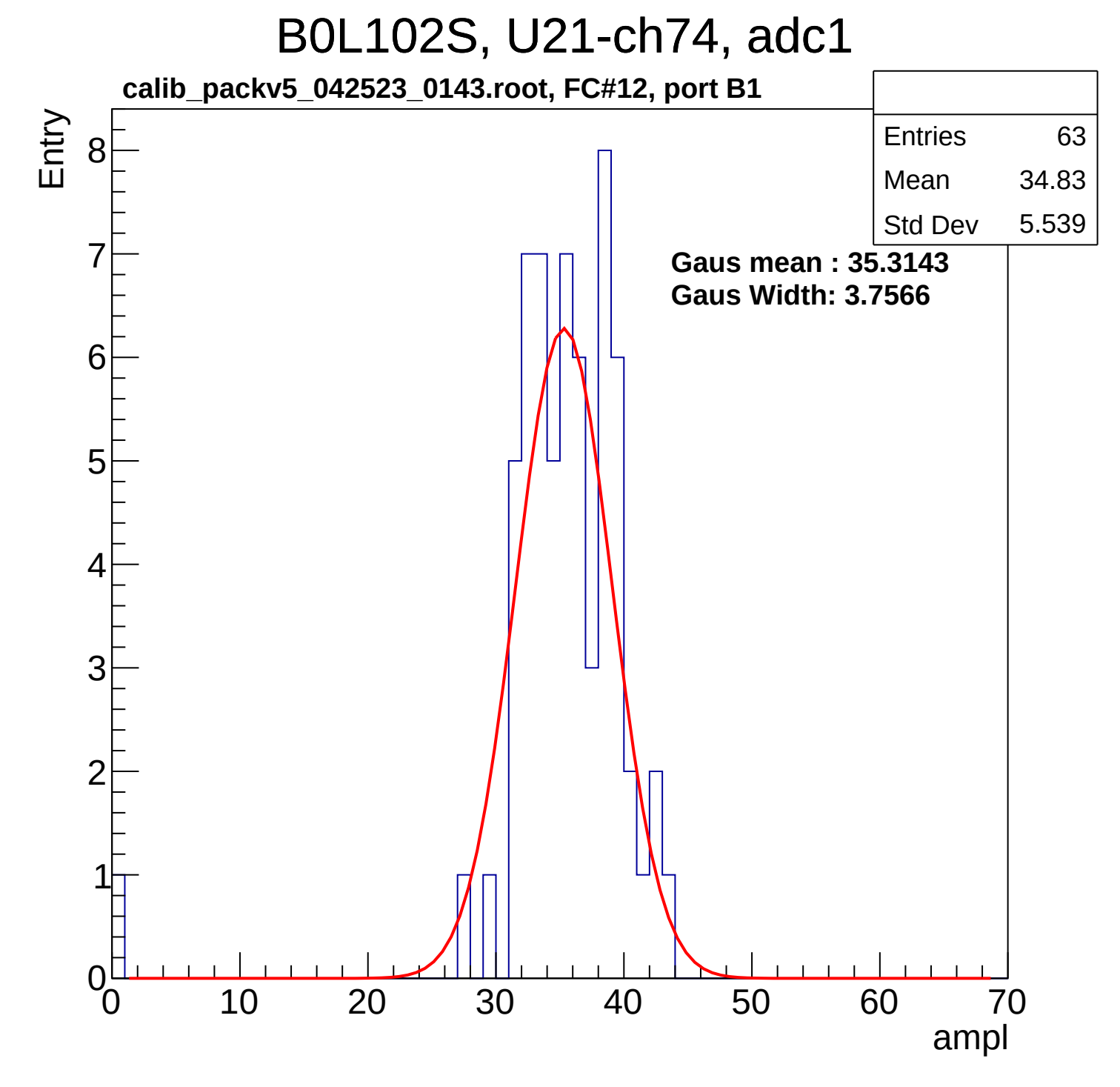
50
60
70
ampl


B0L102S, U21-ch73, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch74, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 27.88
Std Dev 6.143
Gaus mean : 28.6247
Gaus Width: 3.7336
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch74, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 34.83
Std Dev 5.539
Gaus mean : 35.3143
Gaus Width: 3.7566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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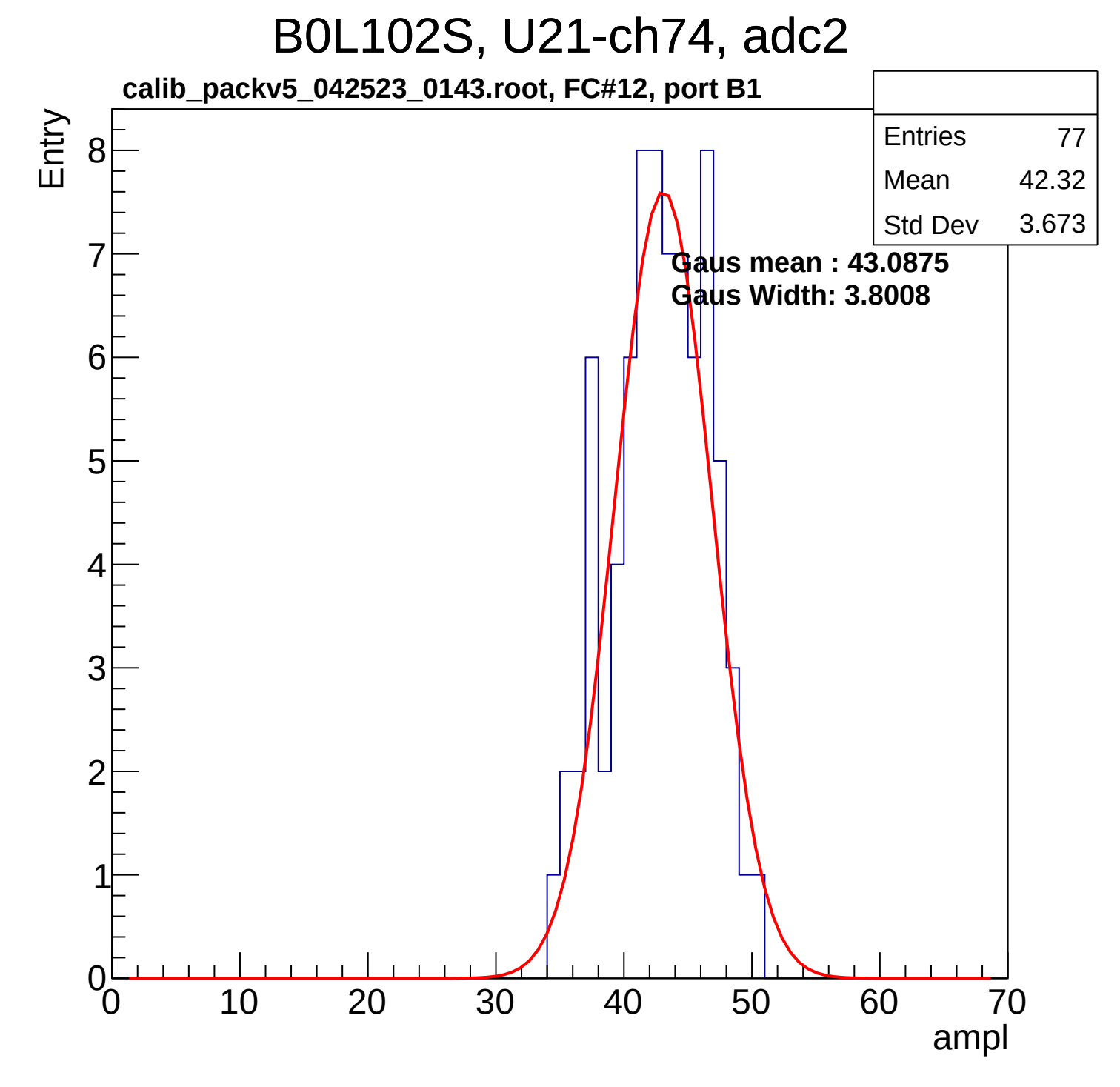
B0L102S, U21-ch74, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 42.32
Std Dev 3.673
Gaus mean : 43.0875
Gaus Width: 3.8008
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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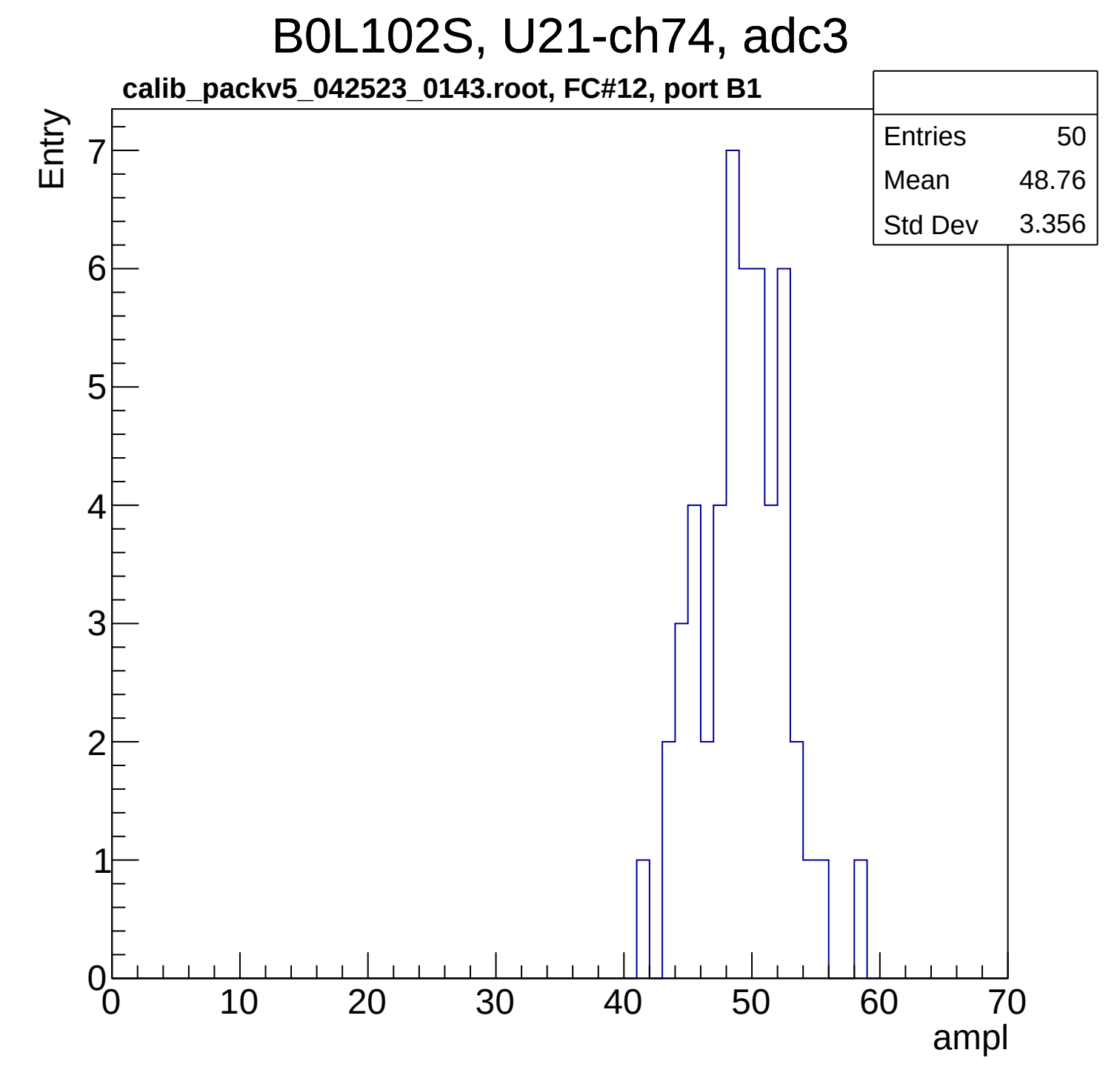
B0L102S, U21-ch74, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 48.76
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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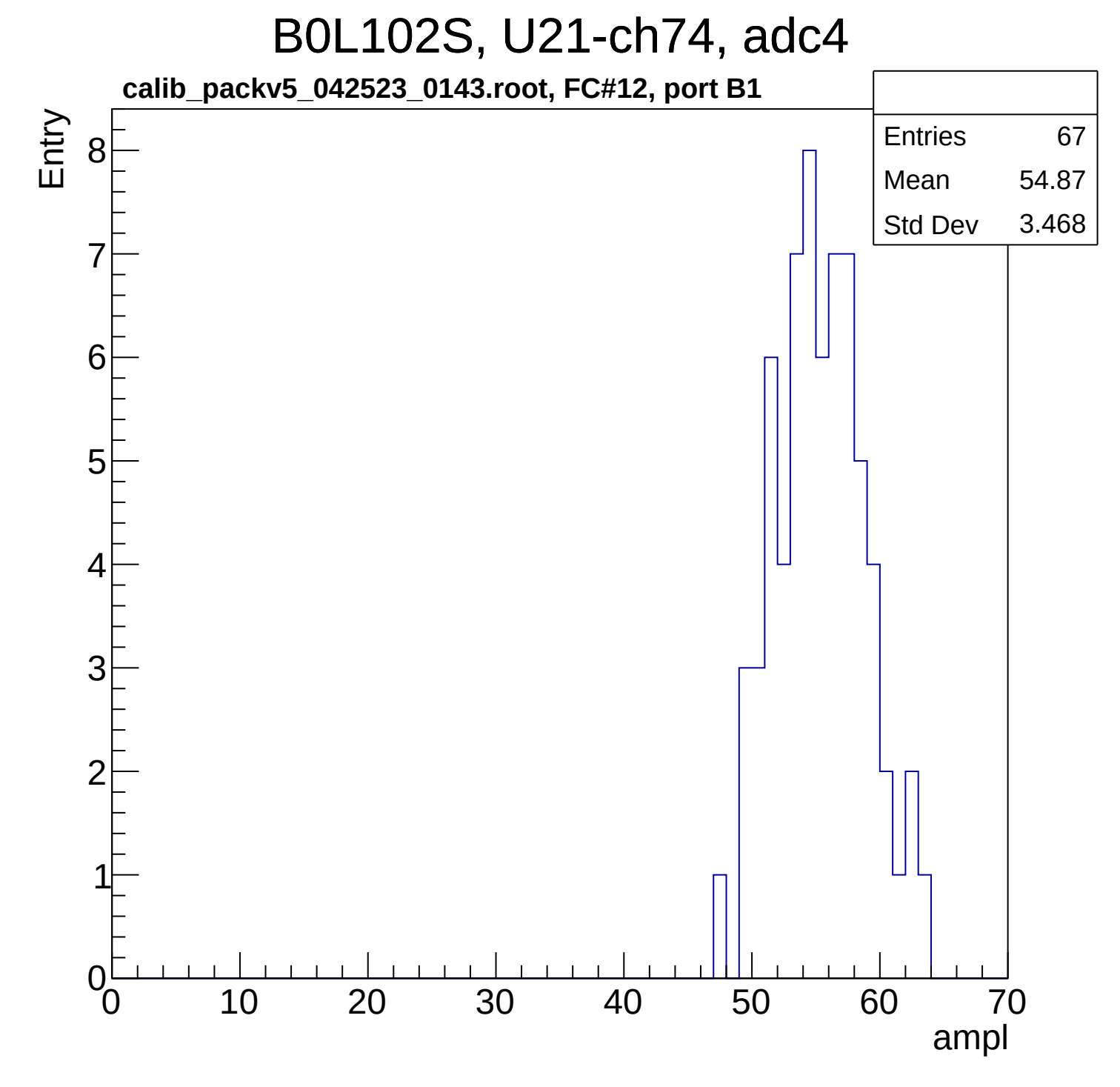
B0L102S, U21-ch74, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 54.87
Std Dev 3.468
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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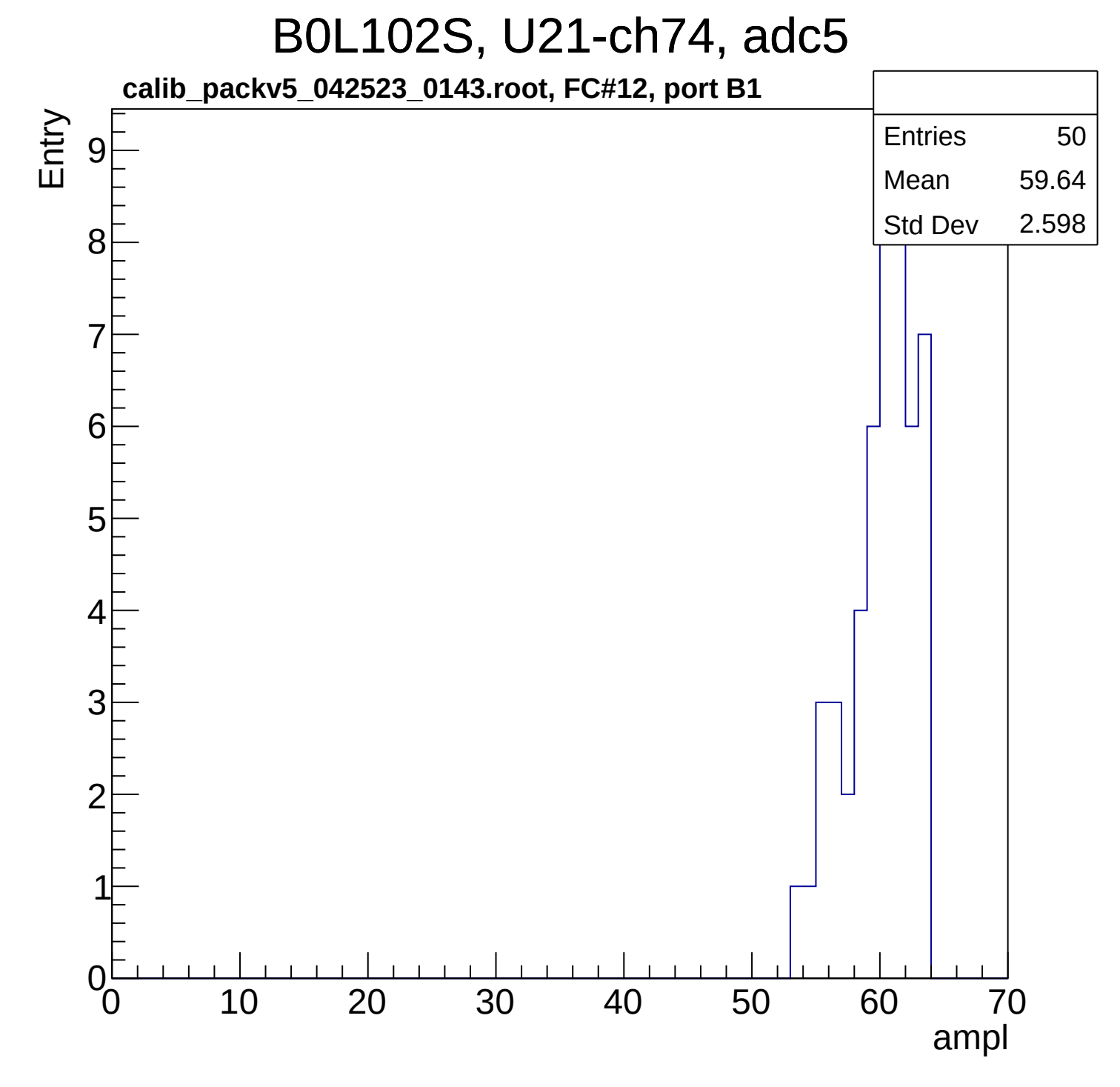
B0L102S, U21-ch74, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 59.64
Std Dev 2.598
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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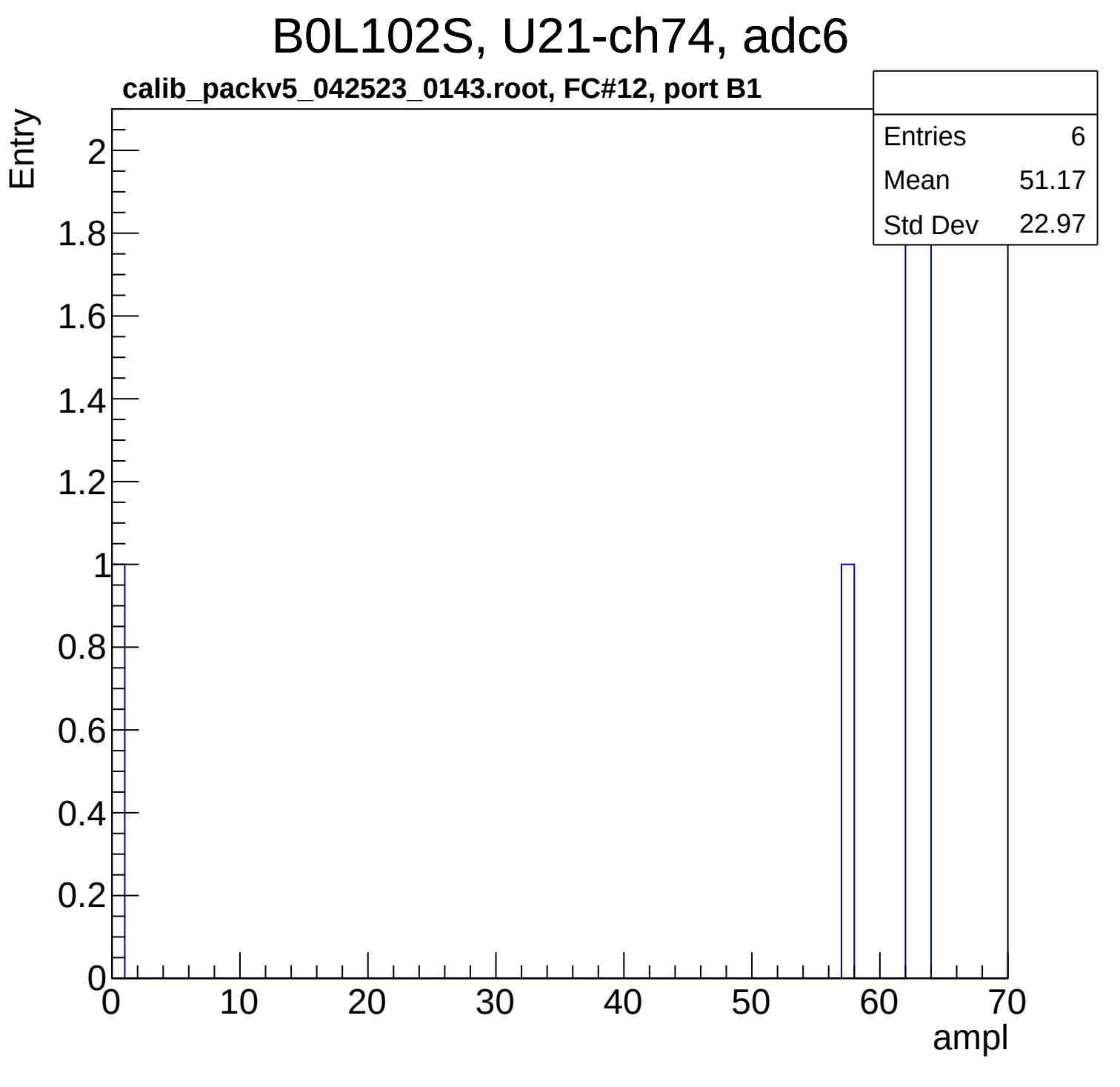
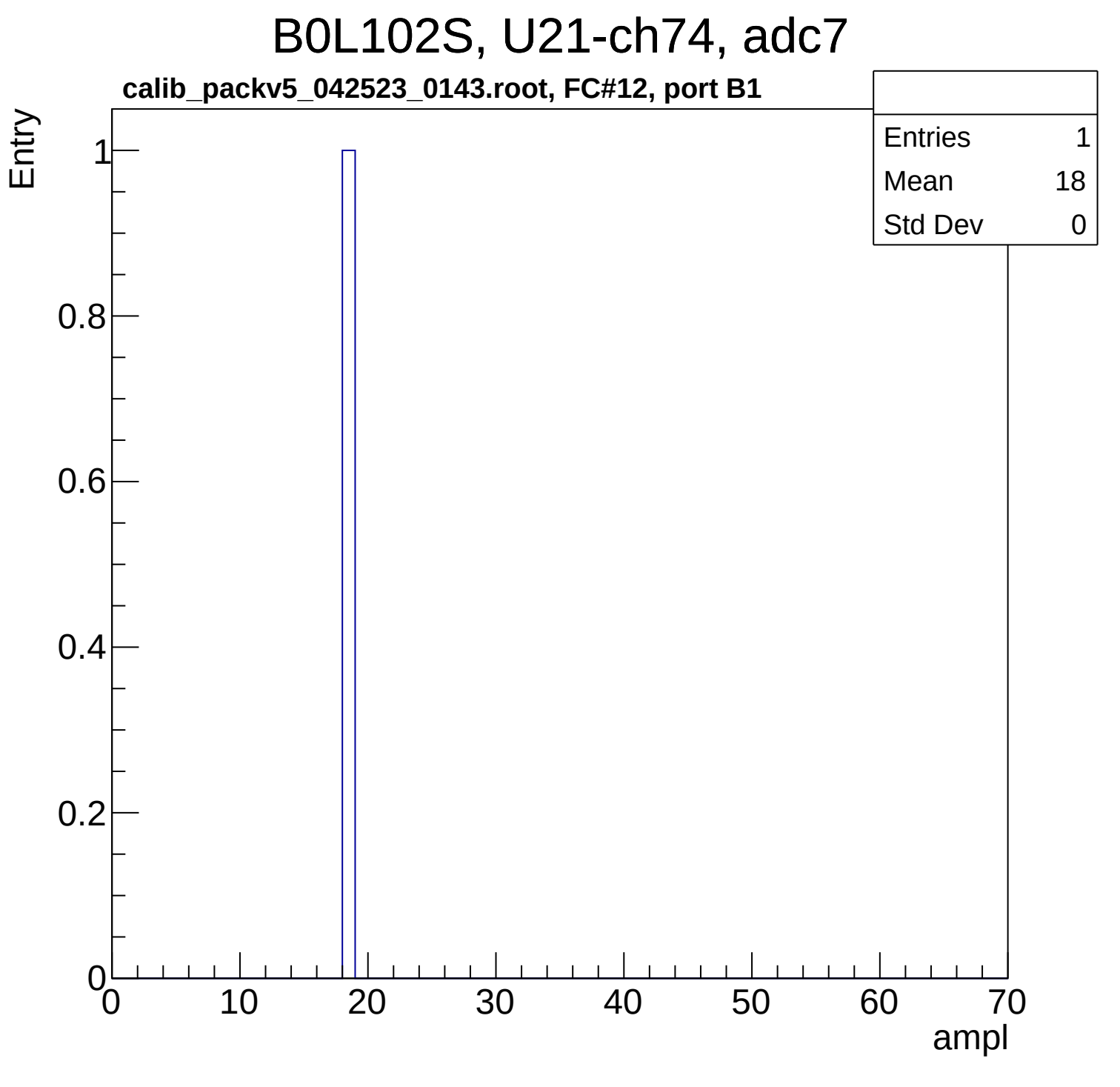
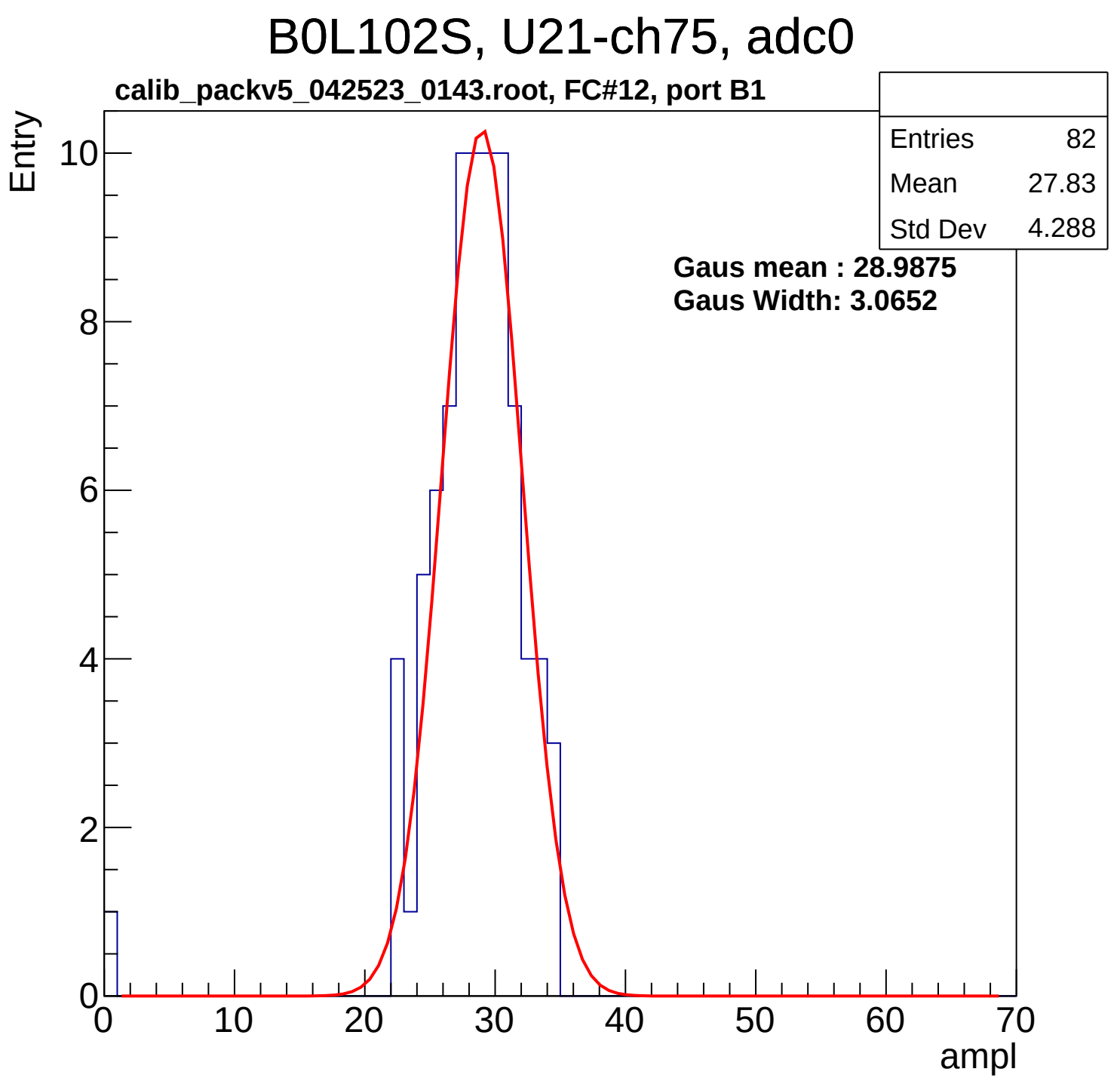
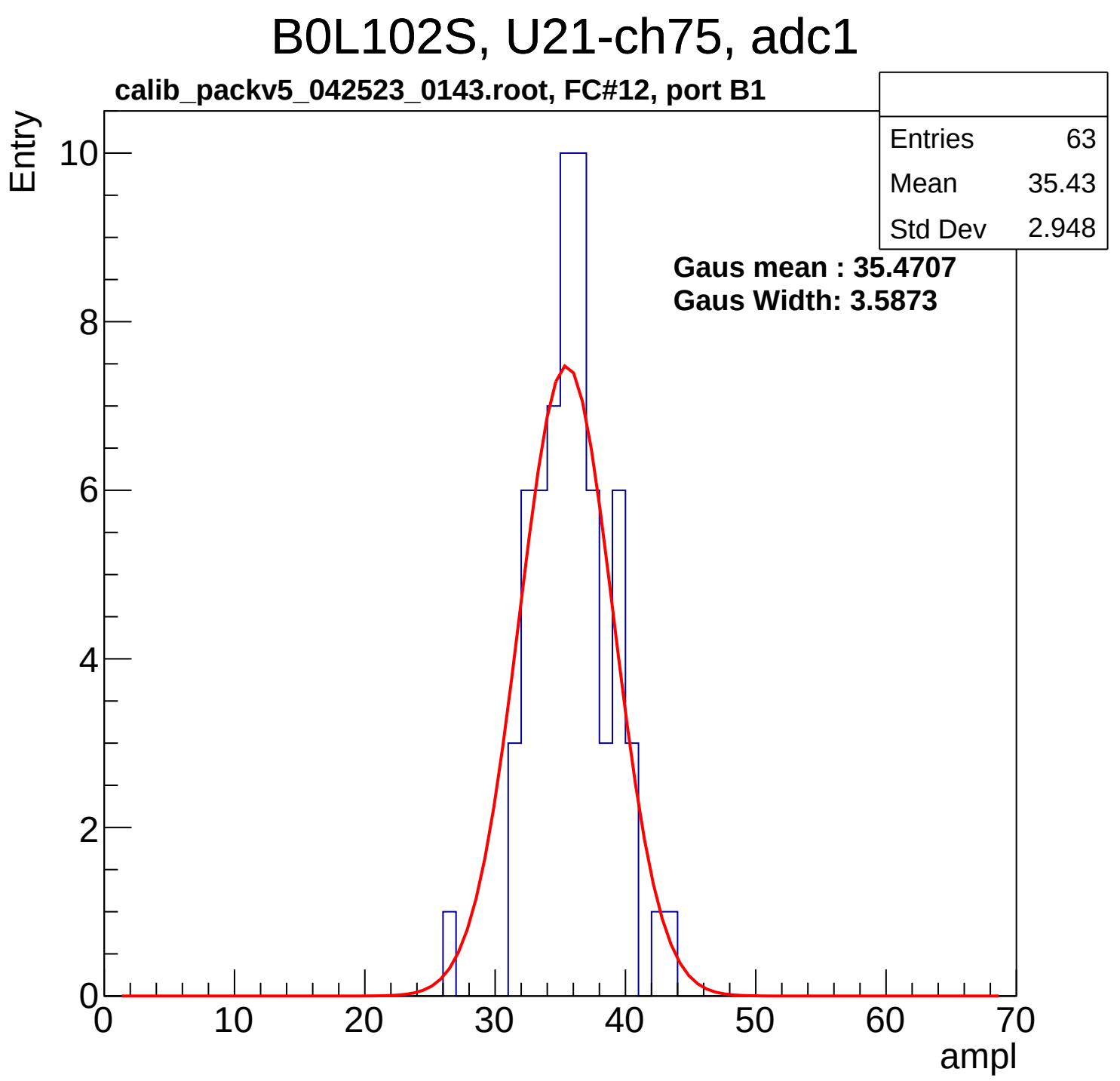
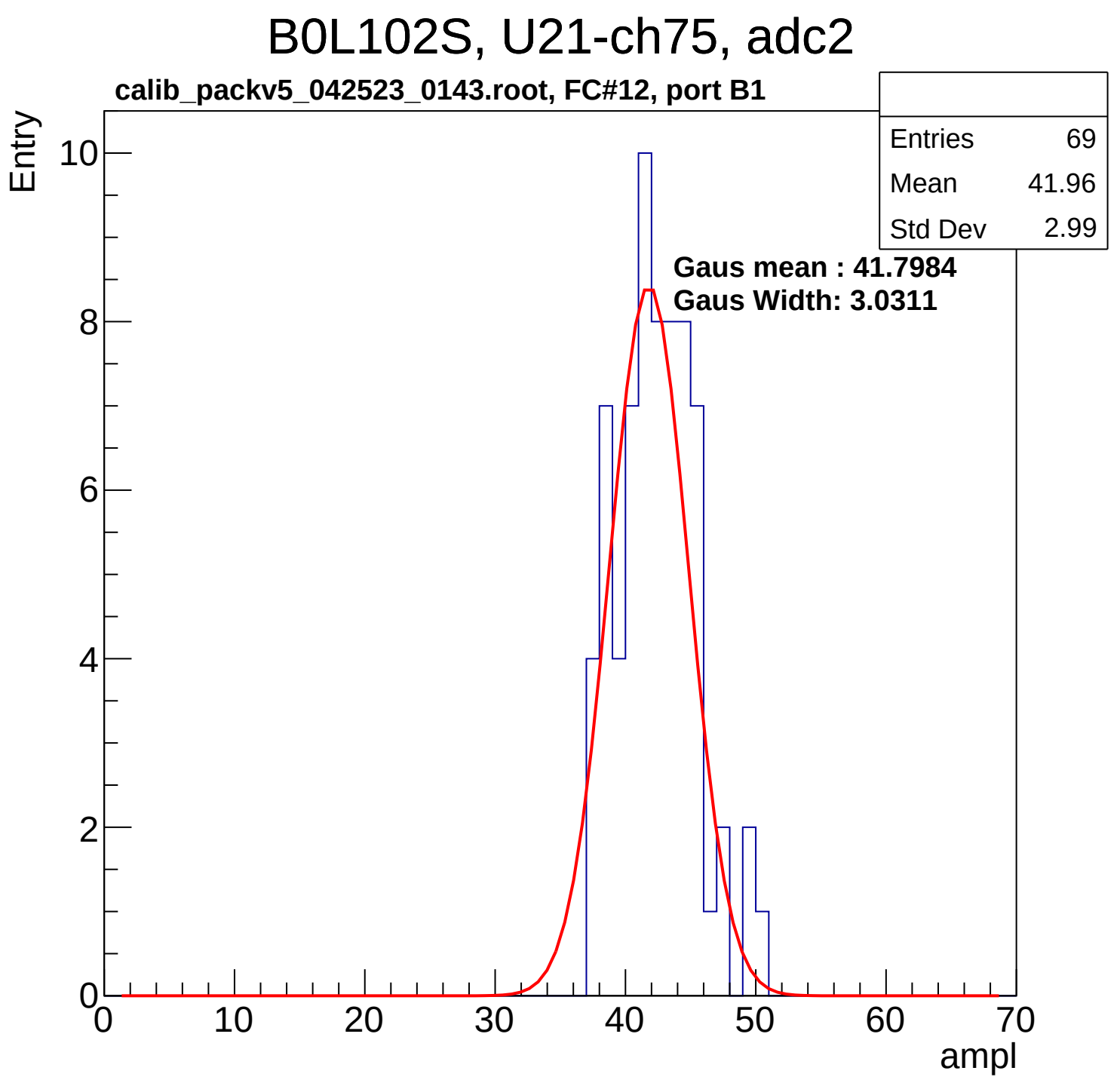
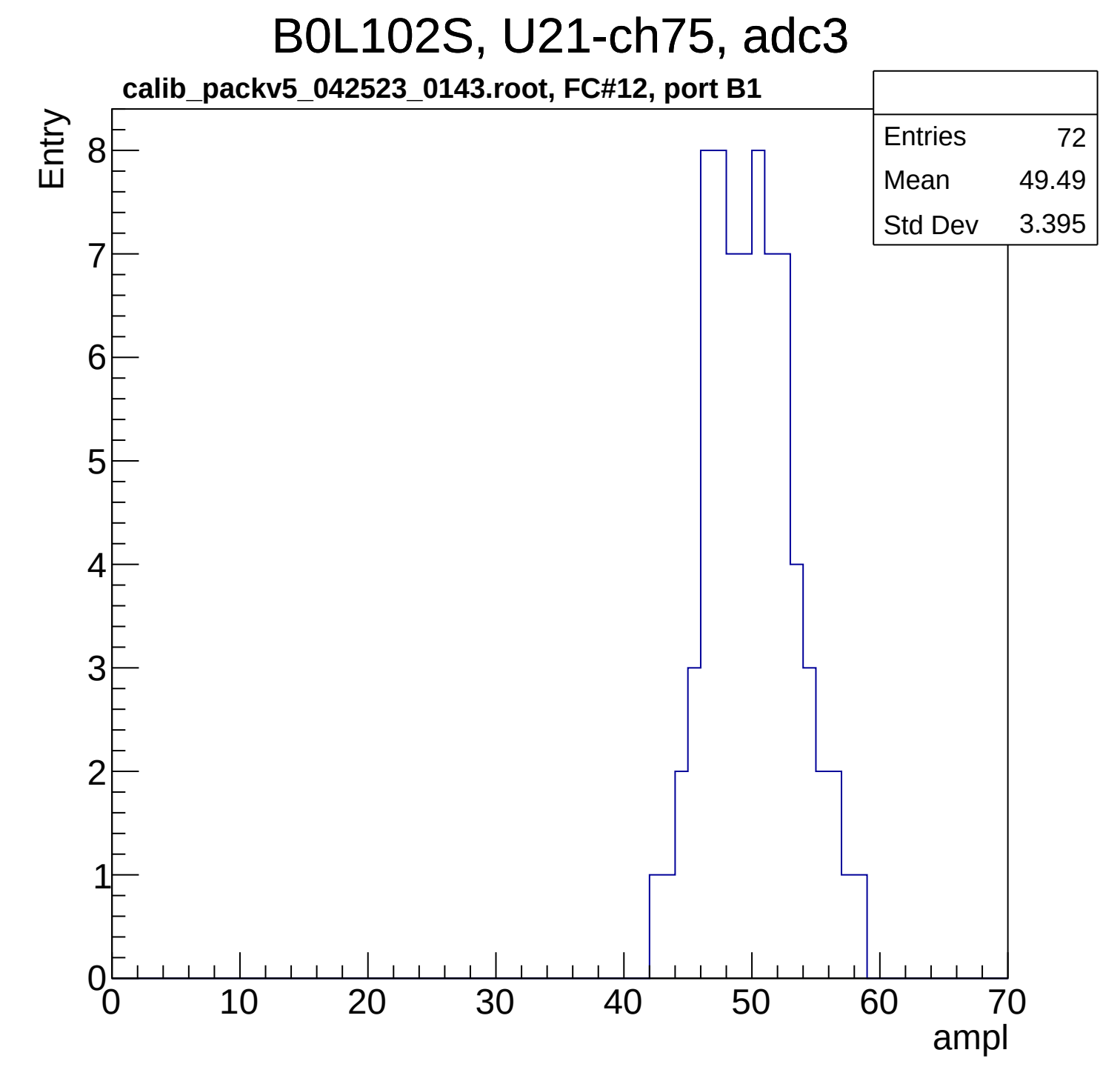

B0L102S, U21-ch74, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 51.17
Std Dev 22.97
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

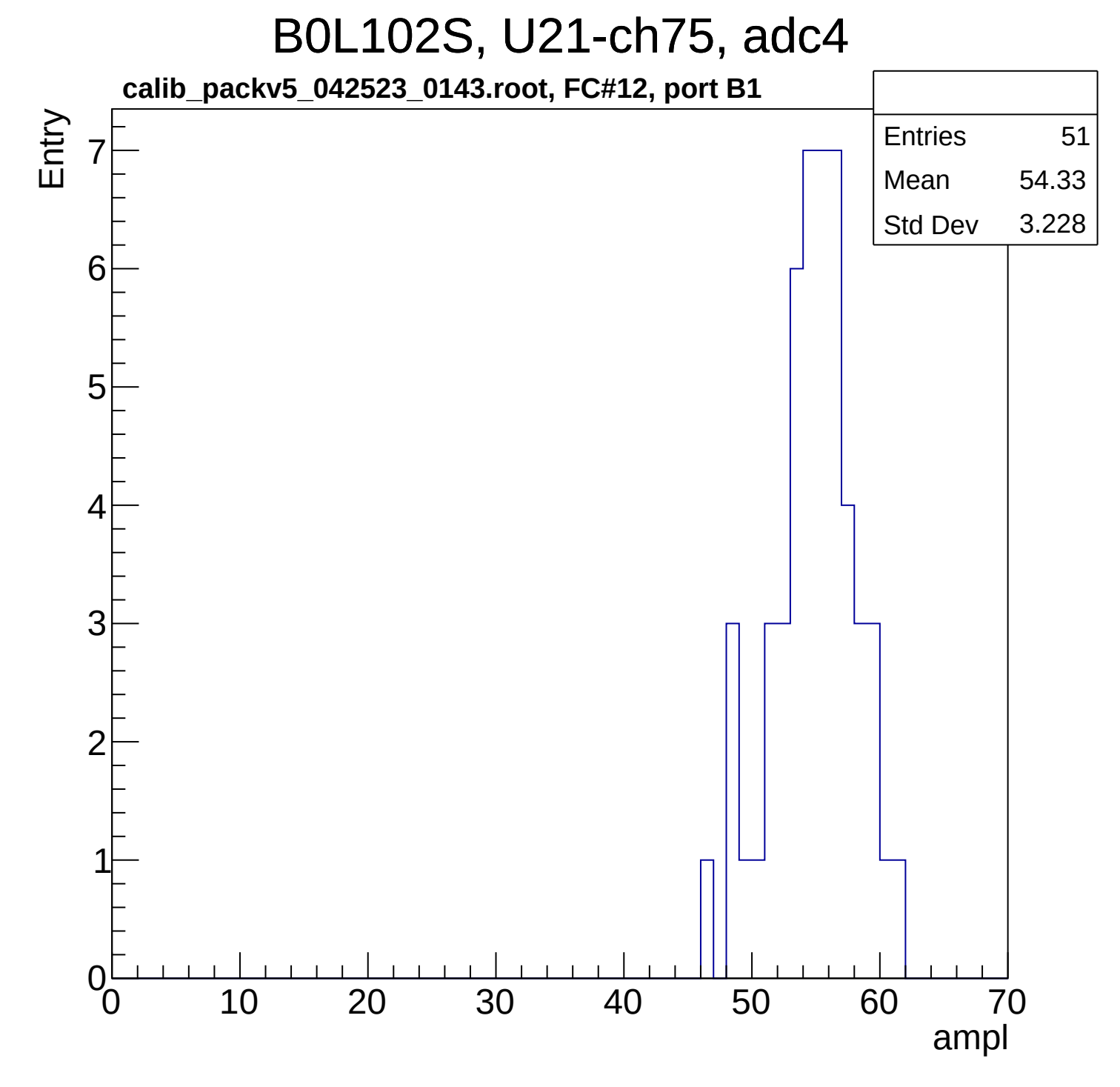

B0L102S, U21-ch74, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

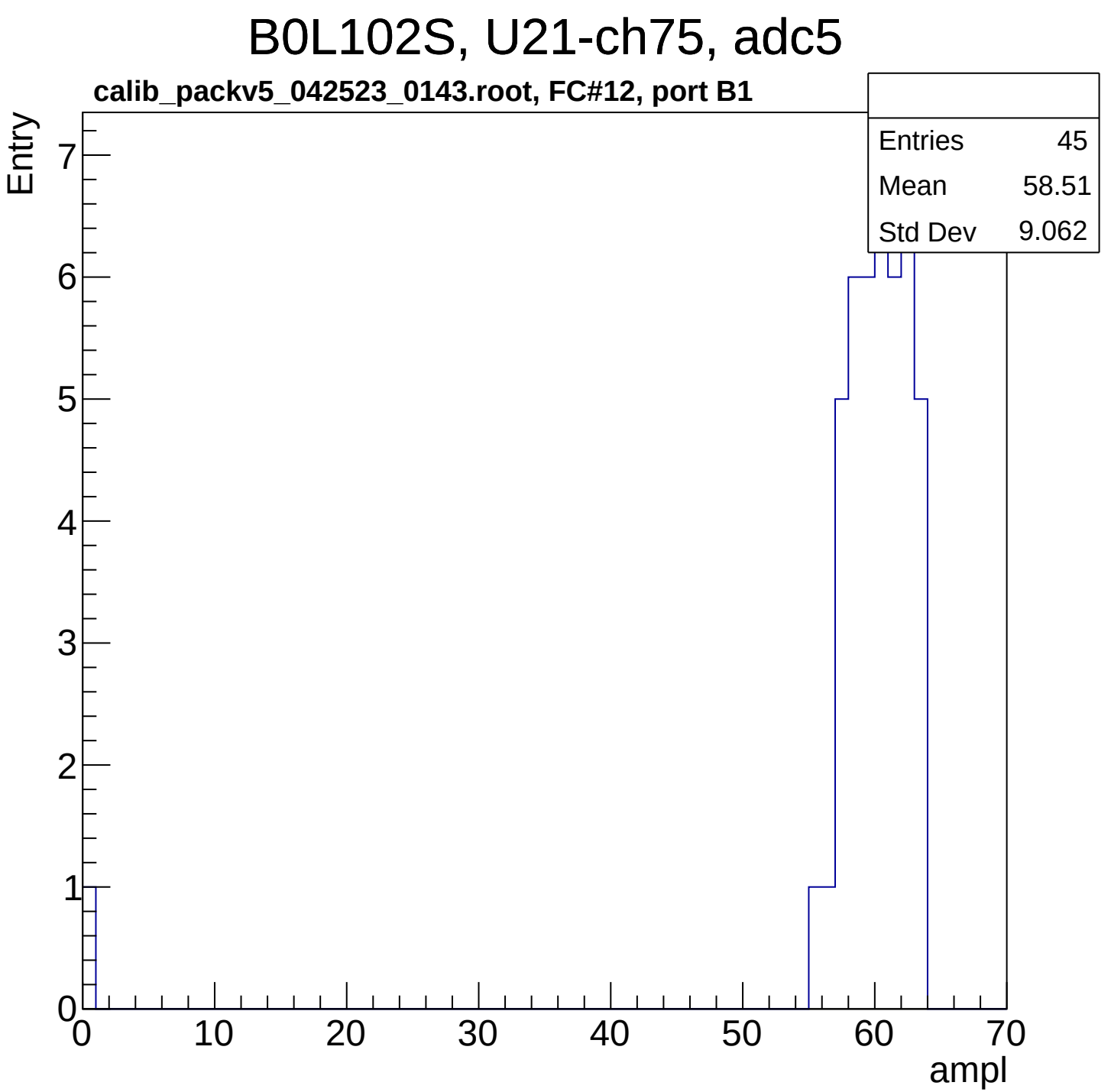

B0L102S, U21-ch75, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 27.83
Std Dev 4.288
Gaus mean : 28.9875
Gaus Width: 3.0652
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

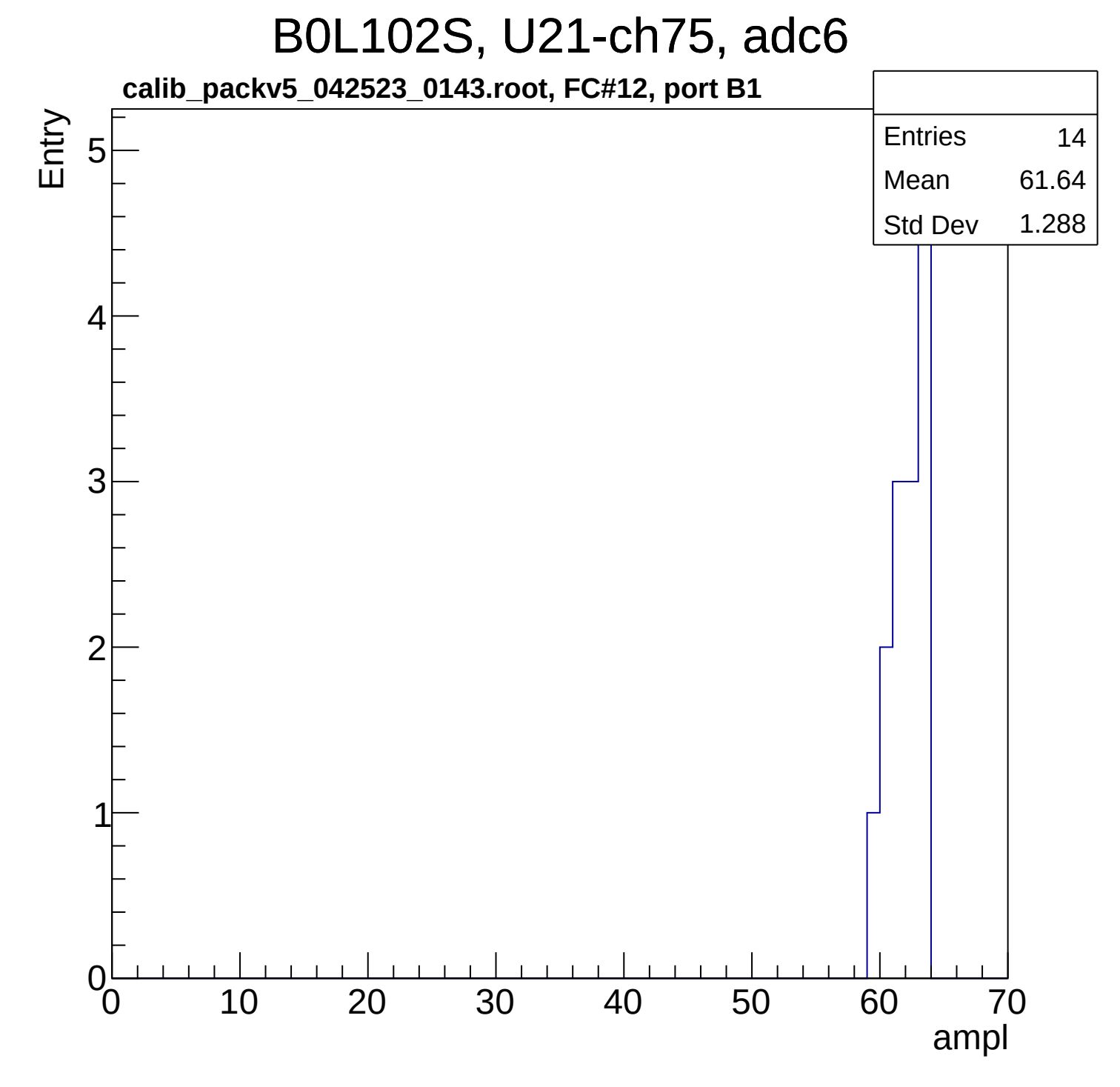

B0L102S, U21-ch75, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 35.43
Std Dev 2.948
Gaus mean : 35.4707
Gaus Width: 3.5873
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

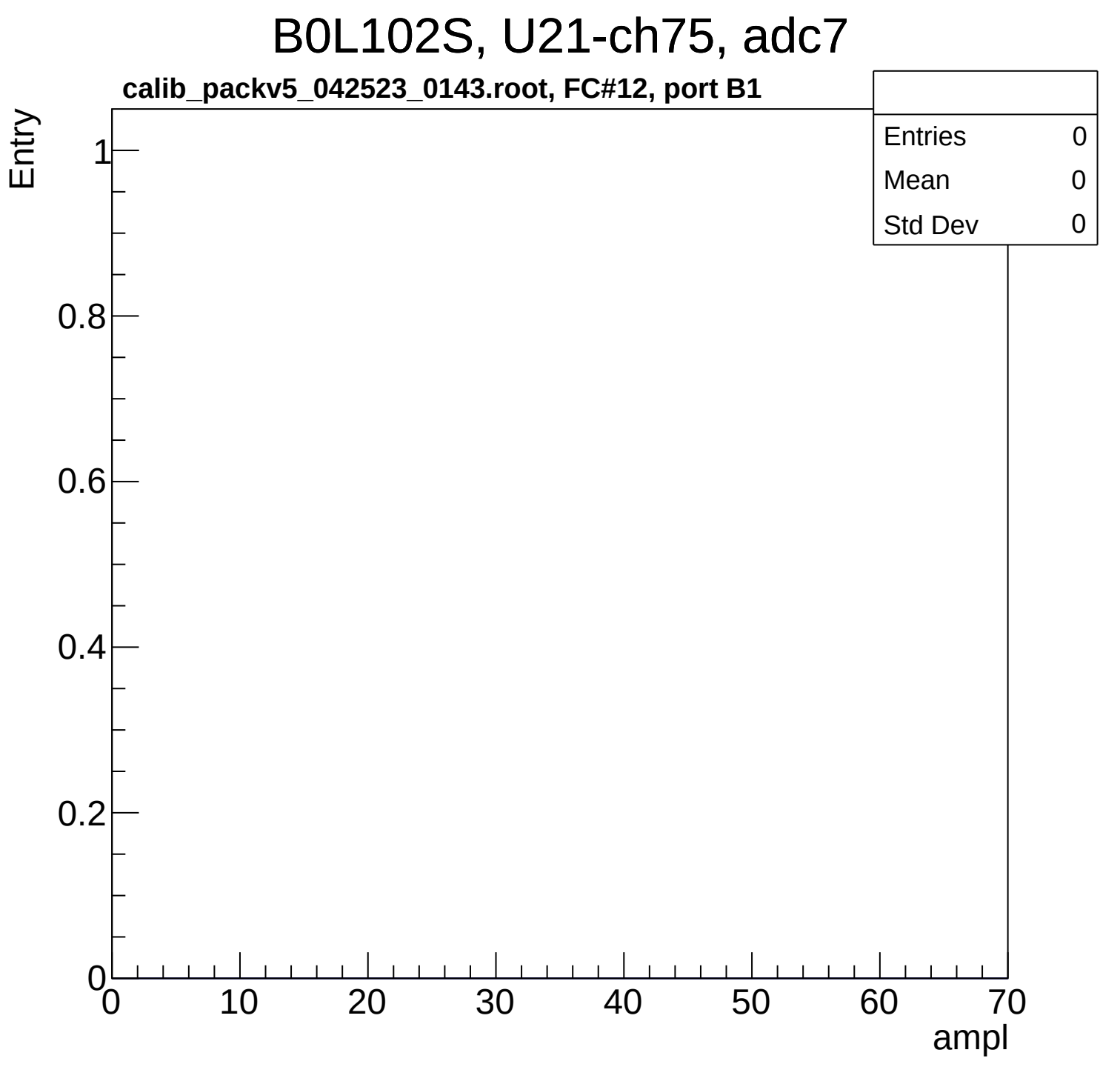

B0L102S, U21-ch75, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 41.96
Std Dev 2.99
Gaus mean : 41.7984
Gaus Width: 3.0311
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

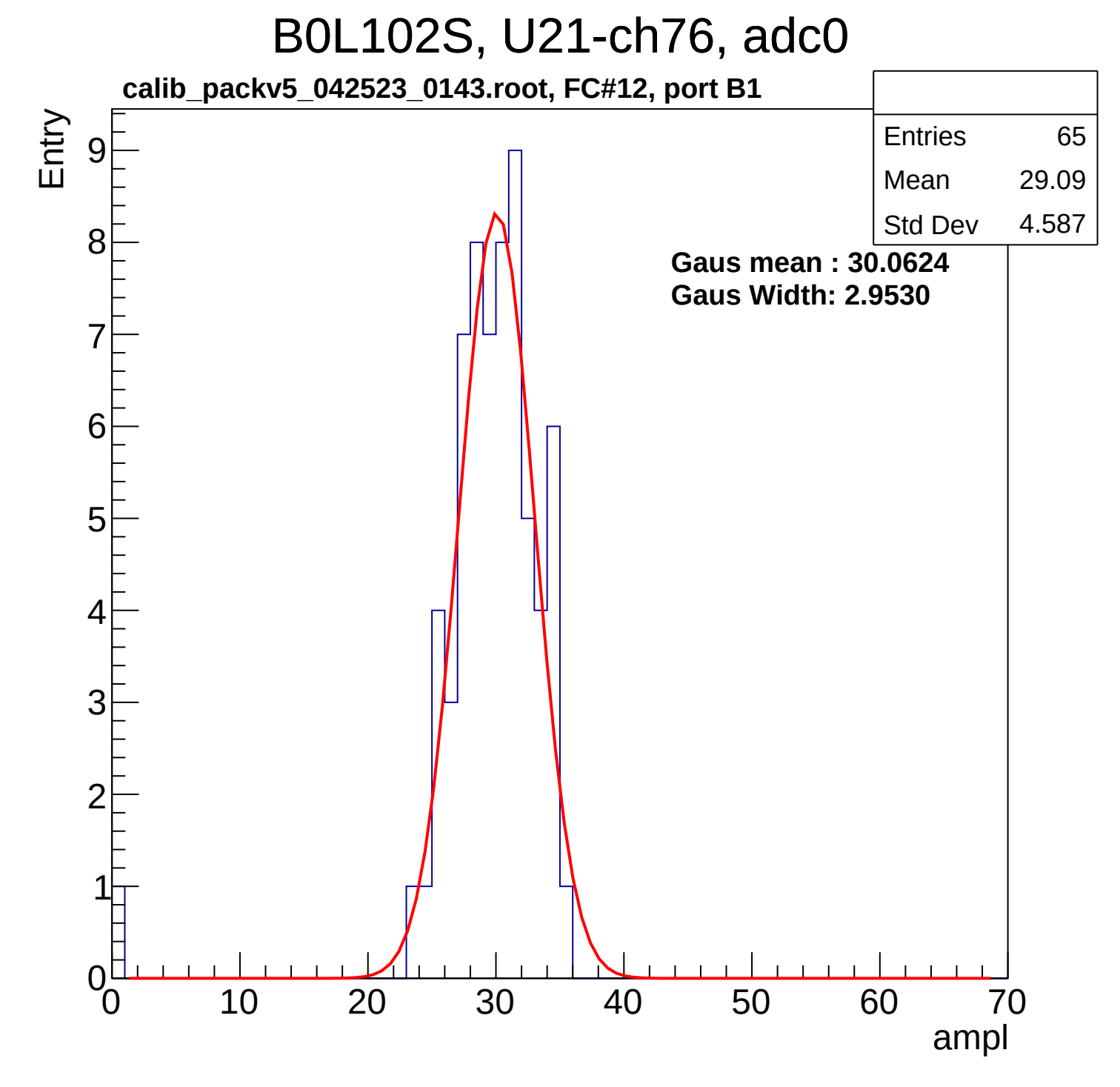

B0L102S, U21-ch75, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 49.49
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

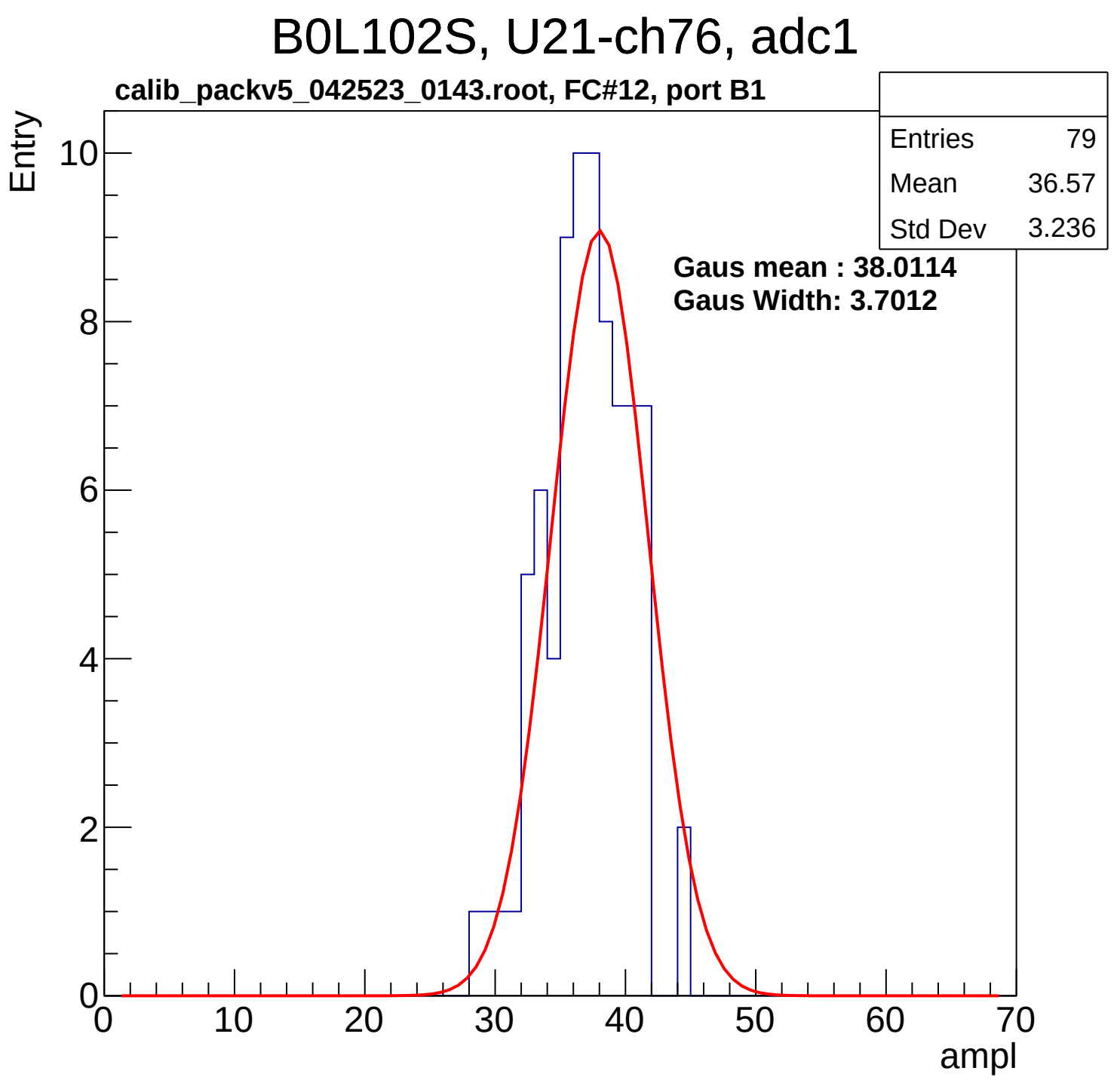

B0L102S, U21-ch75, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 54.33
Std Dev 3.228
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

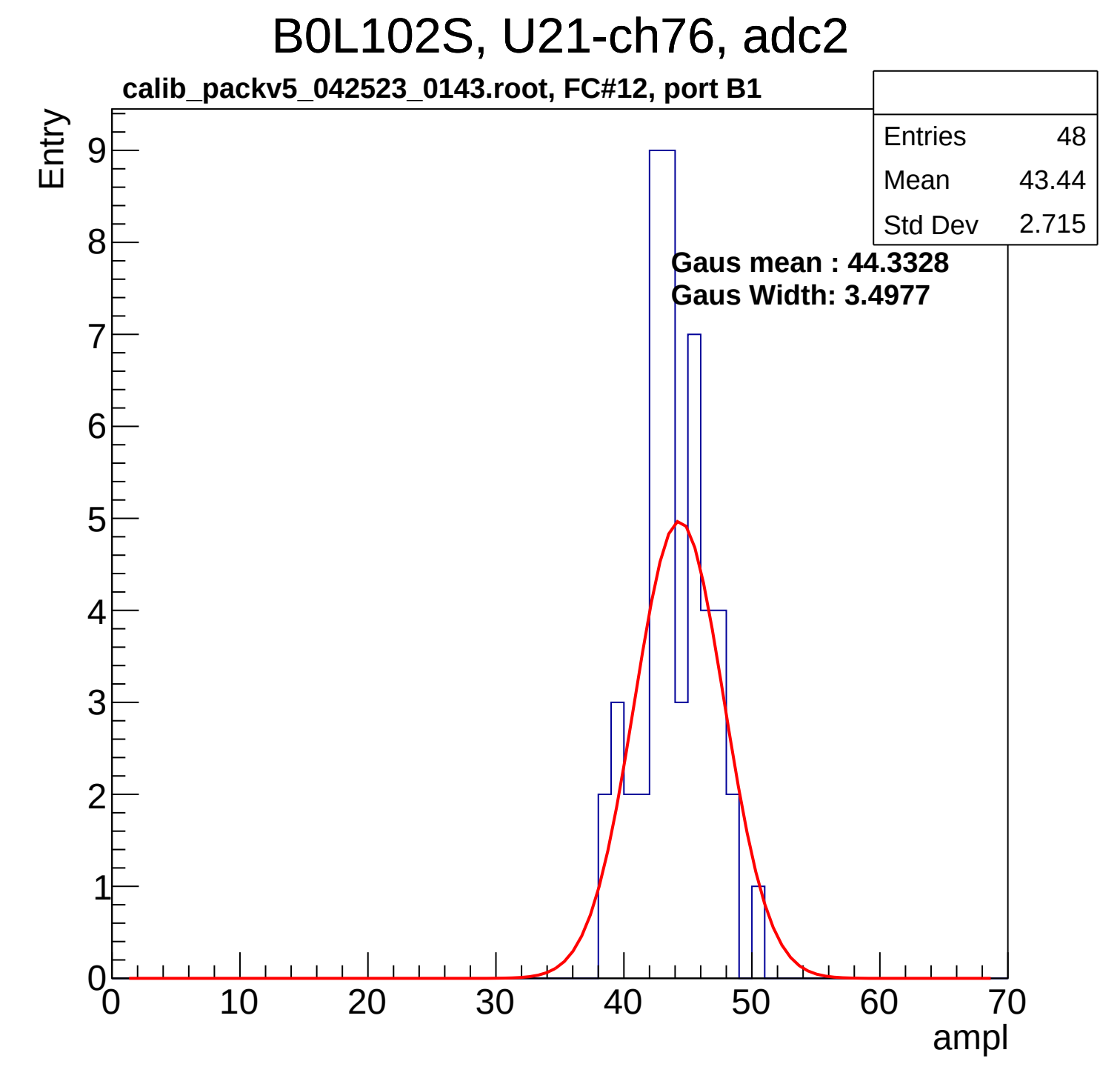

B0L102S, U21-ch75, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.51
Std Dev 9.062
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

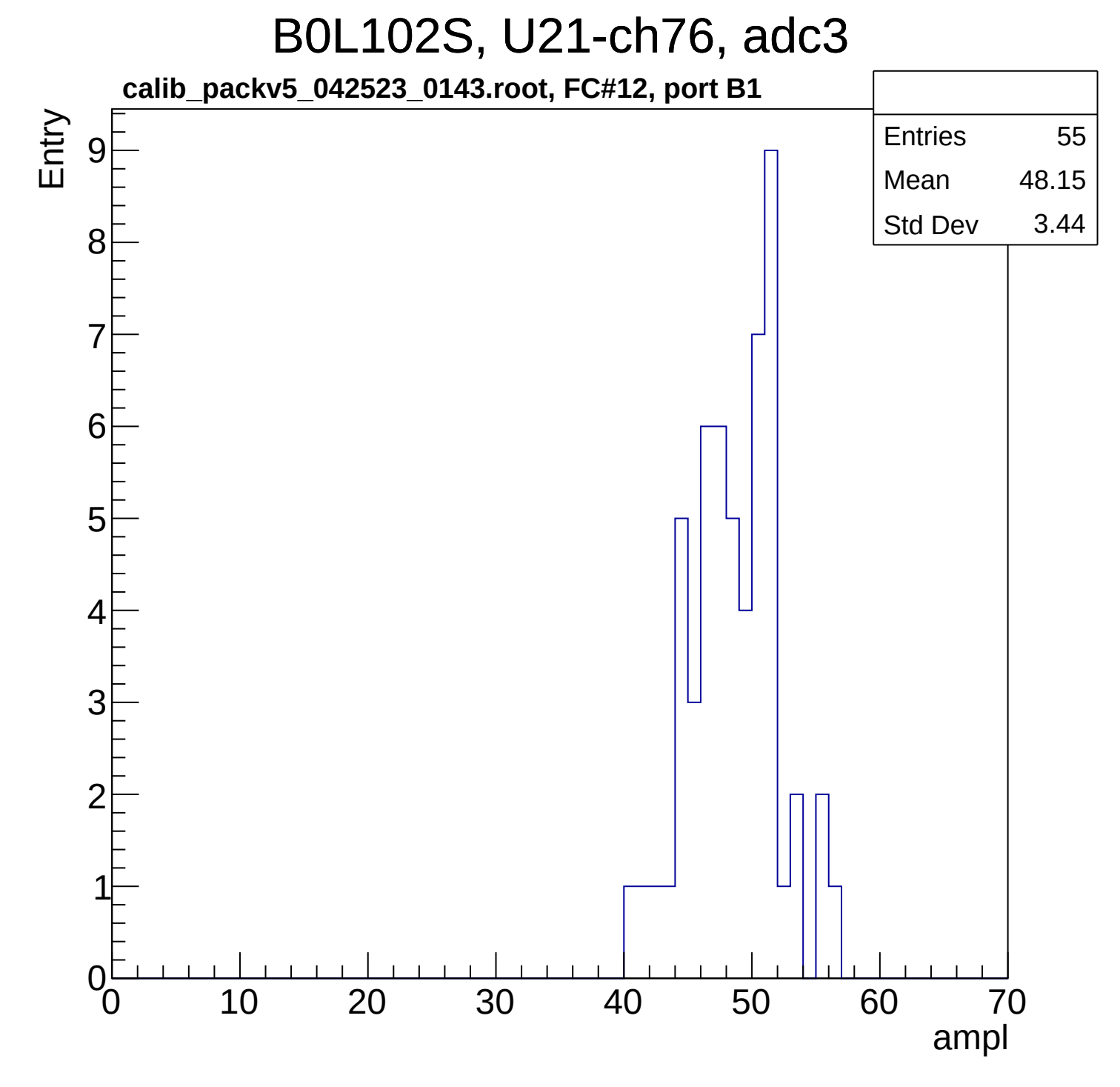

B0L102S, U21-ch75, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.64
Std Dev 1.288
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch75, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch76, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 29.09
Std Dev 4.587
Gaus mean : 30.0624
Gaus Width: 2.9530
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch76, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 36.57
Std Dev 3.236
Gaus mean : 38.0114
Gaus Width: 3.7012
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch76, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 43.44
Std Dev 2.715
Gaus mean : 44.3328
Gaus Width: 3.4977
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch76, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 48.15
Std Dev 3.44
Entry
ampl
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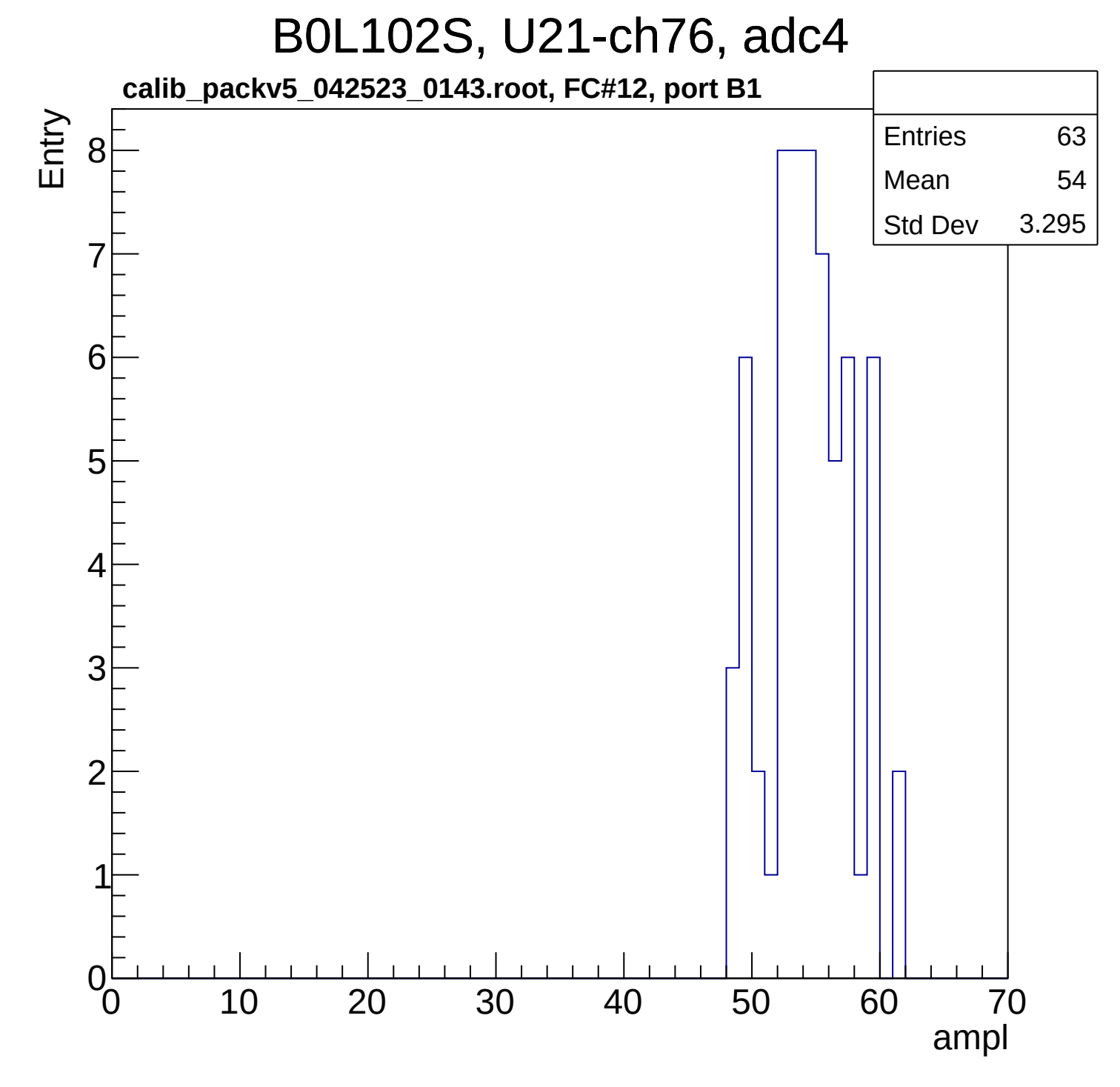
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch76, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54
Std Dev 3.295
Entry
ampl
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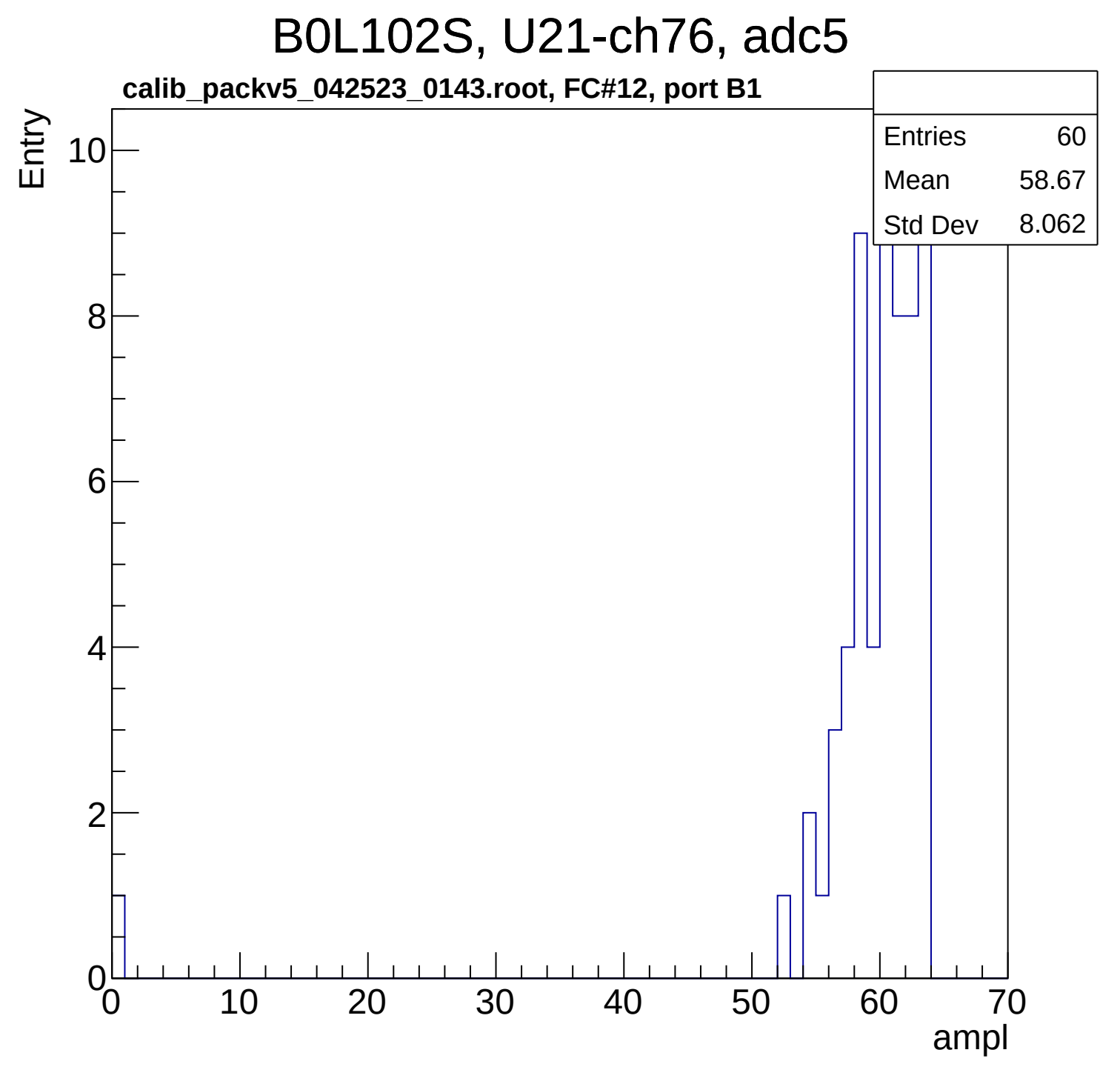
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch76, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.67
Std Dev 8.062
Entry
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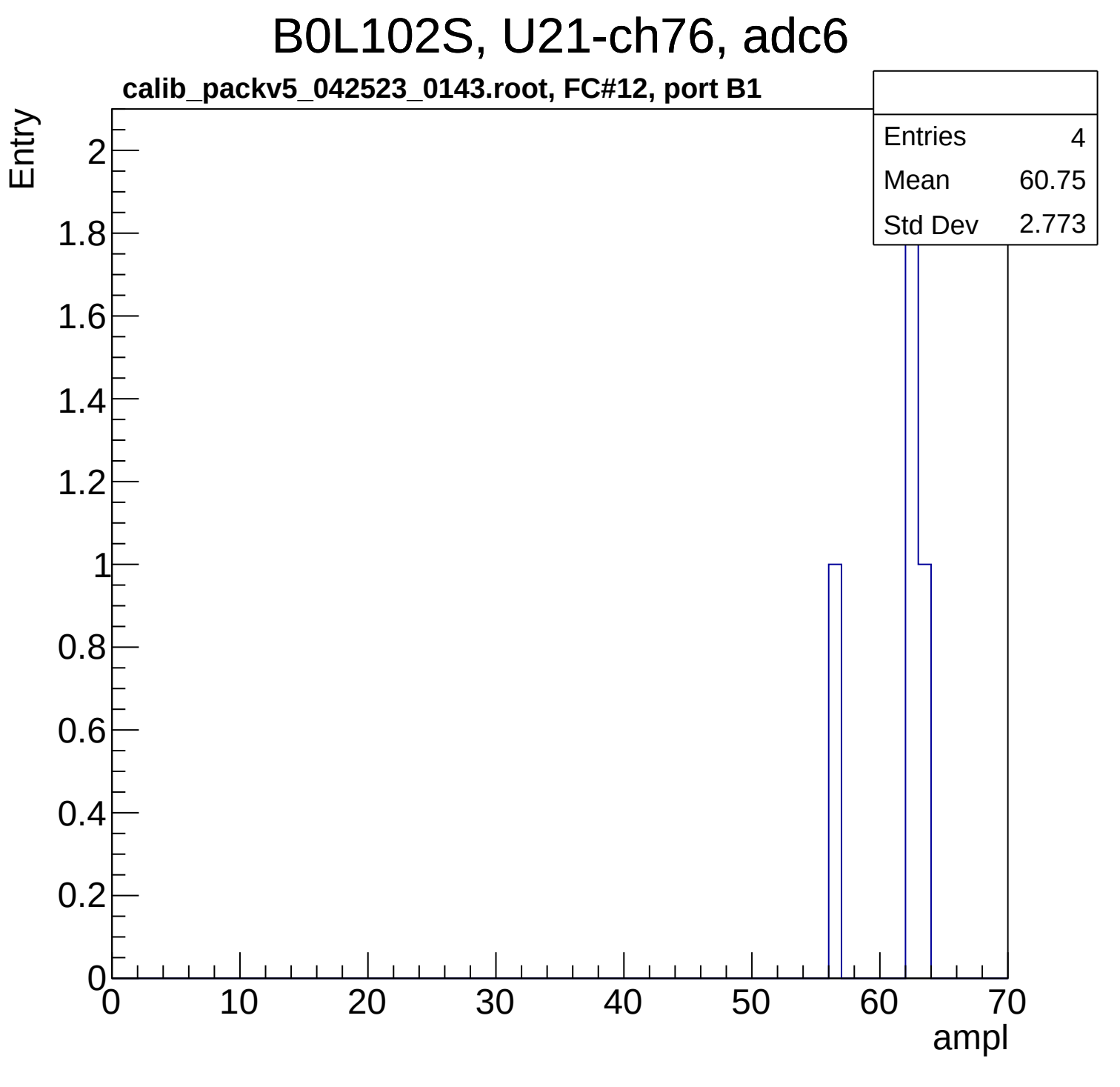
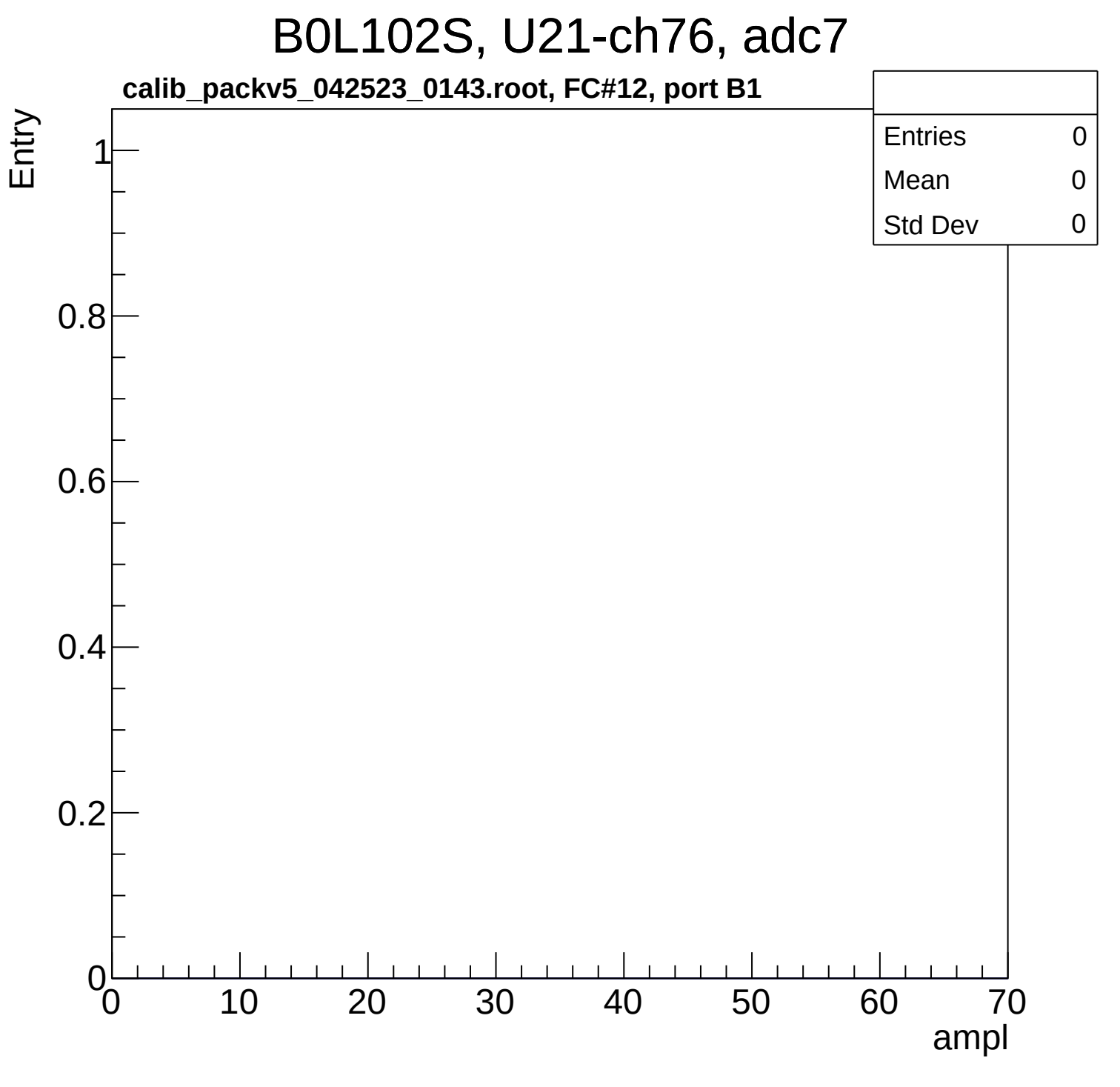
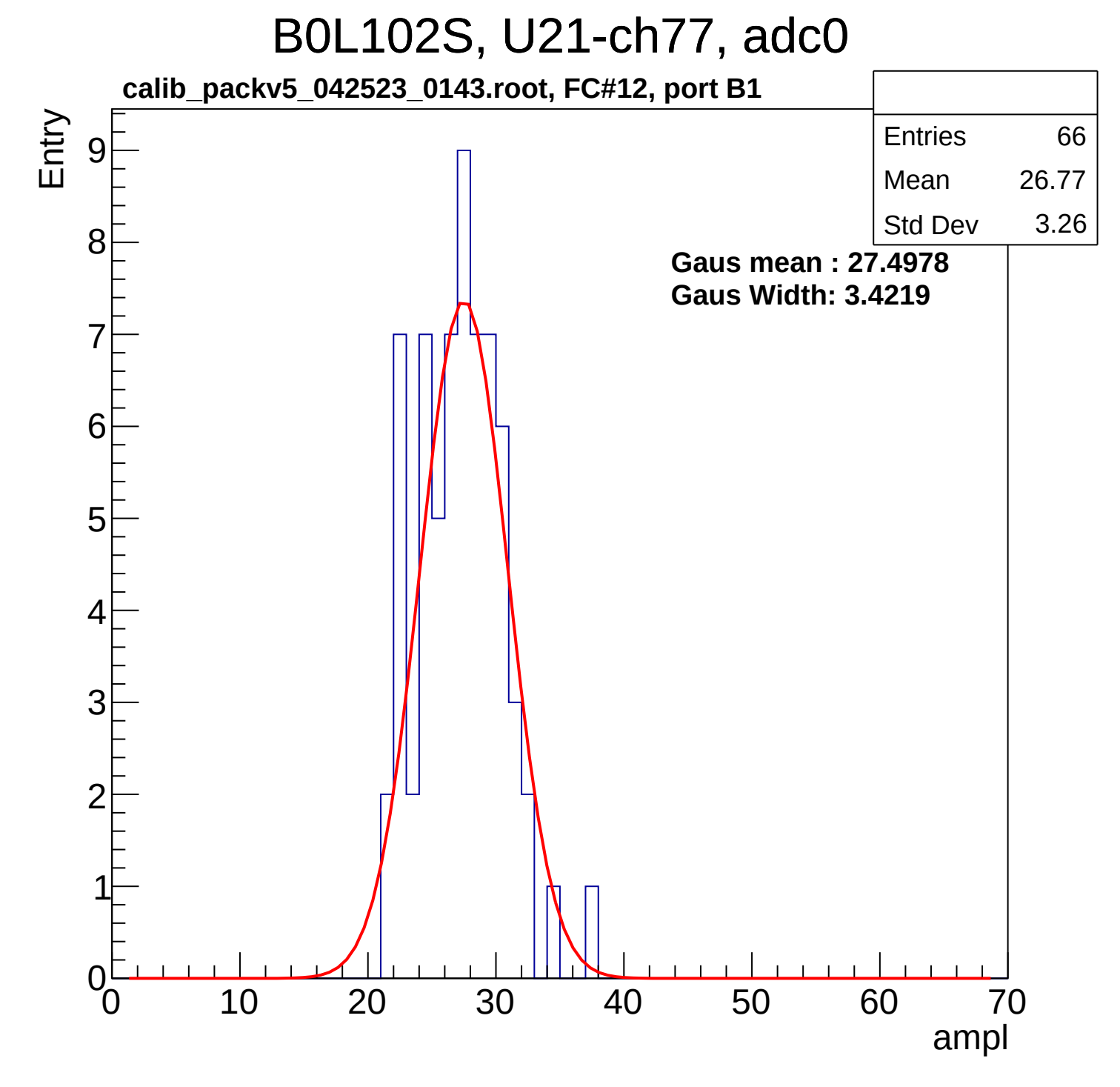
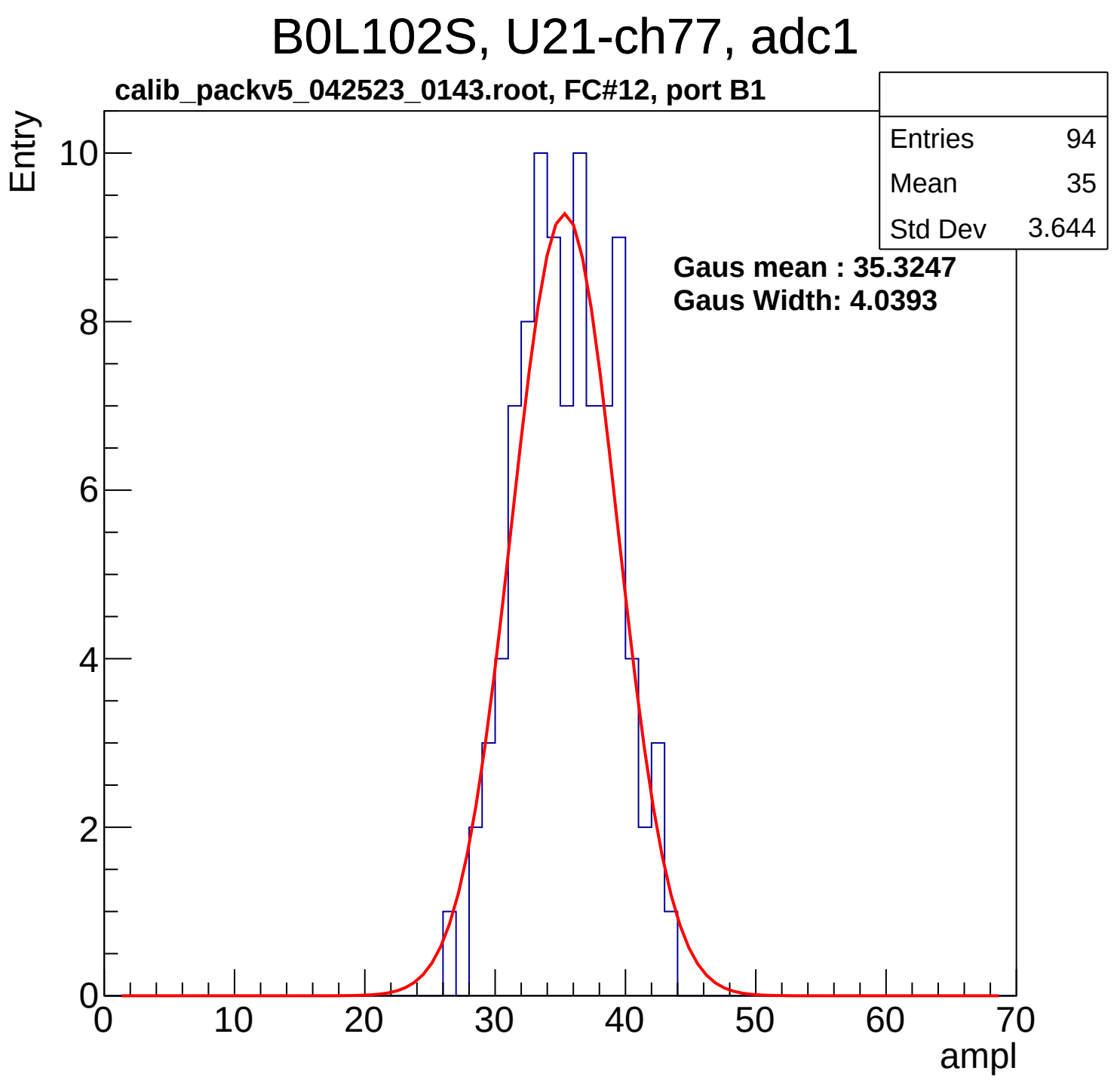
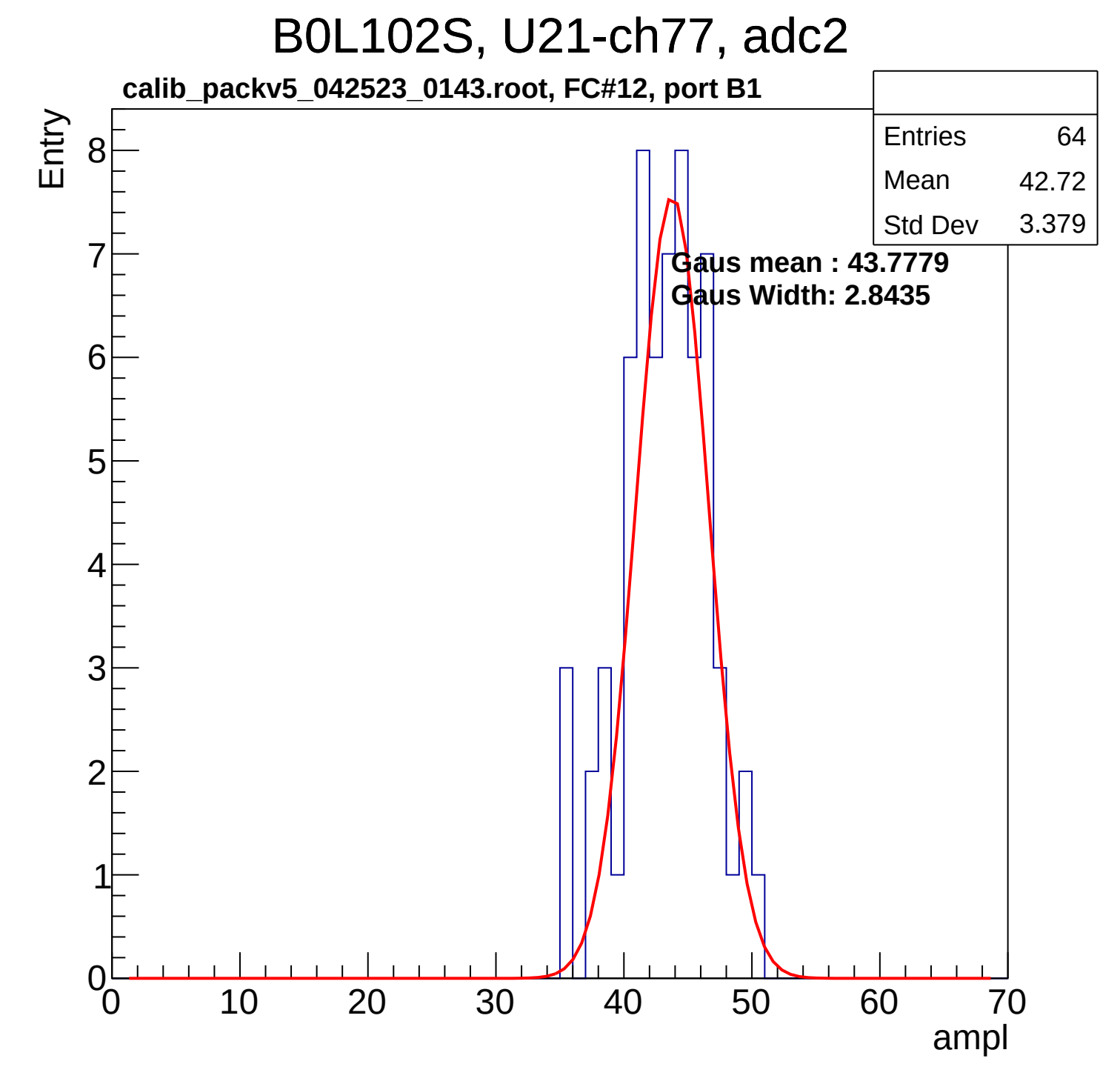
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

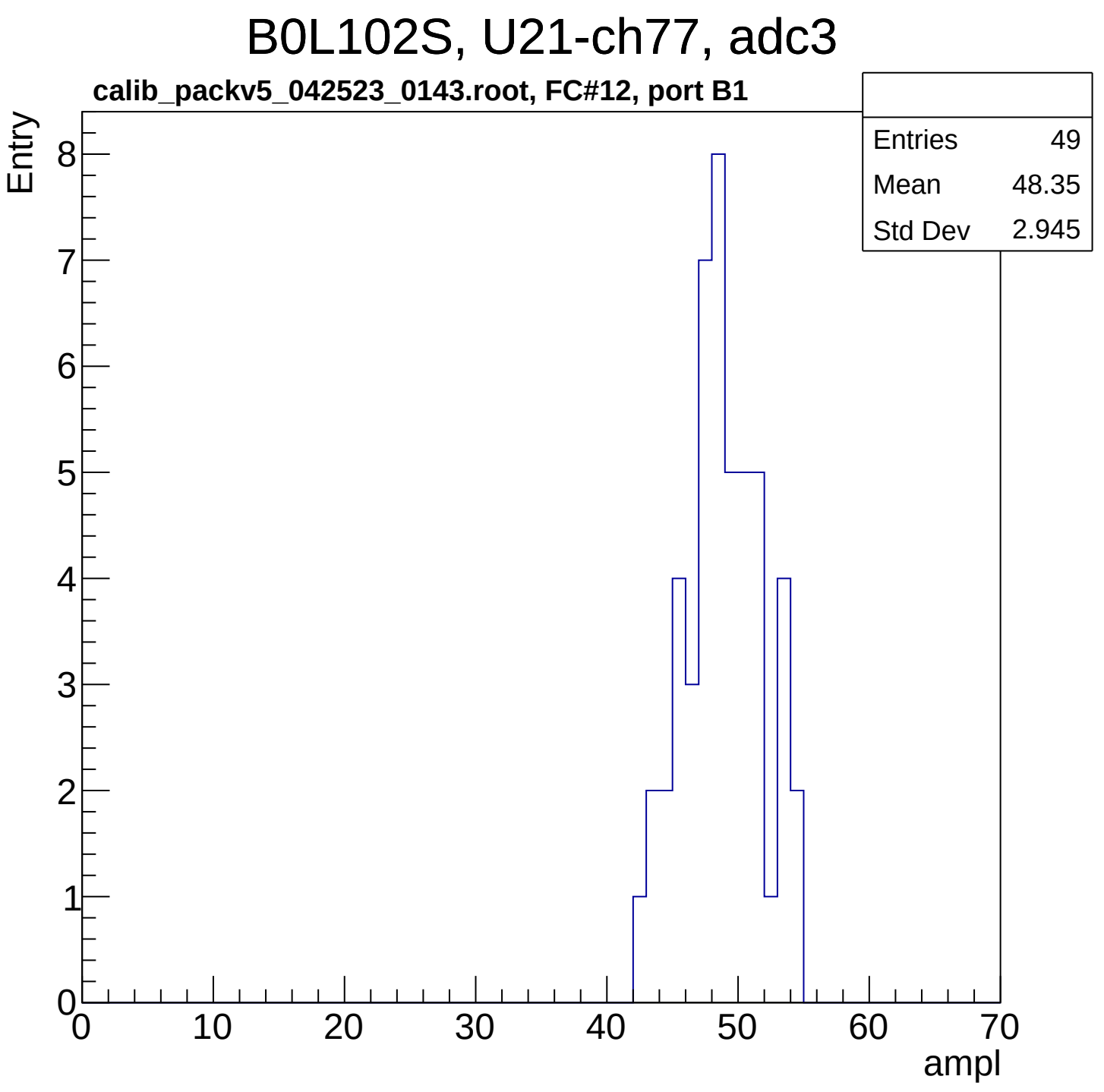

B0L102S, U21-ch76, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 60.75
Std Dev 2.773
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

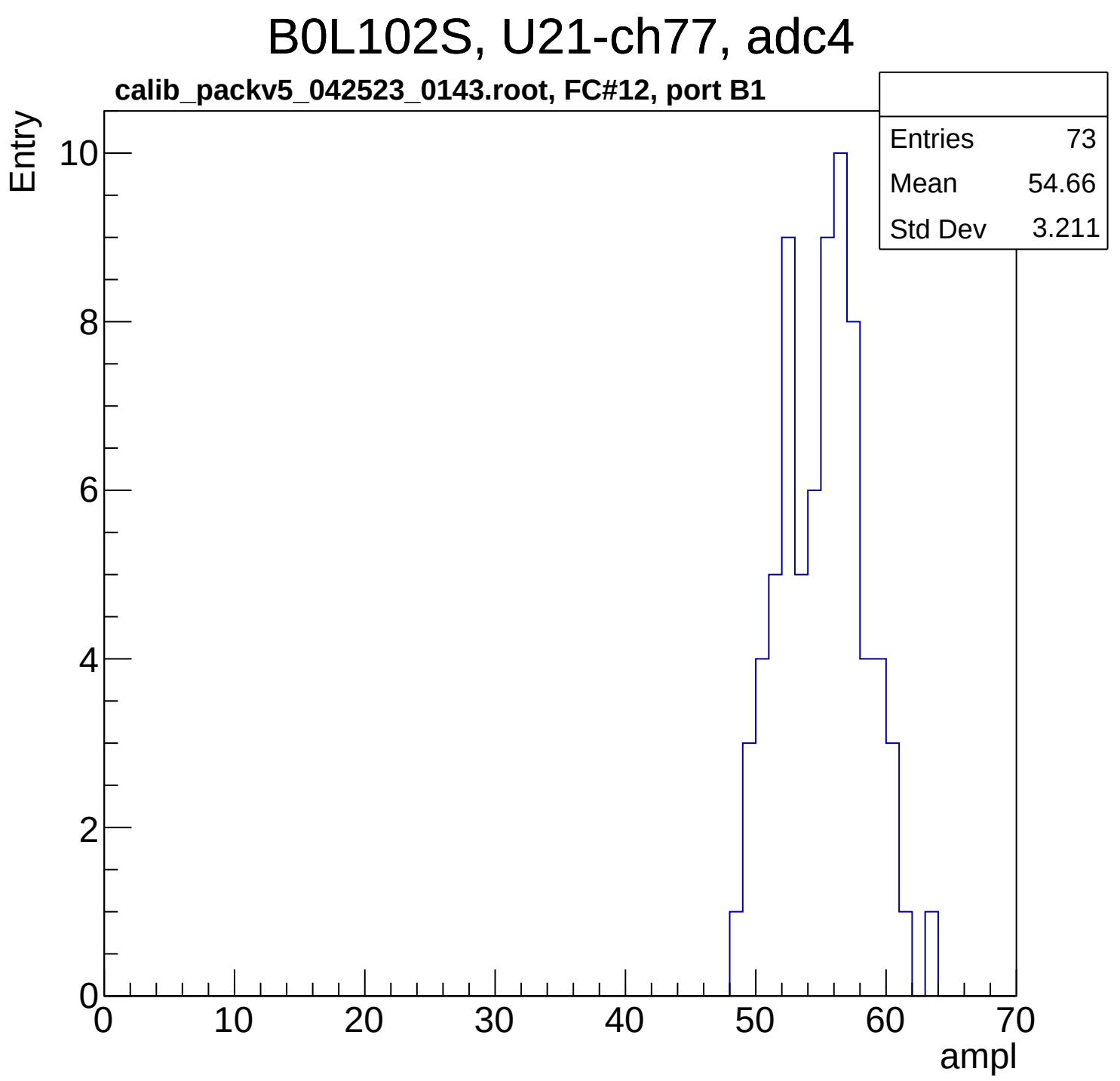

B0L102S, U21-ch76, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch77, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 26.77
Std Dev 3.26
Gaus mean : 27.4978
Gaus Width: 3.4219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch77, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 94
Mean 35
Std Dev 3.644
Gaus mean : 35.3247
Gaus Width: 4.0393
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch77, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 42.72
Std Dev 3.379
Gaus mean : 43.7779
Gaus Width: 2.8435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch77, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 48.35
Std Dev 2.945
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch77, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 54.66
Std Dev 3.211
Entry
ampl
0
2
4
6
8
10
0
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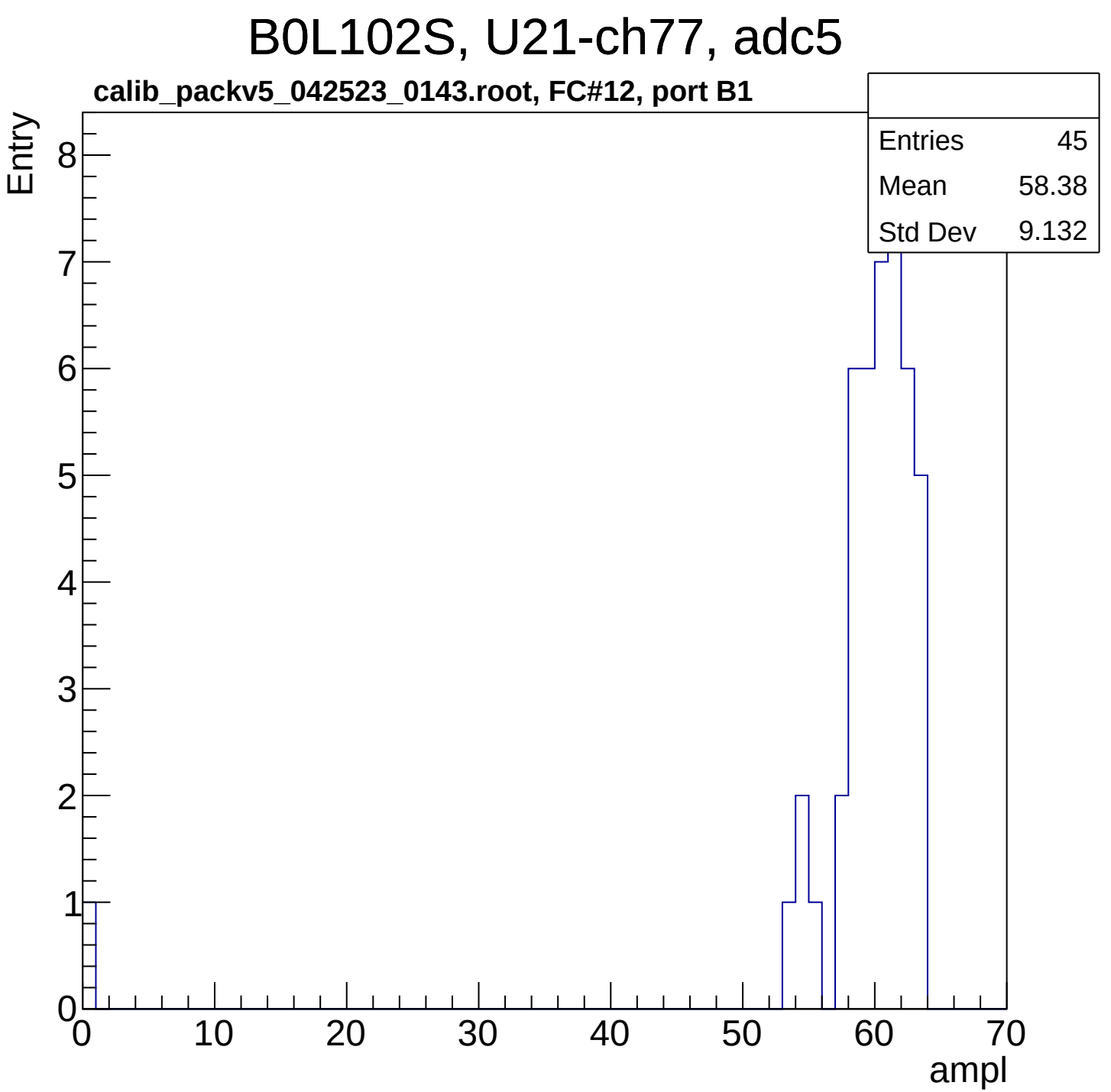
20
30
40
50
60
70


B0L102S, U21-ch77, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.38
Std Dev 9.132
Entry
0
1
2
3
4
5
6
7
8
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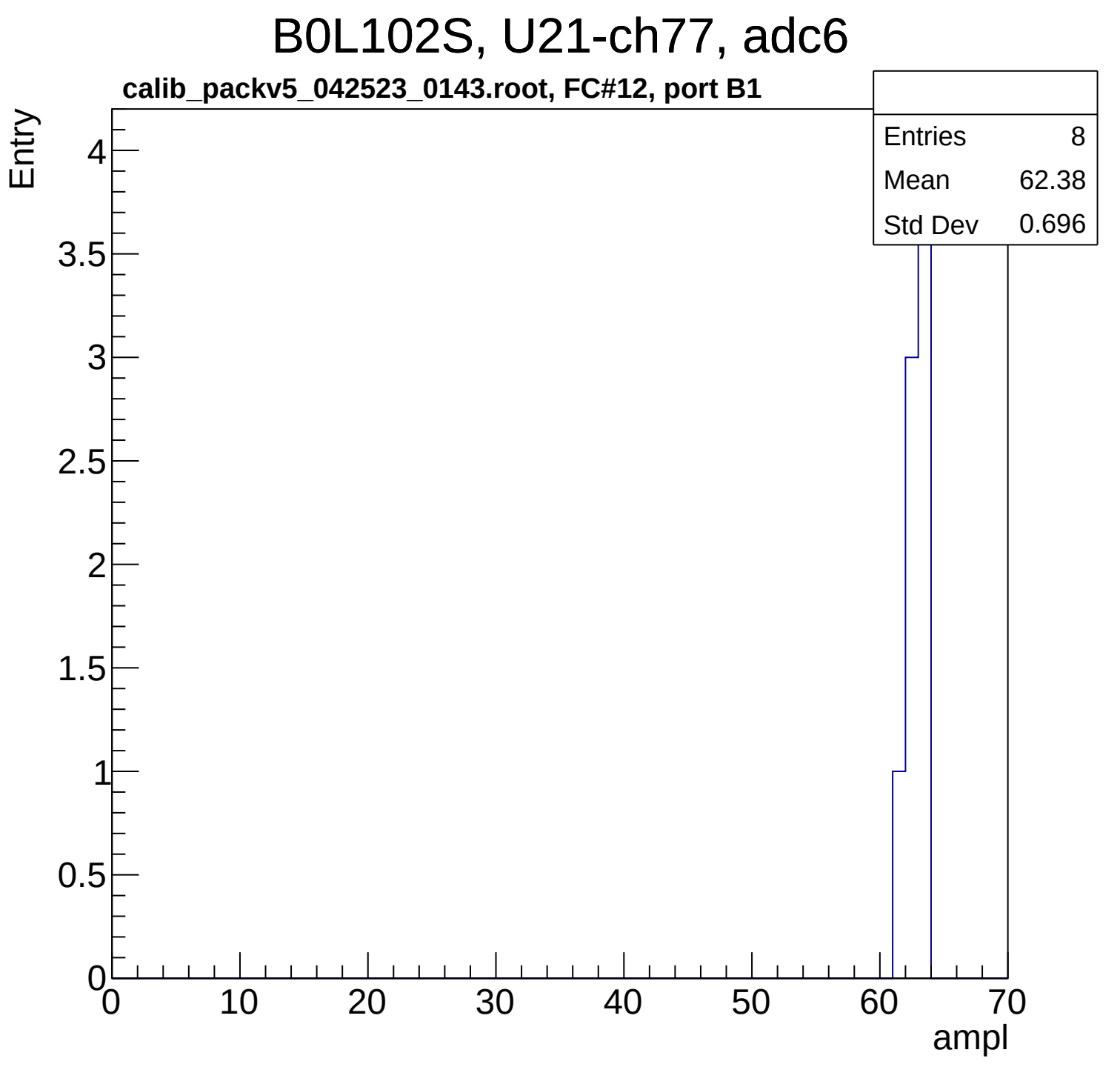
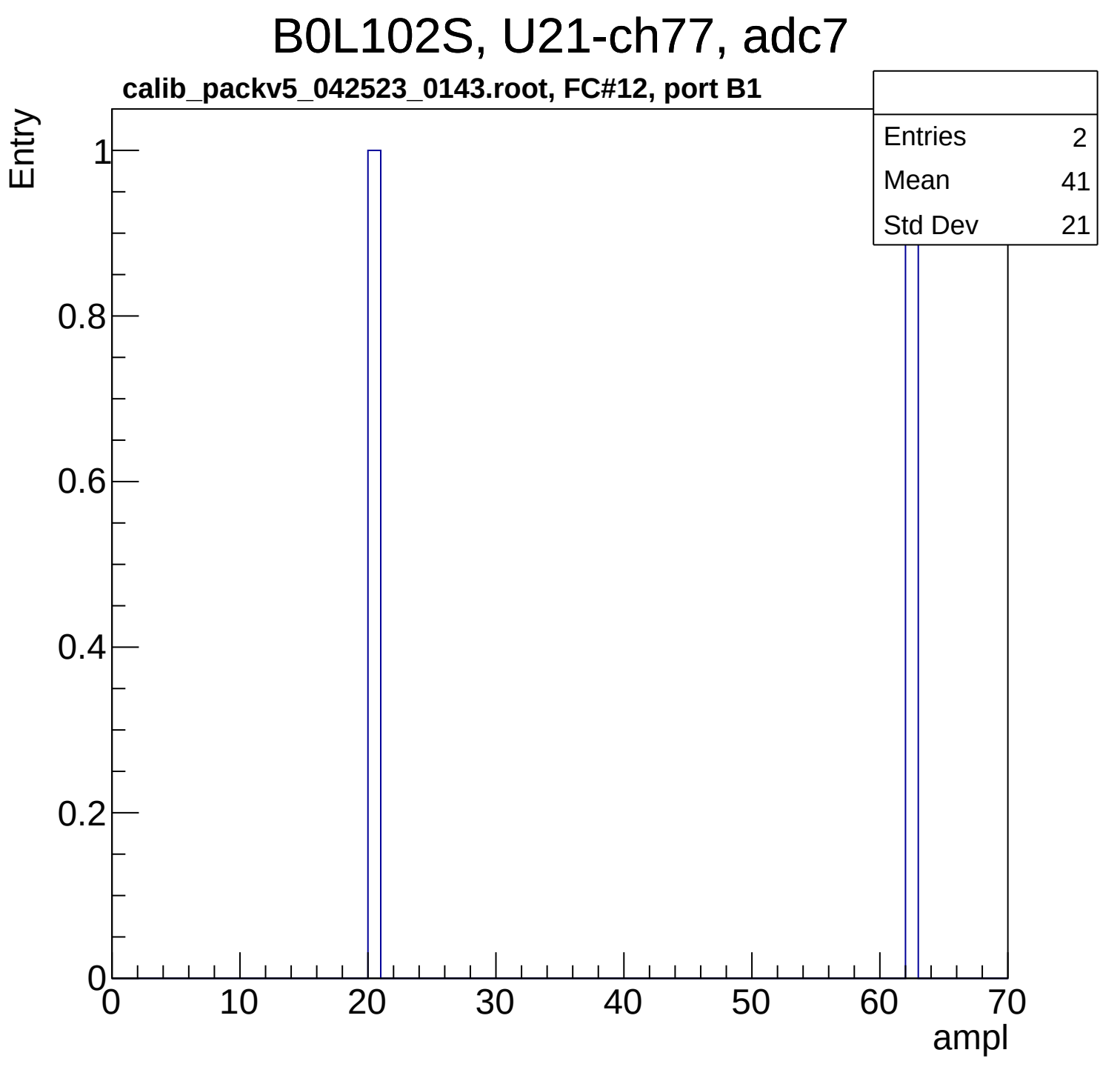
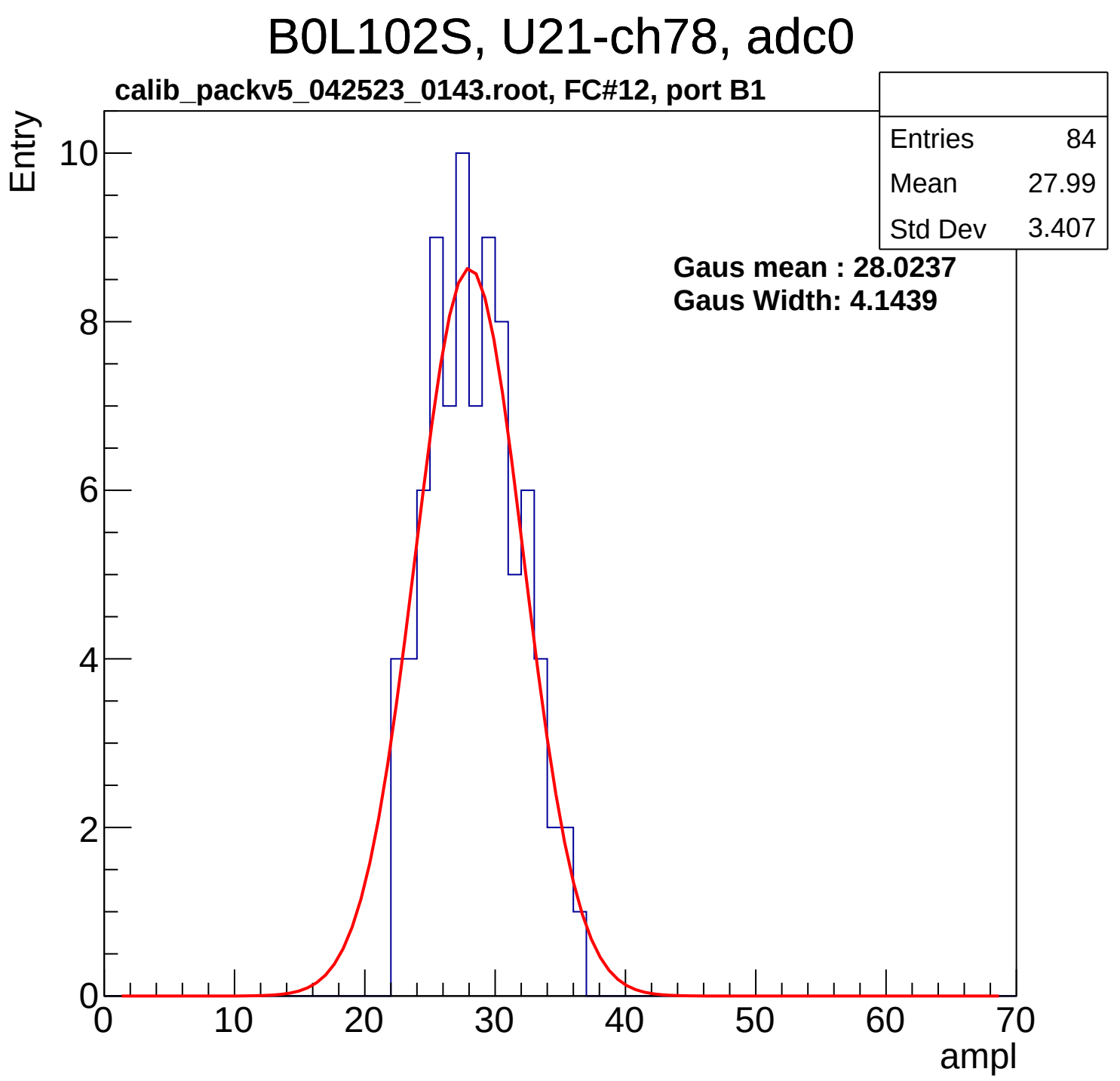
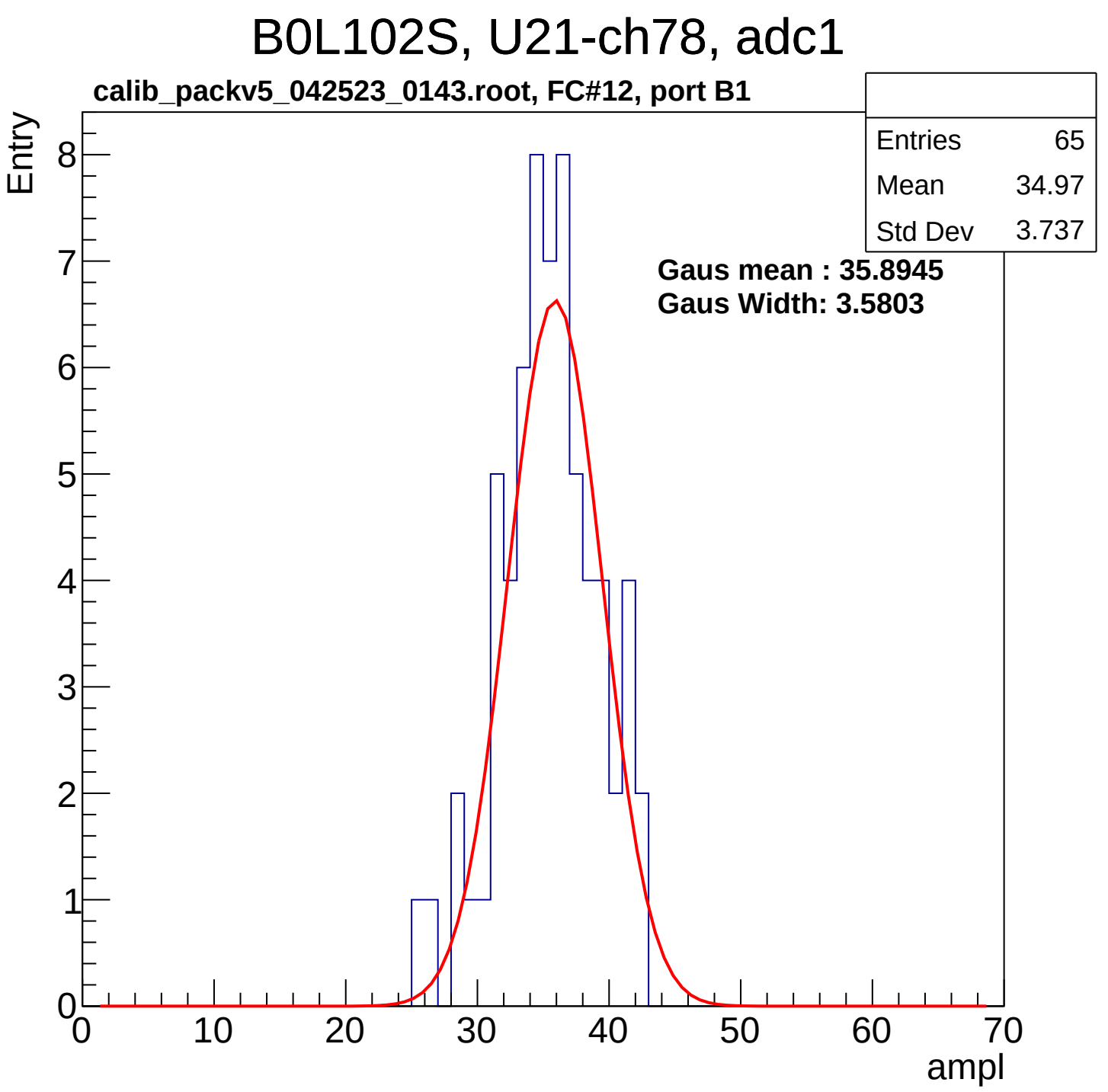
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch77, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.38
Std Dev 0.696
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch77, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 41
Std Dev 21
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch78, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 27.99
Std Dev 3.407
Gaus mean : 28.0237
Gaus Width: 4.1439
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch78, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 34.97
Std Dev 3.737
Gaus mean : 35.8945
Gaus Width: 3.5803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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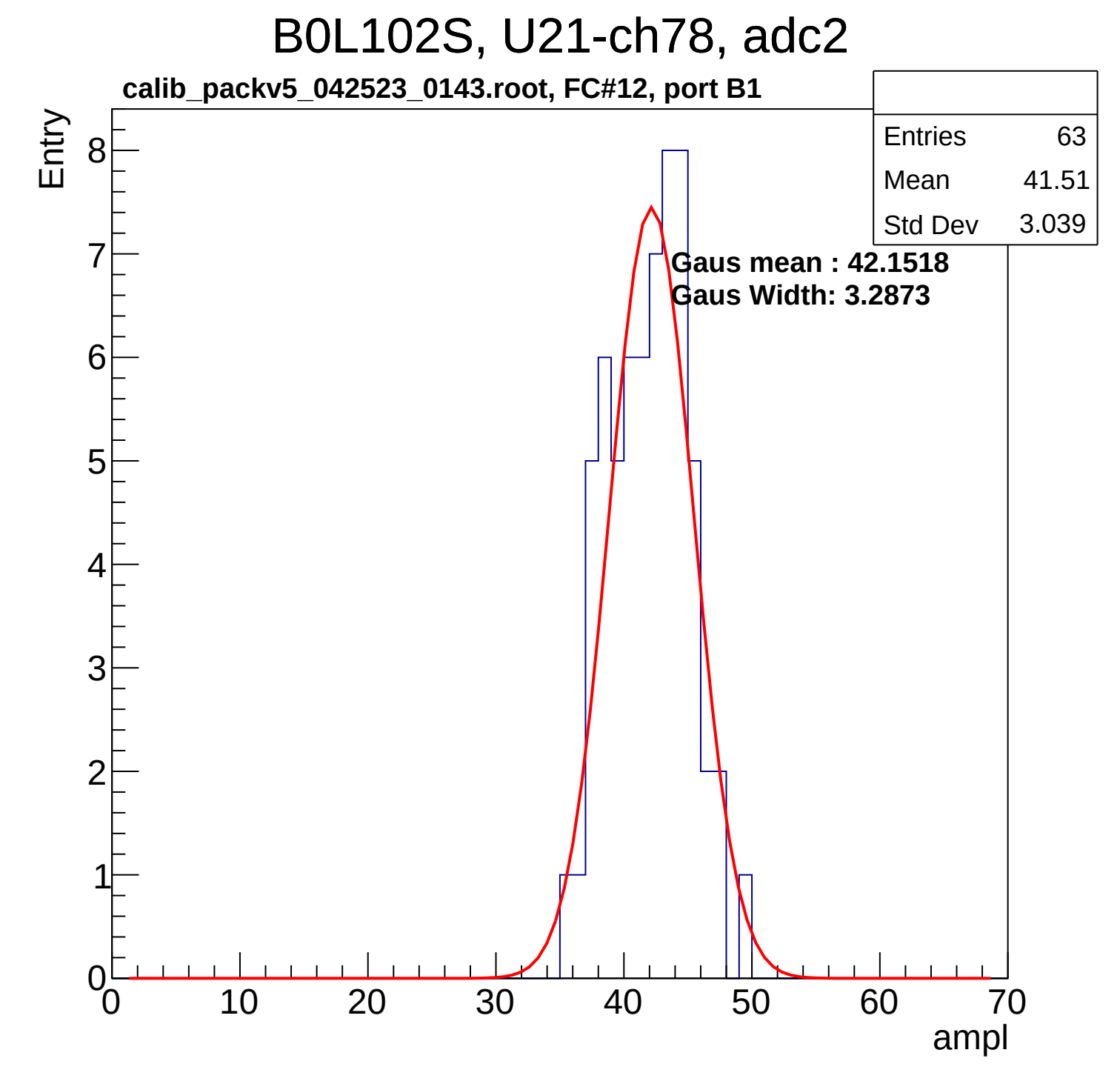
B0L102S, U21-ch78, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 41.51
Std Dev 3.039
Gaus mean : 42.1518
Gaus Width: 3.2873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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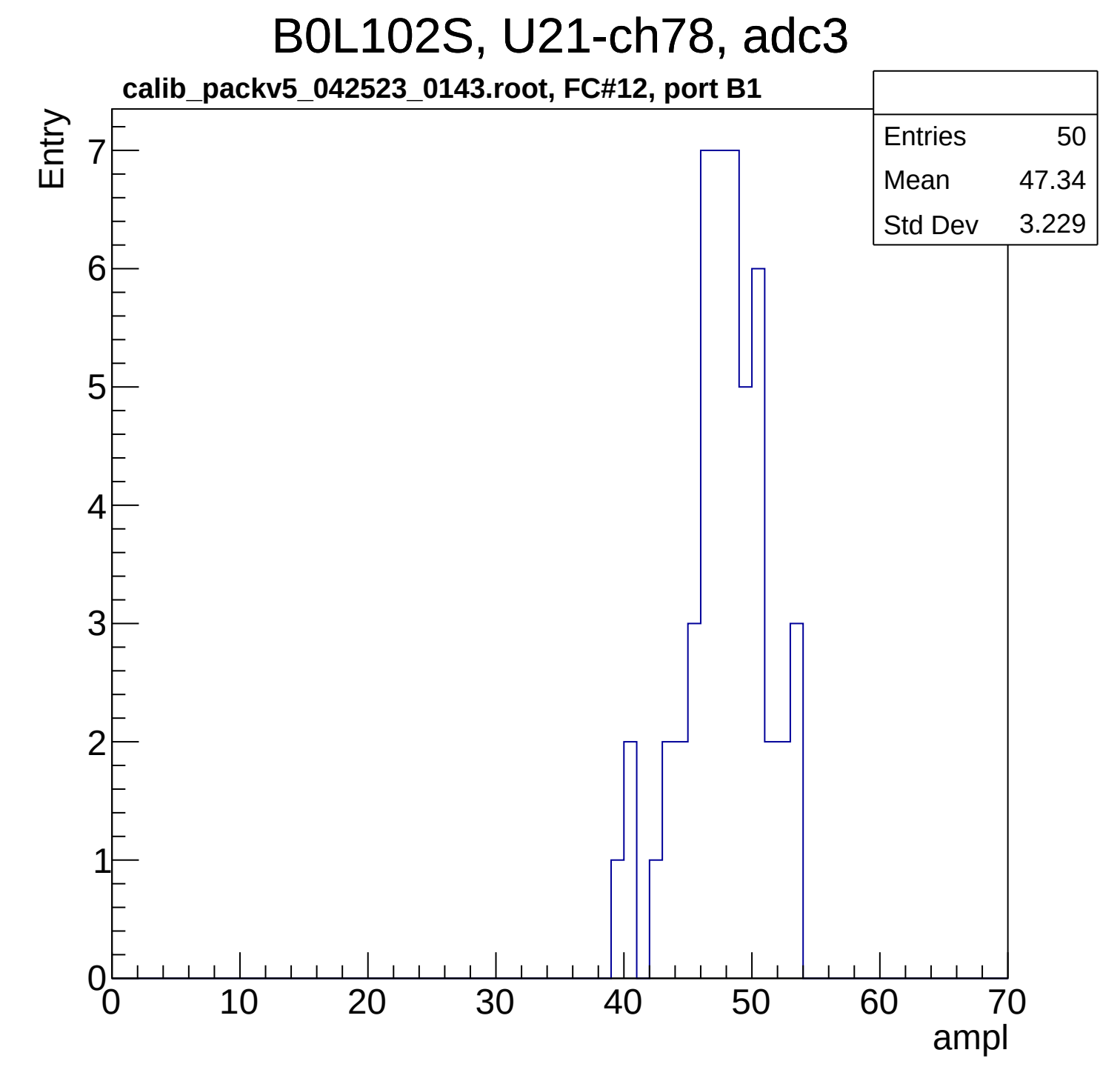
B0L102S, U21-ch78, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 47.34
Std Dev 3.229
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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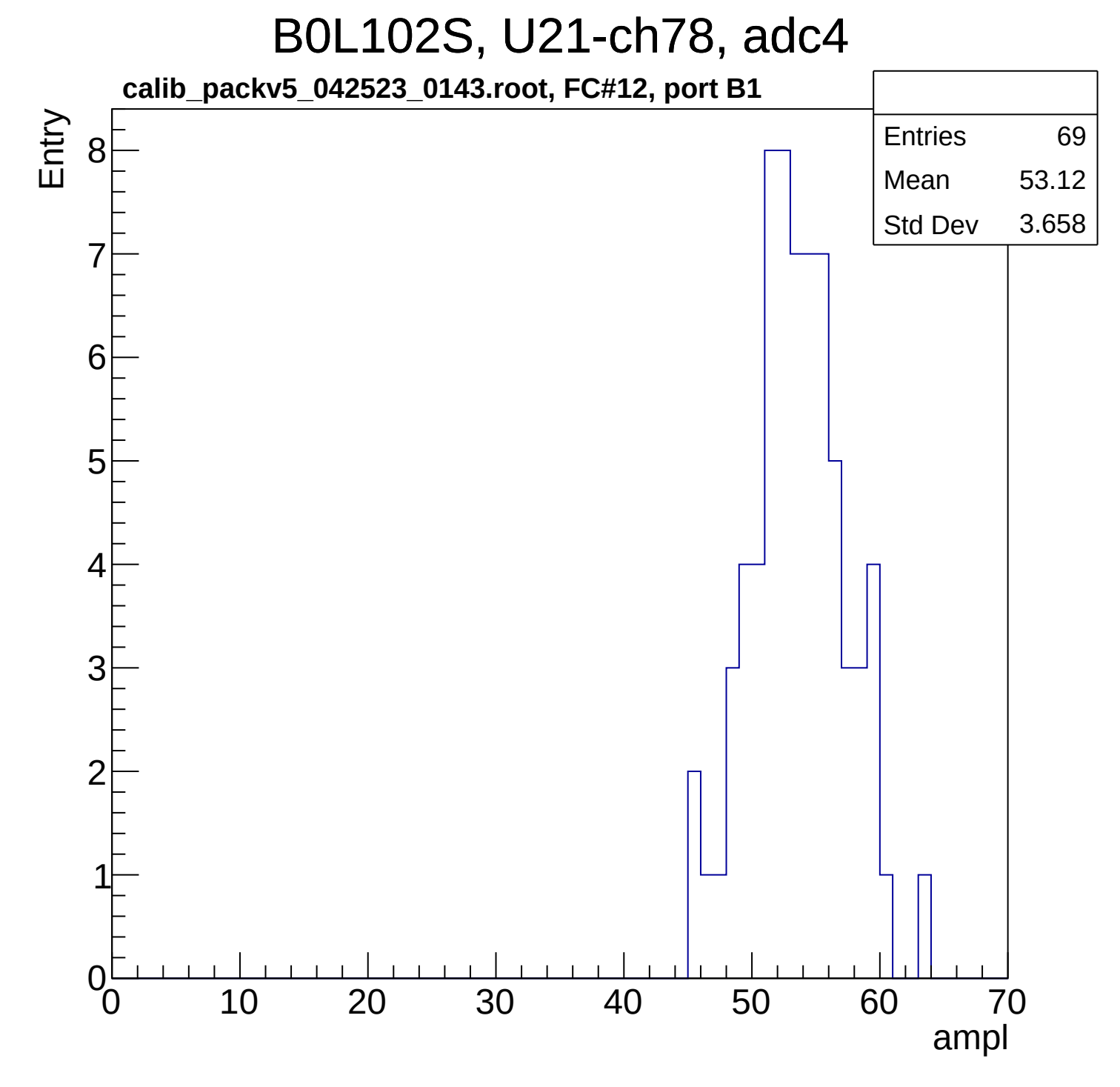
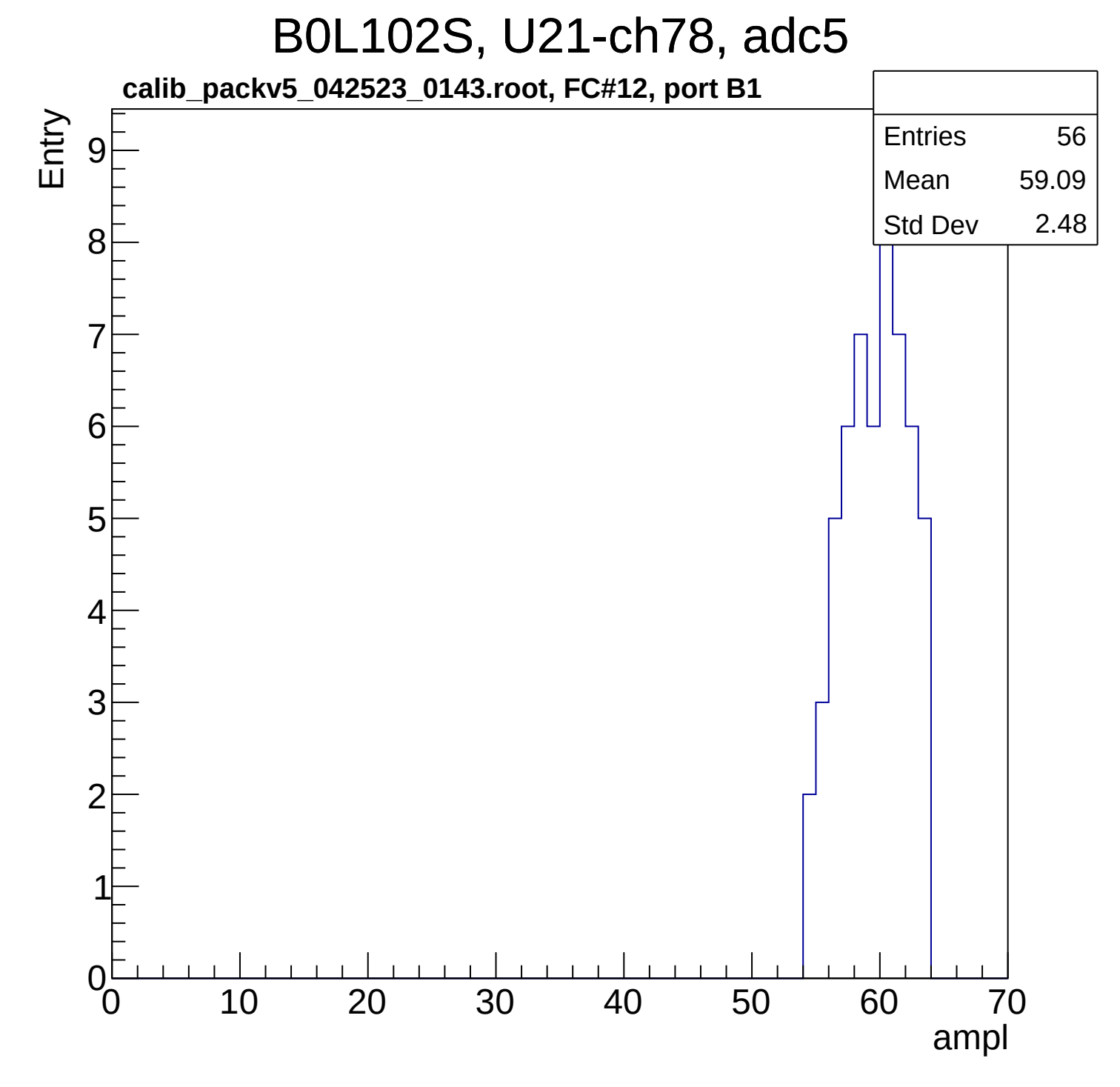
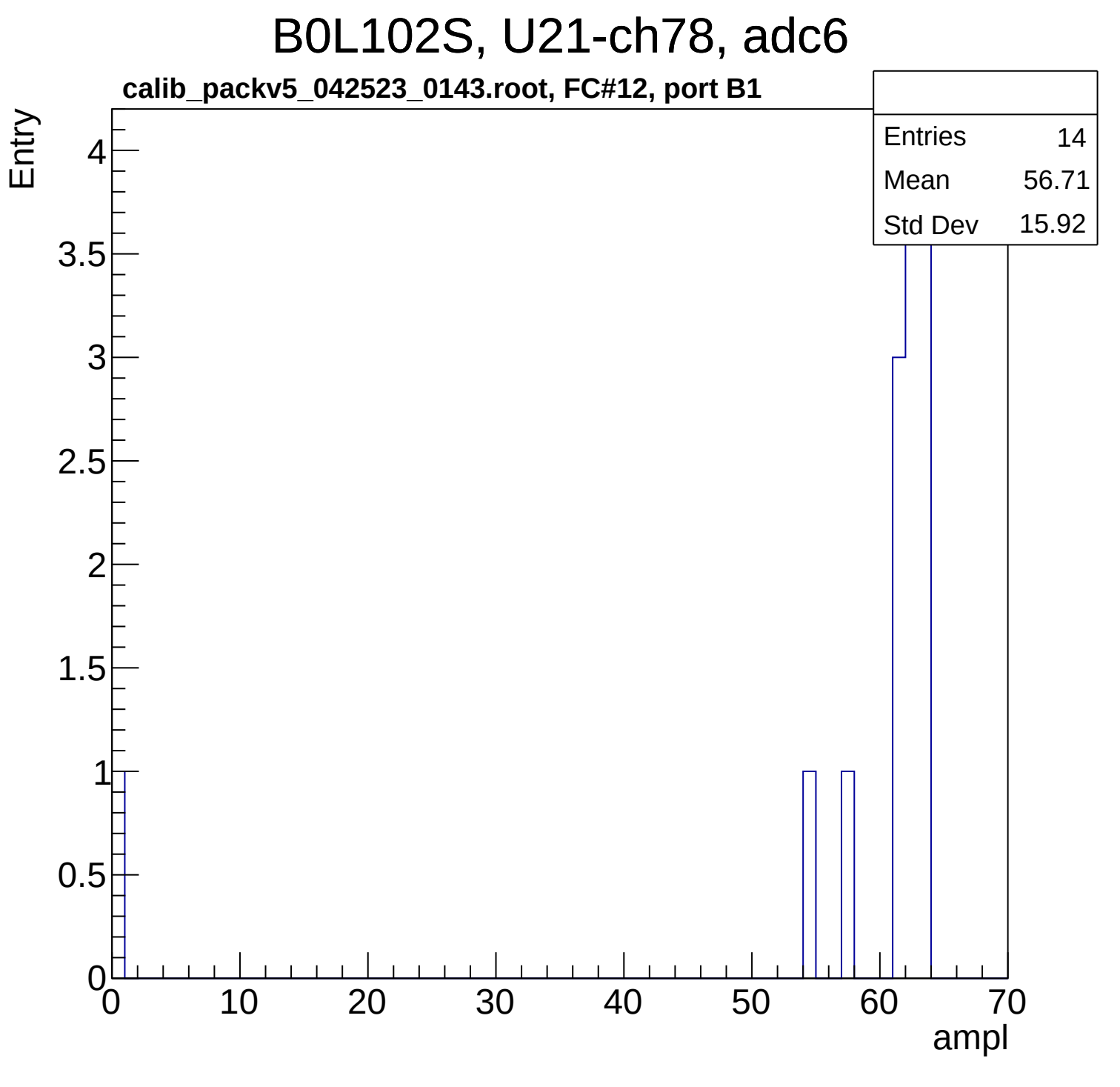
40
50
60
70


B0L102S, U21-ch78, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 53.12
Std Dev 3.658
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch78, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 59.09
Std Dev 2.48
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch78, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 56.71
Std Dev 15.92
Entry
ampl
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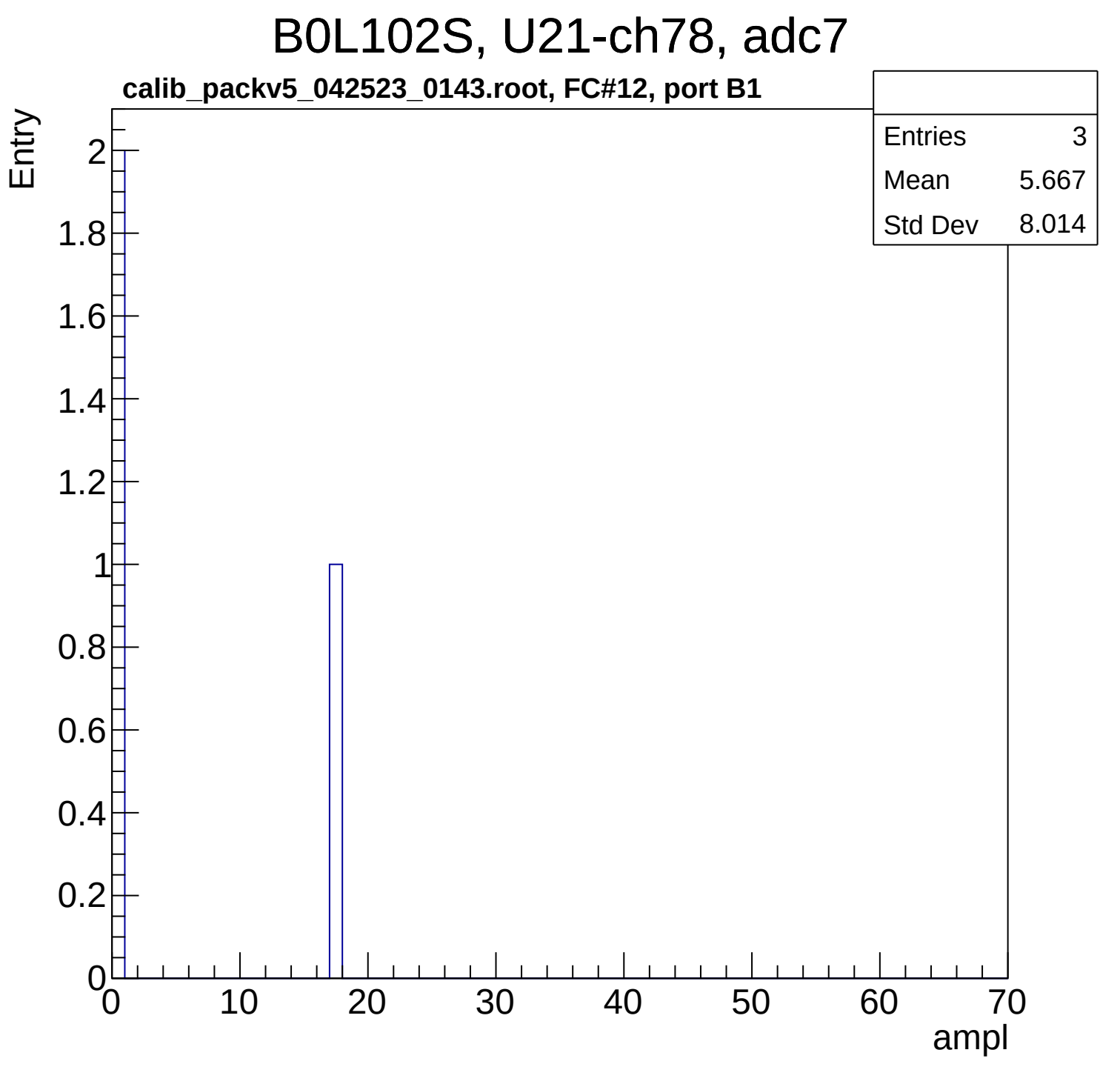
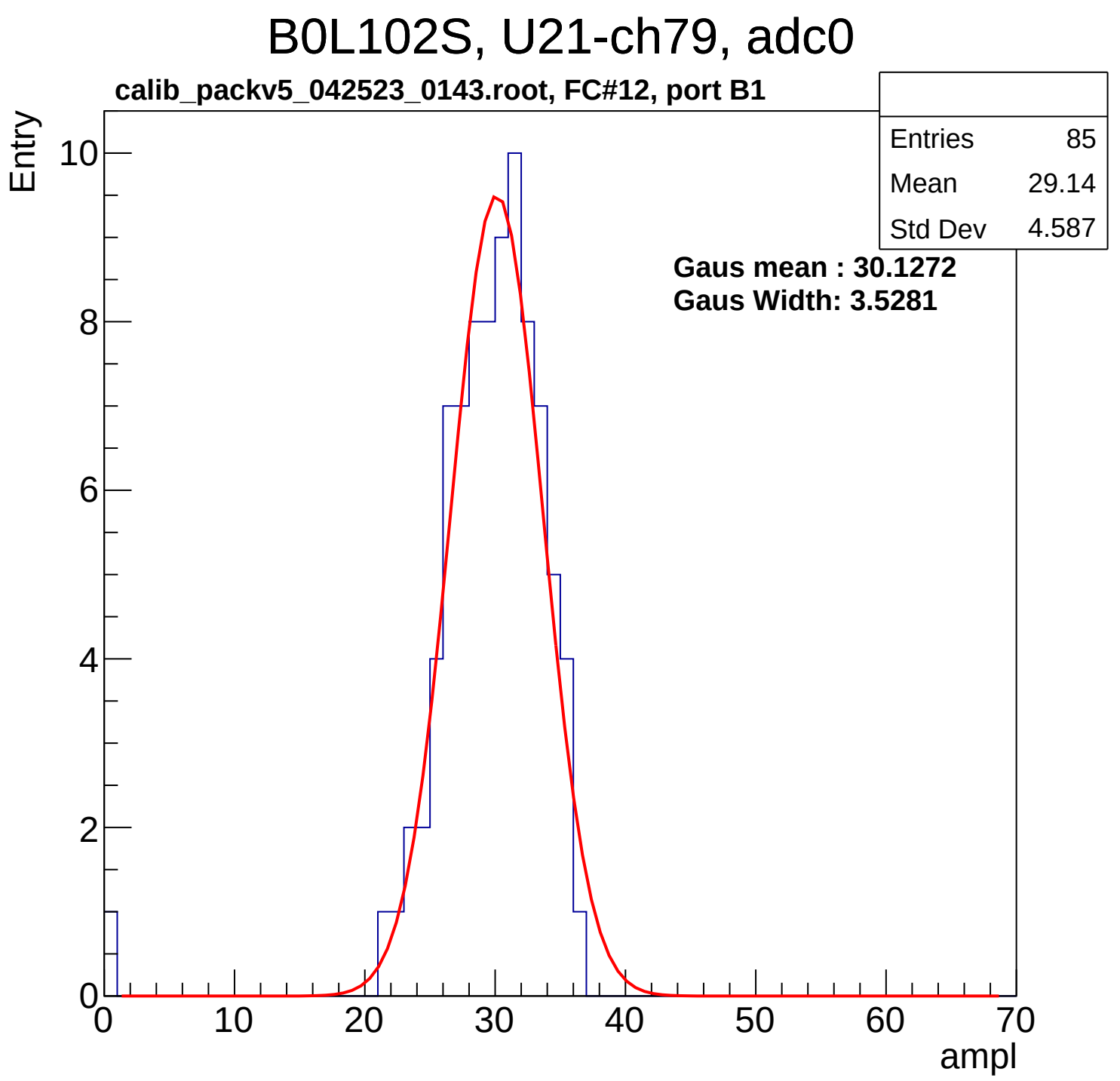
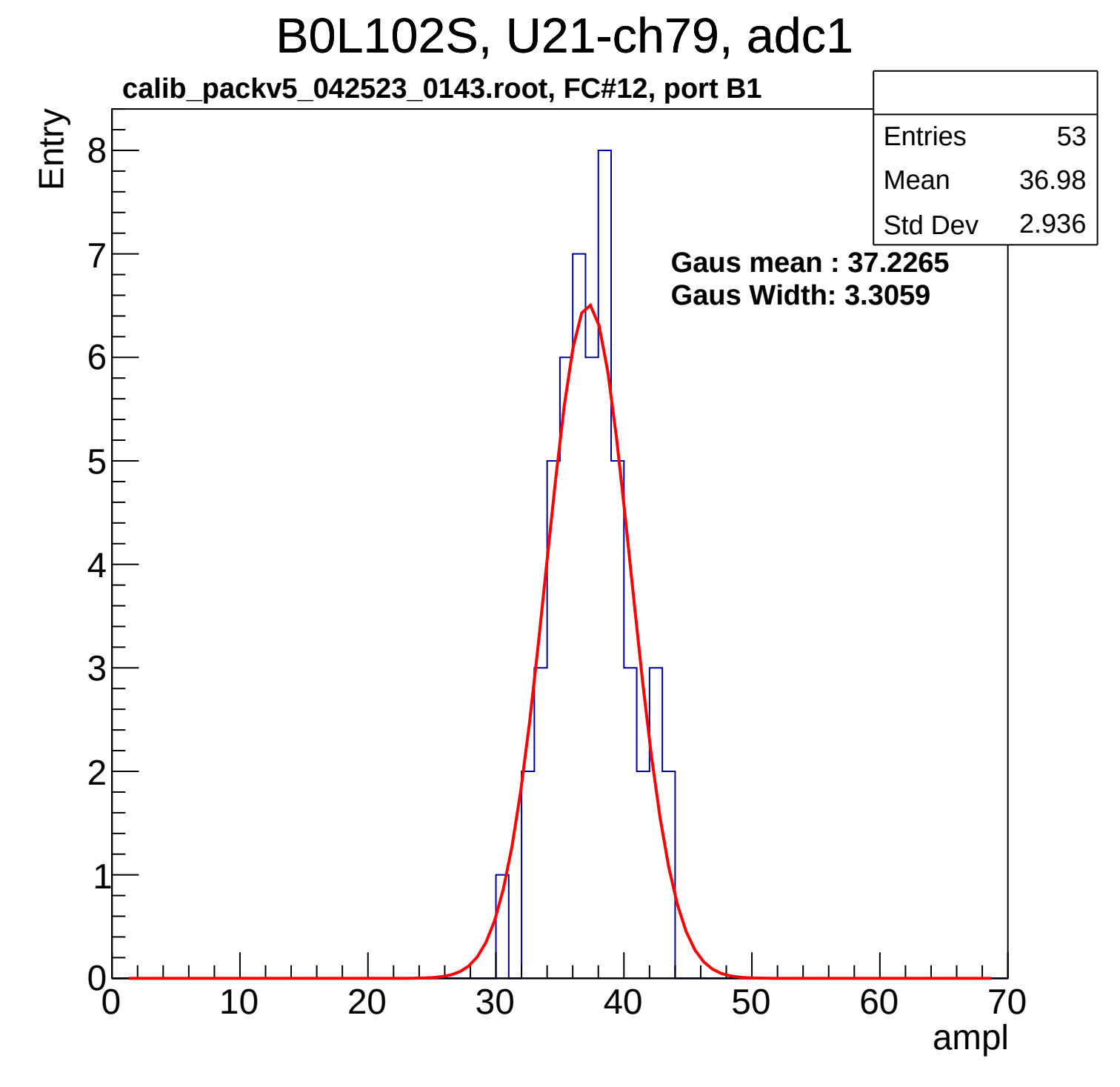
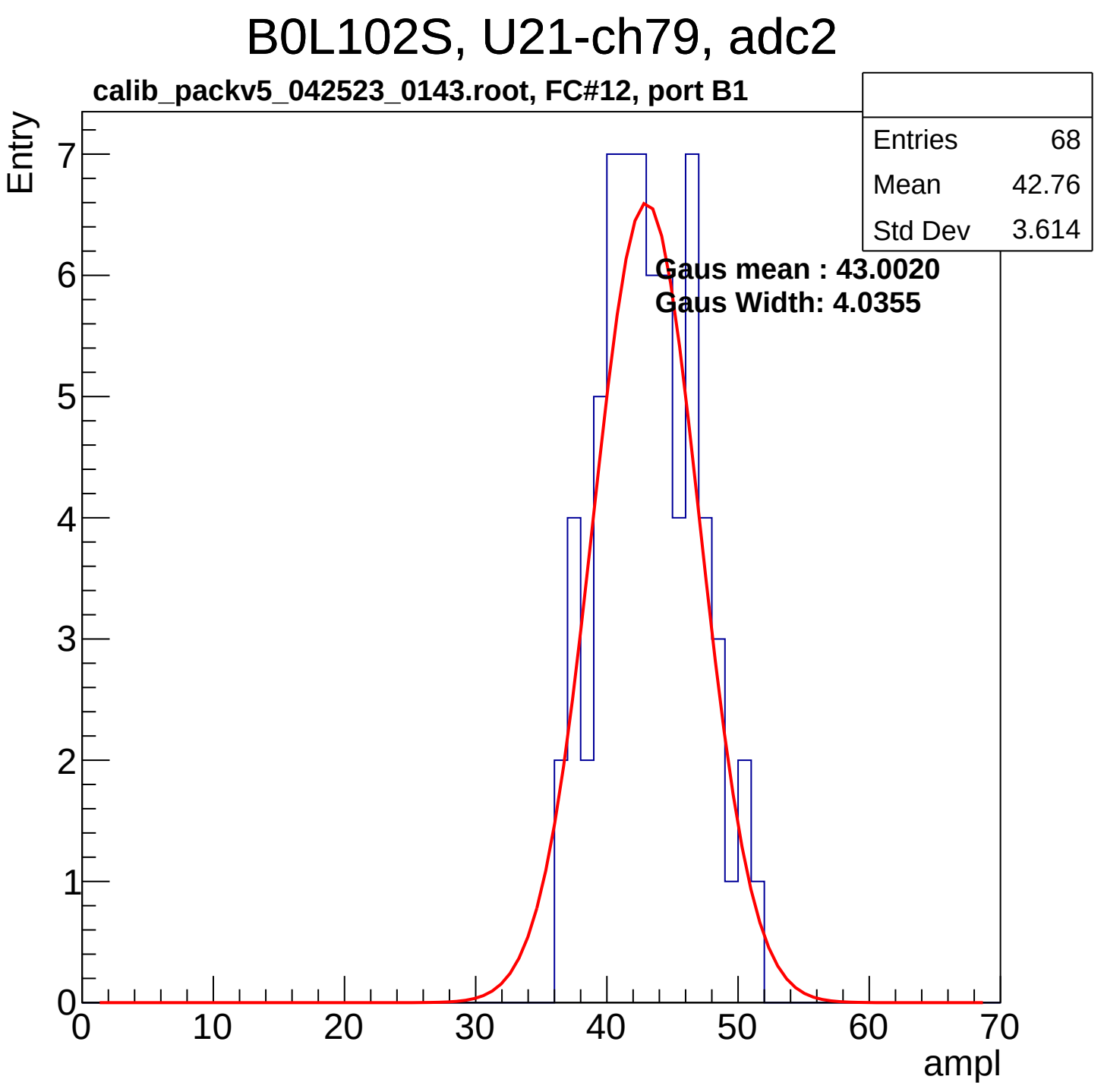
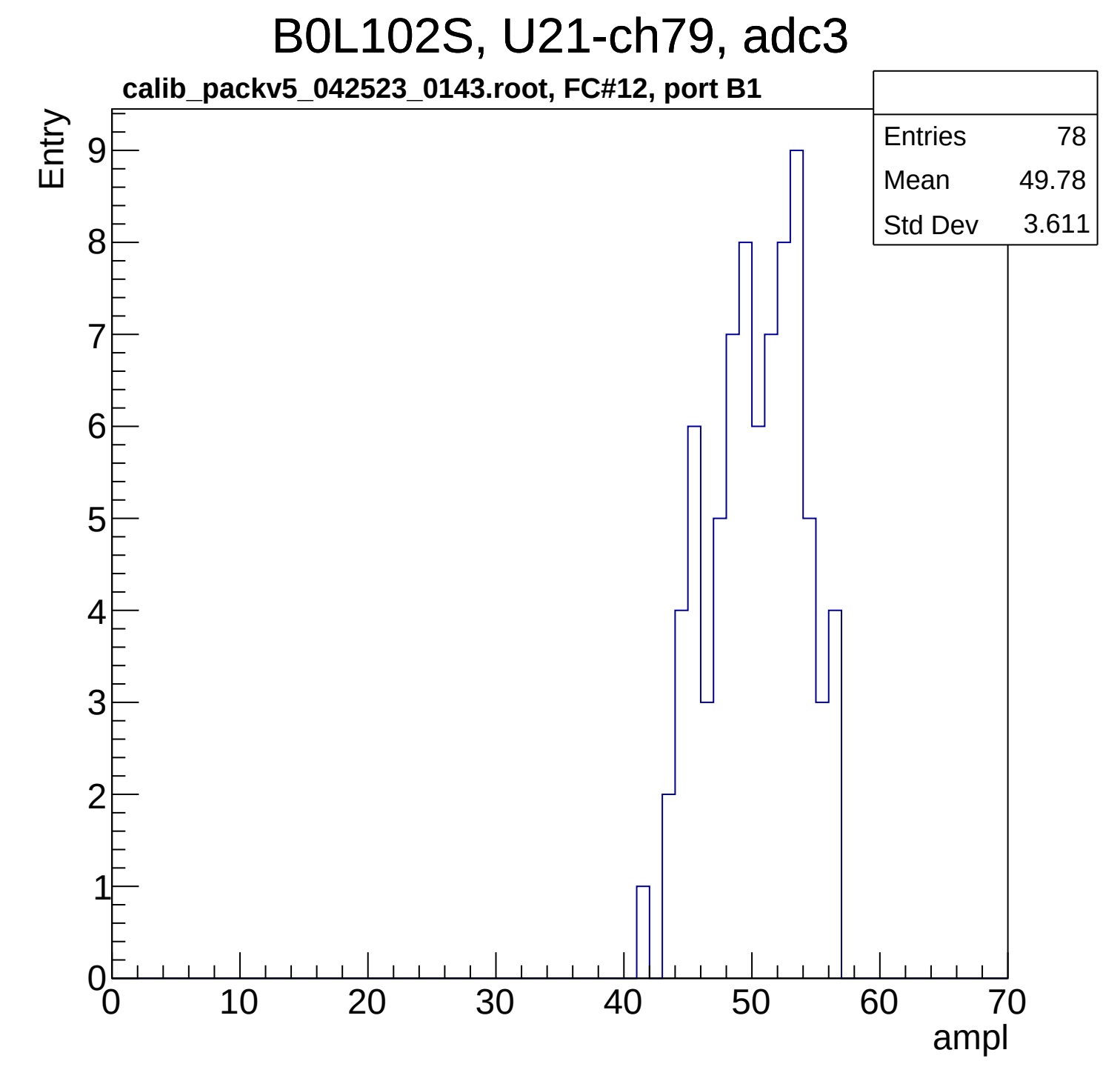
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch78, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 5.667
Std Dev 8.014
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch79, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 29.14
Std Dev 4.587
Gaus mean : 30.1272
Gaus Width: 3.5281
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch79, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 36.98
Std Dev 2.936
Gaus mean : 37.2265
Gaus Width: 3.3059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch79, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 42.76
Std Dev 3.614
Gaus mean : 43.0020
Gaus Width: 4.0355
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch79, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 49.78
Std Dev 3.611
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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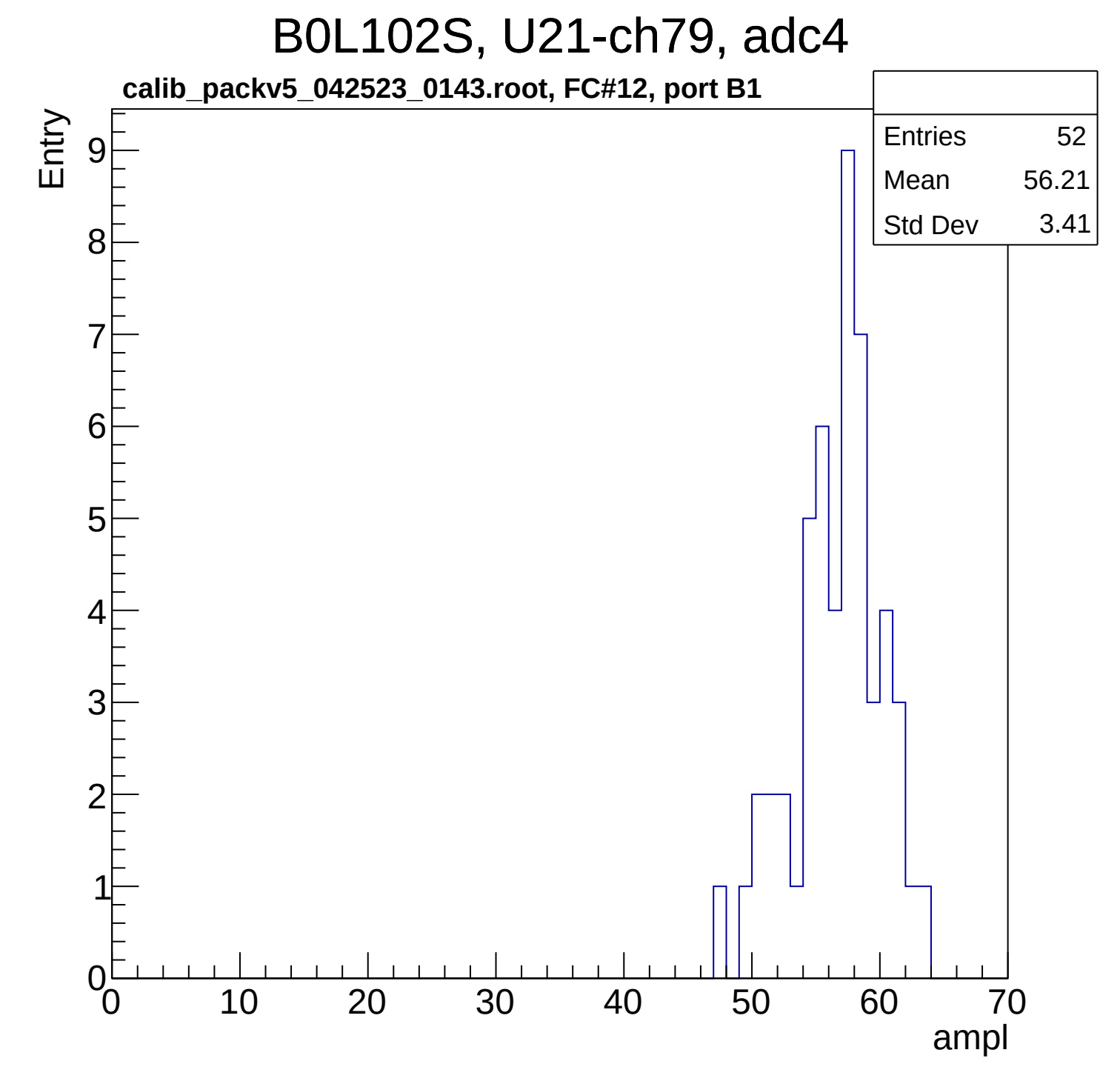
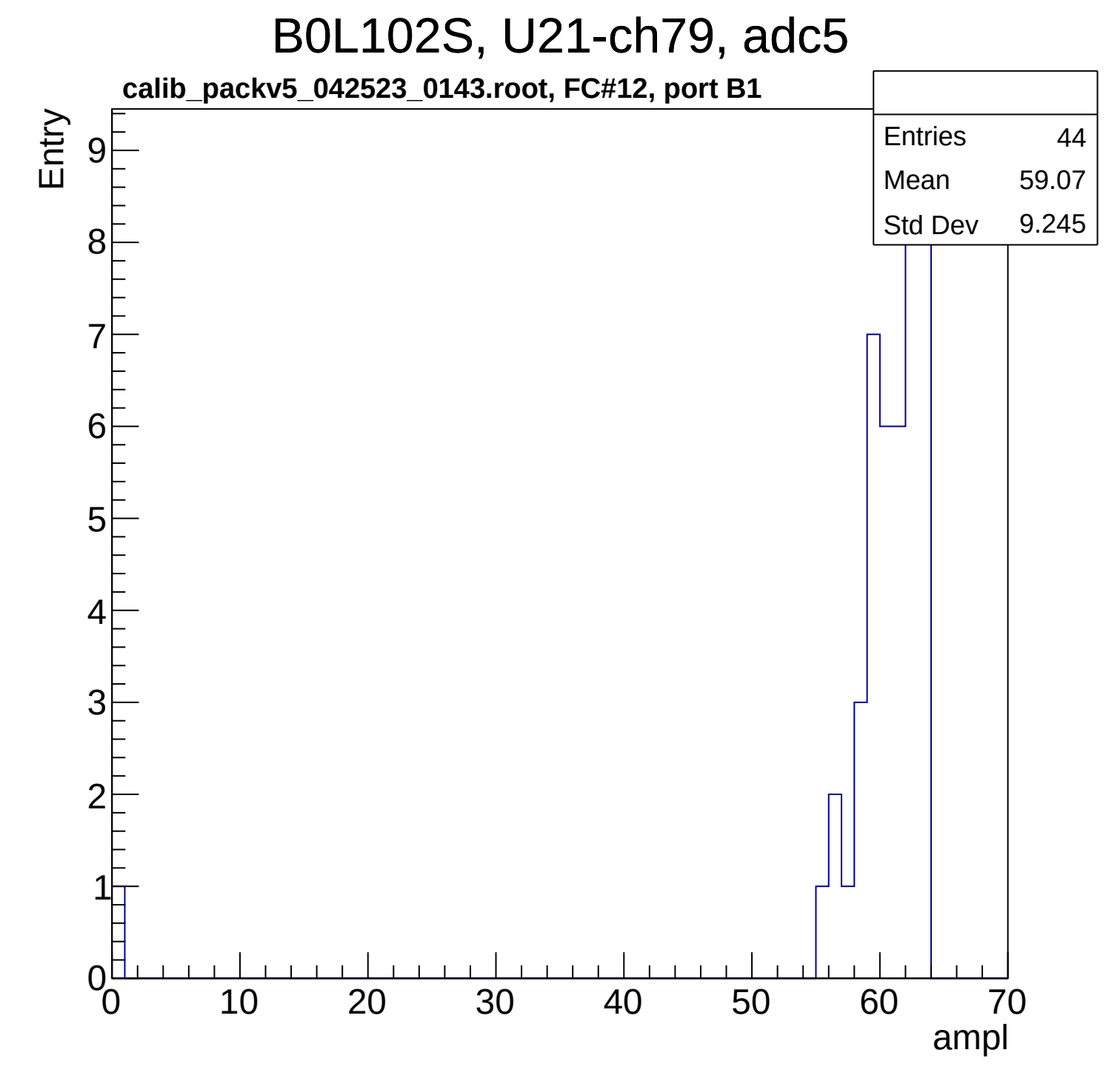

B0L102S, U21-ch79, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 56.21
Std Dev 3.41
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch79, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.07
Std Dev 9.245
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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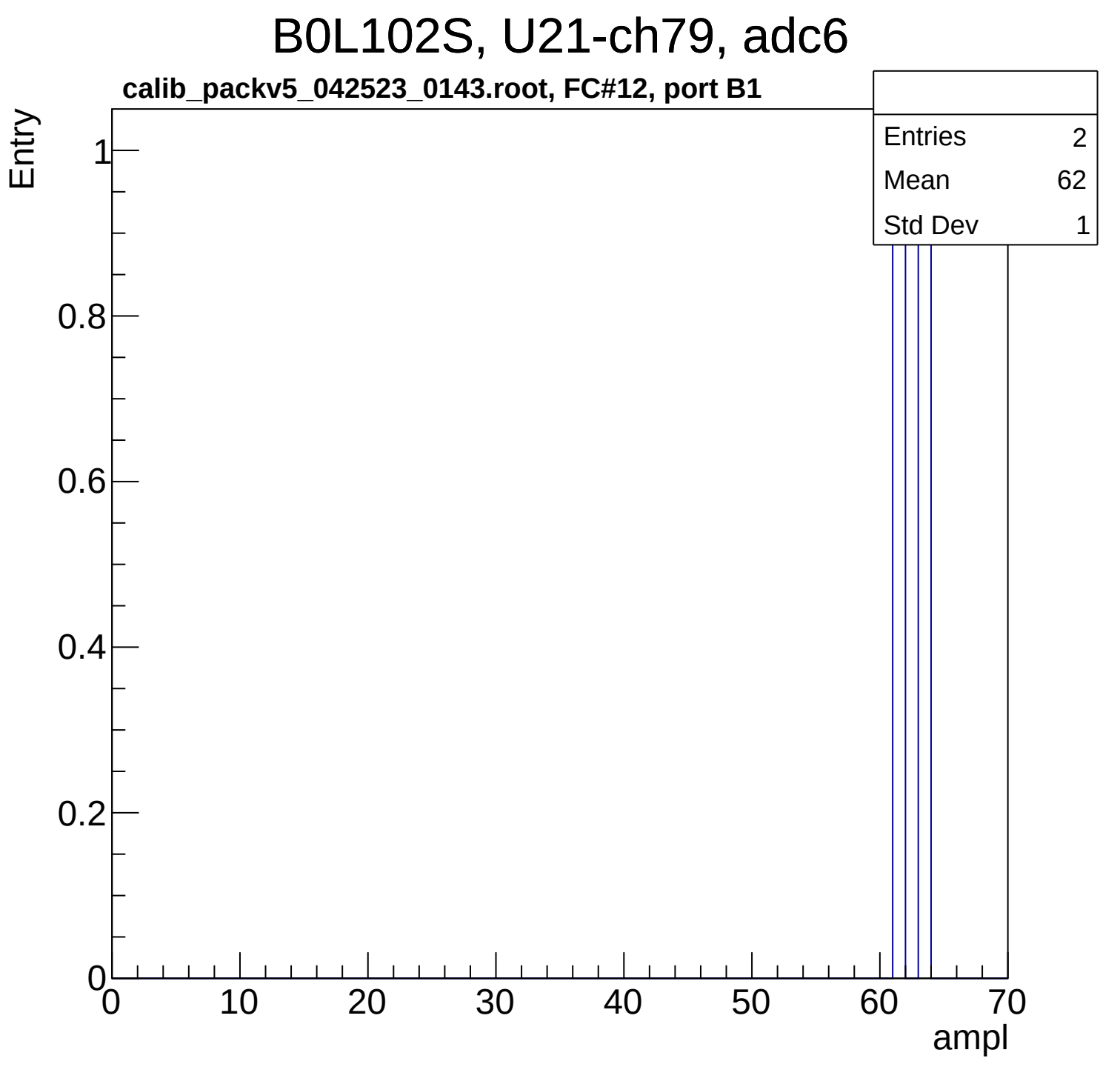
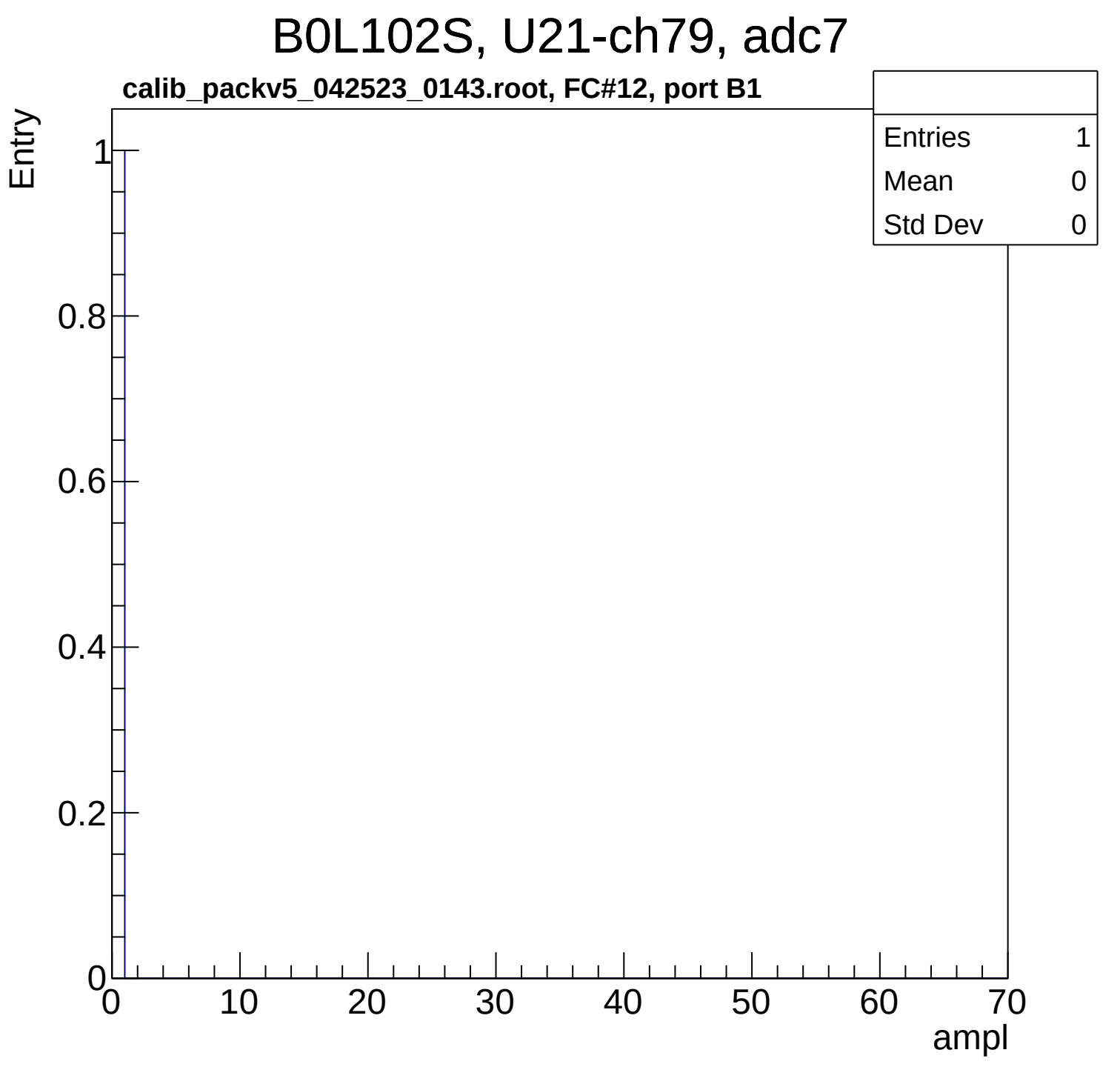
60
70


B0L102S, U21-ch79, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch79, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
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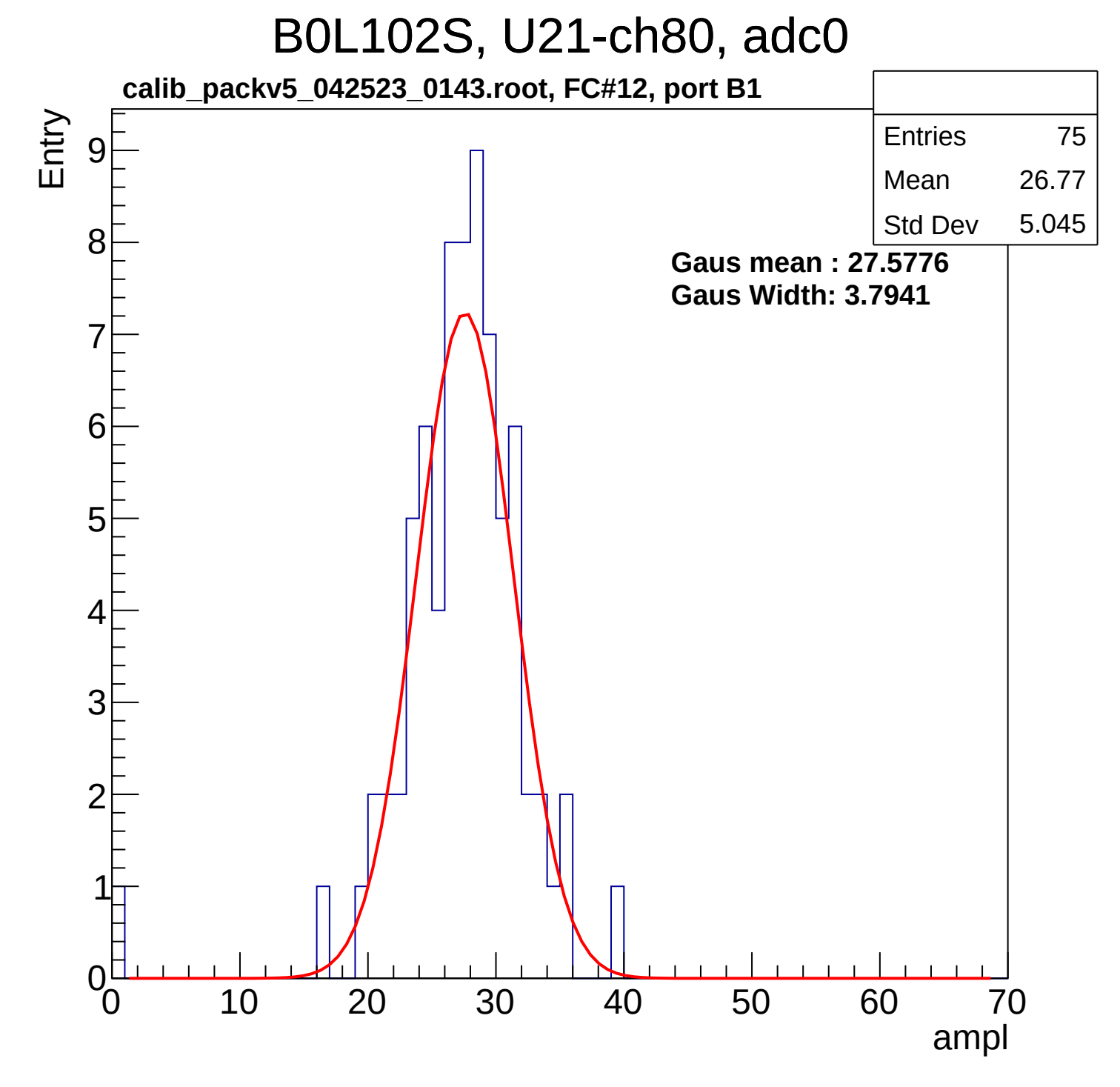
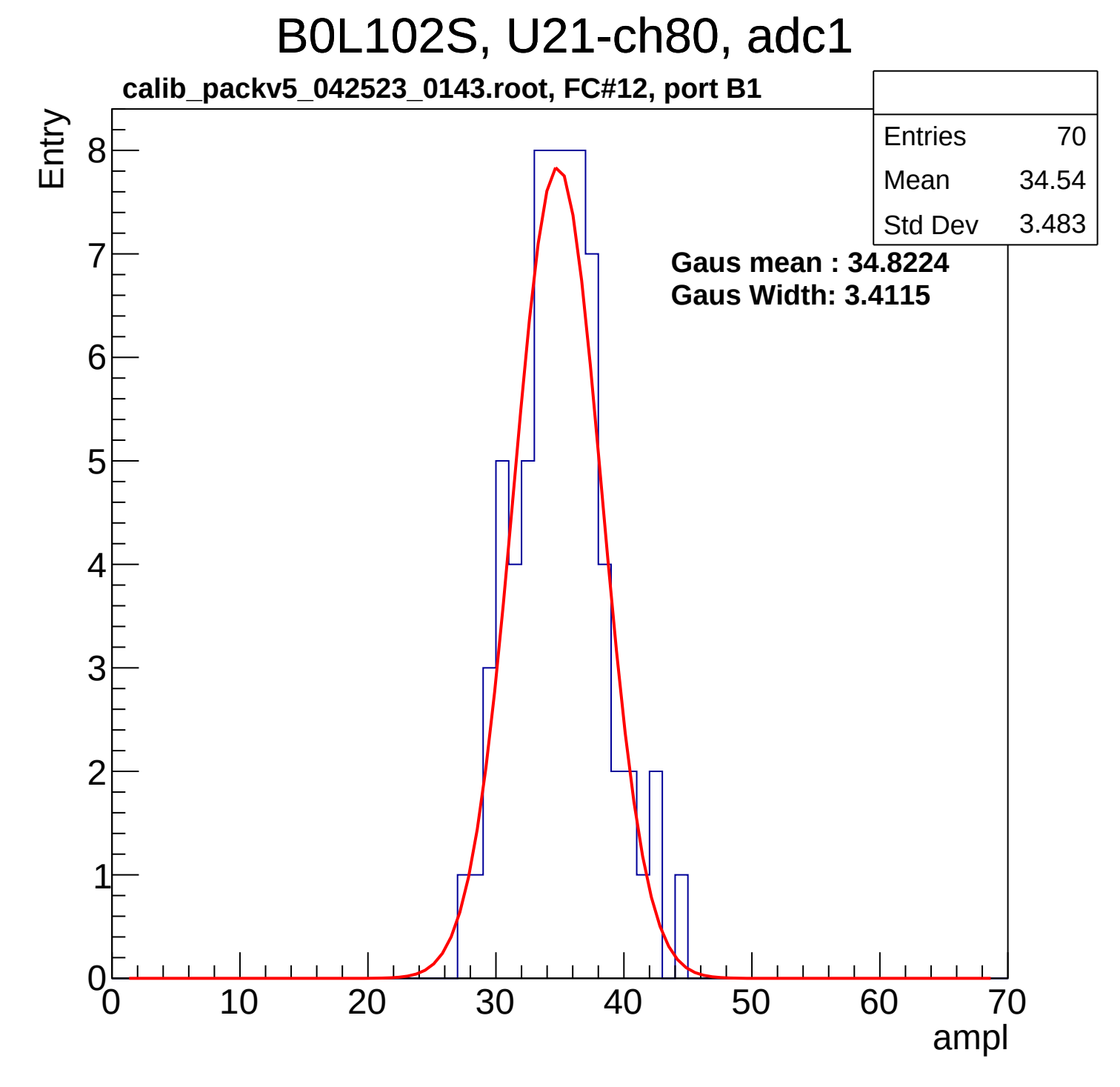
50
60
70
ampl


B0L102S, U21-ch80, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 26.77
Std Dev 5.045
Gaus mean : 27.5776
Gaus Width: 3.7941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch80, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 34.54
Std Dev 3.483
Gaus mean : 34.8224
Gaus Width: 3.4115
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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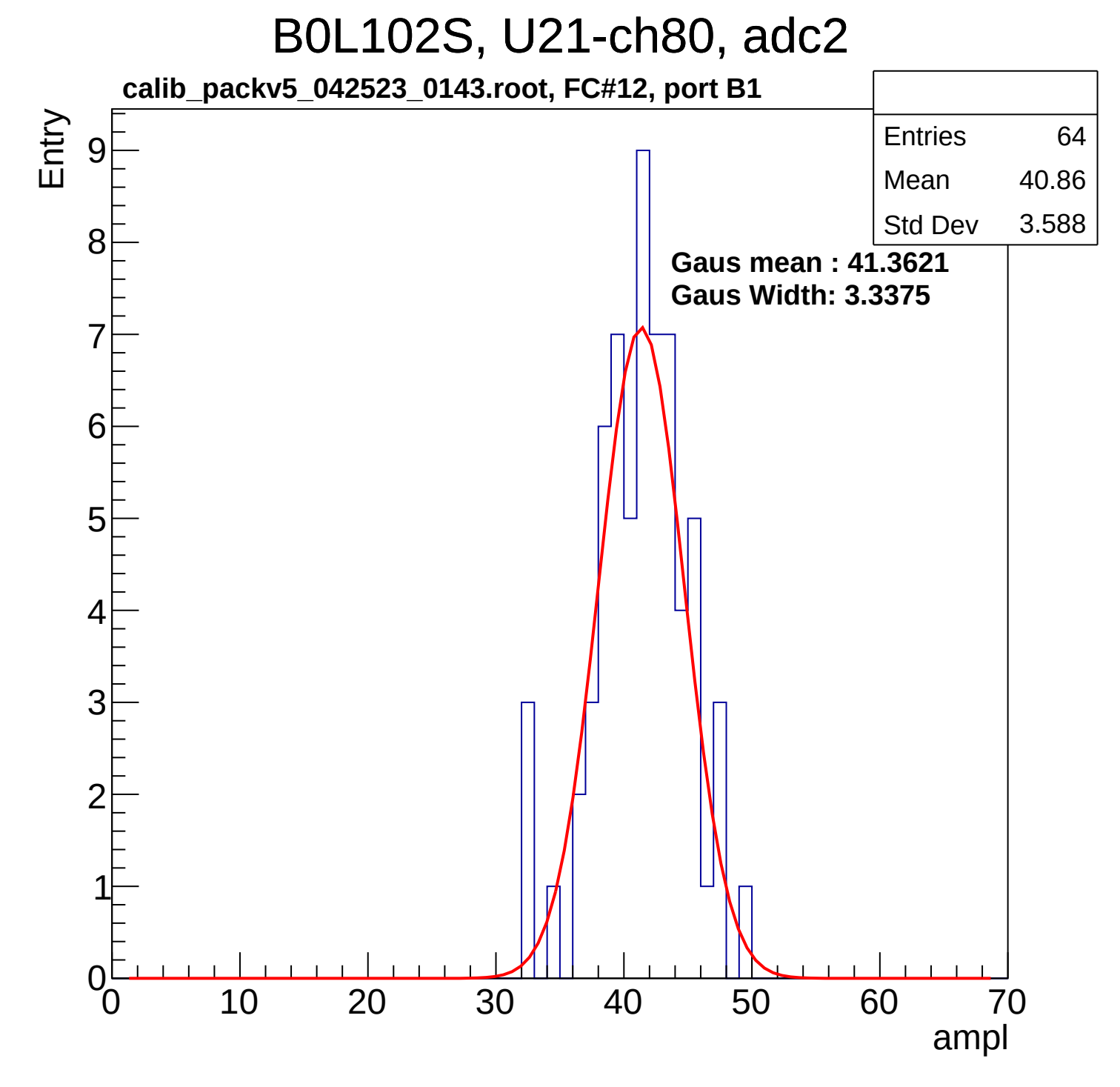
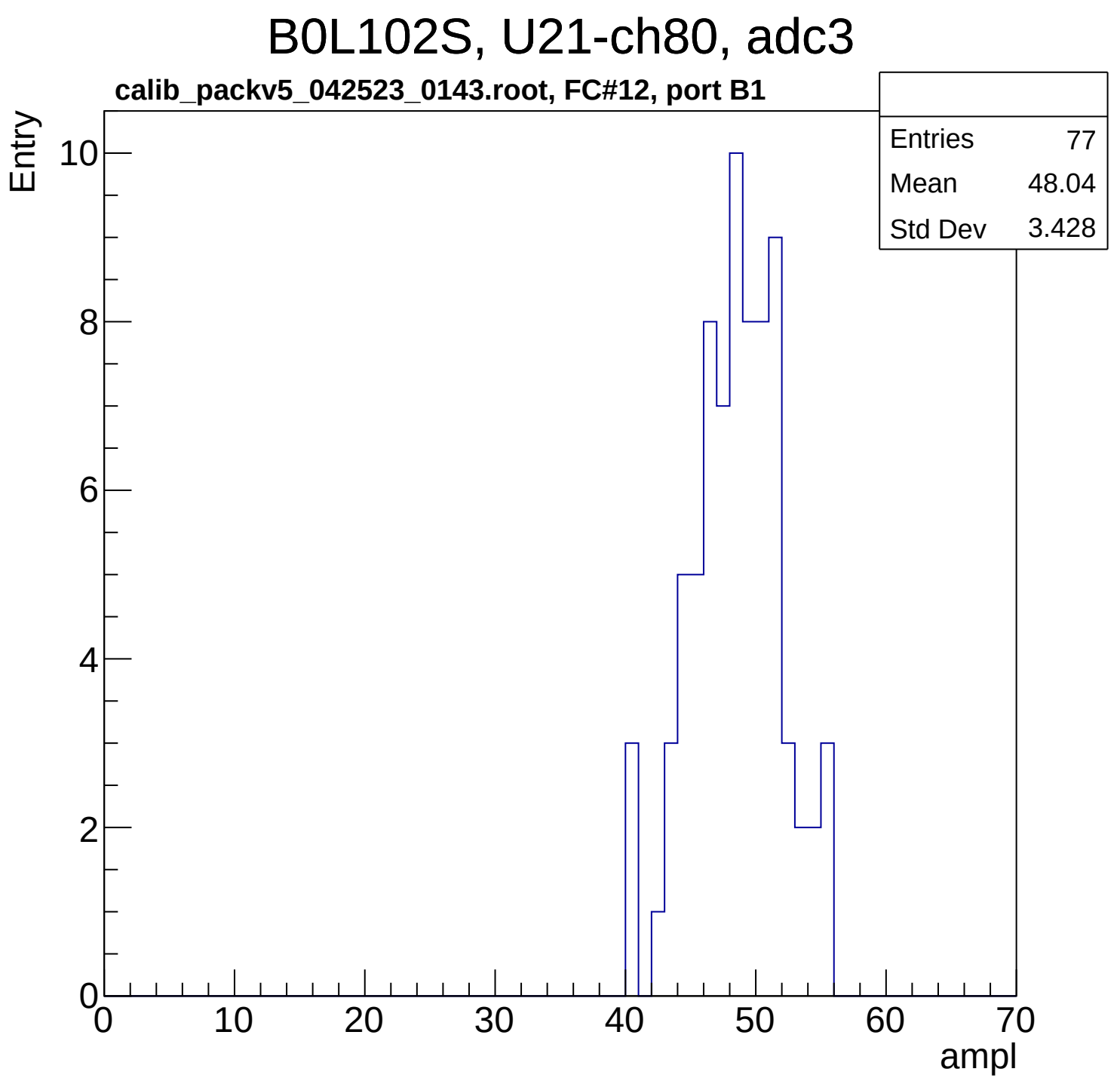

B0L102S, U21-ch80, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 40.86
Std Dev 3.588
Gaus mean : 41.3621
Gaus Width: 3.3375
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch80, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 48.04
Std Dev 3.428
Entry
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4
6
8
10
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10
20
30
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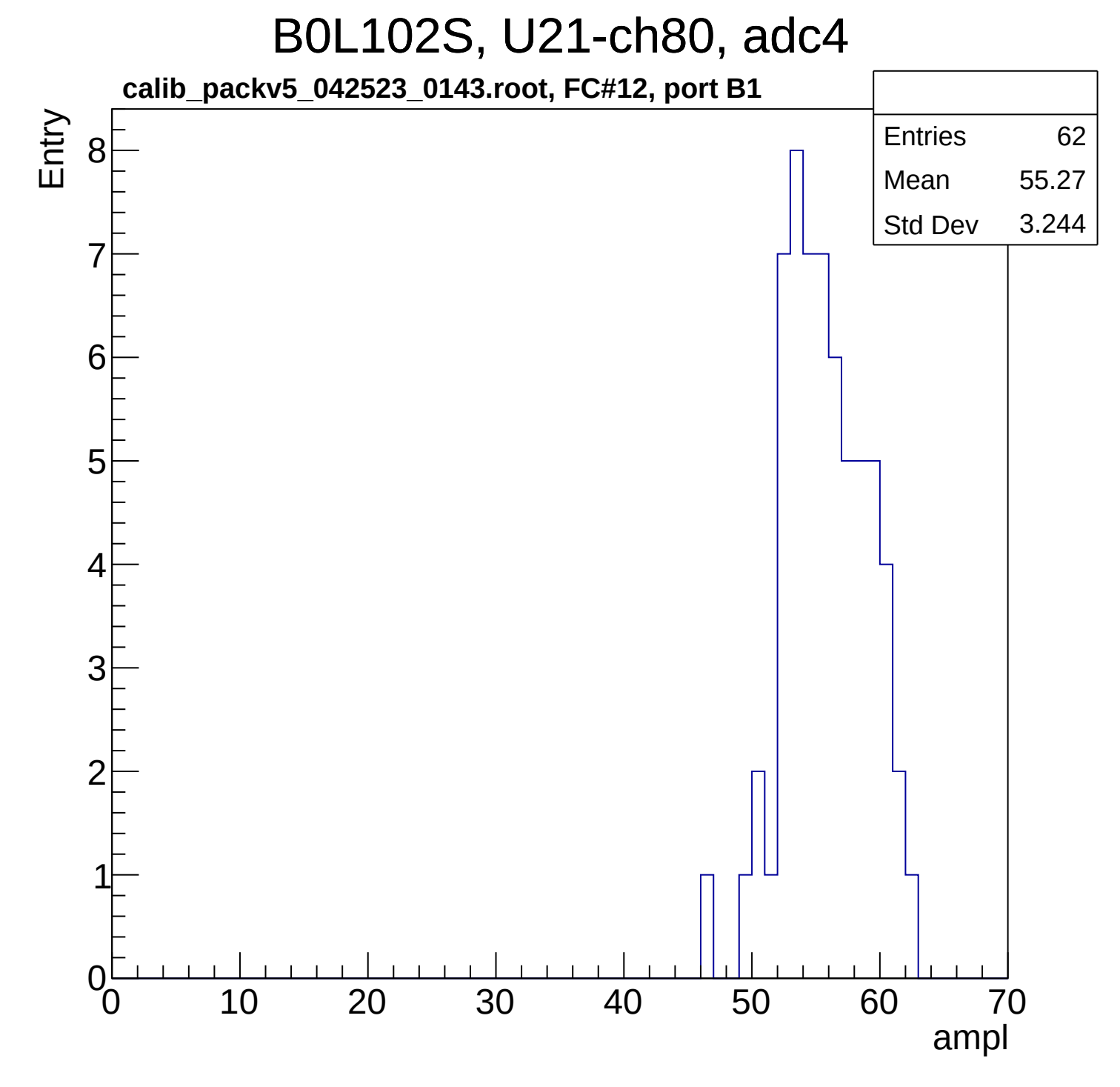
50
60
70
ampl


B0L102S, U21-ch80, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 55.27
Std Dev 3.244
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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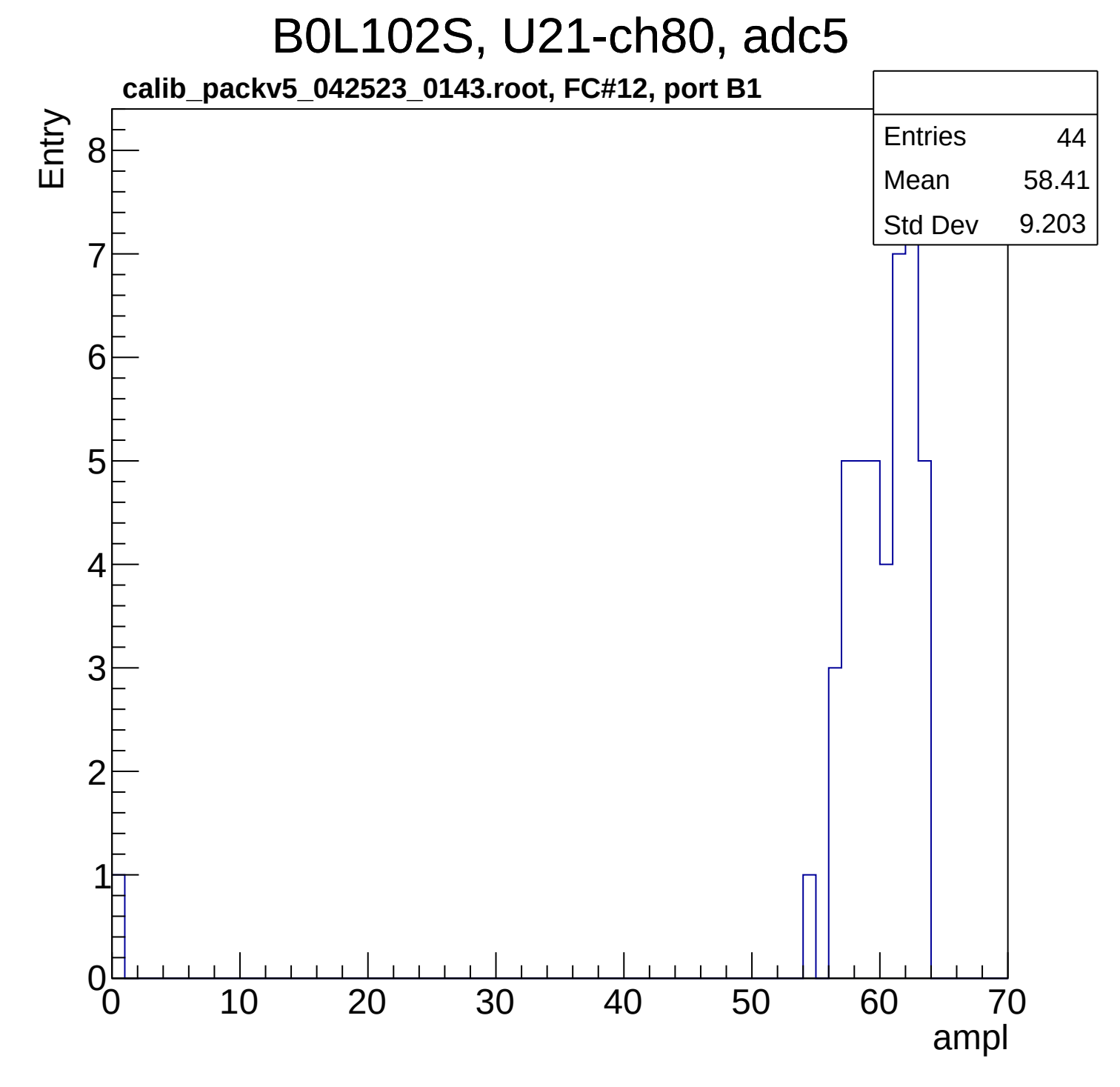
B0L102S, U21-ch80, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.41
Std Dev 9.203
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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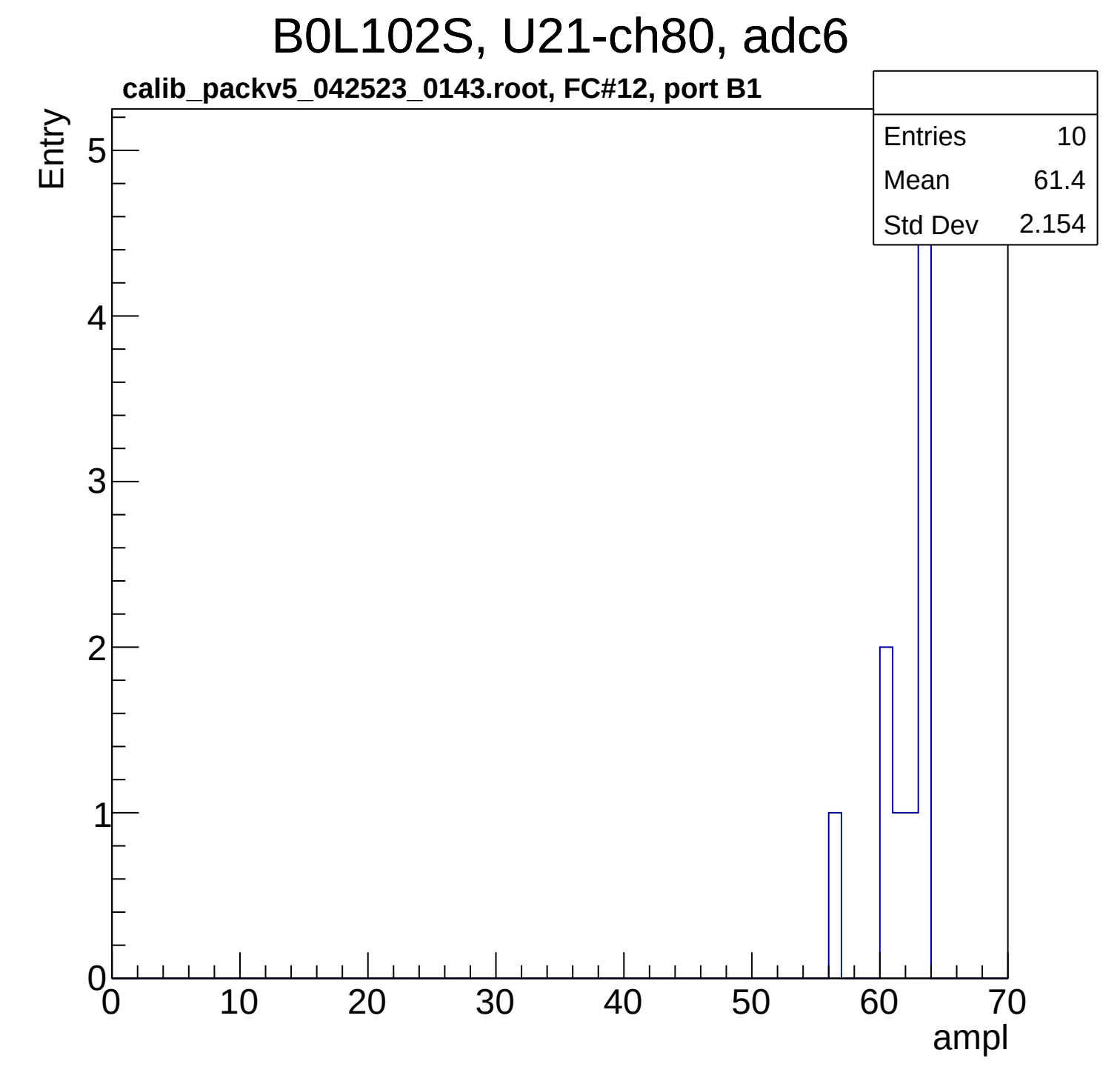
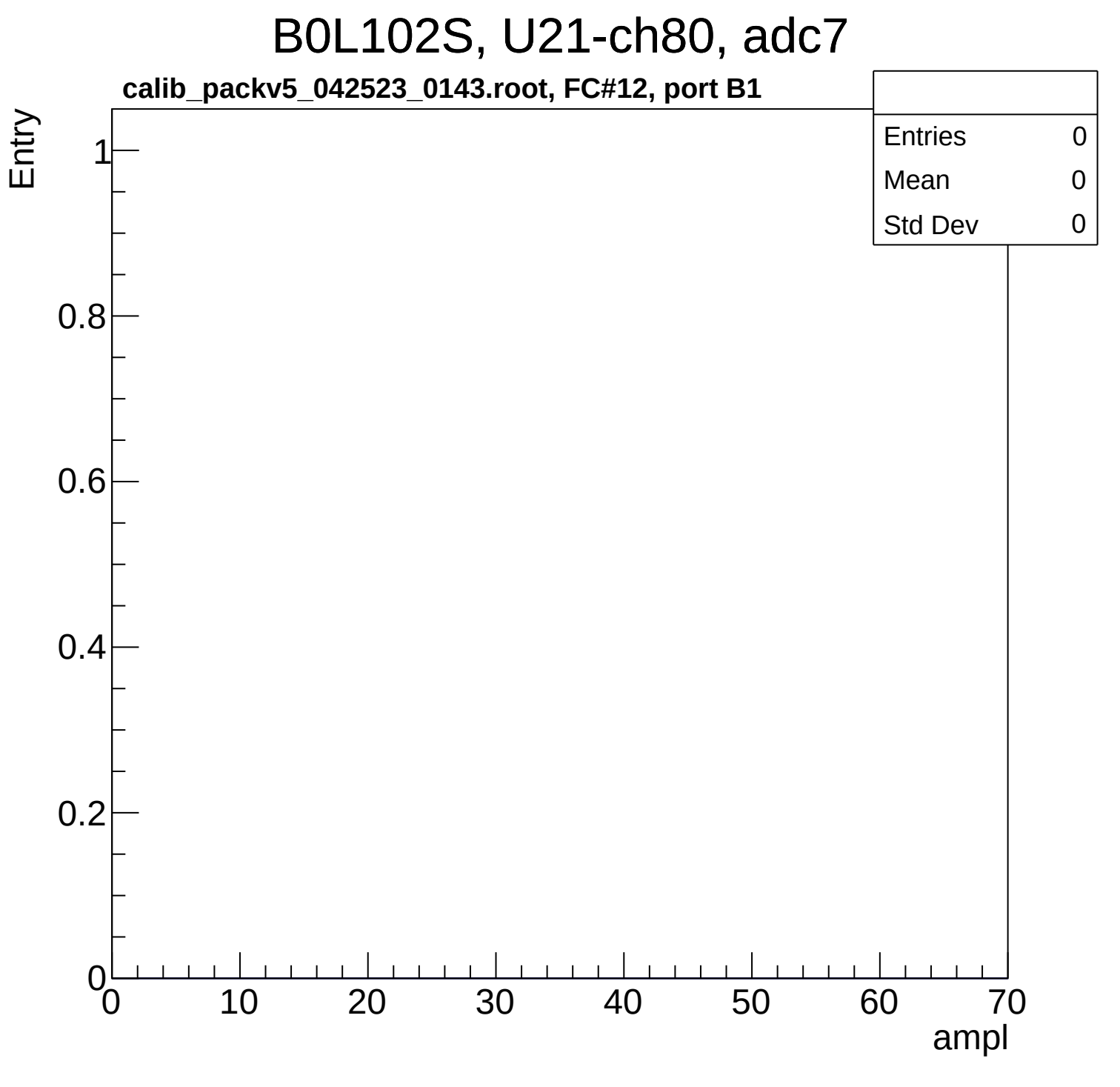

B0L102S, U21-ch80, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 61.4
Std Dev 2.154
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch80, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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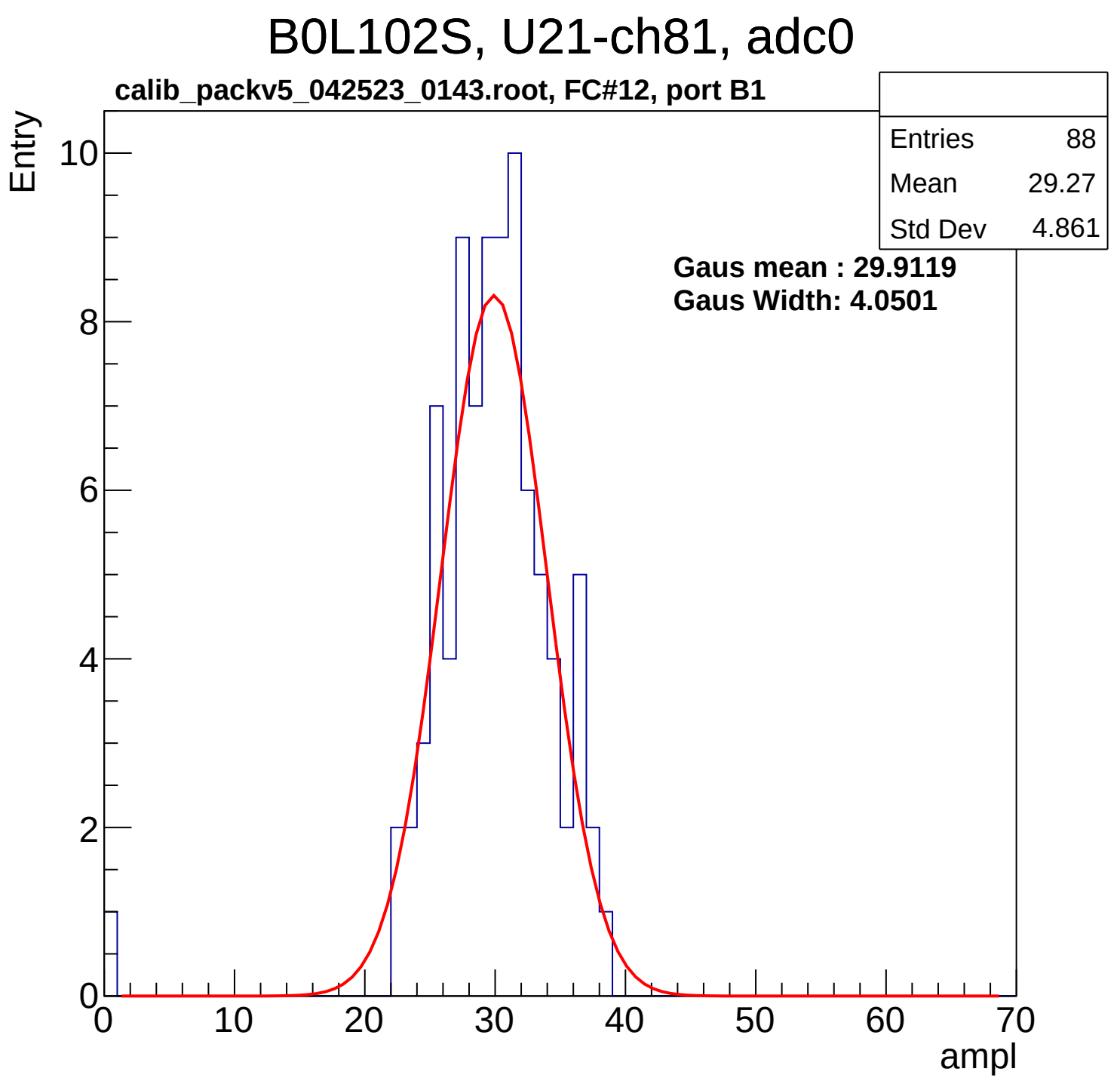
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch81, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 29.27
Std Dev 4.861
Gaus mean : 29.9119
Gaus Width: 4.0501
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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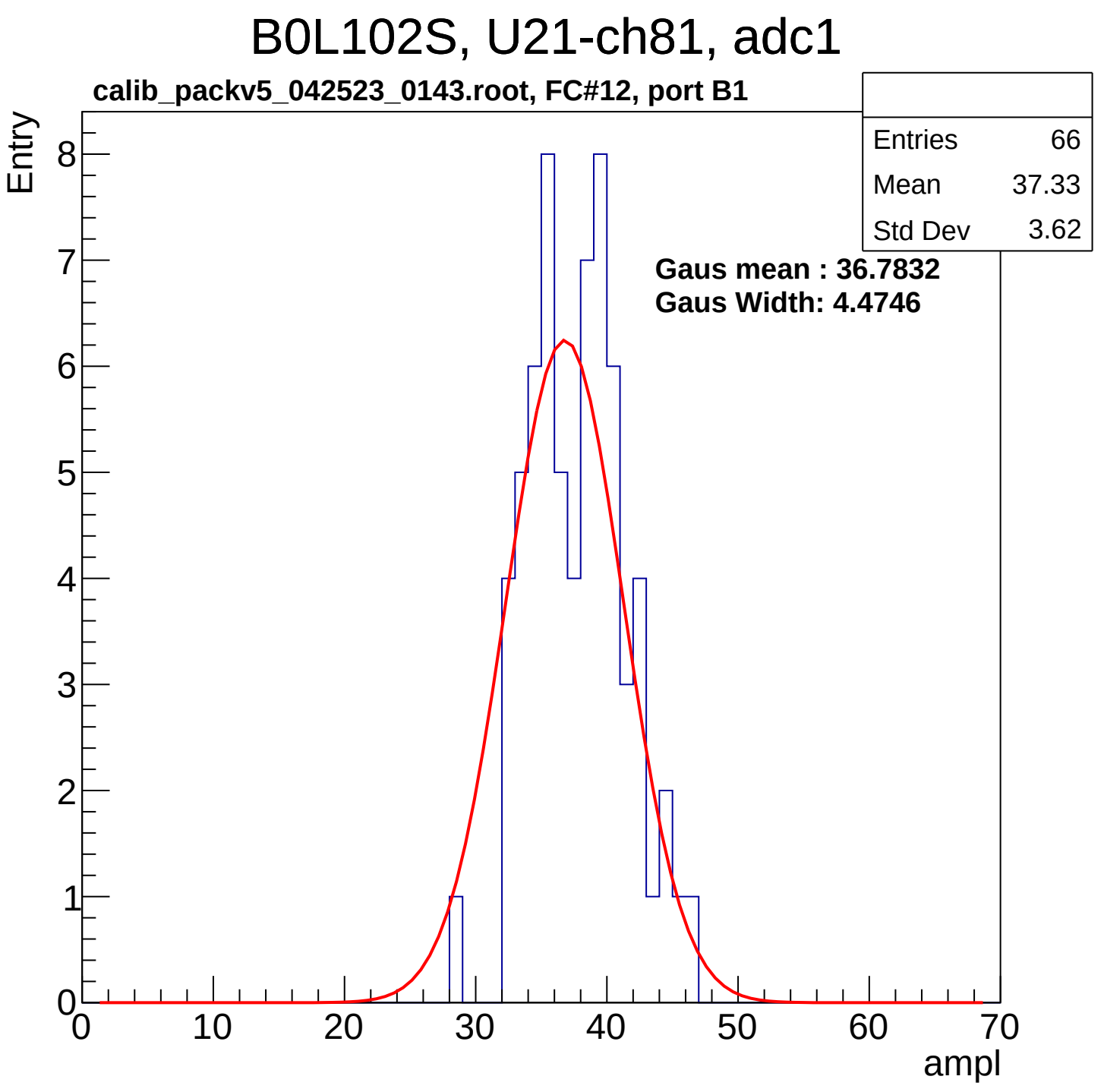
B0L102S, U21-ch81, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 37.33
Std Dev 3.62
Gaus mean : 36.7832
Gaus Width: 4.4746
Entry
ampl
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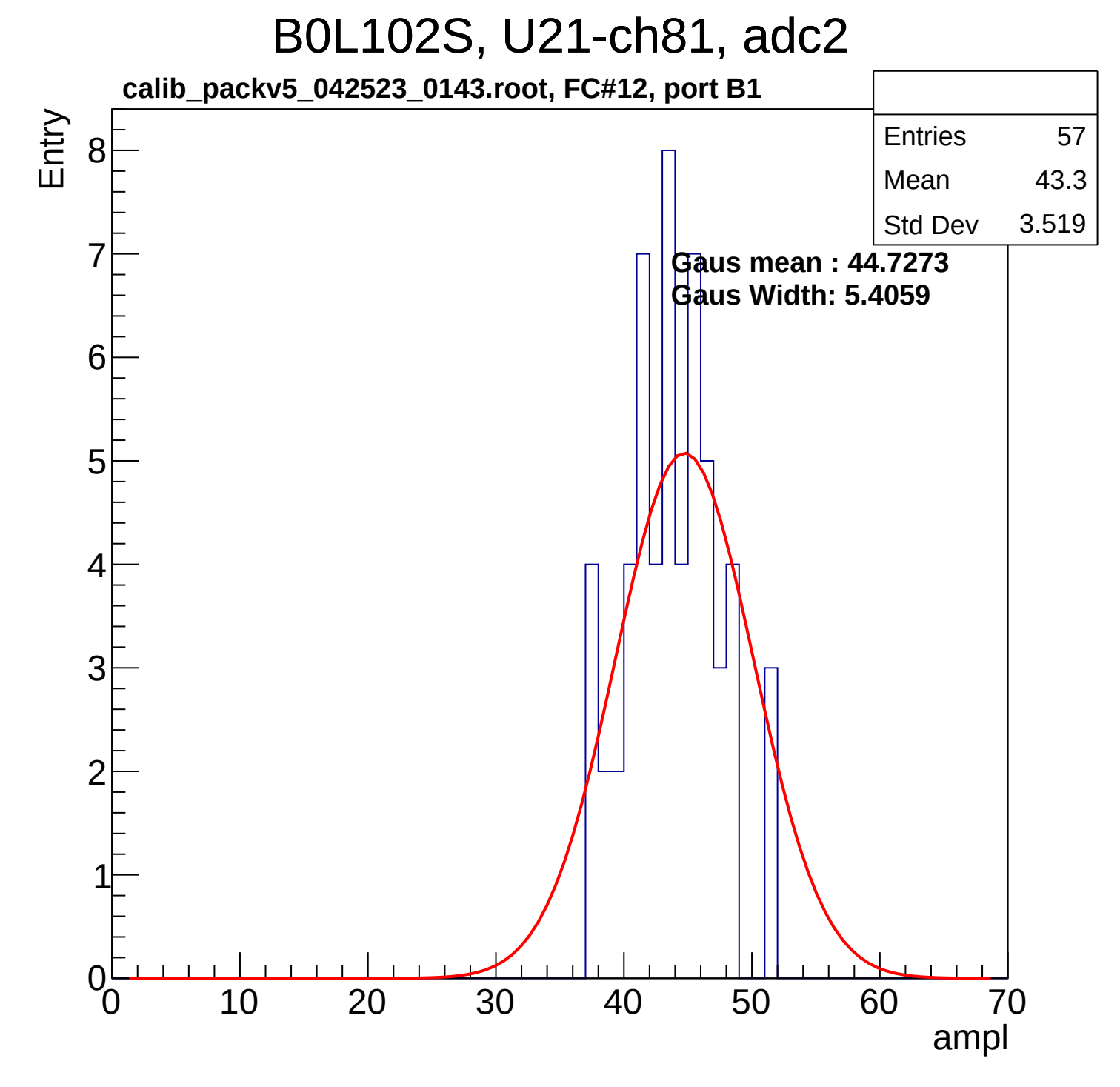
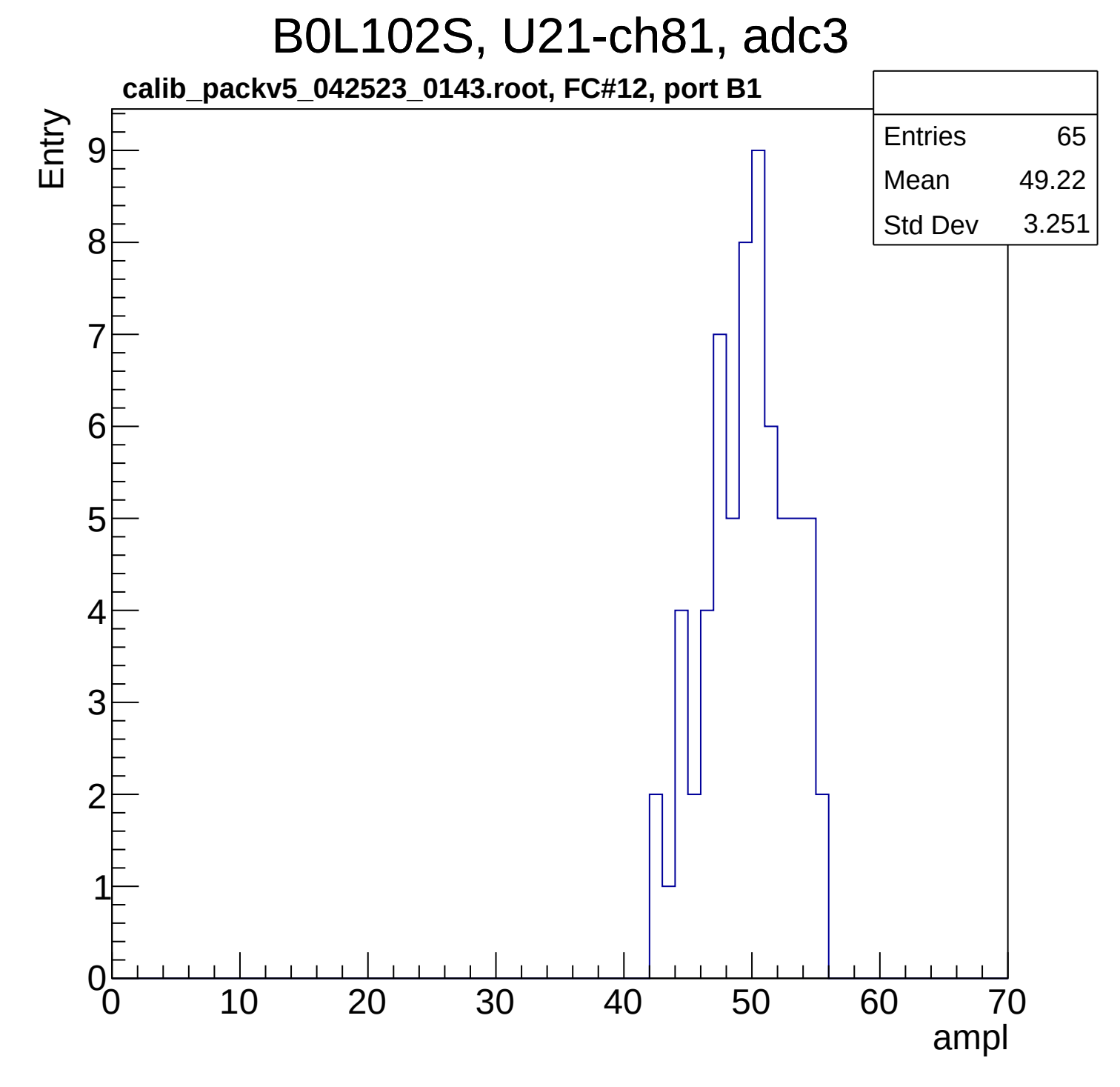
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch81, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.3
Std Dev 3.519
Gaus mean : 44.7273
Gaus Width: 5.4059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch81, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 49.22
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
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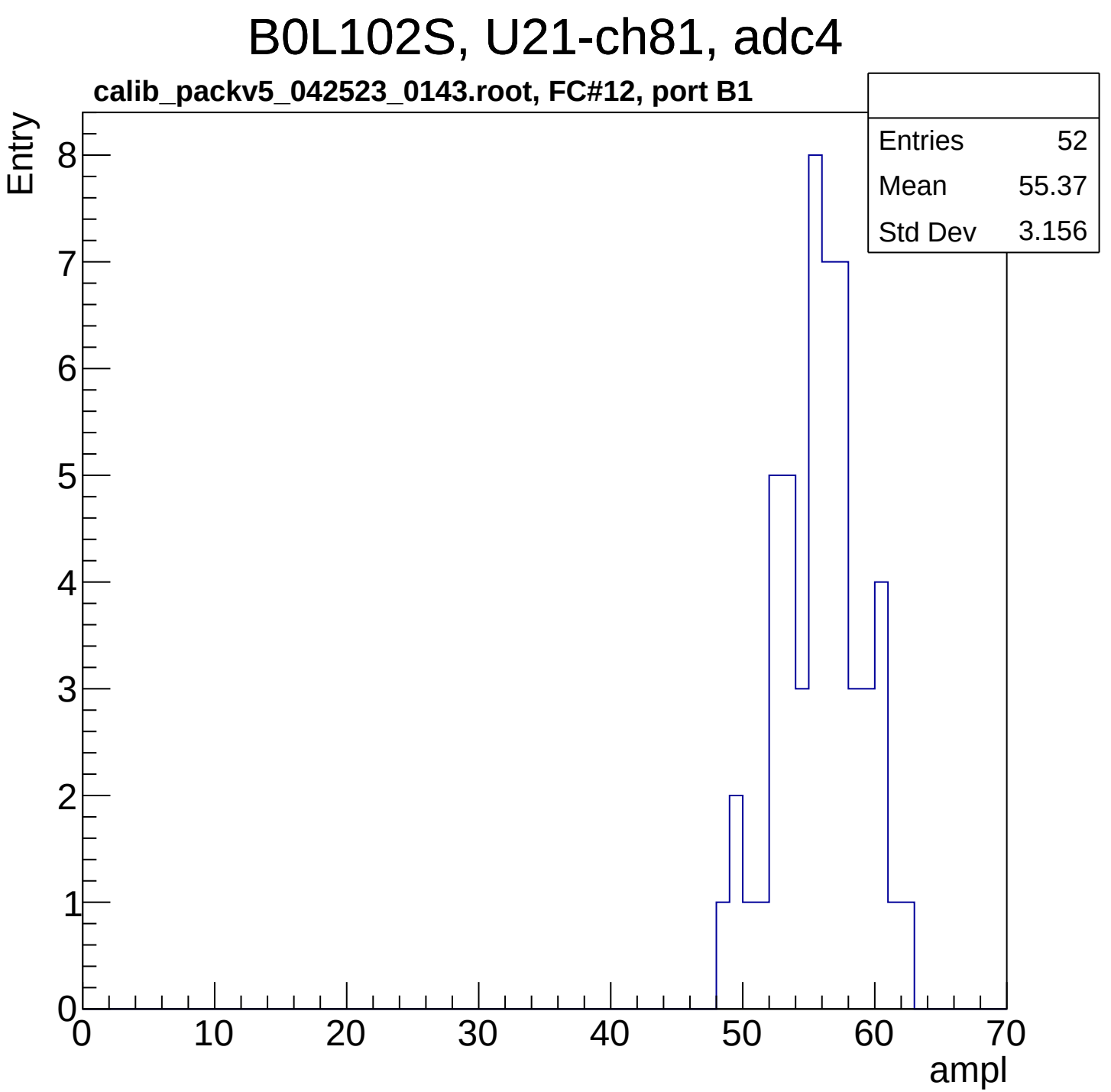
B0L102S, U21-ch81, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 55.37
Std Dev 3.156
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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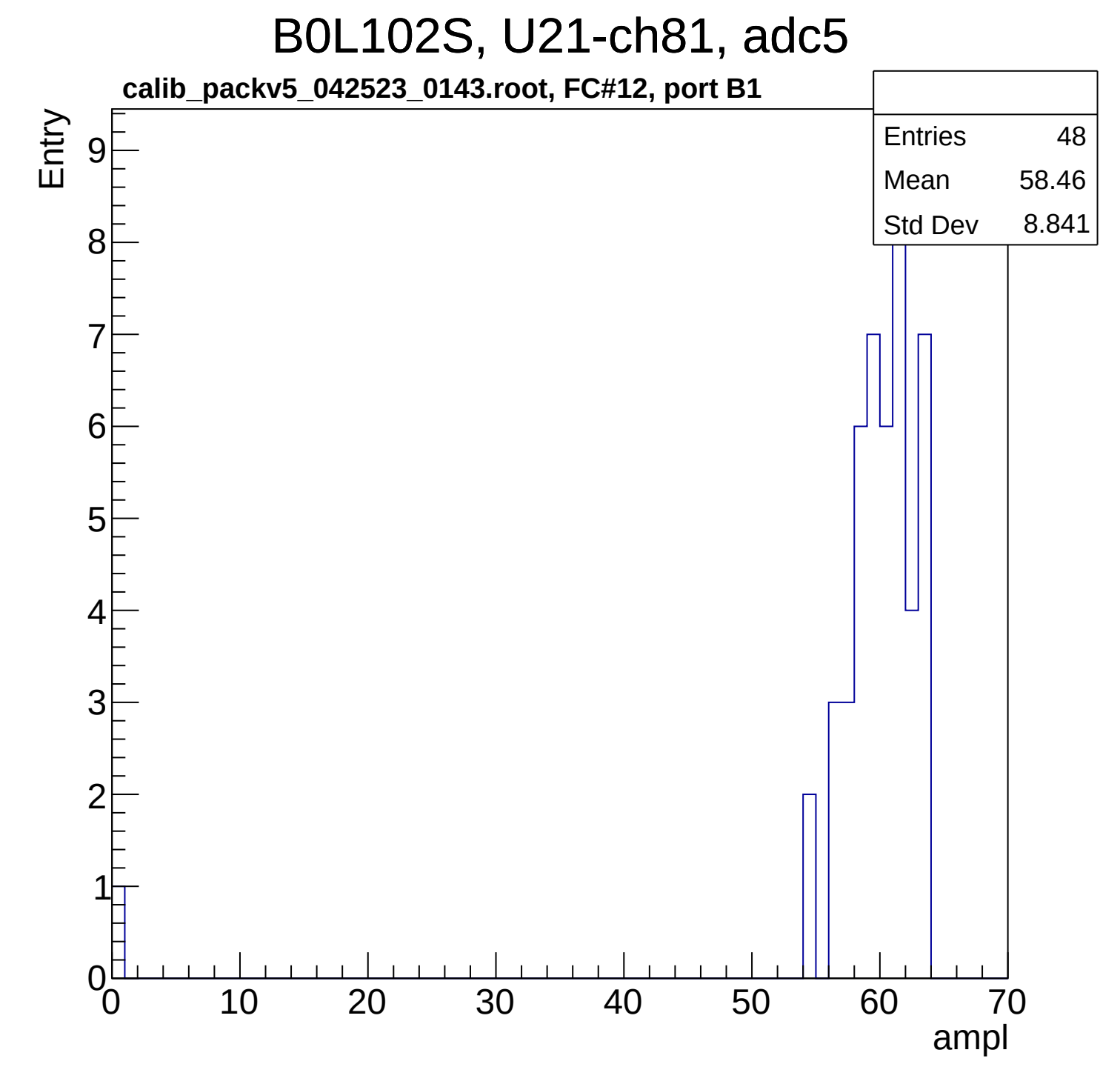
B0L102S, U21-ch81, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.46
Std Dev 8.841
Entry
ampl
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2
3
4
5
6
7
8
9
0
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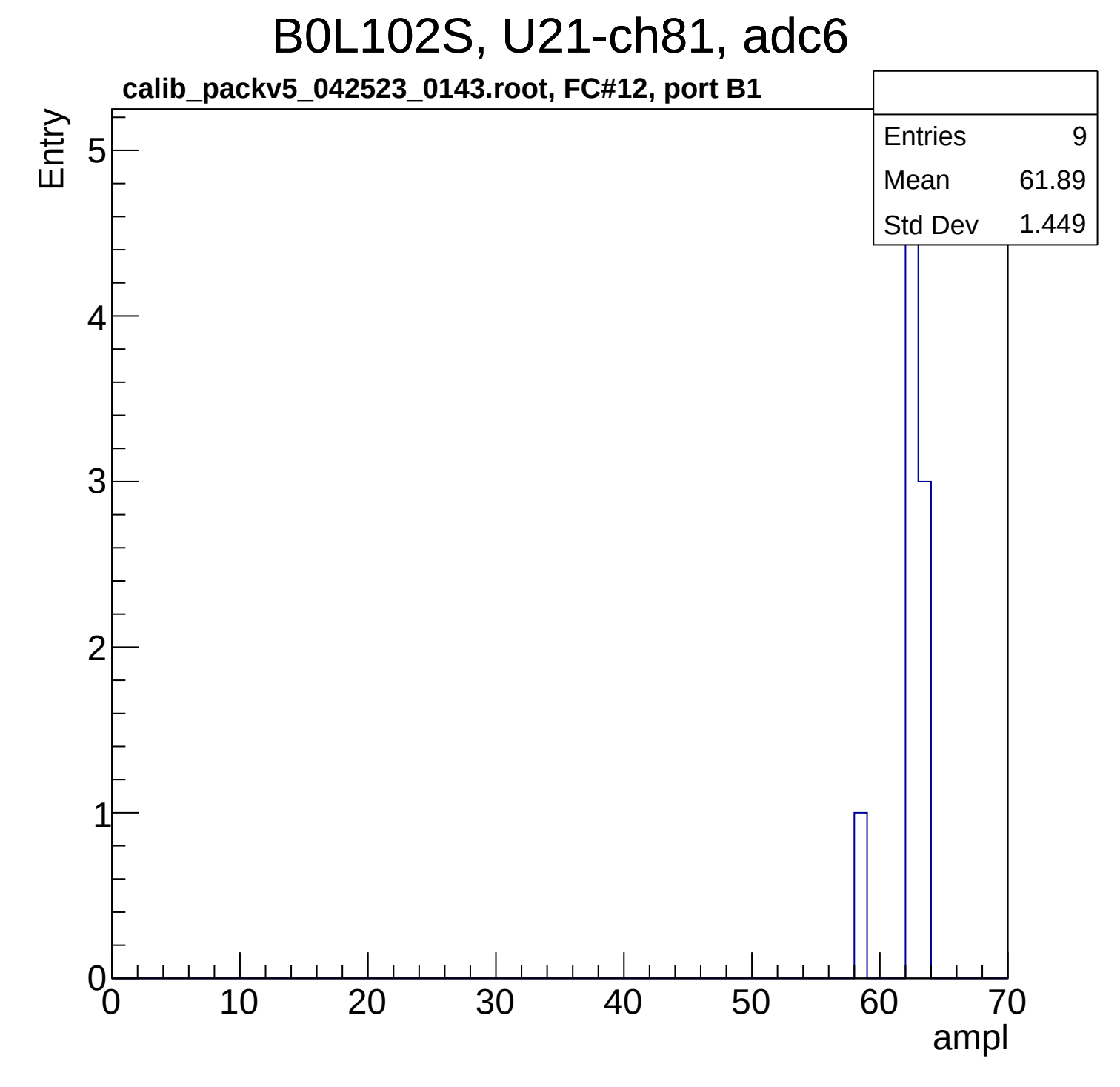
B0L102S, U21-ch81, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.89
Std Dev 1.449
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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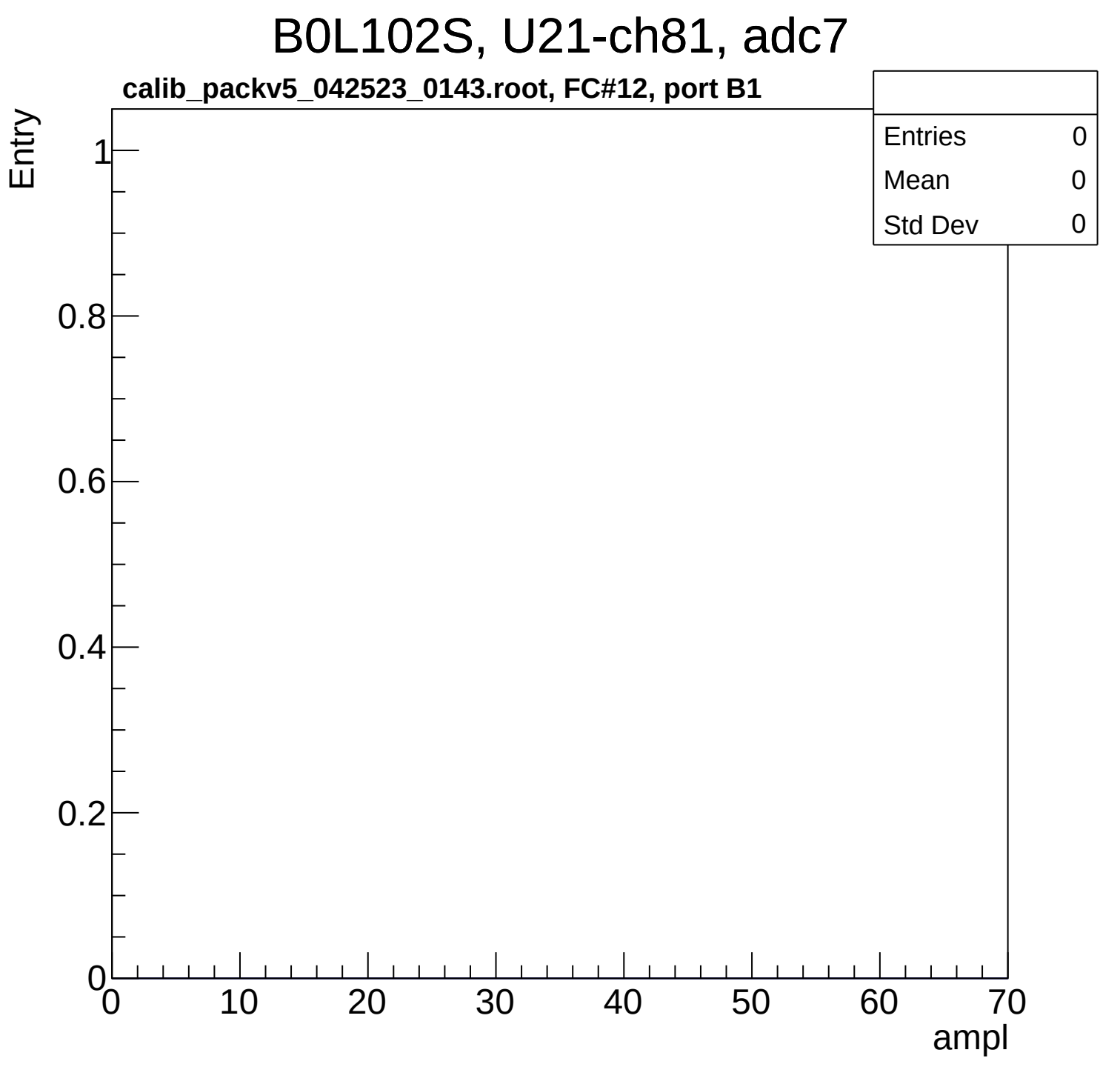
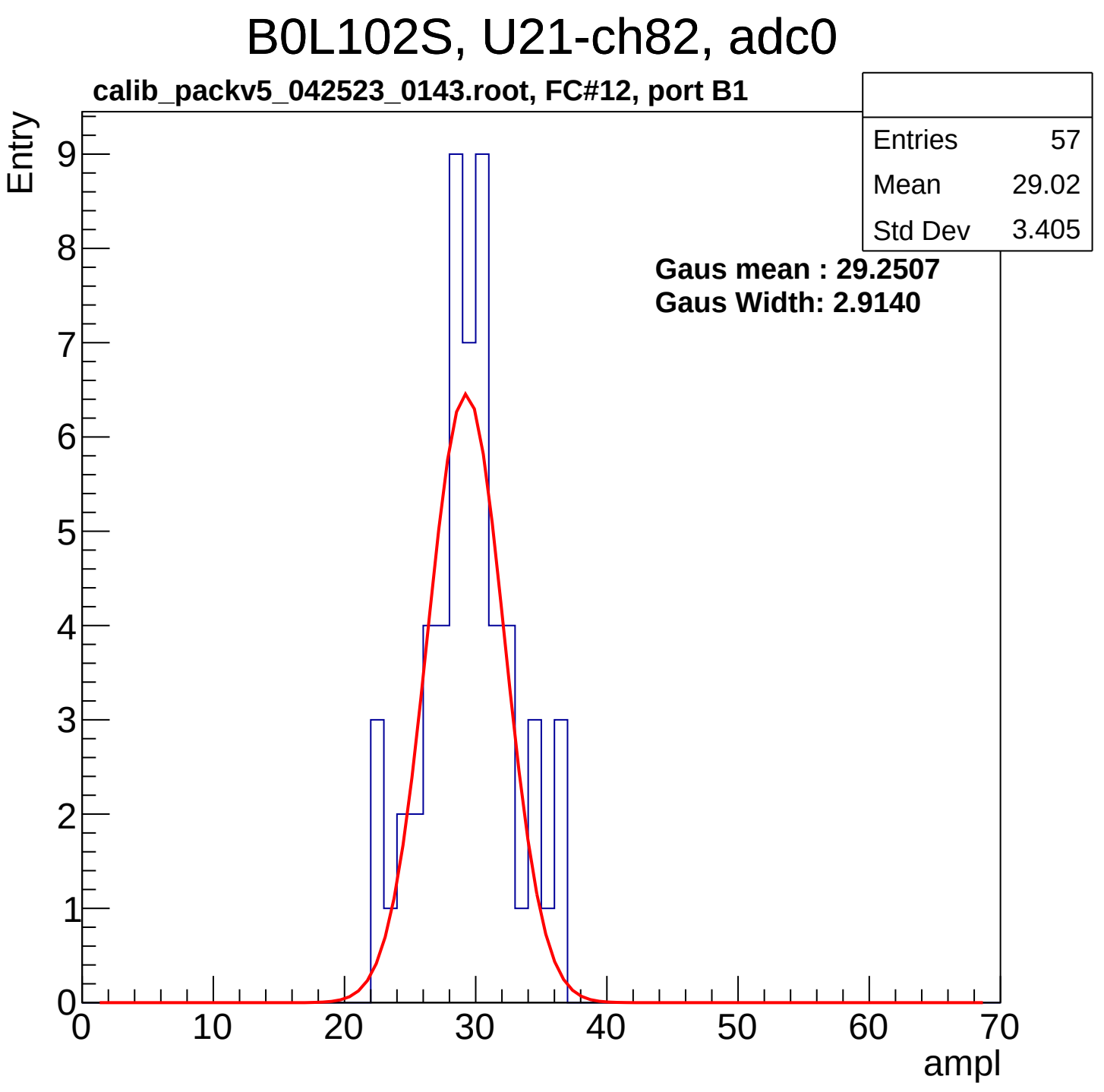

B0L102S, U21-ch81, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch82, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 29.02
Std Dev 3.405
Gaus mean : 29.2507
Gaus Width: 2.9140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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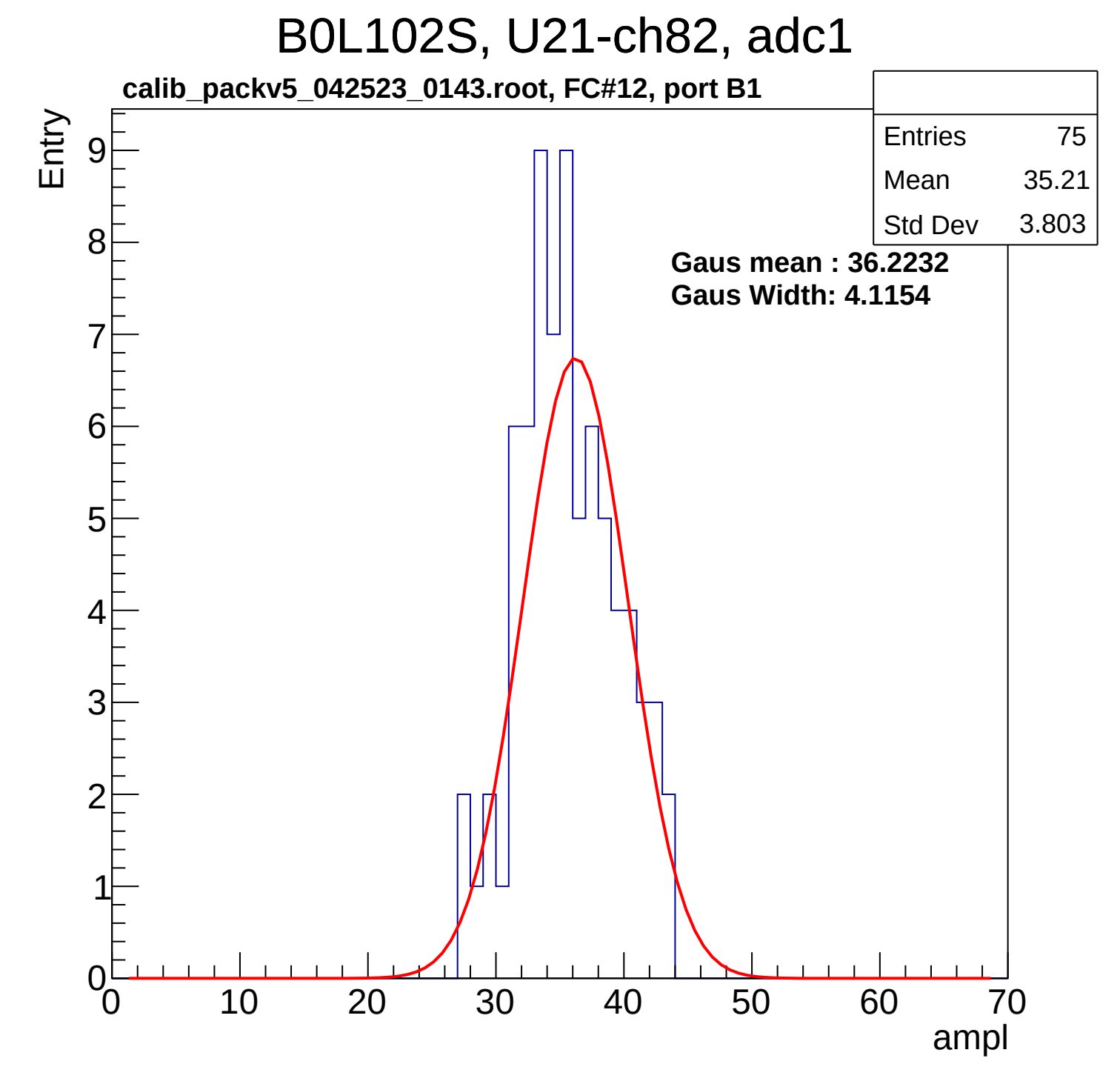
B0L102S, U21-ch82, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 35.21
Std Dev 3.803
Gaus mean : 36.2232
Gaus Width: 4.1154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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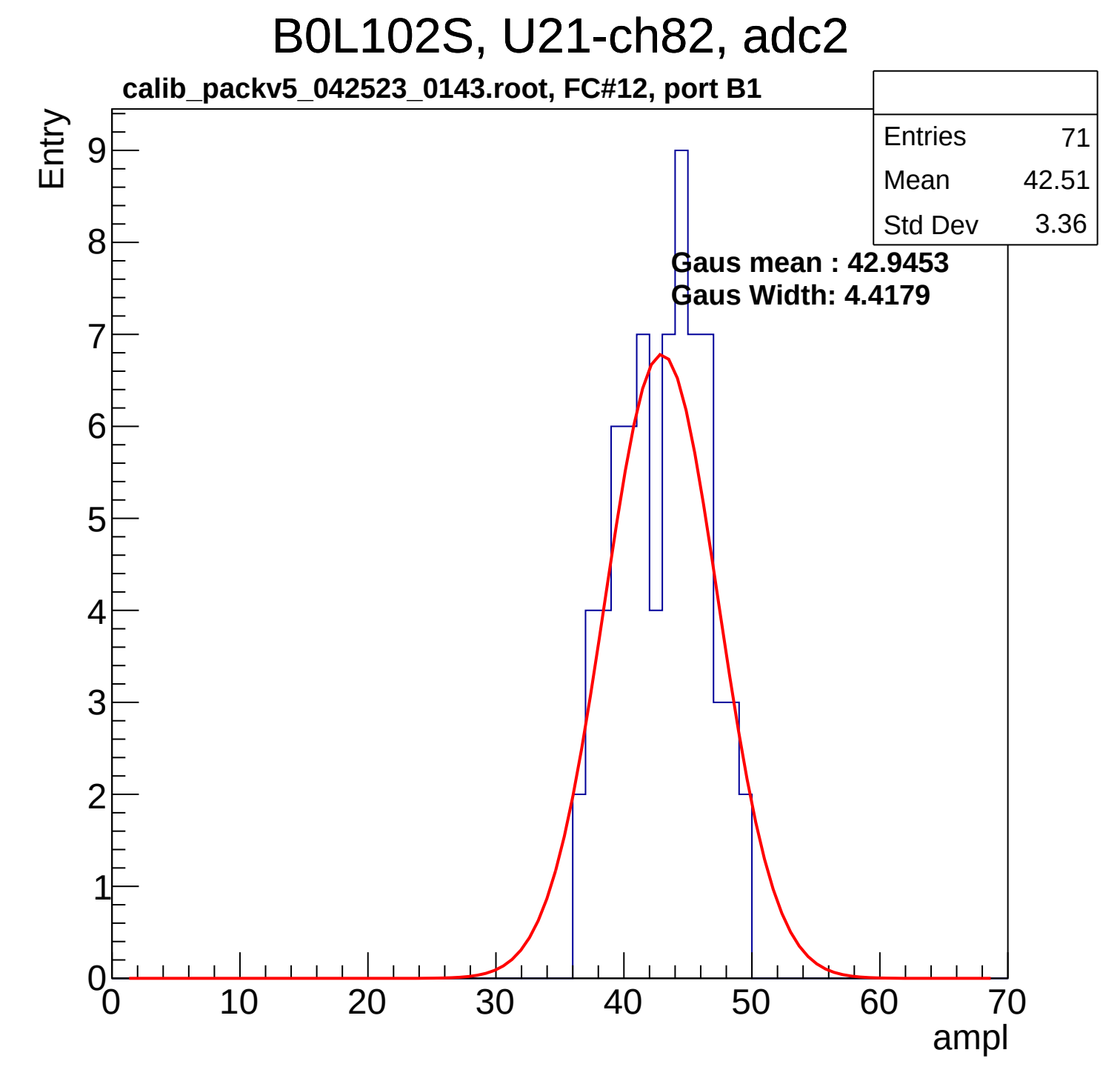
B0L102S, U21-ch82, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 42.51
Std Dev 3.36
Gaus mean : 42.9453
Gaus Width: 4.4179
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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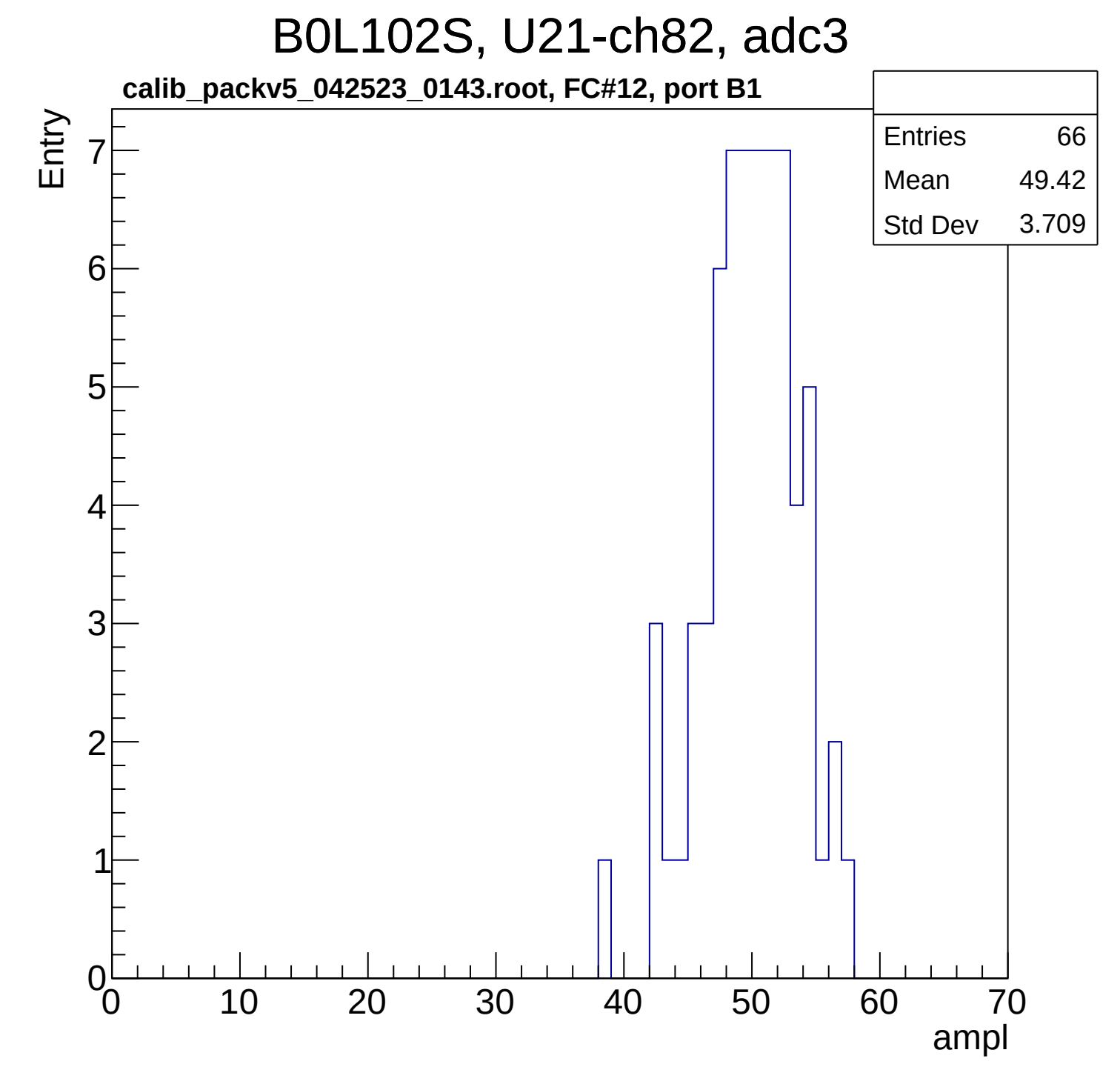
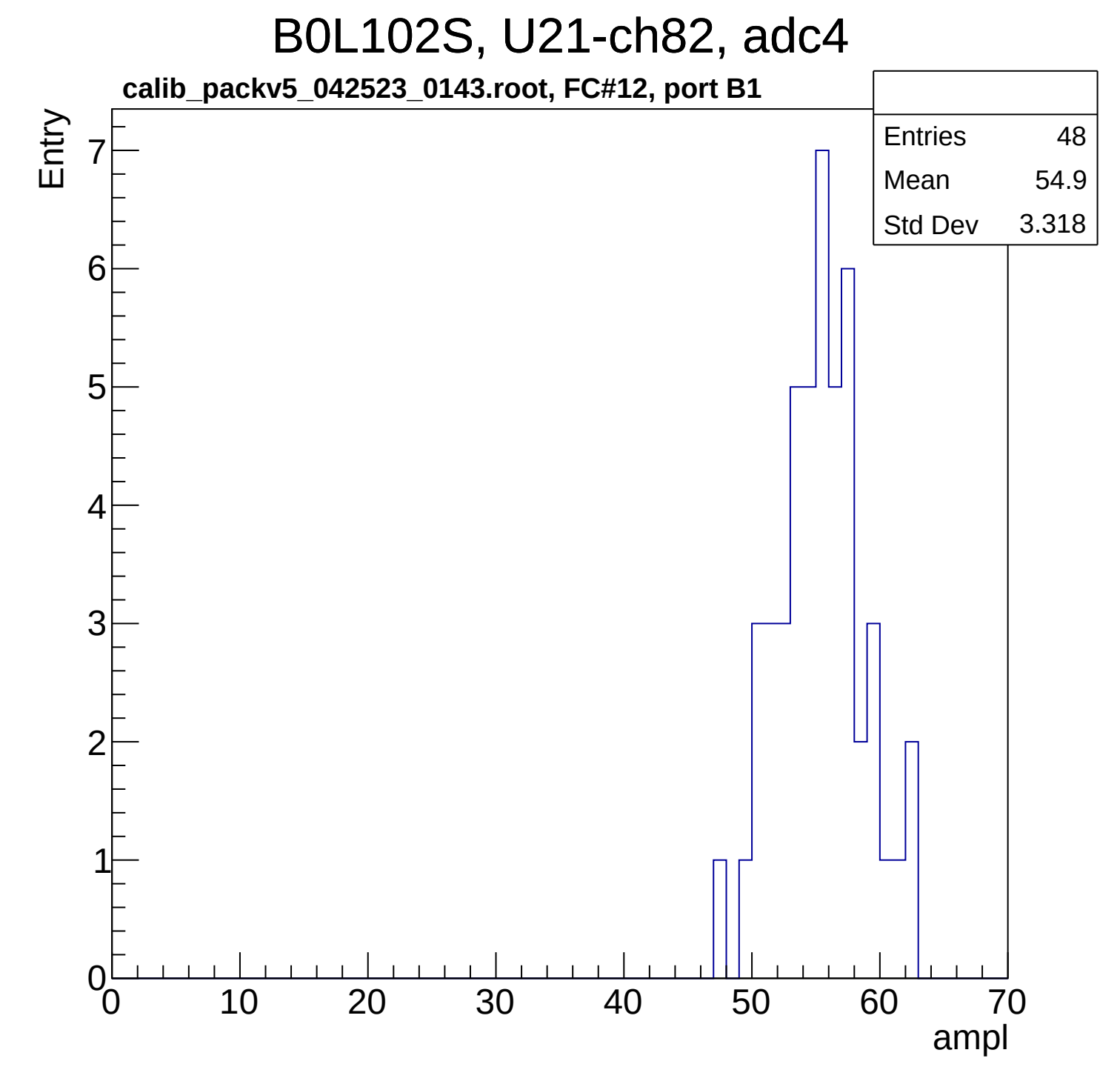

B0L102S, U21-ch82, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.42
Std Dev 3.709
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch82, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 54.9
Std Dev 3.318
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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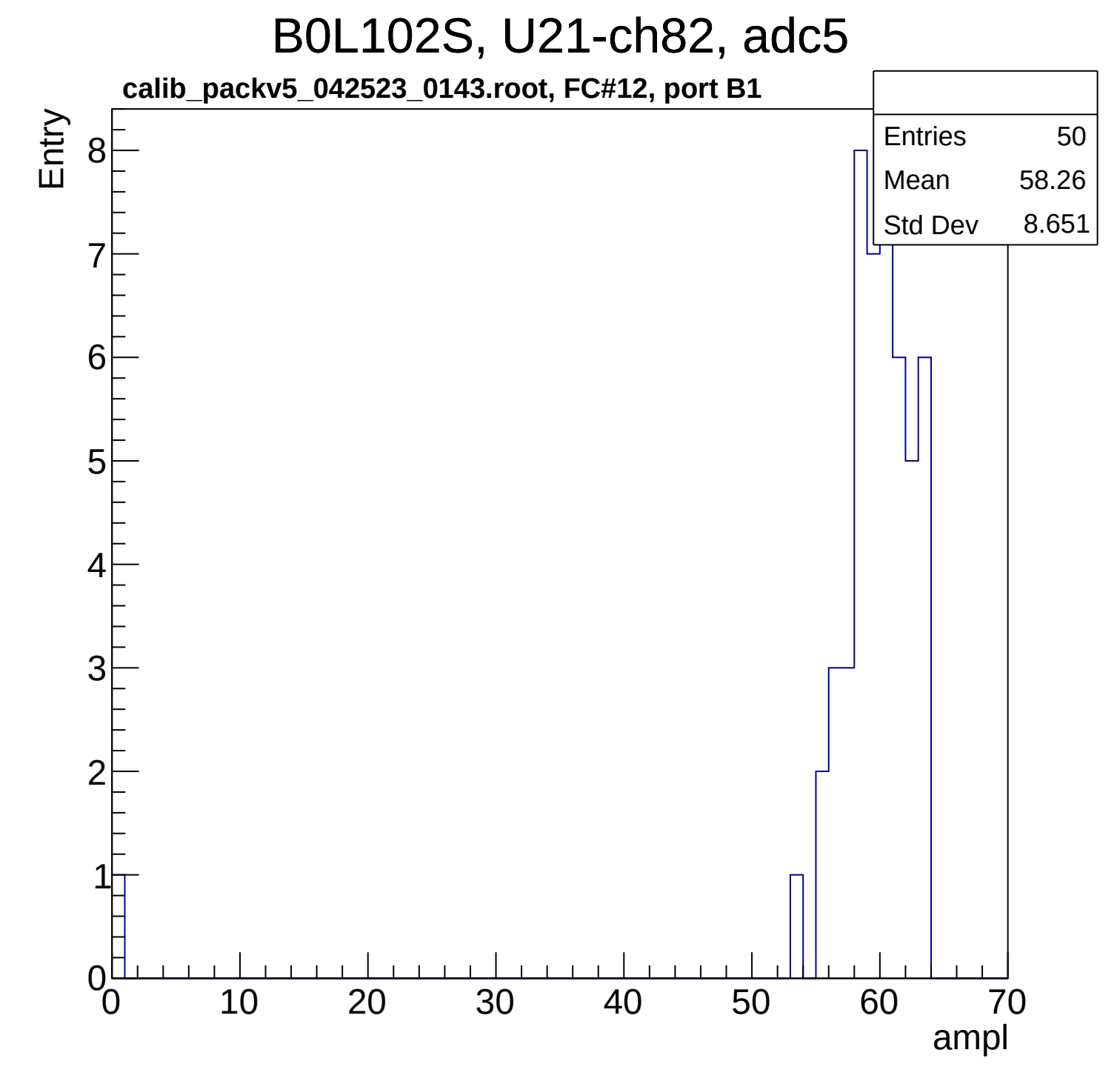
B0L102S, U21-ch82, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.26
Std Dev 8.651
Entry
ampl
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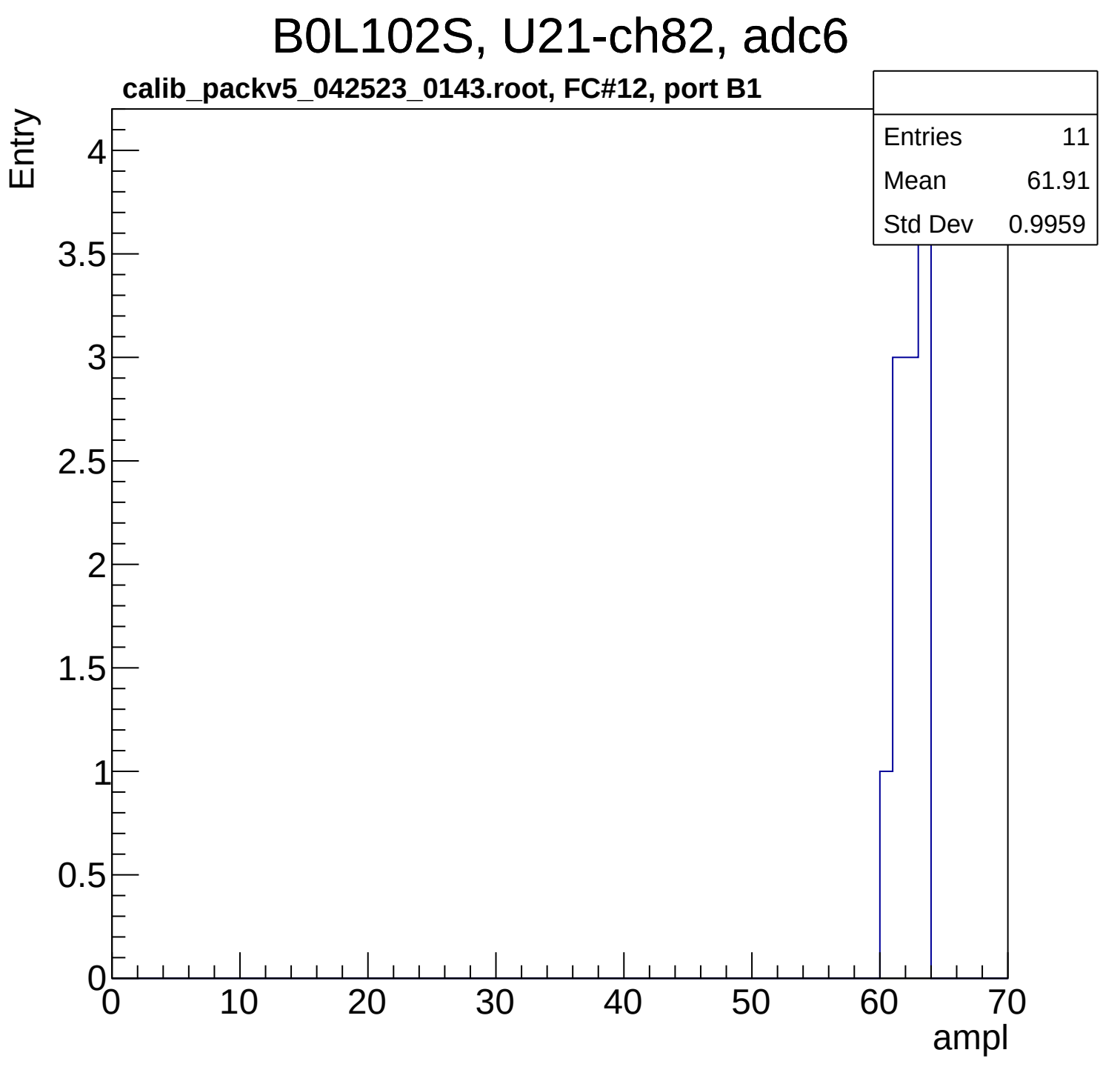
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch82, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 61.91
Std Dev 0.9959
Entry
ampl
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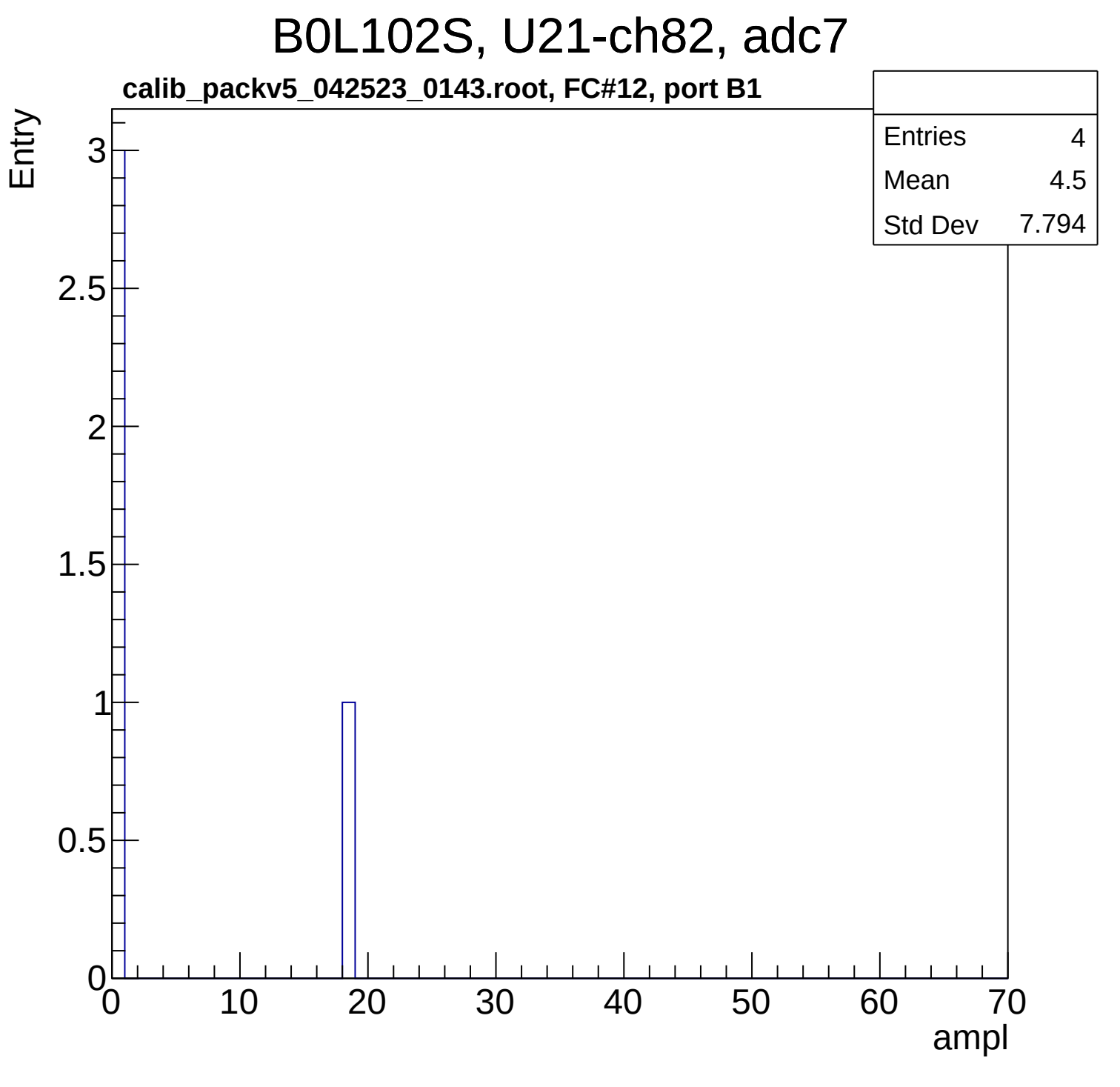
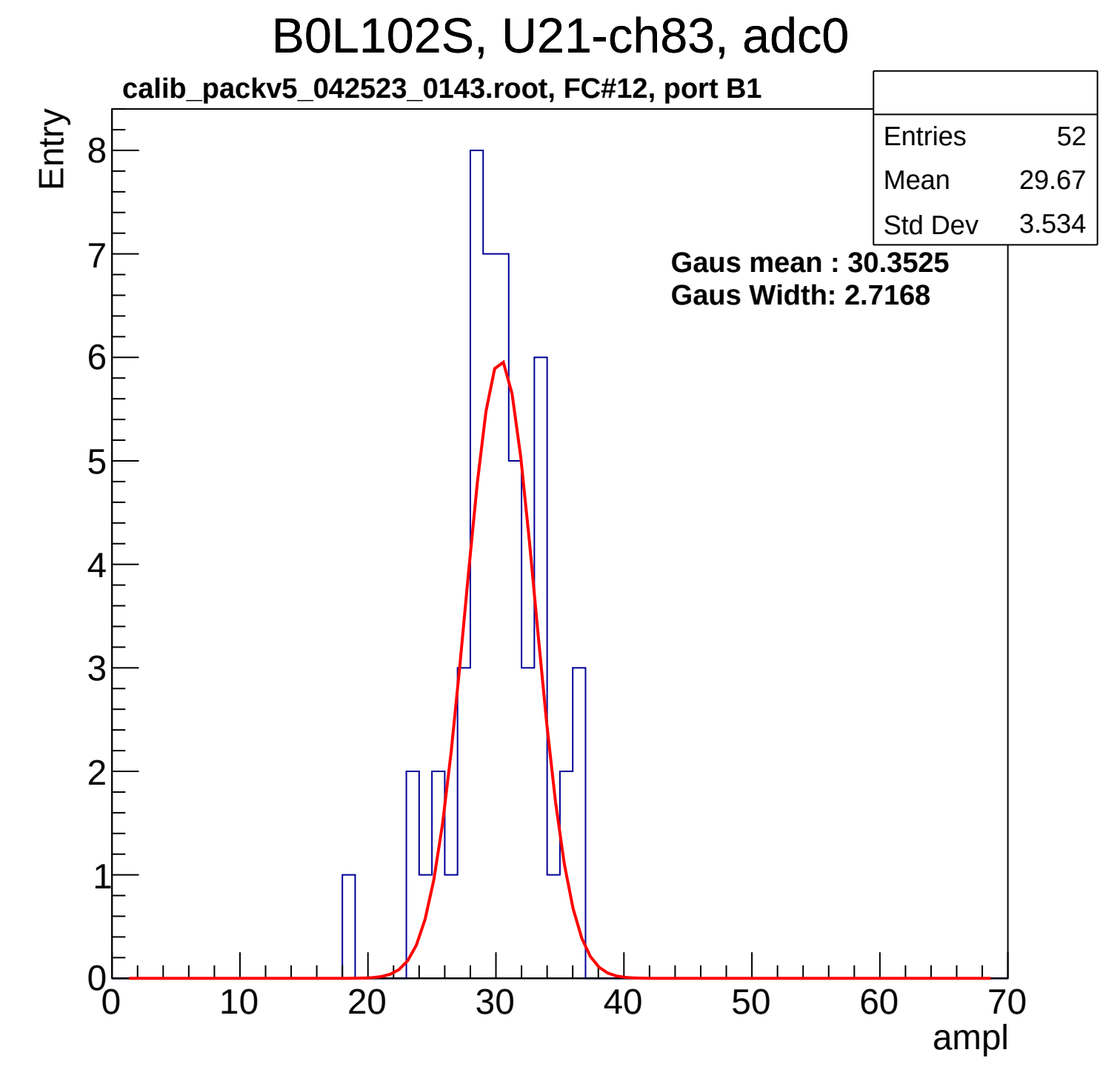
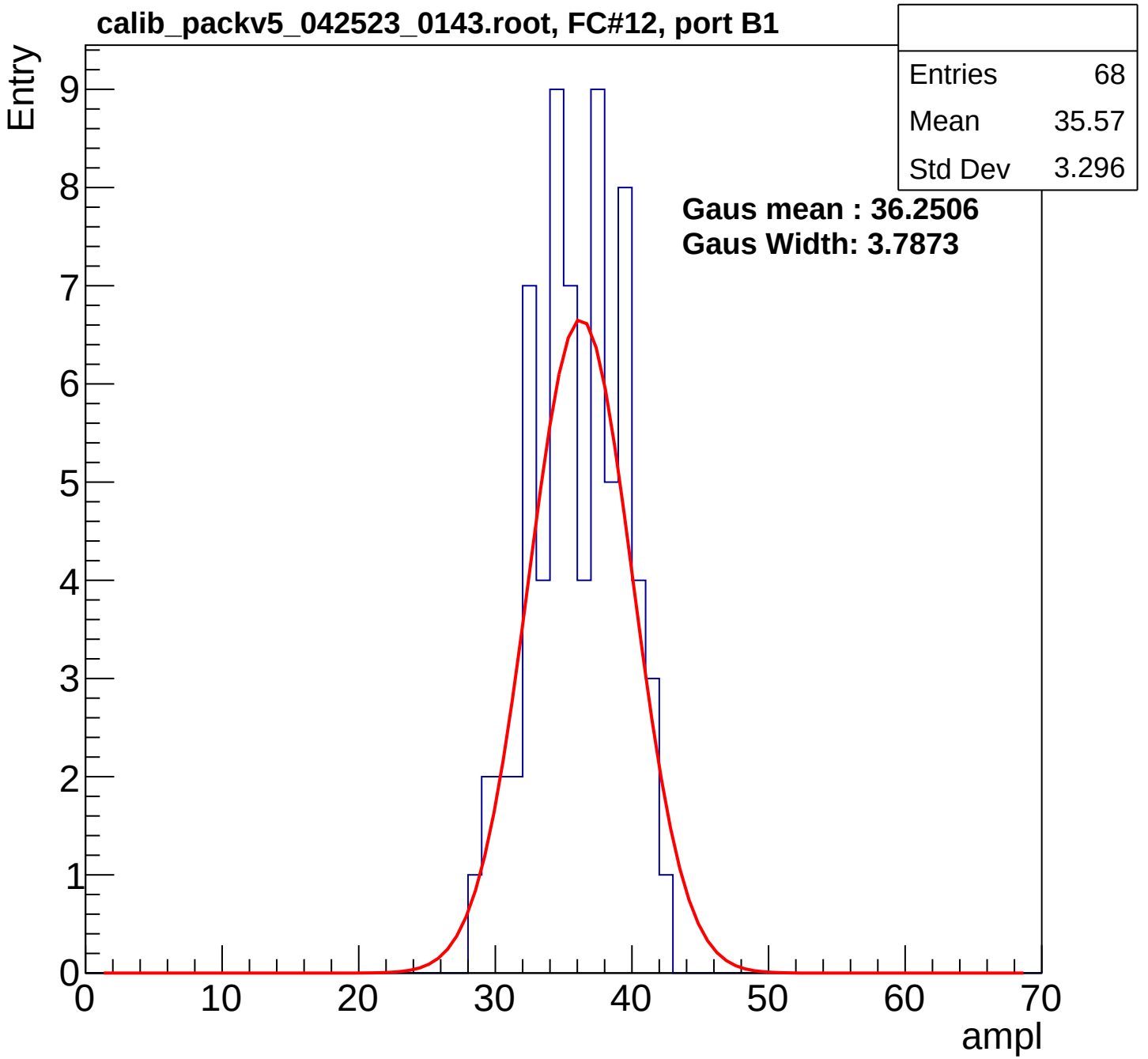
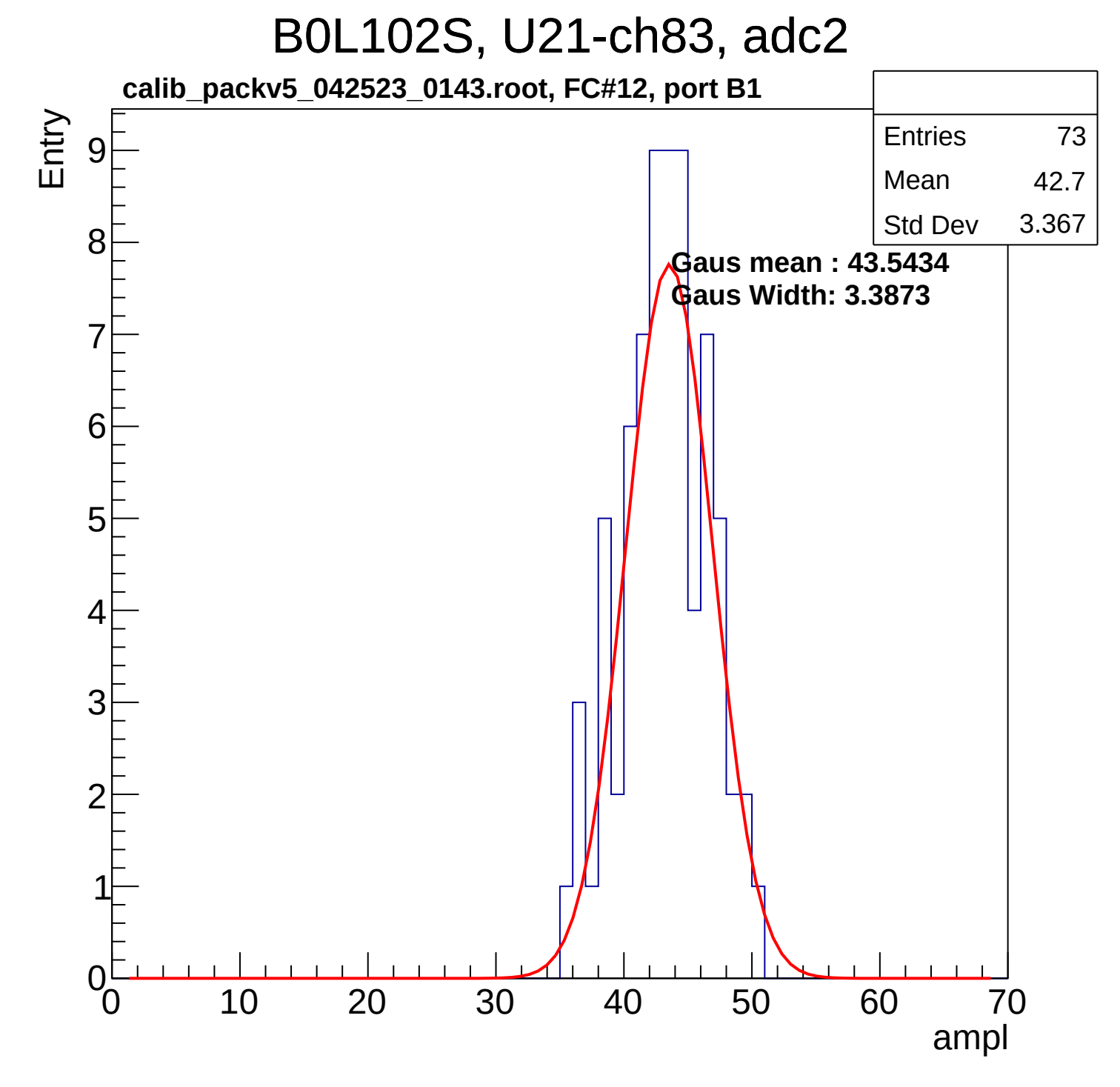
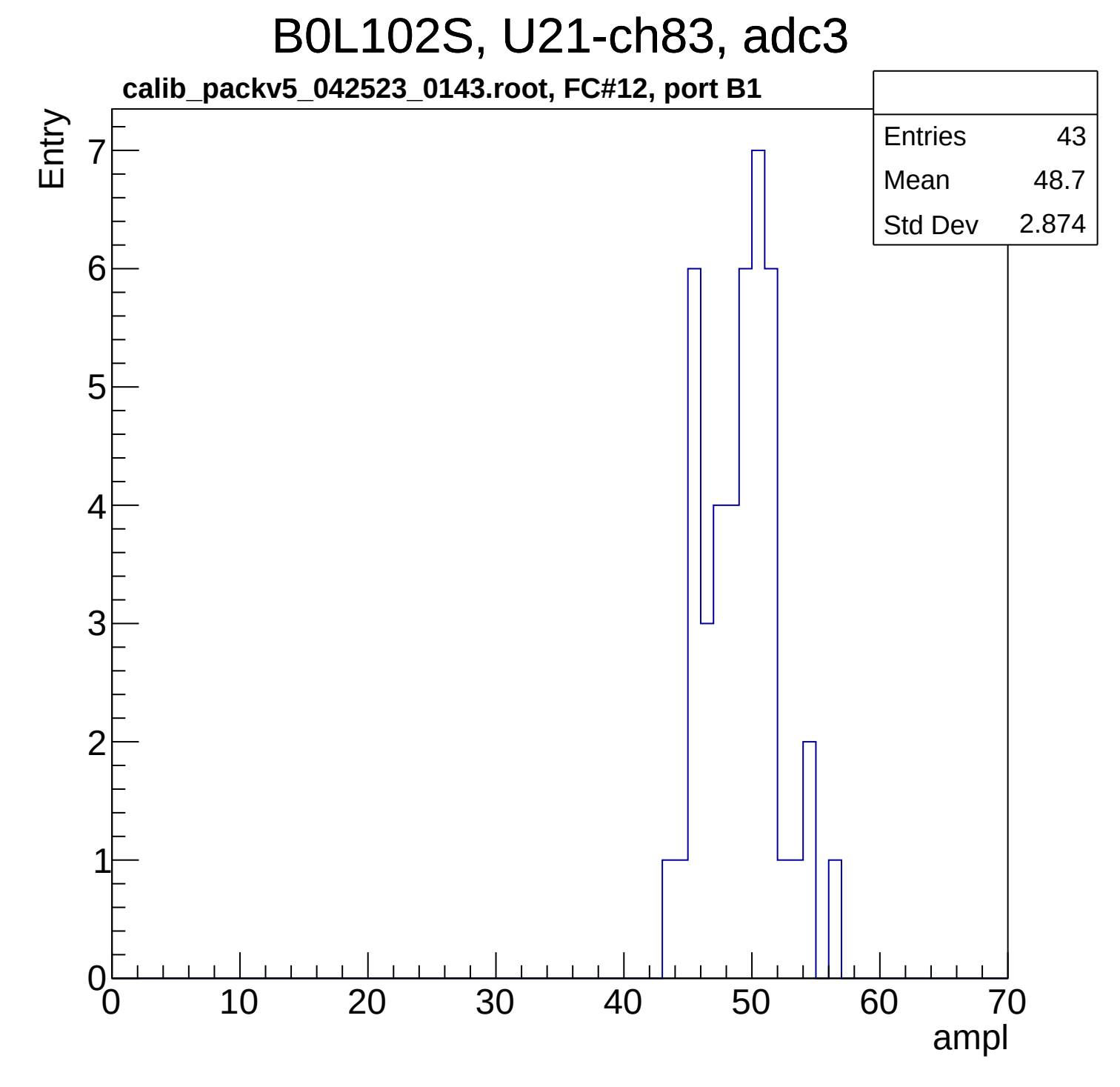
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch82, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 4.5
Std Dev 7.794
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch83, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 29.67
Std Dev 3.534
Gaus mean : 30.3525
Gaus Width: 2.7168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch83, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 35.57
Std Dev 3.296
Gaus mean : 36.2506
Gaus Width: 3.7873
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch83, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 42.7
Std Dev 3.367
Gaus mean : 43.5434
Gaus Width: 3.3873
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch83, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 48.7
Std Dev 2.874
Entry
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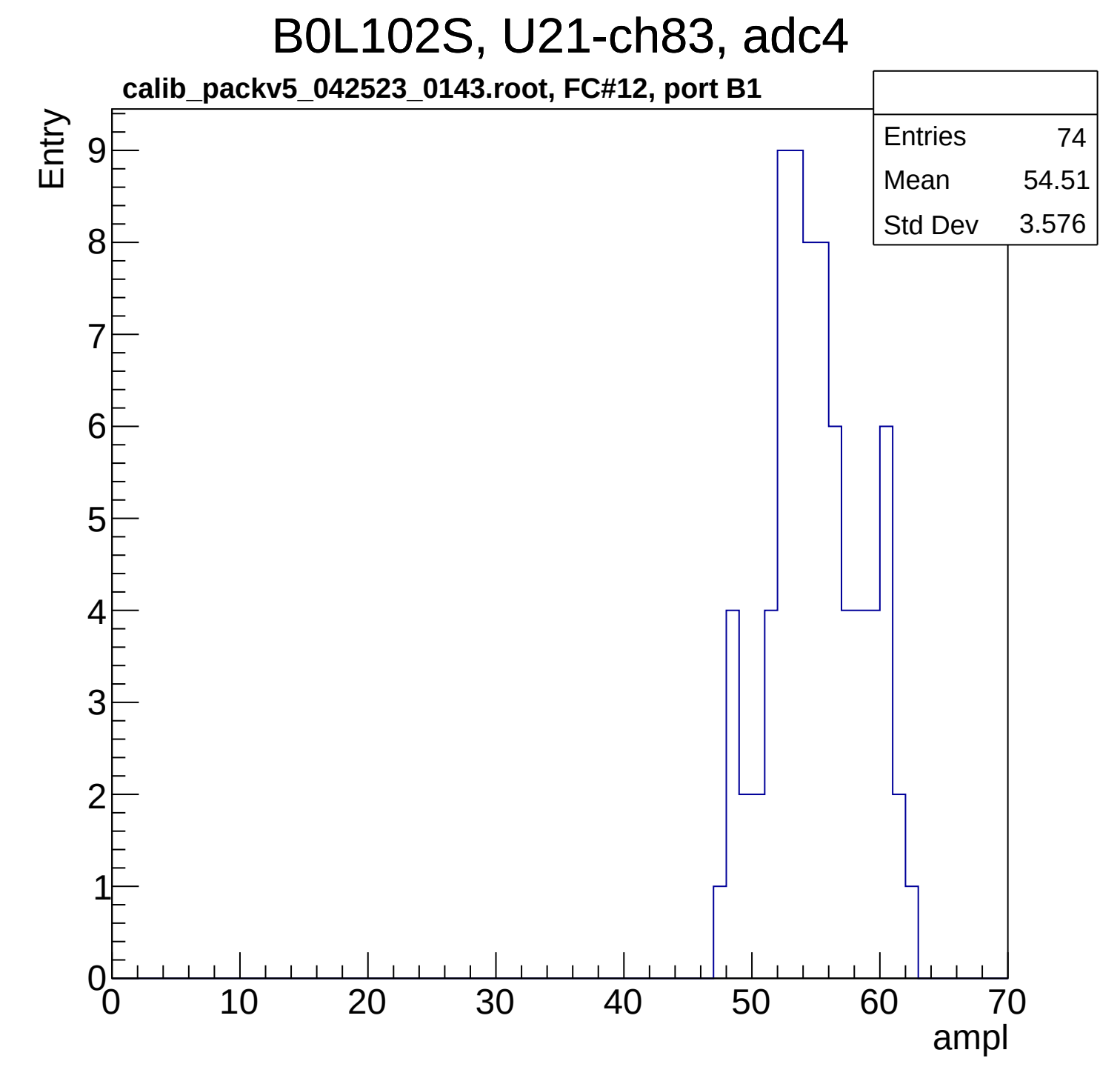
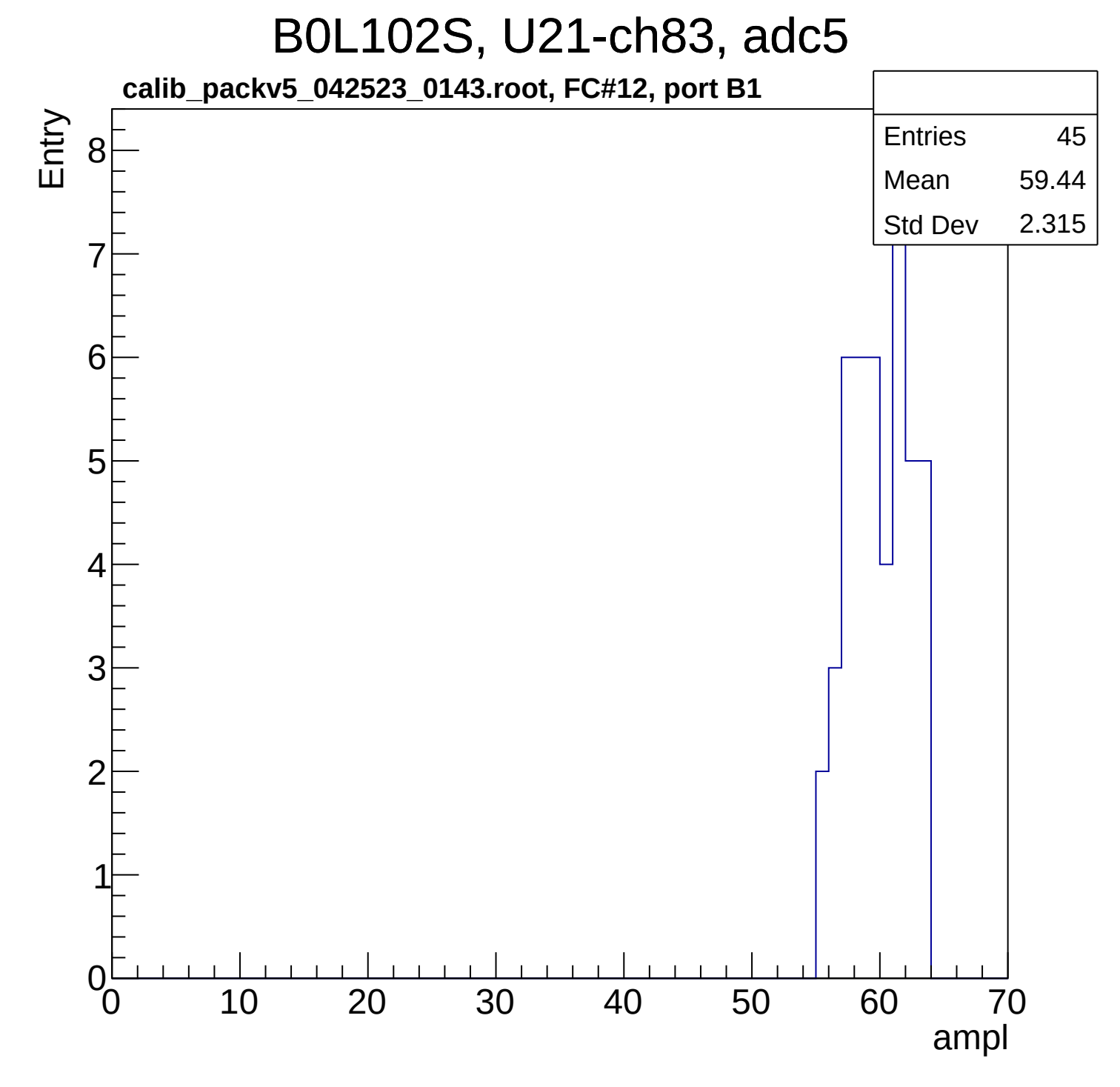
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch83, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 54.51
Std Dev 3.576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch83, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 59.44
Std Dev 2.315
Entry
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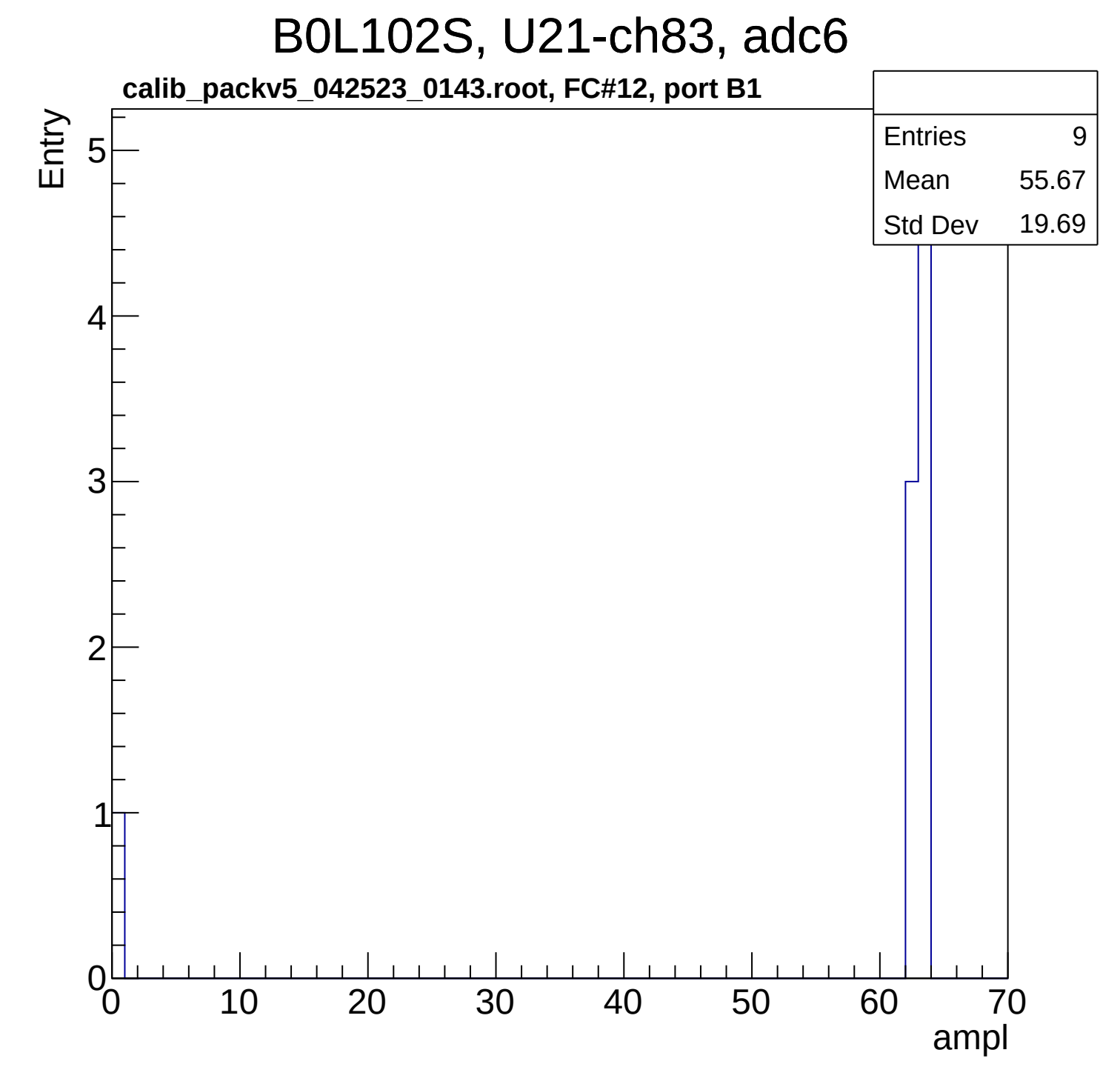
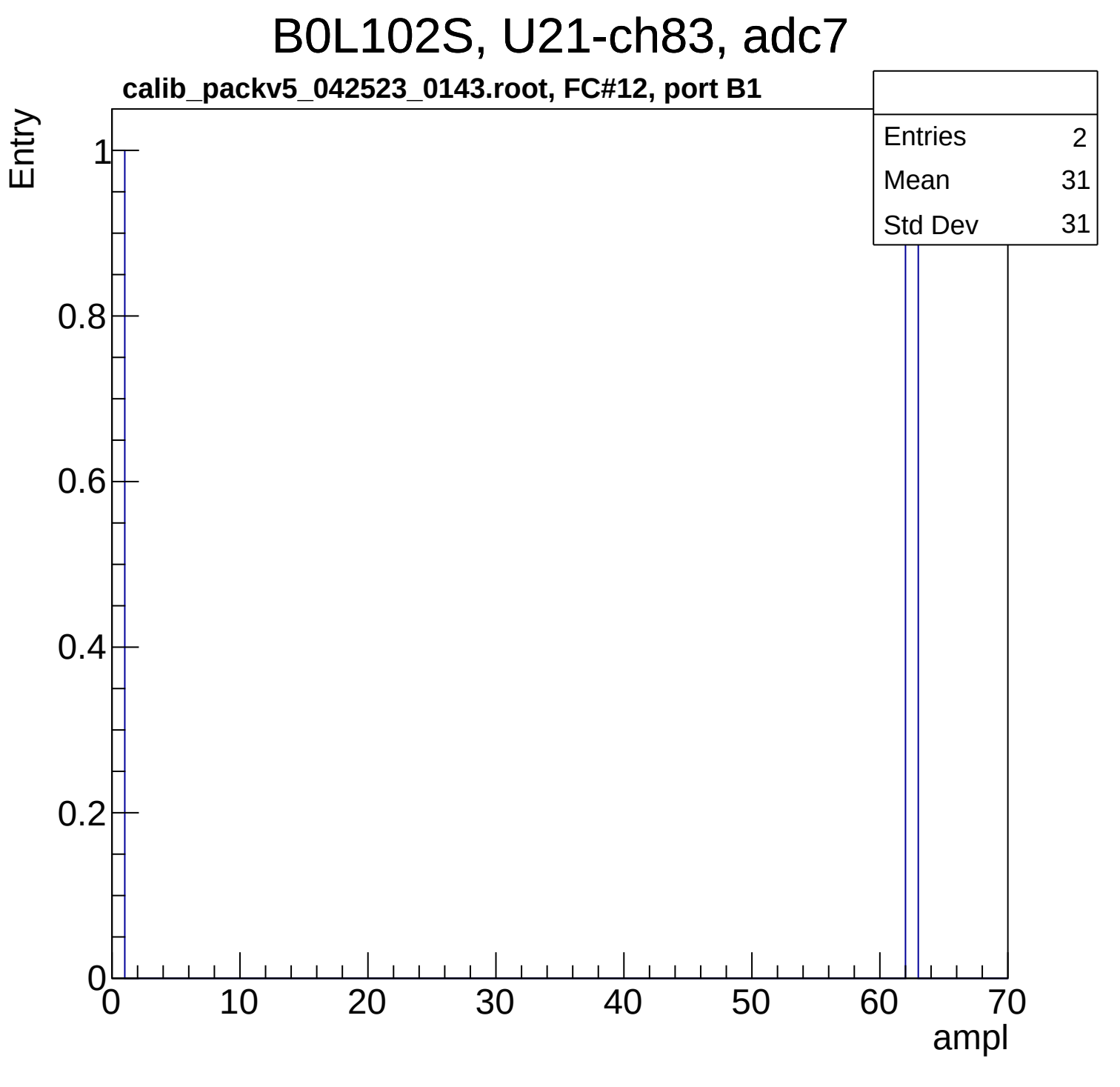
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch83, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 55.67
Std Dev 19.69
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch83, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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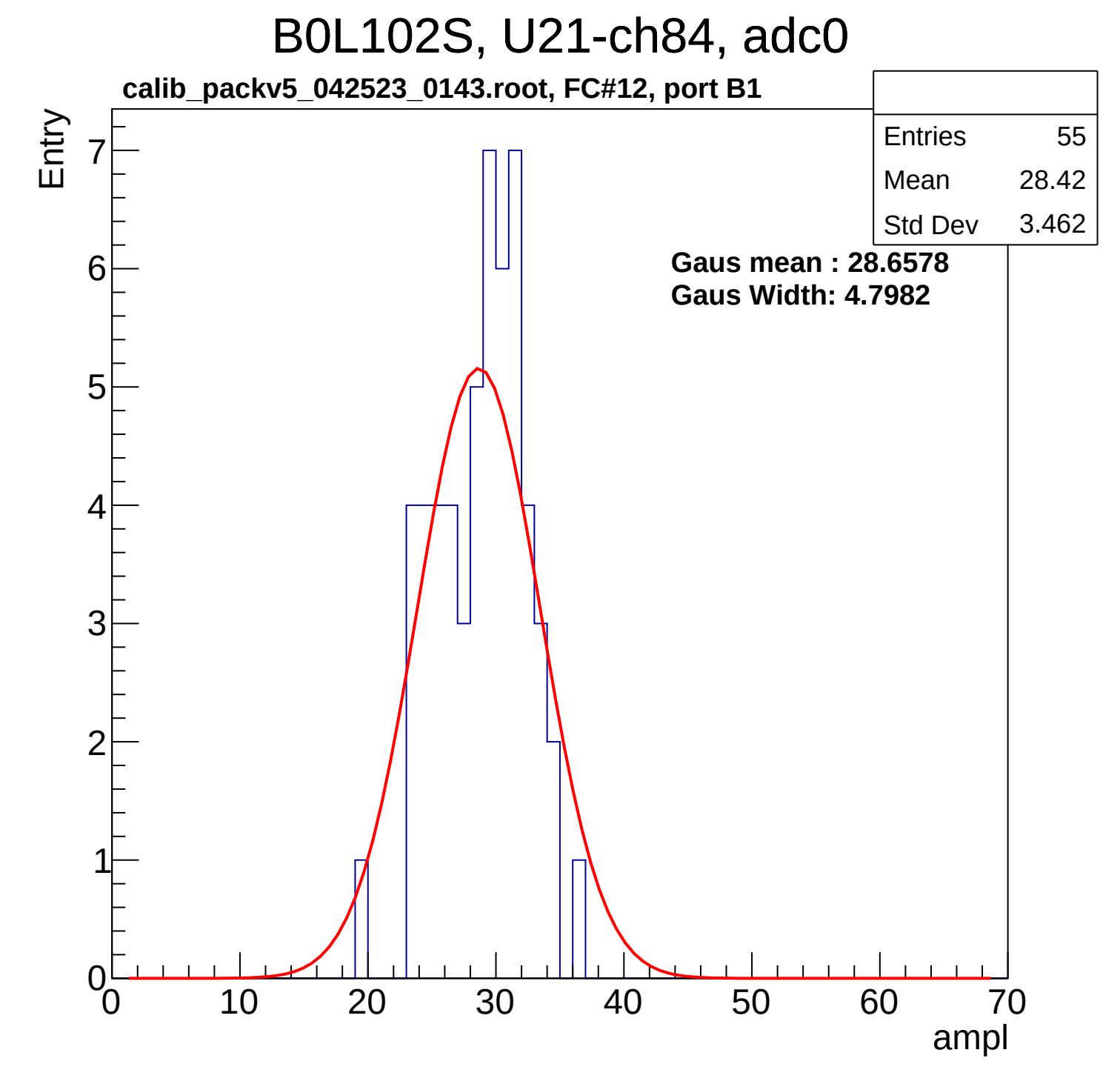
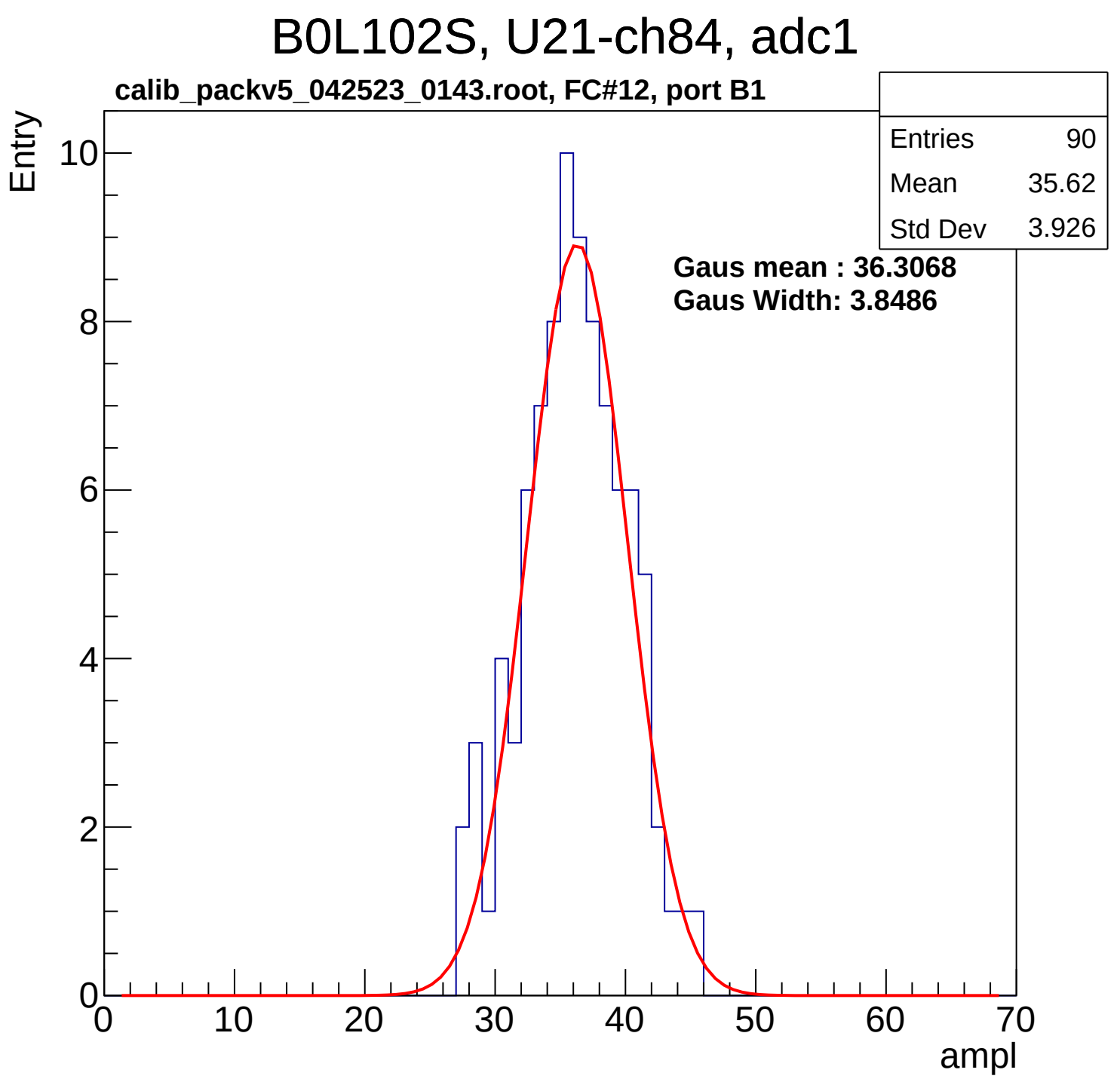
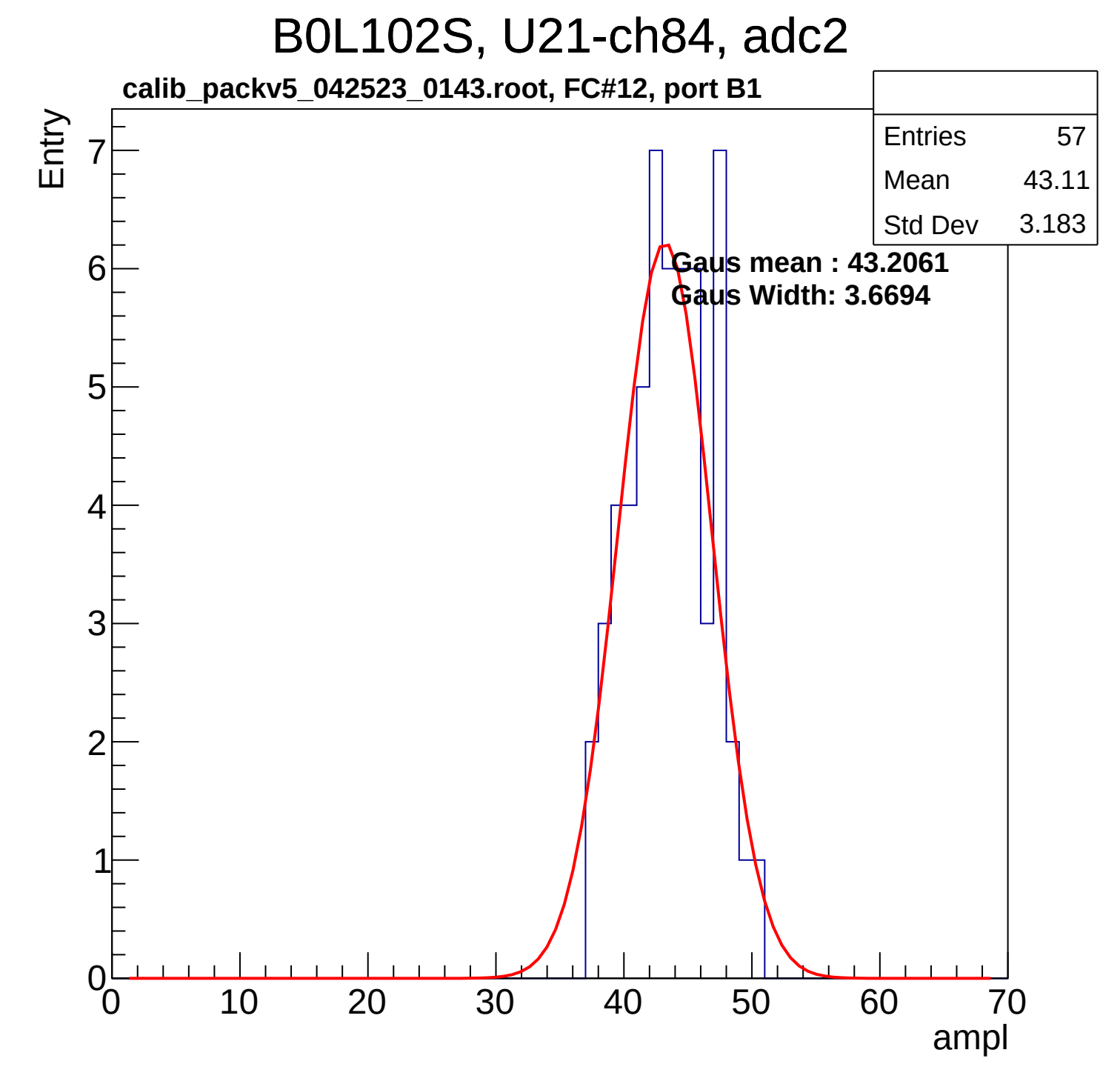
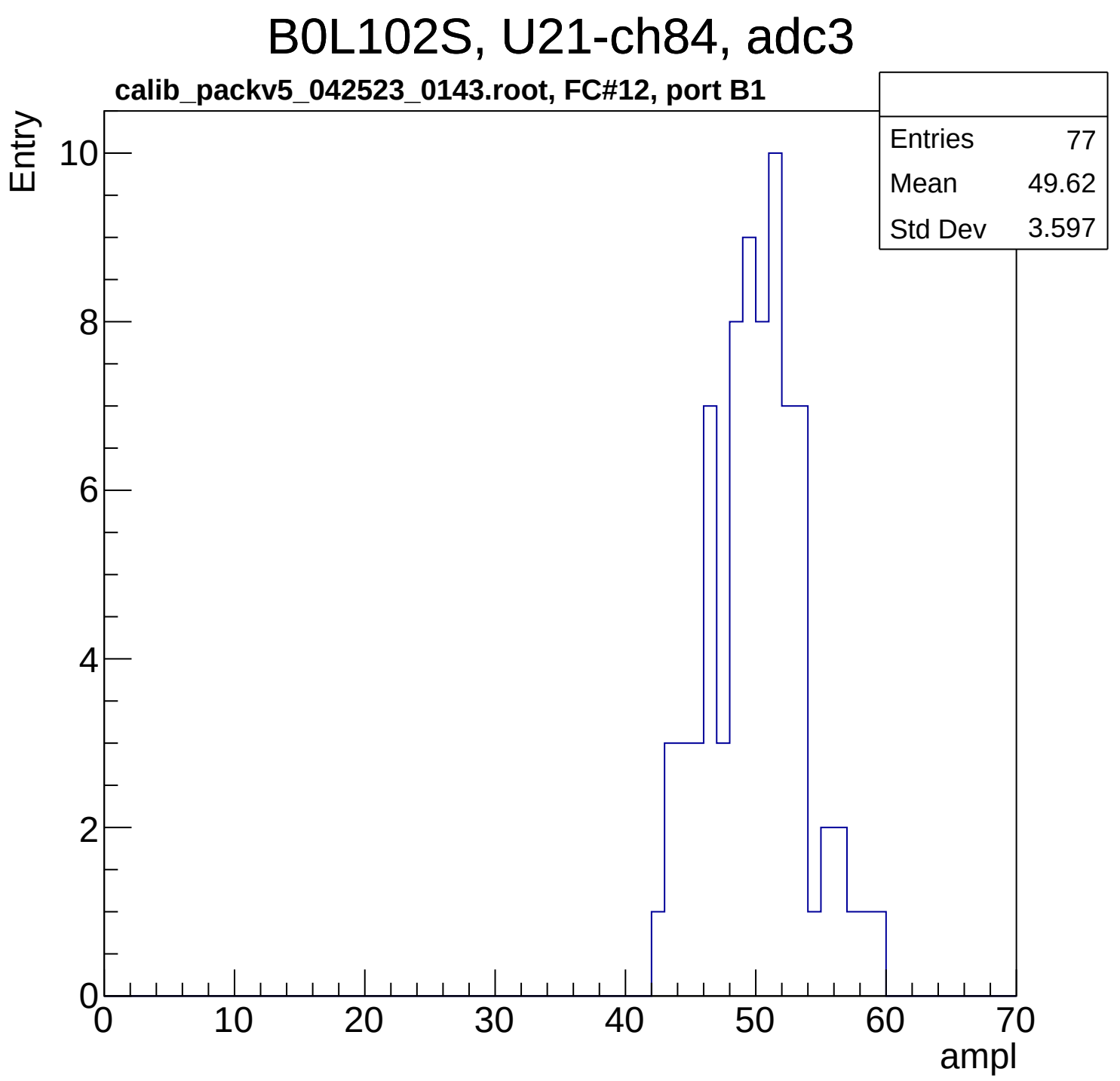
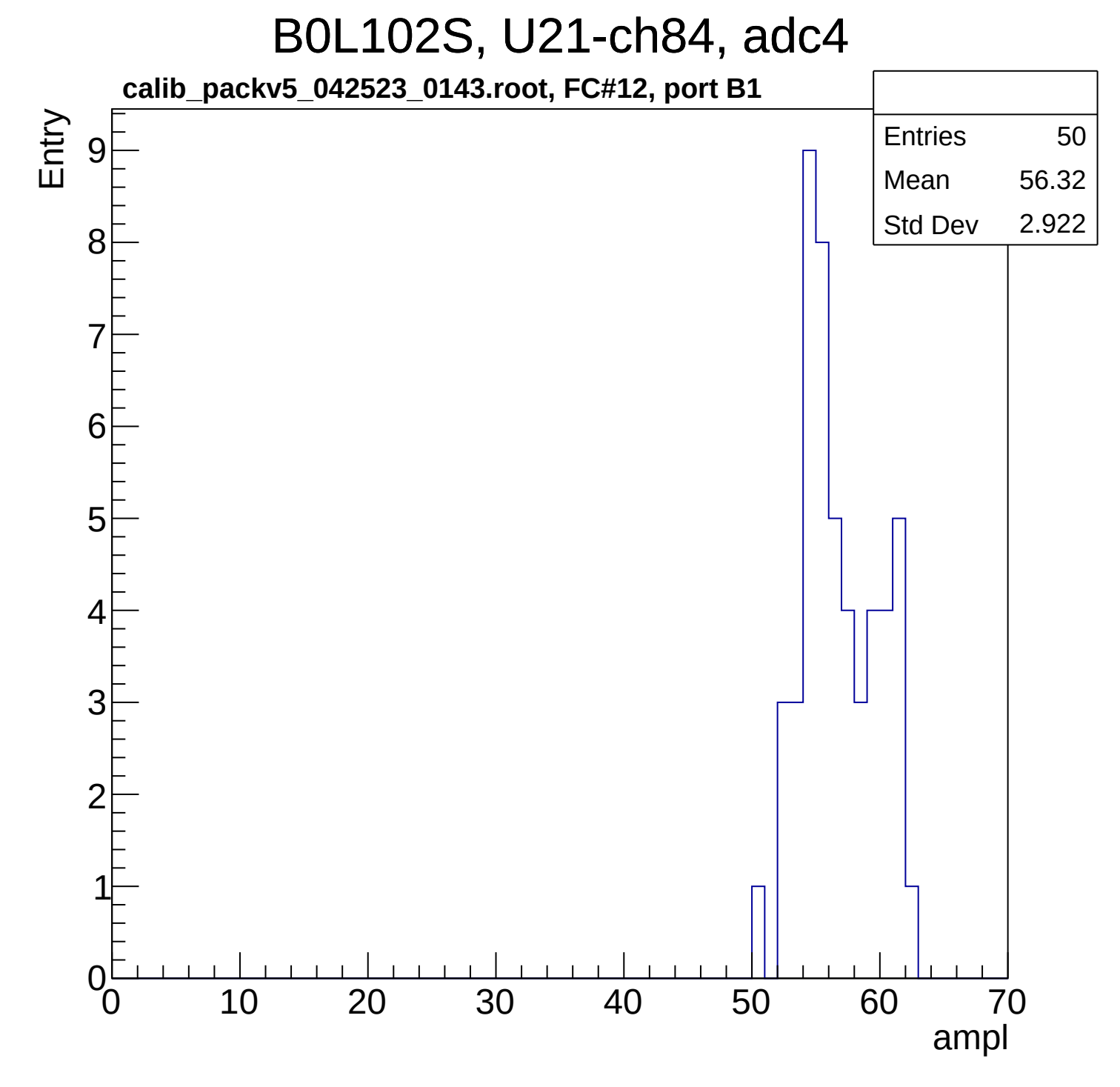
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch84, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 28.42
Std Dev 3.462
Gaus mean : 28.6578
Gaus Width: 4.7982
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch84, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 90
Mean 35.62
Std Dev 3.926
Gaus mean : 36.3068
Gaus Width: 3.8486
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch84, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.11
Std Dev 3.183
Gaus mean : 43.2061
Gaus Width: 3.6694
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch84, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 49.62
Std Dev 3.597
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch84, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 56.32
Std Dev 2.922
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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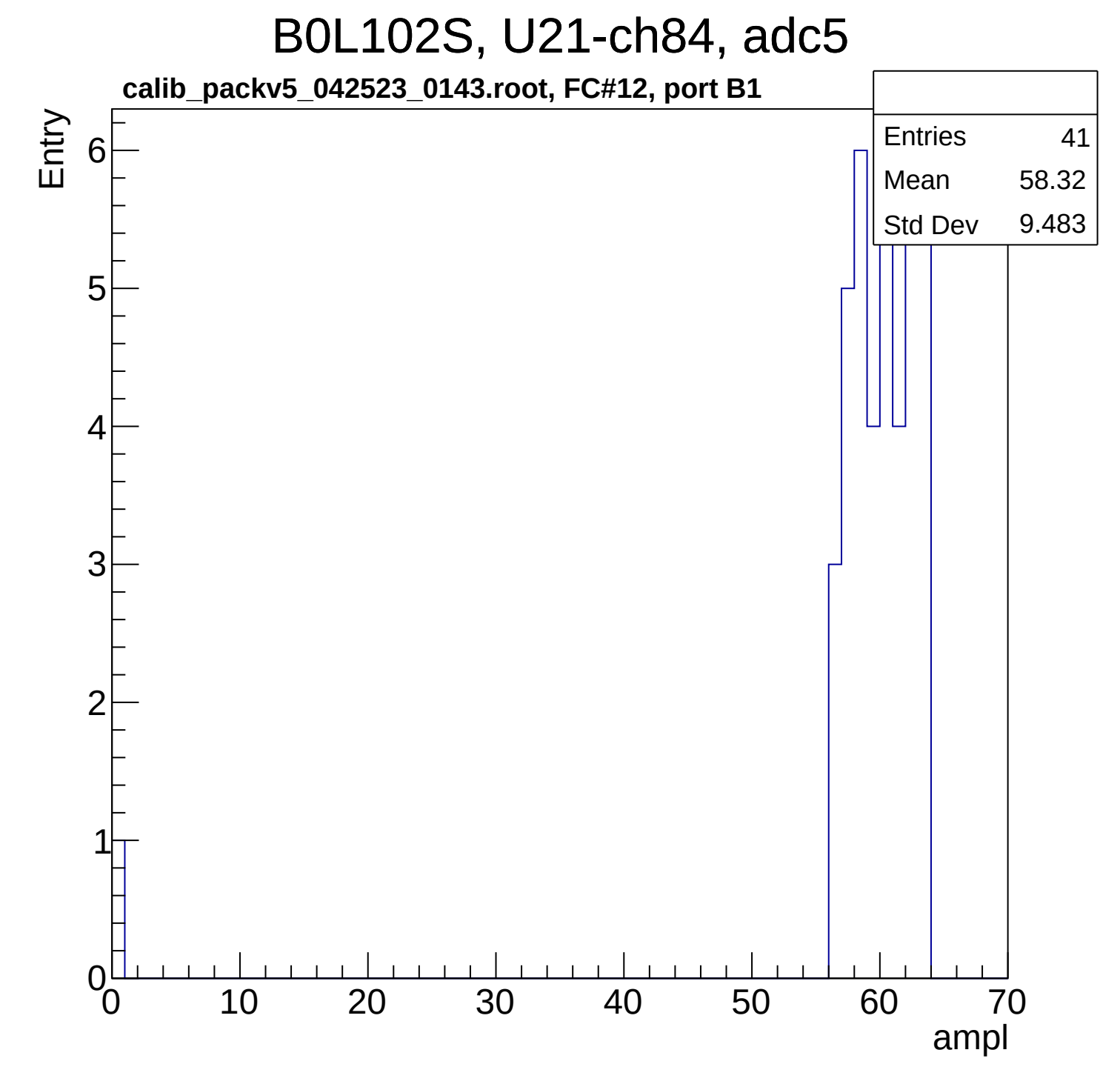
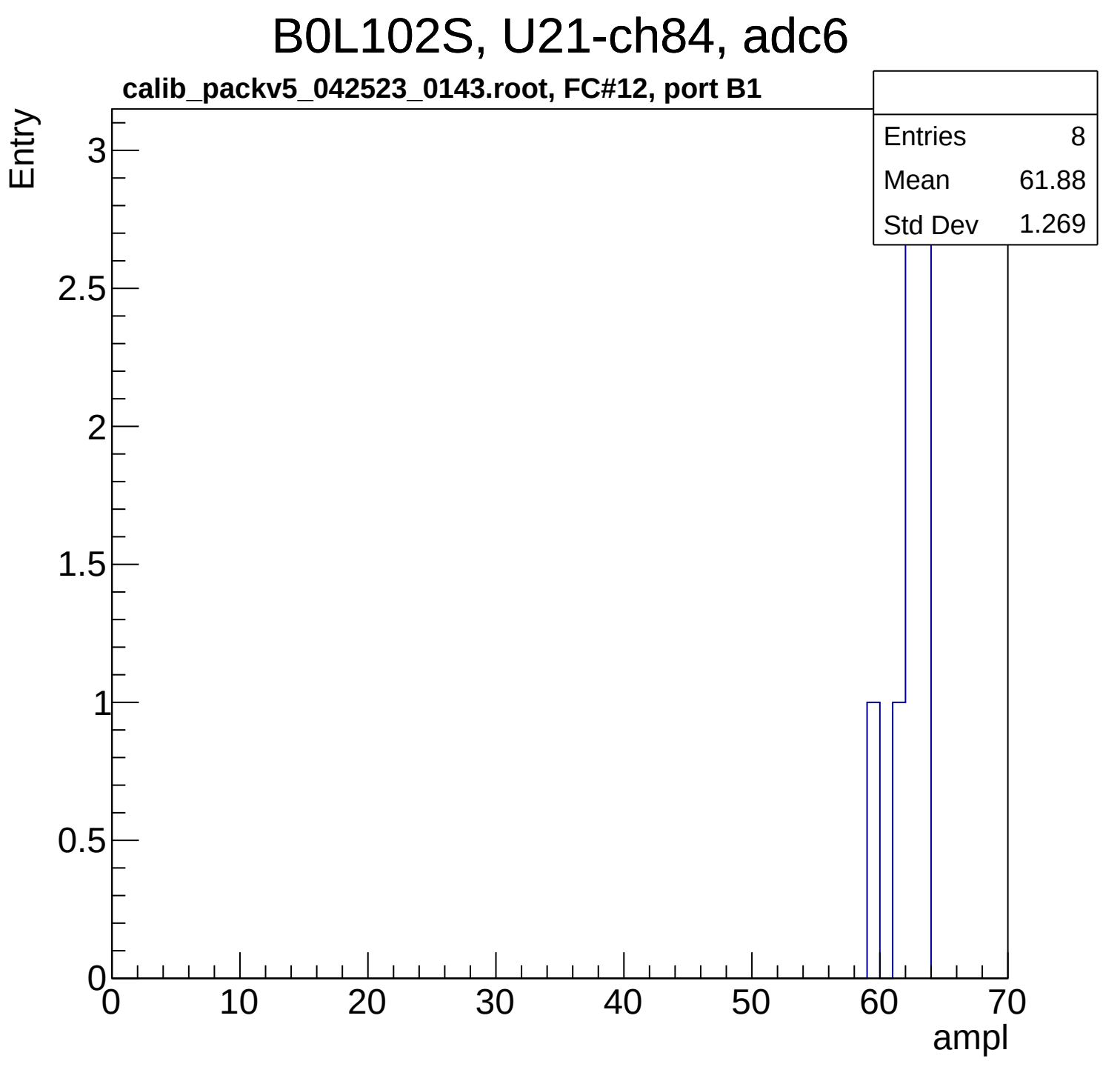

B0L102S, U21-ch84, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.32
Std Dev 9.483
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U21-ch84, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.88
Std Dev 1.269
Entry
ampl
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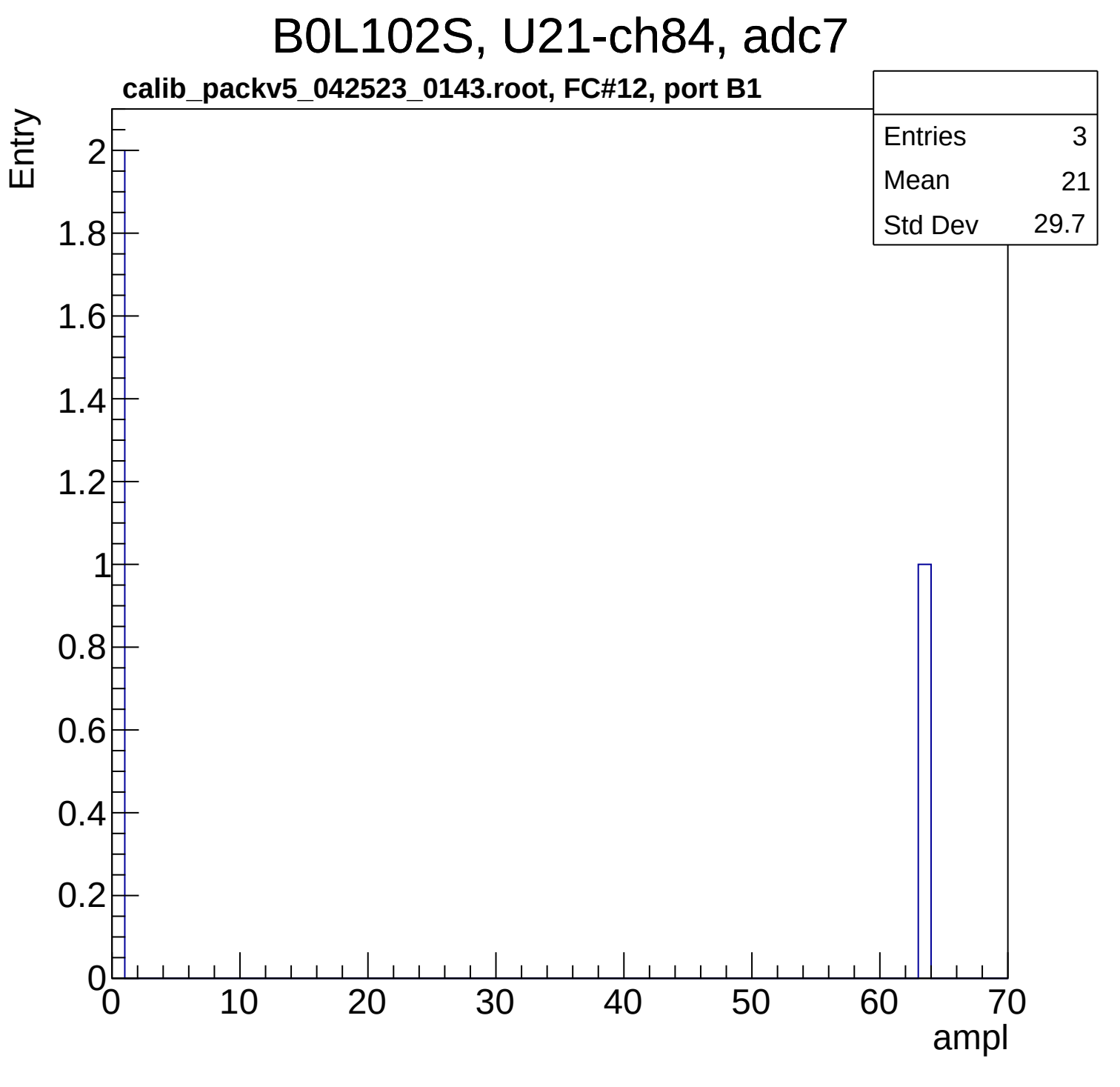
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch84, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
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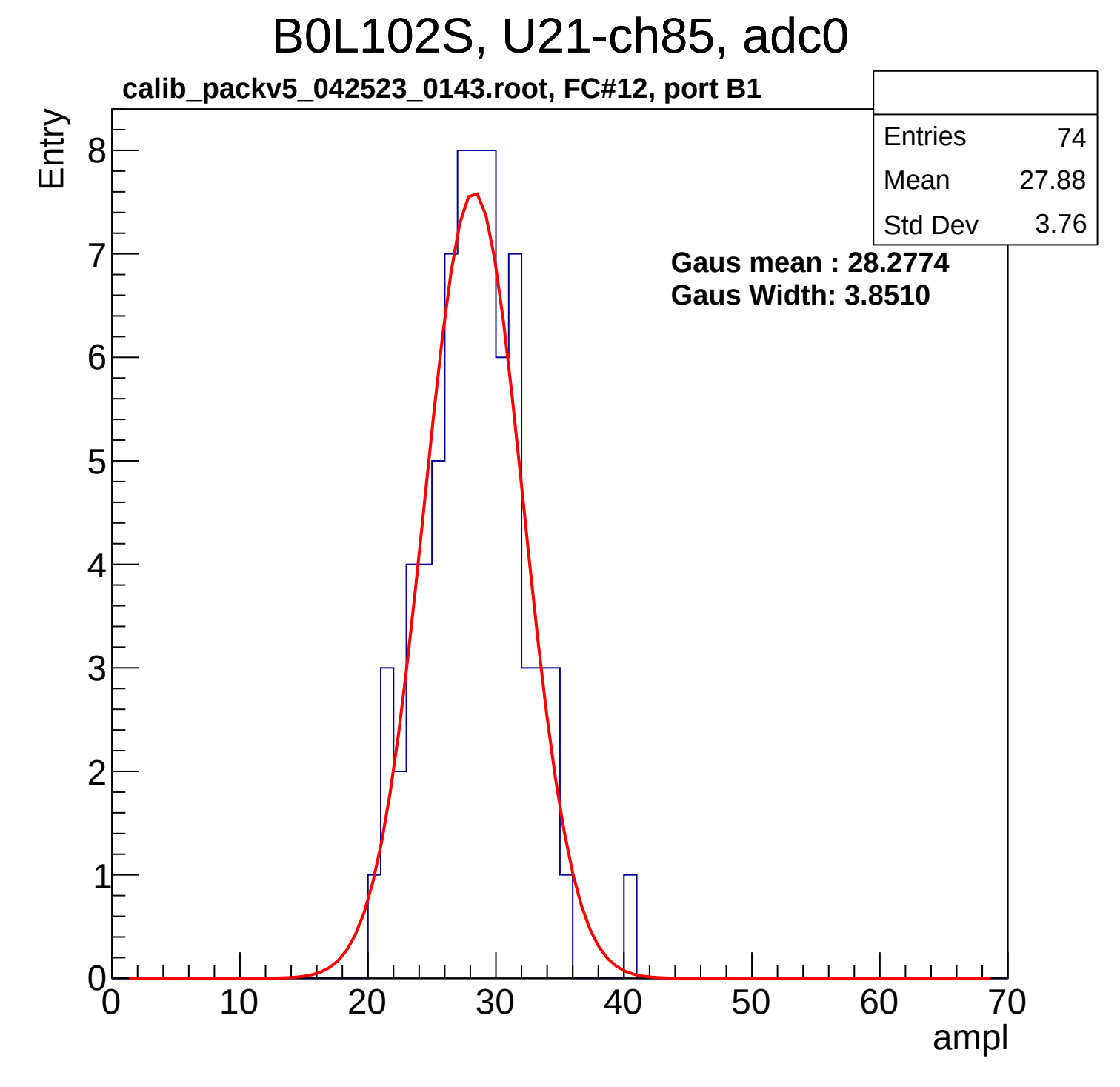
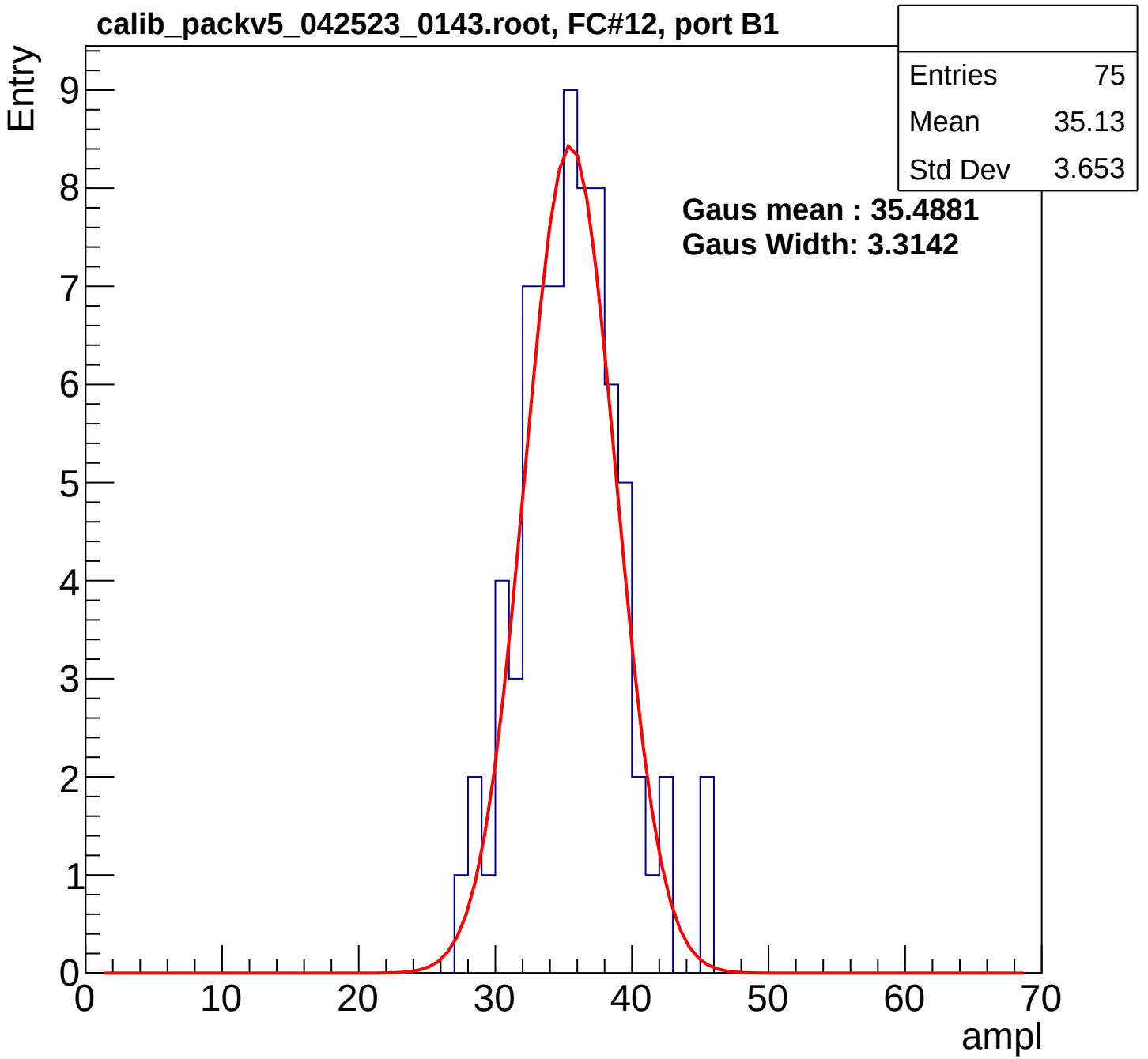
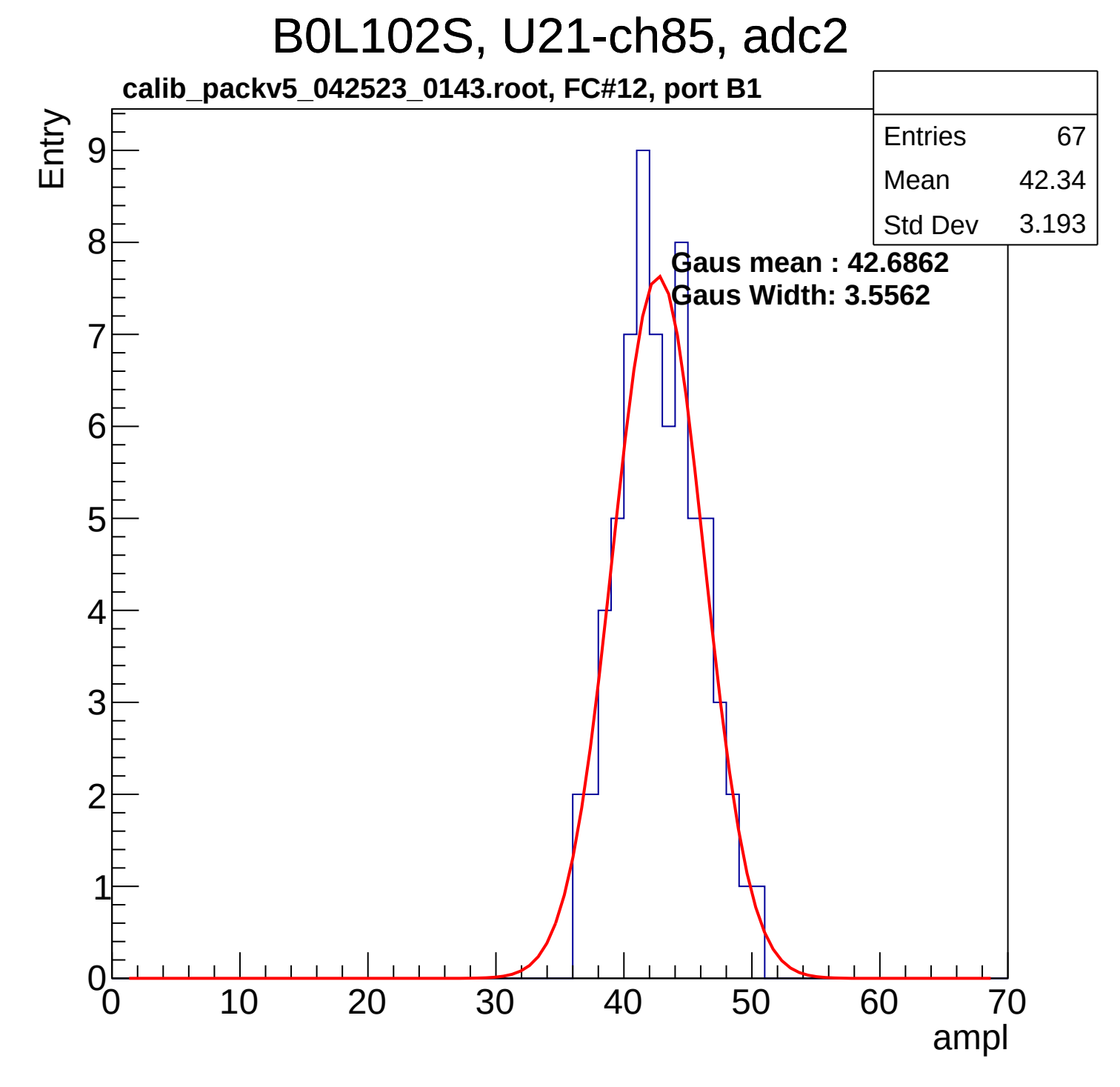
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch85, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 27.88
Std Dev 3.76
Gaus mean : 28.2774
Gaus Width: 3.8510
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch85, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 35.13
Std Dev 3.653
Gaus mean : 35.4881
Gaus Width: 3.3142
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch85, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 42.34
Std Dev 3.193
Gaus mean : 42.6862
Gaus Width: 3.5562
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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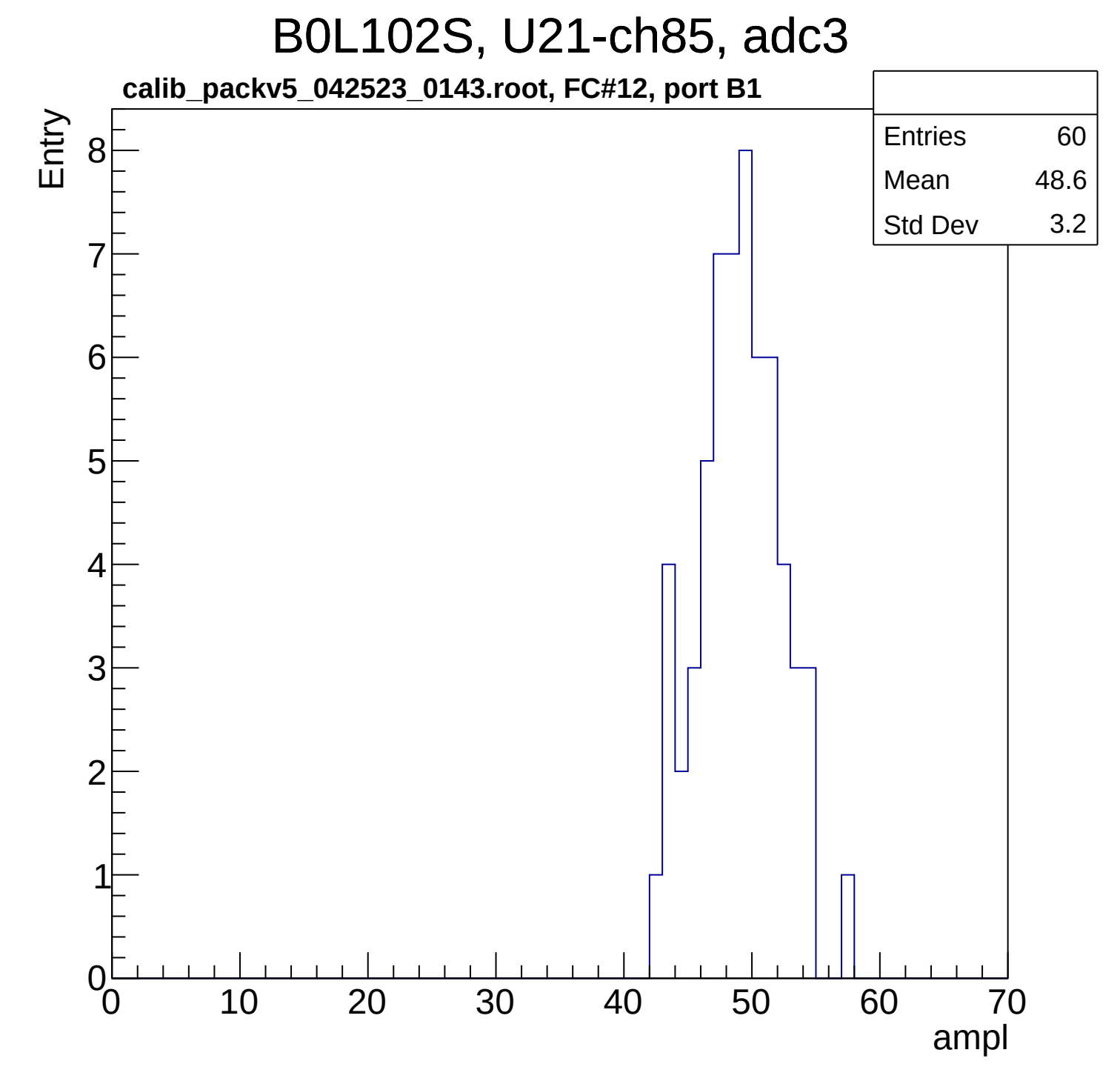
B0L102S, U21-ch85, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 48.6
Std Dev 3.2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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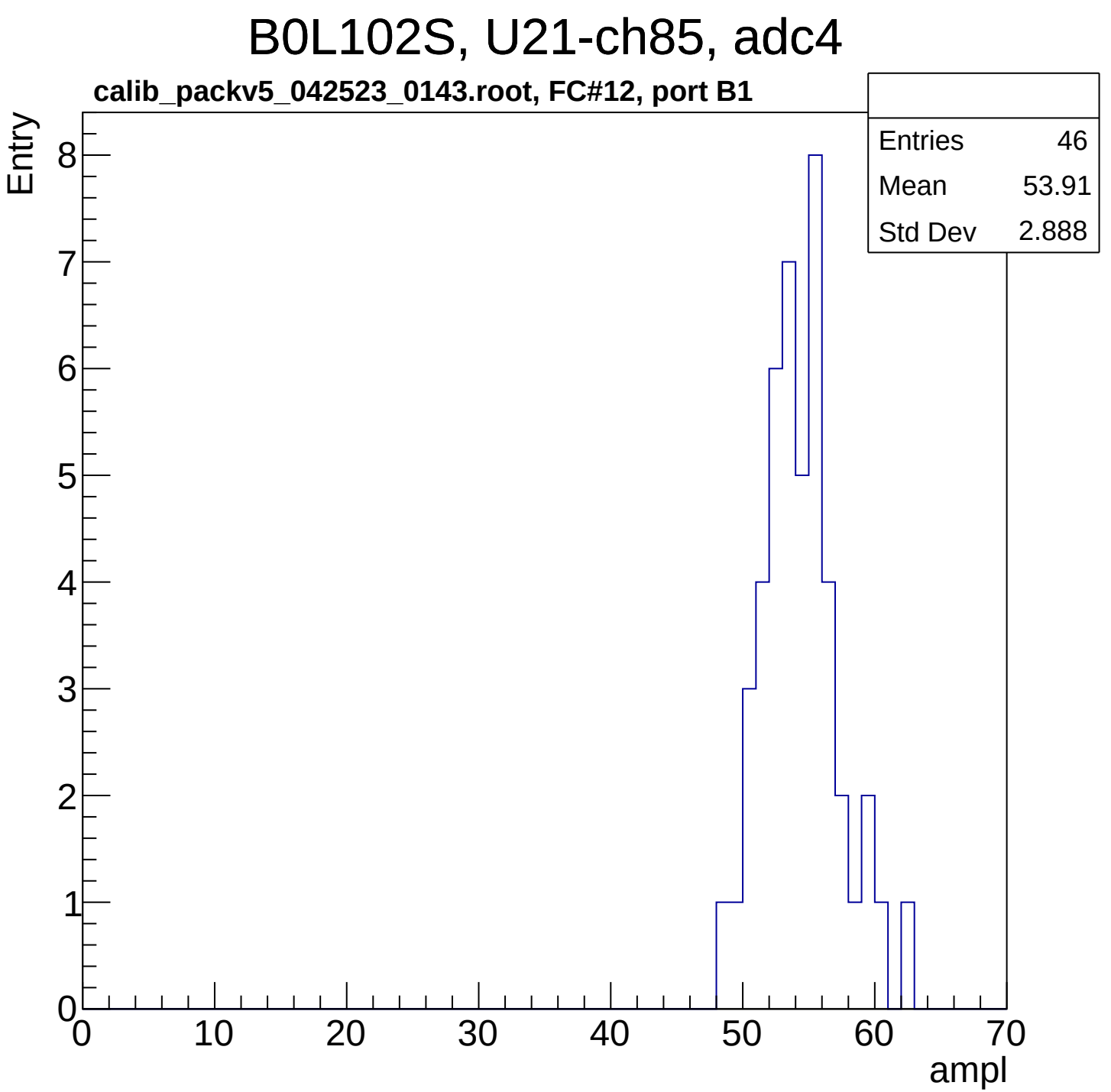
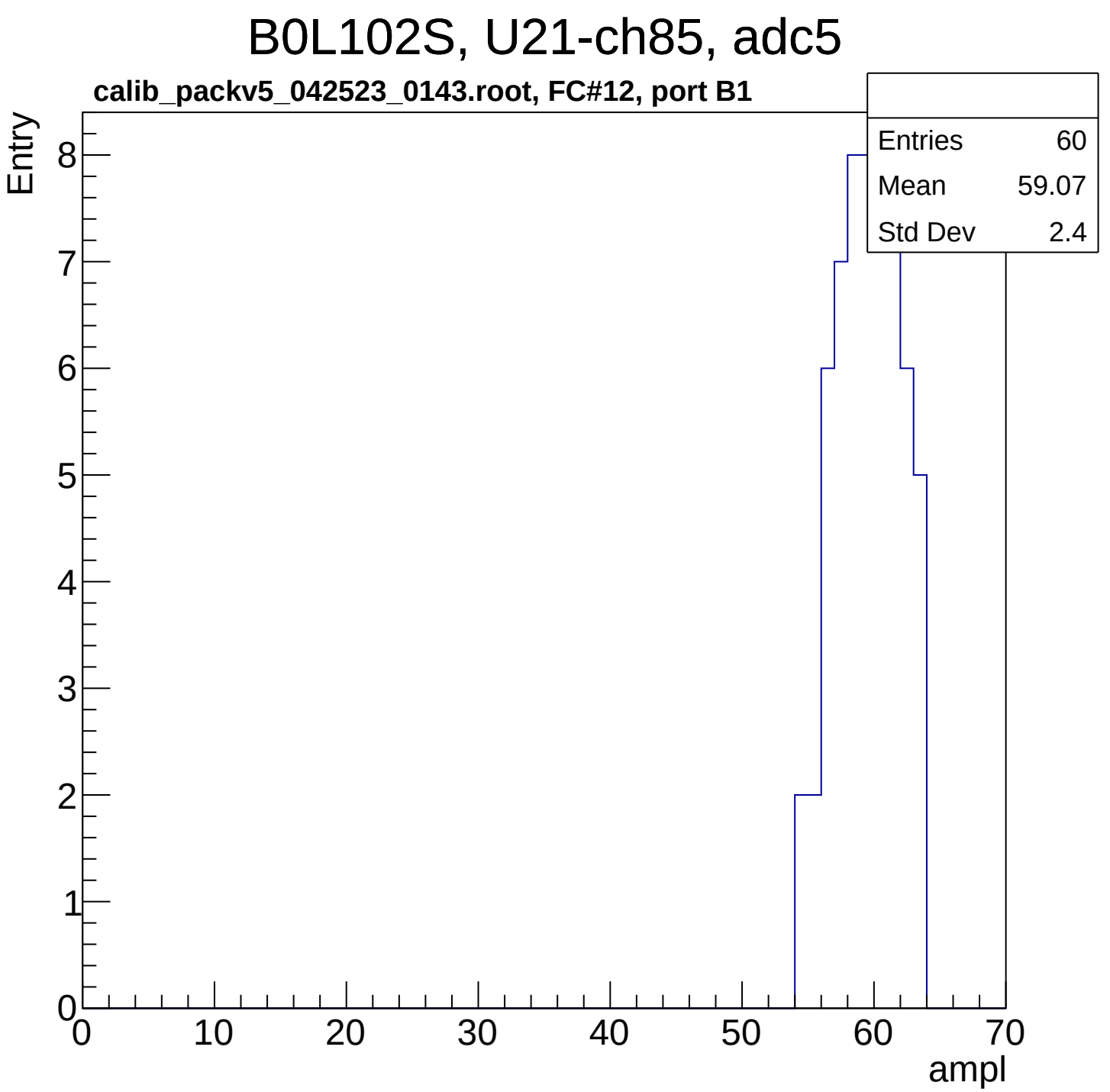
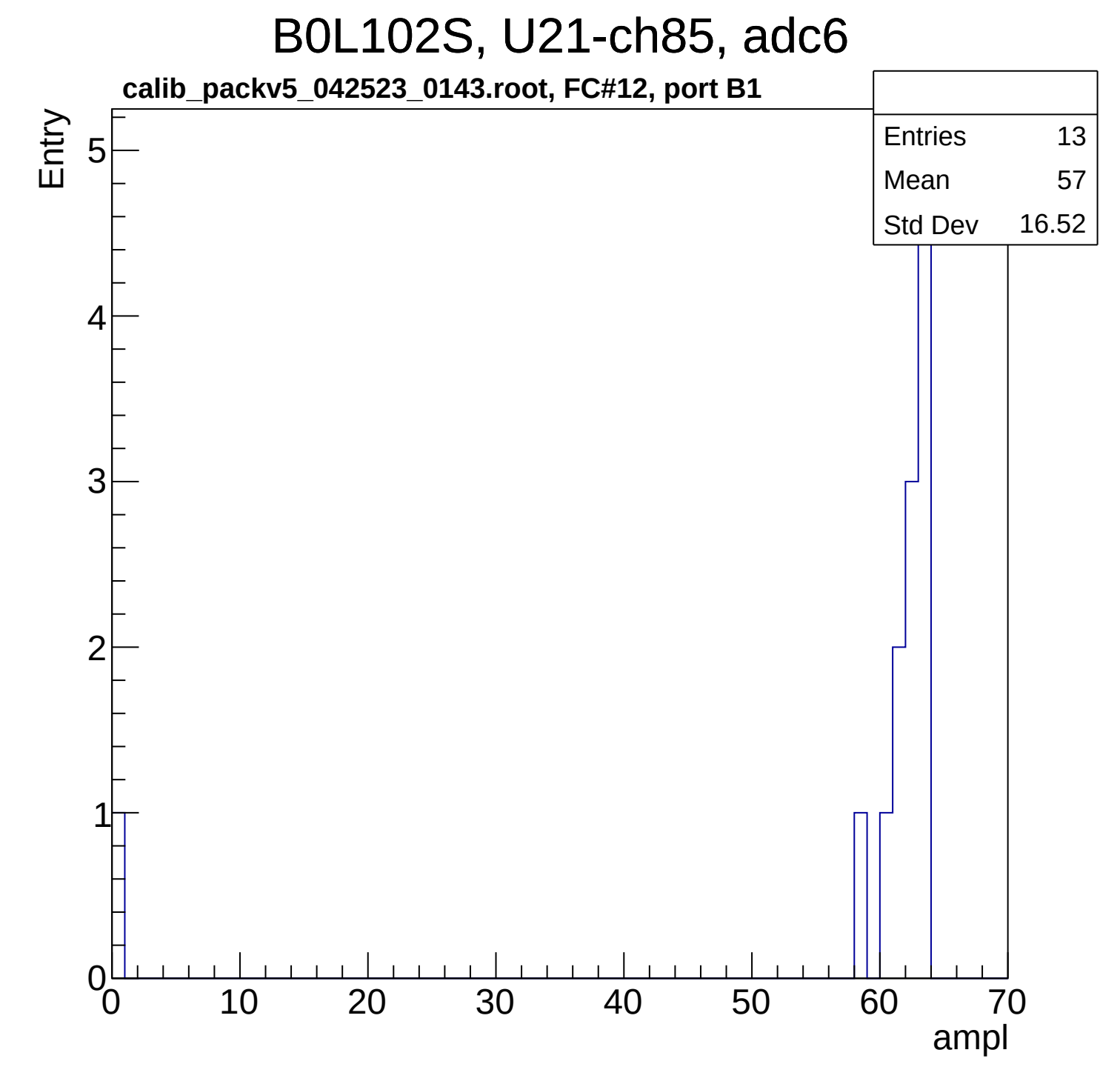
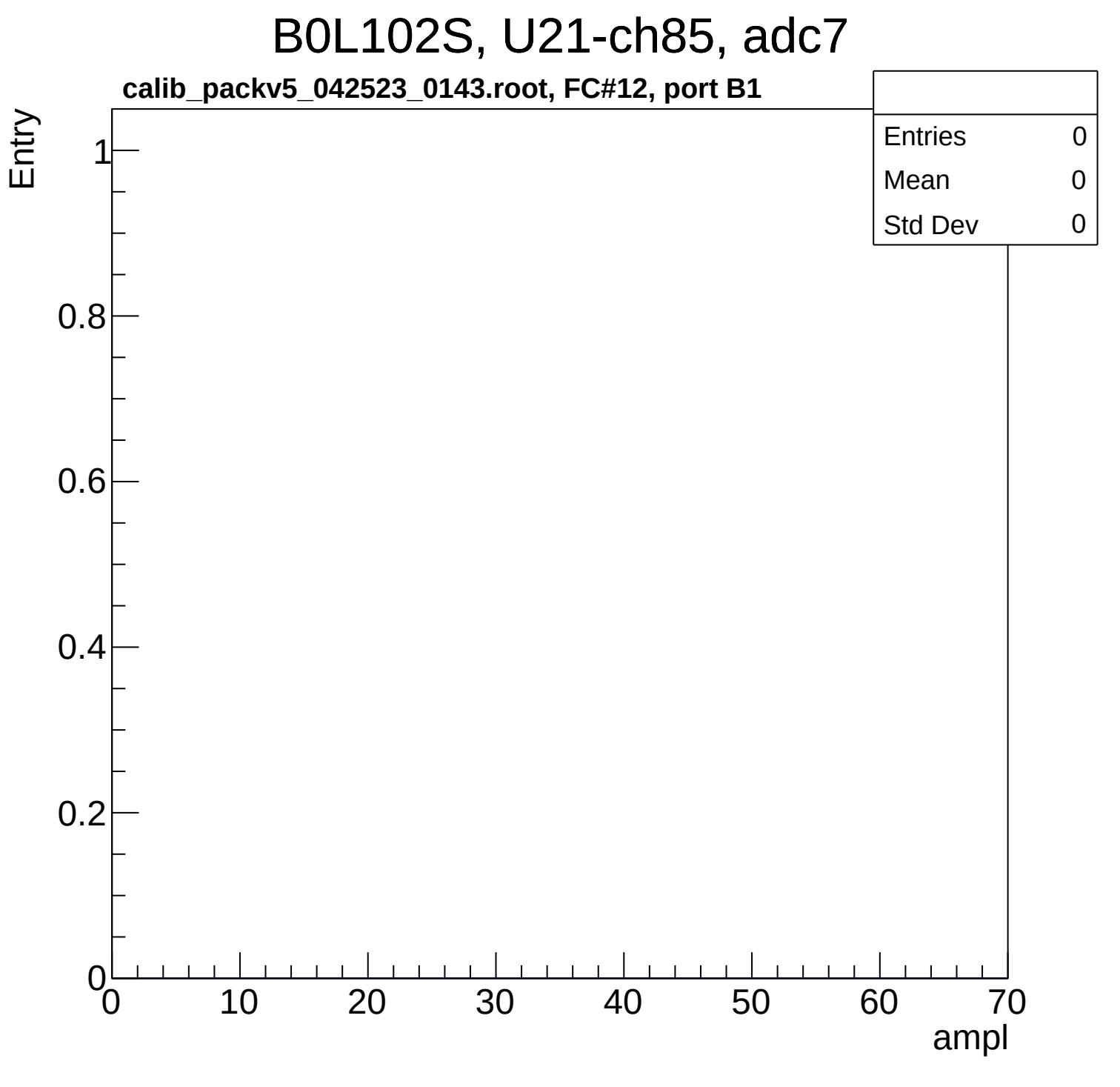
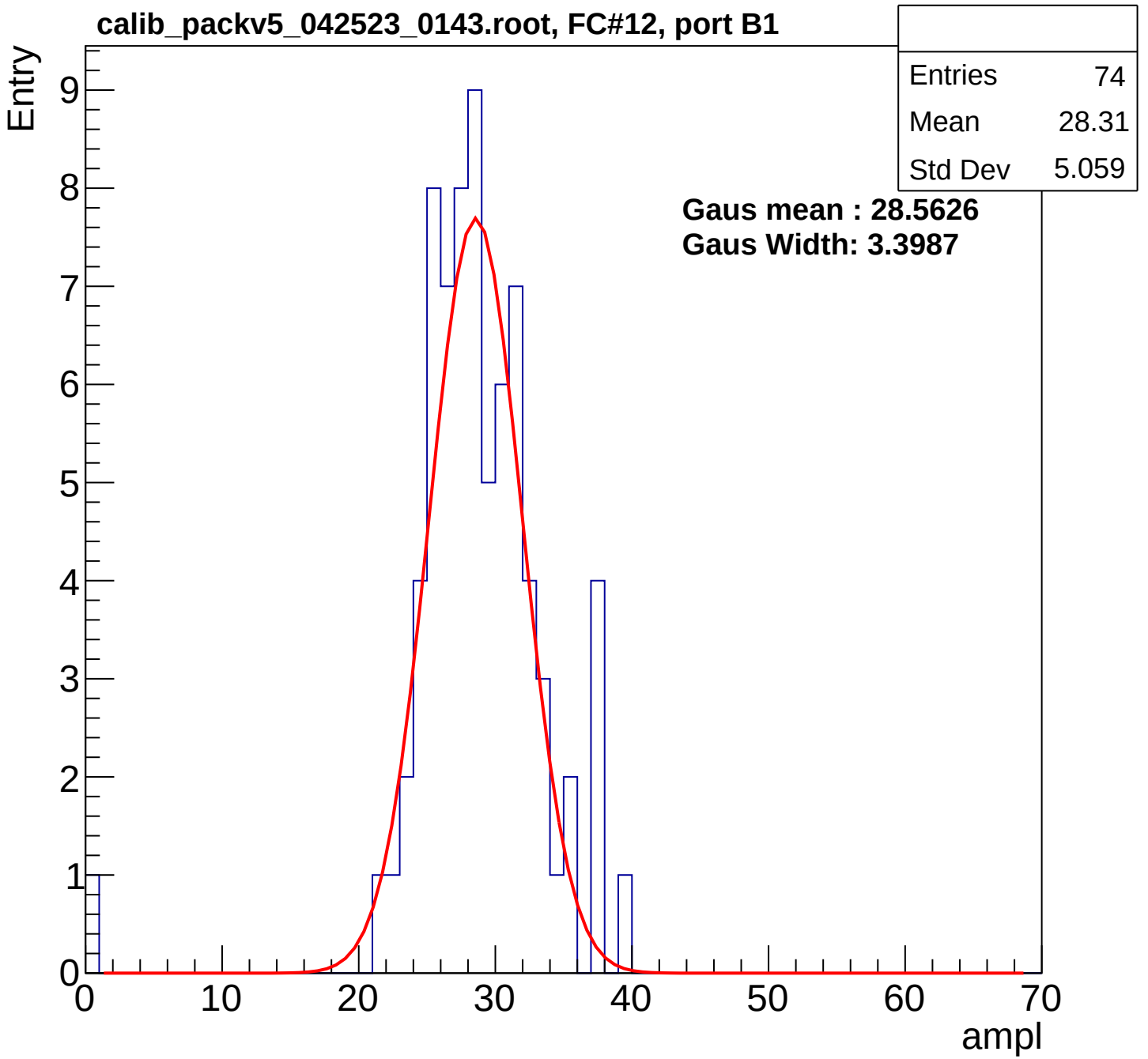
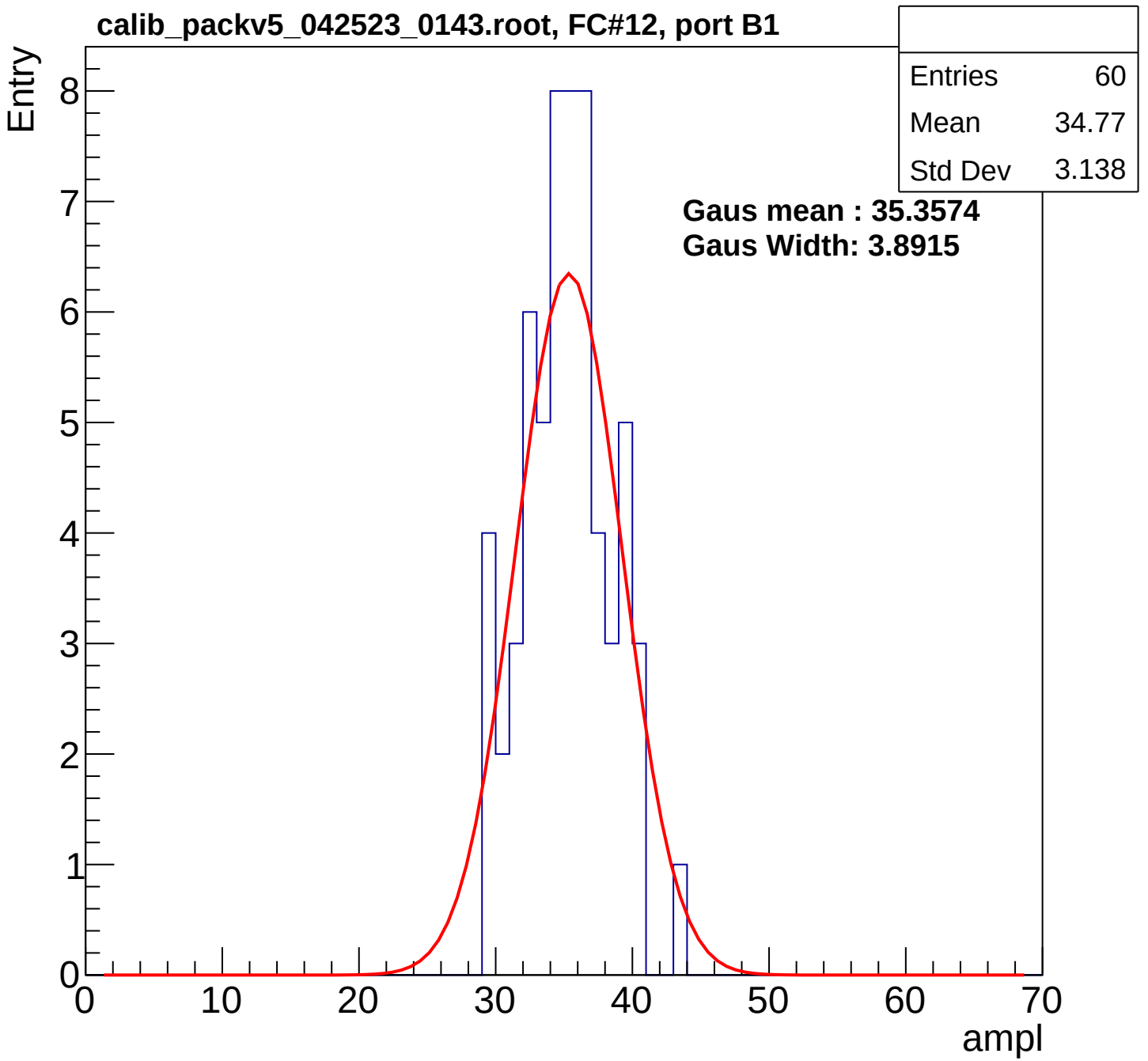

B0L102S, U21-ch85, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 53.91
Std Dev 2.888
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

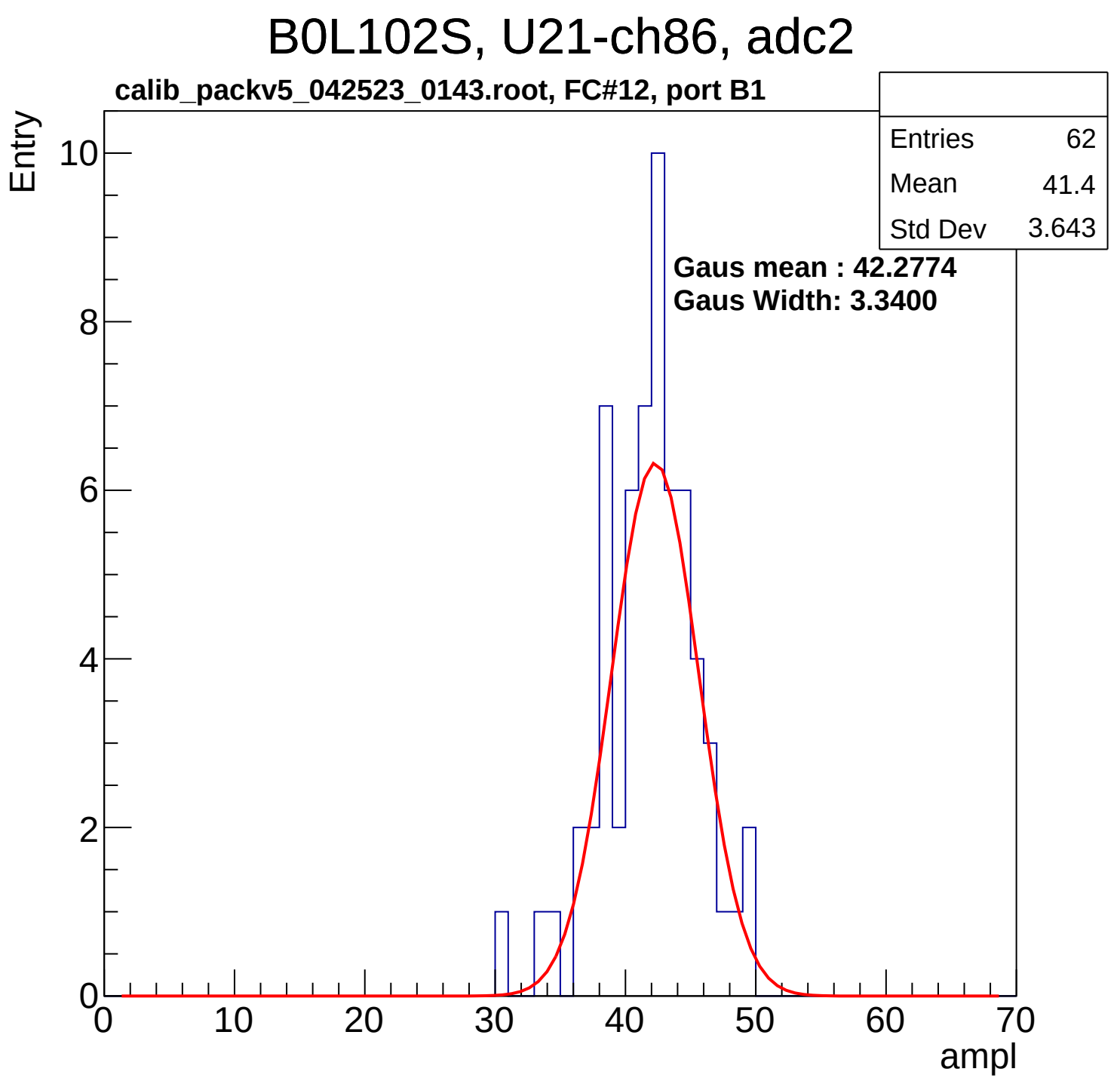

B0L102S, U21-ch85, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 59.07
Std Dev 2.4
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

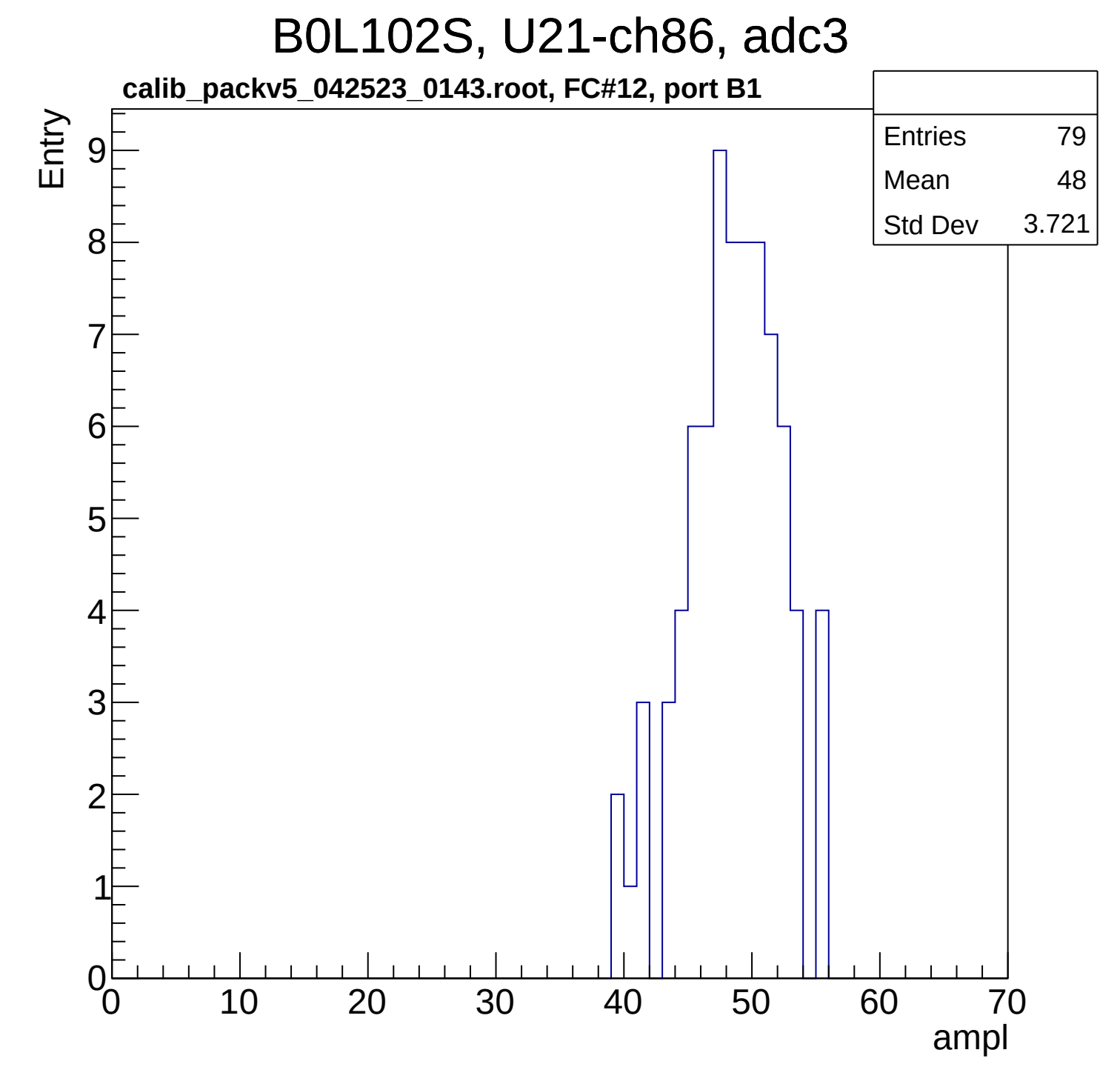

B0L102S, U21-ch85, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 57
Std Dev 16.52
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

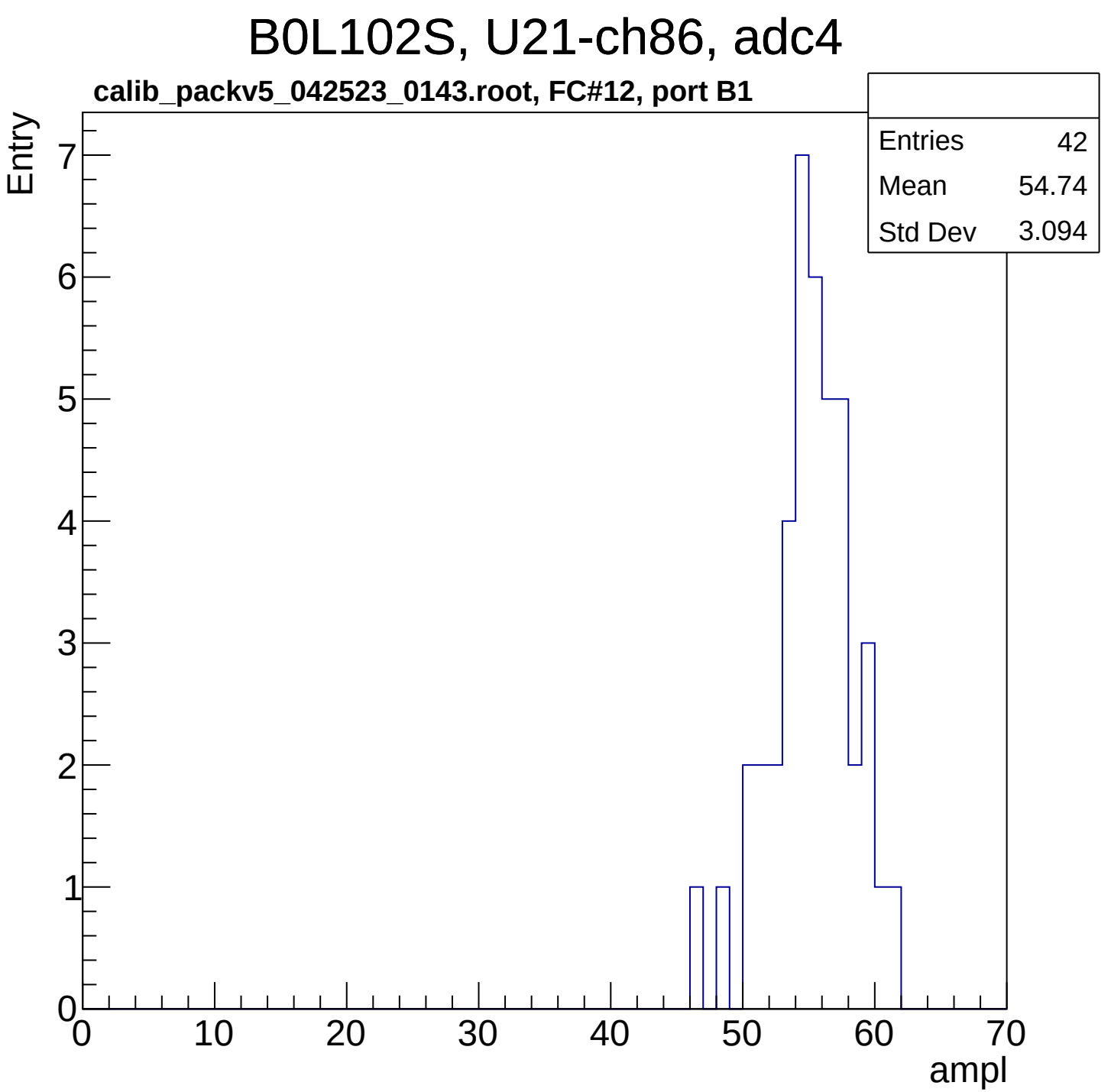

B0L102S, U21-ch85, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch86, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 28.31
Std Dev 5.059
Gaus mean : 28.5626
Gaus Width: 3.3987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch86, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 34.77
Std Dev 3.138
Gaus mean : 35.3574
Gaus Width: 3.8915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch86, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 41.4
Std Dev 3.643
Gaus mean : 42.2774
Gaus Width: 3.3400
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch86, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 48
Std Dev 3.721
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch86, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 54.74
Std Dev 3.094
Entry
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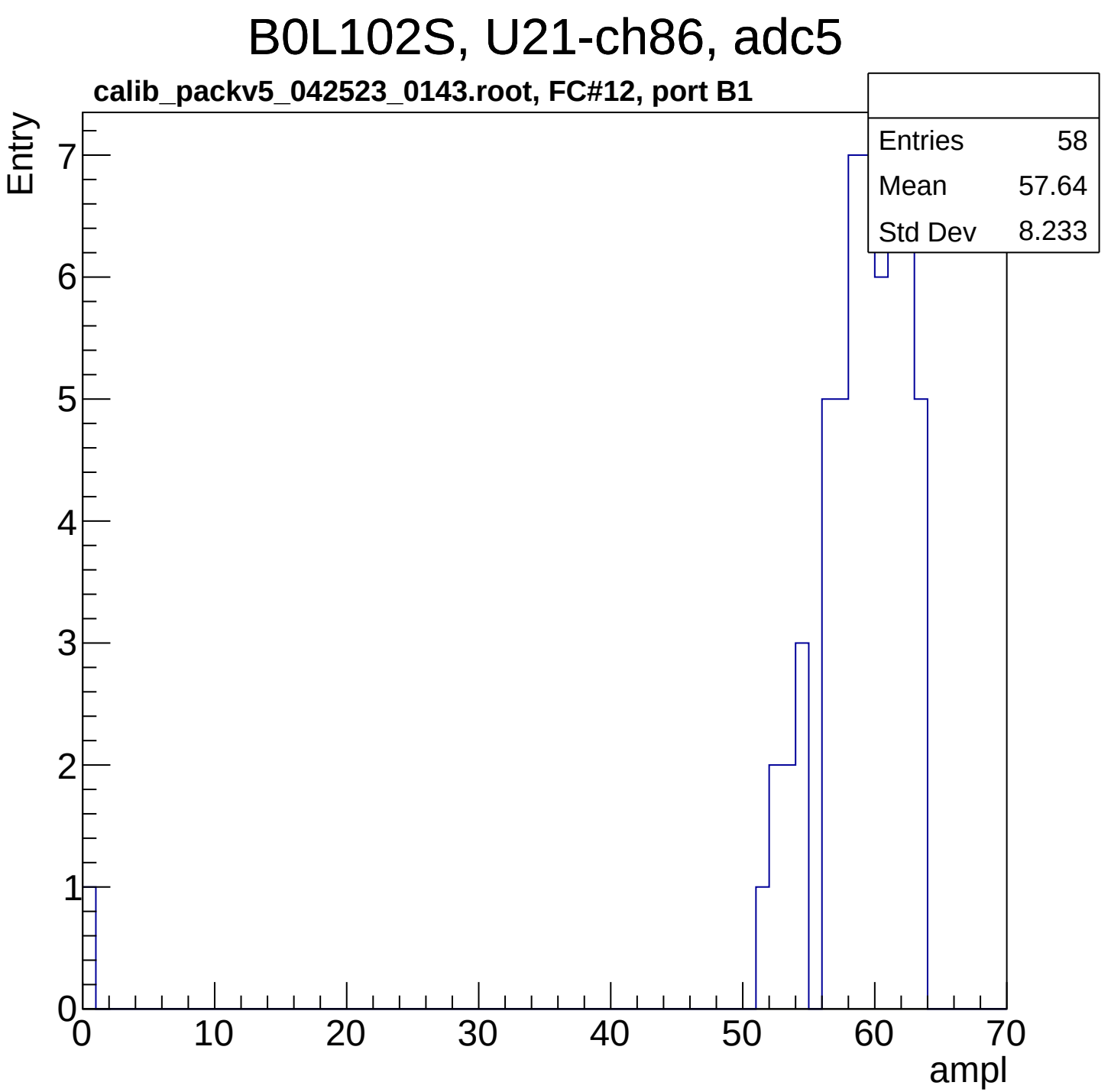
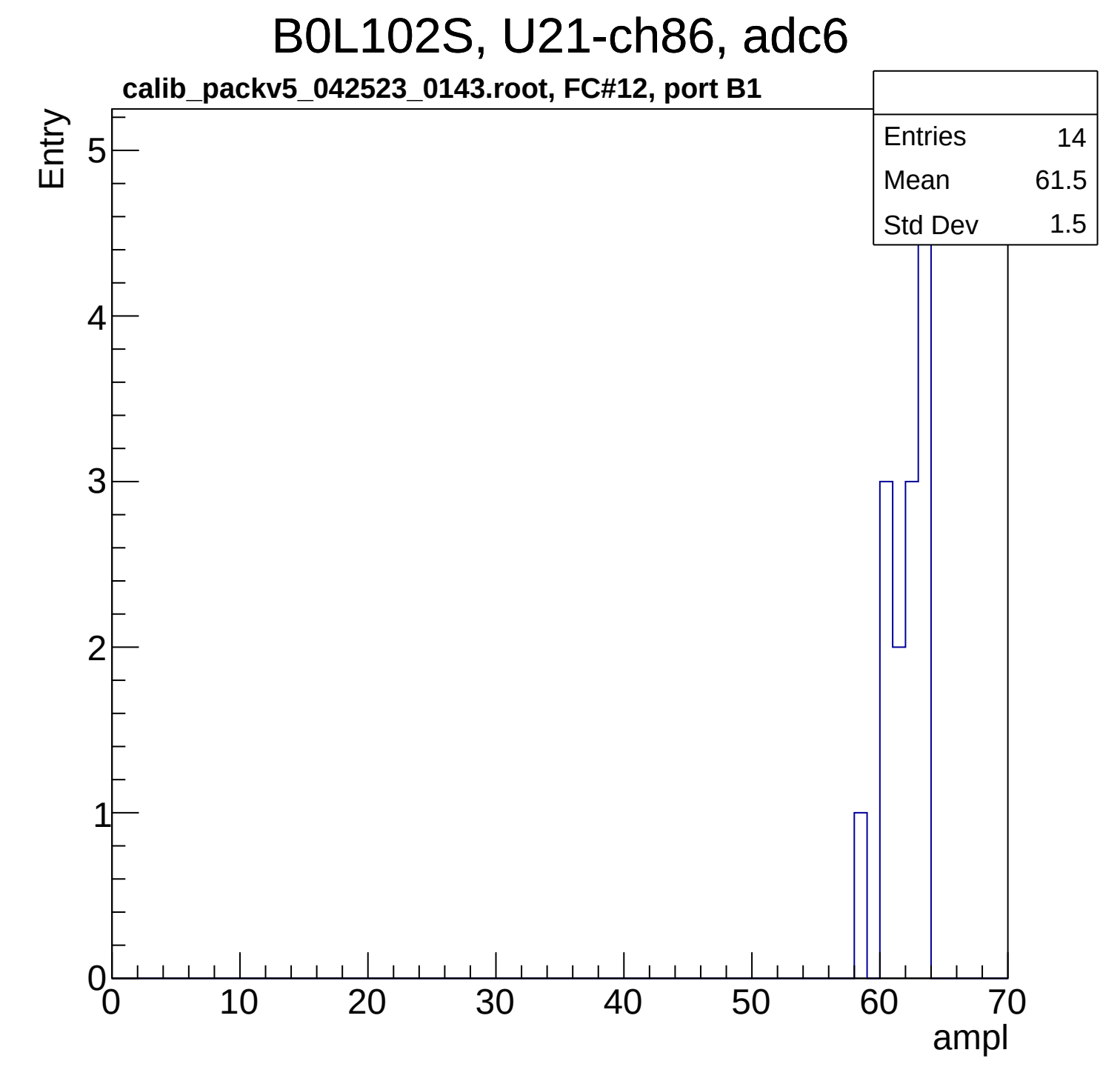
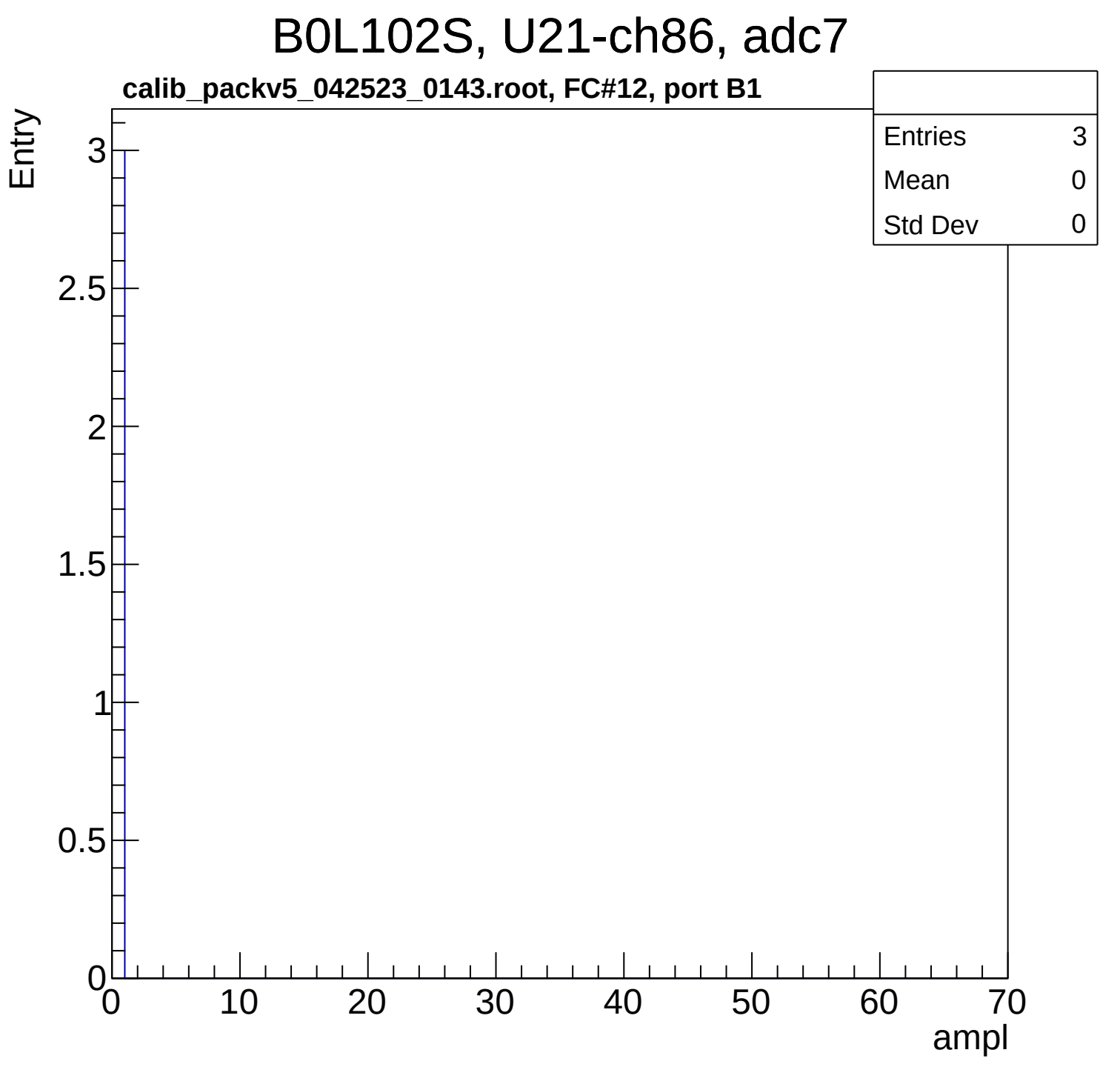
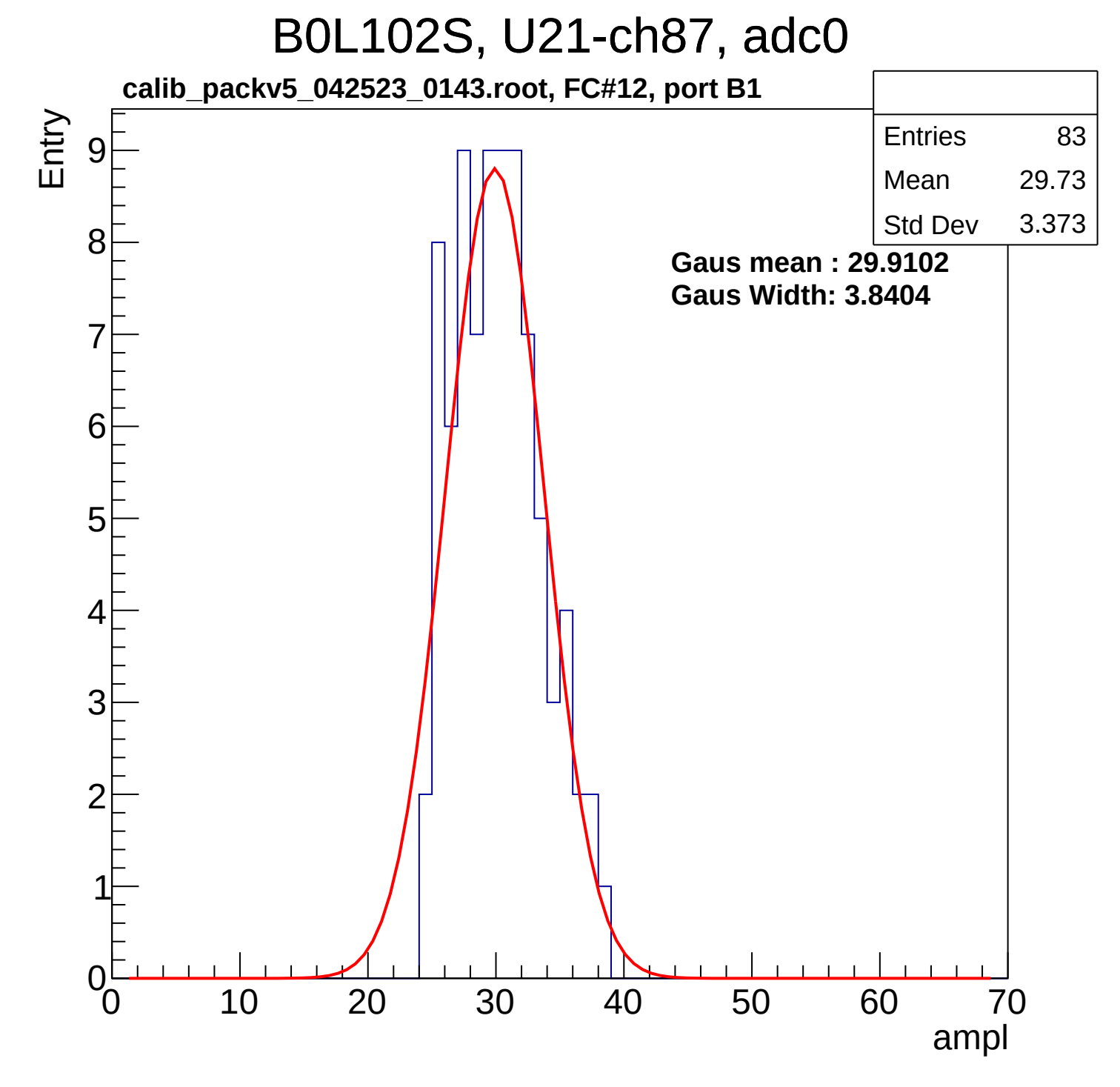
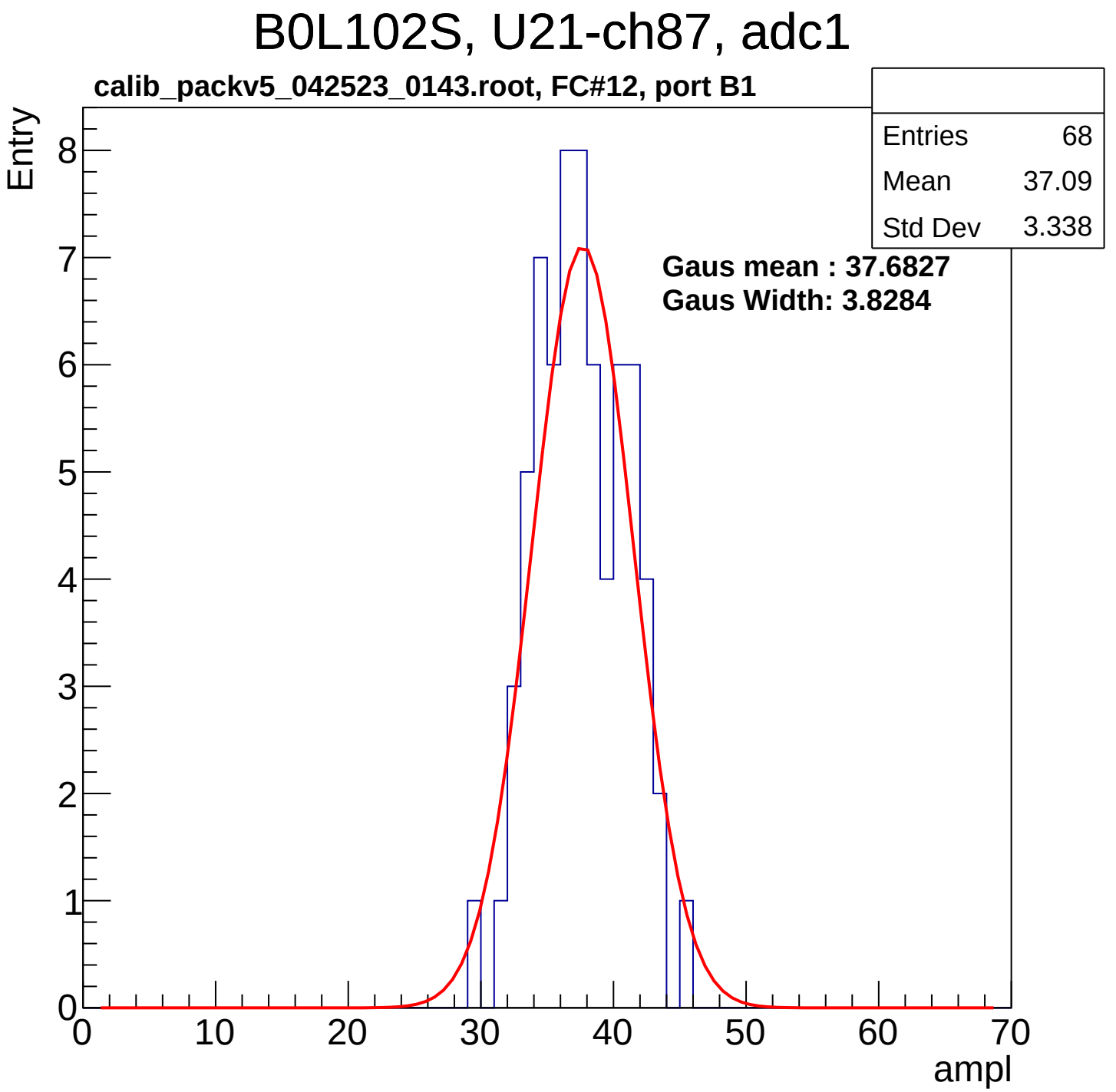
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

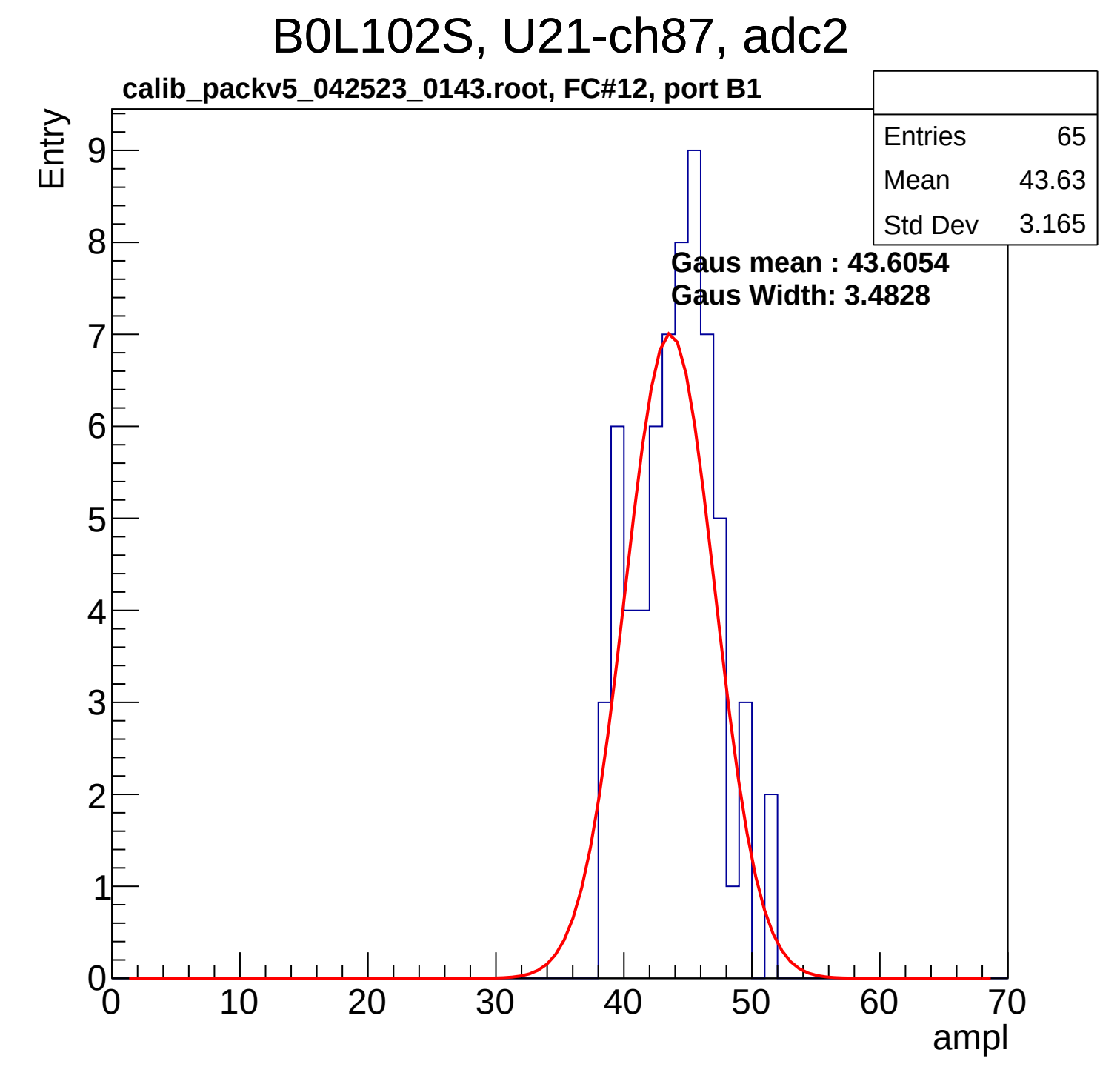

B0L102S, U21-ch86, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 57.64
Std Dev 8.233
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch86, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.5
Std Dev 1.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch86, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch87, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 29.73
Std Dev 3.373
Gaus mean : 29.9102
Gaus Width: 3.8404
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch87, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 37.09
Std Dev 3.338
Gaus mean : 37.6827
Gaus Width: 3.8284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch87, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.63
Std Dev 3.165
Gaus mean : 43.6054
Gaus Width: 3.4828
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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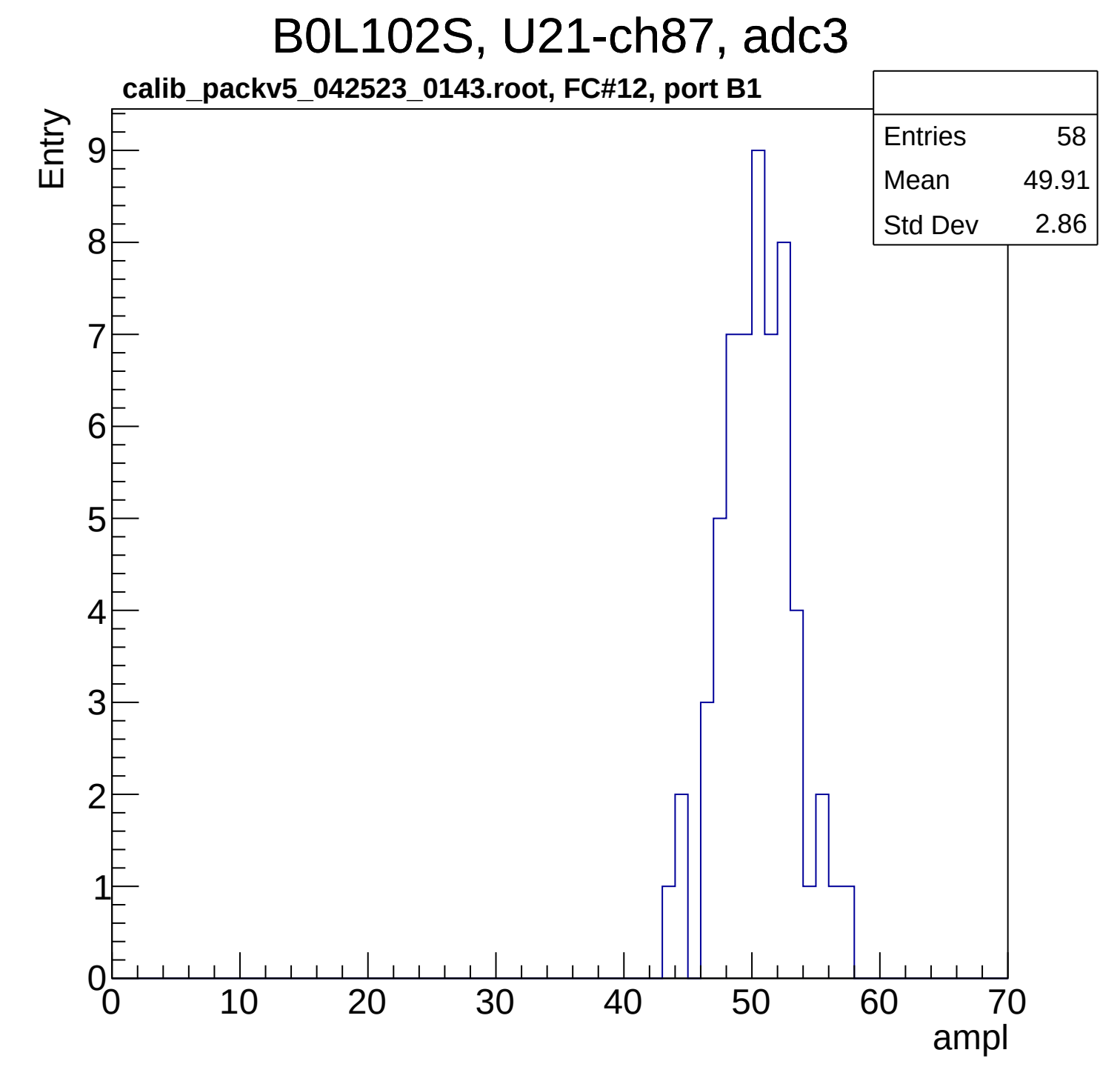
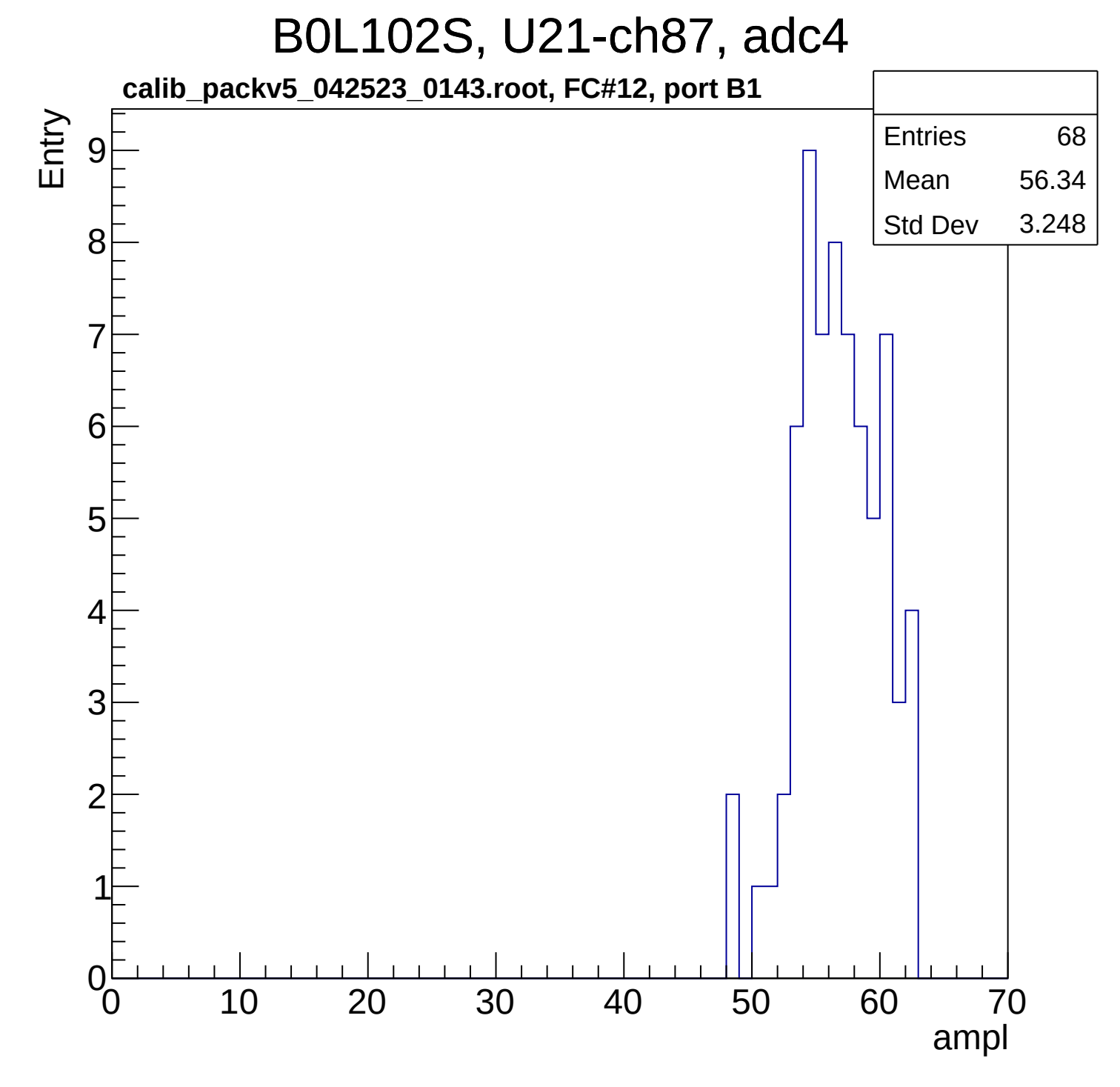

B0L102S, U21-ch87, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 49.91
Std Dev 2.86
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch87, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 56.34
Std Dev 3.248
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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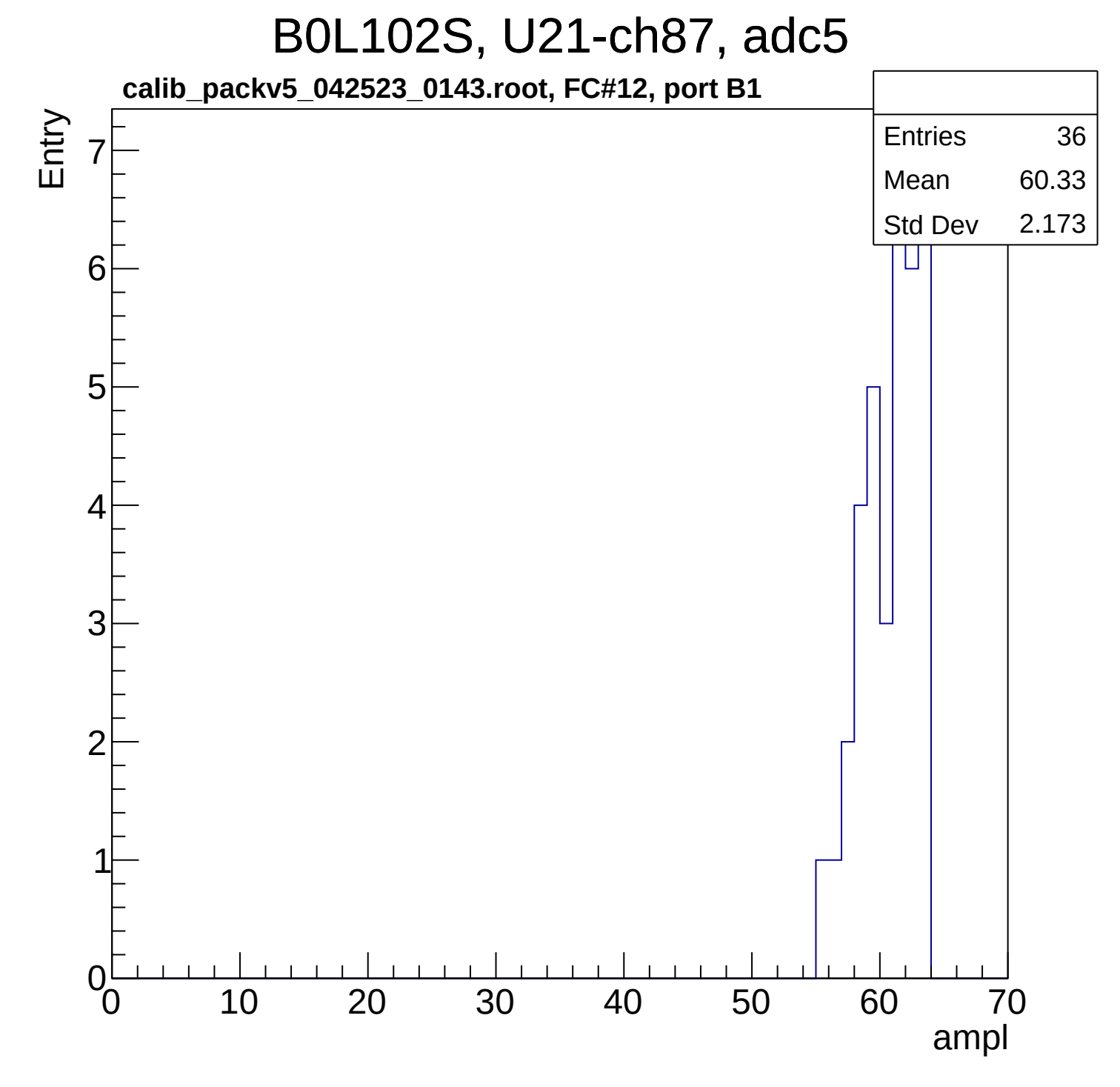
60
70


B0L102S, U21-ch87, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 60.33
Std Dev 2.173
Entry
ampl
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1
2
3
4
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0
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60
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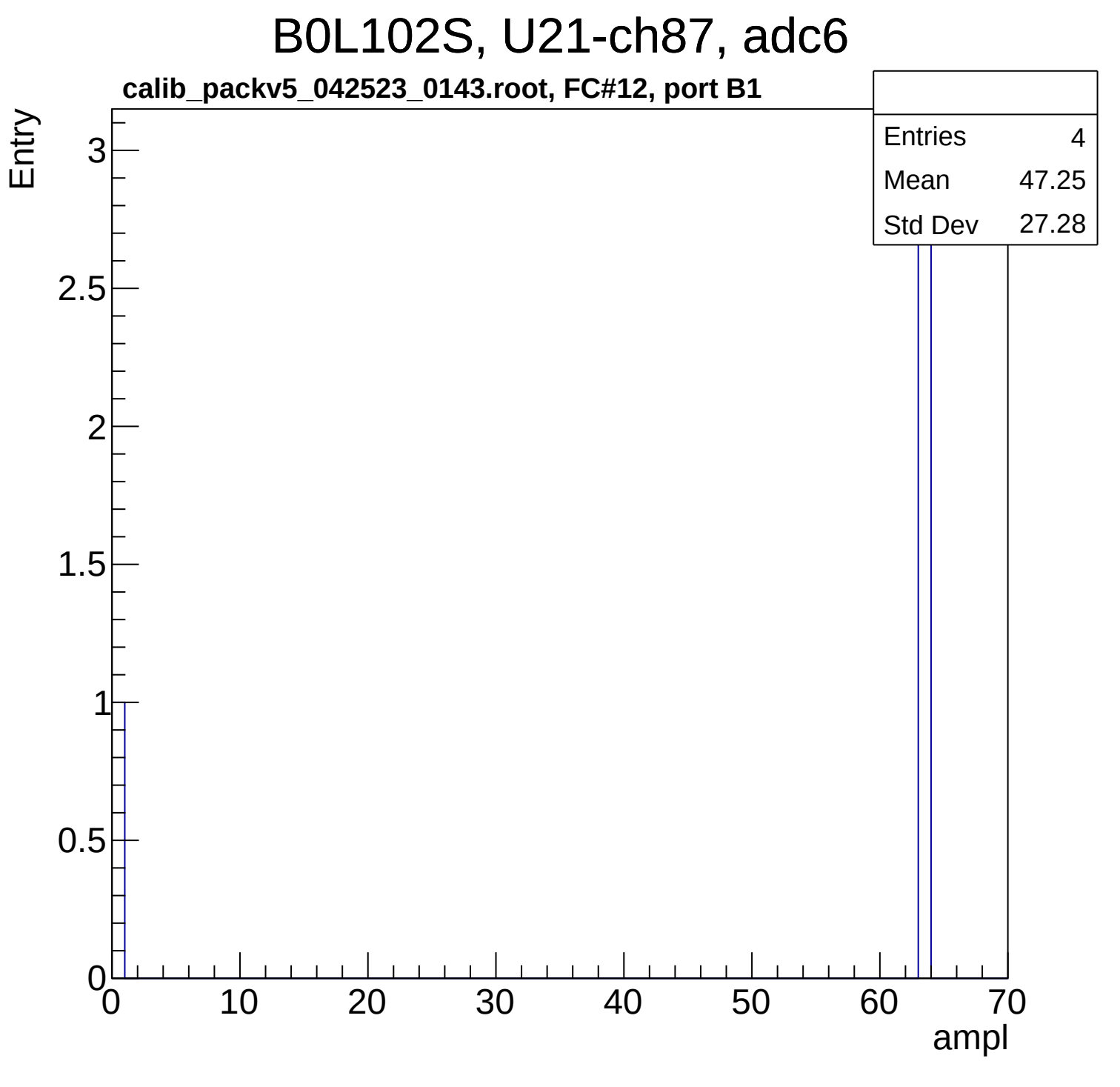
B0L102S, U21-ch87, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
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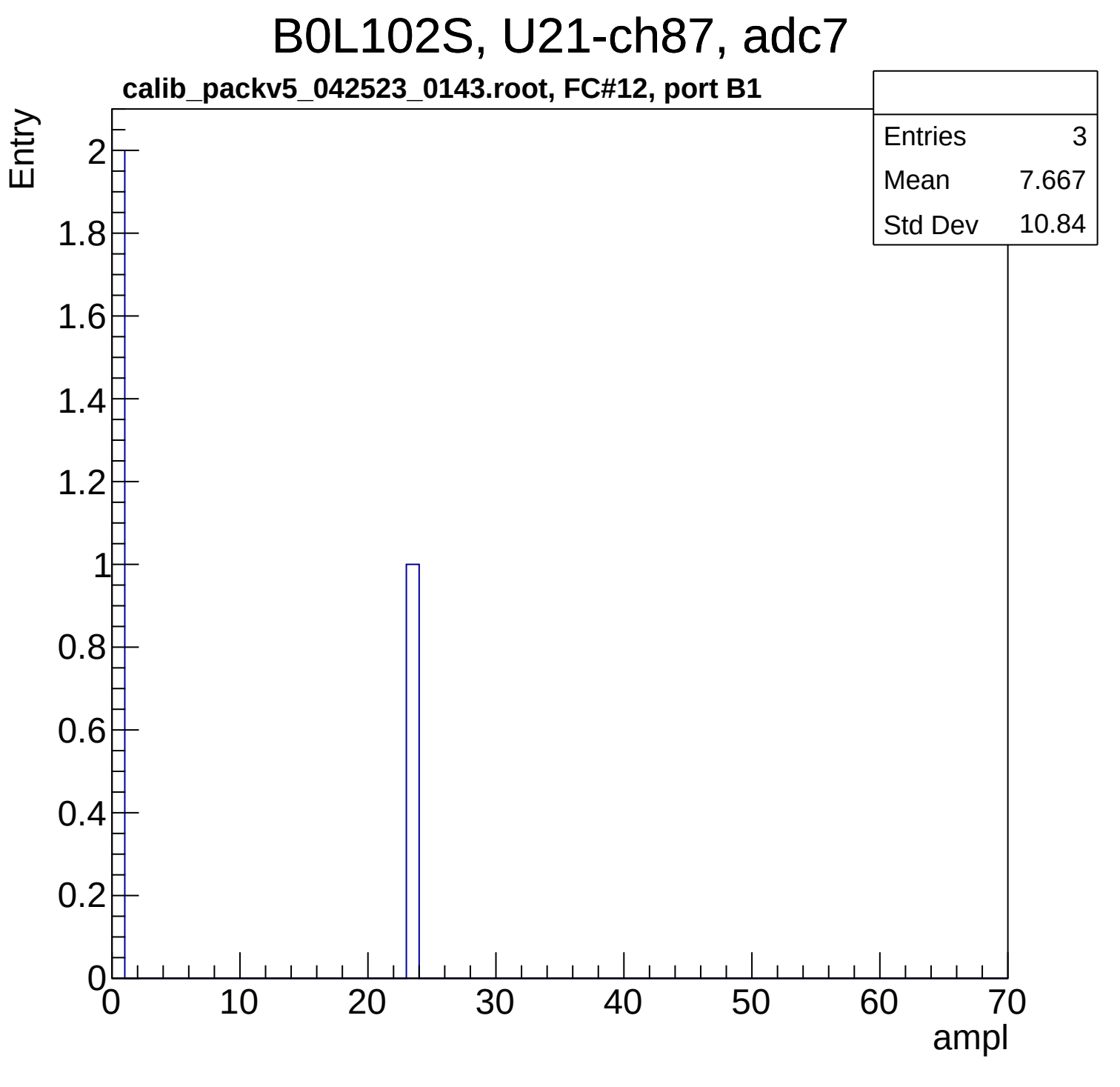
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch87, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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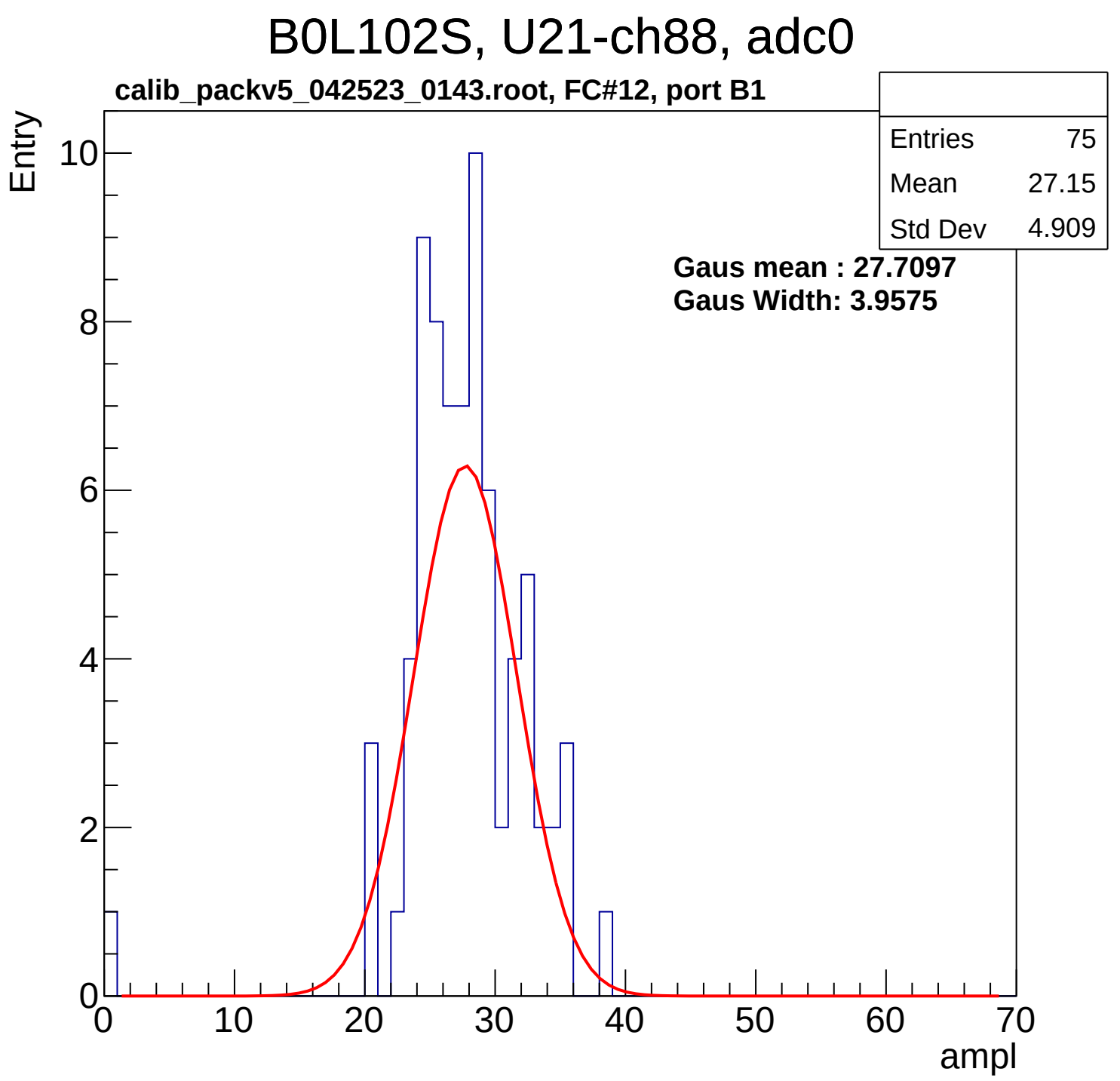
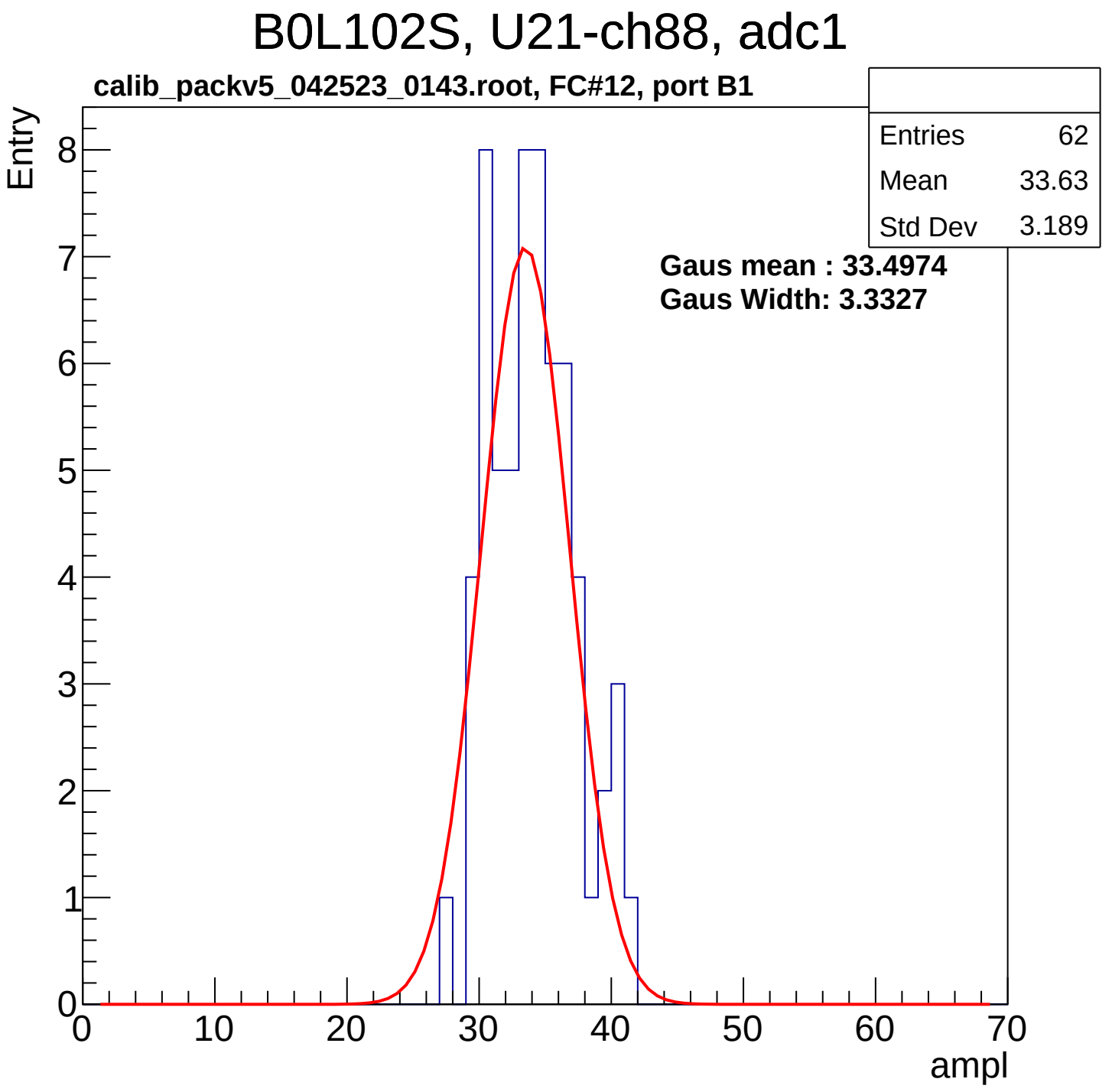
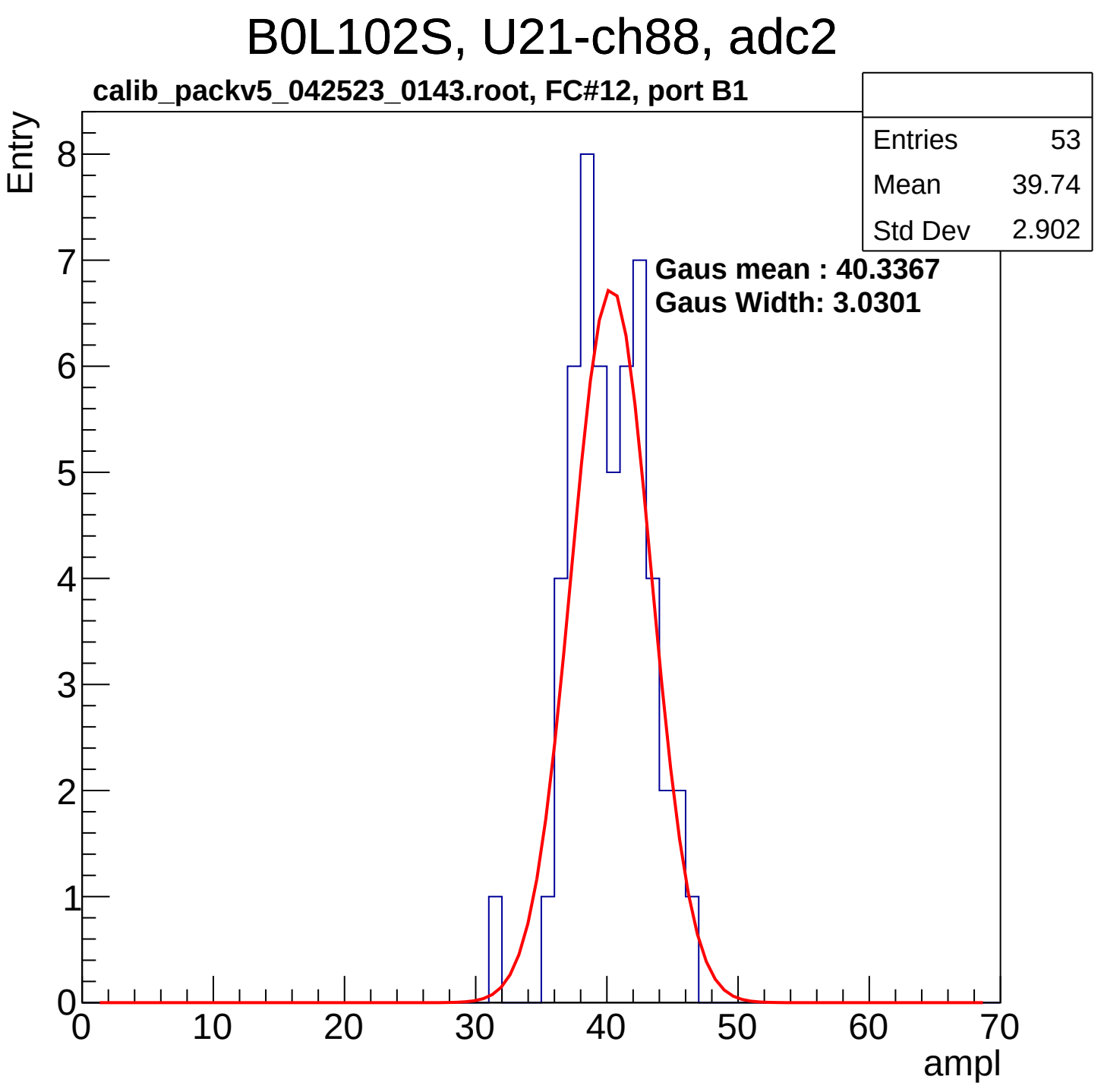

B0L102S, U21-ch88, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 27.15
Std Dev 4.909
Gaus mean : 27.7097
Gaus Width: 3.9575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch88, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 33.63
Std Dev 3.189
Gaus mean : 33.4974
Gaus Width: 3.3327
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch88, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 39.74
Std Dev 2.902
Gaus mean : 40.3367
Gaus Width: 3.0301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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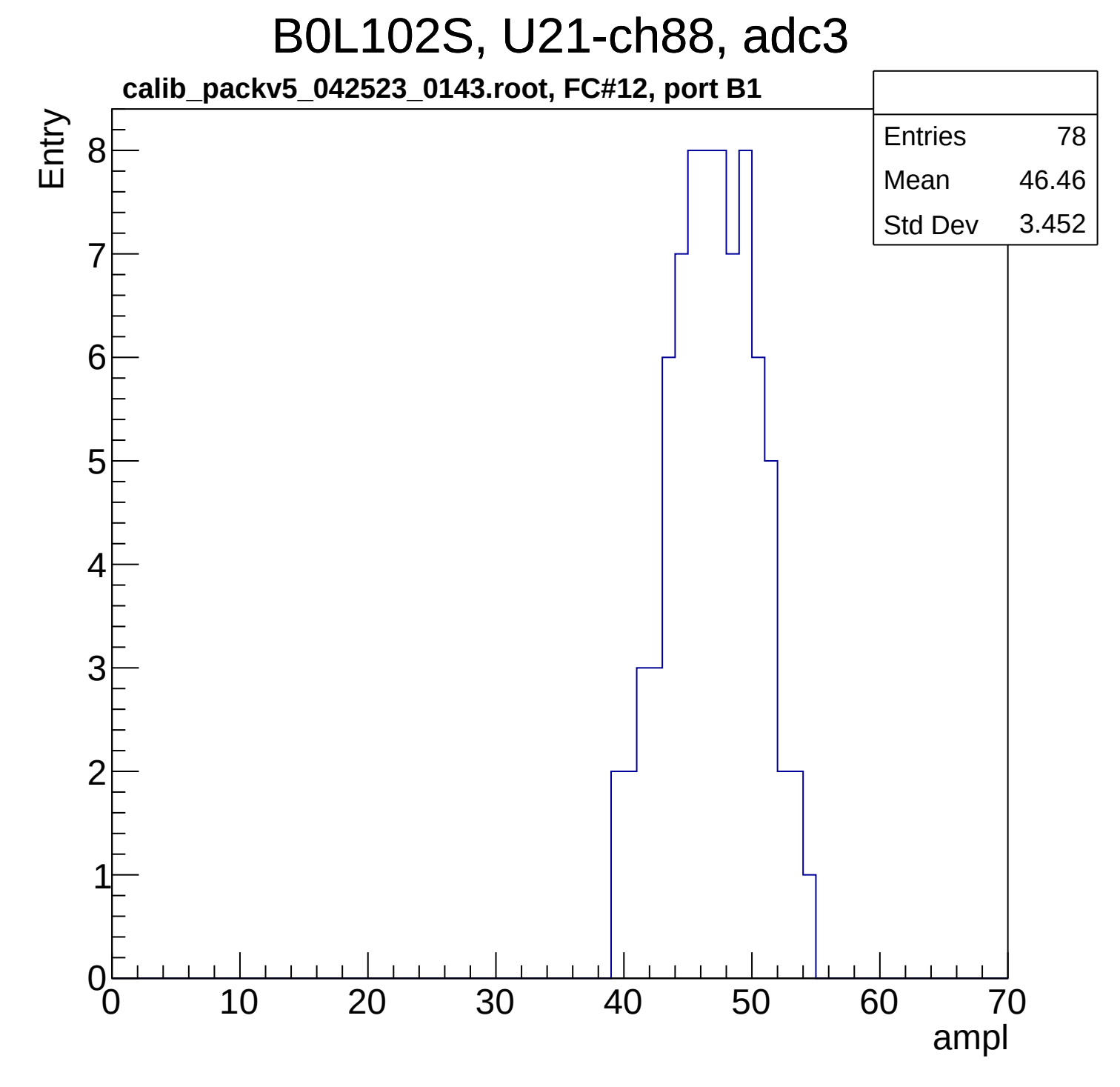
B0L102S, U21-ch88, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 46.46
Std Dev 3.452
Entry
ampl
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5
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7
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0
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60
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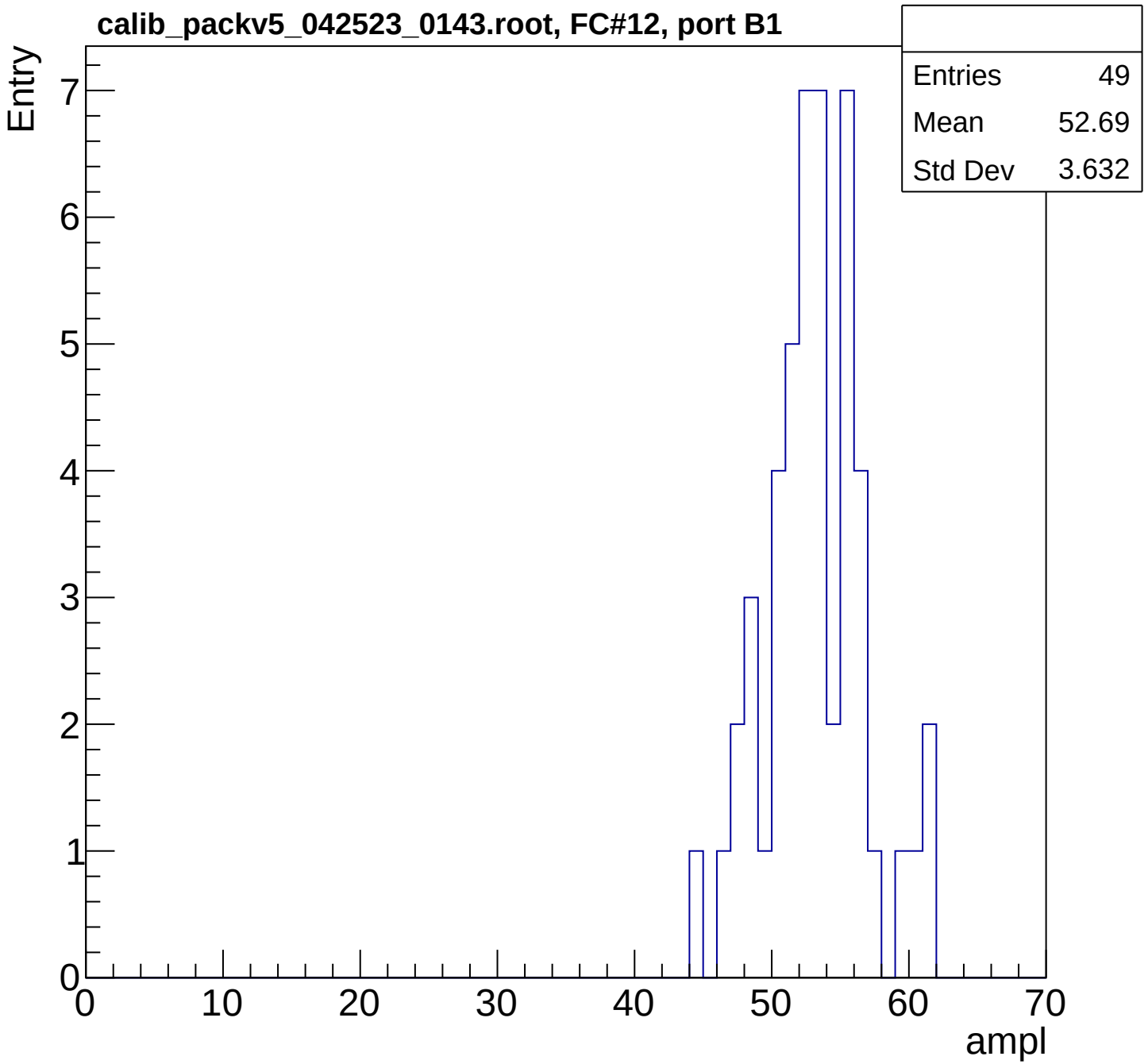
B0L102S, U21-ch88, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 52.69
Std Dev 3.632
Entry
ampl
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3
4
5
6
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0
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40
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60
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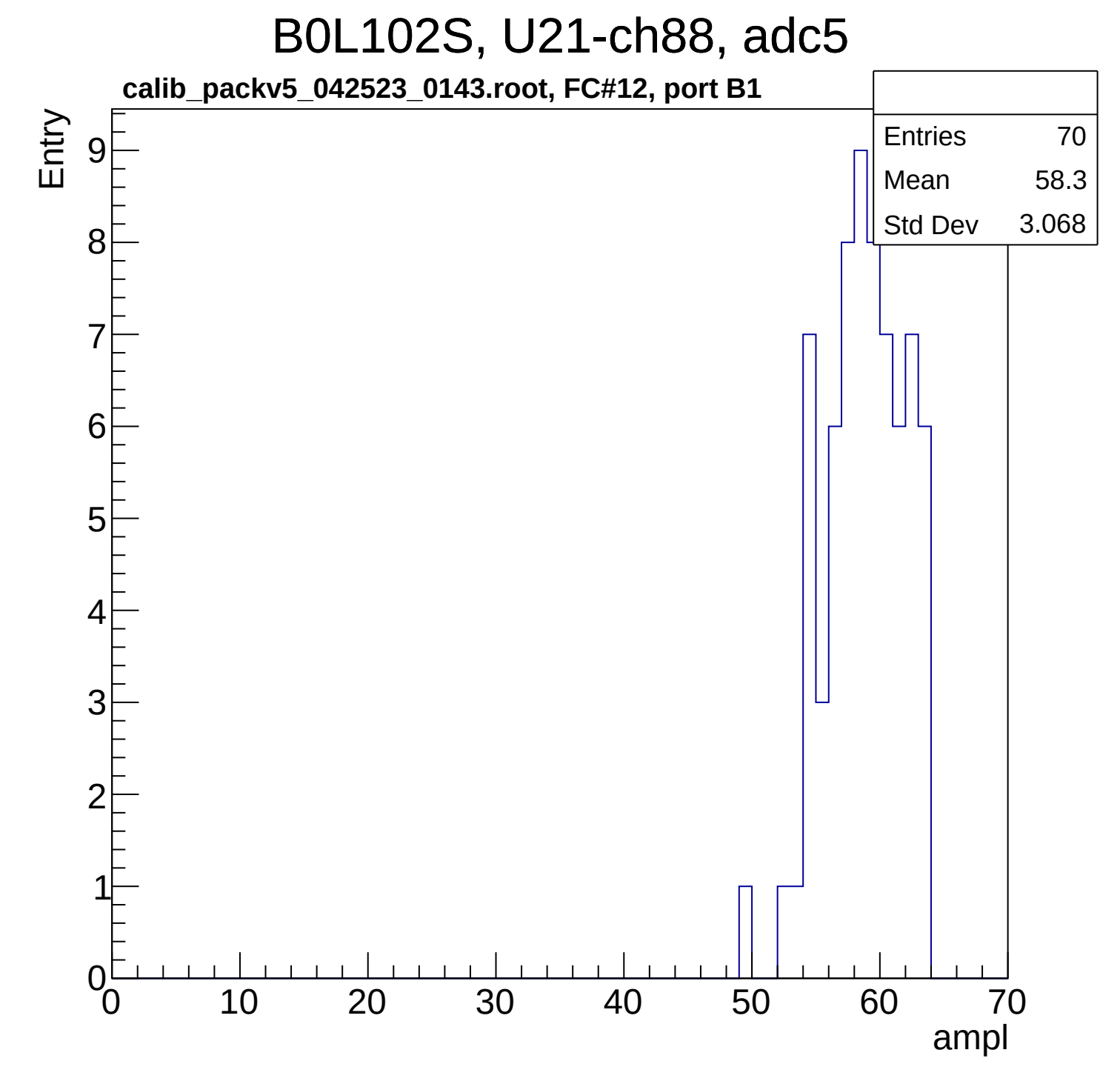
B0L102S, U21-ch88, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 58.3
Std Dev 3.068
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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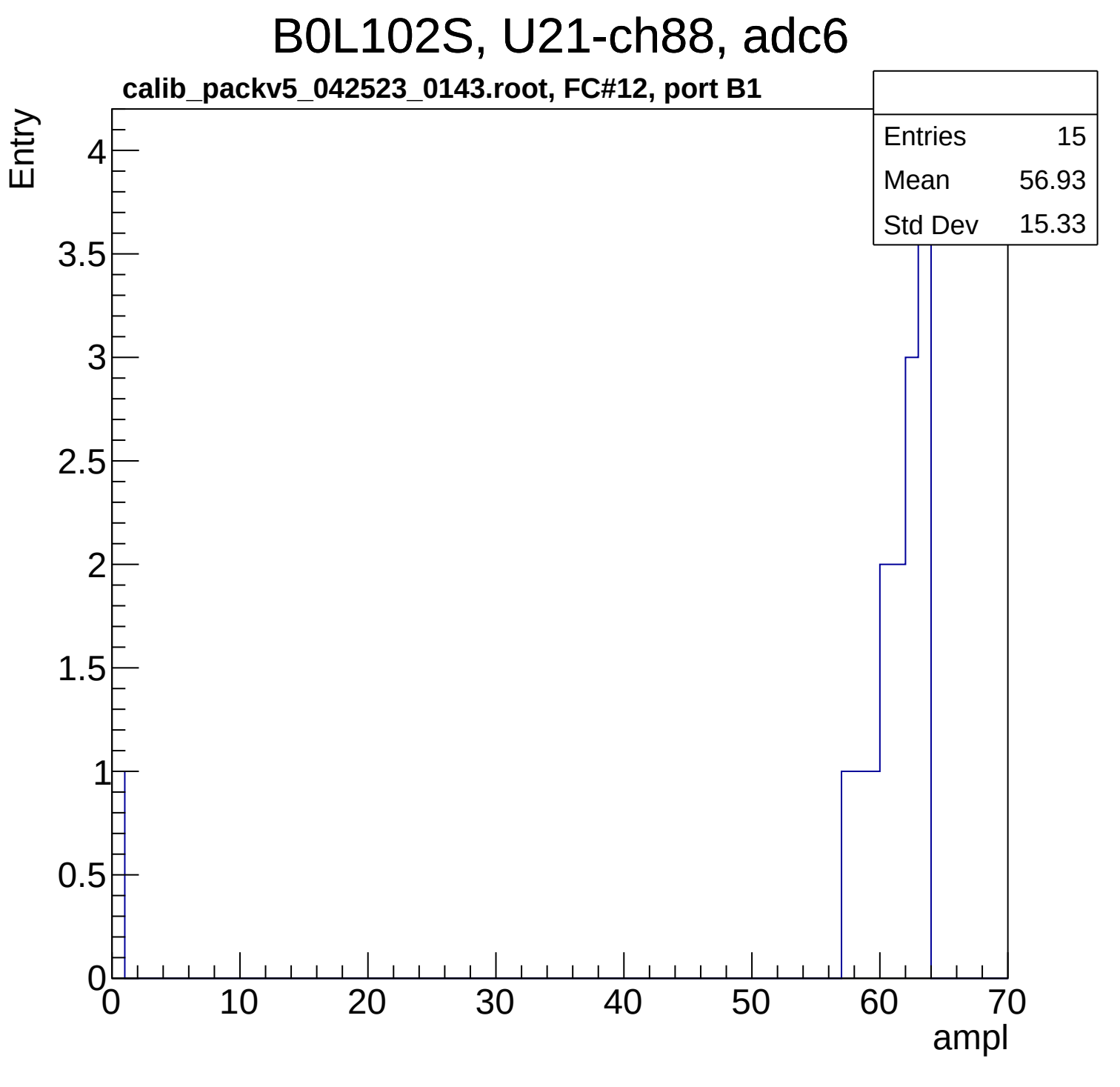
B0L102S, U21-ch88, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 56.93
Std Dev 15.33
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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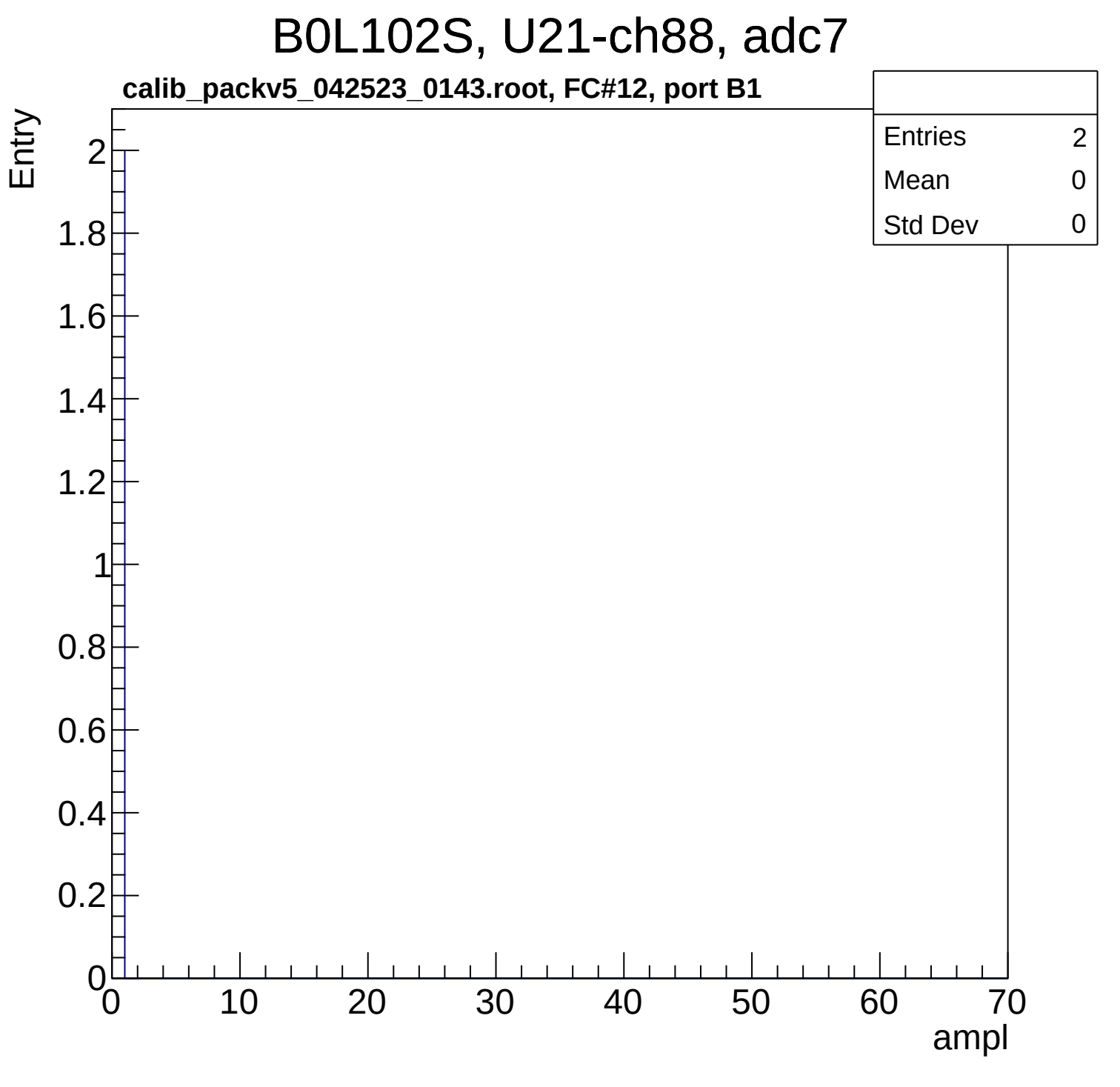
B0L102S, U21-ch88, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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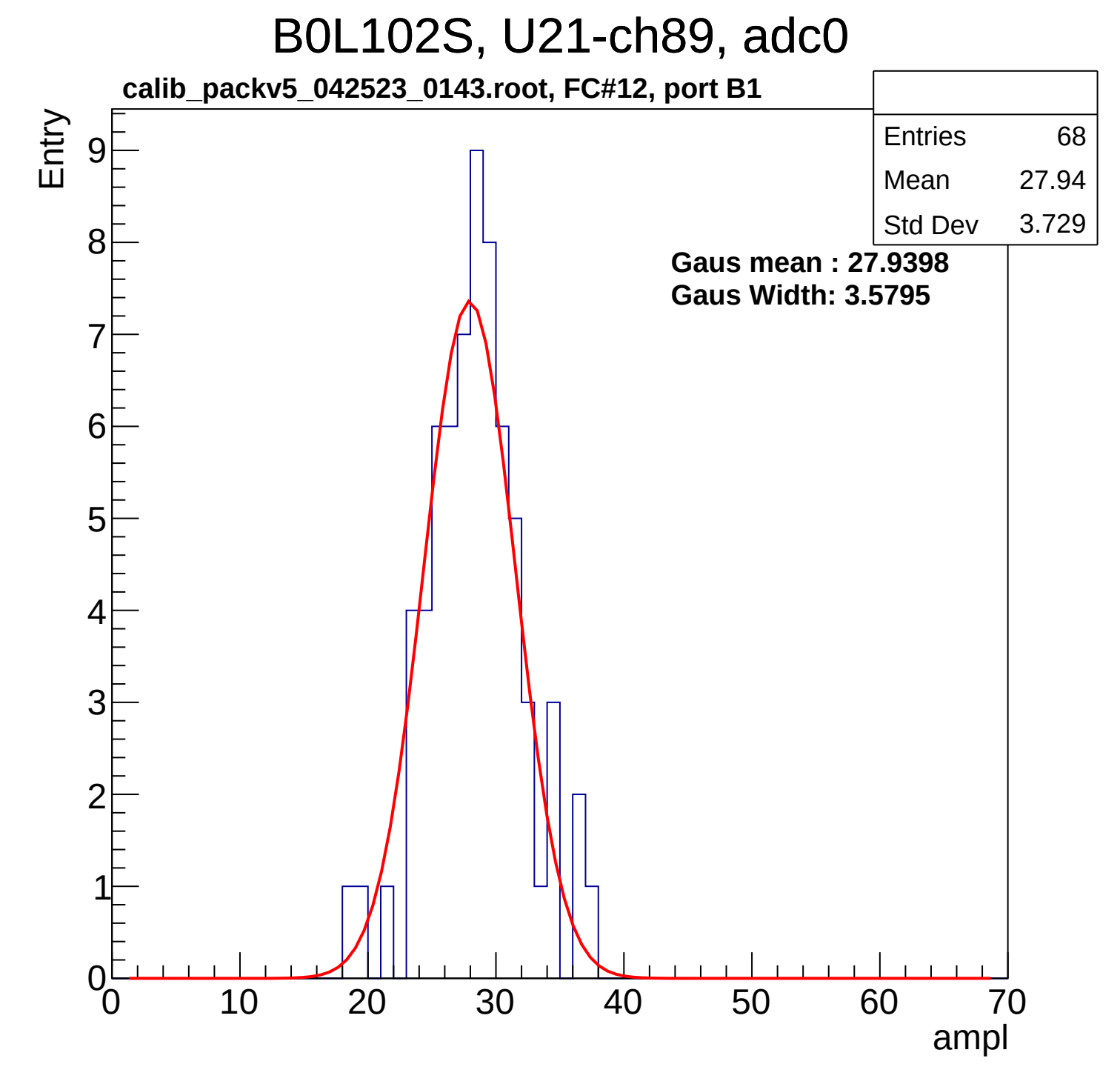
B0L102S, U21-ch89, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 27.94
Std Dev 3.729
Gaus mean : 27.9398
Gaus Width: 3.5795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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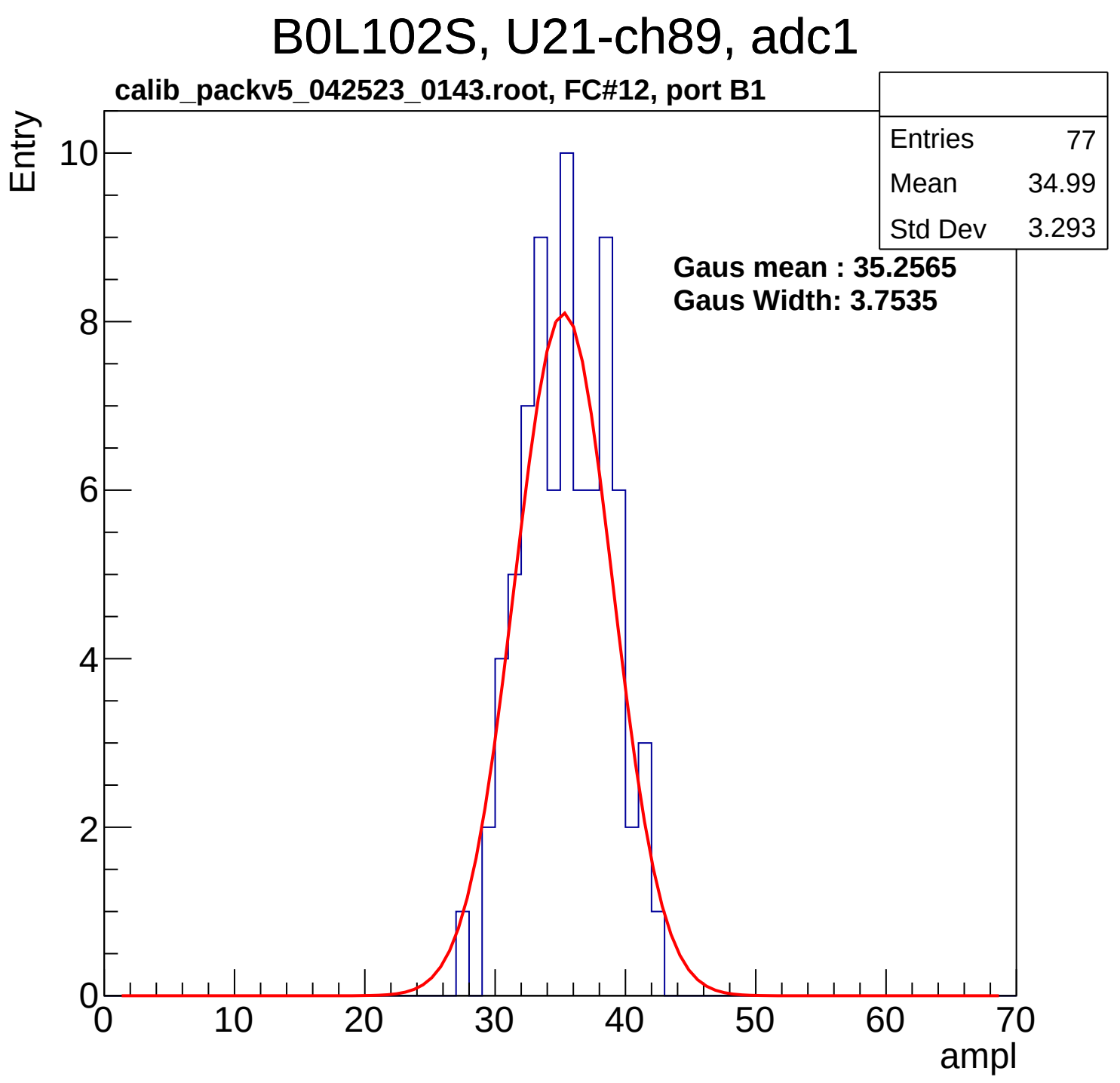
B0L102S, U21-ch89, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 34.99
Std Dev 3.293
Gaus mean : 35.2565
Gaus Width: 3.7535
Entry
ampl
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2
4
6
8
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0
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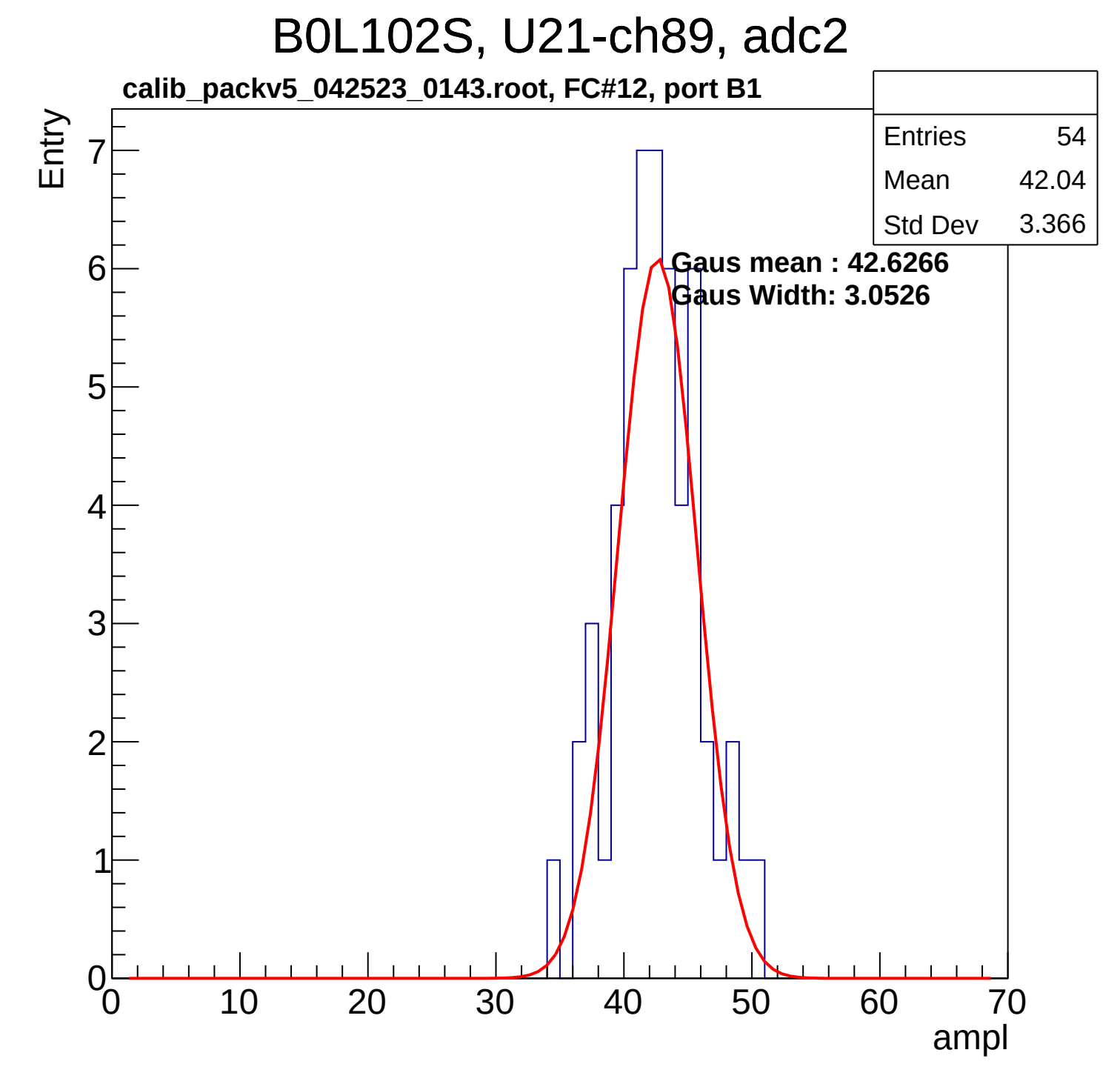
B0L102S, U21-ch89, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 42.04
Std Dev 3.366
Gaus mean : 42.6266
Gaus Width: 3.0526
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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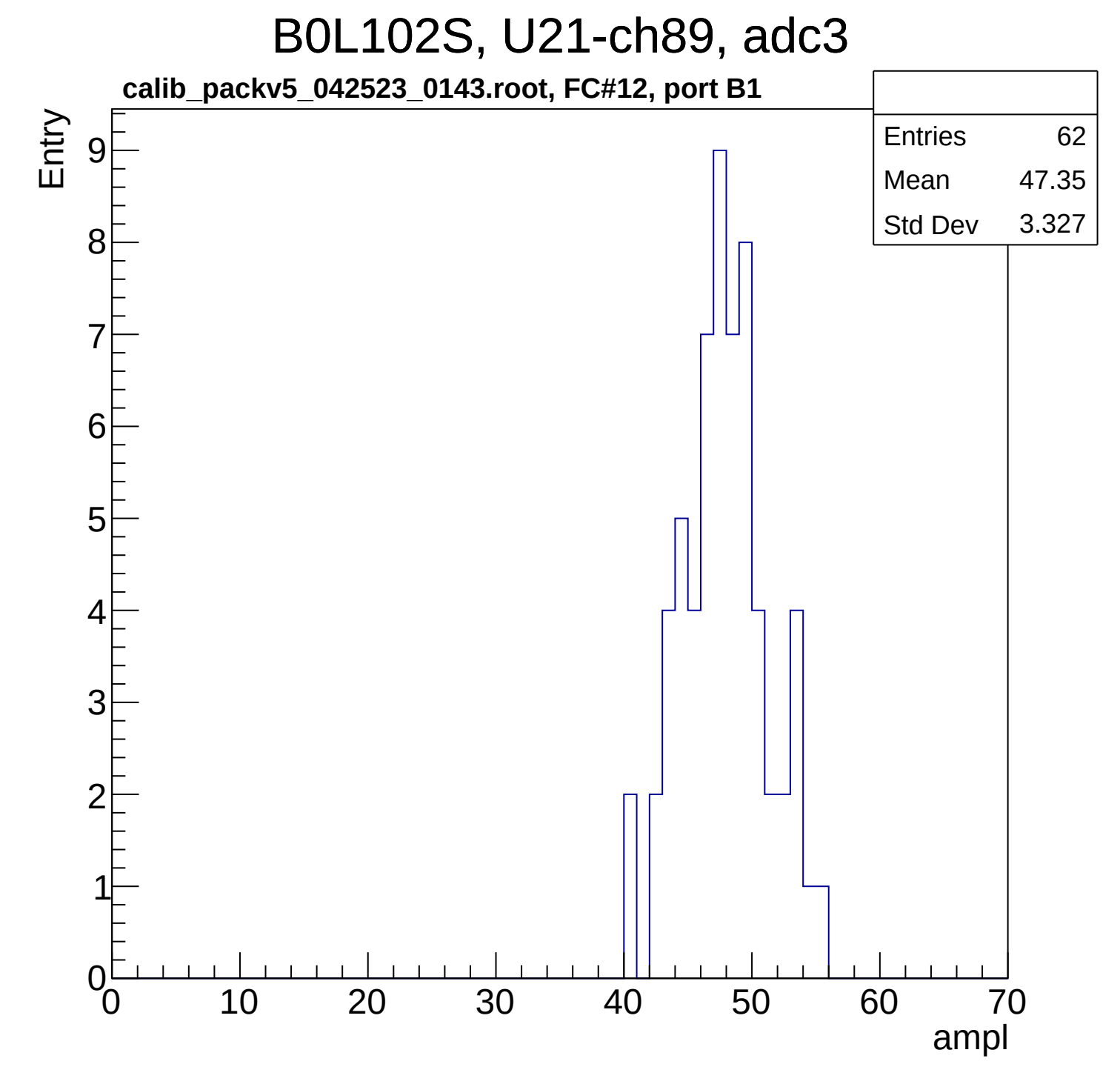
B0L102S, U21-ch89, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 47.35
Std Dev 3.327
Entry
ampl
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1
2
3
4
5
6
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0
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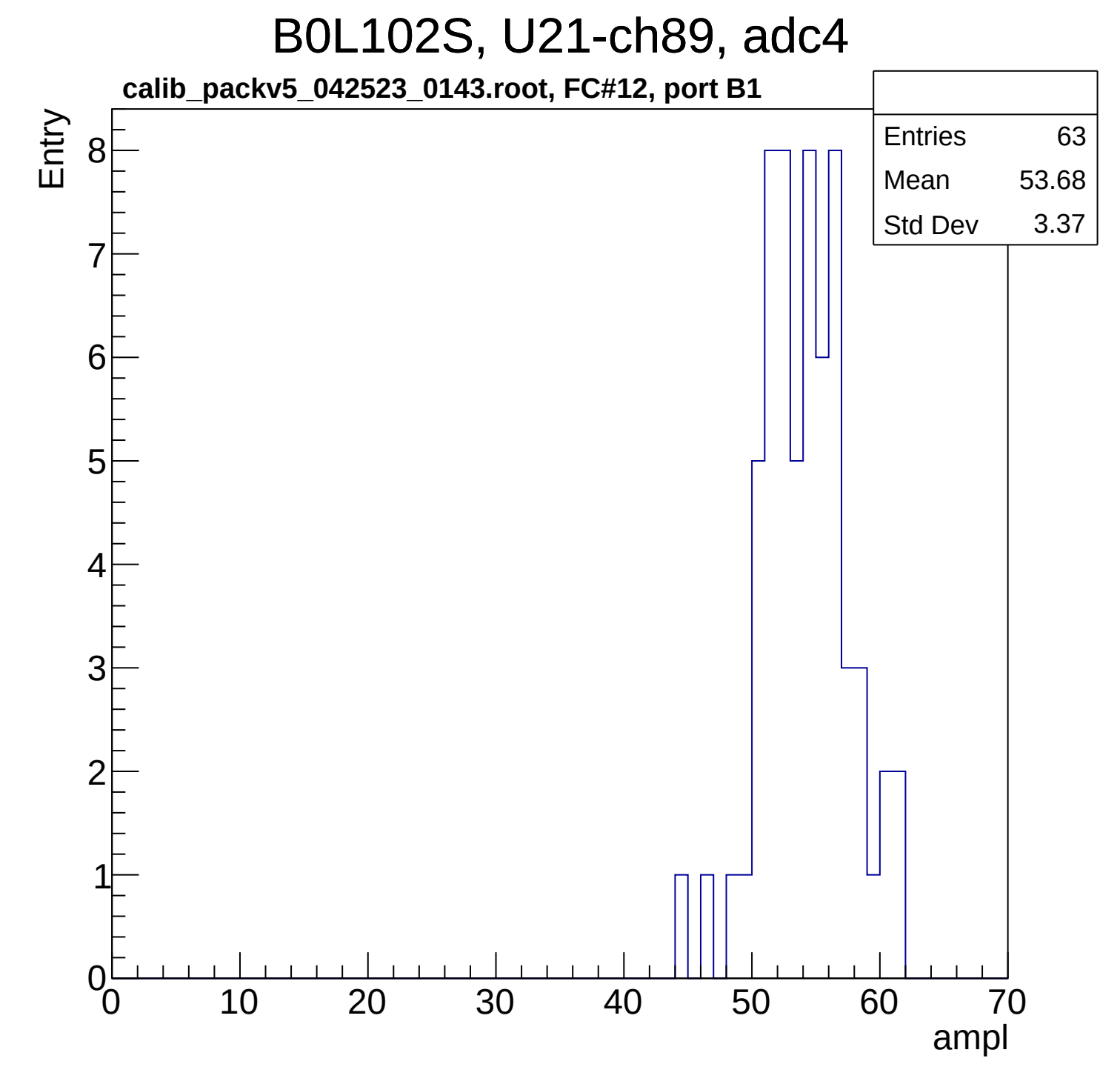
B0L102S, U21-ch89, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 53.68
Std Dev 3.37
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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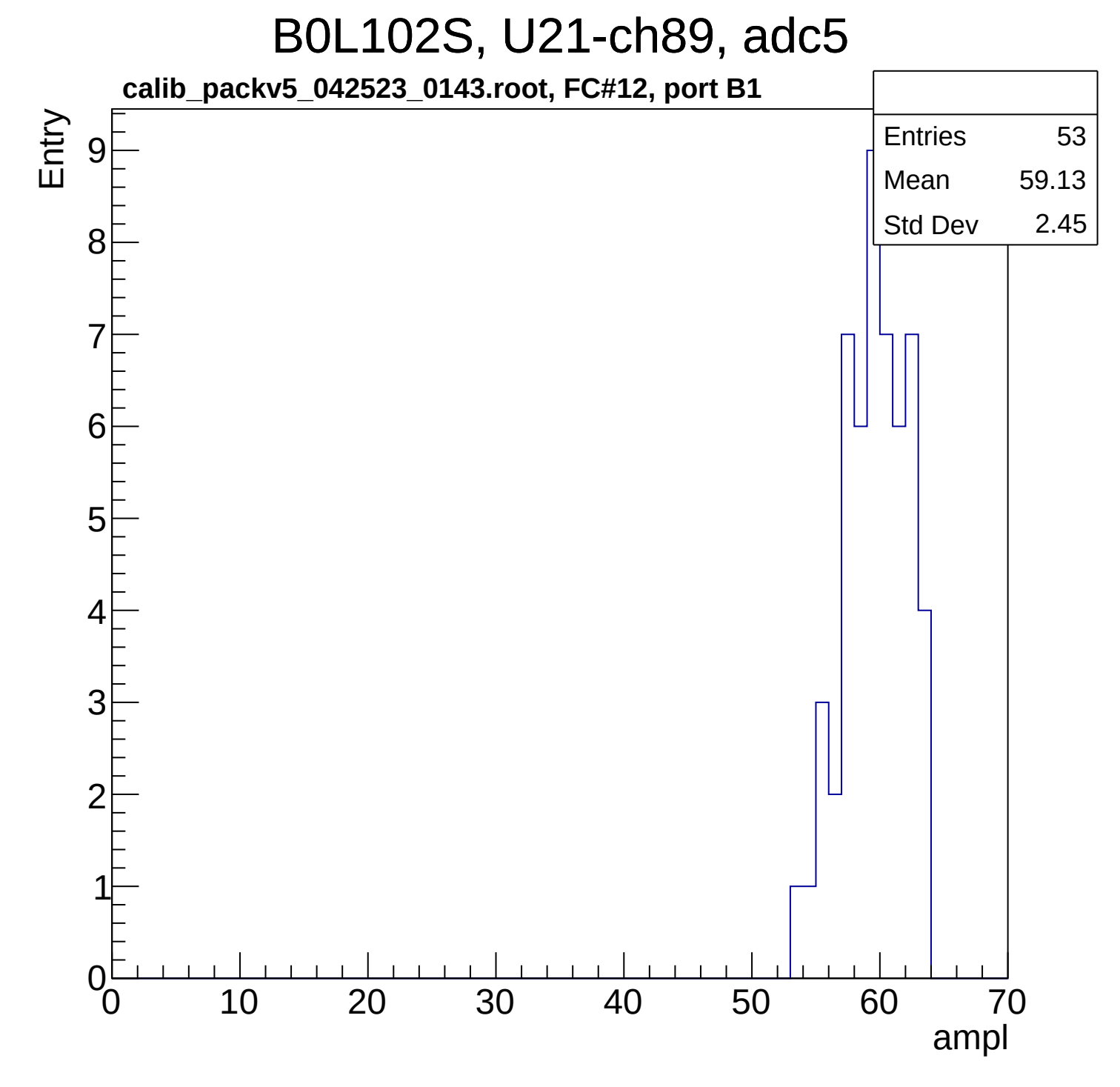
B0L102S, U21-ch89, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 59.13
Std Dev 2.45
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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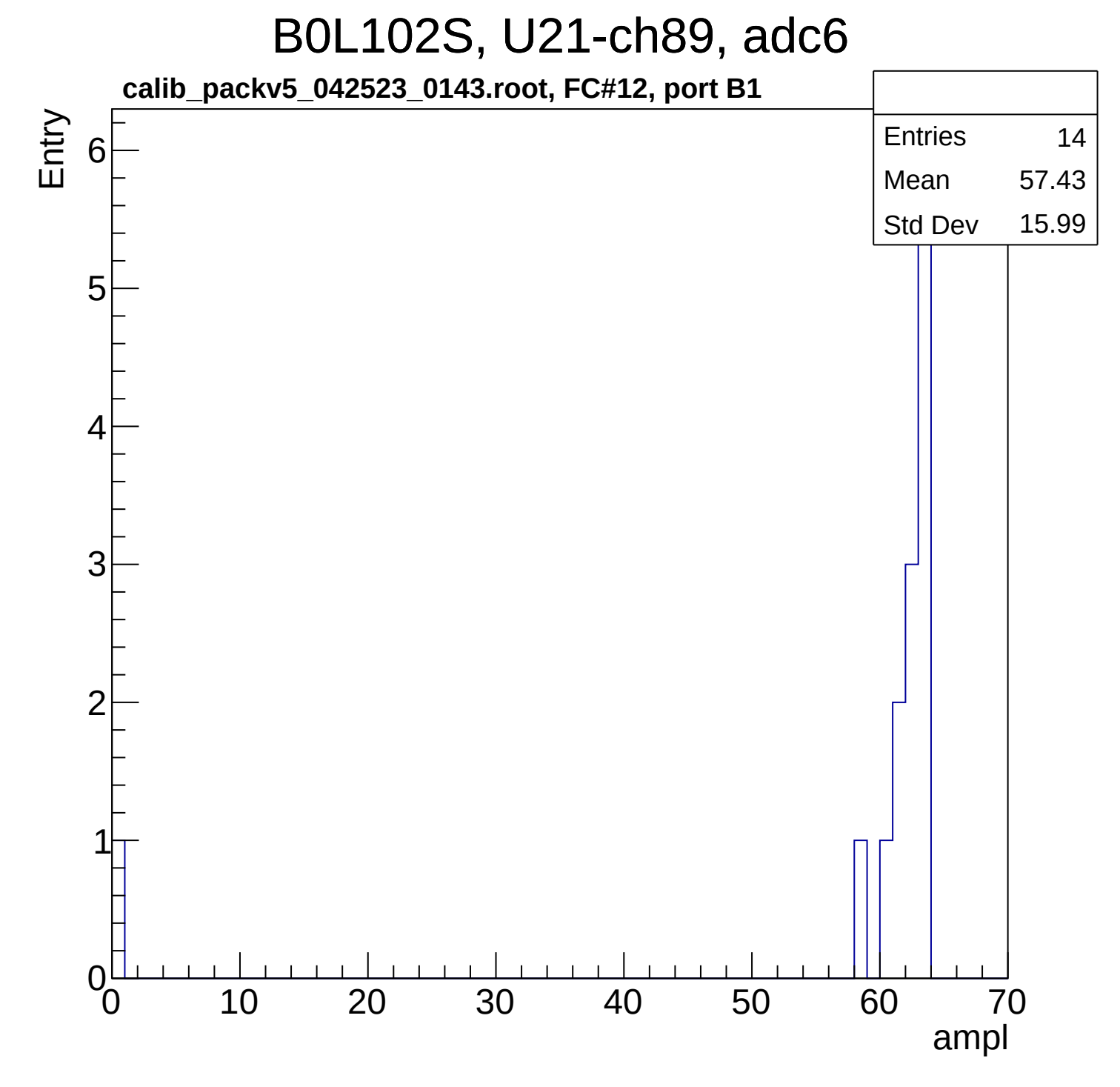
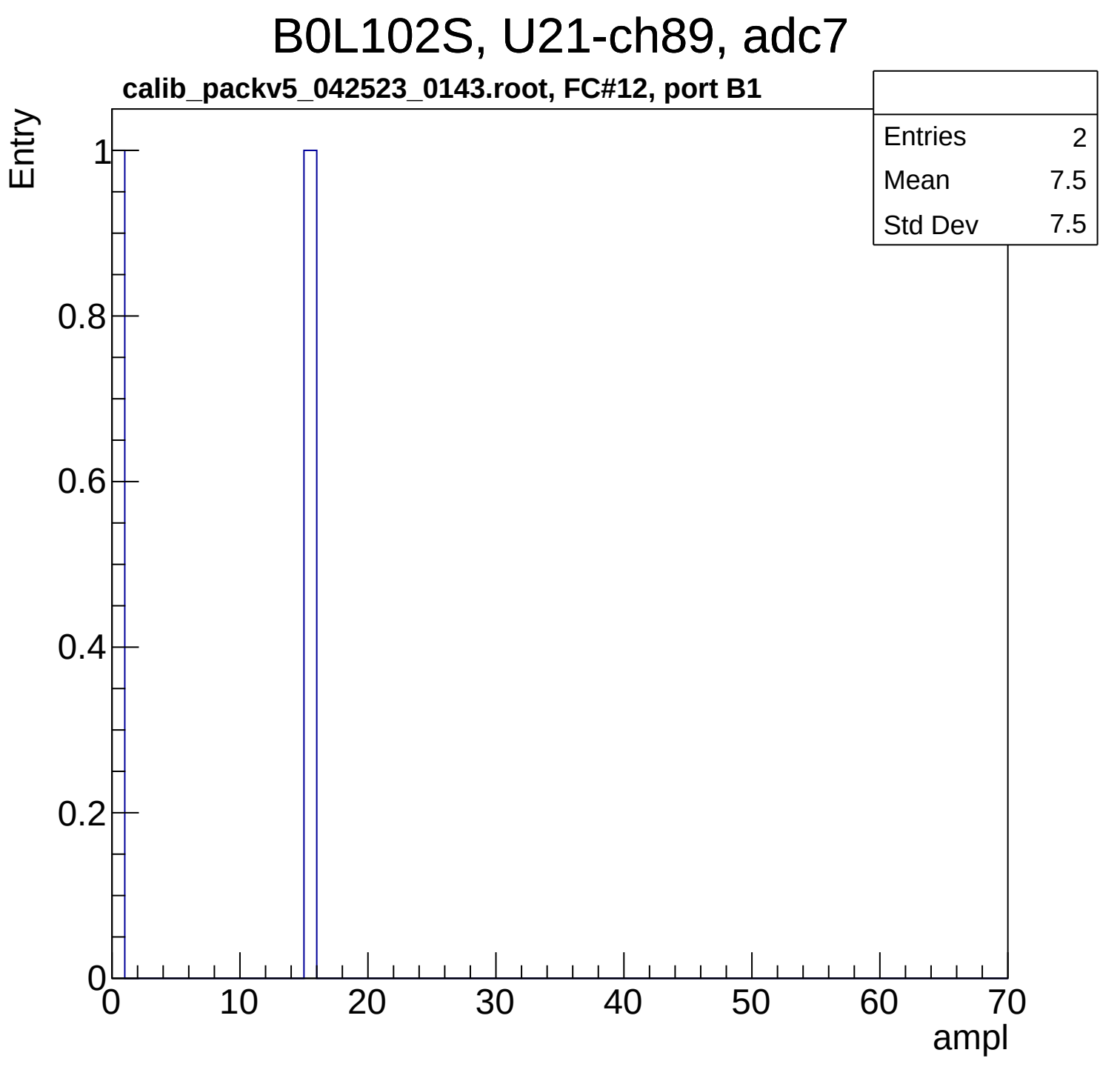
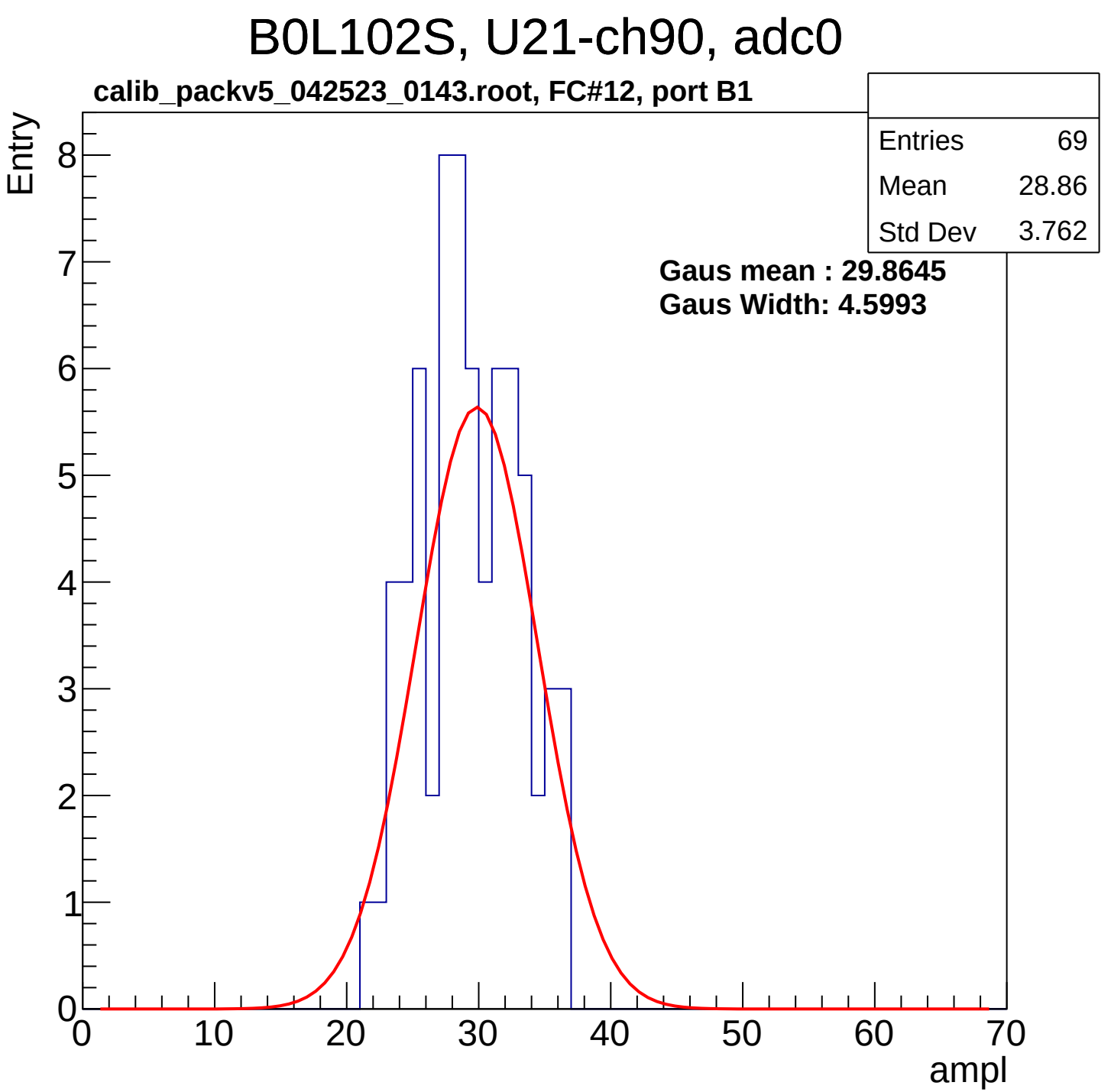
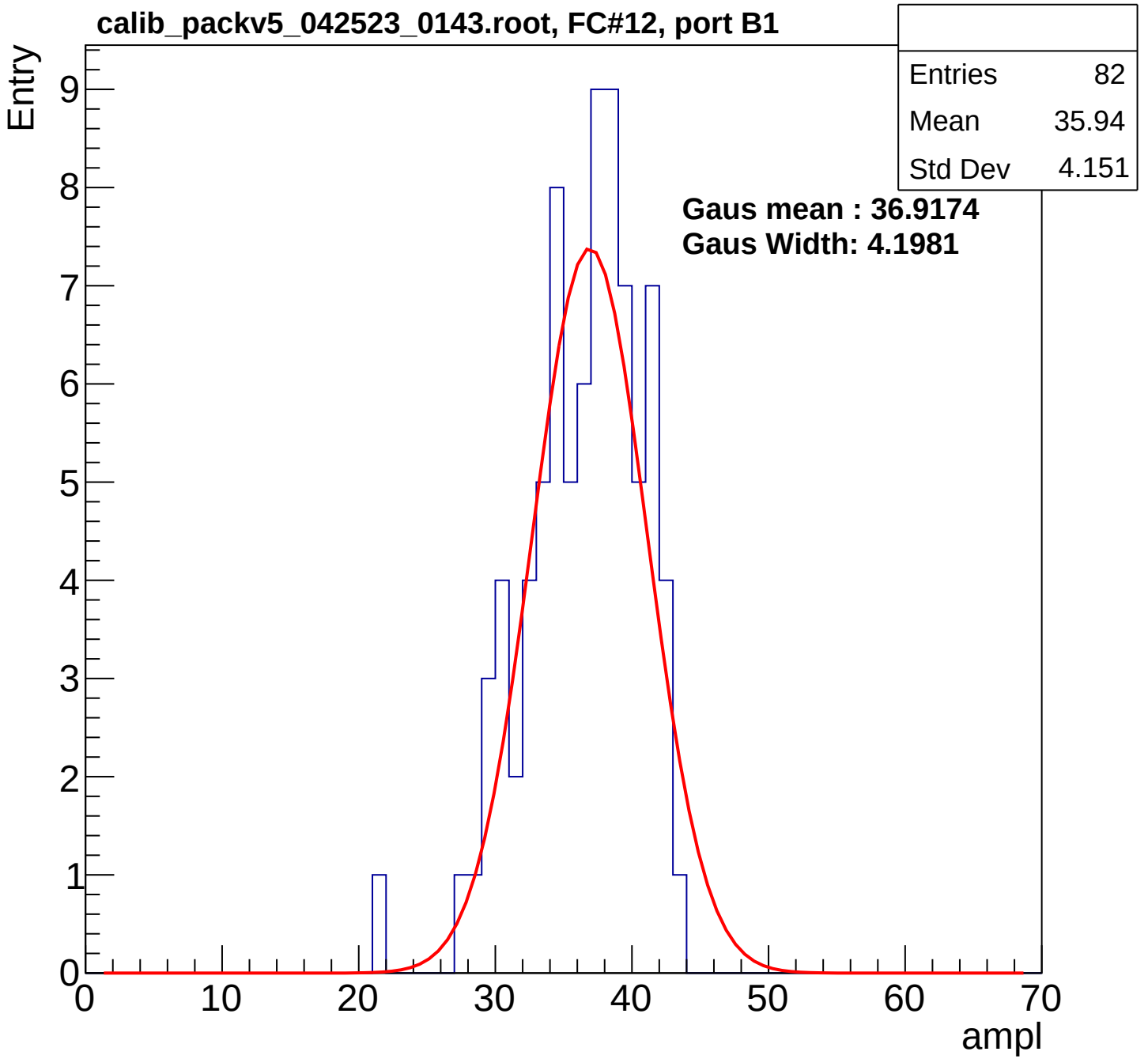
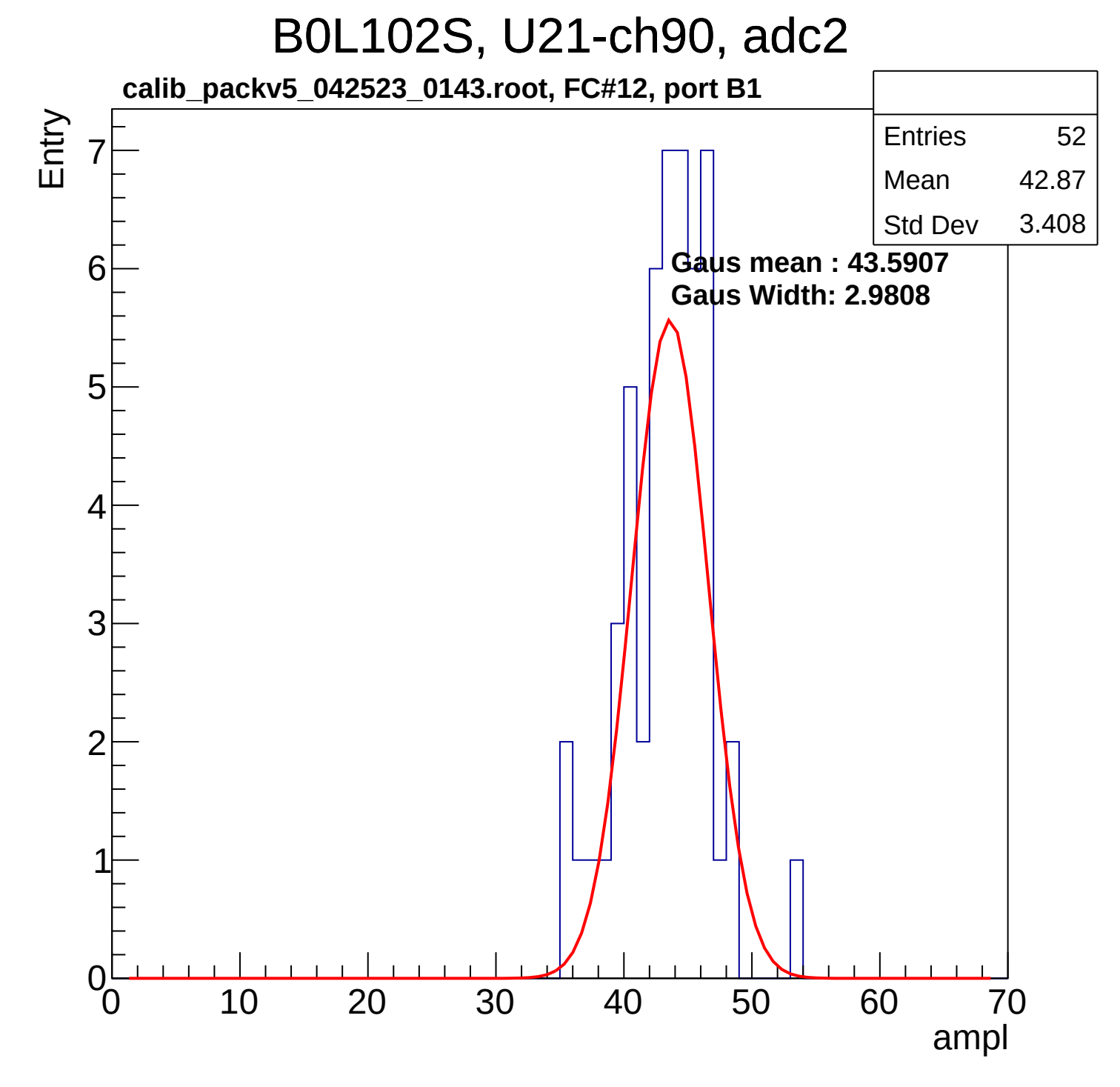
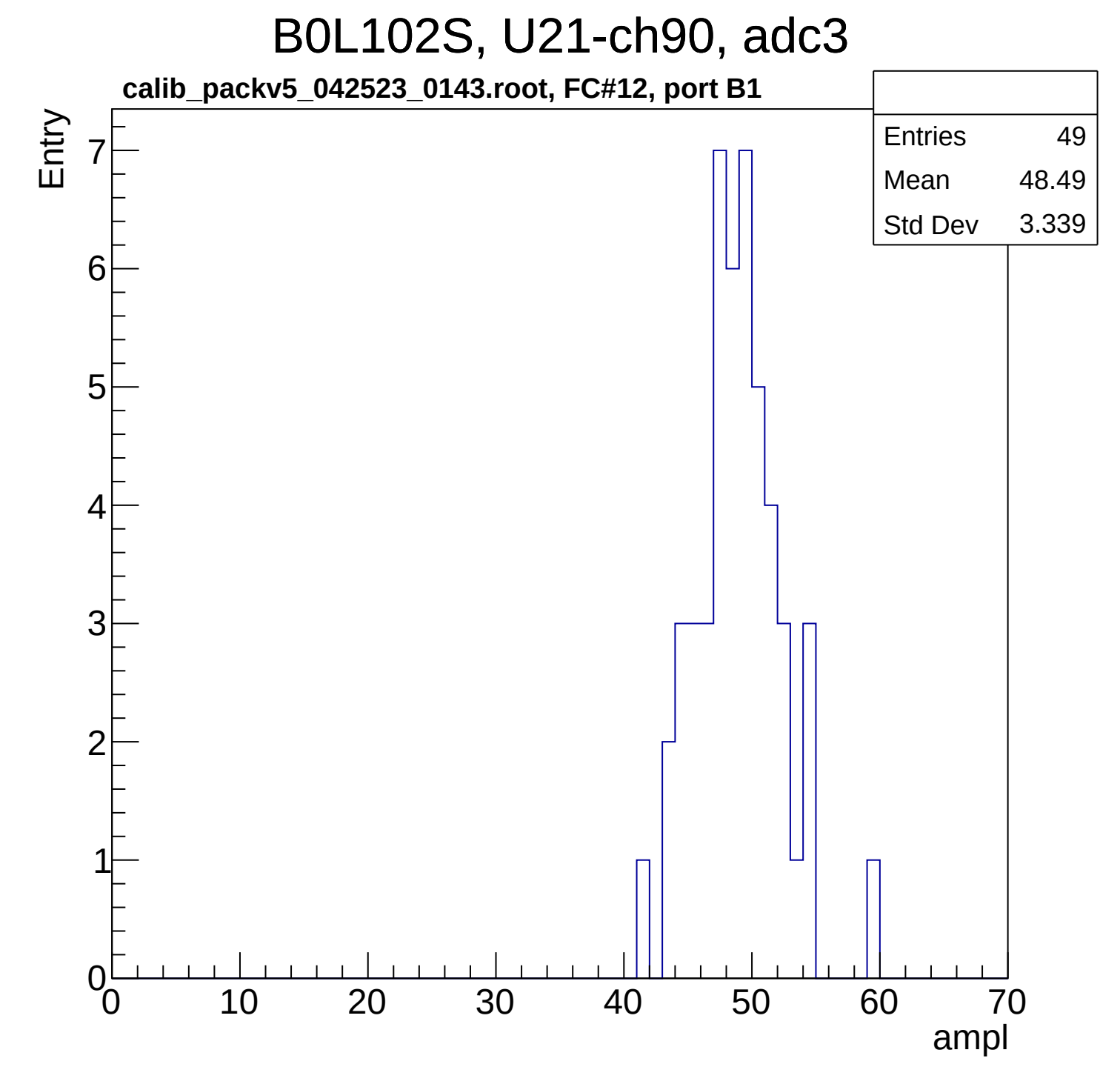

B0L102S, U21-ch89, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 57.43
Std Dev 15.99
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

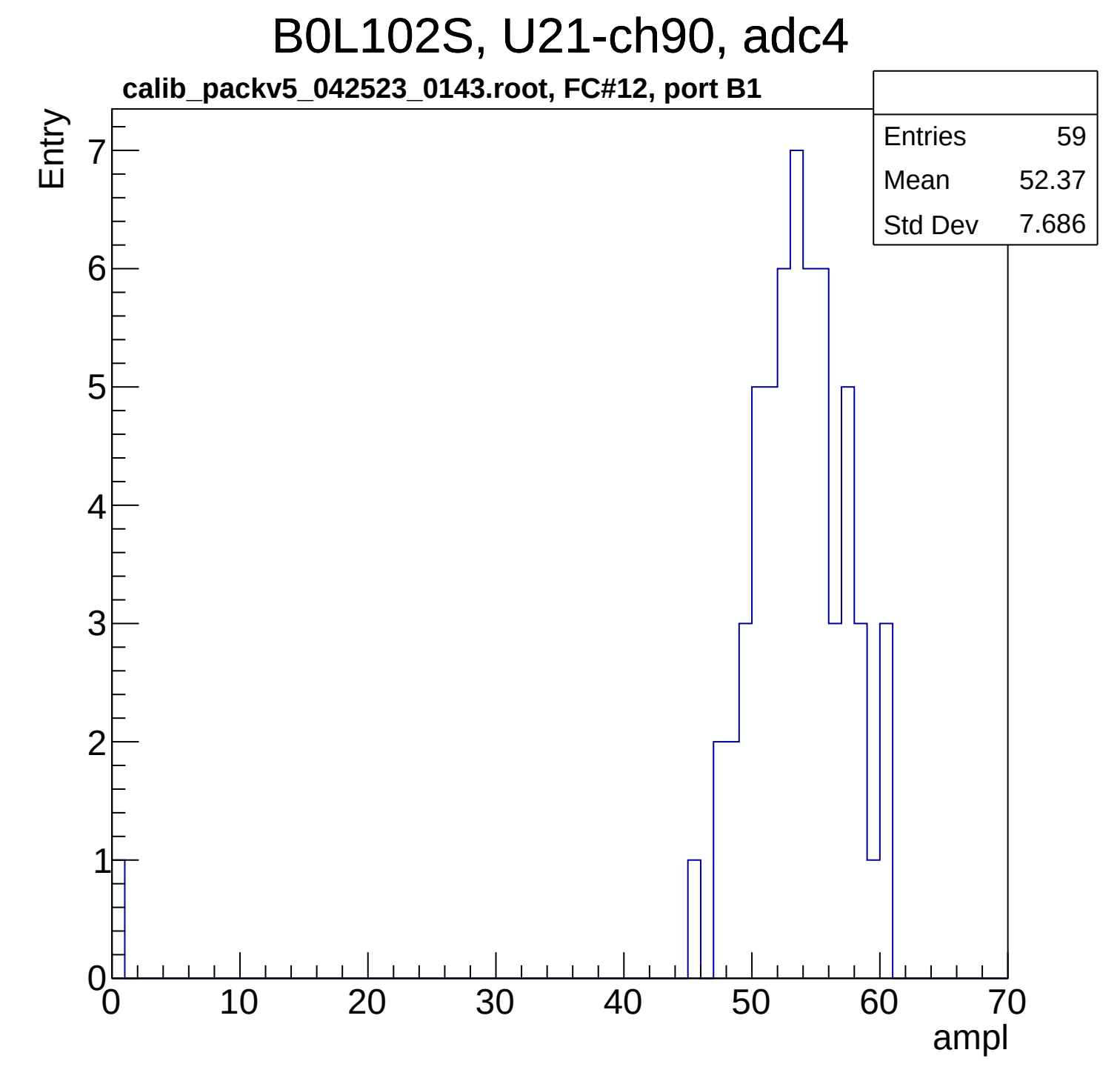

B0L102S, U21-ch89, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 7.5
Std Dev 7.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

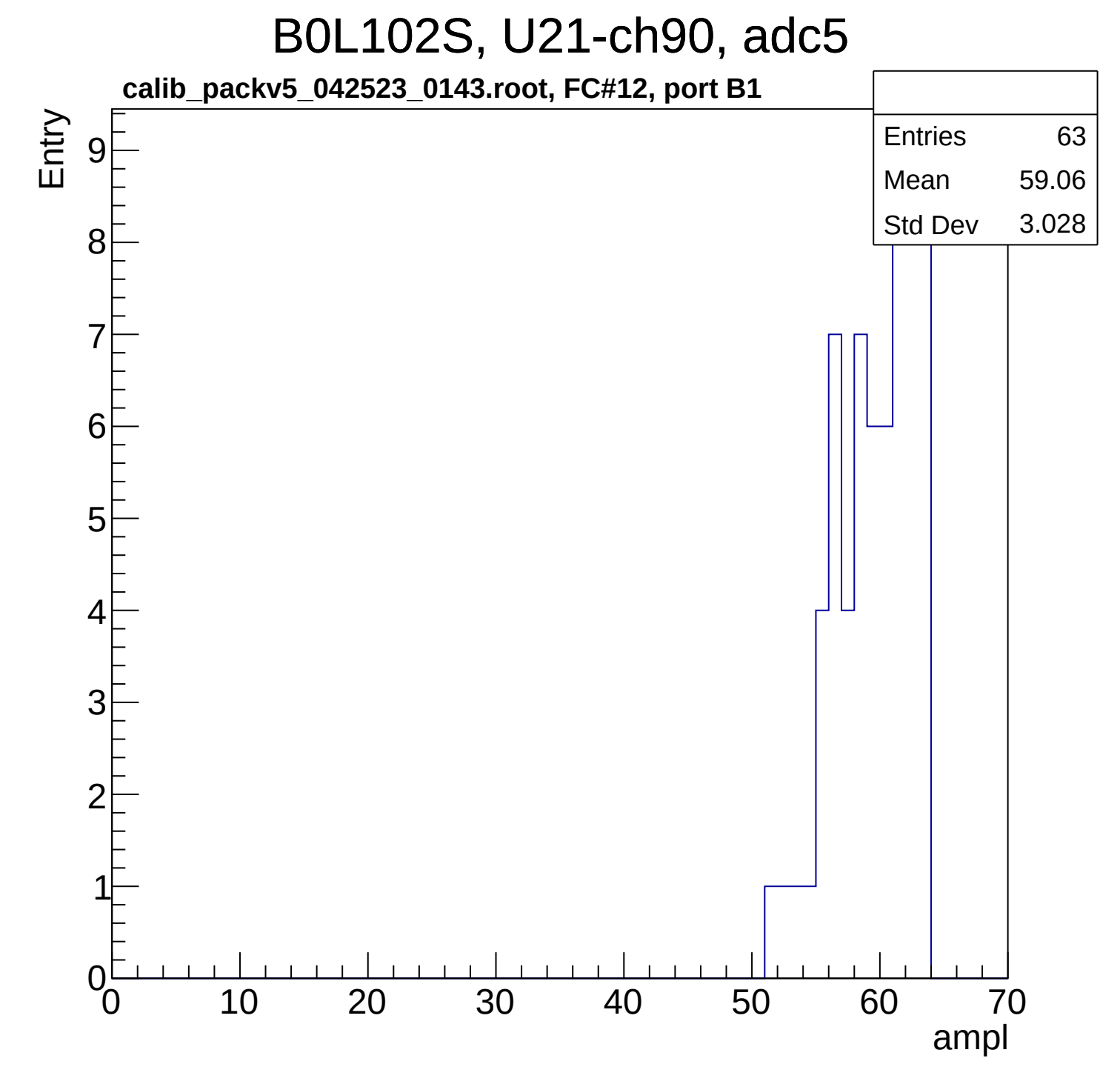

B0L102S, U21-ch90, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 28.86
Std Dev 3.762
Gaus mean : 29.8645
Gaus Width: 4.5993
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch90, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 35.94
Std Dev 4.151
Gaus mean : 36.9174
Gaus Width: 4.1981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch90, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 42.87
Std Dev 3.408
Gaus mean : 43.5907
Gaus Width: 2.9808
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch90, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 48.49
Std Dev 3.339
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch90, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 52.37
Std Dev 7.686
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch90, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 59.06
Std Dev 3.028
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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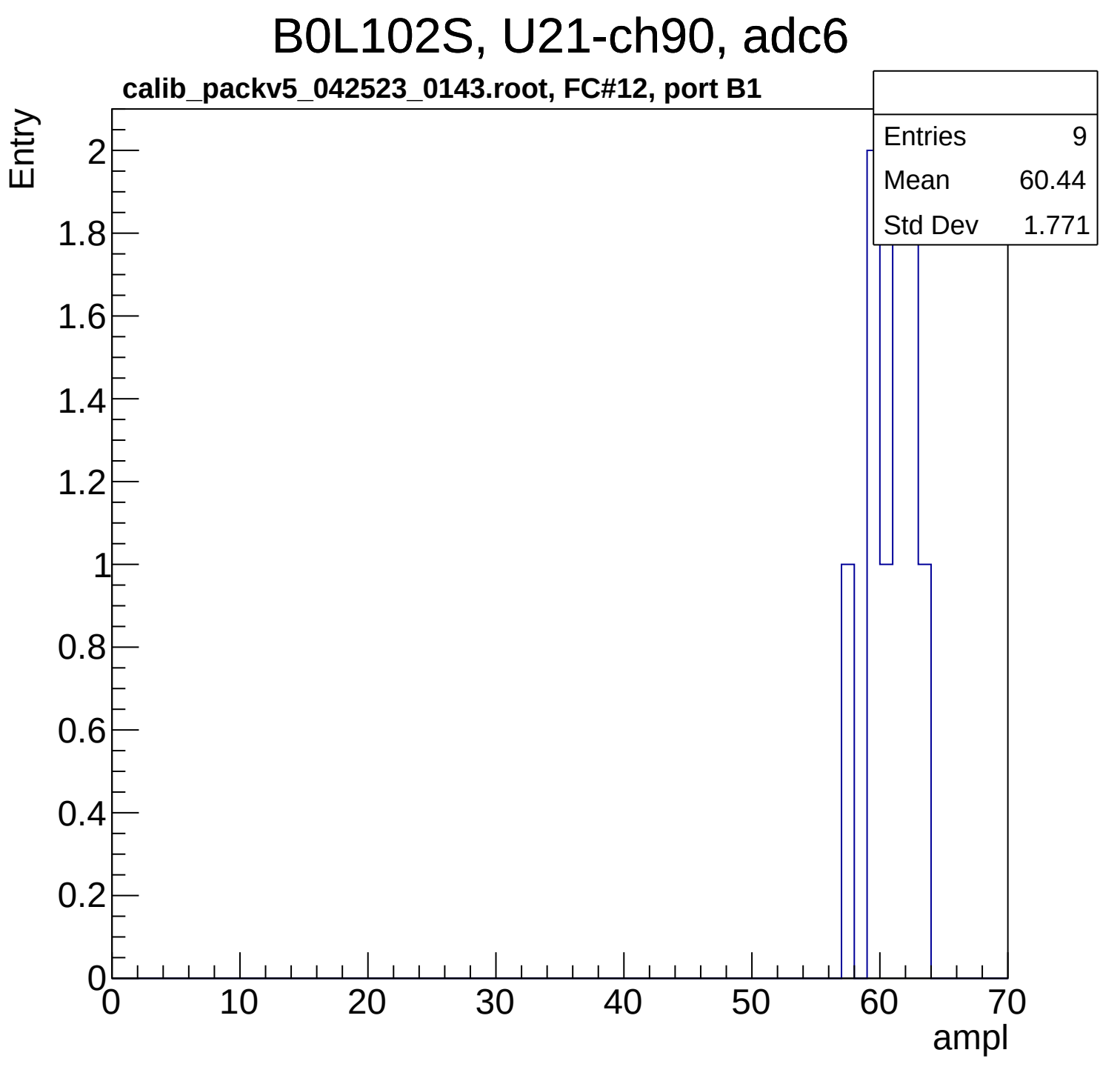
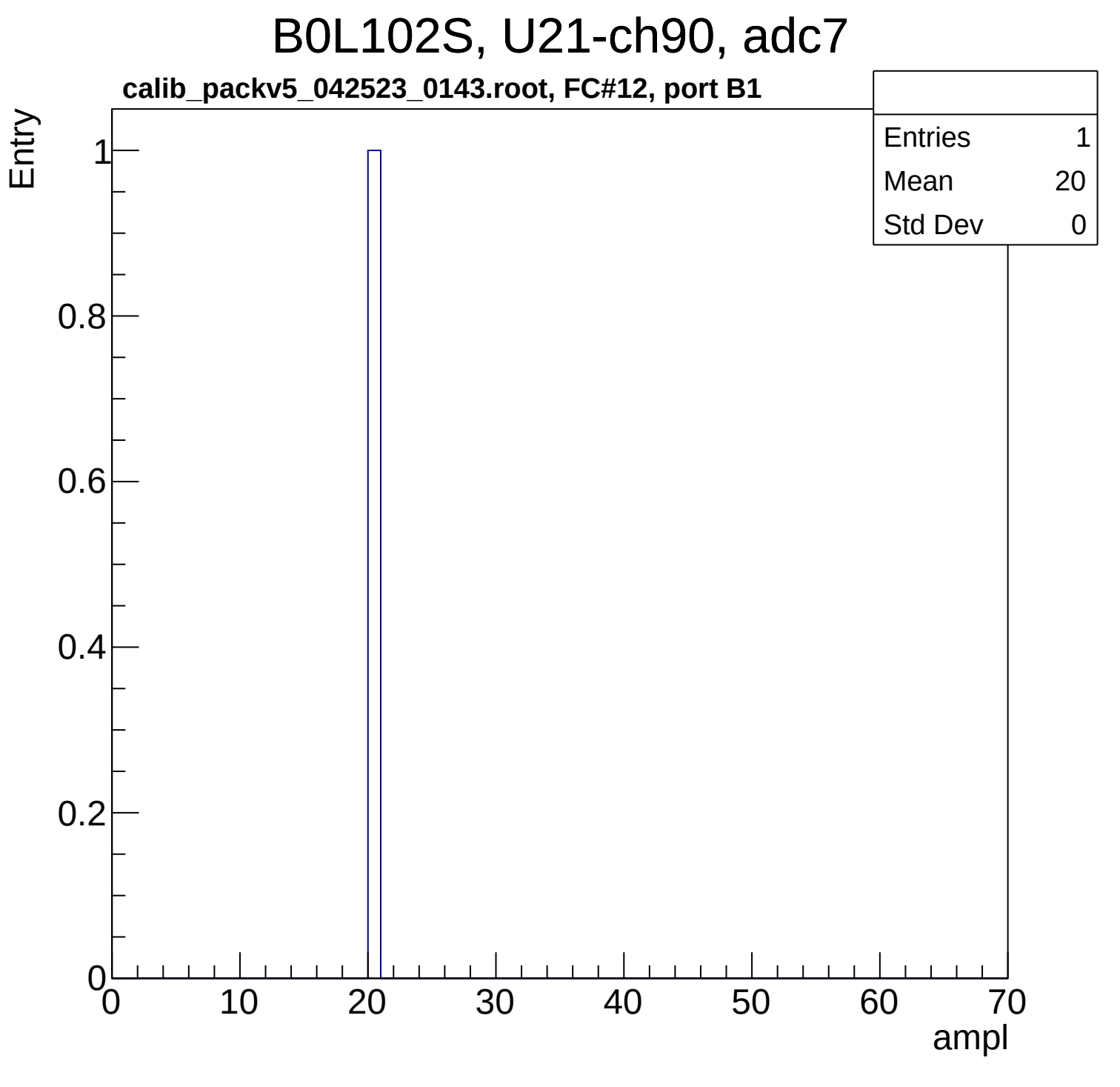
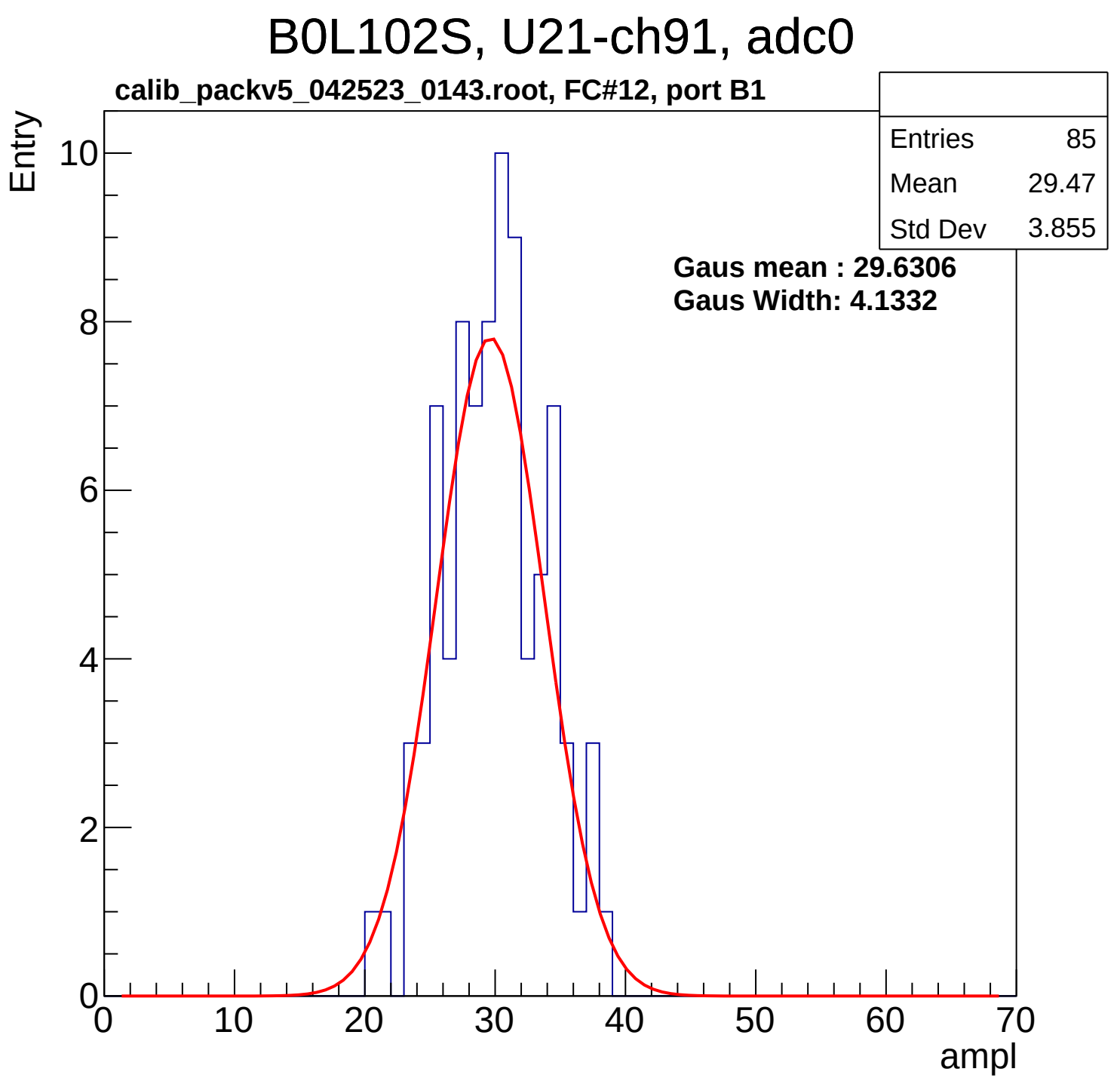
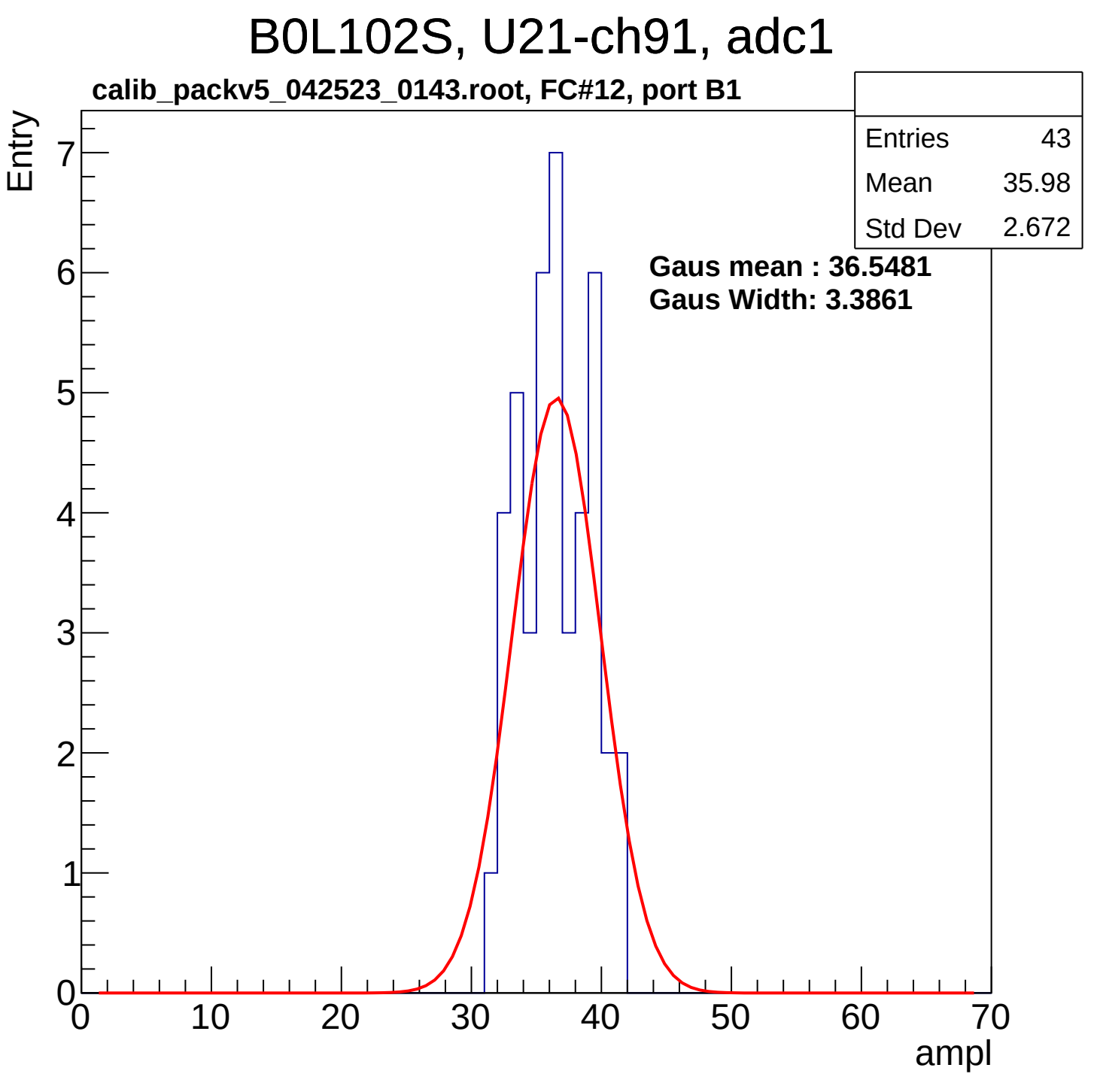

B0L102S, U21-ch90, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 60.44
Std Dev 1.771
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch90, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch91, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 29.47
Std Dev 3.855
Gaus mean : 29.6306
Gaus Width: 4.1332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch91, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 35.98
Std Dev 2.672
Gaus mean : 36.5481
Gaus Width: 3.3861
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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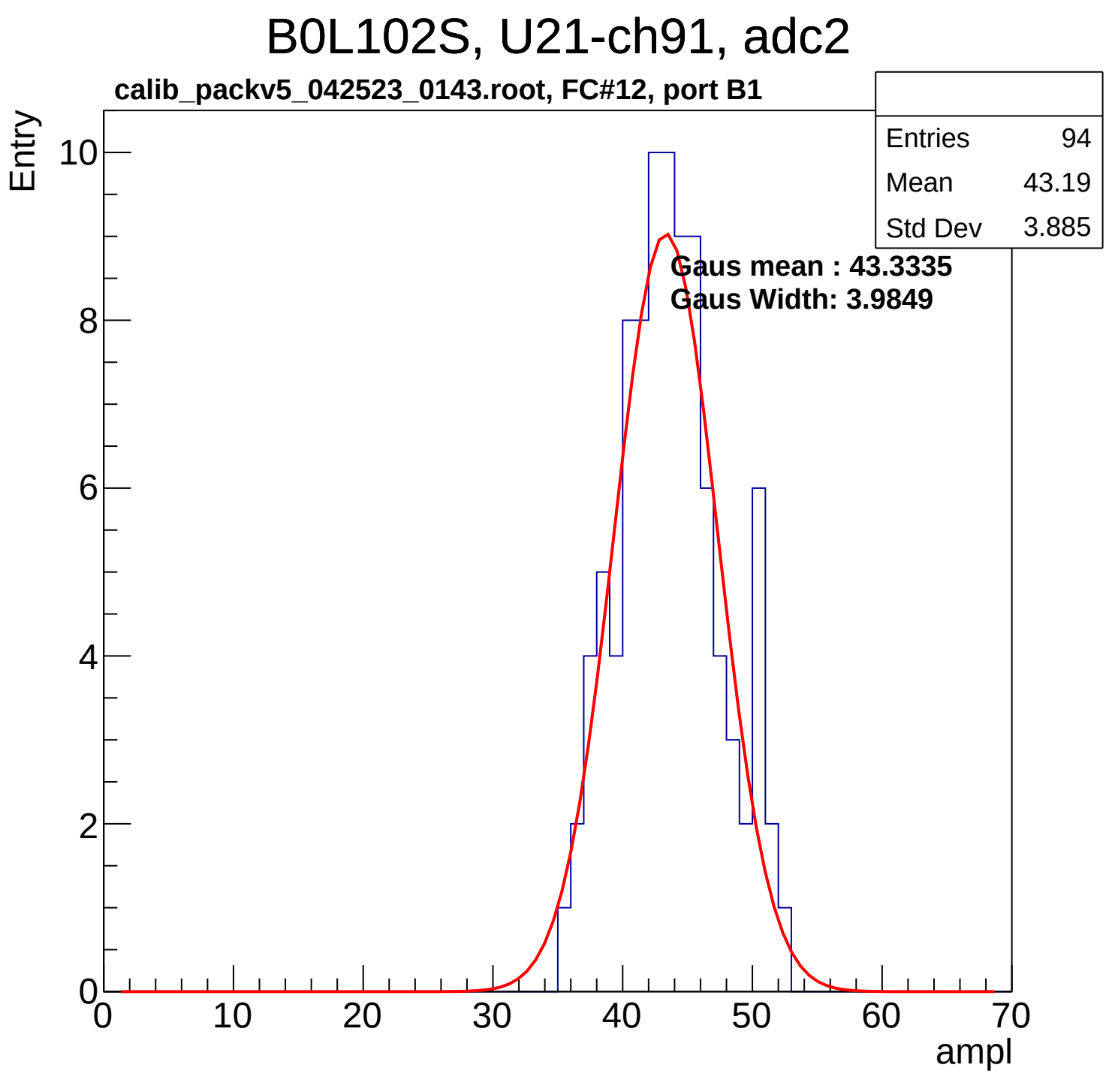
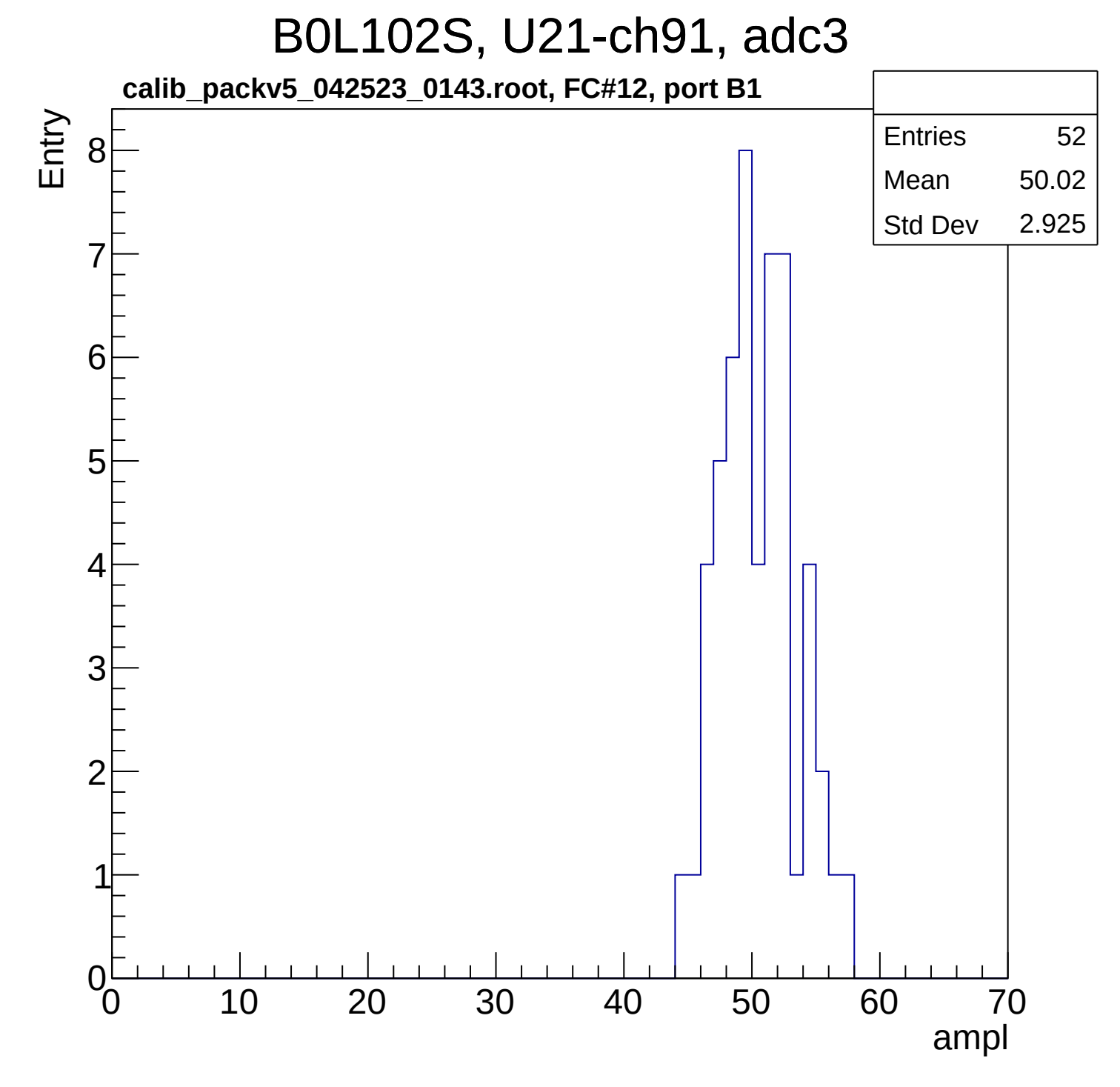

B0L102S, U21-ch91, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 94
Mean 43.19
Std Dev 3.885
Gaus mean : 43.3335
Gaus Width: 3.9849
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch91, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 50.02
Std Dev 2.925
Entry
ampl
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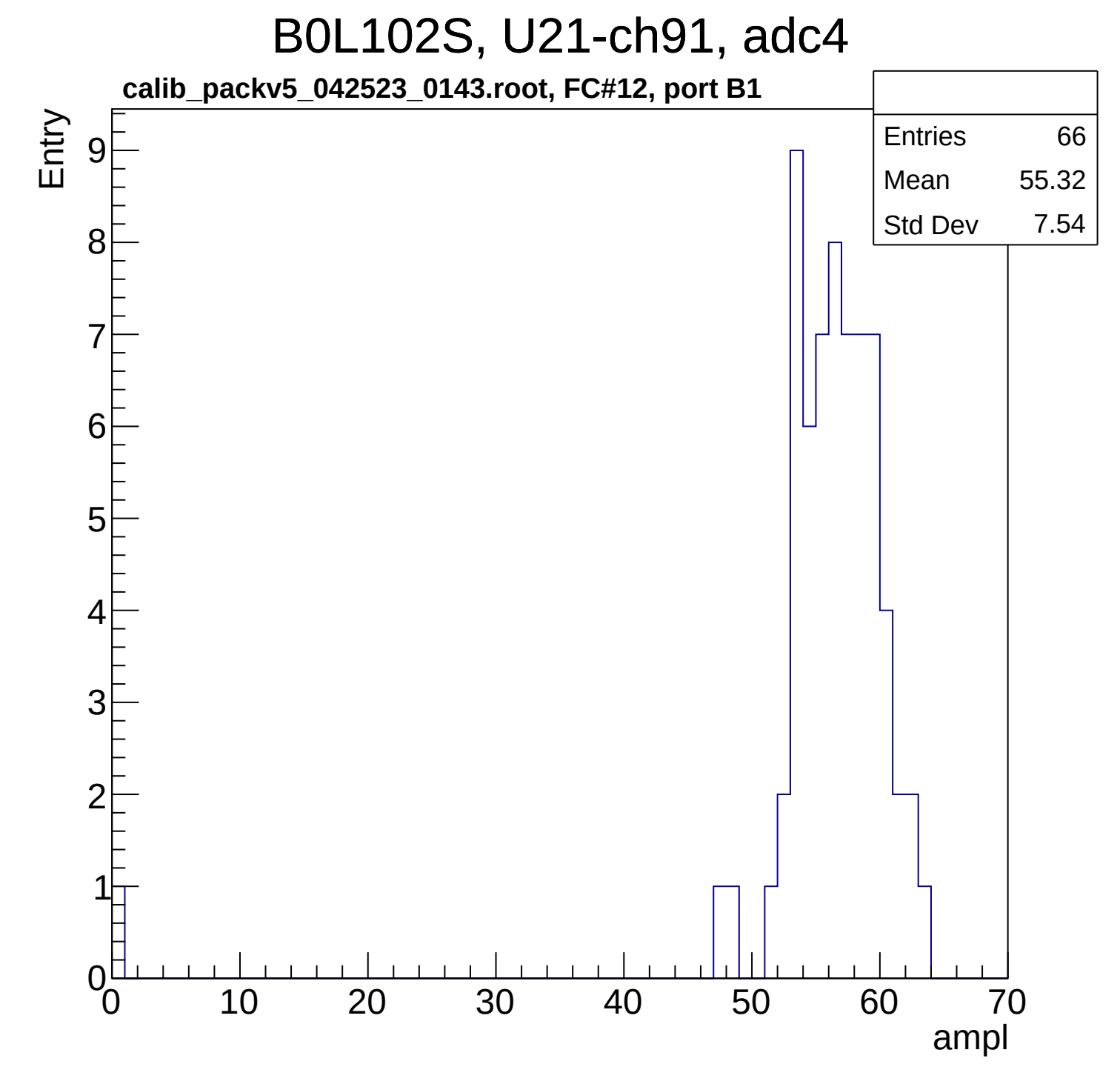
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch91, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 55.32
Std Dev 7.54
Entry
0
1
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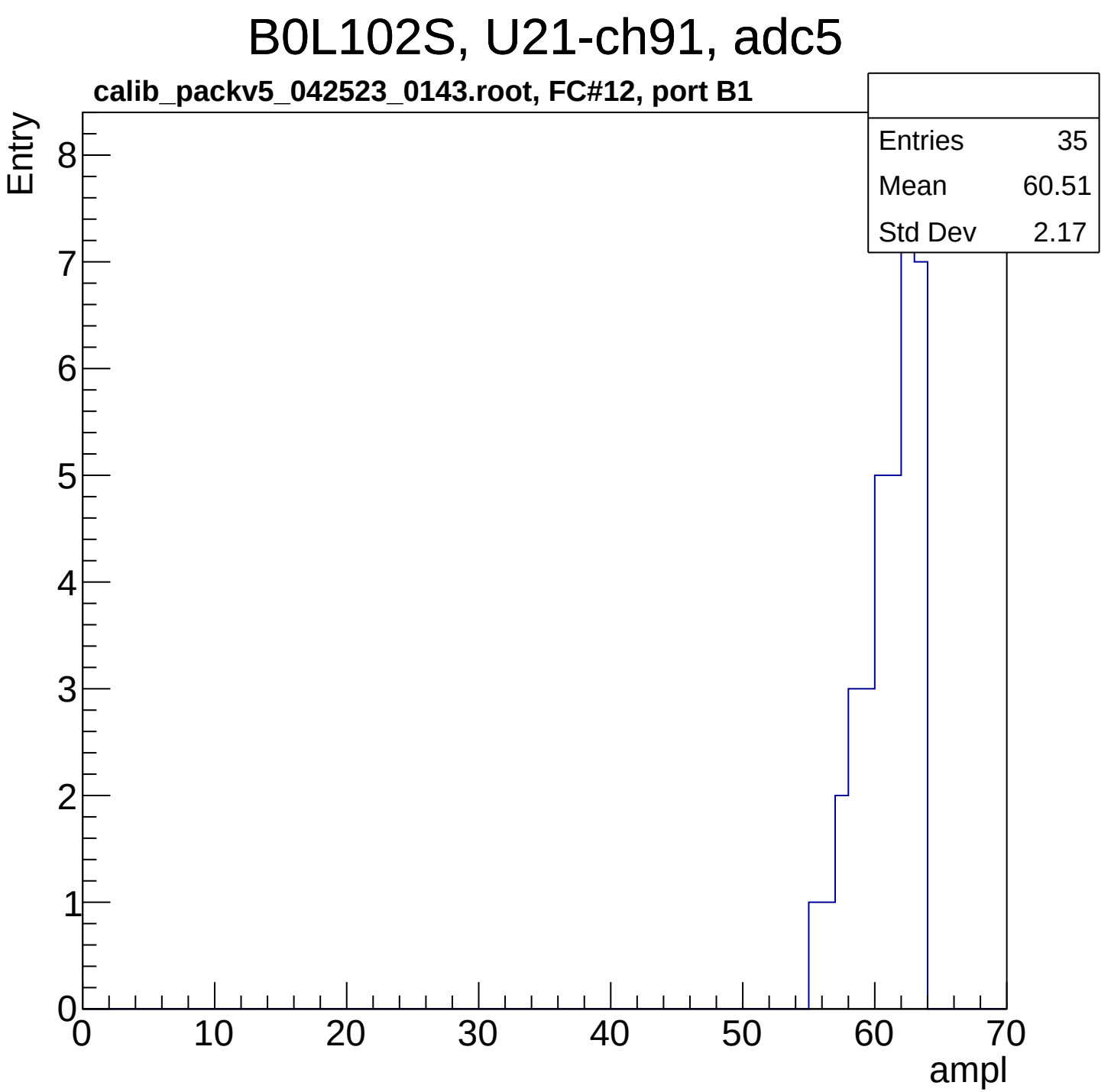
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch91, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 60.51
Std Dev 2.17
Entry
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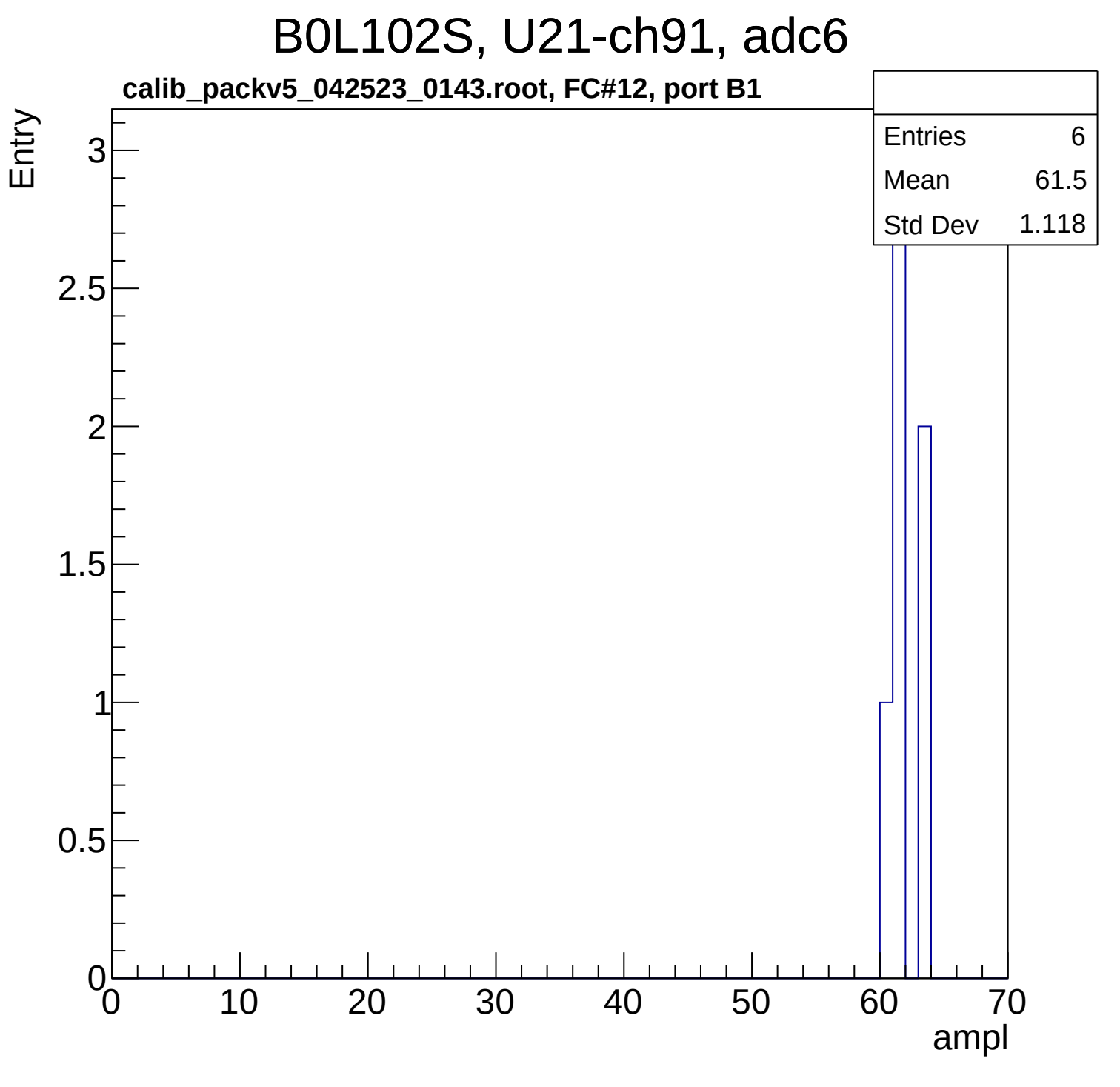
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch91, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.5
Std Dev 1.118
Entry
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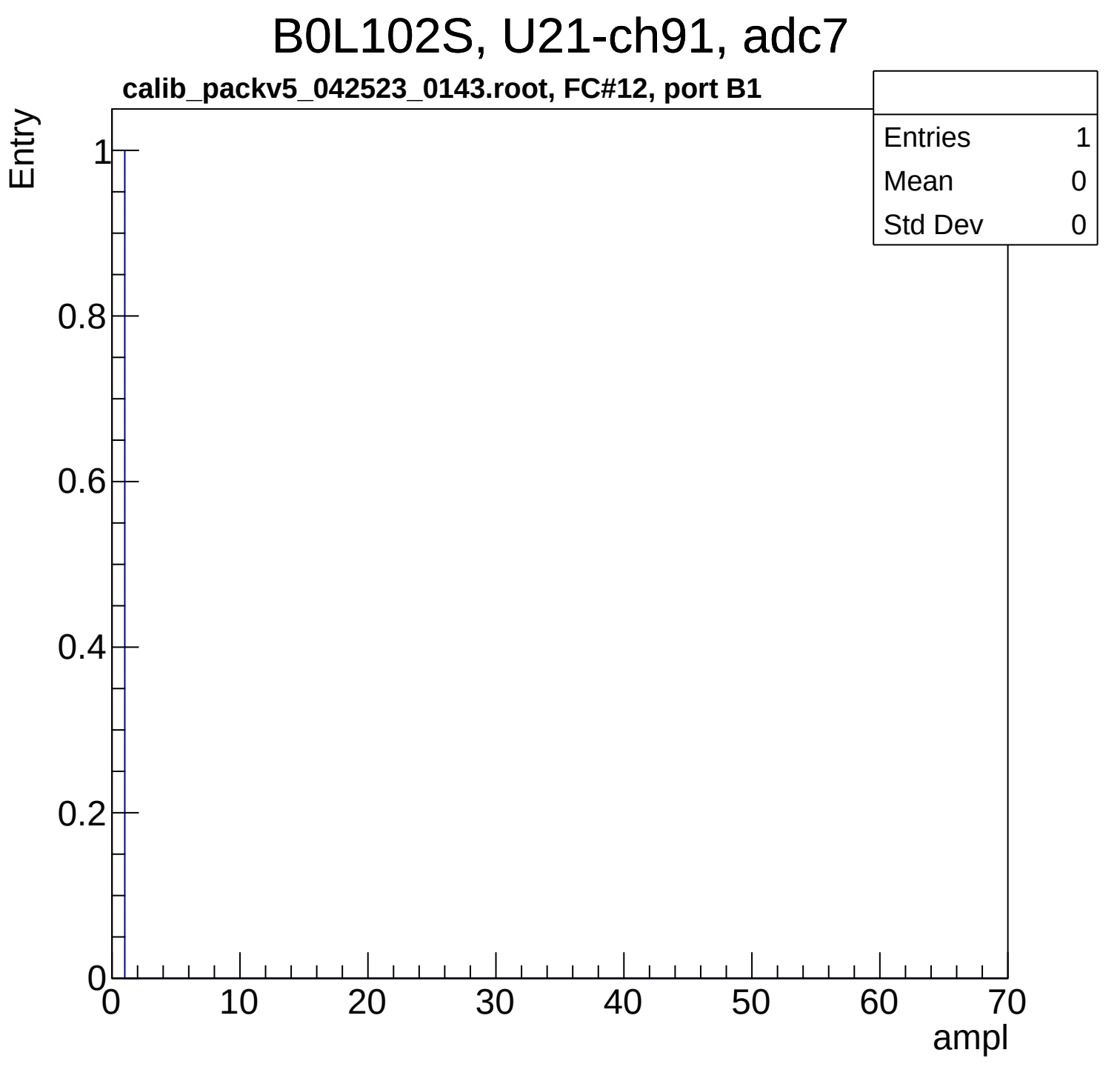
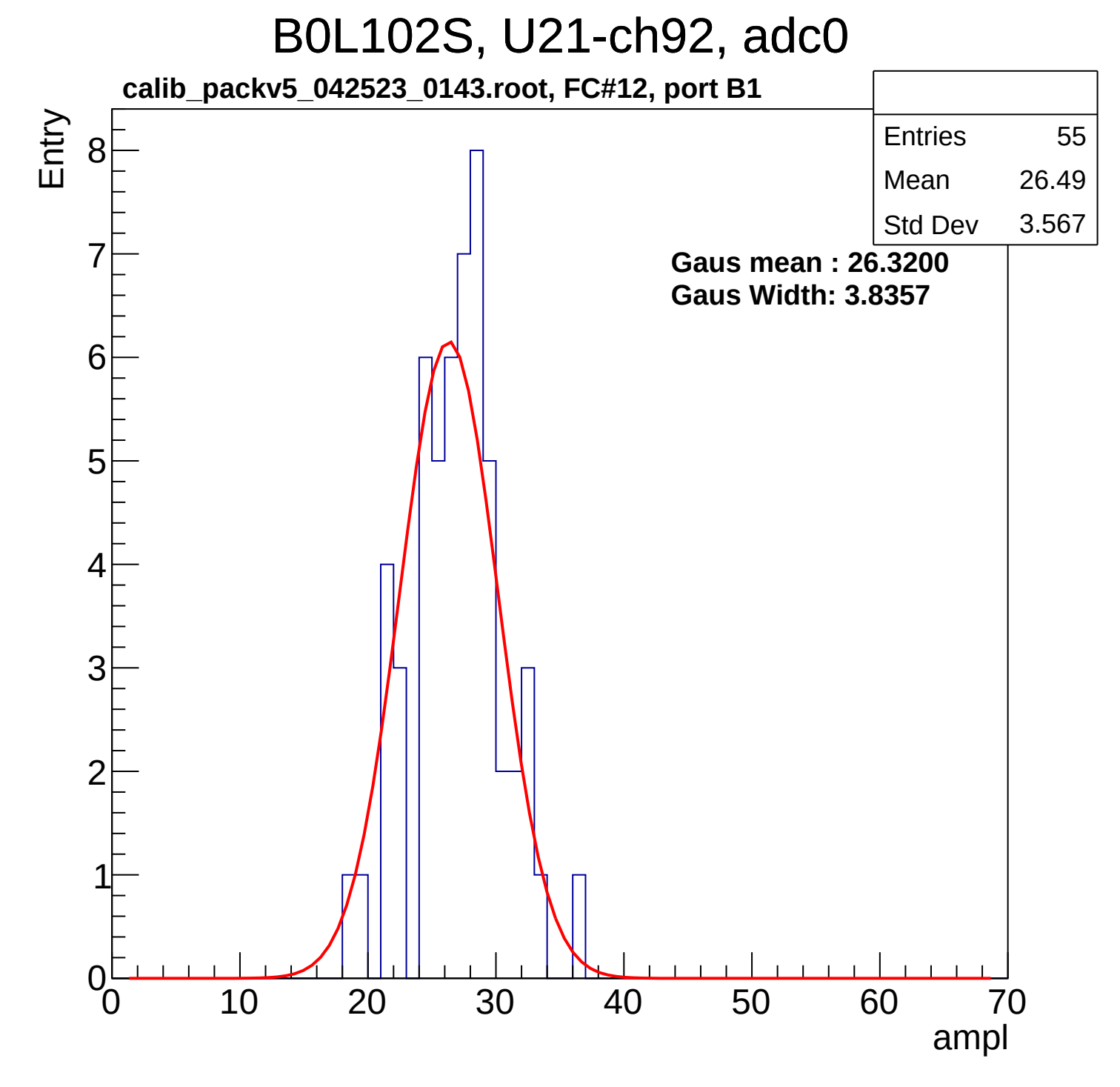
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch91, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch92, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 26.49
Std Dev 3.567
Gaus mean : 26.3200
Gaus Width: 3.8357
Entry
ampl
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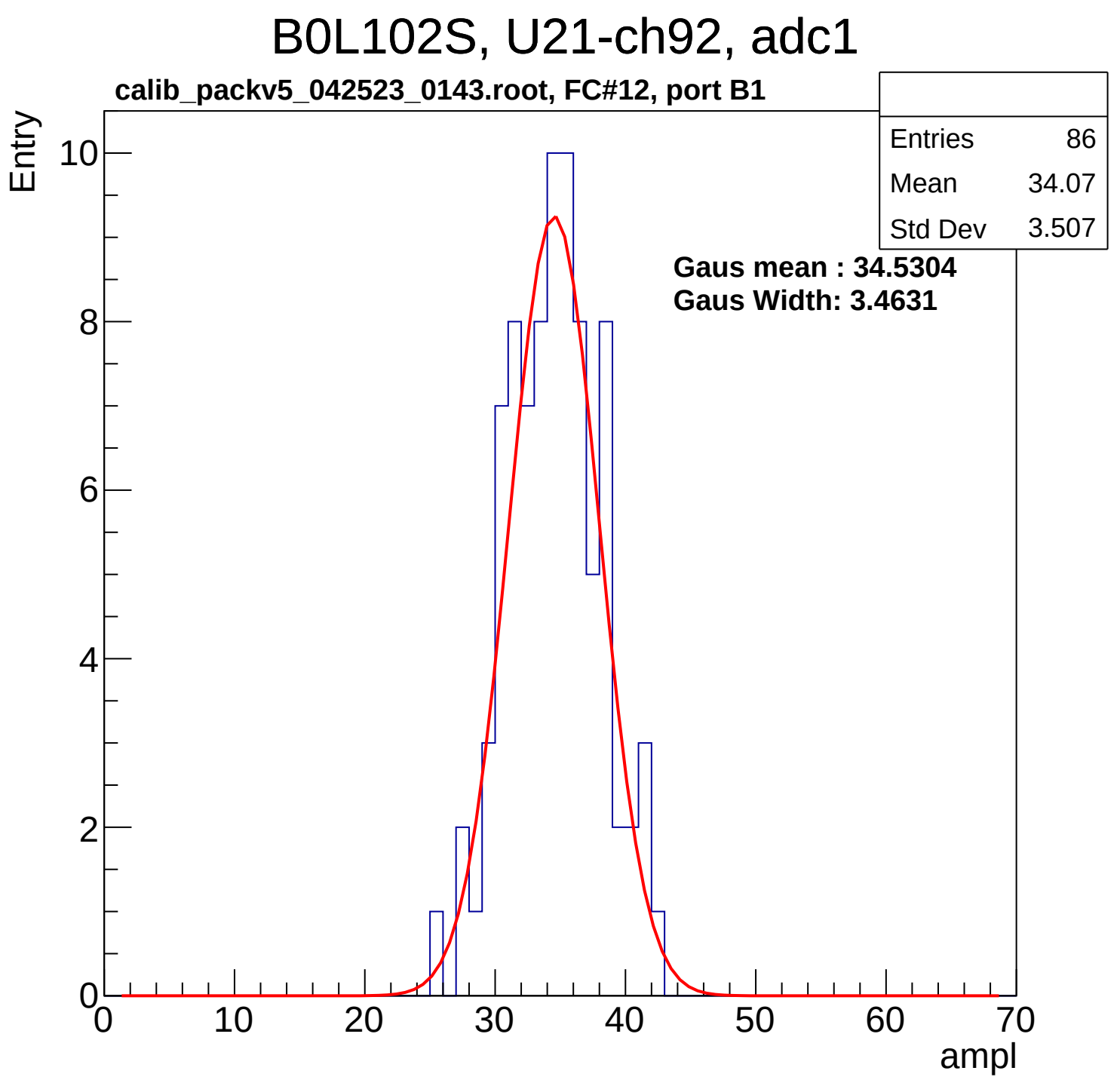
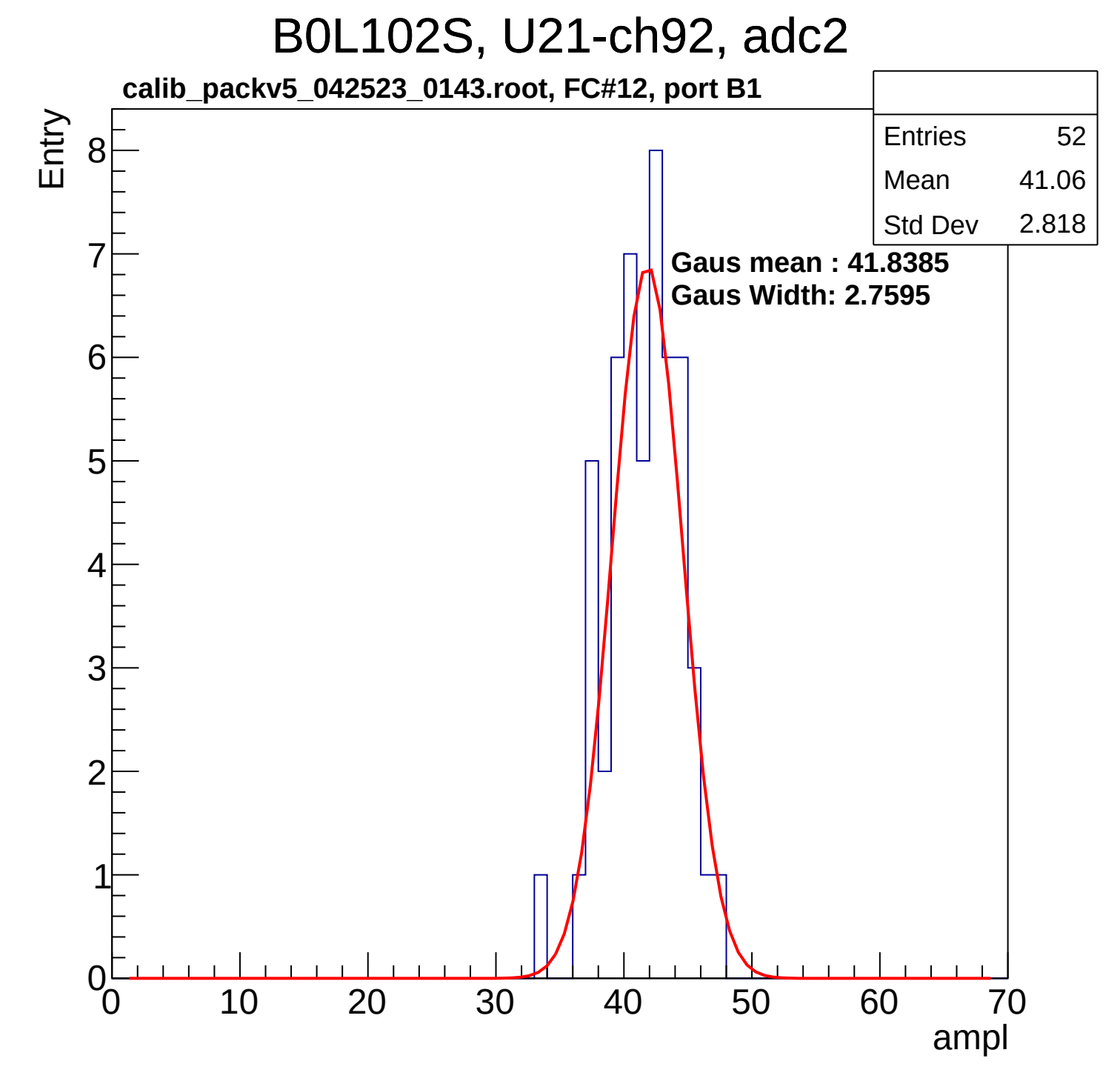
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch92, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 34.07
Std Dev 3.507
Gaus mean : 34.5304
Gaus Width: 3.4631
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch92, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 41.06
Std Dev 2.818
Gaus mean : 41.8385
Gaus Width: 2.7595
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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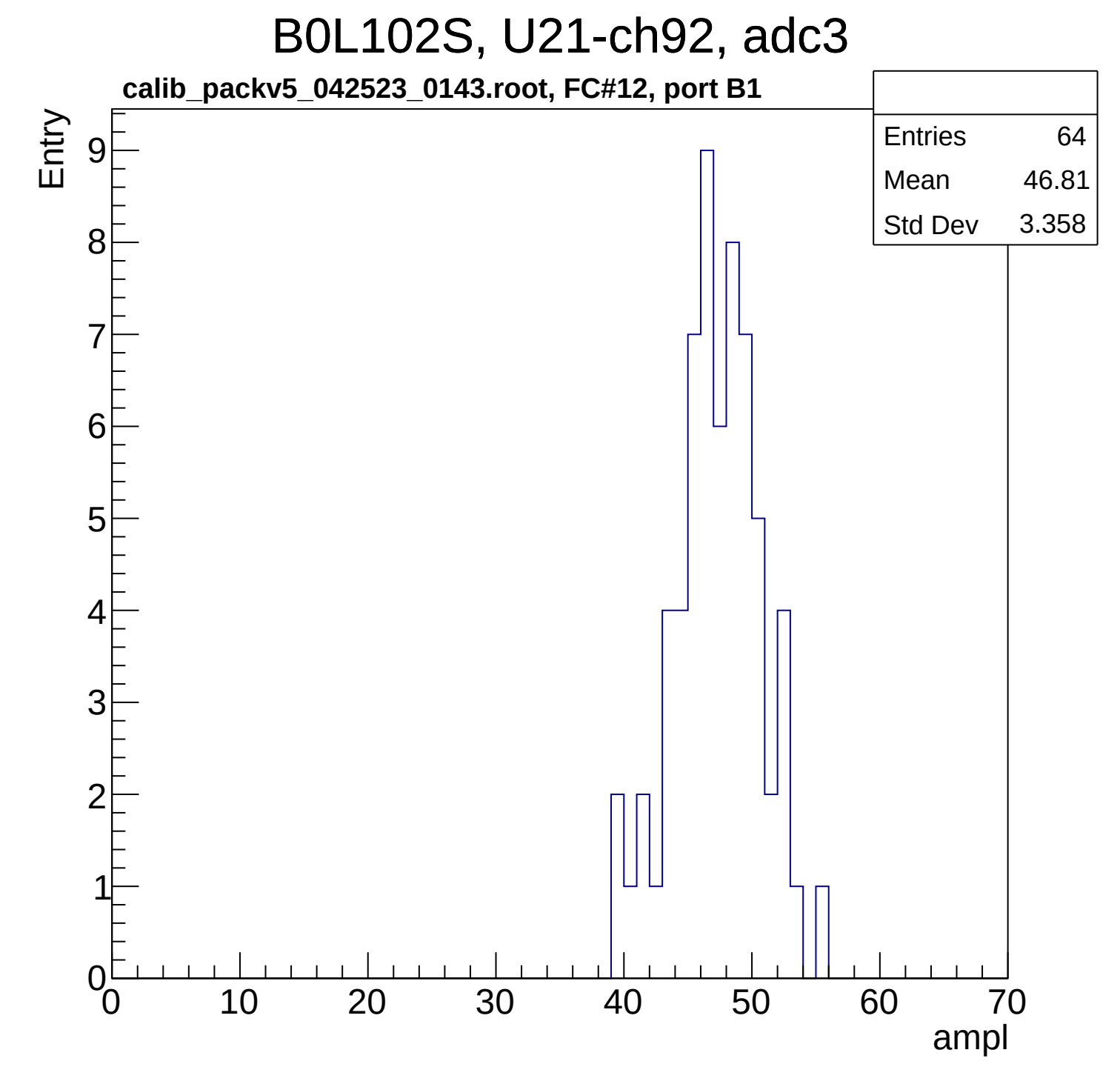
B0L102S, U21-ch92, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 46.81
Std Dev 3.358
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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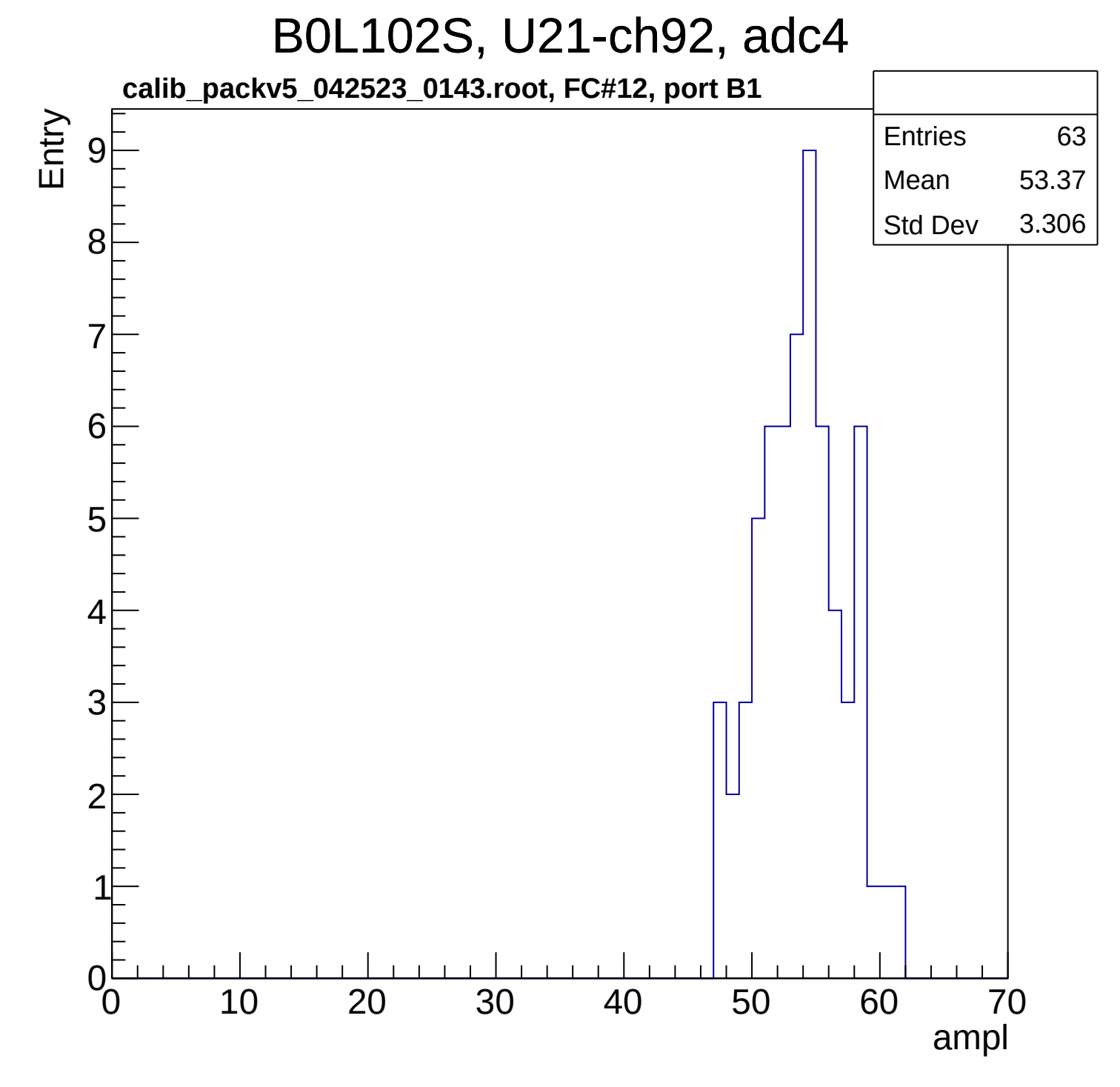
B0L102S, U21-ch92, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 53.37
Std Dev 3.306
Entry
ampl
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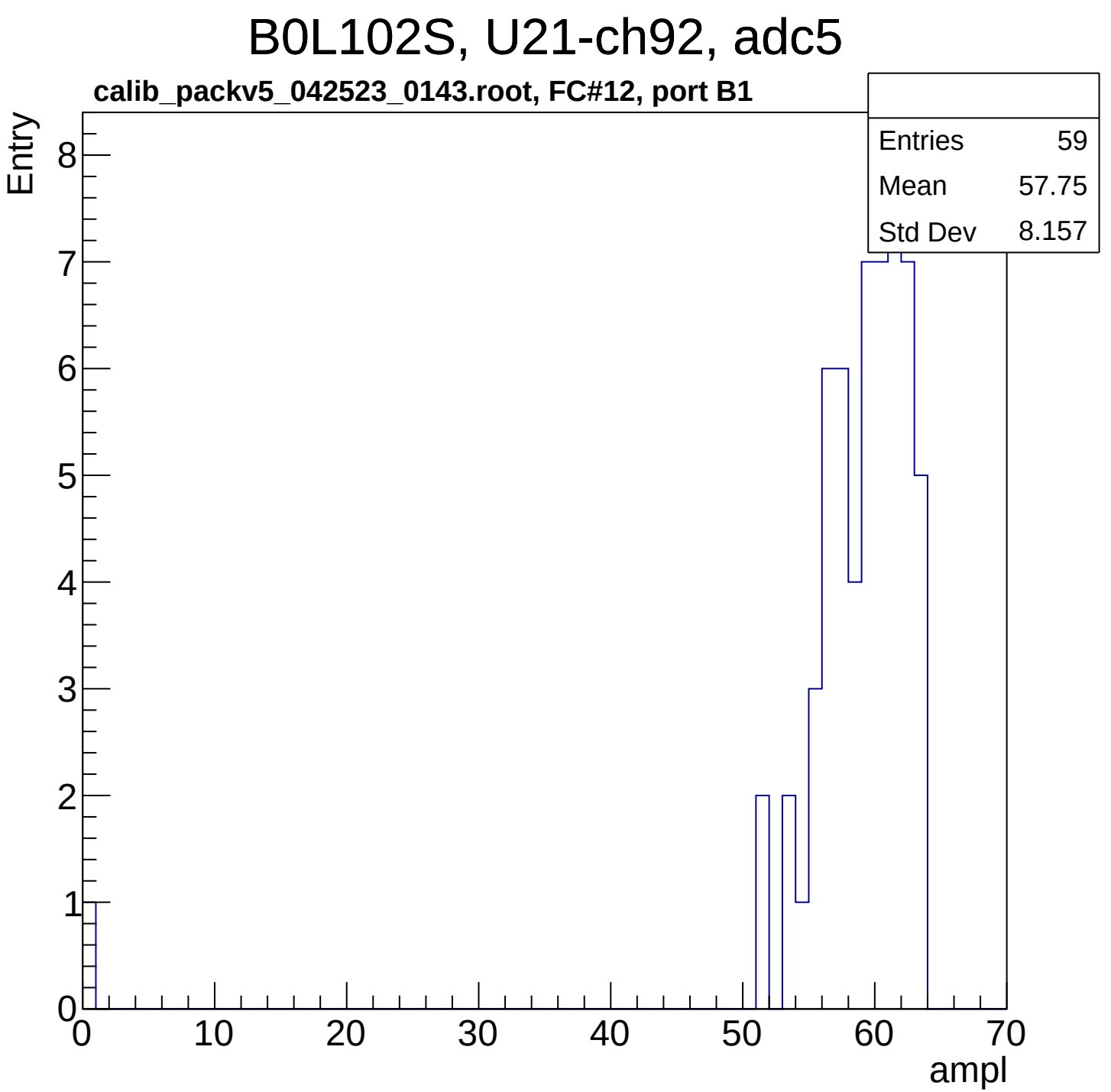
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch92, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 57.75
Std Dev 8.157
Entry
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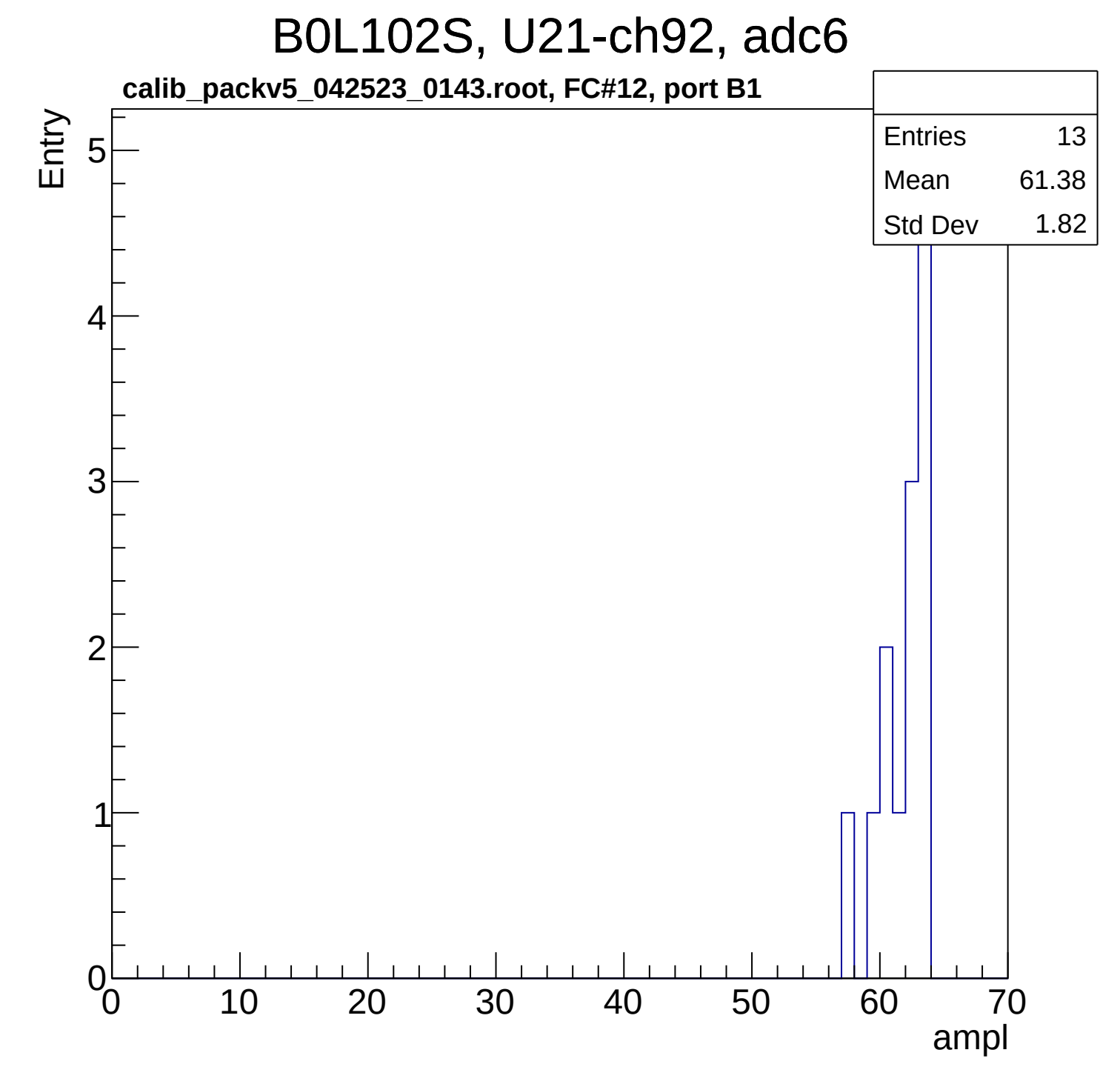
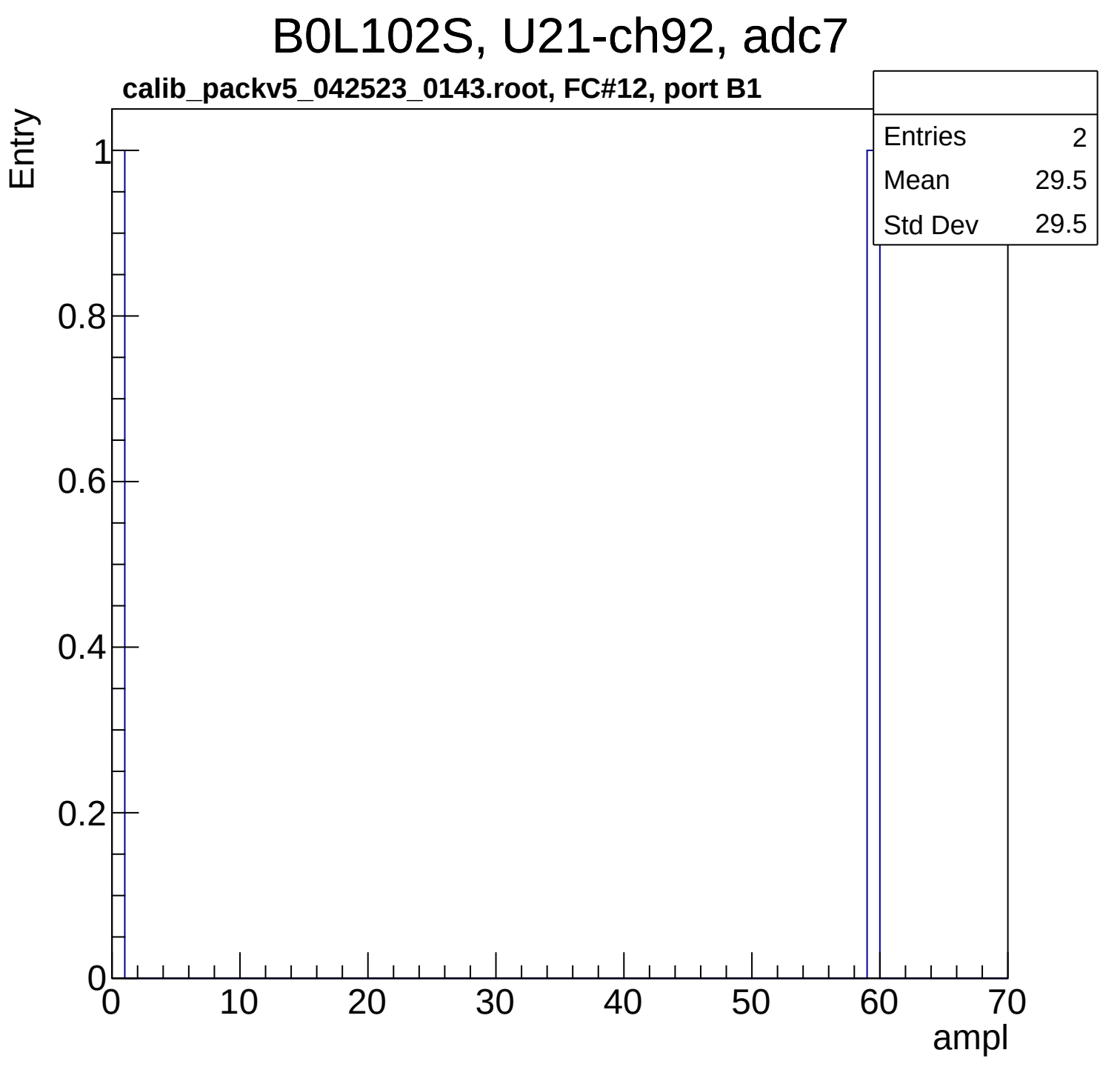
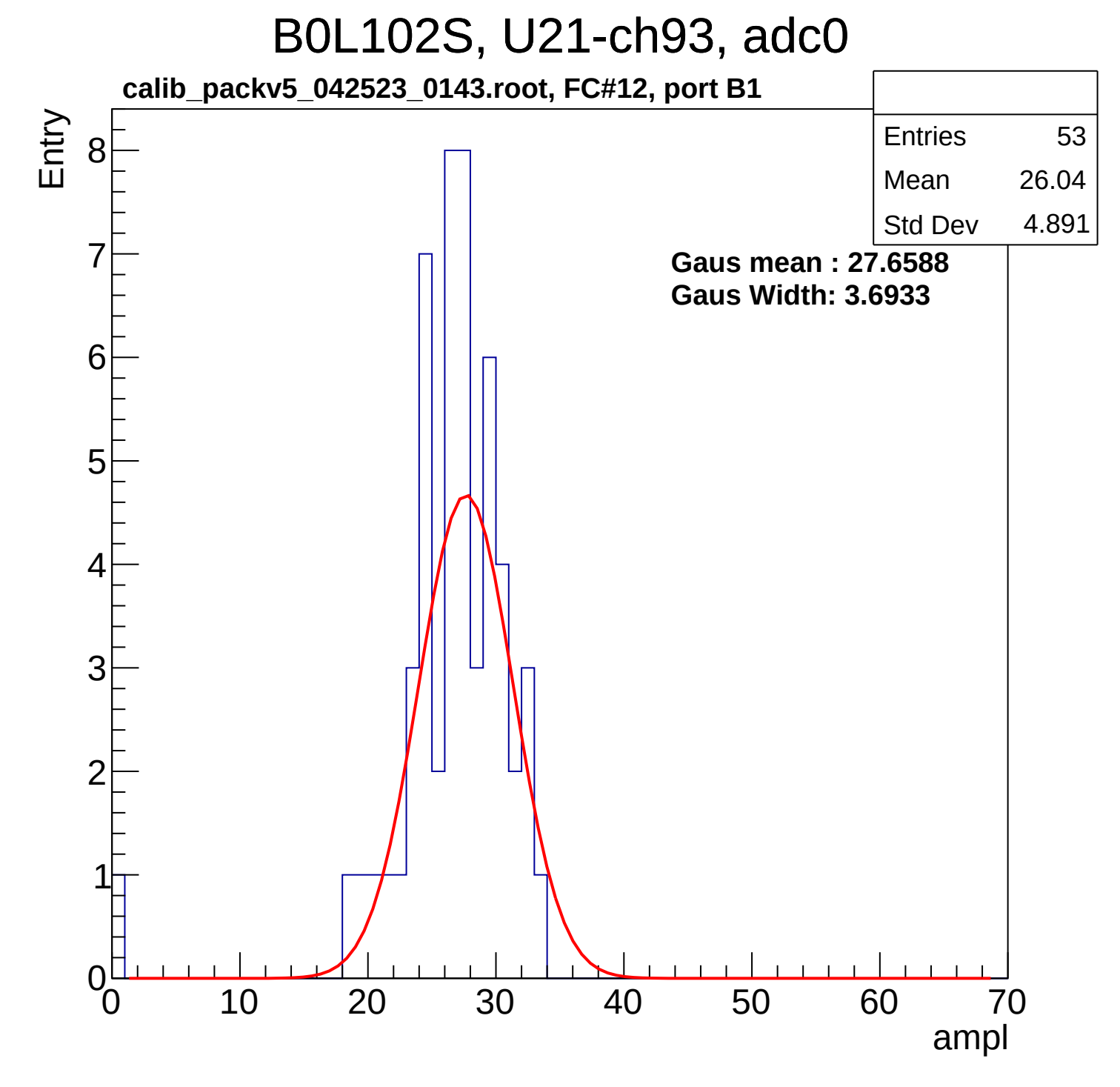
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch92, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 61.38
Std Dev 1.82
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U21-ch92, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 29.5
Std Dev 29.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch93, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 26.04
Std Dev 4.891
Gaus mean : 27.6588
Gaus Width: 3.6933
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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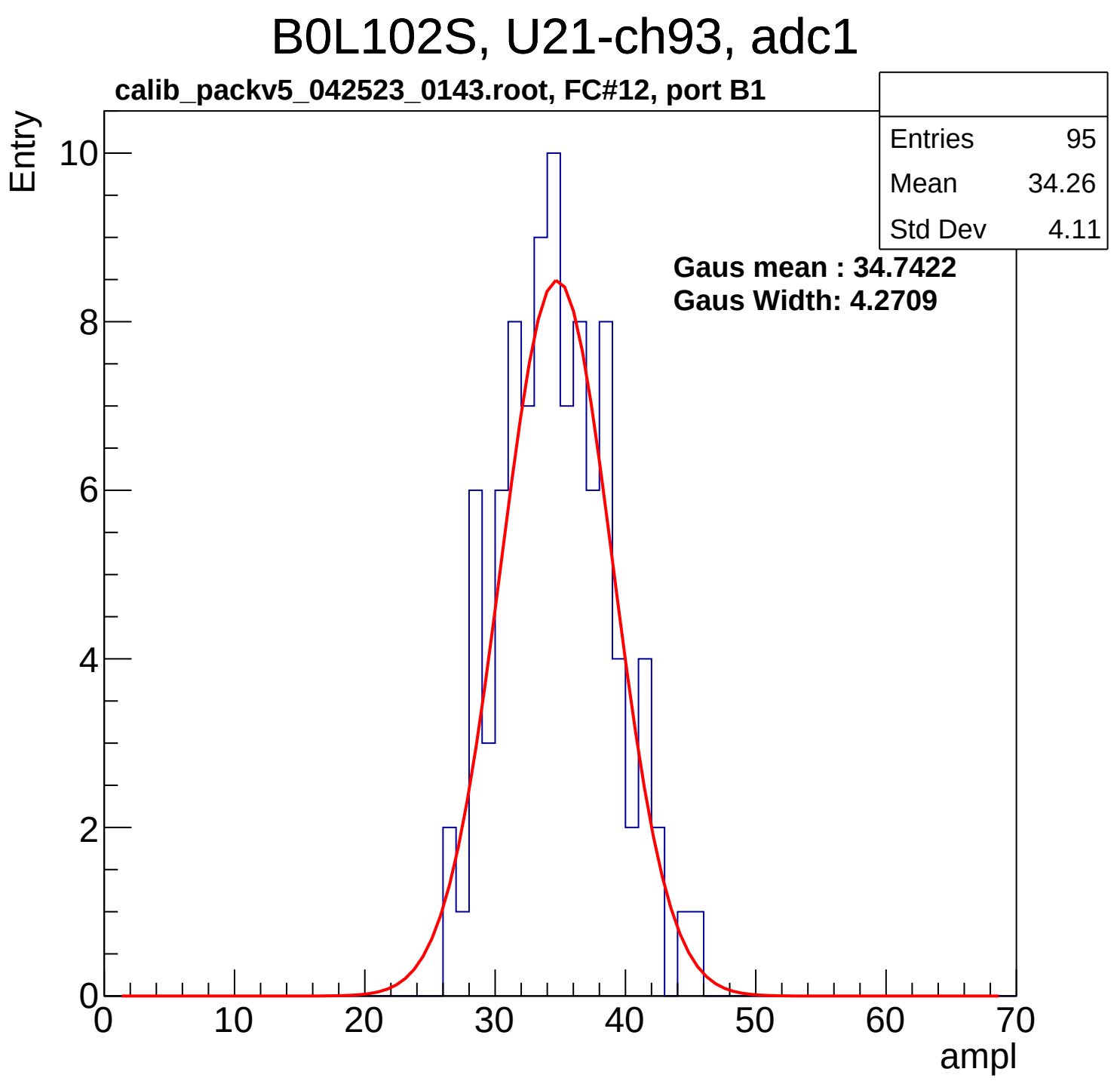
B0L102S, U21-ch93, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 95
Mean 34.26
Std Dev 4.11
Gaus mean : 34.7422
Gaus Width: 4.2709
Entry
ampl
0
2
4
6
8
10
0
10
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30
40
50
60
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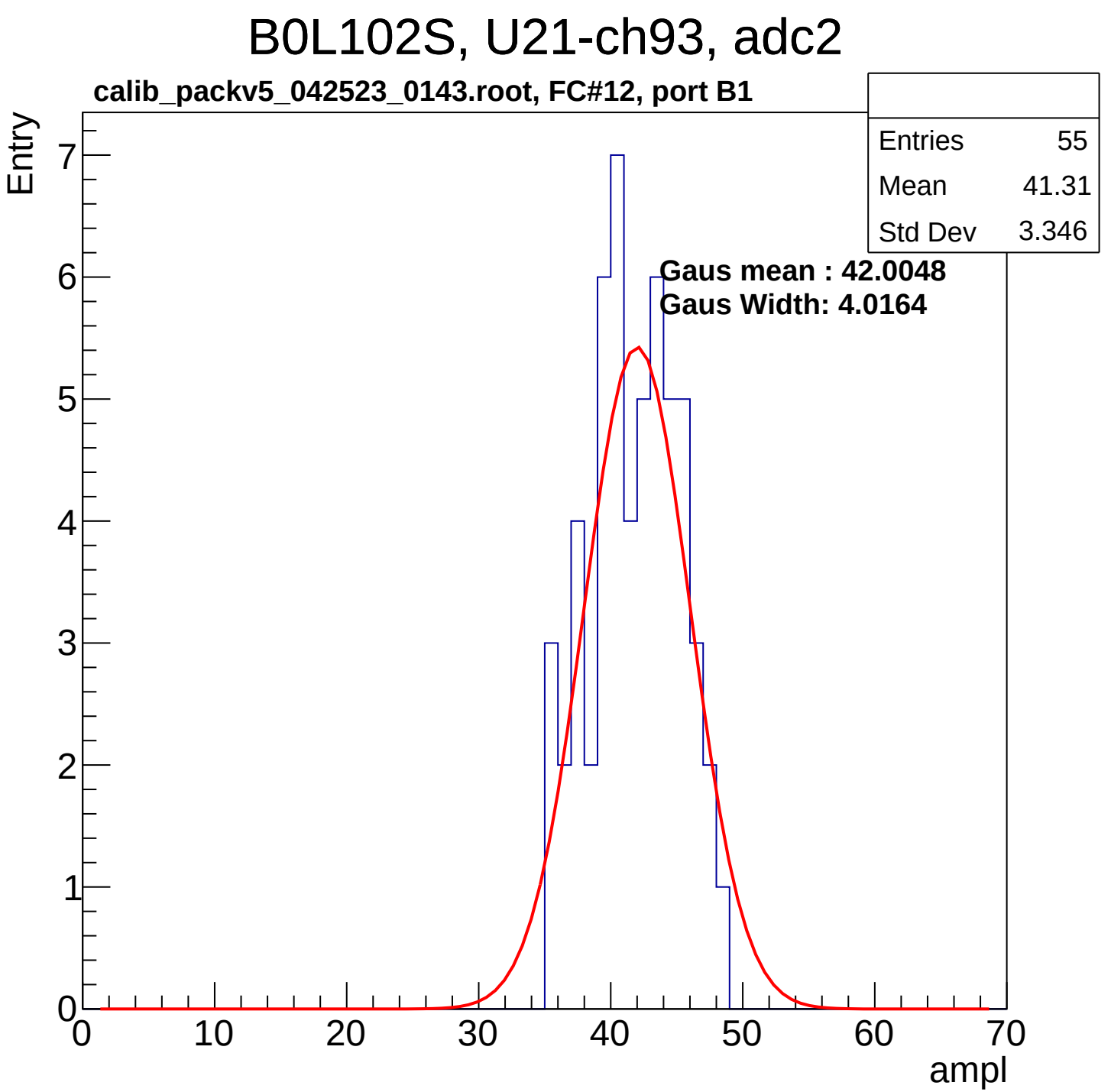
B0L102S, U21-ch93, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 41.31
Std Dev 3.346
Gaus mean : 42.0048
Gaus Width: 4.0164
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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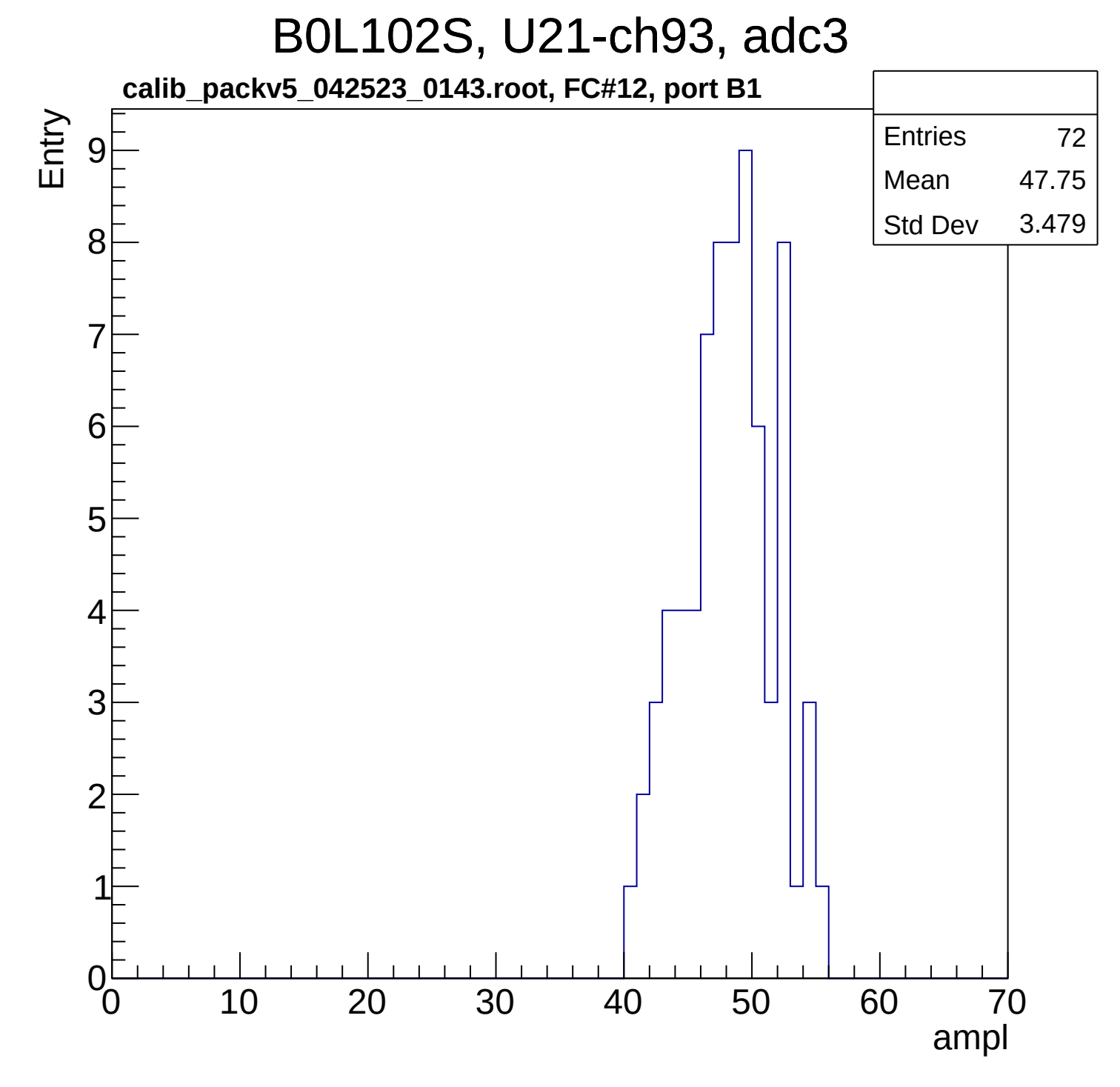
B0L102S, U21-ch93, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 47.75
Std Dev 3.479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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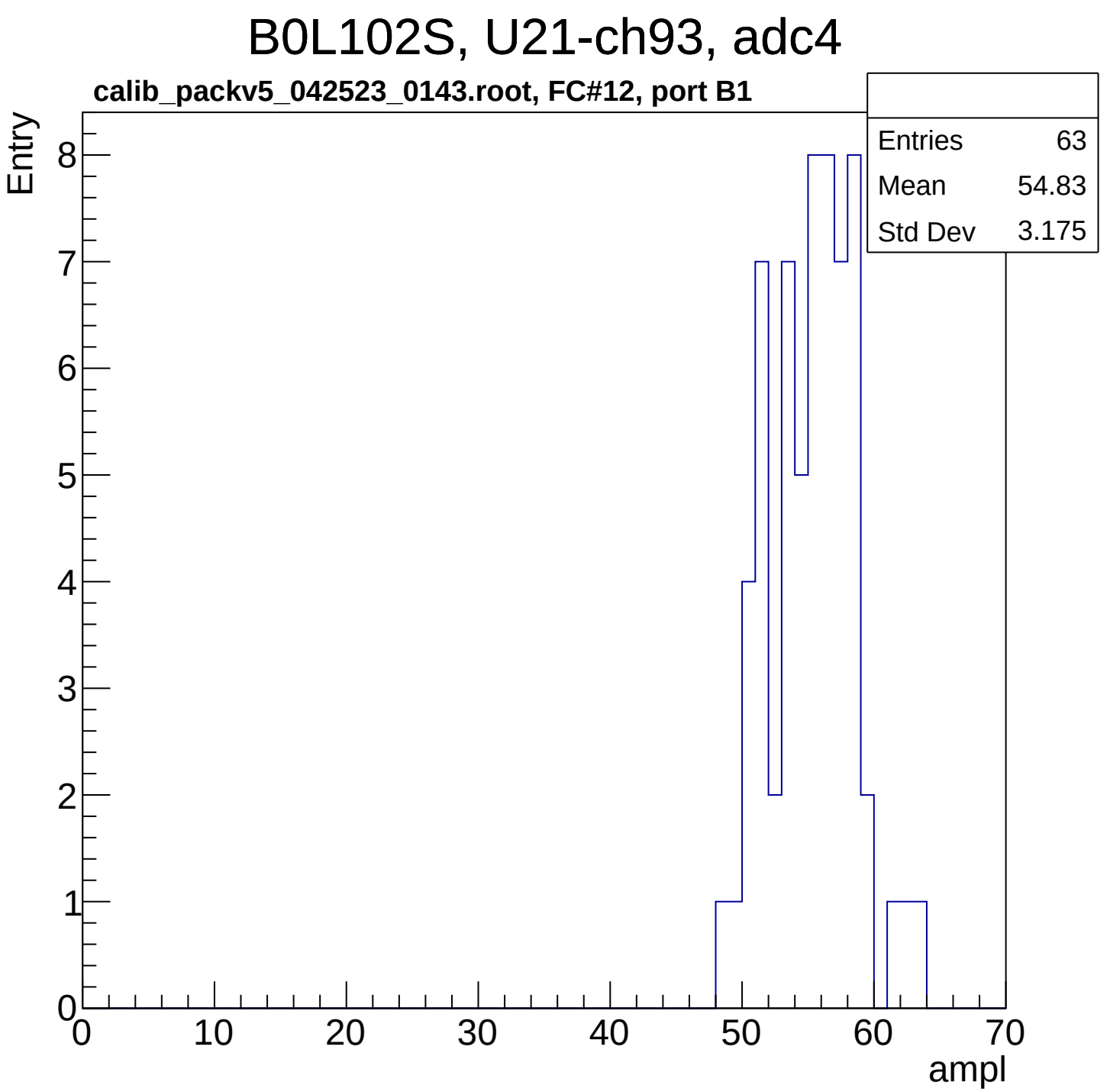
B0L102S, U21-ch93, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54.83
Std Dev 3.175
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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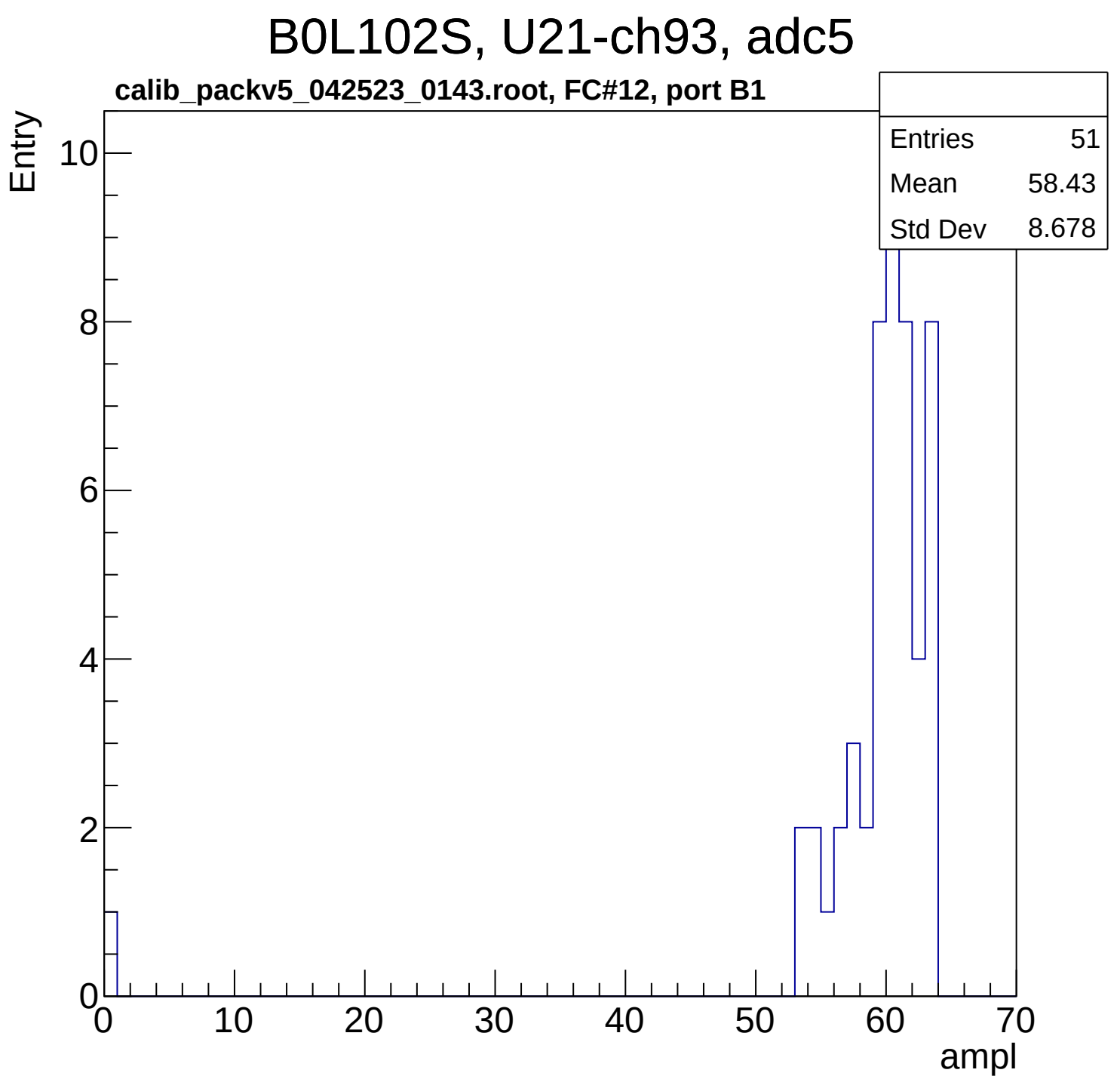
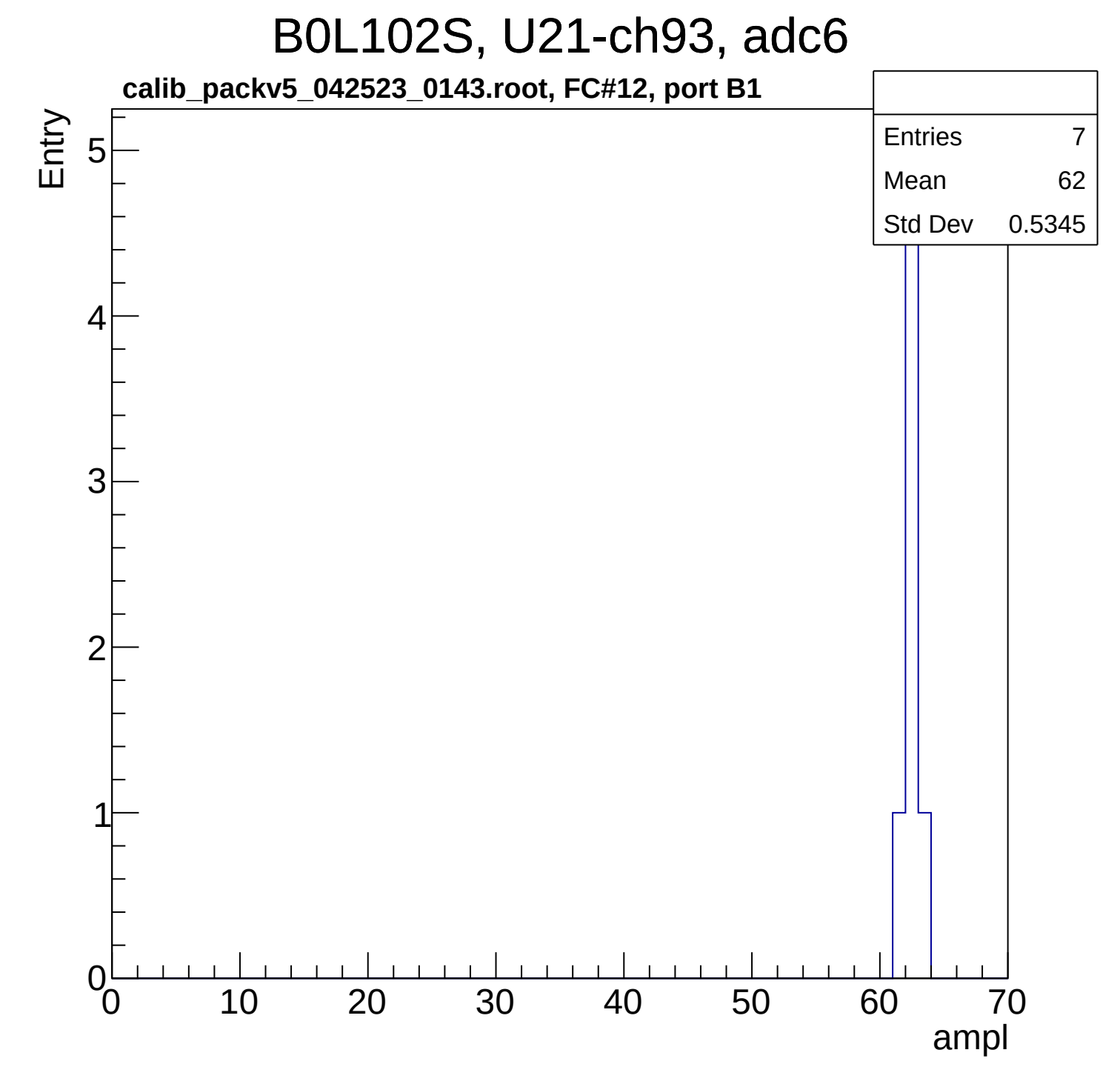
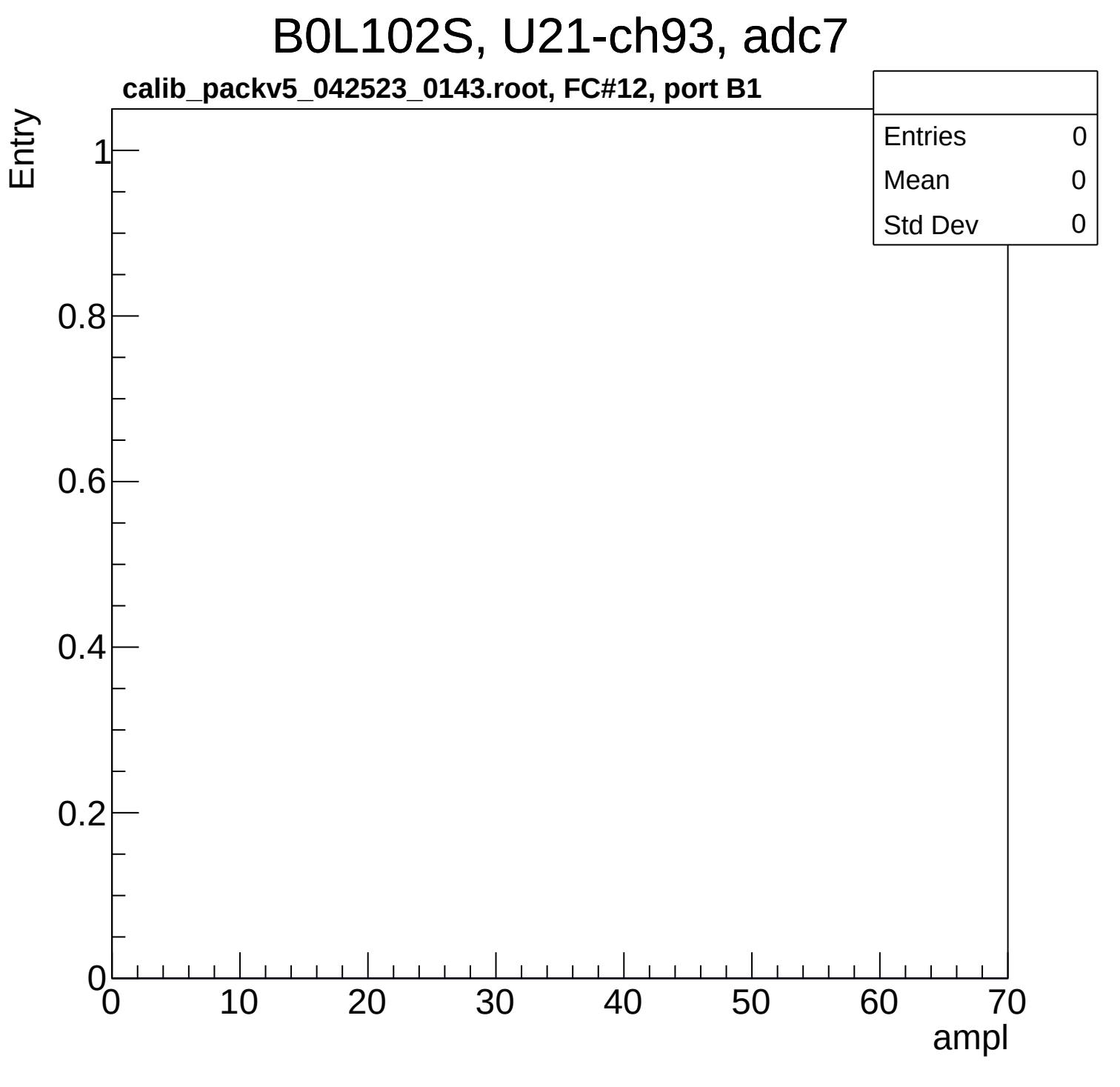

B0L102S, U21-ch93, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 58.43
Std Dev 8.678
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch93, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62
Std Dev 0.5345
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch93, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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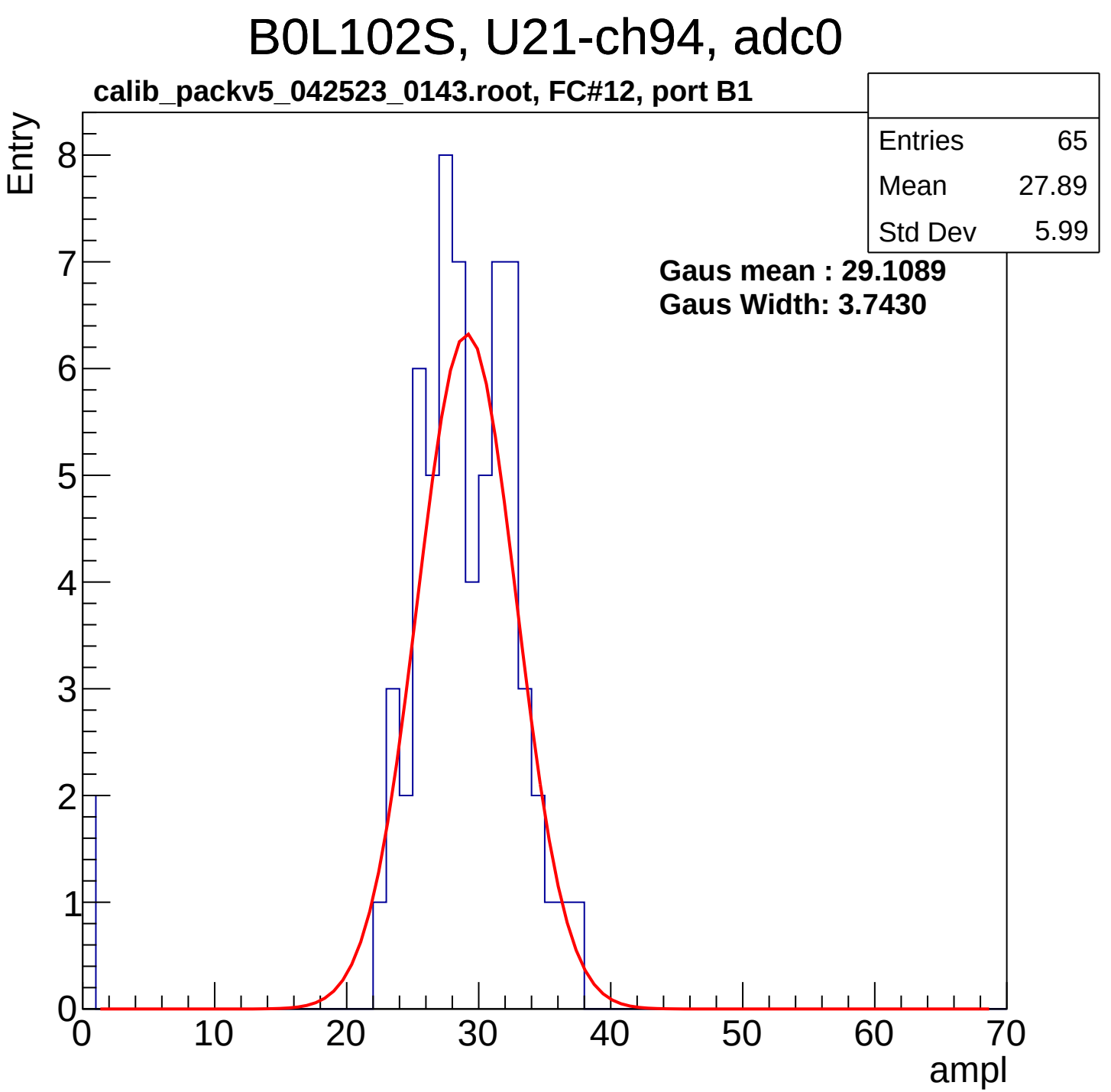
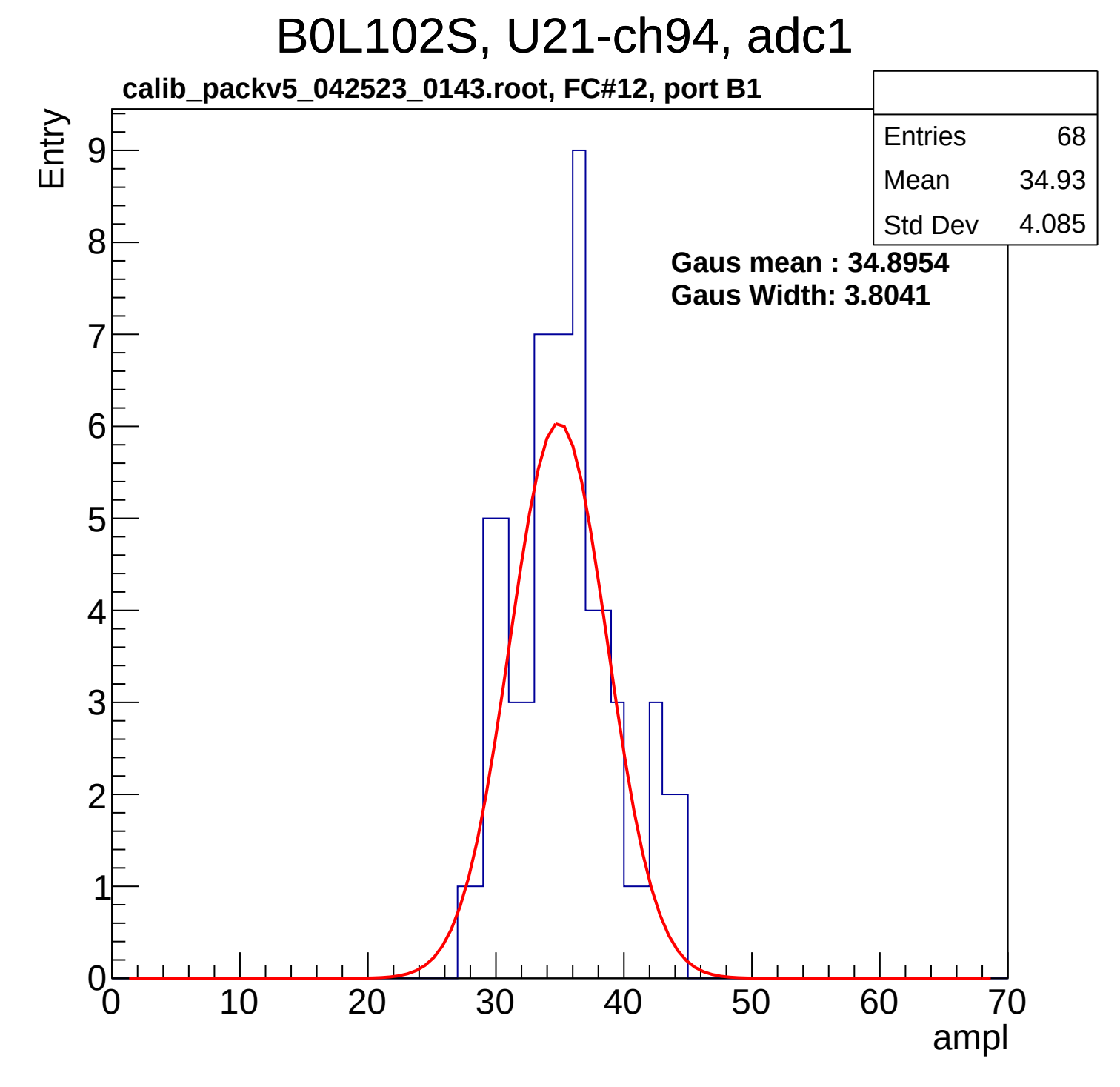
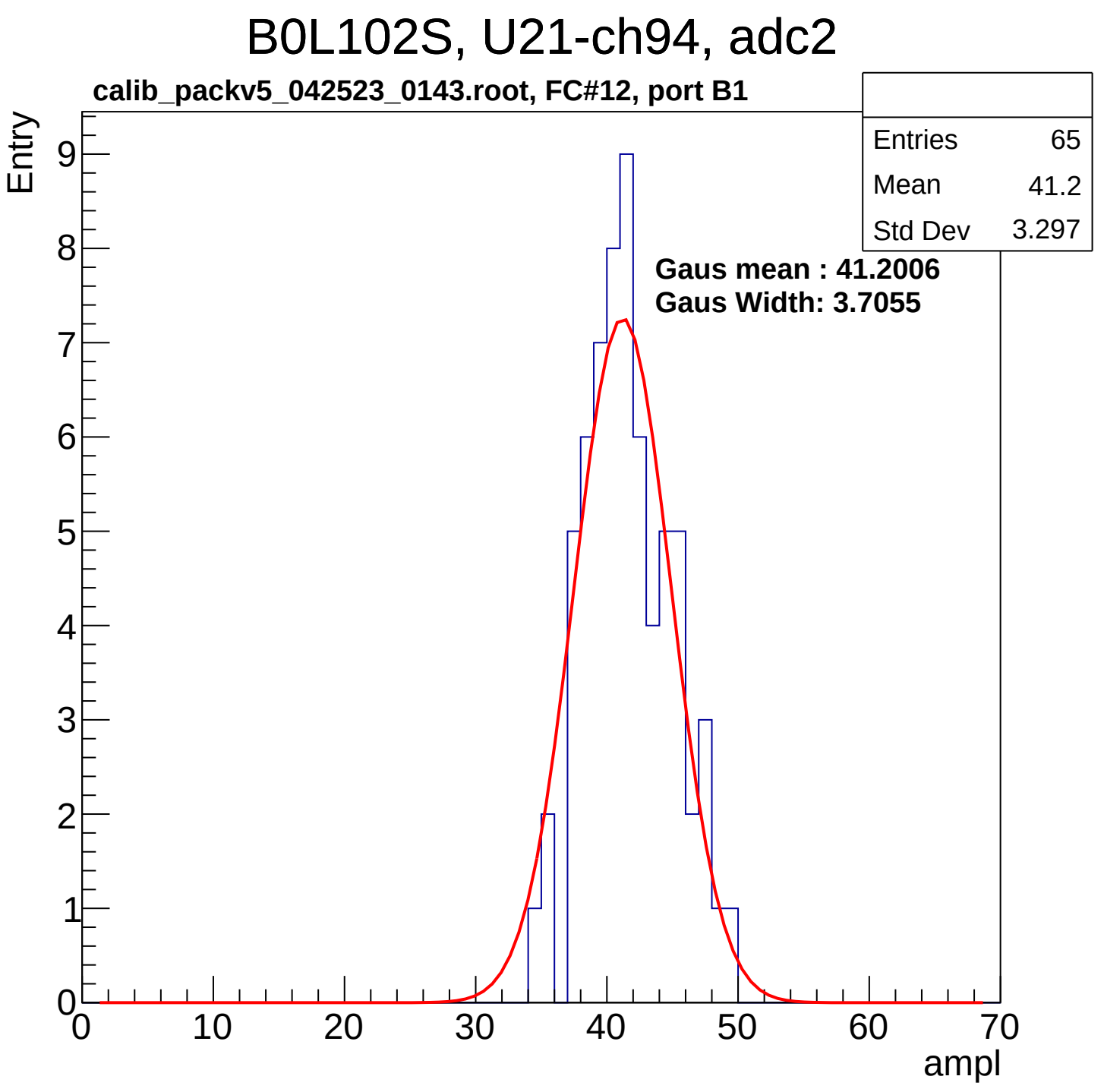
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch94, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 27.89
Std Dev 5.99
Gaus mean : 29.1089
Gaus Width: 3.7430
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch94, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 34.93
Std Dev 4.085
Gaus mean : 34.8954
Gaus Width: 3.8041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch94, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 41.2
Std Dev 3.297
Gaus mean : 41.2006
Gaus Width: 3.7055
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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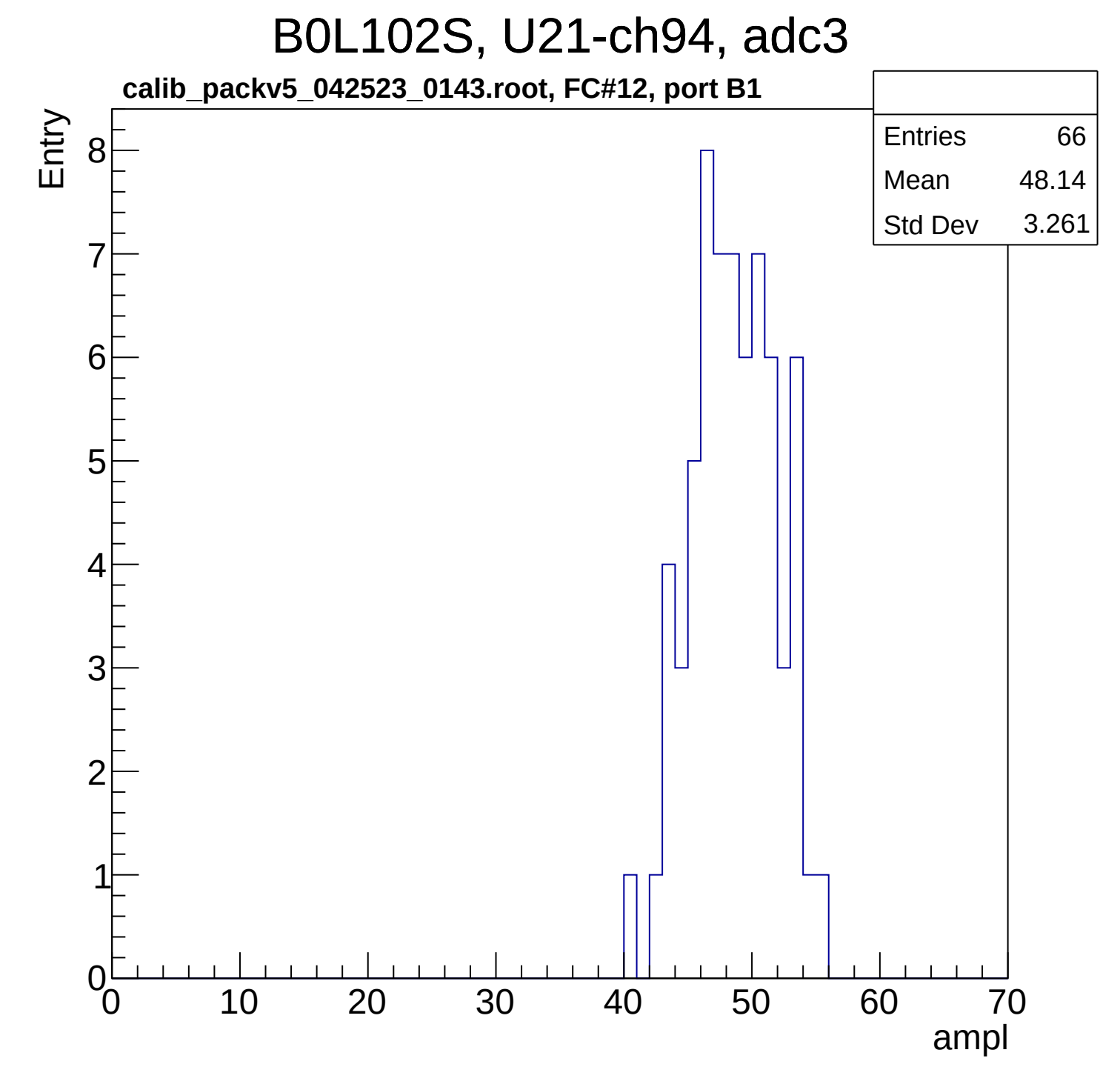
B0L102S, U21-ch94, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 48.14
Std Dev 3.261
Entry
ampl
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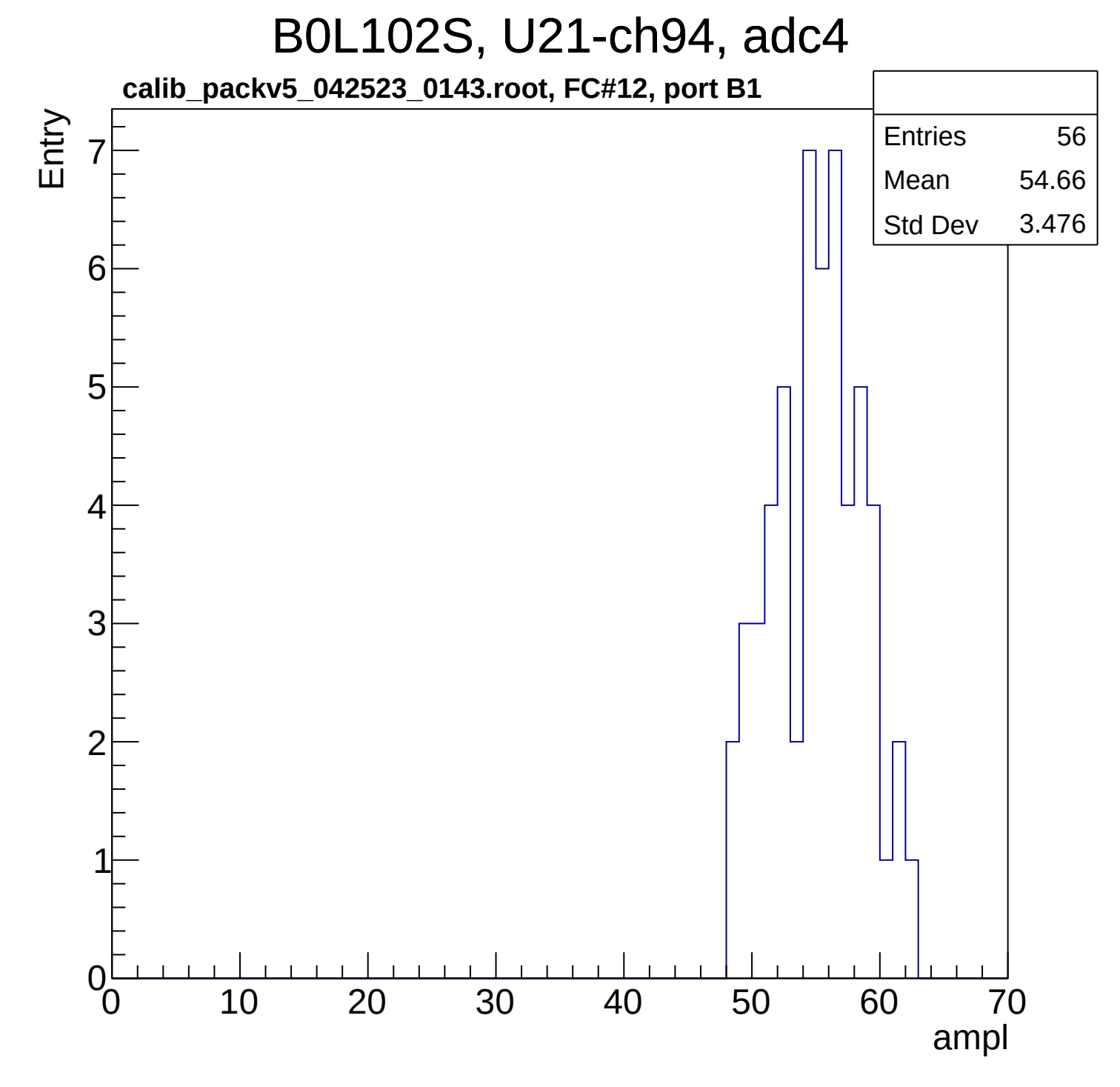
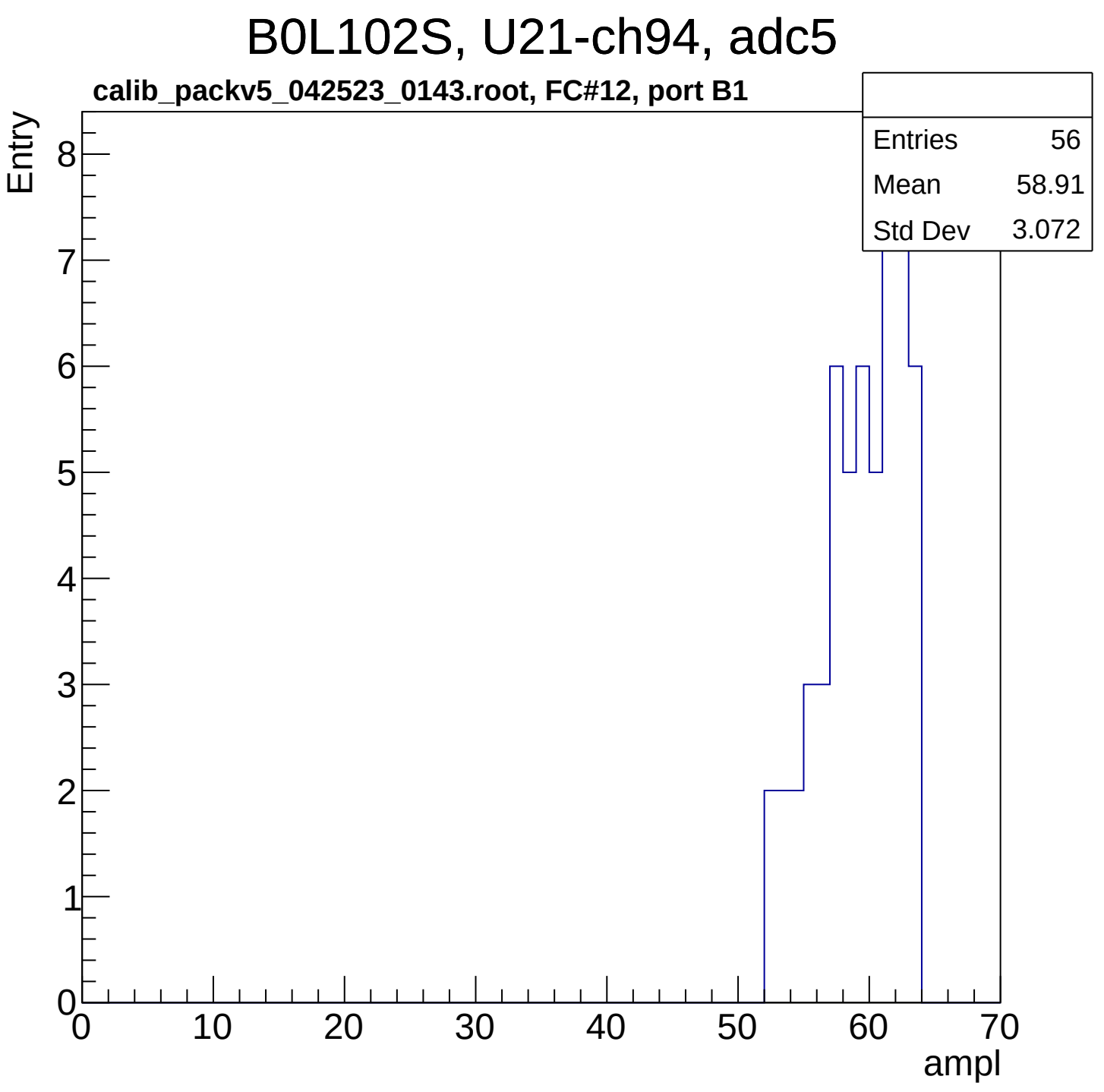
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch94, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 54.66
Std Dev 3.476
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch94, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 58.91
Std Dev 3.072
Entry
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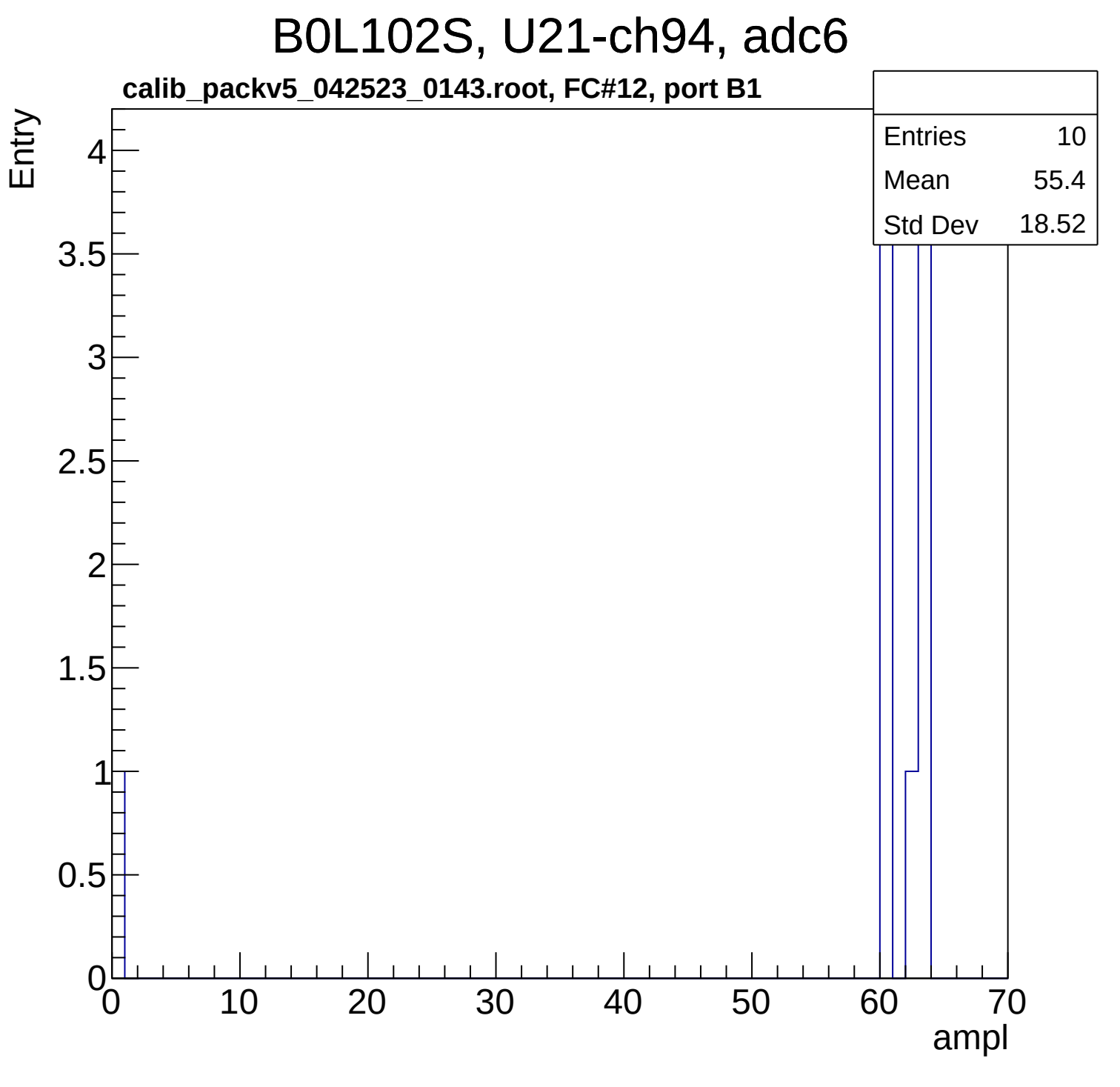
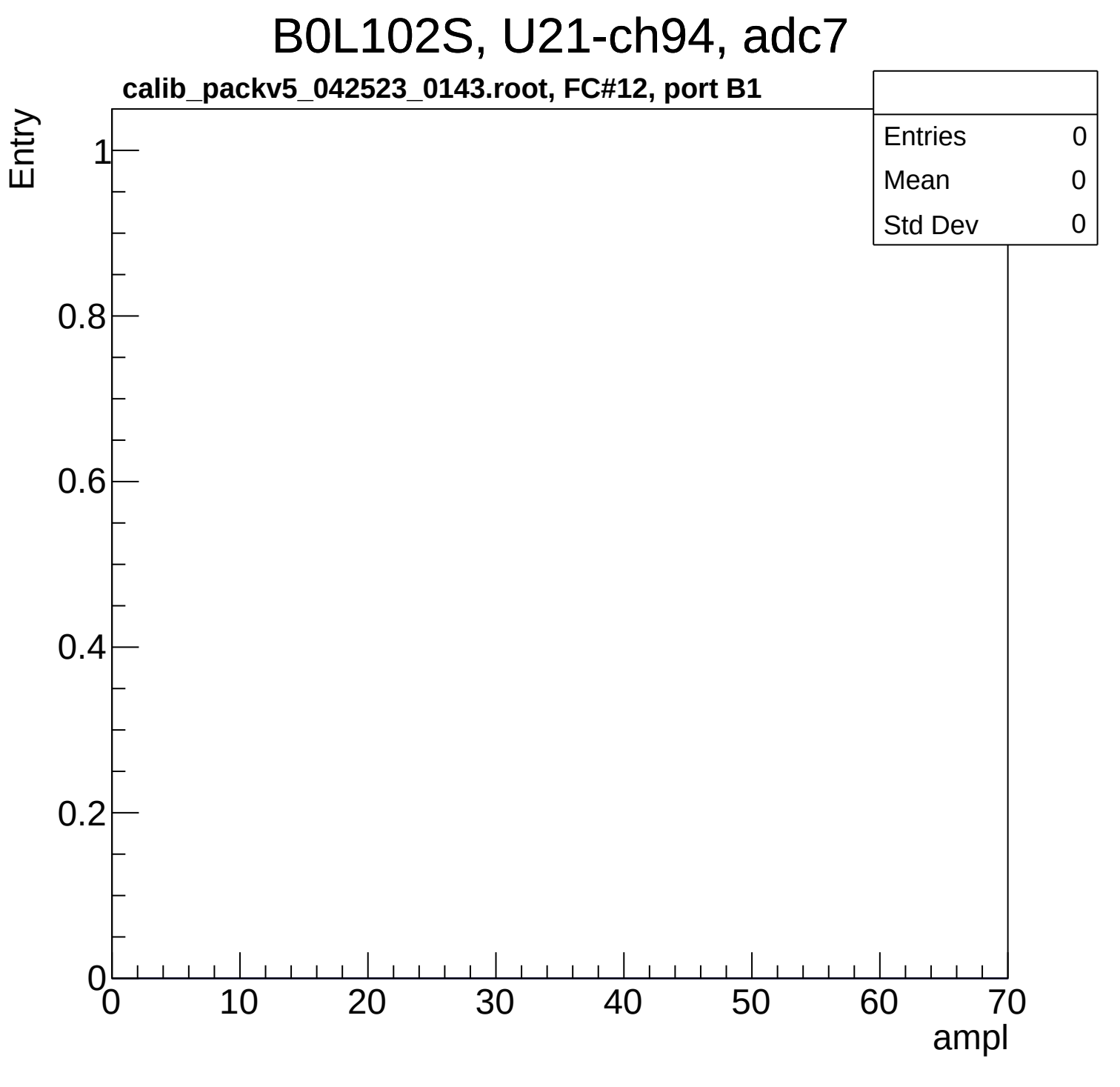
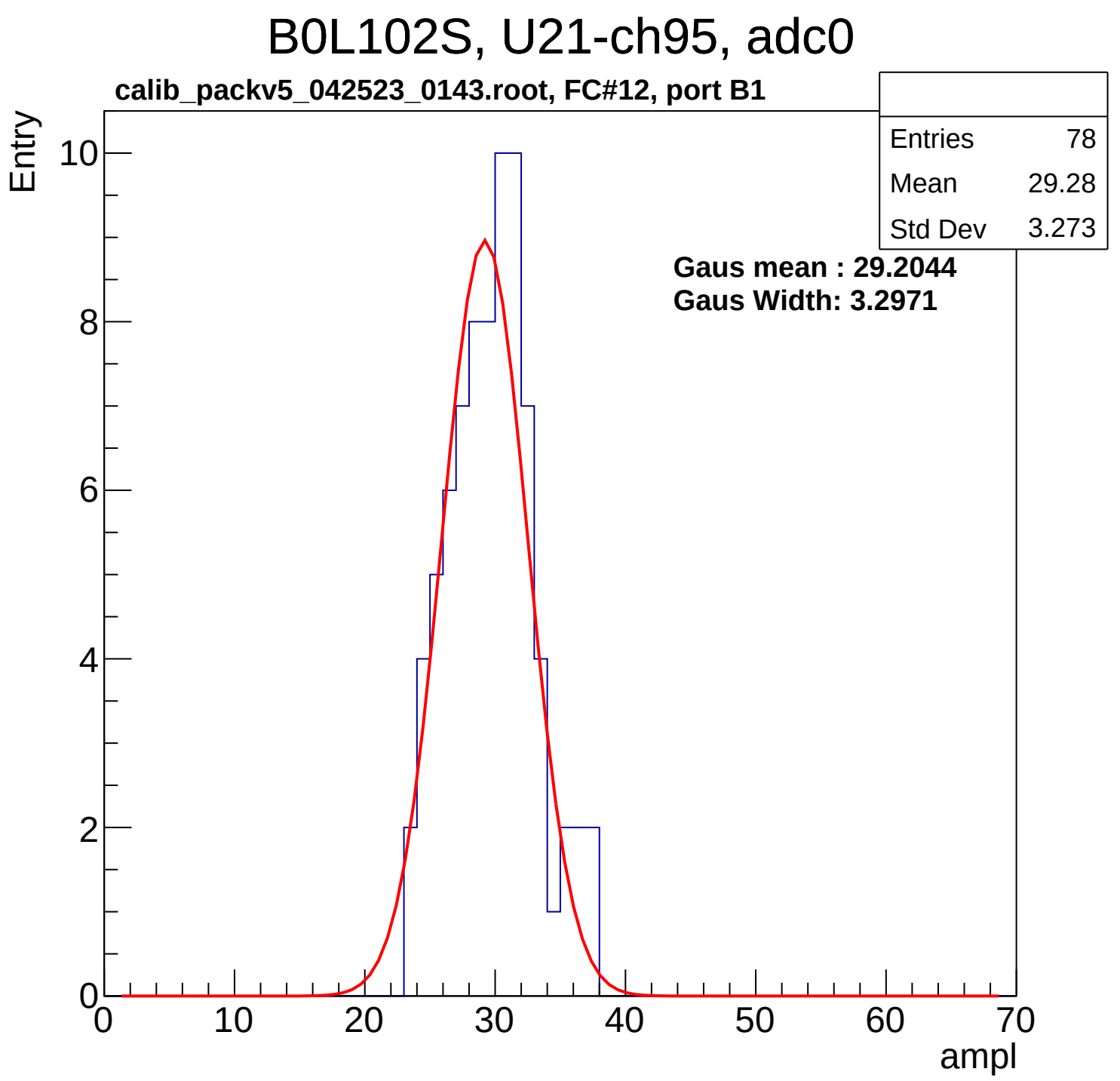
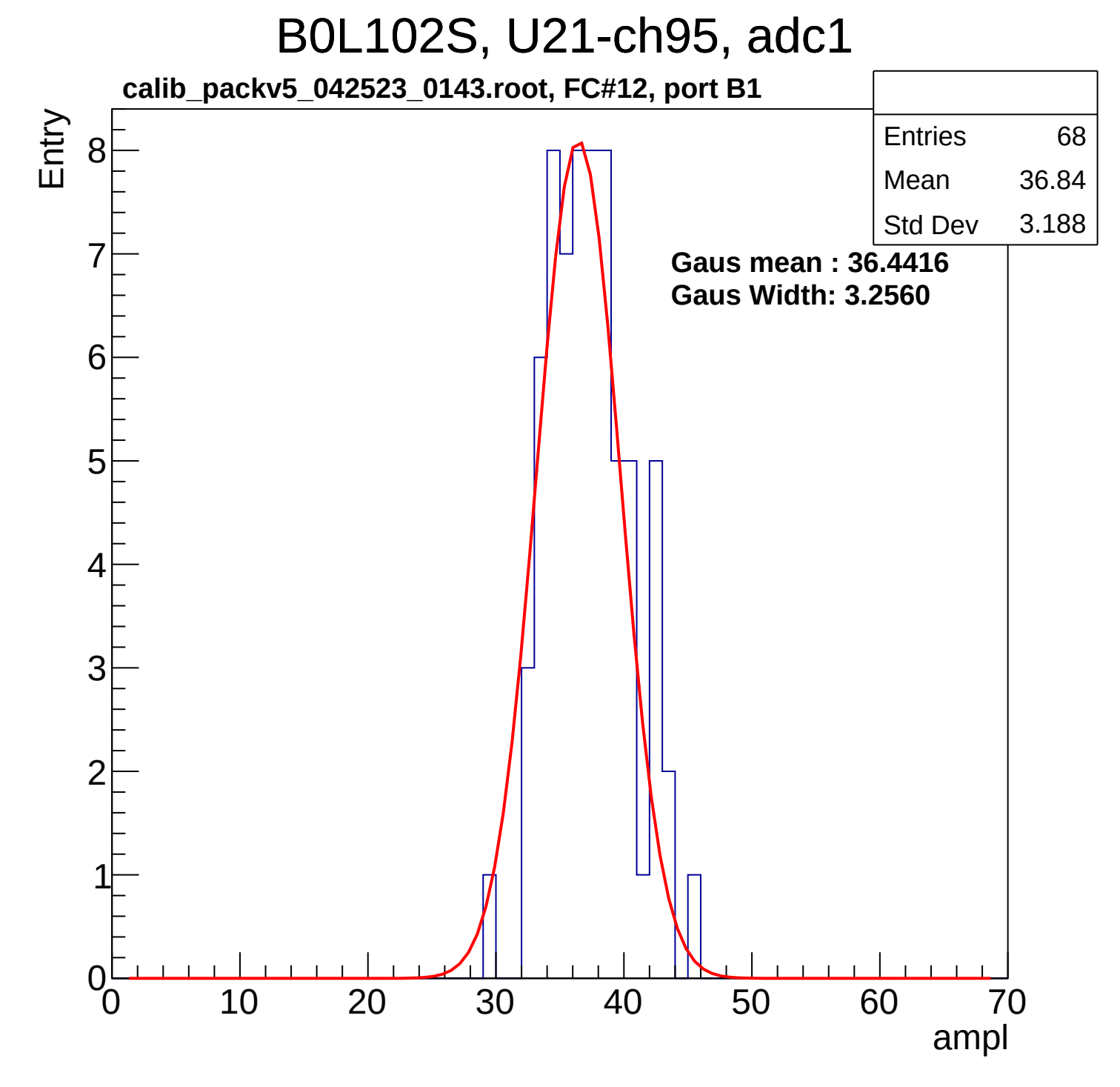
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch94, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 55.4
Std Dev 18.52
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch94, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch95, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 29.28
Std Dev 3.273
Gaus mean : 29.2044
Gaus Width: 3.2971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch95, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 36.84
Std Dev 3.188
Gaus mean : 36.4416
Gaus Width: 3.2560
Entry
ampl
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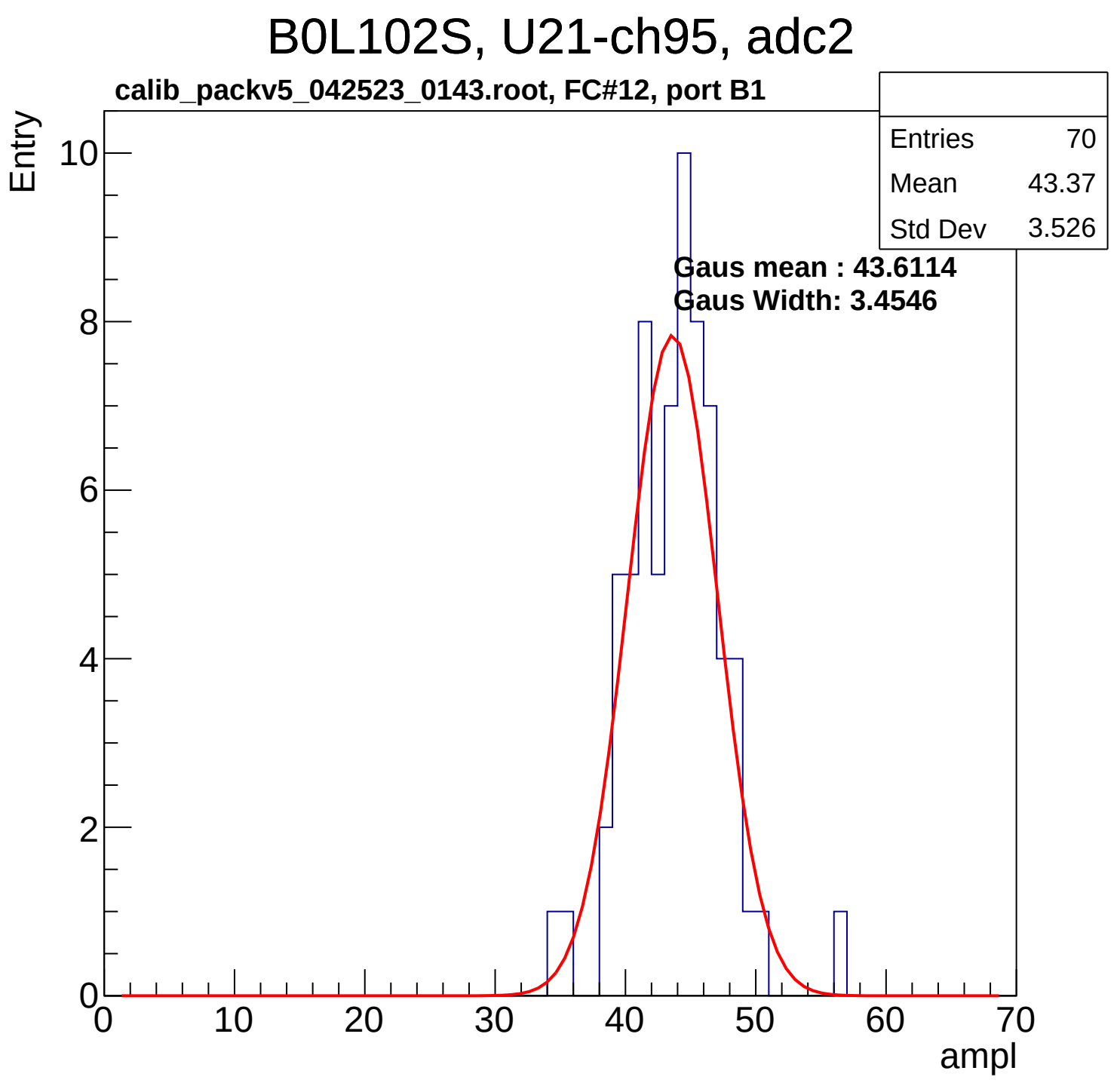
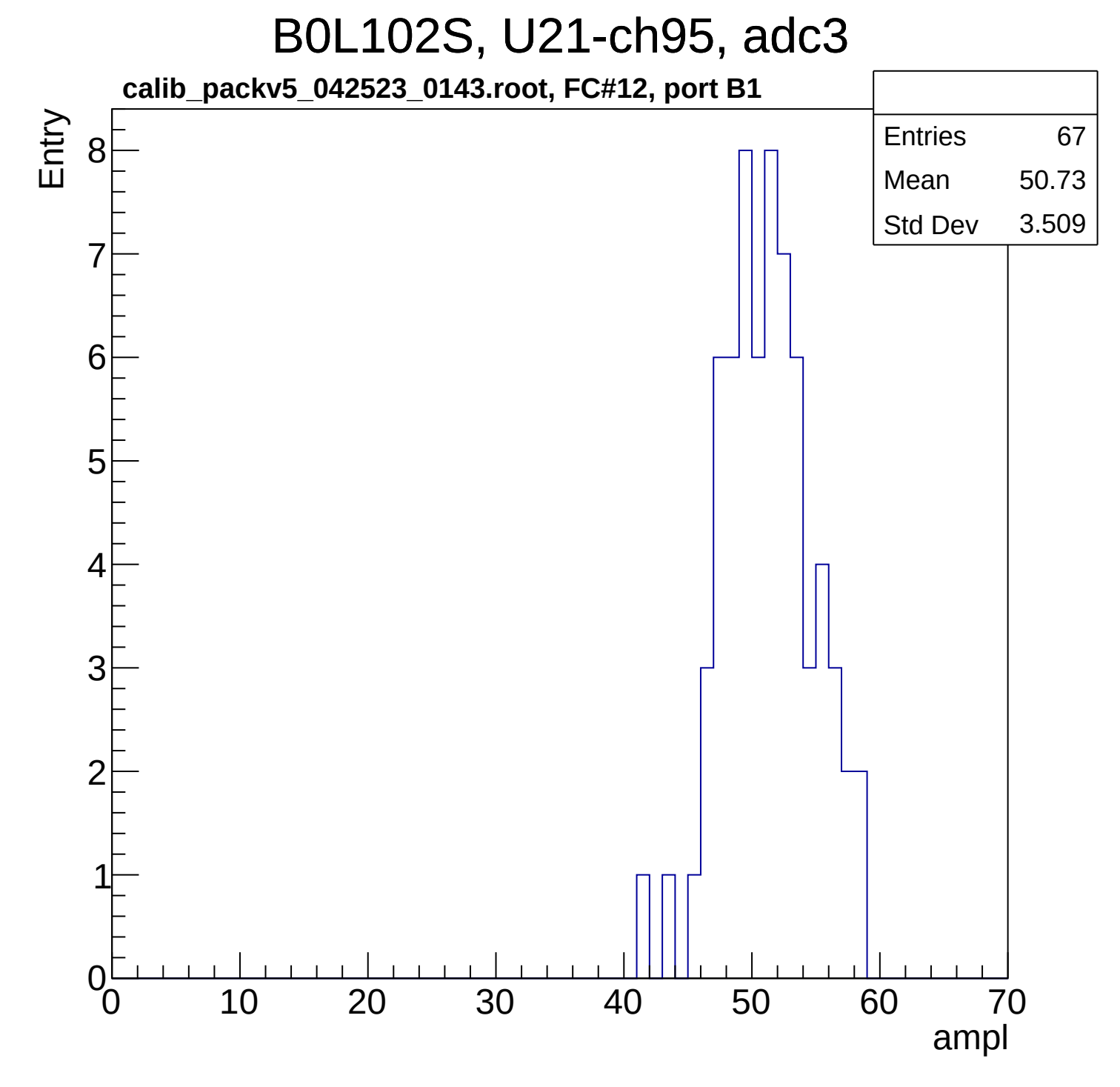
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch95, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 43.37
Std Dev 3.526
Gaus mean : 43.6114
Gaus Width: 3.4546
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch95, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.73
Std Dev 3.509
Entry
ampl
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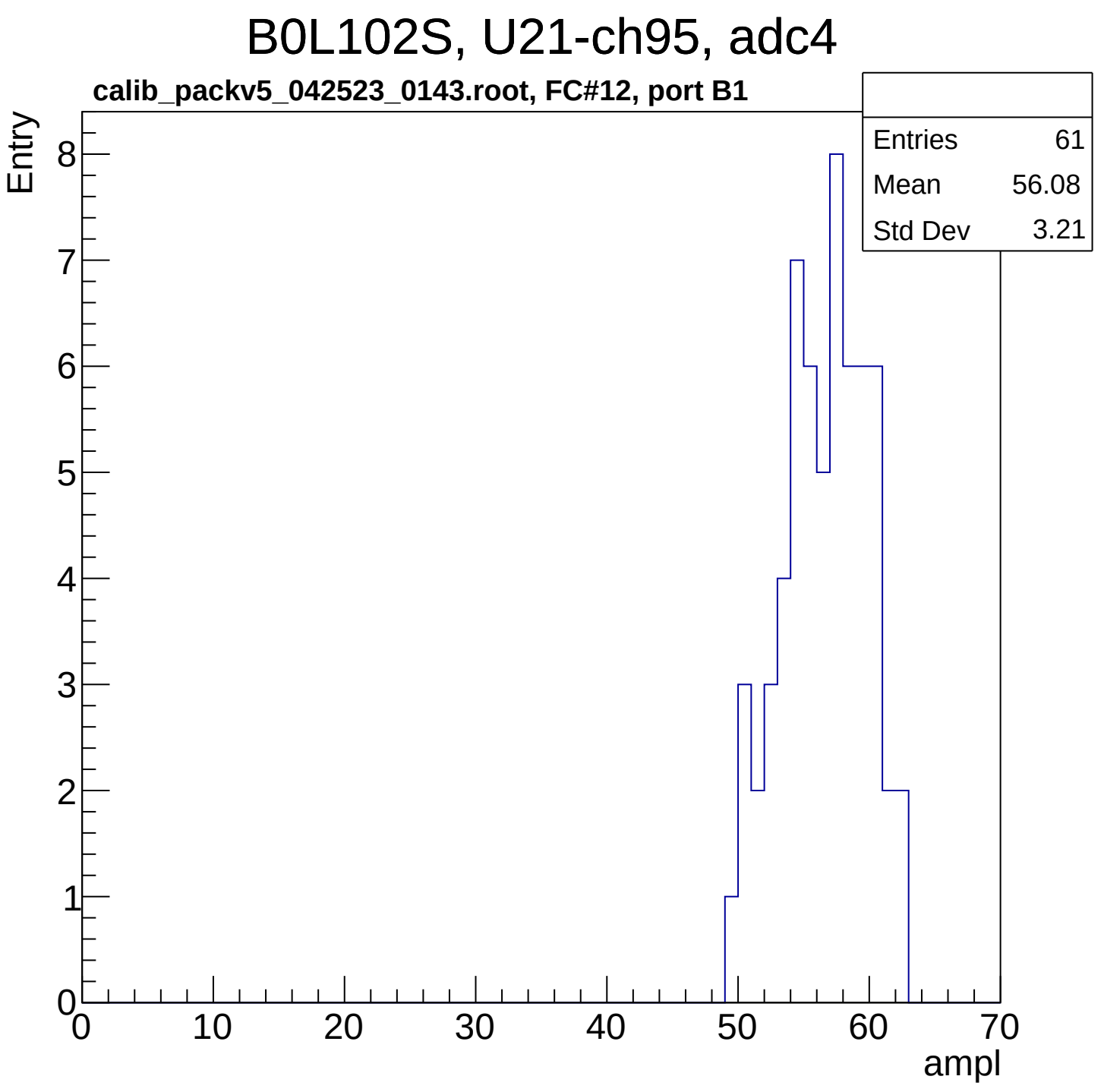
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch95, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 56.08
Std Dev 3.21
Entry
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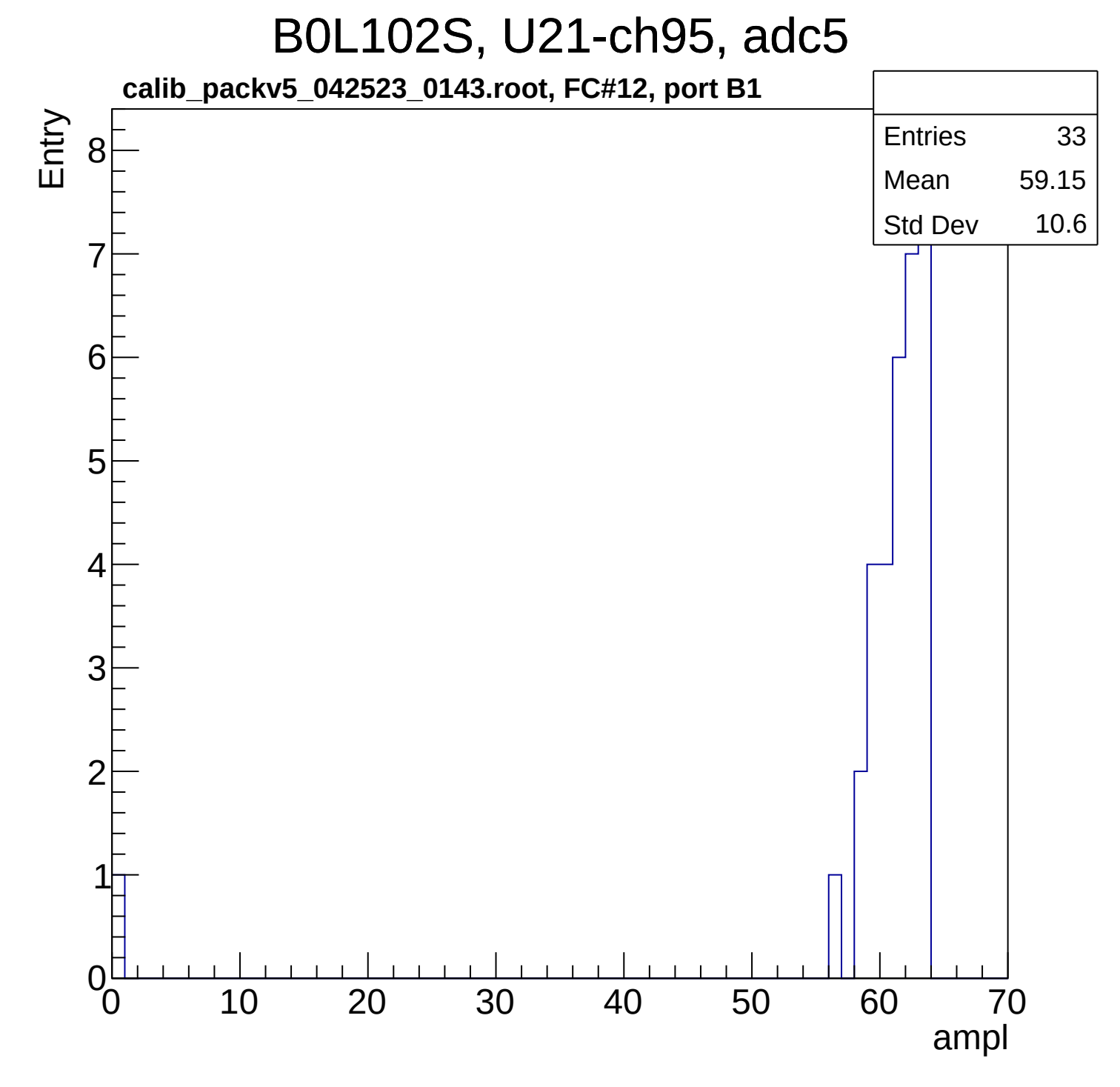
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch95, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 59.15
Std Dev 10.6
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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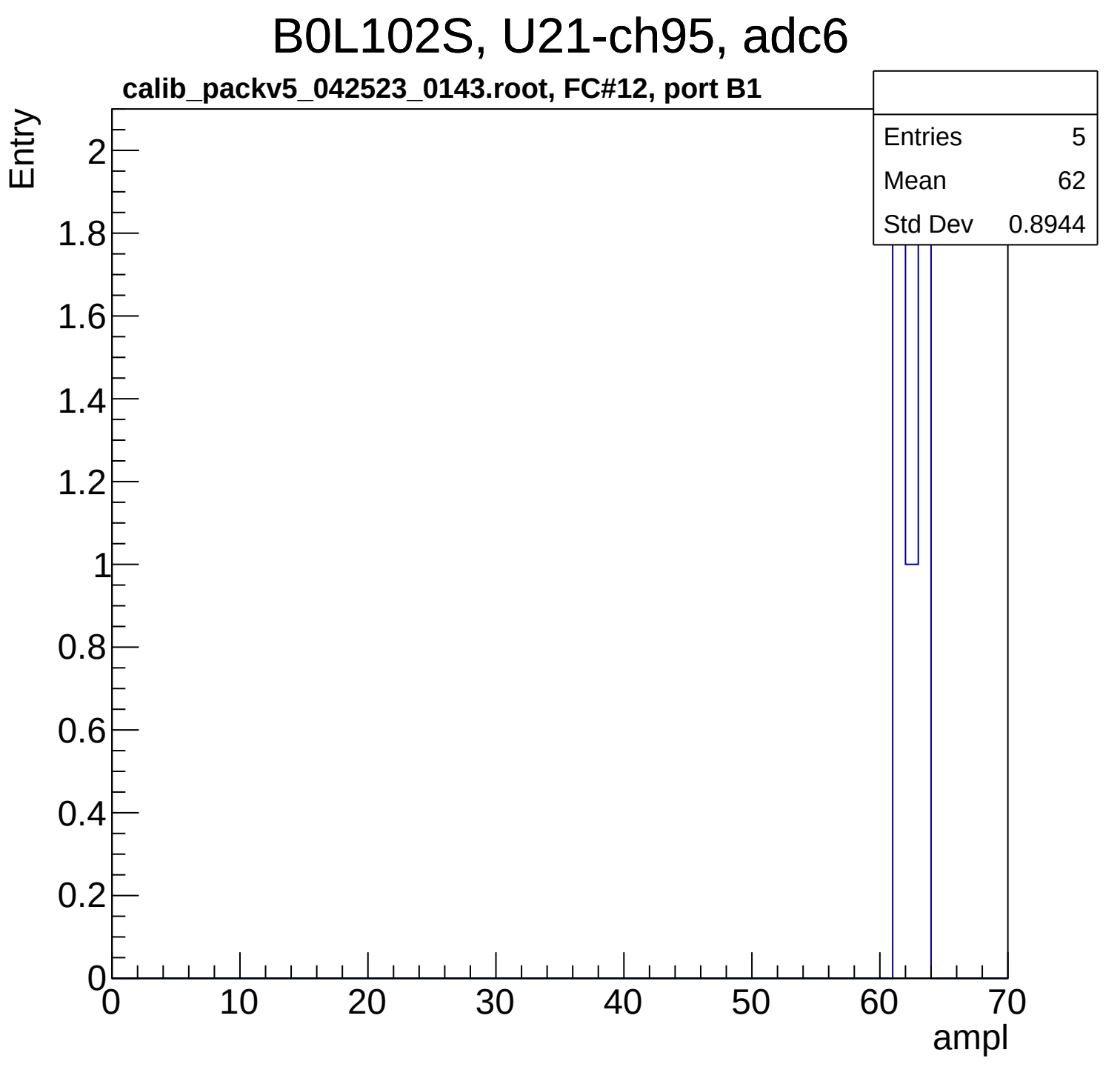
B0L102S, U21-ch95, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
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0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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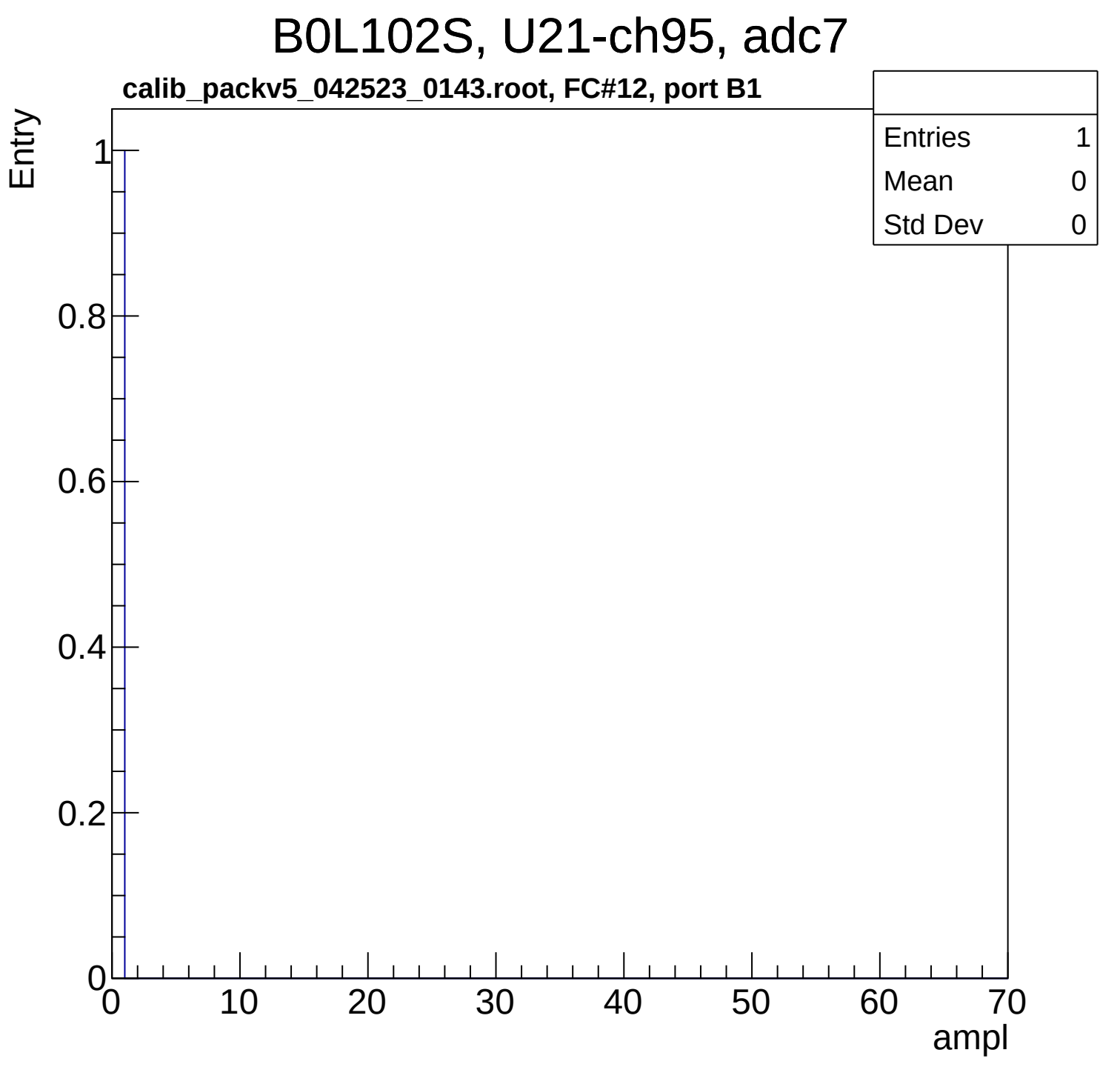
B0L102S, U21-ch95, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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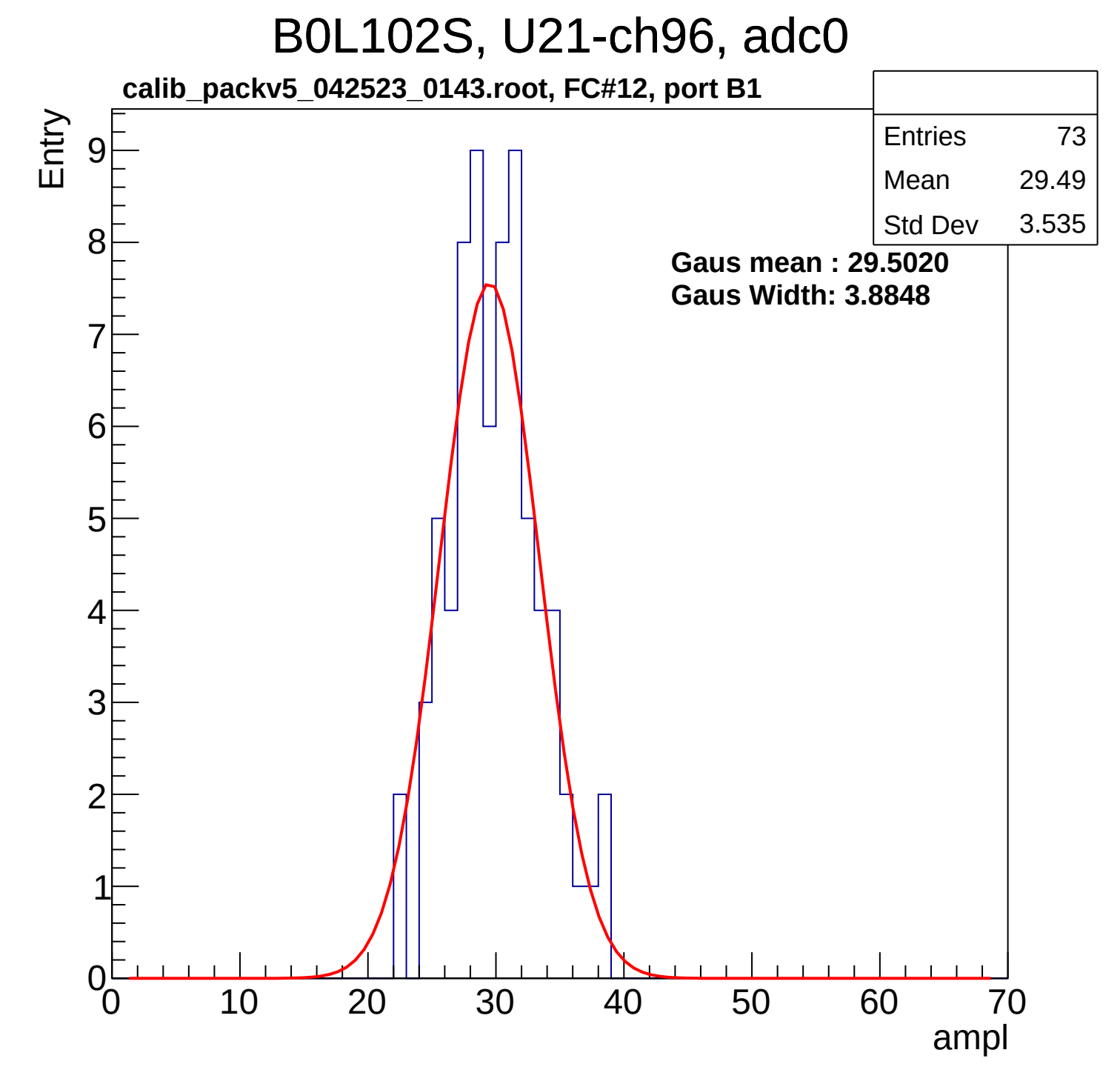
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch96, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 29.49
Std Dev 3.535
Gaus mean : 29.5020
Gaus Width: 3.8848
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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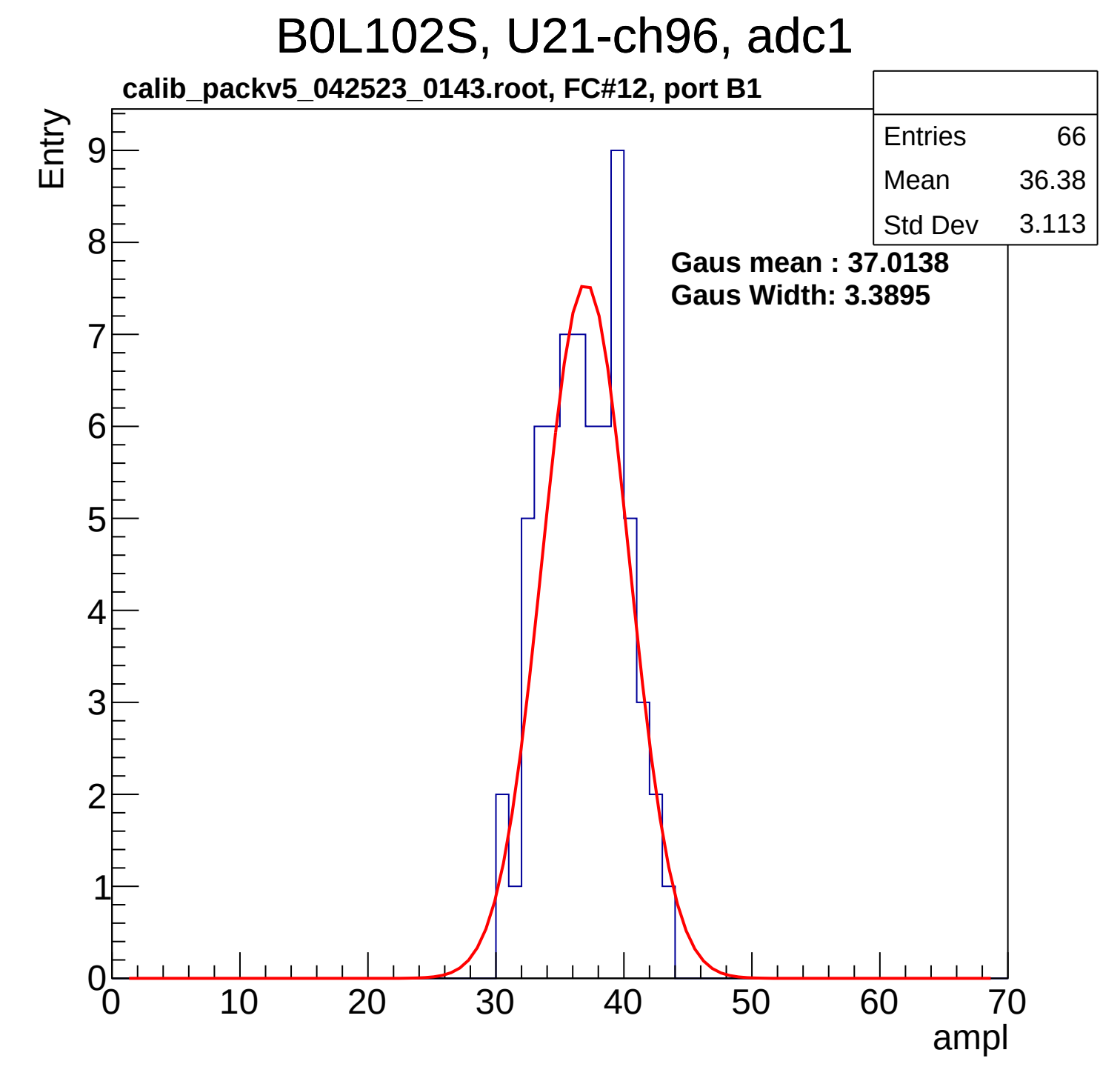
B0L102S, U21-ch96, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.38
Std Dev 3.113
Gaus mean : 37.0138
Gaus Width: 3.3895
Entry
ampl
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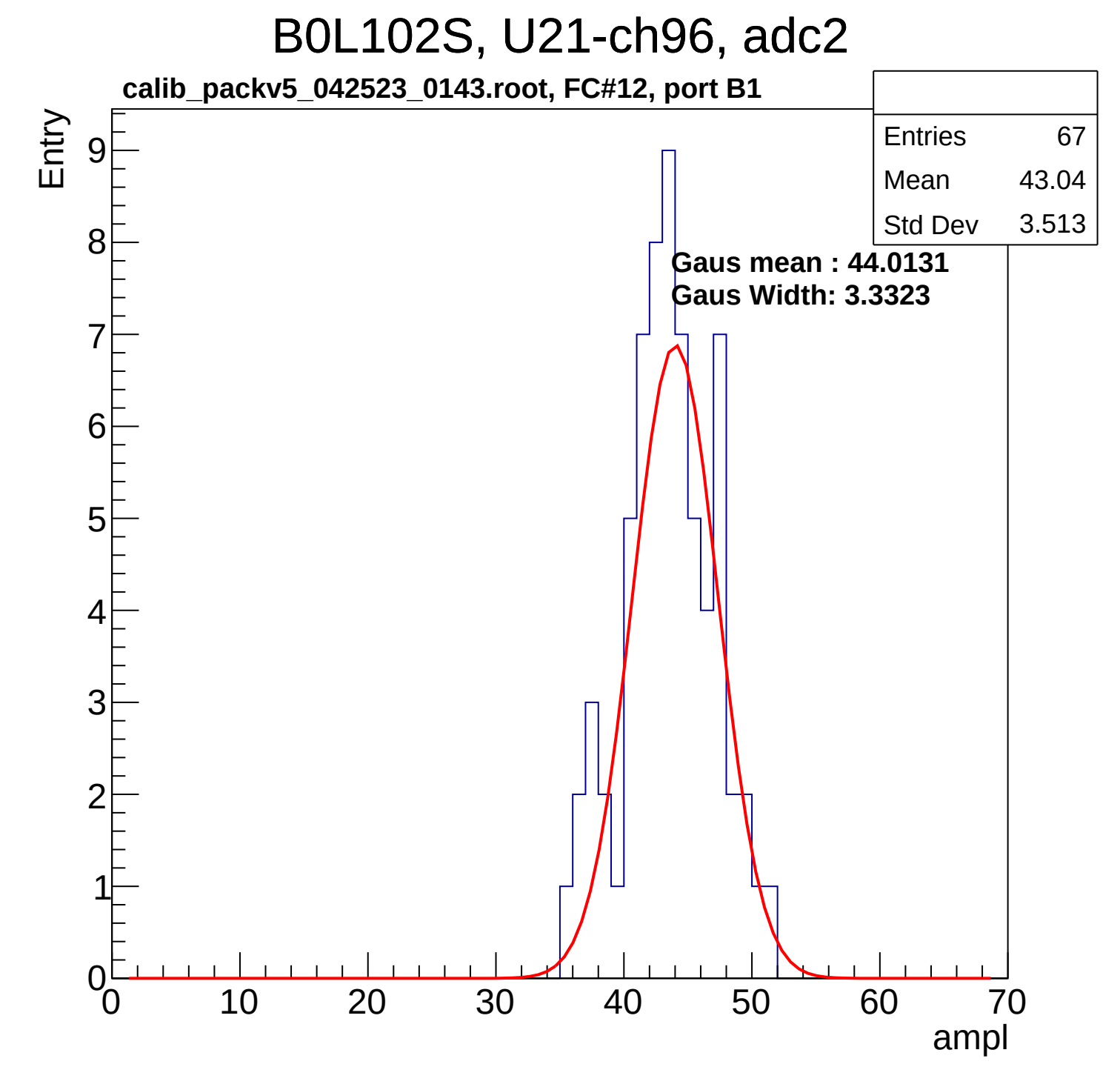
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch96, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 43.04
Std Dev 3.513
Gaus mean : 44.0131
Gaus Width: 3.3323
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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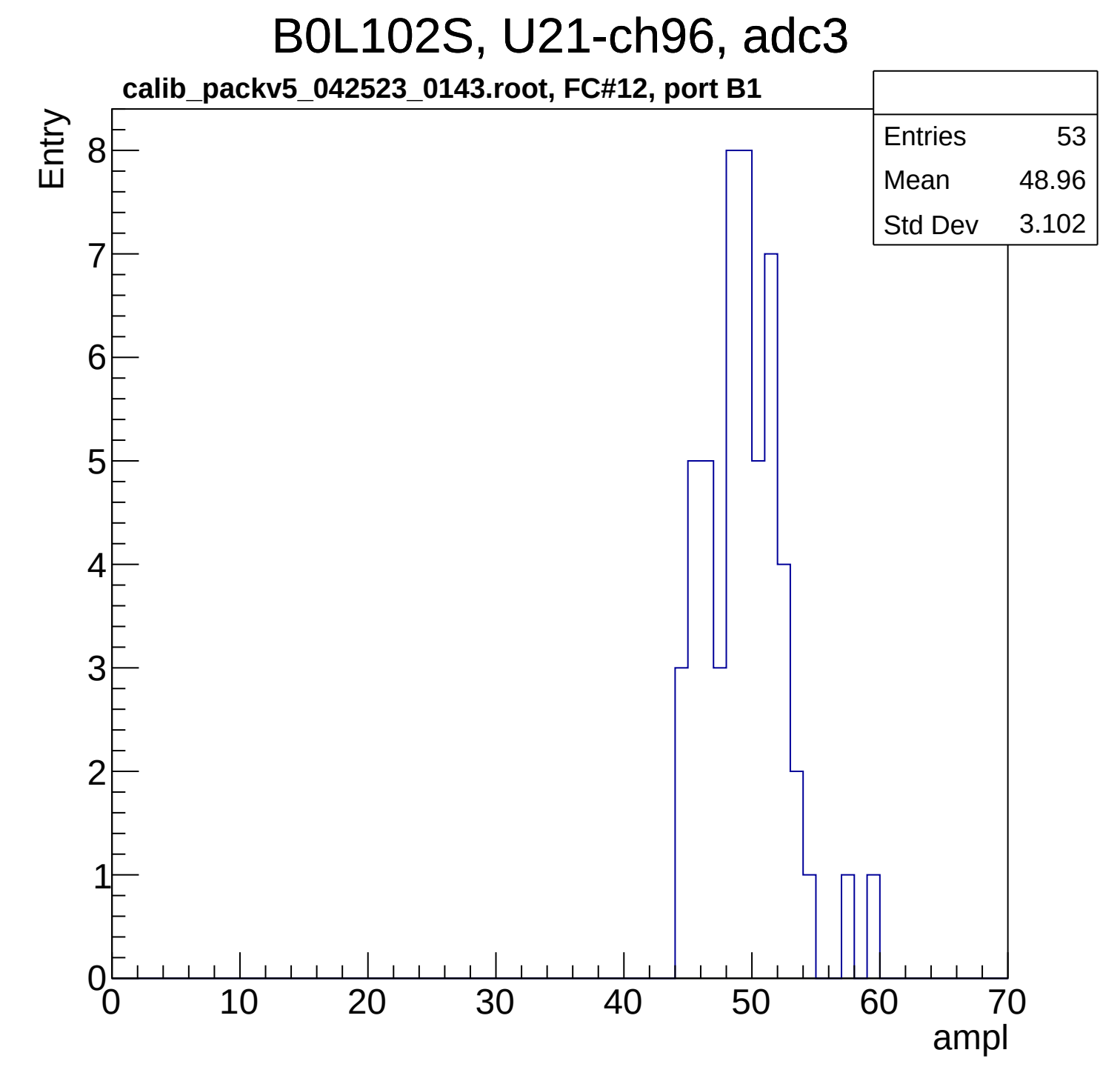
B0L102S, U21-ch96, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 48.96
Std Dev 3.102
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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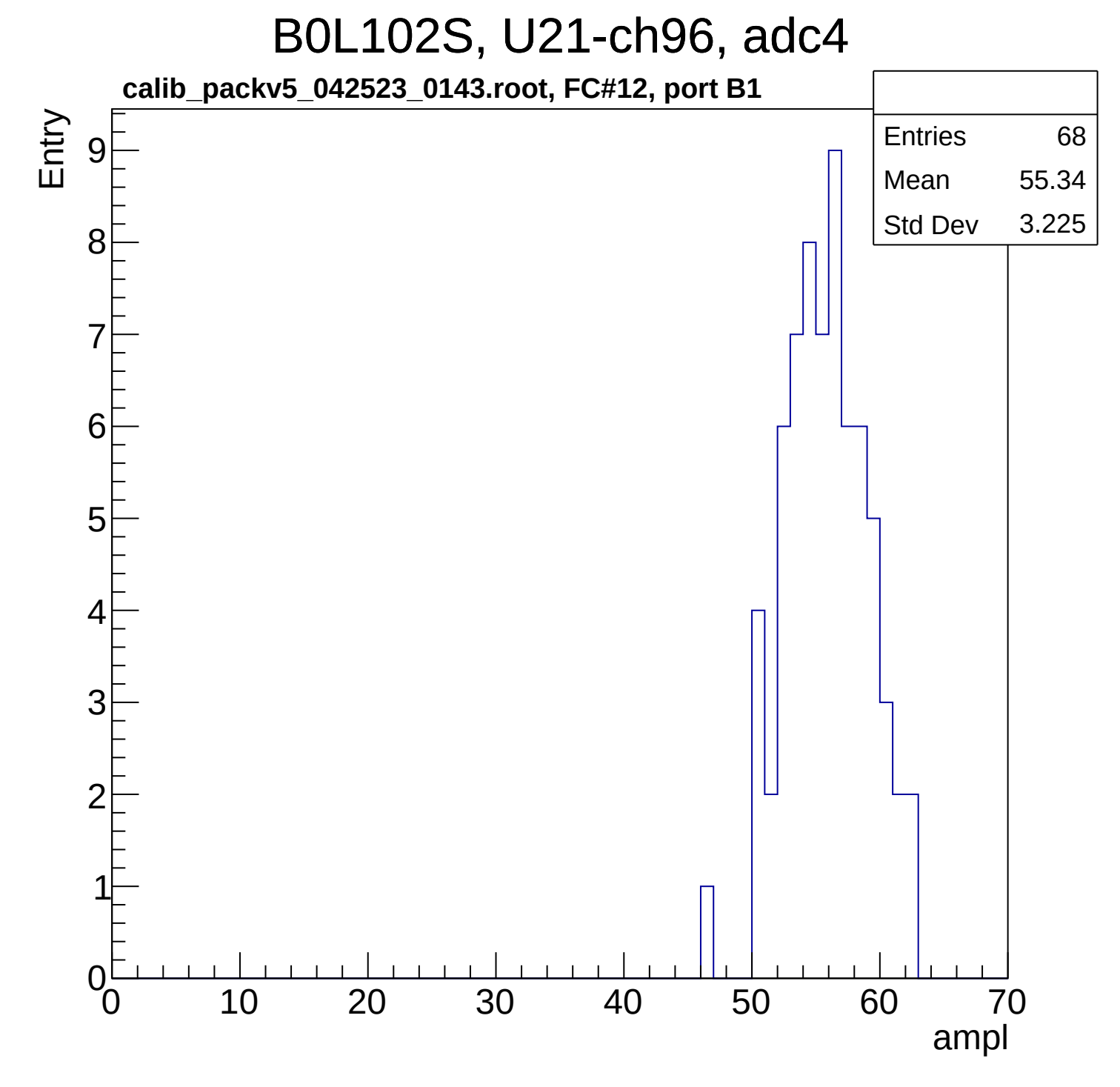
B0L102S, U21-ch96, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 55.34
Std Dev 3.225
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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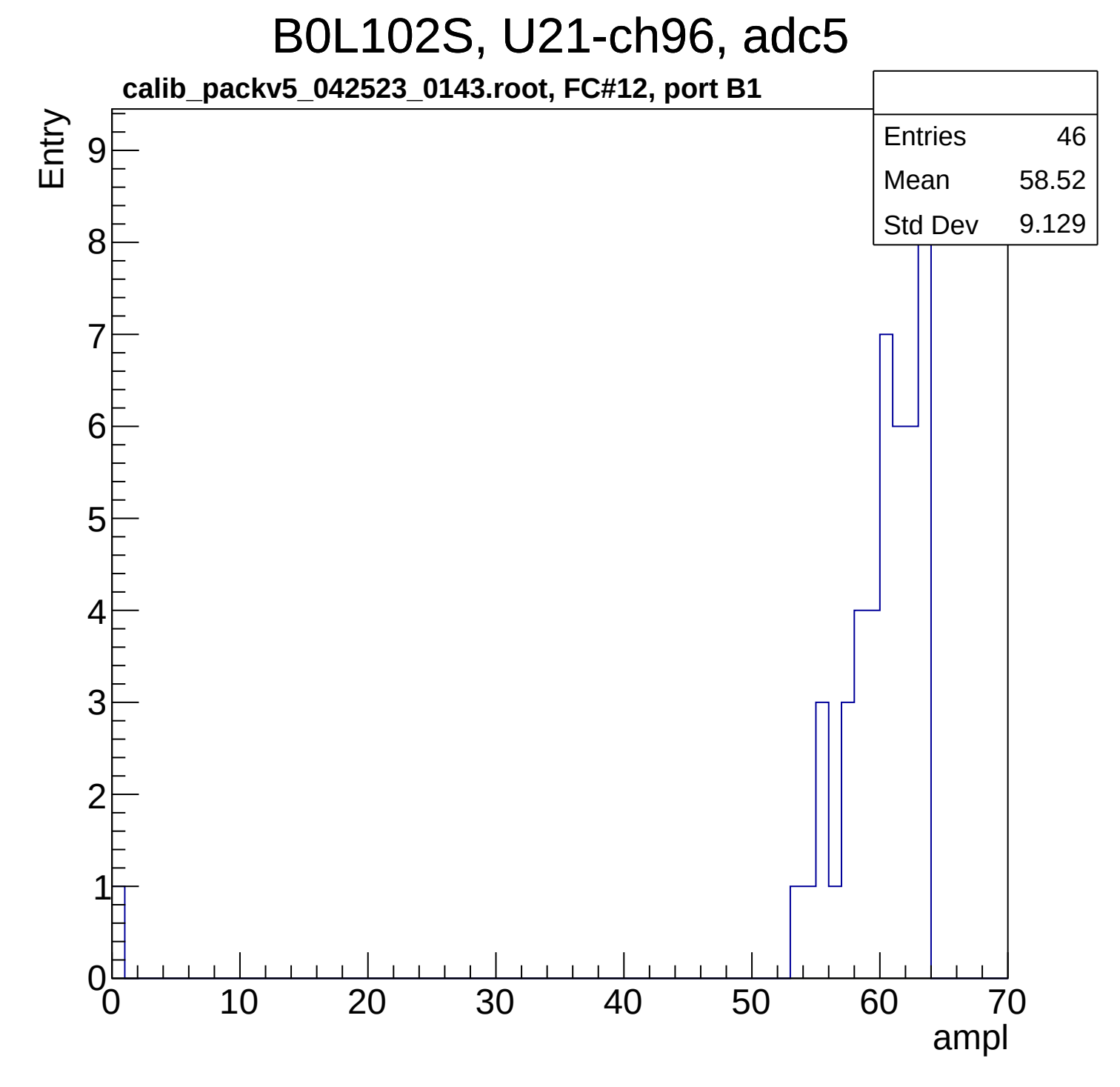
B0L102S, U21-ch96, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.52
Std Dev 9.129
Entry
ampl
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3
4
5
6
7
8
9
0
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40
50
60
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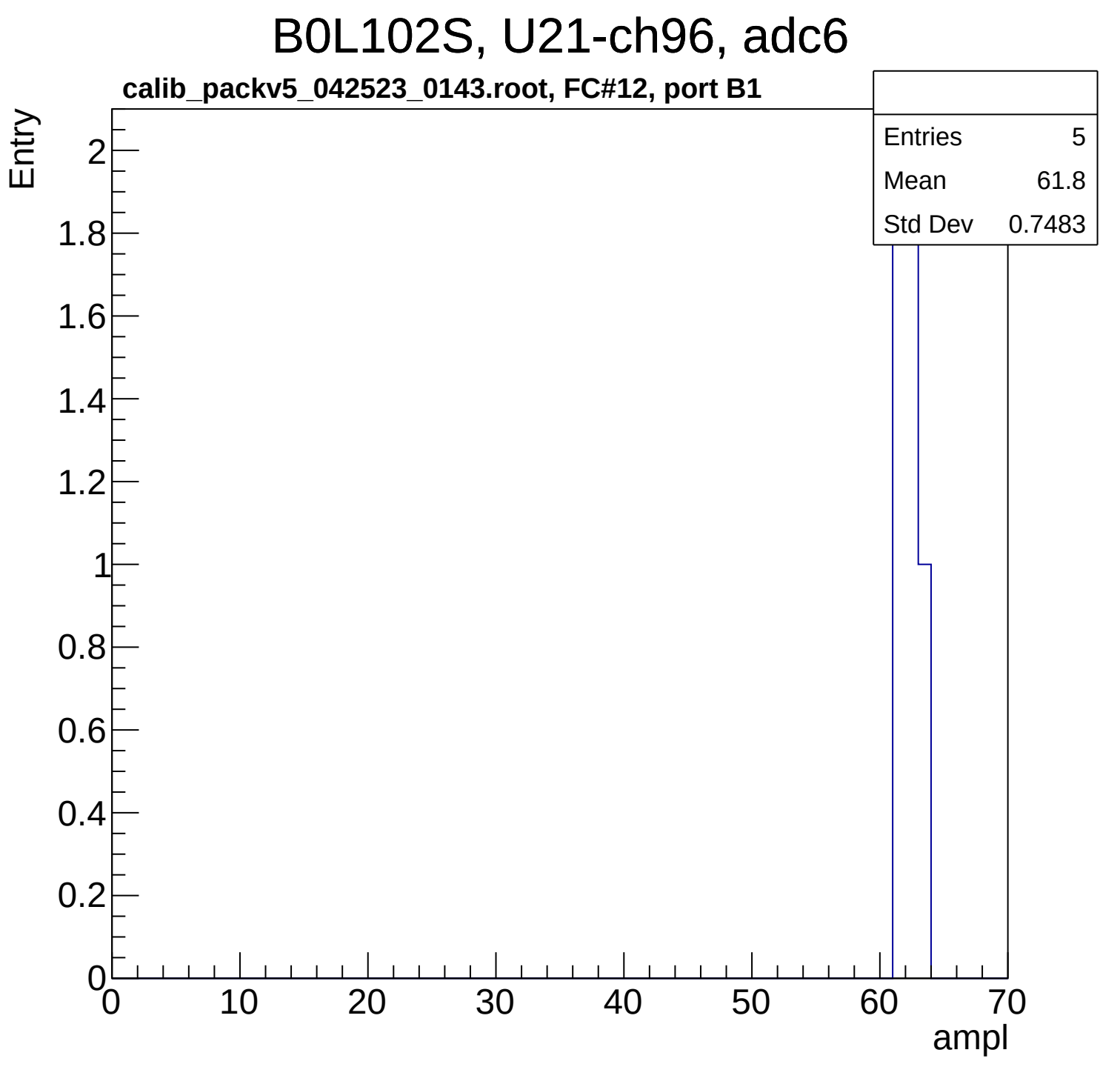
B0L102S, U21-ch96, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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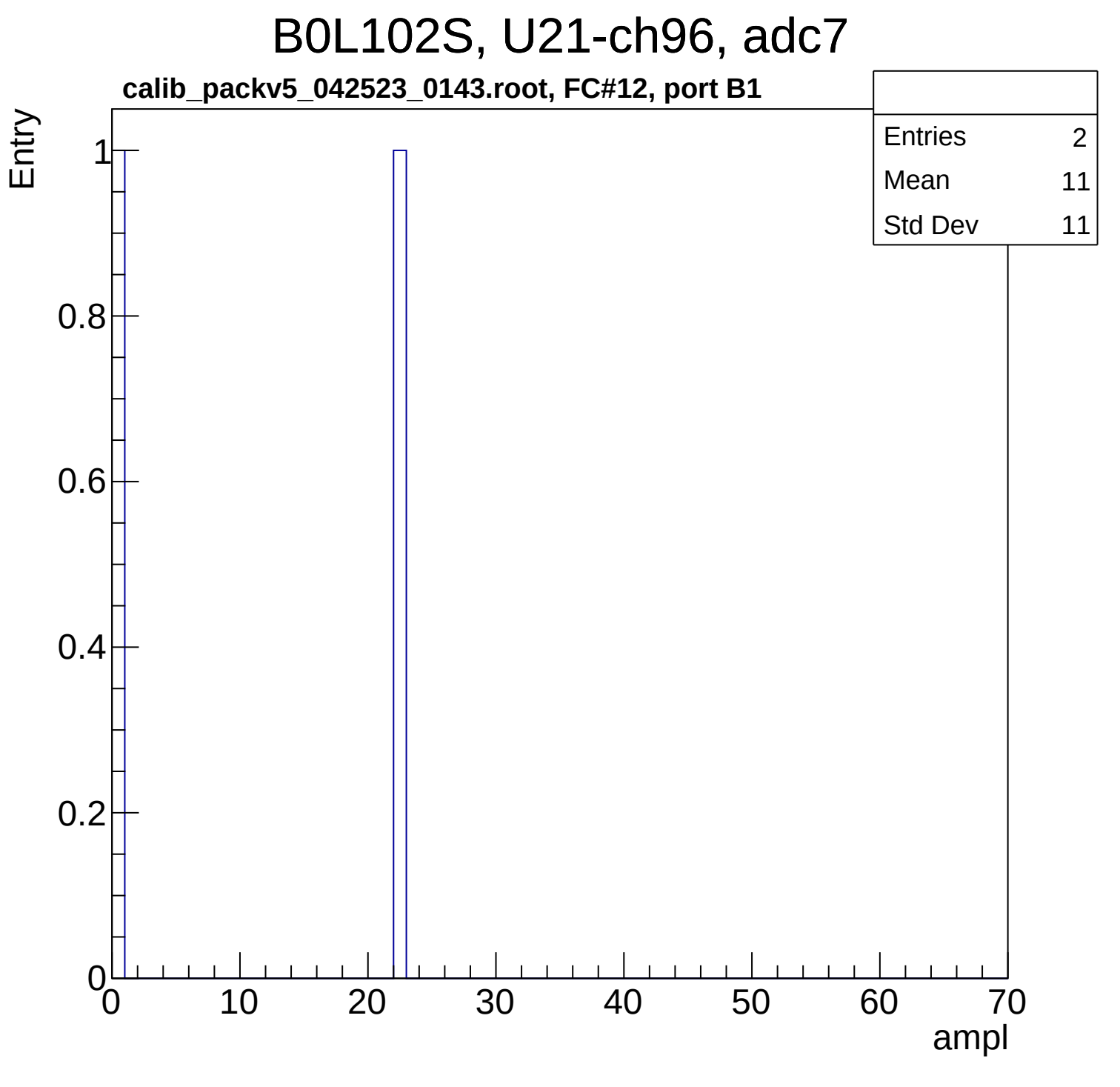
B0L102S, U21-ch96, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
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10
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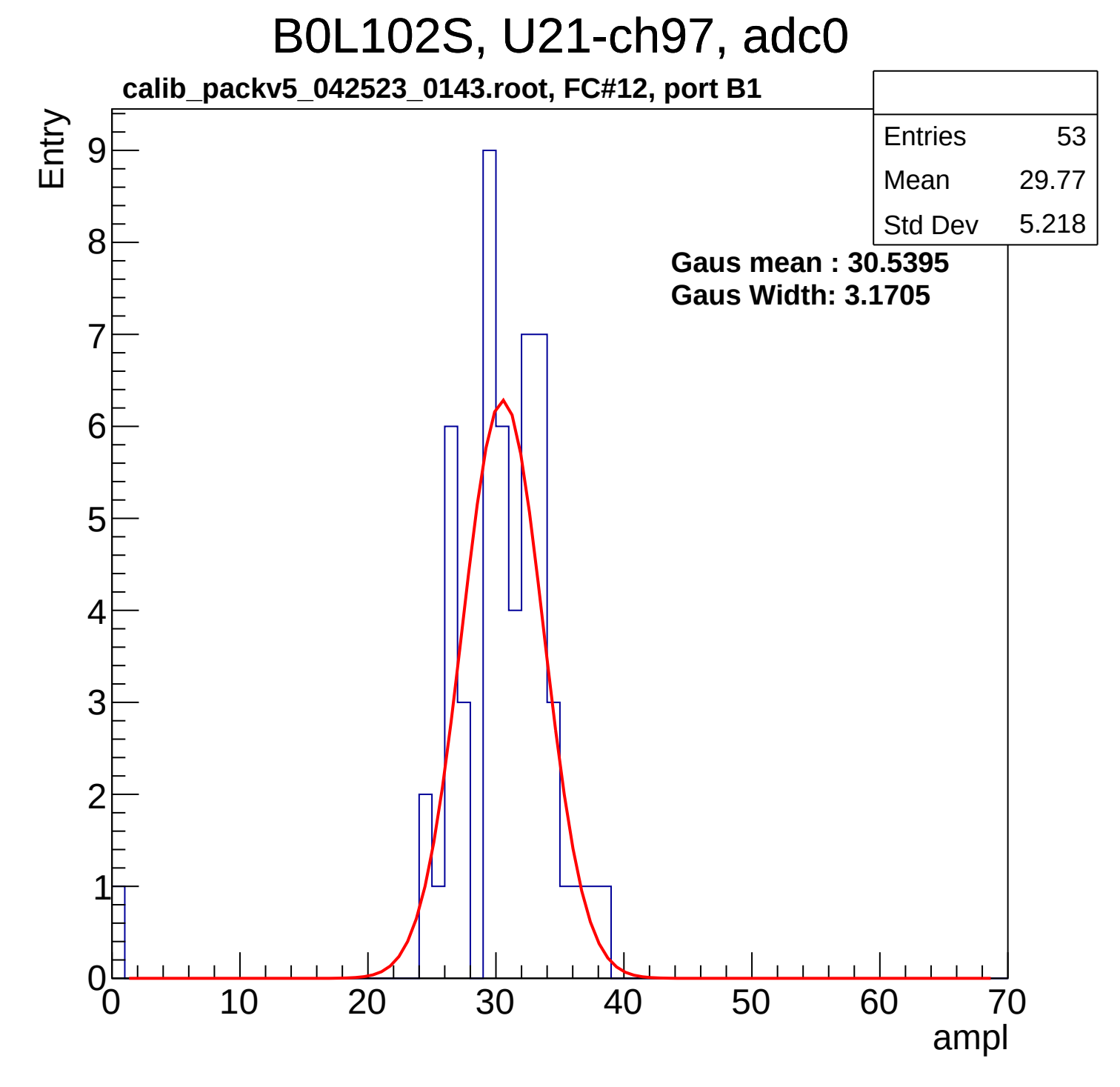
50
60
70
ampl


B0L102S, U21-ch97, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 29.77
Std Dev 5.218
Gaus mean : 30.5395
Gaus Width: 3.1705
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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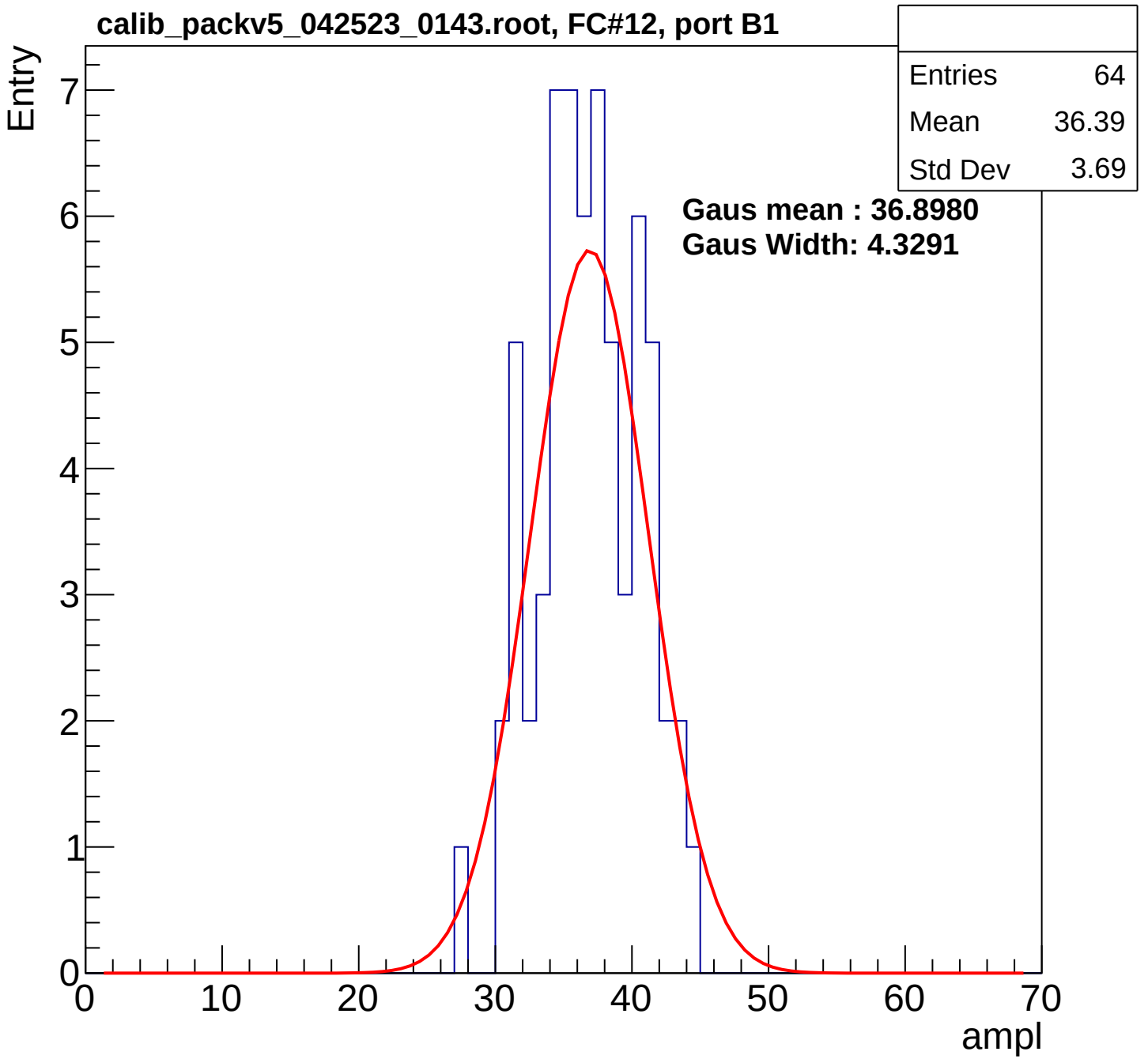
B0L102S, U21-ch97, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.39
Std Dev 3.69
Gaus mean : 36.8980
Gaus Width: 4.3291
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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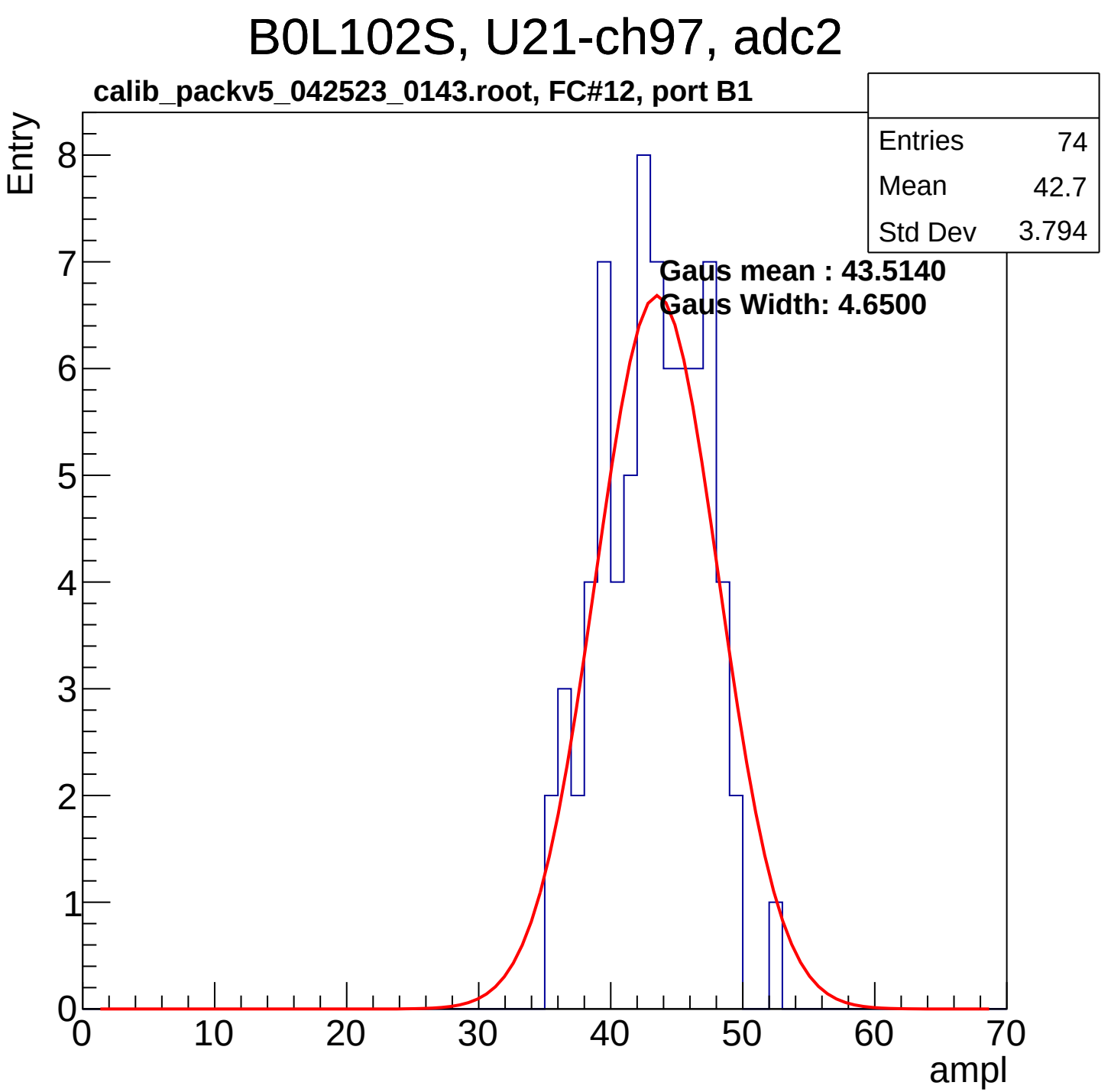
B0L102S, U21-ch97, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 42.7
Std Dev 3.794
Gaus mean : 43.5140
Gaus Width: 4.6500
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
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60
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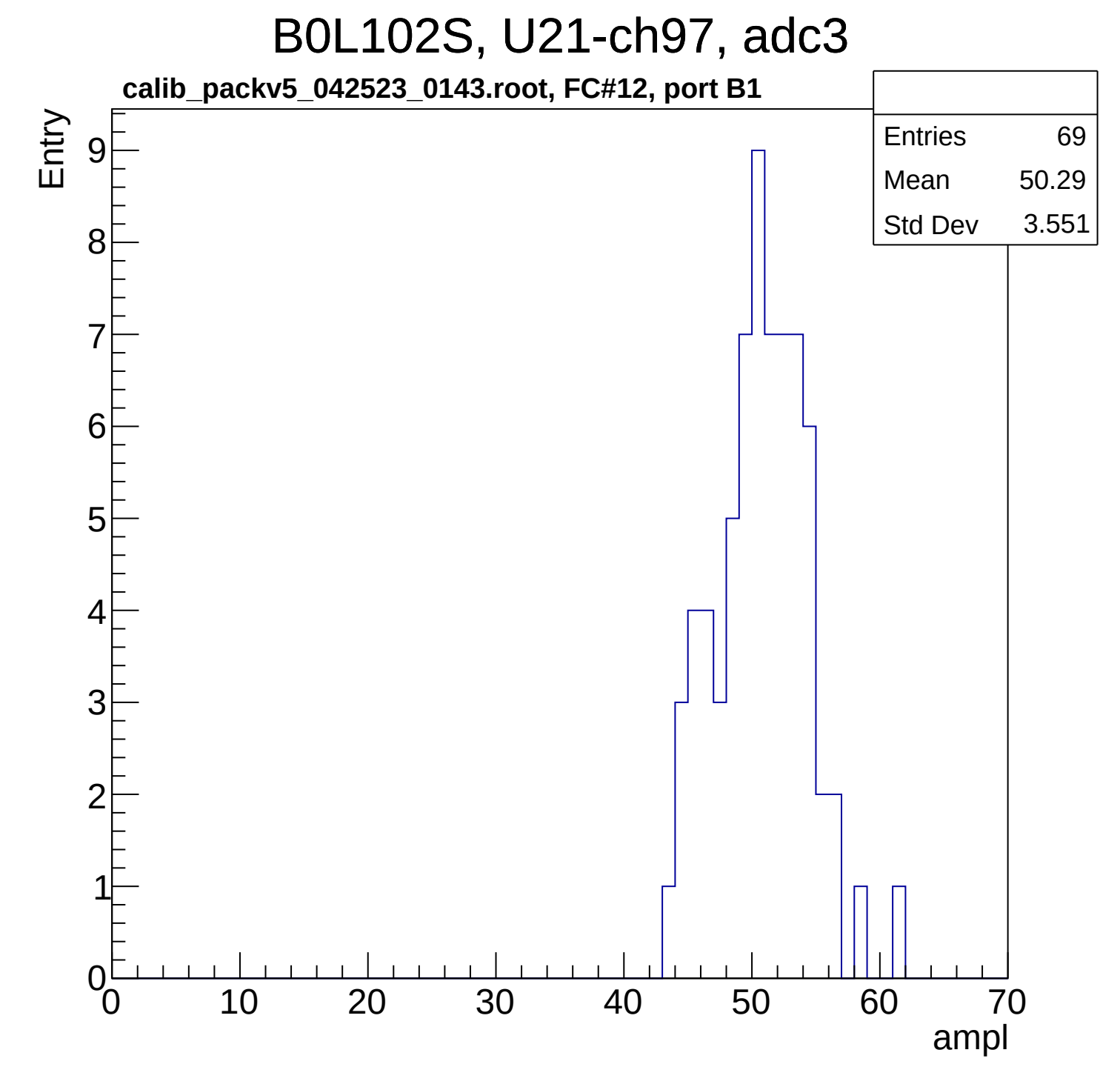
B0L102S, U21-ch97, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 50.29
Std Dev 3.551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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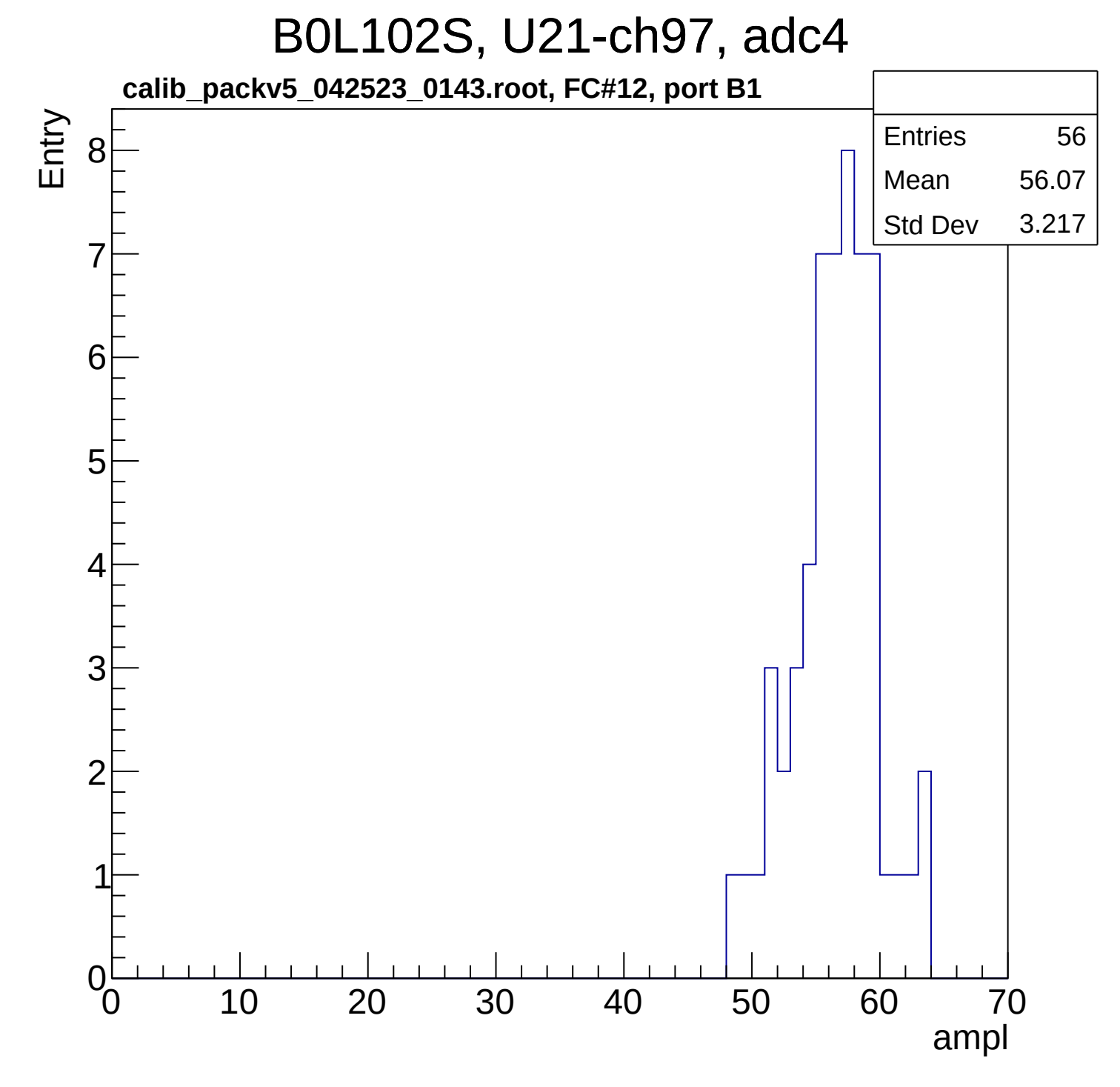
B0L102S, U21-ch97, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 56.07
Std Dev 3.217
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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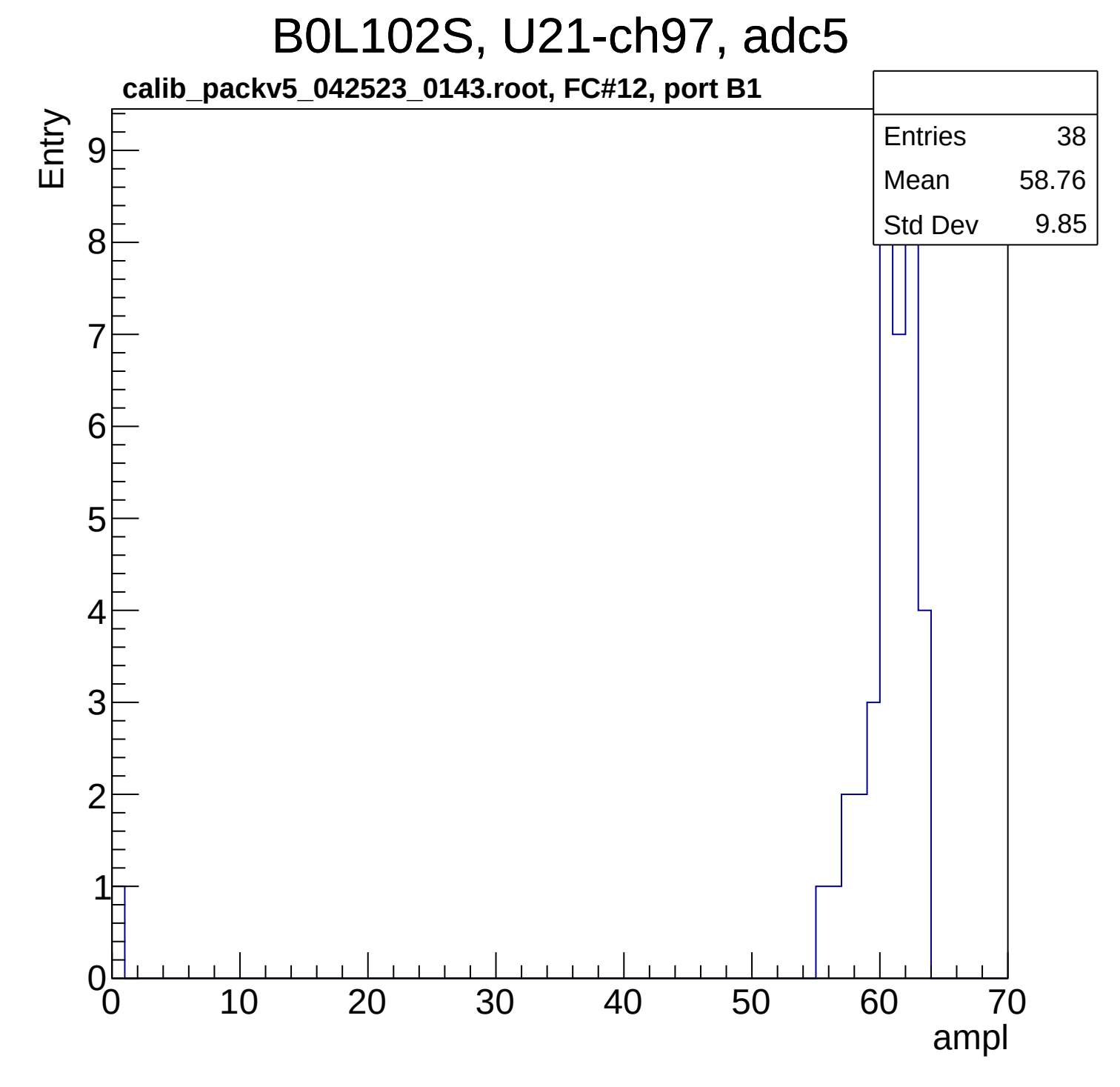
B0L102S, U21-ch97, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.76
Std Dev 9.85
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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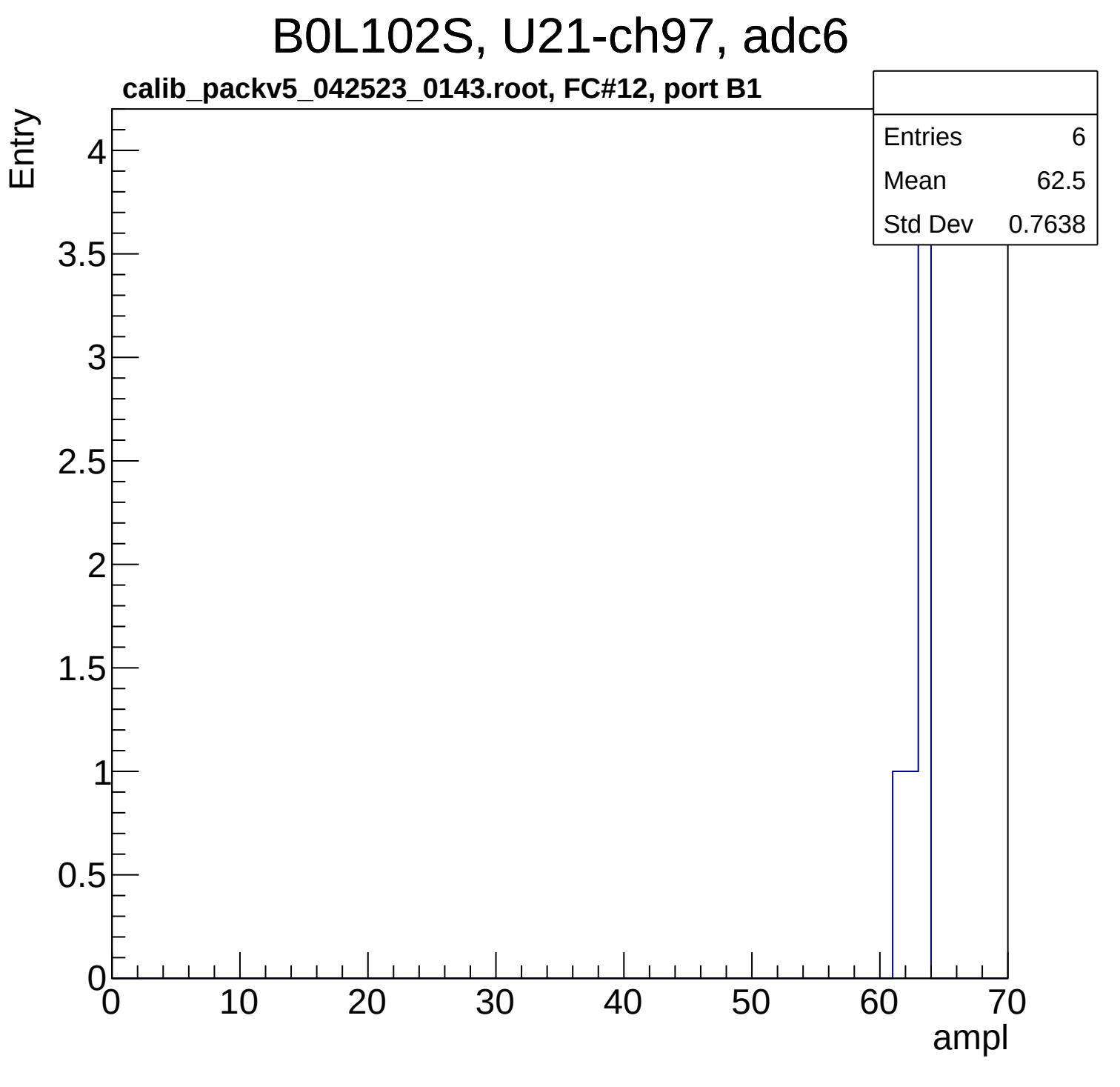
B0L102S, U21-ch97, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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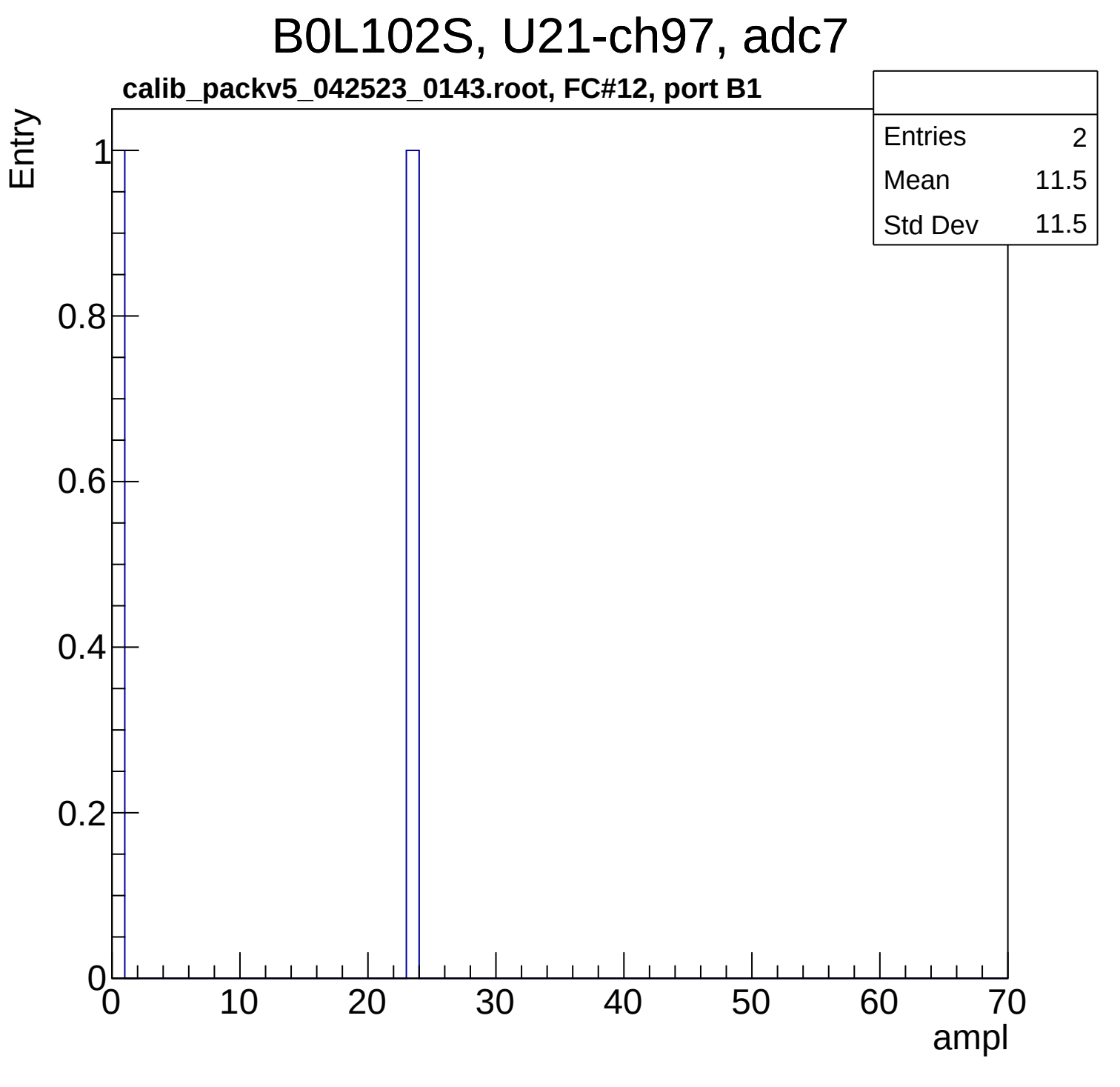
B0L102S, U21-ch97, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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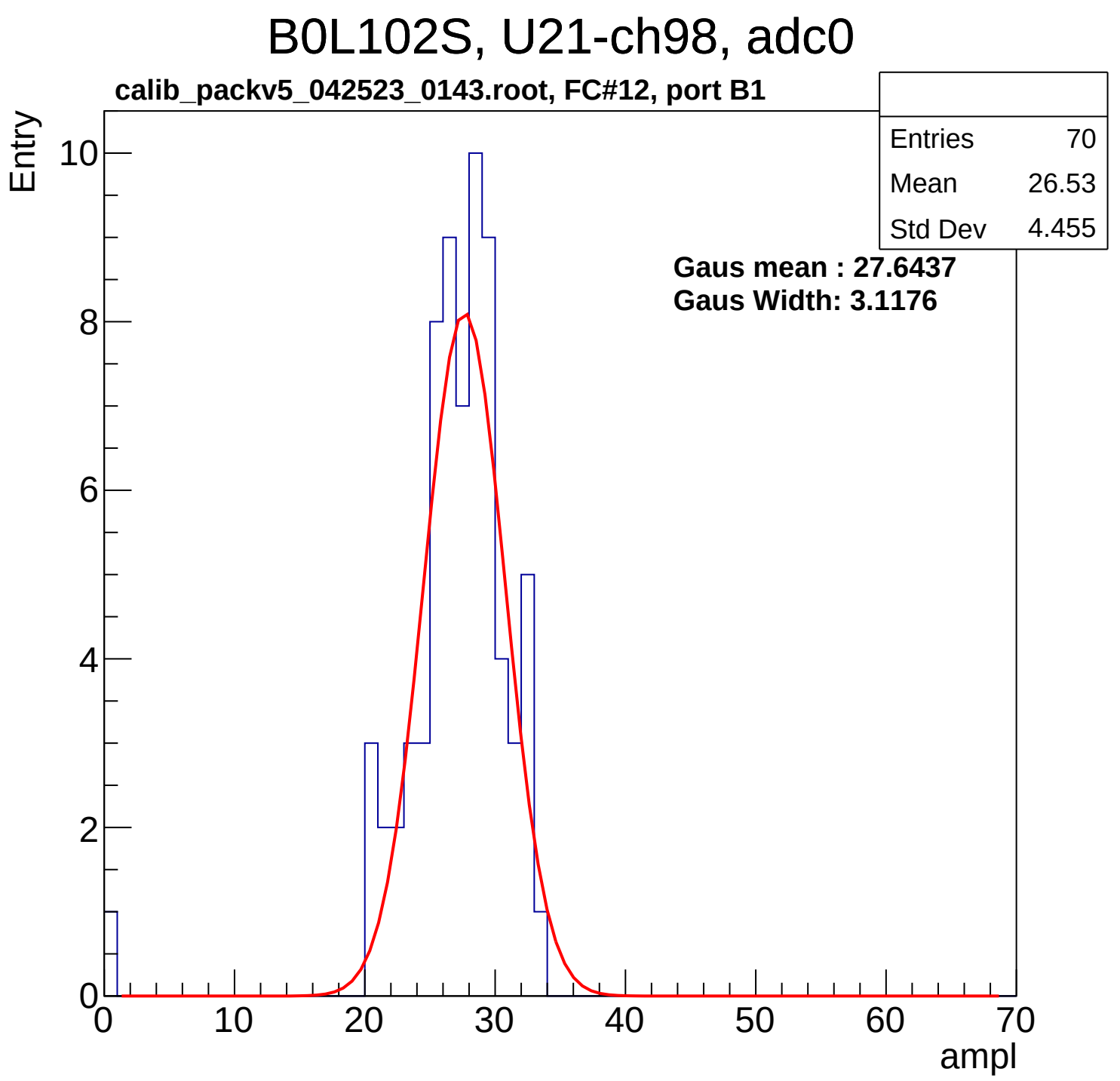
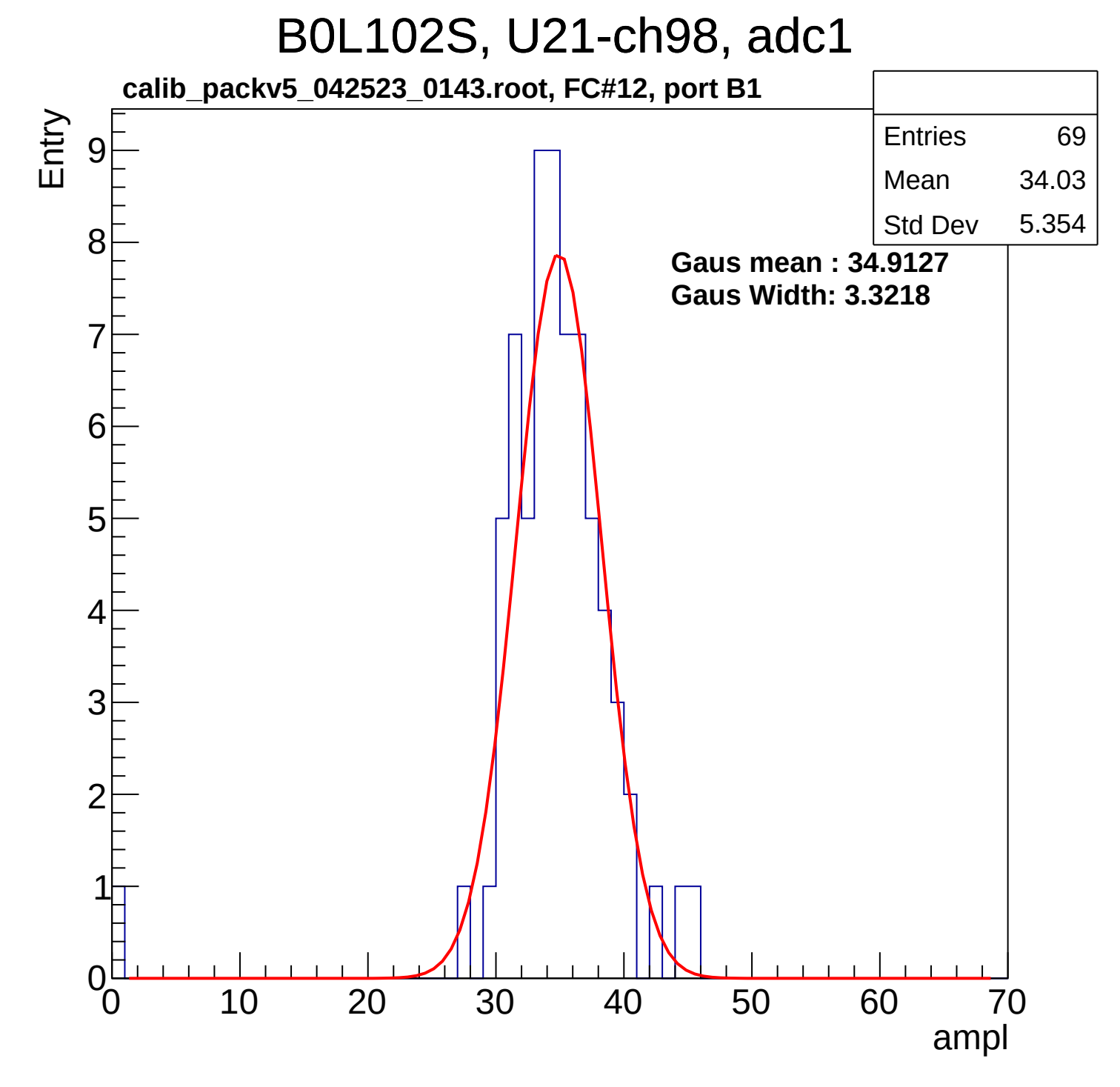
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch98, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 26.53
Std Dev 4.455
Gaus mean : 27.6437
Gaus Width: 3.1176
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch98, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 34.03
Std Dev 5.354
Gaus mean : 34.9127
Gaus Width: 3.3218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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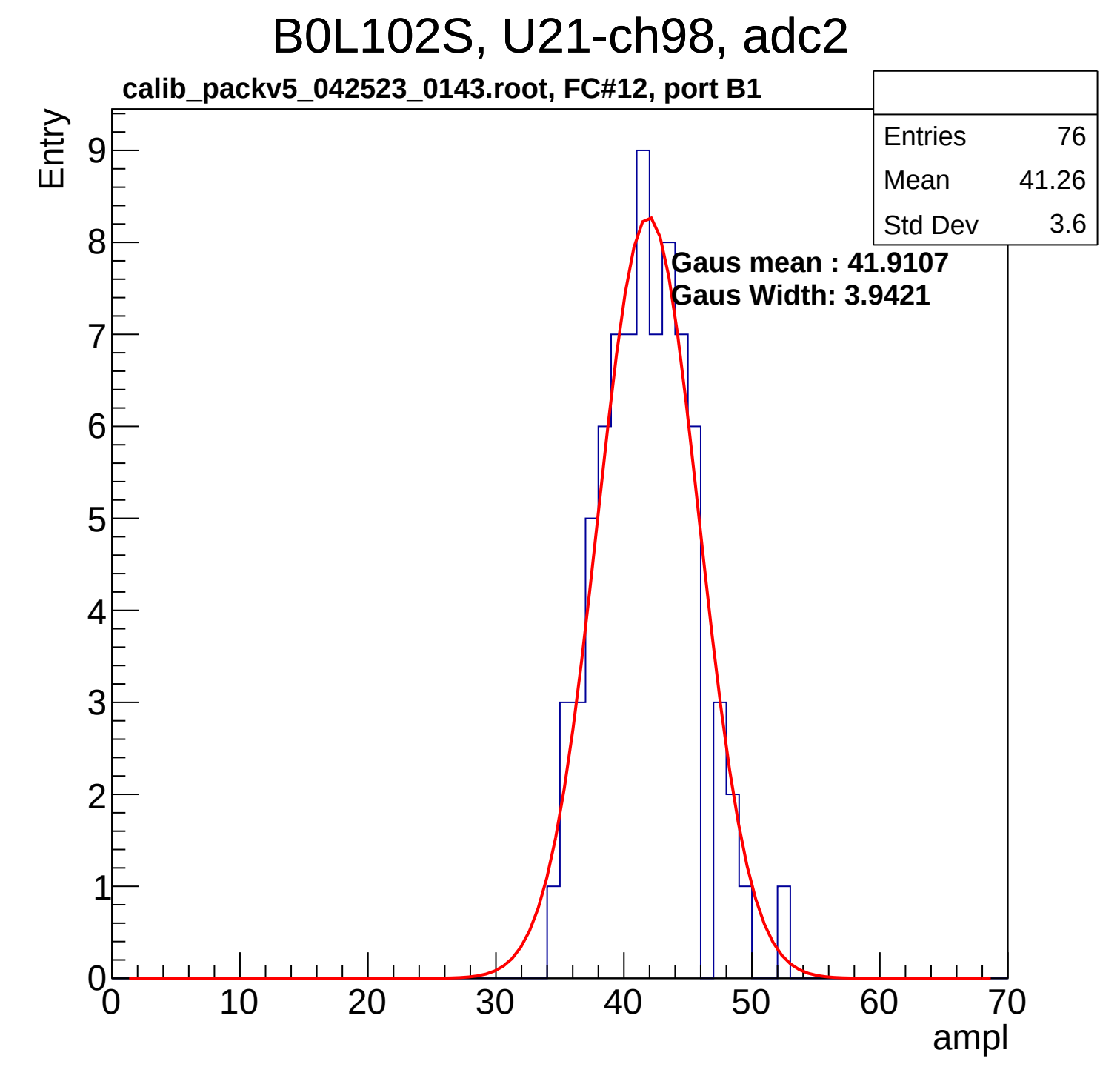
B0L102S, U21-ch98, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 41.26
Std Dev 3.6
Gaus mean : 41.9107
Gaus Width: 3.9421
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
10
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60
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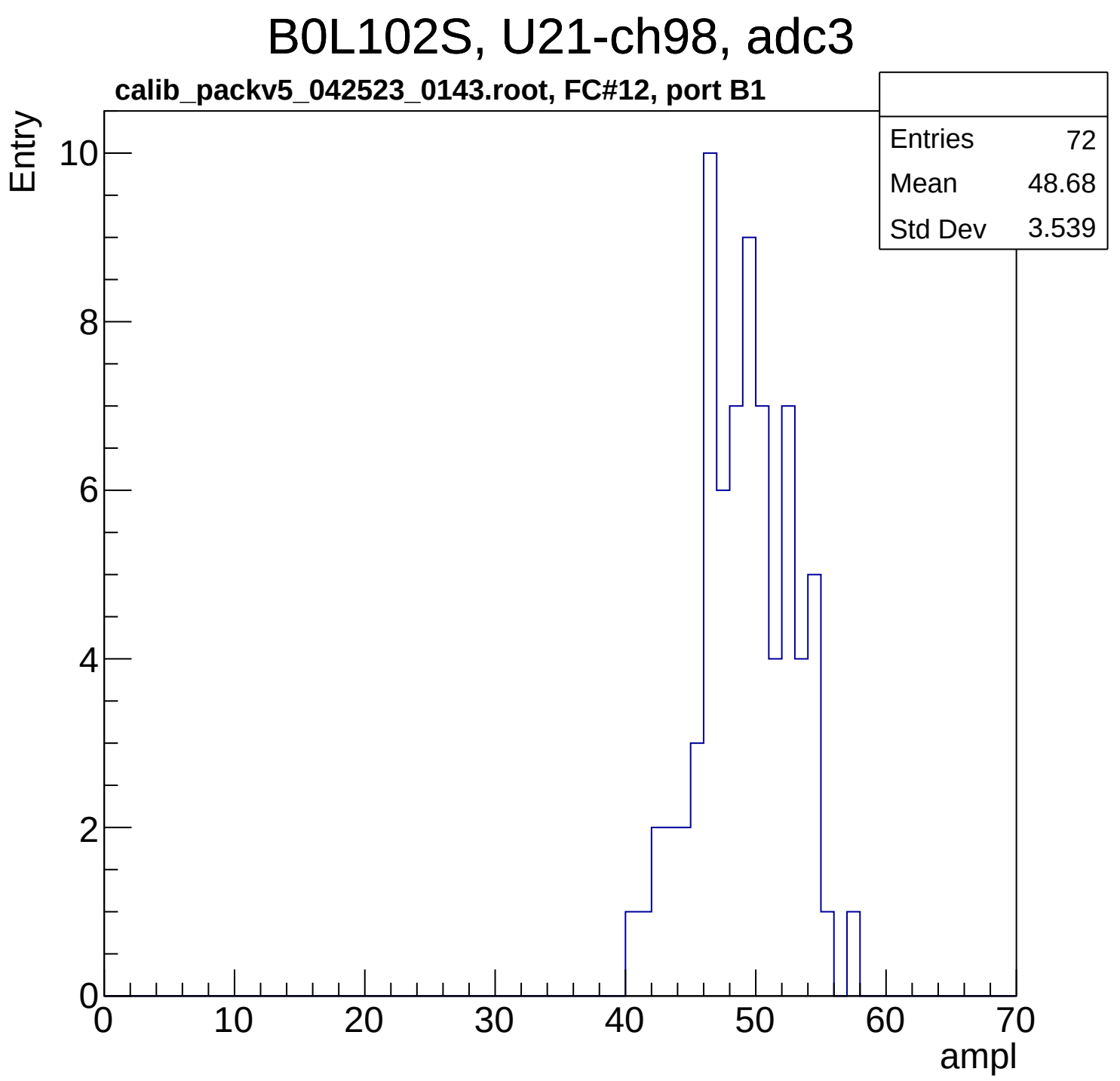
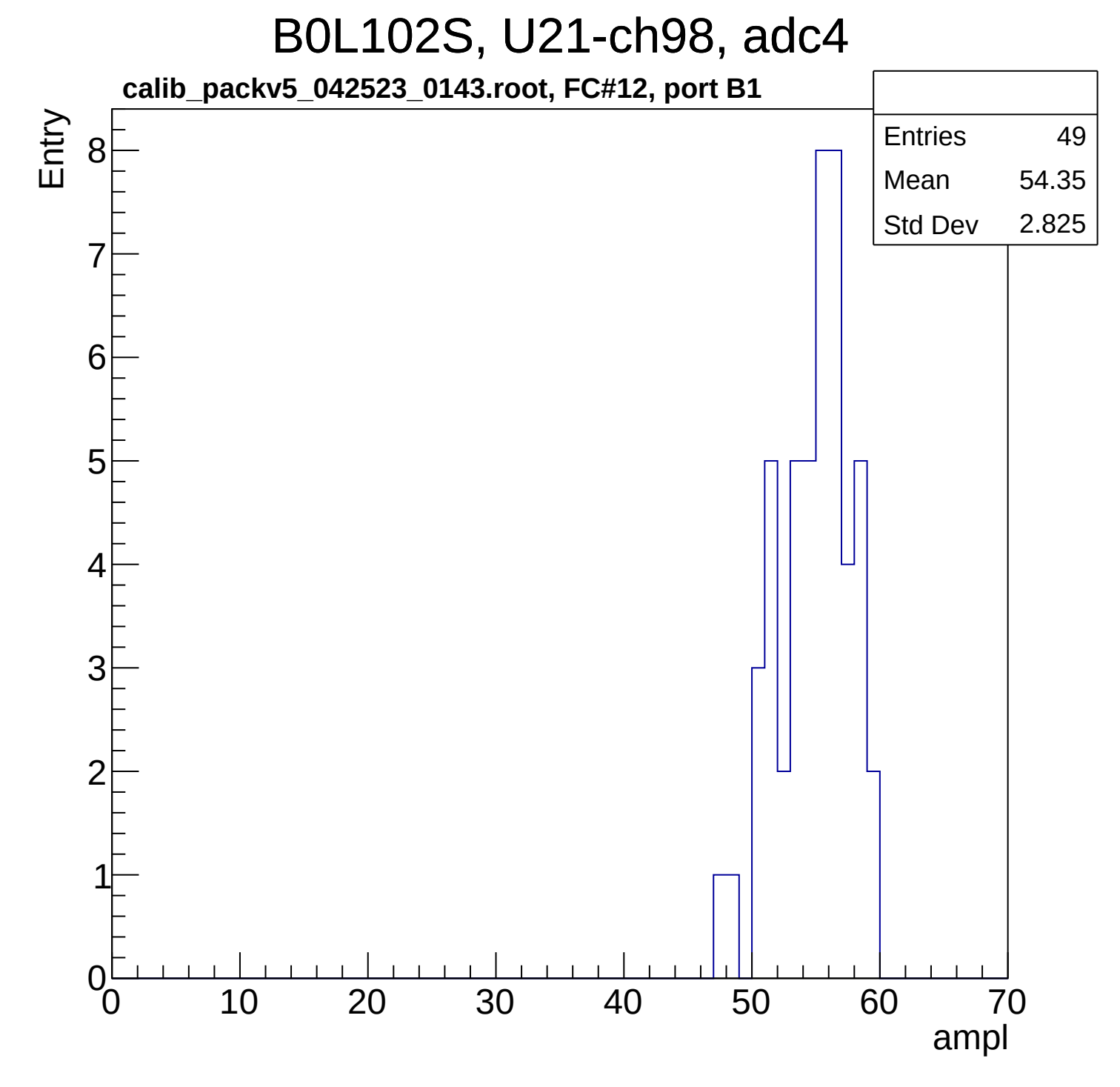
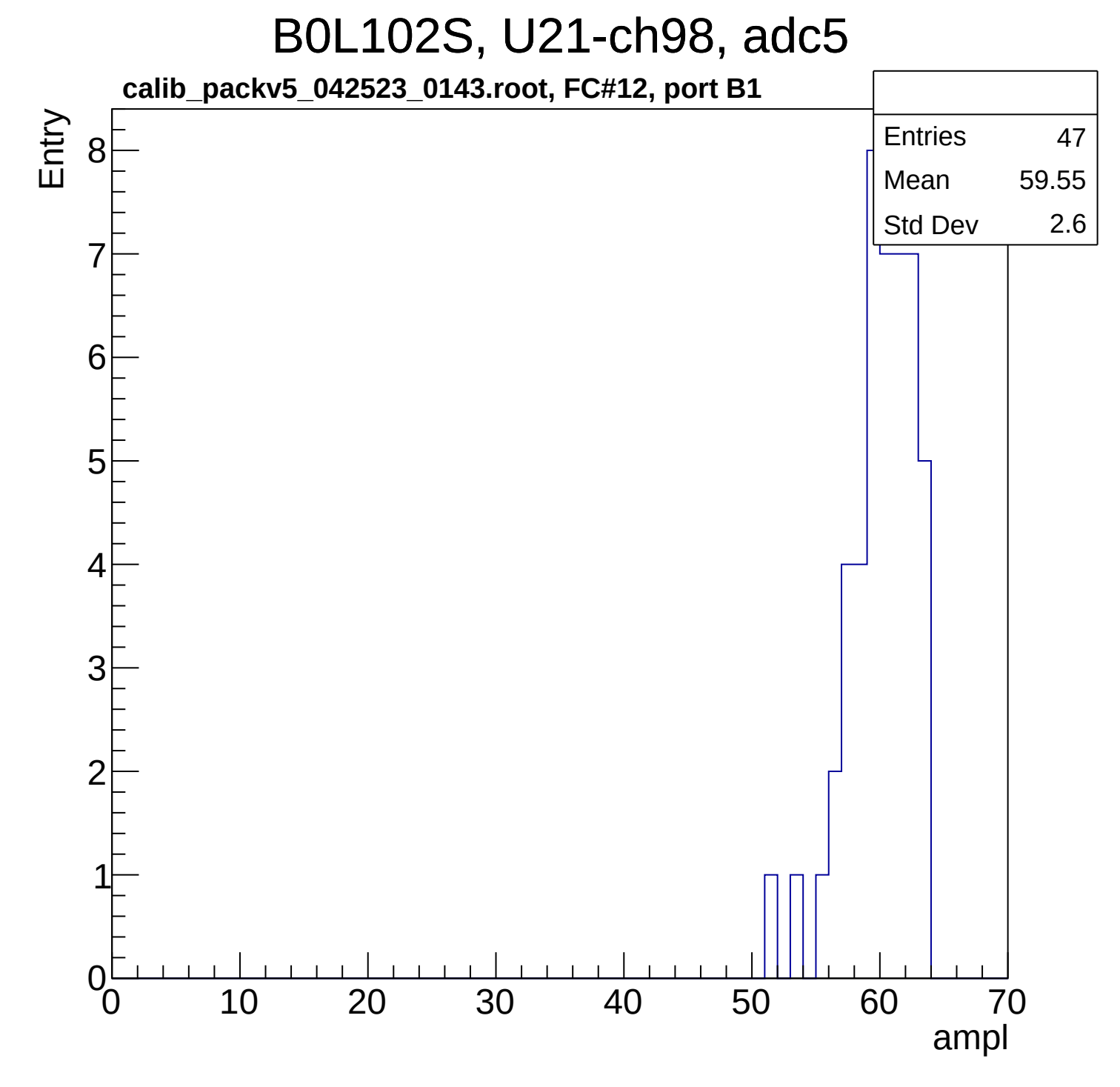
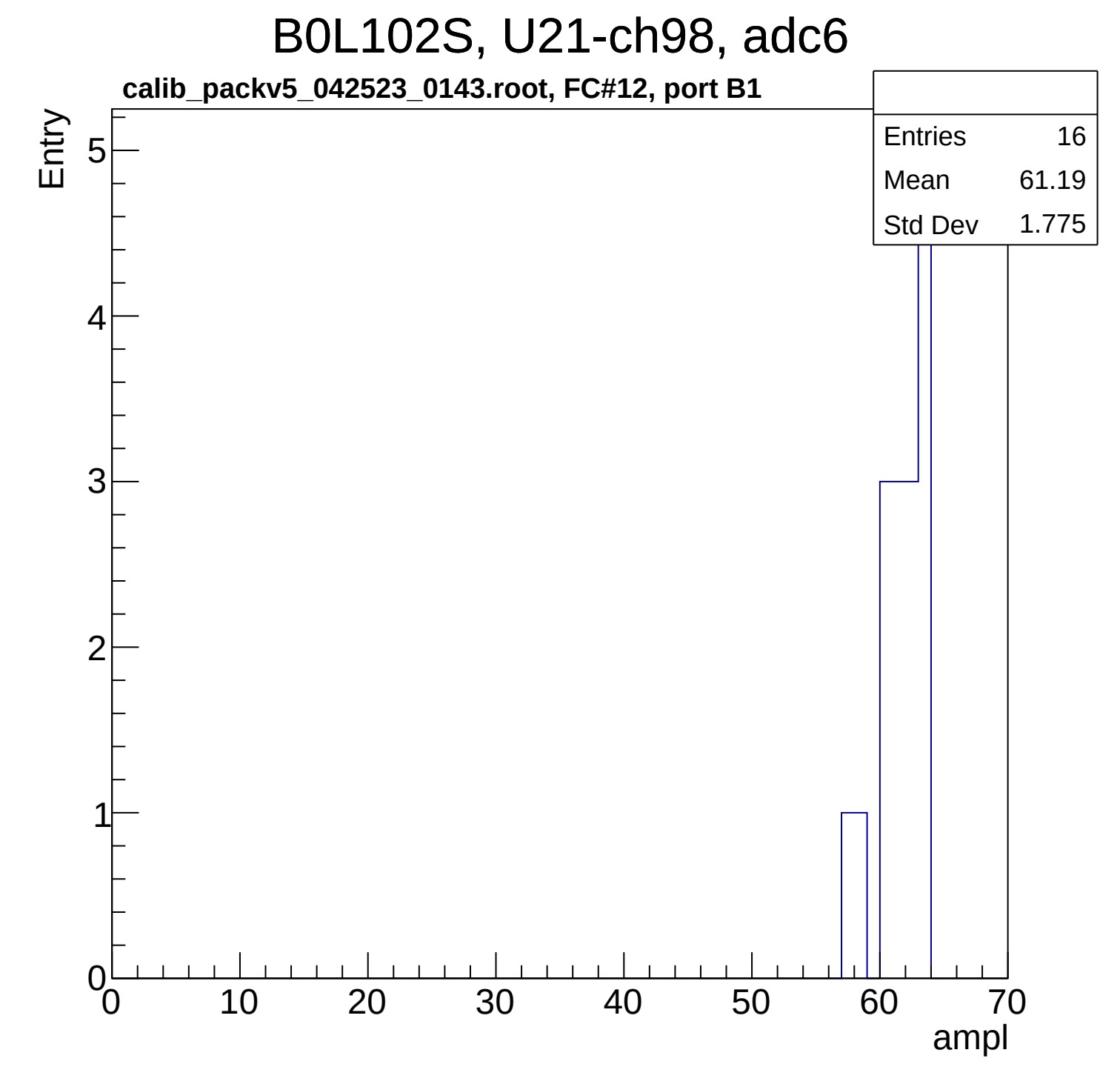

B0L102S, U21-ch98, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 48.68
Std Dev 3.539
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch98, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 54.35
Std Dev 2.825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch98, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.55
Std Dev 2.6
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch98, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 61.19
Std Dev 1.775
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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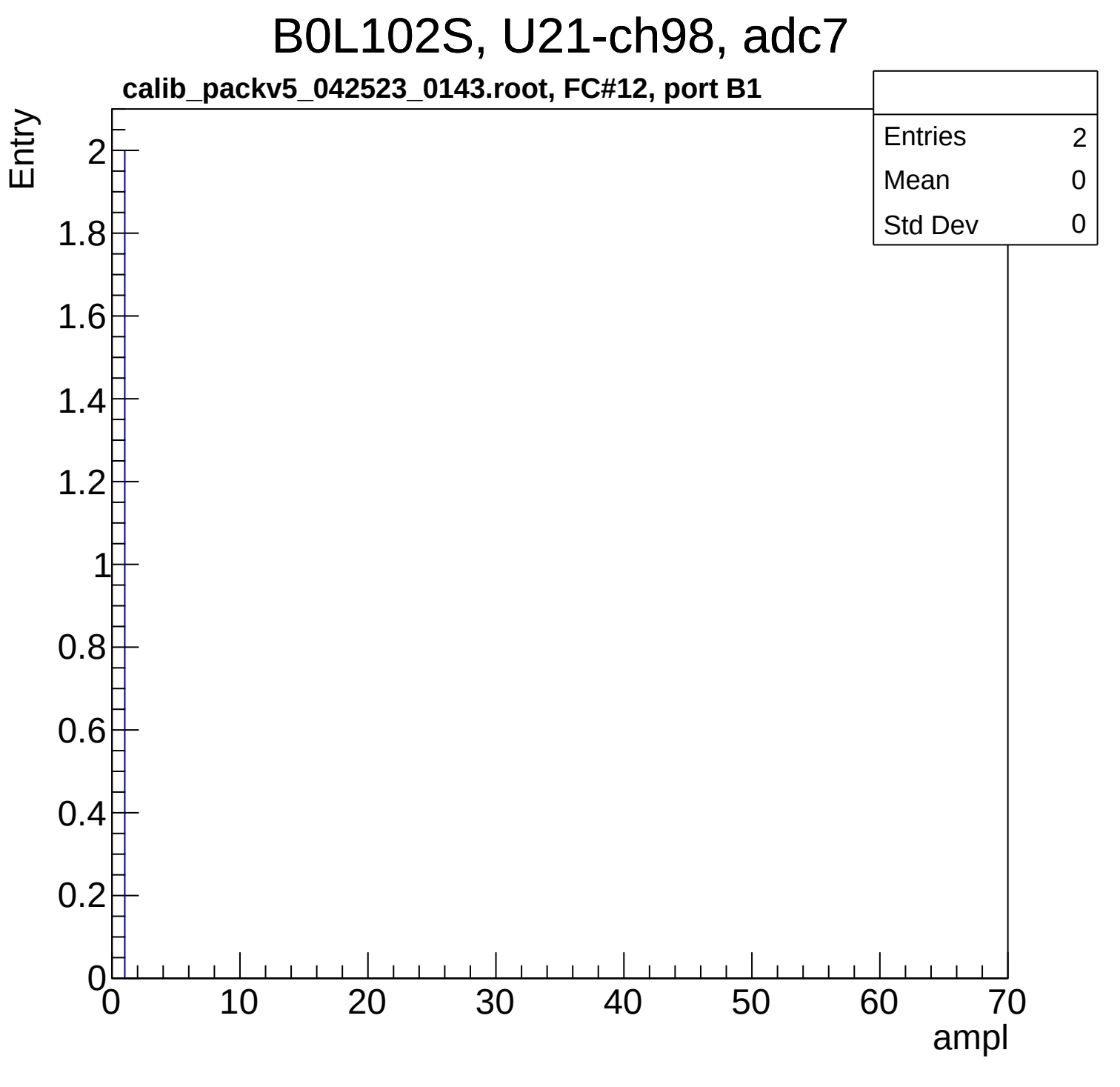
B0L102S, U21-ch98, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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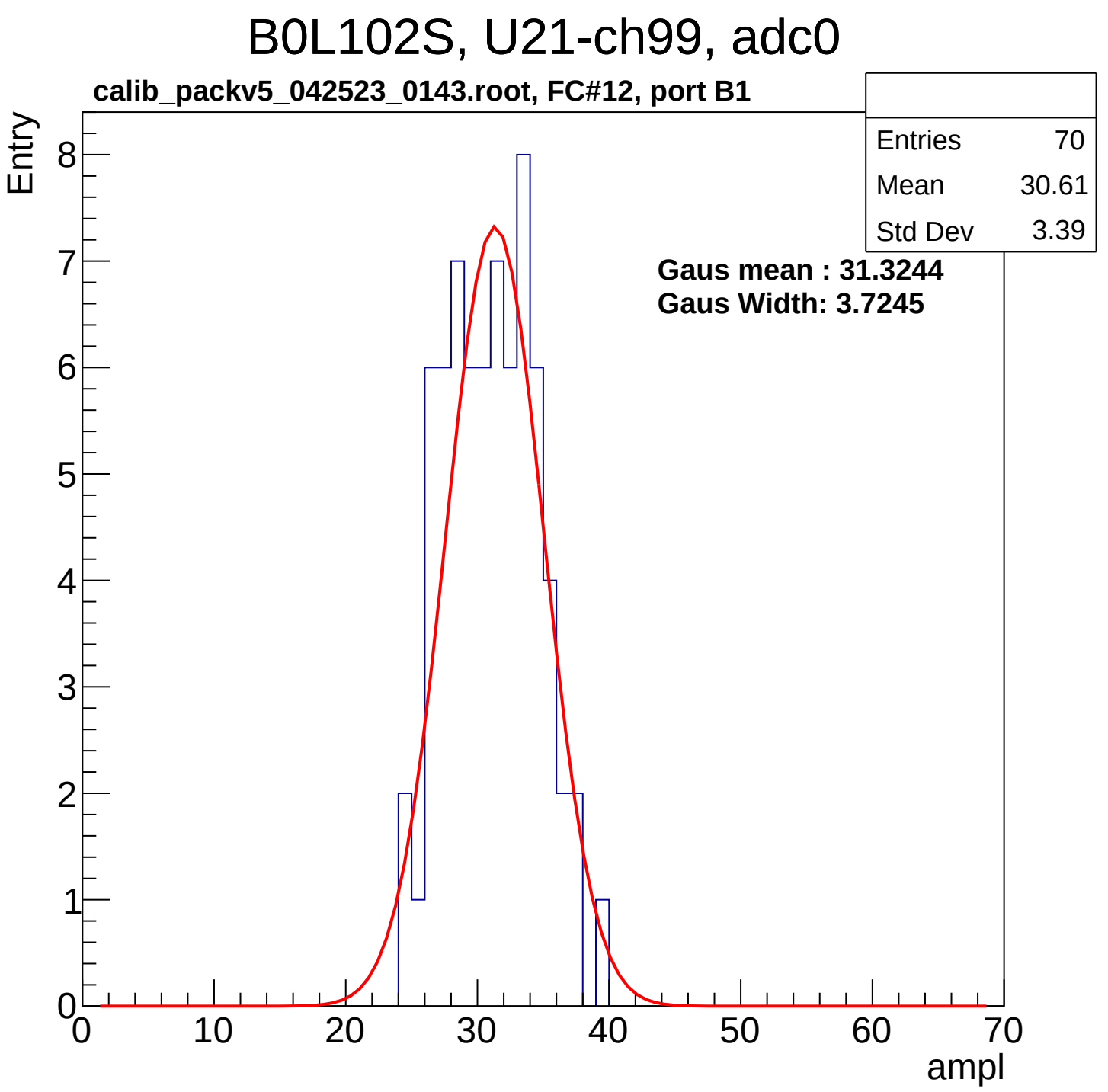
B0L102S, U21-ch99, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 30.61
Std Dev 3.39
Gaus mean : 31.3244
Gaus Width: 3.7245
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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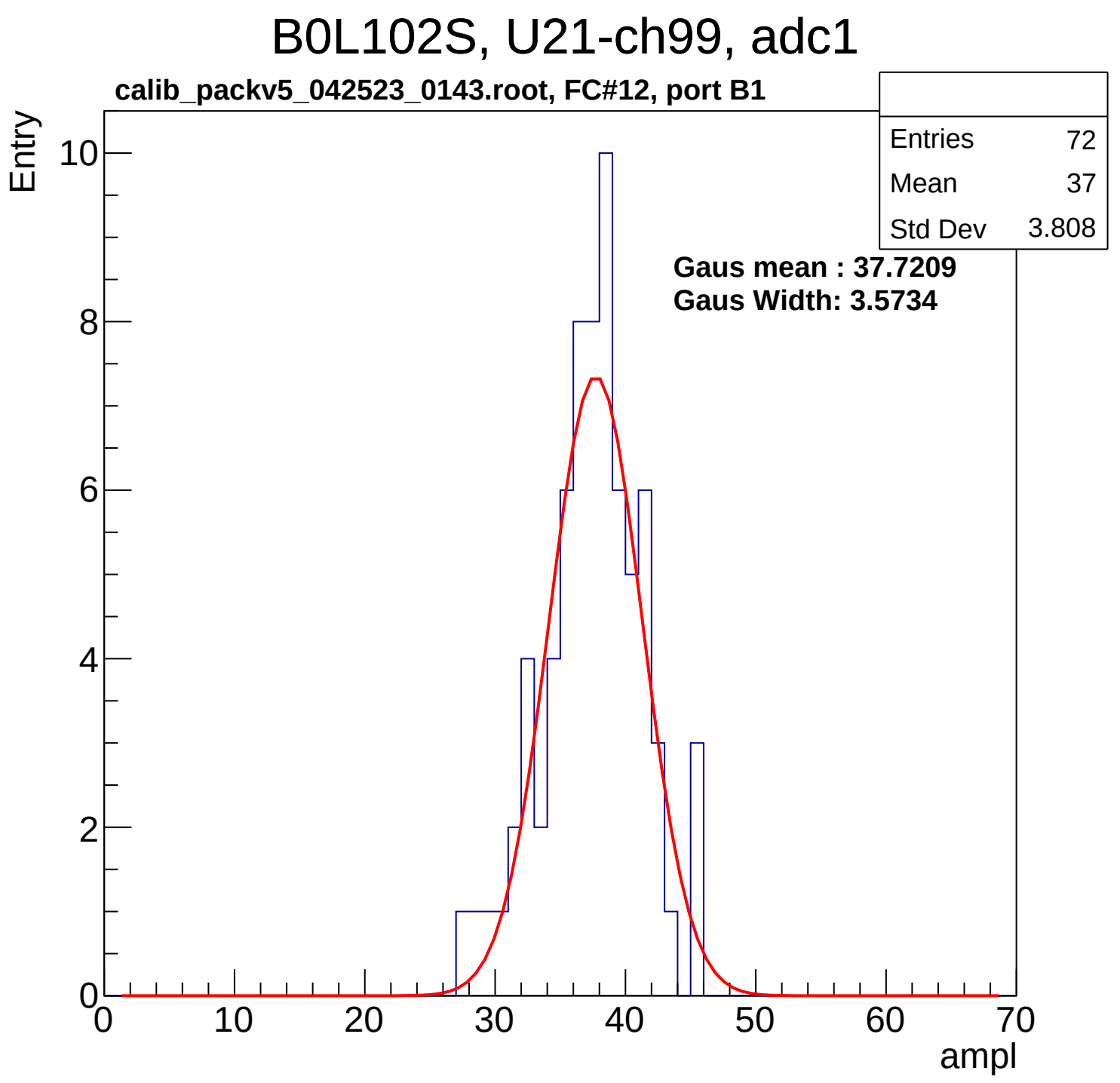
40
50
60
70


B0L102S, U21-ch99, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 37
Std Dev 3.808
Gaus mean : 37.7209
Gaus Width: 3.5734
Entry
ampl
0
2
4
6
8
10
0
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20
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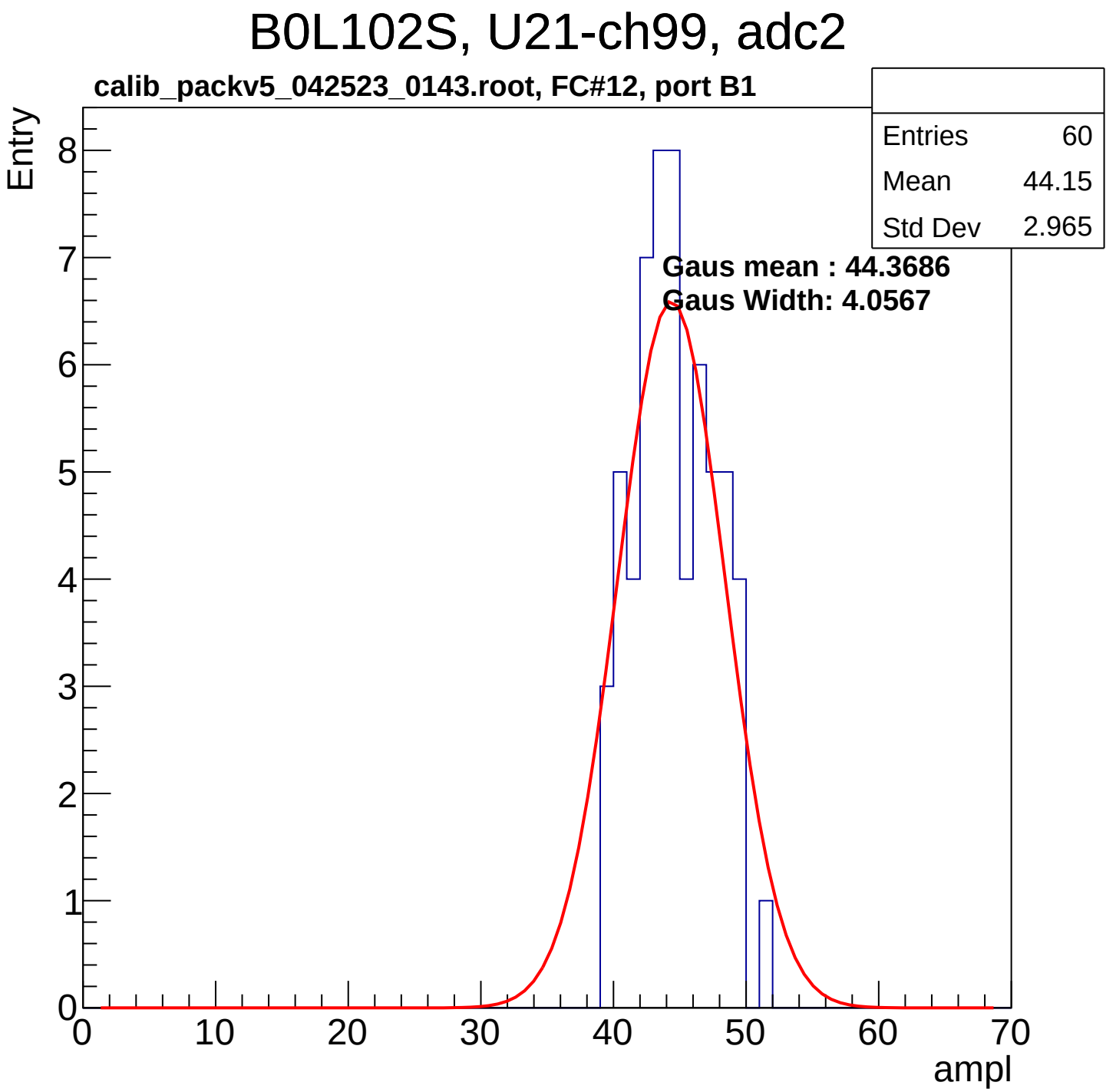
40
50
60
70


B0L102S, U21-ch99, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 44.15
Std Dev 2.965
Gaus mean : 44.3686
Gaus Width: 4.0567
Entry
ampl
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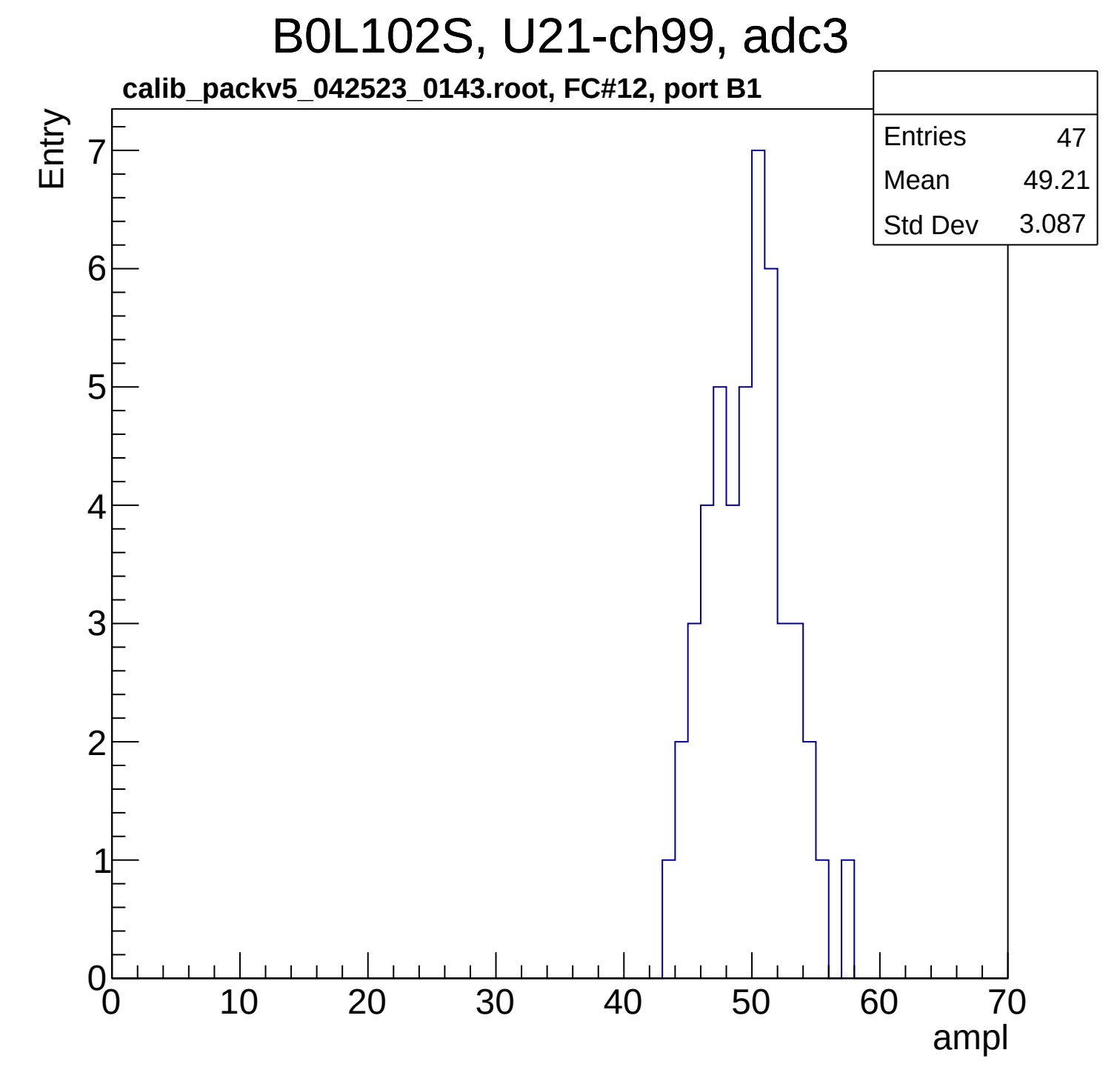
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch99, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 49.21
Std Dev 3.087
Entry
ampl
0
1
2
3
4
5
6
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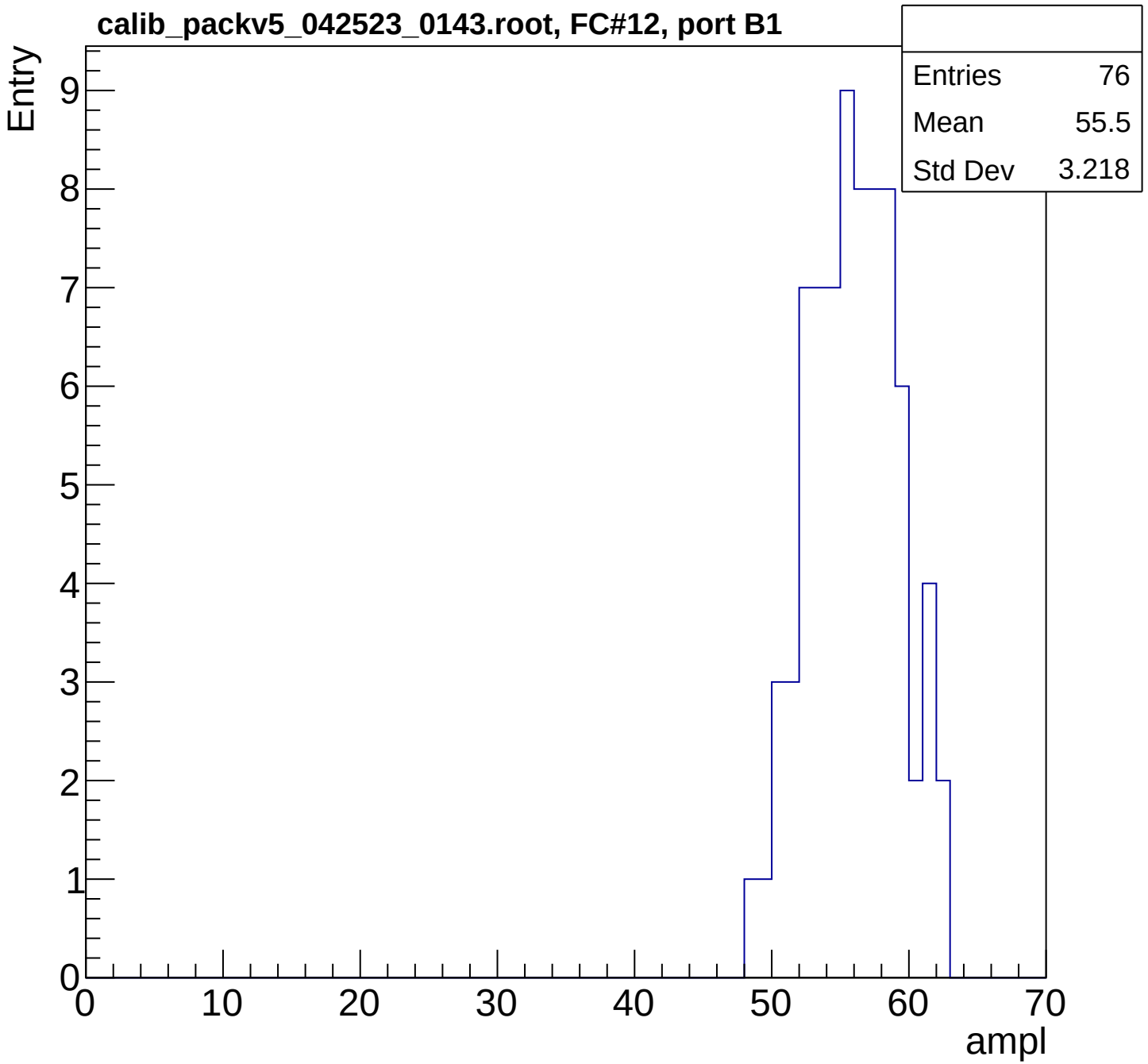
0
10
20
30
40
50
60
70


B0L102S, U21-ch99, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 55.5
Std Dev 3.218
Entry
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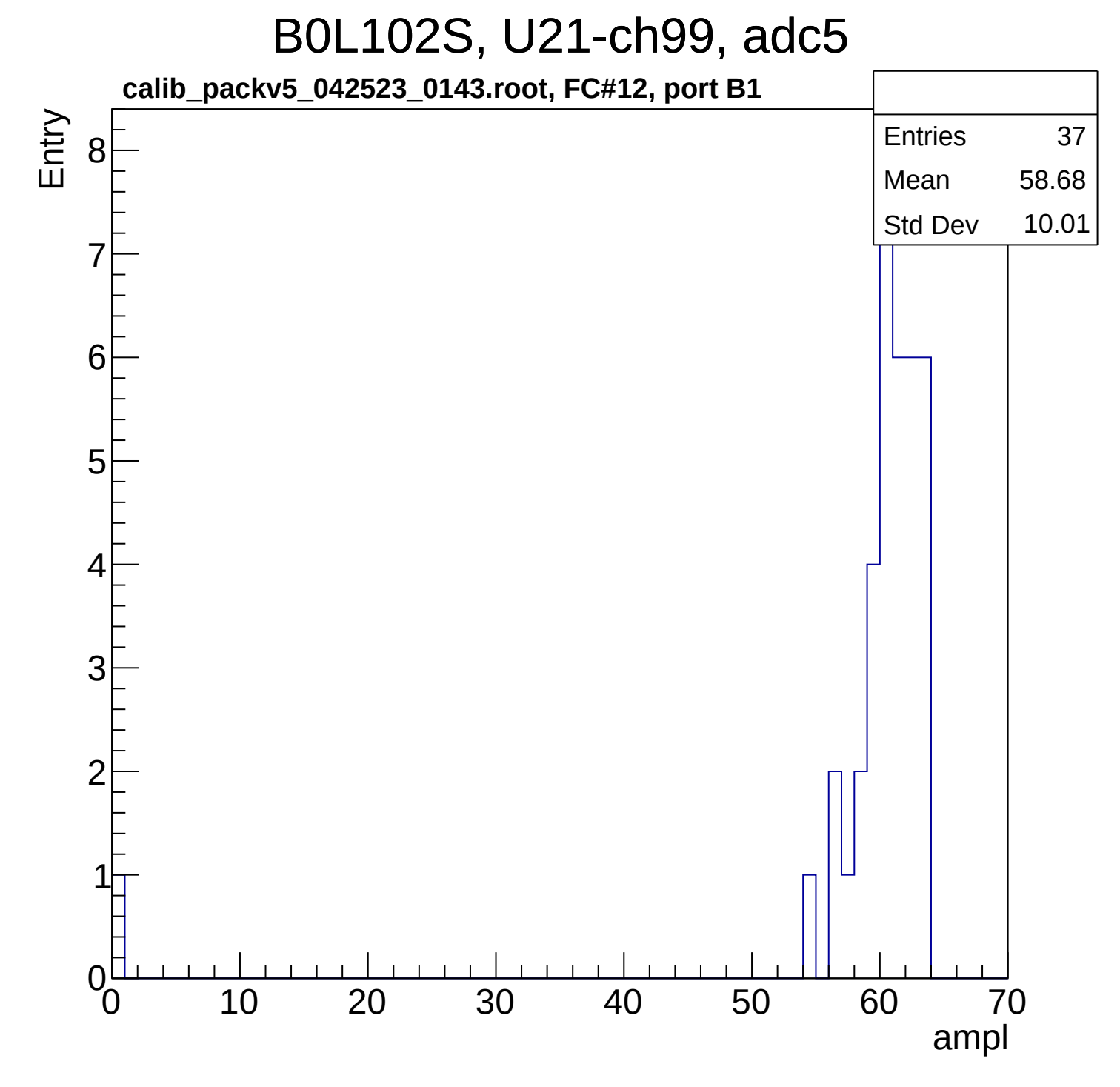
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch99, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 58.68
Std Dev 10.01
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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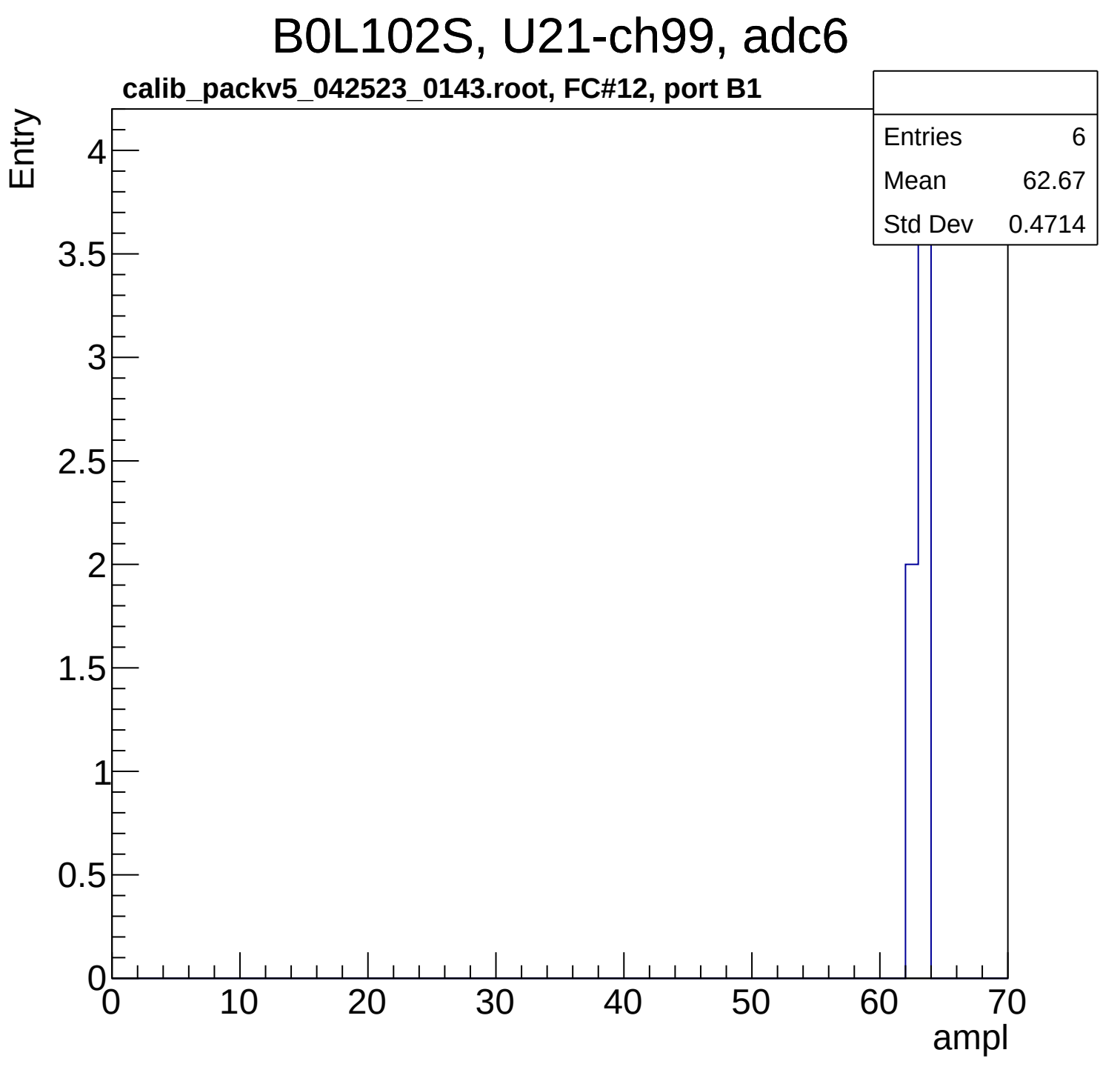
B0L102S, U21-ch99, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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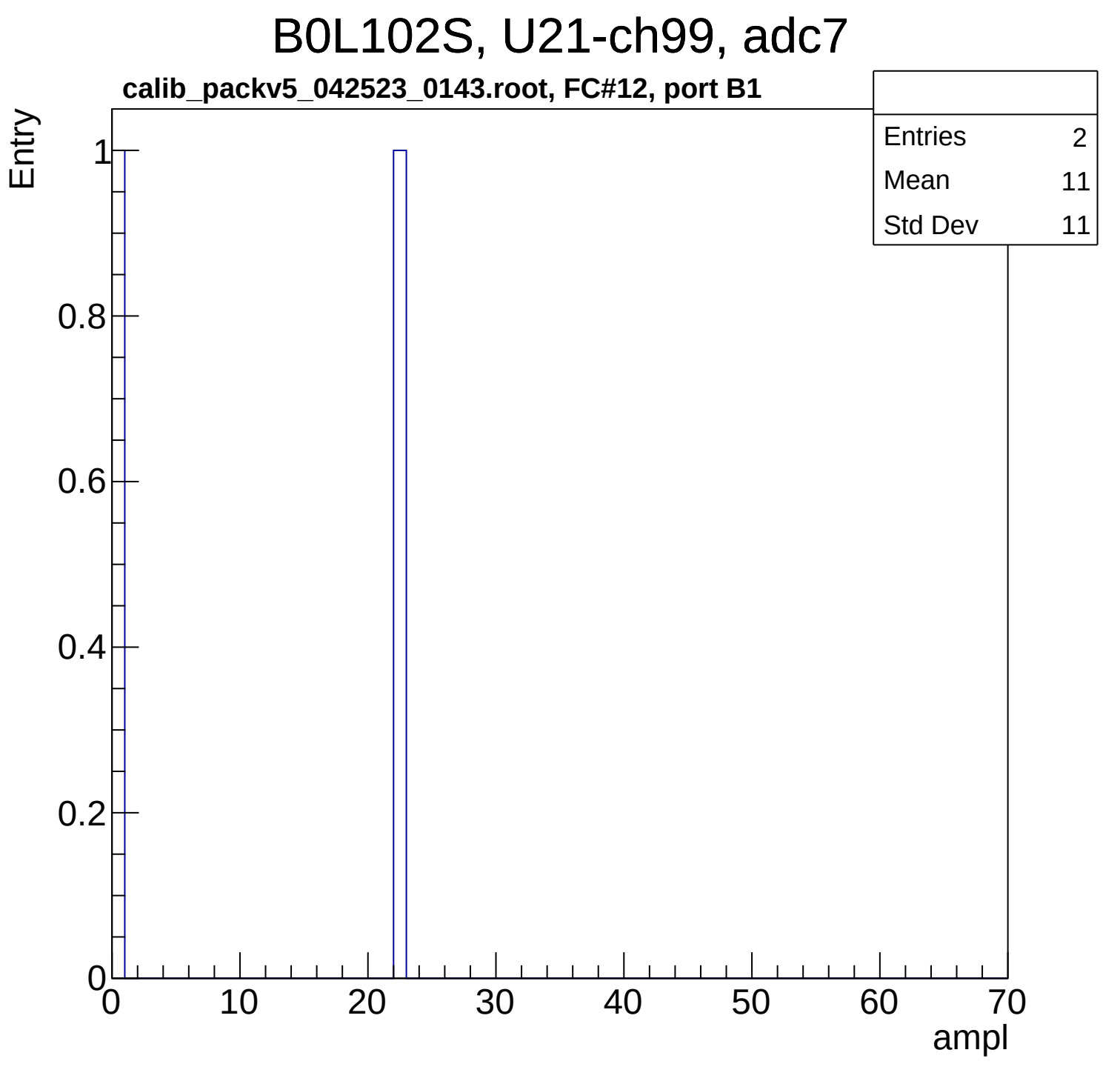
B0L102S, U21-ch99, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
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0
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10
20
30
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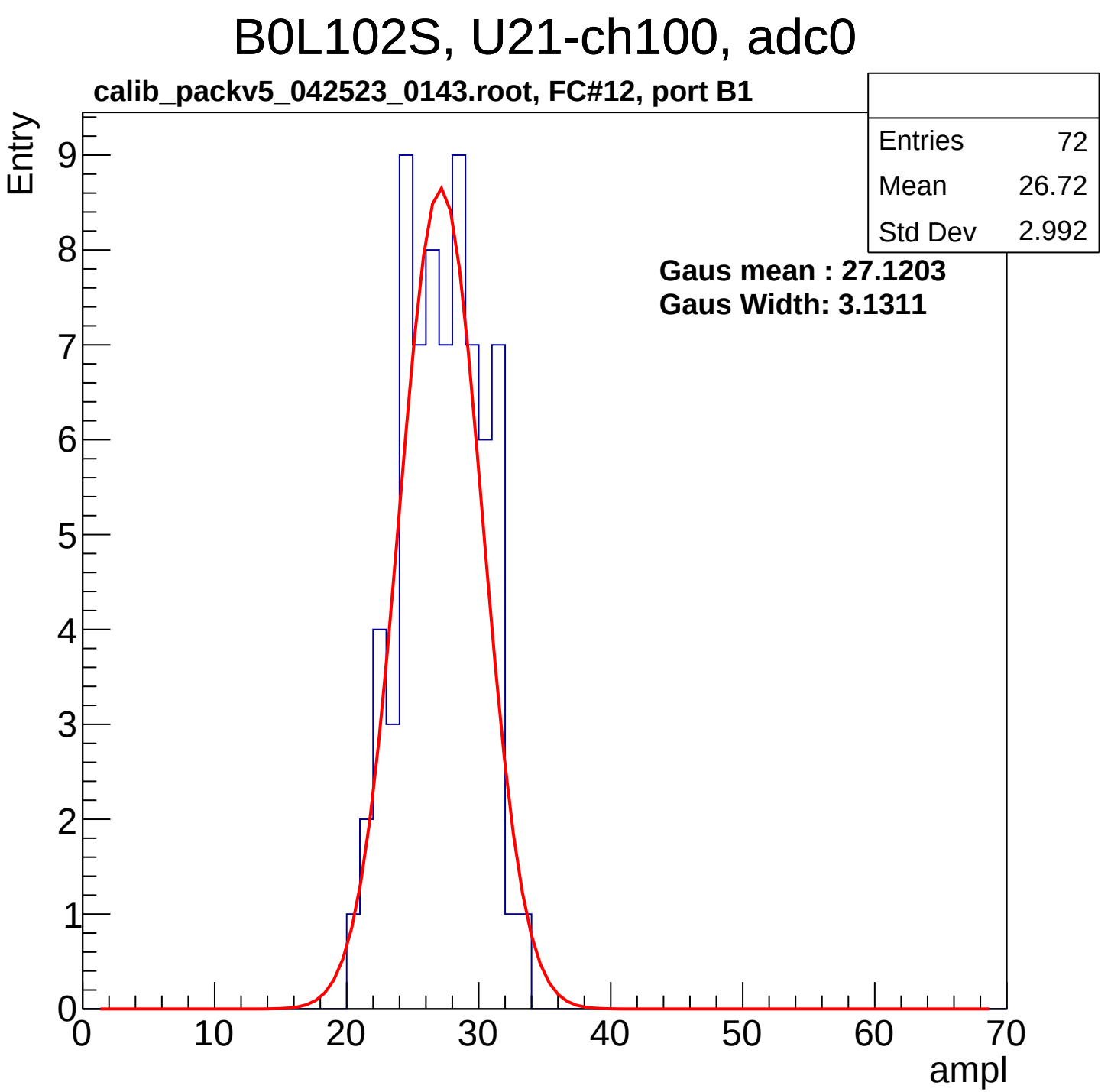
50
60
70
ampl


B0L102S, U21-ch100, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 26.72
Std Dev 2.992
Gaus mean : 27.1203
Gaus Width: 3.1311
Entry
ampl
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2
3
4
5
6
7
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9
0
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40
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60
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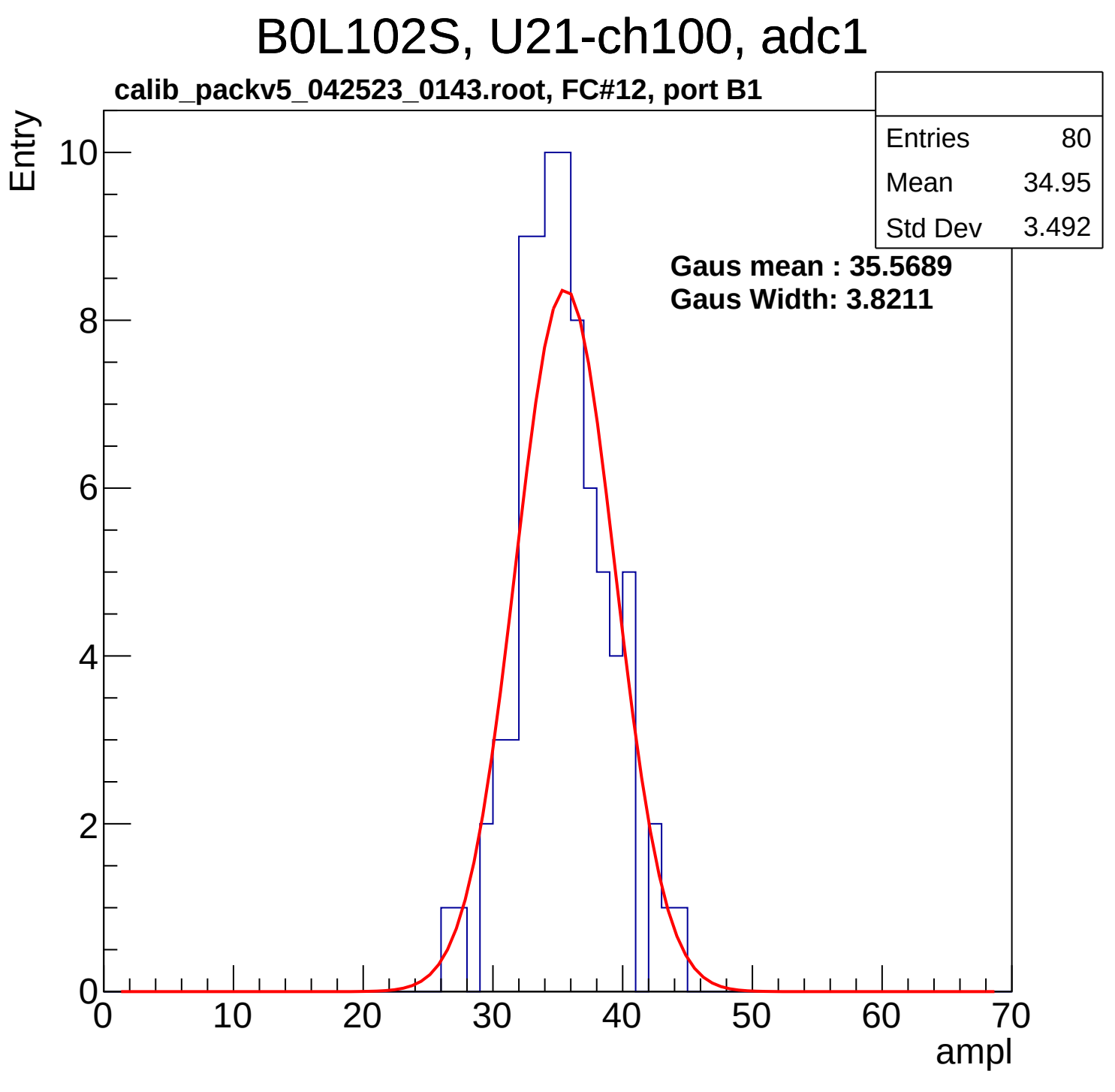
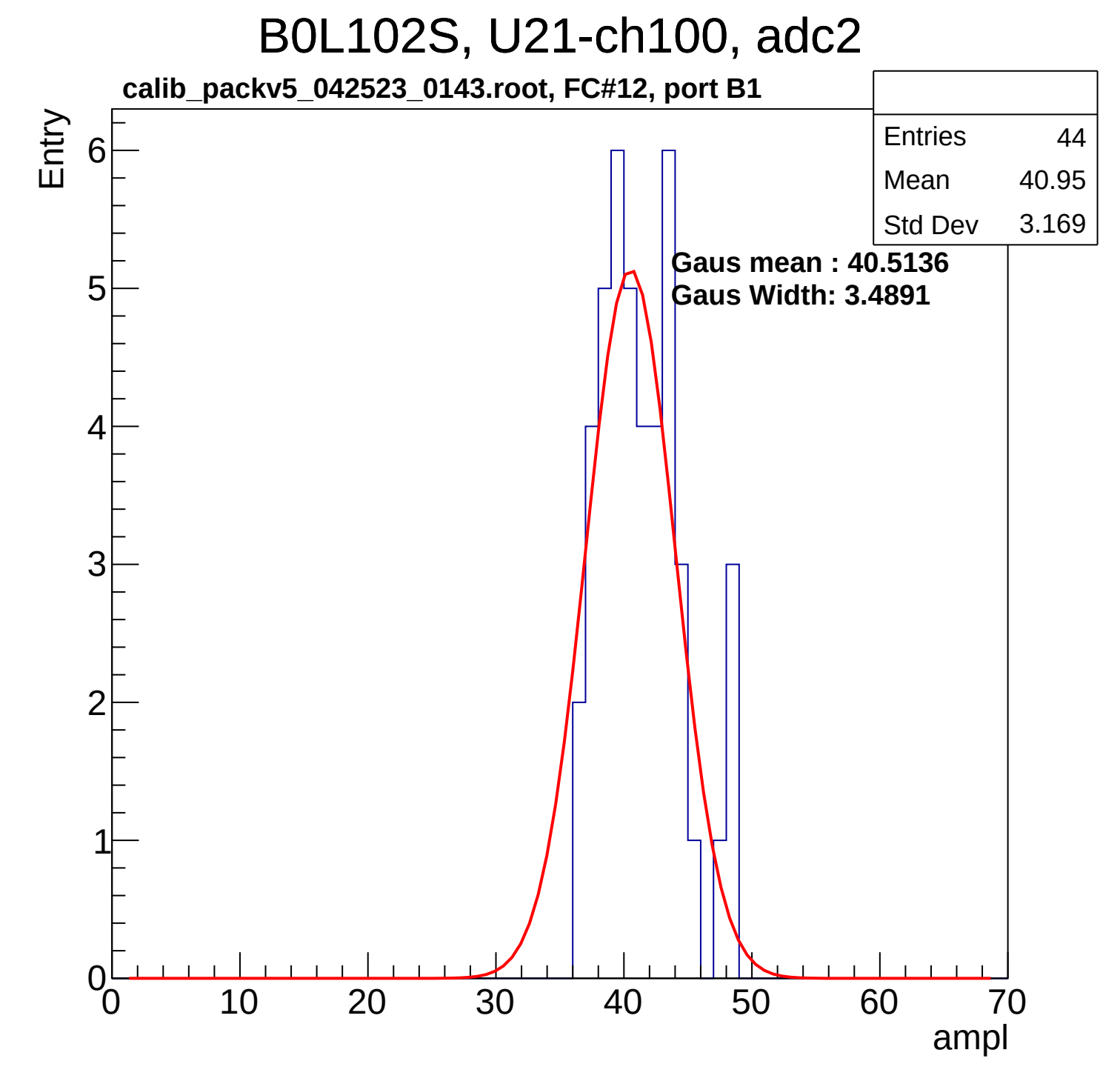

B0L102S, U21-ch100, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 34.95
Std Dev 3.492
Gaus mean : 35.5689
Gaus Width: 3.8211
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch100, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 40.95
Std Dev 3.169
Gaus mean : 40.5136
Gaus Width: 3.4891
Entry
ampl
0
1
2
3
4
5
6
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40
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60
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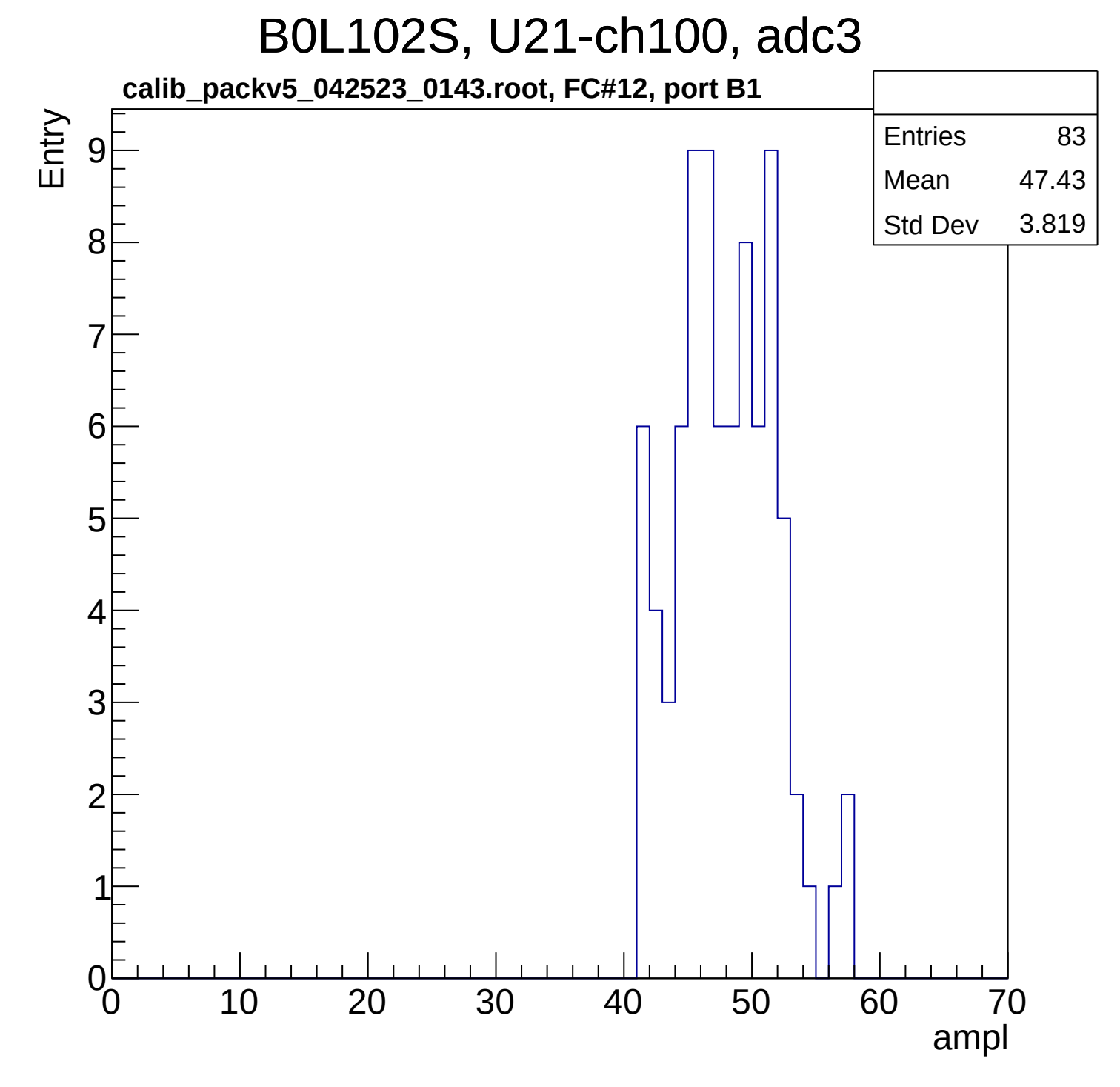
B0L102S, U21-ch100, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 47.43
Std Dev 3.819
Entry
ampl
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5
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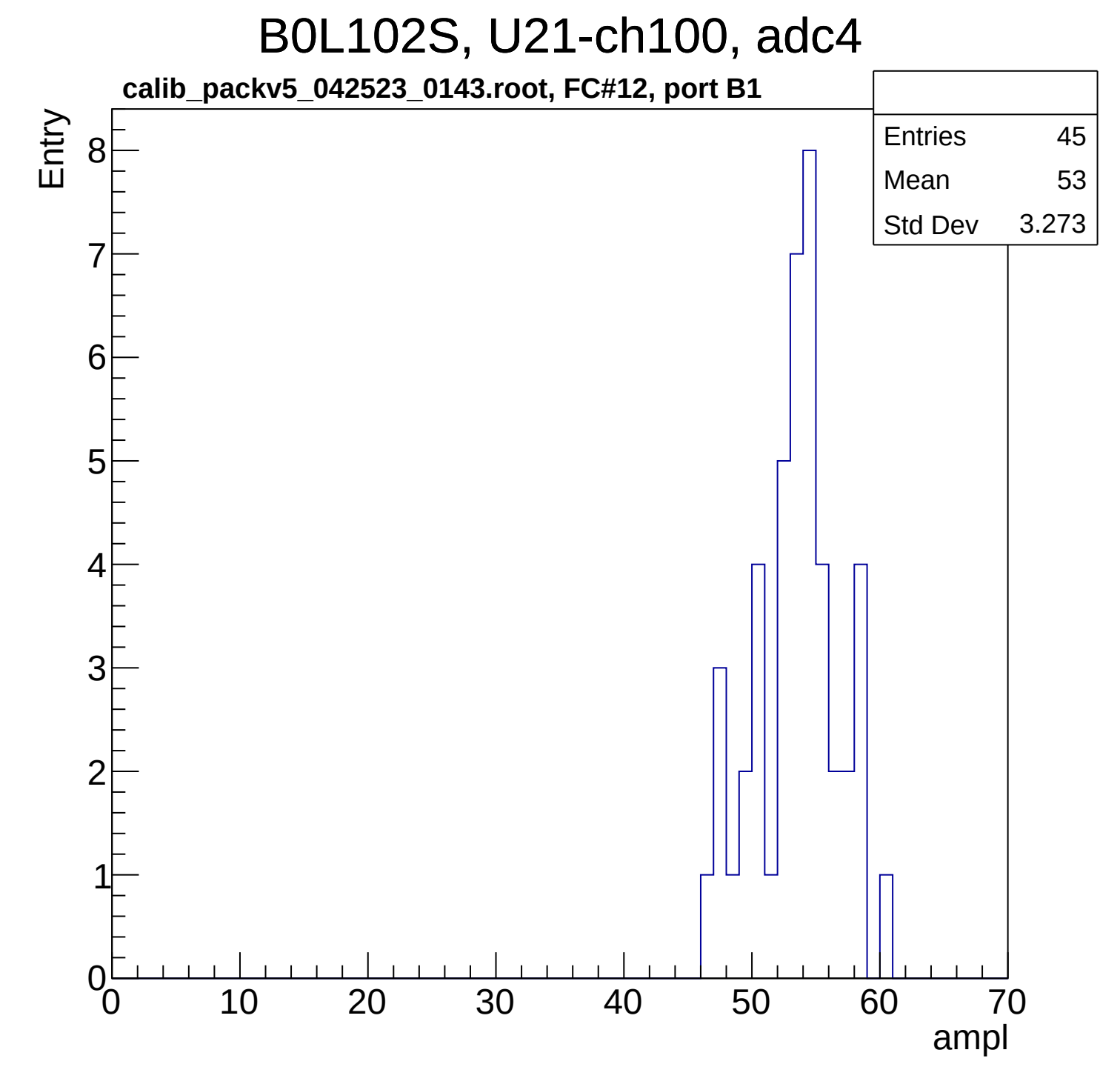
B0L102S, U21-ch100, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 53
Std Dev 3.273
Entry
ampl
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3
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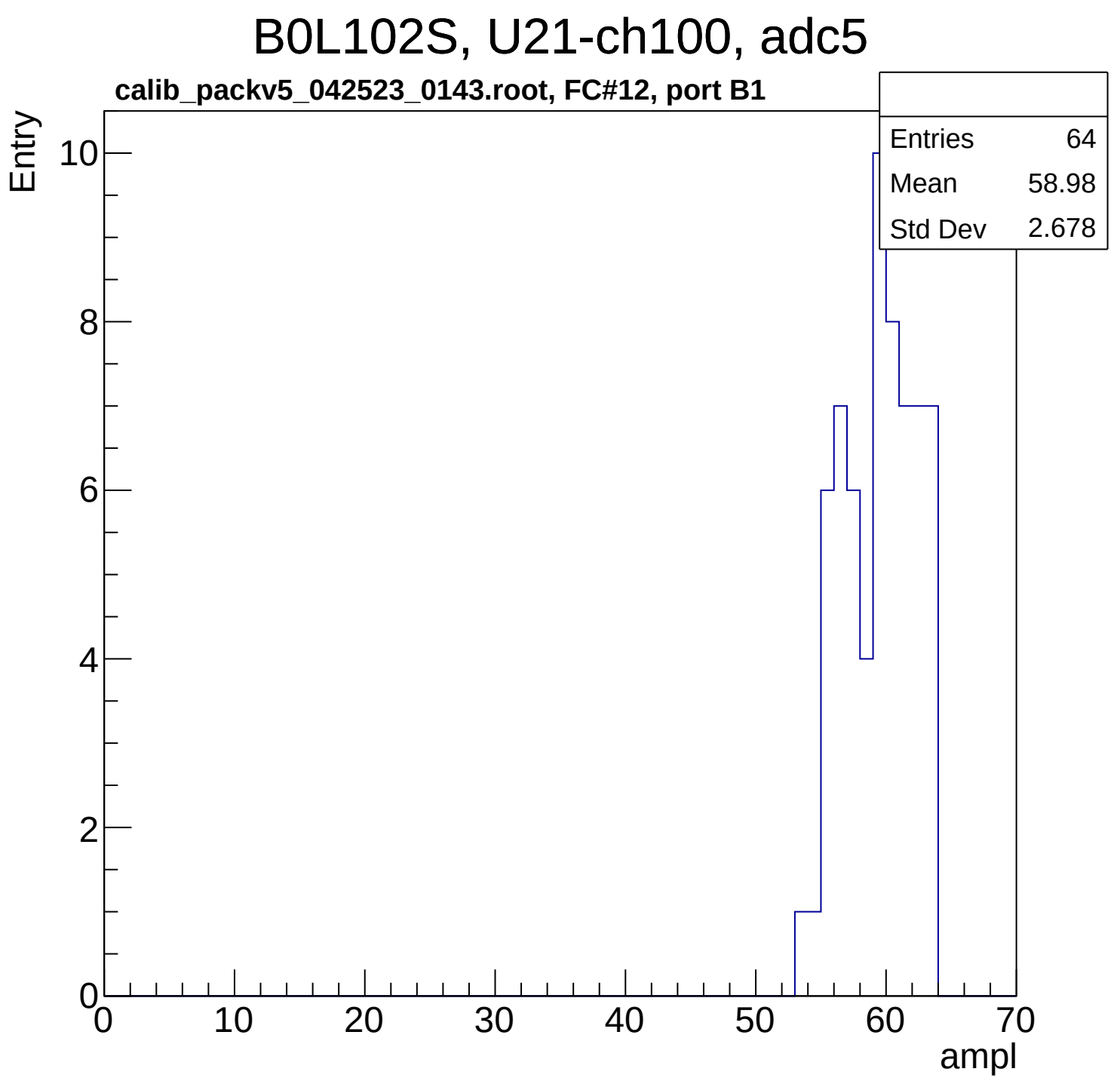
B0L102S, U21-ch100, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 58.98
Std Dev 2.678
Entry
ampl
0
2
4
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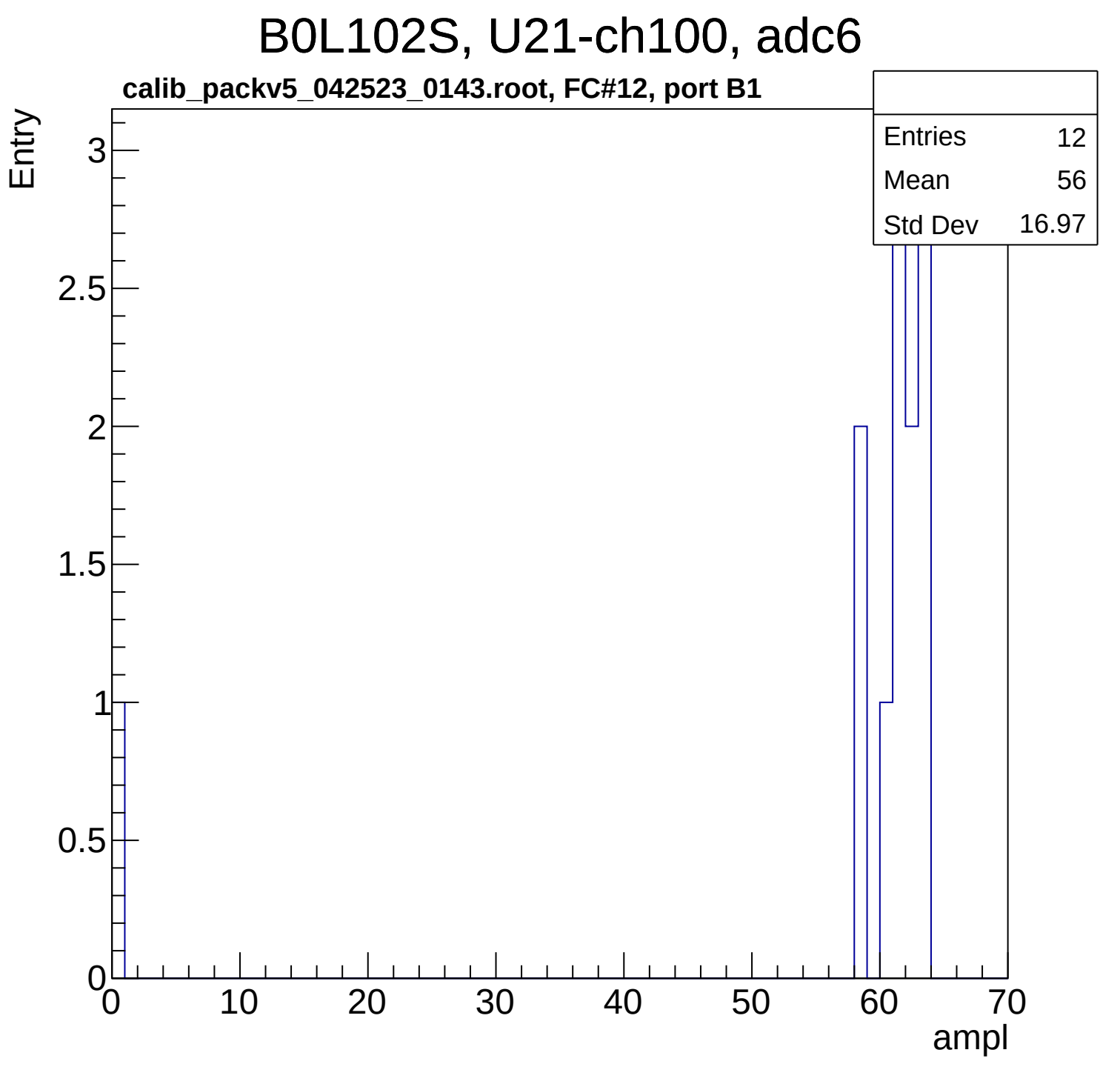
B0L102S, U21-ch100, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 56
Std Dev 16.97
Entry
ampl
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0.5
1
1.5
2
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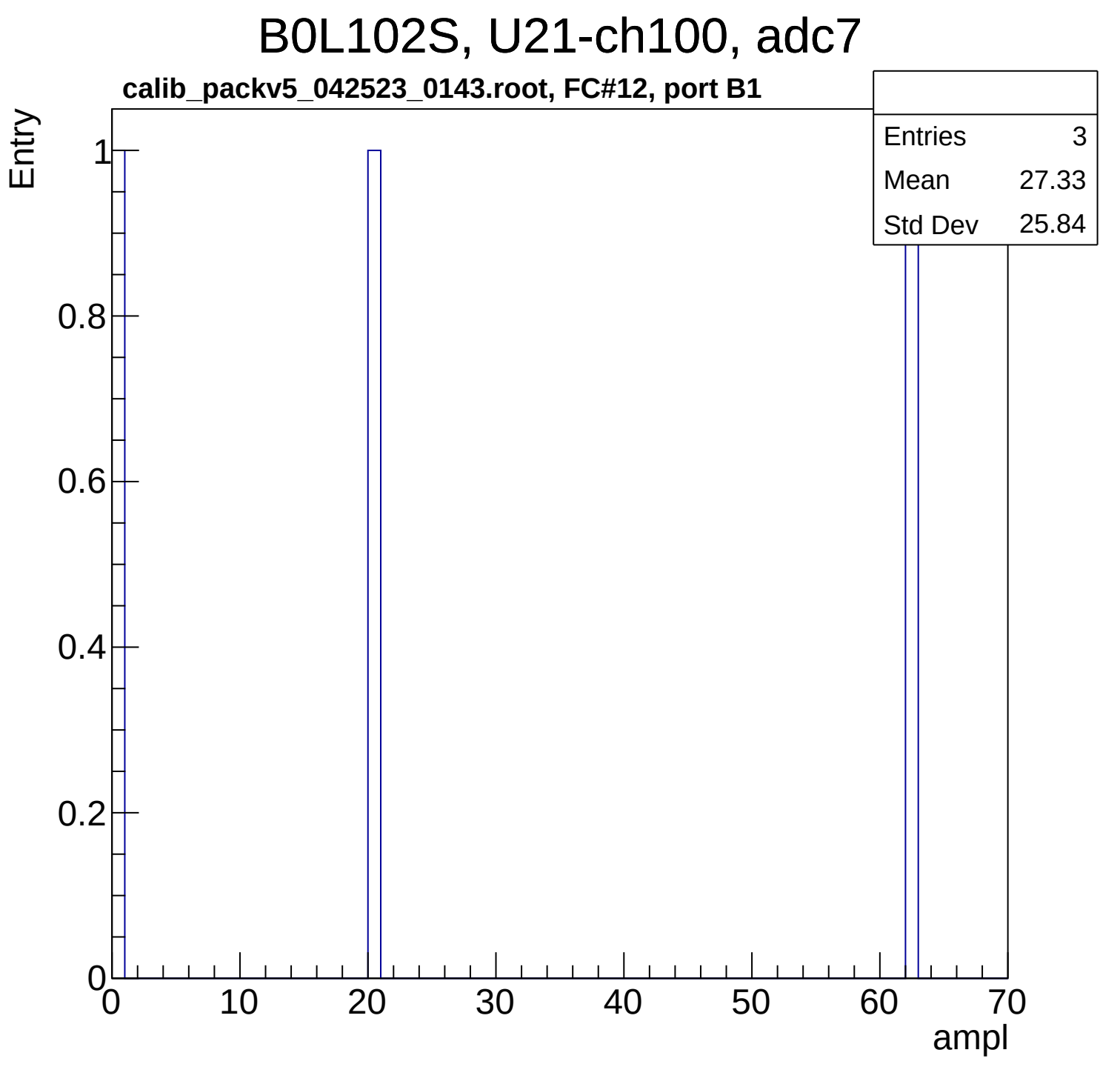
B0L102S, U21-ch100, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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0.2
0.4
0.6
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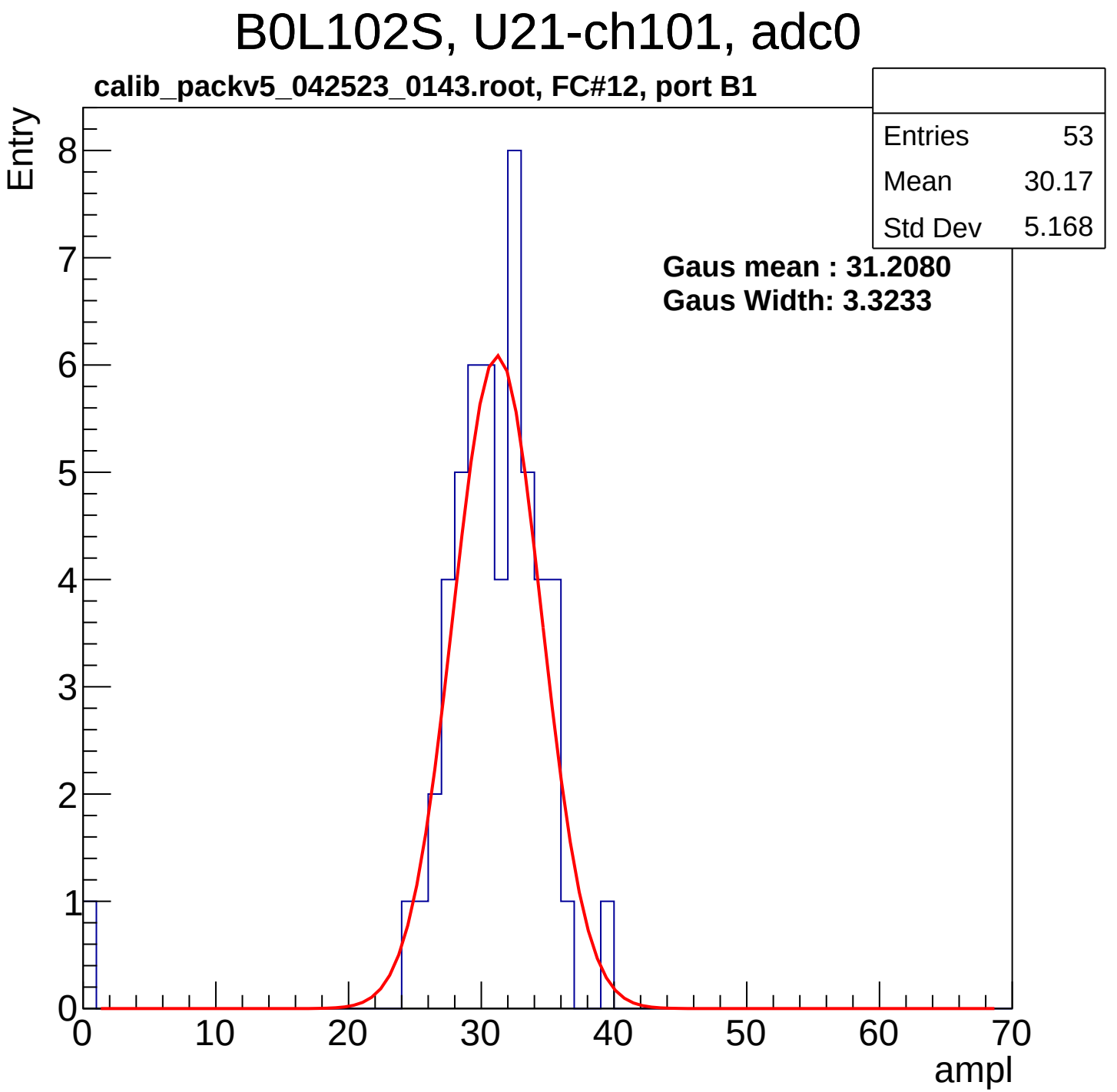
B0L102S, U21-ch101, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 30.17
Std Dev 5.168
Gaus mean : 31.2080
Gaus Width: 3.3233
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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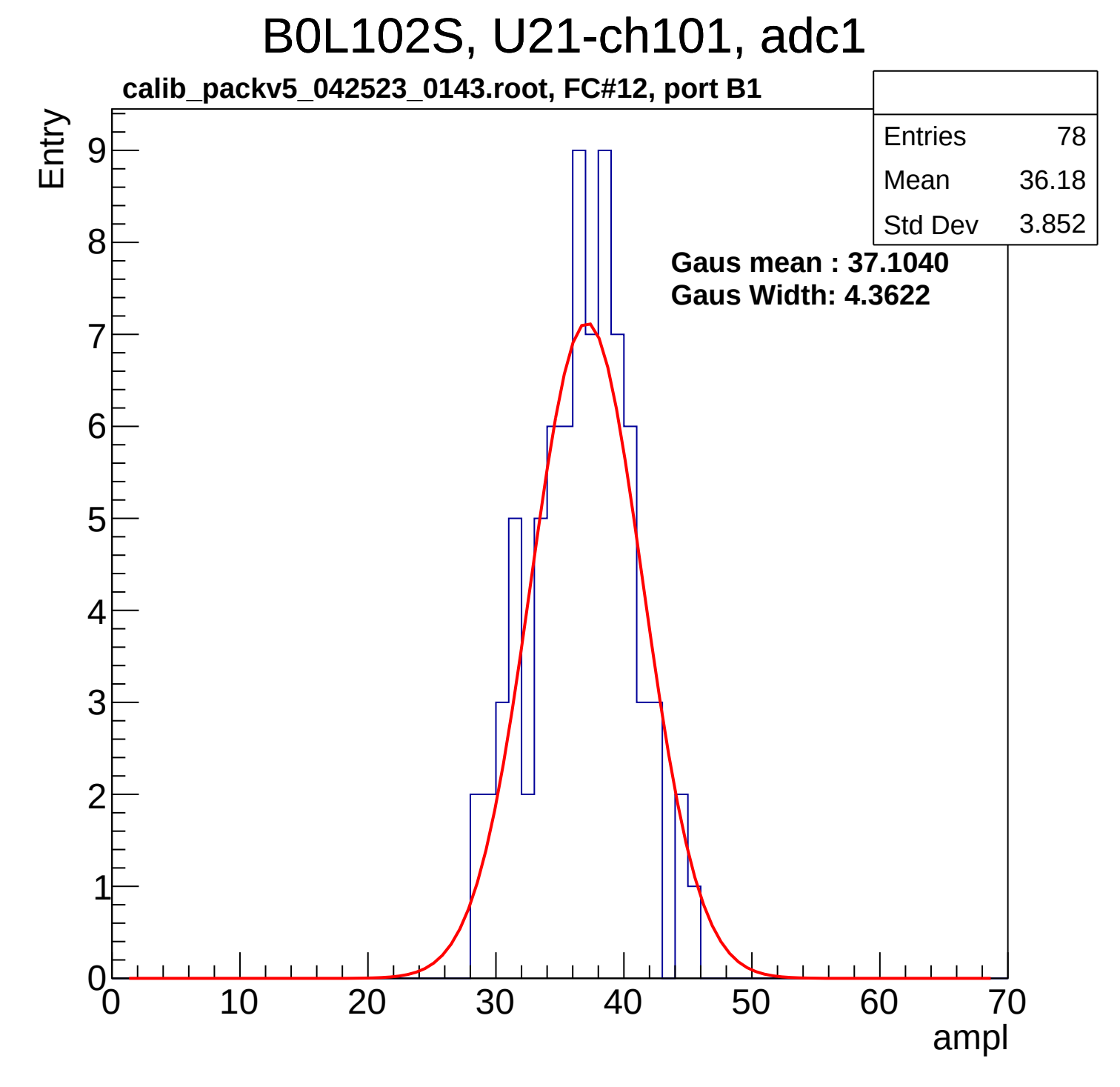
B0L102S, U21-ch101, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 36.18
Std Dev 3.852
Gaus mean : 37.1040
Gaus Width: 4.3622
Entry
ampl
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1
2
3
4
5
6
7
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0
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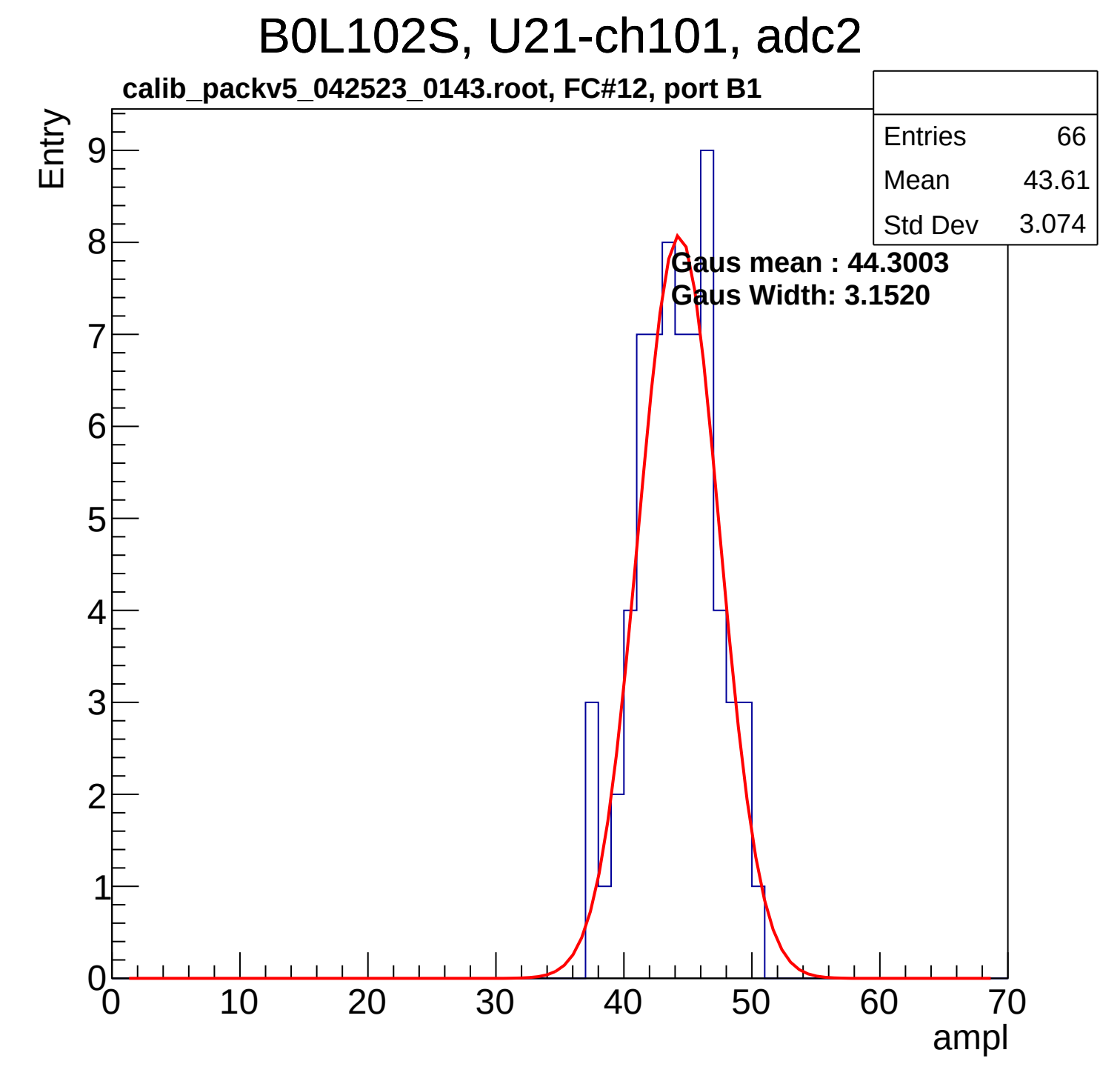
B0L102S, U21-ch101, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.61
Std Dev 3.074
Gaus mean : 44.3003
Gaus Width: 3.1520
Entry
ampl
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2
3
4
5
6
7
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0
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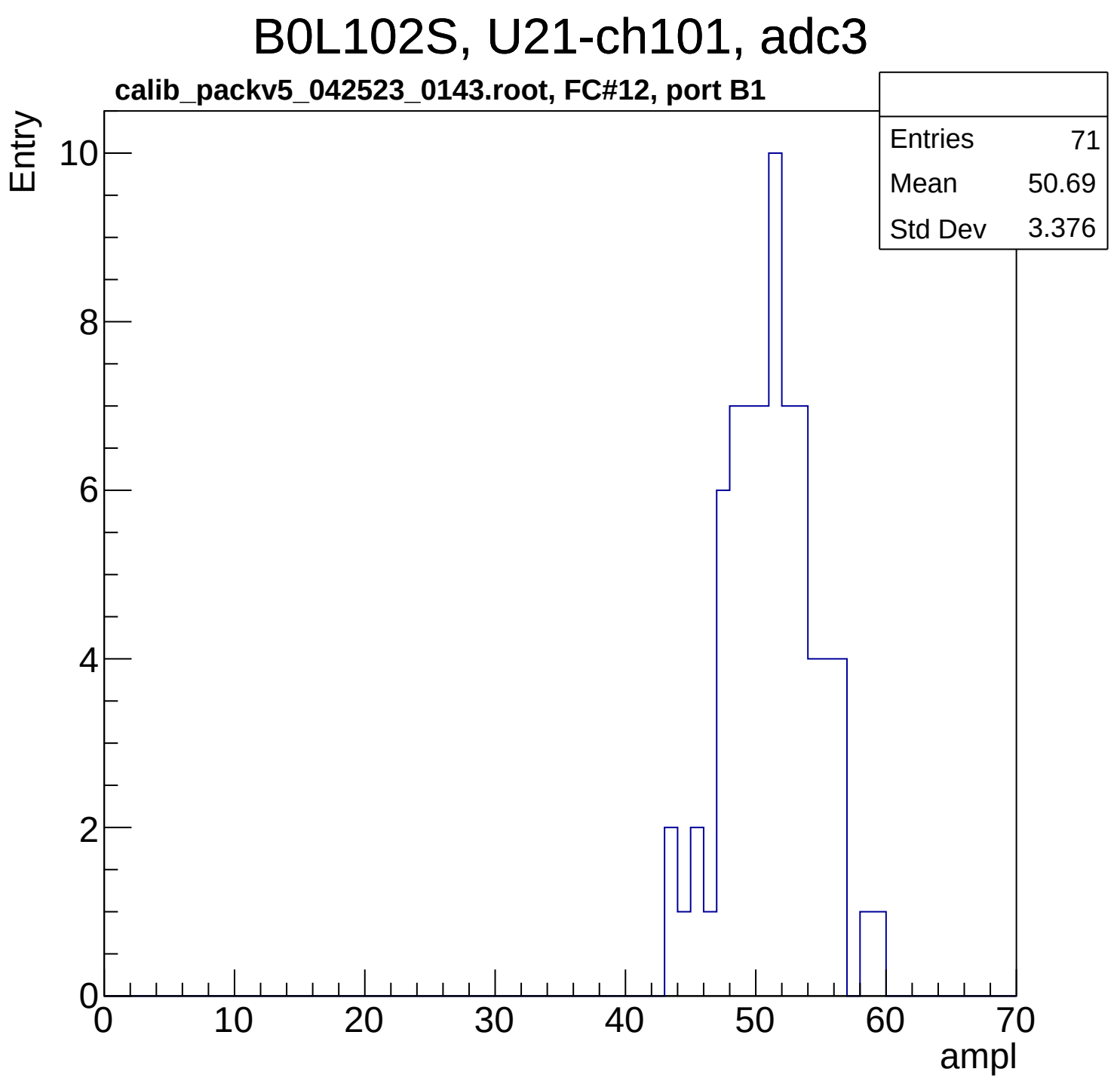
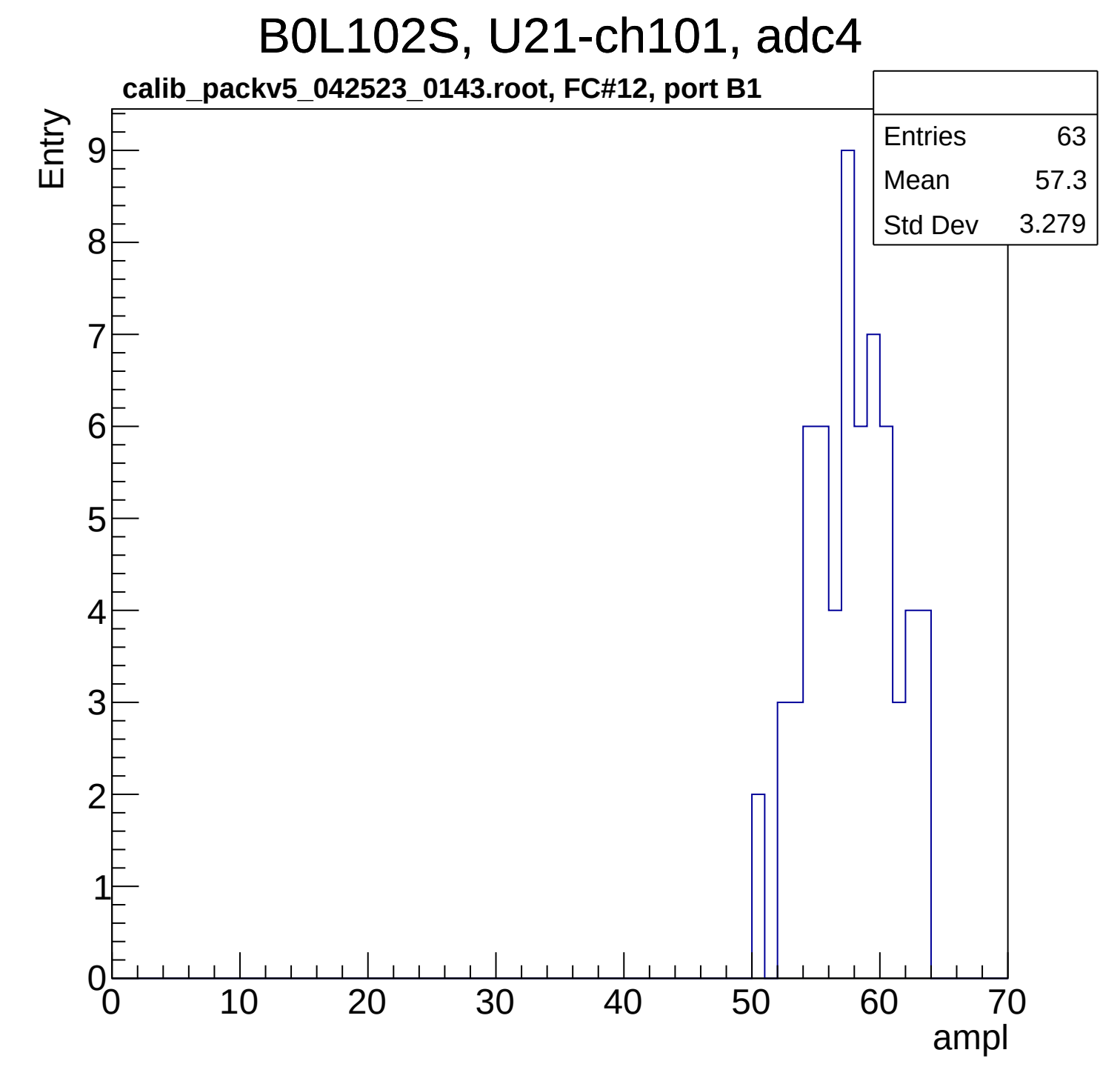

B0L102S, U21-ch101, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 50.69
Std Dev 3.376
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch101, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 57.3
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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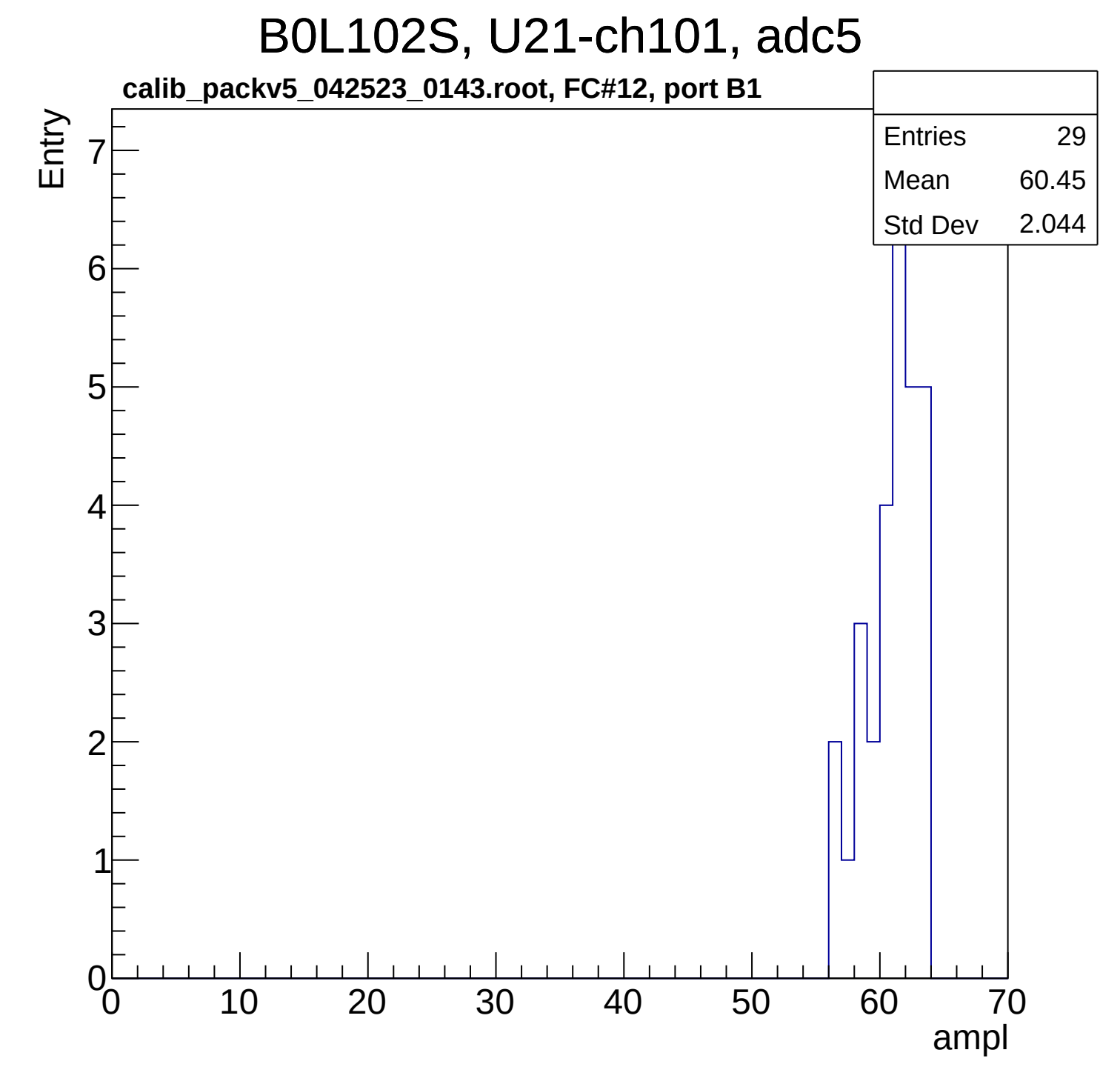
B0L102S, U21-ch101, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 29
Mean 60.45
Std Dev 2.044
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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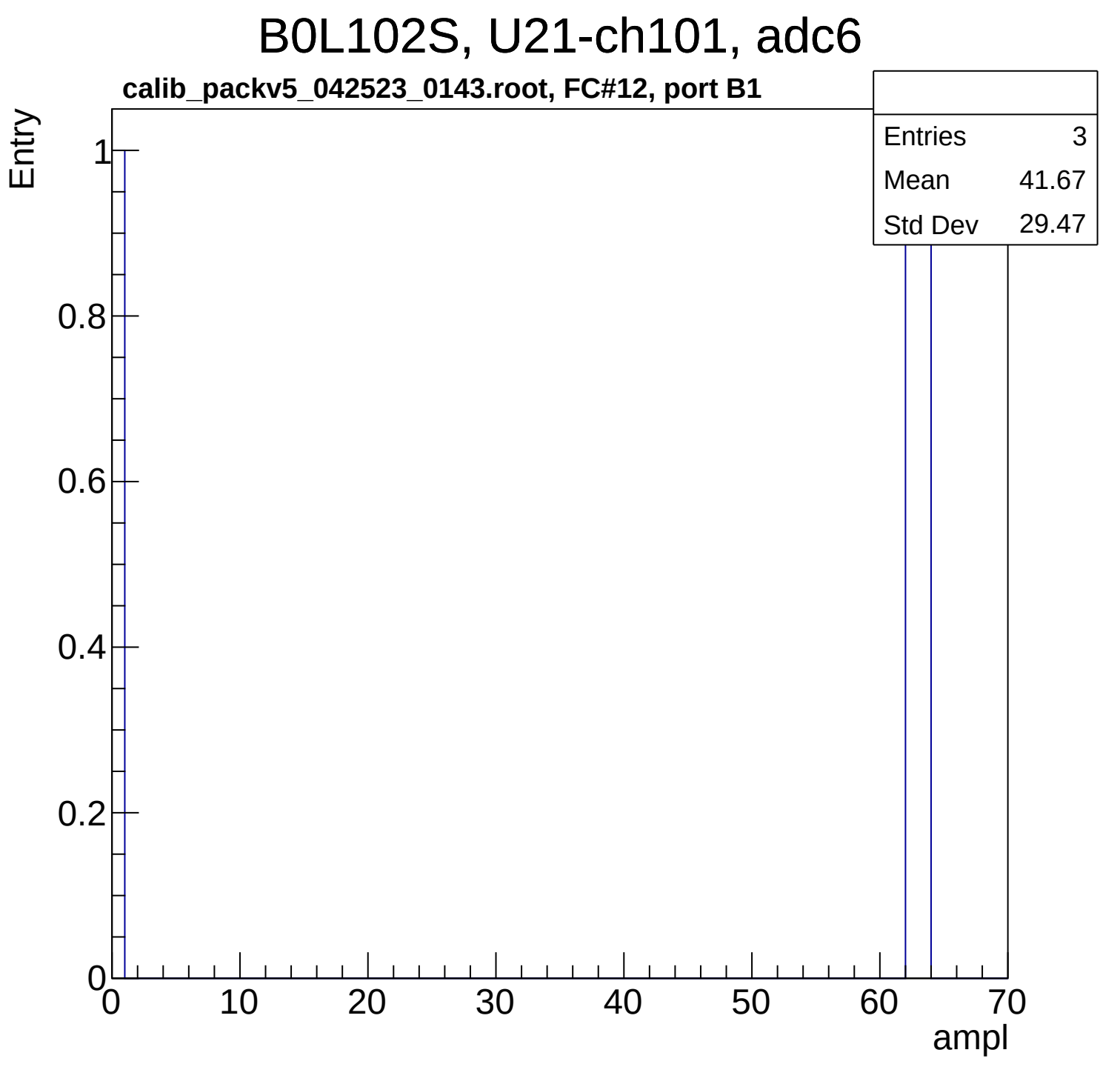
B0L102S, U21-ch101, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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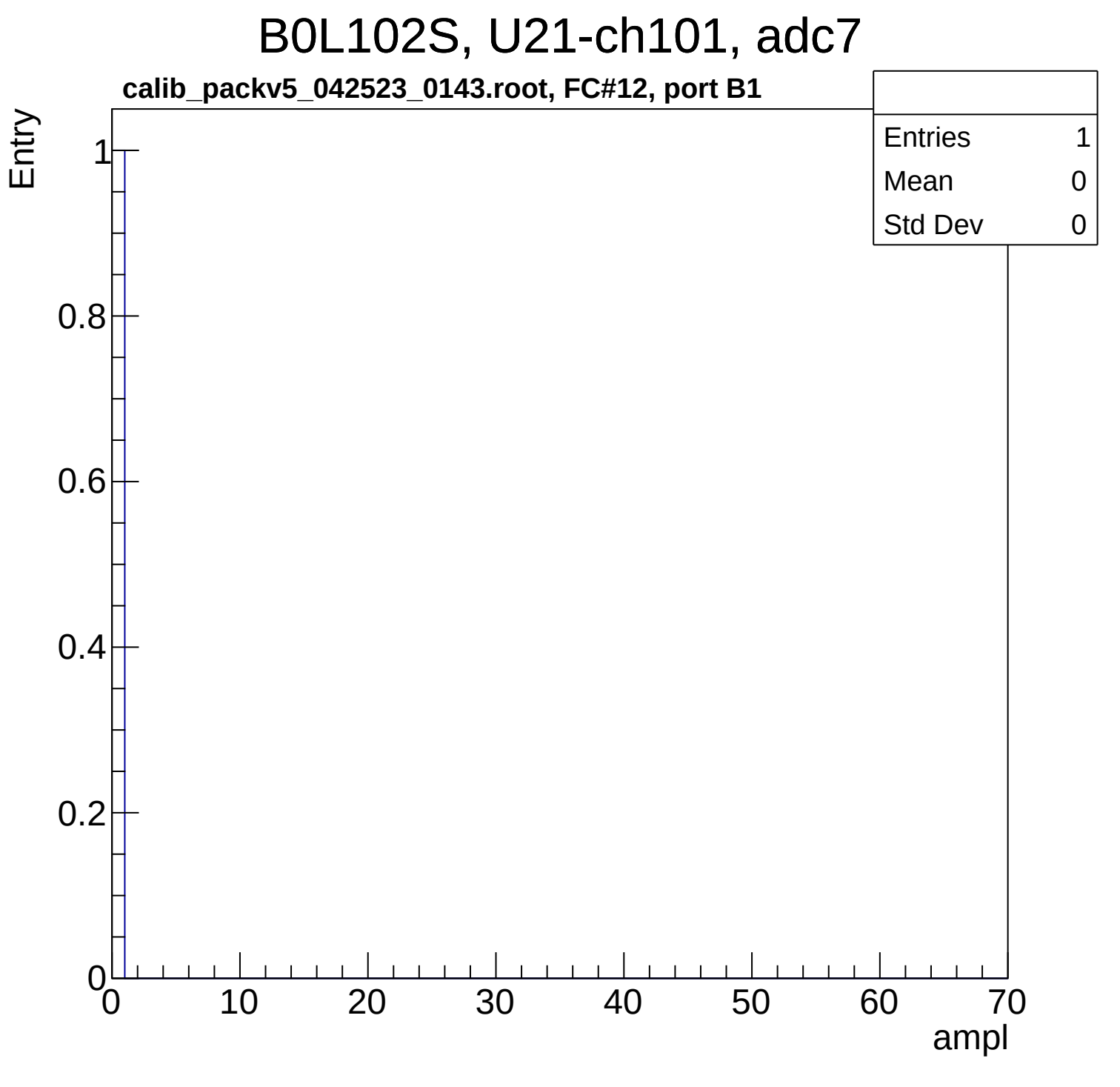
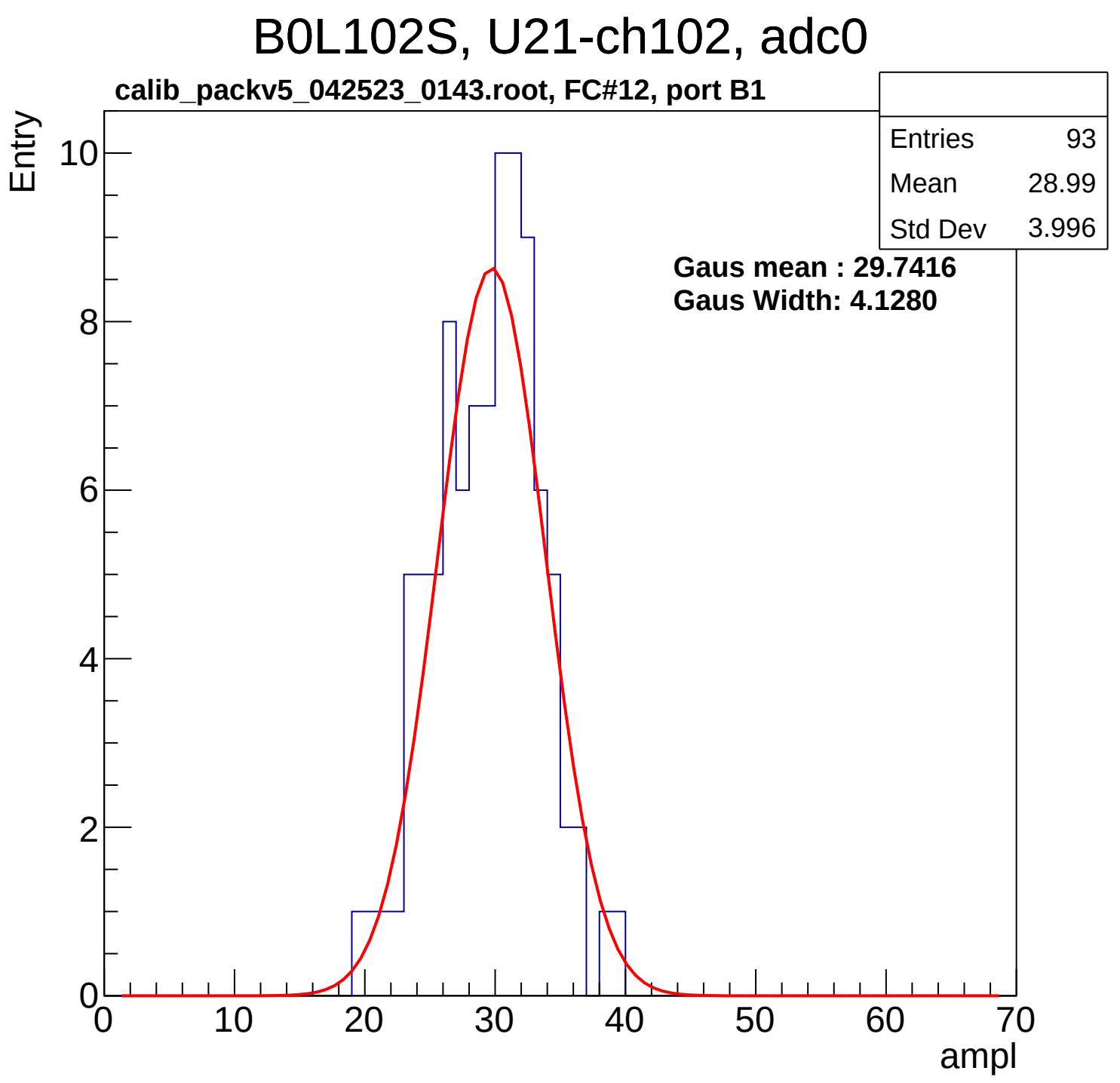
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch101, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch102, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 93
Mean 28.99
Std Dev 3.996
Gaus mean : 29.7416
Gaus Width: 4.1280
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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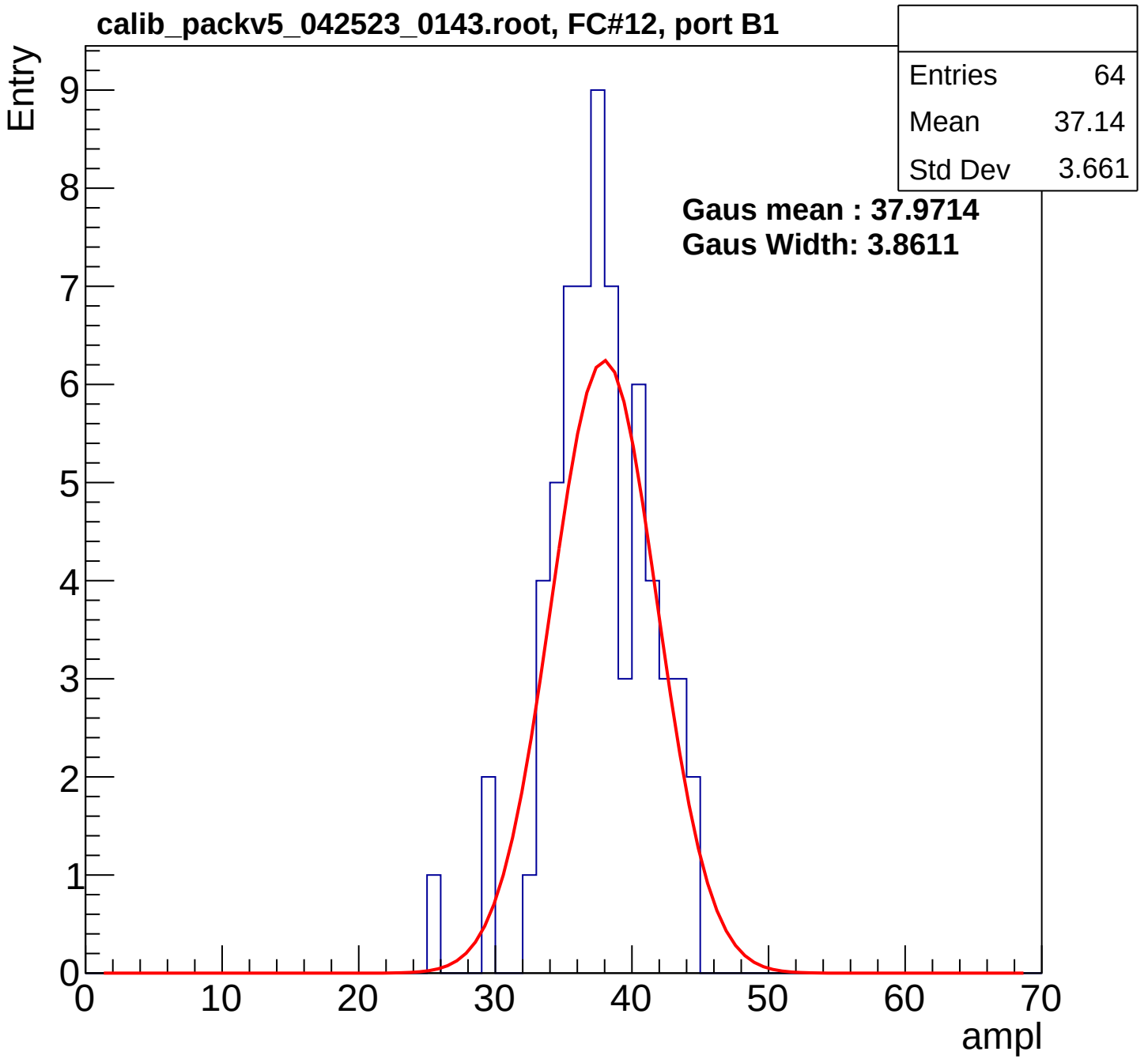
B0L102S, U21-ch102, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 37.14
Std Dev 3.661
Gaus mean : 37.9714
Gaus Width: 3.8611
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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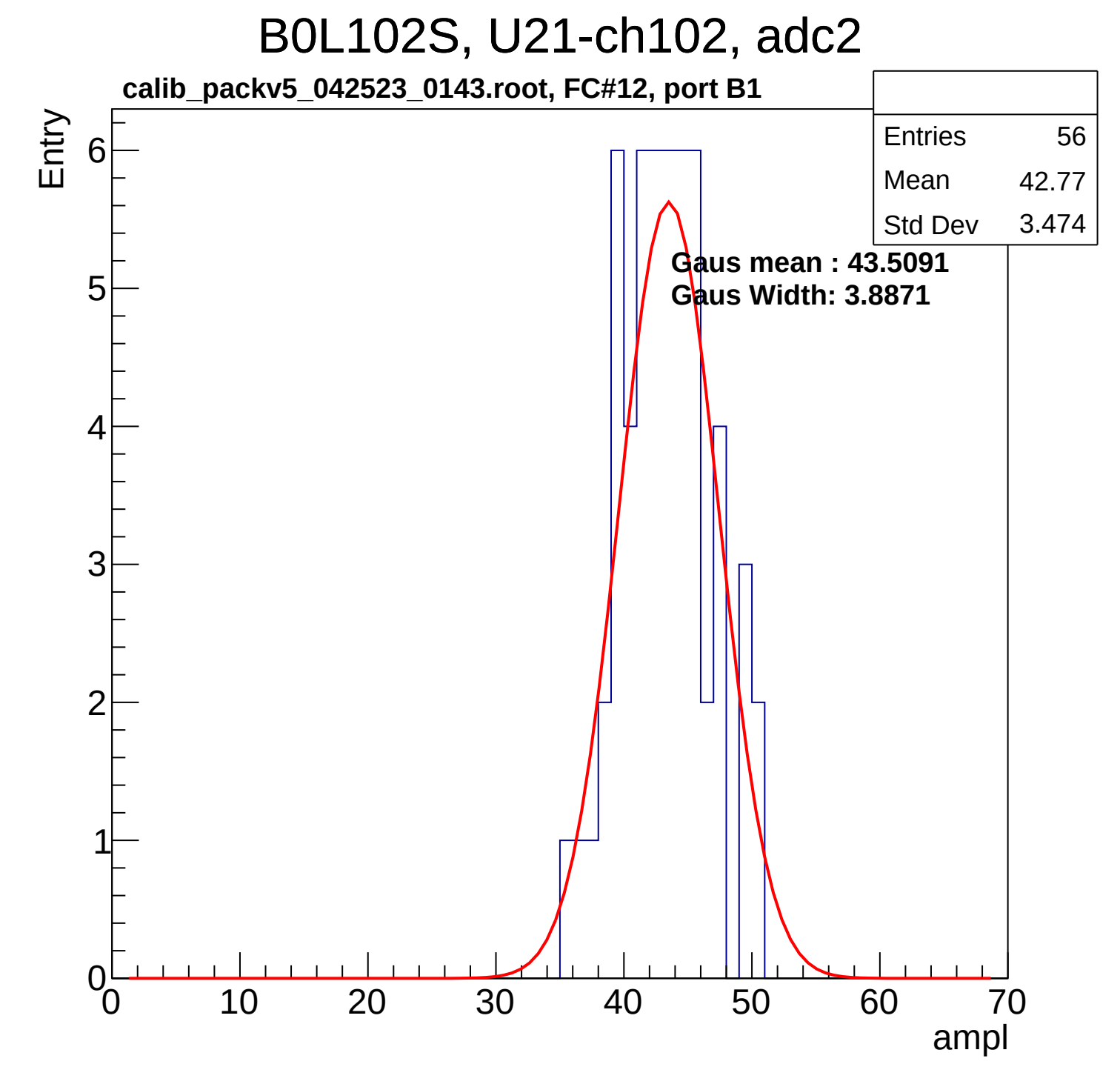
B0L102S, U21-ch102, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 42.77
Std Dev 3.474
Gaus mean : 43.5091
Gaus Width: 3.8871
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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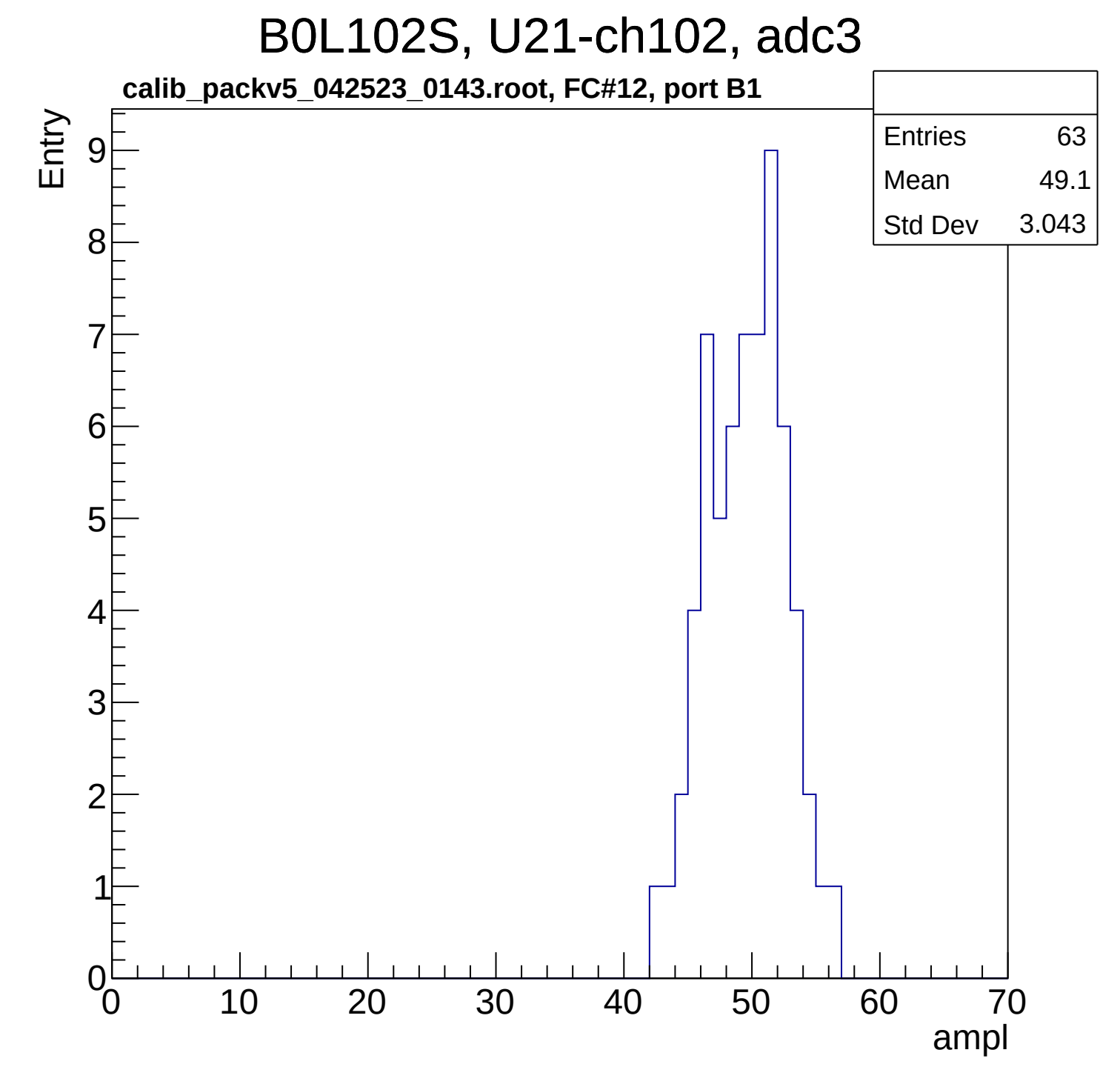
B0L102S, U21-ch102, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 49.1
Std Dev 3.043
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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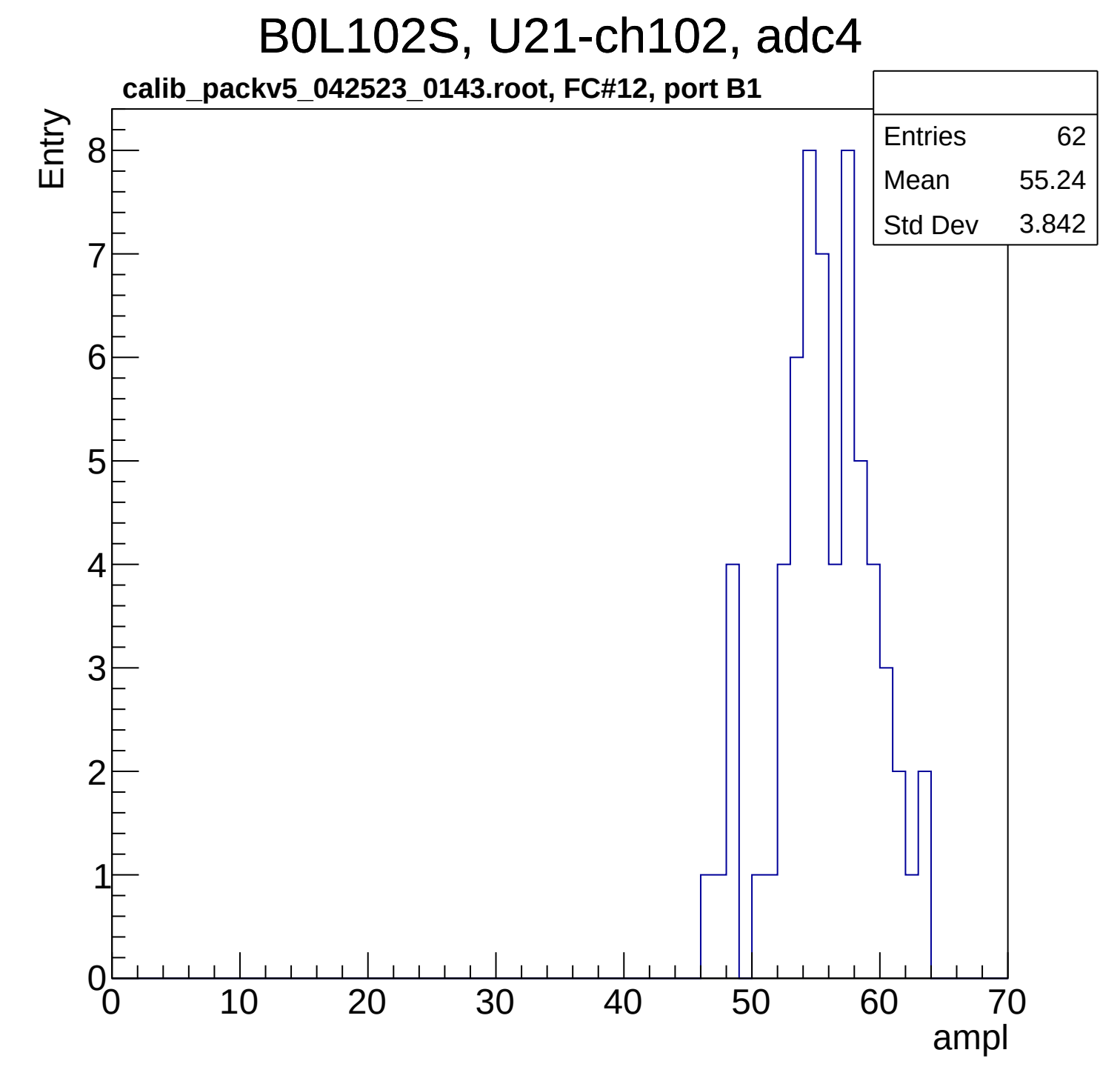
B0L102S, U21-ch102, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 55.24
Std Dev 3.842
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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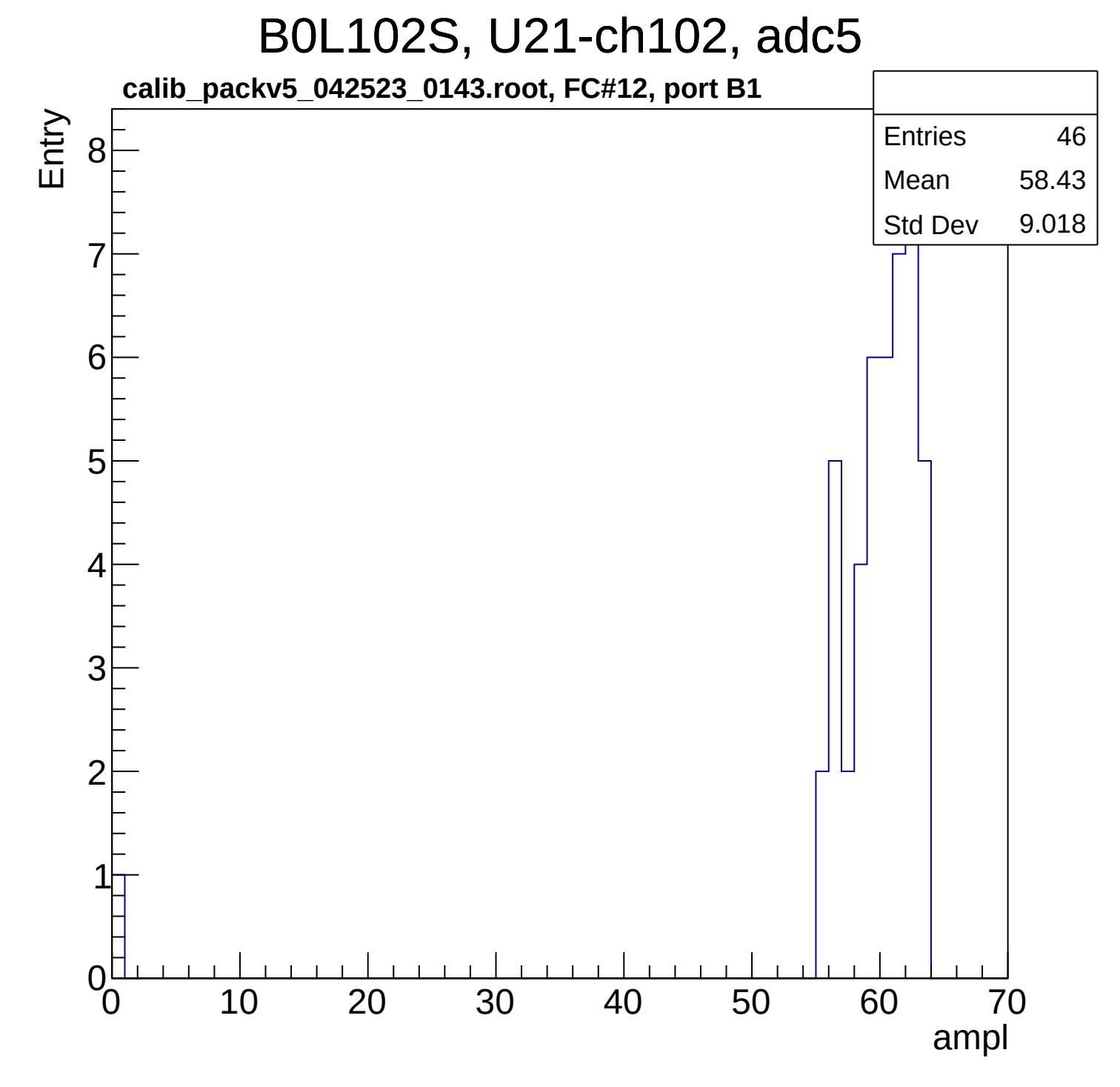
B0L102S, U21-ch102, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.43
Std Dev 9.018
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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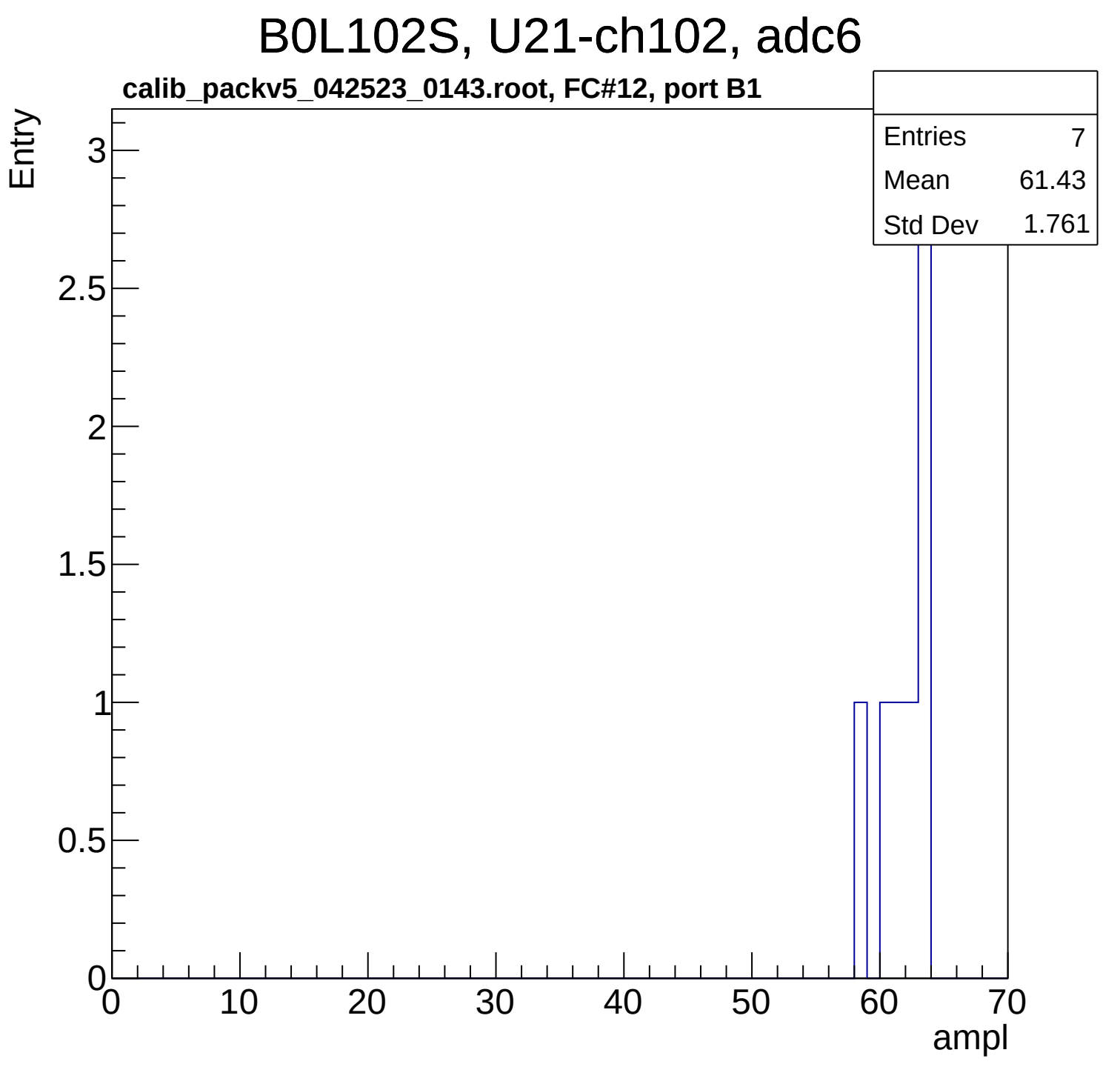
B0L102S, U21-ch102, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.43
Std Dev 1.761
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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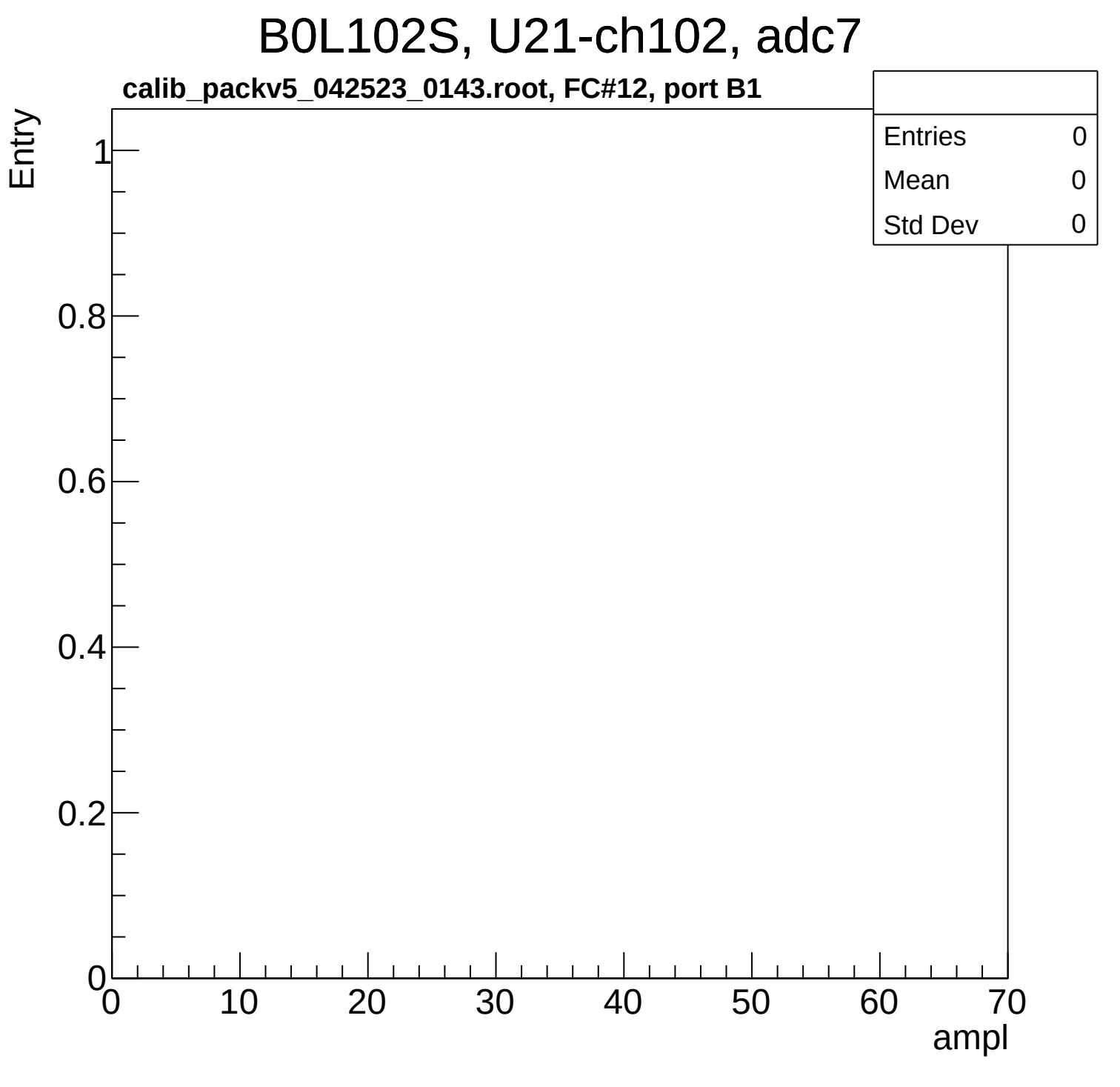
B0L102S, U21-ch102, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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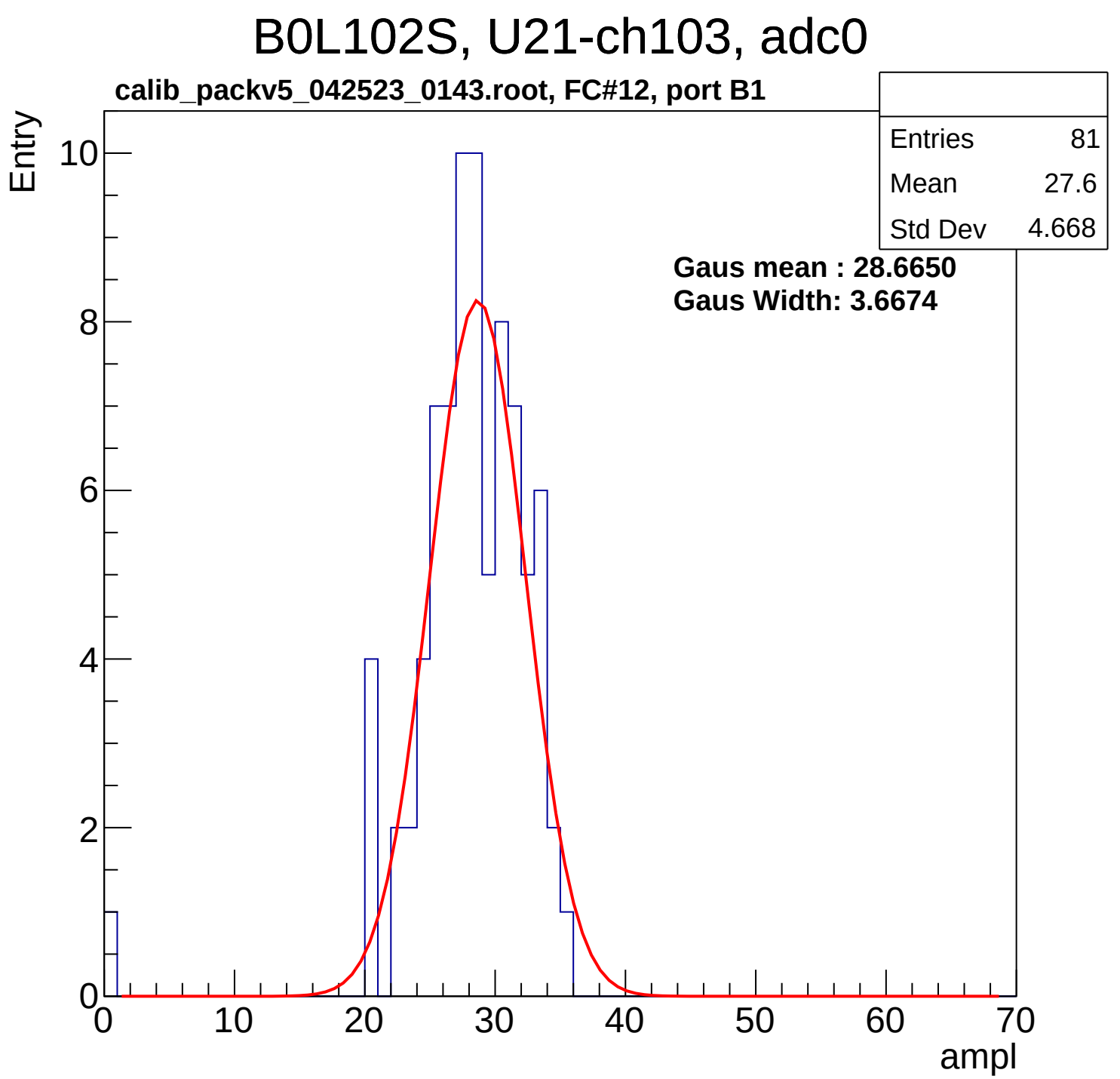
30
40
50
60
70
ampl


B0L102S, U21-ch103, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 27.6
Std Dev 4.668
Gaus mean : 28.6650
Gaus Width: 3.6674
Entry
ampl
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2
4
6
8
10
0
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20
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50
60
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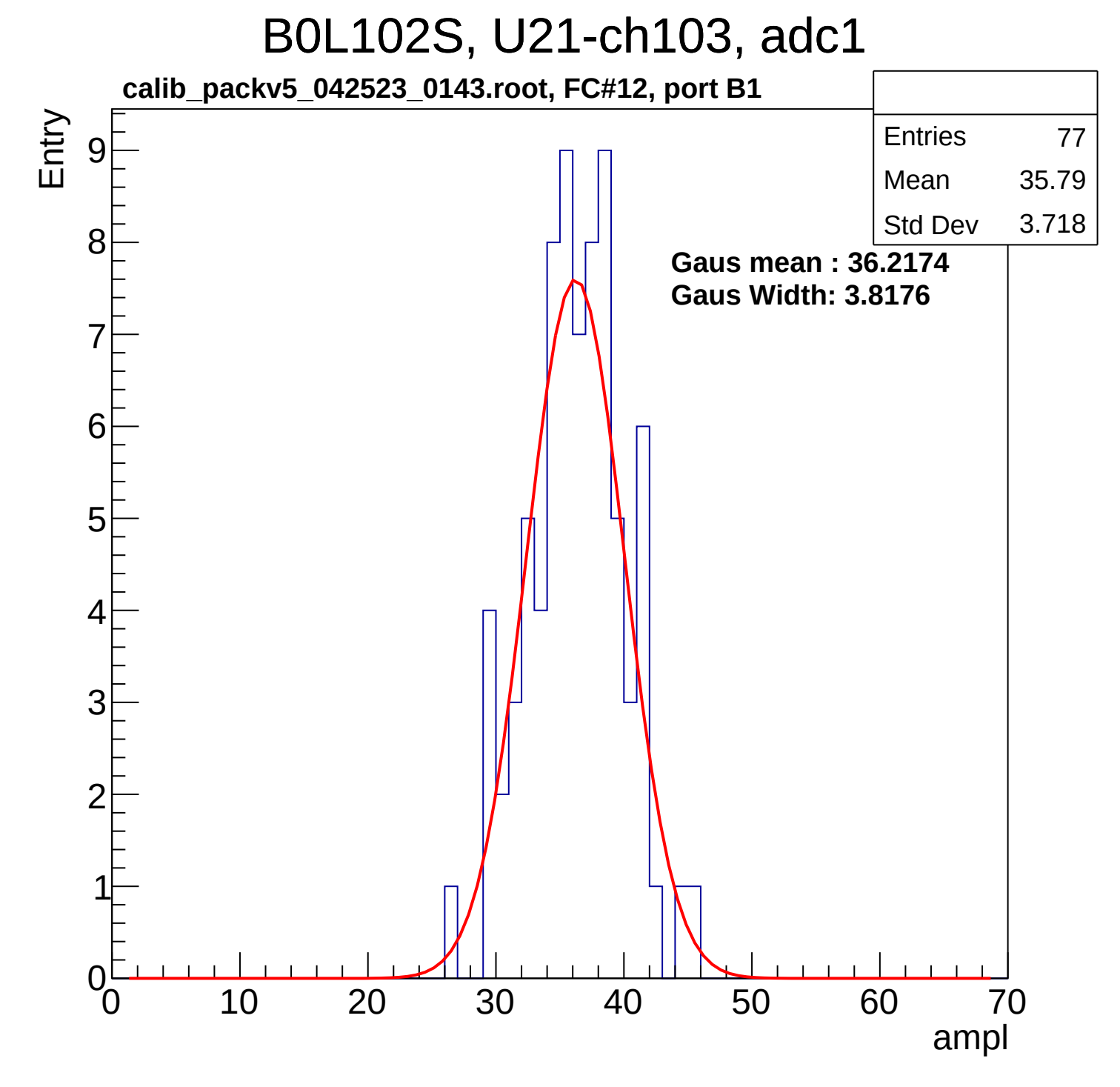
B0L102S, U21-ch103, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 35.79
Std Dev 3.718
Gaus mean : 36.2174
Gaus Width: 3.8176
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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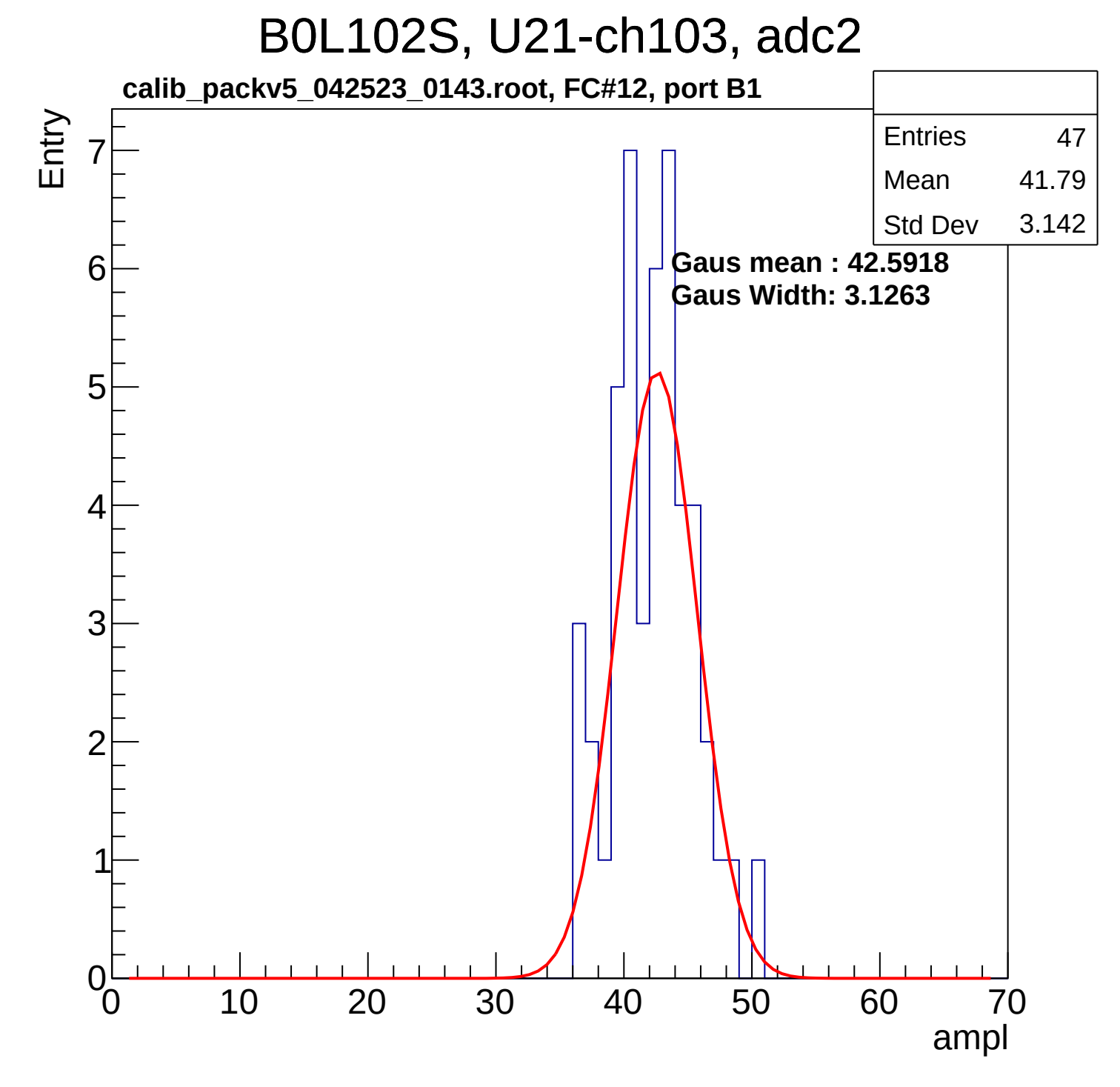
B0L102S, U21-ch103, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 41.79
Std Dev 3.142
Gaus mean : 42.5918
Gaus Width: 3.1263
Entry
ampl
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4
5
6
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0
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60
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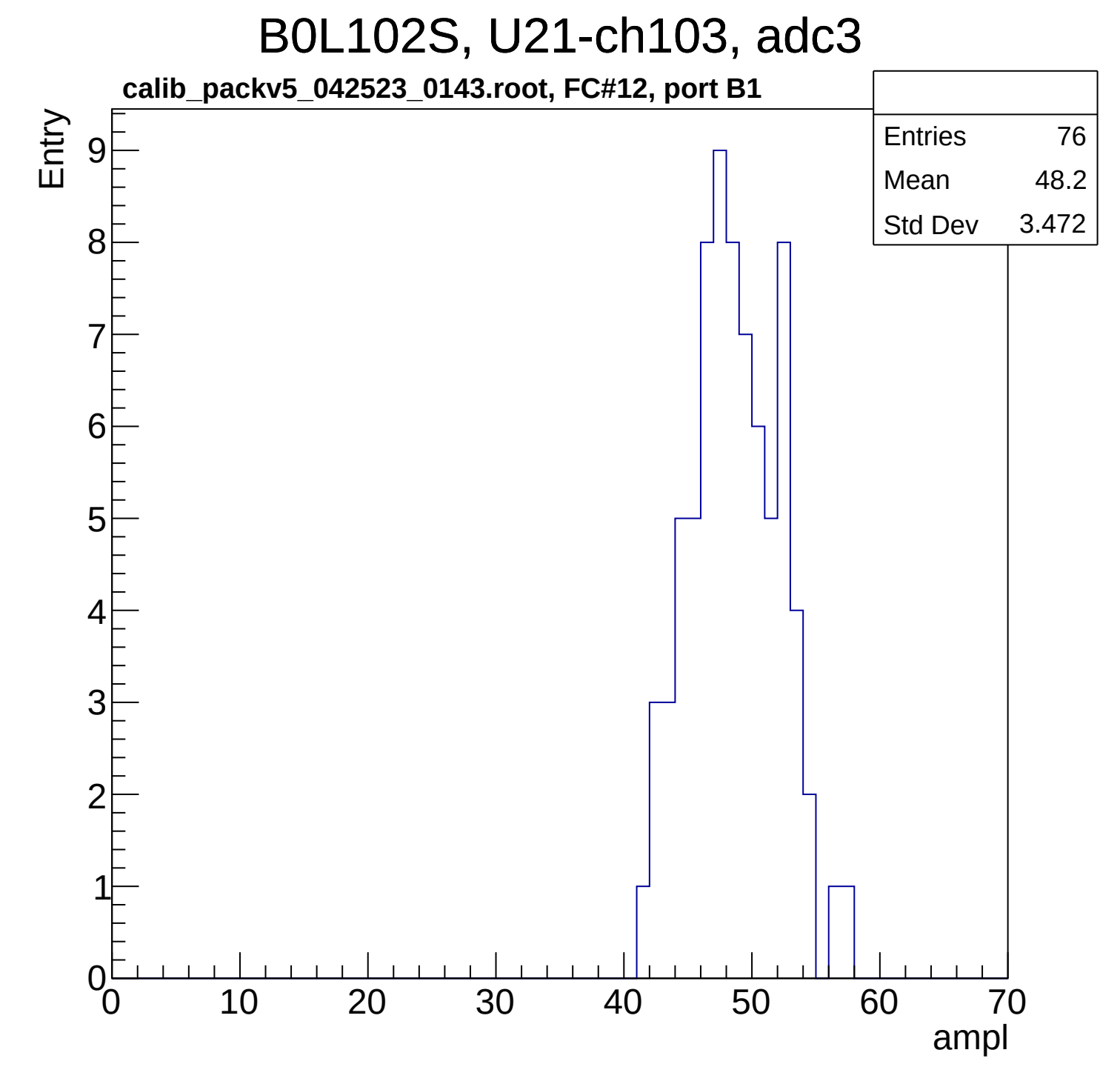
B0L102S, U21-ch103, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 48.2
Std Dev 3.472
Entry
ampl
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4
5
6
7
8
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0
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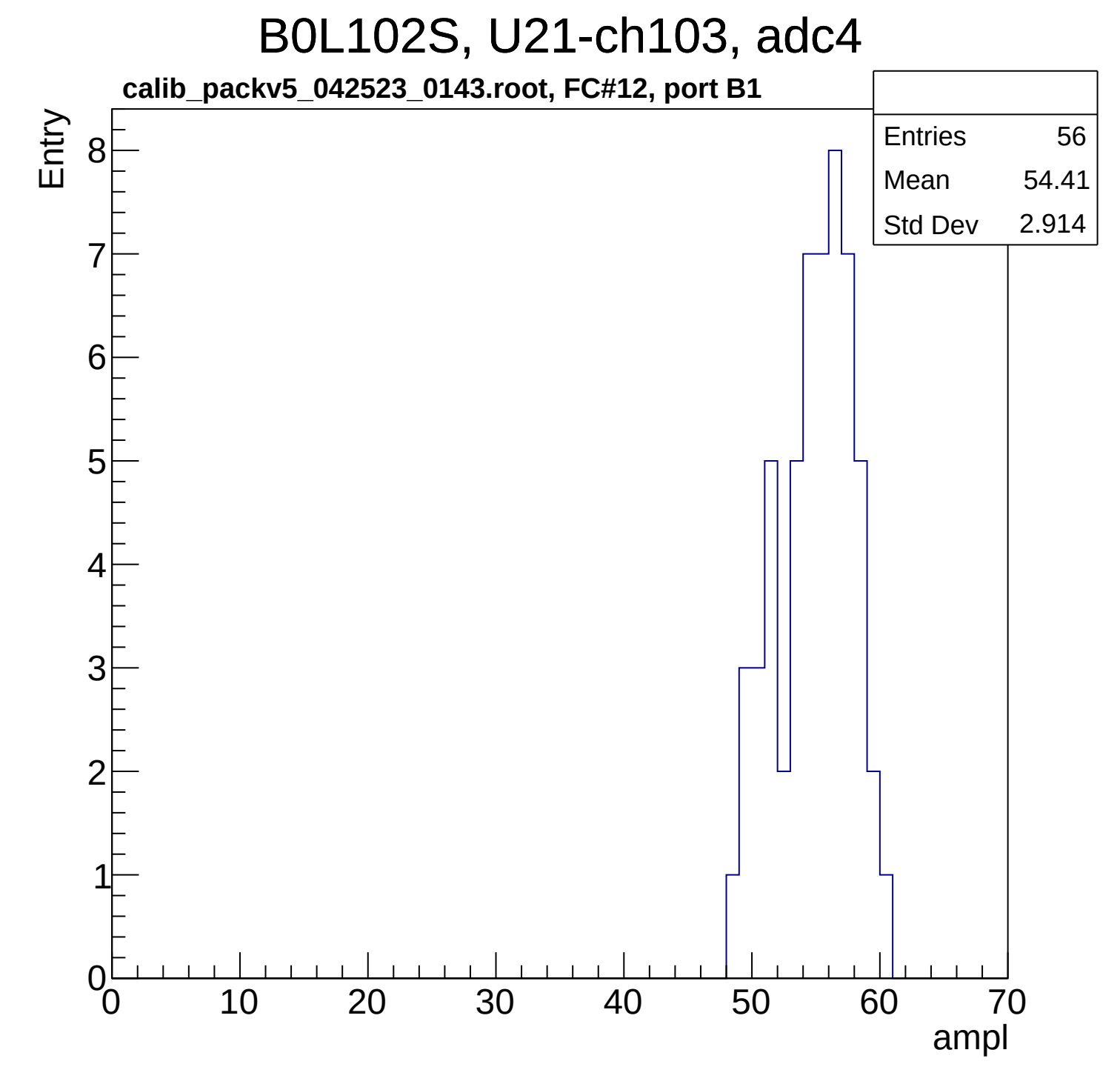
B0L102S, U21-ch103, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 54.41
Std Dev 2.914
Entry
ampl
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0
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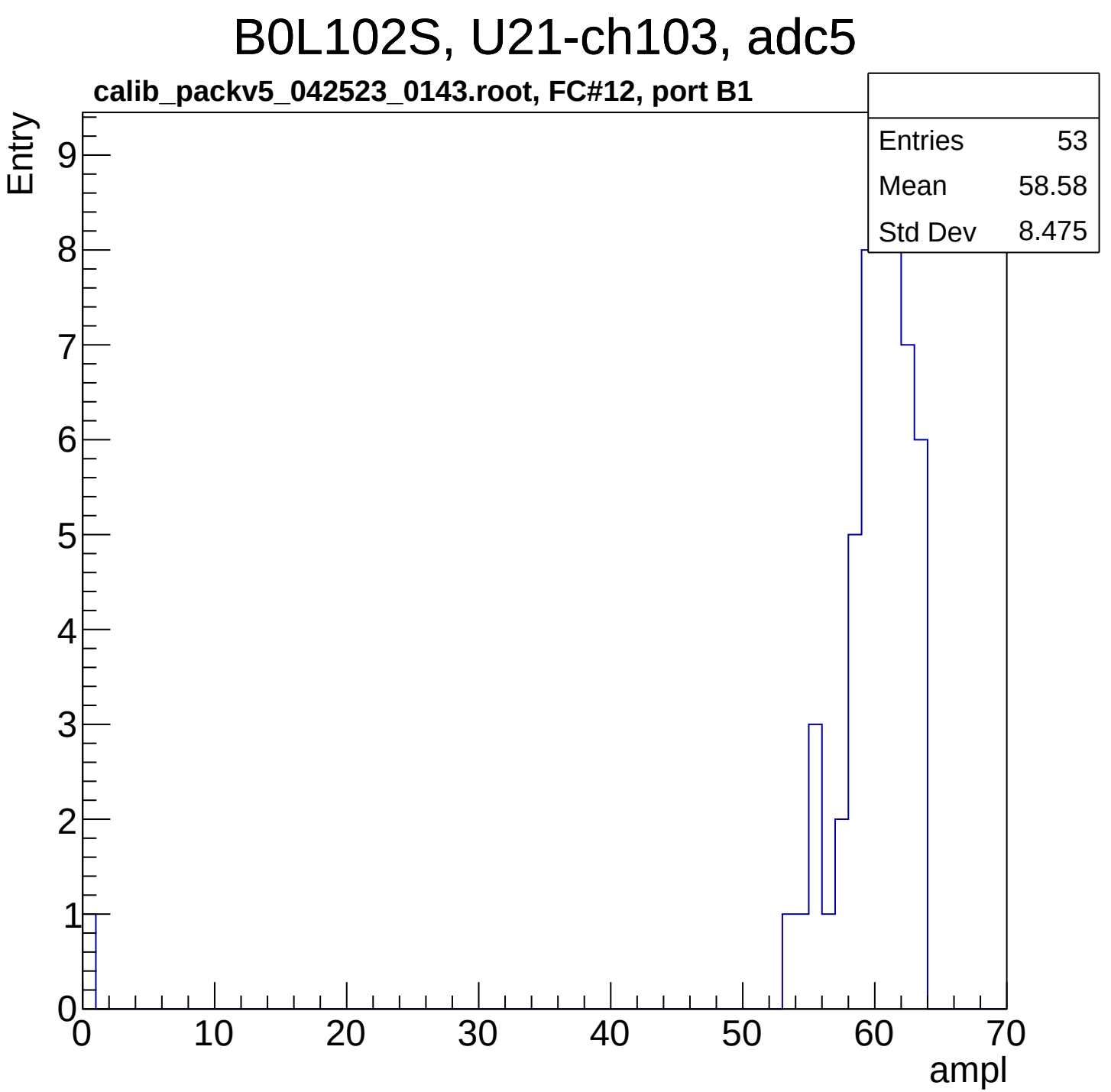
B0L102S, U21-ch103, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 58.58
Std Dev 8.475
Entry
ampl
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7
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0
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60
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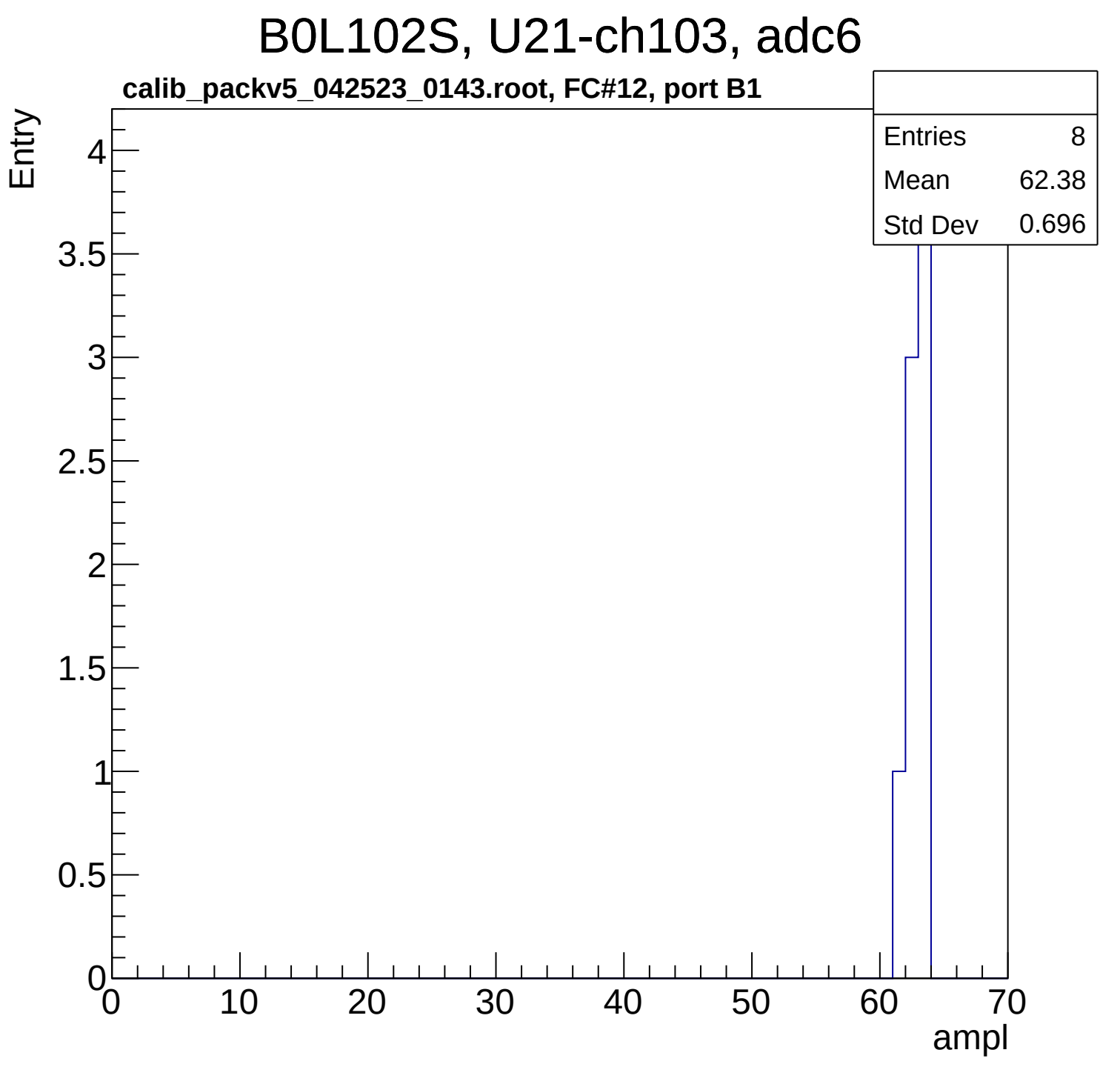
B0L102S, U21-ch103, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.38
Std Dev 0.696
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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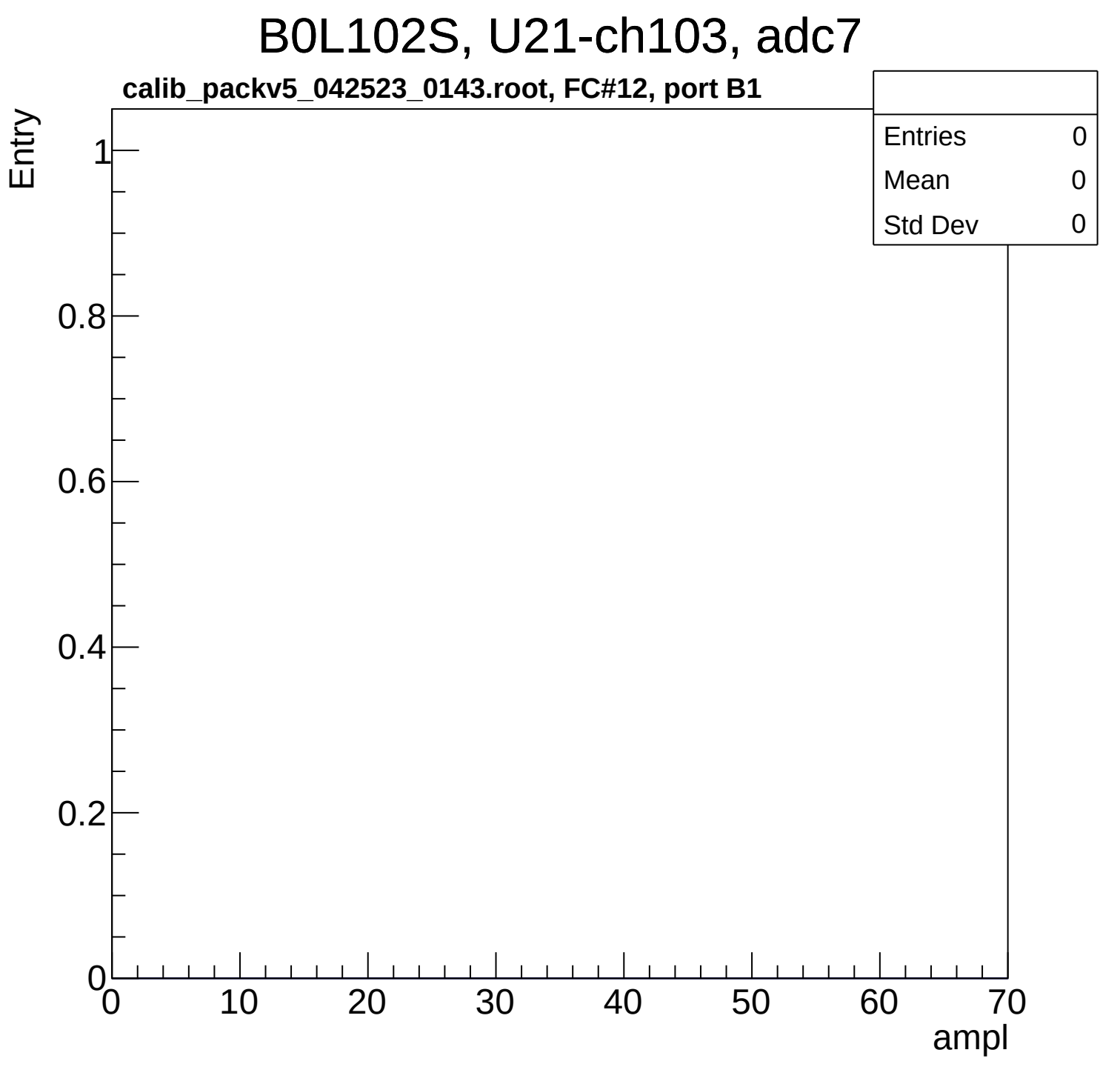
B0L102S, U21-ch103, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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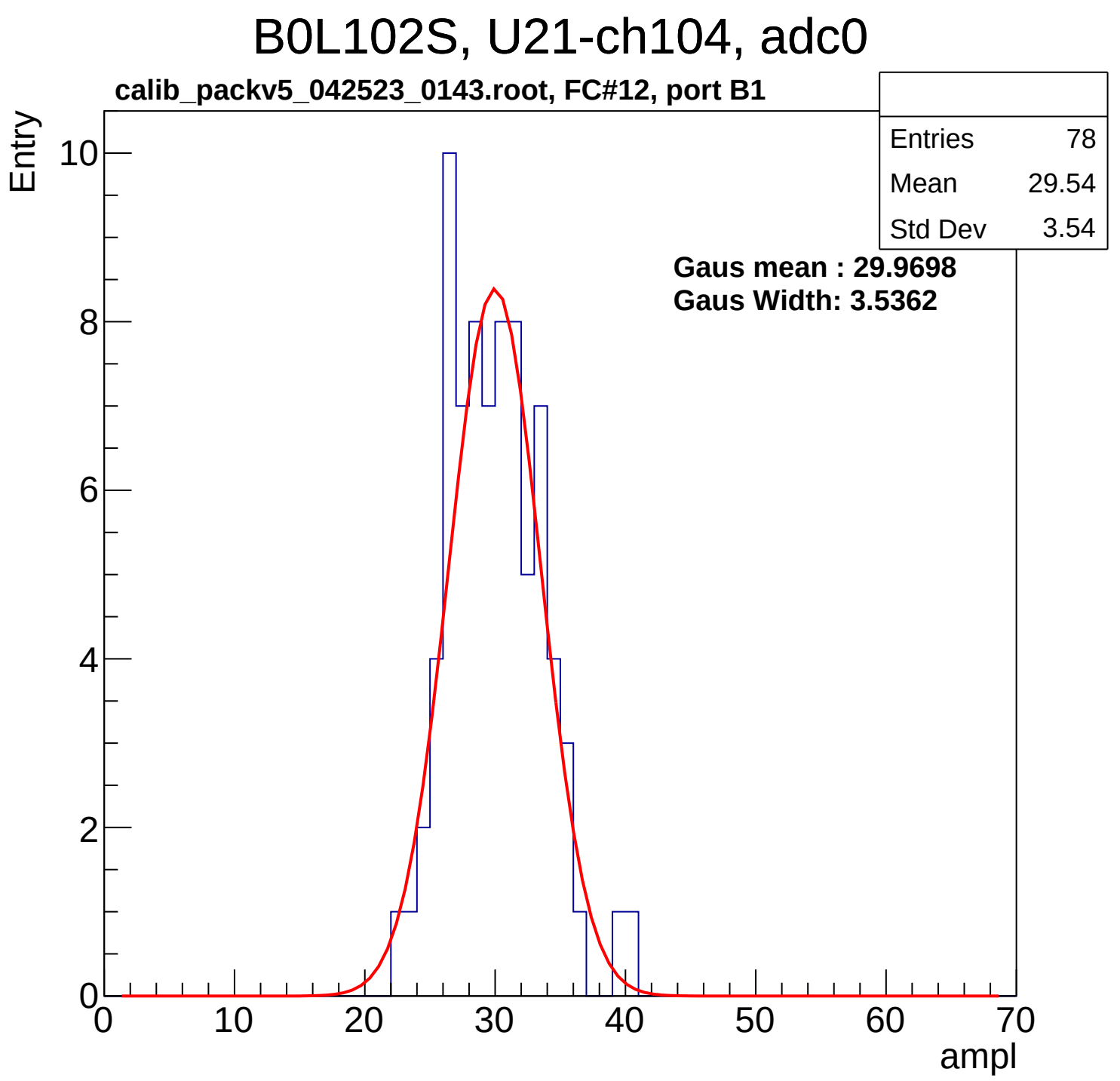
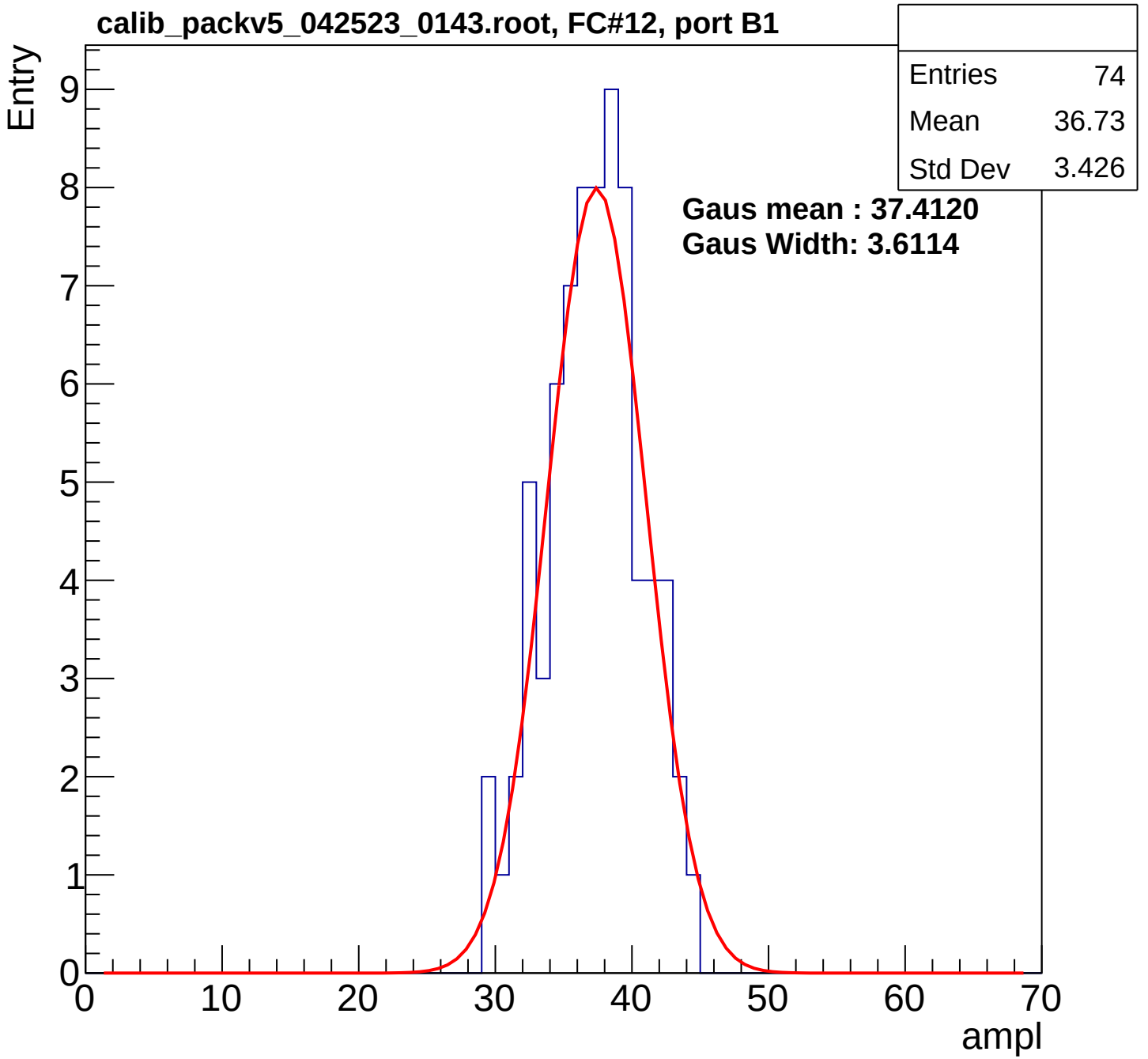
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch104, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 29.54
Std Dev 3.54
Gaus mean : 29.9698
Gaus Width: 3.5362
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch104, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.73
Std Dev 3.426
Gaus mean : 37.4120
Gaus Width: 3.6114
Entry
ampl
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10
20
30
40
50
60
70
1
2
3
4
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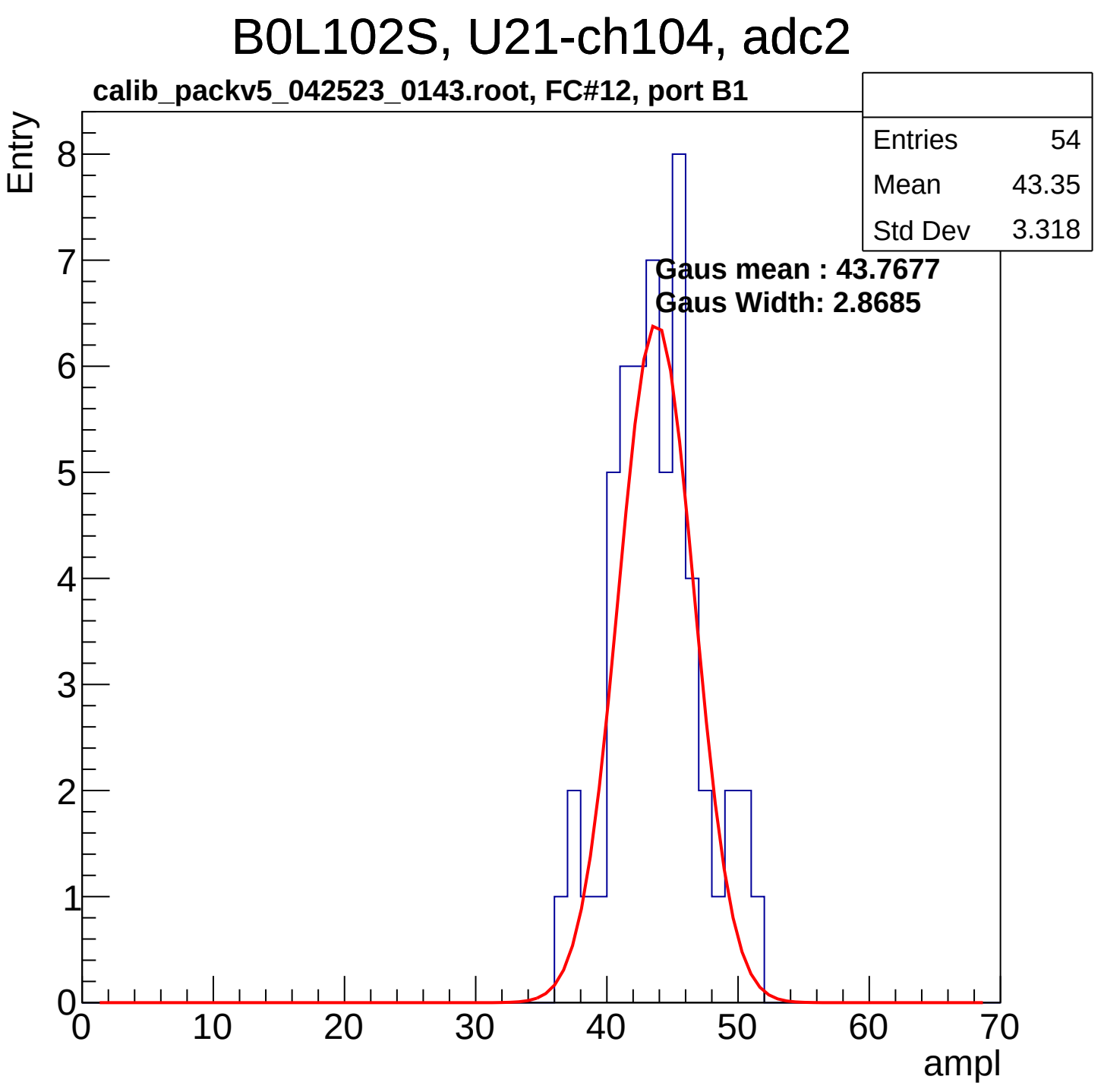
6
7
8
9


B0L102S, U21-ch104, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43.35
Std Dev 3.318
Gaus mean : 43.7677
Gaus Width: 2.8685
Entry
ampl
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2
3
4
5
6
7
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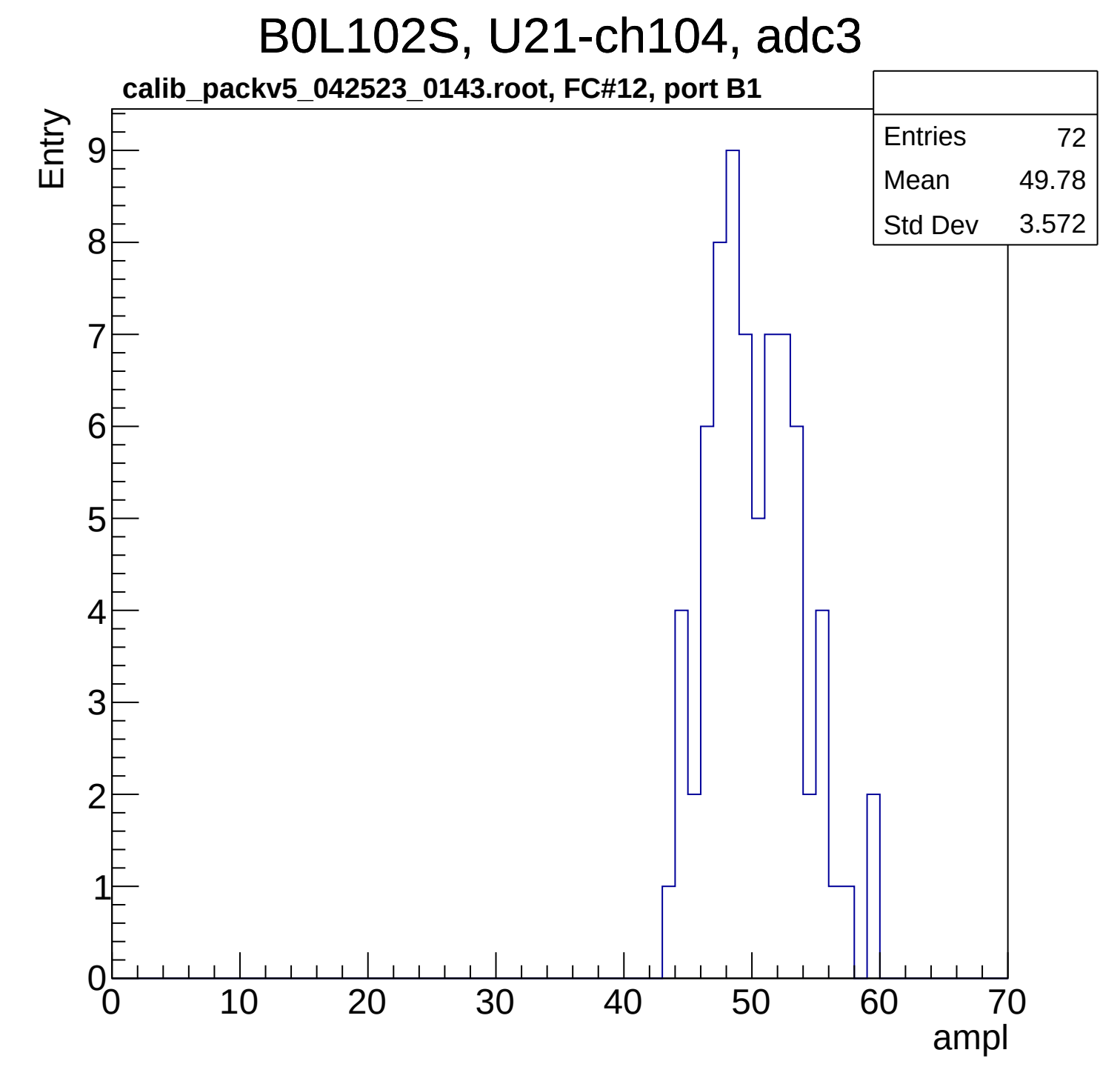
B0L102S, U21-ch104, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 49.78
Std Dev 3.572
Entry
ampl
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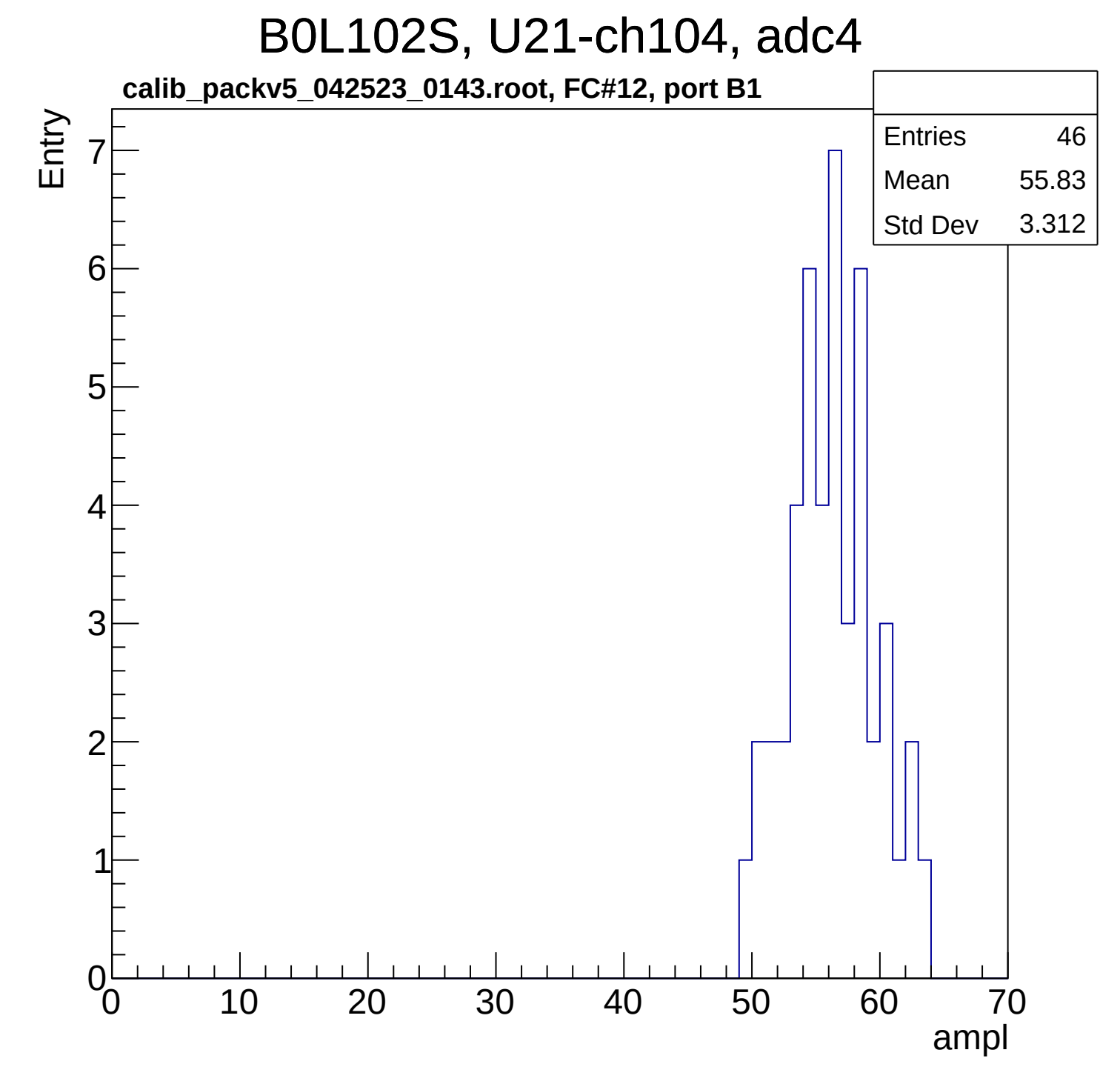
B0L102S, U21-ch104, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 55.83
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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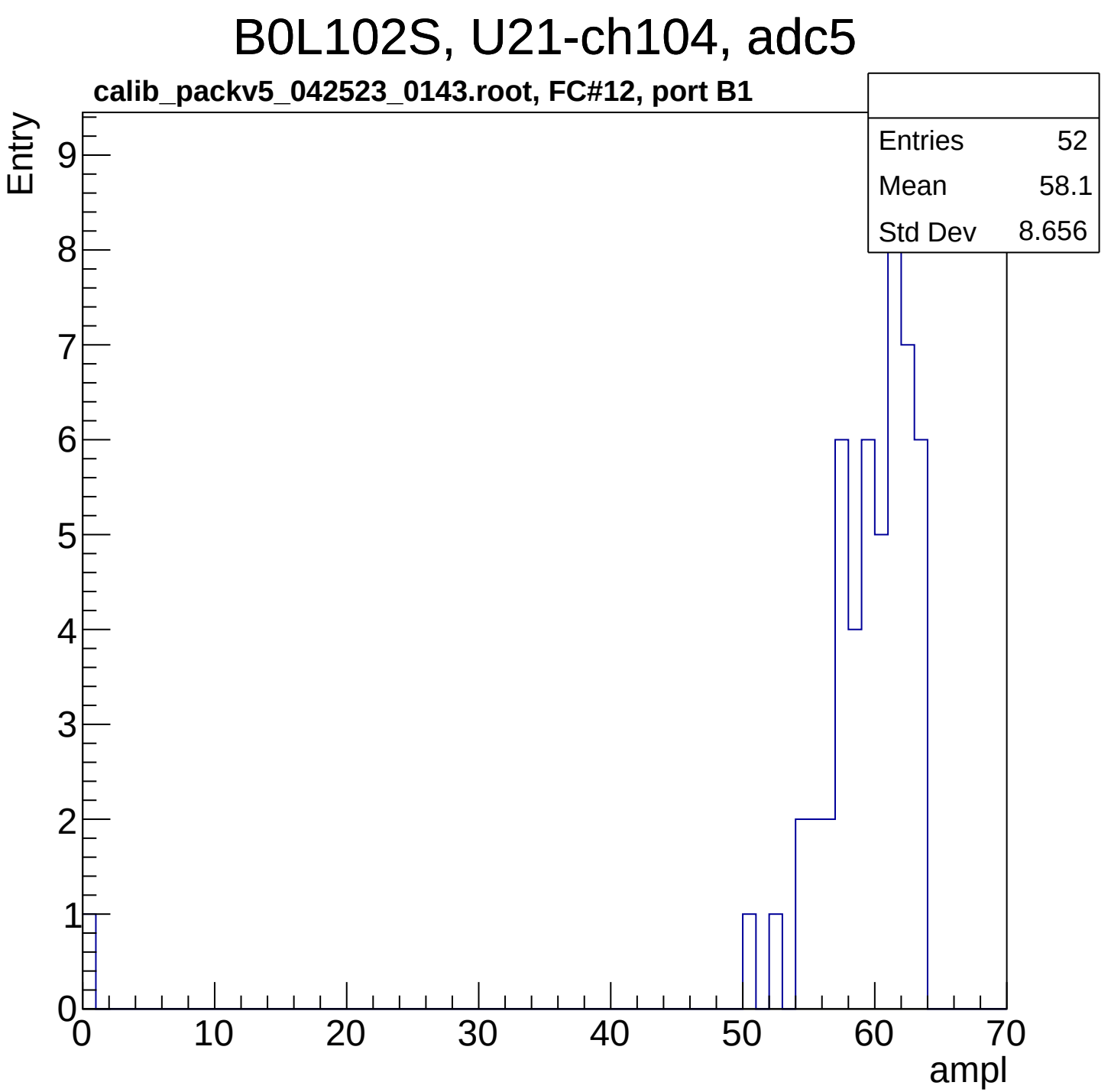
B0L102S, U21-ch104, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.1
Std Dev 8.656
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
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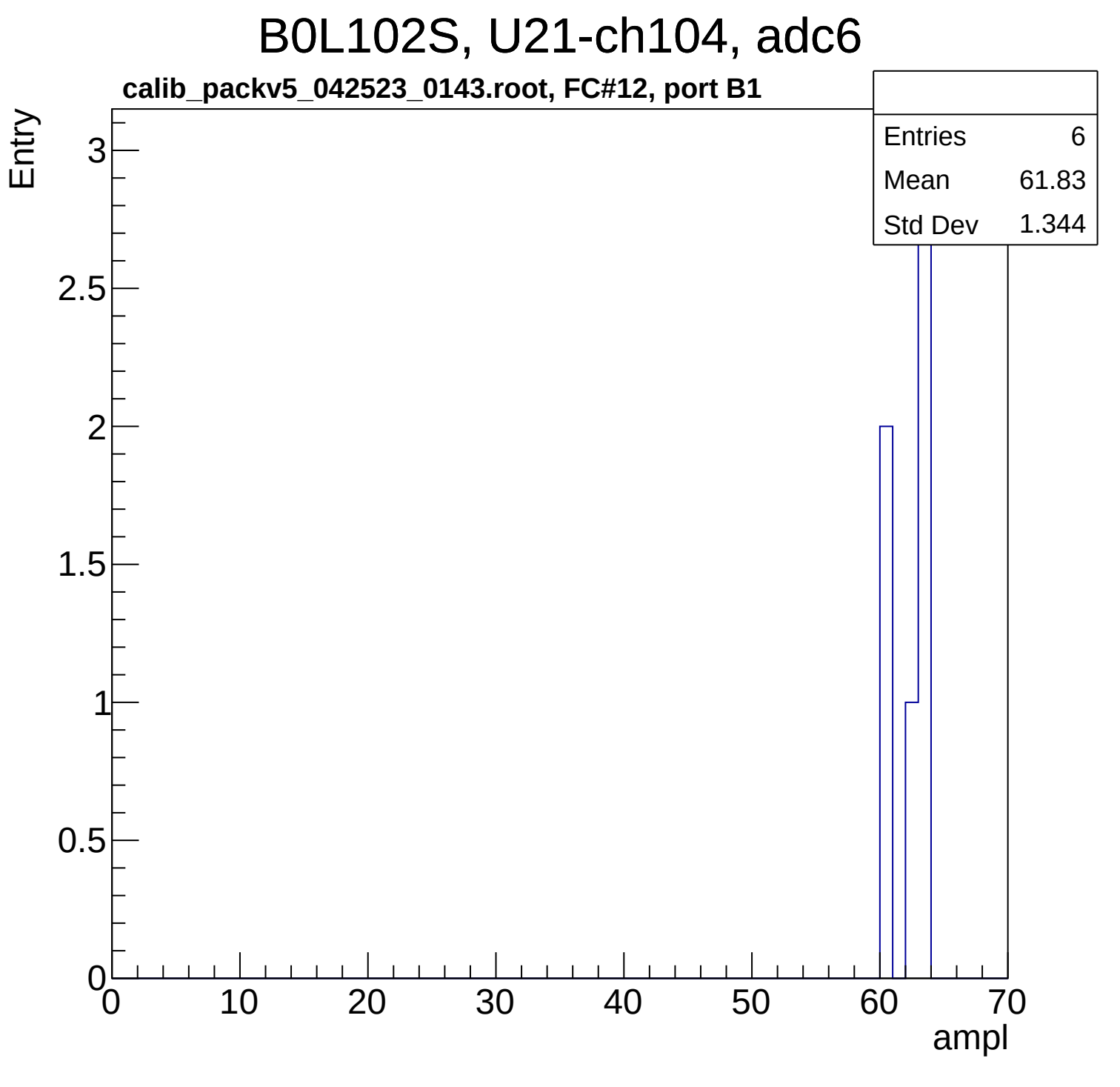
40
50
60
70
ampl


B0L102S, U21-ch104, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.83
Std Dev 1.344
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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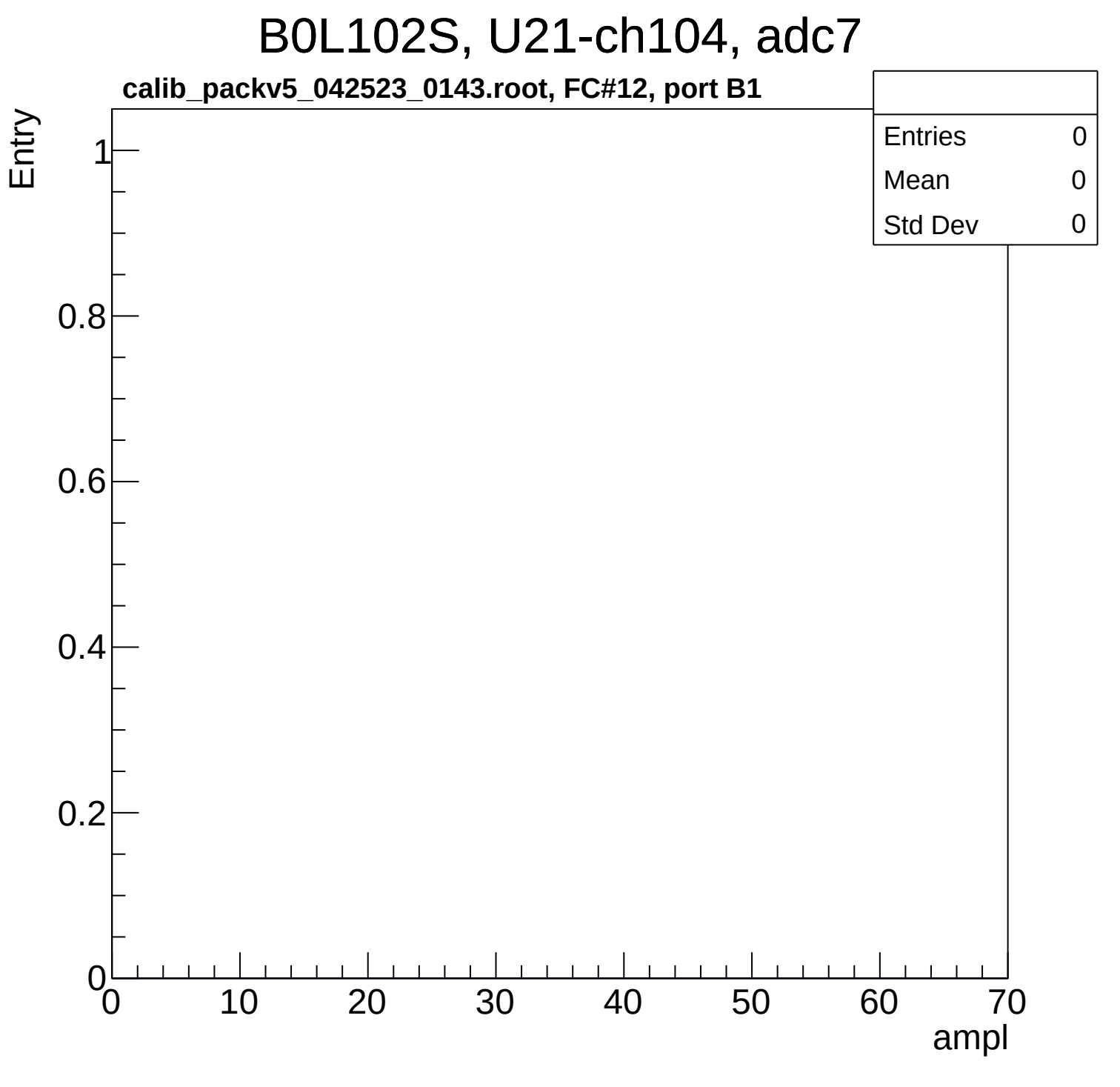
40
50
60
70


B0L102S, U21-ch104, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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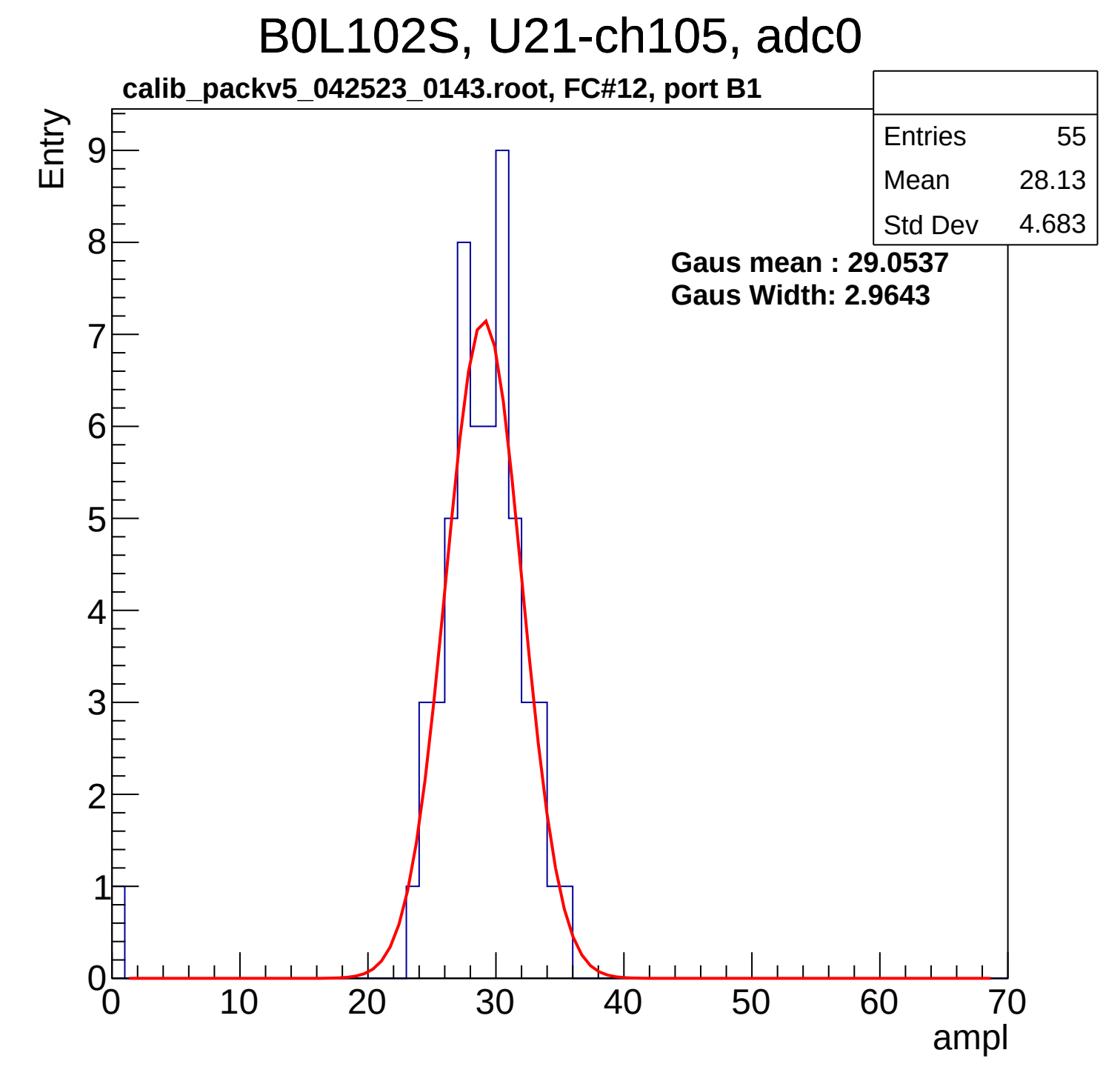
50
60
70
ampl


B0L102S, U21-ch105, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 28.13
Std Dev 4.683
Gaus mean : 29.0537
Gaus Width: 2.9643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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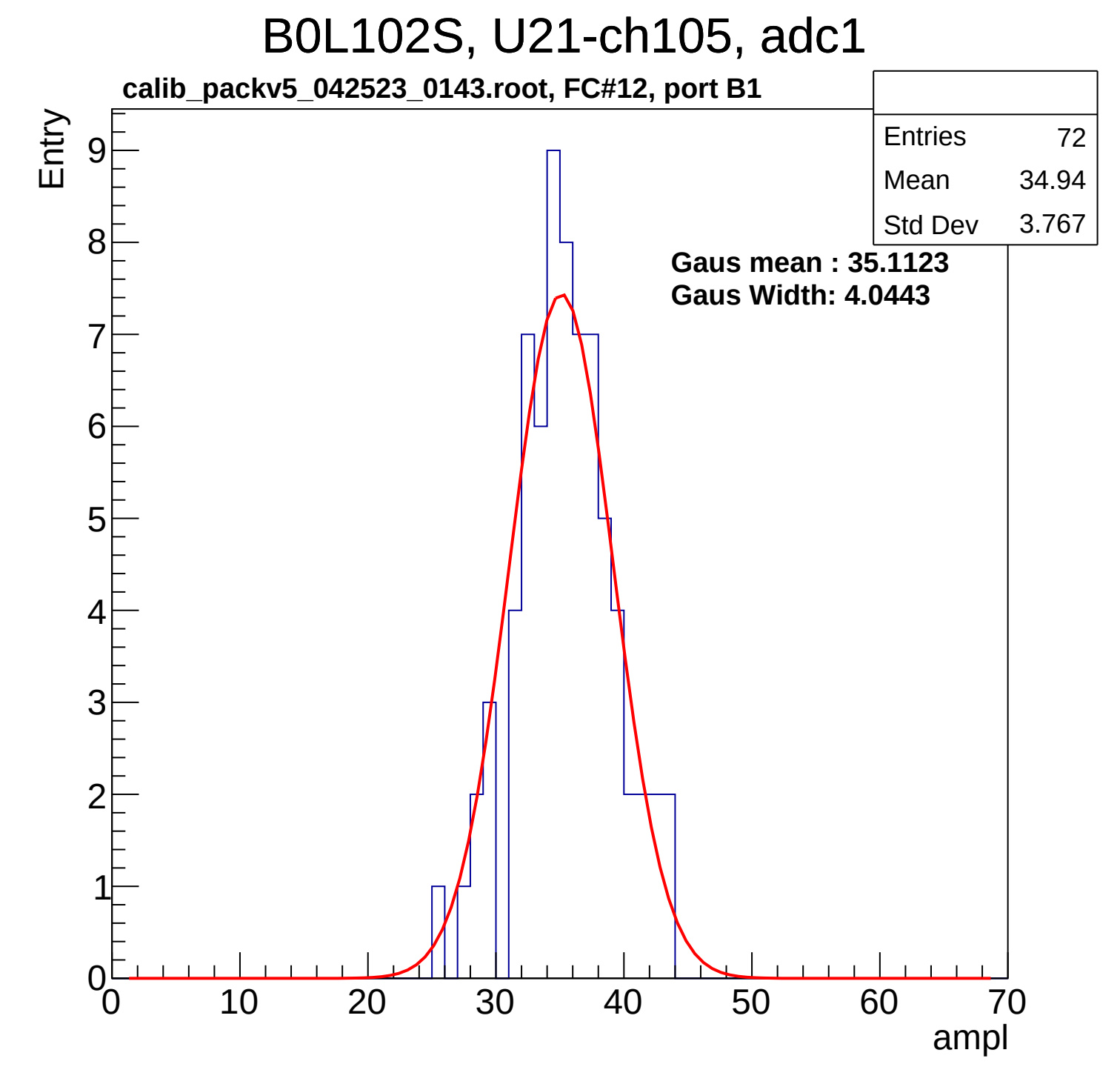
B0L102S, U21-ch105, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 34.94
Std Dev 3.767
Gaus mean : 35.1123
Gaus Width: 4.0443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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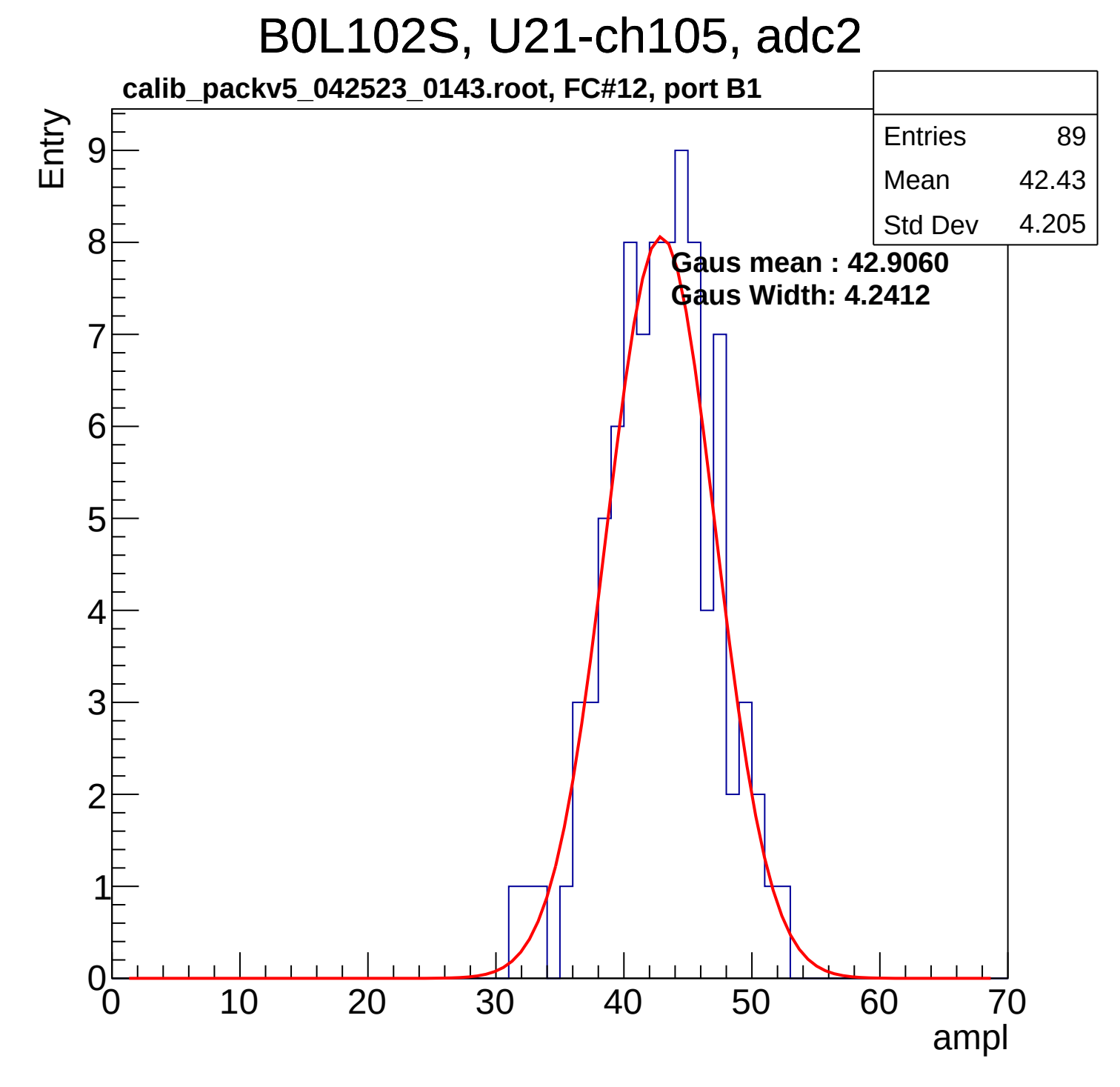
B0L102S, U21-ch105, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 42.43
Std Dev 4.205
Gaus mean : 42.9060
Gaus Width: 4.2412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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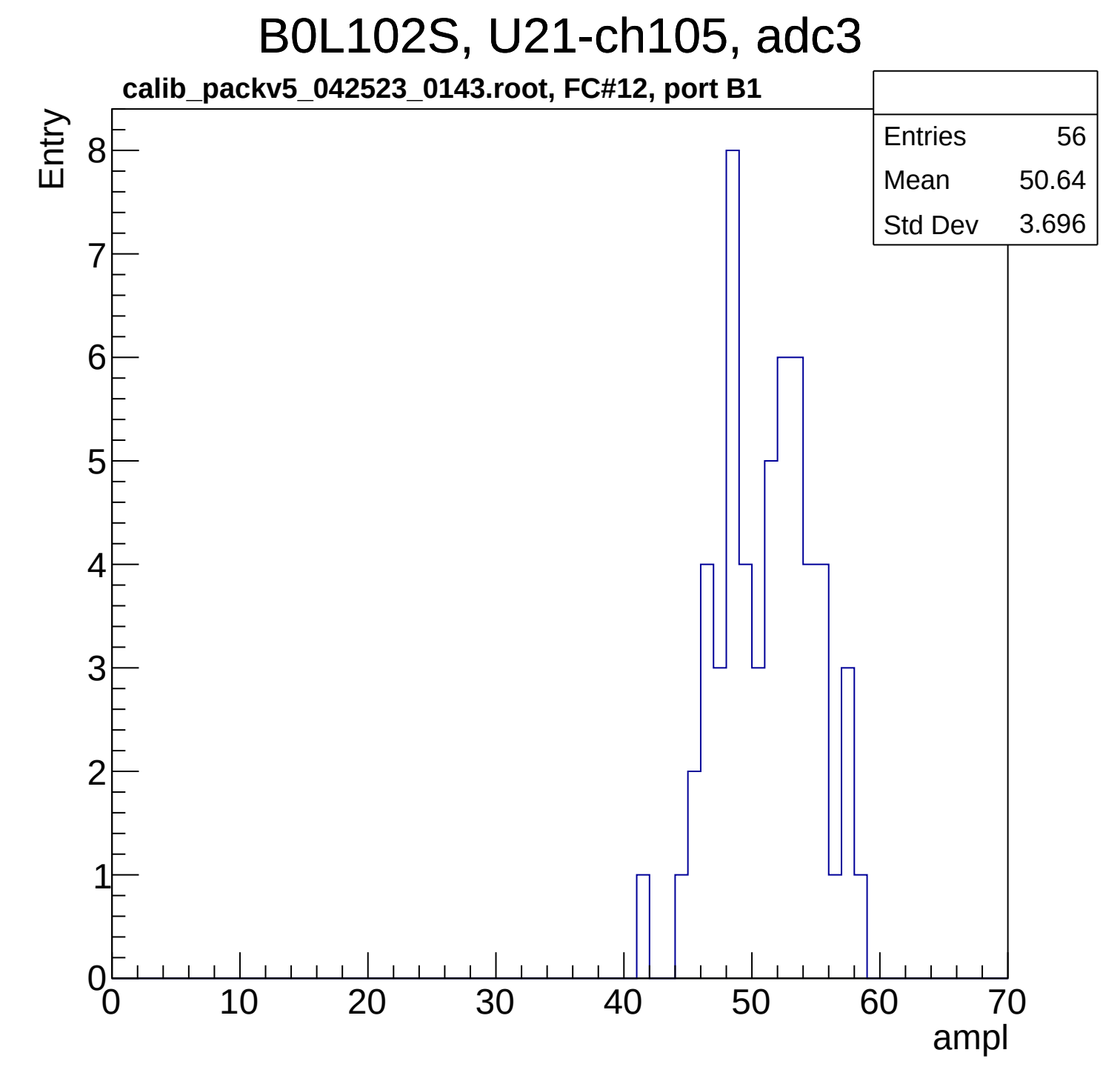
B0L102S, U21-ch105, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 50.64
Std Dev 3.696
Entry
ampl
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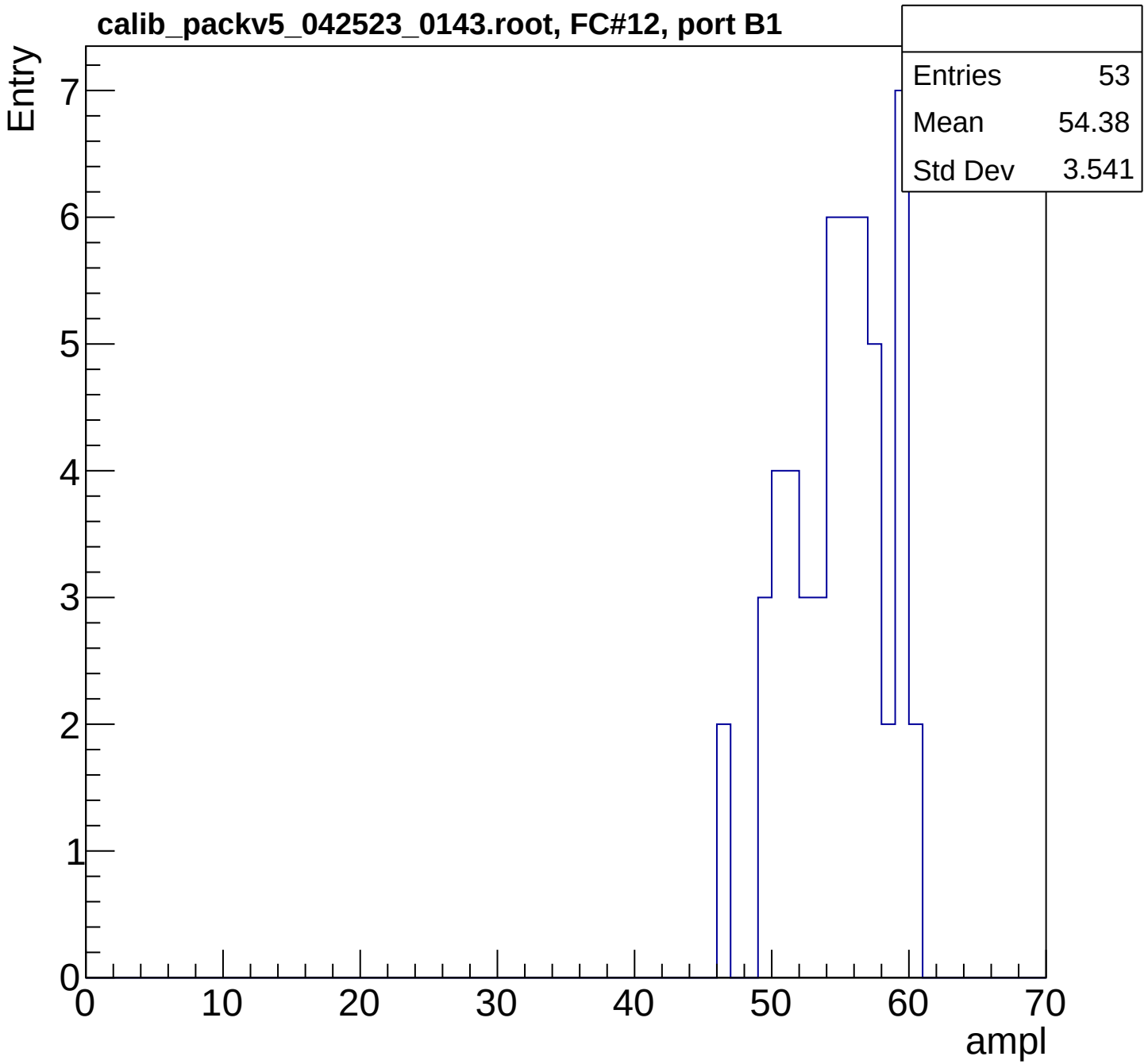
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch105, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 54.38
Std Dev 3.541
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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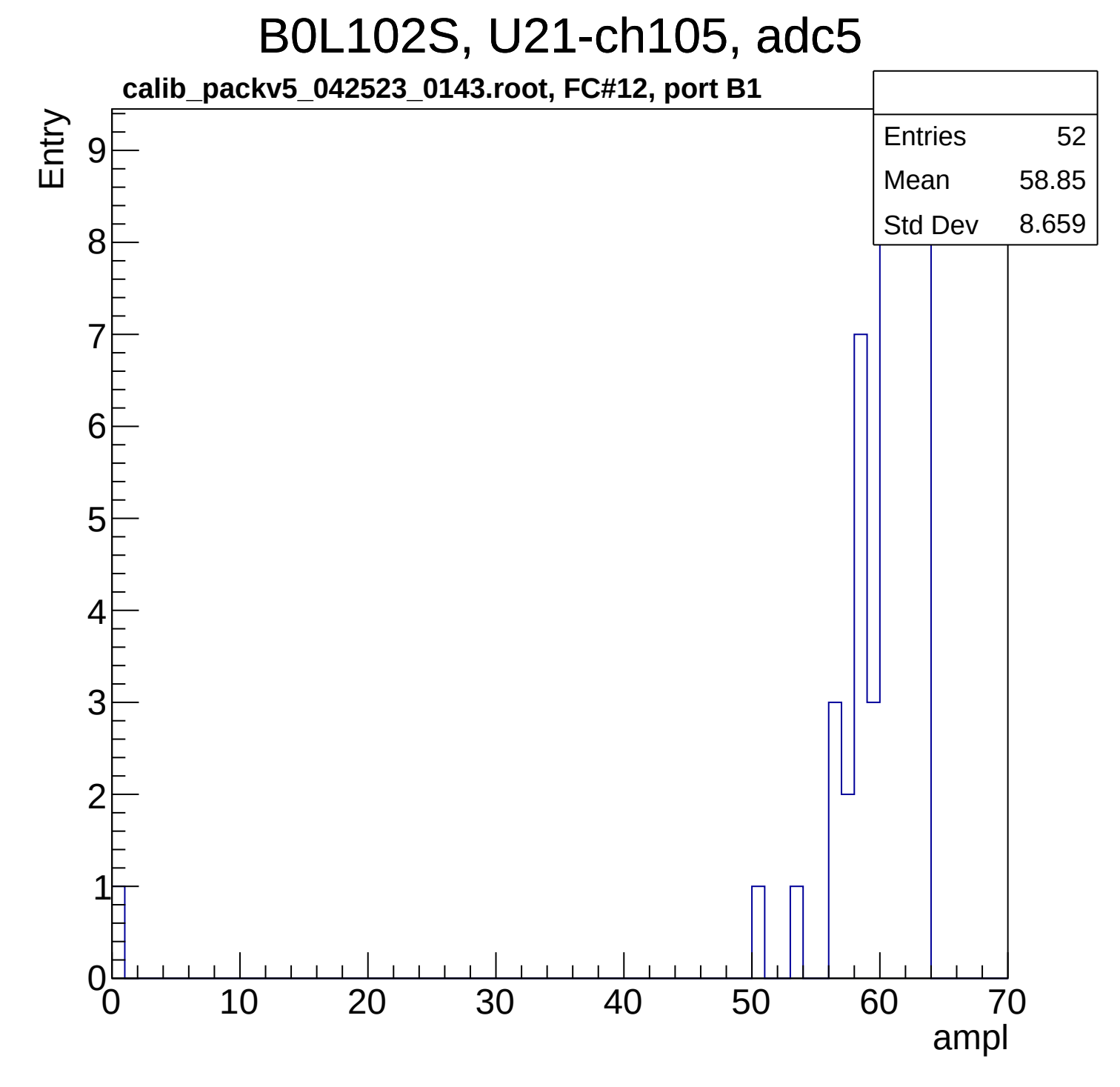
B0L102S, U21-ch105, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.85
Std Dev 8.659
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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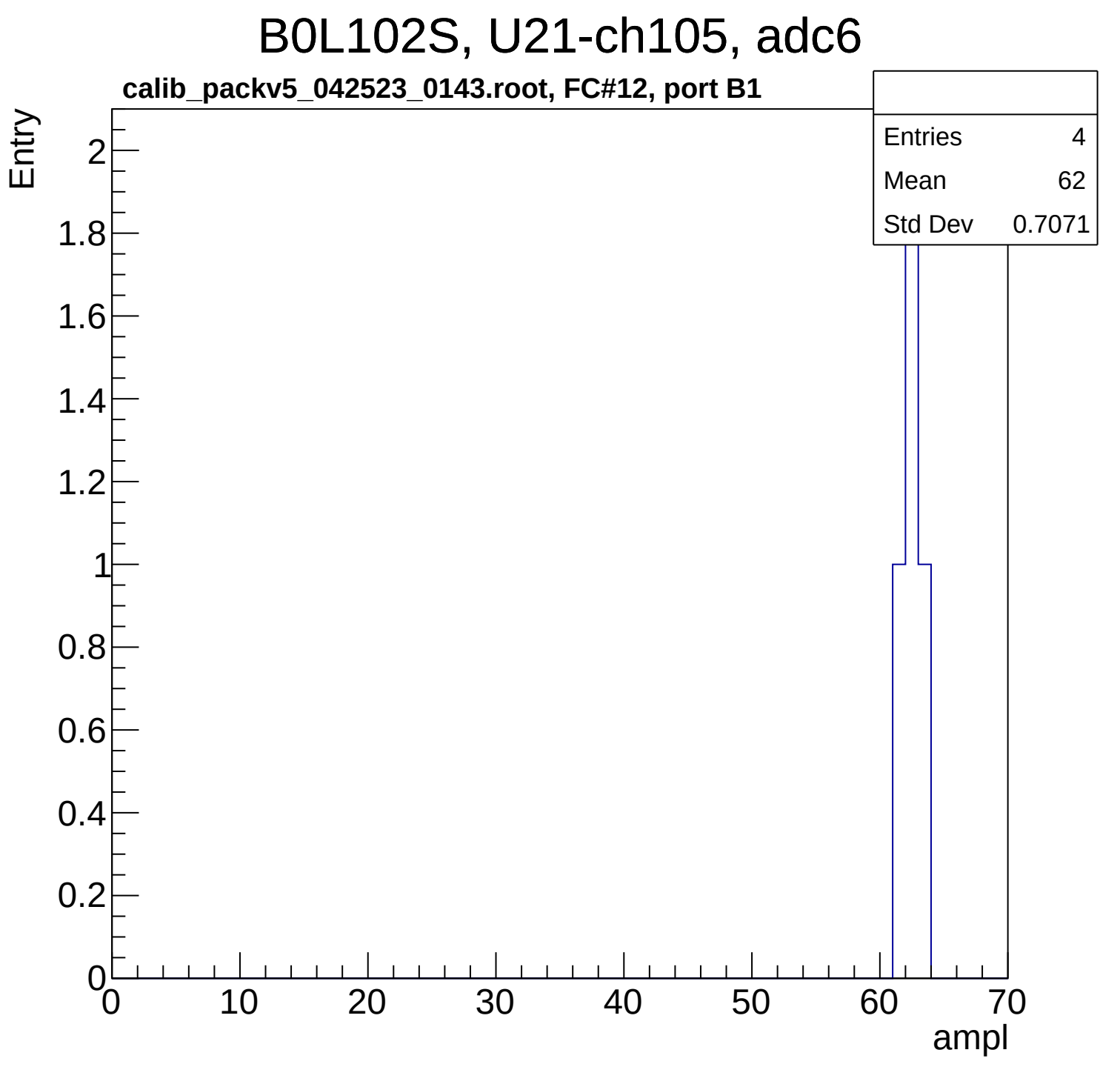
B0L102S, U21-ch105, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
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60
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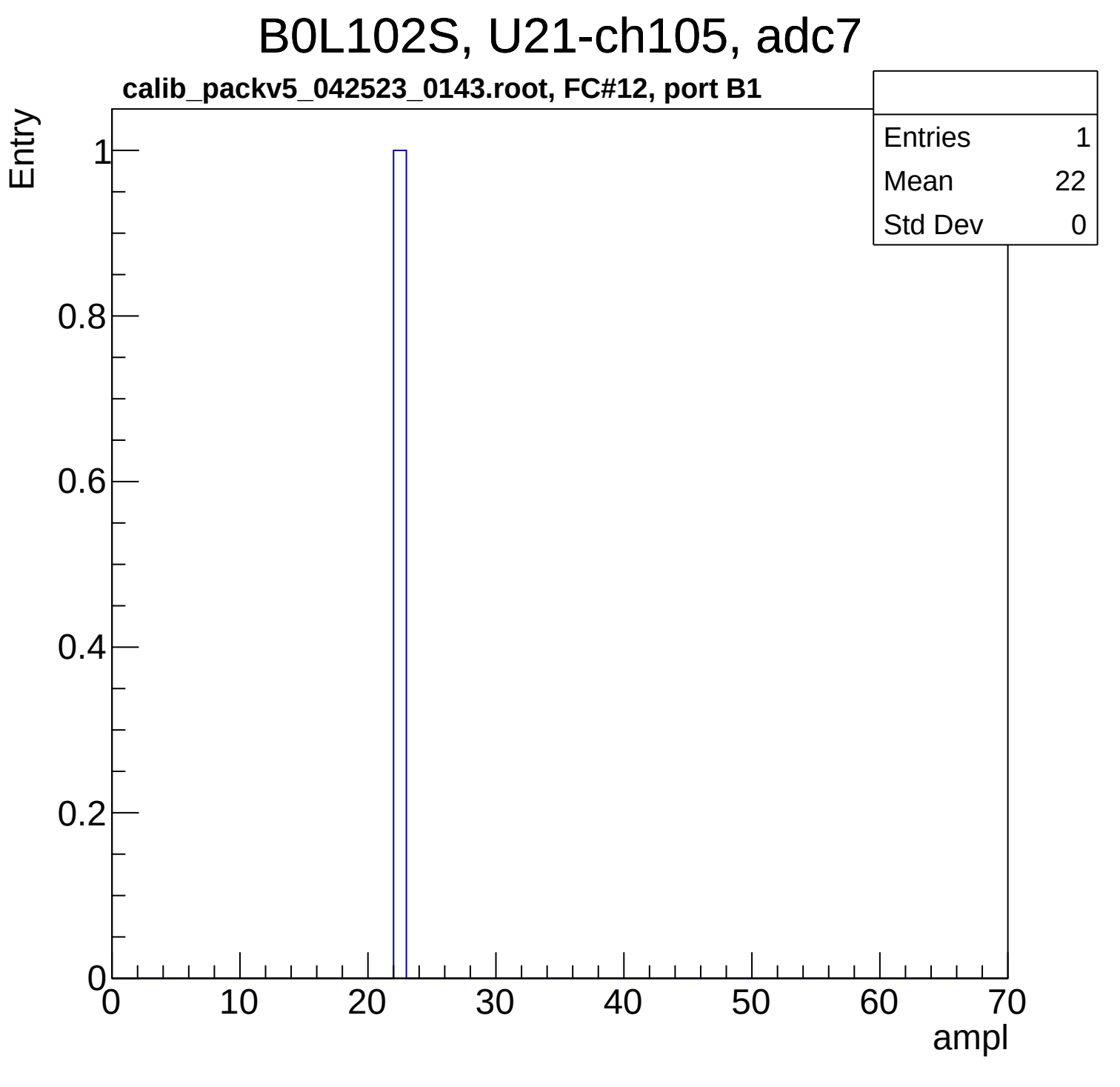
B0L102S, U21-ch105, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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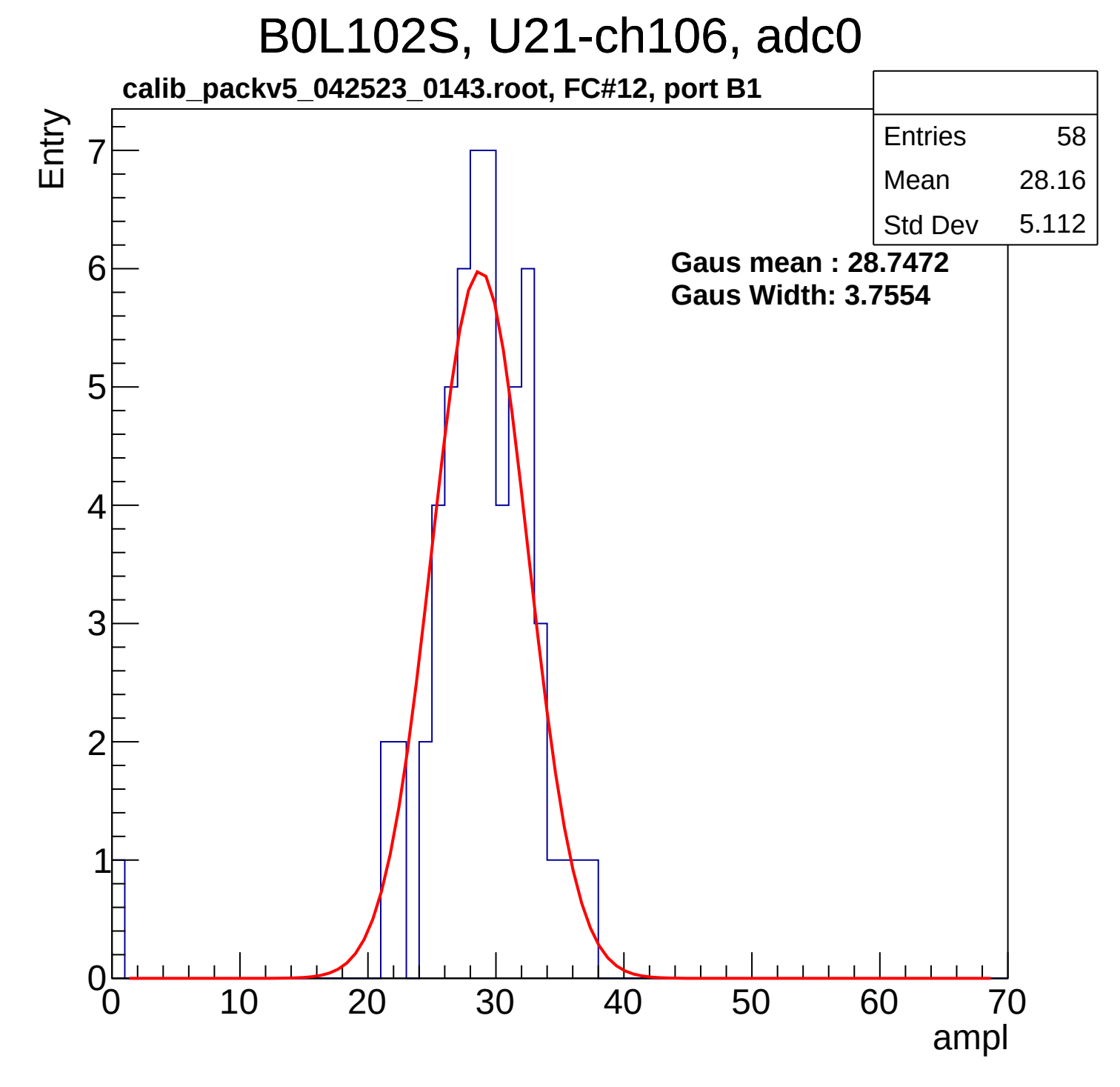
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch106, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 28.16
Std Dev 5.112
Gaus mean : 28.7472
Gaus Width: 3.7554
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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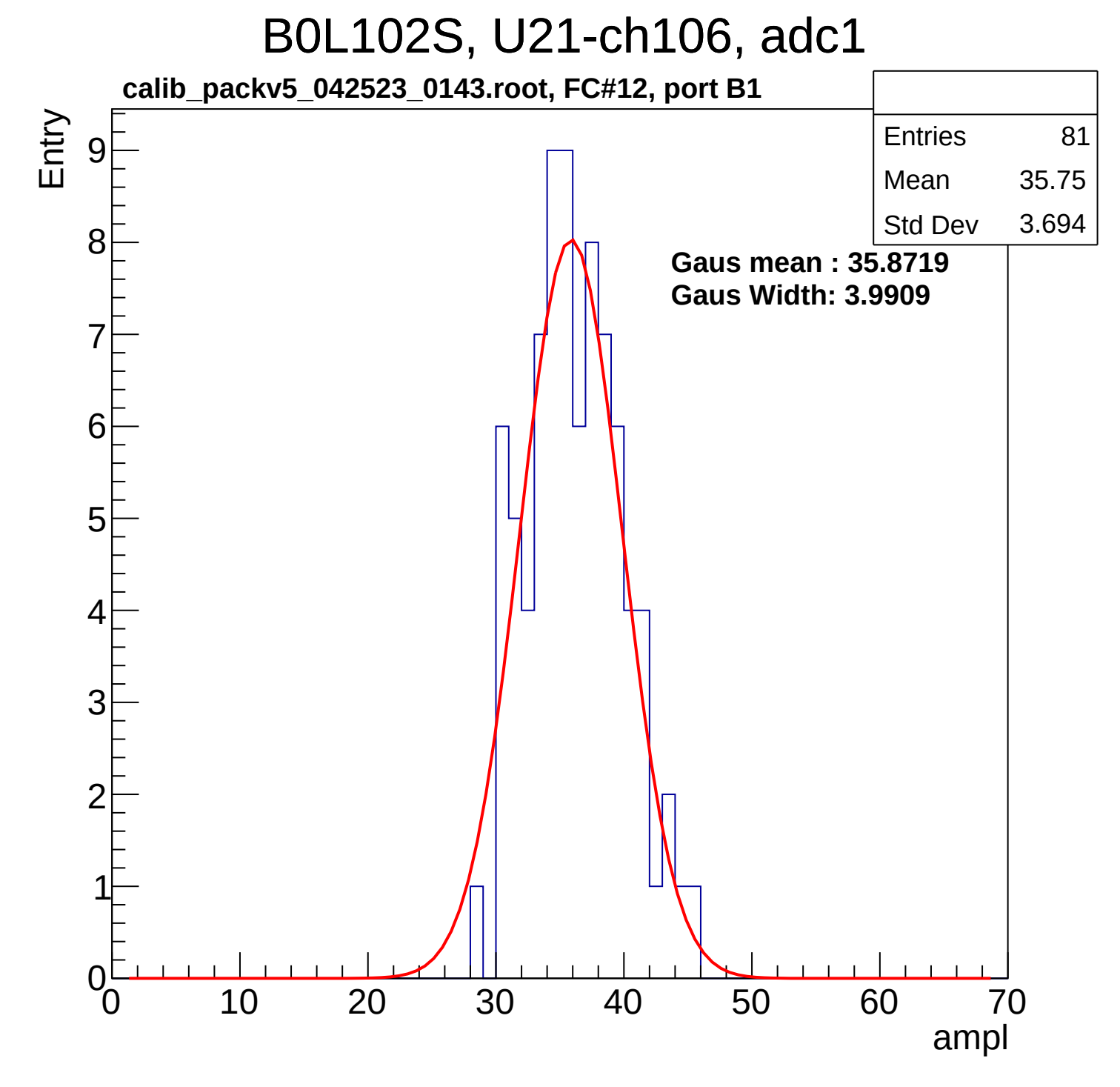
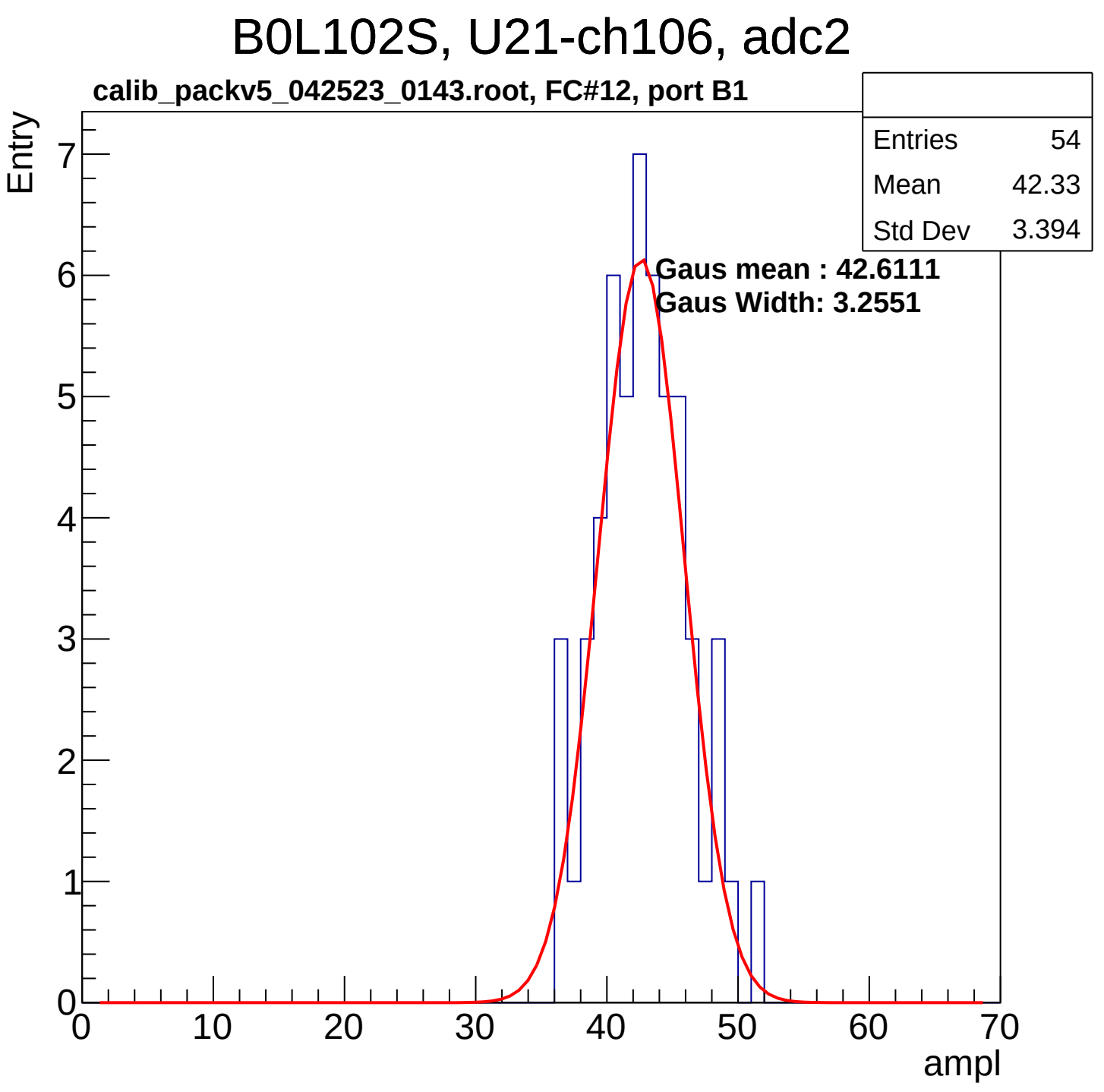

B0L102S, U21-ch106, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 35.75
Std Dev 3.694
Gaus mean : 35.8719
Gaus Width: 3.9909
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch106, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 42.33
Std Dev 3.394
Gaus mean : 42.6111
Gaus Width: 3.2551
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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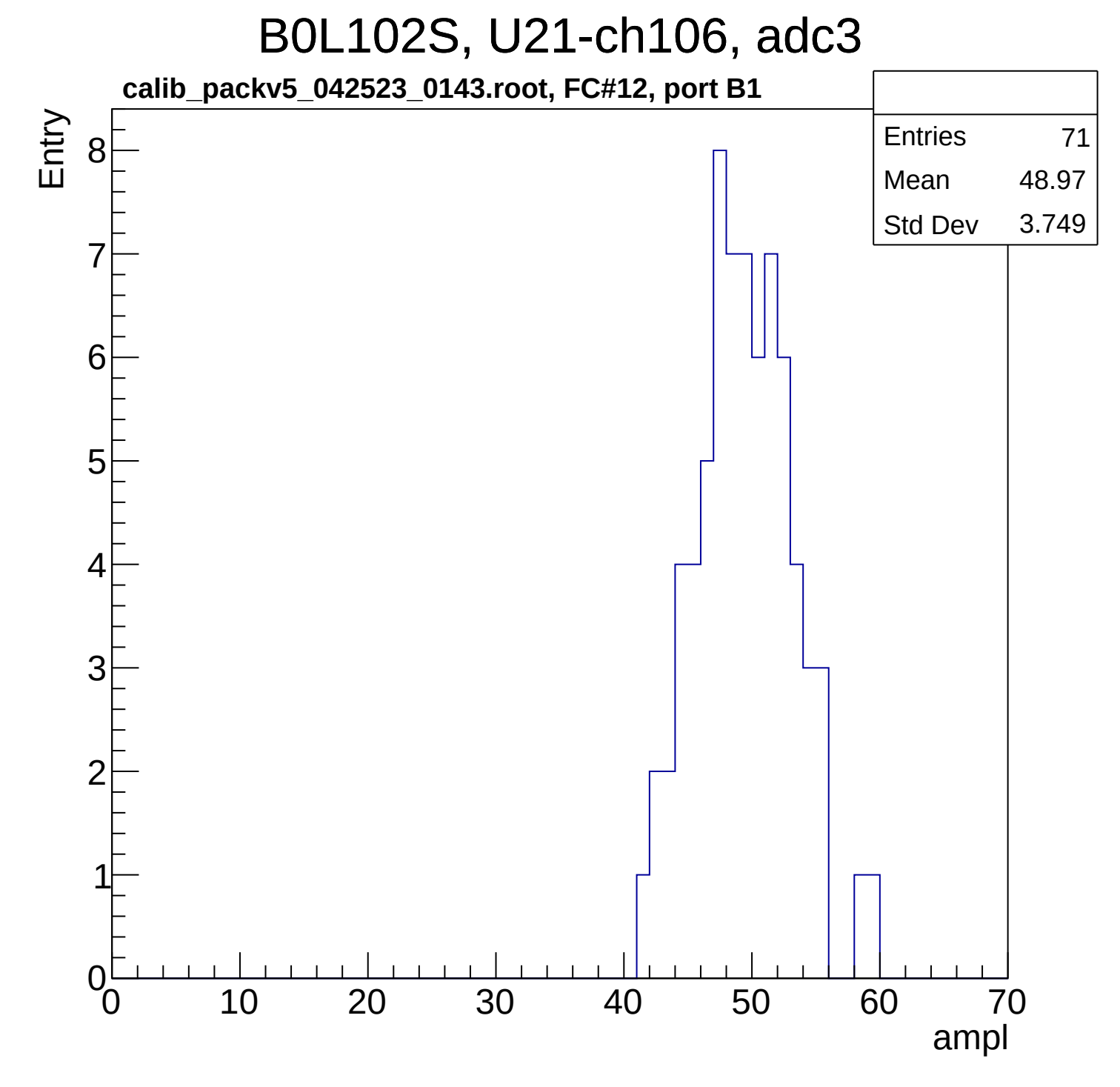
B0L102S, U21-ch106, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 48.97
Std Dev 3.749
Entry
ampl
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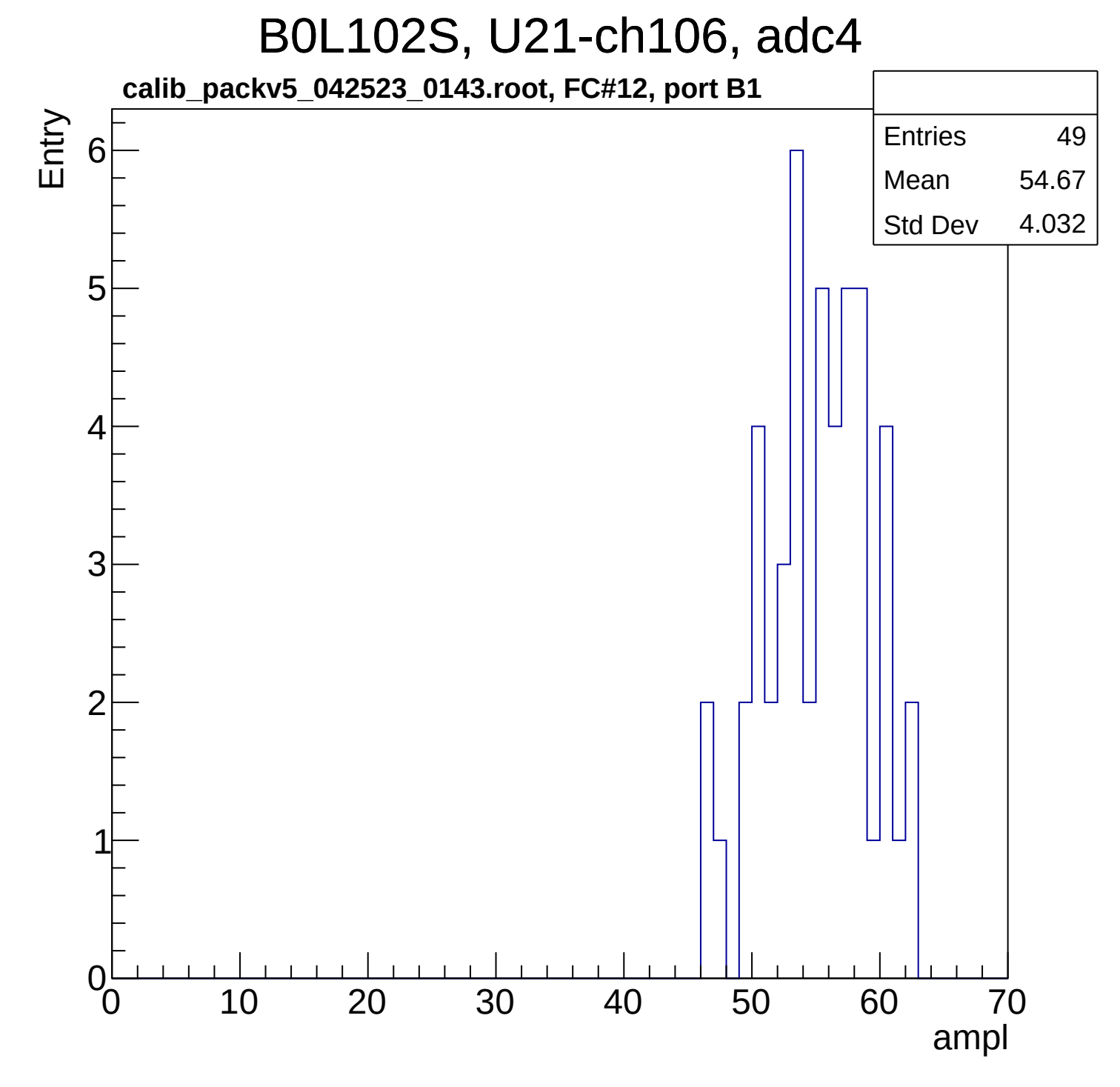
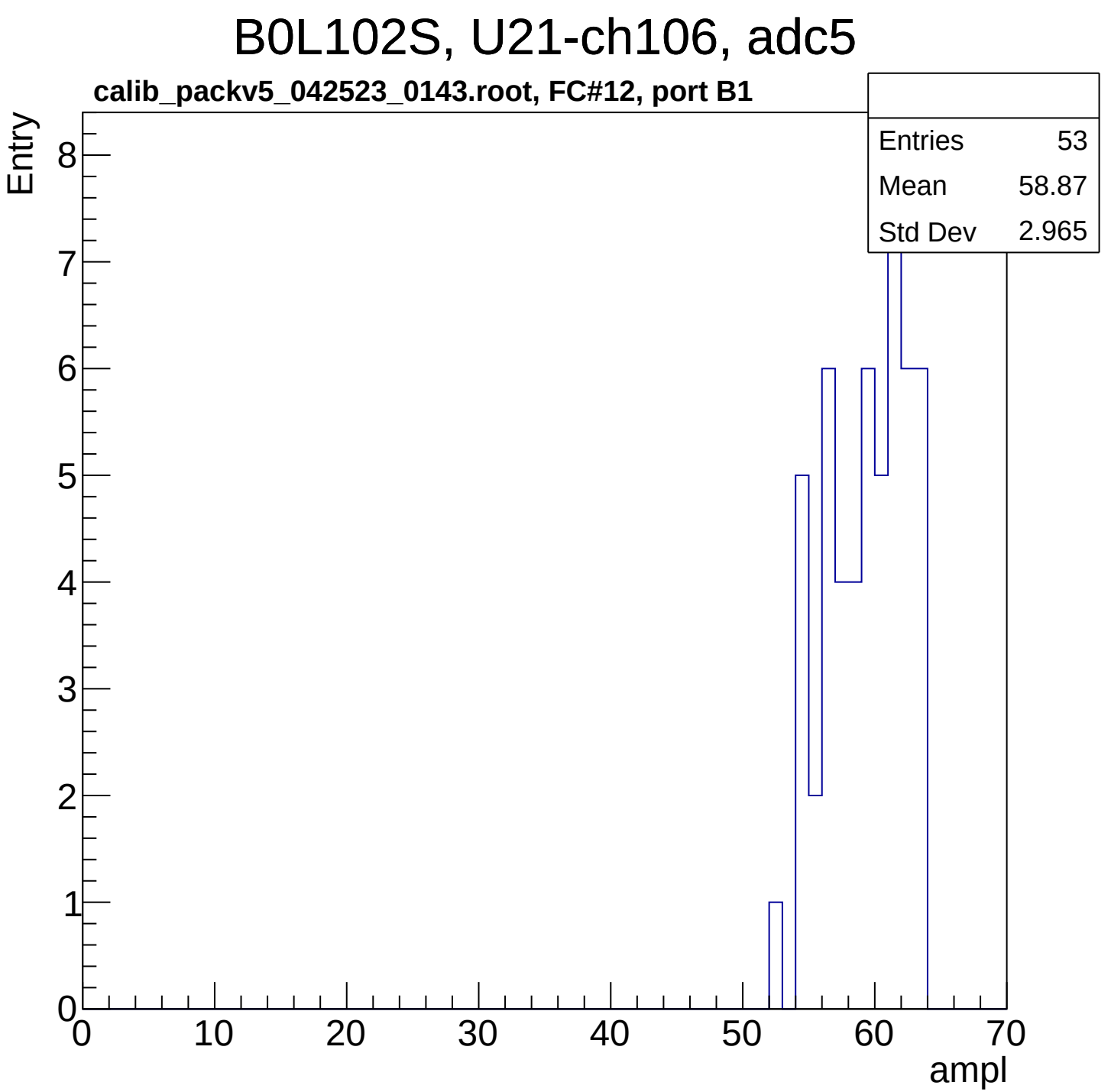
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch106, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 54.67
Std Dev 4.032
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch106, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 58.87
Std Dev 2.965
Entry
ampl
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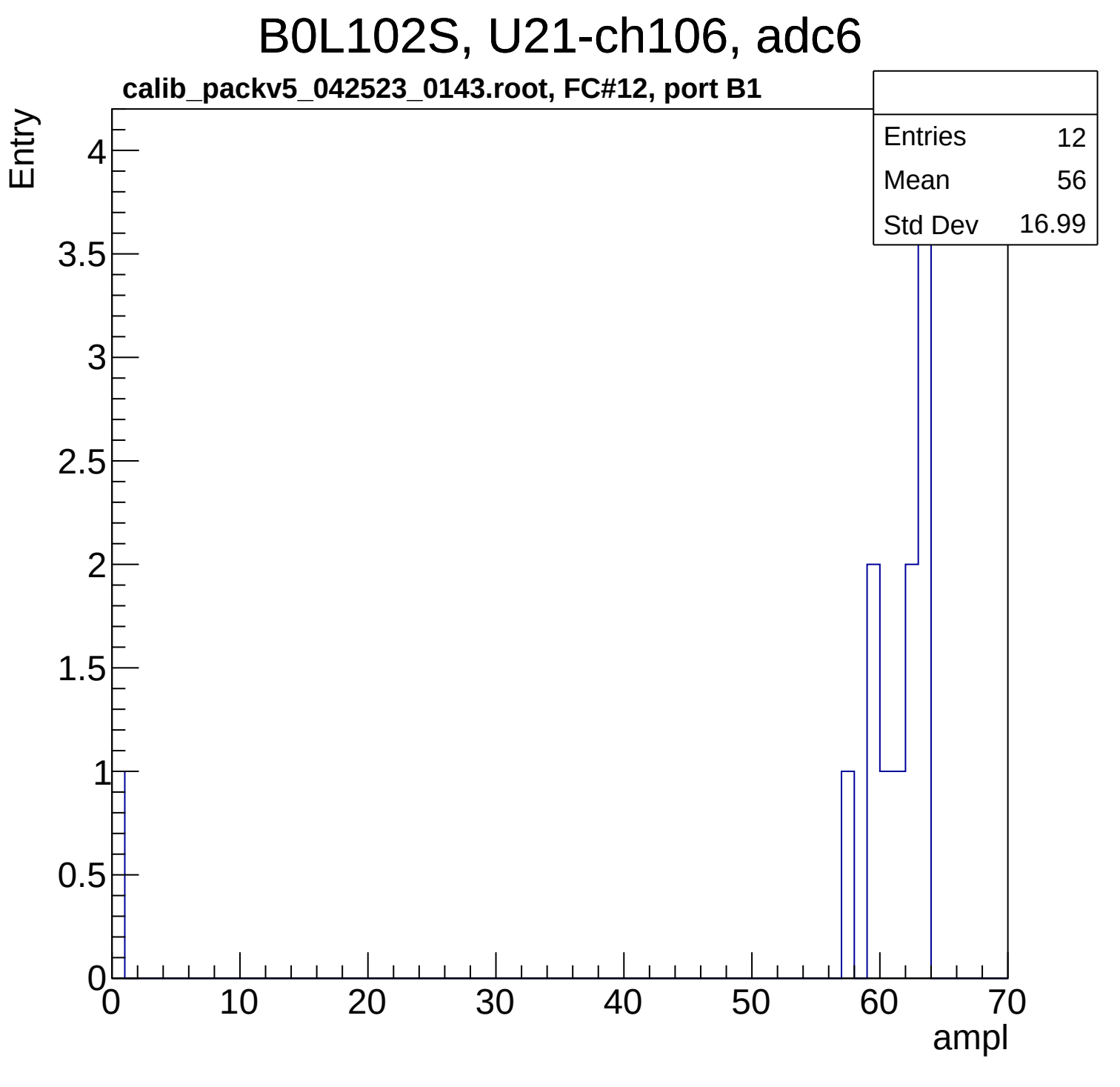
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch106, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 56
Std Dev 16.99
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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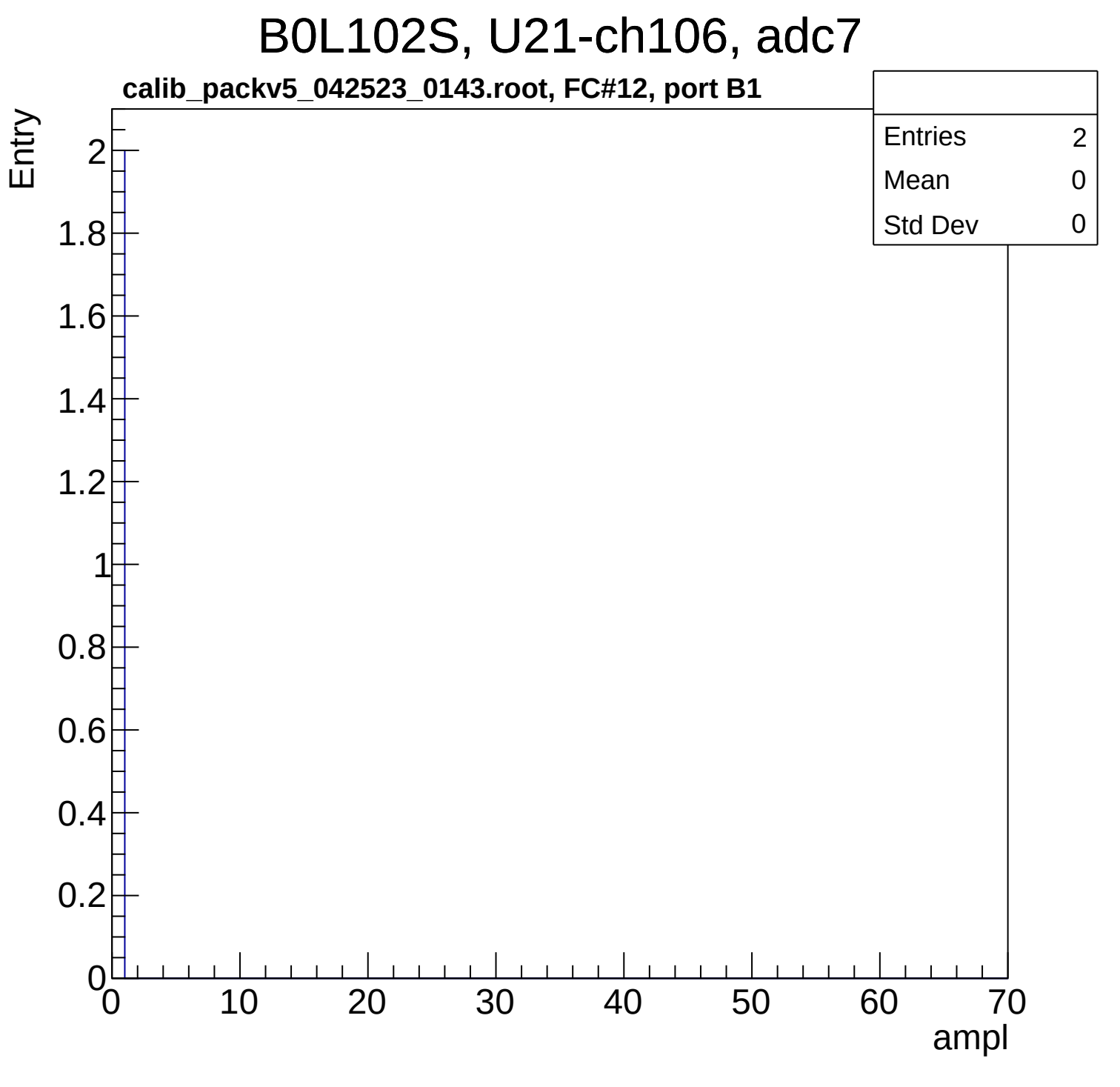
40
50
60
70


B0L102S, U21-ch106, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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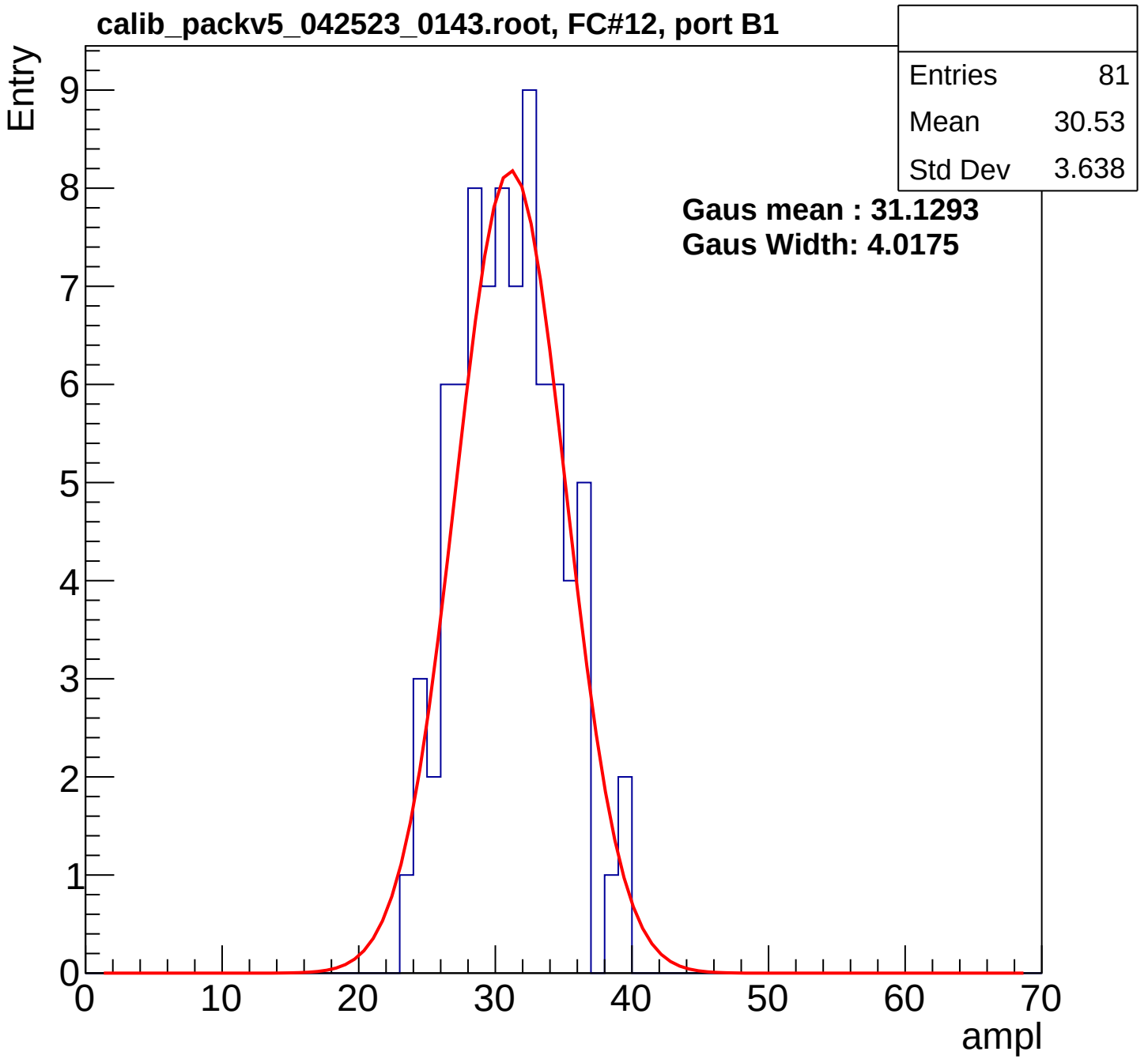
B0L102S, U21-ch107, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 30.53
Std Dev 3.638
Gaus mean : 31.1293
Gaus Width: 4.0175
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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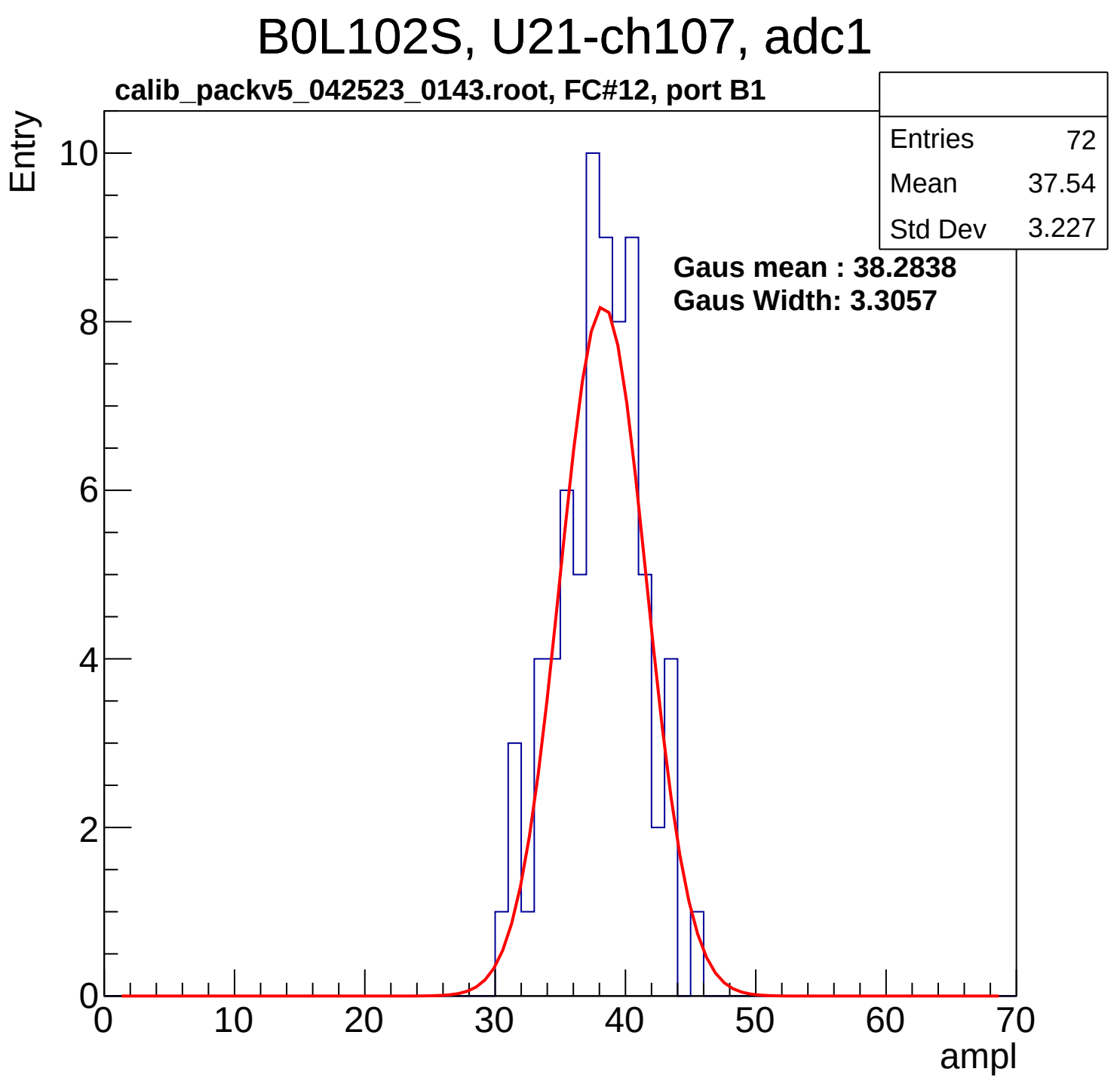
60
70


B0L102S, U21-ch107, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 37.54
Std Dev 3.227
Gaus mean : 38.2838
Gaus Width: 3.3057
Entry
ampl
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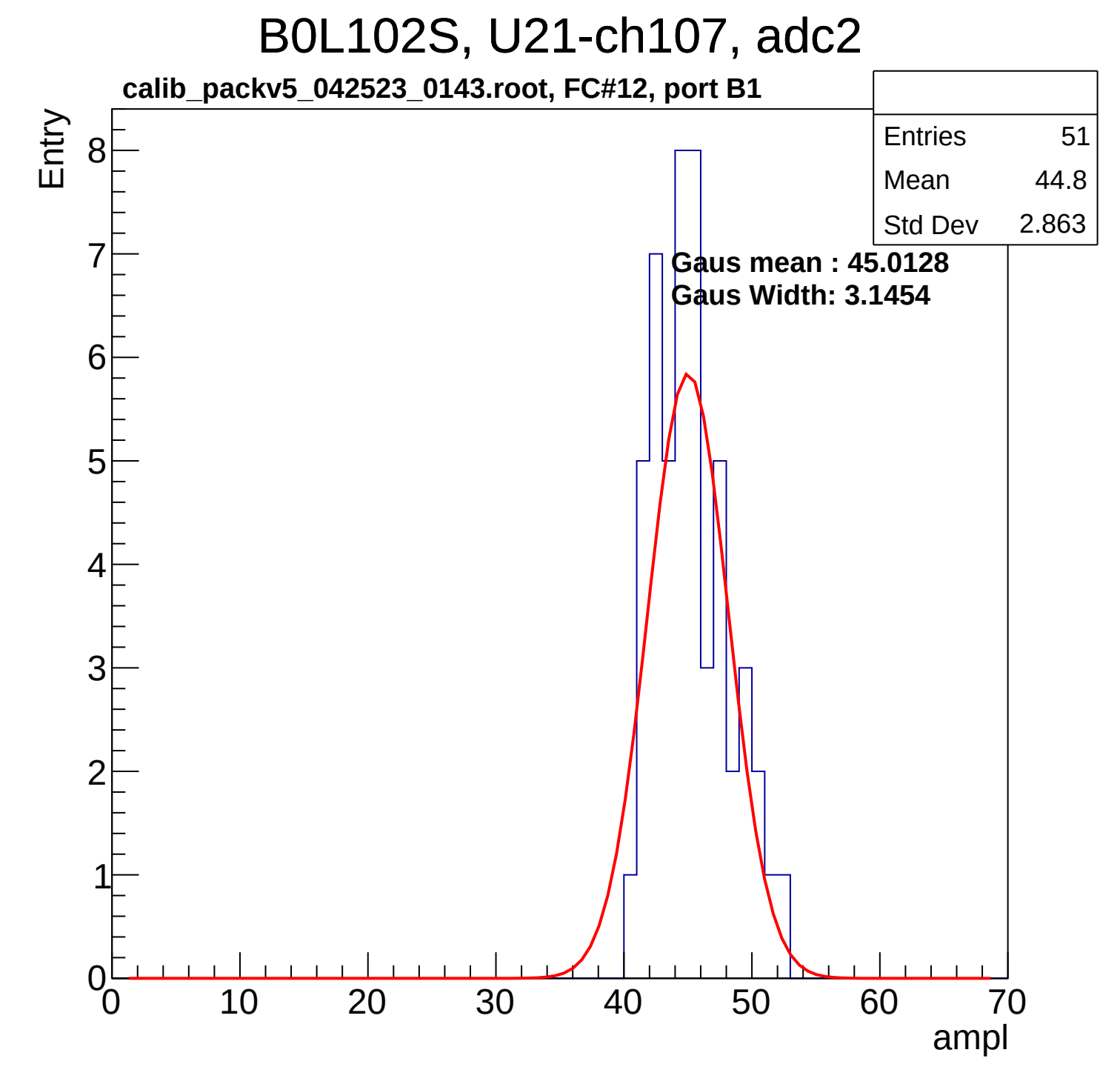
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch107, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 44.8
Std Dev 2.863
Gaus mean : 45.0128
Gaus Width: 3.1454
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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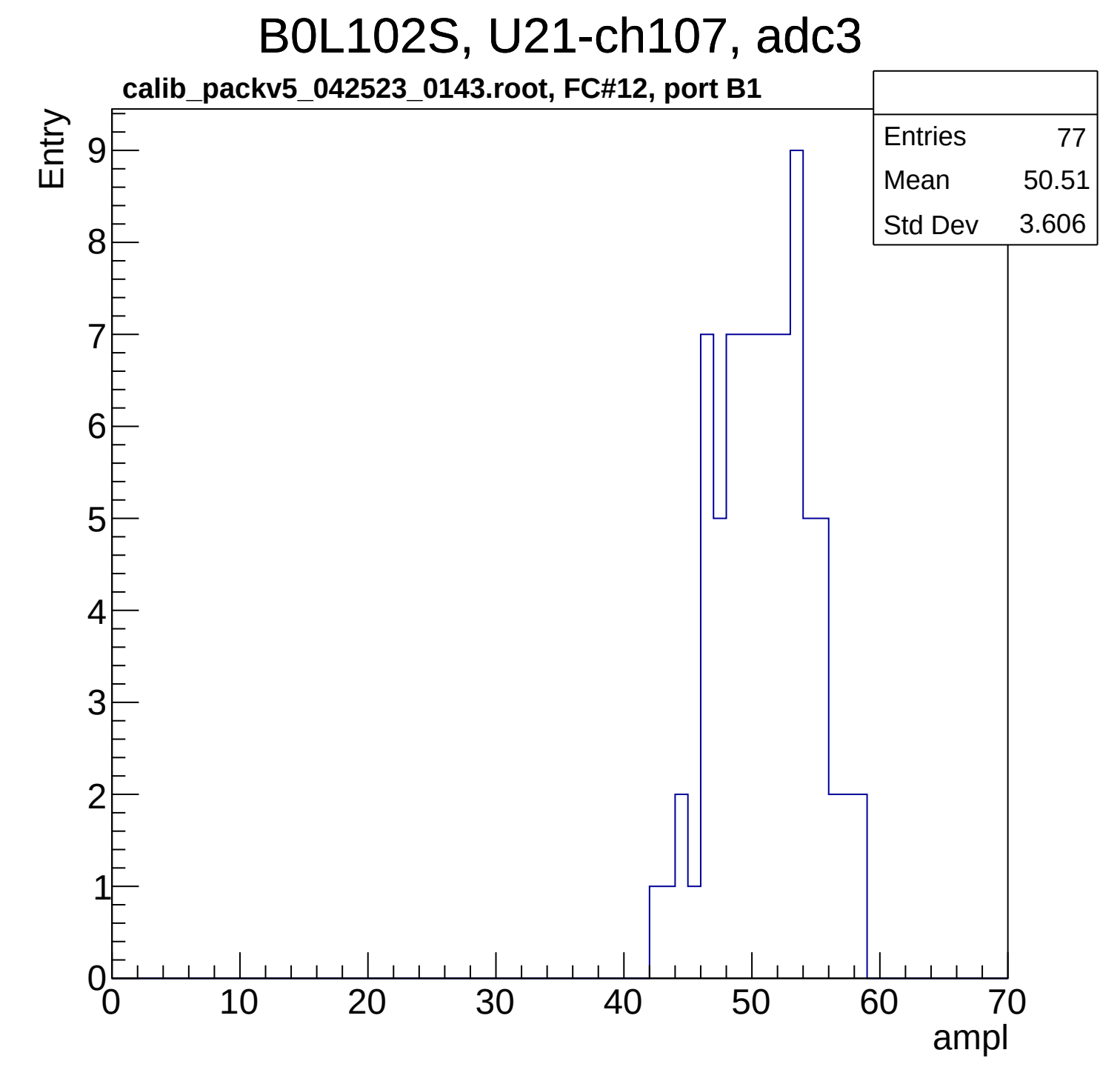
B0L102S, U21-ch107, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 50.51
Std Dev 3.606
Entry
ampl
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3
4
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6
7
8
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0
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60
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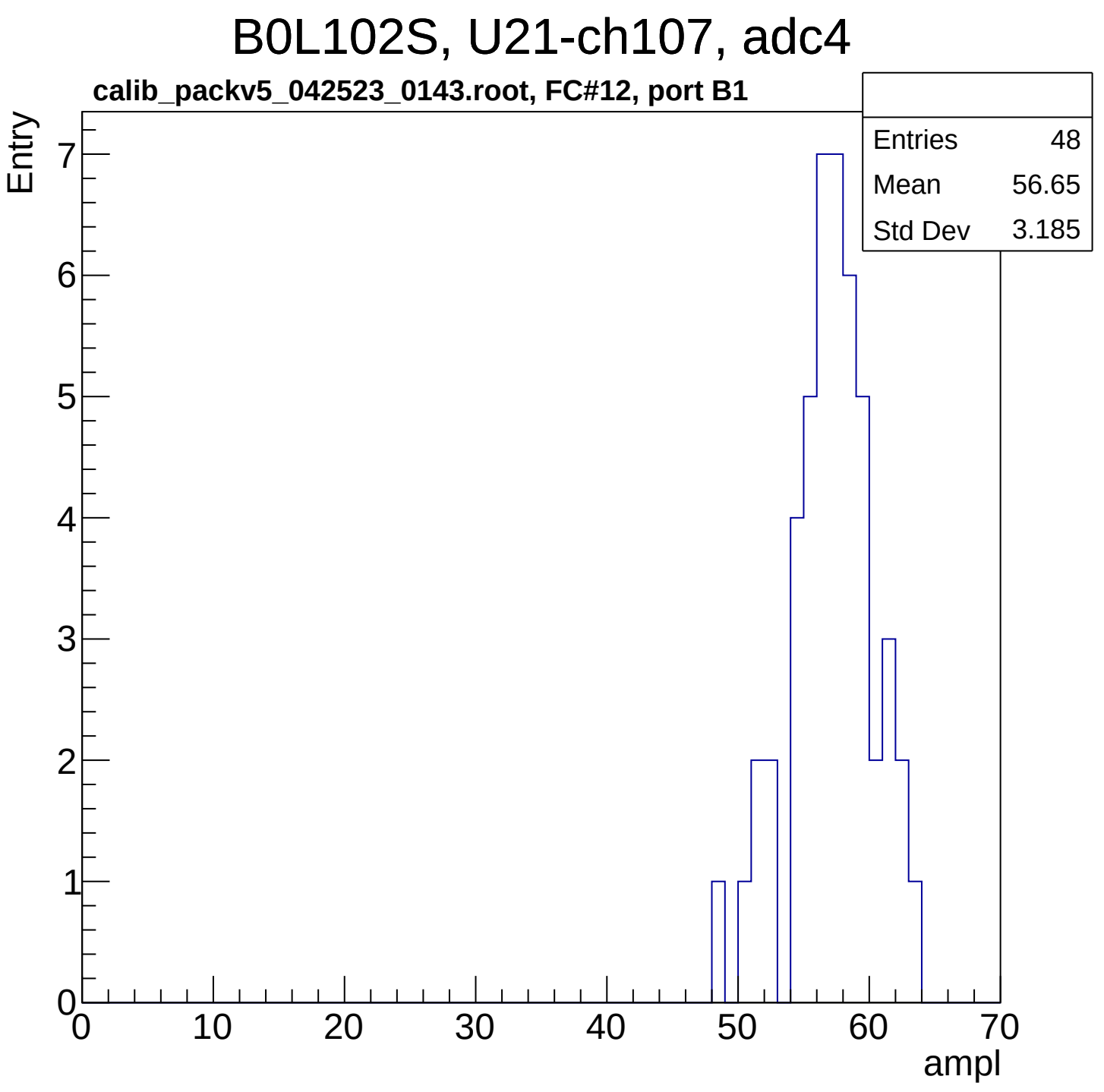
B0L102S, U21-ch107, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 56.65
Std Dev 3.185
Entry
ampl
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1
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4
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60
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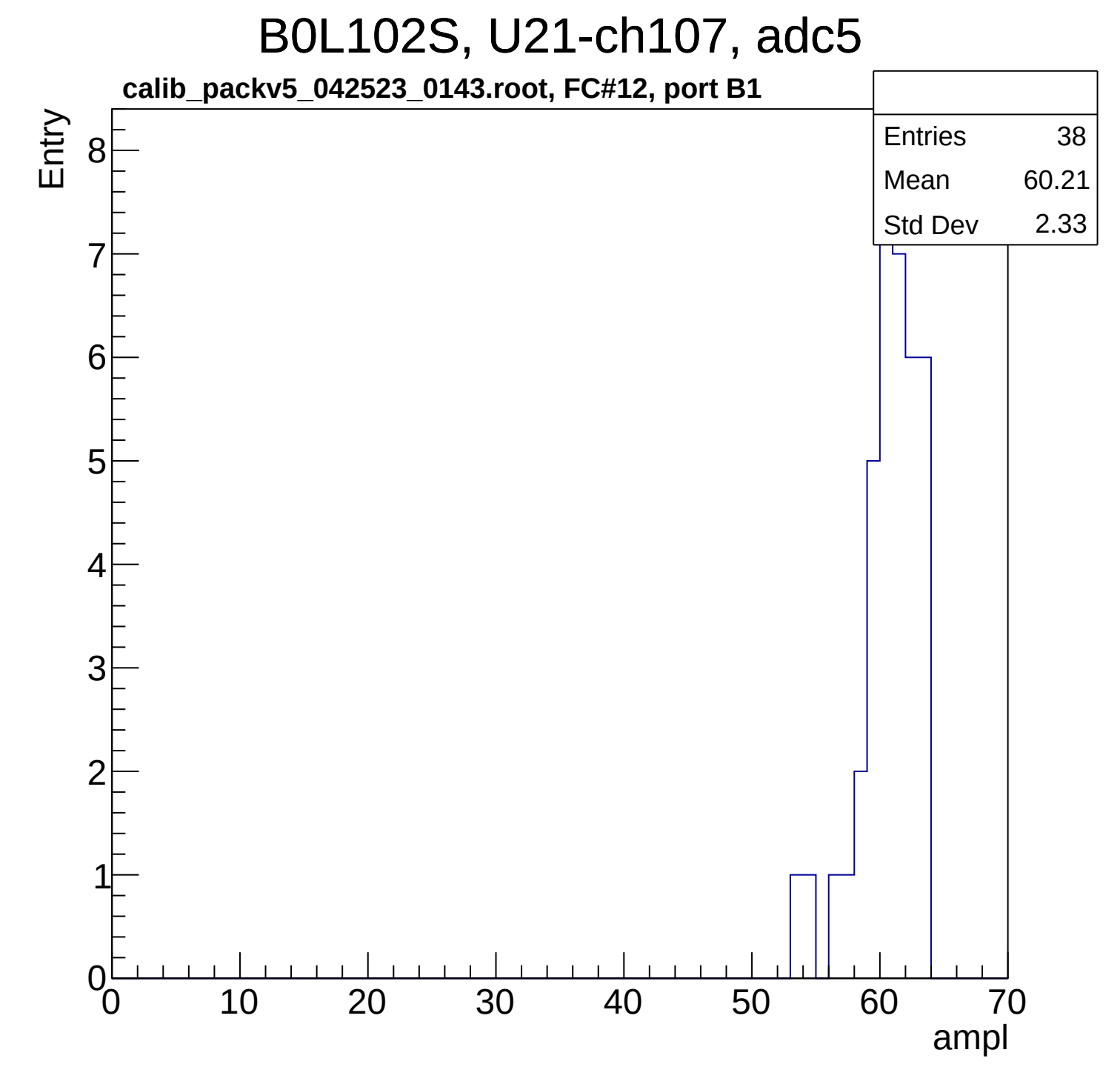
B0L102S, U21-ch107, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 60.21
Std Dev 2.33
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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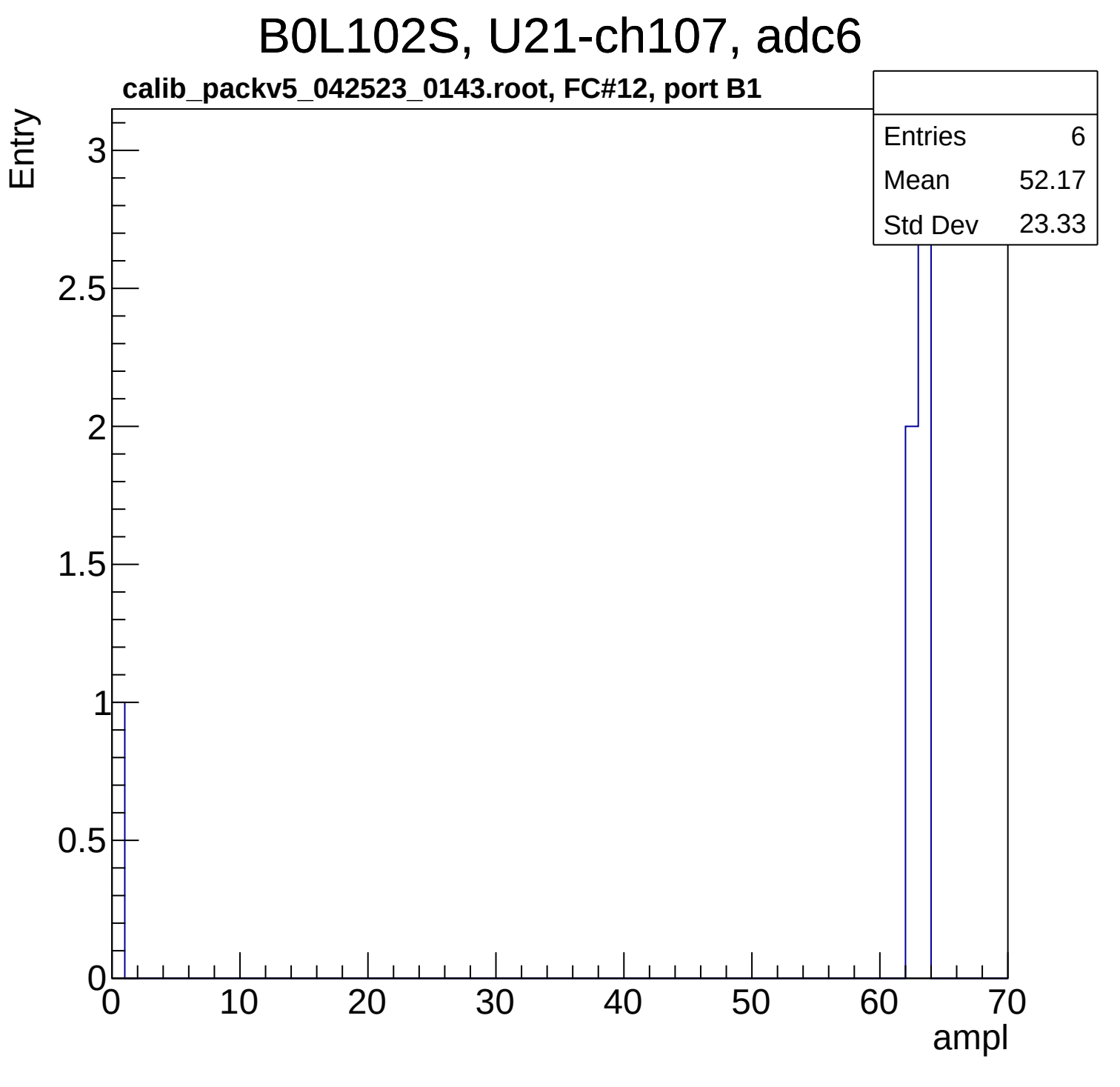
B0L102S, U21-ch107, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 52.17
Std Dev 23.33
Entry
ampl
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0.5
1
1.5
2
2.5
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0
10
20
30
40
50
60
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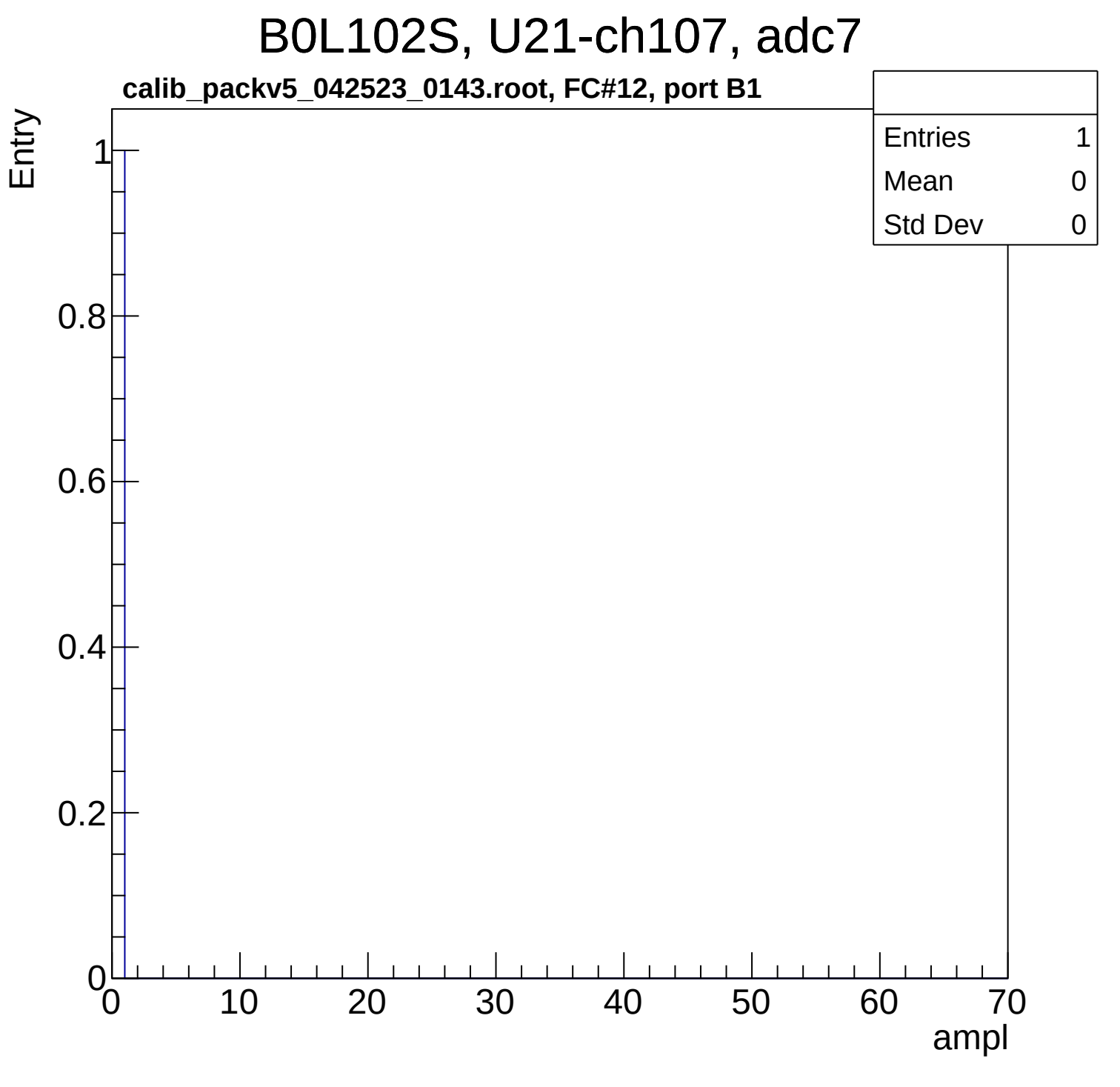
B0L102S, U21-ch107, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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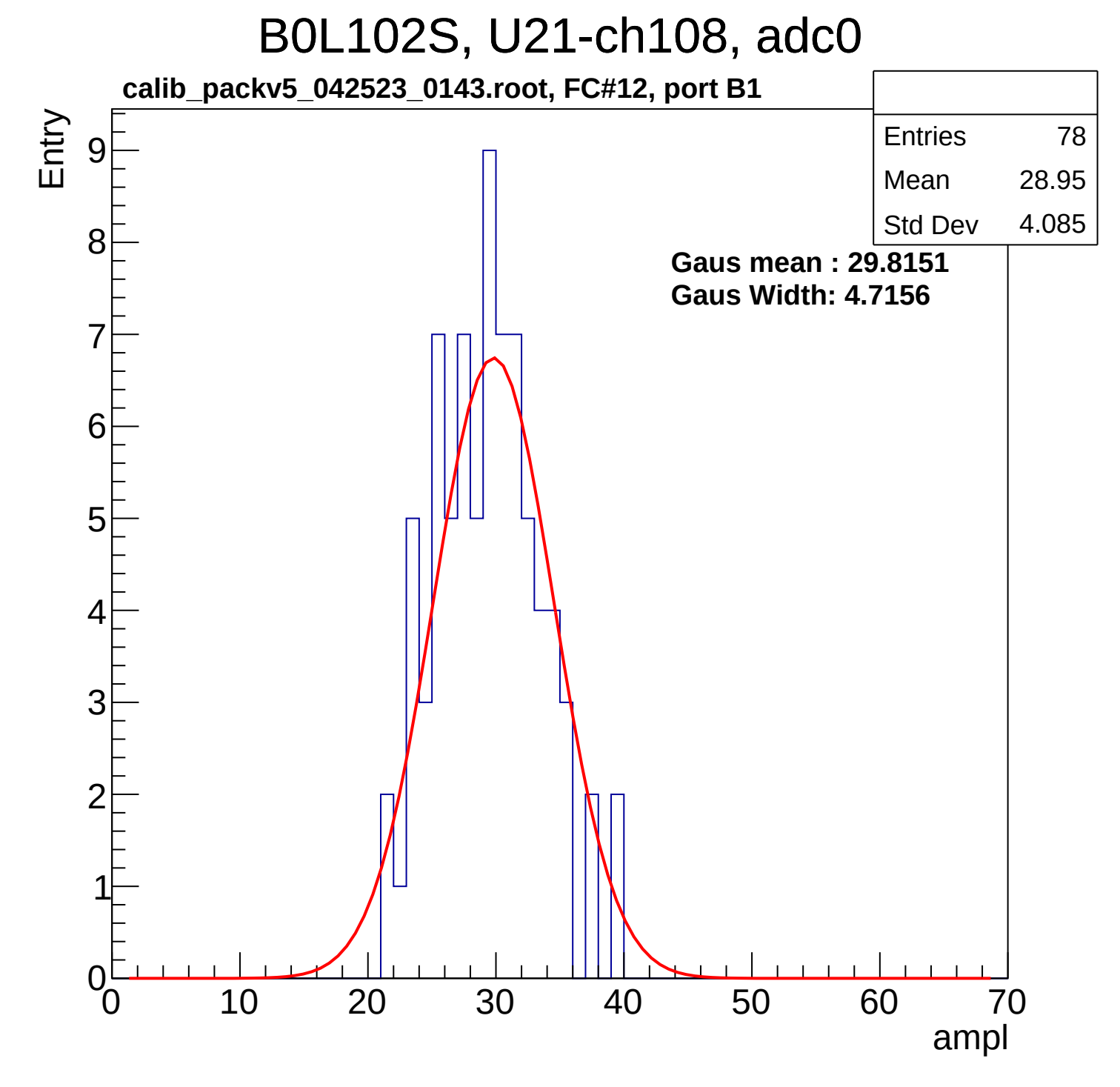
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch108, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 28.95
Std Dev 4.085
Gaus mean : 29.8151
Gaus Width: 4.7156
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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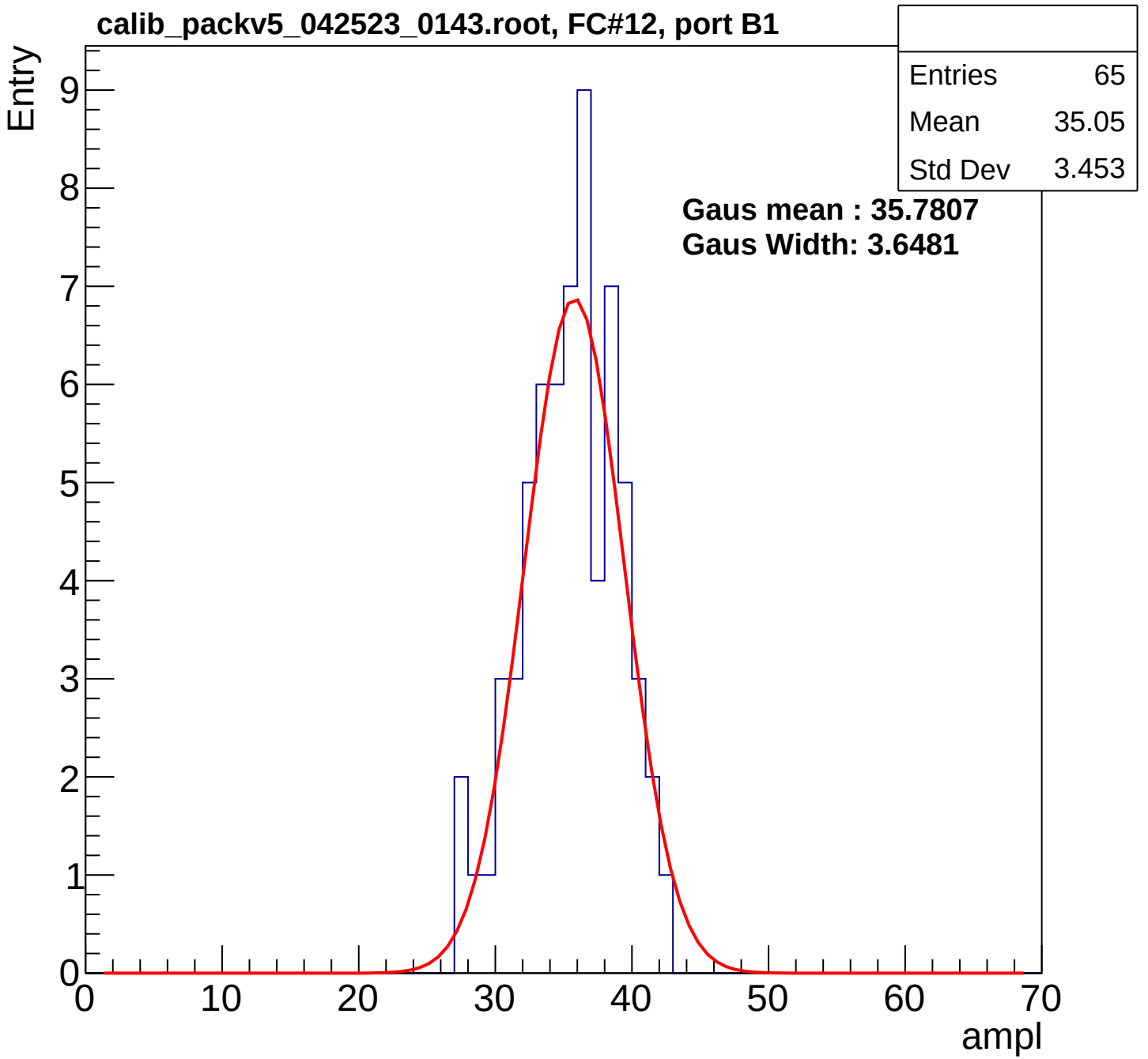
B0L102S, U21-ch108, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.05
Std Dev 3.453
Gaus mean : 35.7807
Gaus Width: 3.6481
Entry
ampl
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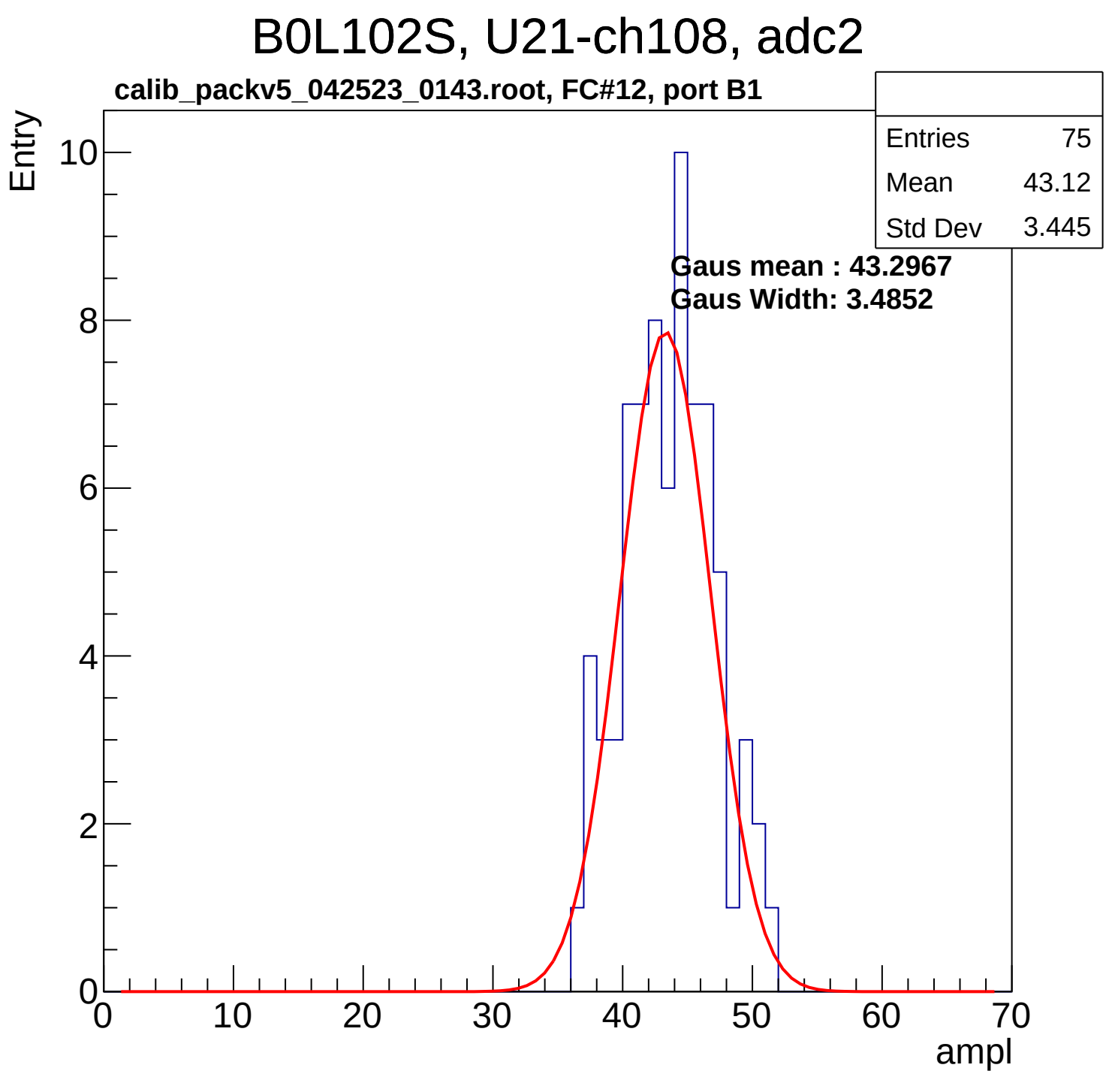
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch108, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 43.12
Std Dev 3.445
Gaus mean : 43.2967
Gaus Width: 3.4852
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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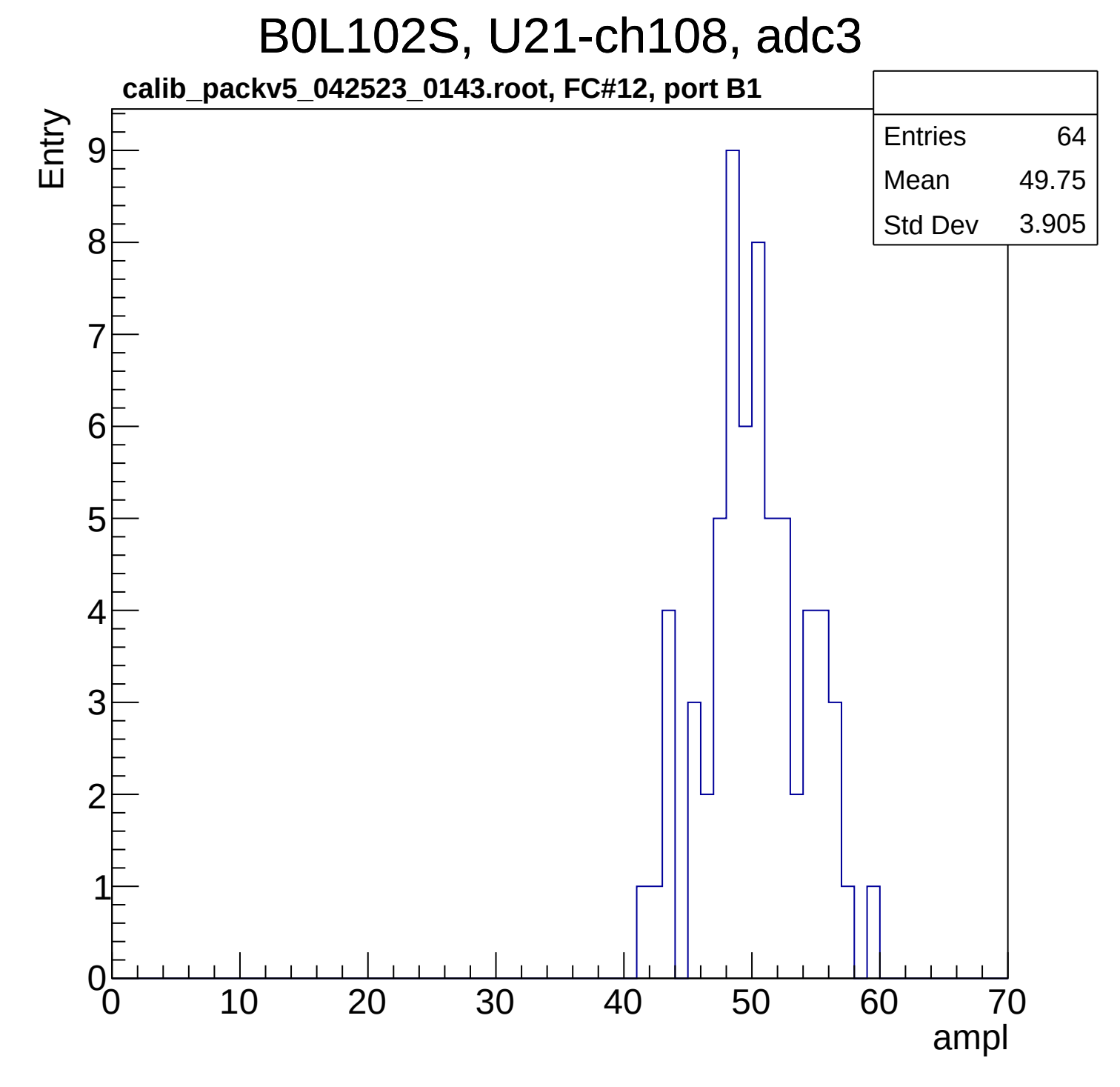
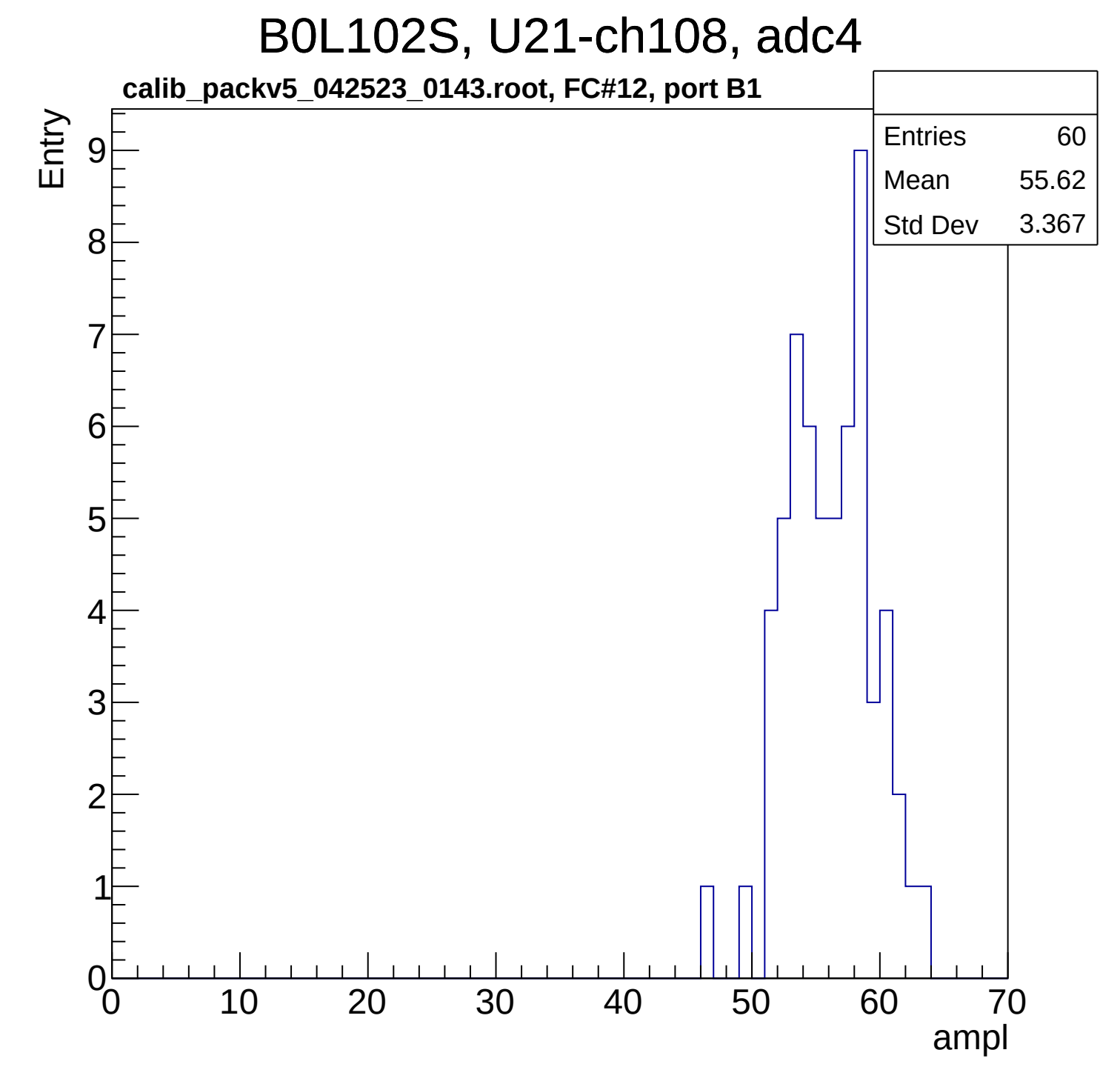

B0L102S, U21-ch108, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.75
Std Dev 3.905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch108, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 55.62
Std Dev 3.367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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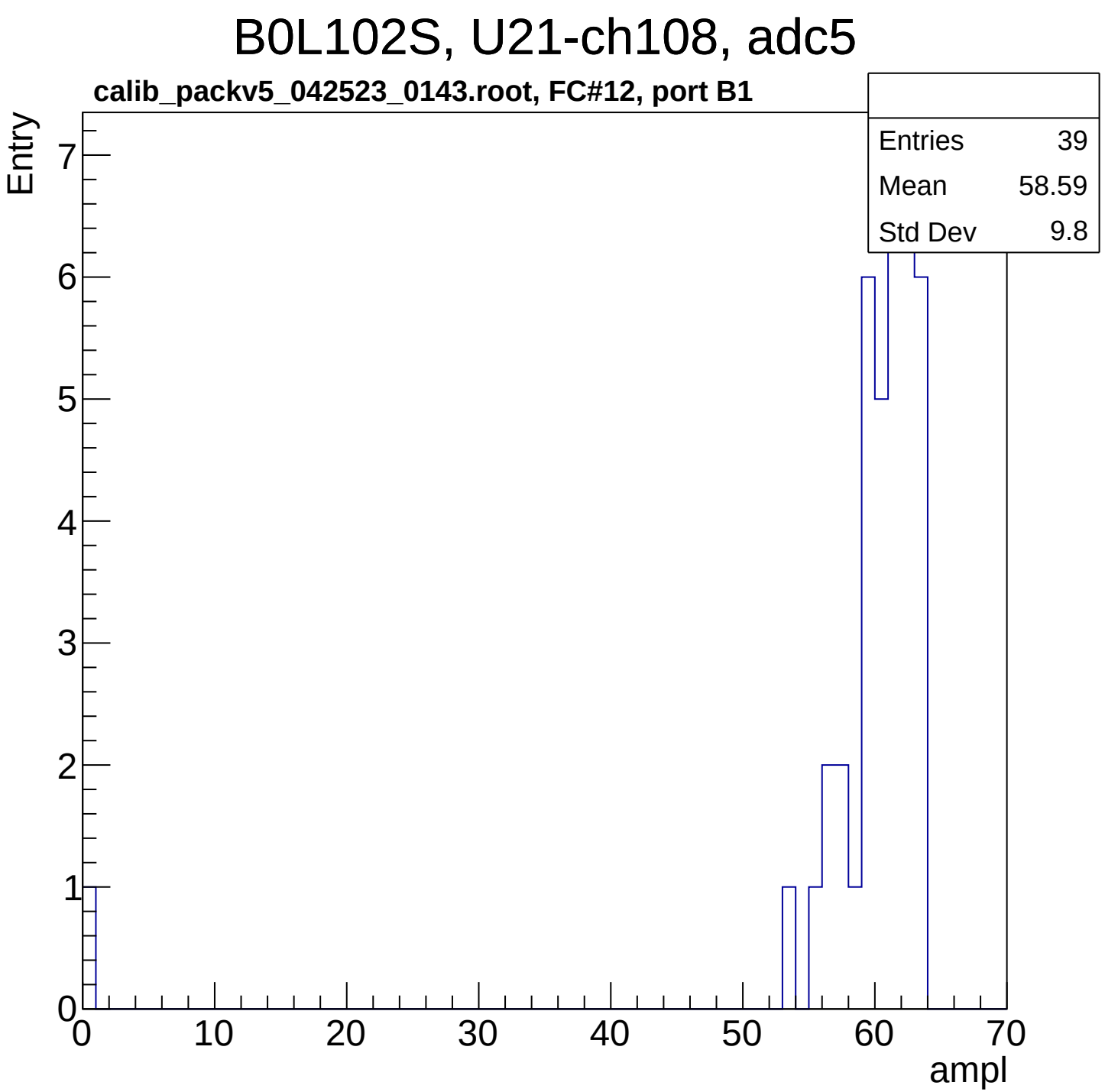
B0L102S, U21-ch108, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 58.59
Std Dev 9.8
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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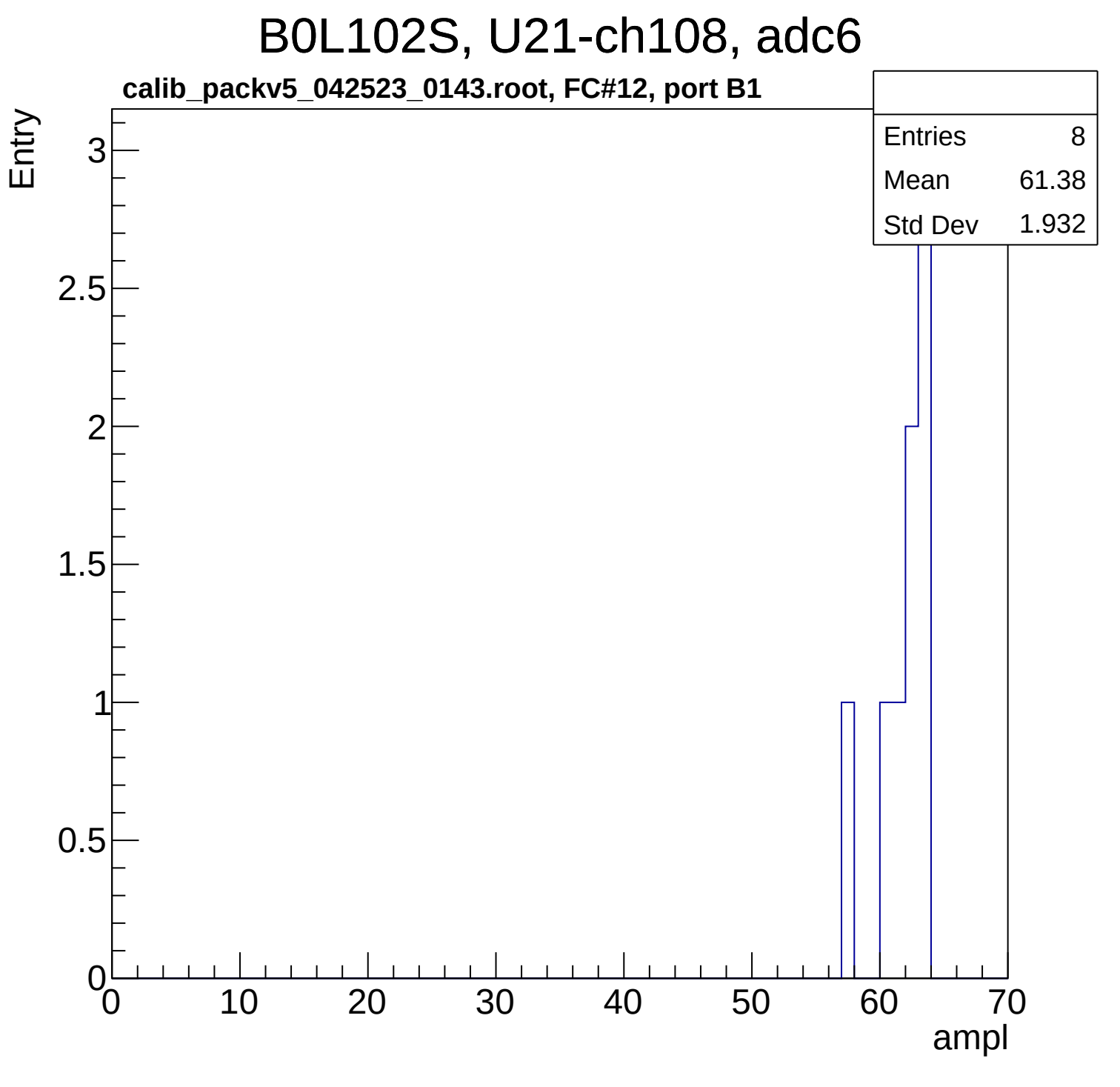
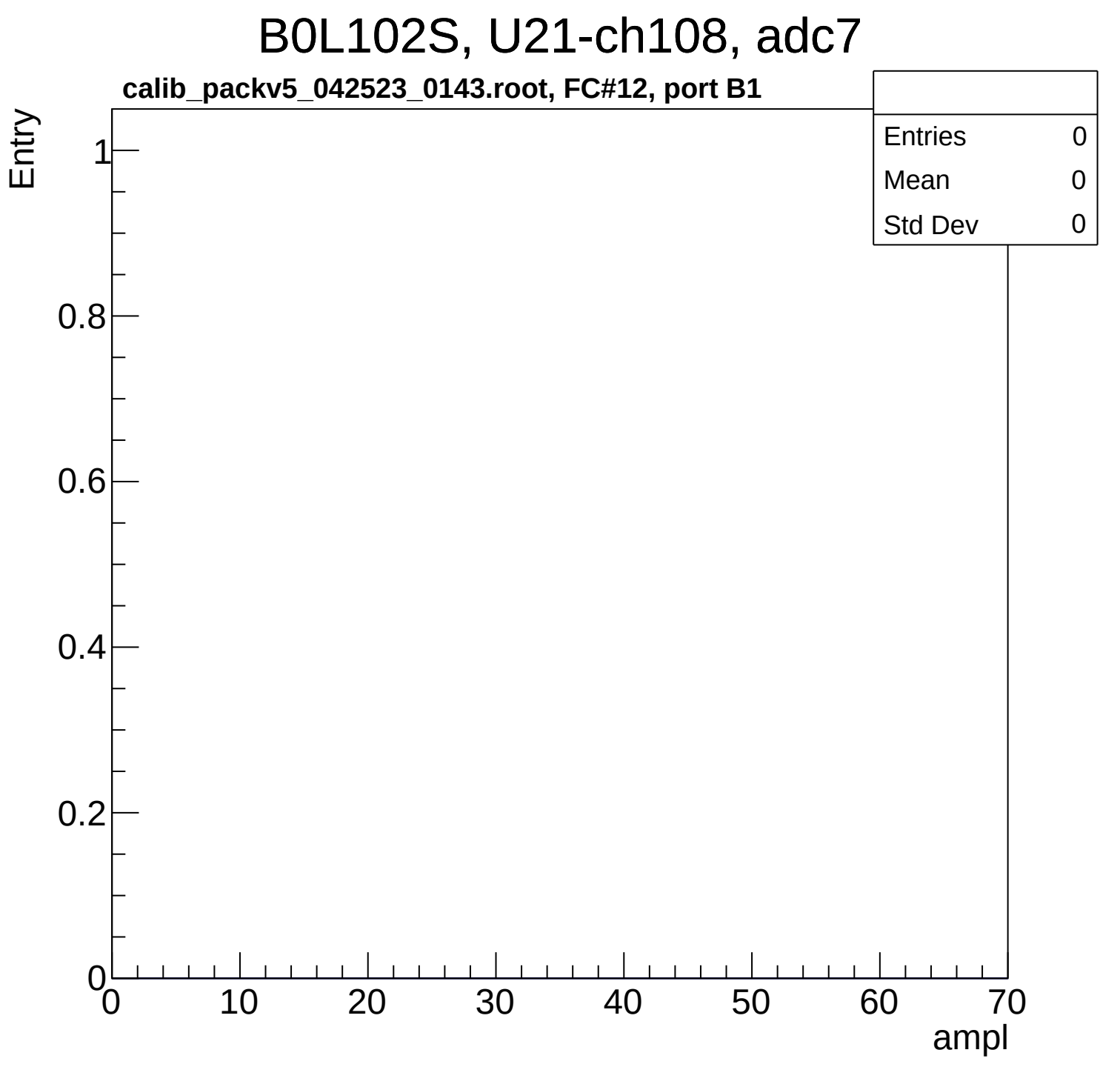
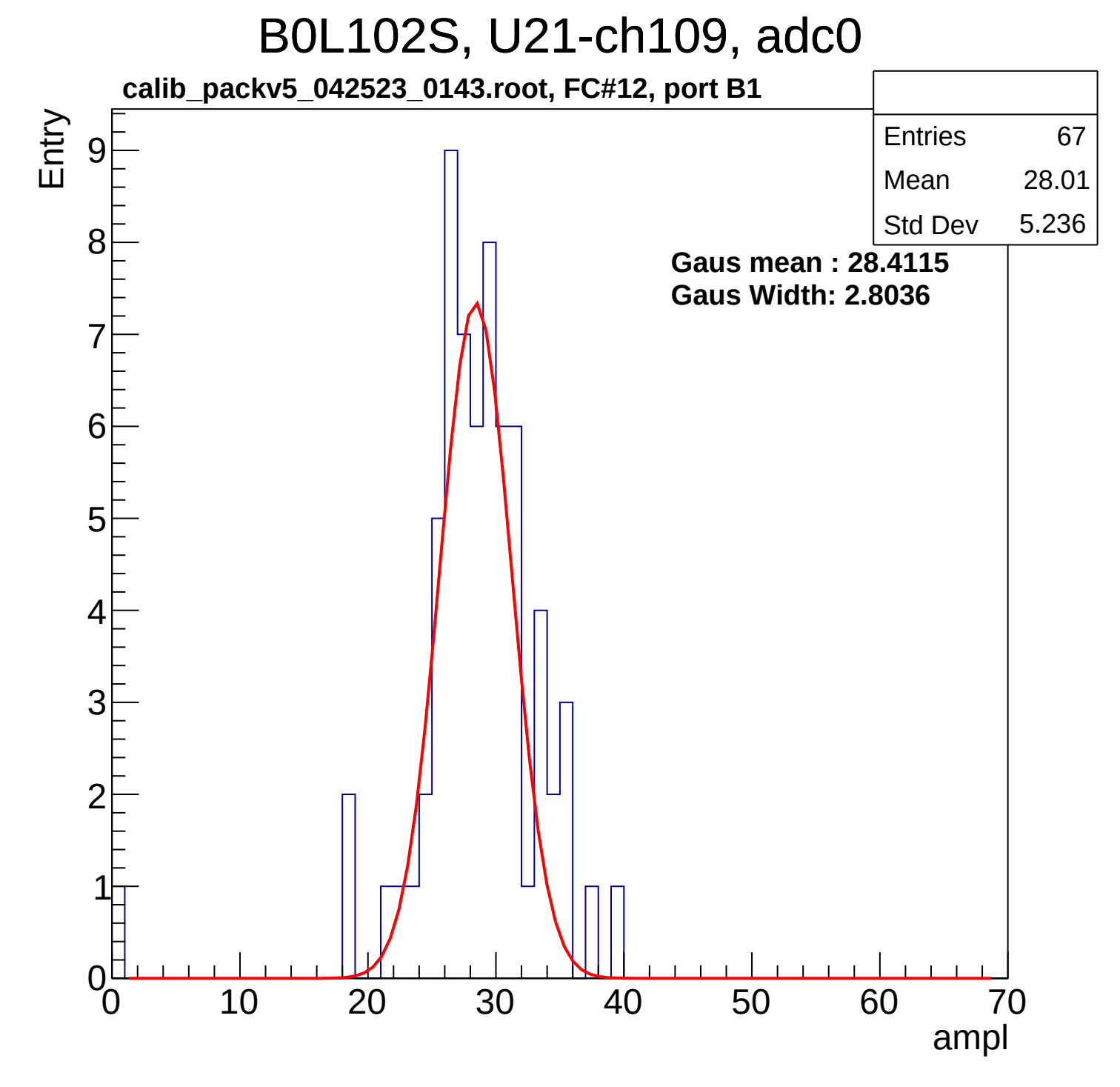

B0L102S, U21-ch108, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.38
Std Dev 1.932
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch108, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch109, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.01
Std Dev 5.236
Gaus mean : 28.4115
Gaus Width: 2.8036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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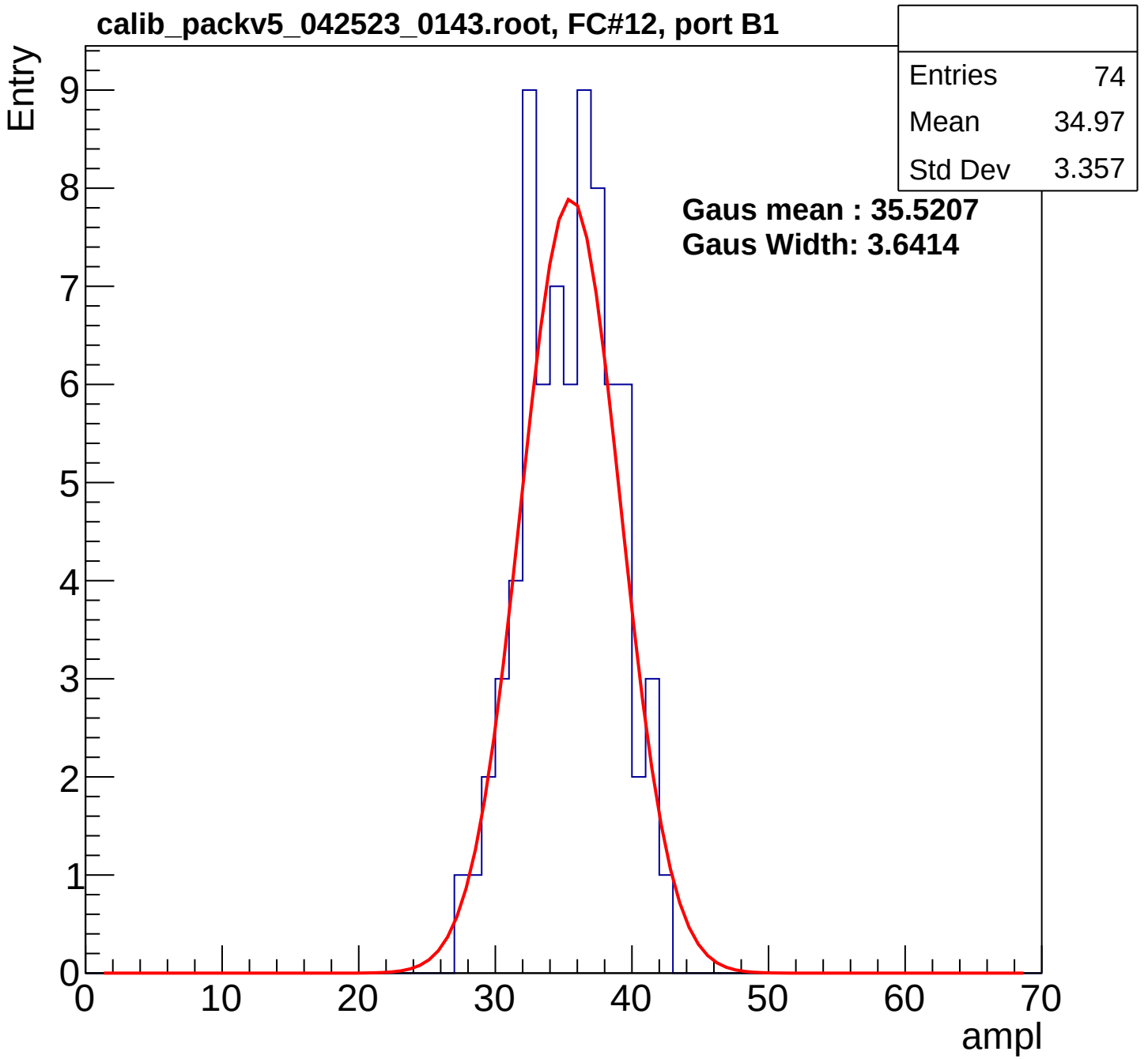
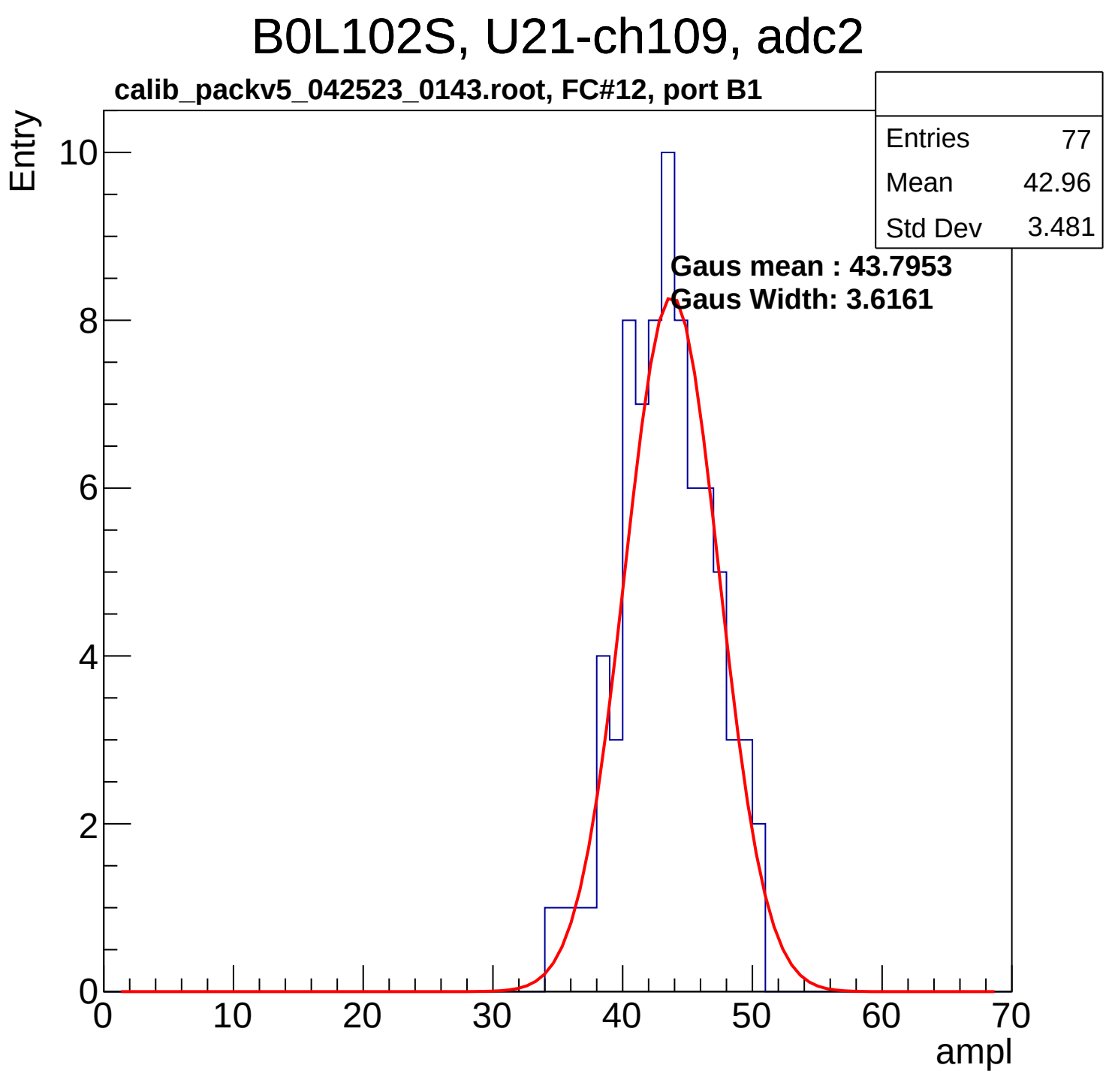

B0L102S, U21-ch109, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 34.97
Std Dev 3.357
Gaus mean : 35.5207
Gaus Width: 3.6414
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U21-ch109, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 42.96
Std Dev 3.481
Gaus mean : 43.7953
Gaus Width: 3.6161
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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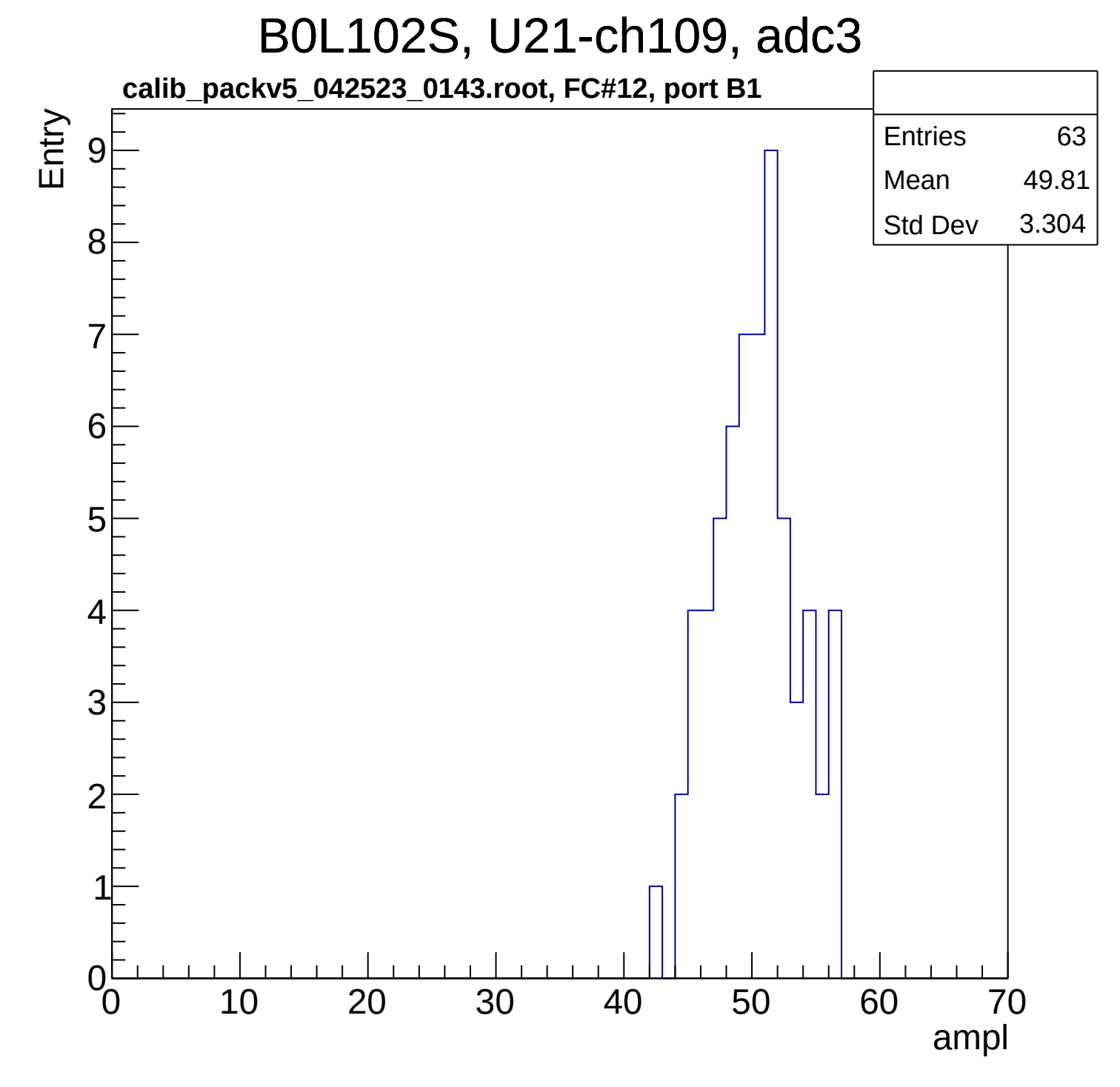
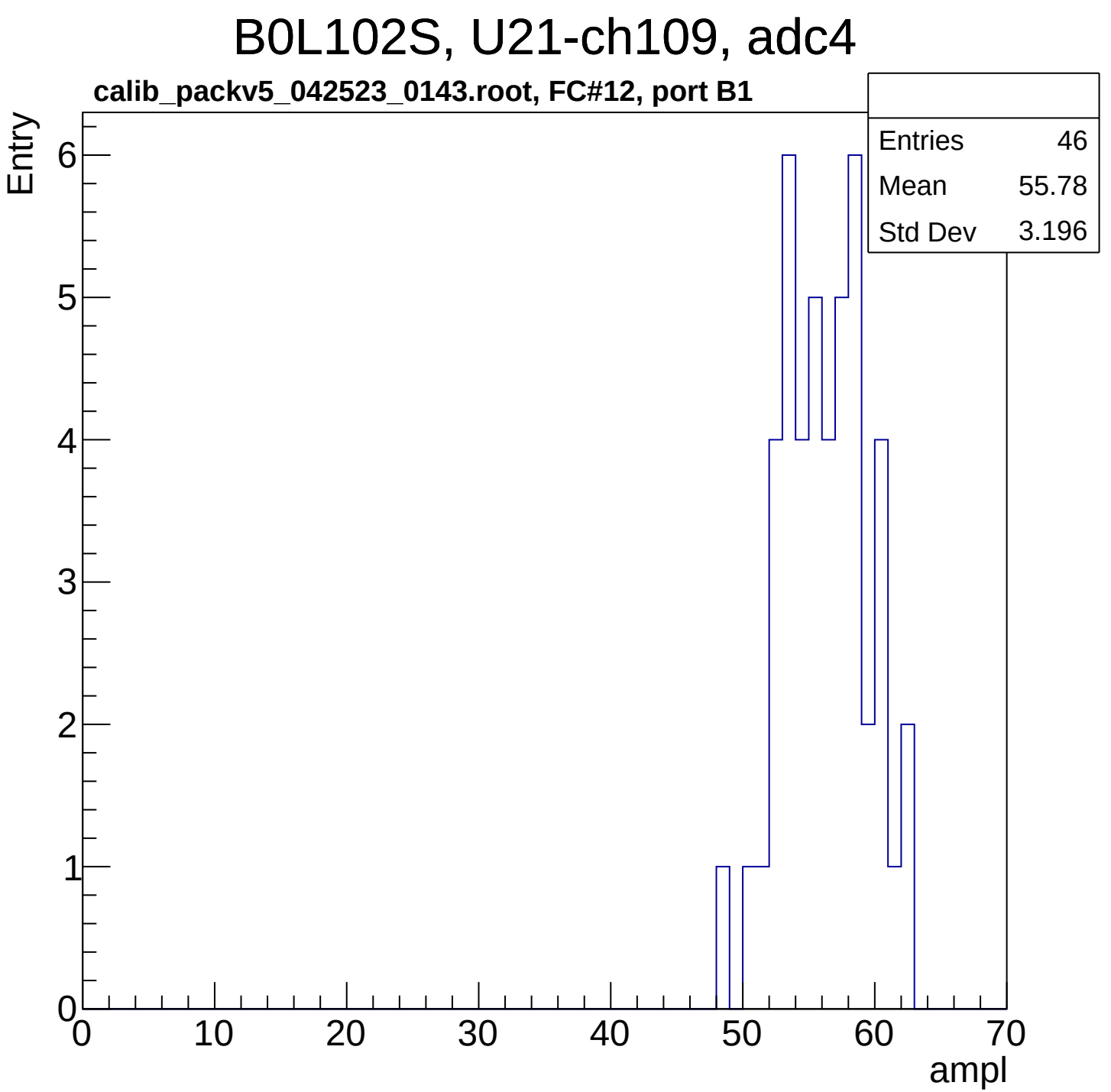
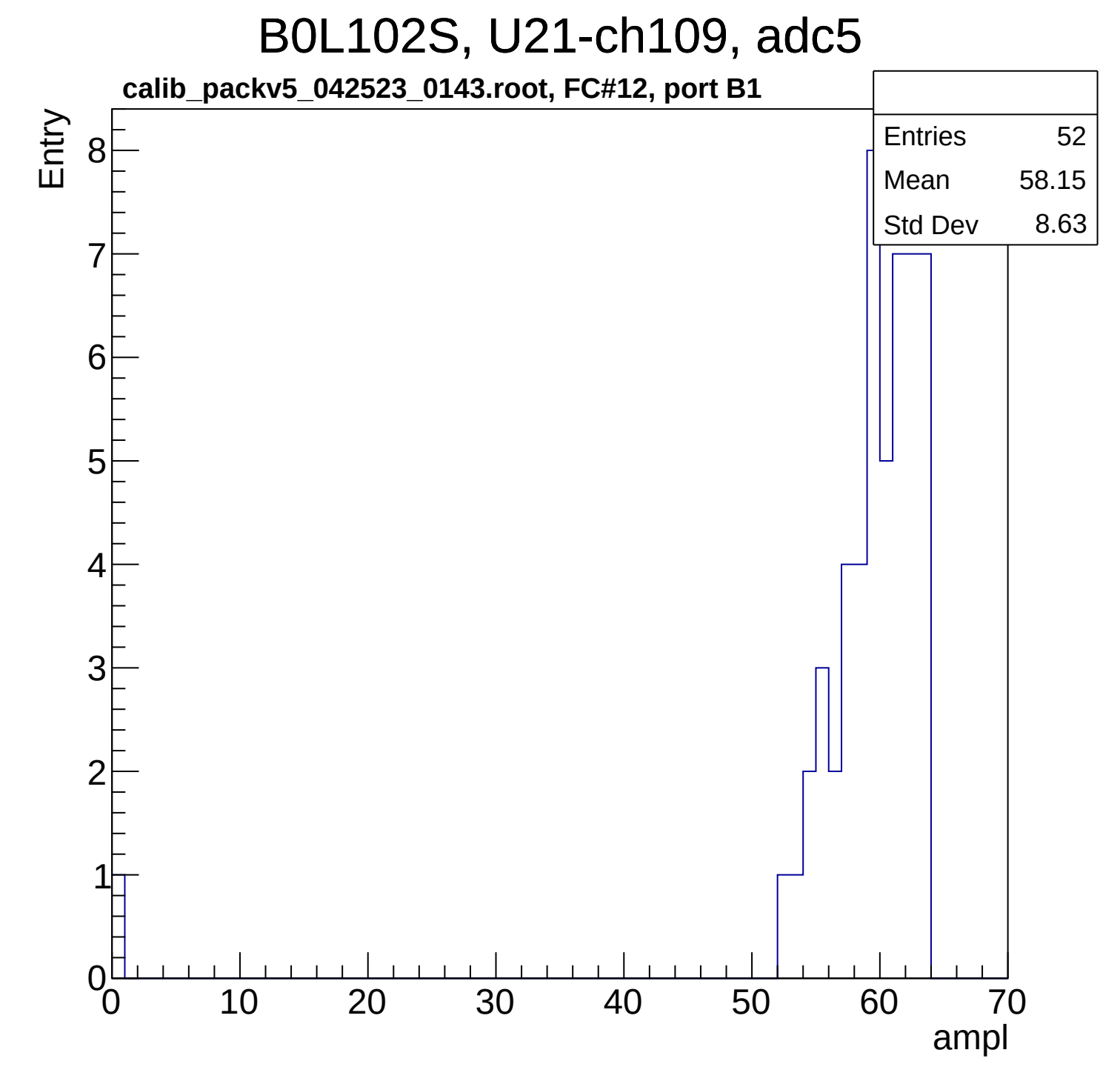

B0L102S, U21-ch109, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 49.81
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch109, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 55.78
Std Dev 3.196
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch109, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.15
Std Dev 8.63
Entry
ampl
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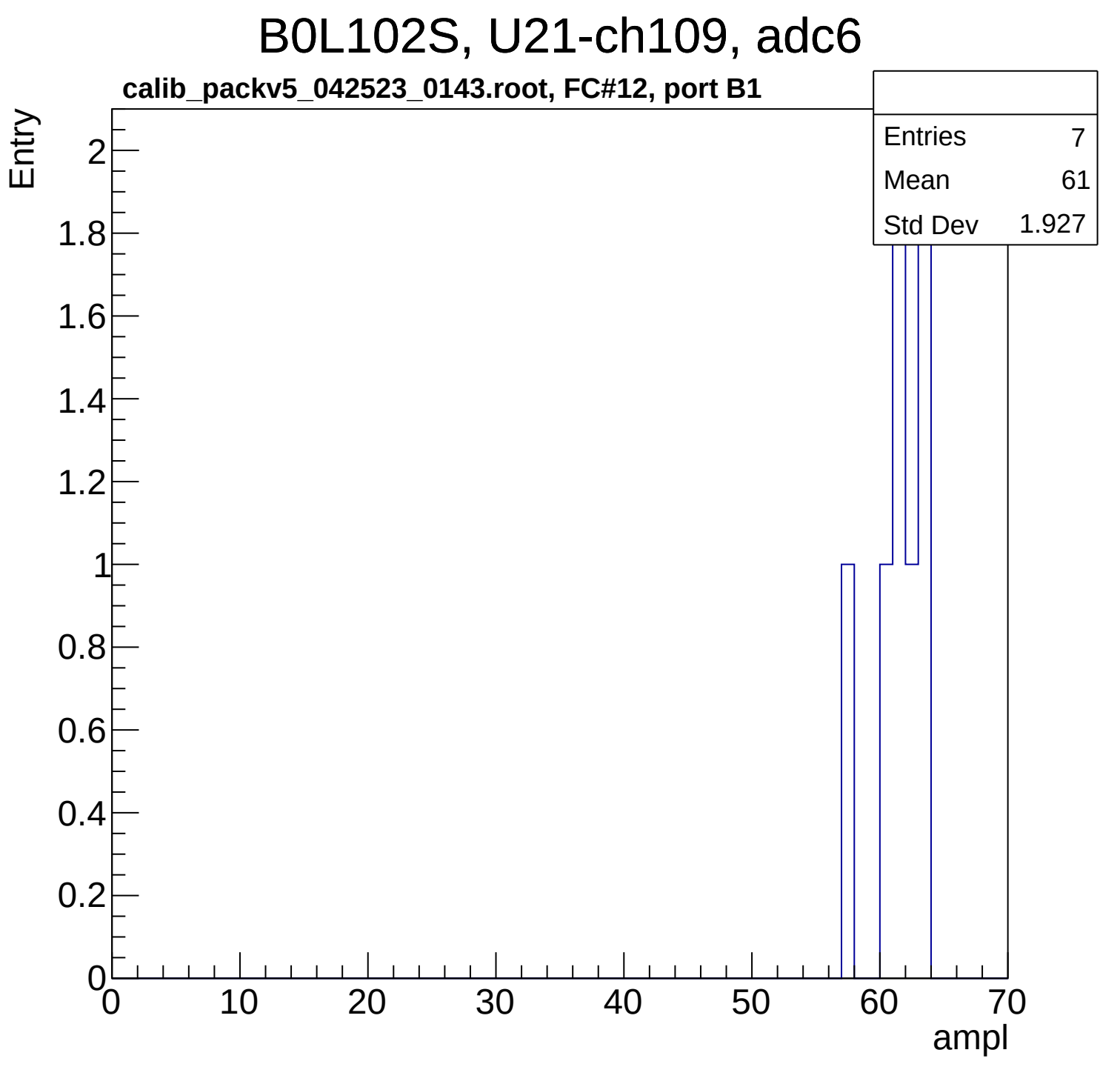
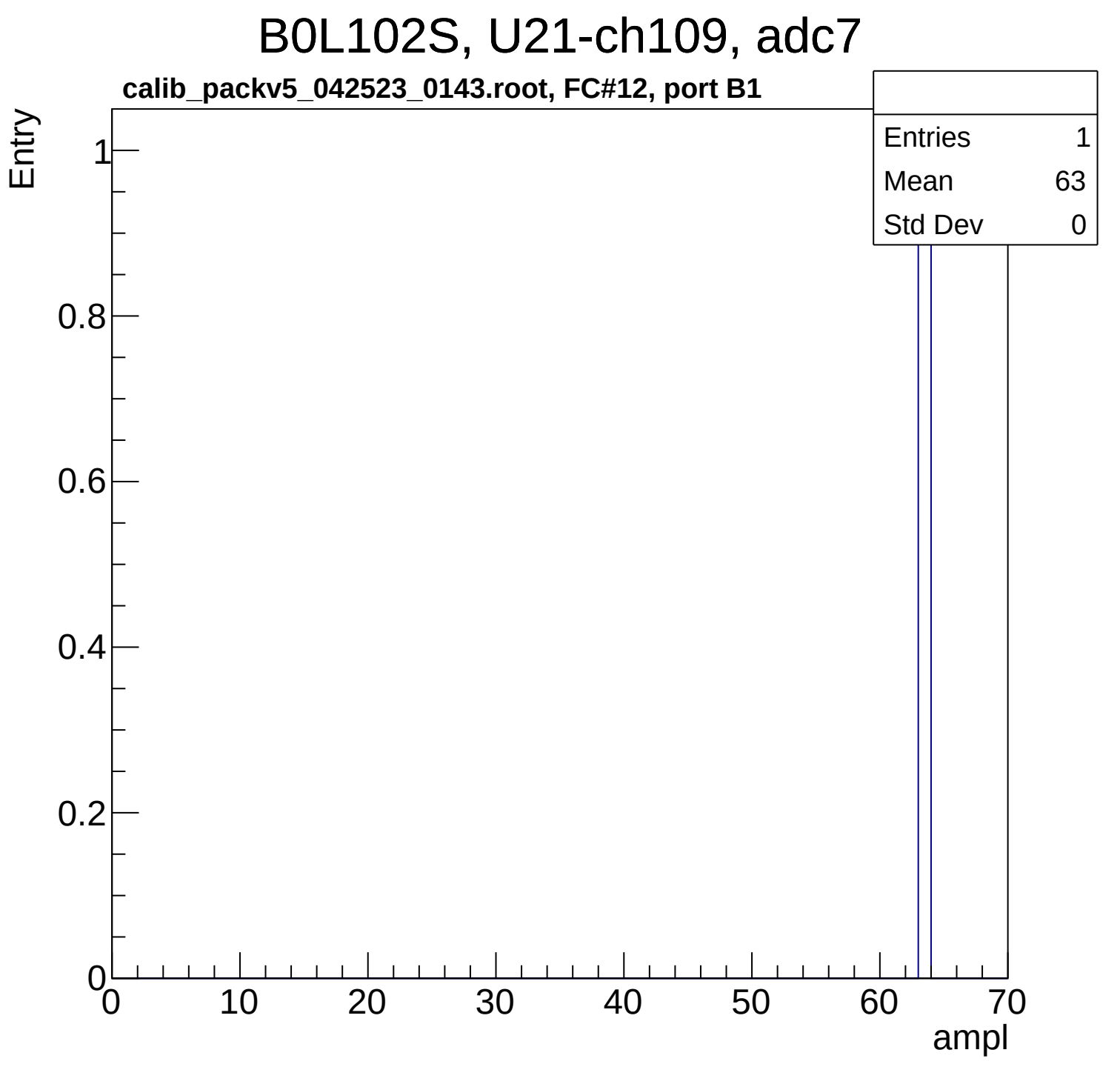
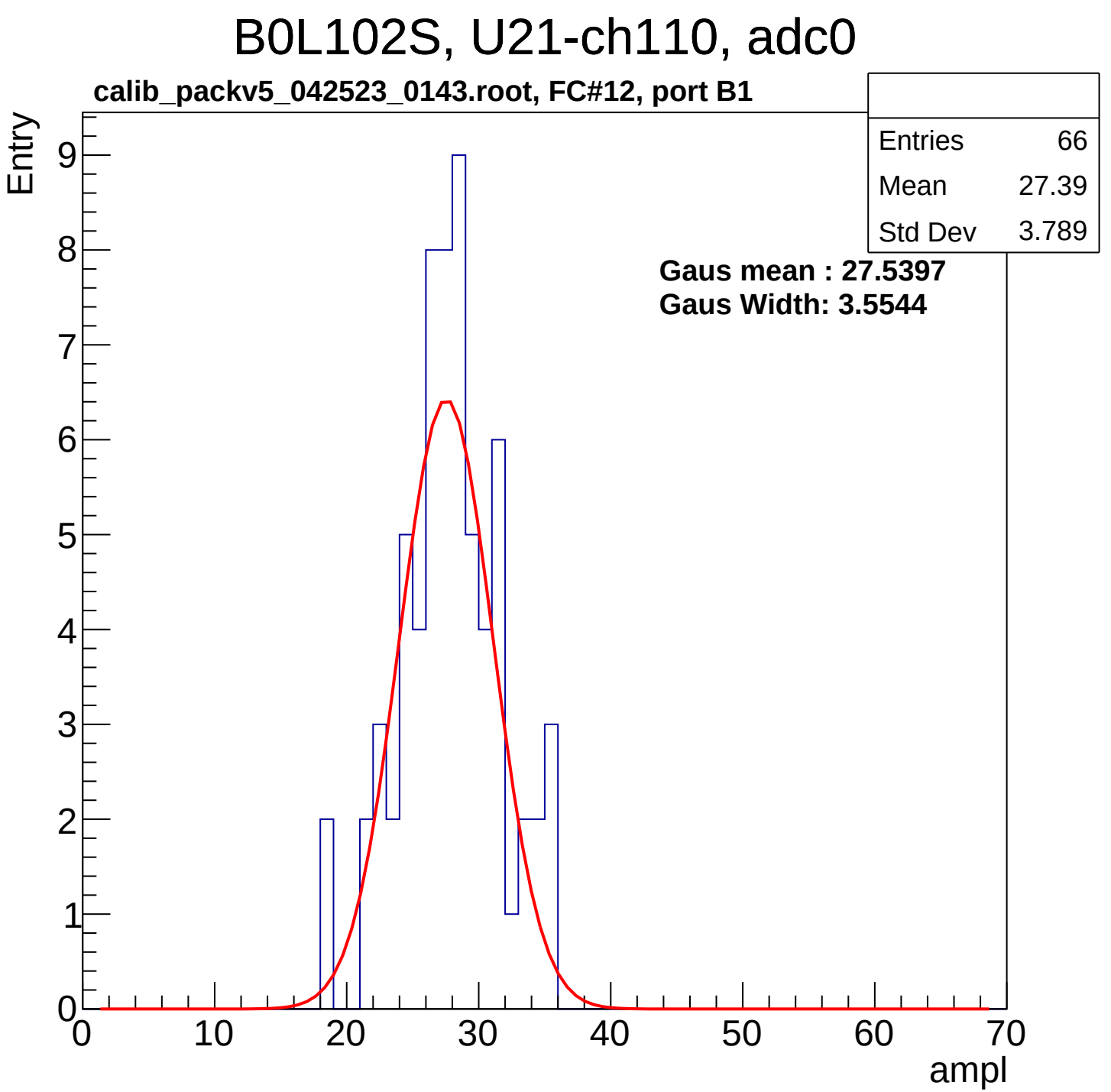
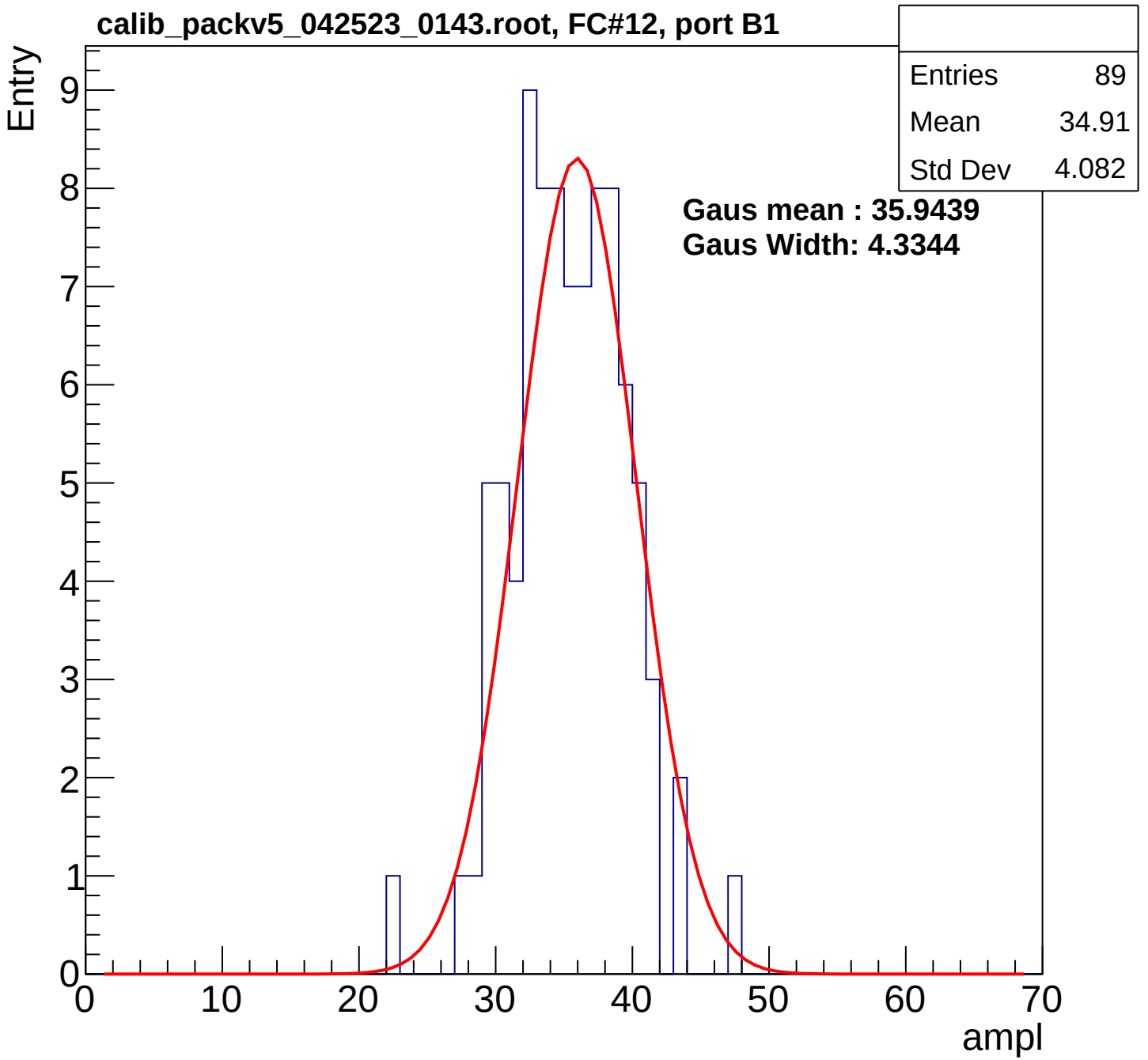
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch109, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61
Std Dev 1.927
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch109, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch110, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 27.39
Std Dev 3.789
Gaus mean : 27.5397
Gaus Width: 3.5544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch110, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 34.91
Std Dev 4.082
Gaus mean : 35.9439
Gaus Width: 4.3344
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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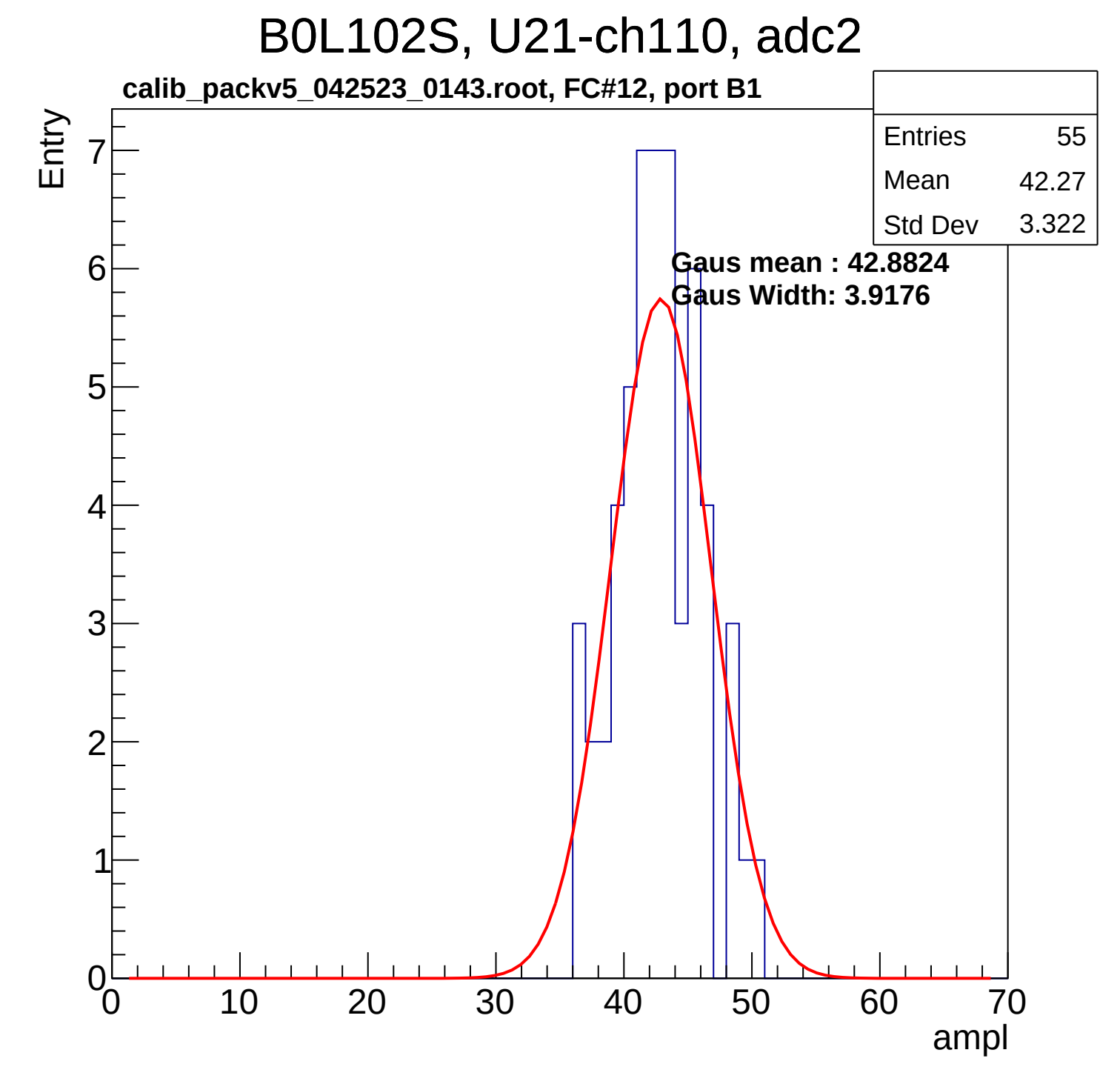
B0L102S, U21-ch110, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 42.27
Std Dev 3.322
Gaus mean : 42.8824
Gaus Width: 3.9176
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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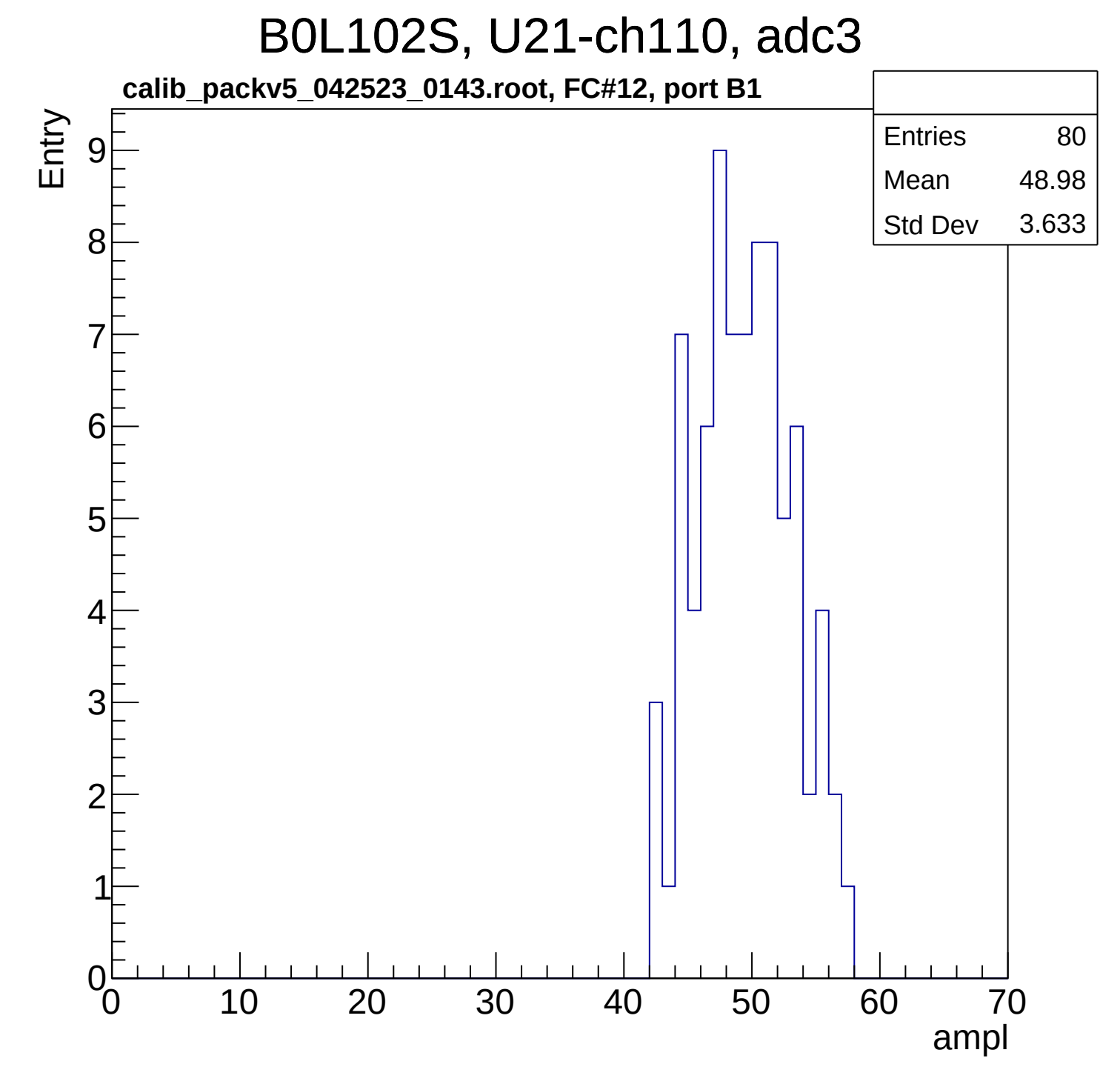
B0L102S, U21-ch110, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 48.98
Std Dev 3.633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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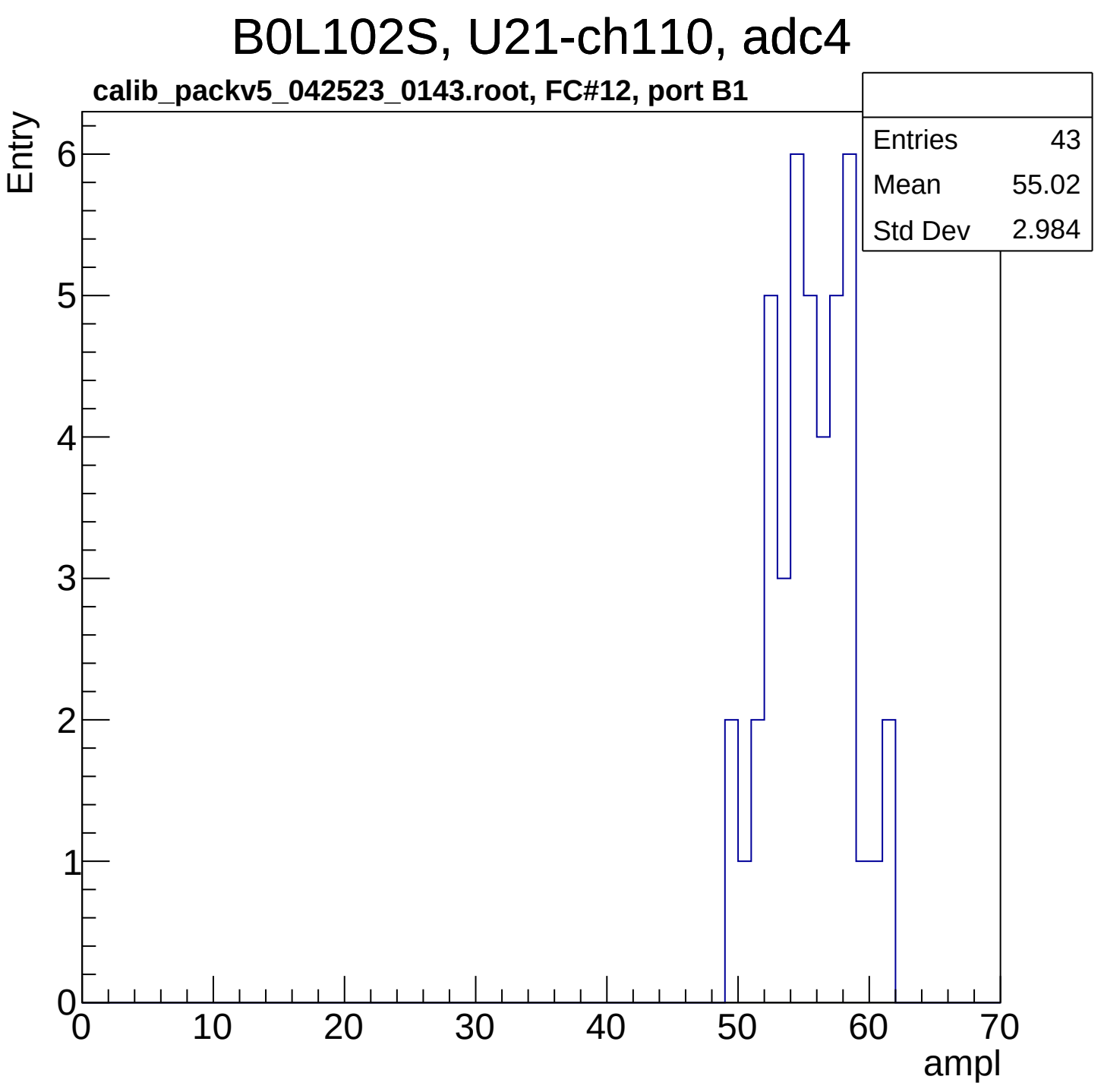
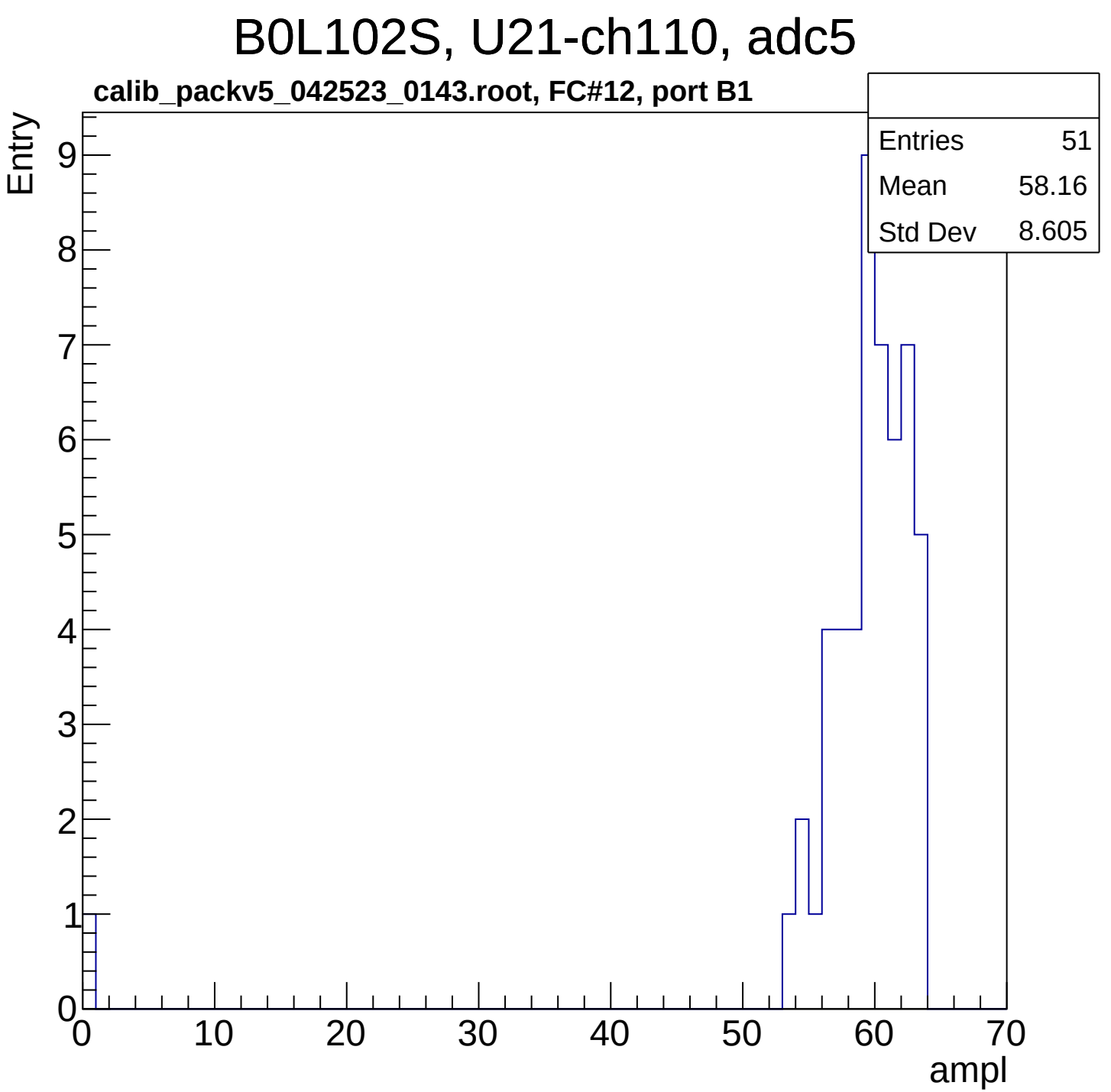

B0L102S, U21-ch110, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.02
Std Dev 2.984
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U21-ch110, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 58.16
Std Dev 8.605
Entry
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3
4
5
6
7
8
9
0
10
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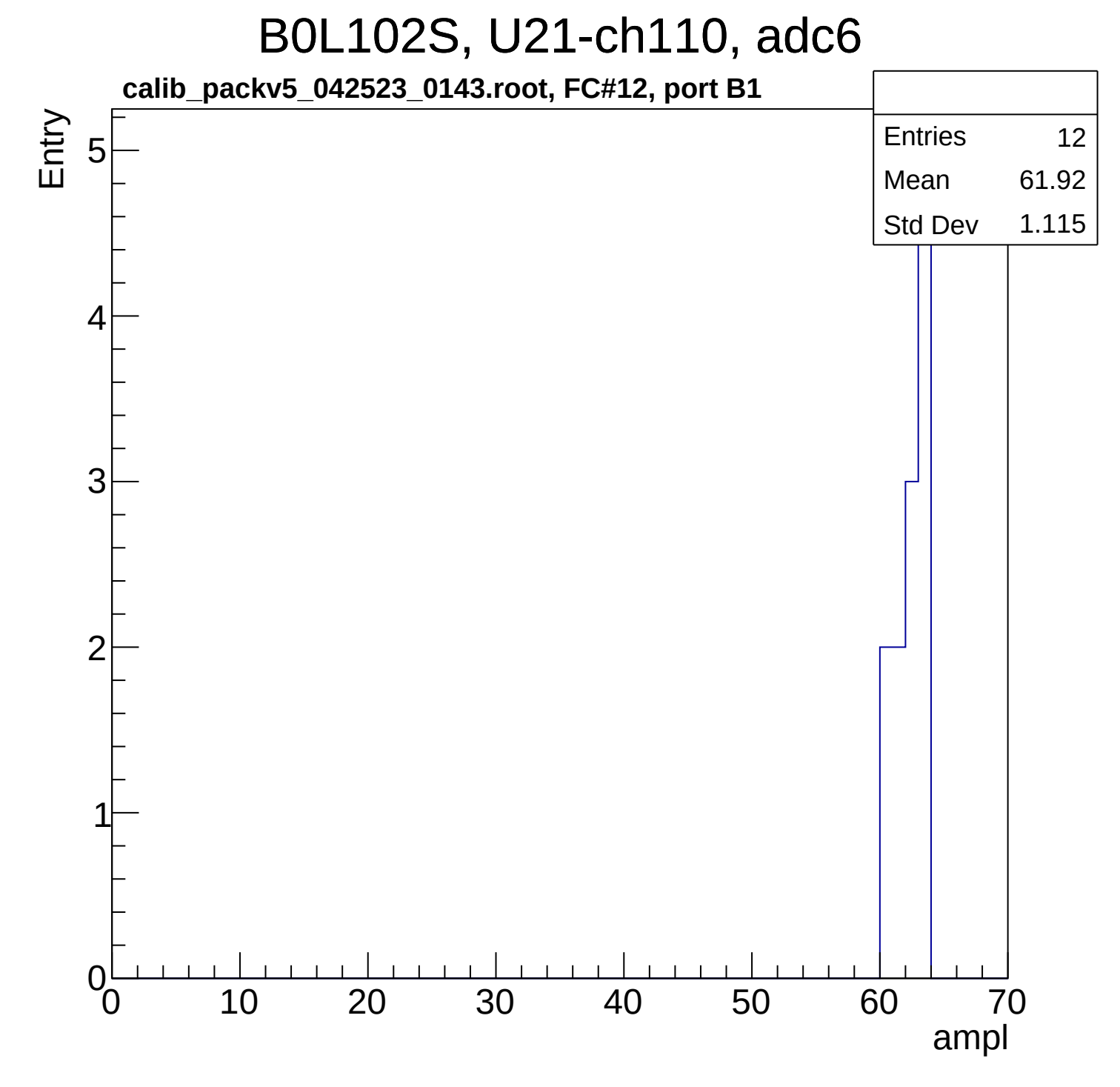
50
60
70
ampl


B0L102S, U21-ch110, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 61.92
Std Dev 1.115
Entry
0
1
2
3
4
5
0
10
20
30
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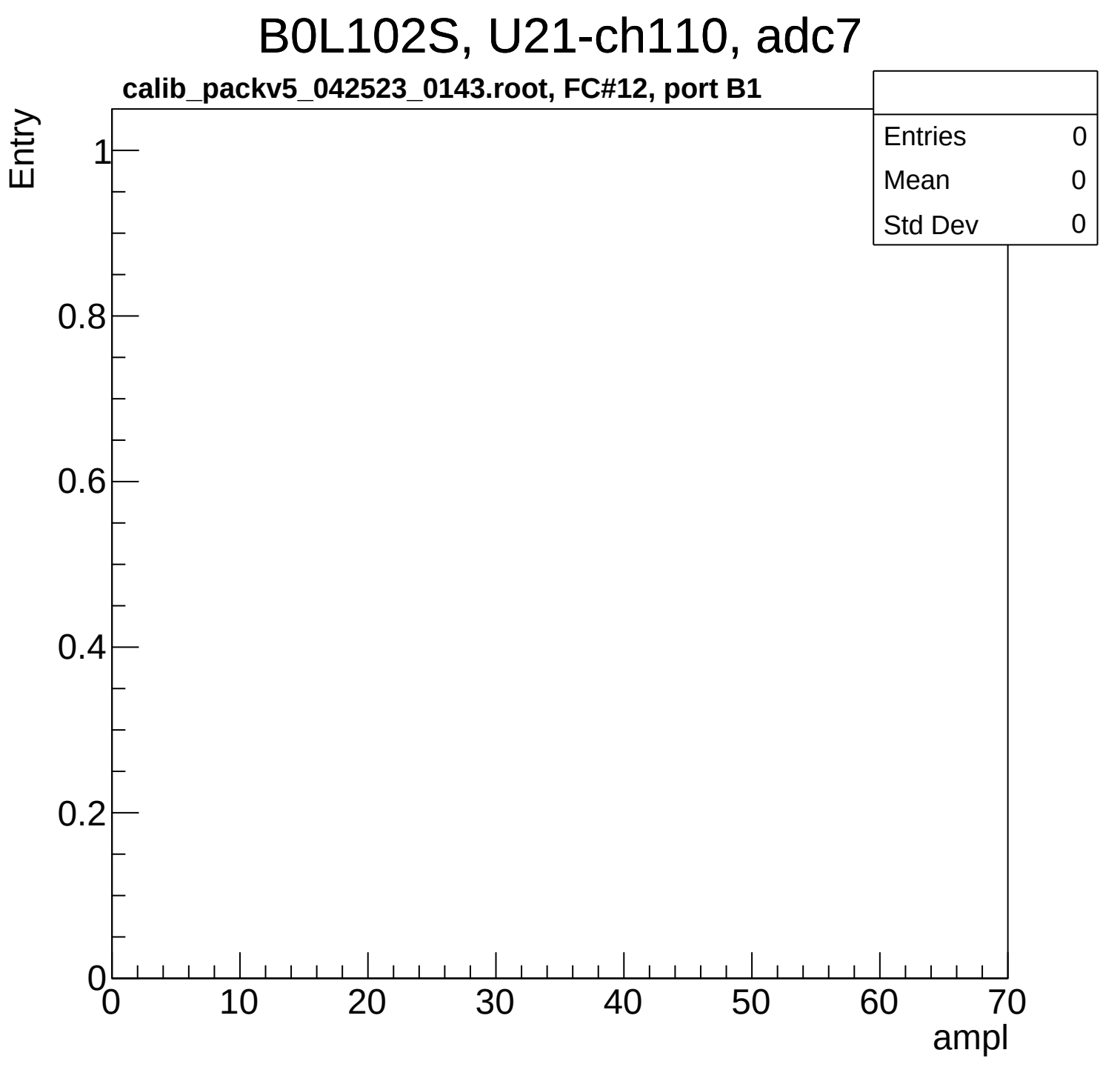
50
60
70
ampl


B0L102S, U21-ch110, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
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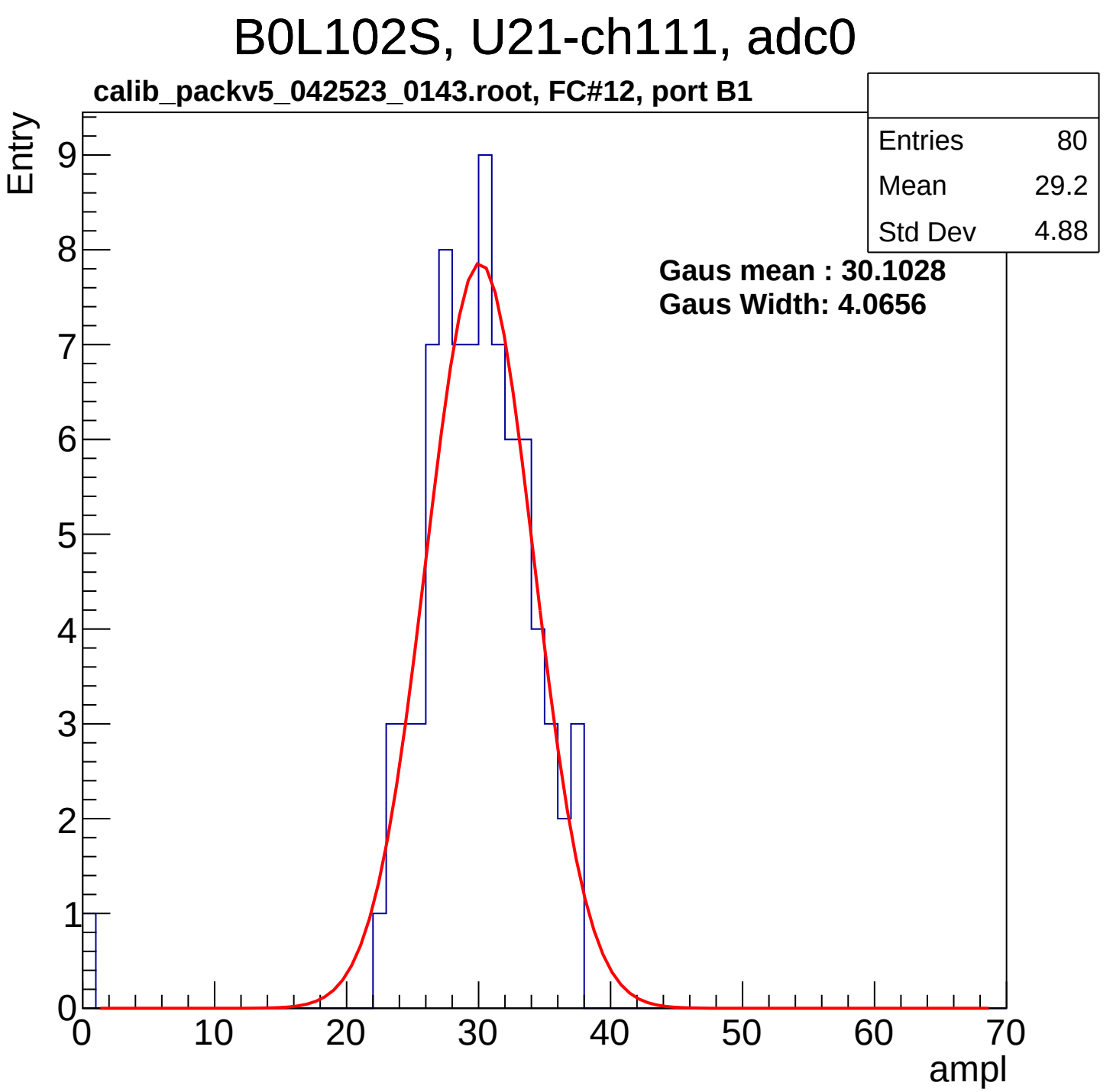
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch111, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 29.2
Std Dev 4.88
Gaus mean : 30.1028
Gaus Width: 4.0656
Entry
ampl
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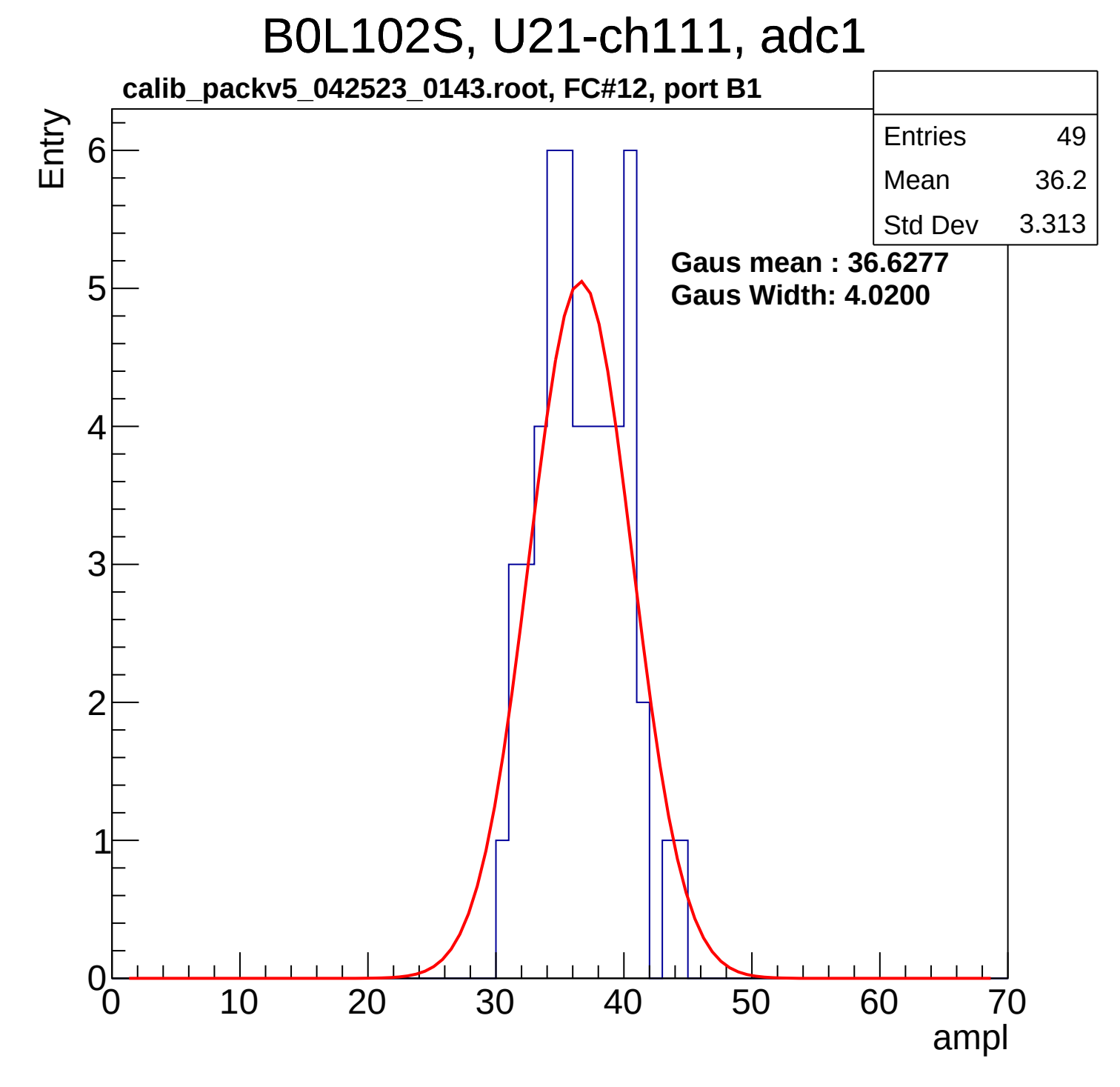
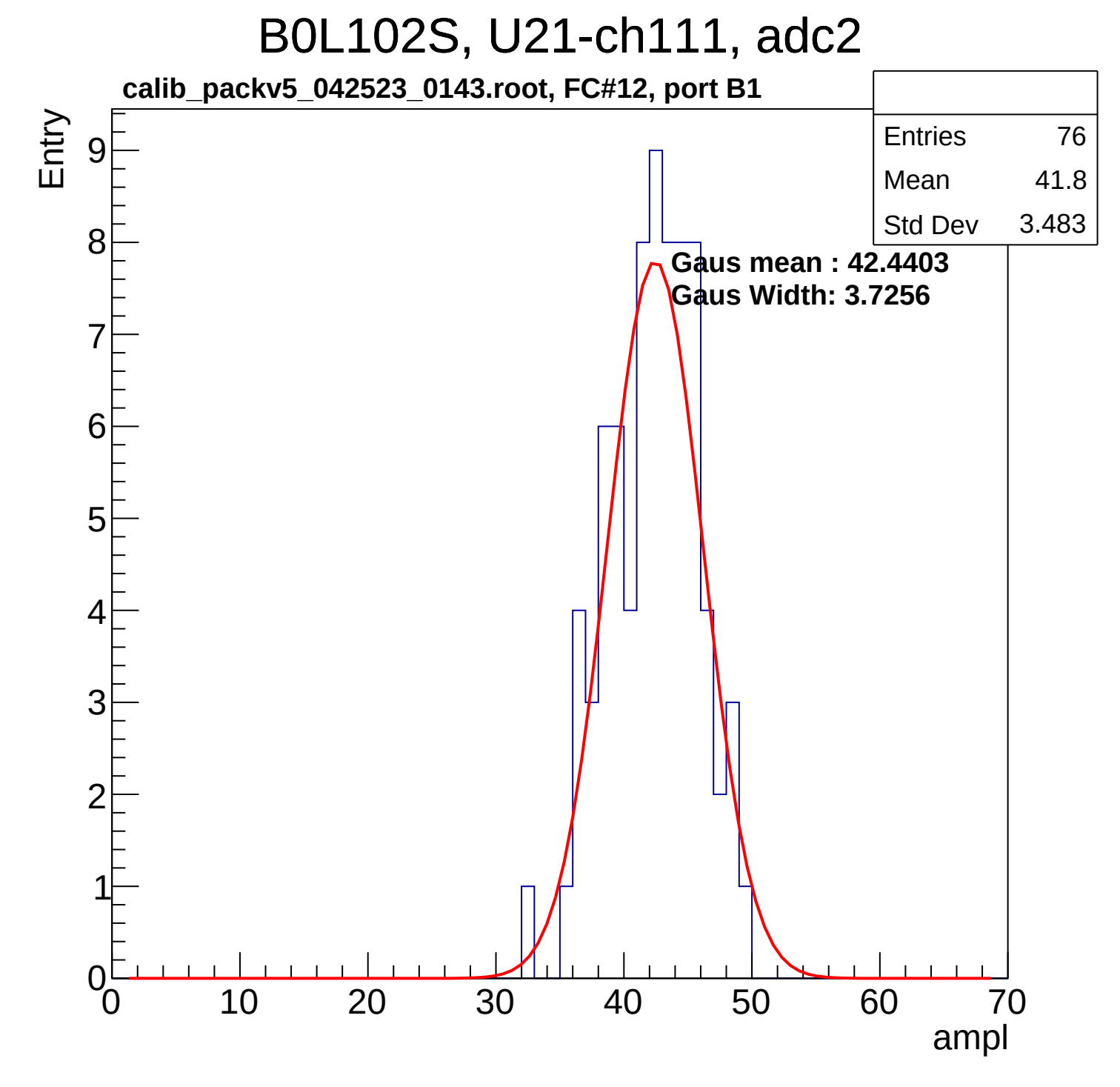
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch111, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 36.2
Std Dev 3.313
Gaus mean : 36.6277
Gaus Width: 4.0200
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U21-ch111, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 41.8
Std Dev 3.483
Gaus mean : 42.4403
Gaus Width: 3.7256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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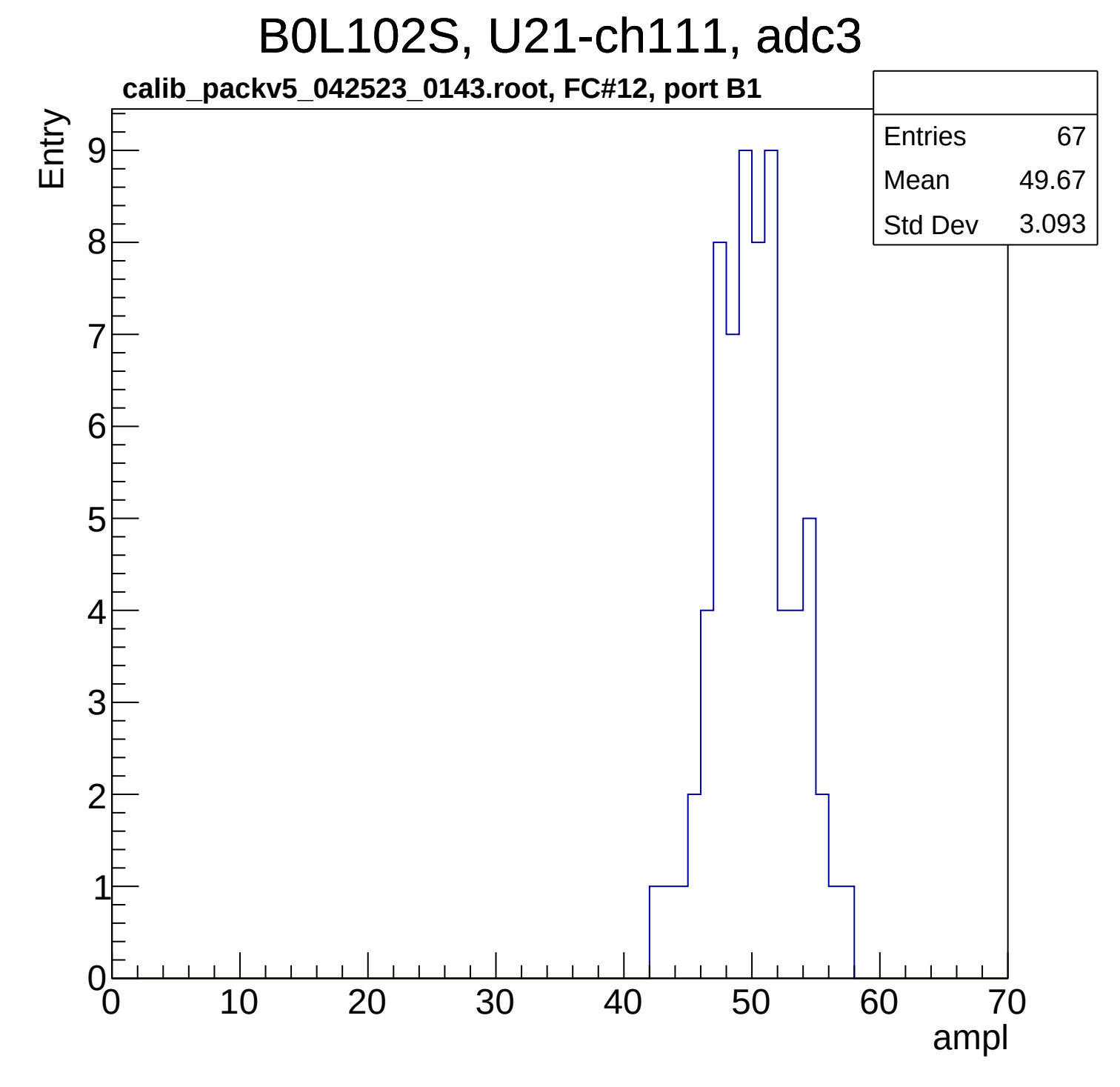
B0L102S, U21-ch111, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 49.67
Std Dev 3.093
Entry
ampl
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6
7
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0
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60
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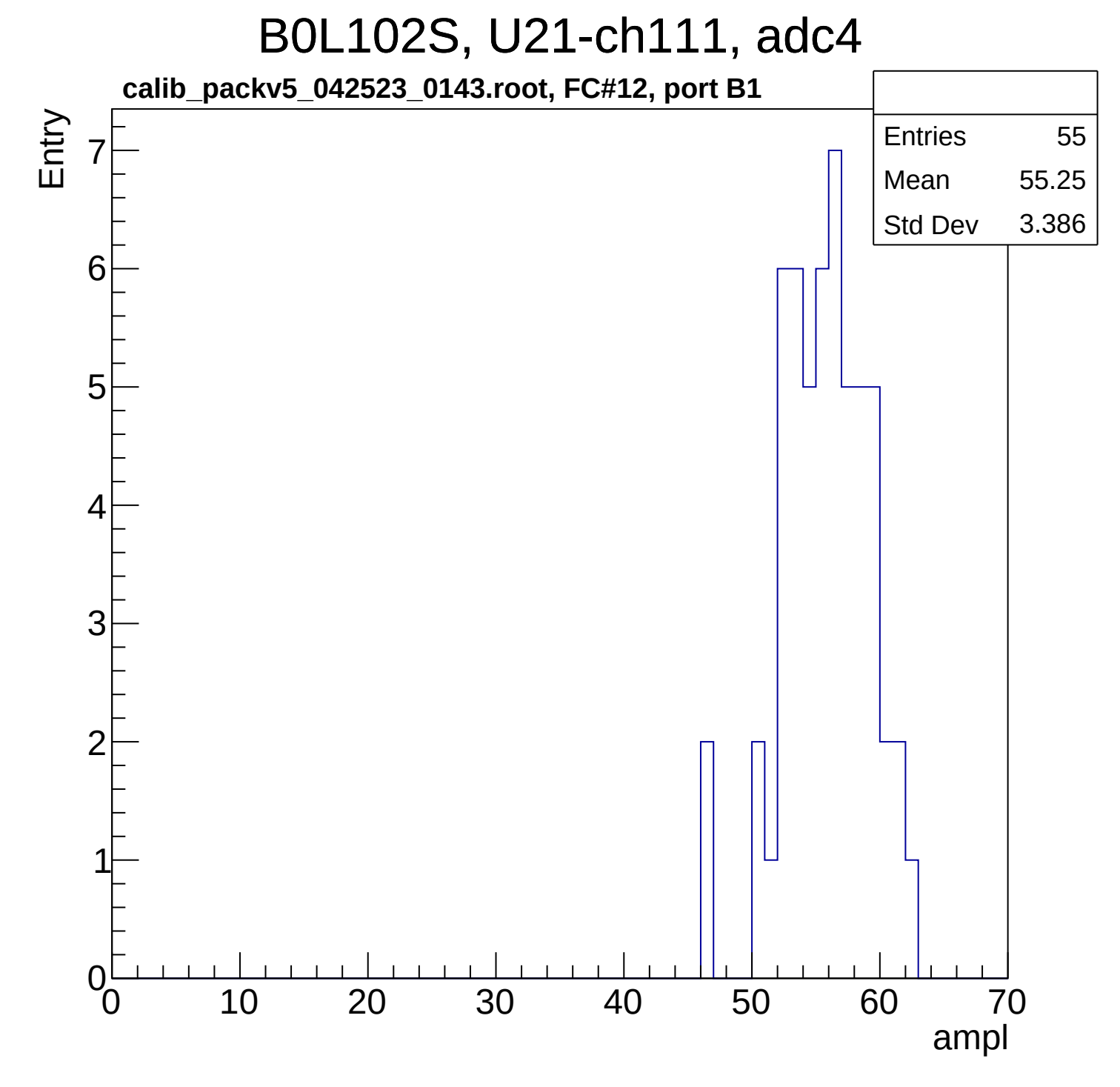
B0L102S, U21-ch111, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.25
Std Dev 3.386
Entry
ampl
0
1
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3
4
5
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0
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40
50
60
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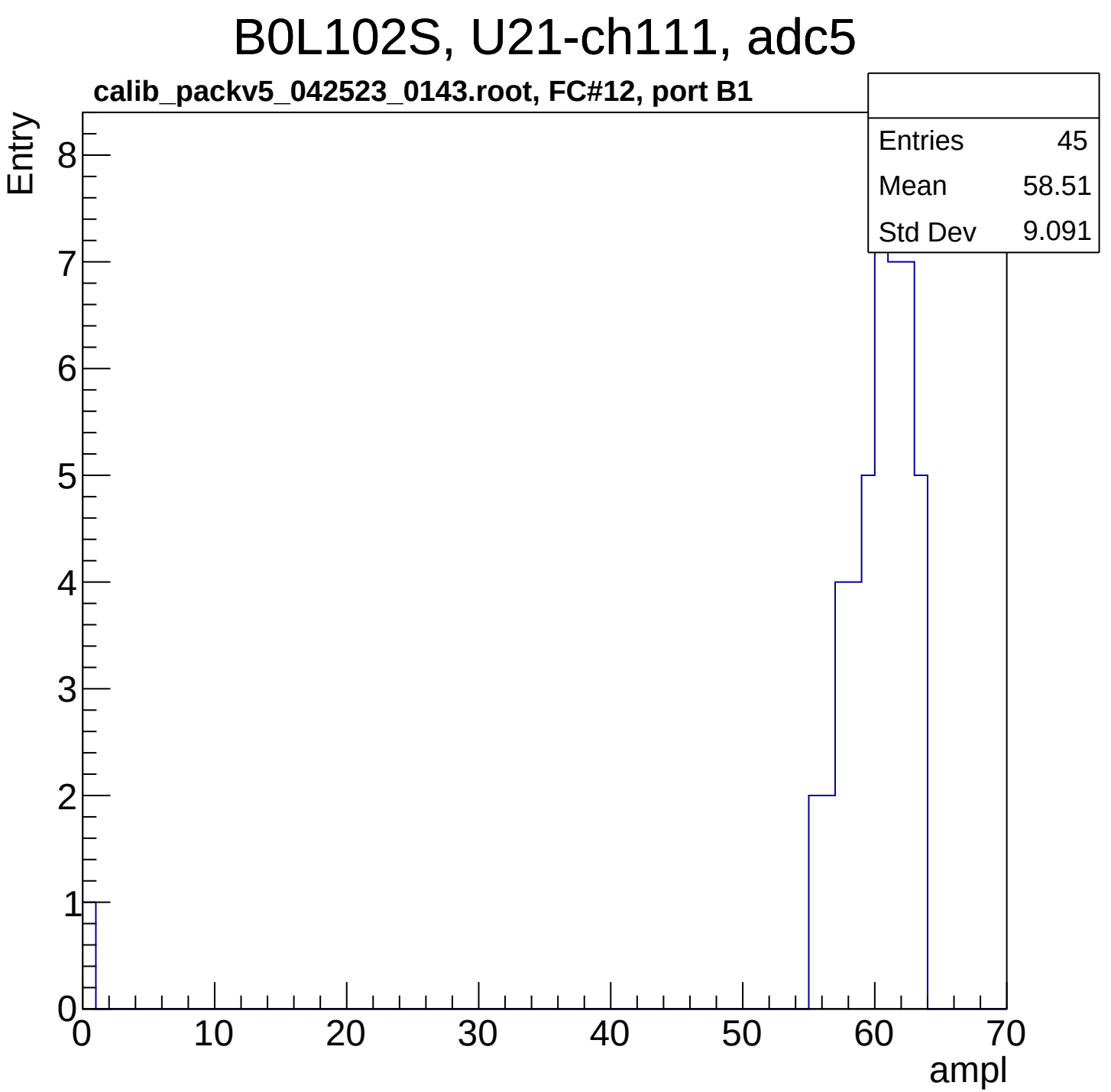
B0L102S, U21-ch111, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.51
Std Dev 9.091
Entry
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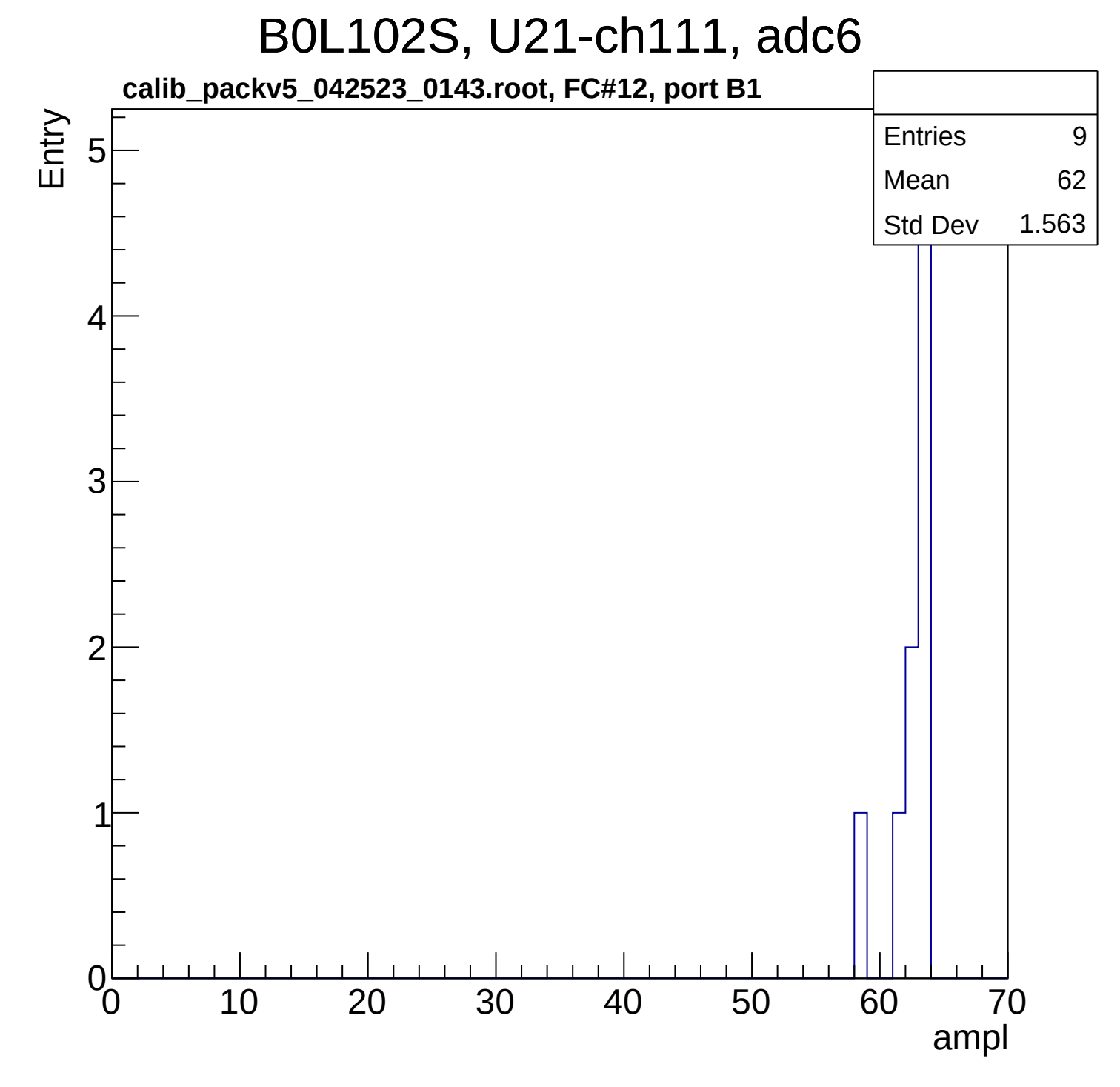
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch111, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 62
Std Dev 1.563
Entry
0
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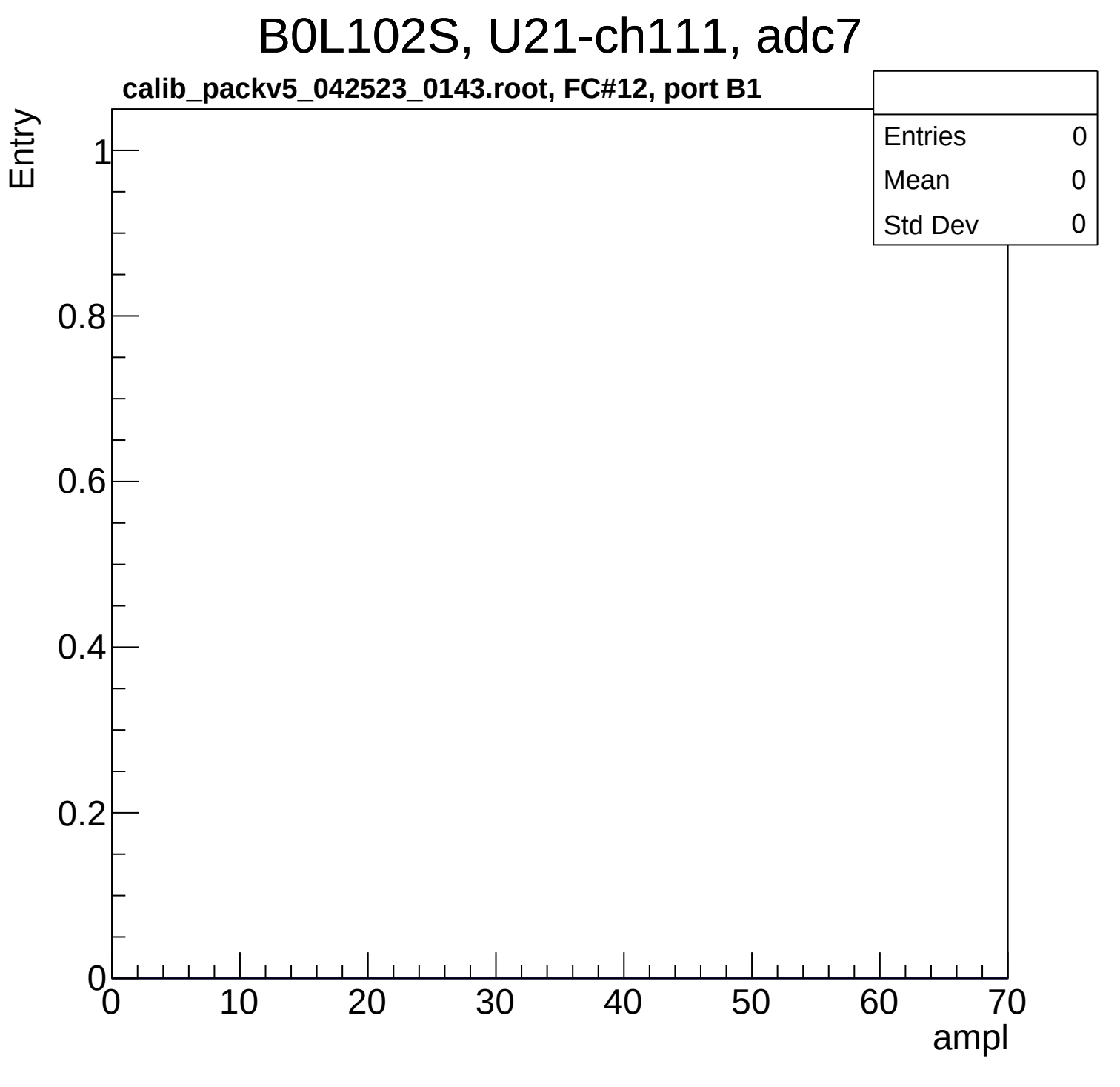
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch111, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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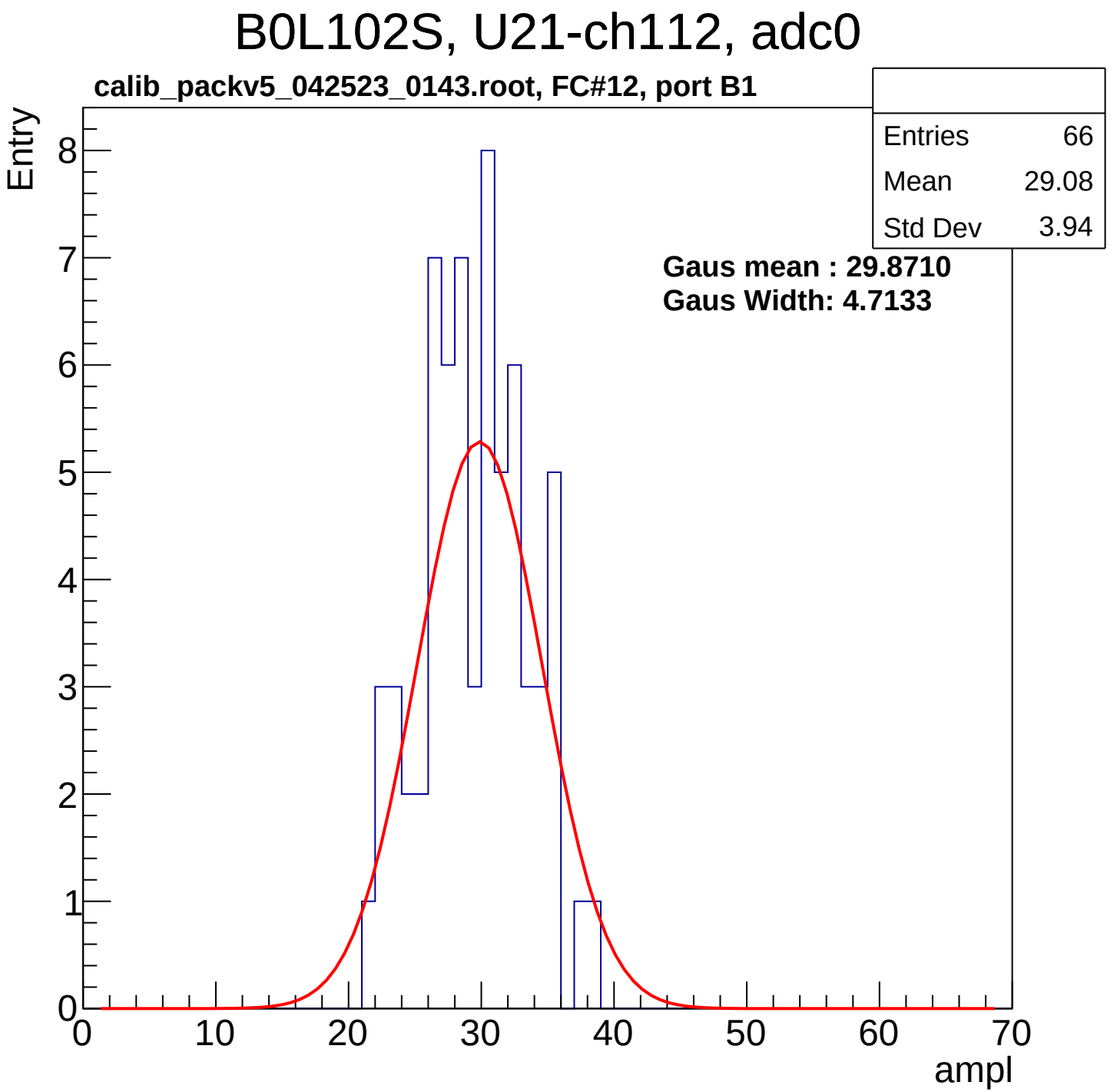
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch112, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 29.08
Std Dev 3.94
Gaus mean : 29.8710
Gaus Width: 4.7133
Entry
ampl
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1
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4
5
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0
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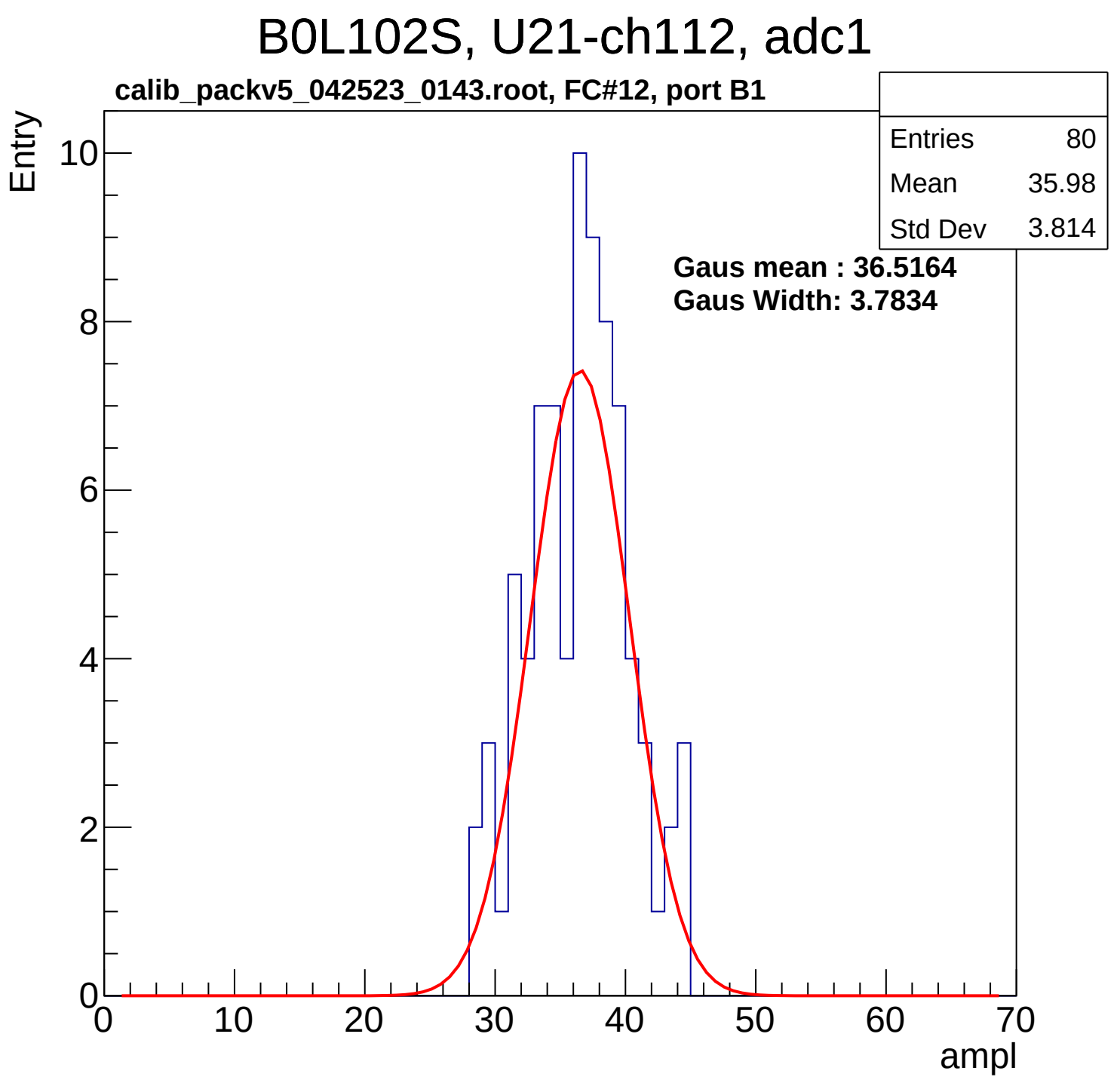
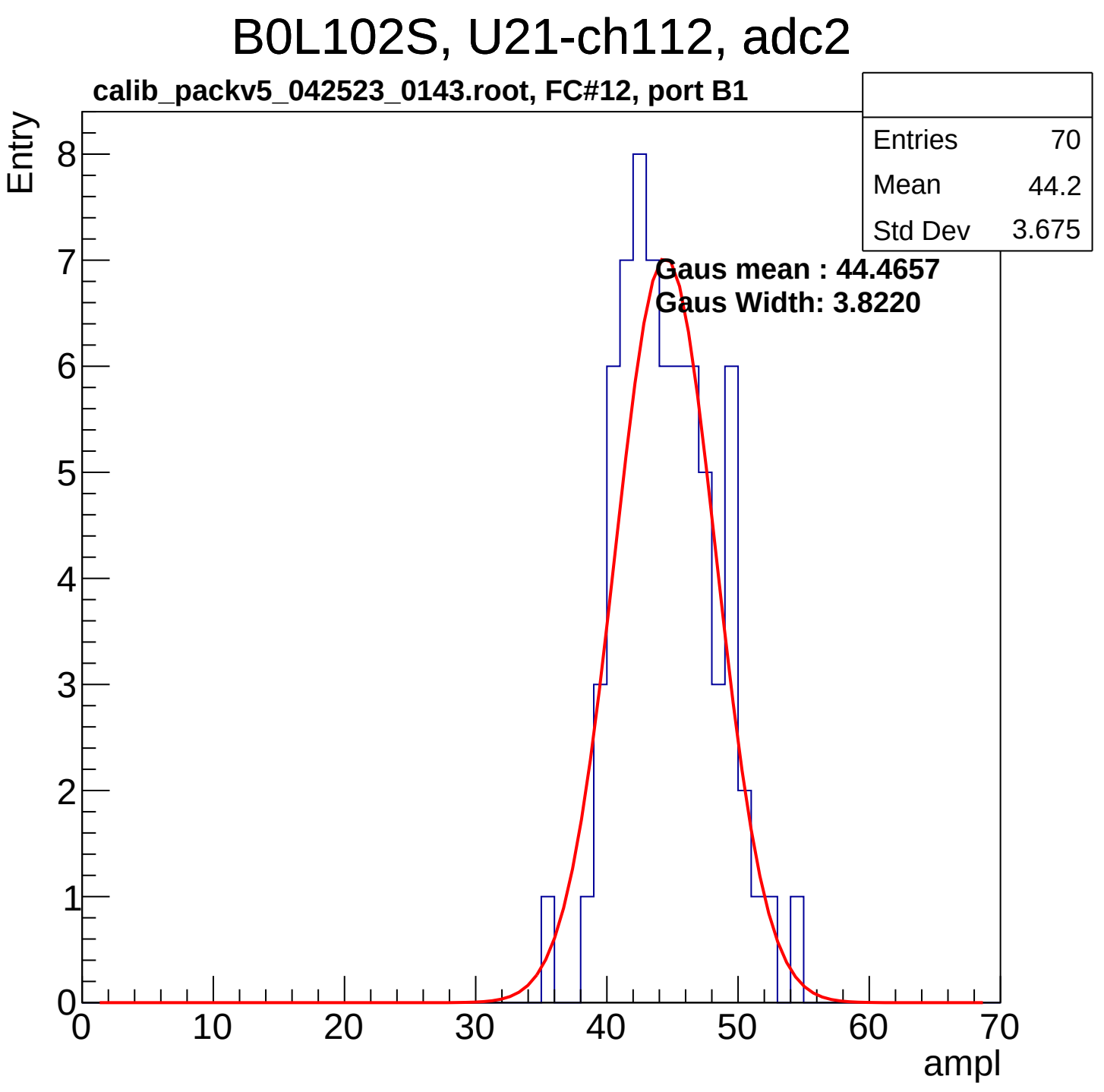
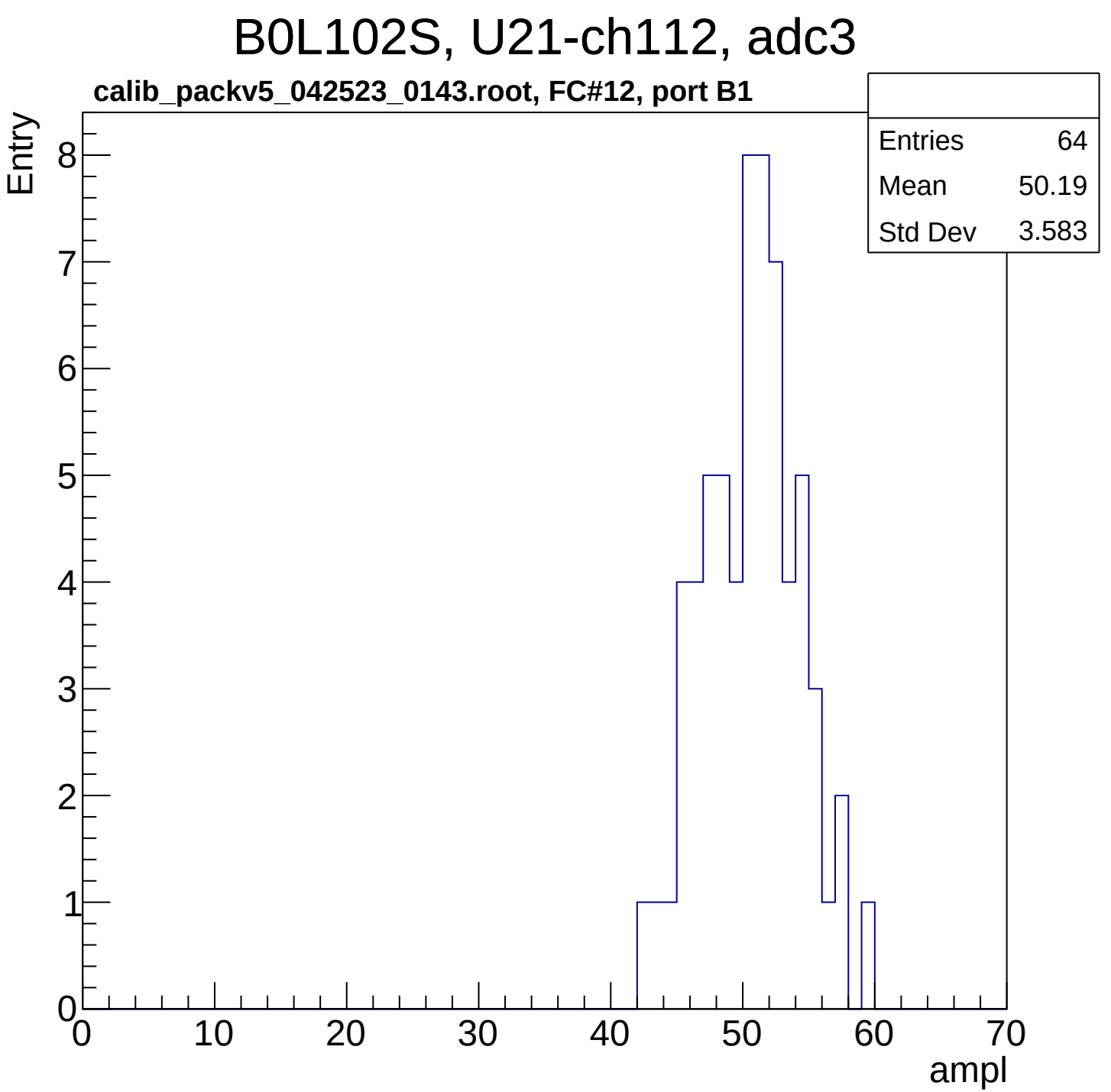
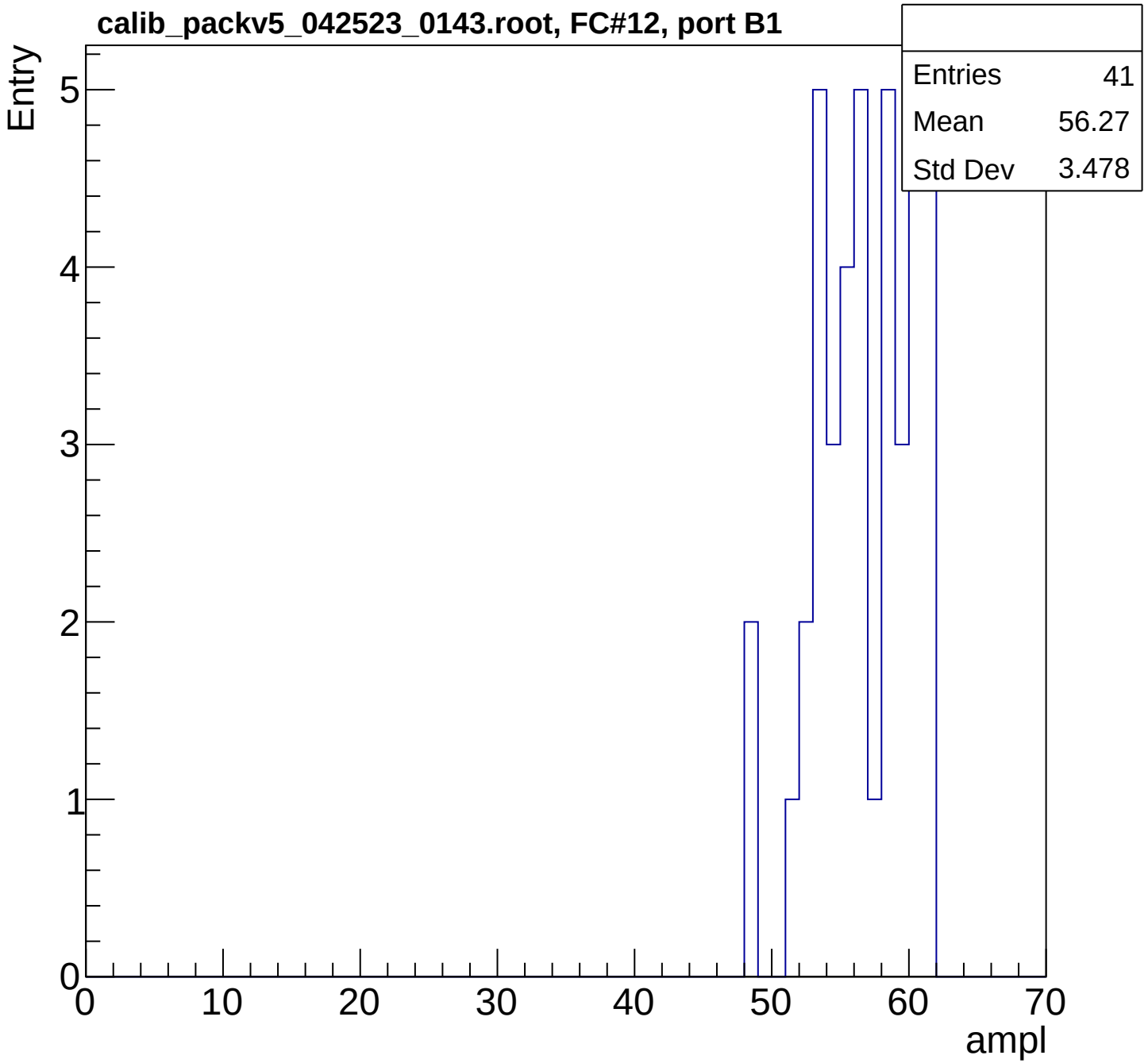
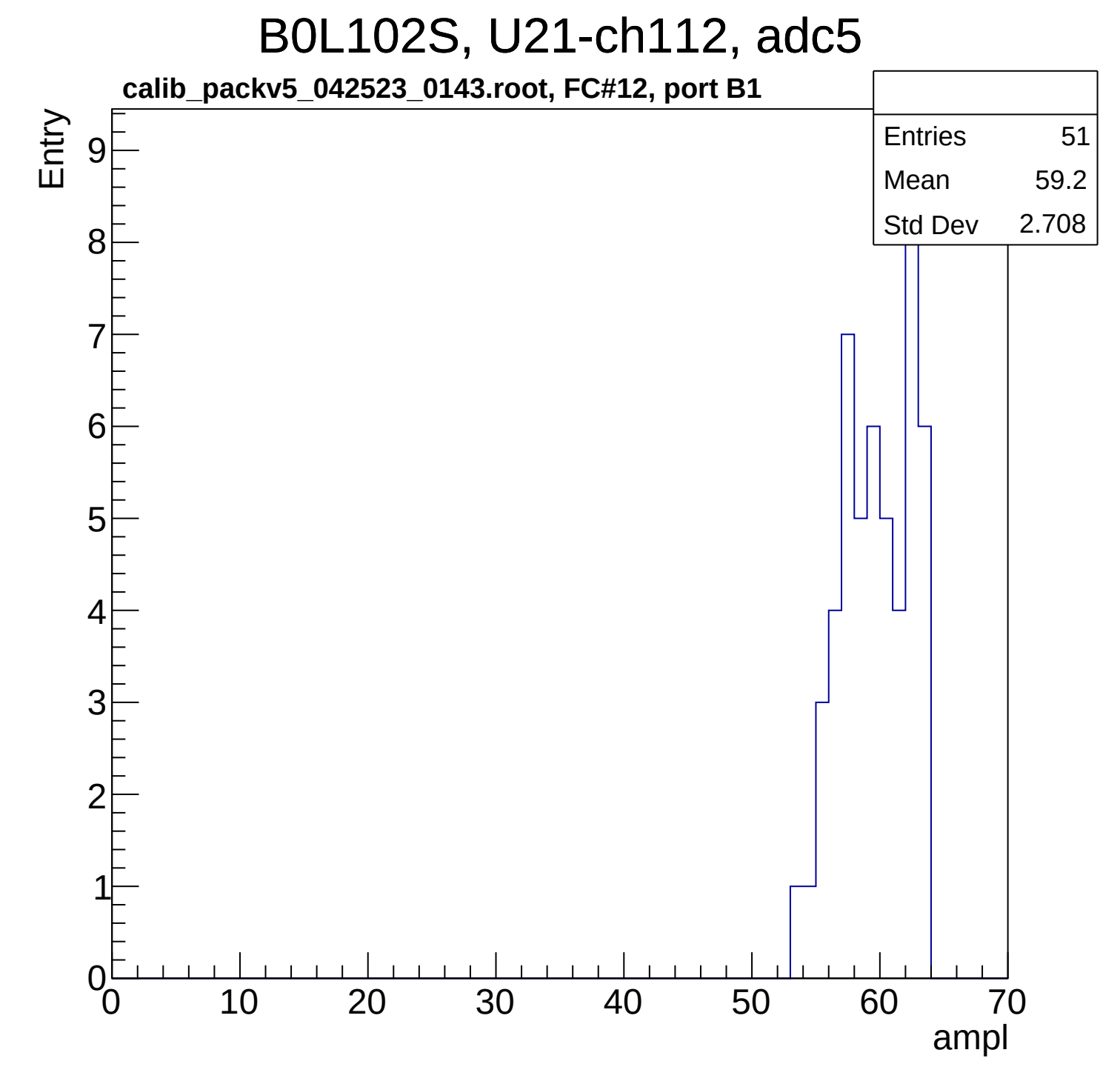

B0L102S, U21-ch112, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 35.98
Std Dev 3.814
Gaus mean : 36.5164
Gaus Width: 3.7834
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch112, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 44.2
Std Dev 3.675
Gaus mean : 44.4657
Gaus Width: 3.8220
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch112, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 50.19
Std Dev 3.583
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch112, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 56.27
Std Dev 3.478
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch112, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 59.2
Std Dev 2.708
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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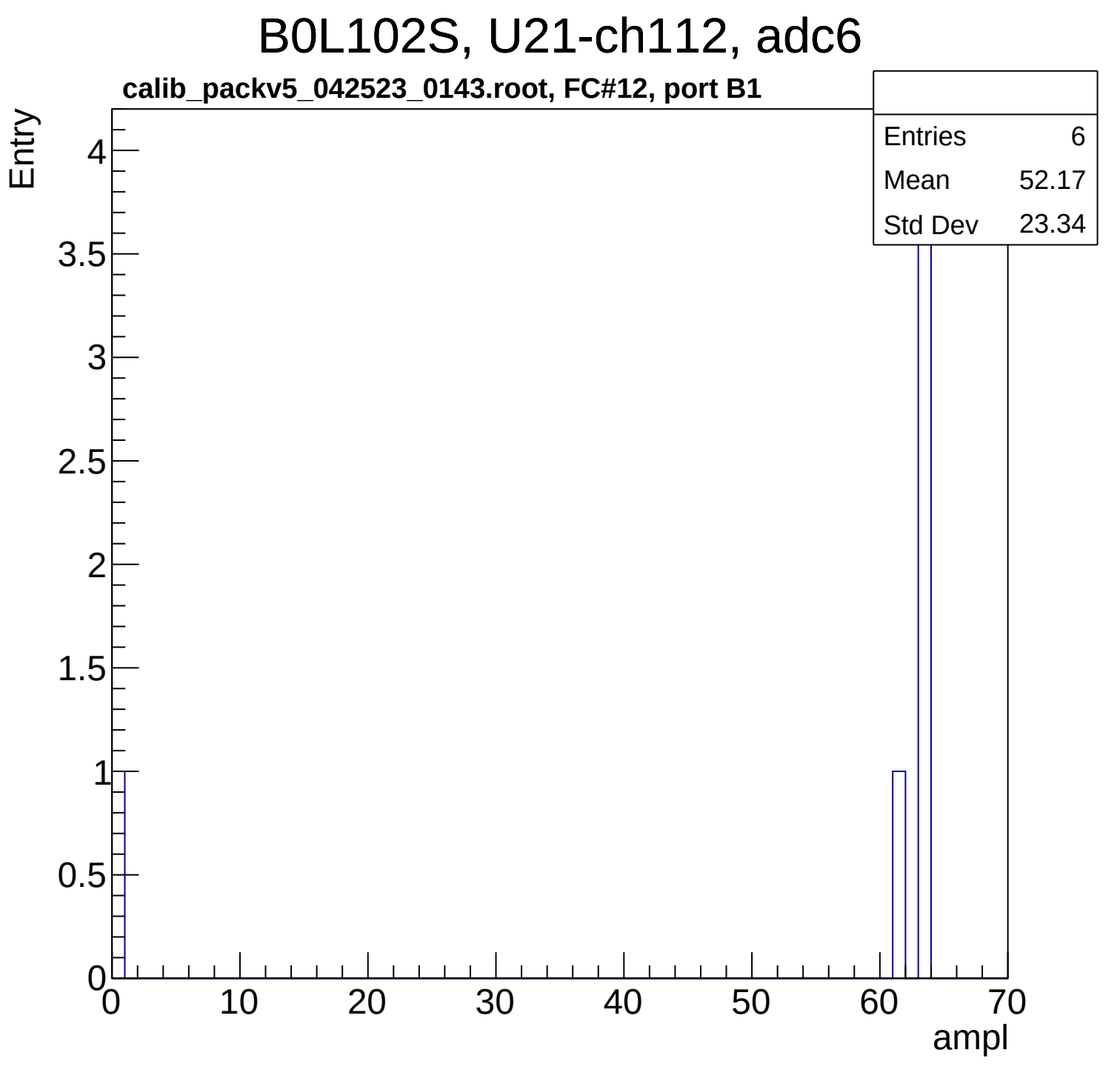
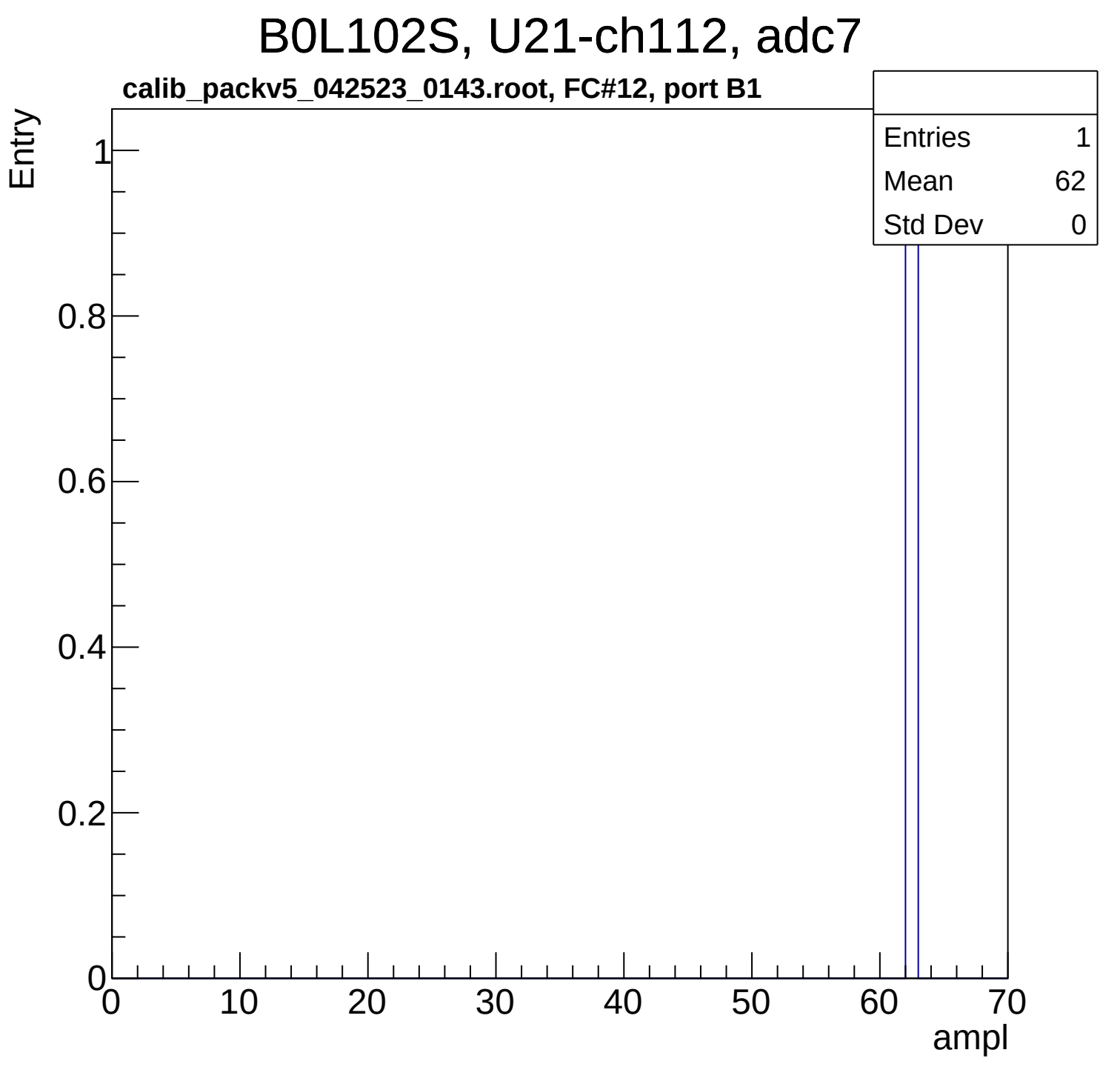
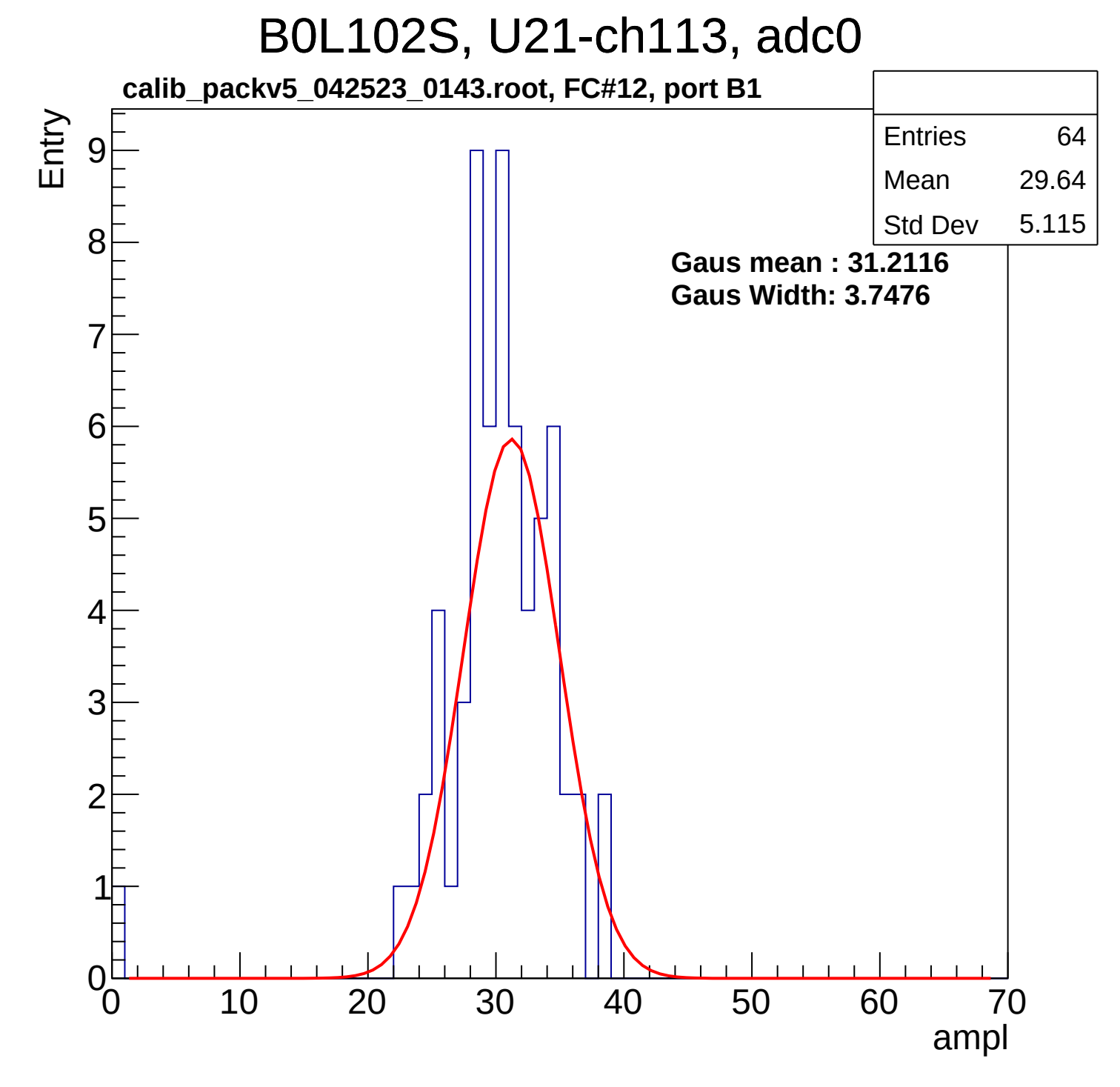
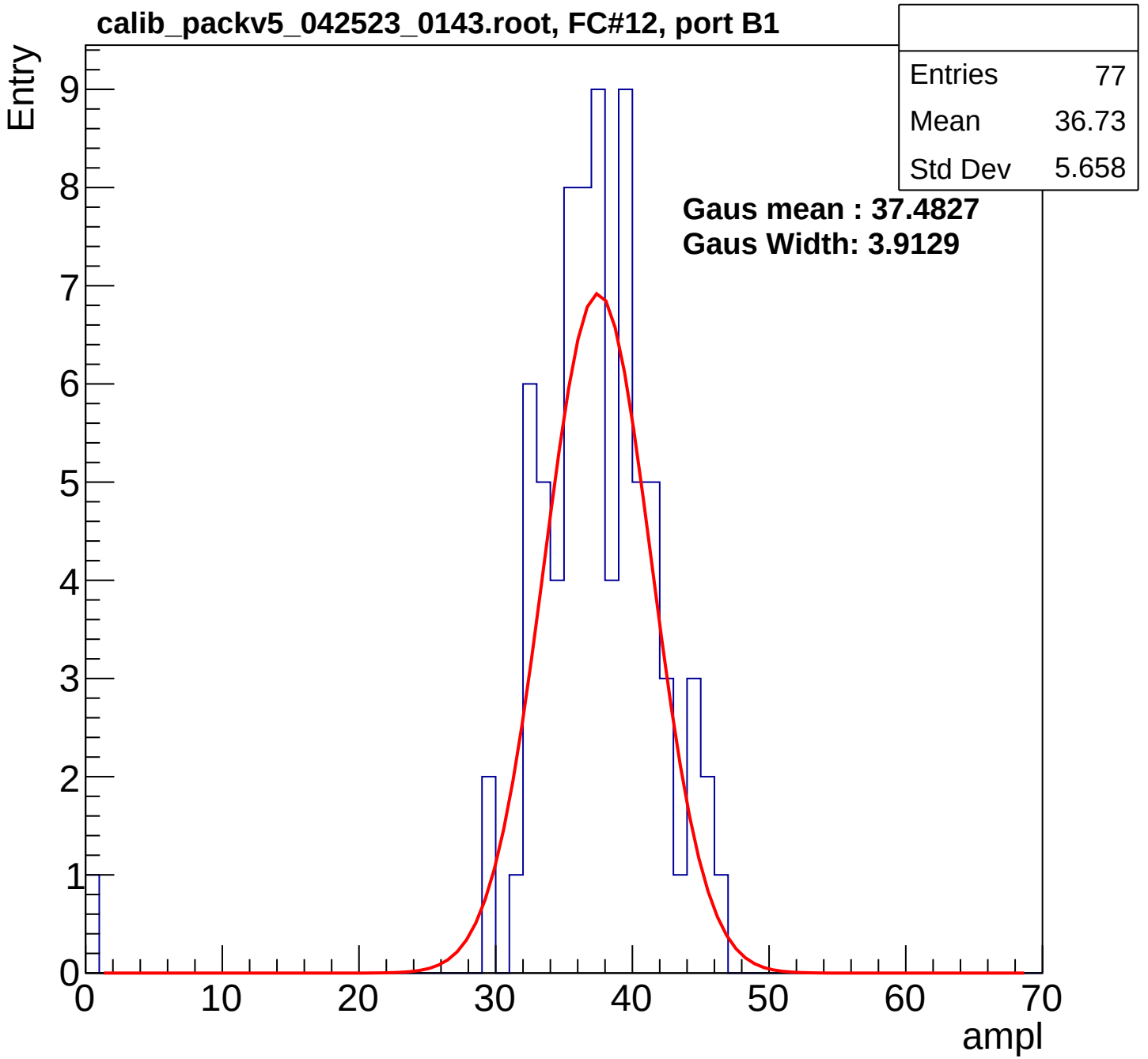

B0L102S, U21-ch112, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 52.17
Std Dev 23.34
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch112, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch113, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 29.64
Std Dev 5.115
Gaus mean : 31.2116
Gaus Width: 3.7476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch113, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 36.73
Std Dev 5.658
Gaus mean : 37.4827
Gaus Width: 3.9129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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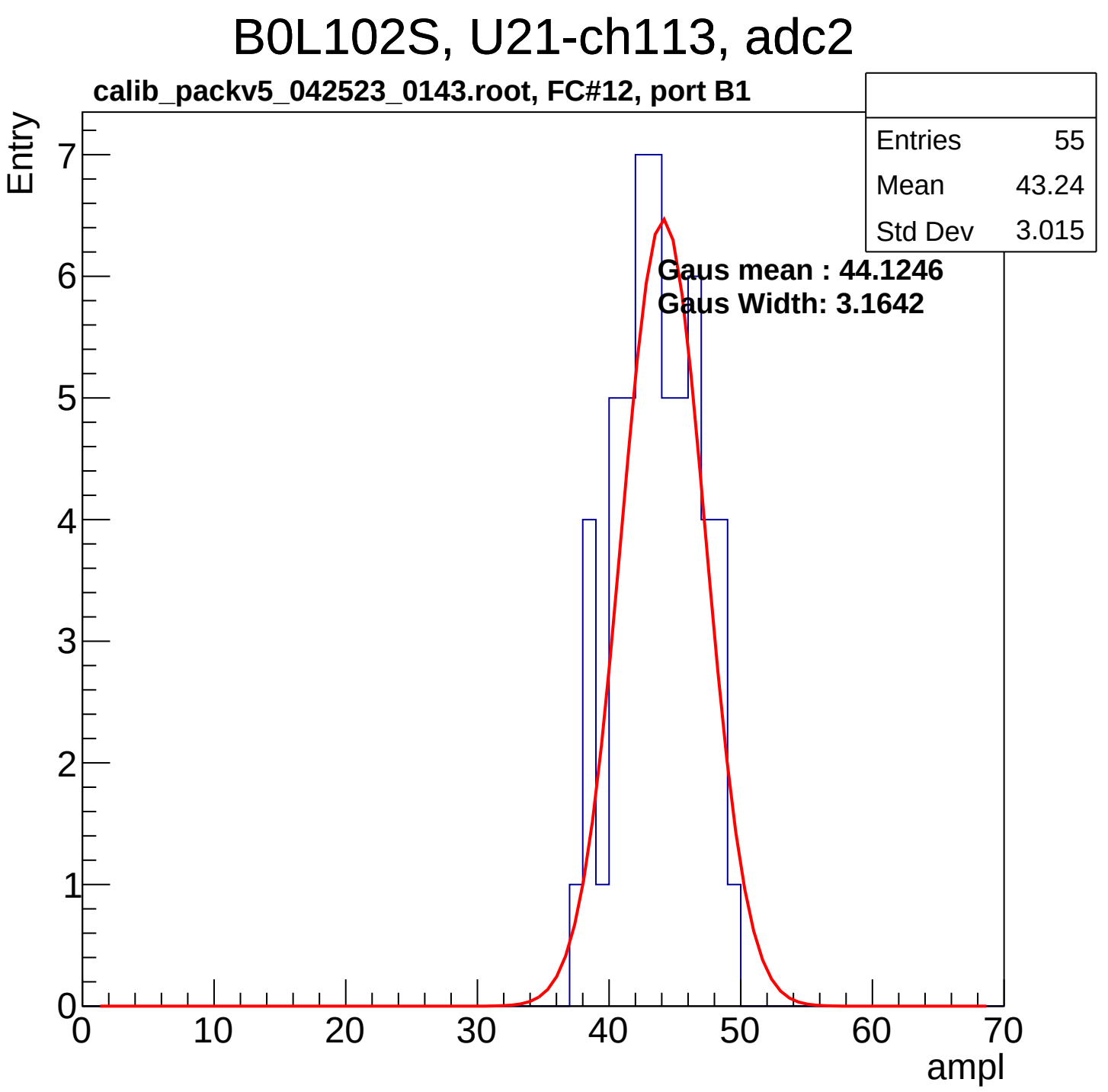
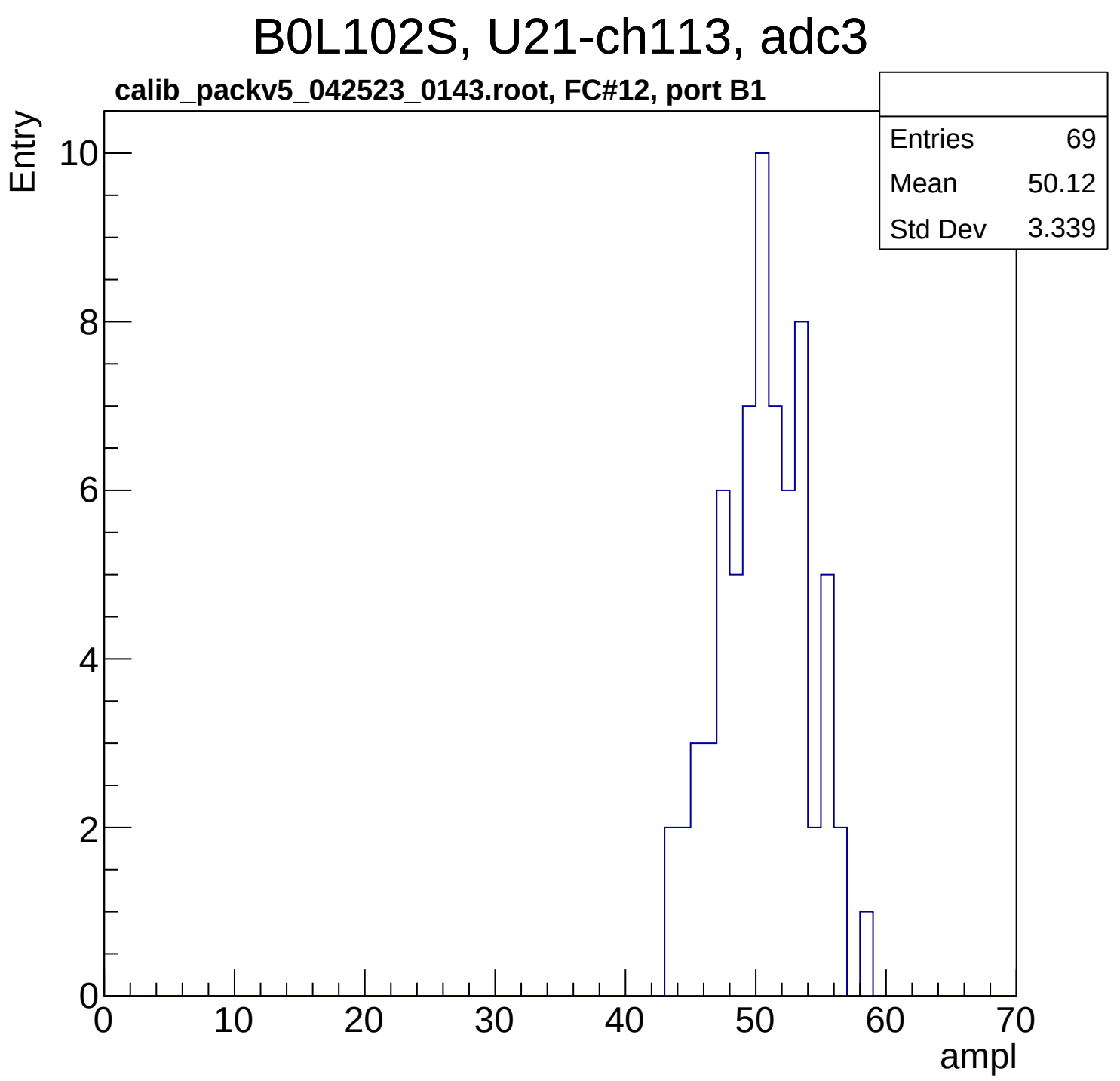
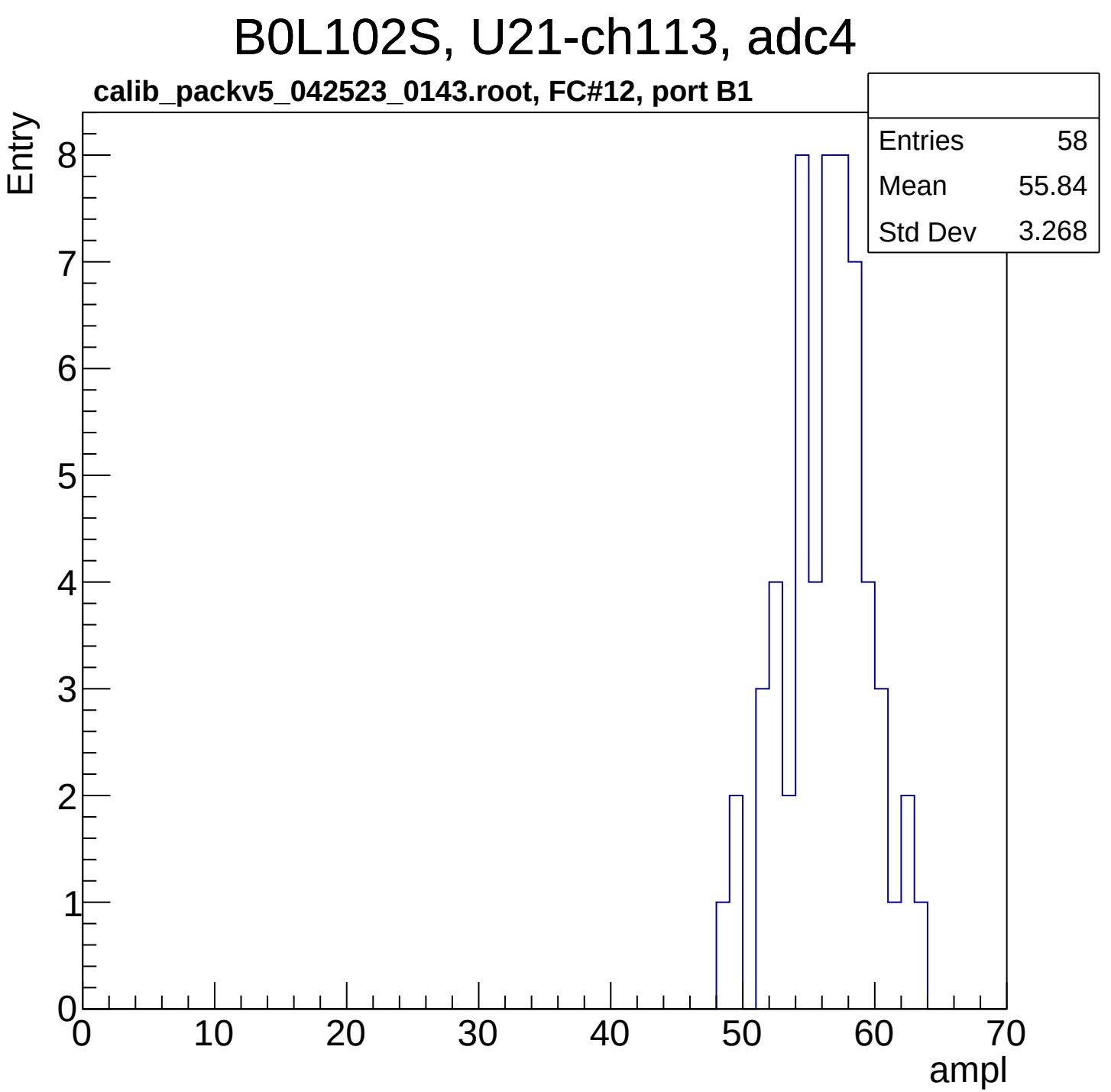
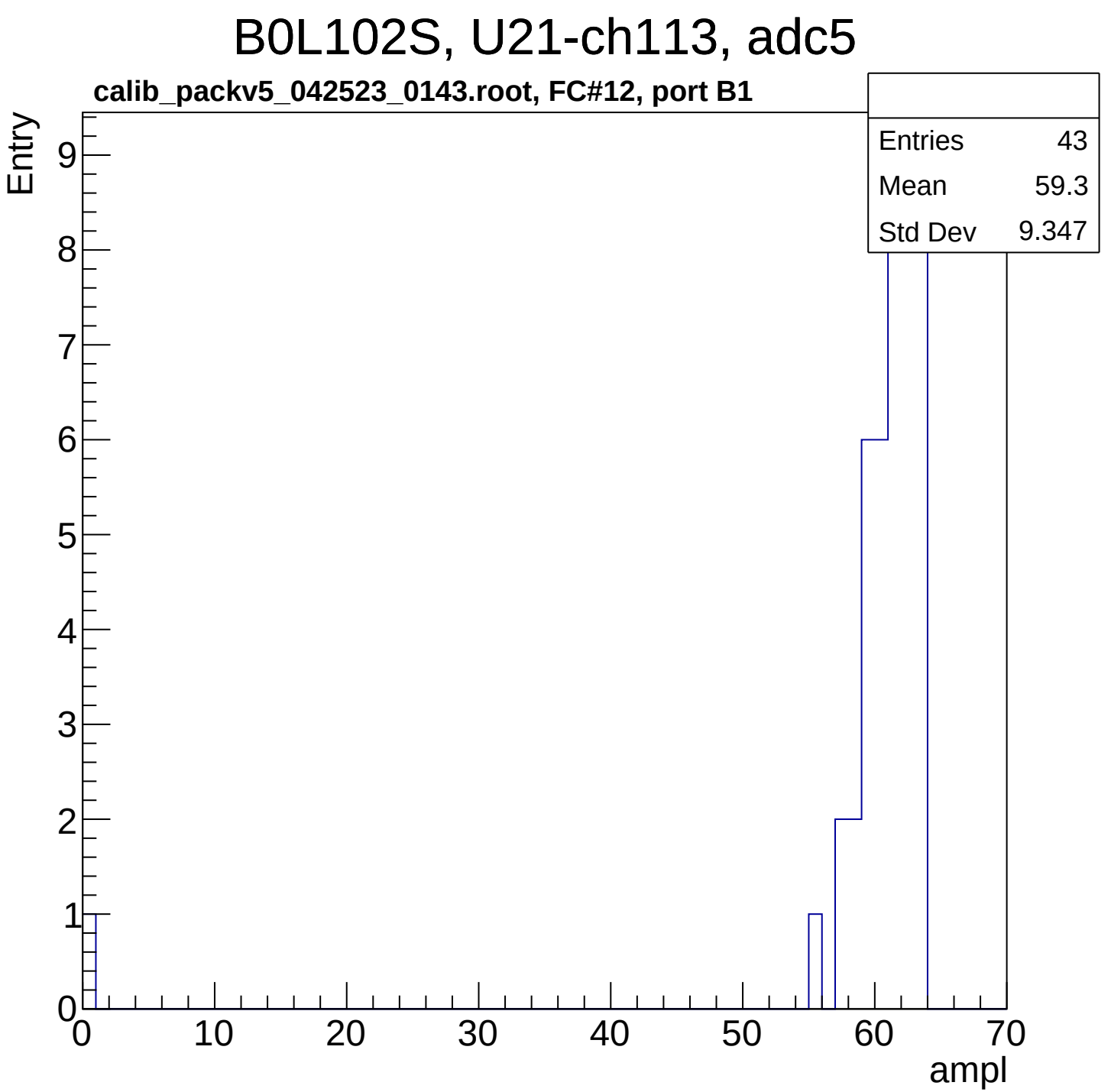
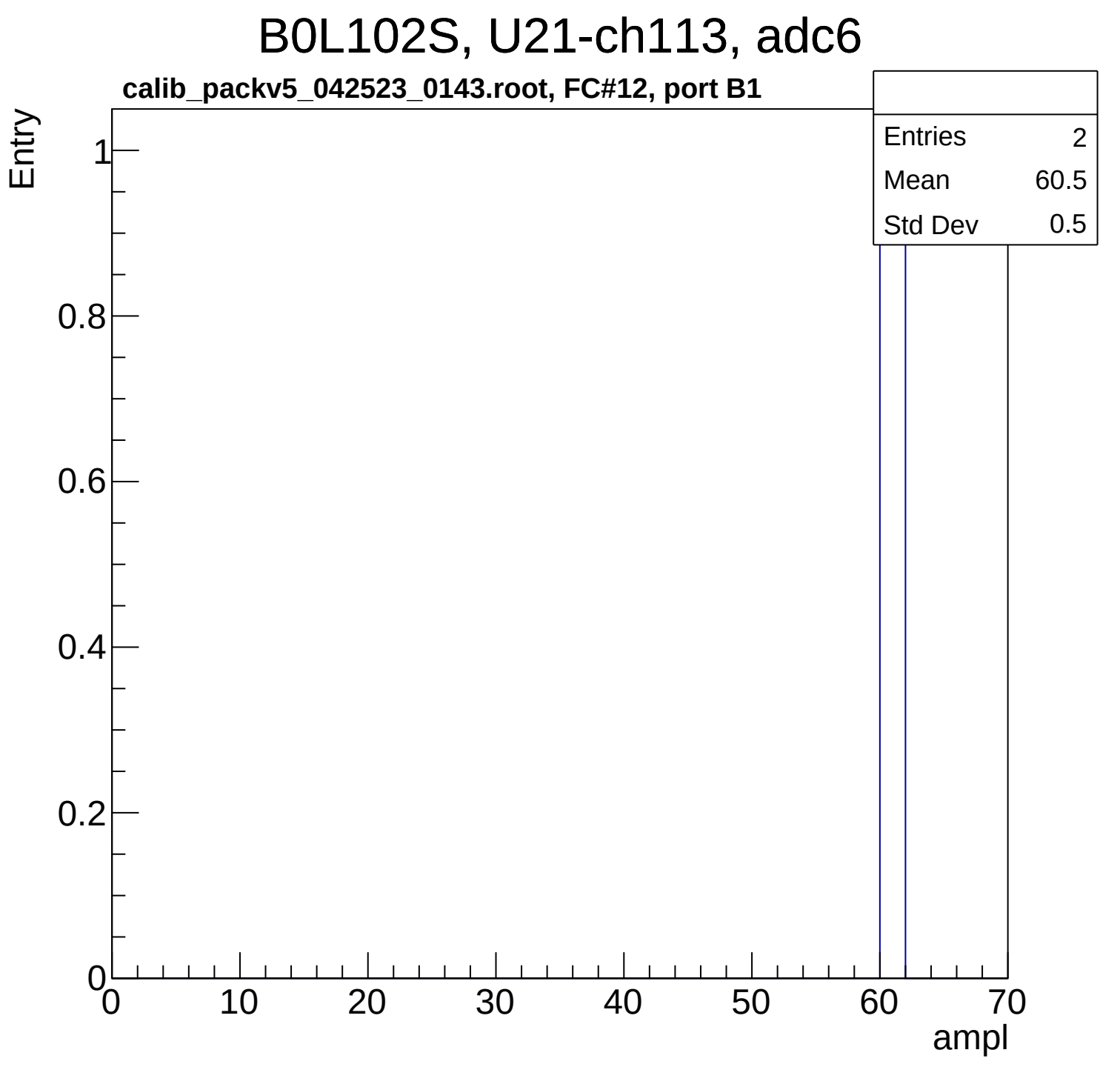
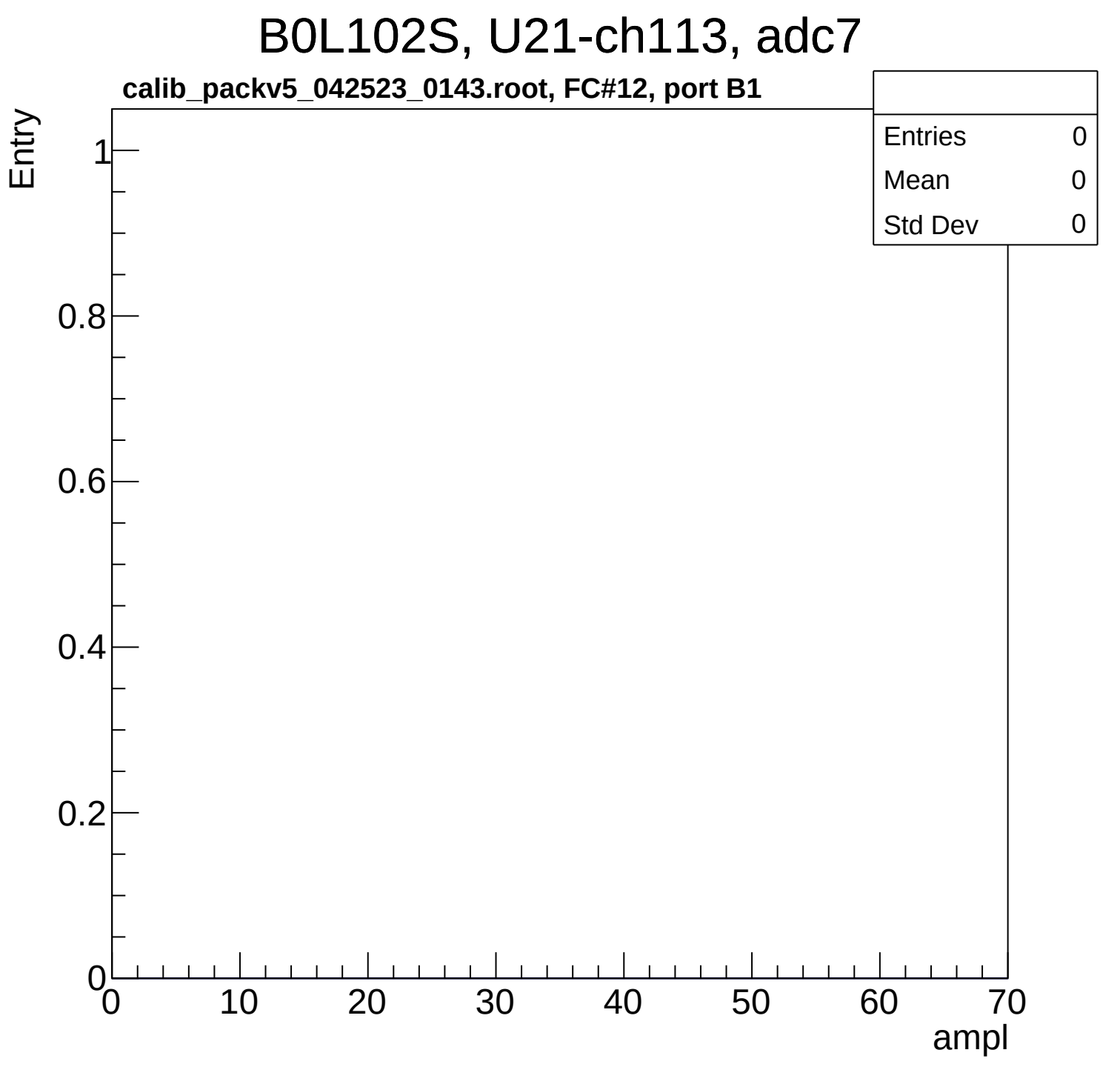

B0L102S, U21-ch113, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 43.24
Std Dev 3.015
Gaus mean : 44.1246
Gaus Width: 3.1642
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

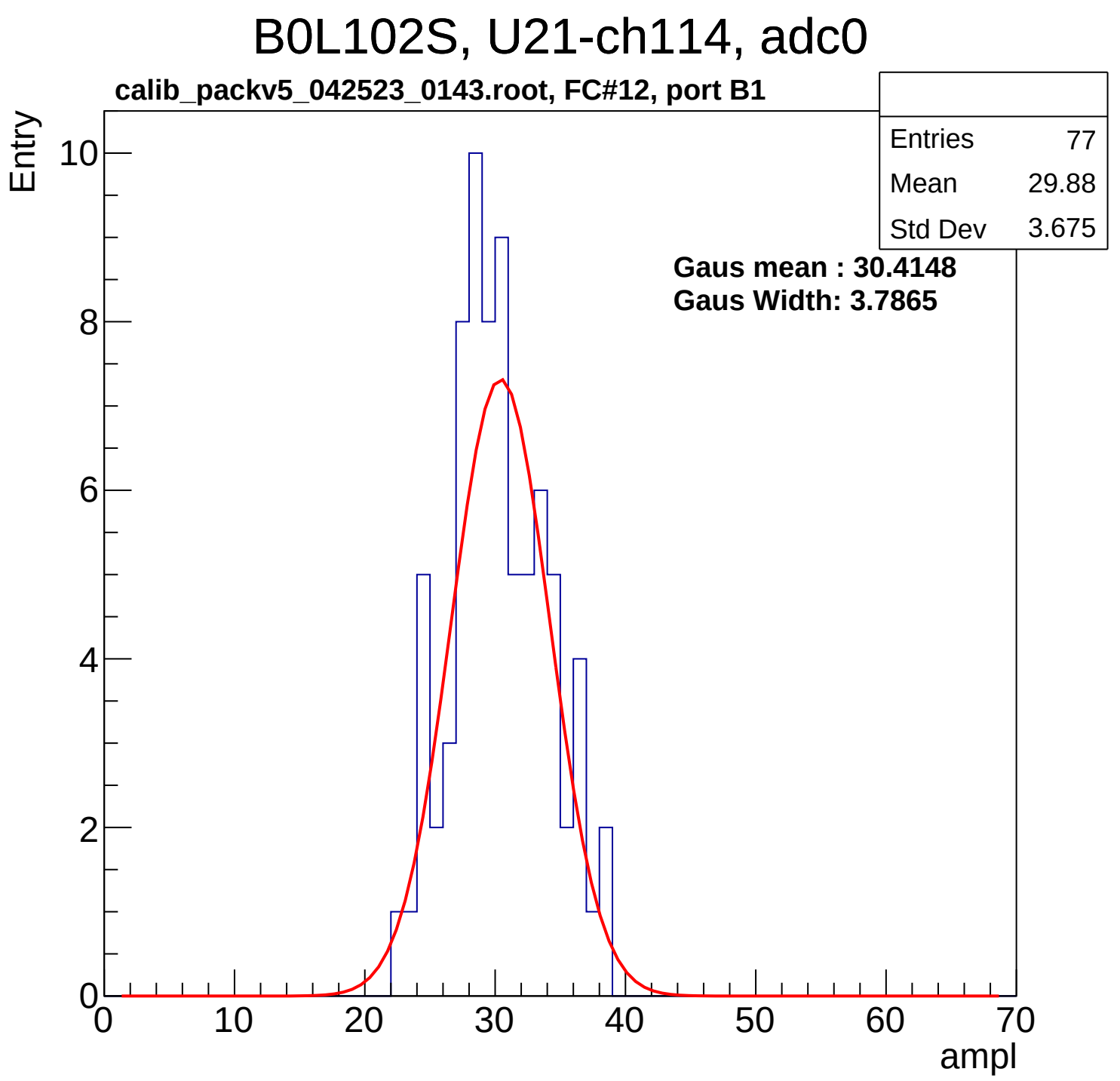

B0L102S, U21-ch113, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 50.12
Std Dev 3.339
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

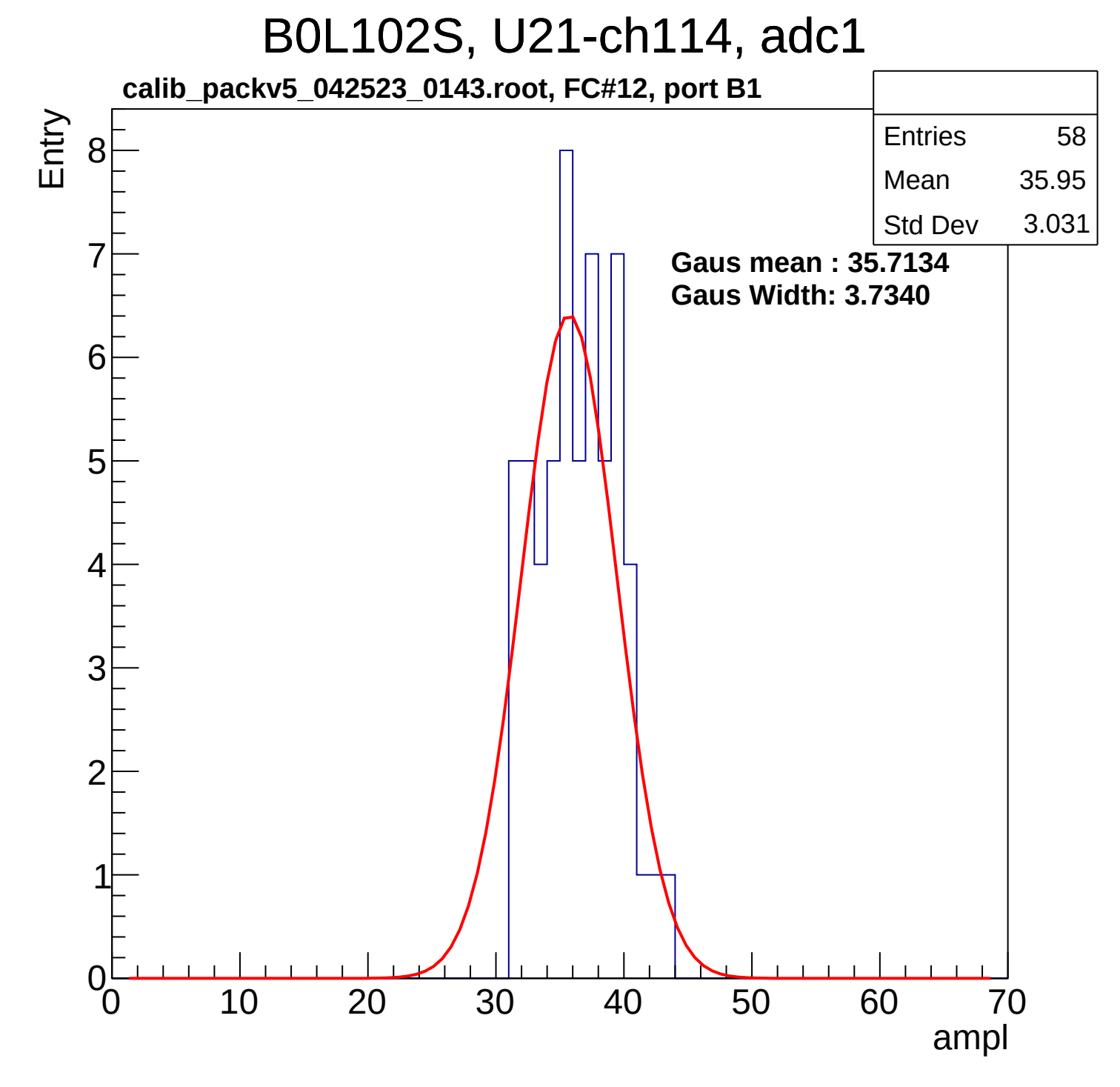

B0L102S, U21-ch113, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 55.84
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

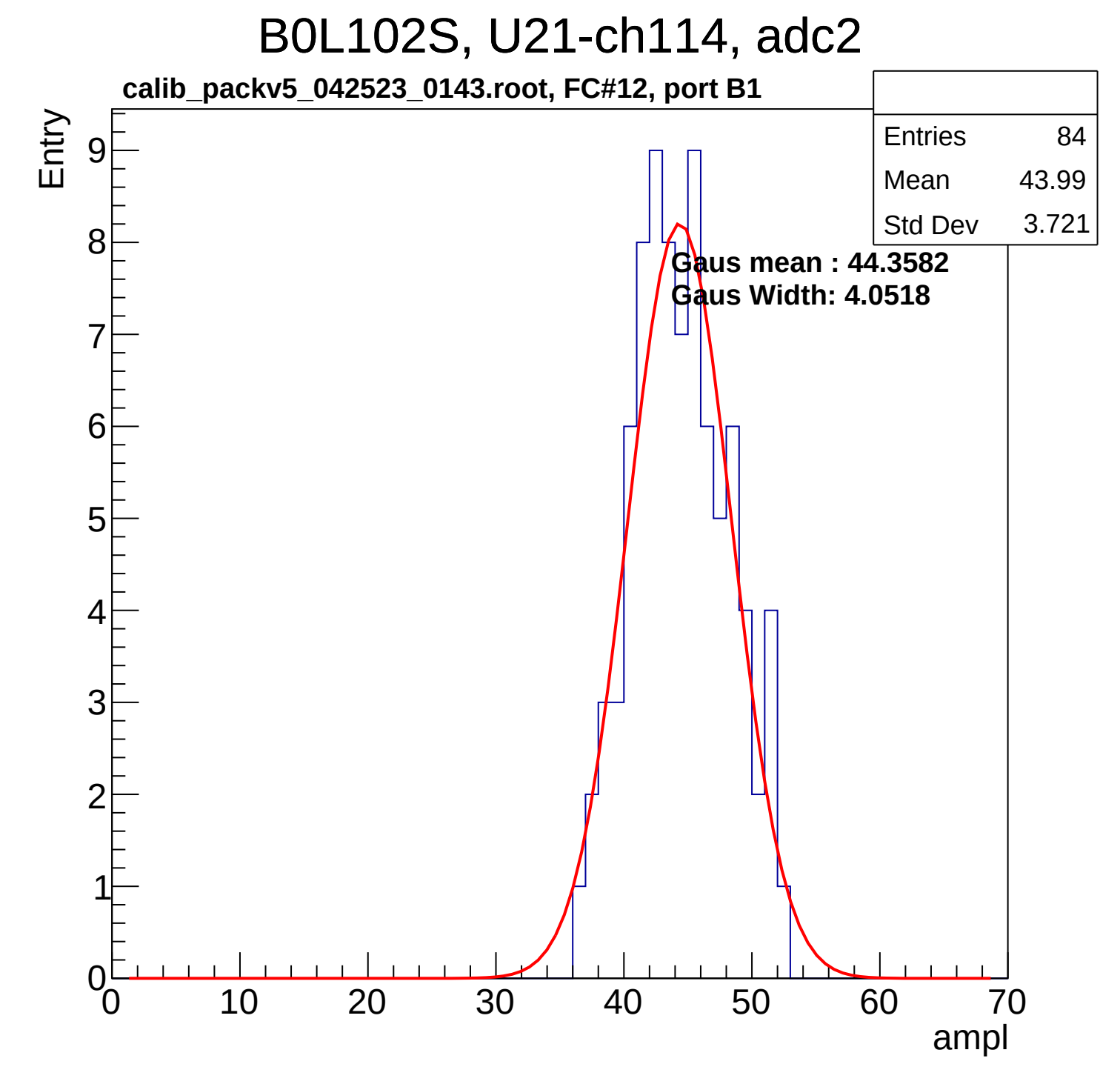

B0L102S, U21-ch113, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.3
Std Dev 9.347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch113, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 60.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch113, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch114, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 29.88
Std Dev 3.675
Gaus mean : 30.4148
Gaus Width: 3.7865
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch114, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 35.95
Std Dev 3.031
Gaus mean : 35.7134
Gaus Width: 3.7340
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch114, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 43.99
Std Dev 3.721
Gaus mean : 44.3582
Gaus Width: 4.0518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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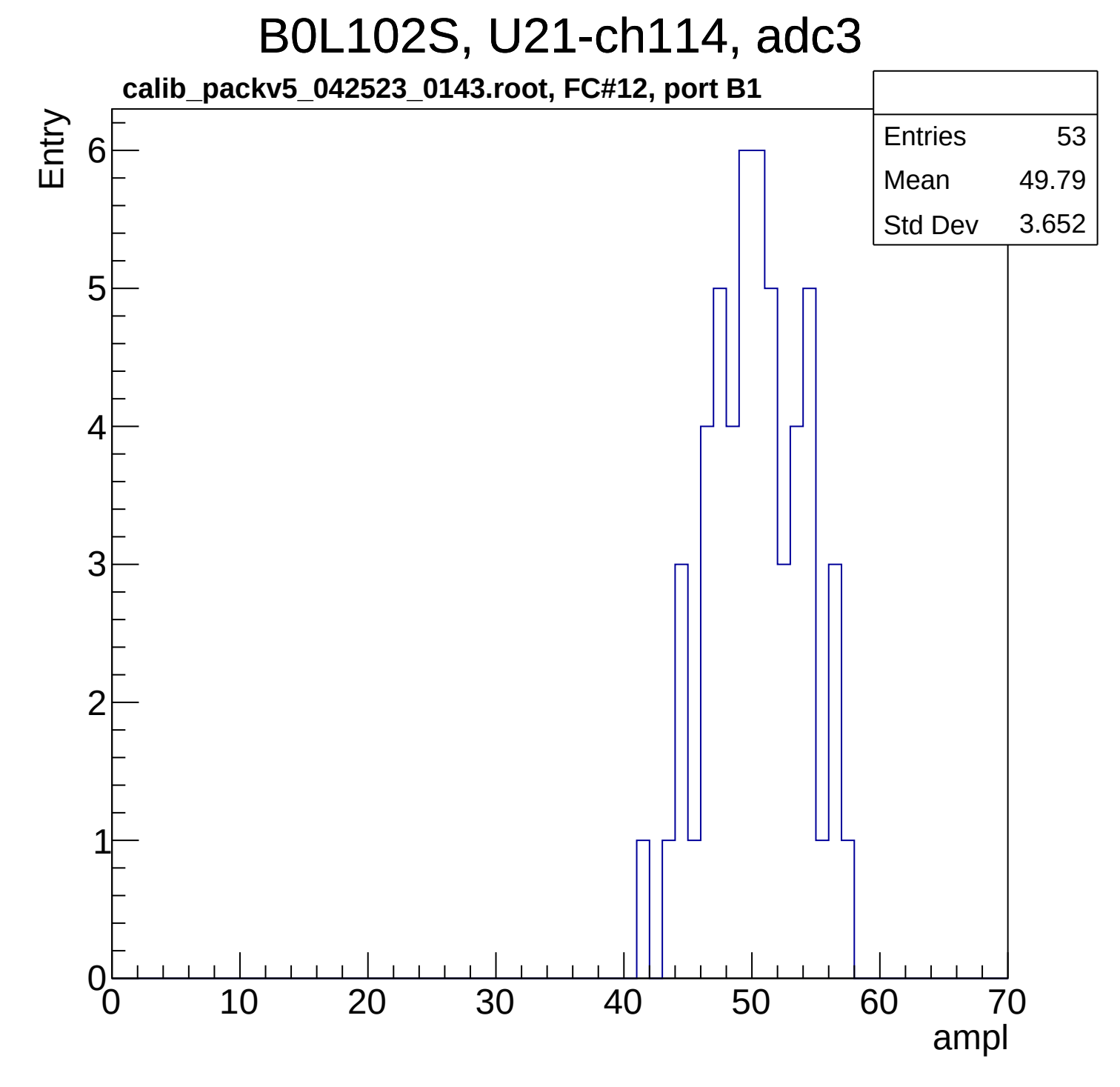
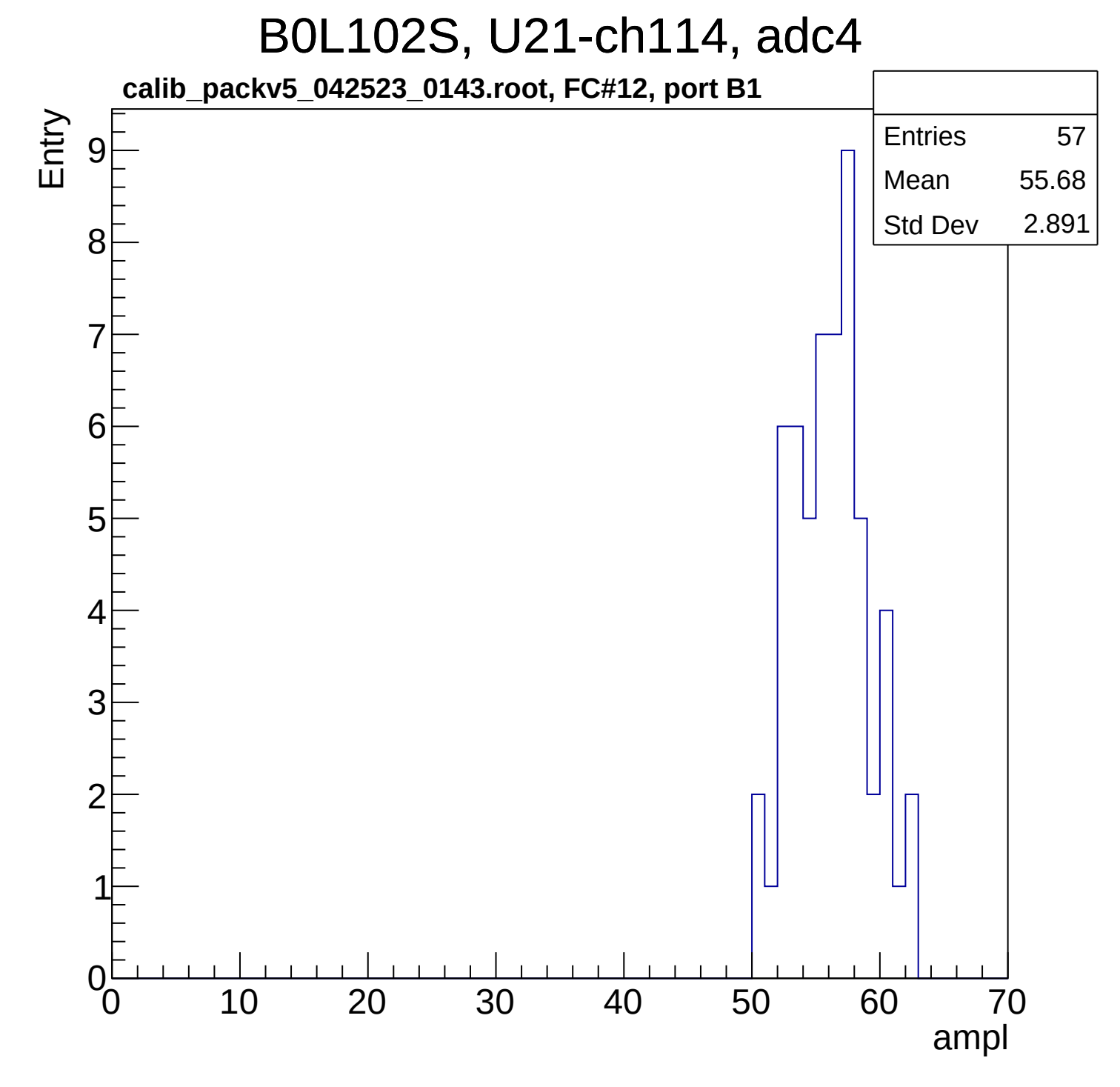

B0L102S, U21-ch114, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 49.79
Std Dev 3.652
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch114, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.68
Std Dev 2.891
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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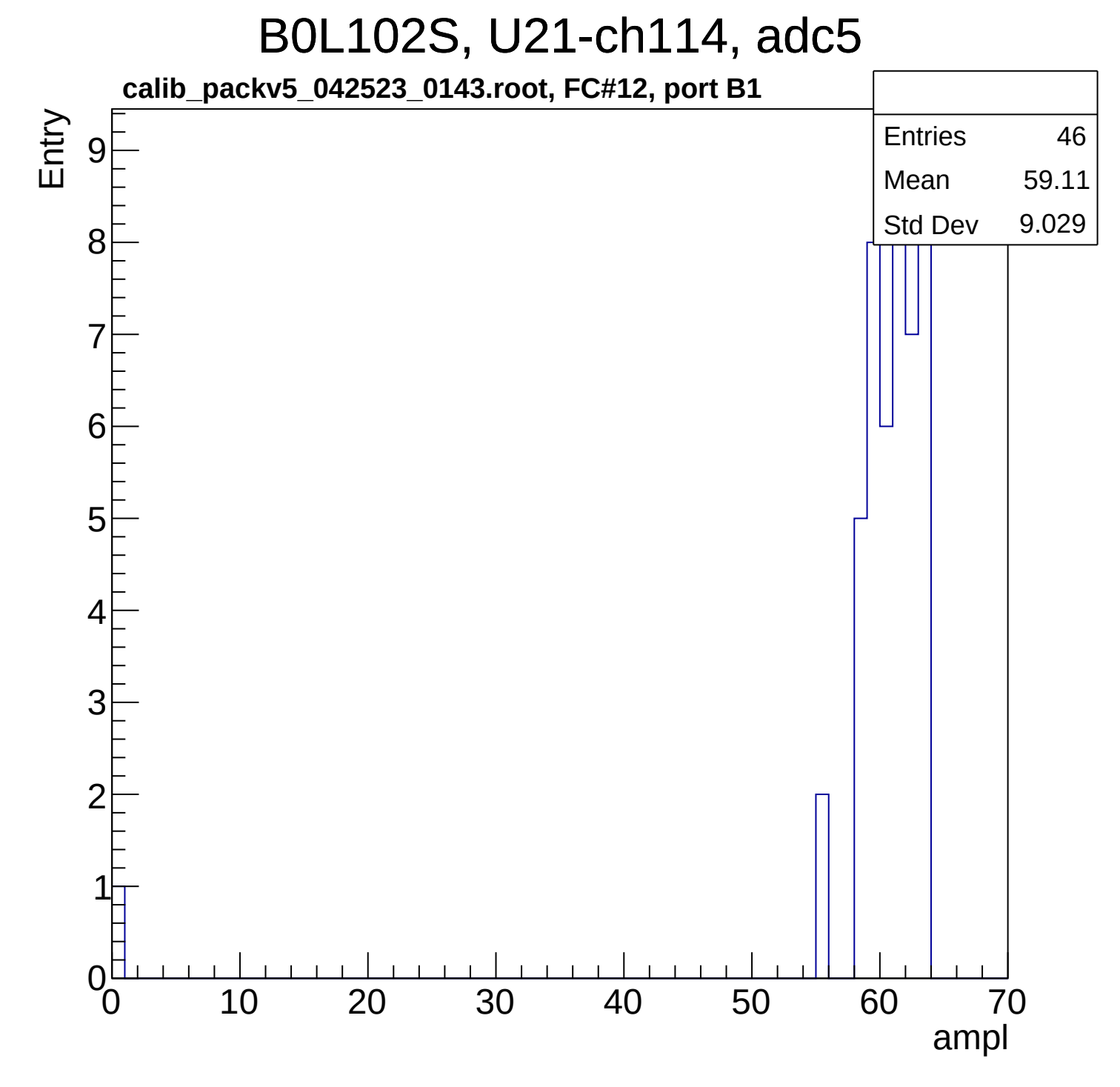
B0L102S, U21-ch114, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.11
Std Dev 9.029
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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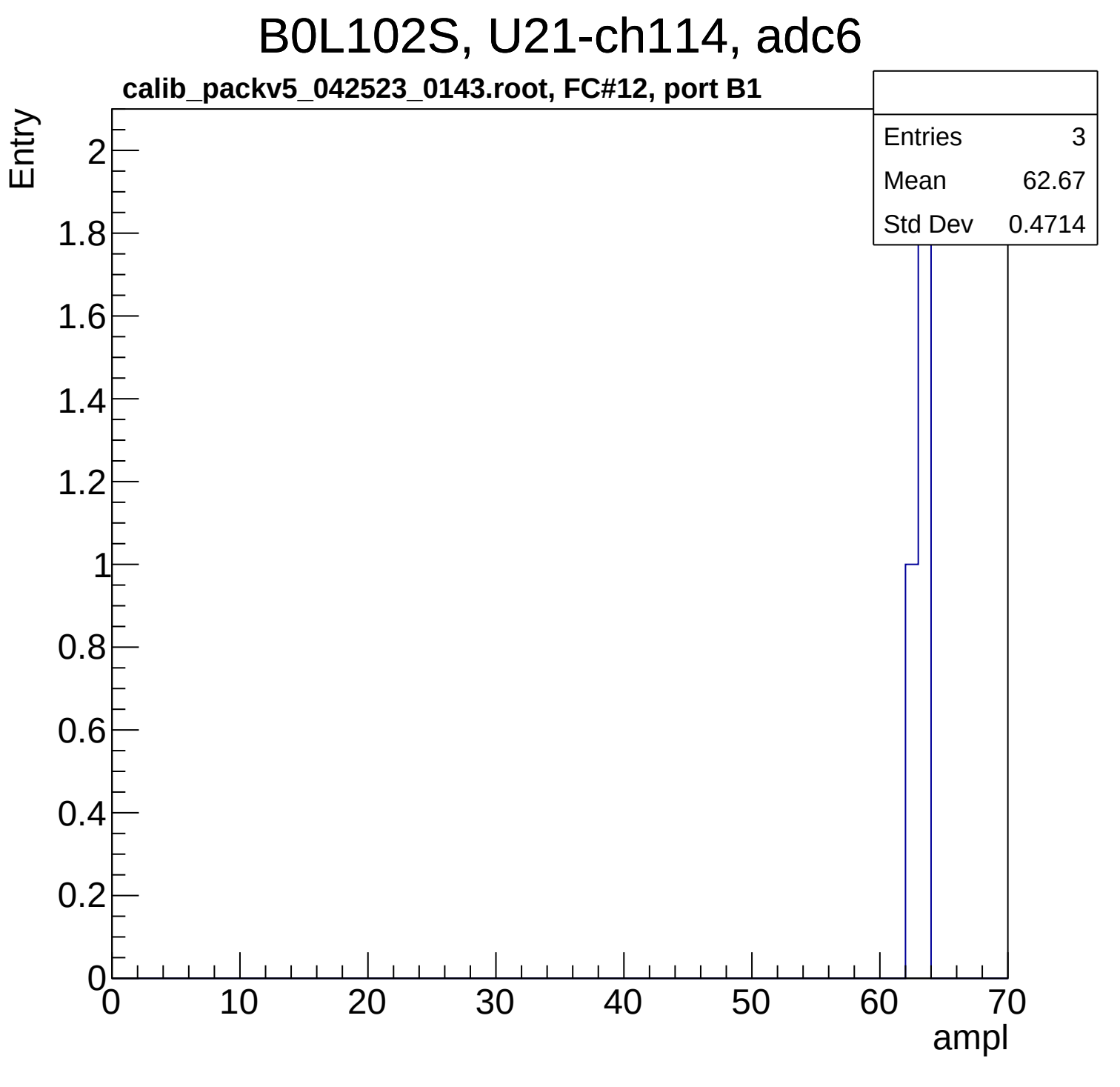
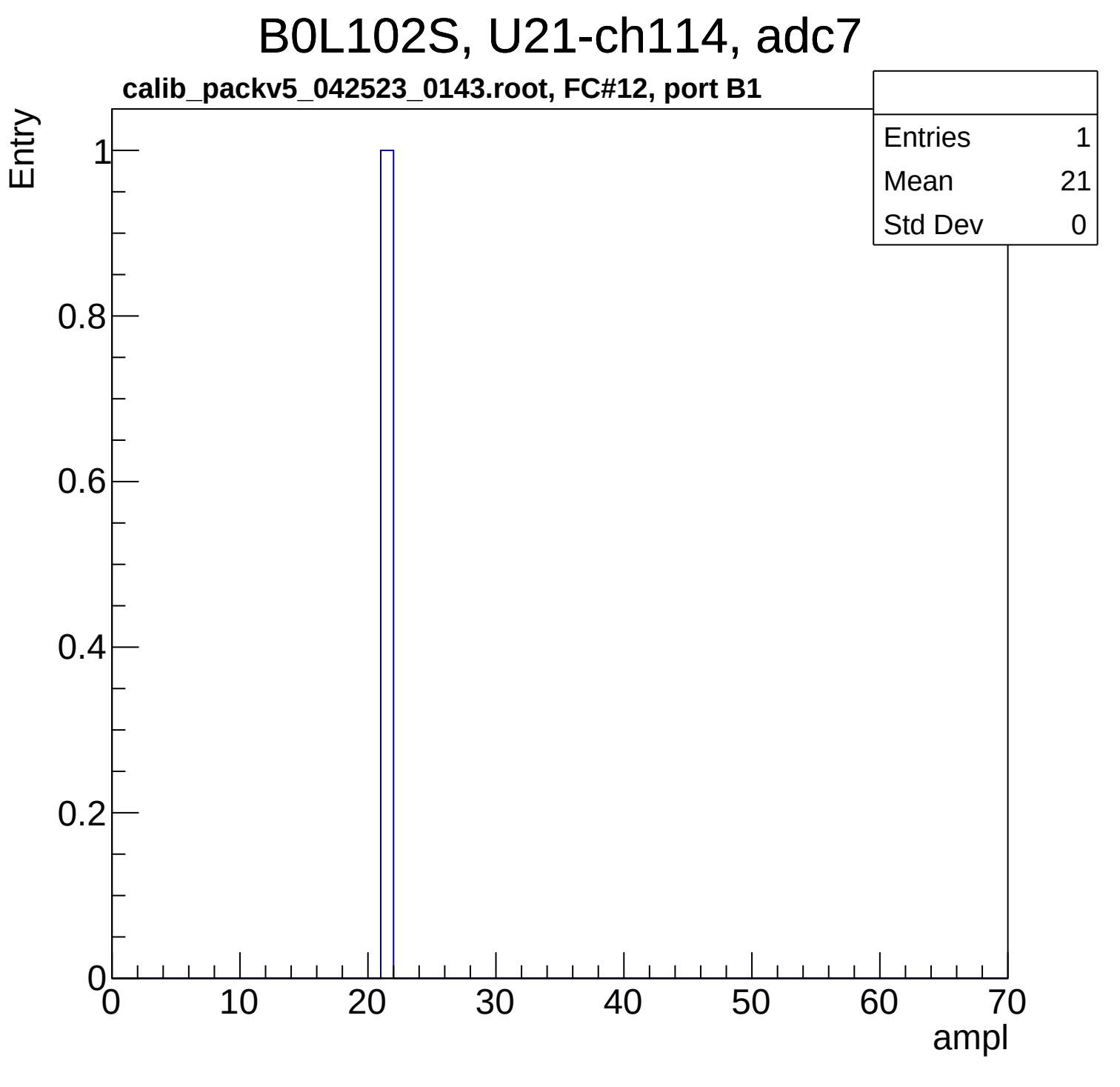
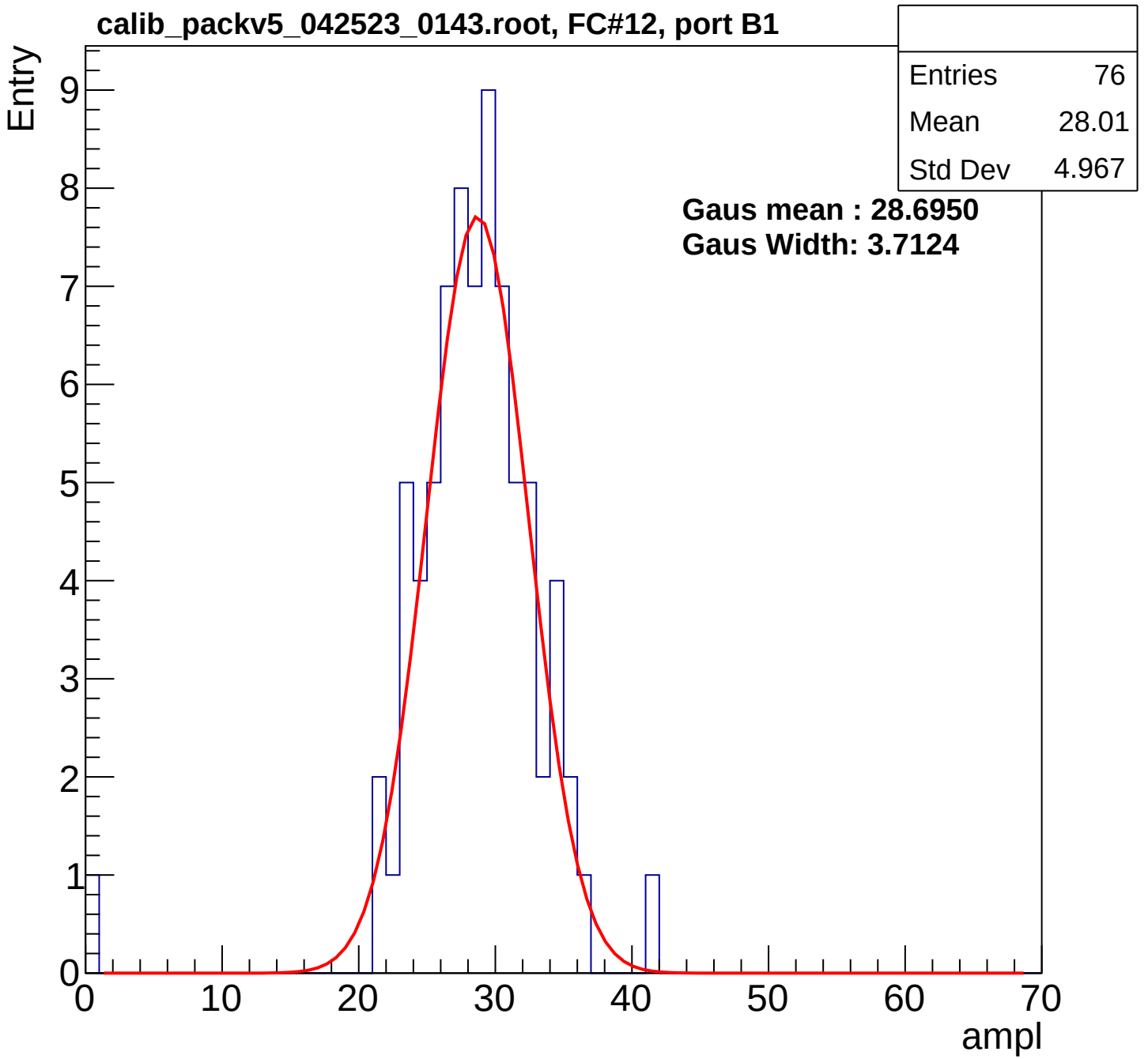
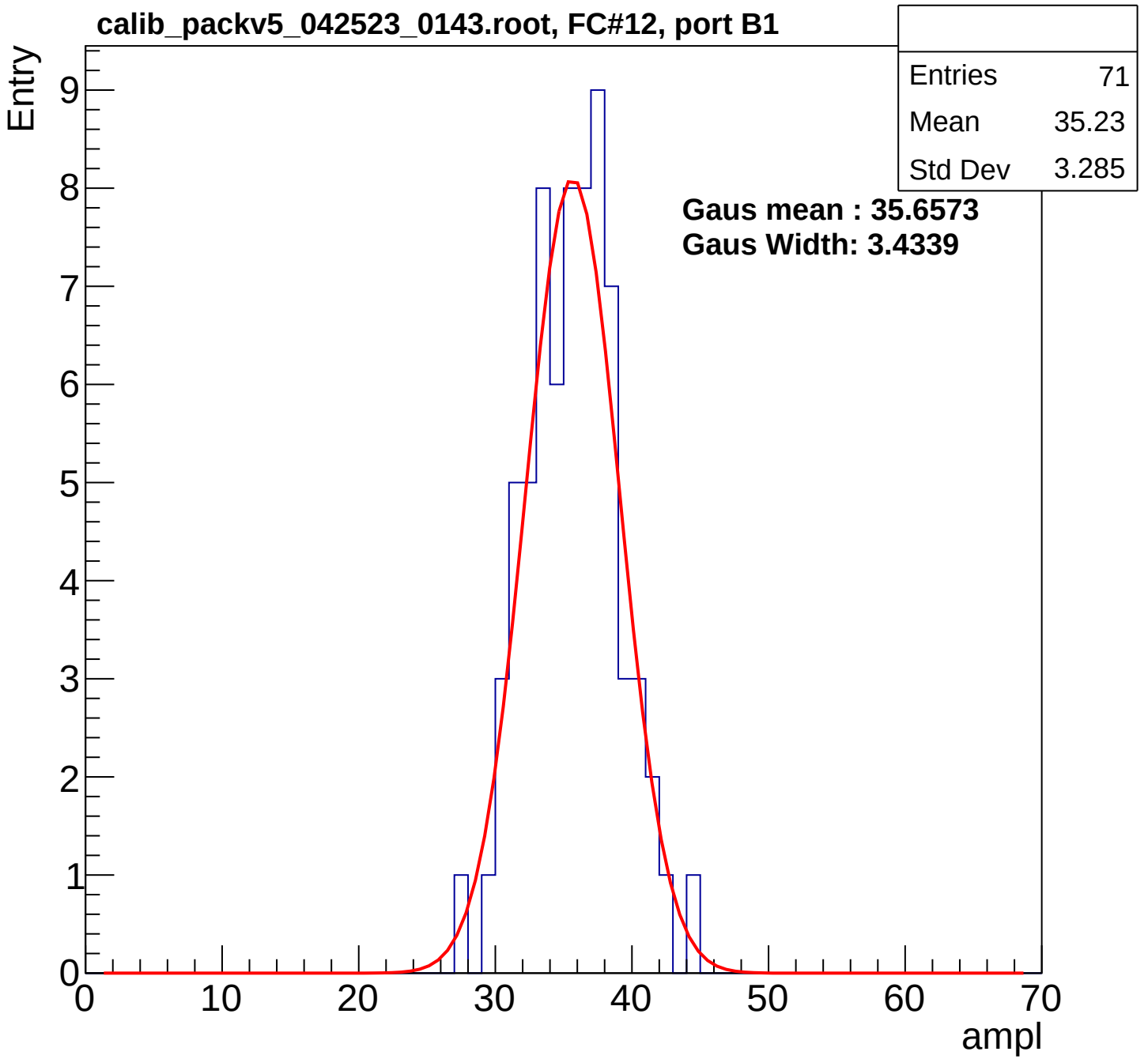
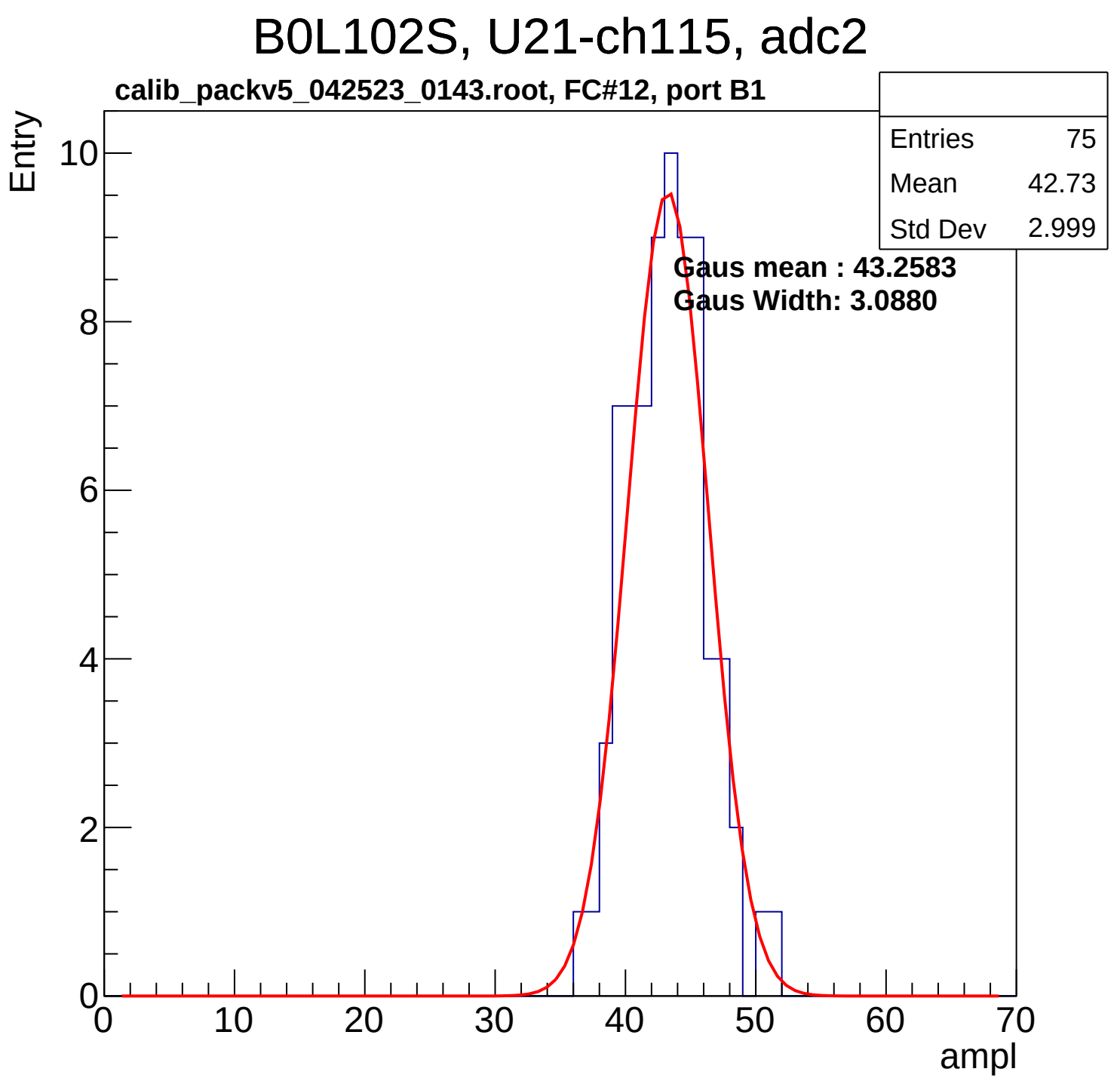
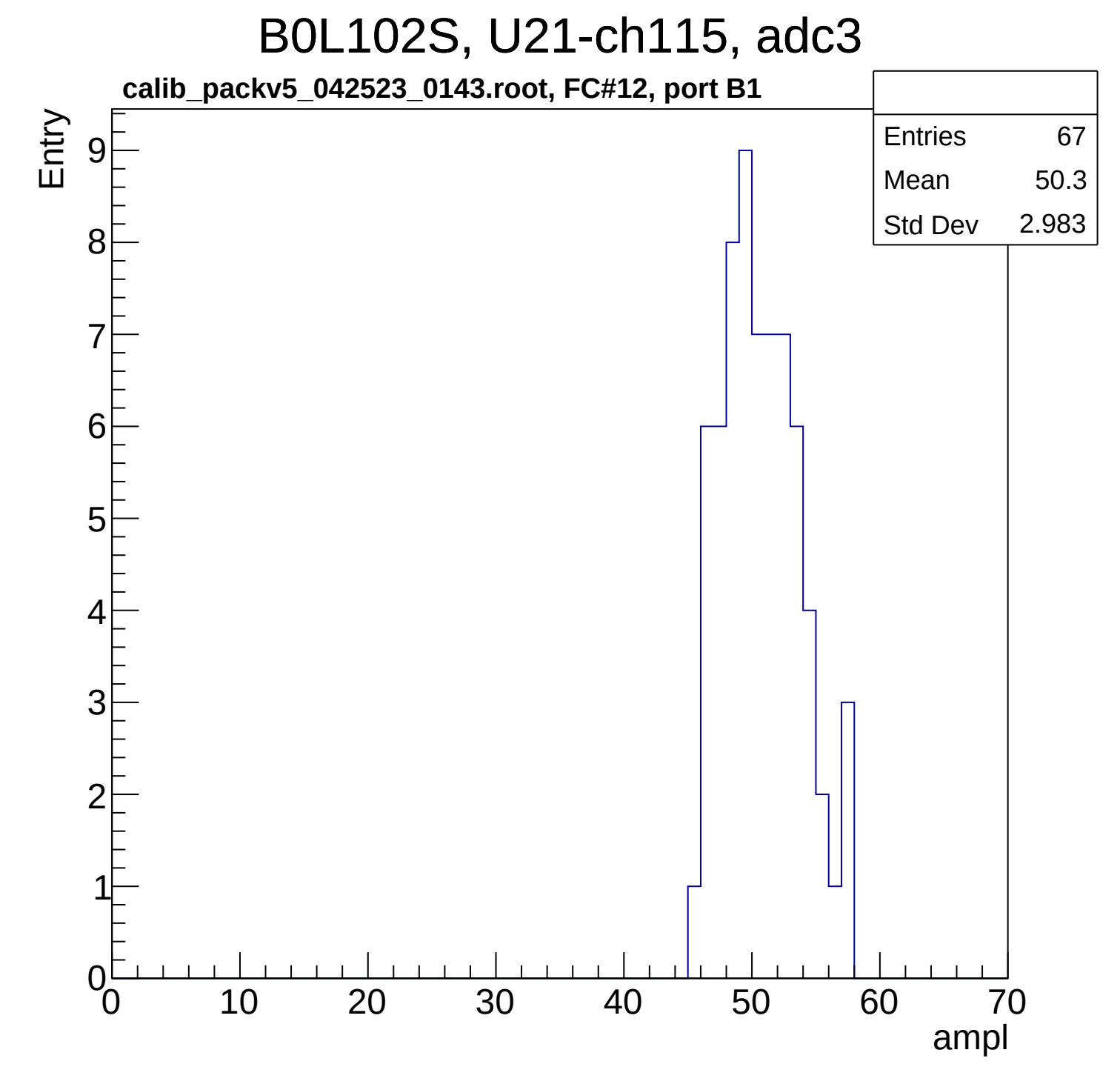

B0L102S, U21-ch114, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch114, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch115, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 28.01
Std Dev 4.967
Gaus mean : 28.6950
Gaus Width: 3.7124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch115, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 35.23
Std Dev 3.285
Gaus mean : 35.6573
Gaus Width: 3.4339
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch115, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 42.73
Std Dev 2.999
Gaus mean : 43.2583
Gaus Width: 3.0880
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch115, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.3
Std Dev 2.983
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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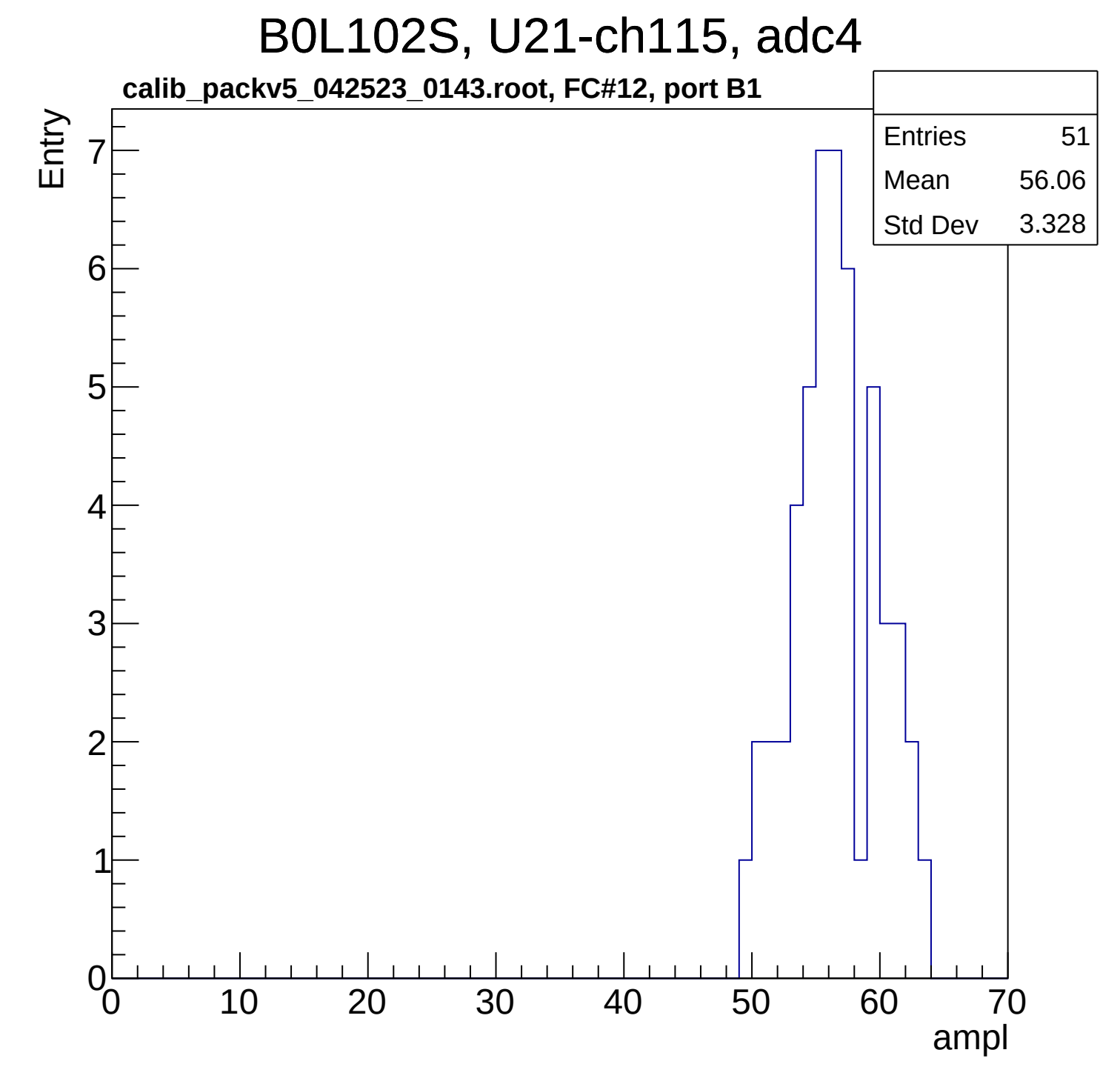
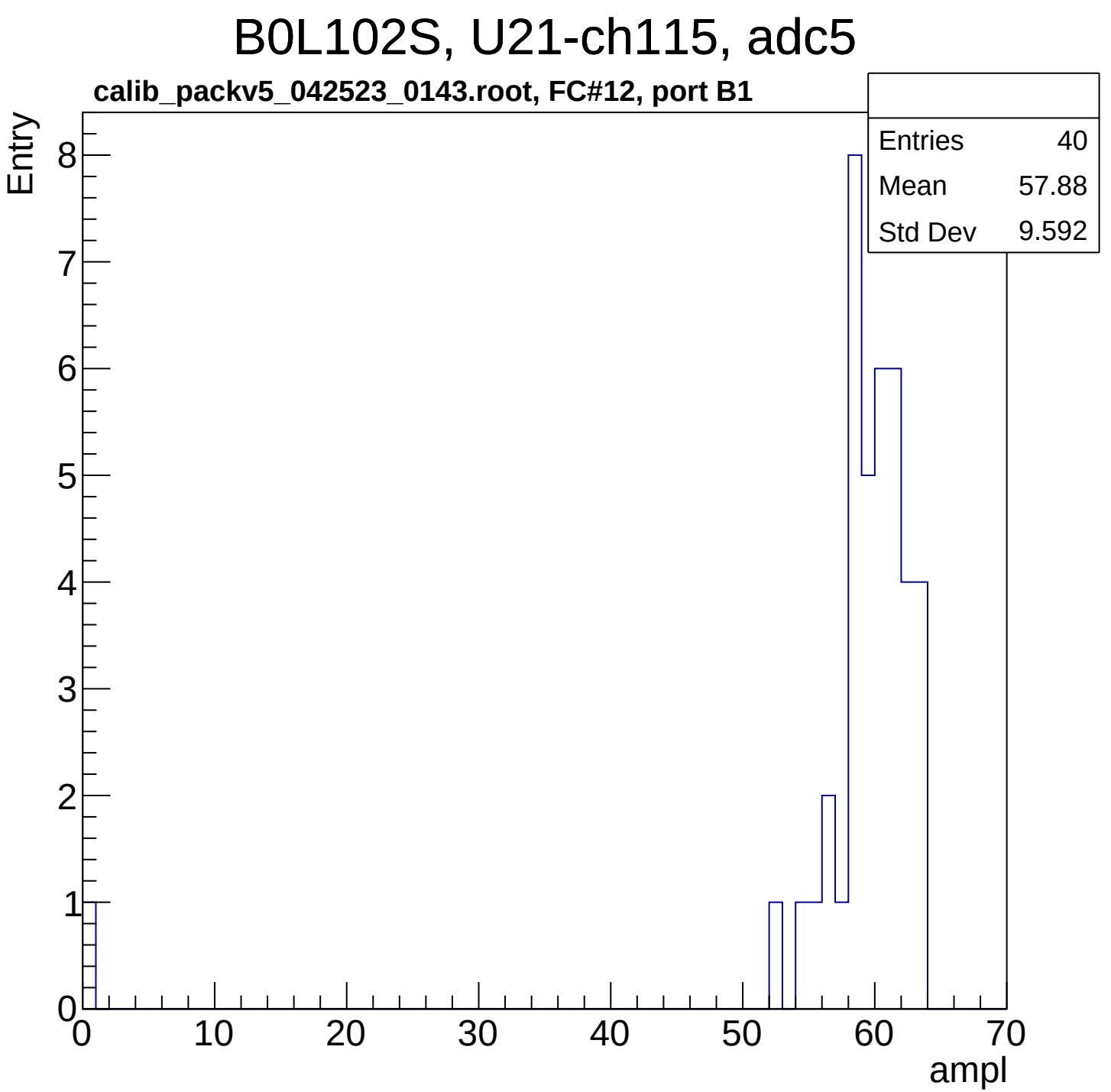
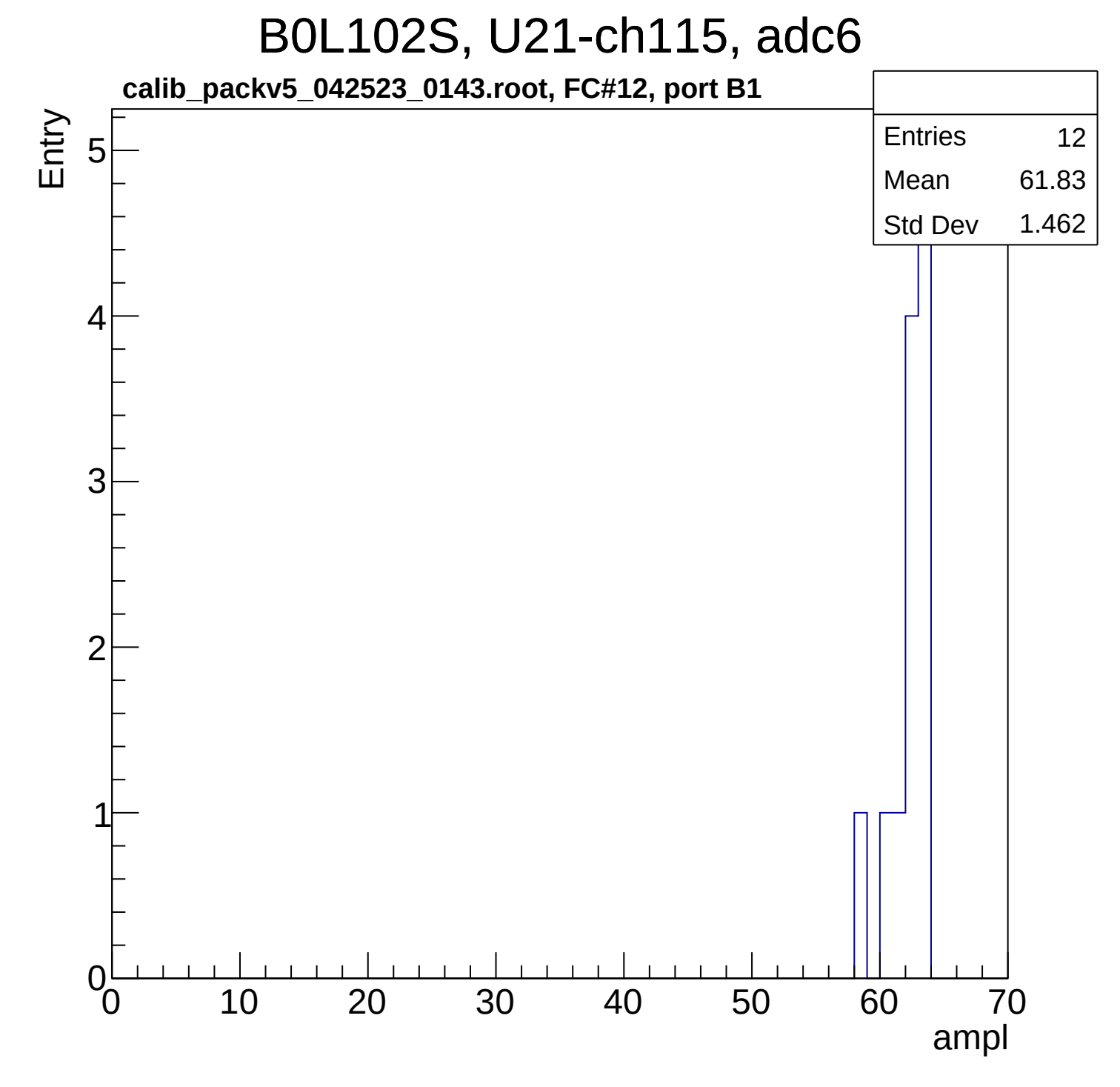
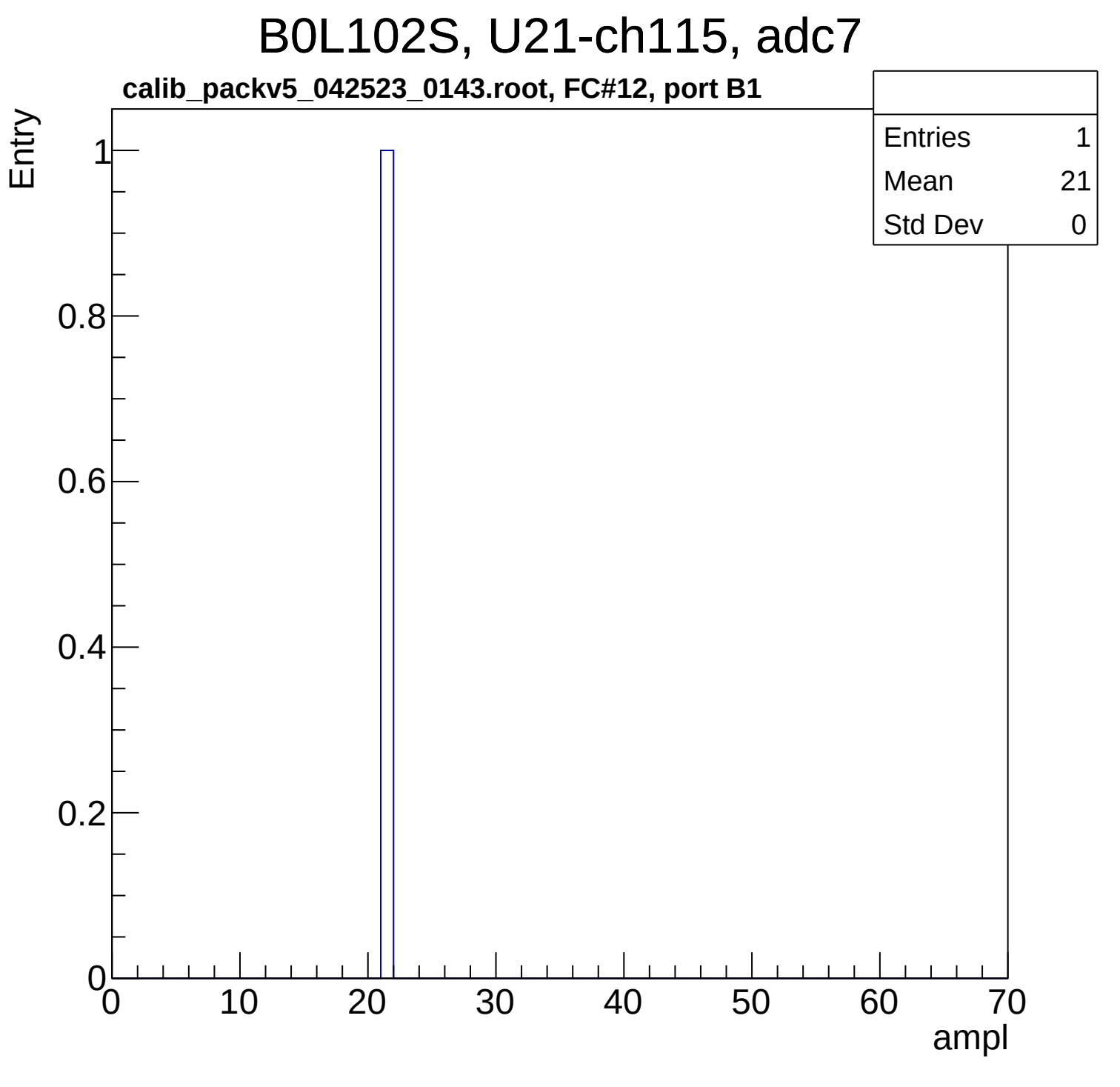

B0L102S, U21-ch115, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 56.06
Std Dev 3.328
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch115, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 57.88
Std Dev 9.592
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch115, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 61.83
Std Dev 1.462
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U21-ch115, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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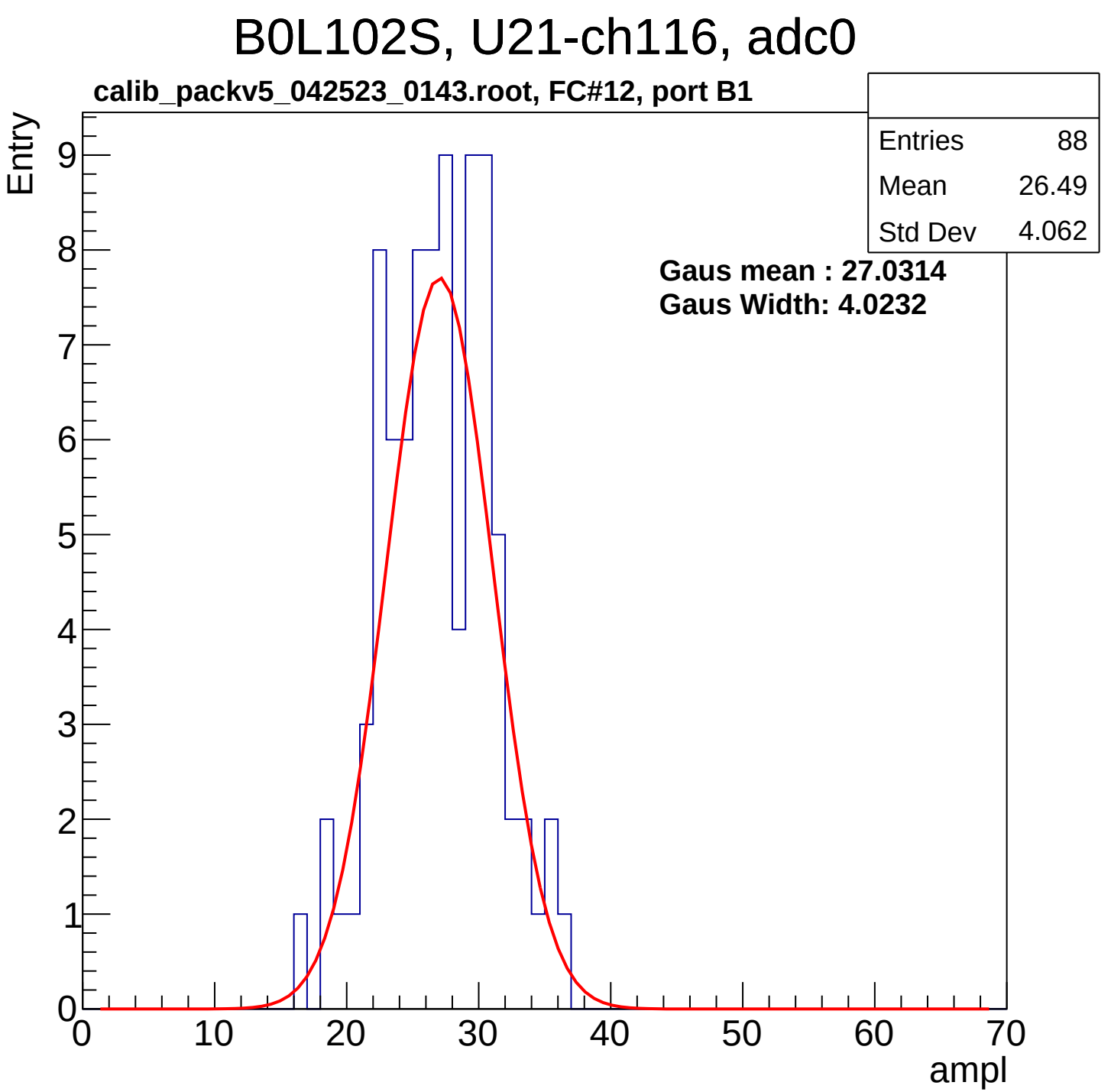
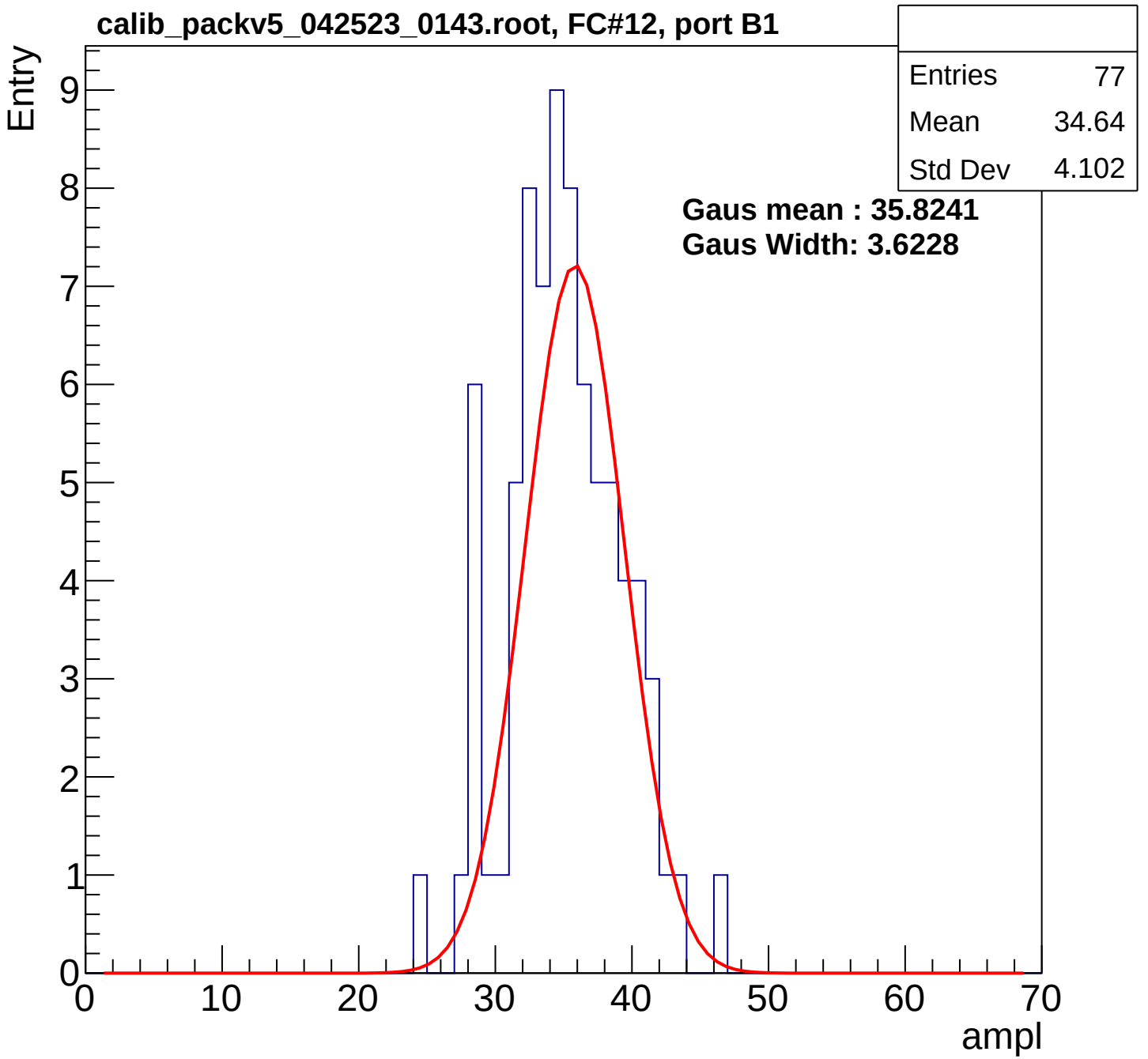
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch116, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 26.49
Std Dev 4.062
Gaus mean : 27.0314
Gaus Width: 4.0232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch116, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 34.64
Std Dev 4.102
Gaus mean : 35.8241
Gaus Width: 3.6228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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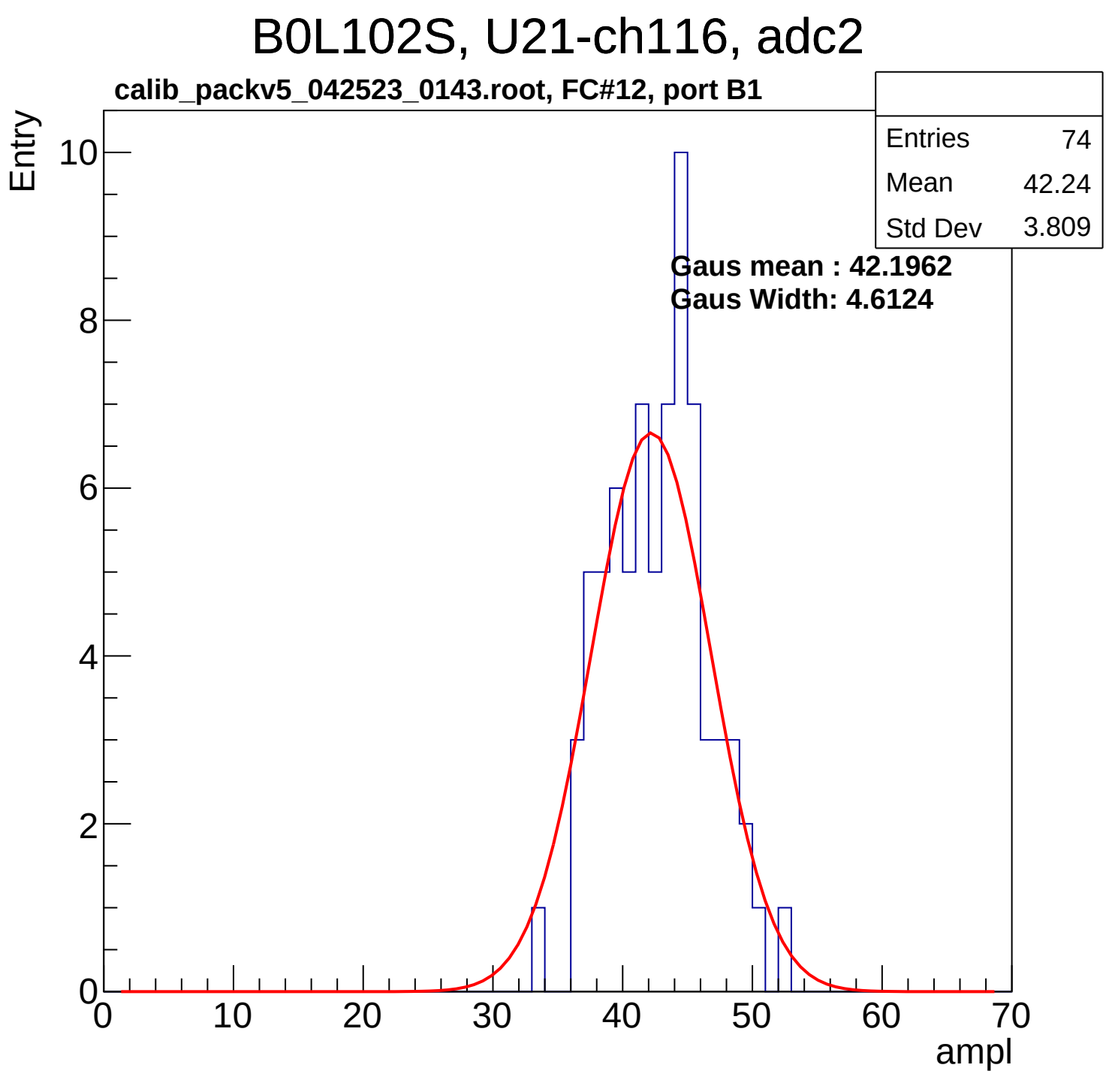
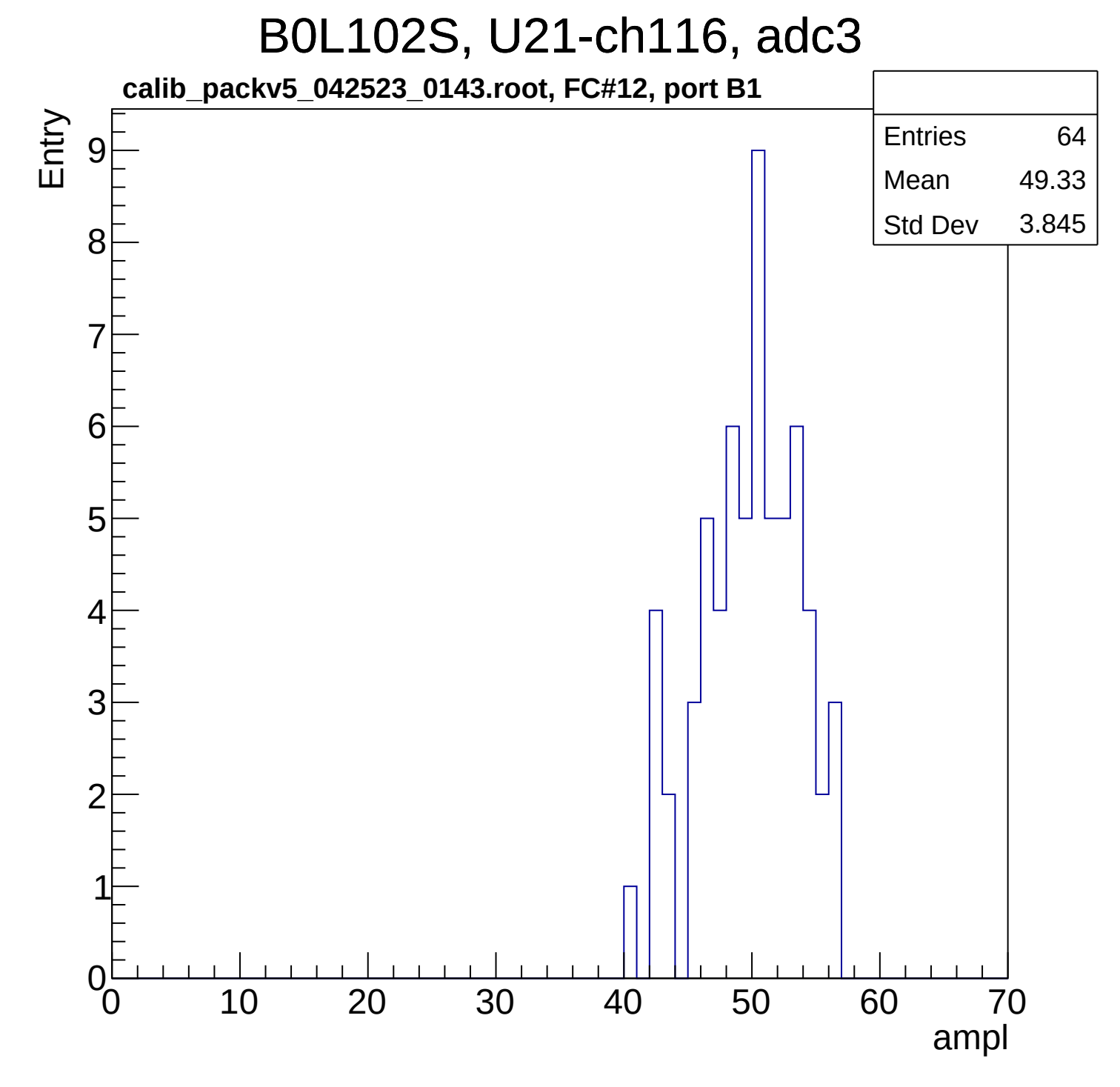

B0L102S, U21-ch116, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 42.24
Std Dev 3.809
Gaus mean : 42.1962
Gaus Width: 4.6124
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch116, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.33
Std Dev 3.845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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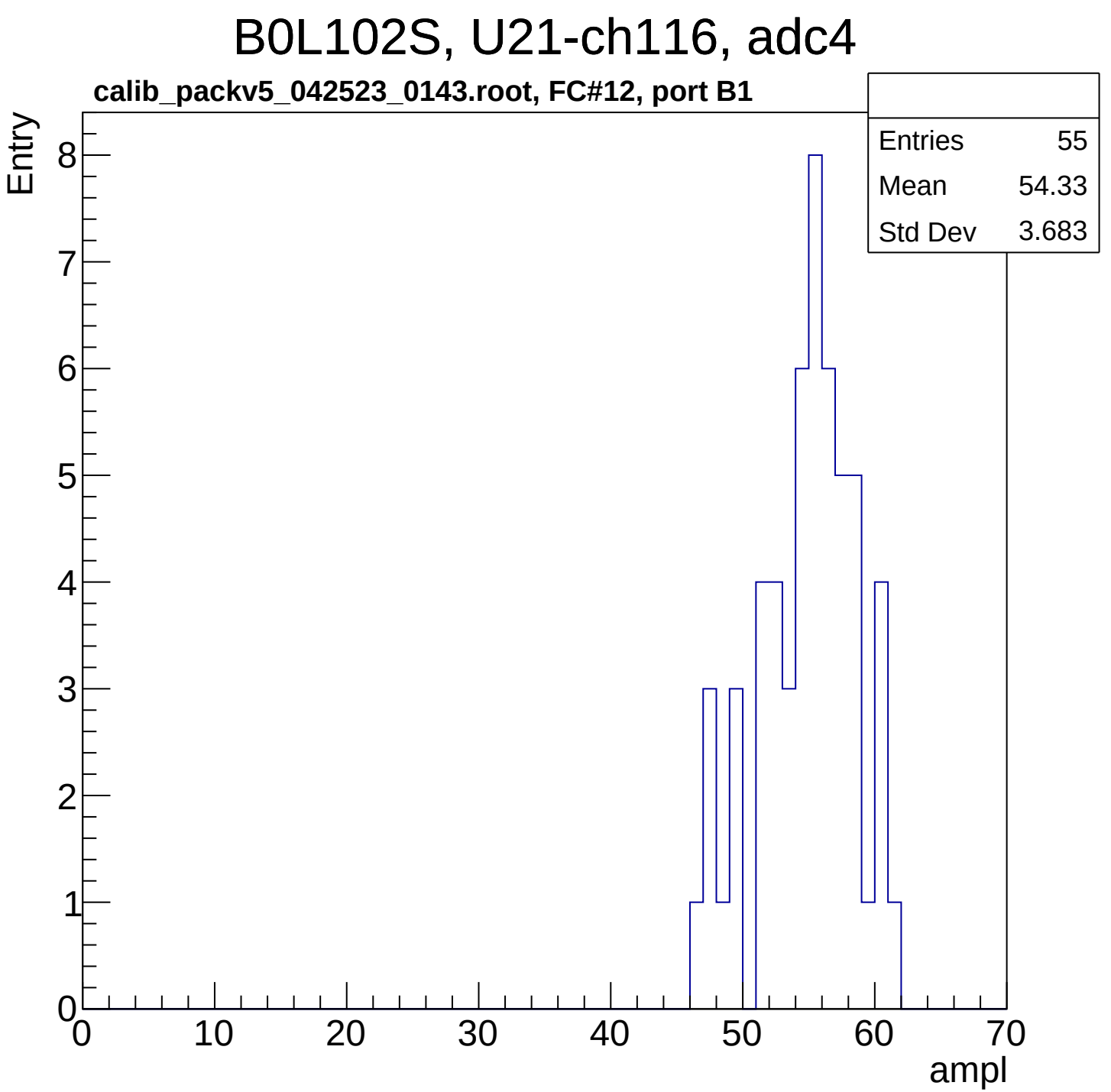
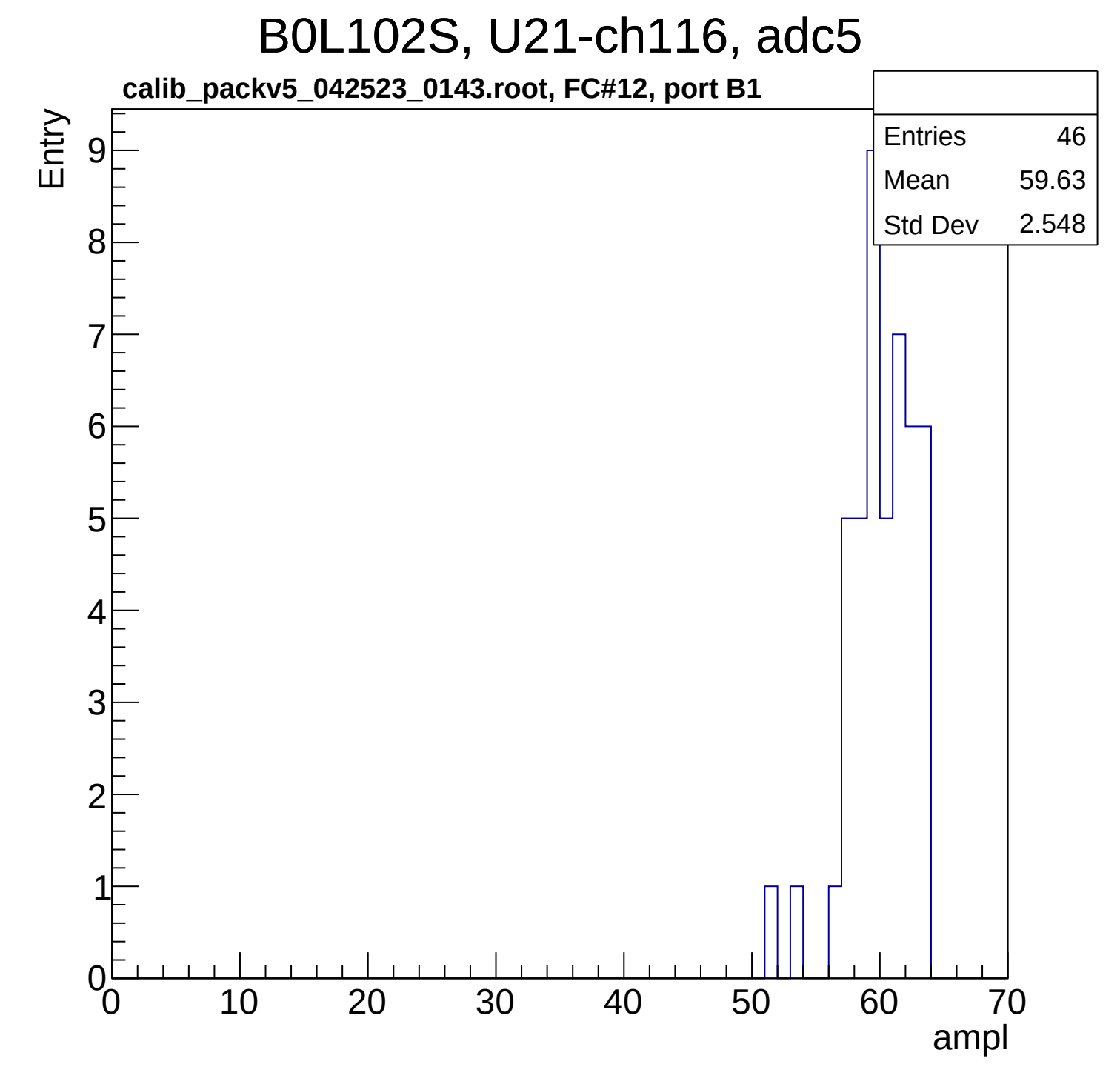
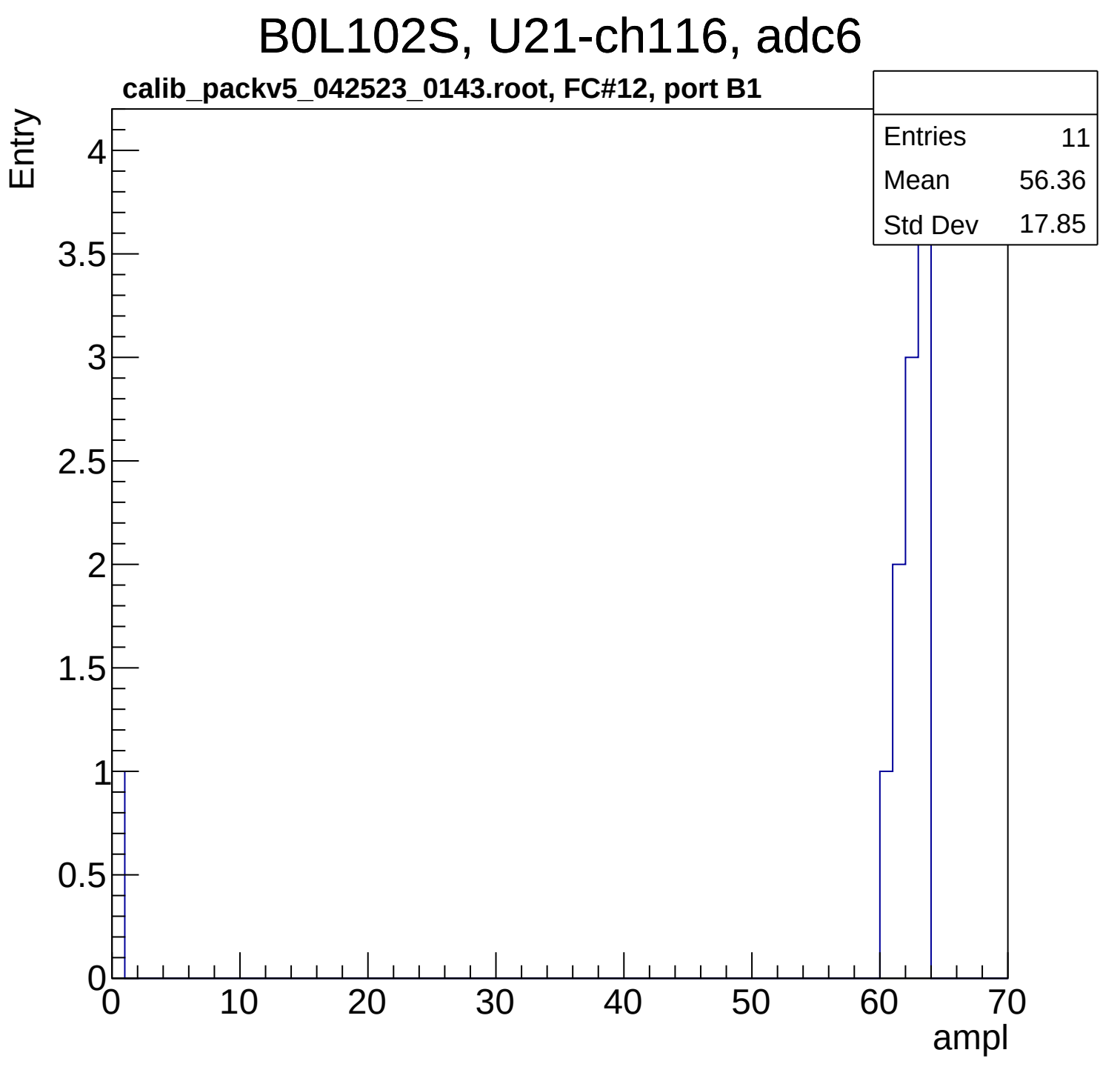
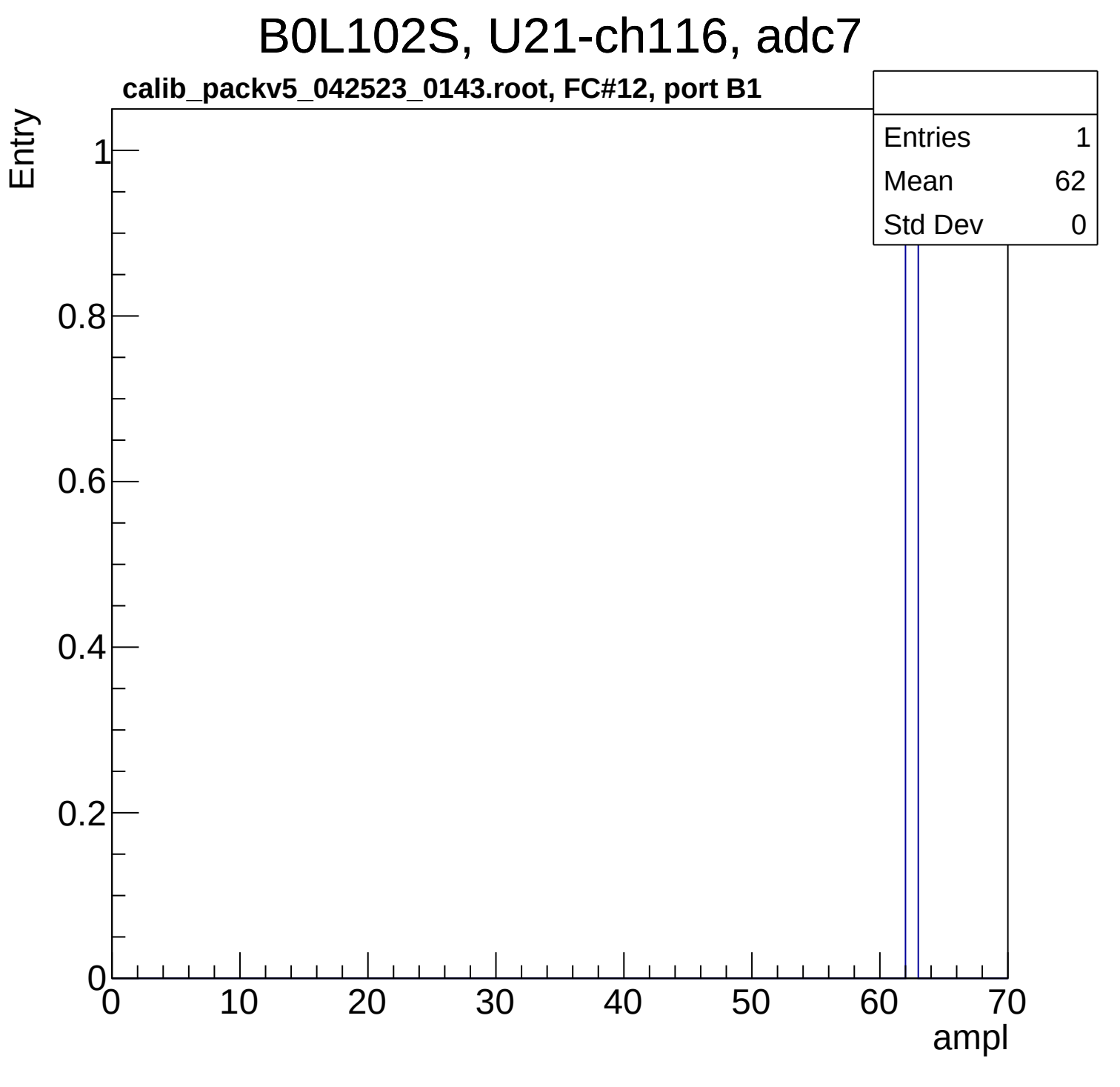
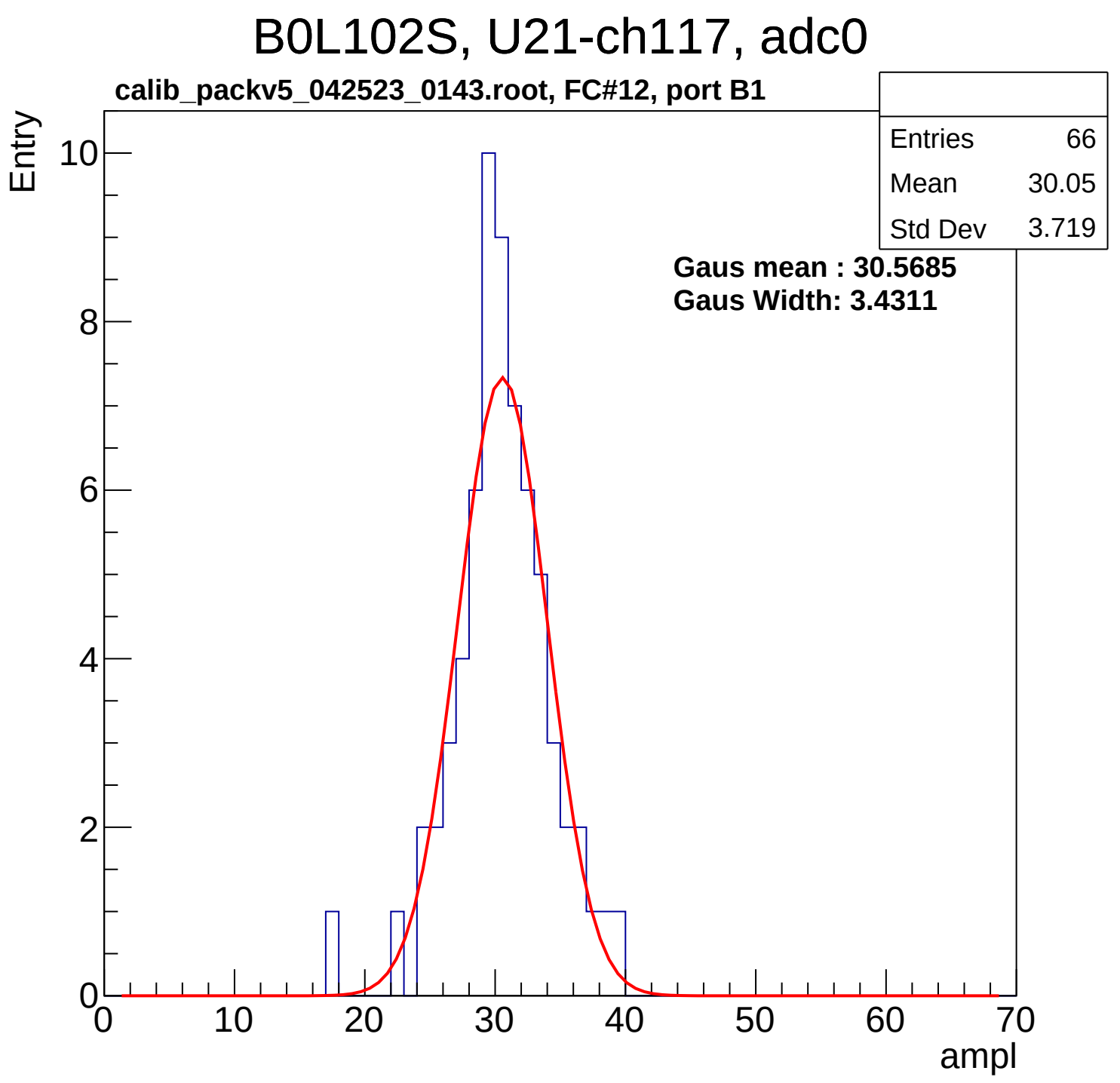
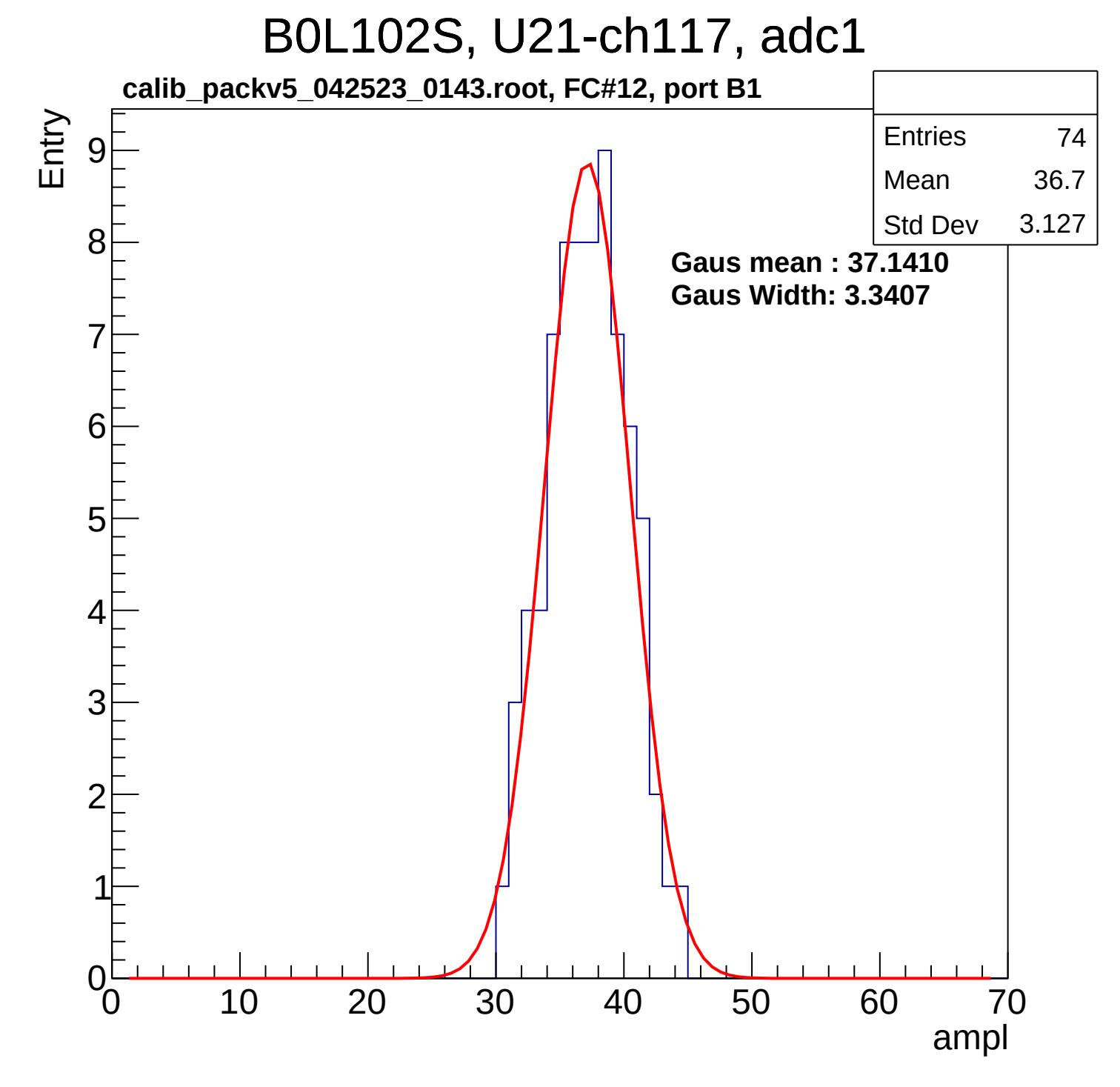

B0L102S, U21-ch116, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 54.33
Std Dev 3.683
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

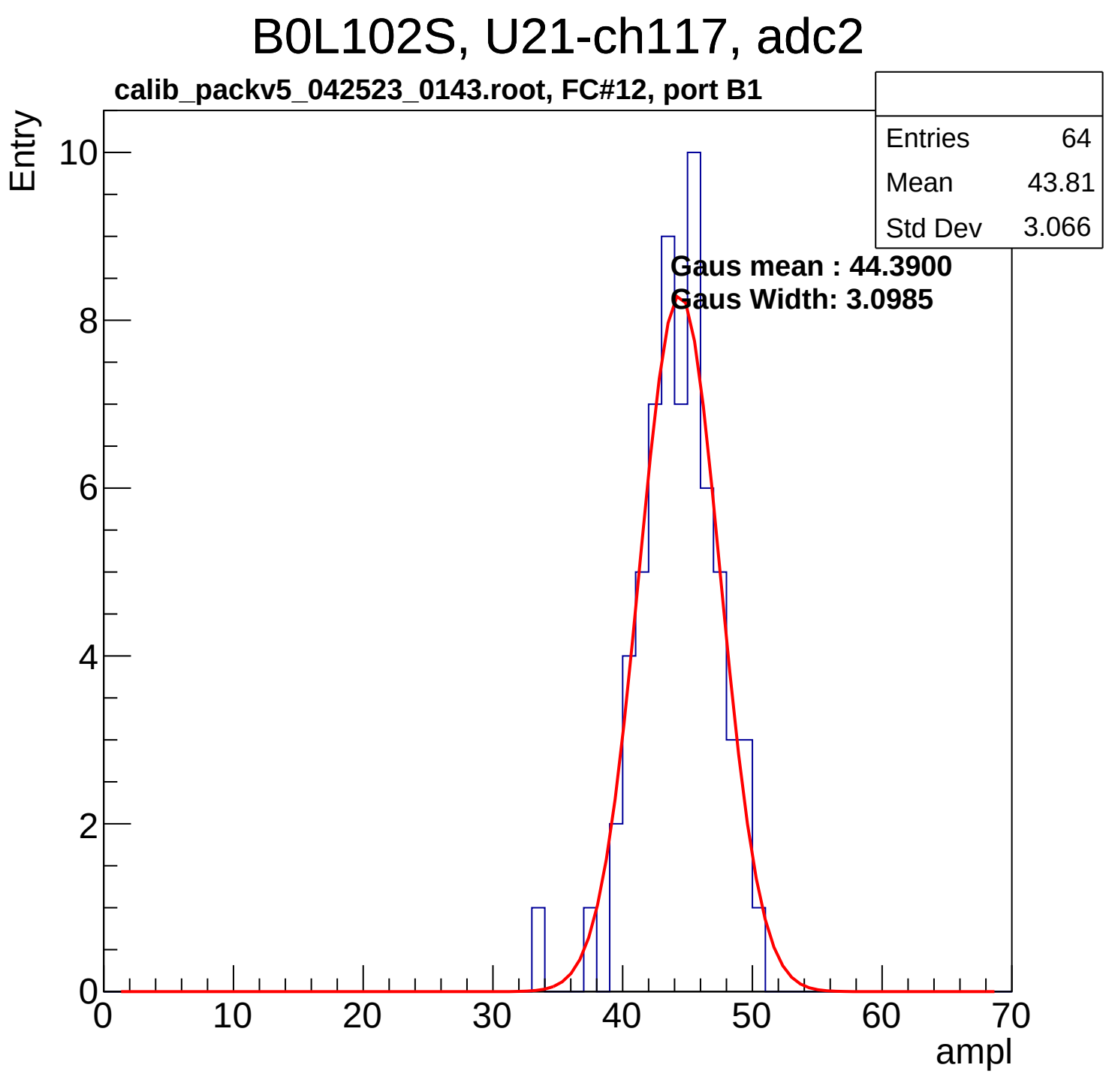

B0L102S, U21-ch116, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.63
Std Dev 2.548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

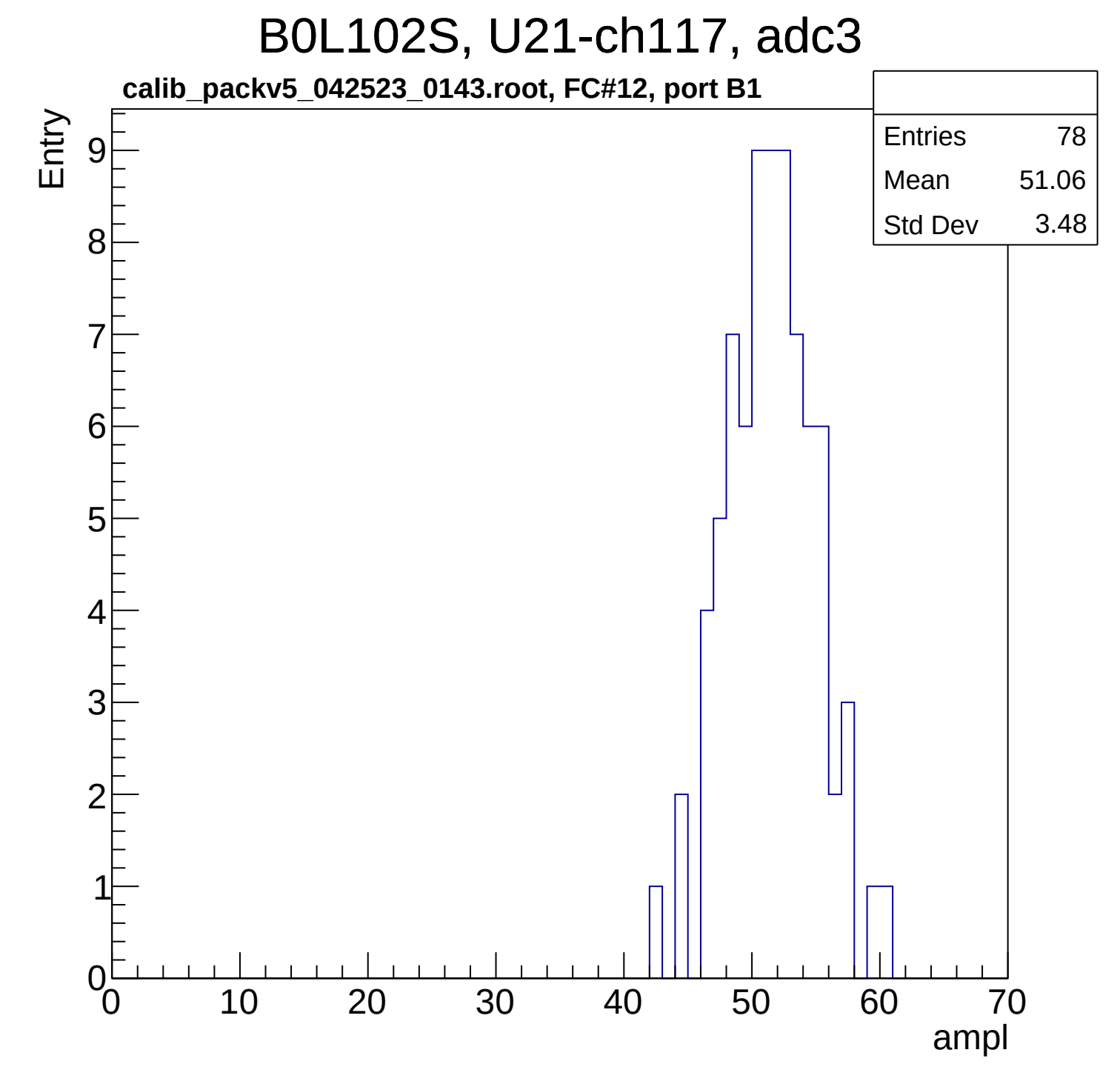

B0L102S, U21-ch116, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 56.36
Std Dev 17.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

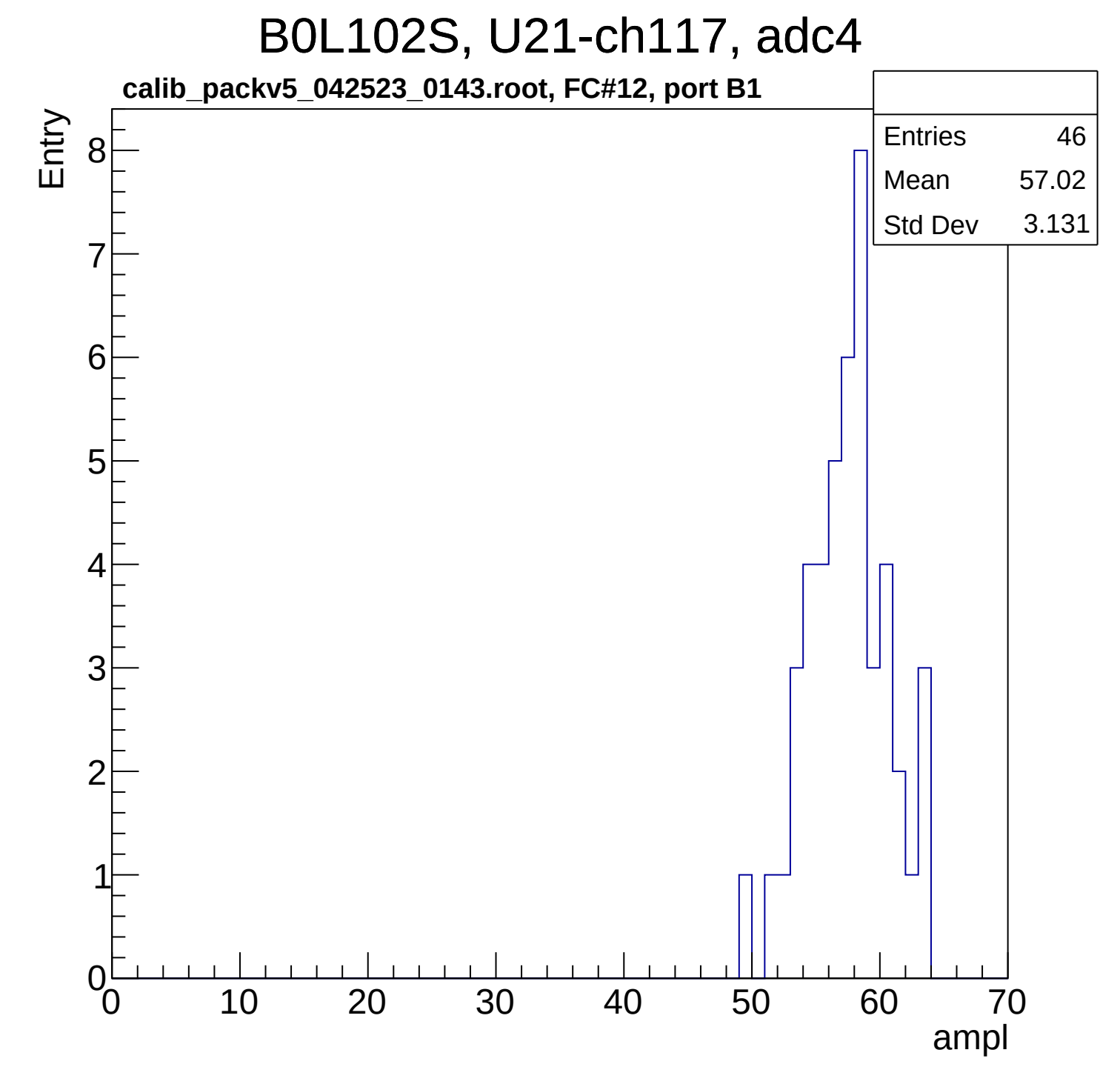

B0L102S, U21-ch116, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch117, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 30.05
Std Dev 3.719
Gaus mean : 30.5685
Gaus Width: 3.4311
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch117, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.7
Std Dev 3.127
Gaus mean : 37.1410
Gaus Width: 3.3407
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch117, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.81
Std Dev 3.066
Gaus mean : 44.3900
Gaus Width: 3.0985
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch117, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 51.06
Std Dev 3.48
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch117, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 57.02
Std Dev 3.131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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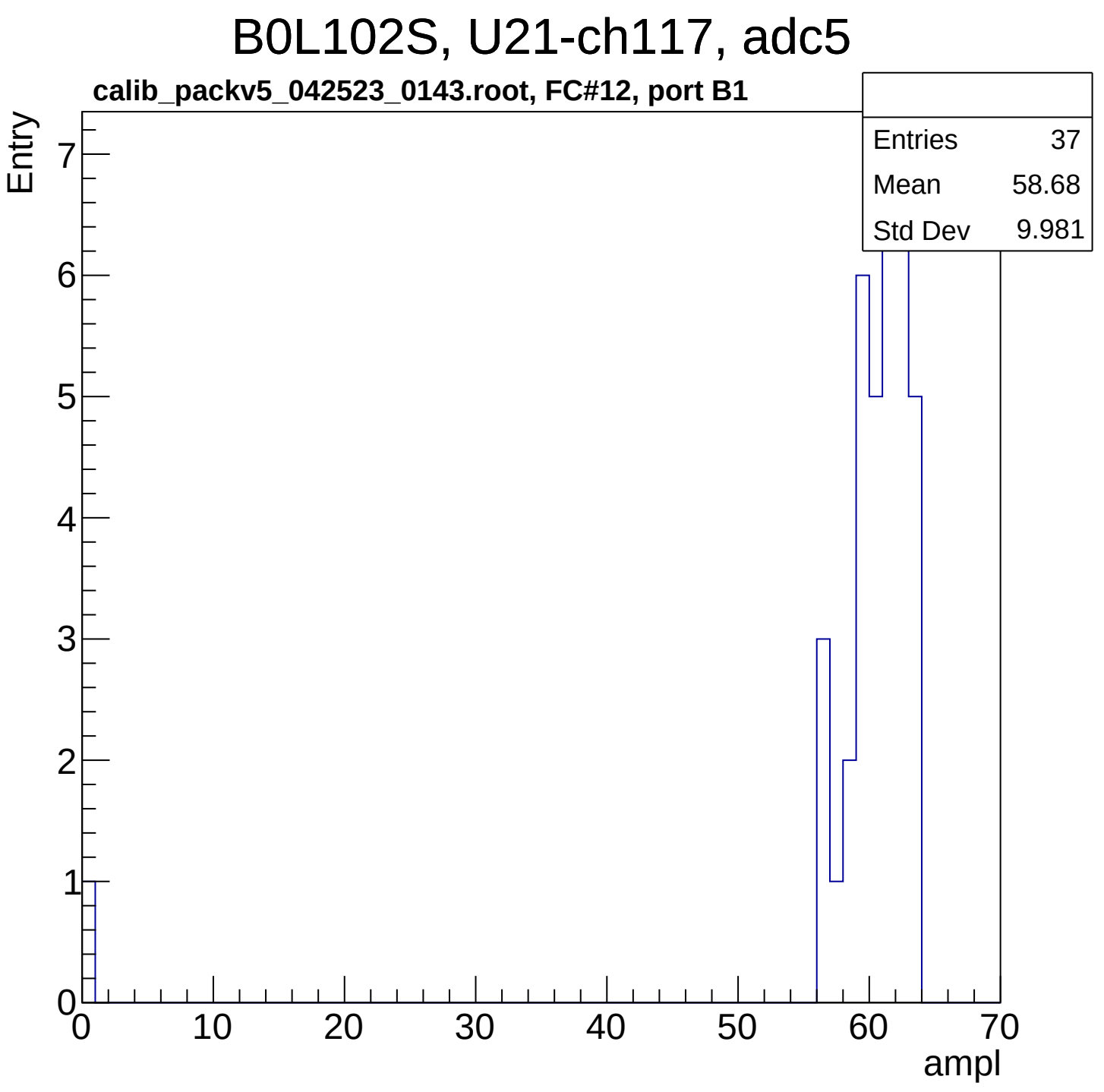
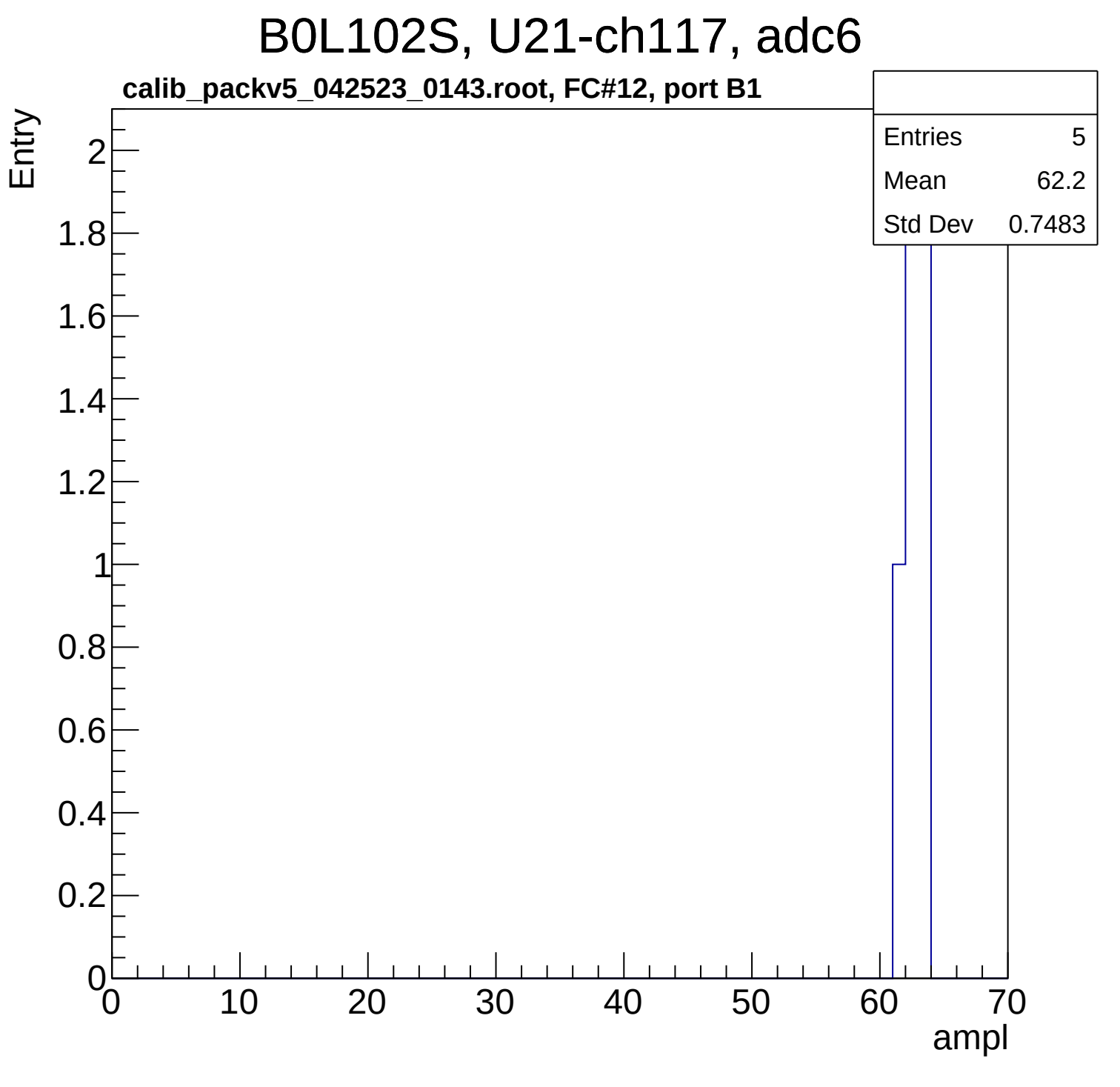
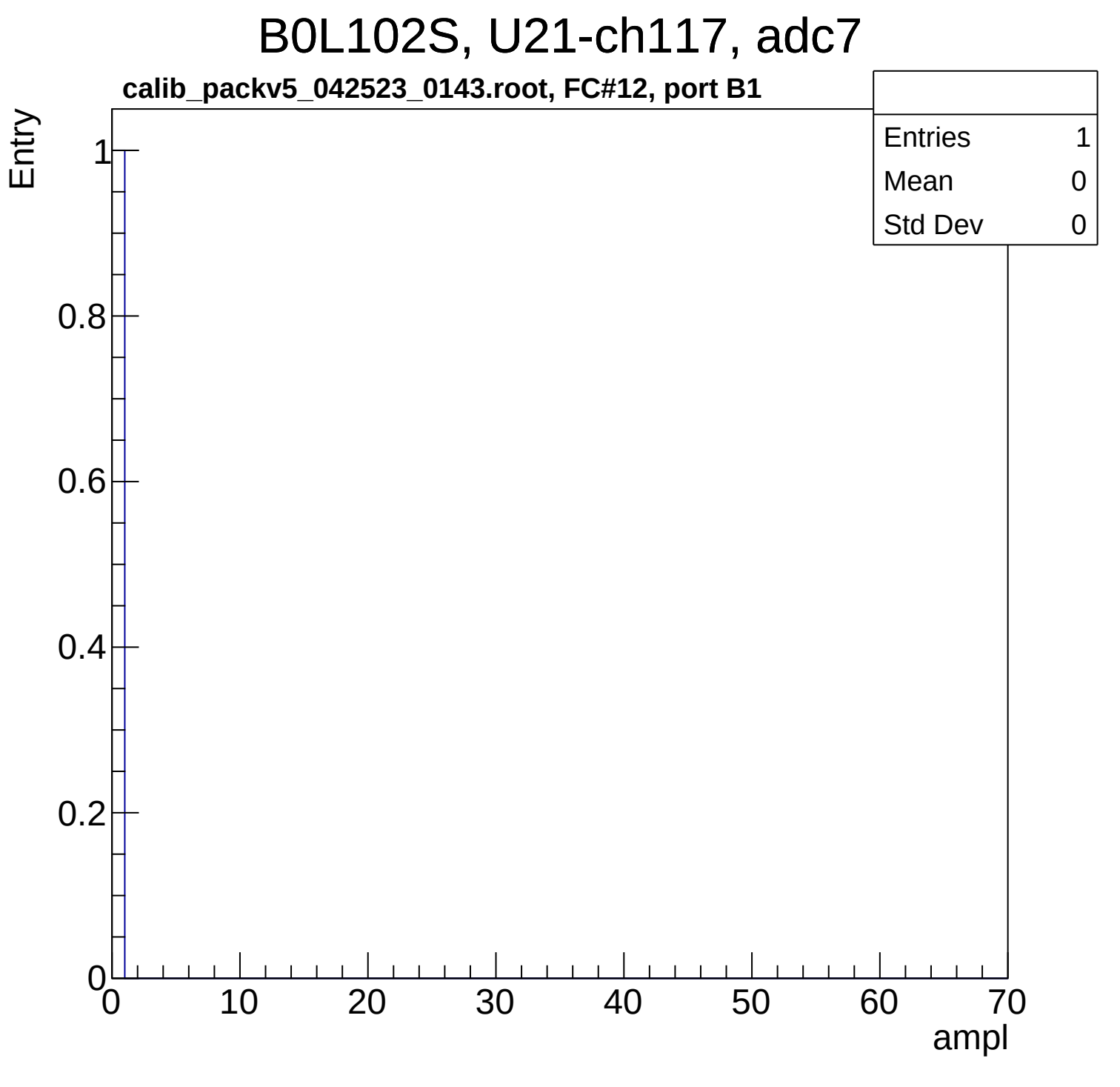
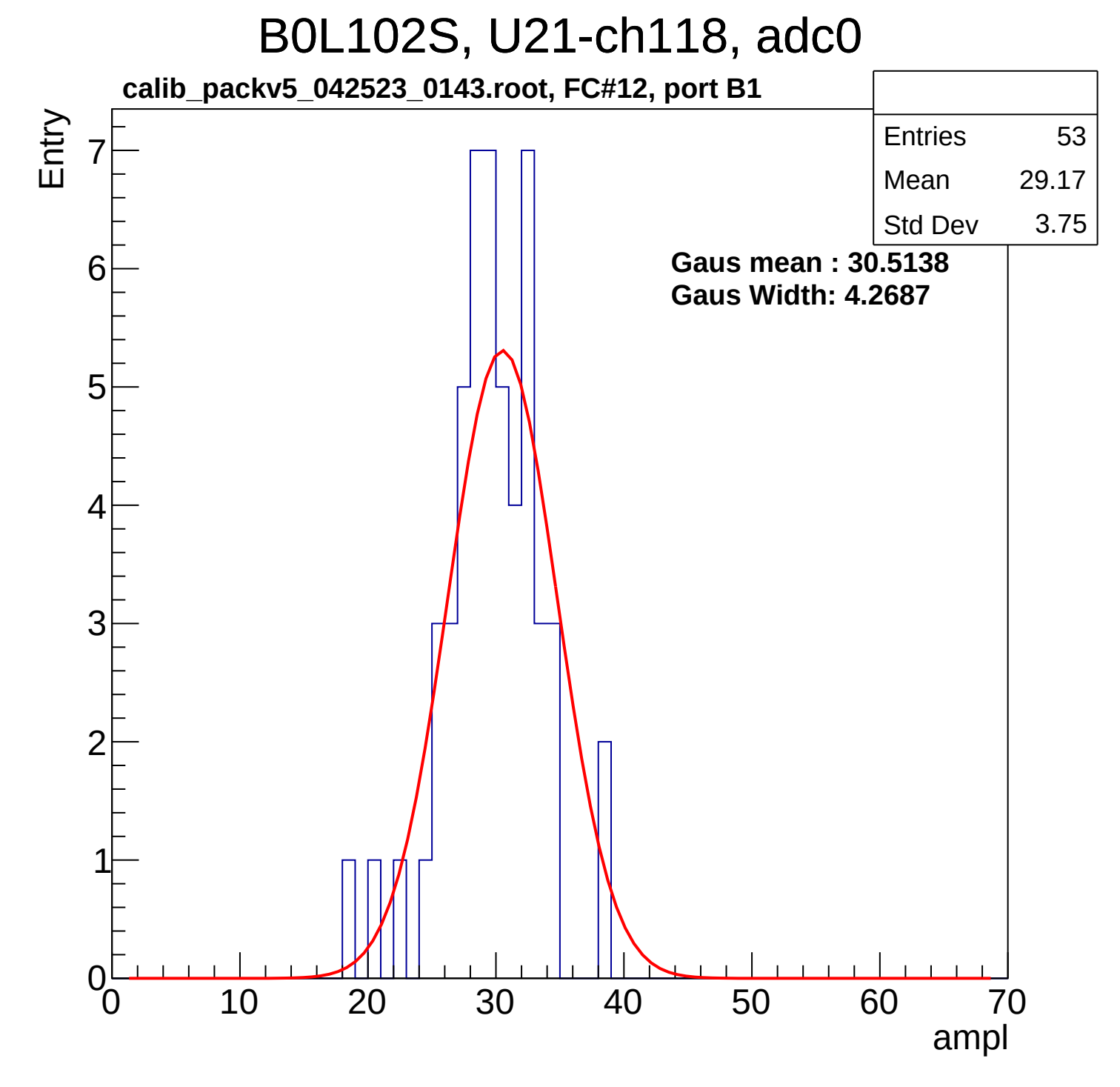
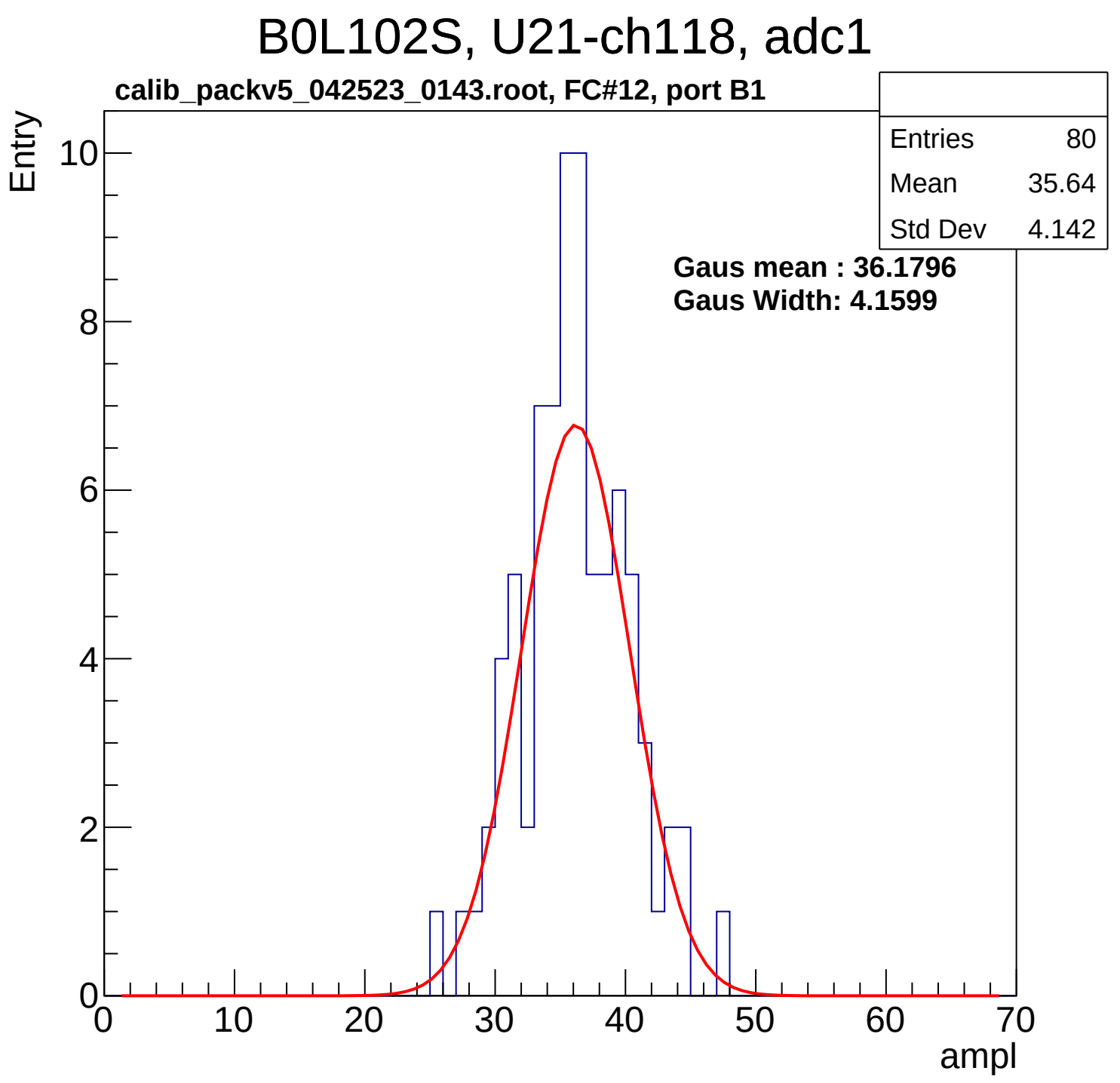
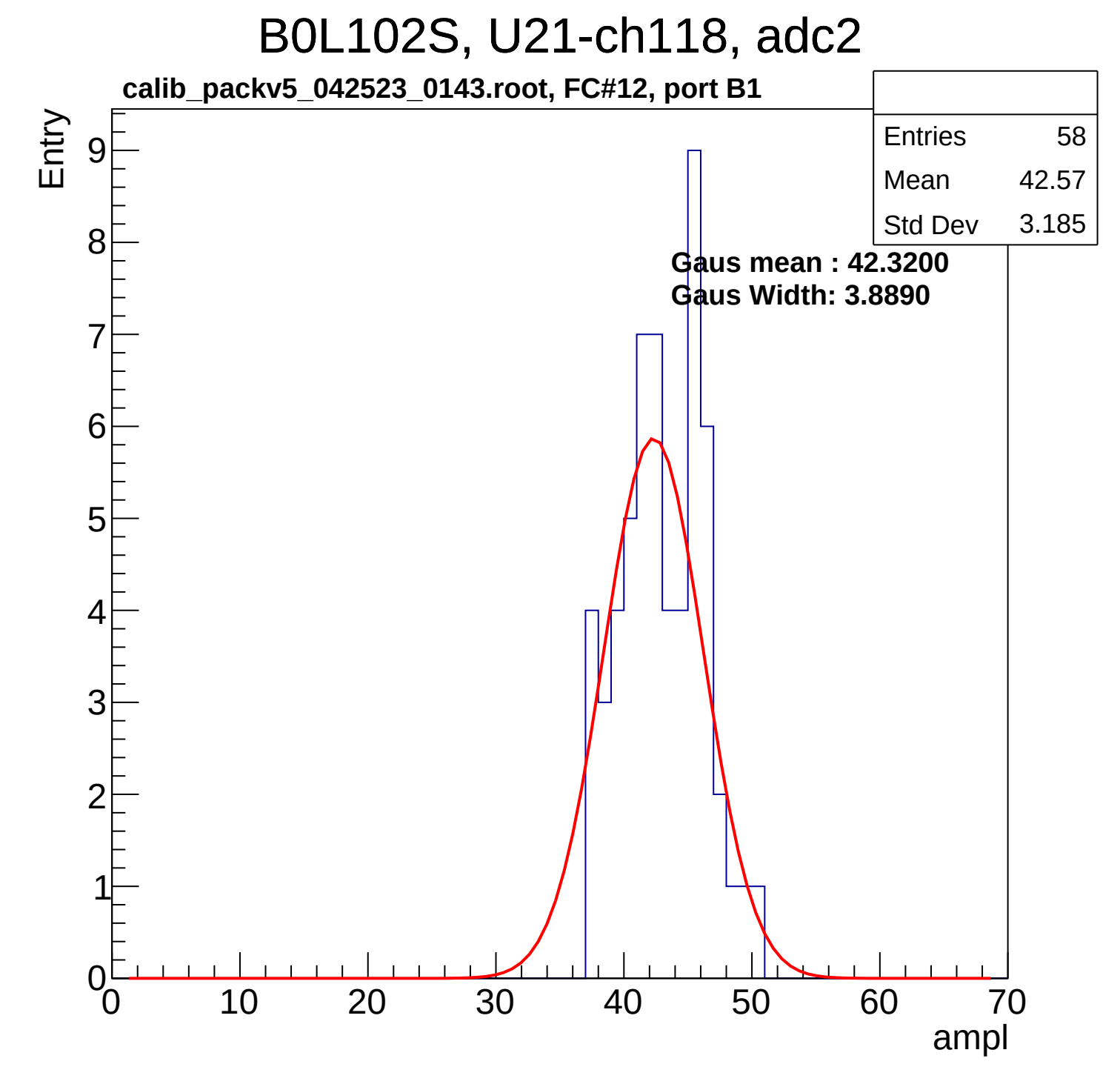

B0L102S, U21-ch117, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 58.68
Std Dev 9.981
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

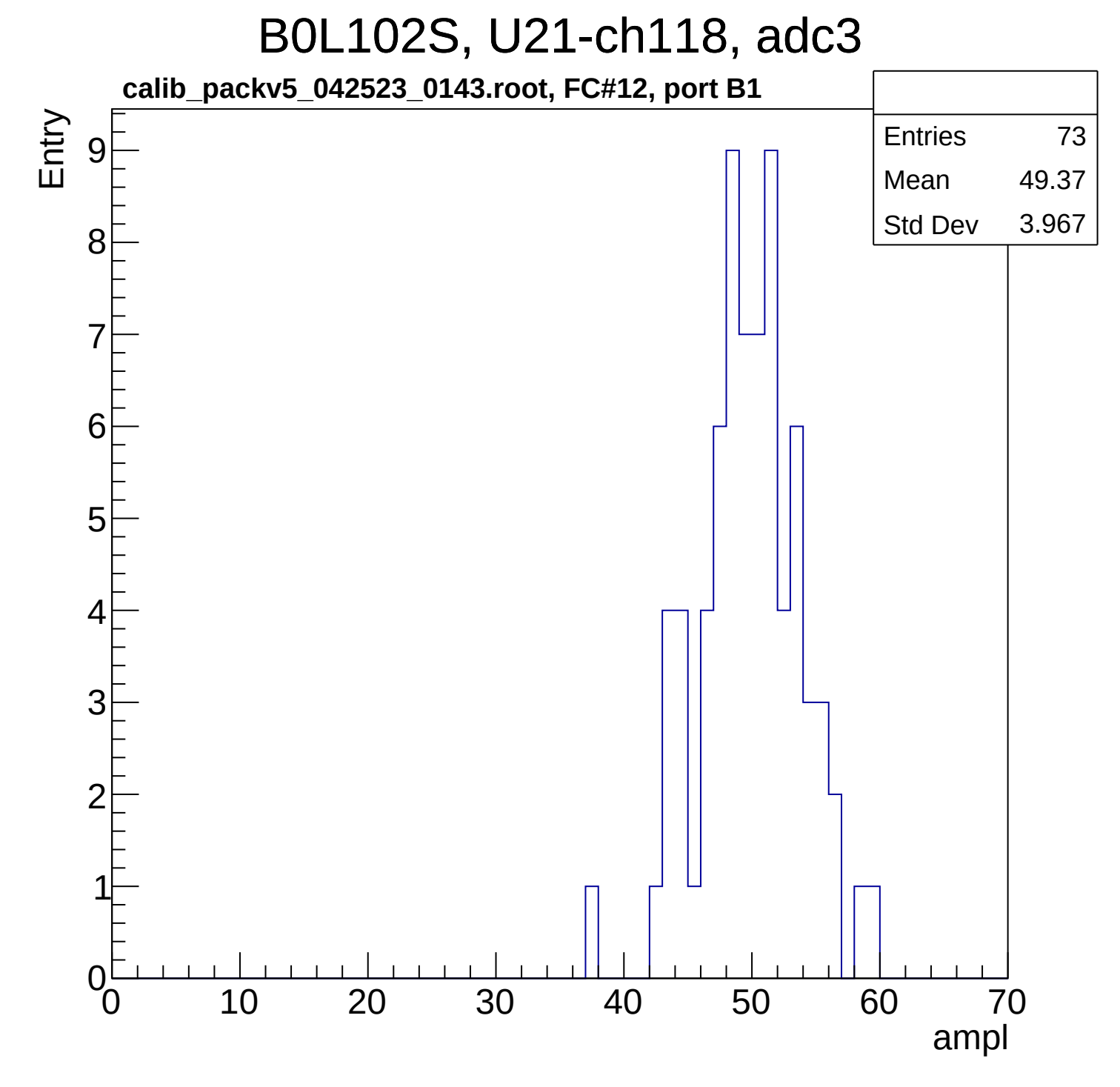

B0L102S, U21-ch117, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch117, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch118, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 29.17
Std Dev 3.75
Gaus mean : 30.5138
Gaus Width: 4.2687
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch118, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 35.64
Std Dev 4.142
Gaus mean : 36.1796
Gaus Width: 4.1599
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch118, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 42.57
Std Dev 3.185
Gaus mean : 42.3200
Gaus Width: 3.8890
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch118, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 49.37
Std Dev 3.967
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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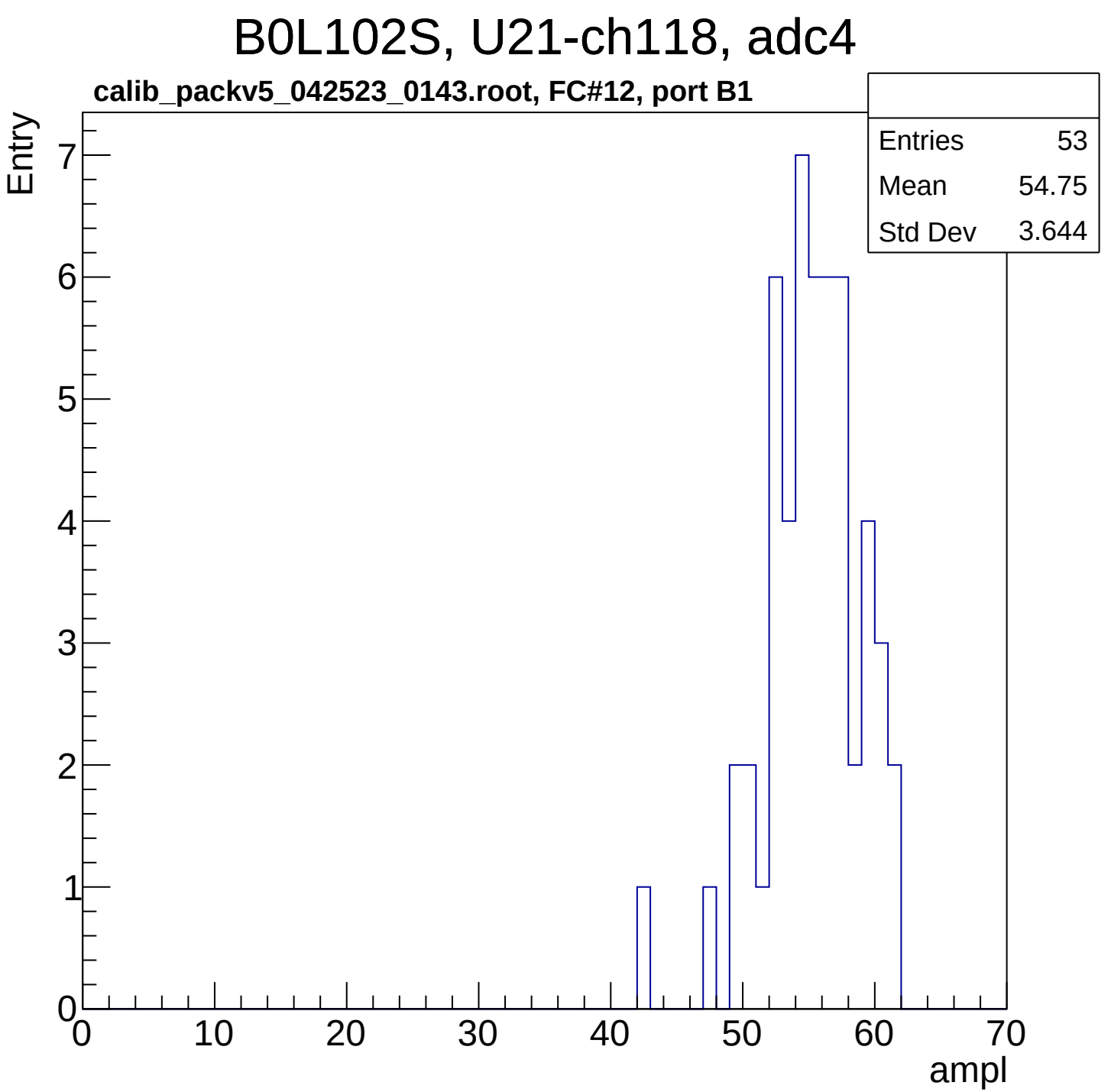
20
30
40
50
60
70


B0L102S, U21-ch118, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 54.75
Std Dev 3.644
Entry
ampl
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2
3
4
5
6
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0
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40
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60
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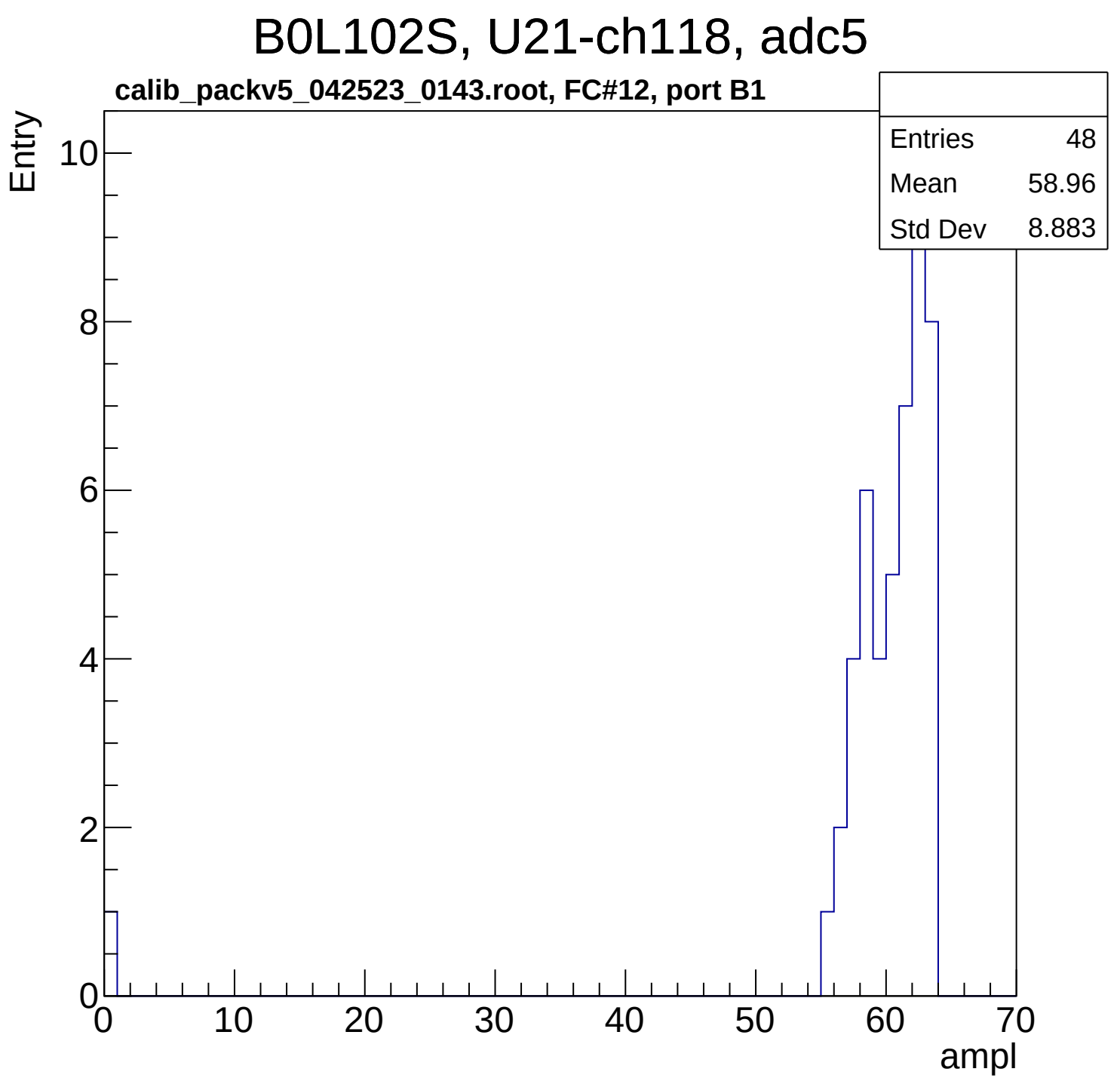
B0L102S, U21-ch118, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.96
Std Dev 8.883
Entry
ampl
0
2
4
6
8
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0
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20
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40
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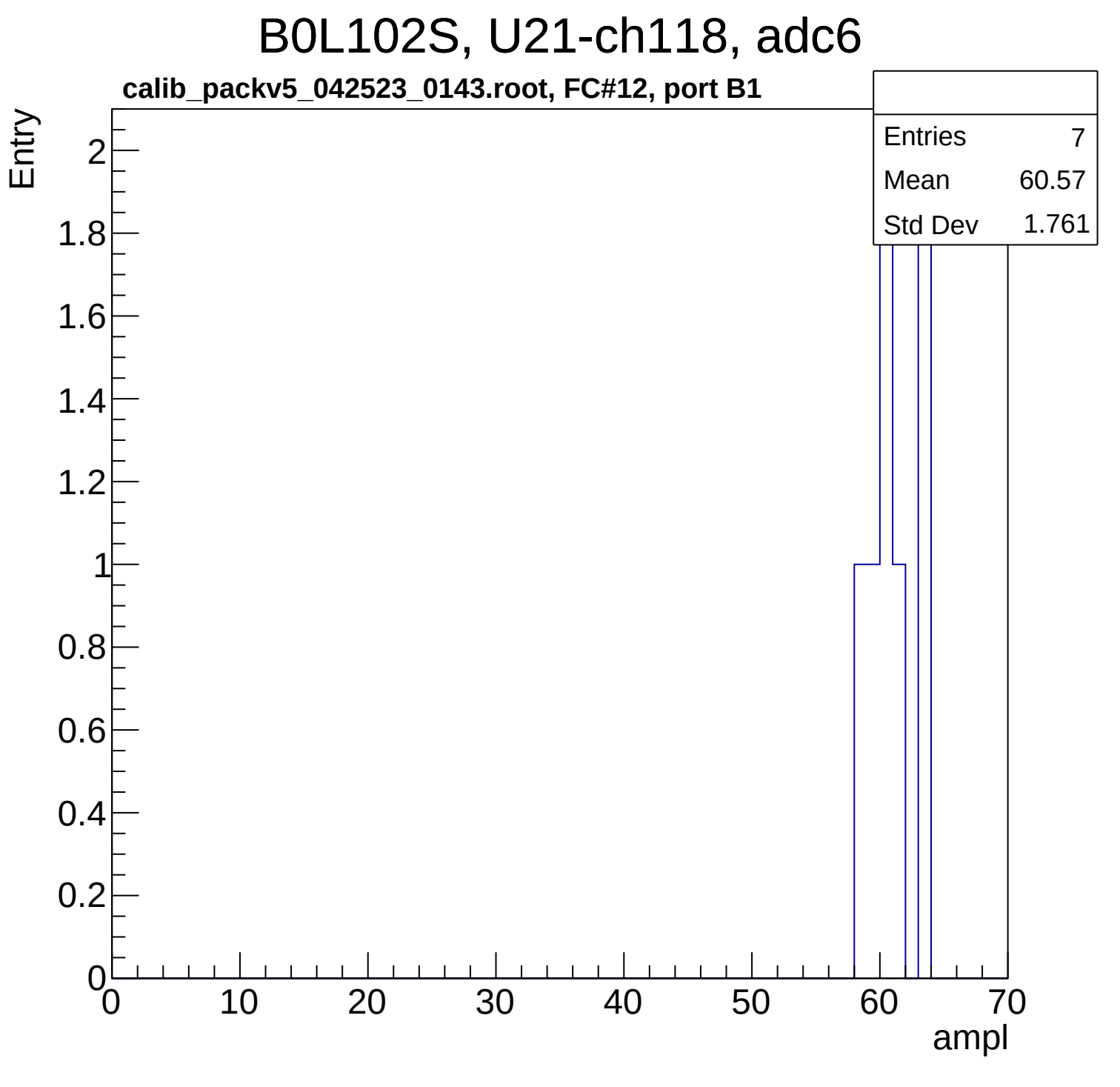
60
70


B0L102S, U21-ch118, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 60.57
Std Dev 1.761
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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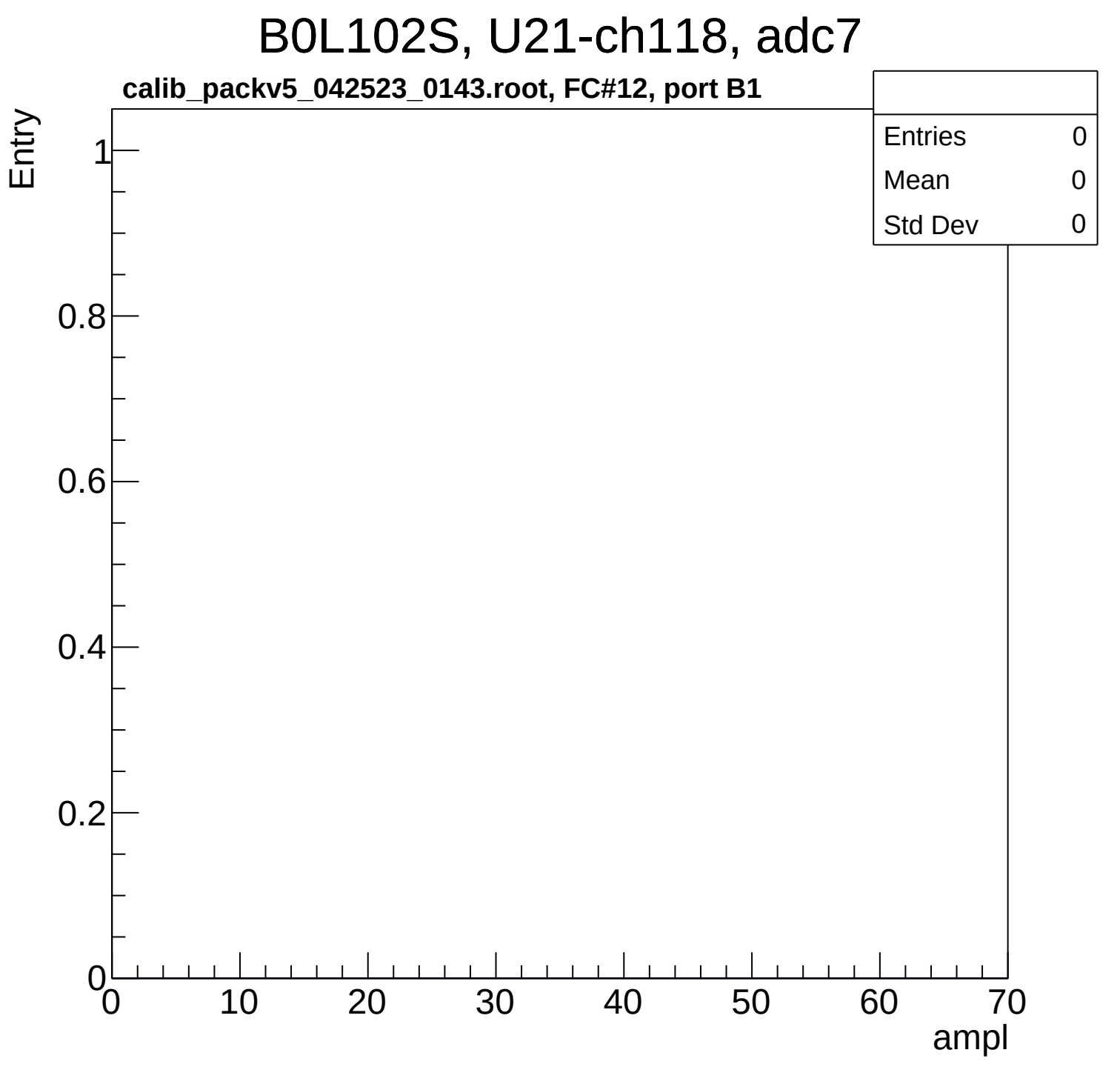
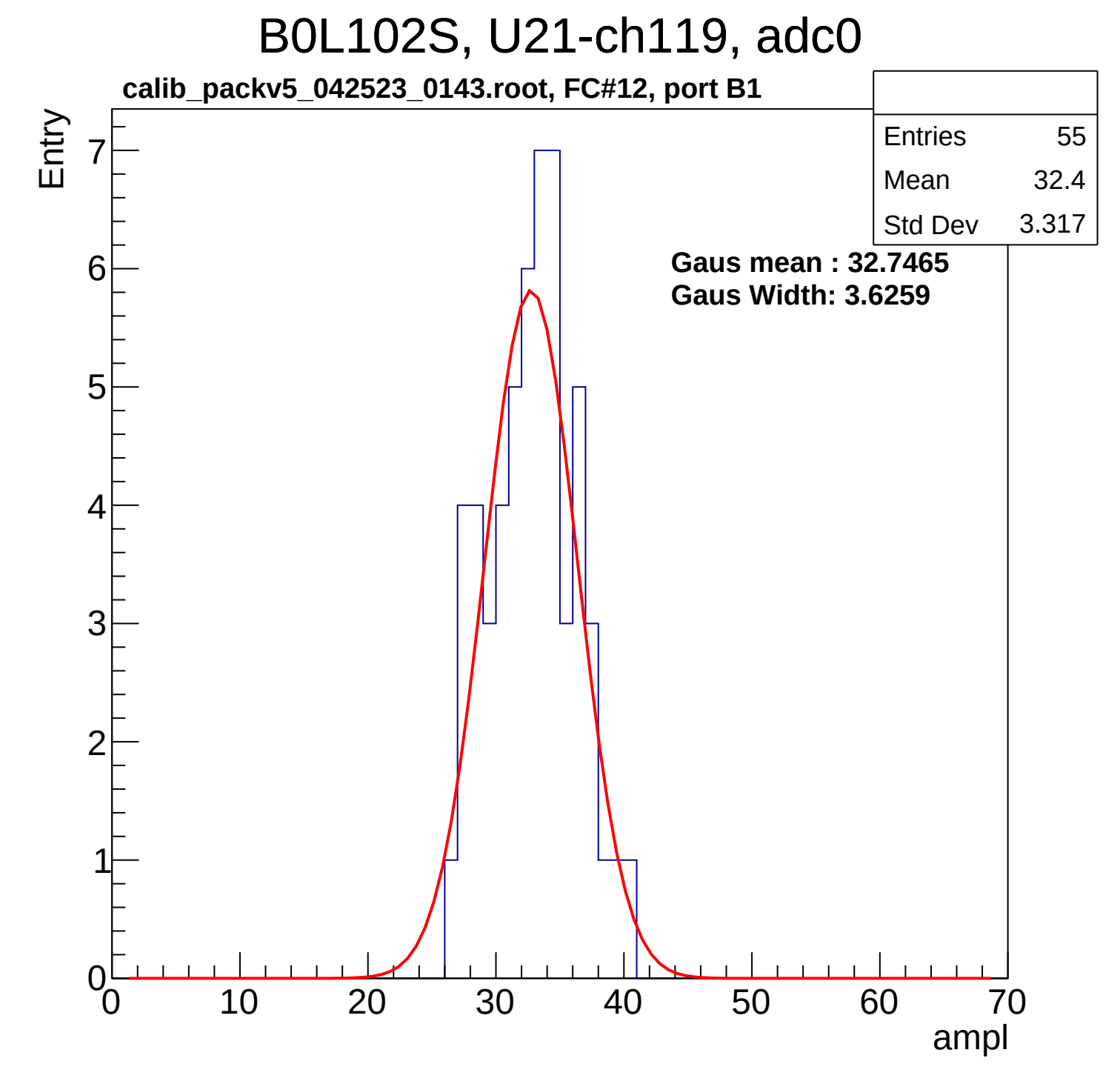
20
30
40
50
60
70


B0L102S, U21-ch118, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch119, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 32.4
Std Dev 3.317
Gaus mean : 32.7465
Gaus Width: 3.6259
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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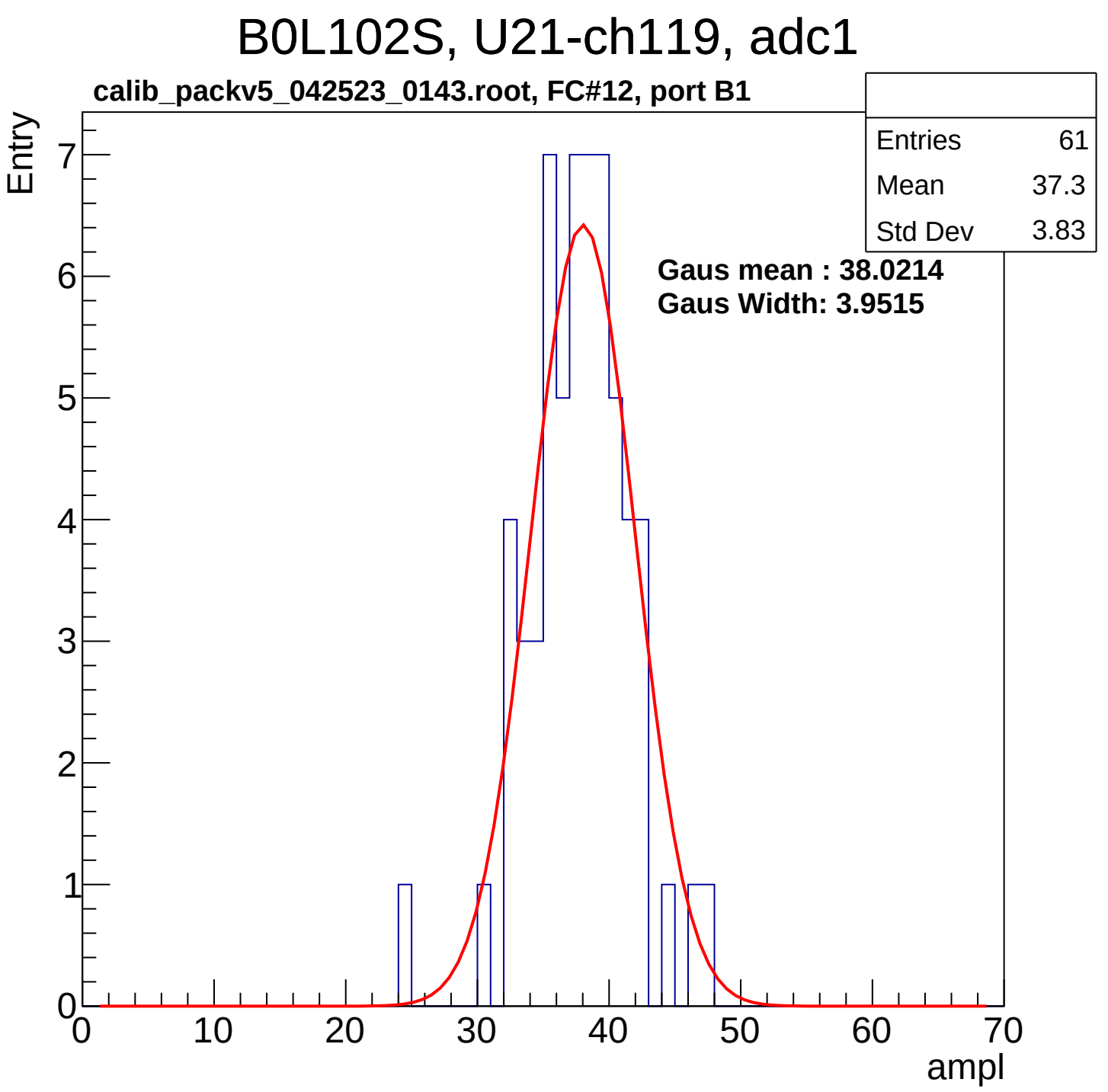
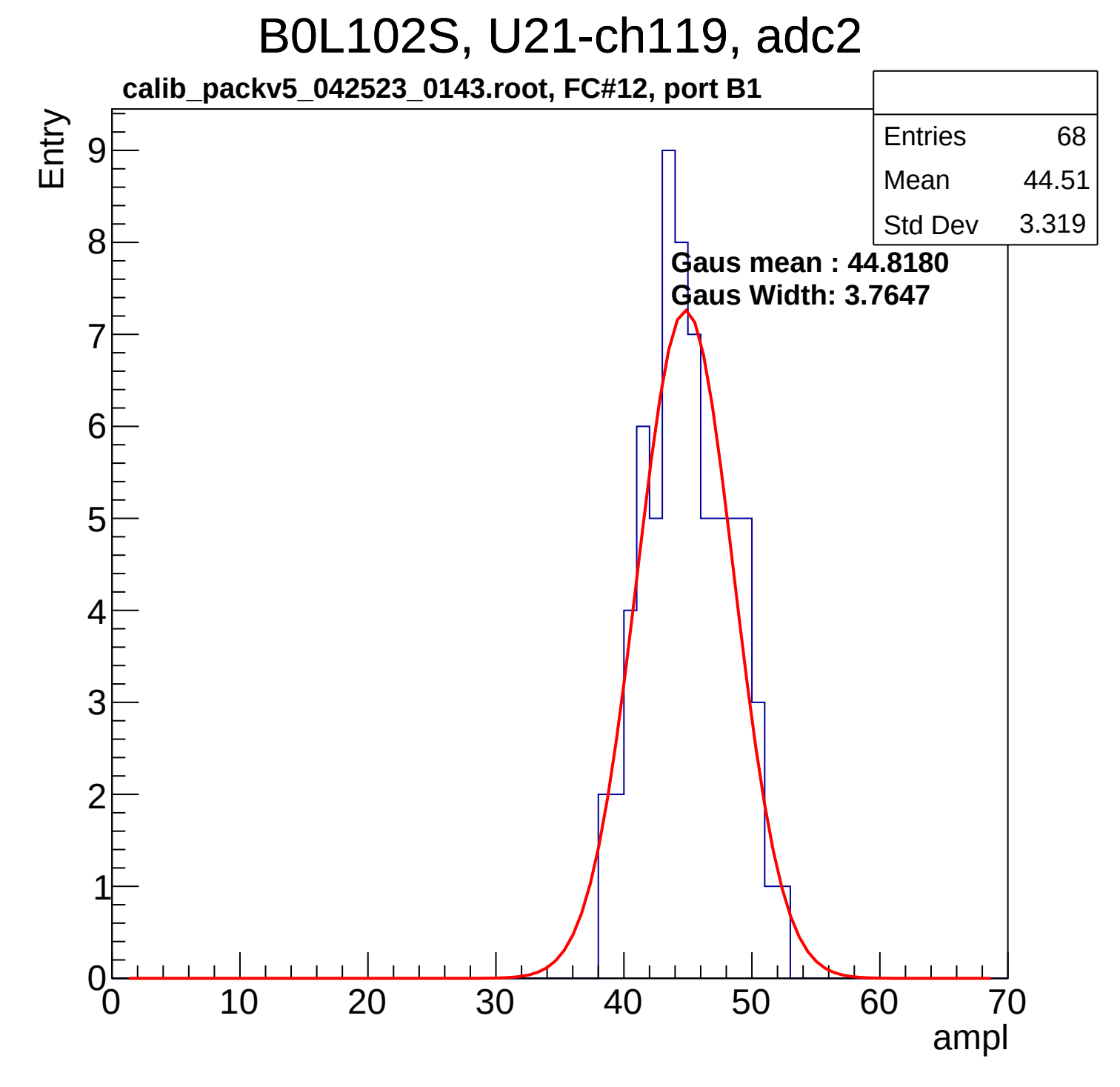
40
50
60
70


B0L102S, U21-ch119, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 37.3
Std Dev 3.83
Gaus mean : 38.0214
Gaus Width: 3.9515
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch119, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 44.51
Std Dev 3.319
Gaus mean : 44.8180
Gaus Width: 3.7647
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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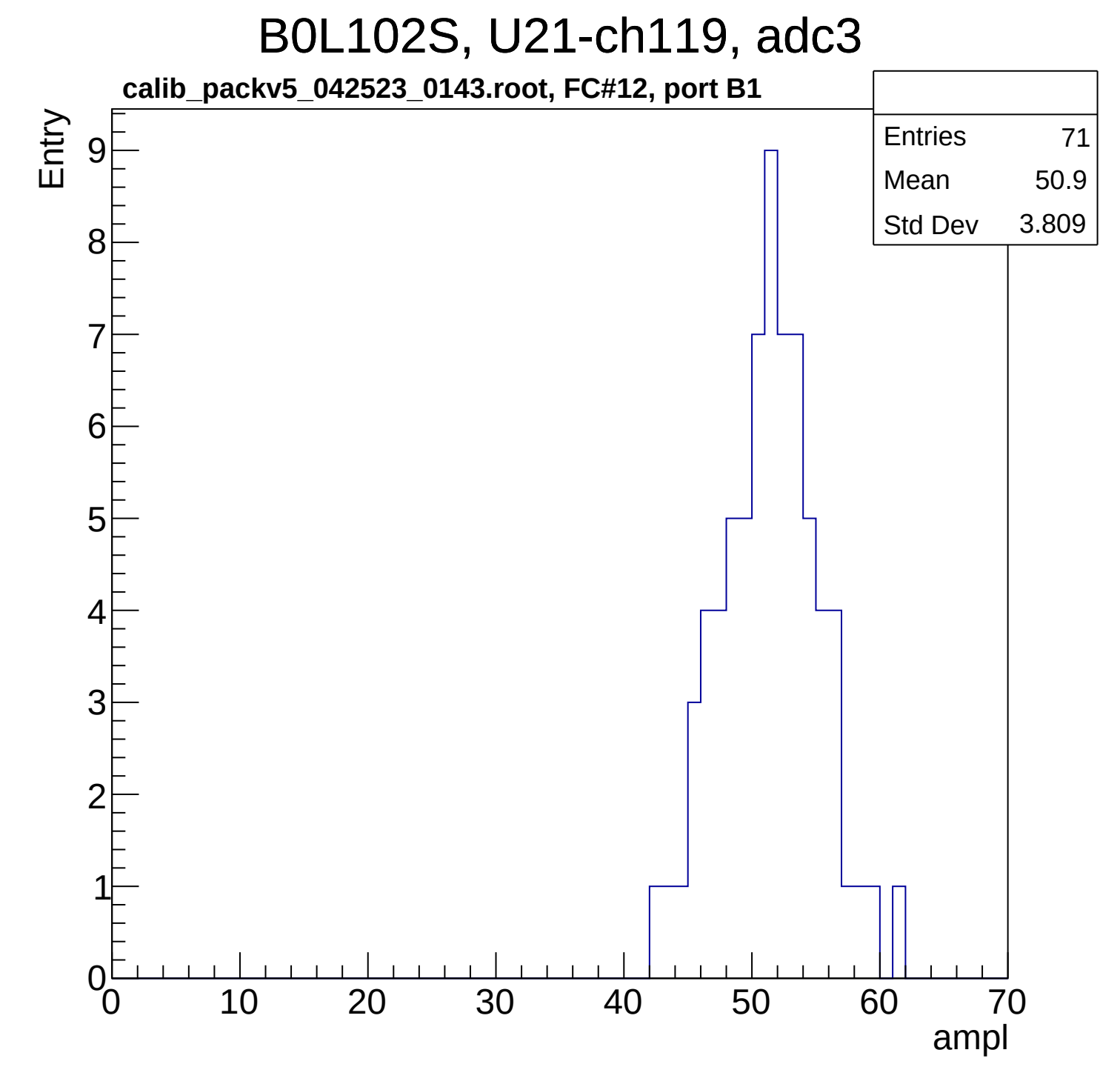
B0L102S, U21-ch119, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 50.9
Std Dev 3.809
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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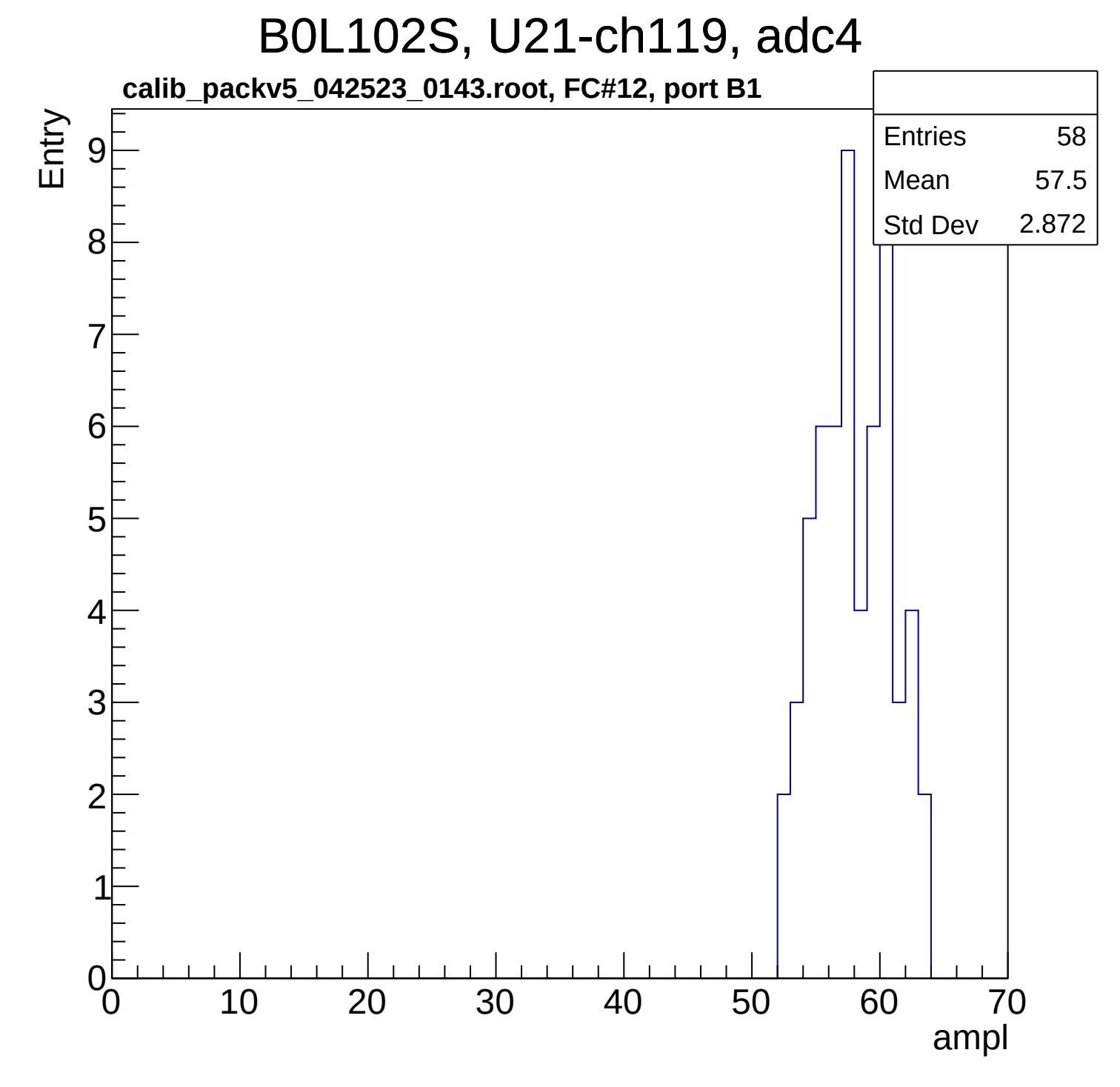
60
70


B0L102S, U21-ch119, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 57.5
Std Dev 2.872
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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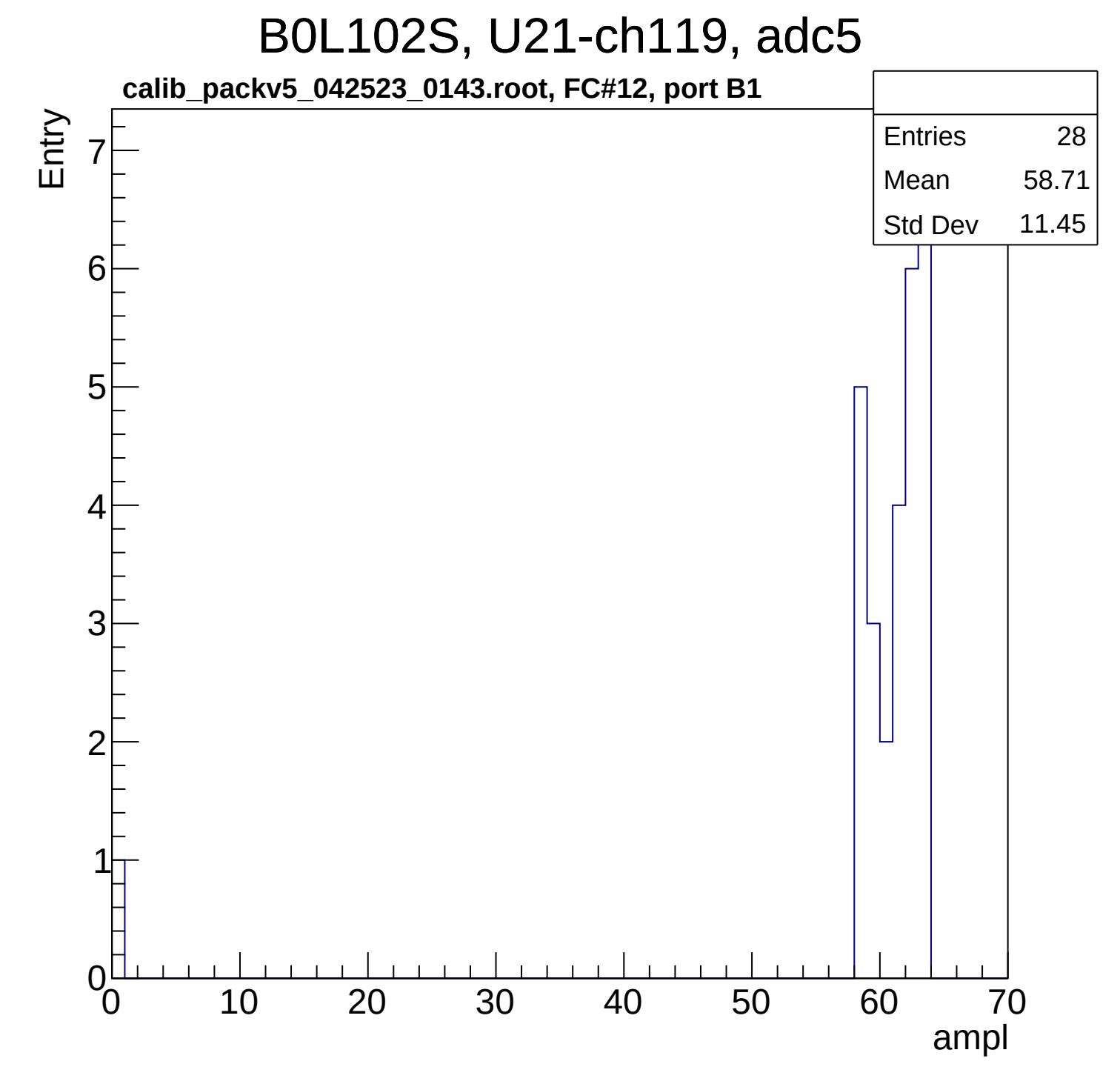
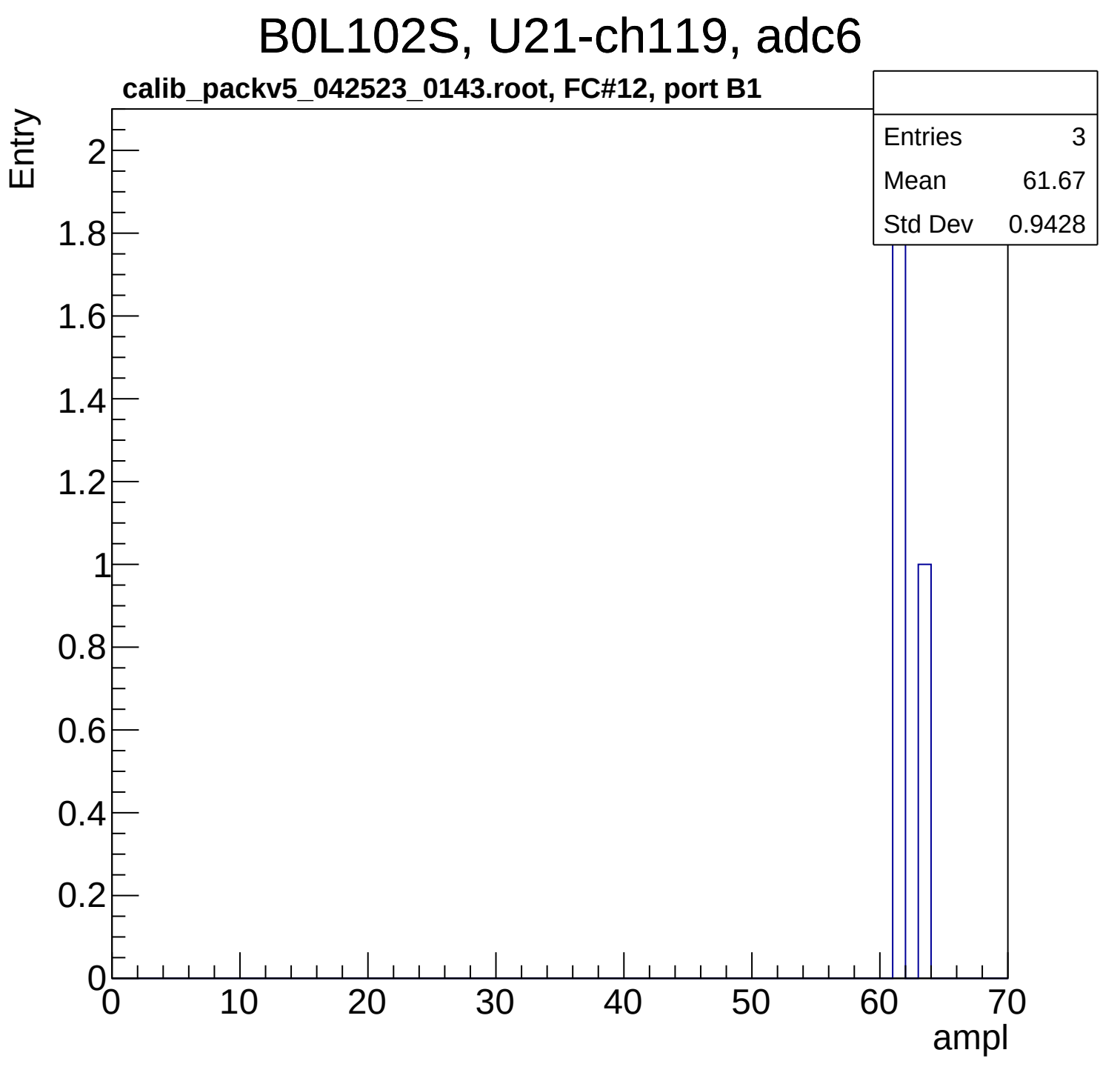
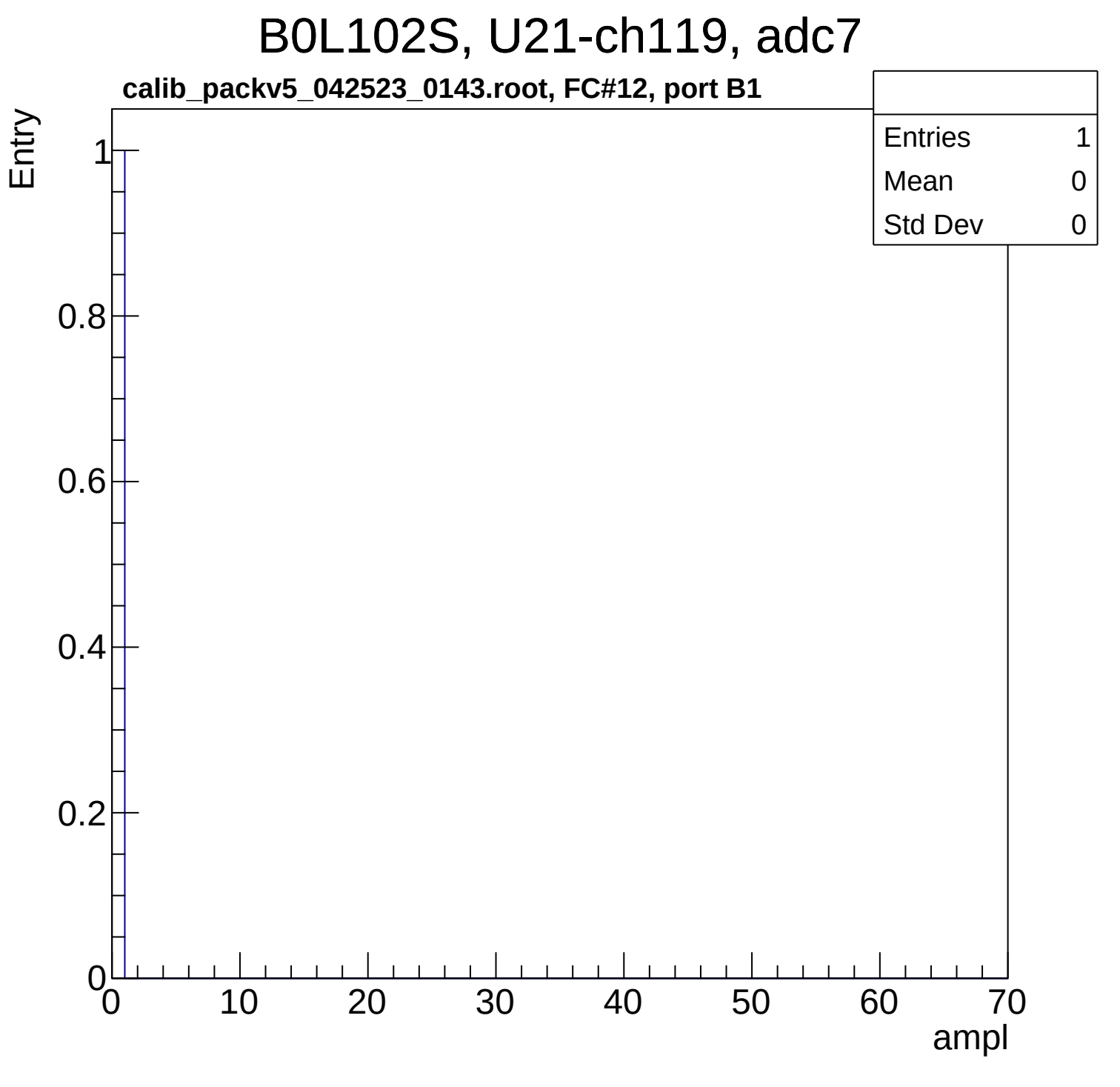
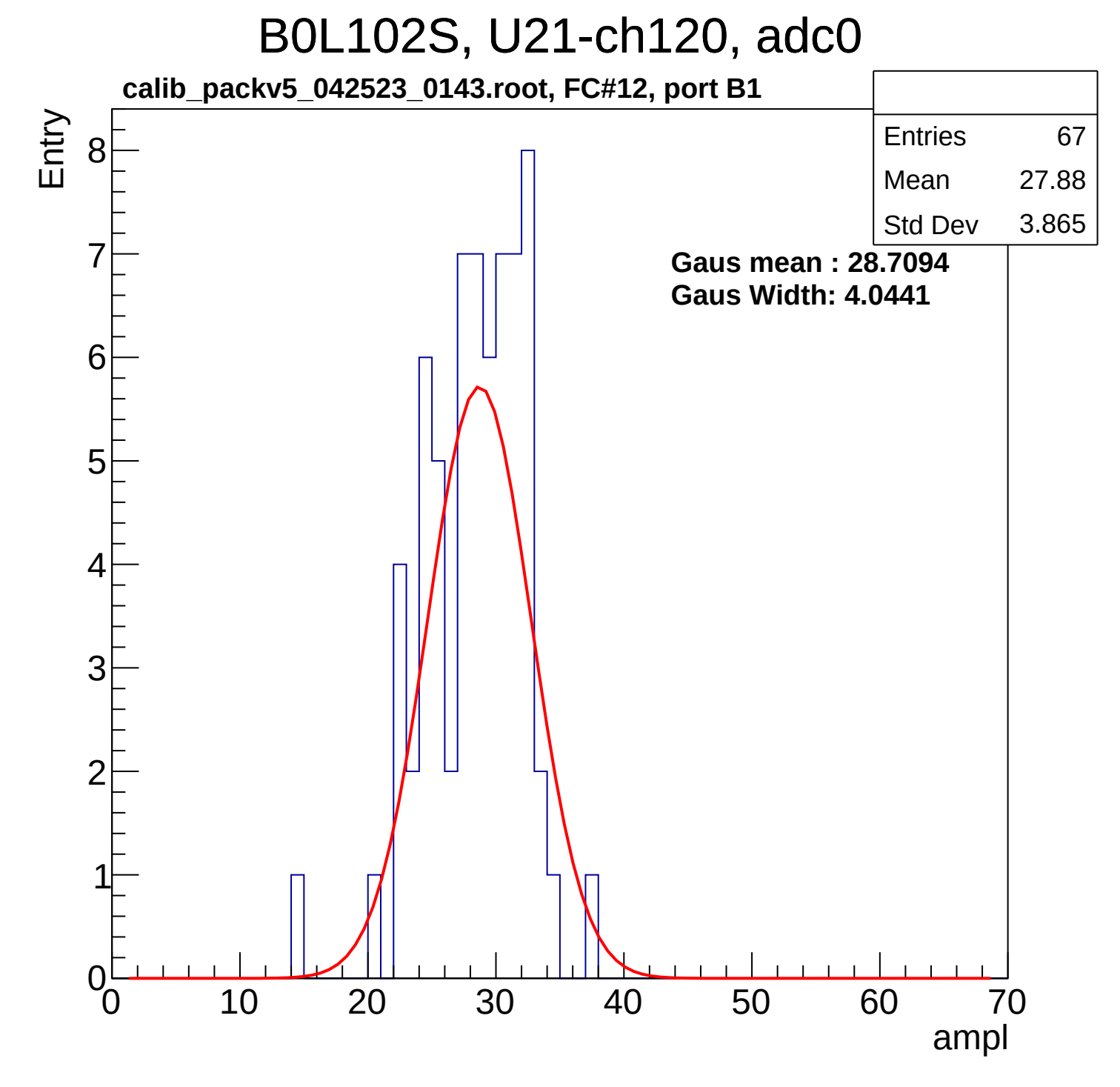

B0L102S, U21-ch119, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 28
Mean 58.71
Std Dev 11.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch119, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U21-ch119, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch120, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 27.88
Std Dev 3.865
Gaus mean : 28.7094
Gaus Width: 4.0441
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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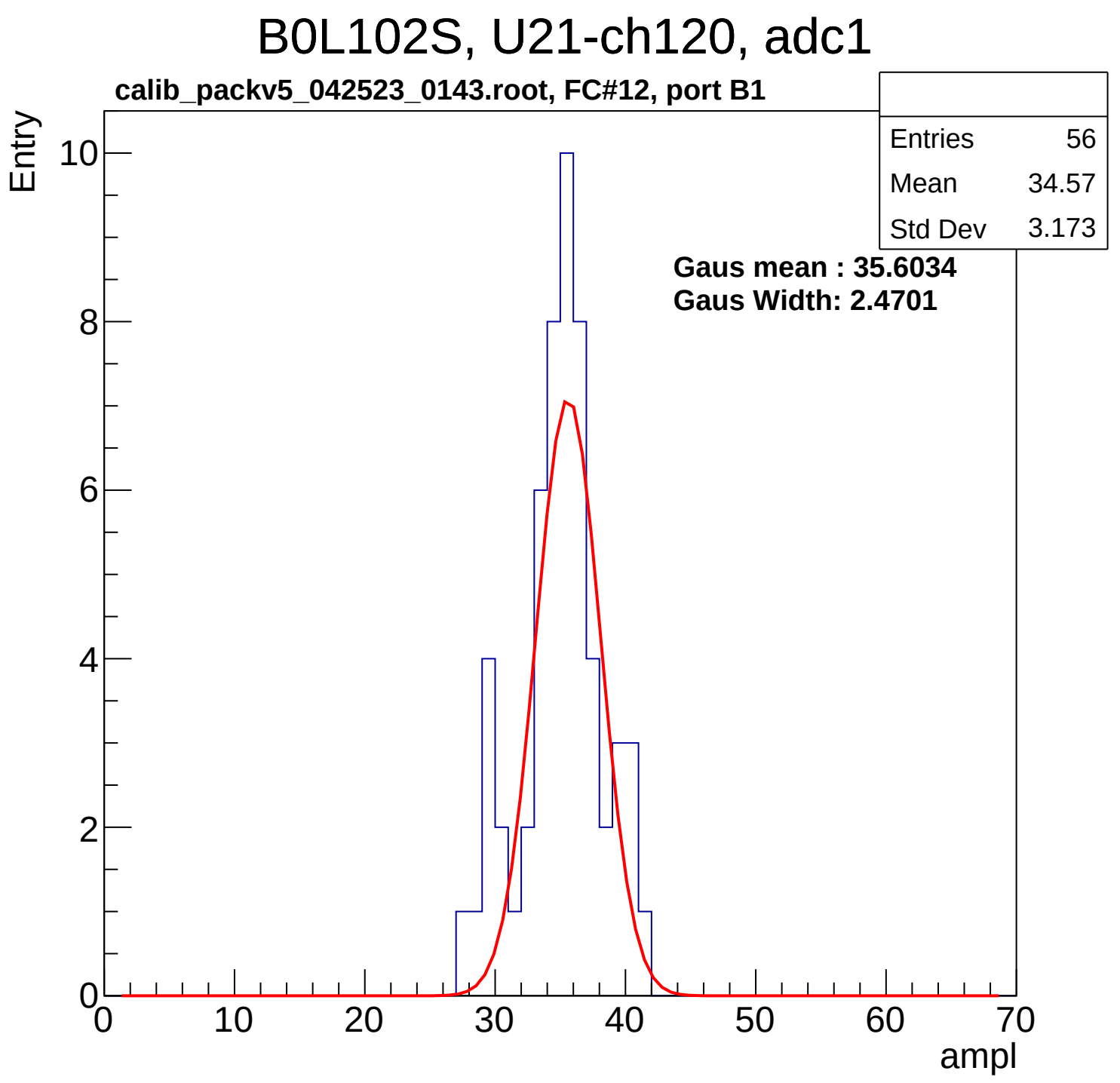
B0L102S, U21-ch120, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 34.57
Std Dev 3.173
Gaus mean : 35.6034
Gaus Width: 2.4701
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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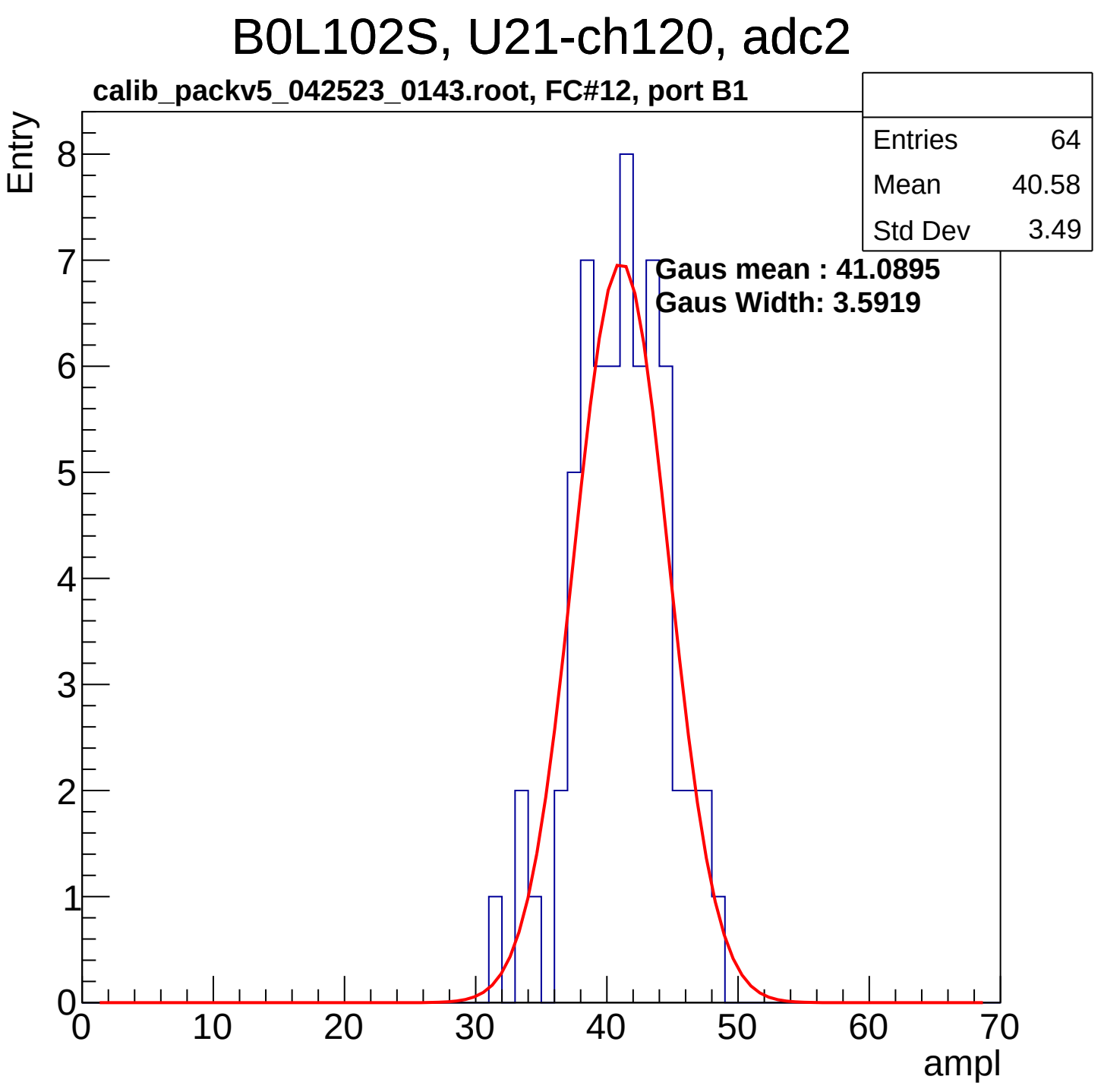
B0L102S, U21-ch120, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 40.58
Std Dev 3.49
Gaus mean : 41.0895
Gaus Width: 3.5919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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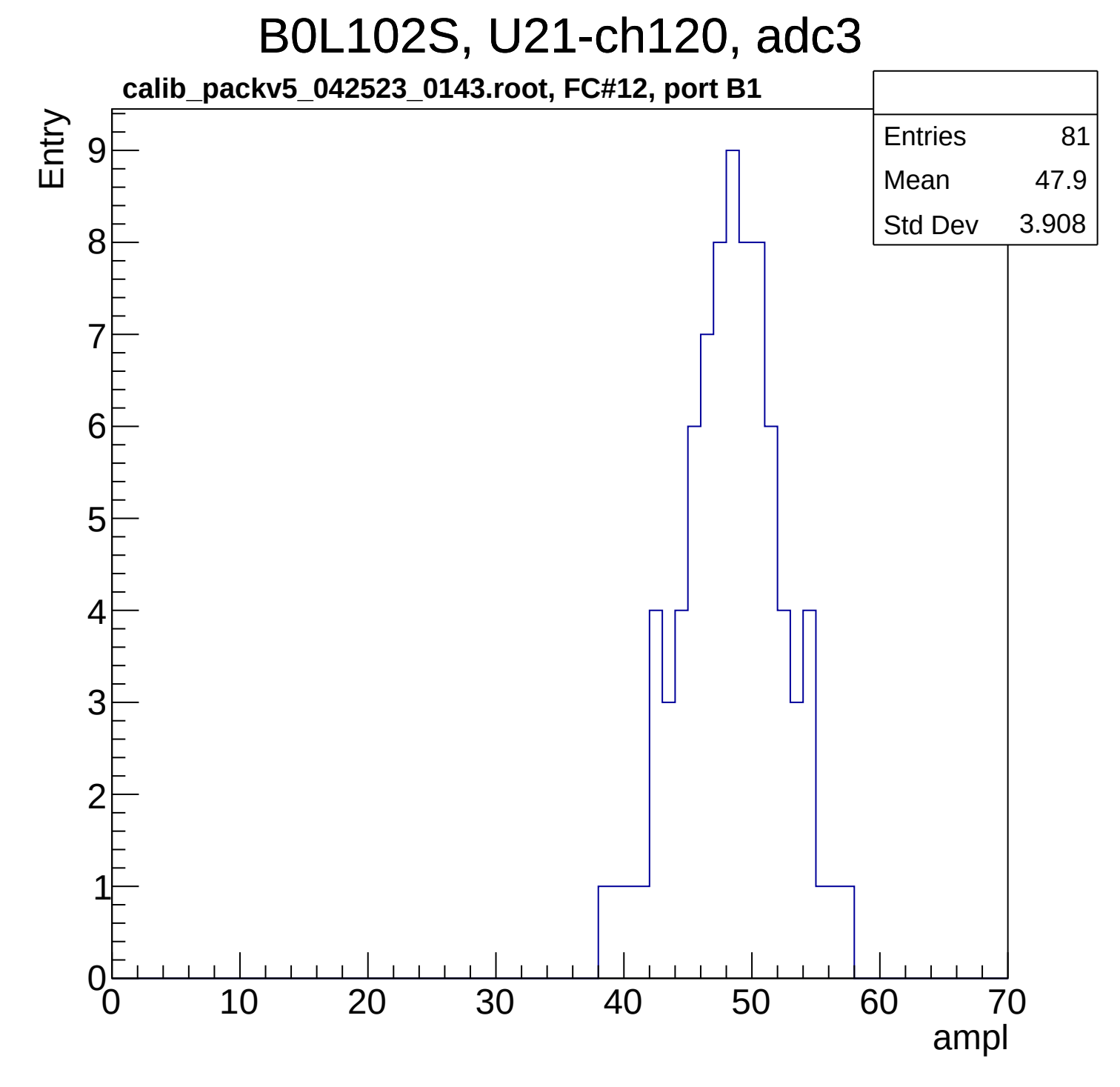
B0L102S, U21-ch120, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 47.9
Std Dev 3.908
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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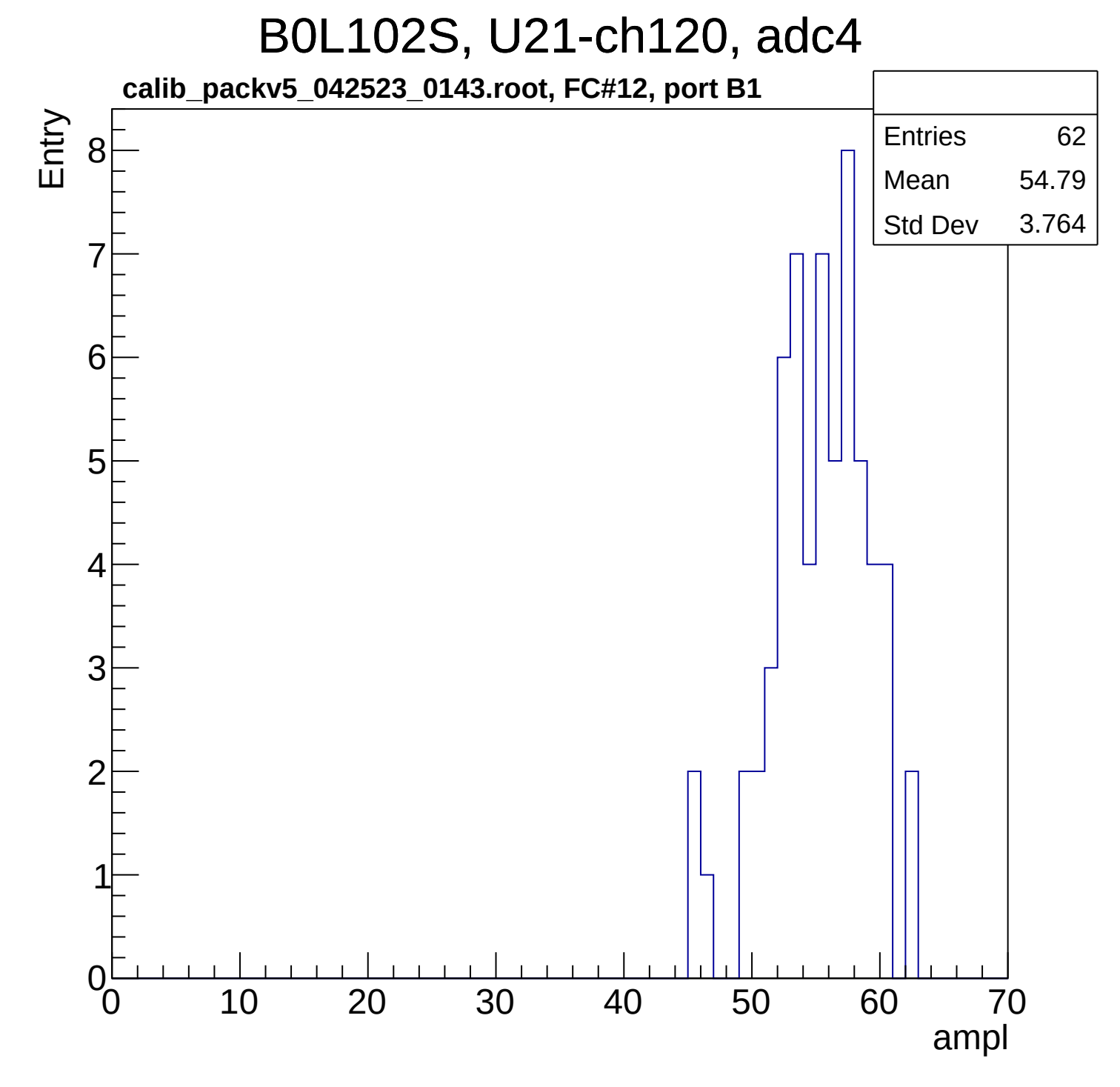
B0L102S, U21-ch120, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 54.79
Std Dev 3.764
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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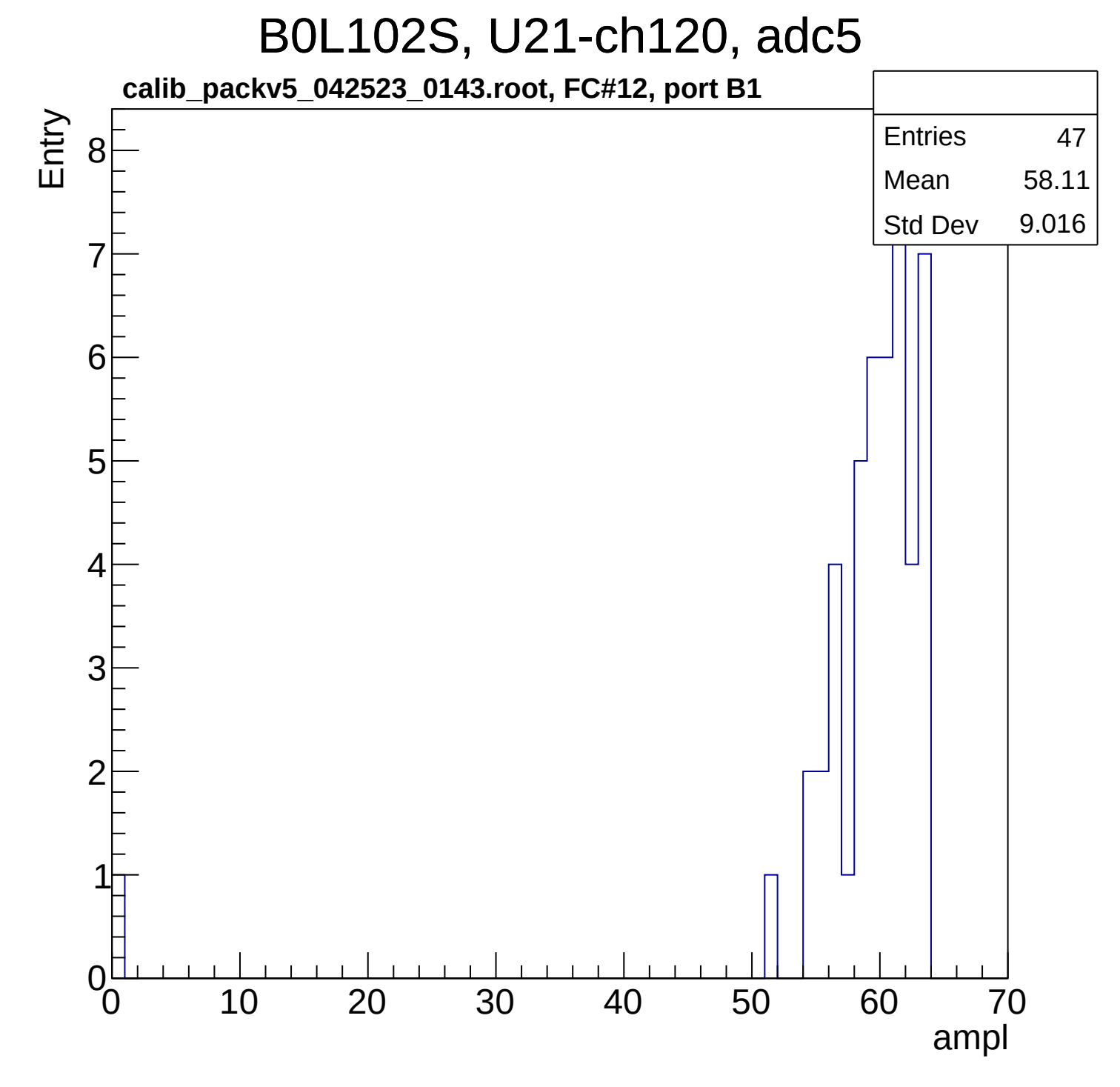
B0L102S, U21-ch120, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 58.11
Std Dev 9.016
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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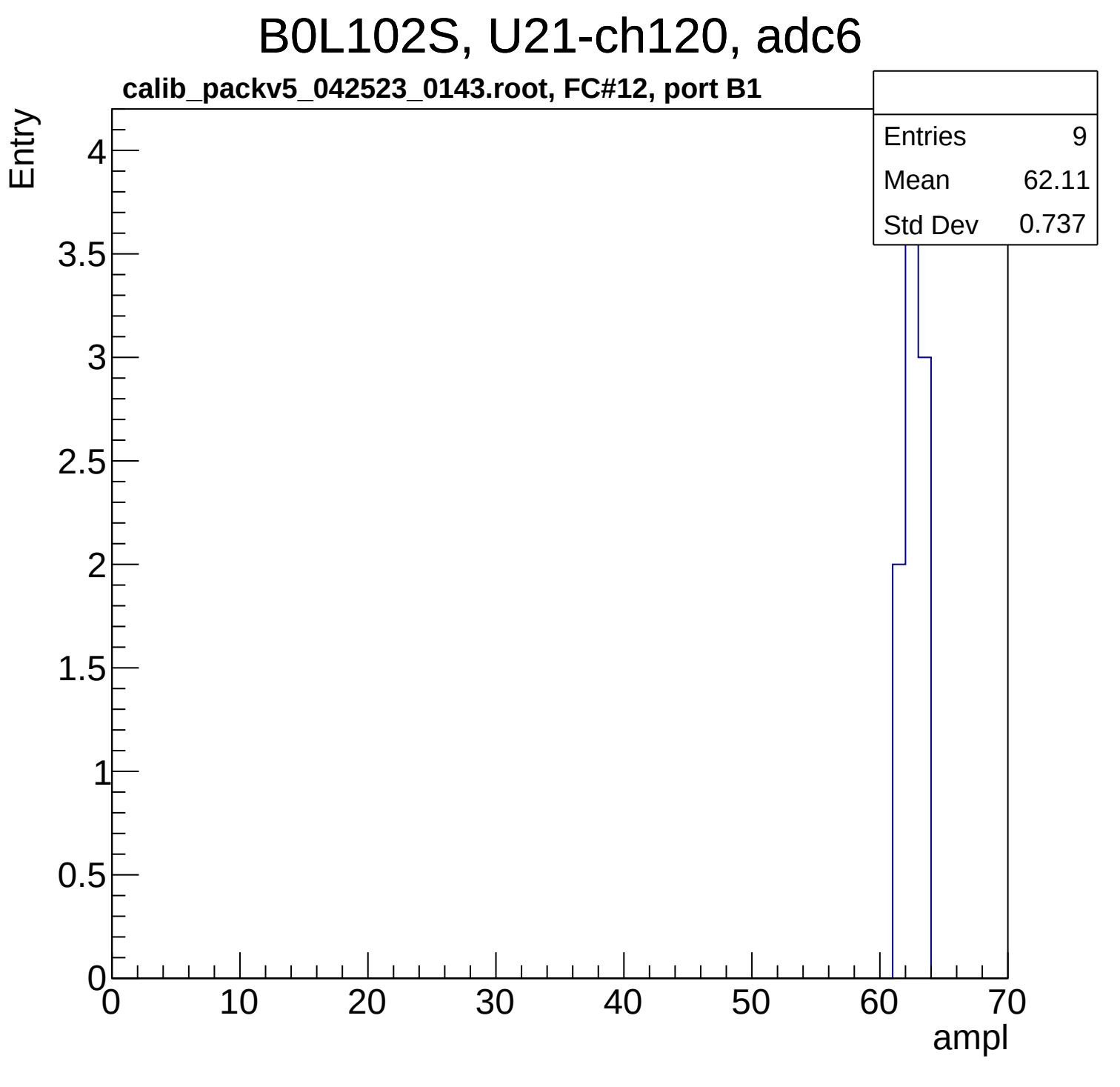
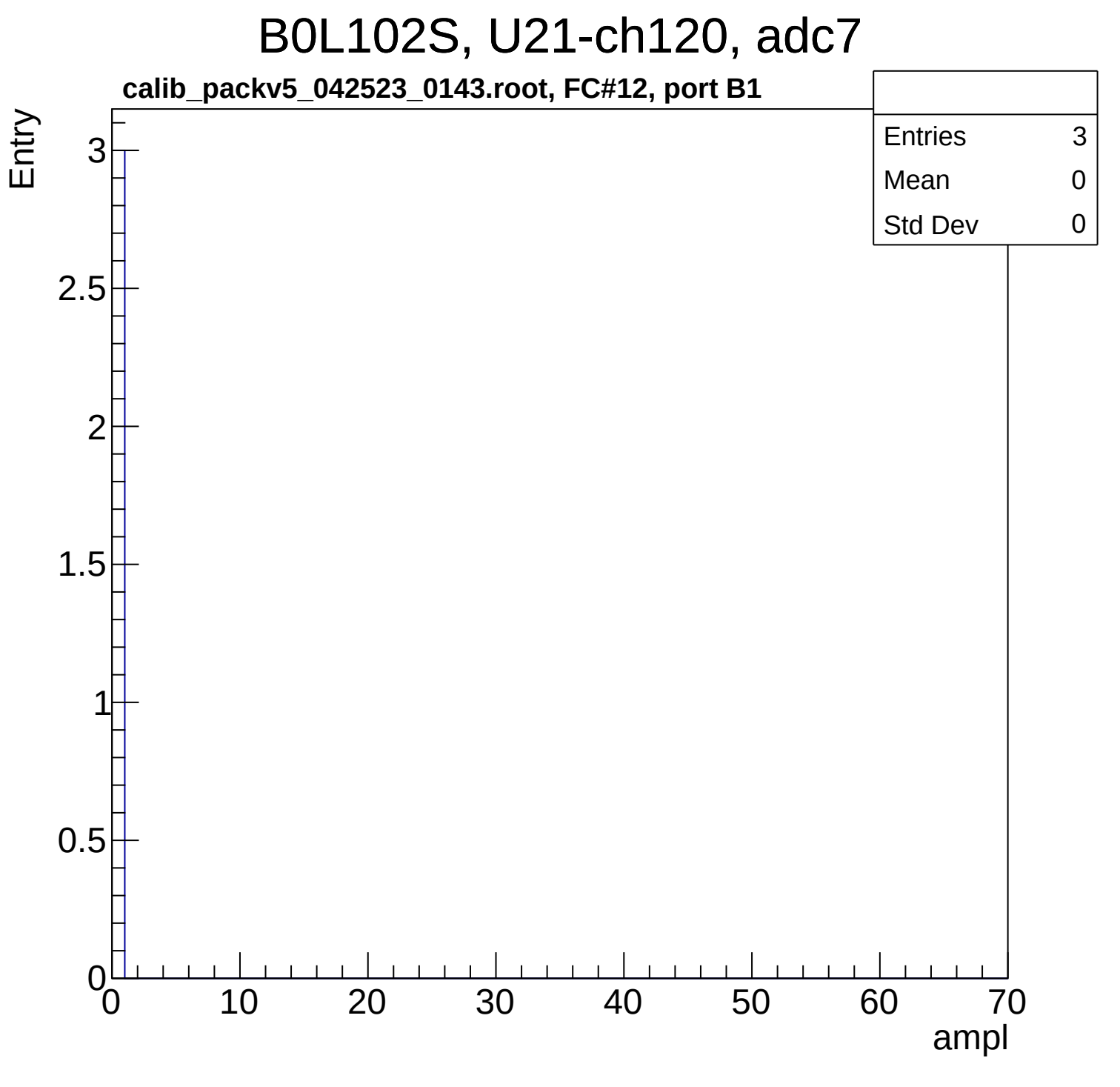
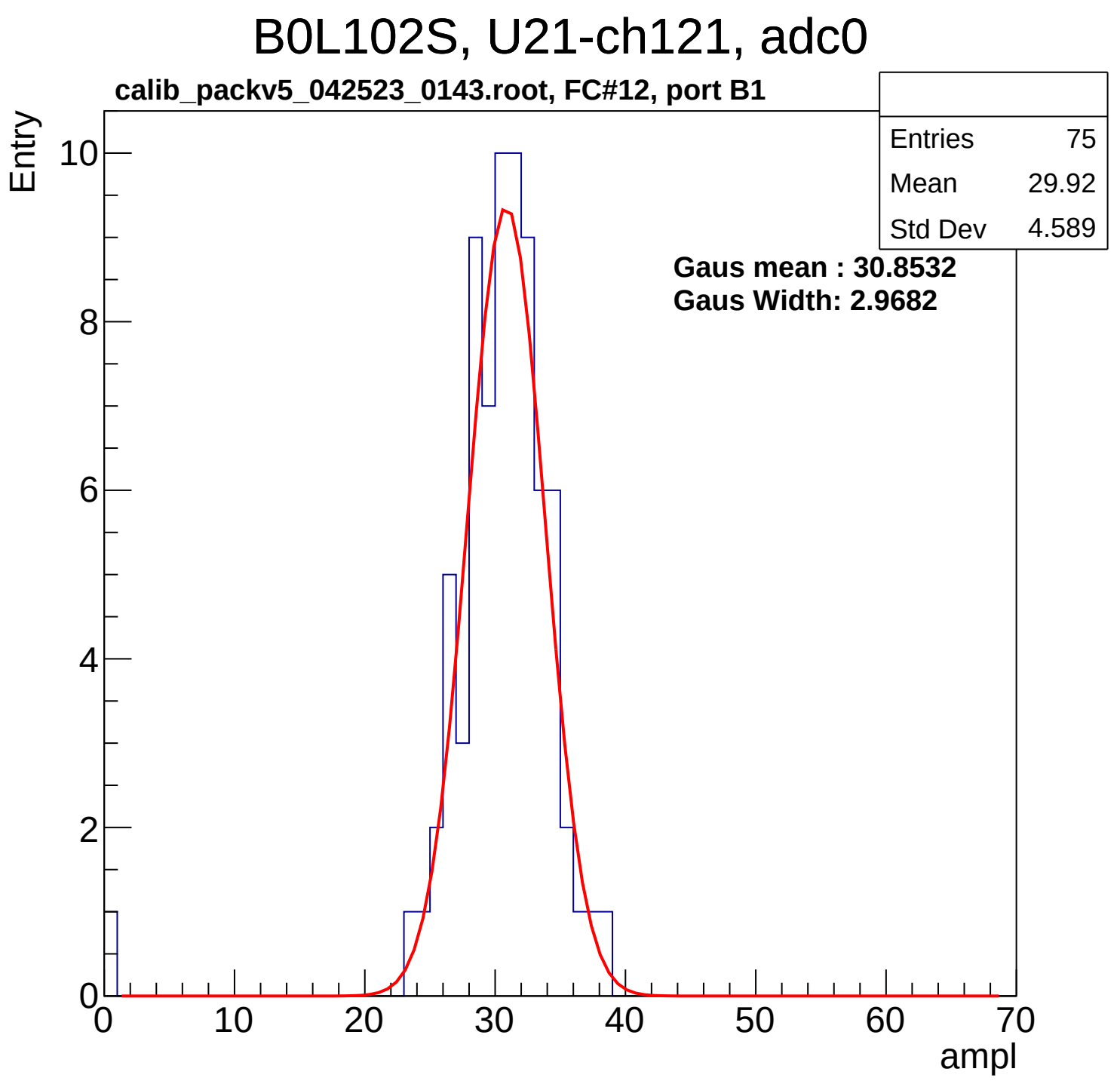
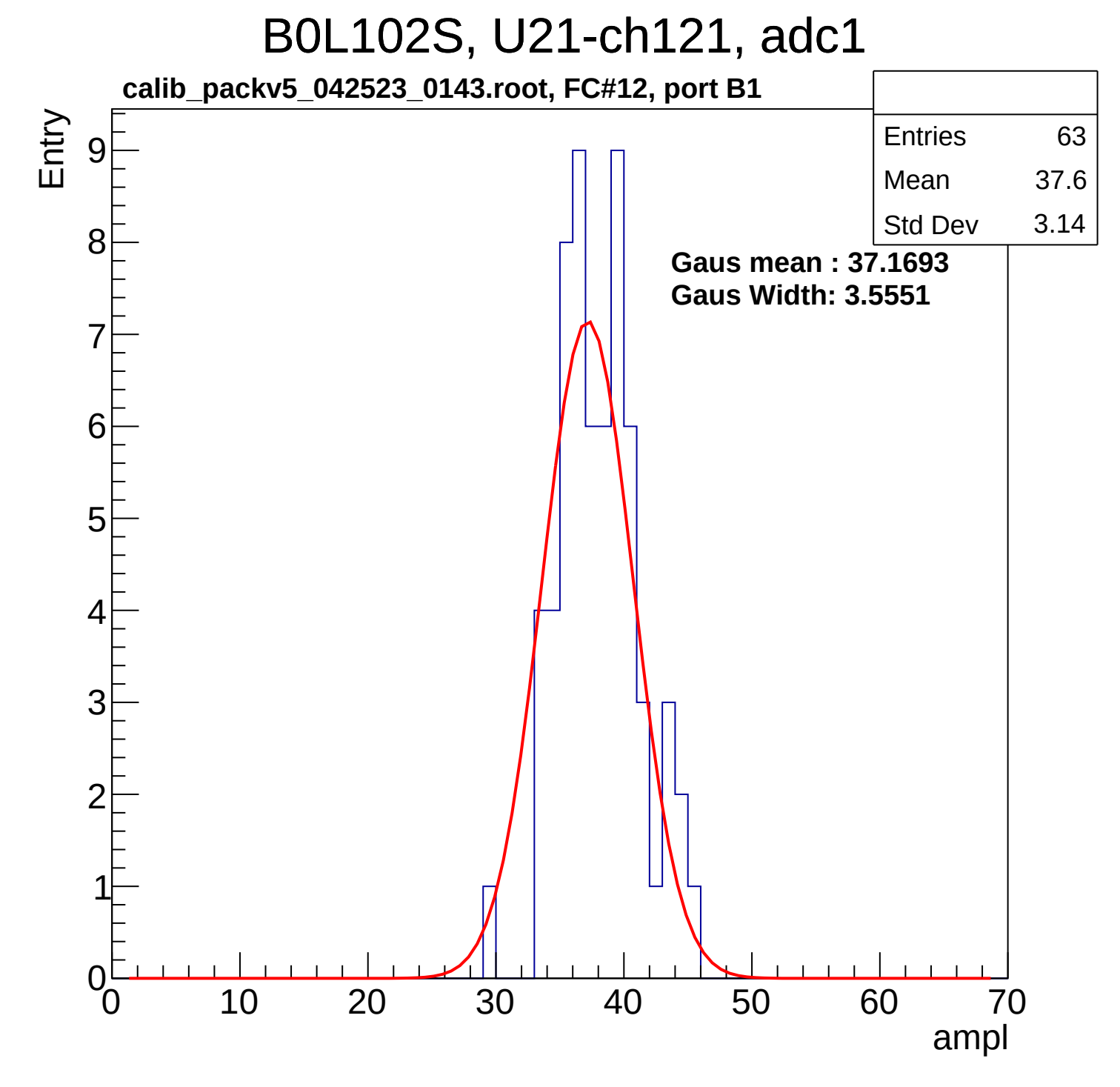
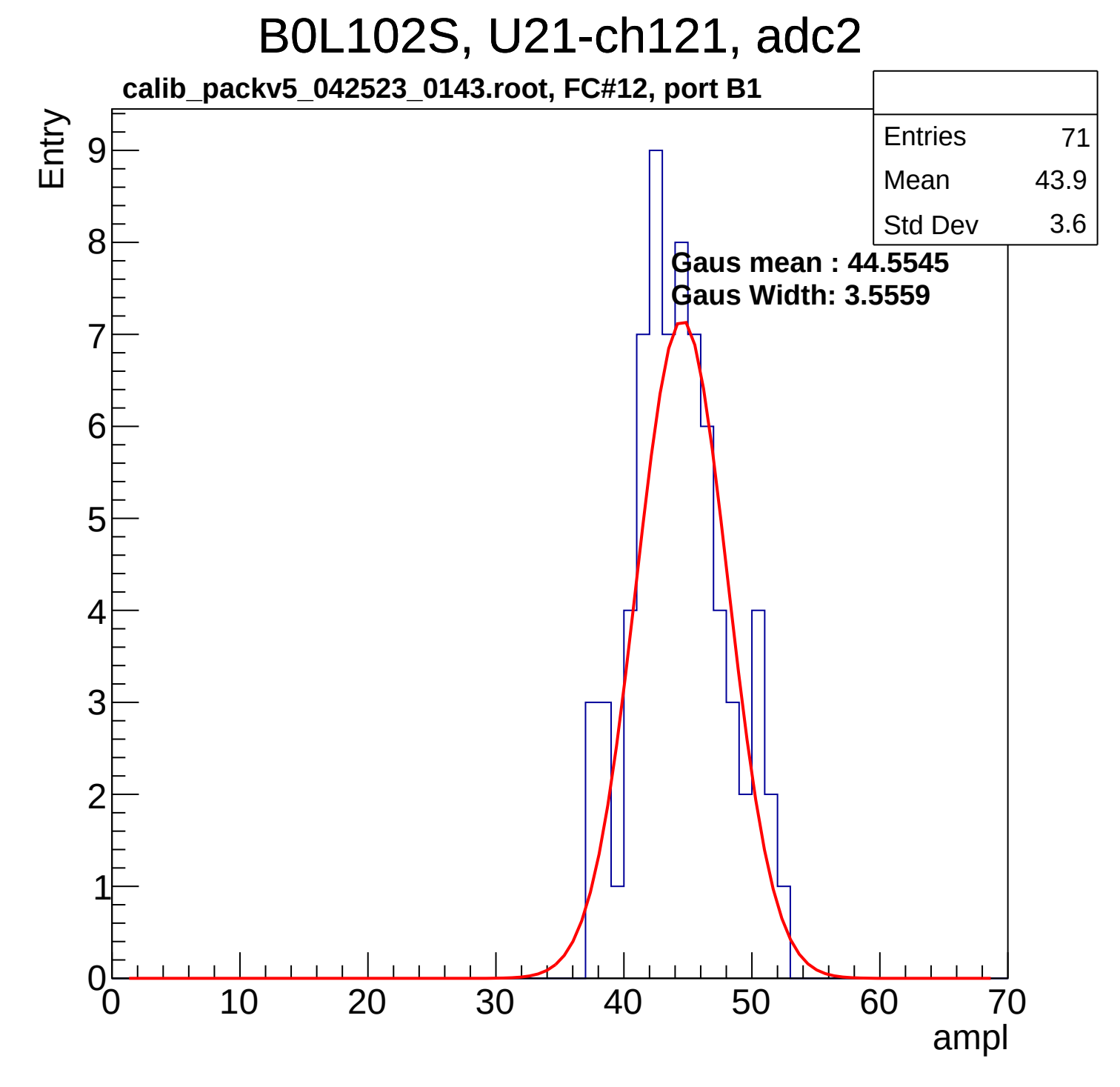
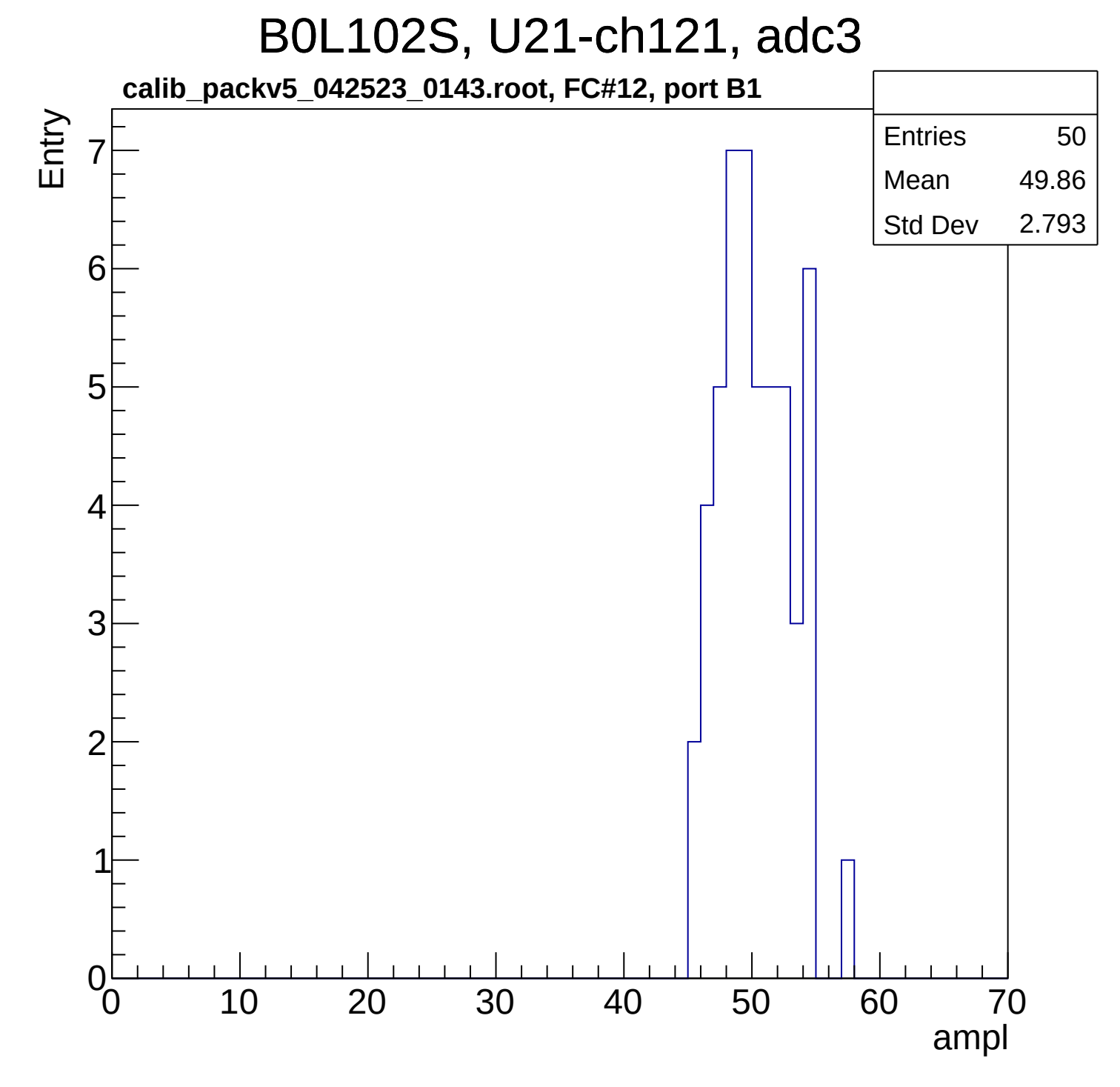

B0L102S, U21-ch120, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 62.11
Std Dev 0.737
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

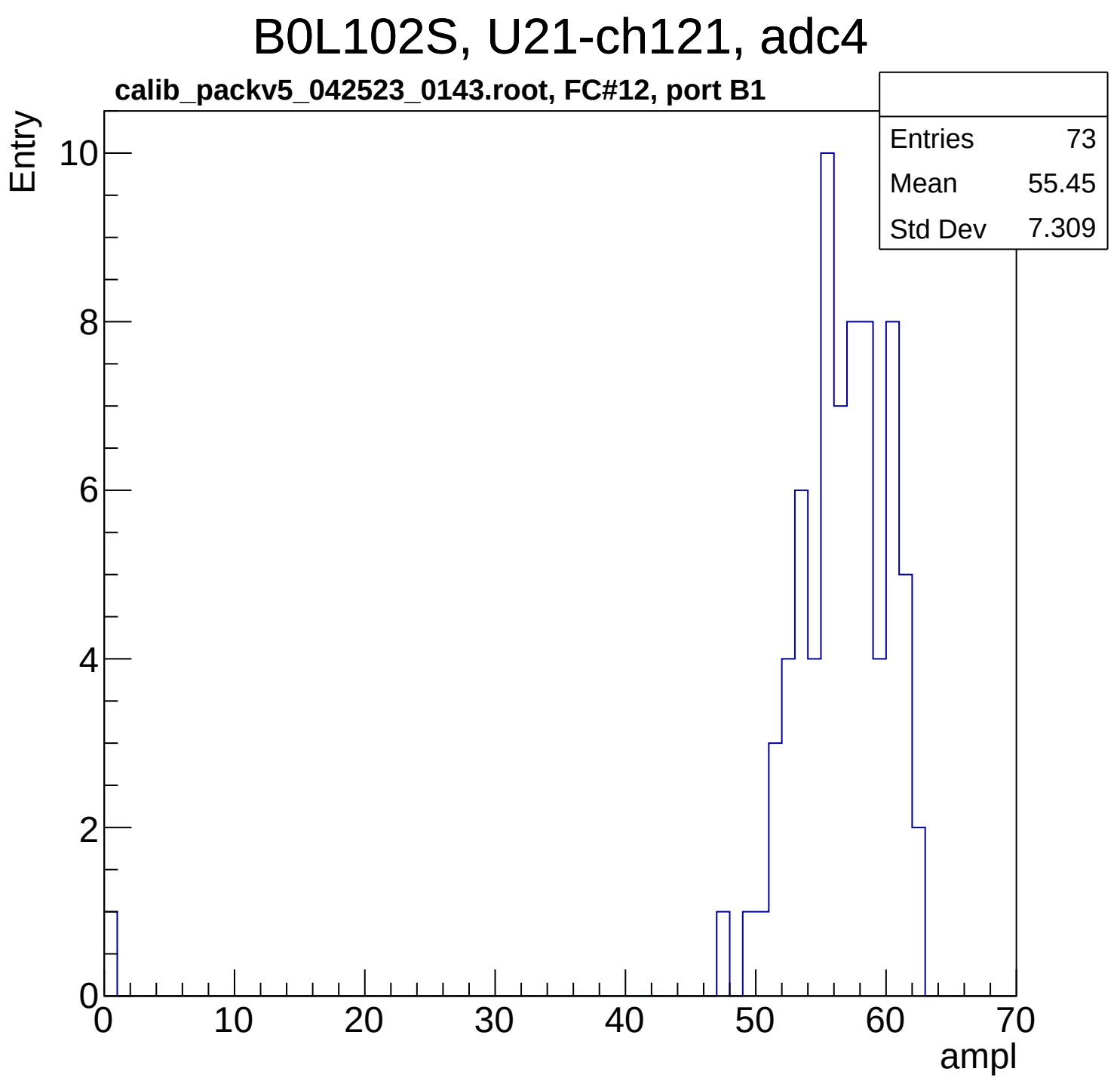

B0L102S, U21-ch120, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

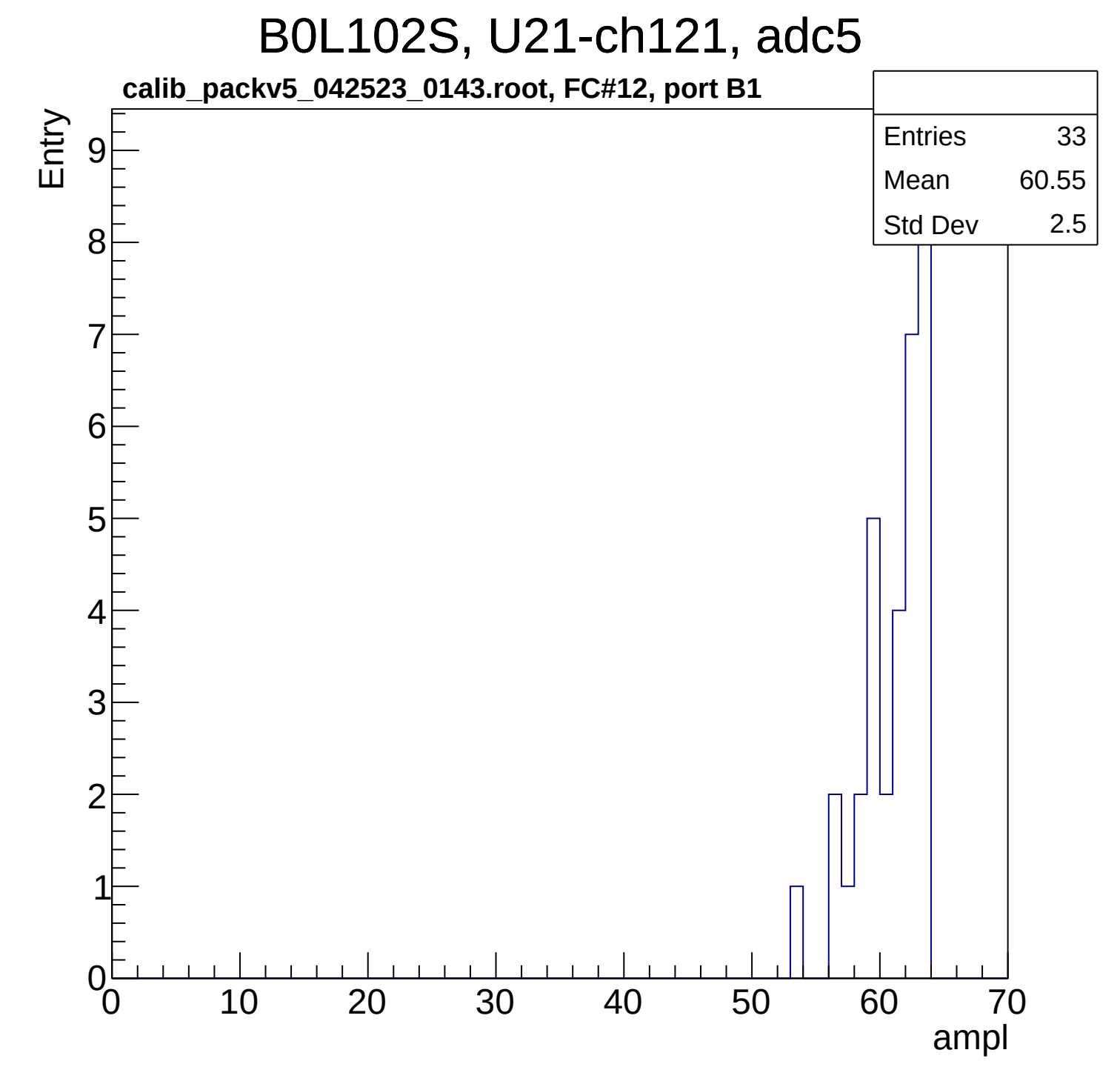

B0L102S, U21-ch121, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 29.92
Std Dev 4.589
Gaus mean : 30.8532
Gaus Width: 2.9682
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

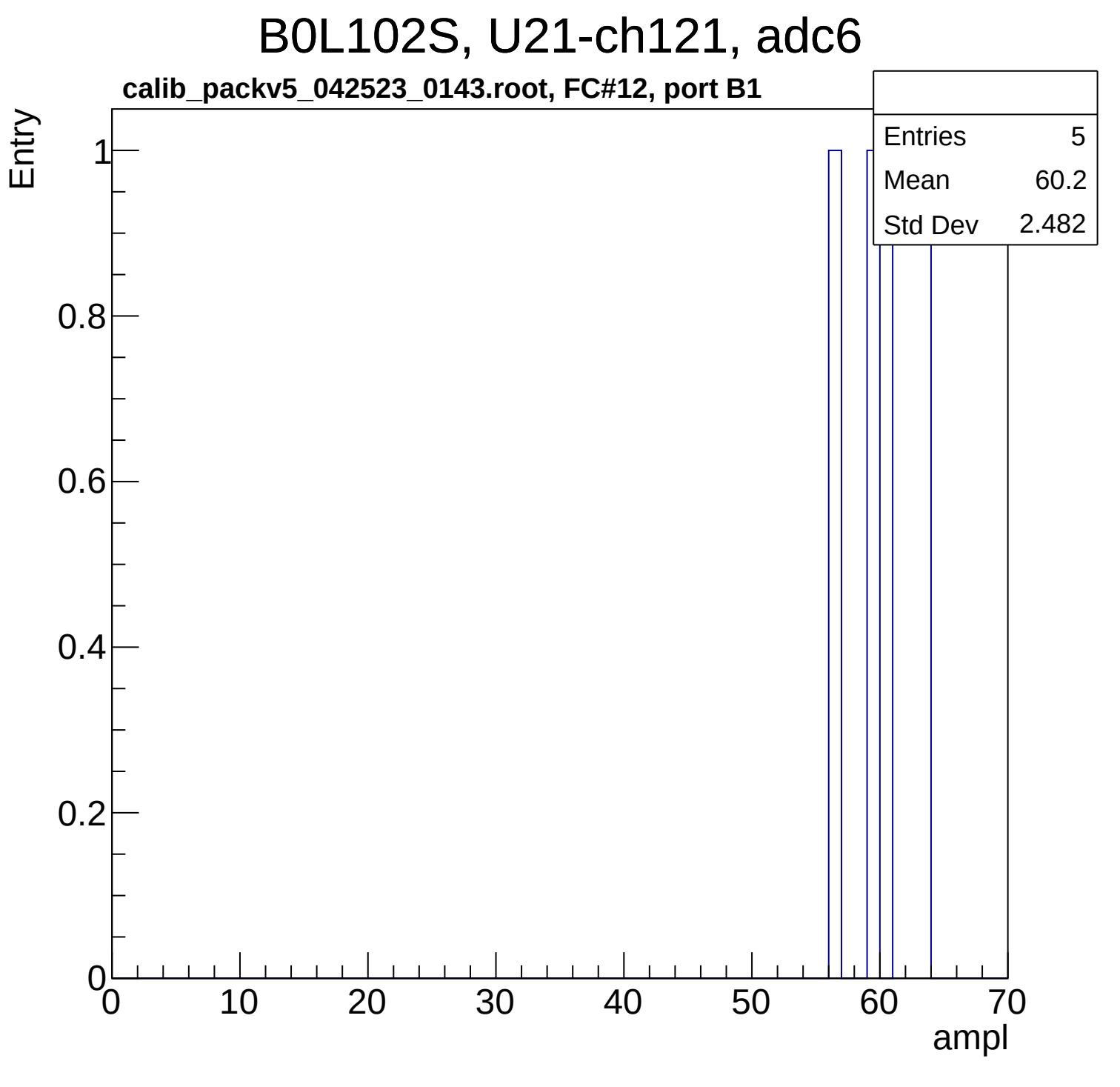

B0L102S, U21-ch121, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 37.6
Std Dev 3.14
Gaus mean : 37.1693
Gaus Width: 3.5551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

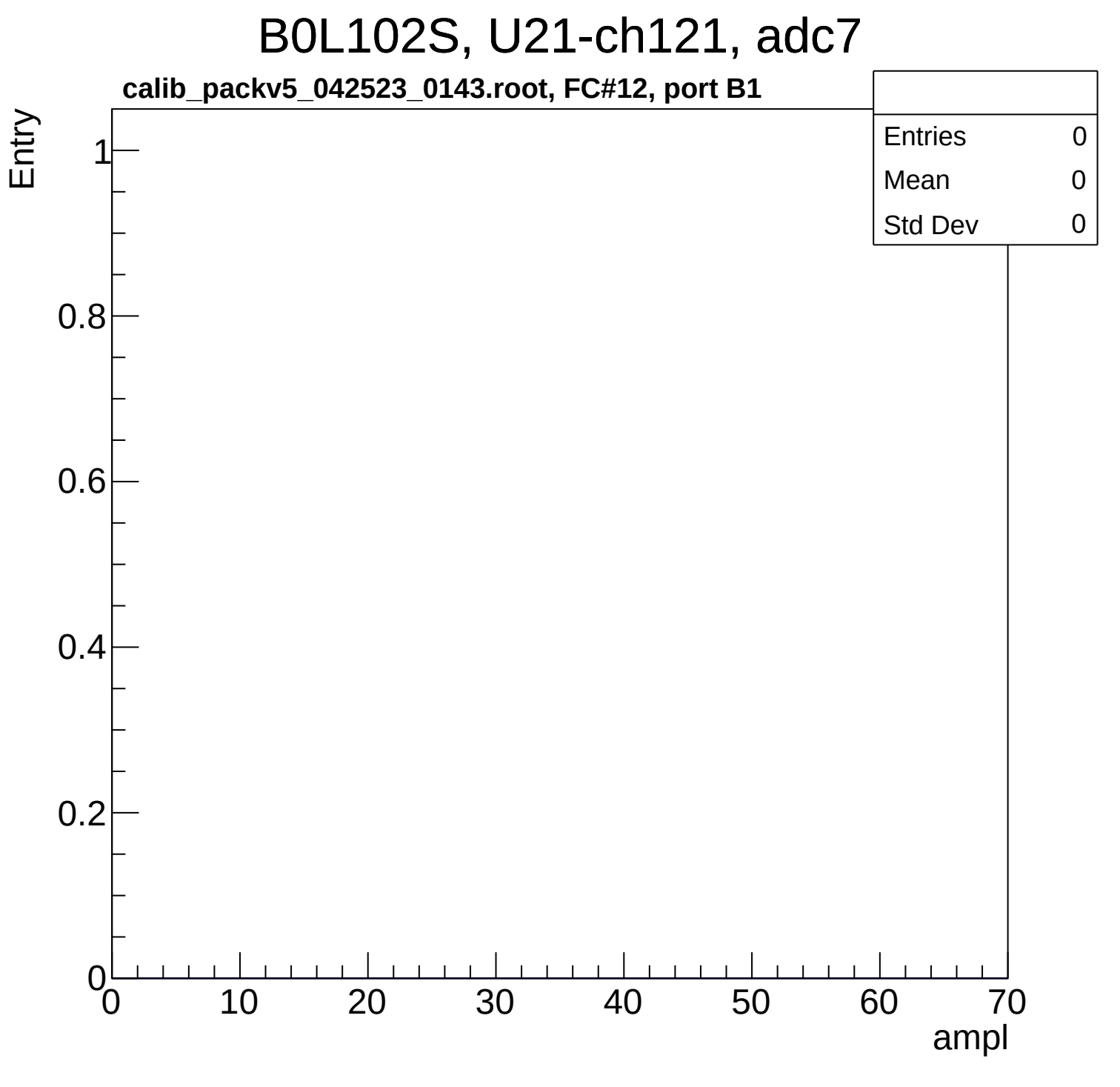

B0L102S, U21-ch121, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 43.9
Std Dev 3.6
Gaus mean : 44.5545
Gaus Width: 3.5559
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

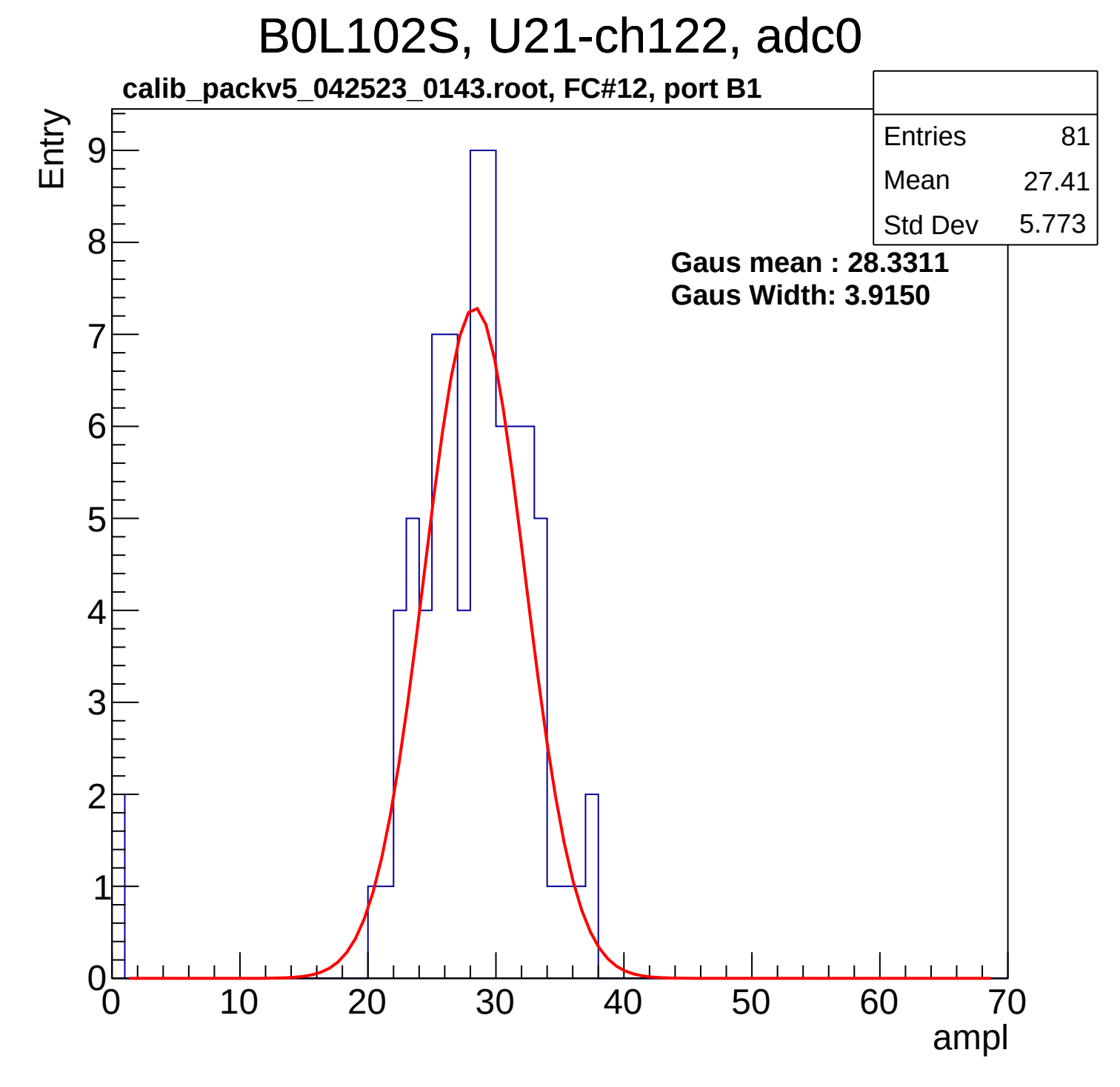

B0L102S, U21-ch121, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 49.86
Std Dev 2.793
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

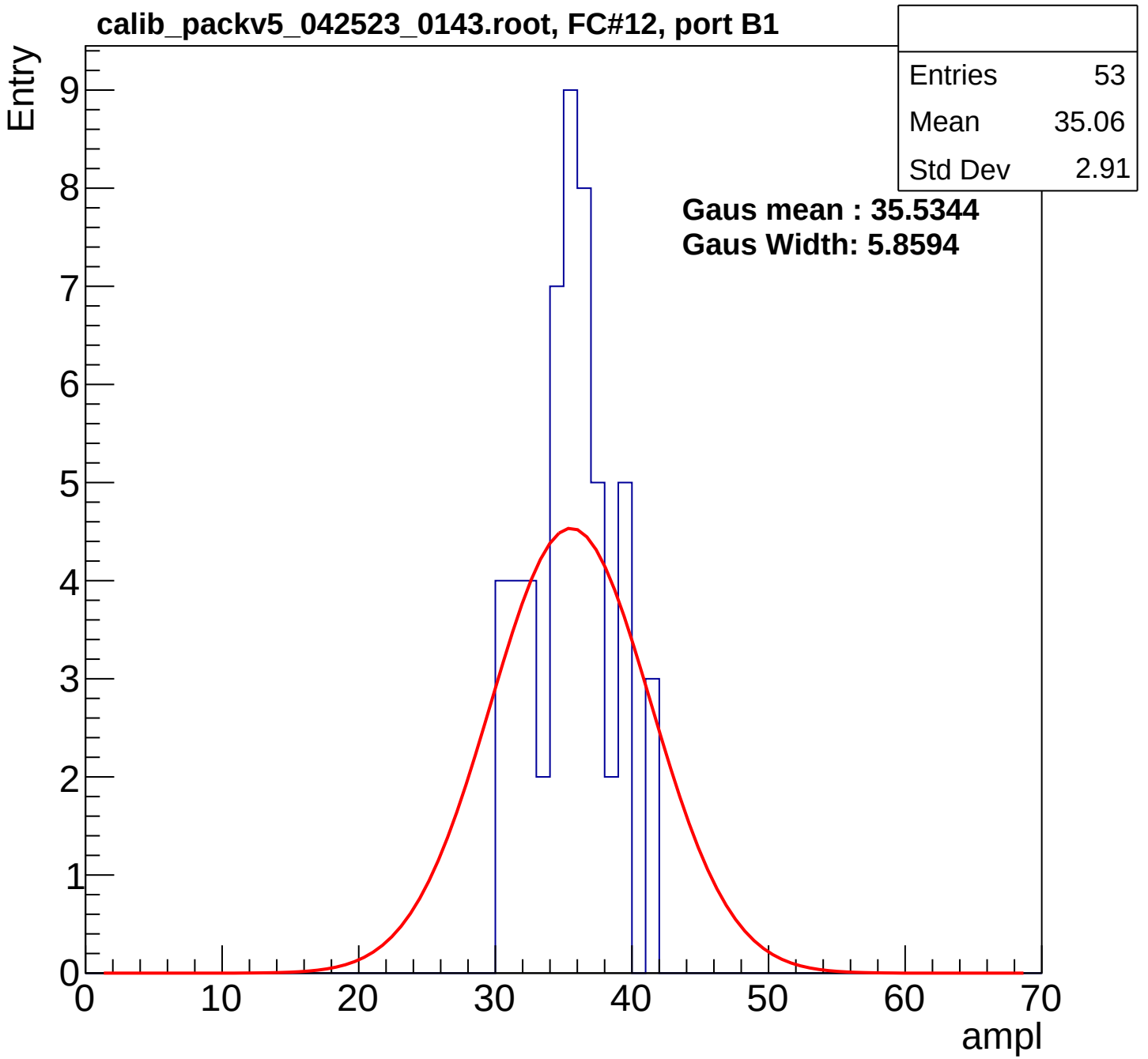

B0L102S, U21-ch121, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 55.45
Std Dev 7.309
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

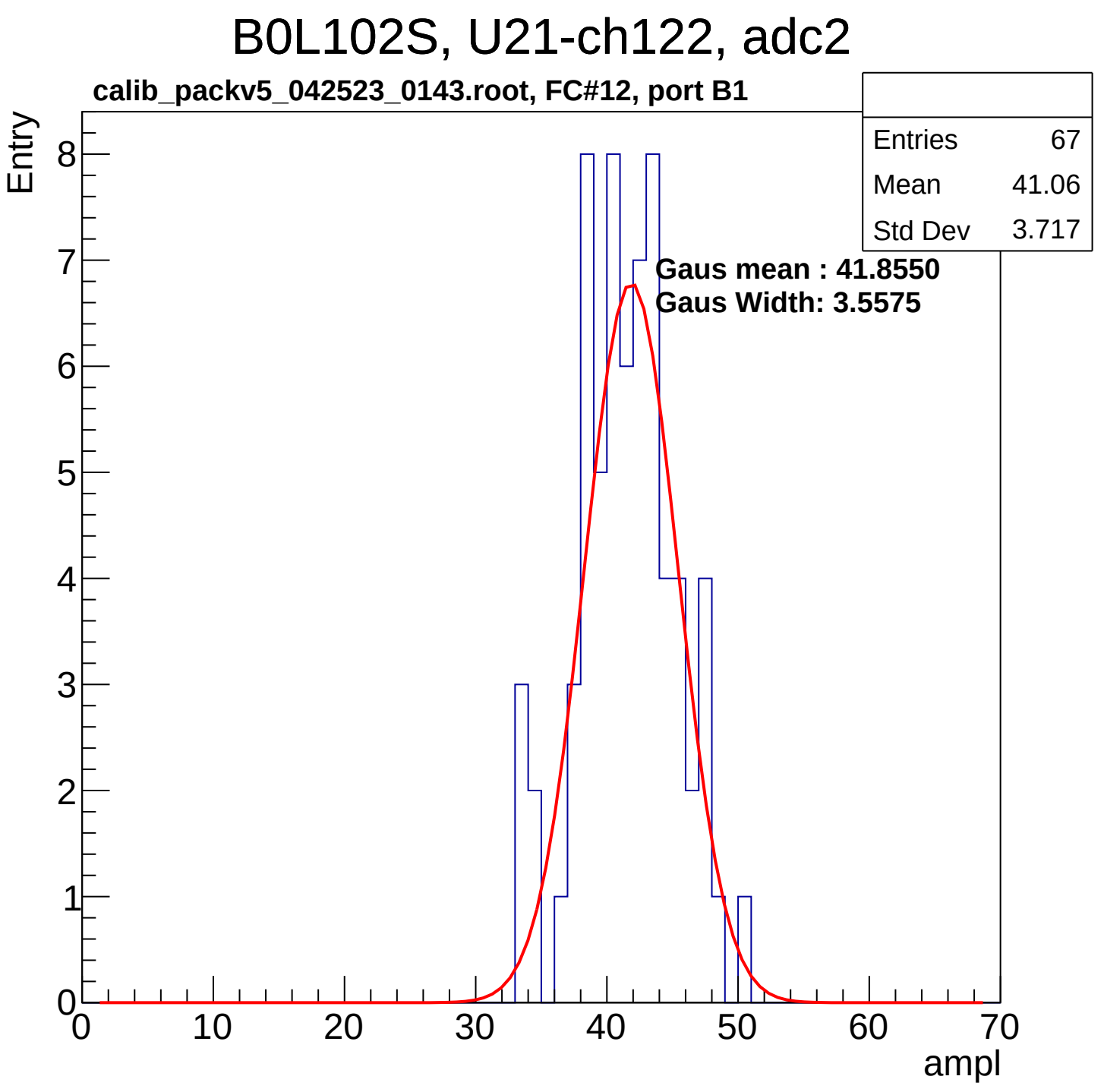

B0L102S, U21-ch121, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 60.55
Std Dev 2.5
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch121, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 60.2
Std Dev 2.482
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U21-ch121, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch122, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 27.41
Std Dev 5.773
Gaus mean : 28.3311
Gaus Width: 3.9150
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch122, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 35.06
Std Dev 2.91
Gaus mean : 35.5344
Gaus Width: 5.8594
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch122, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 41.06
Std Dev 3.717
Gaus mean : 41.8550
Gaus Width: 3.5575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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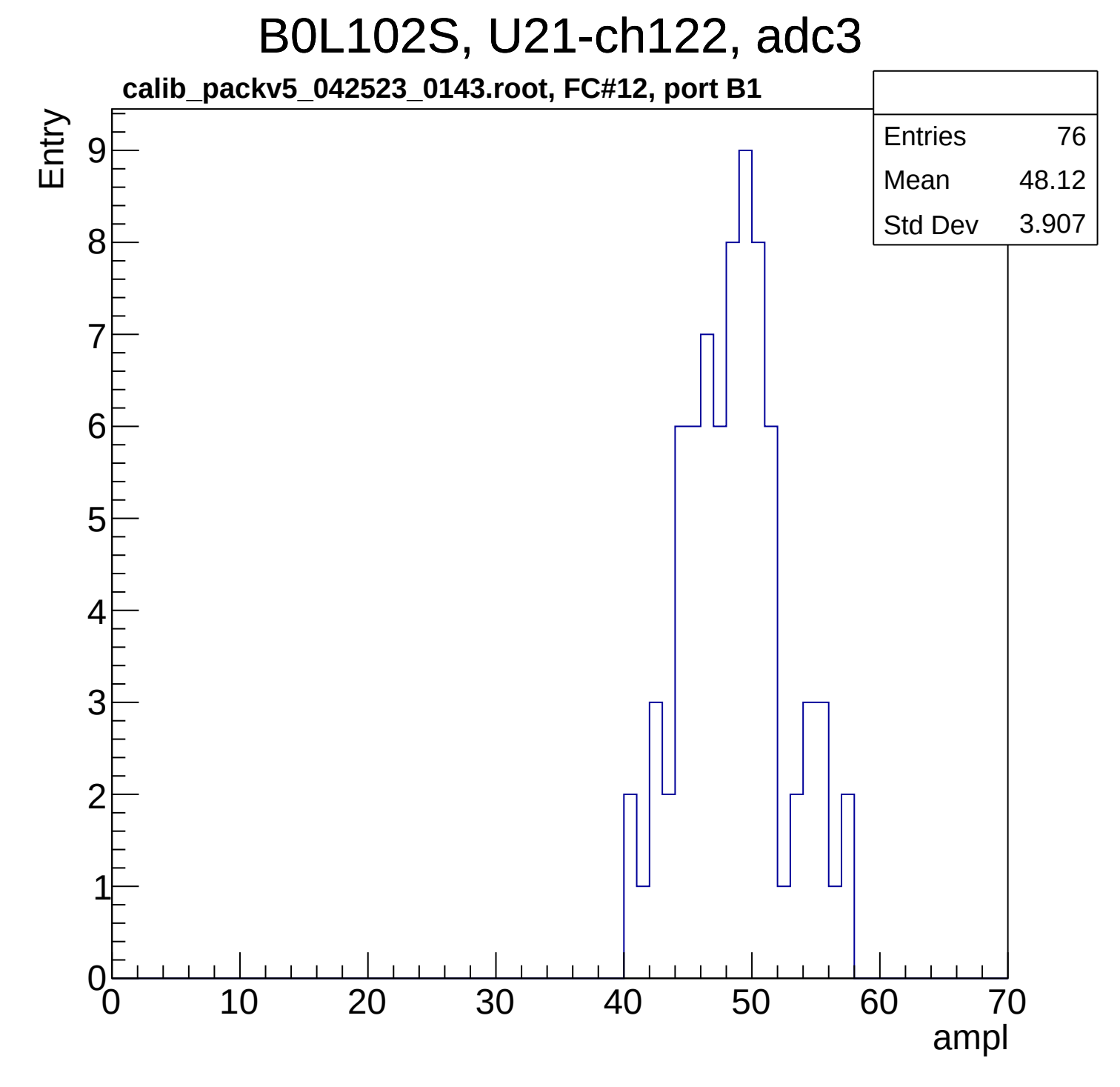
20
30
40
50
60
70


B0L102S, U21-ch122, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 48.12
Std Dev 3.907
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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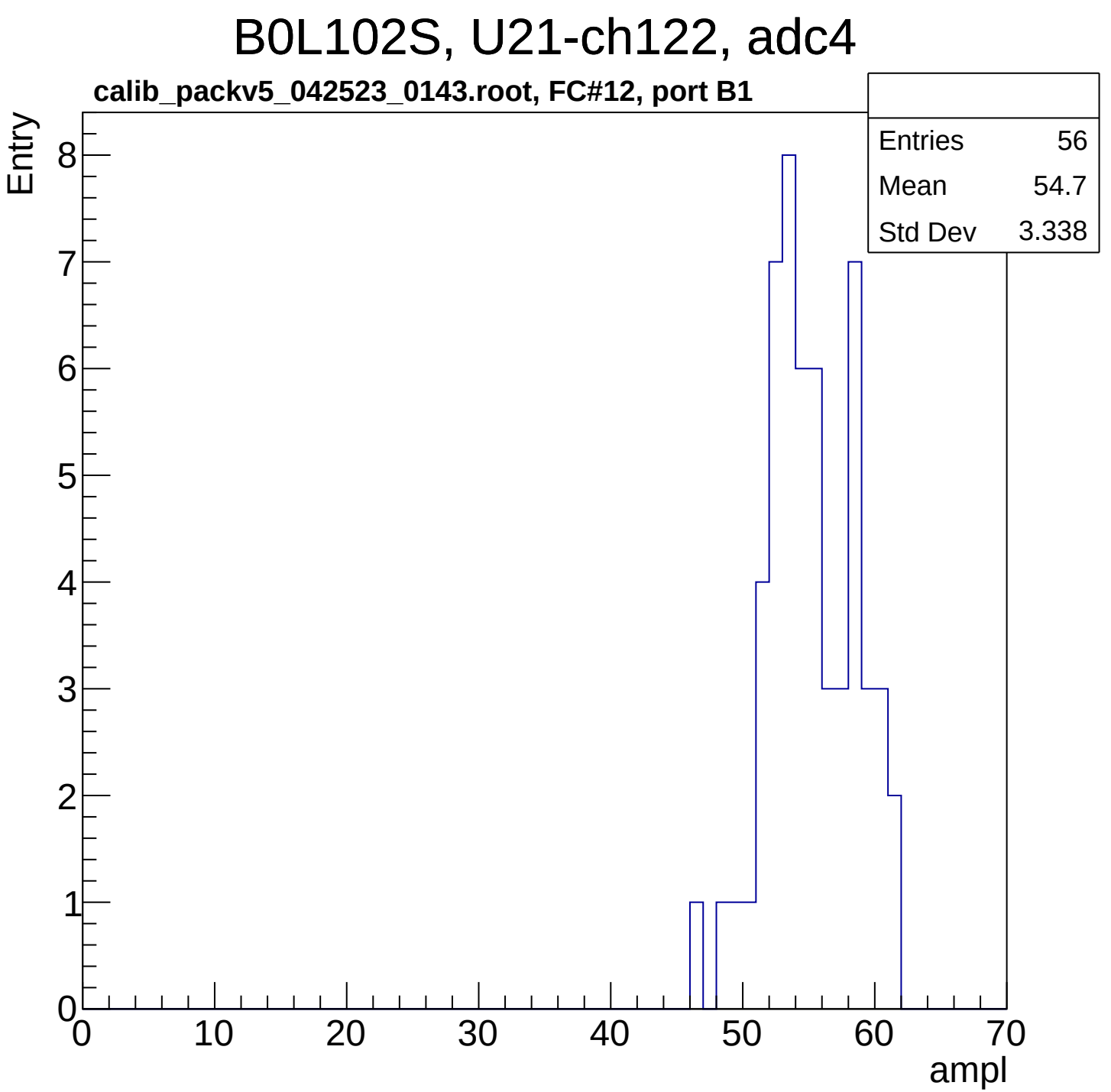
B0L102S, U21-ch122, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 54.7
Std Dev 3.338
Entry
ampl
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2
3
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5
6
7
8
0
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40
50
60
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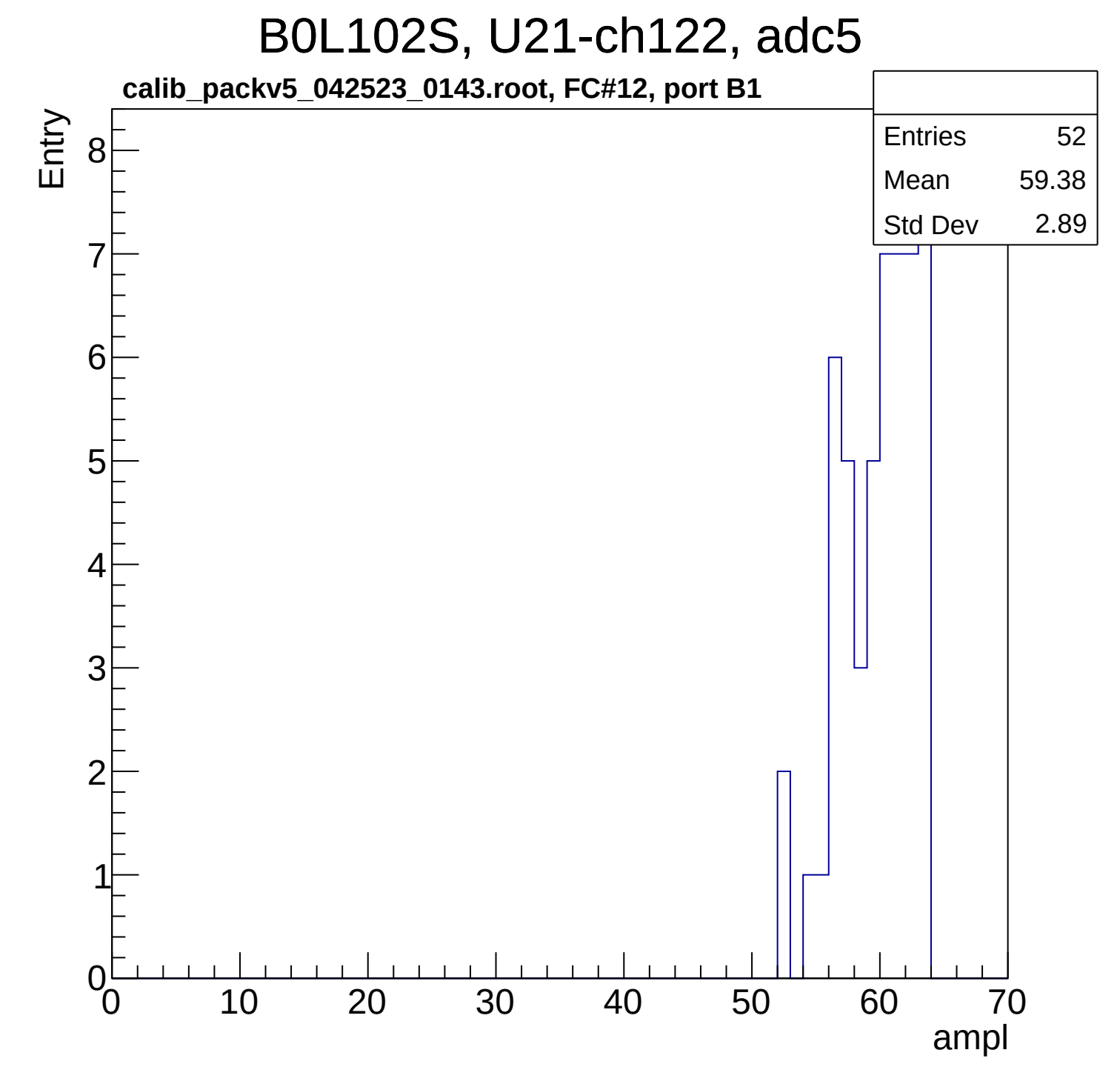
B0L102S, U21-ch122, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 59.38
Std Dev 2.89
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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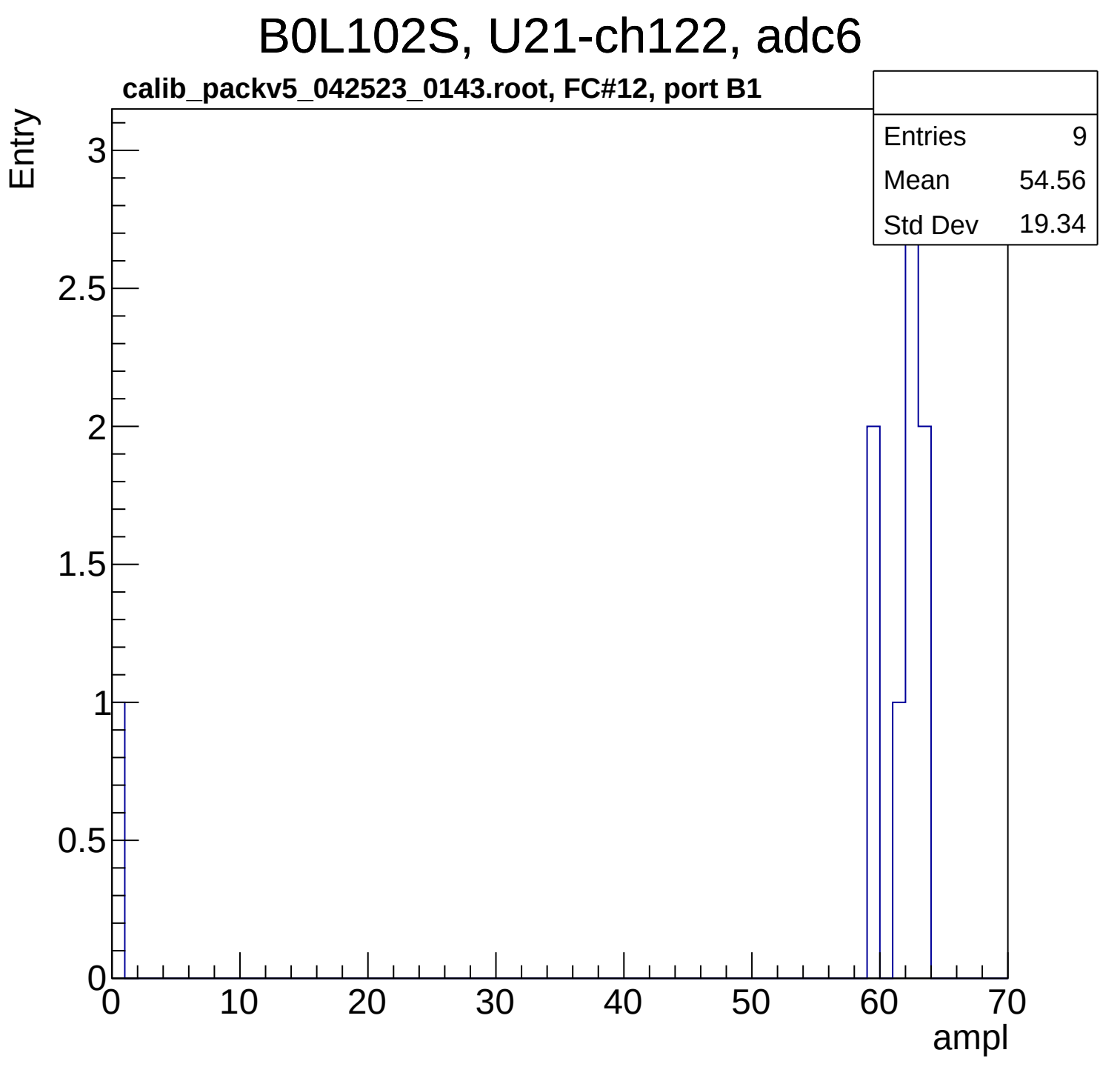
B0L102S, U21-ch122, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 54.56
Std Dev 19.34
Entry
ampl
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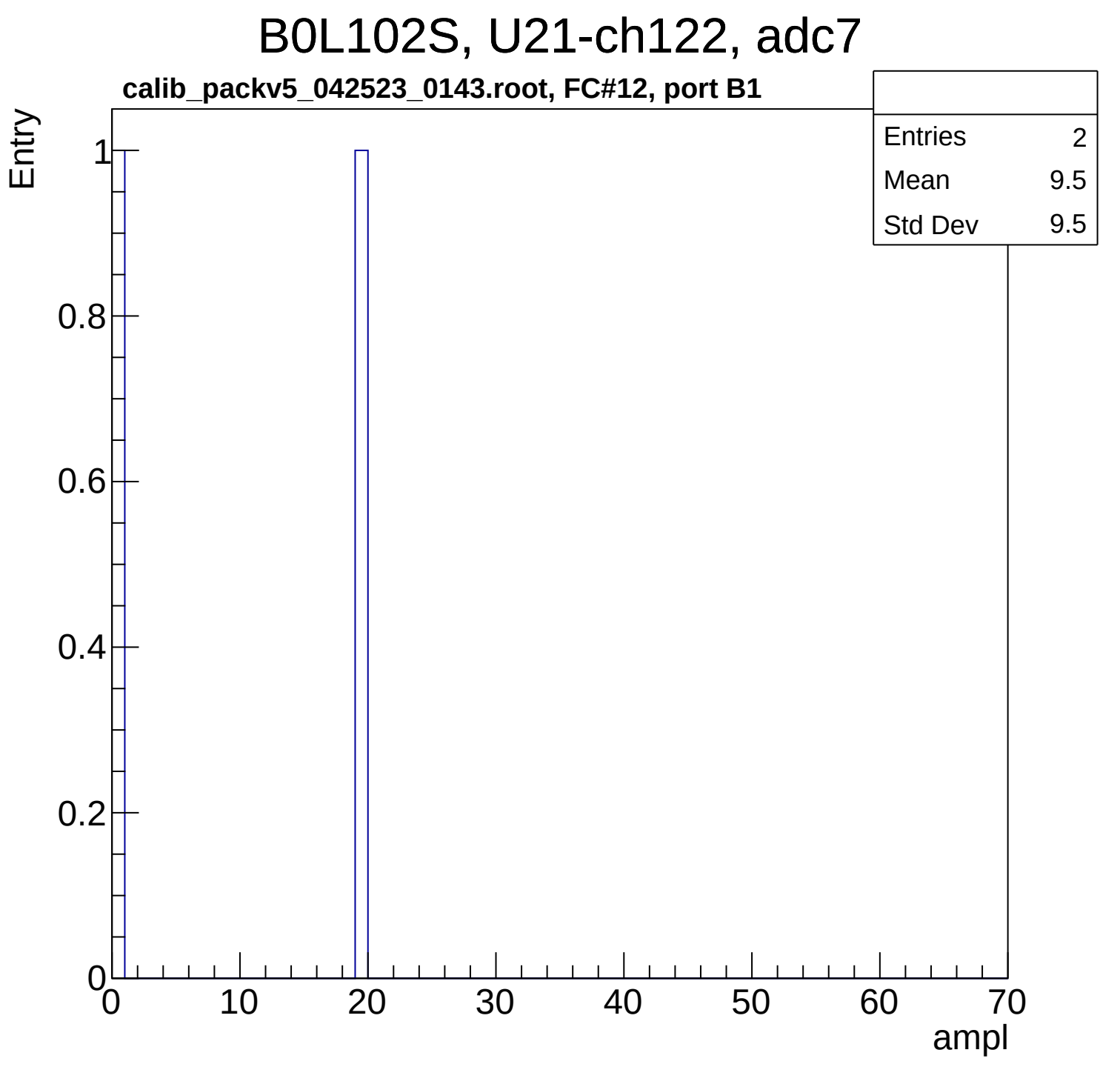
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U21-ch122, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
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0.6
0.4
0.2
0
0
10
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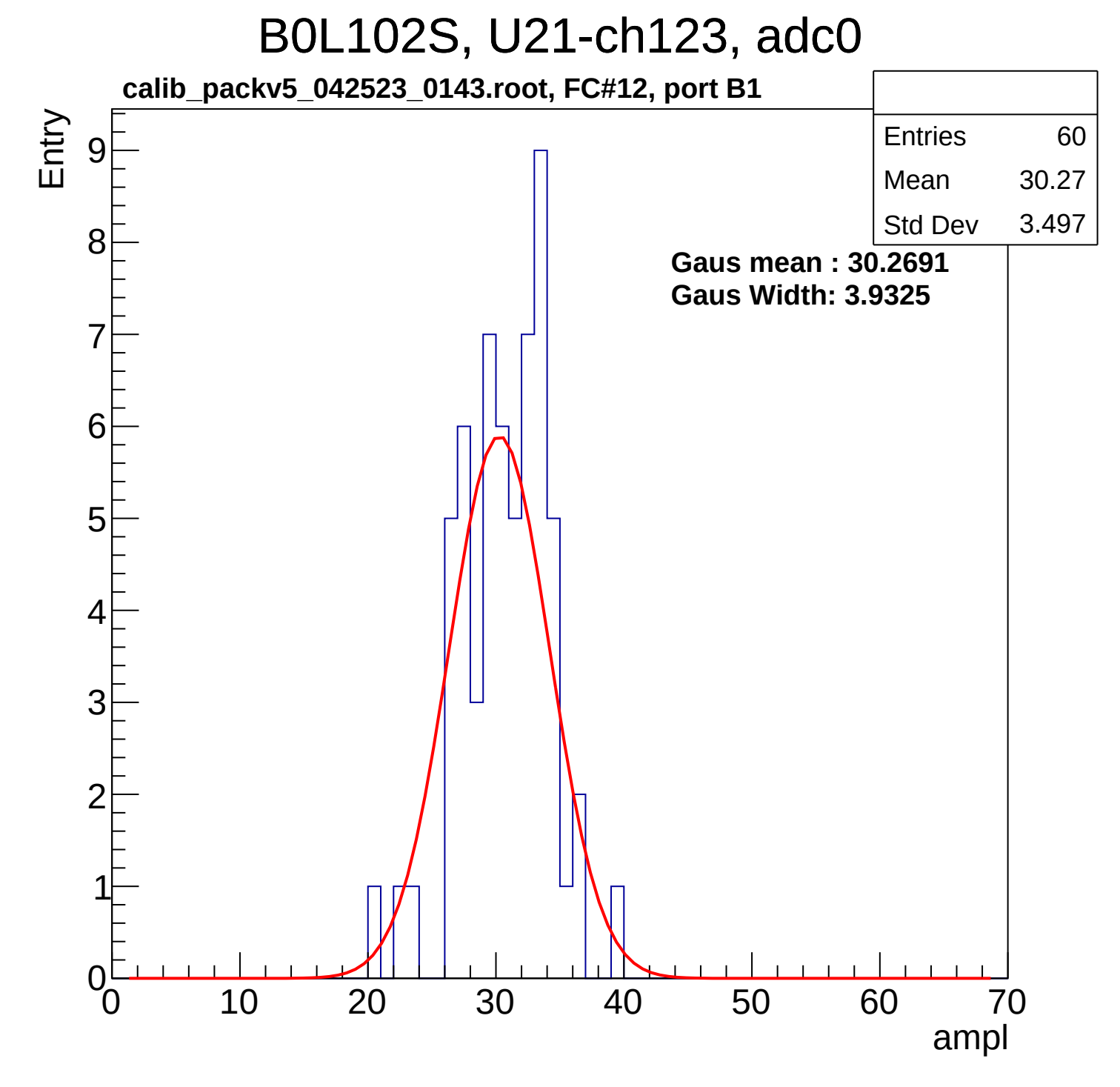
30
40
50
60
70
ampl


B0L102S, U21-ch123, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 30.27
Std Dev 3.497
Gaus mean : 30.2691
Gaus Width: 3.9325
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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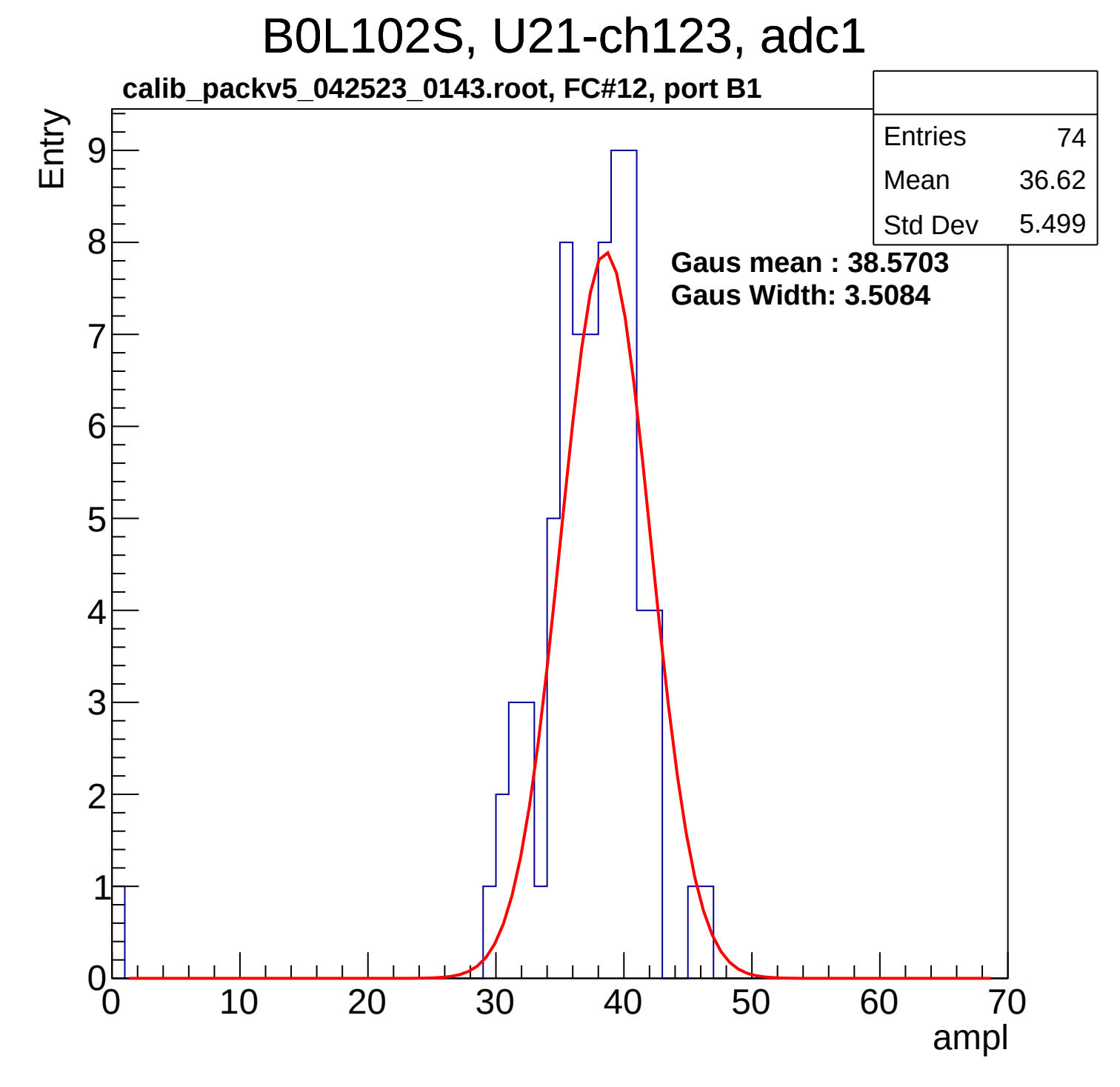
B0L102S, U21-ch123, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.62
Std Dev 5.499
Gaus mean : 38.5703
Gaus Width: 3.5084
Entry
ampl
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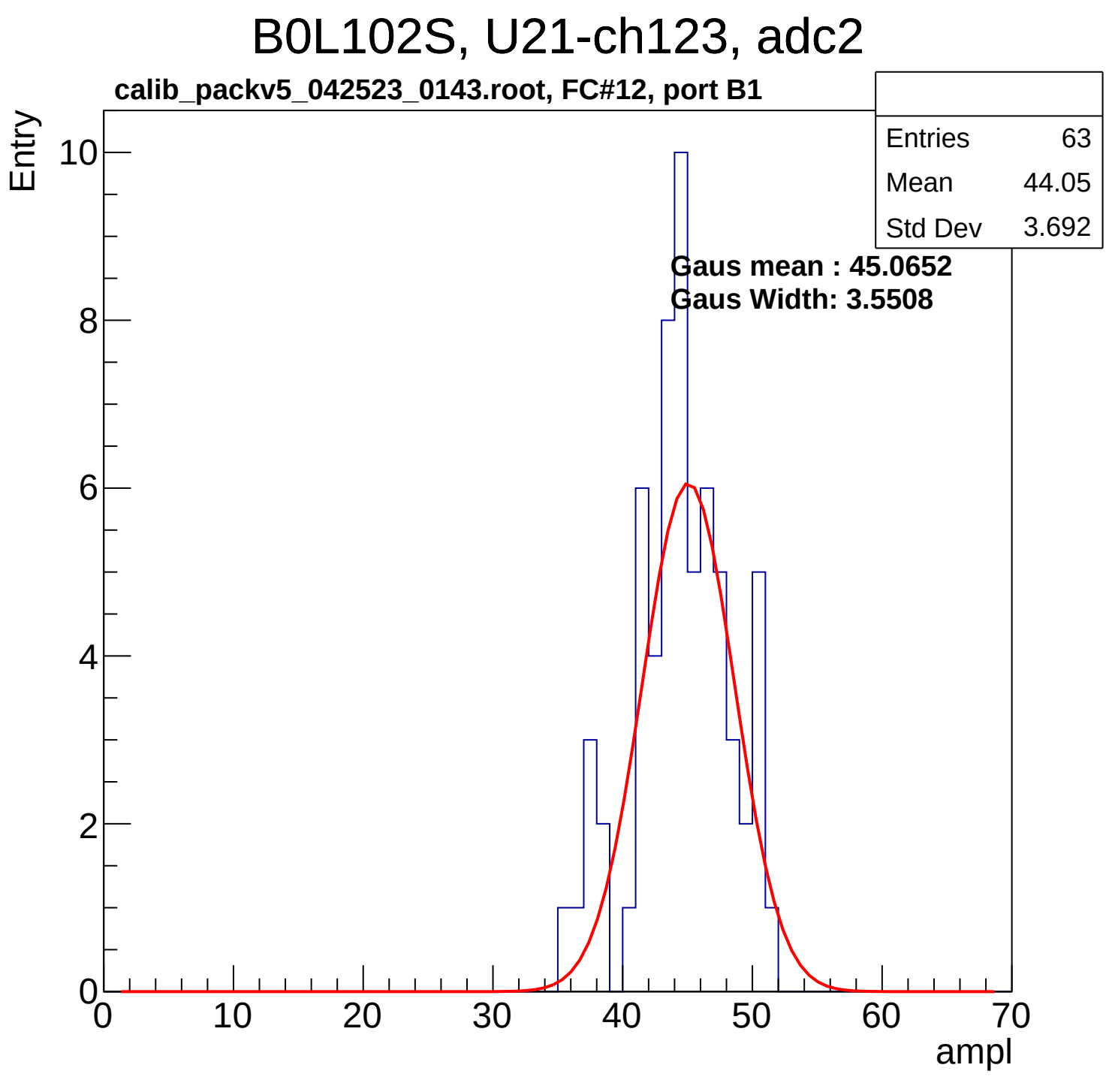
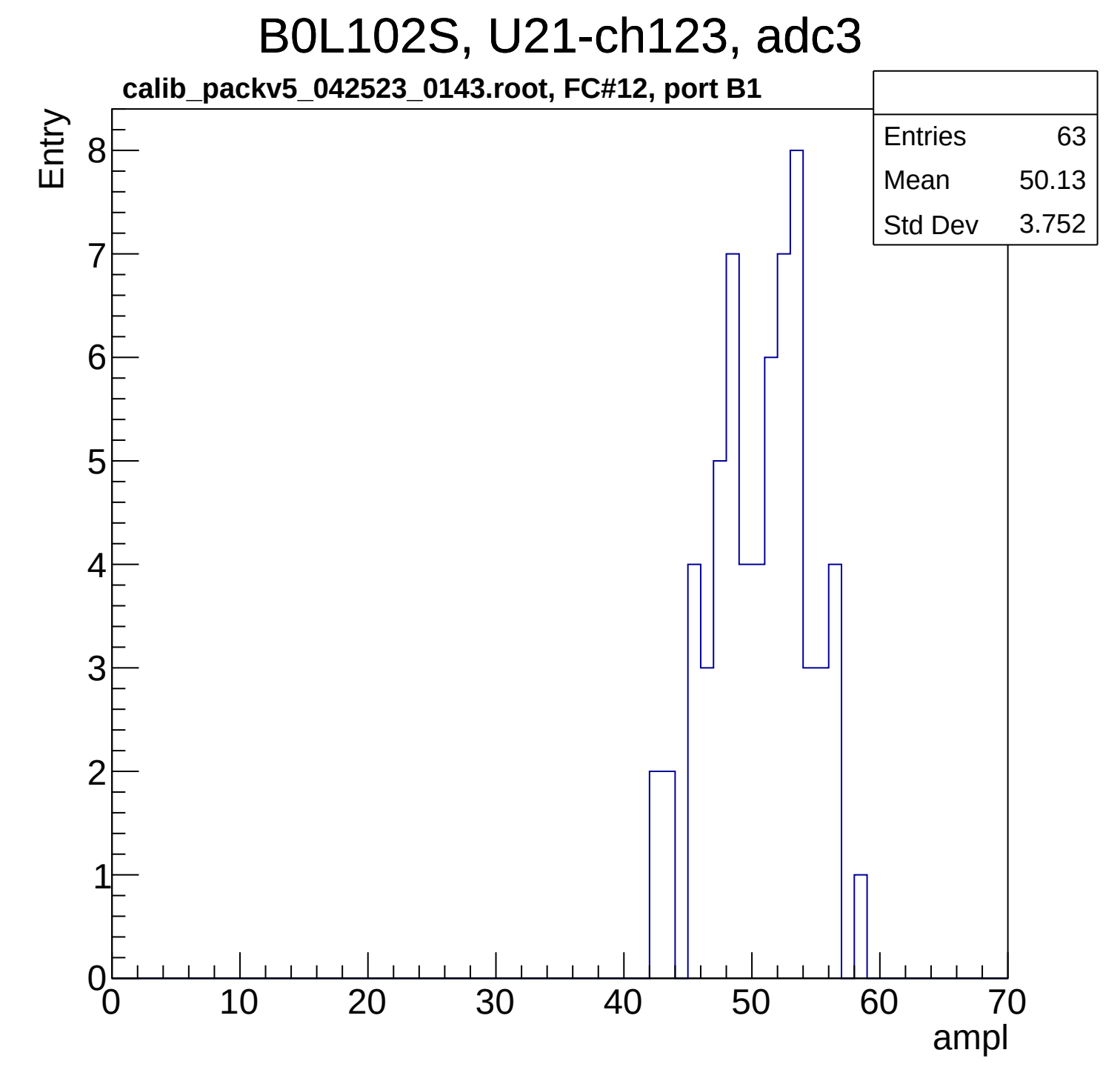
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch123, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 44.05
Std Dev 3.692
Gaus mean : 45.0652
Gaus Width: 3.5508
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch123, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 50.13
Std Dev 3.752
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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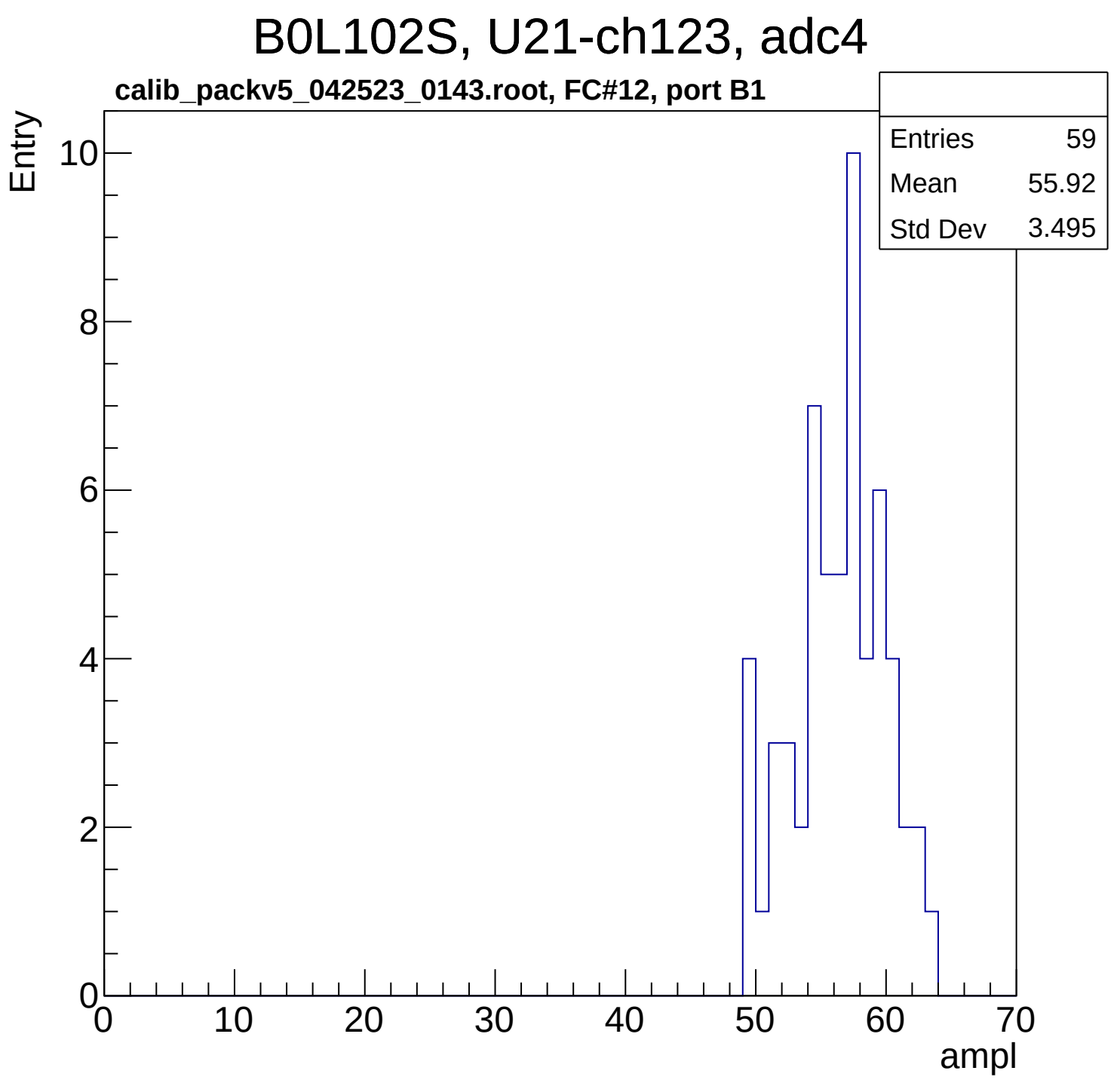
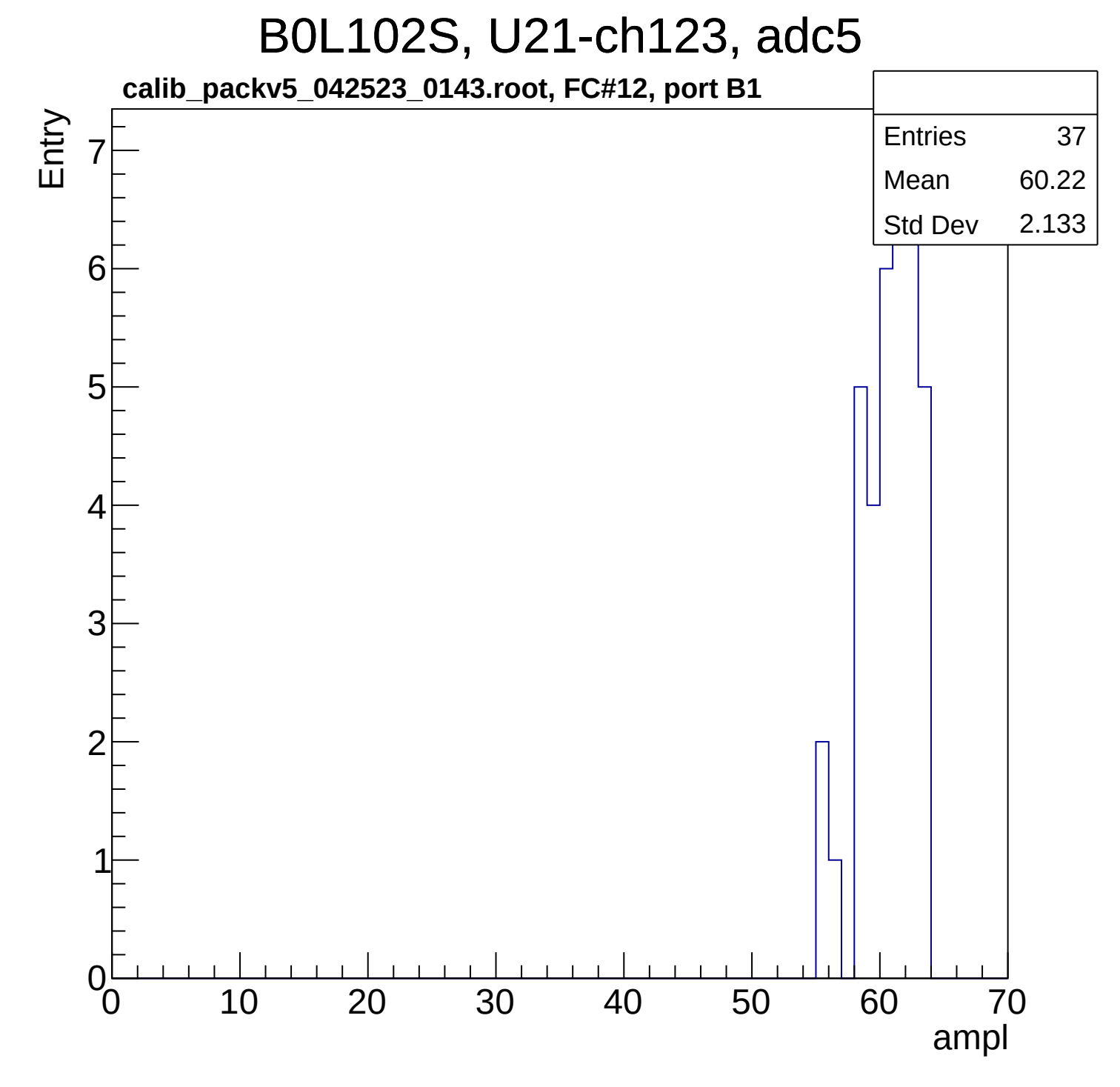

B0L102S, U21-ch123, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.92
Std Dev 3.495
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch123, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 60.22
Std Dev 2.133
Entry
ampl
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1
2
3
4
5
6
7
0
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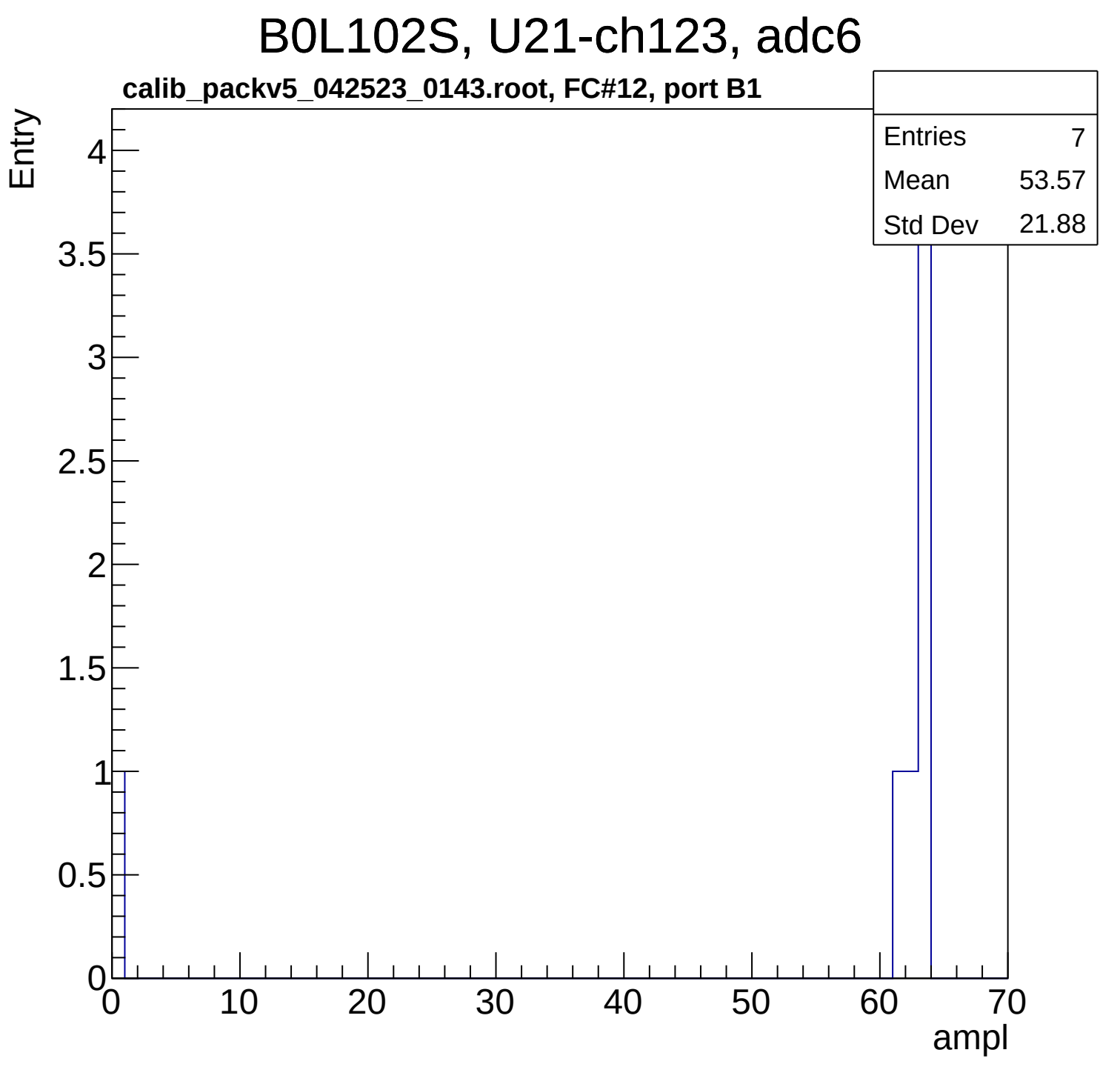
20
30
40
50
60
70


B0L102S, U21-ch123, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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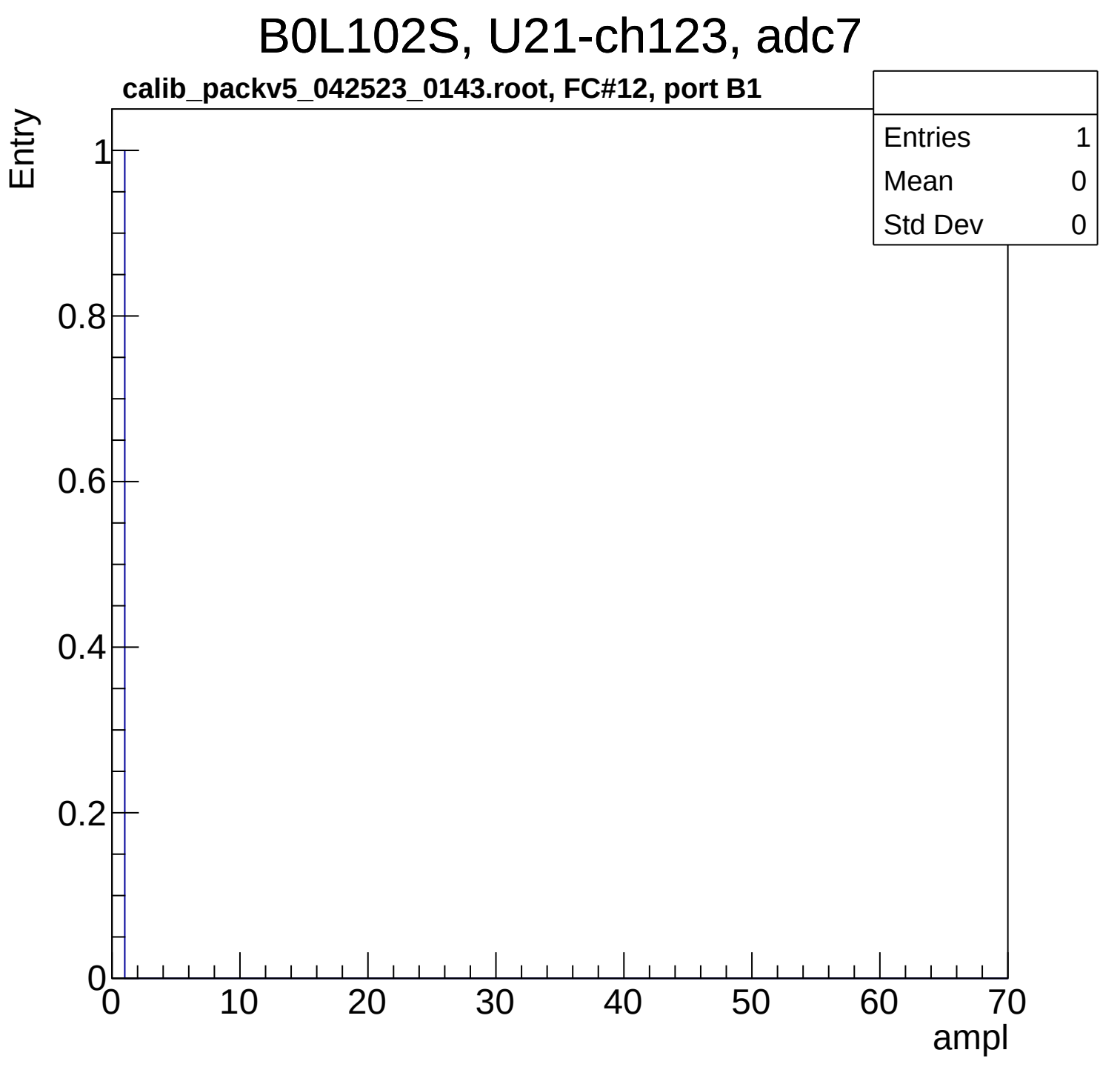
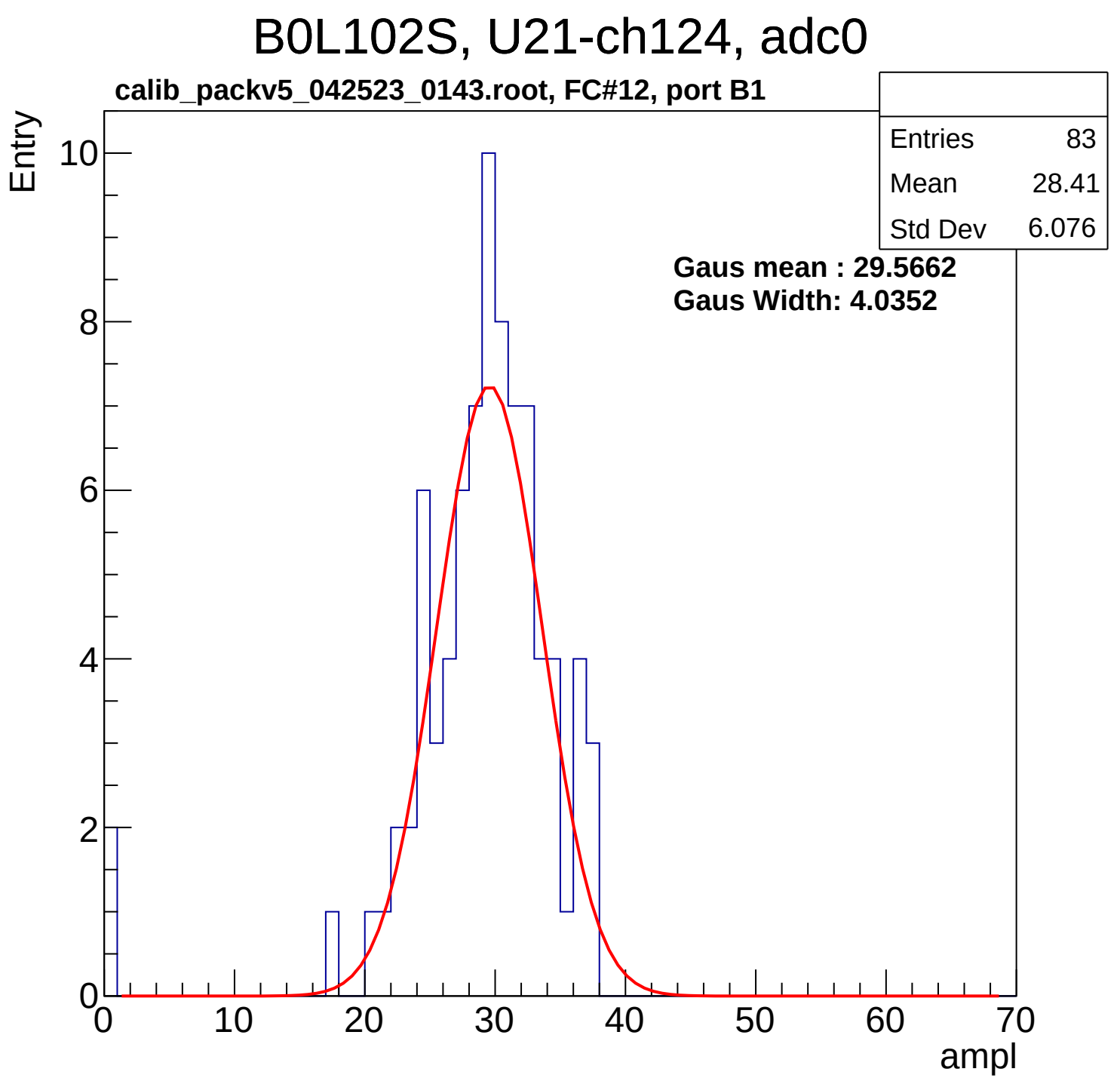
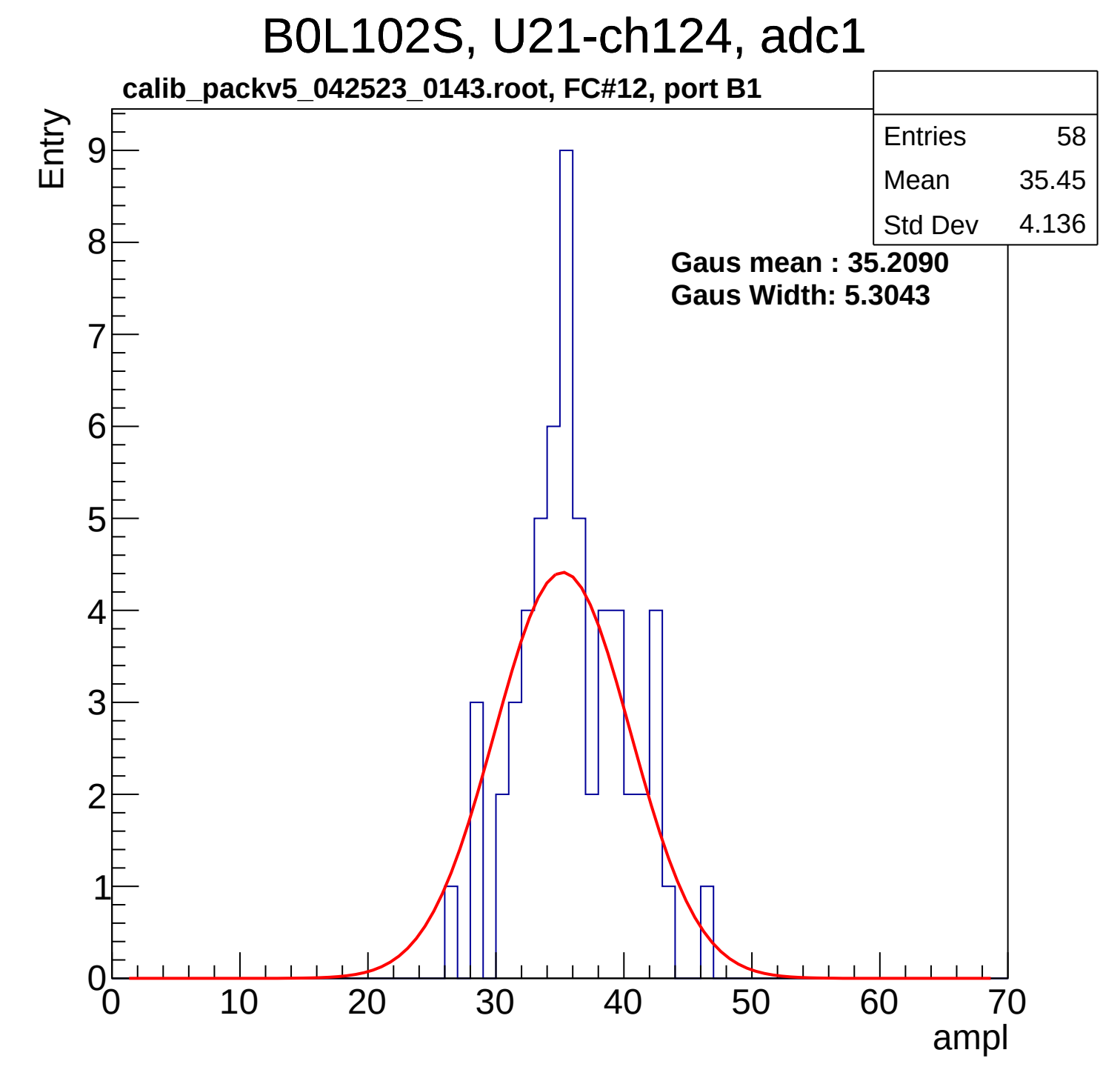
40
50
60
70


B0L102S, U21-ch123, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch124, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 28.41
Std Dev 6.076
Gaus mean : 29.5662
Gaus Width: 4.0352
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch124, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 35.45
Std Dev 4.136
Gaus mean : 35.2090
Gaus Width: 5.3043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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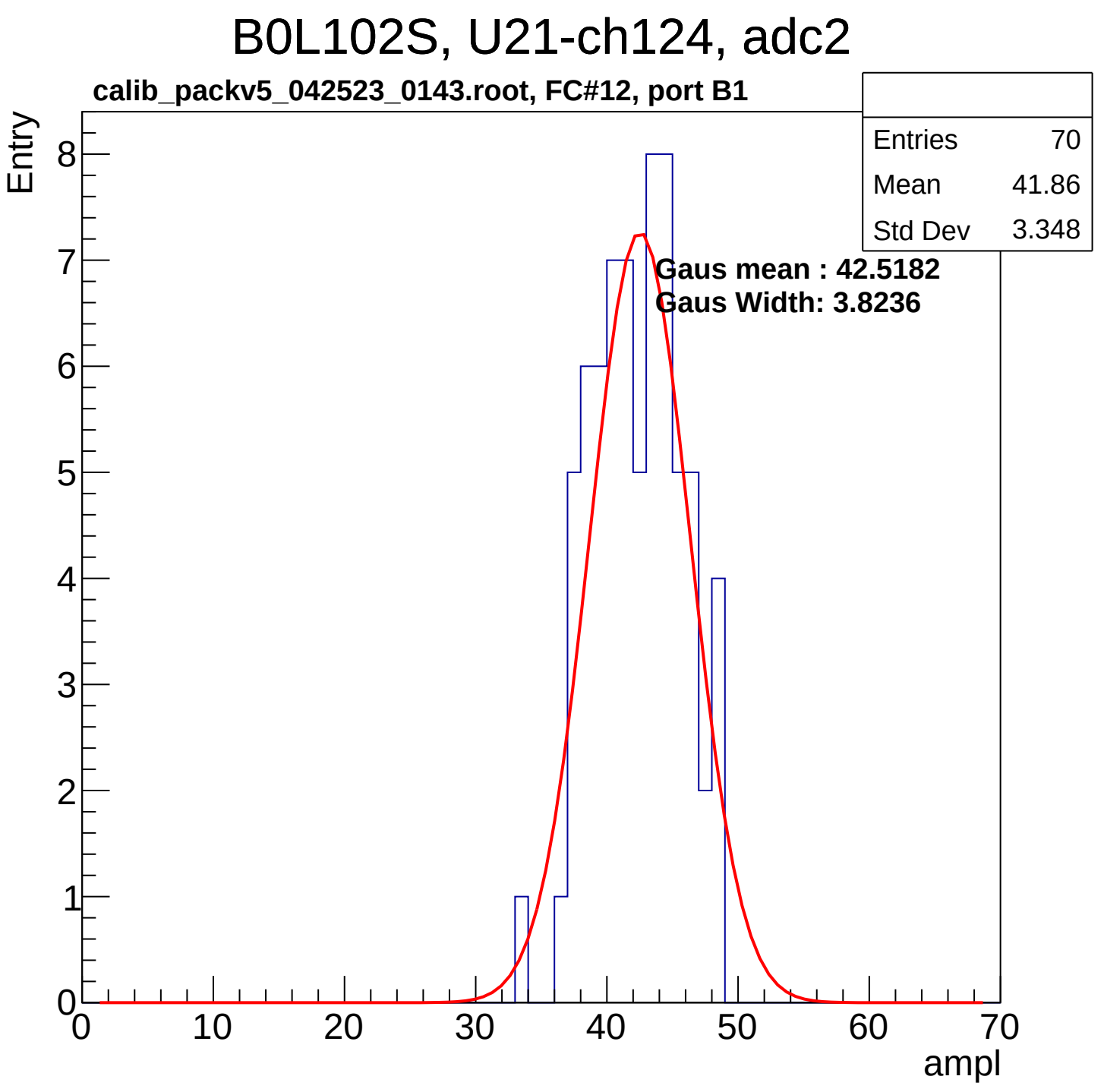
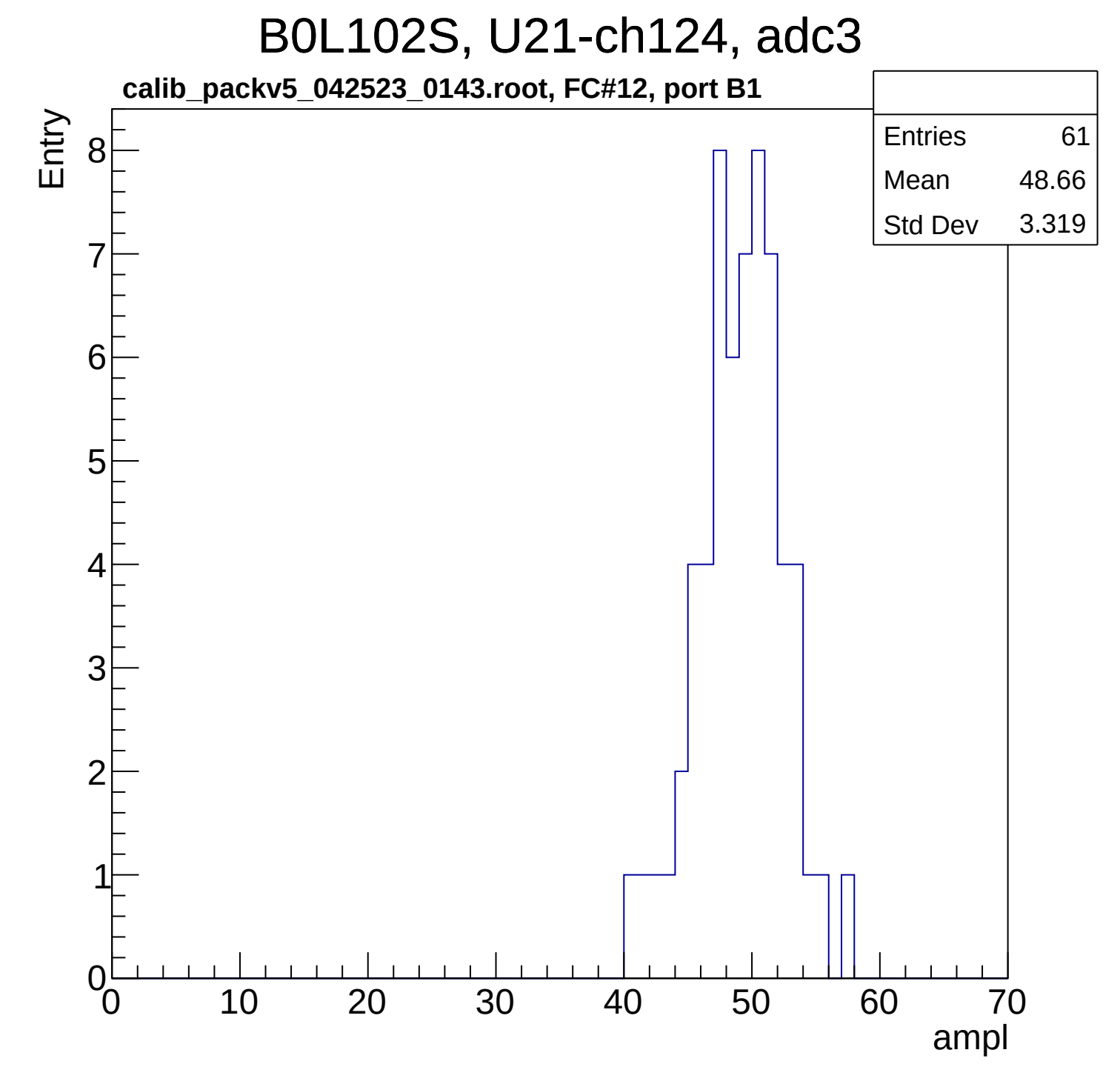

B0L102S, U21-ch124, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 41.86
Std Dev 3.348
Gaus mean : 42.5182
Gaus Width: 3.8236
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch124, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 48.66
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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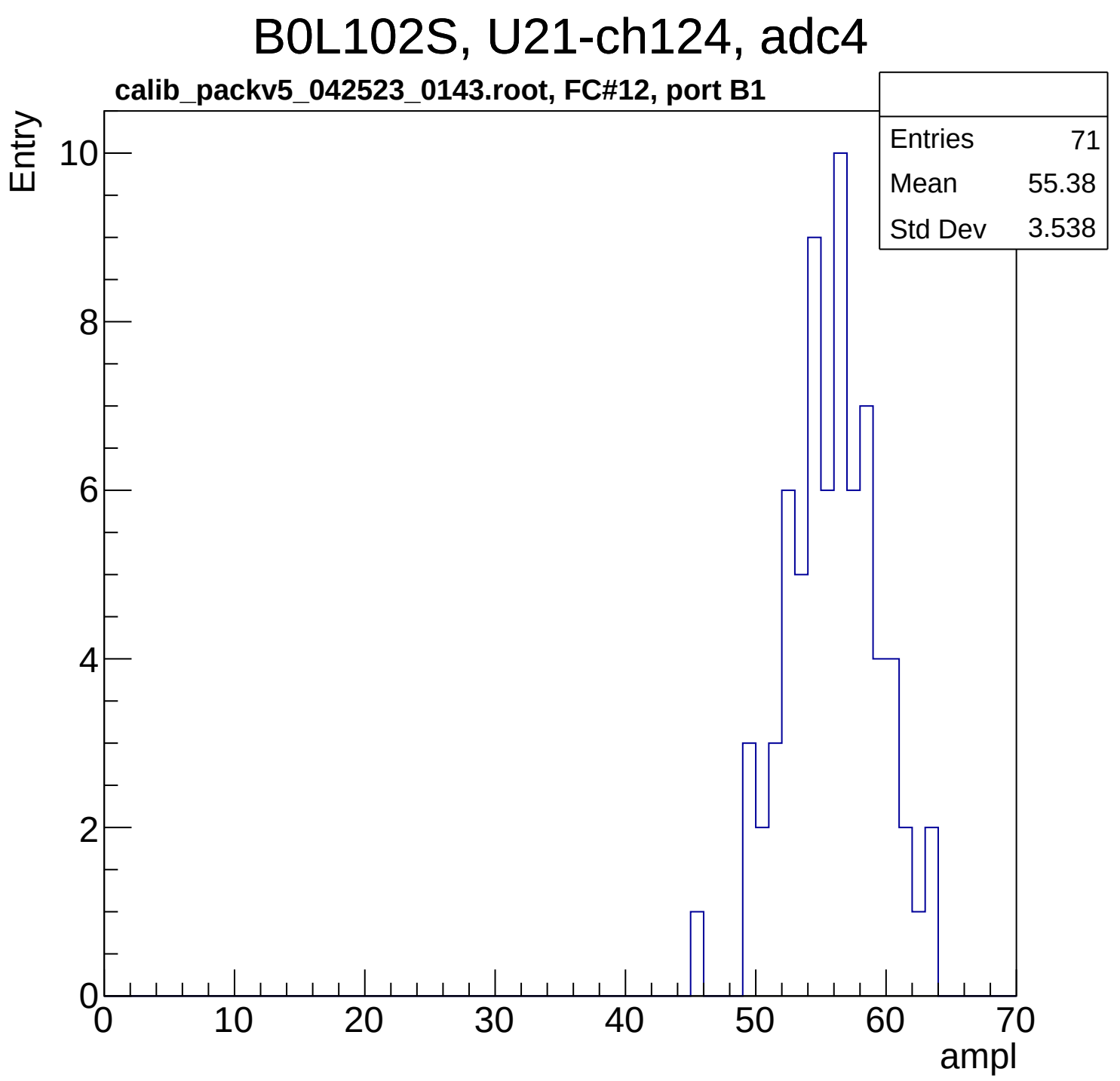
B0L102S, U21-ch124, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 55.38
Std Dev 3.538
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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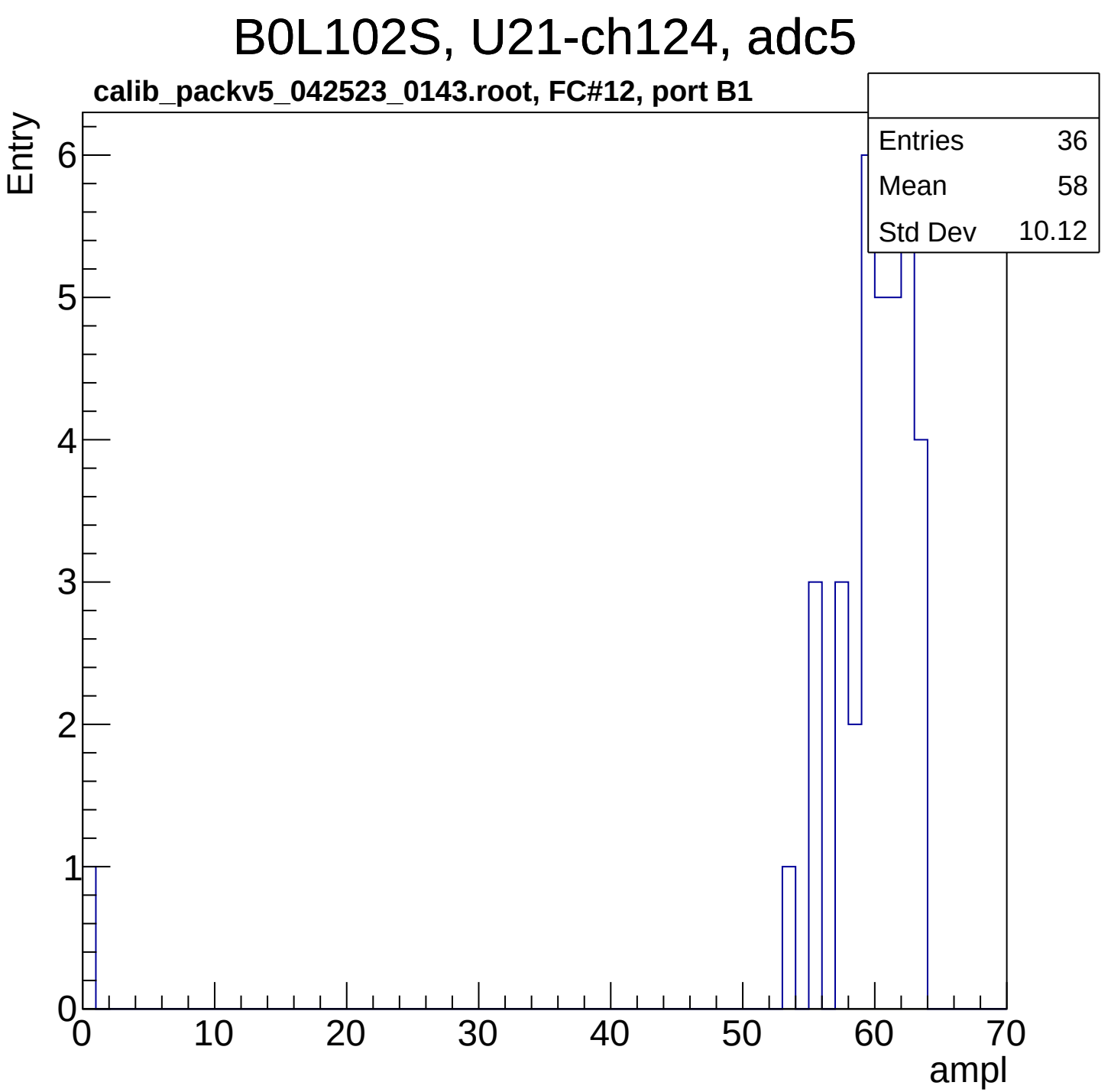
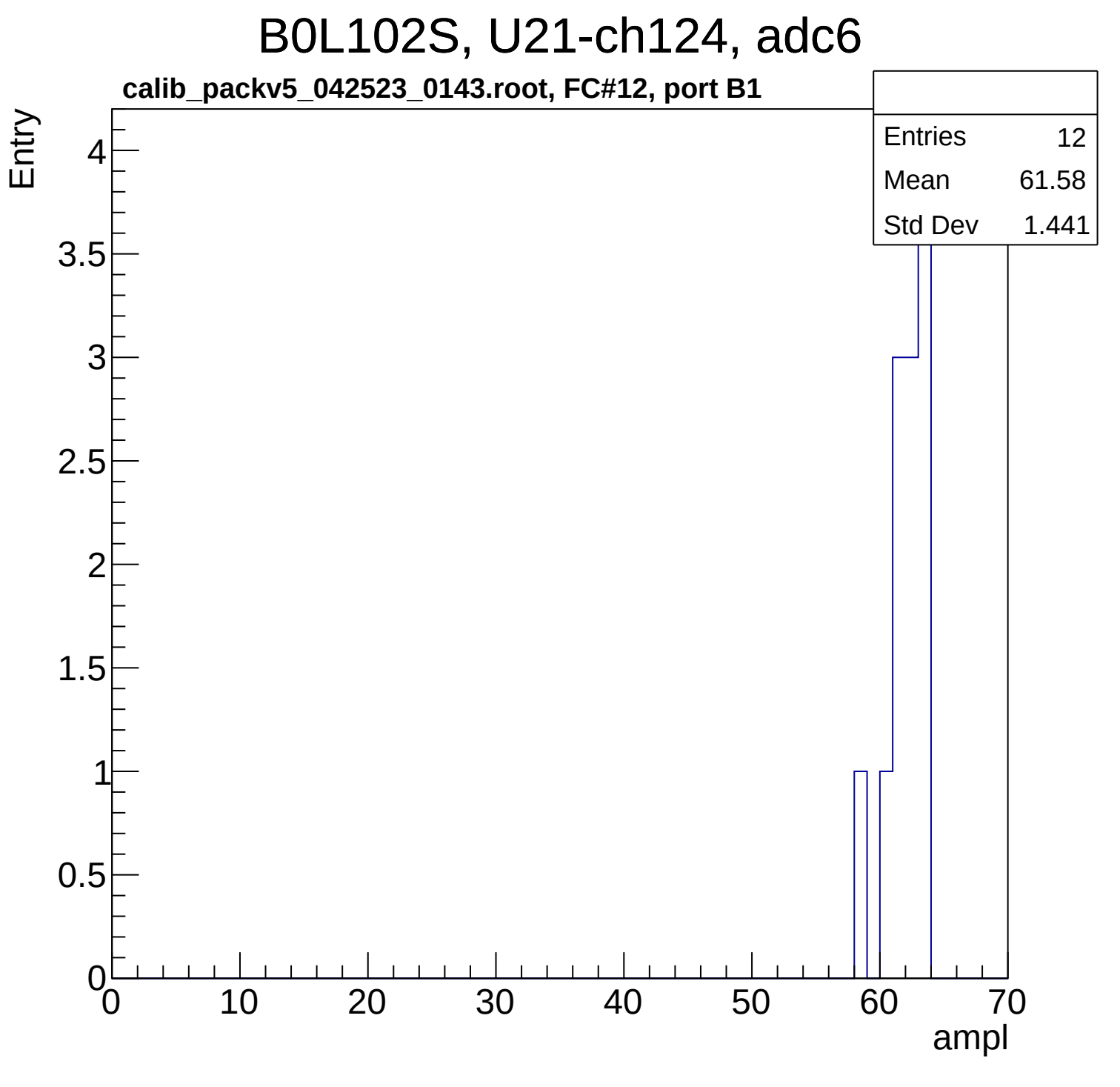
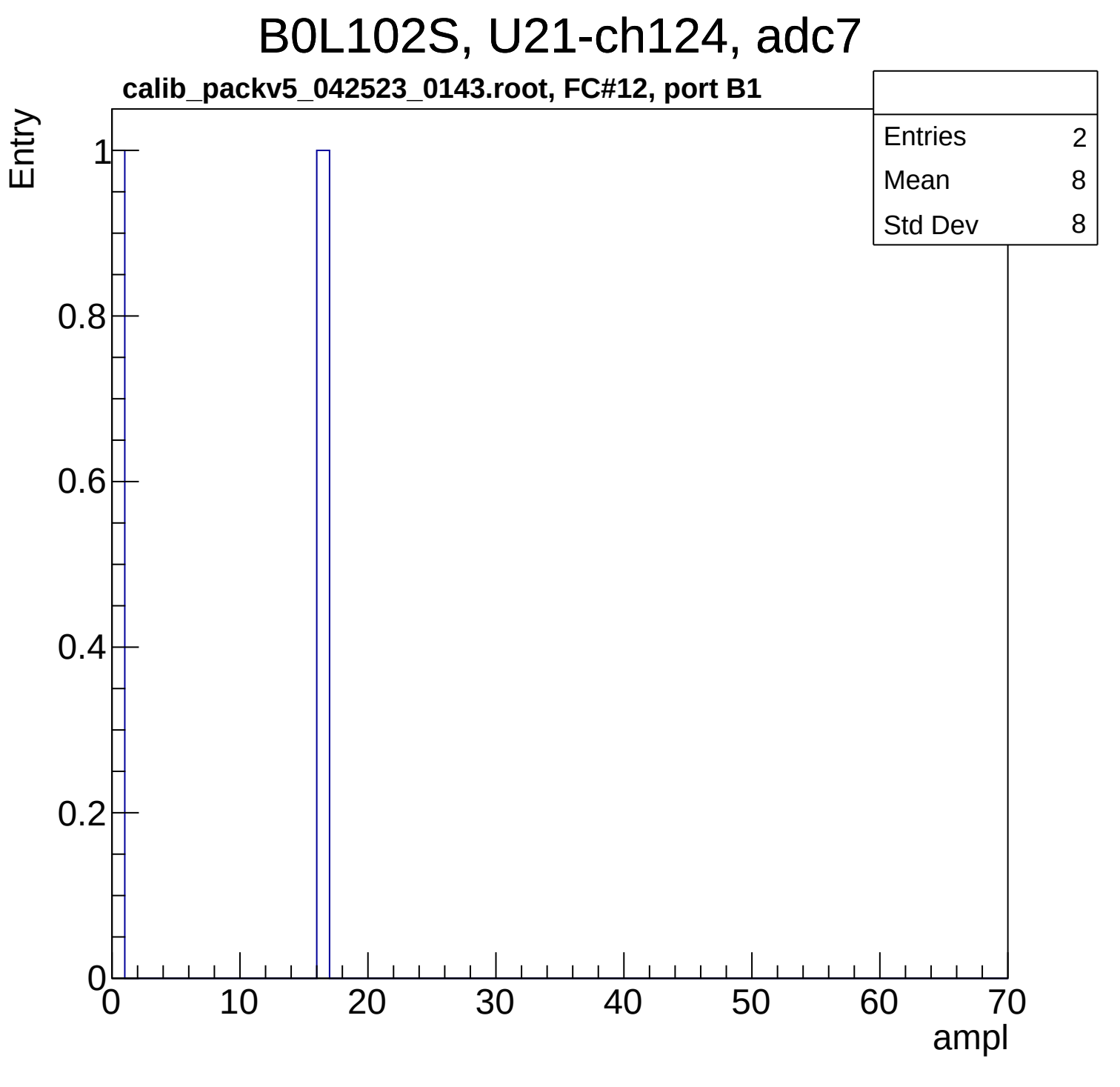
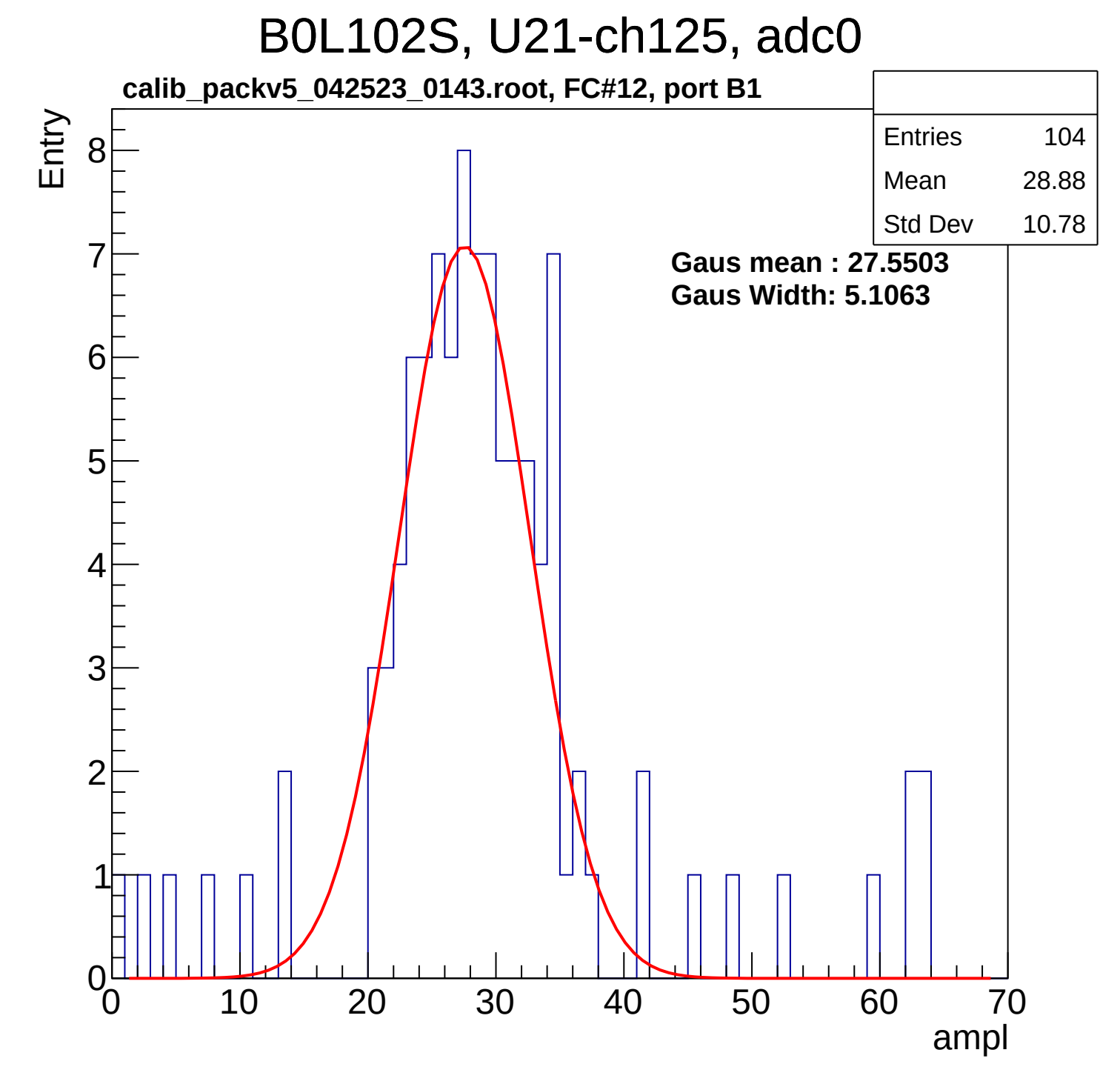
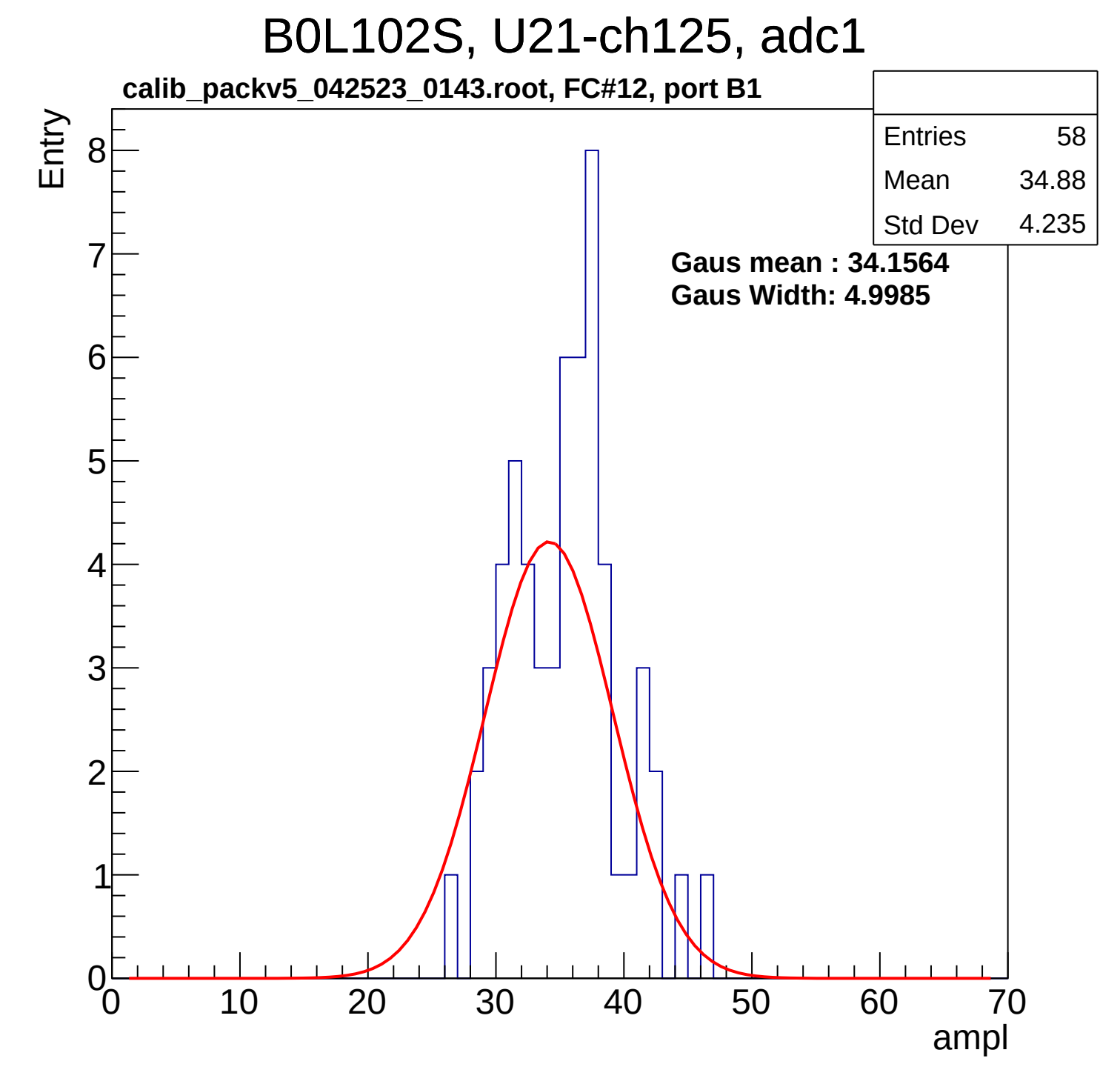
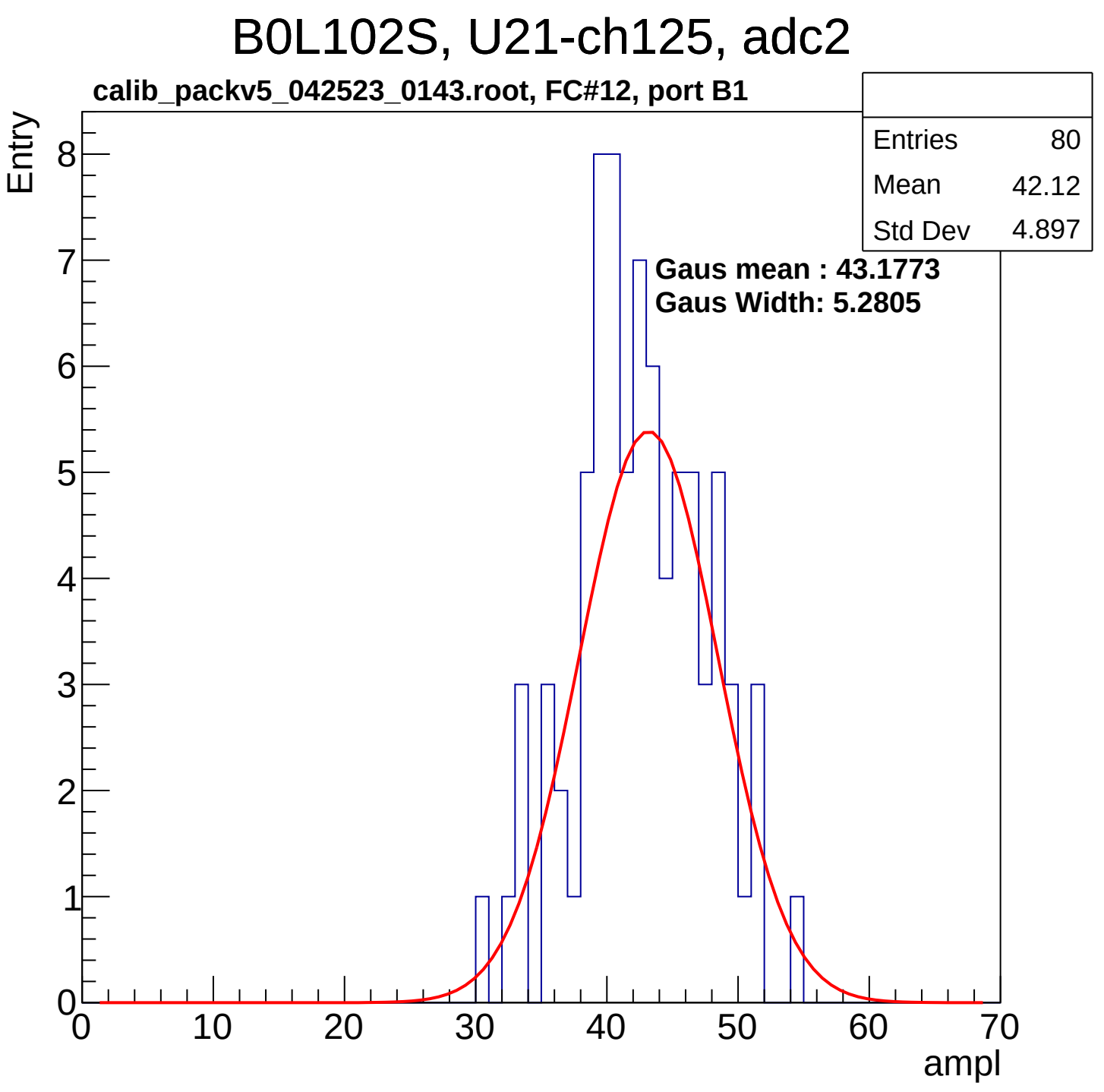

B0L102S, U21-ch124, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58
Std Dev 10.12
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch124, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 61.58
Std Dev 1.441
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch124, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 8
Std Dev 8
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch125, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 104
Mean 28.88
Std Dev 10.78
Gaus mean : 27.5503
Gaus Width: 5.1063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch125, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 34.88
Std Dev 4.235
Gaus mean : 34.1564
Gaus Width: 4.9985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch125, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 42.12
Std Dev 4.897
Gaus mean : 43.1773
Gaus Width: 5.2805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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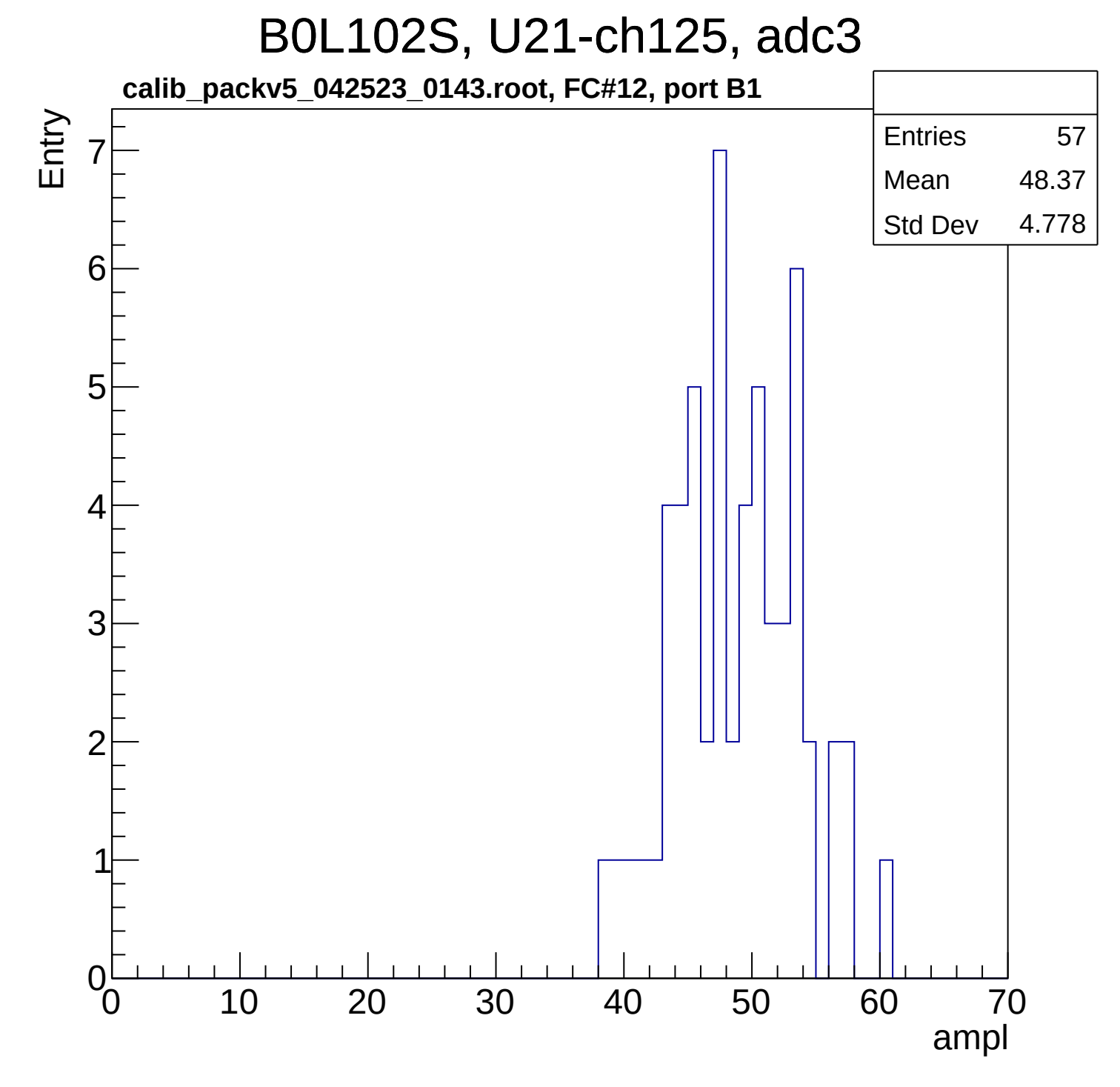
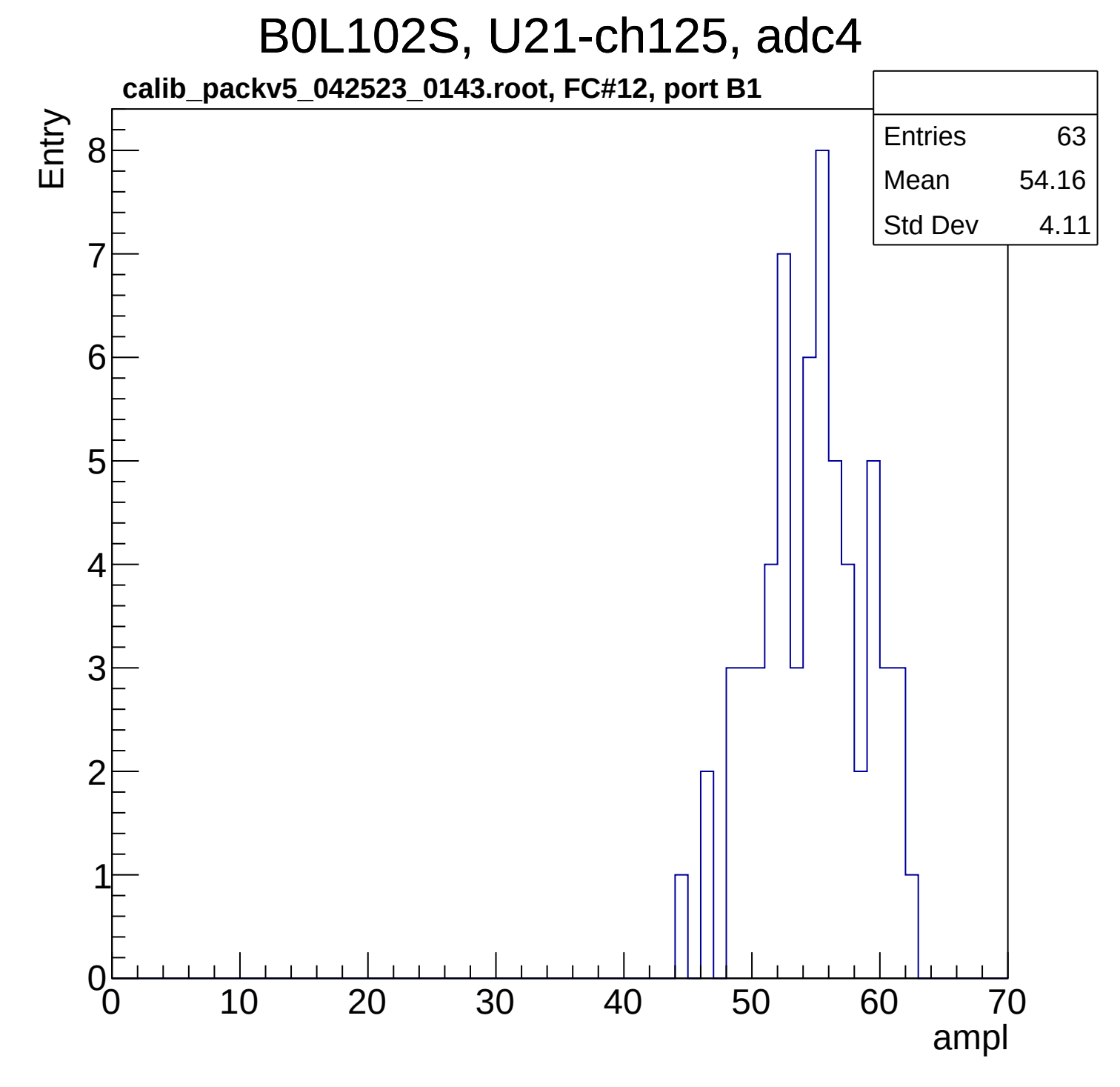
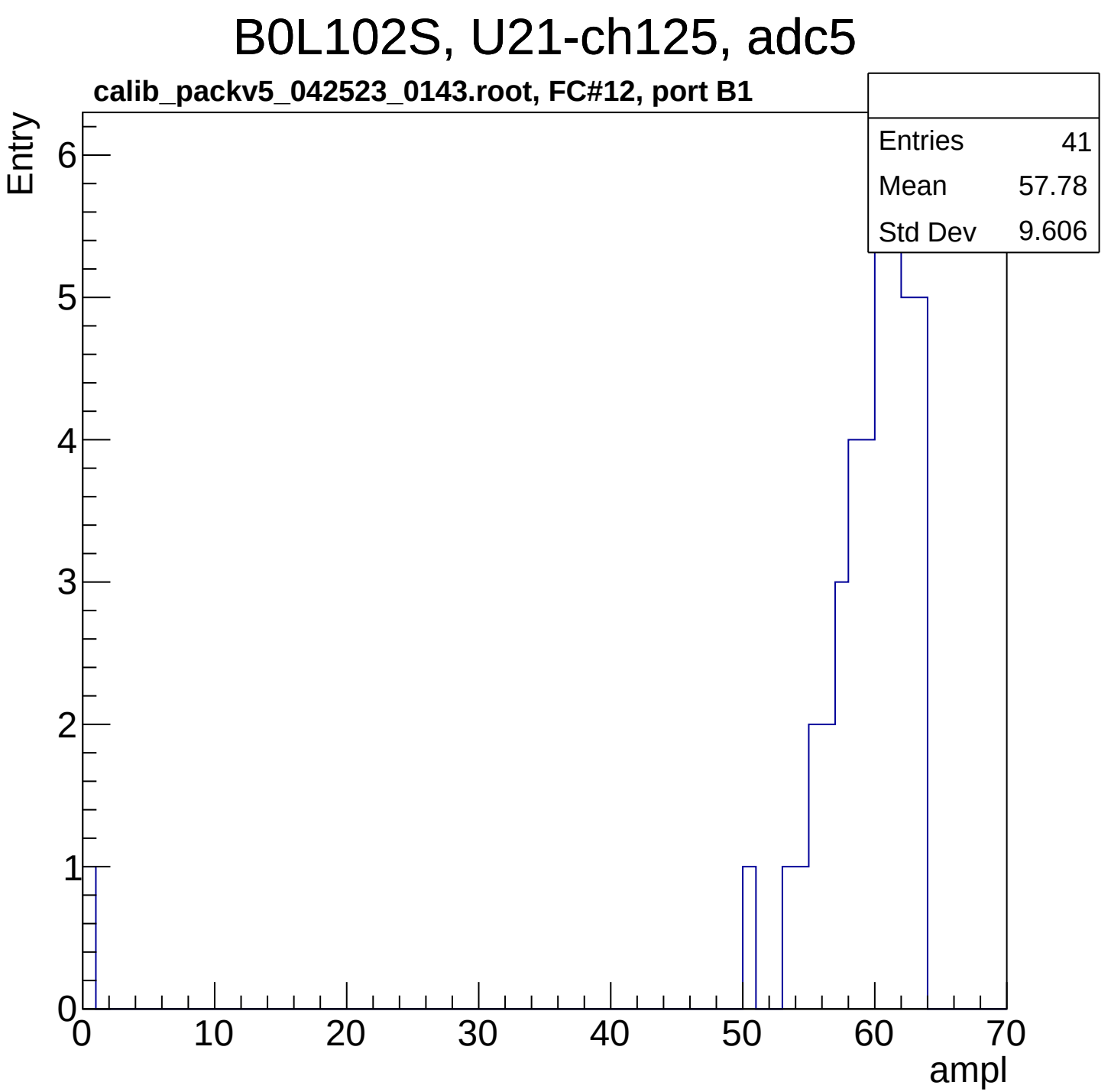
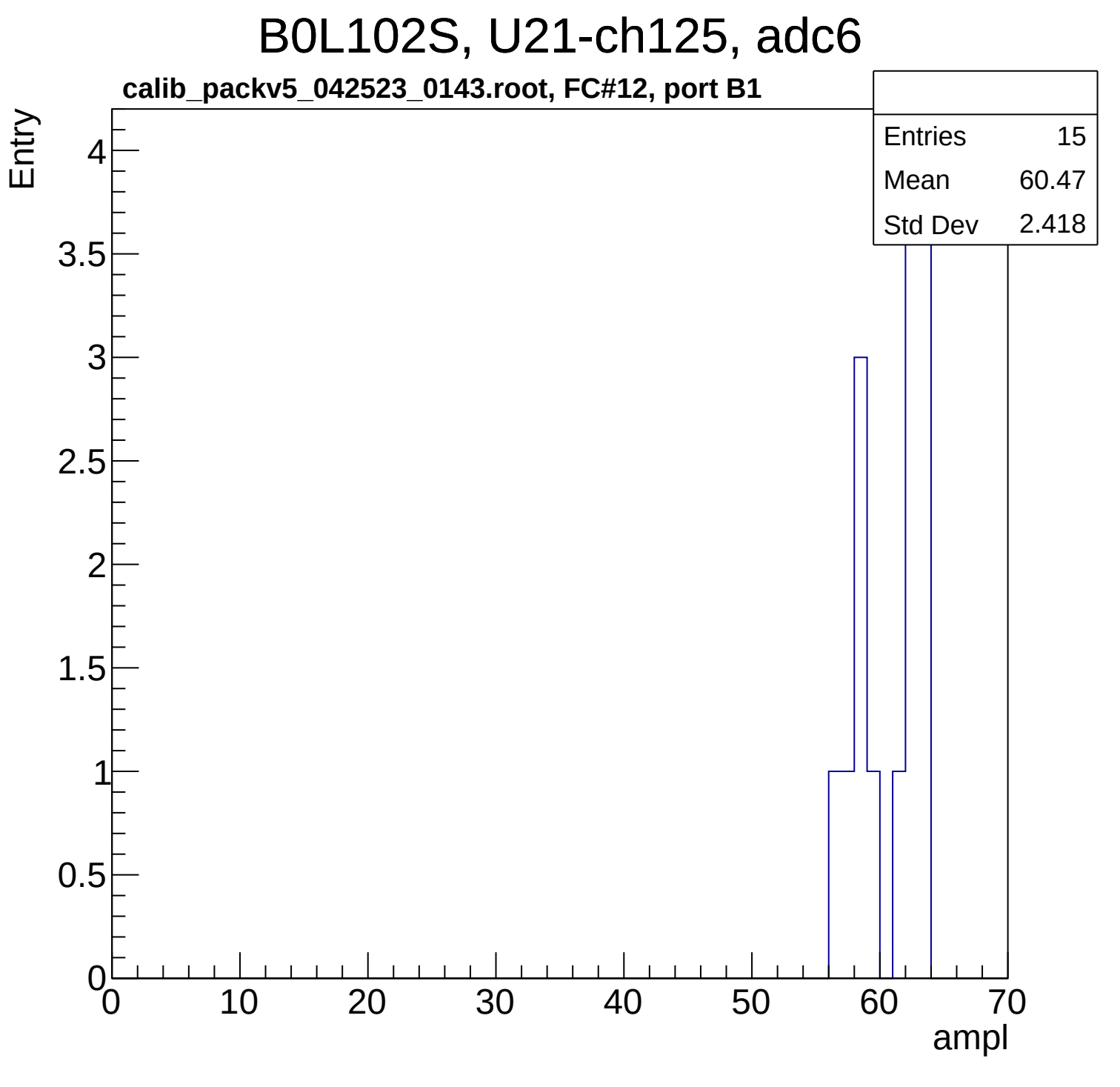
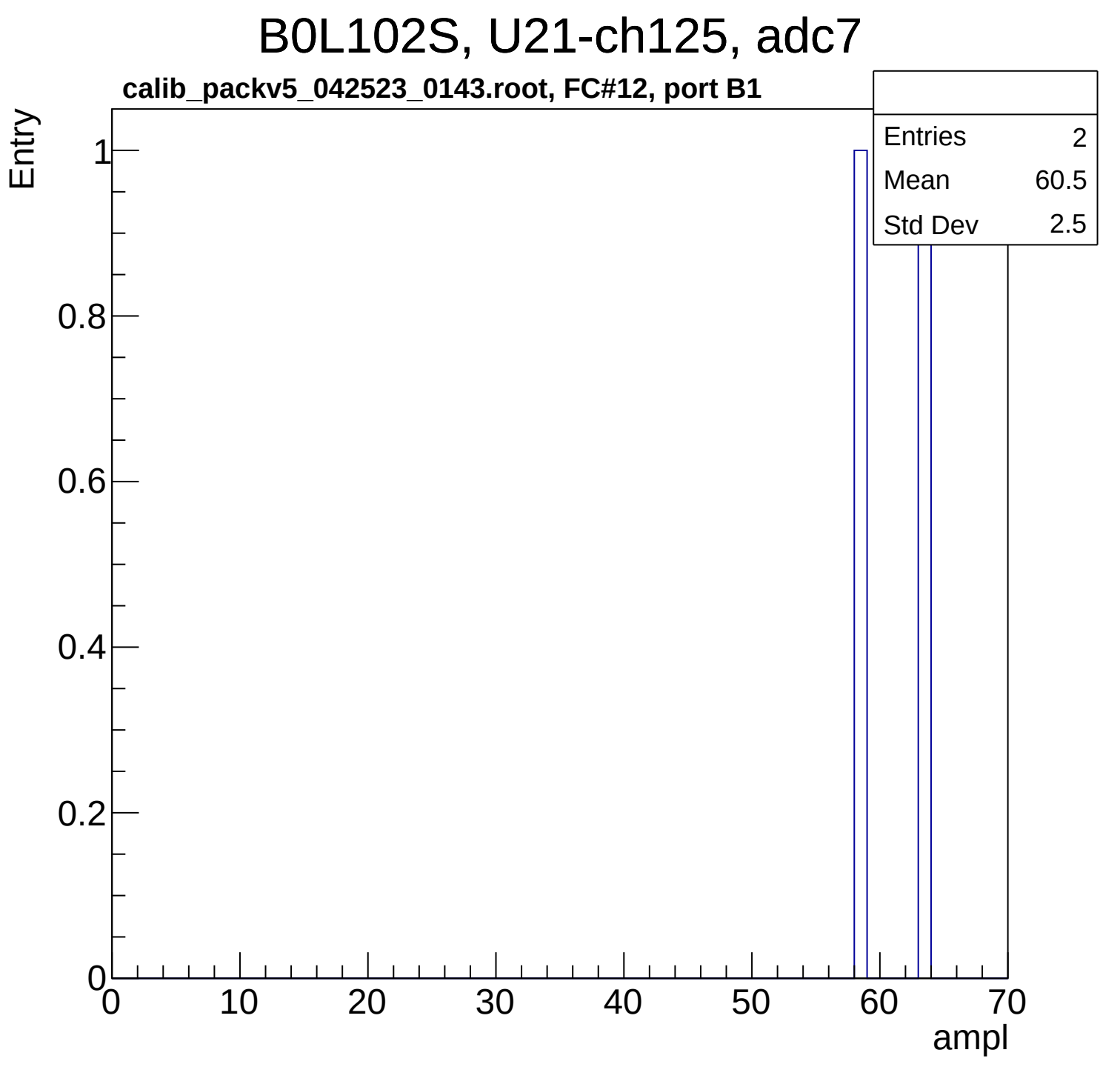
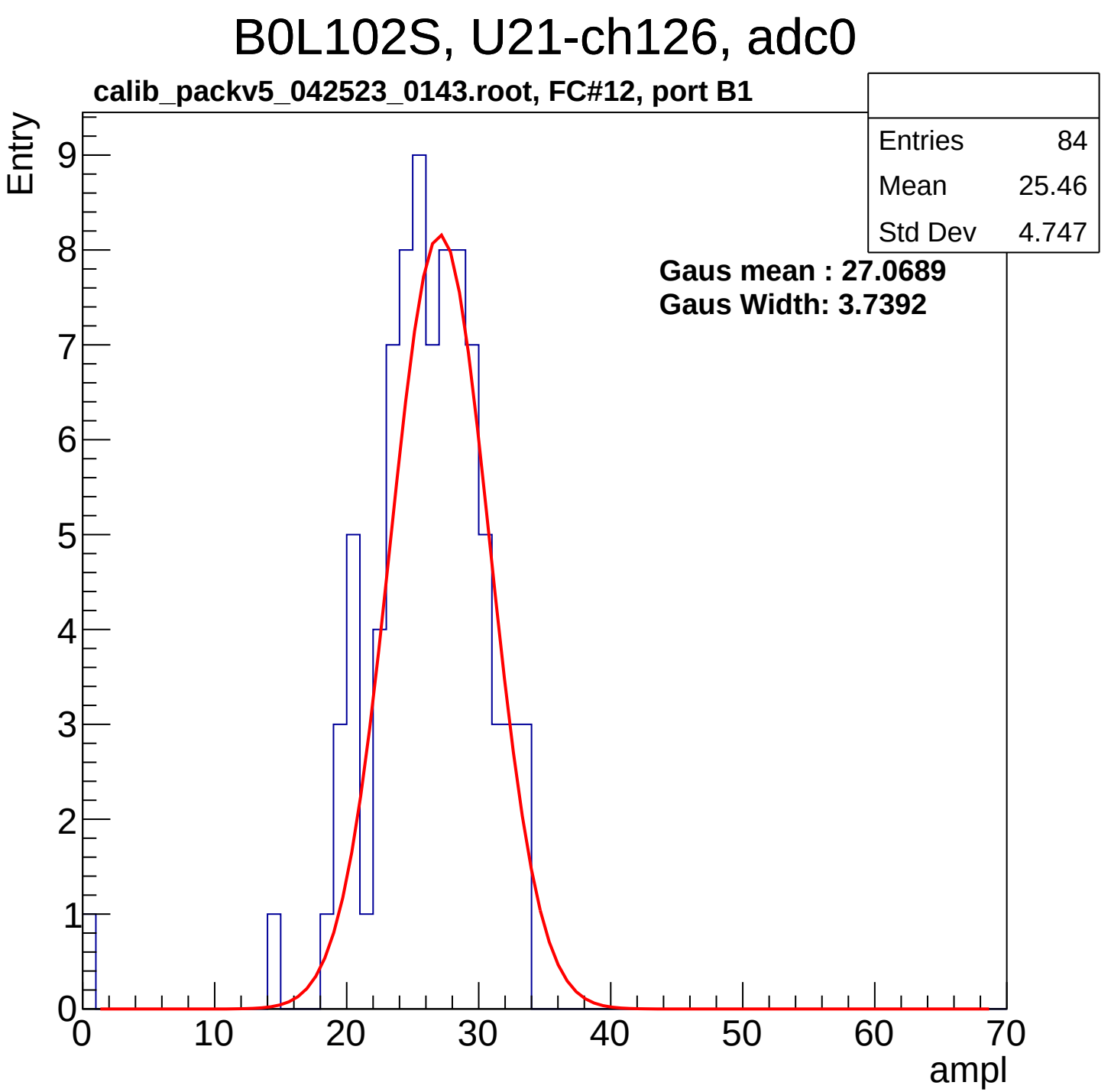

B0L102S, U21-ch125, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 48.37
Std Dev 4.778
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

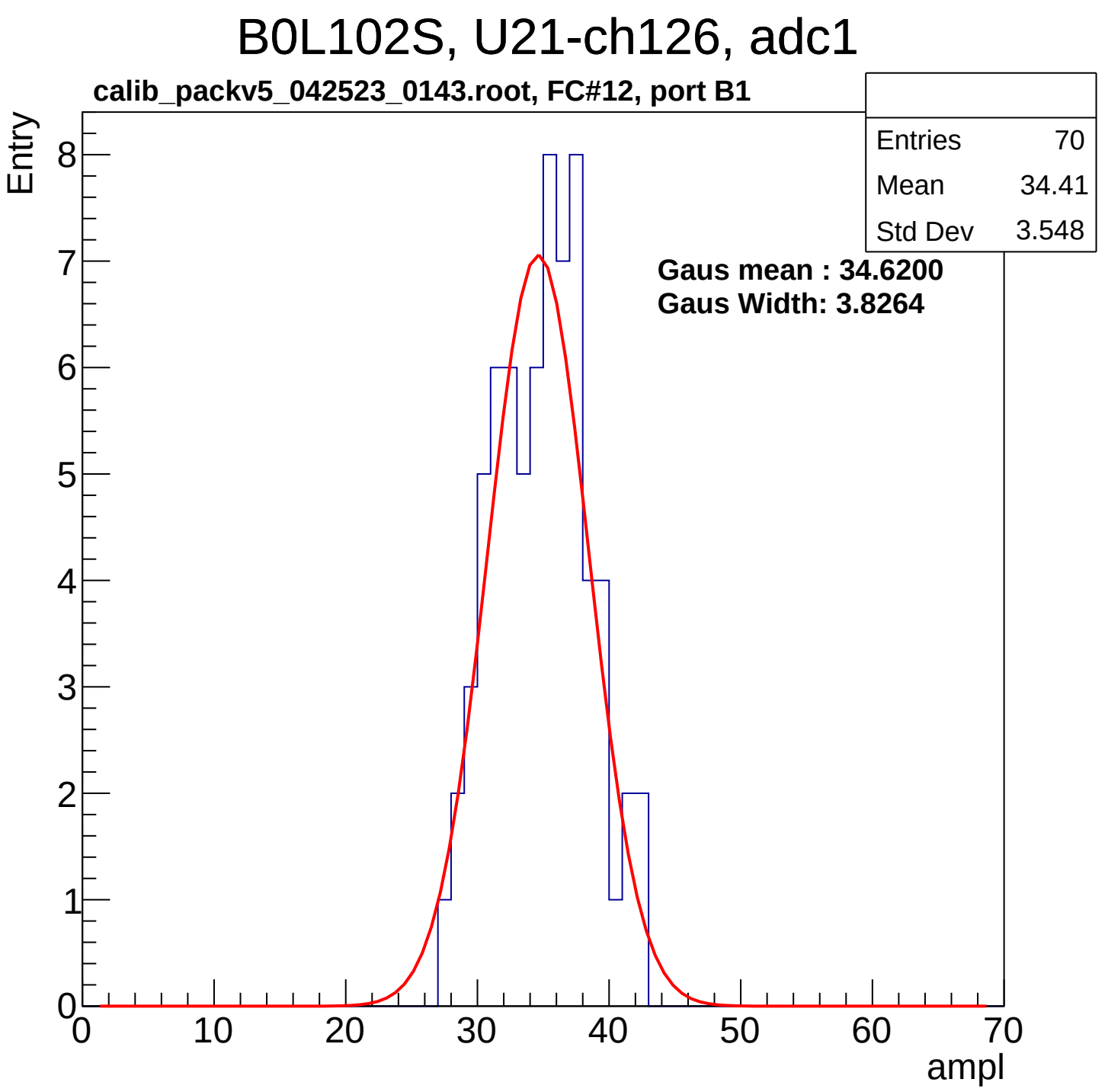

B0L102S, U21-ch125, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54.16
Std Dev 4.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch125, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 57.78
Std Dev 9.606
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch125, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 60.47
Std Dev 2.418
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U21-ch125, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 60.5
Std Dev 2.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U21-ch126, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 25.46
Std Dev 4.747
Gaus mean : 27.0689
Gaus Width: 3.7392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U21-ch126, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 34.41
Std Dev 3.548
Gaus mean : 34.6200
Gaus Width: 3.8264
Entry
ampl
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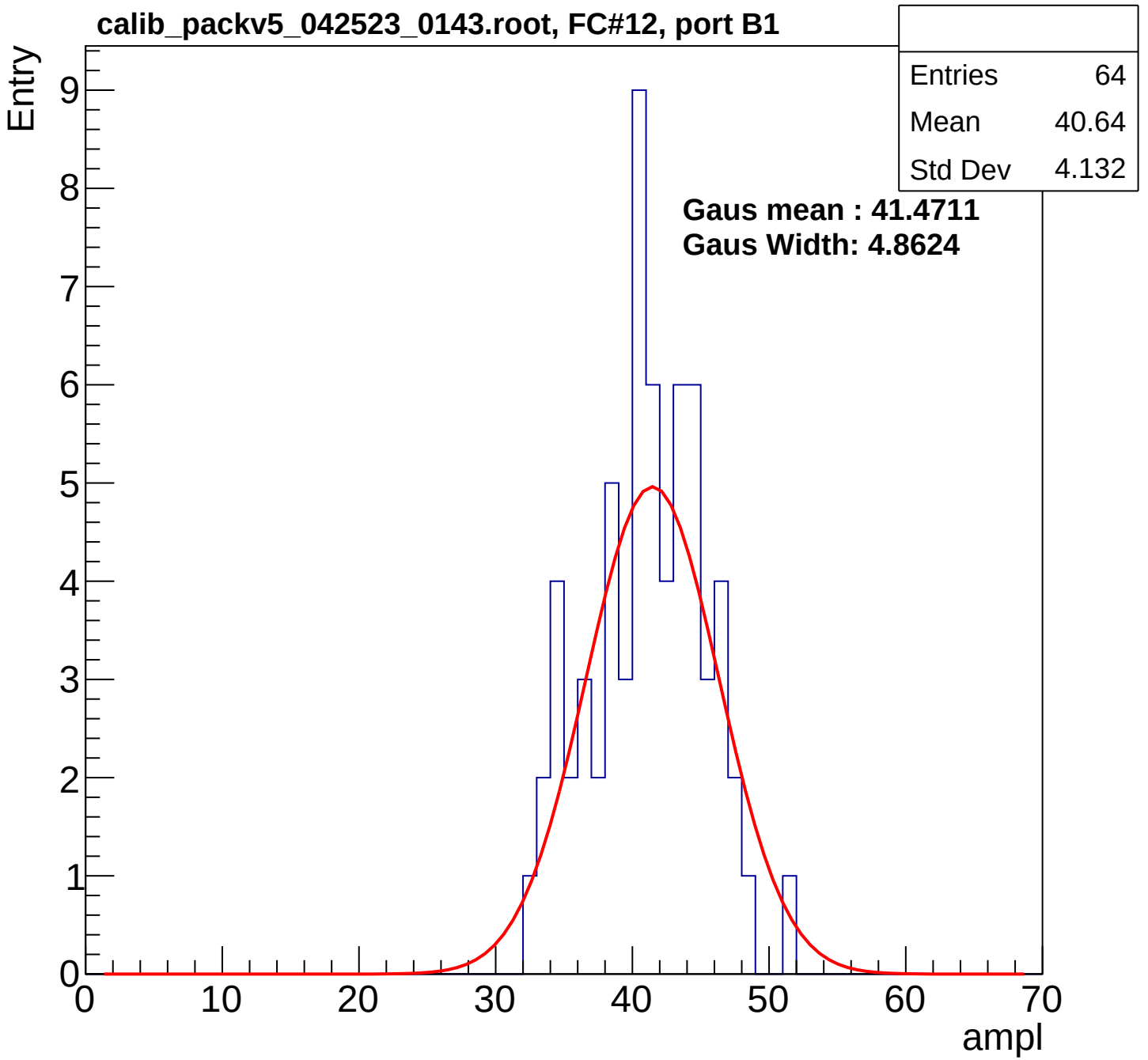
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch126, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 40.64
Std Dev 4.132
Gaus mean : 41.4711
Gaus Width: 4.8624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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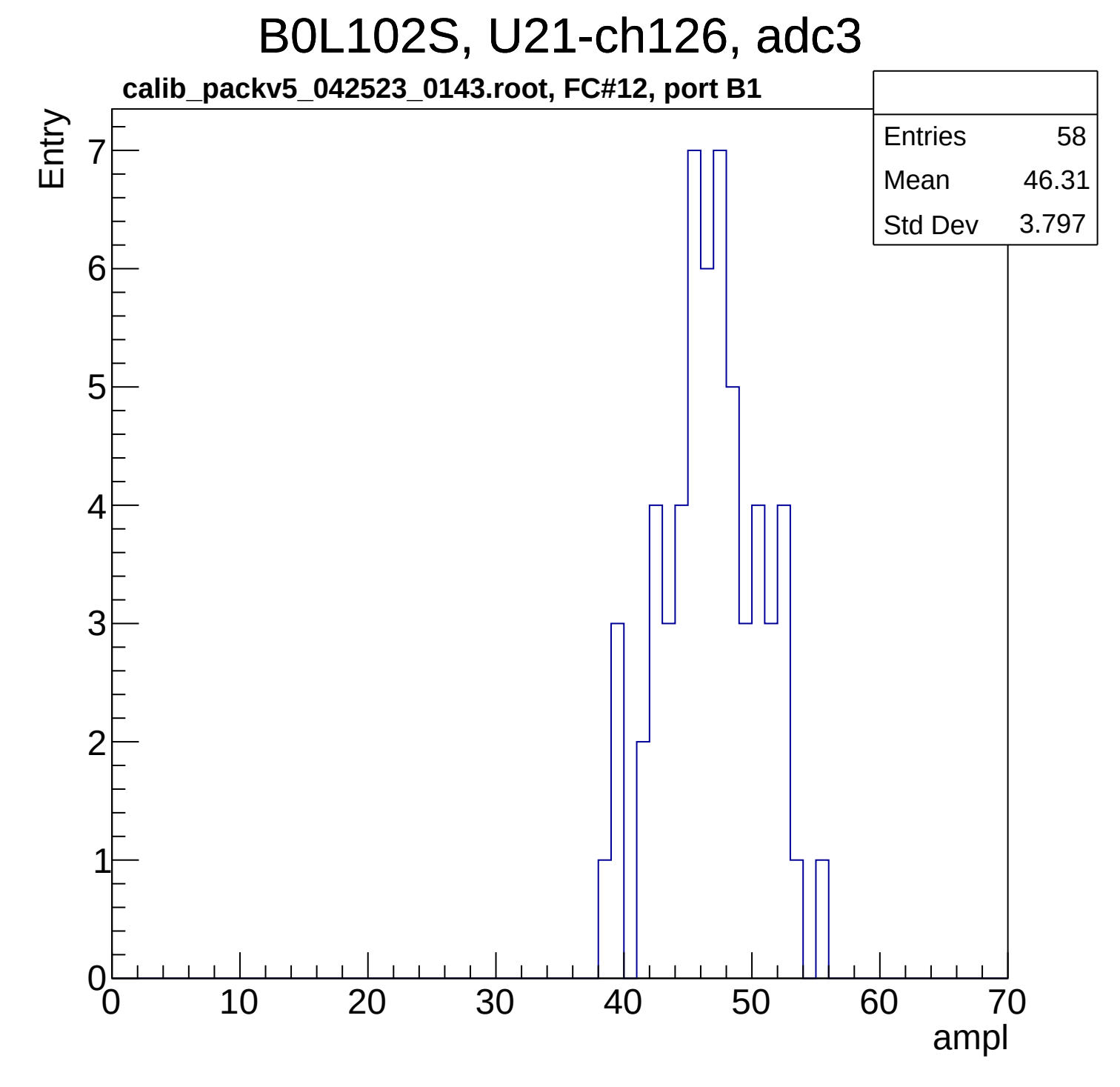
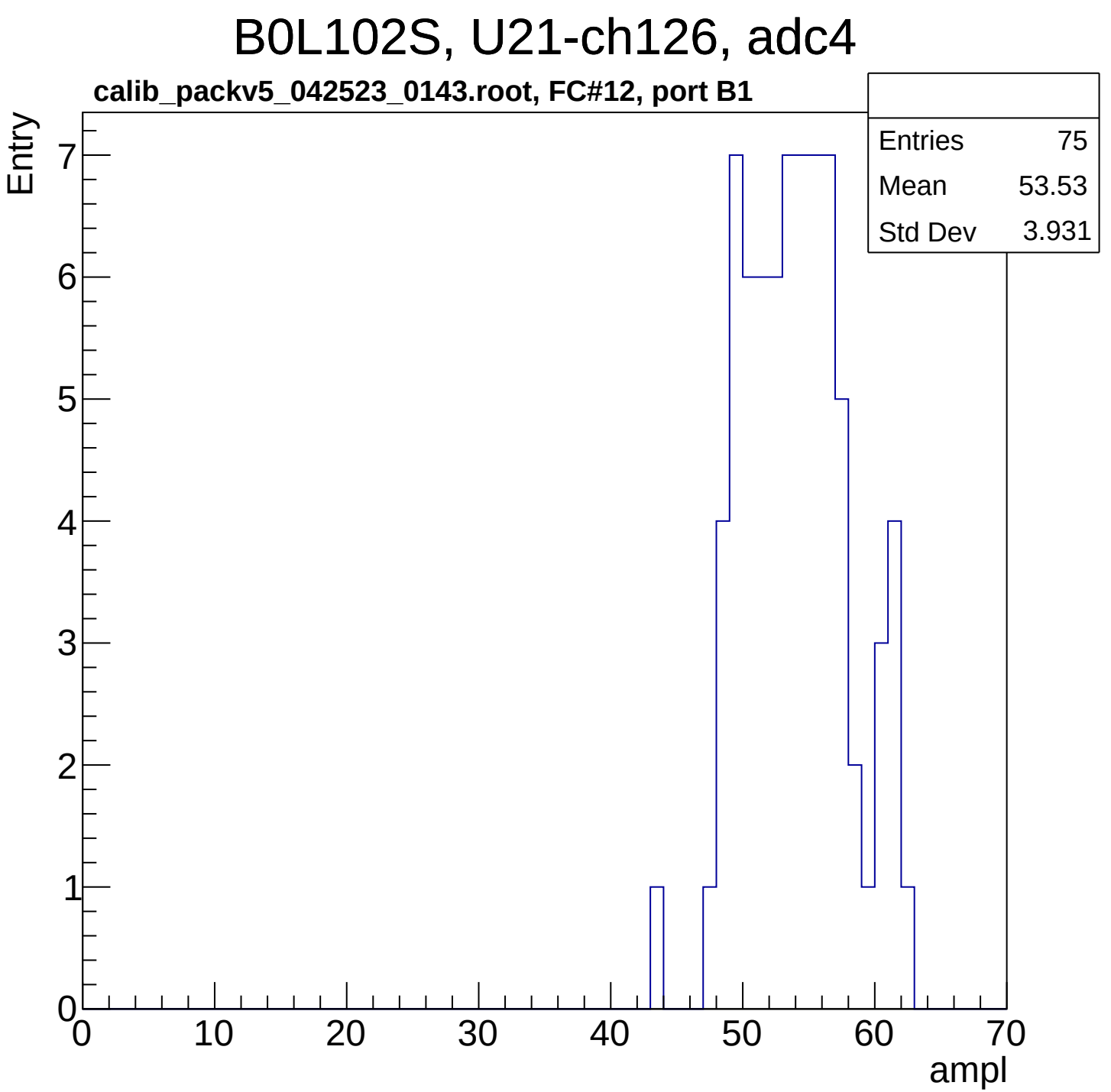
60
70


B0L102S, U21-ch126, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 46.31
Std Dev 3.797
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch126, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 53.53
Std Dev 3.931
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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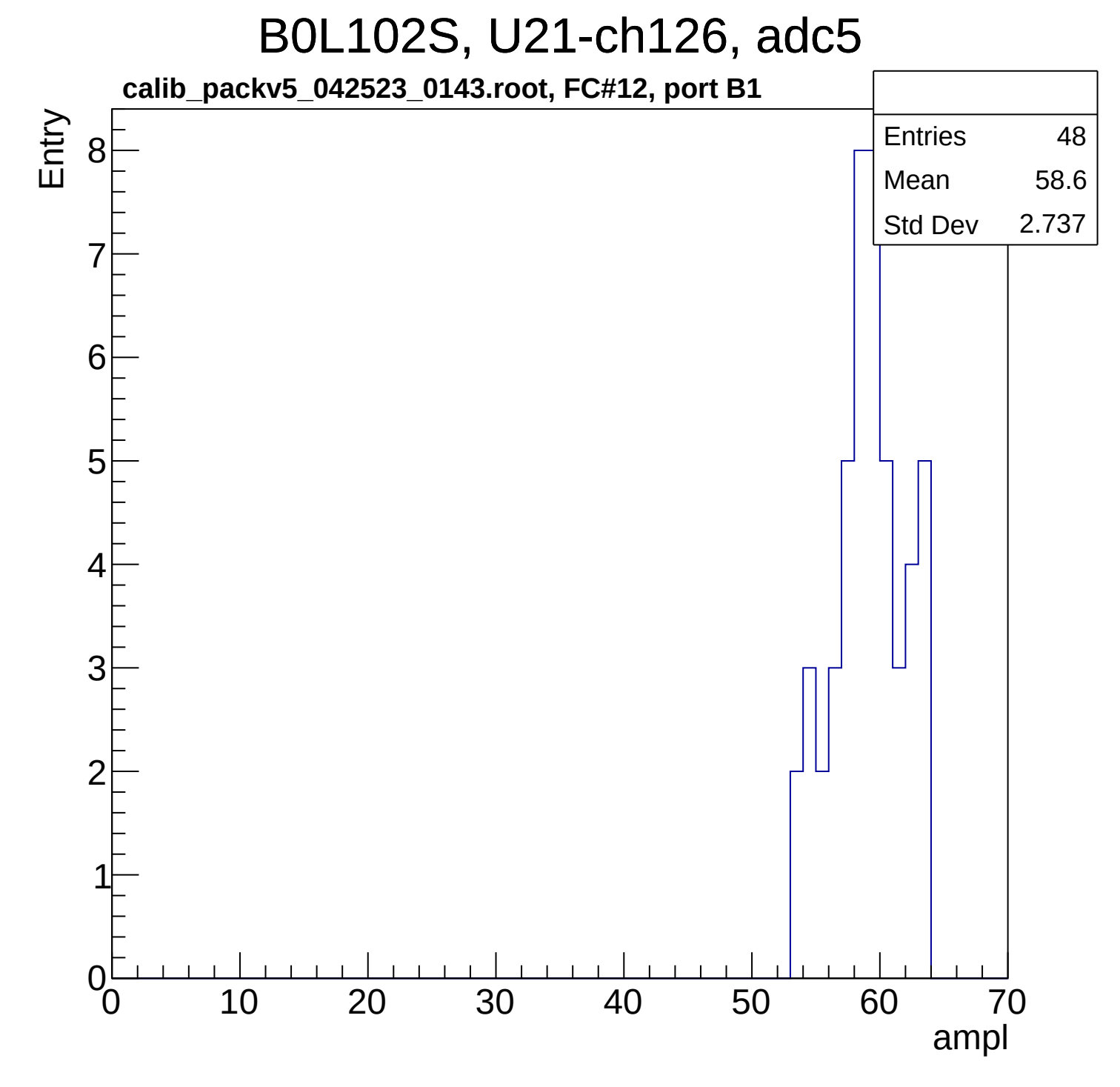
40
50
60
70


B0L102S, U21-ch126, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.6
Std Dev 2.737
Entry
ampl
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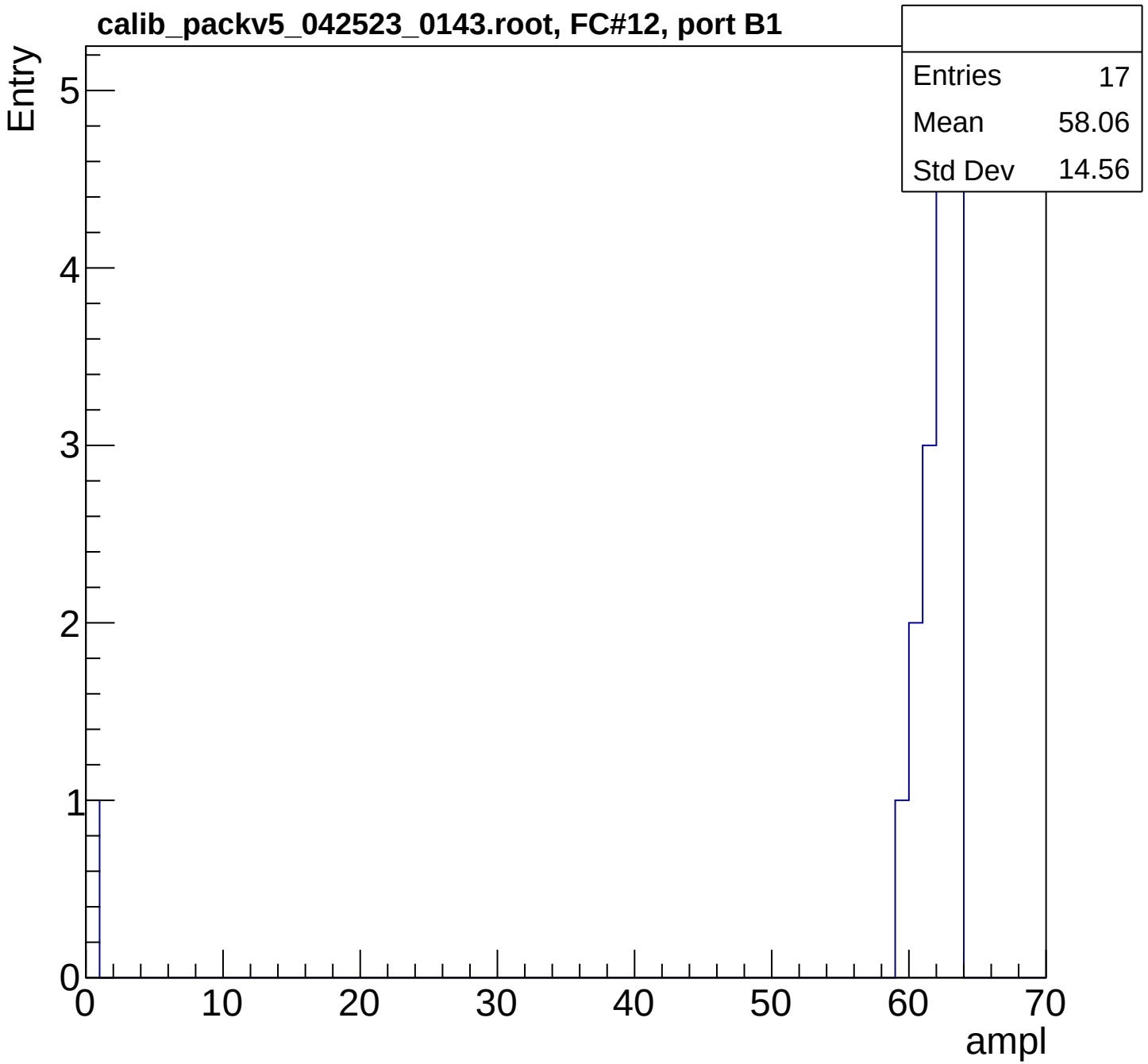
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U21-ch126, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 17
Mean 58.06
Std Dev 14.56
Entry
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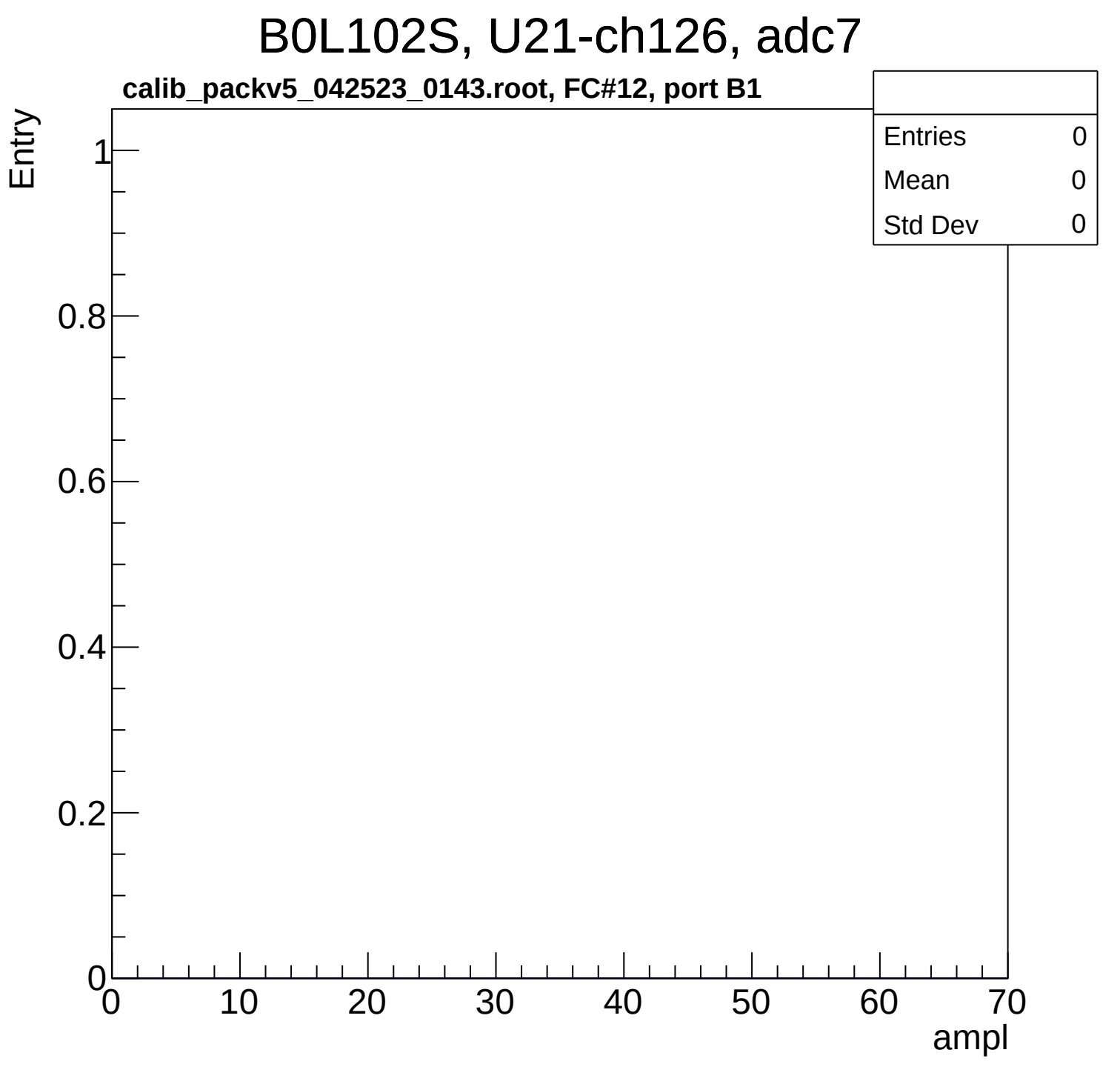
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch126, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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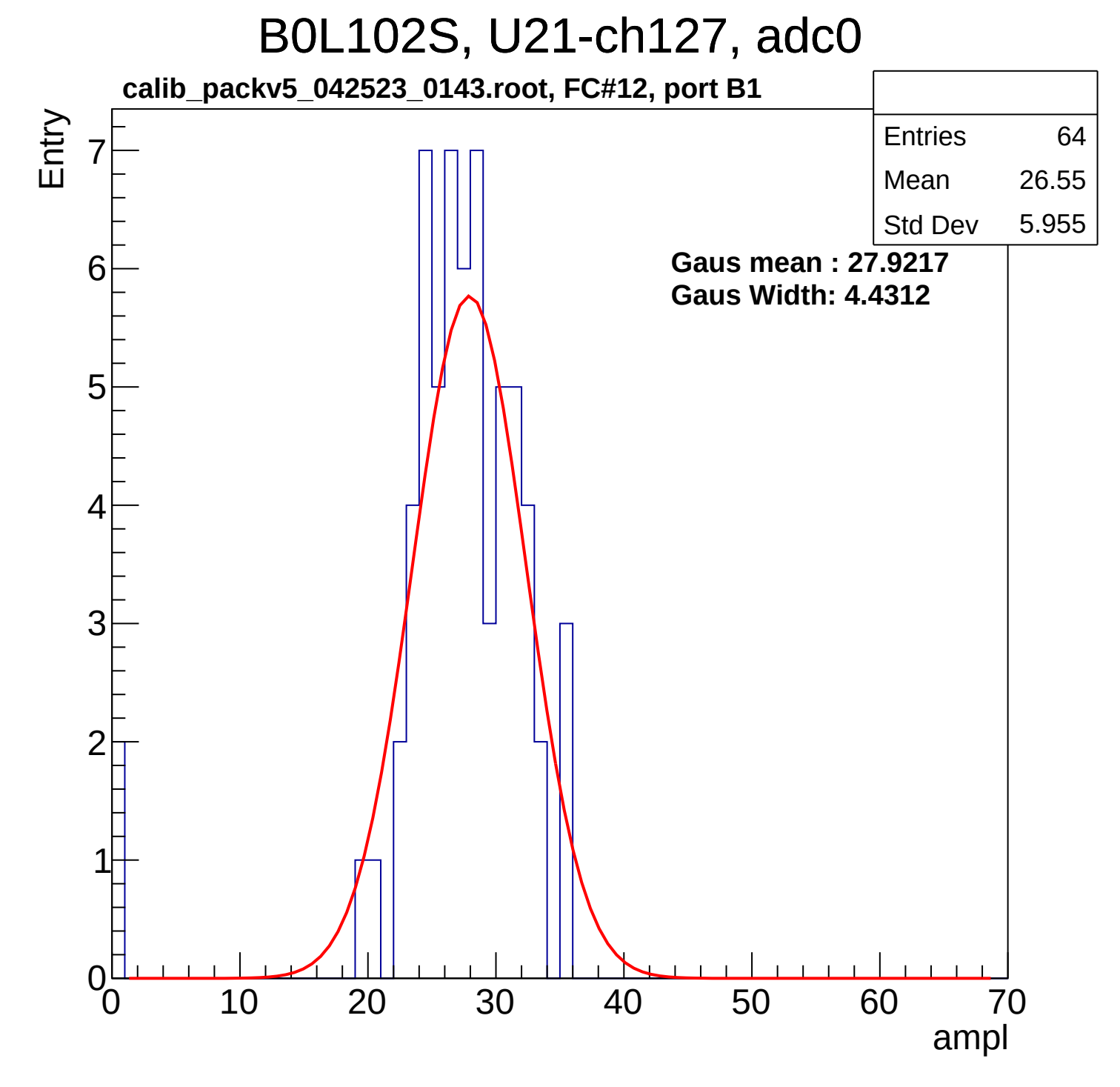
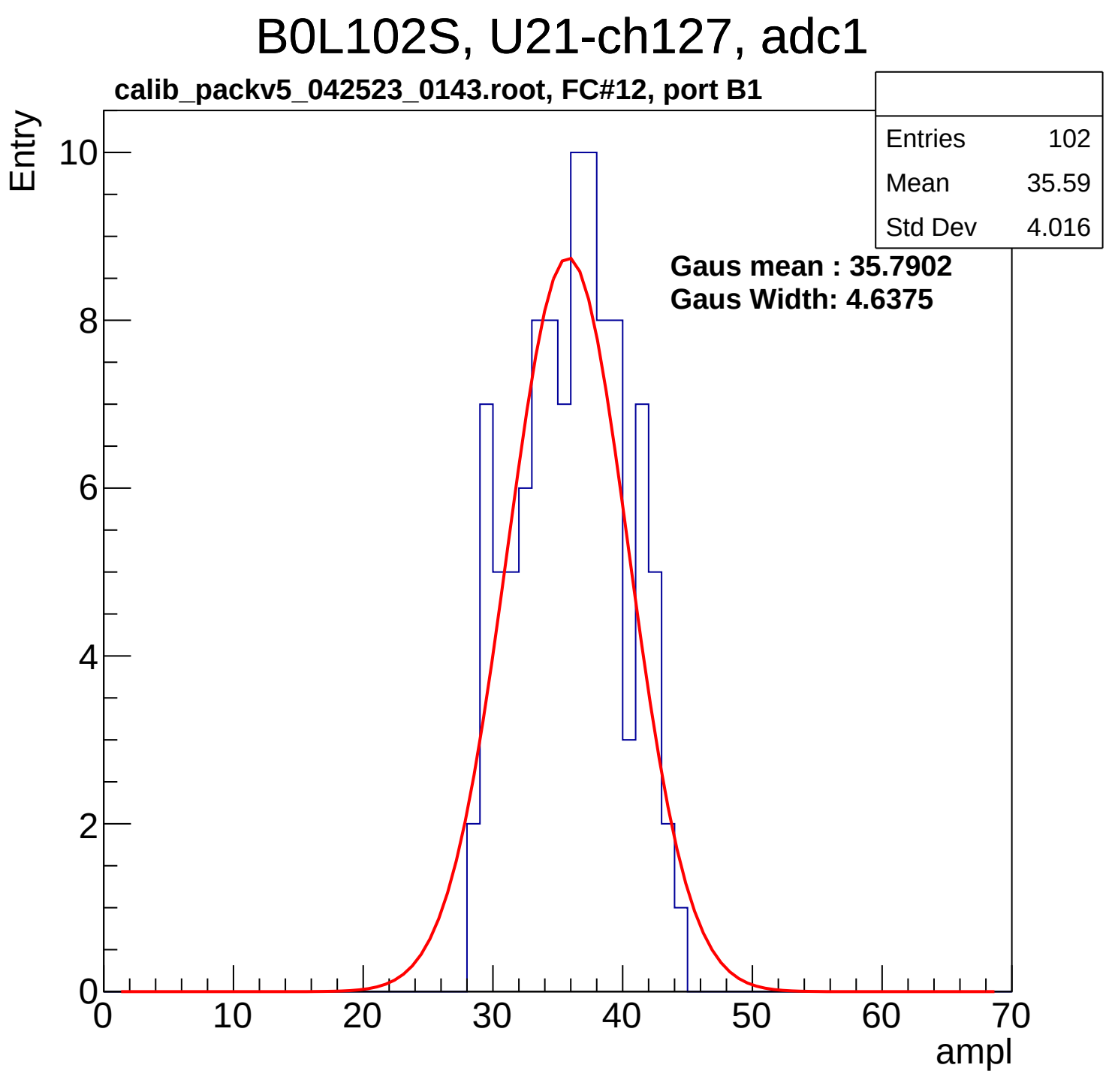
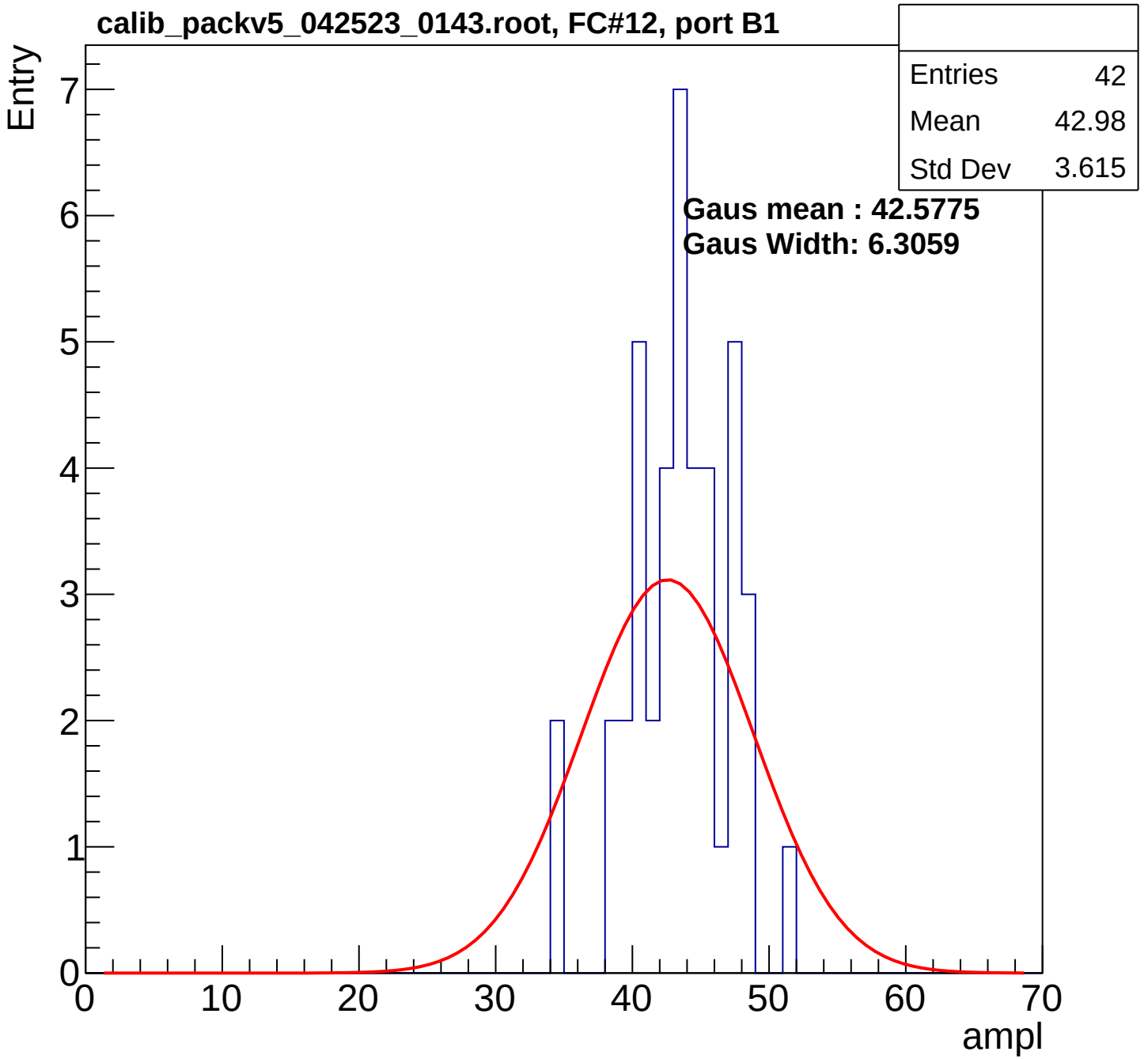
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch127, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 26.55
Std Dev 5.955
Gaus mean : 27.9217
Gaus Width: 4.4312
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U21-ch127, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 102
Mean 35.59
Std Dev 4.016
Gaus mean : 35.7902
Gaus Width: 4.6375
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U21-ch127, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 42.98
Std Dev 3.615
Gaus mean : 42.5775
Gaus Width: 6.3059
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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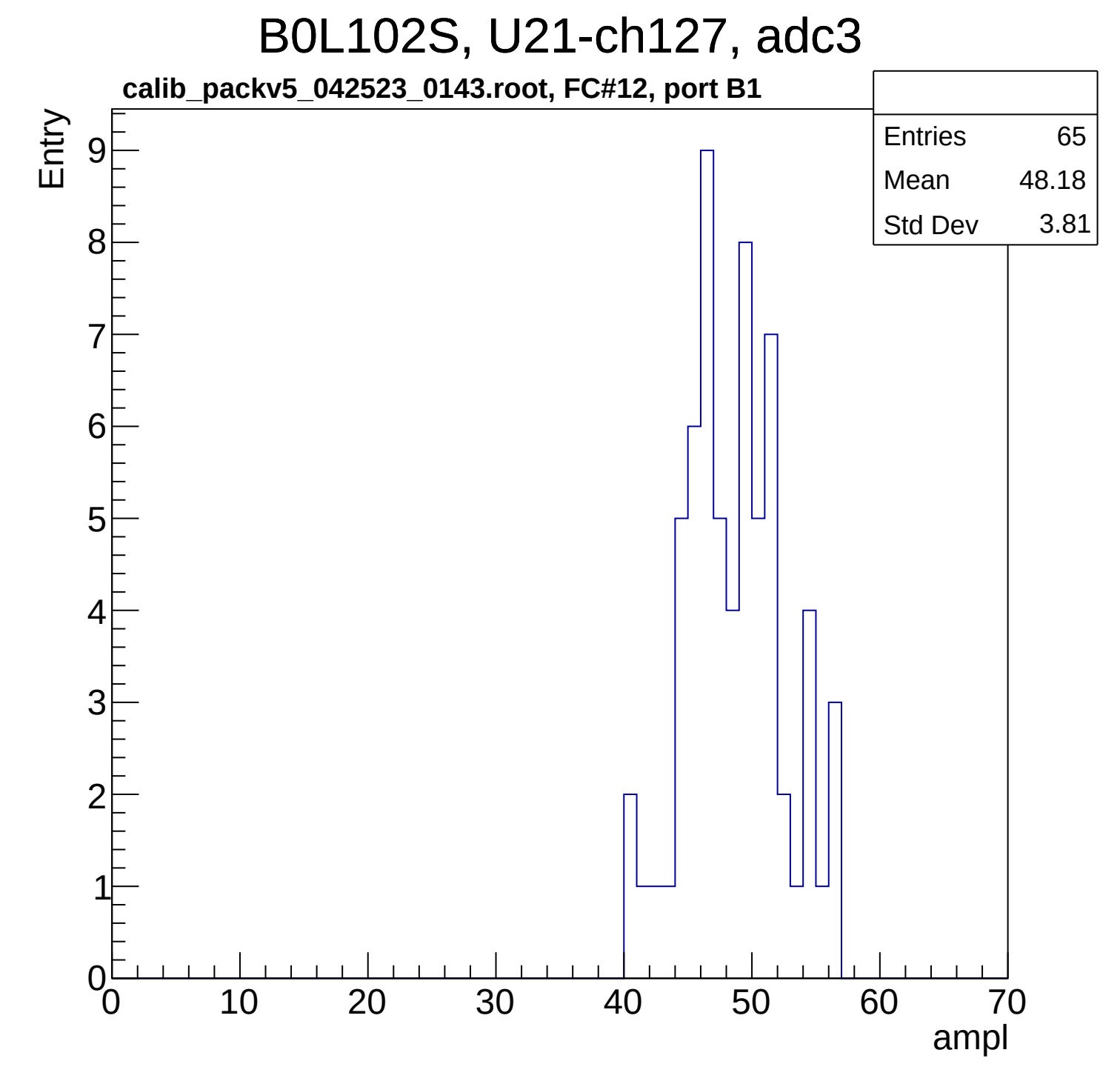
B0L102S, U21-ch127, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.18
Std Dev 3.81
Entry
ampl
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2
3
4
5
6
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10
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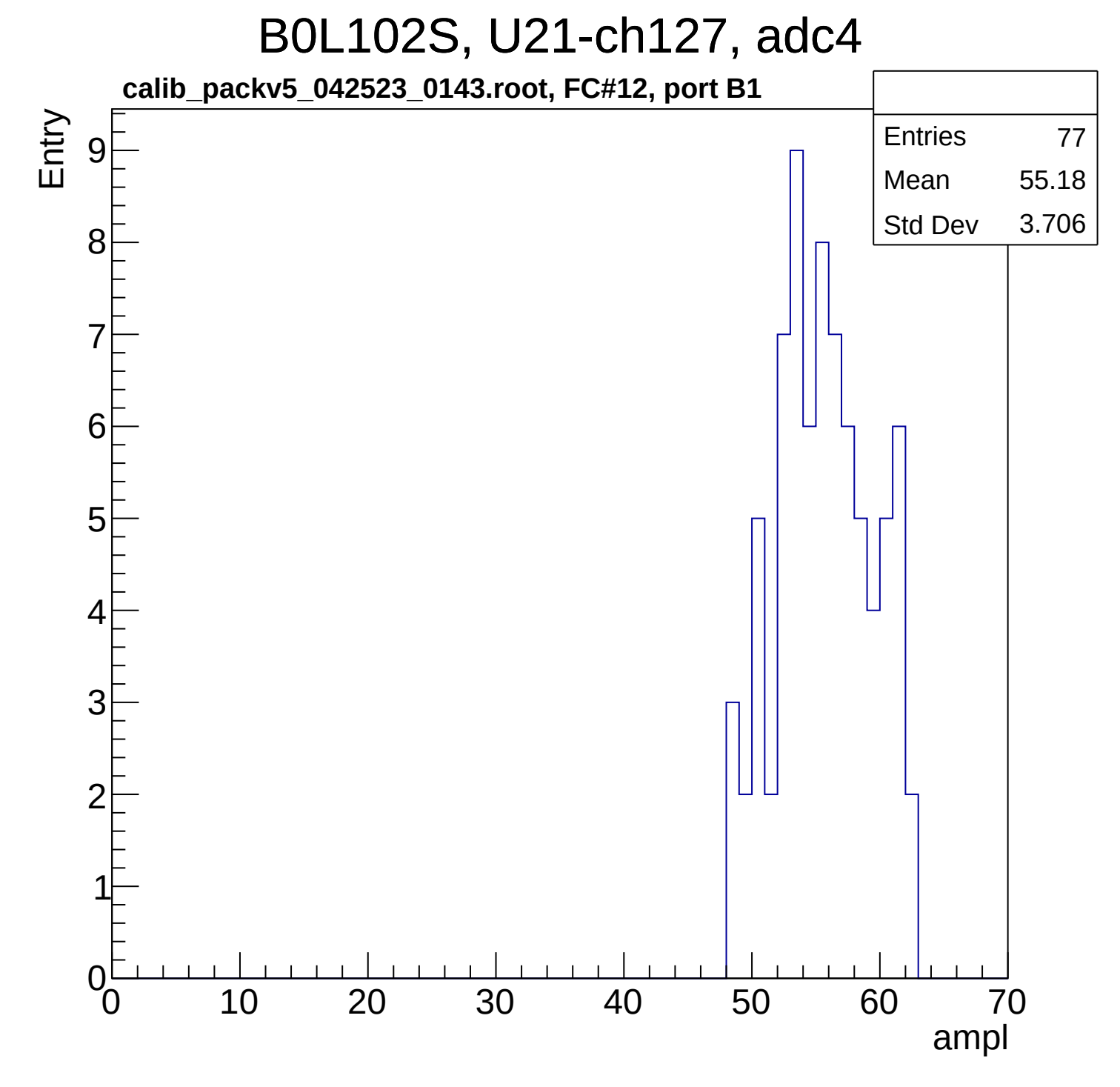
B0L102S, U21-ch127, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 55.18
Std Dev 3.706
Entry
ampl
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3
4
5
6
7
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0
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60
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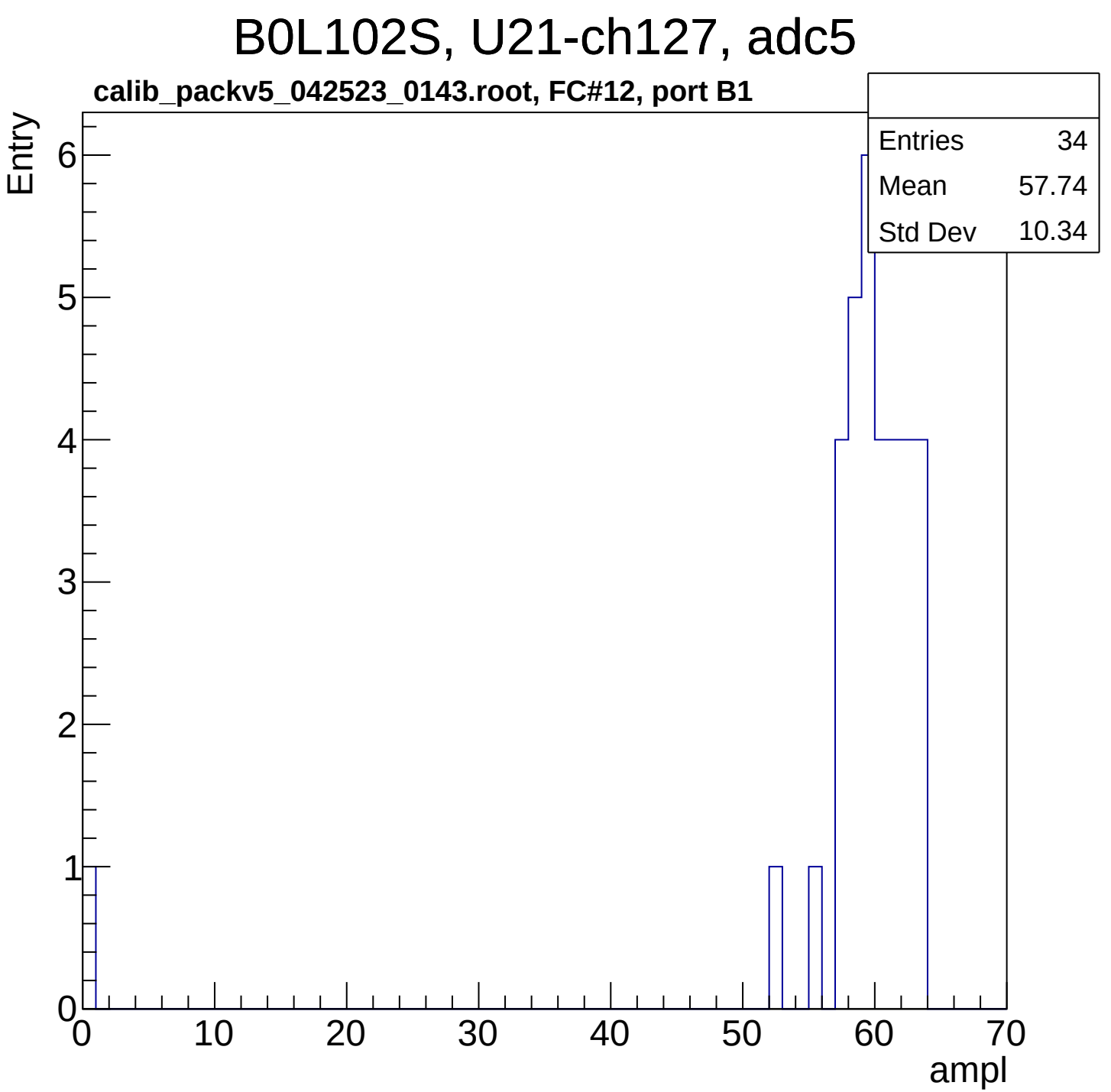
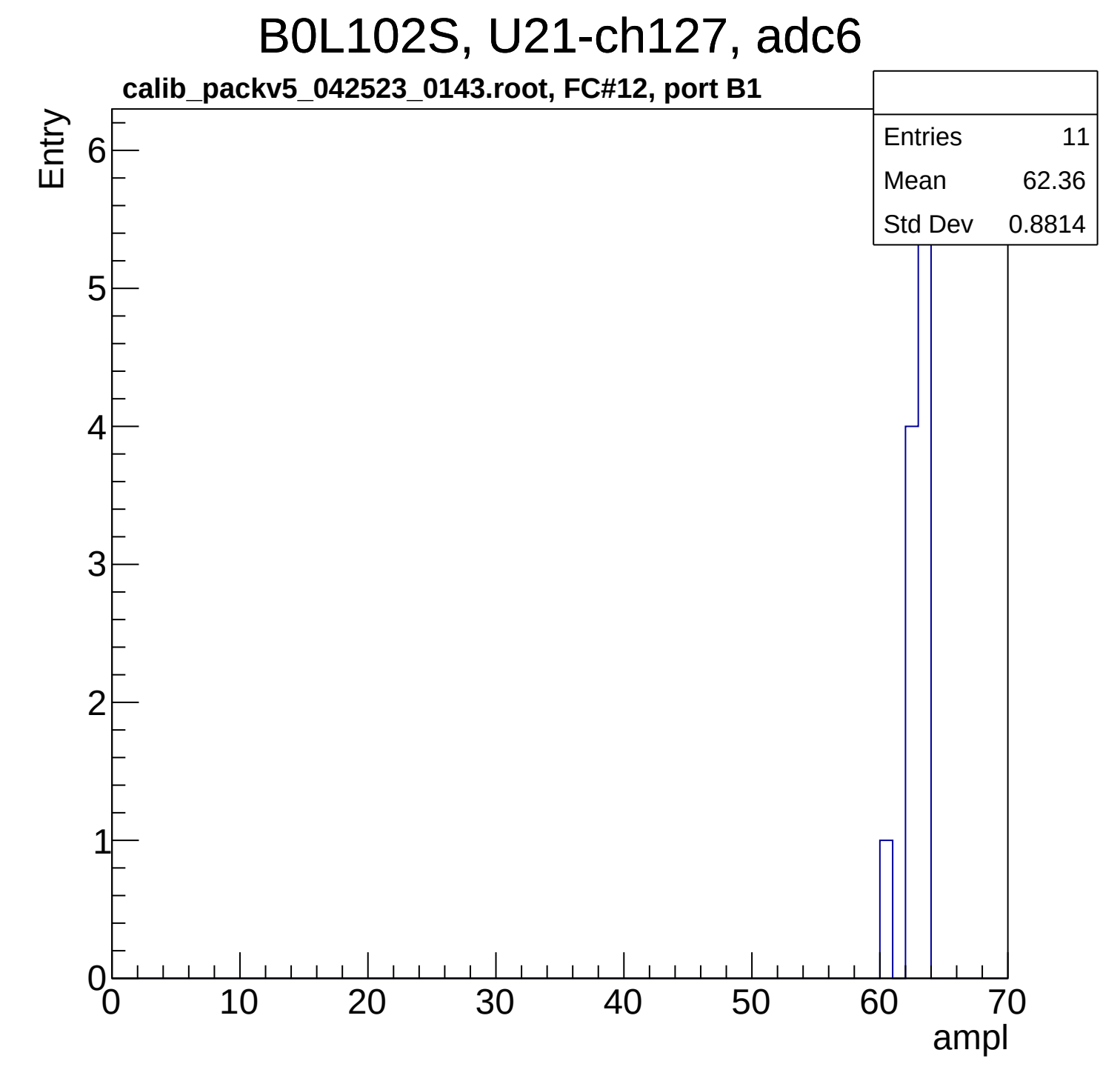
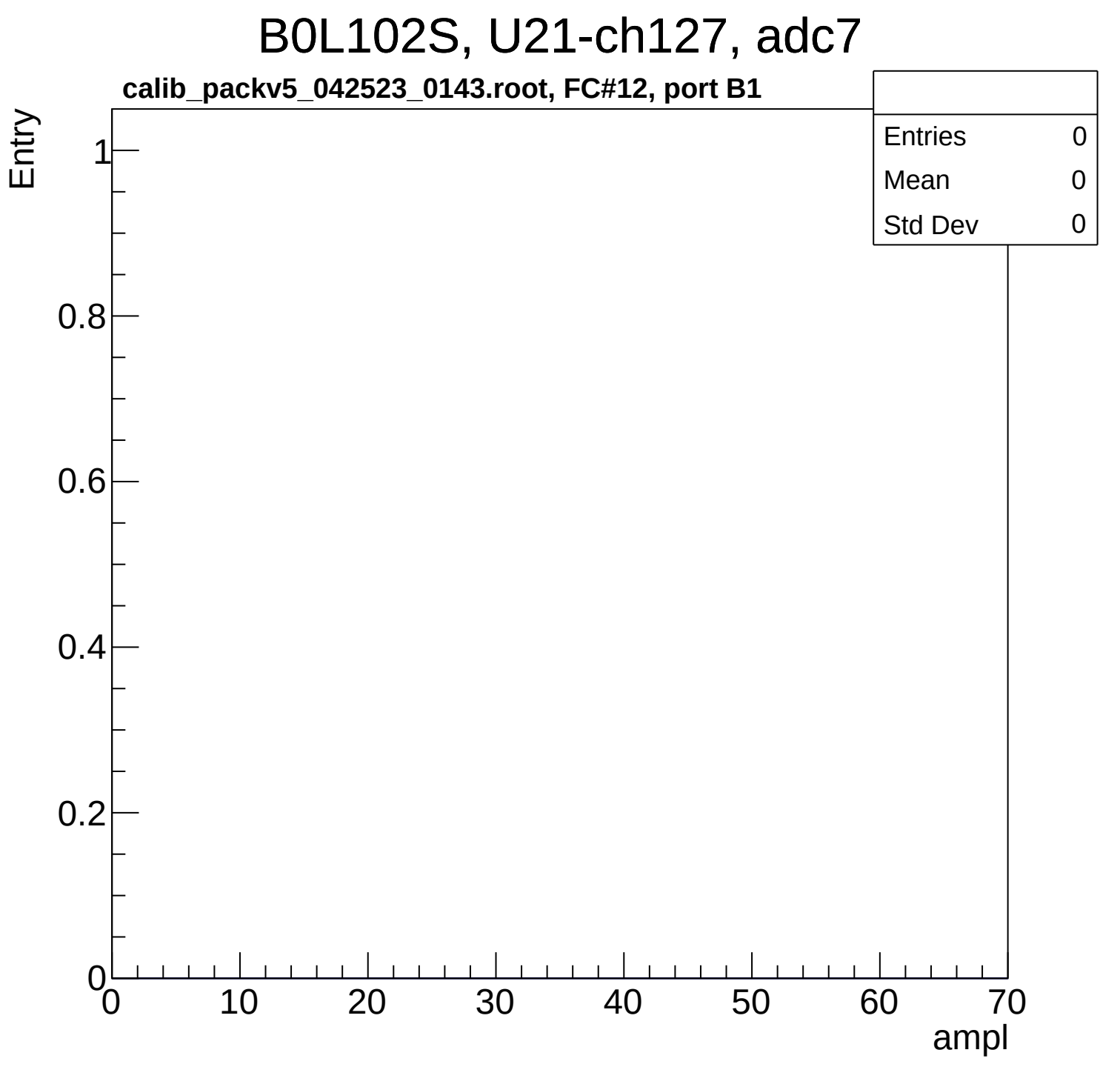
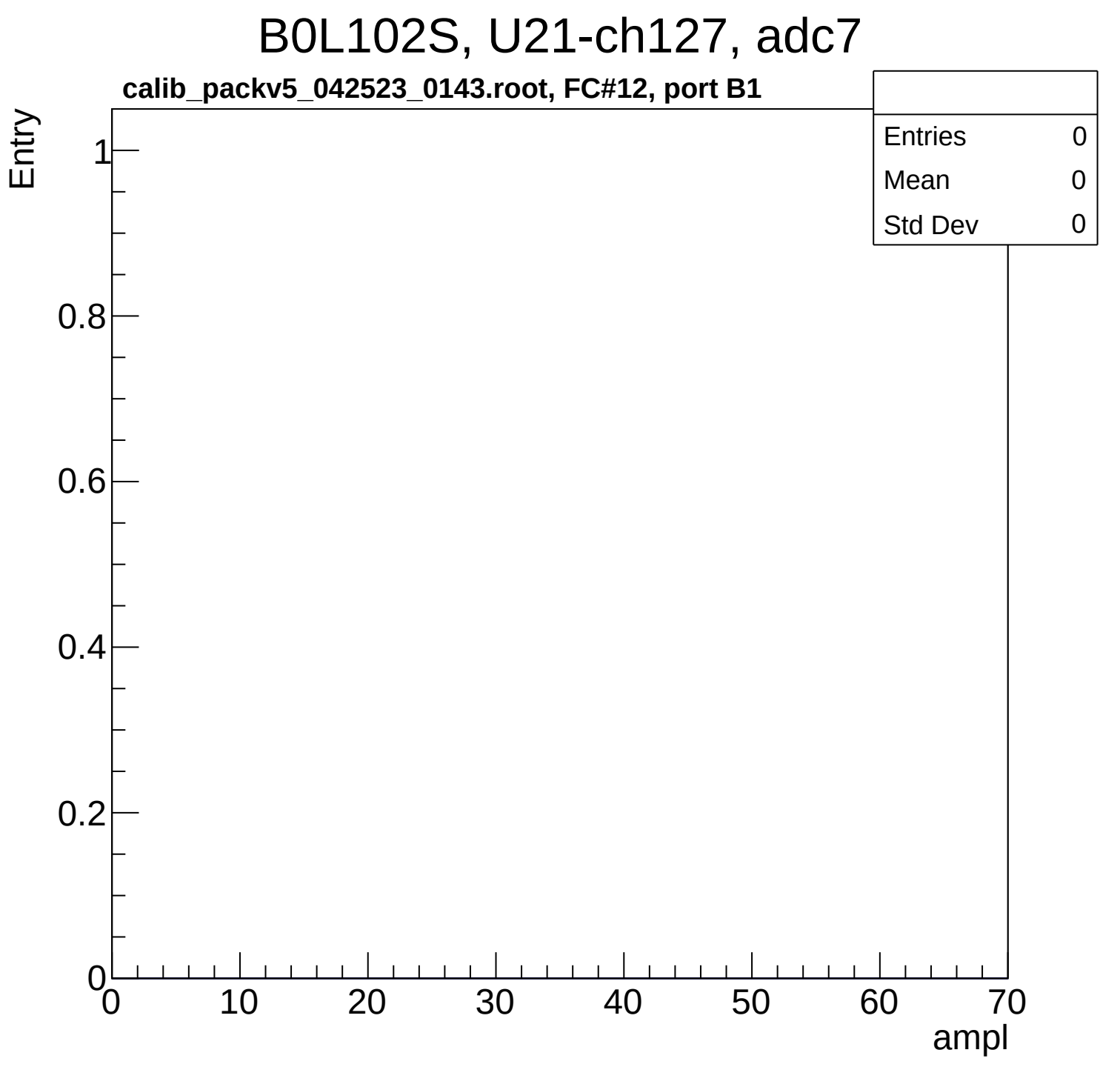

B0L102S, U21-ch127, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 57.74
Std Dev 10.34
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch127, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 11
Mean 62.36
Std Dev 0.8814
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U21-ch127, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U21-ch127, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl