

B1L100S, U11-ch0, adc0

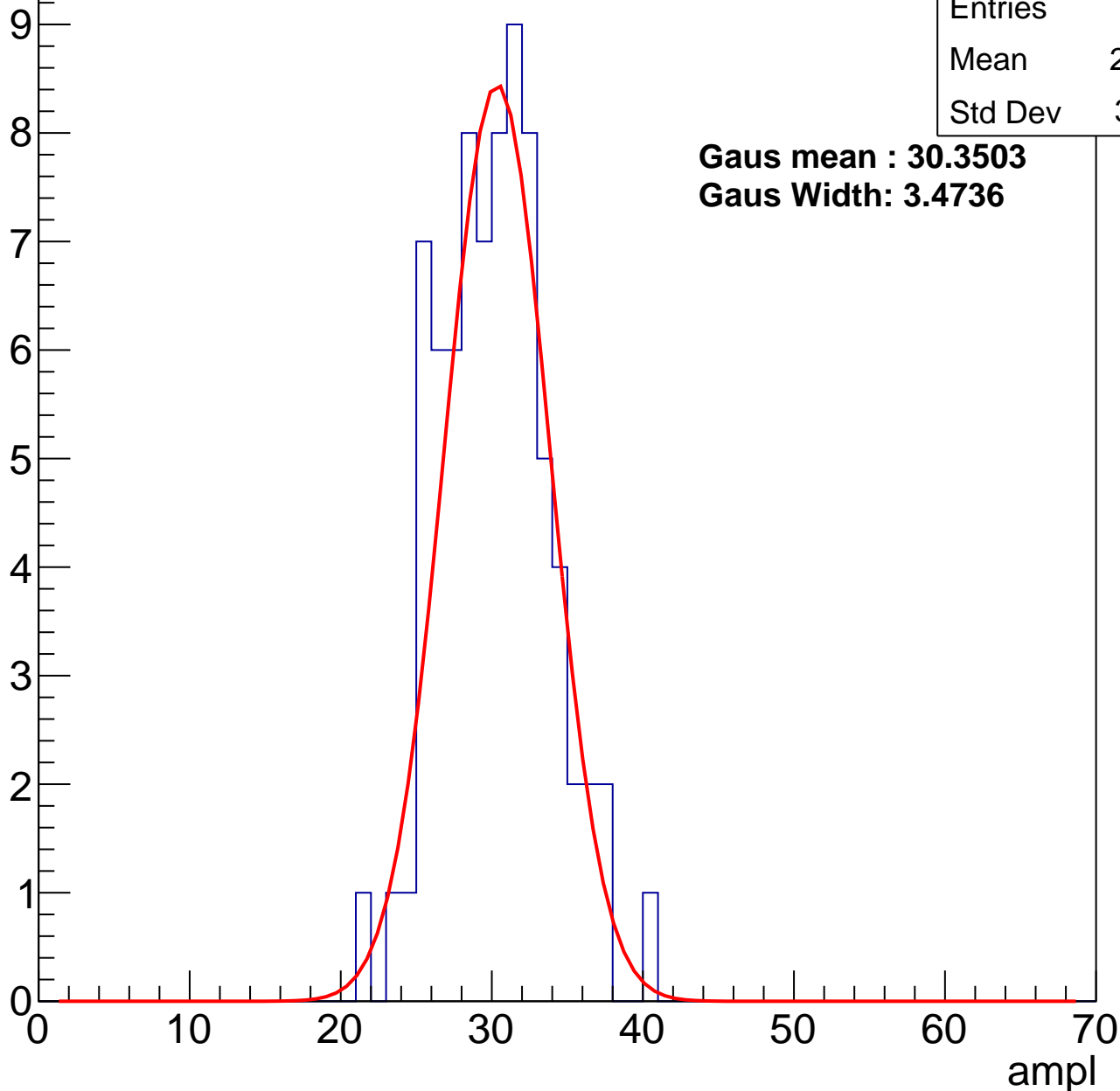
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	29.74
Std Dev	3.571

Gaus mean : 30.3503

Gaus Width: 3.4736



B1L100S, U11-ch0, adc1

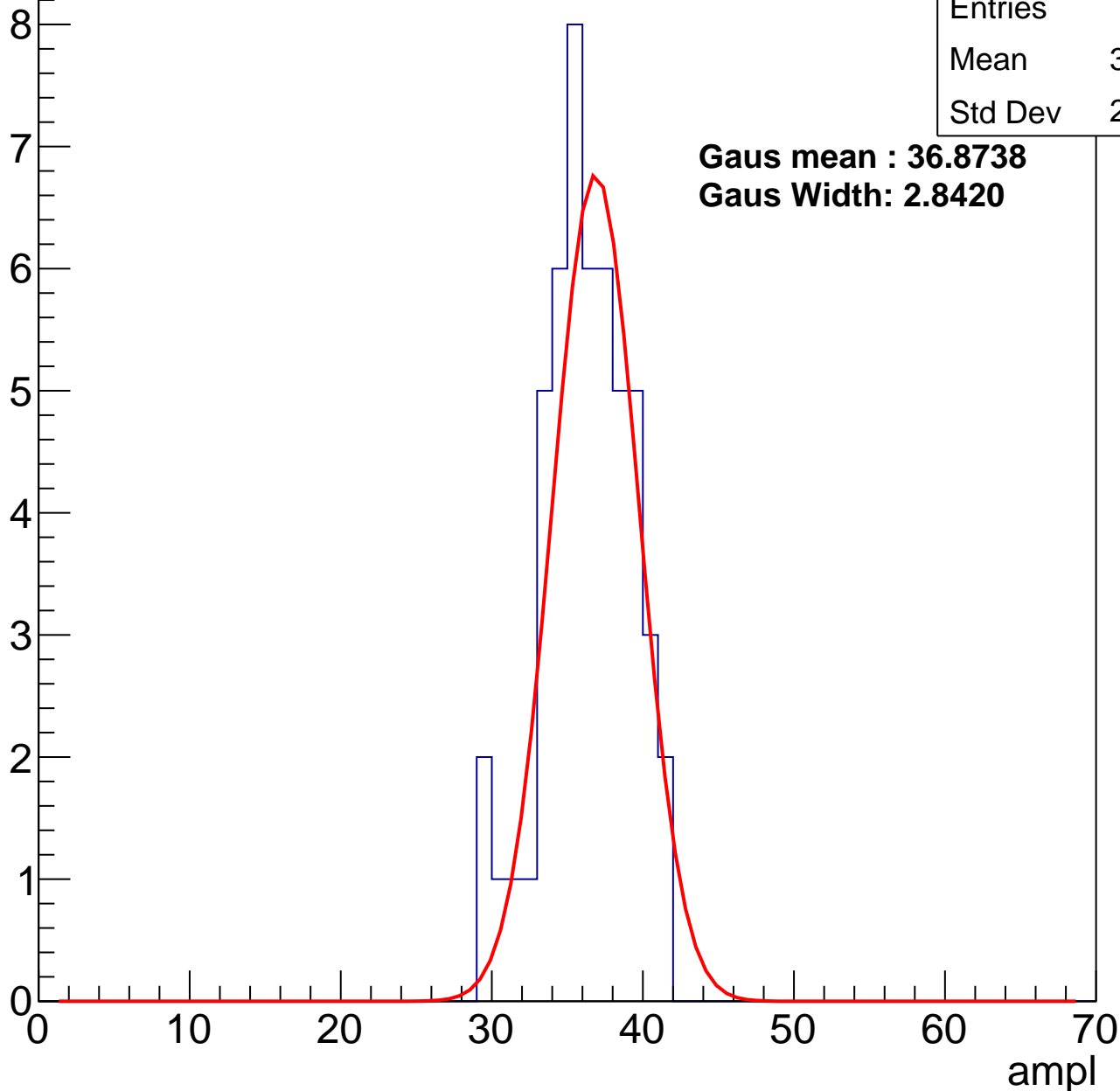
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	35.78
Std Dev	2.865

Gaus mean : 36.8738

Gaus Width: 2.8420



B1L100S, U11-ch0, adc2

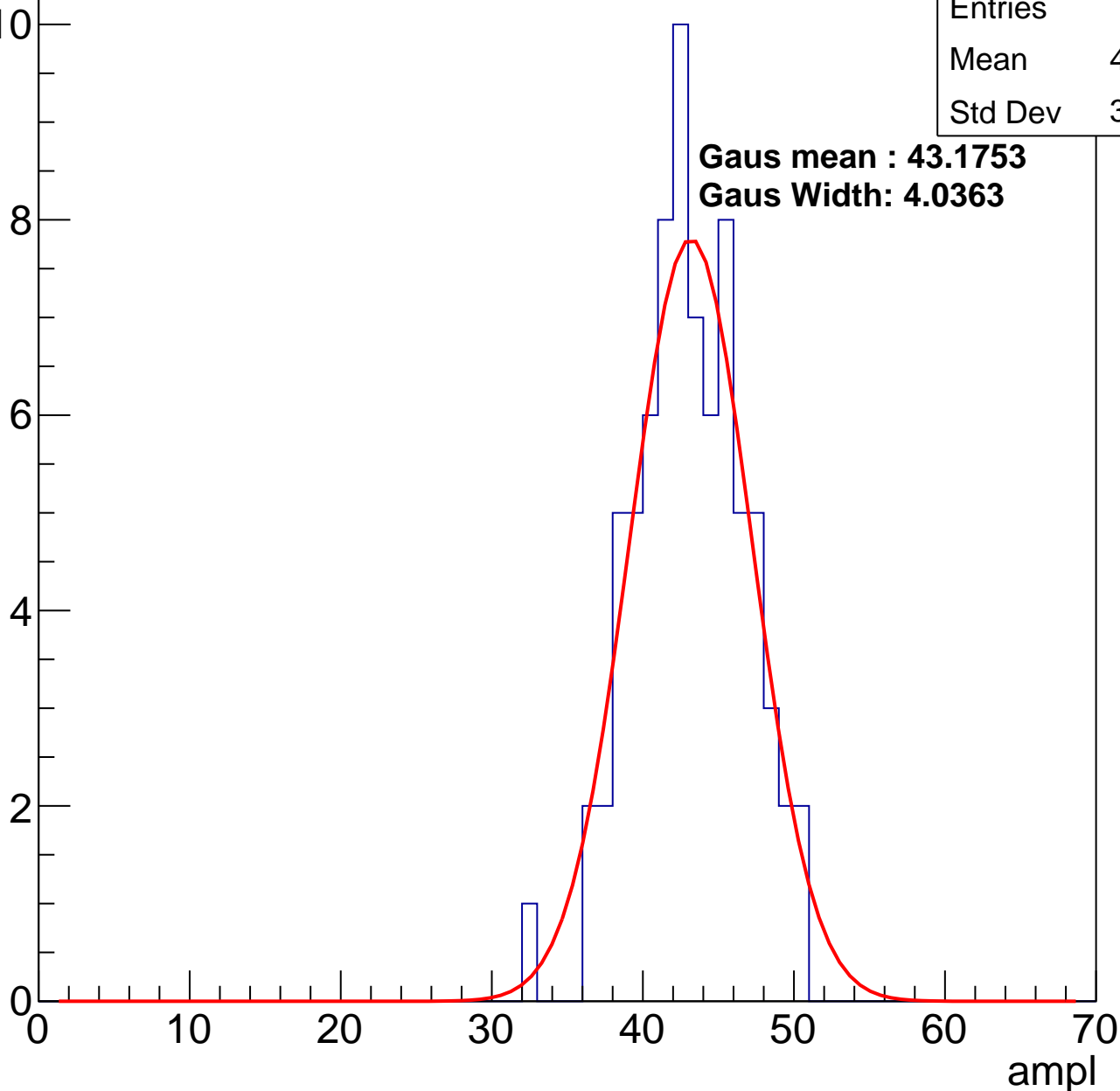
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	42.64
Std Dev	3.585

Gaus mean : 43.1753

Gaus Width: 4.0363

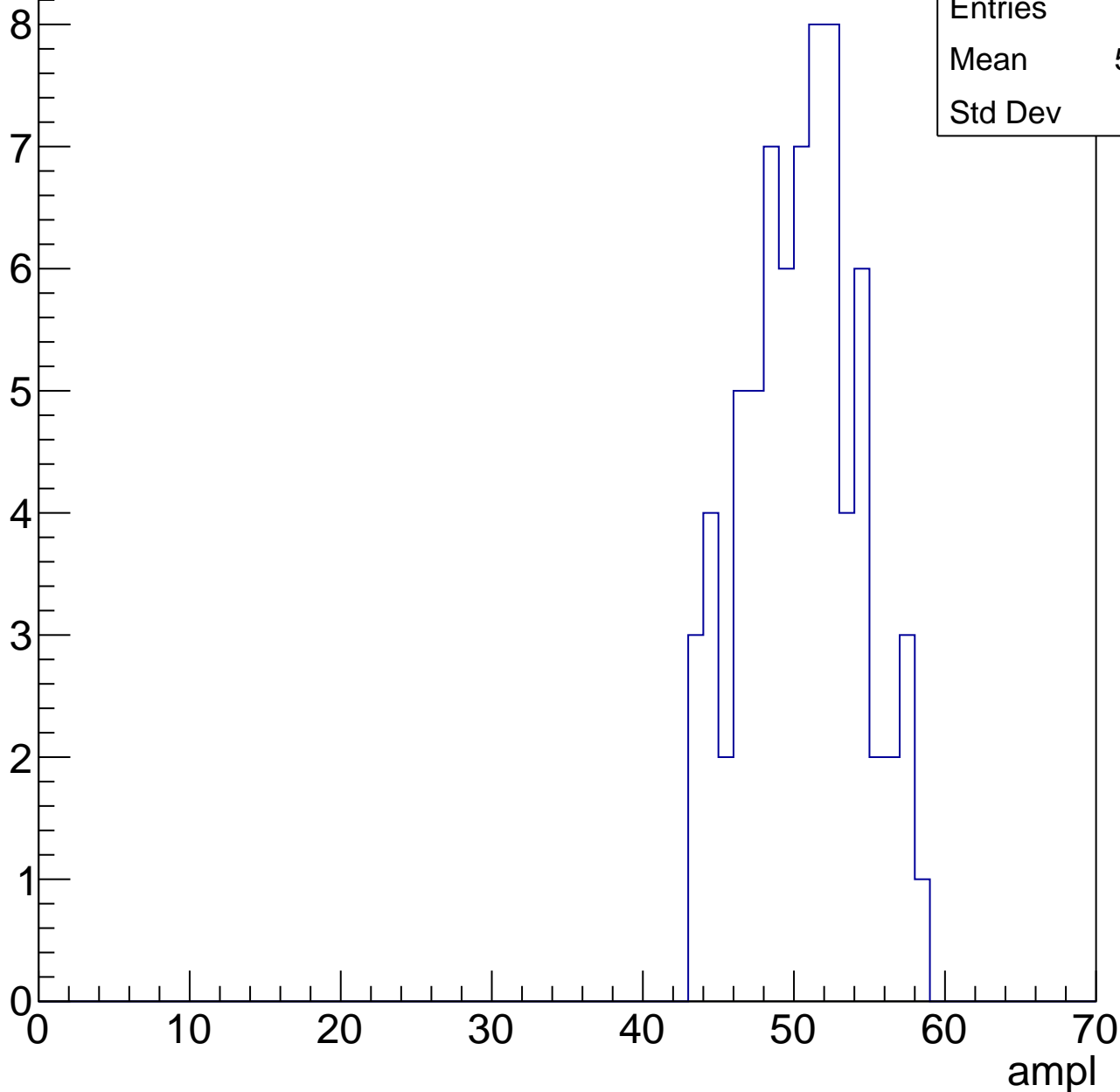


B1L100S, U11-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	50.01
Std Dev	3.71

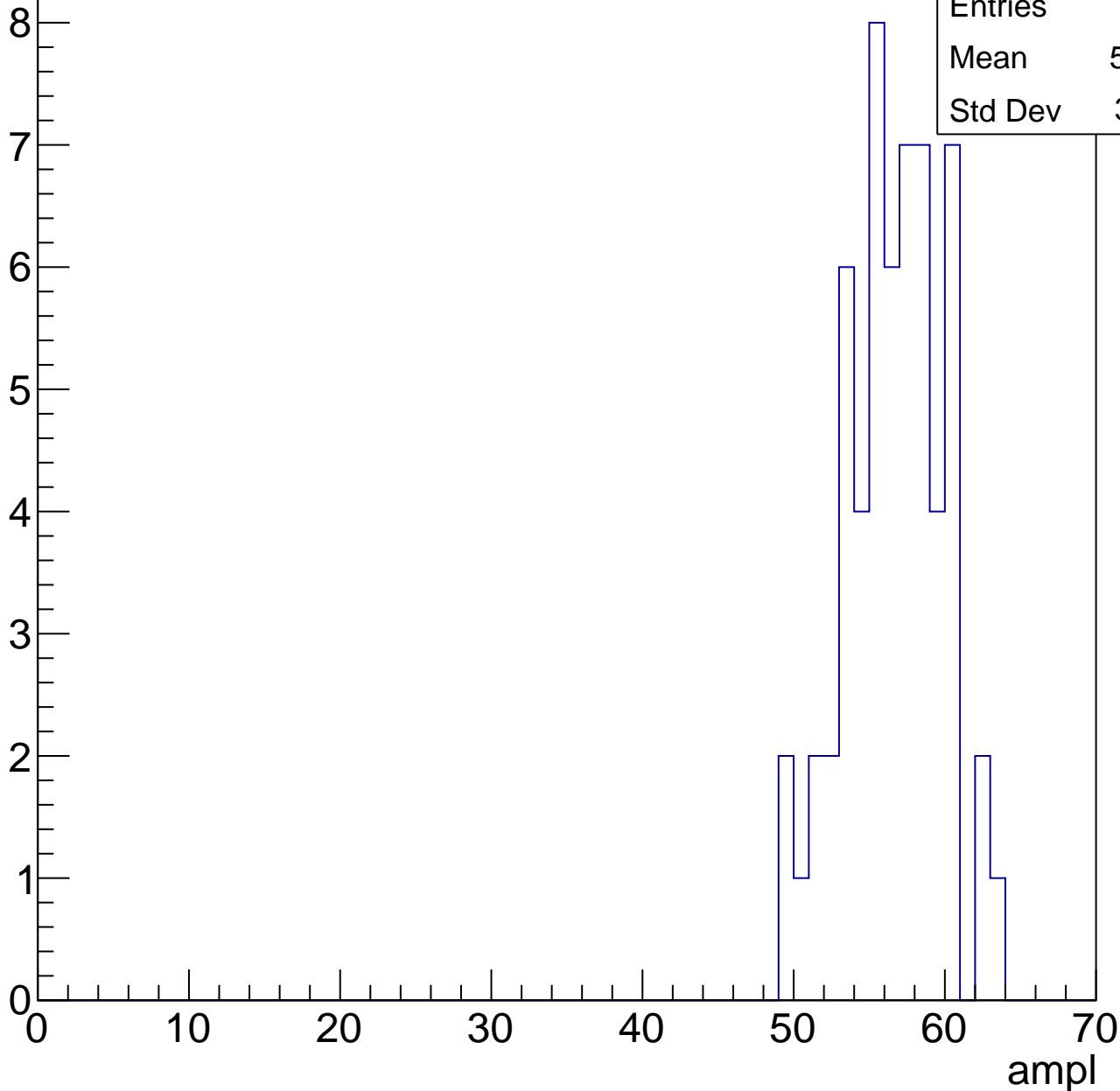


B1L100S, U11-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.14
Std Dev	3.181

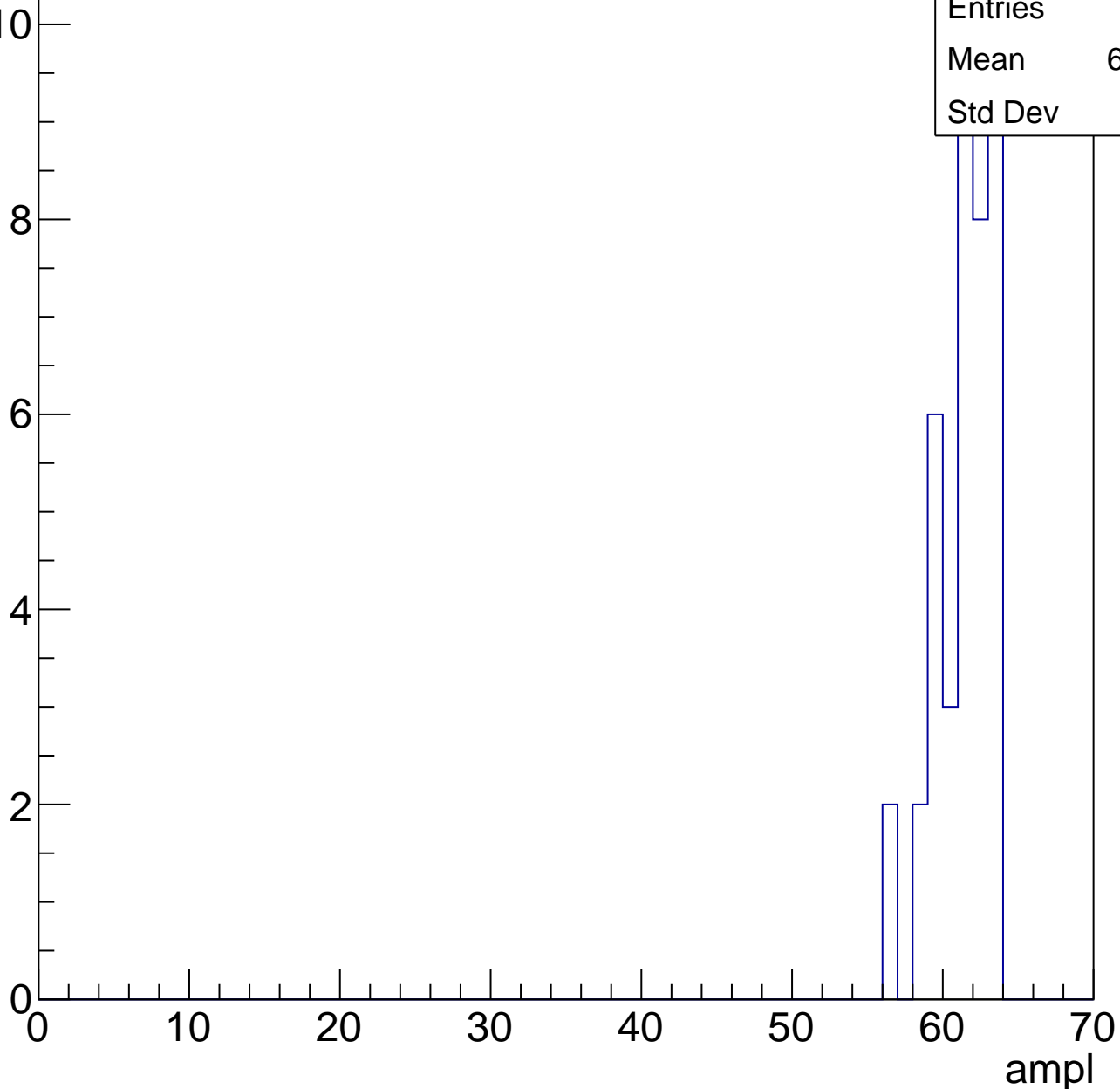


B1L100S, U11-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	60.88
Std Dev	1.86



B1L100S, U11-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch1, adc0

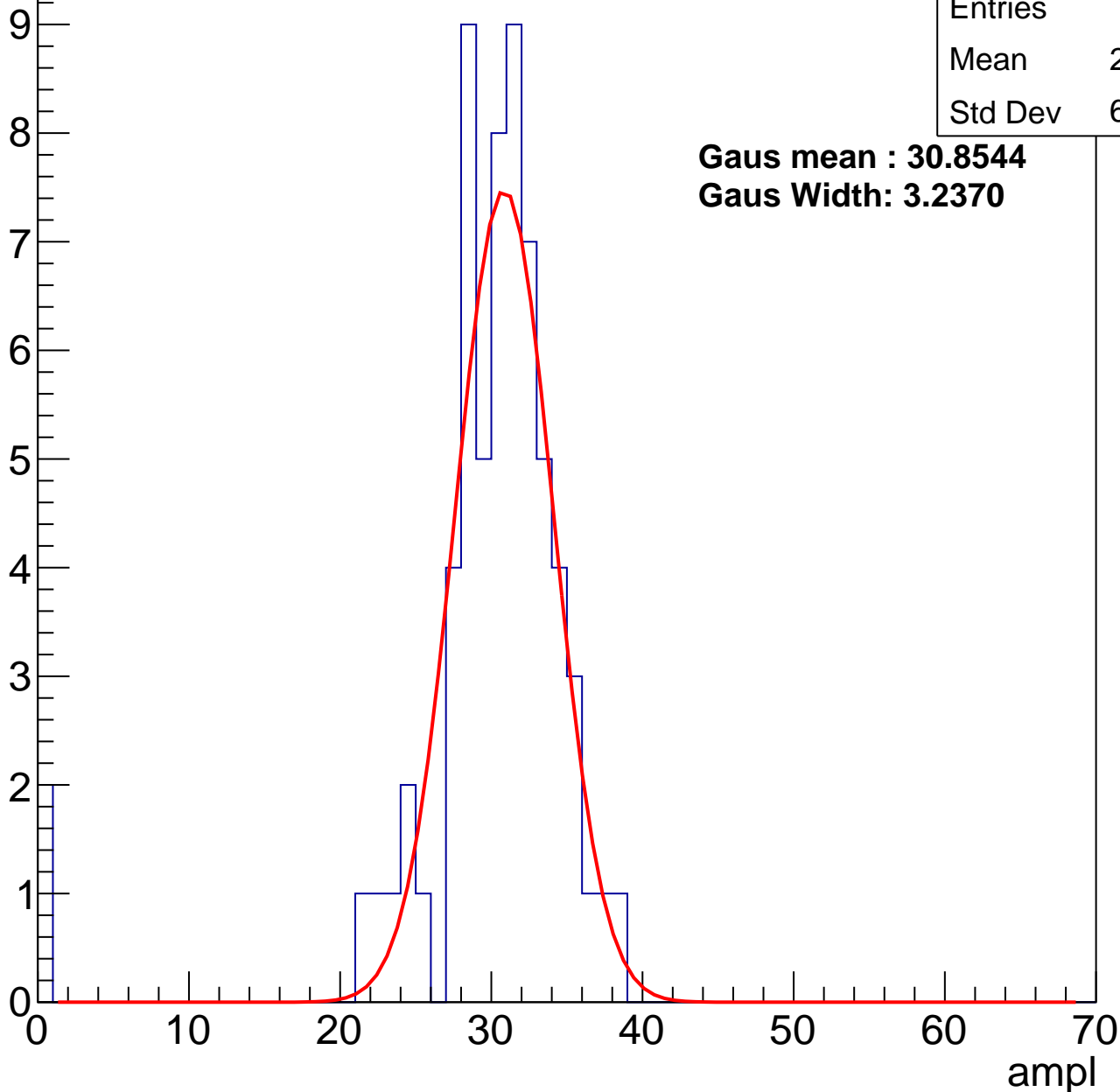
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.29
Std Dev	6.208

Gaus mean : 30.8544

Gaus Width: 3.2370



B1L100S, U11-ch1, adc1

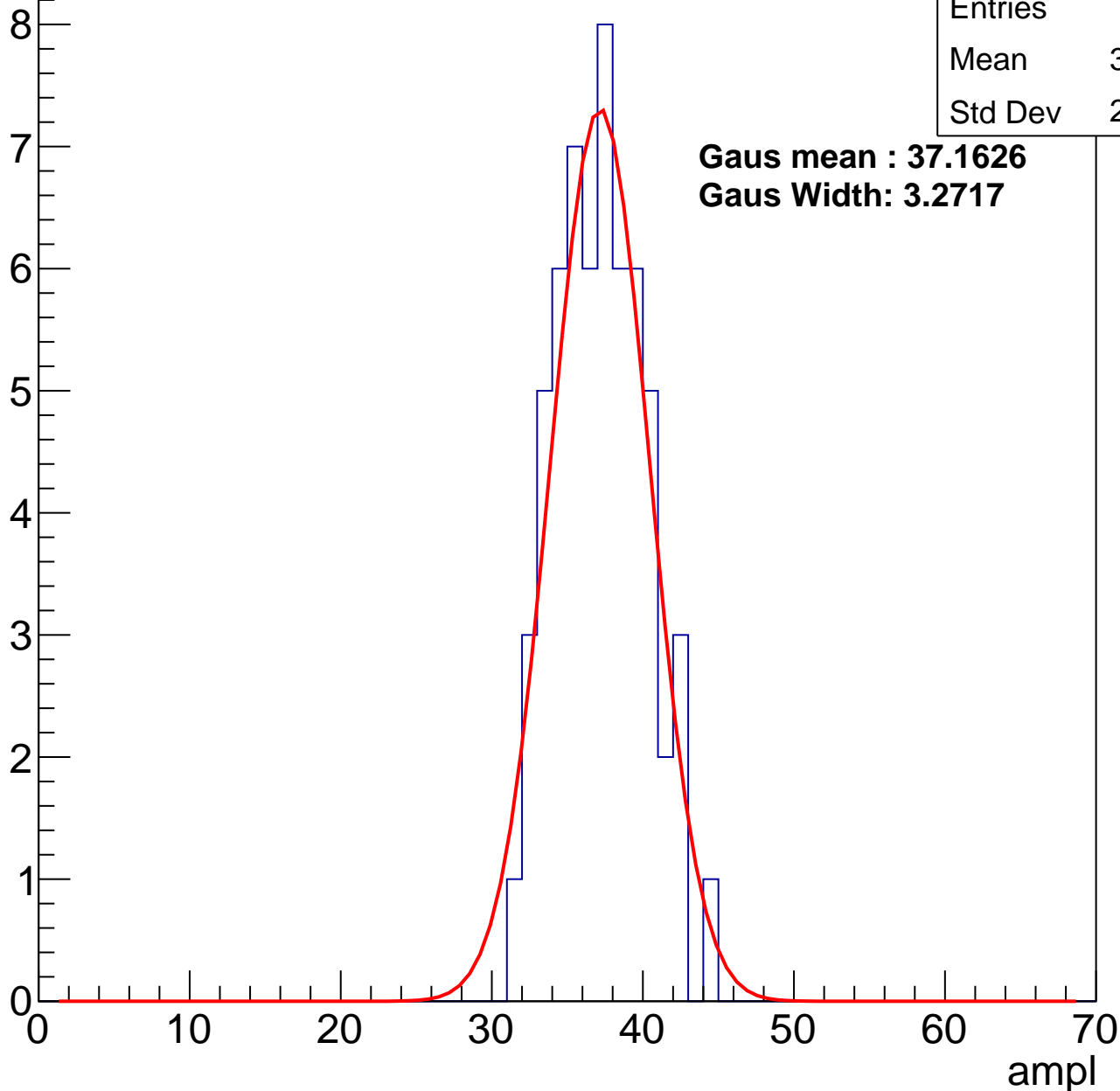
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36.73
Std Dev	2.928

Gaus mean : 37.1626

Gaus Width: 3.2717



B1L100S, U11-ch1, adc2

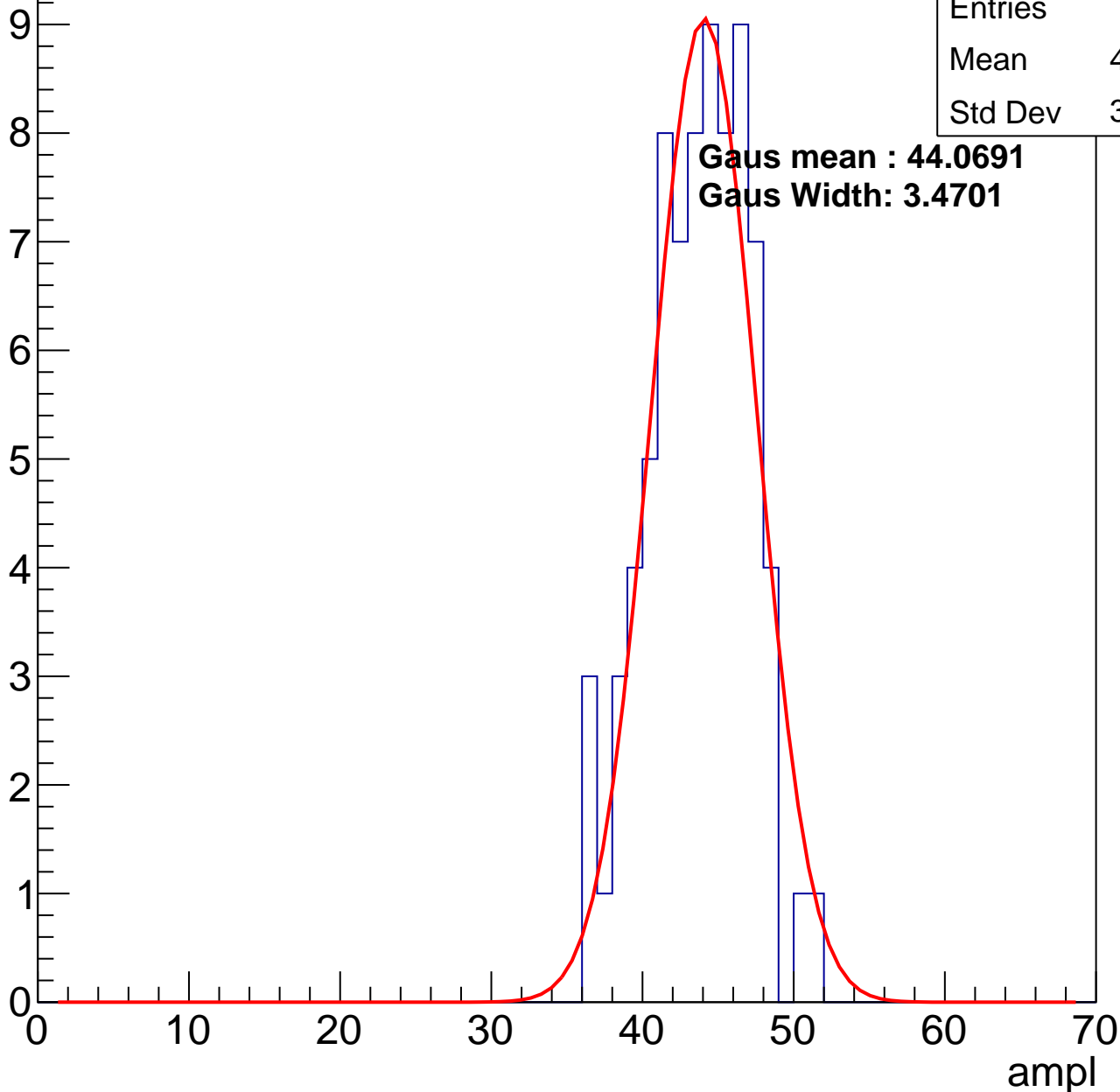
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	43.24
Std Dev	3.298

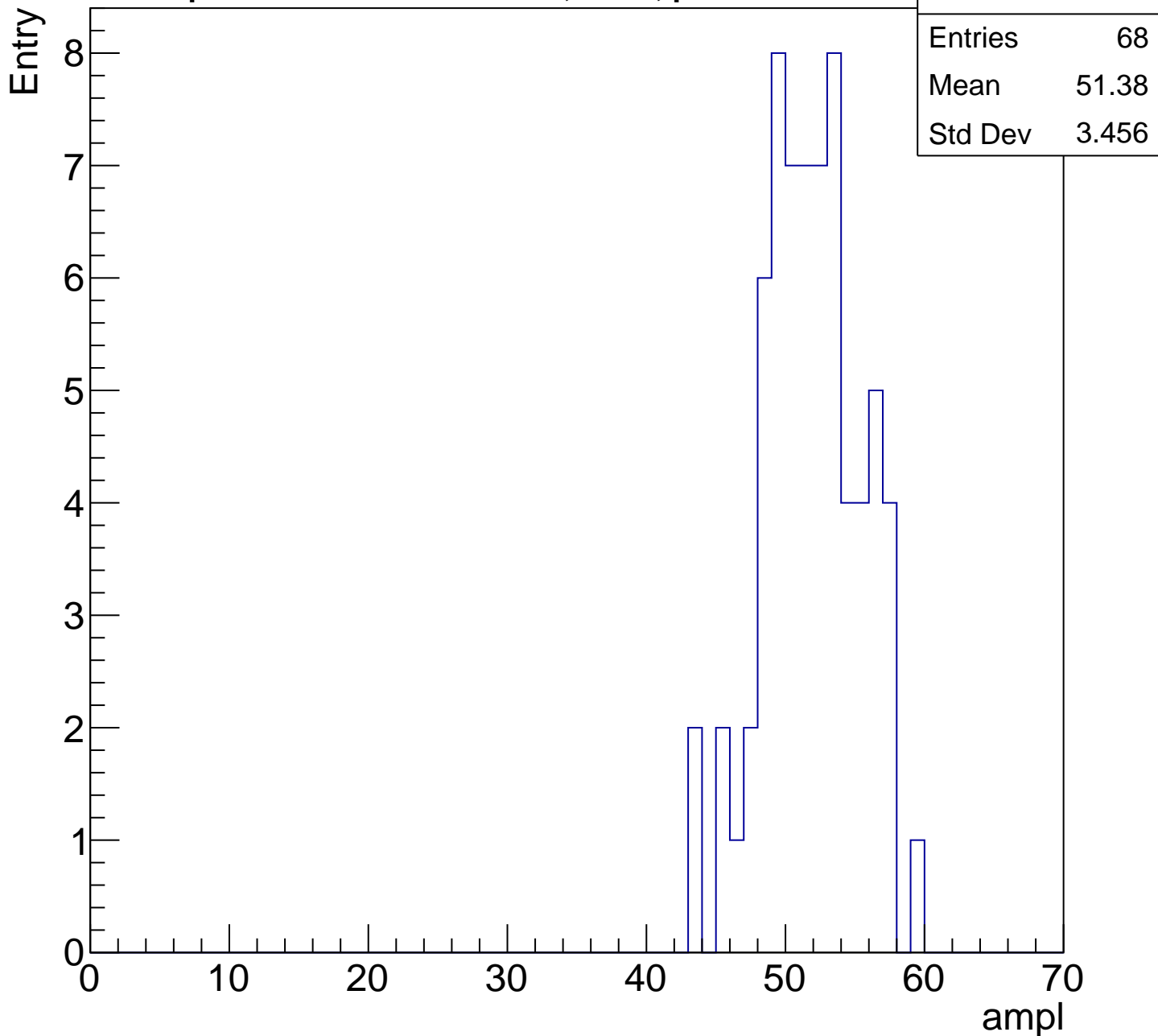
Gaus mean : 44.0691

Gaus Width: 3.4701



B1L100S, U11-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

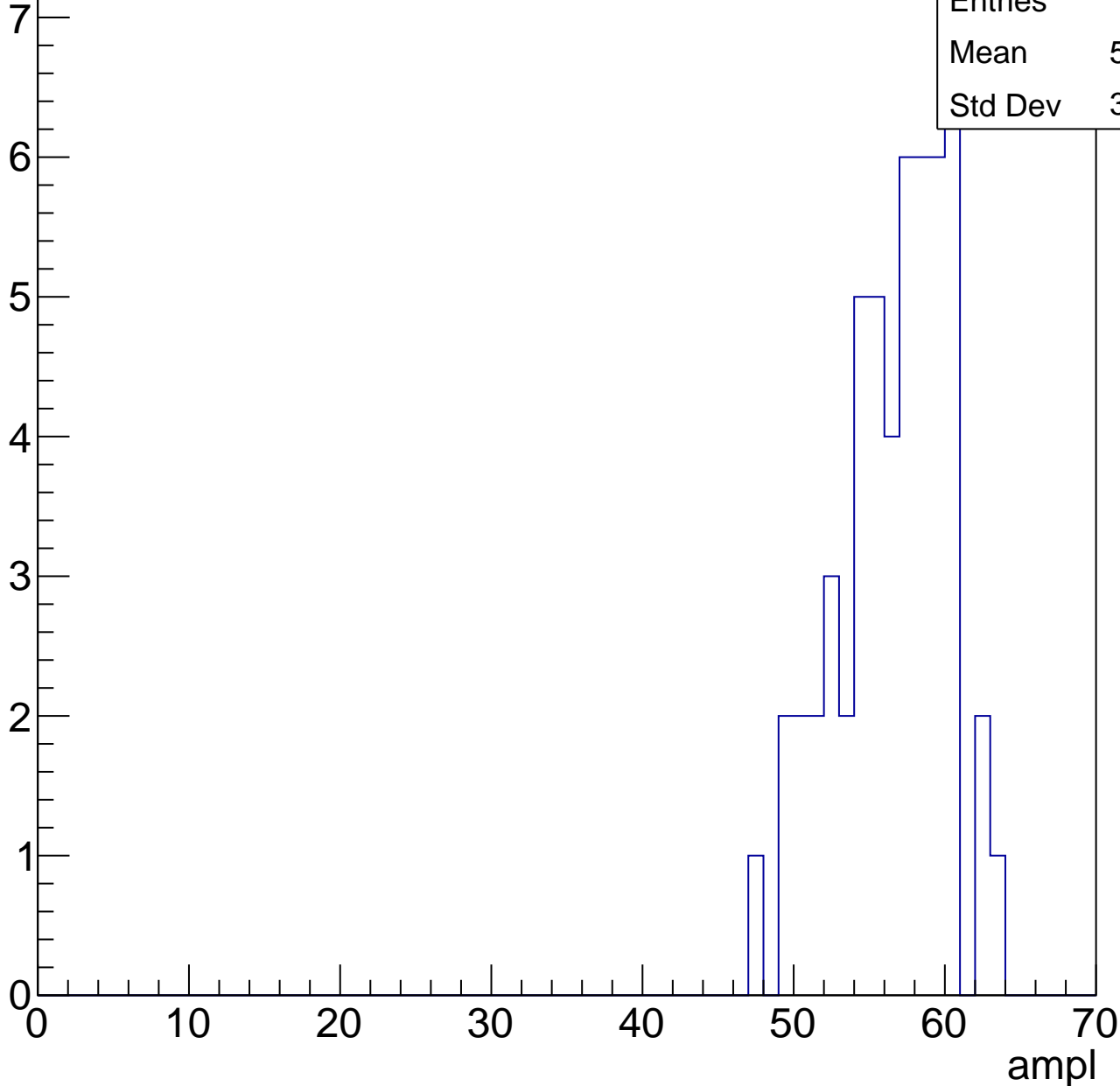


B1L100S, U11-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

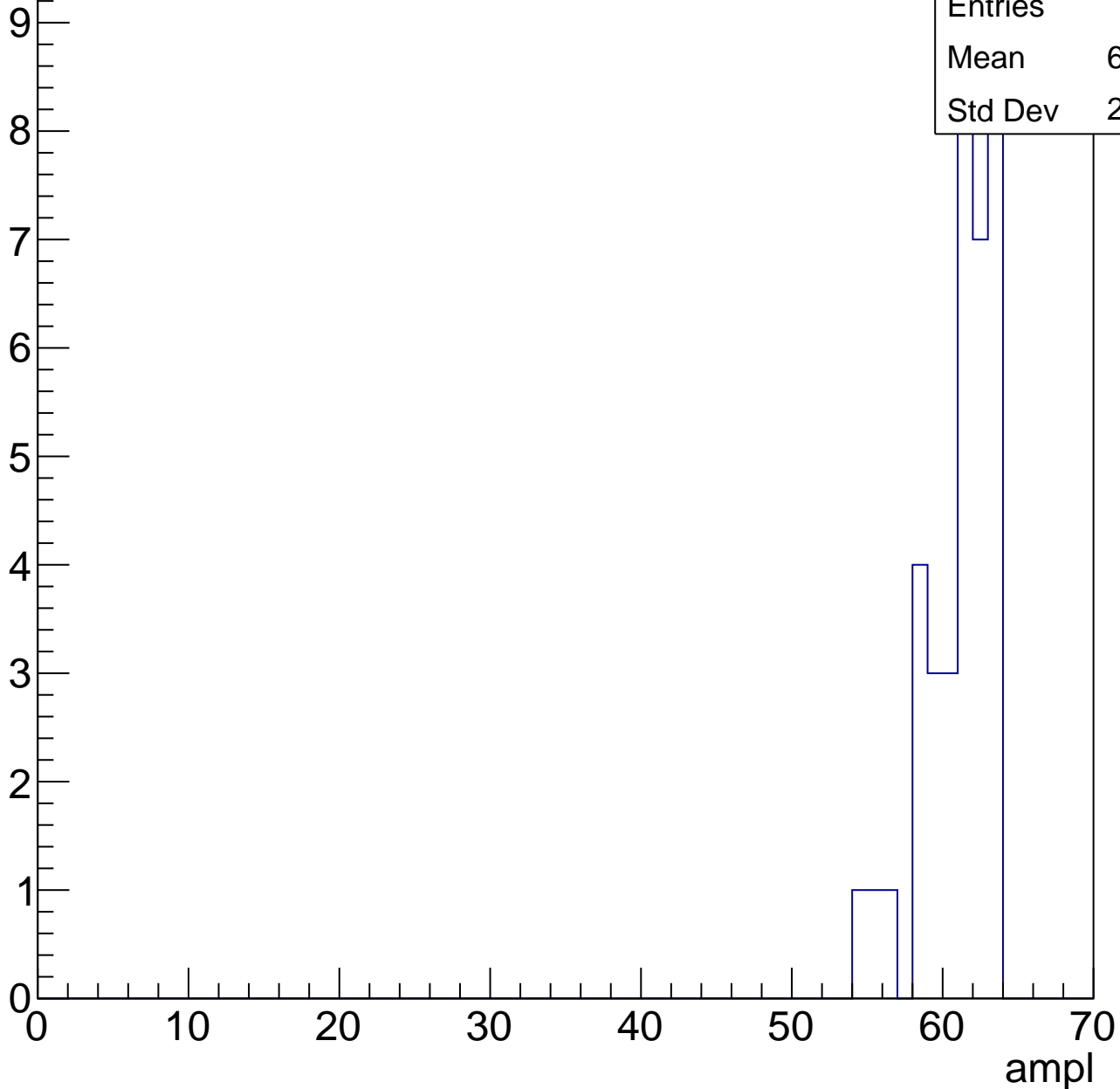
Entries	54
Mean	56.09
Std Dev	3.622



B1L100S, U11-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

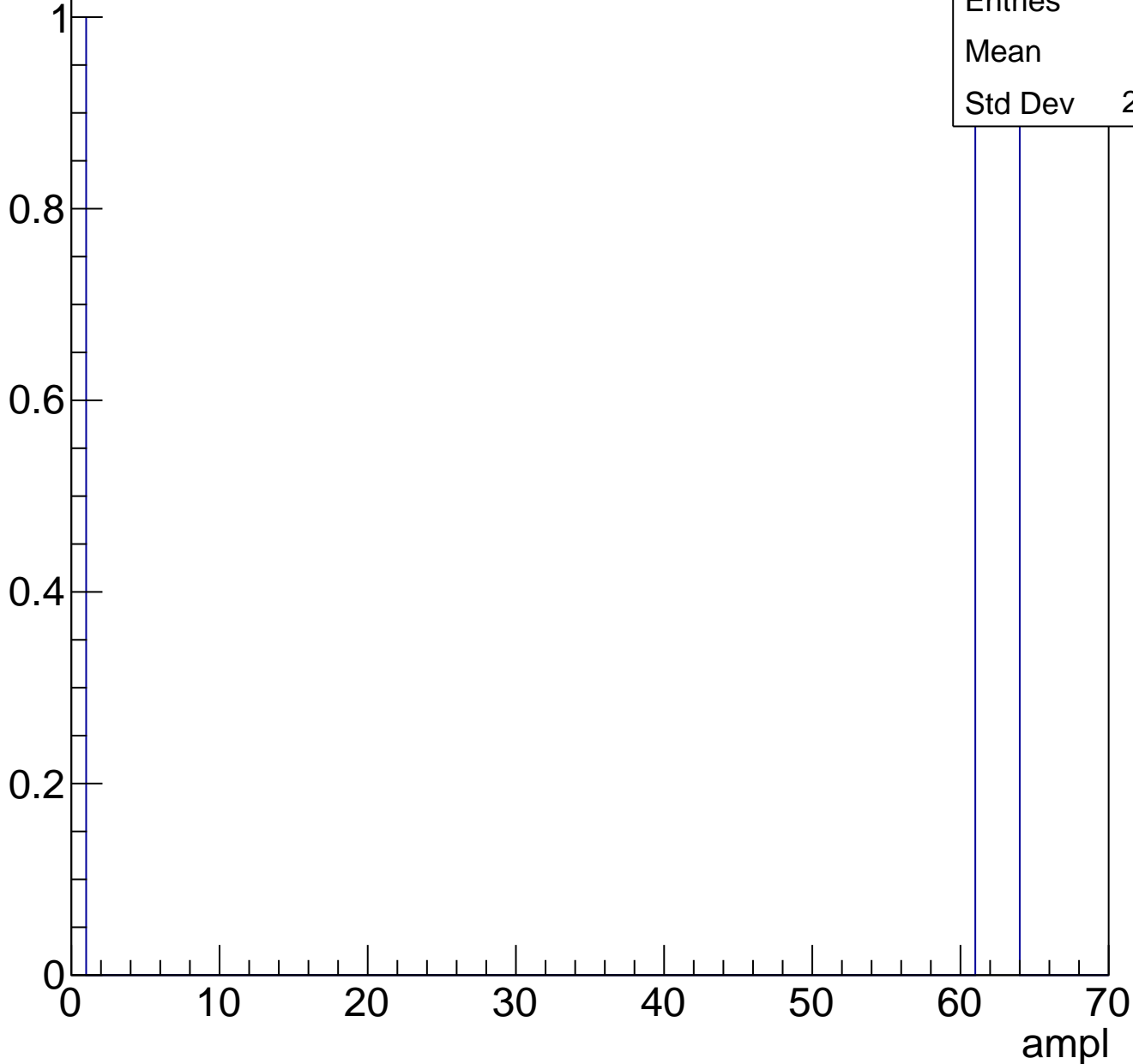
Entry



B1L100S, U11-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch2, adc0

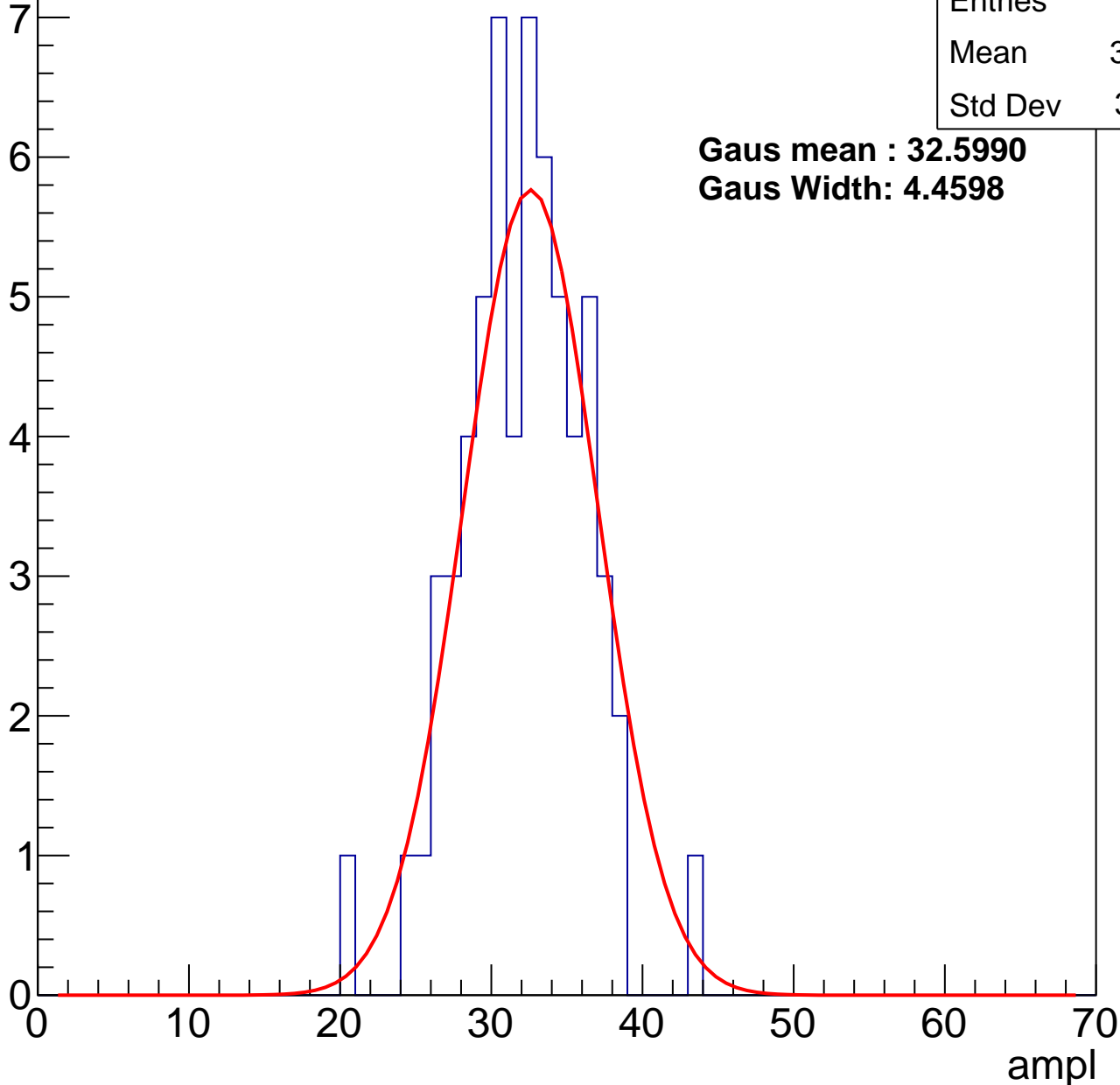
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	31.63
Std Dev	3.981

Gaus mean : 32.5990

Gaus Width: 4.4598



B1L100S, U11-ch2, adc1

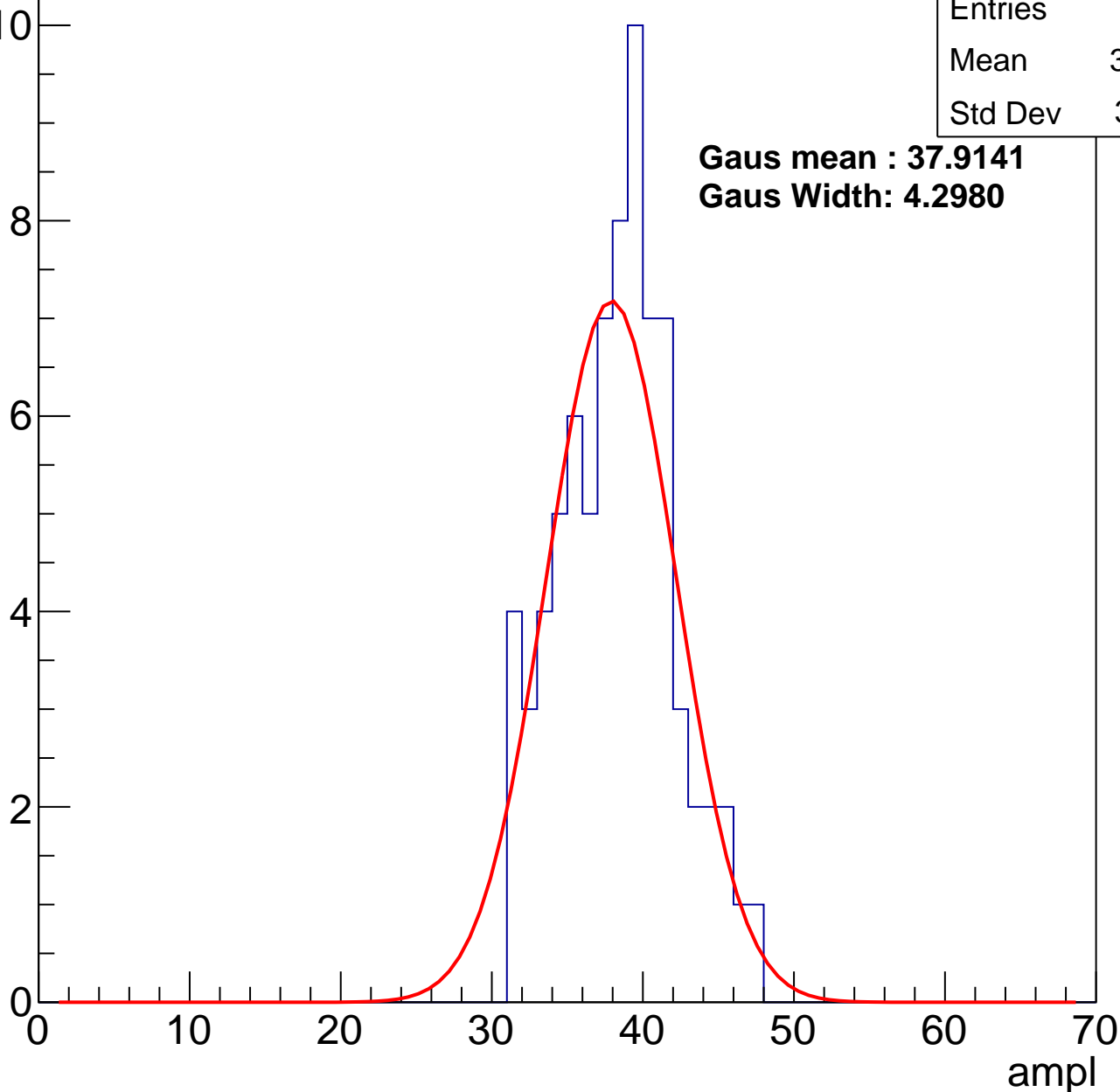
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	37.86
Std Dev	3.741

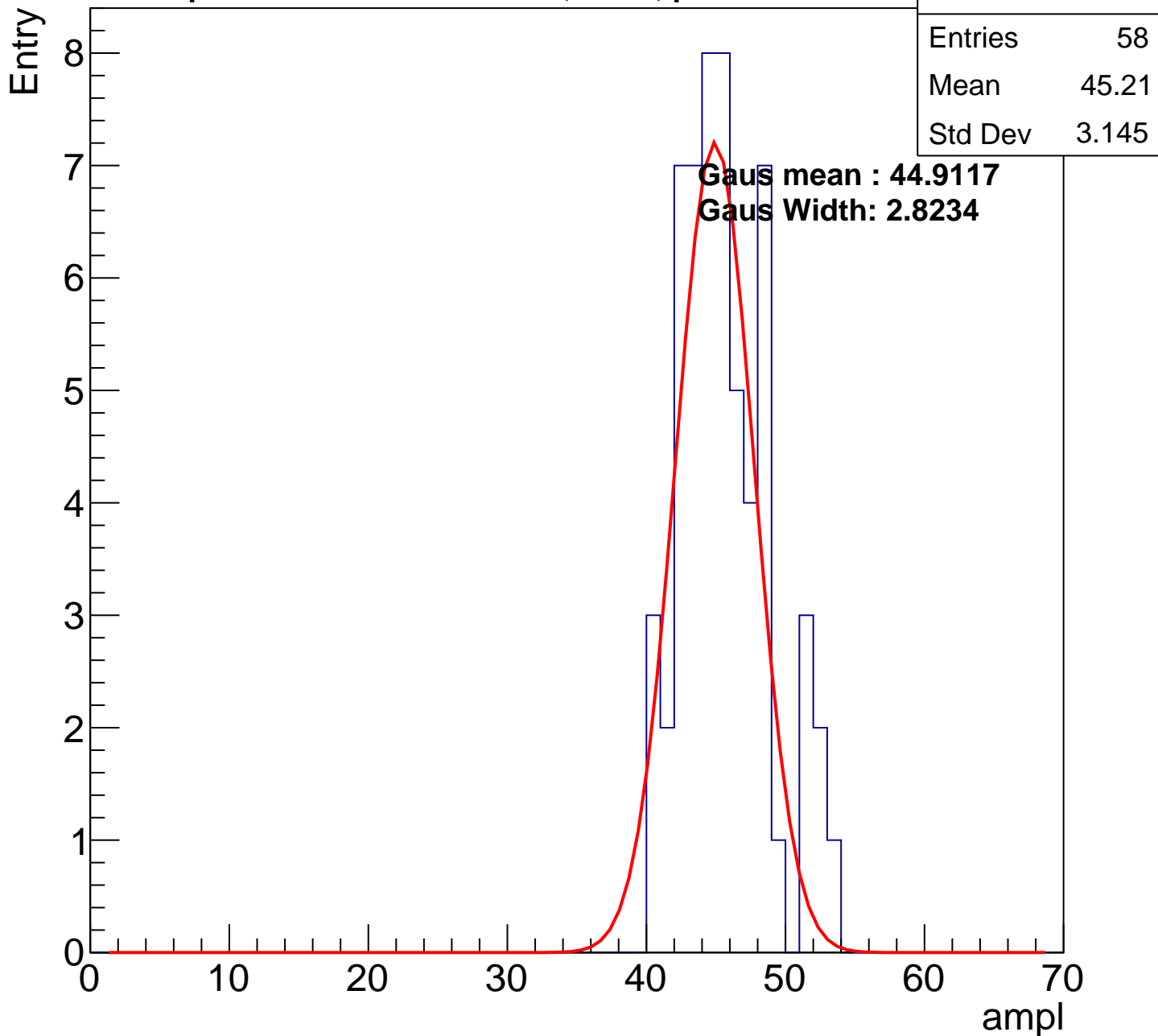
Gaus mean : 37.9141

Gaus Width: 4.2980



B1L100S, U11-ch2, adc2

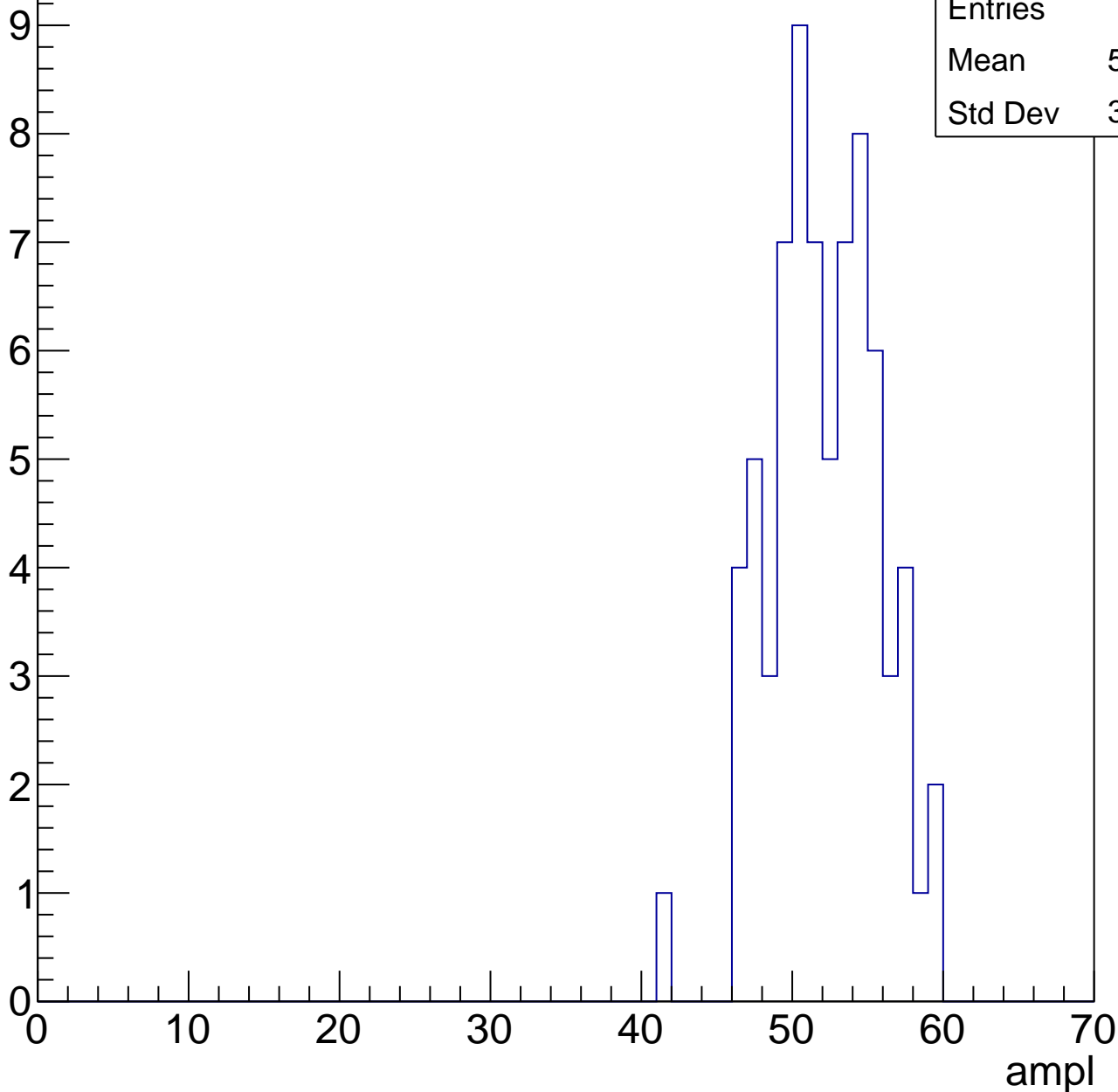
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

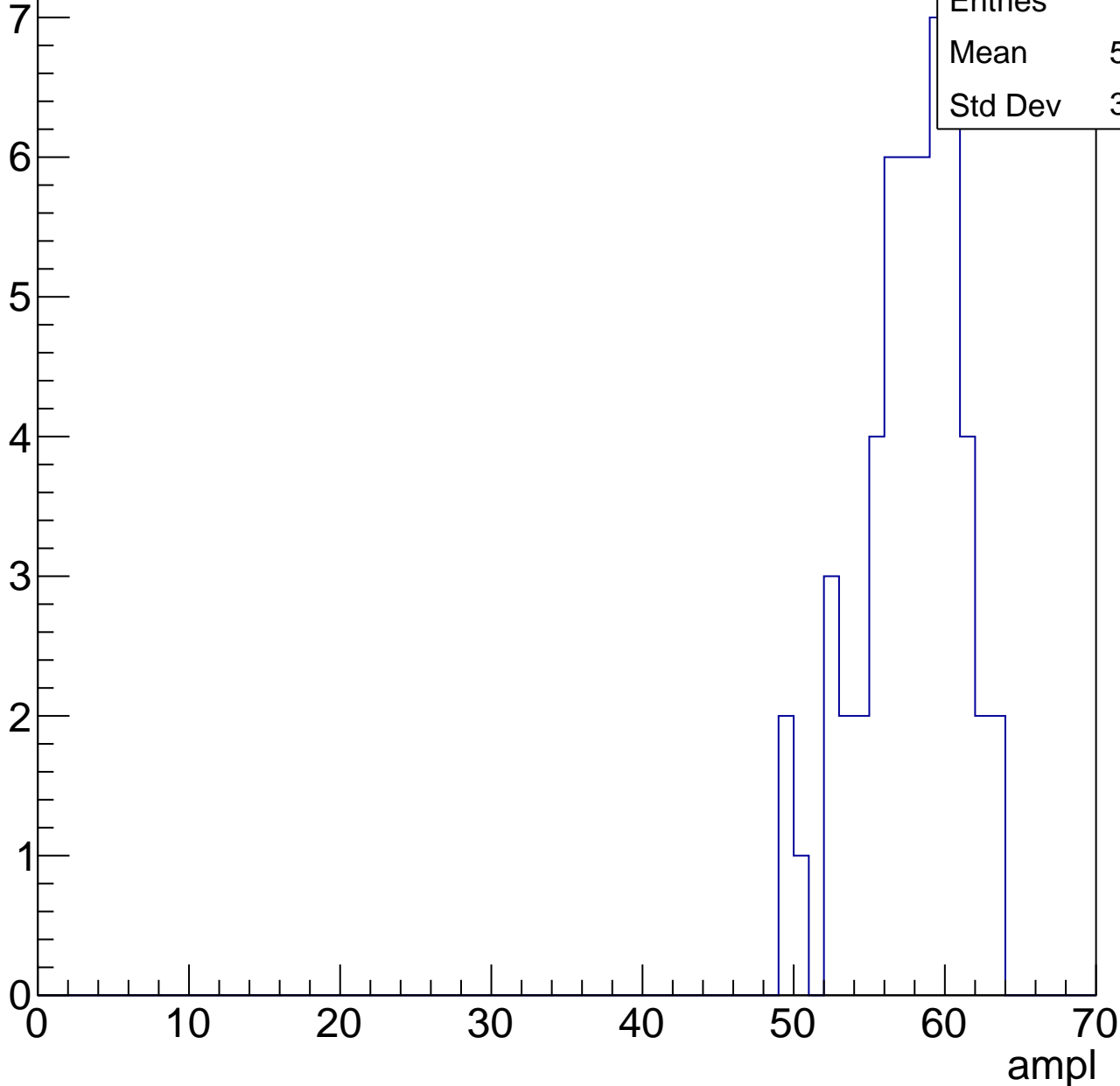


B1L100S, U11-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.24
Std Dev	3.344

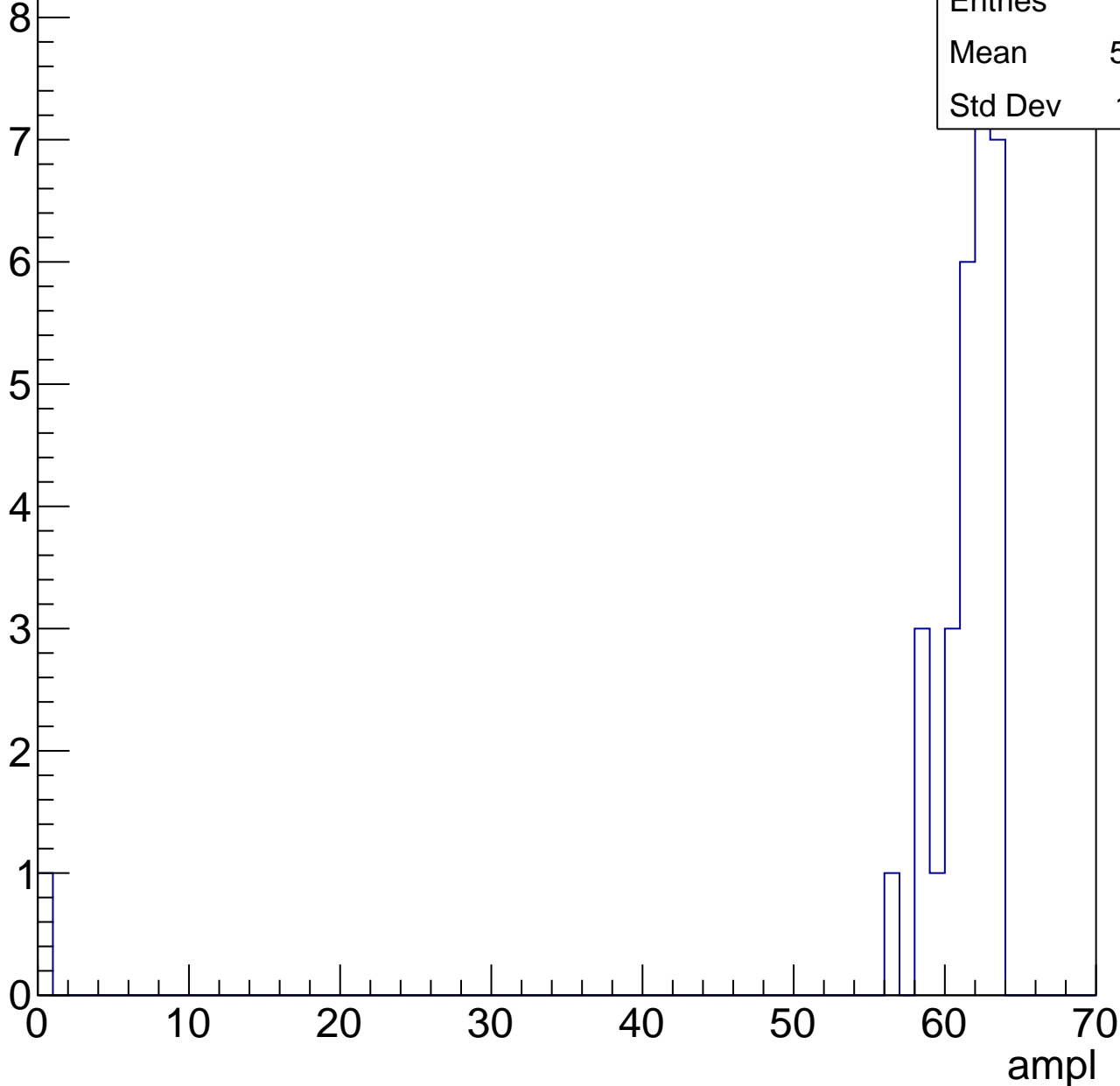


B1L100S, U11-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

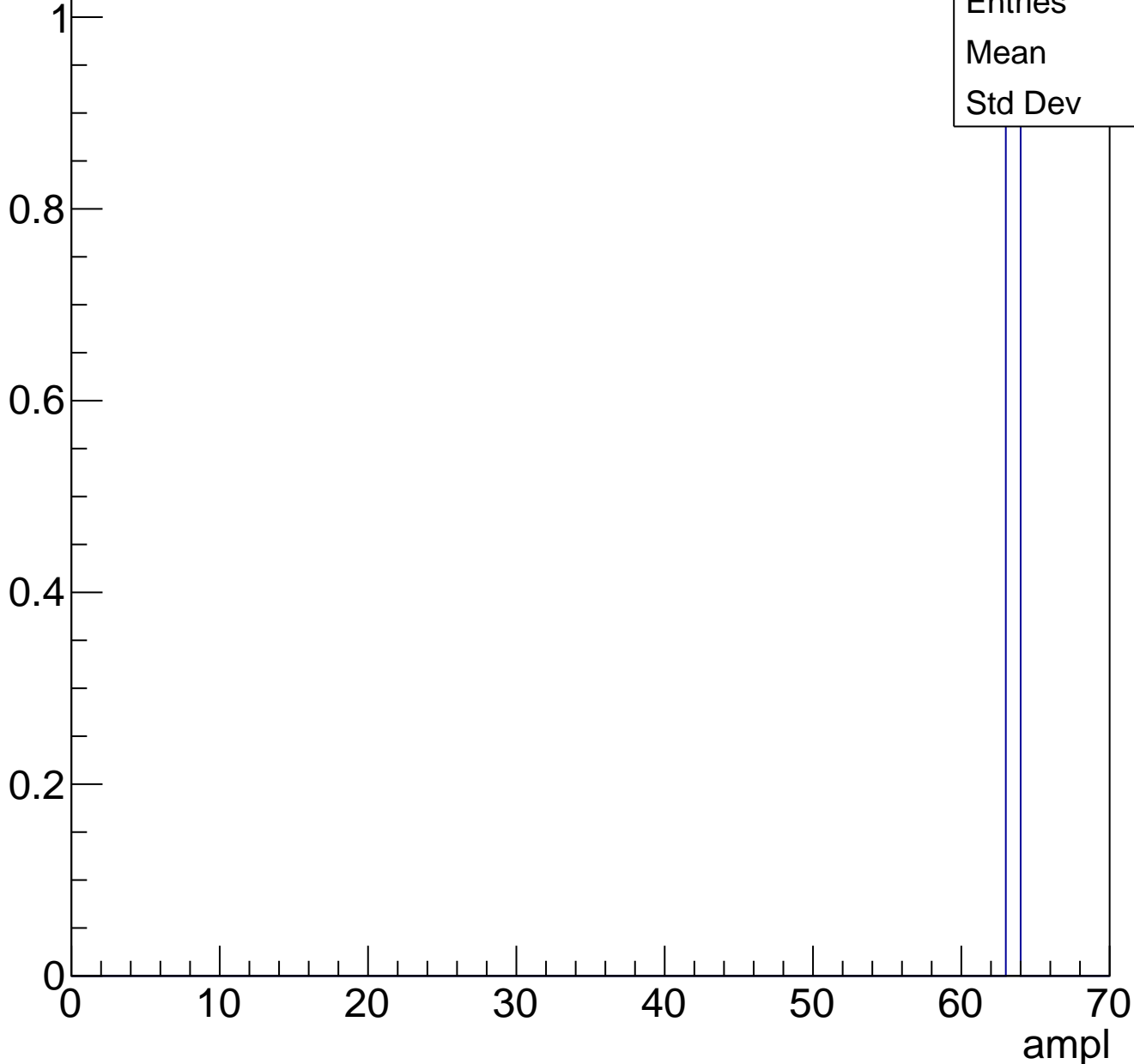
Entries	30
Mean	59.07
Std Dev	11.11



B1L100S, U11-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch3, adc0

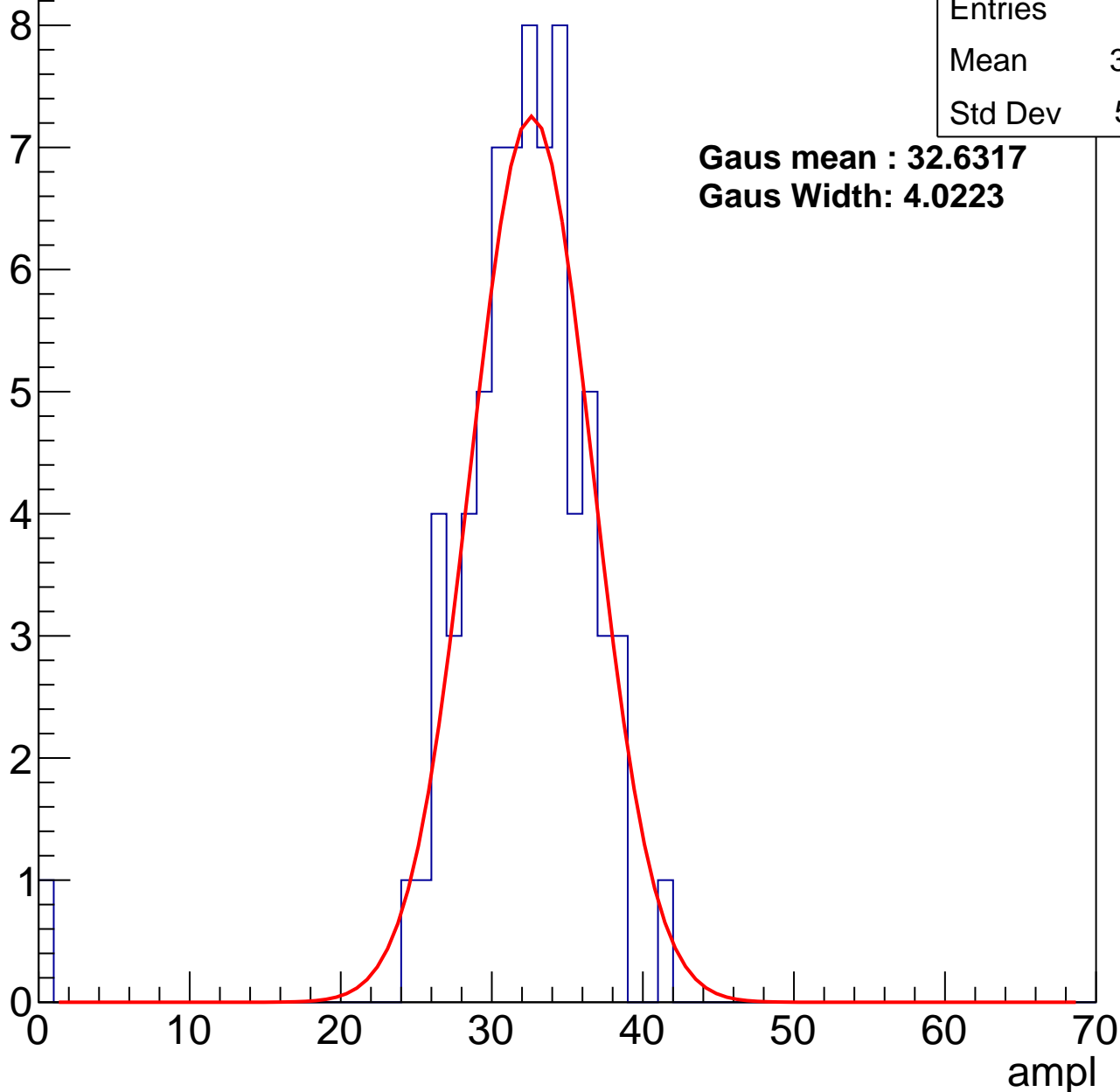
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	31.43
Std Dev	5.131

Gaus mean : 32.6317

Gaus Width: 4.0223



B1L100S, U11-ch3, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	39.38
Std Dev	3.809

Gaus mean : 39.6563

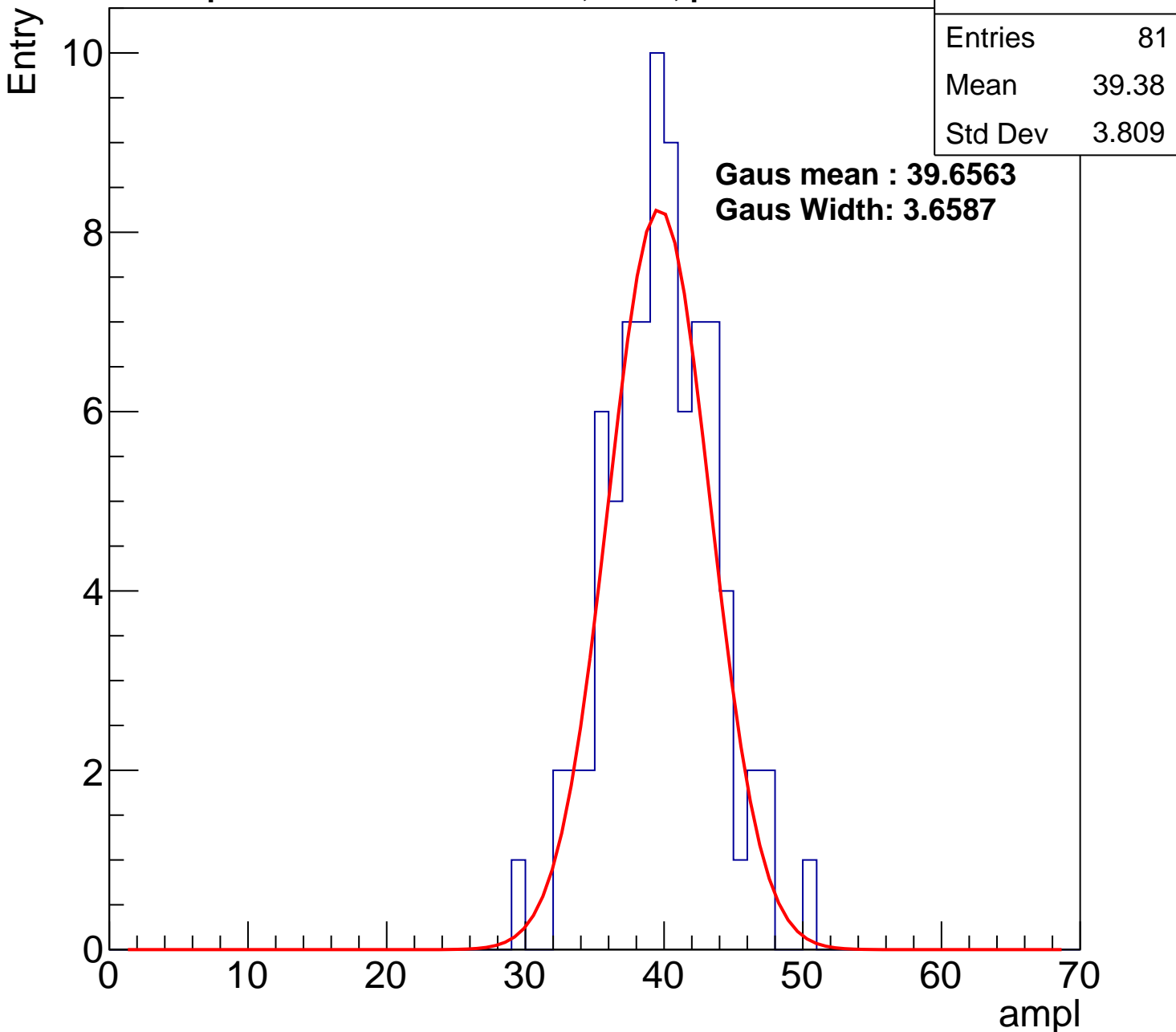
Gaus Width: 3.6587

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch3, adc2

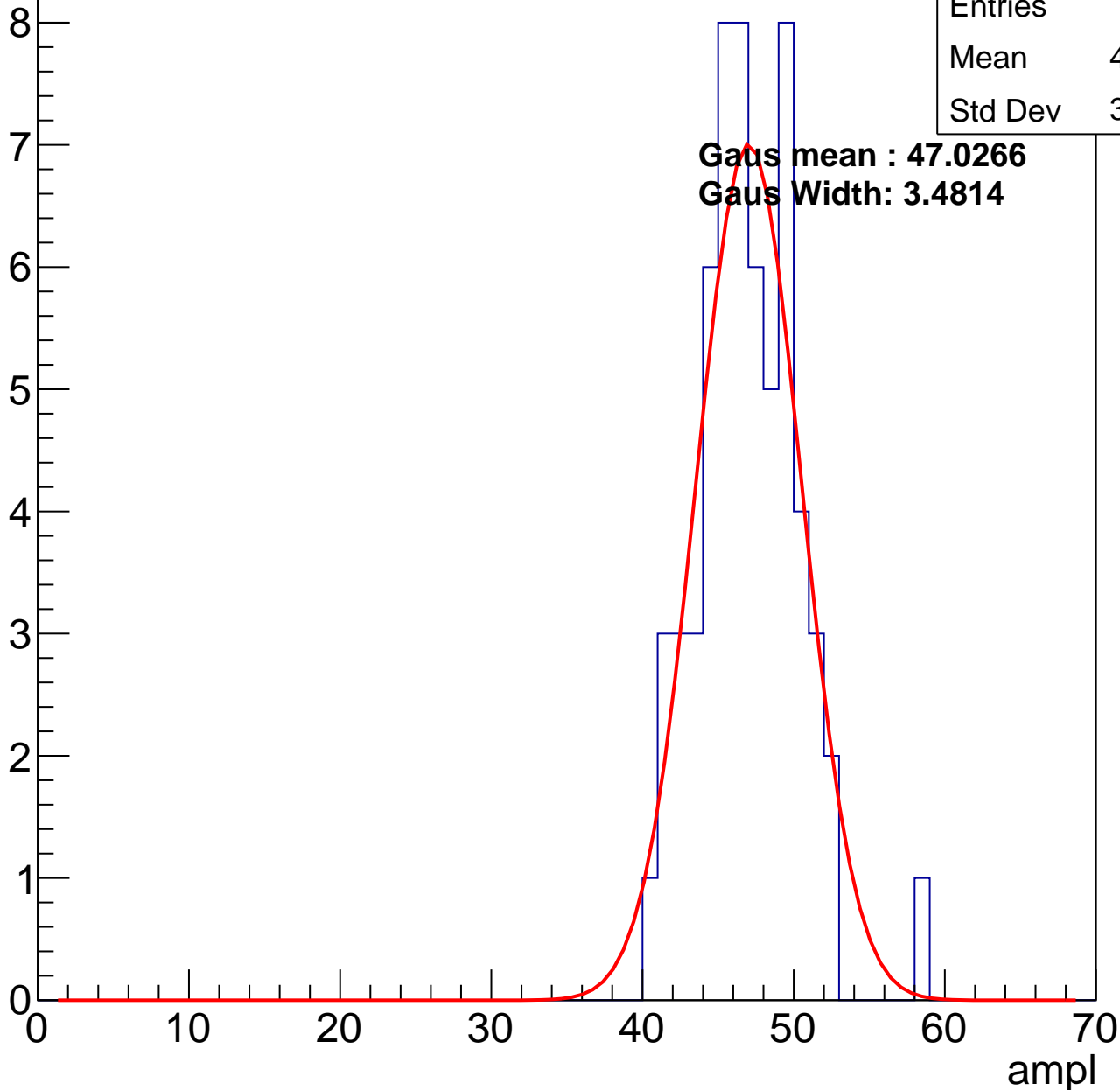
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	46.54
Std Dev	3.272

Gaus mean : 47.0266

Gaus Width: 3.4814

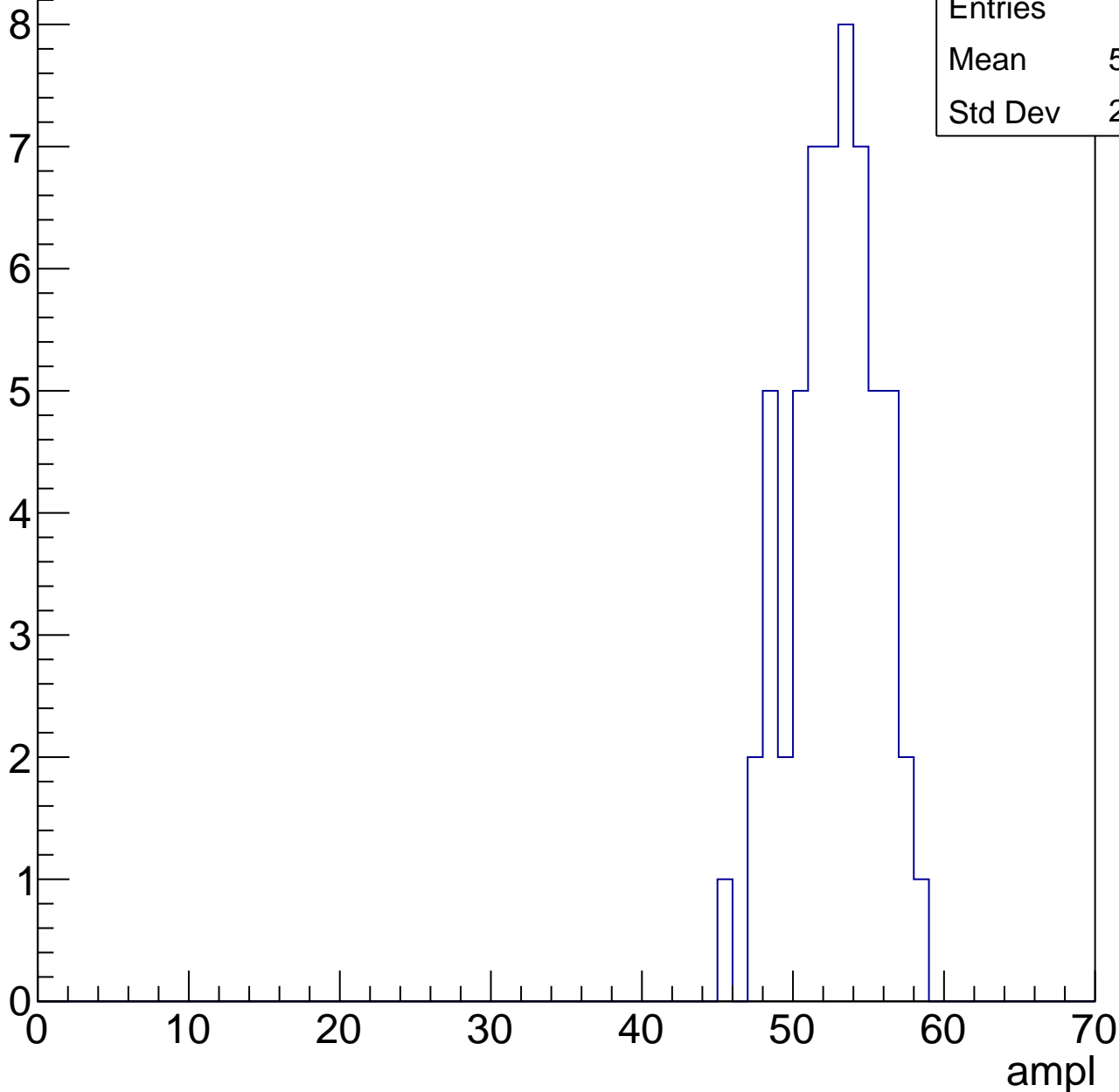


B1L100S, U11-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

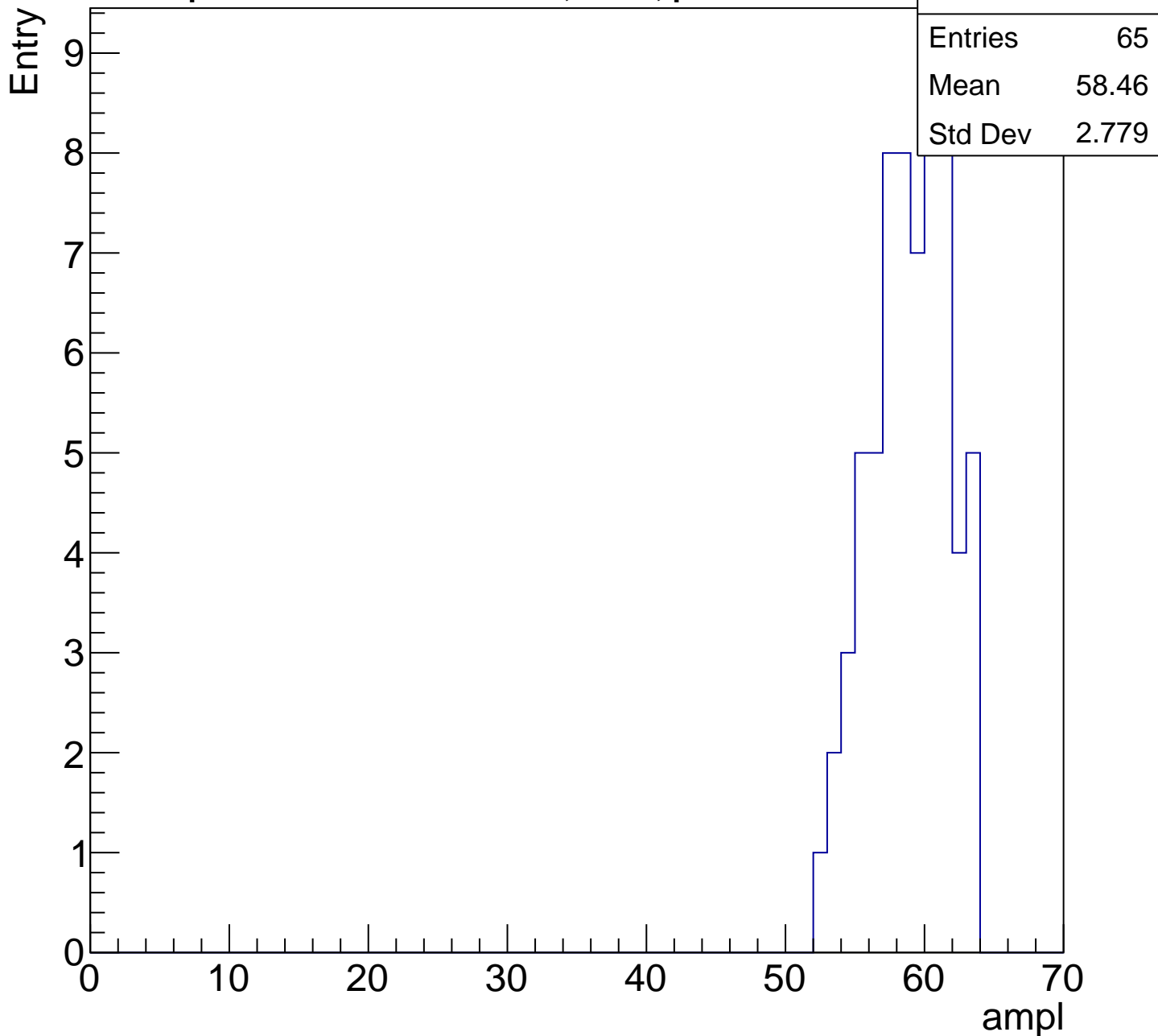
Entry

Entries	57
Mean	52.23
Std Dev	2.866



B1L100S, U11-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

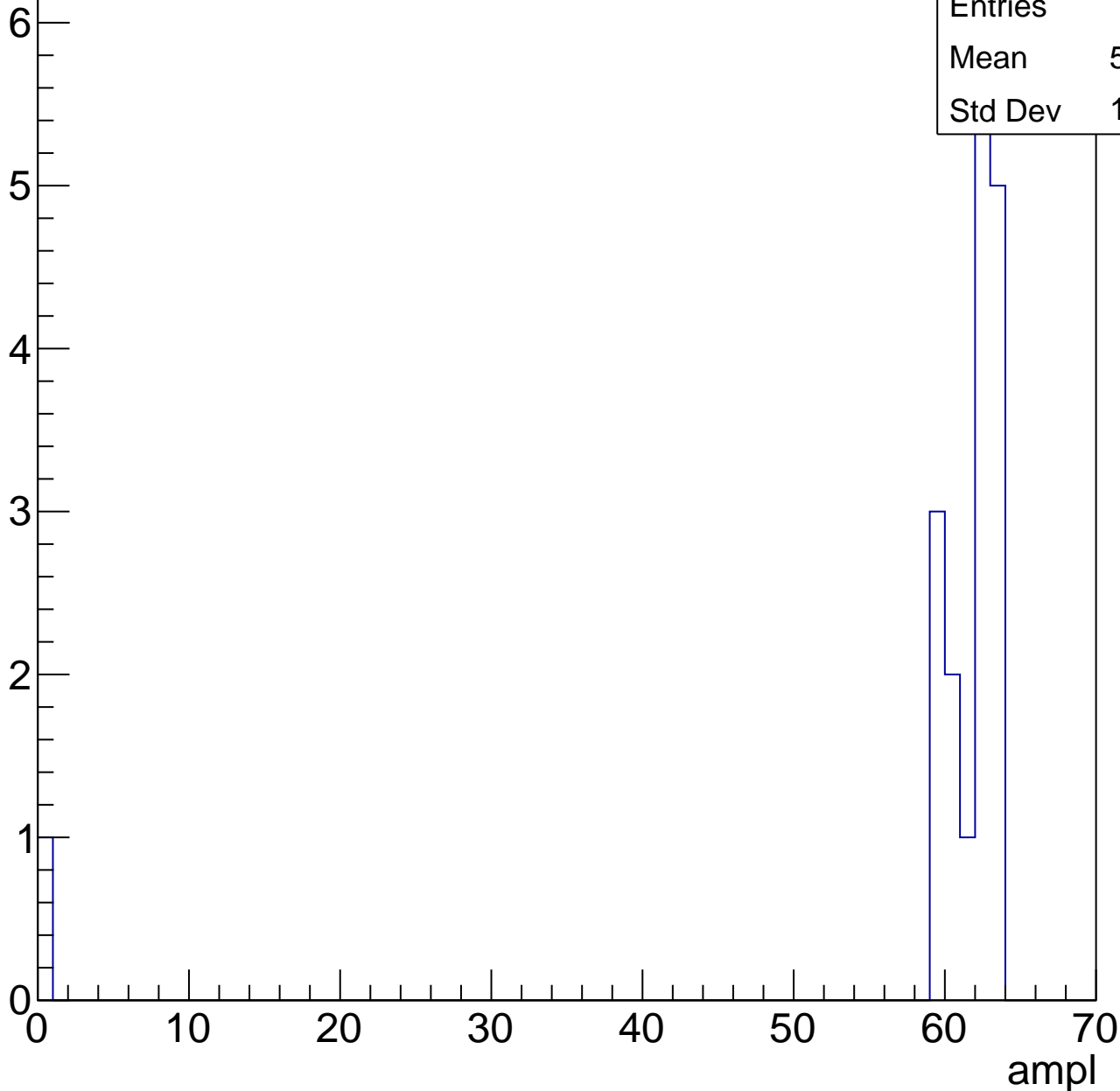


B1L100S, U11-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	58.06
Std Dev	14.15



B1L100S, U11-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

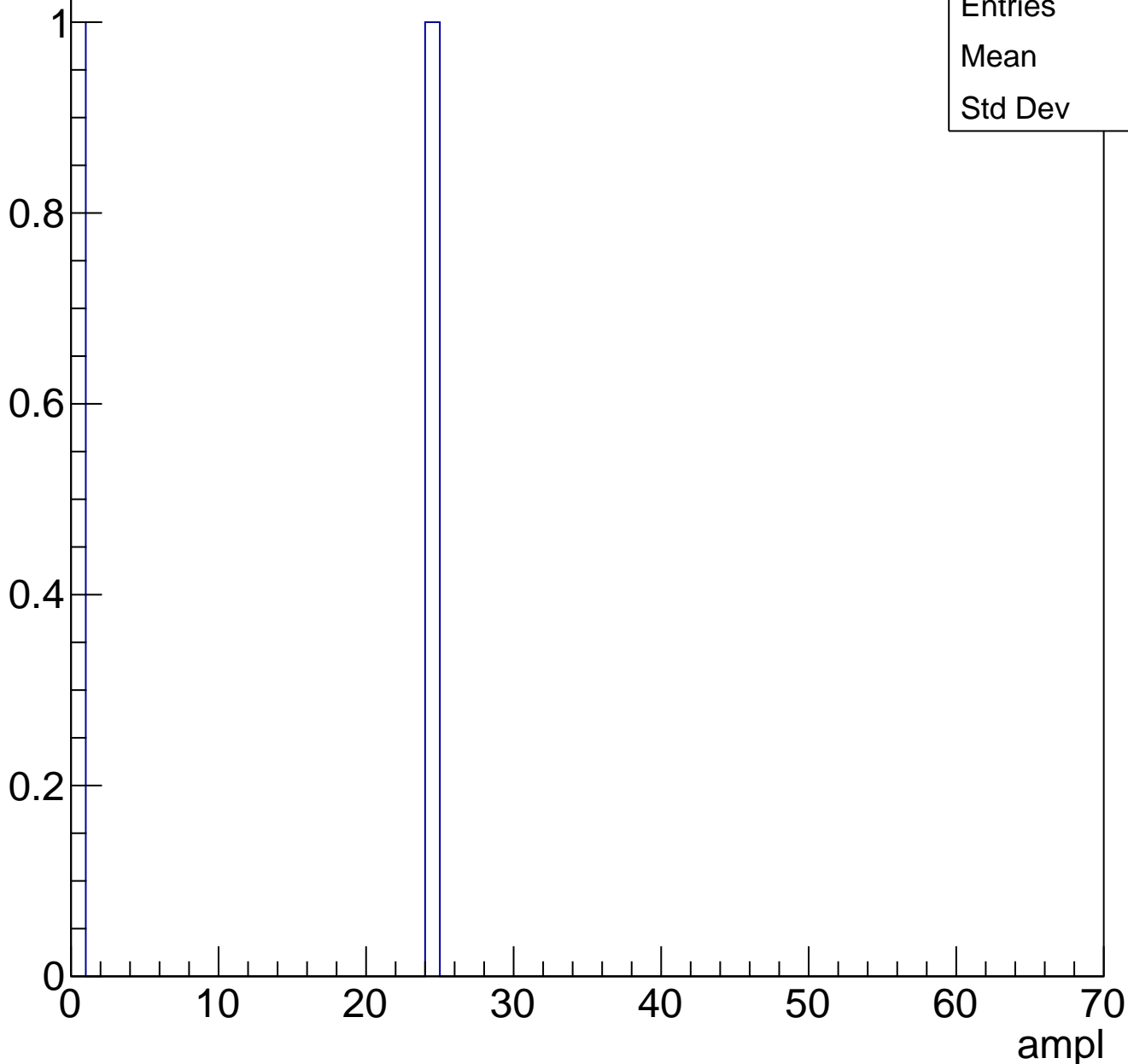


Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch4, adc0

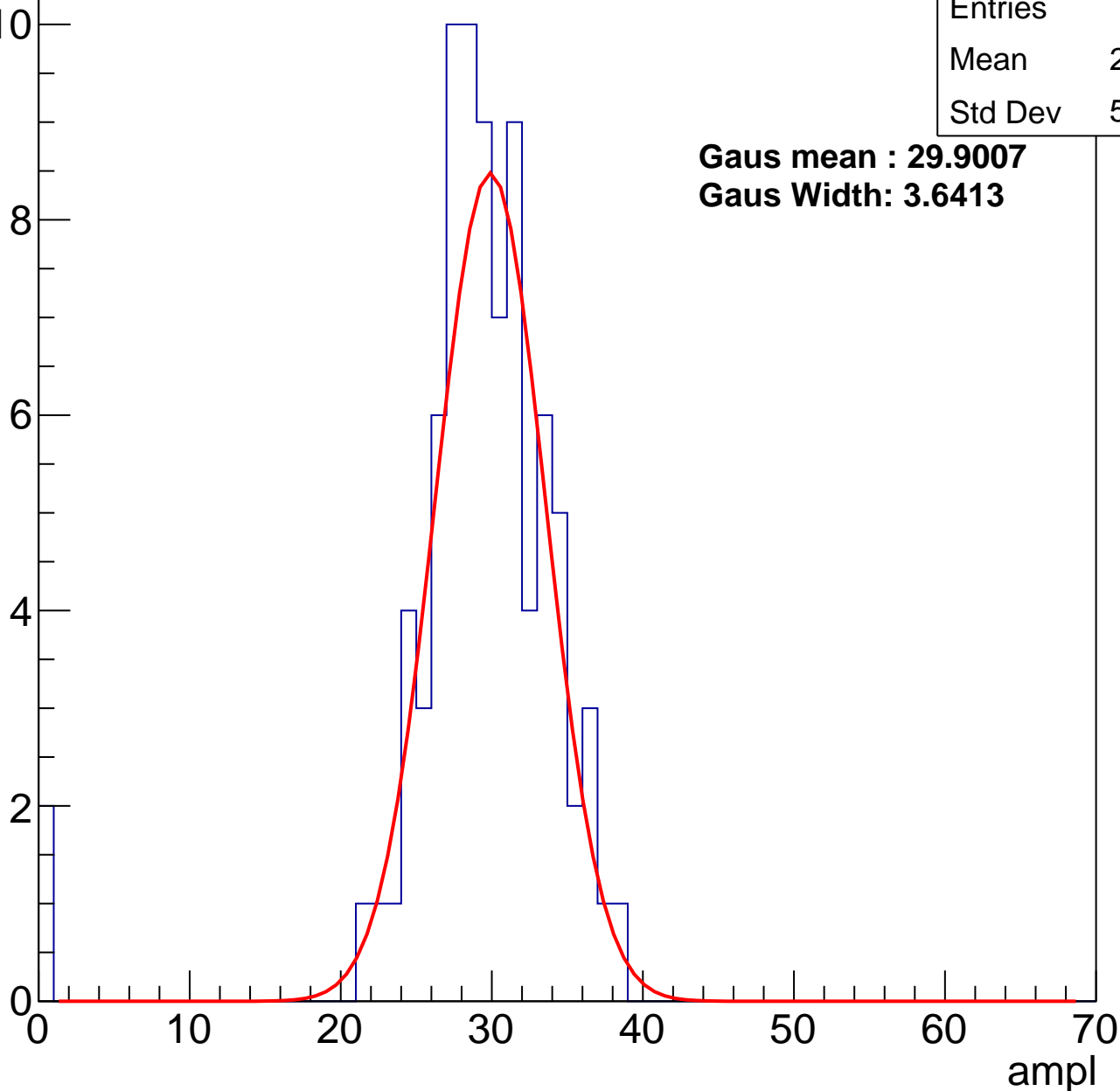
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	28.73
Std Dev	5.678

Gaus mean : 29.9007

Gaus Width: 3.6413



B1L100S, U11-ch4, adc1

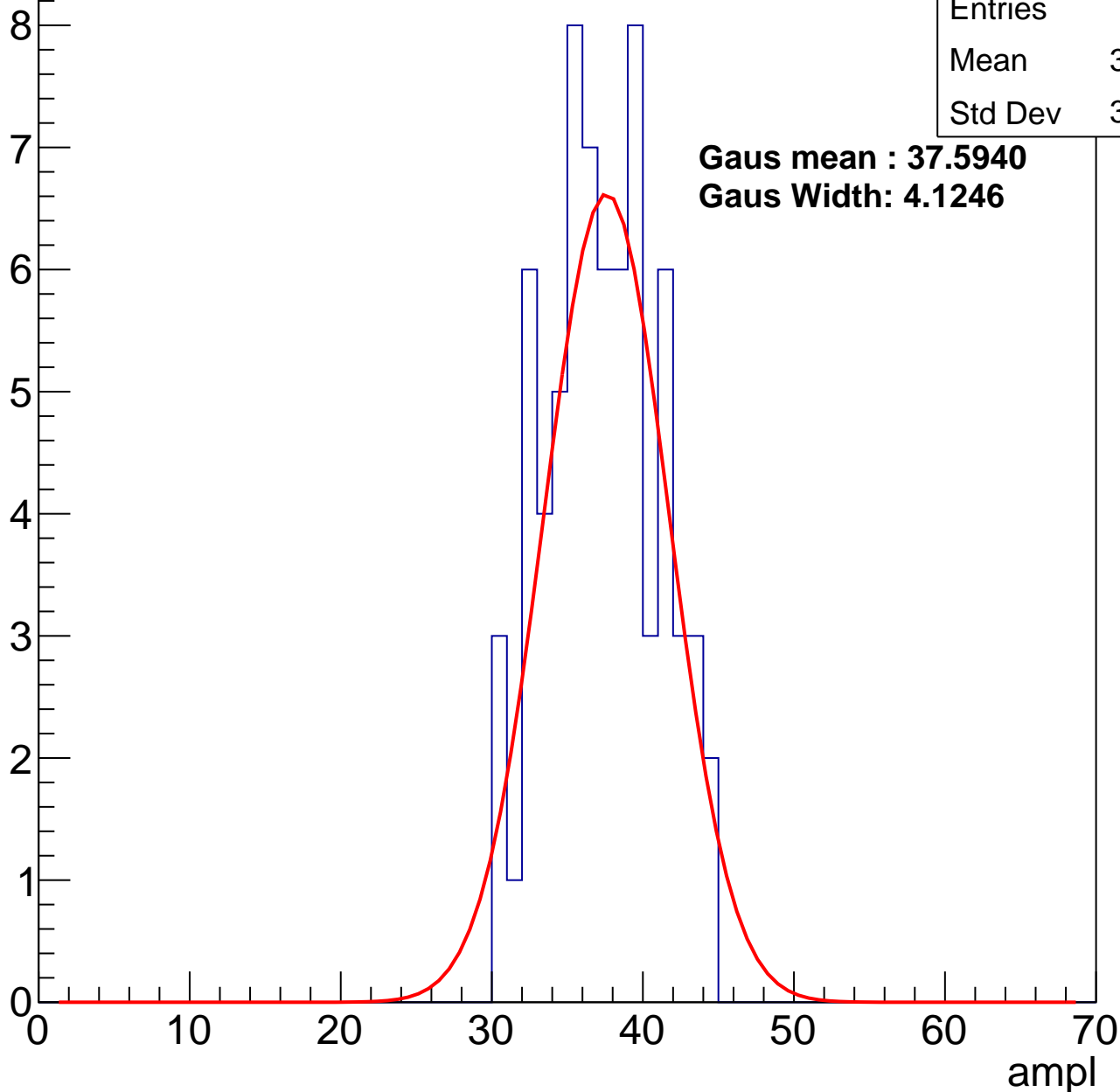
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.87
Std Dev	3.603

Gaus mean : 37.5940

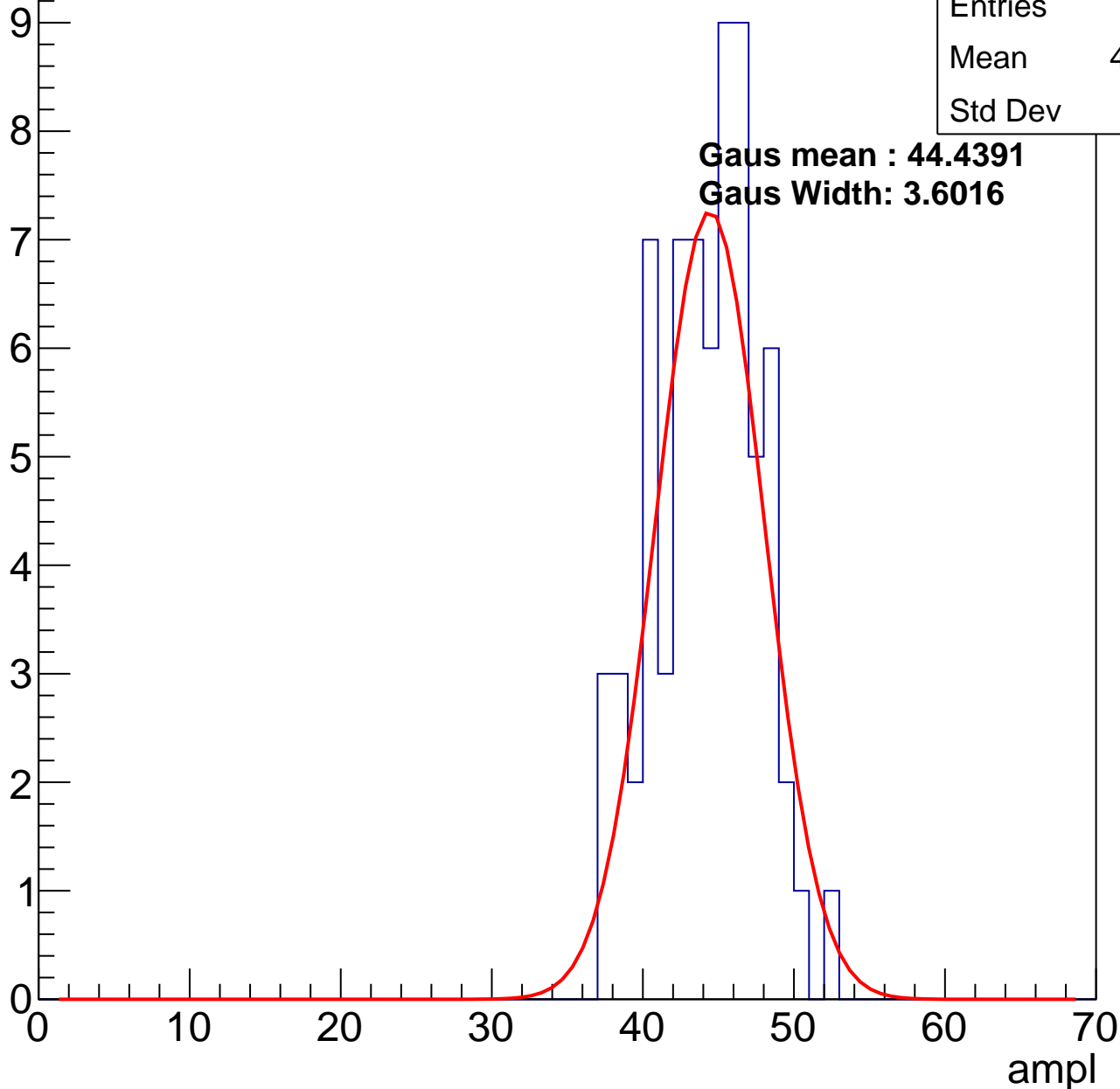
Gaus Width: 4.1246



B1L100S, U11-ch4, adc2

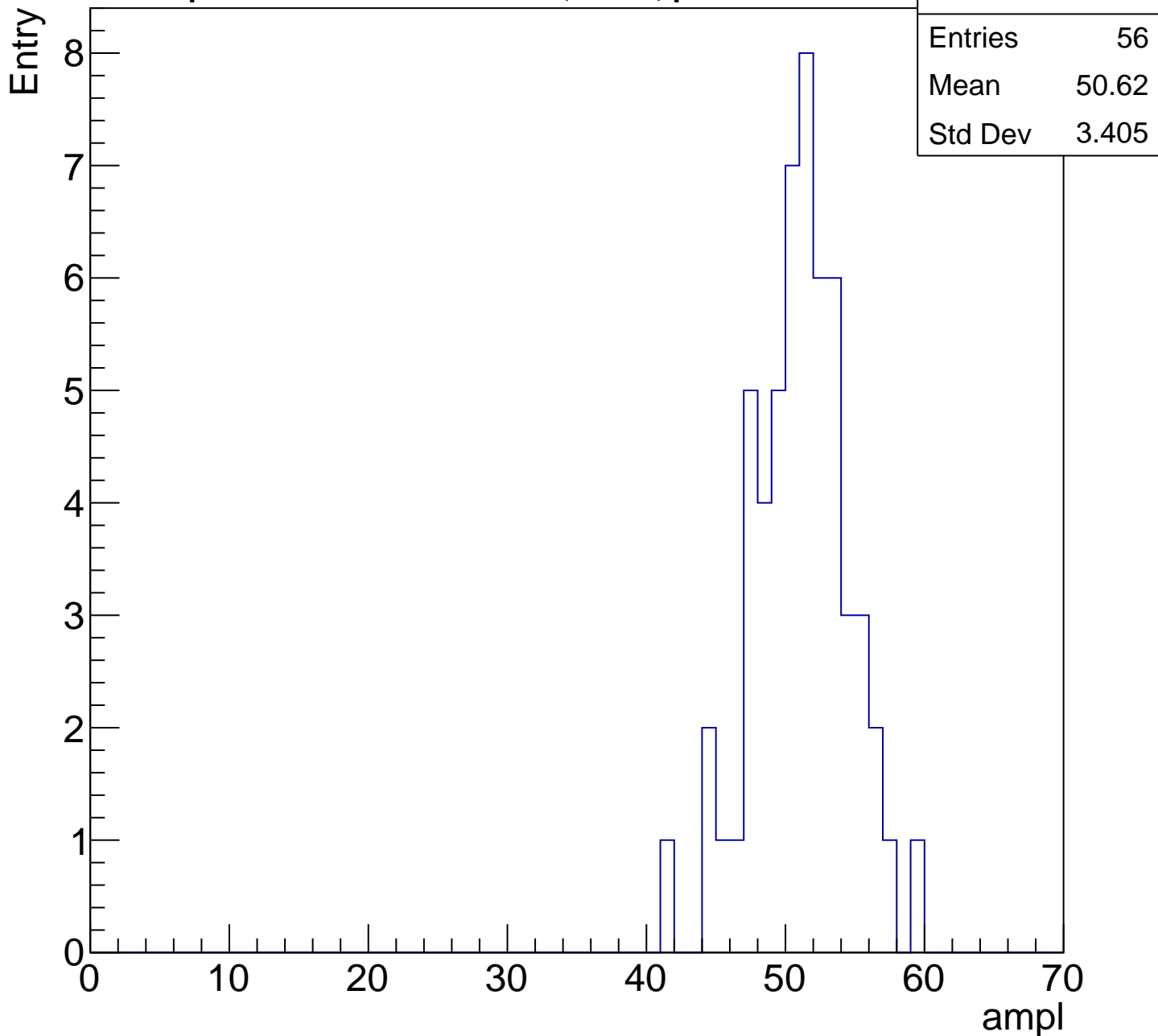
calib_packv5_042523_0143.root, FC#4, port A2

Entry



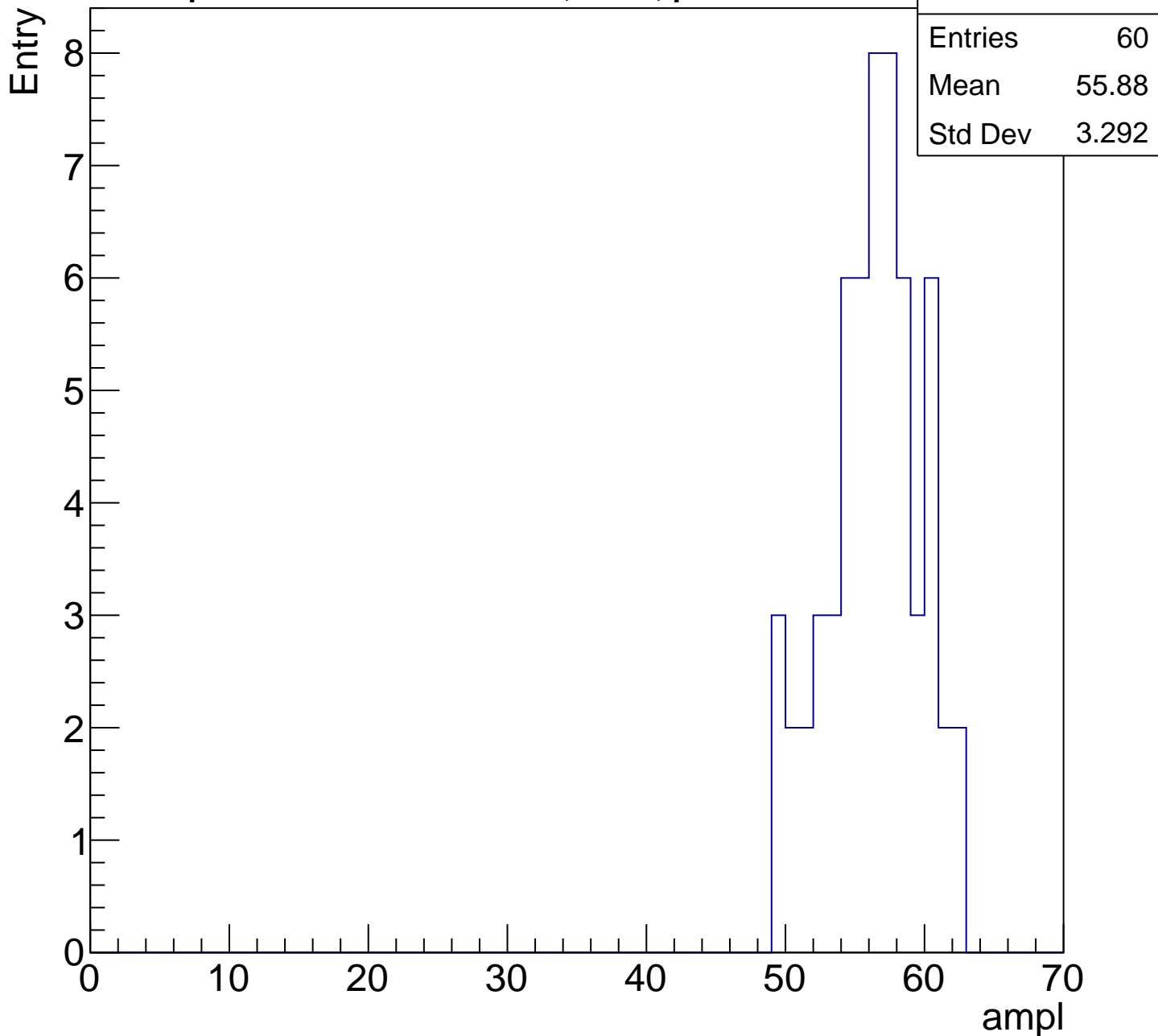
B1L100S, U11-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2



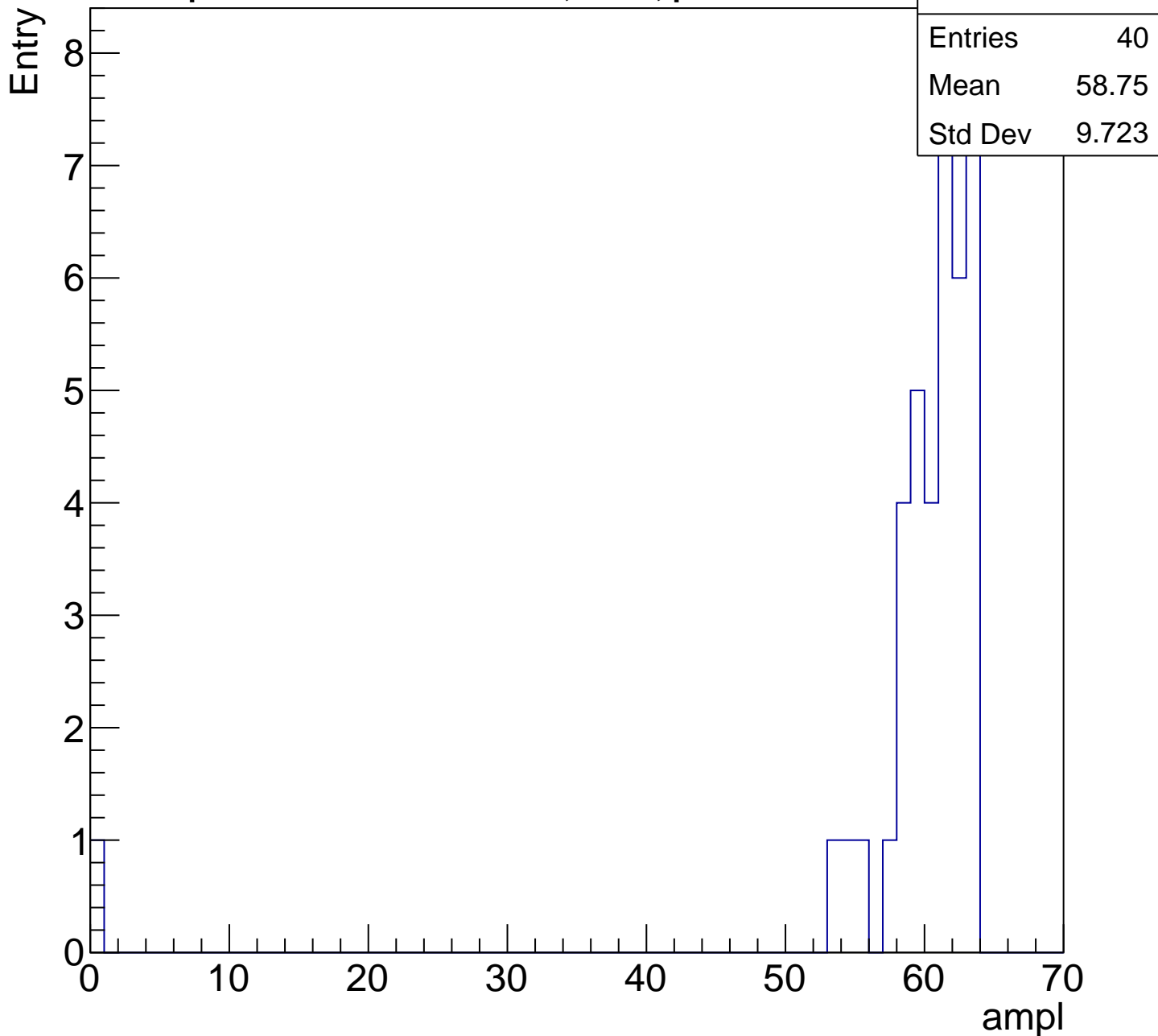
B1L100S, U11-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

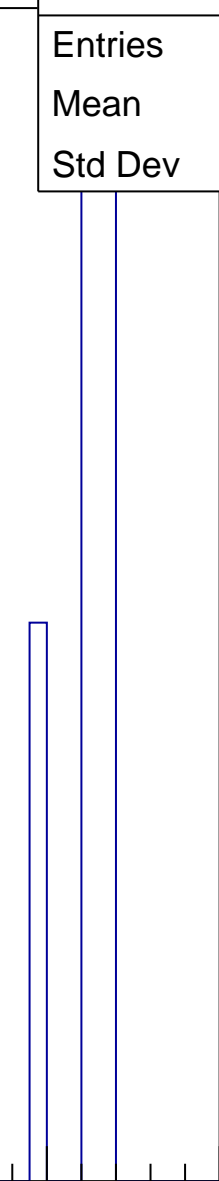
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.47

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch5, adc0

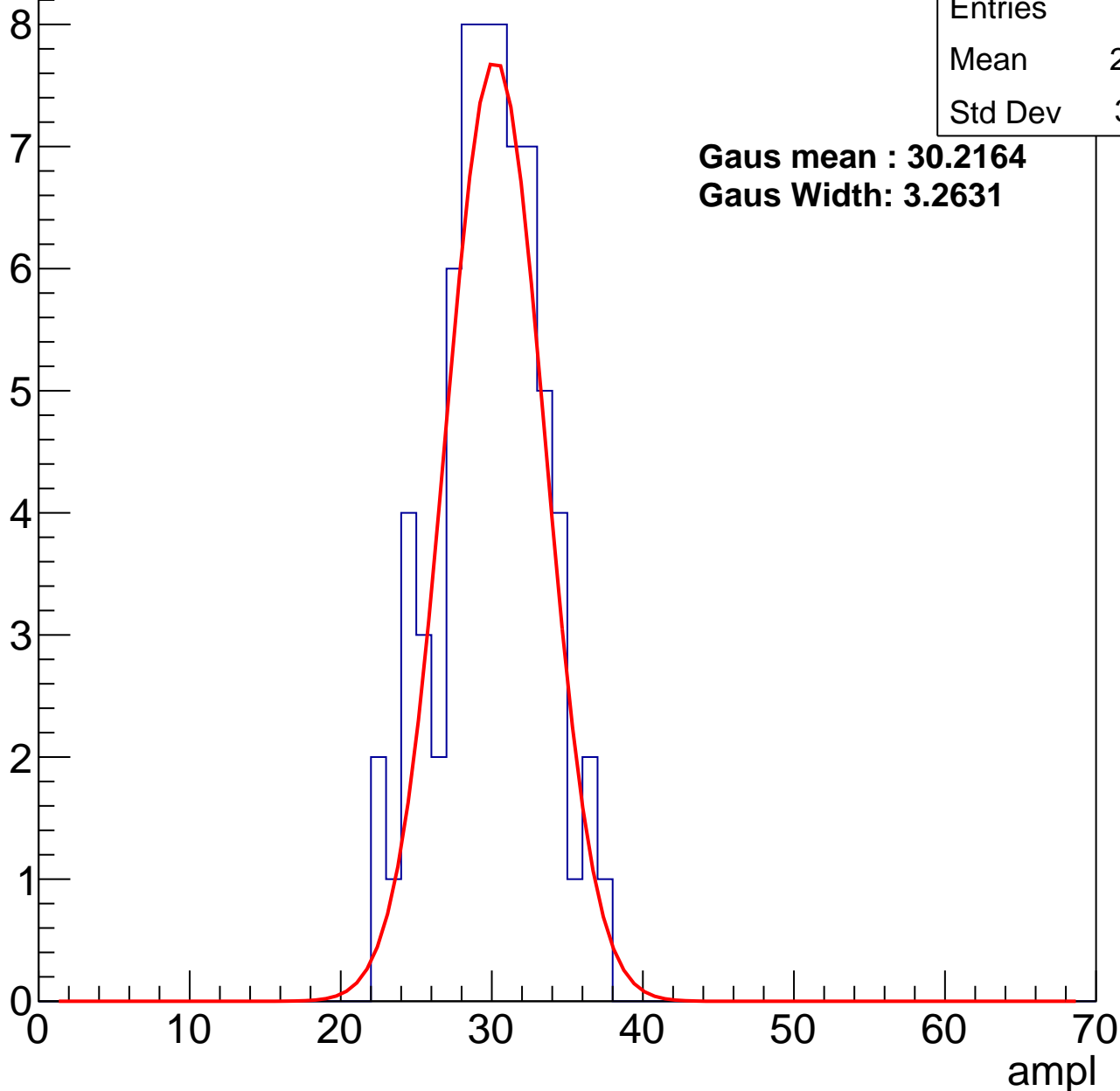
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.48
Std Dev	3.391

Gaus mean : 30.2164

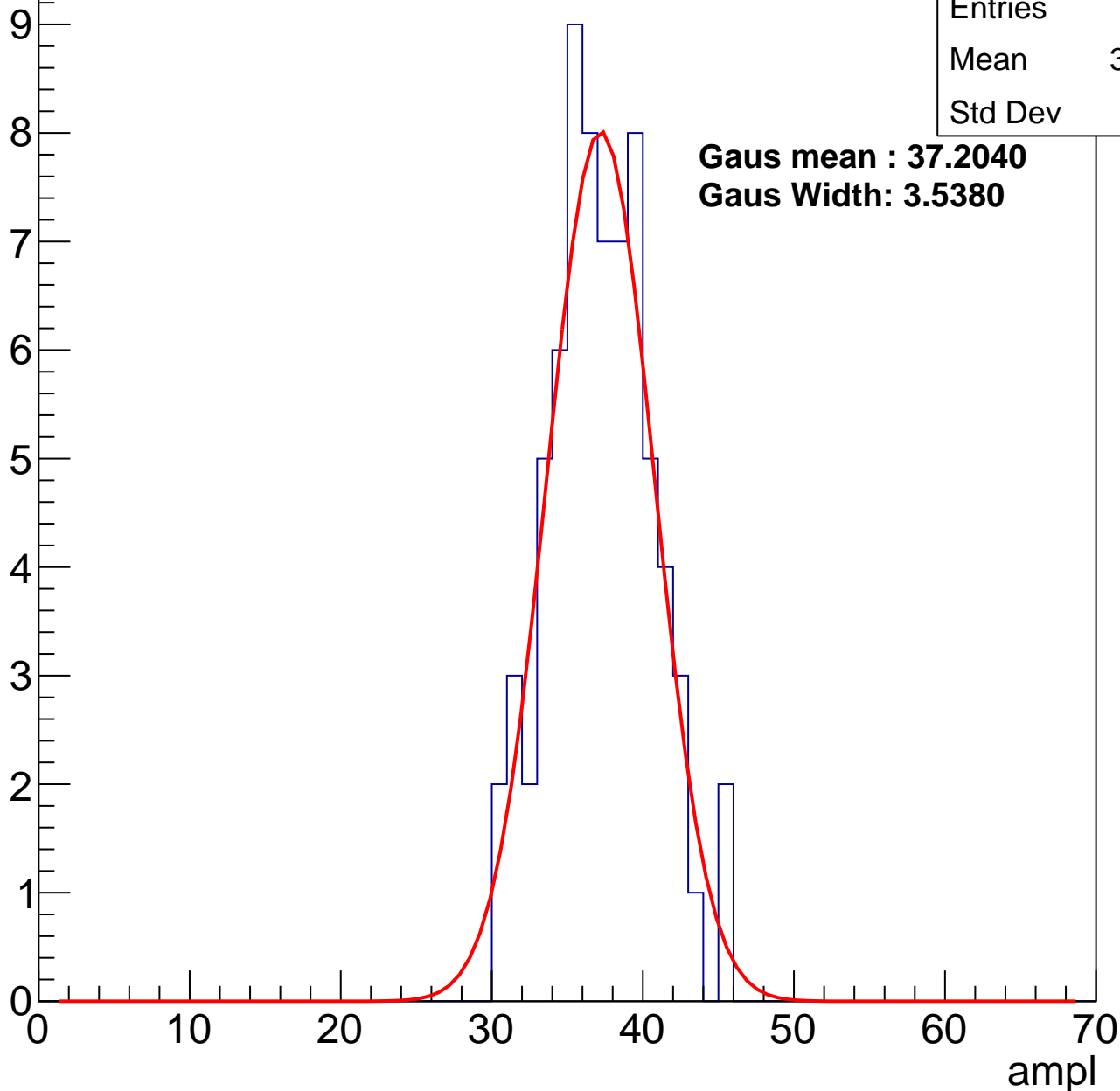
Gaus Width: 3.2631



B1L100S, U11-ch5, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch5, adc2

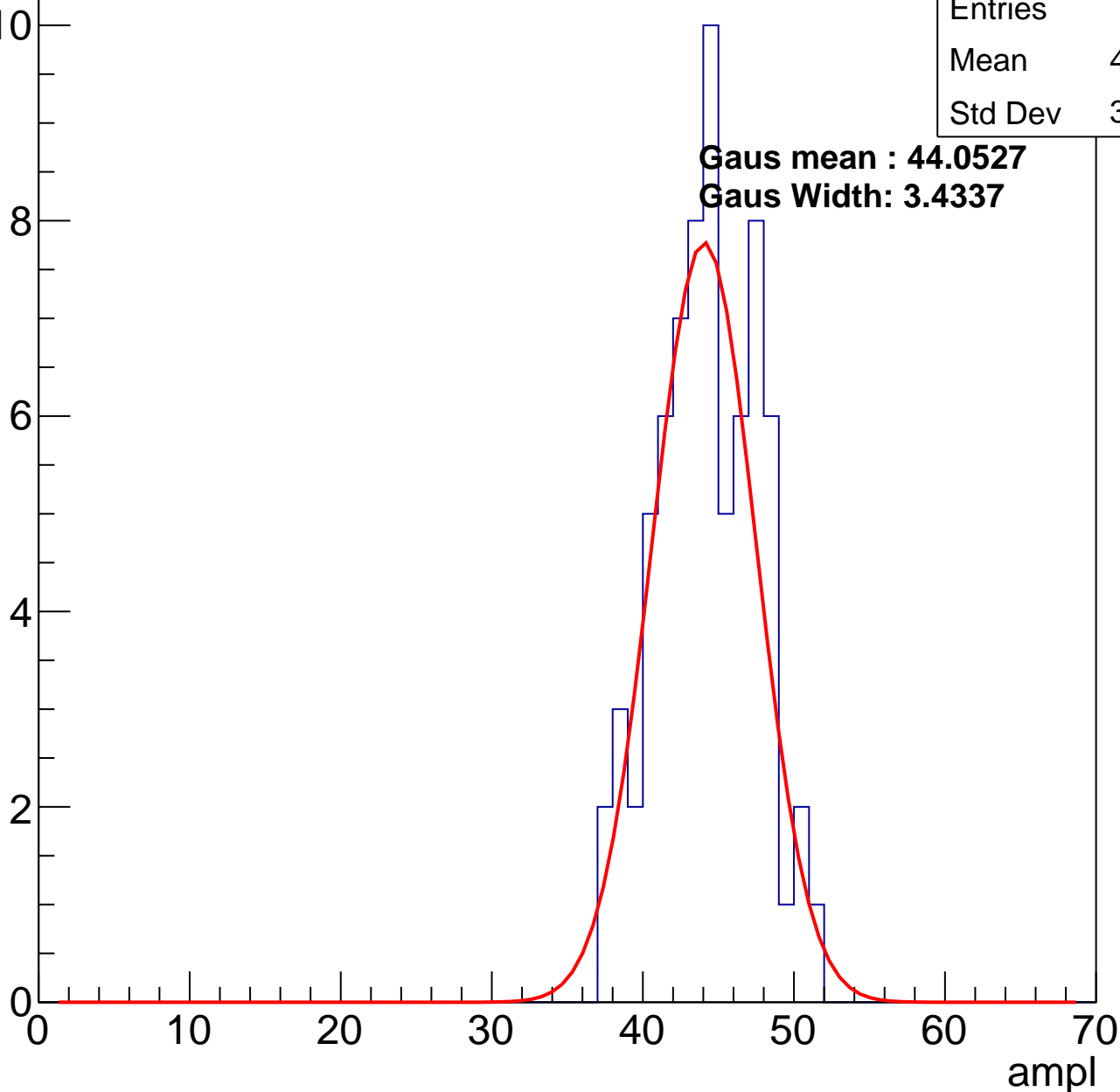
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.82
Std Dev	3.263

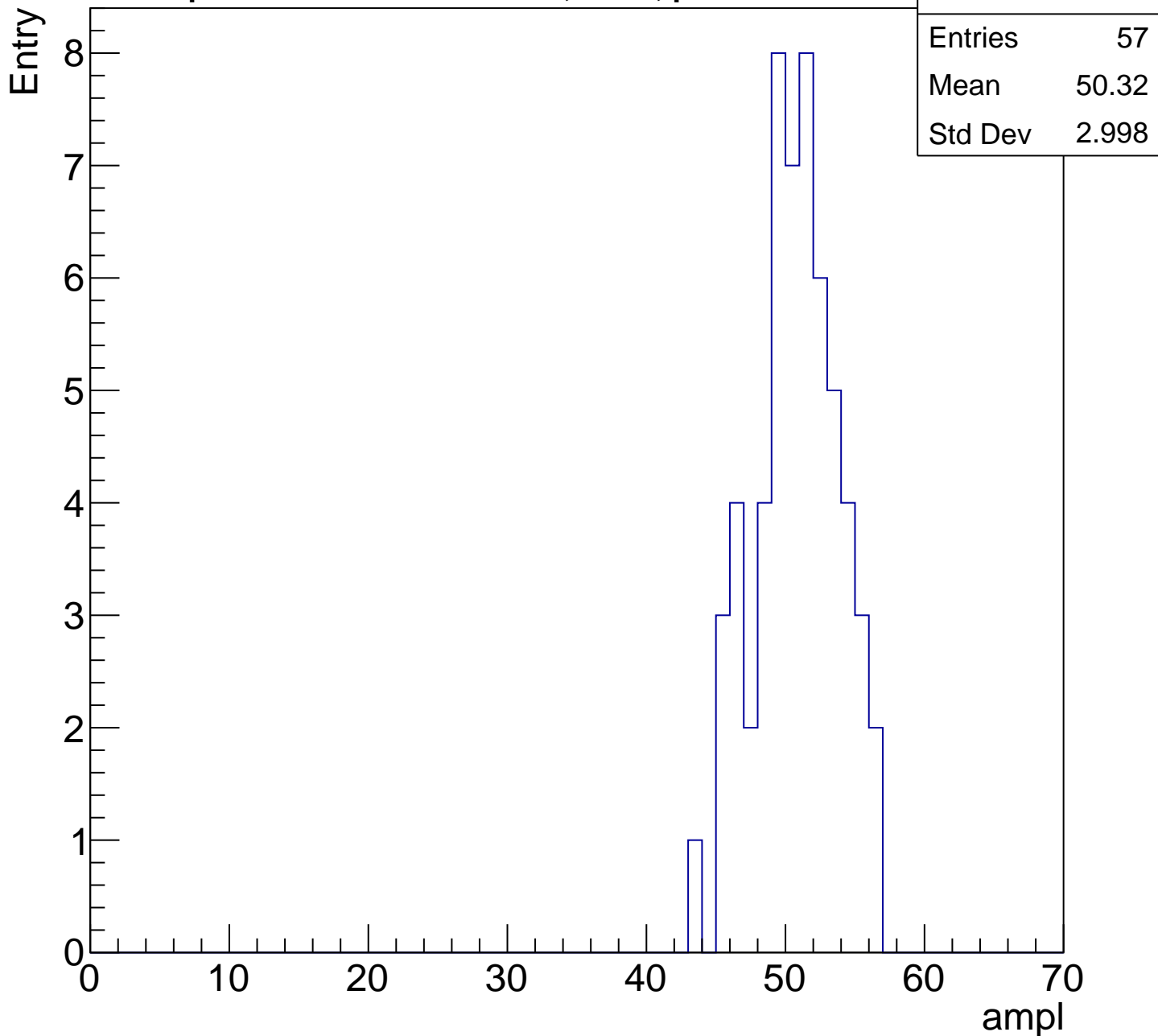
Gaus mean : 44.0527

Gaus Width: 3.4337



B1L100S, U11-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch5, adc4

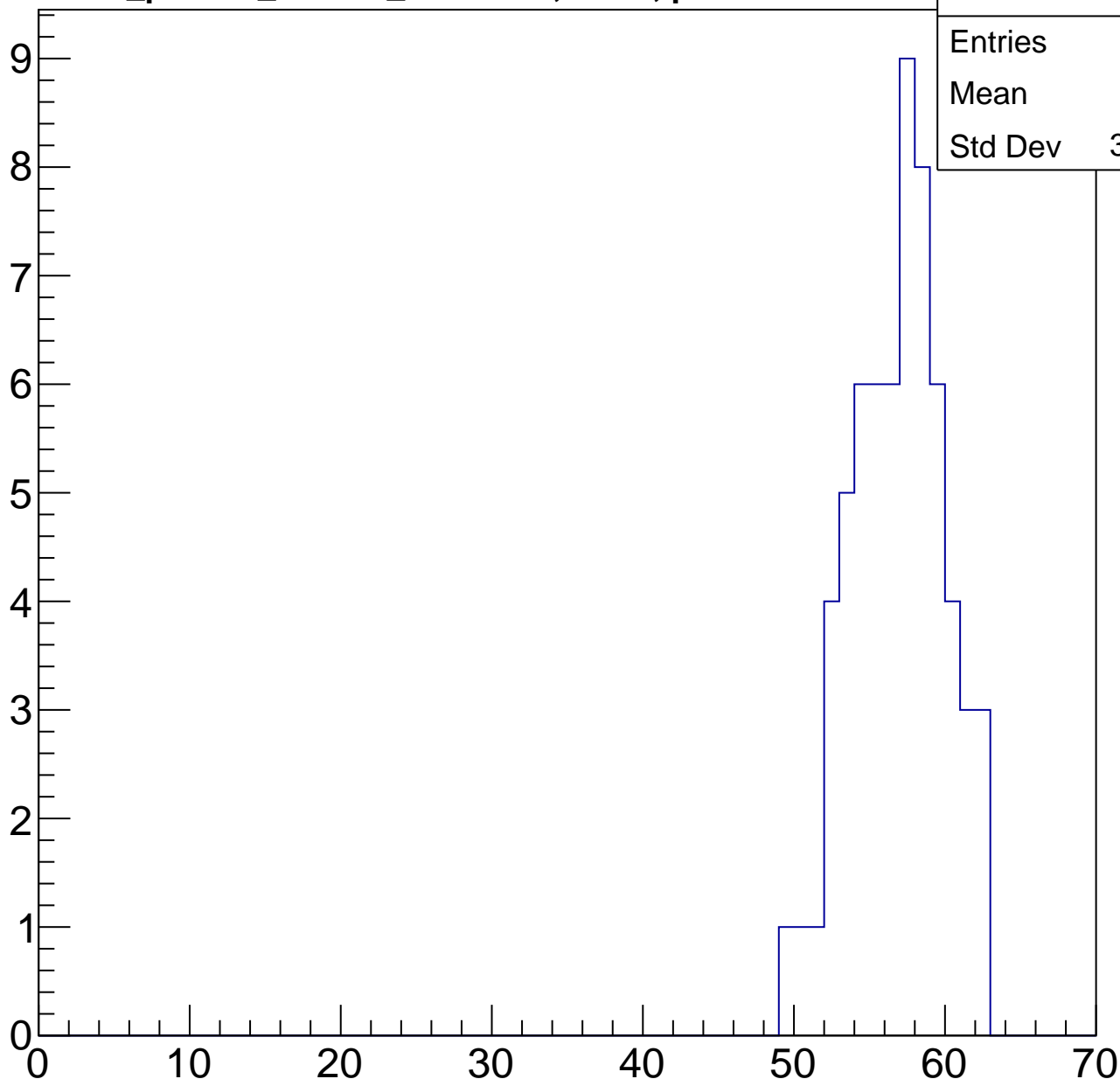
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	56.4
Std Dev	3.037

ampl

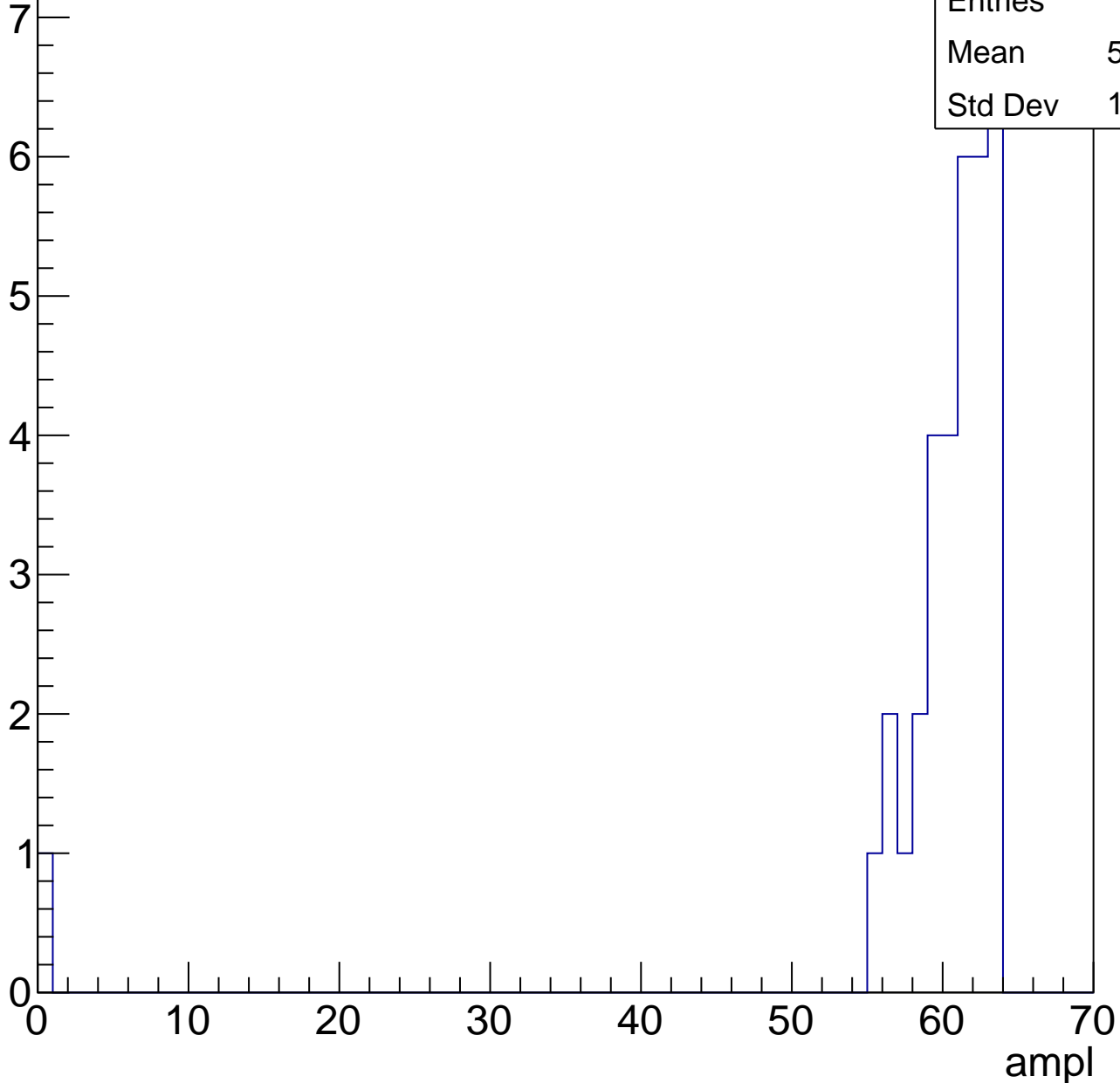


B1L100S, U11-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

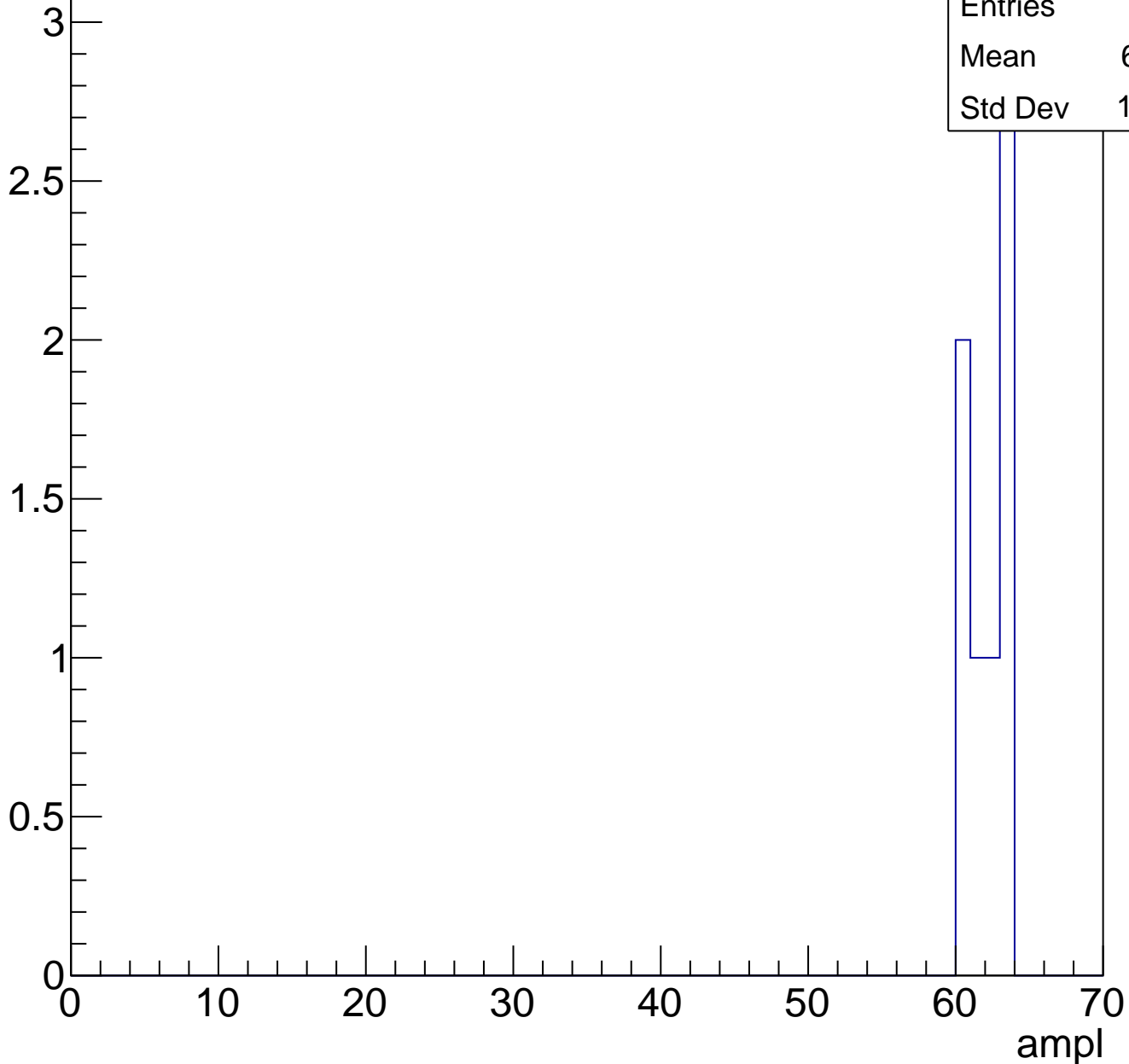
Entries	34
Mean	58.68
Std Dev	10.45



B1L100S, U11-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch6, adc0

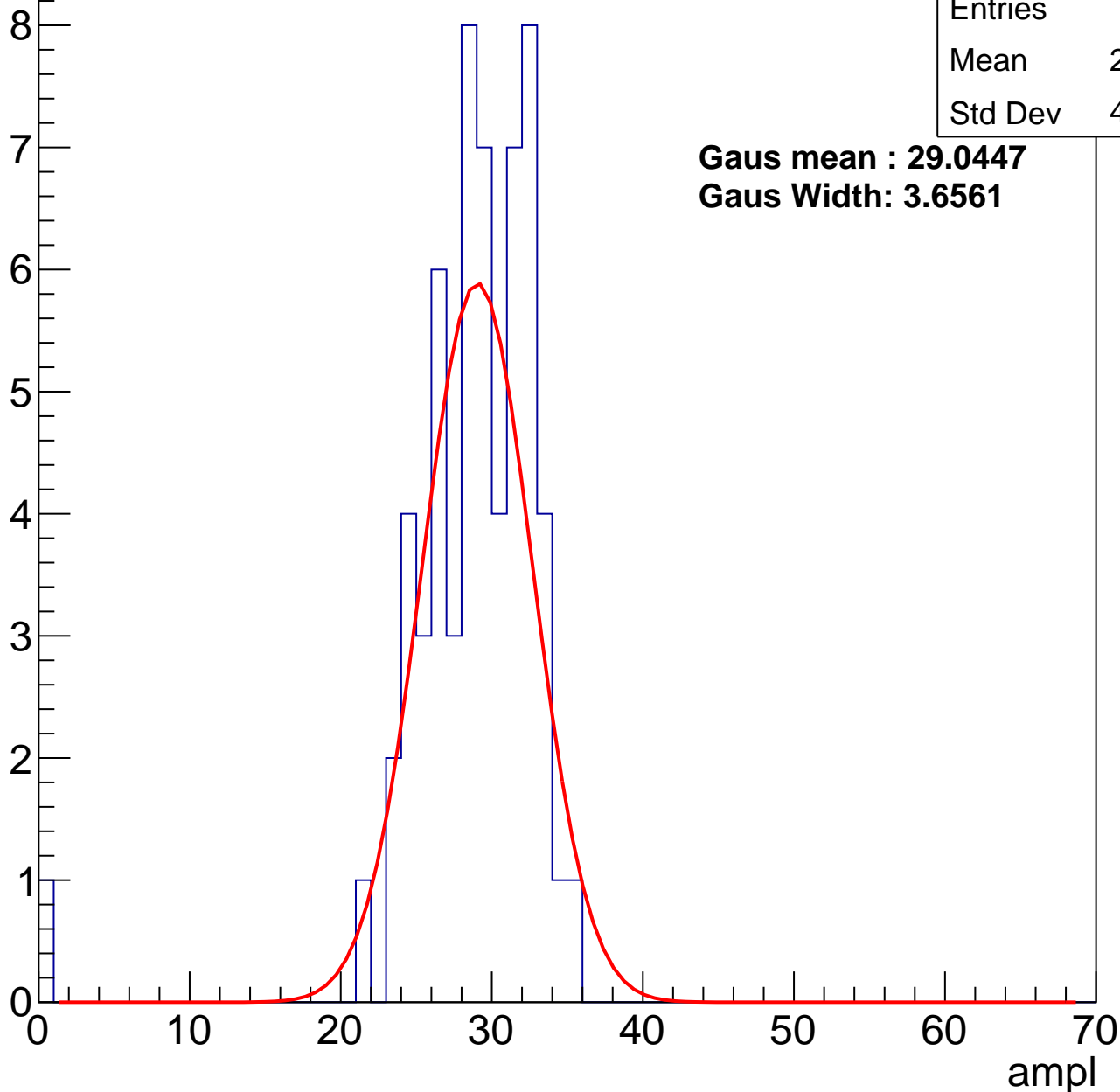
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	28.27
Std Dev	4.823

Gaus mean : 29.0447

Gaus Width: 3.6561



B1L100S, U11-ch6, adc1

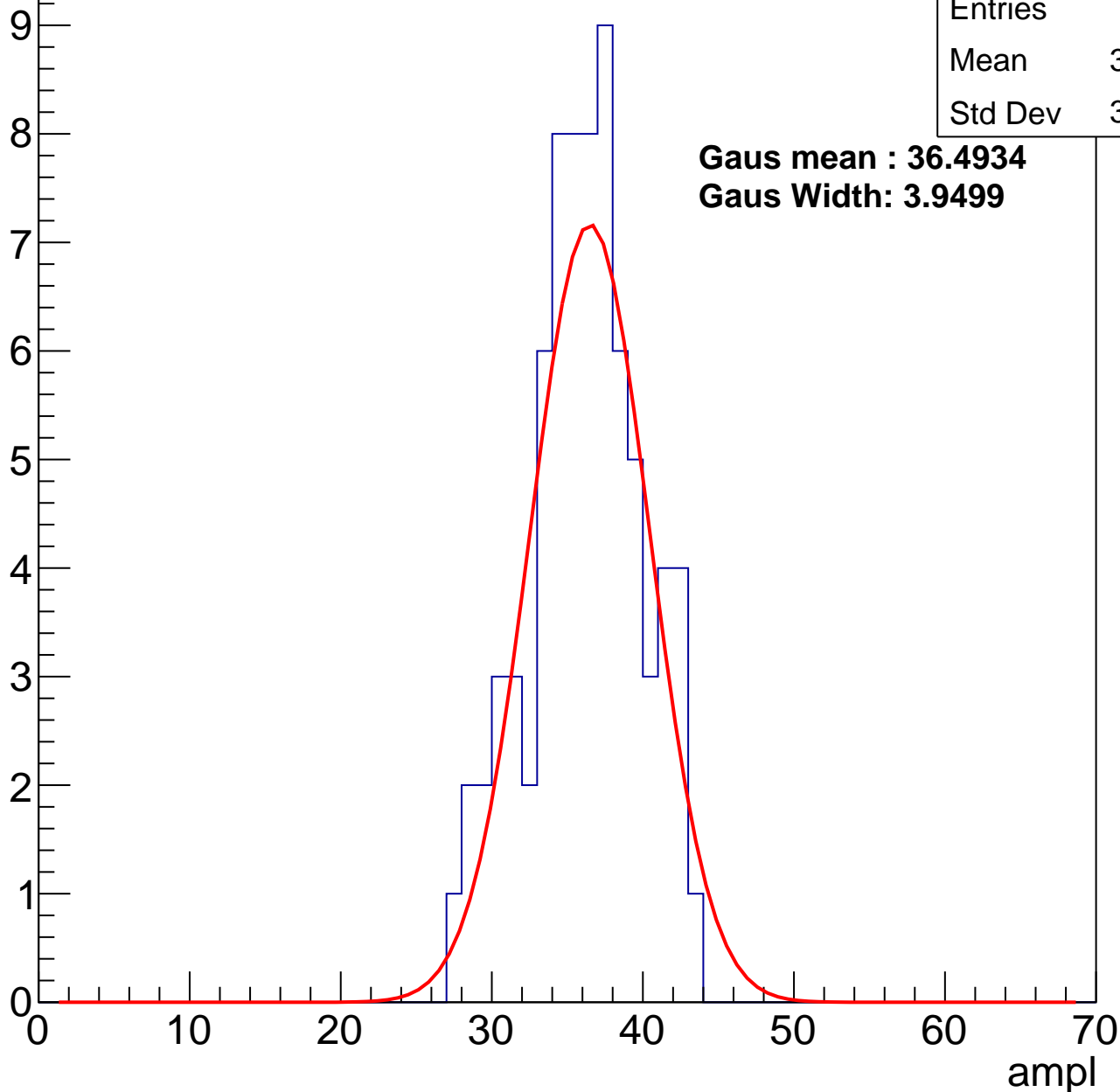
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	35.69
Std Dev	3.702

Gaus mean : 36.4934

Gaus Width: 3.9499



B1L100S, U11-ch6, adc2

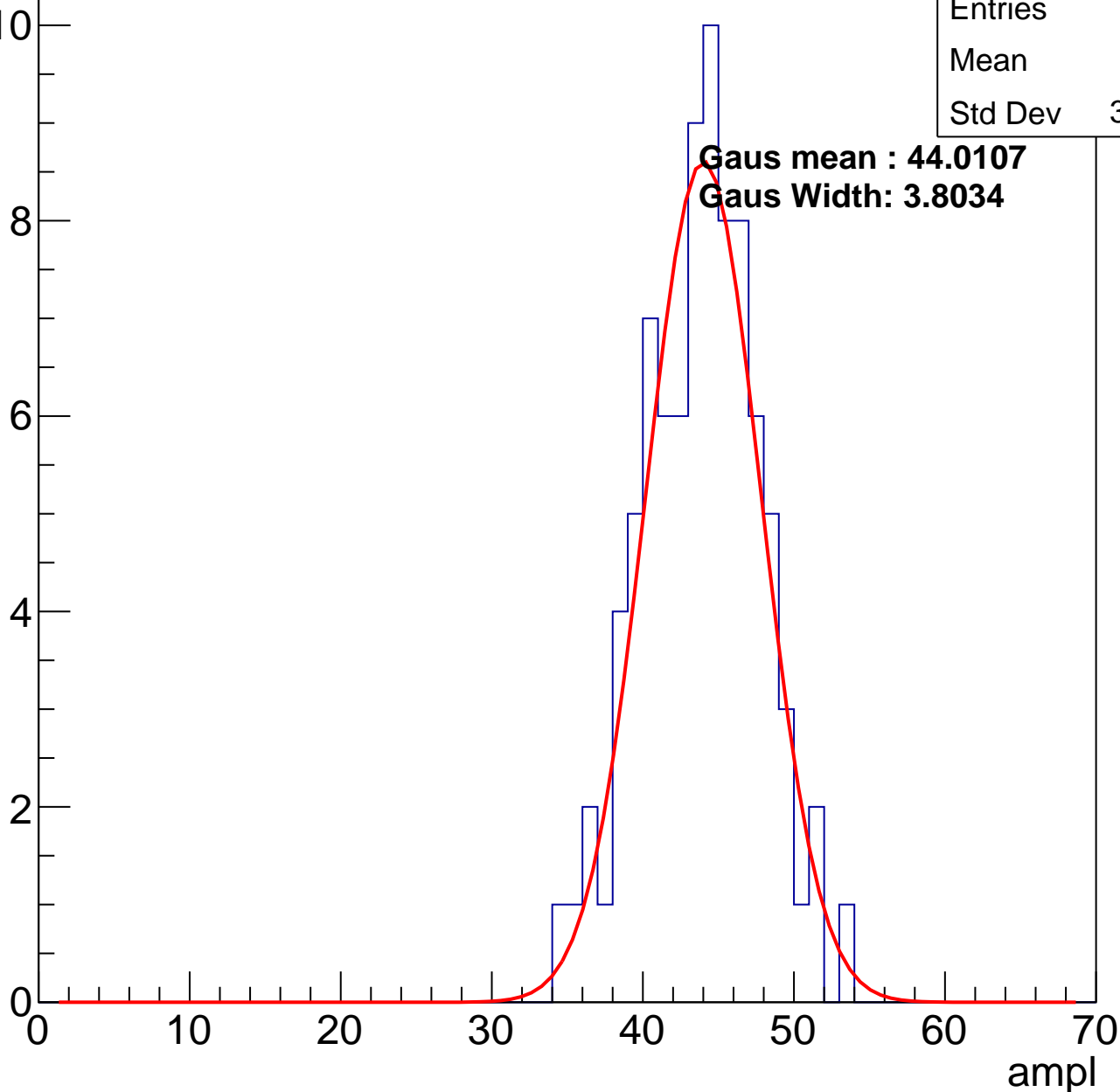
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	43.4
Std Dev	3.825

Gaus mean : 44.0107

Gaus Width: 3.8034

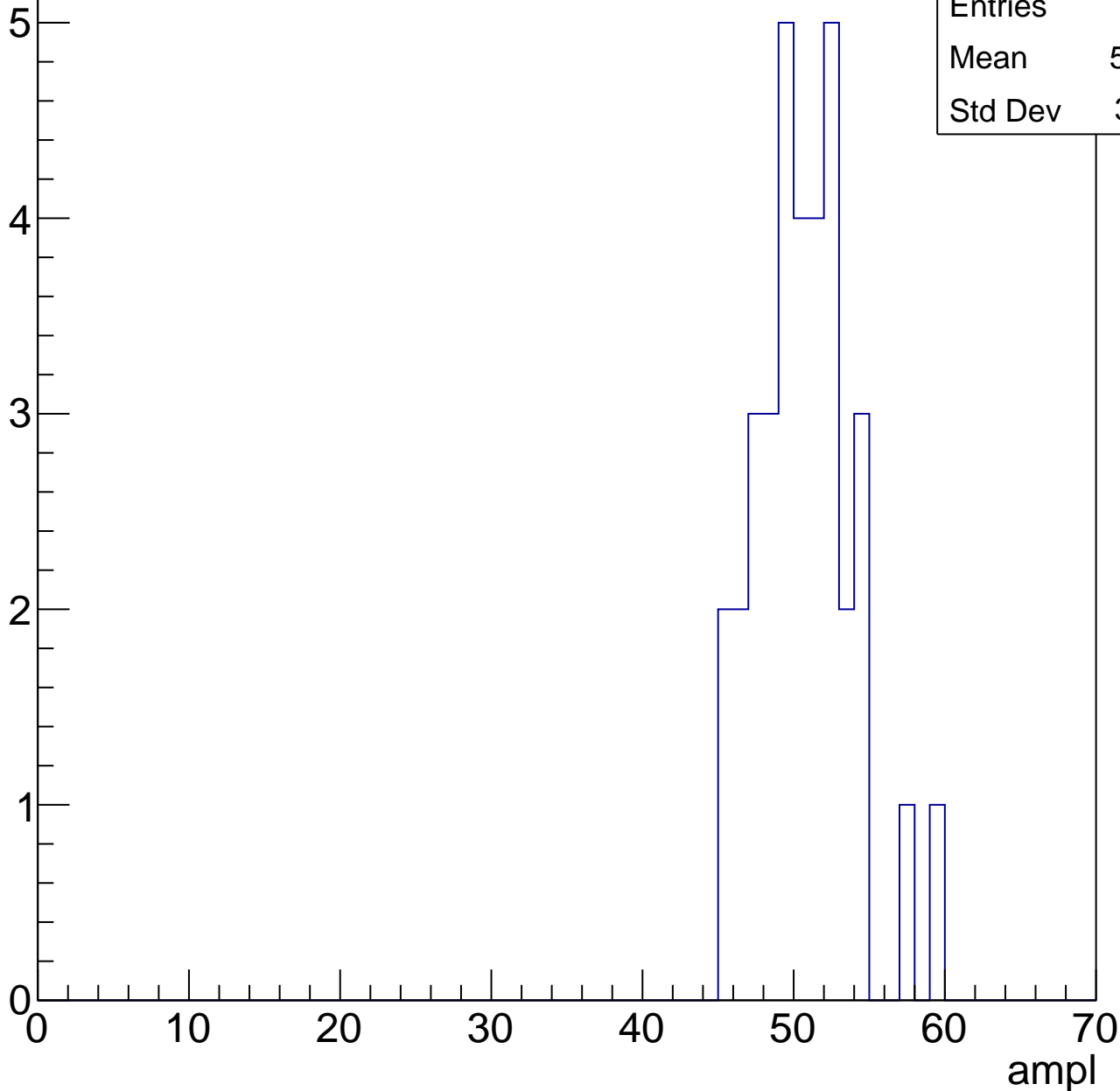


B1L100S, U11-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	50.29
Std Dev	3.131



B1L100S, U11-ch6, adc4

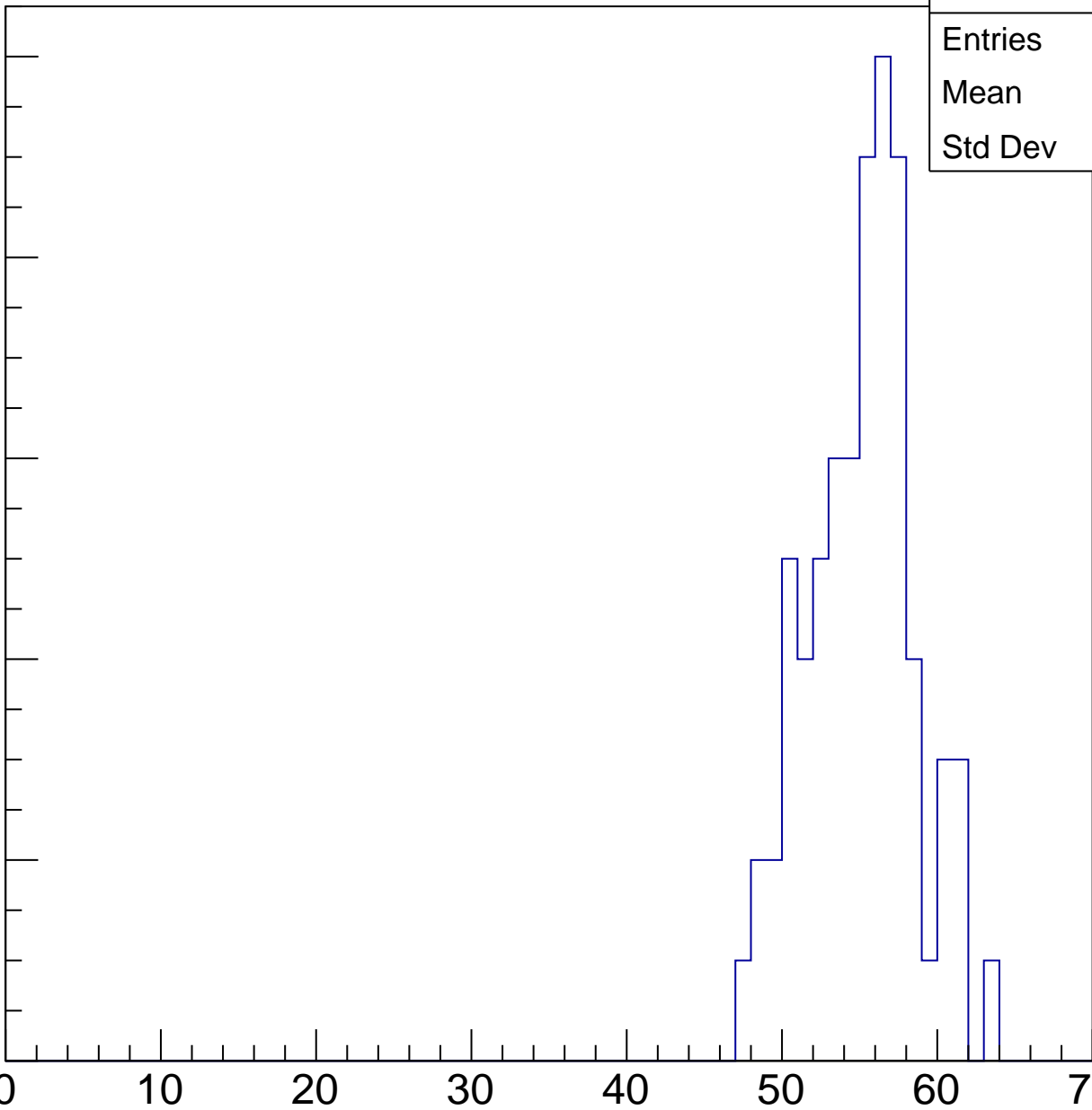
calib_packv5_042523_0143.root, FC#4, port A2

Entry

10
8
6
4
2
0

Entries	71
Mean	54.68
Std Dev	3.43

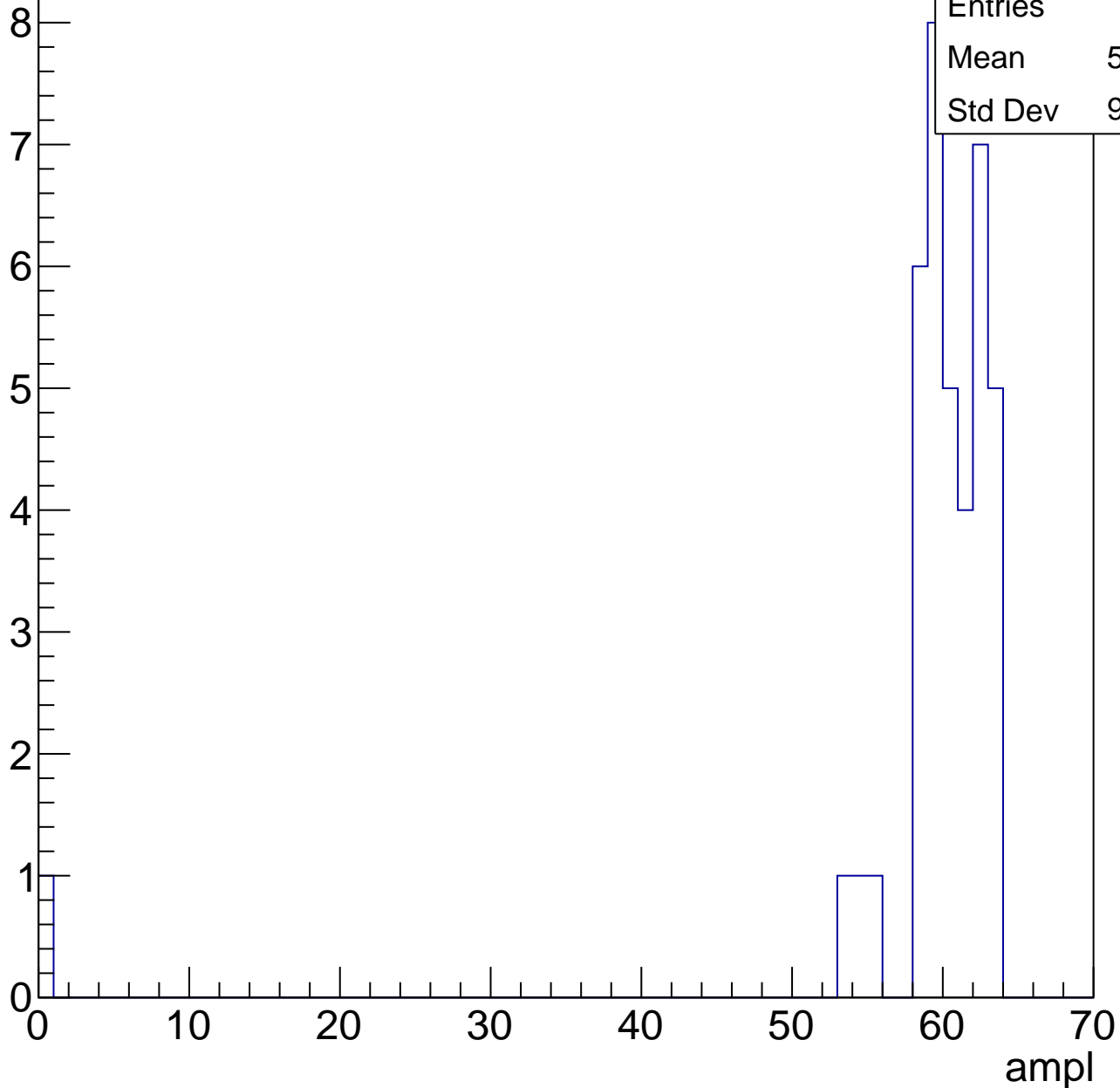
ampl



B1L100S, U11-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

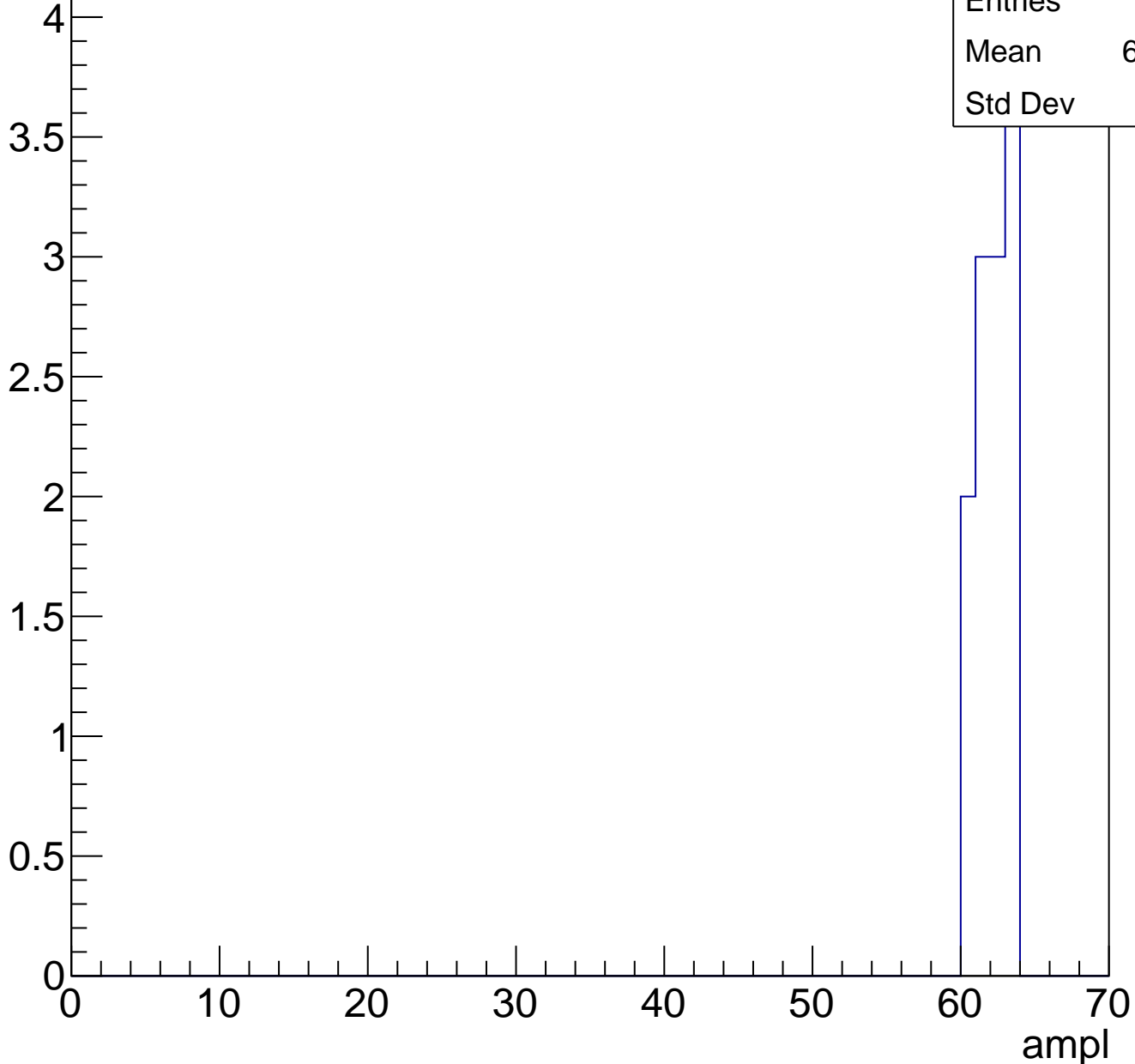


Entries	39
Mean	58.33
Std Dev	9.754

B1L100S, U11-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch7, adc0

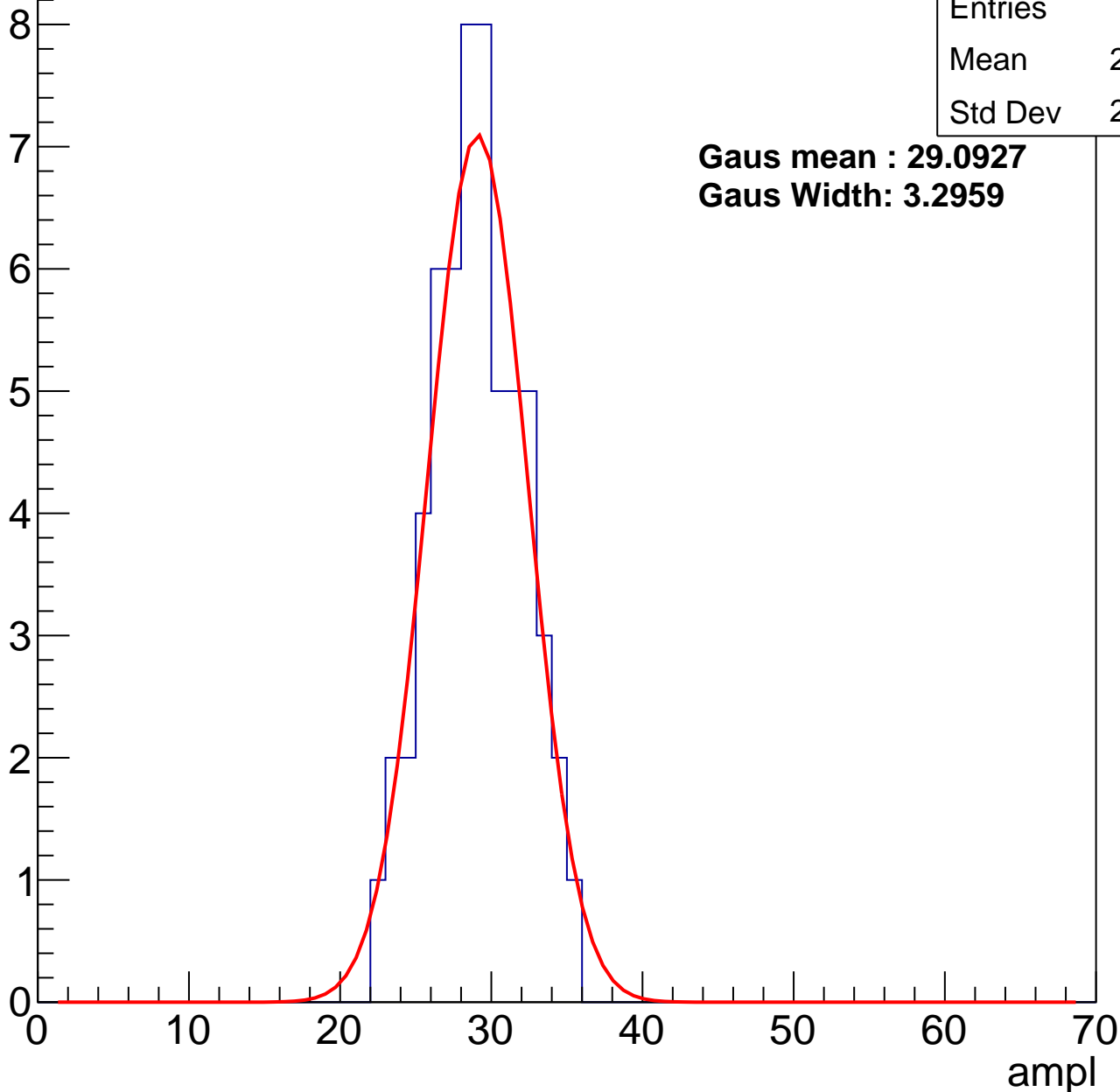
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	28.57
Std Dev	2.978

Gaus mean : 29.0927

Gaus Width: 3.2959



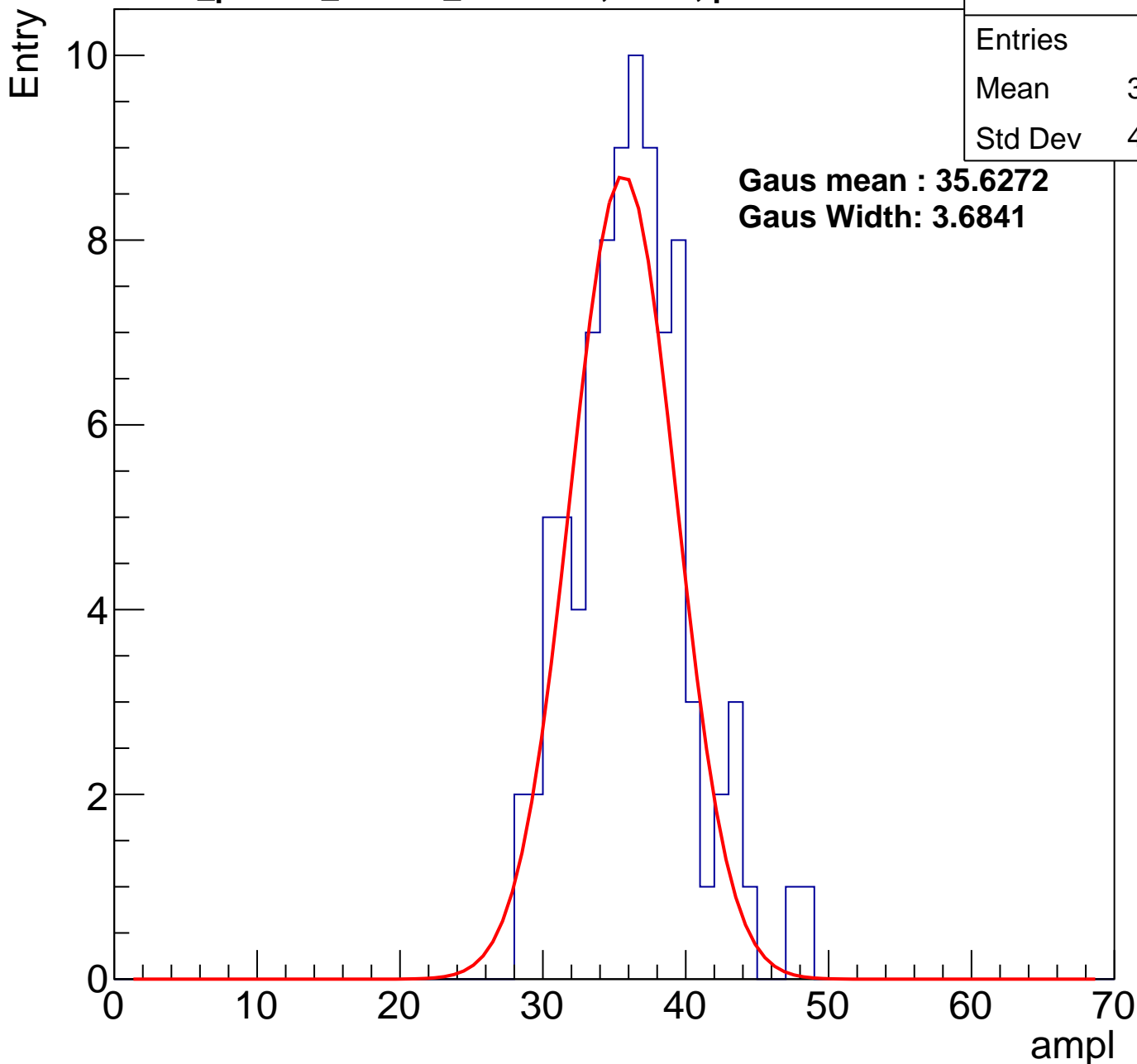
B1L100S, U11-ch7, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	35.78
Std Dev	4.024

Gaus mean : 35.6272

Gaus Width: 3.6841



B1L100S, U11-ch7, adc2

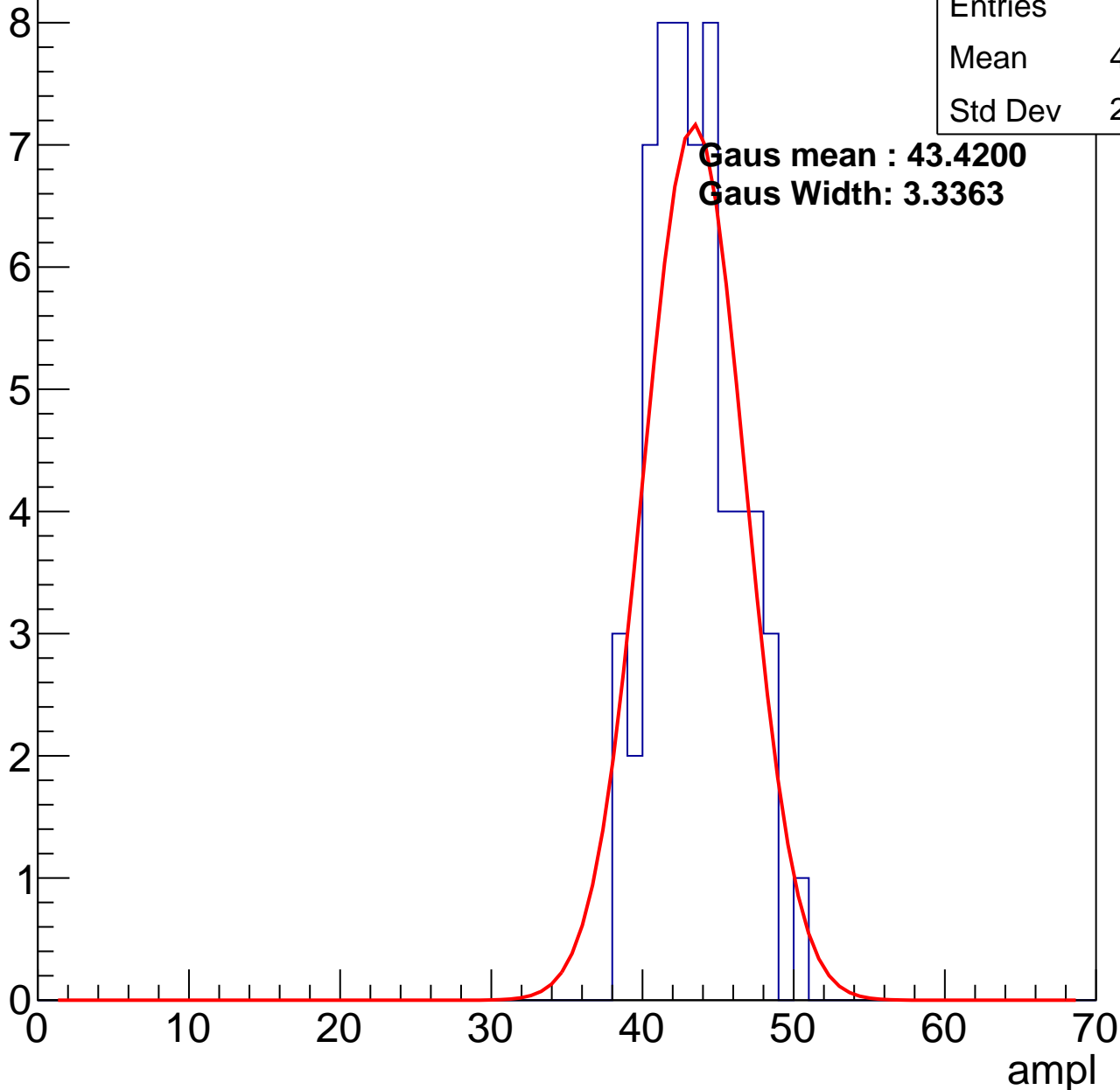
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.97
Std Dev	2.786

Gaus mean : 43.4200

Gaus Width: 3.3363

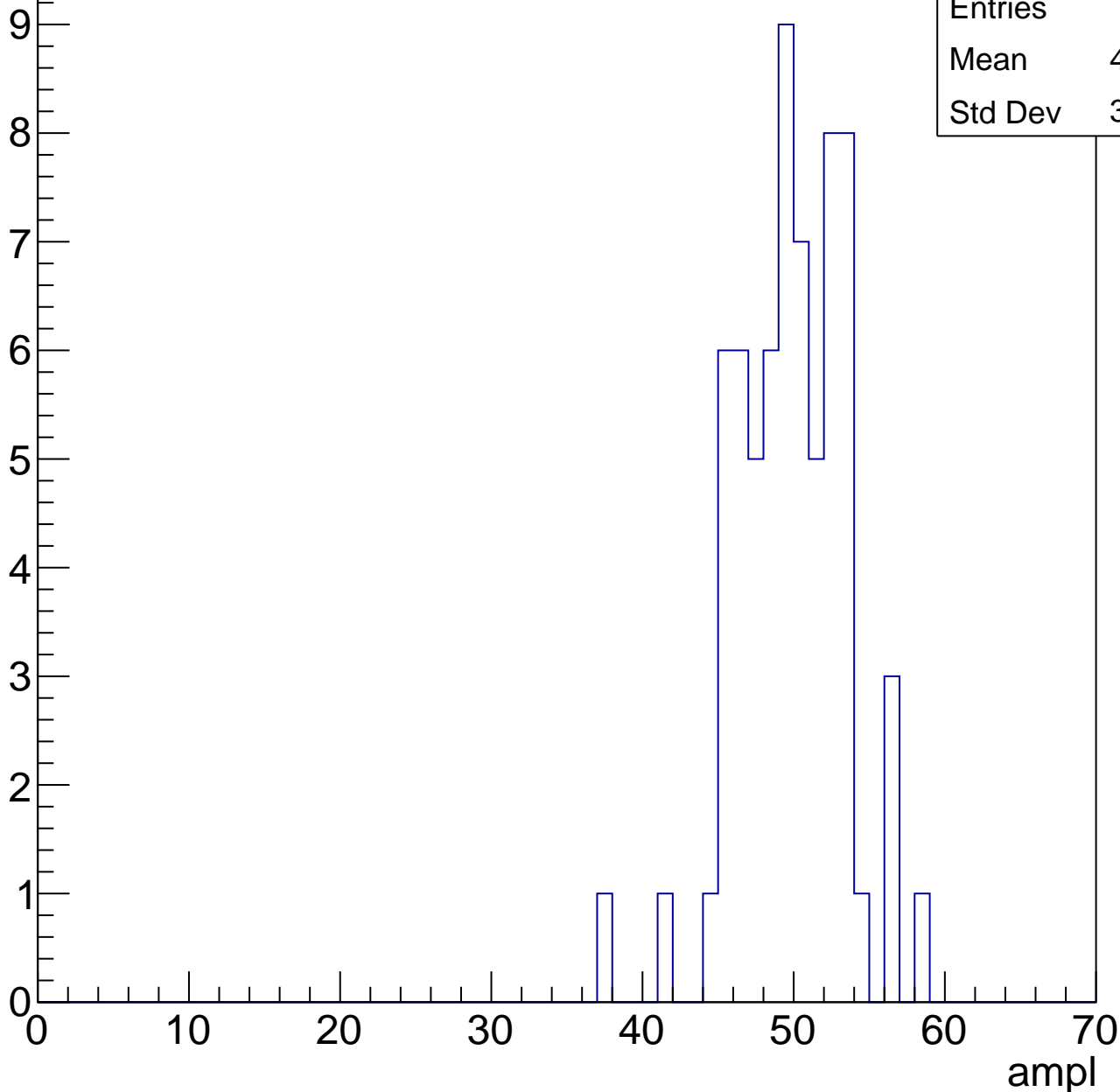


B1L100S, U11-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

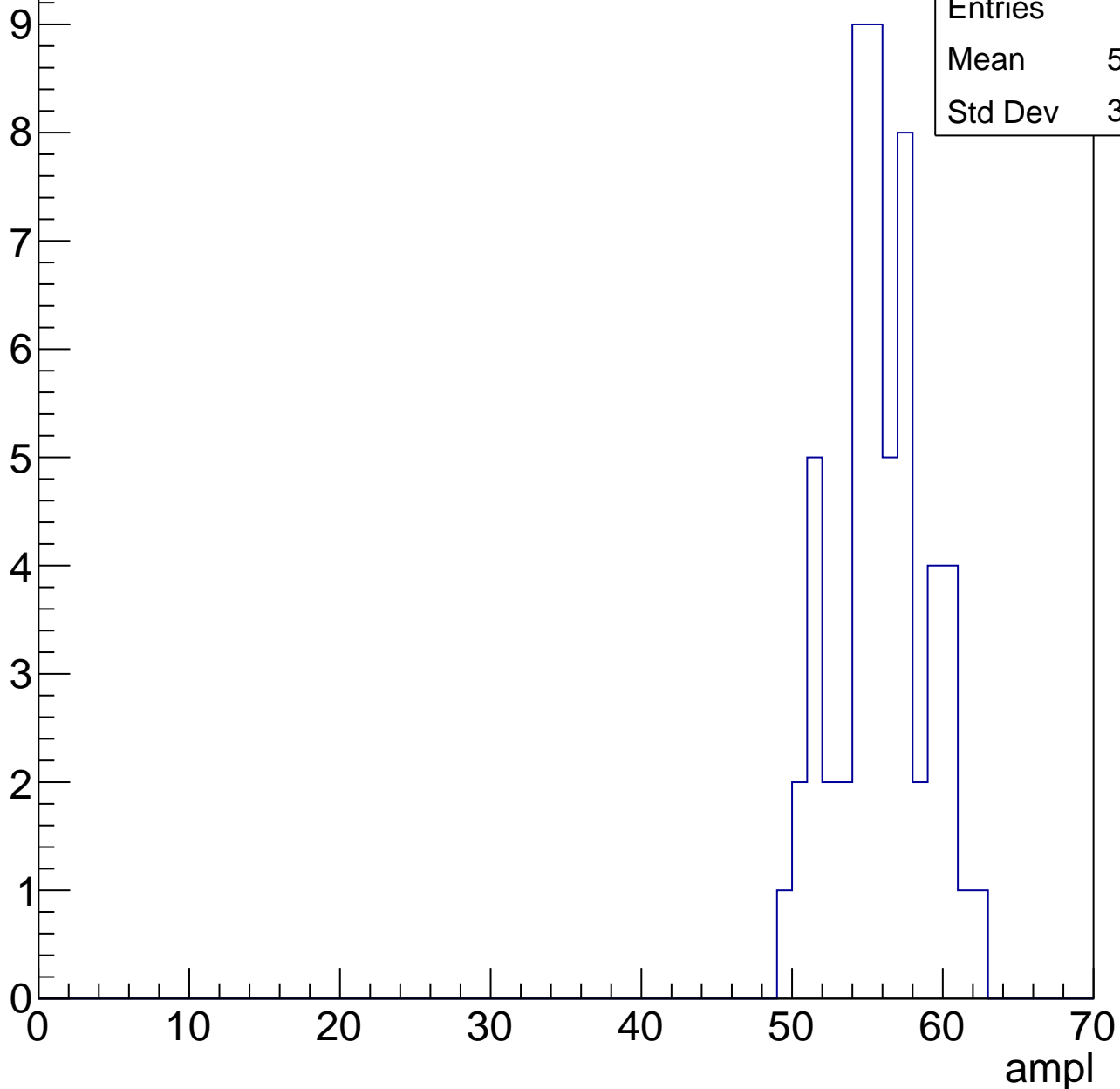
Entries	68
Mean	49.37
Std Dev	3.597



B1L100S, U11-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	55
Mean	55.38
Std Dev	3.006

B1L100S, U11-ch7, adc5

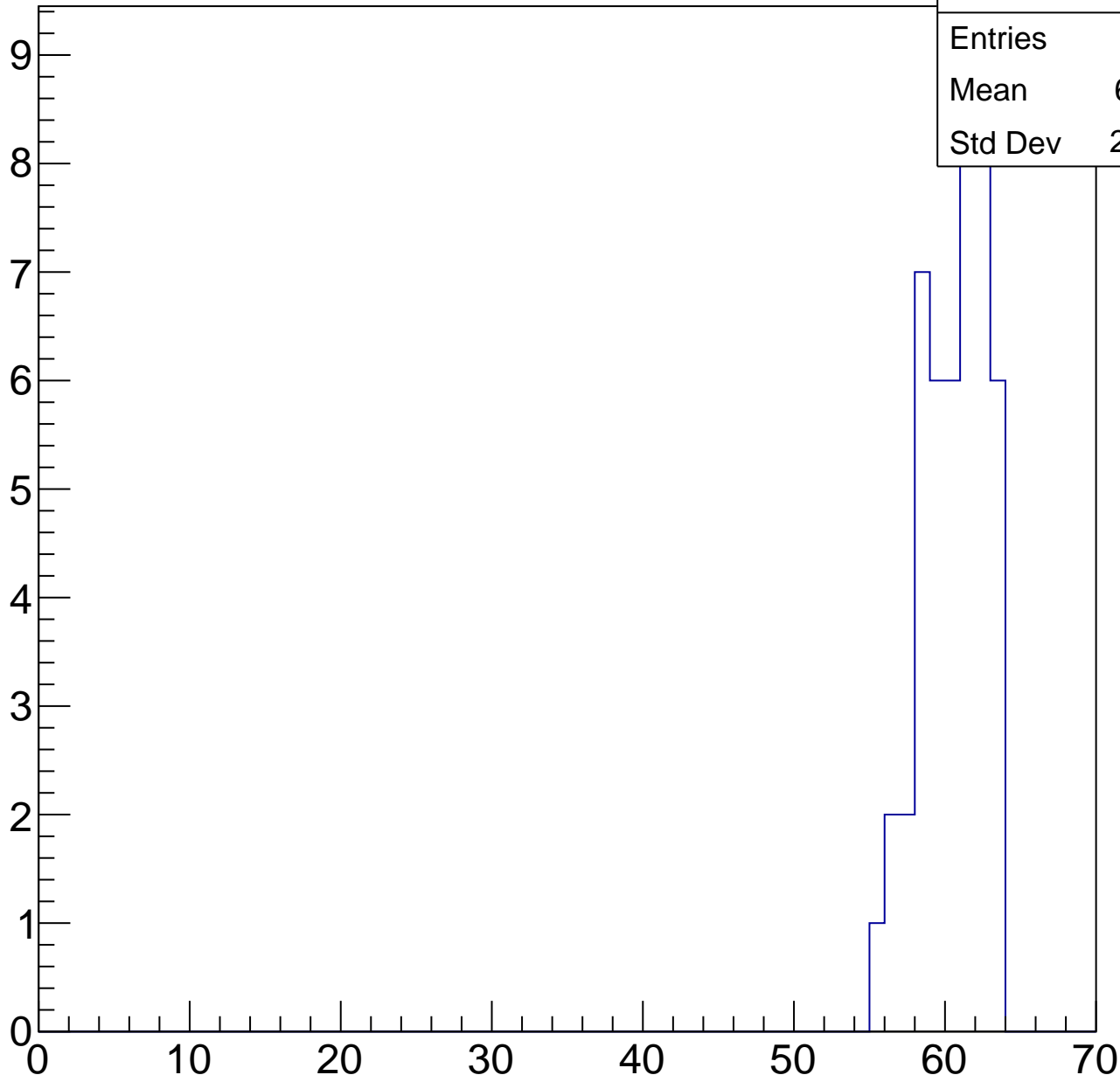
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	60.11
Std Dev	2.096

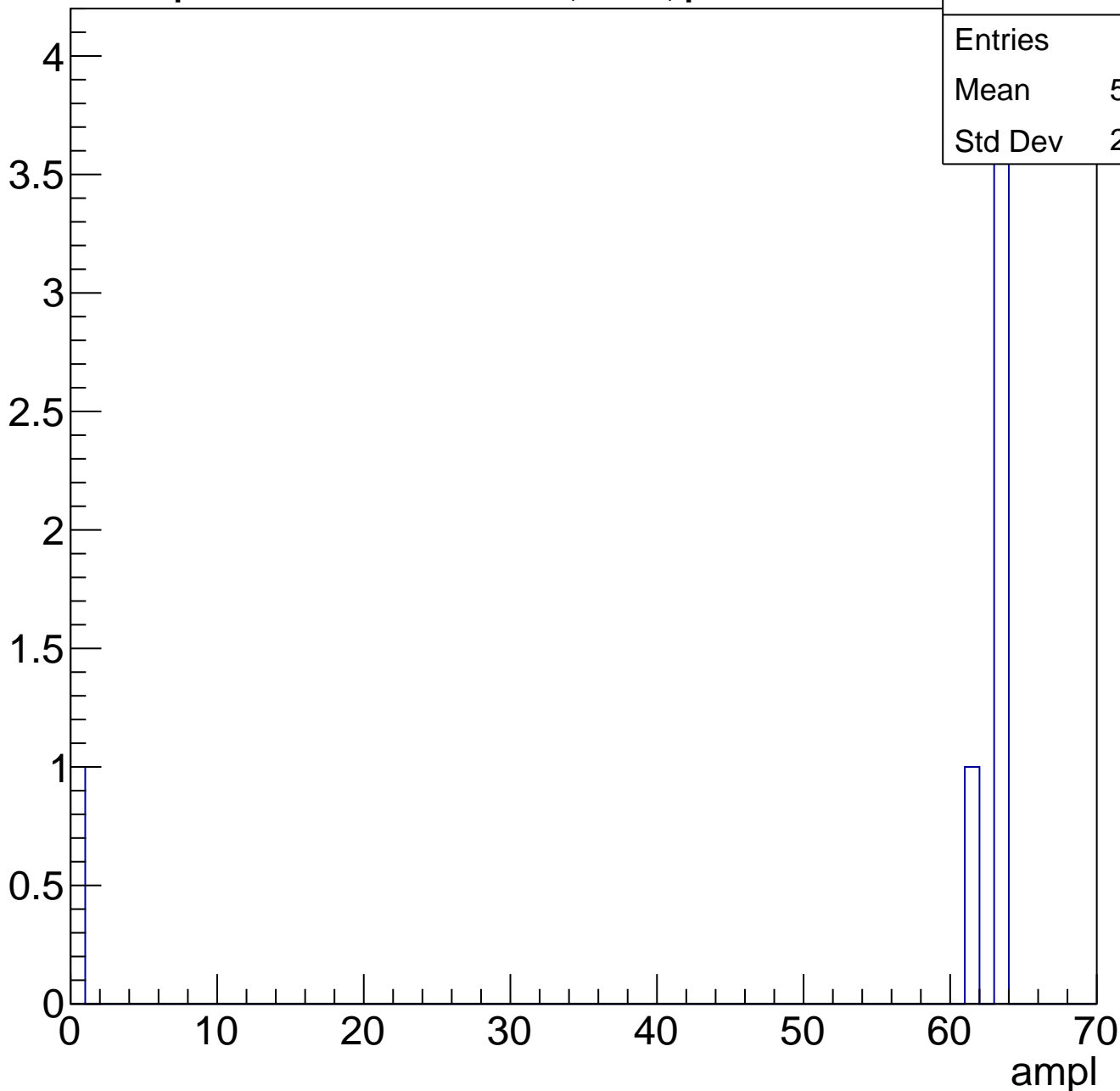
ampl



B1L100S, U11-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	6
Mean	52.17
Std Dev	23.34

B1L100S, U11-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	17
Std Dev	0

ampl

B1L100S, U11-ch8, adc0

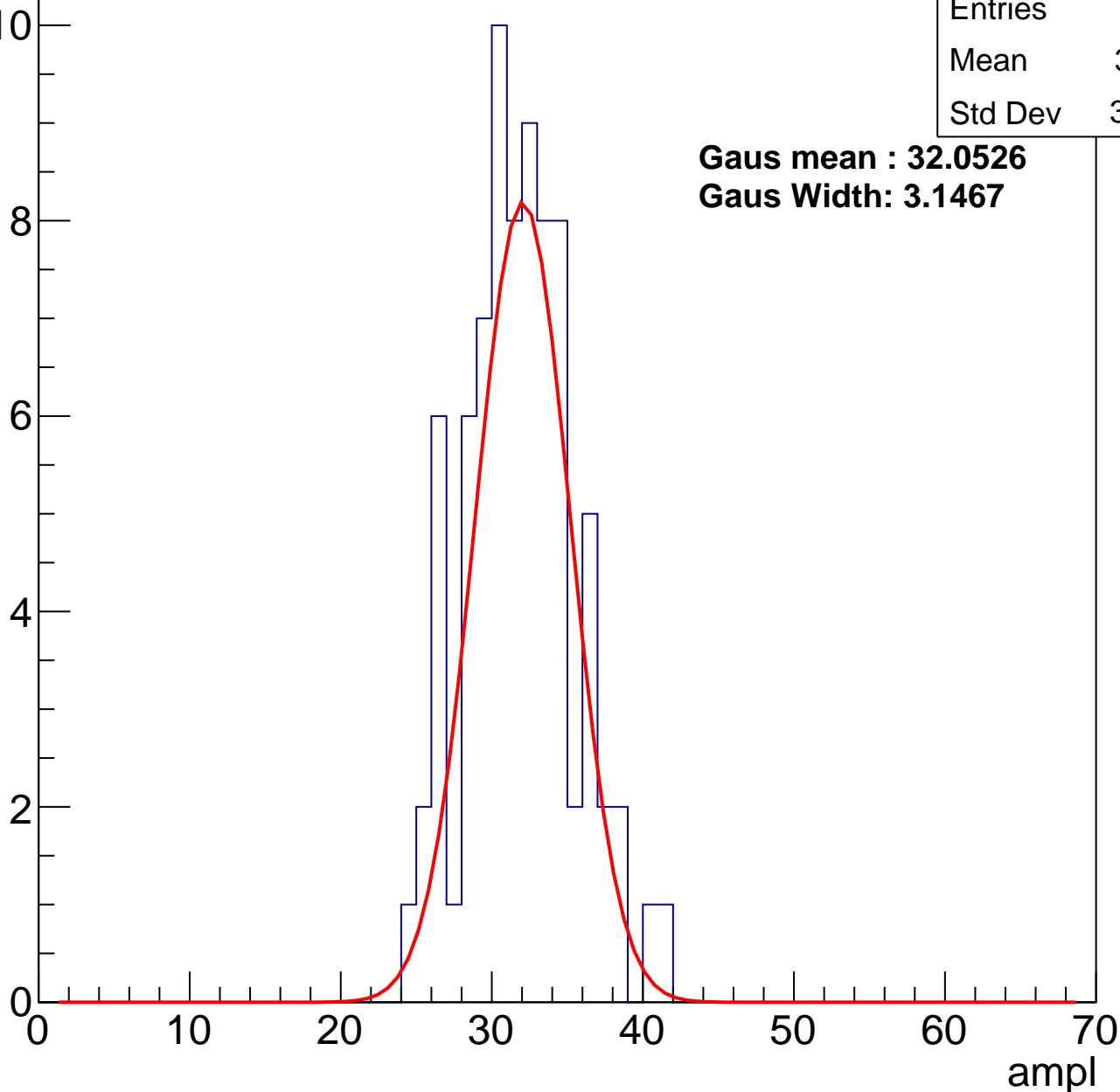
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	31.41
Std Dev	3.535

Gaus mean : 32.0526

Gaus Width: 3.1467



B1L100S, U11-ch8, adc1

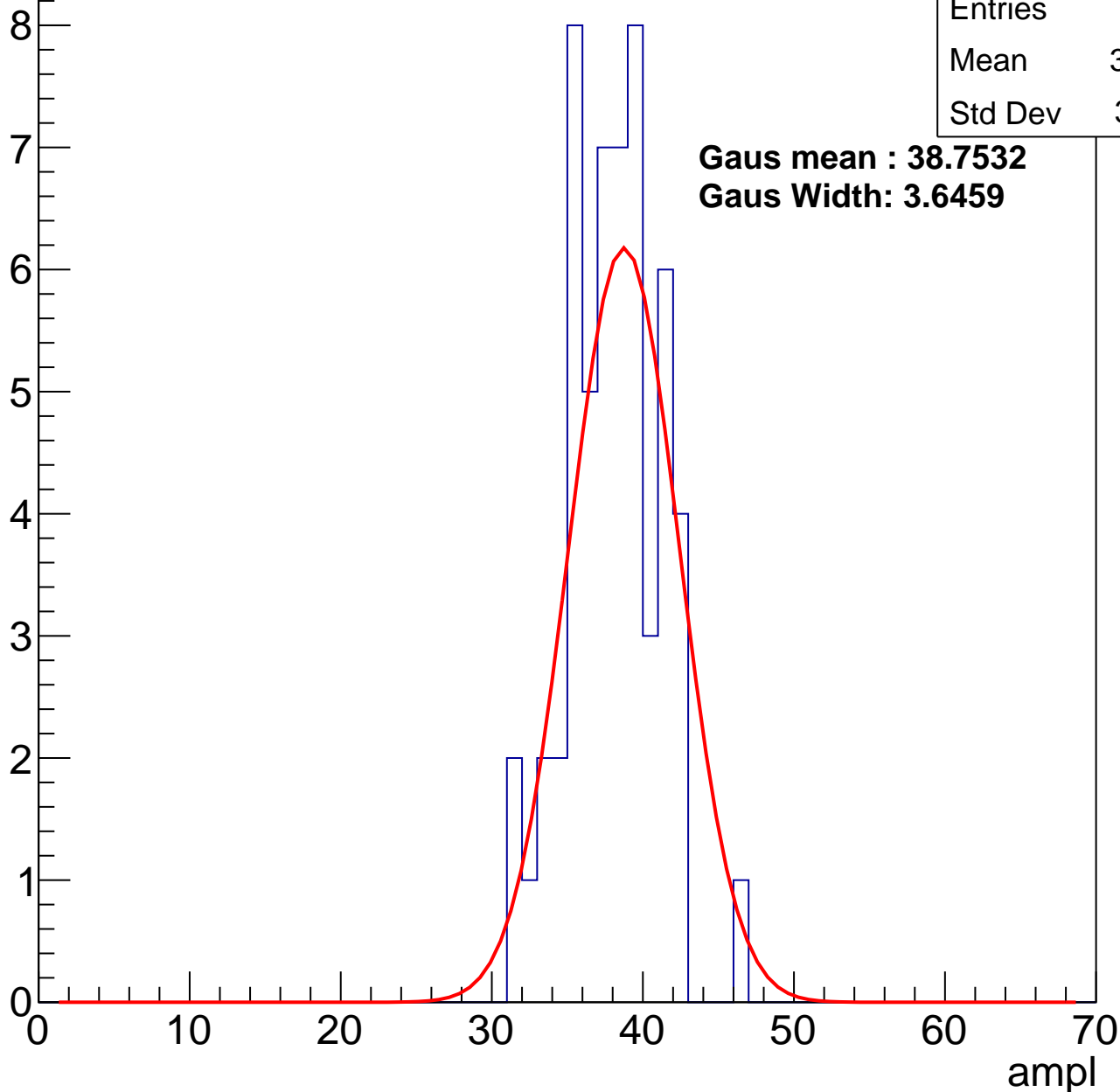
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	37.59
Std Dev	3.011

Gaus mean : 38.7532

Gaus Width: 3.6459



B1L100S, U11-ch8, adc2

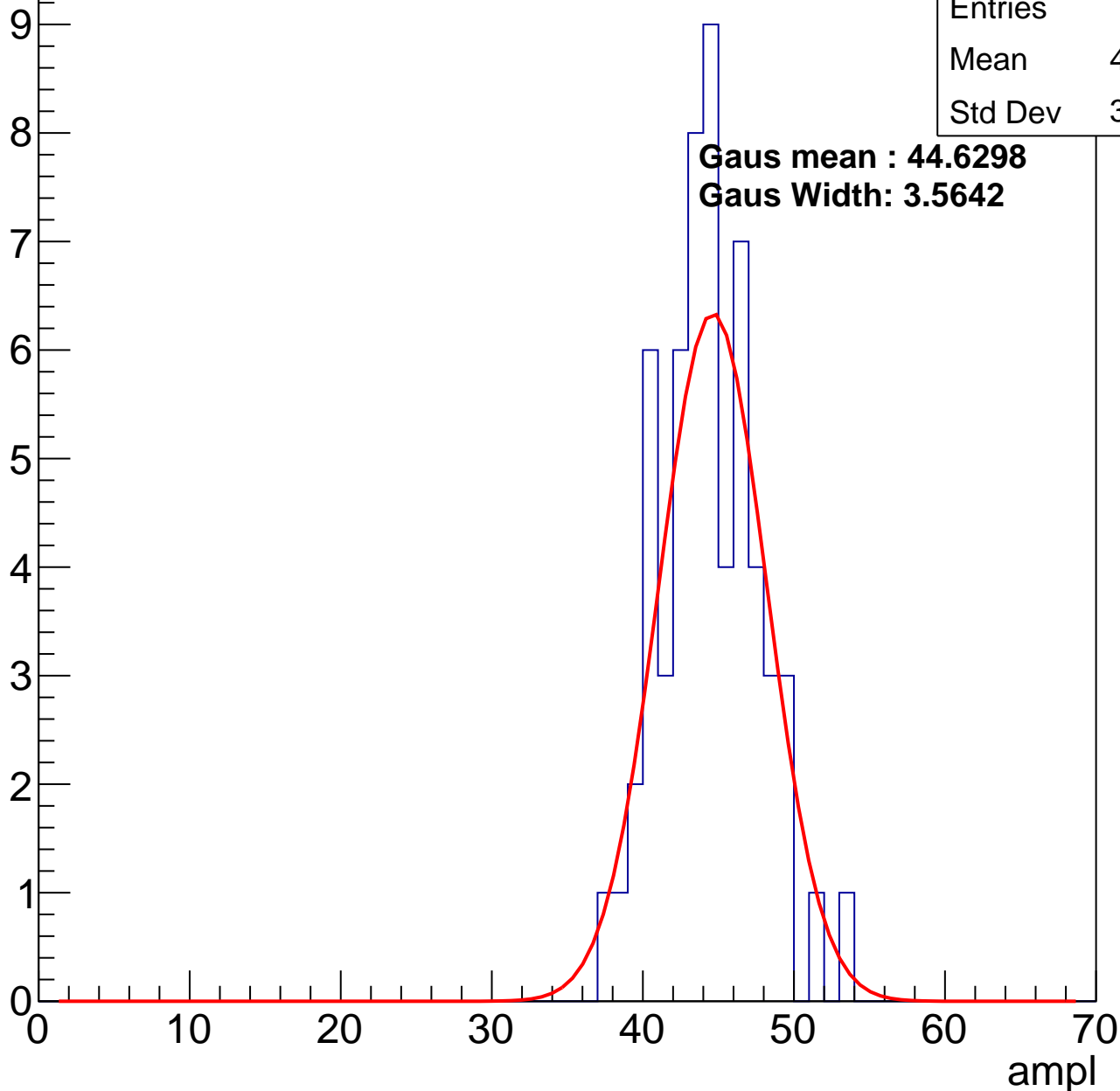
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.95
Std Dev	3.218

Gaus mean : 44.6298

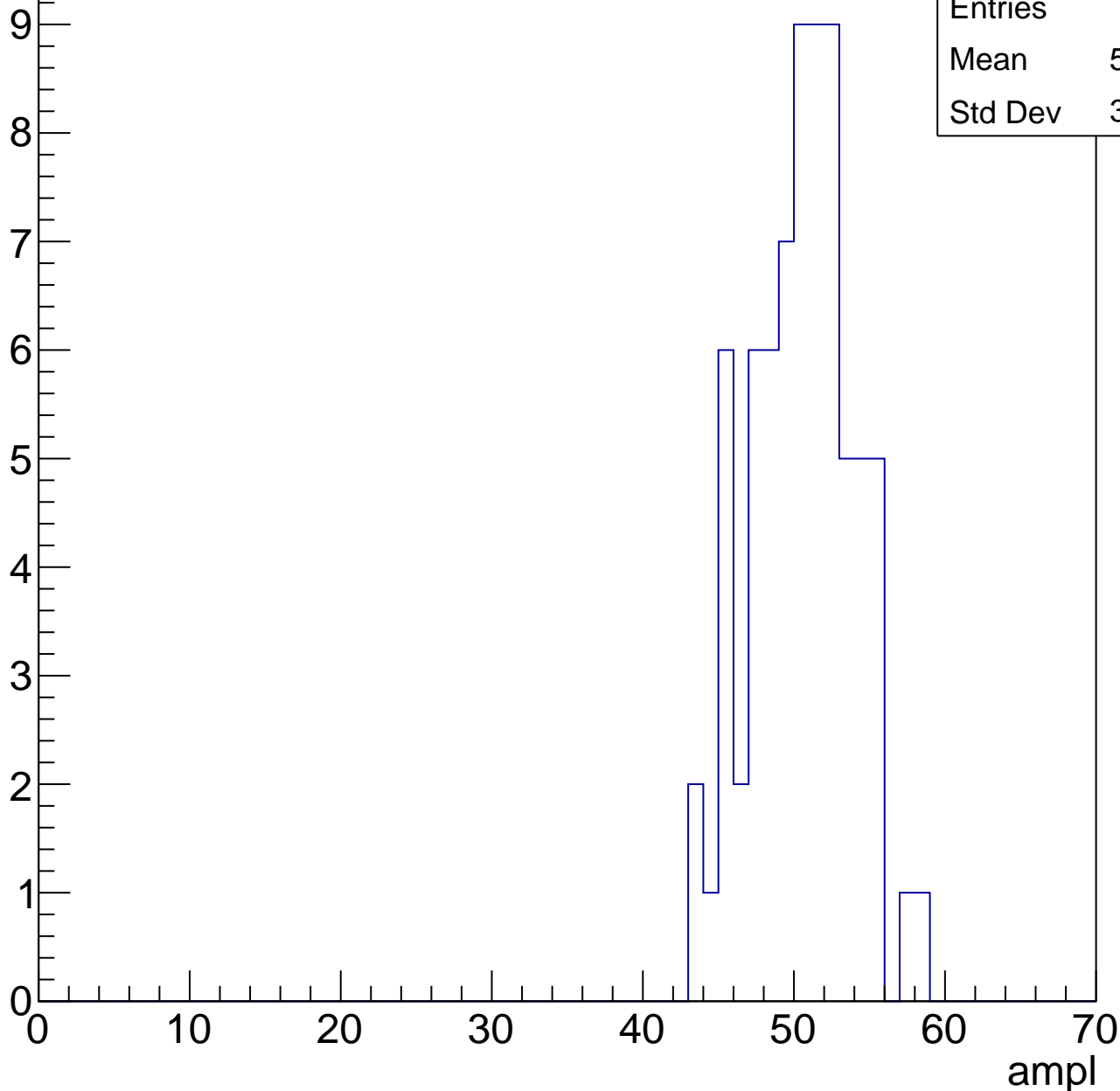
Gaus Width: 3.5642



B1L100S, U11-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	74
Mean	50.09
Std Dev	3.305

B1L100S, U11-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	56.41
Std Dev	7.626

Entry

10

8

6

4

2

0

0

10

20

30

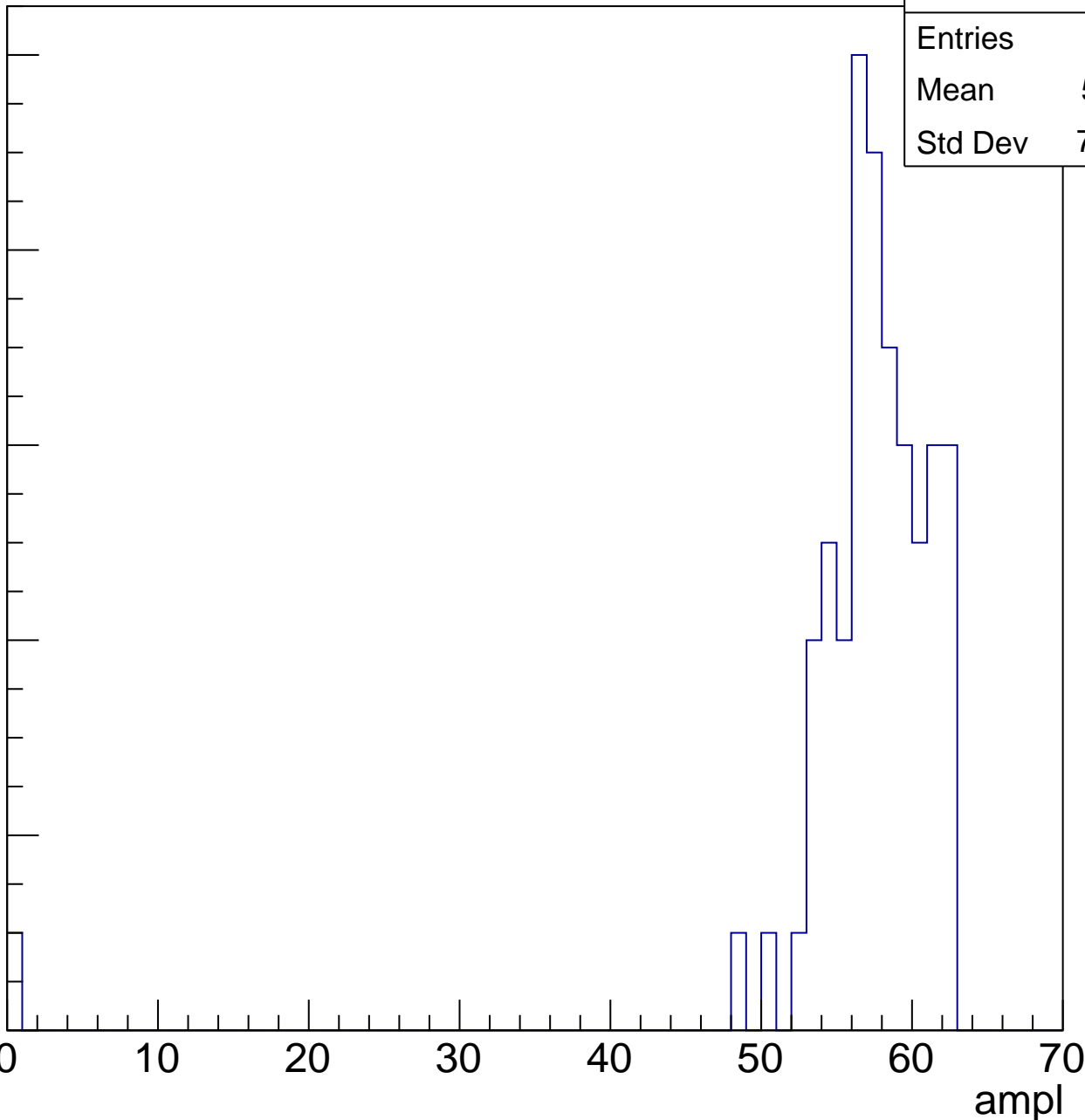
40

50

60

70

ampl

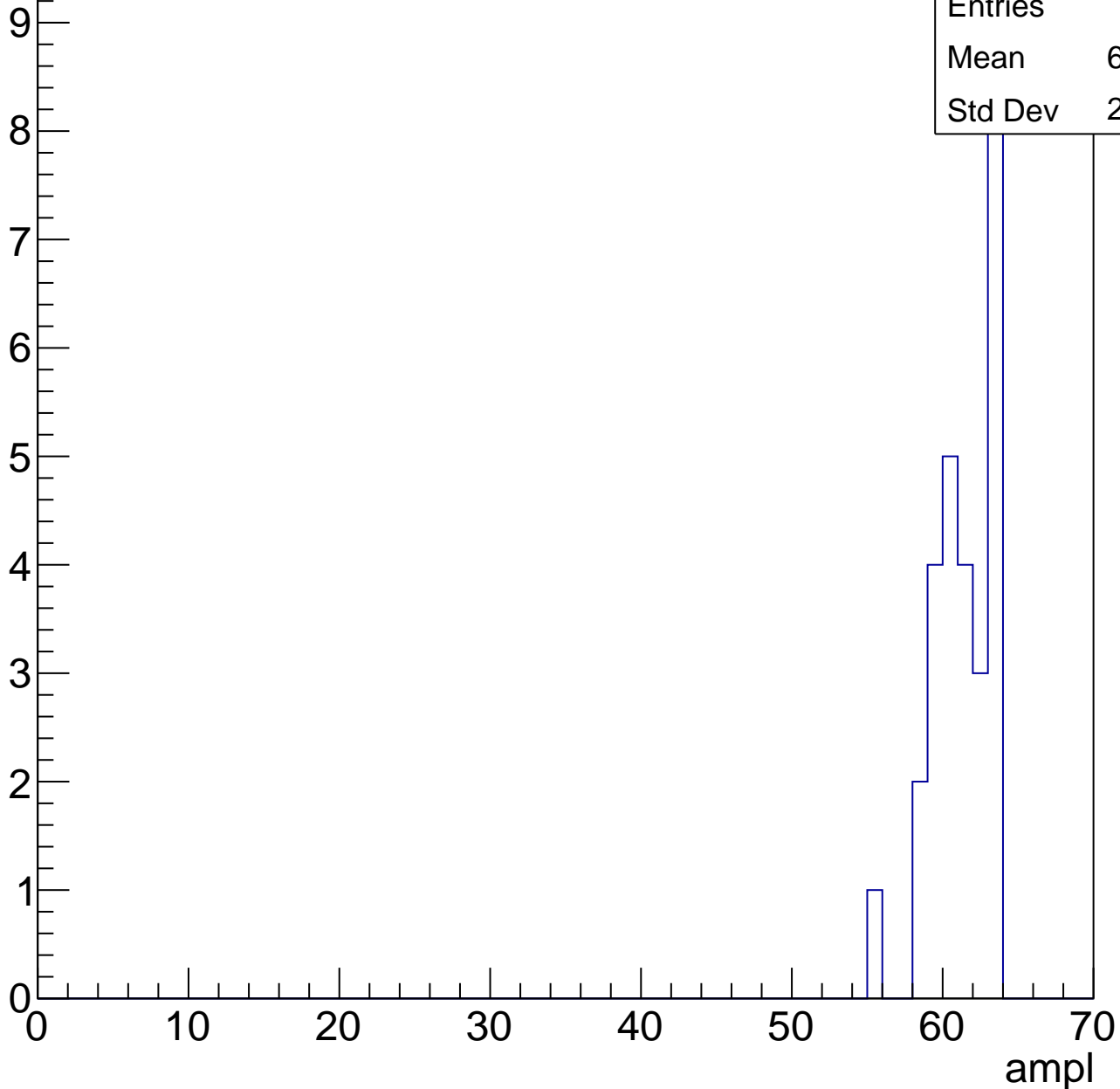


B1L100S, U11-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	60.86
Std Dev	2.013



B1L100S, U11-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch9, adc0

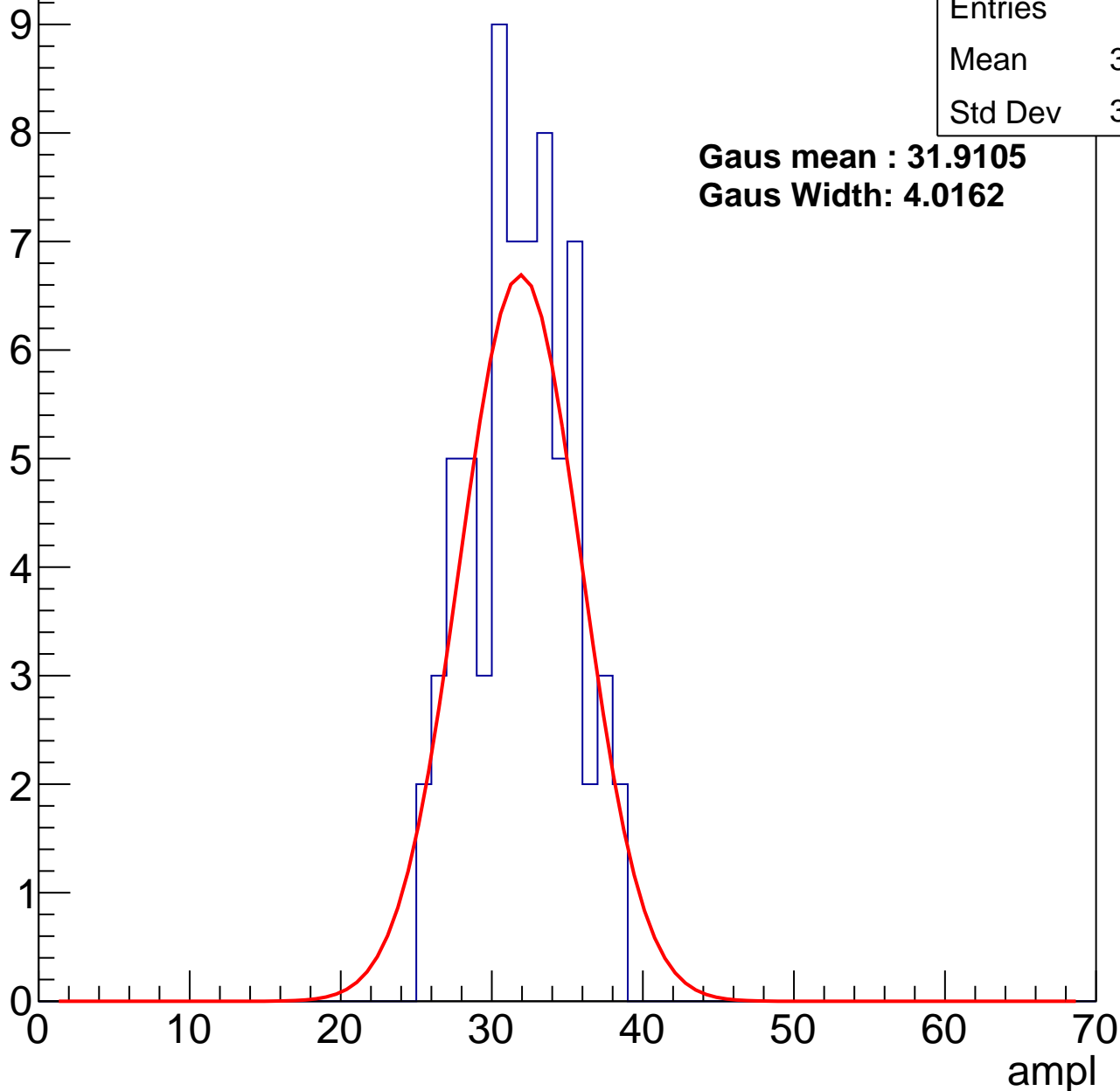
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	31.46
Std Dev	3.278

Gaus mean : 31.9105

Gaus Width: 4.0162



B1L100S, U11-ch9, adc1

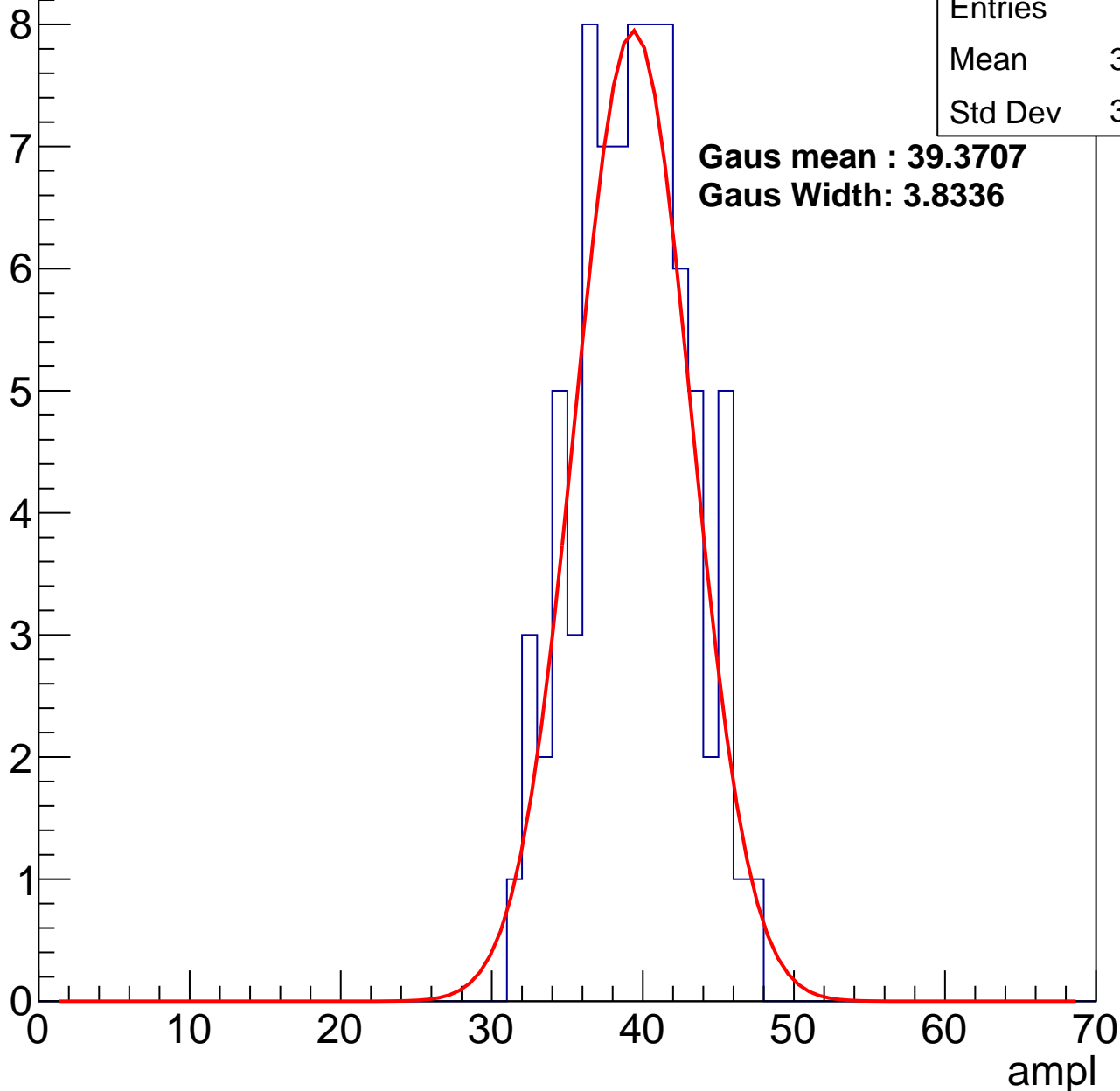
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	38.92
Std Dev	3.673

Gaus mean : 39.3707

Gaus Width: 3.8336



B1L100S, U11-ch9, adc2

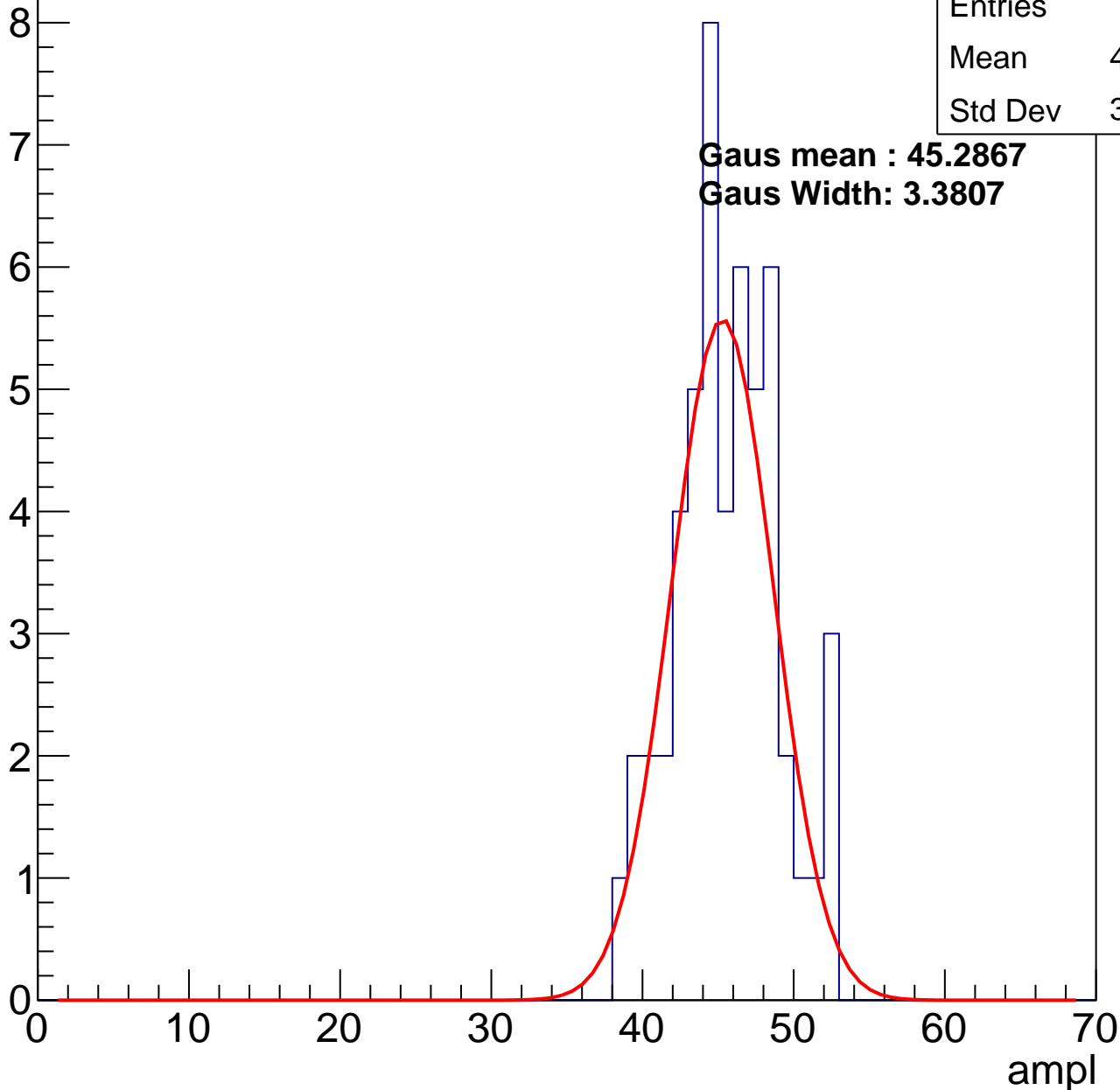
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	45.13
Std Dev	3.357

Gaus mean : 45.2867

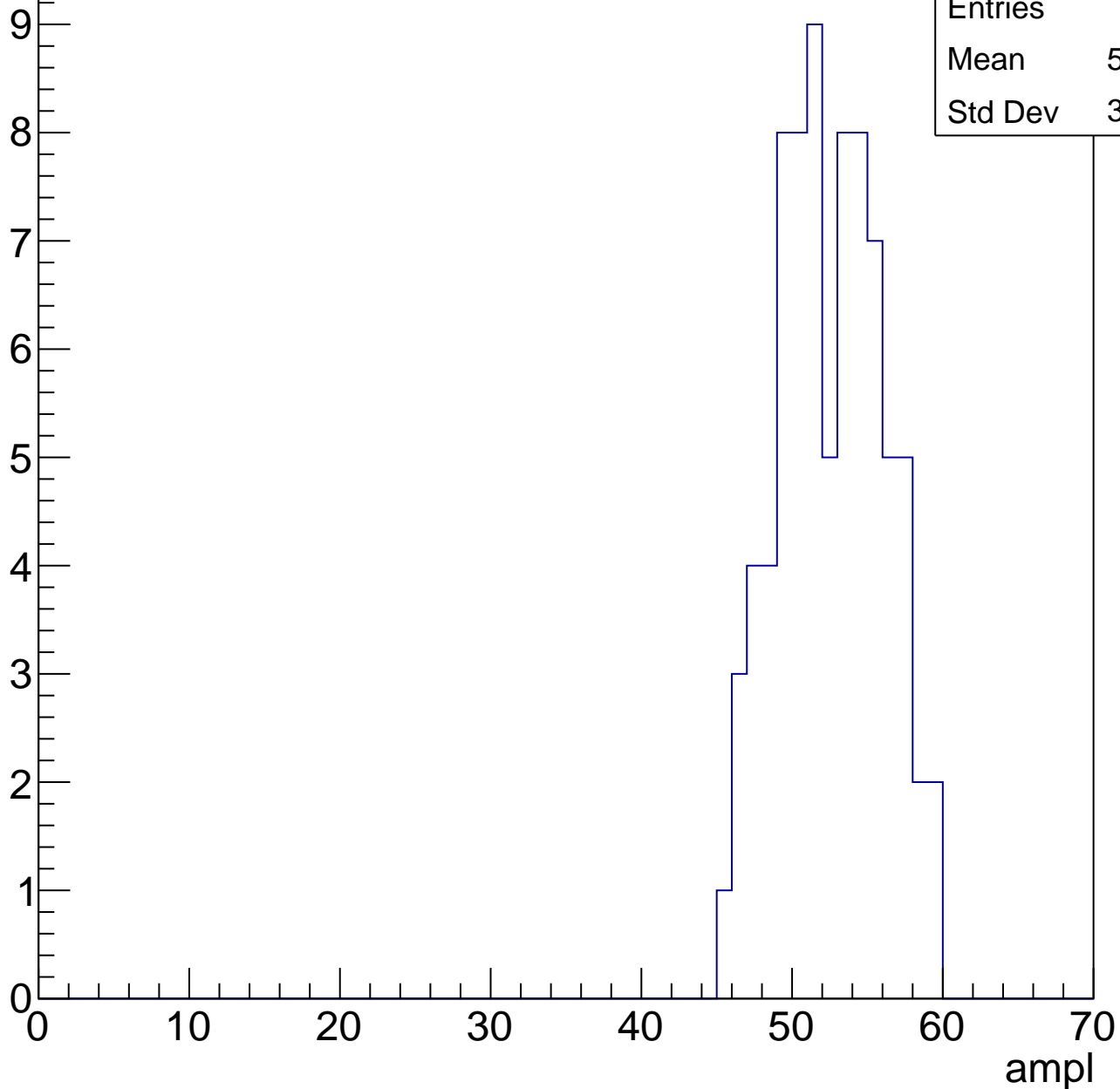
Gaus Width: 3.3807



B1L100S, U11-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

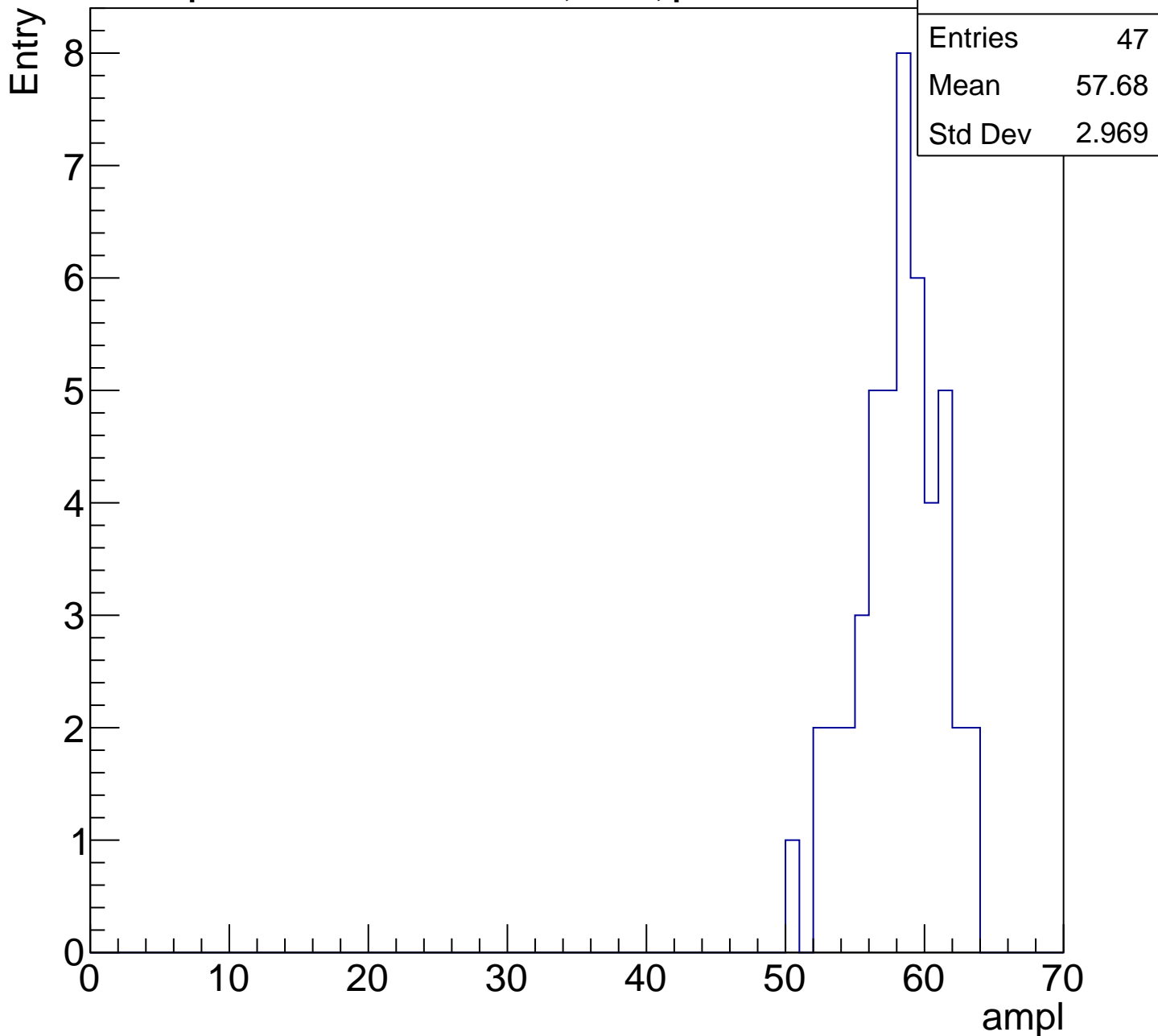
Entry



Entries	79
Mean	52.08
Std Dev	3.397

B1L100S, U11-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

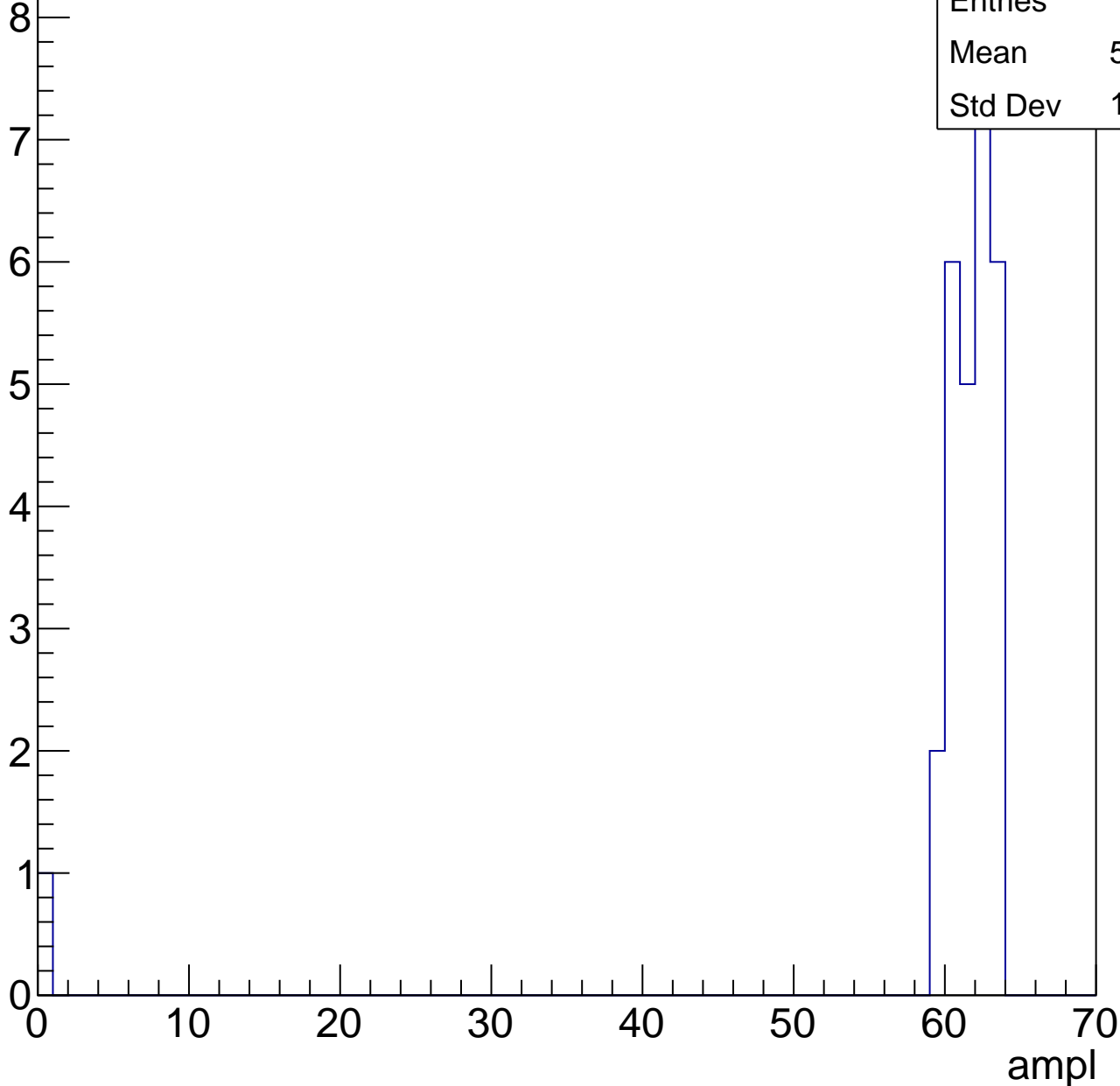


B1L100S, U11-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	59.18
Std Dev	11.46



B1L100S, U11-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U11-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch10, adc0

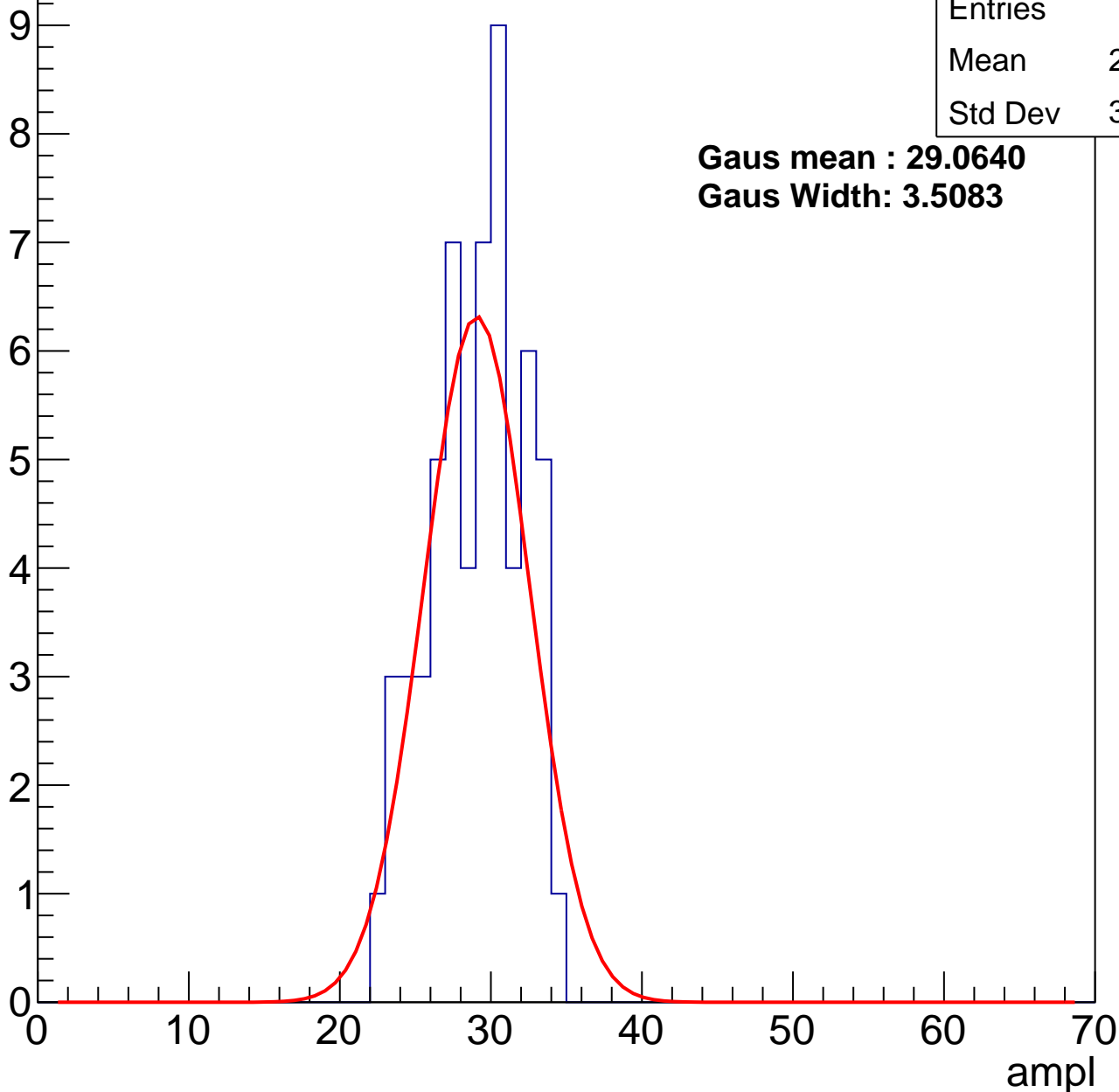
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	28.57
Std Dev	3.024

Gaus mean : 29.0640

Gaus Width: 3.5083

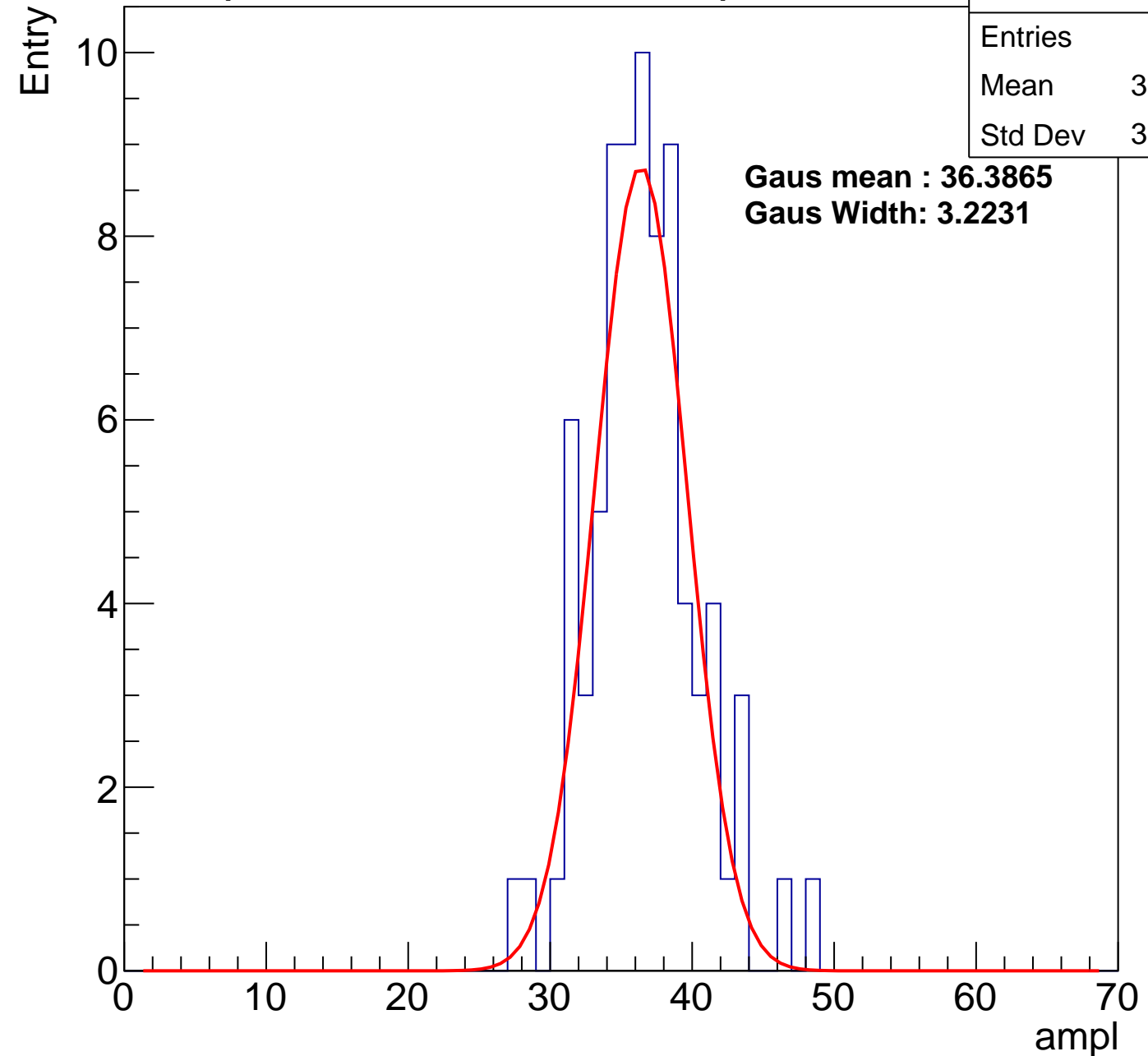


B1L100S, U11-ch10, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	36.15
Std Dev	3.769

Gaus mean : 36.3865
Gaus Width: 3.2231



B1L100S, U11-ch10, adc2

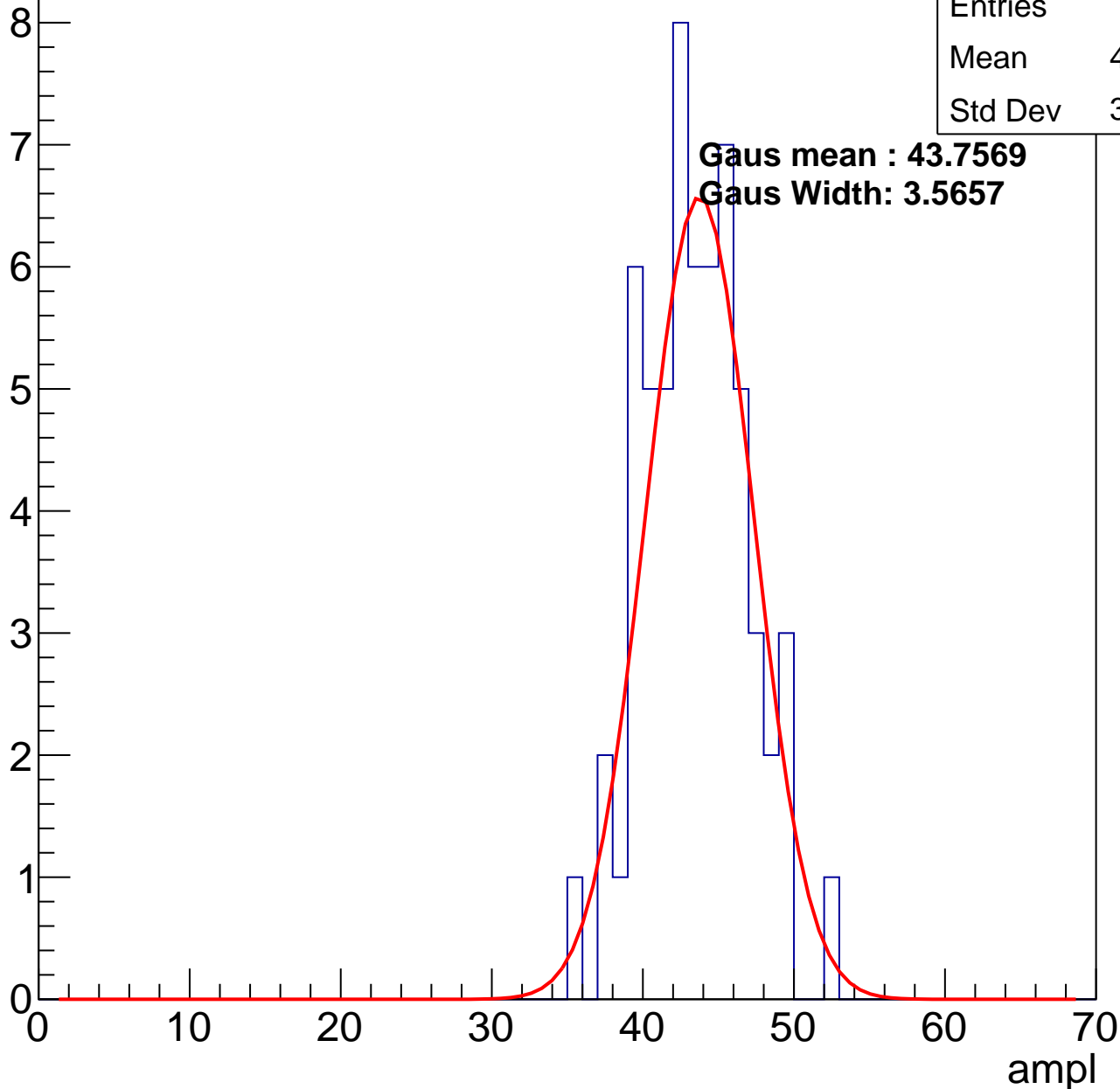
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.03
Std Dev	3.378

Gaus mean : 43.7569

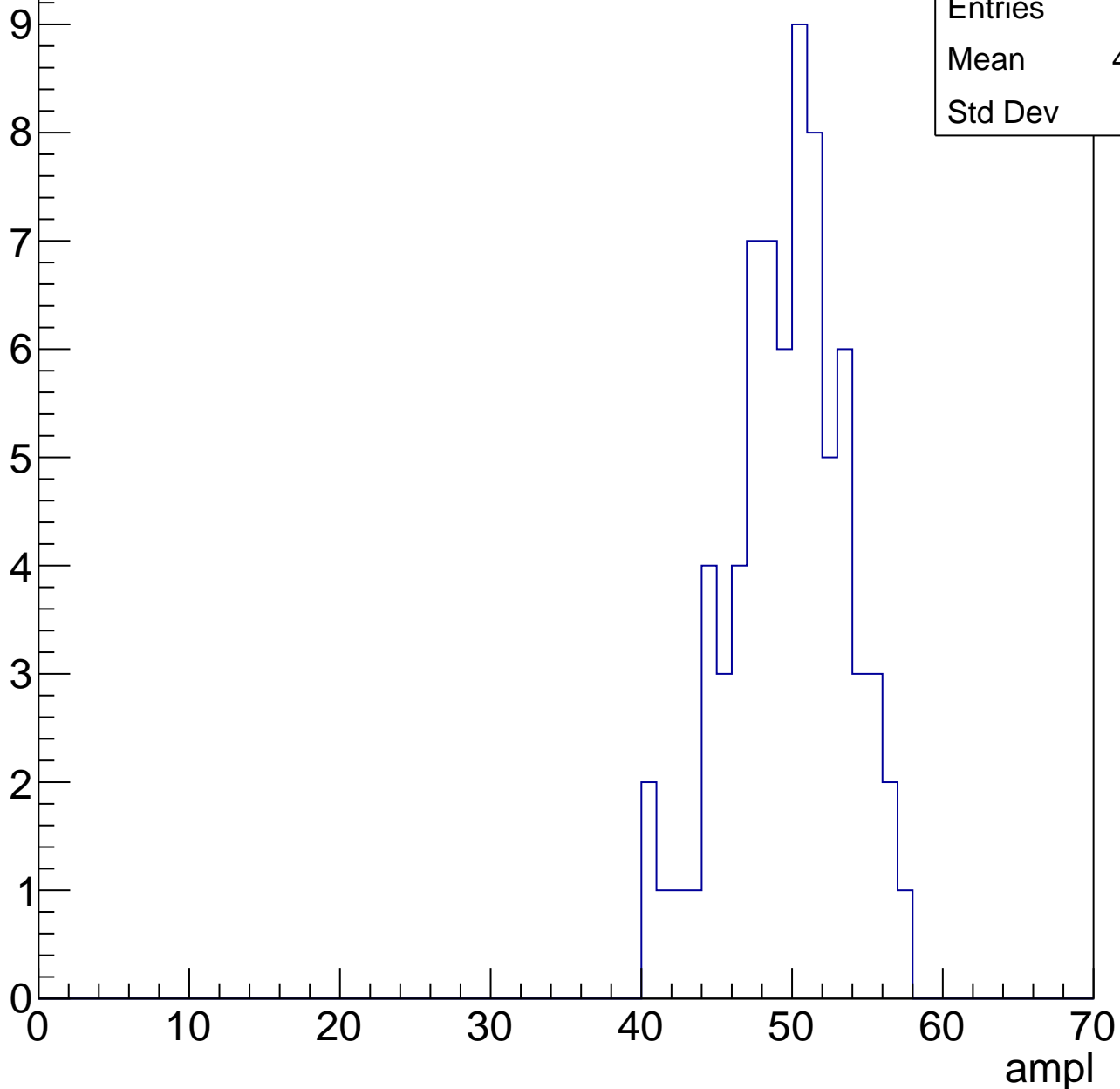
Gaus Width: 3.5657



B1L100S, U11-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

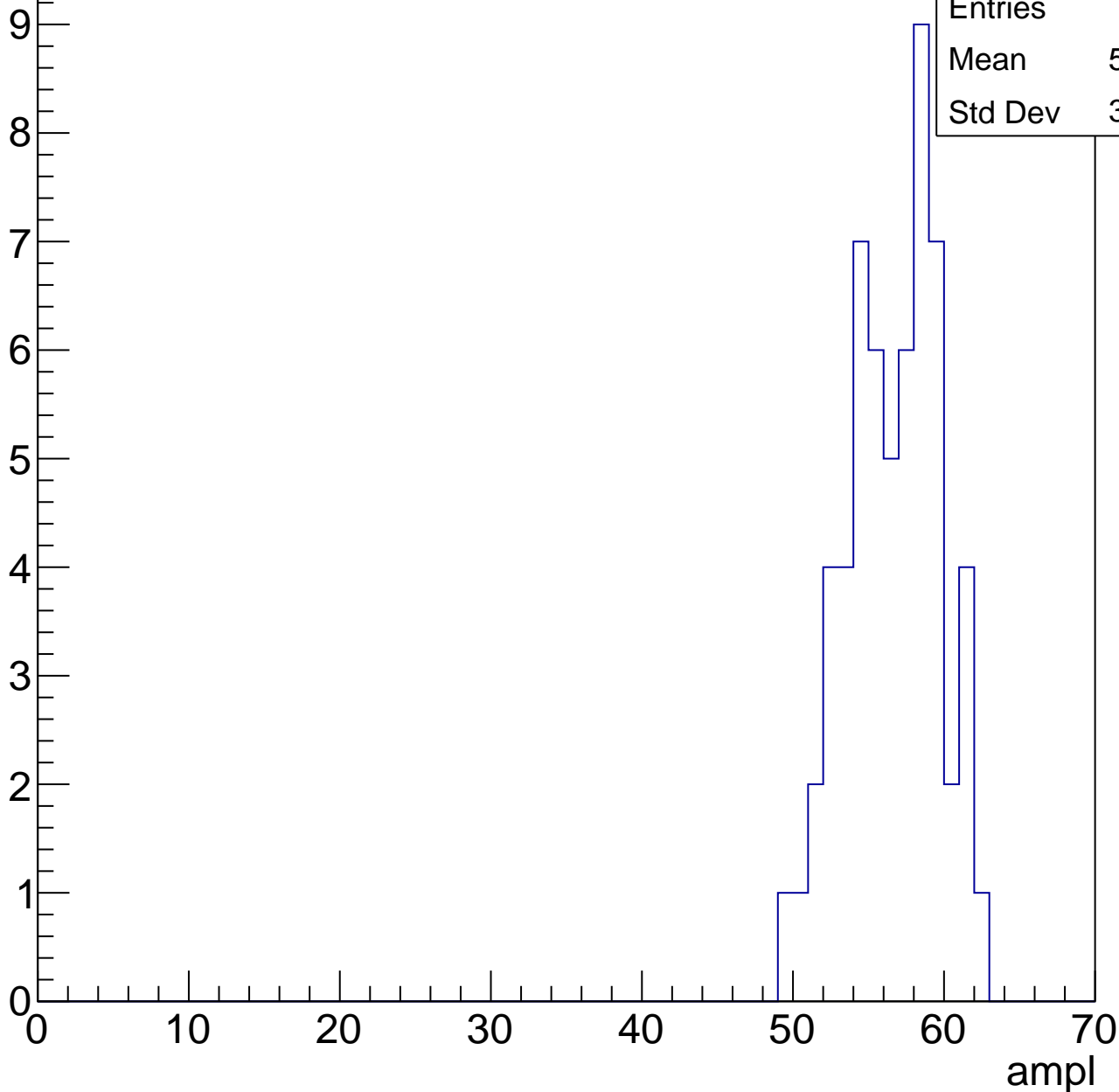


B1L100S, U11-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.14
Std Dev	3.028



B1L100S, U11-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	42
Mean	60.48
Std Dev	2.312

Entry

10

8

6

4

2

0

0

10

20

30

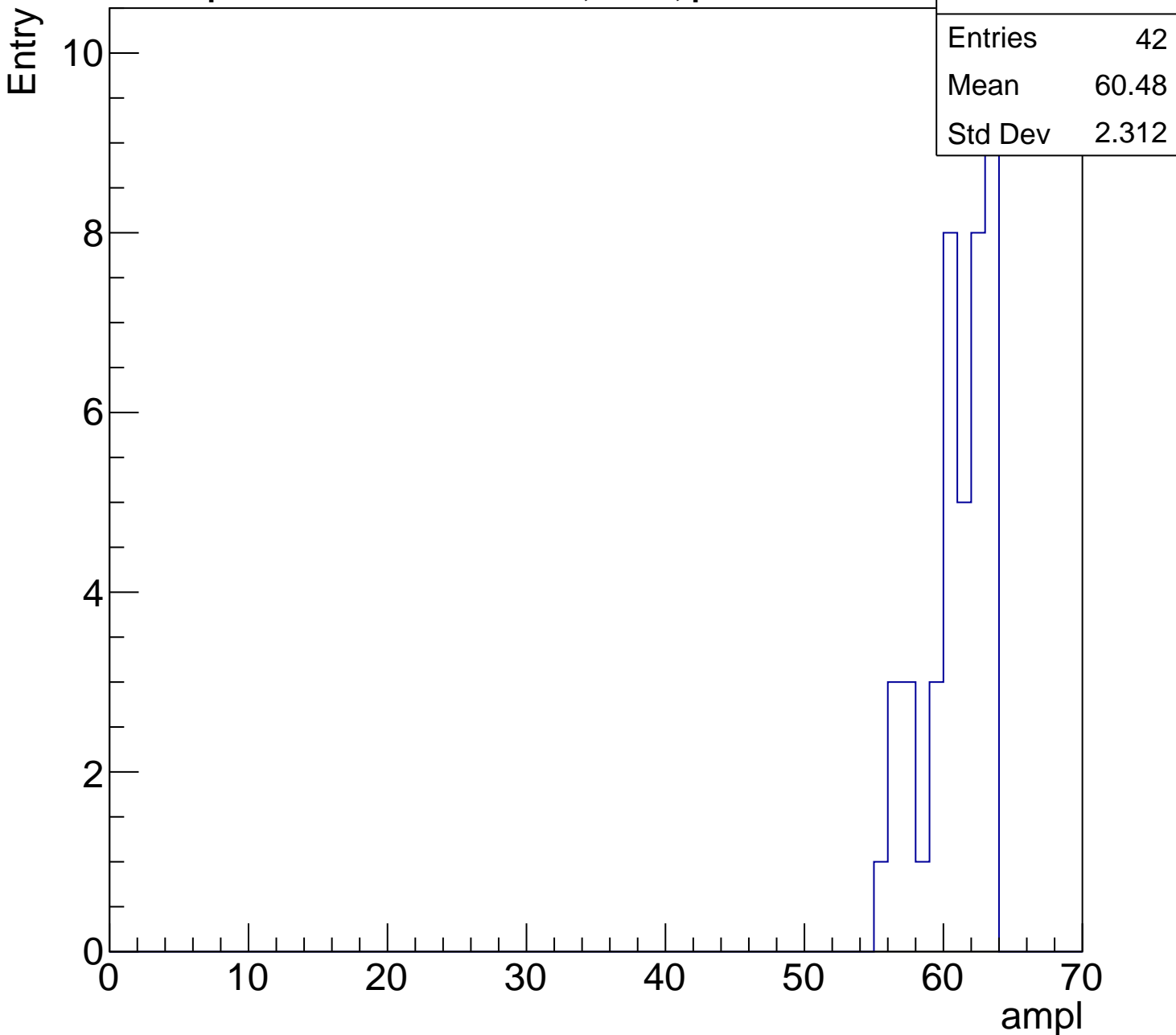
40

50

60

ampl

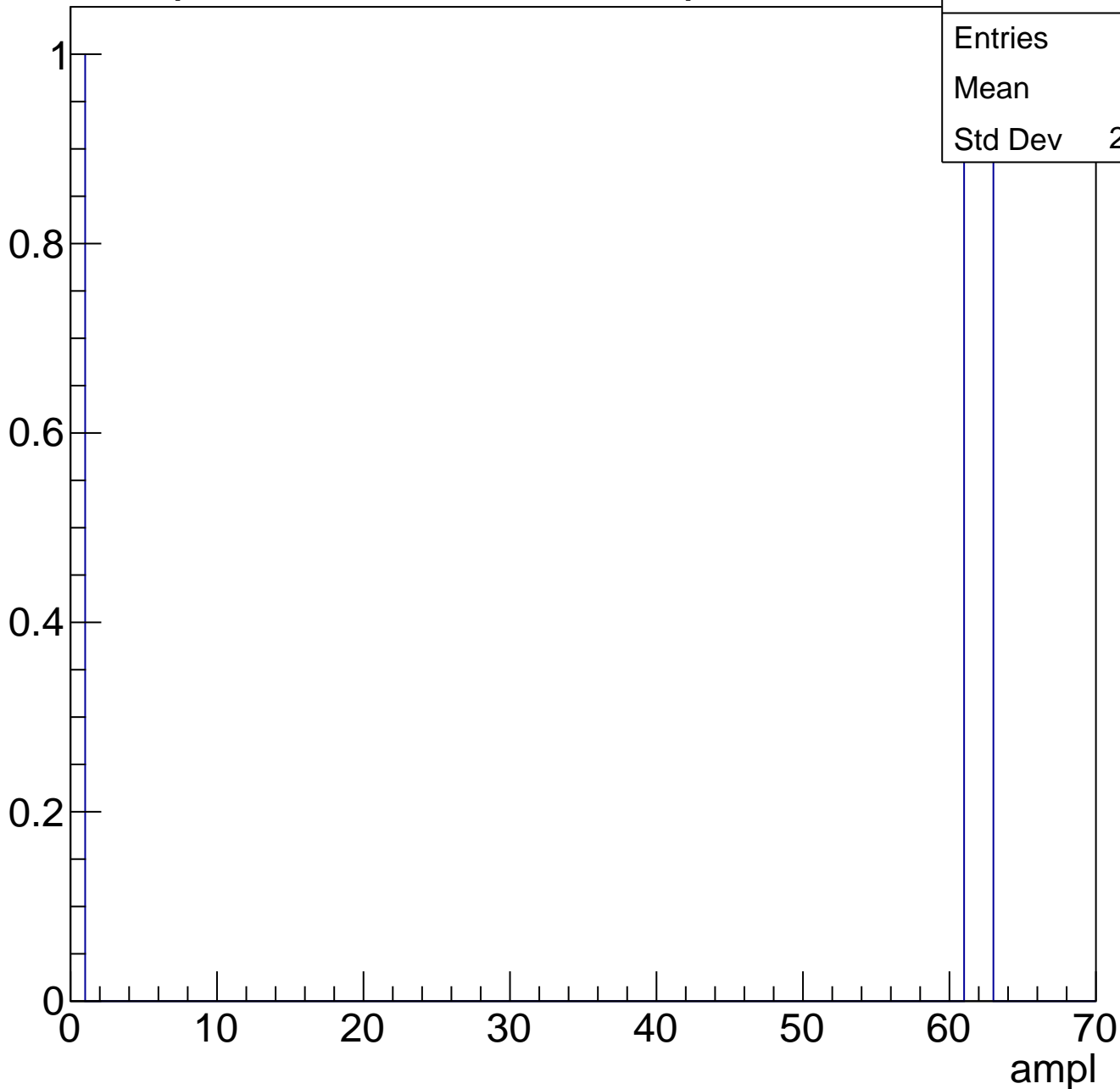
70



B1L100S, U11-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

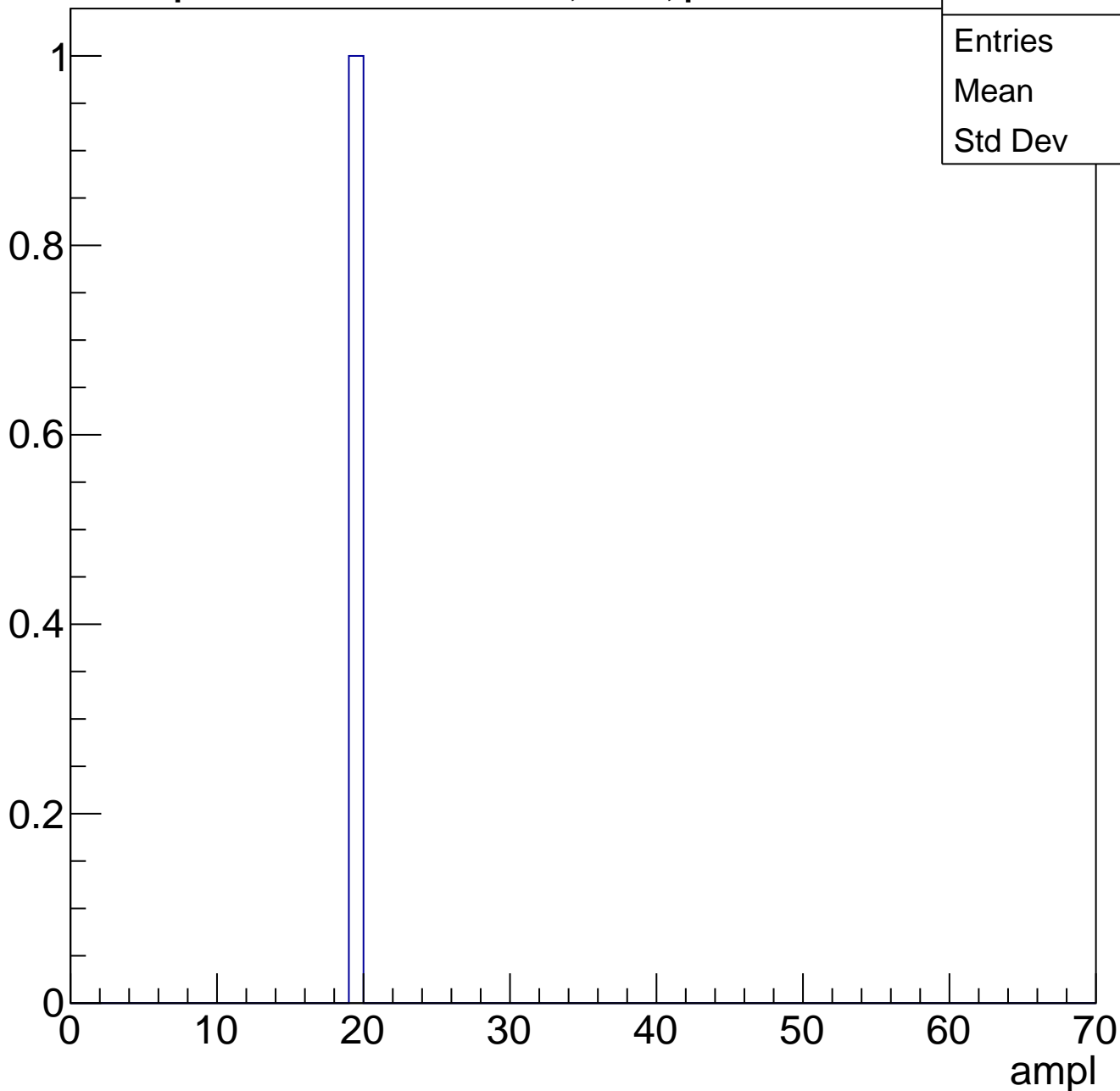
Entry



B1L100S, U11-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch11, adc0

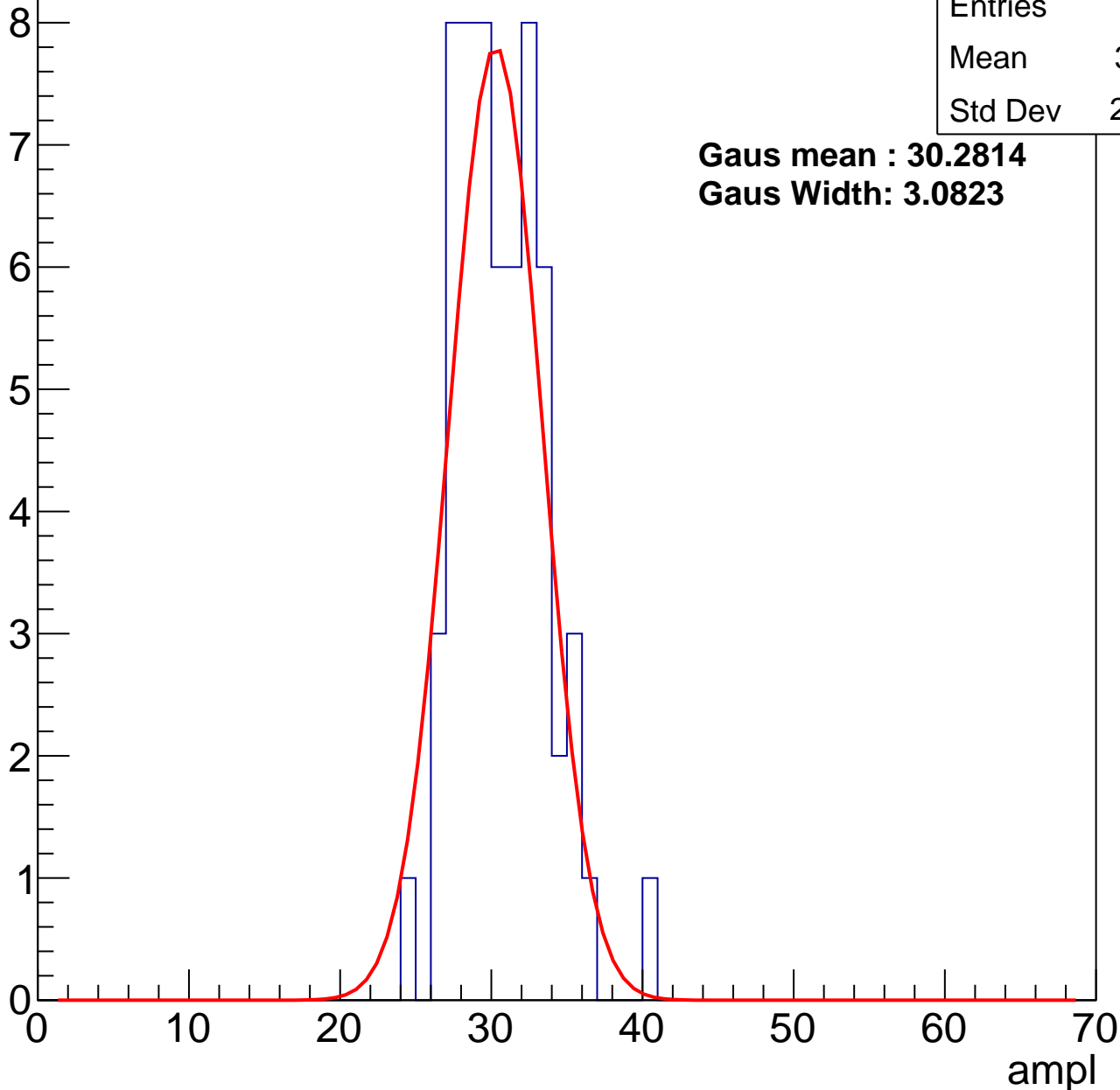
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	30.21
Std Dev	2.943

Gaus mean : 30.2814

Gaus Width: 3.0823



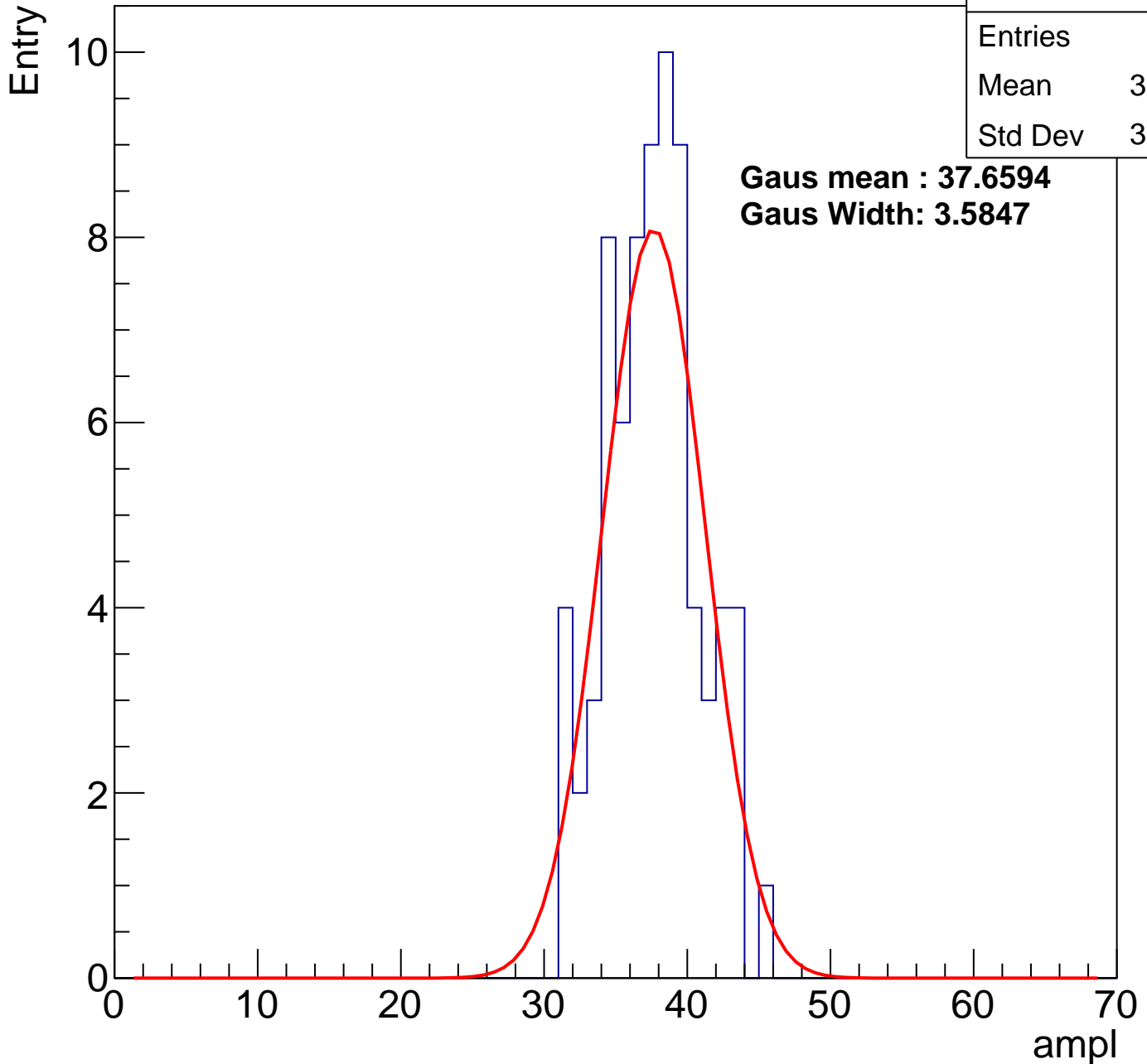
B1L100S, U11-ch11, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	37.19
Std Dev	3.228

Gaus mean : 37.6594

Gaus Width: 3.5847



B1L100S, U11-ch11, adc2

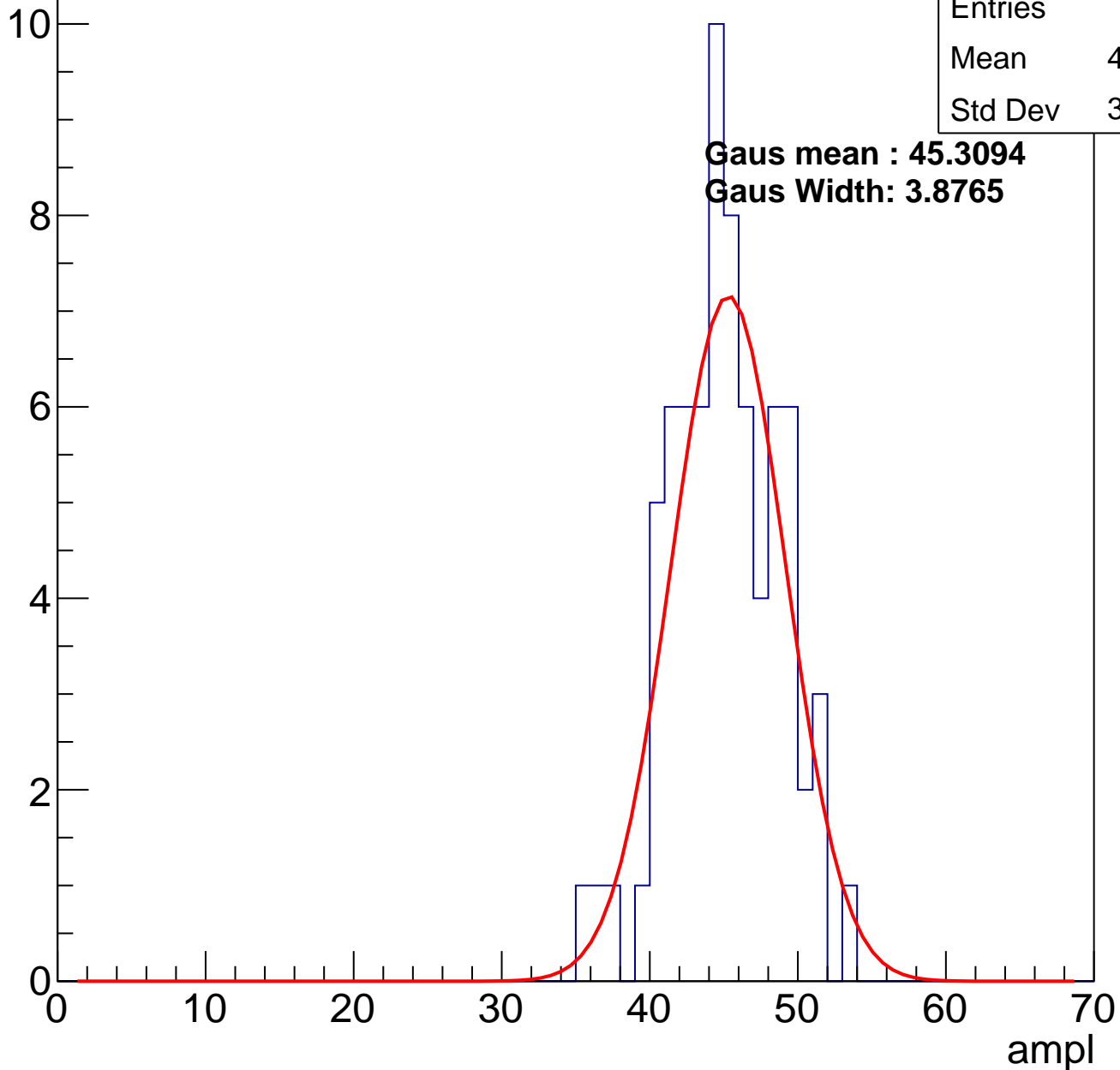
calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	44.59
Std Dev	3.648

Gaus mean : 45.3094

Gaus Width: 3.8765

Entry



B1L100S, U11-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

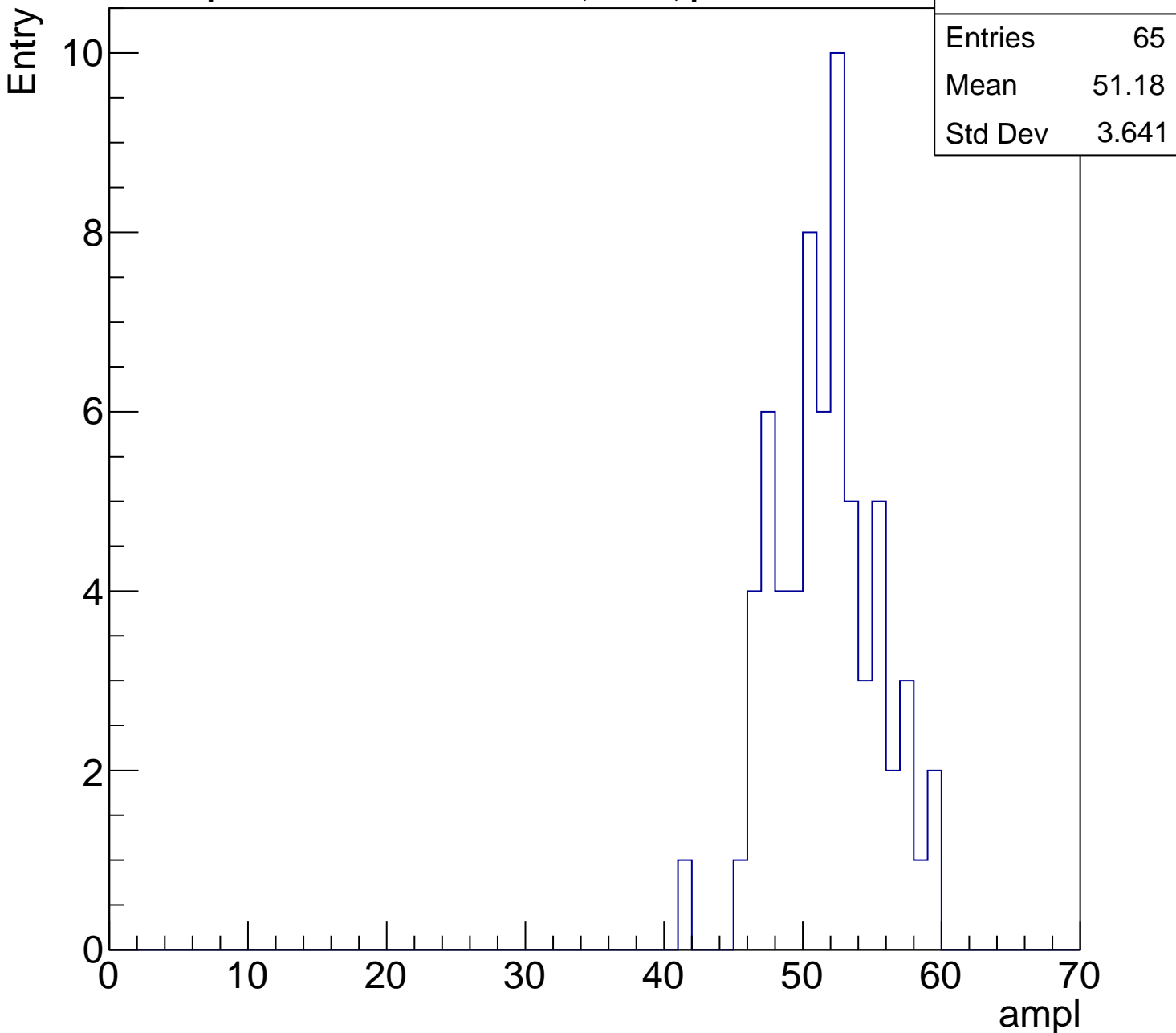
Entries	65
Mean	51.18
Std Dev	3.641

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

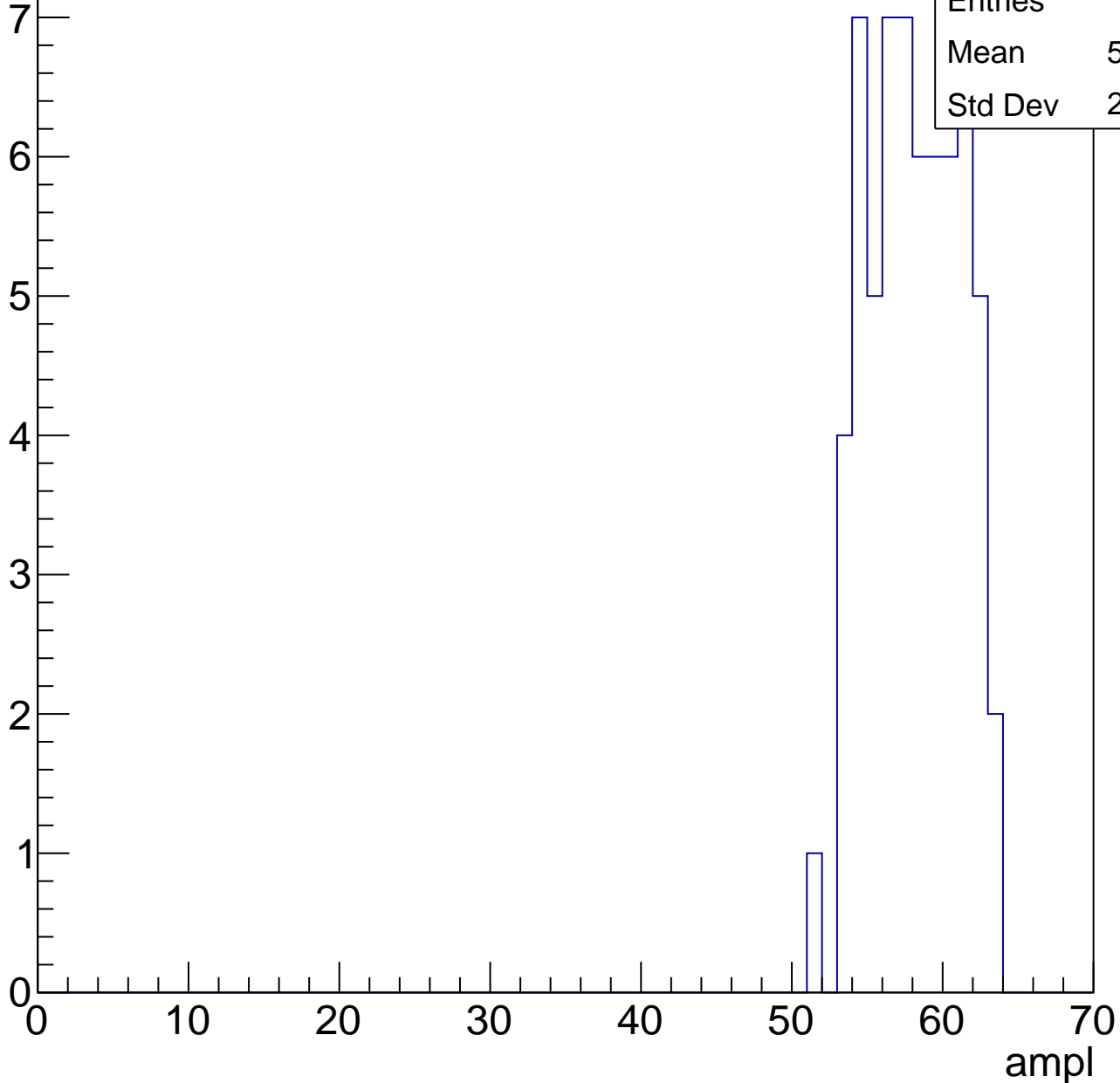


B1L100S, U11-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	57.65
Std Dev	2.972

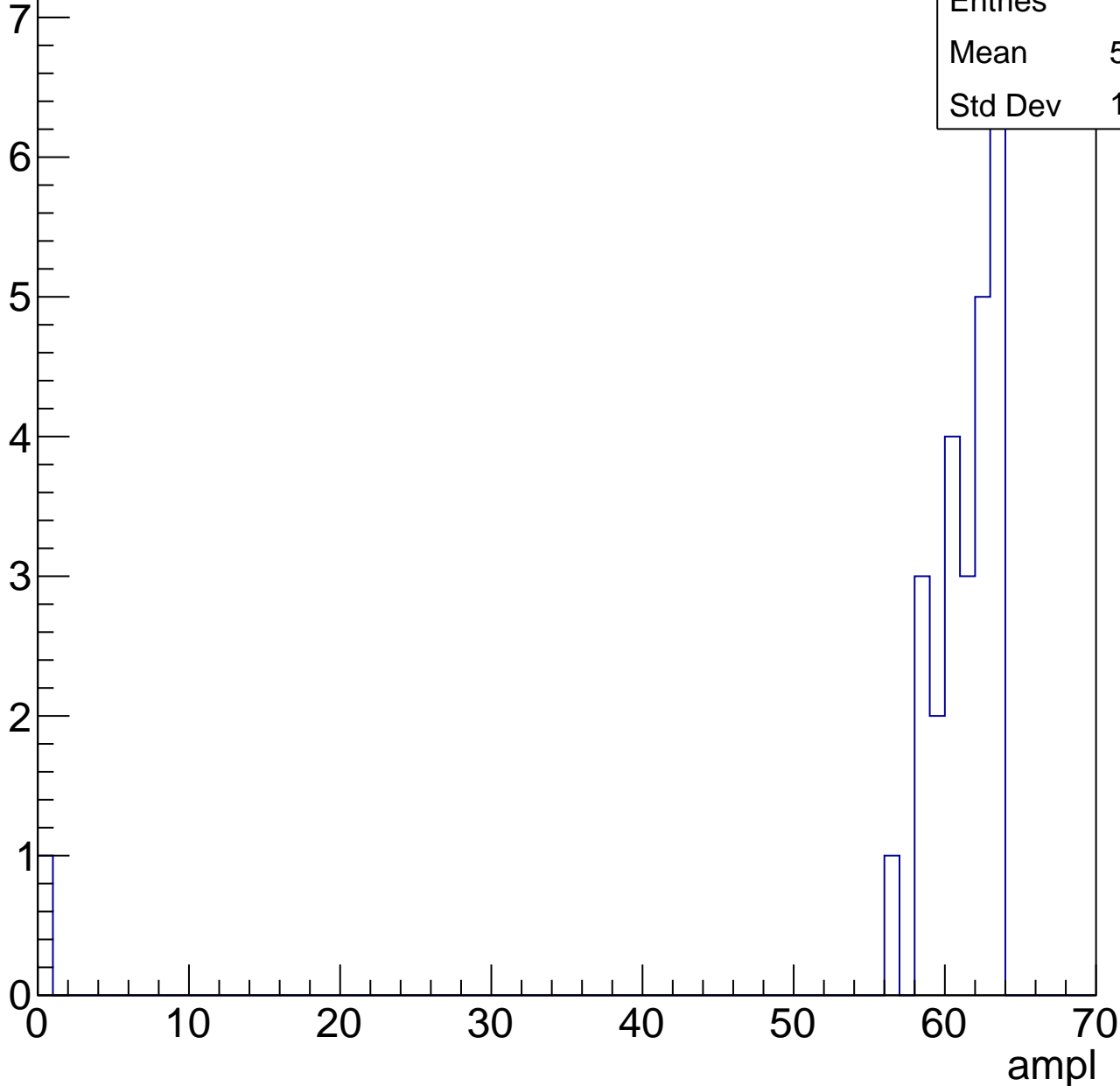


B1L100S, U11-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.54
Std Dev	11.87



B1L100S, U11-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L100S, U11-ch12, adc0

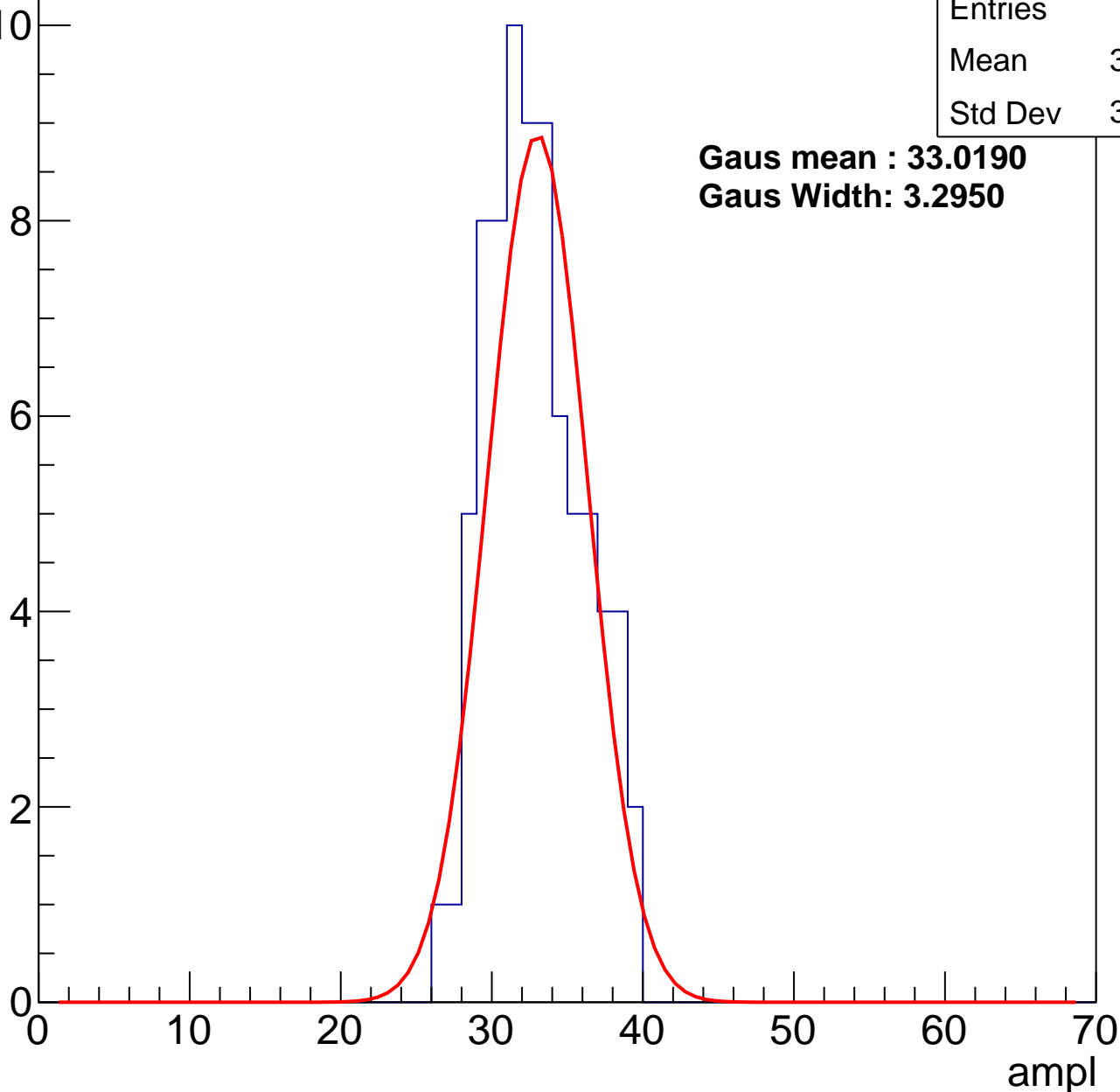
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	32.43
Std Dev	3.102

Gaus mean : 33.0190

Gaus Width: 3.2950



B1L100S, U11-ch12, adc1

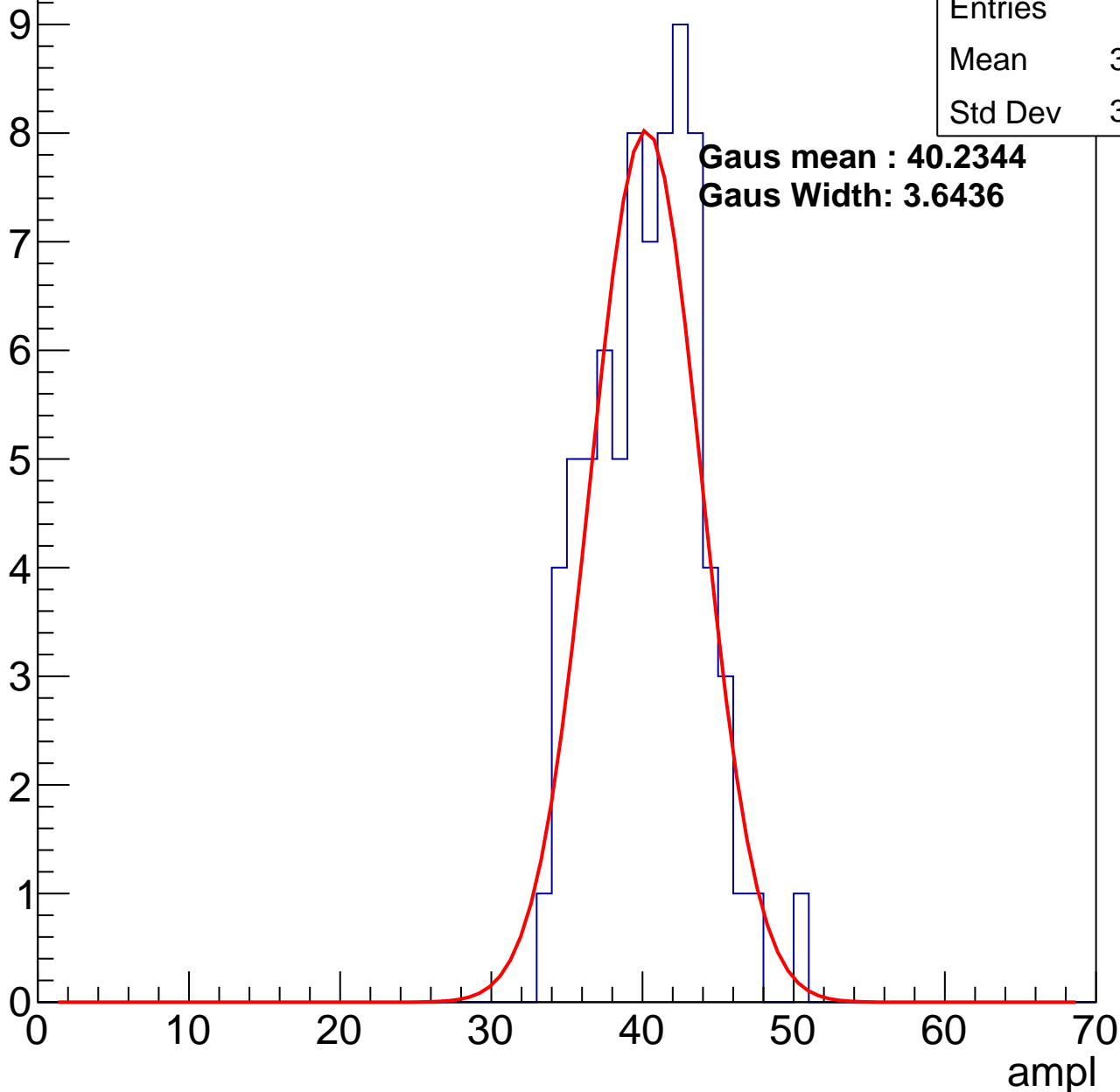
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	39.89
Std Dev	3.478

Gaus mean : 40.2344

Gaus Width: 3.6436



B1L100S, U11-ch12, adc2

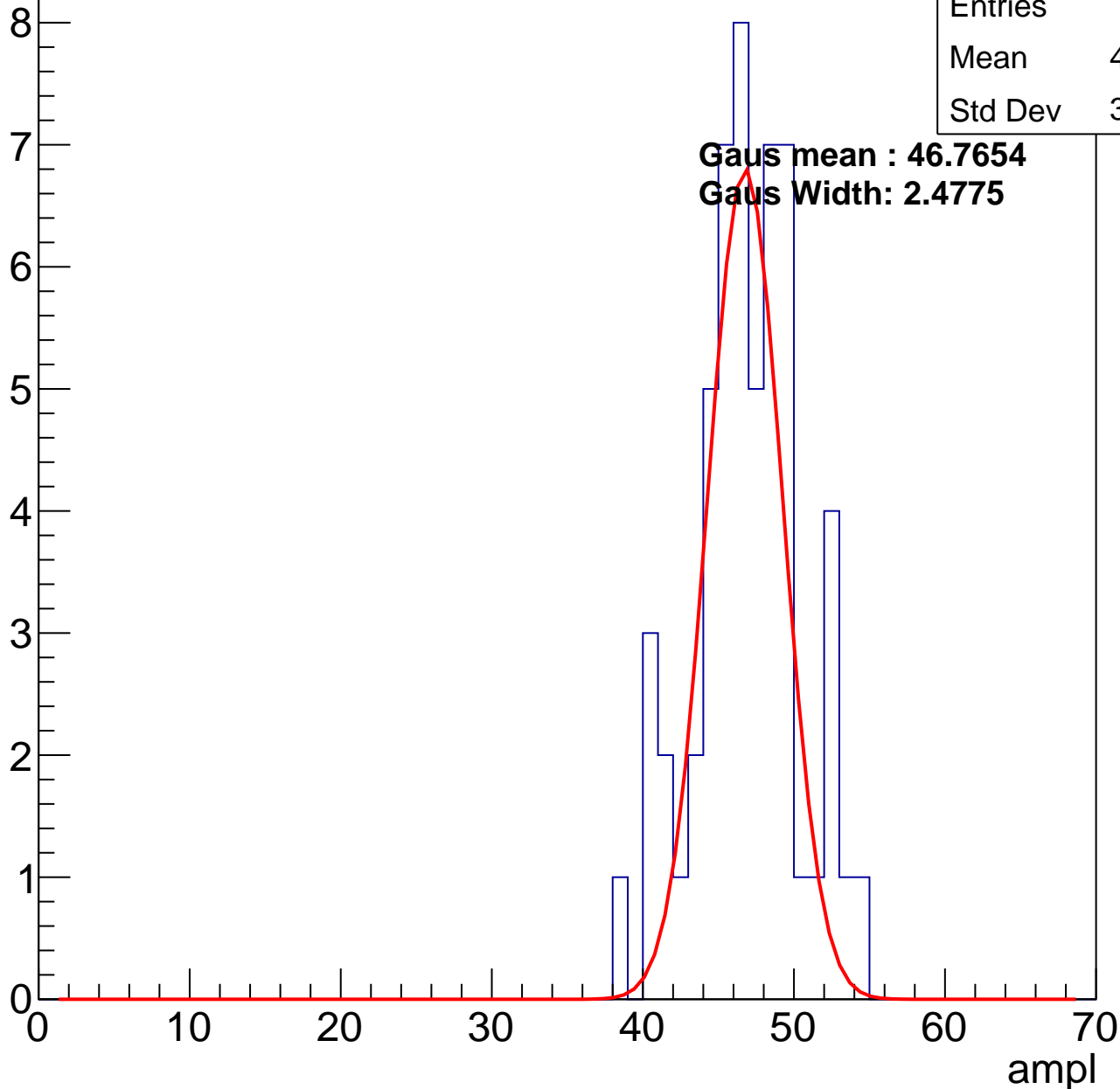
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	46.45
Std Dev	3.448

Gaus mean : 46.7654

Gaus Width: 2.4775

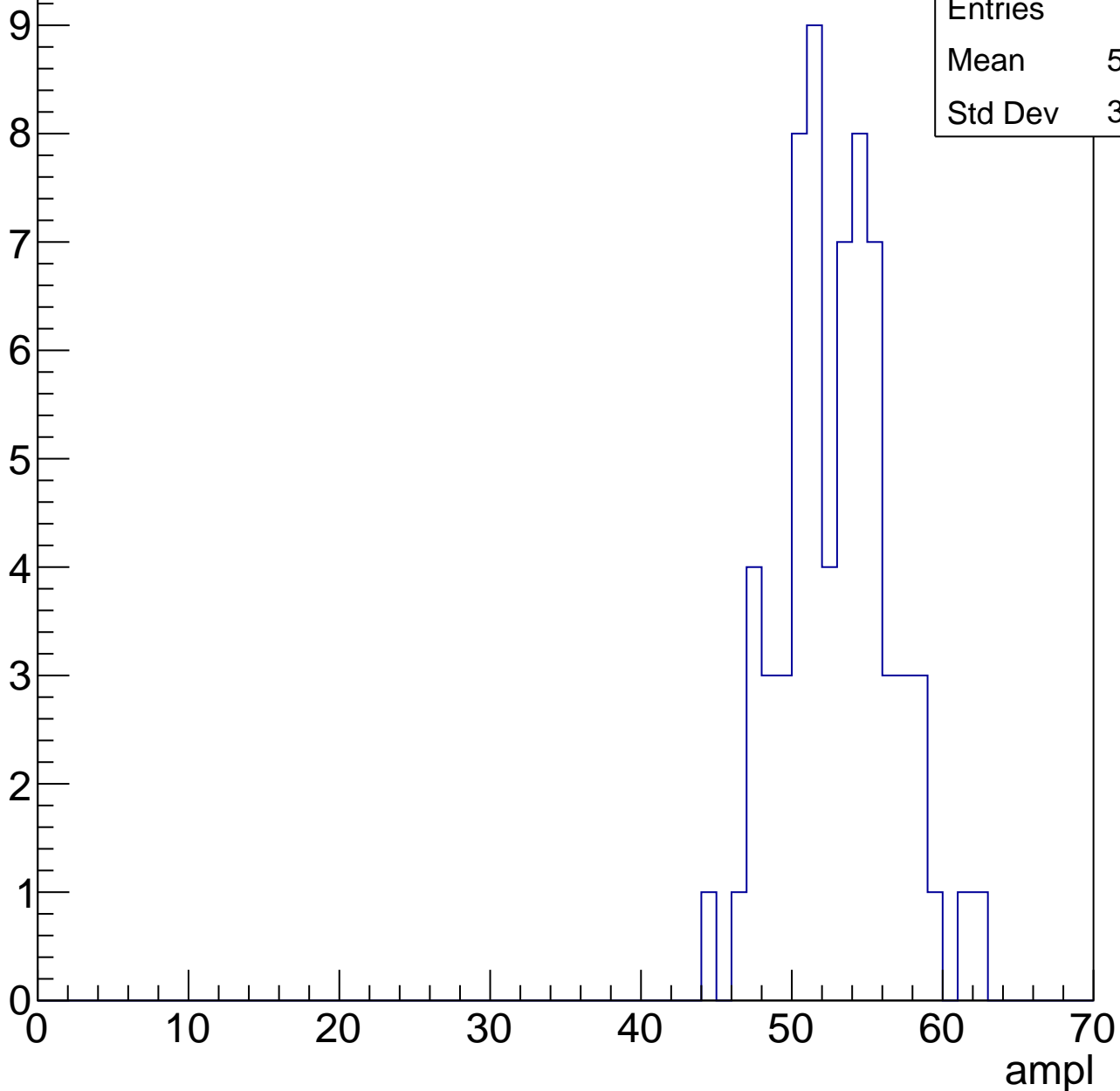


B1L100S, U11-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	52.52
Std Dev	3.584

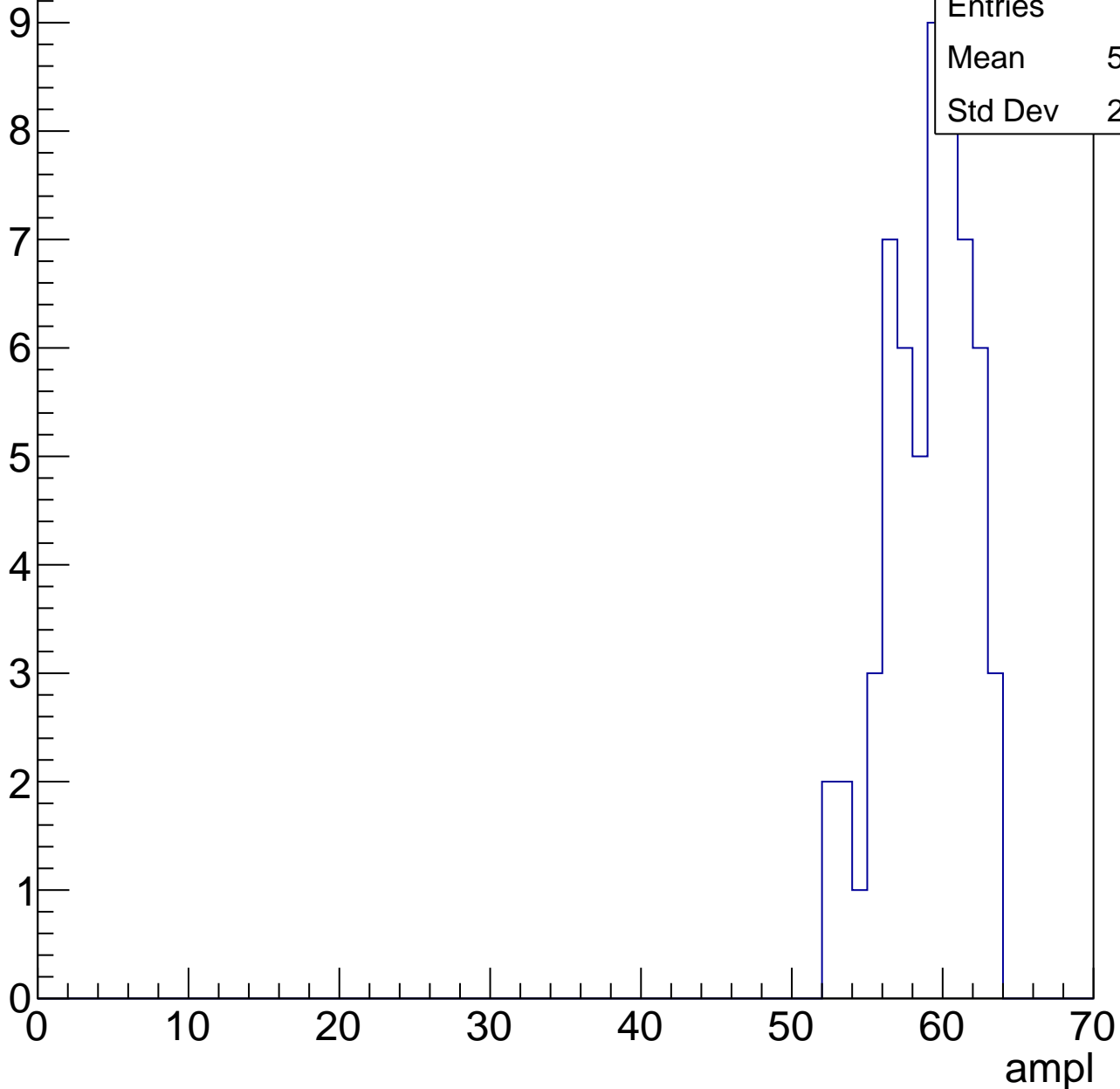


B1L100S, U11-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	58.53
Std Dev	2.772

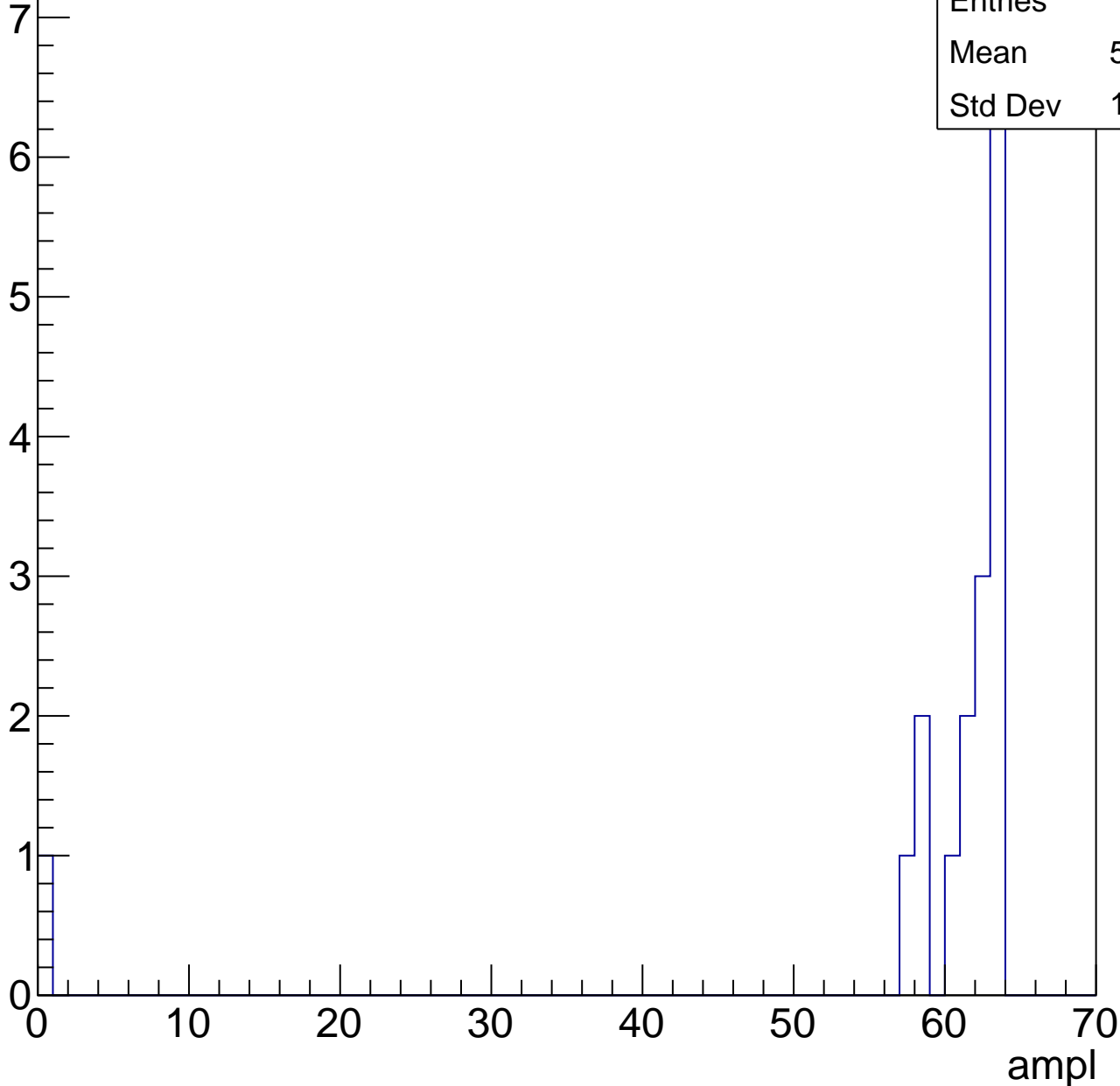


B1L100S, U11-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.76
Std Dev	14.57



B1L100S, U11-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L100S, U11-ch13, adc0

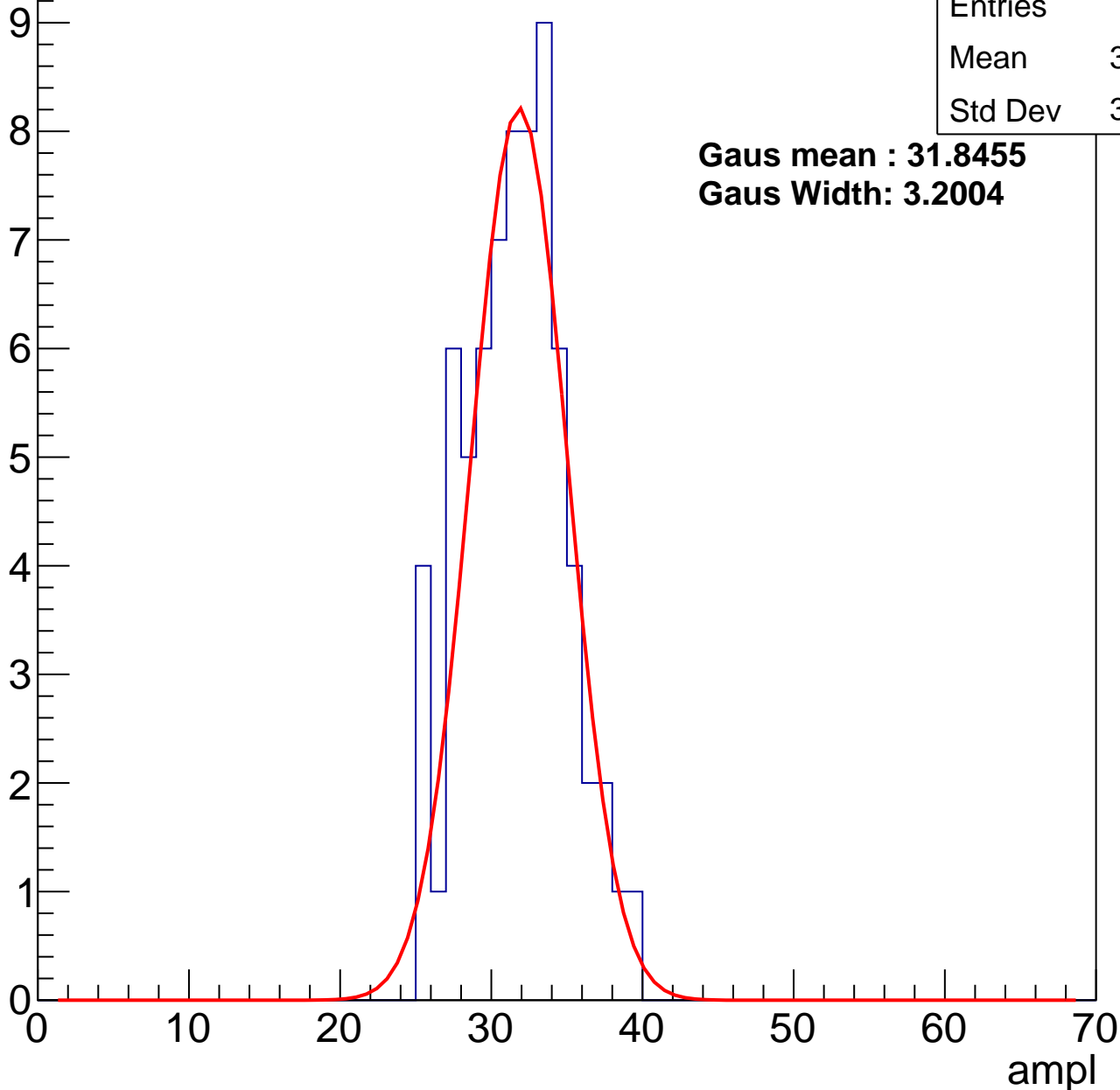
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	31.14
Std Dev	3.244

Gaus mean : 31.8455

Gaus Width: 3.2004



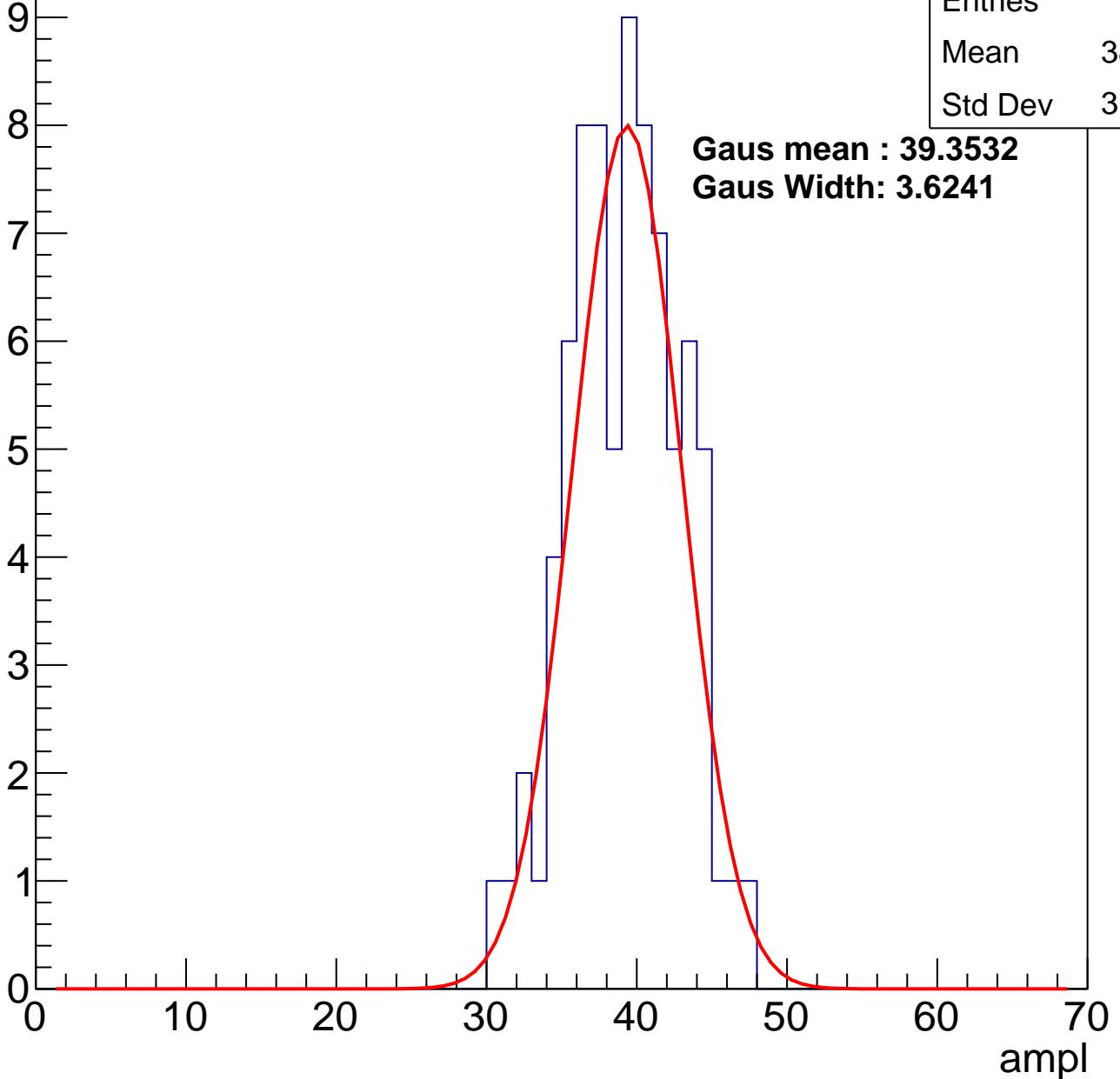
B1L100S, U11-ch13, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	38.76
Std Dev	3.612

Gaus mean : 39.3532
Gaus Width: 3.6241



B1L100S, U11-ch13, adc2

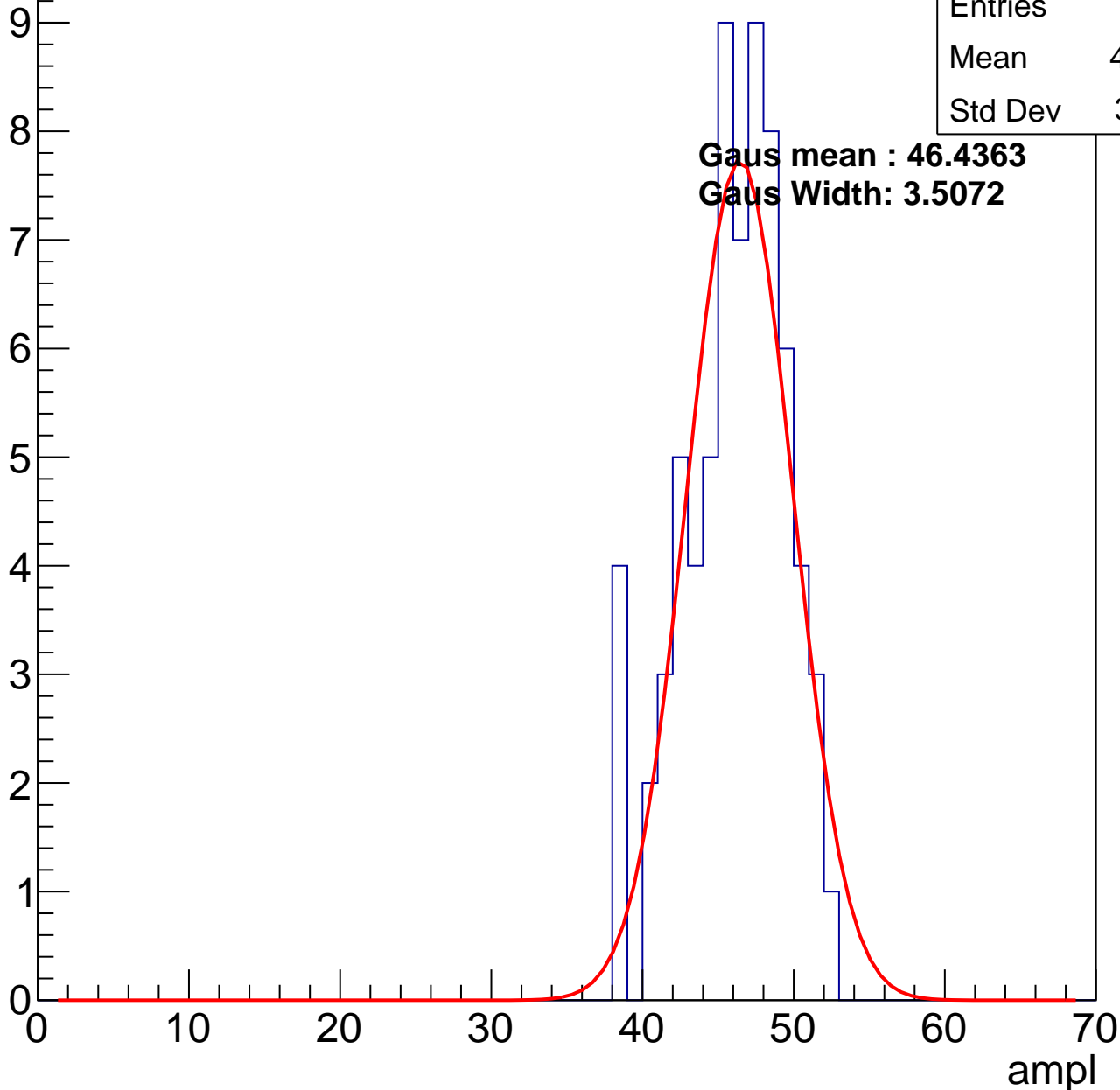
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	45.57
Std Dev	3.391

Gaus mean : 46.4363

Gaus Width: 3.5072

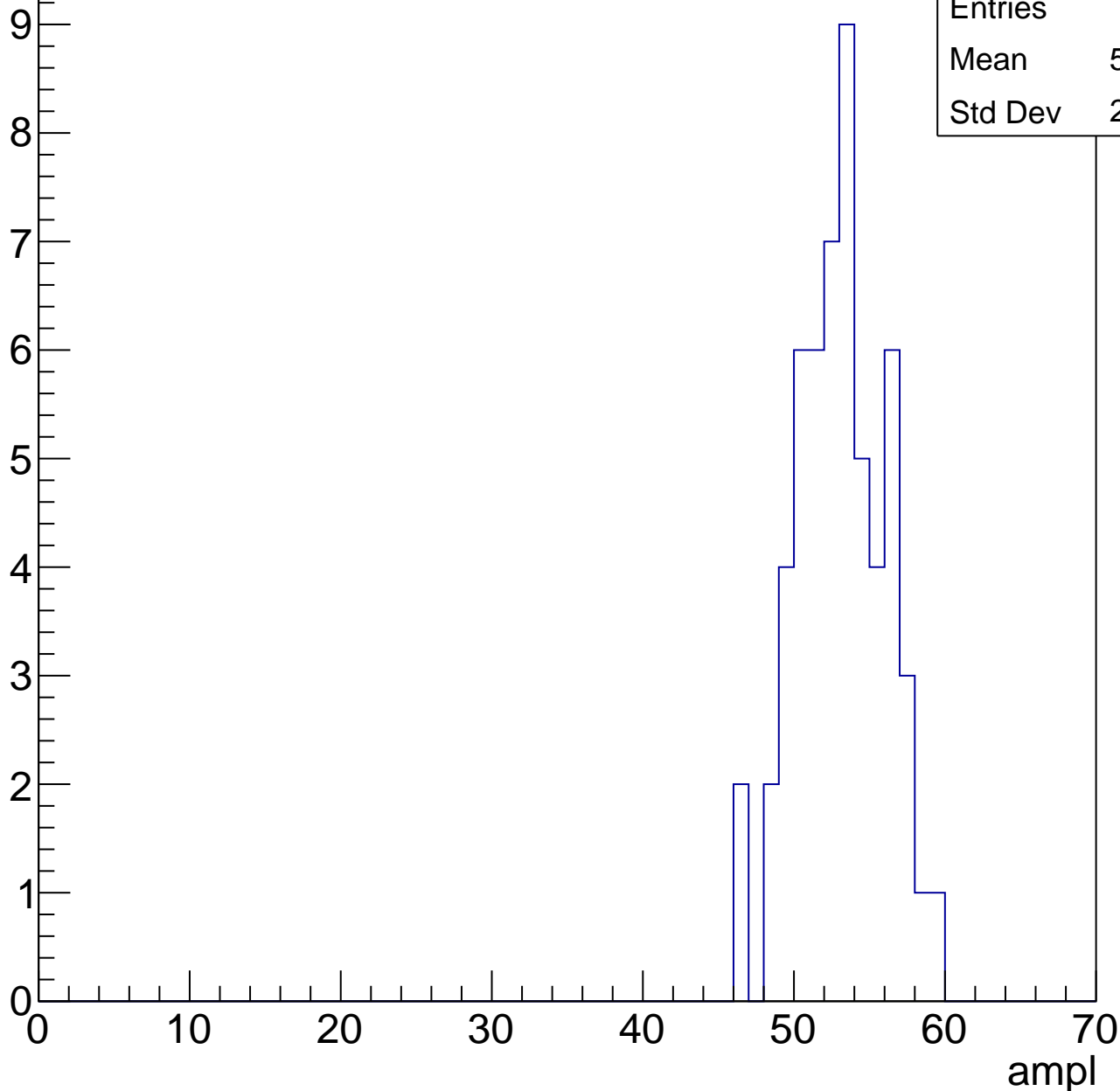


B1L100S, U11-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	52.59
Std Dev	2.902

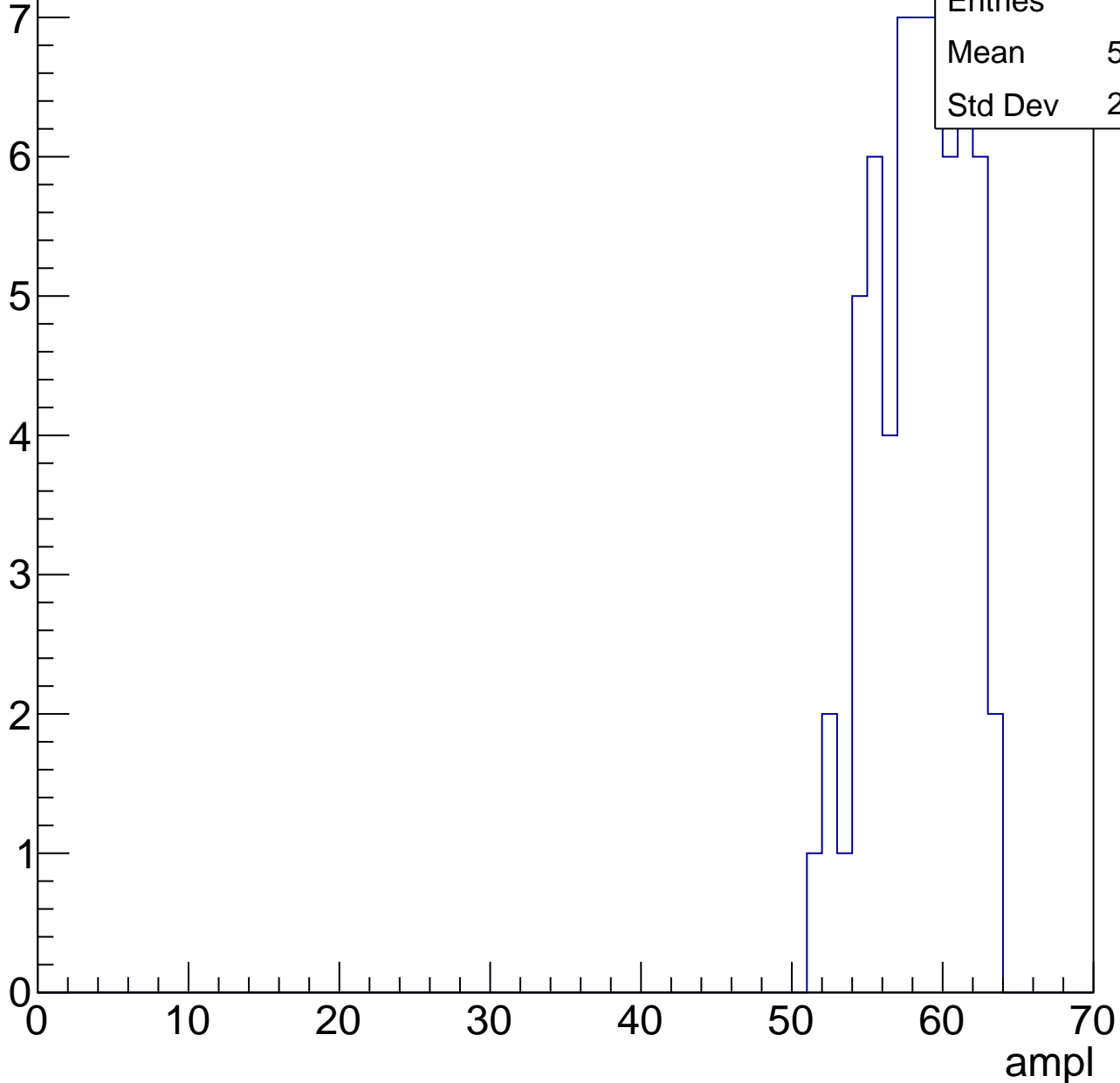


B1L100S, U11-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	57.95
Std Dev	2.983

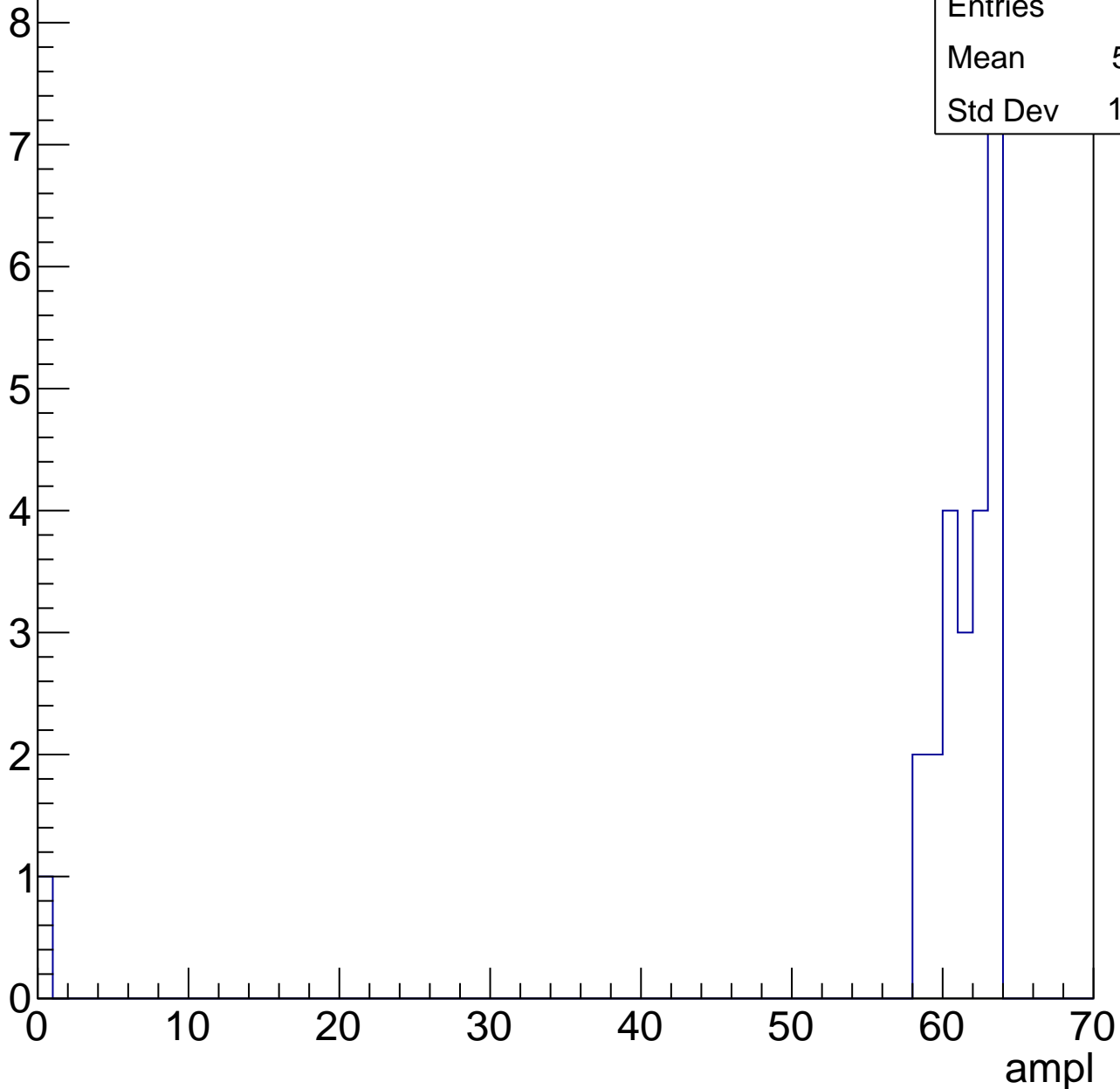


B1L100S, U11-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	58.71
Std Dev	12.35



B1L100S, U11-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

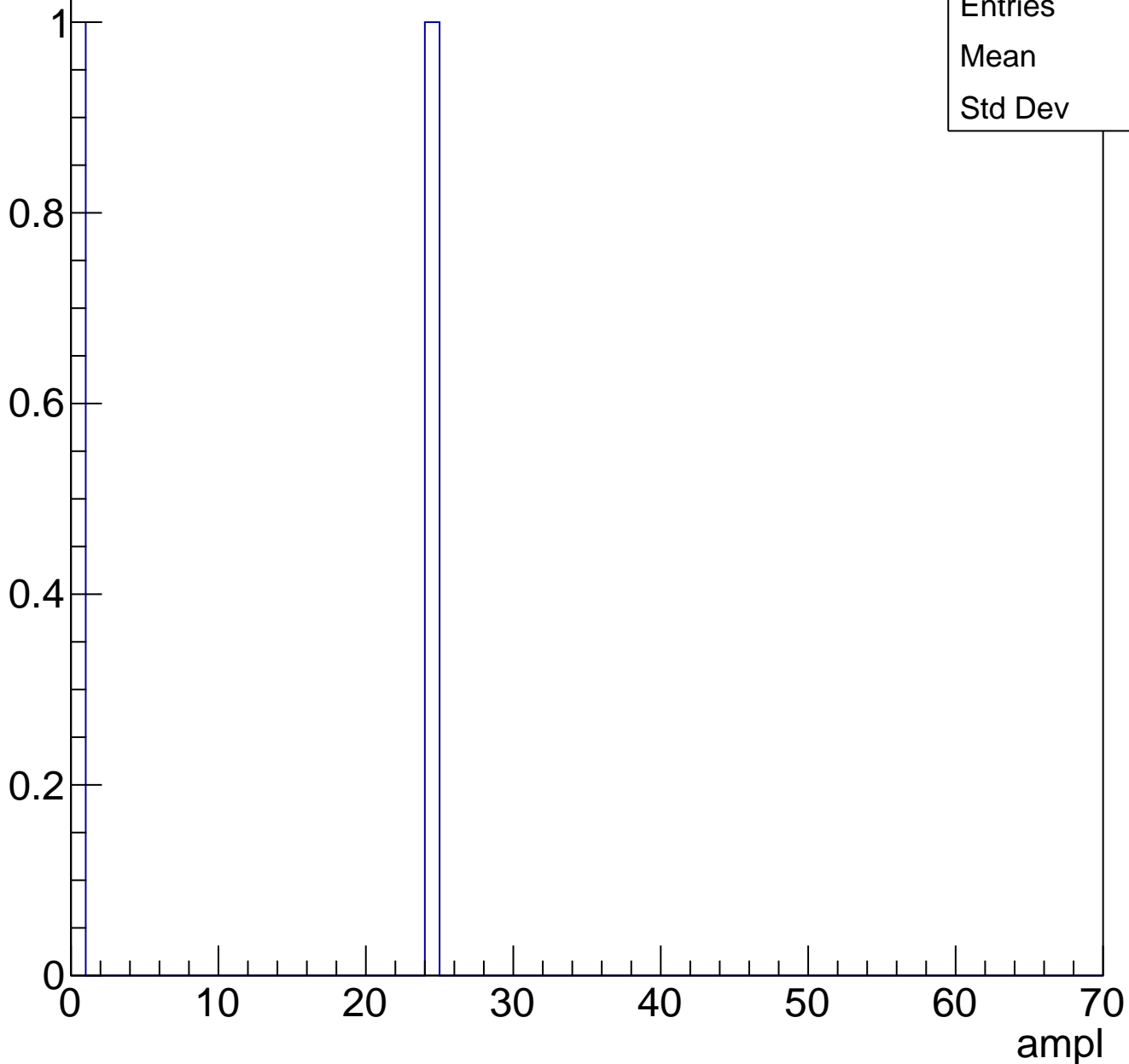
Entry



B1L100S, U11-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch14, adc0

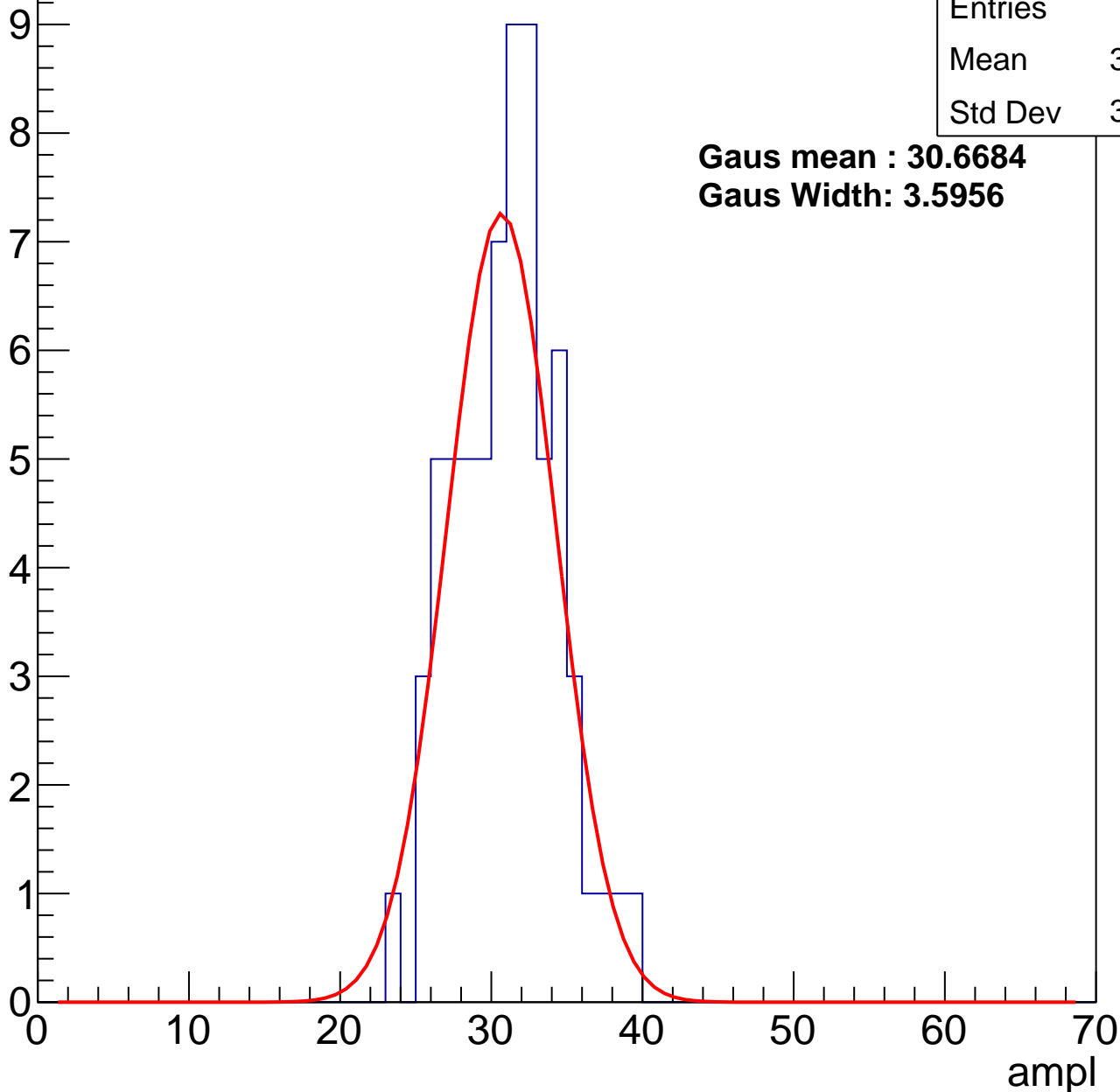
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.58
Std Dev	3.329

Gaus mean : 30.6684

Gaus Width: 3.5956



B1L100S, U11-ch14, adc1

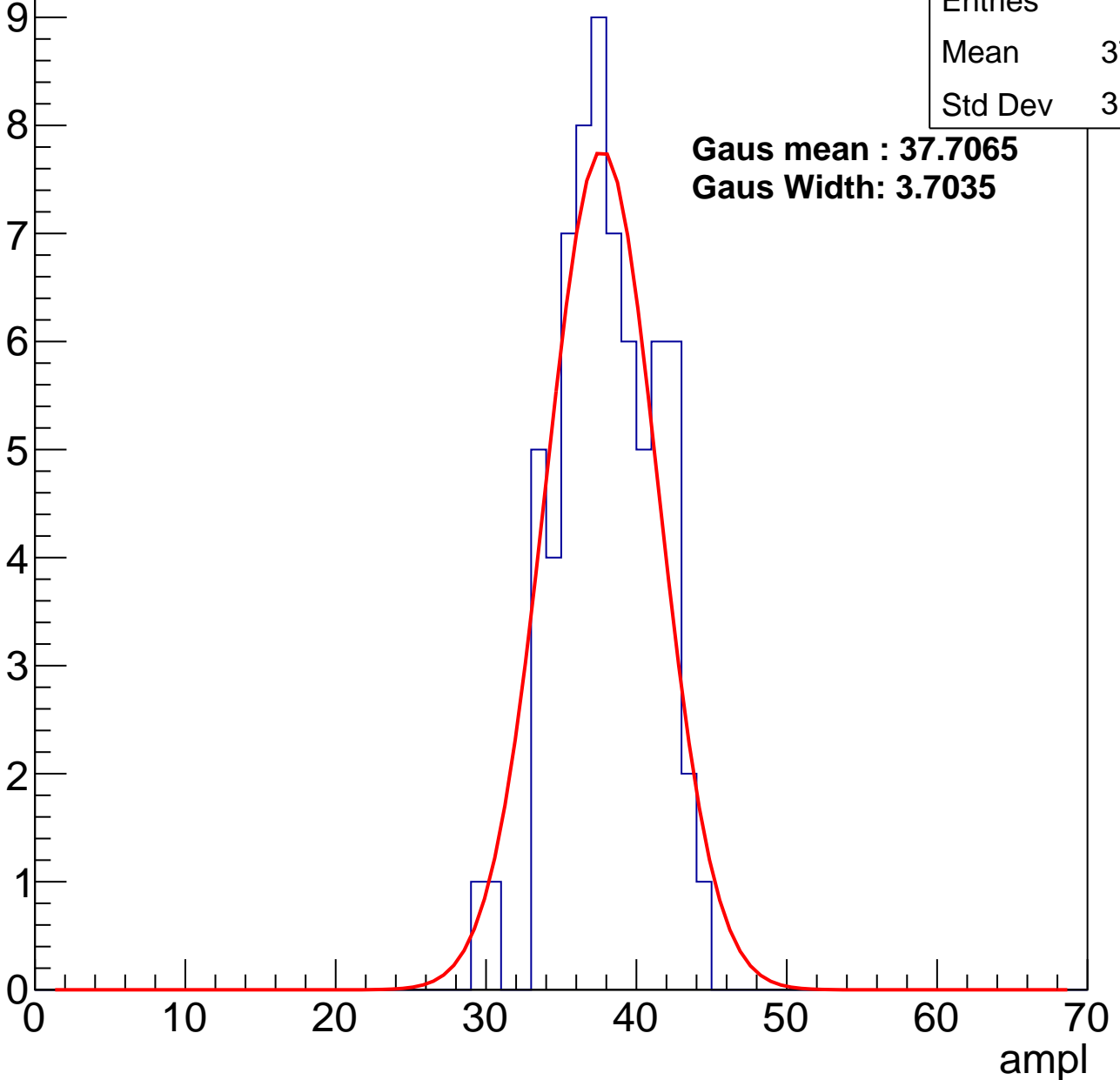
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.56
Std Dev	3.178

Gaus mean : 37.7065

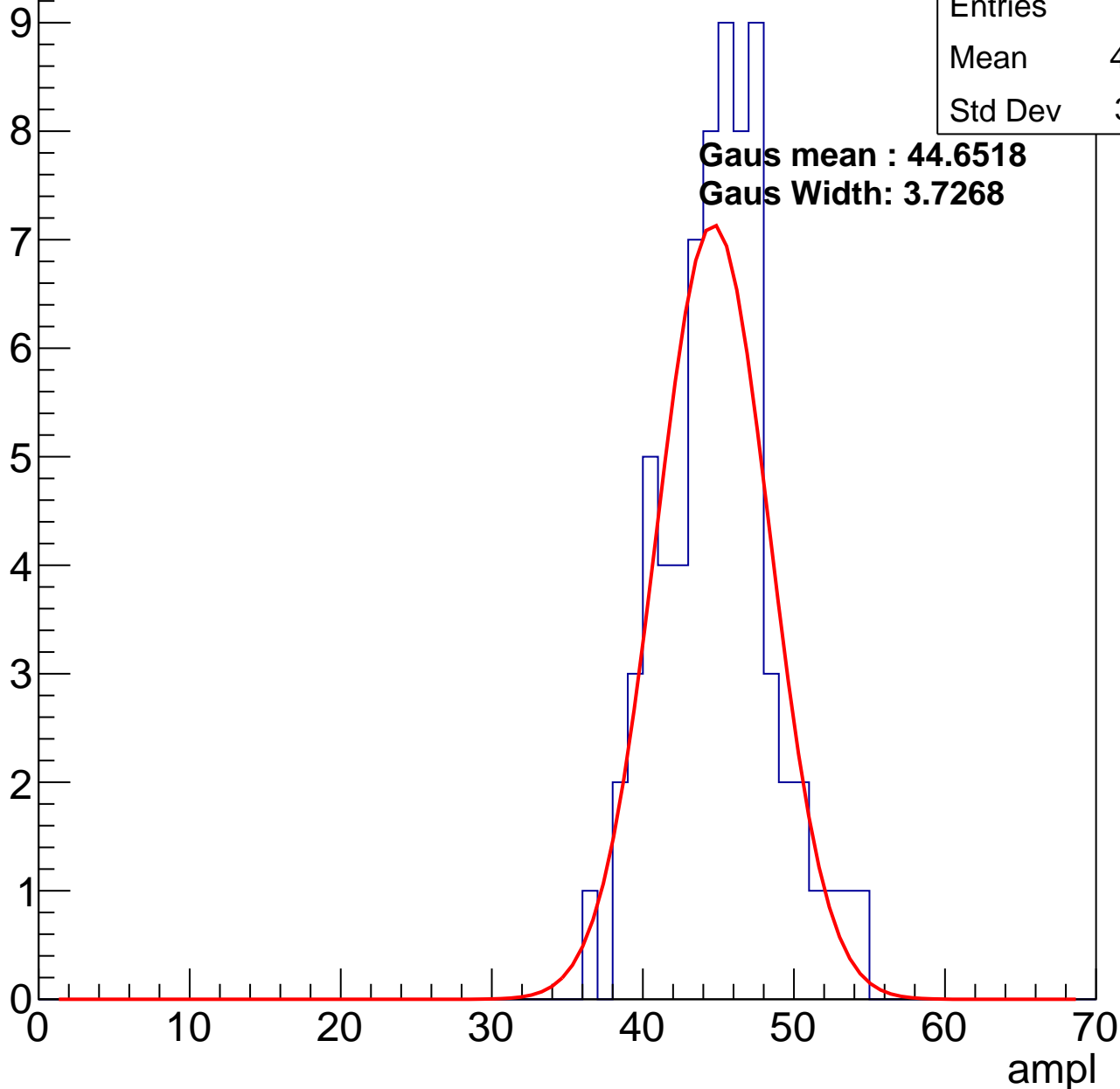
Gaus Width: 3.7035



B1L100S, U11-ch14, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



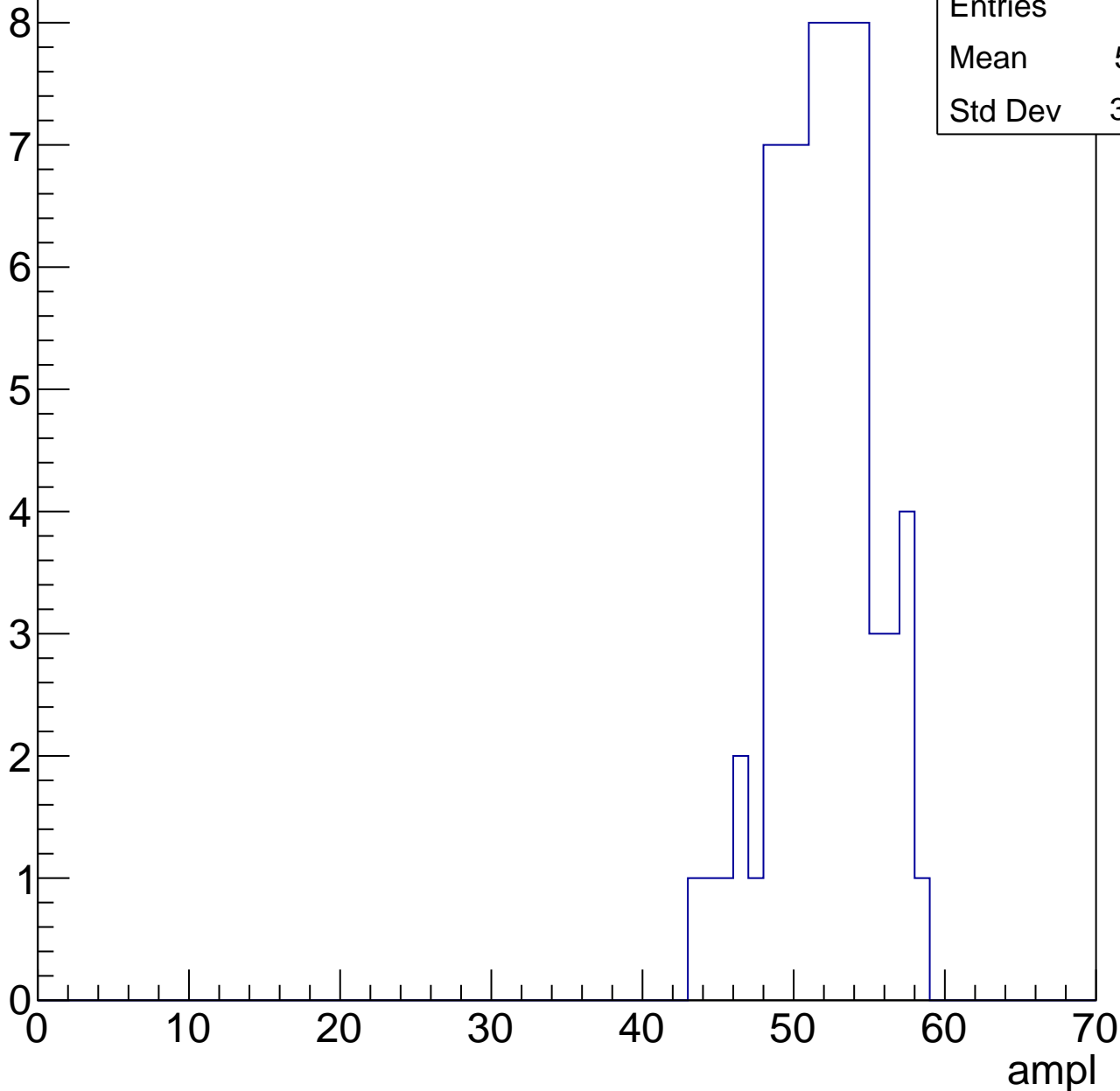
Entries	71
Mean	44.54
Std Dev	3.591

B1L100S, U11-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	51.41
Std Dev	3.227

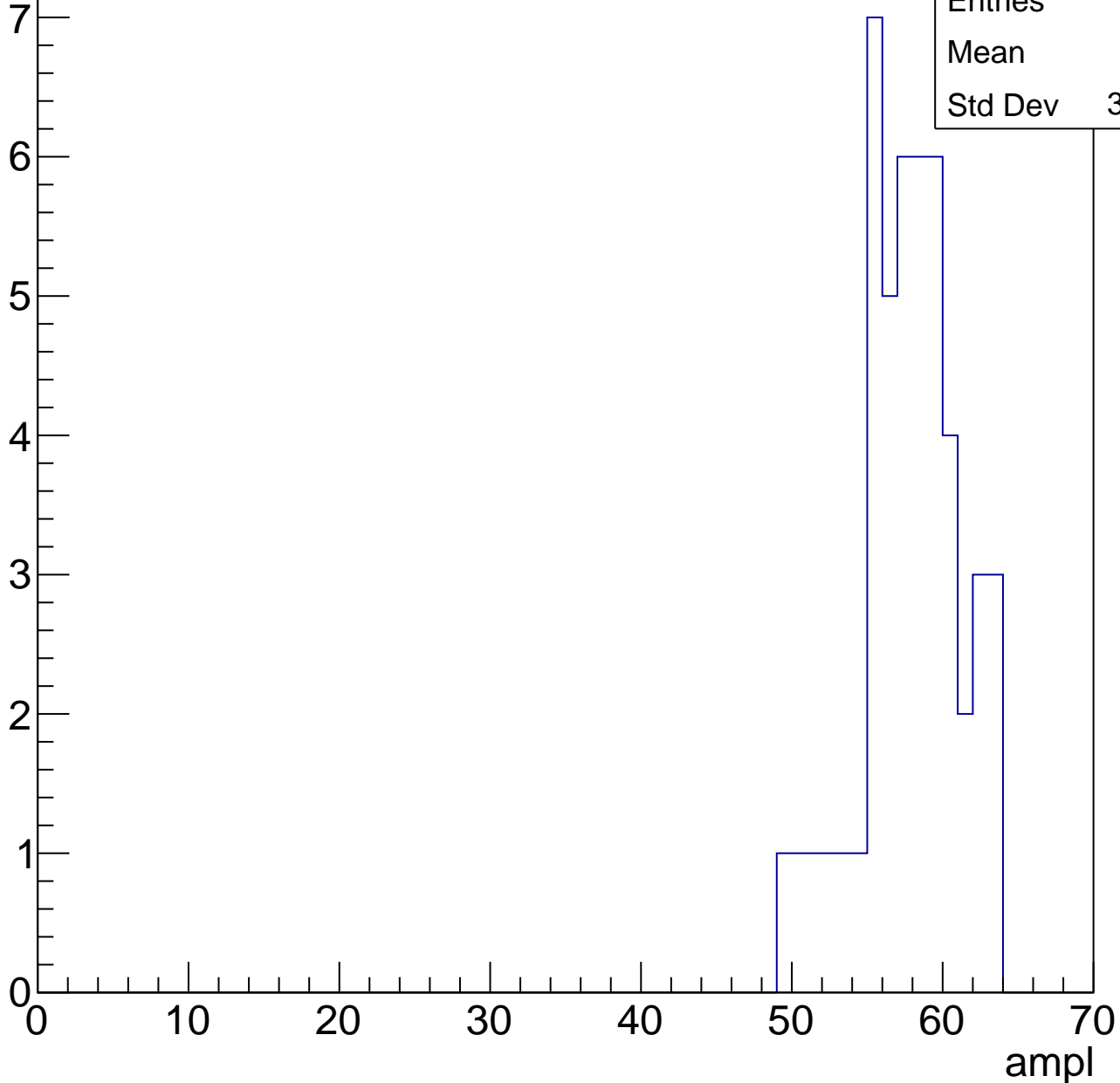


B1L100S, U11-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	57.4
Std Dev	3.245

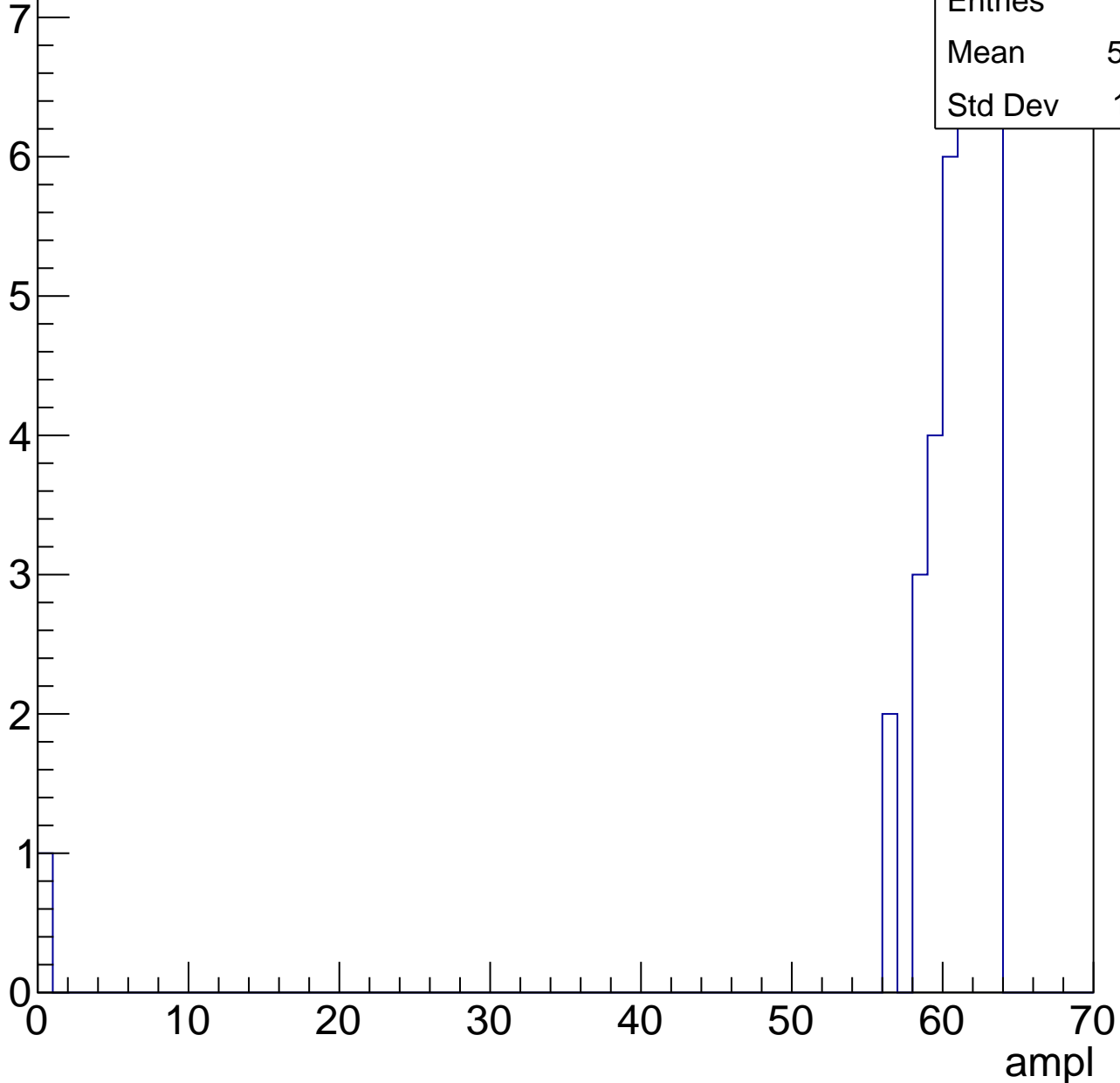


B1L100S, U11-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	59.03
Std Dev	10.01



B1L100S, U11-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch15, adc0

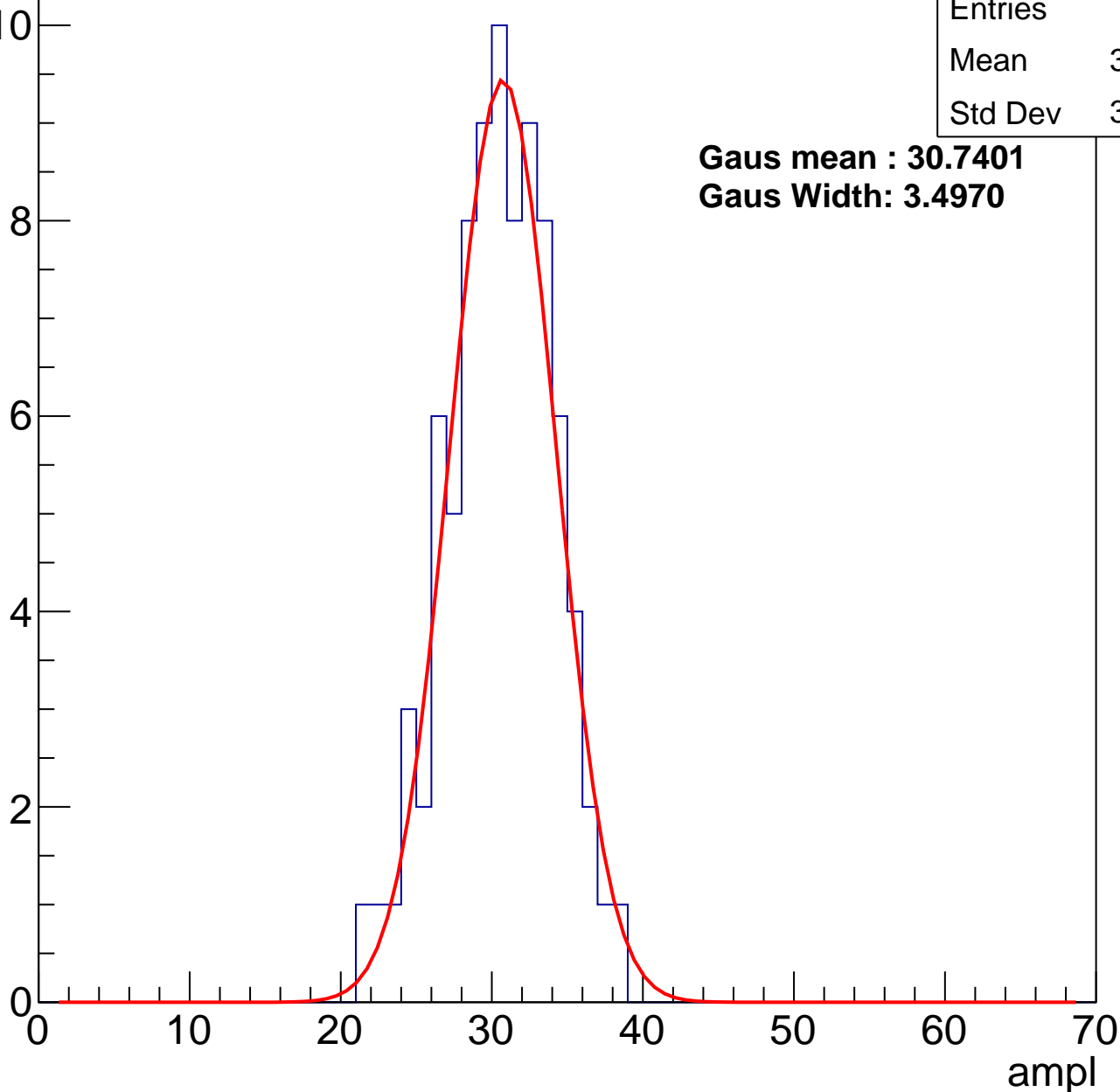
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	30.06
Std Dev	3.482

Gaus mean : 30.7401

Gaus Width: 3.4970



B1L100S, U11-ch15, adc1

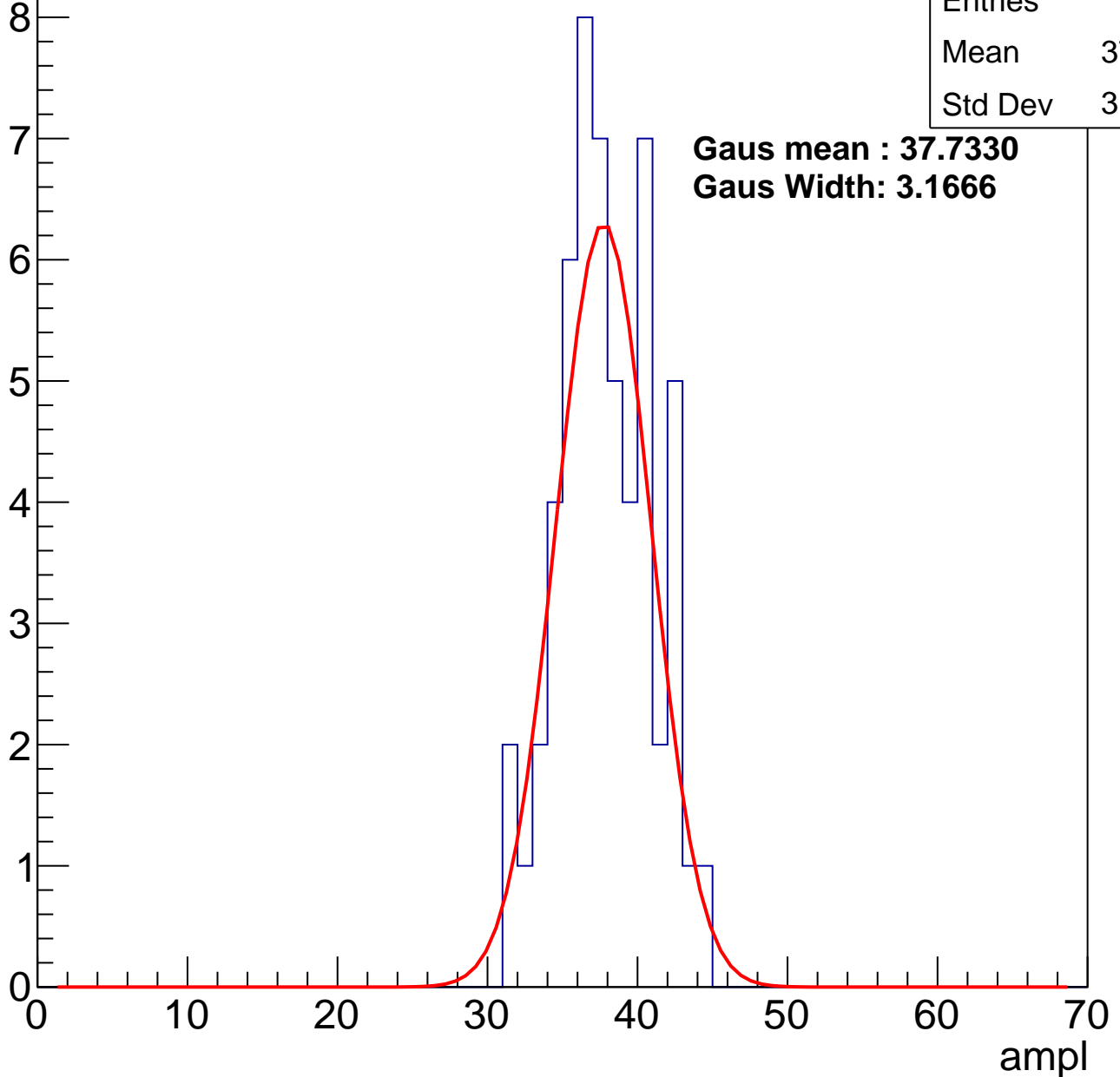
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	37.42
Std Dev	3.055

Gaus mean : 37.7330

Gaus Width: 3.1666



B1L100S, U11-ch15, adc2

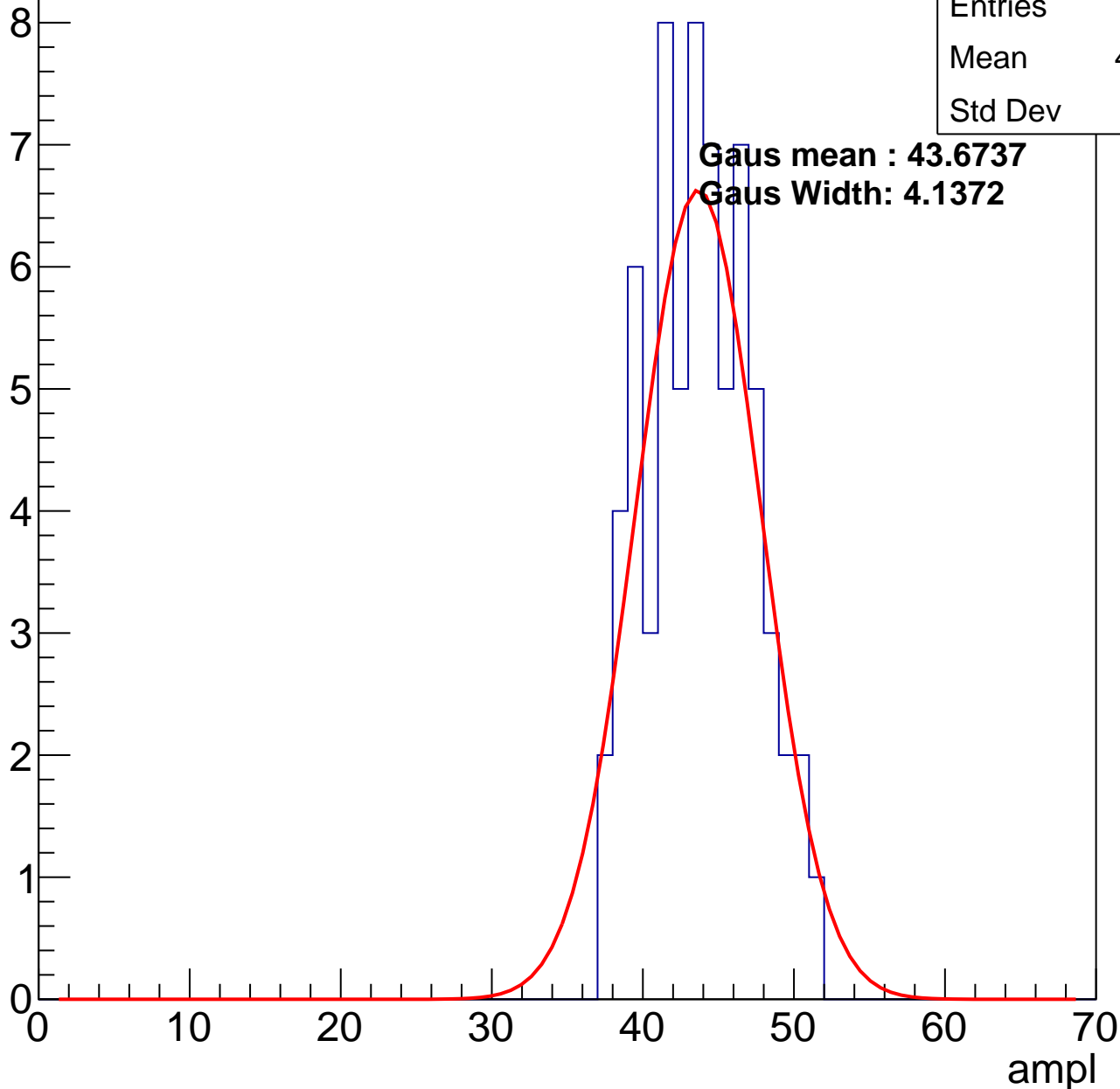
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.31
Std Dev	3.44

Gaus mean : 43.6737

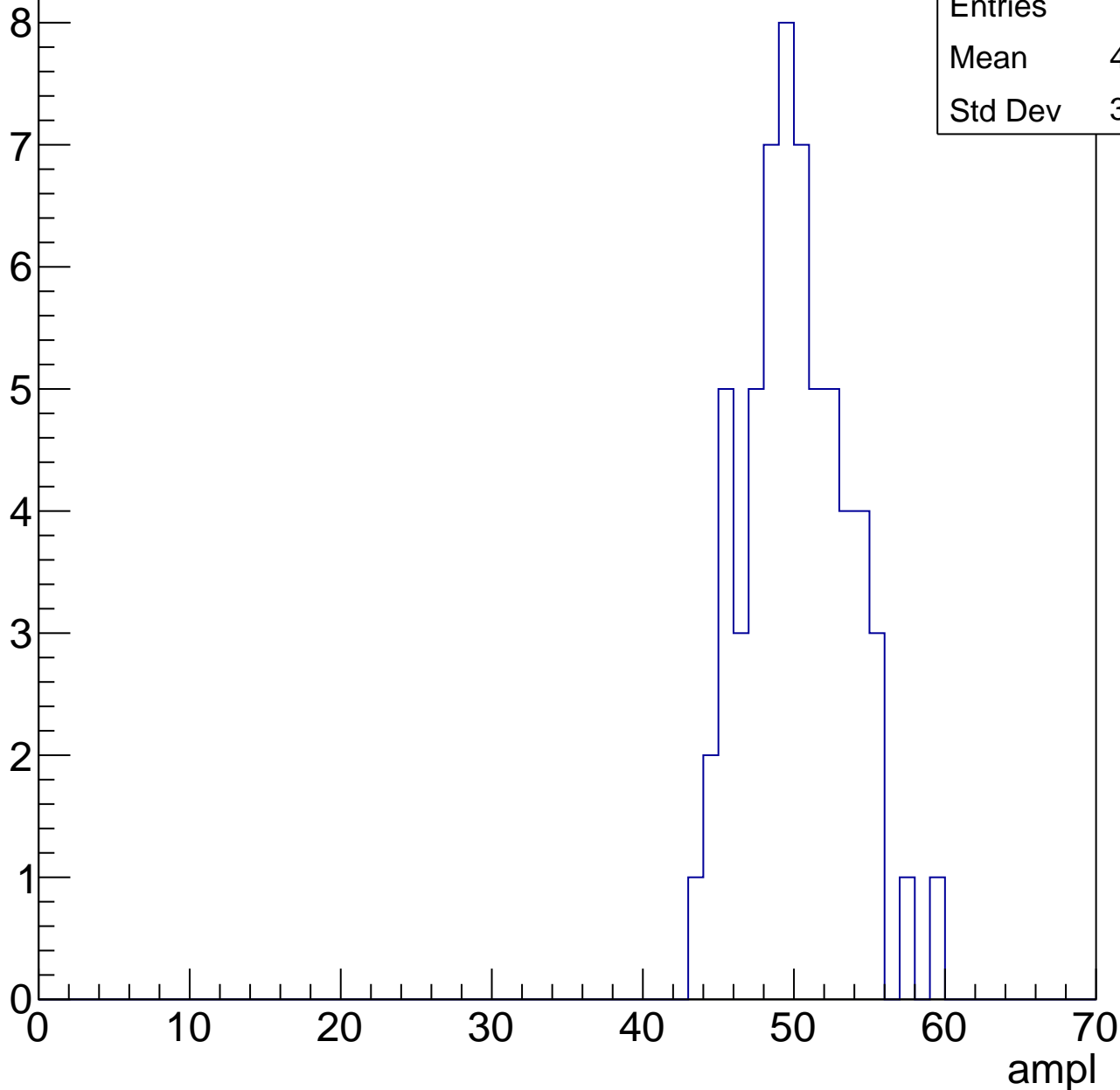
Gaus Width: 4.1372



B1L100S, U11-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



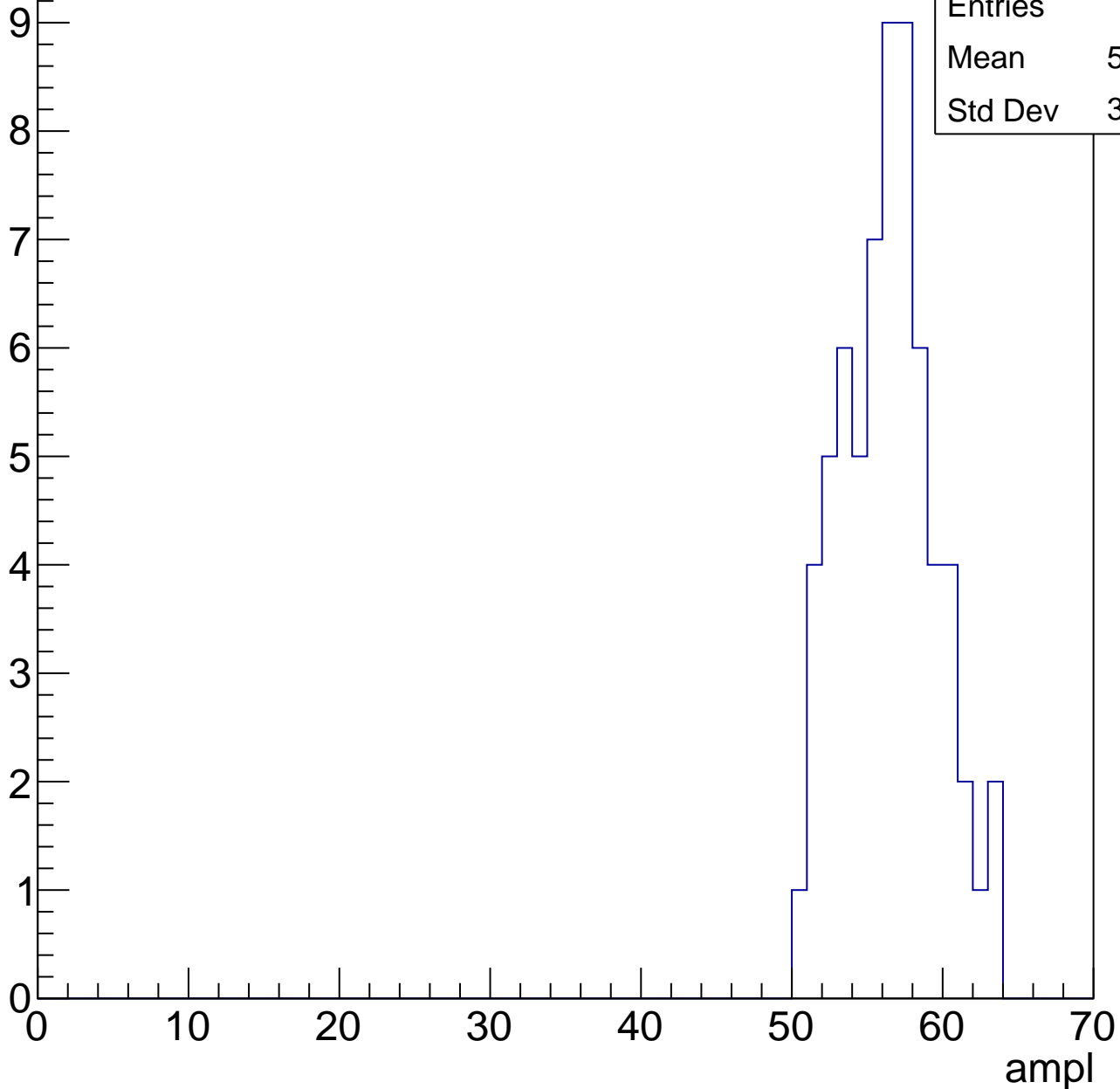
Entries	61
Mean	49.69
Std Dev	3.385

B1L100S, U11-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	55.97
Std Dev	3.073

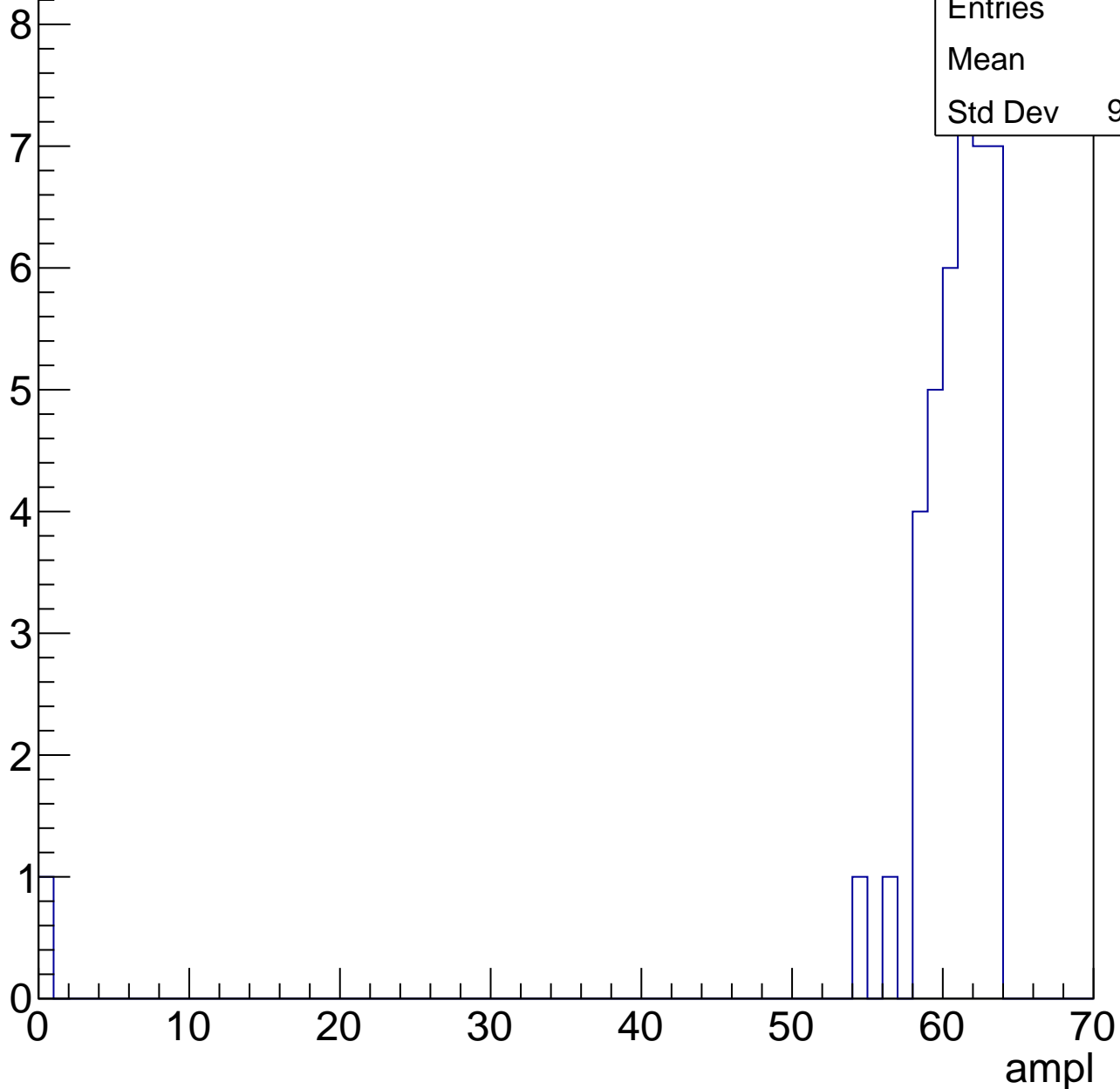


B1L100S, U11-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	59
Std Dev	9.659



B1L100S, U11-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

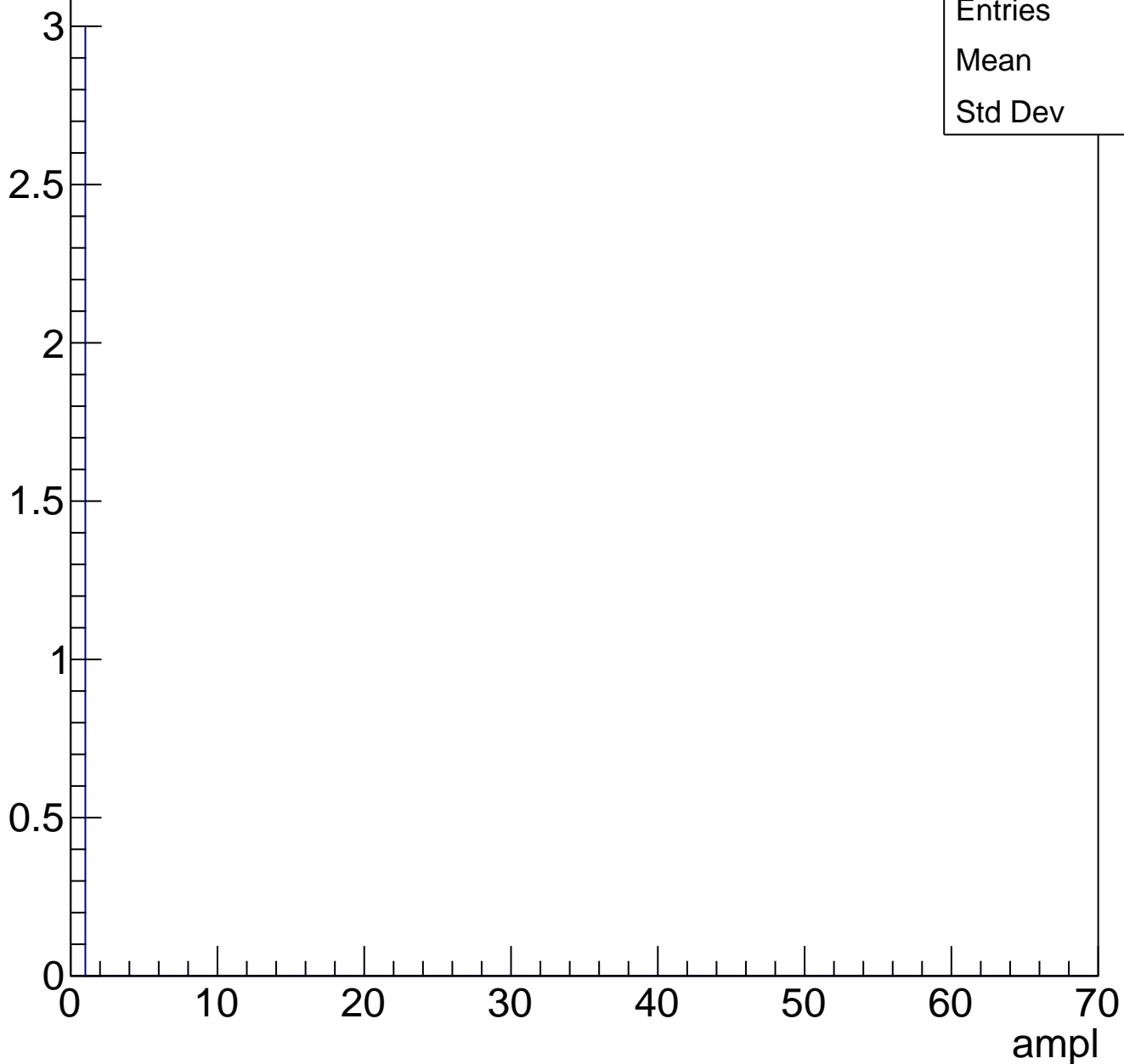
Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L100S, U11-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U11-ch16, adc0

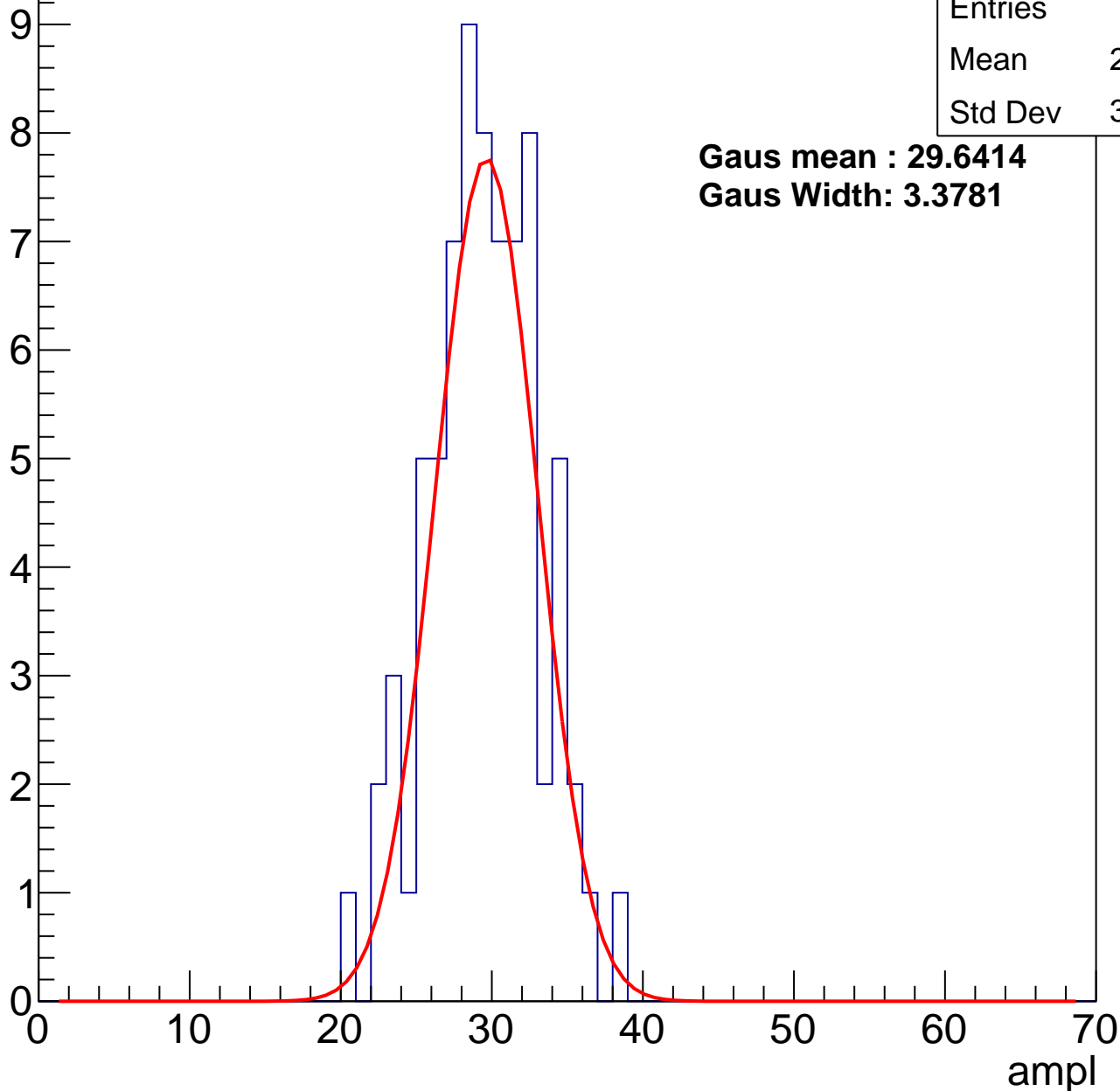
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.03
Std Dev	3.564

Gaus mean : 29.6414

Gaus Width: 3.3781



B1L100S, U11-ch16, adc1

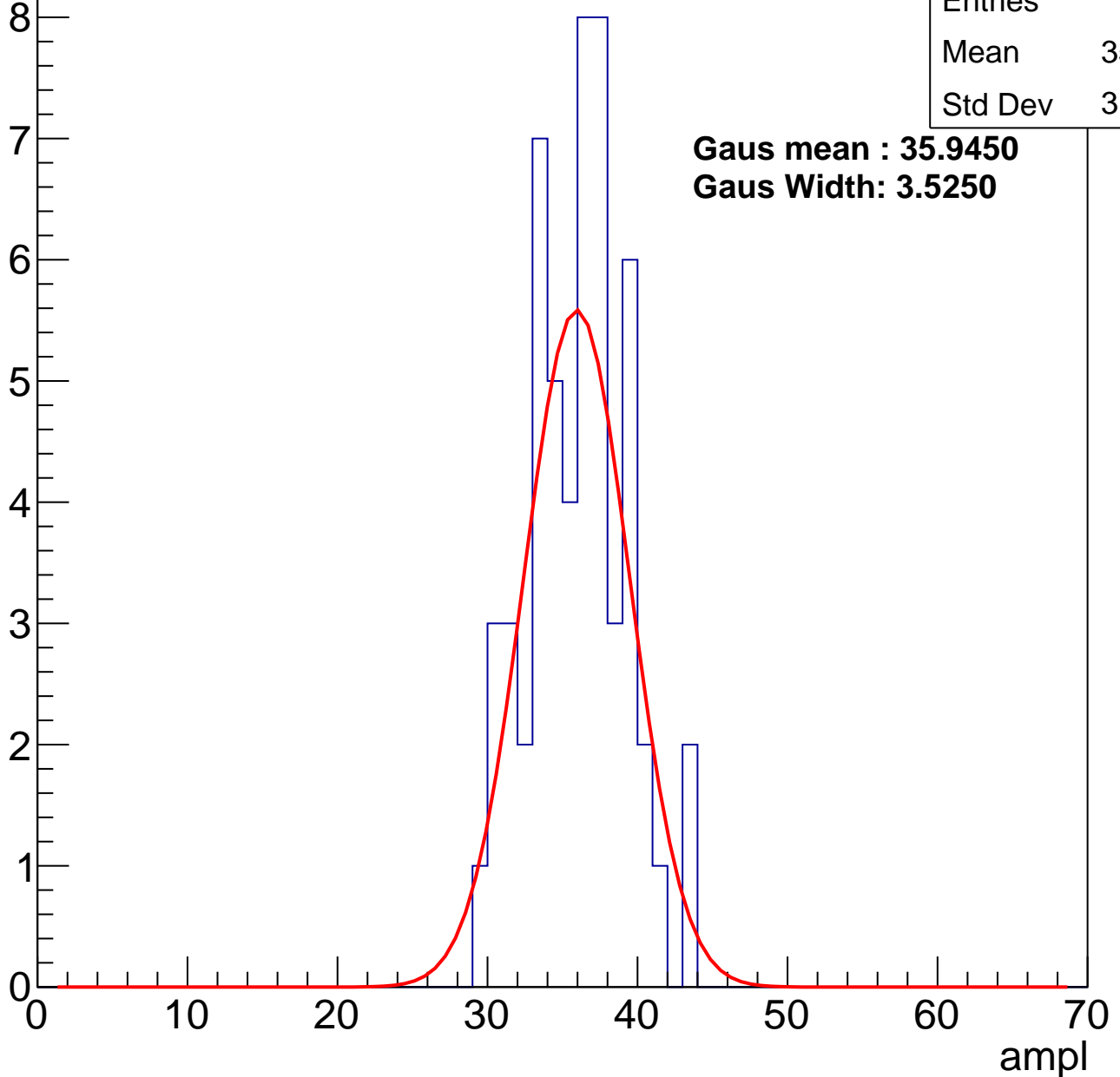
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	35.56
Std Dev	3.218

Gaus mean : 35.9450

Gaus Width: 3.5250



B1L100S, U11-ch16, adc2

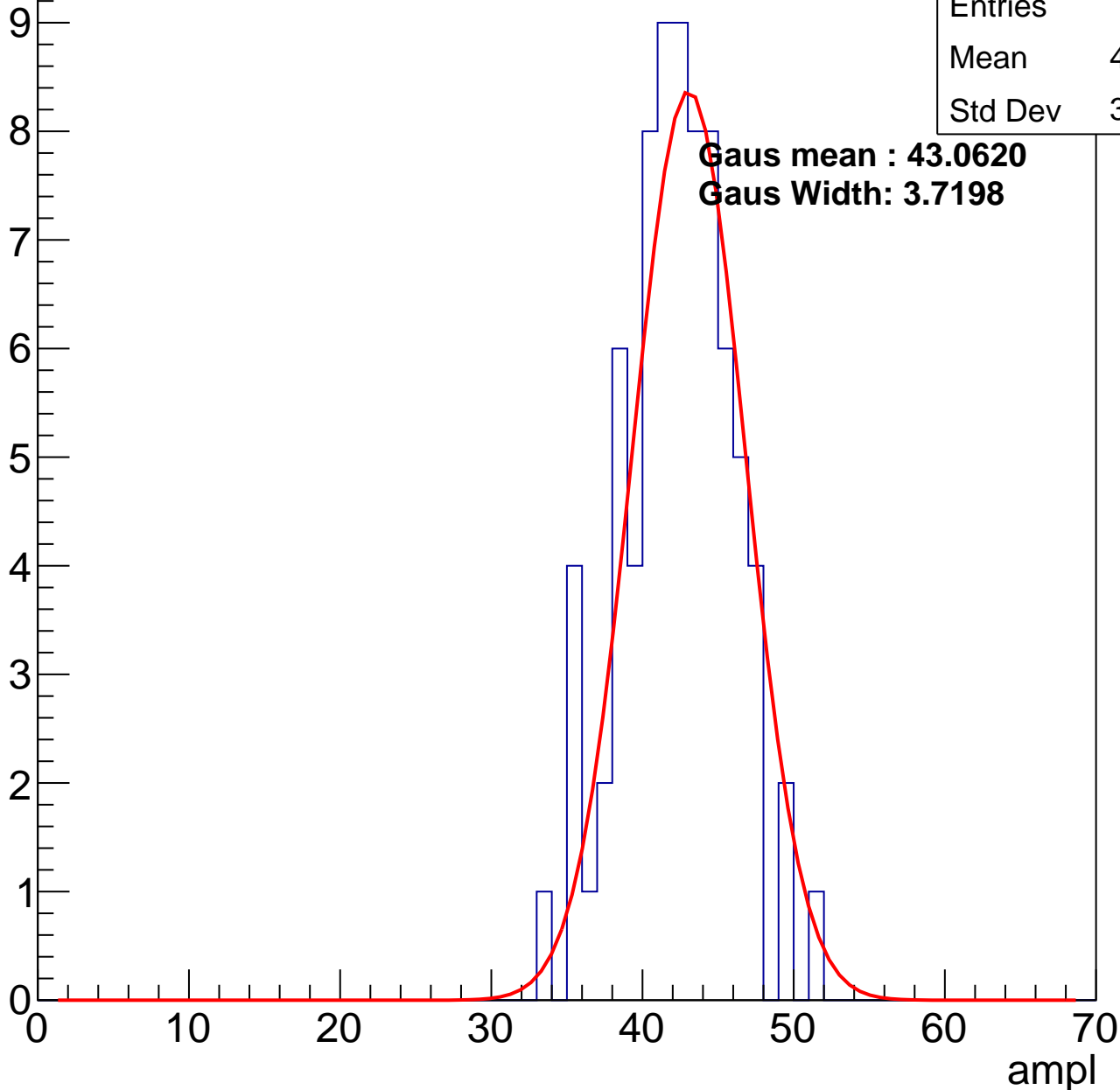
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	41.88
Std Dev	3.559

Gaus mean : 43.0620

Gaus Width: 3.7198



B1L100S, U11-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

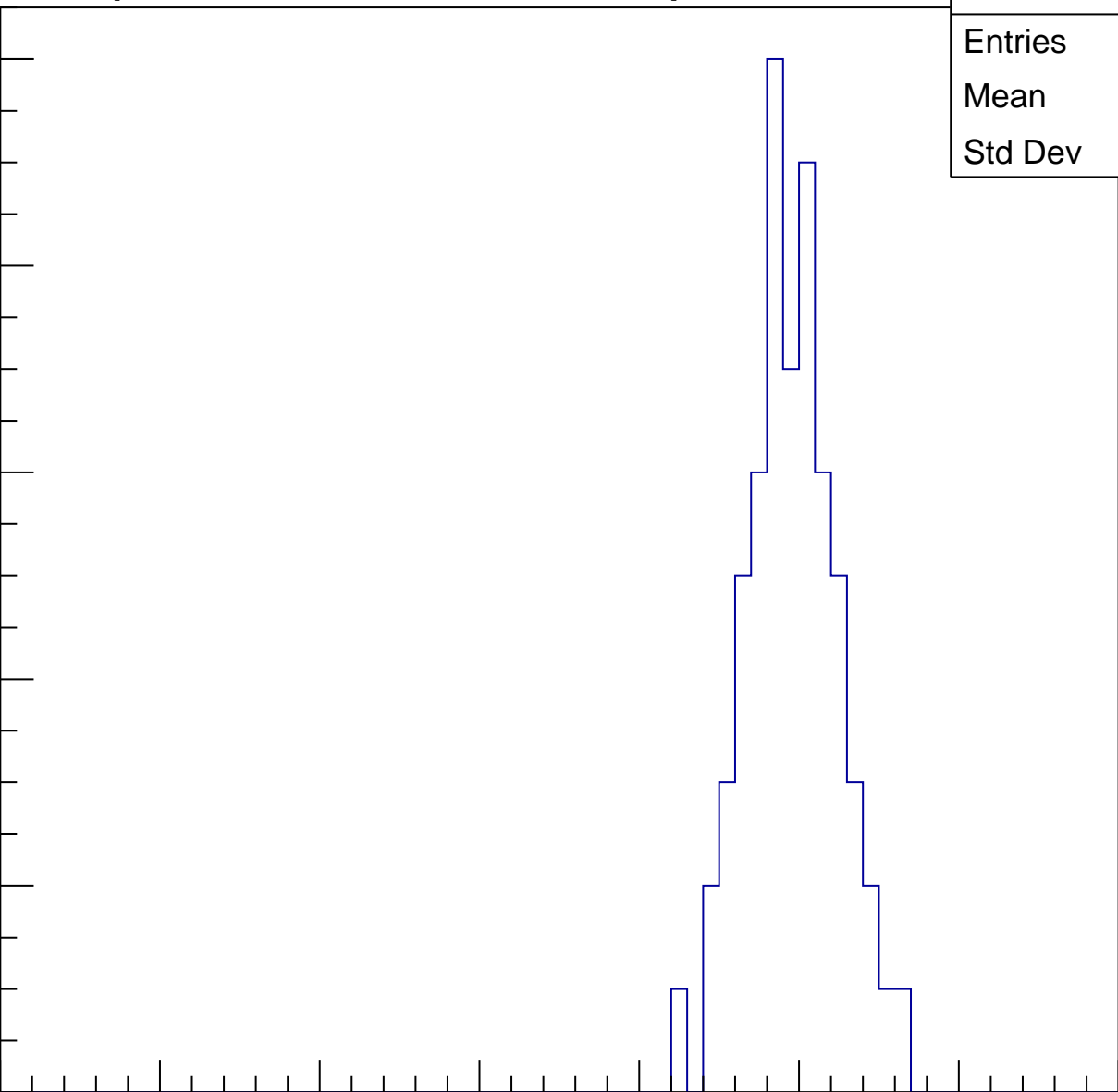
Entries	61
Mean	49.08
Std Dev	2.824

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

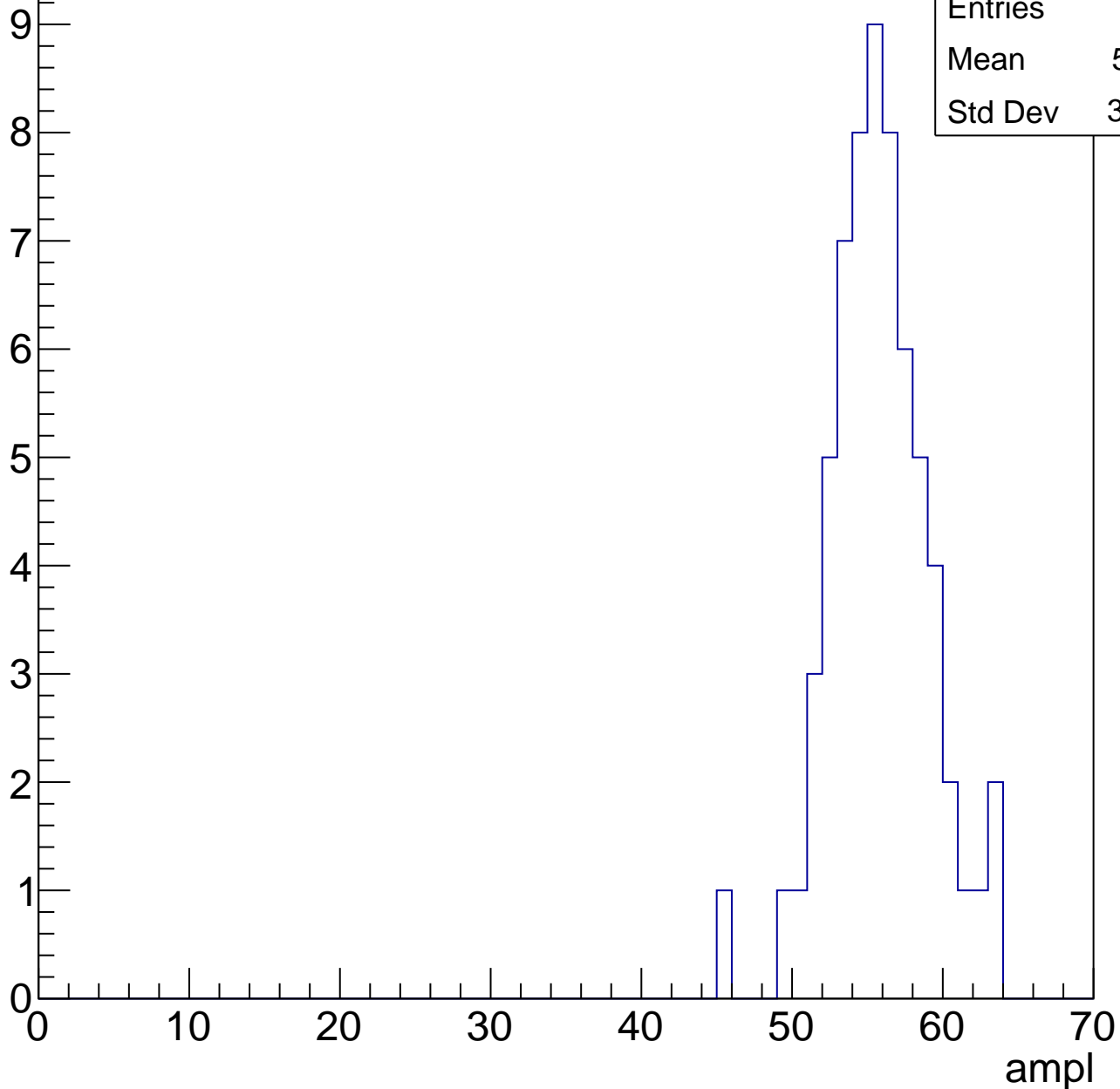
ampl



B1L100S, U11-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

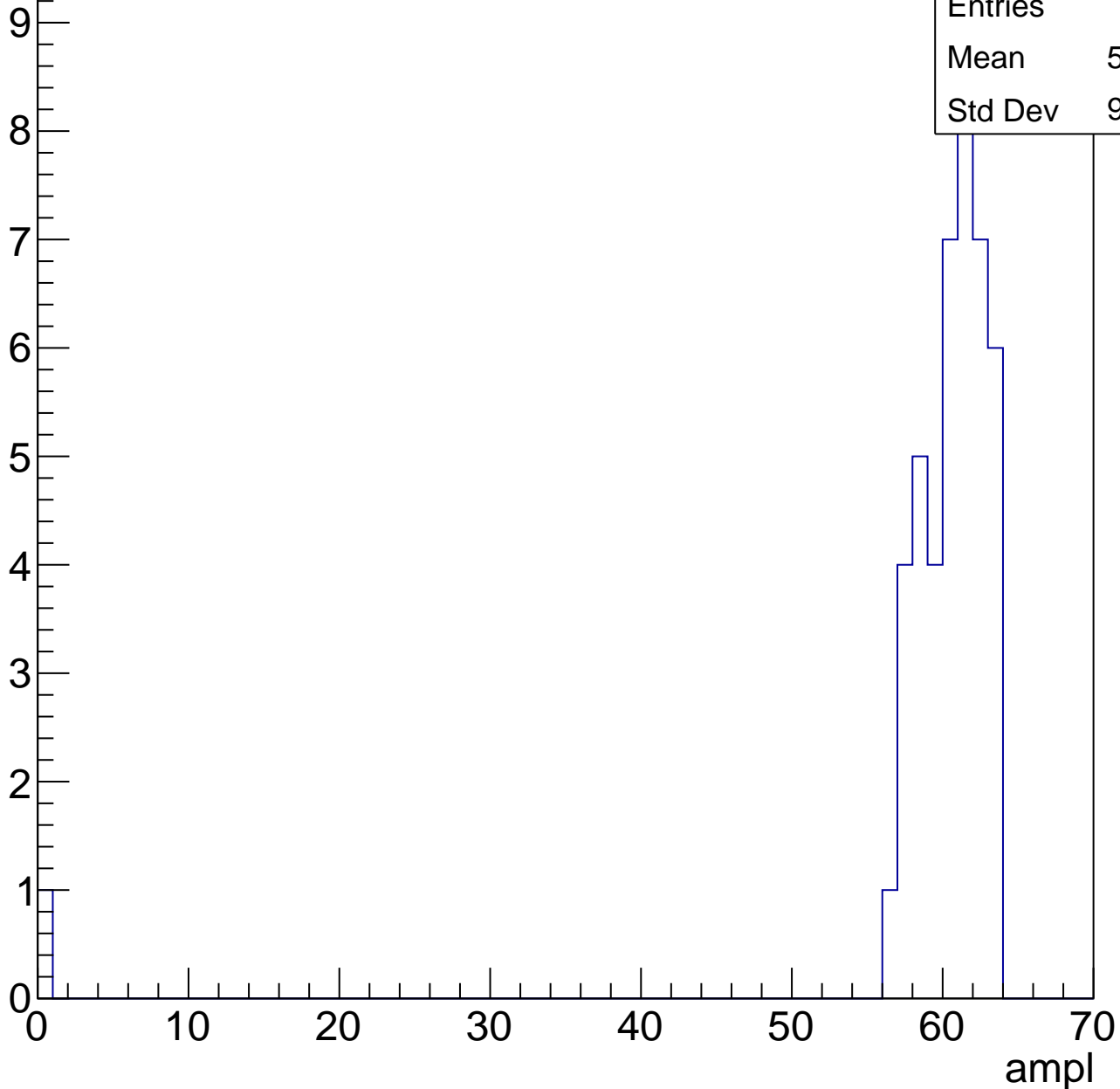


B1L100S, U11-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	58.89
Std Dev	9.186



B1L100S, U11-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

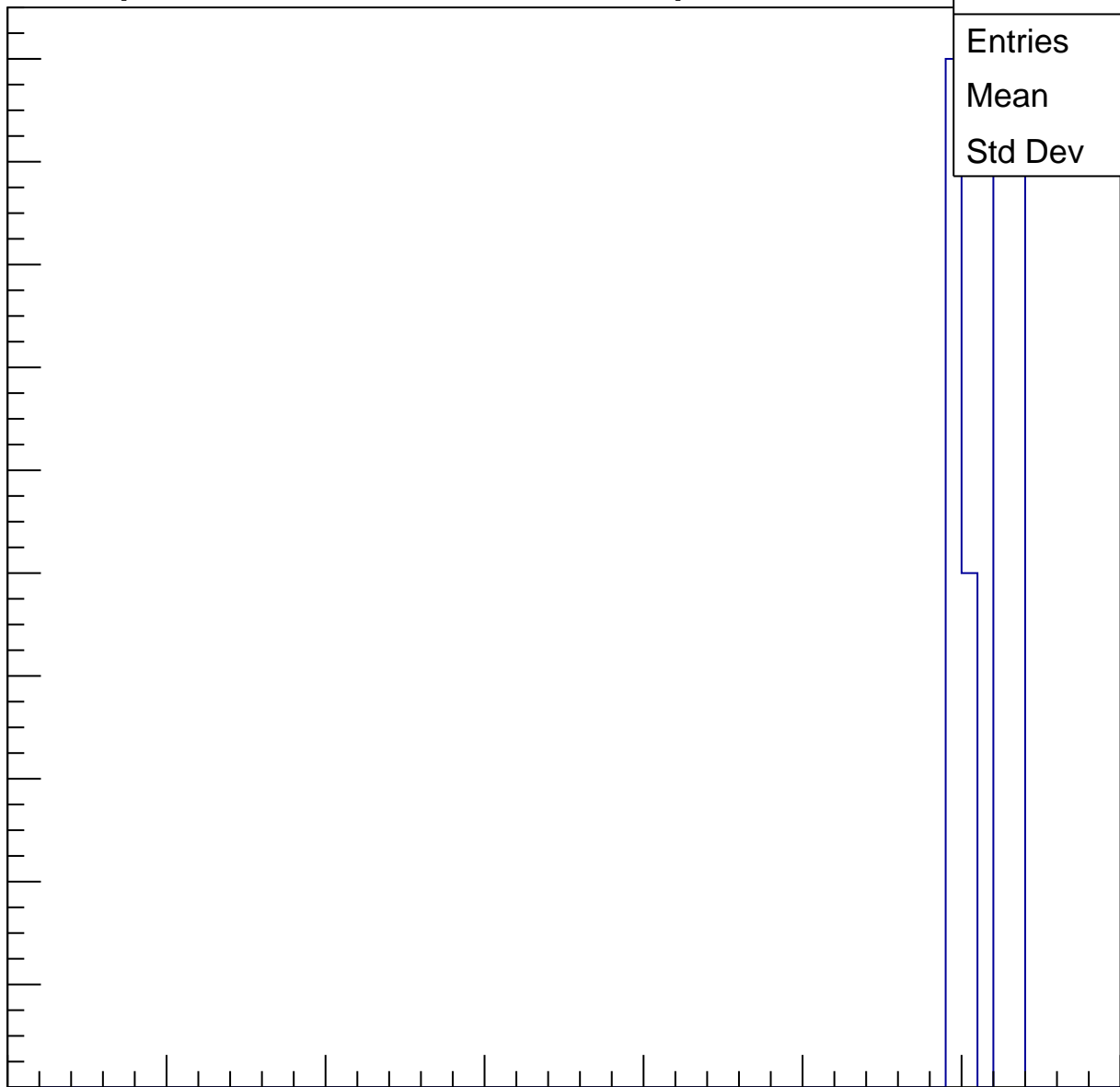
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.14
Std Dev	1.641

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch17, adc0

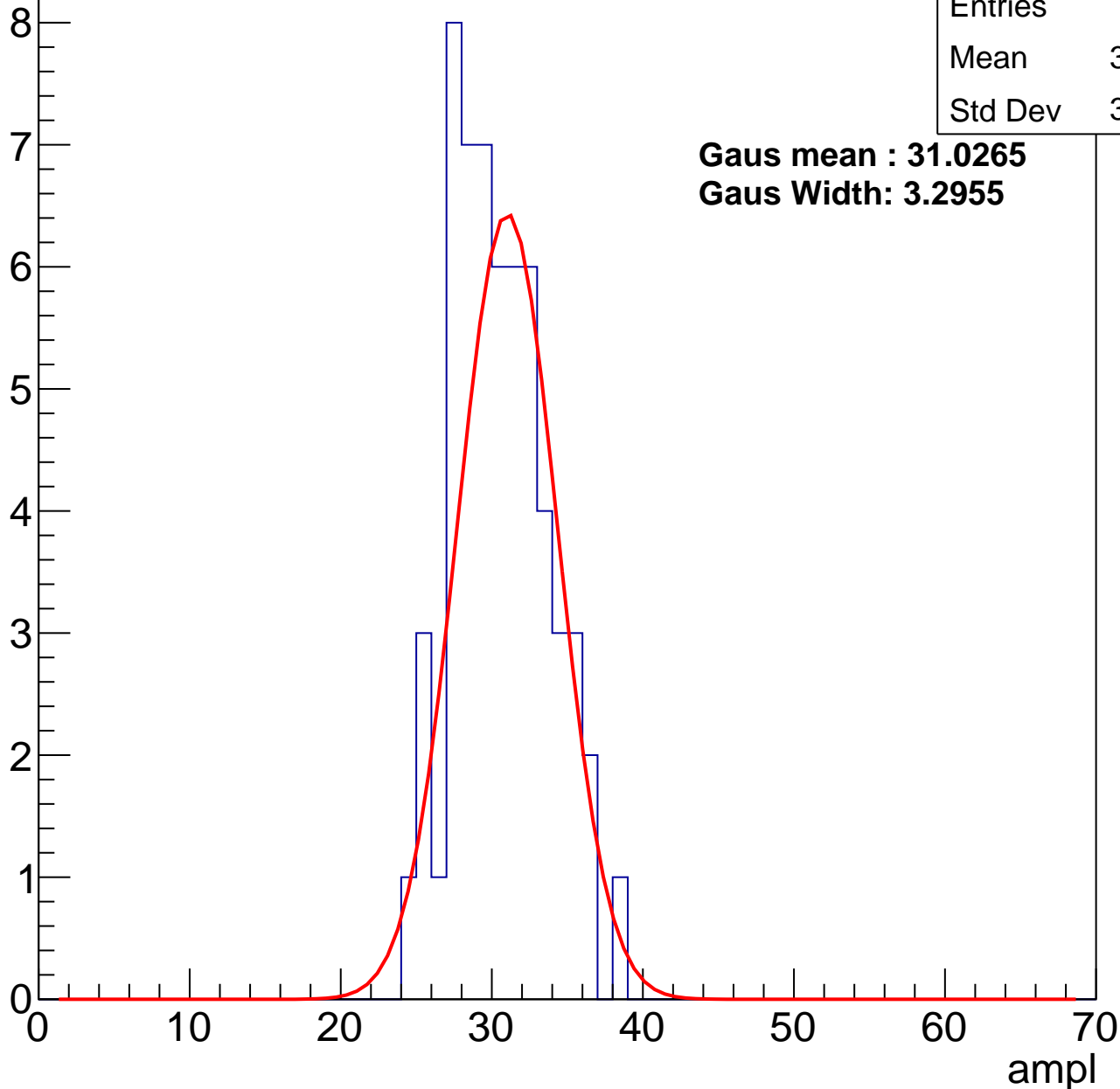
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	30.12
Std Dev	3.102

Gaus mean : 31.0265

Gaus Width: 3.2955



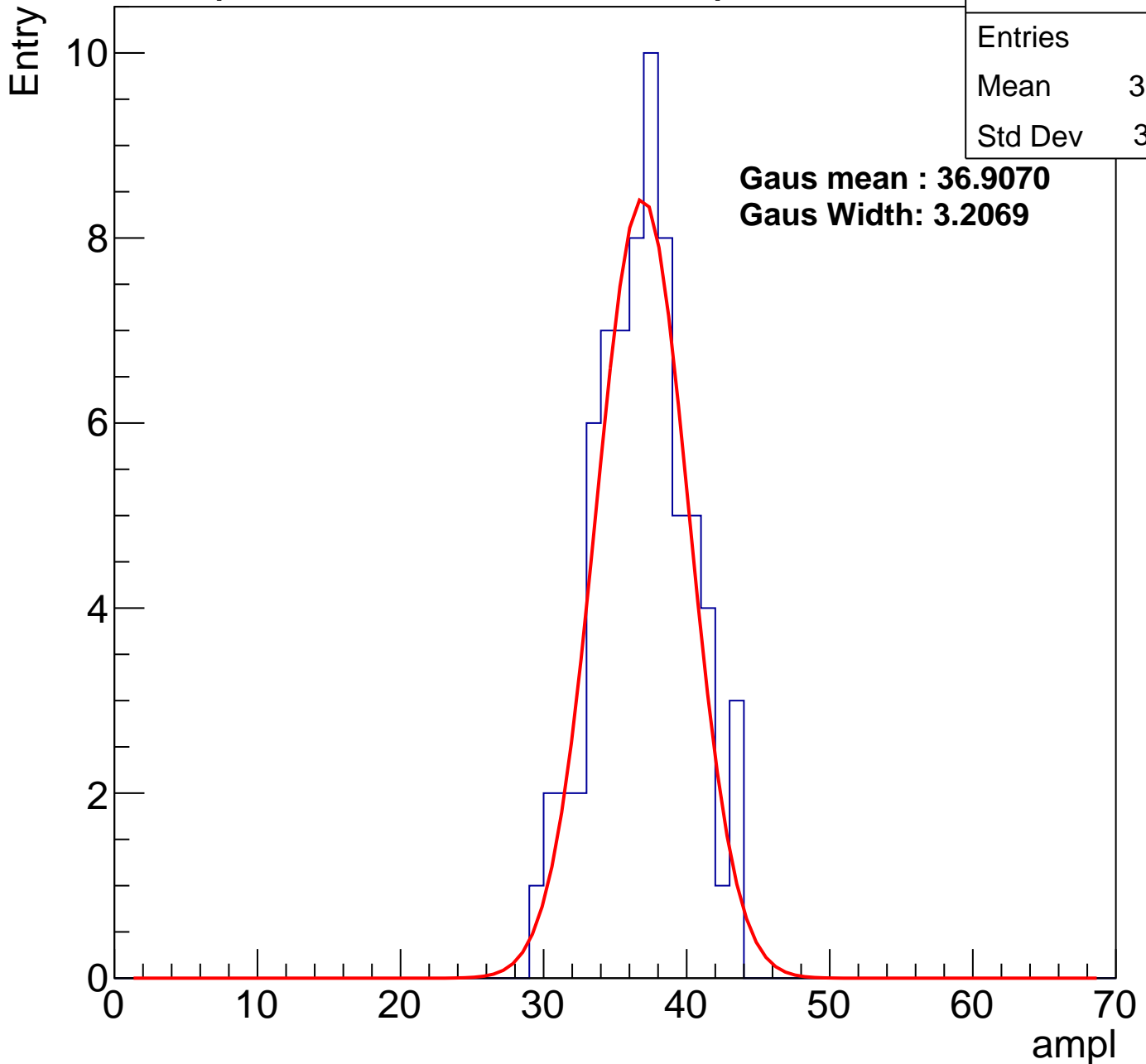
B1L100S, U11-ch17, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	36.45
Std Dev	3.201

Gaus mean : 36.9070

Gaus Width: 3.2069



B1L100S, U11-ch17, adc2

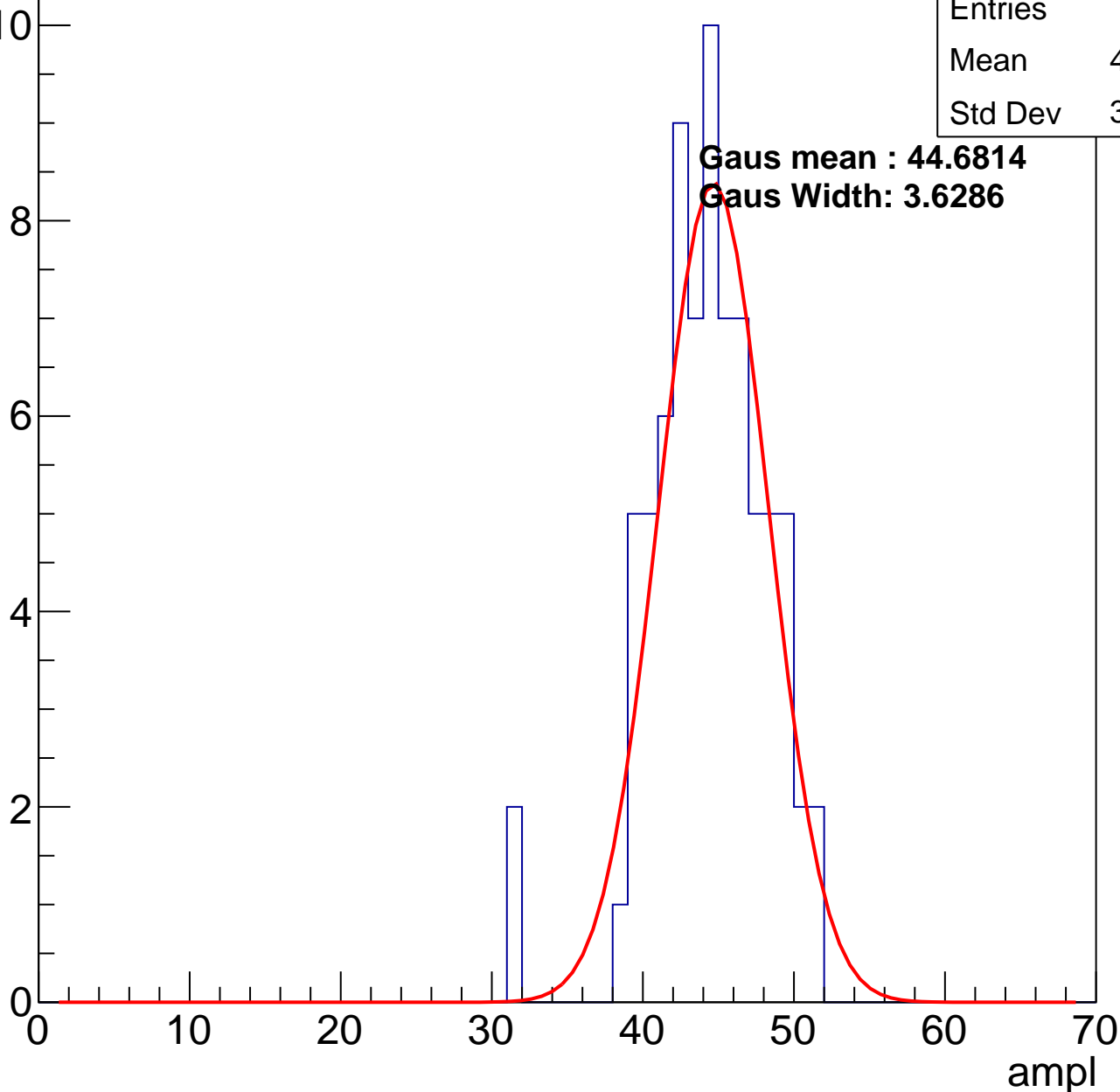
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	43.83
Std Dev	3.804

Gaus mean : 44.6814

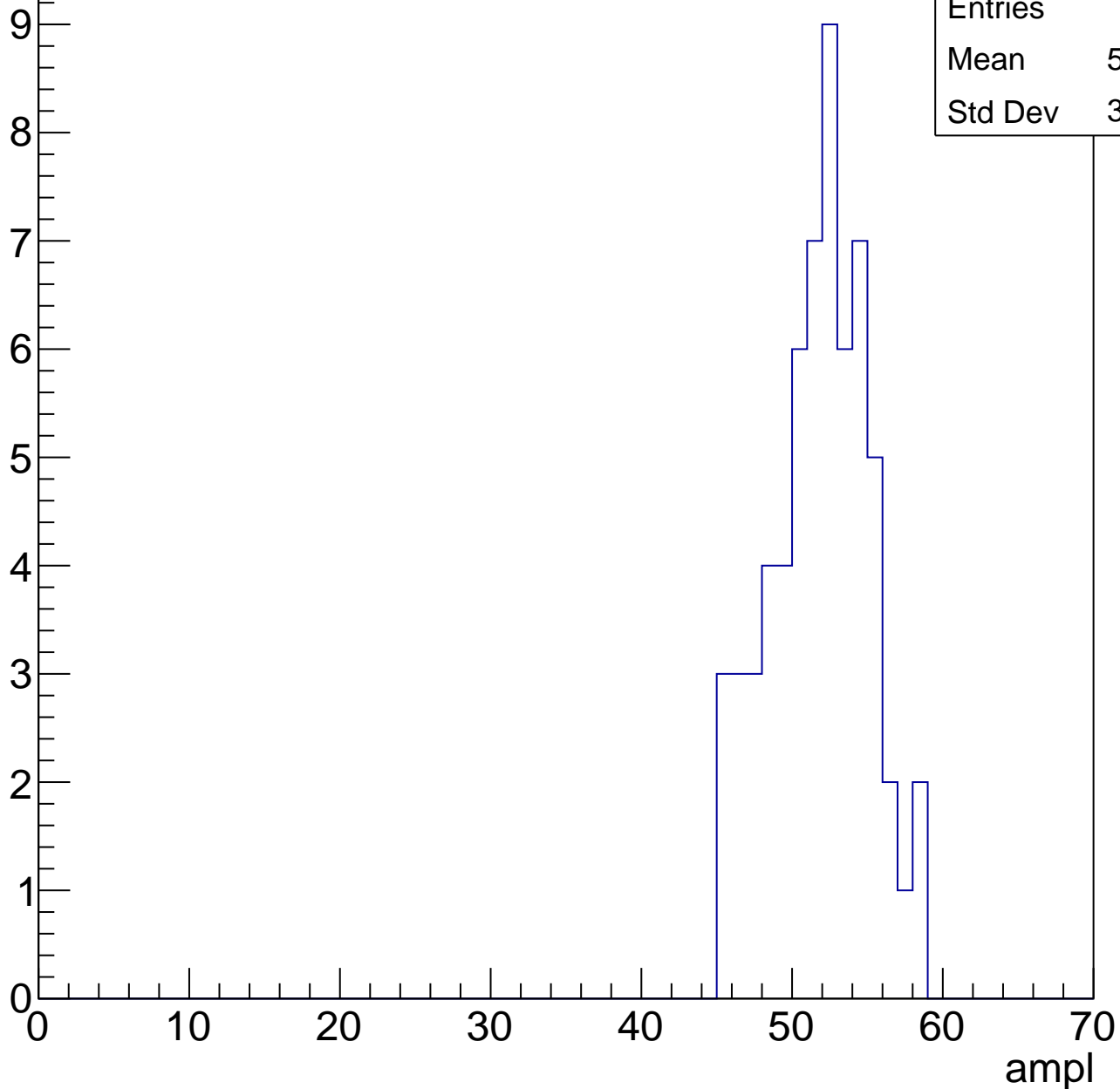
Gaus Width: 3.6286



B1L100S, U11-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

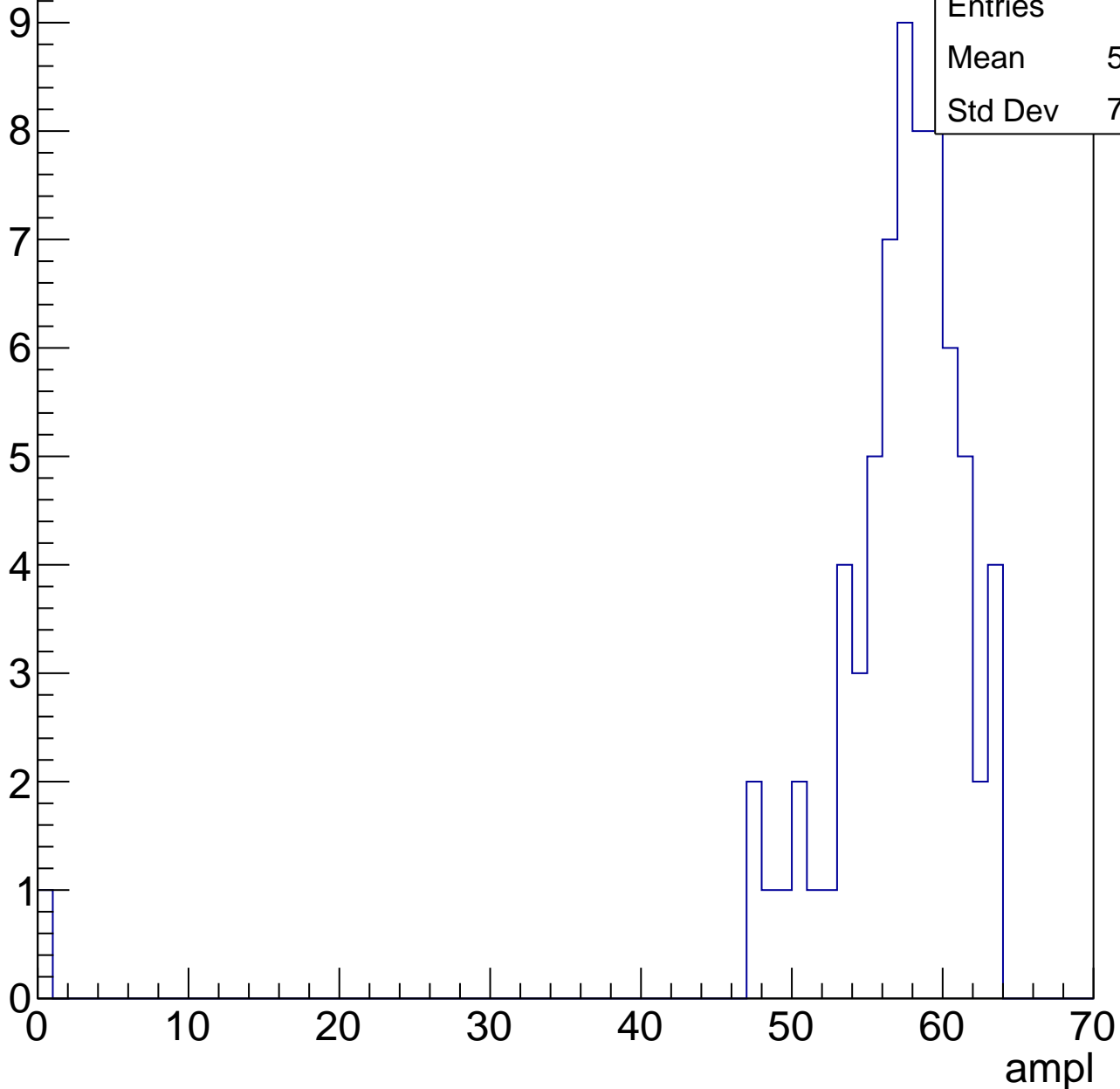


B1L100S, U11-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	56.07
Std Dev	7.726

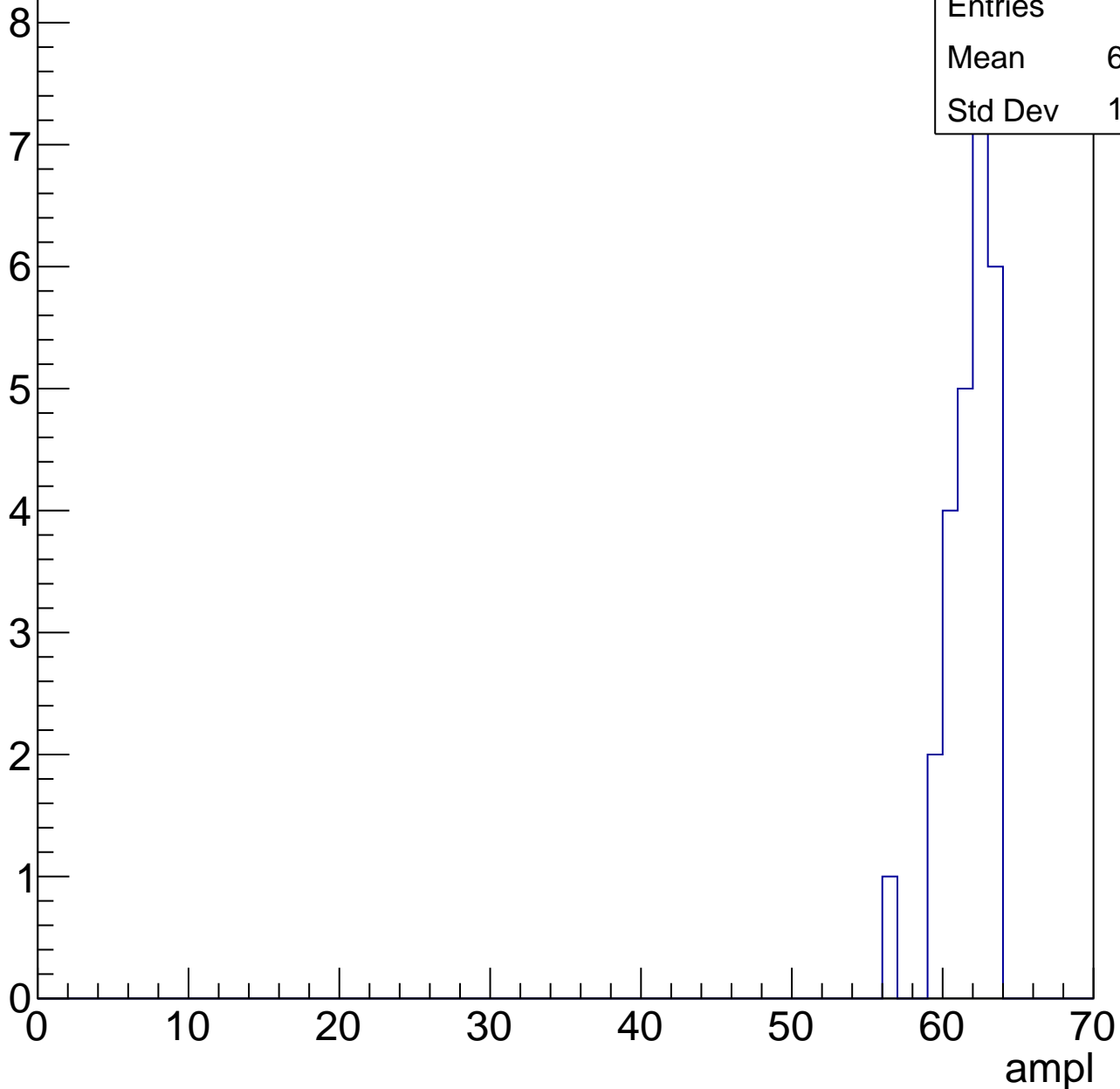


B1L100S, U11-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	61.27
Std Dev	1.607



B1L100S, U11-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch18, adc0

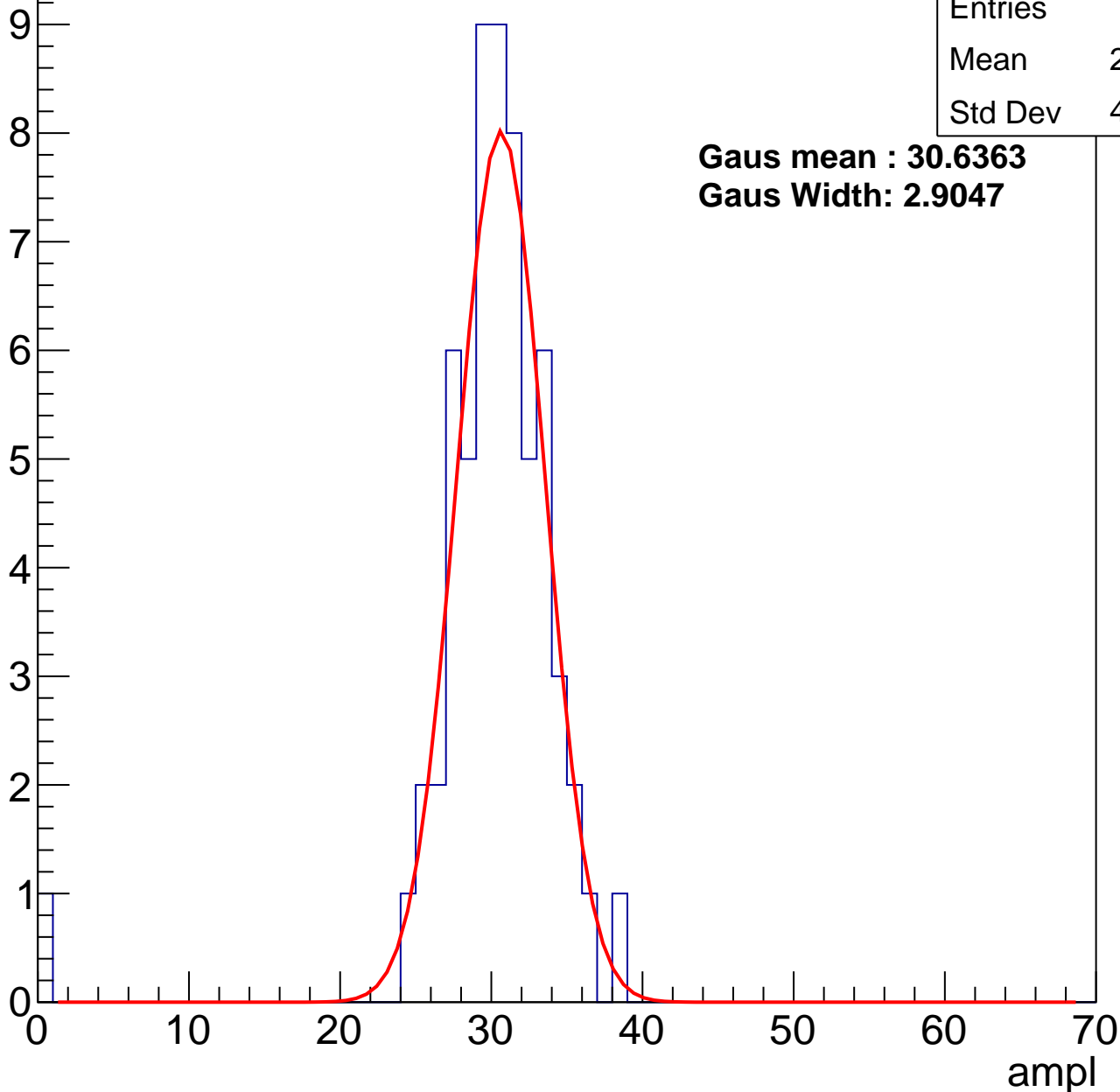
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.69
Std Dev	4.748

Gaus mean : 30.6363

Gaus Width: 2.9047



B1L100S, U11-ch18, adc1

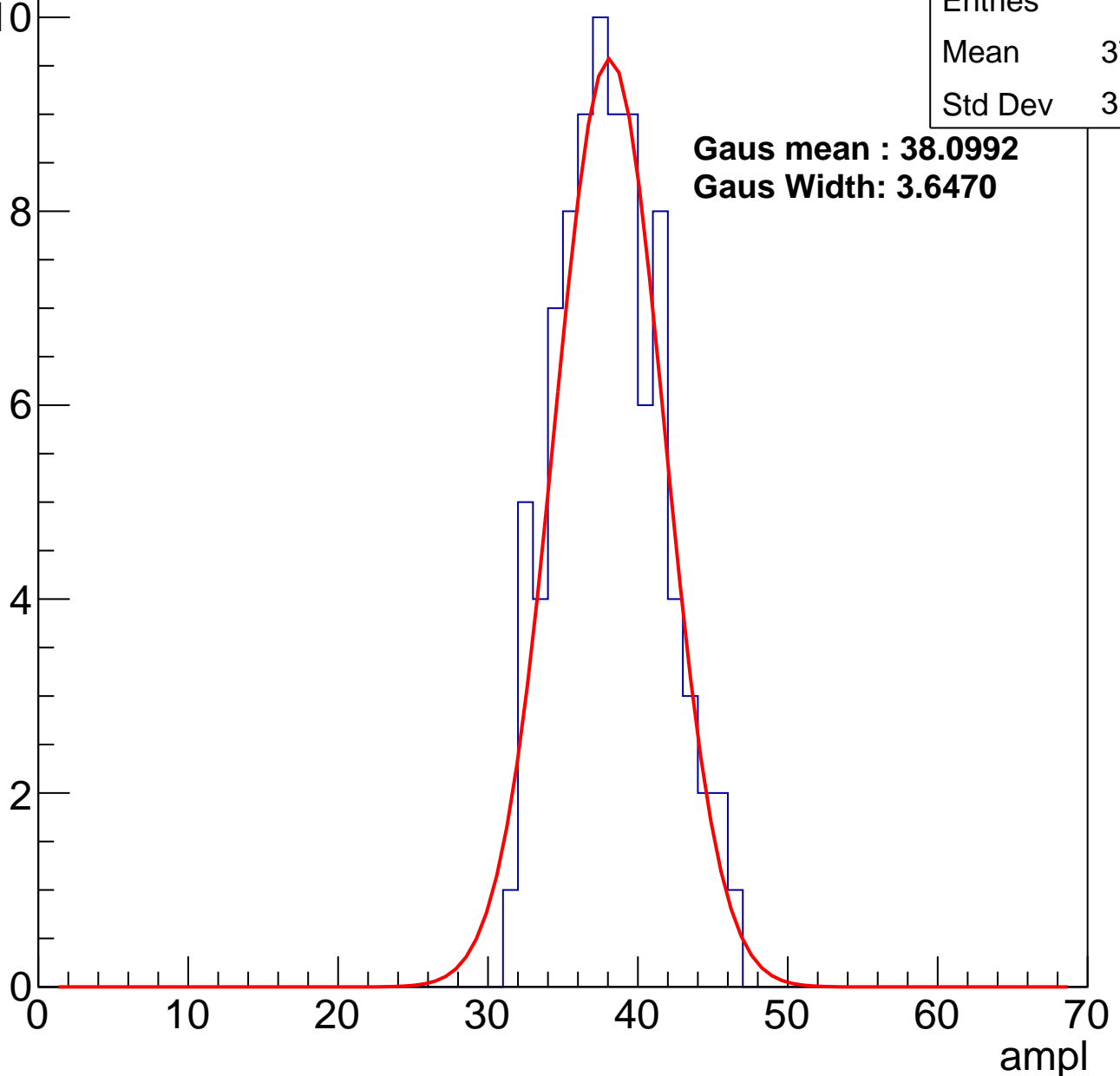
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	37.69
Std Dev	3.419

Gaus mean : 38.0992

Gaus Width: 3.6470



B1L100S, U11-ch18, adc2

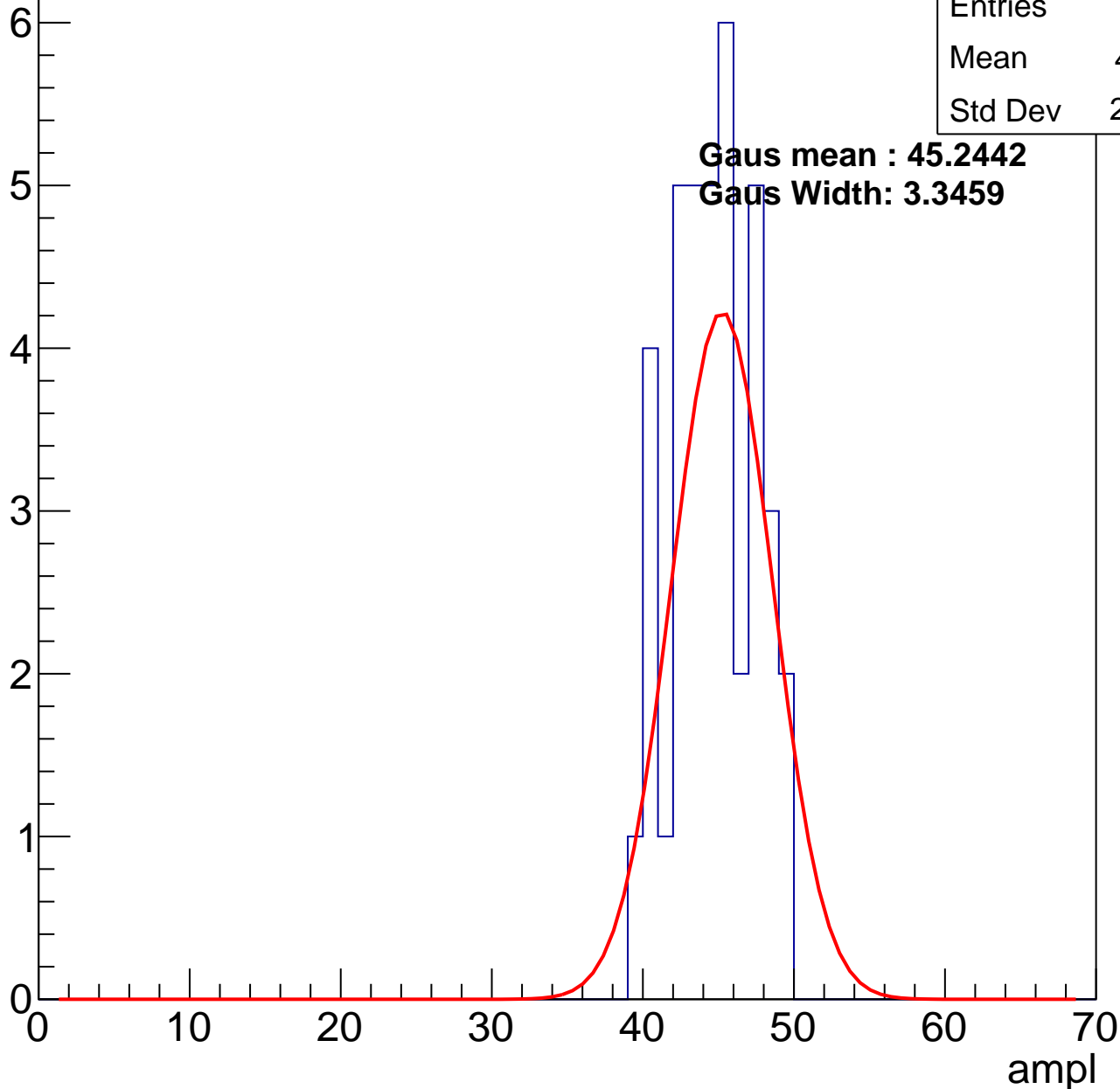
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	44.21
Std Dev	2.672

Gaus mean : 45.2442

Gaus Width: 3.3459

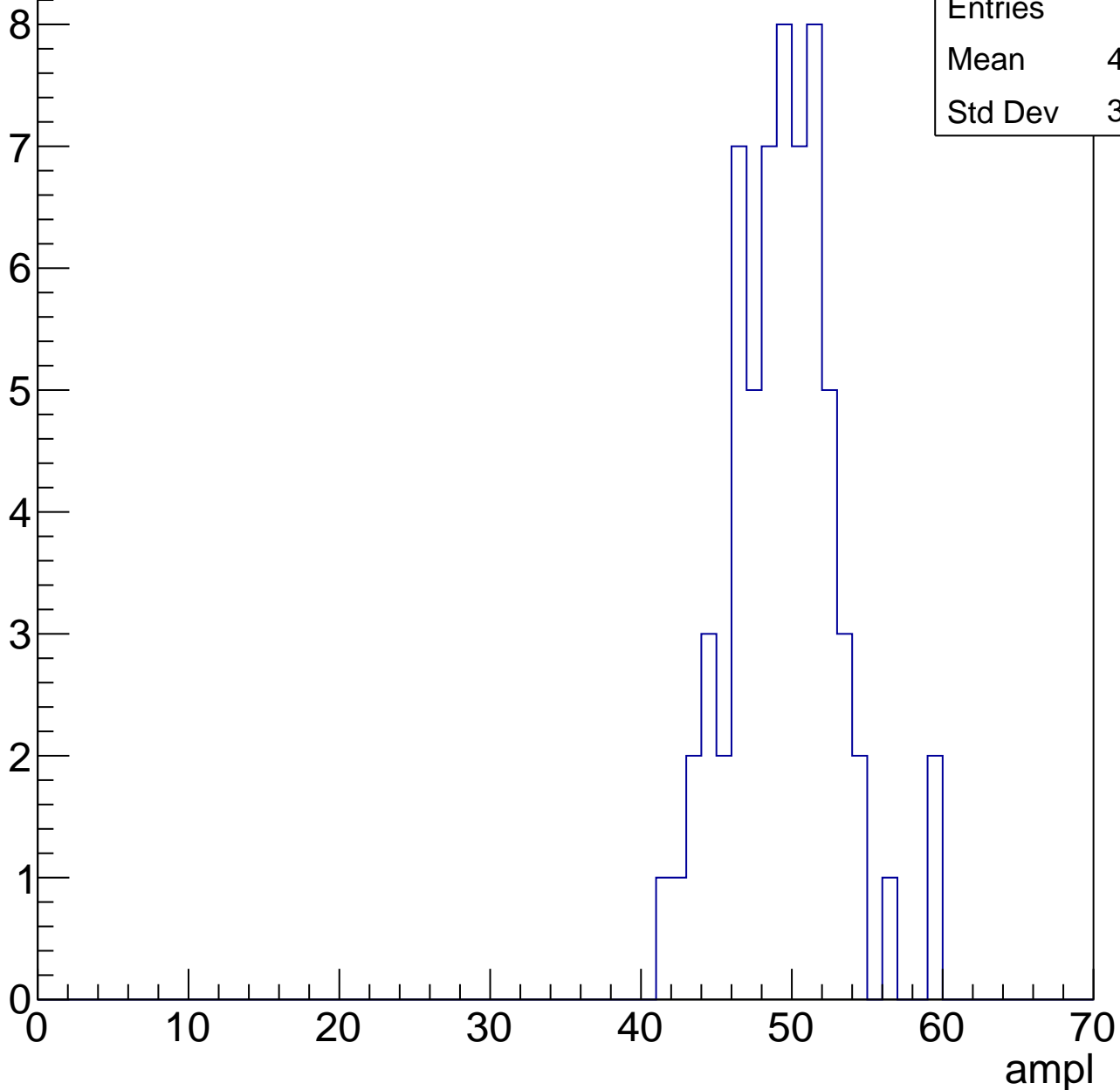


B1L100S, U11-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	48.98
Std Dev	3.564

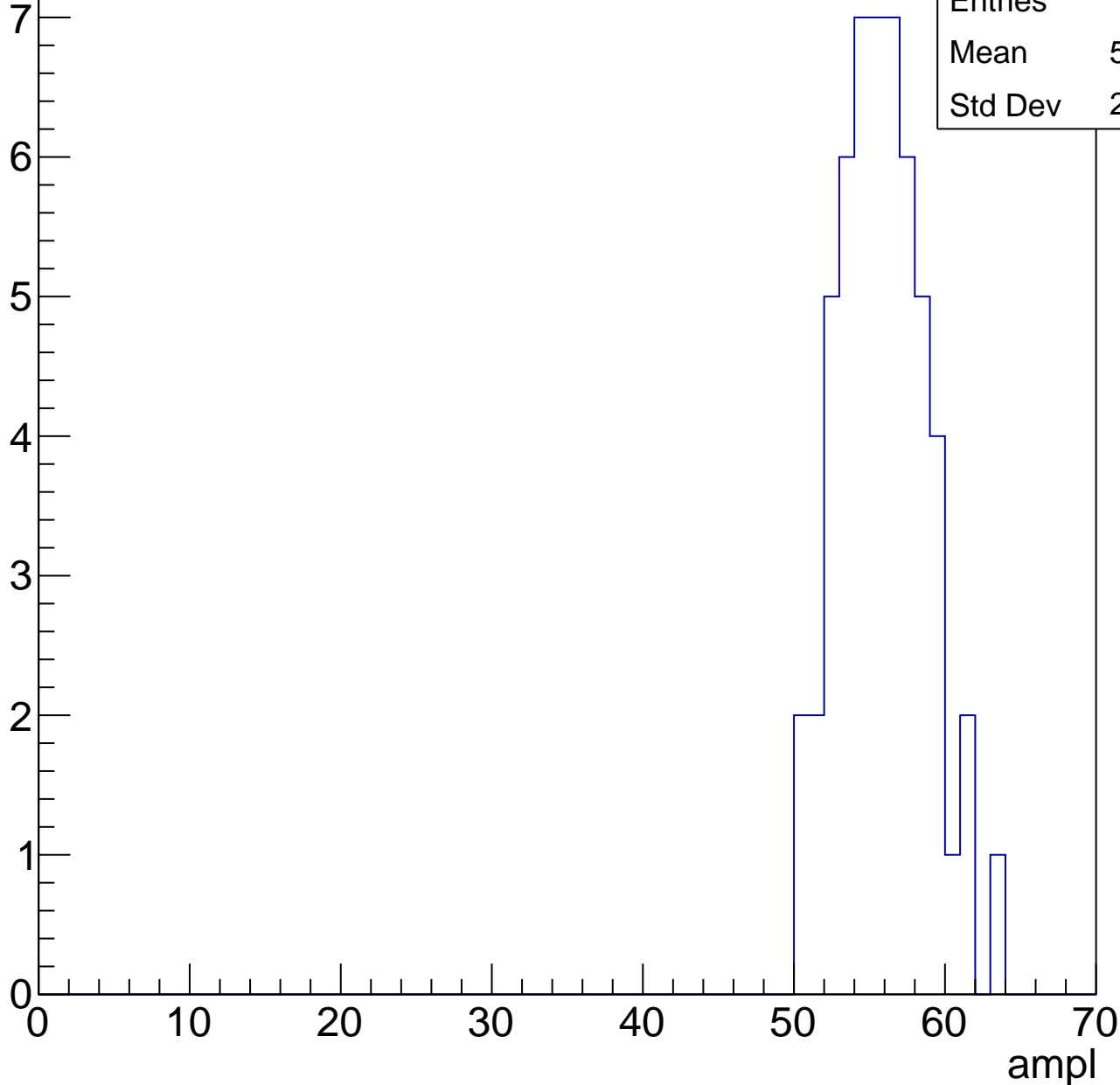


B1L100S, U11-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	55.42
Std Dev	2.858

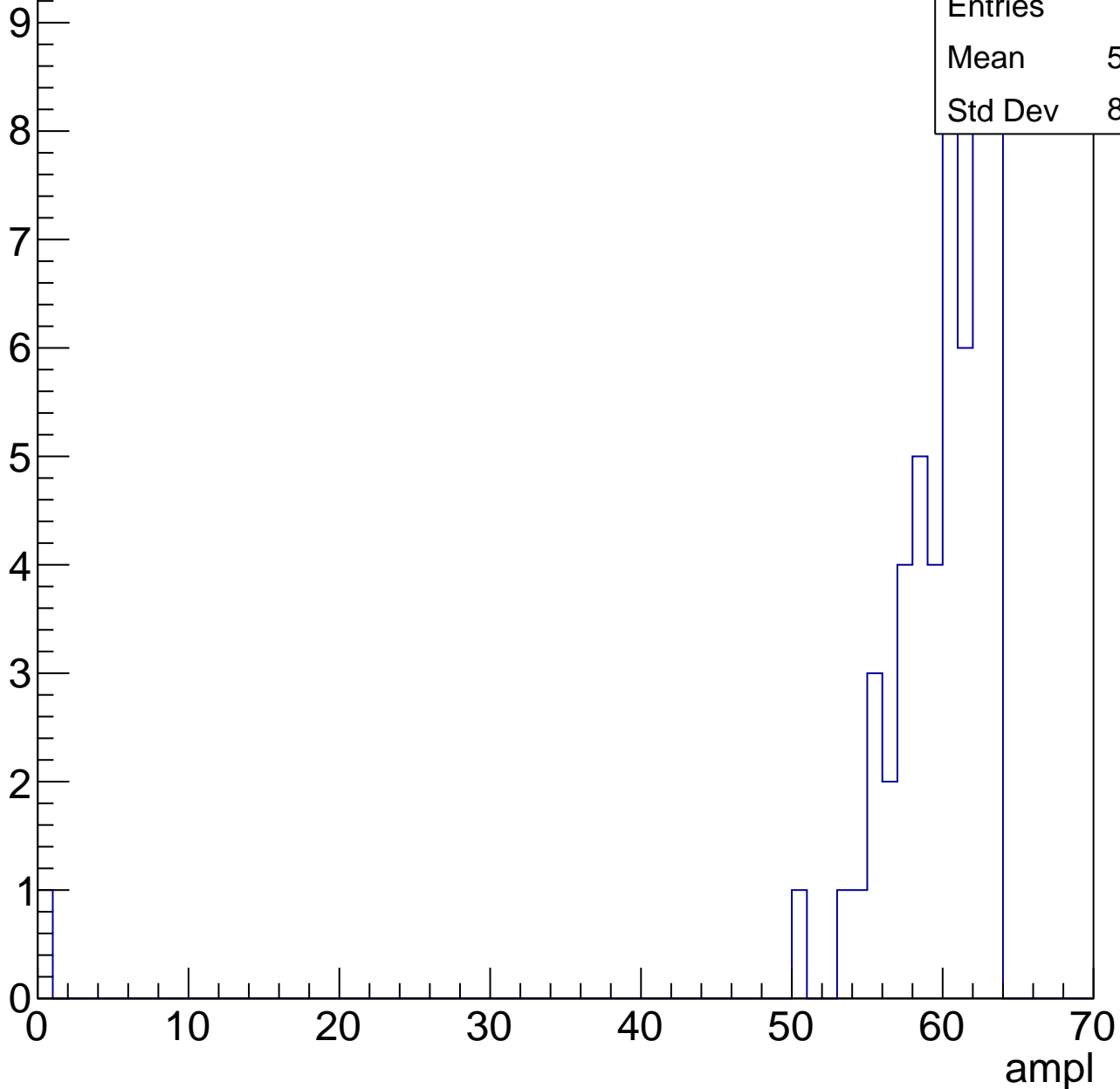


B1L100S, U11-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	58.44
Std Dev	8.537



B1L100S, U11-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L100S, U11-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U11-ch19, adc0

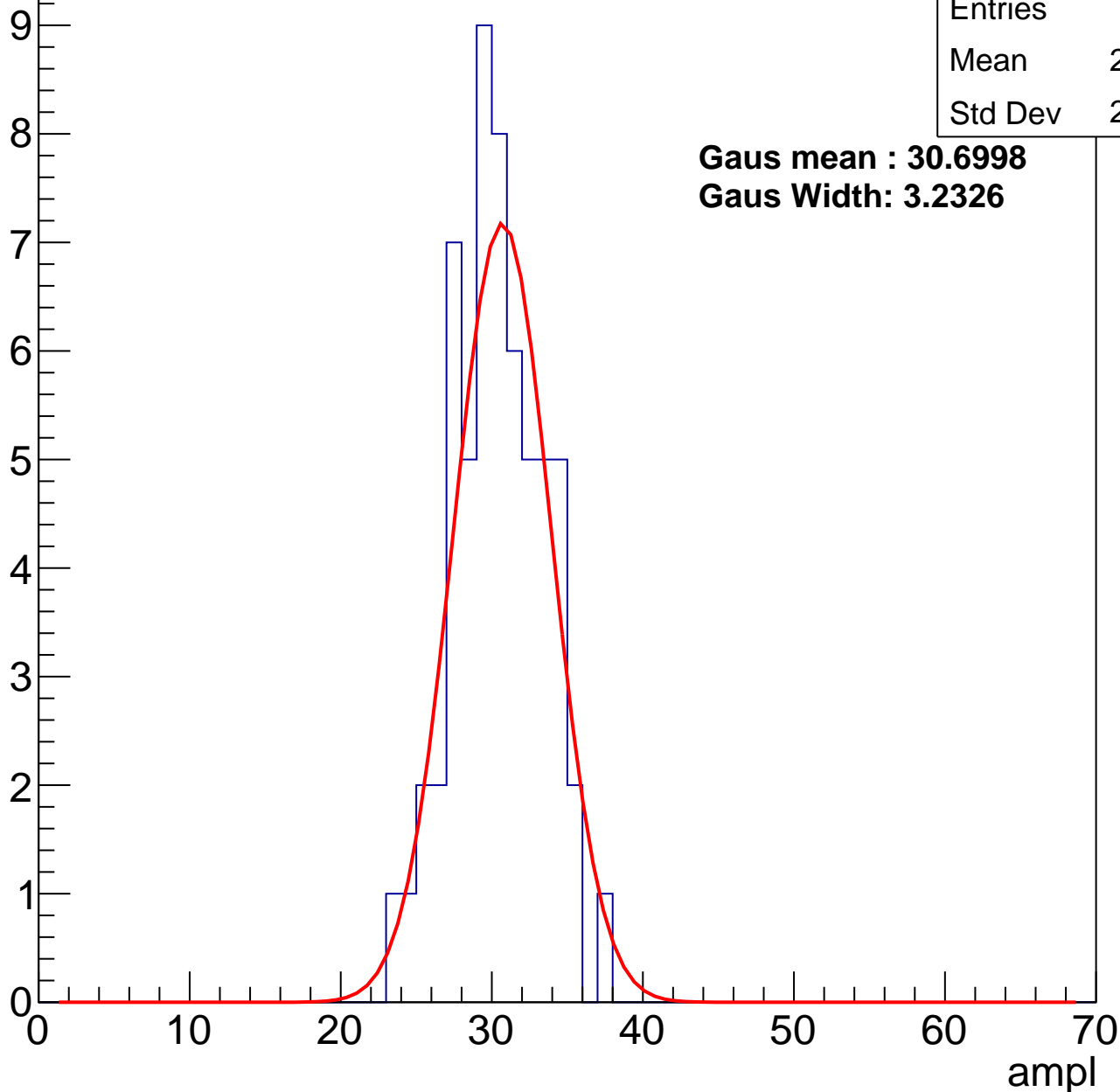
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.95
Std Dev	2.937

Gaus mean : 30.6998

Gaus Width: 3.2326



B1L100S, U11-ch19, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	36.14
Std Dev	3.295

Gaus mean : 37.1446

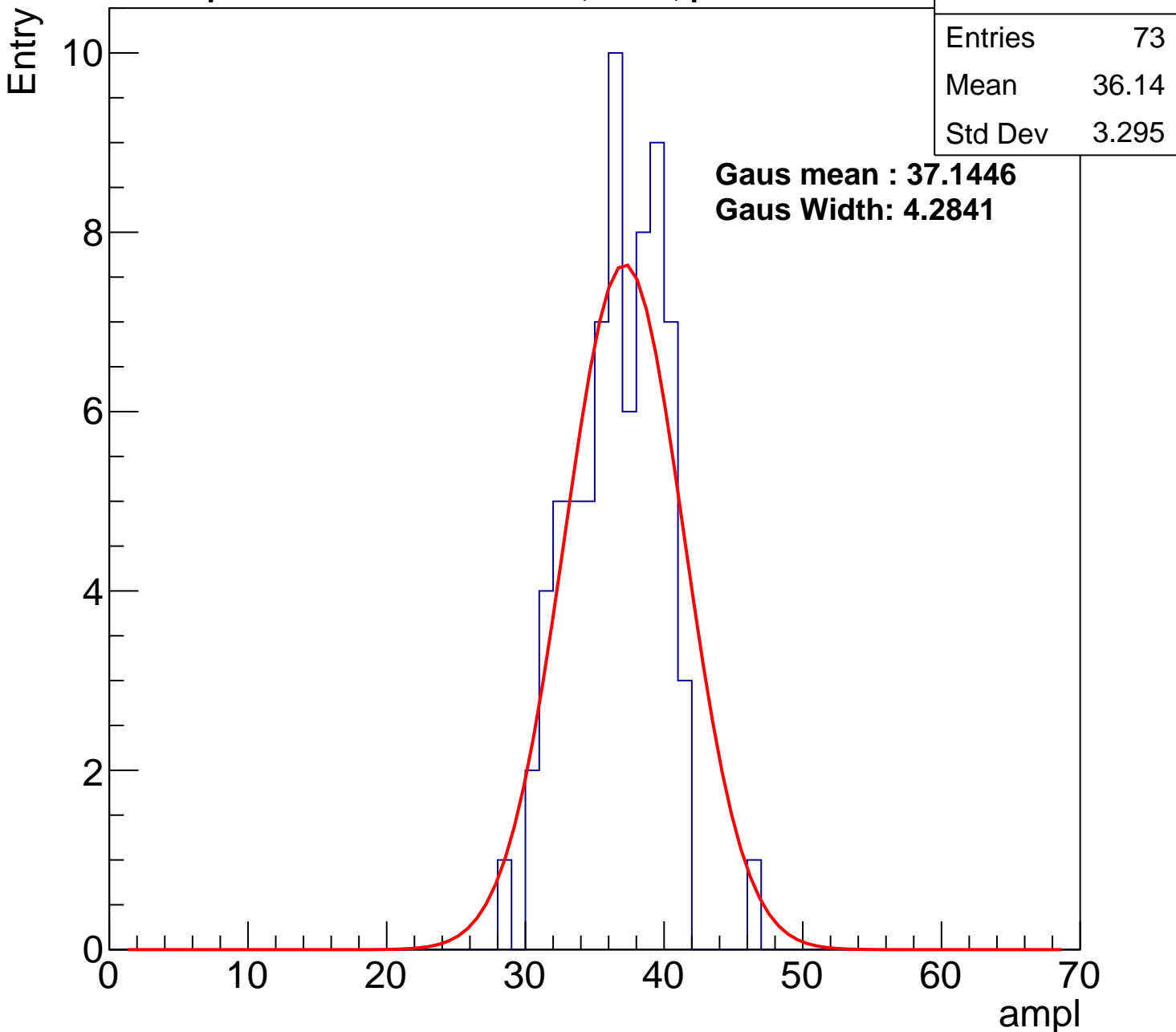
Gaus Width: 4.2841

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch19, adc2

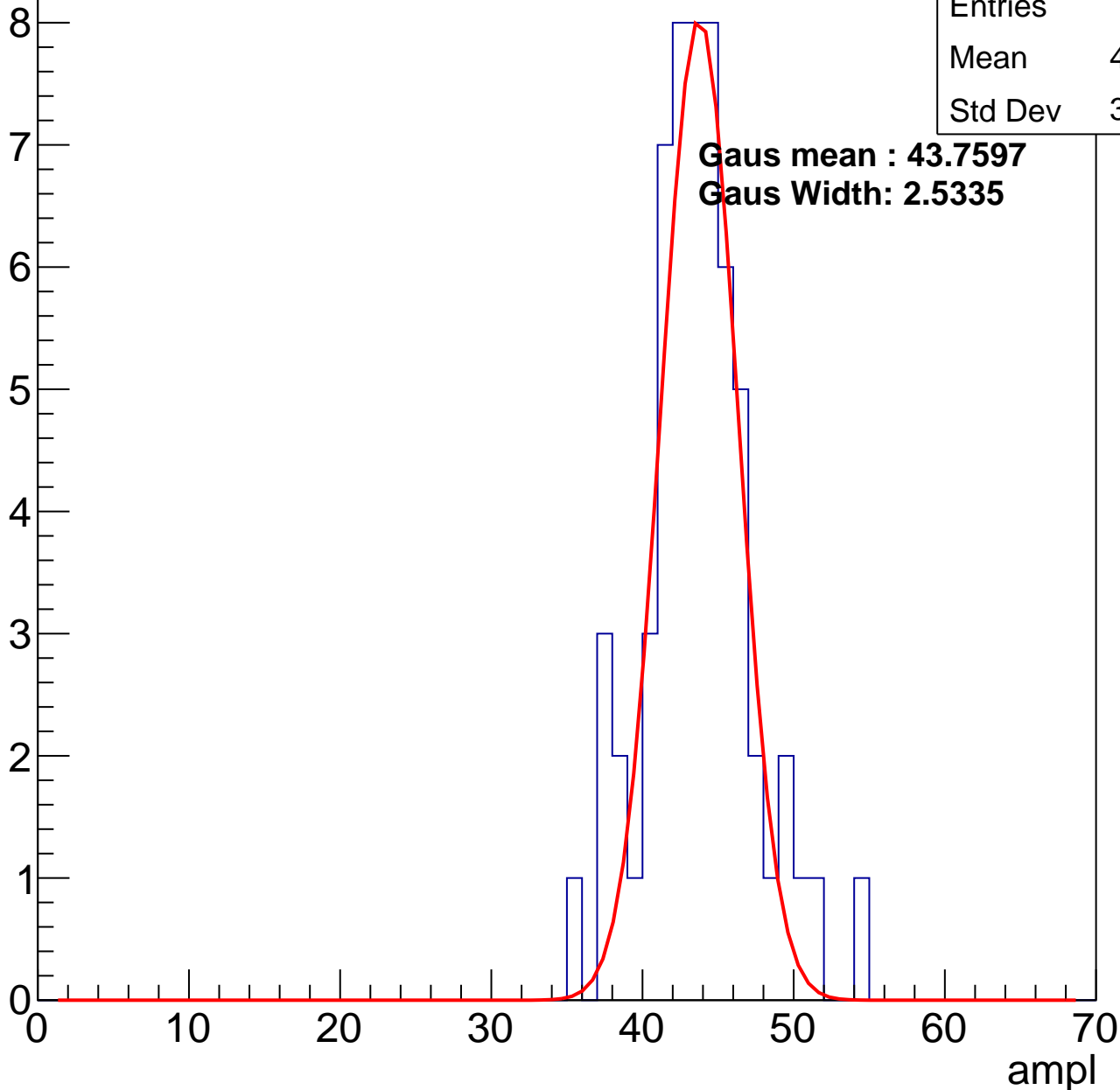
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.25
Std Dev	3.505

Gaus mean : 43.7597

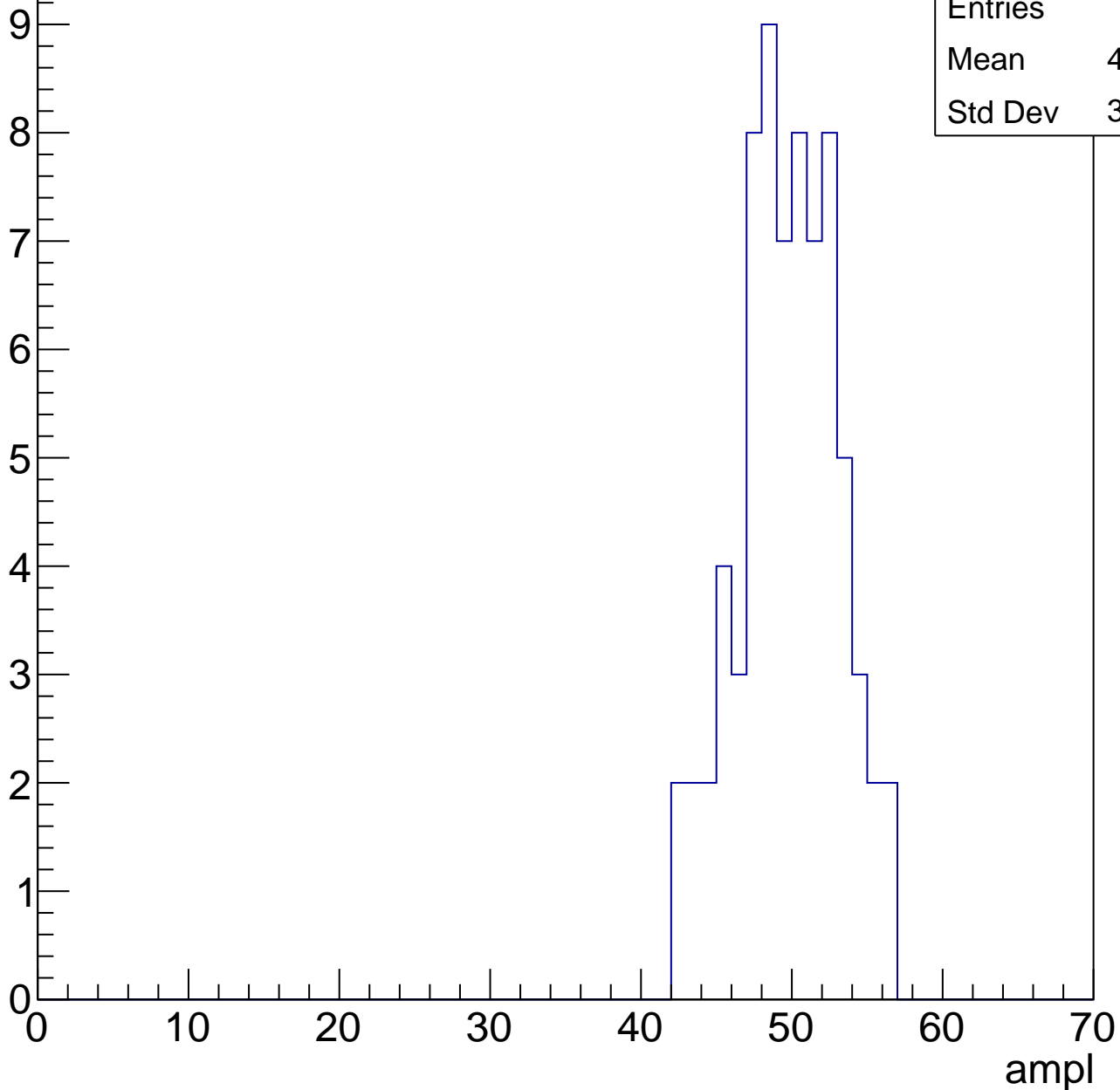
Gaus Width: 2.5335



B1L100S, U11-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

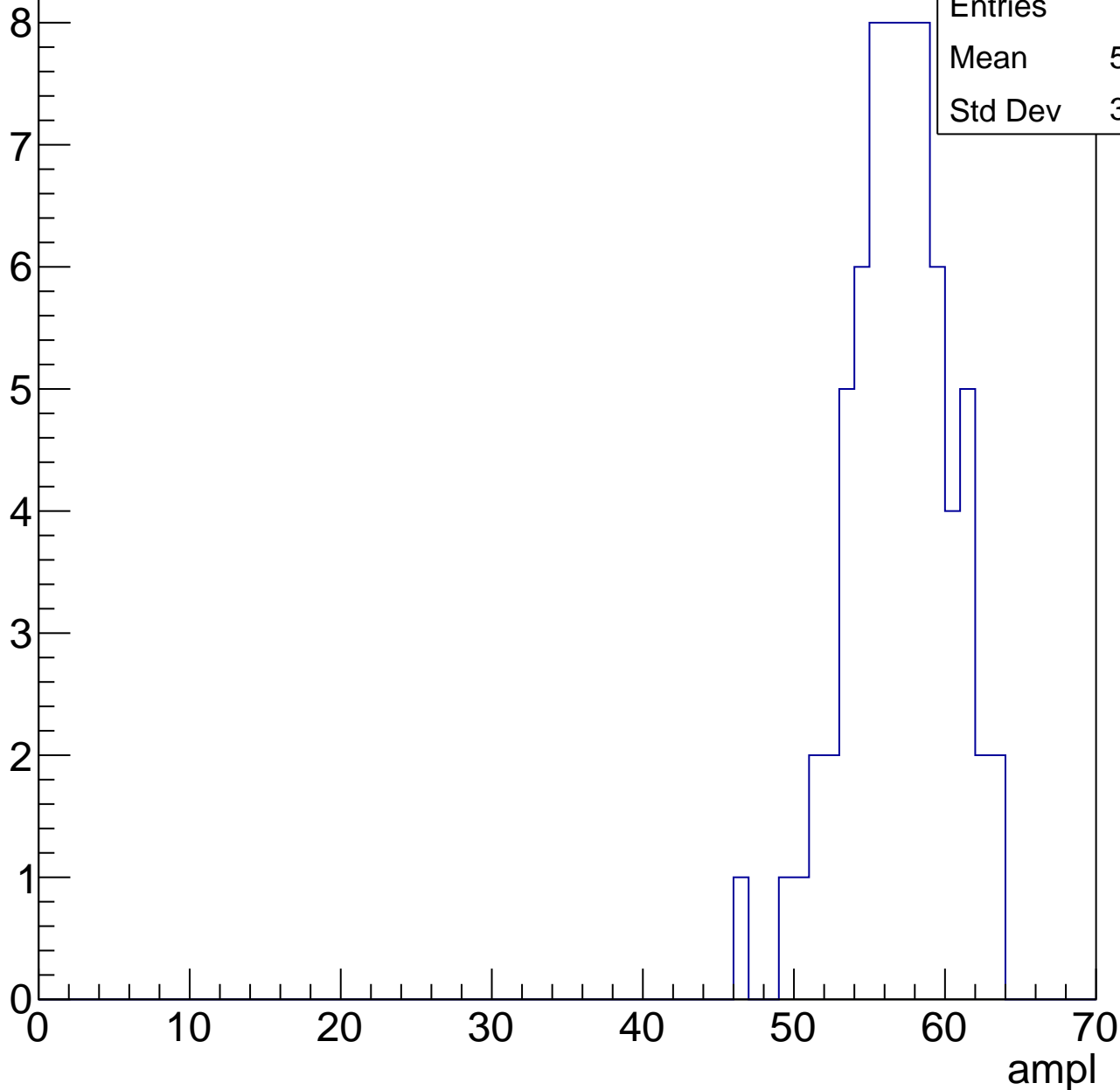


B1L100S, U11-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	56.48
Std Dev	3.365

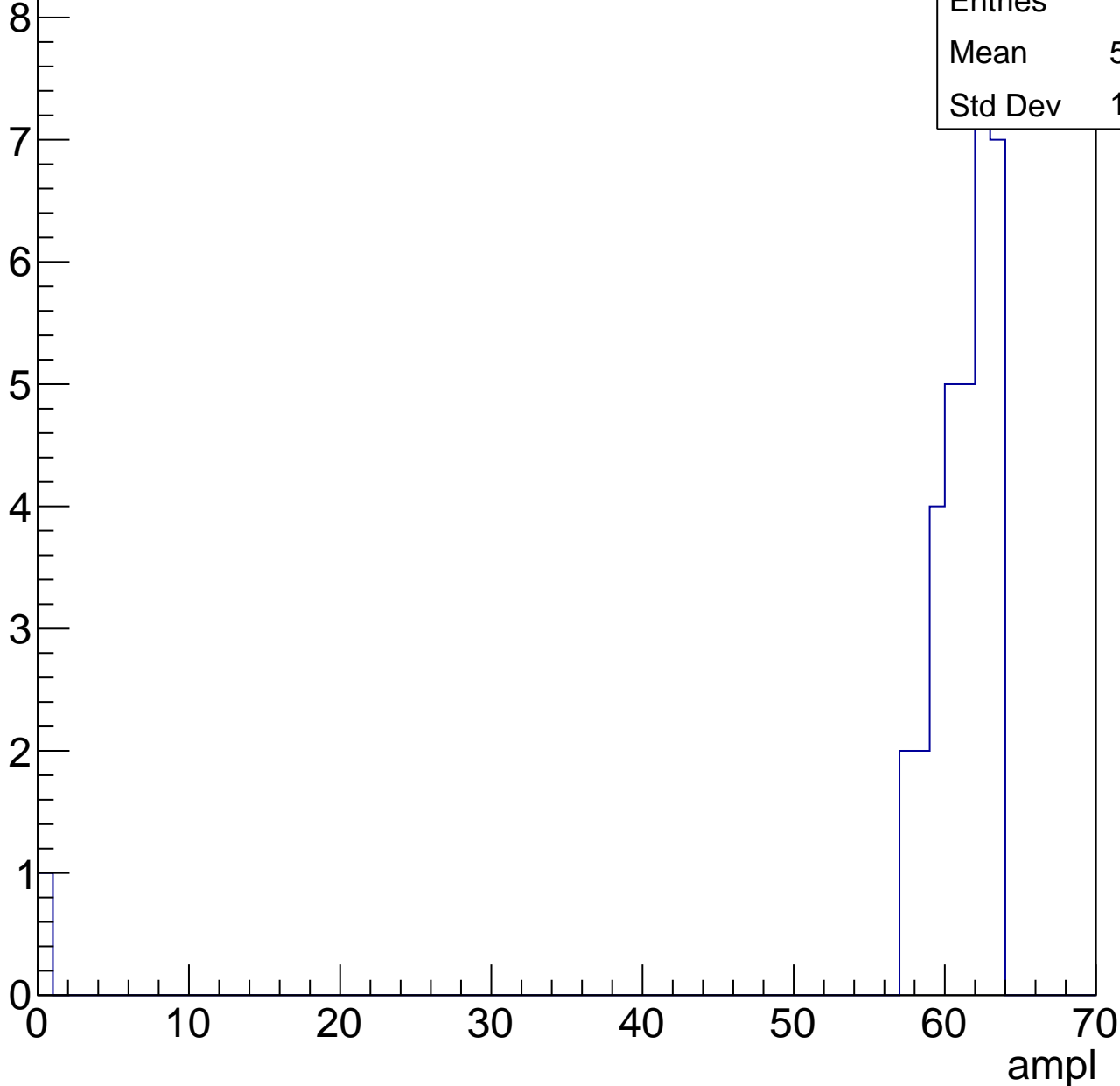


B1L100S, U11-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	59.06
Std Dev	10.43



B1L100S, U11-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

61.5

Std Dev

1.5

B1L100S, U11-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch20, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	28.05
Std Dev	3.312

Gaus mean : 28.6127

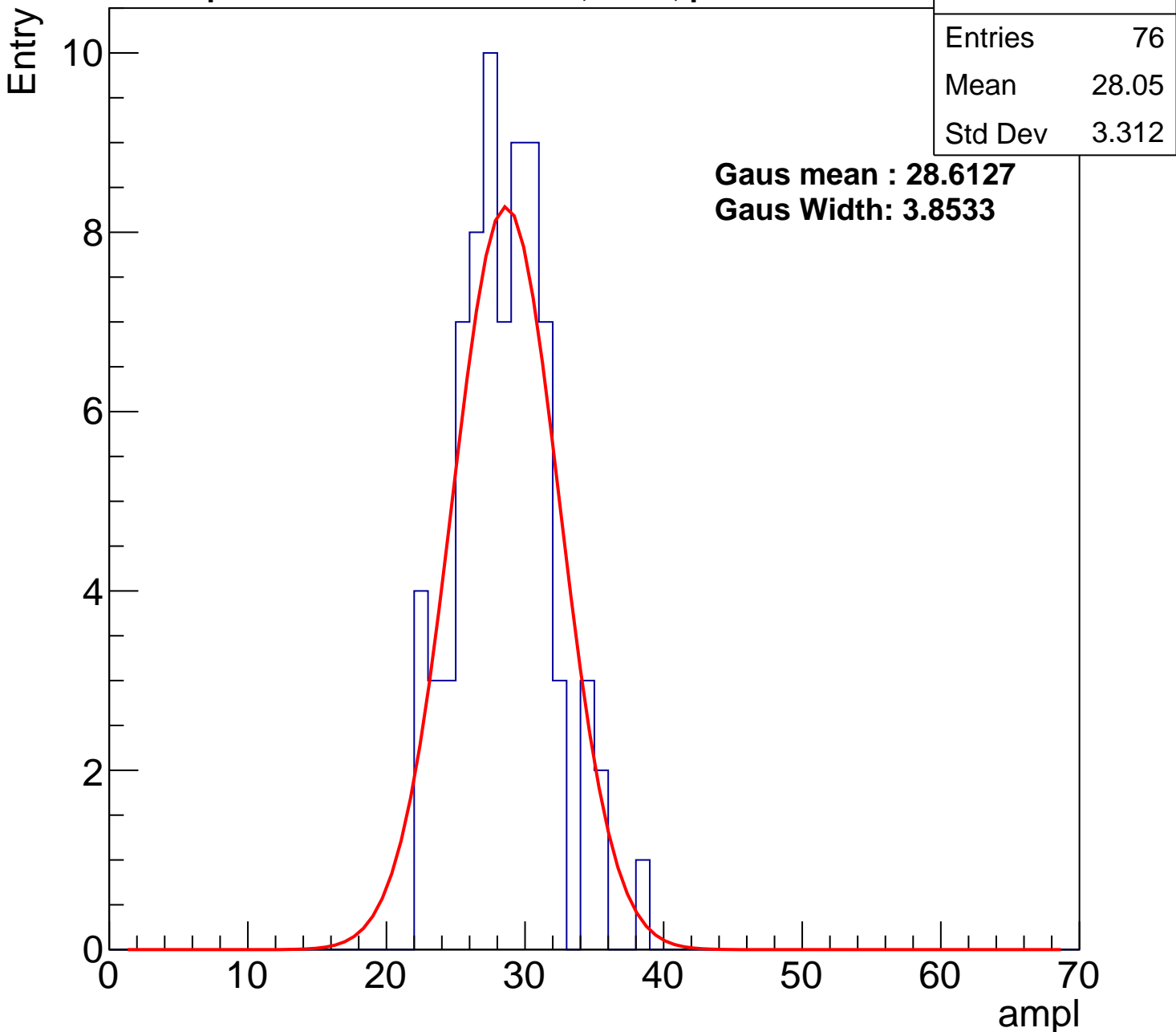
Gaus Width: 3.8533

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch20, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	36.11
Std Dev	3.331

Gaus mean : 36.5141

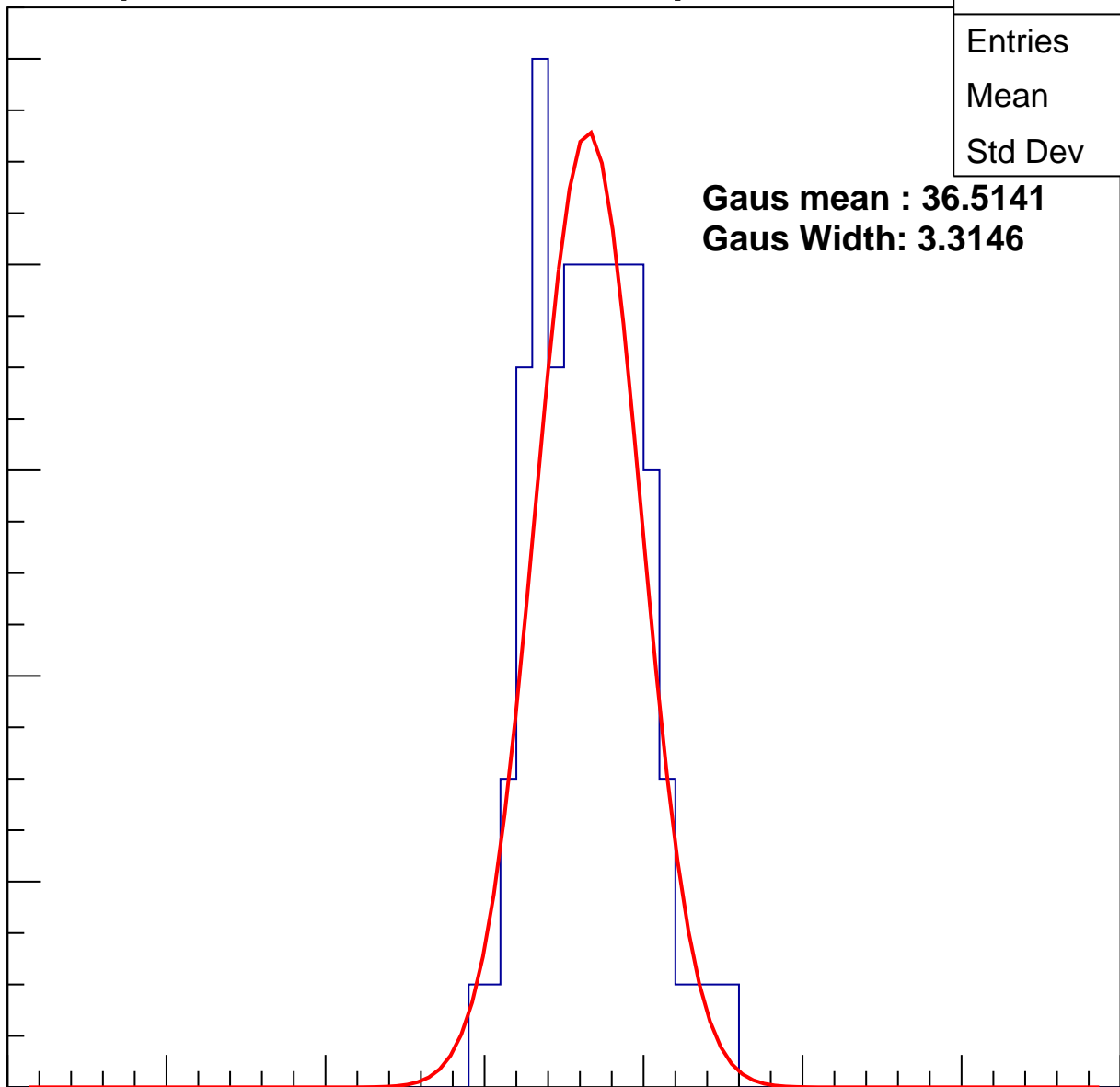
Gaus Width: 3.3146

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch20, adc2

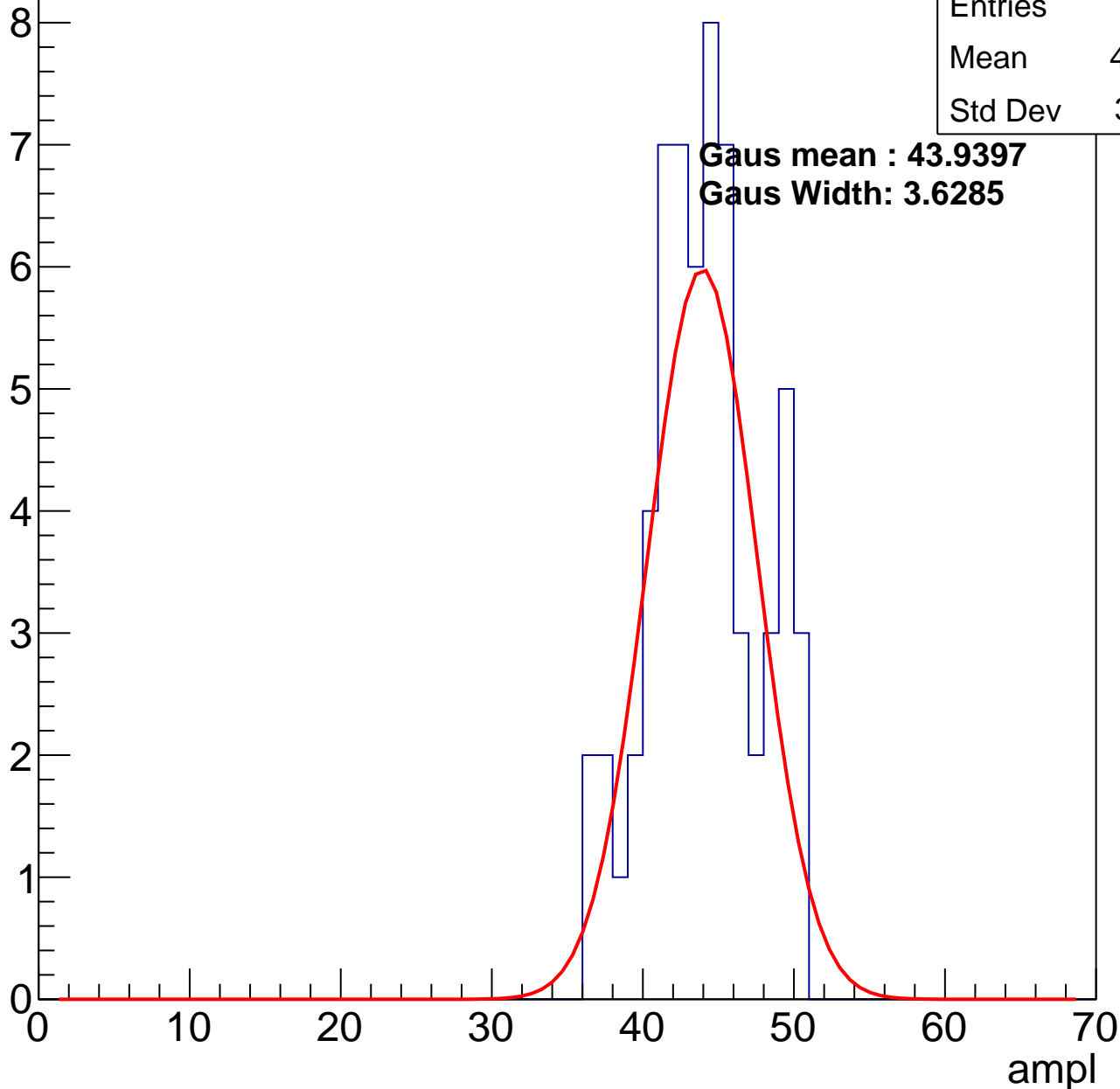
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.53
Std Dev	3.541

Gaus mean : 43.9397

Gaus Width: 3.6285

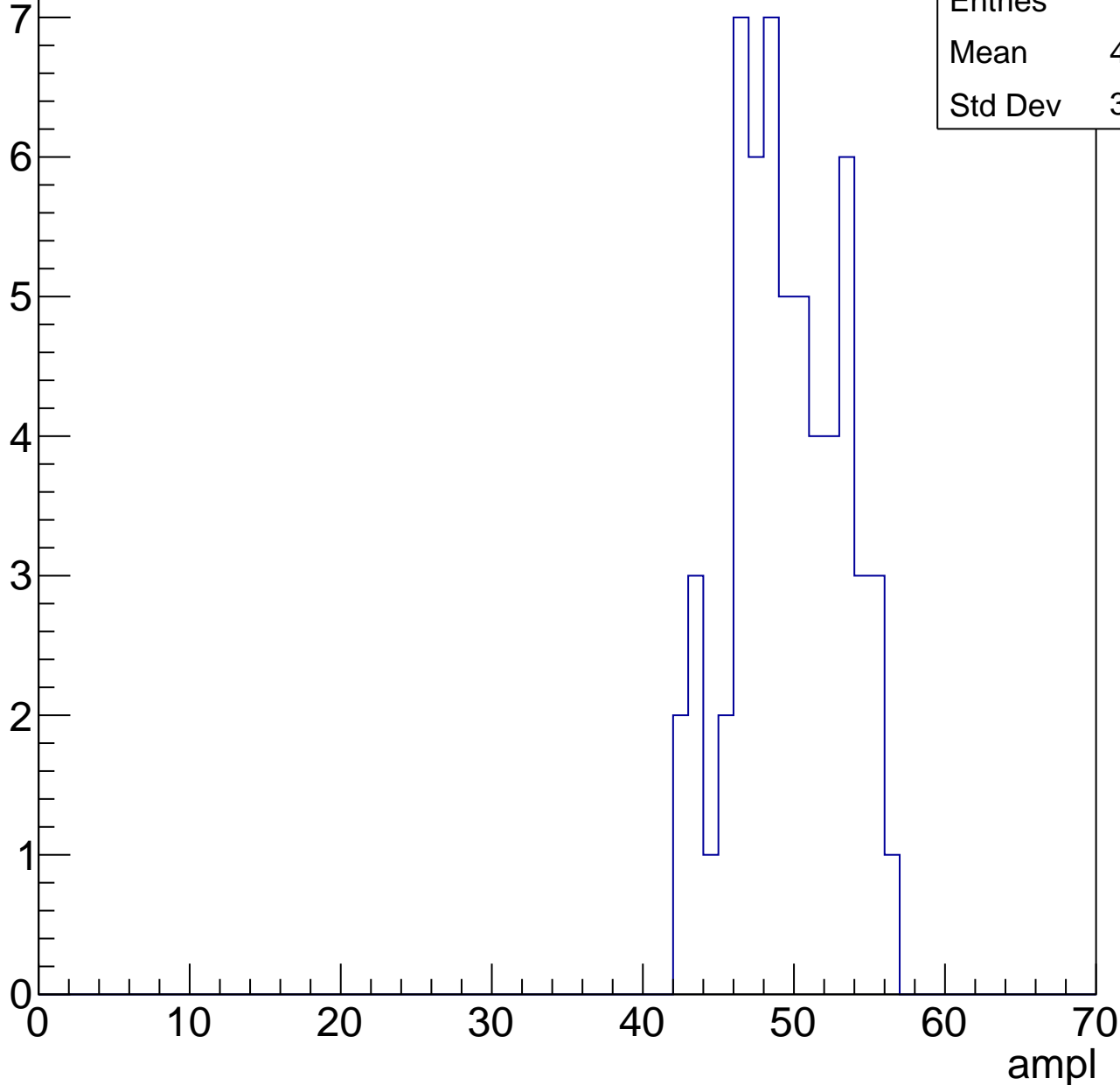


B1L100S, U11-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.07
Std Dev	3.546

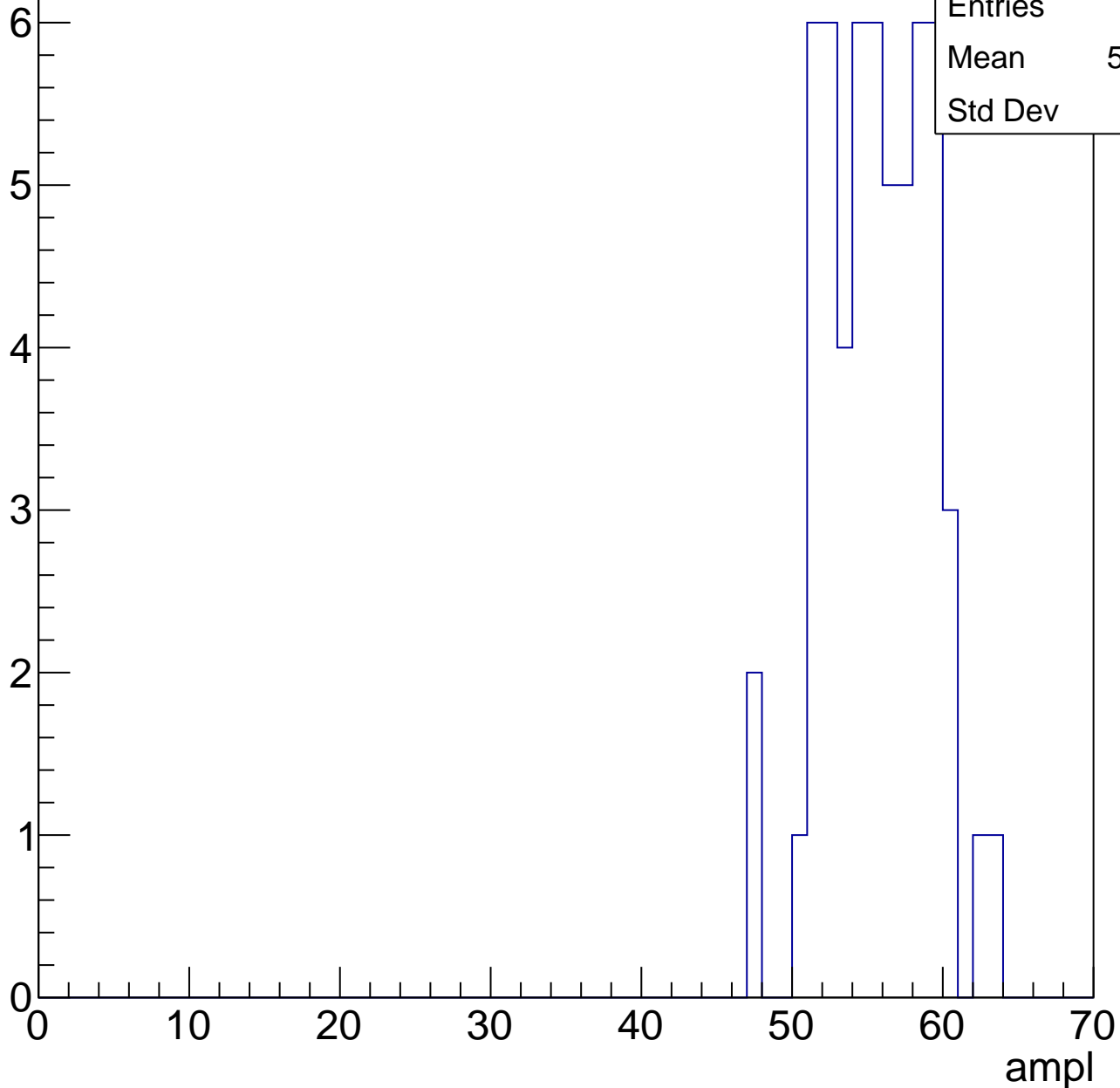


B1L100S, U11-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	55.17
Std Dev	3.44

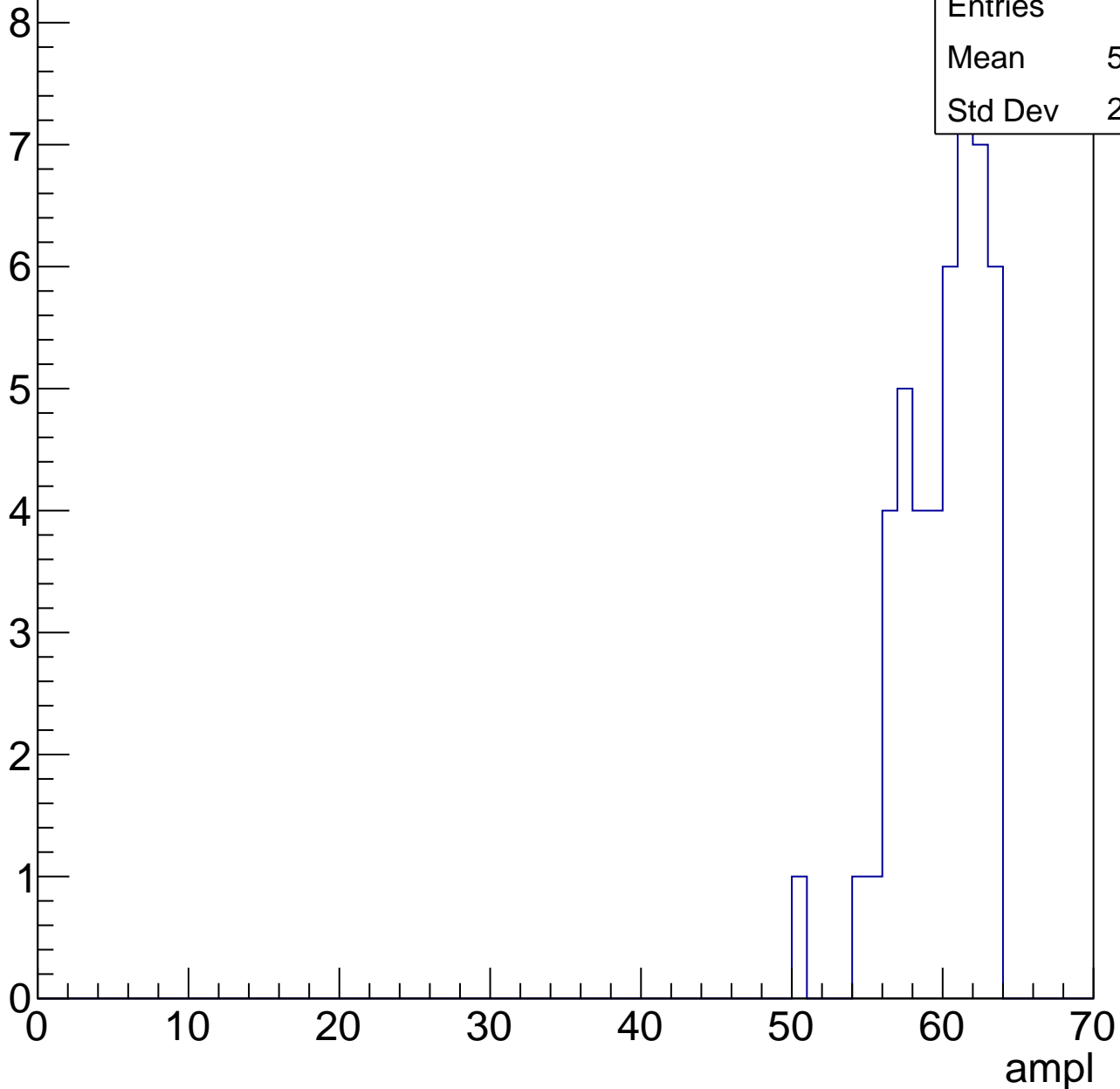


B1L100S, U11-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

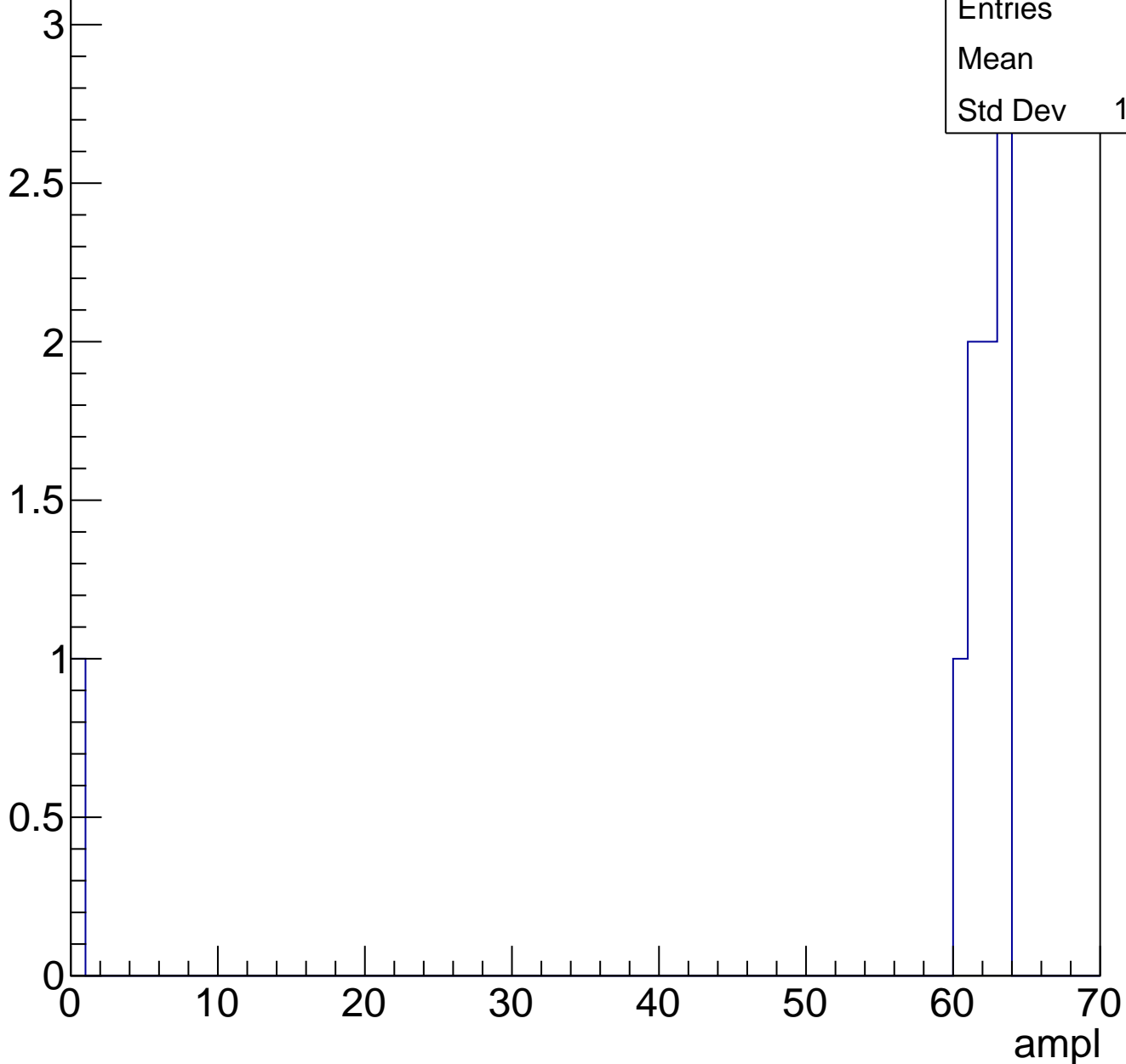
Entries	47
Mean	59.49
Std Dev	2.797



B1L100S, U11-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch21, adc0

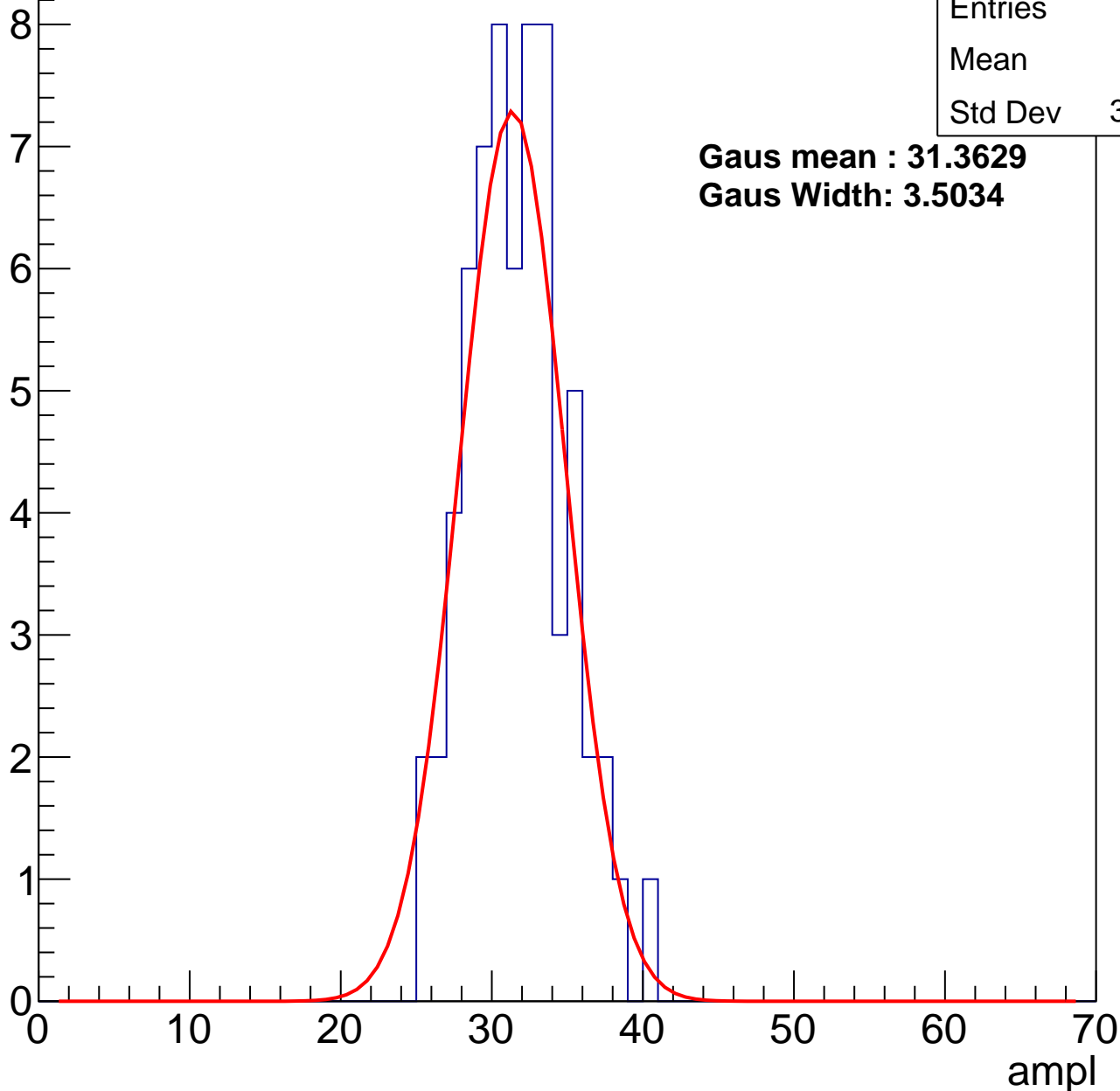
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	31.2
Std Dev	3.216

Gaus mean : 31.3629

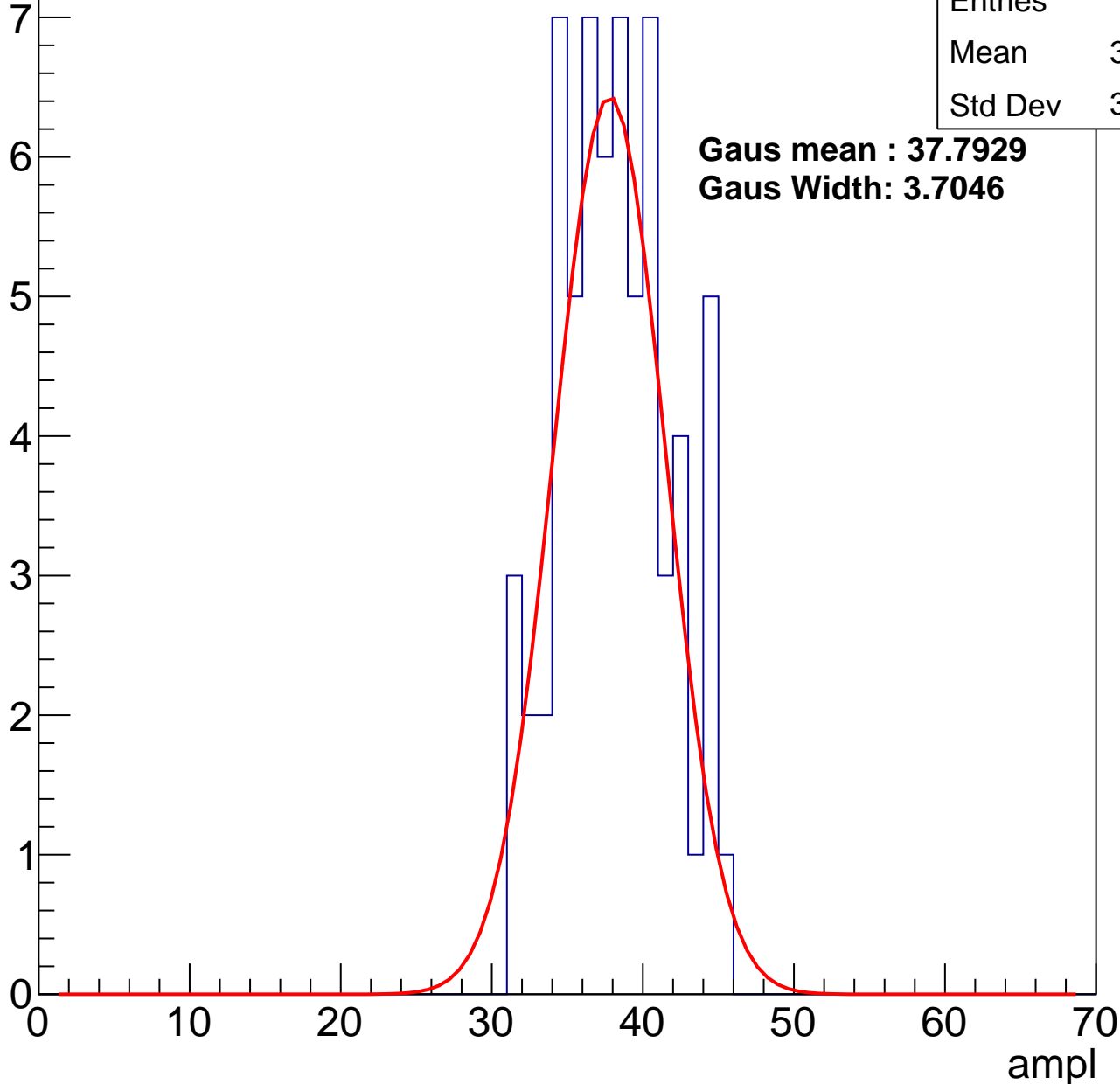
Gaus Width: 3.5034



B1L100S, U11-ch21, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch21, adc2

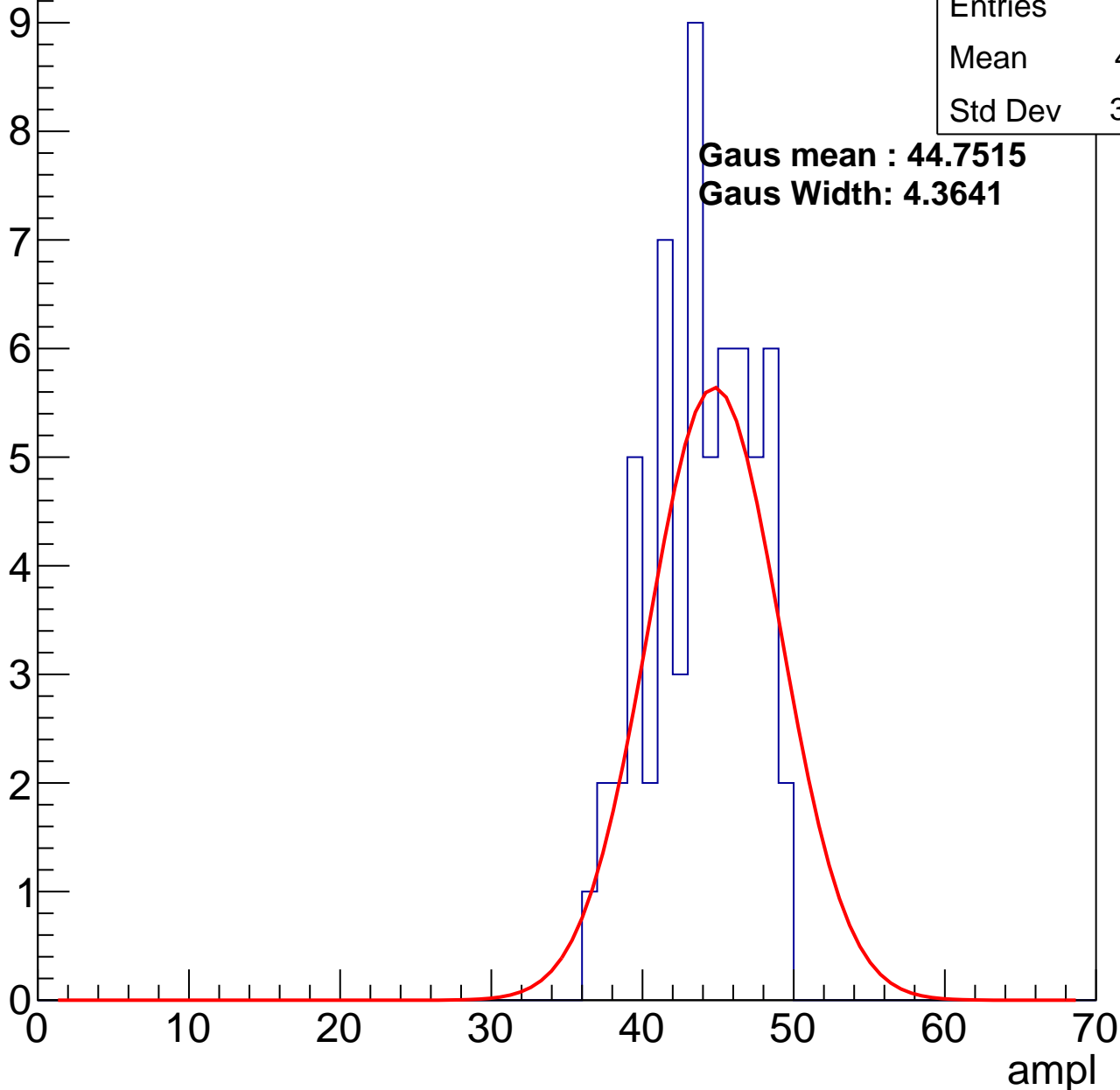
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.41
Std Dev	3.326

Gaus mean : 44.7515

Gaus Width: 4.3641

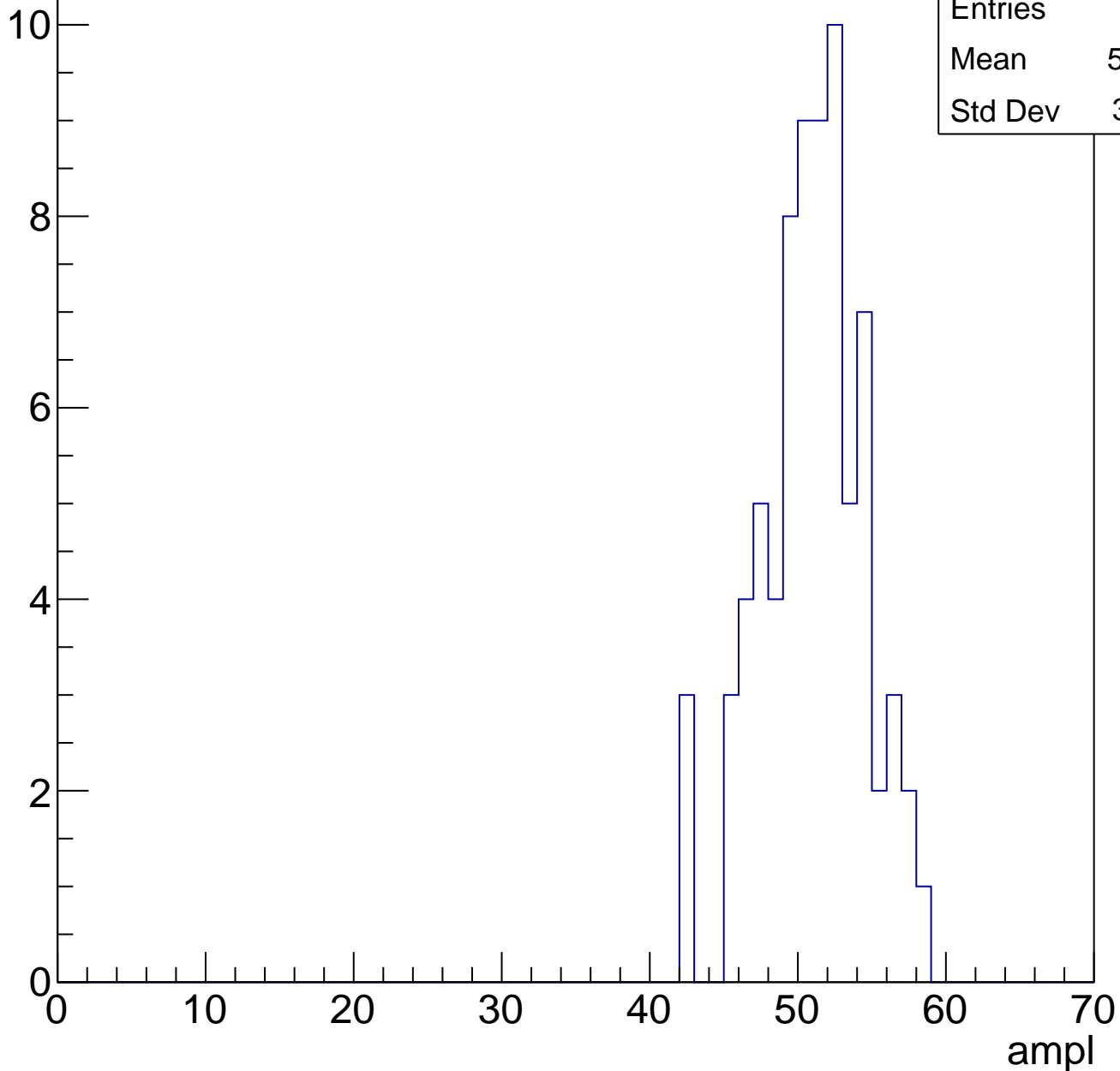


B1L100S, U11-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	50.48
Std Dev	3.481

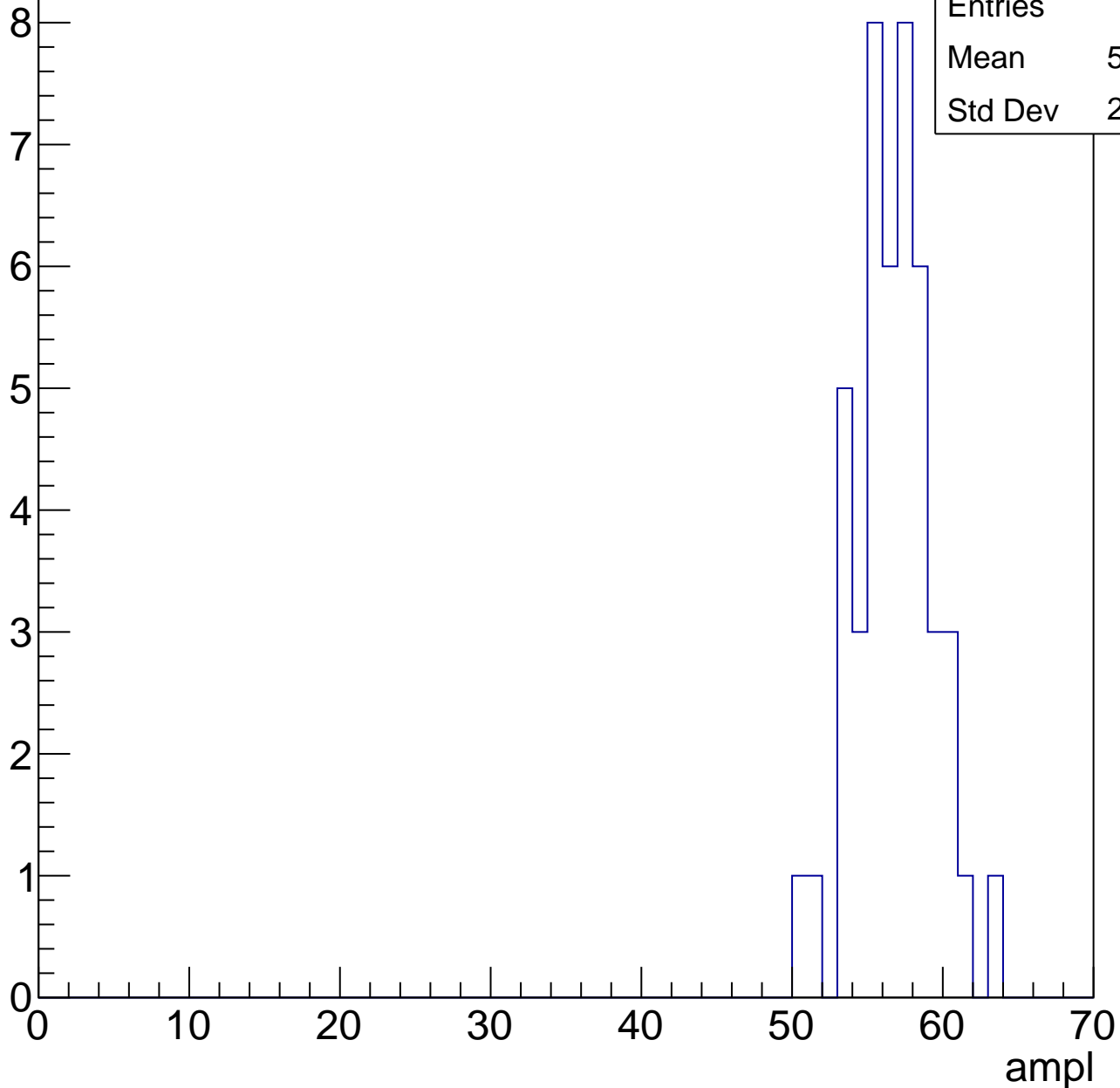
Entry



B1L100S, U11-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

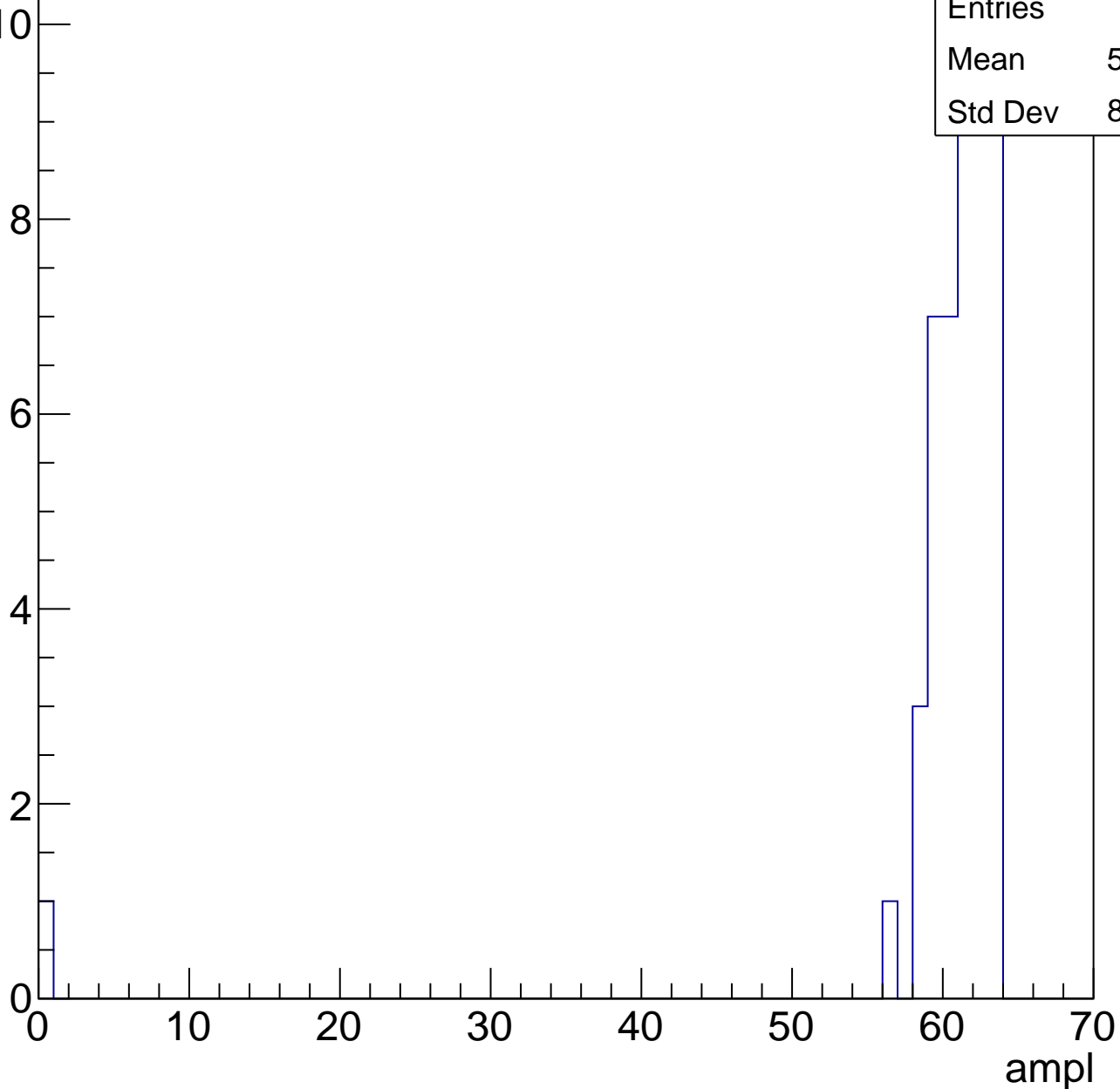


B1L100S, U11-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	59.55
Std Dev	8.939



B1L100S, U11-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch22, adc0

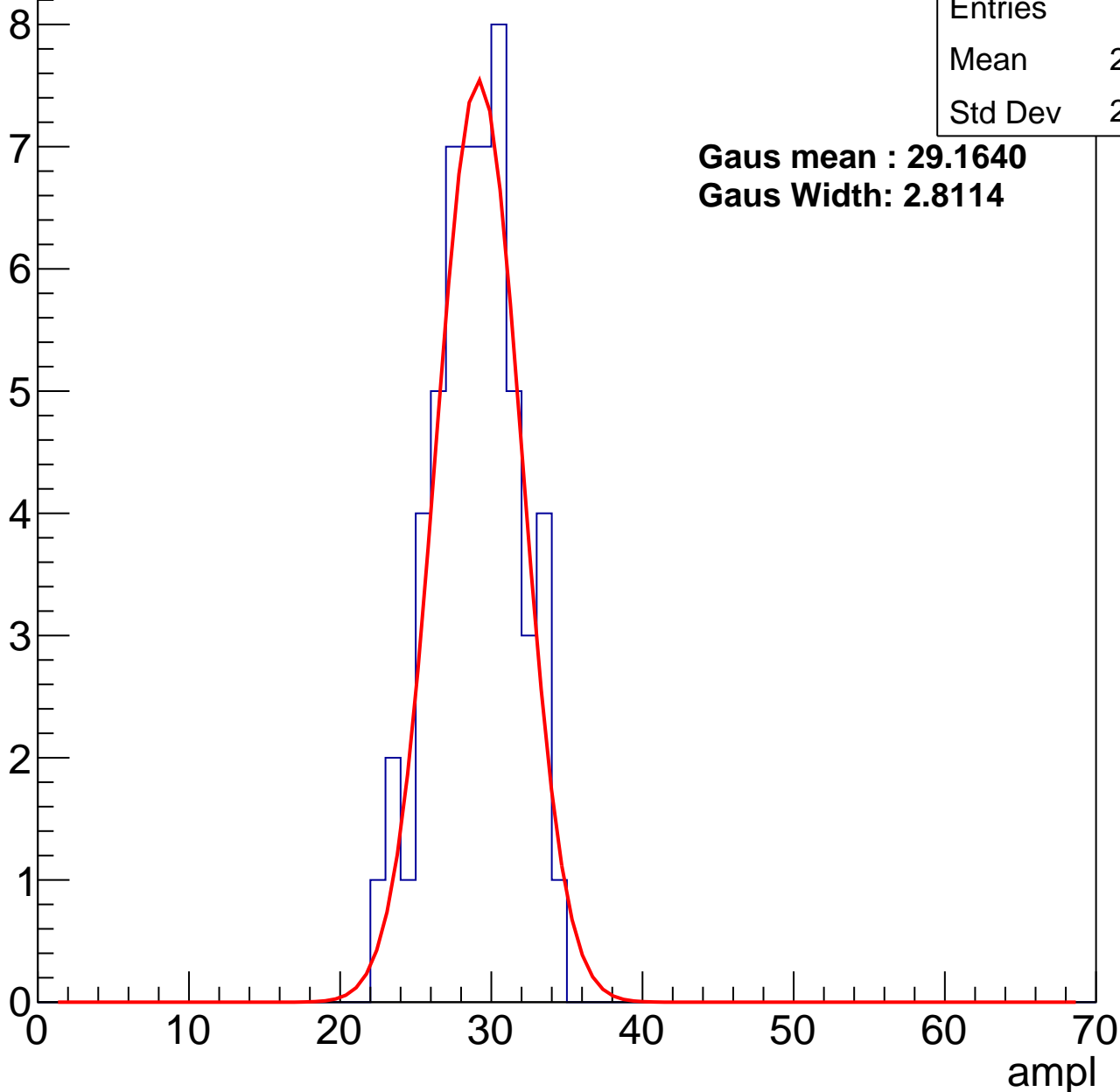
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	28.49
Std Dev	2.763

Gaus mean : 29.1640

Gaus Width: 2.8114



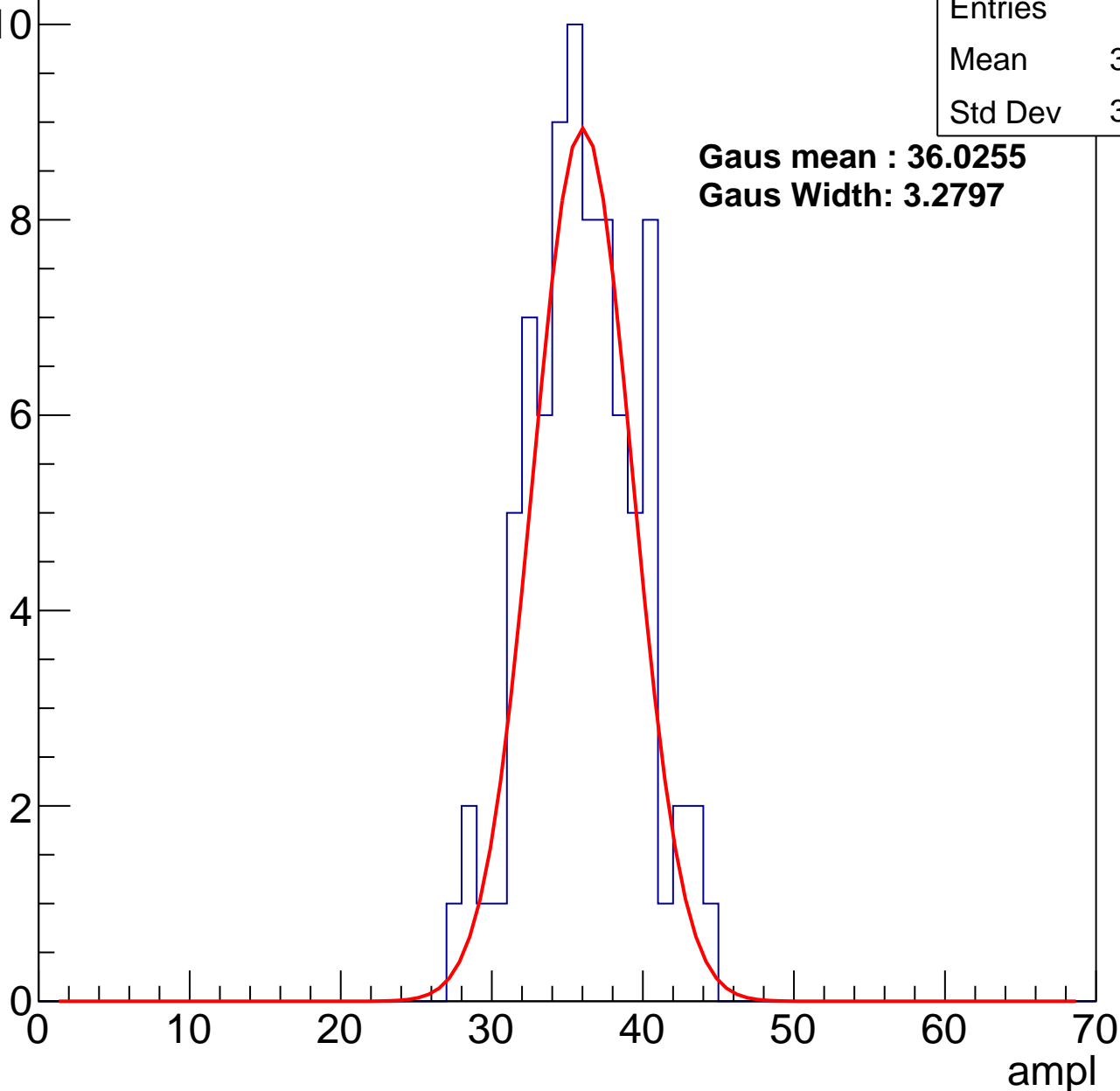
B1L100S, U11-ch22, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	35.63
Std Dev	3.613

Gaus mean : 36.0255
Gaus Width: 3.2797



B1L100S, U11-ch22, adc2

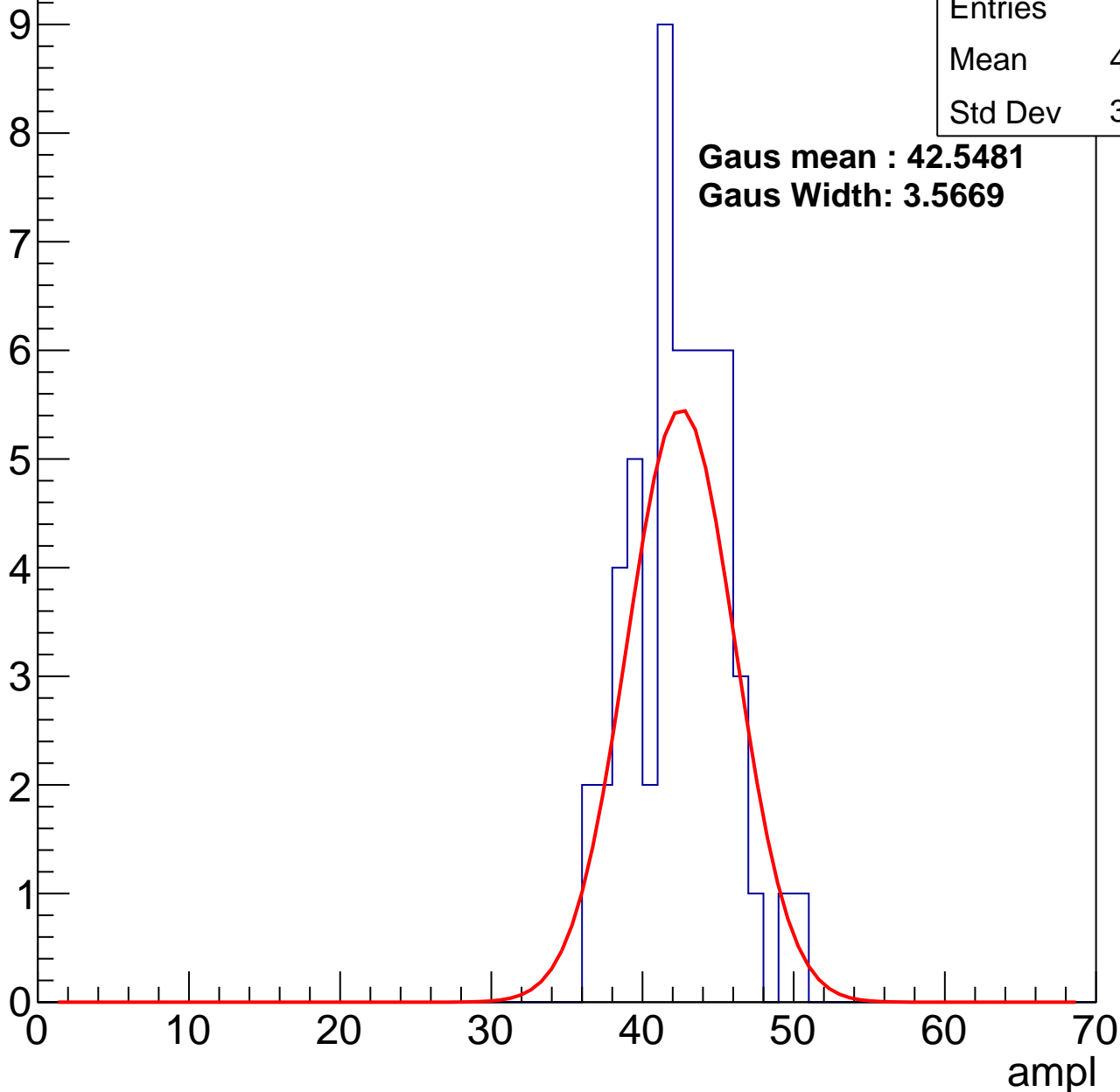
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	42.04
Std Dev	3.097

Gaus mean : 42.5481

Gaus Width: 3.5669



B1L100S, U11-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	49.29
Std Dev	3.399

Entry

10

8

6

4

2

0

0

10

20

30

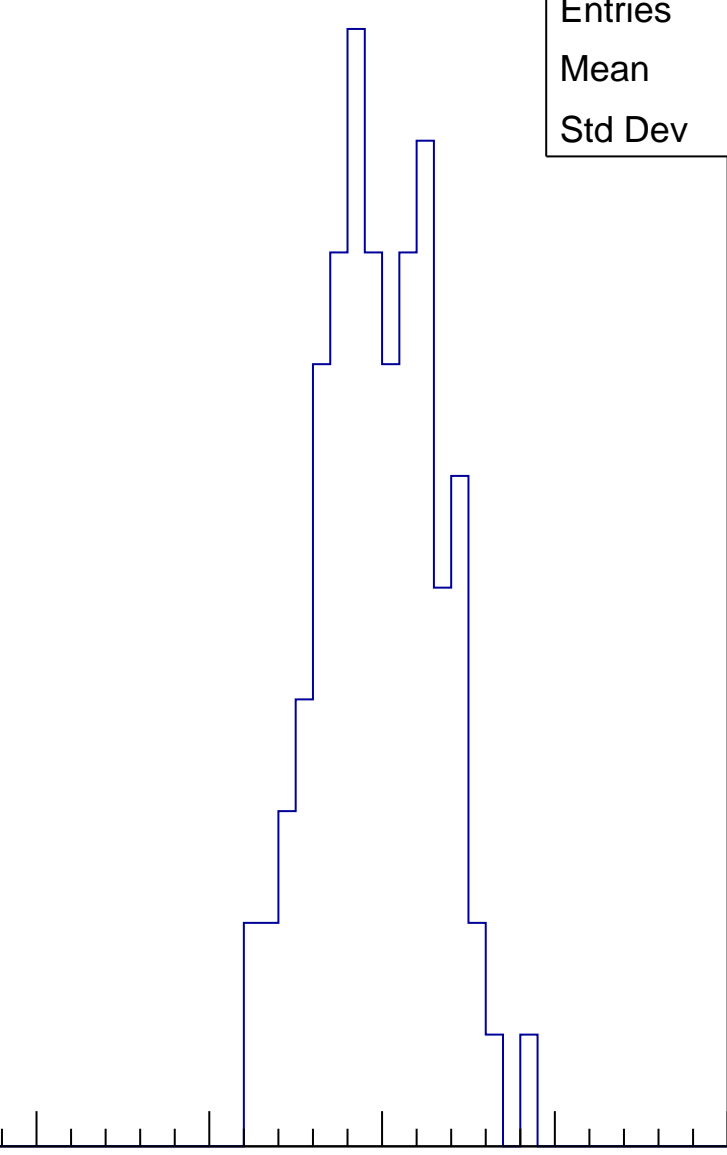
40

50

60

ampl

70

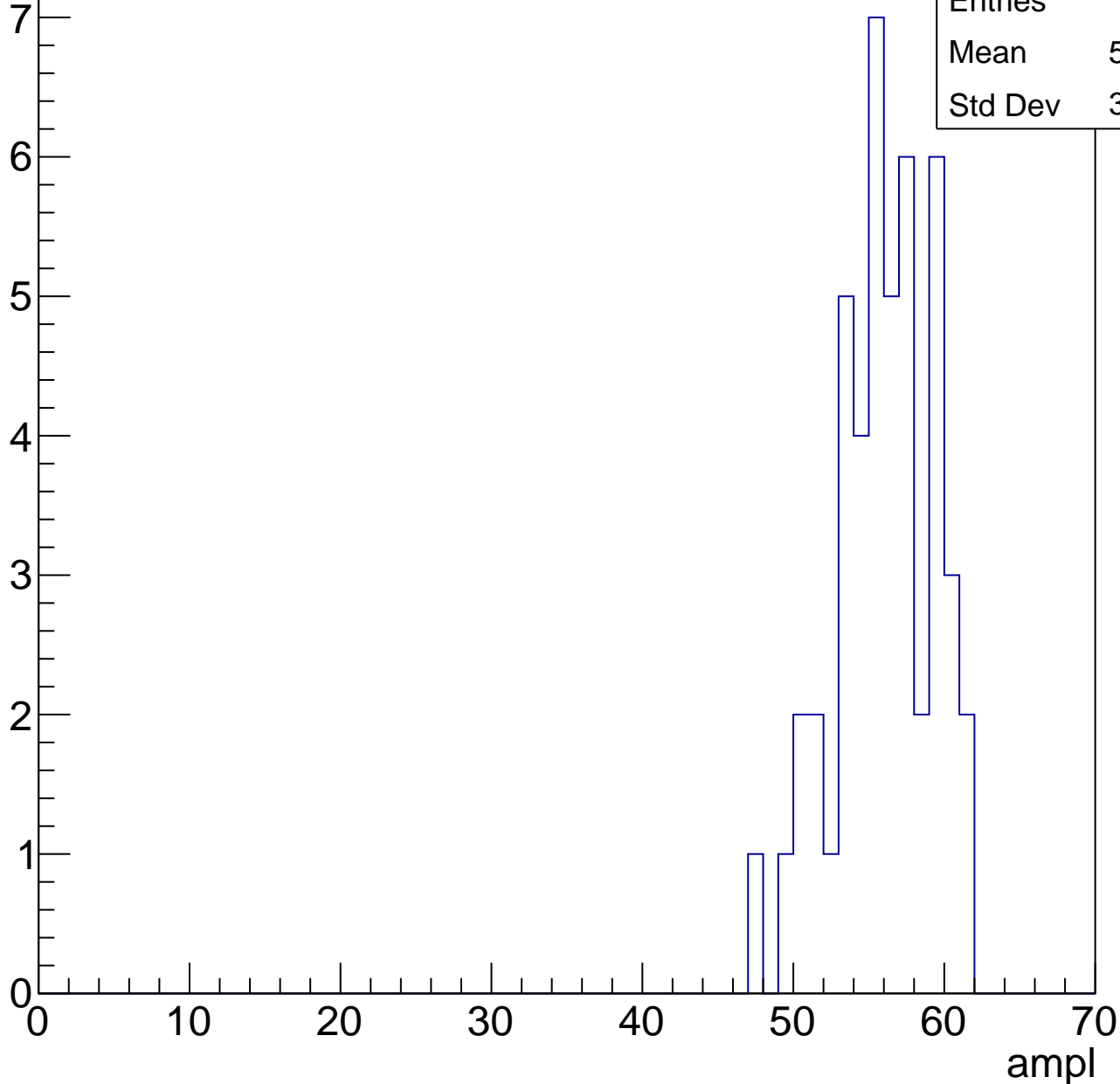


B1L100S, U11-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	55.53
Std Dev	3.234

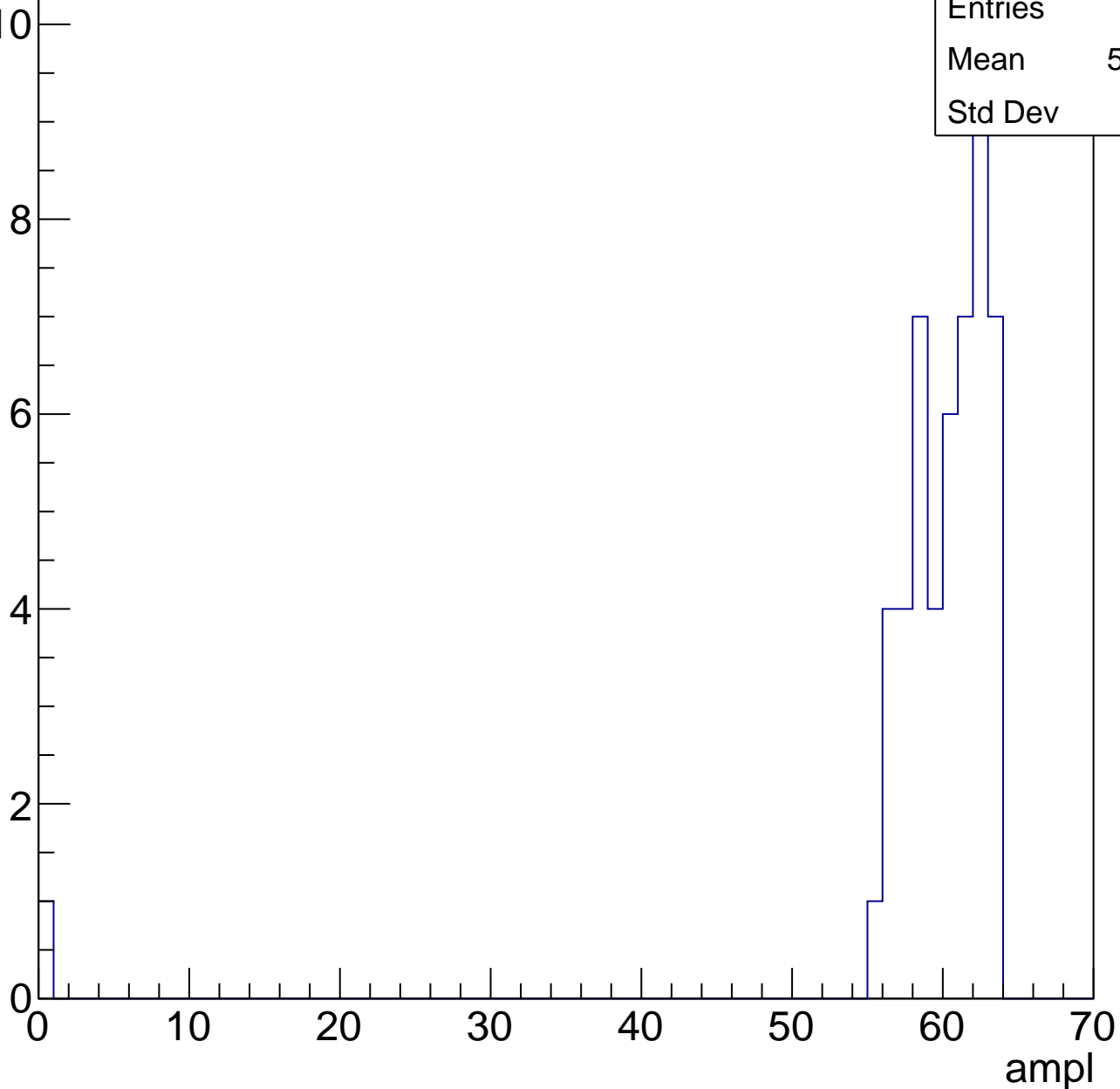


B1L100S, U11-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

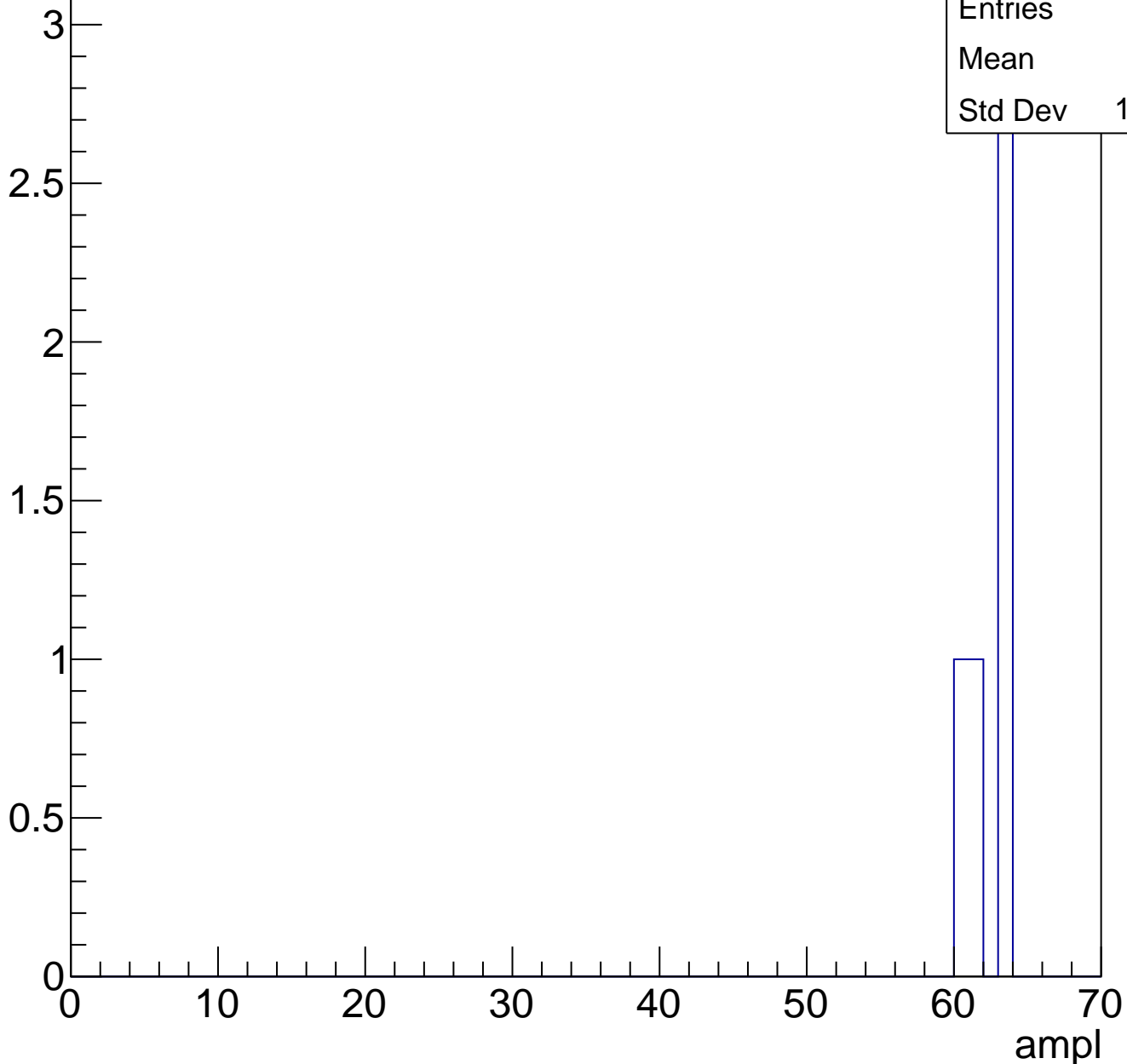
Entries	51
Mean	58.76
Std Dev	8.62



B1L100S, U11-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch23, adc0

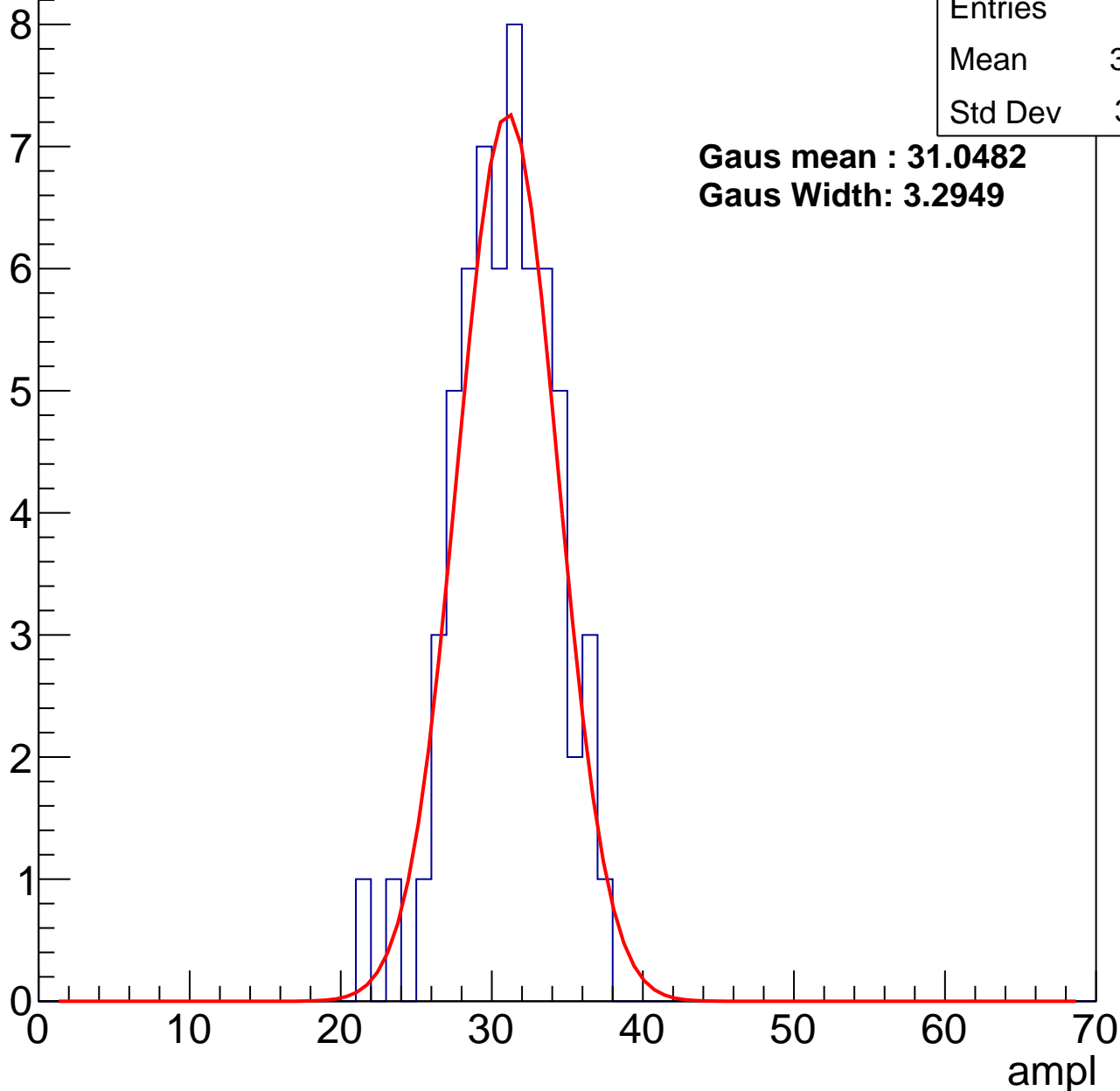
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	30.43
Std Dev	3.241

Gaus mean : 31.0482

Gaus Width: 3.2949

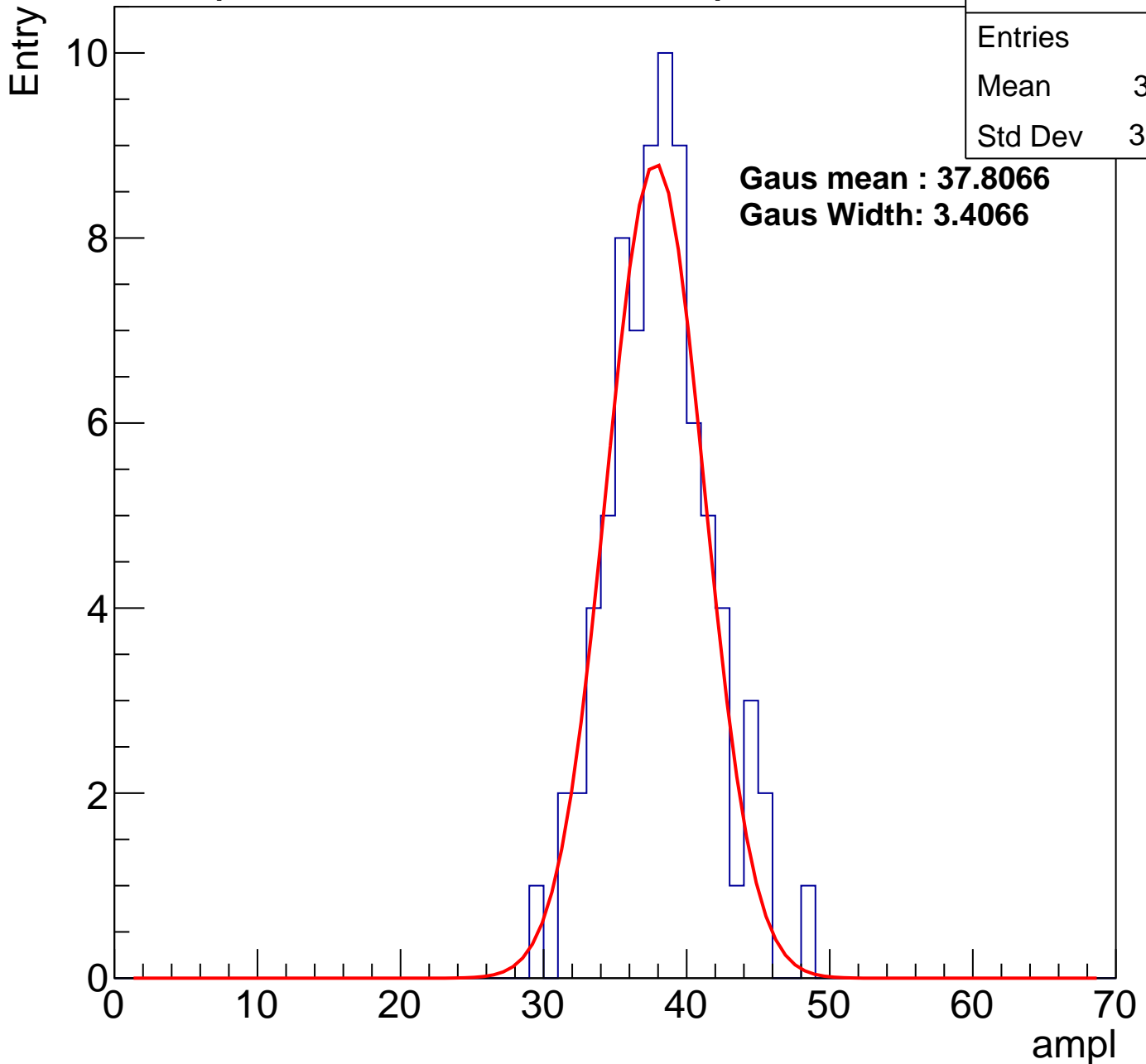


B1L100S, U11-ch23, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	37.71
Std Dev	3.569

Gaus mean : 37.8066
Gaus Width: 3.4066



B1L100S, U11-ch23, adc2

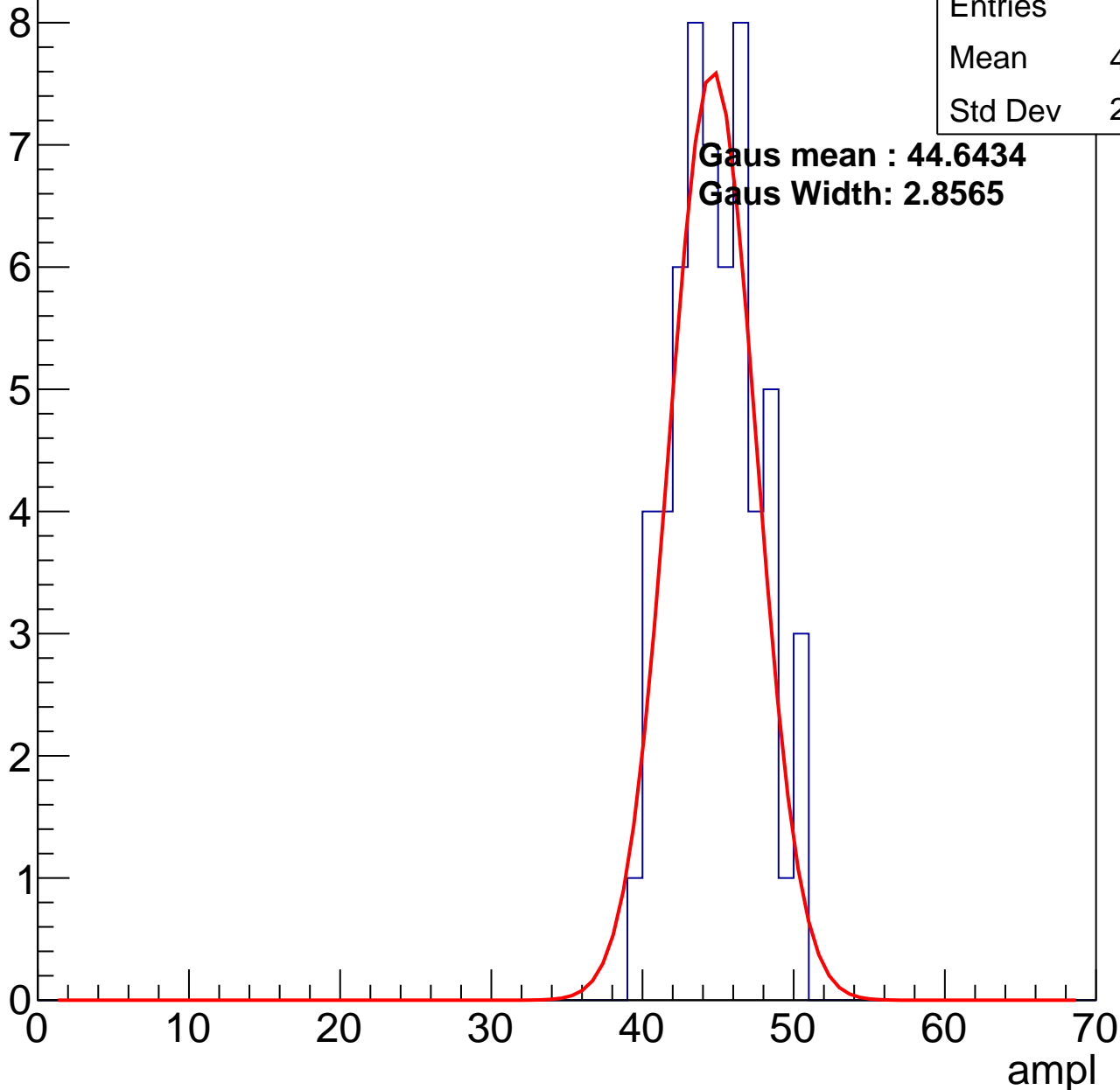
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.42
Std Dev	2.759

Gaus mean : 44.6434

Gaus Width: 2.8565

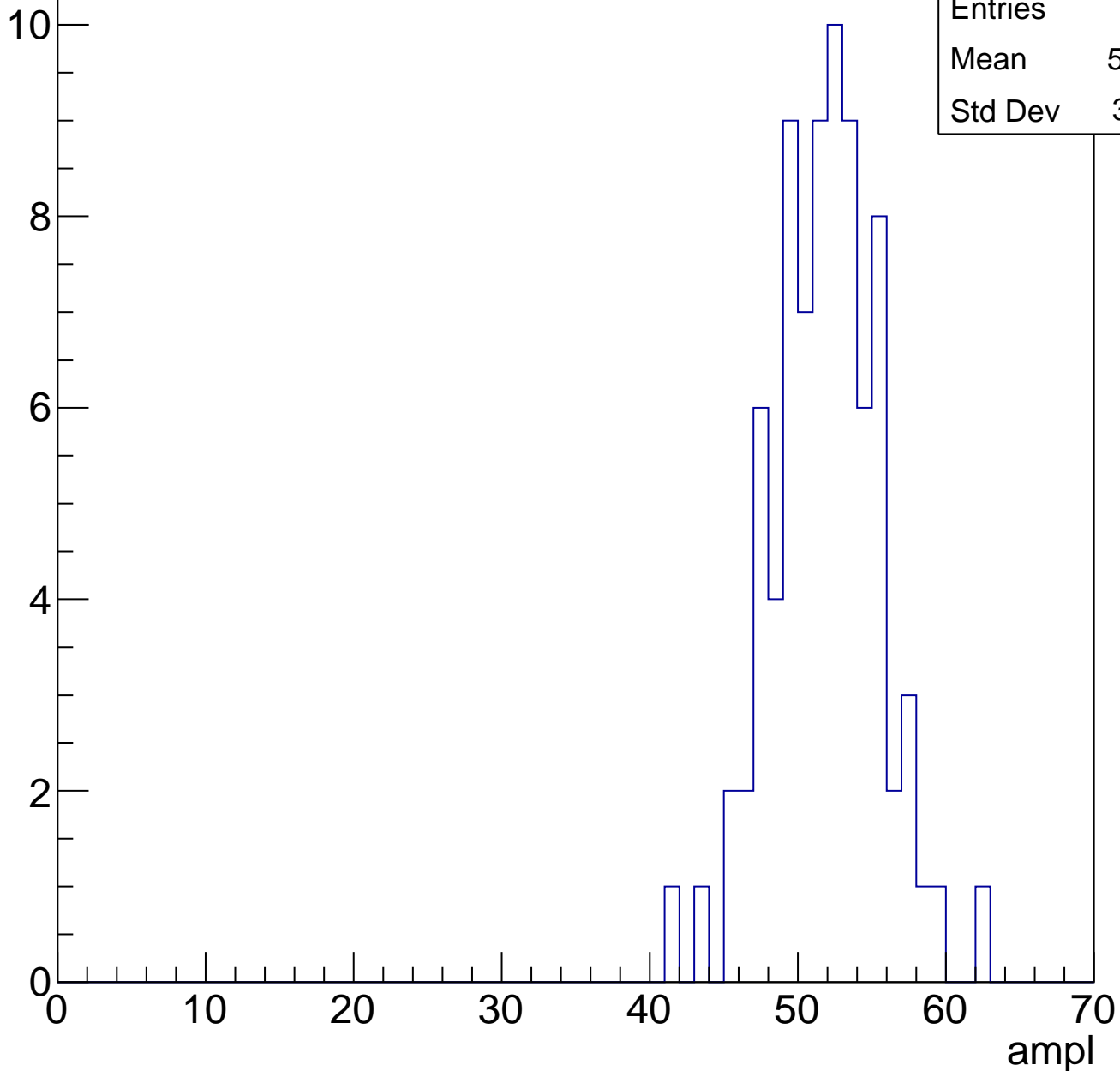


B1L100S, U11-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	51.38
Std Dev	3.611

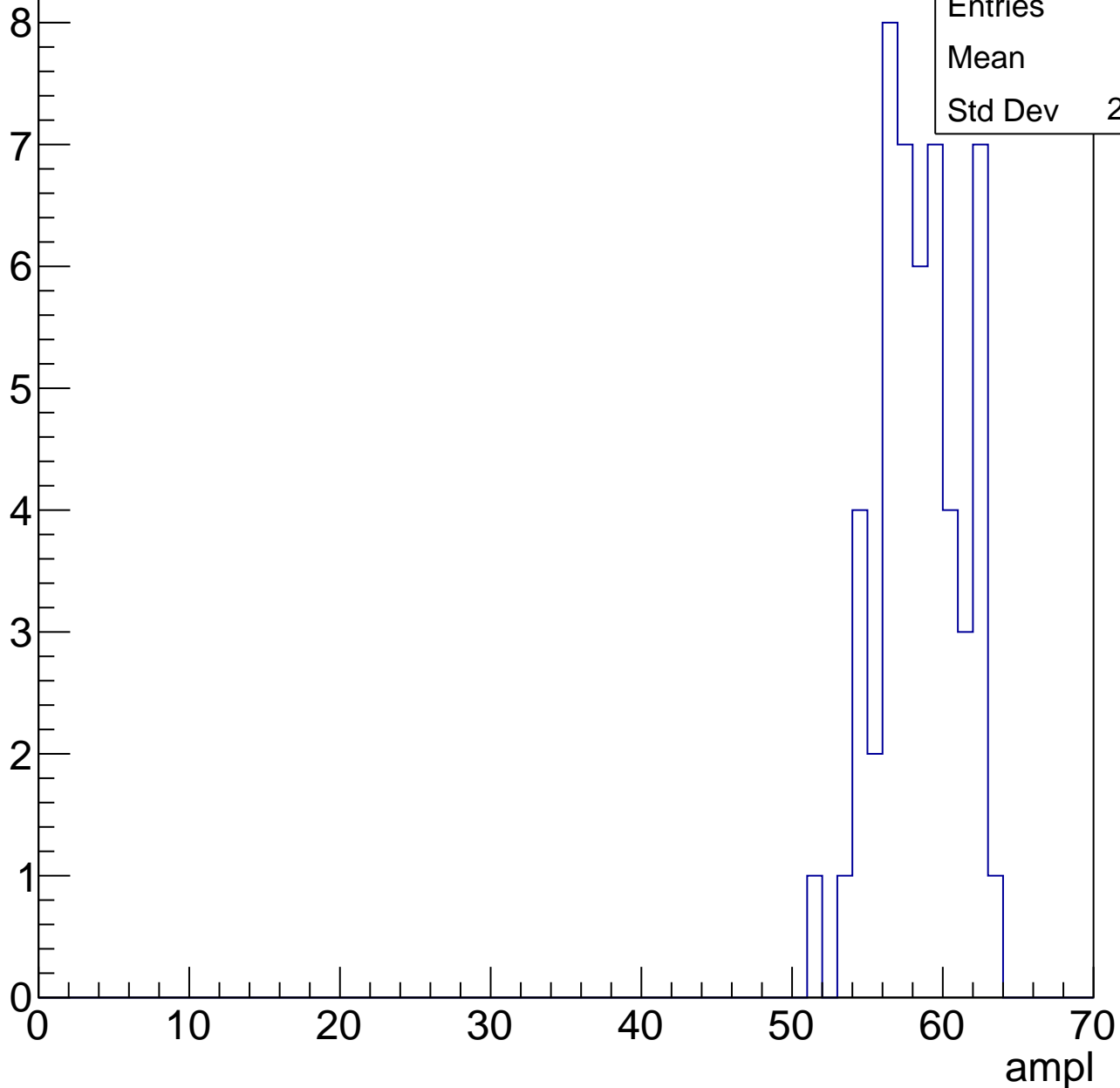
Entry



B1L100S, U11-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

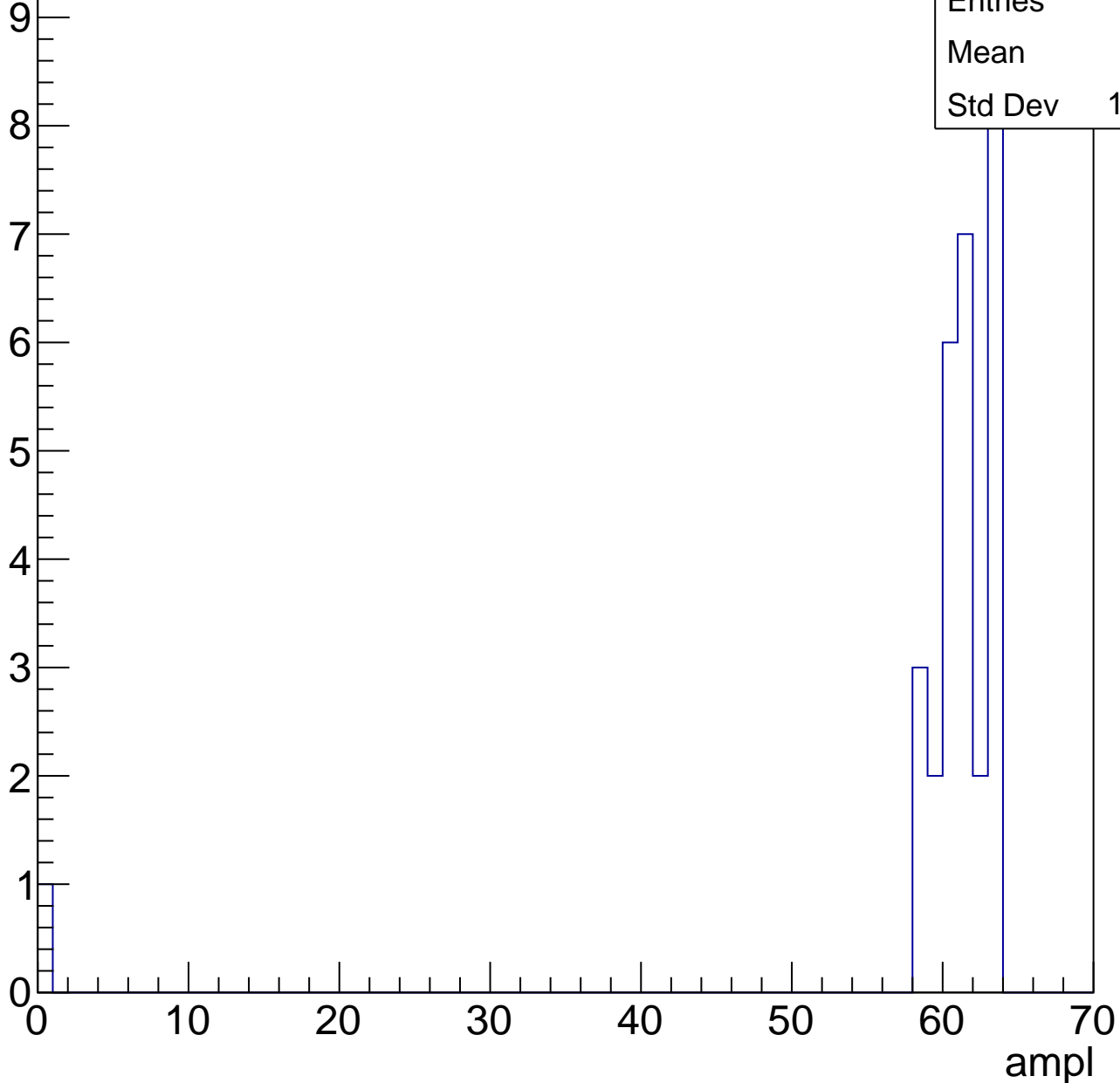


B1L100S, U11-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	59
Std Dev	11.08



B1L100S, U11-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L100S, U11-ch24, adc0

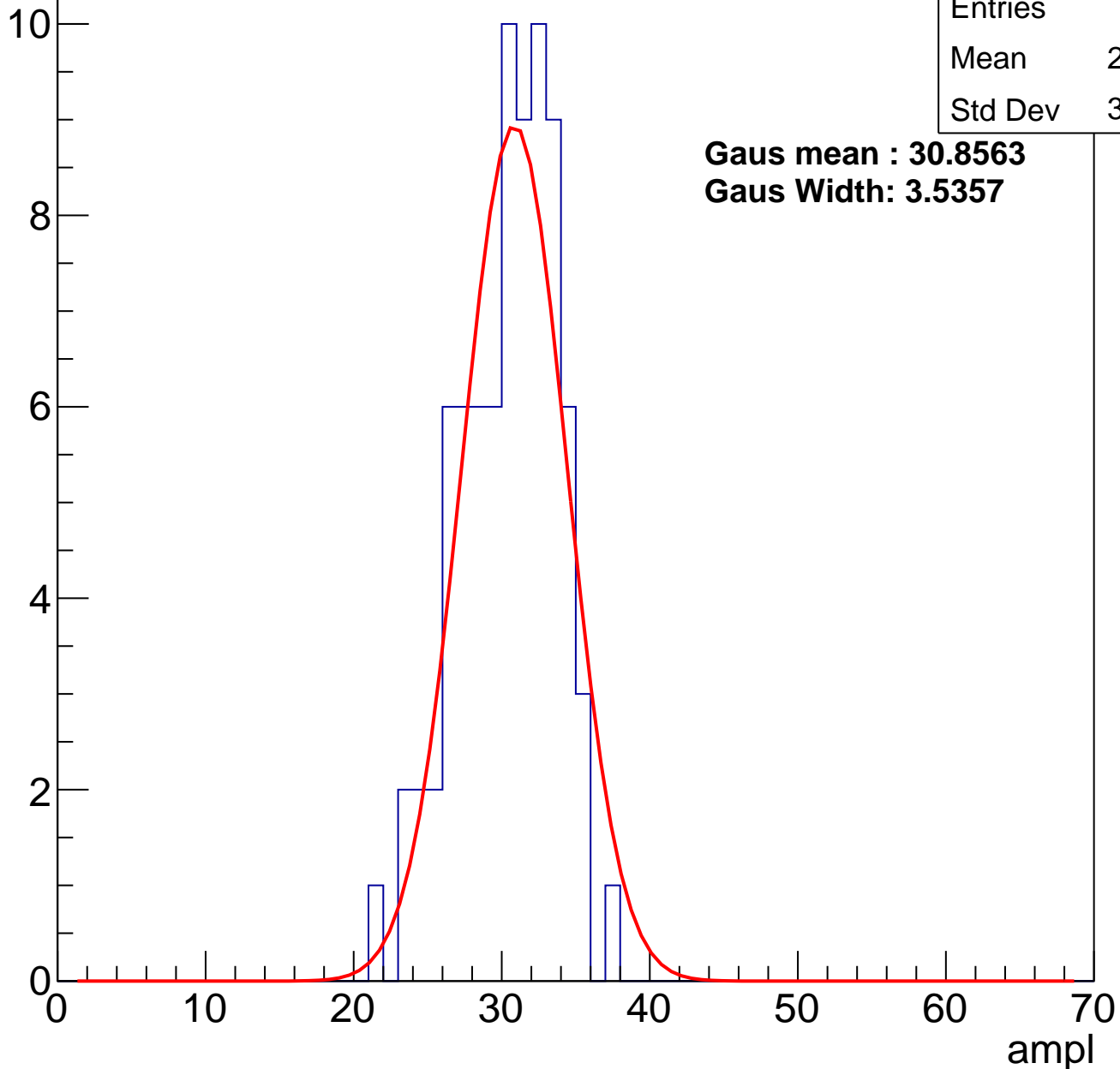
calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	29.96
Std Dev	3.243

Gaus mean : 30.8563

Gaus Width: 3.5357

Entry



B1L100S, U11-ch24, adc1

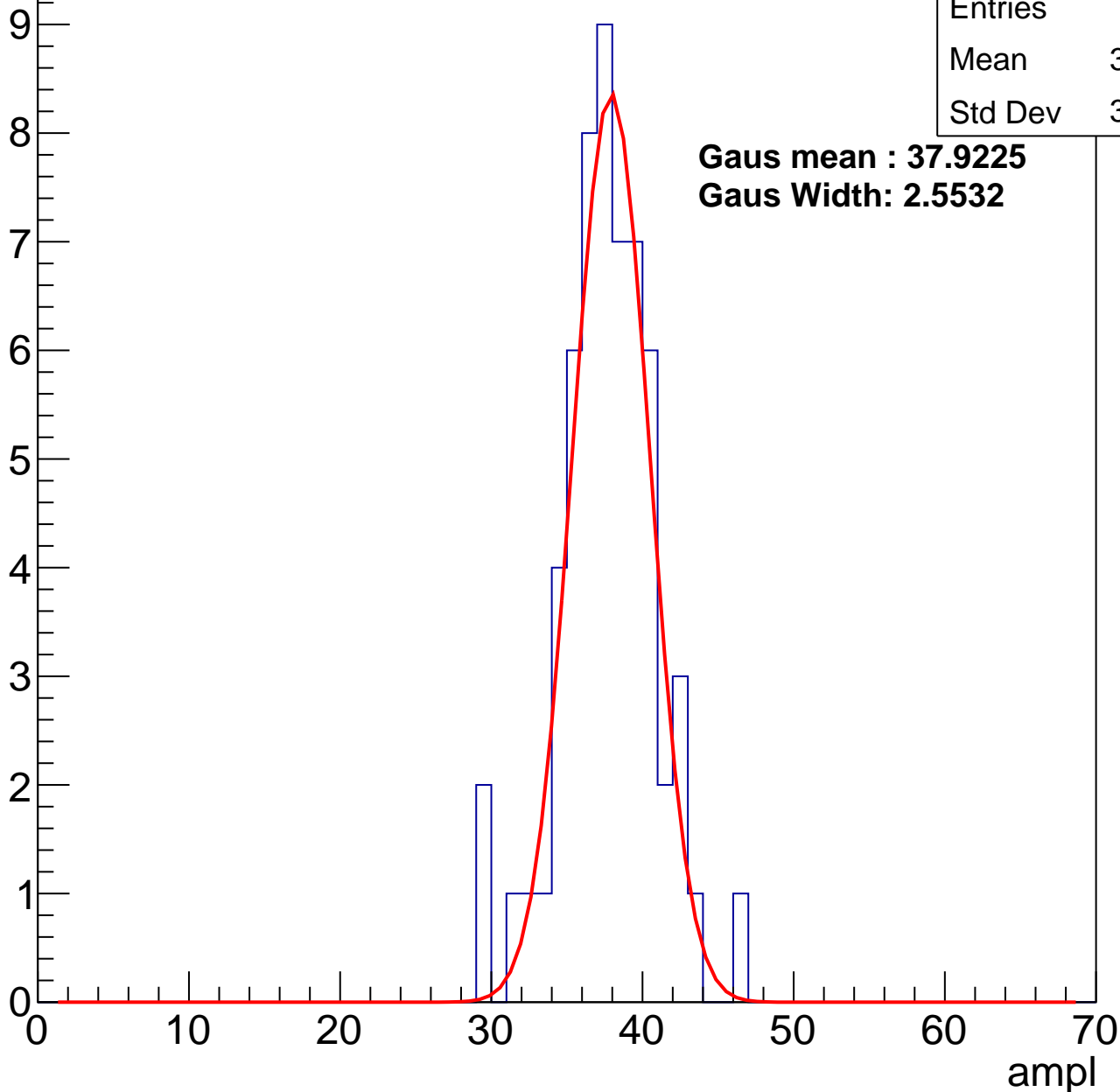
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	37.24
Std Dev	3.143

Gaus mean : 37.9225

Gaus Width: 2.5532



B1L100S, U11-ch24, adc2

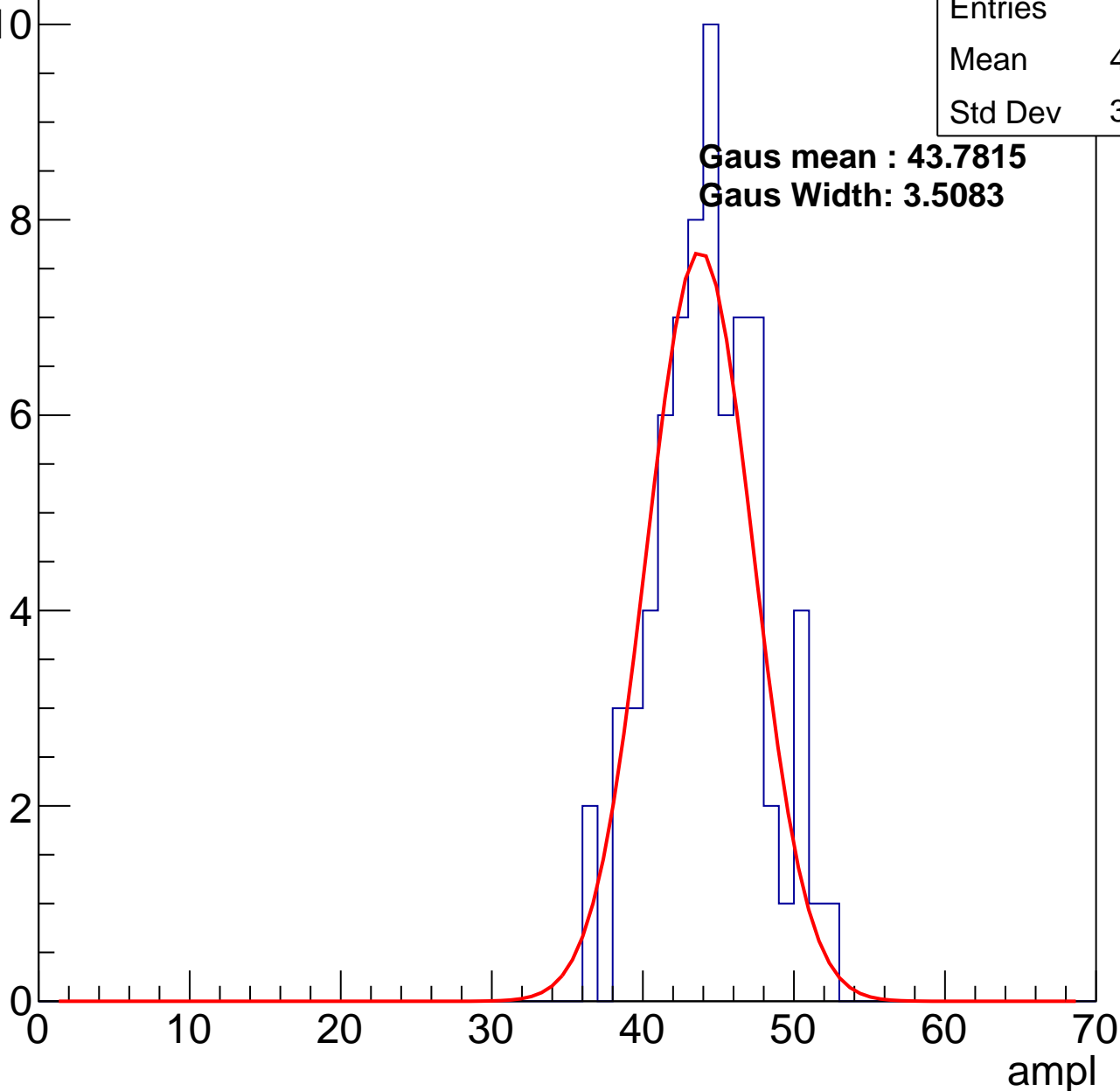
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.83
Std Dev	3.484

Gaus mean : 43.7815

Gaus Width: 3.5083

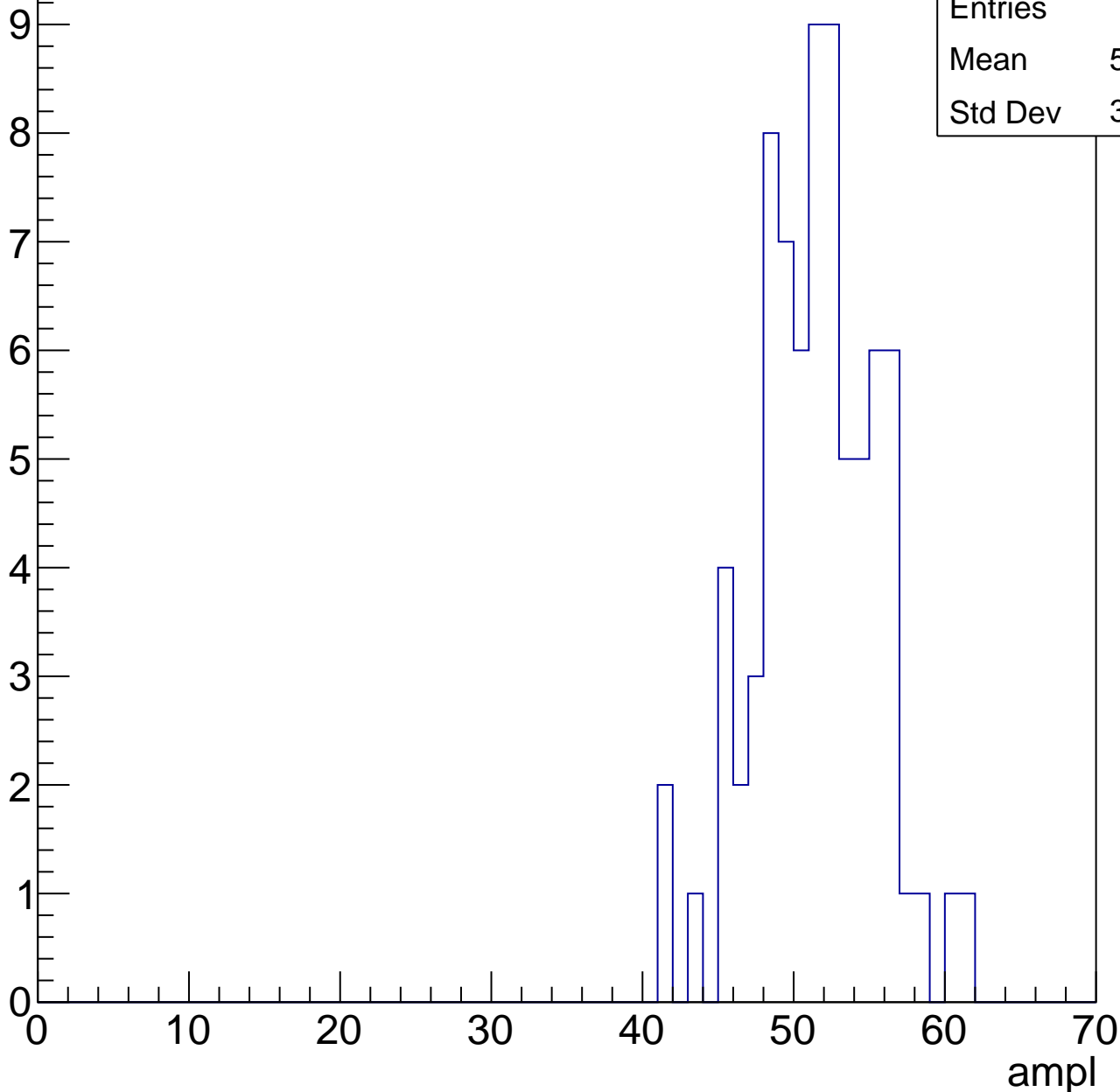


B1L100S, U11-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	51.03
Std Dev	3.944

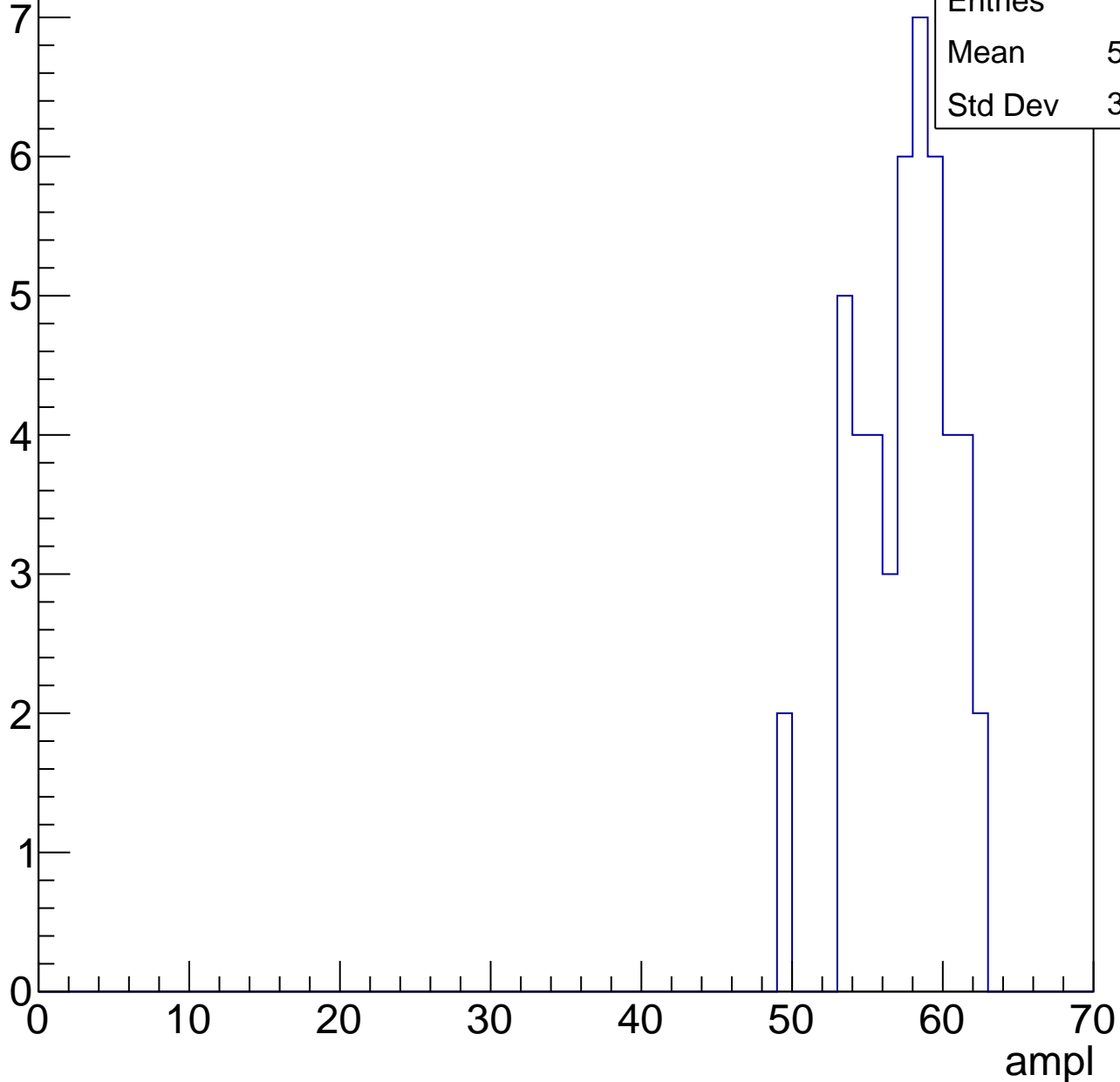


B1L100S, U11-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	56.96
Std Dev	3.073

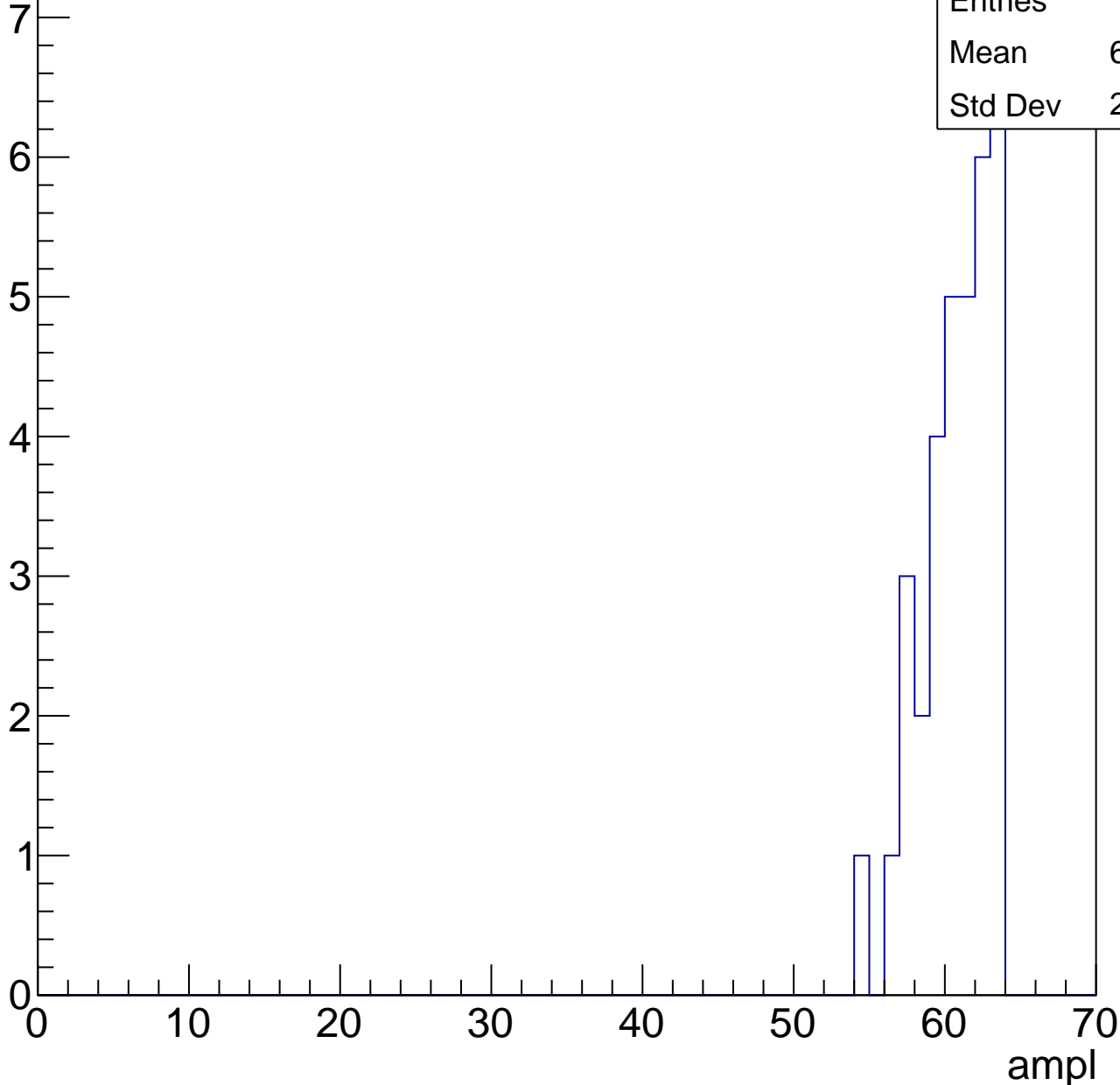


B1L100S, U11-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	60.32
Std Dev	2.297



B1L100S, U11-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch25, adc0

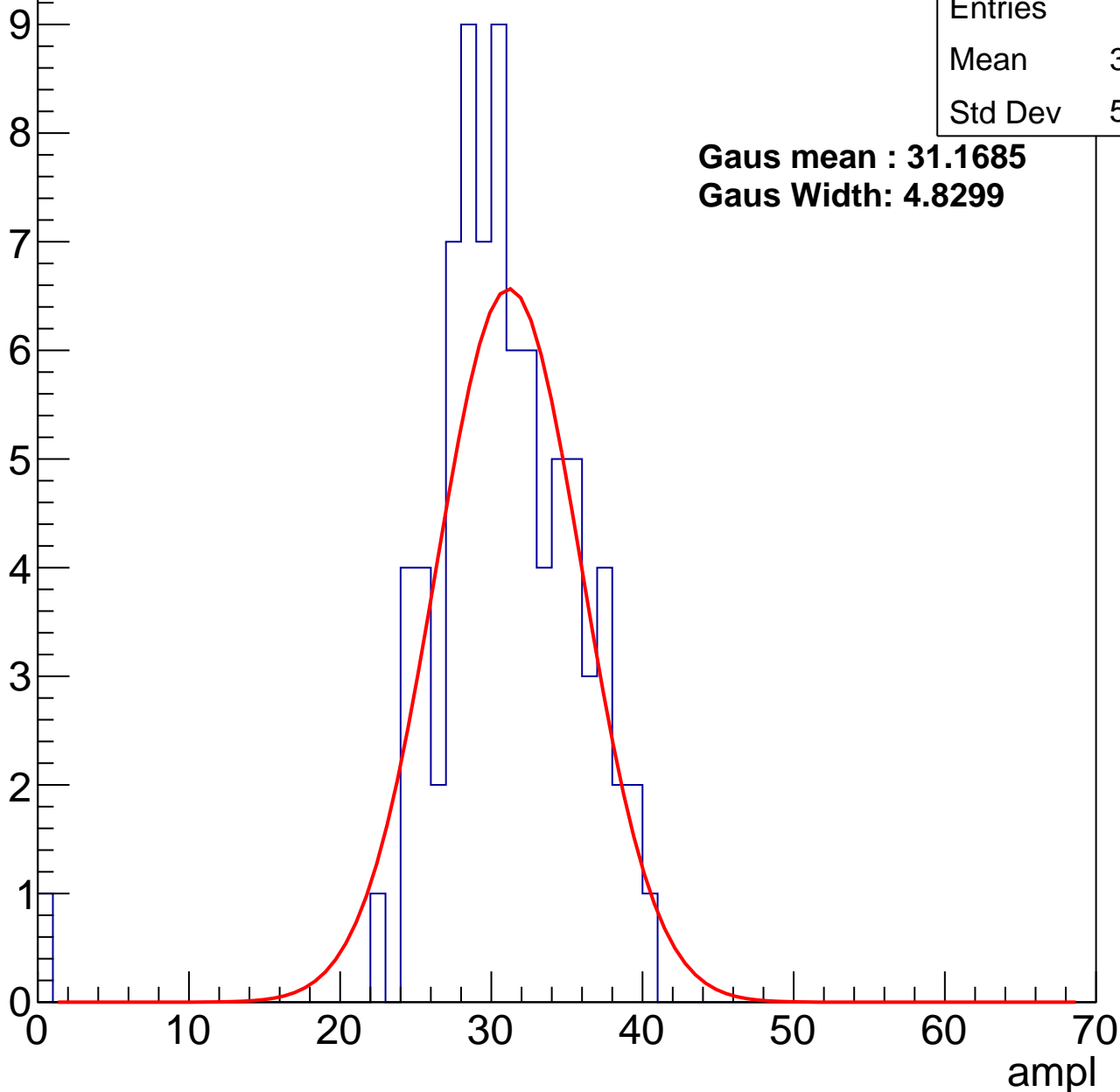
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	30.35
Std Dev	5.297

Gaus mean : 31.1685

Gaus Width: 4.8299



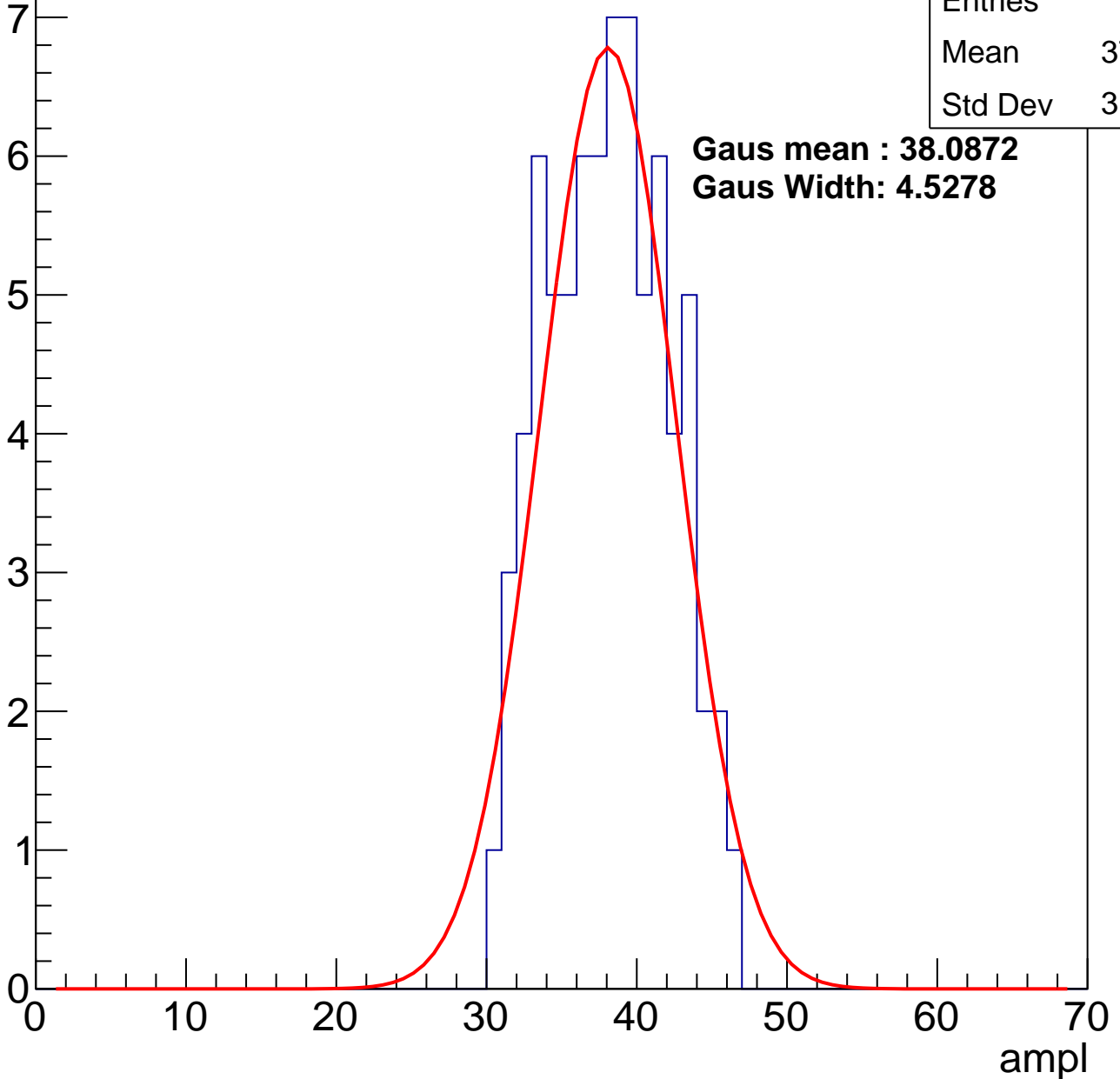
B1L100S, U11-ch25, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.65
Std Dev	3.924

Gaus mean : 38.0872
Gaus Width: 4.5278



B1L100S, U11-ch25, adc2

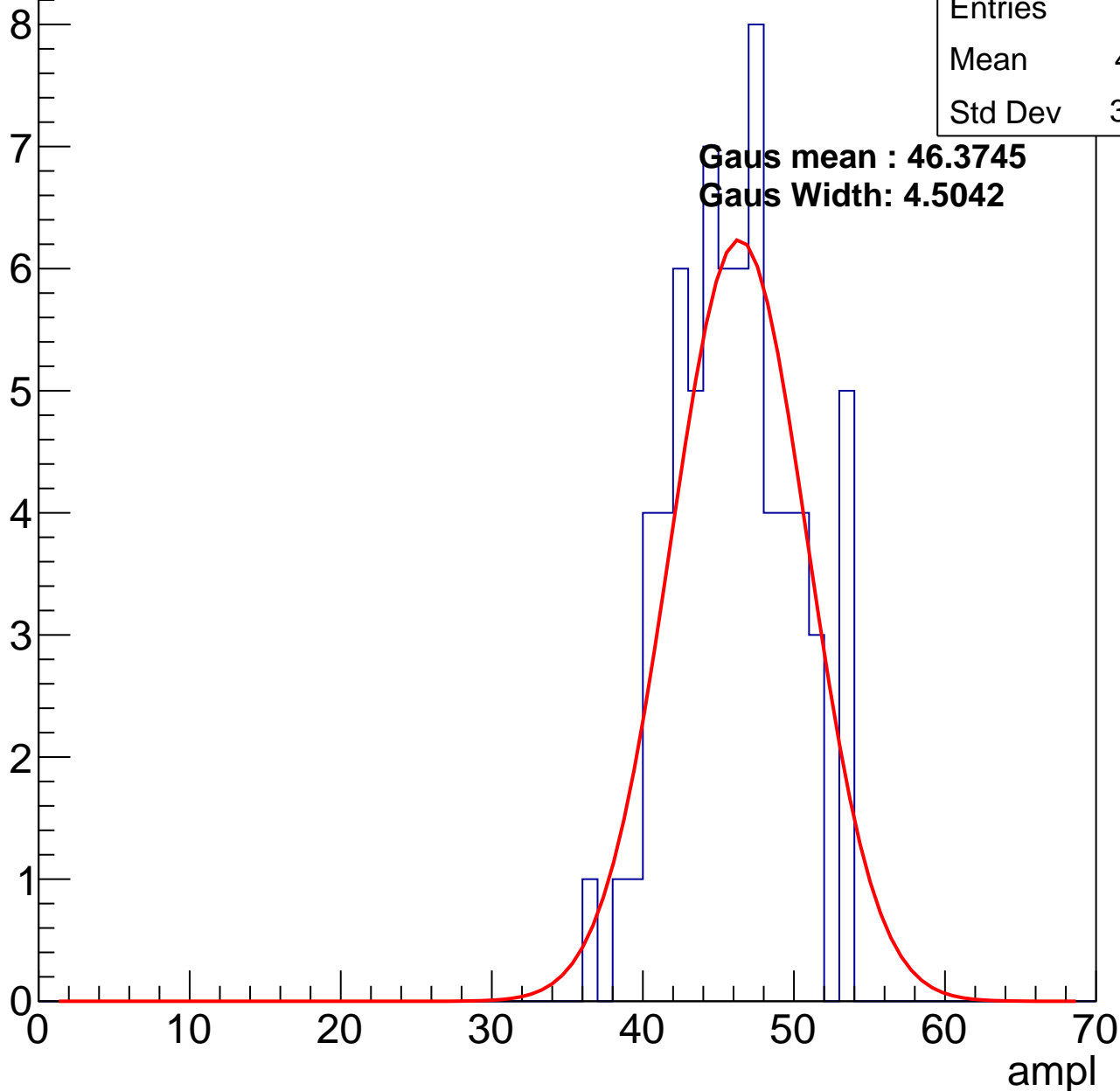
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	45.51
Std Dev	3.918

Gaus mean : 46.3745

Gaus Width: 4.5042

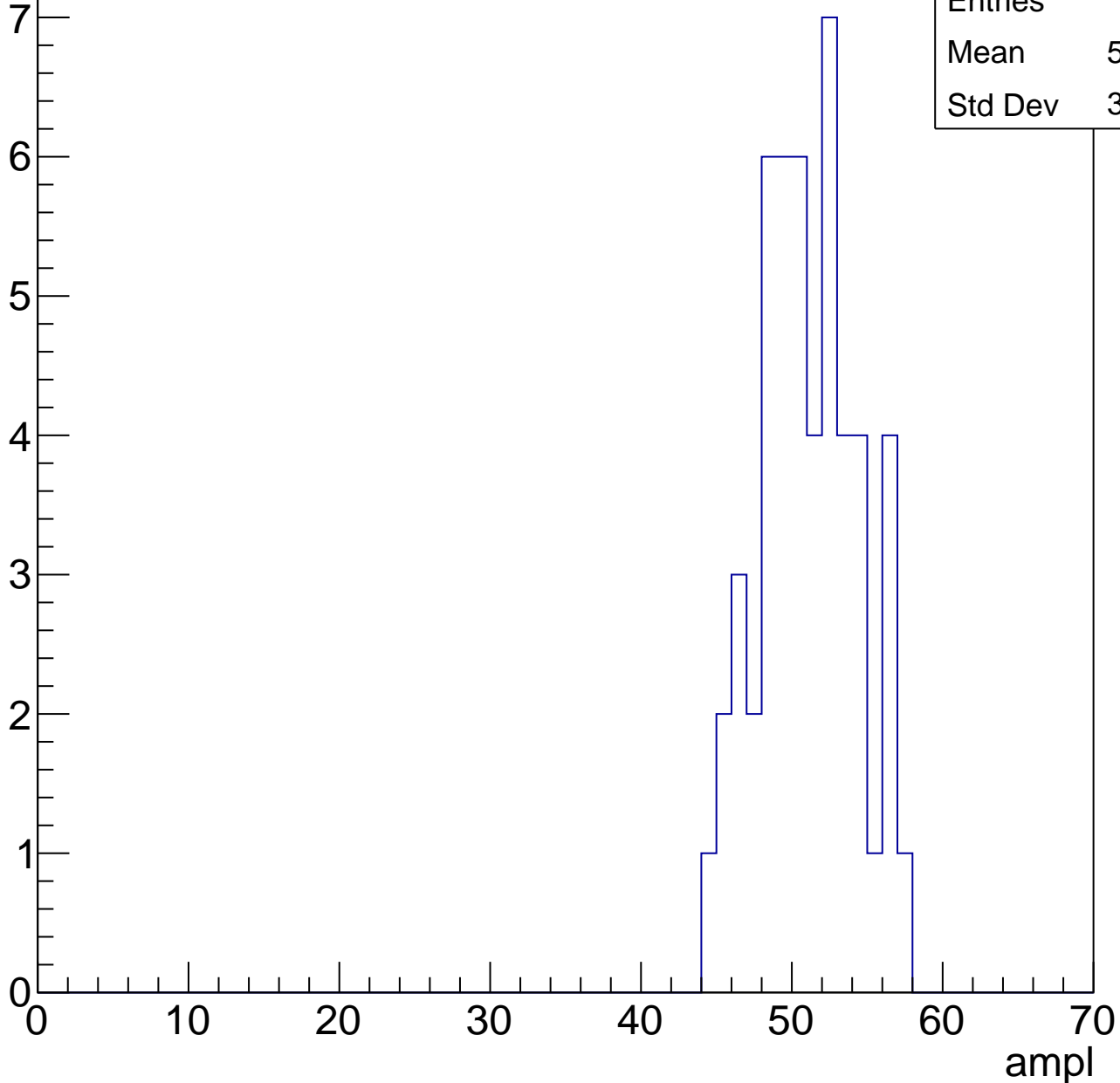


B1L100S, U11-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	50.59
Std Dev	3.176

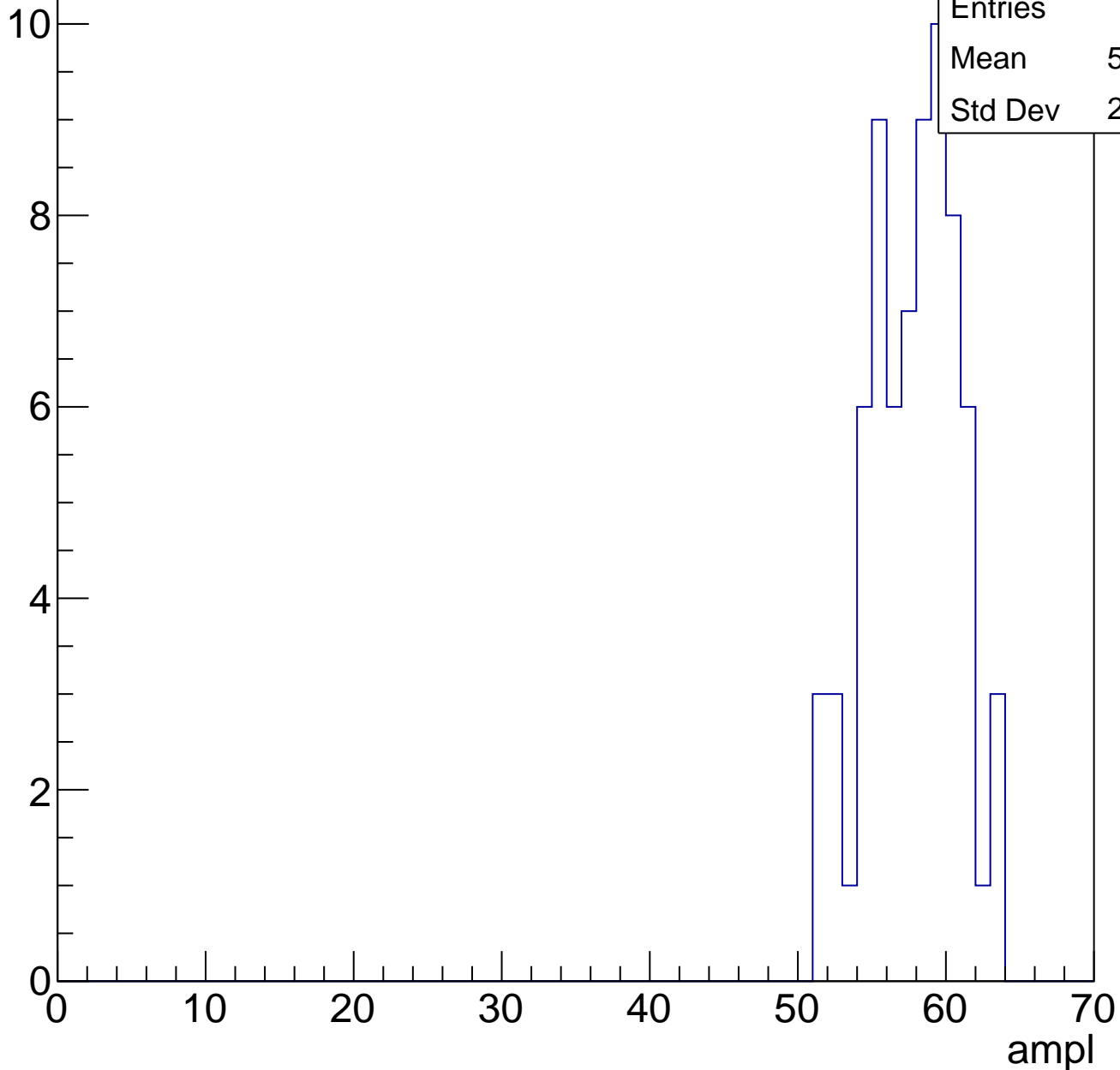


B1L100S, U11-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	57.29
Std Dev	2.979

Entry

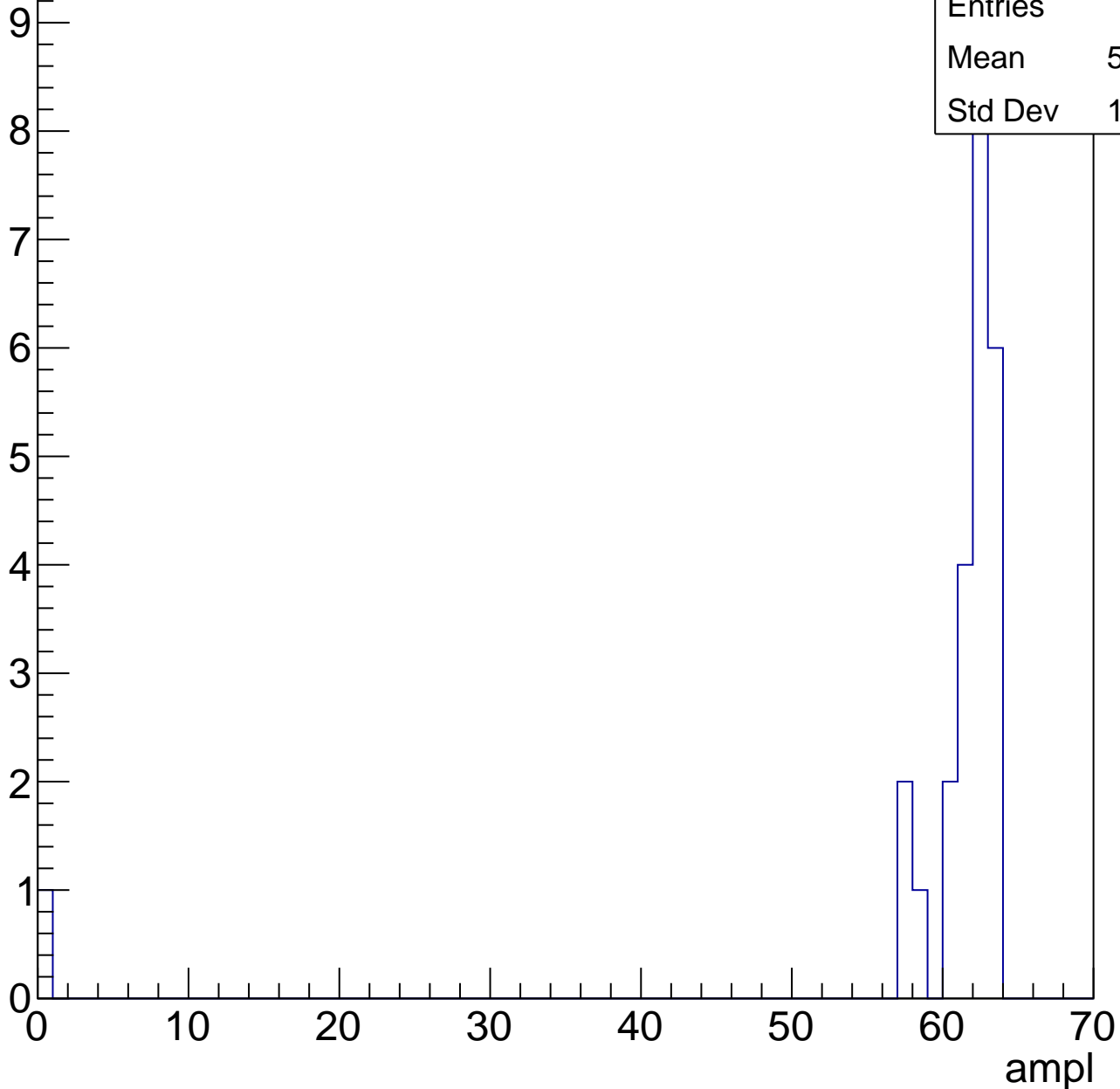


B1L100S, U11-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	58.88
Std Dev	12.14



B1L100S, U11-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch26, adc0

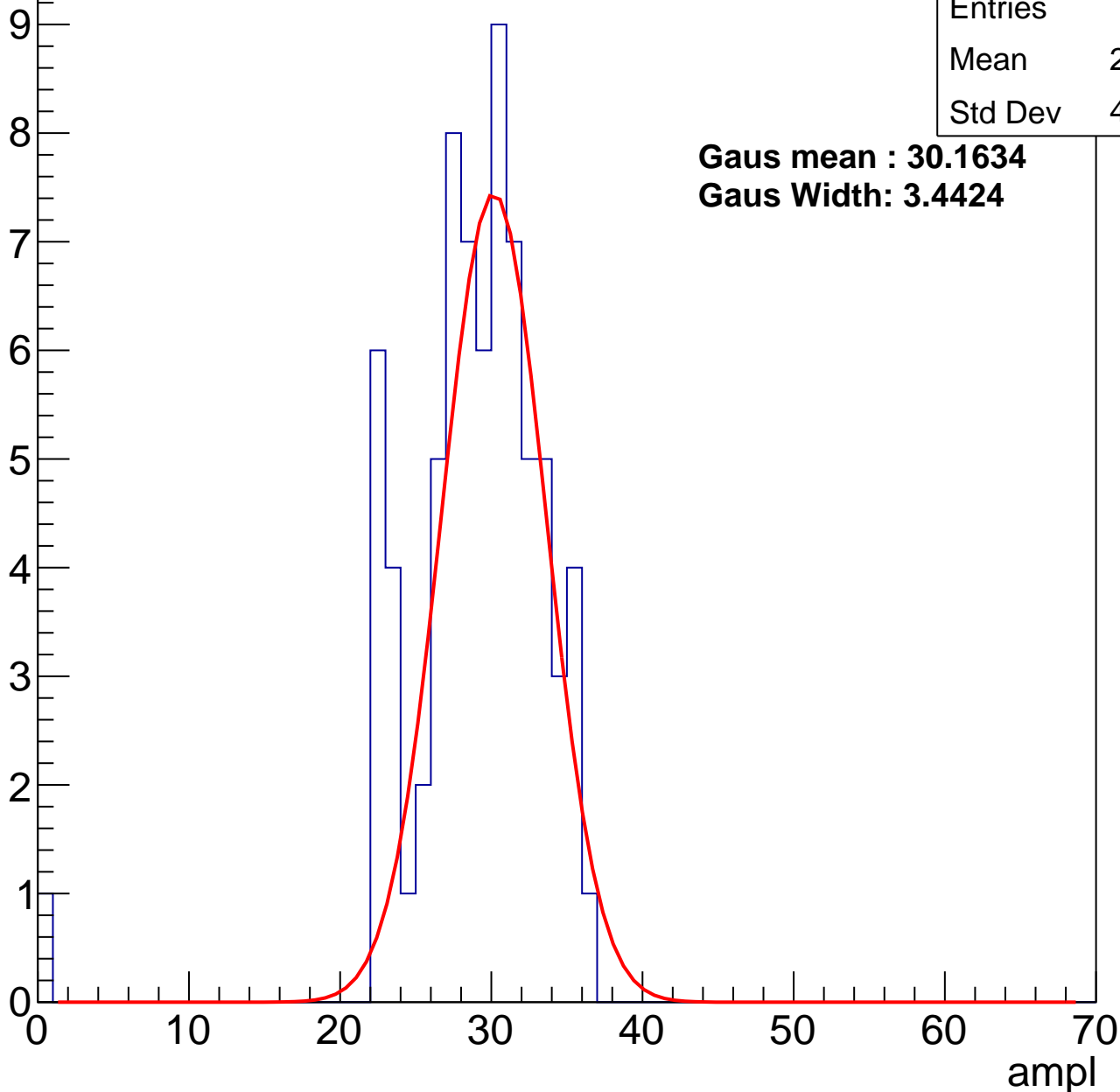
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.43
Std Dev	4.968

Gaus mean : 30.1634

Gaus Width: 3.4424



B1L100S, U11-ch26, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	36.16
Std Dev	3.496

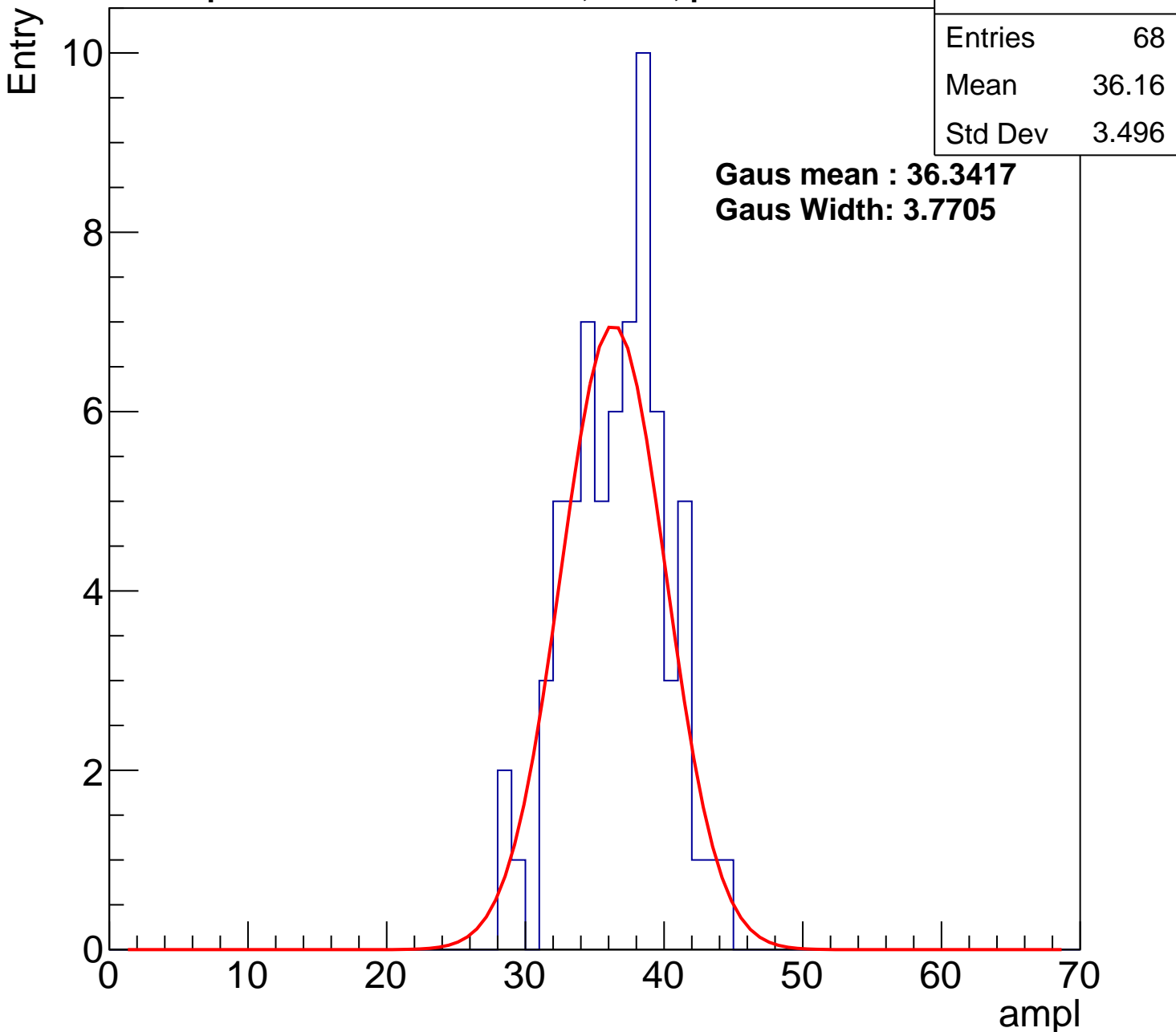
Gaus mean : 36.3417
Gaus Width: 3.7705

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch26, adc2

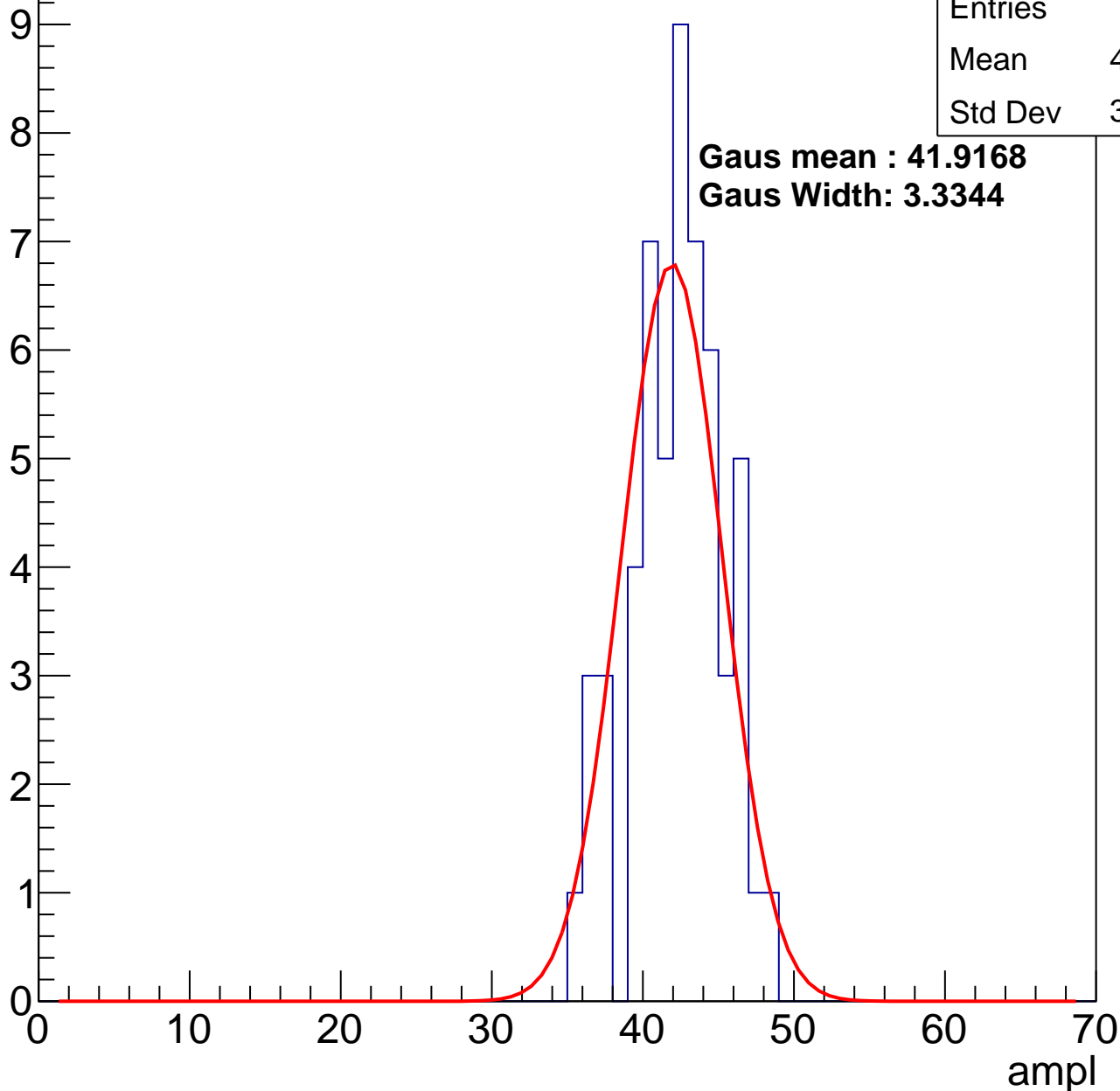
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	41.78
Std Dev	3.007

Gaus mean : 41.9168

Gaus Width: 3.3344



B1L100S, U11-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

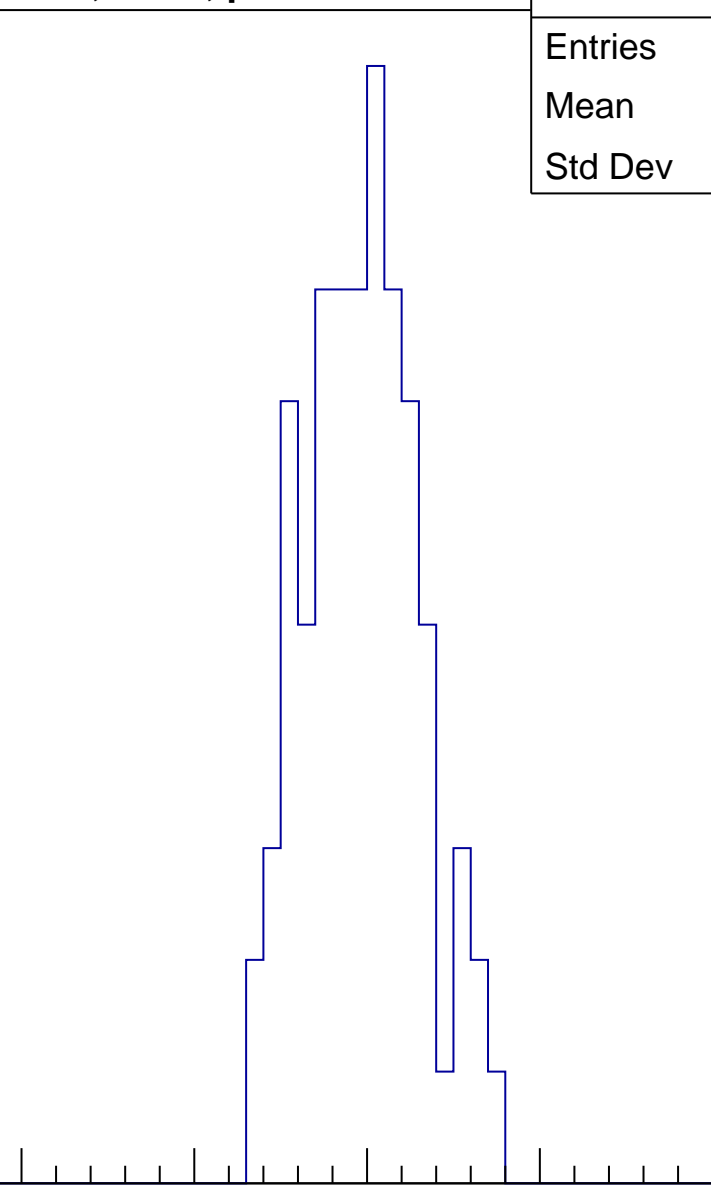
Entries	78
Mean	49.23
Std Dev	3.242

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

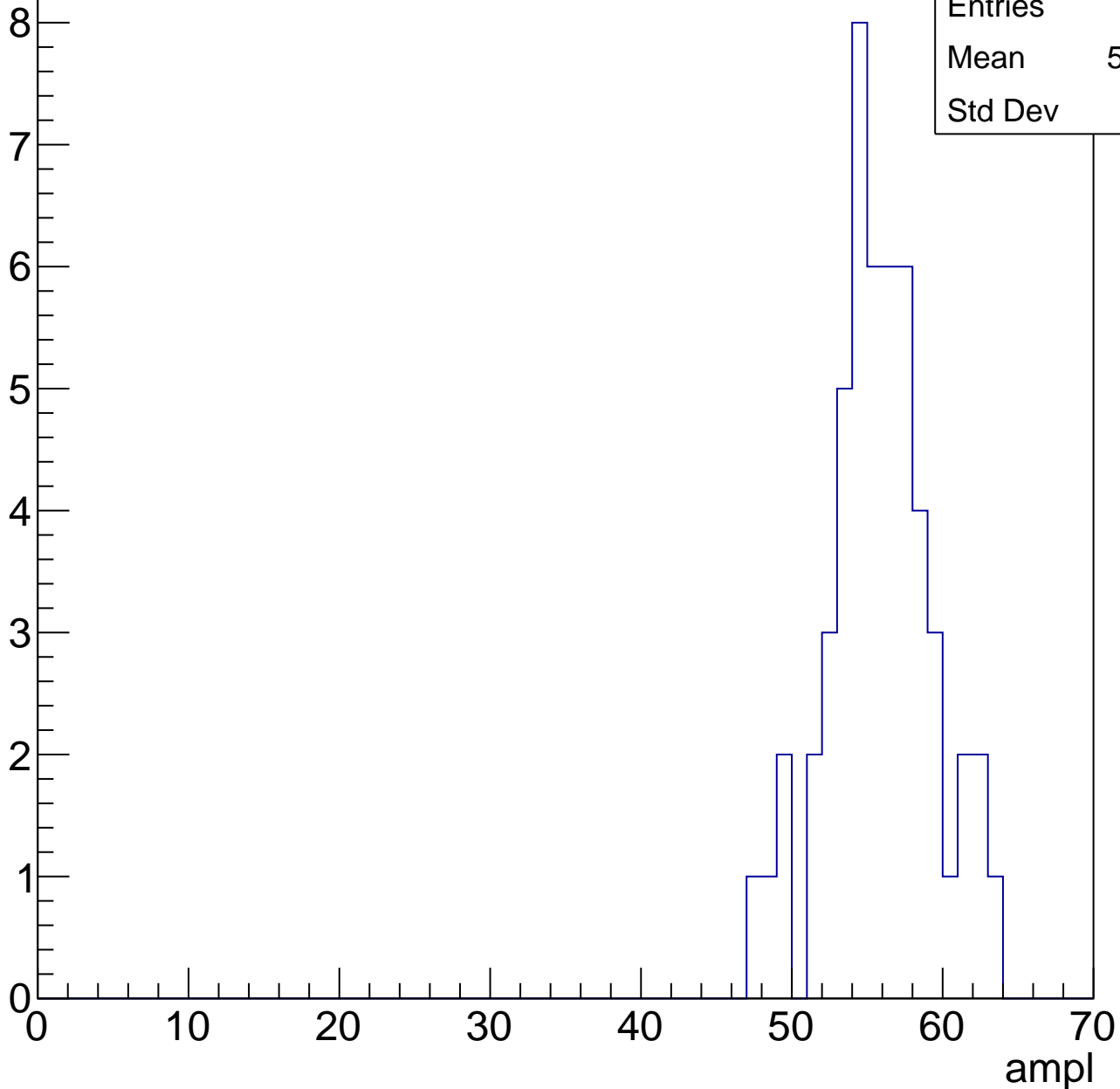


B1L100S, U11-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

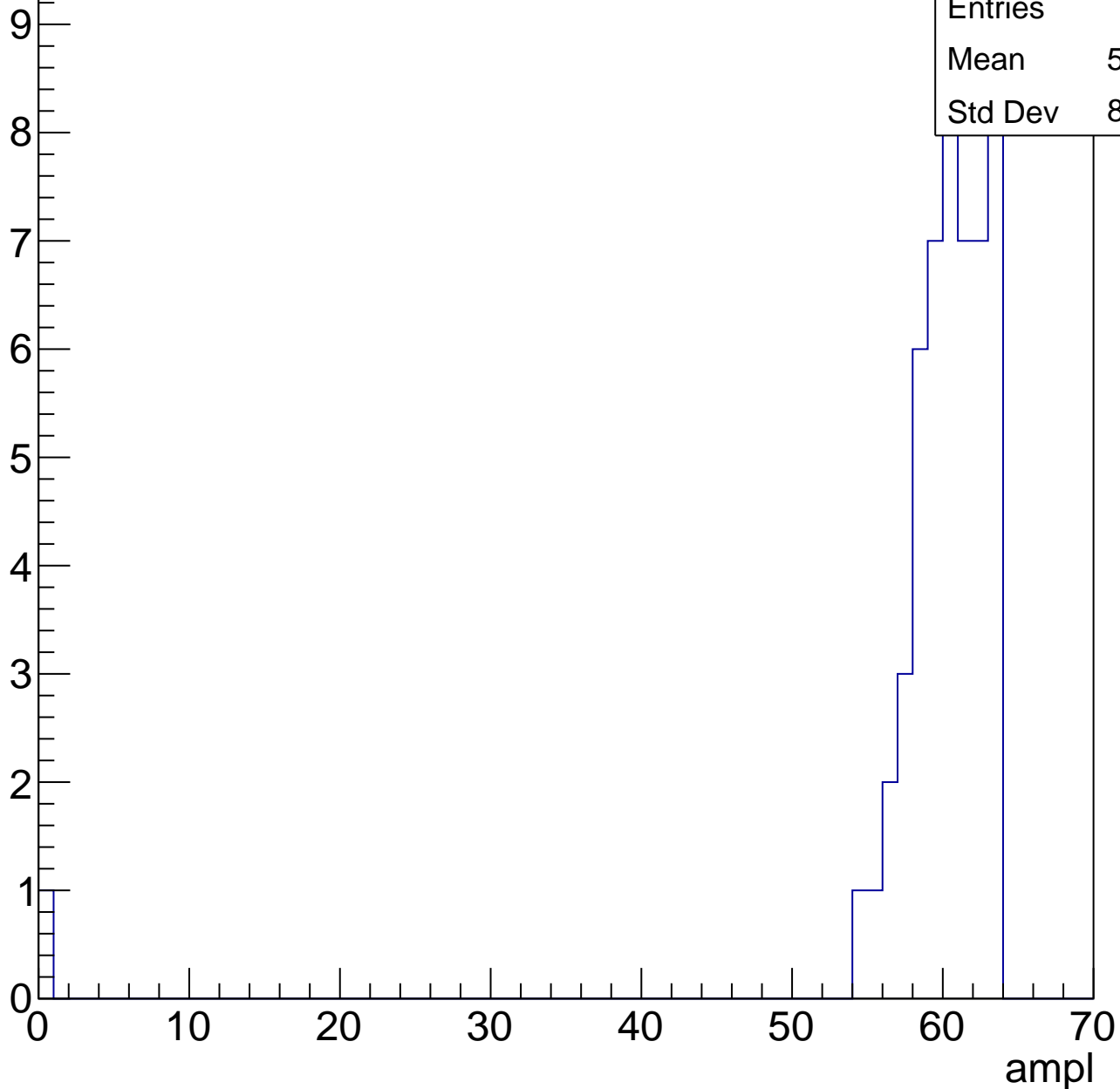
Entries	53
Mean	55.36
Std Dev	3.47



B1L100S, U11-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch27, adc0

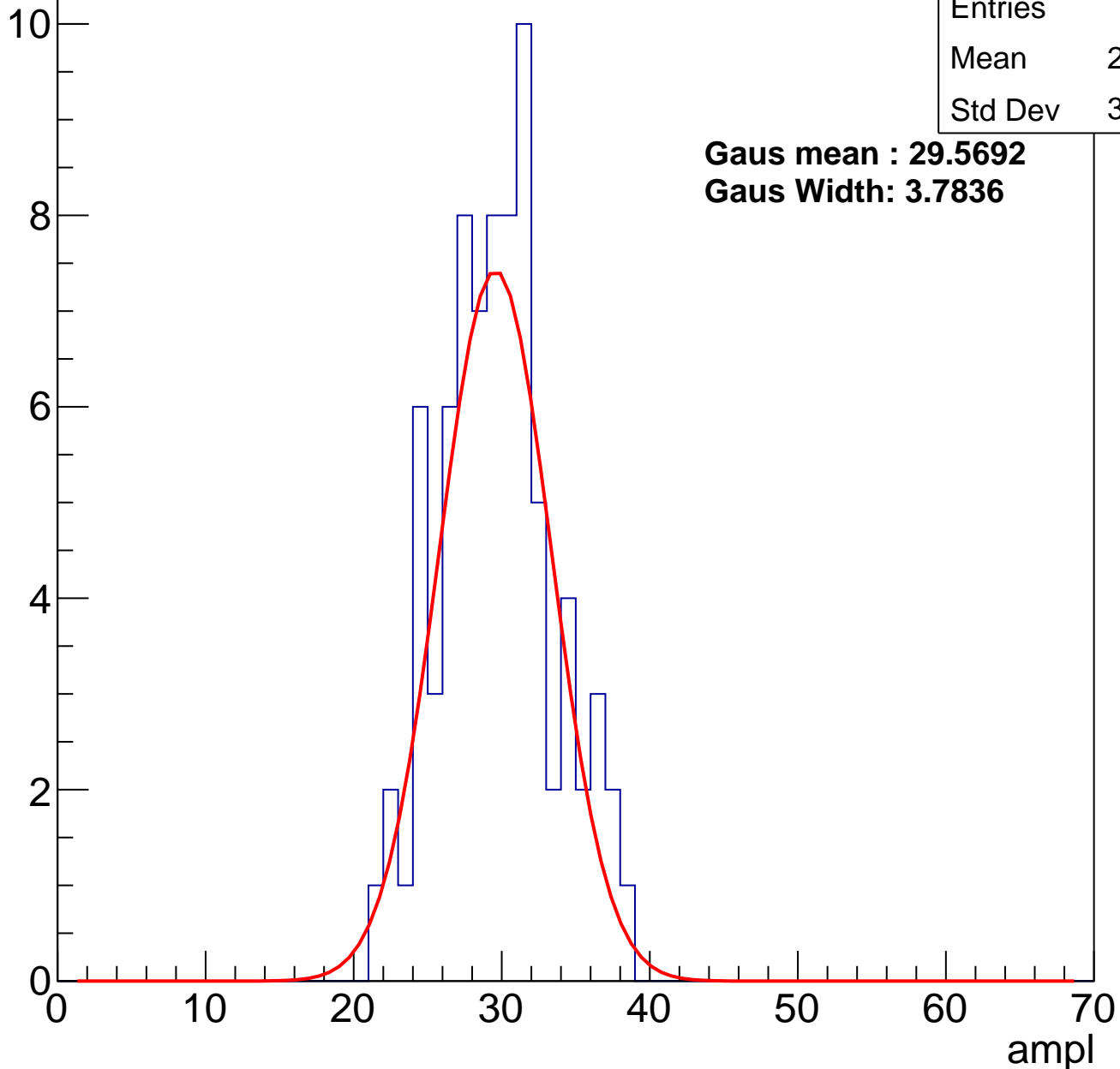
calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	29.23
Std Dev	3.795

Gaus mean : 29.5692

Gaus Width: 3.7836

Entry



B1L100S, U11-ch27, adc1

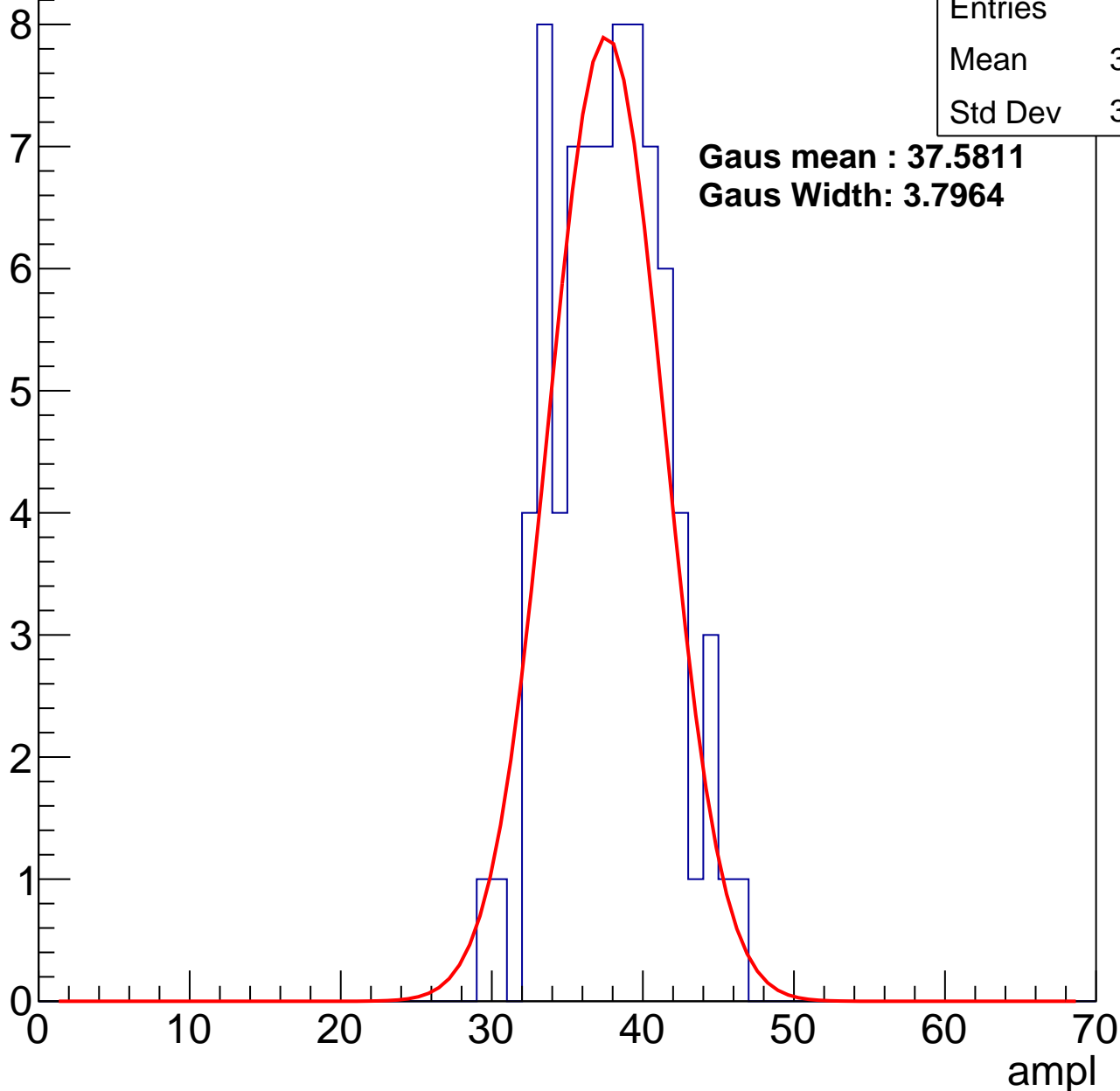
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	37.42
Std Dev	3.625

Gaus mean : 37.5811

Gaus Width: 3.7964

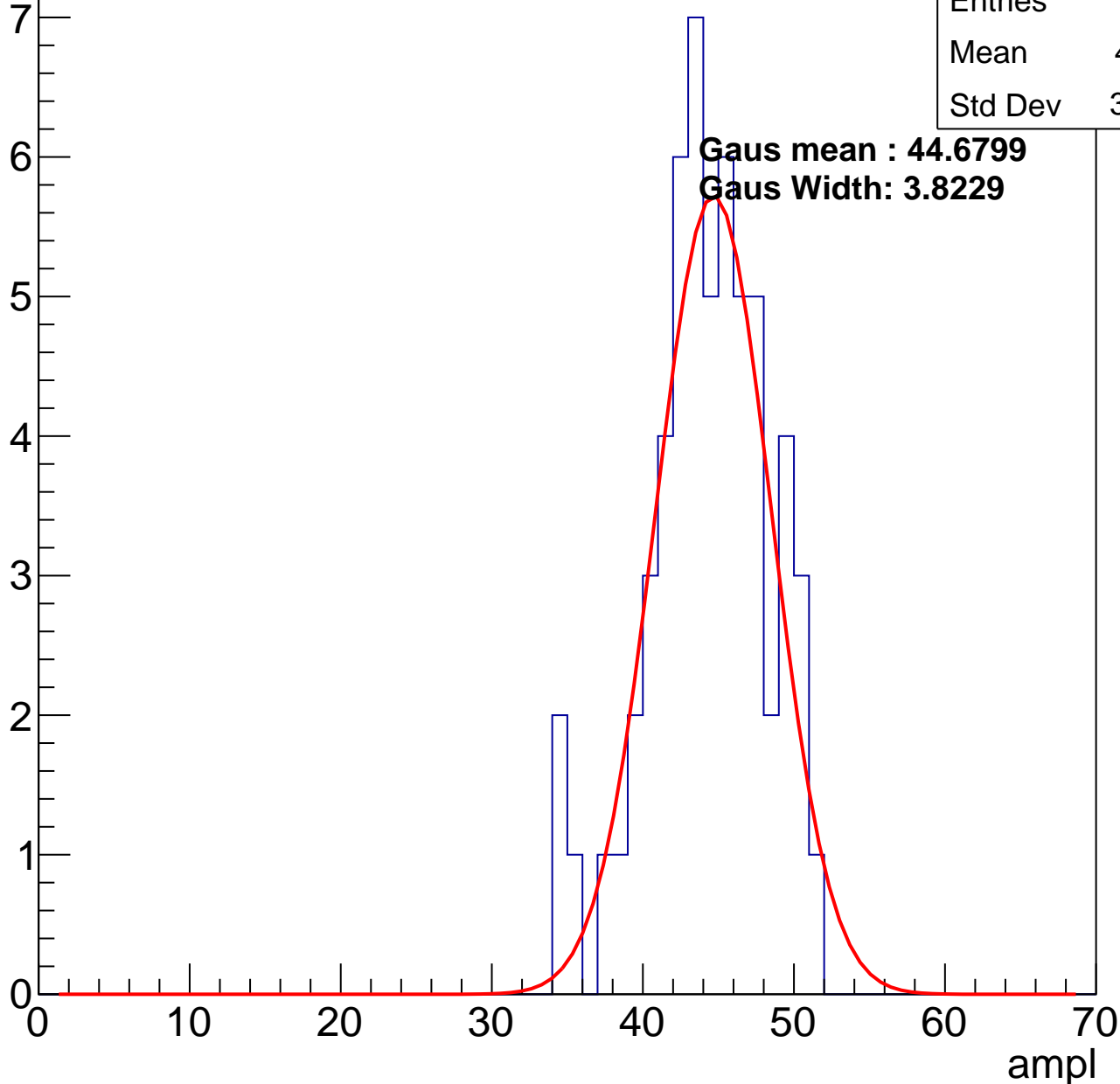


B1L100S, U11-ch27, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.81
Std Dev	3.915

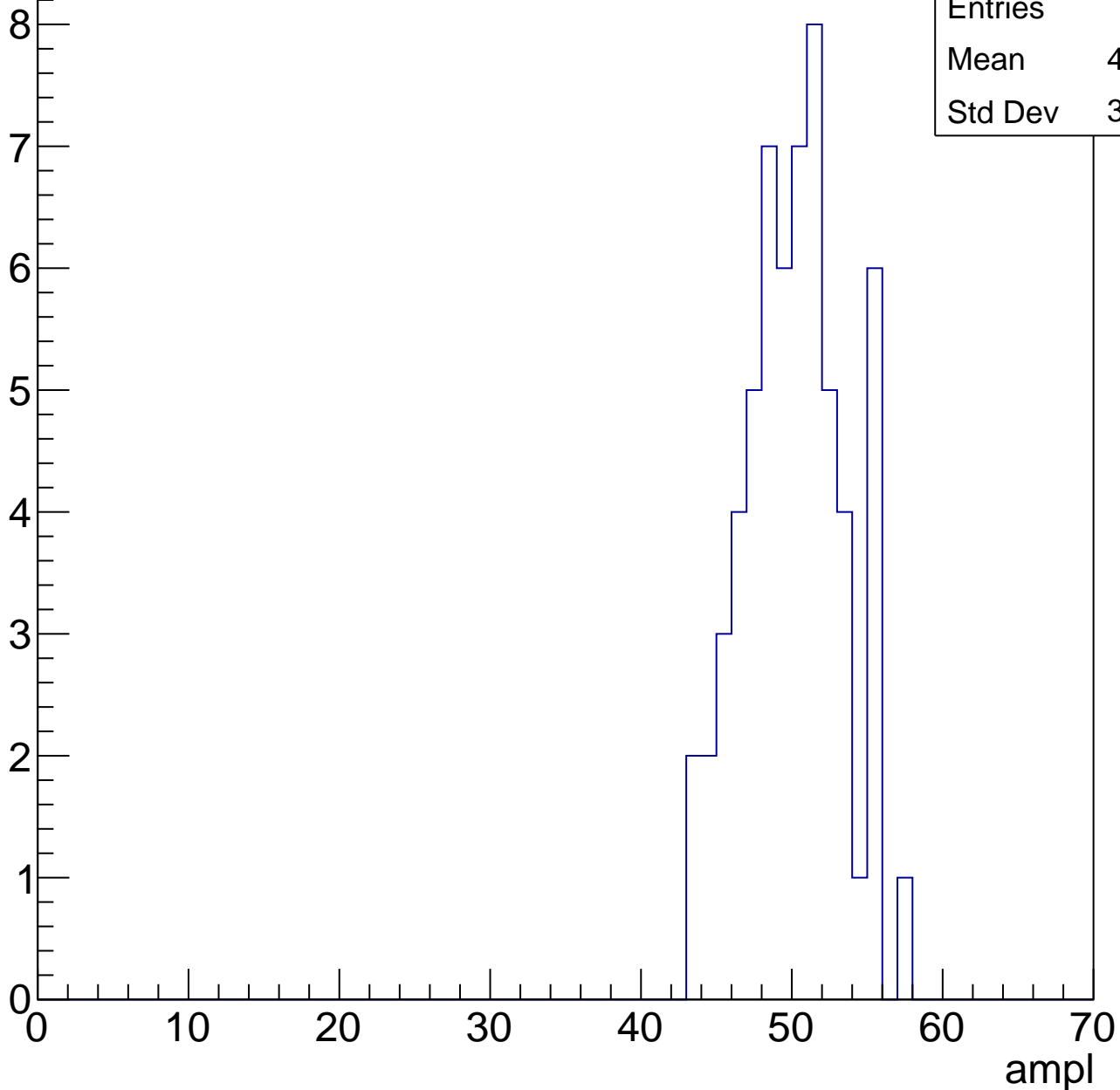


B1L100S, U11-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.66
Std Dev	3.289

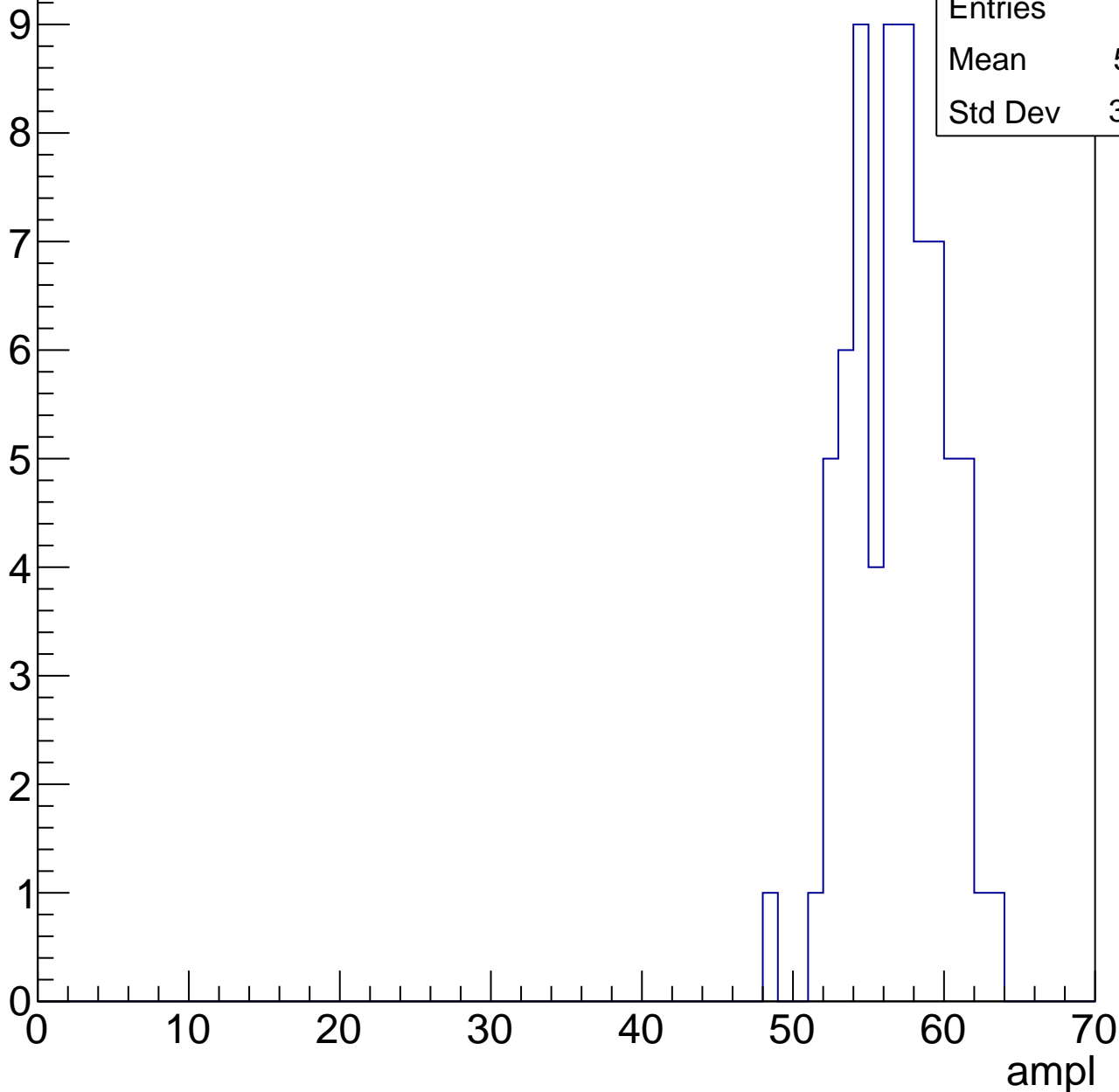


B1L100S, U11-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	56.41
Std Dev	3.026

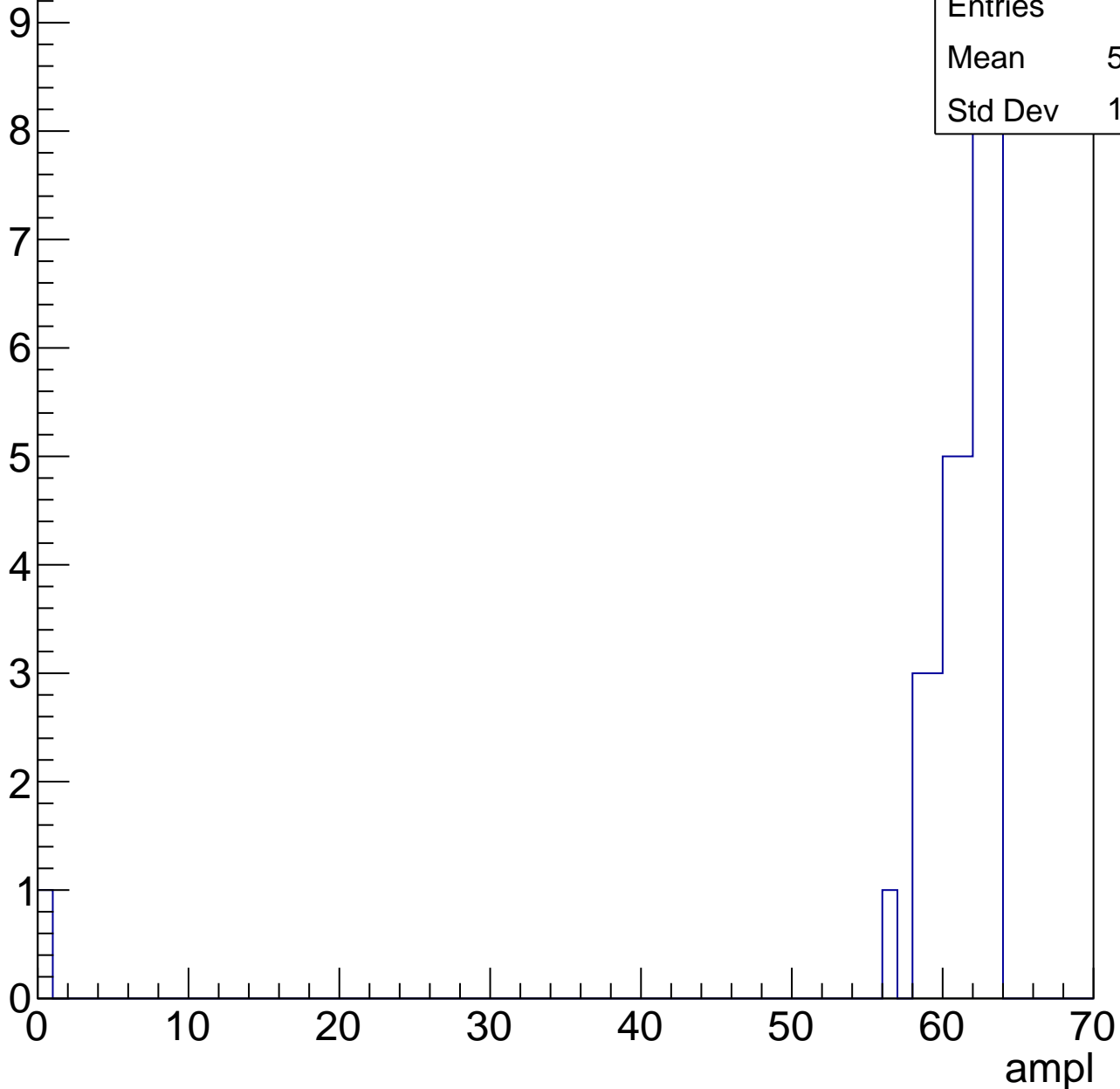


B1L100S, U11-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	59.36
Std Dev	10.19



B1L100S, U11-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch28, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	29.88
Std Dev	3.233

Gaus mean : 30.3838

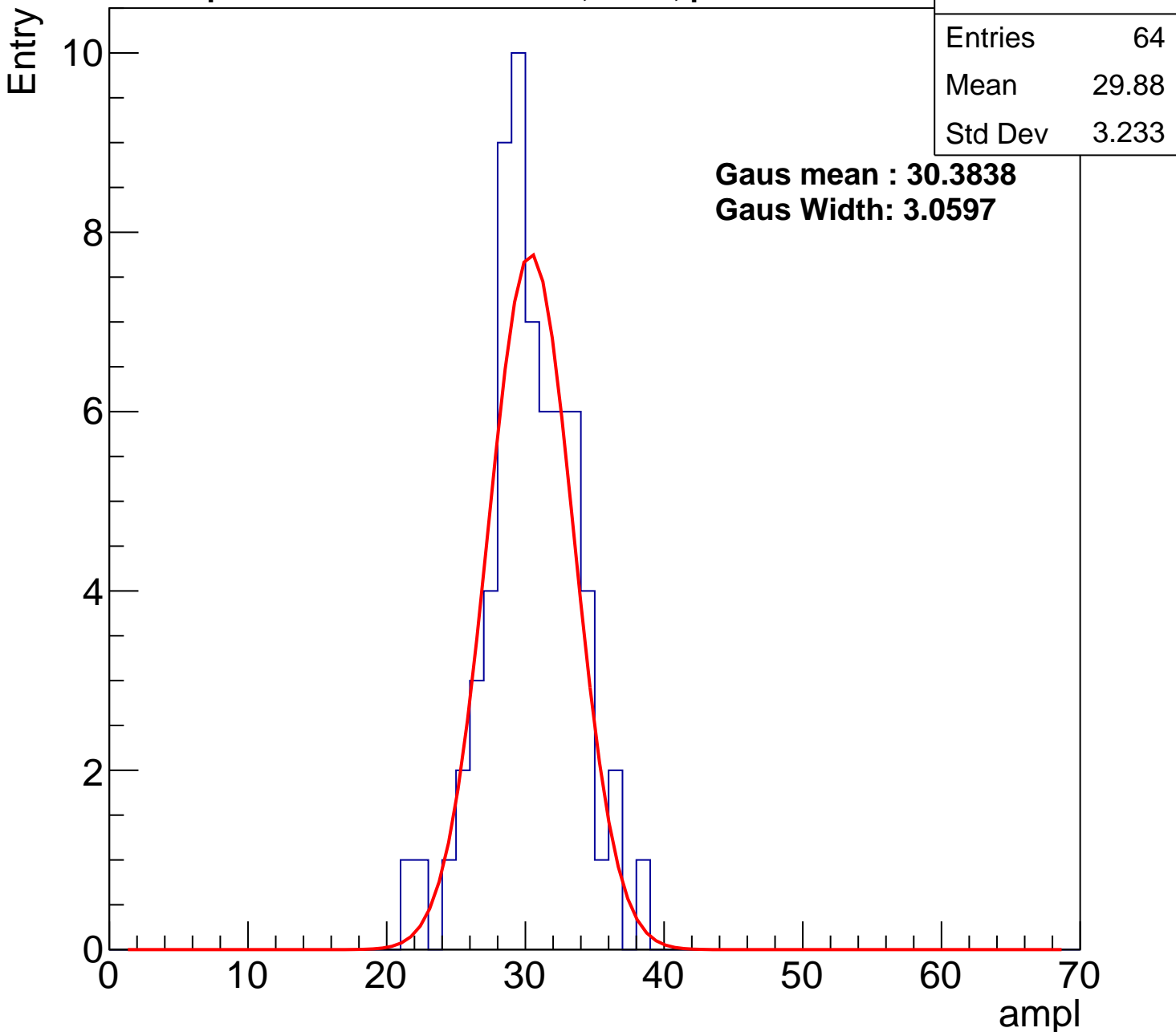
Gaus Width: 3.0597

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



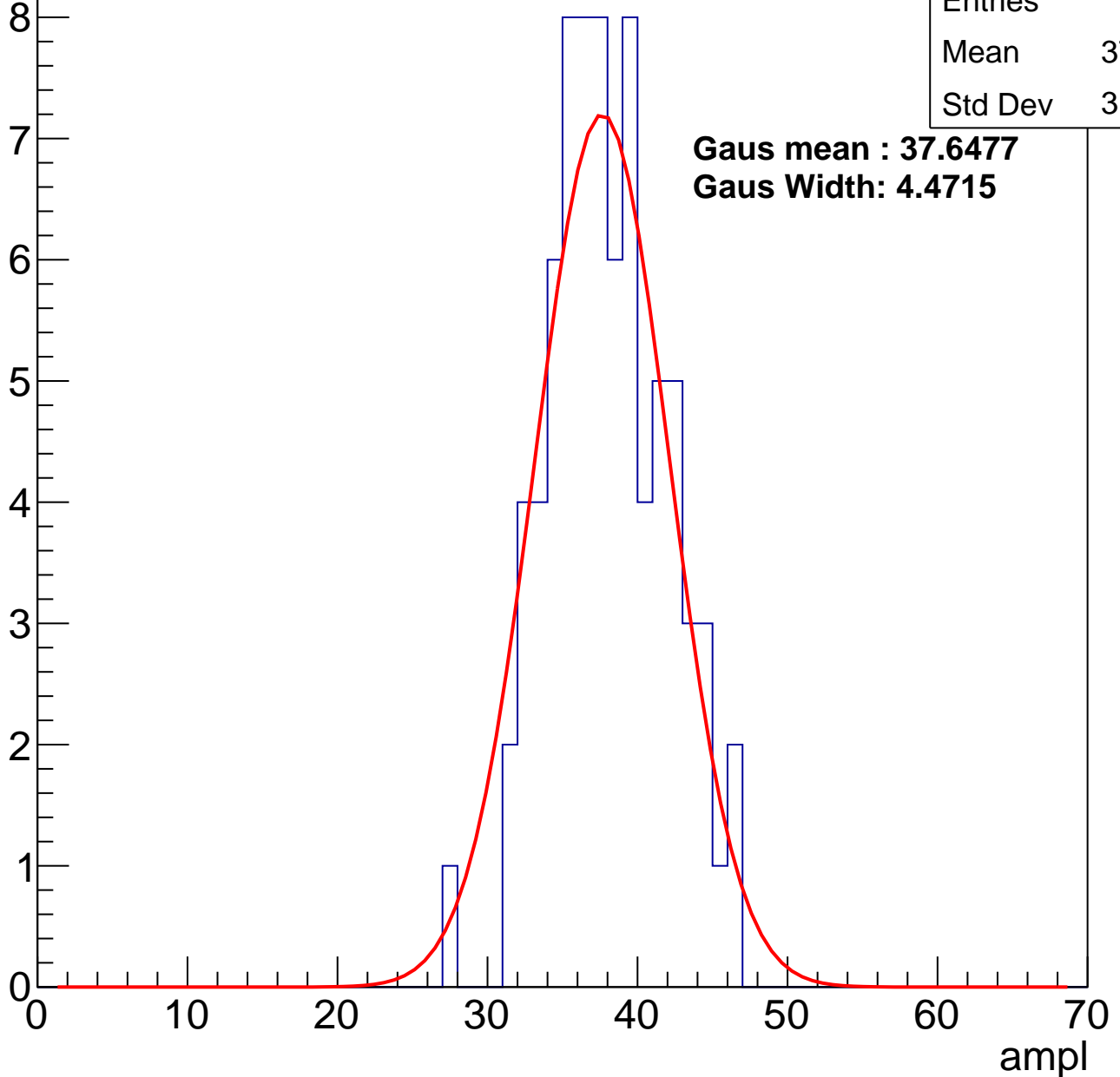
B1L100S, U11-ch28, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	37.56
Std Dev	3.865

Gaus mean : 37.6477
Gaus Width: 4.4715



B1L100S, U11-ch28, adc2

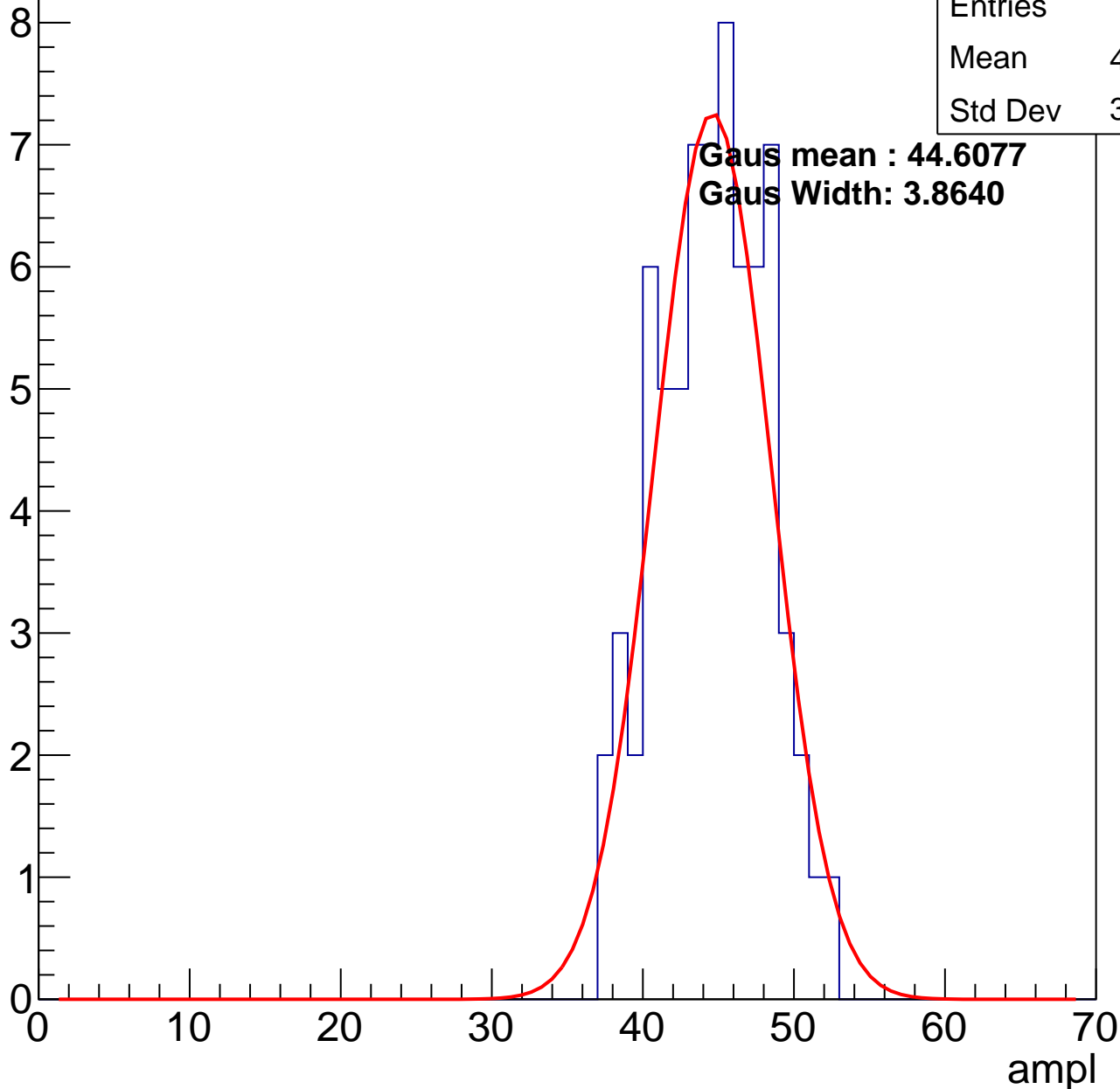
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.14
Std Dev	3.522

Gaus mean : 44.6077

Gaus Width: 3.8640

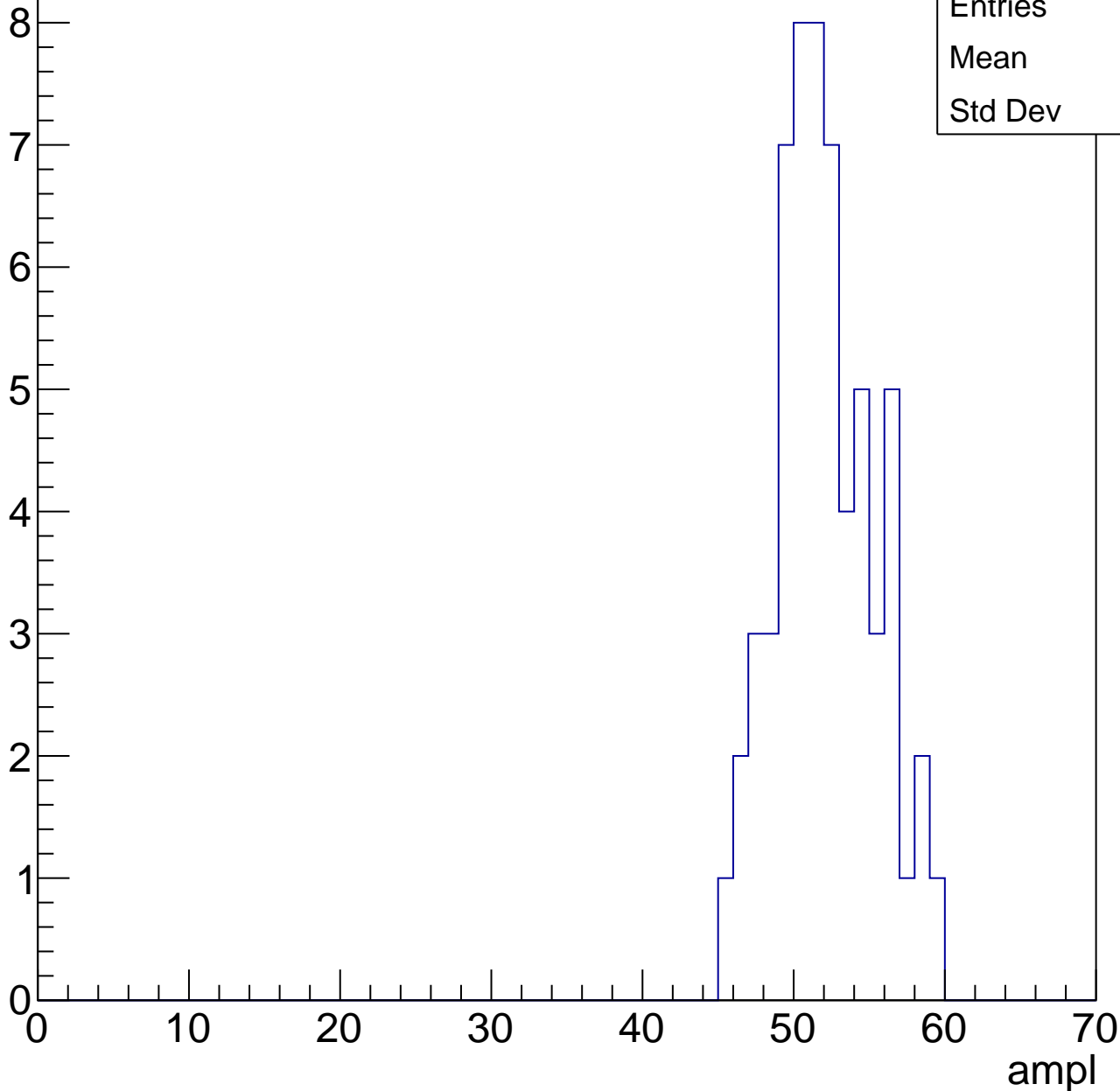


B1L100S, U11-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

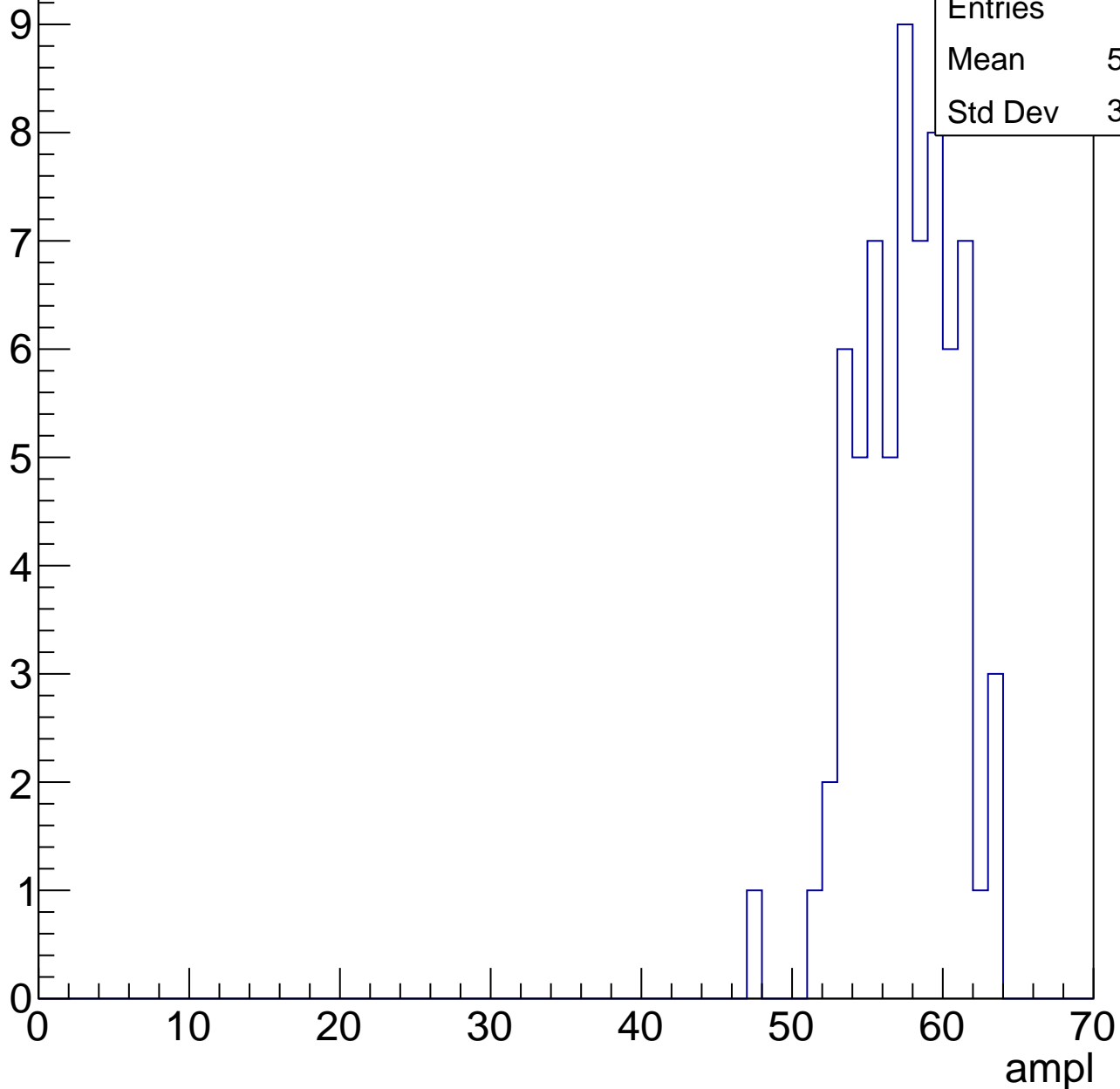
Entries	60
Mean	51.6
Std Dev	3.2



B1L100S, U11-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

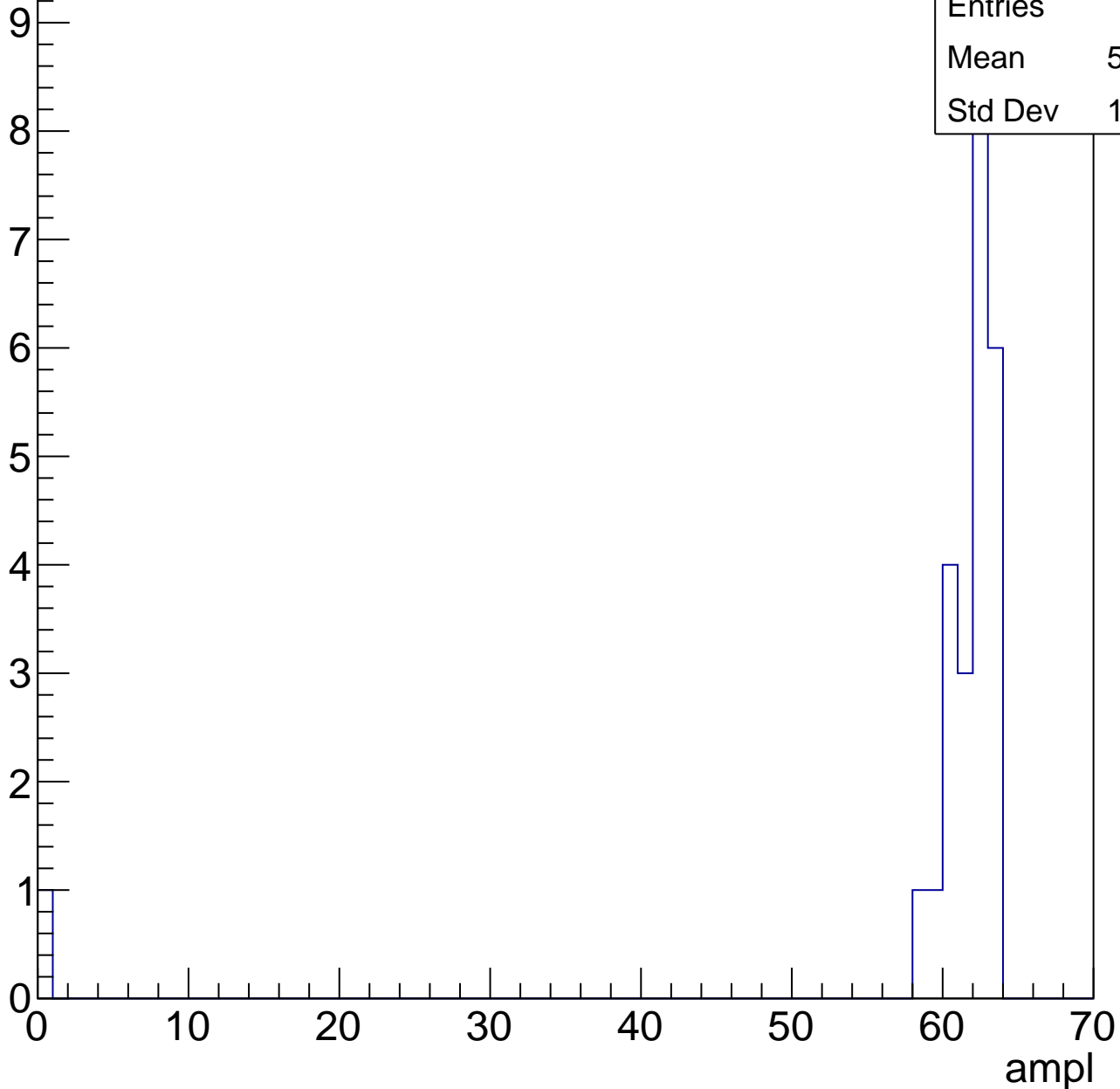


B1L100S, U11-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	59.04
Std Dev	12.12



B1L100S, U11-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch29, adc0

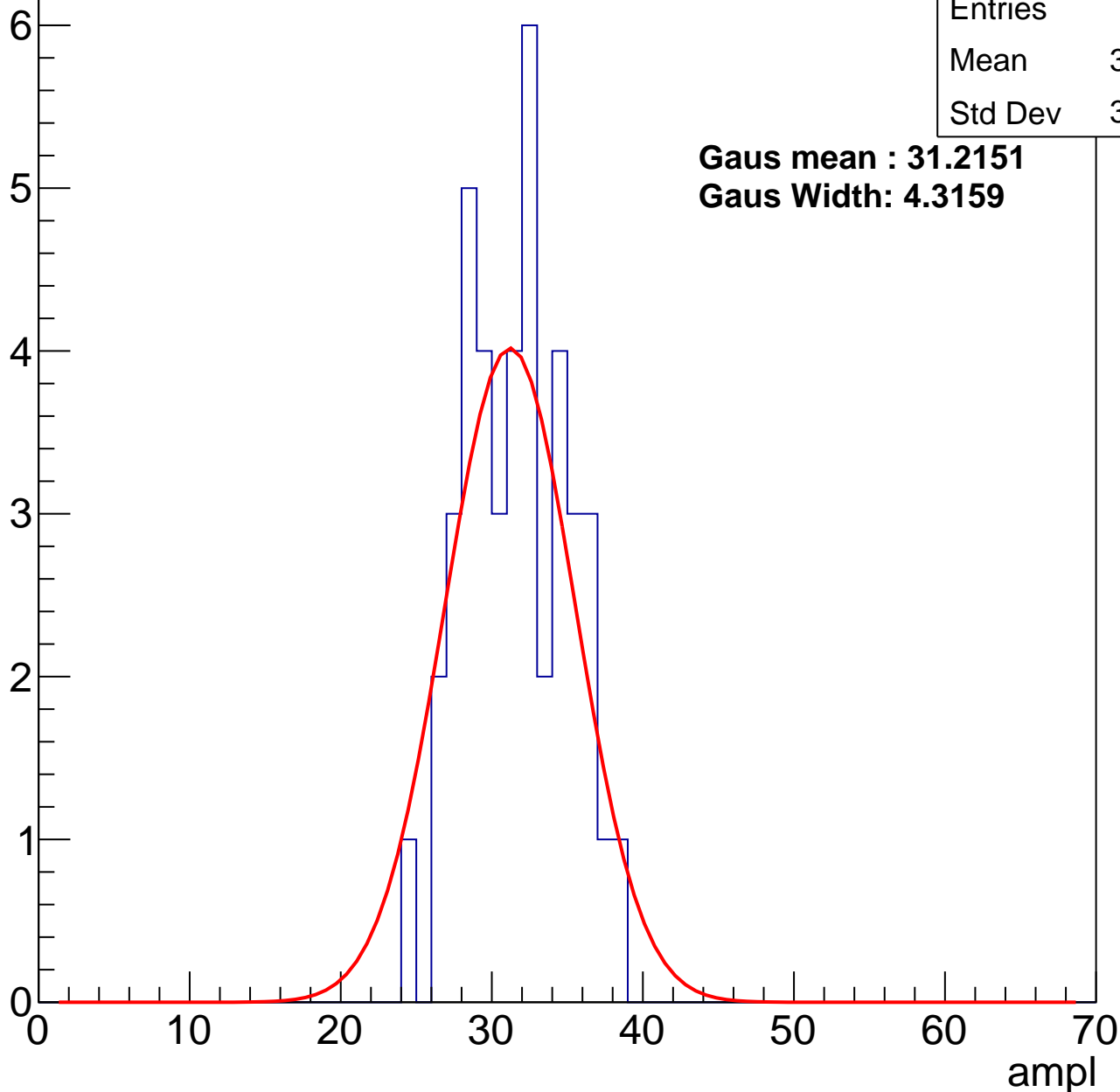
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	31.17
Std Dev	3.337

Gaus mean : 31.2151

Gaus Width: 4.3159



B1L100S, U11-ch29, adc1

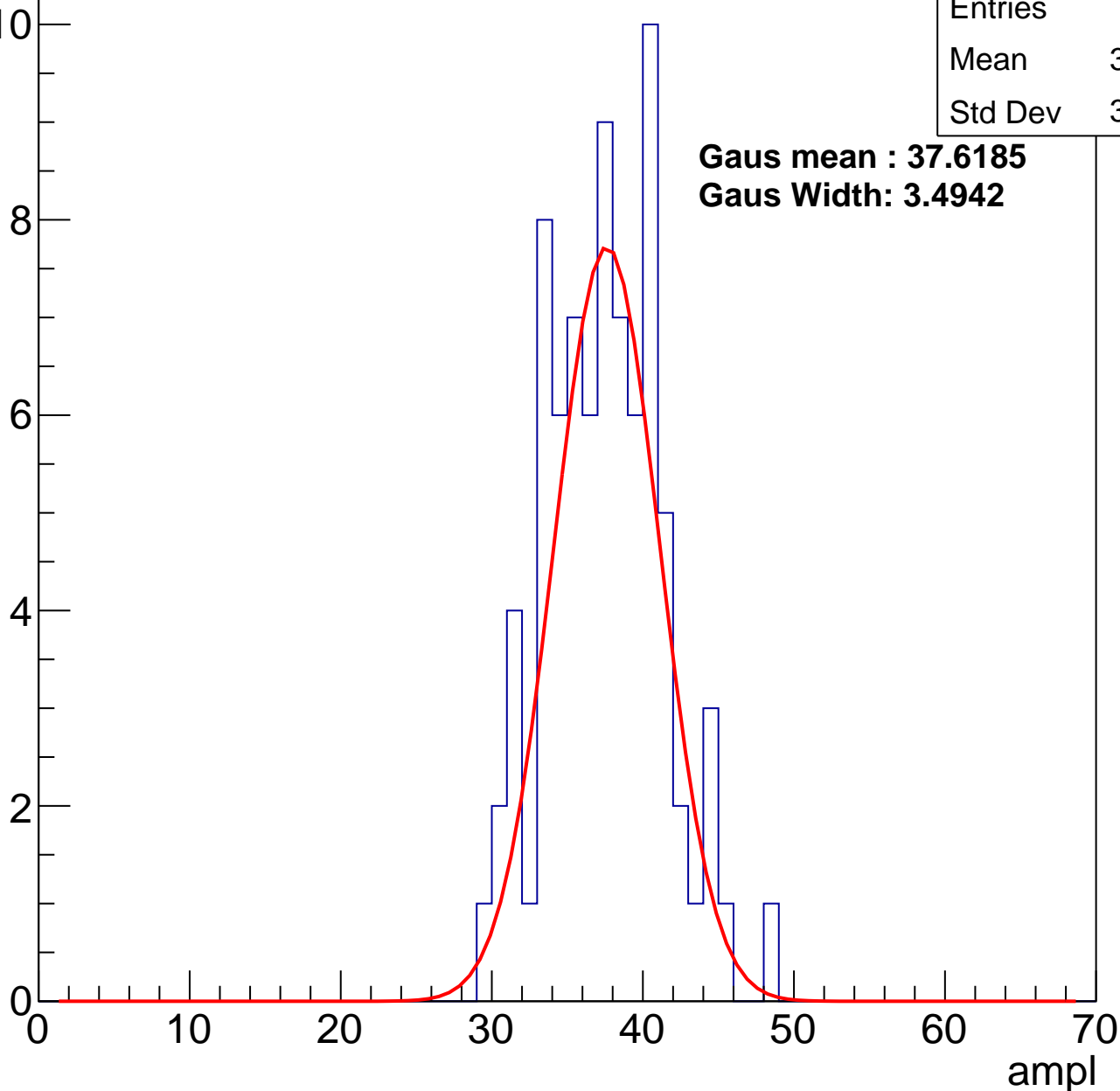
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	37.05
Std Dev	3.817

Gaus mean : 37.6185

Gaus Width: 3.4942



B1L100S, U11-ch29, adc2

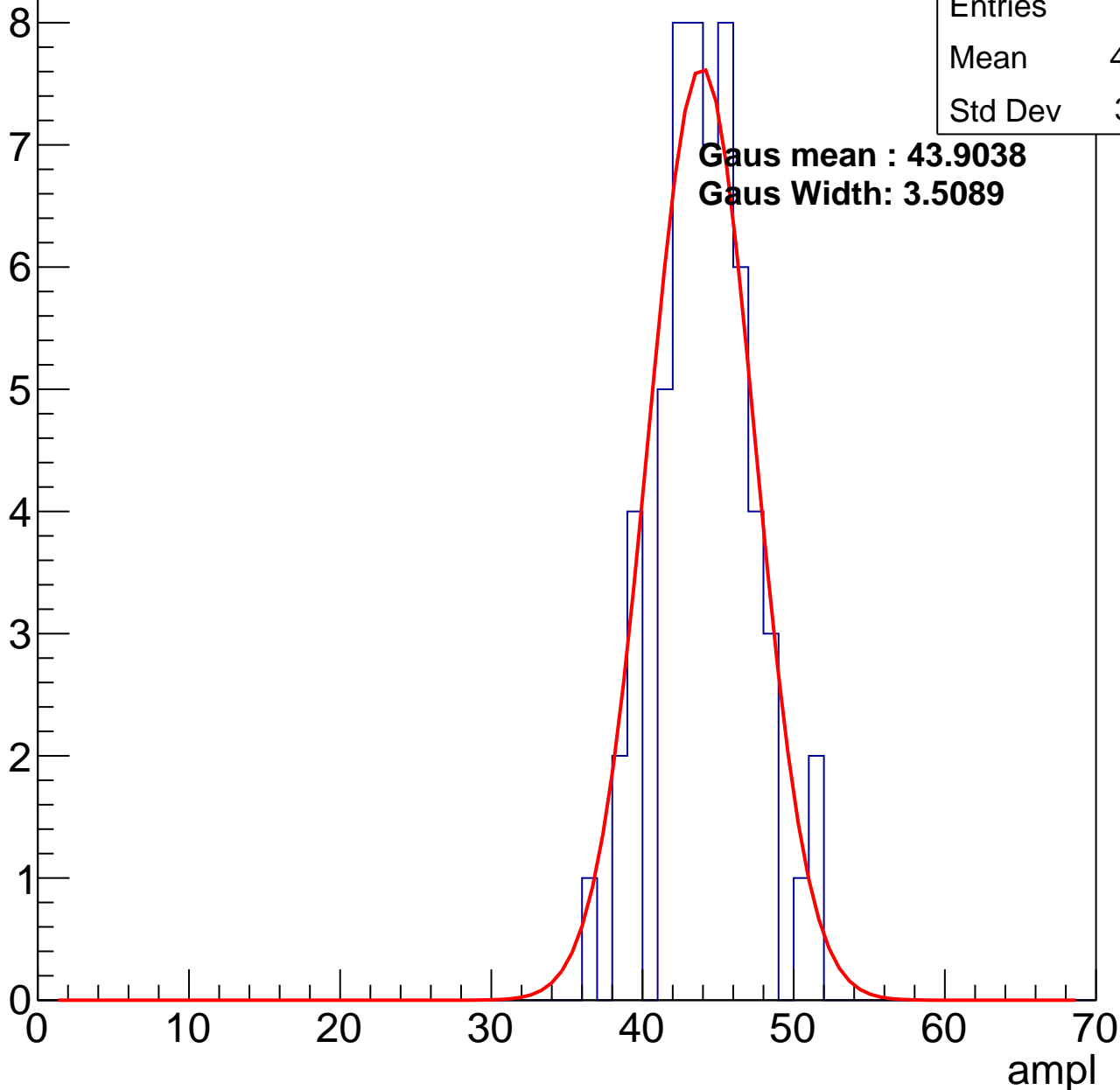
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.75
Std Dev	3.101

Gaus mean : 43.9038

Gaus Width: 3.5089

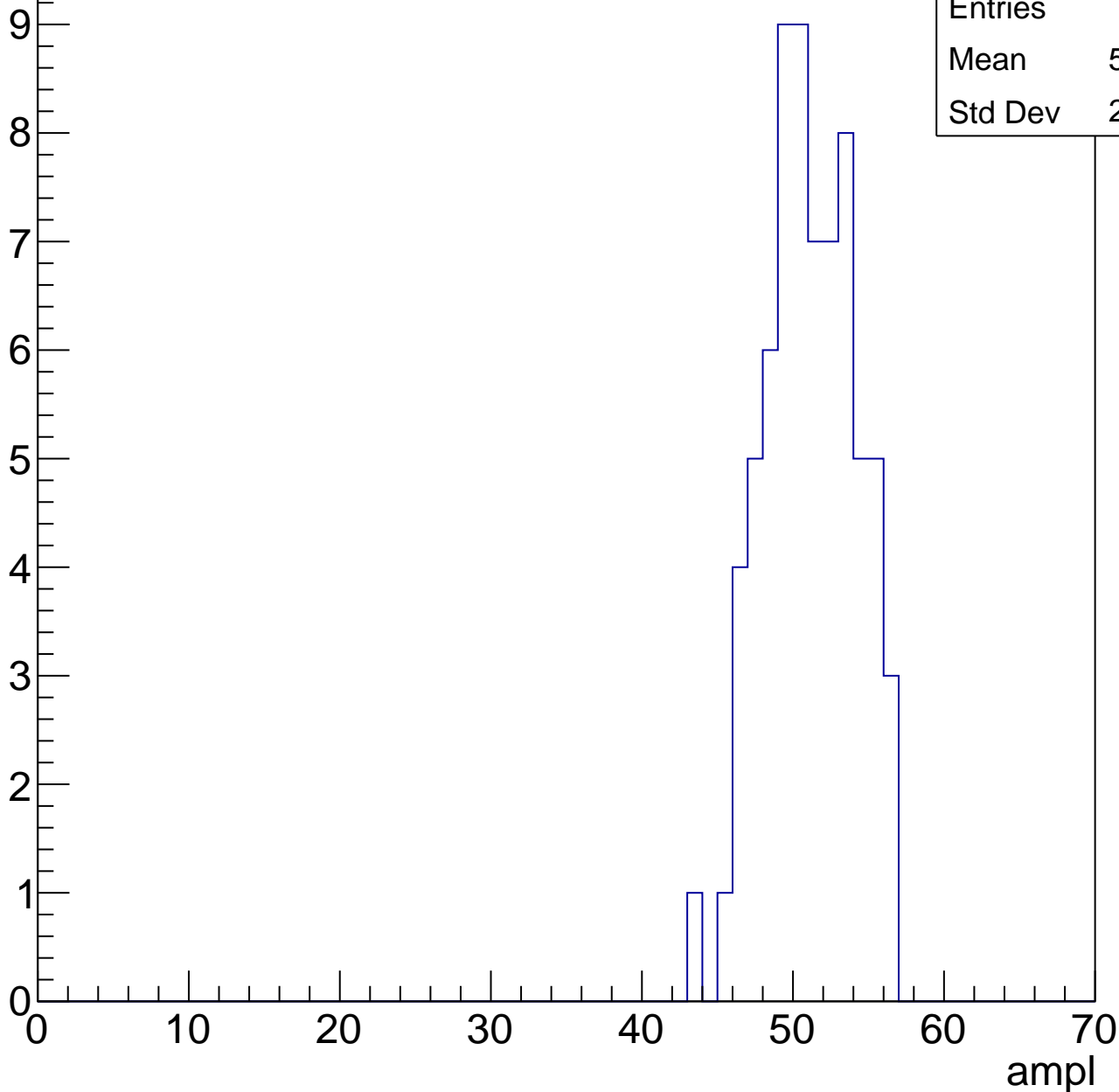


B1L100S, U11-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	50.63
Std Dev	2.948



B1L100S, U11-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	57.33
Std Dev	3.339

Entry

10

8

6

4

2

0

0

10

20

30

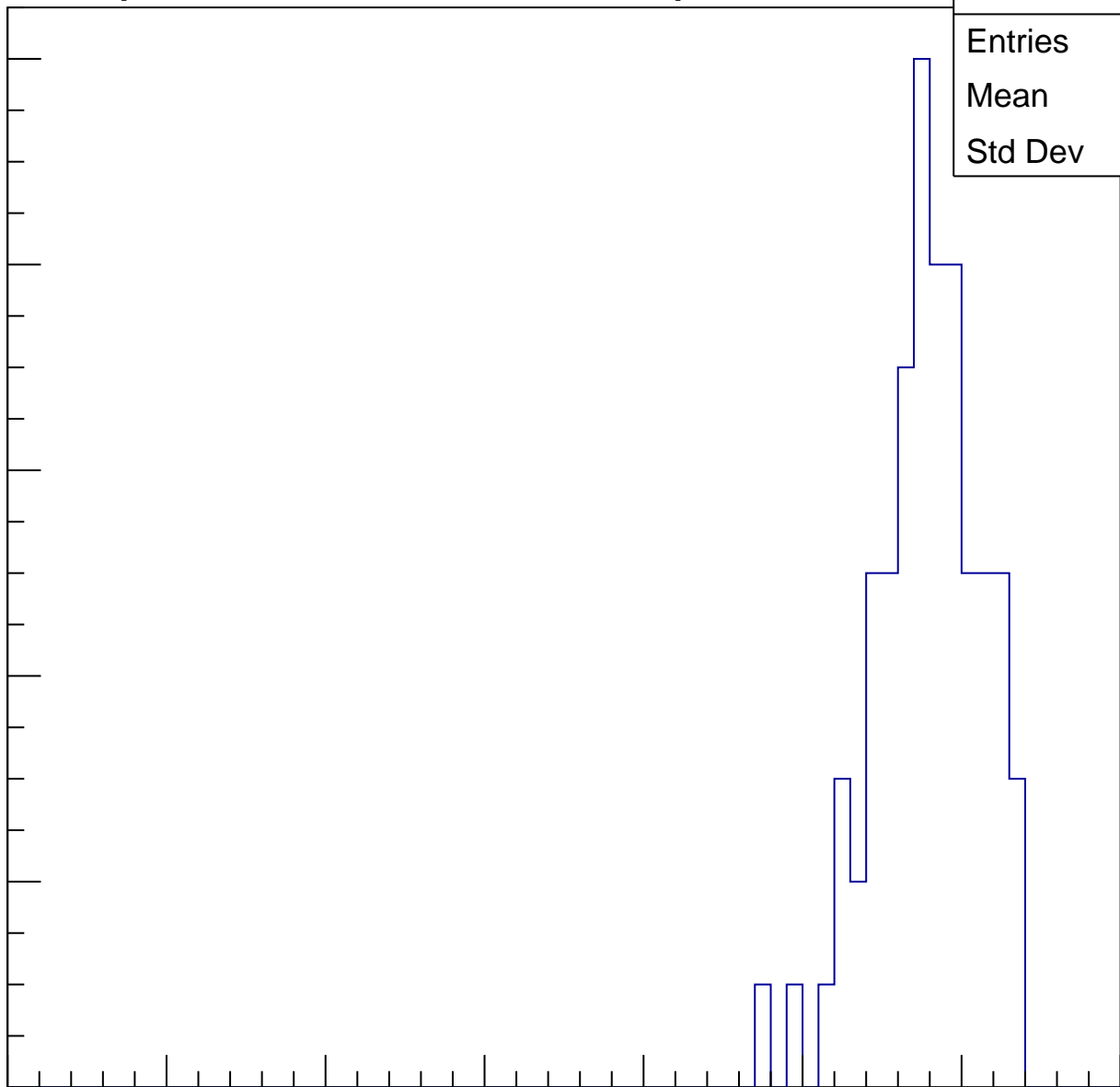
40

50

60

70

ampl

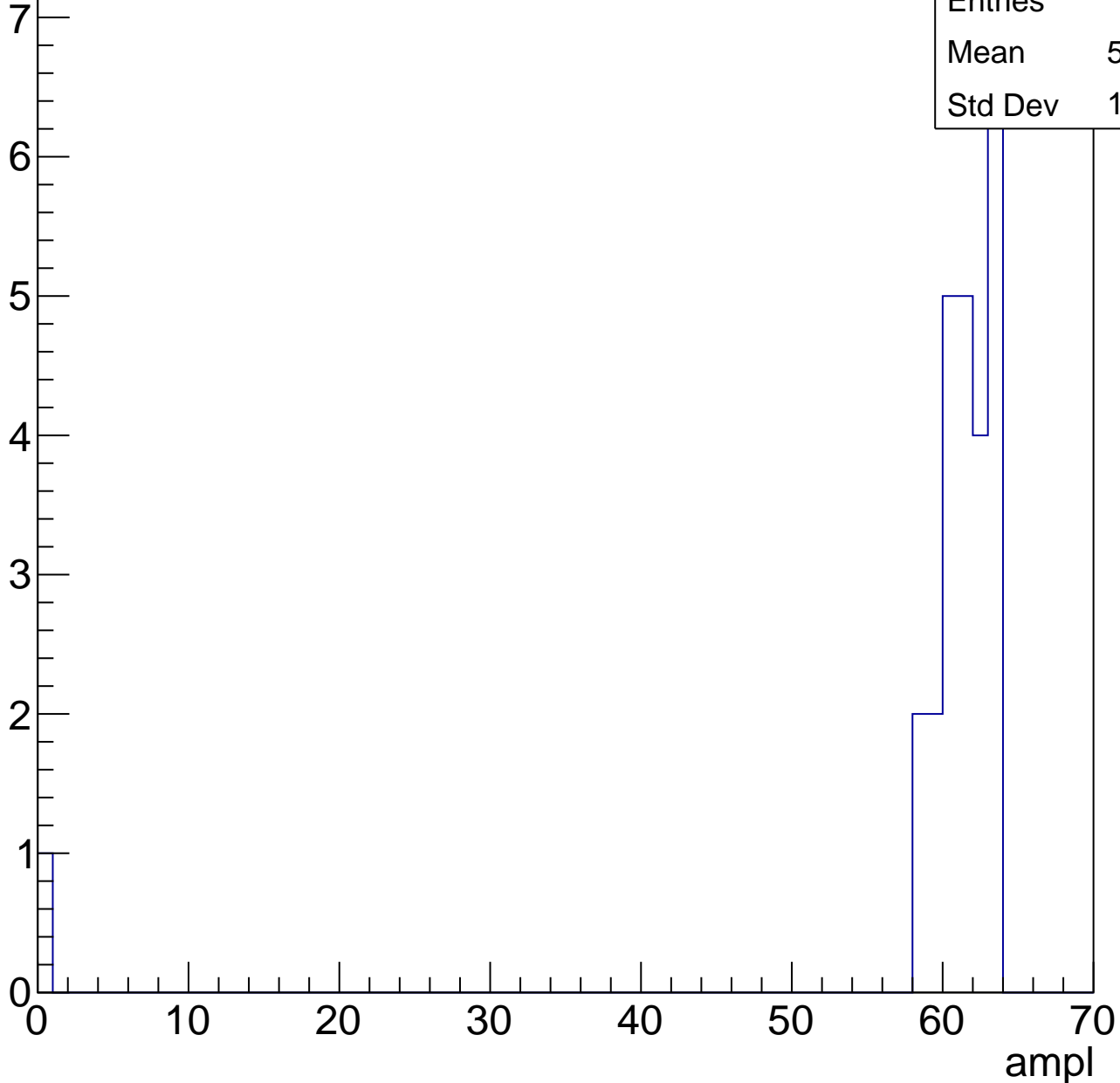


B1L100S, U11-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.77
Std Dev	11.86



B1L100S, U11-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U11-ch30, adc0

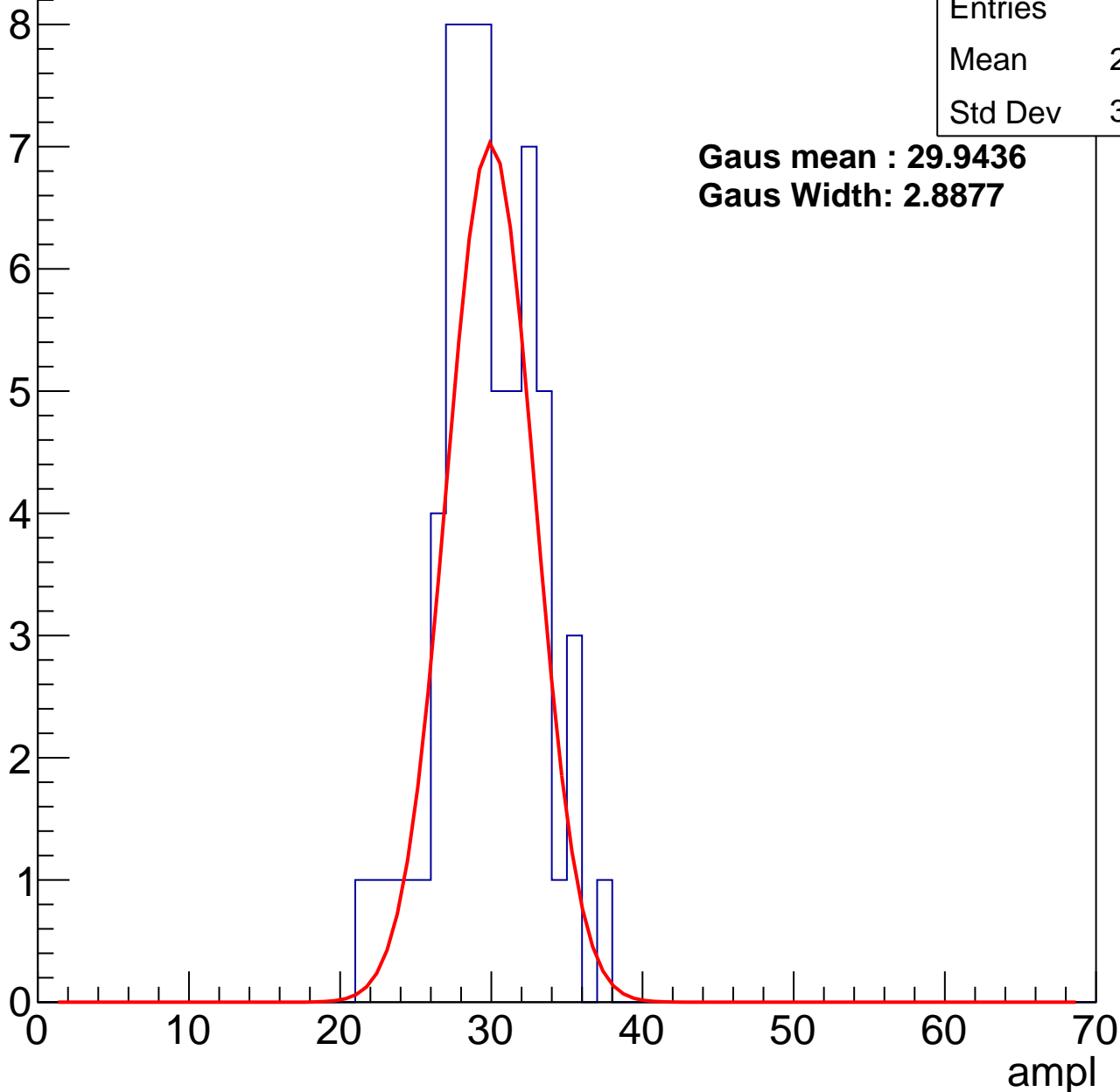
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	29.35
Std Dev	3.224

Gaus mean : 29.9436

Gaus Width: 2.8877

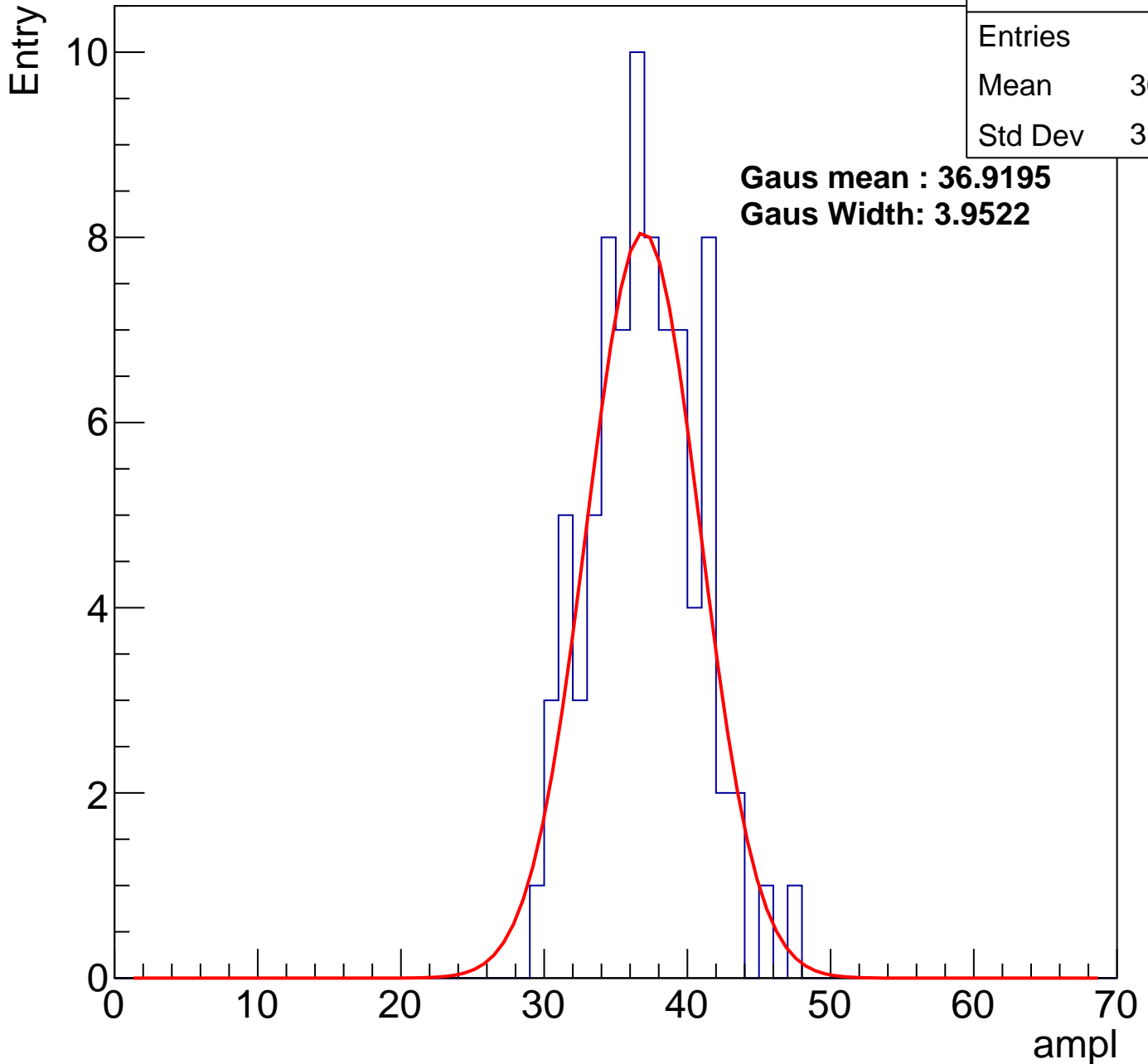


B1L100S, U11-ch30, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	36.55
Std Dev	3.703

Gaus mean : 36.9195
Gaus Width: 3.9522



B1L100S, U11-ch30, adc2

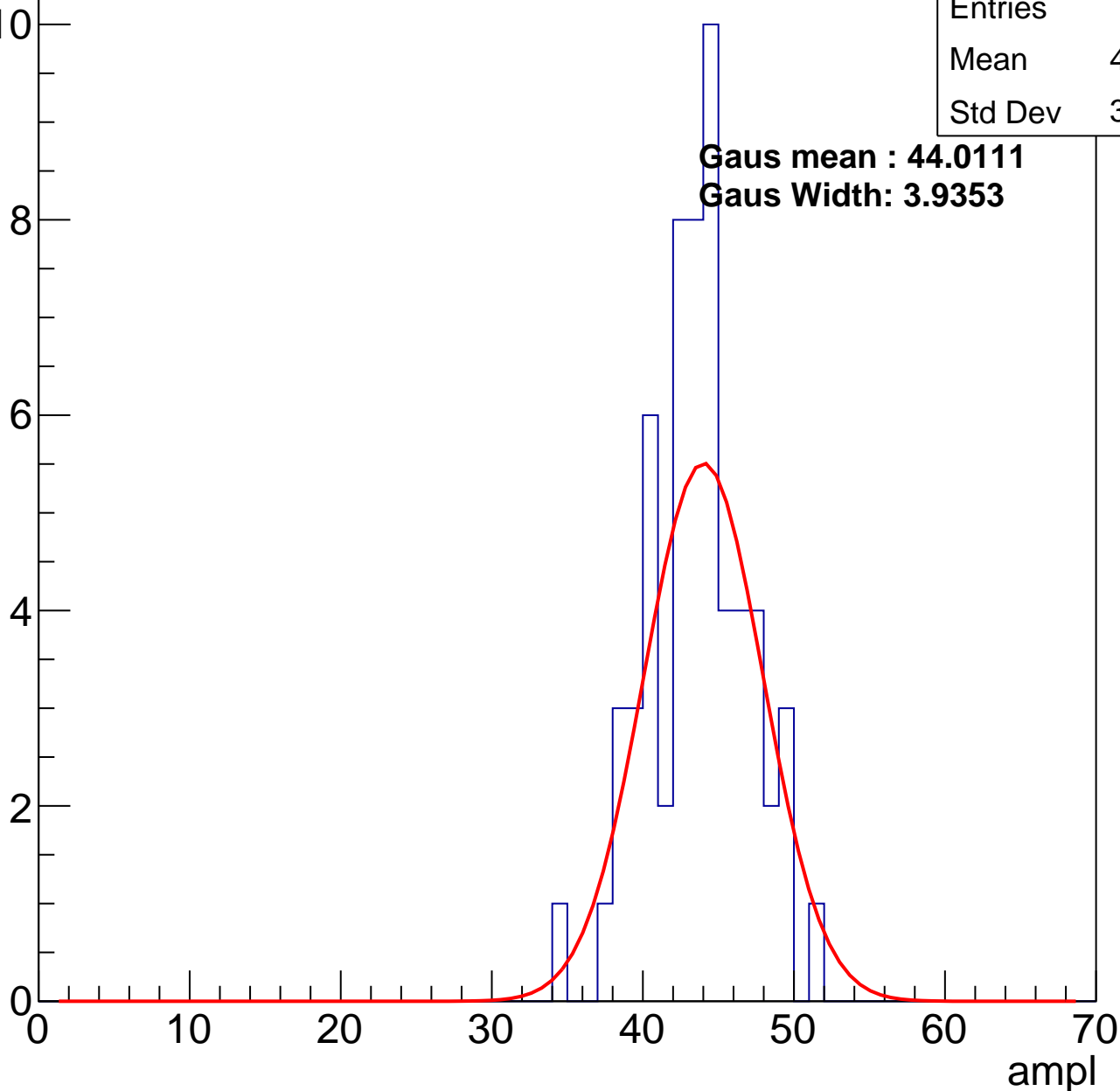
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43.17
Std Dev	3.307

Gaus mean : 44.0111

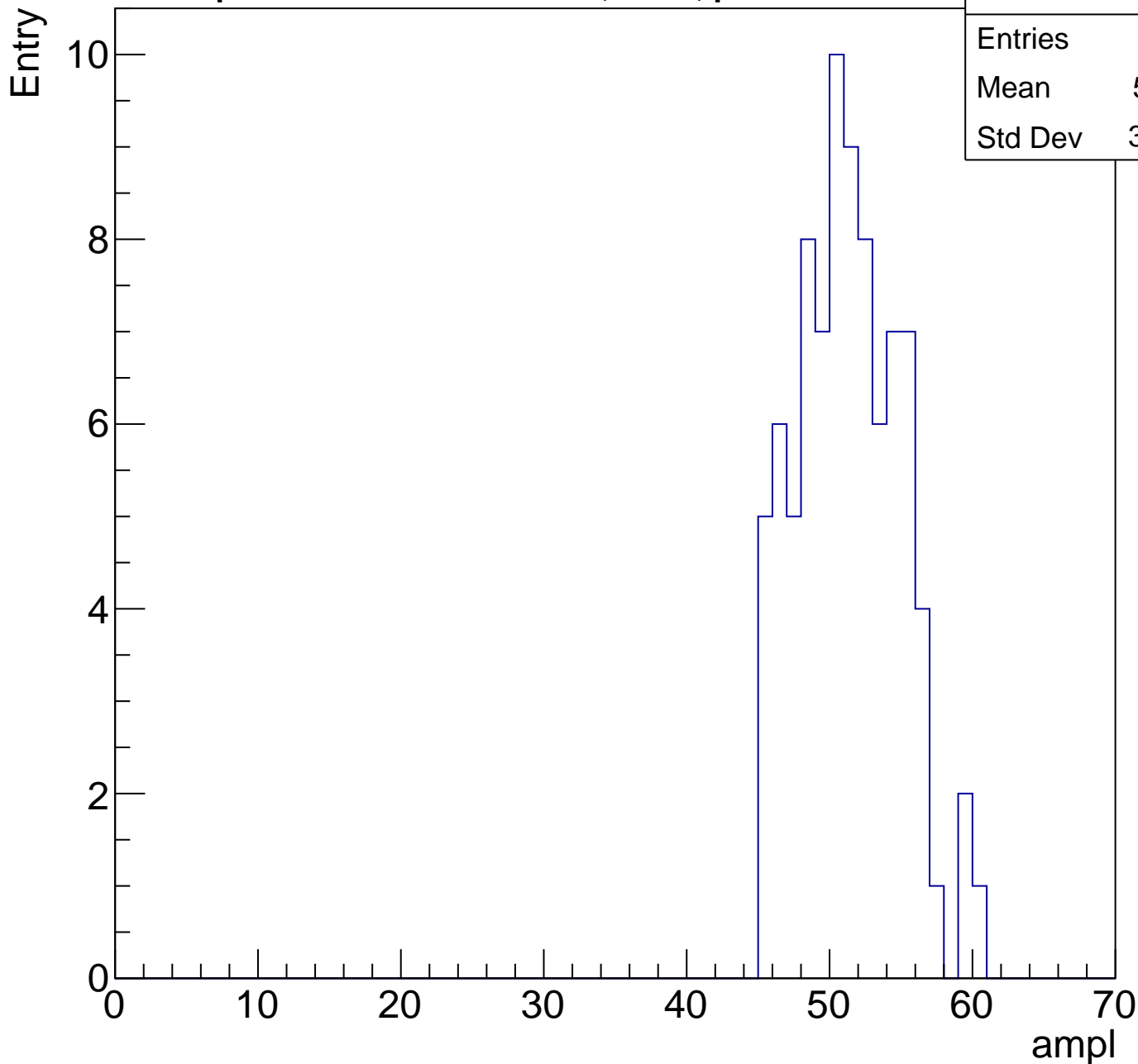
Gaus Width: 3.9353



B1L100S, U11-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	50.91
Std Dev	3.529



B1L100S, U11-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	57.21
Std Dev	2.633

ampl

0

10

20

30

40

50

60

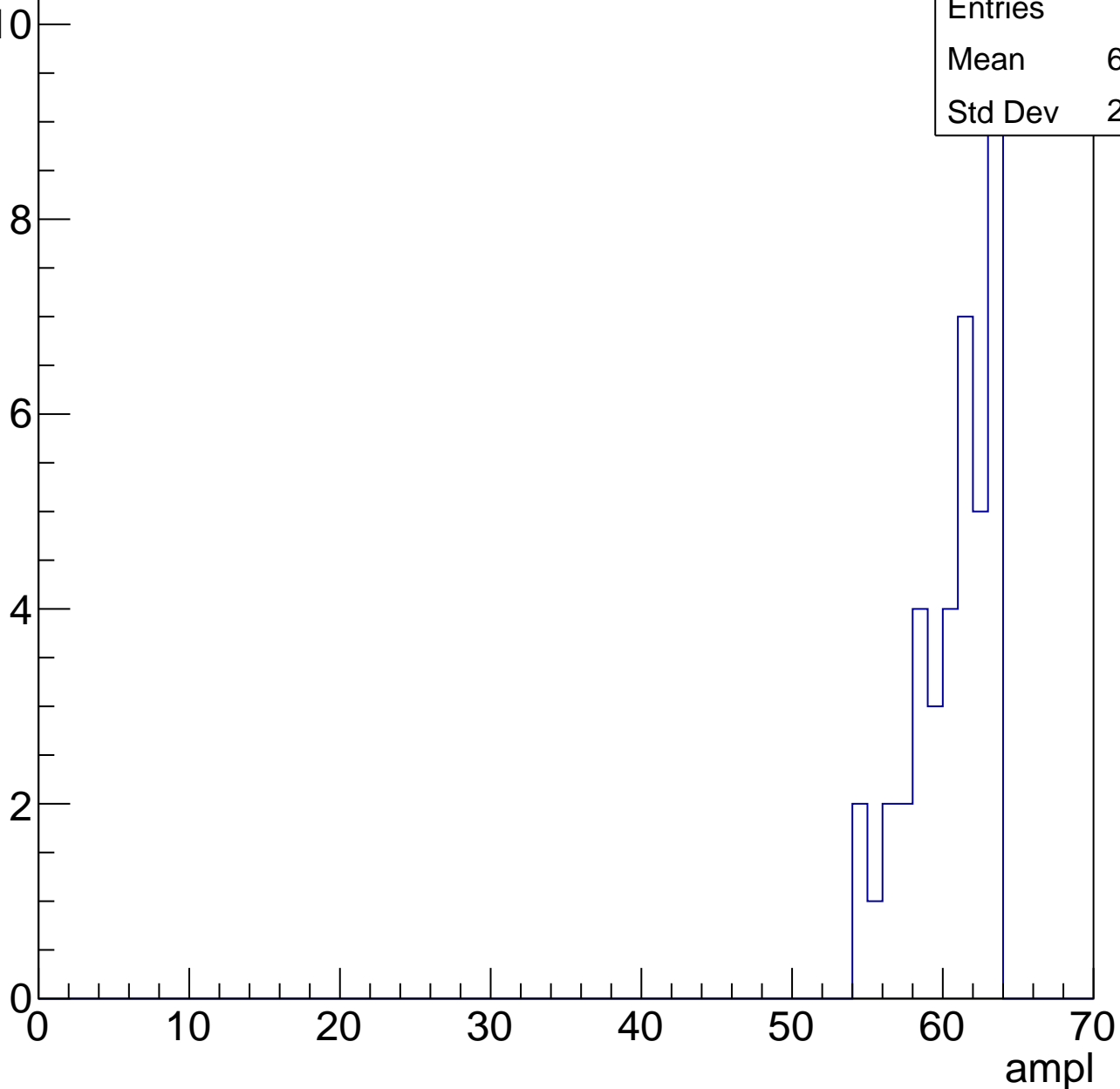
70

B1L100S, U11-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

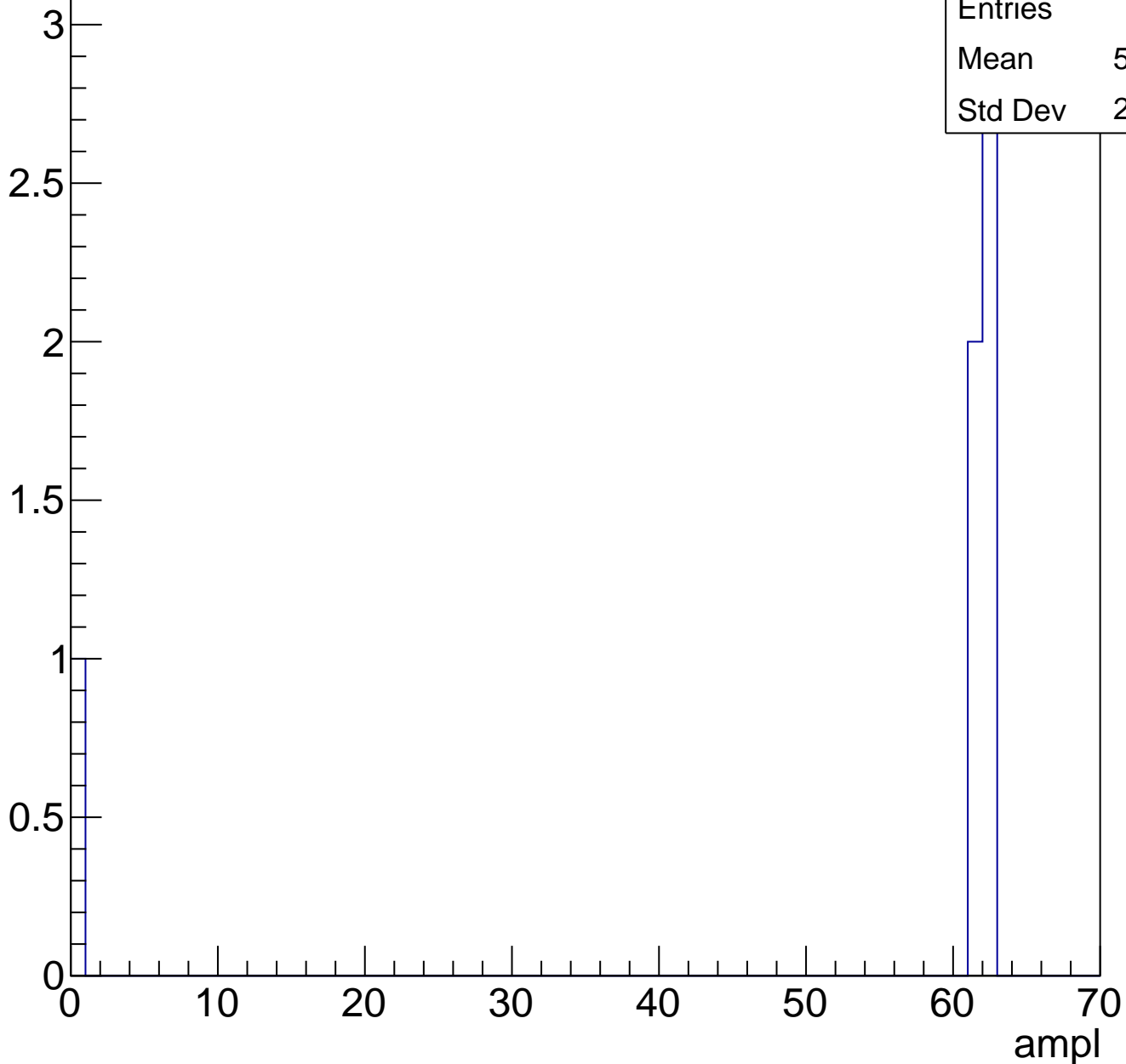
Entries	40
Mean	60.12
Std Dev	2.657



B1L100S, U11-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch31, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	30.66
Std Dev	2.914

Gaus mean : 31.1312

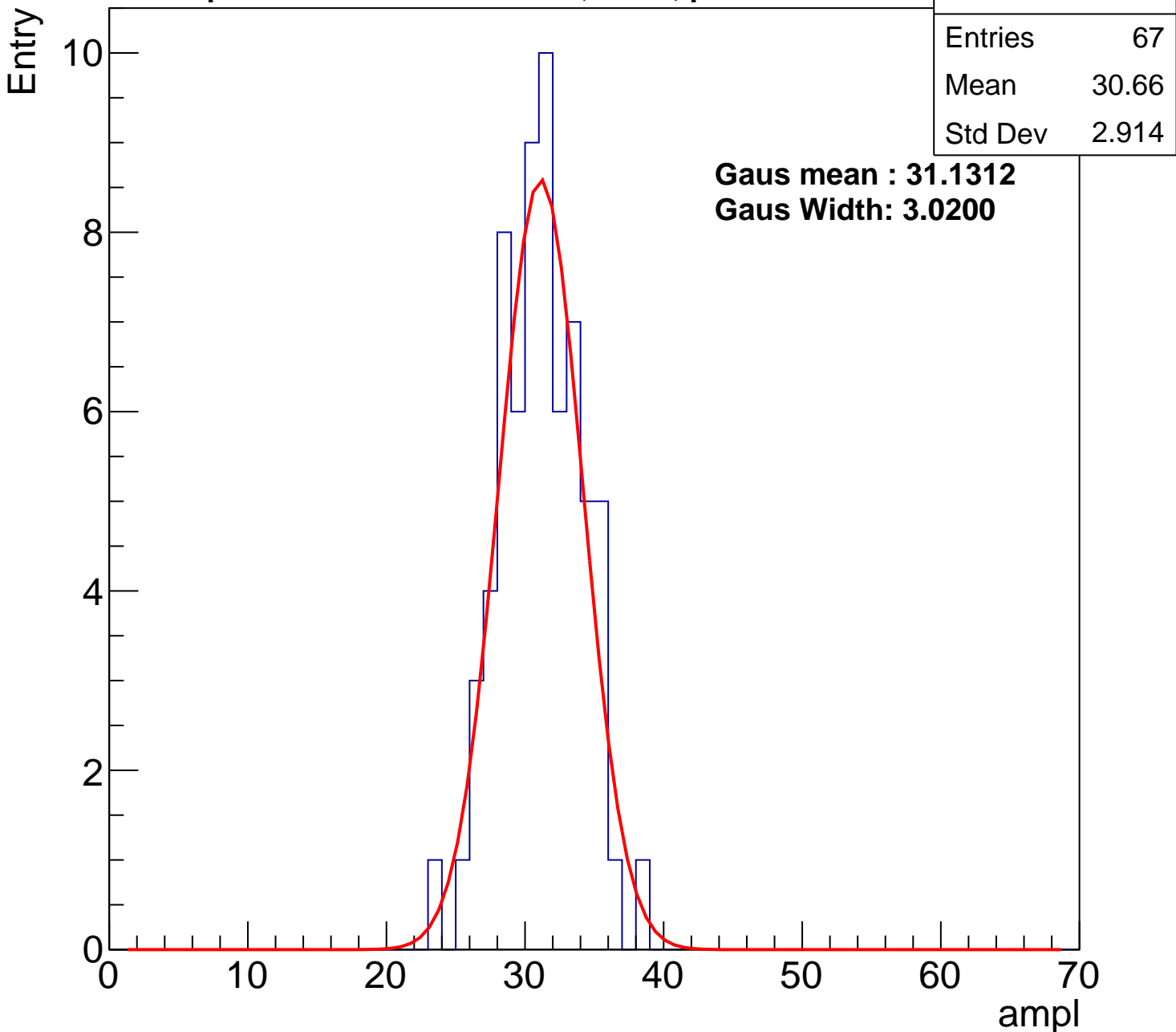
Gaus Width: 3.0200

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



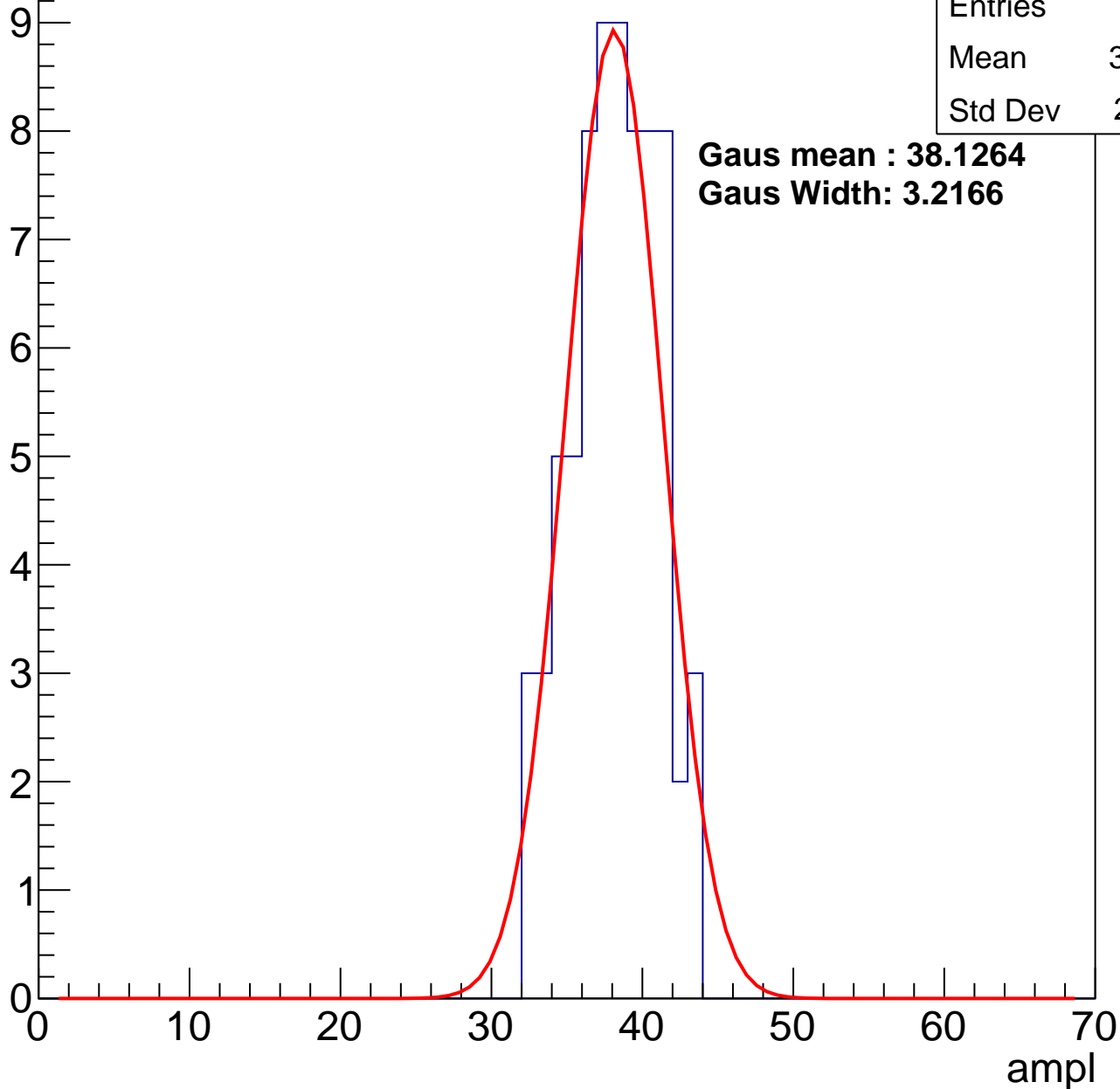
B1L100S, U11-ch31, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	37.69
Std Dev	2.811

Gaus mean : 38.1264
Gaus Width: 3.2166



B1L100S, U11-ch31, adc2

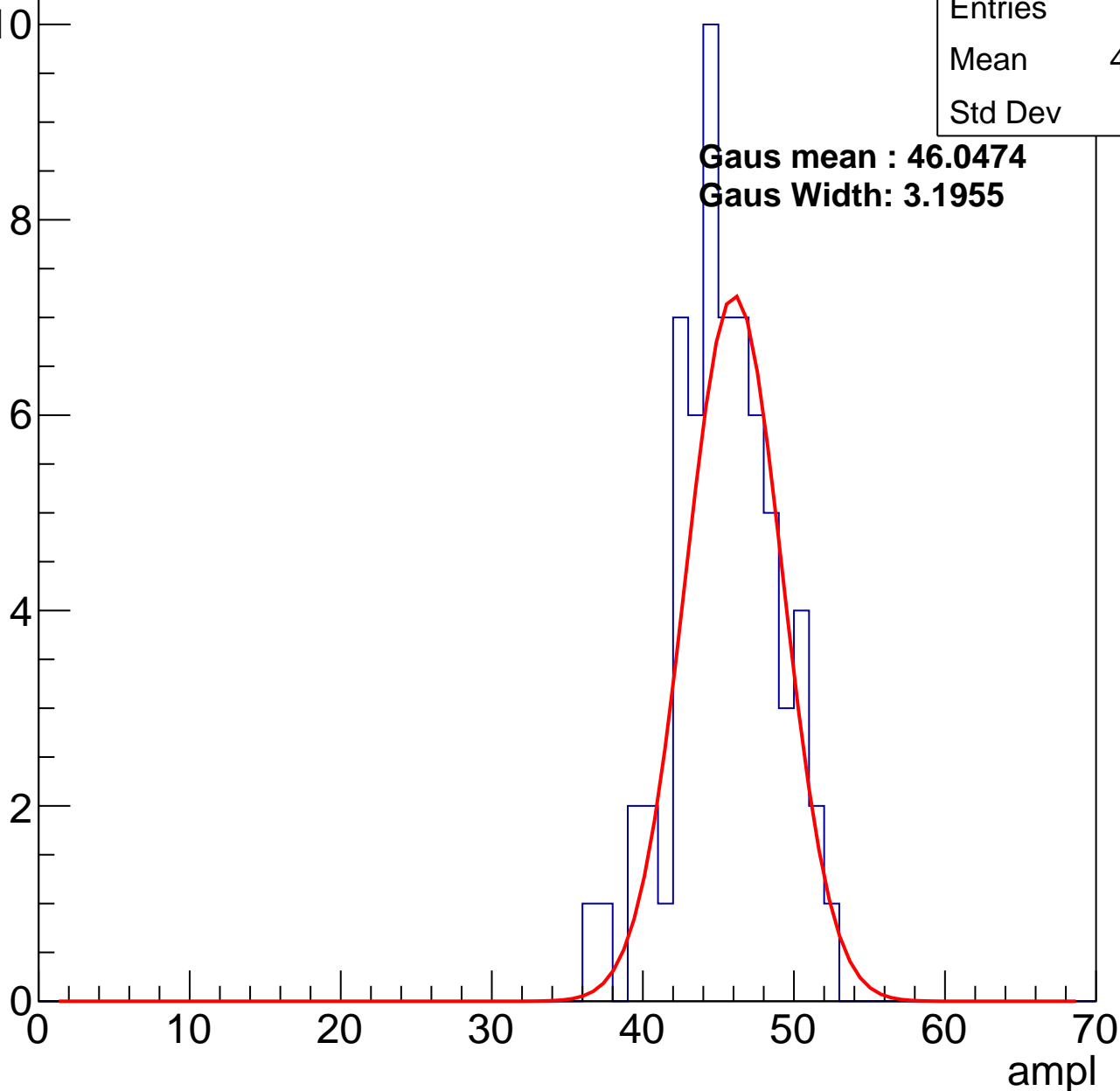
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	44.98
Std Dev	3.34

Gaus mean : 46.0474

Gaus Width: 3.1955

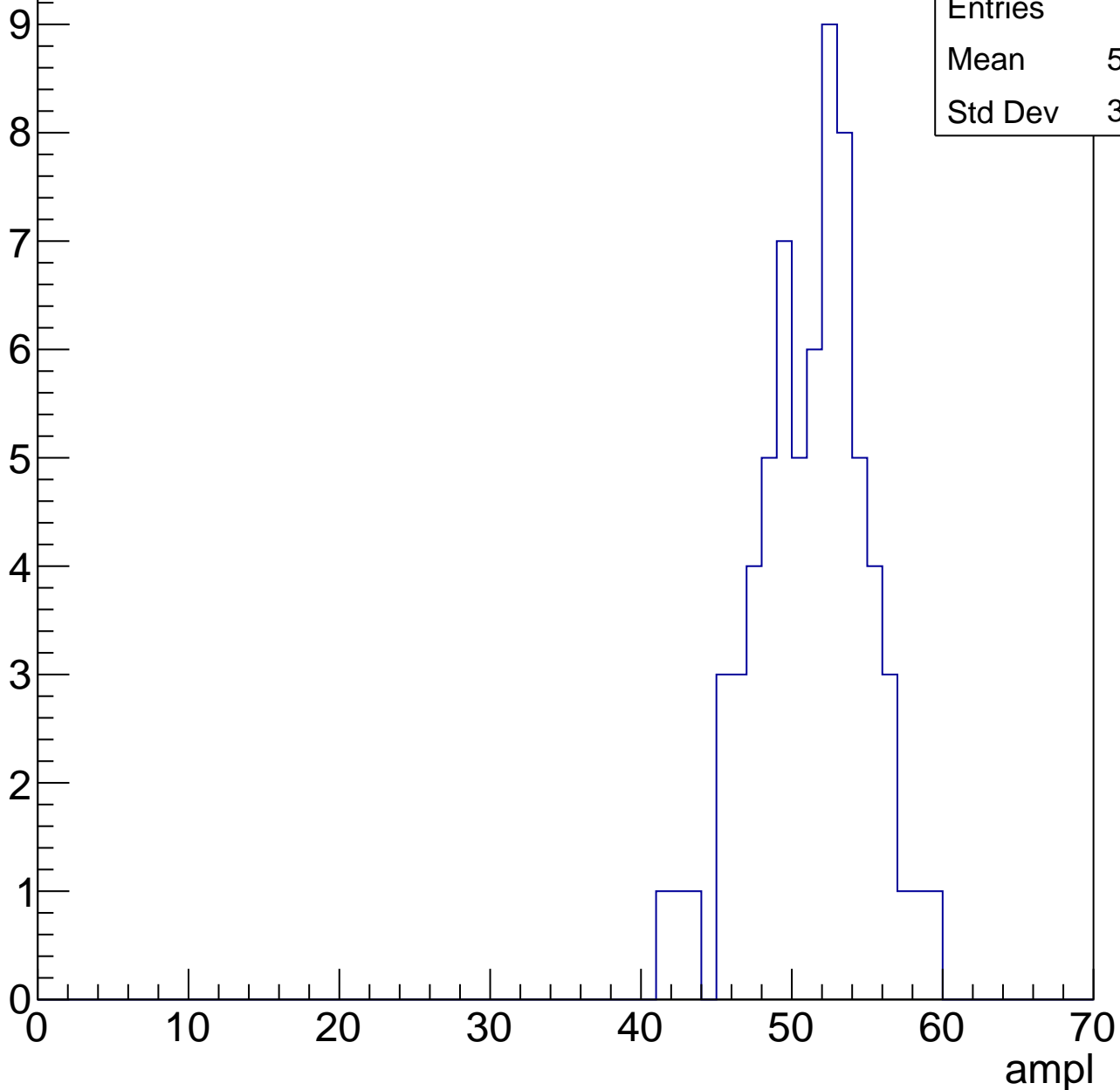


B1L100S, U11-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.74
Std Dev	3.716



B1L100S, U11-ch31, adc4

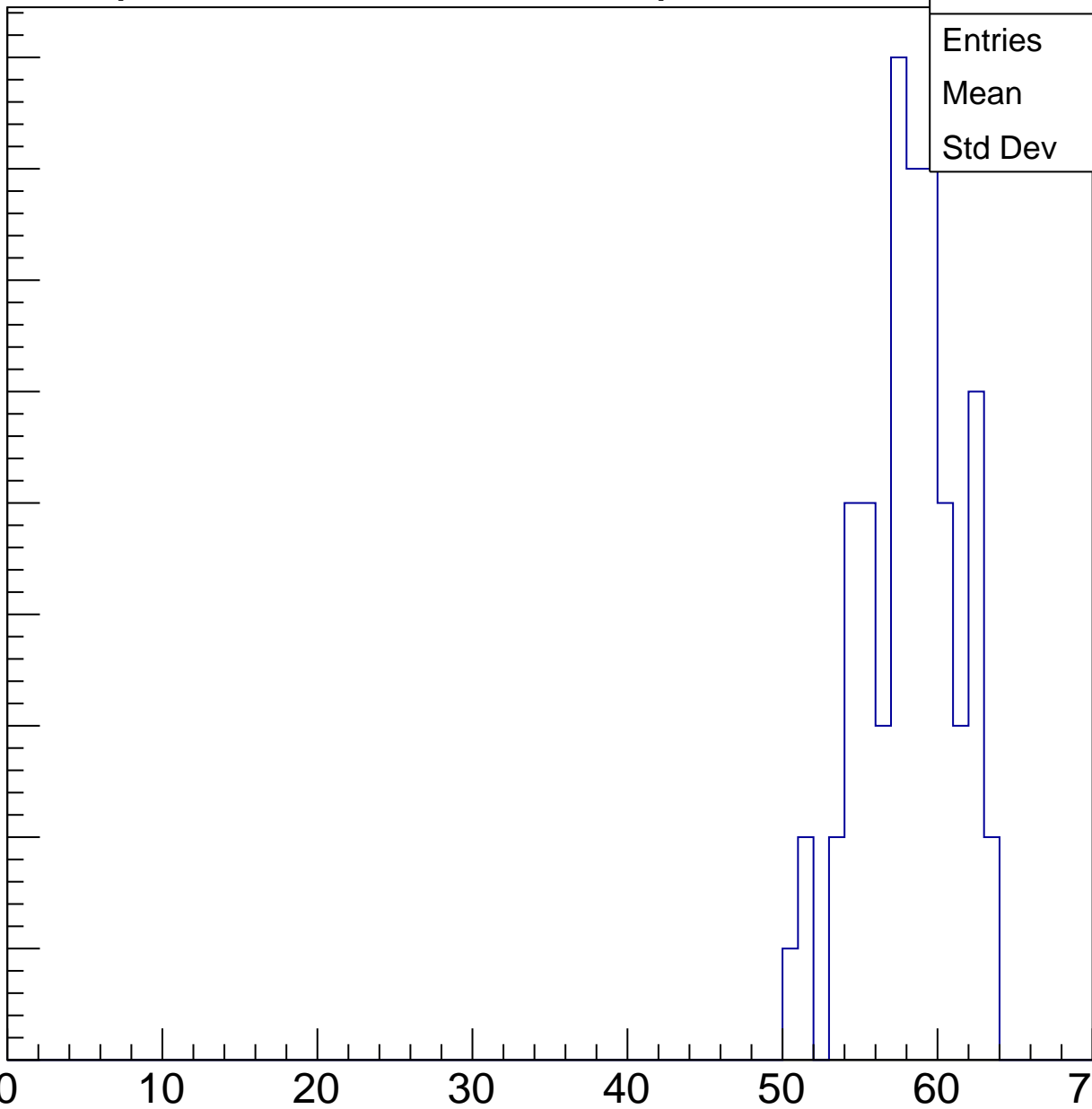
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	57.64
Std Dev	3.057

ampl

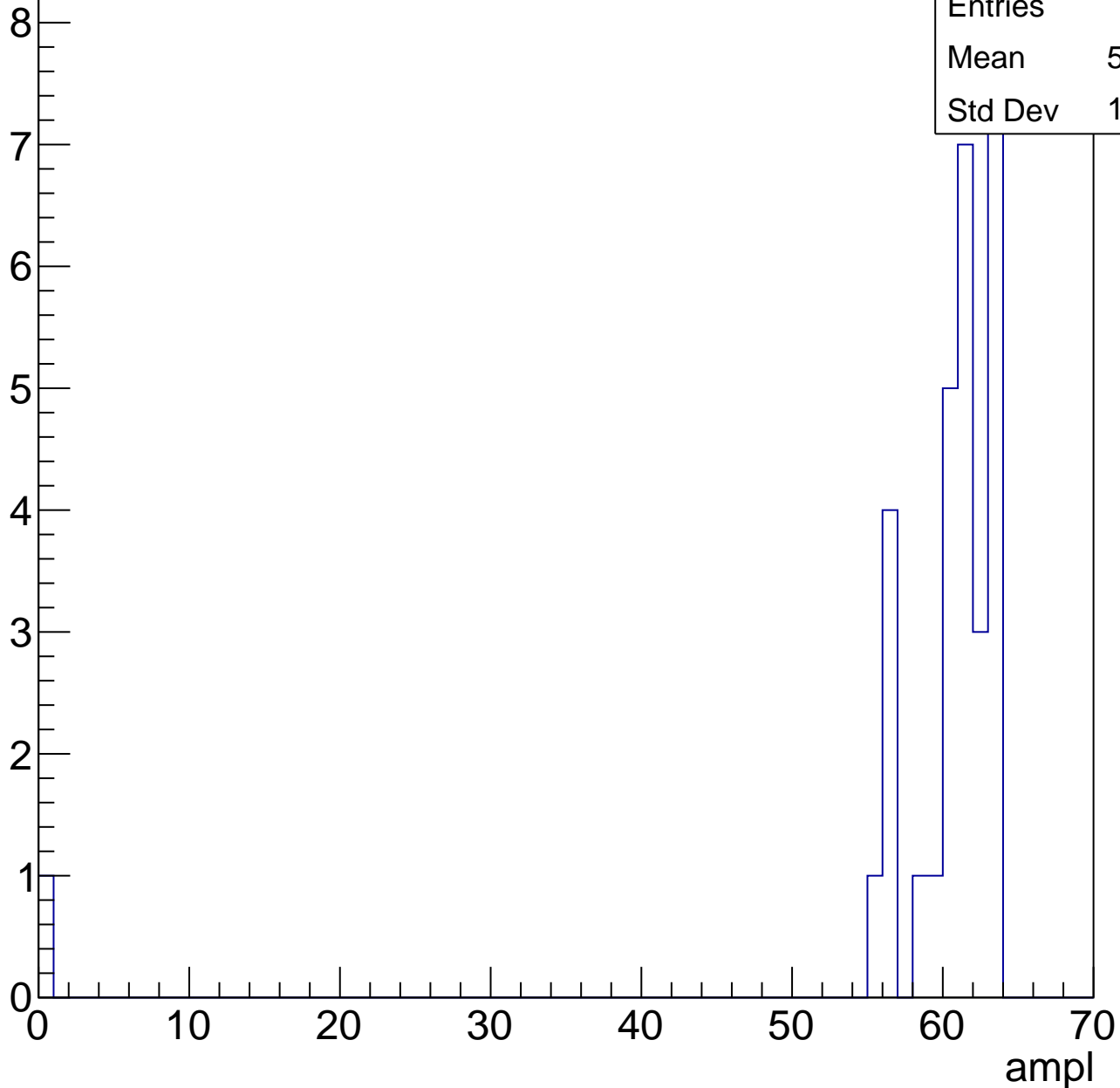


B1L100S, U11-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.48
Std Dev	10.95



B1L100S, U11-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U11-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch32, adc0

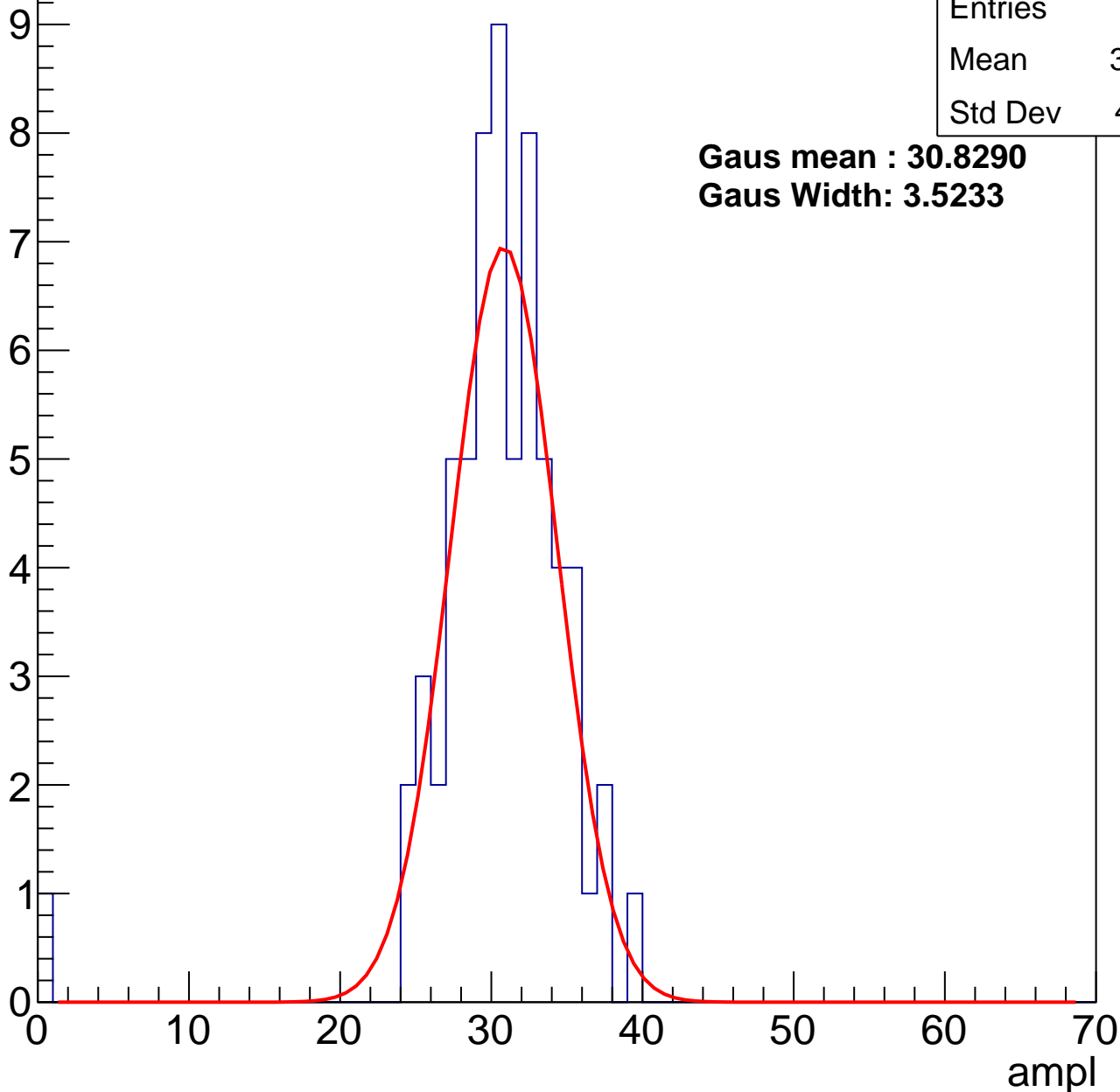
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	30.05
Std Dev	4.981

Gaus mean : 30.8290

Gaus Width: 3.5233



B1L100S, U11-ch32, adc1

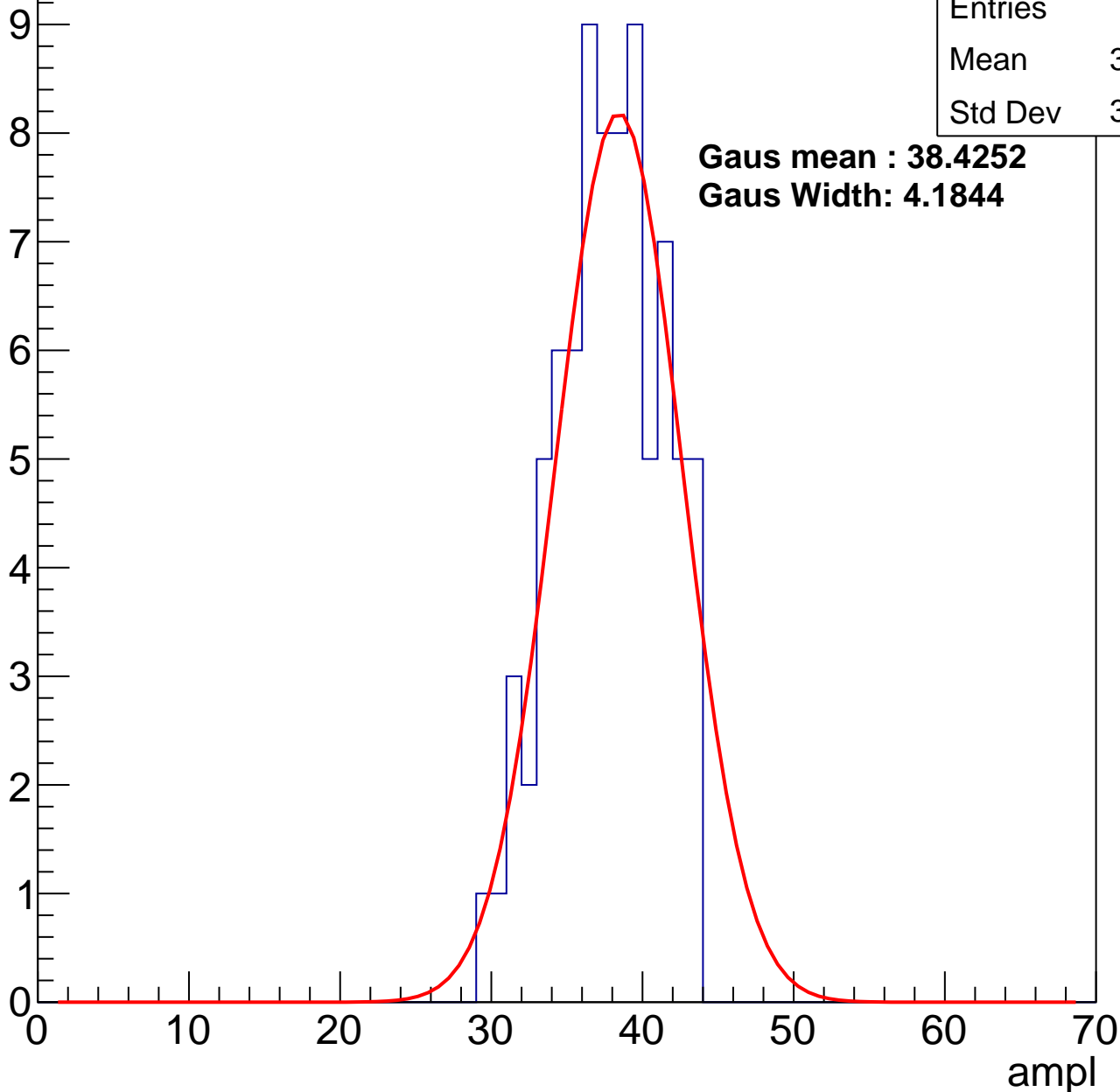
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	37.27
Std Dev	3.424

Gaus mean : 38.4252

Gaus Width: 4.1844



B1L100S, U11-ch32, adc2

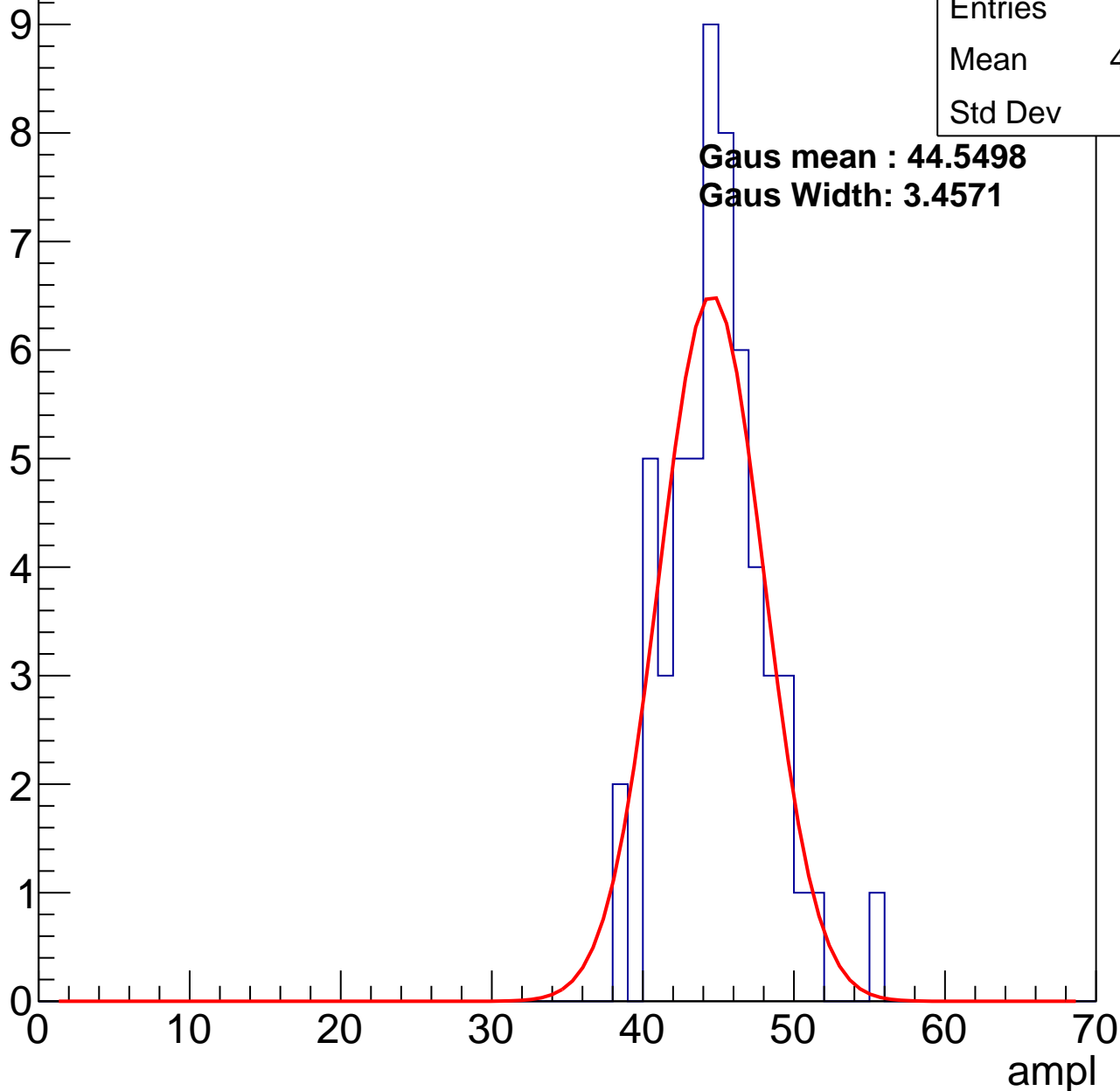
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.48
Std Dev	3.24

Gaus mean : 44.5498

Gaus Width: 3.4571

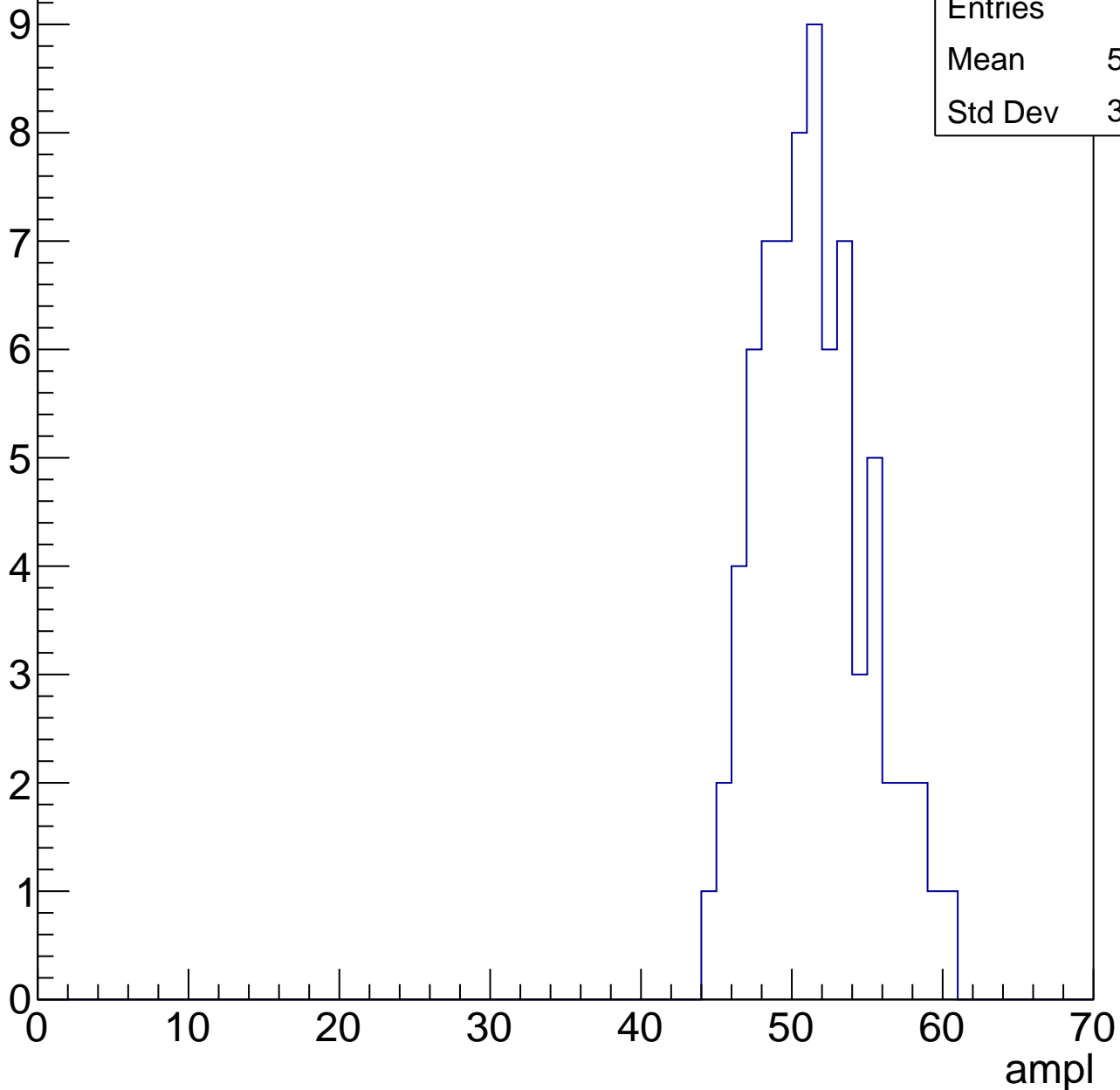


B1L100S, U11-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	50.95
Std Dev	3.553

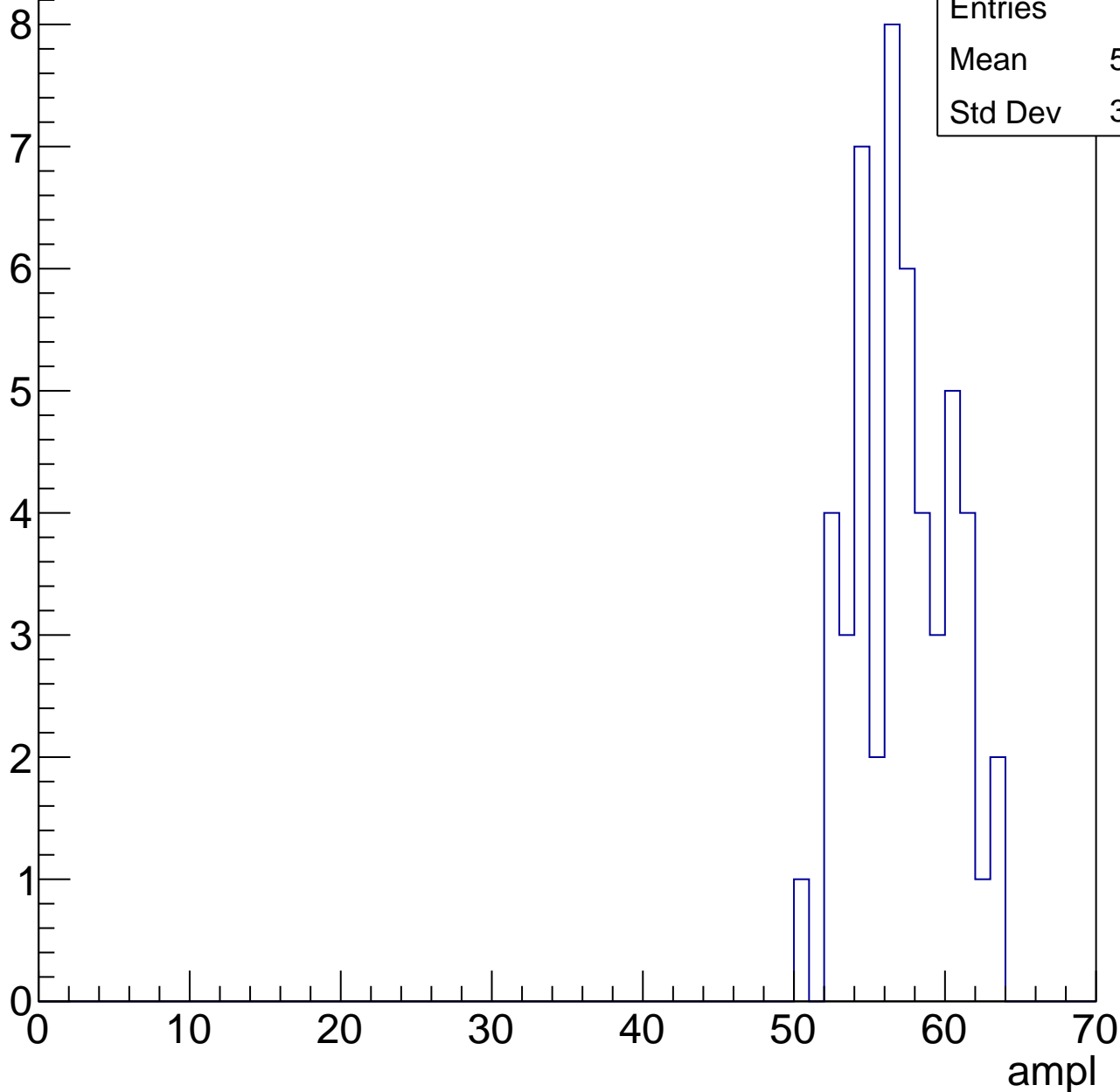


B1L100S, U11-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	56.72
Std Dev	3.144

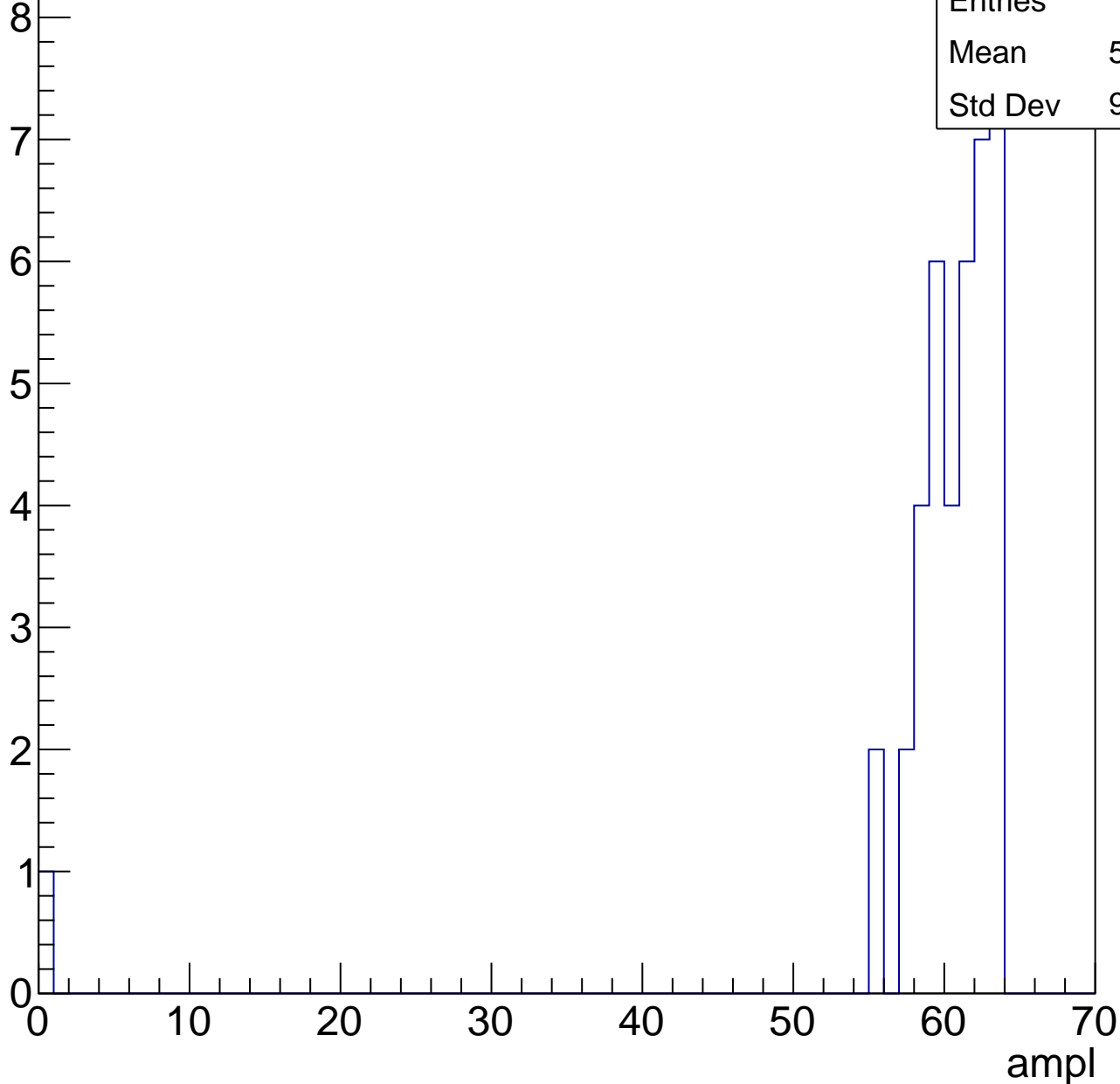


B1L100S, U11-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	58.85
Std Dev	9.674



B1L100S, U11-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch32, adc7

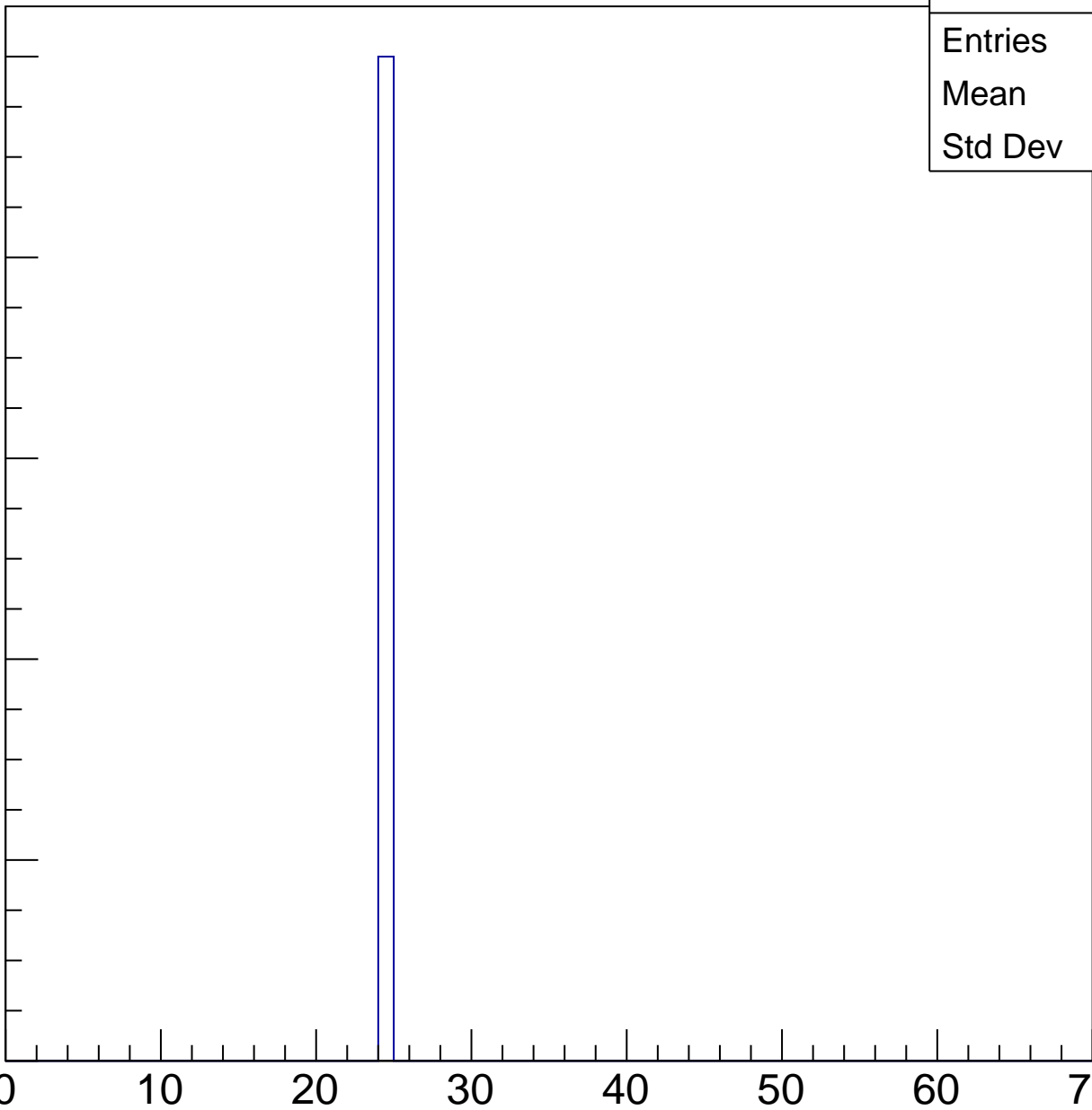
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L100S, U11-ch33, adc0

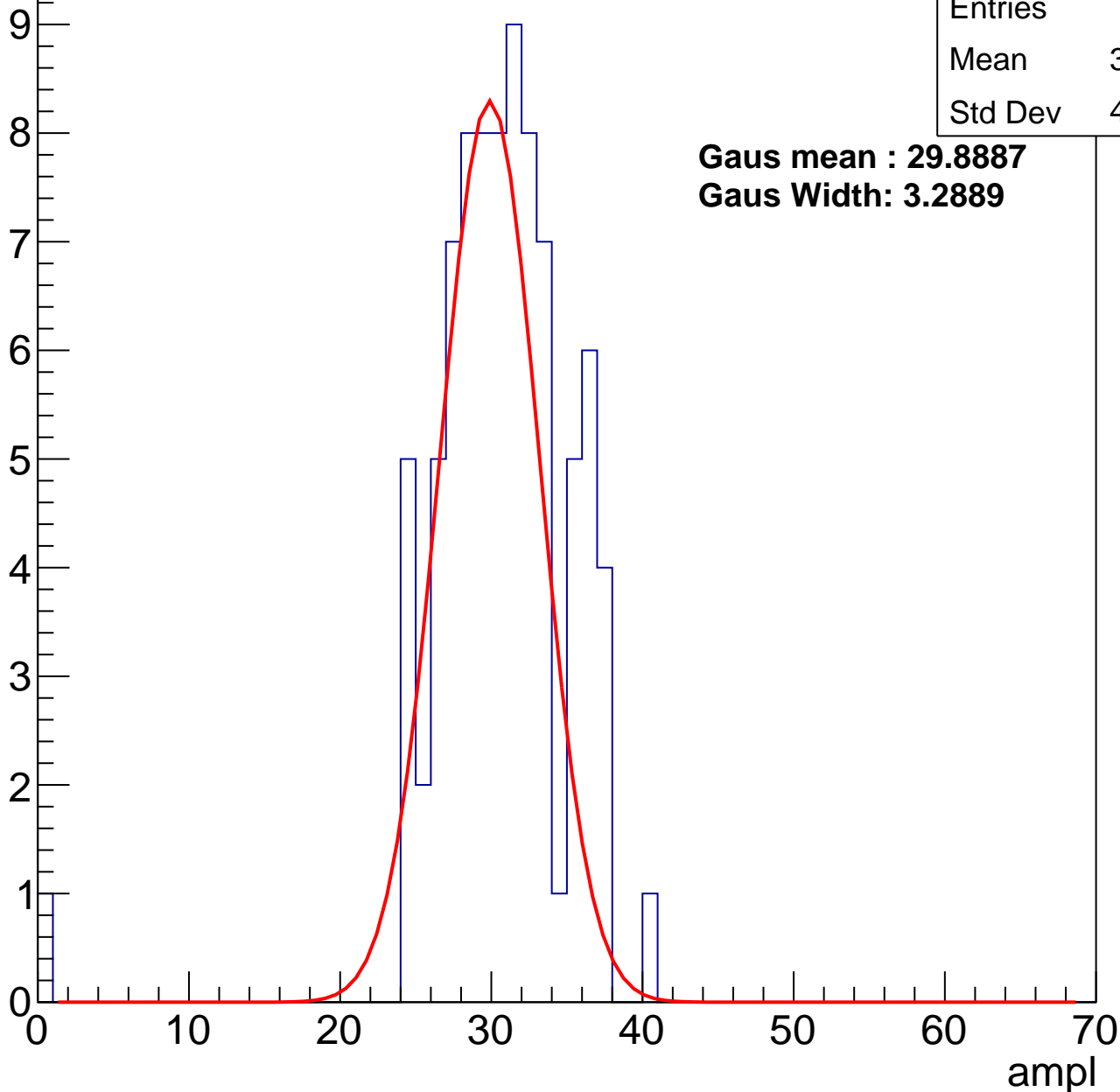
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	30.16
Std Dev	4.932

Gaus mean : 29.8887

Gaus Width: 3.2889

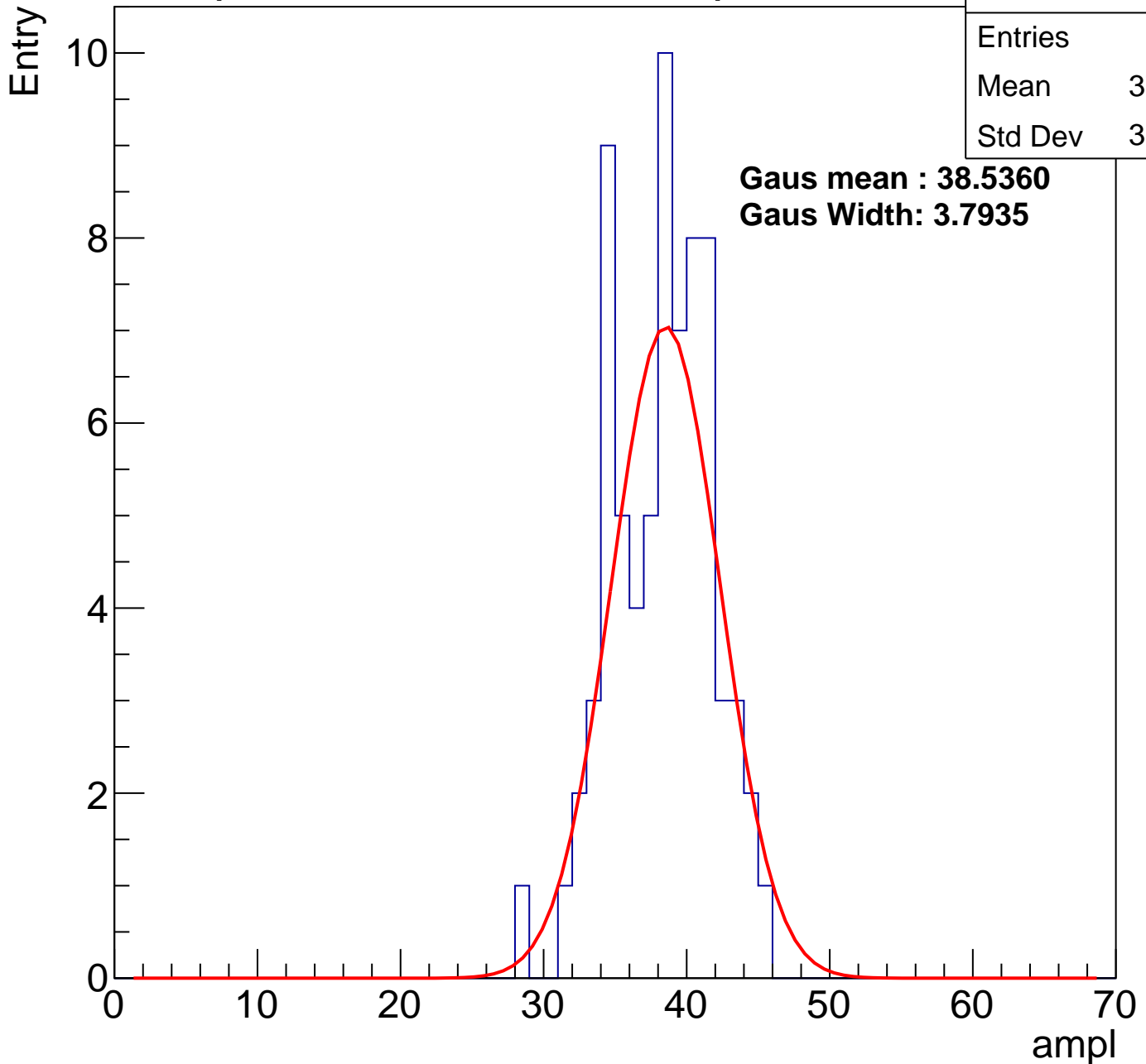


B1L100S, U11-ch33, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	37.79
Std Dev	3.452

Gaus mean : 38.5360
Gaus Width: 3.7935



B1L100S, U11-ch33, adc2

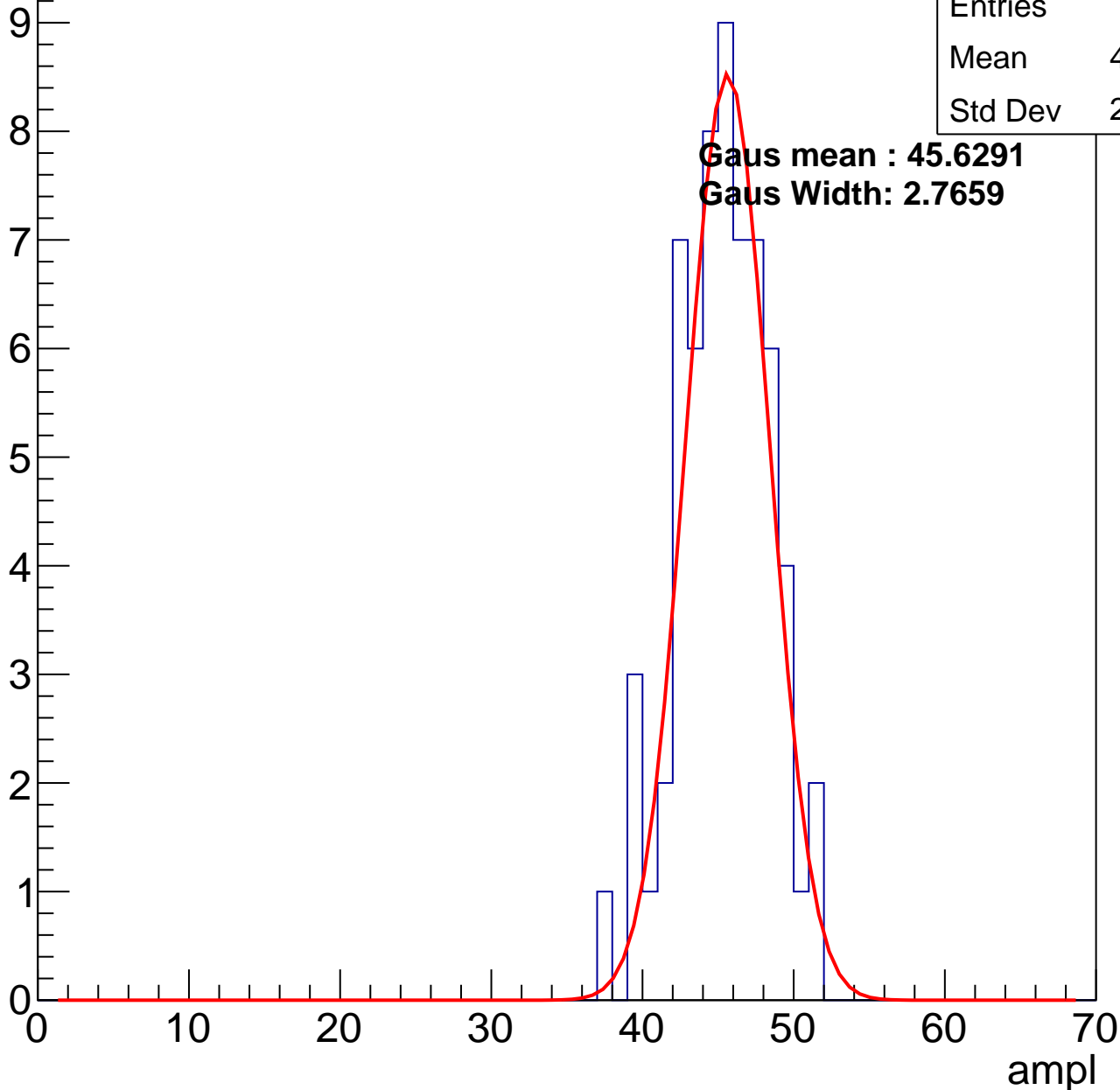
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.88
Std Dev	2.992

Gaus mean : 45.6291

Gaus Width: 2.7659

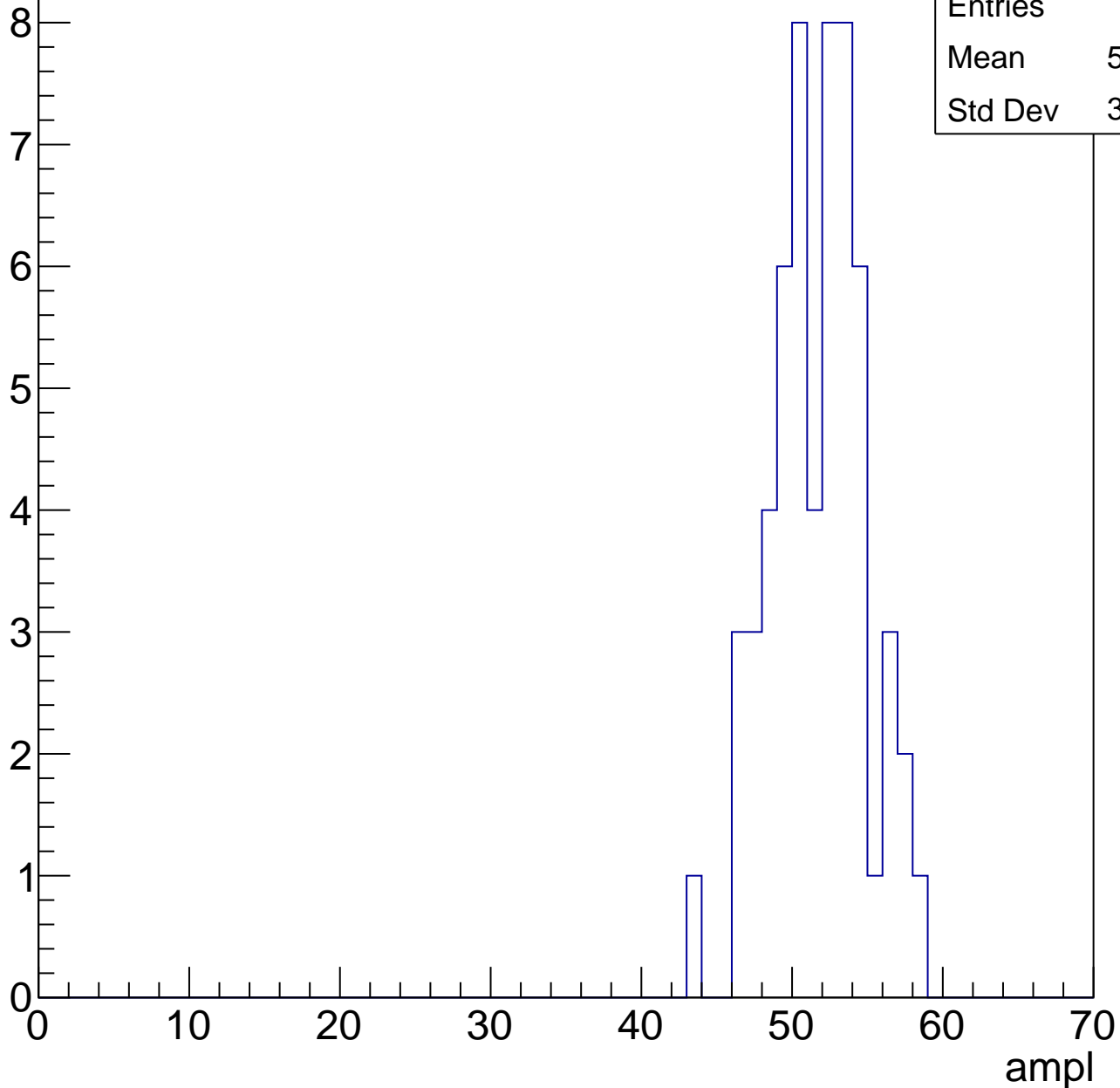


B1L100S, U11-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	51.22
Std Dev	3.102

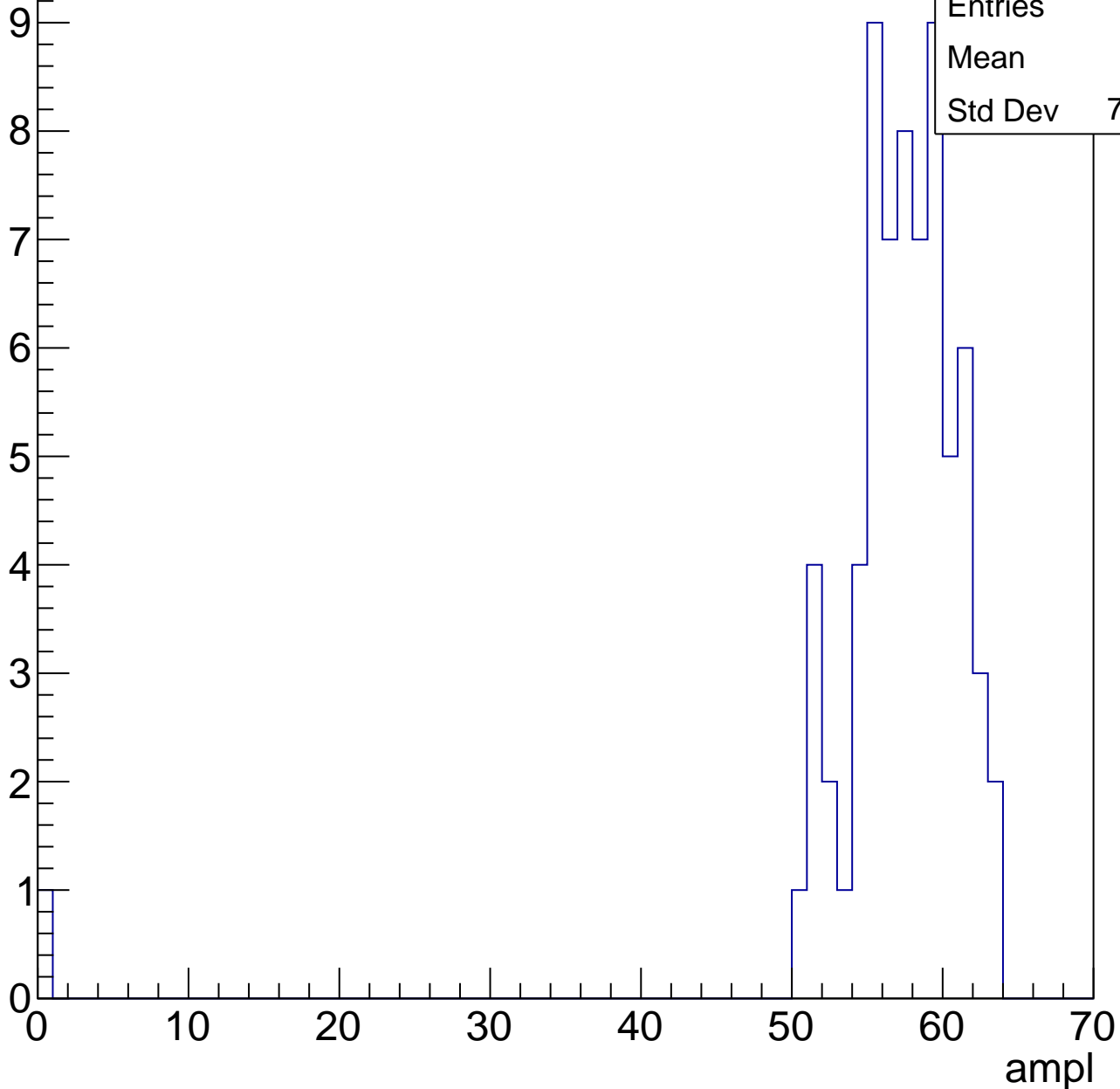


B1L100S, U11-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	56.3
Std Dev	7.503

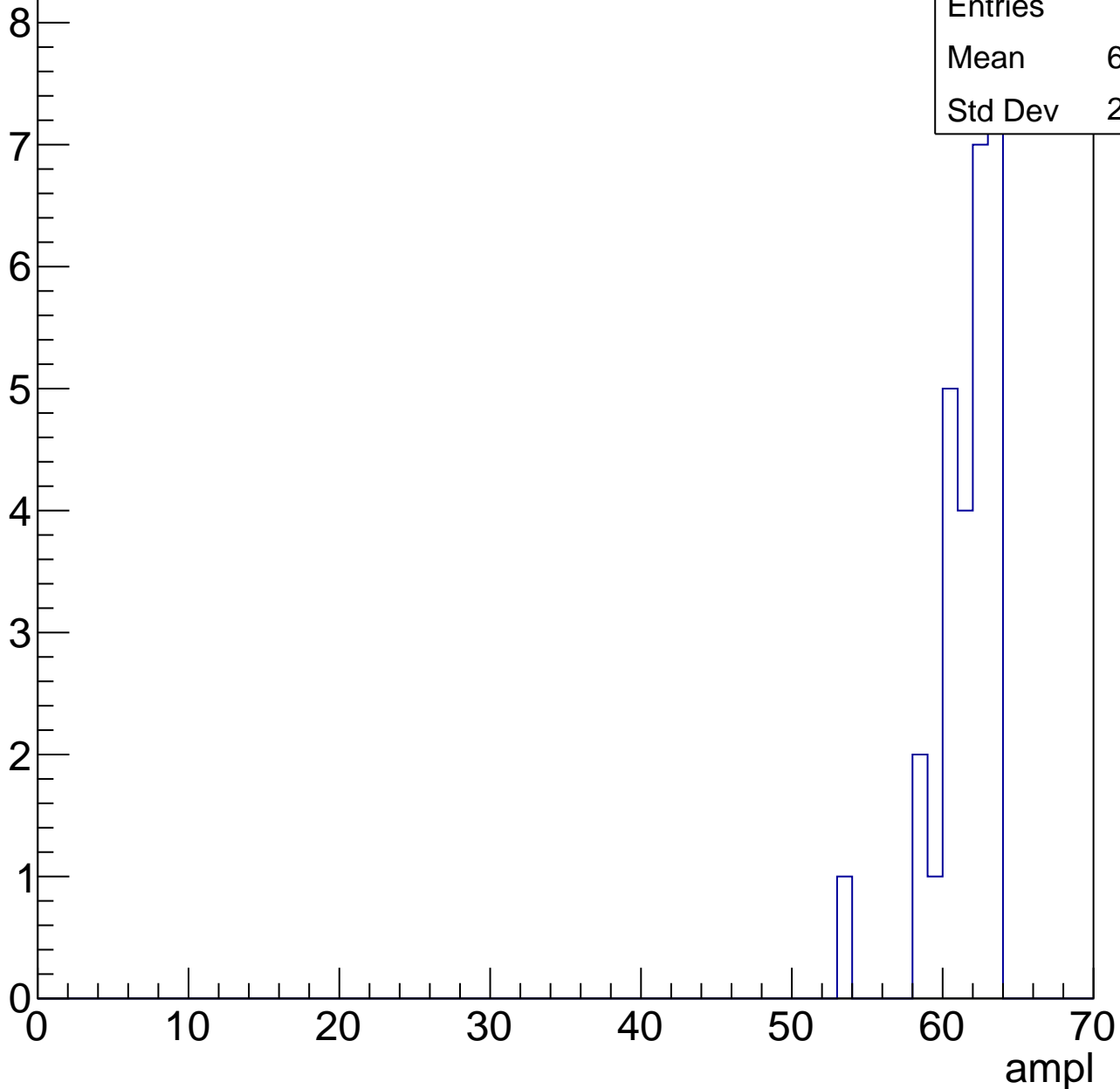


B1L100S, U11-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	61.07
Std Dev	2.154



B1L100S, U11-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	11
Std Dev	11

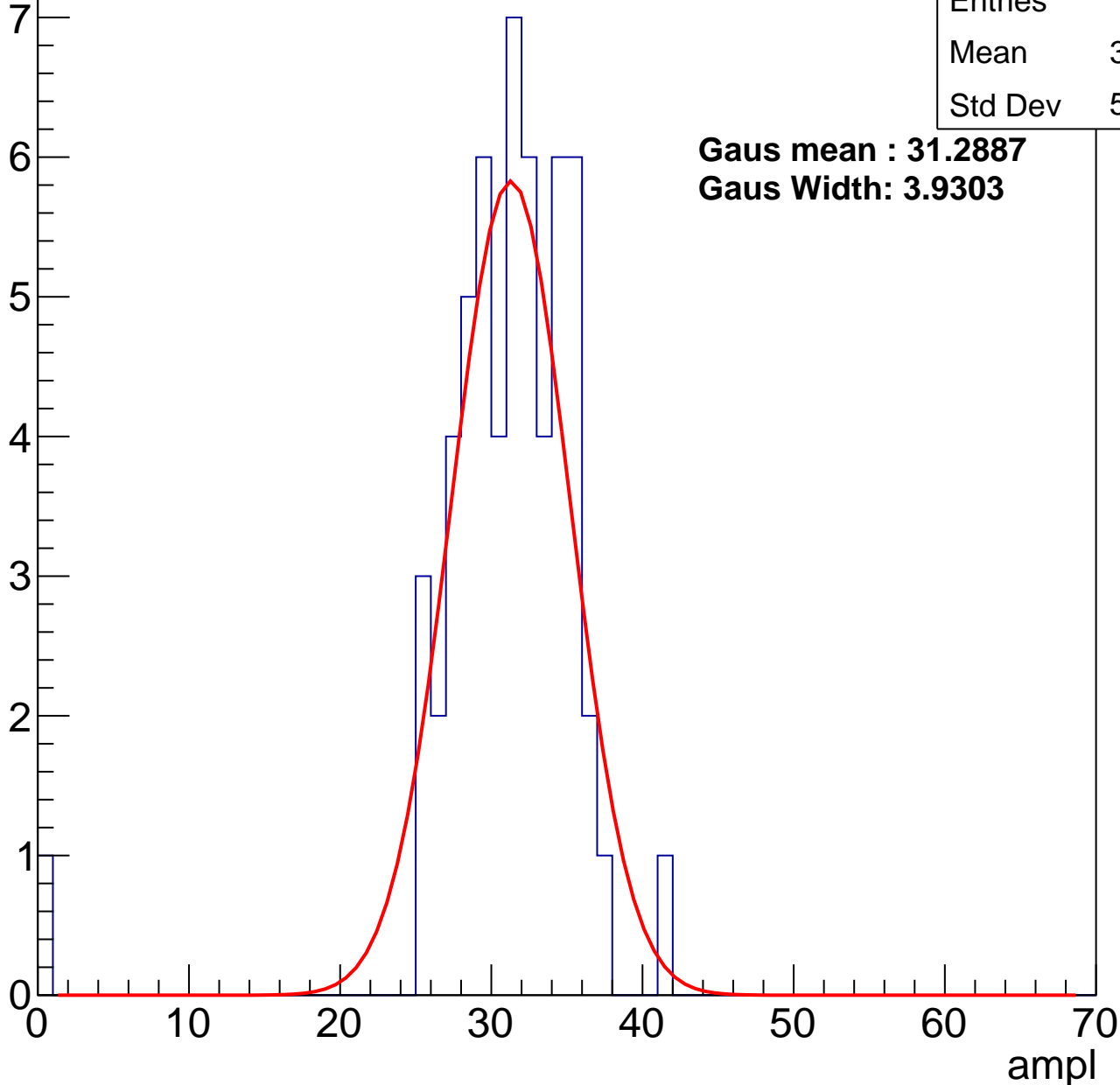
B1L100S, U11-ch34, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	30.59
Std Dev	5.259

Gaus mean : 31.2887
Gaus Width: 3.9303



B1L100S, U11-ch34, adc1

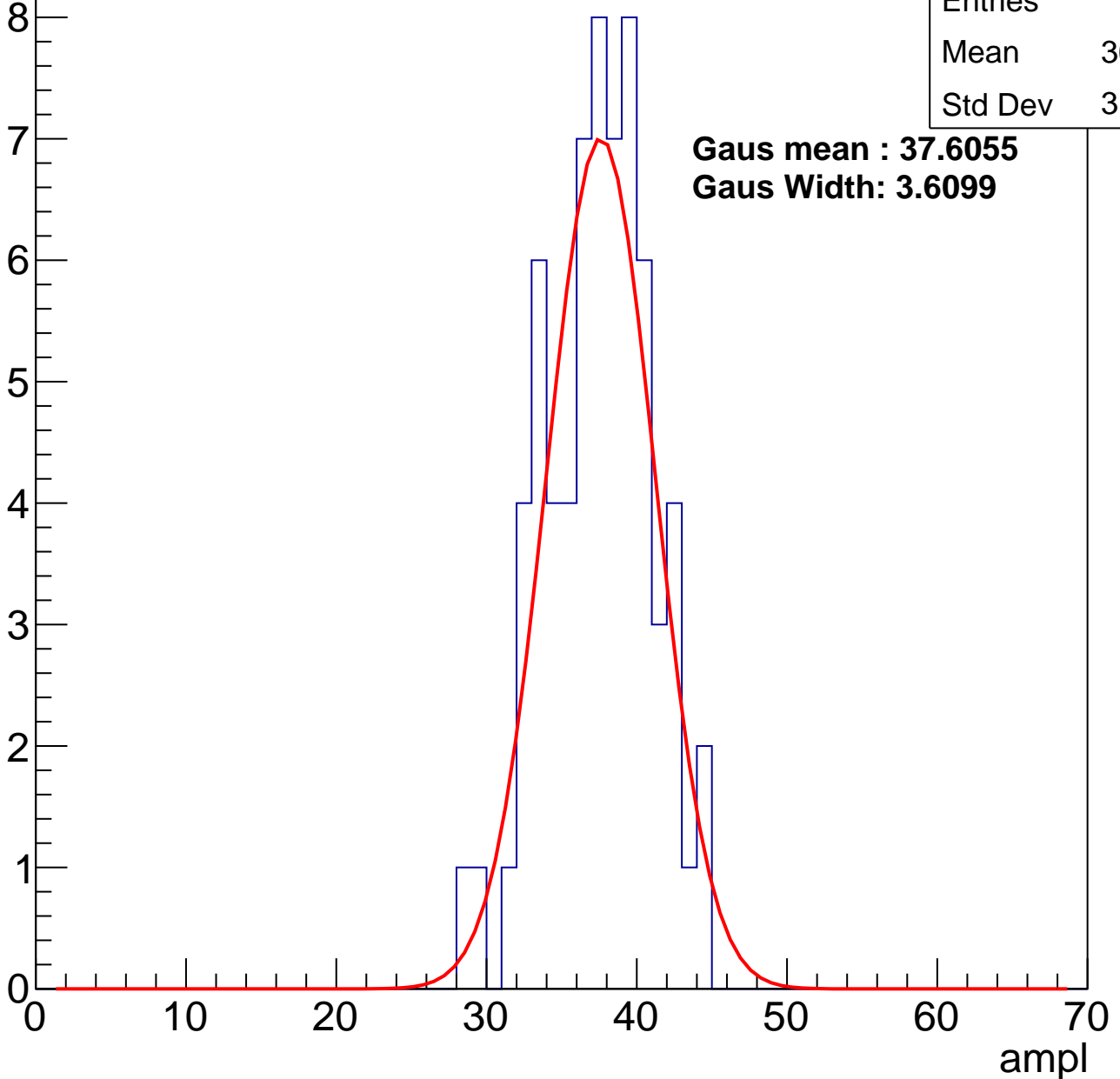
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	36.99
Std Dev	3.479

Gaus mean : 37.6055

Gaus Width: 3.6099



B1L100S, U11-ch34, adc2

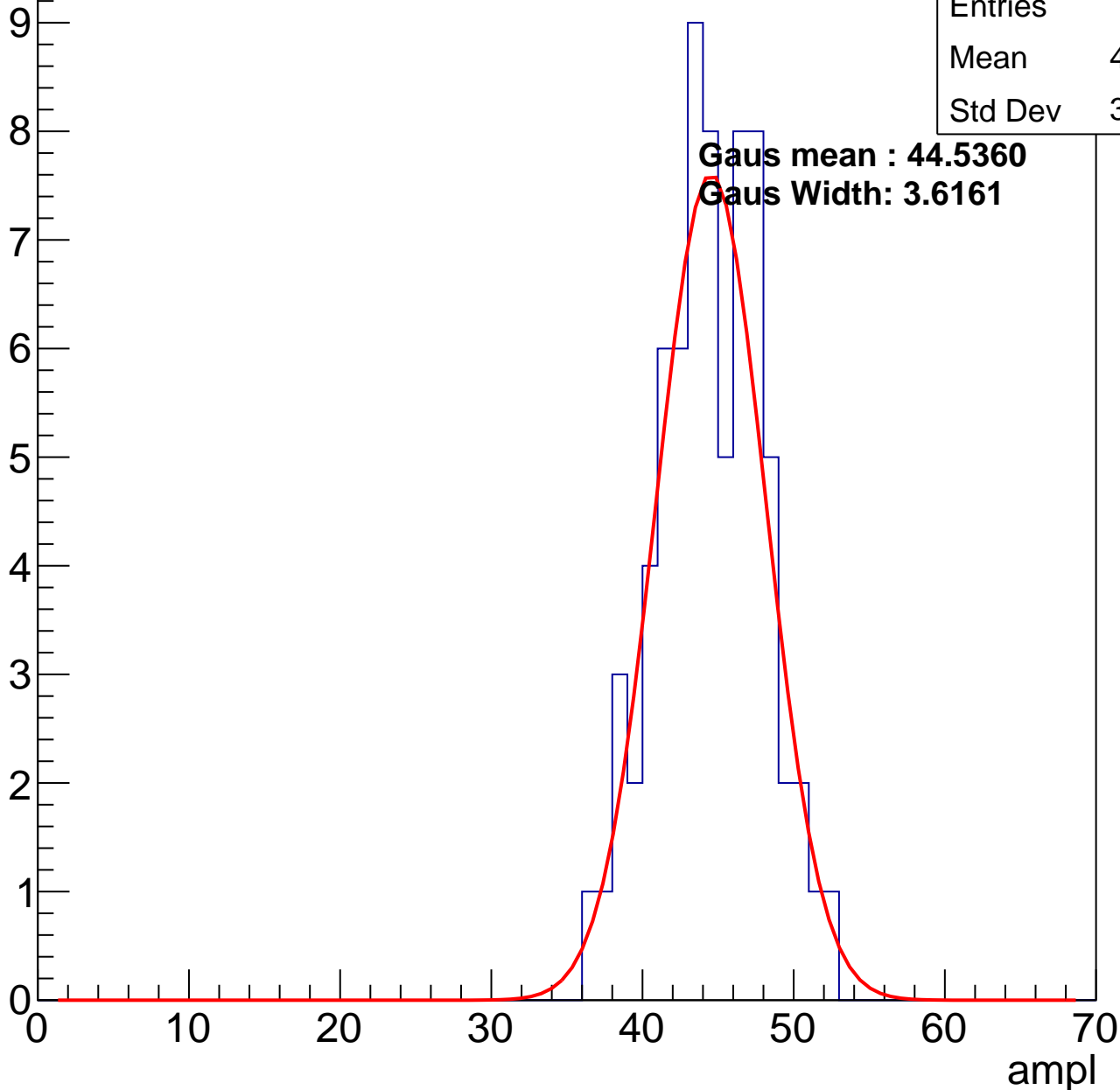
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	44.06
Std Dev	3.427

Gaus mean : 44.5360

Gaus Width: 3.6161

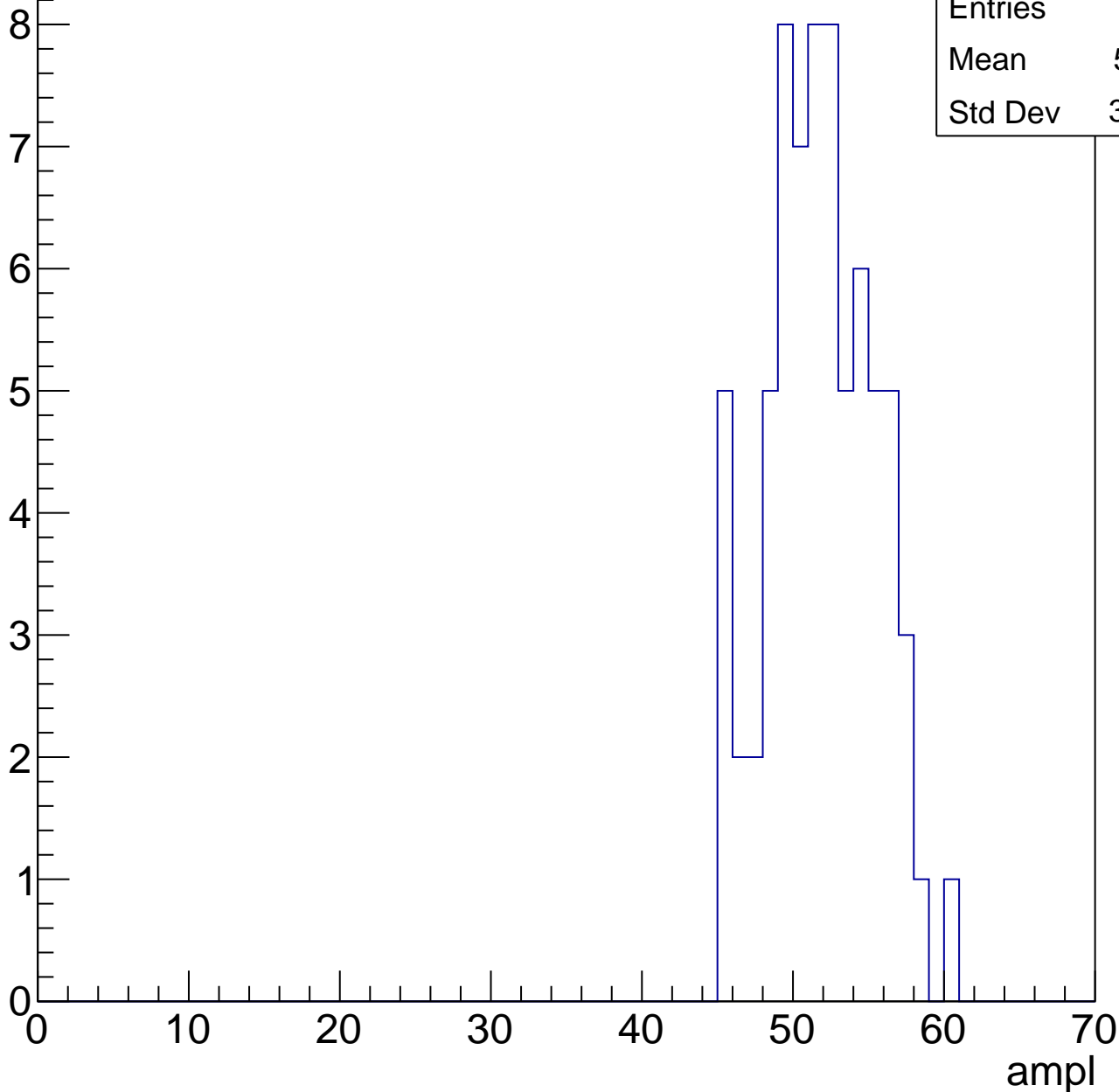


B1L100S, U11-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

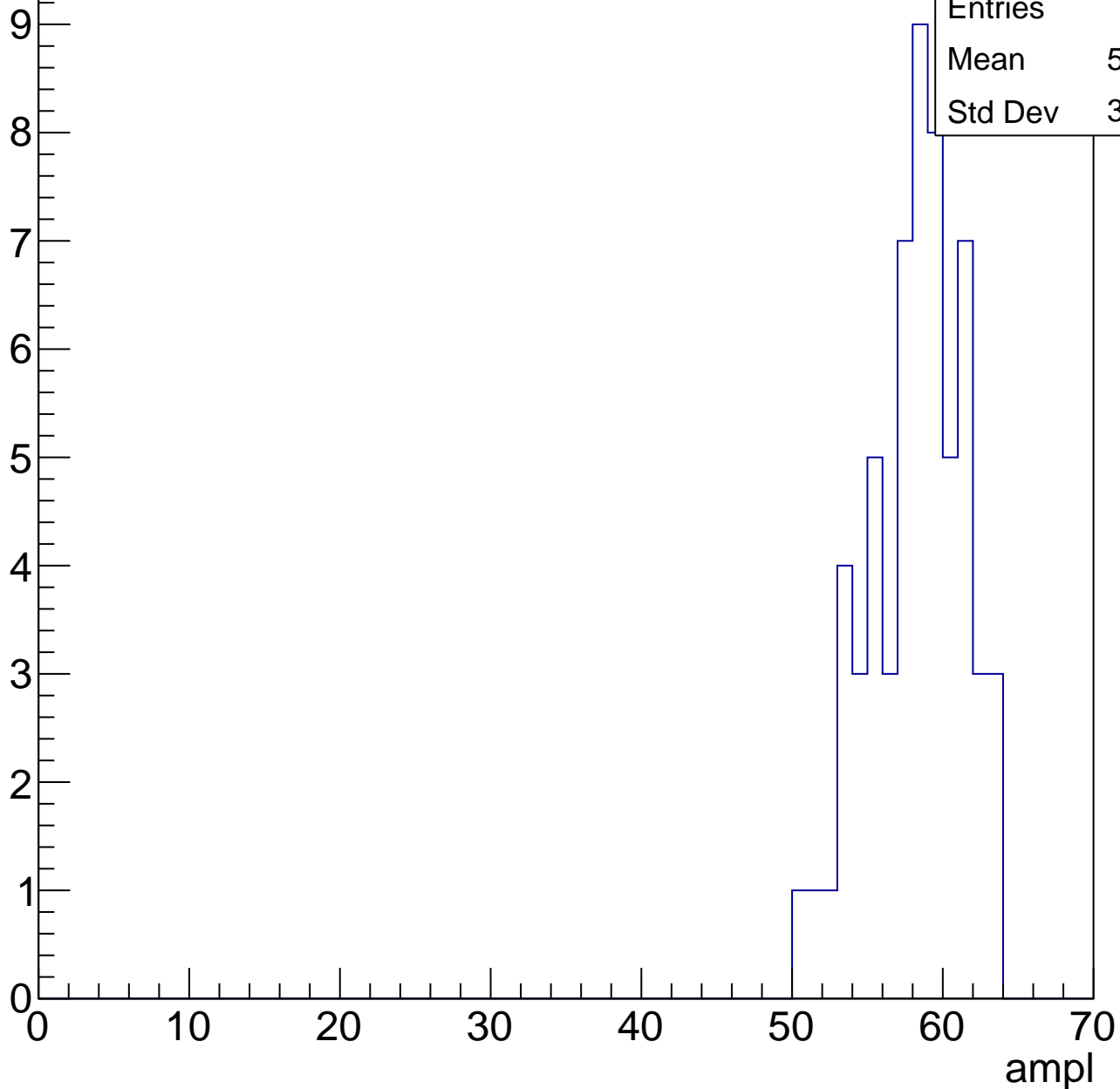
Entries	71
Mean	51.41
Std Dev	3.479



B1L100S, U11-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

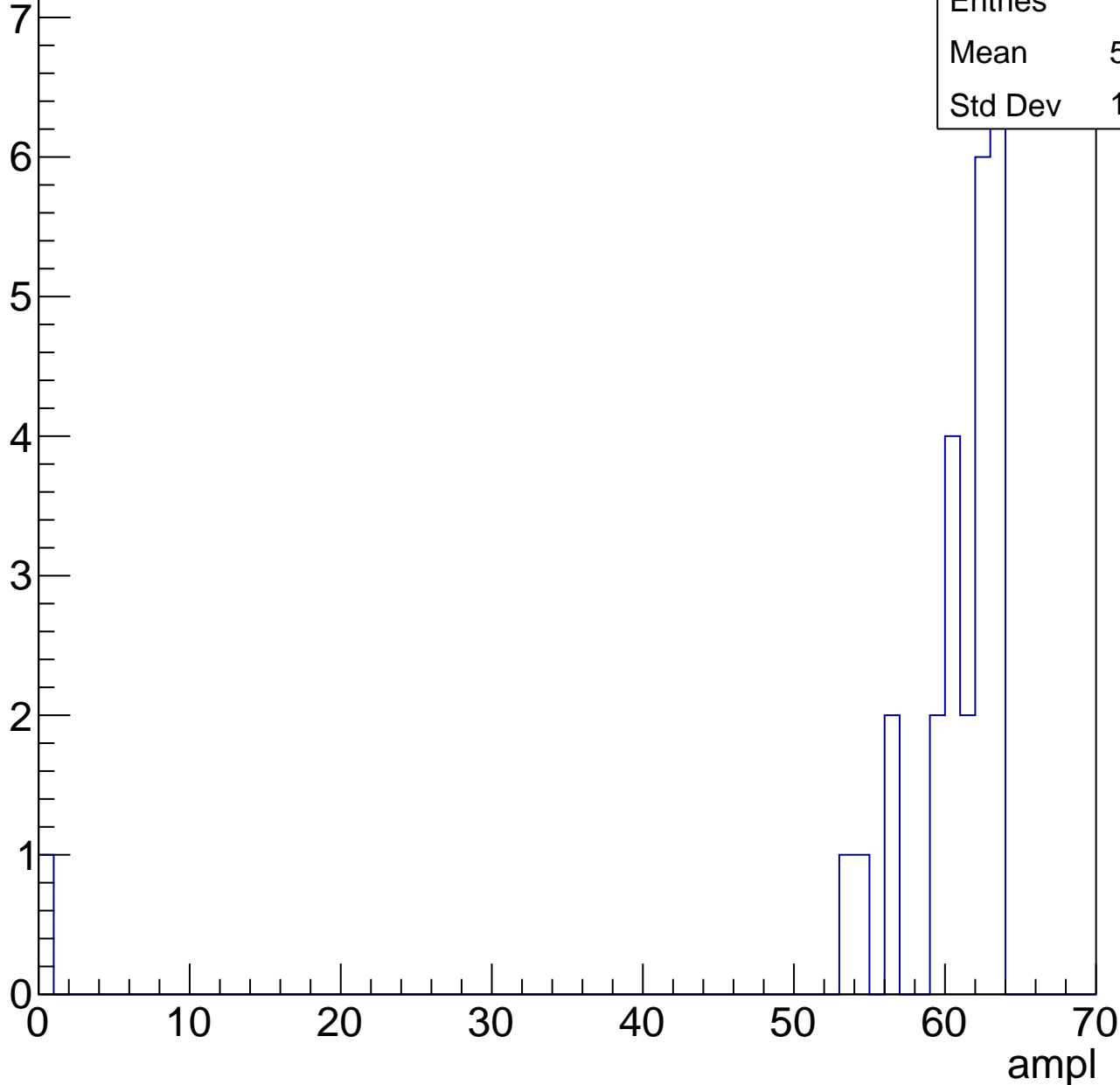


B1L100S, U11-ch34, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.15
Std Dev	11.96



B1L100S, U11-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch35, adc0

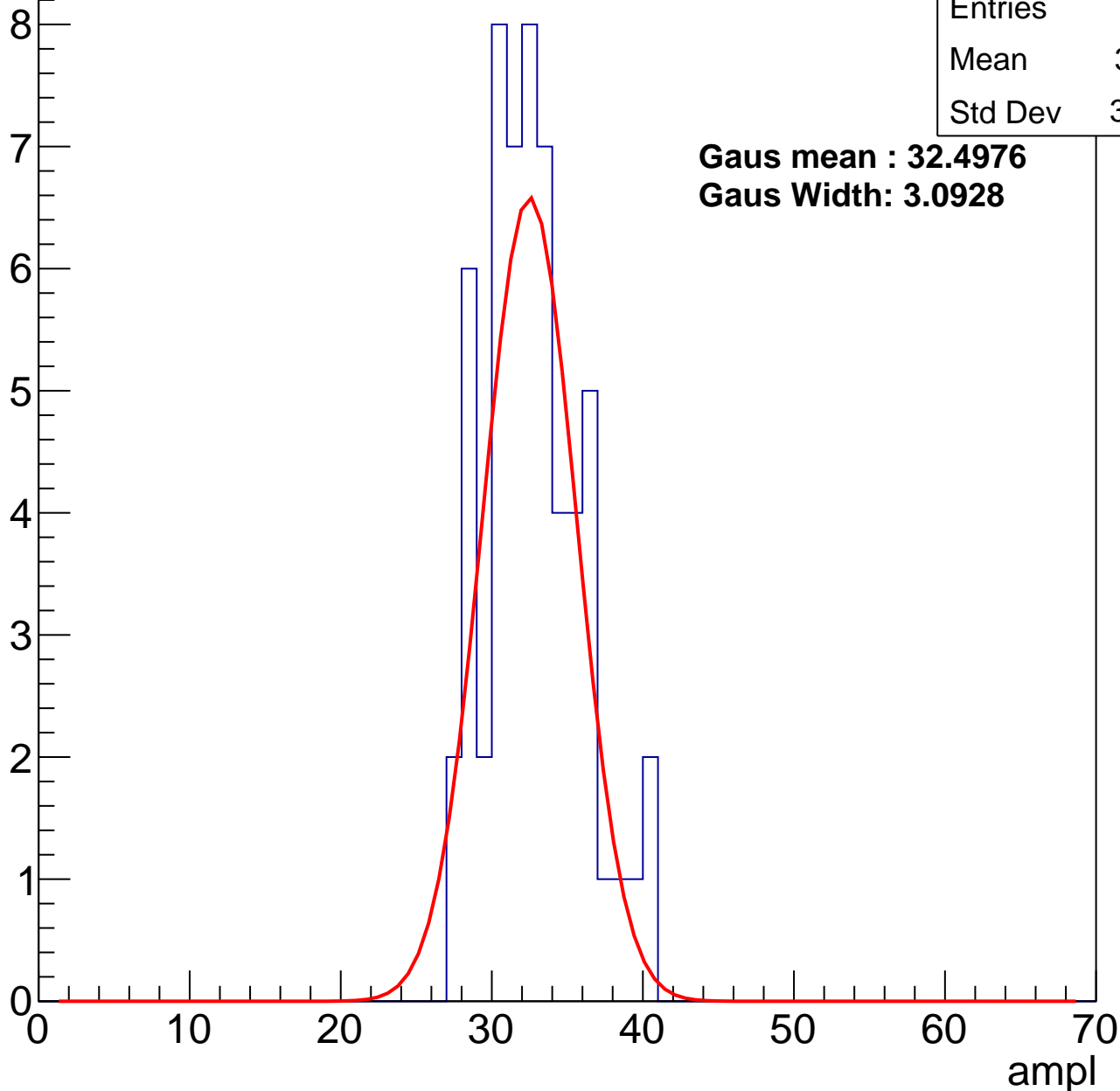
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	32.31
Std Dev	3.147

Gaus mean : 32.4976

Gaus Width: 3.0928



B1L100S, U11-ch35, adc1

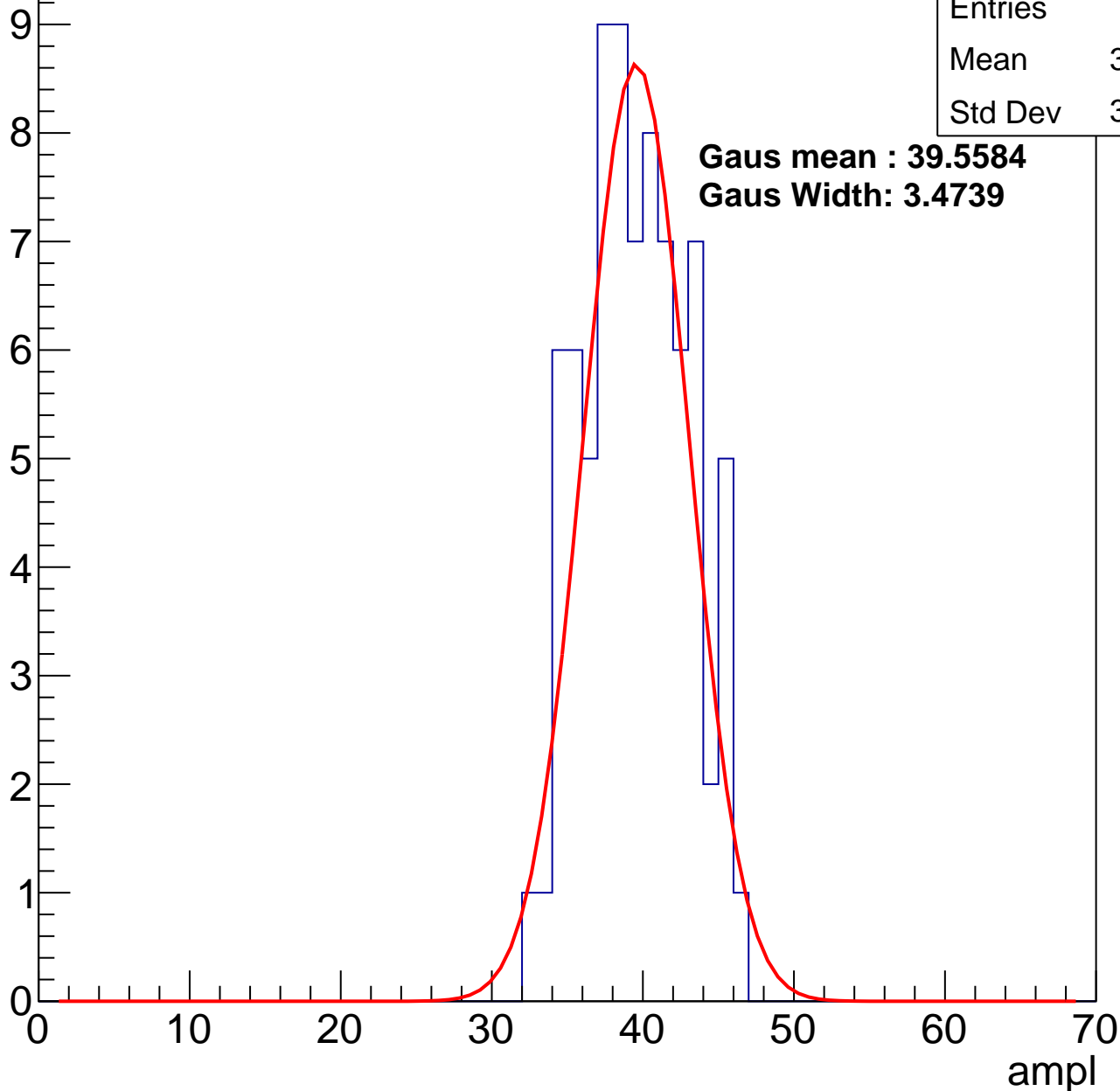
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	39.08
Std Dev	3.357

Gaus mean : 39.5584

Gaus Width: 3.4739



B1L100S, U11-ch35, adc2

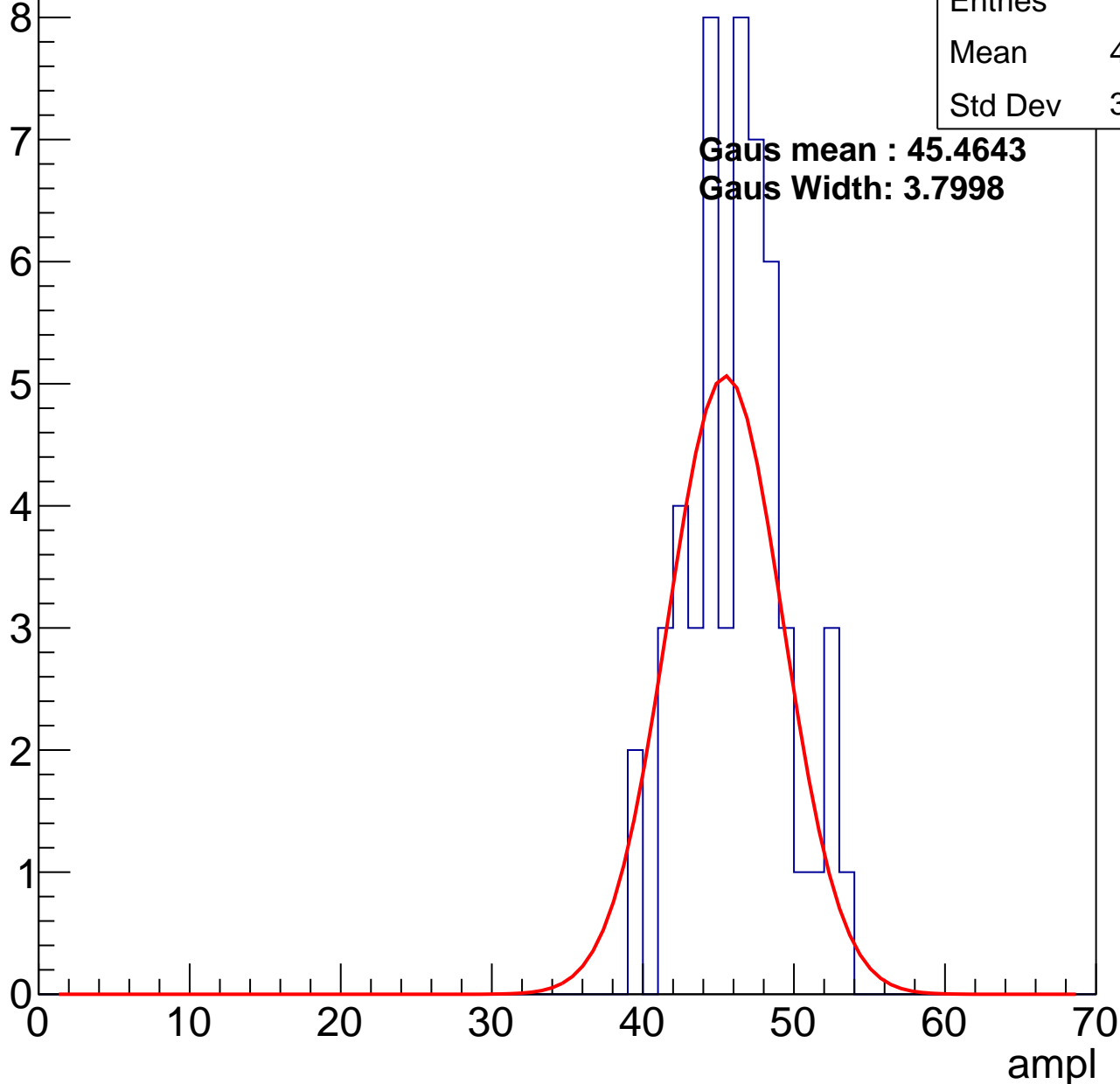
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	45.79
Std Dev	3.229

Gaus mean : 45.4643

Gaus Width: 3.7998

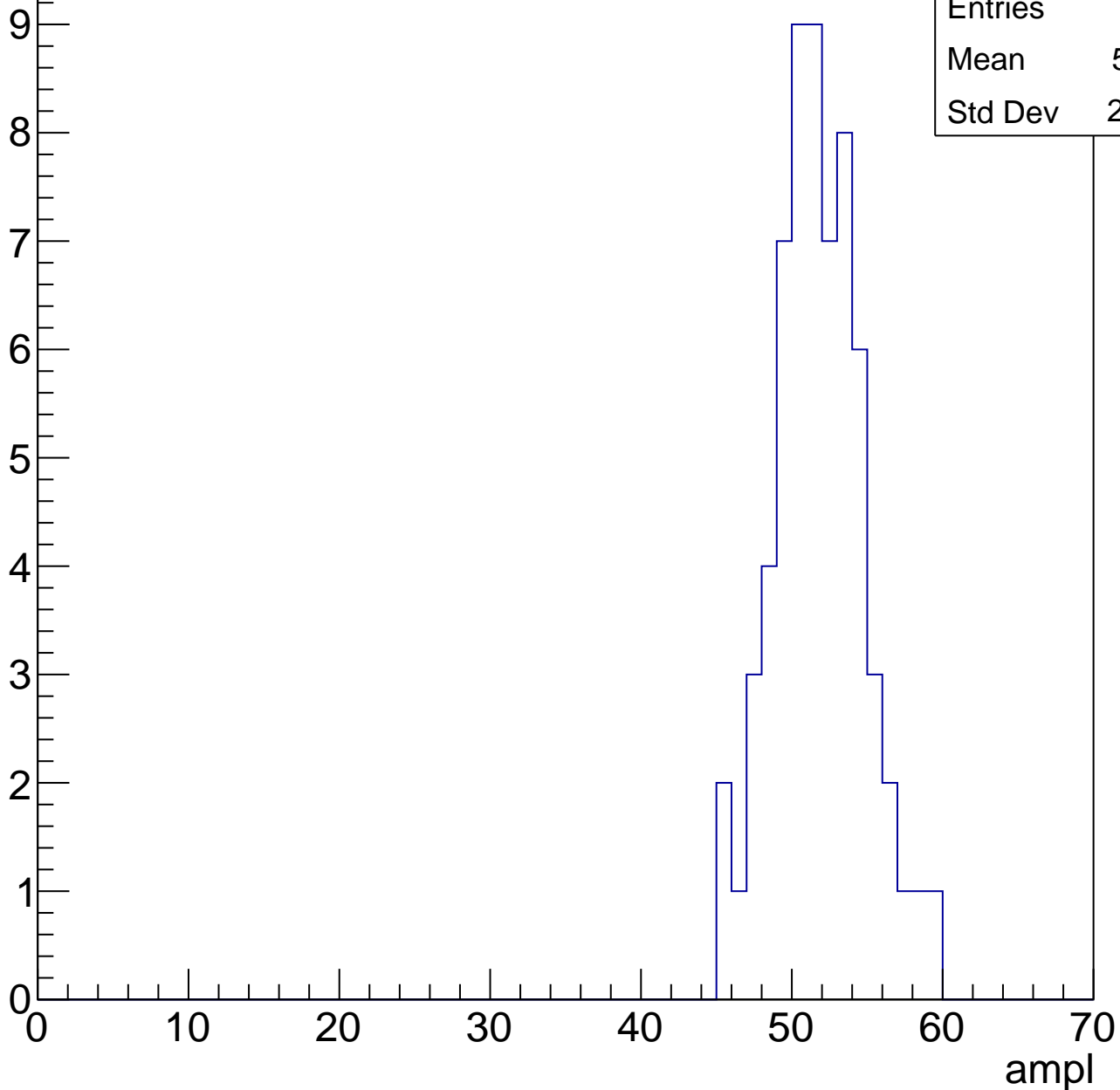


B1L100S, U11-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

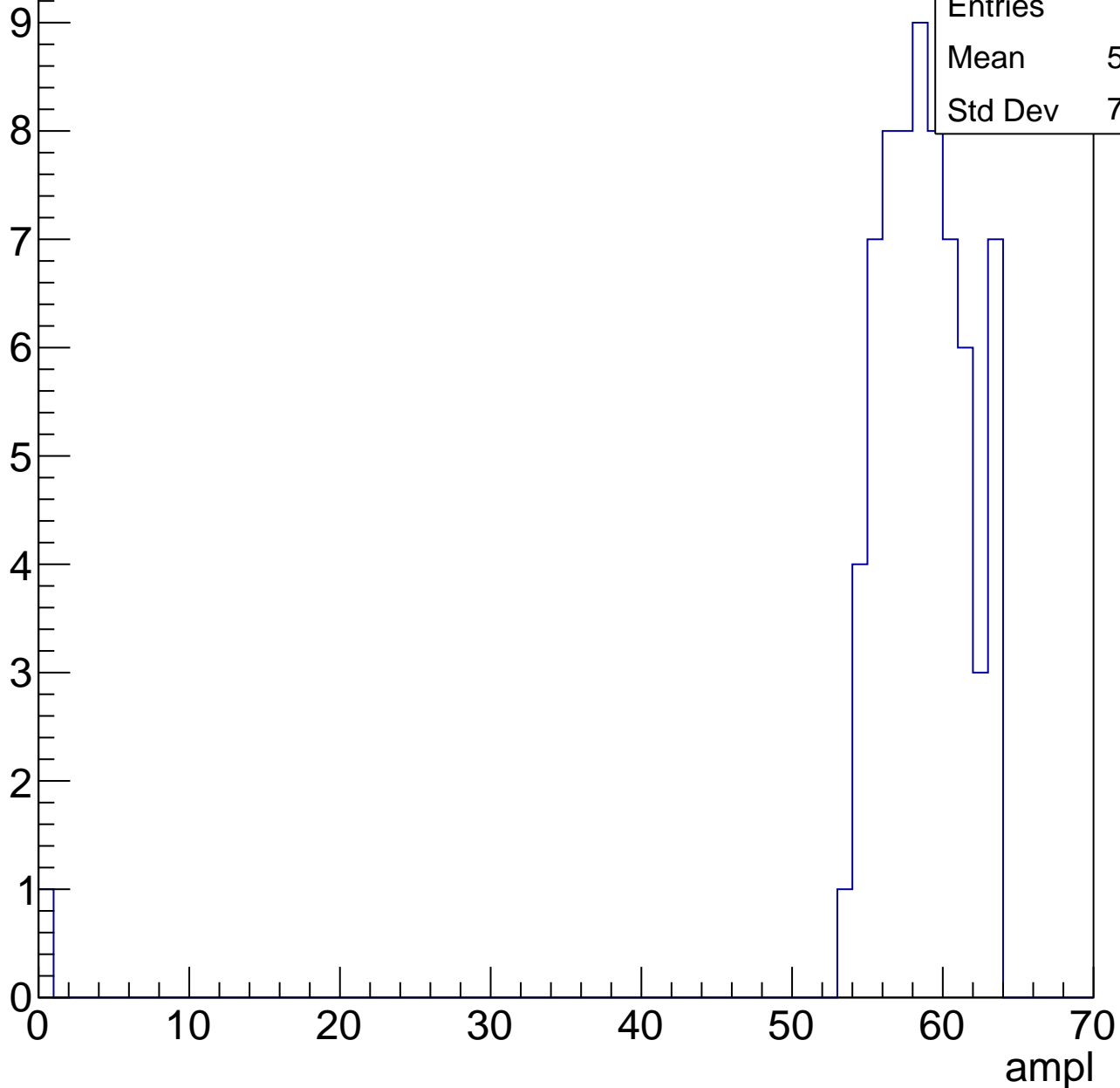
Entries	64
Mean	51.31
Std Dev	2.936



B1L100S, U11-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



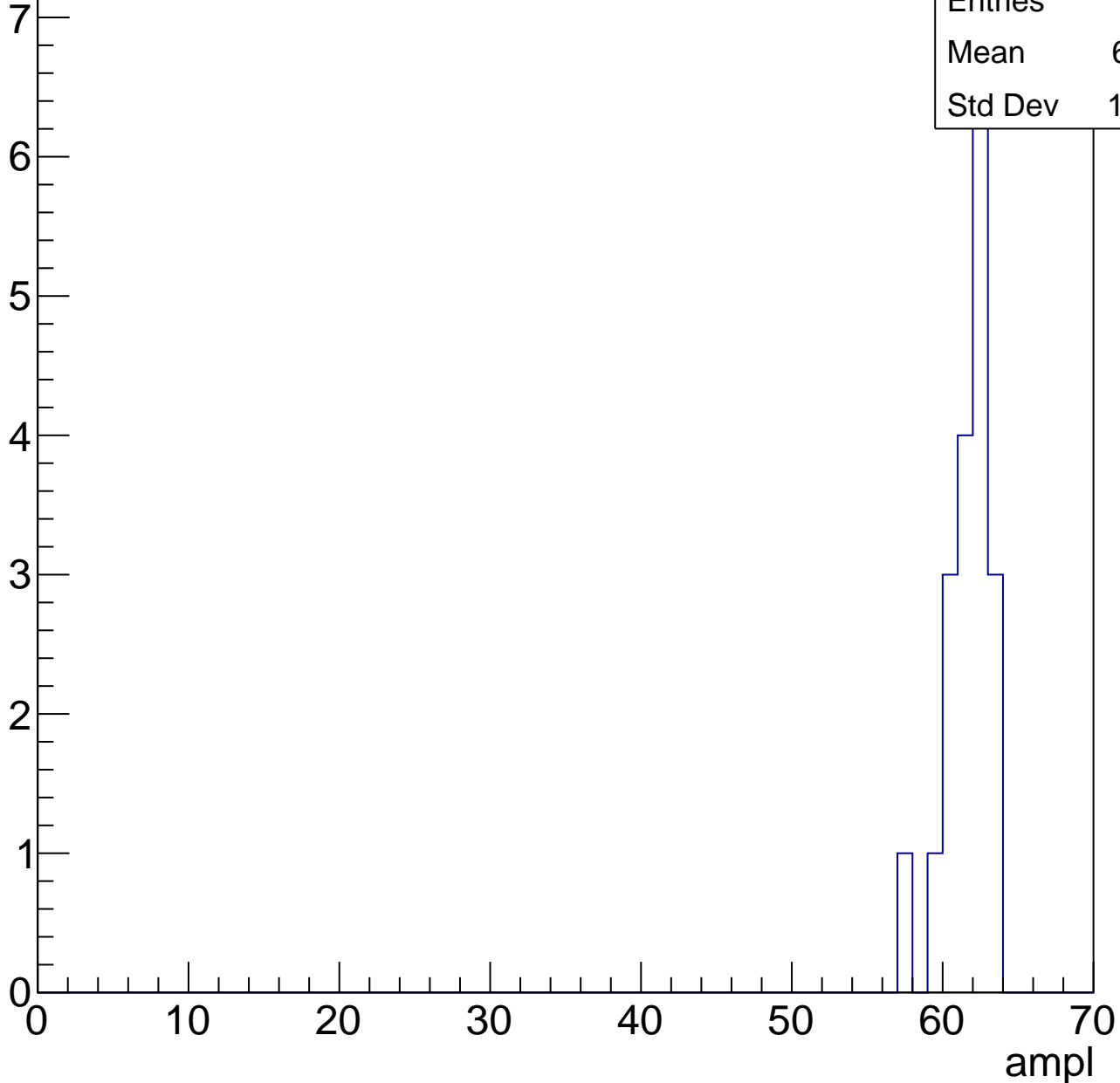
Entries	69
Mean	57.46
Std Dev	7.469

B1L100S, U11-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	61.21
Std Dev	1.472



B1L100S, U11-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

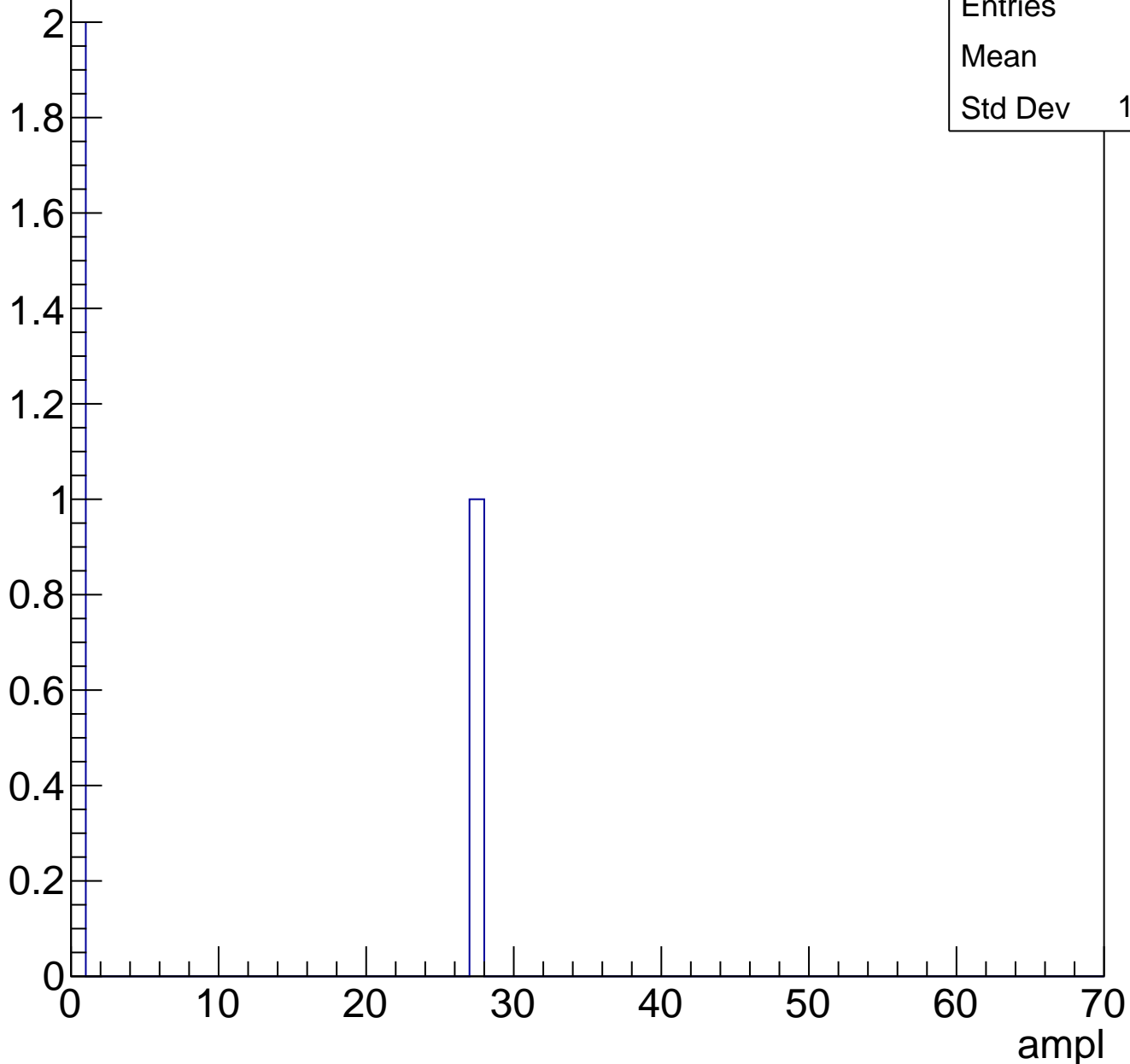
Entry



B1L100S, U11-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	9
Std Dev	12.73

B1L100S, U11-ch36, adc0

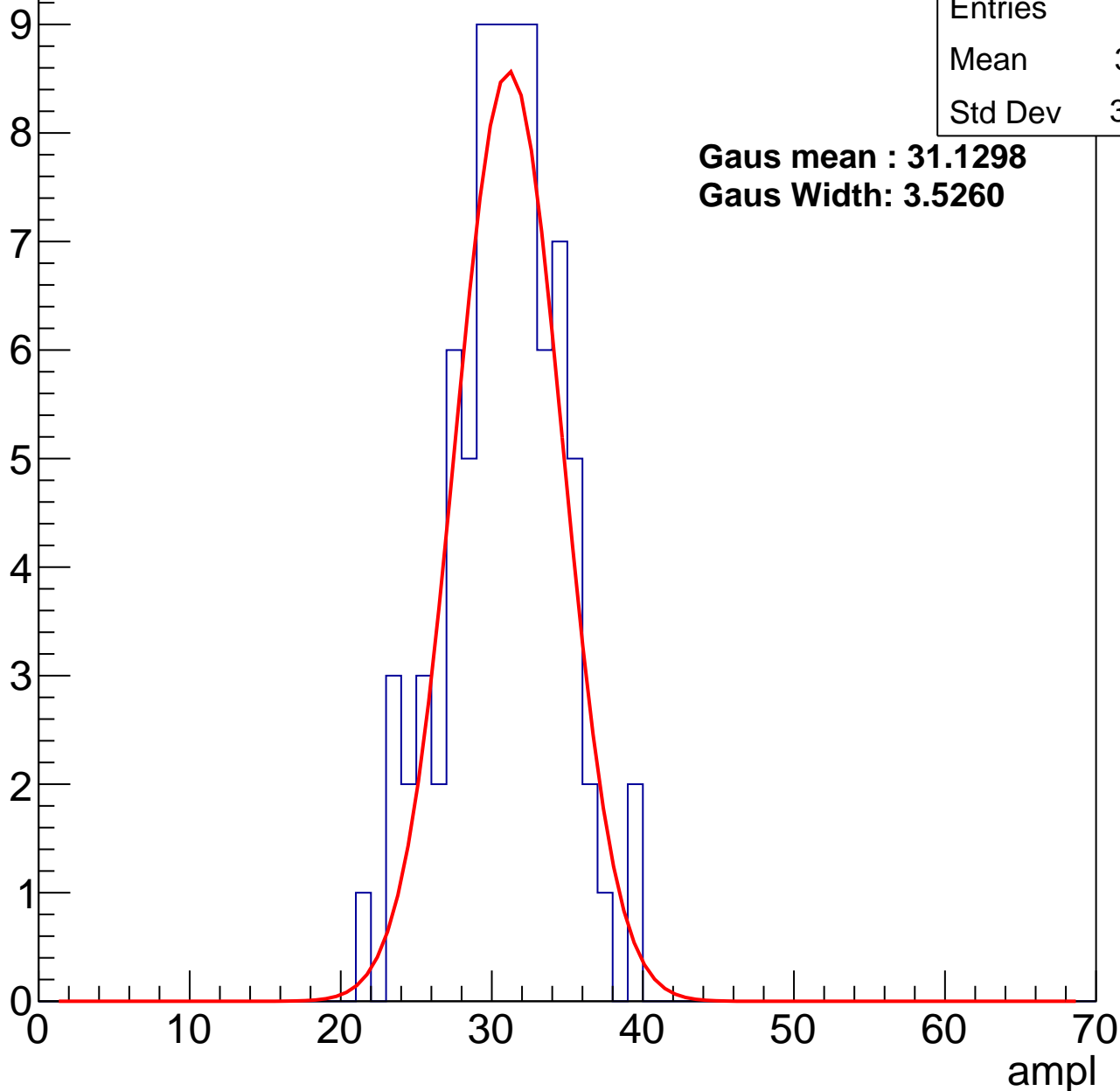
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	30.41
Std Dev	3.678

Gaus mean : 31.1298

Gaus Width: 3.5260



B1L100S, U11-ch36, adc1

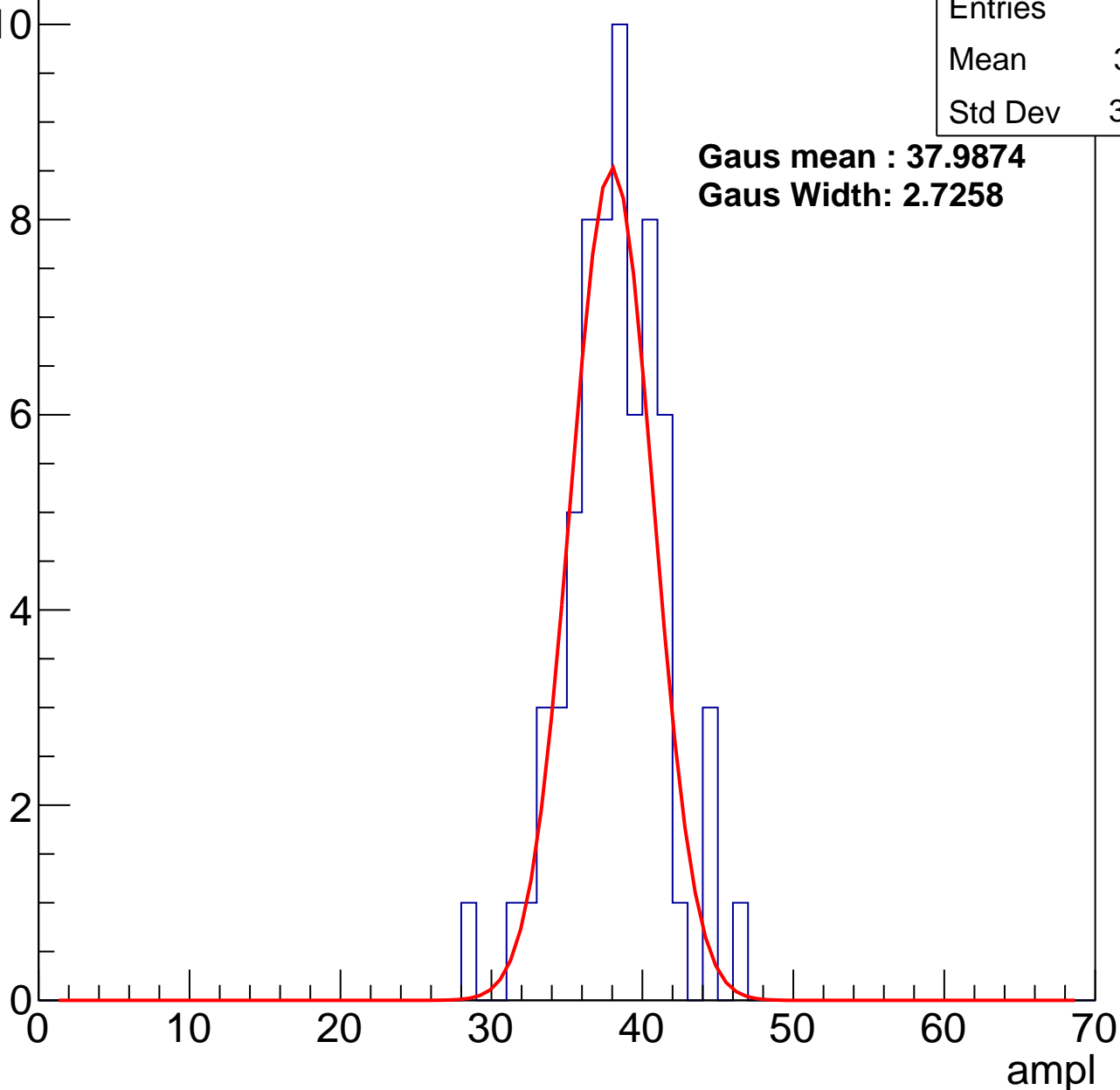
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37.71
Std Dev	3.204

Gaus mean : 37.9874

Gaus Width: 2.7258



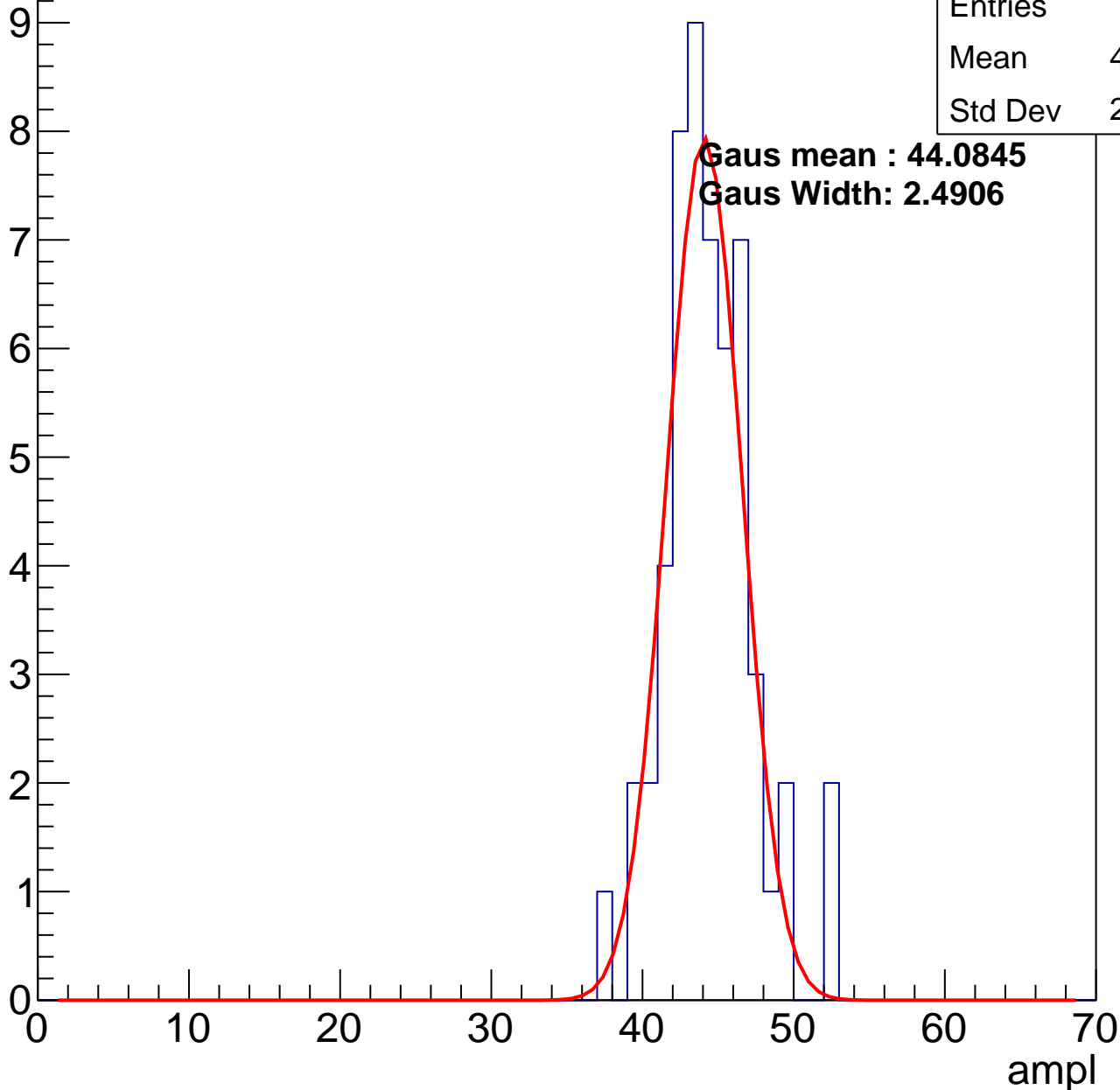
B1L100S, U11-ch36, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.94
Std Dev	2.928

Gaus mean : 44.0845
Gaus Width: 2.4906

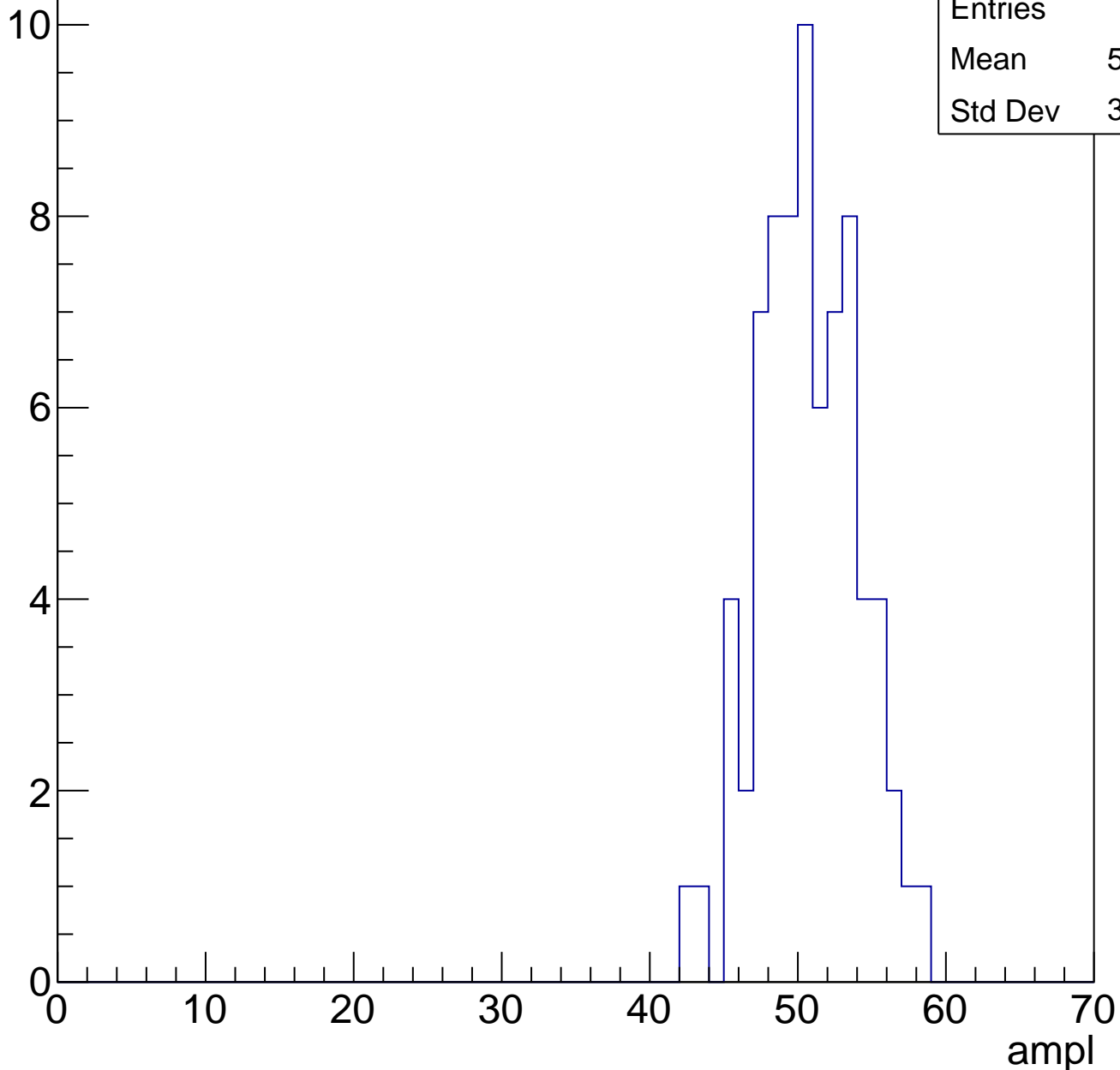


B1L100S, U11-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	50.26
Std Dev	3.284

Entry

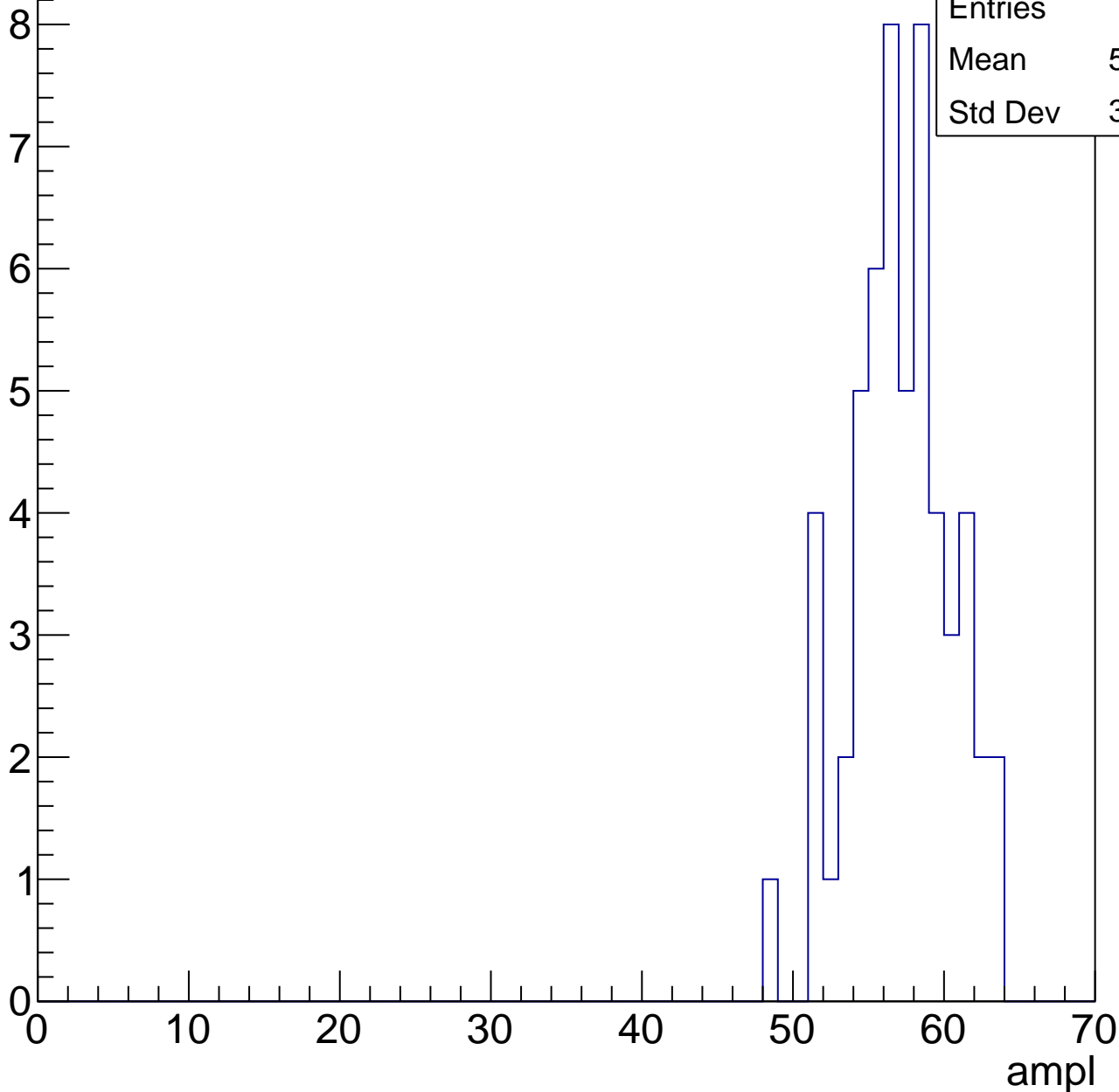


B1L100S, U11-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	56.67
Std Dev	3.276

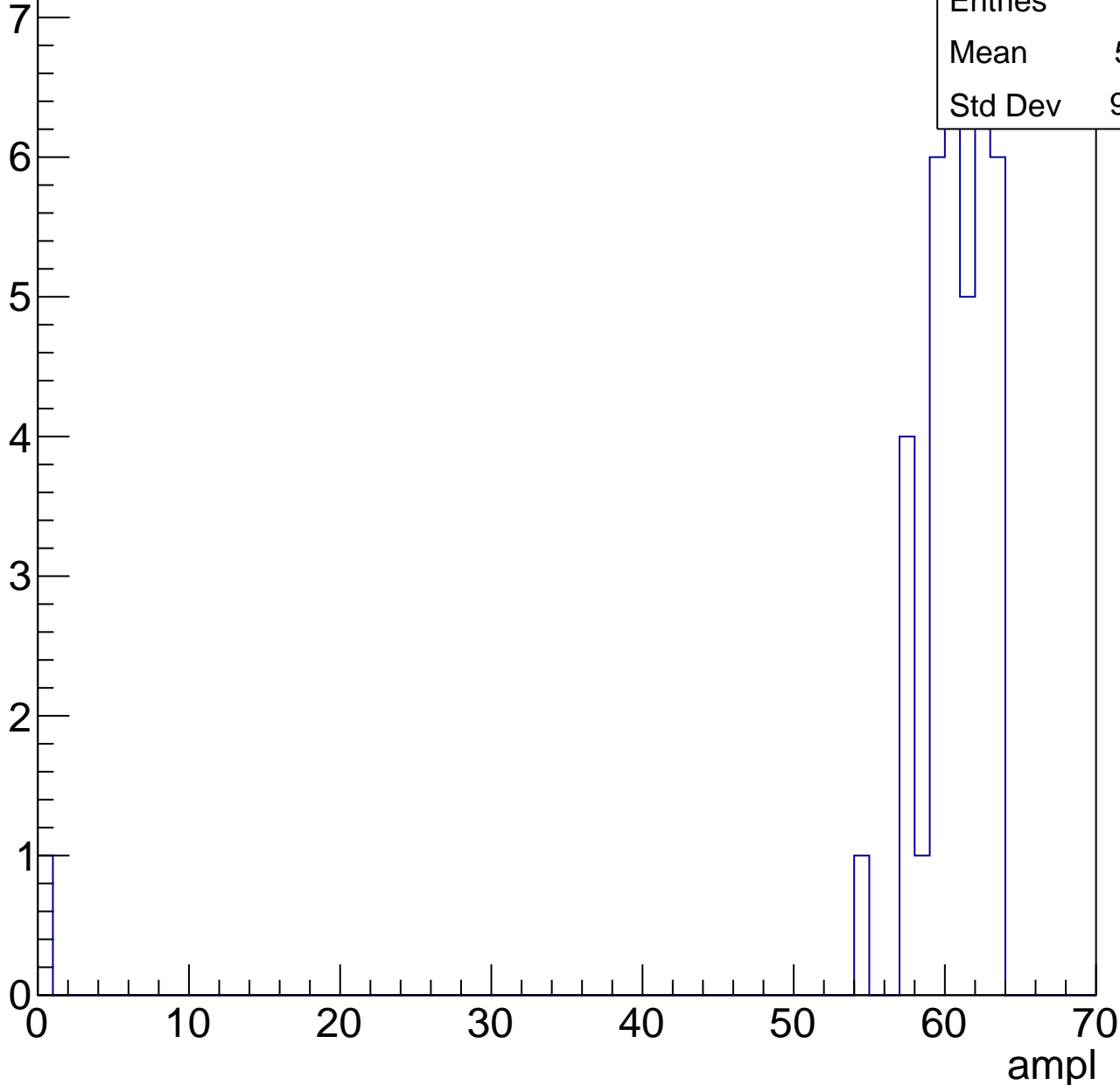


B1L100S, U11-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.71
Std Dev	9.875



B1L100S, U11-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch37, adc0

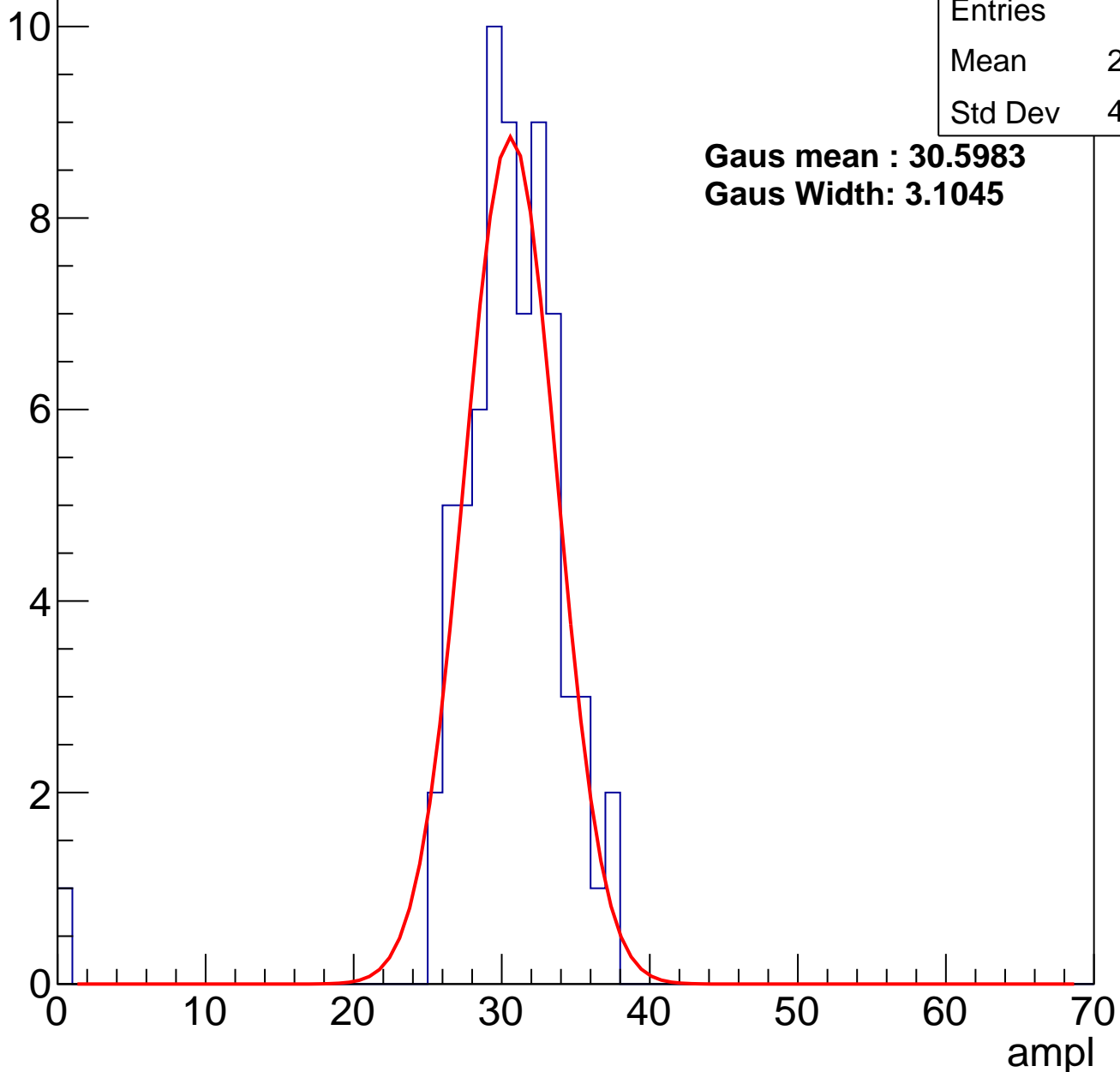
calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	29.94
Std Dev	4.585

Gaus mean : 30.5983

Gaus Width: 3.1045

Entry



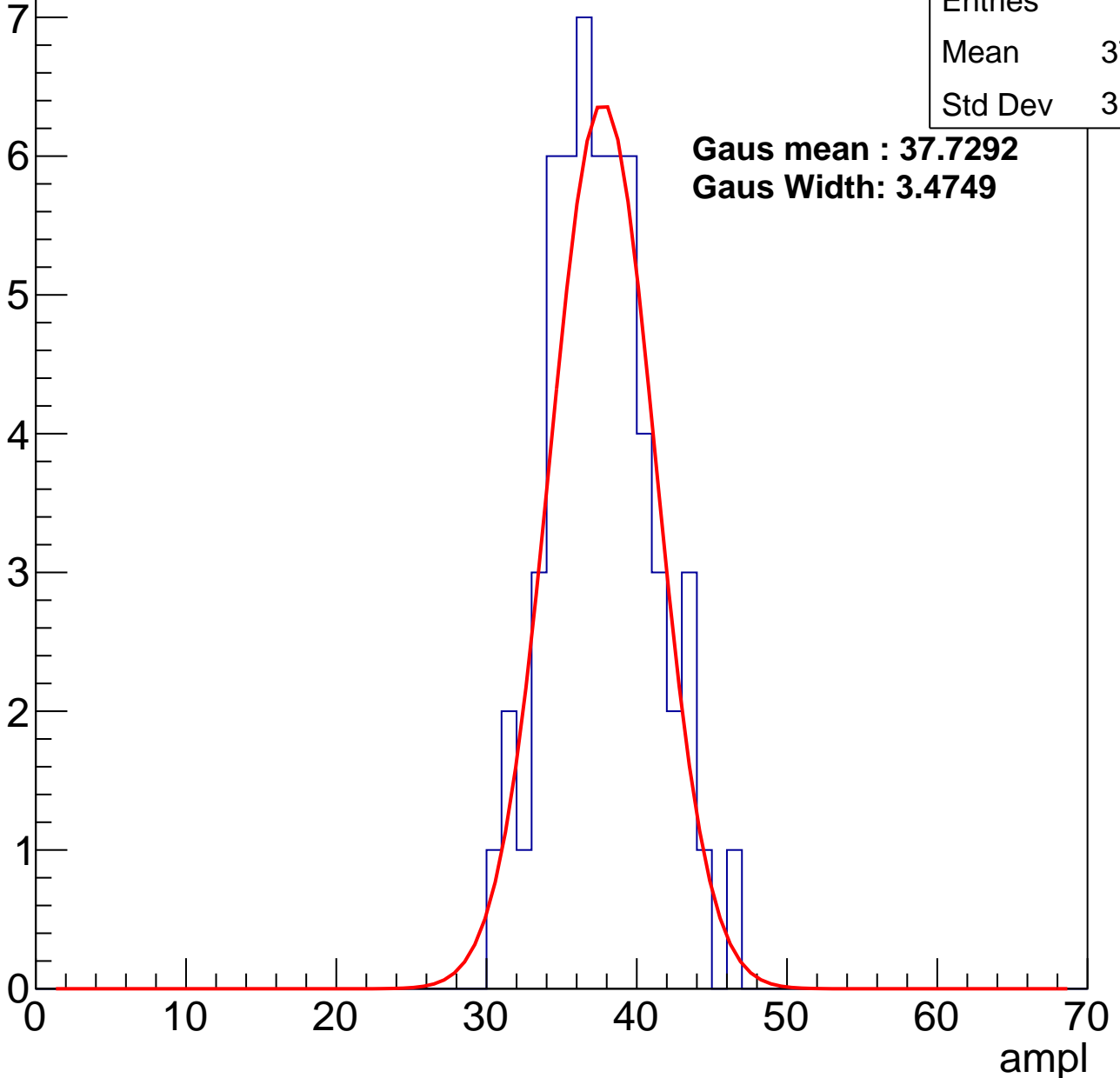
B1L100S, U11-ch37, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	37.22
Std Dev	3.419

Gaus mean : 37.7292
Gaus Width: 3.4749



B1L100S, U11-ch37, adc2

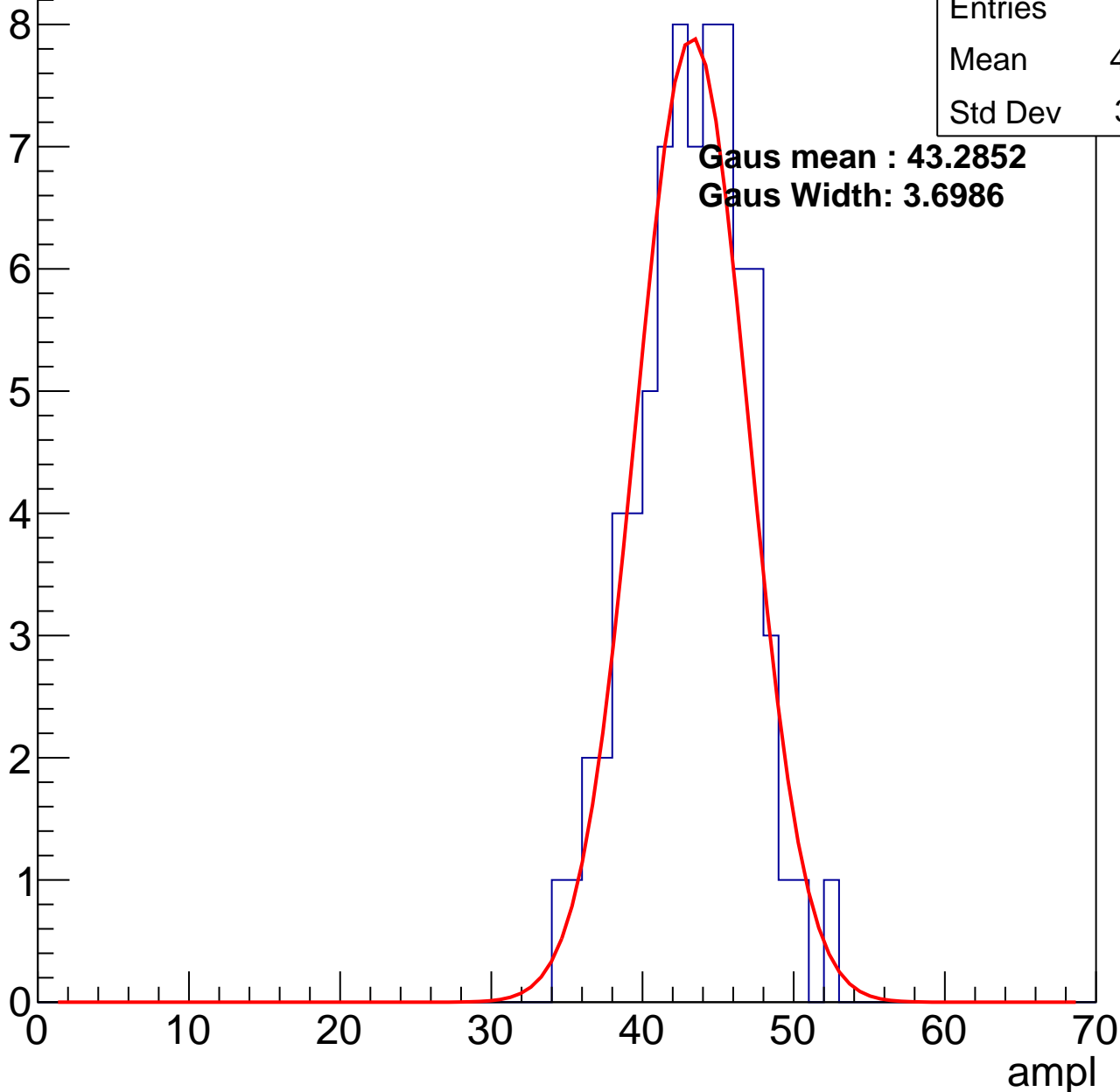
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	42.83
Std Dev	3.631

Gaus mean : 43.2852

Gaus Width: 3.6986

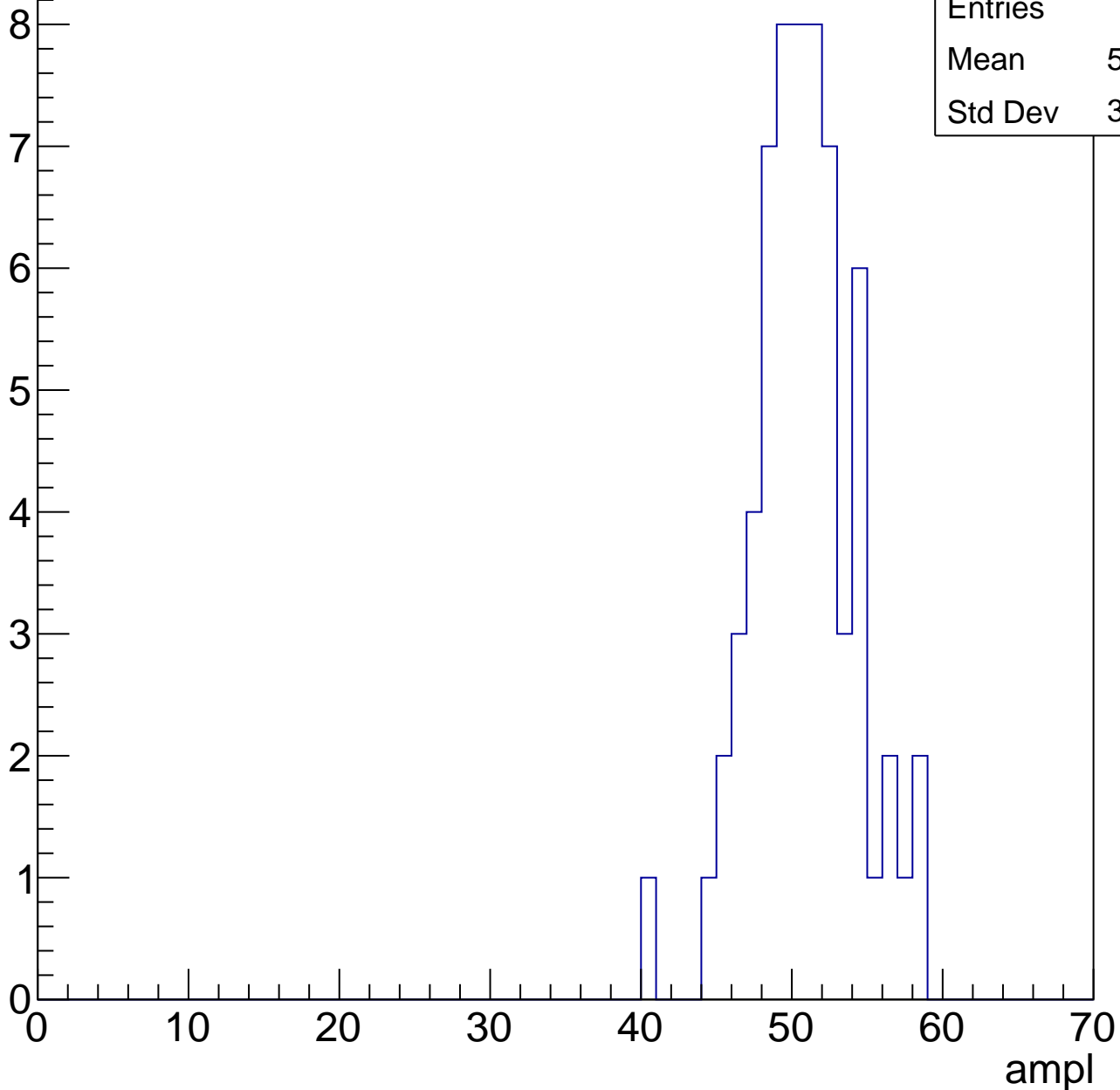


B1L100S, U11-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

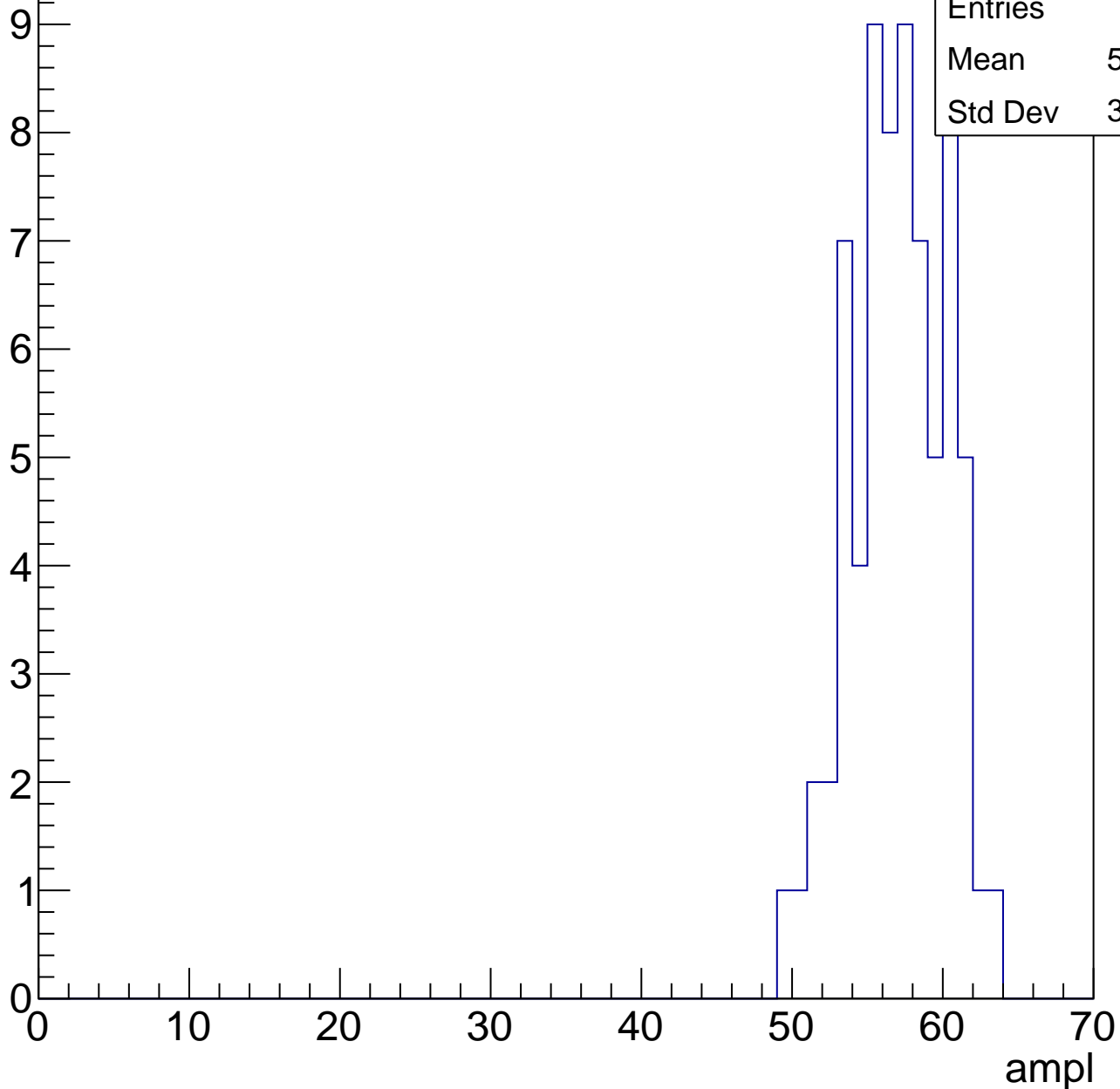
Entries	64
Mean	50.36
Std Dev	3.379



B1L100S, U11-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



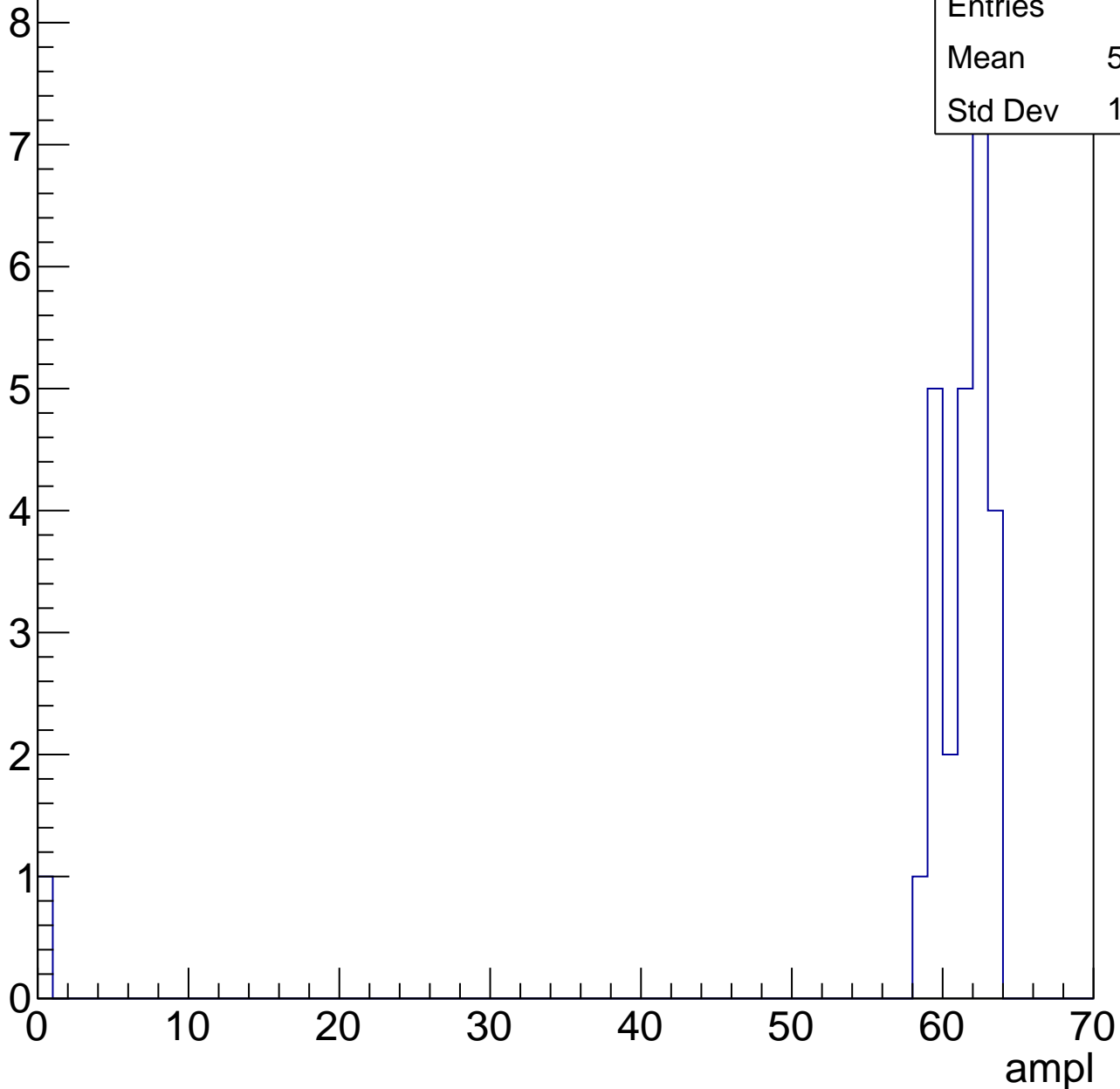
Entries	70
Mean	56.56
Std Dev	3.055

B1L100S, U11-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.69
Std Dev	11.83

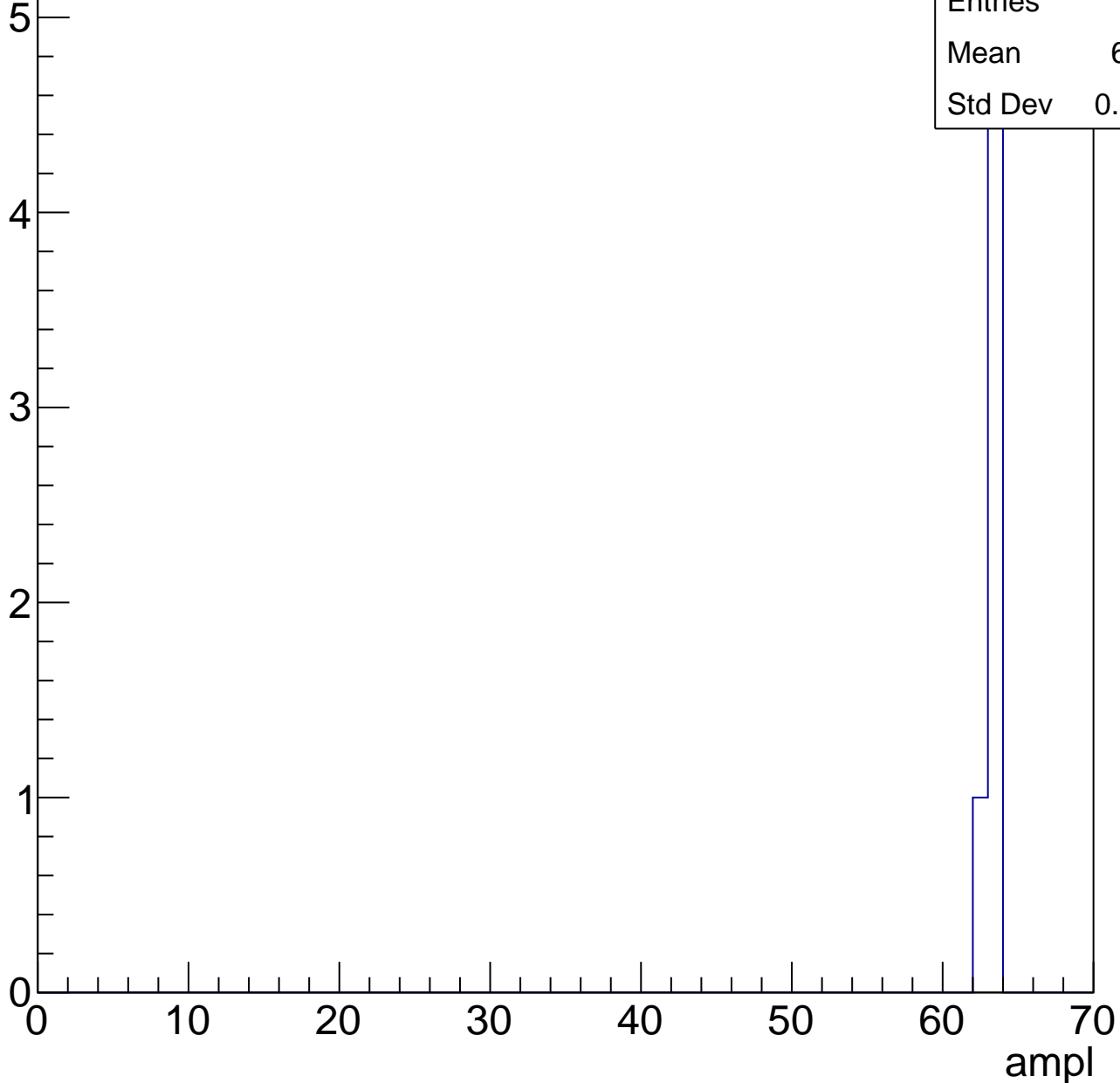


B1L100S, U11-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

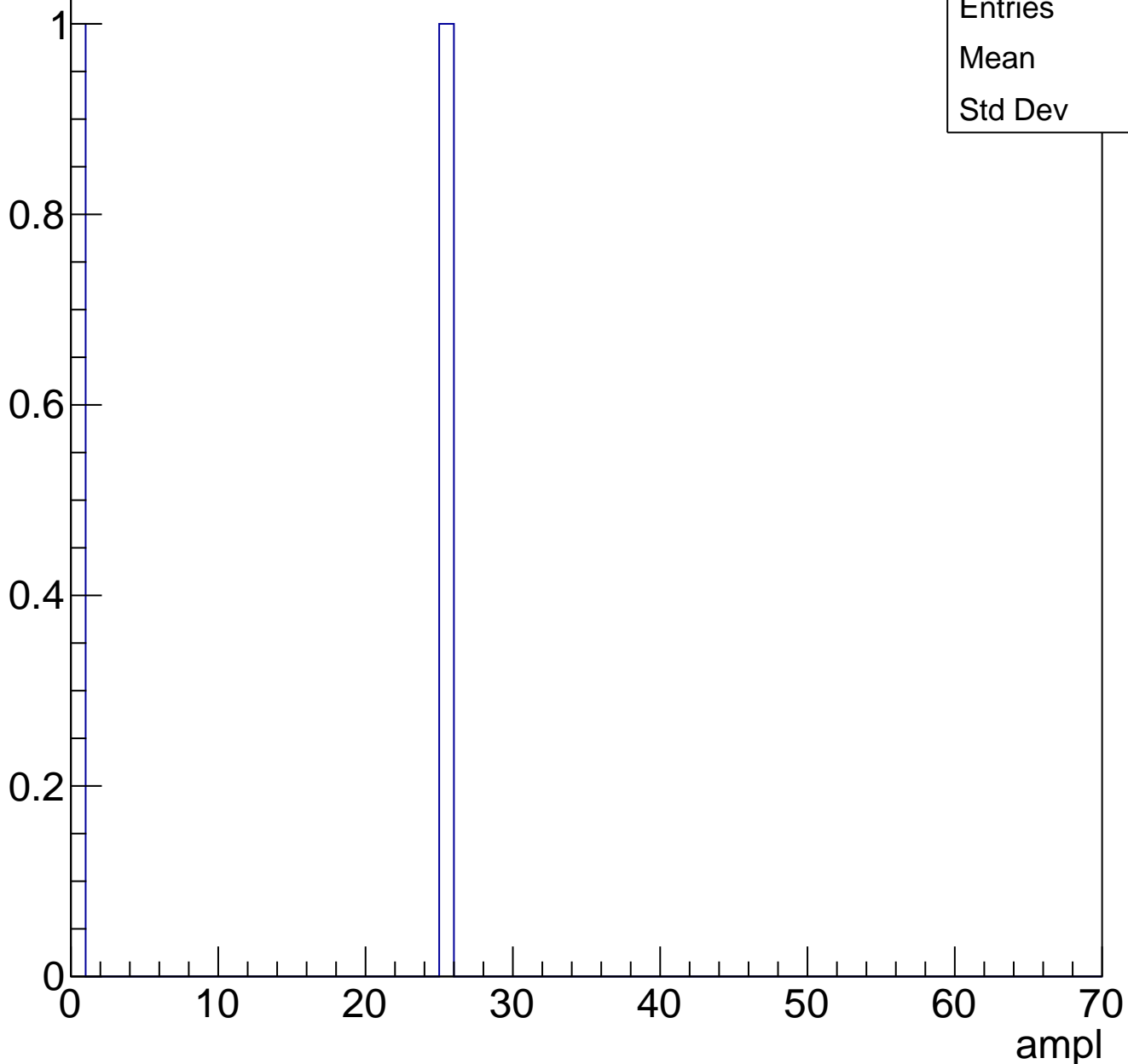
Entries	6
Mean	62.83
Std Dev	0.3727



B1L100S, U11-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B1L100S, U11-ch38, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	59
Mean	31.61
Std Dev	2.725

Gaus mean : 32.1884

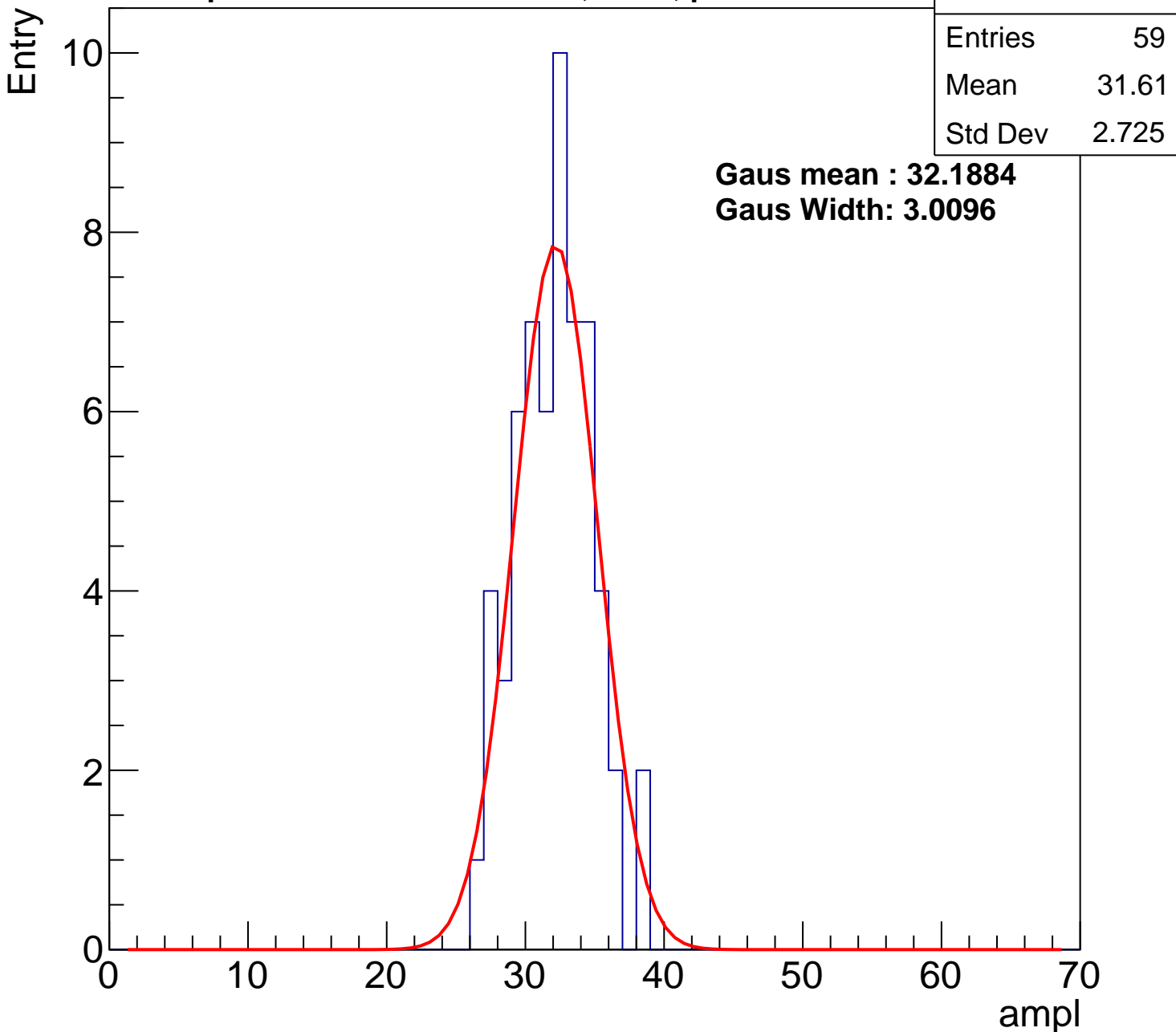
Gaus Width: 3.0096

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch38, adc1

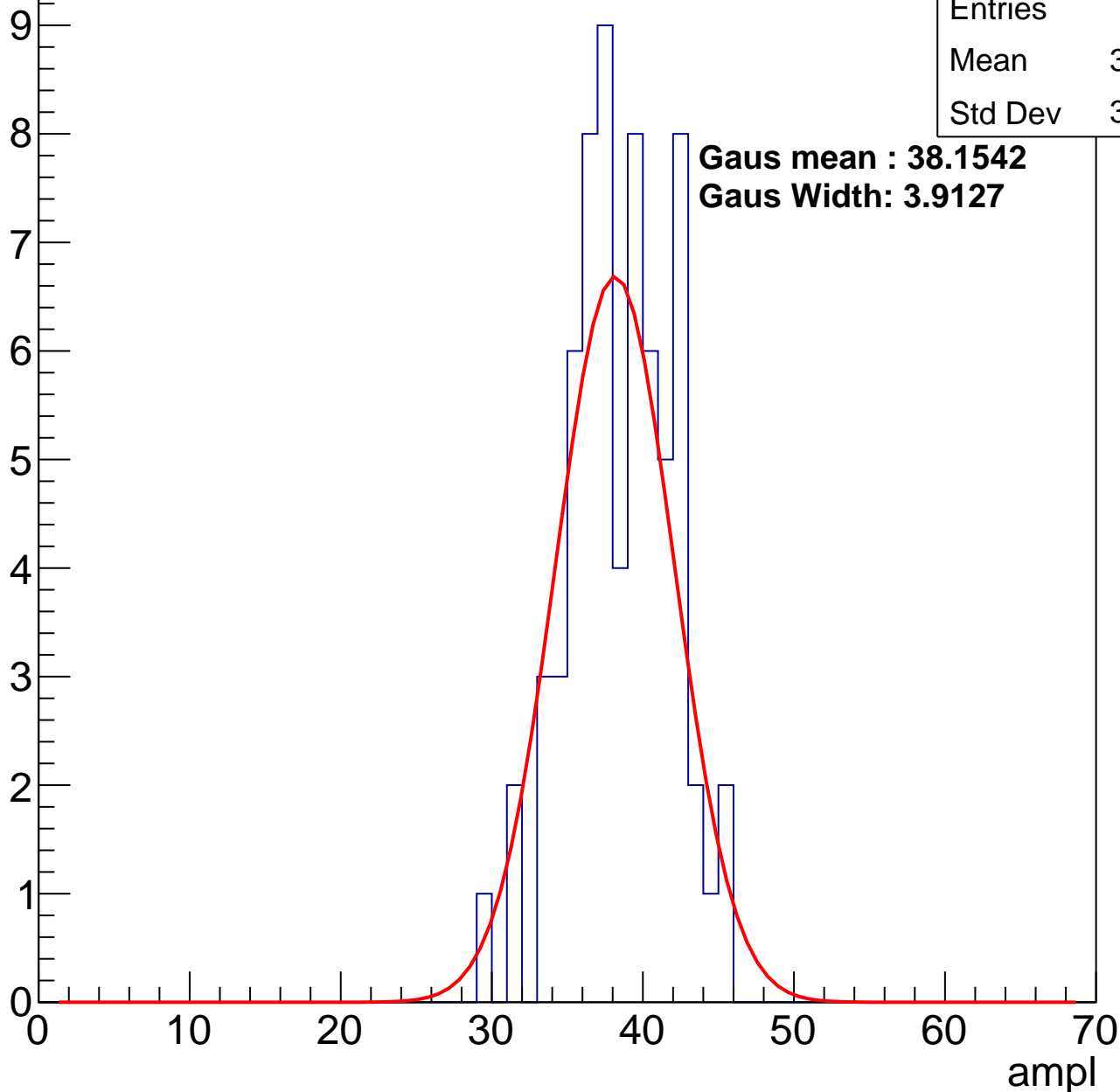
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	38.06
Std Dev	3.399

Gaus mean : 38.1542

Gaus Width: 3.9127



B1L100S, U11-ch38, adc2

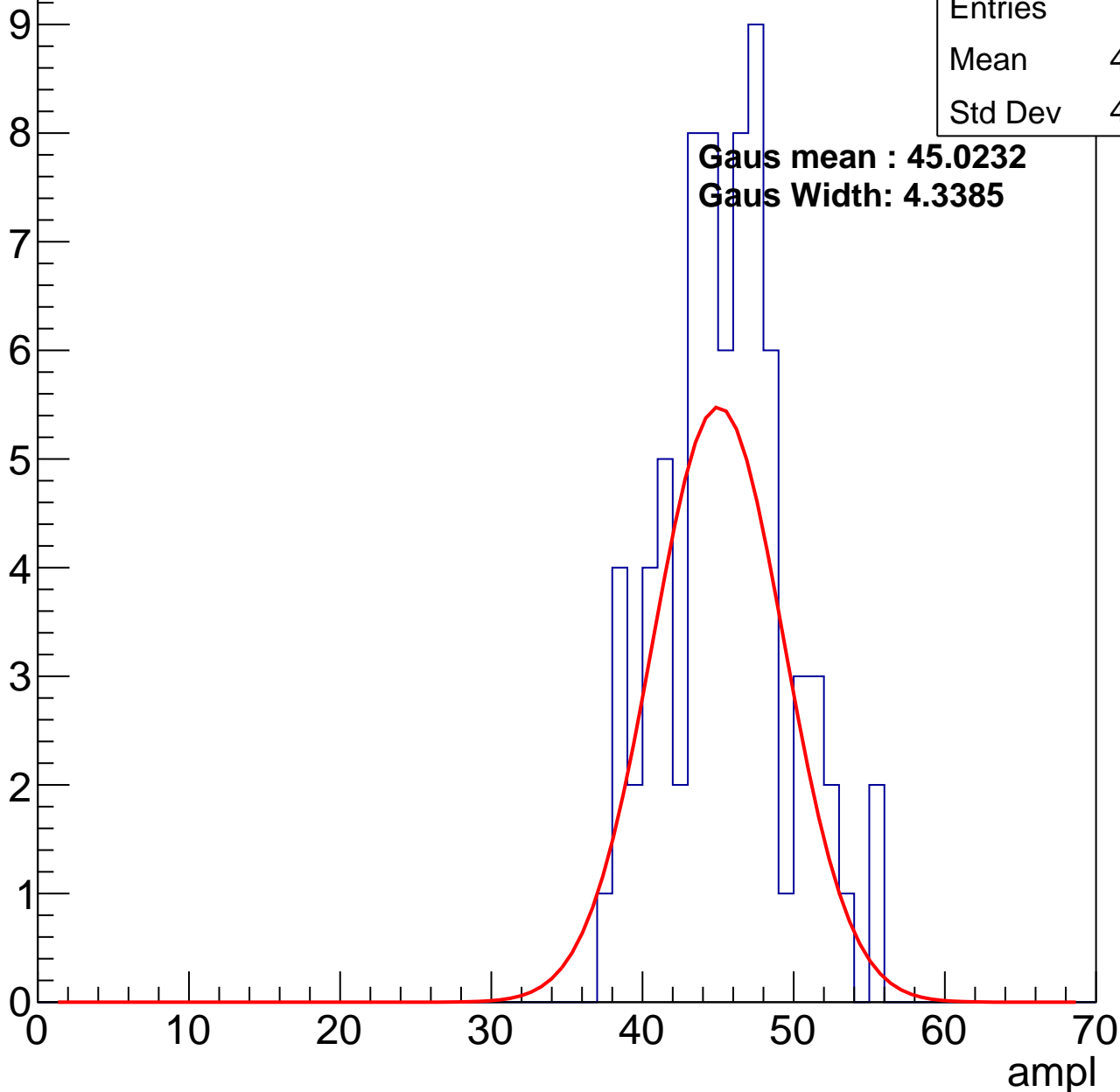
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	45.07
Std Dev	4.047

Gaus mean : 45.0232

Gaus Width: 4.3385

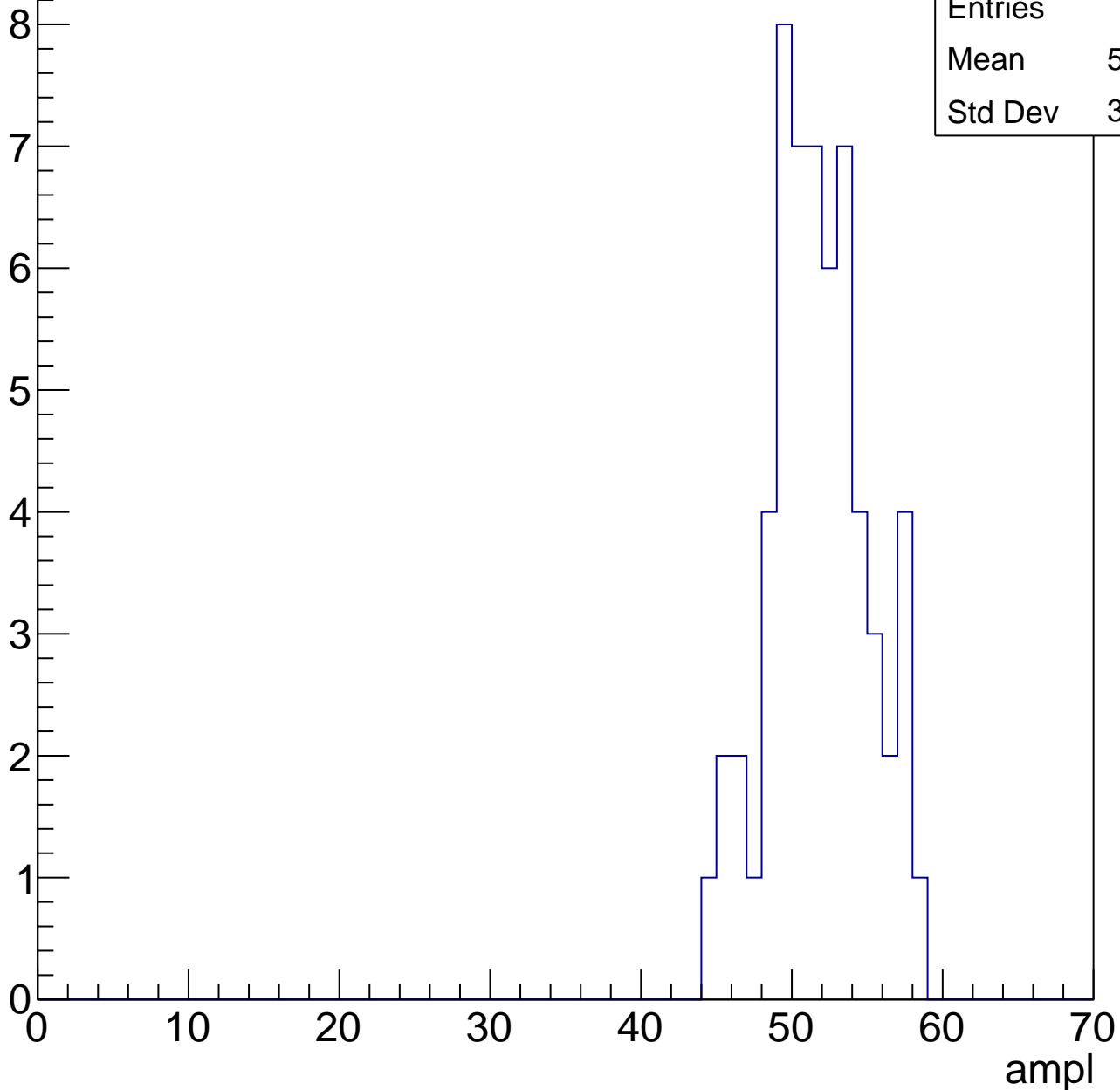


B1L100S, U11-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

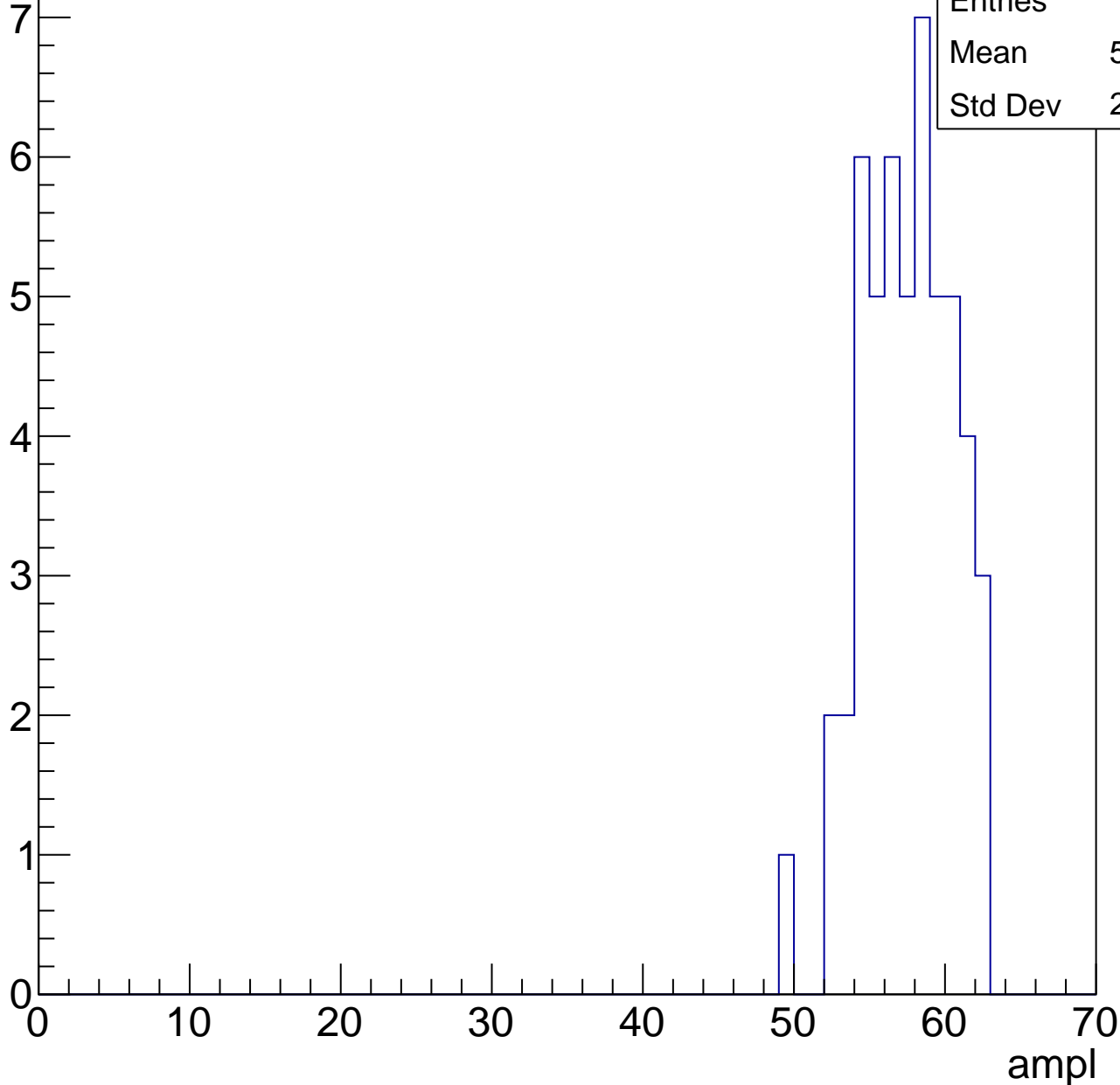
Entries	59
Mean	51.29
Std Dev	3.237



B1L100S, U11-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	39
Mean	59.23
Std Dev	9.812

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

B1L100S, U11-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch39, adc0

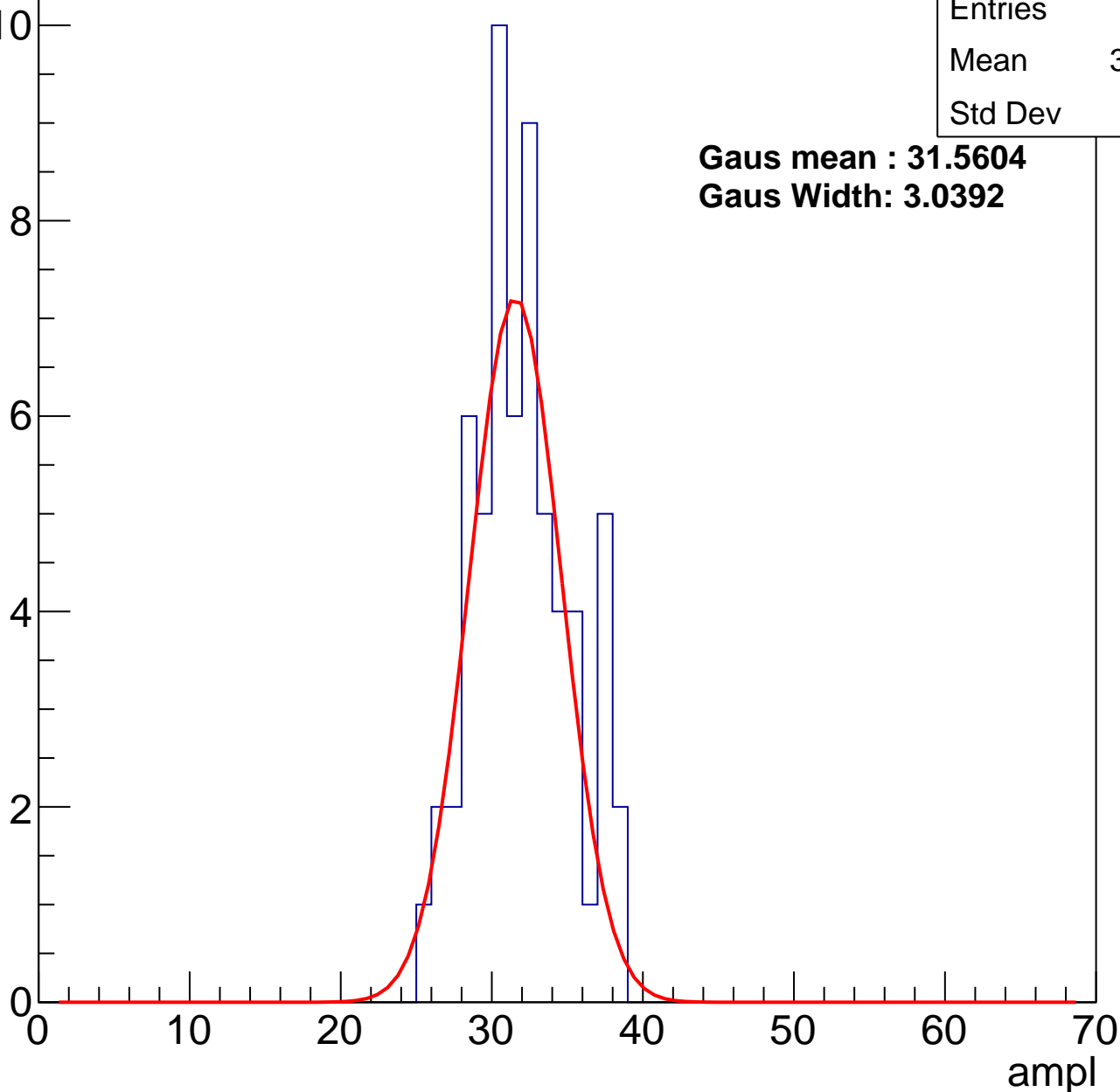
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	31.55
Std Dev	3.15

Gaus mean : 31.5604

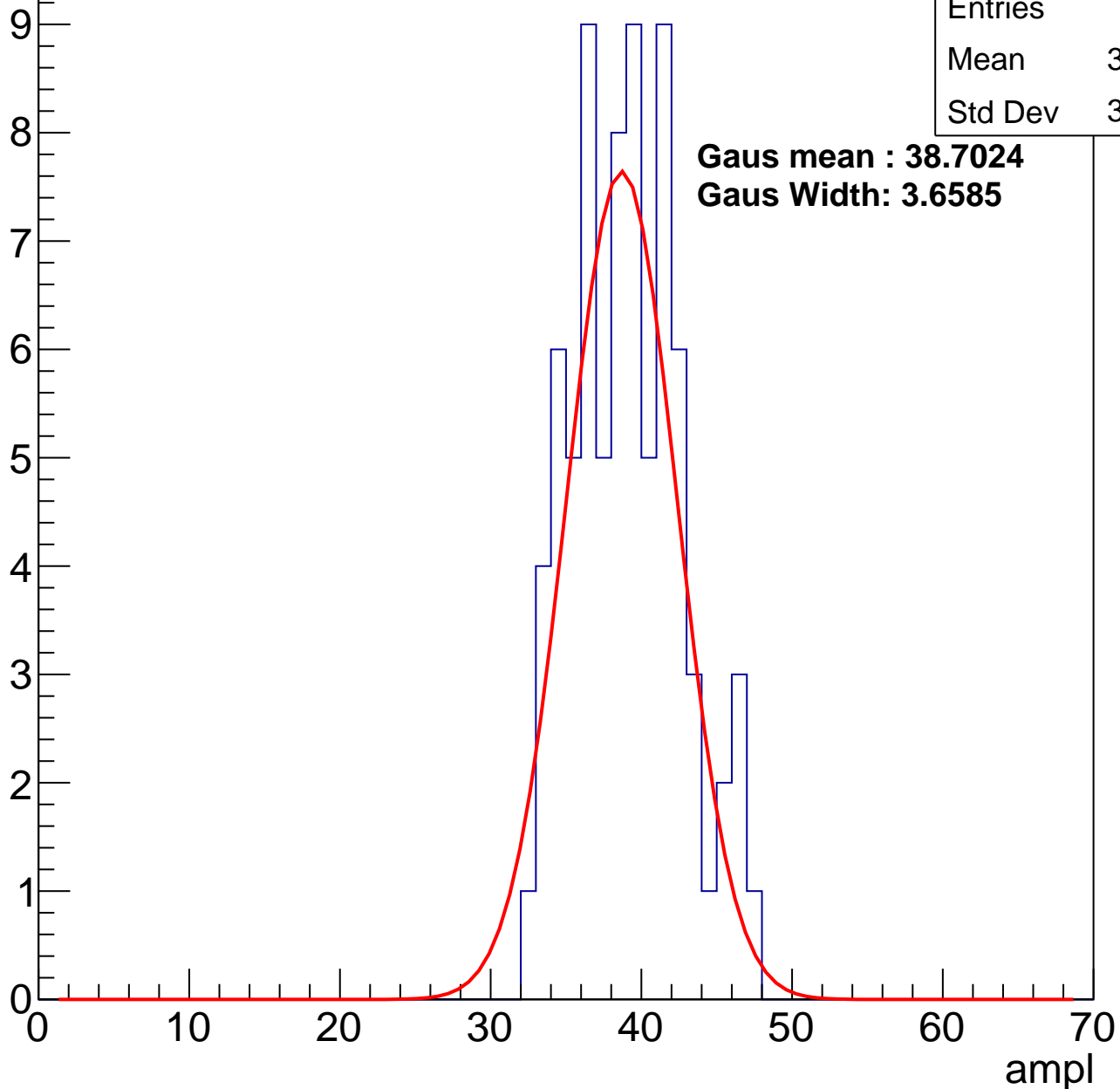
Gaus Width: 3.0392



B1L100S, U11-ch39, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch39, adc2

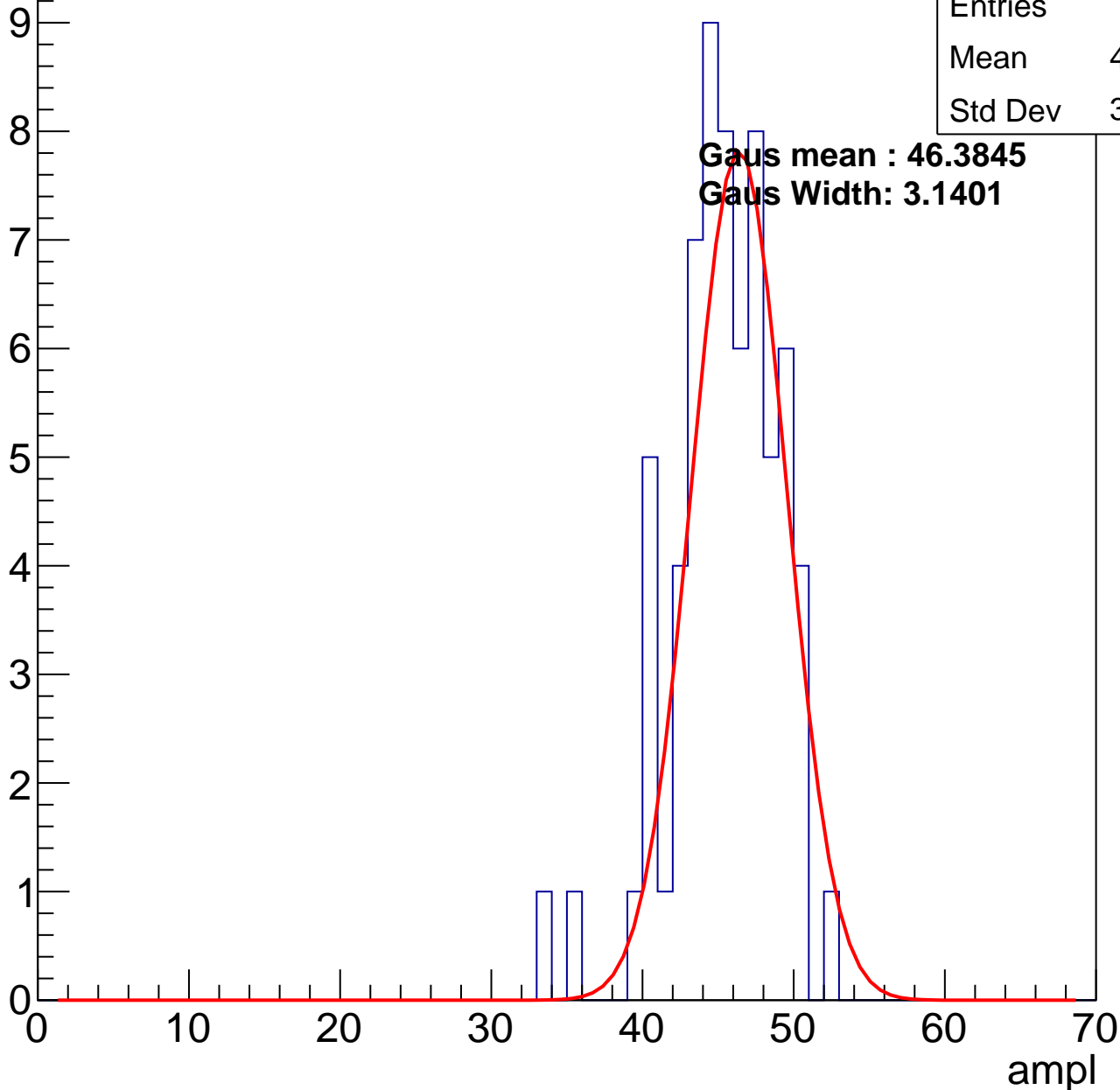
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.94
Std Dev	3.502

Gaus mean : 46.3845

Gaus Width: 3.1401



B1L100S, U11-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

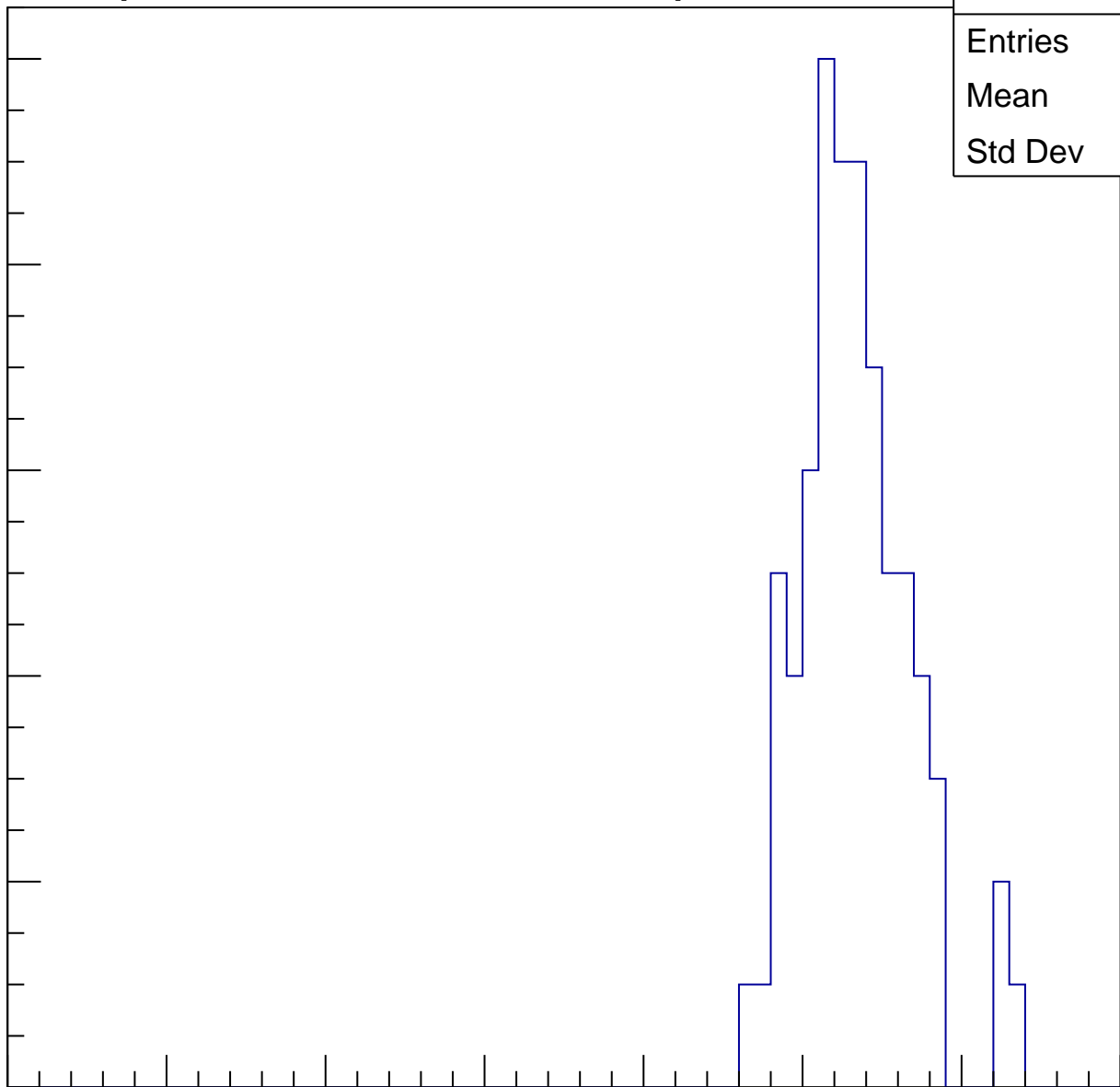
Entries	72
Mean	52.86
Std Dev	3.433

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

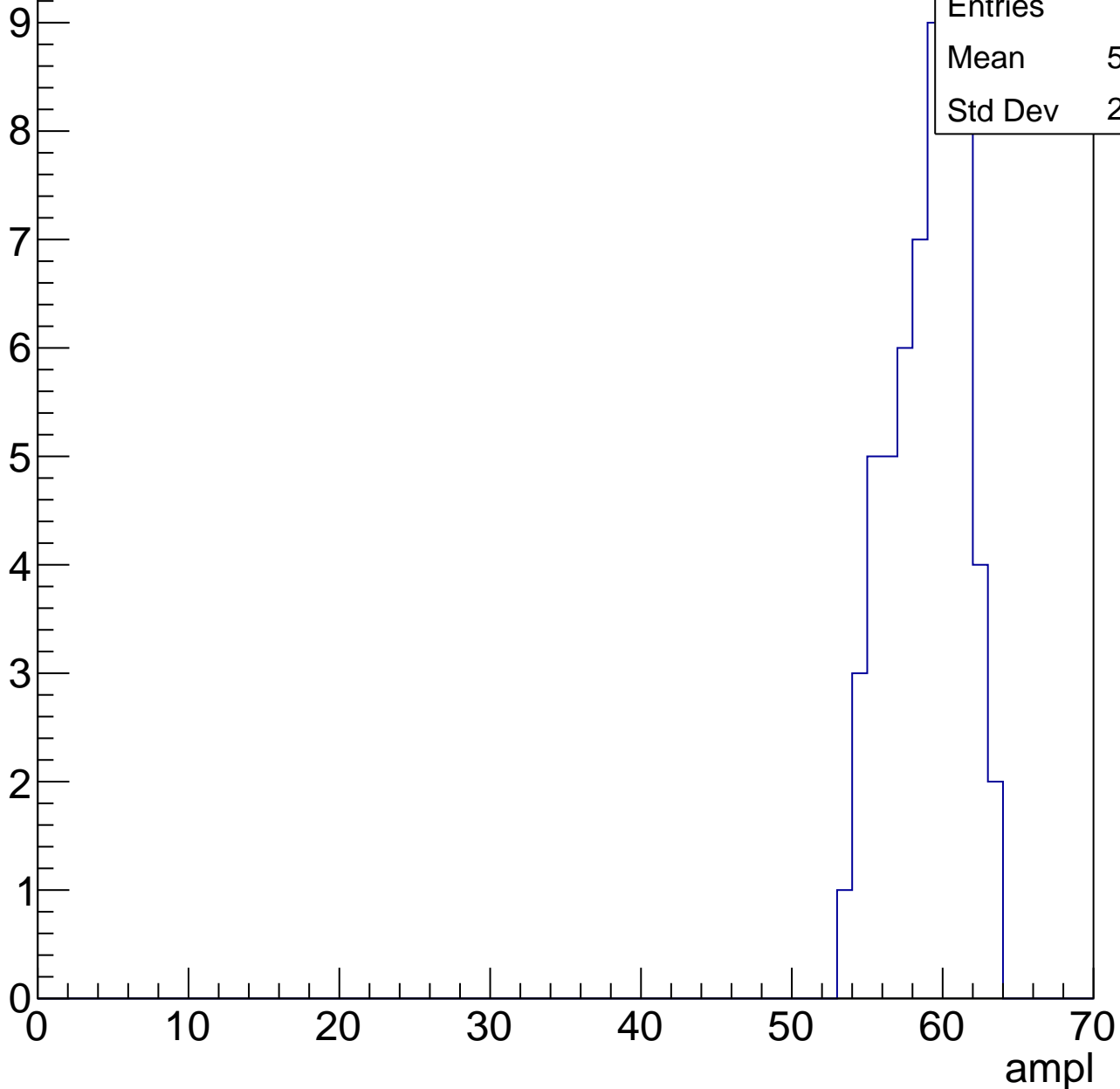


B1L100S, U11-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	58.53
Std Dev	2.473

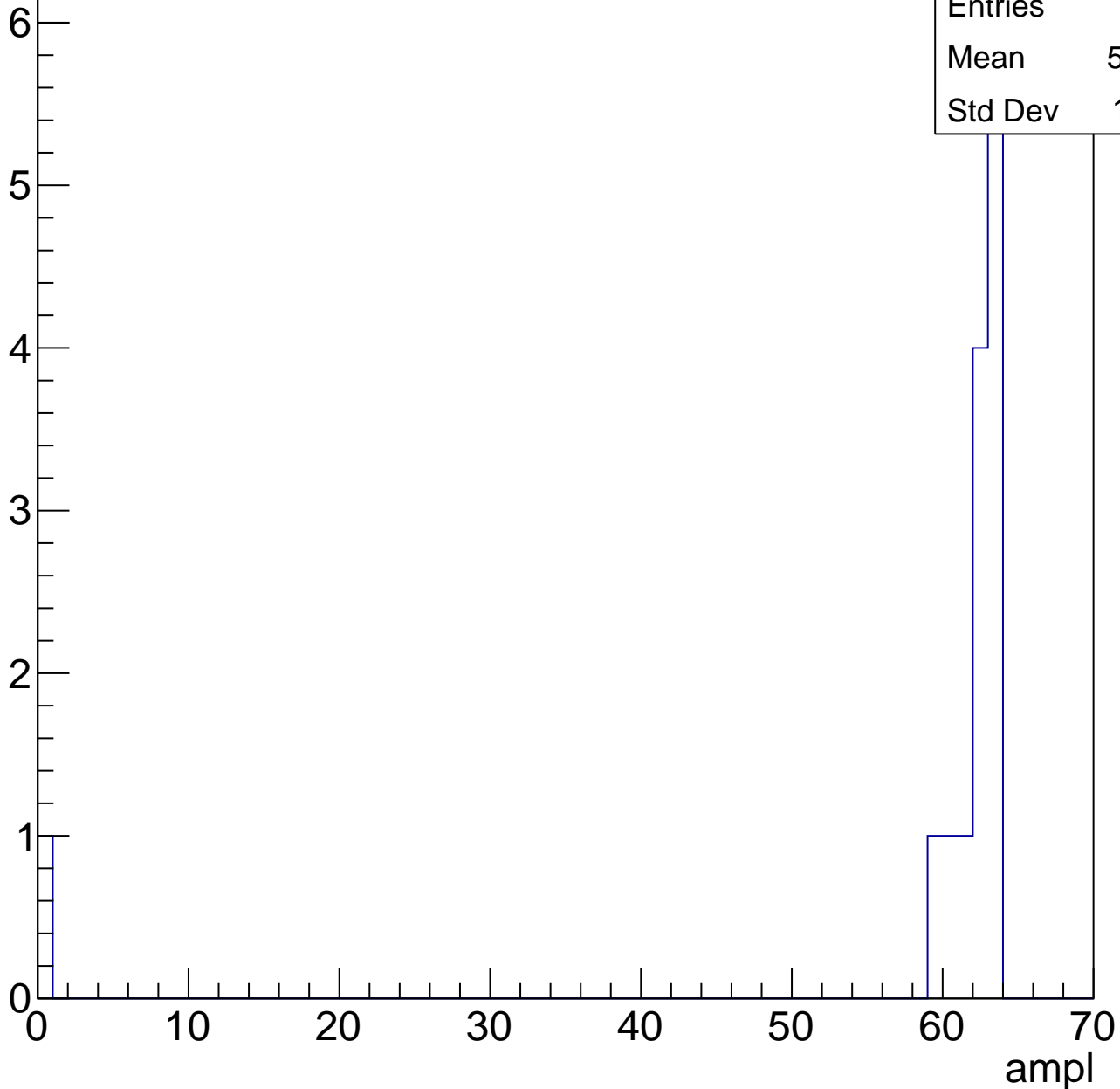


B1L100S, U11-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	57.57
Std Dev	16.01



B1L100S, U11-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch40, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	58
Mean	29.33
Std Dev	5.141

Gaus mean : 30.8335

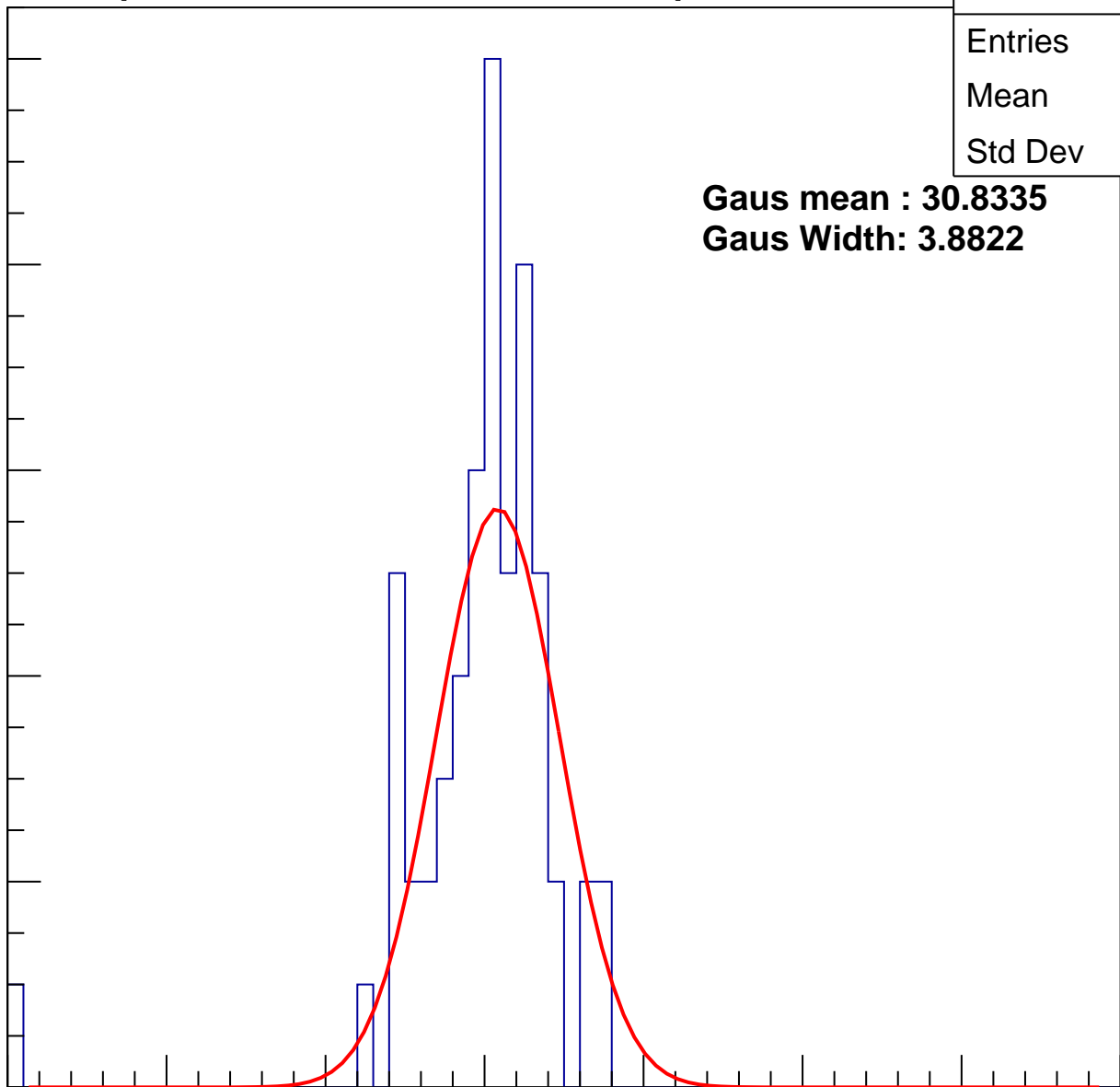
Gaus Width: 3.8822

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch40, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	36.38
Std Dev	3.325

Gaus mean : 36.7350

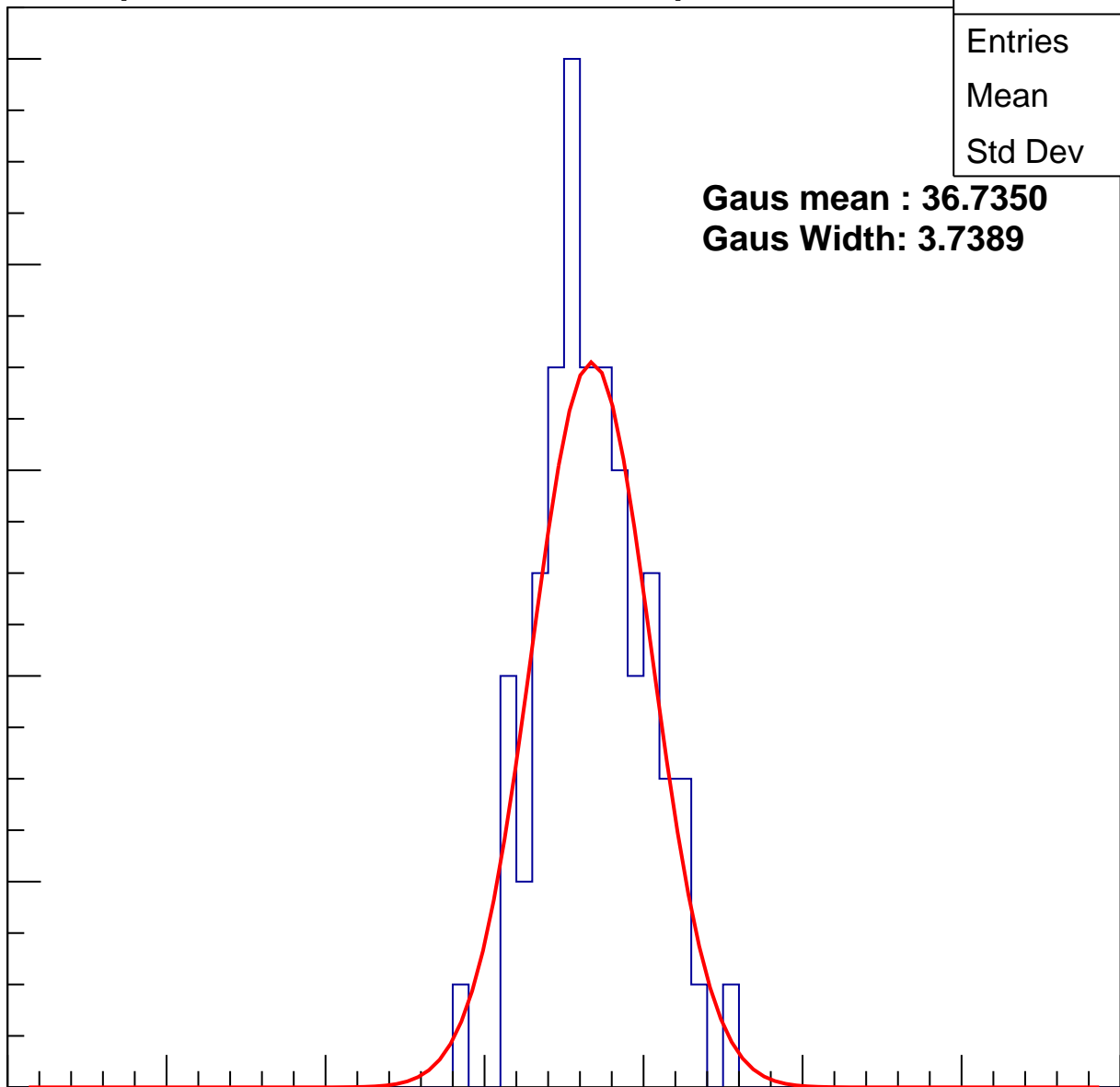
Gaus Width: 3.7389

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch40, adc2

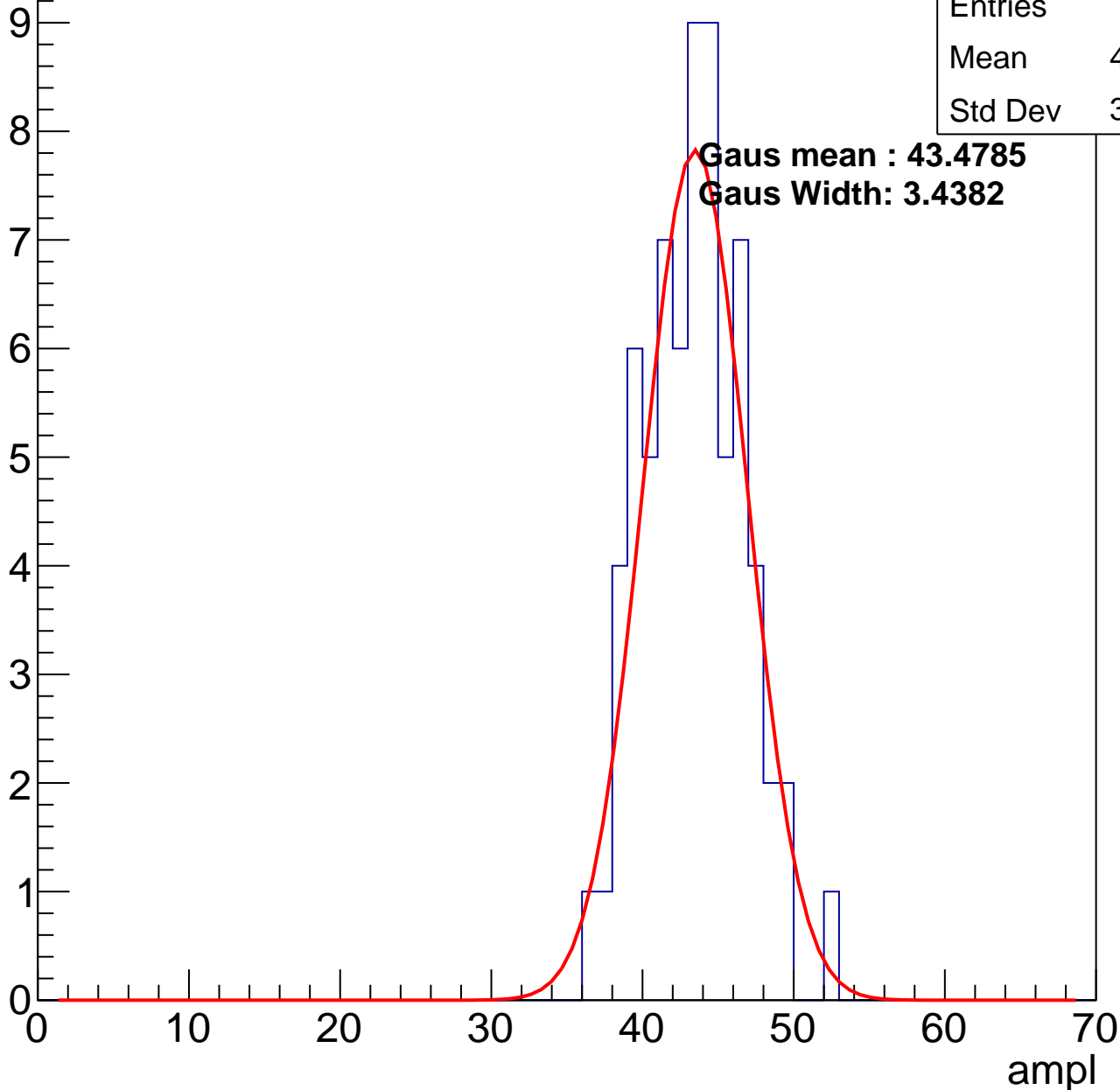
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.93
Std Dev	3.227

Gaus mean : 43.4785

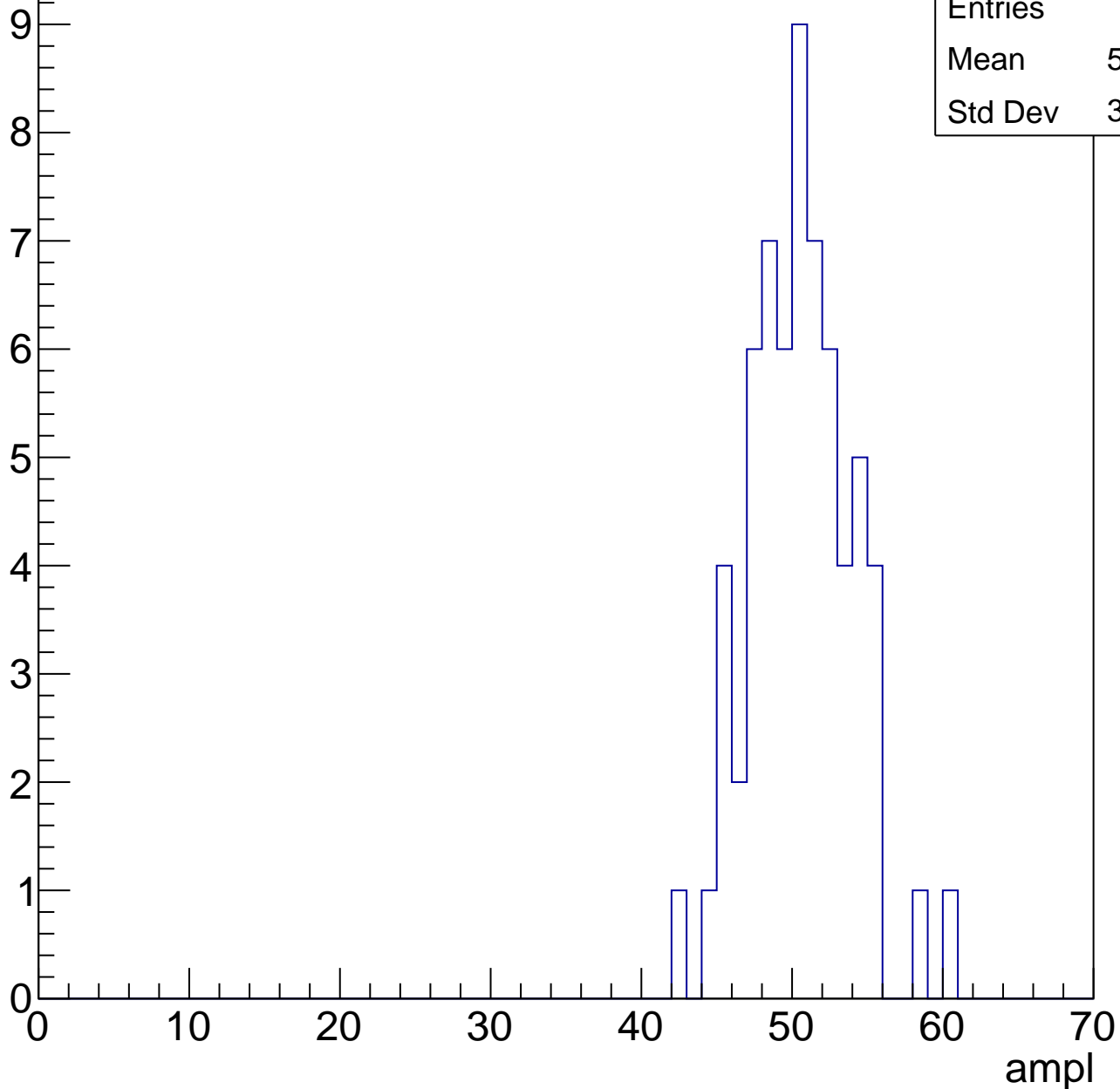
Gaus Width: 3.4382



B1L100S, U11-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

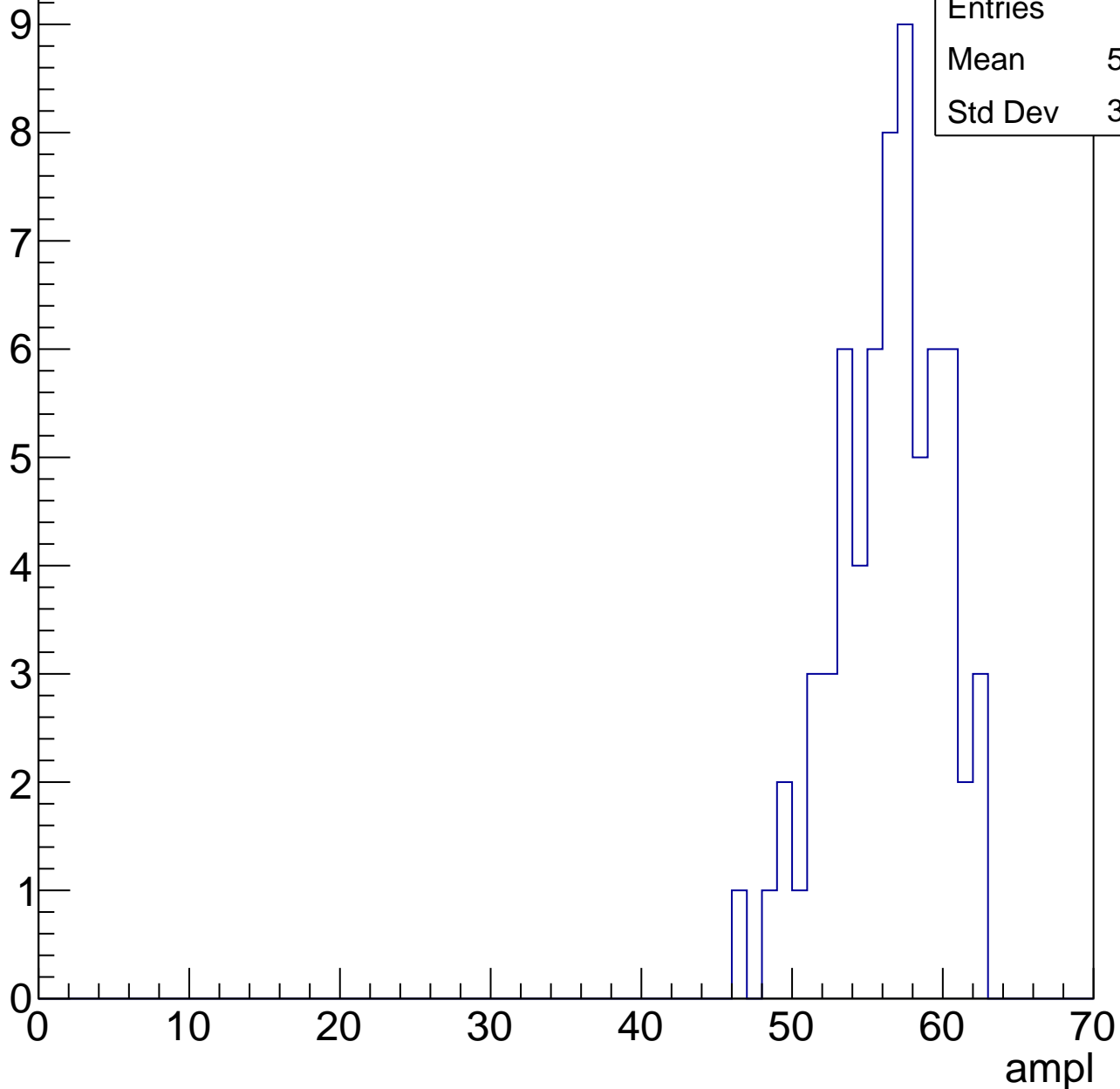


Entries	64
Mean	50.14
Std Dev	3.377

B1L100S, U11-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

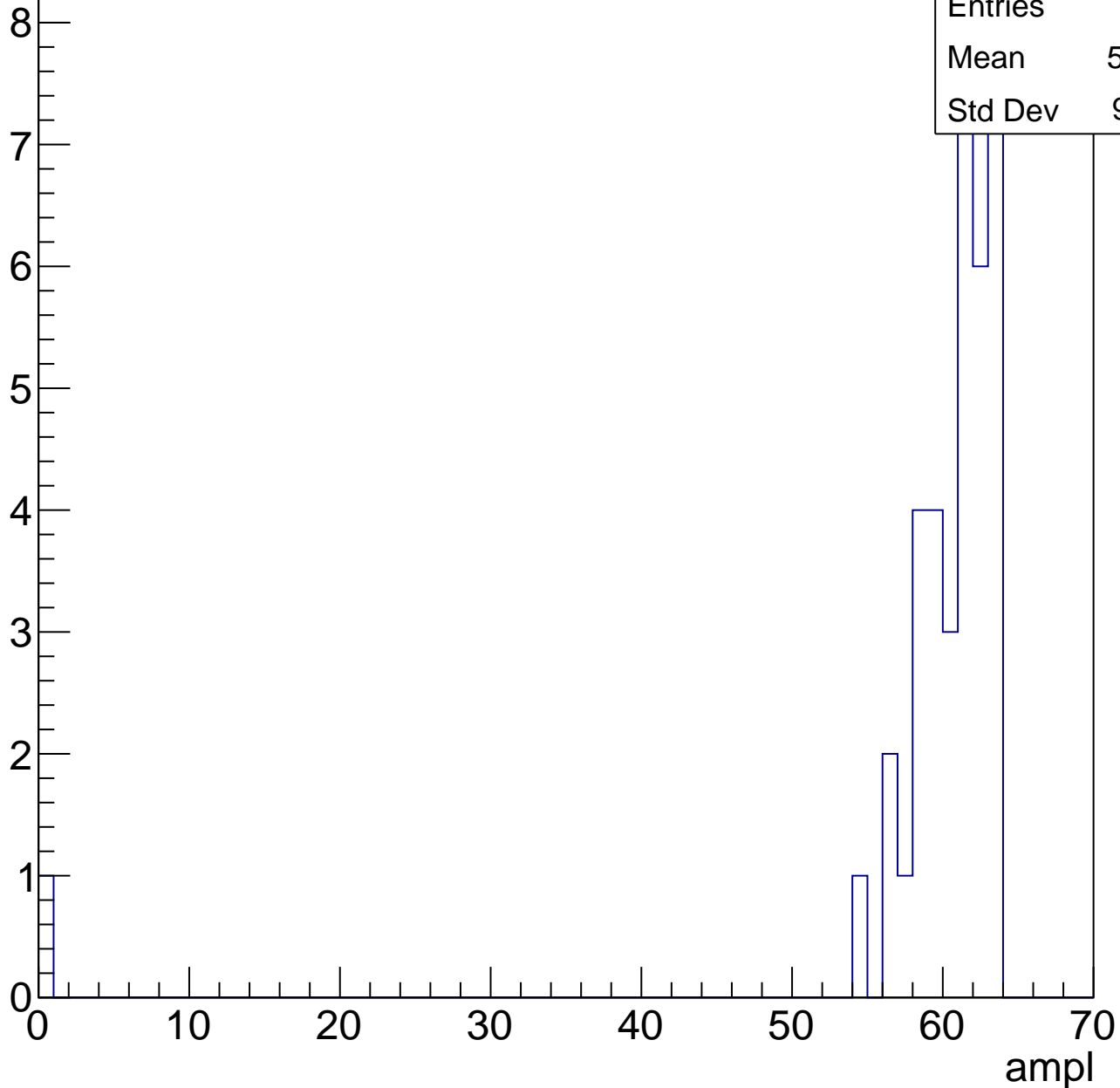


Entries	66
Mean	55.88
Std Dev	3.553

B1L100S, U11-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U11-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch41, adc0

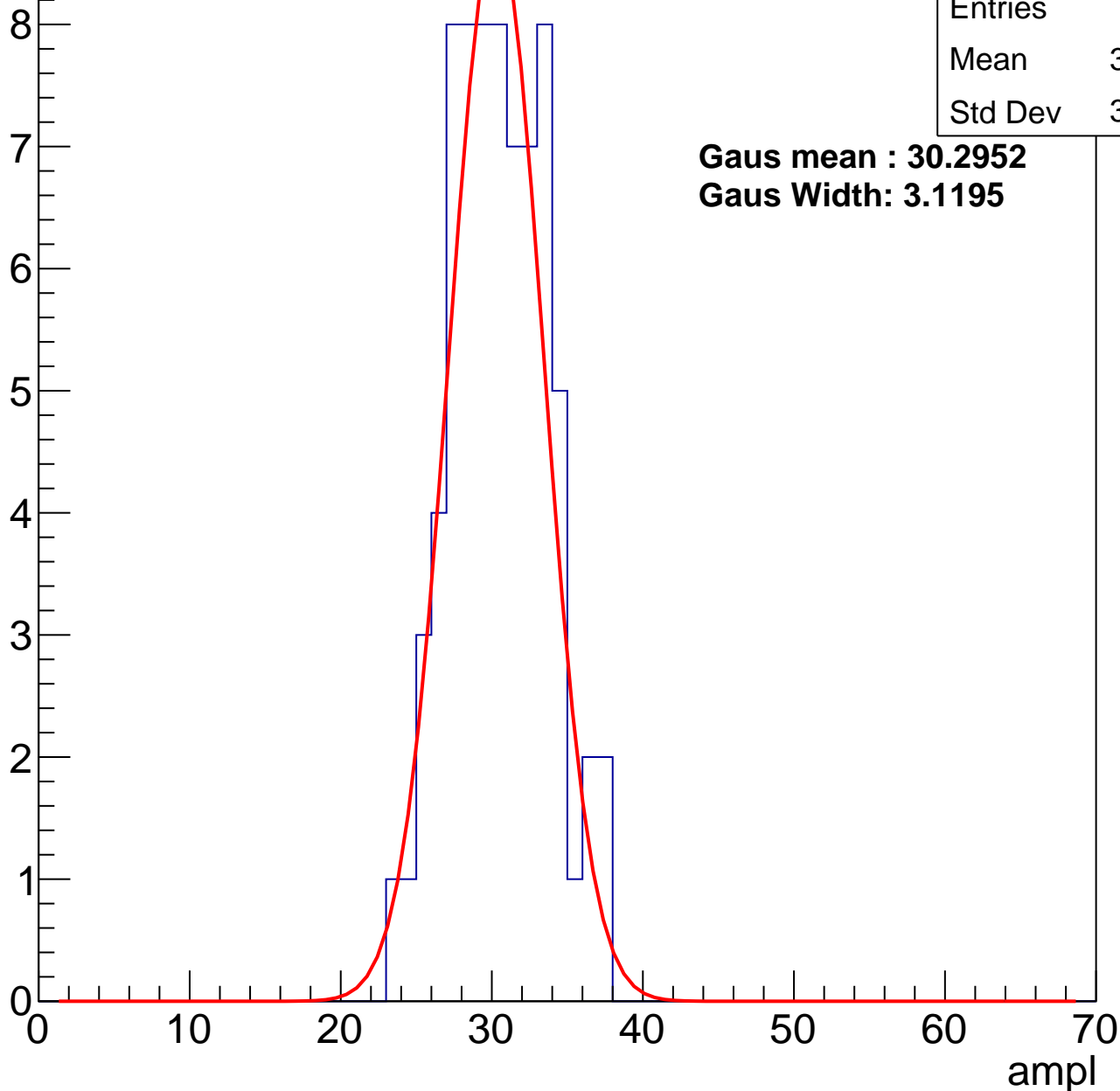
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.05
Std Dev	3.136

Gaus mean : 30.2952

Gaus Width: 3.1195



B1L100S, U11-ch41, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	57
Mean	36.4
Std Dev	3.106

Gaus mean : 37.1605

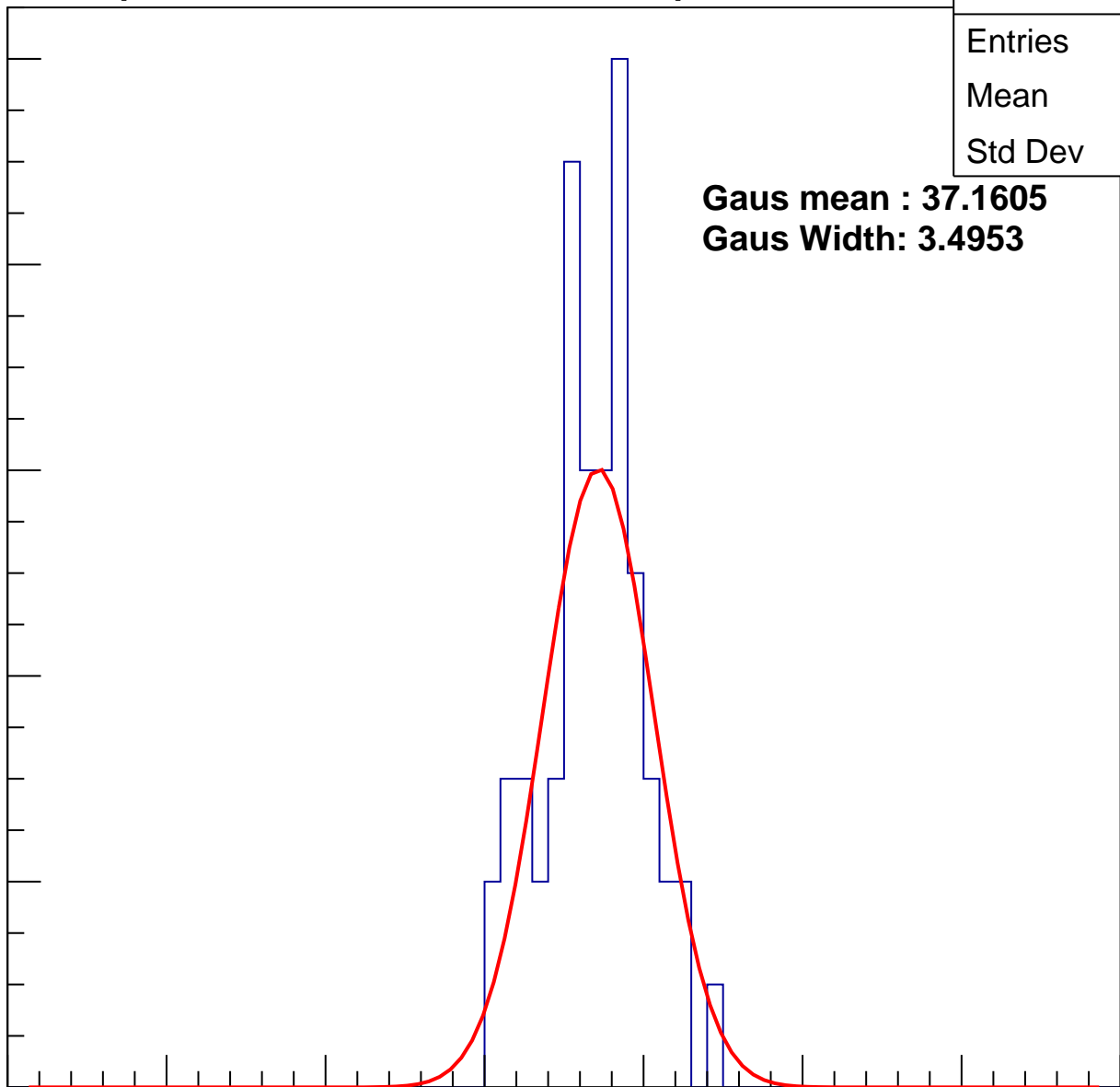
Gaus Width: 3.4953

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch41, adc2

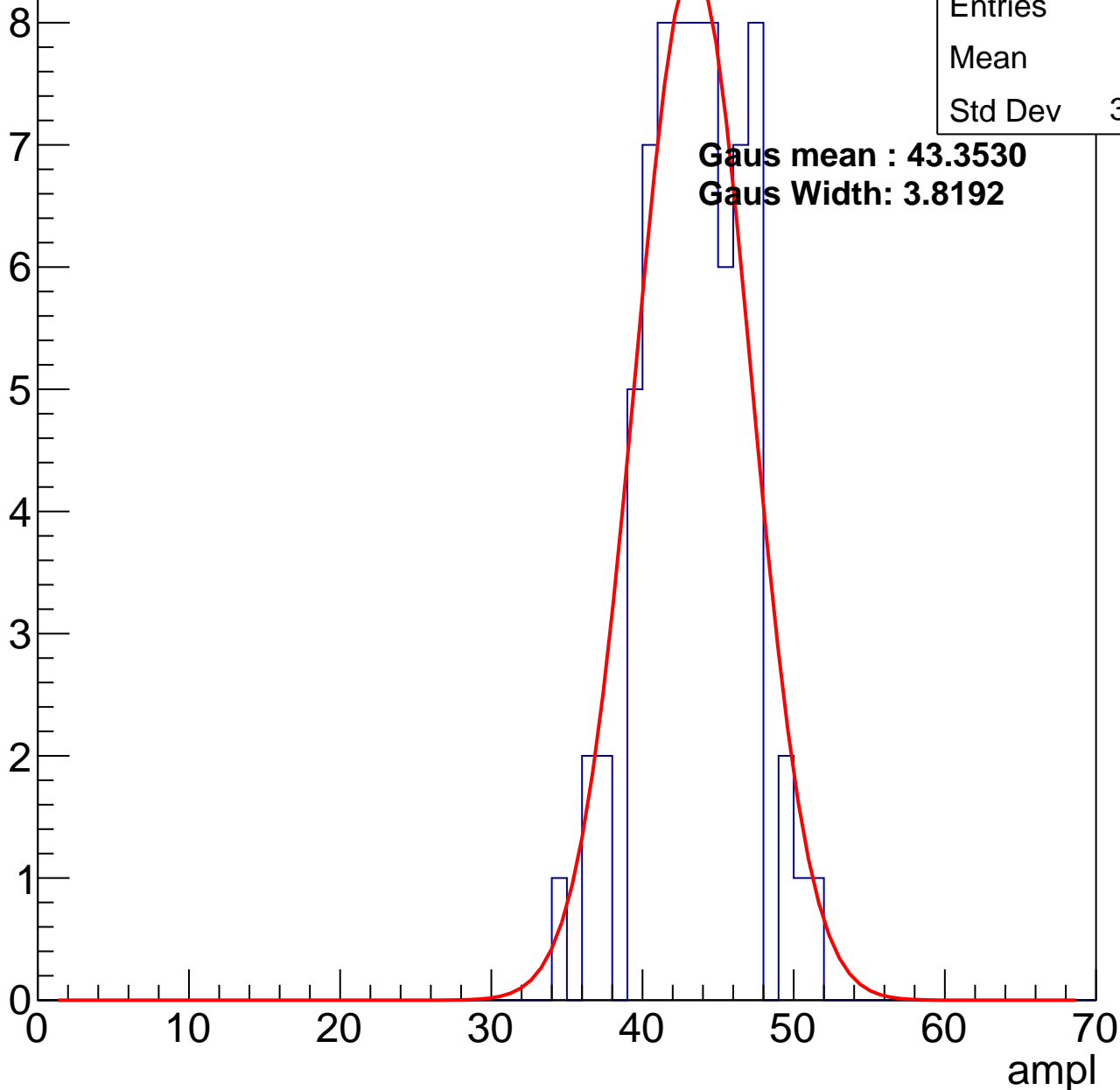
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43
Std Dev	3.373

Gaus mean : 43.3530

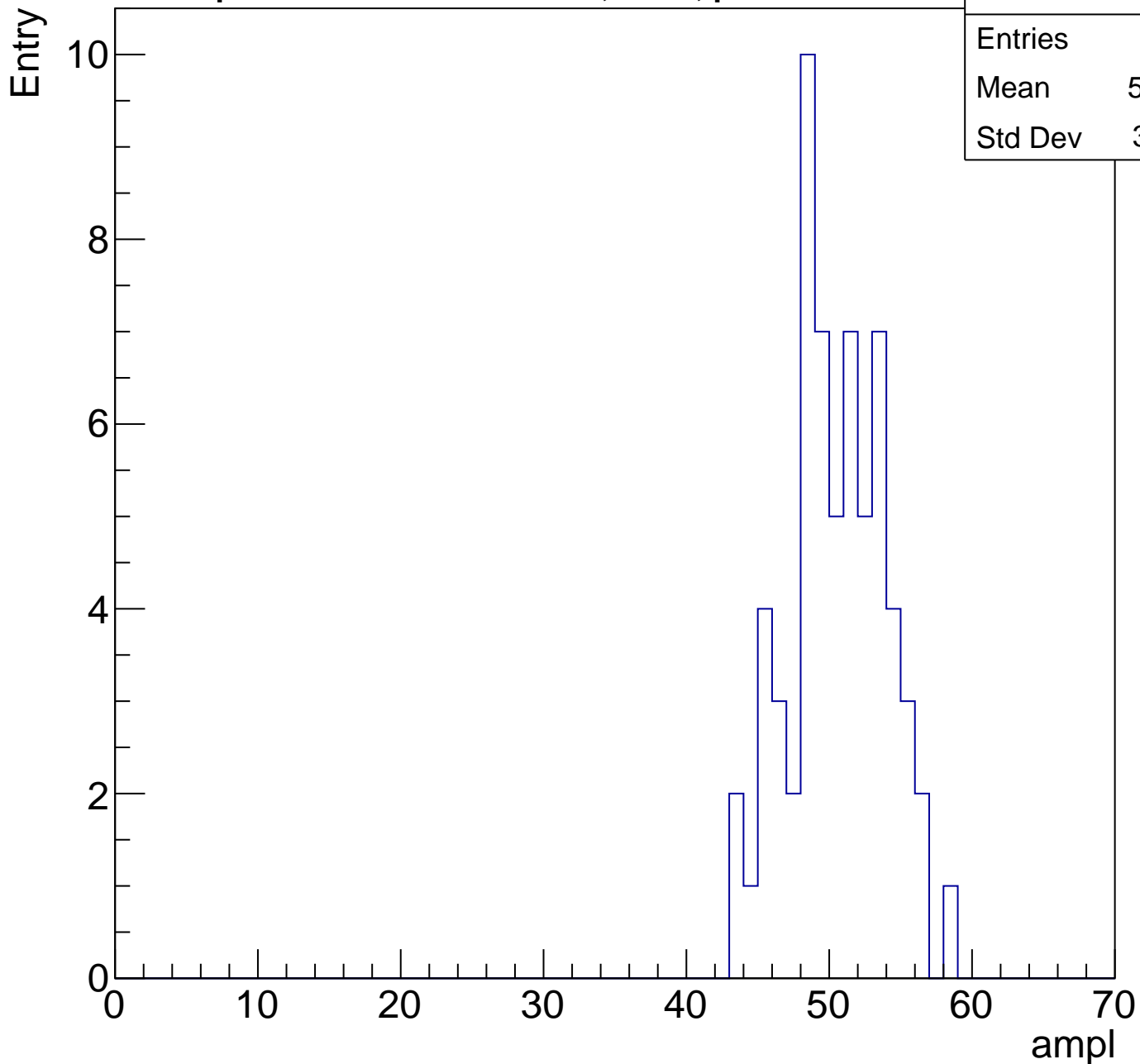
Gaus Width: 3.8192



B1L100S, U11-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

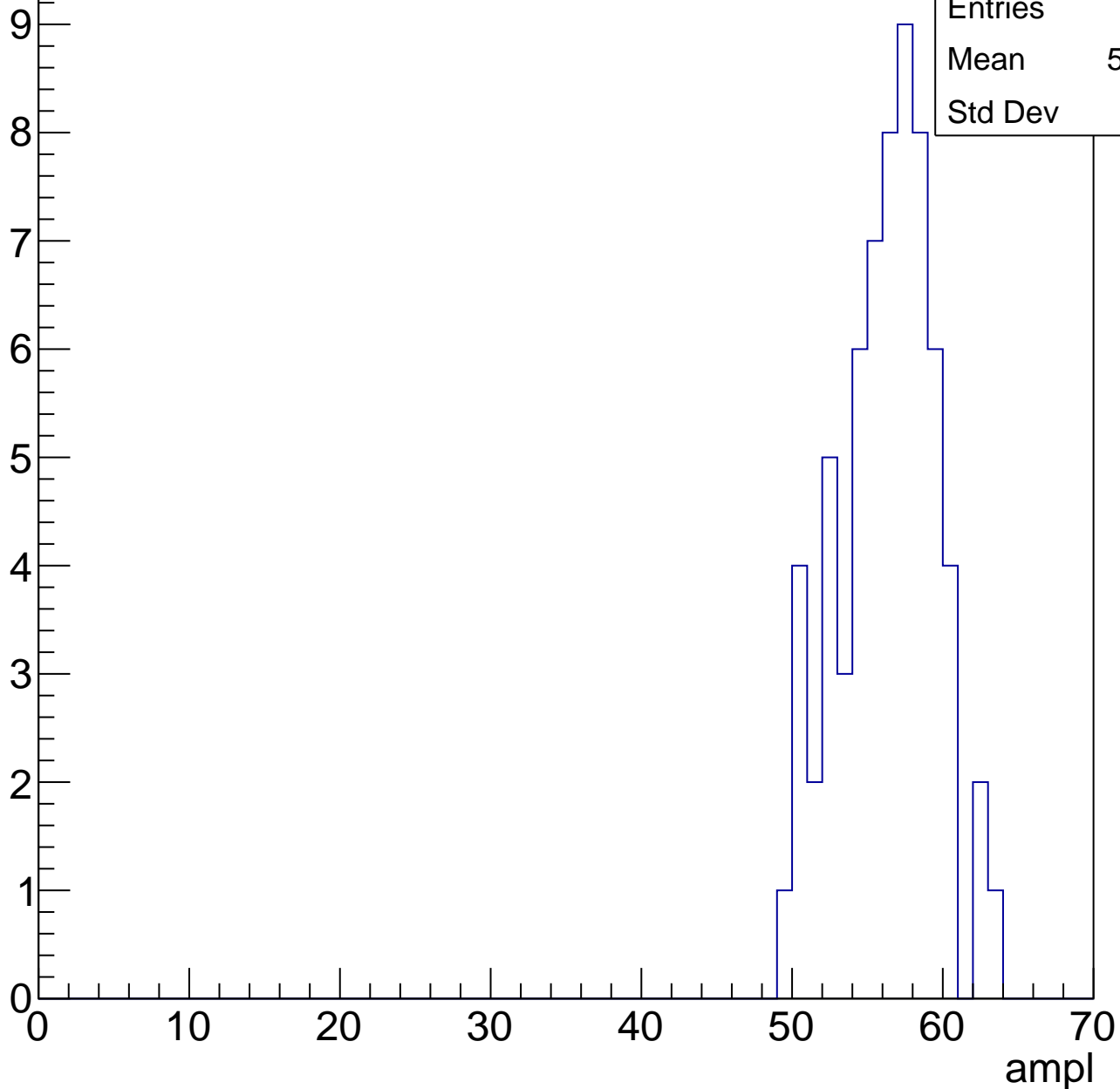
Entries	63
Mean	50.06
Std Dev	3.361



B1L100S, U11-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

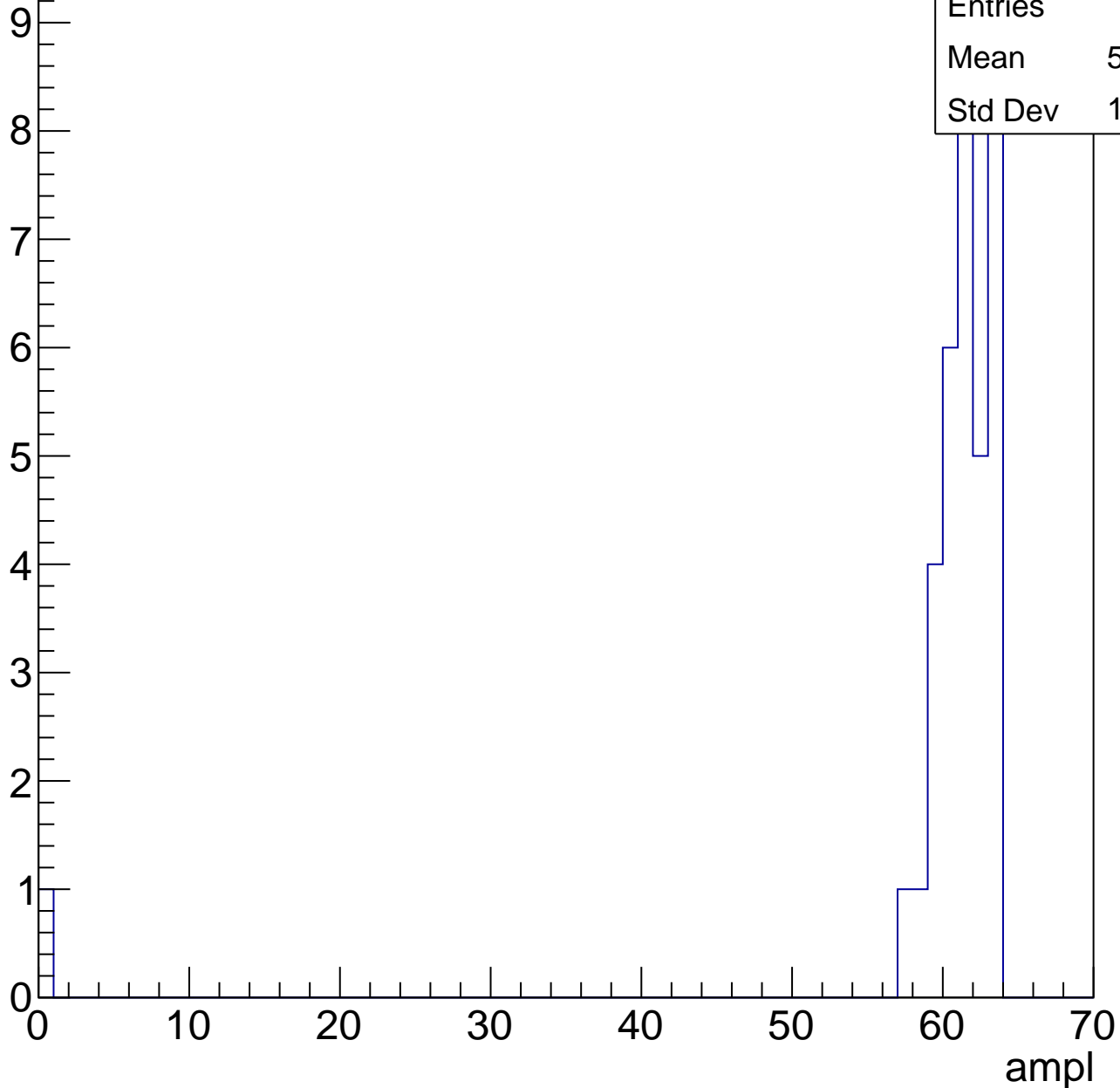
Entry



B1L100S, U11-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch42, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	29.08
Std Dev	3.785

Gaus mean : 30.4313

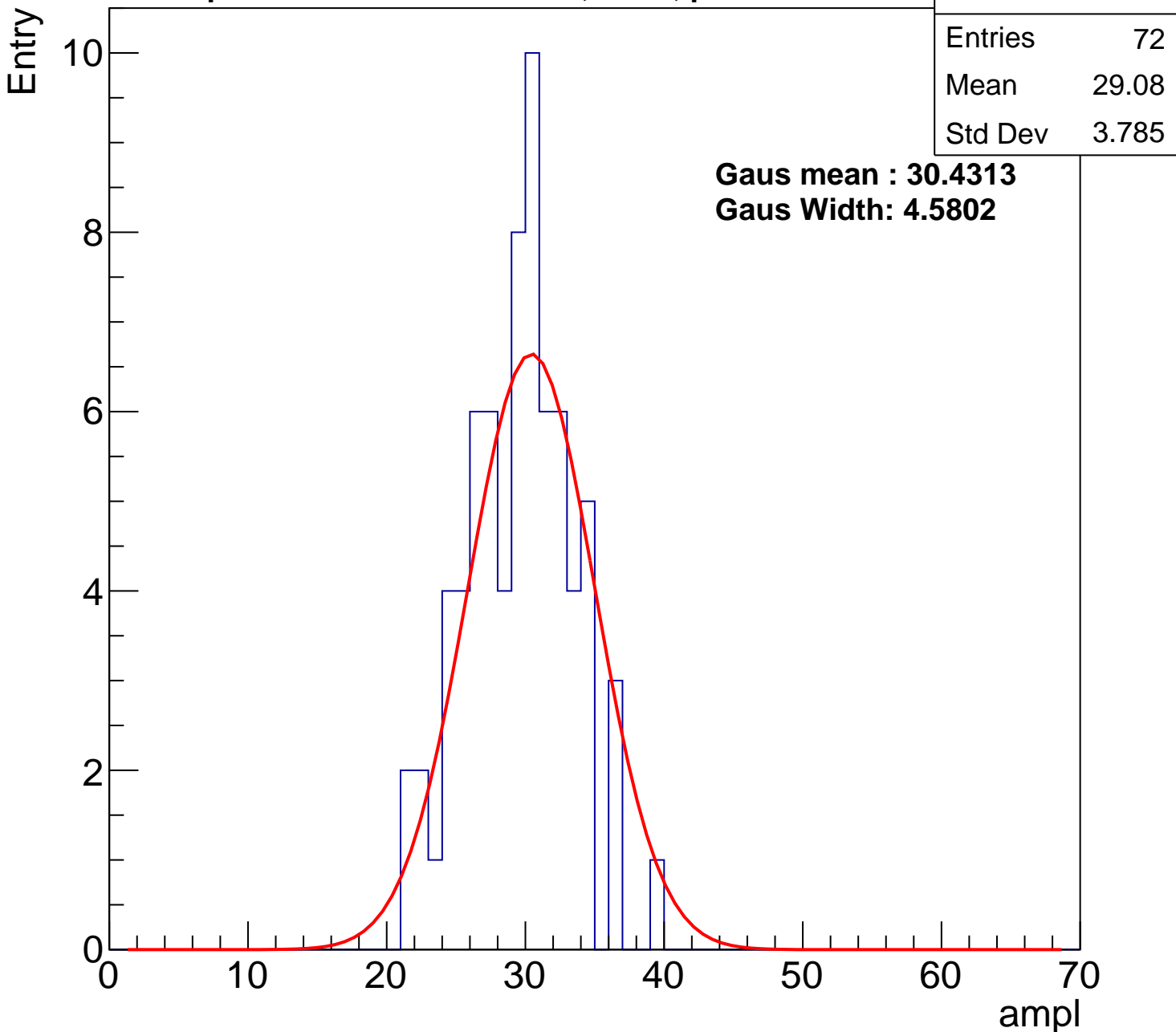
Gaus Width: 4.5802

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch42, adc1

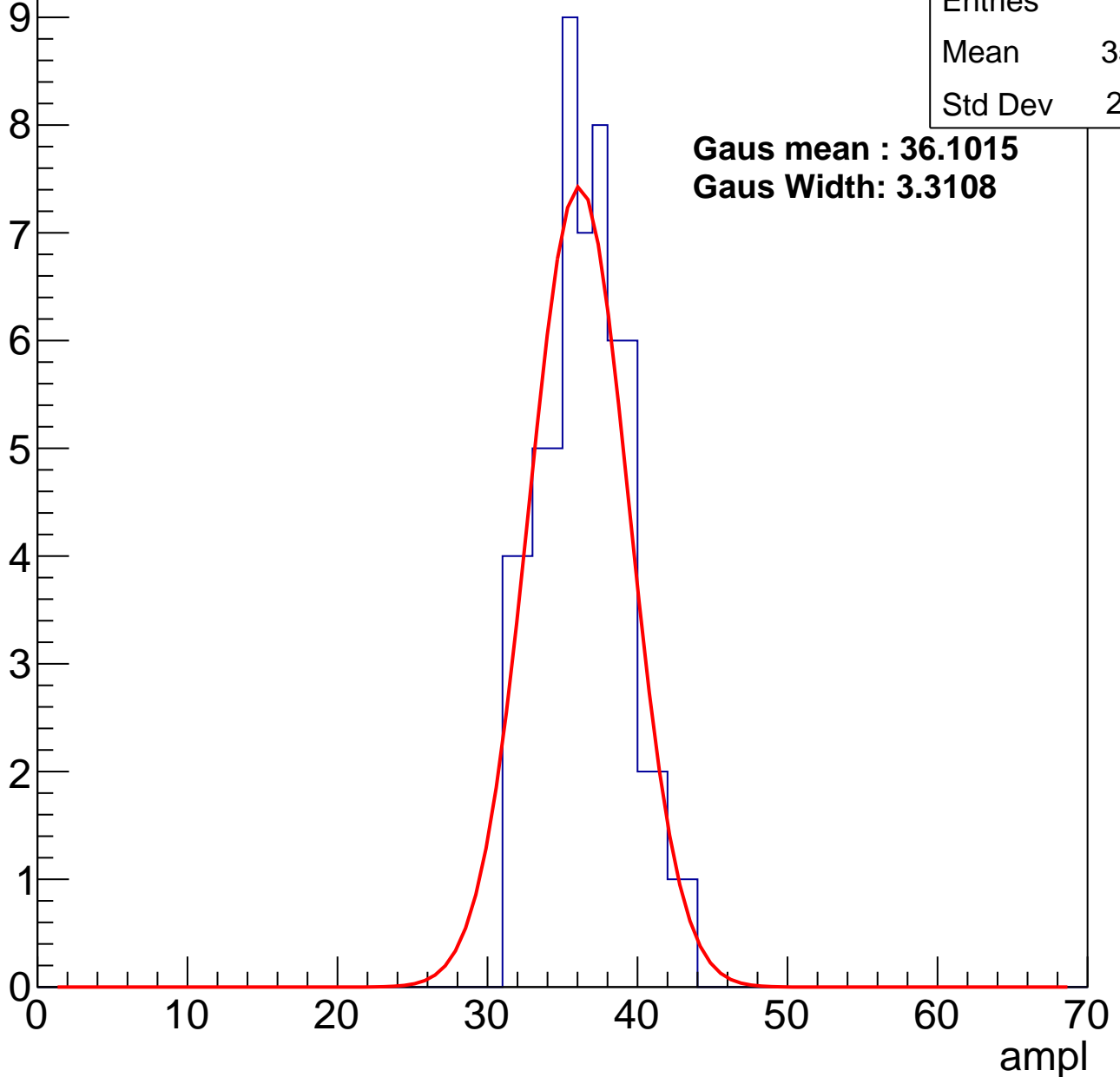
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	35.98
Std Dev	2.861

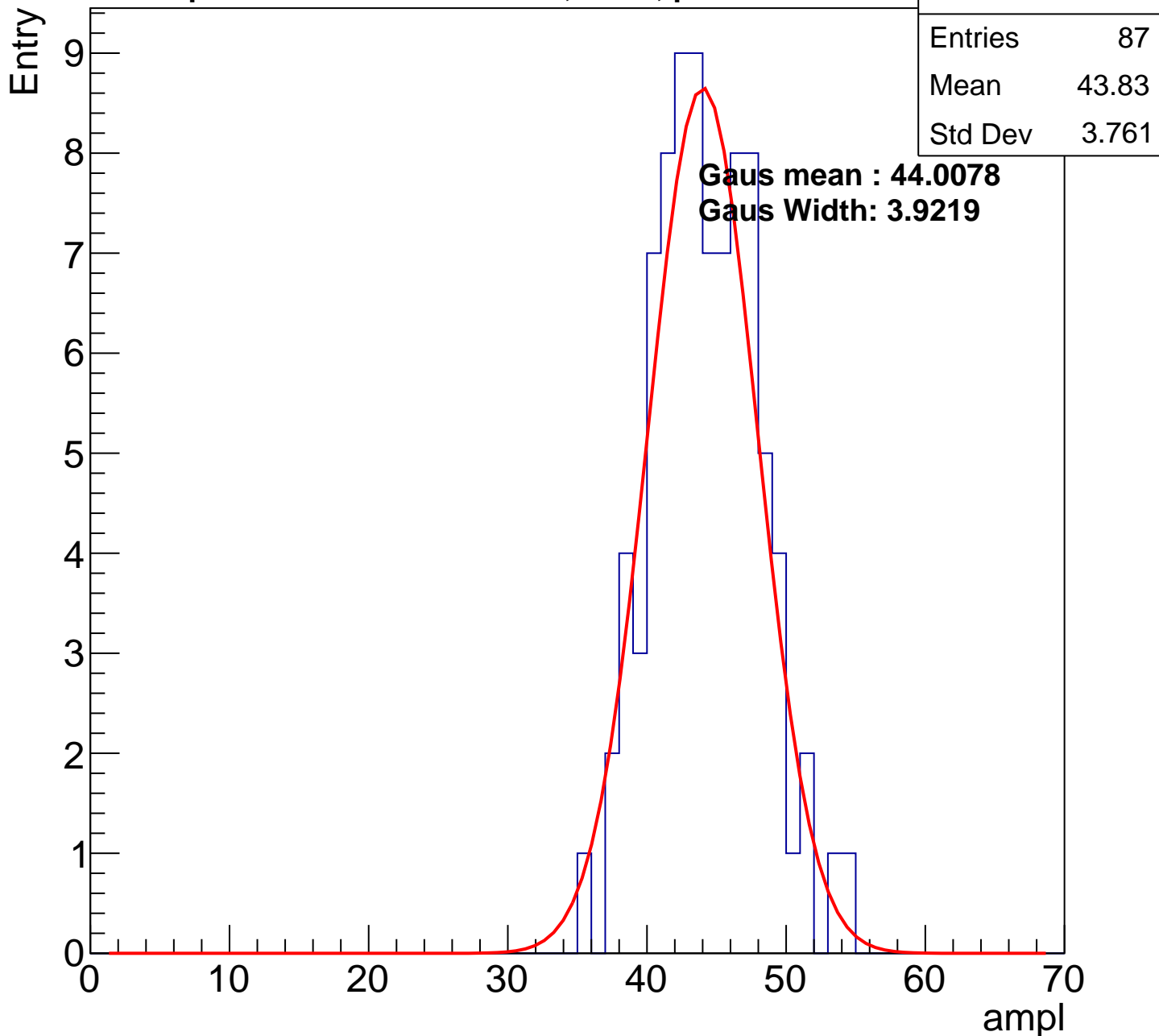
Gaus mean : 36.1015

Gaus Width: 3.3108



B1L100S, U11-ch42, adc2

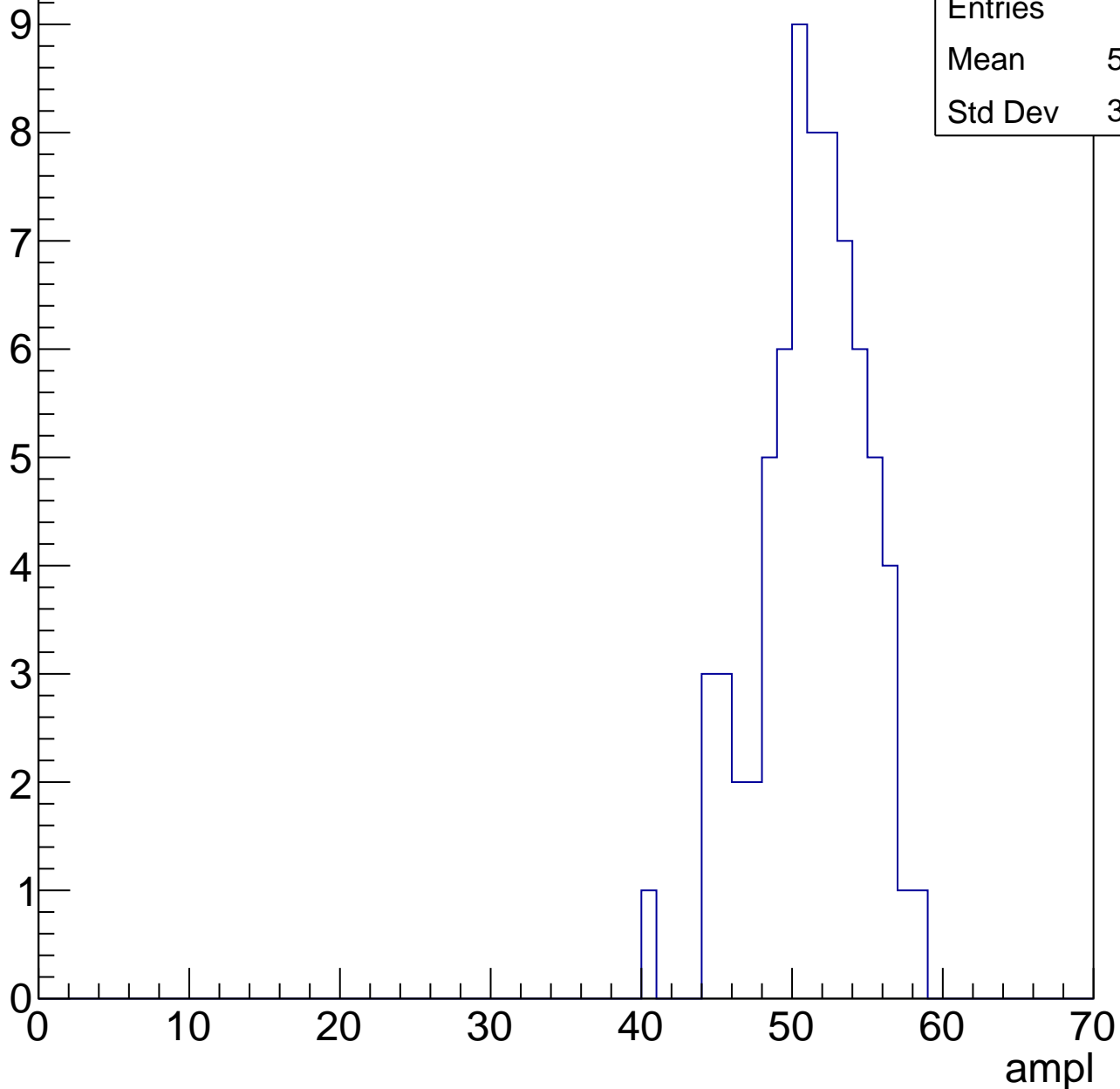
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

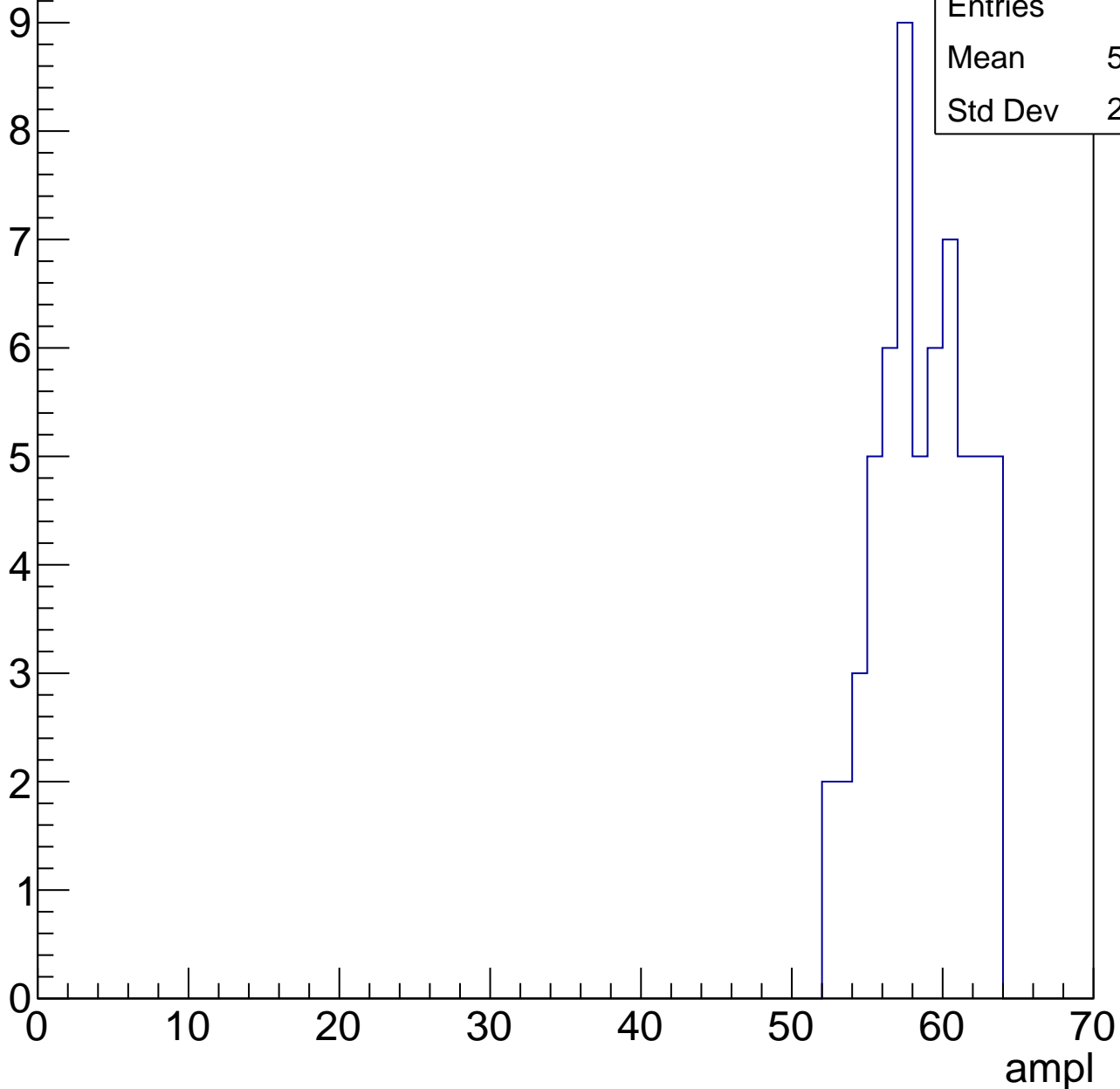


Entries	71
Mean	50.85
Std Dev	3.539

B1L100S, U11-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

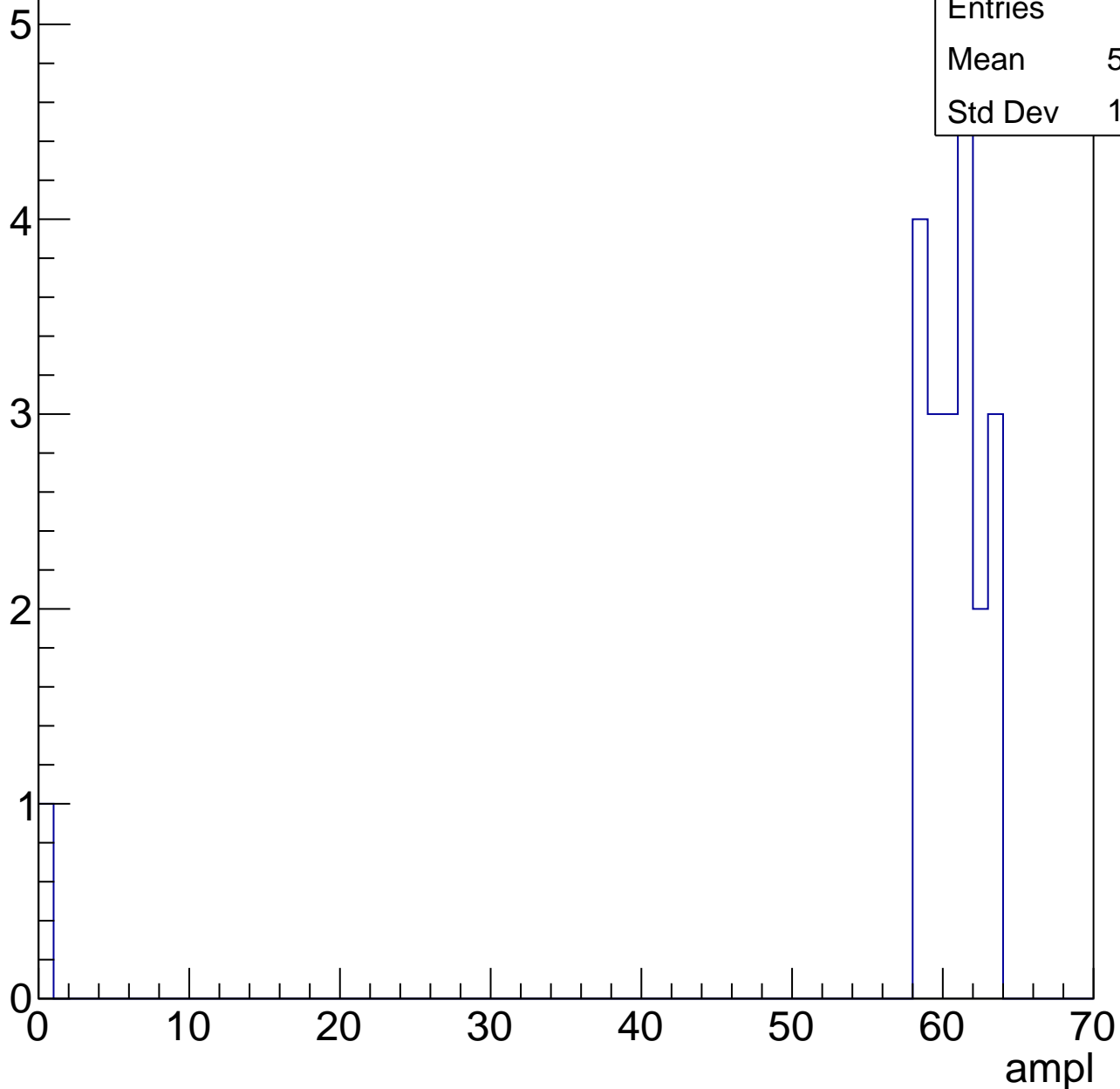


B1L100S, U11-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

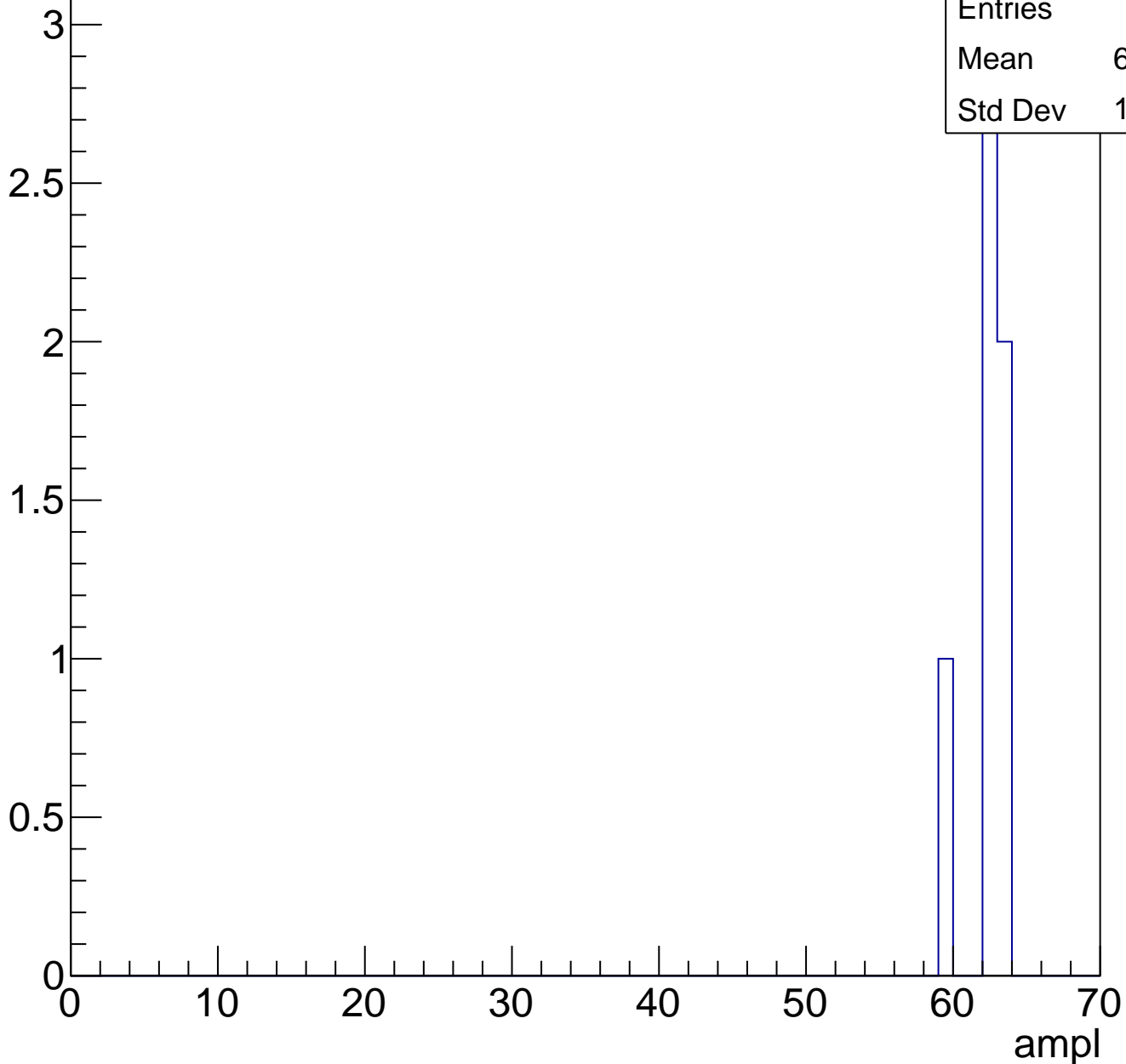
Entries	21
Mean	57.48
Std Dev	12.96



B1L100S, U11-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

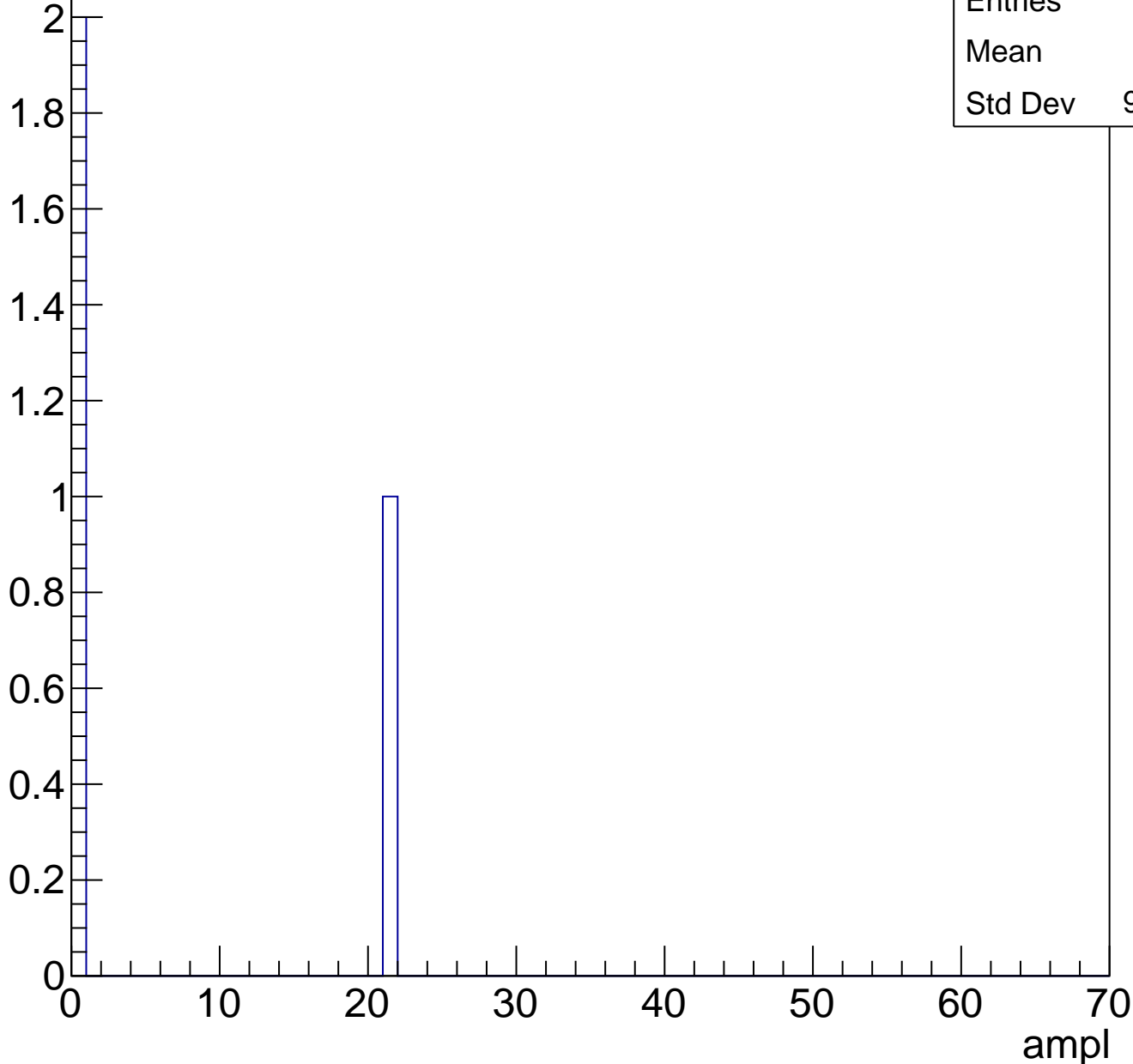
Entry



B1L100S, U11-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L100S, U11-ch43, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	32.3
Std Dev	5.19

Gaus mean : 32.5562

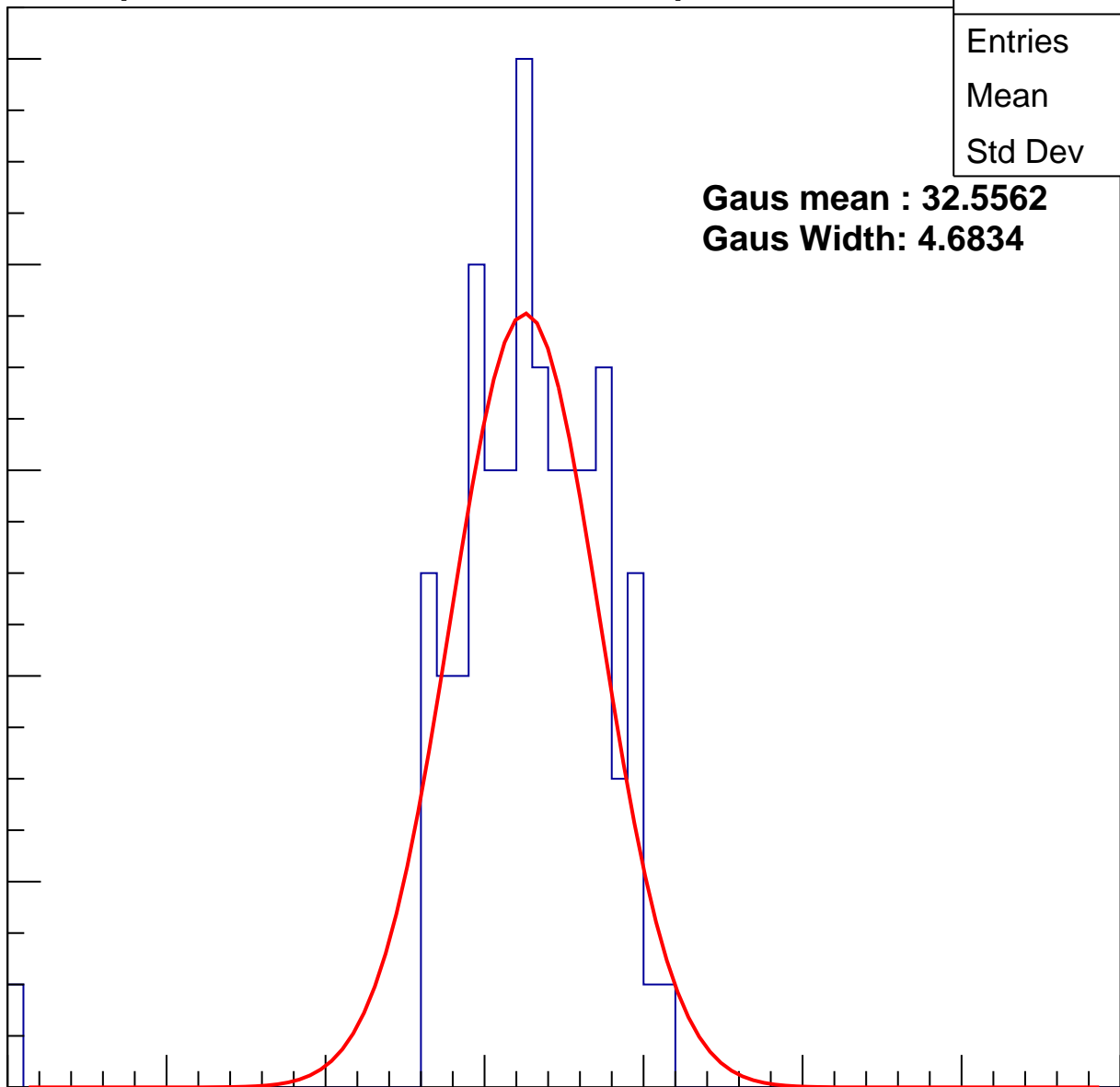
Gaus Width: 4.6834

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch43, adc1

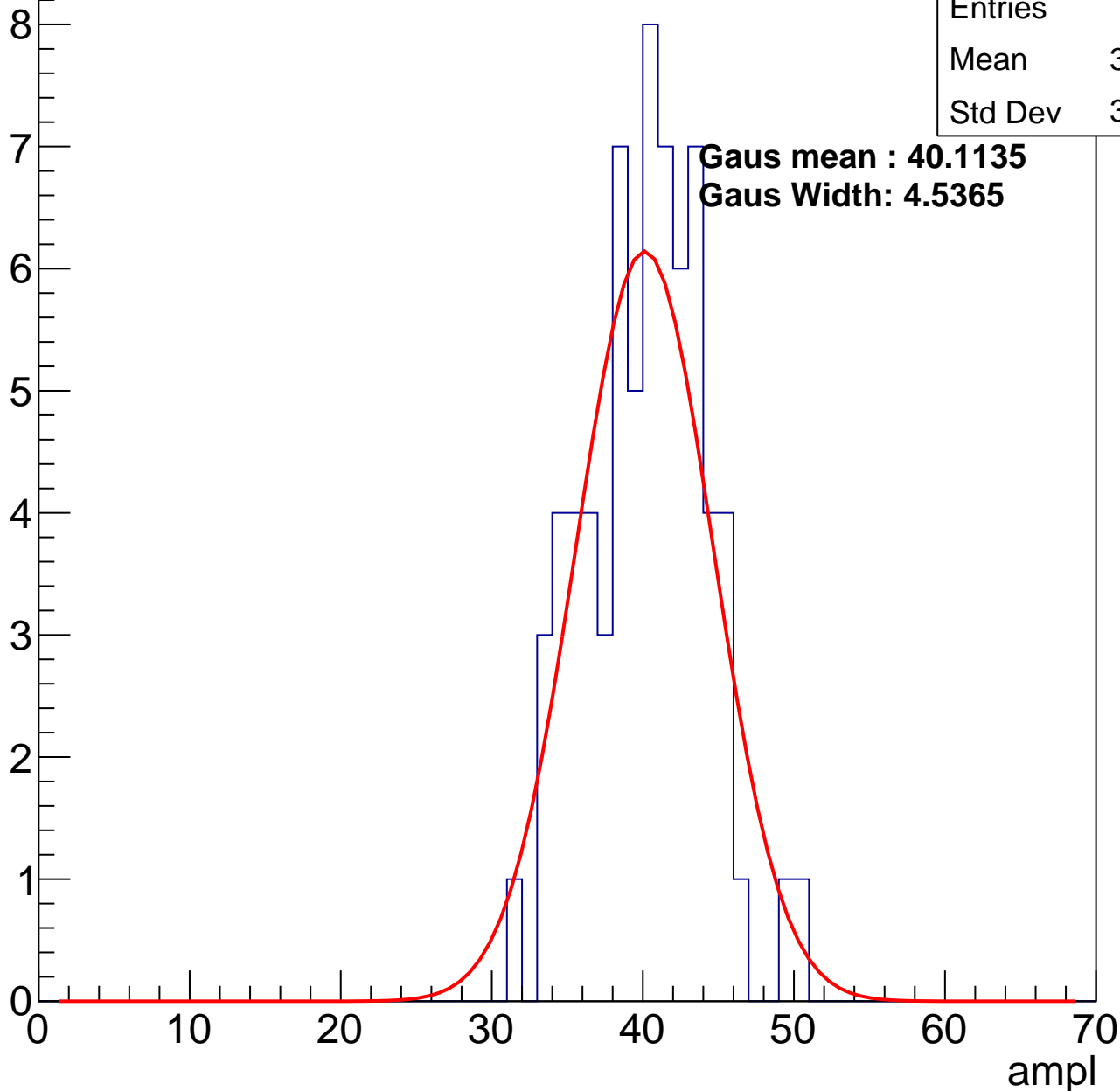
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	39.76
Std Dev	3.904

Gaus mean : 40.1135

Gaus Width: 4.5365



B1L100S, U11-ch43, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	46.39
Std Dev	3.071

Gaus mean : 46.5444

Gaus Width: 3.1390

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

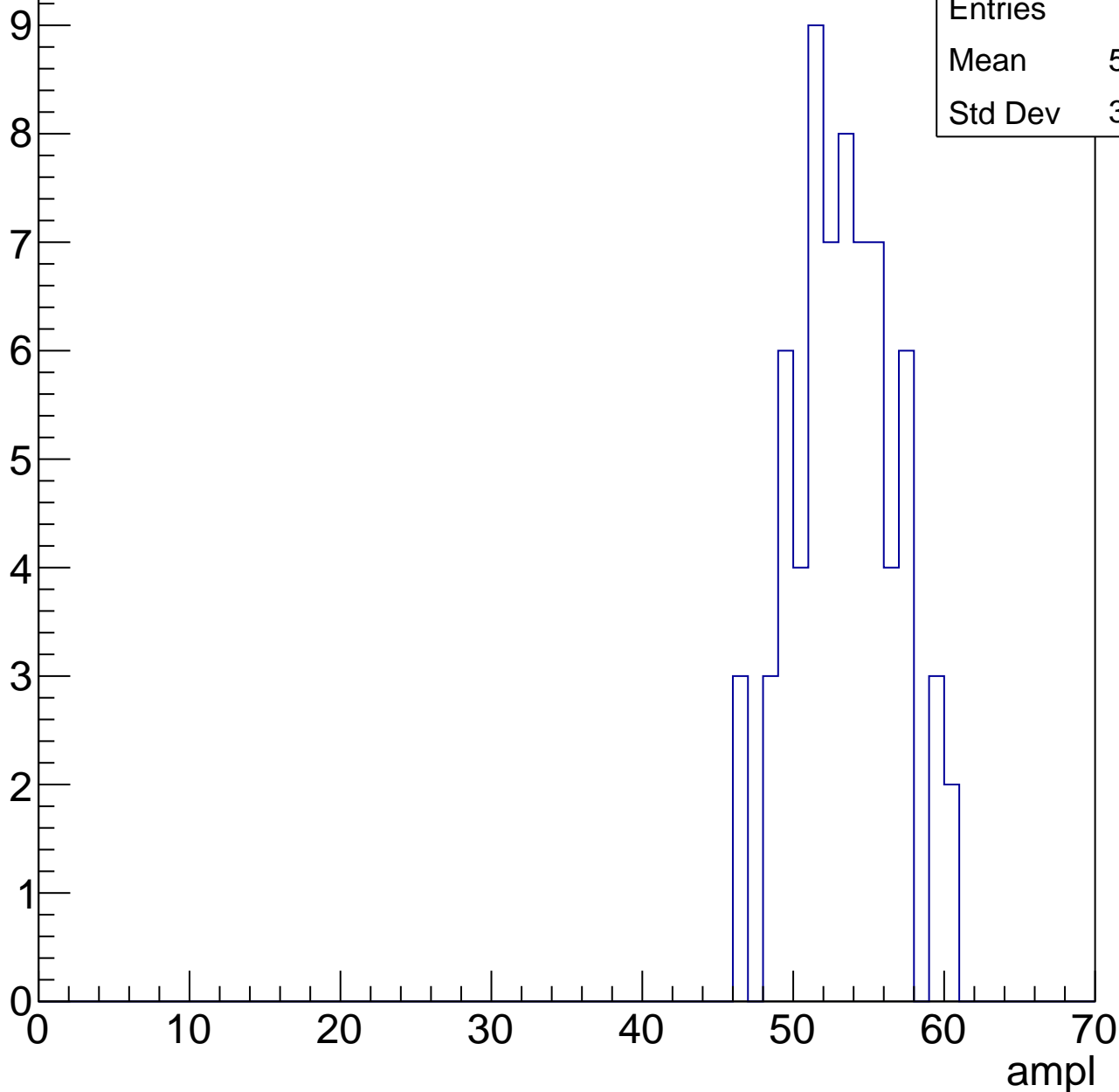
10

B1L100S, U11-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

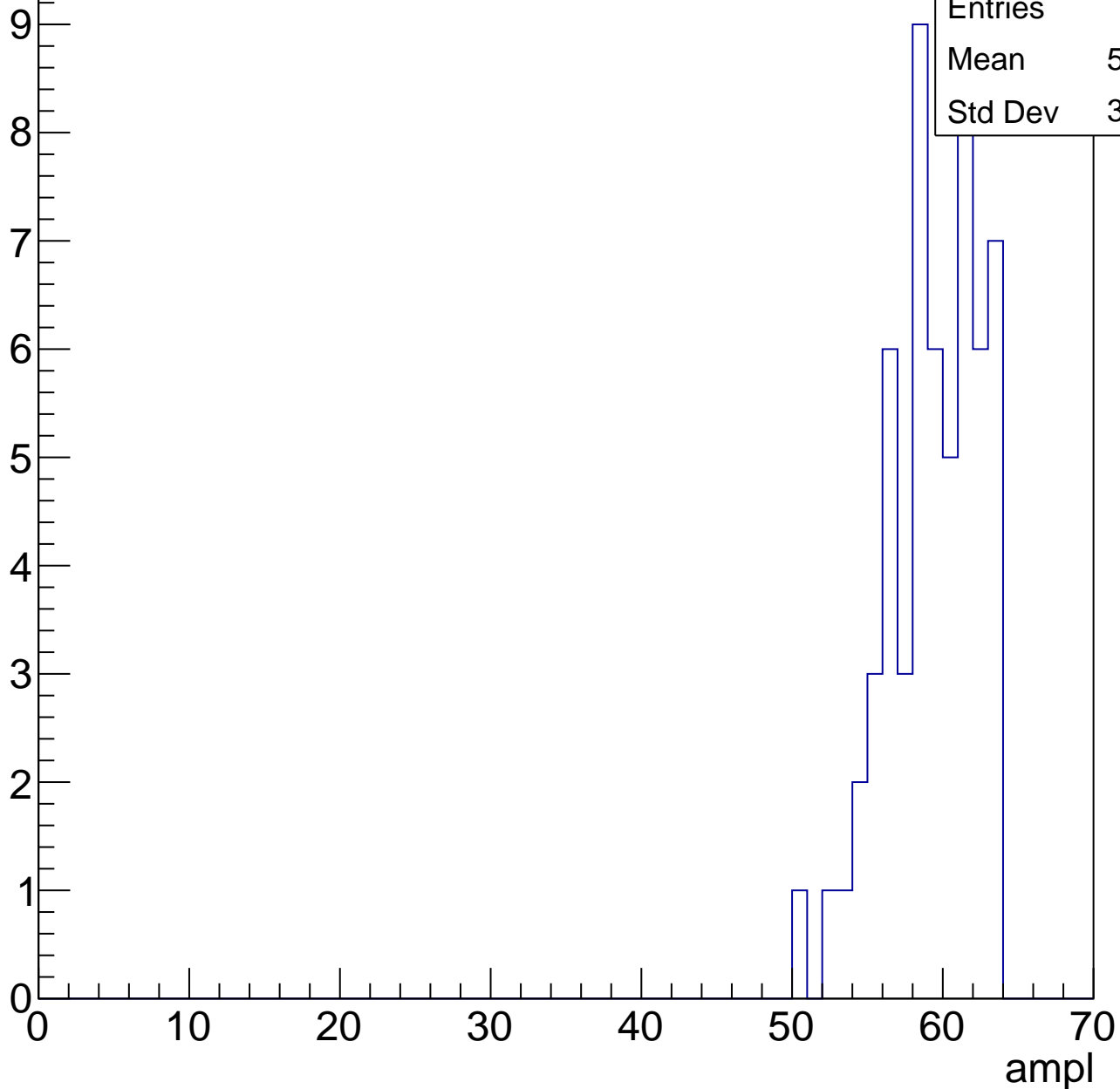
Entries	69
Mean	52.88
Std Dev	3.339



B1L100S, U11-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

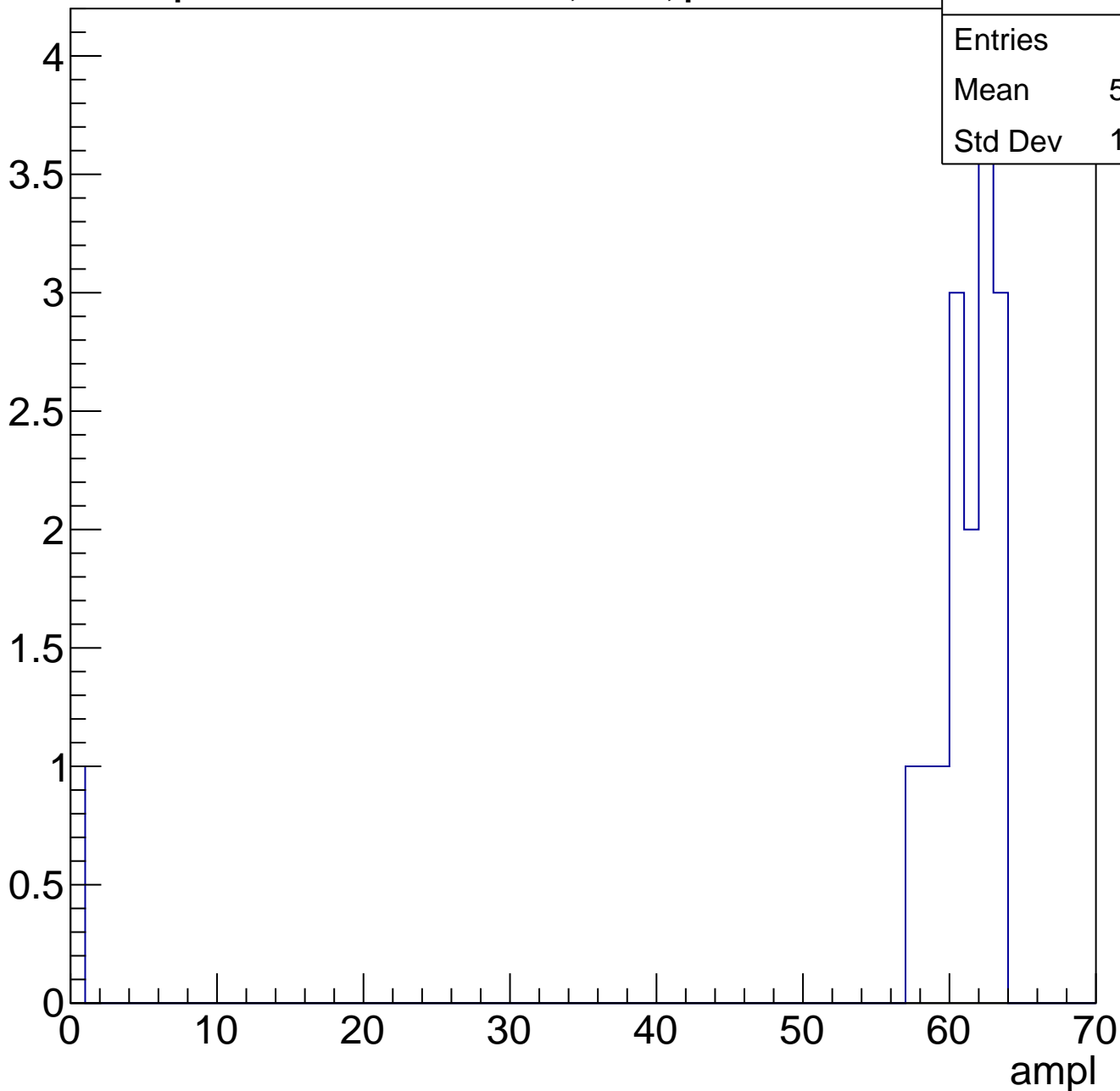
Entry



B1L100S, U11-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

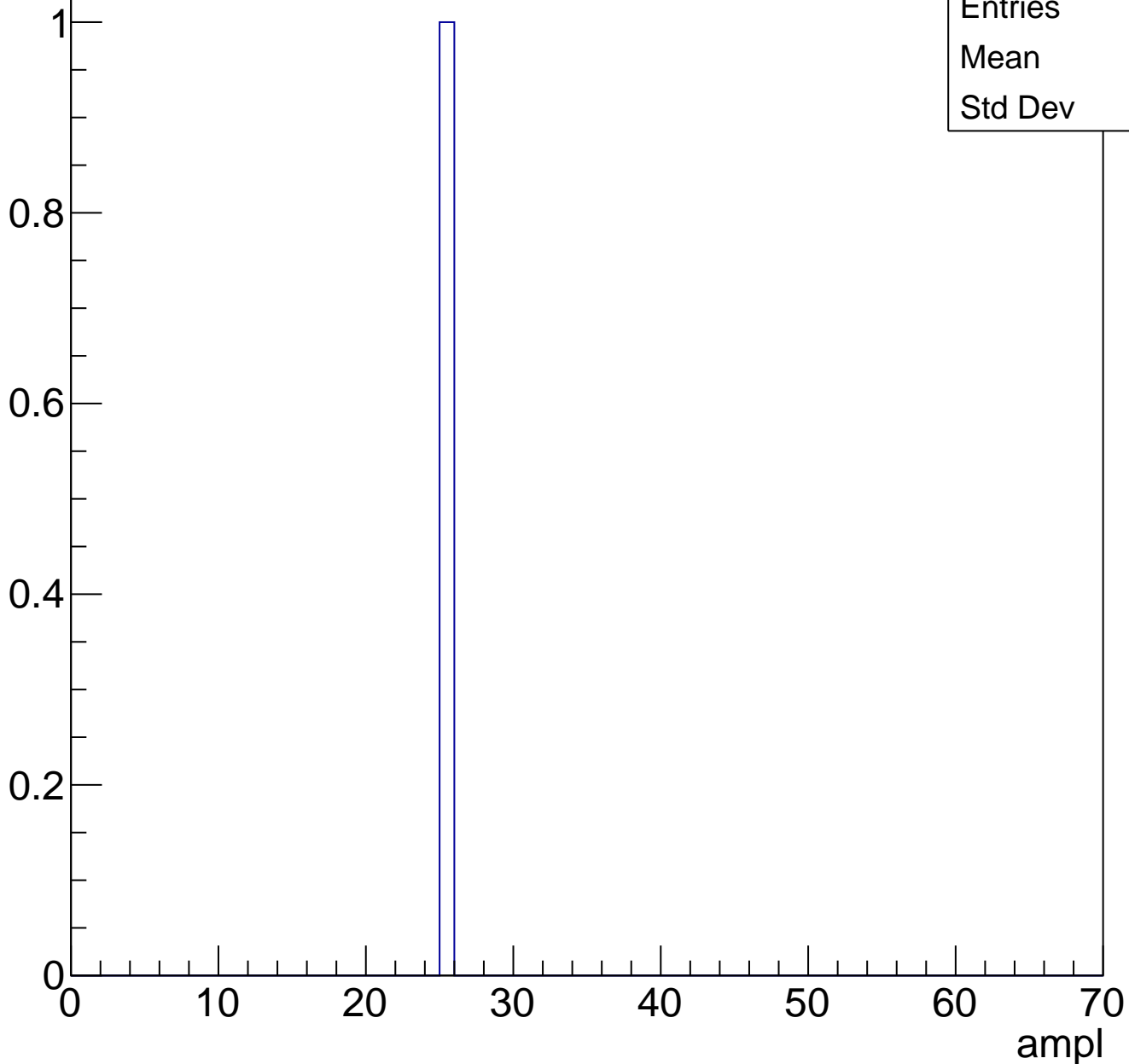
Entry



B1L100S, U11-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch44, adc0

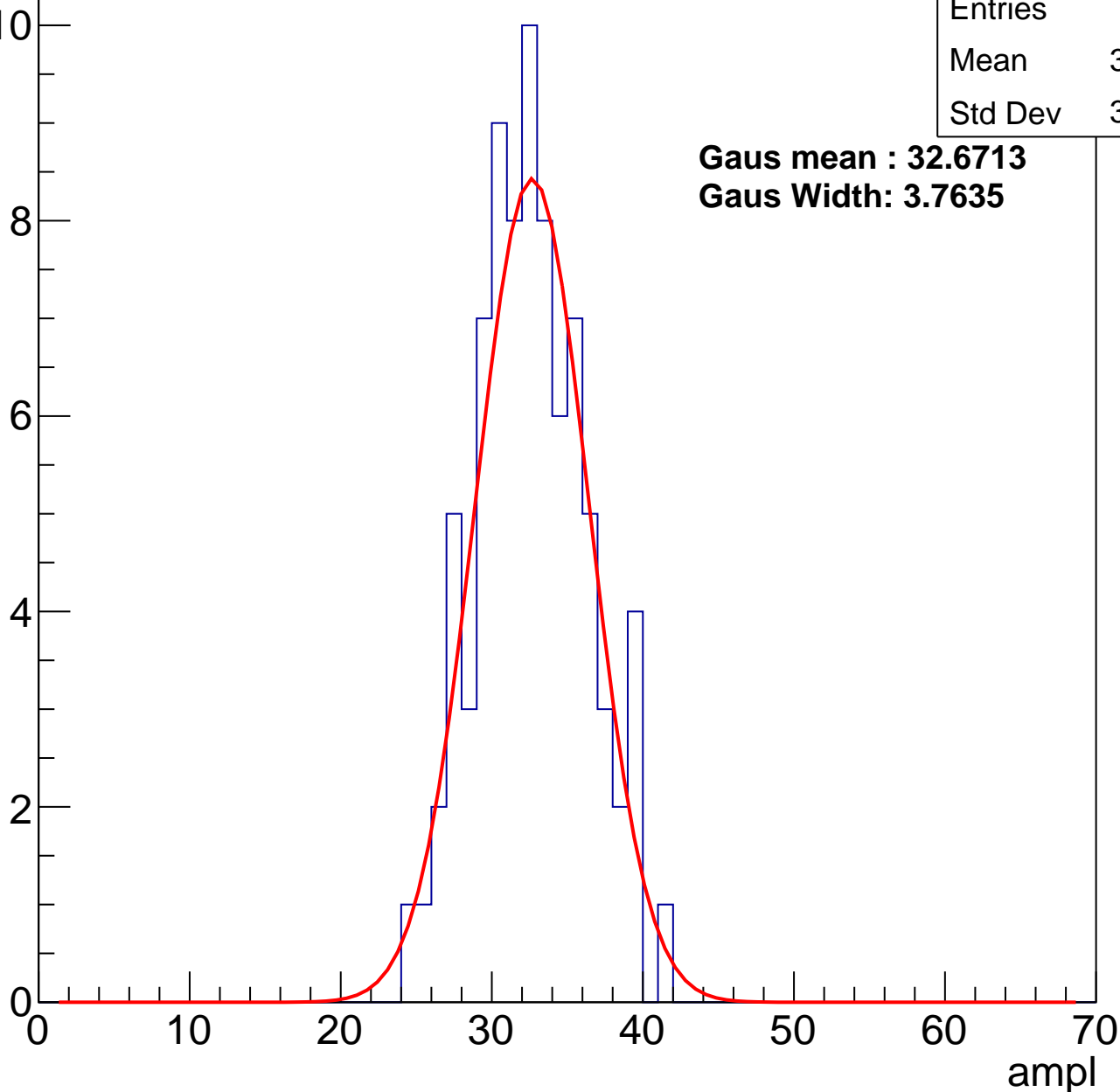
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	32.17
Std Dev	3.598

Gaus mean : 32.6713

Gaus Width: 3.7635



B1L100S, U11-ch44, adc1

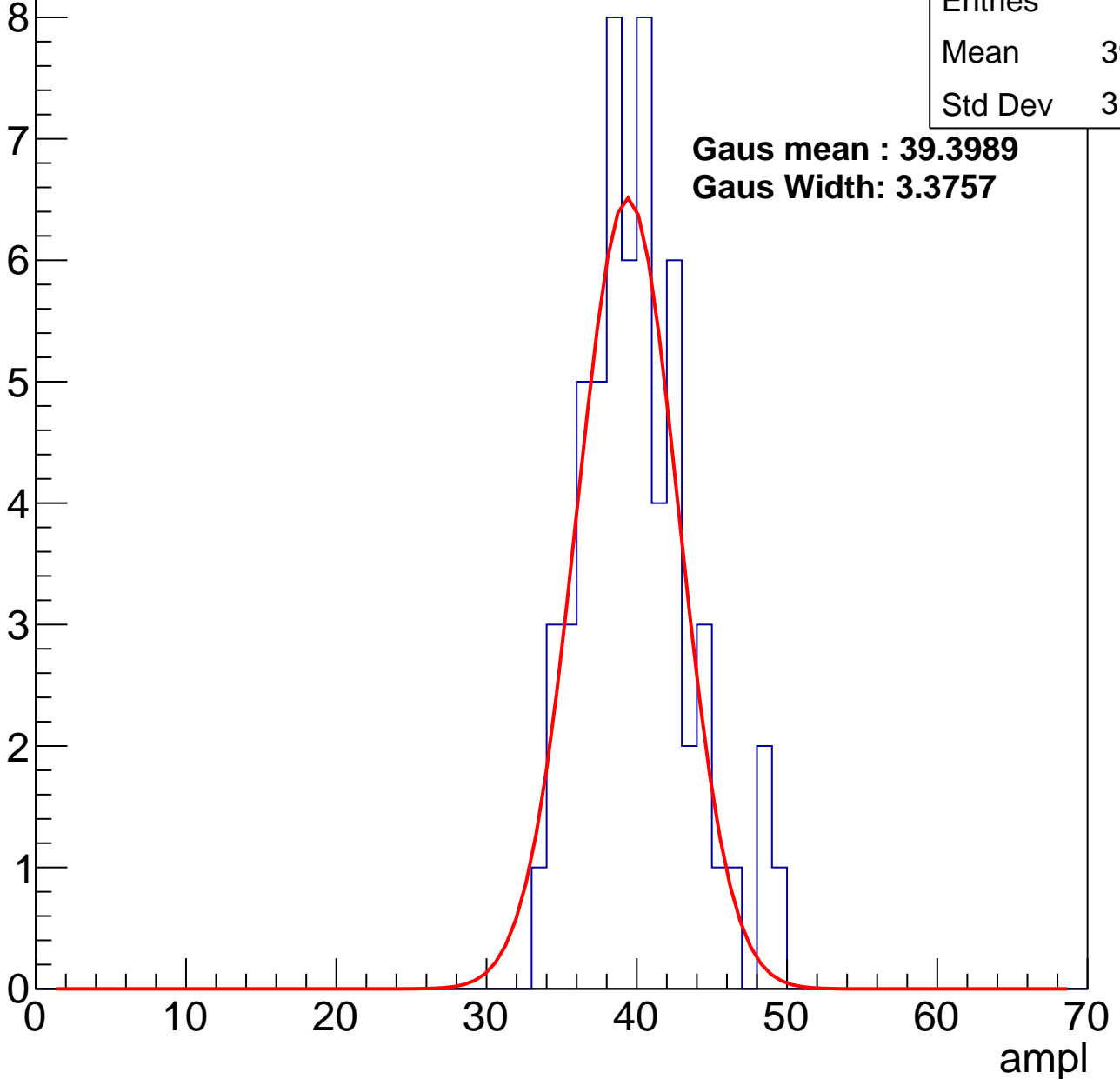
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	39.54
Std Dev	3.557

Gaus mean : 39.3989

Gaus Width: 3.3757



B1L100S, U11-ch44, adc2

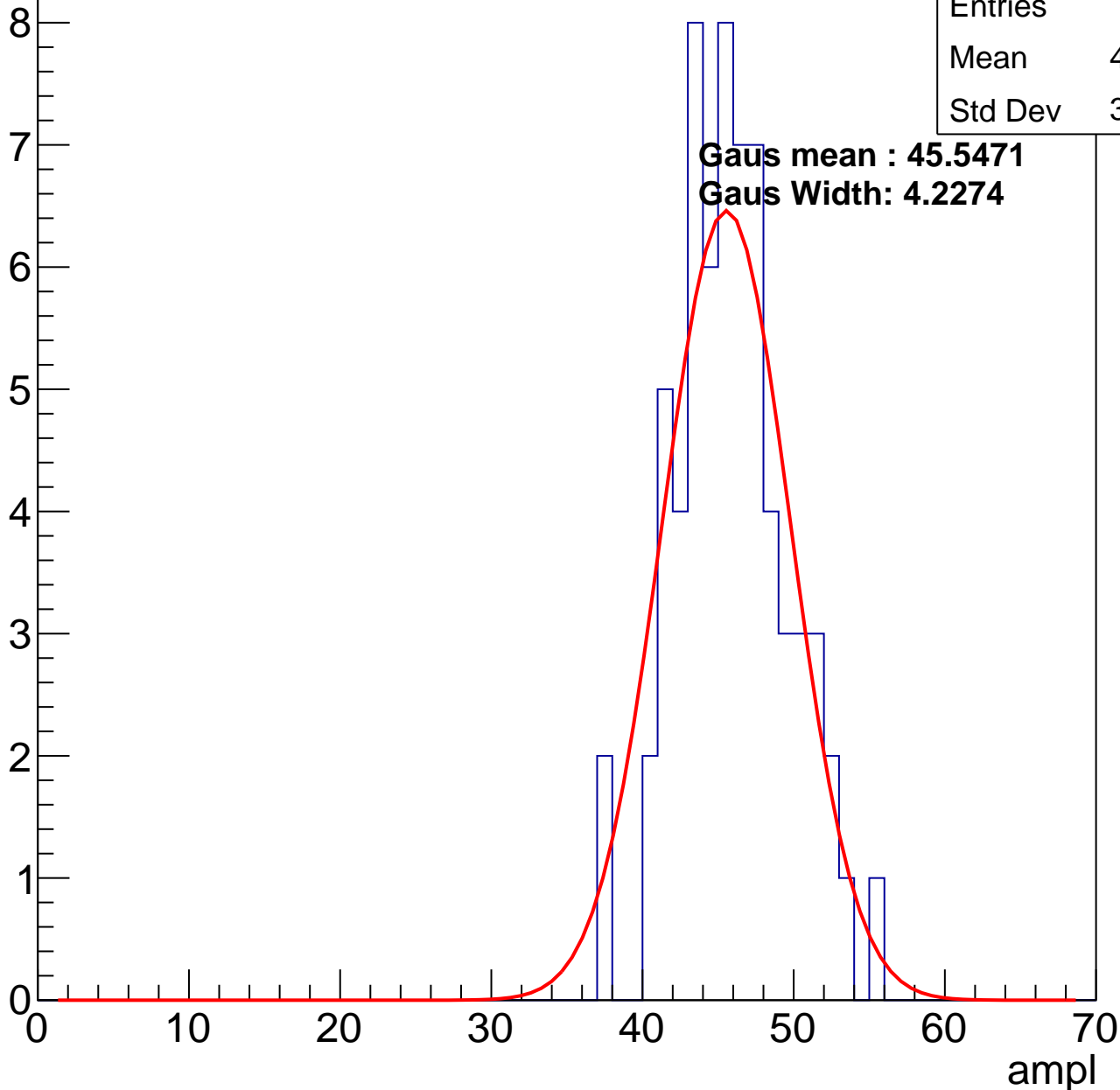
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	45.45
Std Dev	3.656

Gaus mean : 45.5471

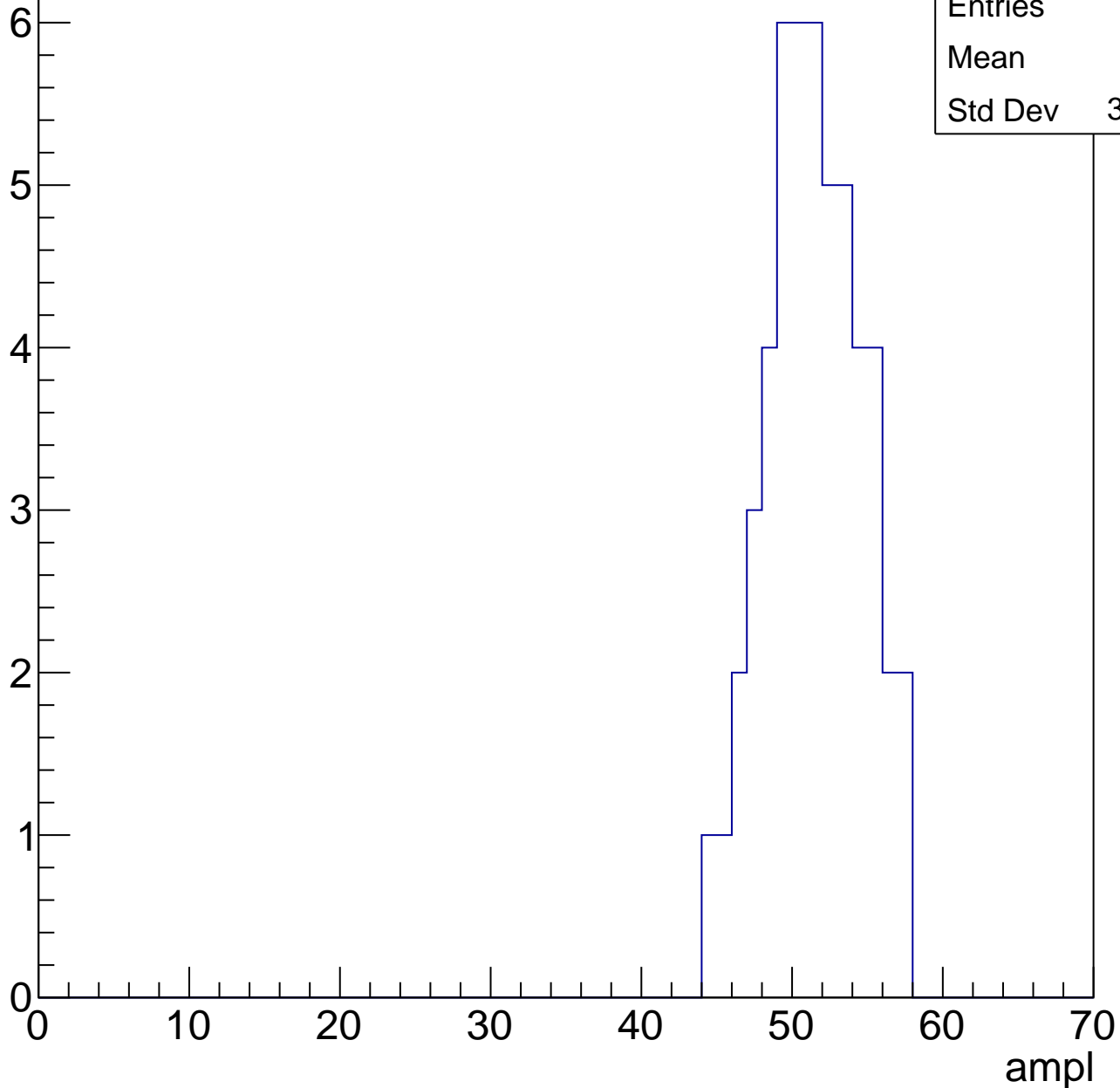
Gaus Width: 4.2274



B1L100S, U11-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

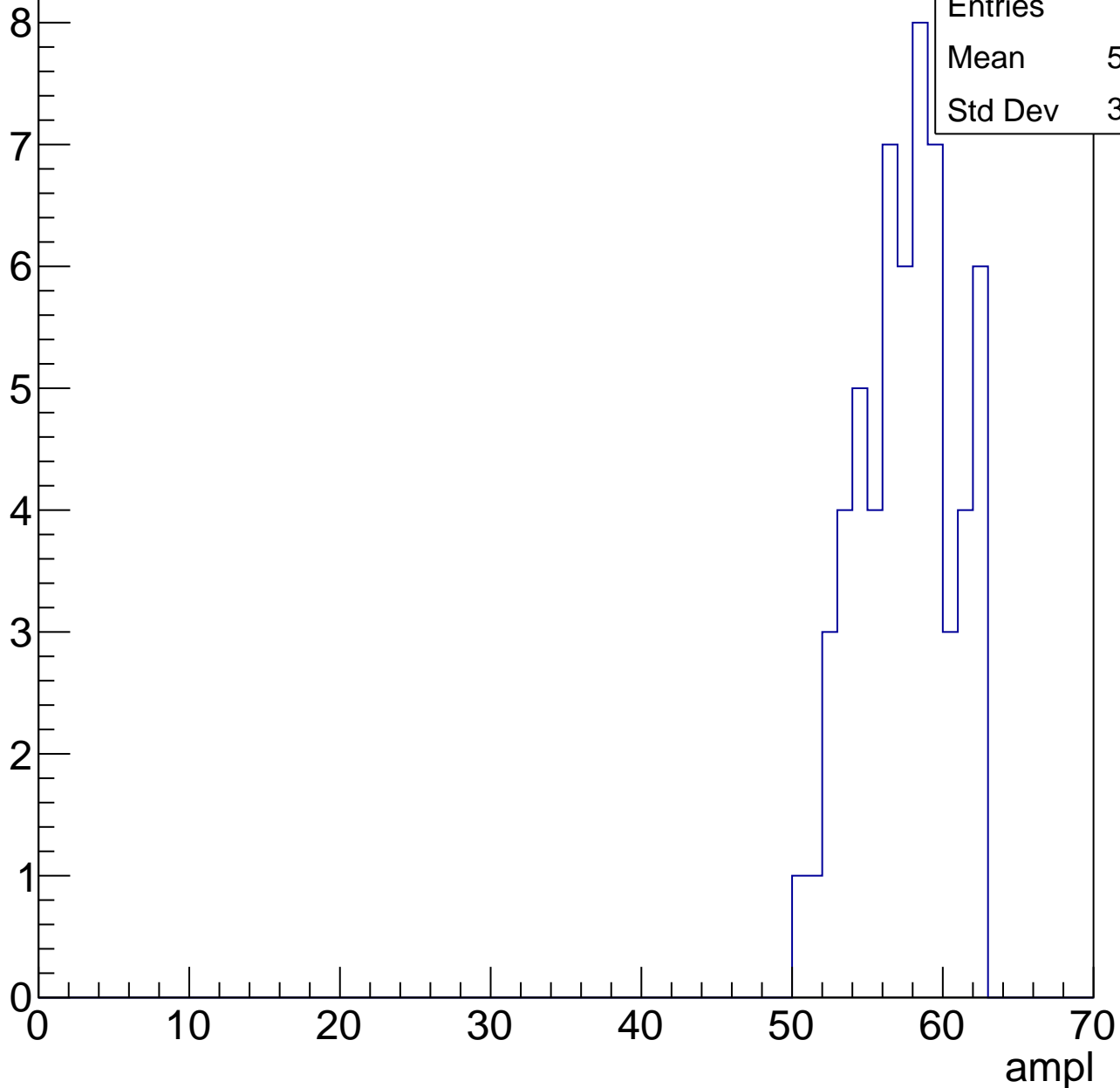


Entries	51
Mean	51
Std Dev	3.119

B1L100S, U11-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	59
Mean	57.05
Std Dev	3.105

B1L100S, U11-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 38

Mean 58.84

Std Dev 9.957

8

6

4

2

0

0

10

20

30

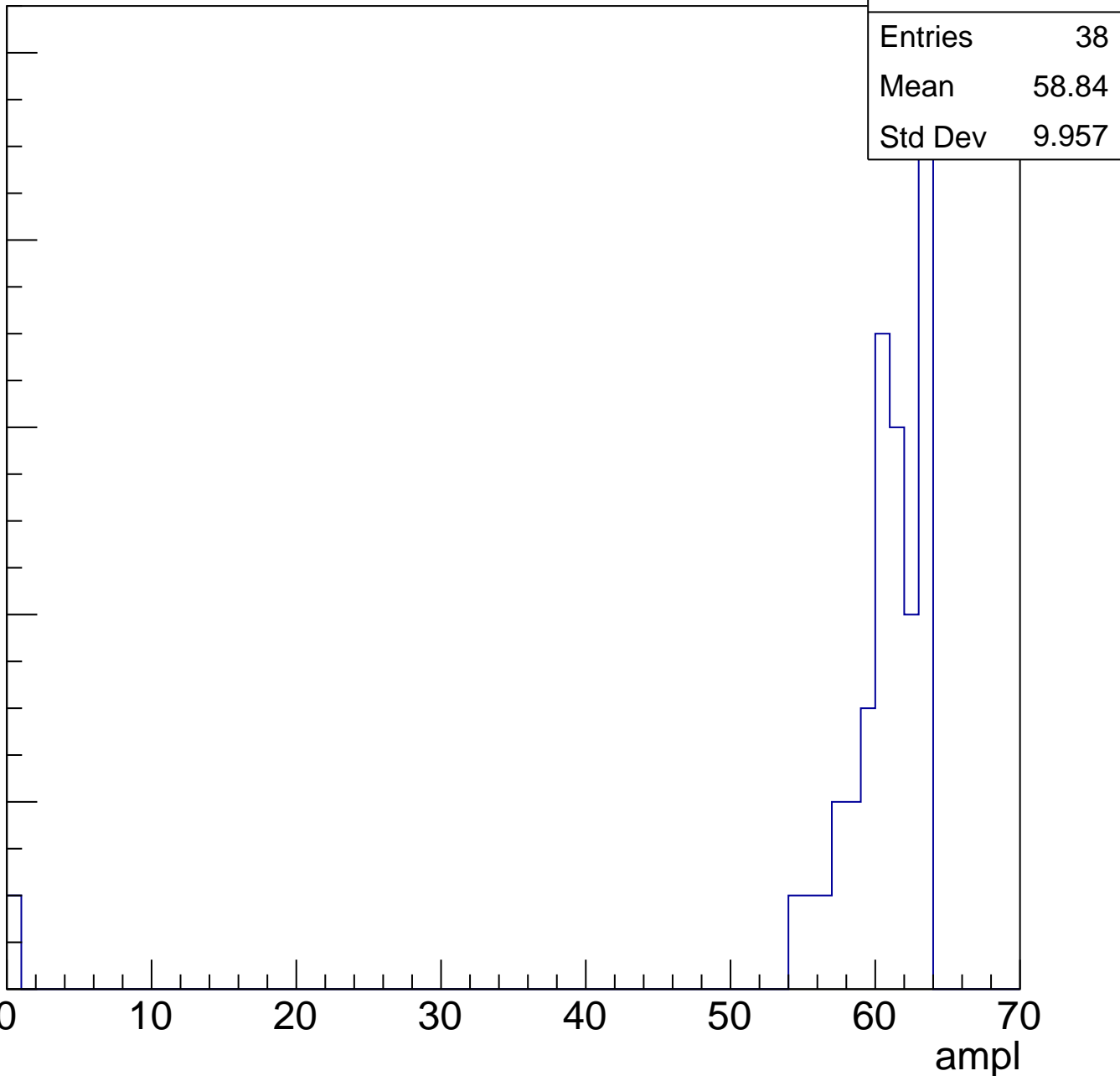
40

50

60

70

ampl



B1L100S, U11-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch45, adc0

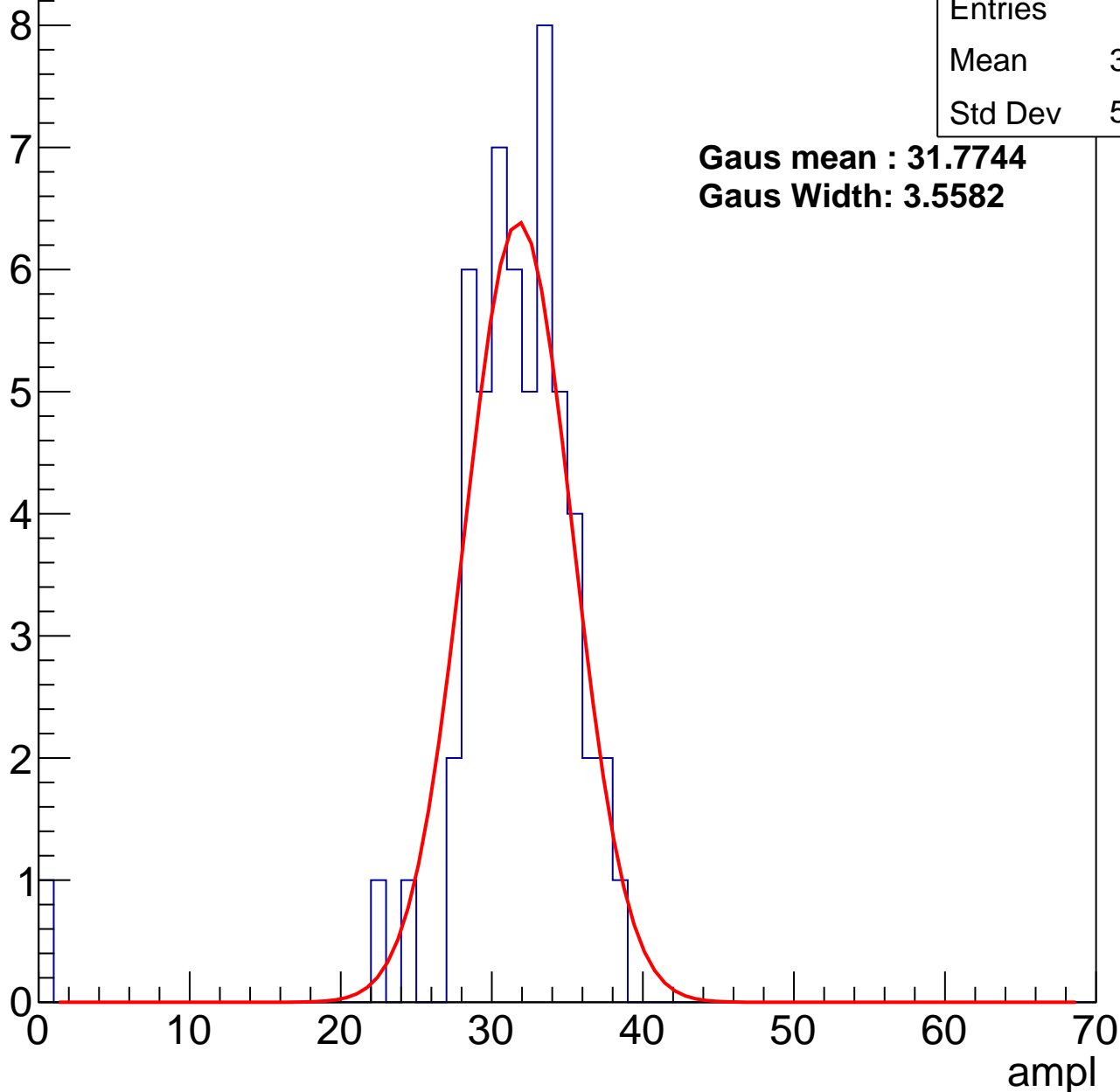
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	30.84
Std Dev	5.209

Gaus mean : 31.7744

Gaus Width: 3.5582



B1L100S, U11-ch45, adc1

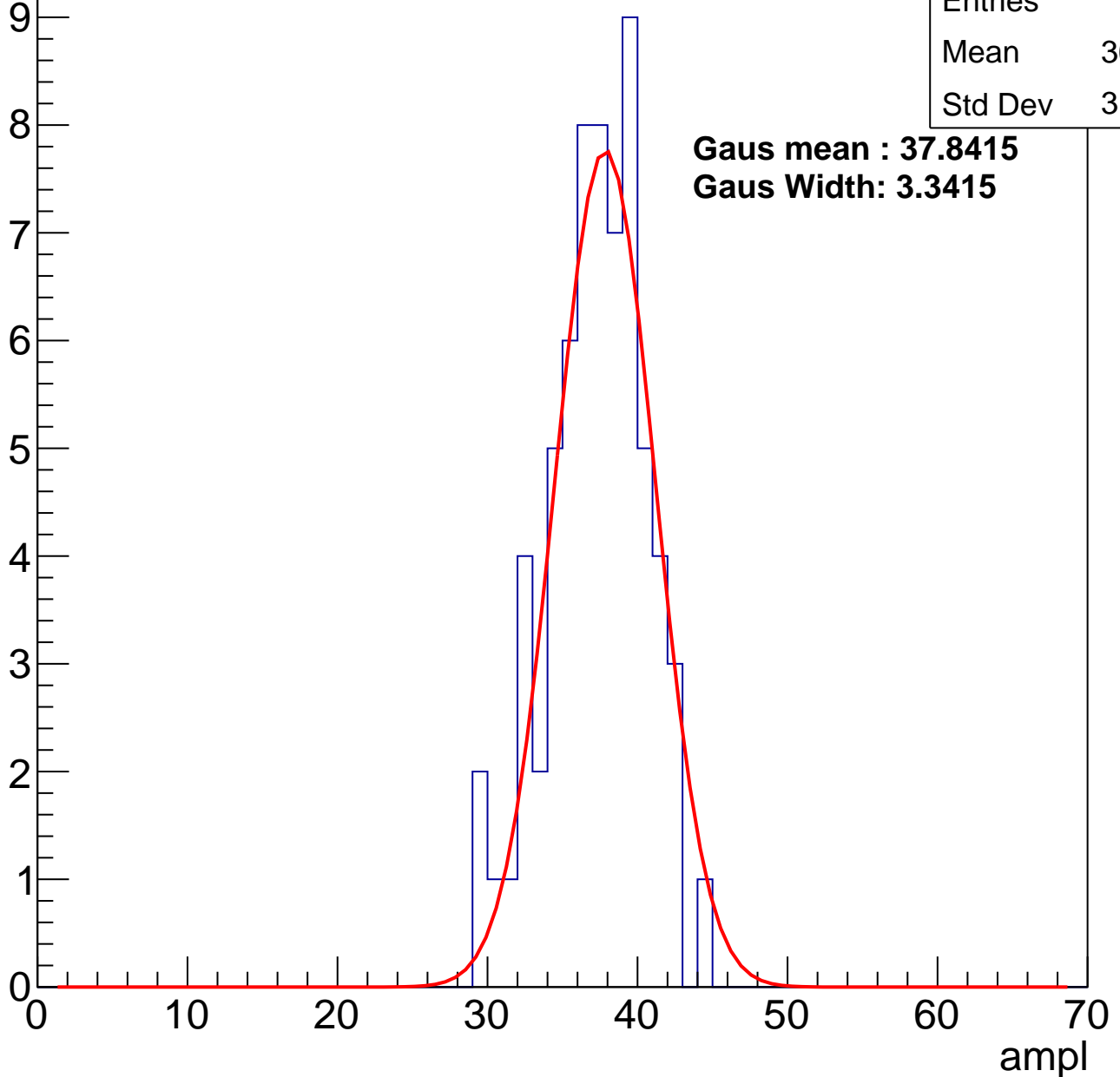
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.79
Std Dev	3.245

Gaus mean : 37.8415

Gaus Width: 3.3415



B1L100S, U11-ch45, adc2

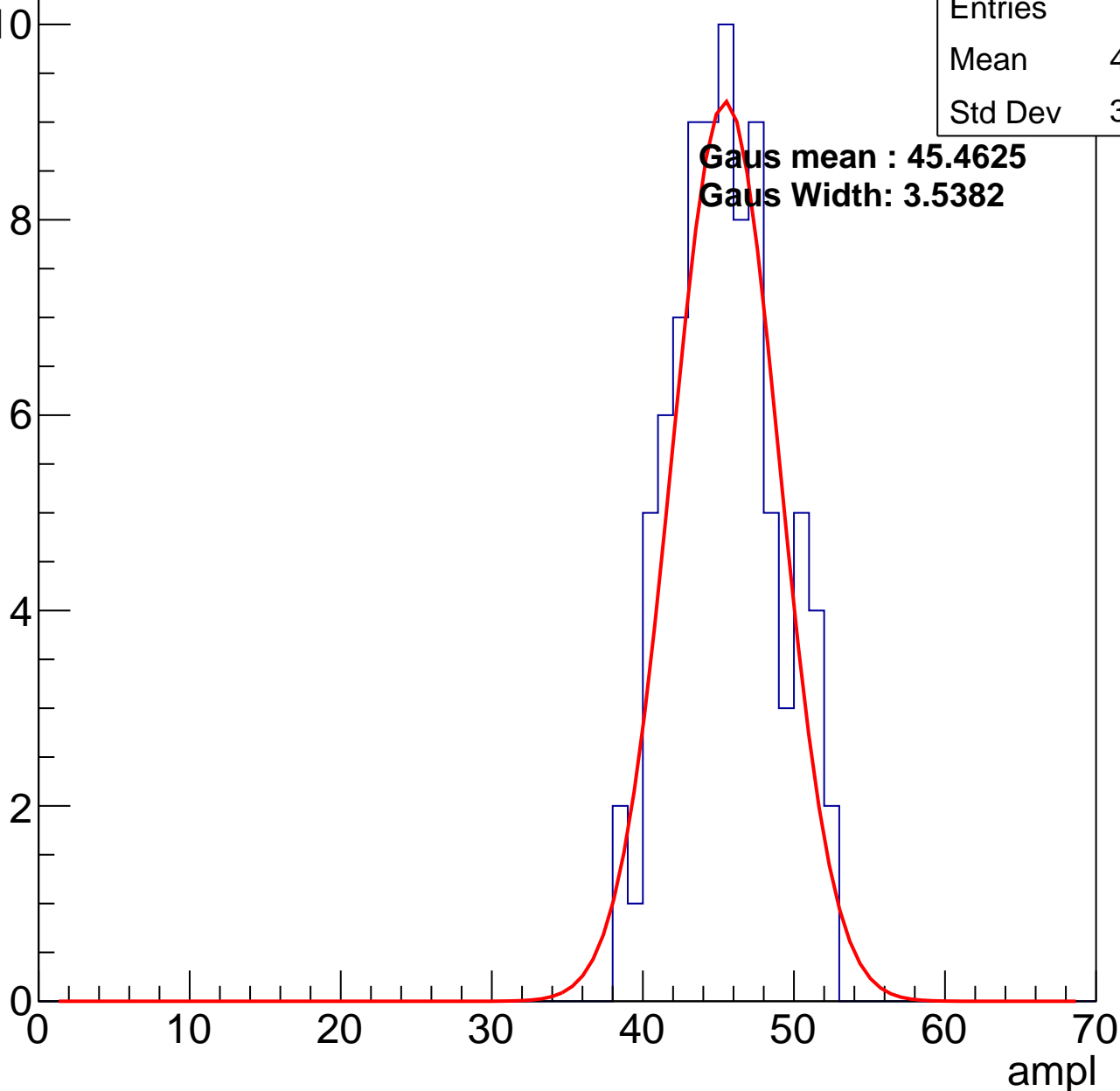
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	44.99
Std Dev	3.373

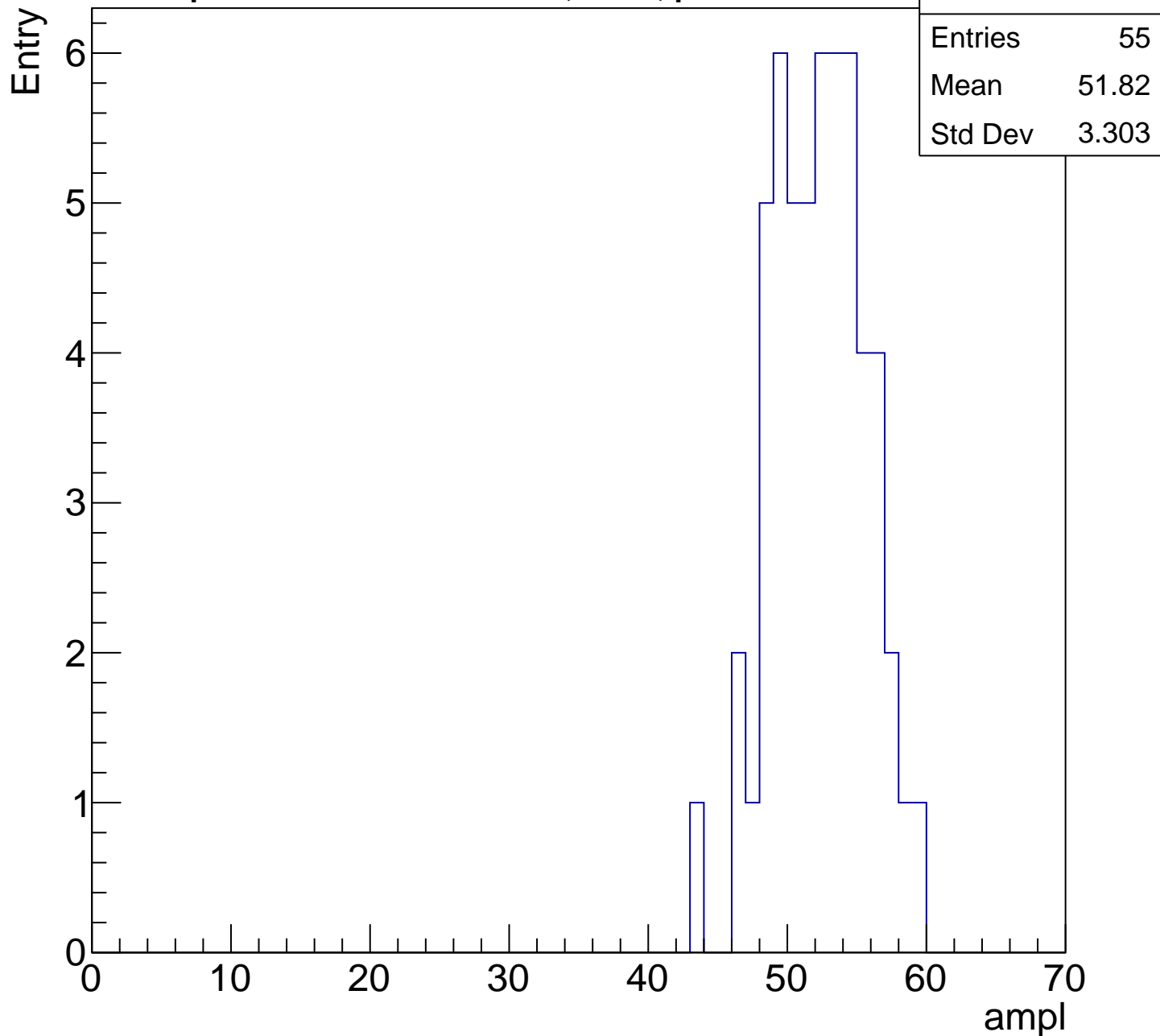
Gaus mean : 45.4625

Gaus Width: 3.5382



B1L100S, U11-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

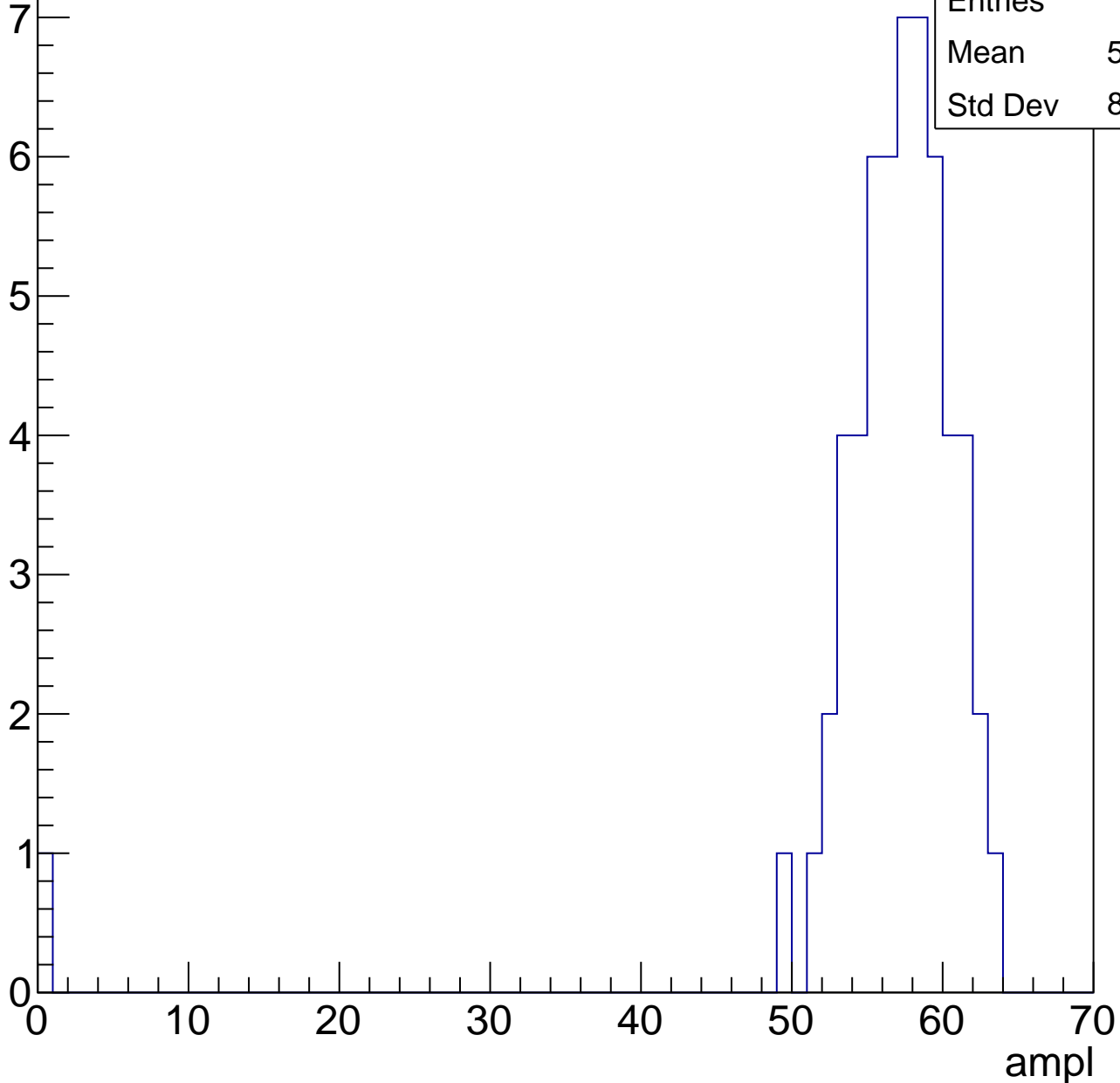


B1L100S, U11-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

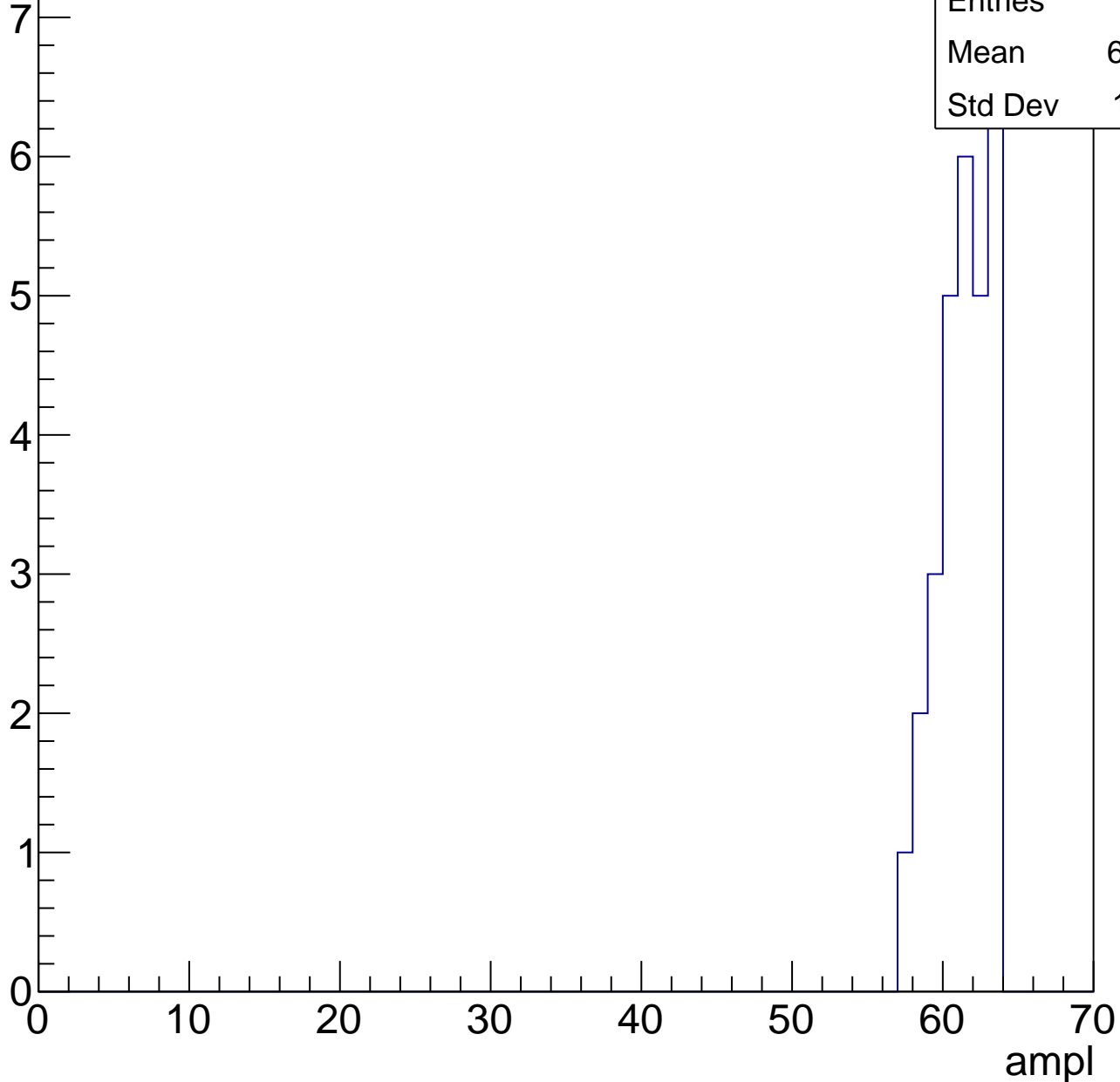
Entries	56
Mean	55.86
Std Dev	8.099



B1L100S, U11-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch45, adc6

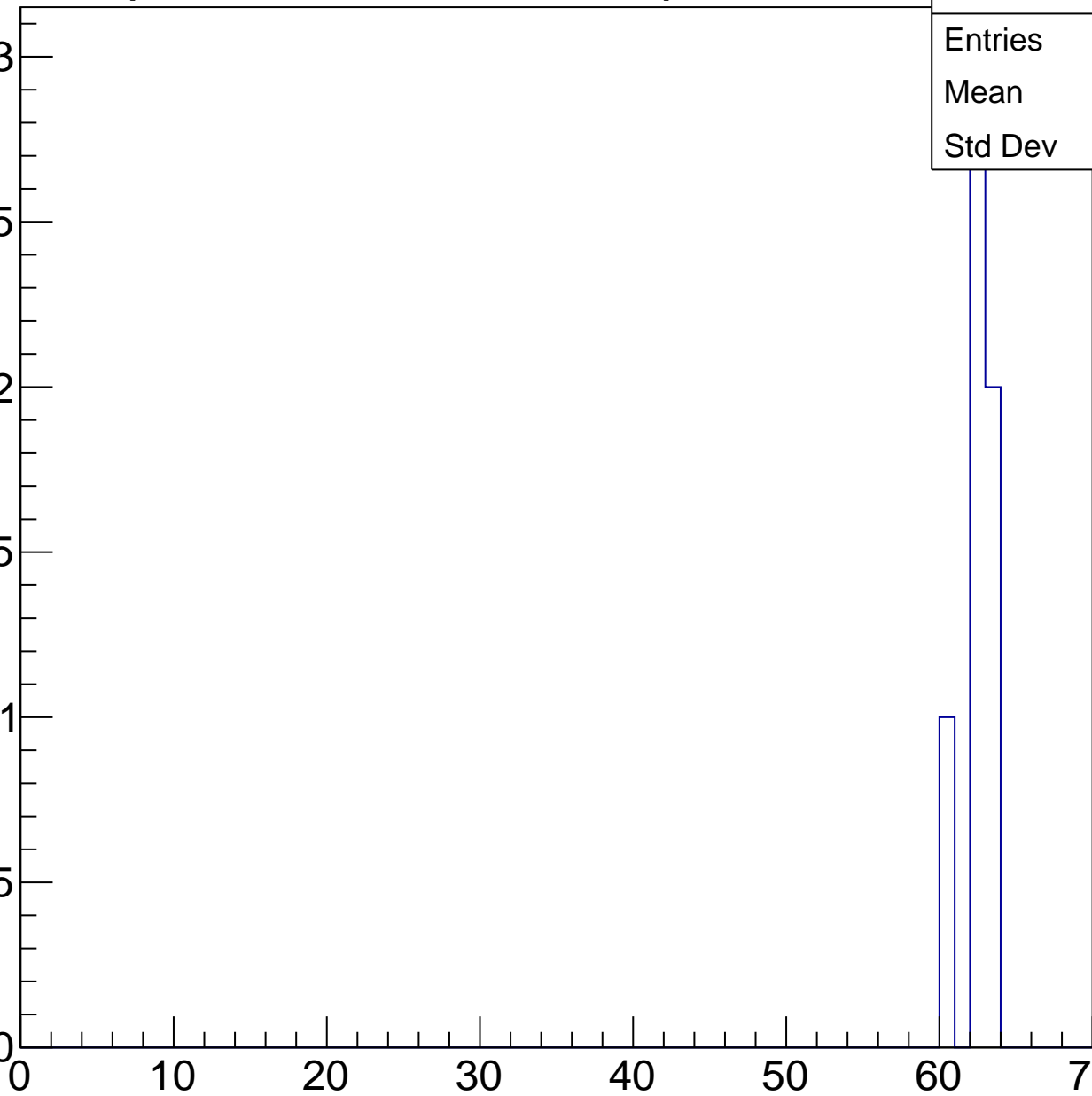
calib_packv5_042523_0143.root, FC#4, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	6
Mean	62
Std Dev	1

ampl



B1L100S, U11-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L100S, U11-ch46, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	31.17
Std Dev	3.279

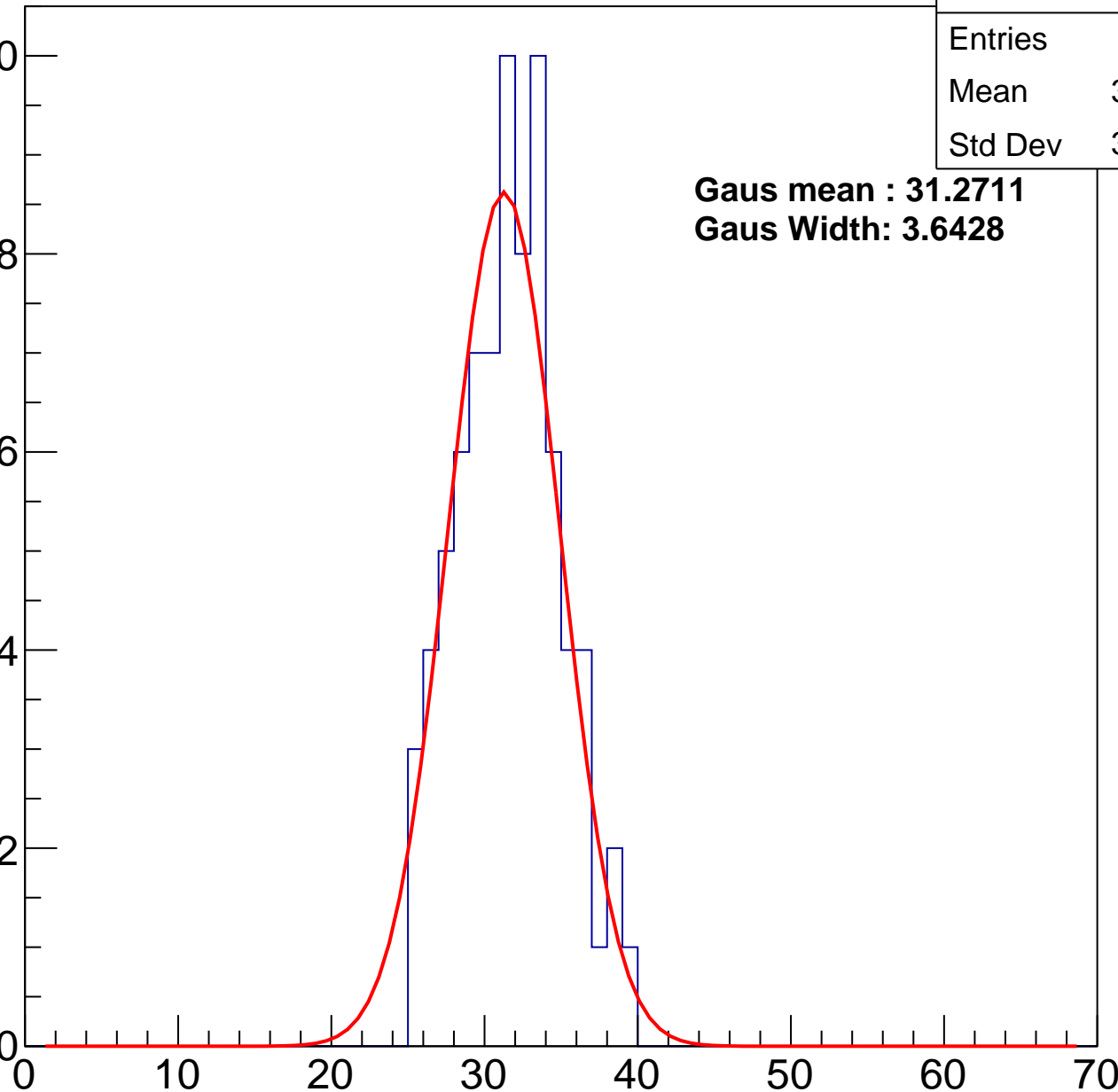
Gaus mean : 31.2711

Gaus Width: 3.6428

Entry

10
8
6
4
2
0

ampl



B1L100S, U11-ch46, adc1

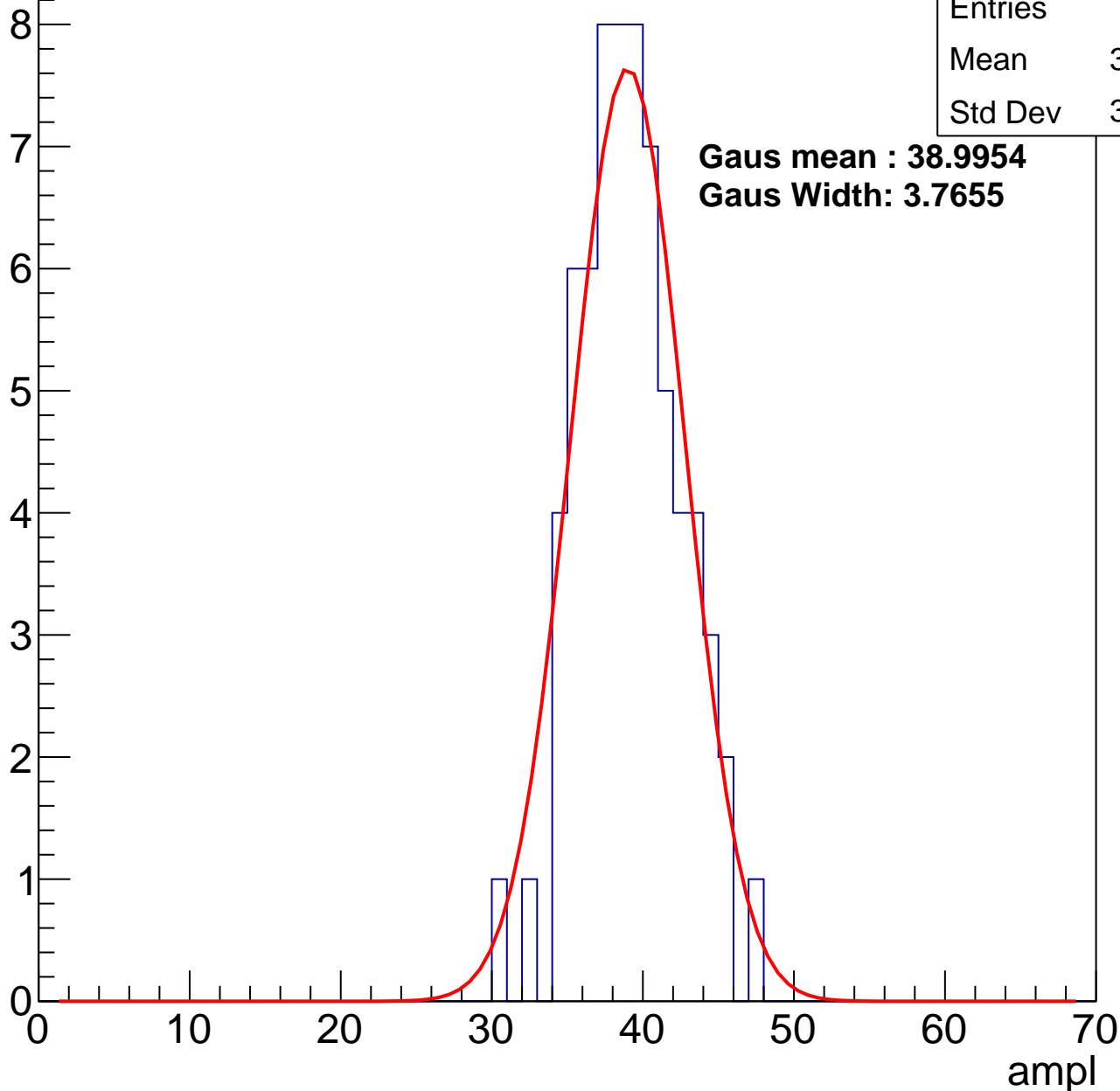
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	38.68
Std Dev	3.323

Gaus mean : 38.9954

Gaus Width: 3.7655



B1L100S, U11-ch46, adc2

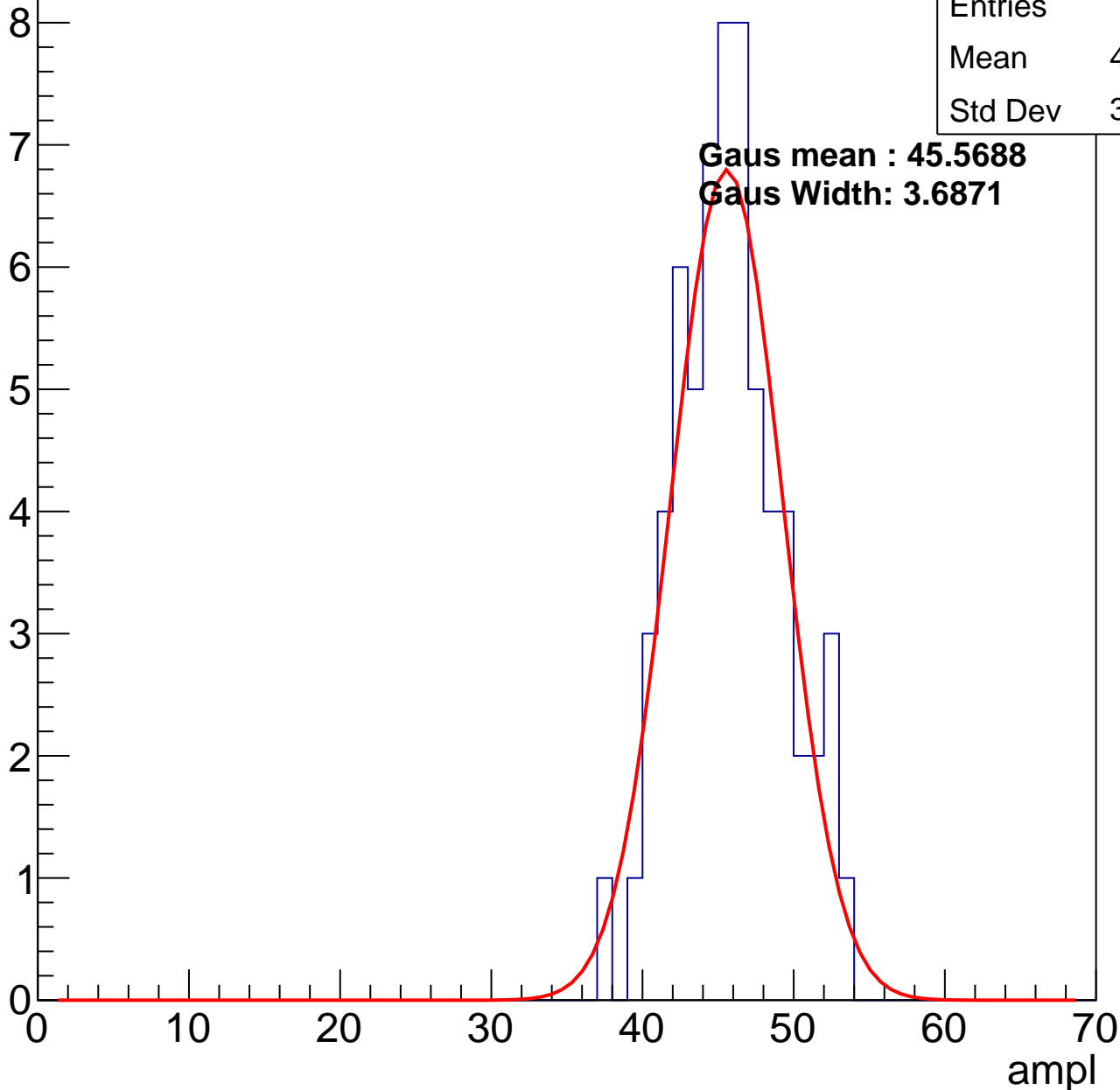
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	45.27
Std Dev	3.483

Gaus mean : 45.5688

Gaus Width: 3.6871

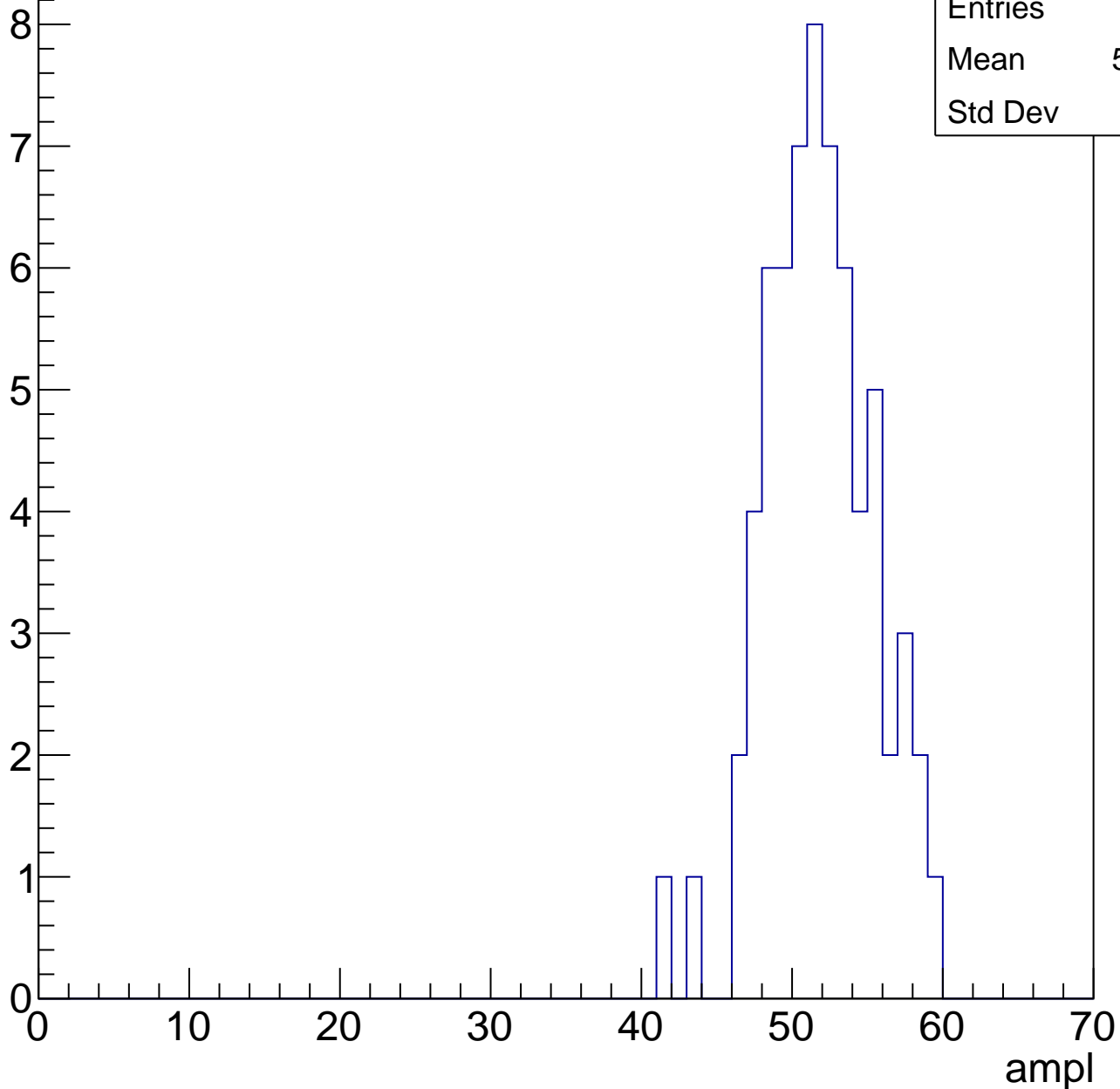


B1L100S, U11-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	51.31
Std Dev	3.56

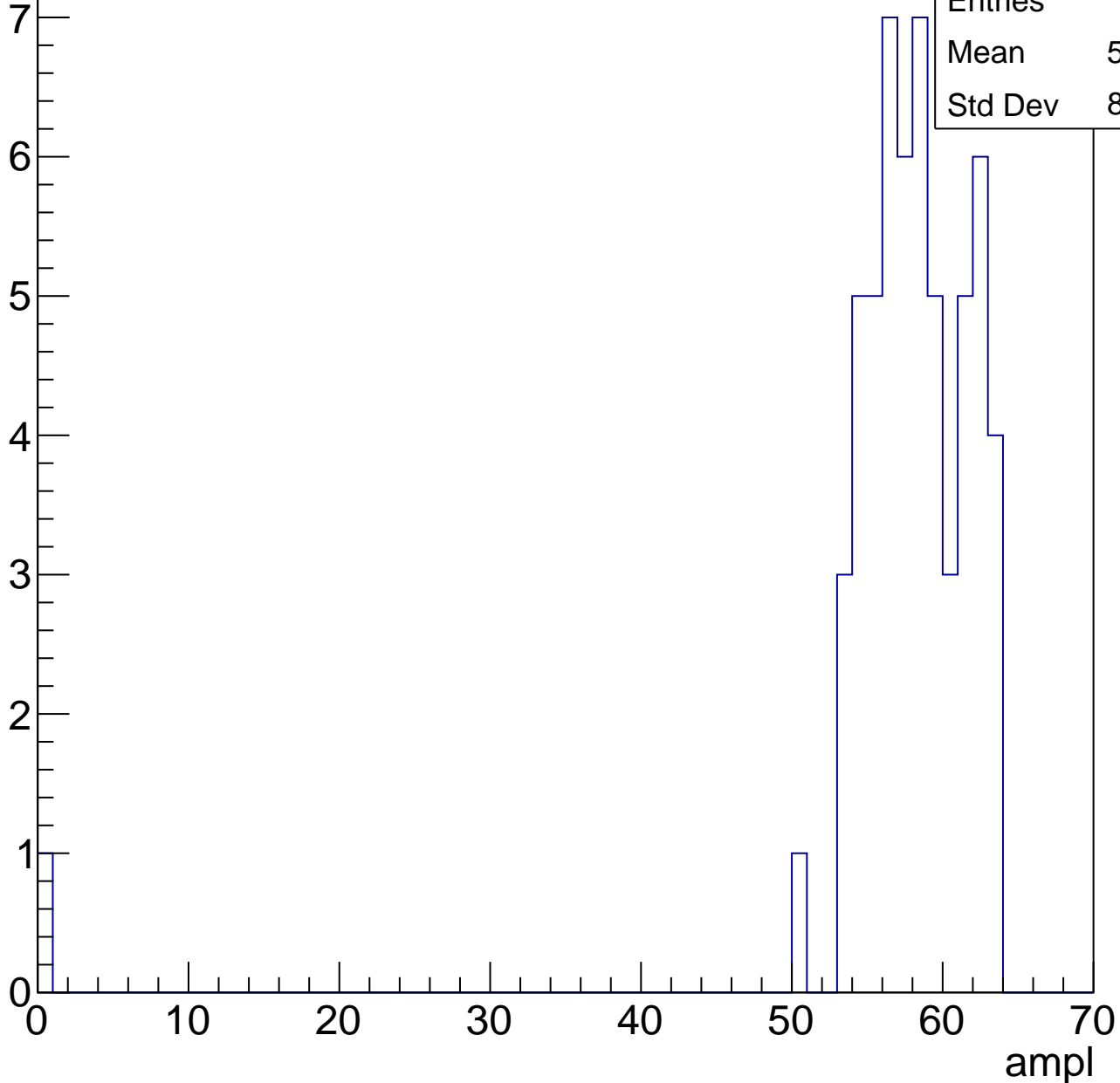


B1L100S, U11-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

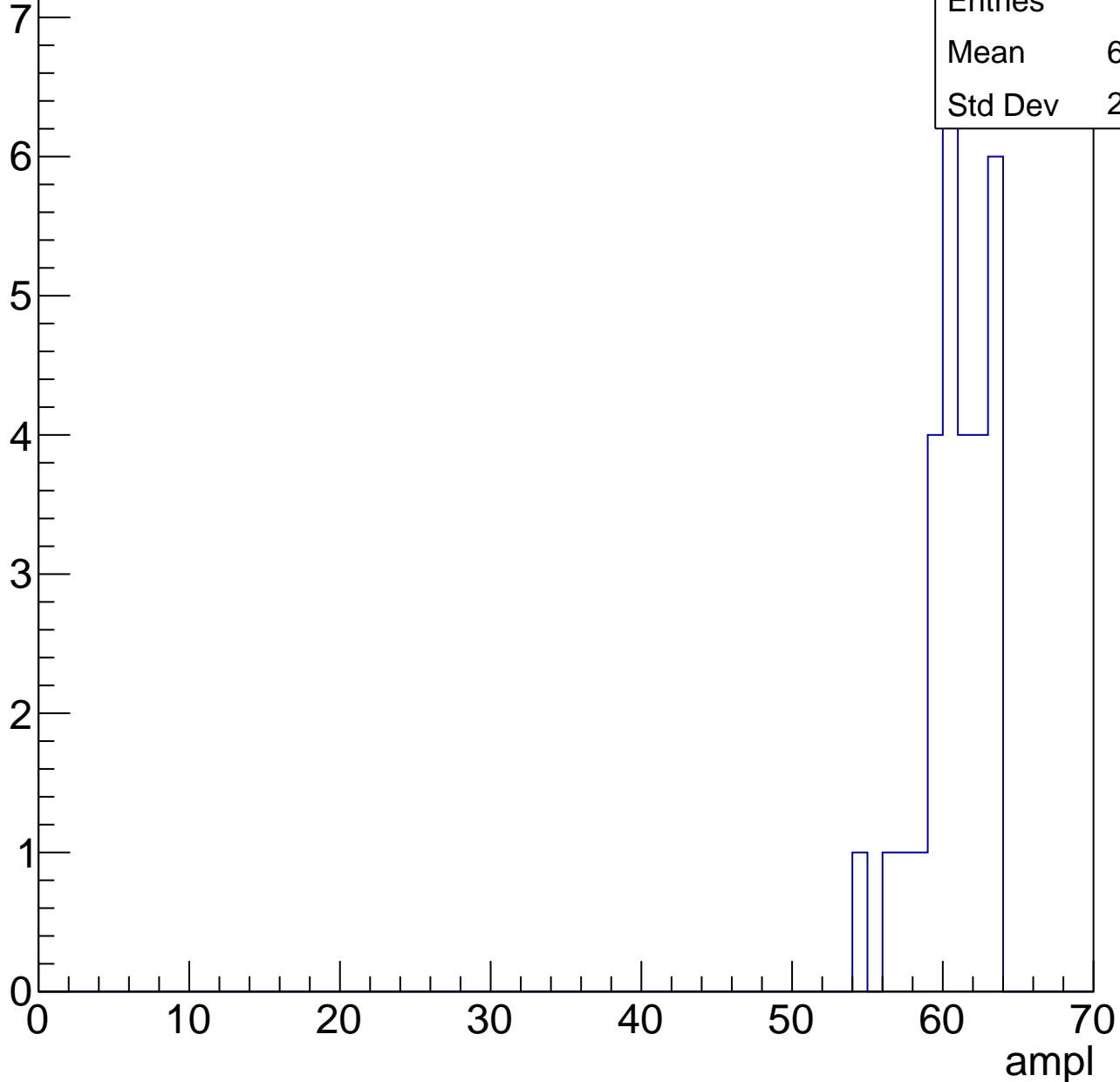
Entries	58
Mean	56.86
Std Dev	8.142



B1L100S, U11-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch47, adc0

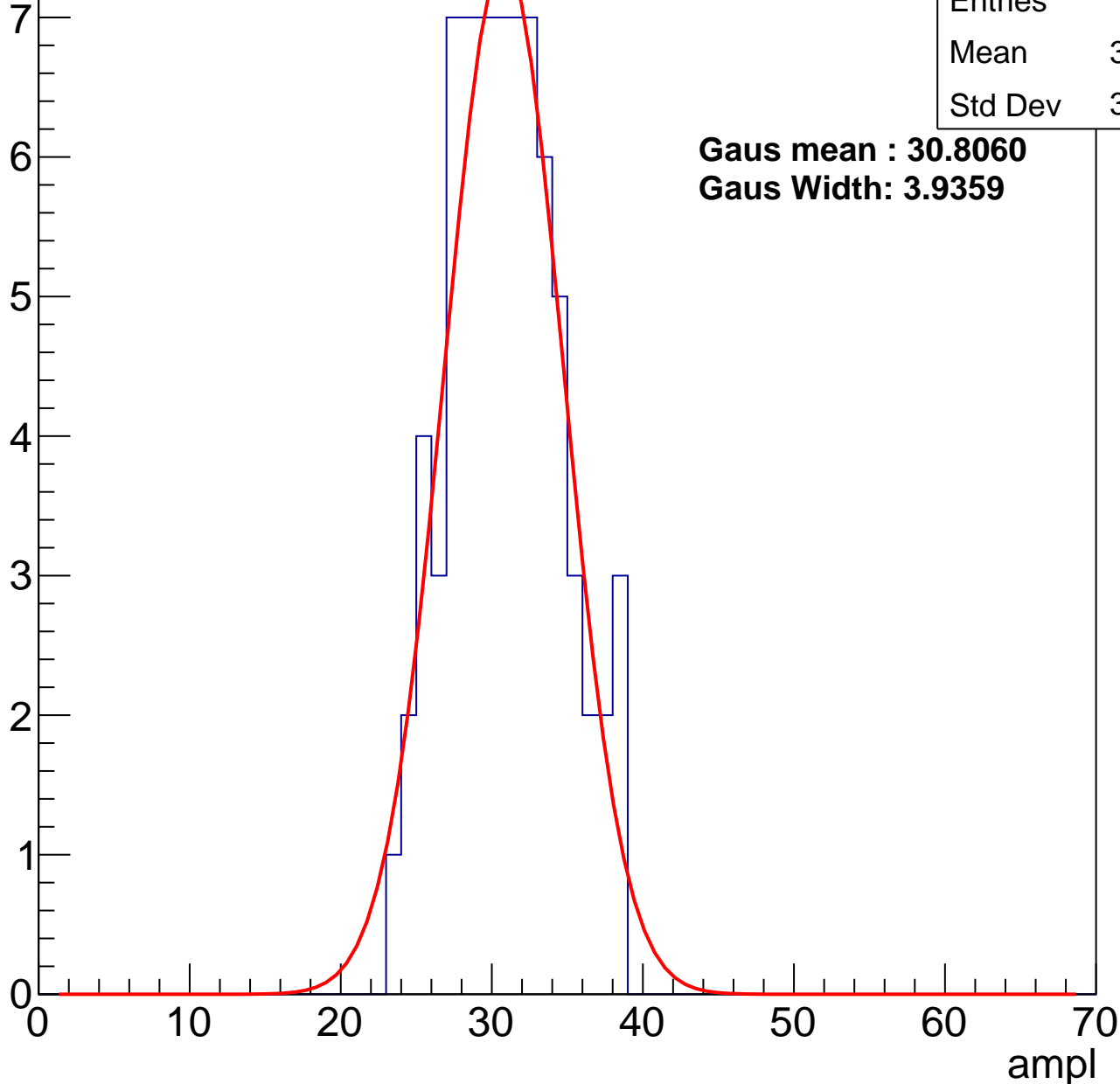
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.42
Std Dev	3.626

Gaus mean : 30.8060

Gaus Width: 3.9359



B1L100S, U11-ch47, adc1

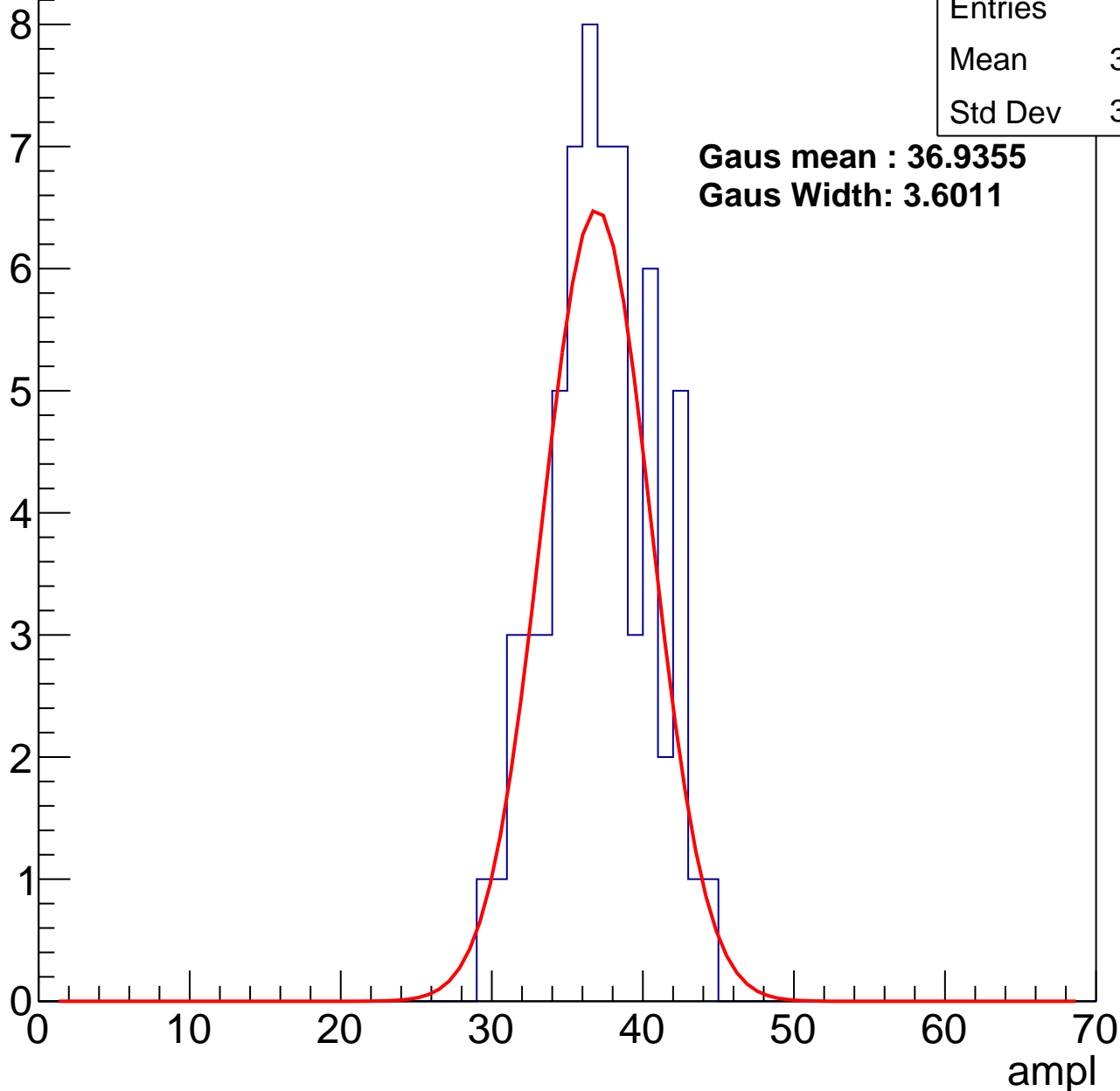
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	36.68
Std Dev	3.413

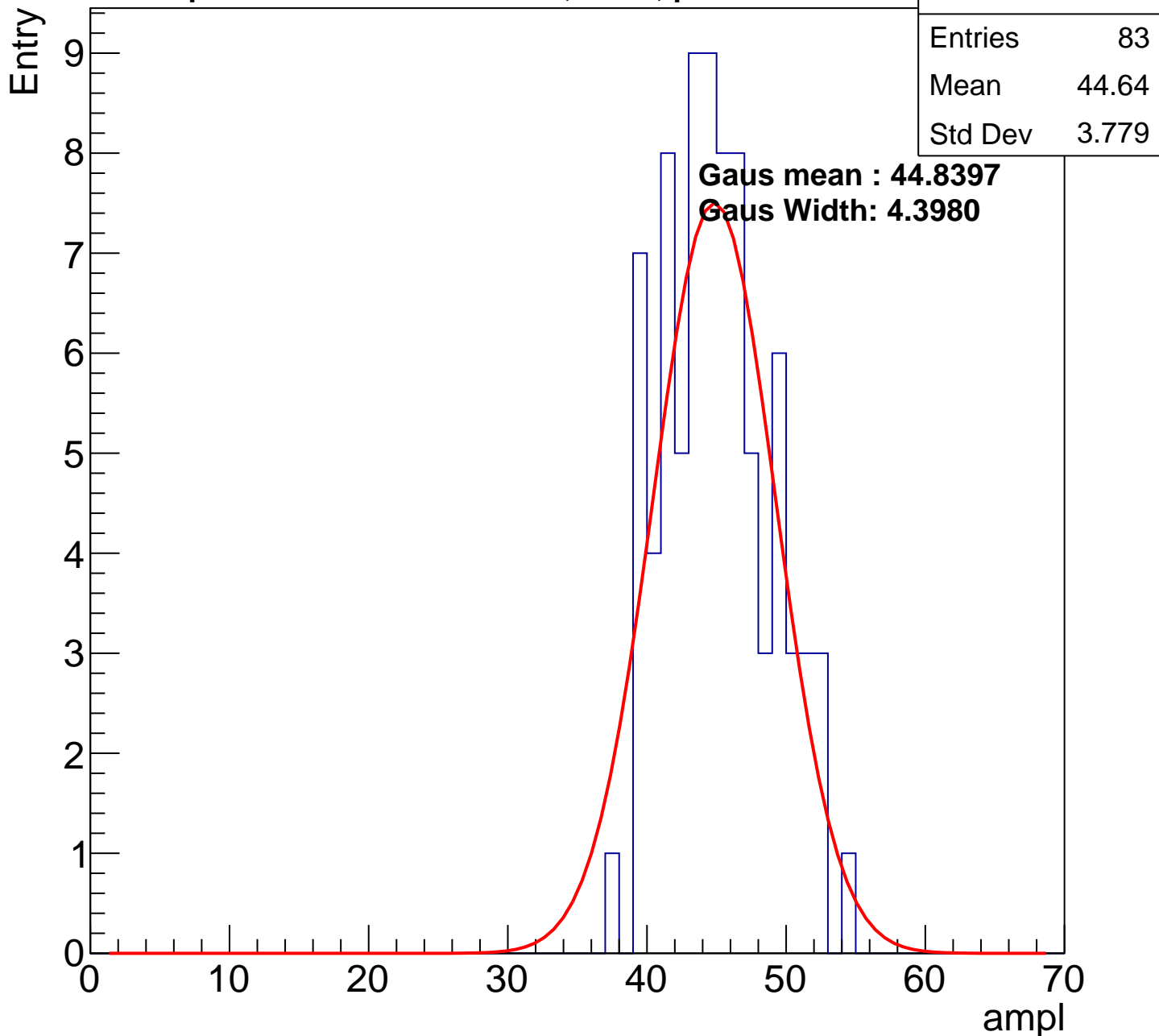
Gaus mean : 36.9355

Gaus Width: 3.6011



B1L100S, U11-ch47, adc2

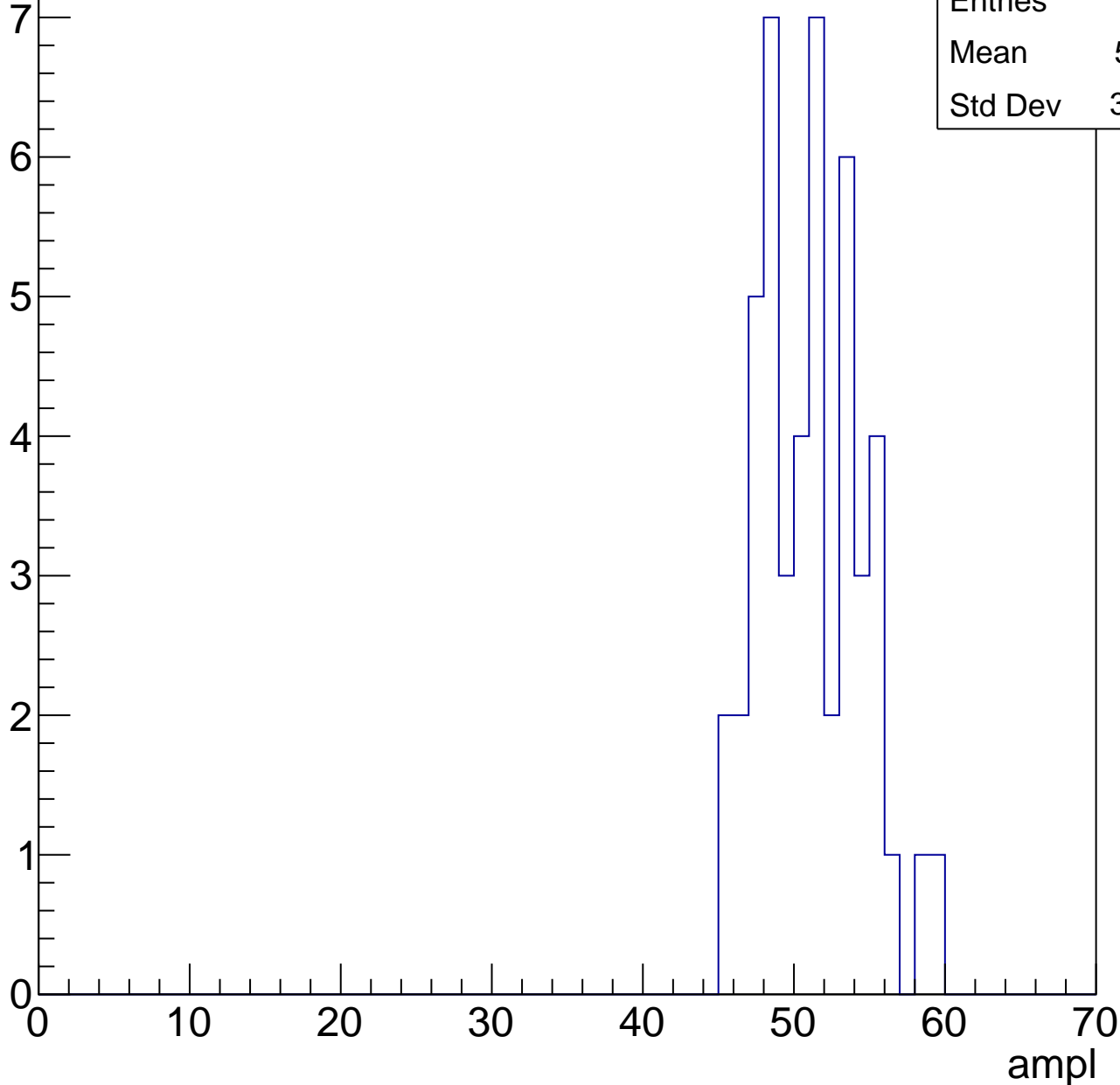
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



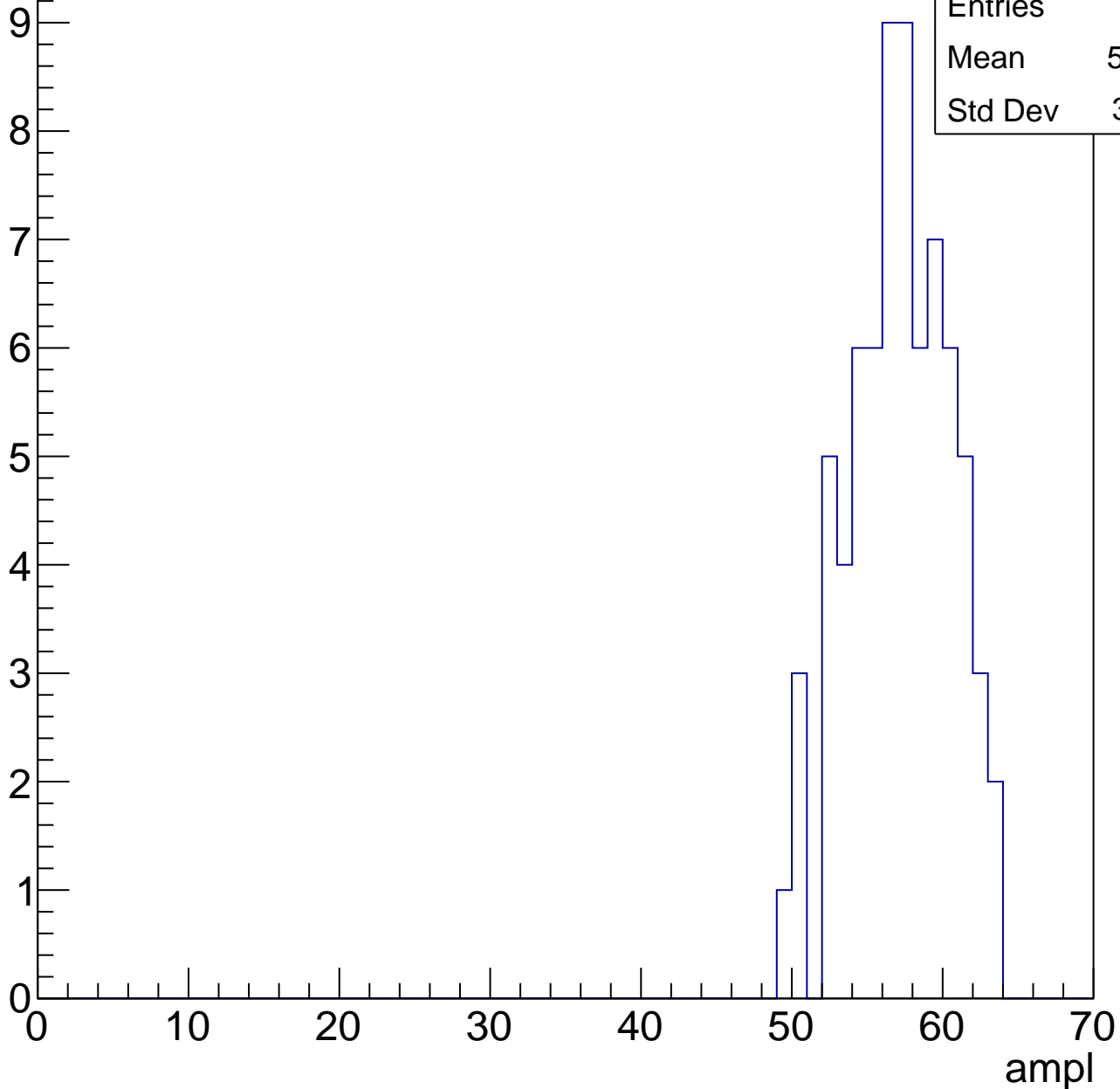
Entries	48
Mean	50.71
Std Dev	3.329

B1L100S, U11-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

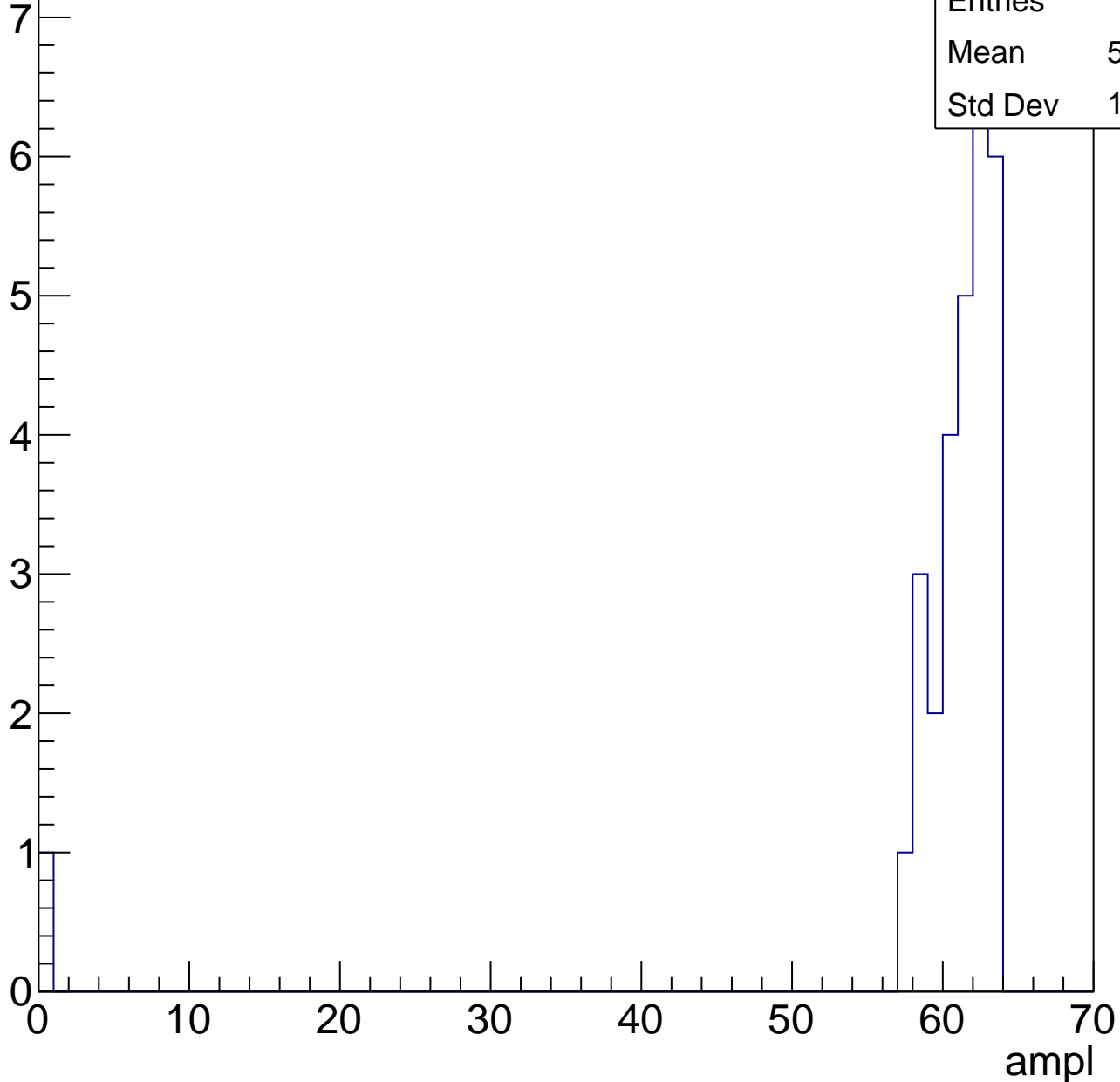
Entries	72
Mean	56.67
Std Dev	3.321



B1L100S, U11-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L100S, U11-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch48, adc0

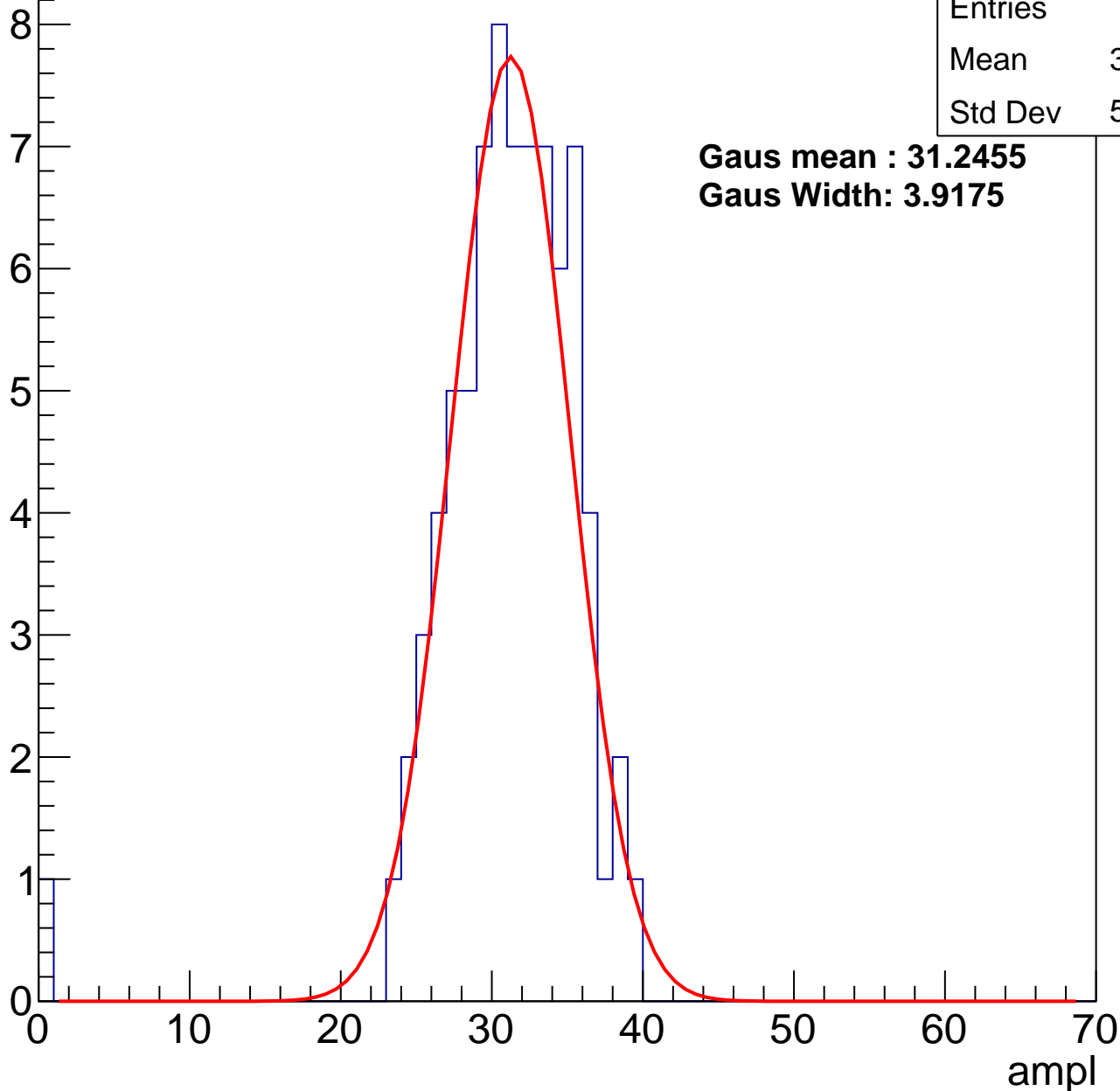
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	30.58
Std Dev	5.035

Gaus mean : 31.2455

Gaus Width: 3.9175



B1L100S, U11-ch48, adc1

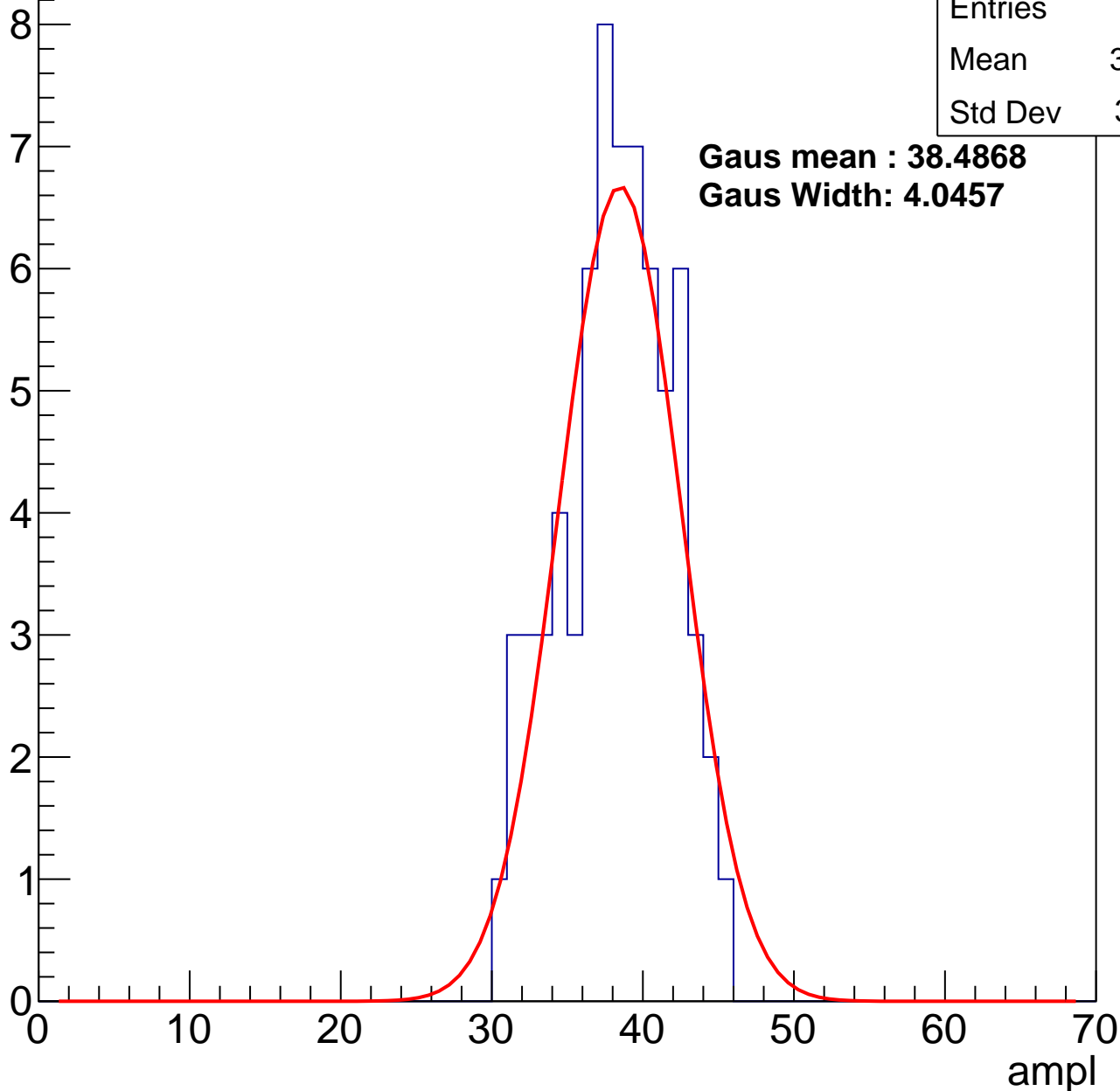
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.78
Std Dev	3.601

Gaus mean : 38.4868

Gaus Width: 4.0457



B1L100S, U11-ch48, adc2

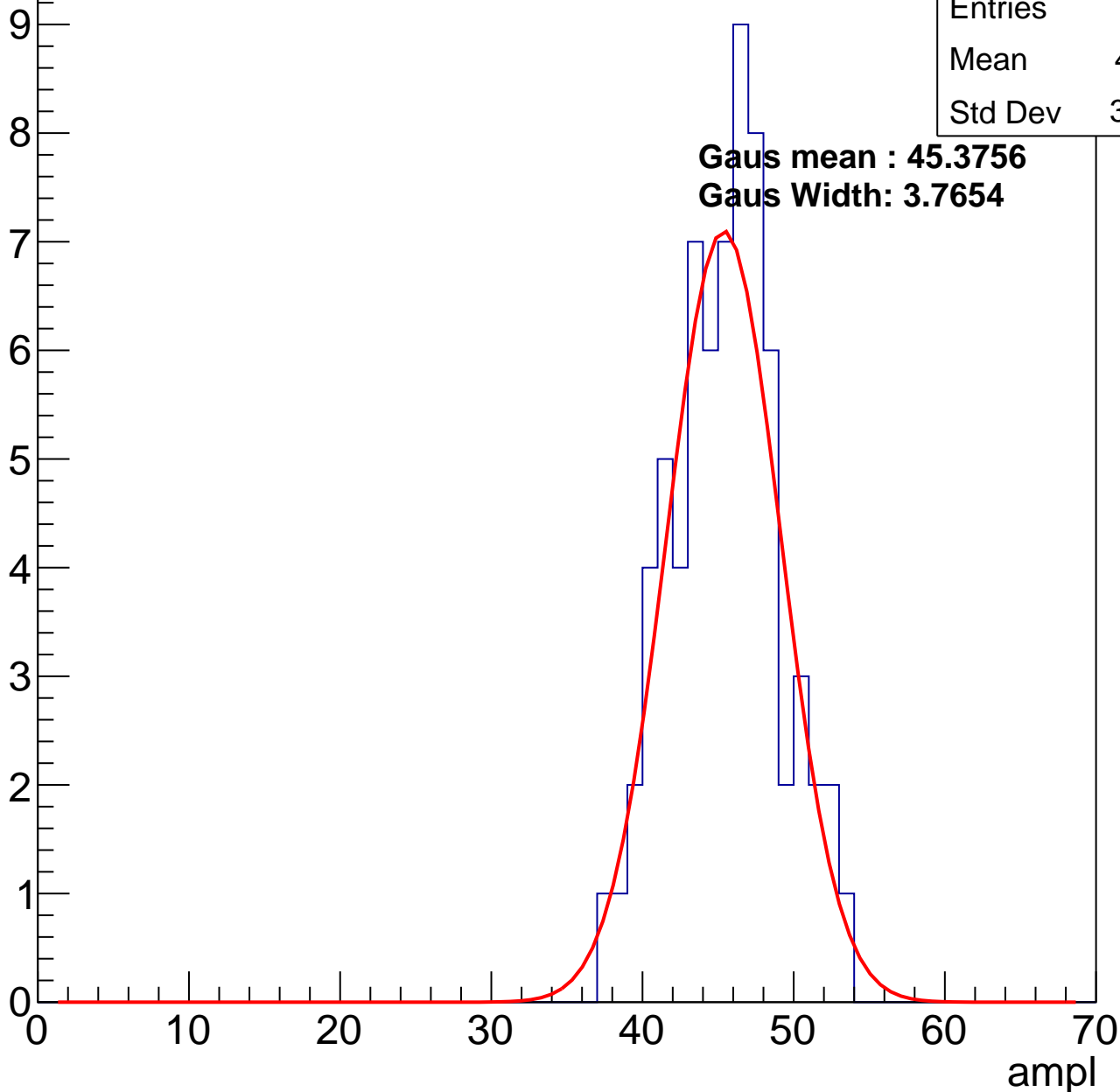
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	45.01
Std Dev	3.527

Gaus mean : 45.3756

Gaus Width: 3.7654

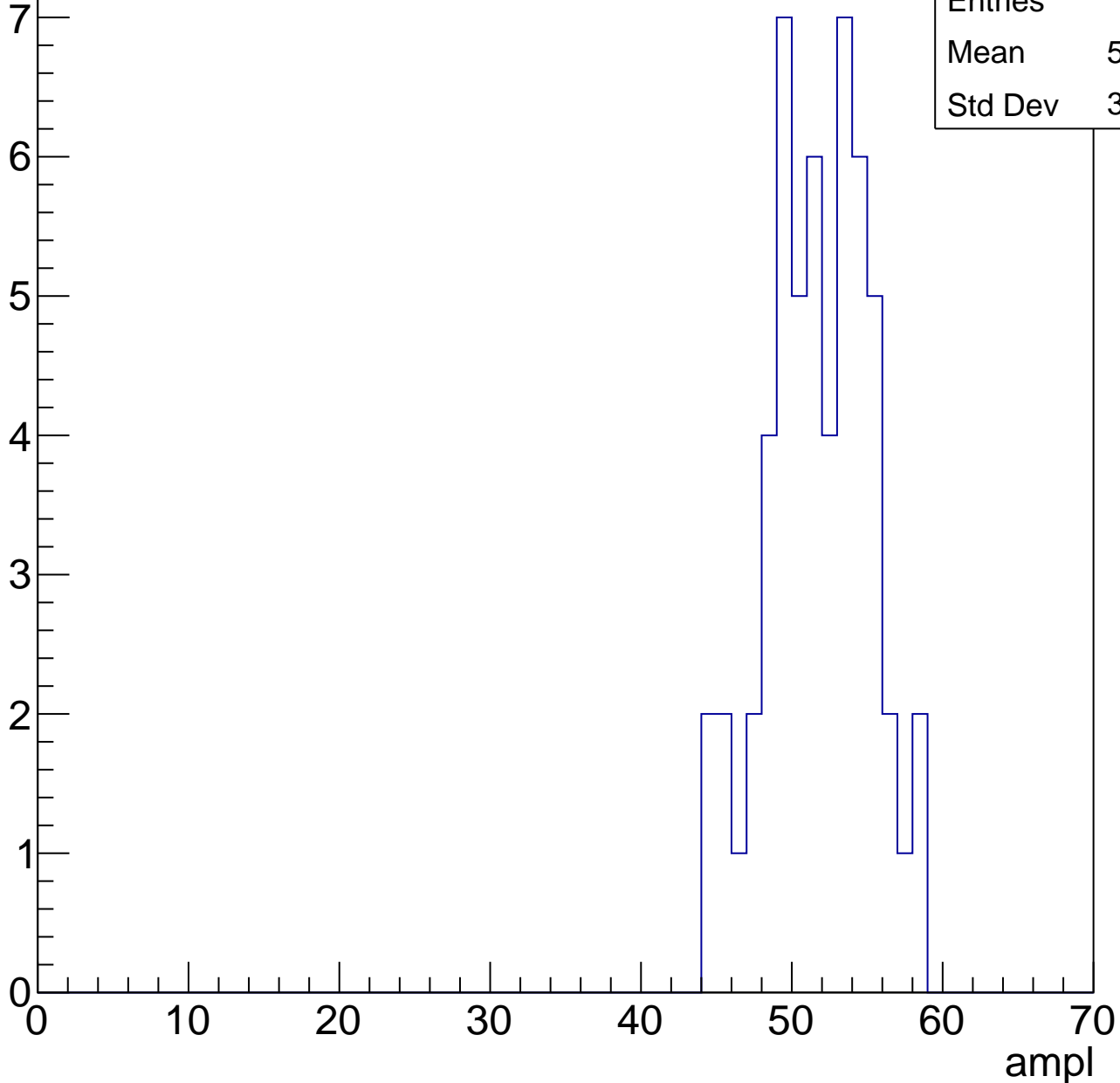


B1L100S, U11-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	51.29
Std Dev	3.384



B1L100S, U11-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	57.08
Std Dev	3.457

Entry

10

8

6

4

2

0

0

10

20

30

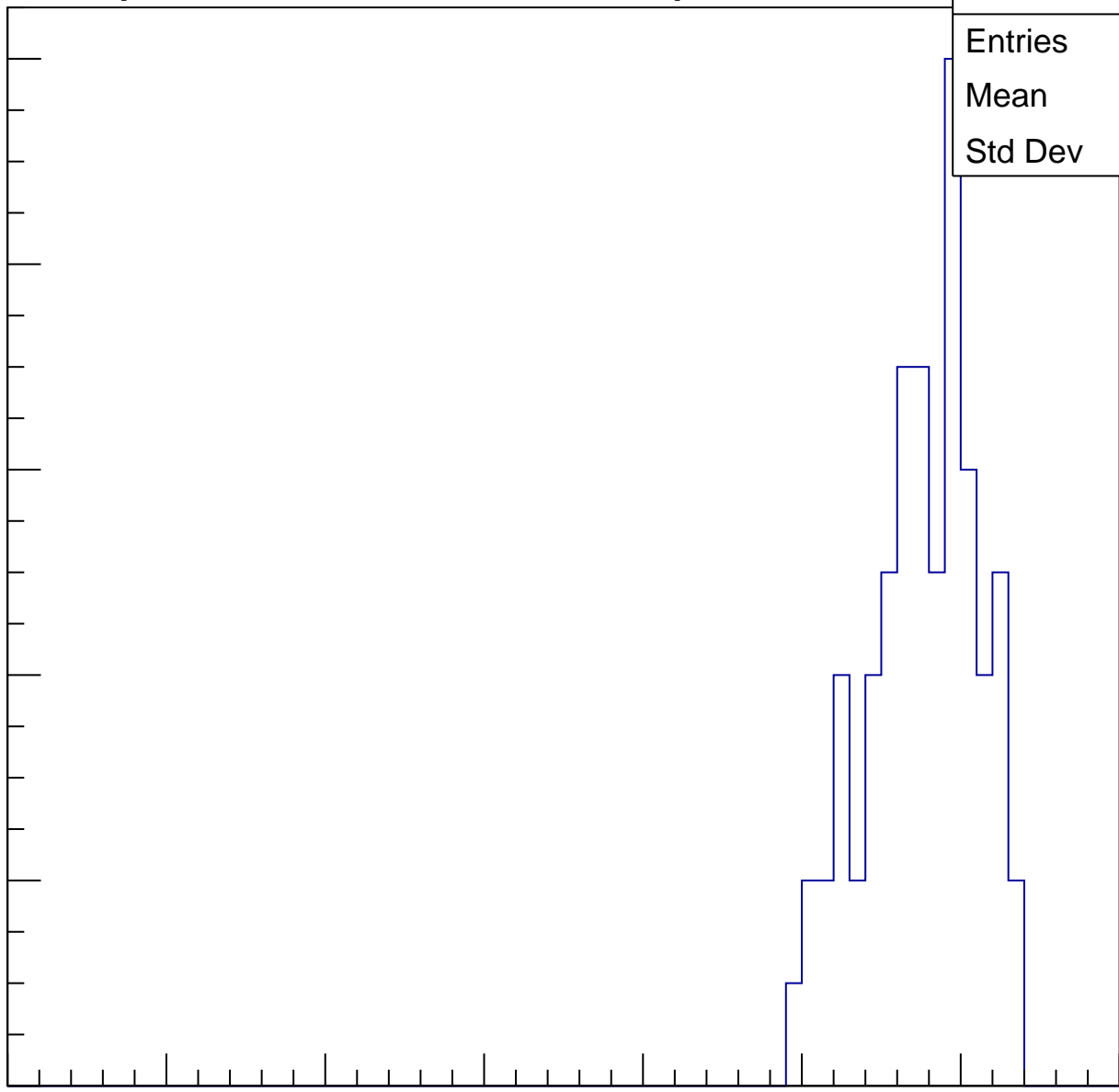
40

50

60

70

ampl

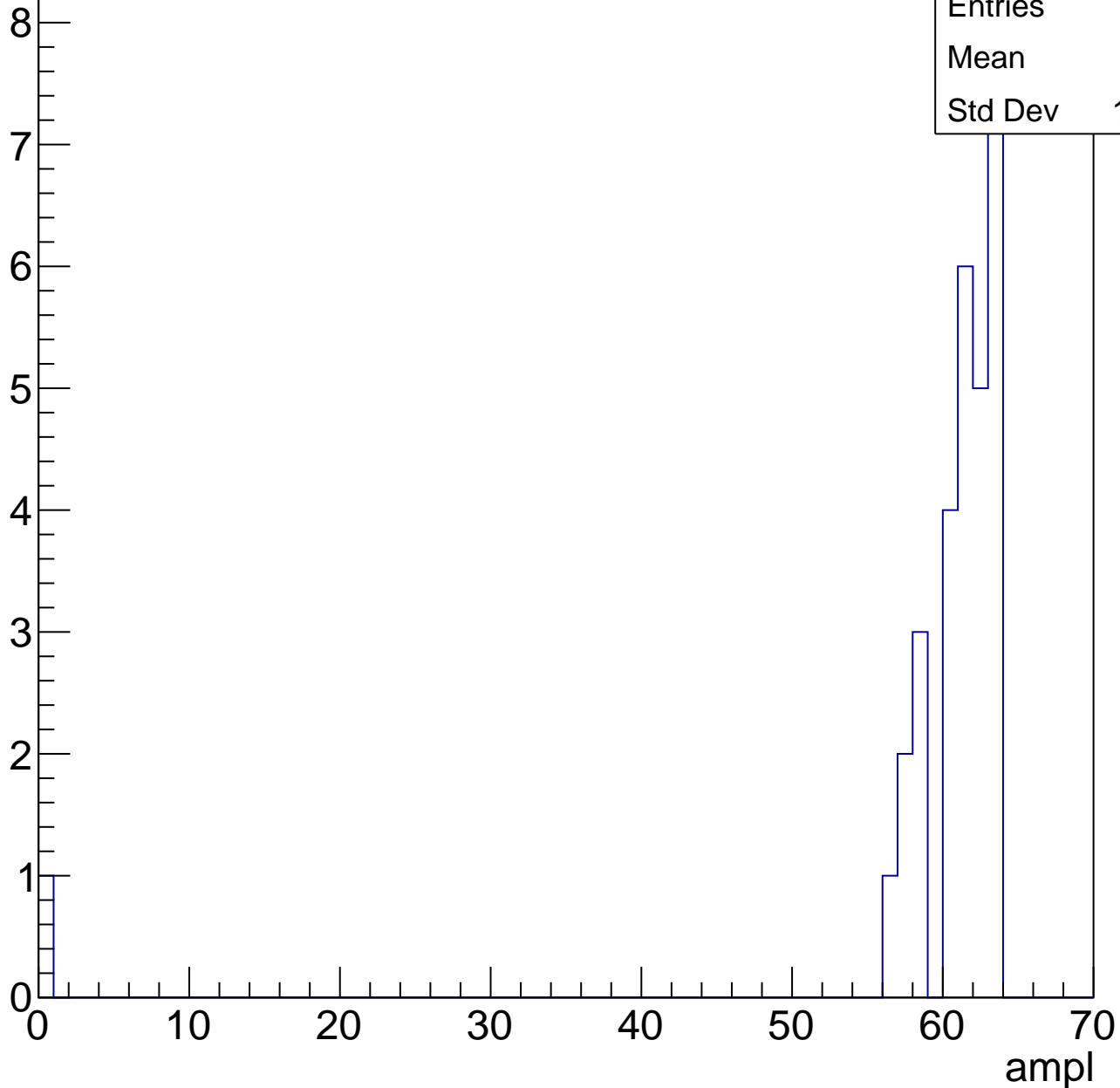


B1L100S, U11-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.8
Std Dev	11.11



B1L100S, U11-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L100S, U11-ch49, adc0

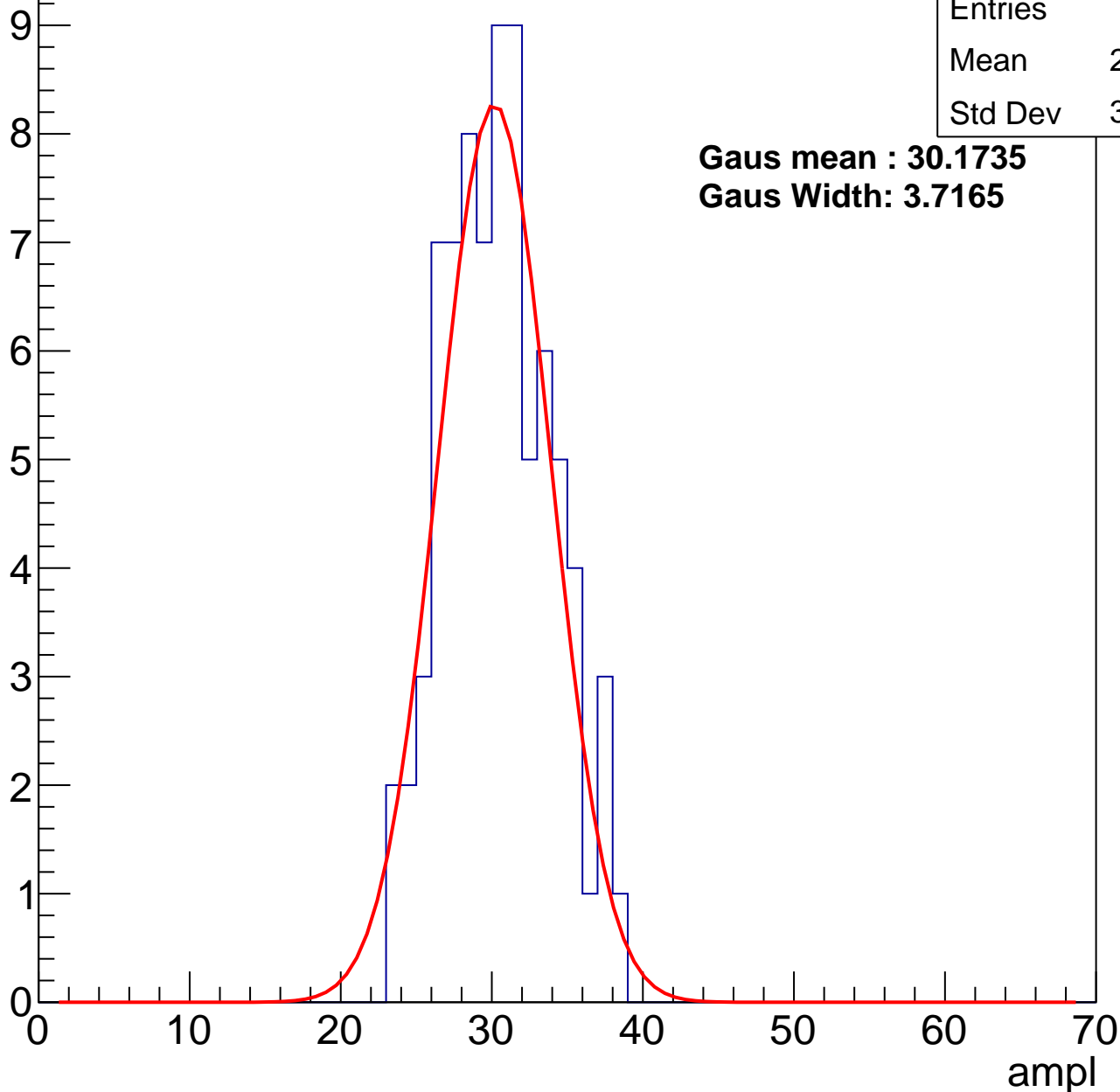
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	29.99
Std Dev	3.502

Gaus mean : 30.1735

Gaus Width: 3.7165



B1L100S, U11-ch49, adc1

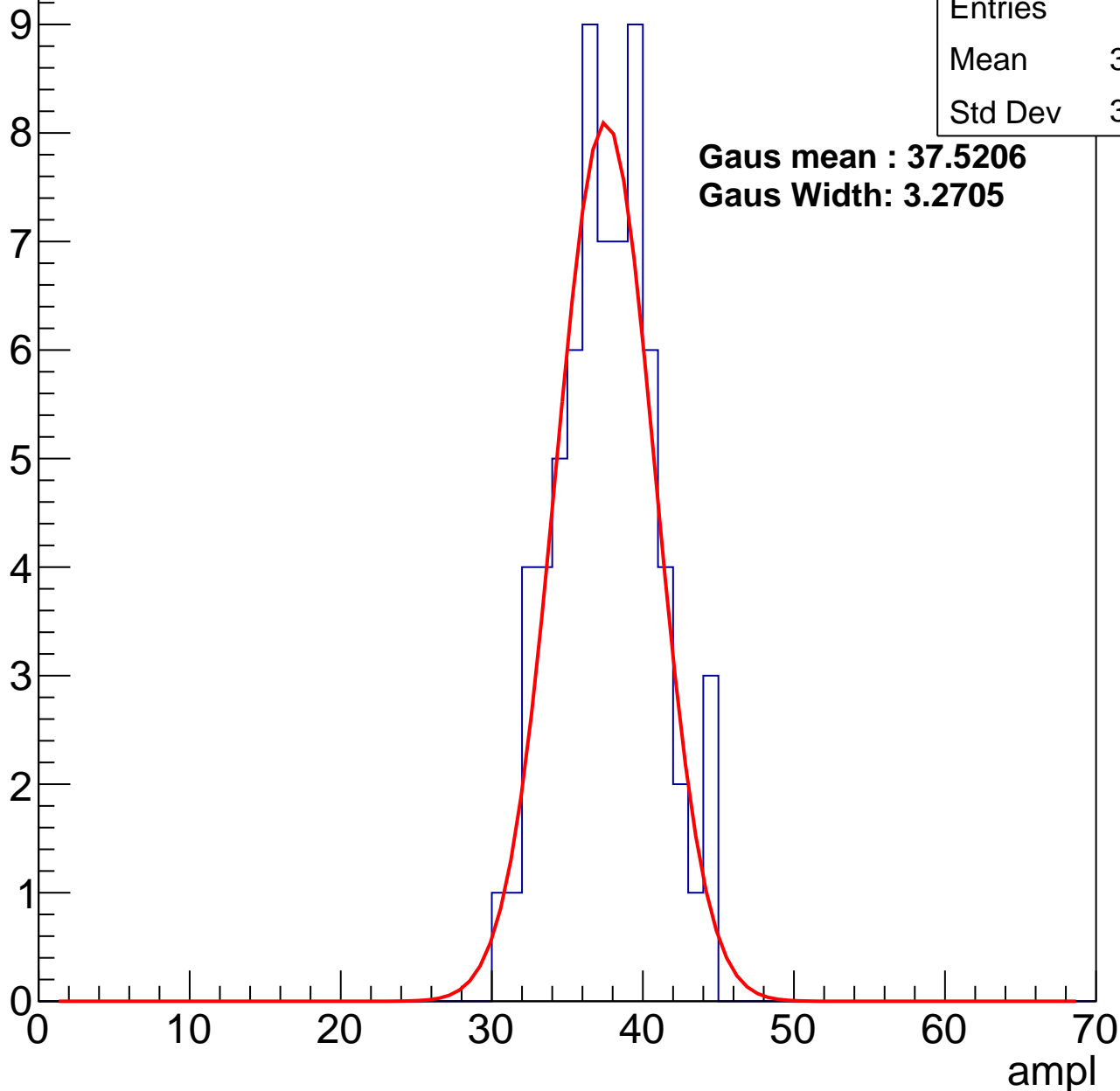
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	37.16
Std Dev	3.229

Gaus mean : 37.5206

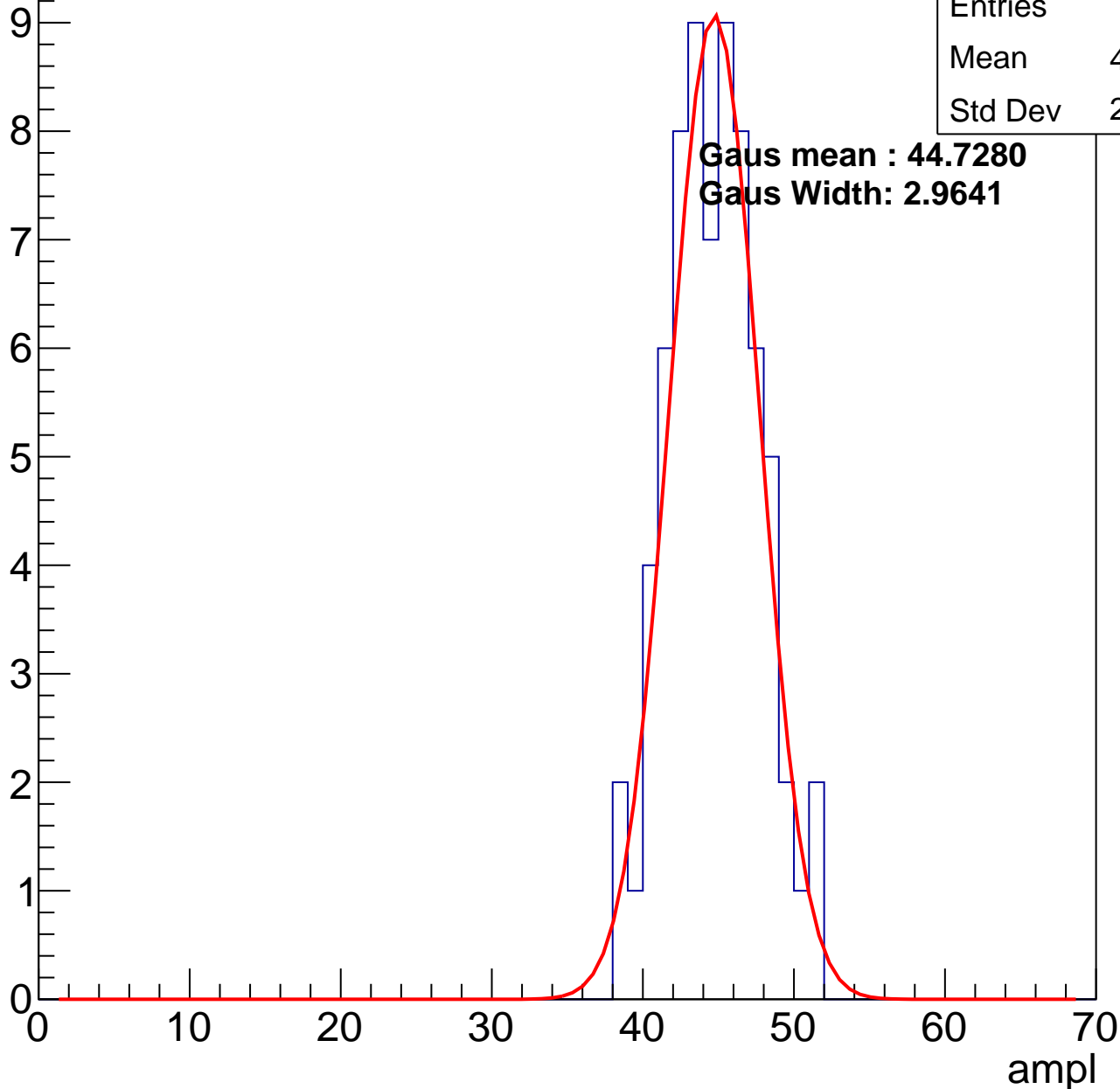
Gaus Width: 3.2705



B1L100S, U11-ch49, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

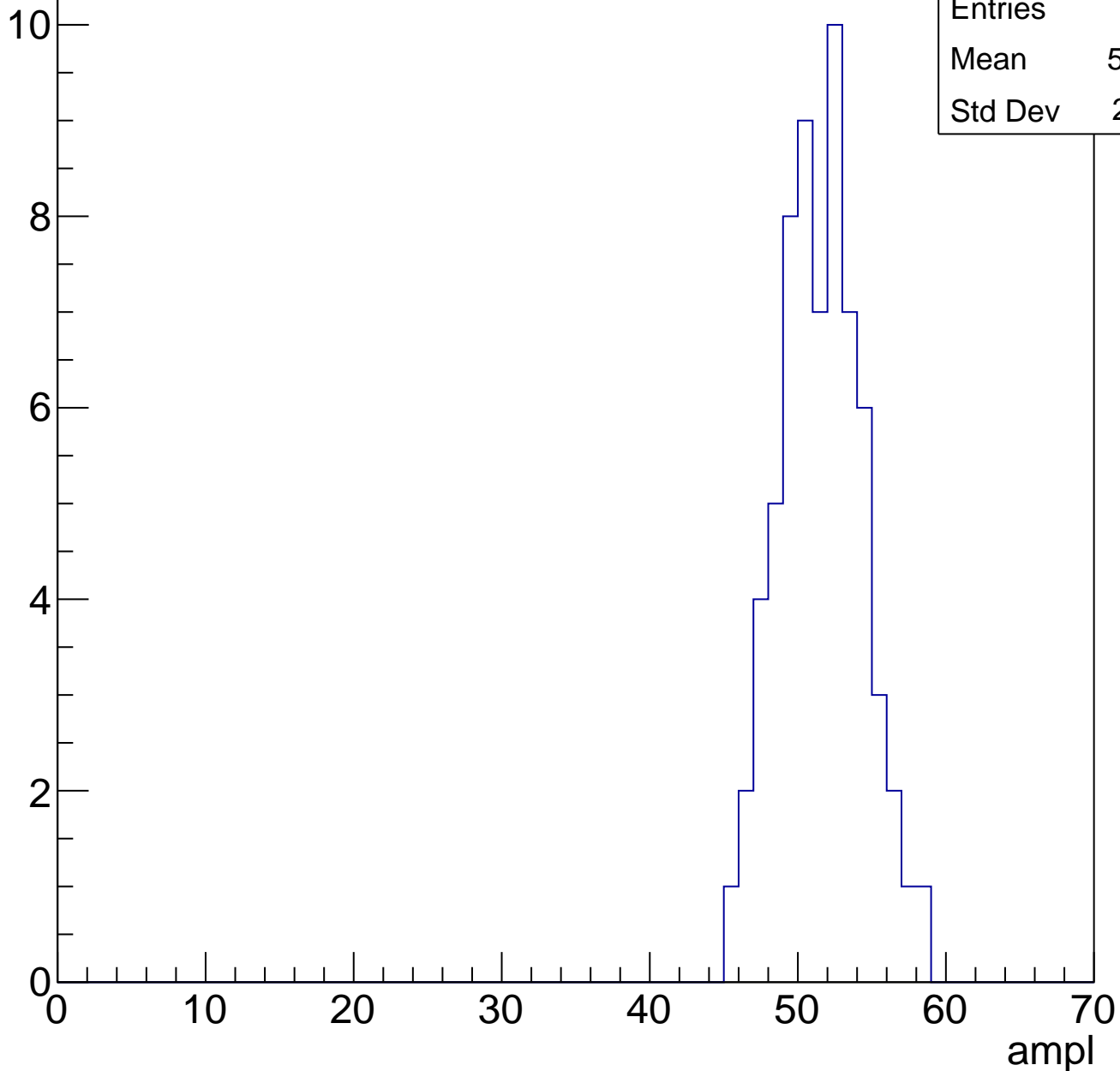


B1L100S, U11-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

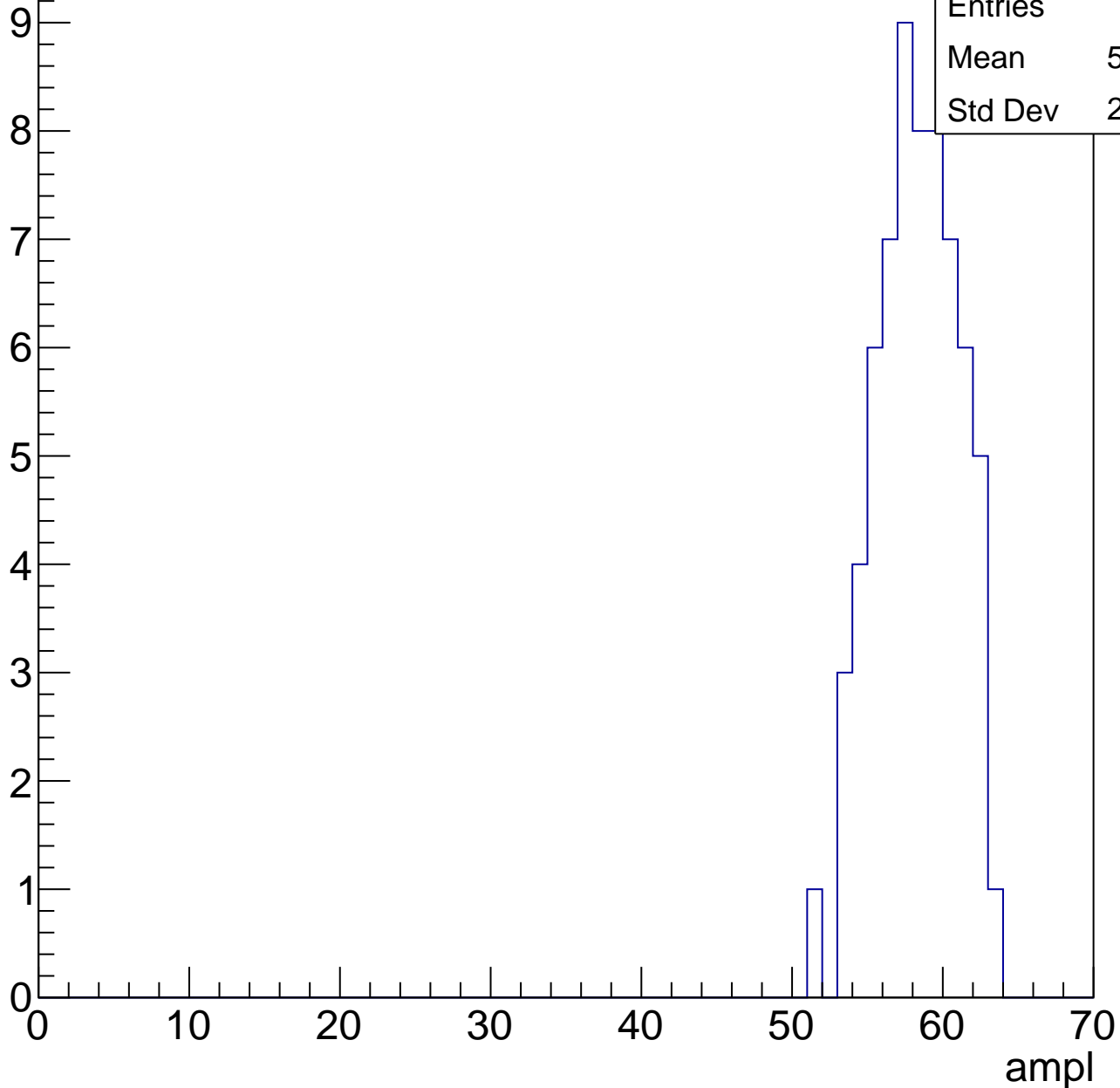
Entries	66
Mean	51.08
Std Dev	2.781



B1L100S, U11-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



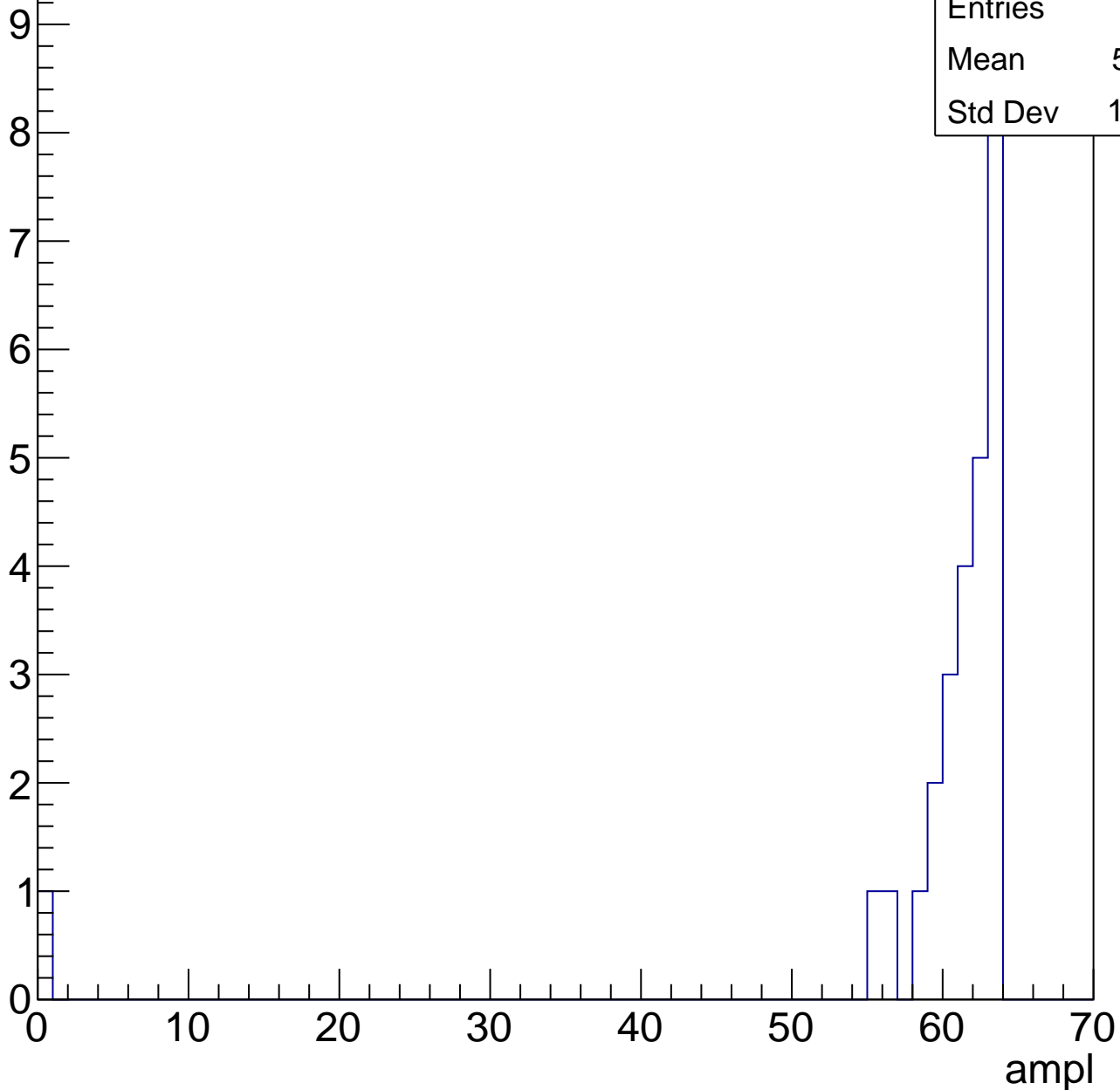
Entries	65
Mean	57.78
Std Dev	2.692

B1L100S, U11-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.81
Std Dev	11.73



B1L100S, U11-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch50, adc0

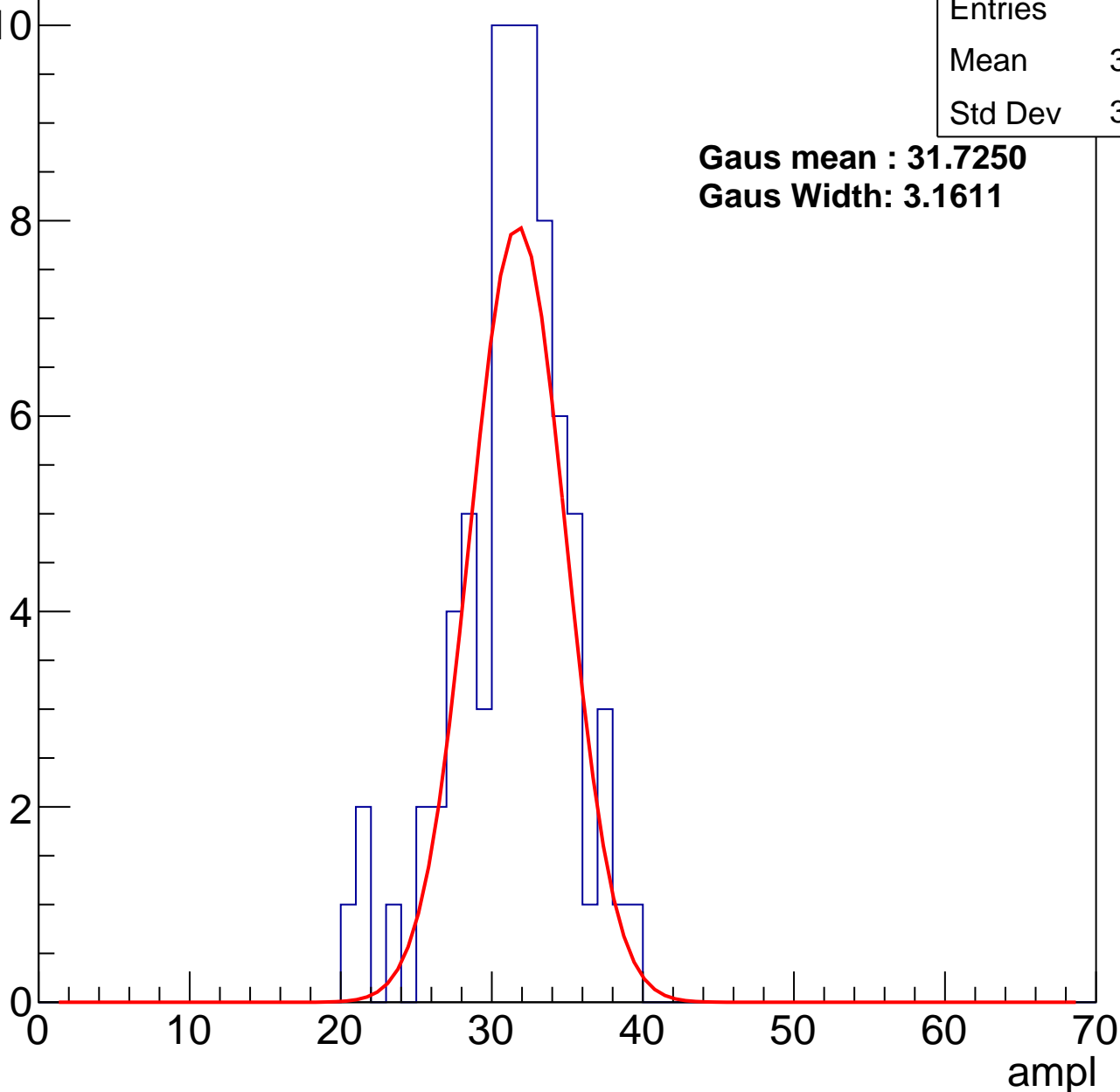
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.92
Std Dev	3.759

Gaus mean : 31.7250

Gaus Width: 3.1611



B1L100S, U11-ch50, adc1

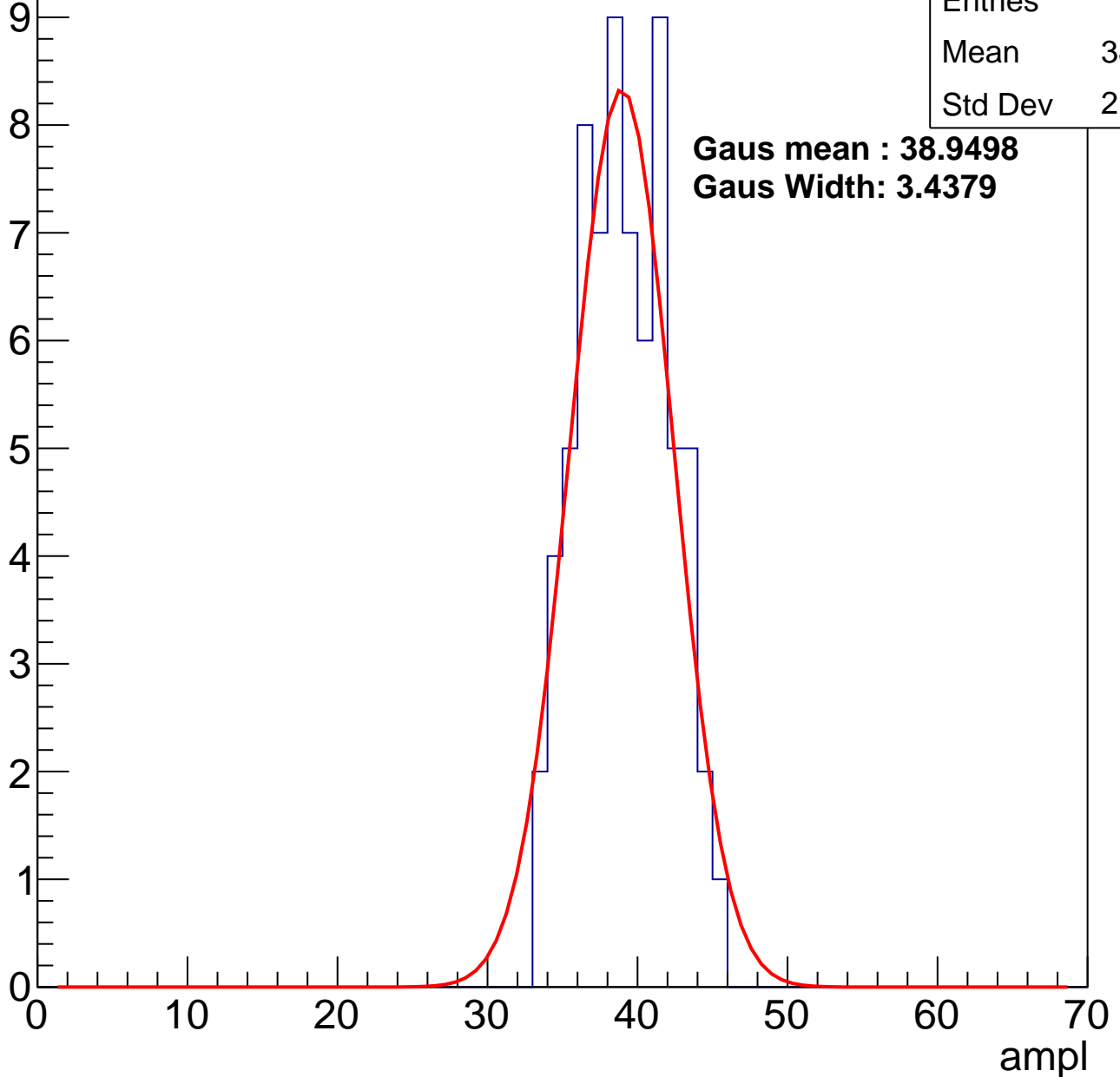
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	38.66
Std Dev	2.942

Gaus mean : 38.9498

Gaus Width: 3.4379



B1L100S, U11-ch50, adc2

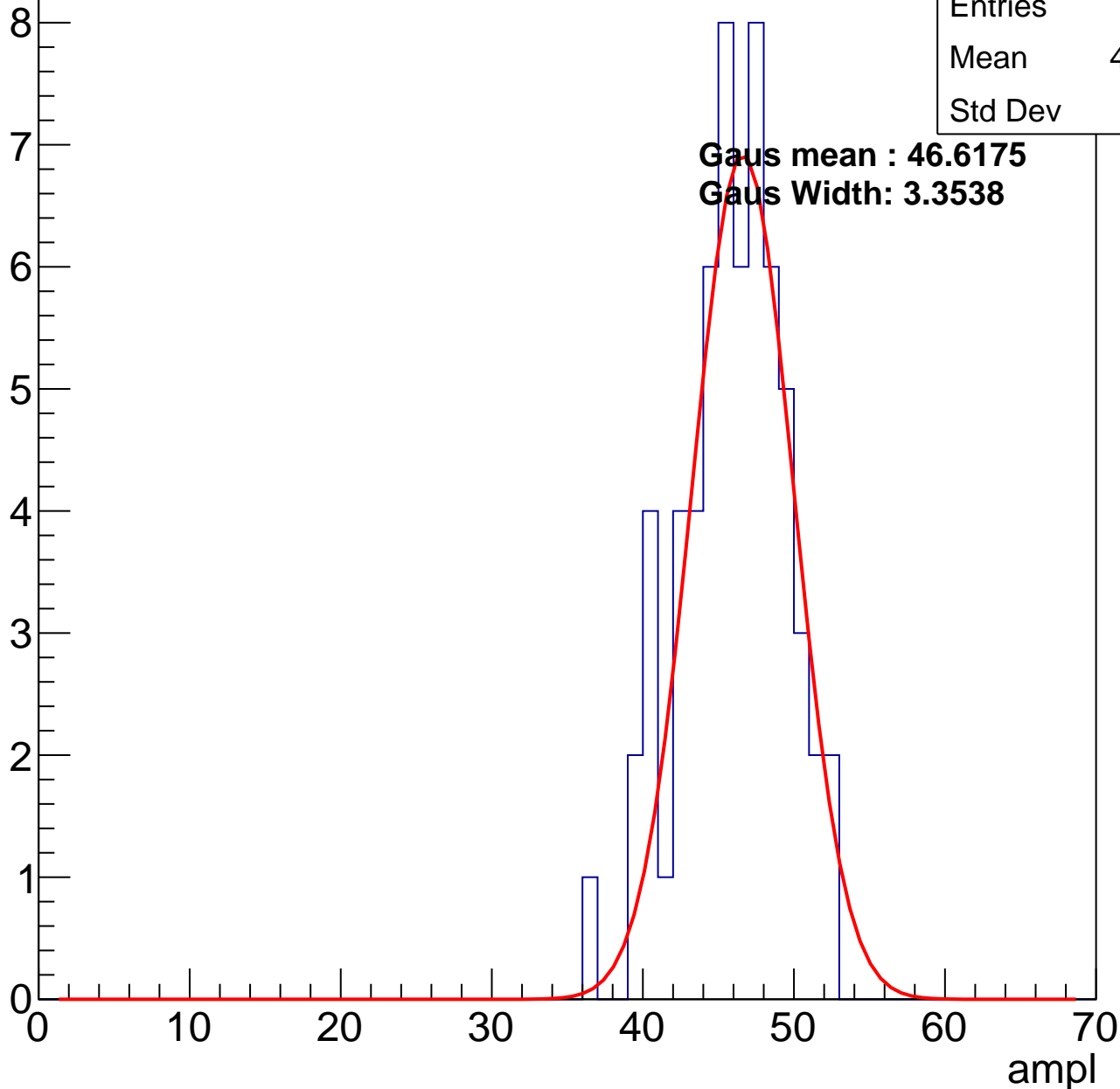
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	45.48
Std Dev	3.43

Gaus mean : 46.6175

Gaus Width: 3.3538

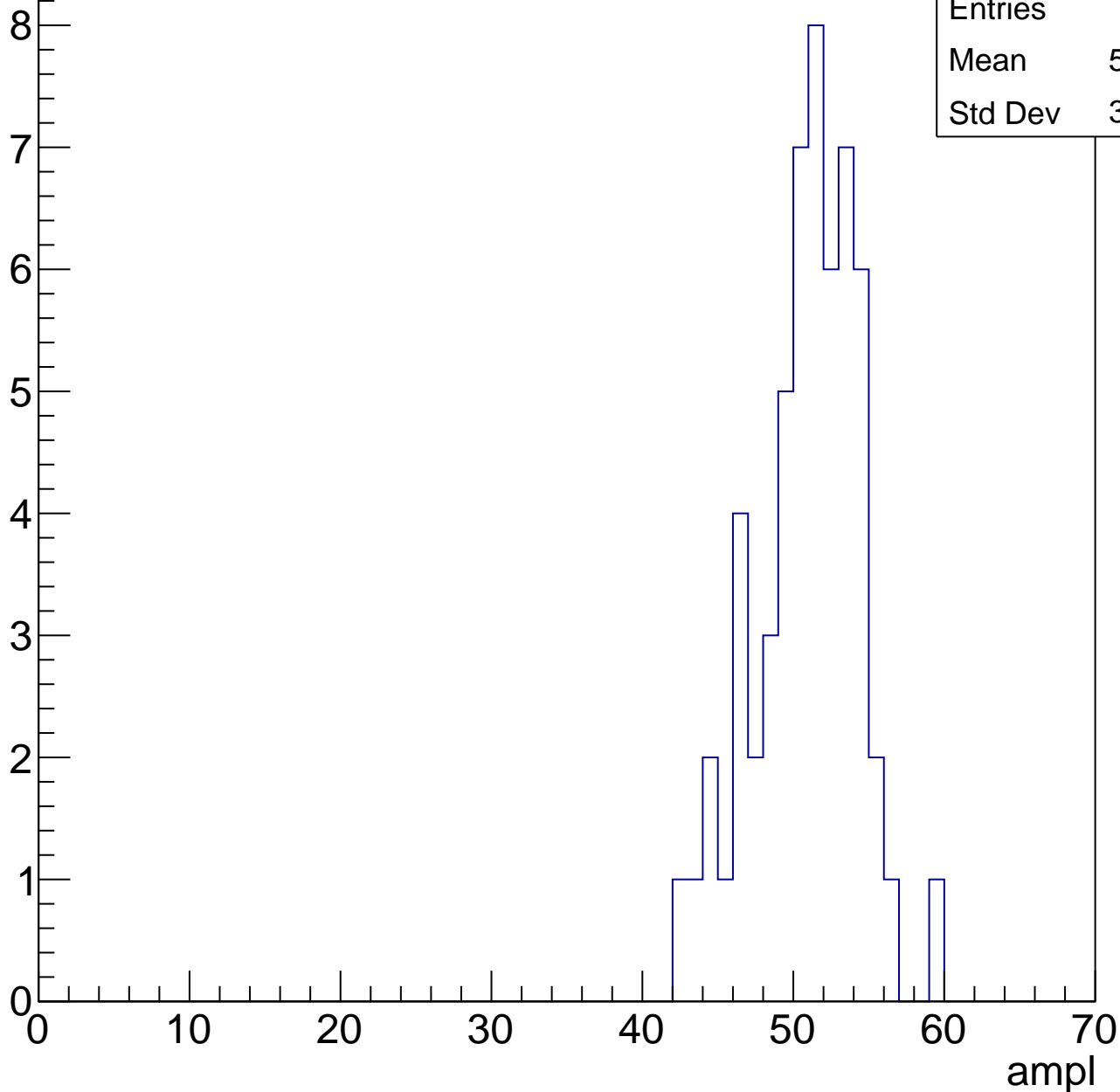


B1L100S, U11-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

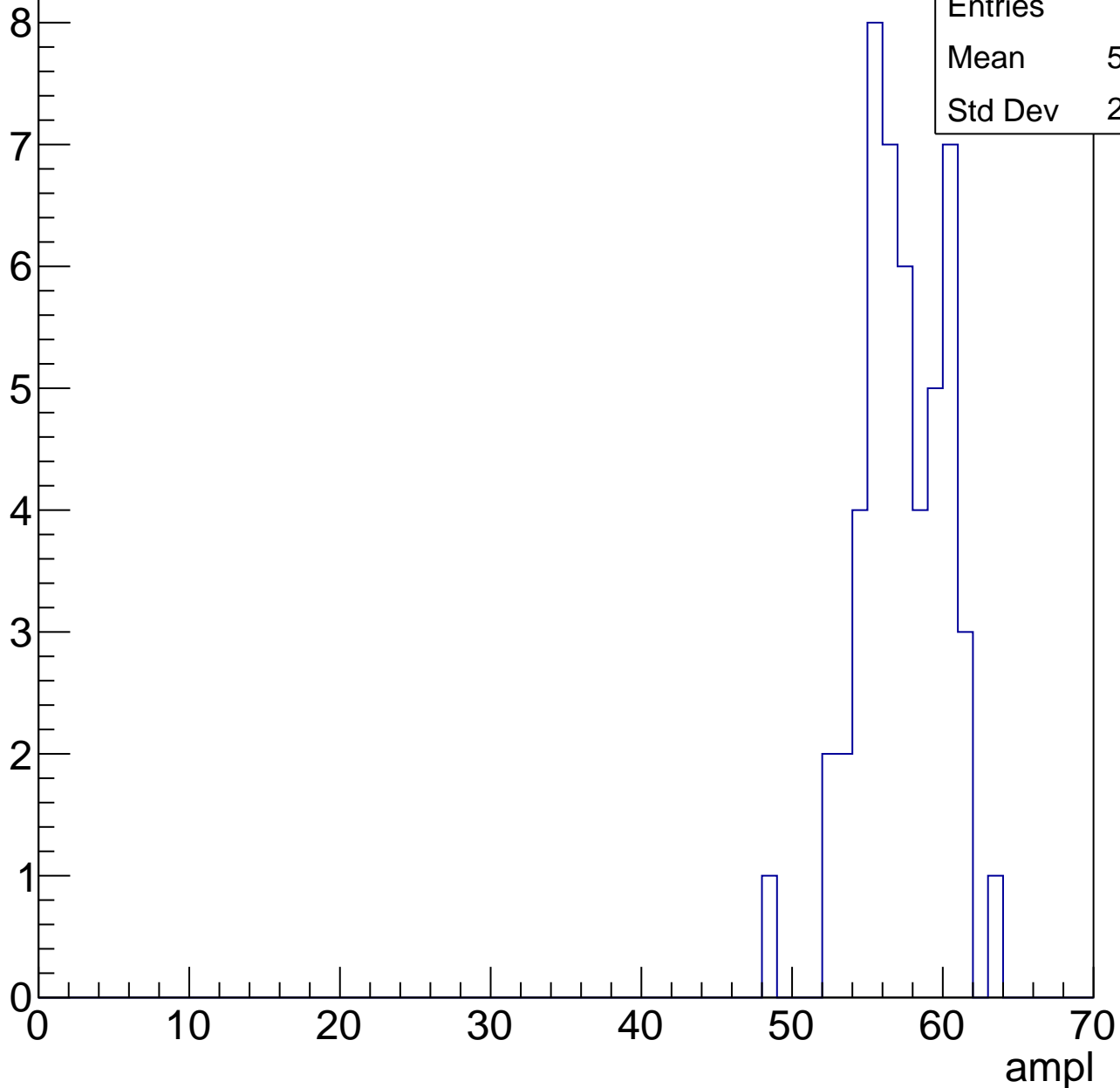
Entries	57
Mean	50.44
Std Dev	3.393



B1L100S, U11-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



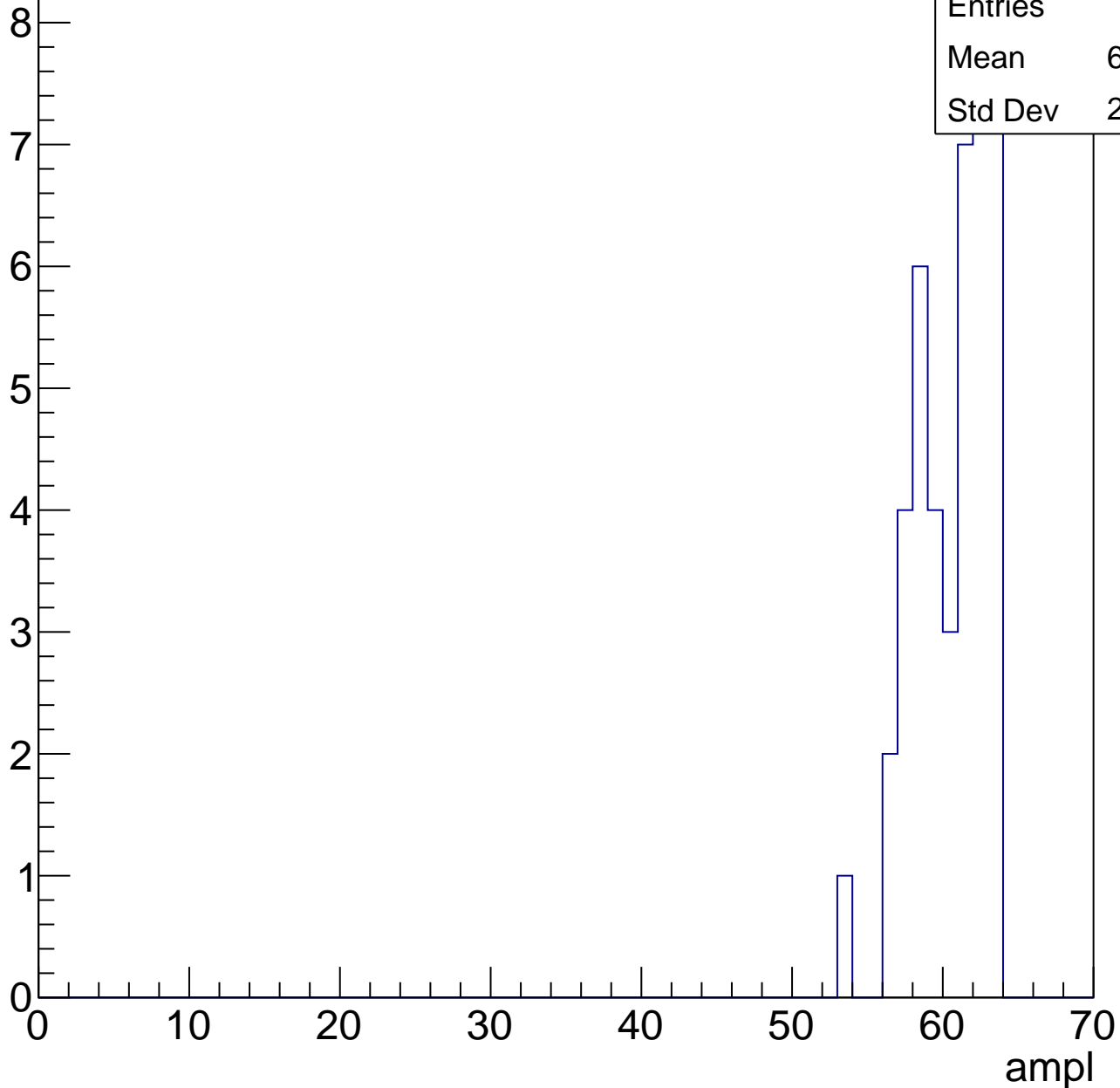
Entries	50
Mean	56.82
Std Dev	2.854

B1L100S, U11-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	60.09
Std Dev	2.438



B1L100S, U11-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	46.75
Std Dev	26.99

B1L100S, U11-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch51, adc0

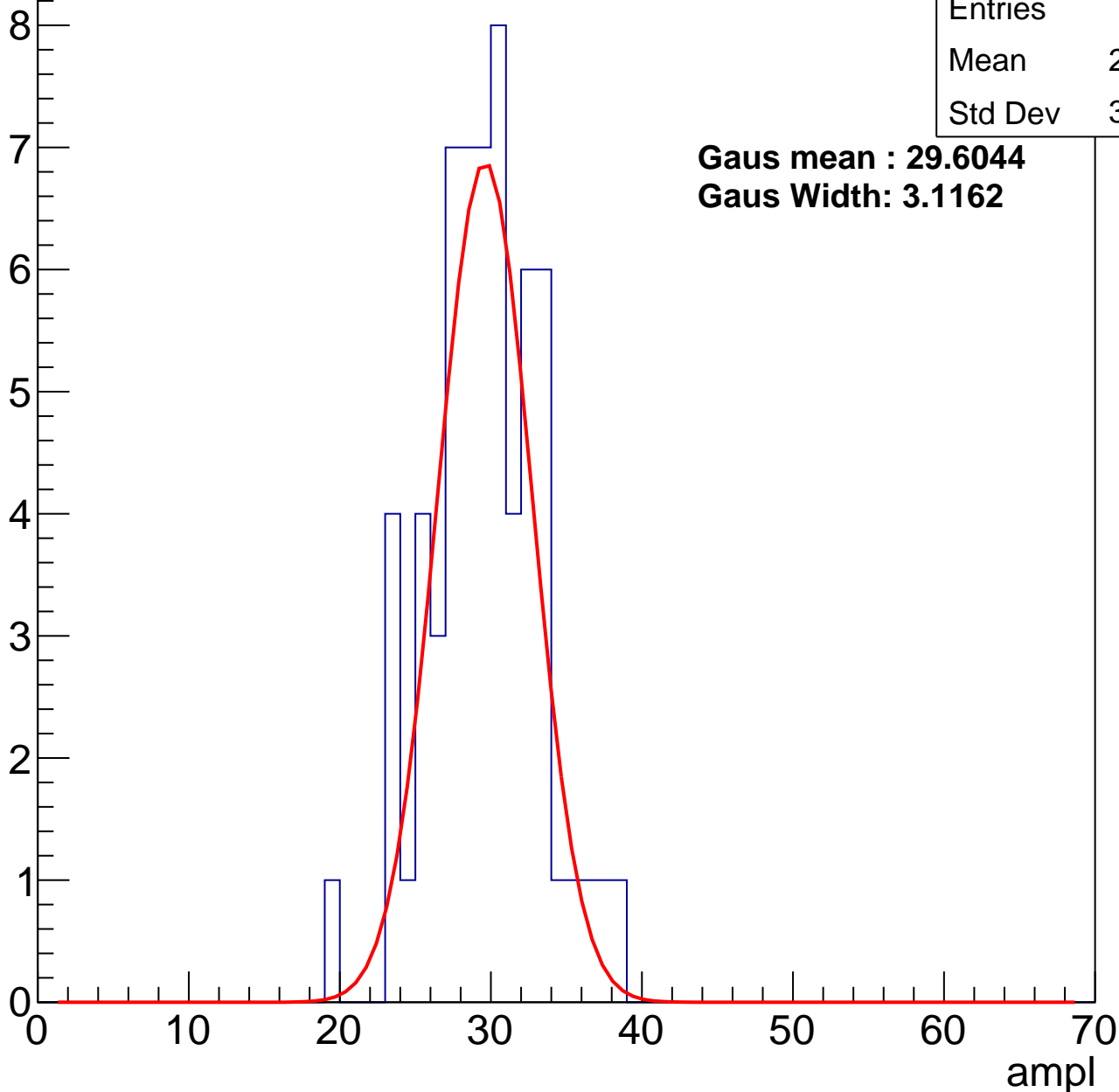
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	29.13
Std Dev	3.614

Gaus mean : 29.6044

Gaus Width: 3.1162



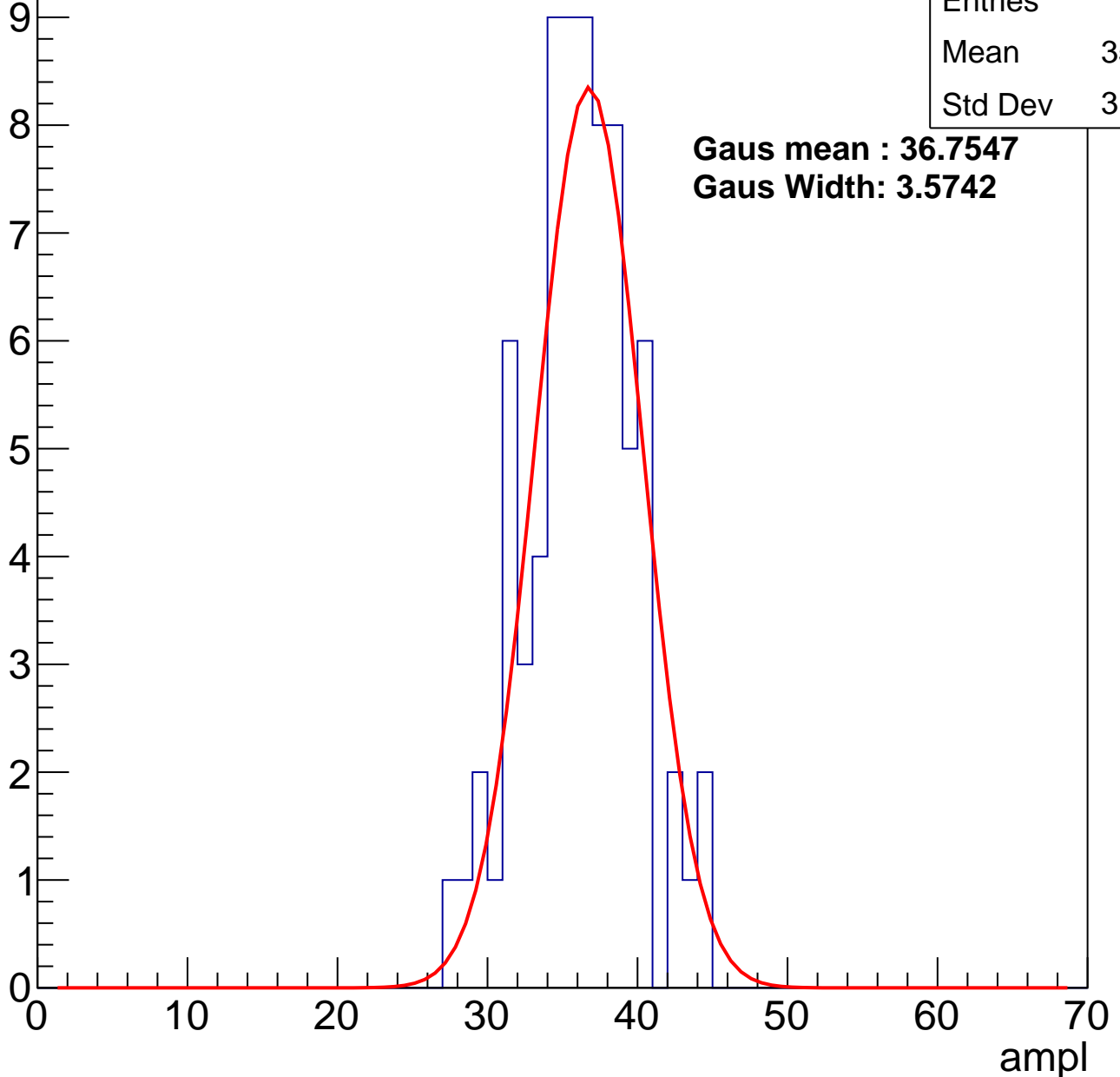
B1L100S, U11-ch51, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	35.74
Std Dev	3.573

Gaus mean : 36.7547
Gaus Width: 3.5742



B1L100S, U11-ch51, adc2

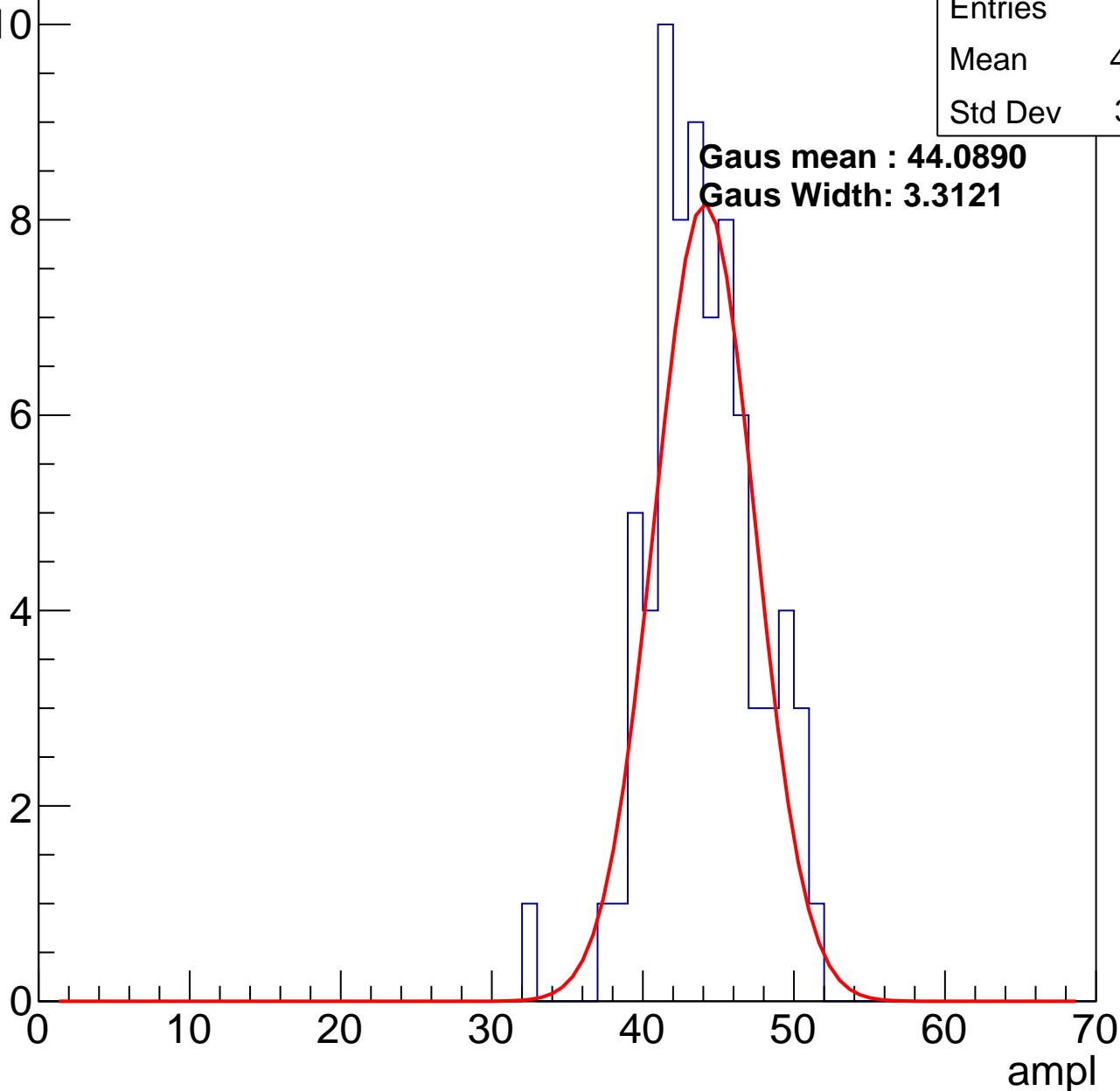
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43.53
Std Dev	3.481

Gaus mean : 44.0890

Gaus Width: 3.3121

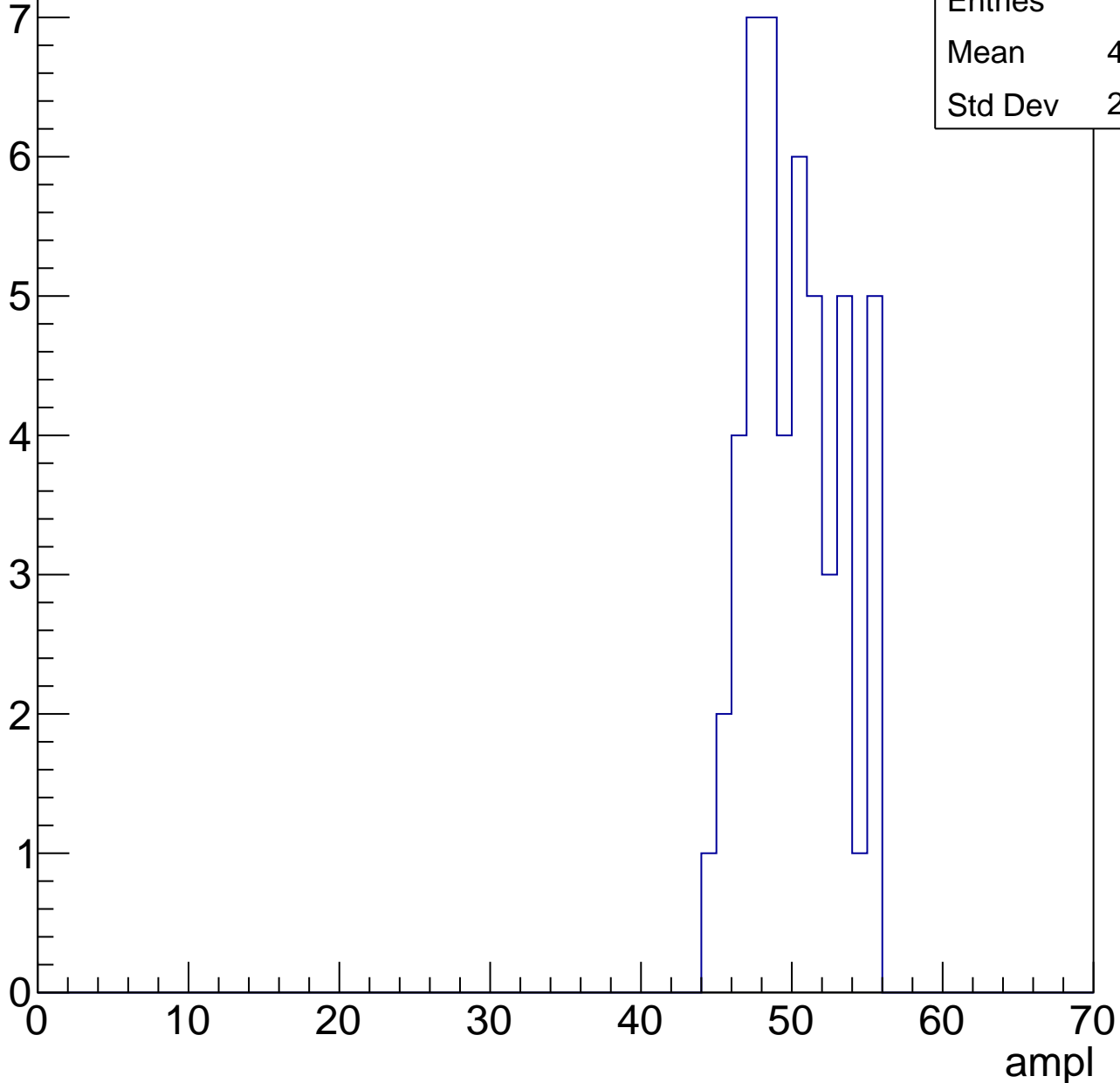


B1L100S, U11-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	49.68
Std Dev	2.976



B1L100S, U11-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	54.83
Std Dev	7.277

Entry

10

8

6

4

2

0

0

10

20

30

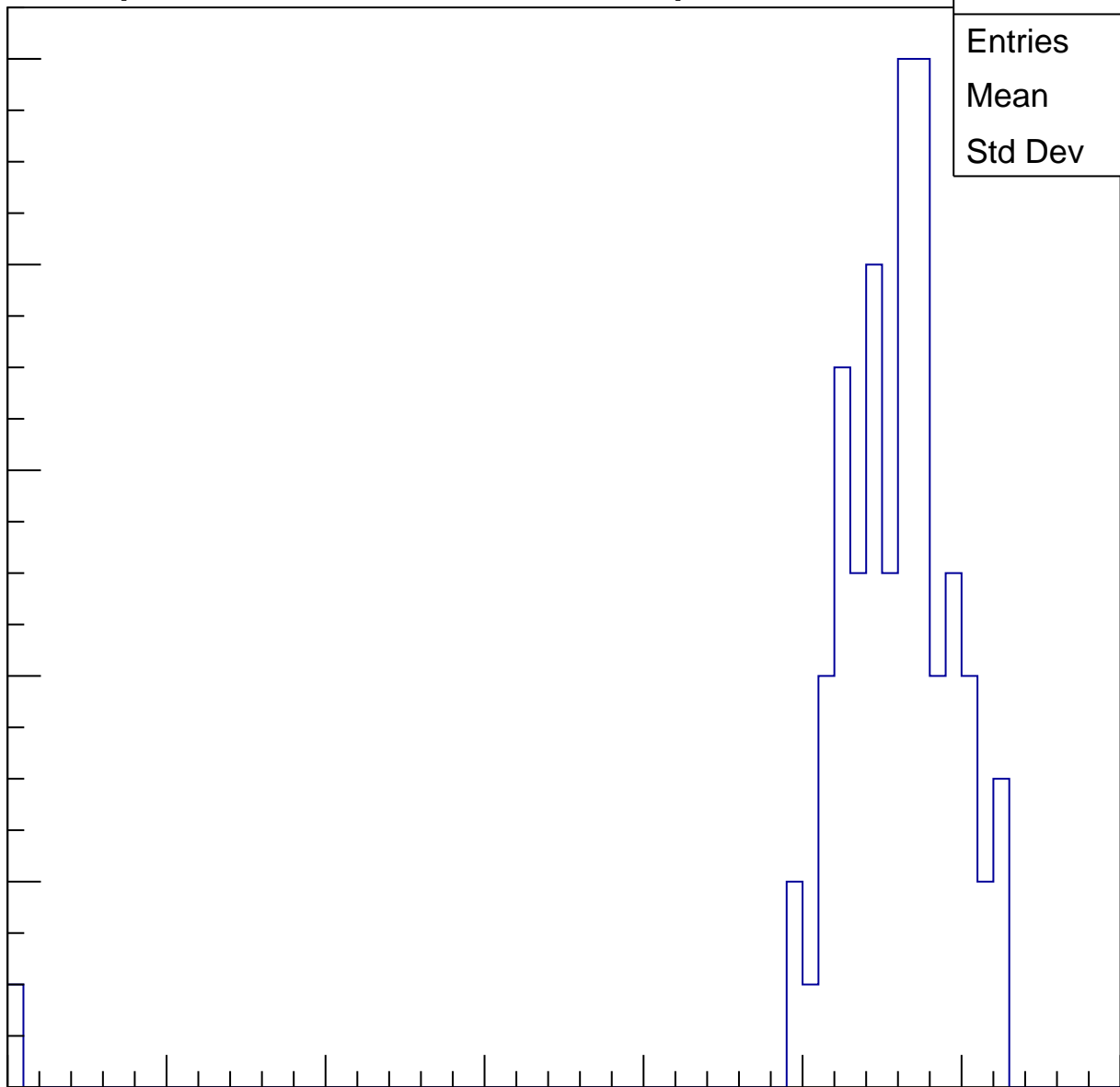
40

50

60

70

ampl

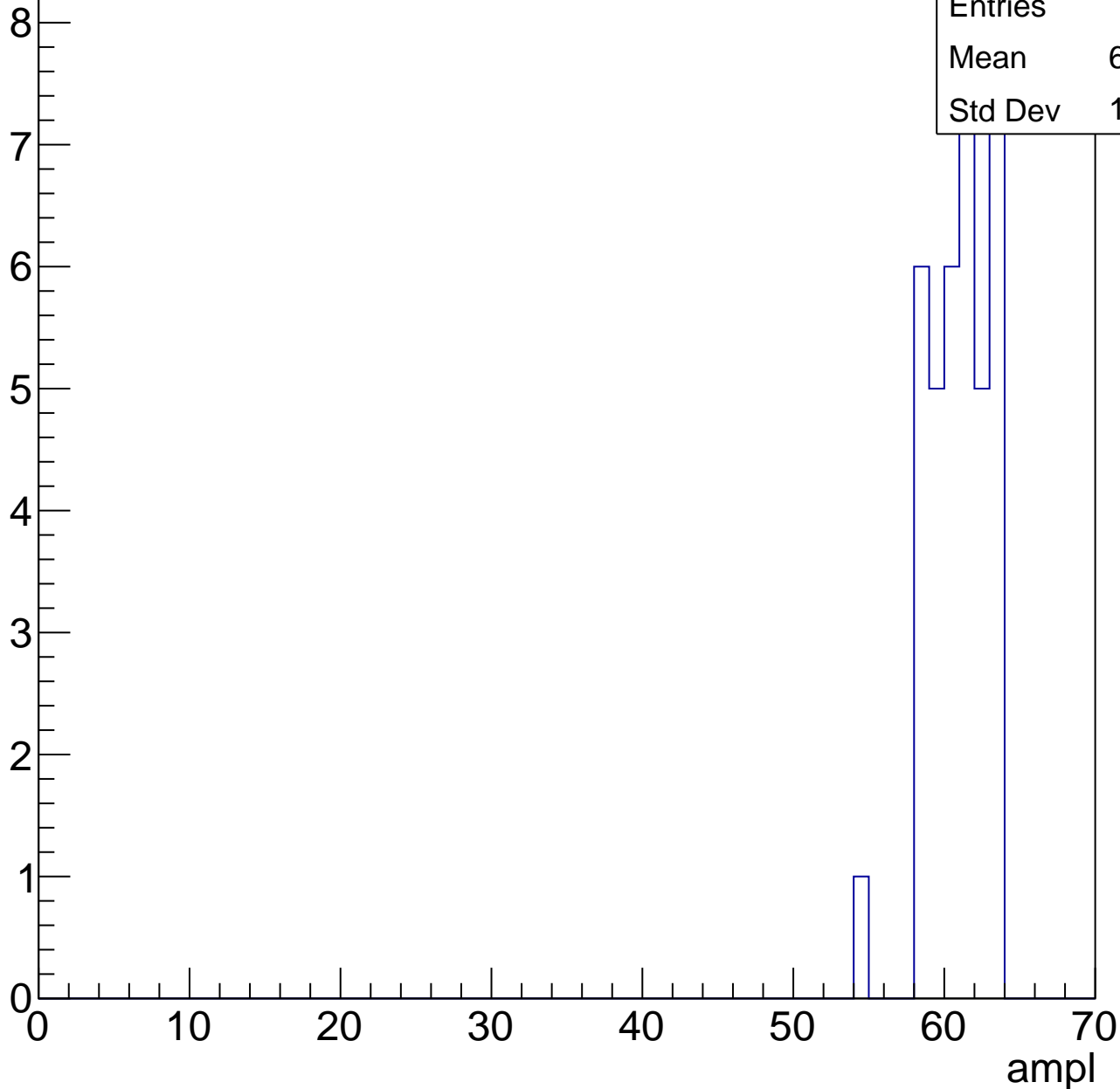


B1L100S, U11-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	60.49
Std Dev	1.998



B1L100S, U11-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch52, adc0

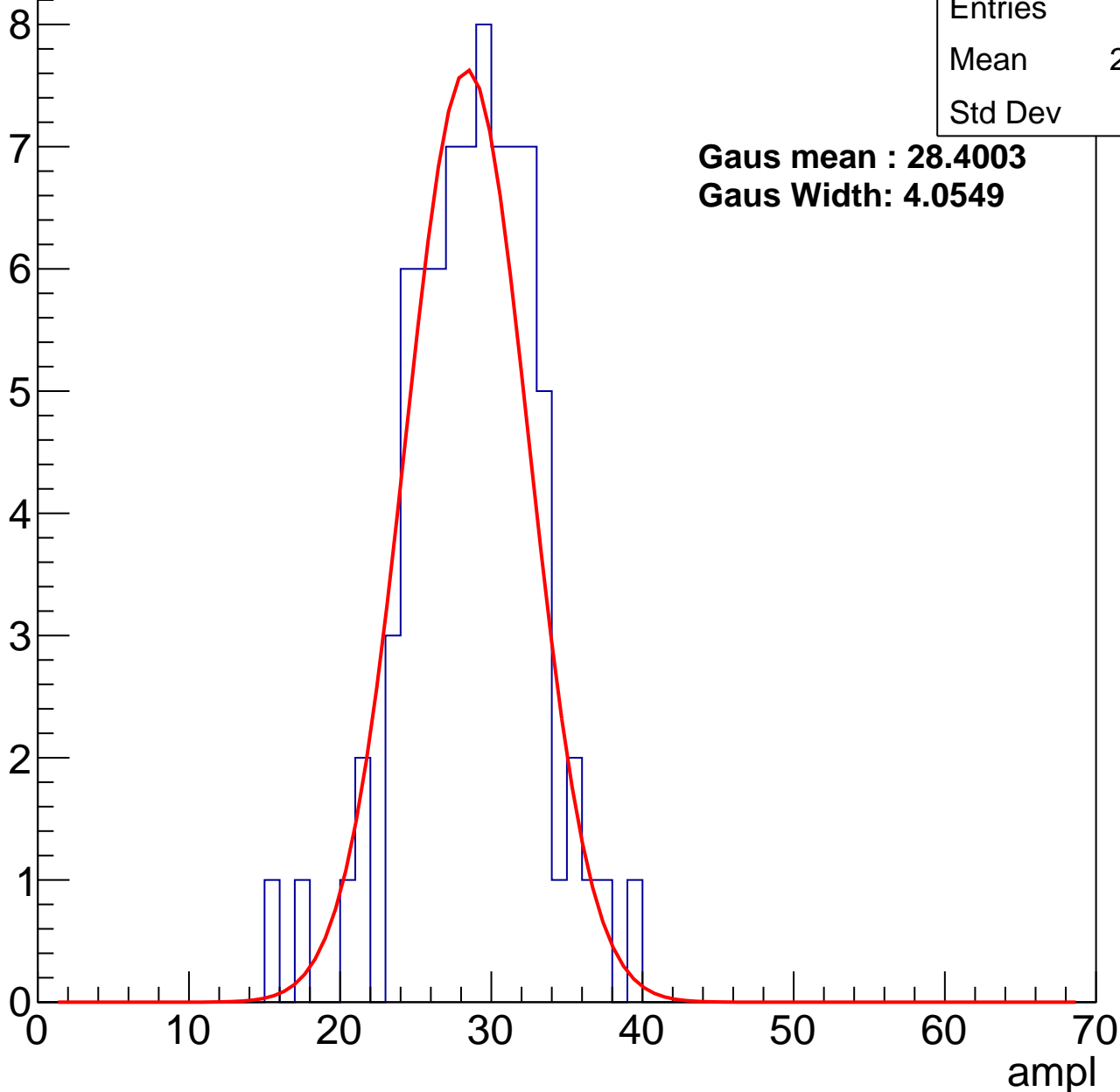
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	28.27
Std Dev	4.24

Gaus mean : 28.4003

Gaus Width: 4.0549



B1L100S, U11-ch52, adc1

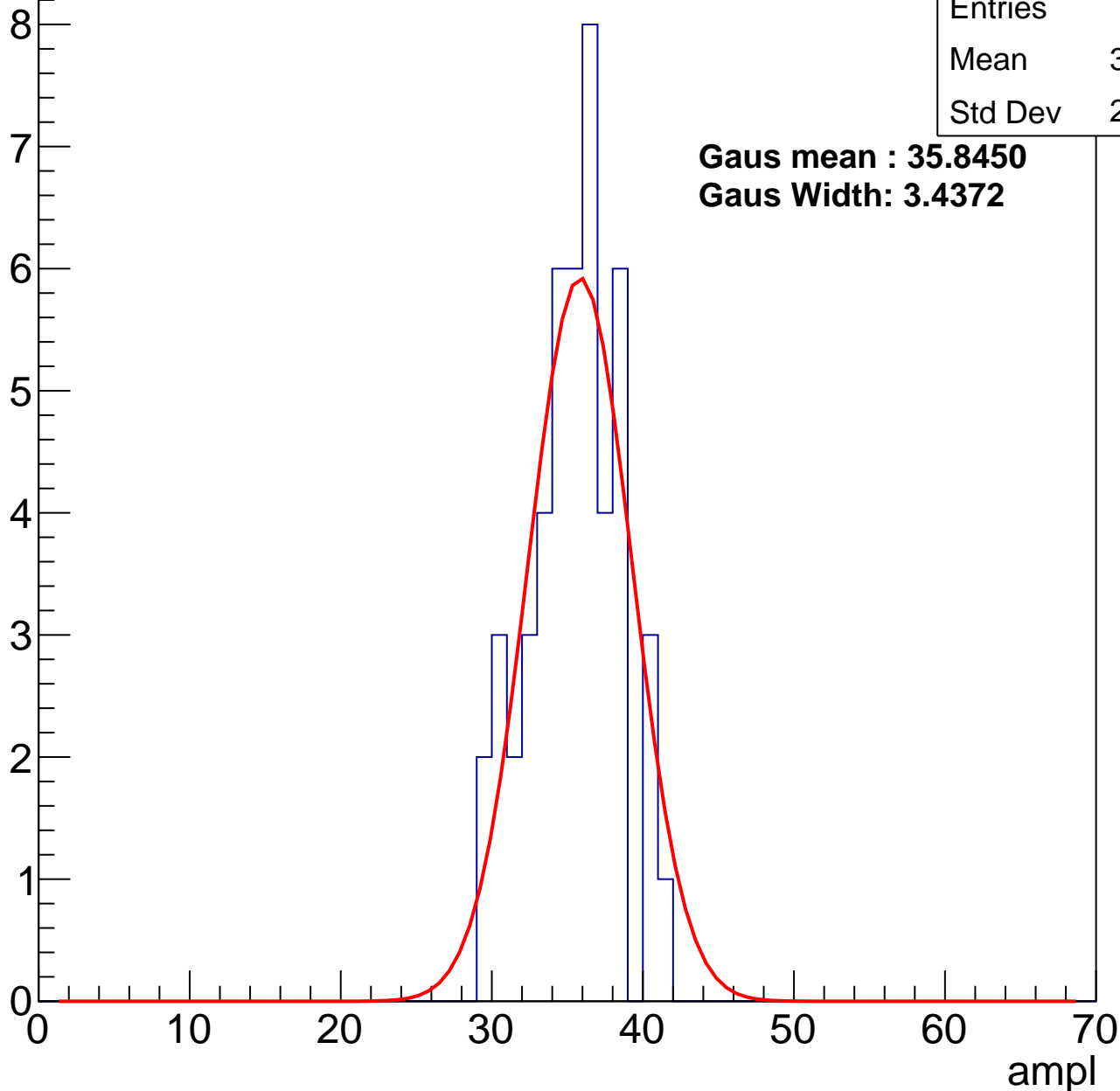
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	34.94
Std Dev	2.947

Gaus mean : 35.8450

Gaus Width: 3.4372



B1L100S, U11-ch52, adc2

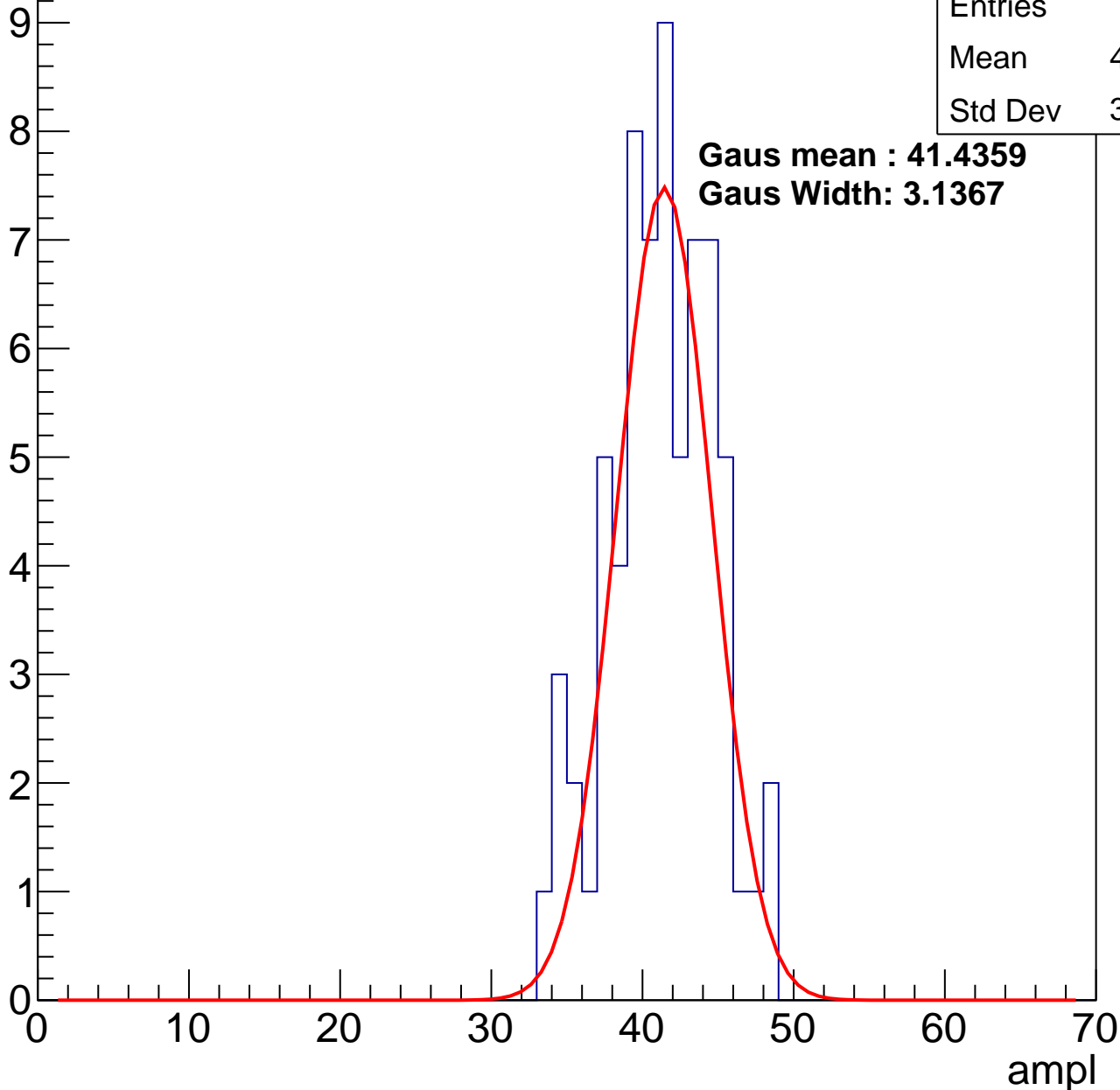
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	40.76
Std Dev	3.418

Gaus mean : 41.4359

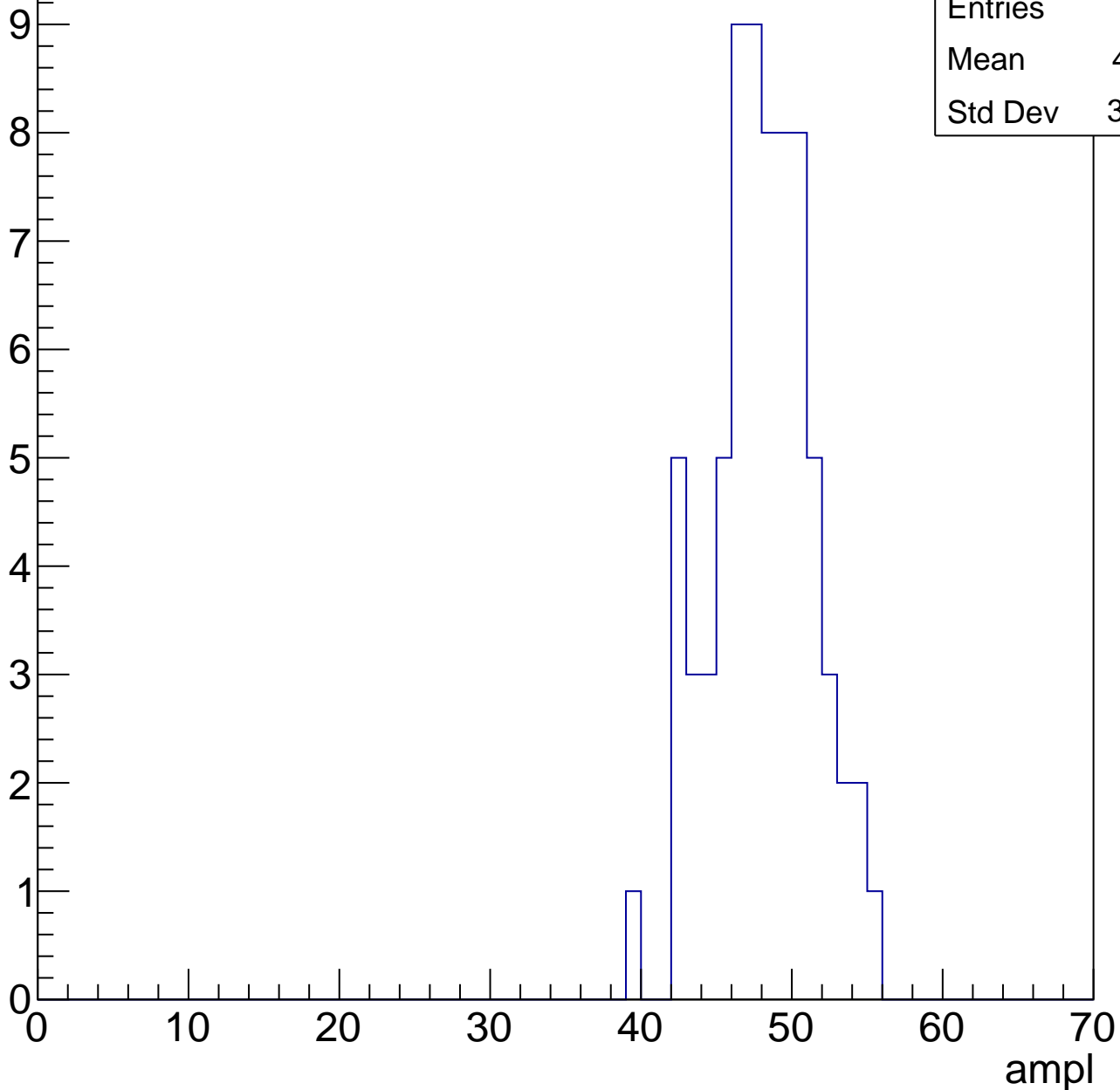
Gaus Width: 3.1367



B1L100S, U11-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



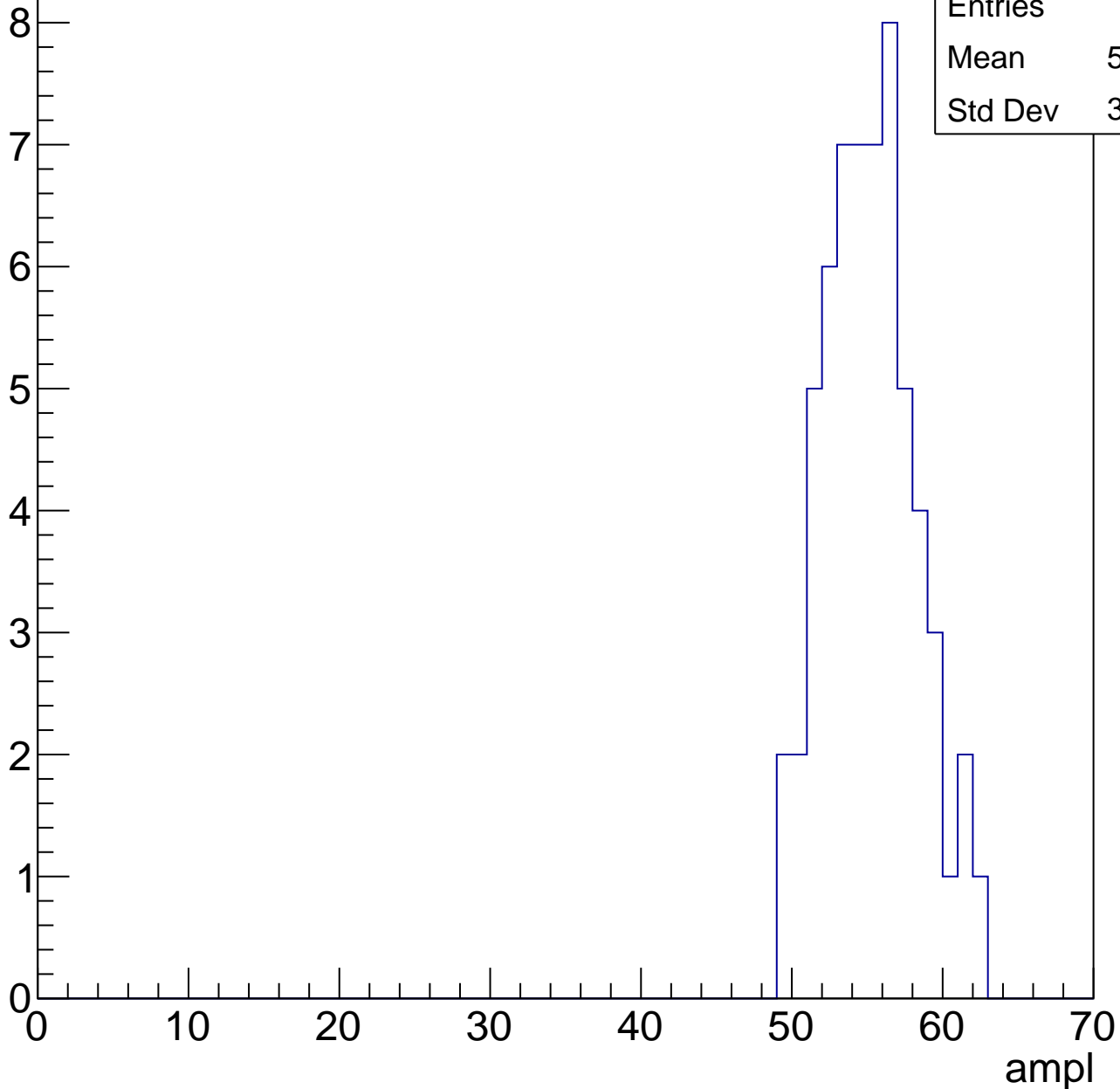
Entries	72
Mean	47.61
Std Dev	3.264

B1L100S, U11-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

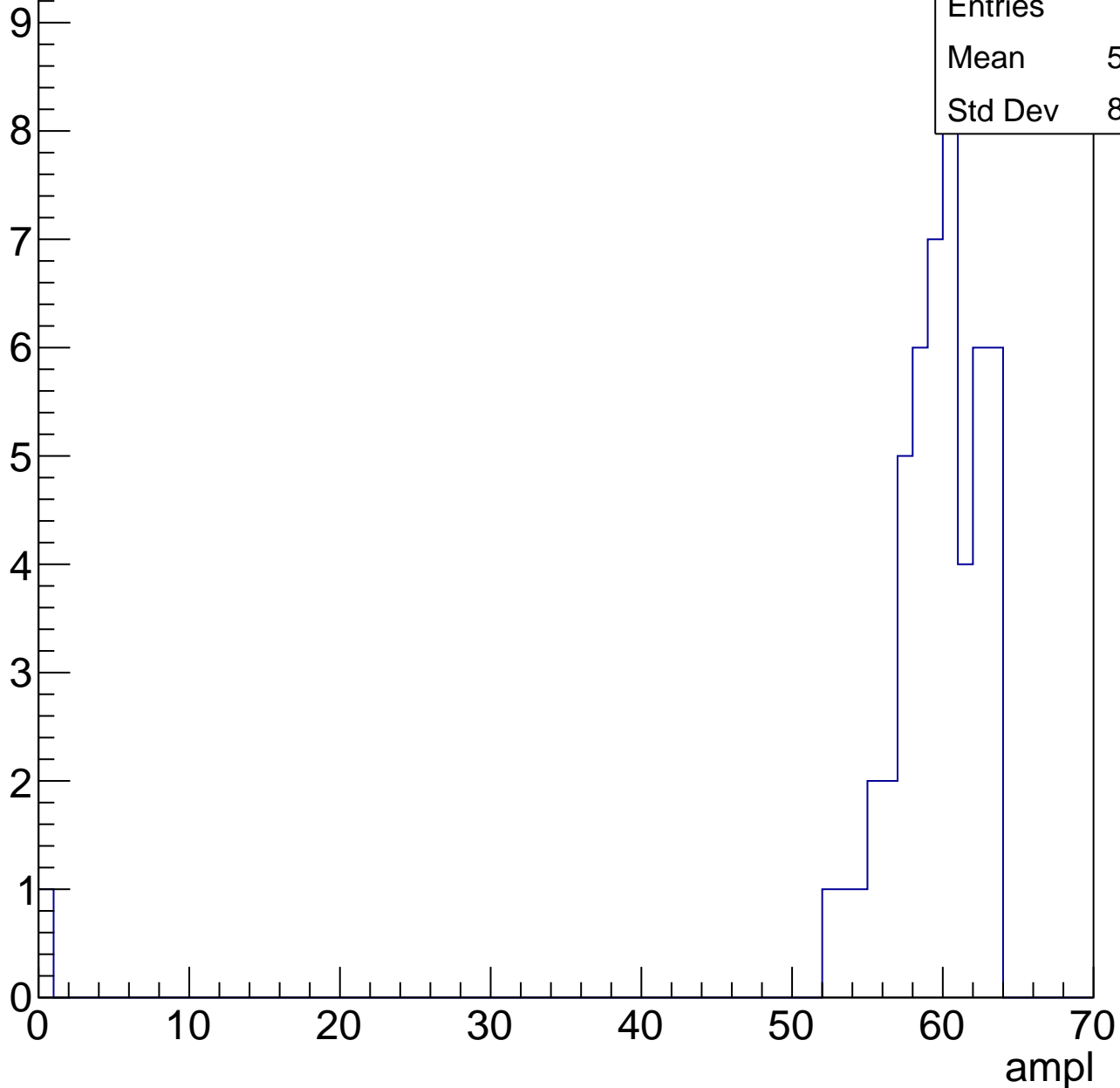
Entries	60
Mean	54.75
Std Dev	3.015



B1L100S, U11-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

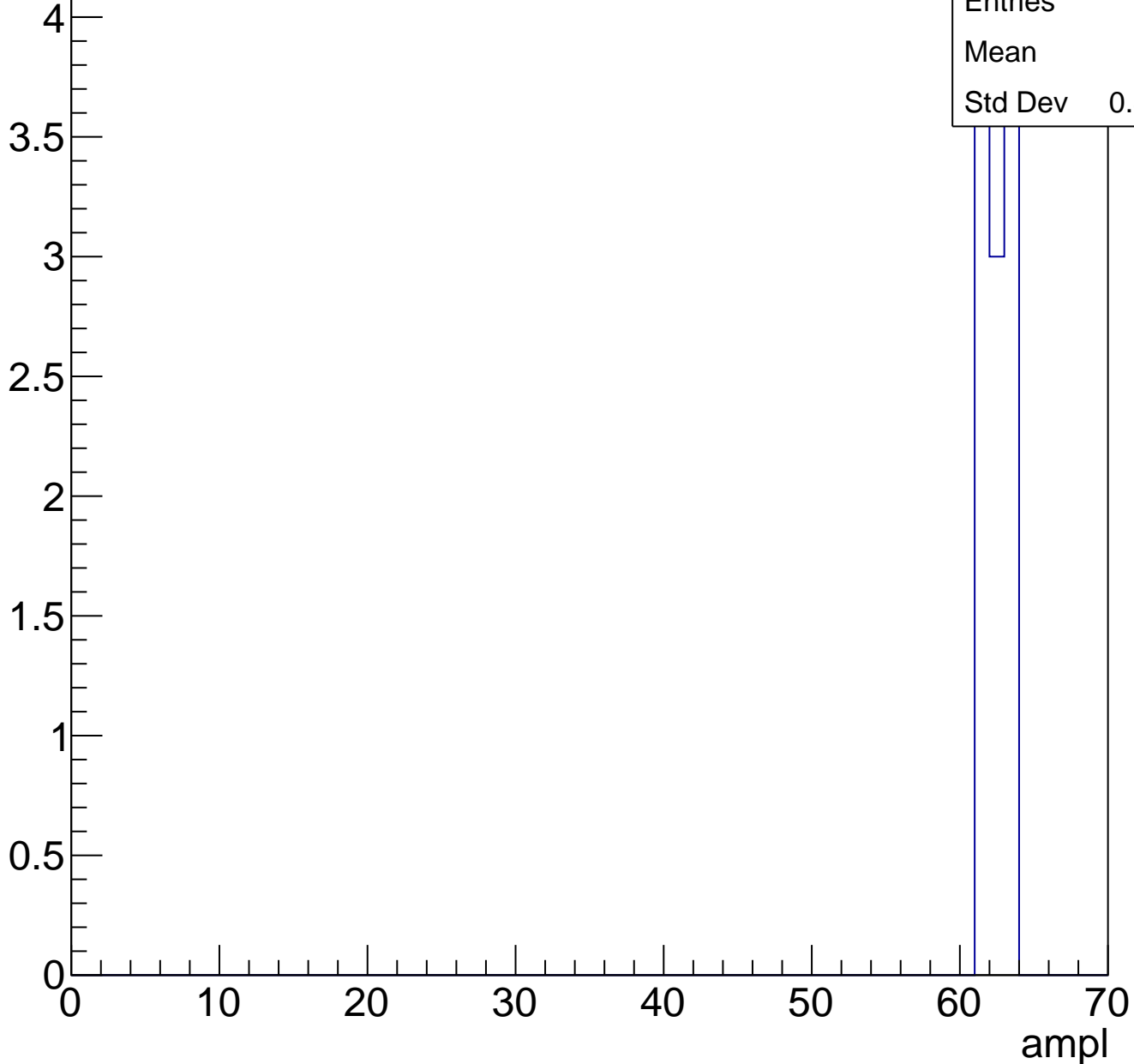
Entry



B1L100S, U11-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch53, adc0

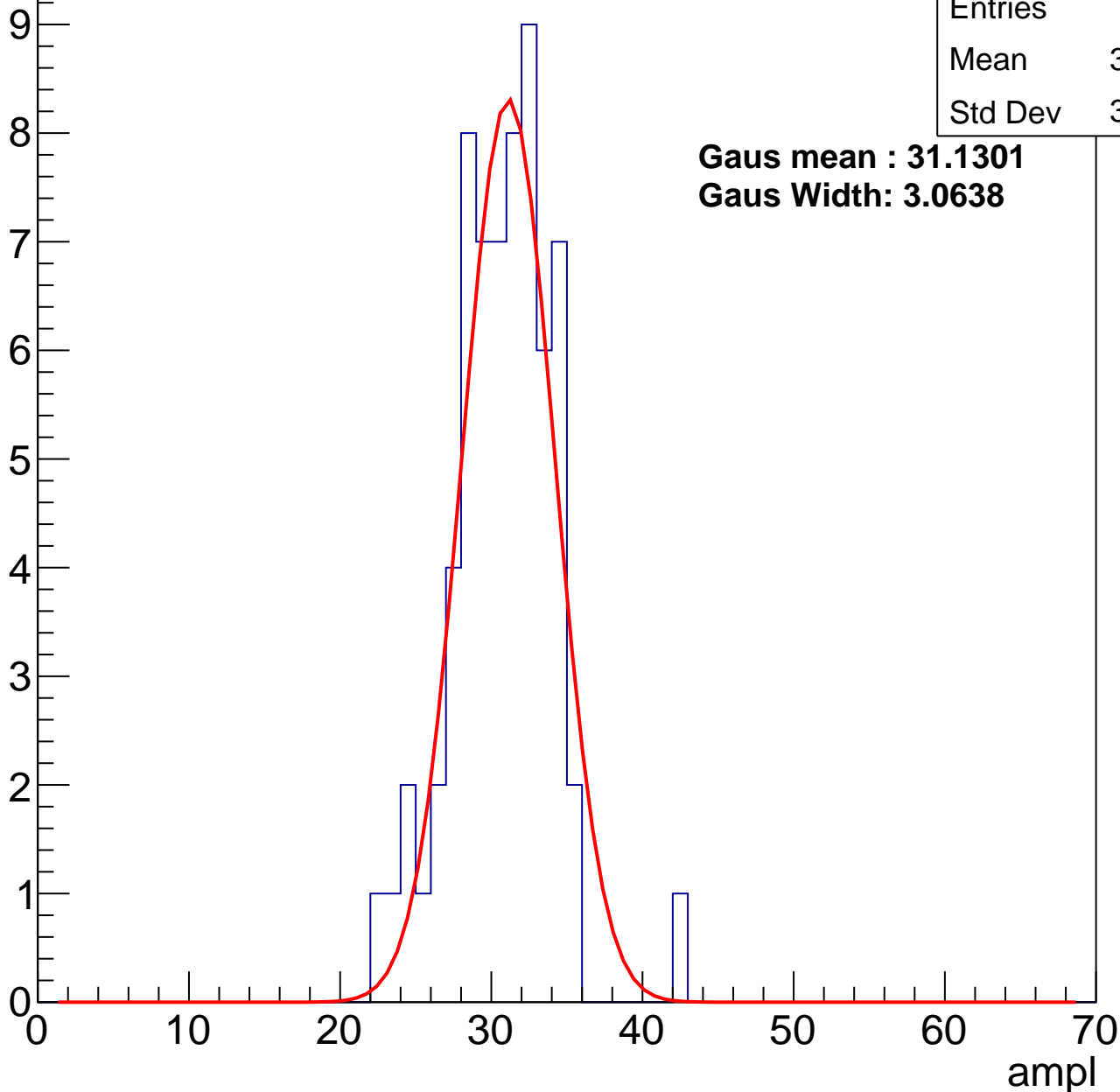
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	30.29
Std Dev	3.293

Gaus mean : 31.1301

Gaus Width: 3.0638



B1L100S, U11-ch53, adc1

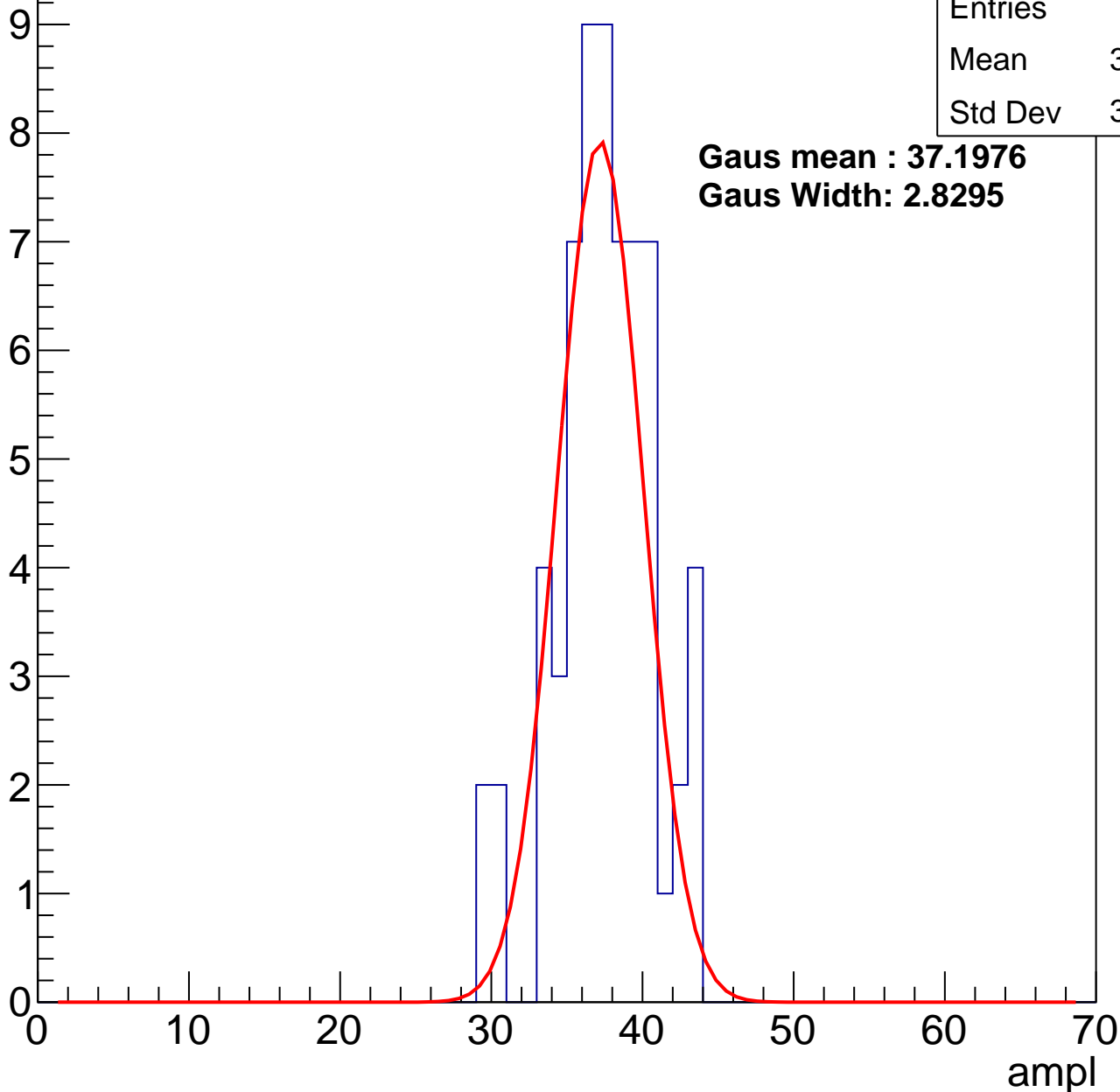
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	37.03
Std Dev	3.216

Gaus mean : 37.1976

Gaus Width: 2.8295



B1L100S, U11-ch53, adc2

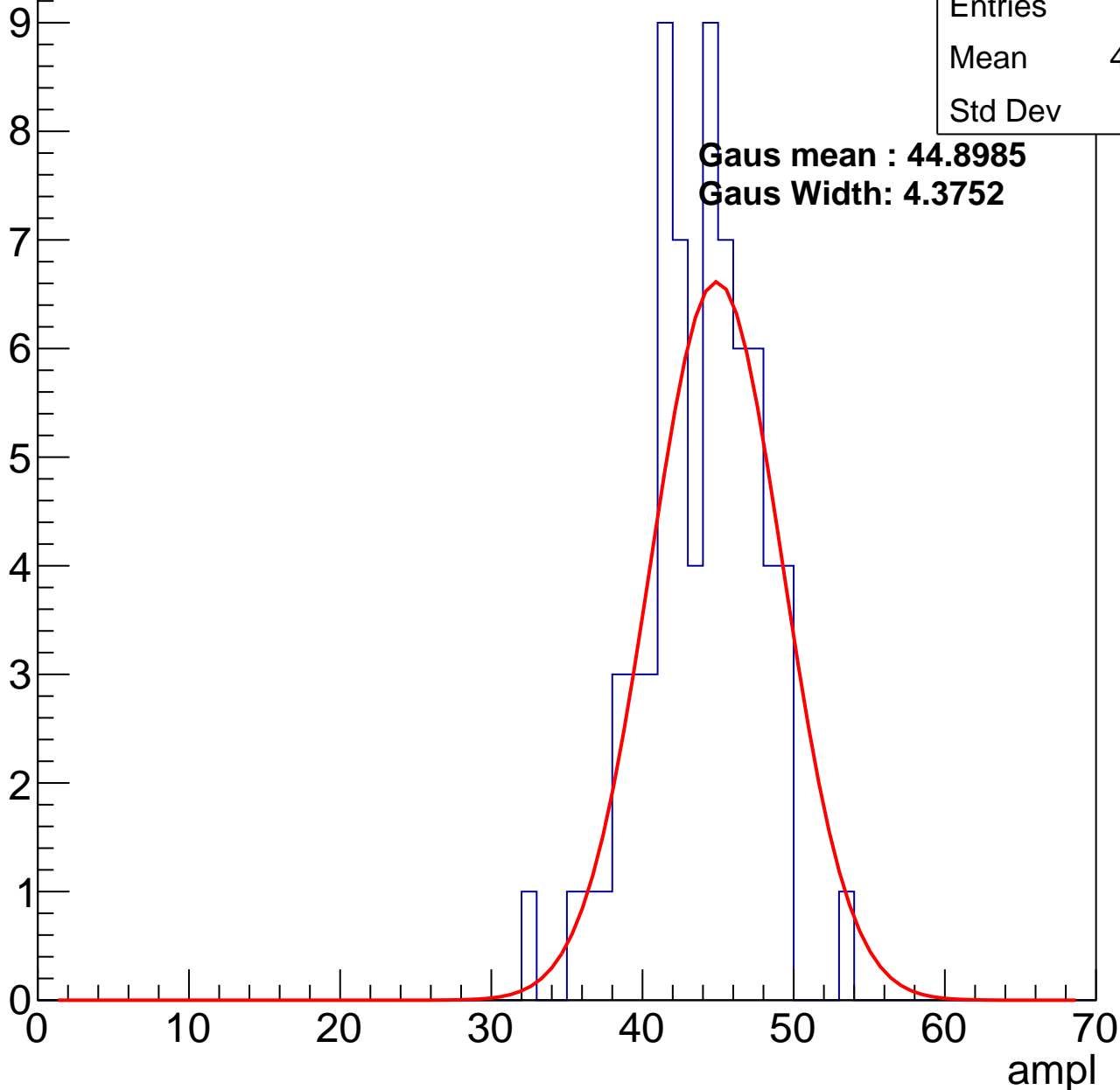
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	43.37
Std Dev	3.75

Gaus mean : 44.8985

Gaus Width: 4.3752



B1L100S, U11-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	50.48
Std Dev	3.598

Entry

10

8

6

4

2

0

0

10

20

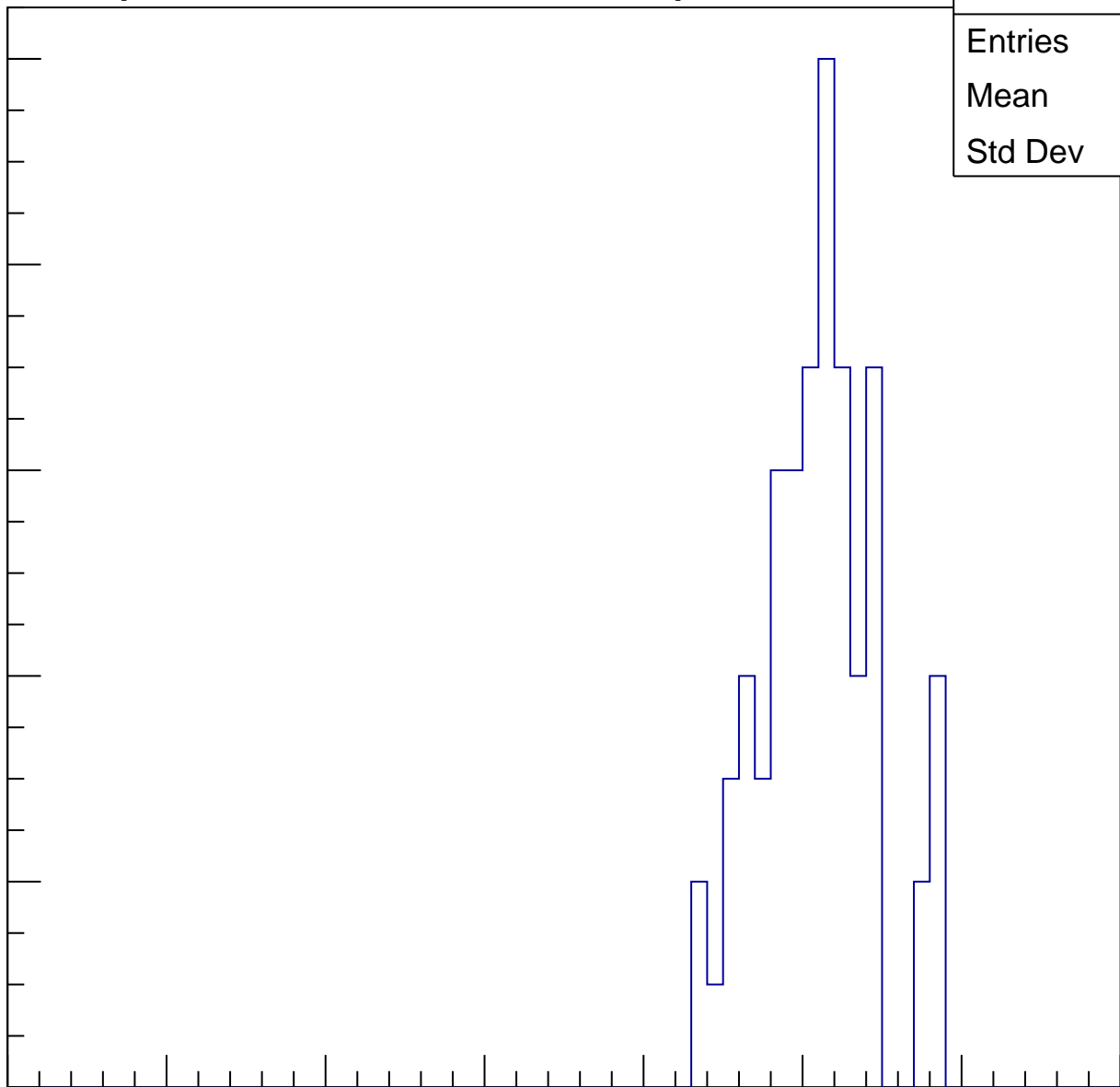
30

40

50

60

ampl

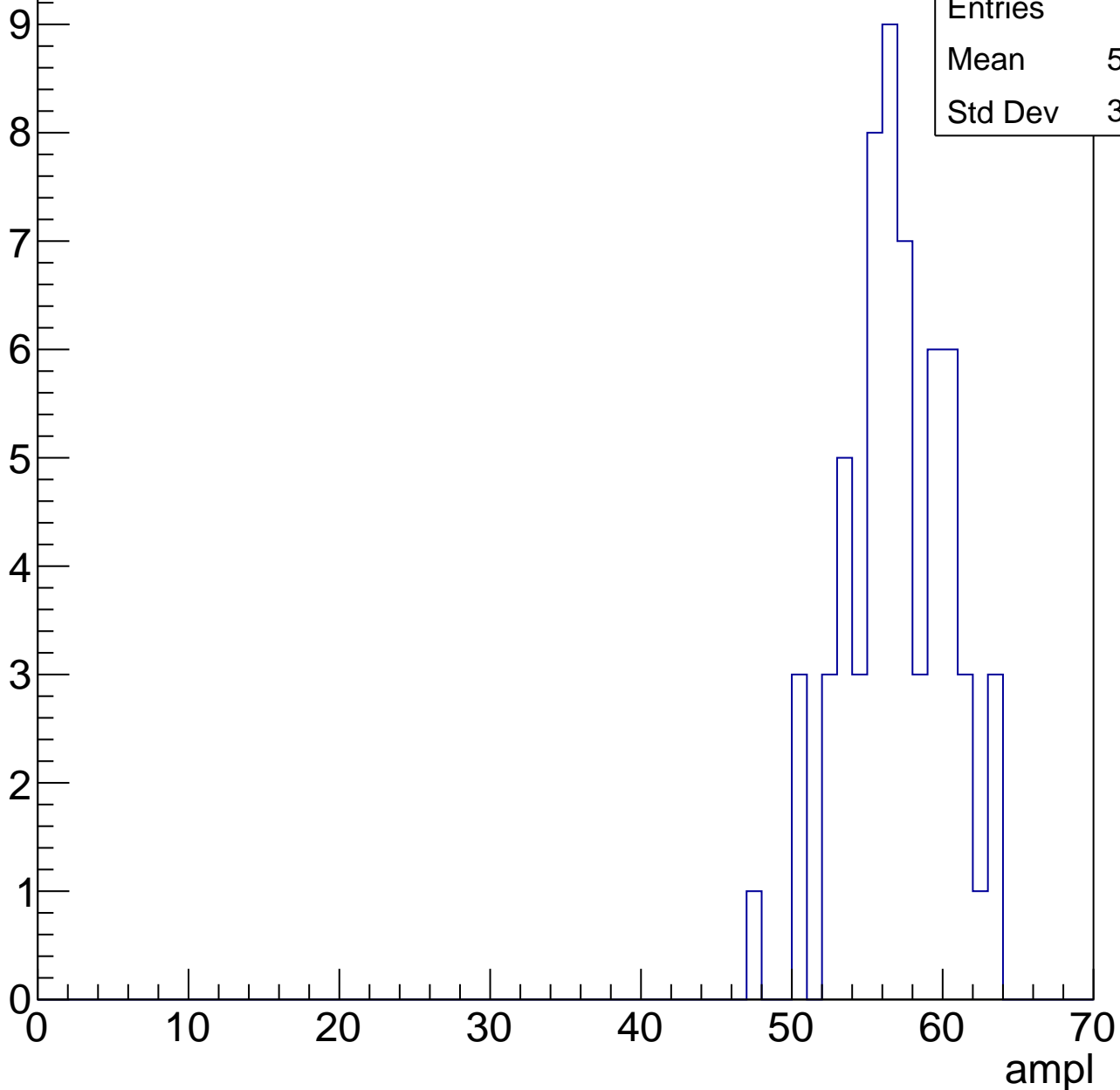


B1L100S, U11-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

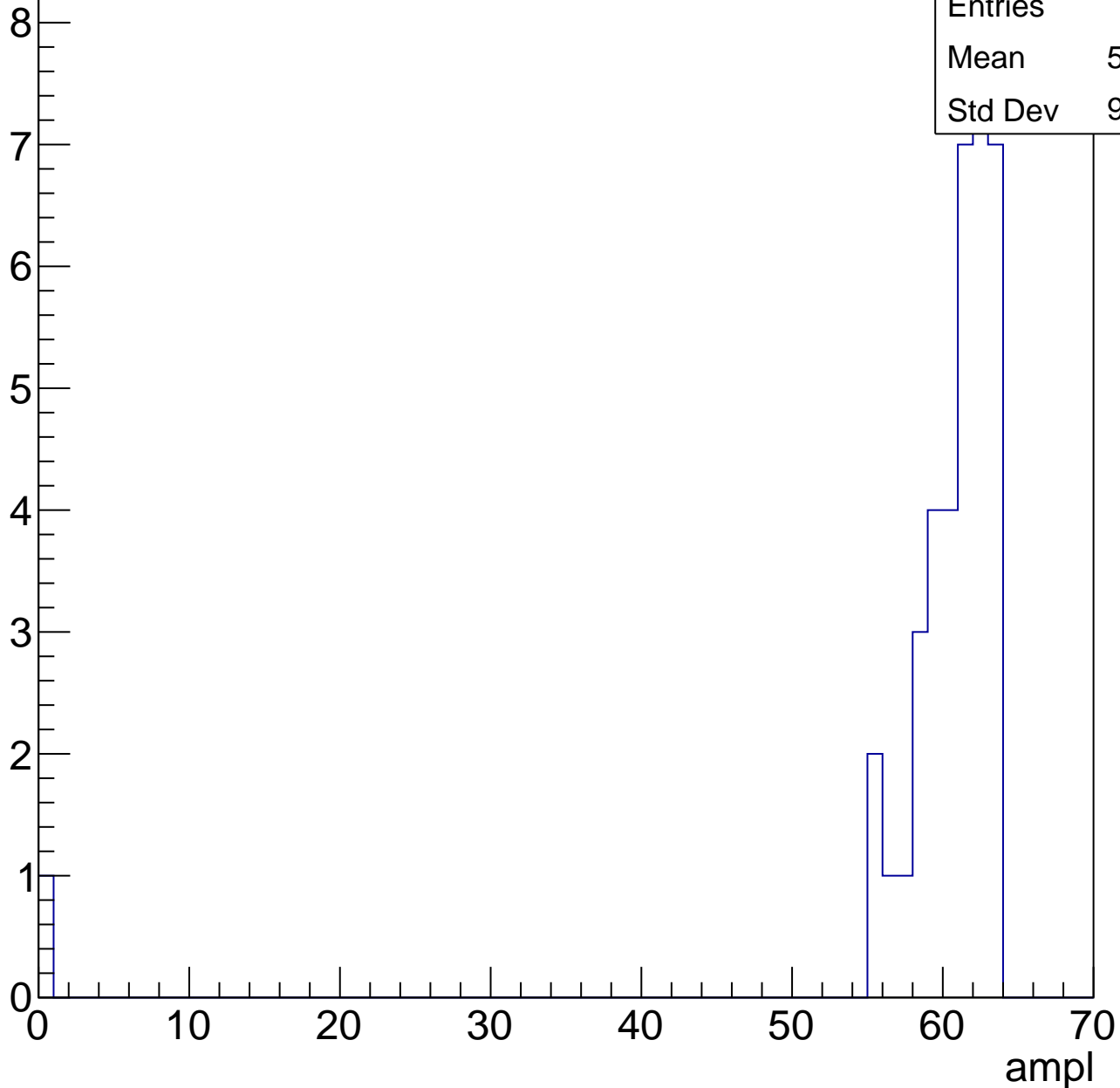
Entries	61
Mean	56.48
Std Dev	3.424



B1L100S, U11-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	38
Mean	58.87
Std Dev	9.926

B1L100S, U11-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch54, adc0

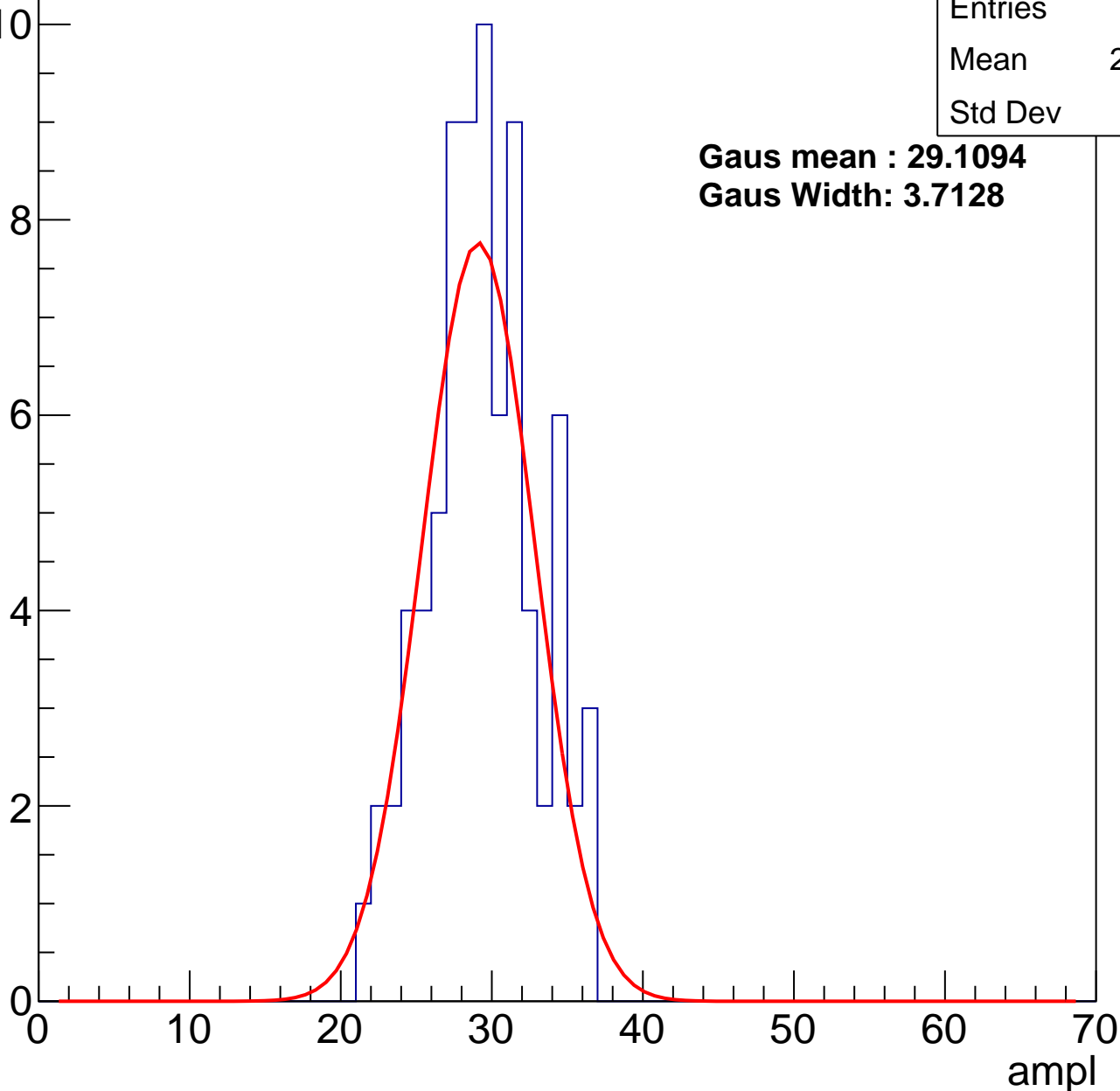
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.94
Std Dev	3.52

Gaus mean : 29.1094

Gaus Width: 3.7128



B1L100S, U11-ch54, adc1

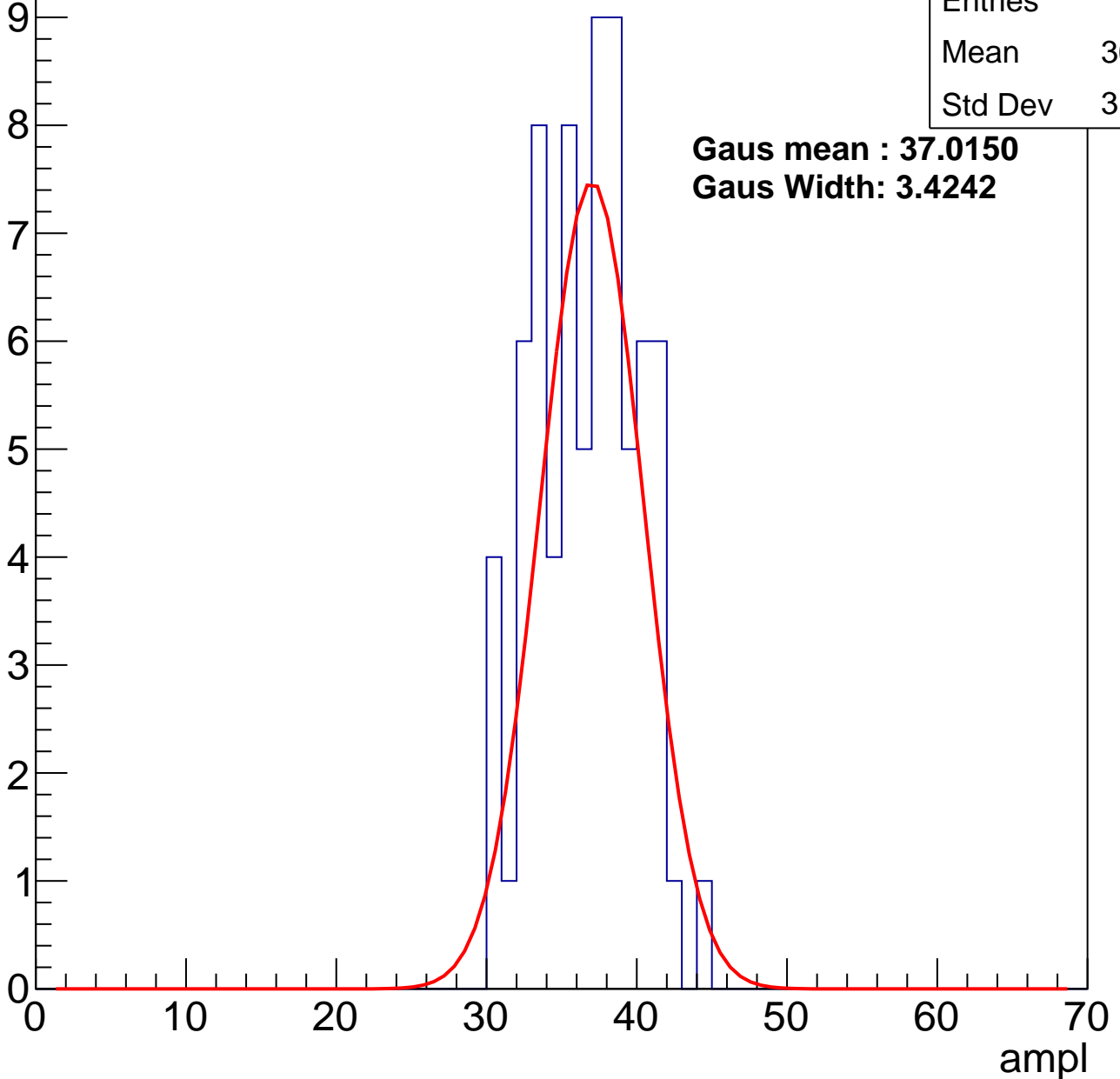
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	36.23
Std Dev	3.296

Gaus mean : 37.0150

Gaus Width: 3.4242



B1L100S, U11-ch54, adc2

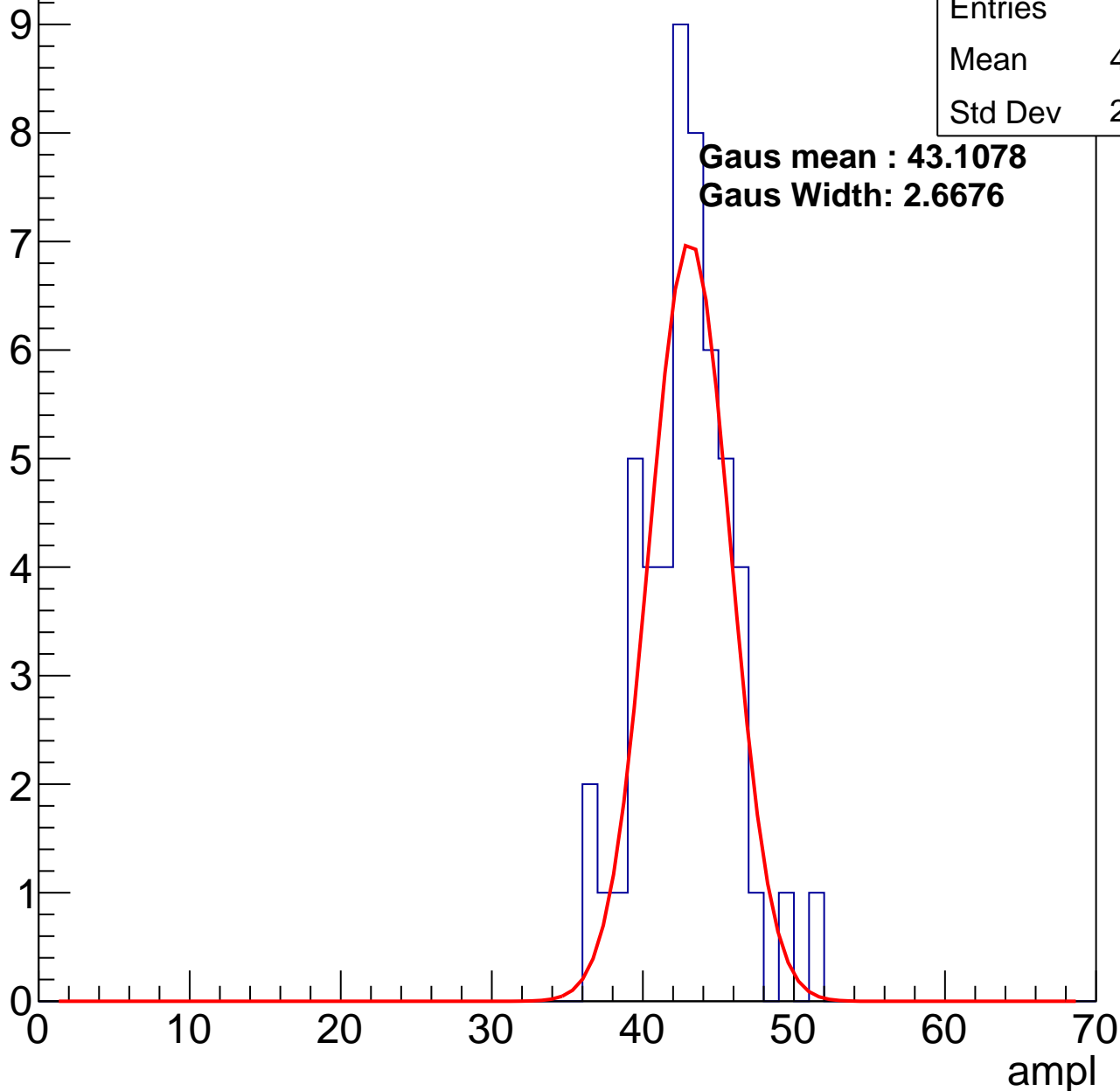
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	42.46
Std Dev	2.984

Gaus mean : 43.1078

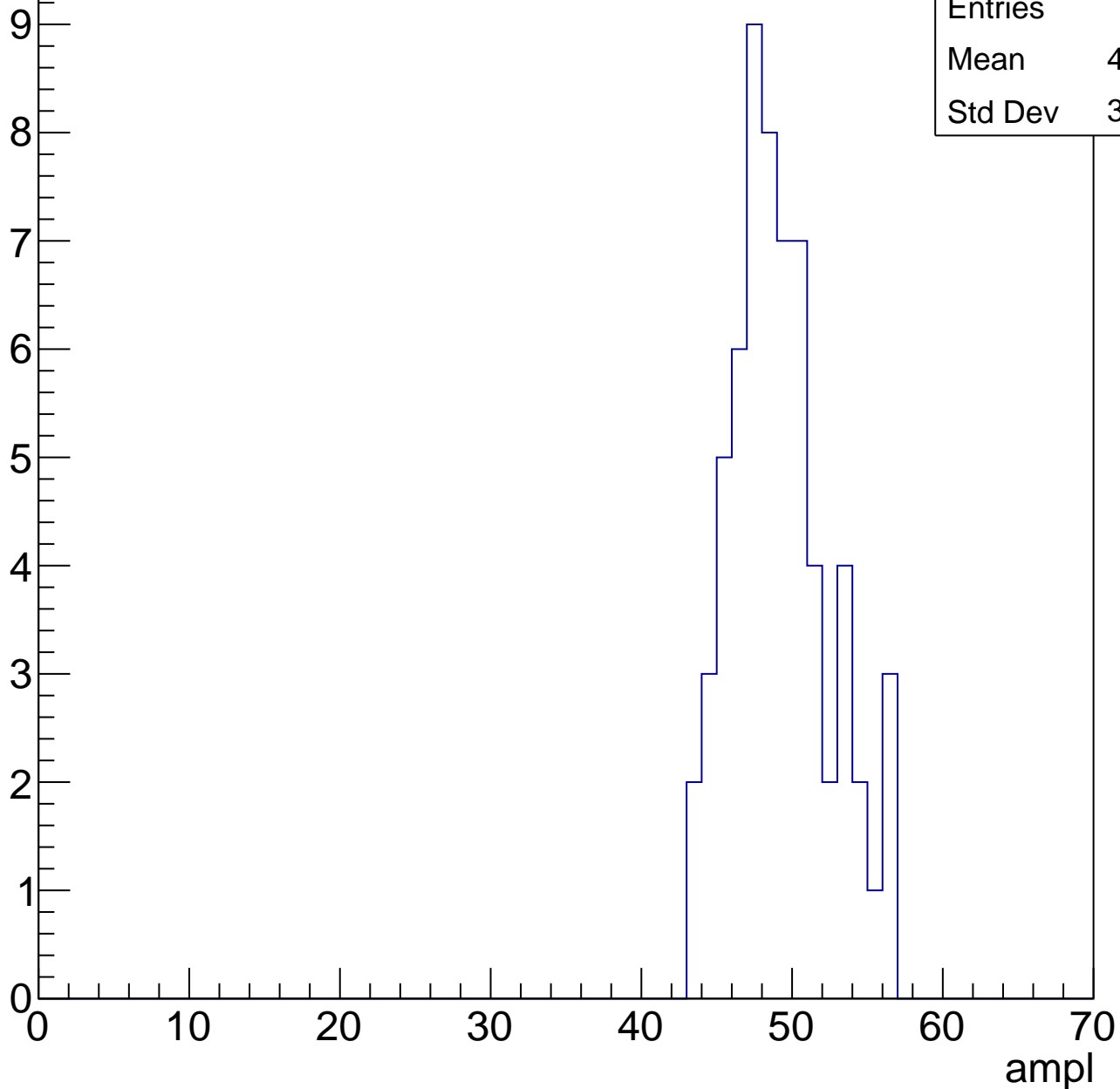
Gaus Width: 2.6676



B1L100S, U11-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



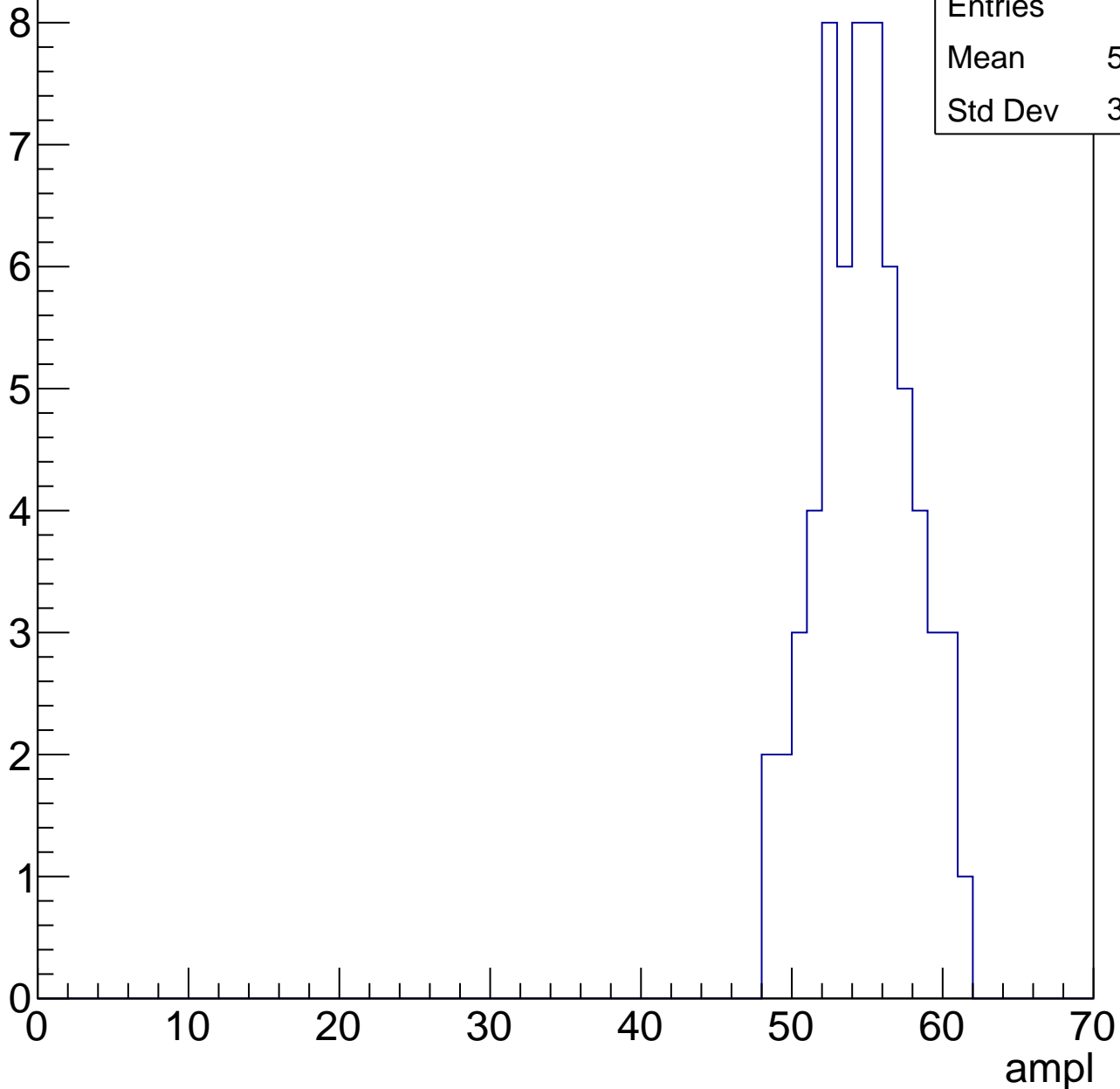
Entries	63
Mean	48.73
Std Dev	3.233

B1L100S, U11-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	54.37
Std Dev	3.108

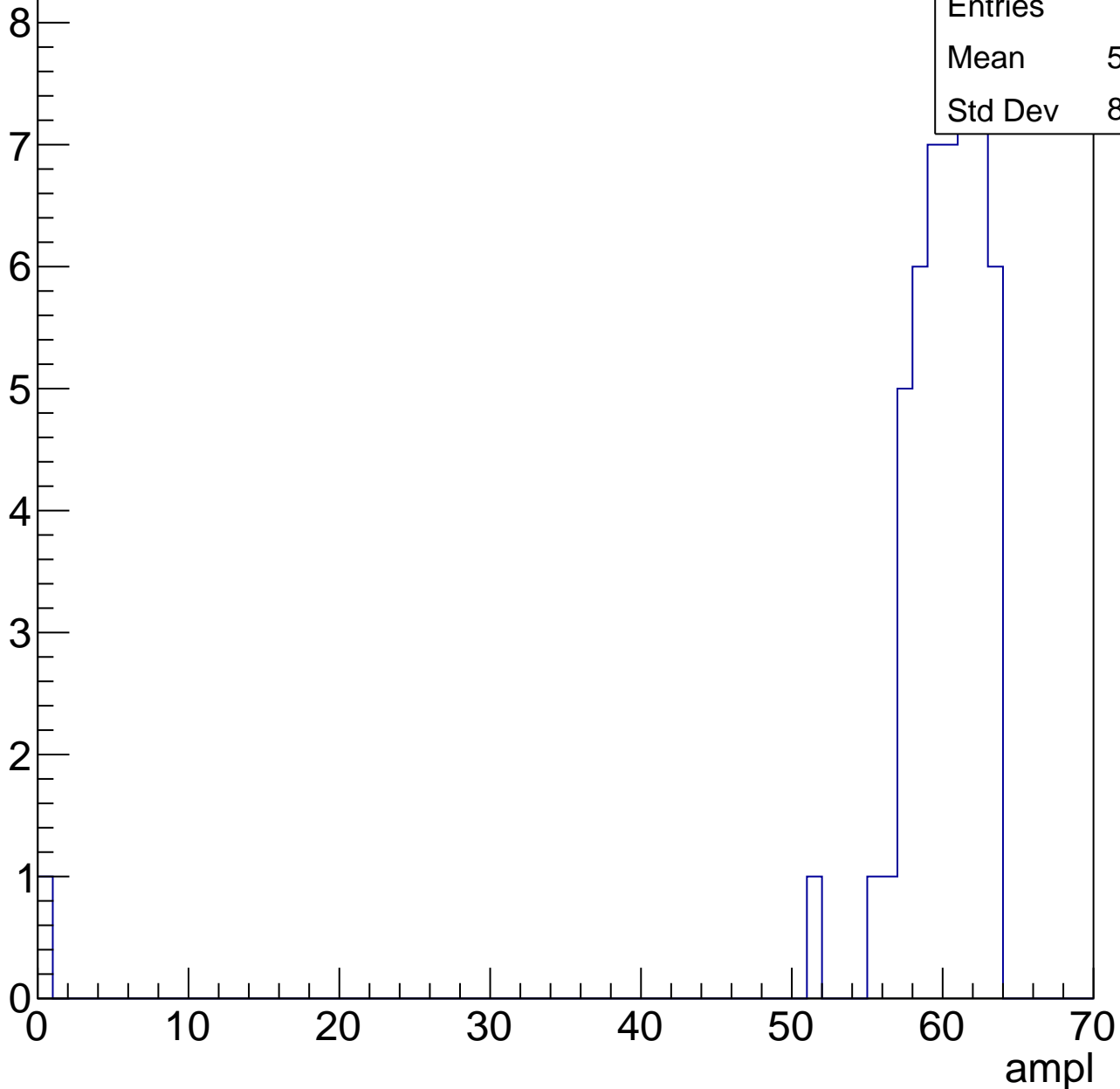


B1L100S, U11-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

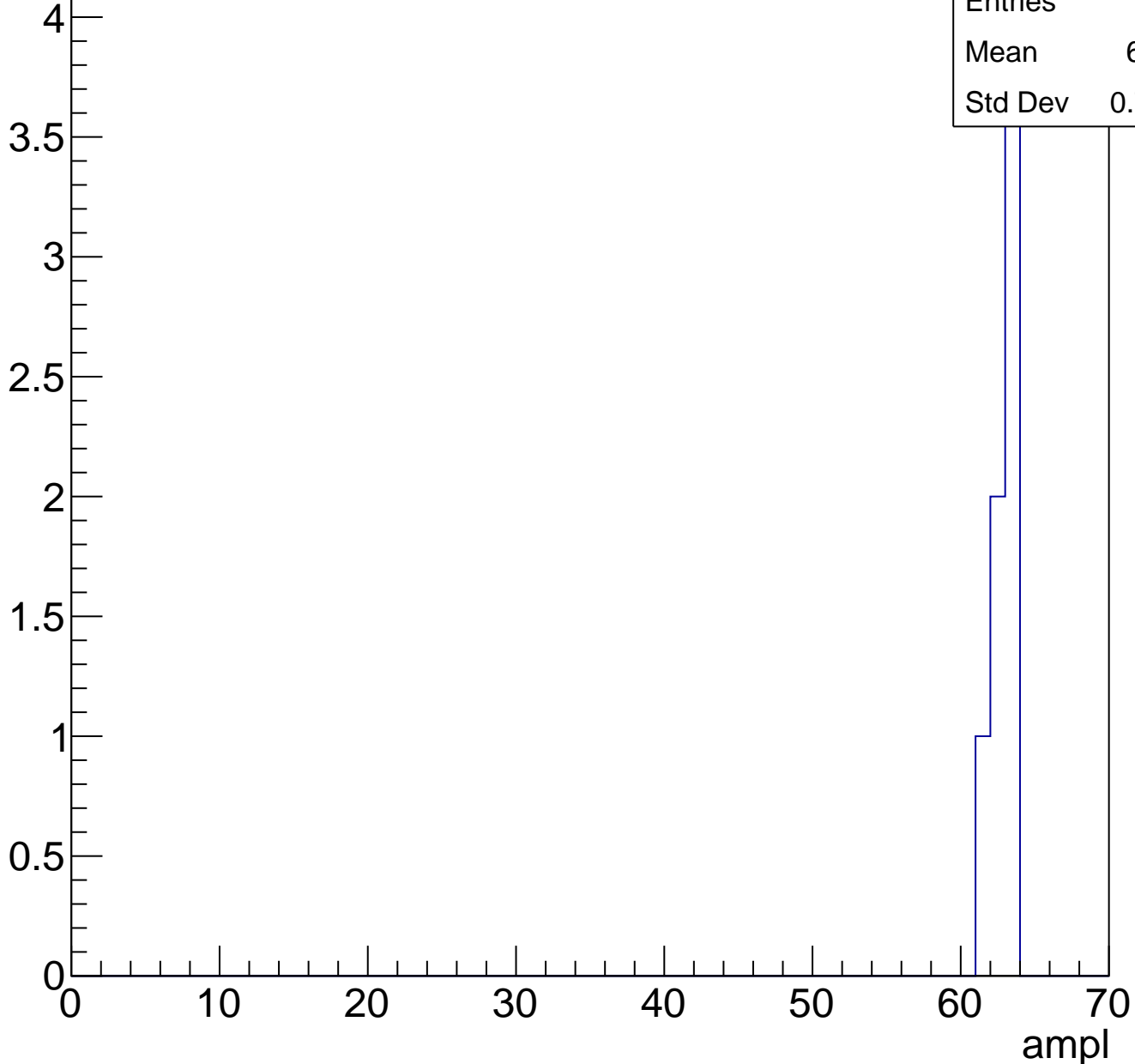
Entries	51
Mean	58.63
Std Dev	8.627



B1L100S, U11-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

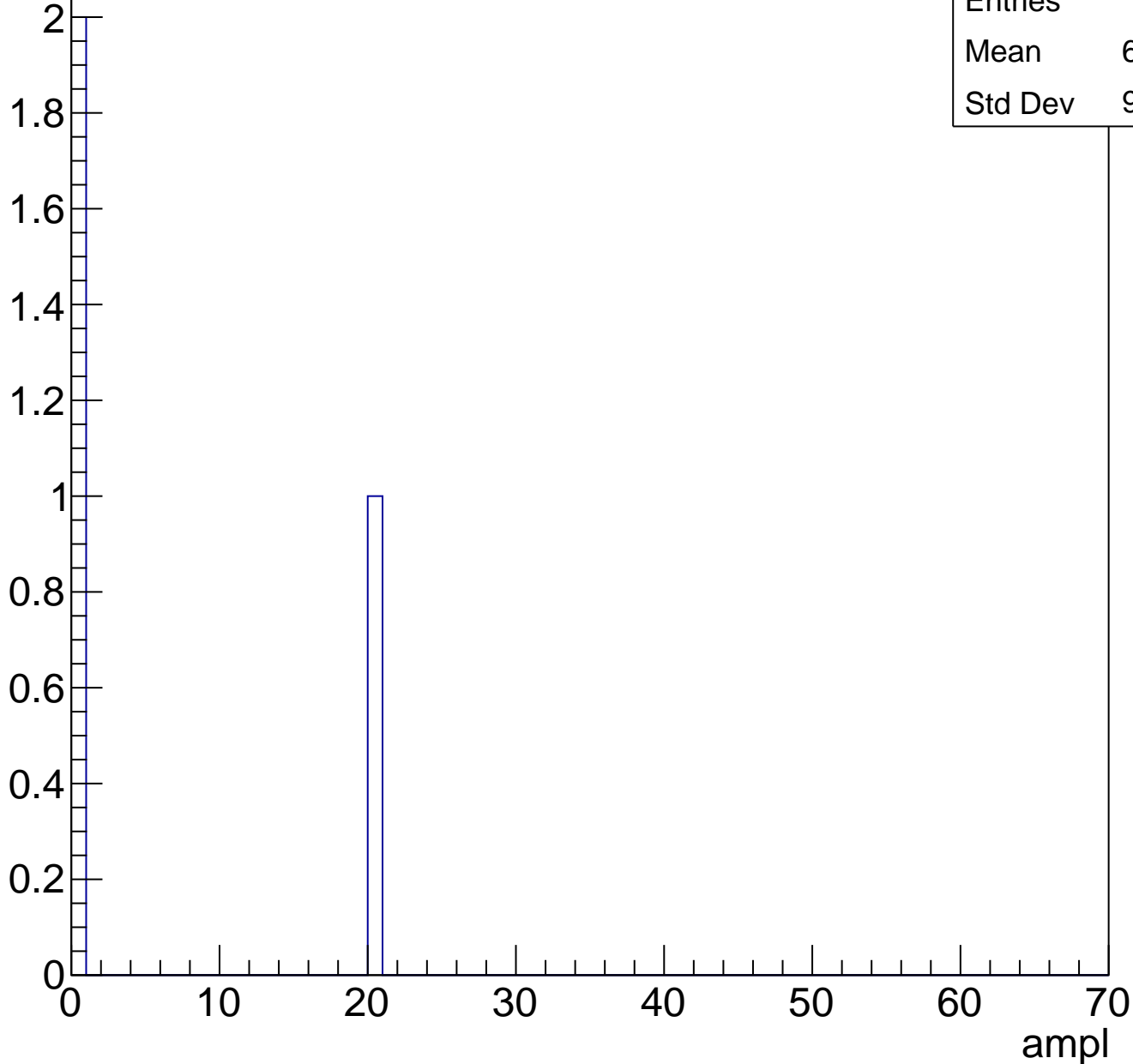
Entry



B1L100S, U11-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L100S, U11-ch55, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	29.69
Std Dev	5.701

Gaus mean : 30.5288

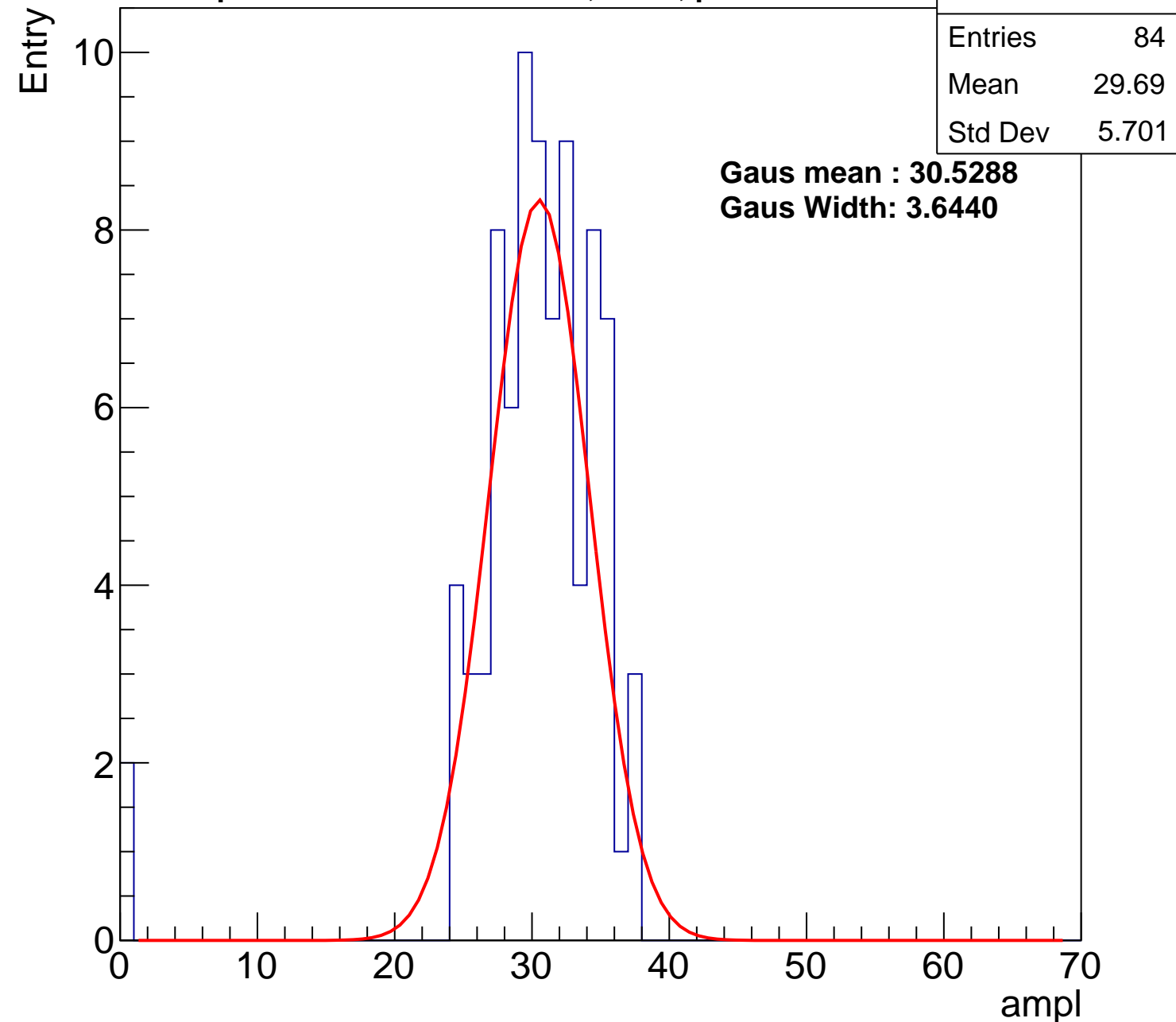
Gaus Width: 3.6440

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch55, adc1

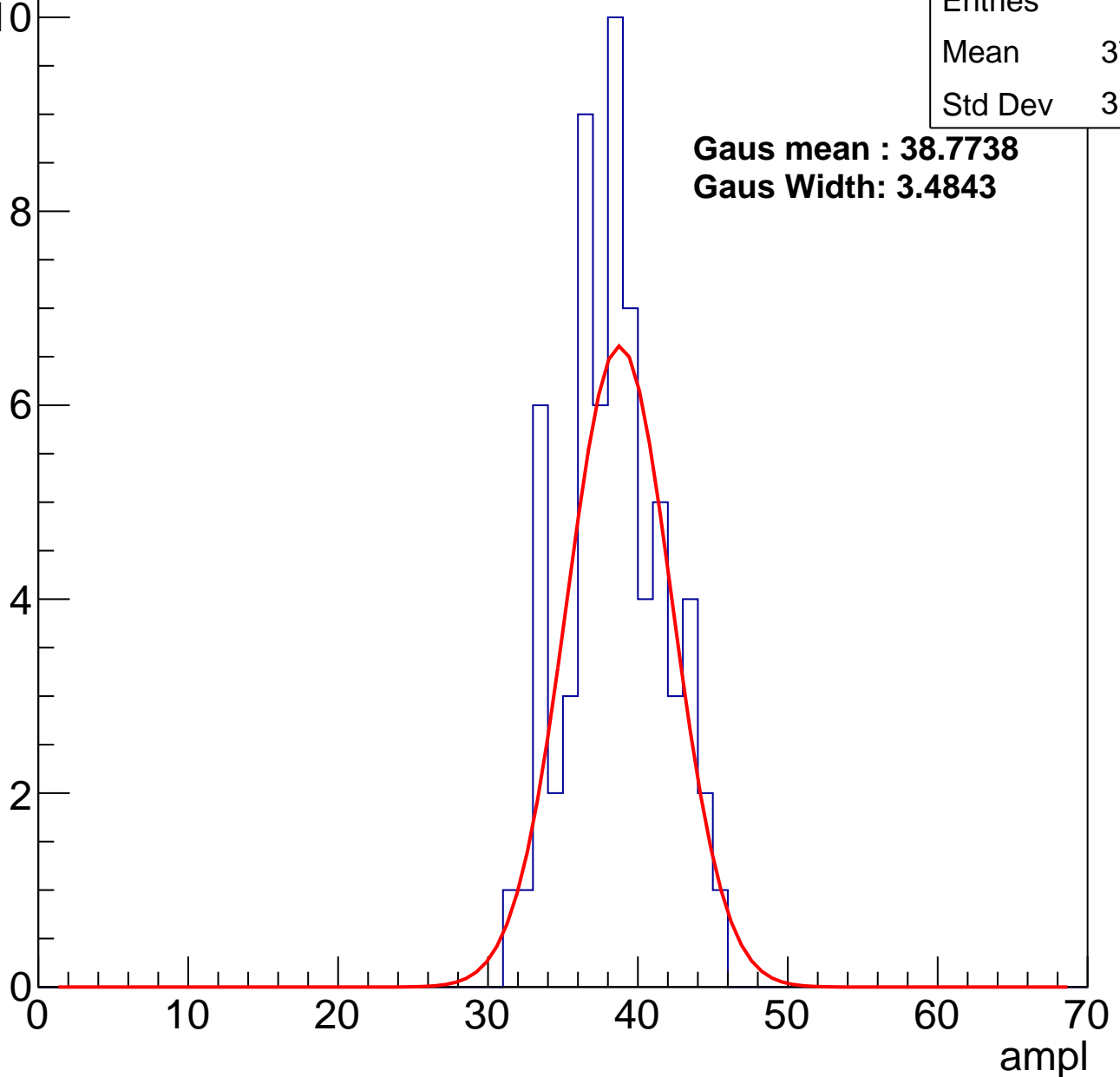
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	37.95
Std Dev	3.242

Gaus mean : 38.7738

Gaus Width: 3.4843



B1L100S, U11-ch55, adc2

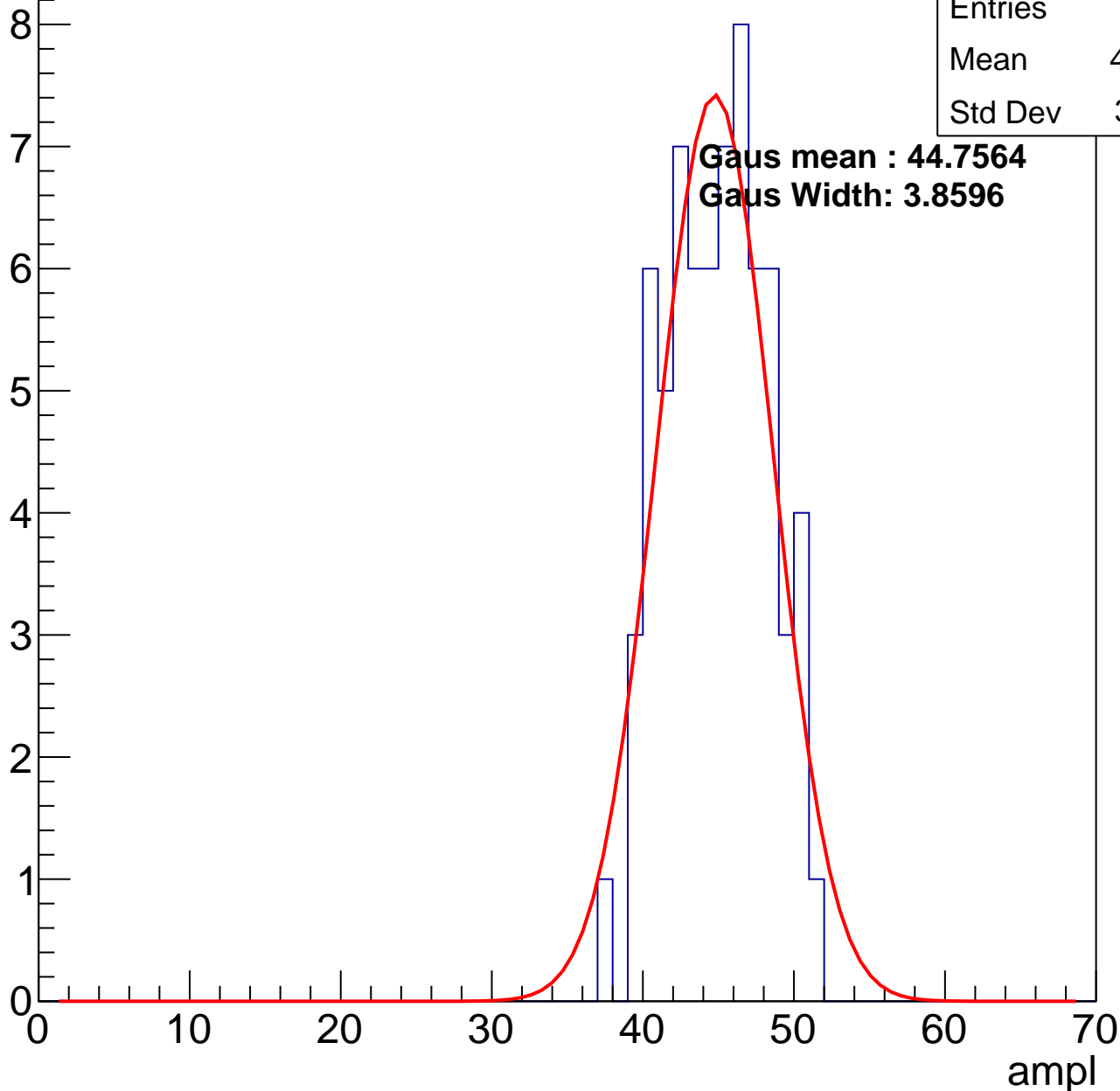
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.43
Std Dev	3.281

Gaus mean : 44.7564

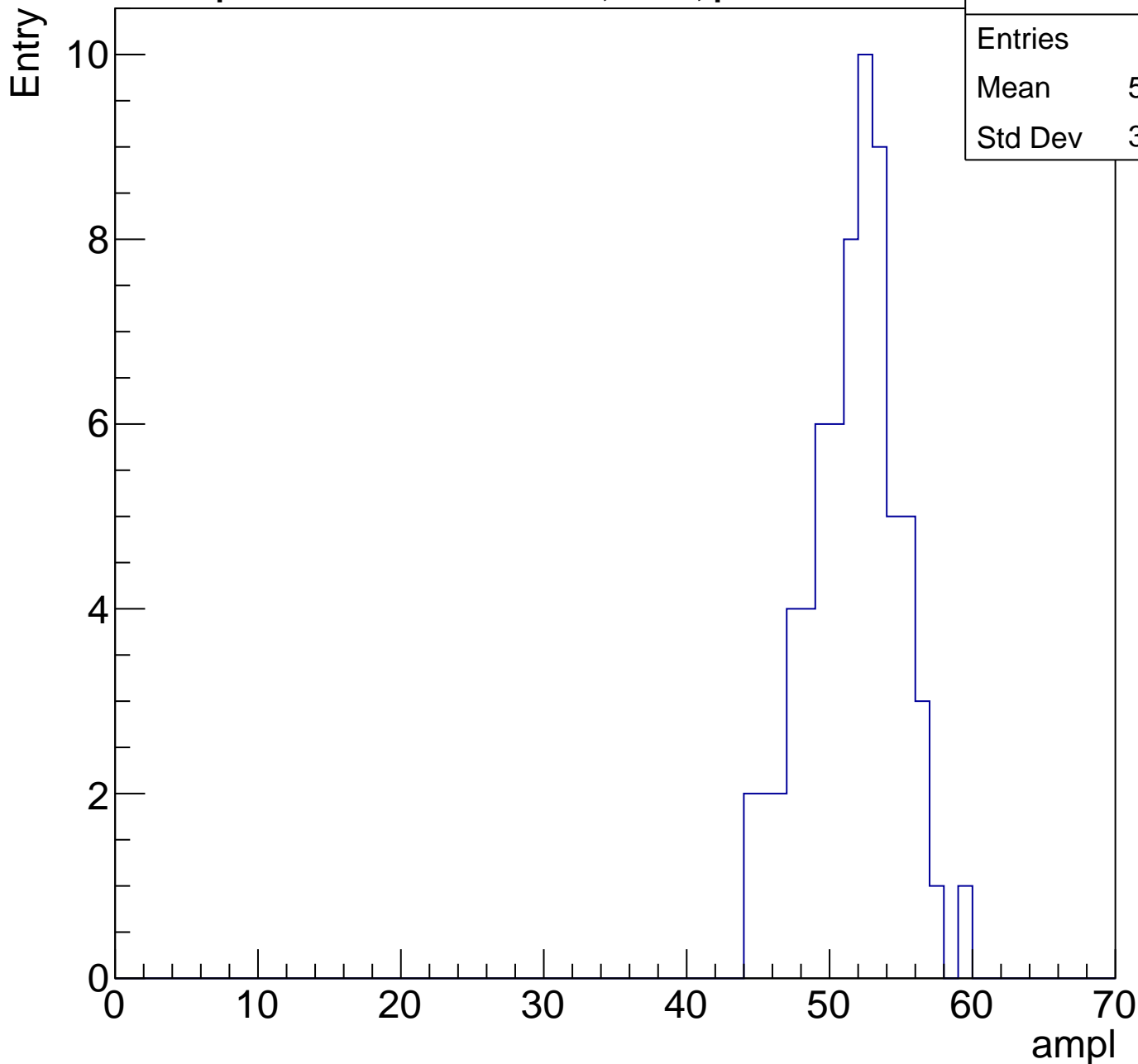
Gaus Width: 3.8596



B1L100S, U11-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	51.15
Std Dev	3.196

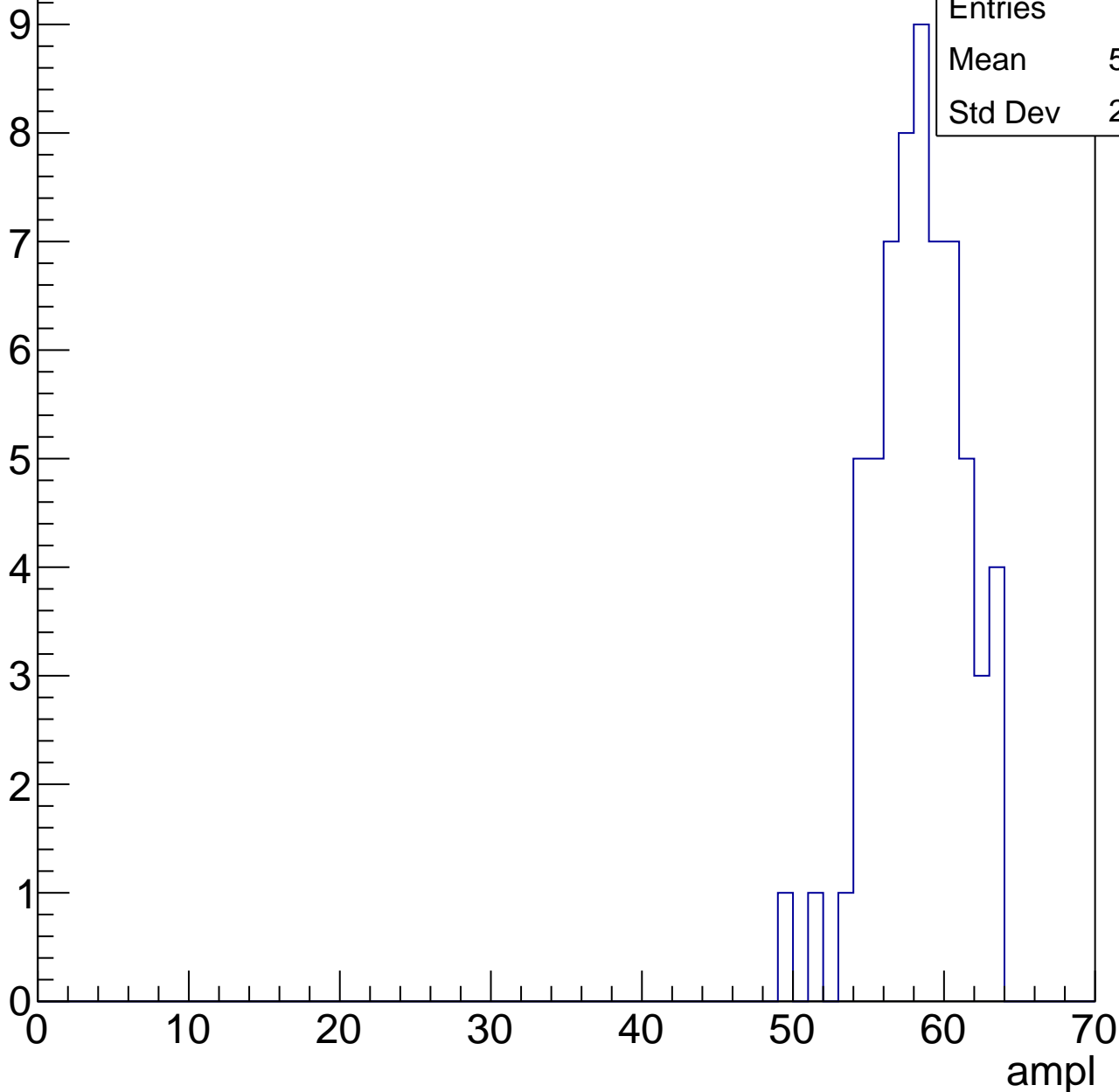


B1L100S, U11-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	57.84
Std Dev	2.934

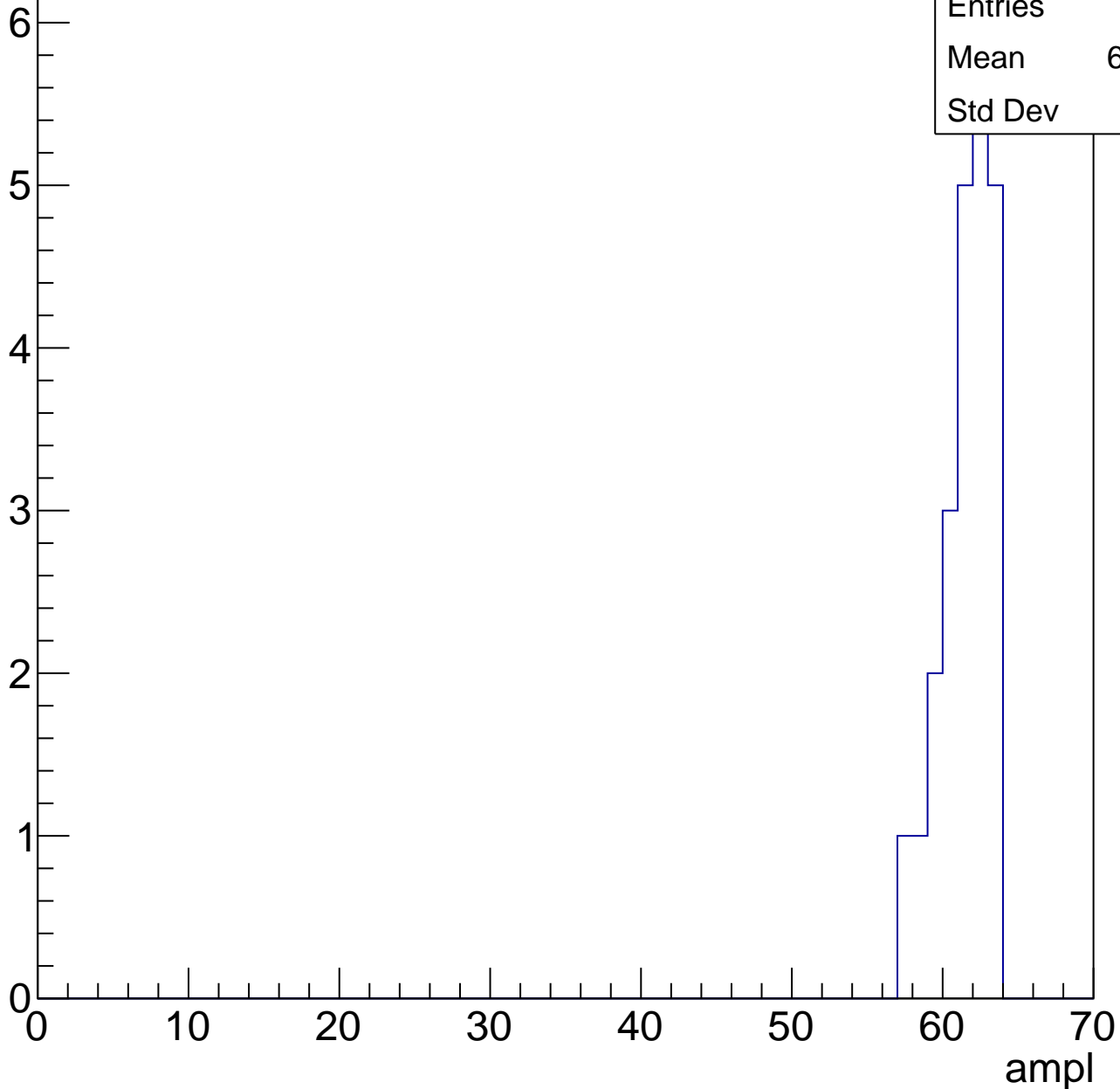


B1L100S, U11-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	61.09
Std Dev	1.64



B1L100S, U11-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L100S, U11-ch56, adc0

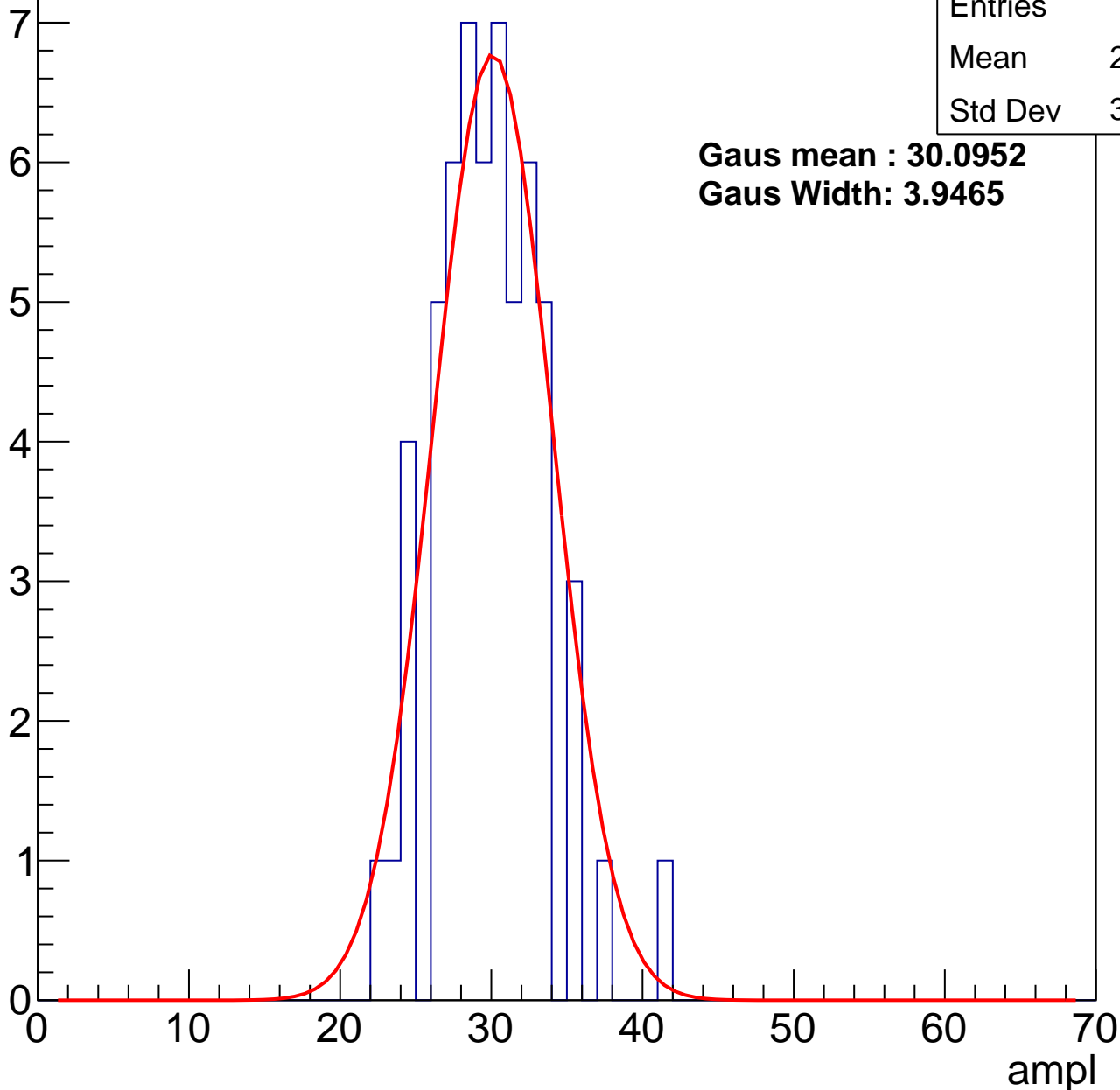
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	29.45
Std Dev	3.539

Gaus mean : 30.0952

Gaus Width: 3.9465



B1L100S, U11-ch56, adc1

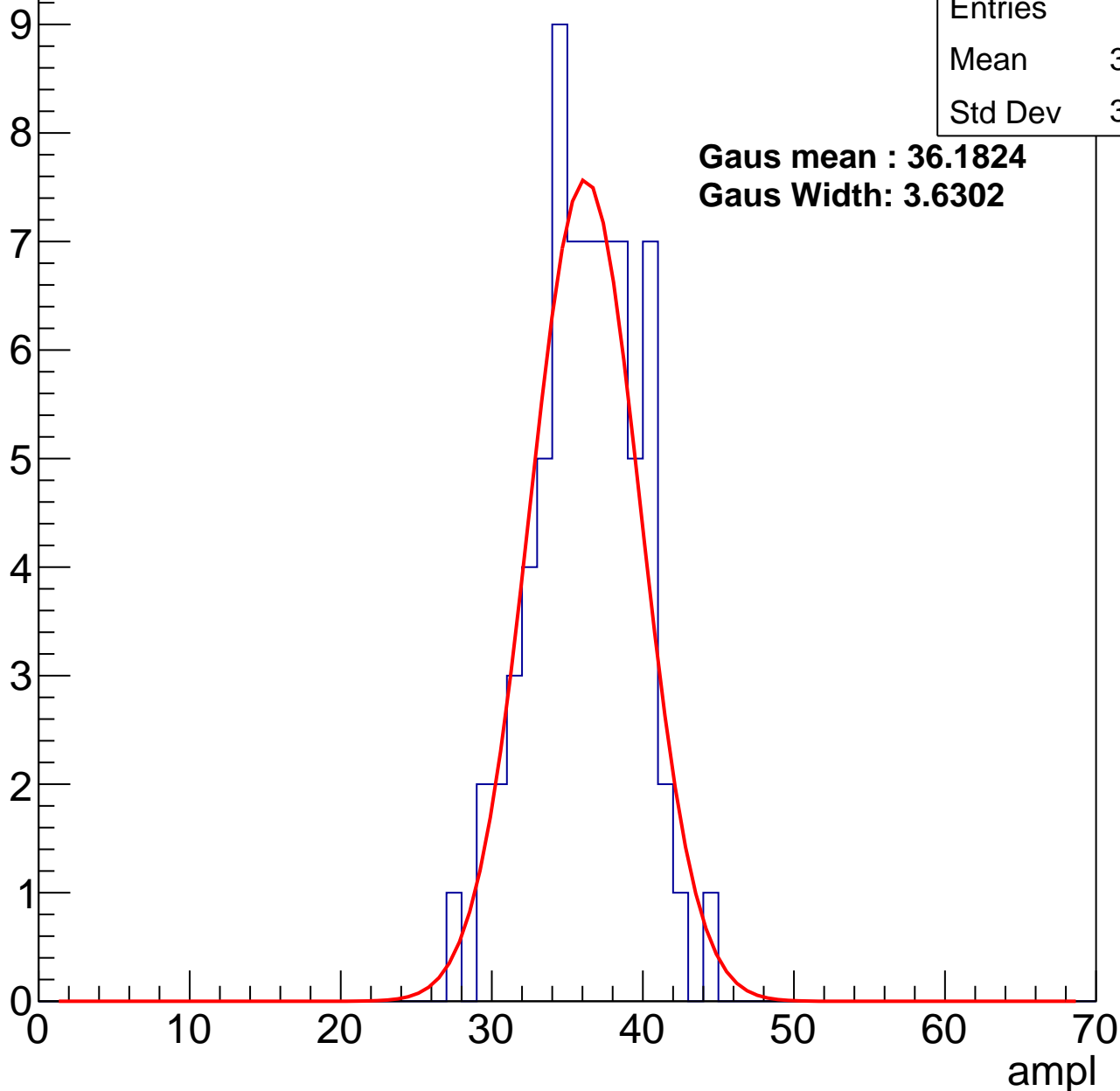
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.74
Std Dev	3.413

Gaus mean : 36.1824

Gaus Width: 3.6302



B1L100S, U11-ch56, adc2

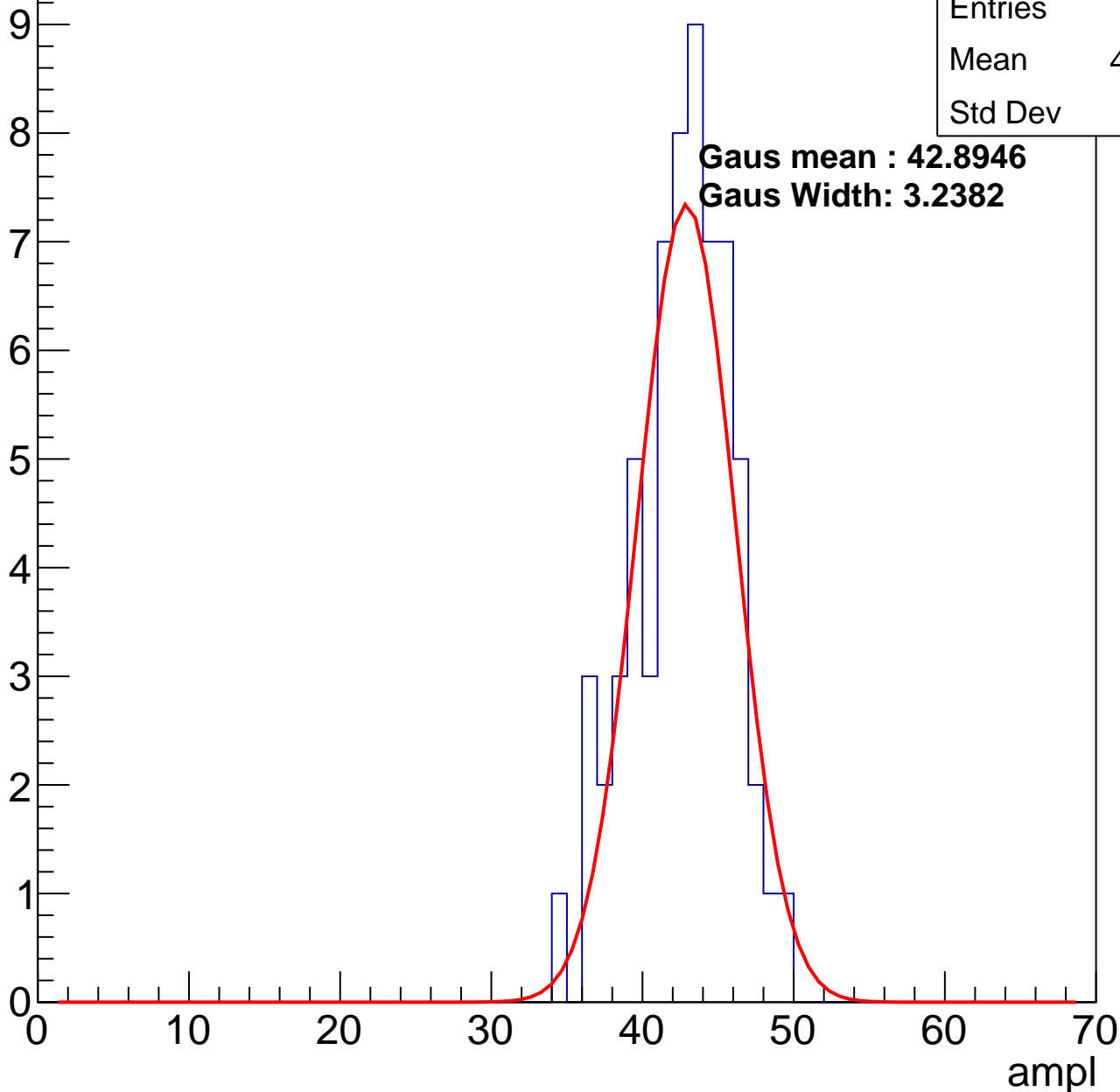
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.17
Std Dev	3.18

Gaus mean : 42.8946

Gaus Width: 3.2382

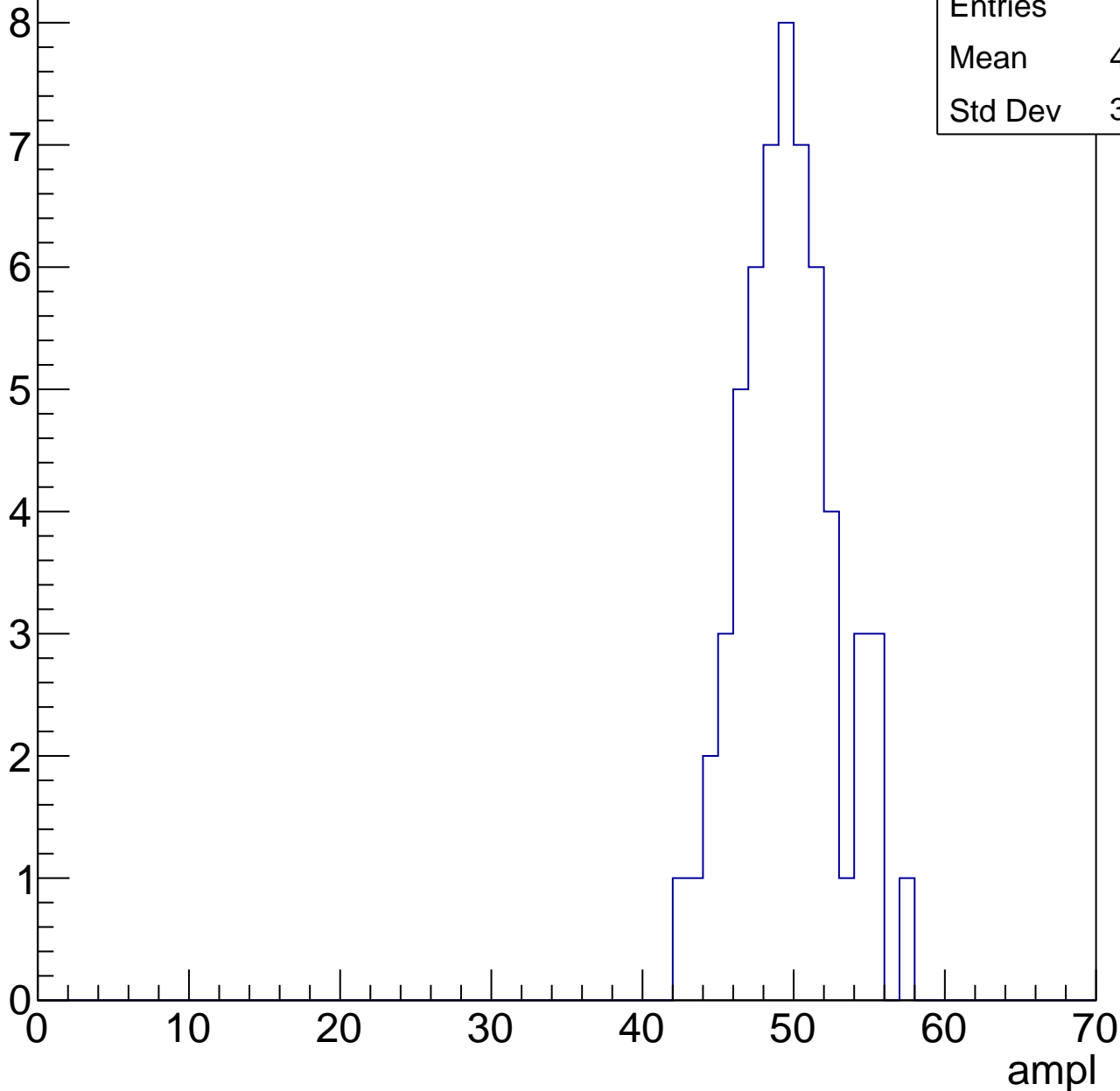


B1L100S, U11-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.12
Std Dev	3.184

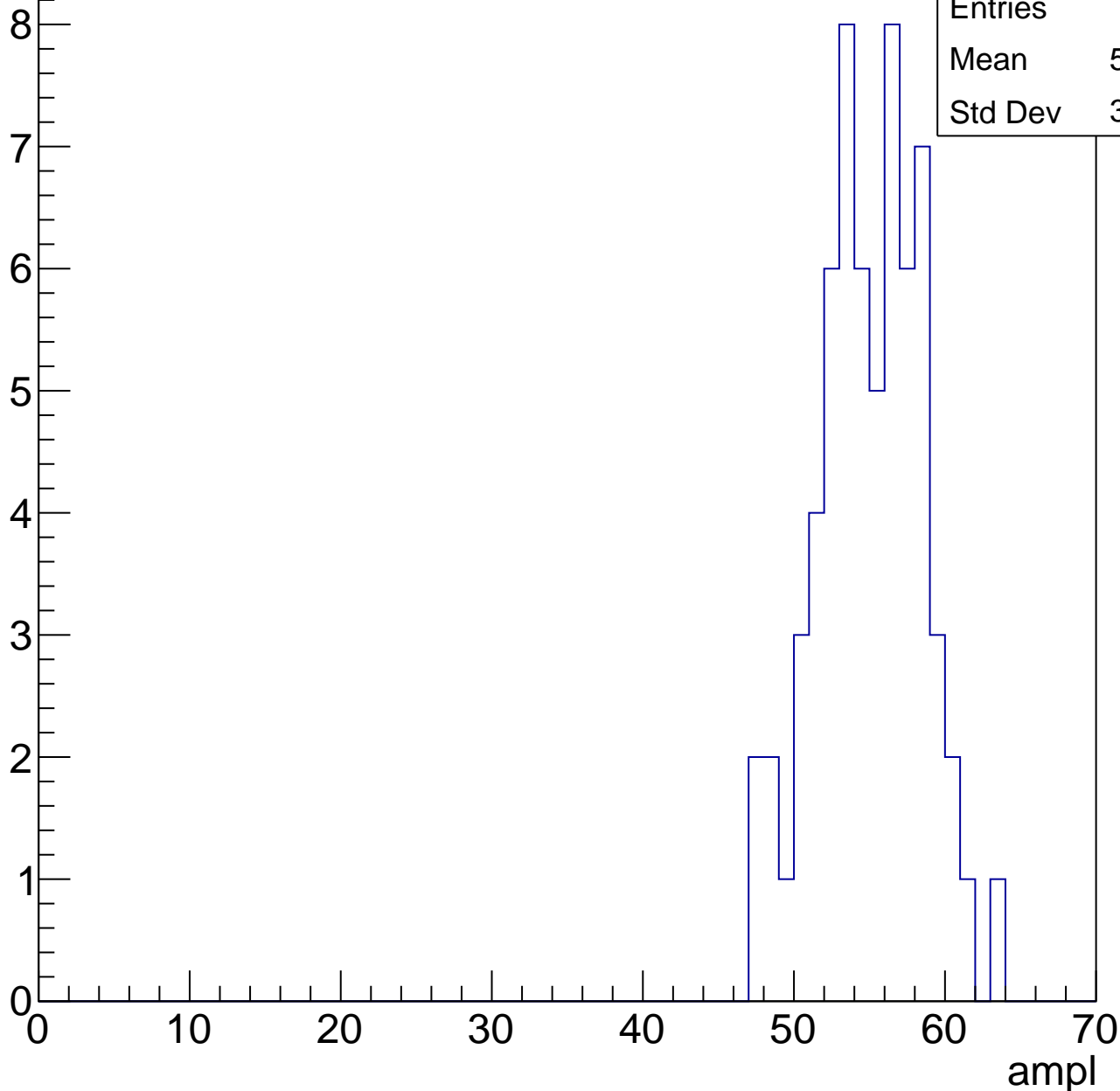


B1L100S, U11-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

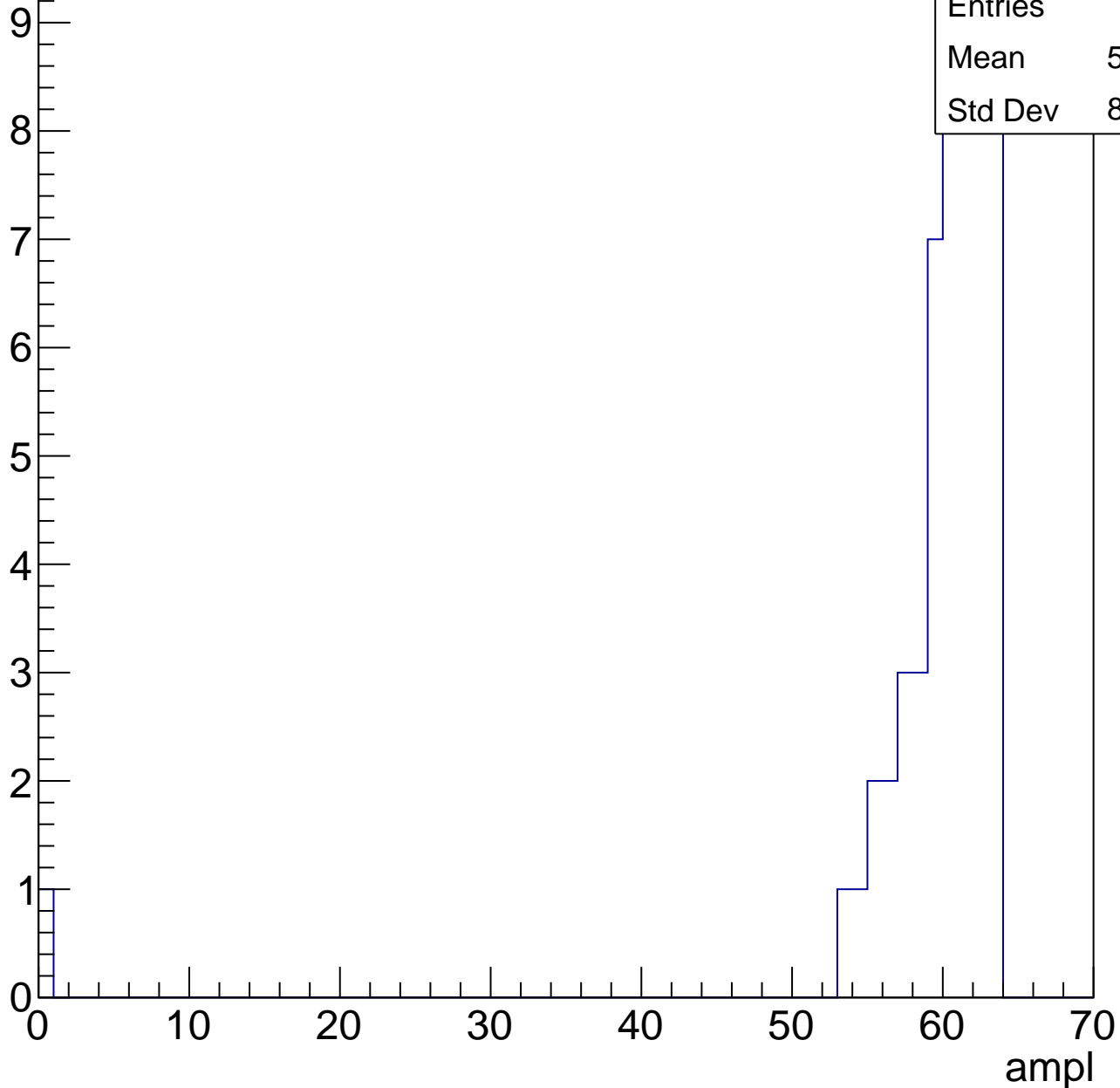
Entries	65
Mean	54.54
Std Dev	3.447



B1L100S, U11-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch57, adc0

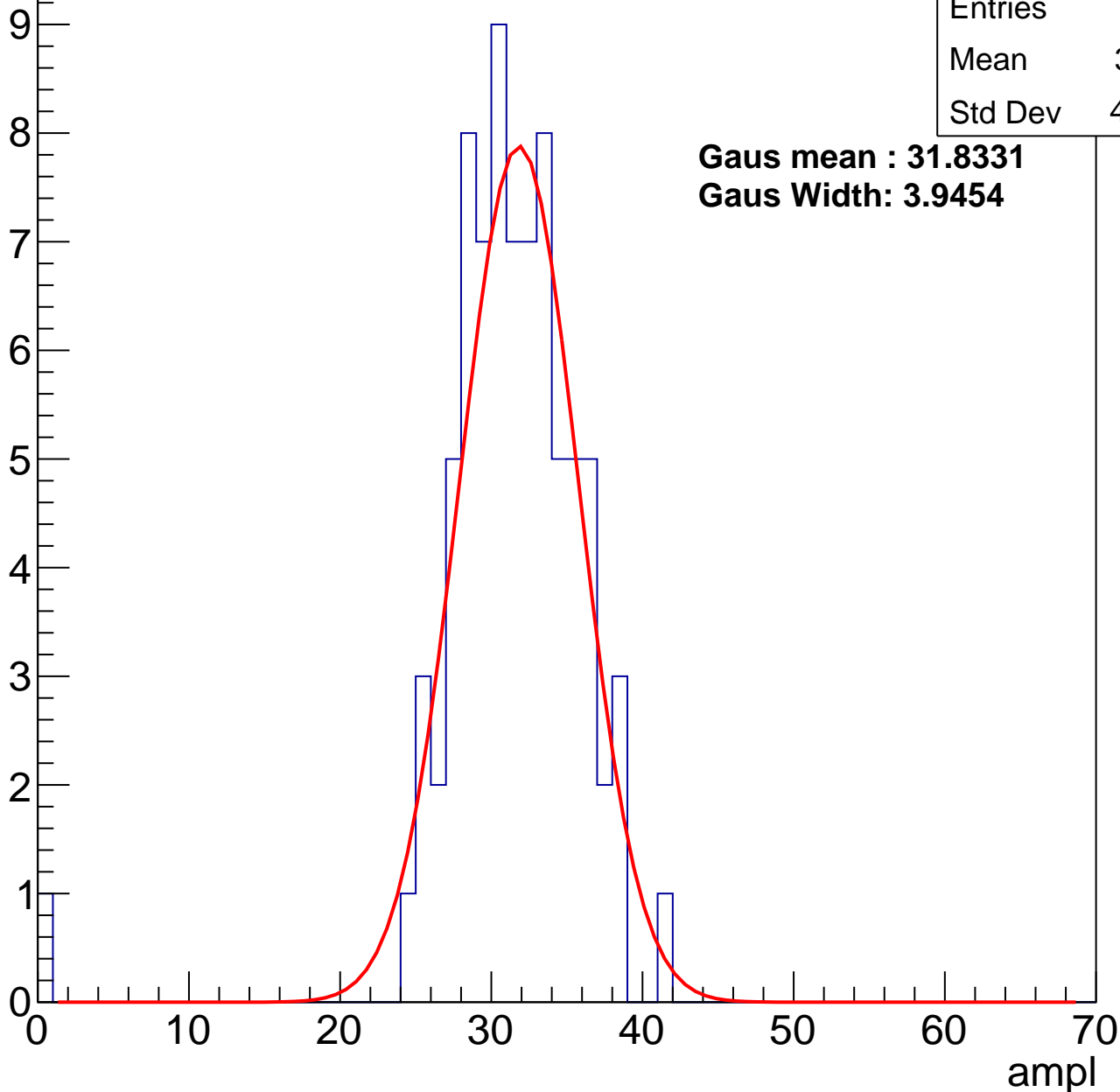
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	30.91
Std Dev	4.987

Gaus mean : 31.8331

Gaus Width: 3.9454



B1L100S, U11-ch57, adc1

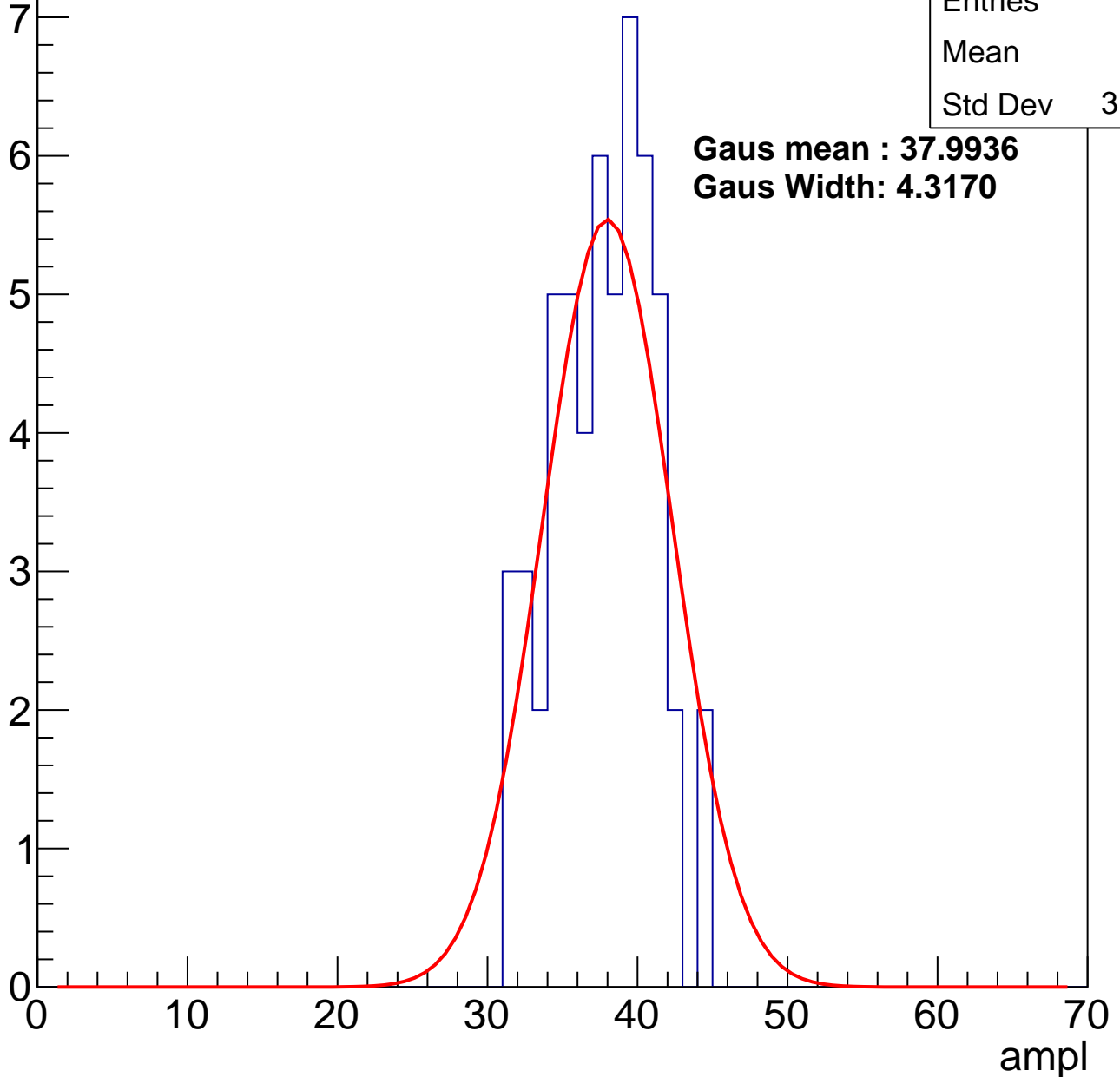
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	37.2
Std Dev	3.294

Gaus mean : 37.9936

Gaus Width: 4.3170



B1L100S, U11-ch57, adc2

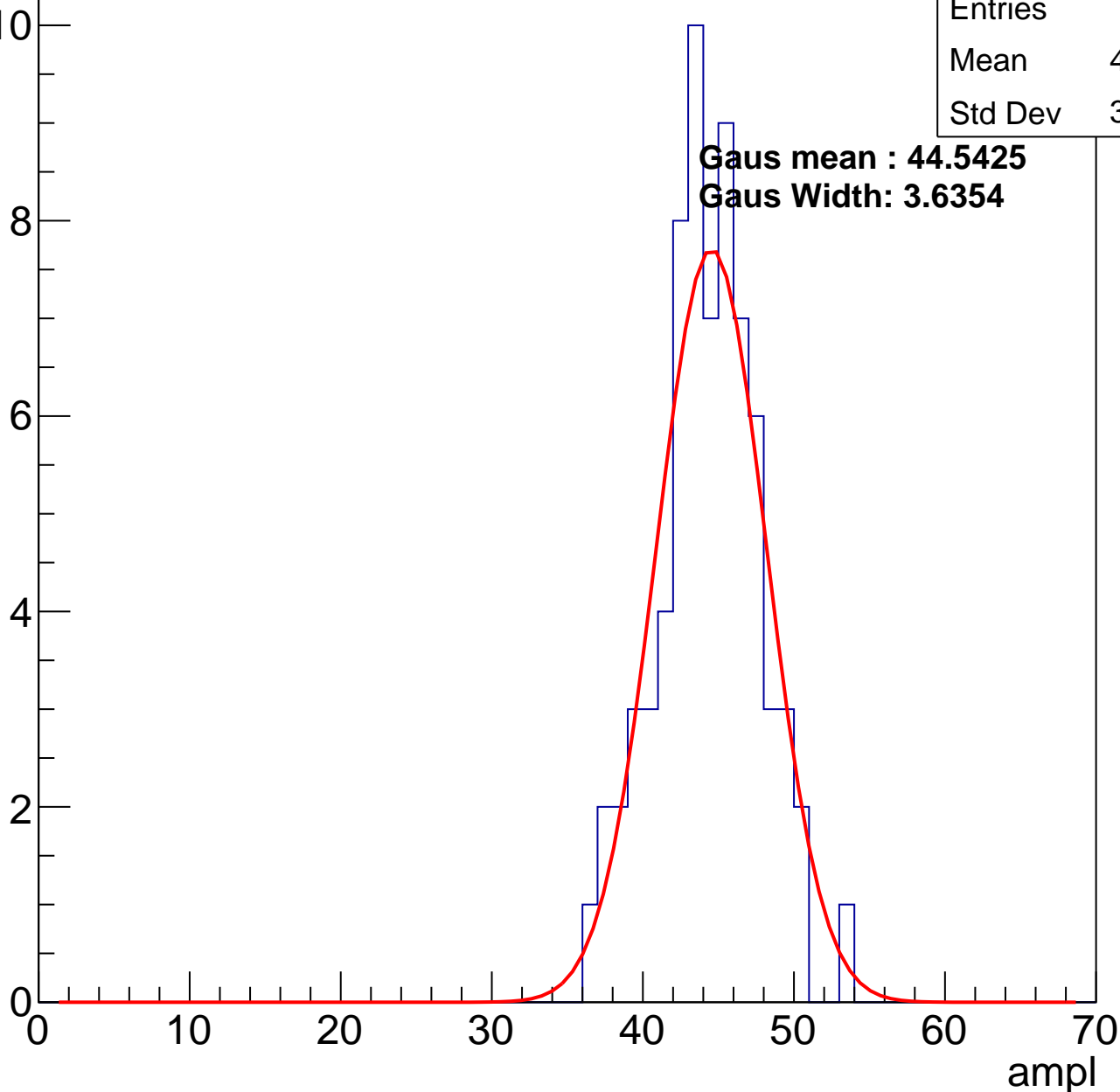
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.86
Std Dev	3.358

Gaus mean : 44.5425

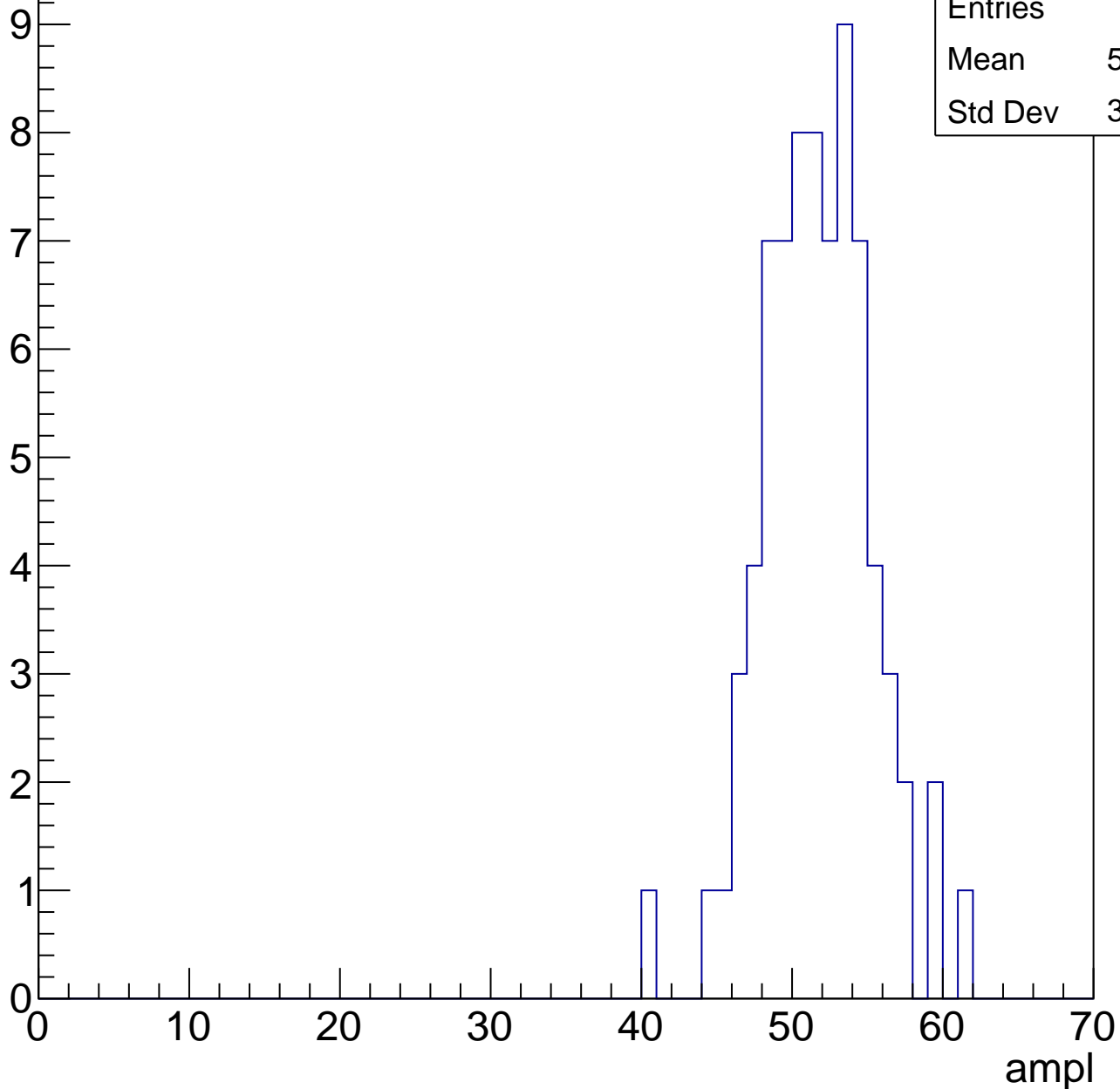
Gaus Width: 3.6354



B1L100S, U11-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



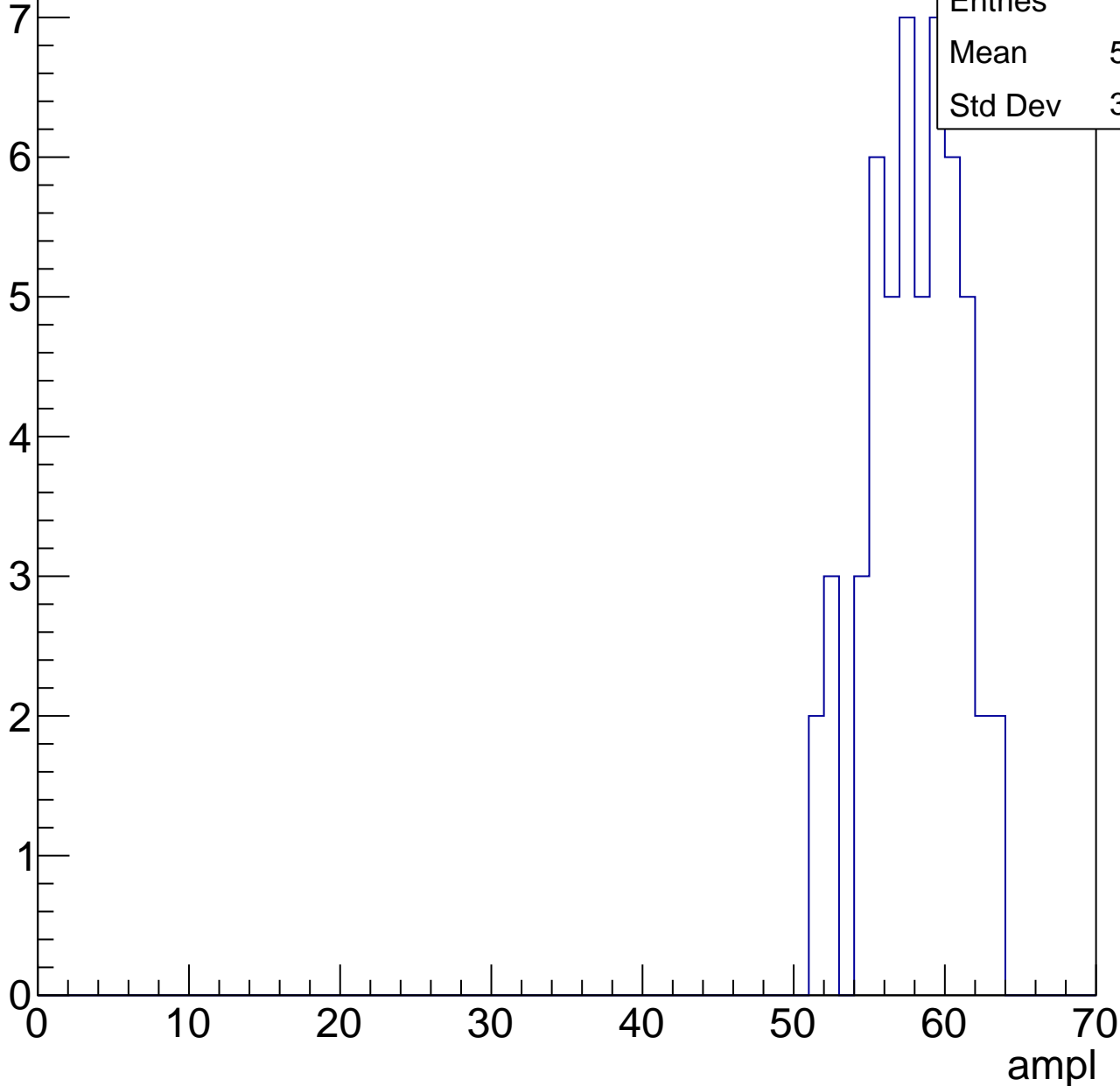
Entries	75
Mean	51.23
Std Dev	3.624

B1L100S, U11-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	57.49
Std Dev	3.007

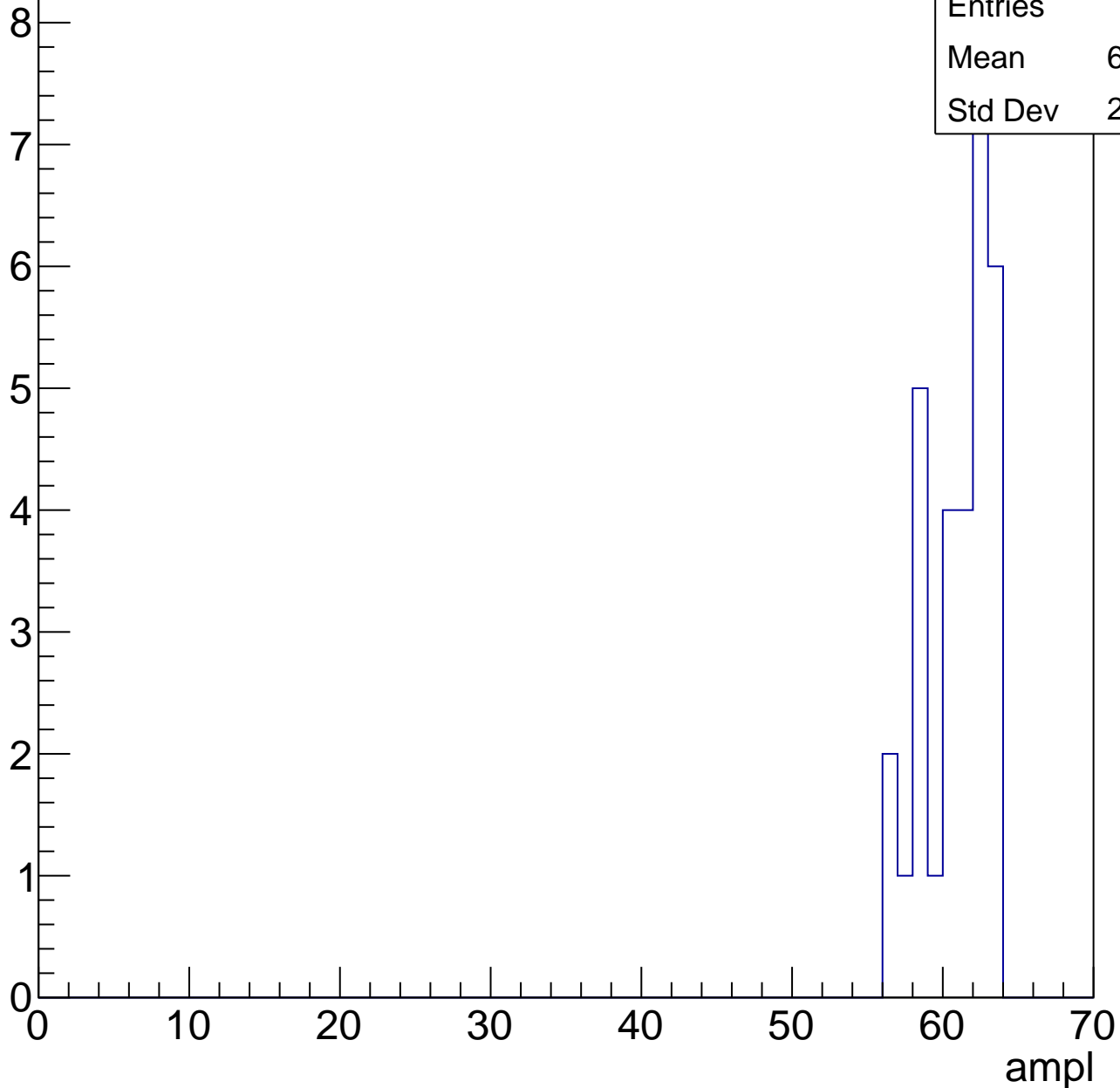


B1L100S, U11-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	60.52
Std Dev	2.153



B1L100S, U11-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch58, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	28.15
Std Dev	3.471

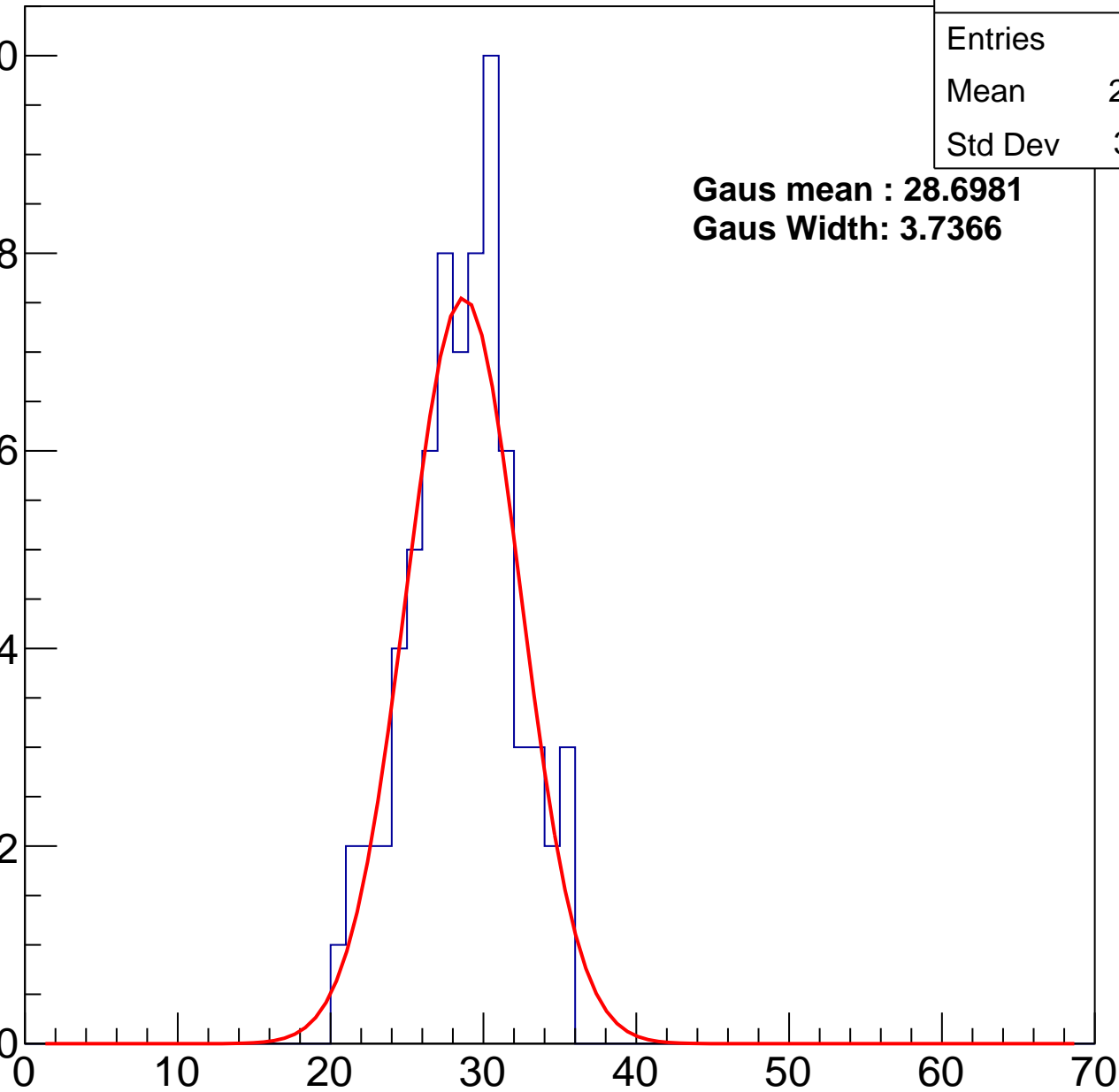
Gaus mean : 28.6981

Gaus Width: 3.7366

Entry

10
8
6
4
2
0

ampl



B1L100S, U11-ch58, adc1

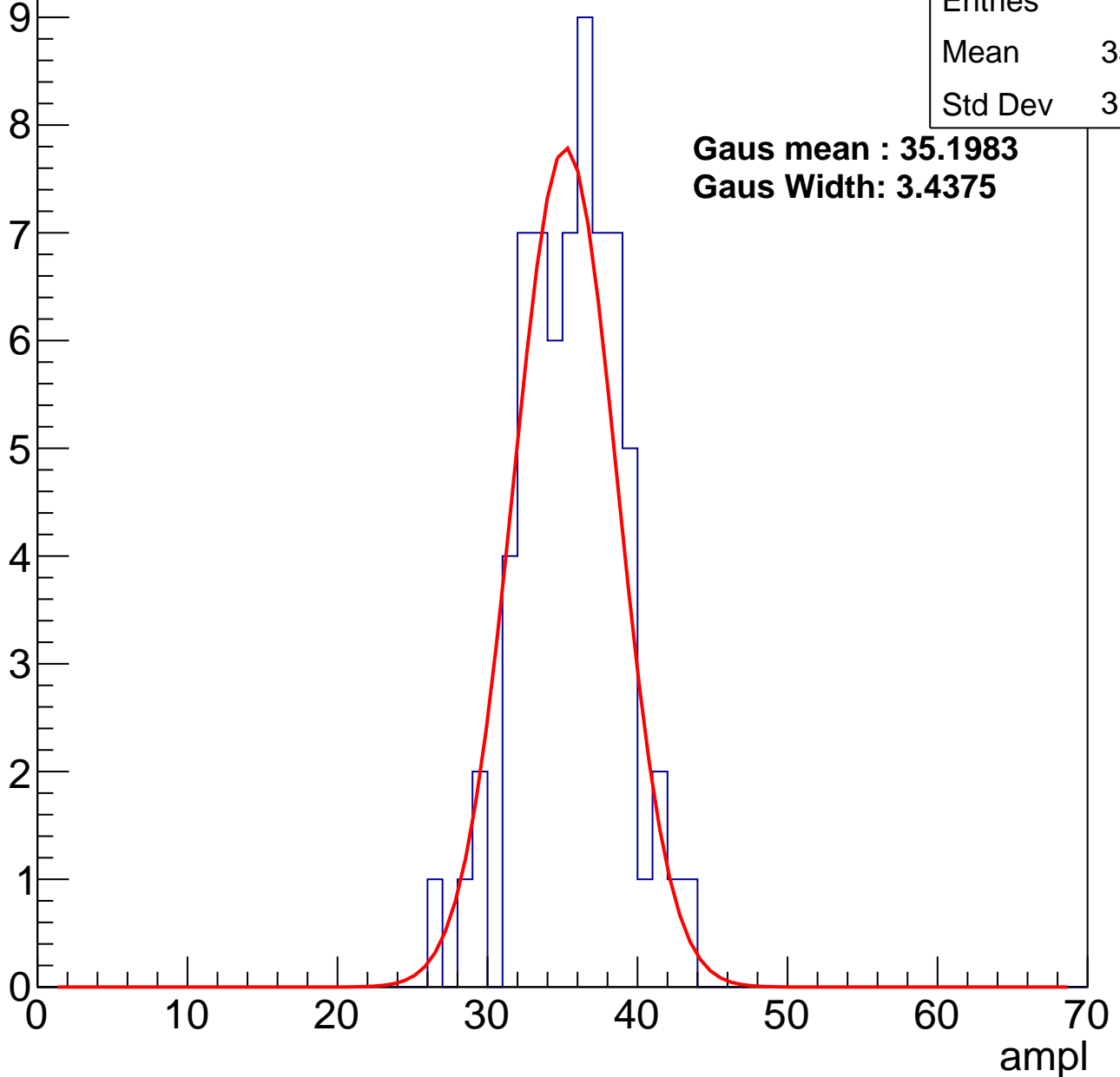
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.16
Std Dev	3.324

Gaus mean : 35.1983

Gaus Width: 3.4375



B1L100S, U11-ch58, adc2

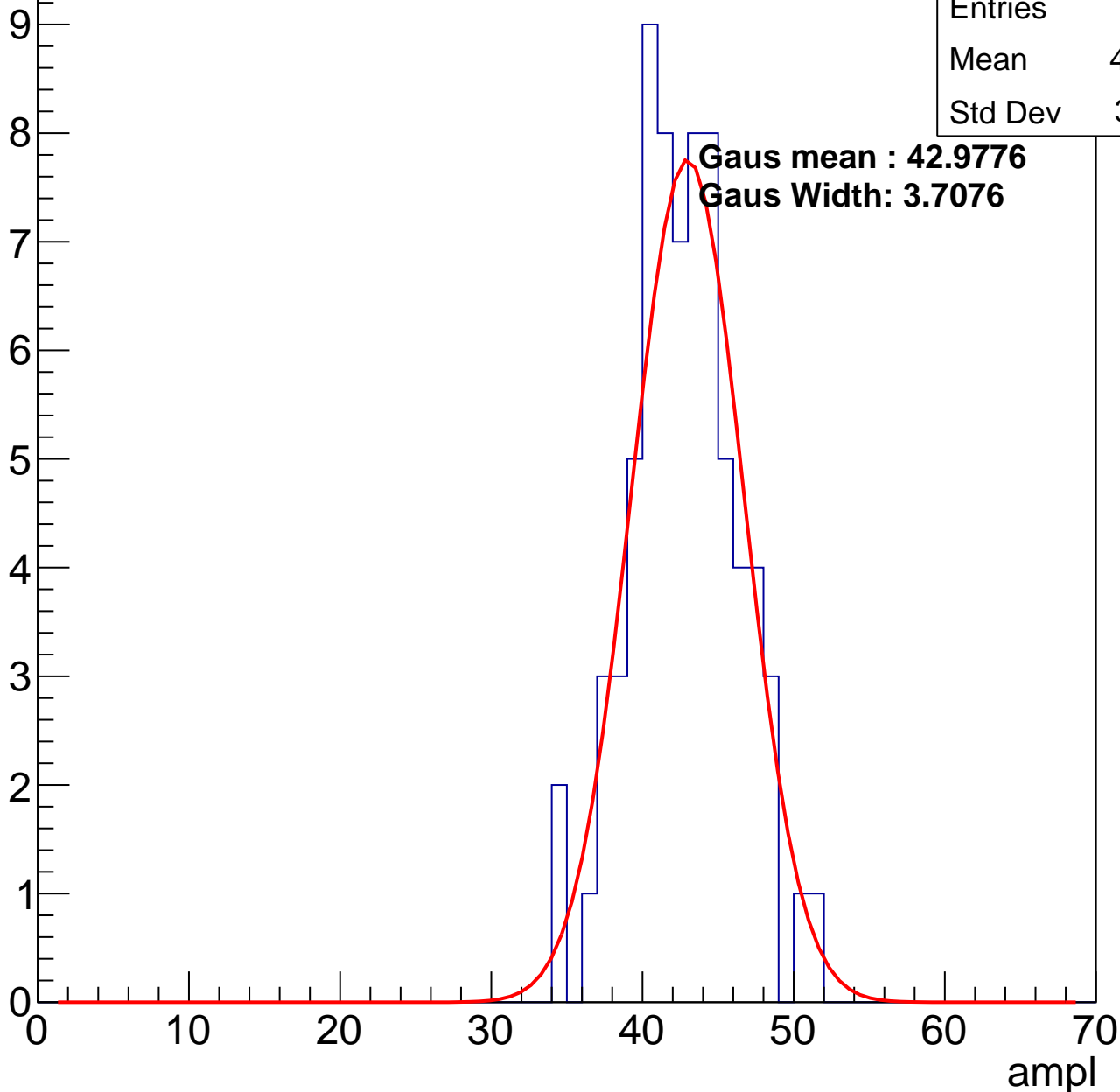
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	42.28
Std Dev	3.481

Gaus mean : 42.9776

Gaus Width: 3.7076

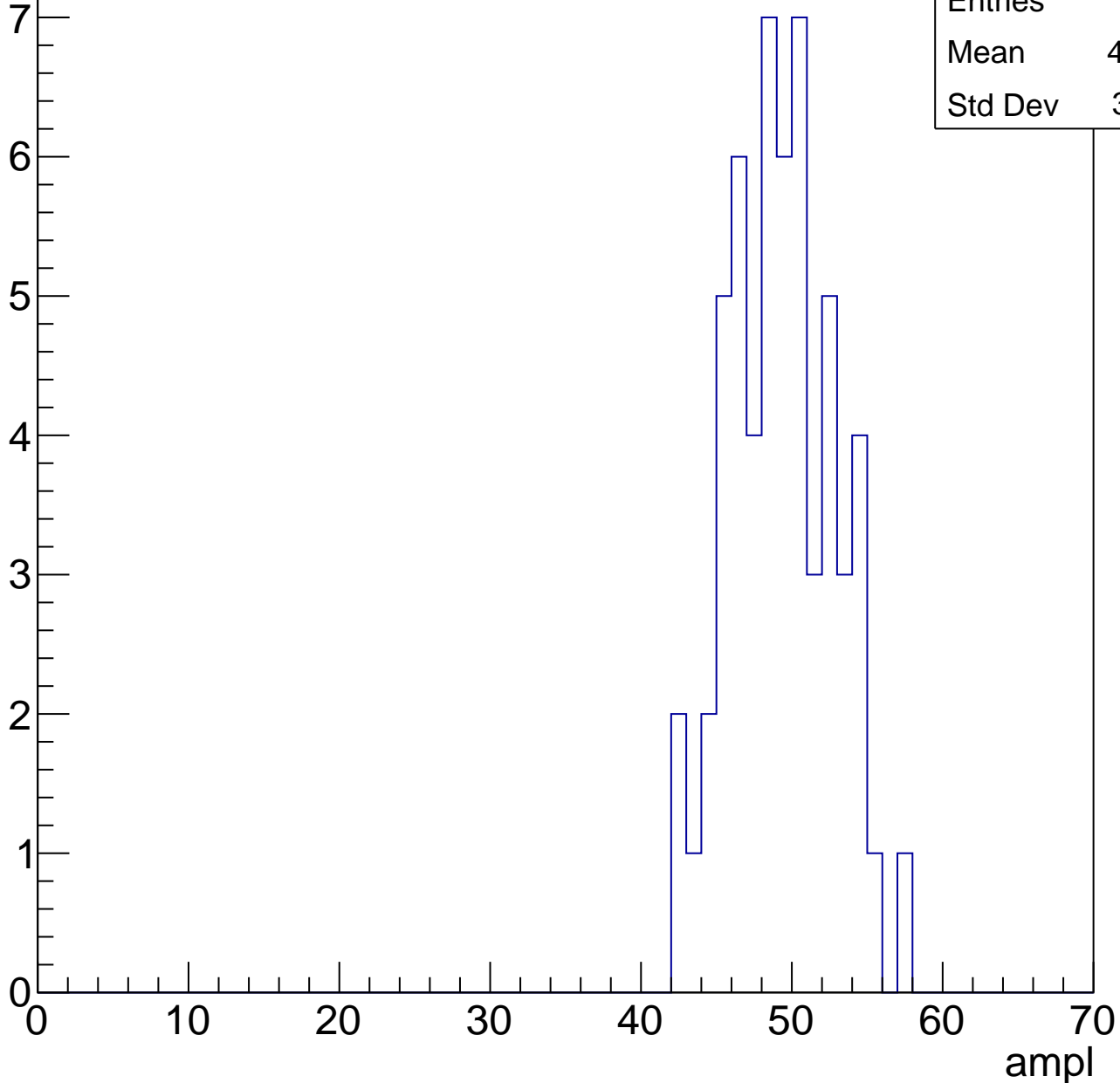


B1L100S, U11-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	48.84
Std Dev	3.381

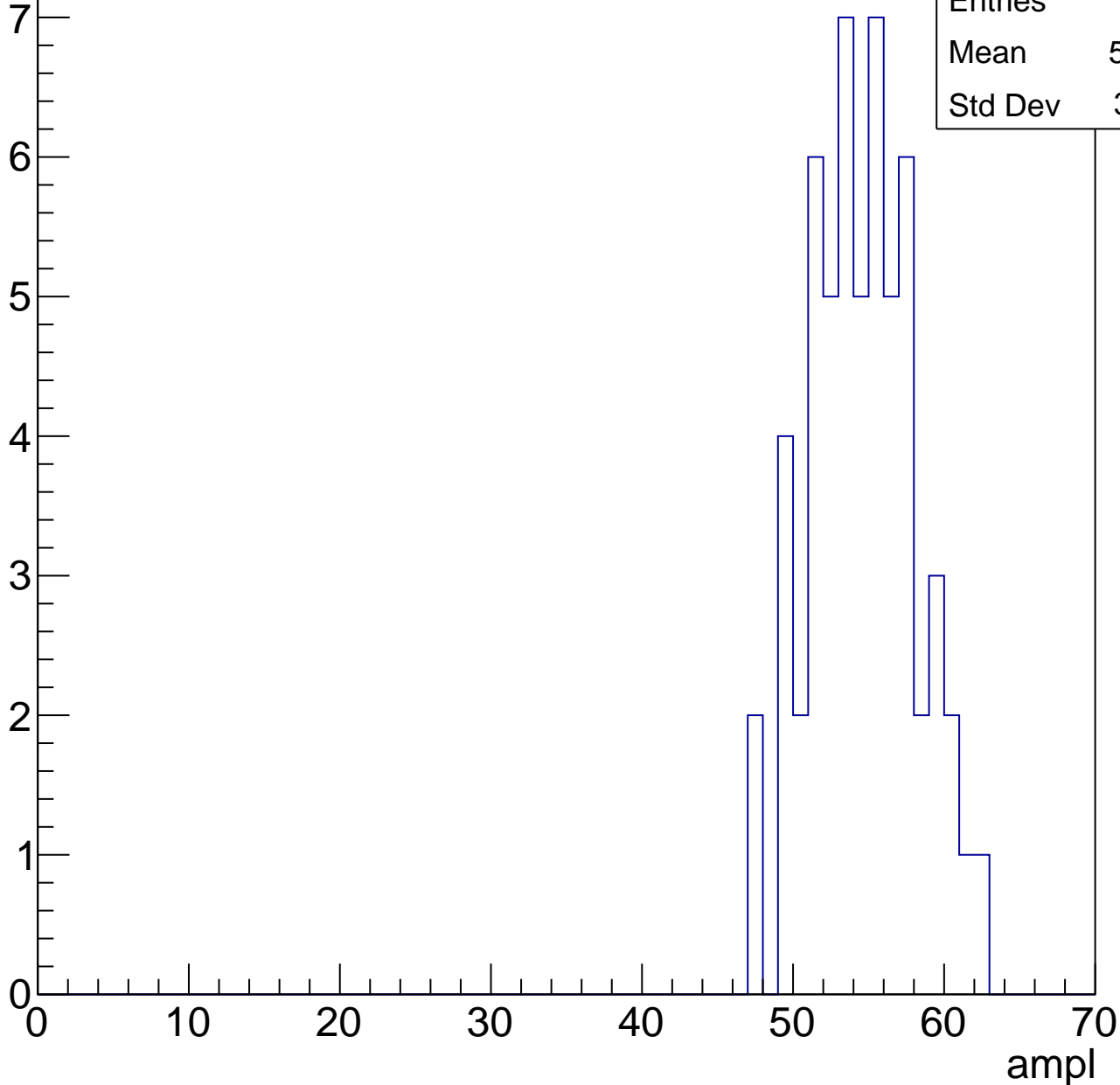


B1L100S, U11-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	54.14
Std Dev	3.431



B1L100S, U11-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	55
Mean	59.49
Std Dev	2.365

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L100S, U11-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch59, adc0

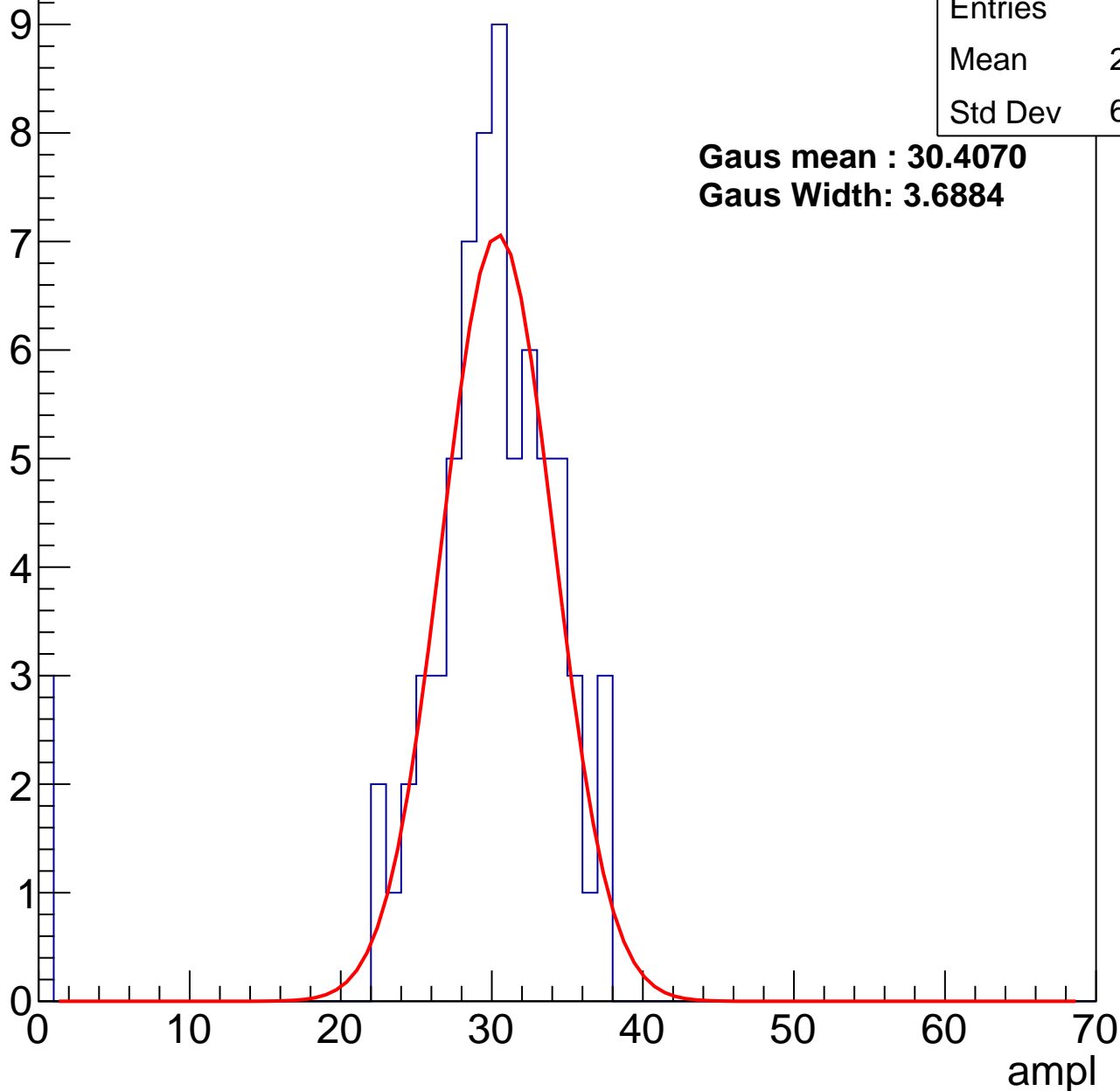
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	28.66
Std Dev	6.959

Gaus mean : 30.4070

Gaus Width: 3.6884



B1L100S, U11-ch59, adc1

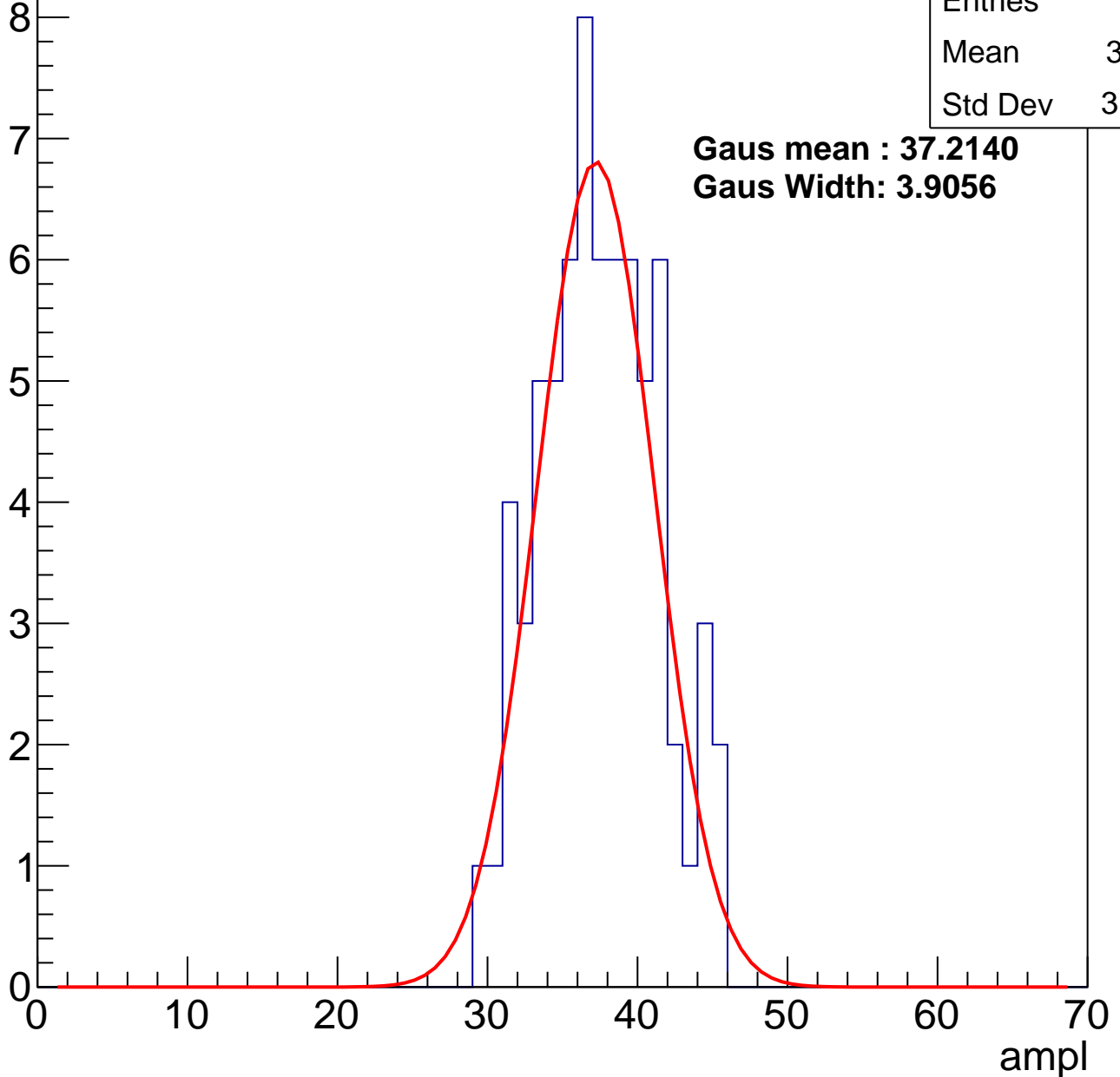
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	37.01
Std Dev	3.819

Gaus mean : 37.2140

Gaus Width: 3.9056

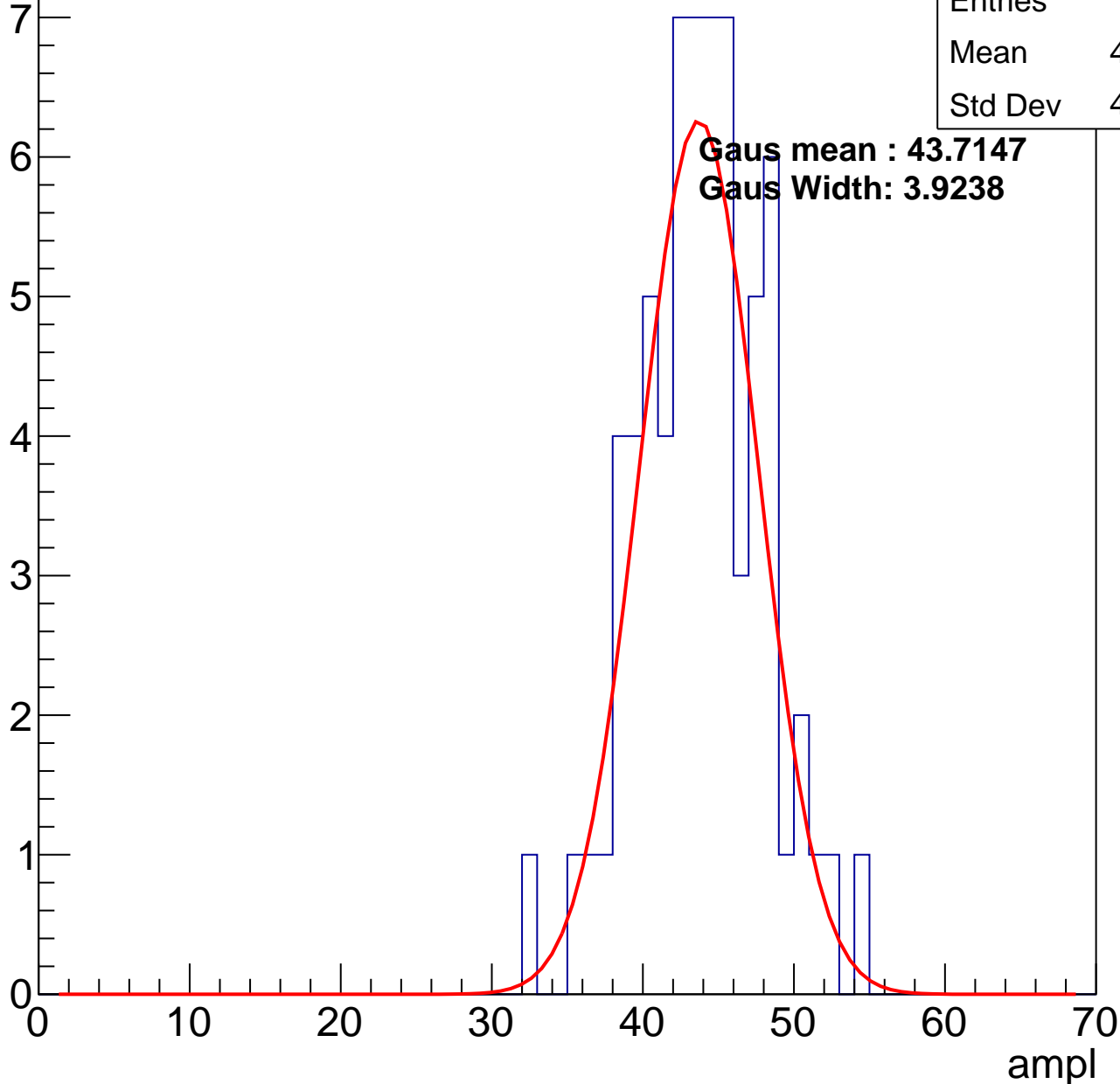


B1L100S, U11-ch59, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	43.43
Std Dev	4.137

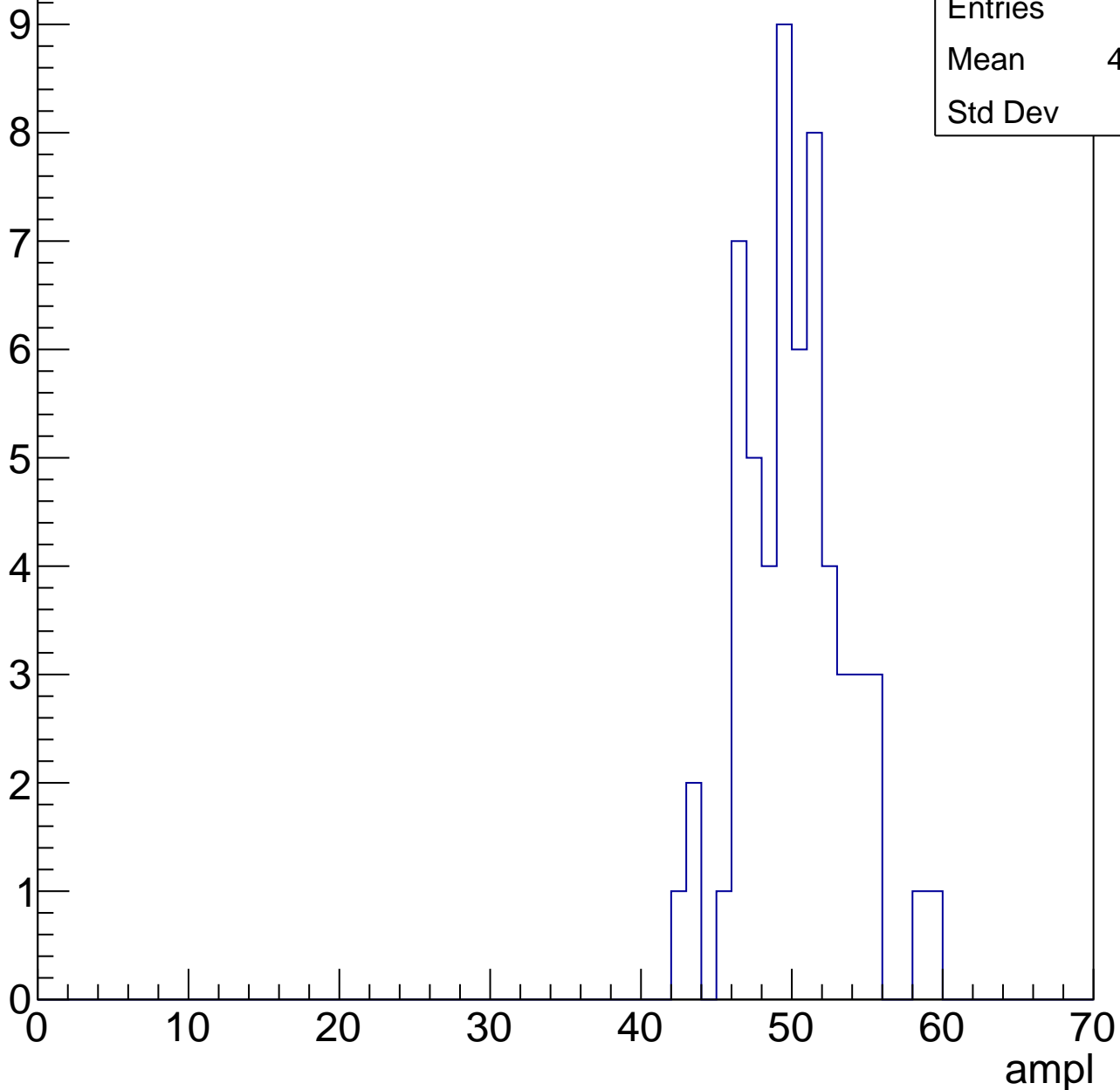


B1L100S, U11-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.69
Std Dev	3.43

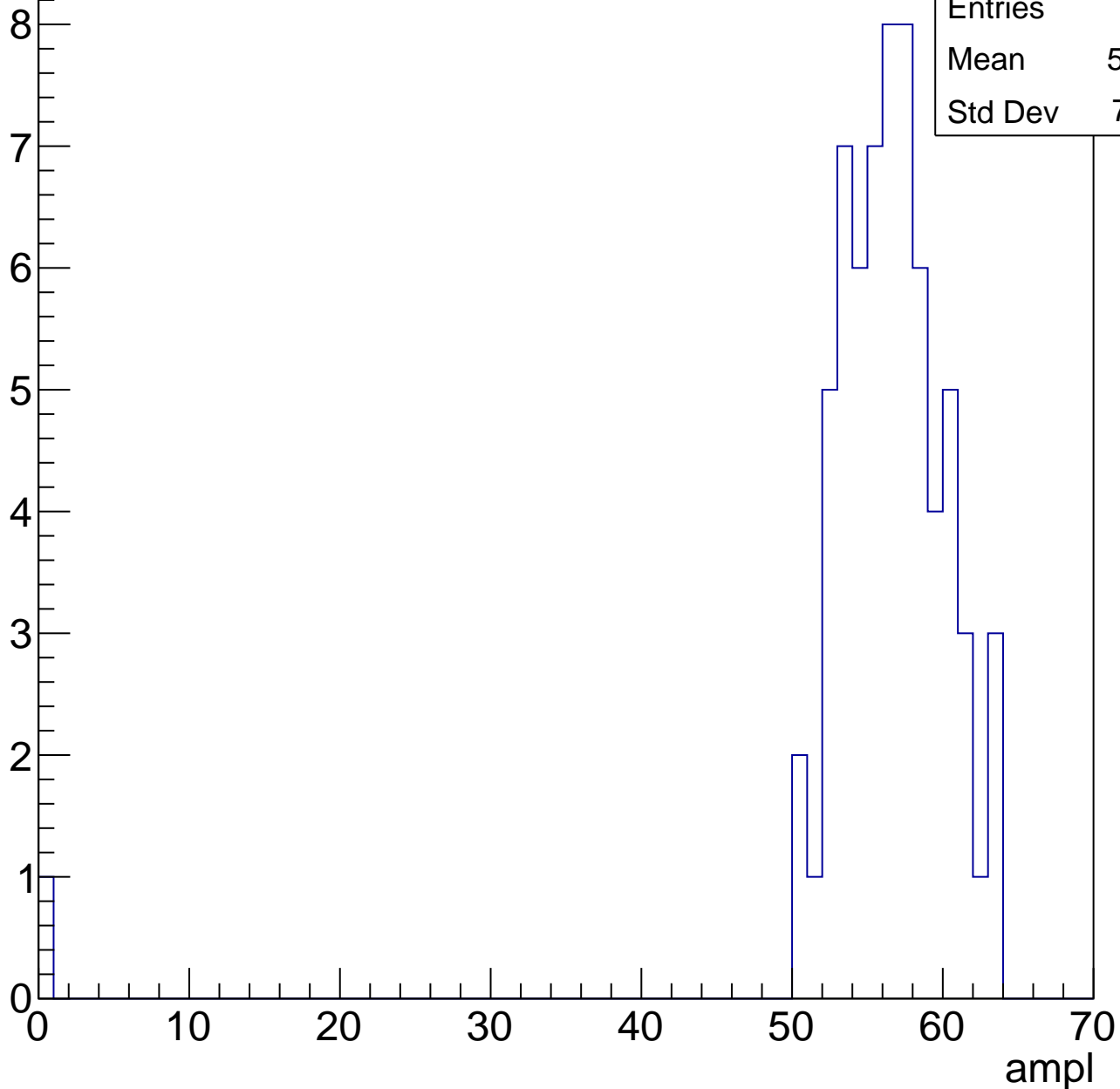


B1L100S, U11-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	55.42
Std Dev	7.521

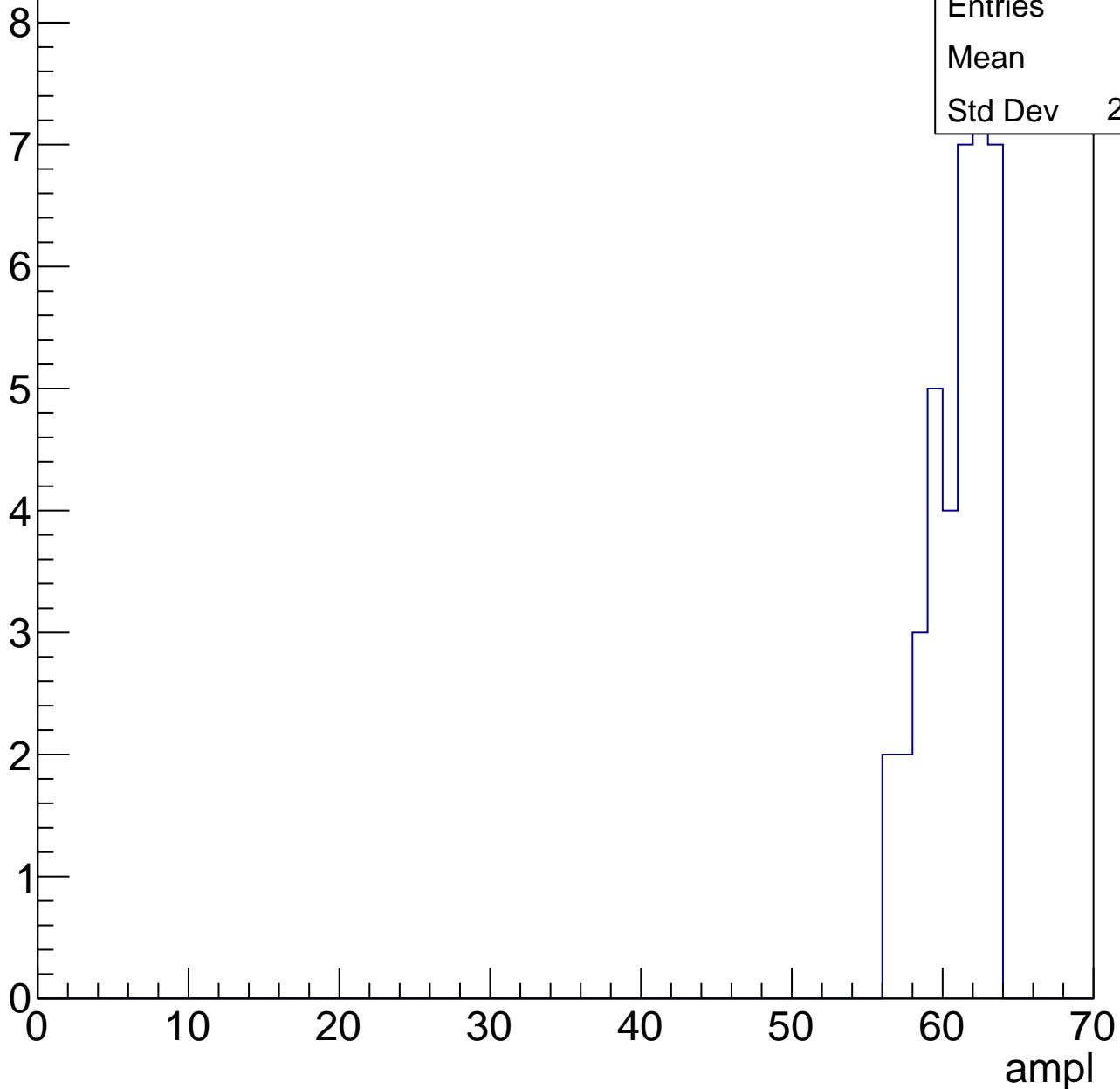


B1L100S, U11-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	60.5
Std Dev	2.049



B1L100S, U11-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch60, adc0

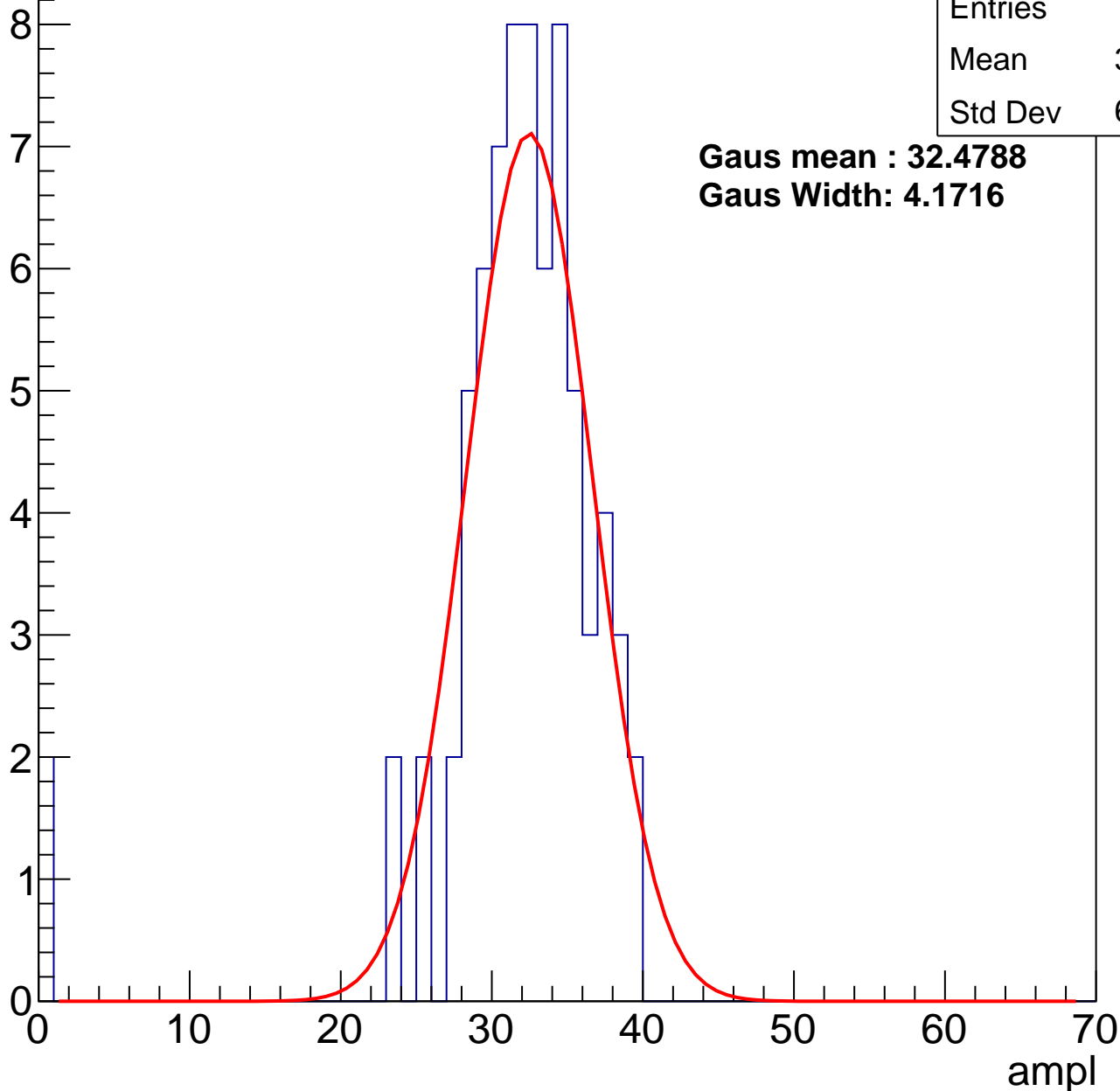
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	31.11
Std Dev	6.311

Gaus mean : 32.4788

Gaus Width: 4.1716



B1L100S, U11-ch60, adc1

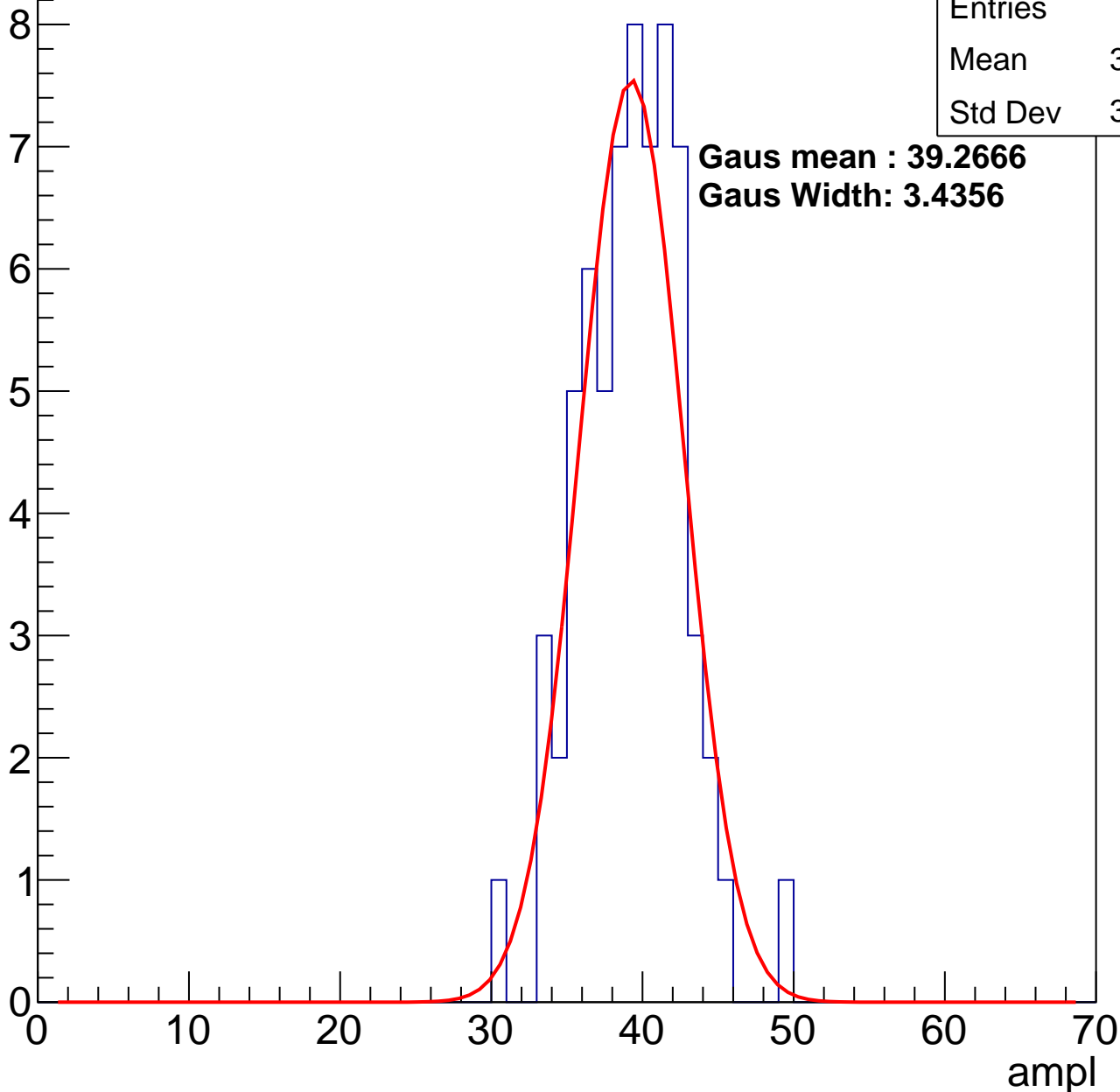
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	38.85
Std Dev	3.336

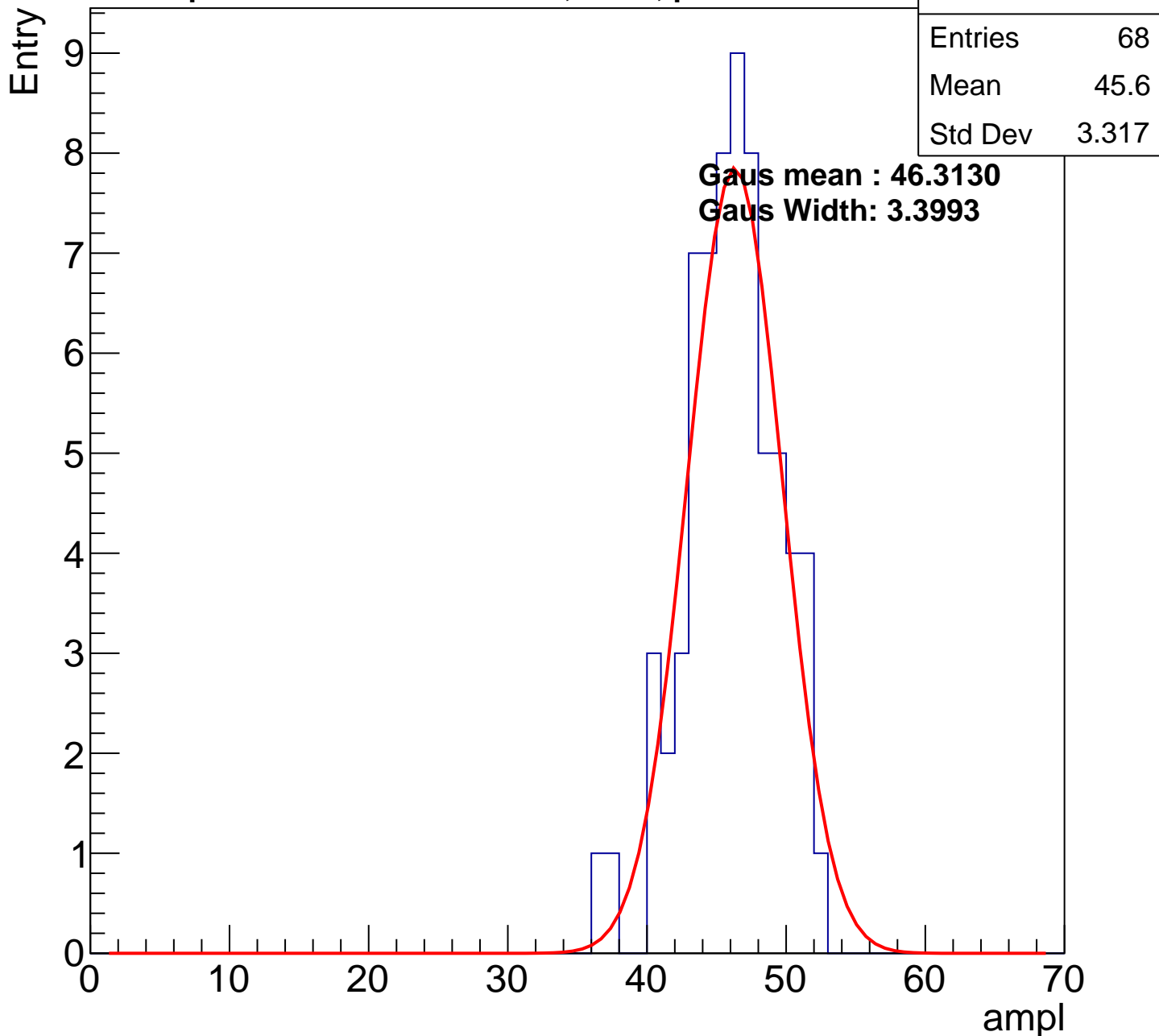
Gaus mean : 39.2666

Gaus Width: 3.4356



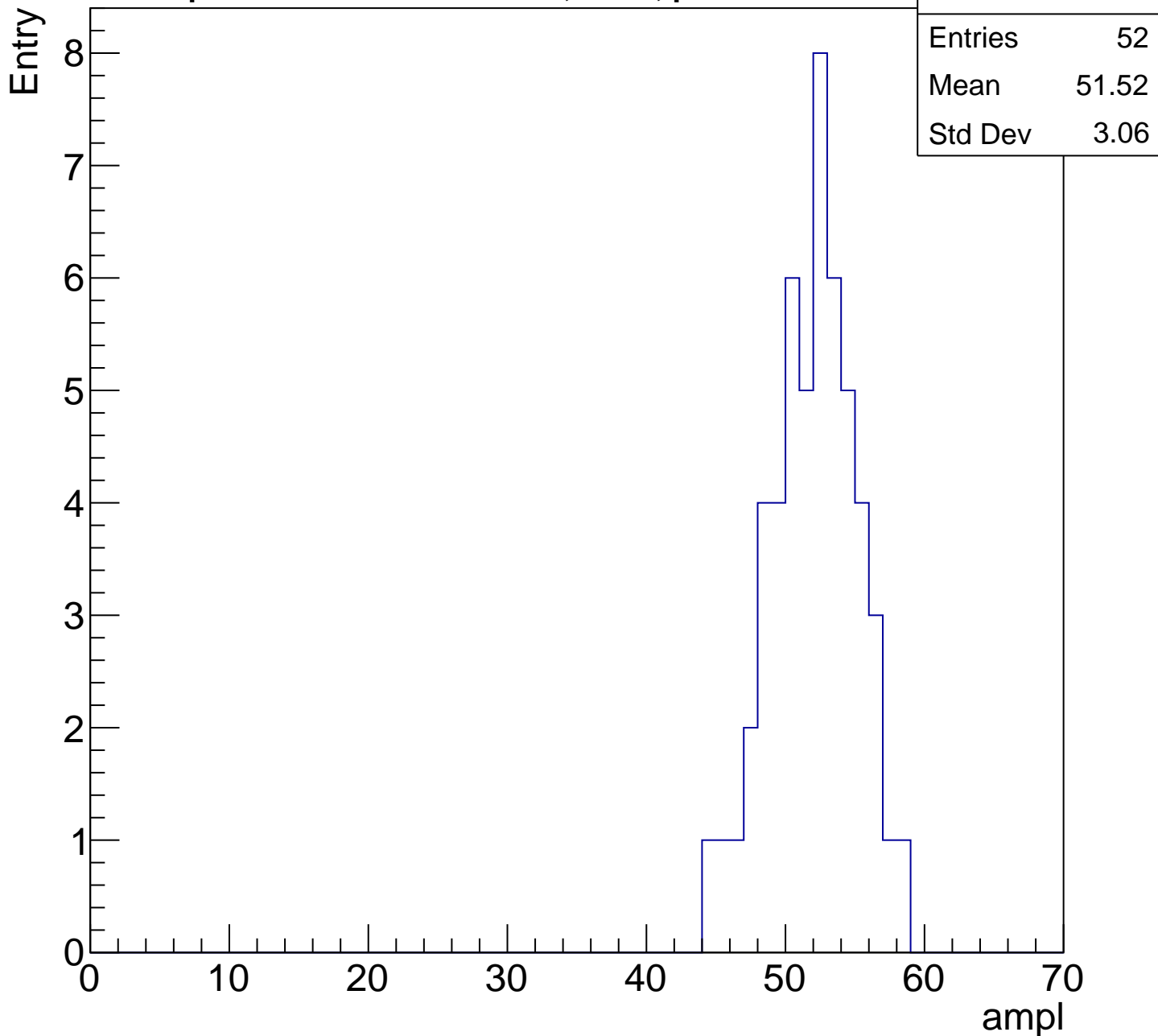
B1L100S, U11-ch60, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

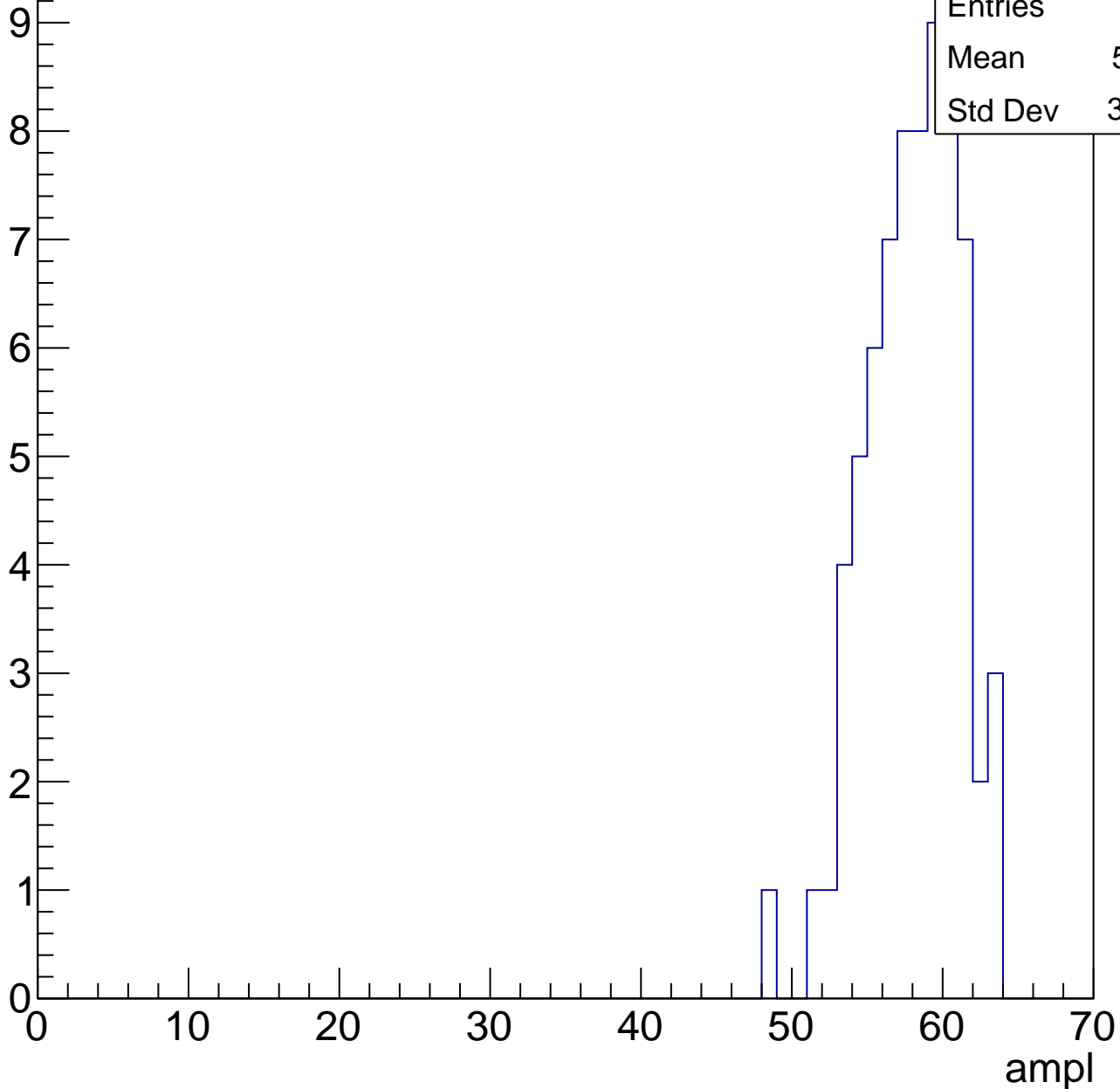


B1L100S, U11-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	57.51
Std Dev	3.046

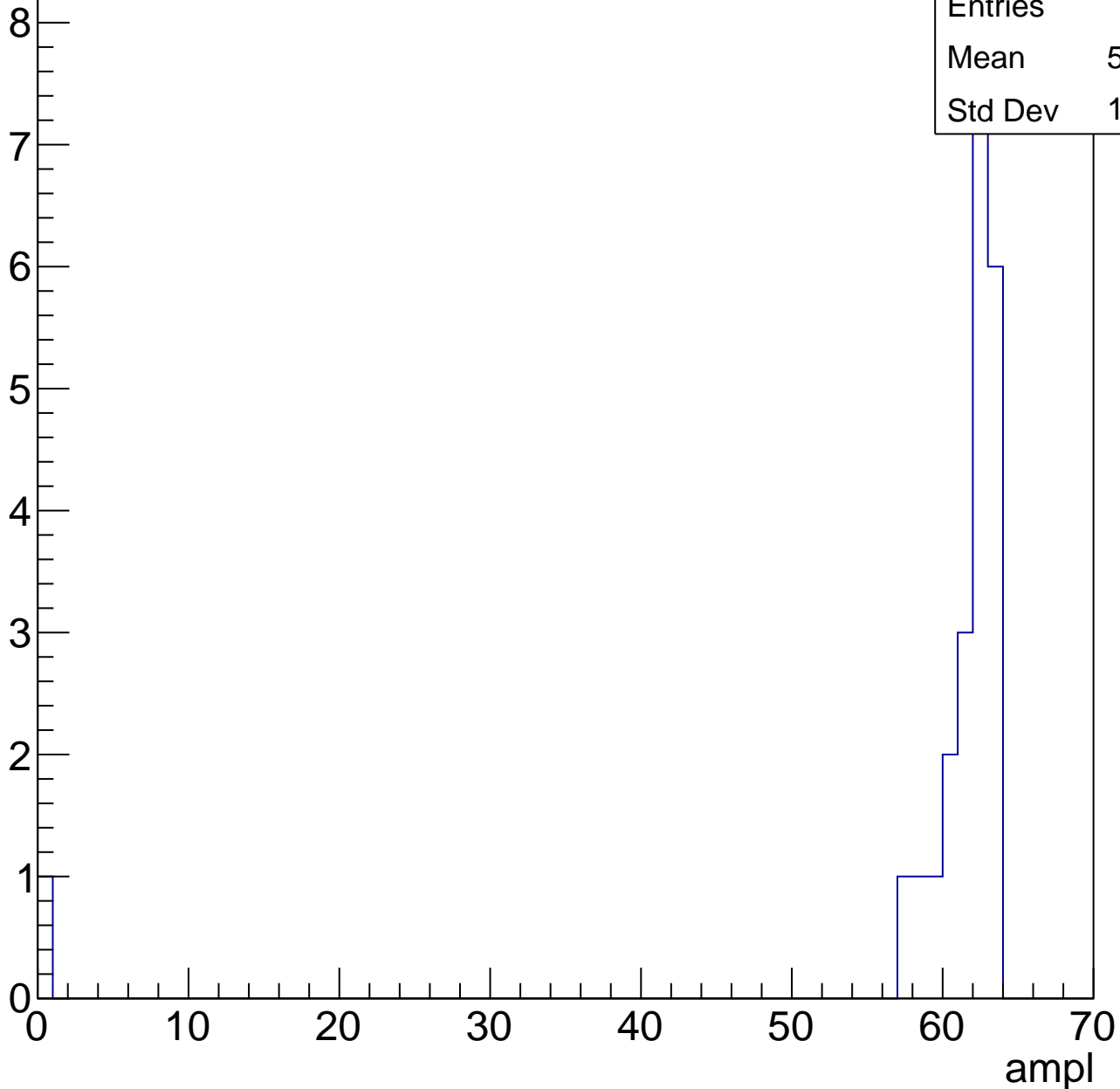


B1L100S, U11-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	58.74
Std Dev	12.63



B1L100S, U11-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch61, adc0

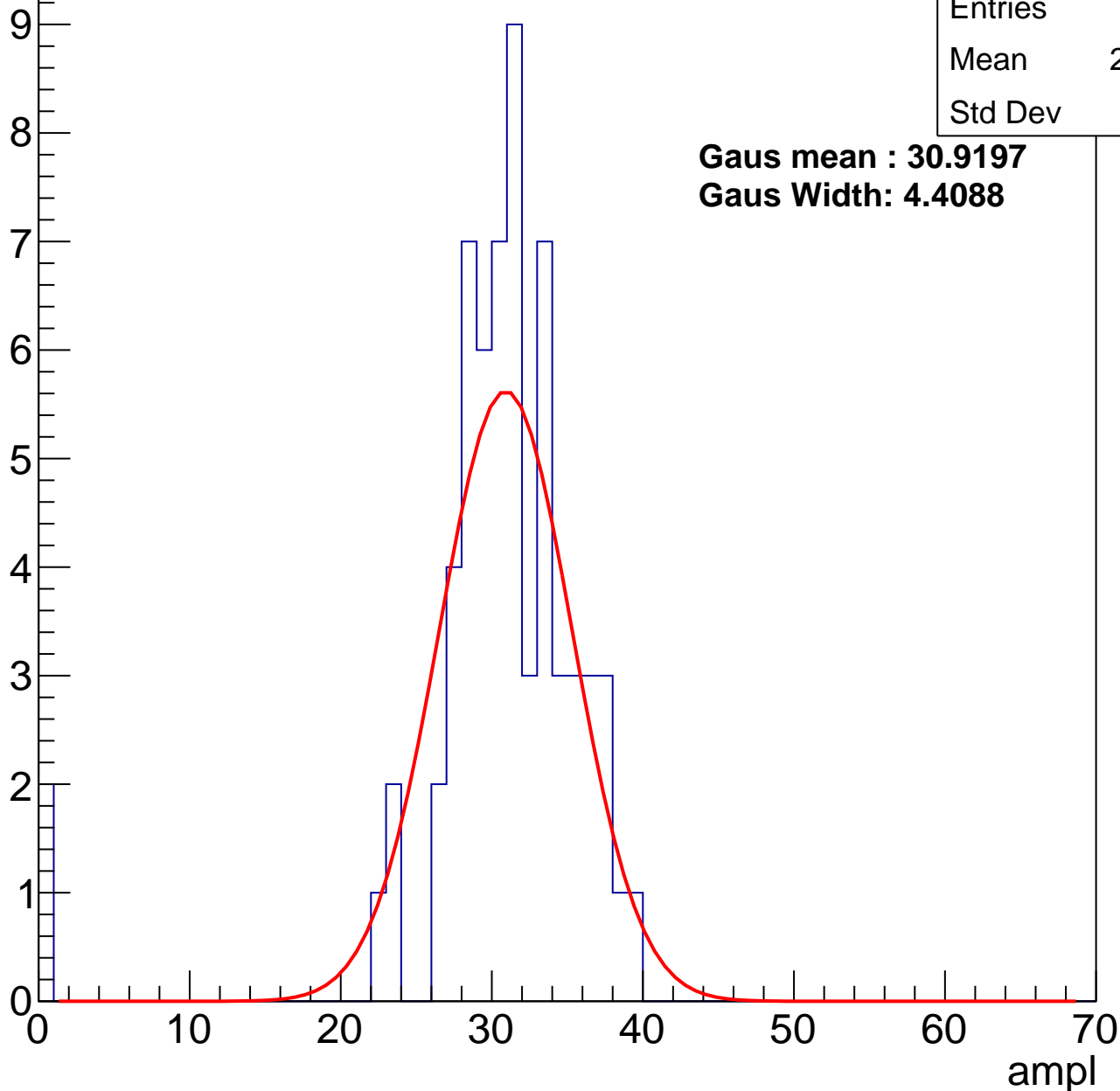
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.95
Std Dev	6.46

Gaus mean : 30.9197

Gaus Width: 4.4088



B1L100S, U11-ch61, adc1

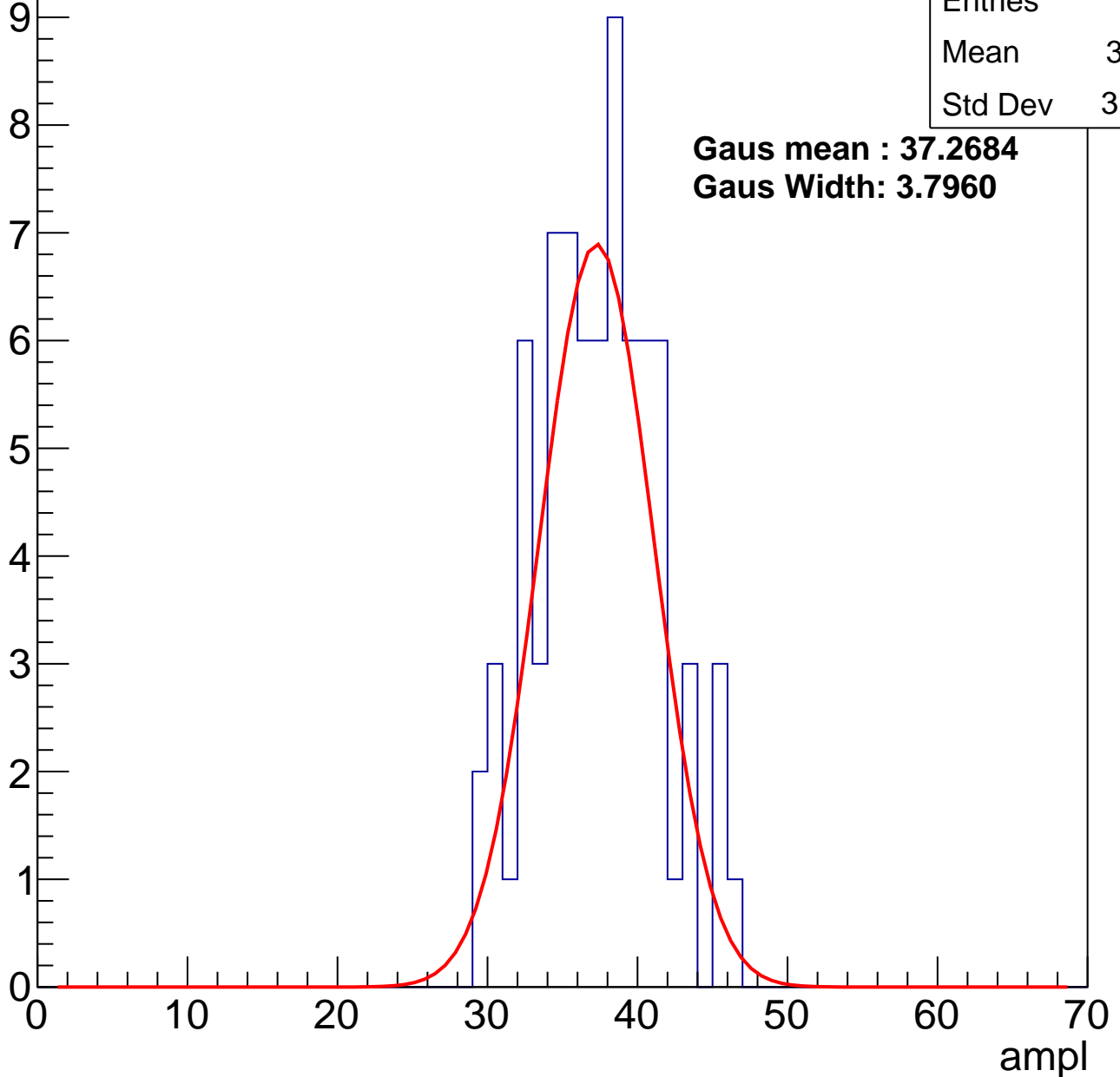
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.91
Std Dev	3.964

Gaus mean : 37.2684

Gaus Width: 3.7960



B1L100S, U11-ch61, adc2

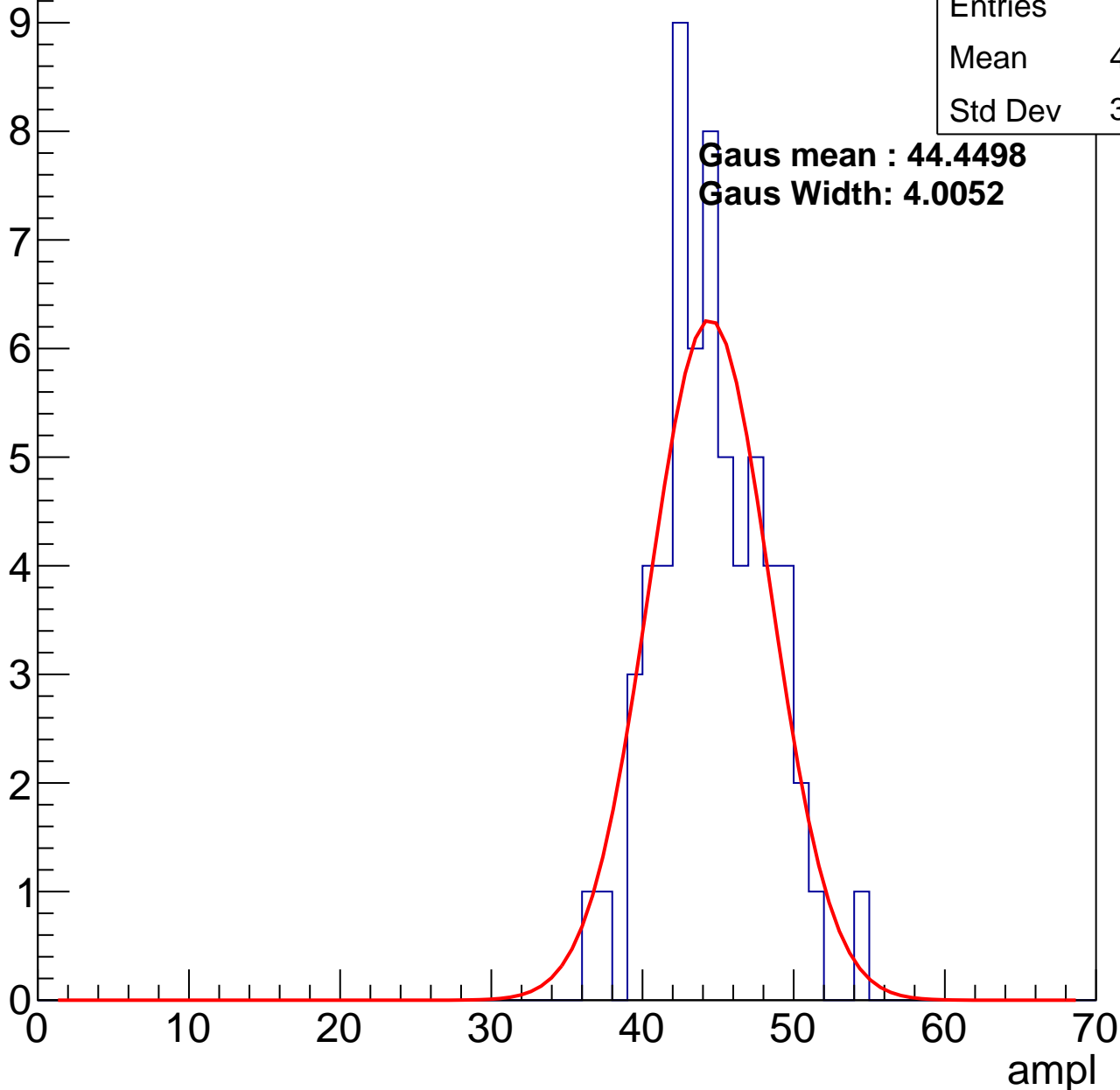
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.18
Std Dev	3.545

Gaus mean : 44.4498

Gaus Width: 4.0052

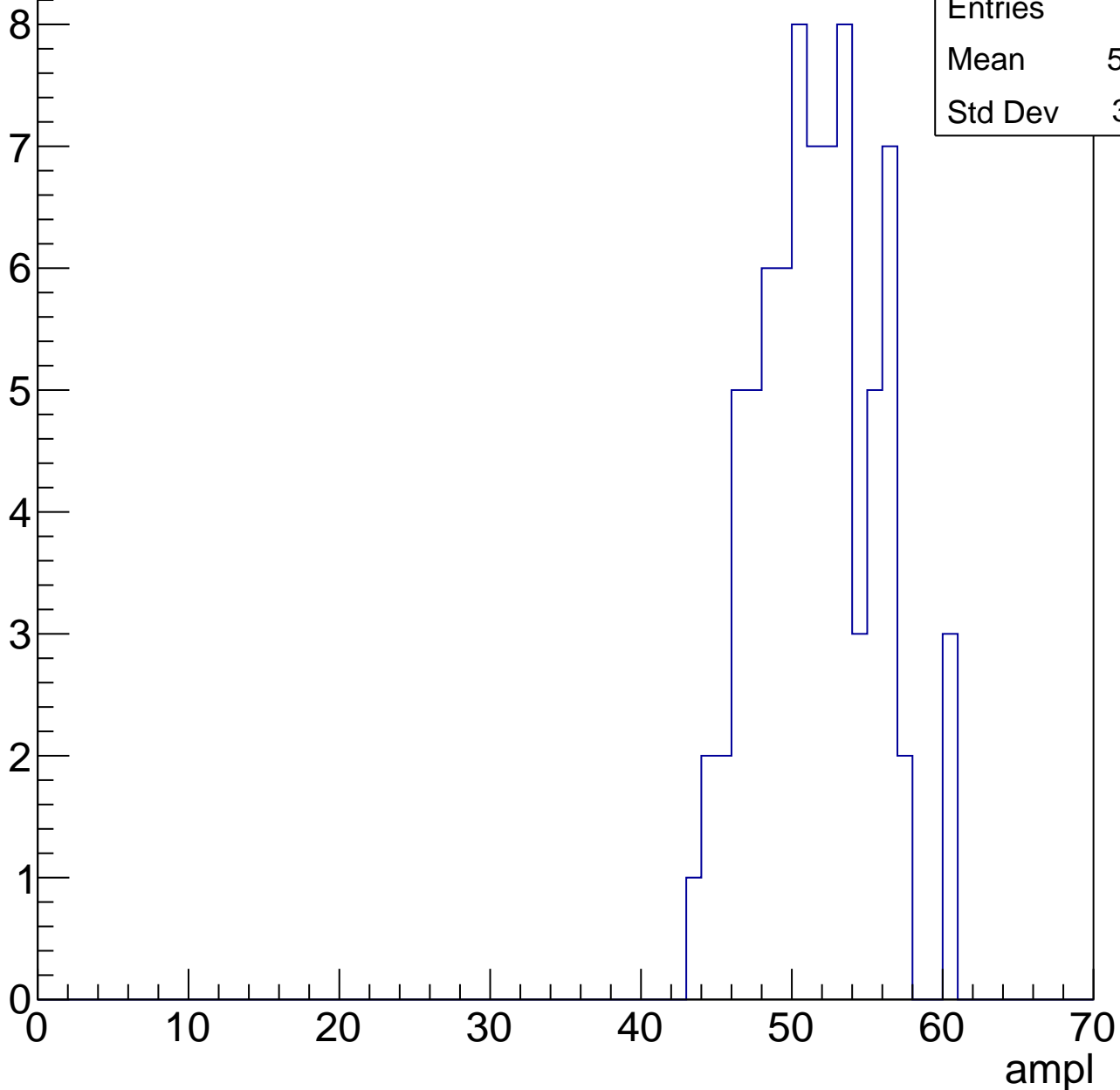


B1L100S, U11-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

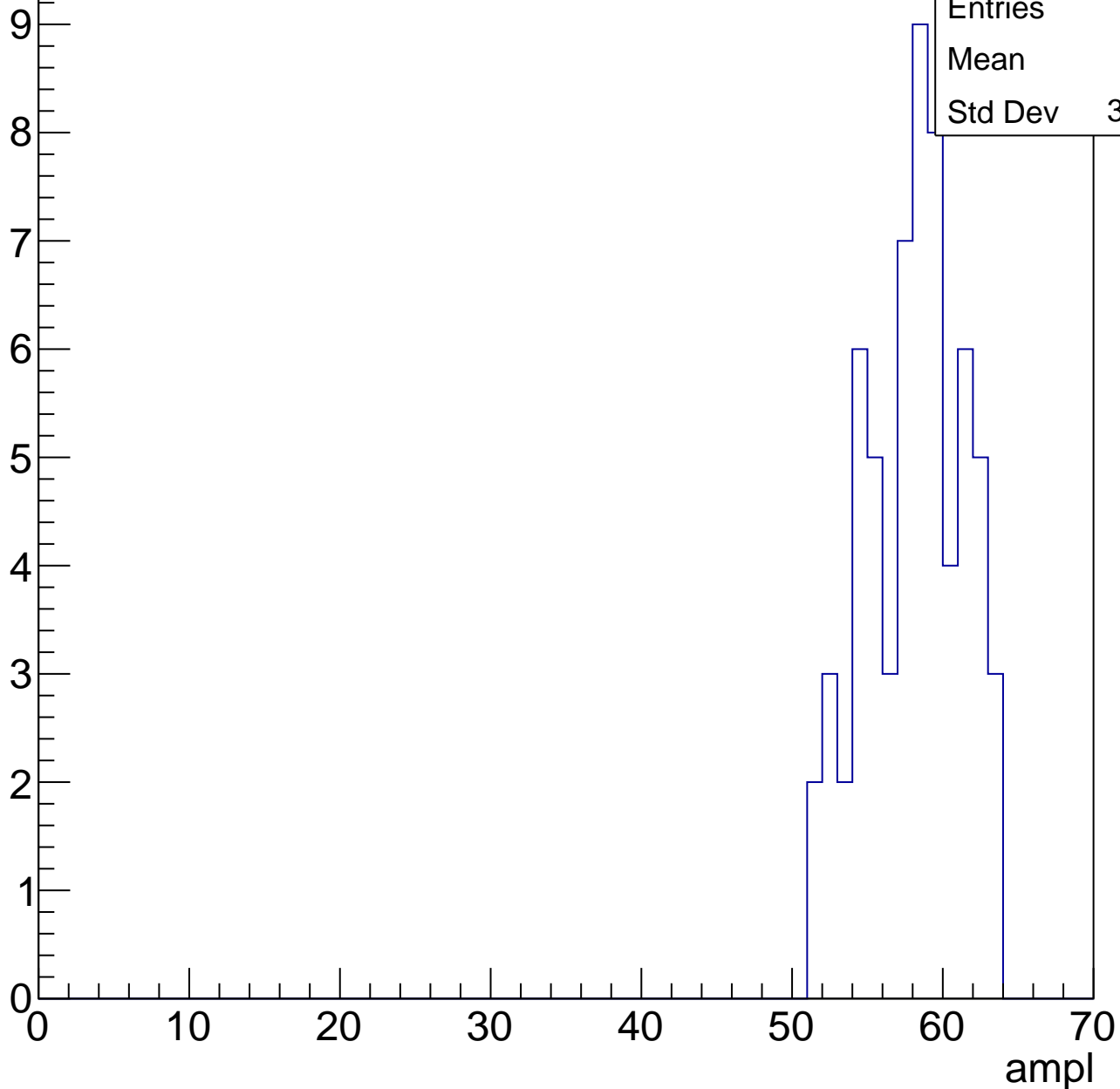
Entries	77
Mean	51.12
Std Dev	3.881



B1L100S, U11-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

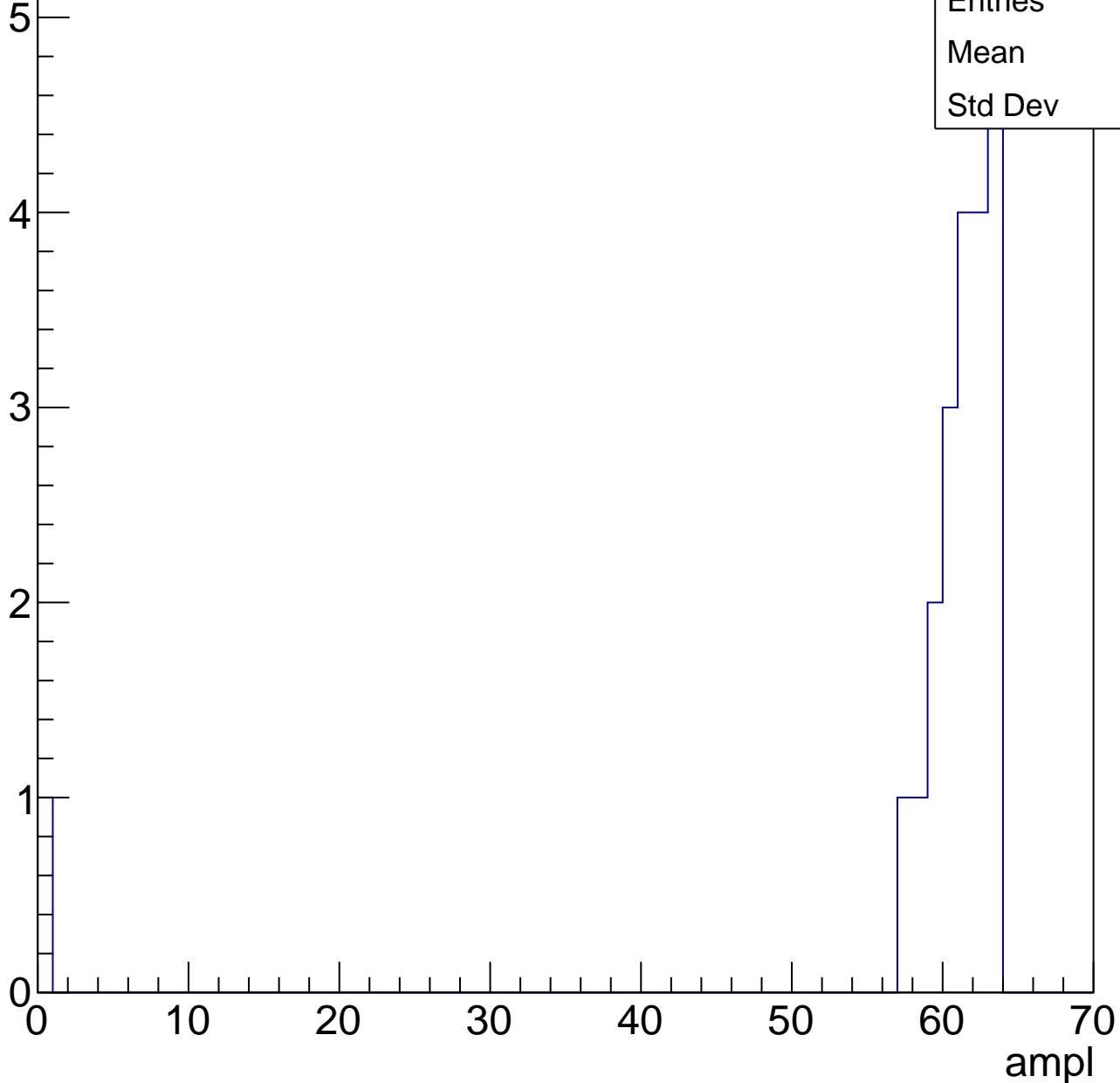


B1L100S, U11-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.1
Std Dev	13.1



B1L100S, U11-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U11-ch62, adc0

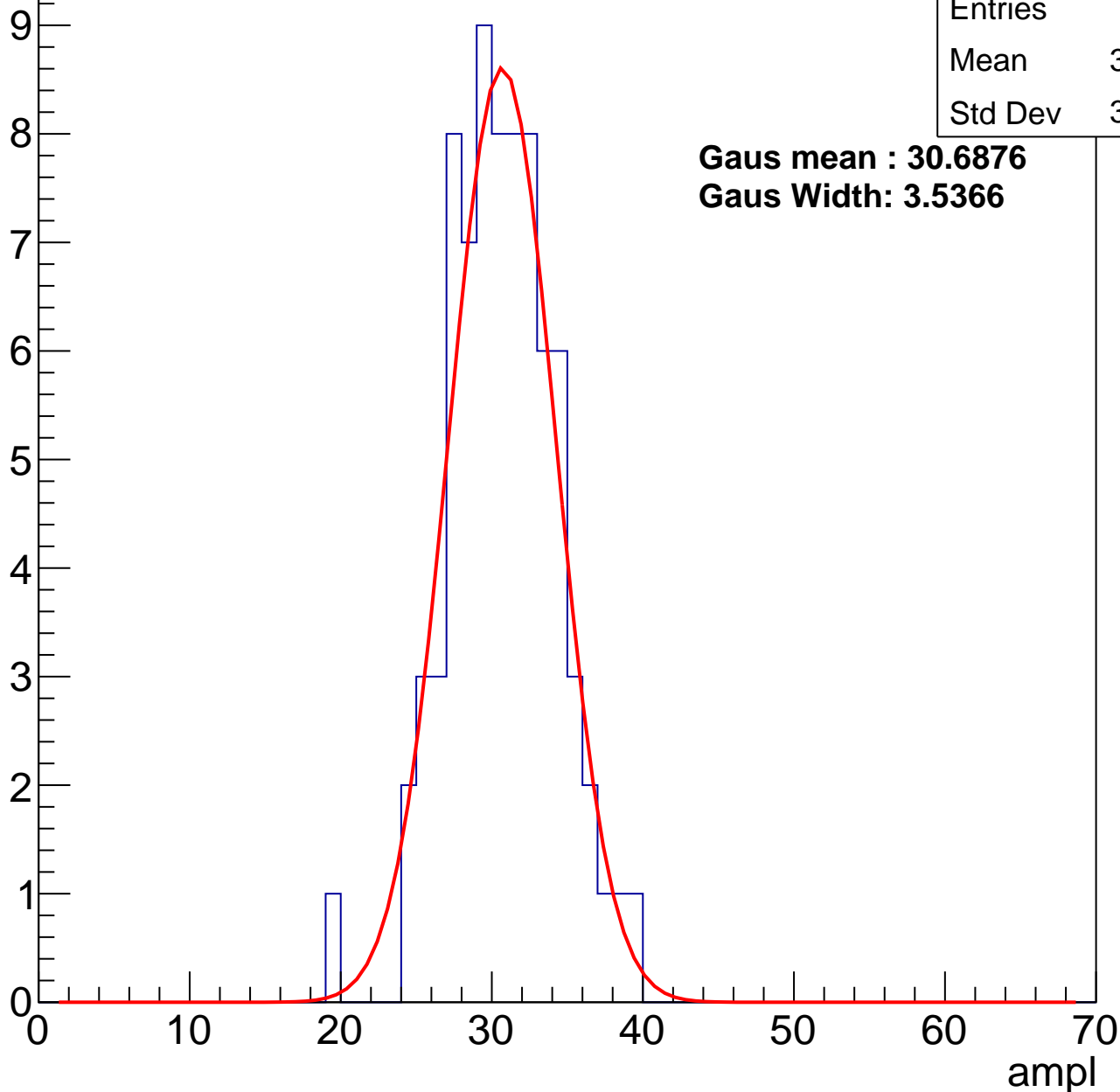
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	30.26
Std Dev	3.514

Gaus mean : 30.6876

Gaus Width: 3.5366



B1L100S, U11-ch62, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

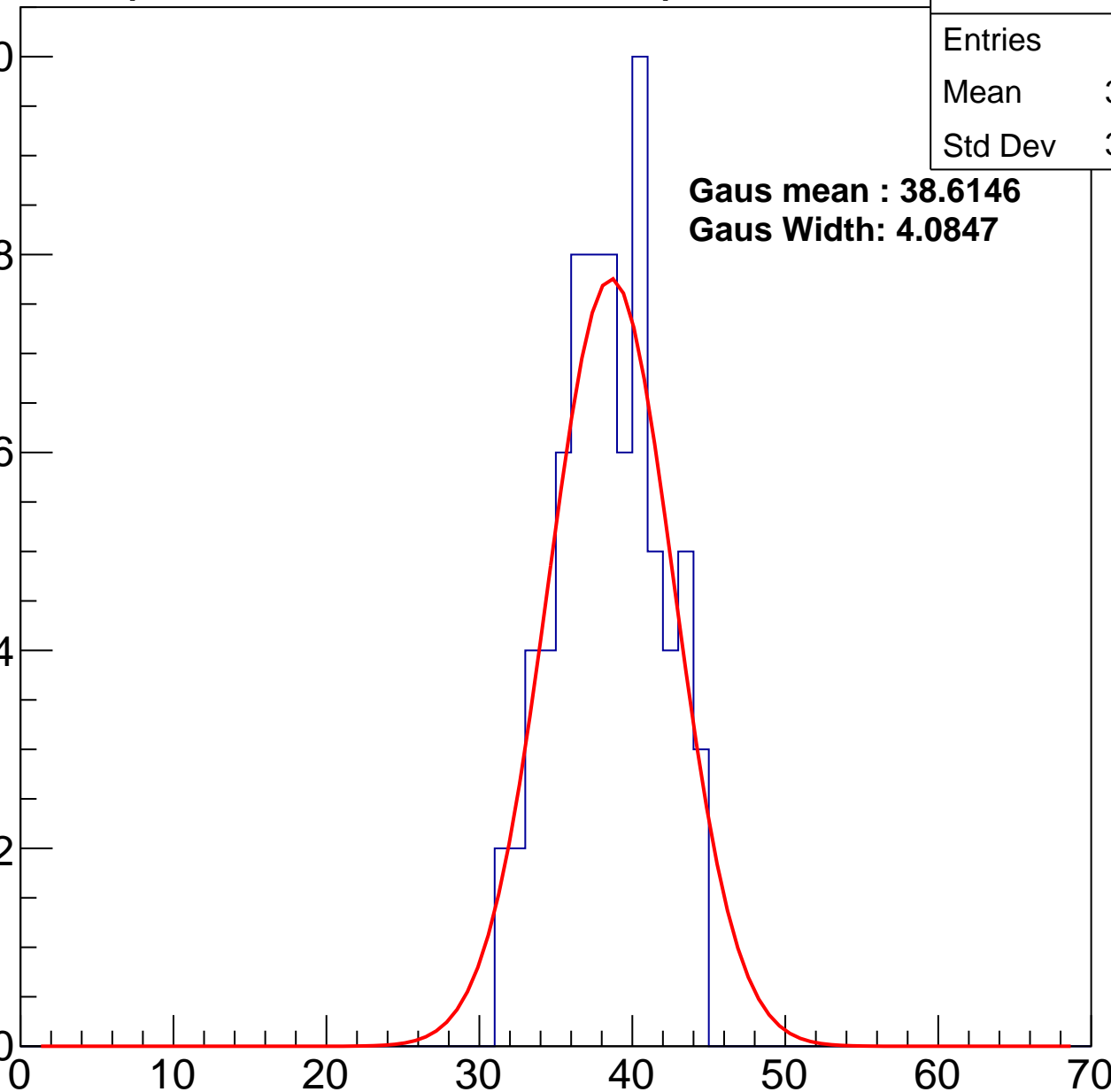
Entries	75
Mean	37.95
Std Dev	3.298

Gaus mean : 38.6146

Gaus Width: 4.0847

10
8
6
4
2
0

ampl



B1L100S, U11-ch62, adc2

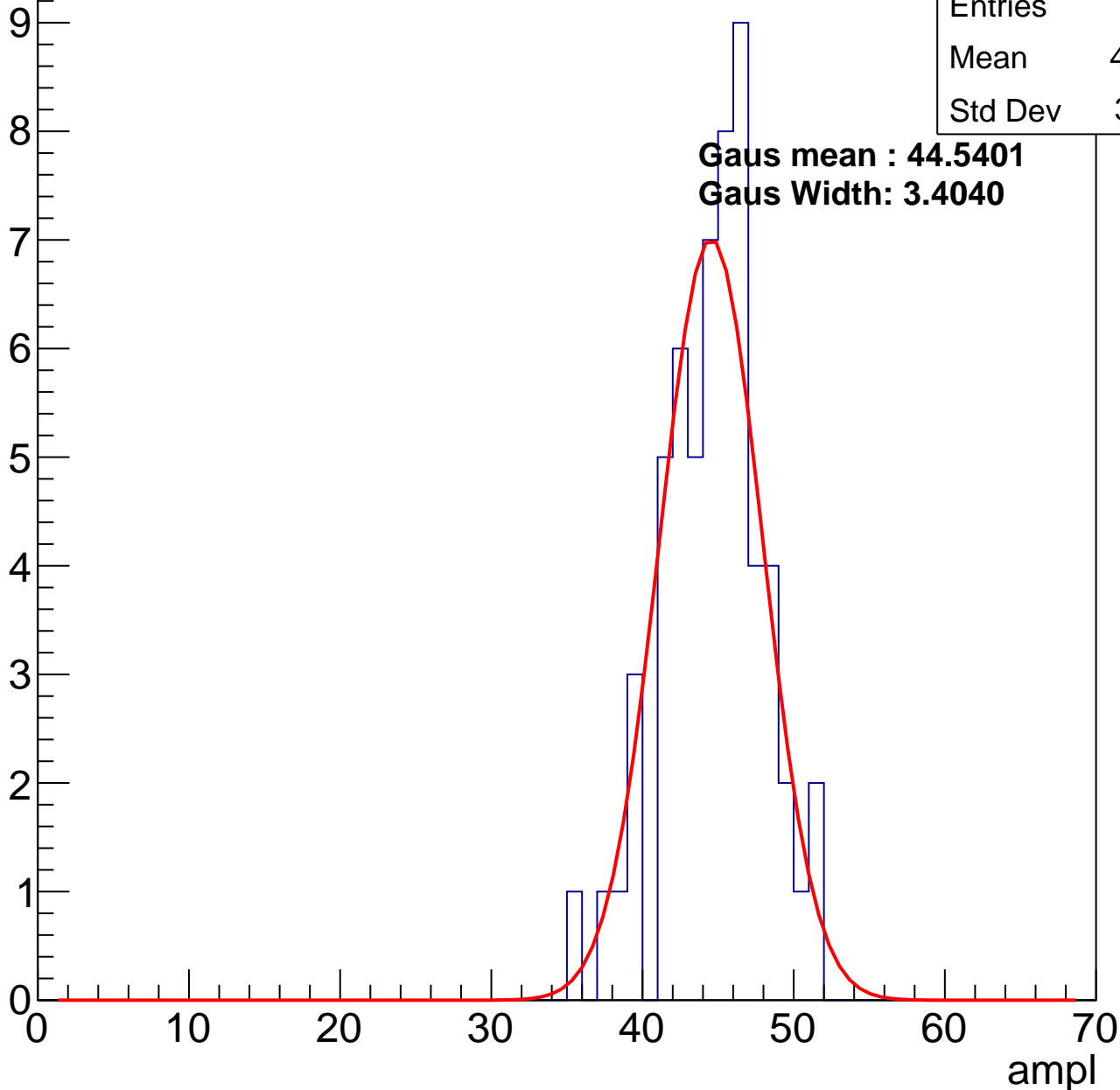
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.25
Std Dev	3.291

Gaus mean : 44.5401

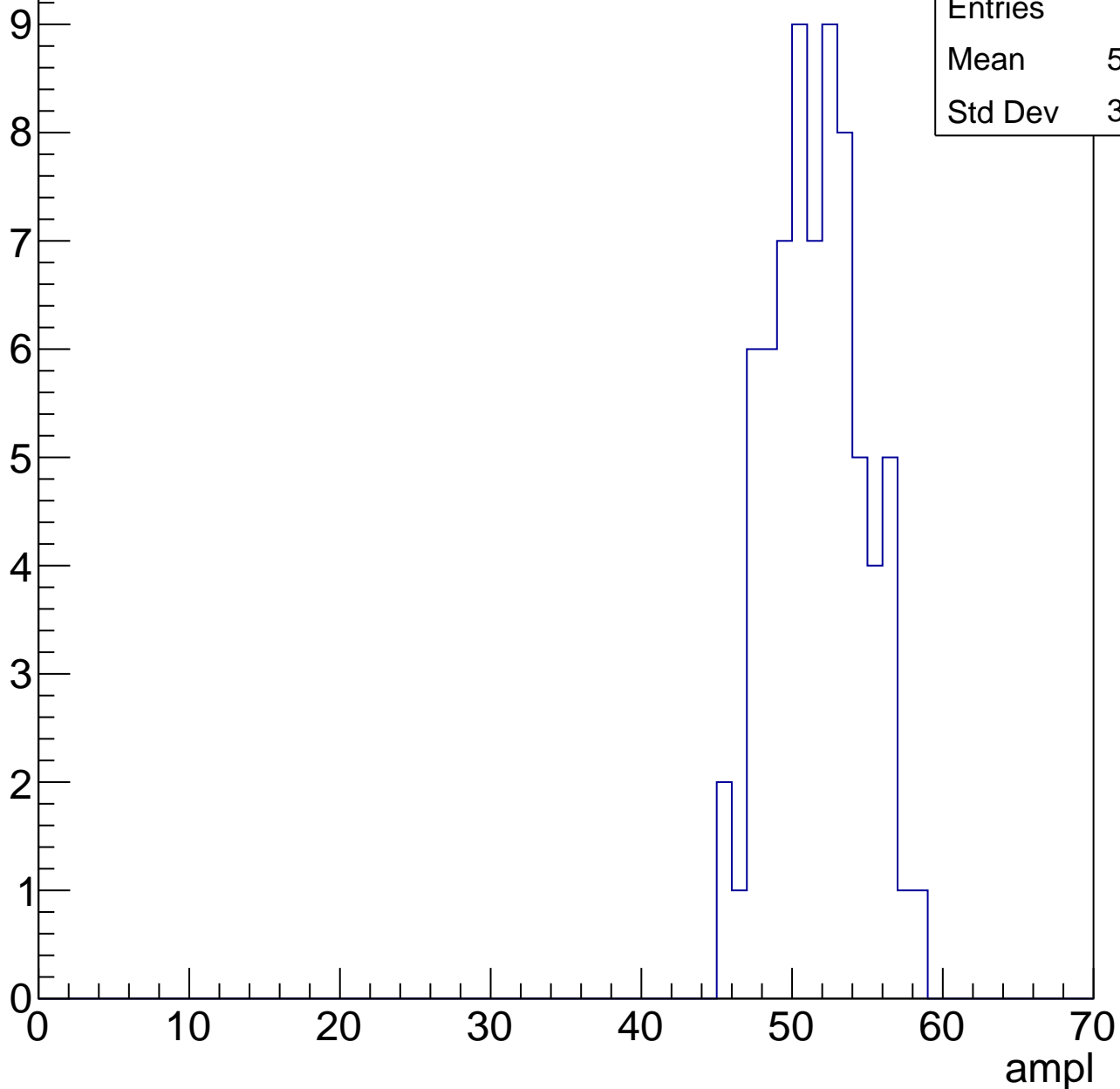
Gaus Width: 3.4040



B1L100S, U11-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

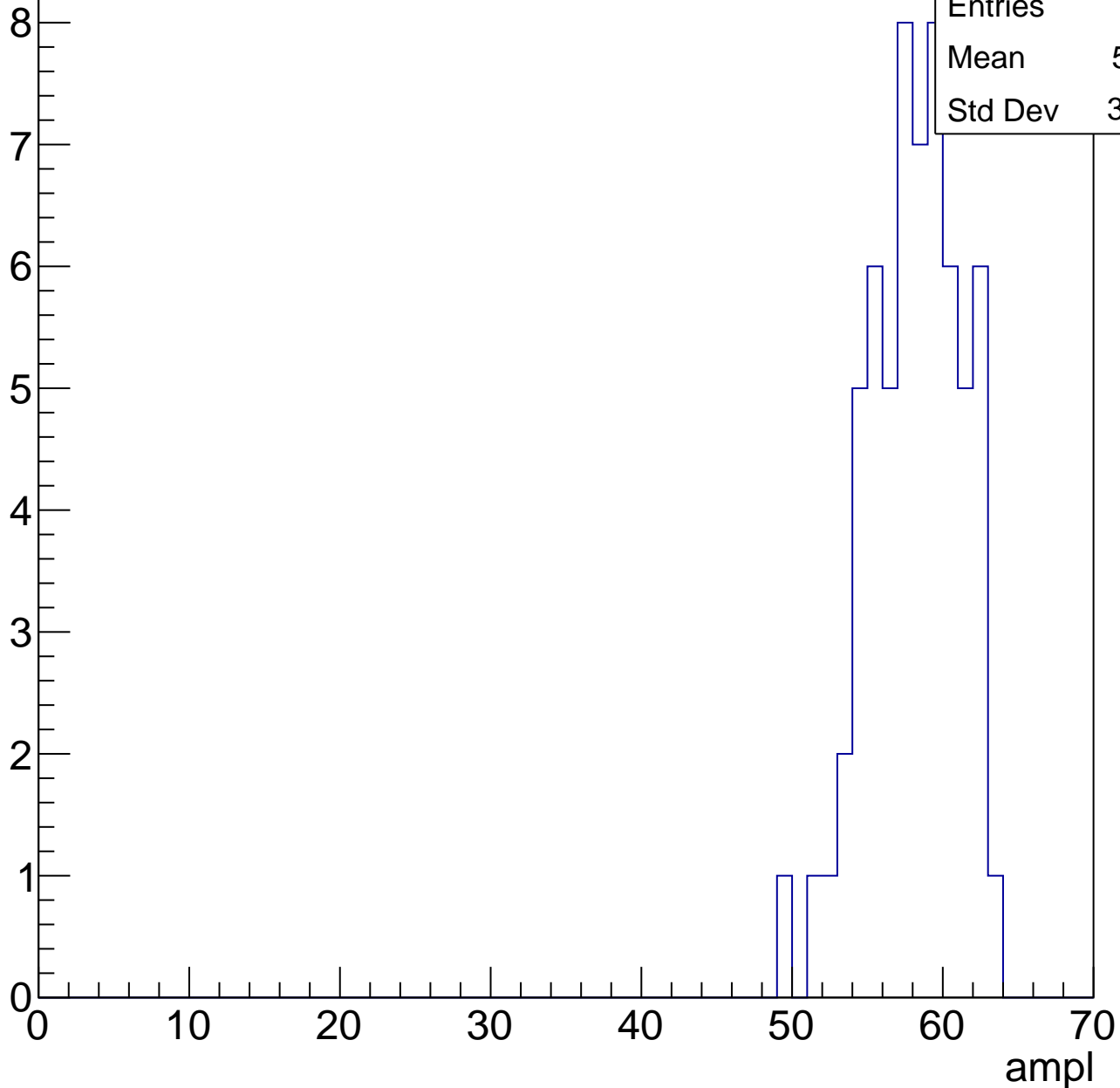


Entries	71
Mean	51.17
Std Dev	3.007

B1L100S, U11-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

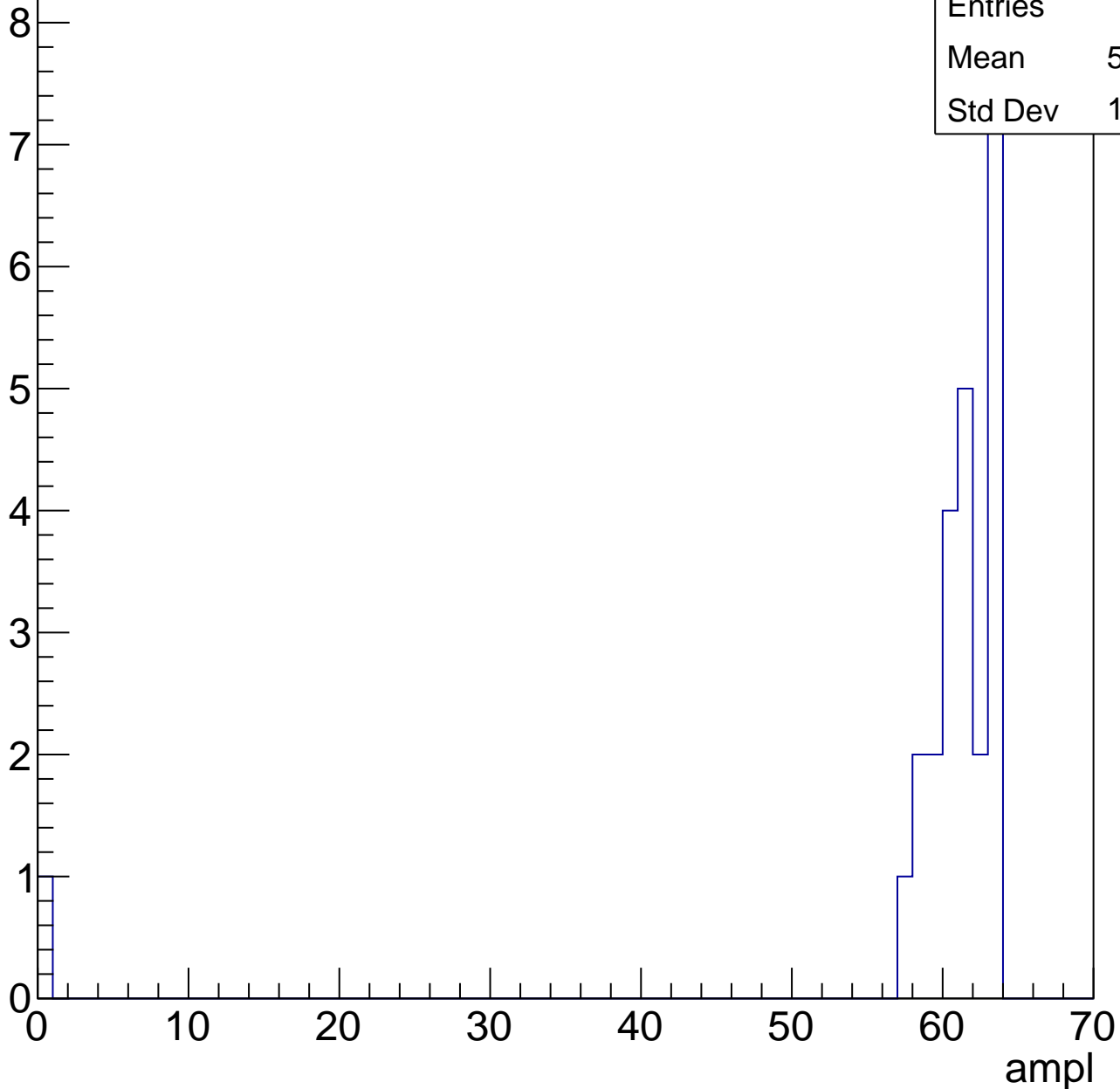


B1L100S, U11-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	58.56
Std Dev	12.09



B1L100S, U11-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L100S, U11-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch63, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	31.3
Std Dev	3.158

Gaus mean : 32.1272

Gaus Width: 3.4366

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L100S, U11-ch63, adc1

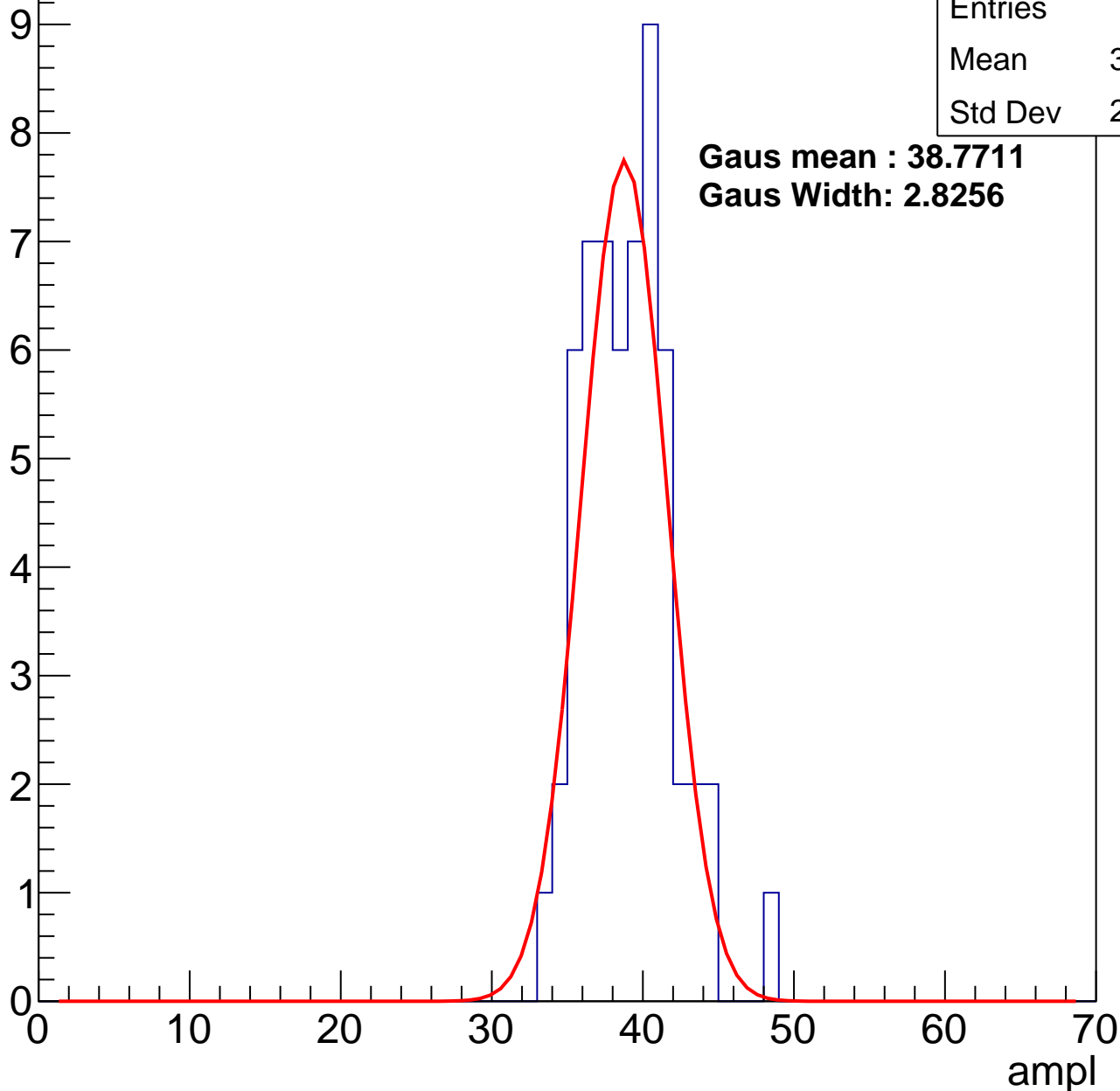
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	38.53
Std Dev	2.878

Gaus mean : 38.7711

Gaus Width: 2.8256



B1L100S, U11-ch63, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	44.72
Std Dev	3.407

Gaus mean : 44.7188

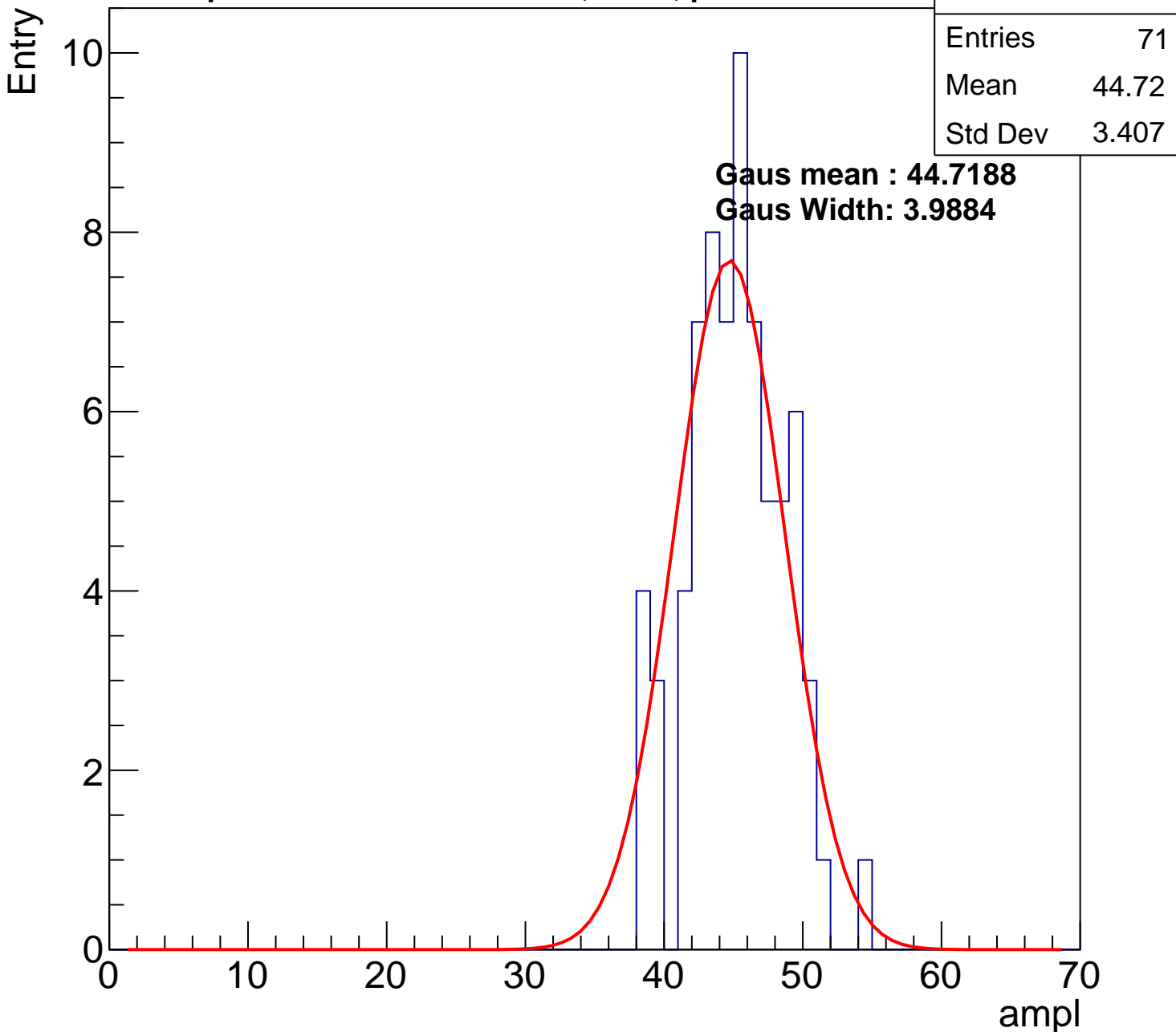
Gaus Width: 3.9884

Entry

10
8
6
4
2
0

ampl

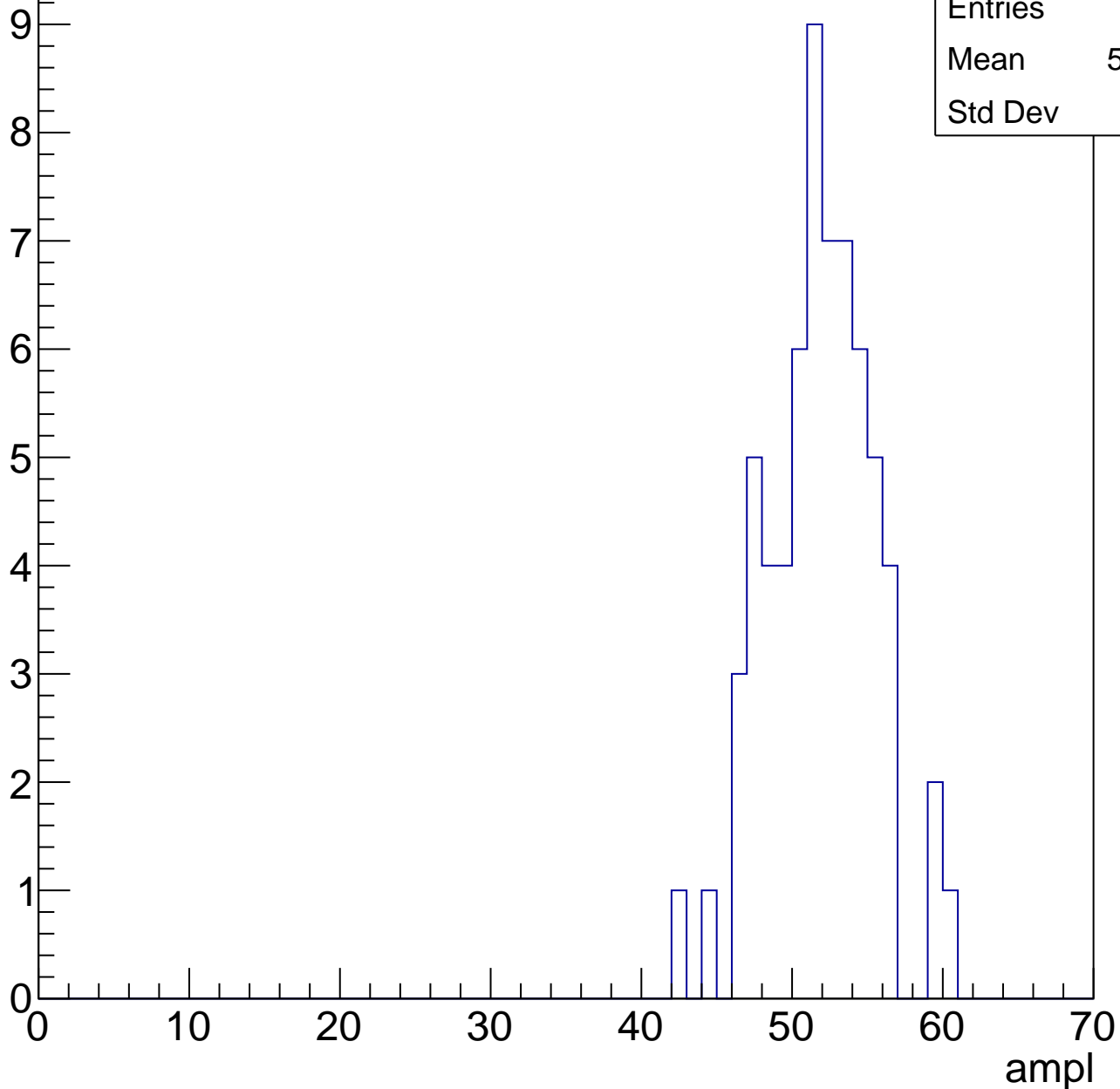
0 10 20 30 40 50 60 70



B1L100S, U11-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

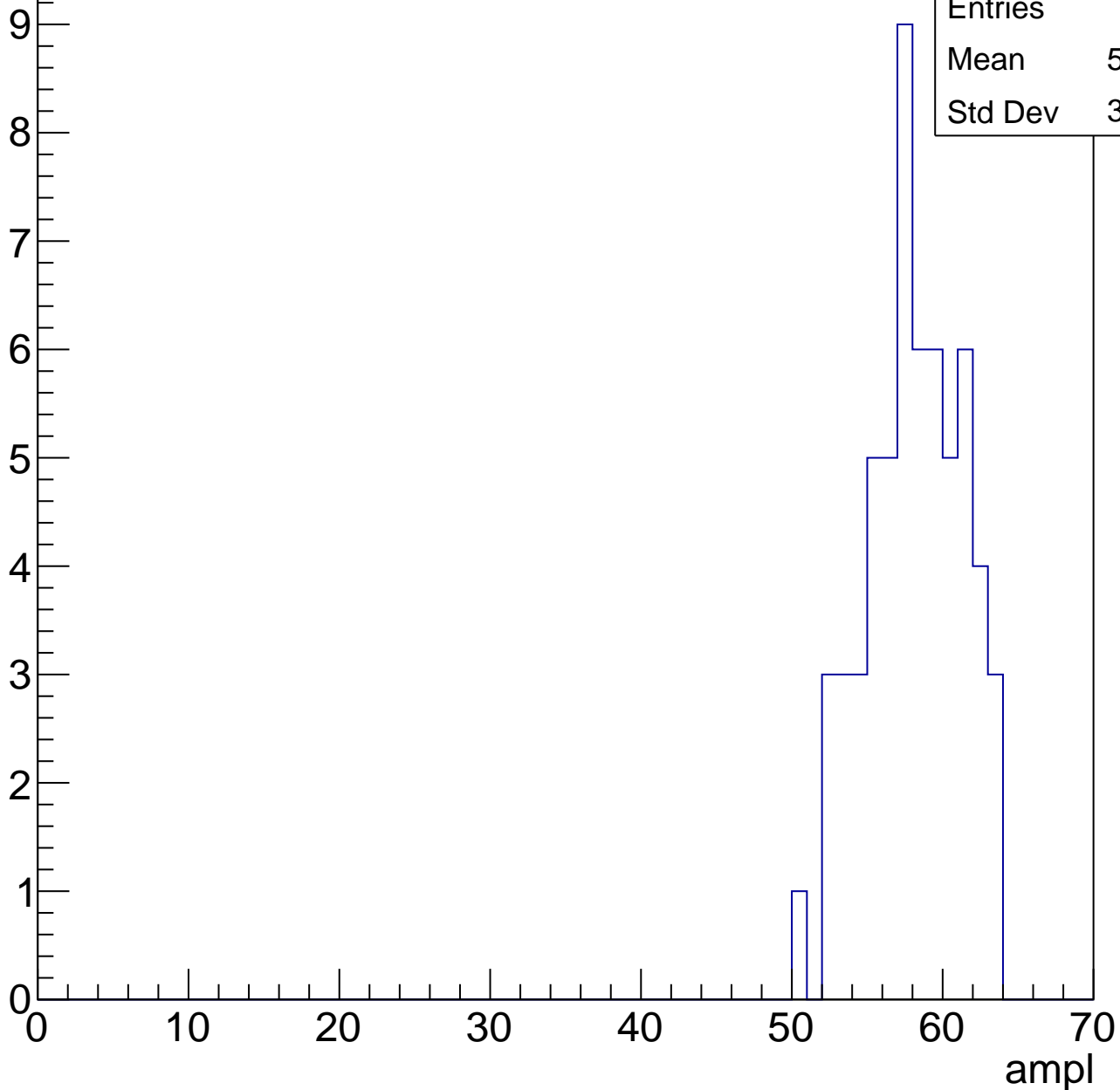


B1L100S, U11-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	57.63
Std Dev	3.135

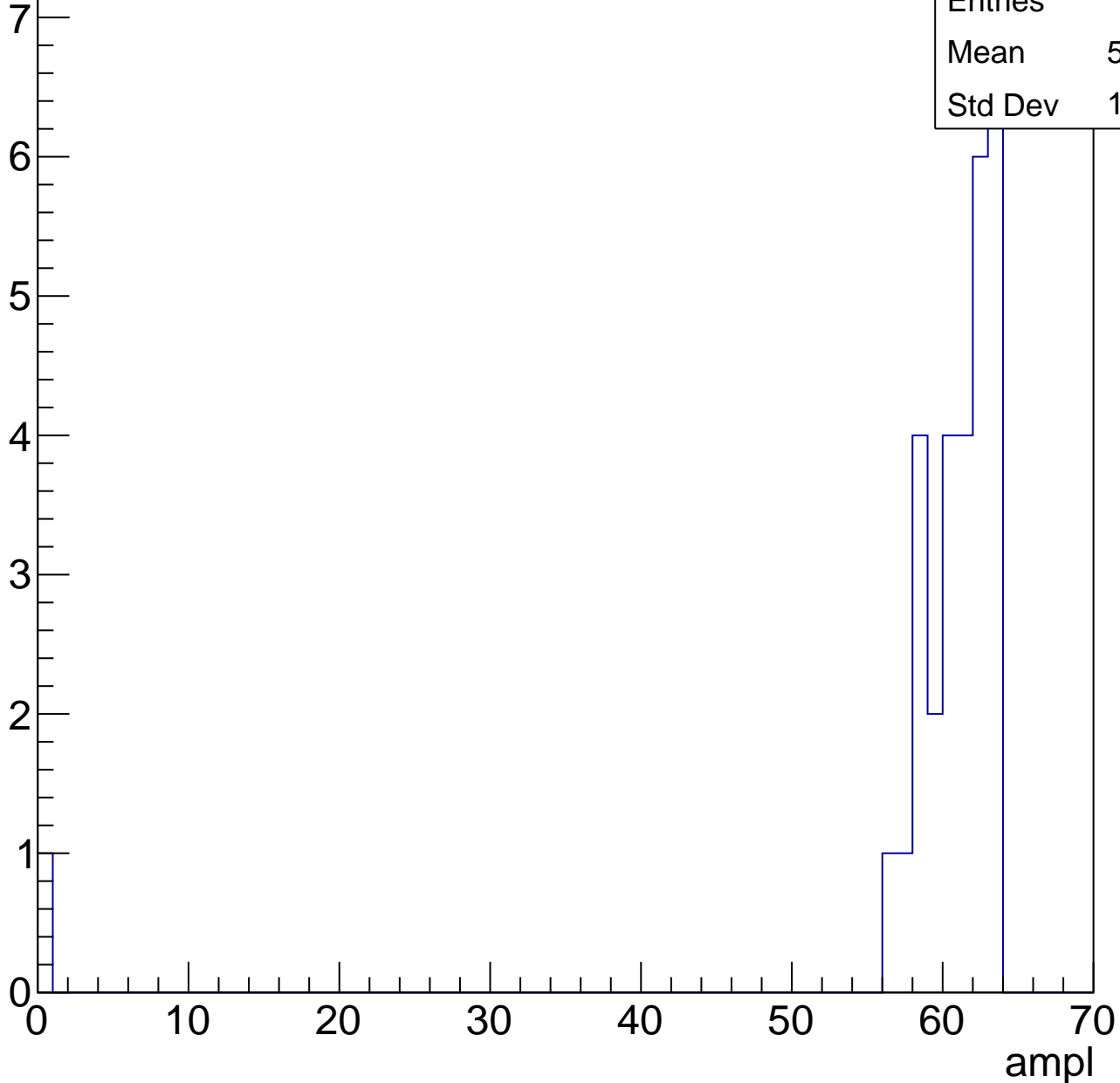


B1L100S, U11-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.67
Std Dev	11.08



B1L100S, U11-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

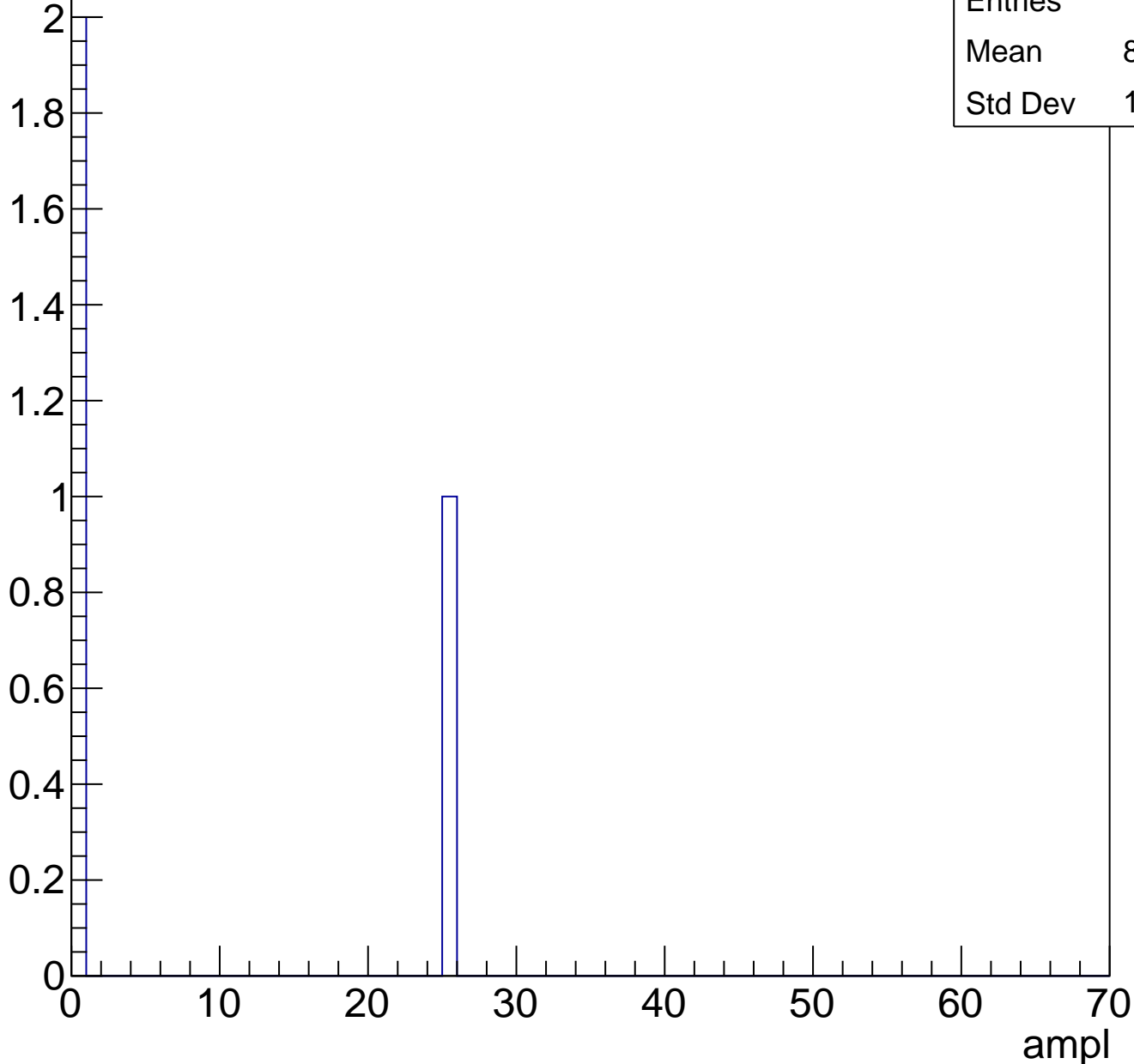
Entry



B1L100S, U11-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B1L100S, U11-ch64, adc0

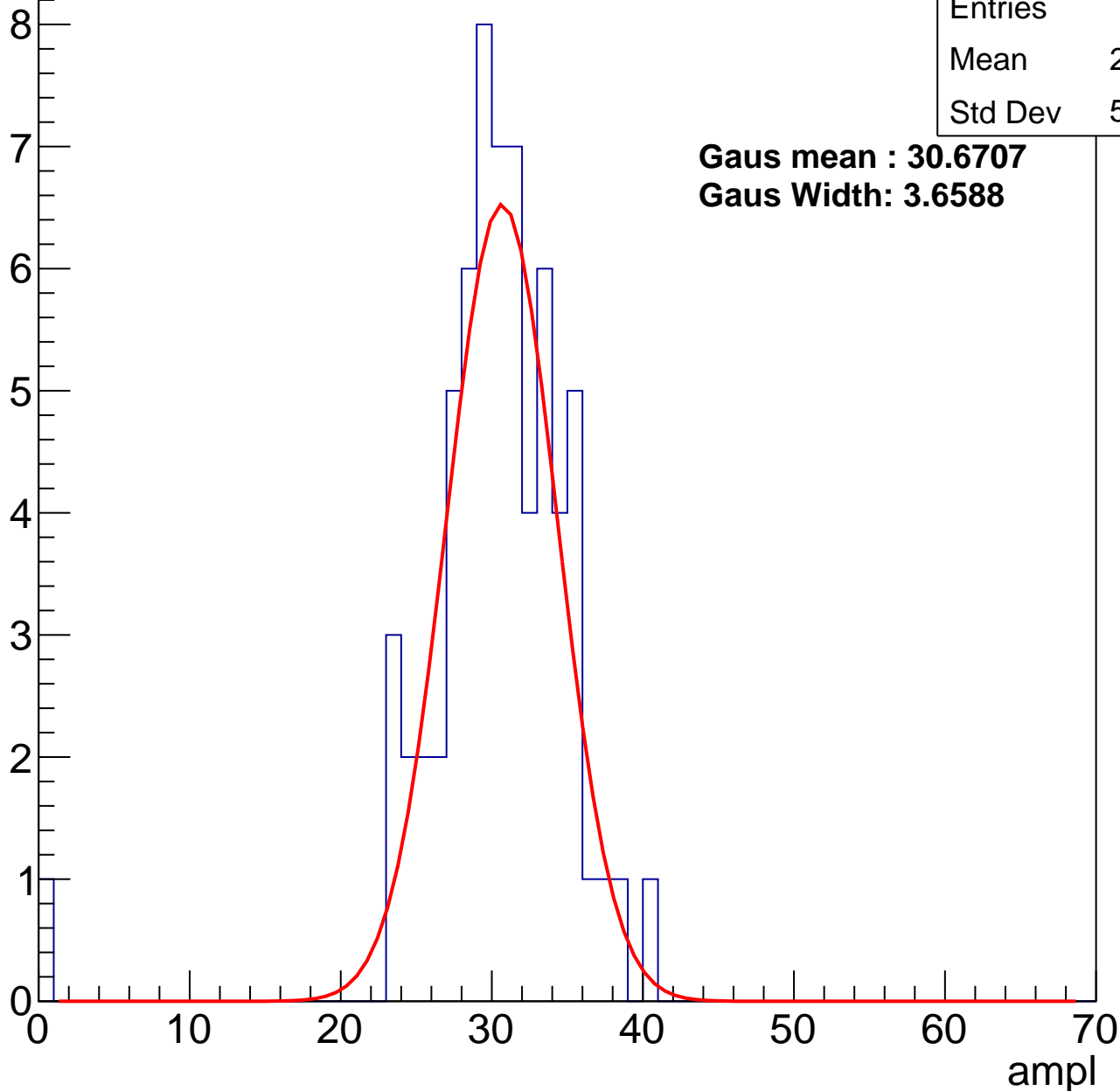
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.83
Std Dev	5.209

Gaus mean : 30.6707

Gaus Width: 3.6588



B1L100S, U11-ch64, adc1

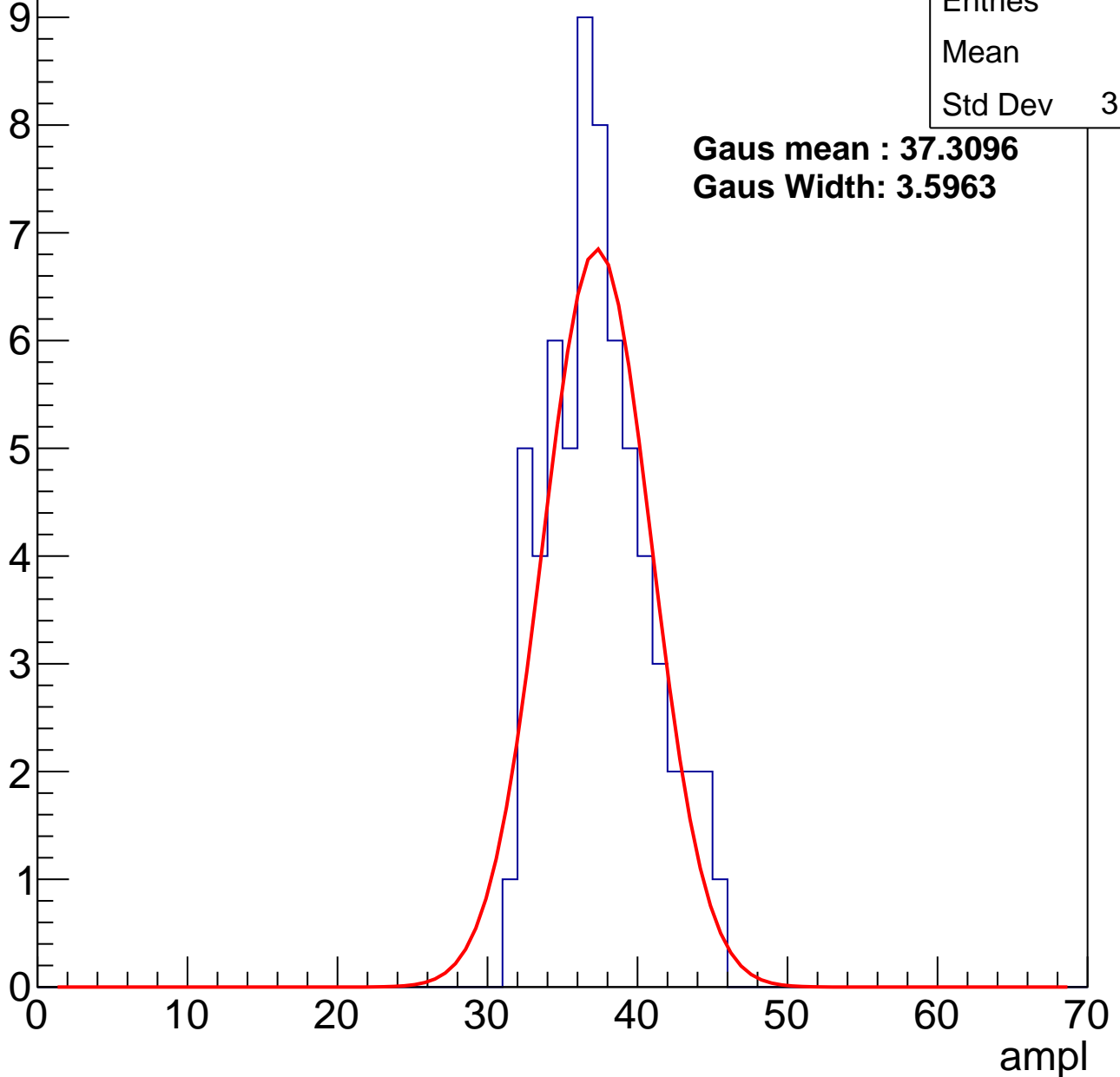
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37
Std Dev	3.338

Gaus mean : 37.3096

Gaus Width: 3.5963



B1L100S, U11-ch64, adc2

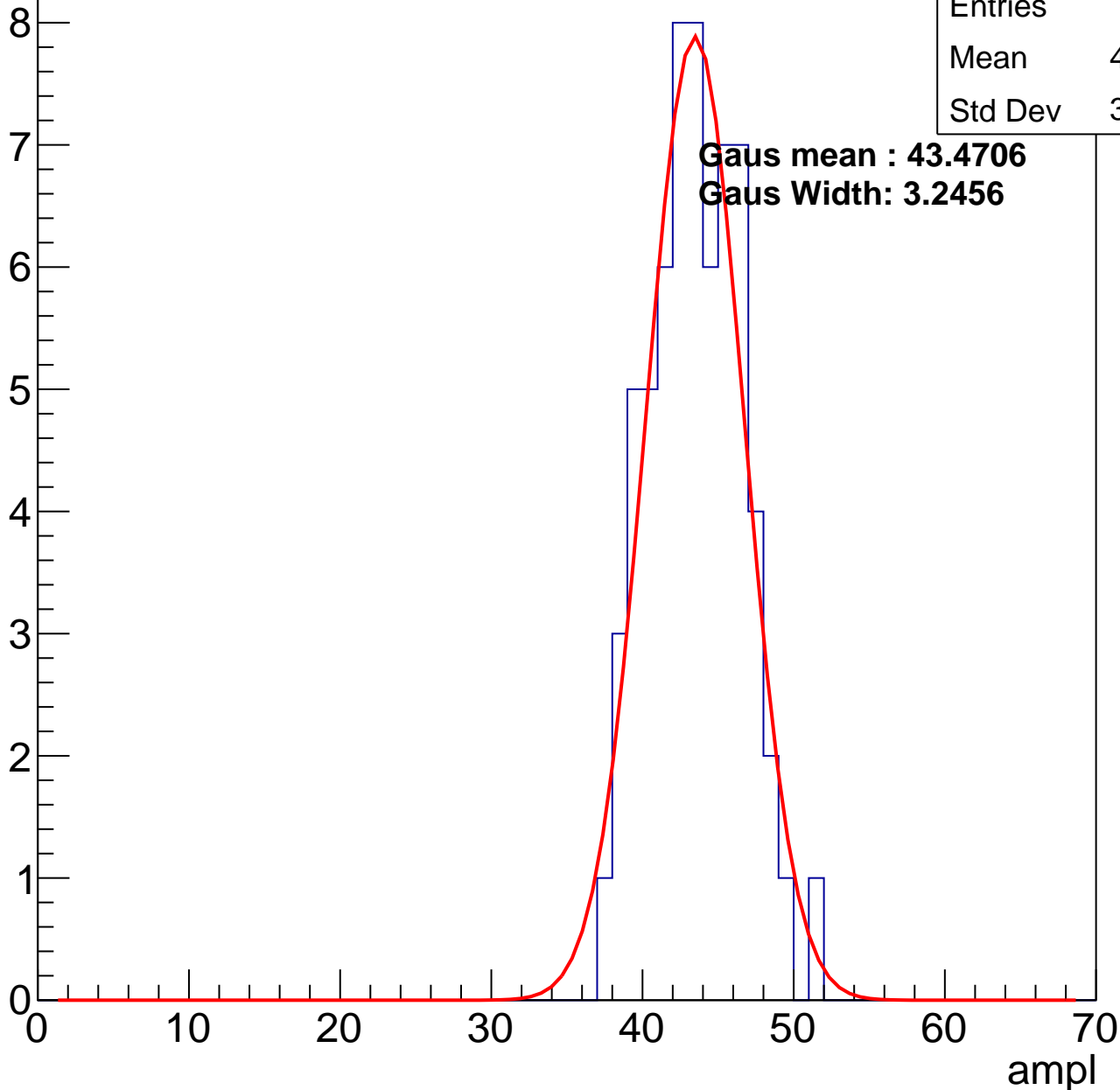
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.08
Std Dev	3.007

Gaus mean : 43.4706

Gaus Width: 3.2456

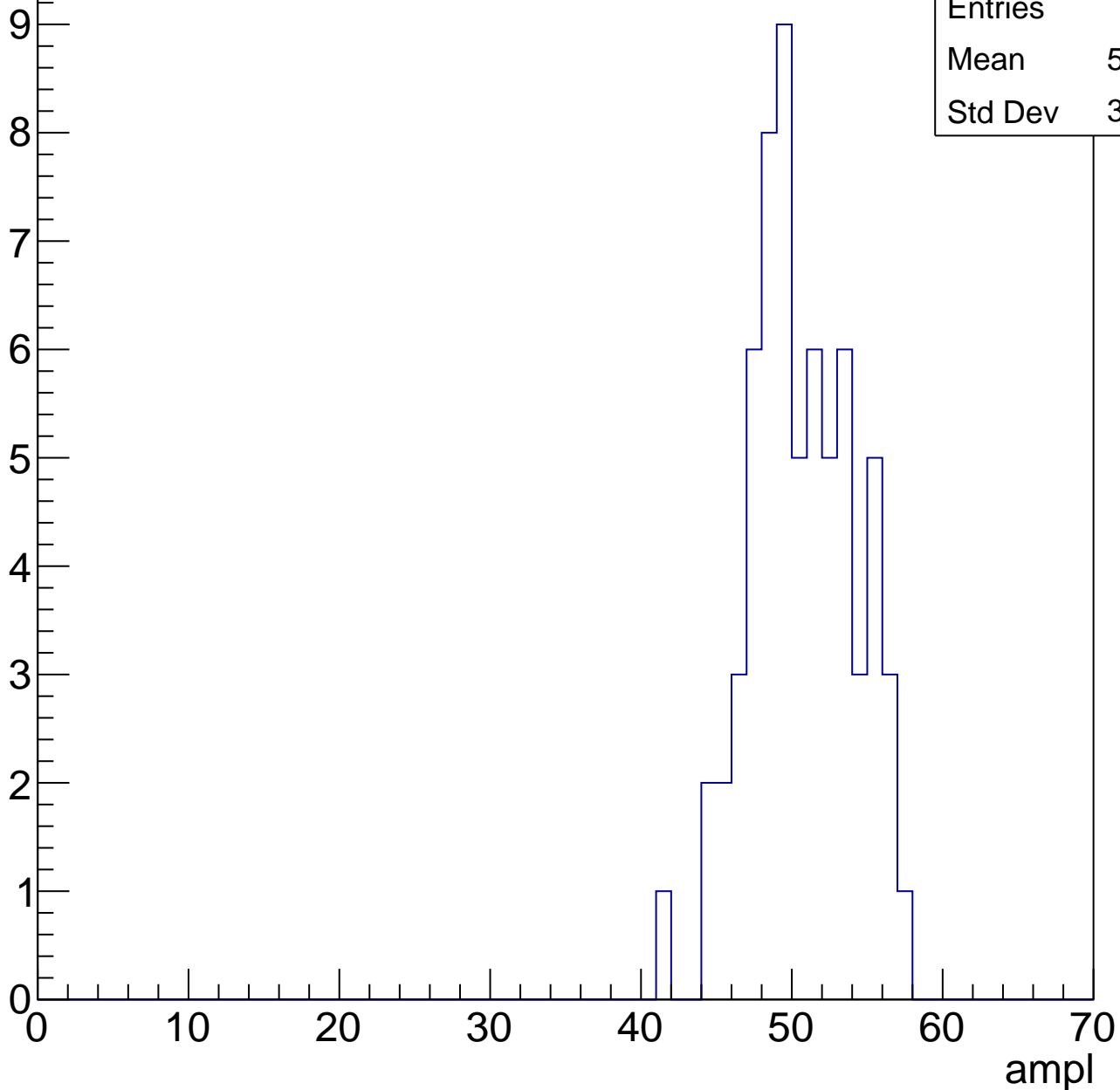


B1L100S, U11-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	50.15
Std Dev	3.407

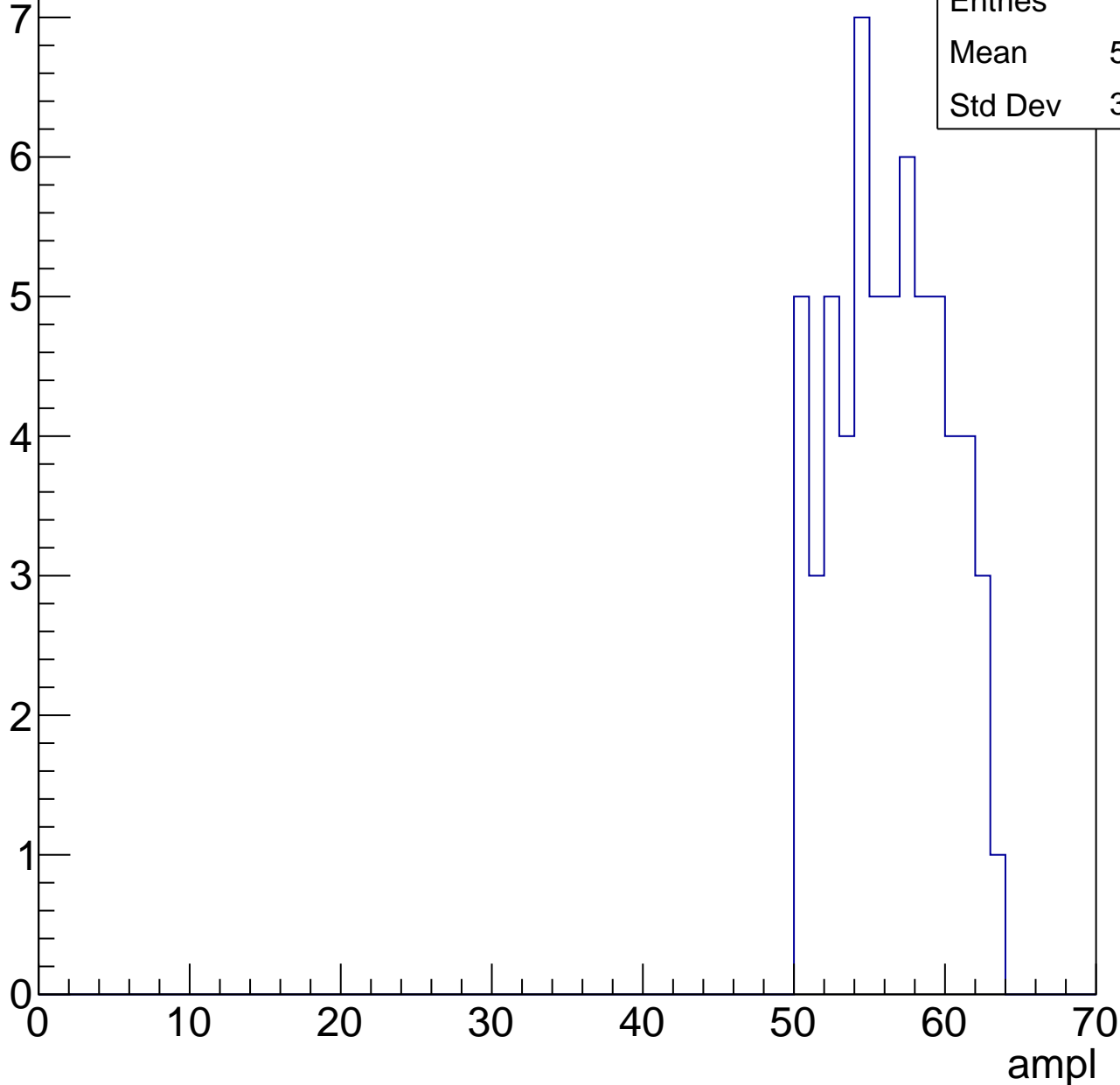


B1L100S, U11-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	55.94
Std Dev	3.583

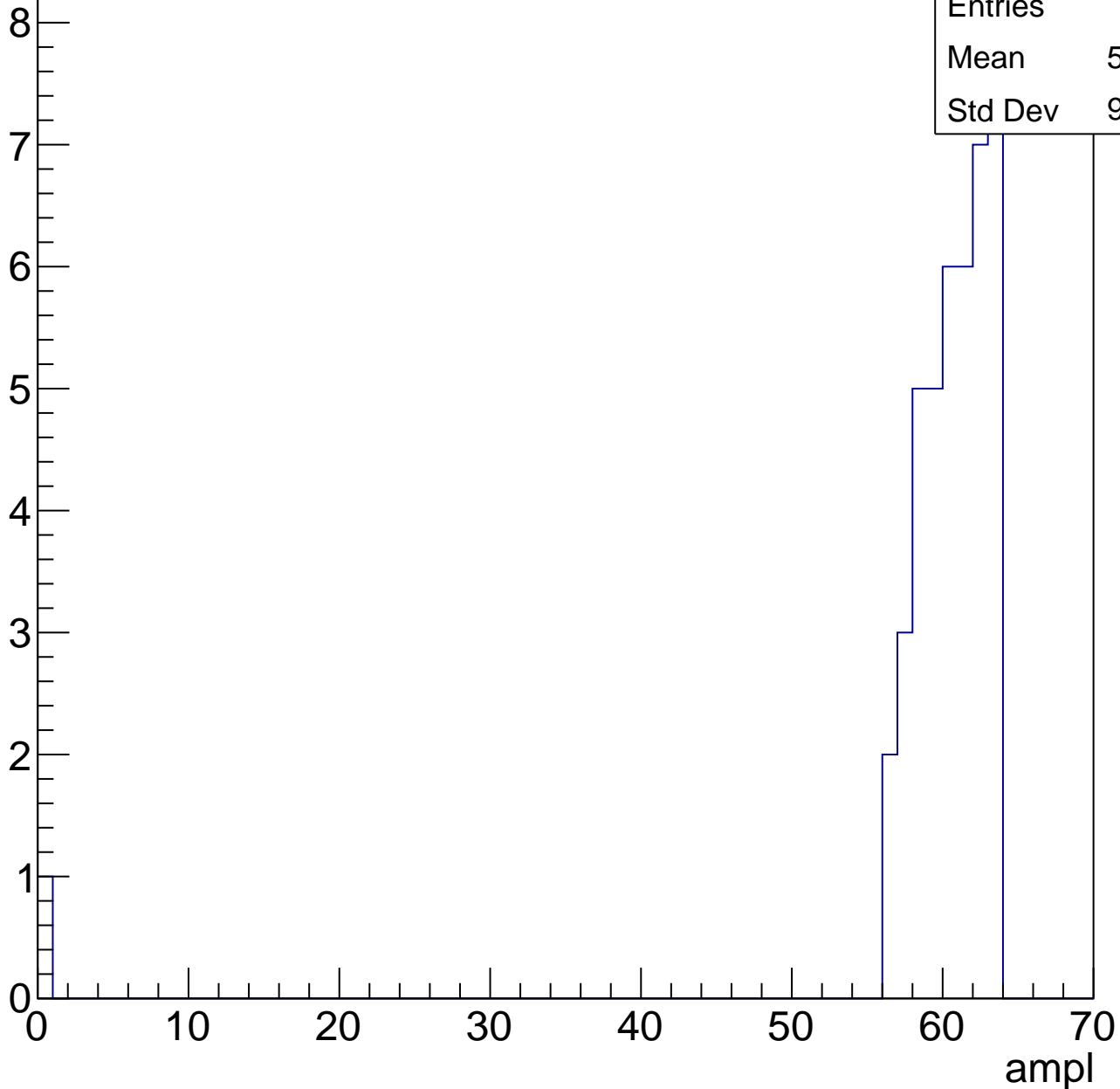


B1L100S, U11-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	58.88
Std Dev	9.322



B1L100S, U11-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch65, adc0

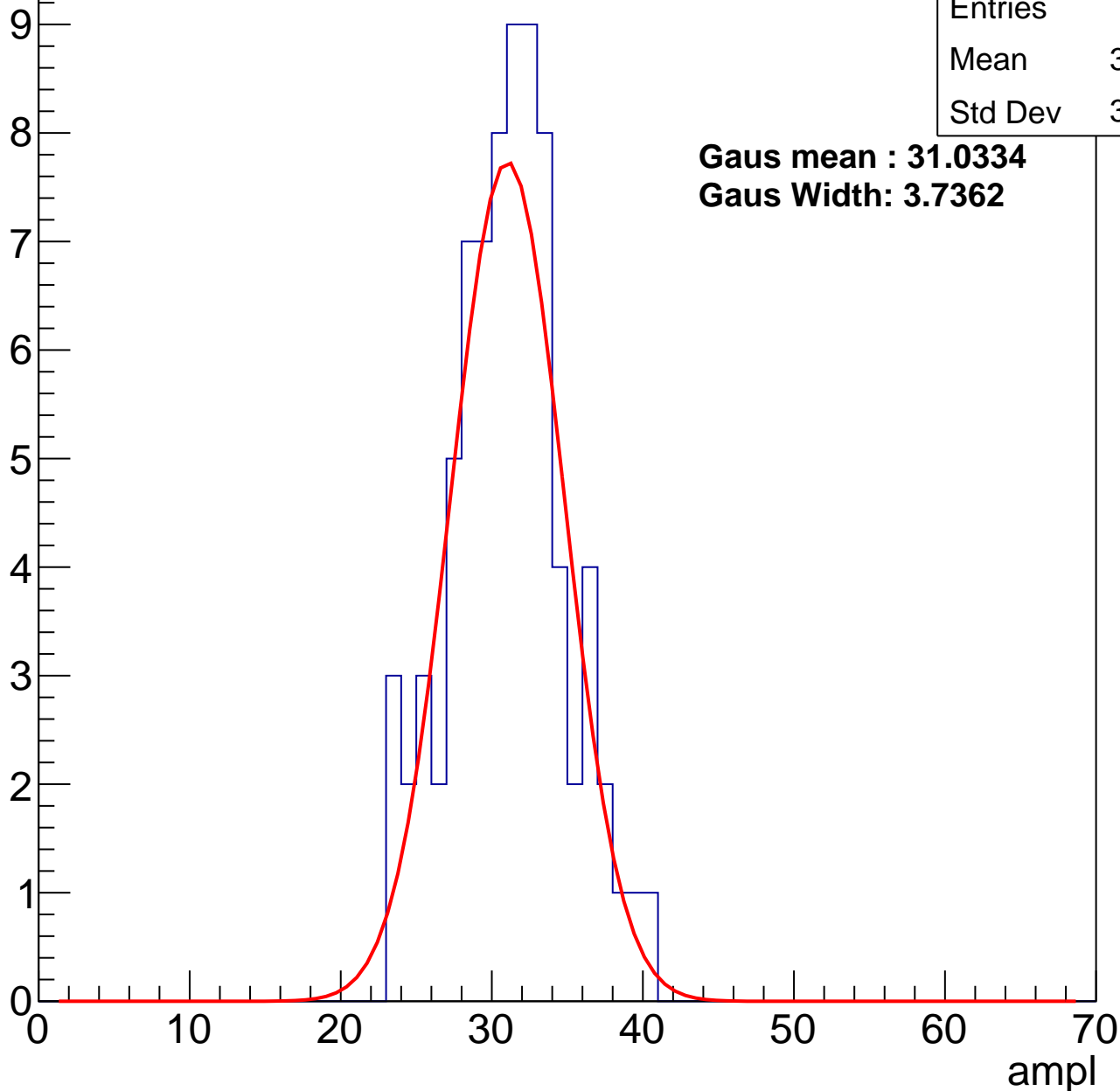
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	30.64
Std Dev	3.742

Gaus mean : 31.0334

Gaus Width: 3.7362



B1L100S, U11-ch65, adc1

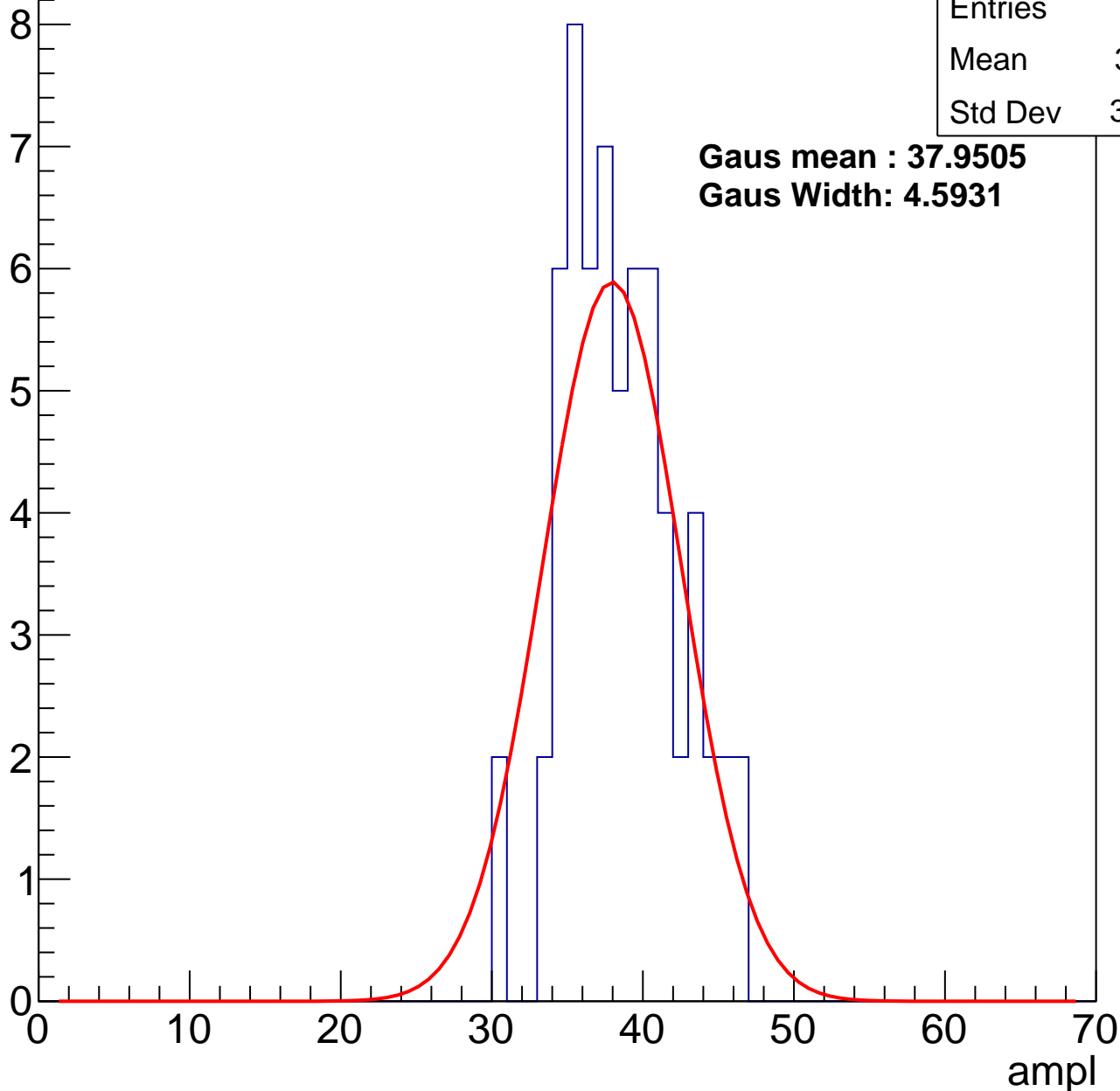
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	38.11
Std Dev	3.692

Gaus mean : 37.9505

Gaus Width: 4.5931



B1L100S, U11-ch65, adc2

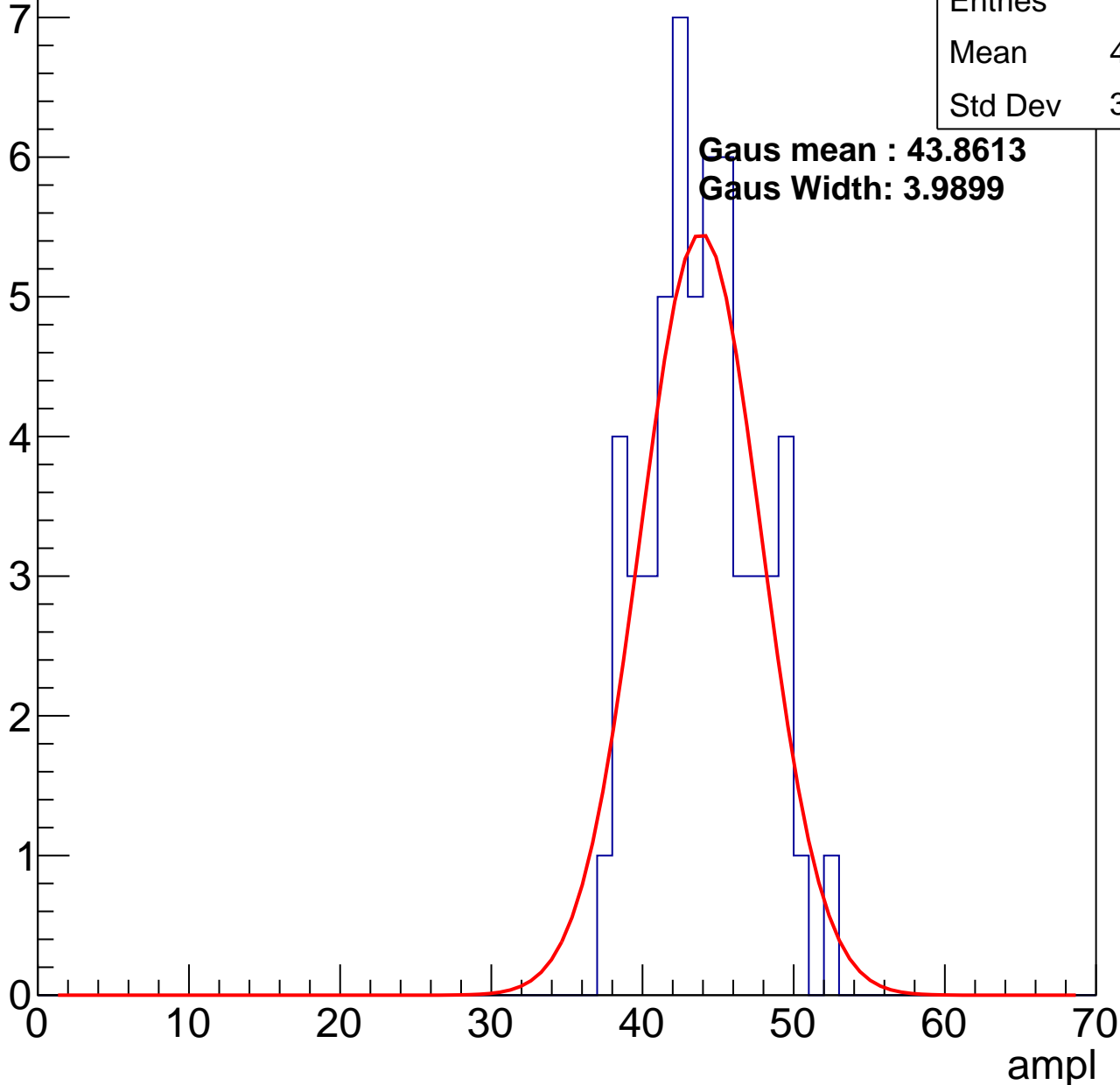
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	43.55
Std Dev	3.505

Gaus mean : 43.8613

Gaus Width: 3.9899

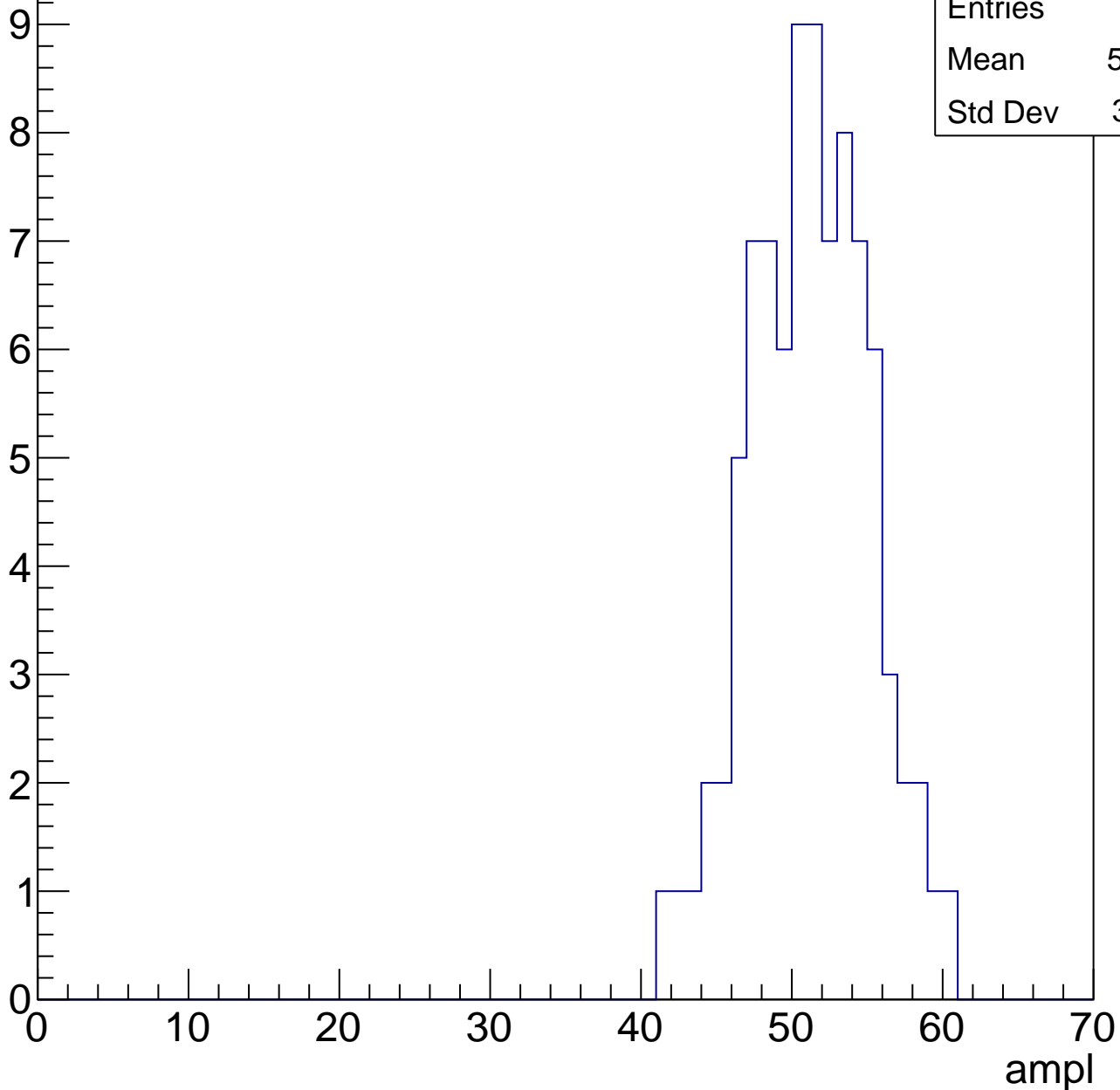


B1L100S, U11-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	50.75
Std Dev	3.911

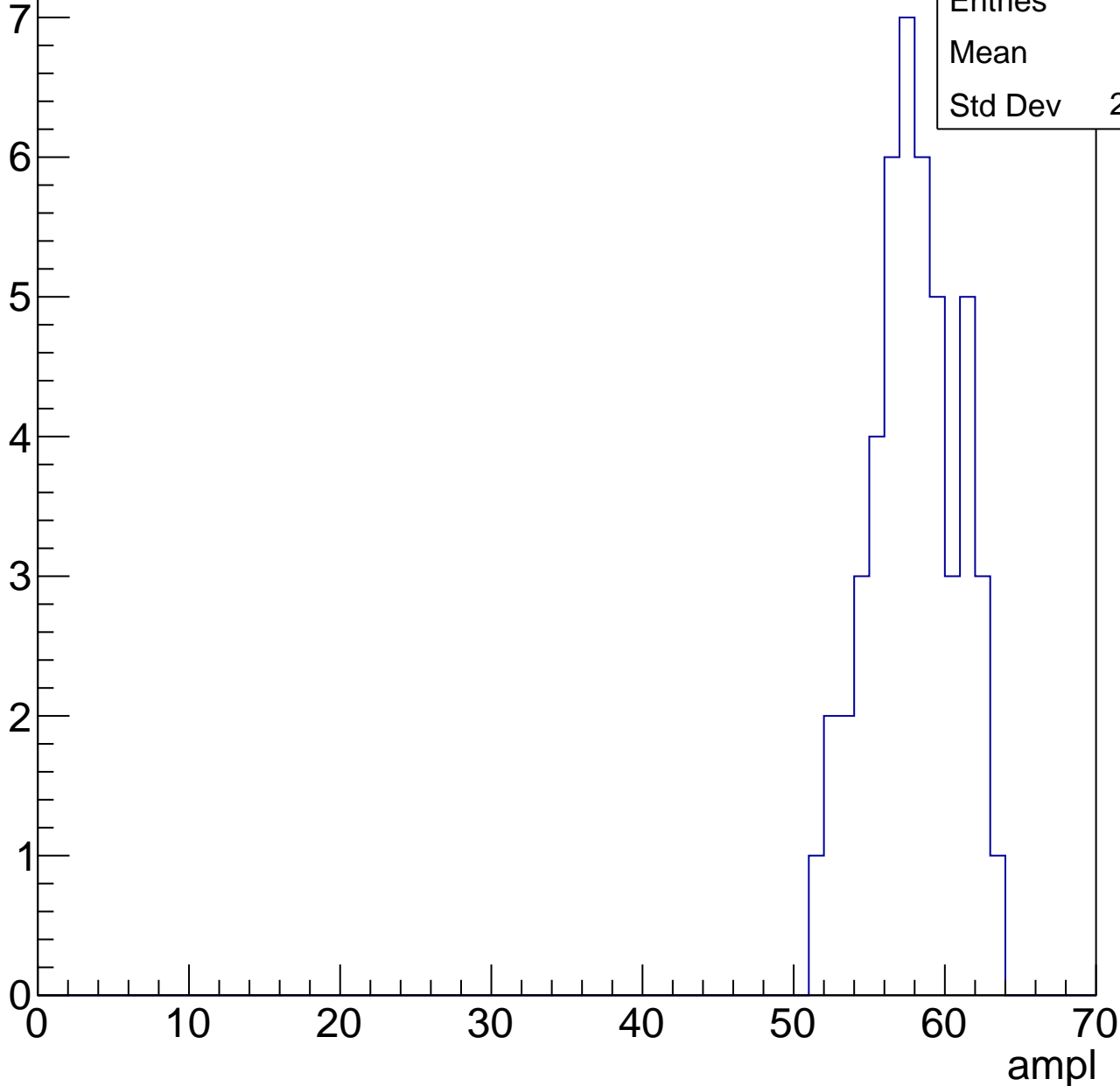


B1L100S, U11-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	57.4
Std Dev	2.899

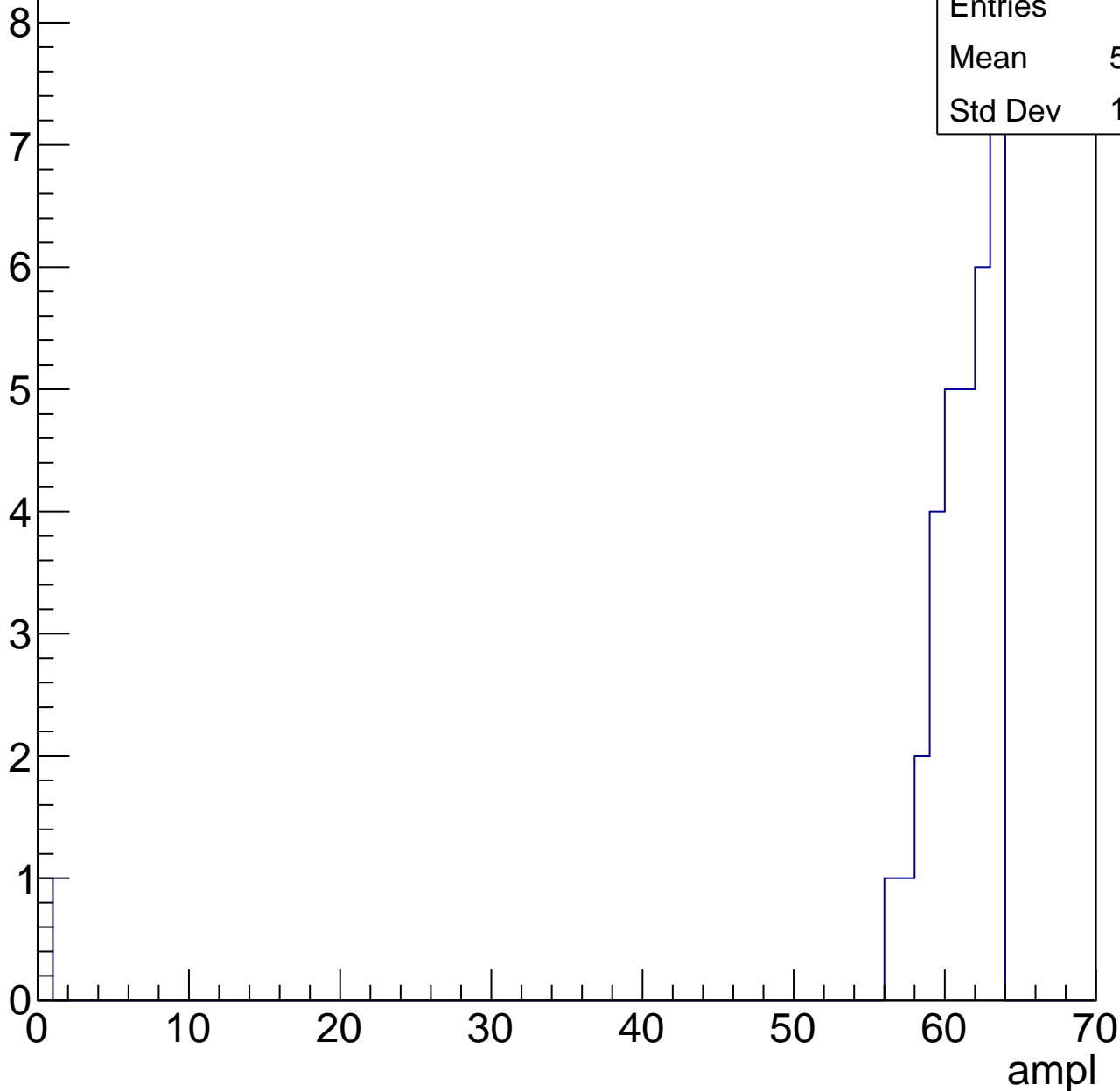


B1L100S, U11-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.97
Std Dev	10.59



B1L100S, U11-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch66, adc0

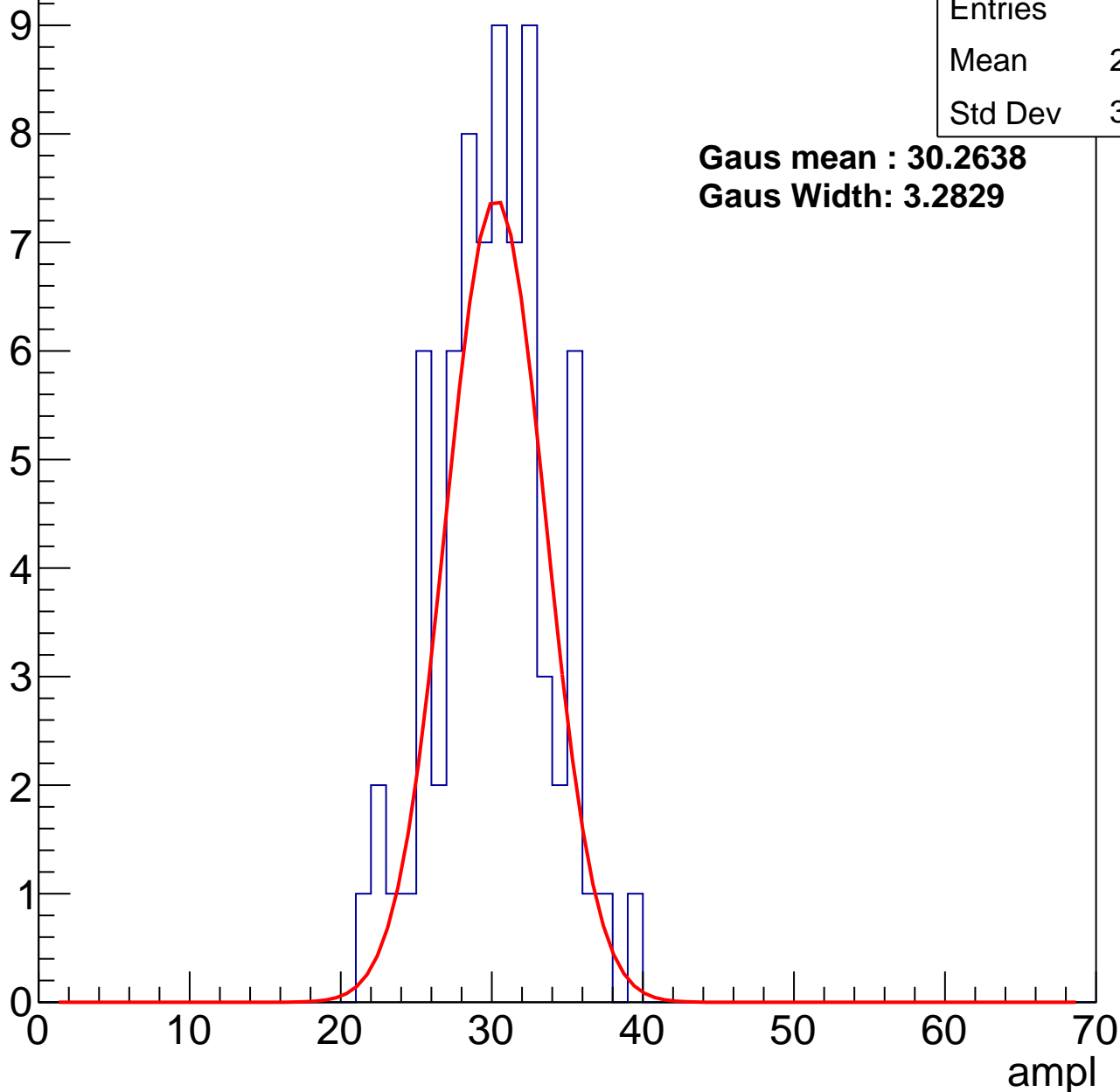
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	29.68
Std Dev	3.664

Gaus mean : 30.2638

Gaus Width: 3.2829



B1L100S, U11-ch66, adc1

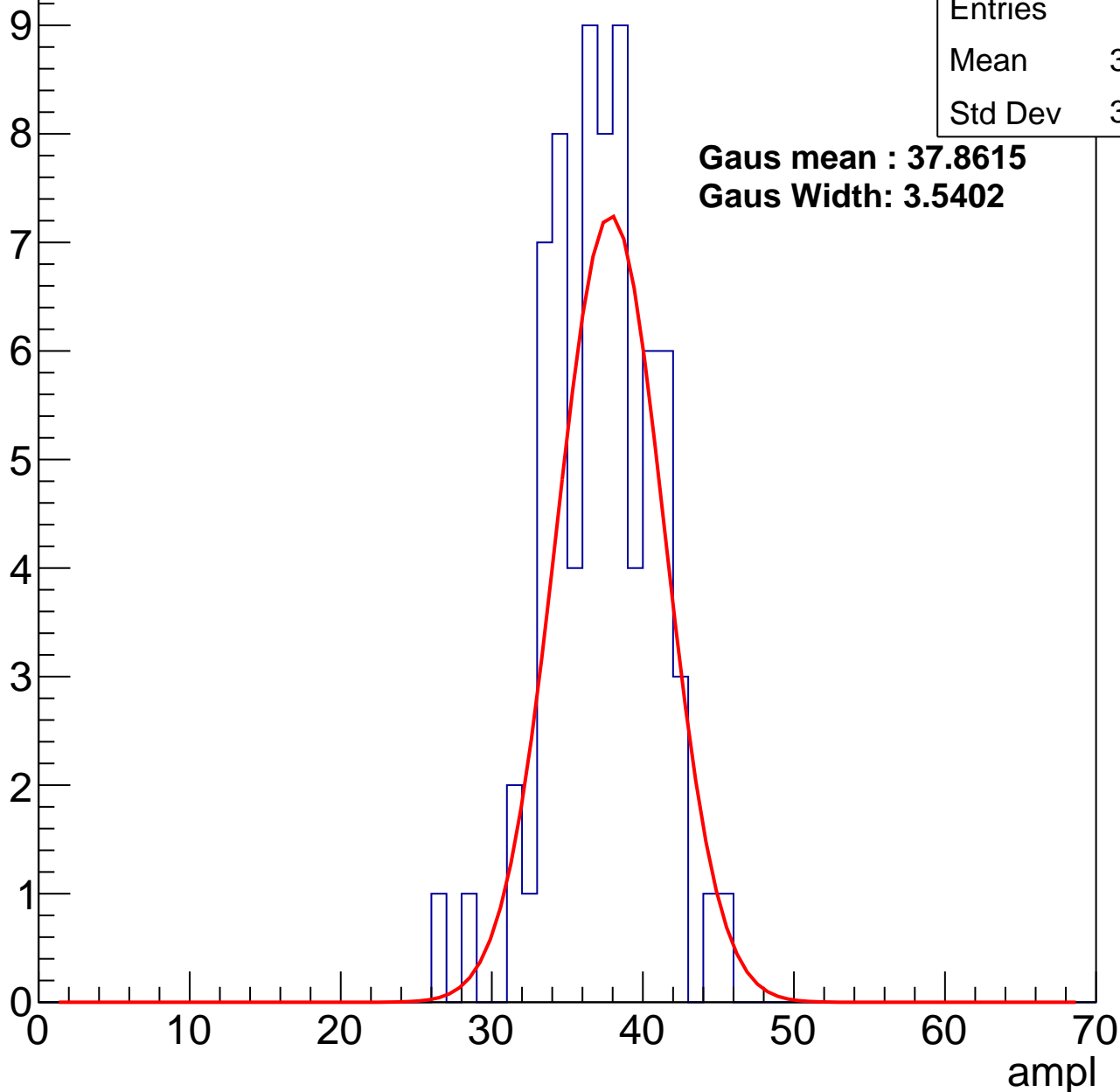
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.76
Std Dev	3.494

Gaus mean : 37.8615

Gaus Width: 3.5402



B1L100S, U11-ch66, adc2

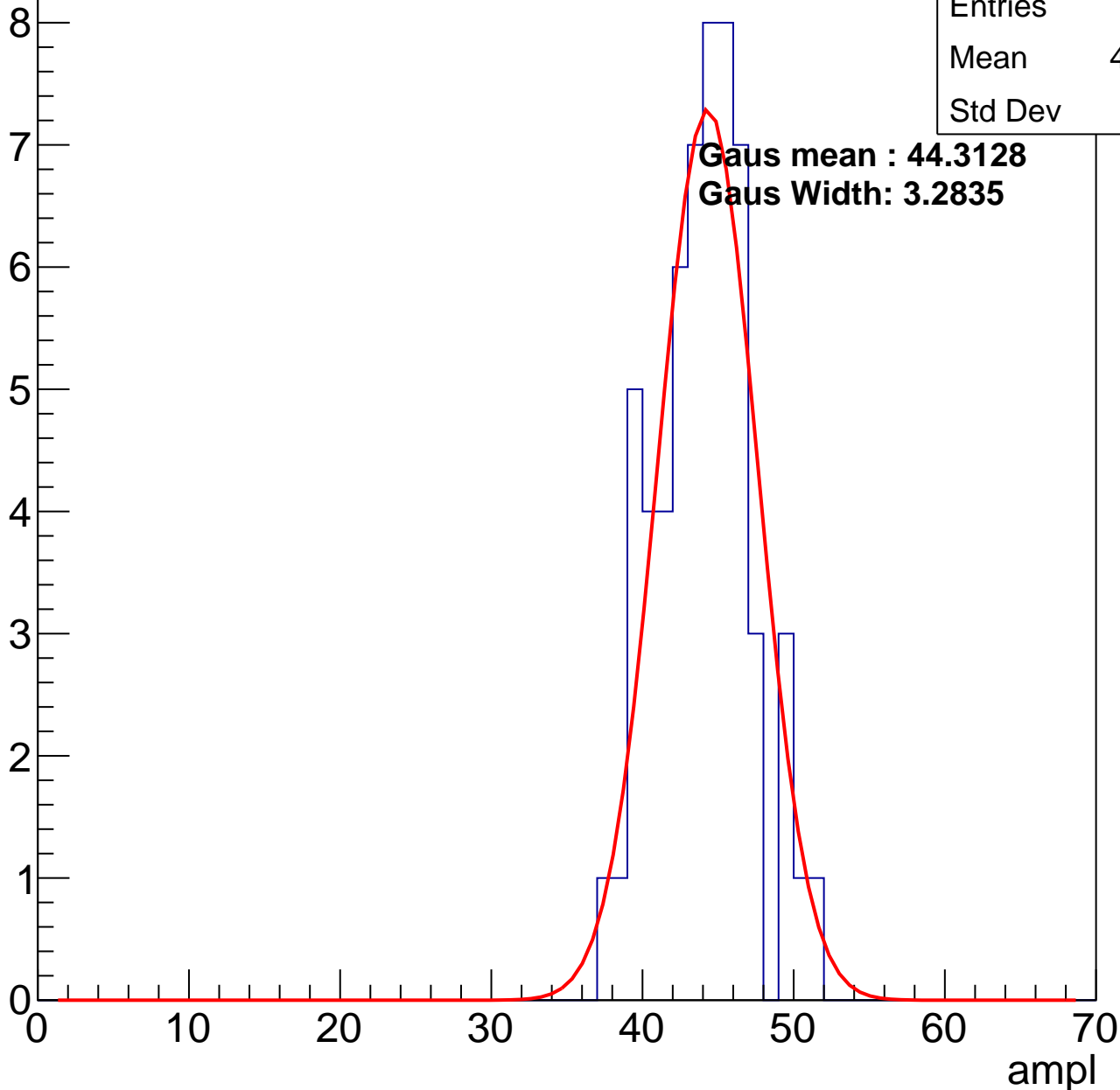
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.56
Std Dev	3.06

Gaus mean : 44.3128

Gaus Width: 3.2835

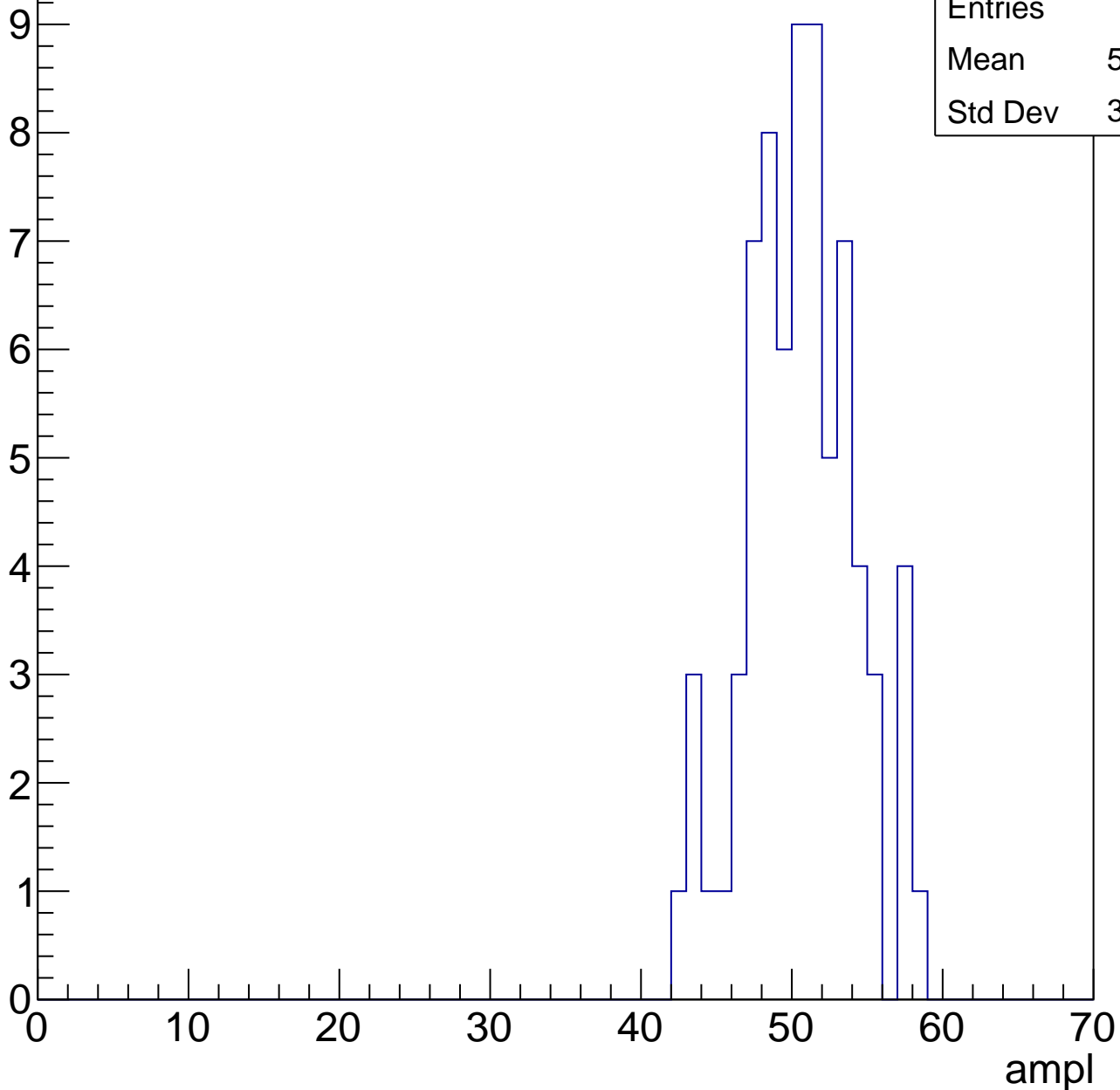


B1L100S, U11-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

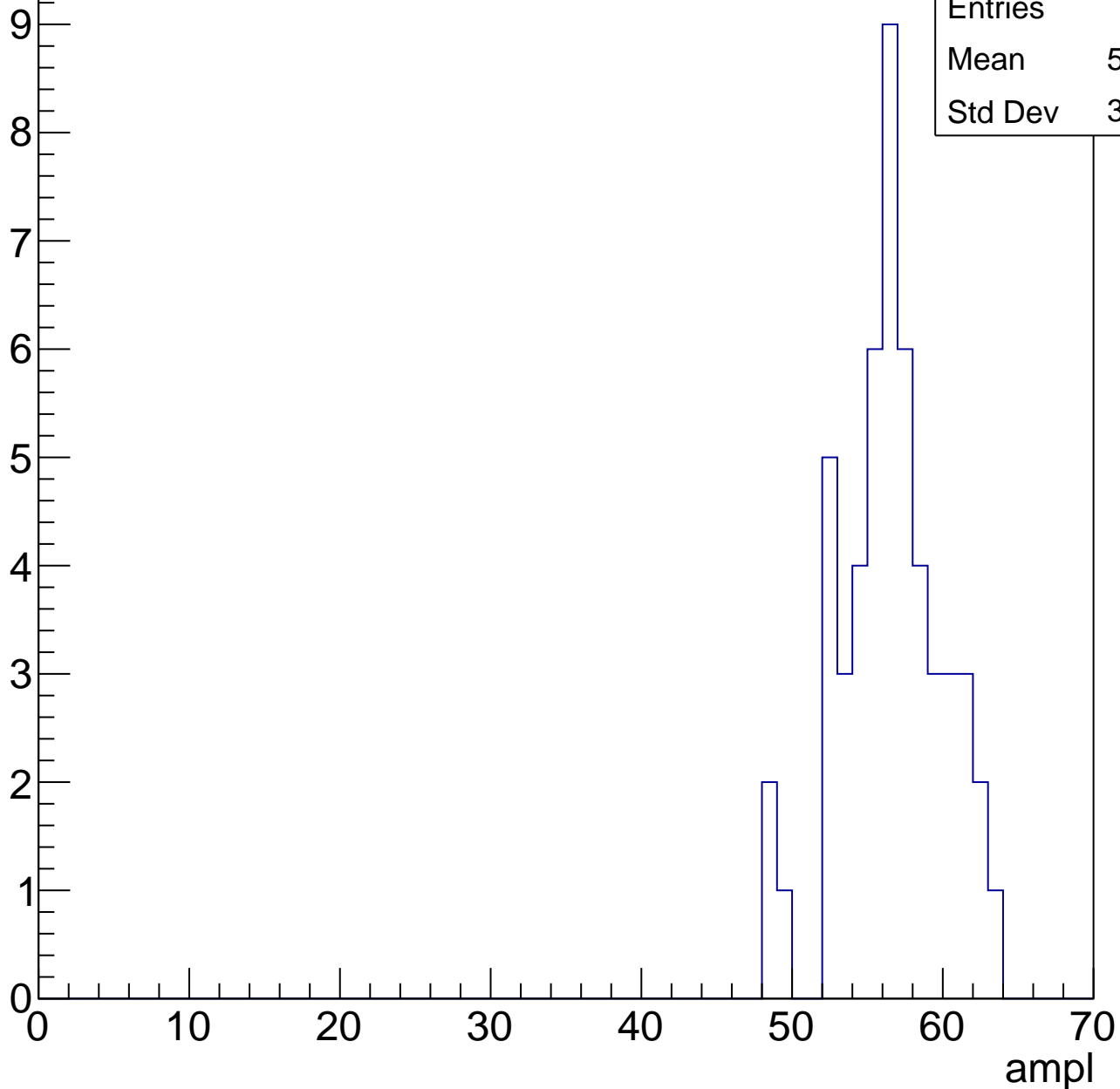
Entries	72
Mean	50.17
Std Dev	3.555



B1L100S, U11-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

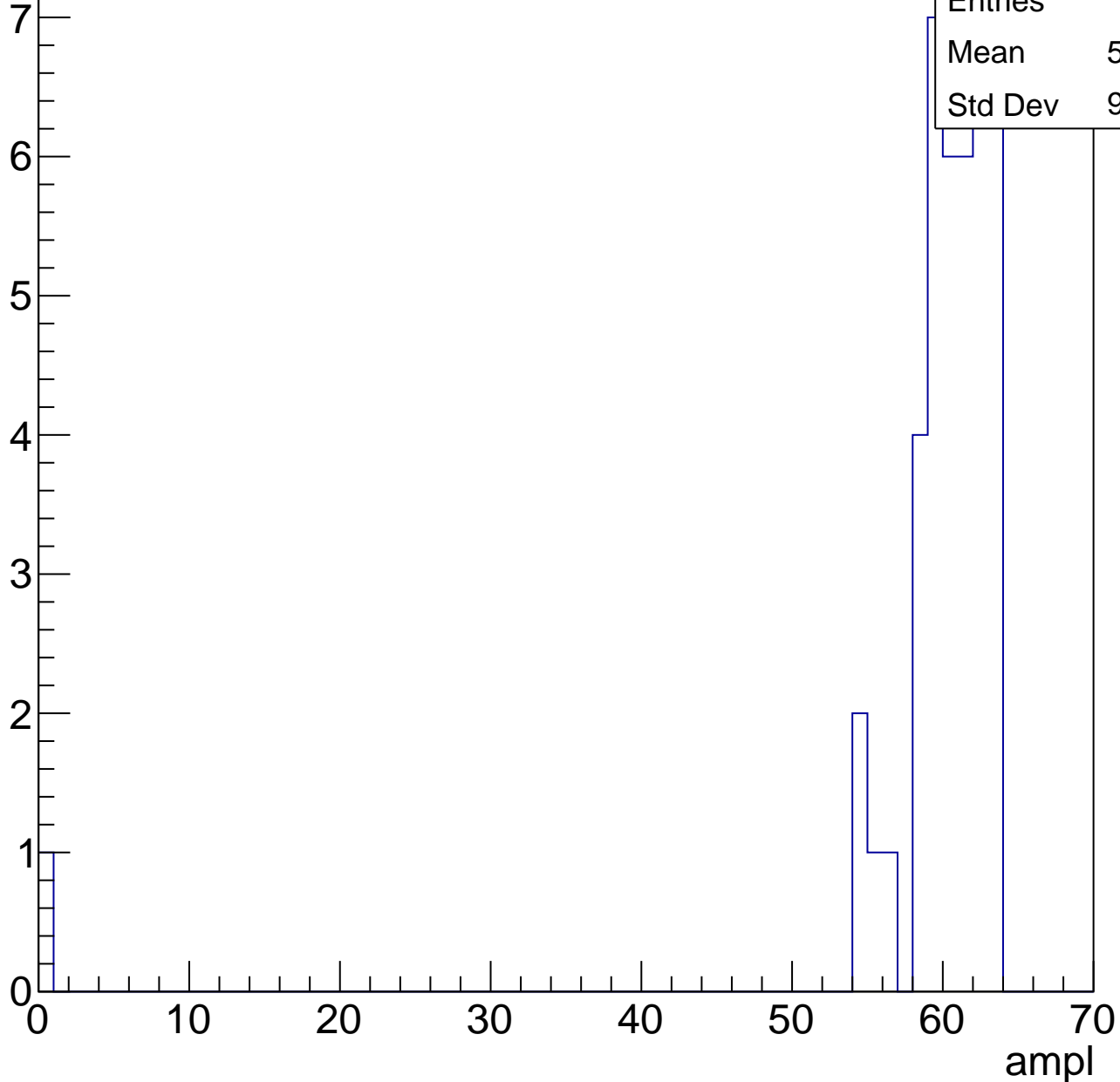


Entries	52
Mean	56.06
Std Dev	3.394

B1L100S, U11-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

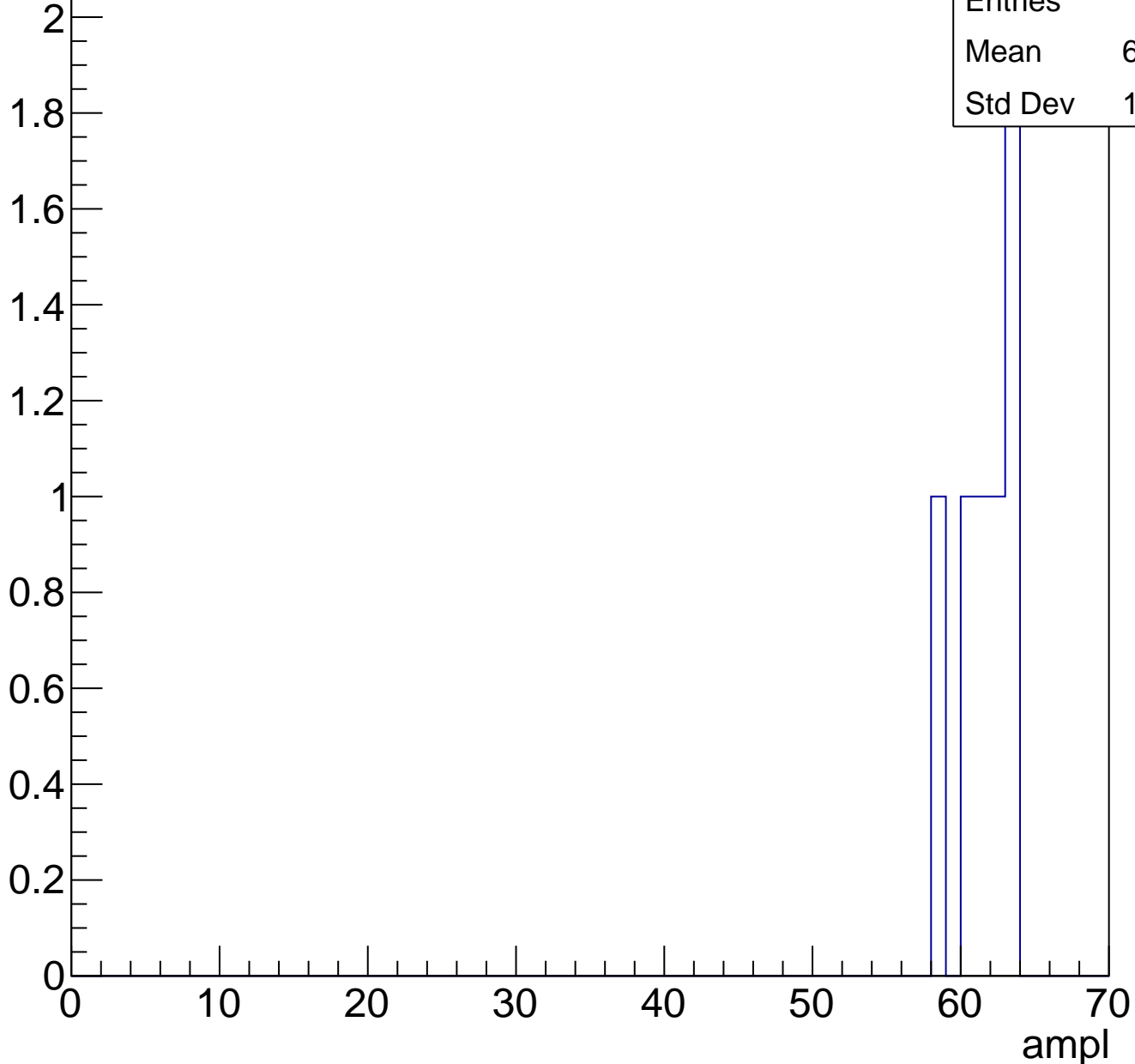
Entry



B1L100S, U11-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	6
Mean	61.17
Std Dev	1.772

B1L100S, U11-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch67, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	31.57
Std Dev	3.453

Gaus mean : 32.2616

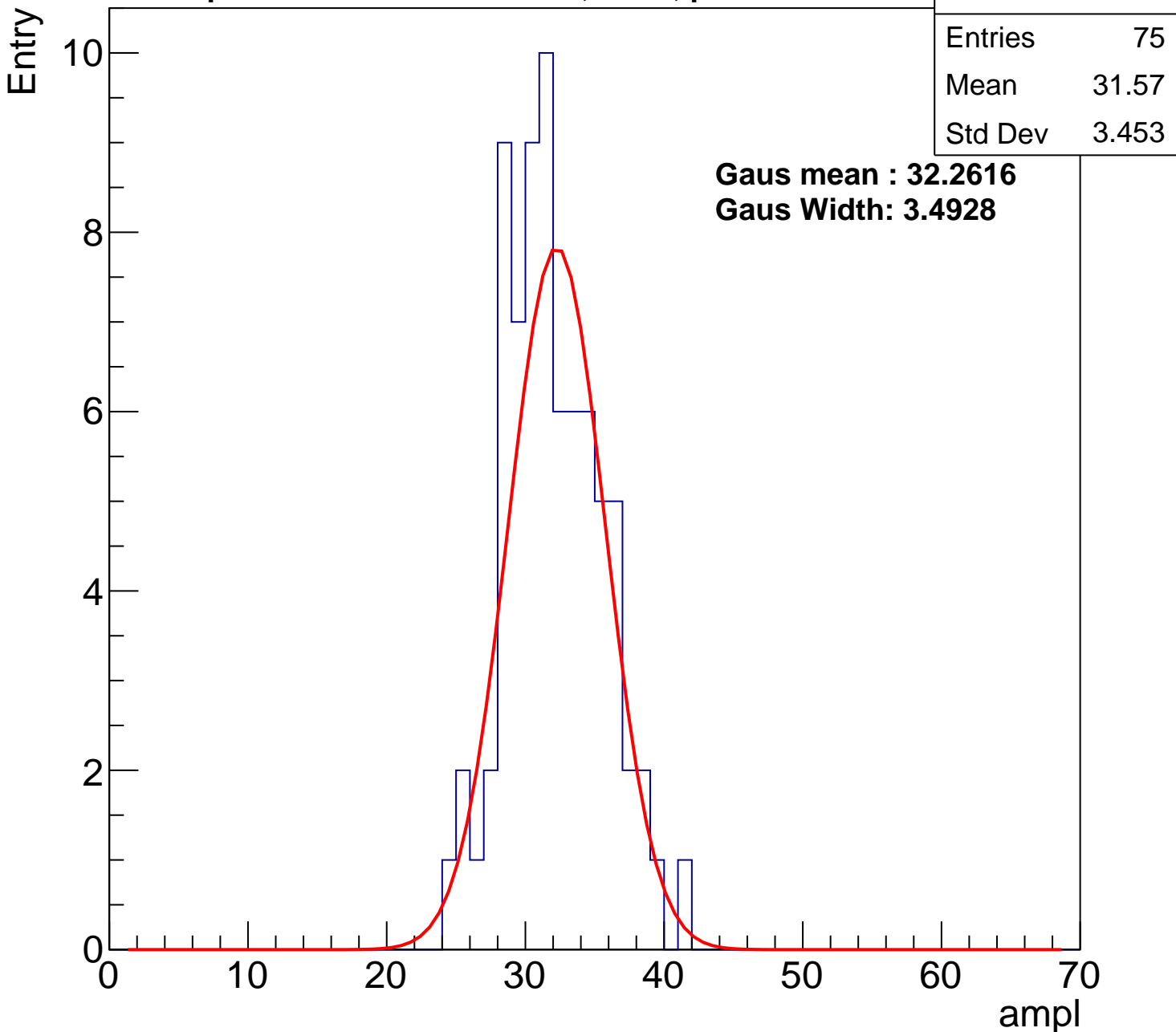
Gaus Width: 3.4928

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch67, adc1

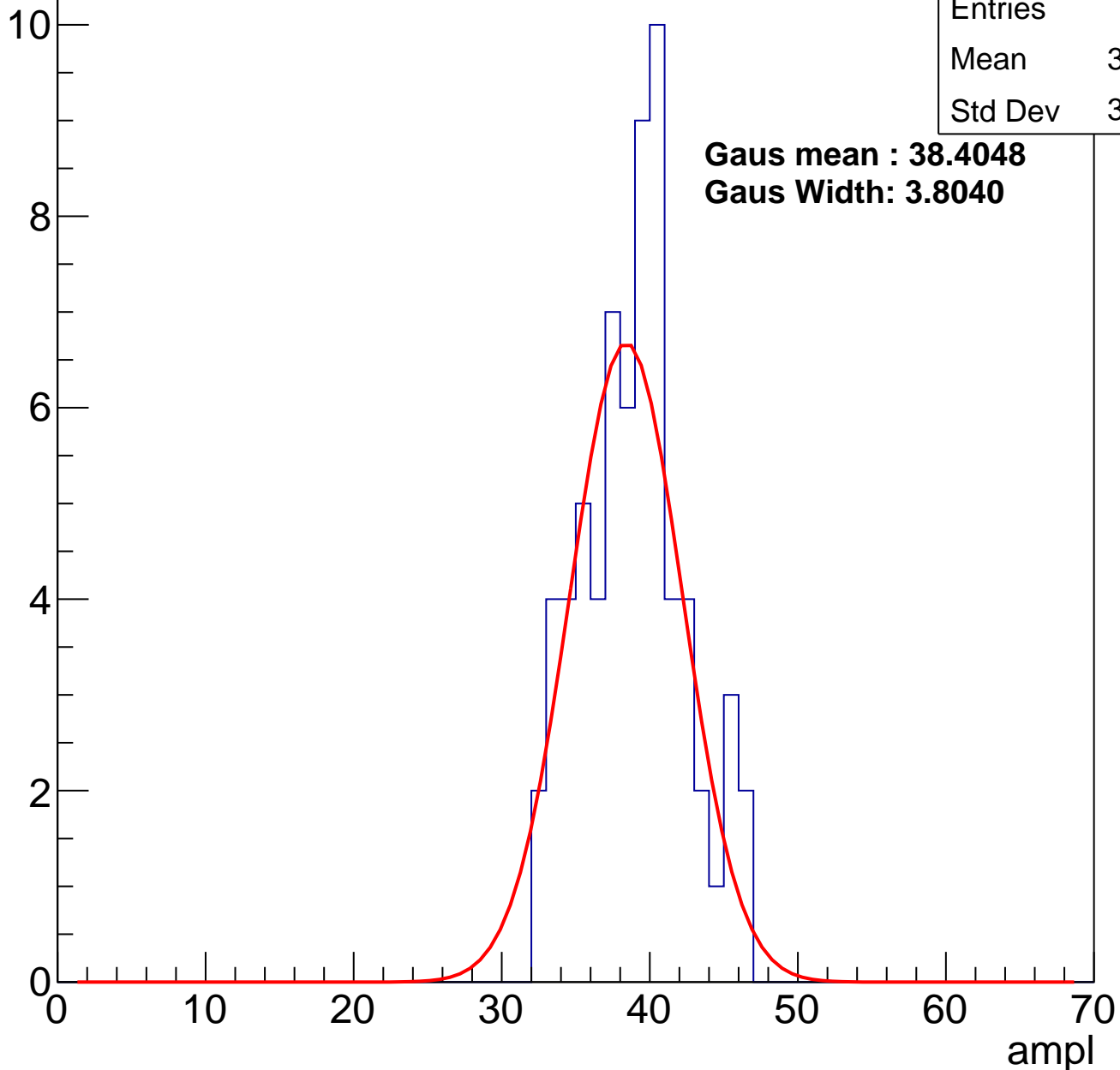
calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	38.48
Std Dev	3.453

Gaus mean : 38.4048

Gaus Width: 3.8040

Entry



B1L100S, U11-ch67, adc2

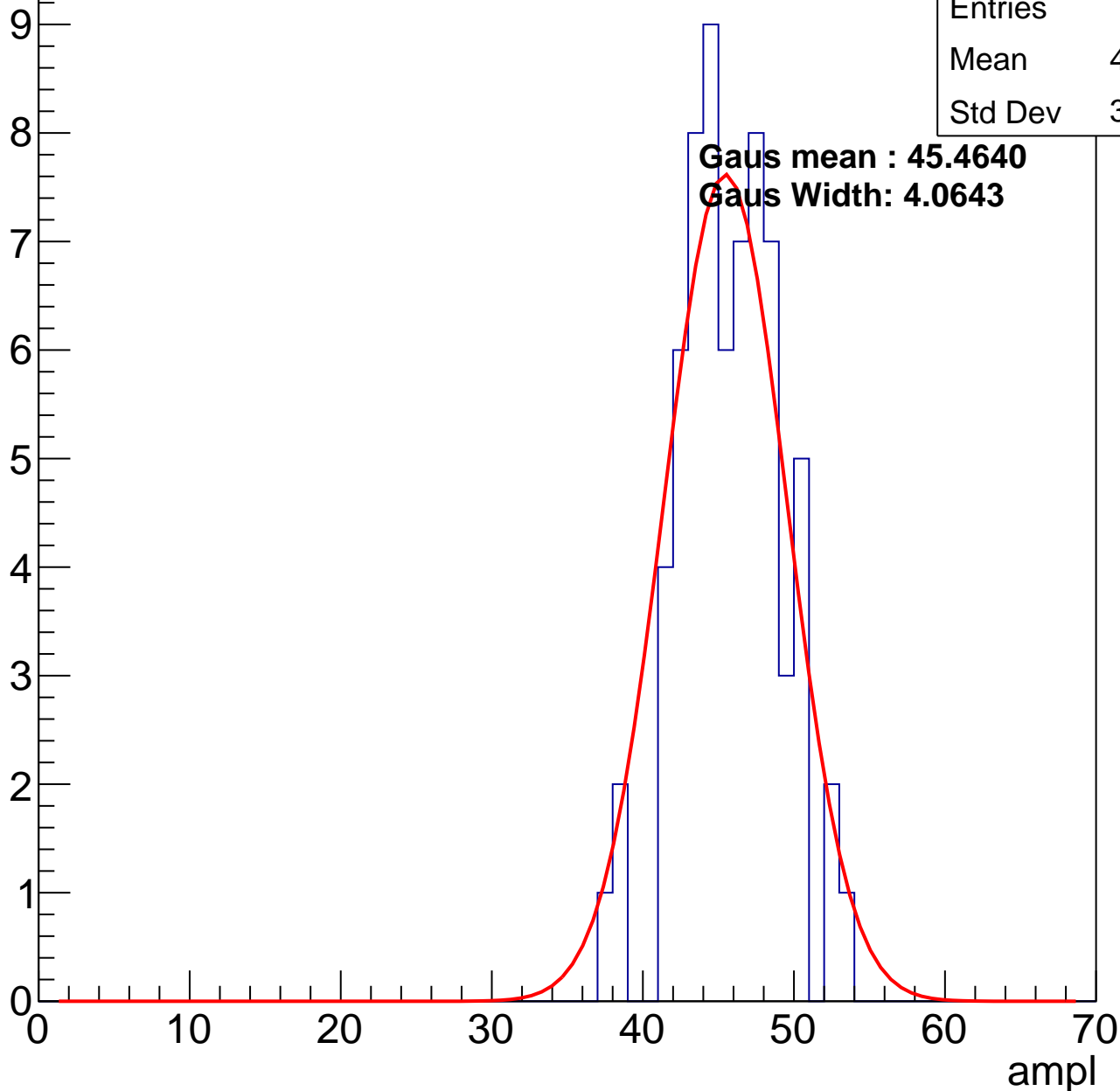
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	45.32
Std Dev	3.299

Gaus mean : 45.4640

Gaus Width: 4.0643

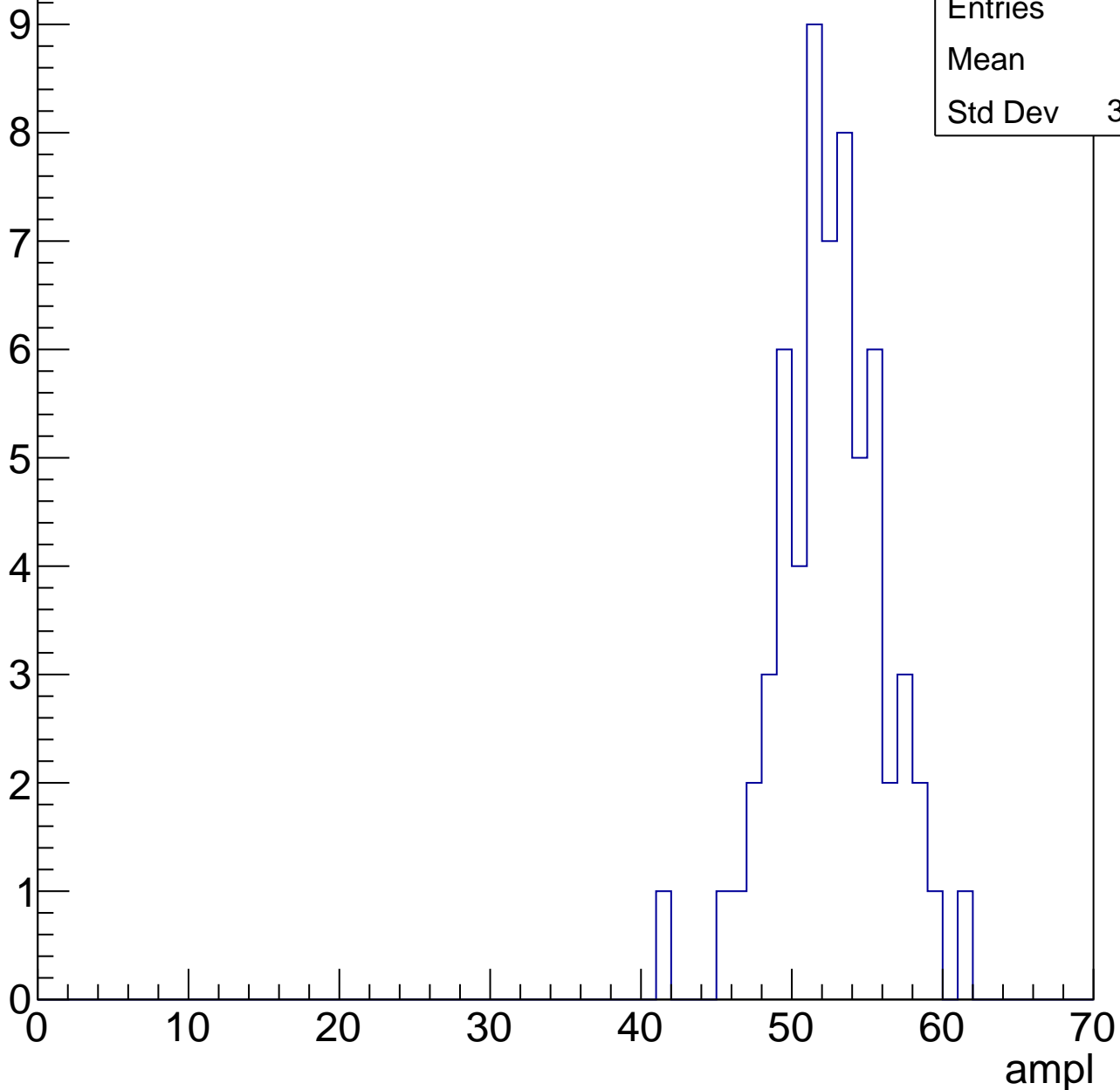


B1L100S, U11-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

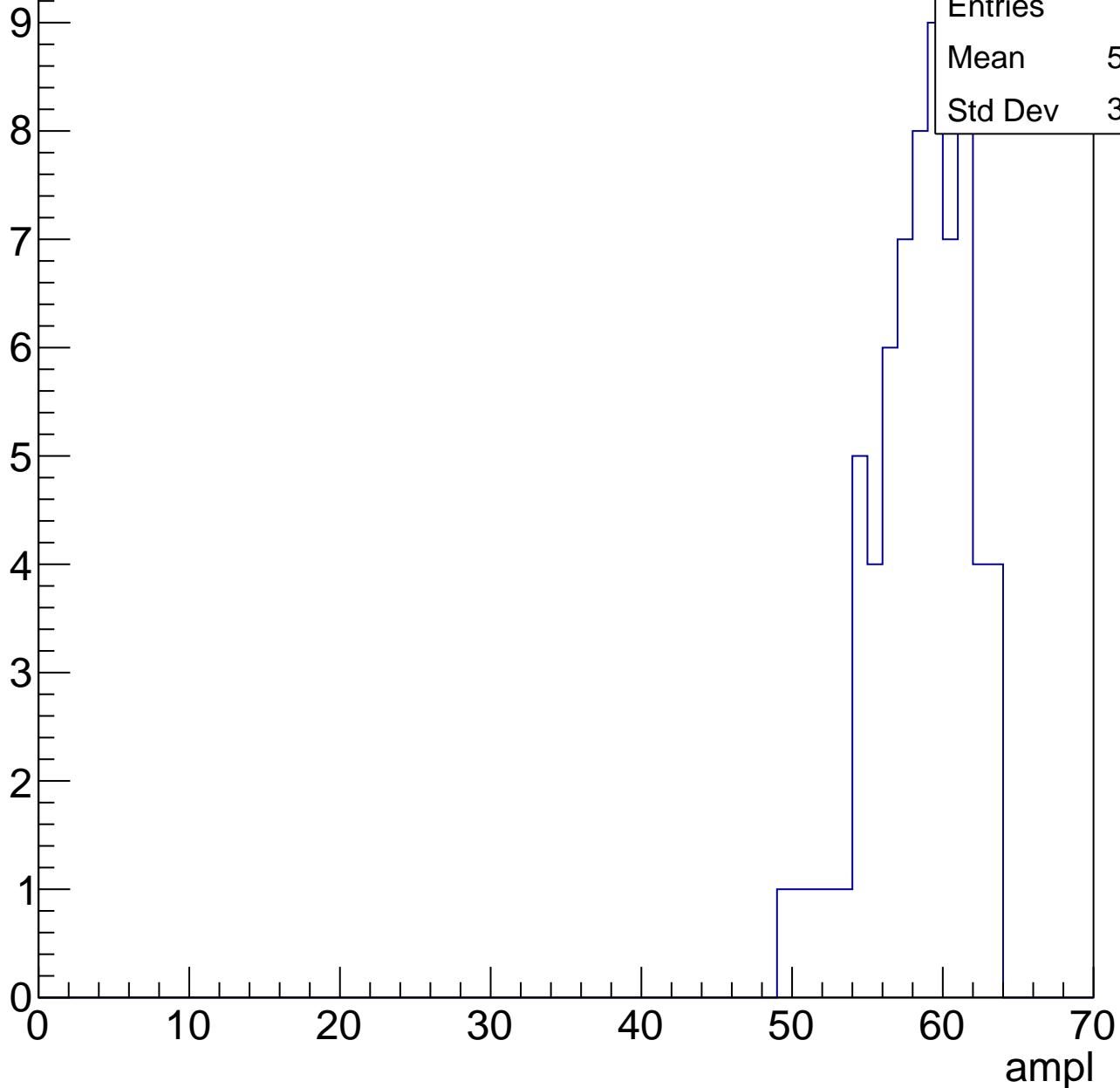
Entries	62
Mean	52.1
Std Dev	3.532



B1L100S, U11-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

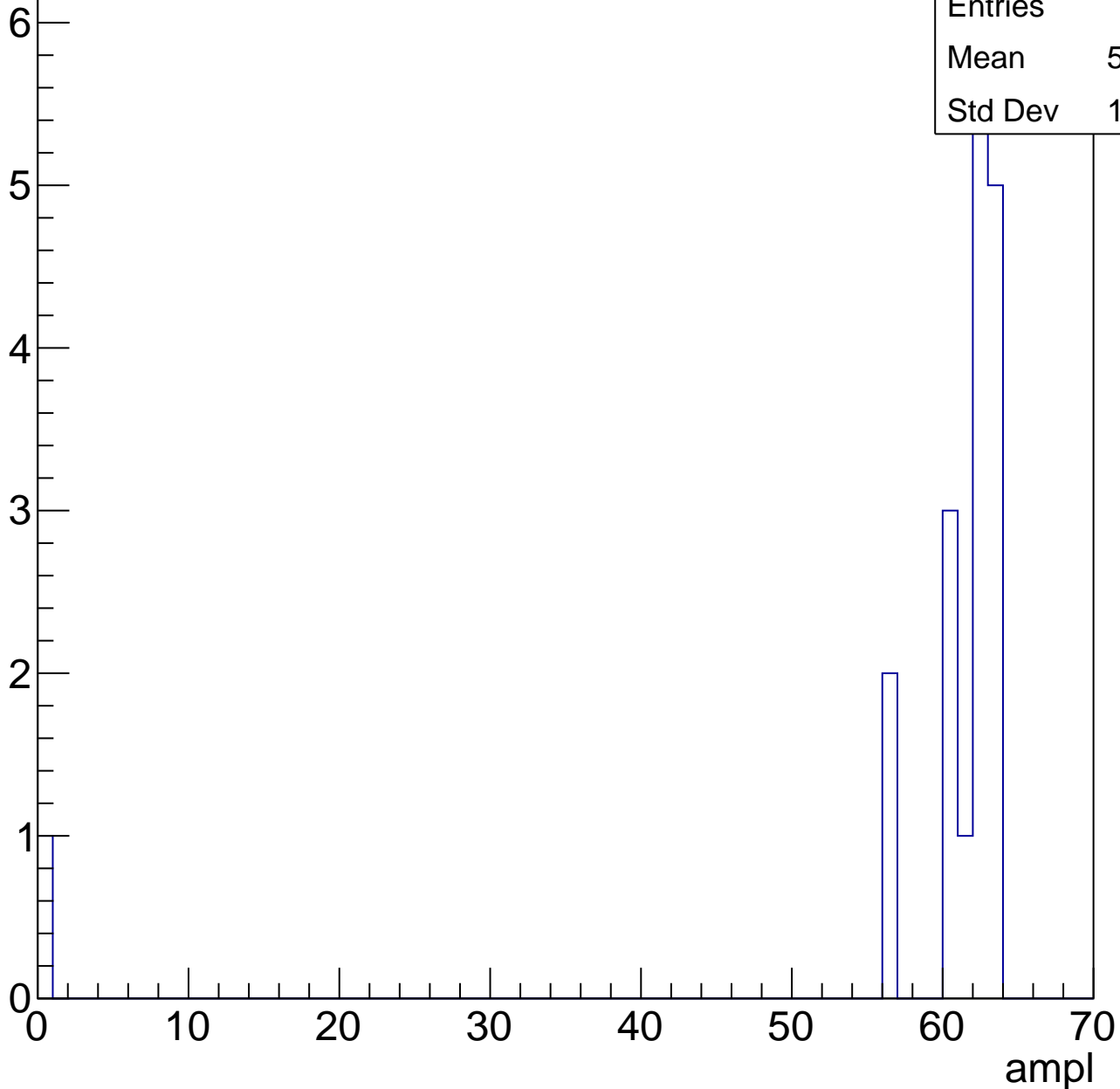


B1L100S, U11-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	57.78
Std Dev	14.17



B1L100S, U11-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch68, adc0

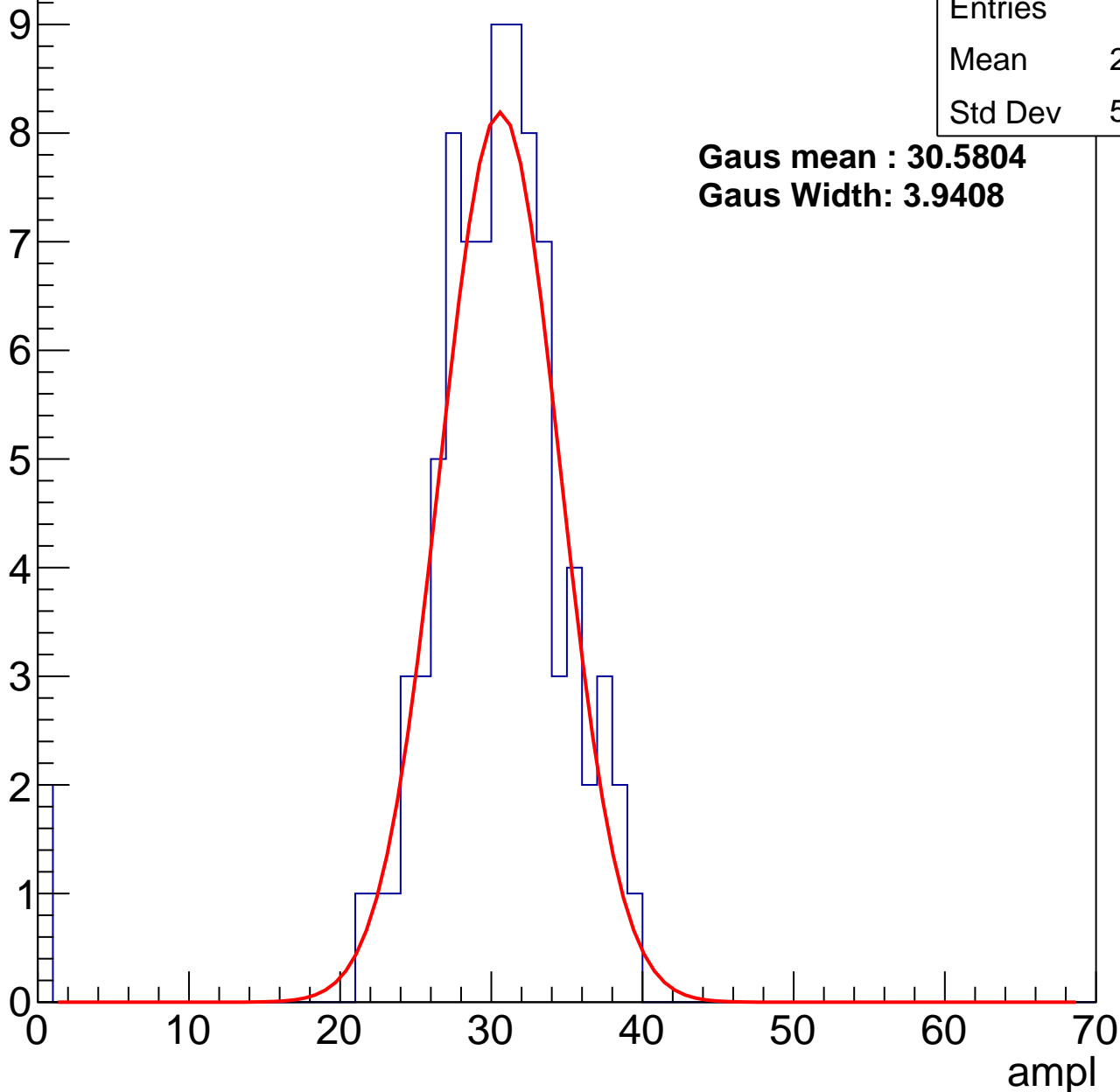
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	29.47
Std Dev	5.925

Gaus mean : 30.5804

Gaus Width: 3.9408



B1L100S, U11-ch68, adc1

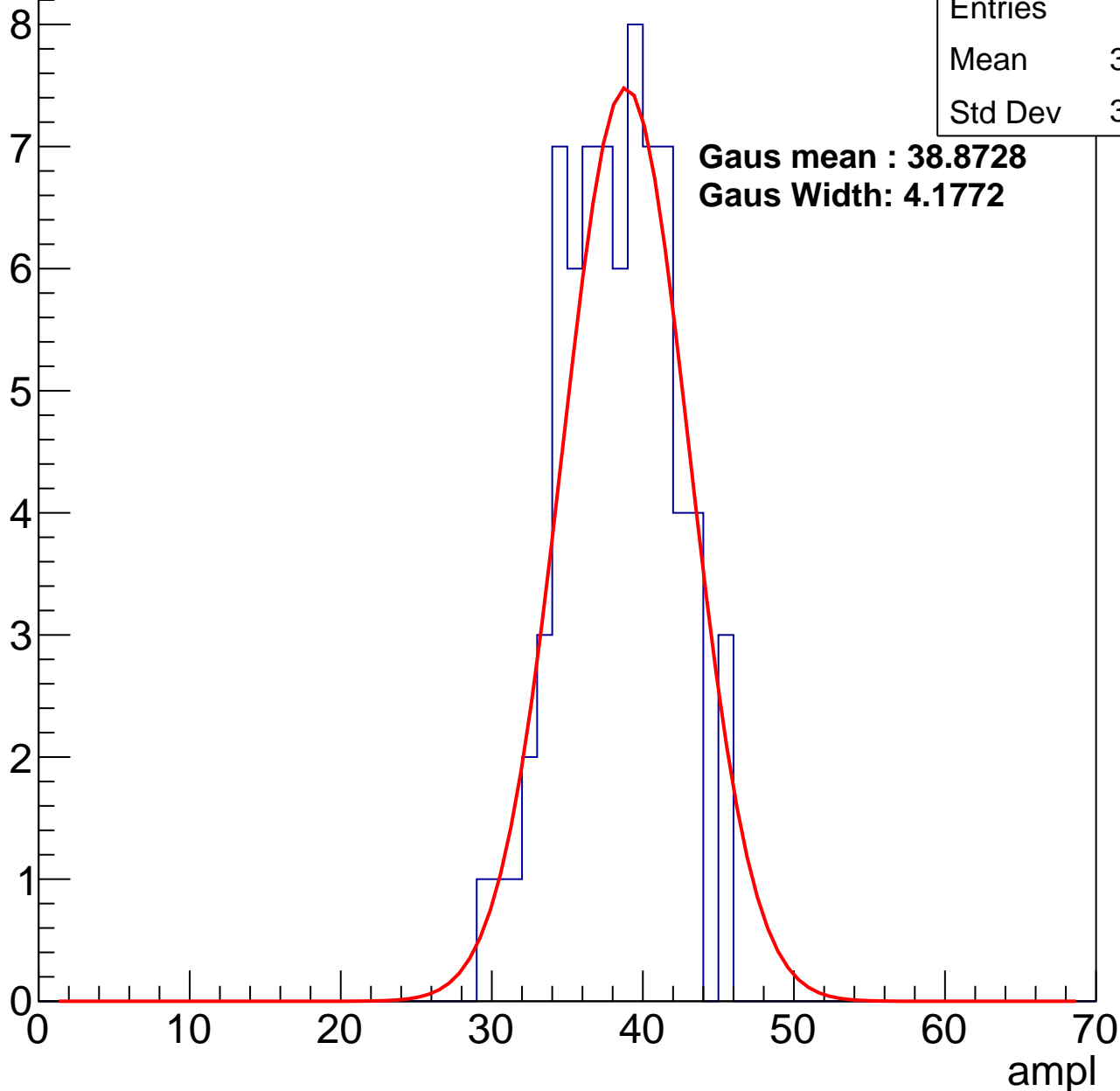
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	37.76
Std Dev	3.579

Gaus mean : 38.8728

Gaus Width: 4.1772



B1L100S, U11-ch68, adc2

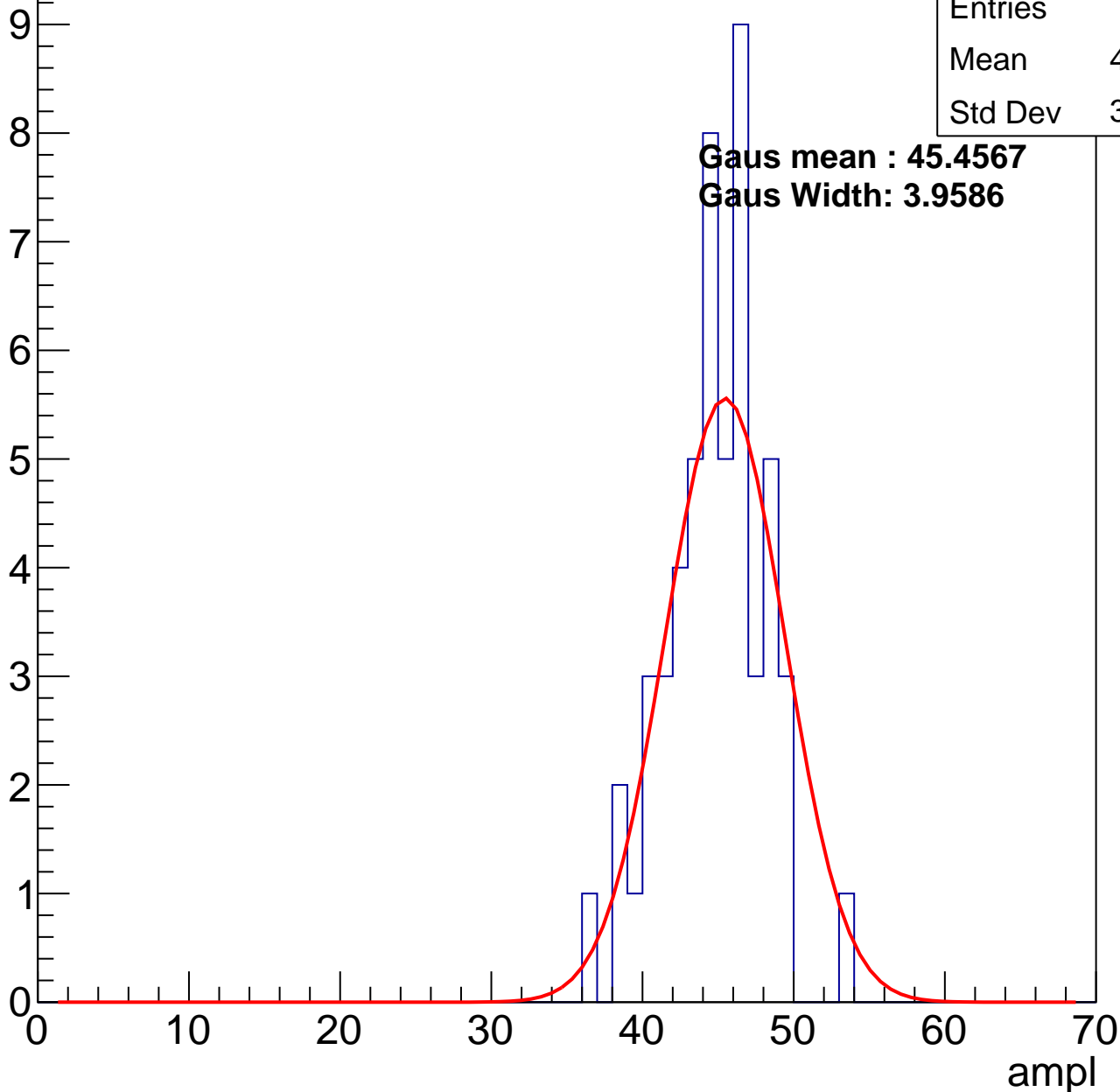
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	44.32
Std Dev	3.238

Gaus mean : 45.4567

Gaus Width: 3.9586

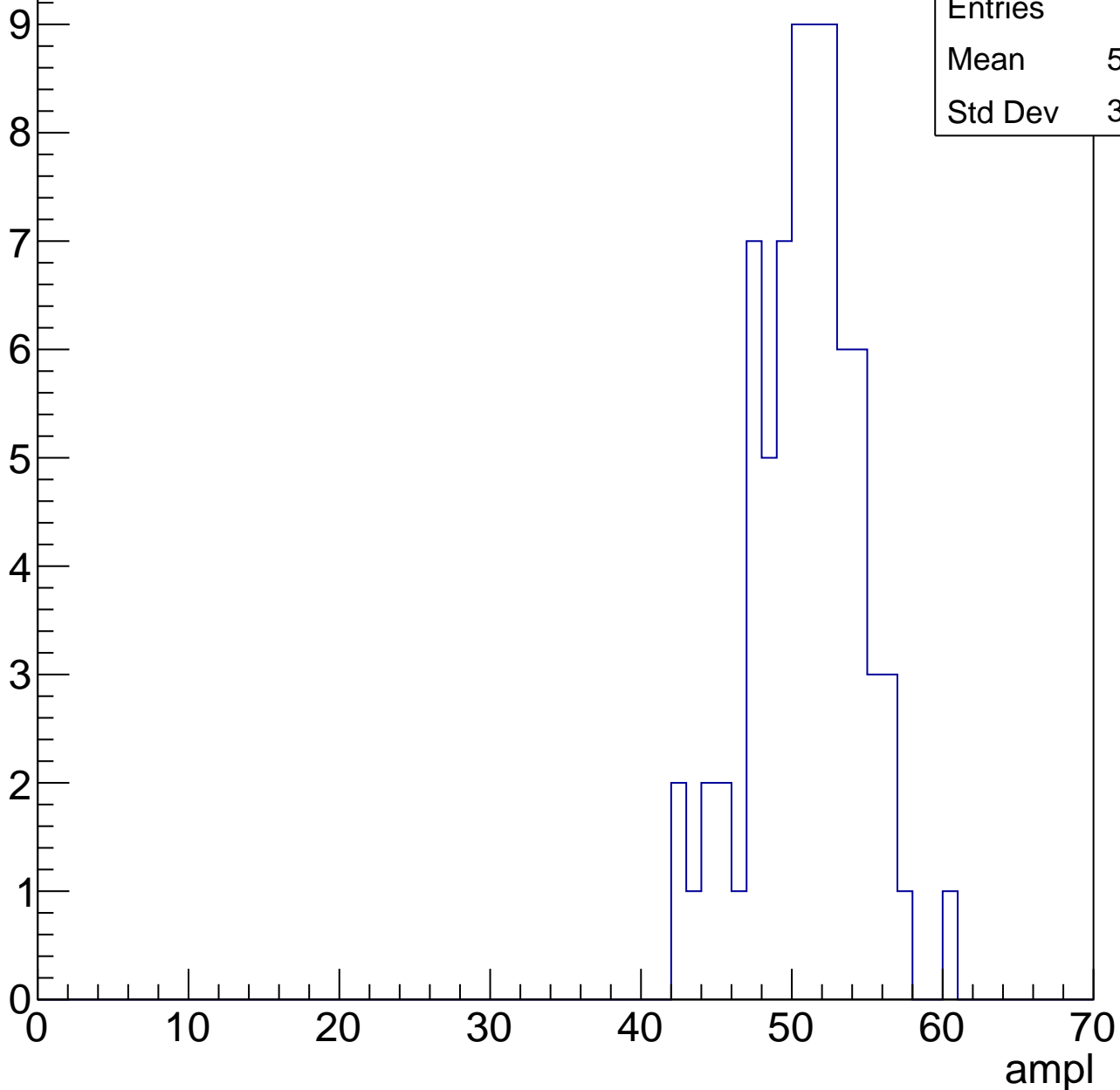


B1L100S, U11-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

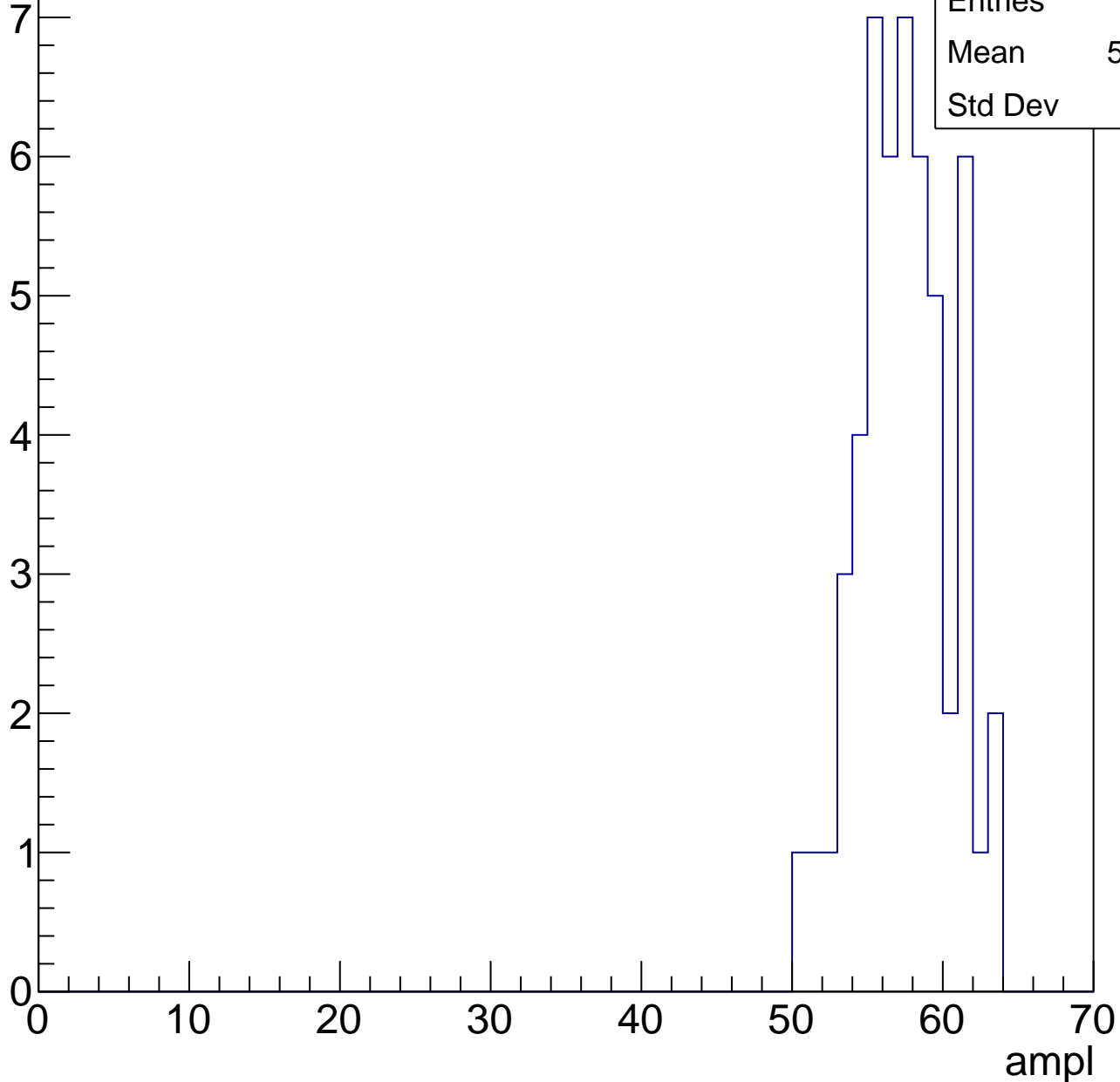
Entries	74
Mean	50.43
Std Dev	3.522



B1L100S, U11-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

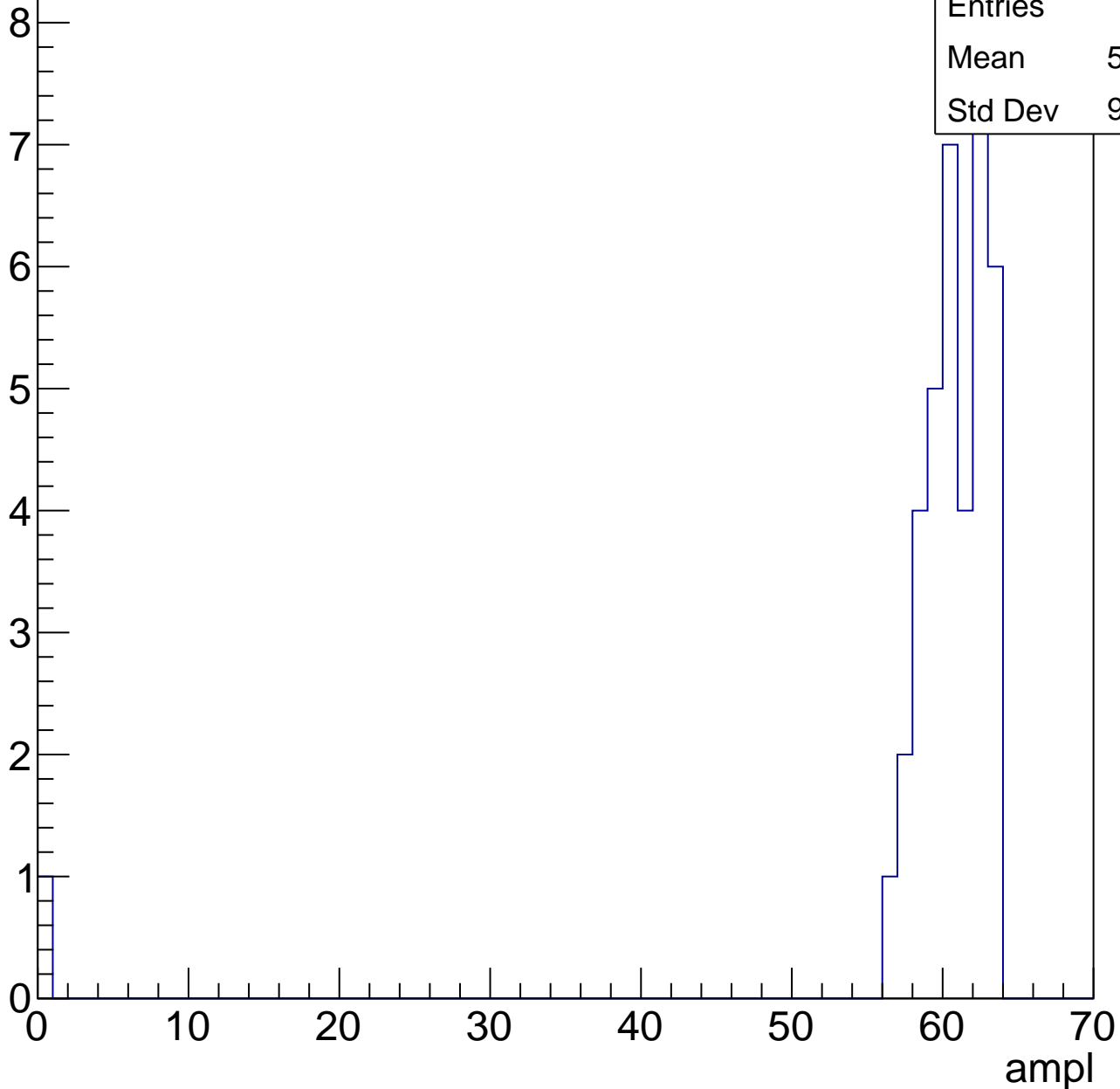


B1L100S, U11-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.82
Std Dev	9.856



B1L100S, U11-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch69, adc0

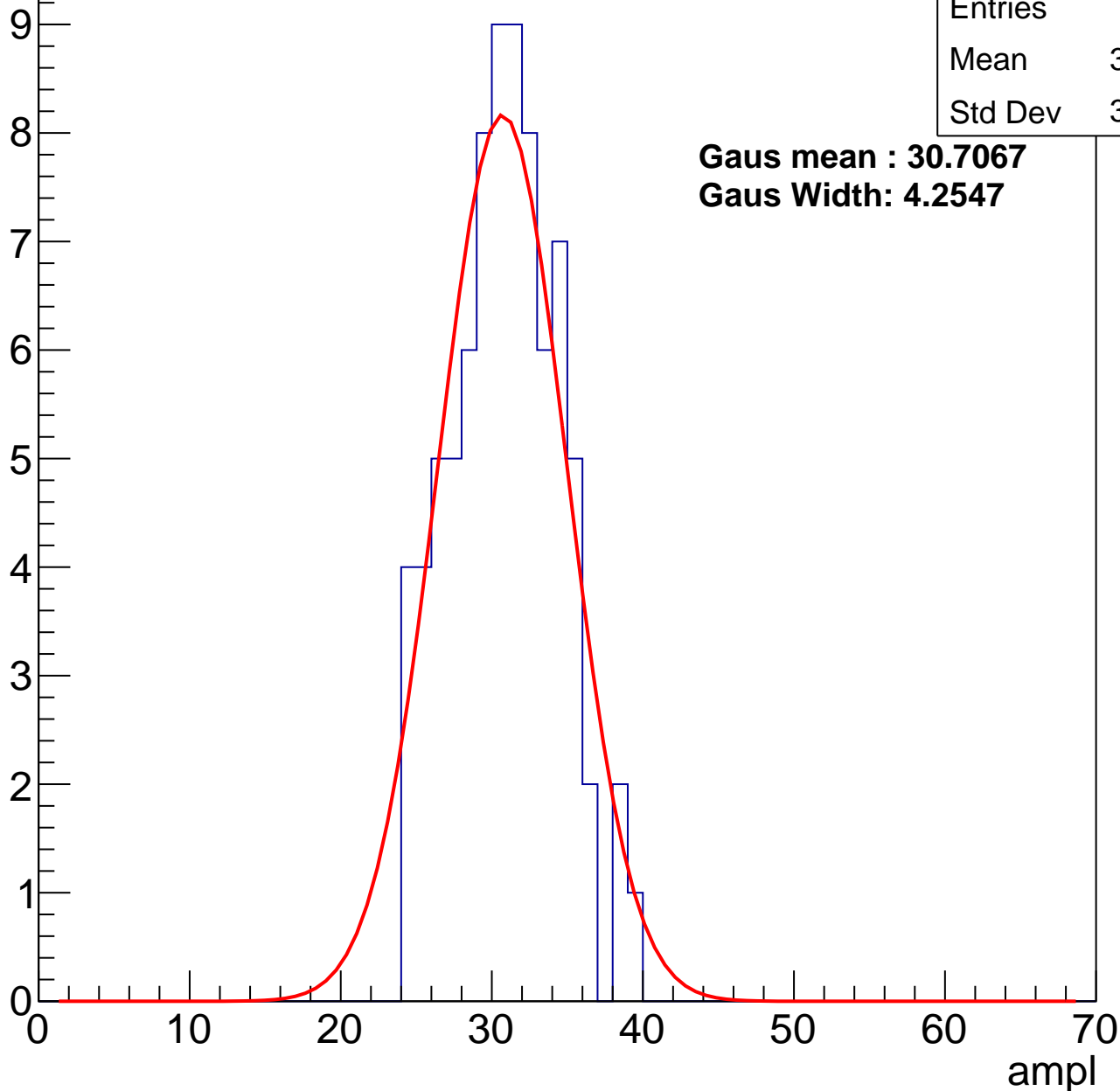
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	30.42
Std Dev	3.513

Gaus mean : 30.7067

Gaus Width: 4.2547



B1L100S, U11-ch69, adc1

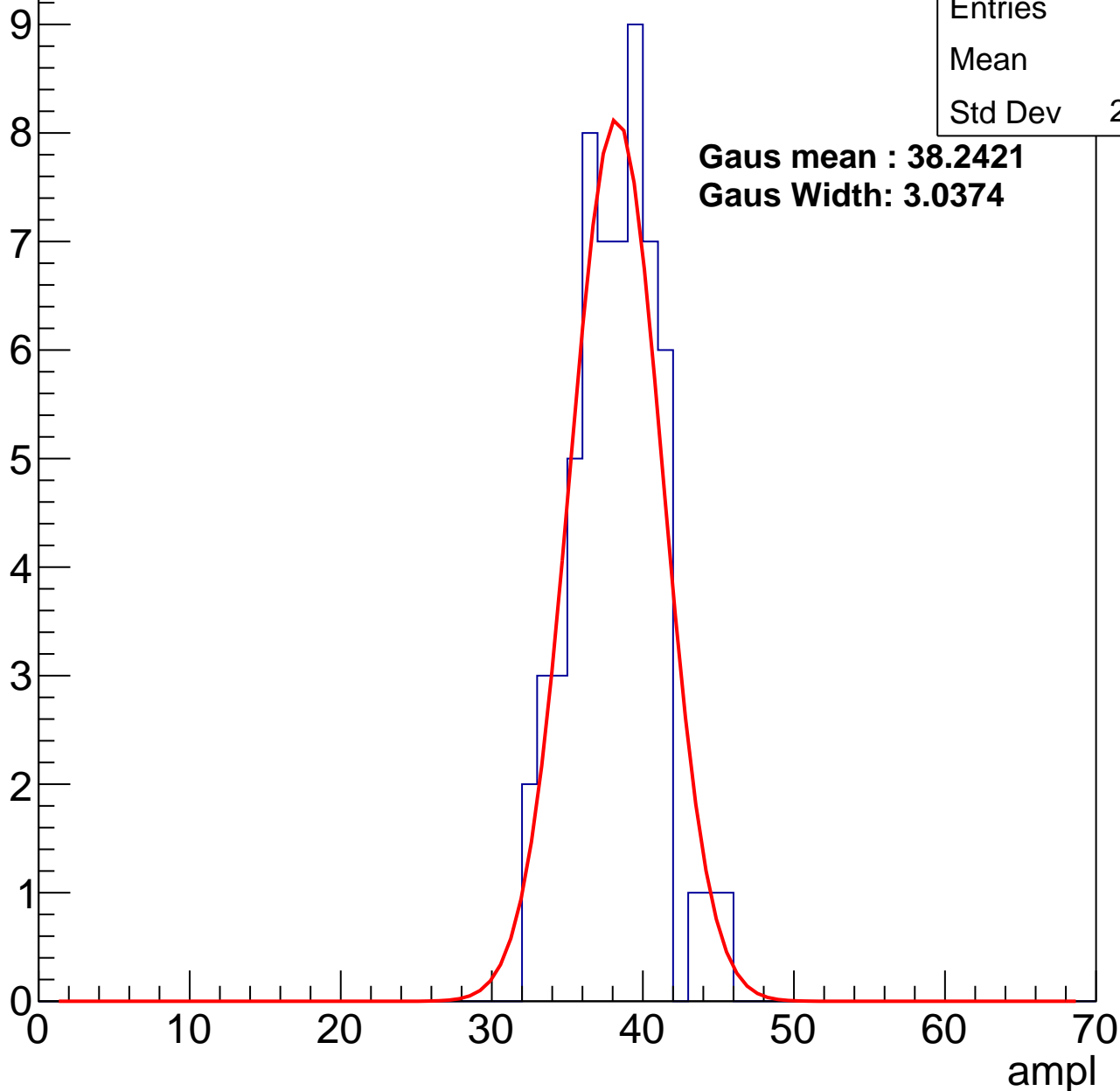
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	37.7
Std Dev	2.812

Gaus mean : 38.2421

Gaus Width: 3.0374



B1L100S, U11-ch69, adc2

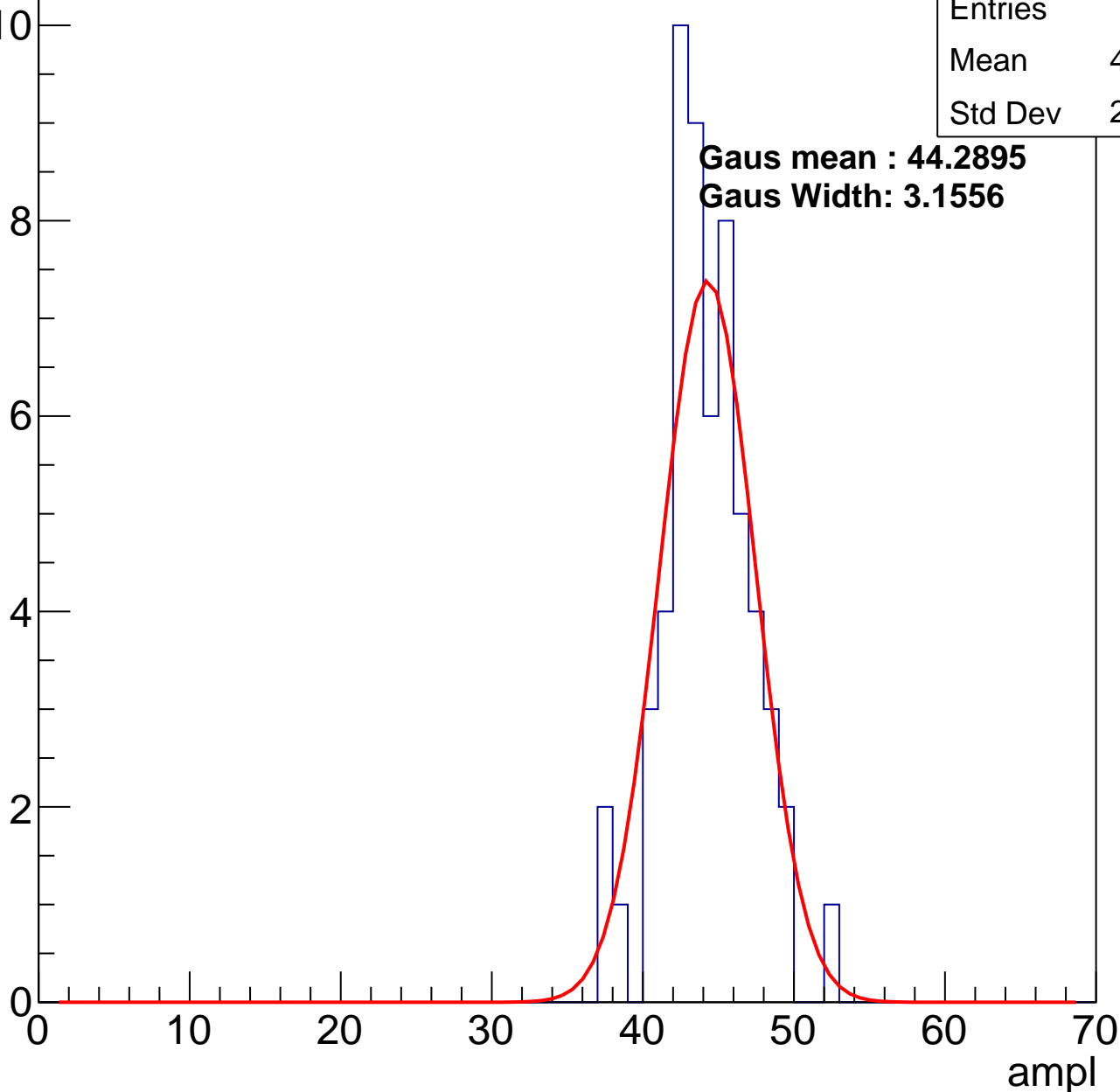
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.78
Std Dev	2.907

Gaus mean : 44.2895

Gaus Width: 3.1556



B1L100S, U11-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

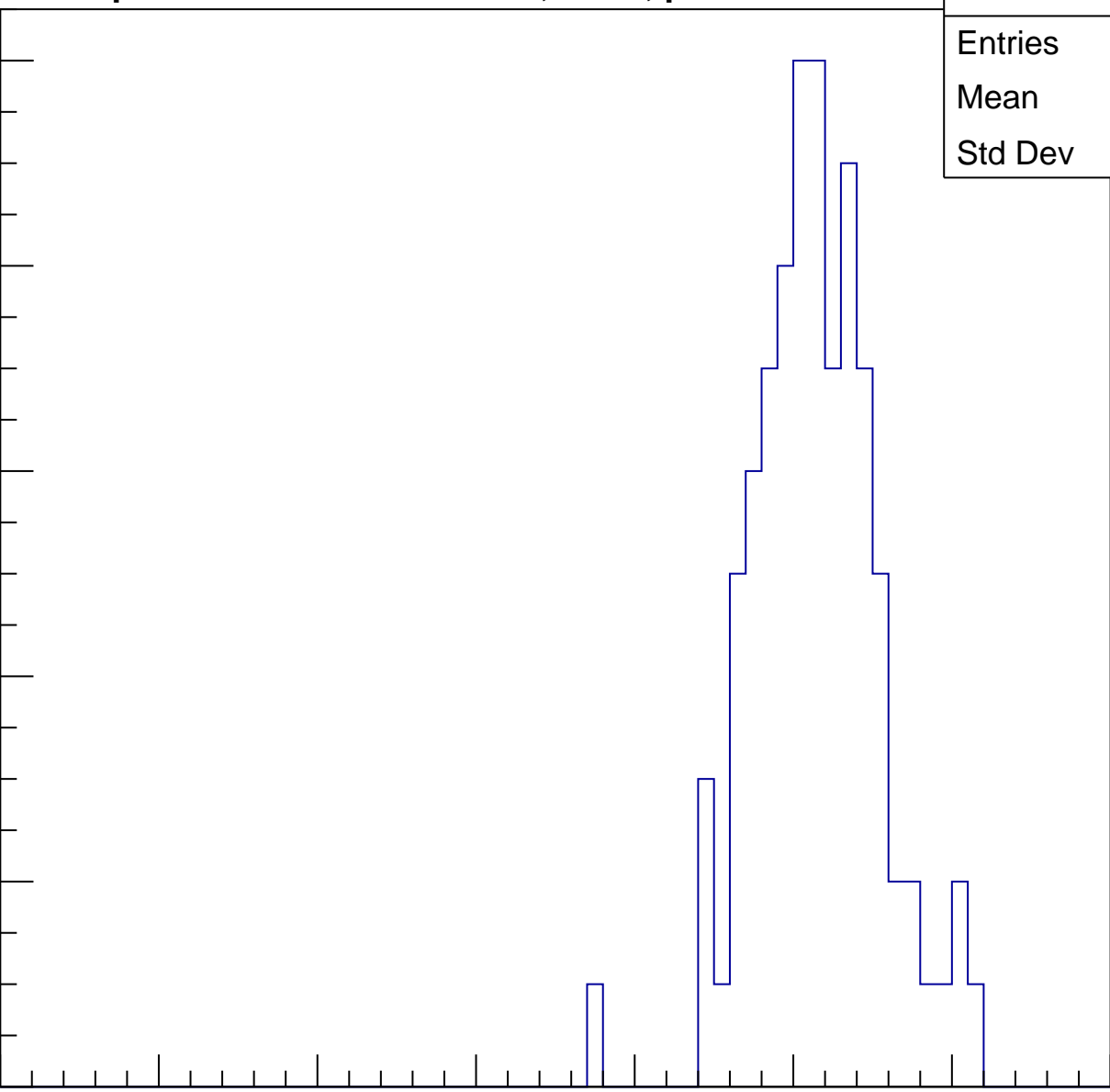
Entries	88
Mean	50.93
Std Dev	3.971

Entry

10
8
6
4
2
0

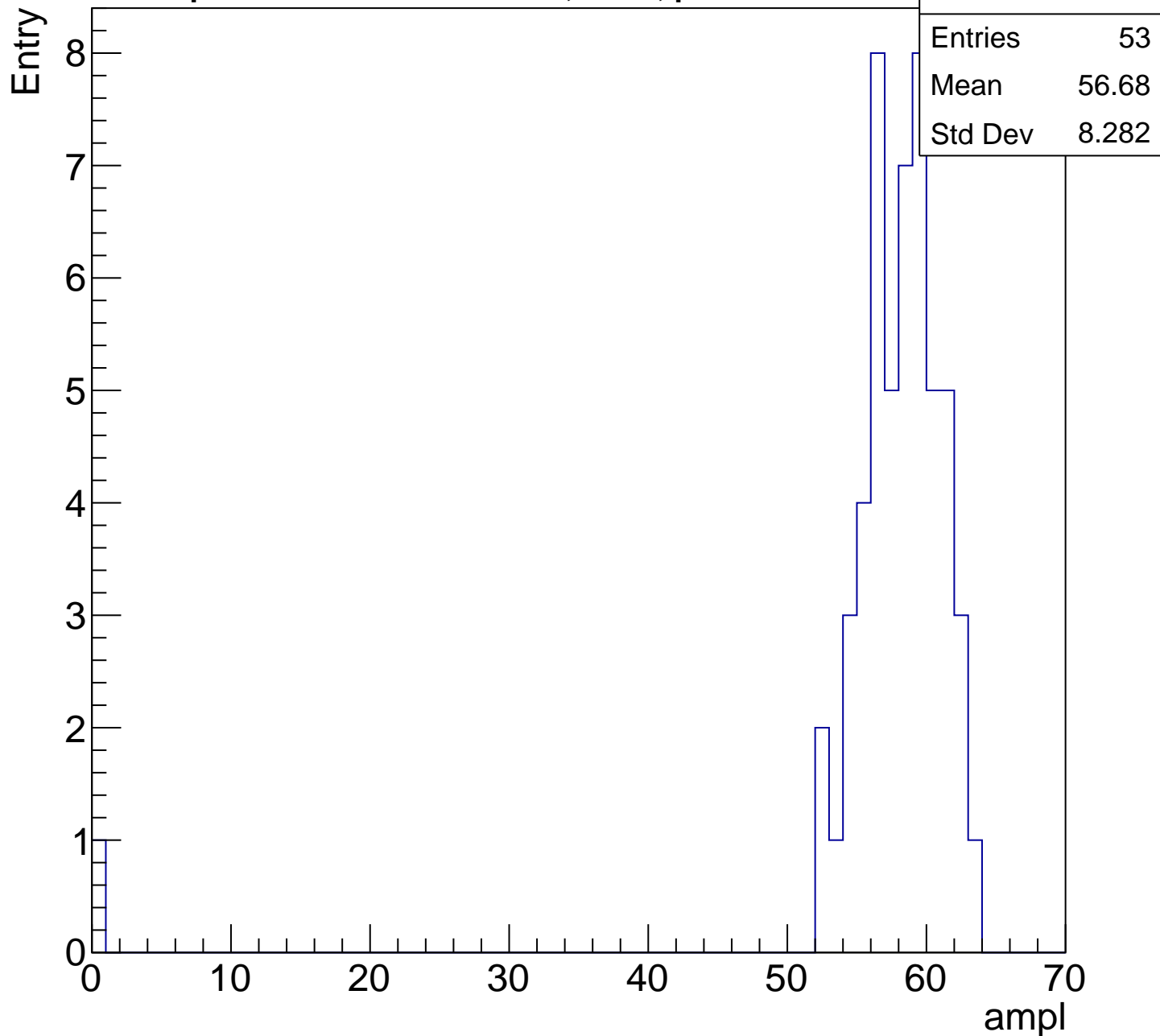
0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

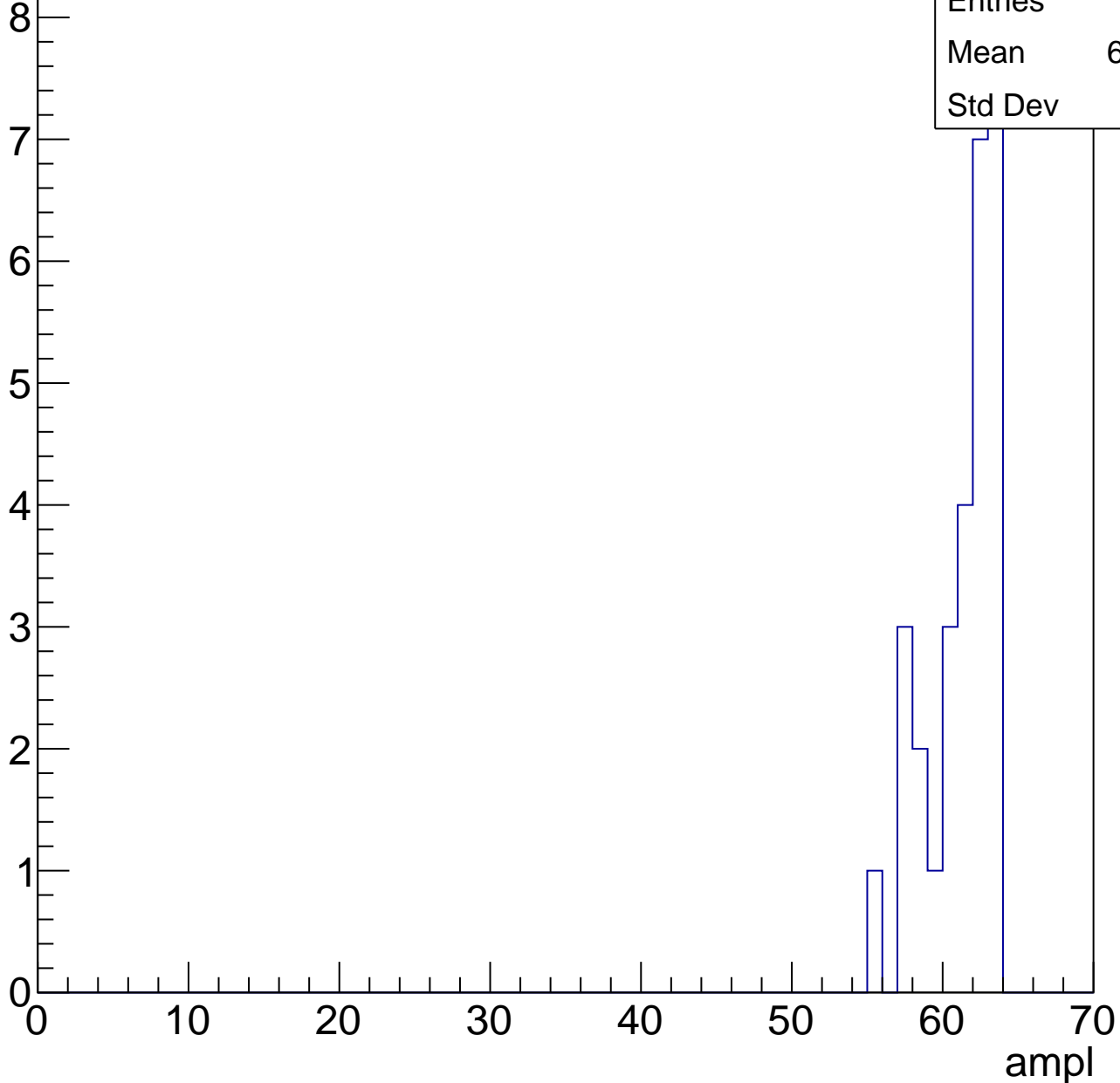


B1L100S, U11-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	60.79
Std Dev	2.25



B1L100S, U11-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U11-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch70, adc0

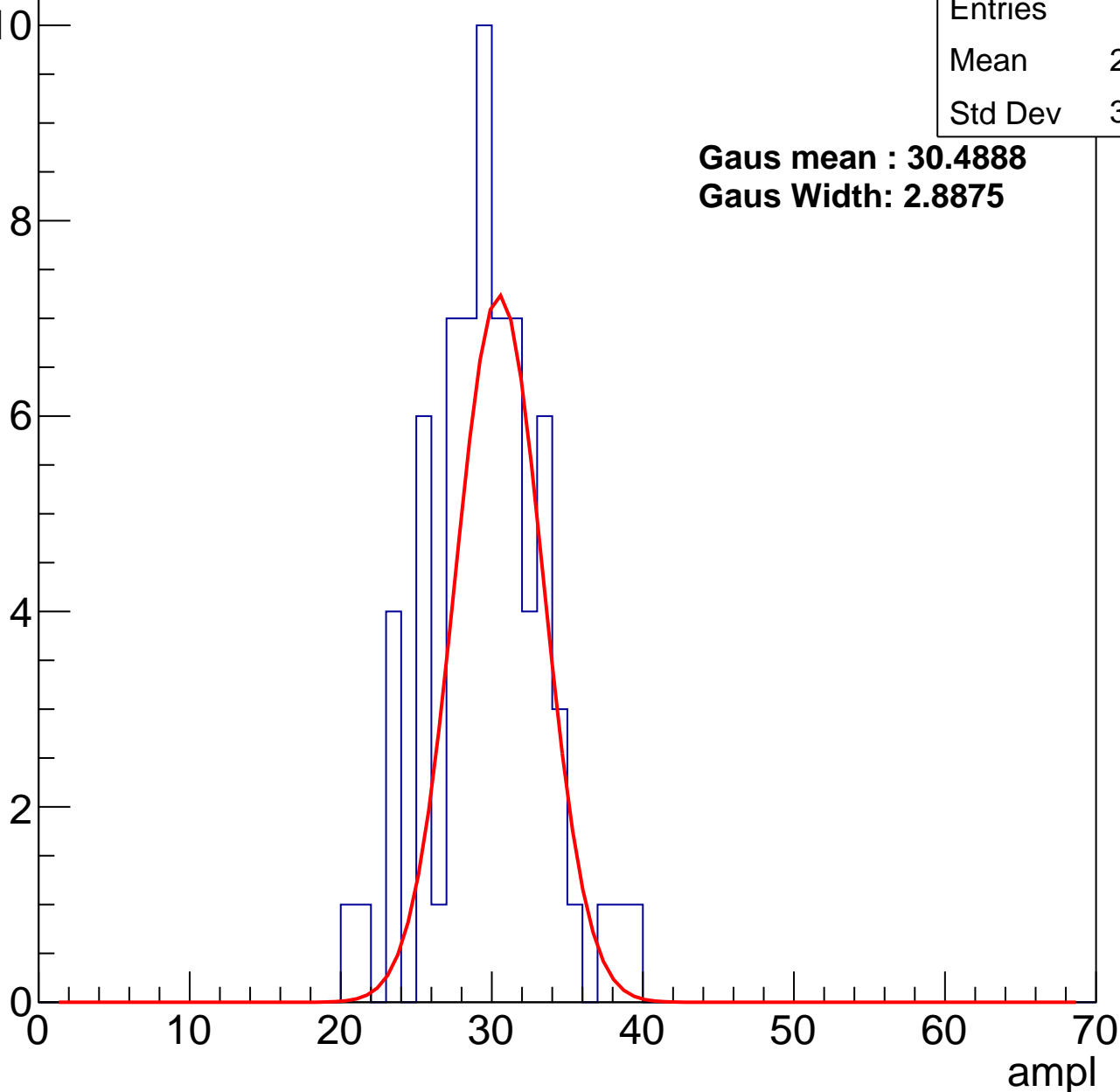
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.24
Std Dev	3.734

Gaus mean : 30.4888

Gaus Width: 2.8875



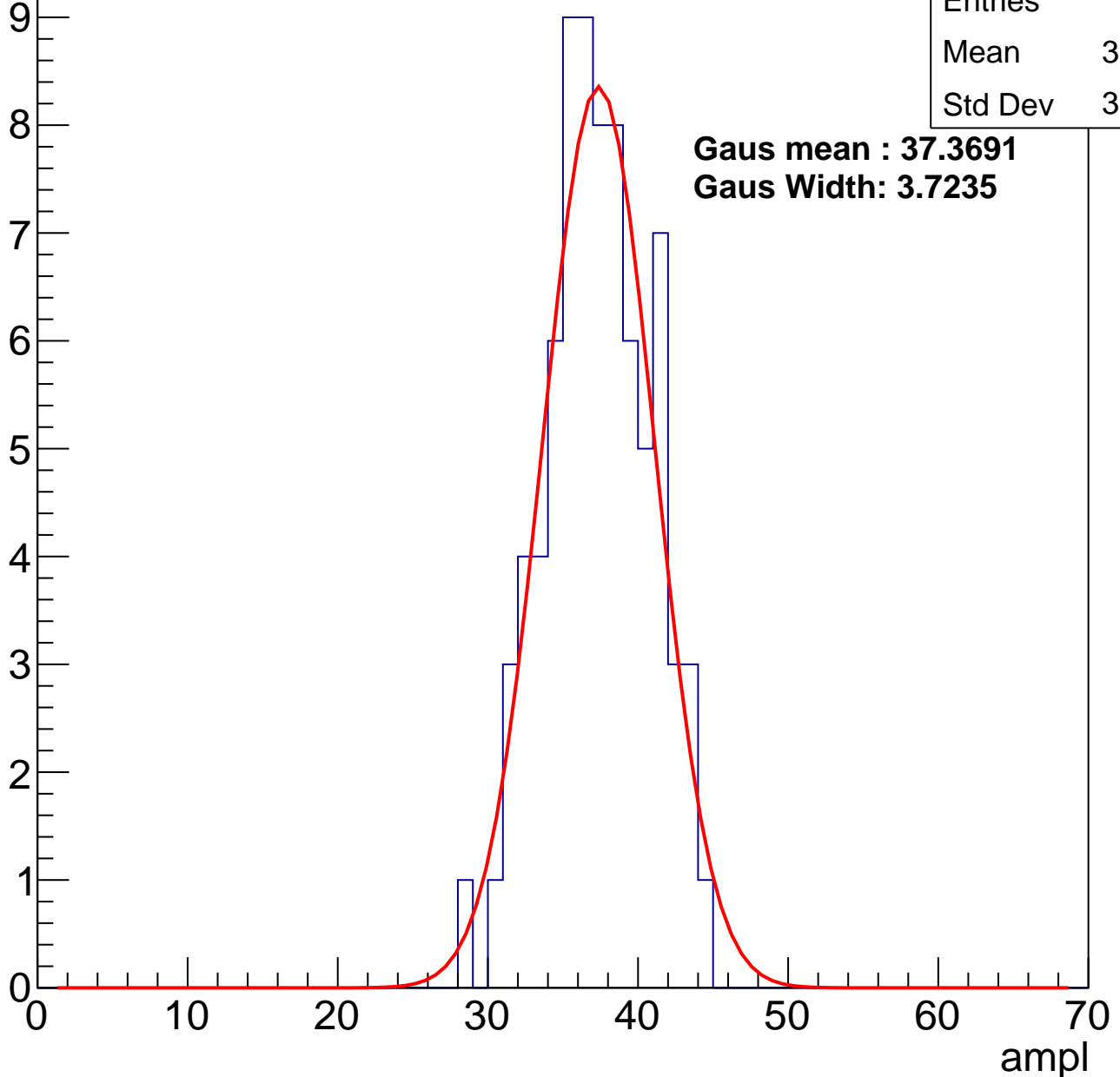
B1L100S, U11-ch70, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	36.85
Std Dev	3.435

Gaus mean : 37.3691
Gaus Width: 3.7235



B1L100S, U11-ch70, adc2

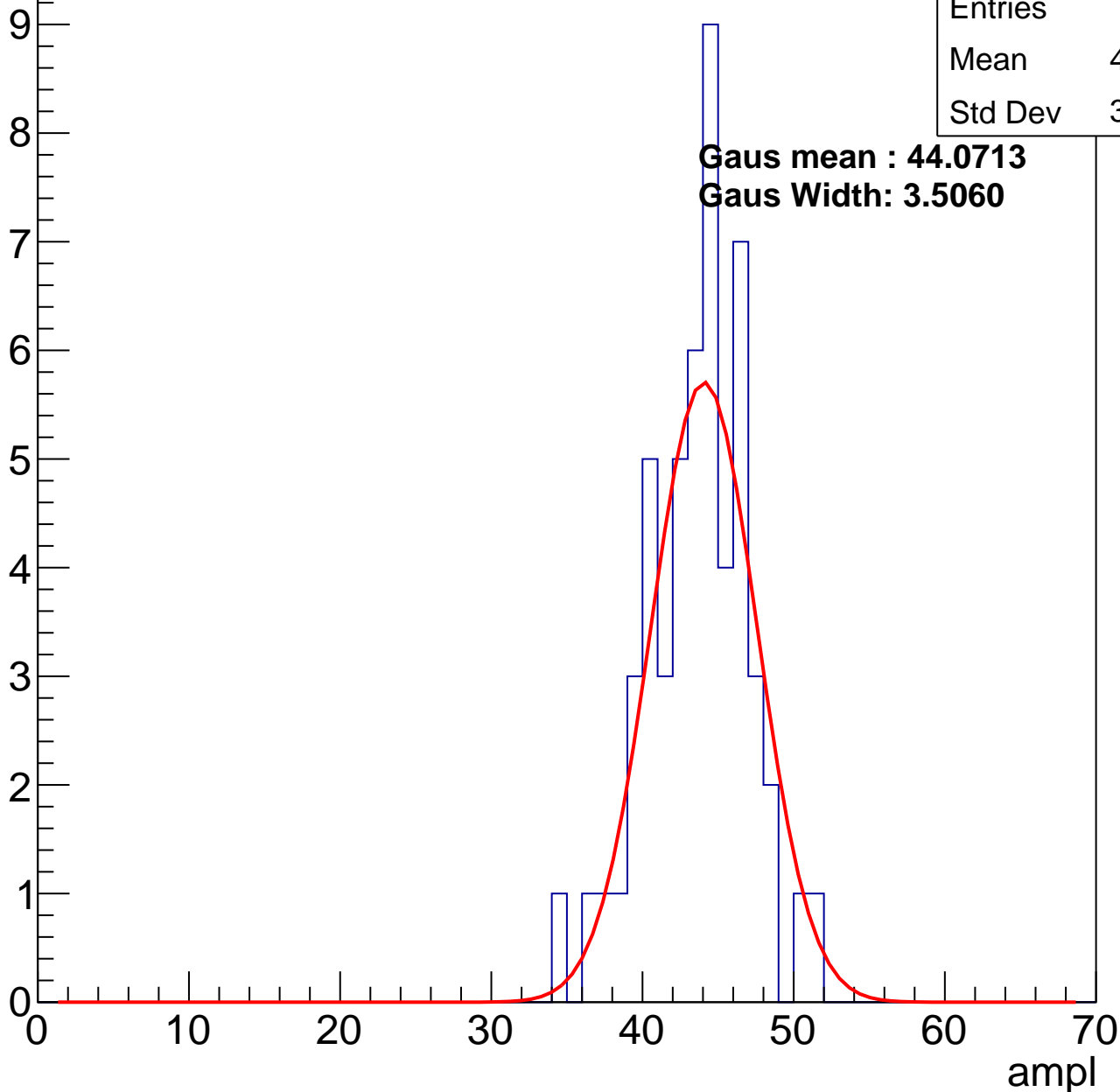
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	43.19
Std Dev	3.359

Gaus mean : 44.0713

Gaus Width: 3.5060

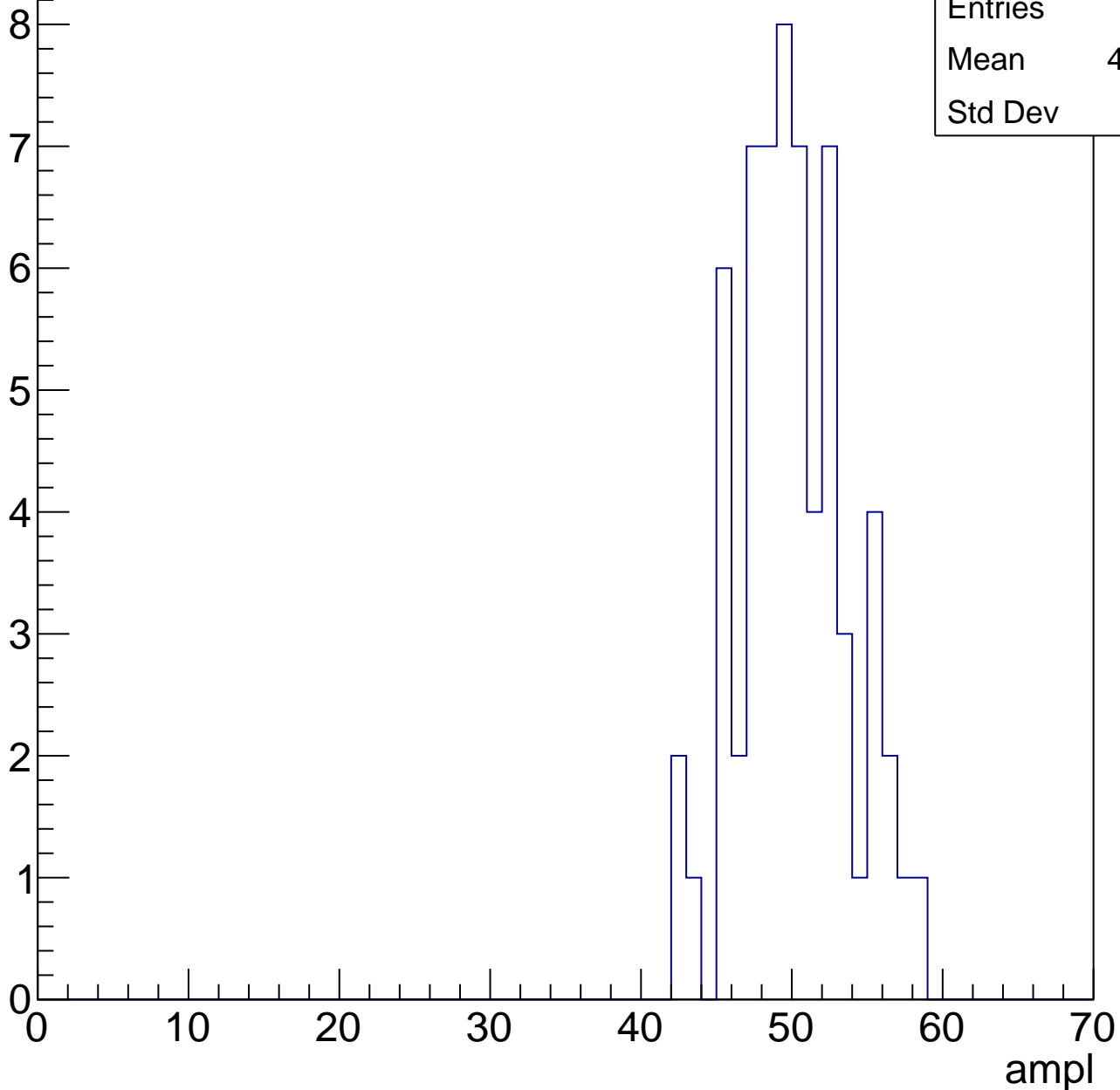


B1L100S, U11-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	49.59
Std Dev	3.58

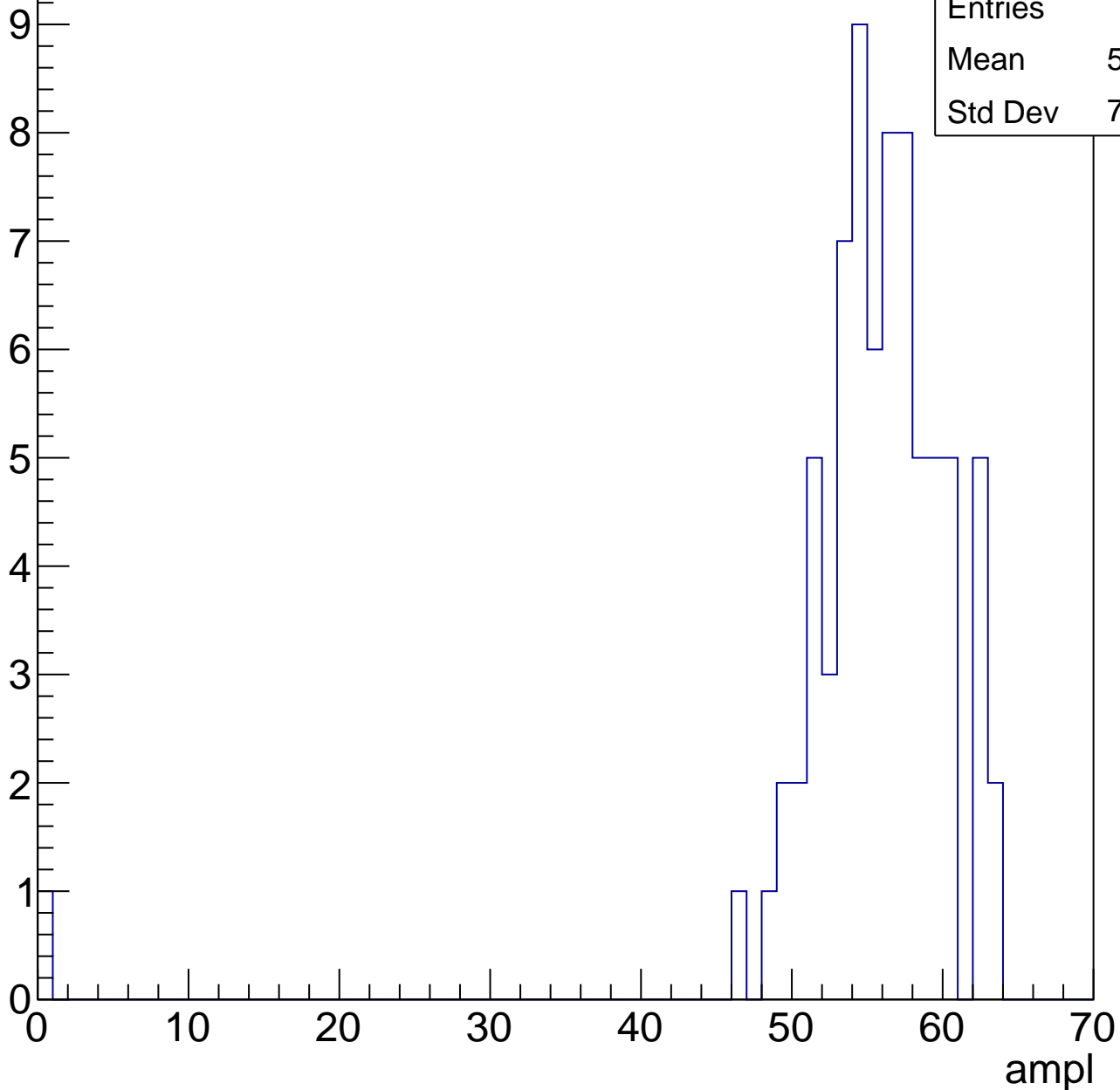


B1L100S, U11-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	54.87
Std Dev	7.378

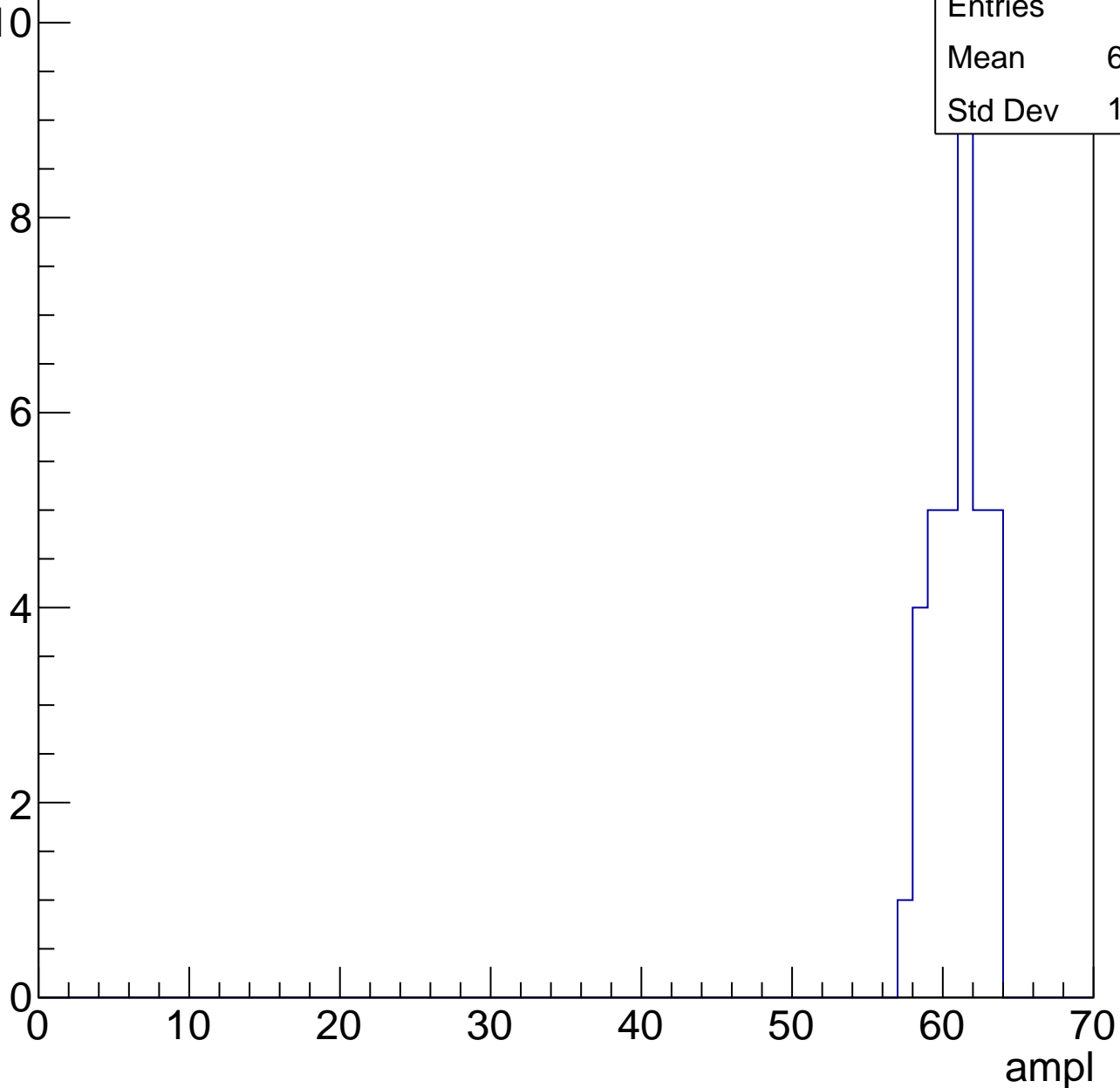


B1L100S, U11-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	60.54
Std Dev	1.645



B1L100S, U11-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch71, adc0

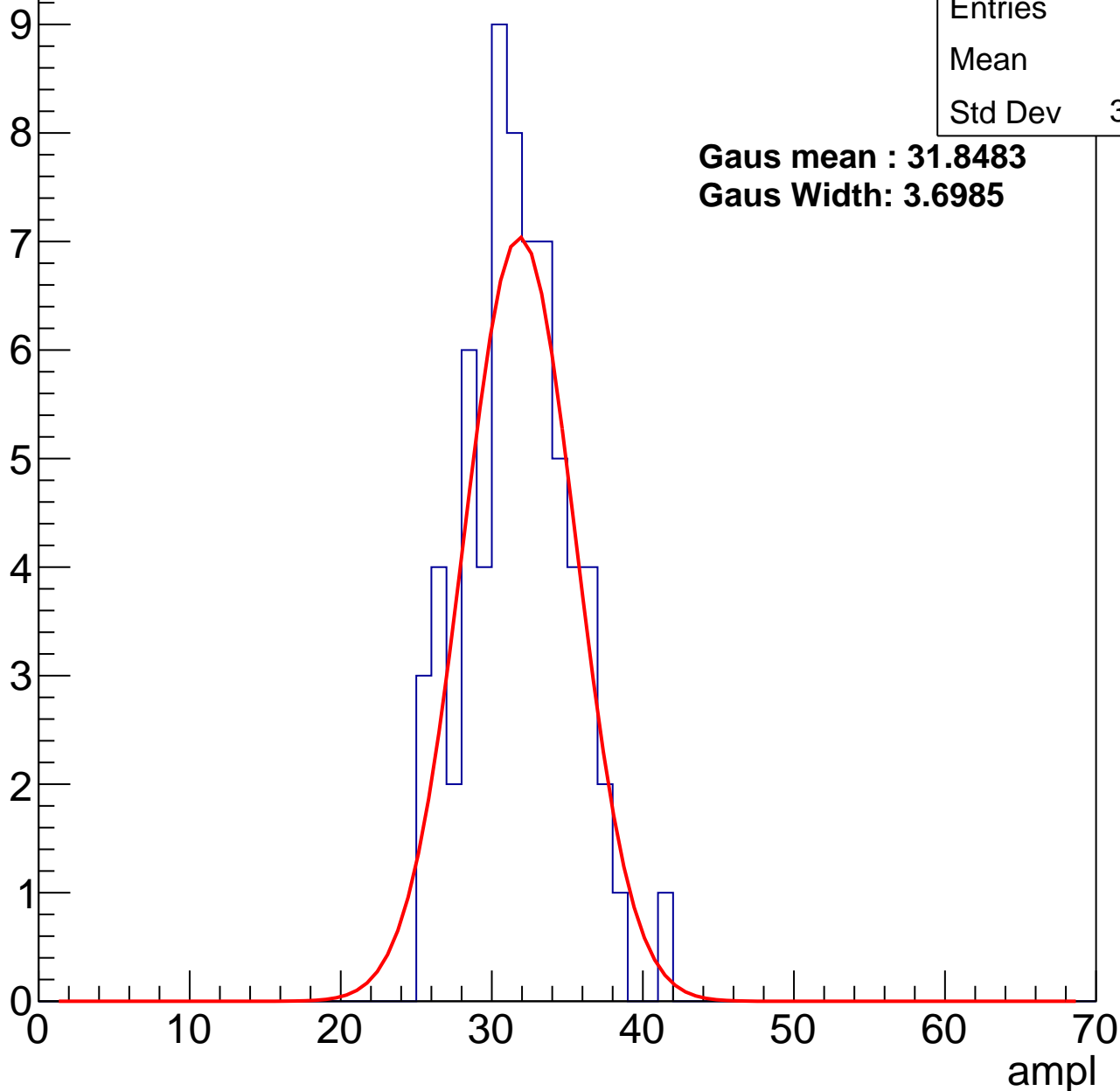
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	31.3
Std Dev	3.408

Gaus mean : 31.8483

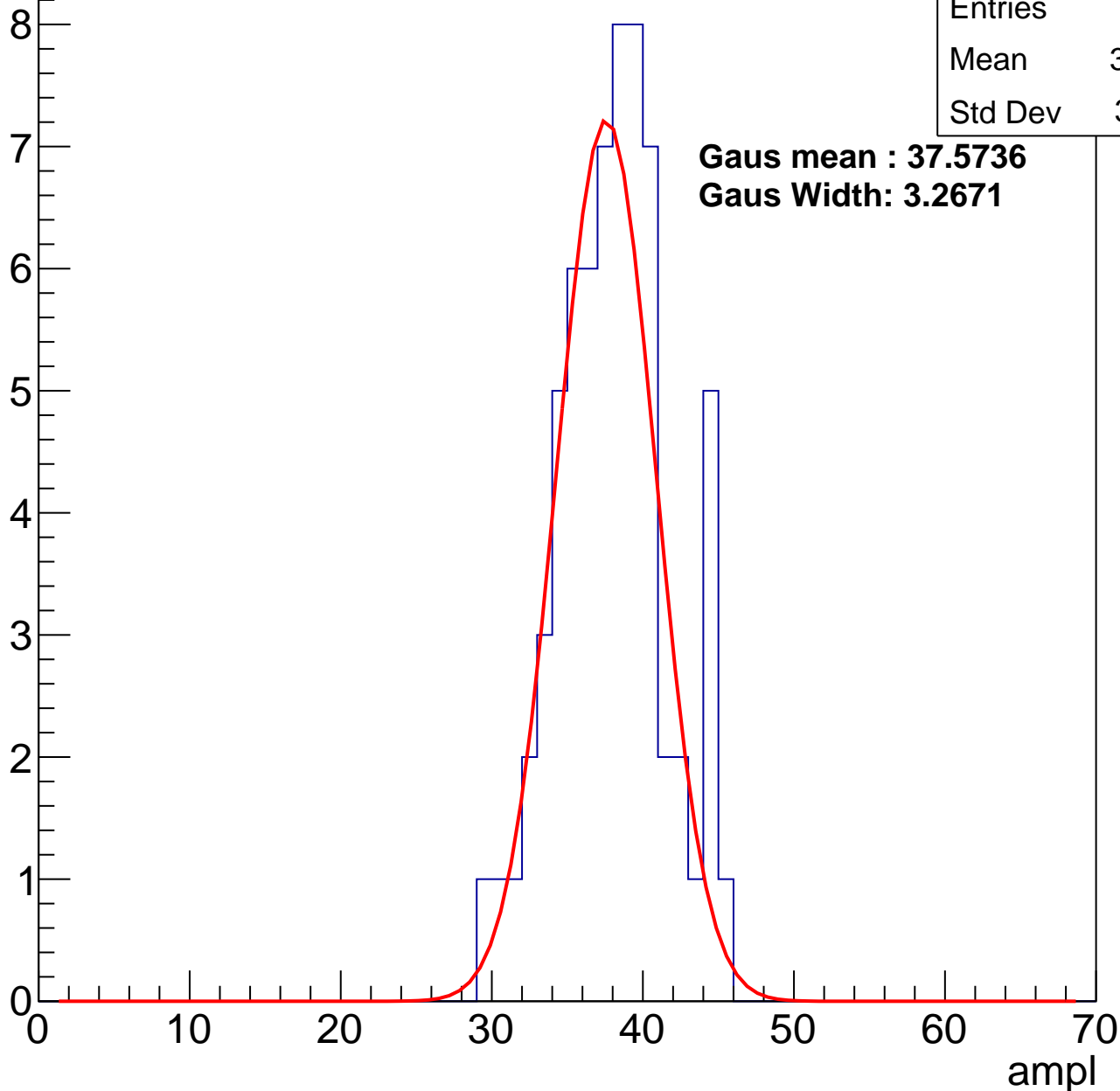
Gaus Width: 3.6985



B1L100S, U11-ch71, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch71, adc2

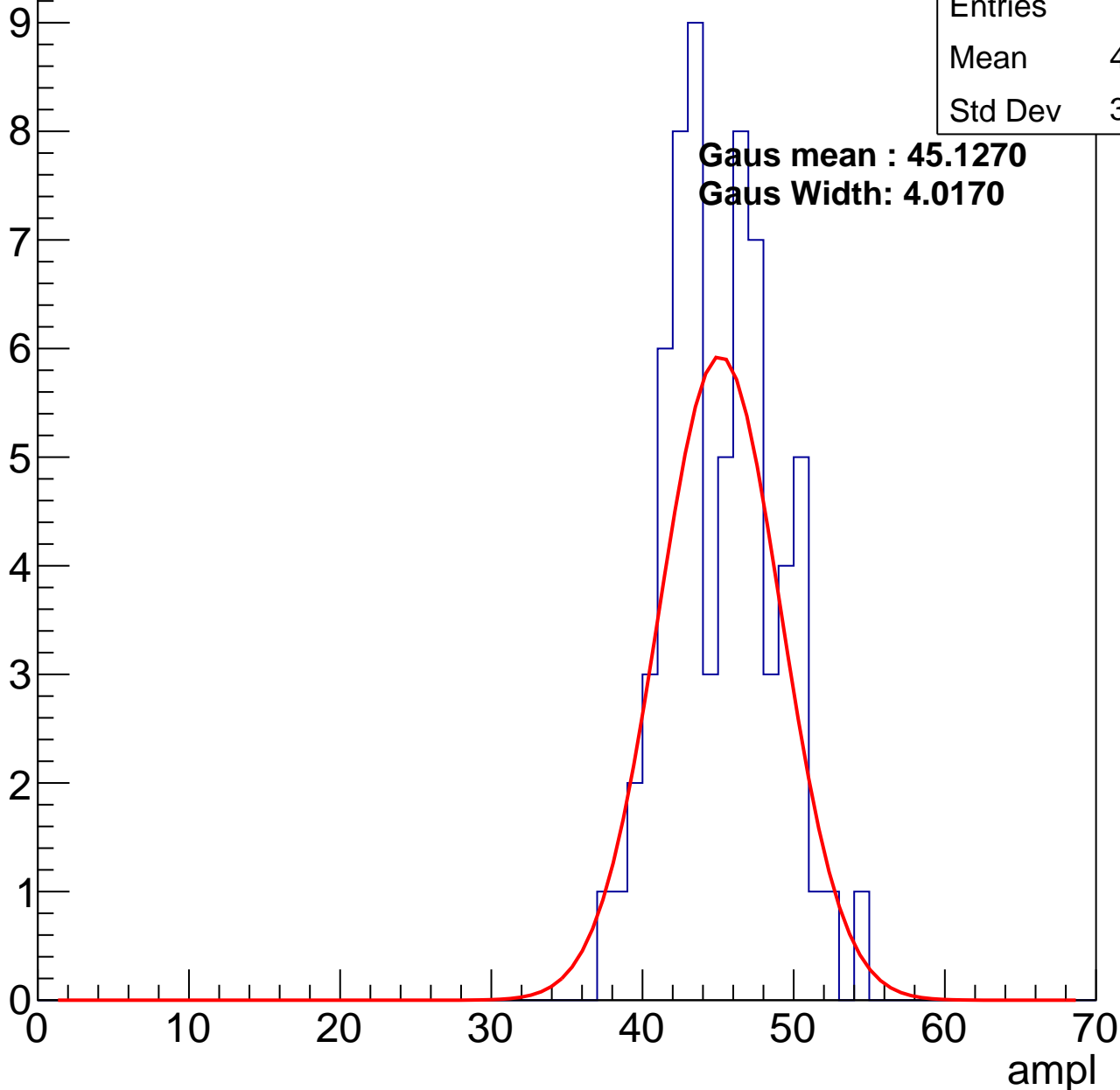
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.75
Std Dev	3.599

Gaus mean : 45.1270

Gaus Width: 4.0170

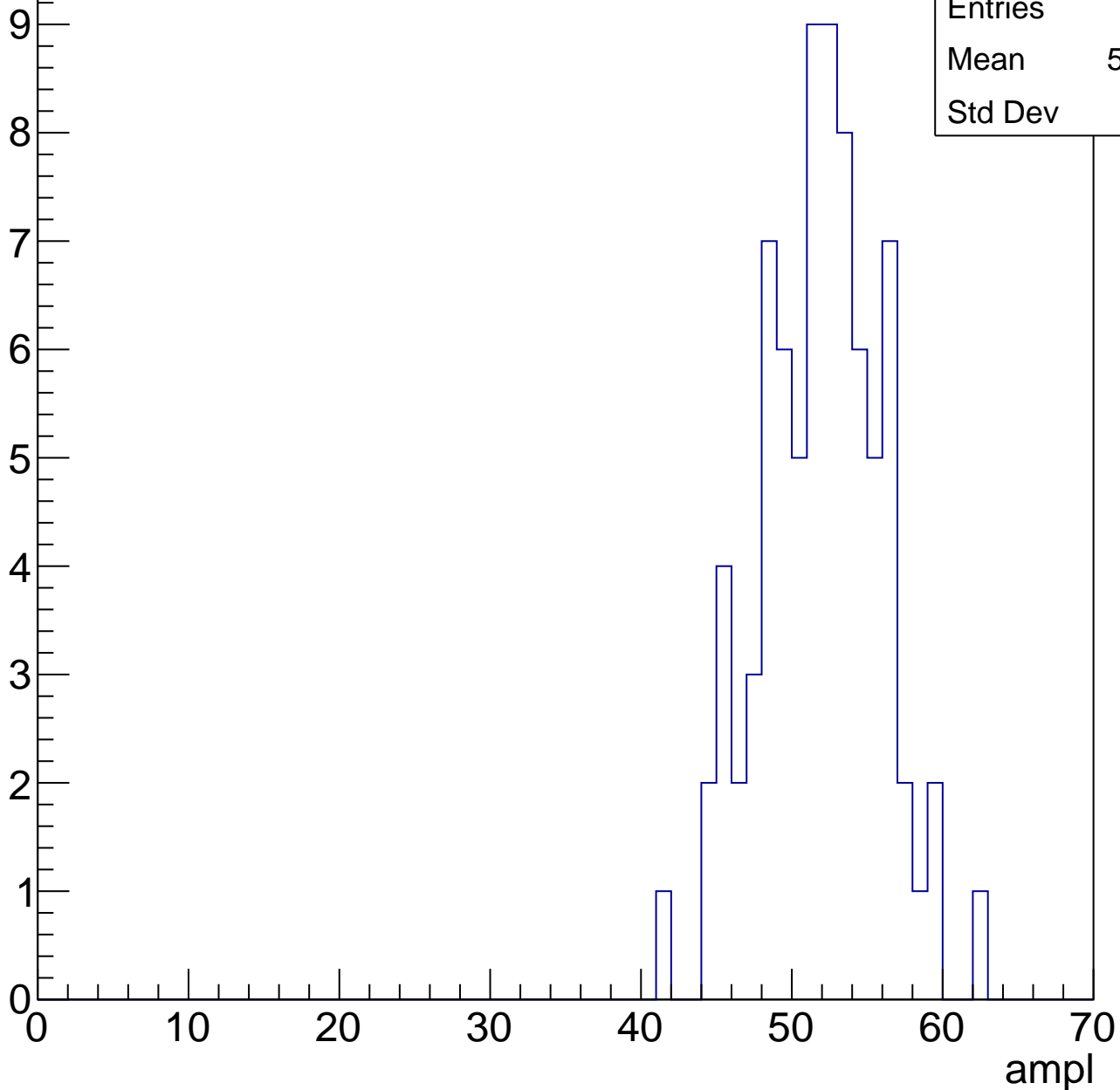


B1L100S, U11-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

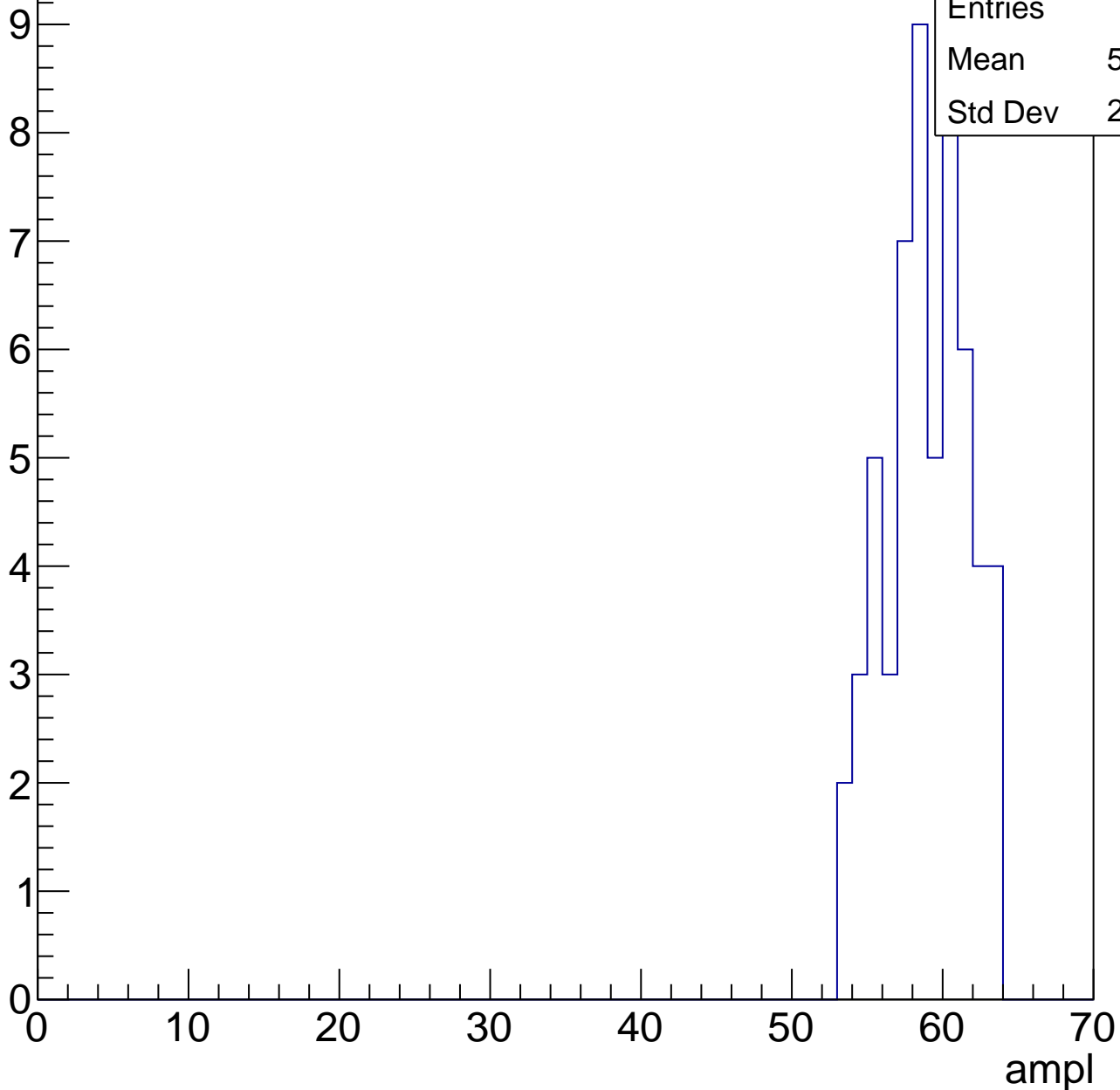
Entries	80
Mean	51.45
Std Dev	3.93



B1L100S, U11-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



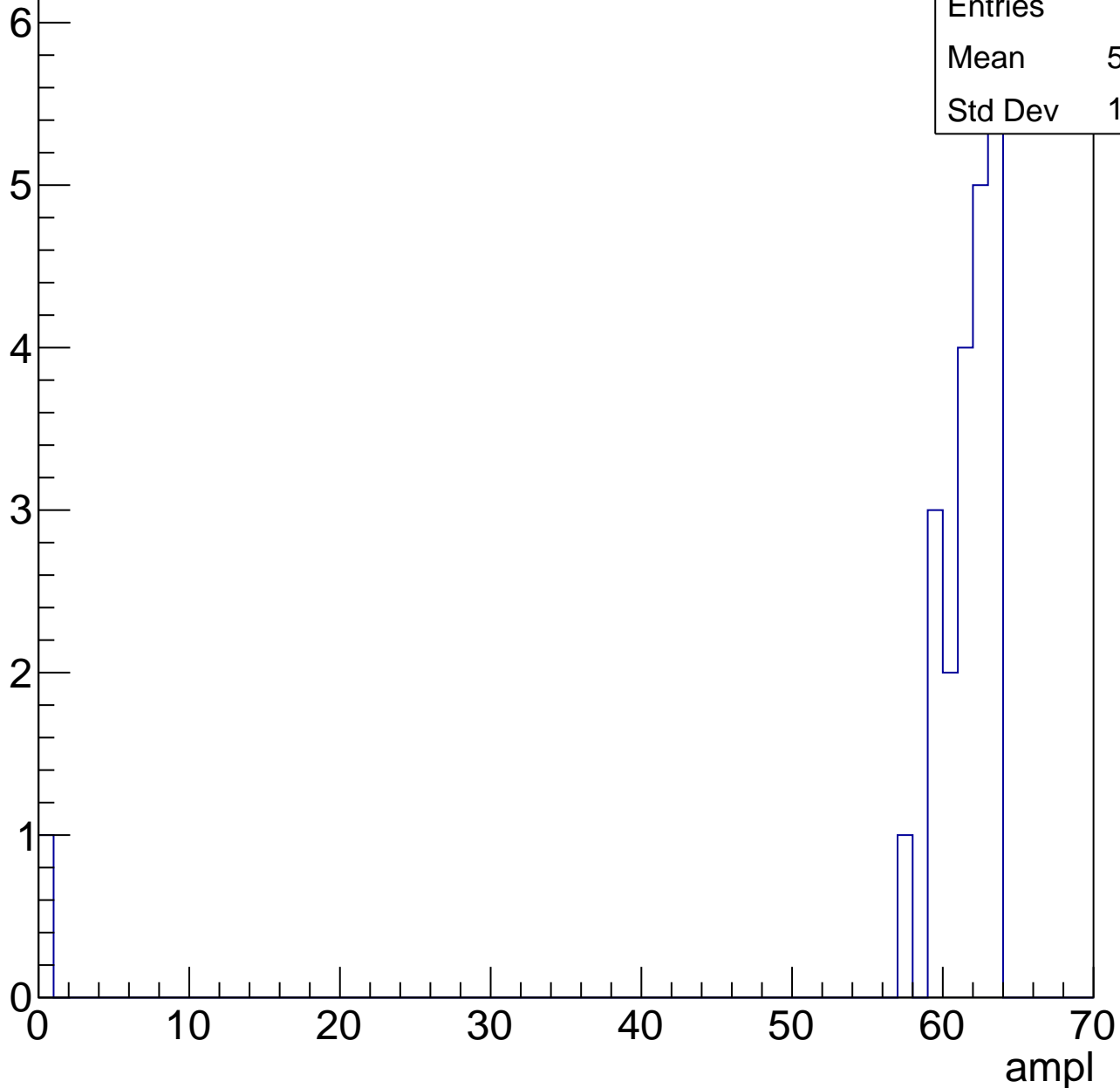
Entries	56
Mean	58.45
Std Dev	2.692

B1L100S, U11-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.45
Std Dev	12.86



B1L100S, U11-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch72, adc0

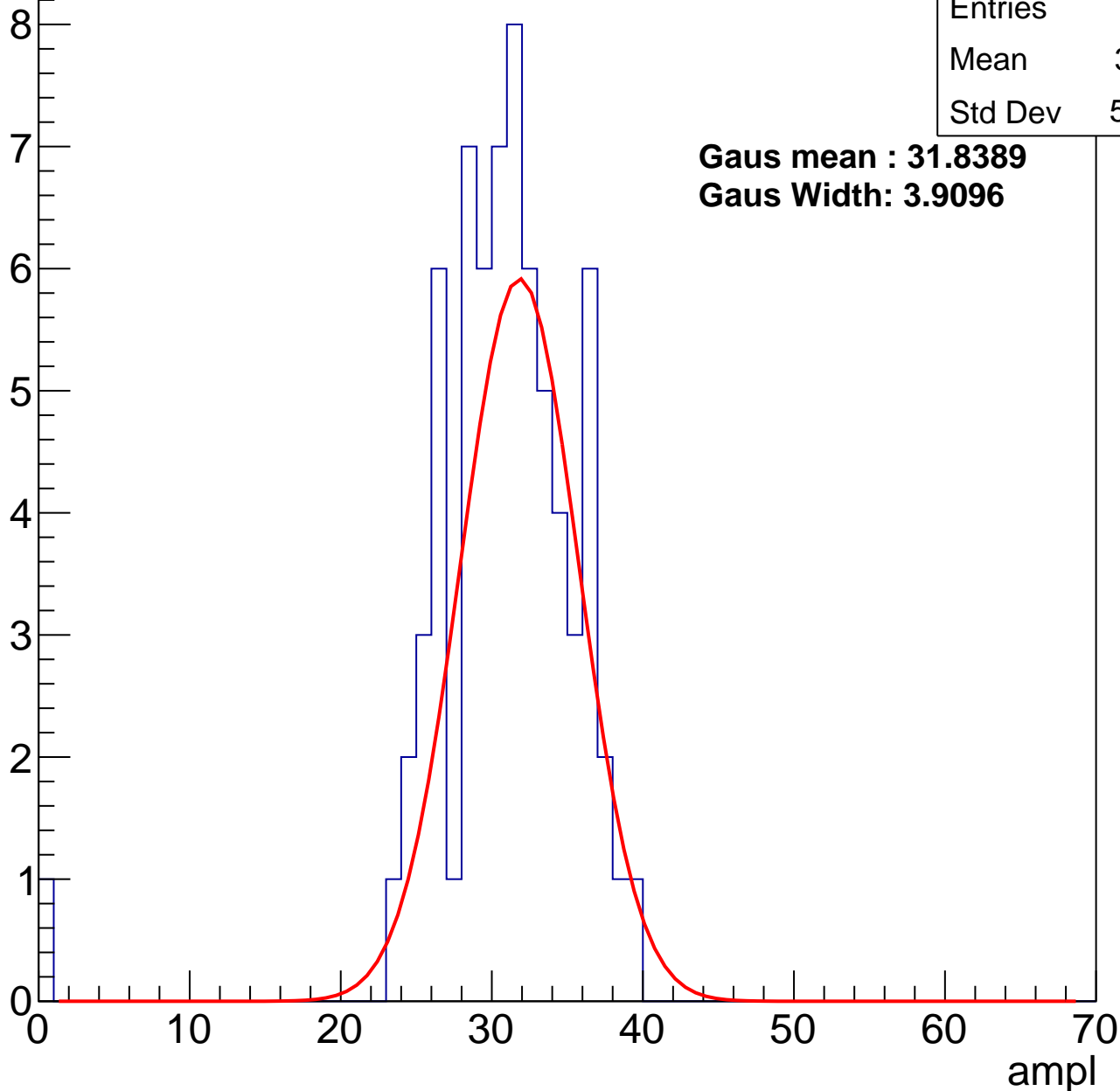
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.31
Std Dev	5.214

Gaus mean : 31.8389

Gaus Width: 3.9096



B1L100S, U11-ch72, adc1

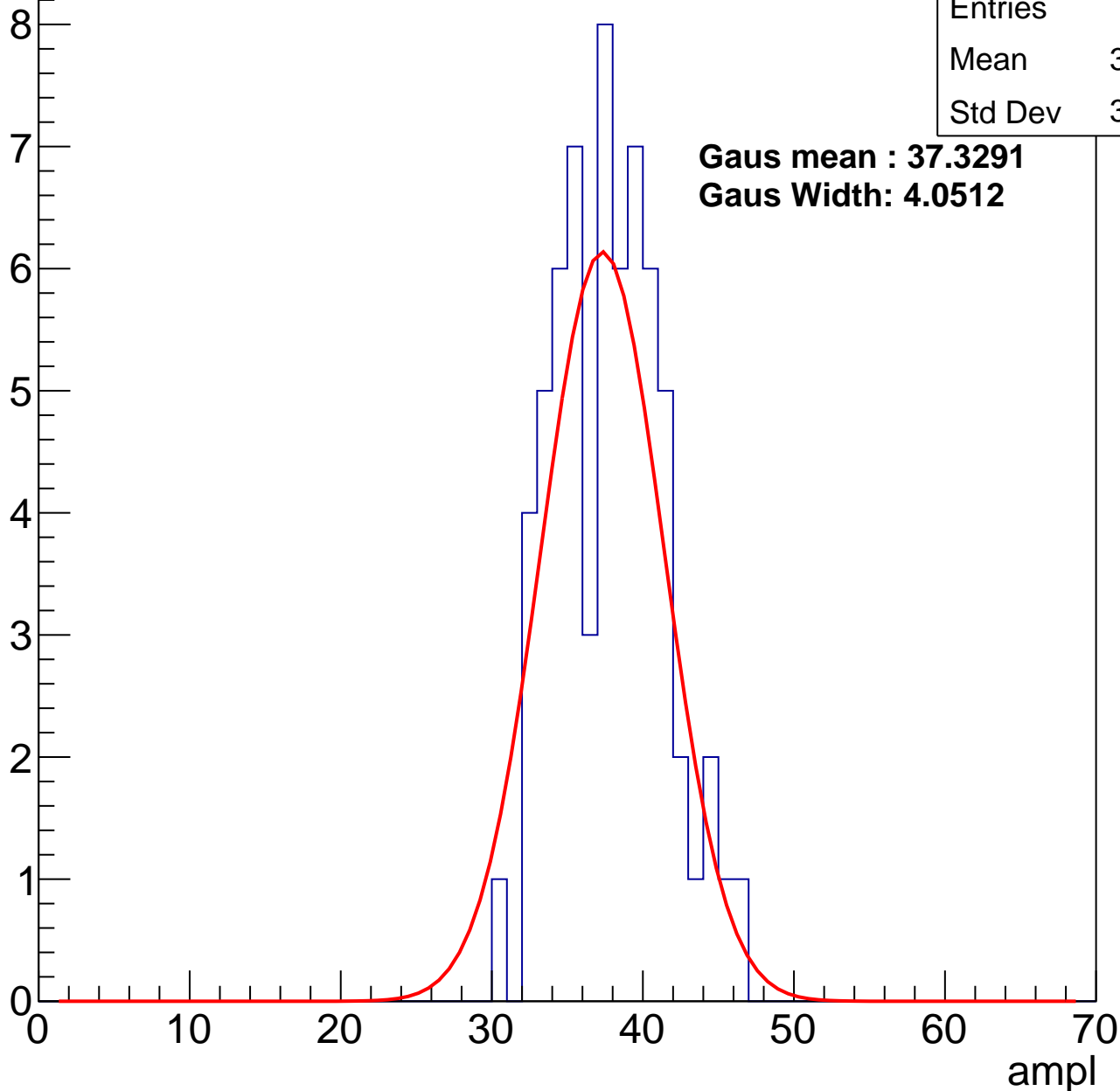
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37.35
Std Dev	3.515

Gaus mean : 37.3291

Gaus Width: 4.0512



B1L100S, U11-ch72, adc2

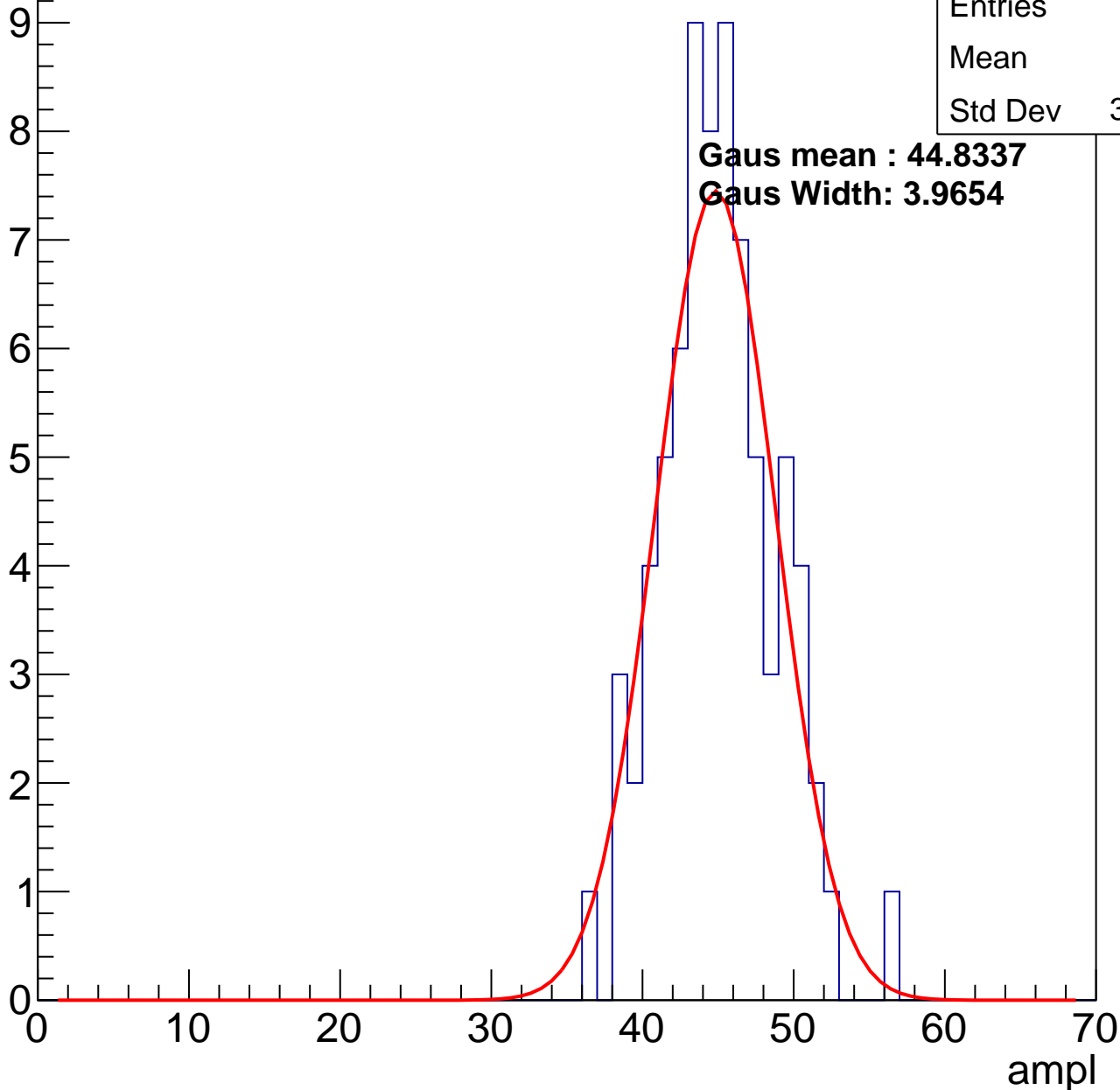
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	44.6
Std Dev	3.727

Gaus mean : 44.8337

Gaus Width: 3.9654

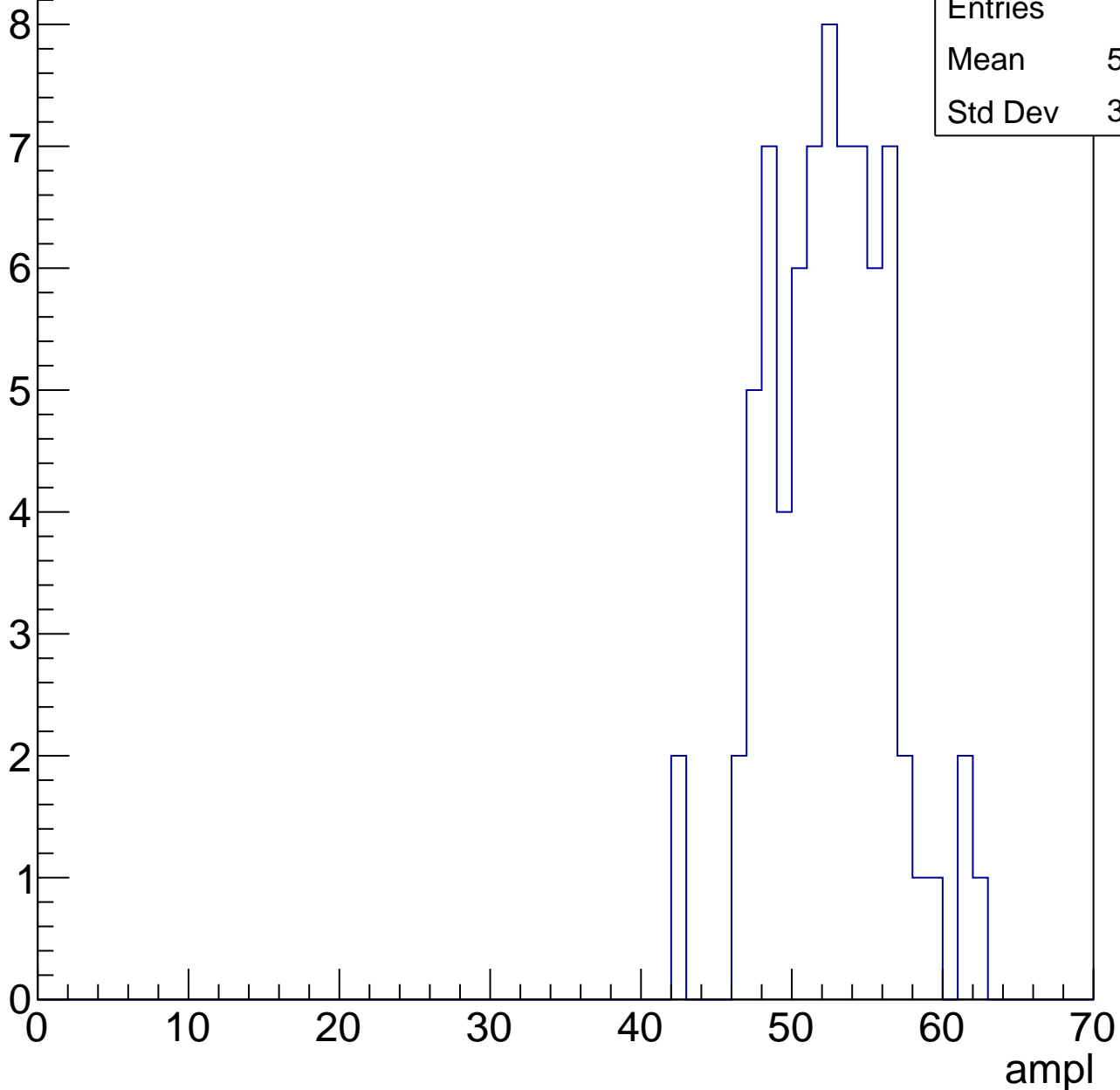


B1L100S, U11-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	52.03
Std Dev	3.956

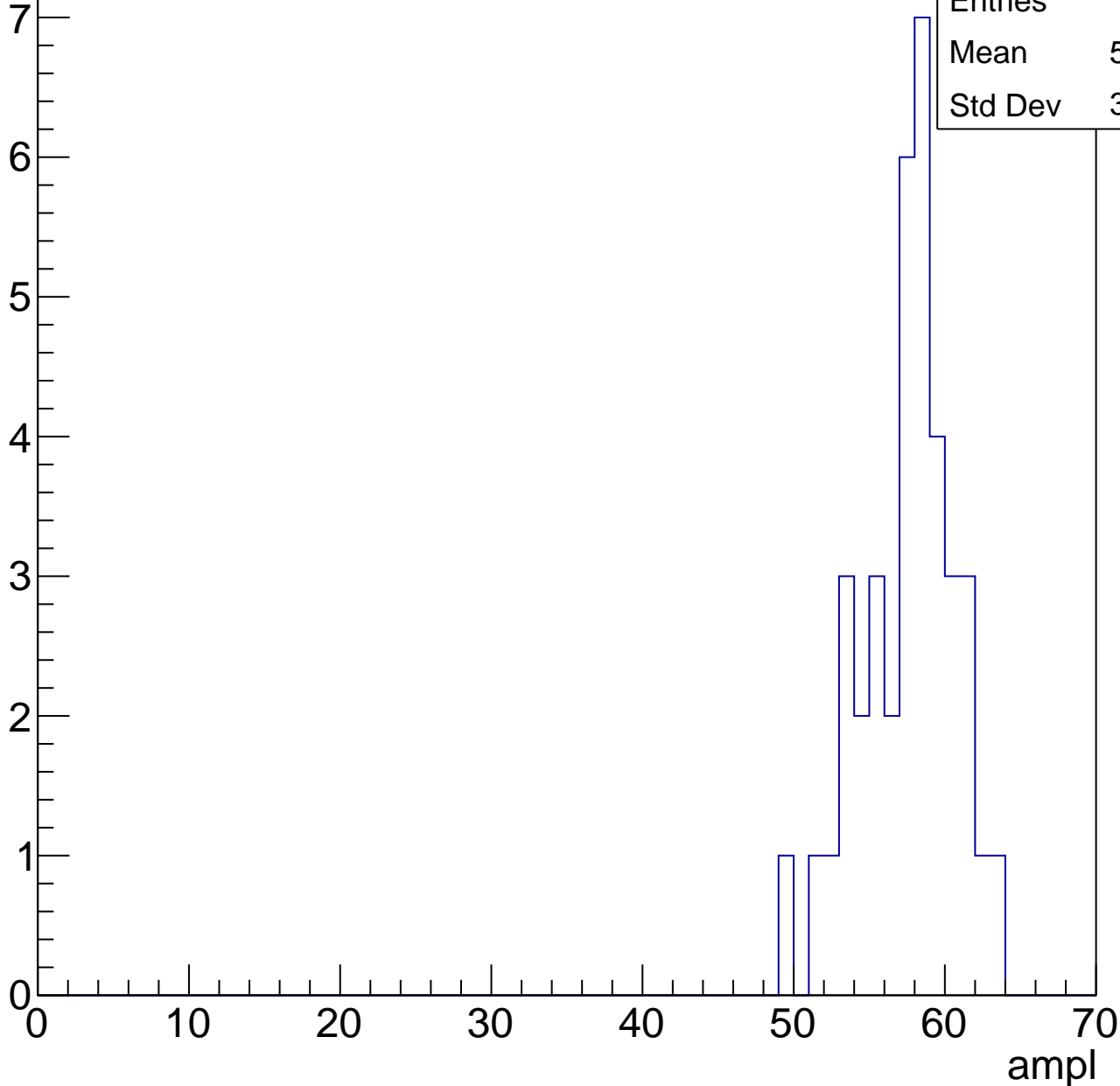


B1L100S, U11-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	57.05
Std Dev	3.095

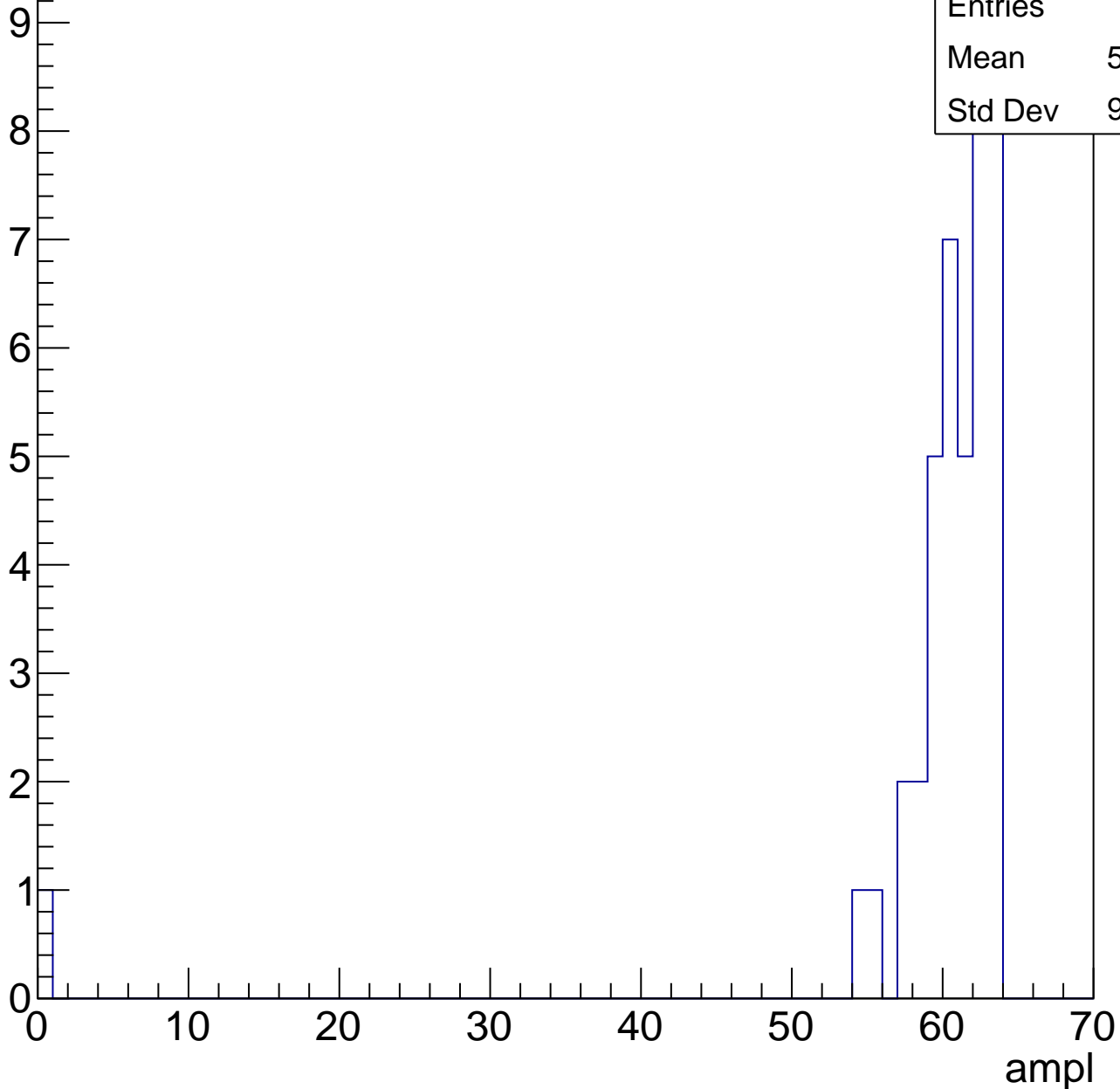


B1L100S, U11-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	59.07
Std Dev	9.595



B1L100S, U11-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L100S, U11-ch73, adc0

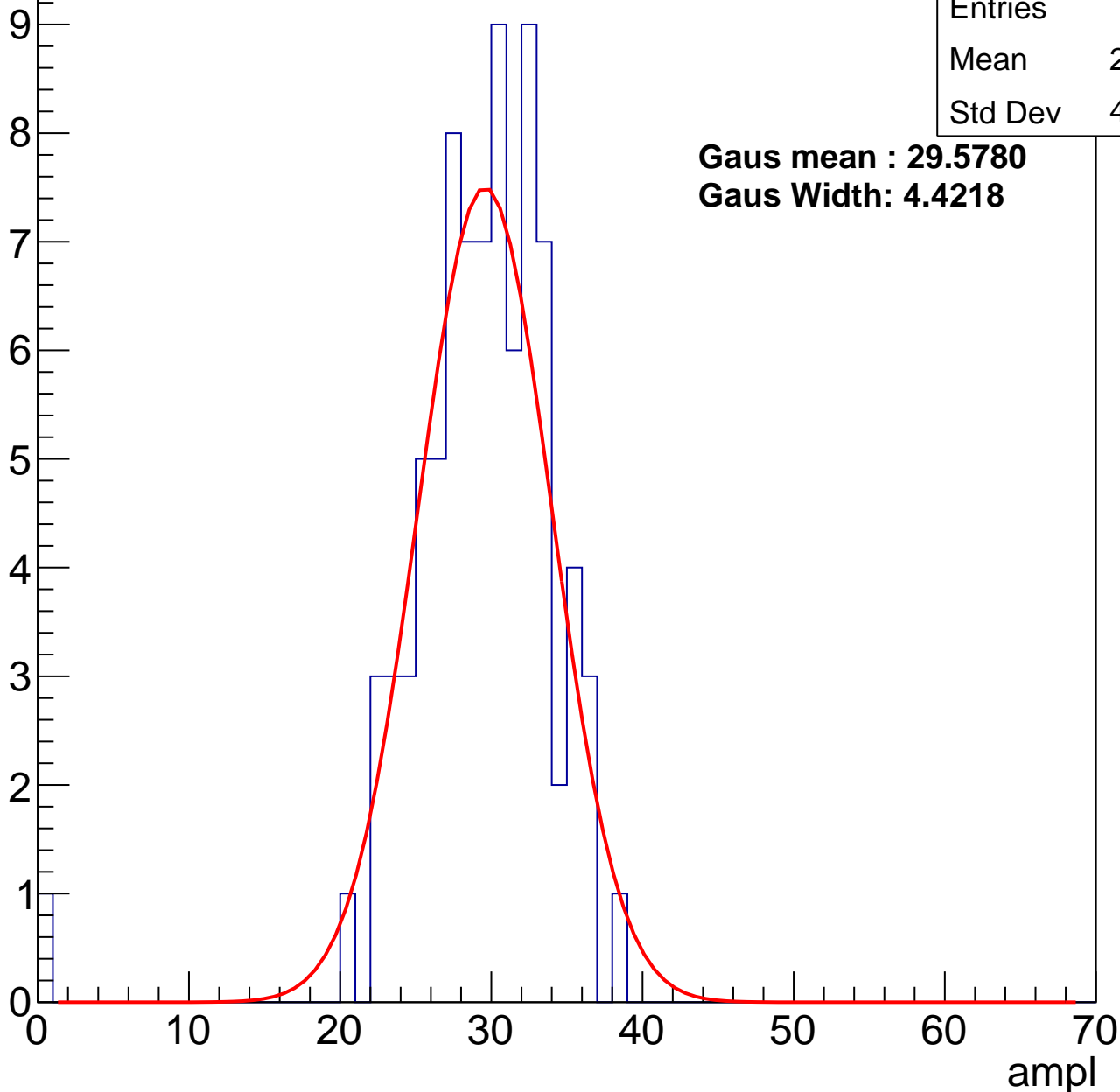
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	28.88
Std Dev	4.956

Gaus mean : 29.5780

Gaus Width: 4.4218



B1L100S, U11-ch73, adc1

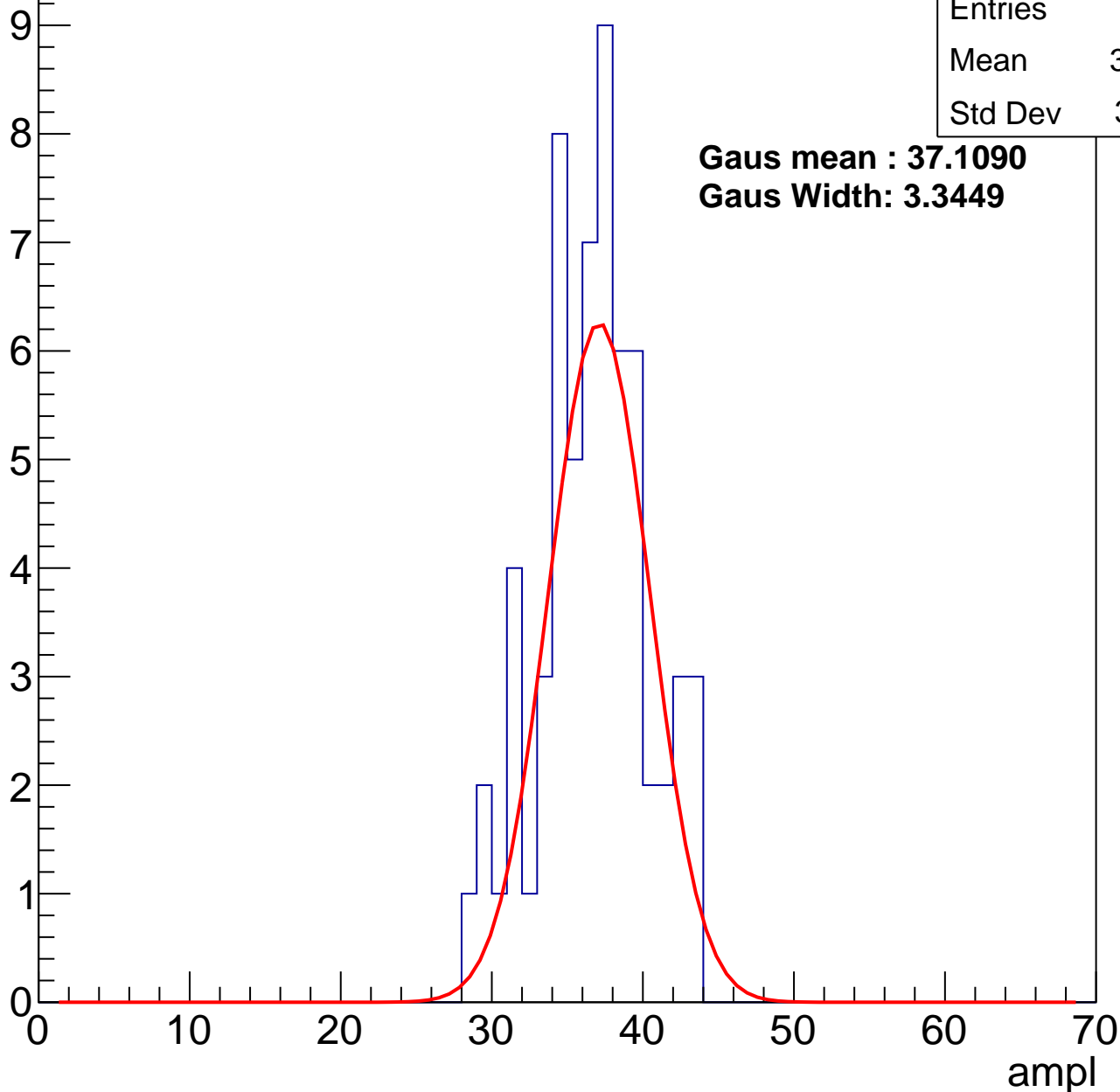
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	36.22
Std Dev	3.561

Gaus mean : 37.1090

Gaus Width: 3.3449



B1L100S, U11-ch73, adc2

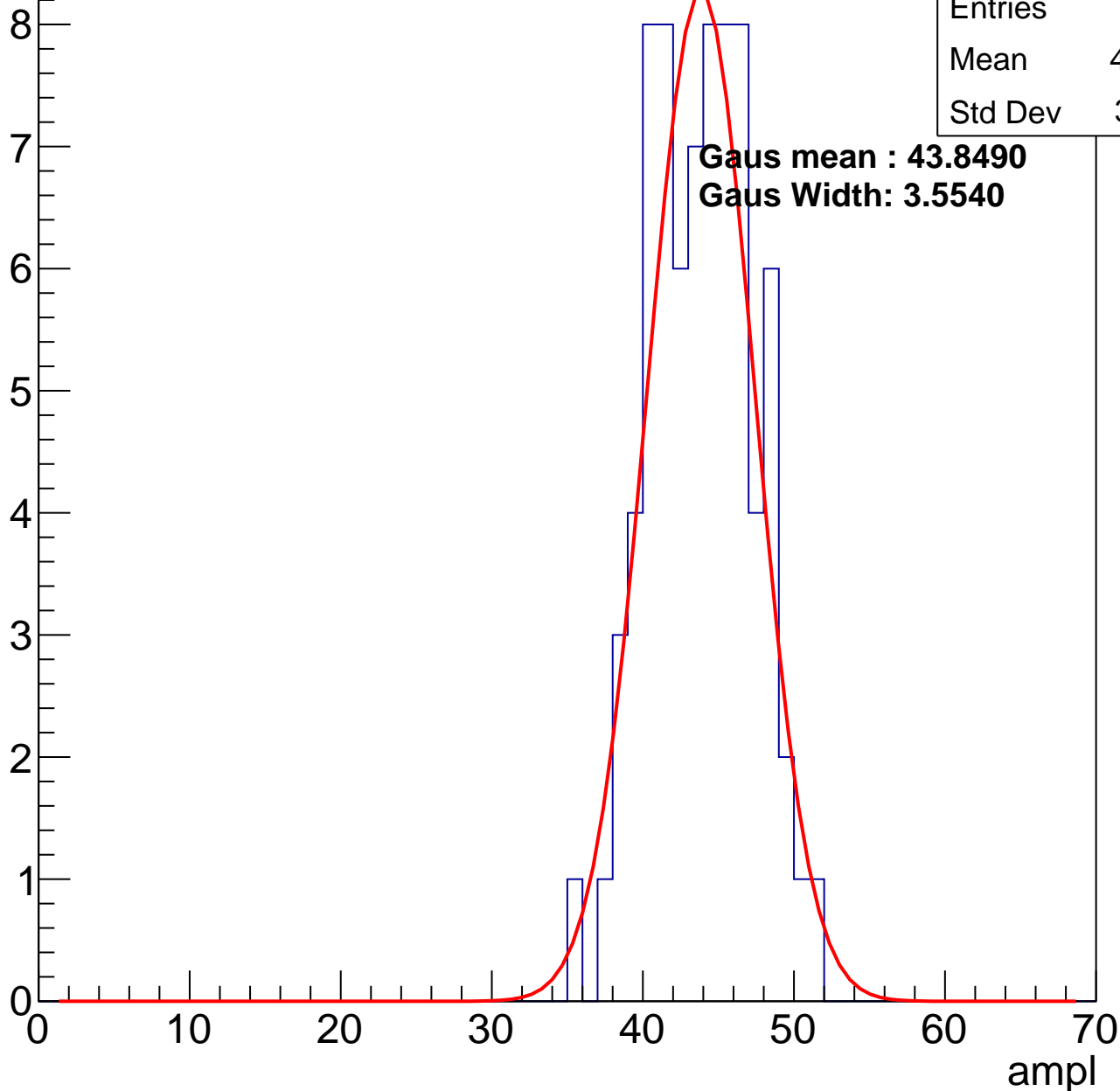
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	43.39
Std Dev	3.341

Gaus mean : 43.8490

Gaus Width: 3.5540

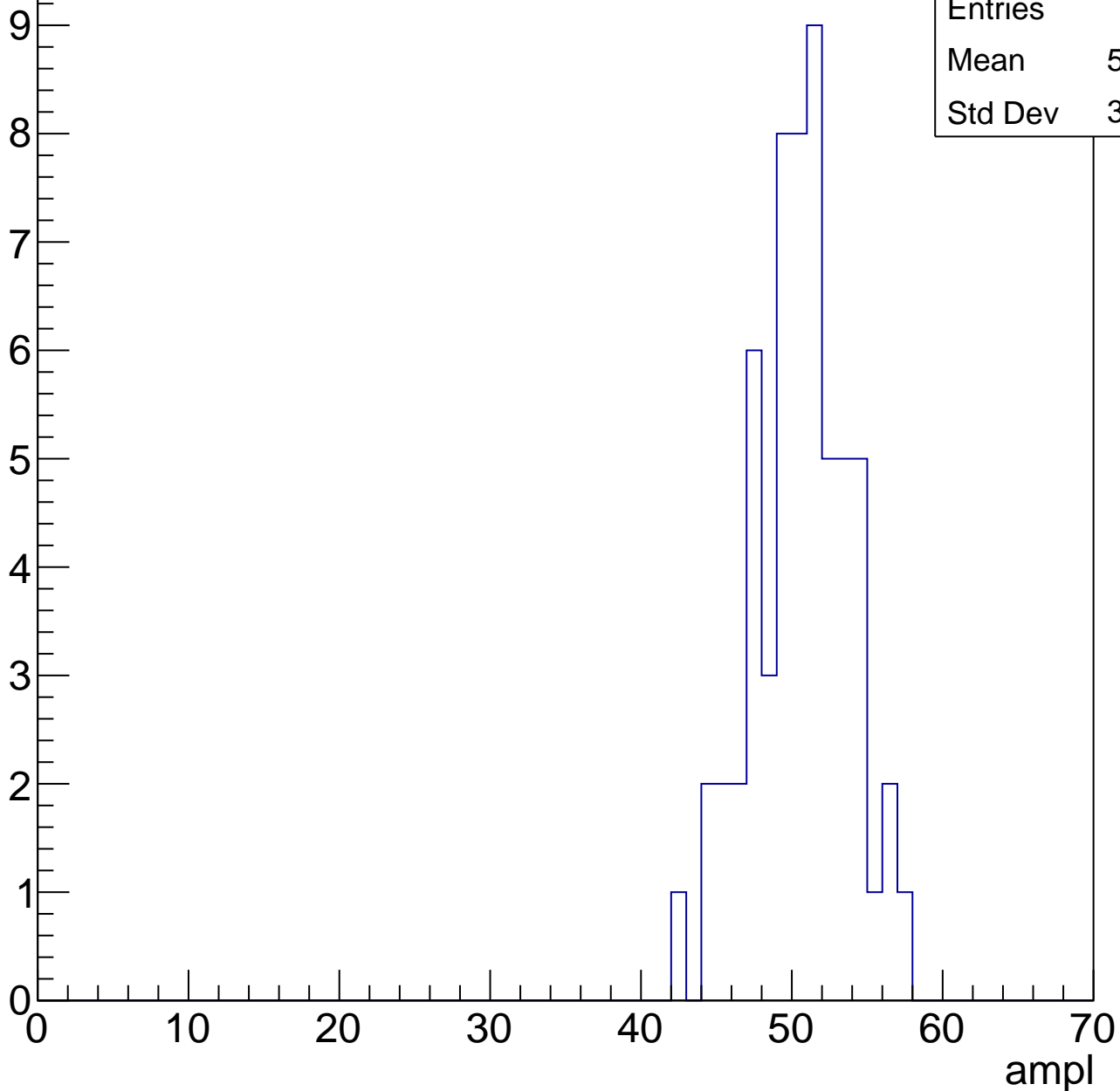


B1L100S, U11-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	50.13
Std Dev	3.138

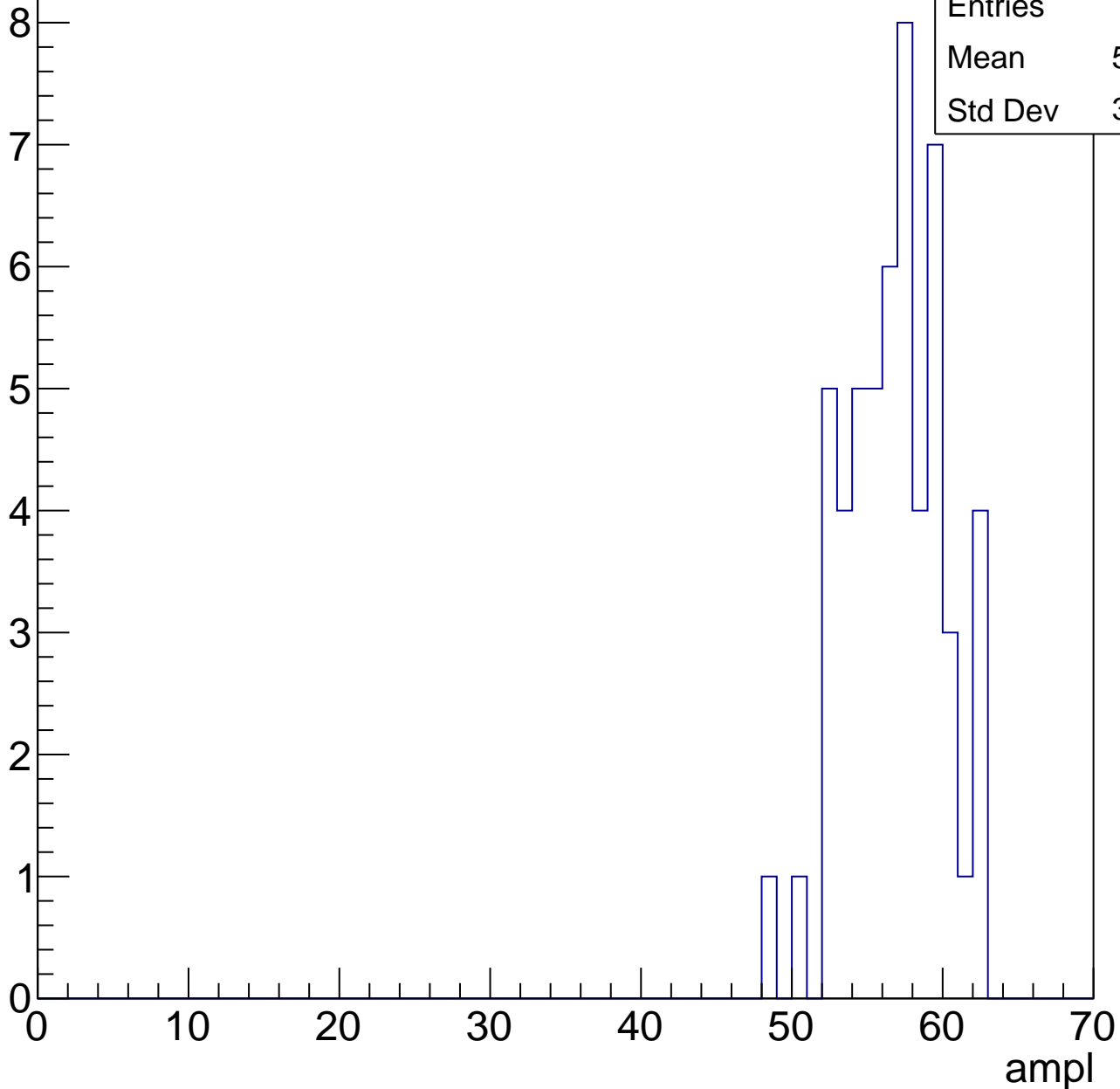


B1L100S, U11-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	56.31
Std Dev	3.161

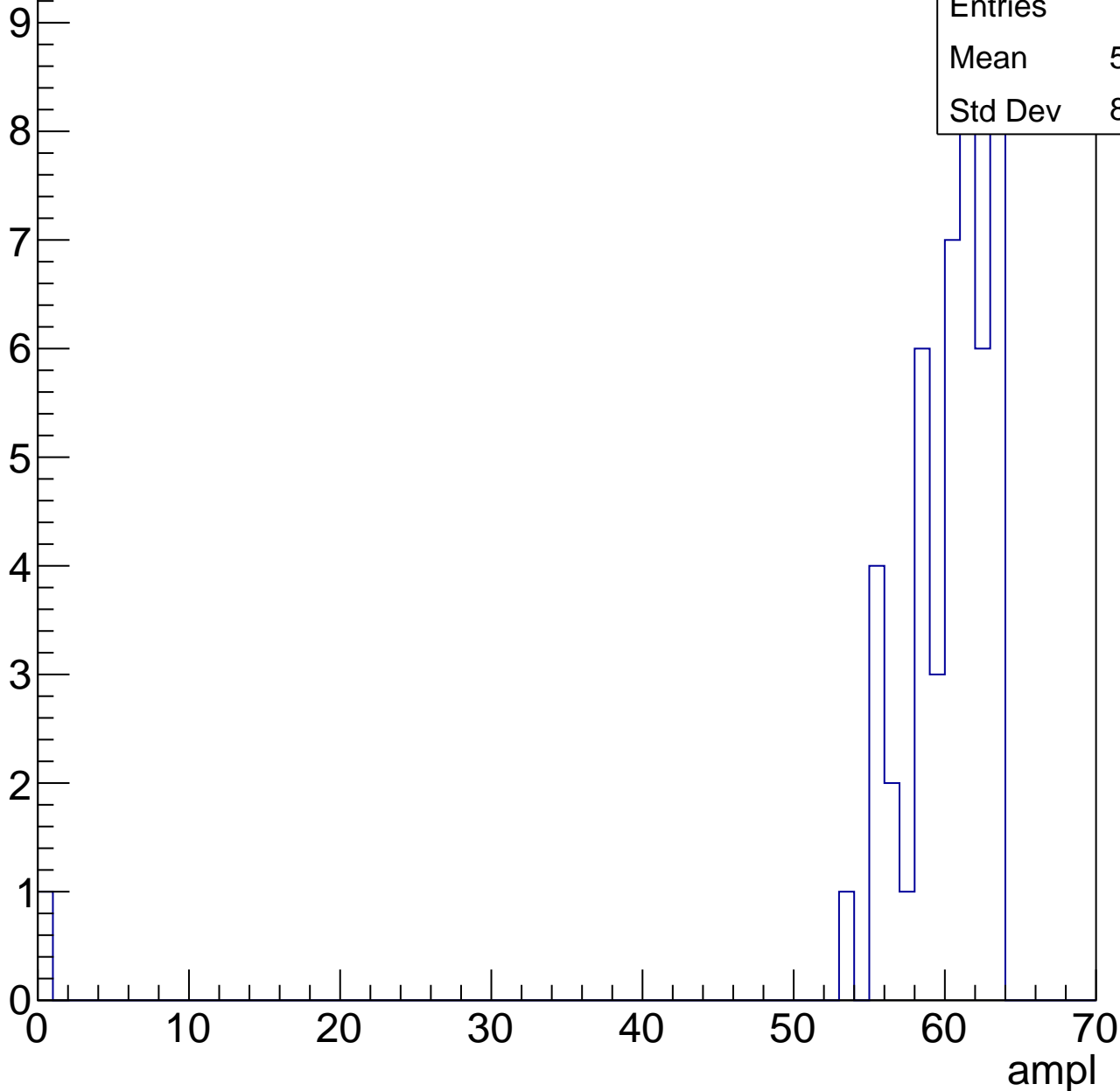


B1L100S, U11-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.67
Std Dev	8.858



B1L100S, U11-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch74, adc0

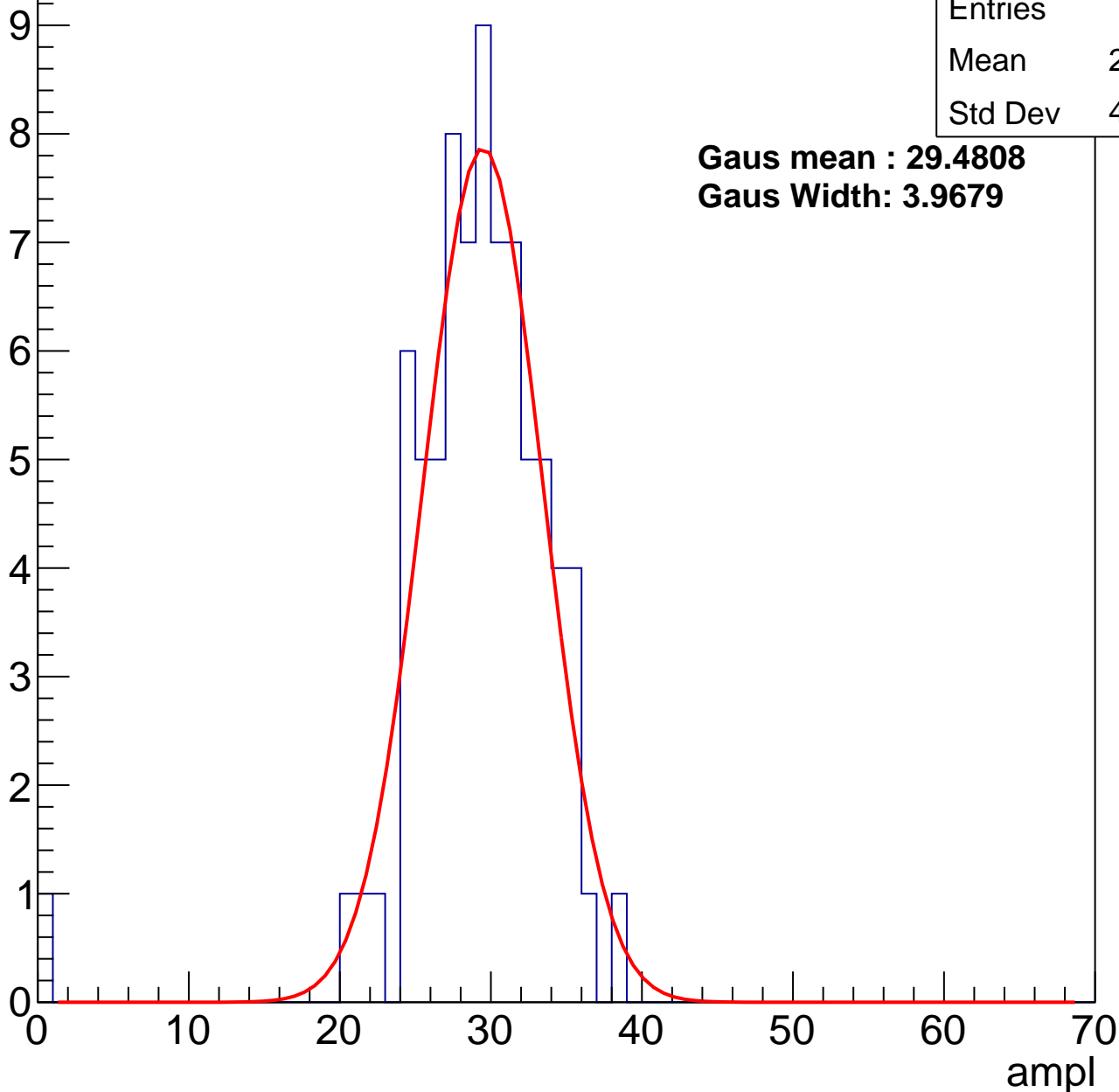
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.68
Std Dev	4.903

Gaus mean : 29.4808

Gaus Width: 3.9679



B1L100S, U11-ch74, adc1

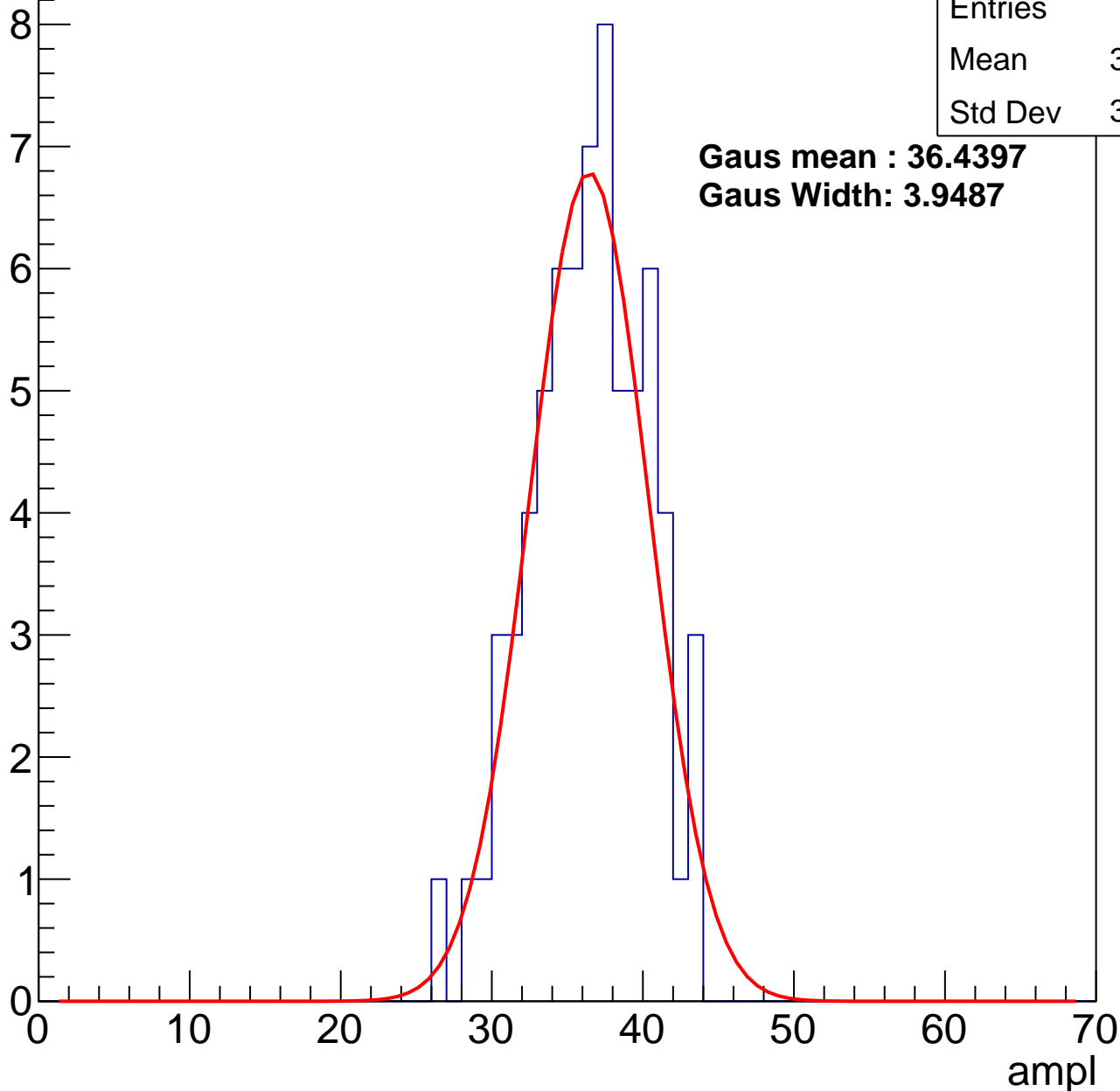
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	35.96
Std Dev	3.782

Gaus mean : 36.4397

Gaus Width: 3.9487



B1L100S, U11-ch74, adc2

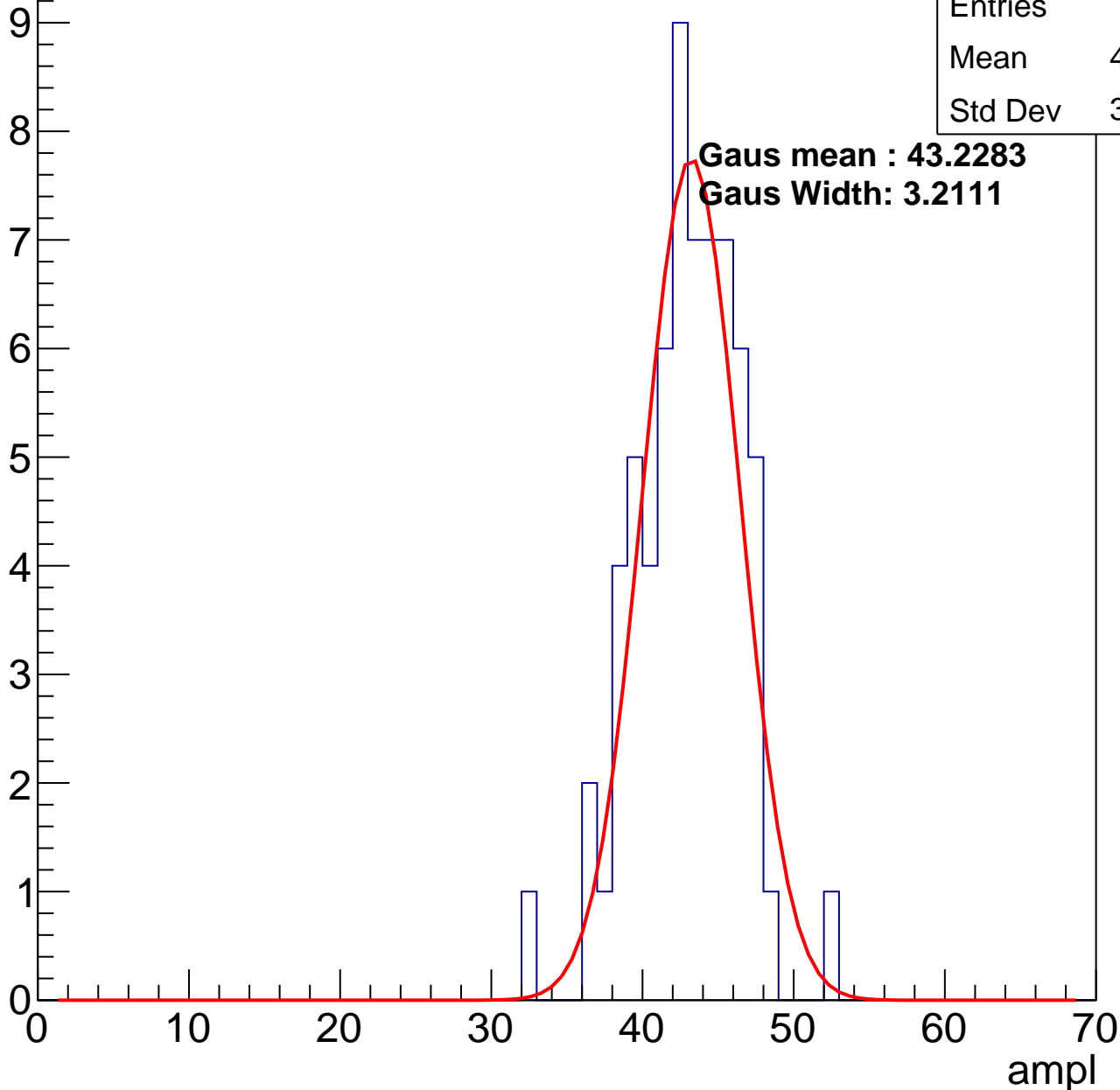
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.53
Std Dev	3.403

Gaus mean : 43.2283

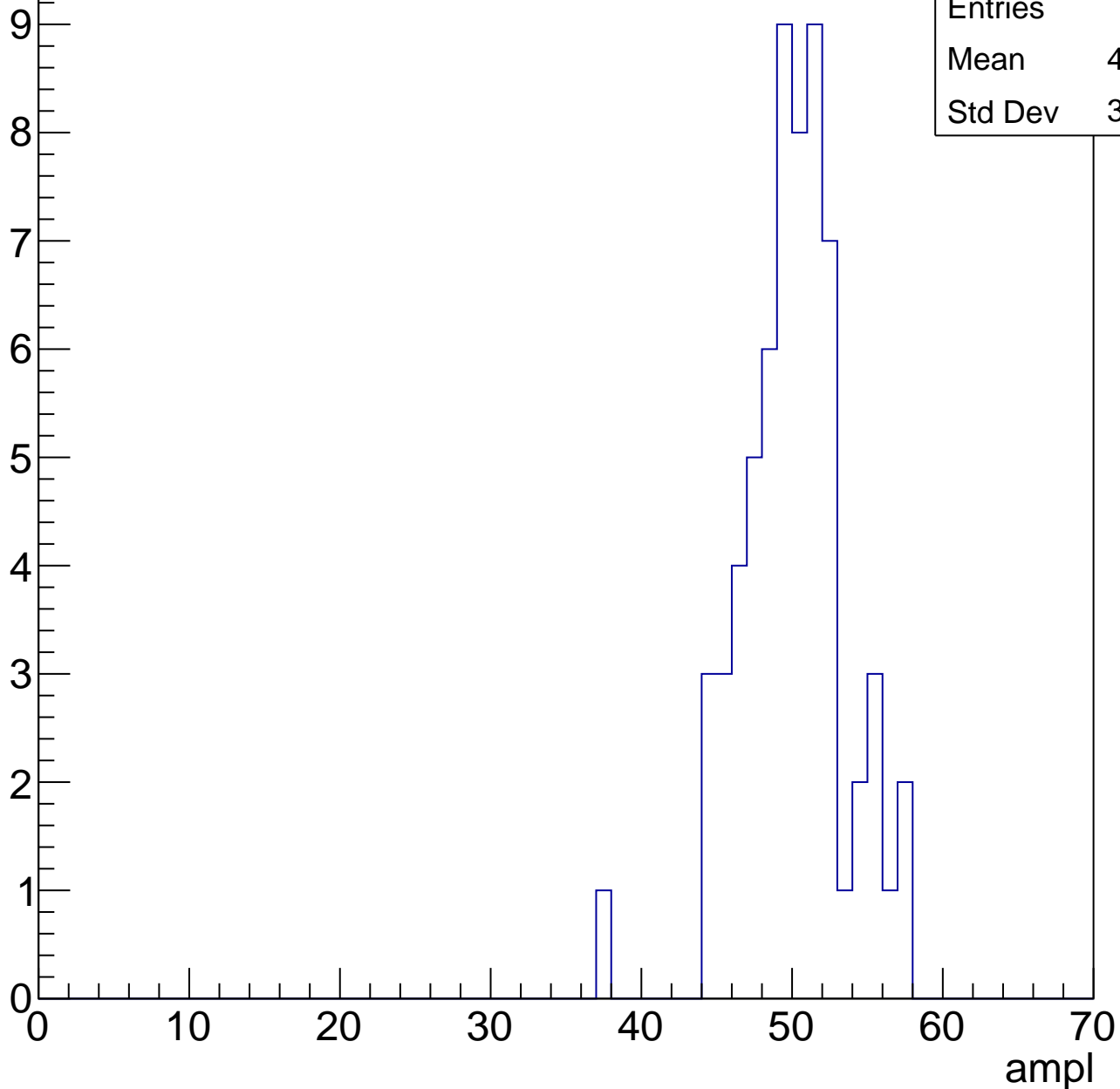
Gaus Width: 3.2111



B1L100S, U11-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

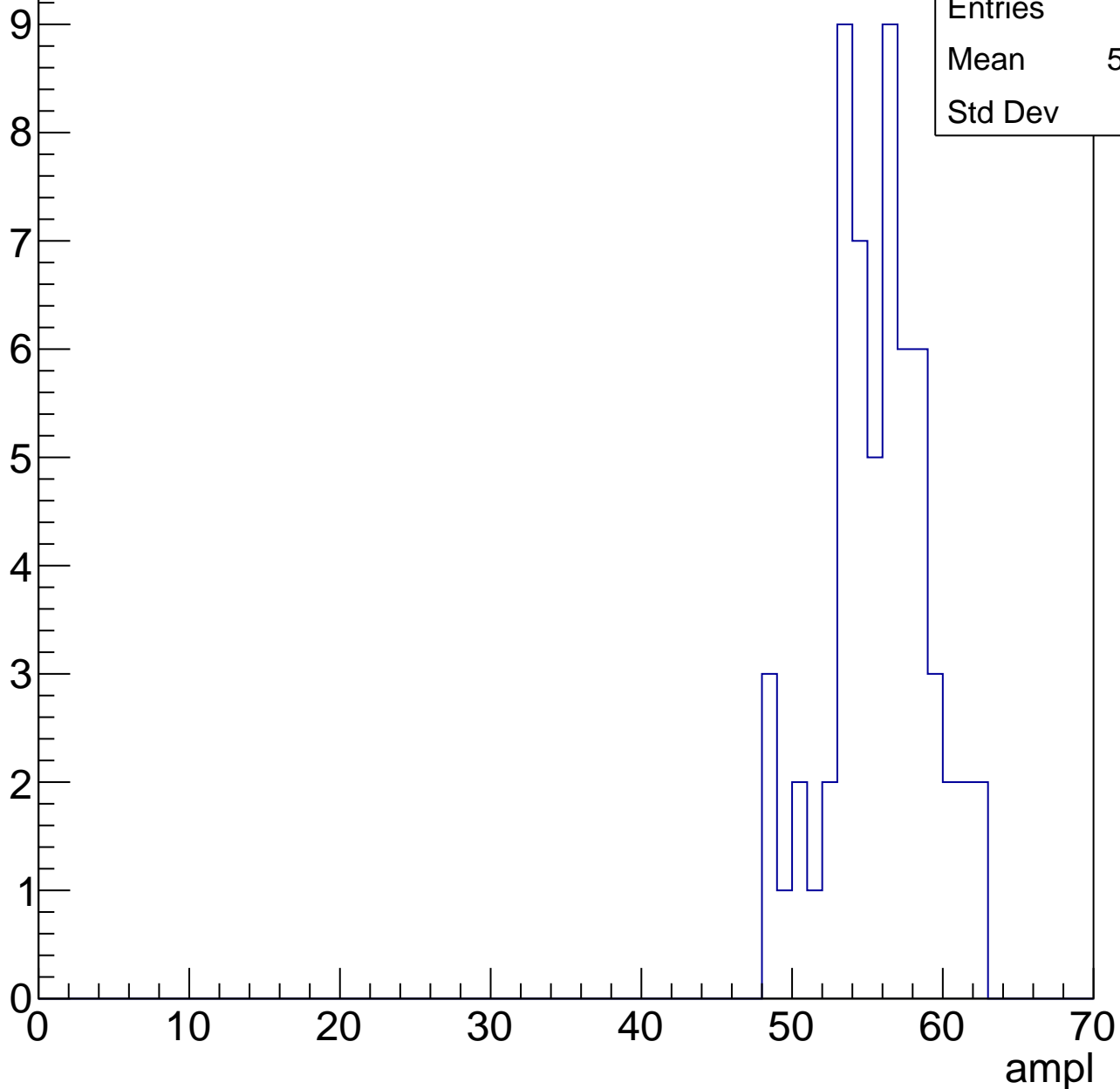
Entry



B1L100S, U11-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

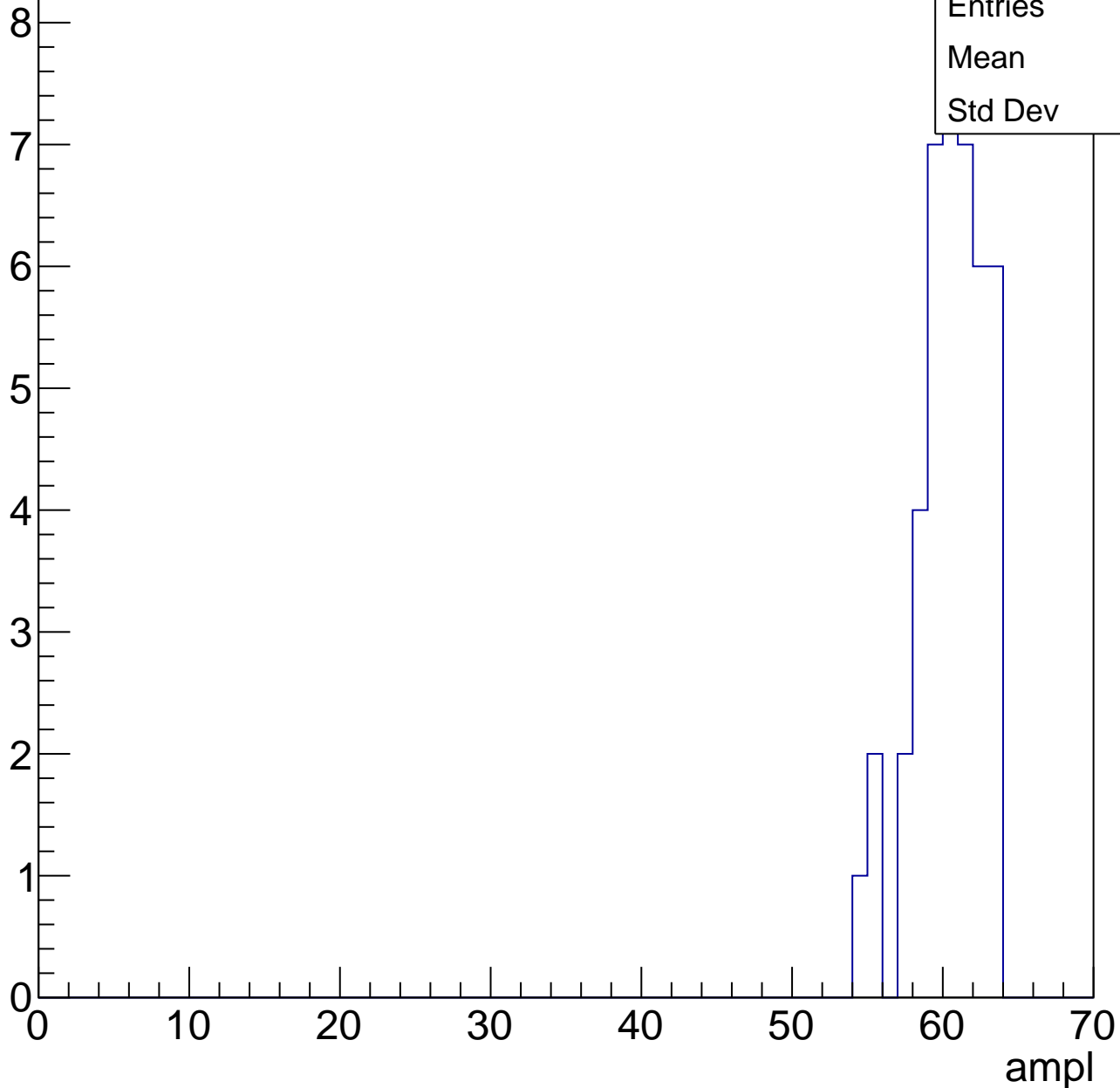


Entries	60
Mean	55.25
Std Dev	3.32

B1L100S, U11-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch75, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	29.72
Std Dev	4.486

Gaus mean : 30.5937

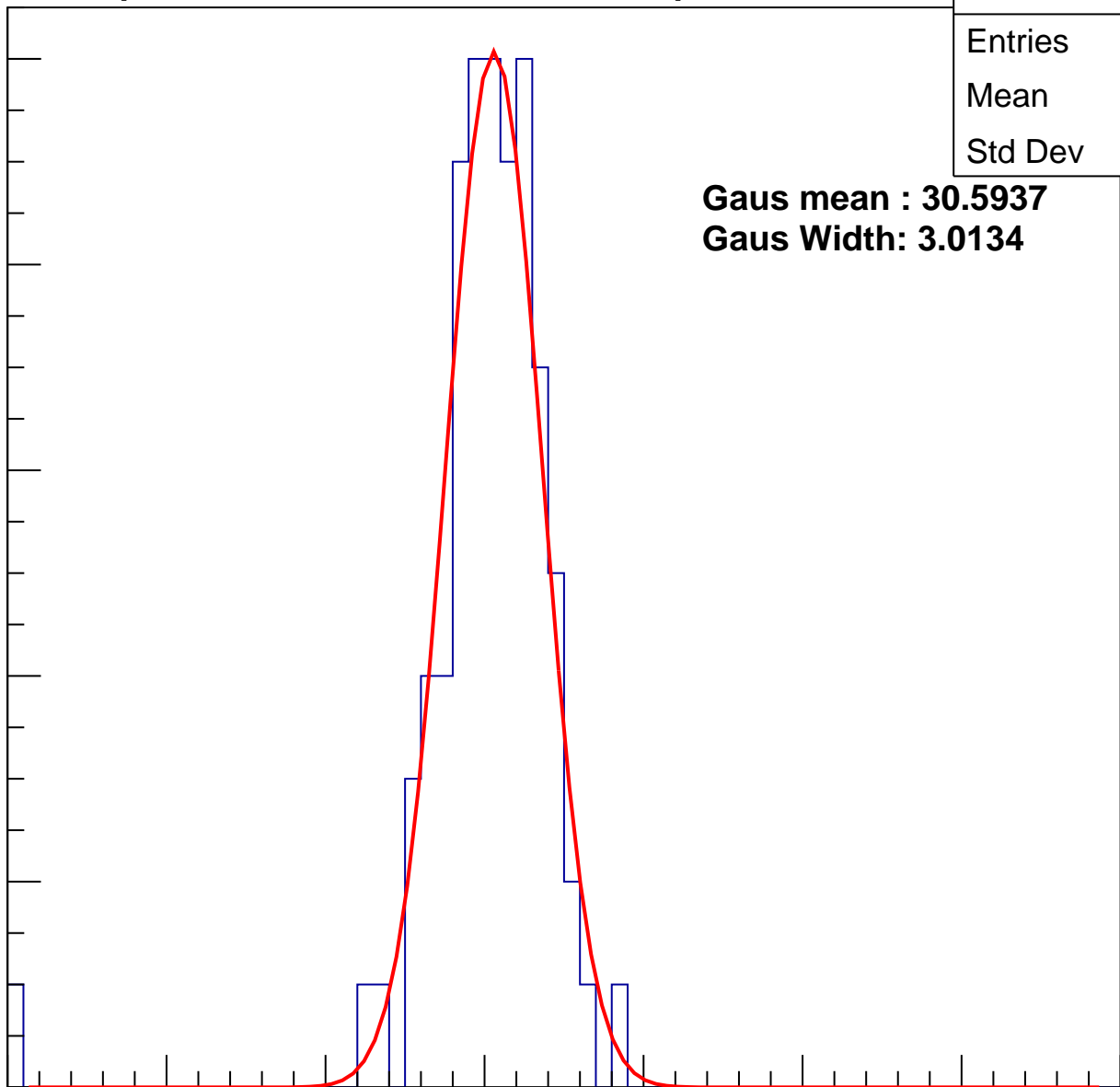
Gaus Width: 3.0134

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch75, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	37.58
Std Dev	2.887

Gaus mean : 37.4377

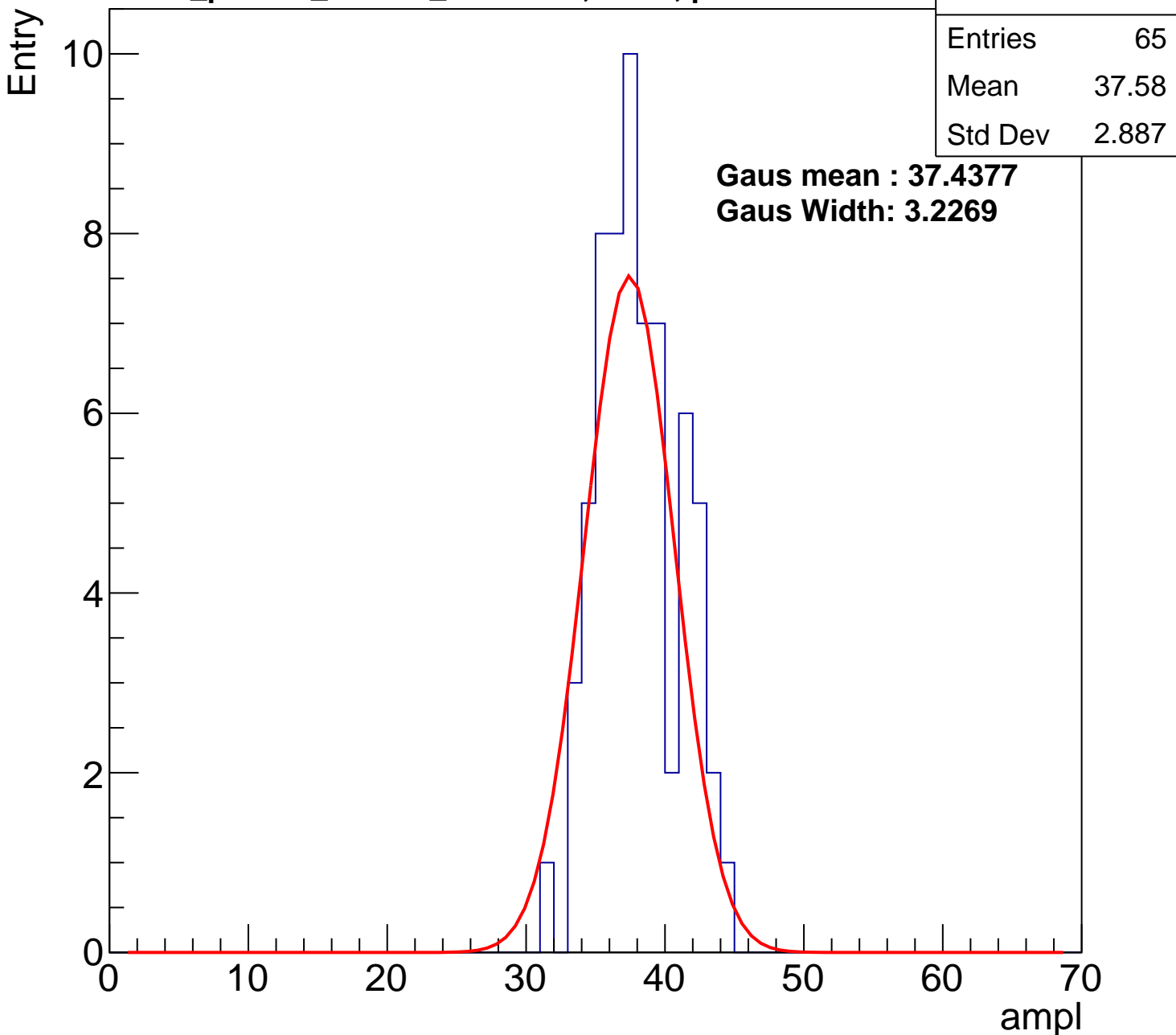
Gaus Width: 3.2269

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch75, adc2

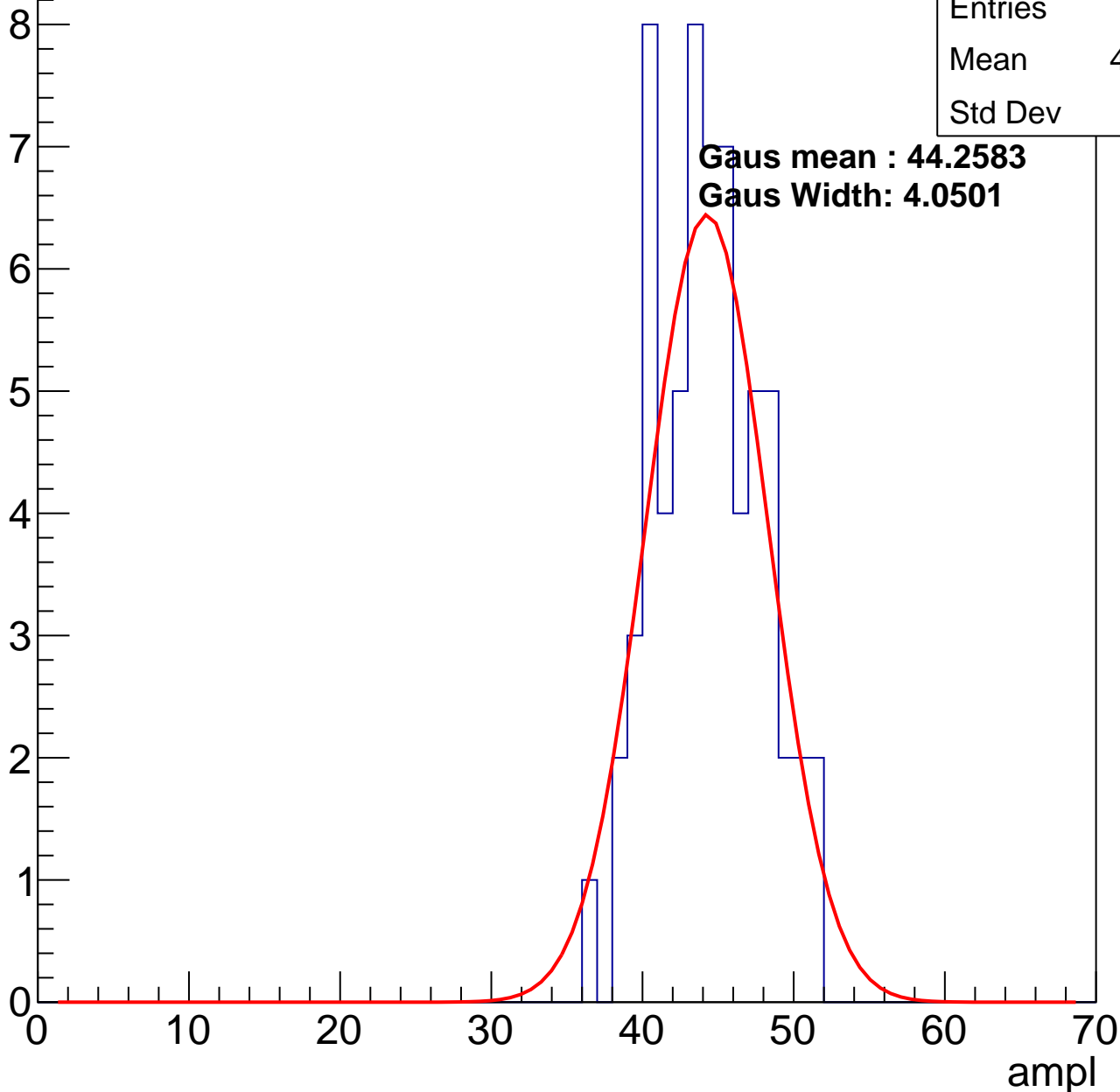
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.83
Std Dev	3.44

Gaus mean : 44.2583

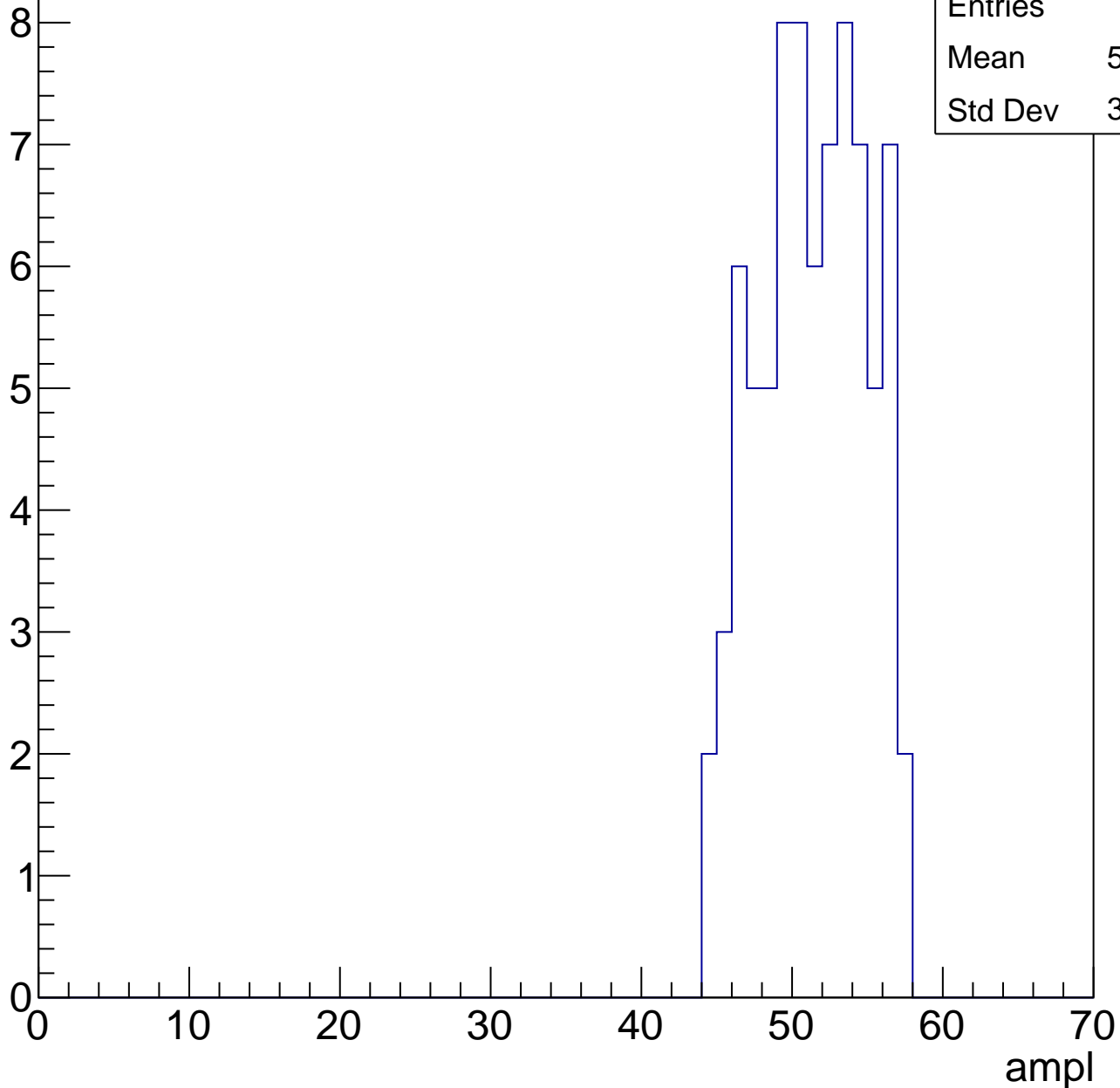
Gaus Width: 4.0501



B1L100S, U11-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

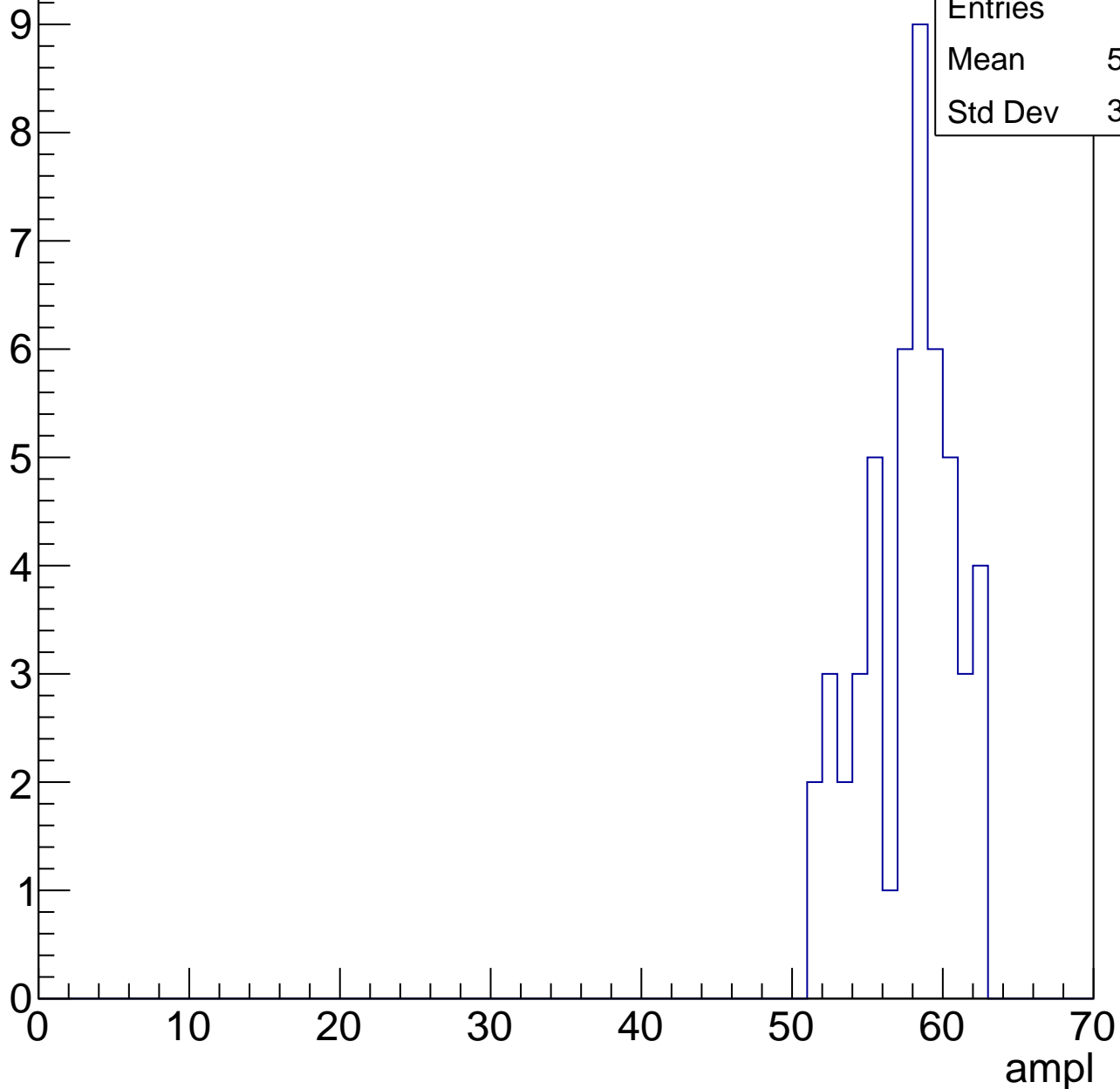
Entry



B1L100S, U11-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

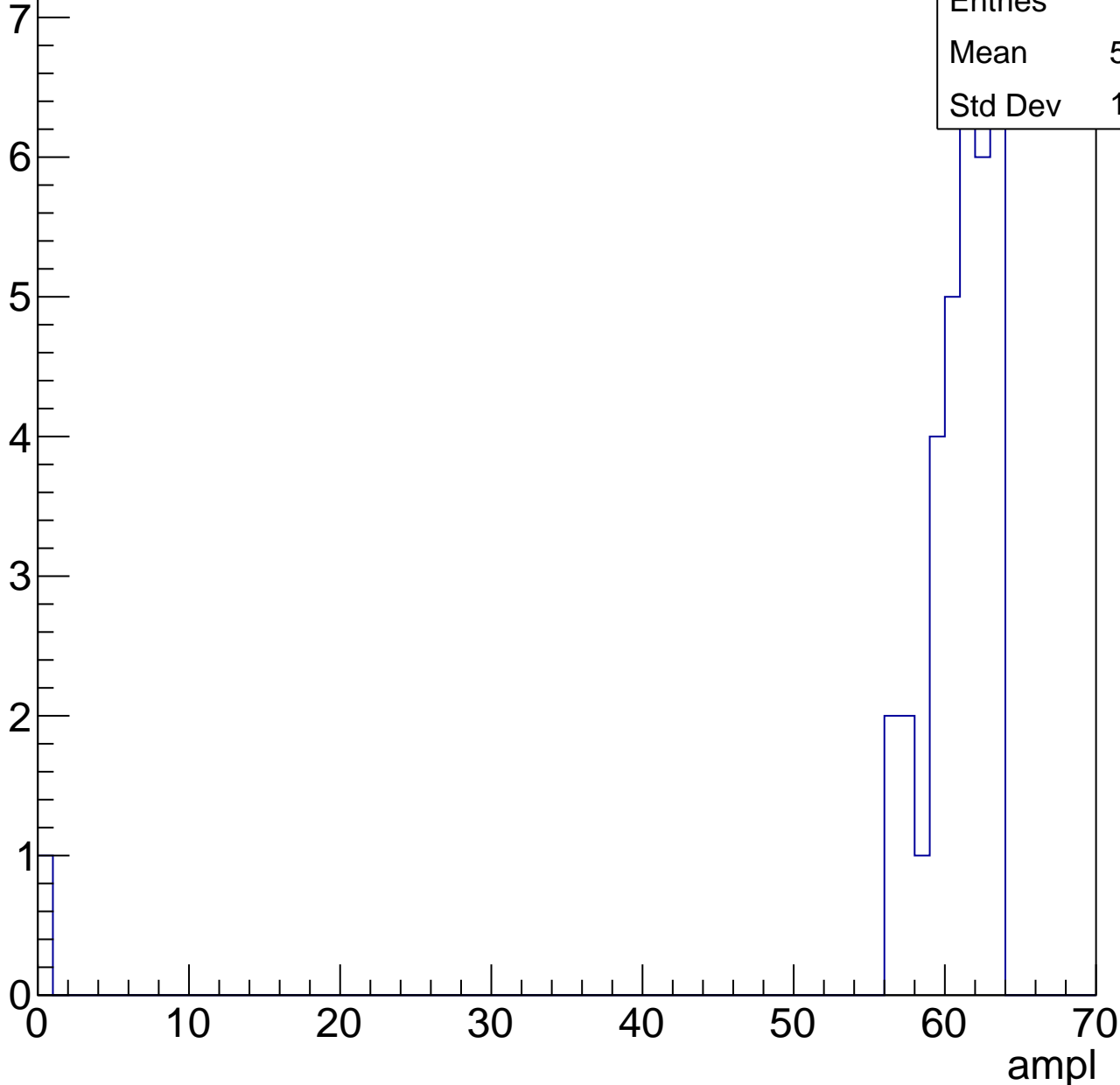


B1L100S, U11-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	58.86
Std Dev	10.29



B1L100S, U11-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch76, adc0

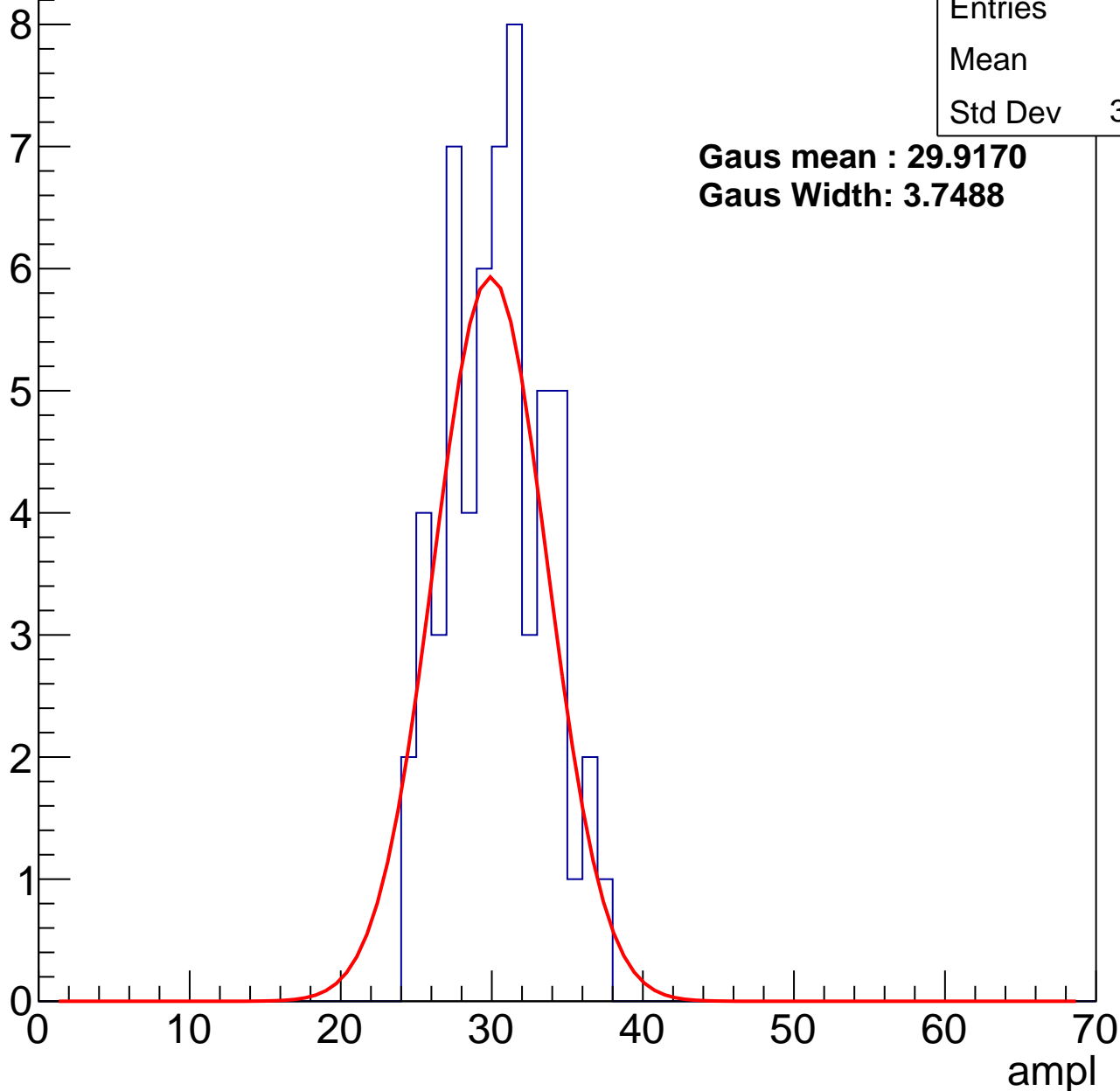
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	29.9
Std Dev	3.204

Gaus mean : 29.9170

Gaus Width: 3.7488



B1L100S, U11-ch76, adc1

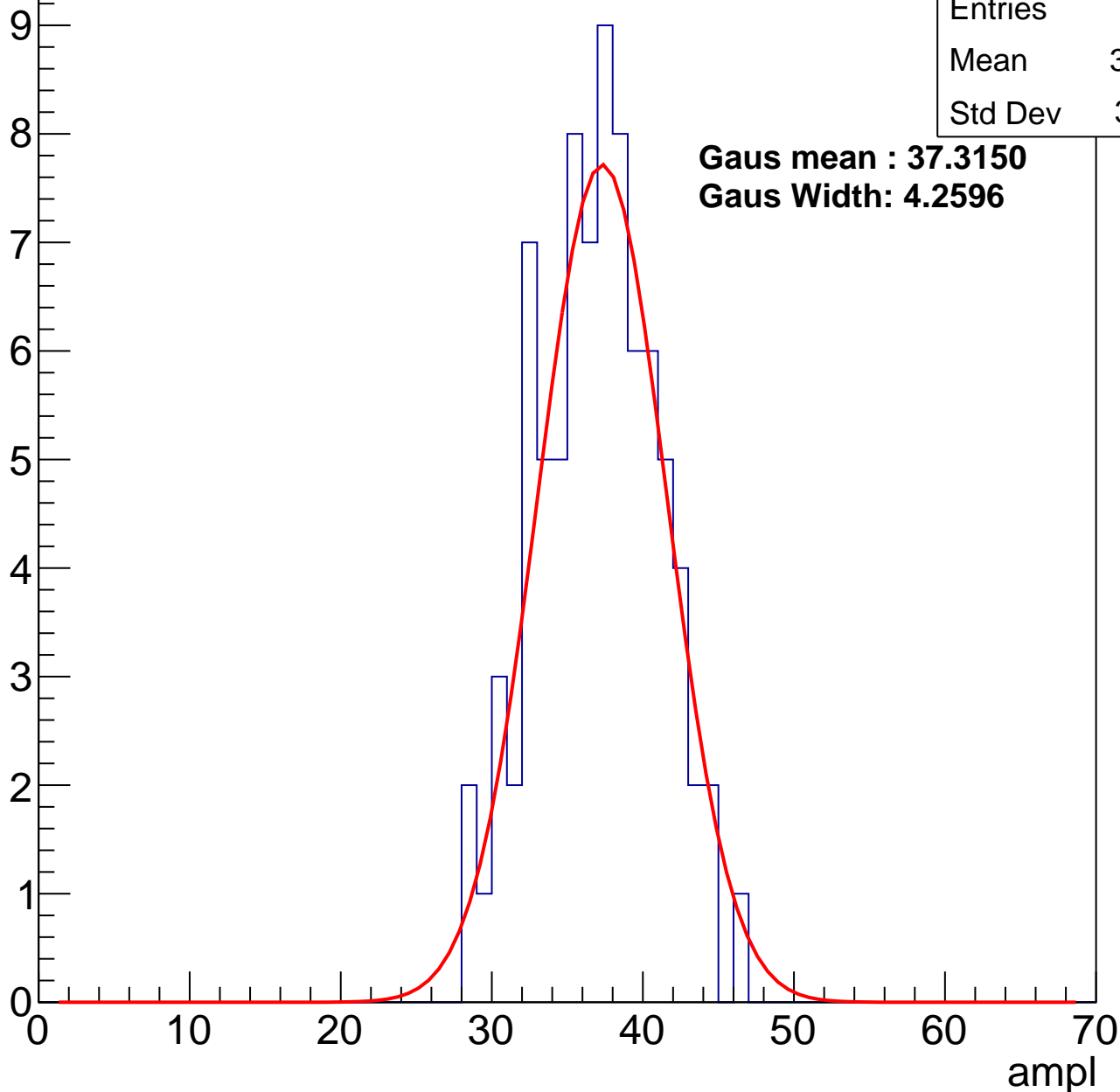
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	36.53
Std Dev	3.931

Gaus mean : 37.3150

Gaus Width: 4.2596



B1L100S, U11-ch76, adc2

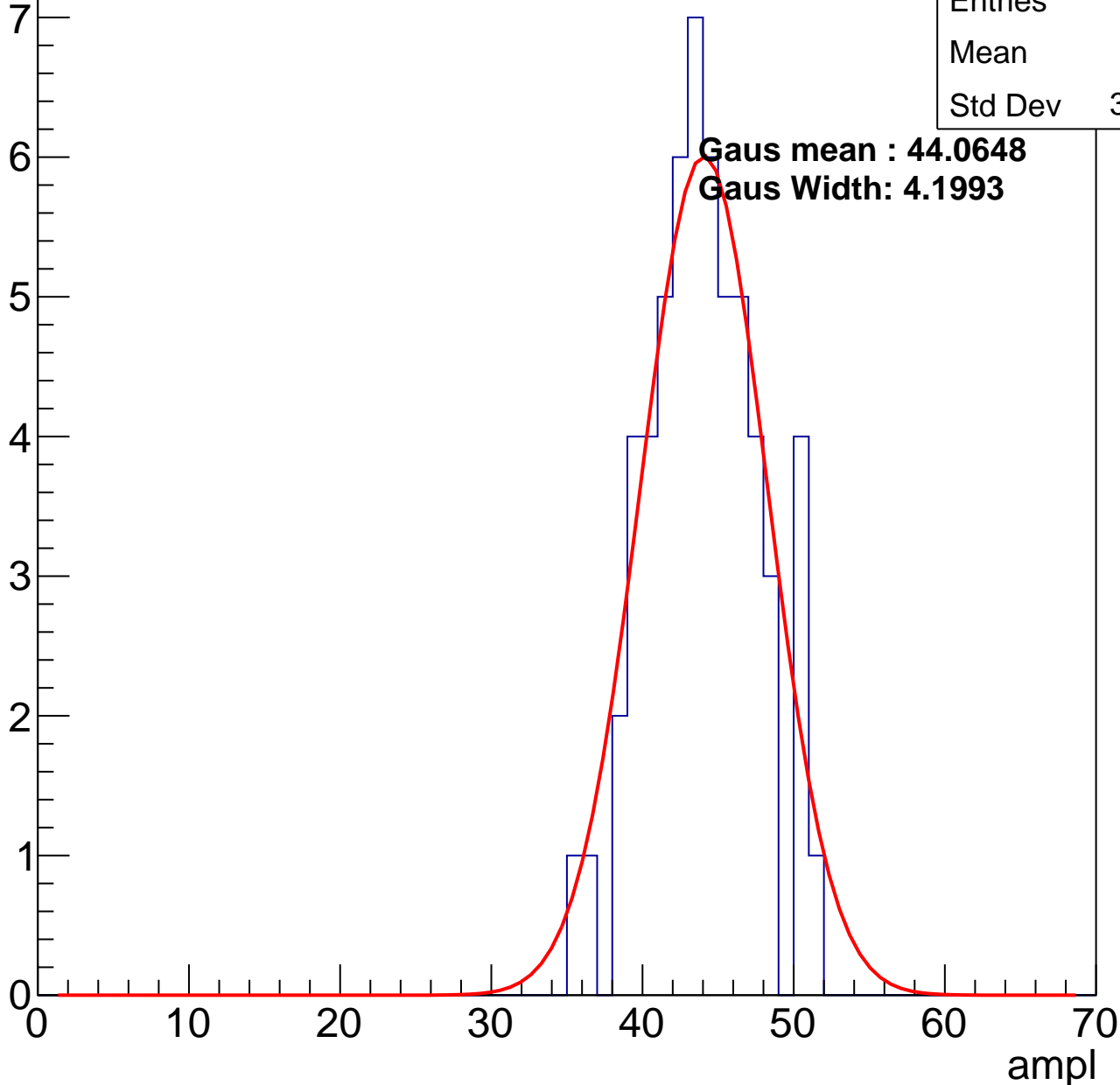
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.5
Std Dev	3.583

Gaus mean : 44.0648

Gaus Width: 4.1993



B1L100S, U11-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

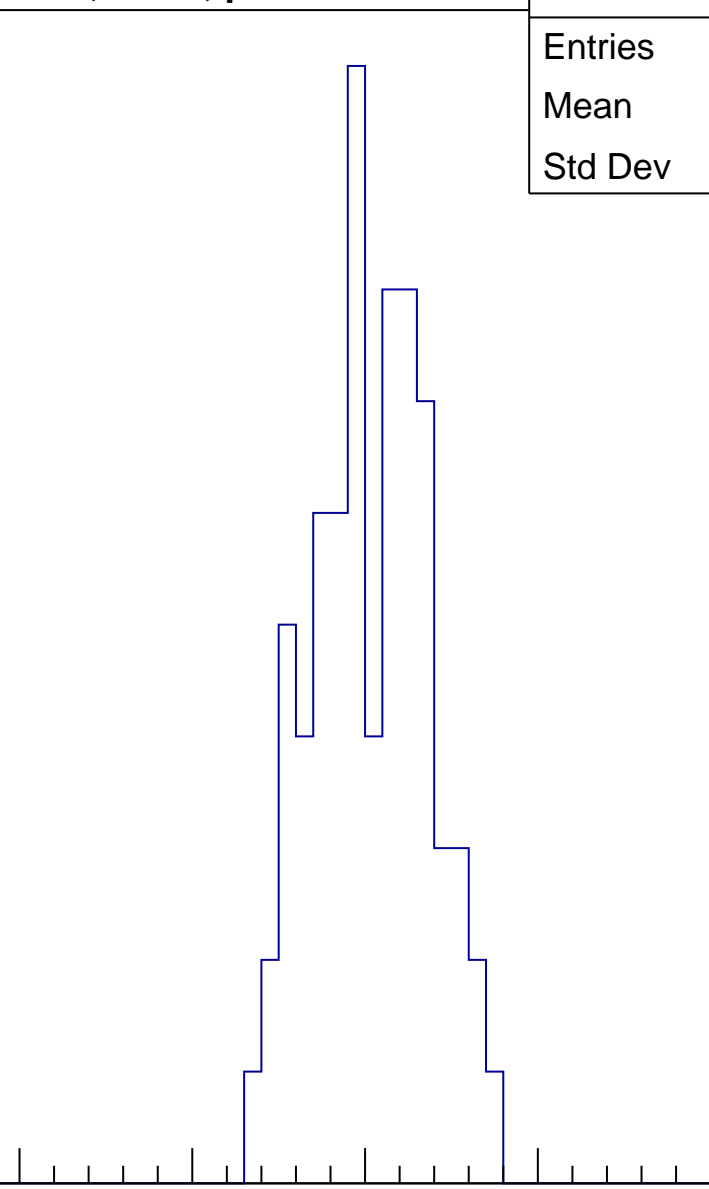
Entries	70
Mean	49.87
Std Dev	3.26

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

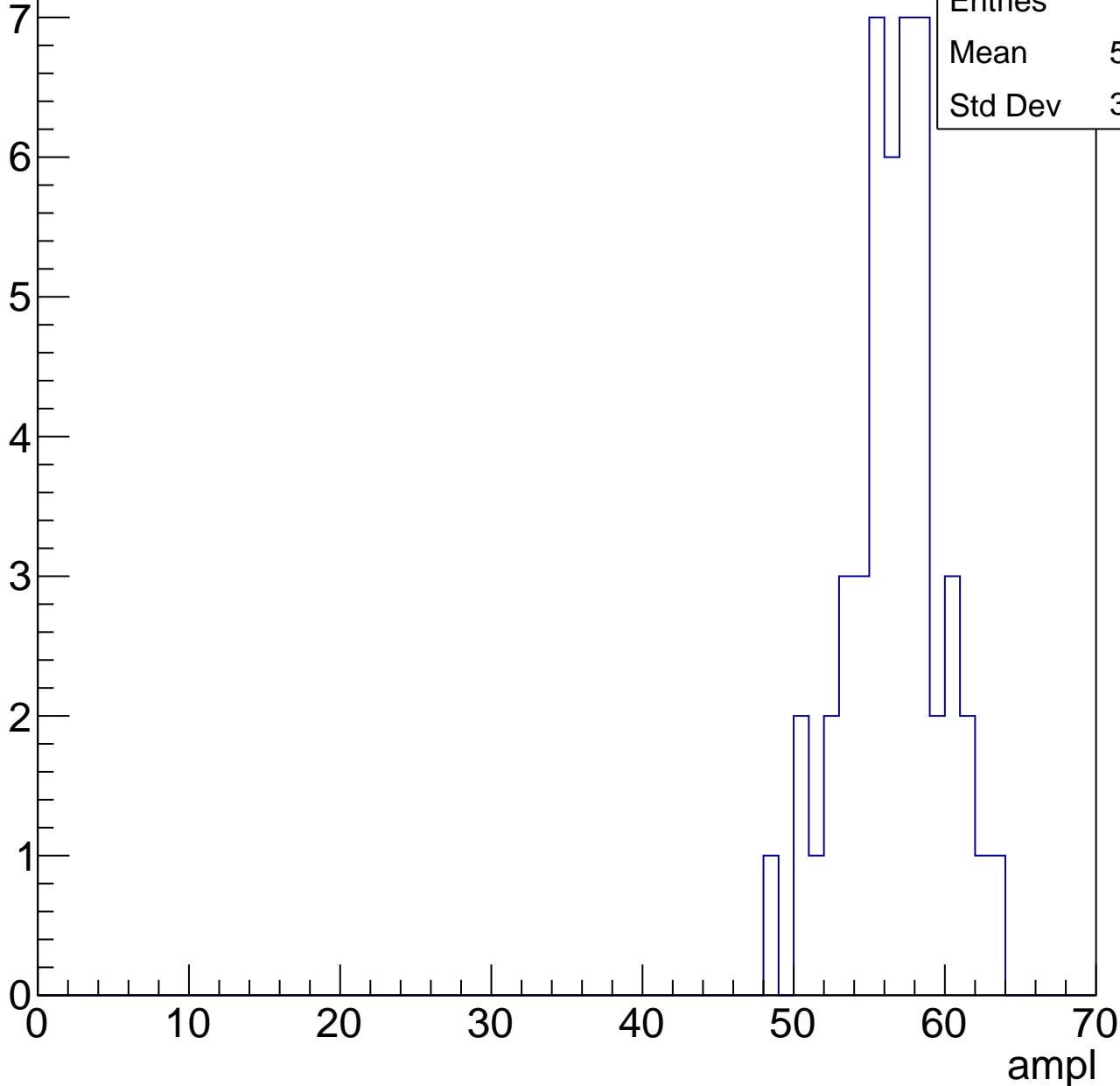


B1L100S, U11-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

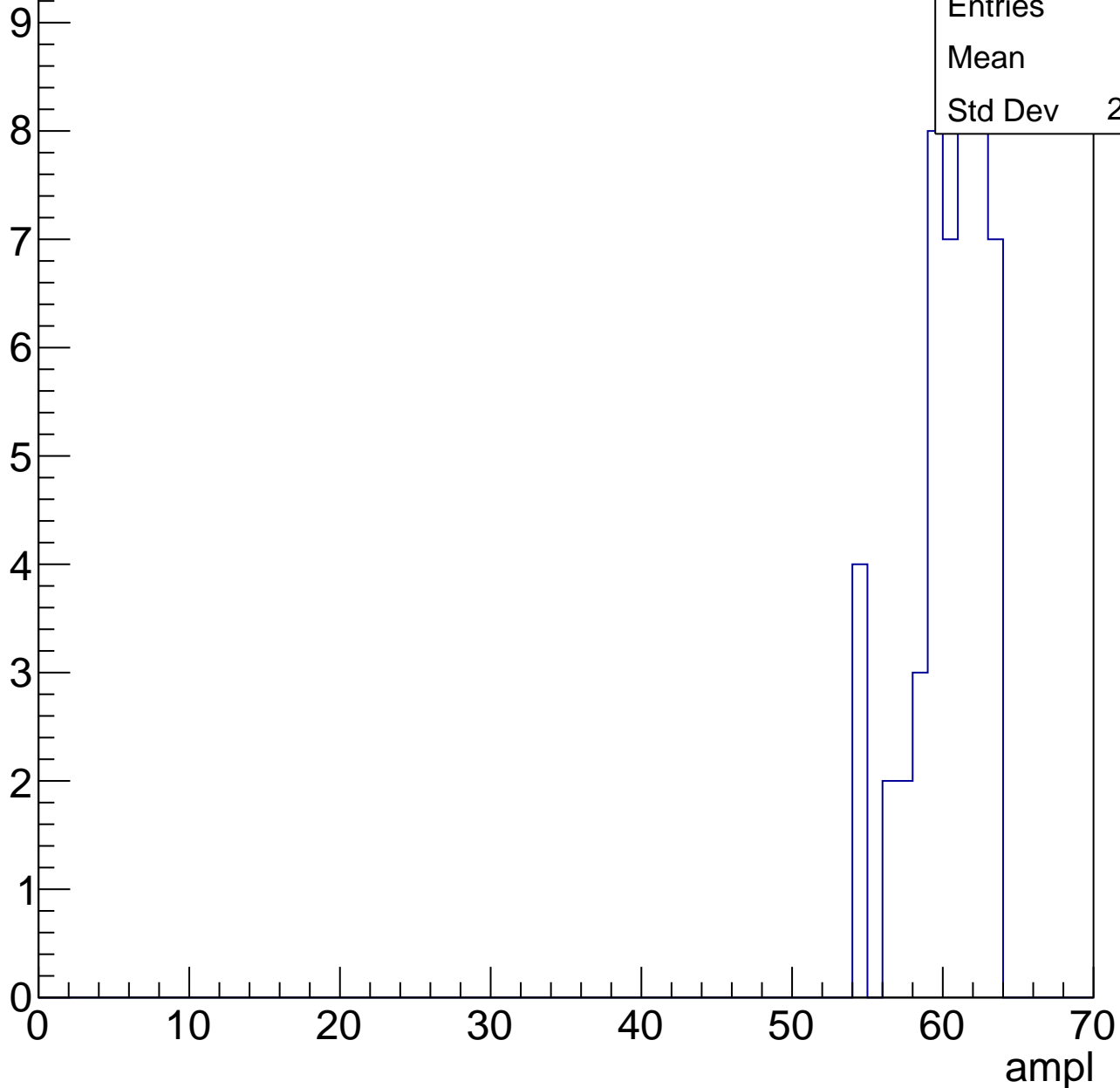
Entries	48
Mean	56.15
Std Dev	3.142



B1L100S, U11-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch77, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	32.27
Std Dev	3.322

Gaus mean : 32.8457

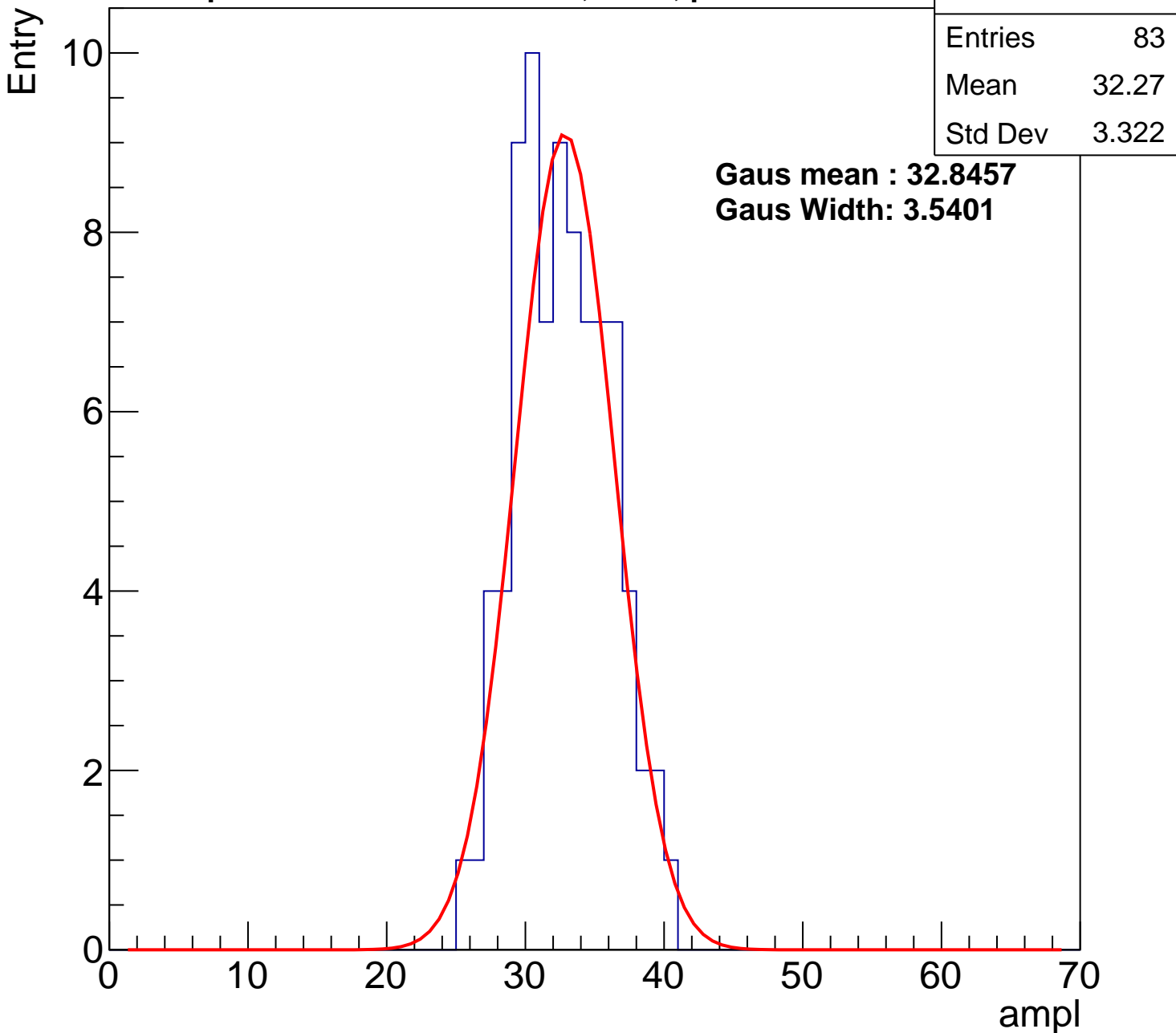
Gaus Width: 3.5401

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch77, adc1

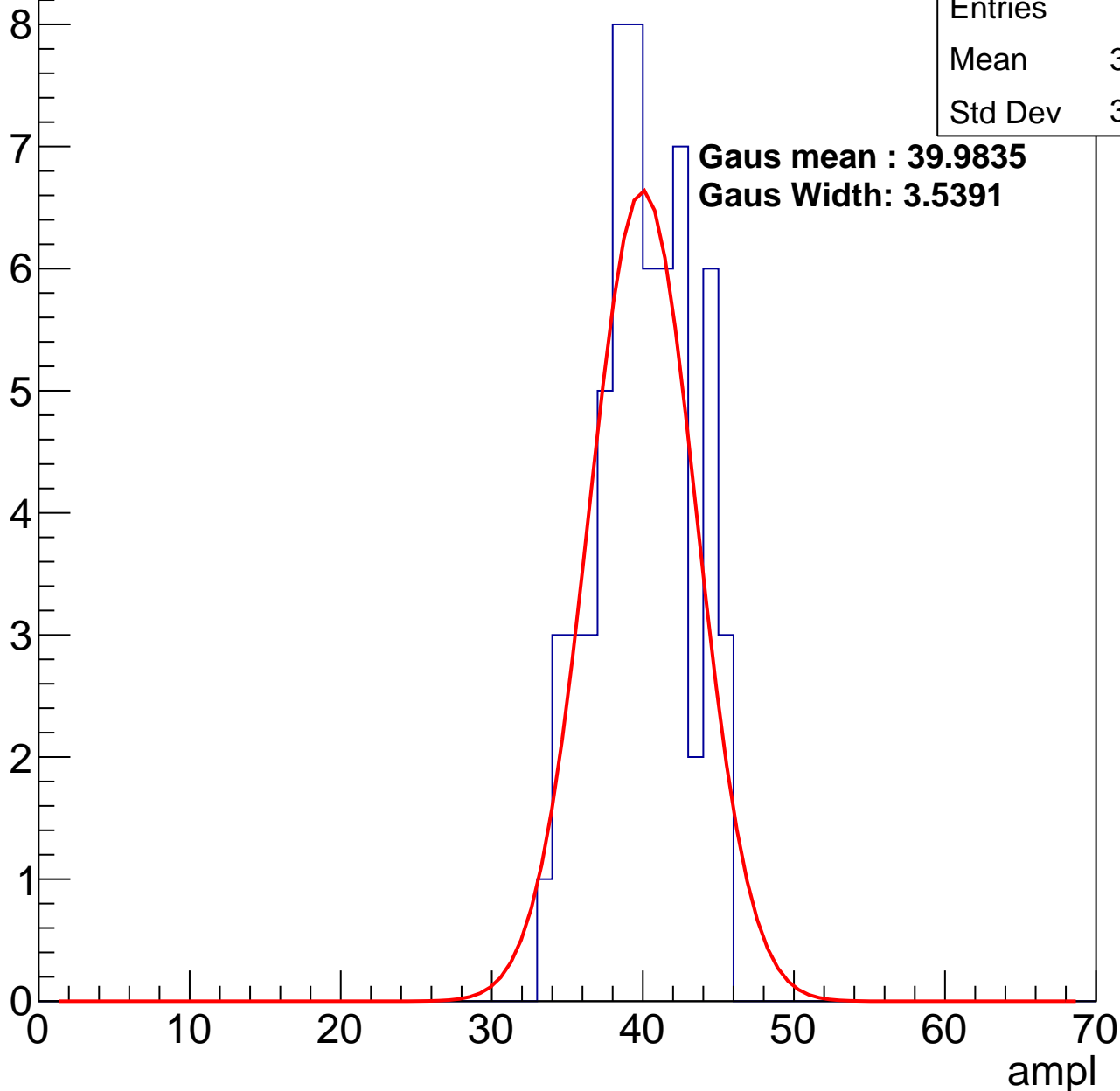
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	39.57
Std Dev	3.075

Gaus mean : 39.9835

Gaus Width: 3.5391



B1L100S, U11-ch77, adc2

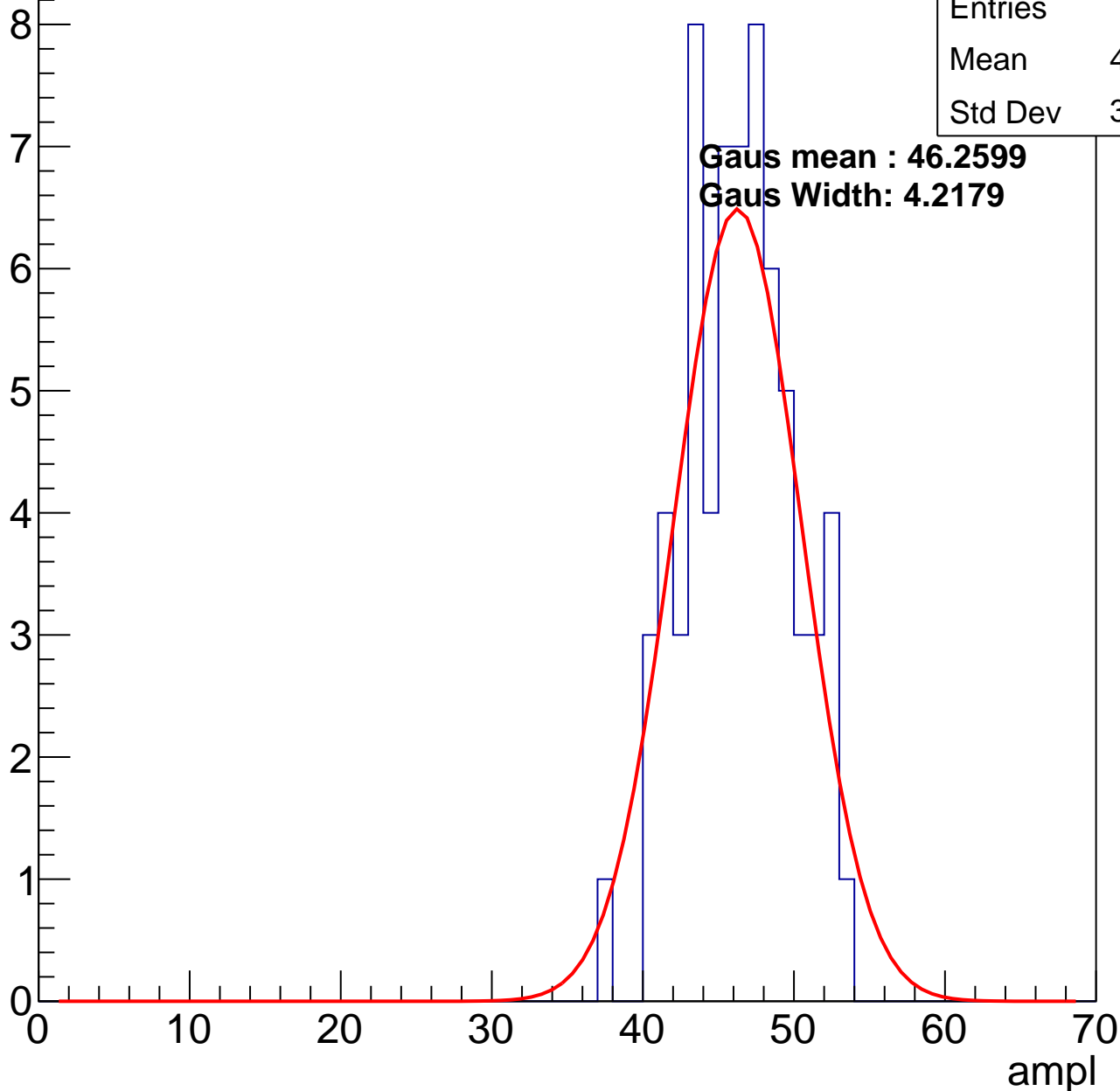
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	45.93
Std Dev	3.508

Gaus mean : 46.2599

Gaus Width: 4.2179

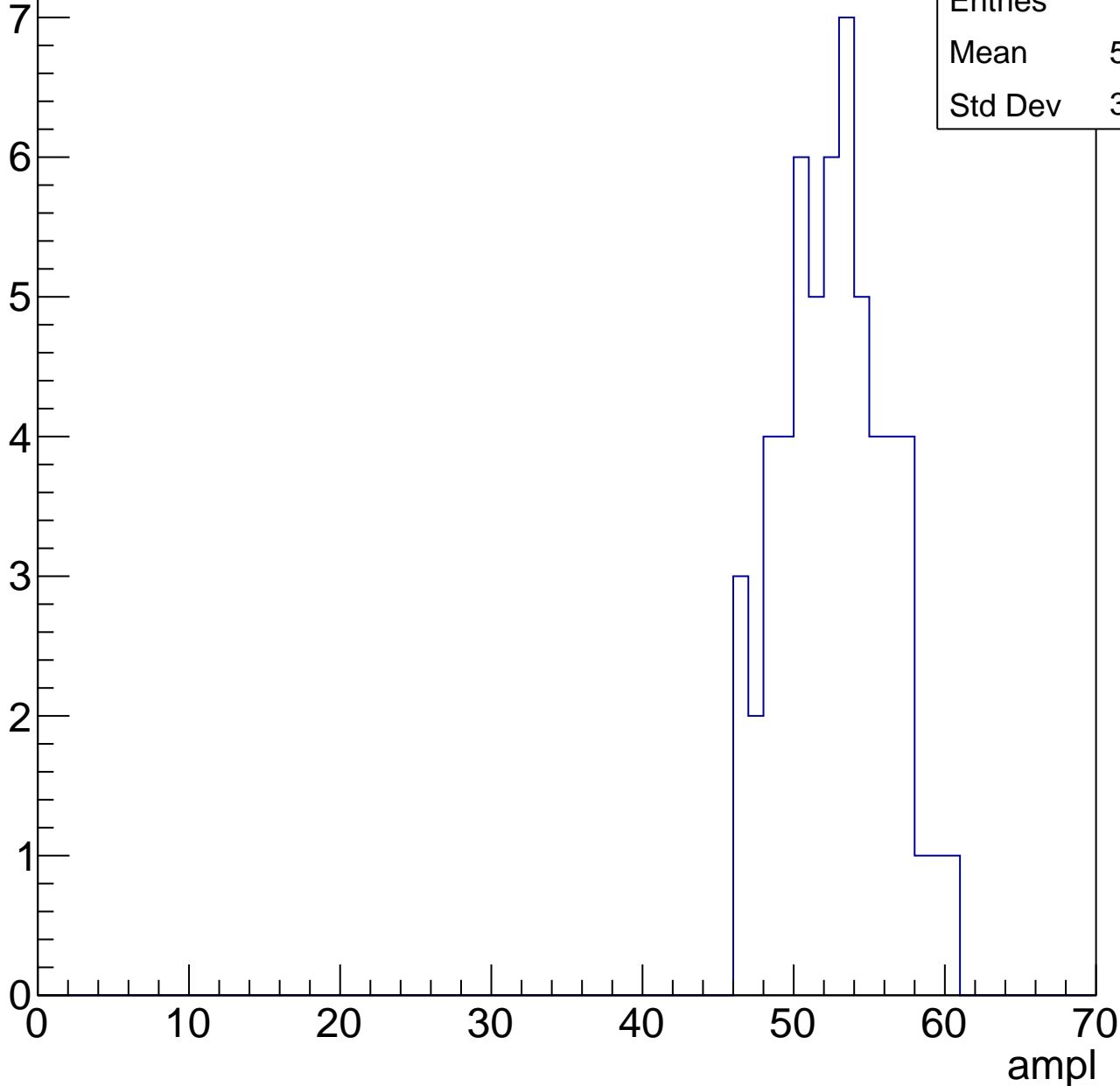


B1L100S, U11-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

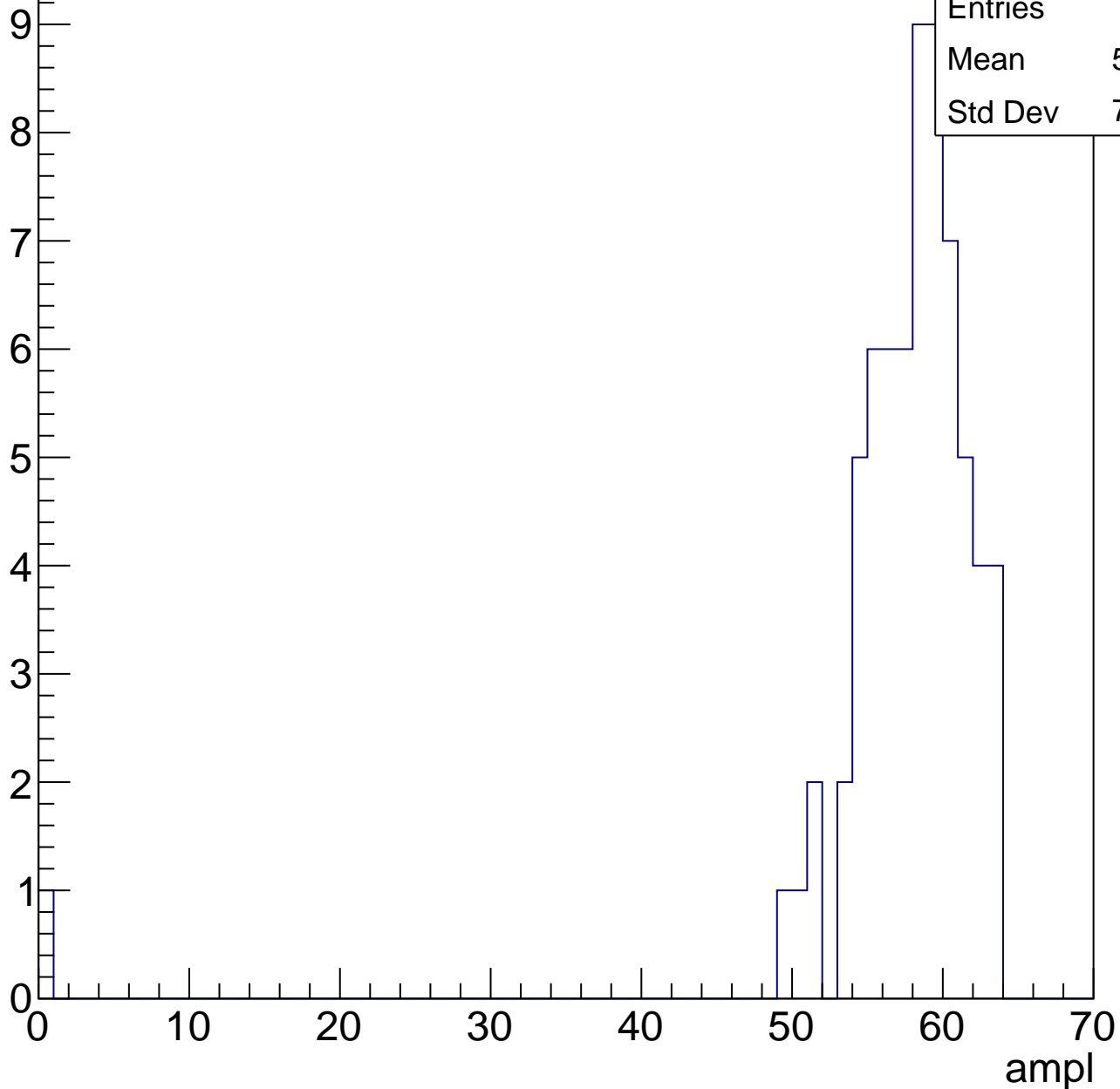
Entries	57
Mean	52.23
Std Dev	3.403



B1L100S, U11-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch77, adc5

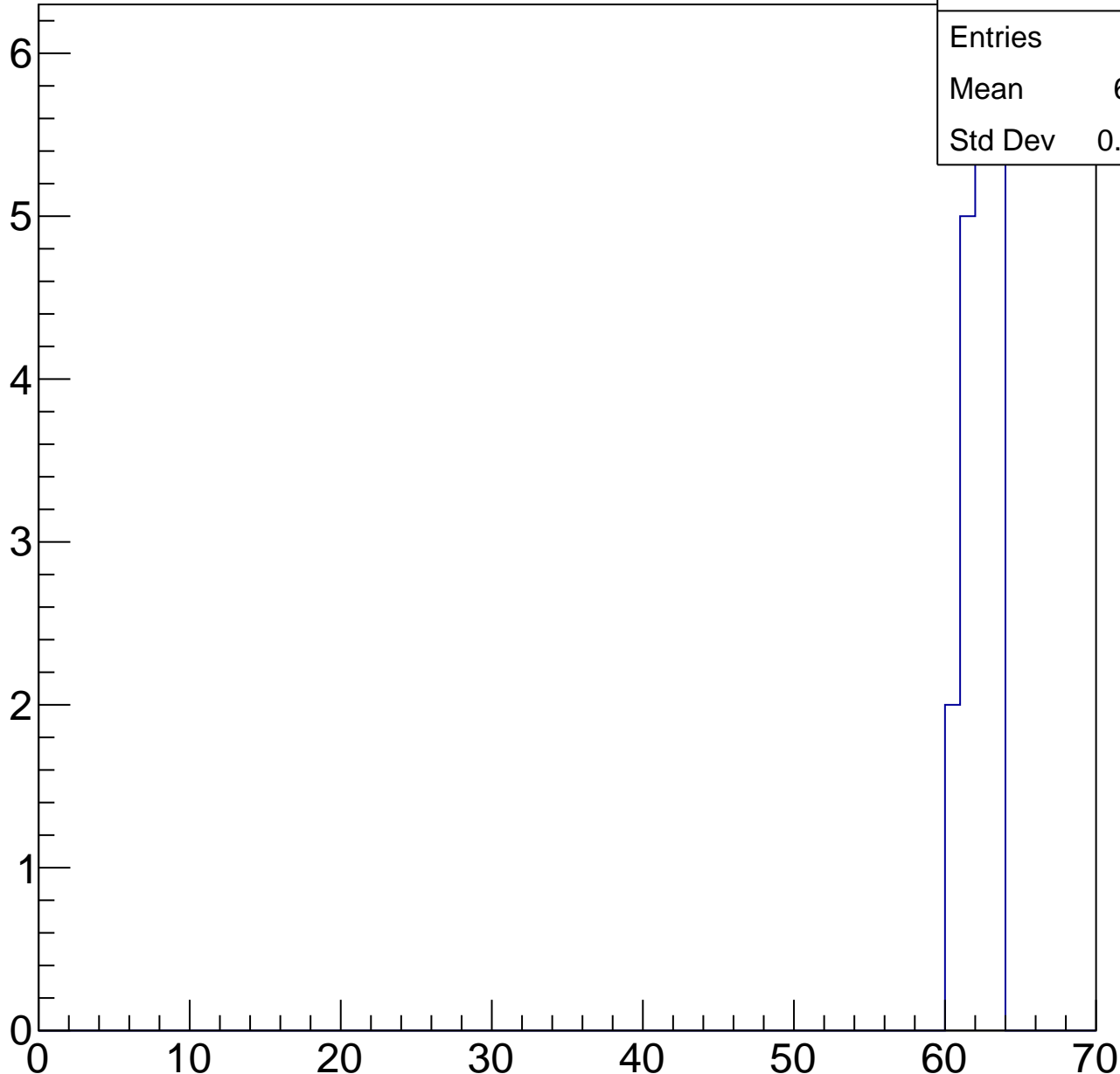
calib_packv5_042523_0143.root, FC#4, port A2

Entry

6
5
4
3
2
1
0

Entries	19
Mean	61.84
Std Dev	0.9875

ampl



B1L100S, U11-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L100S, U11-ch78, adc0

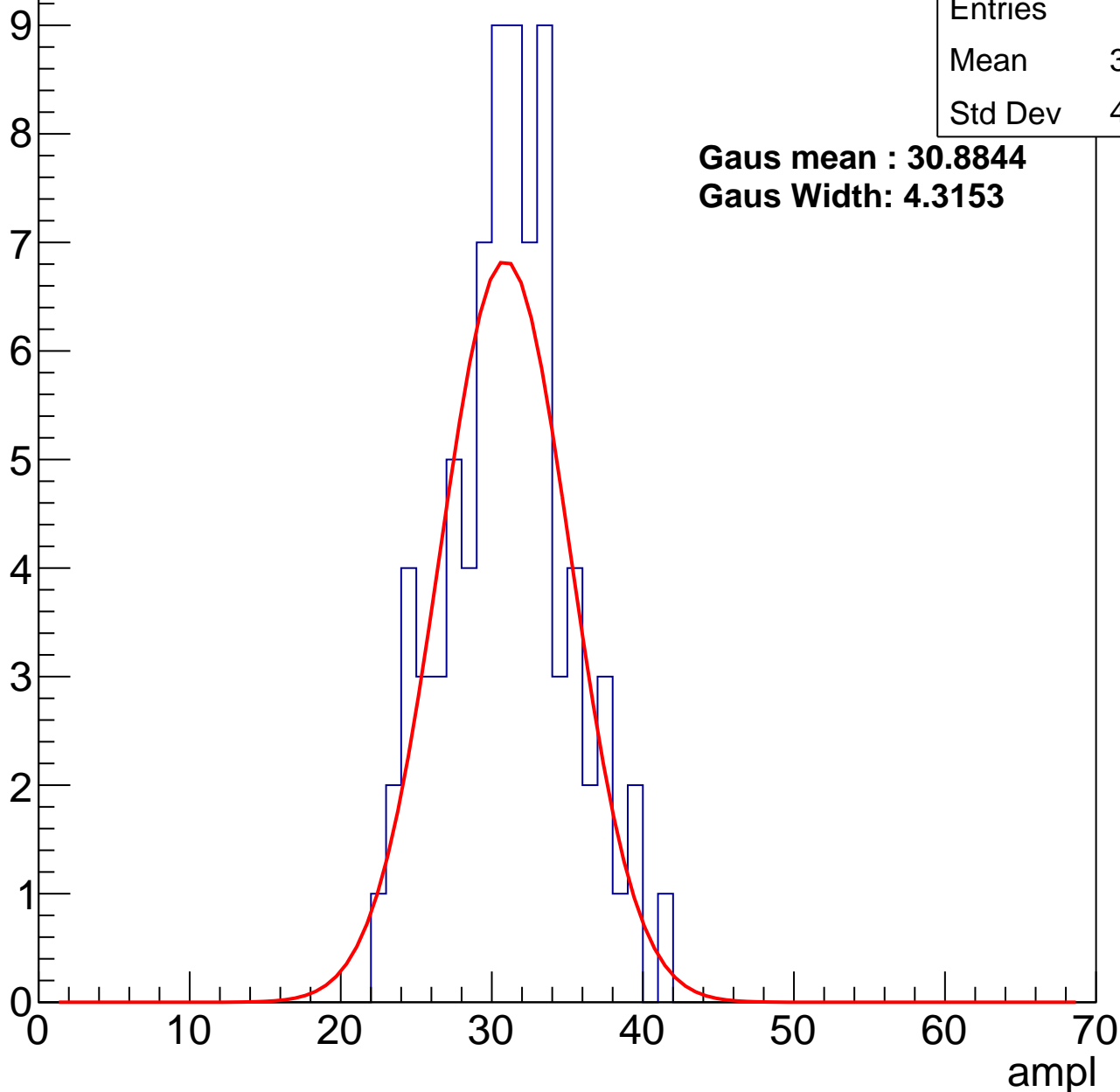
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	30.62
Std Dev	4.054

Gaus mean : 30.8844

Gaus Width: 4.3153



B1L100S, U11-ch78, adc1

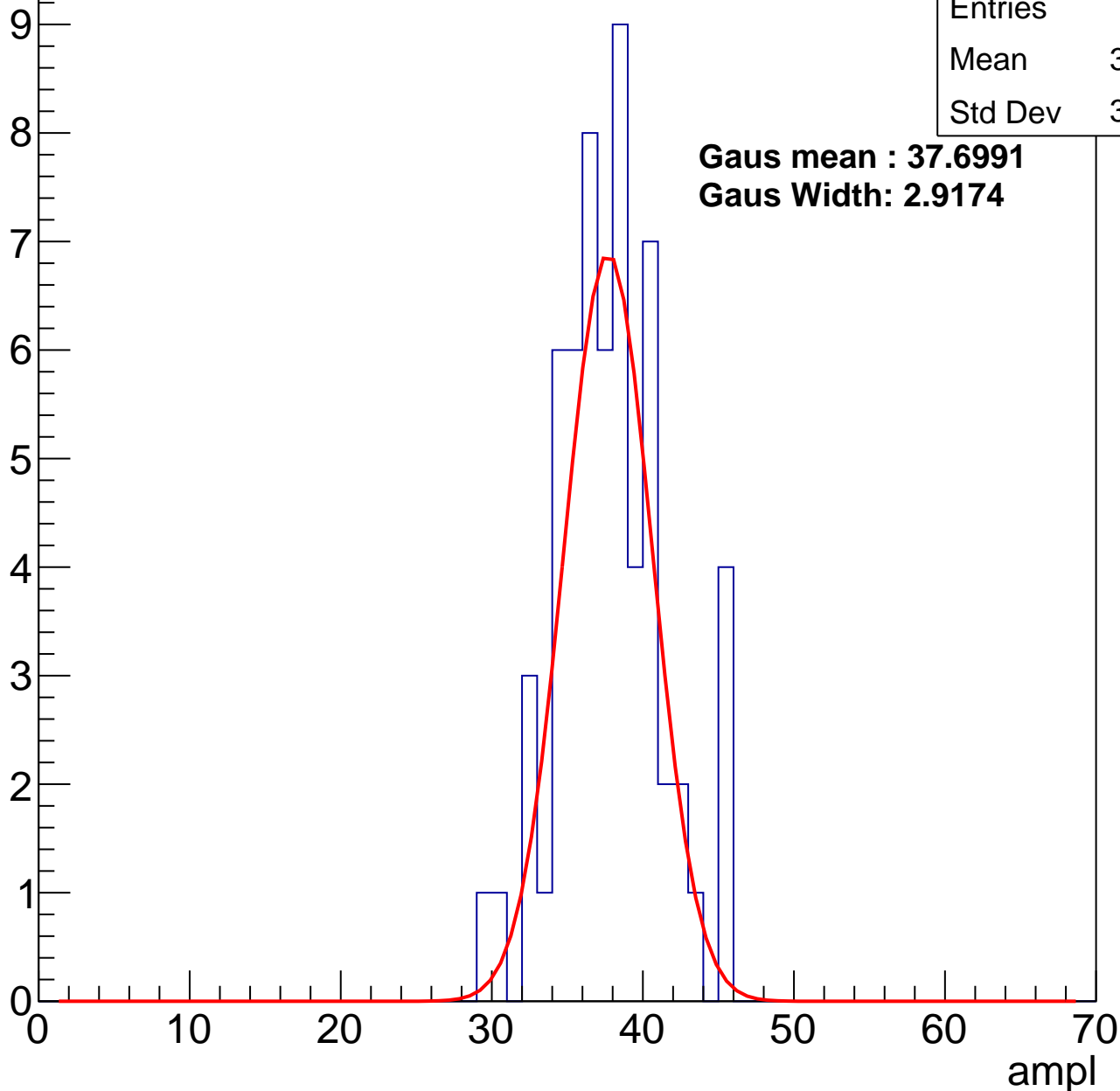
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	37.36
Std Dev	3.493

Gaus mean : 37.6991

Gaus Width: 2.9174



B1L100S, U11-ch78, adc2

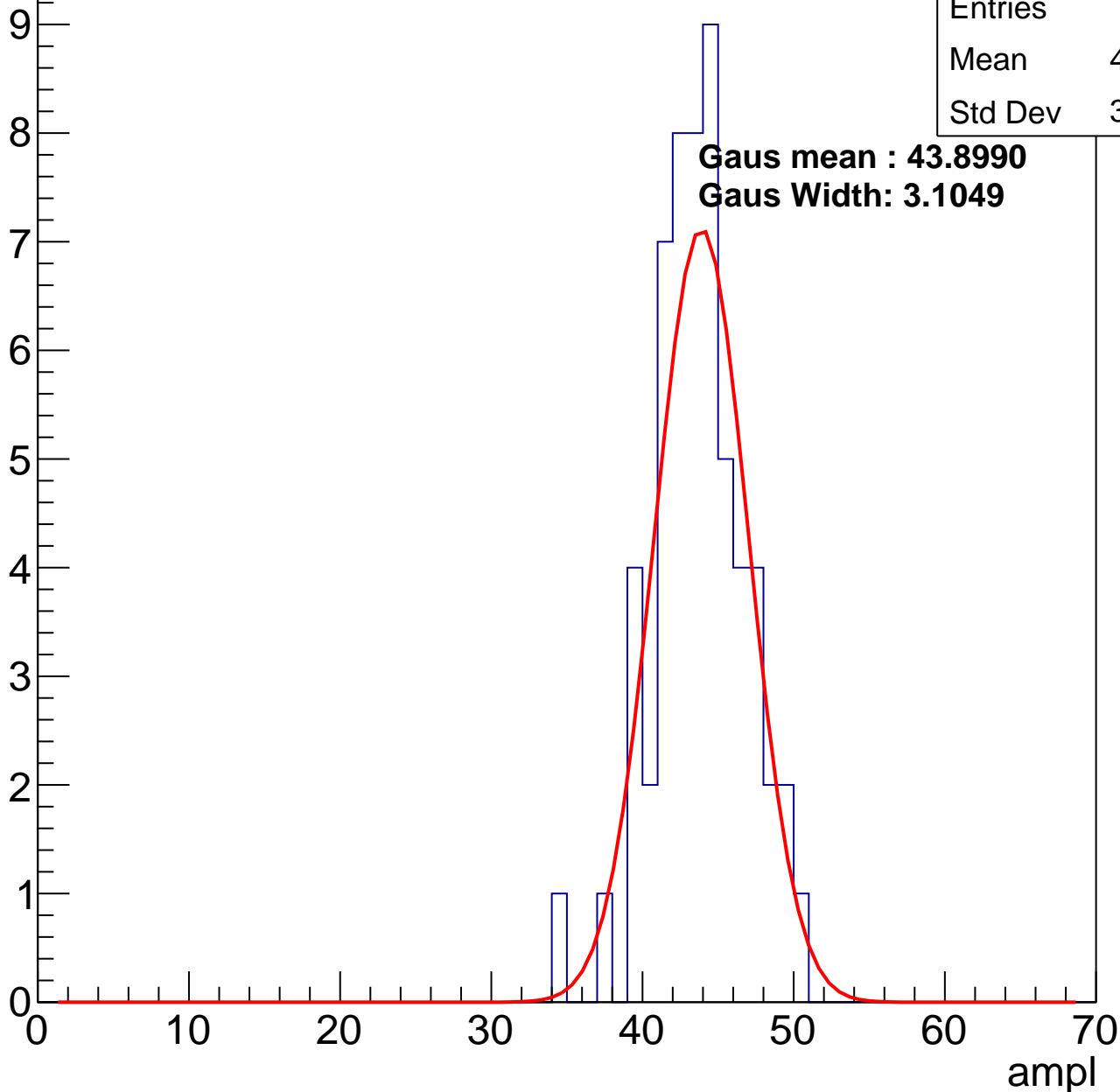
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.29
Std Dev	3.023

Gaus mean : 43.8990

Gaus Width: 3.1049

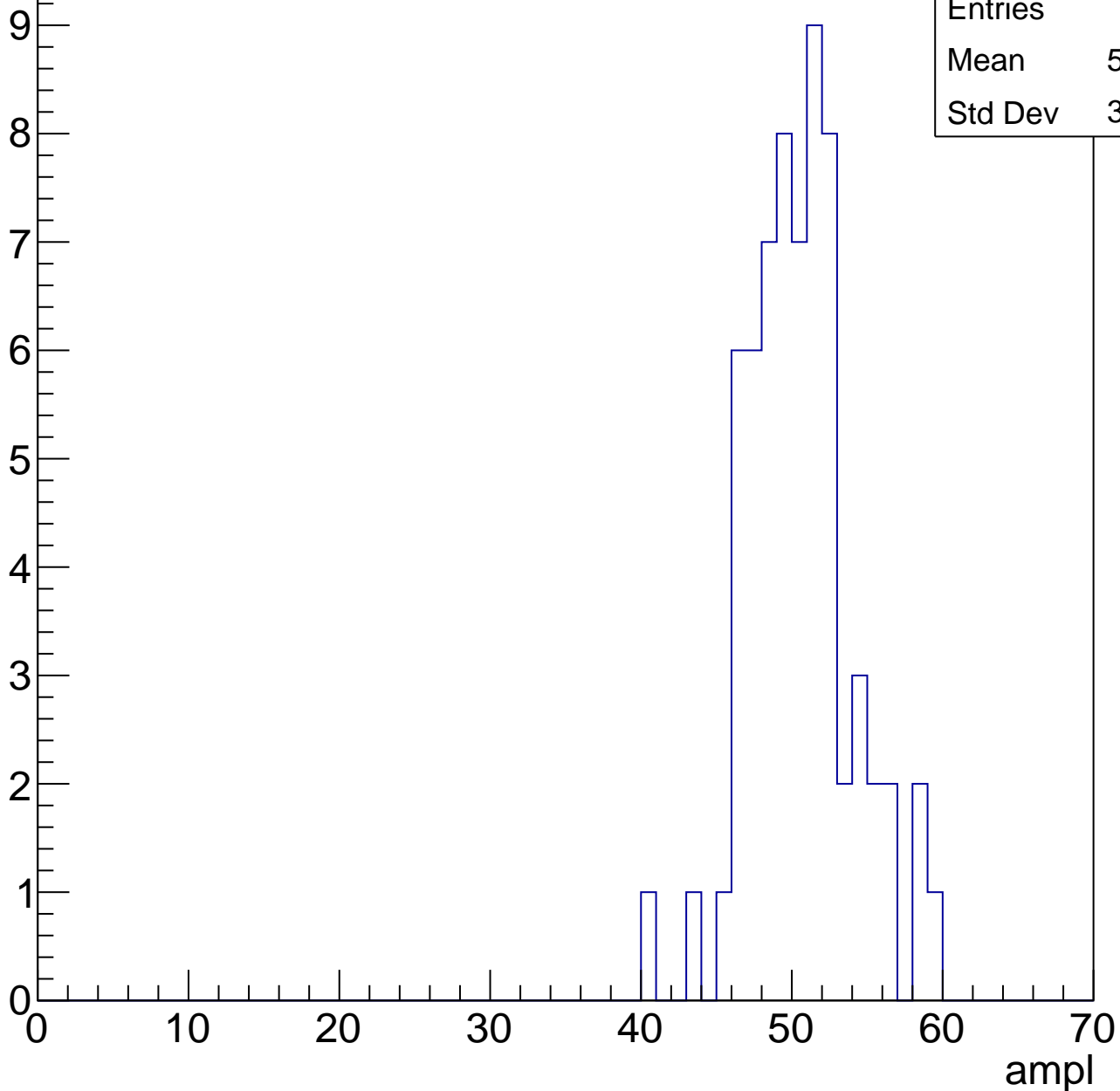


B1L100S, U11-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	50.06
Std Dev	3.477

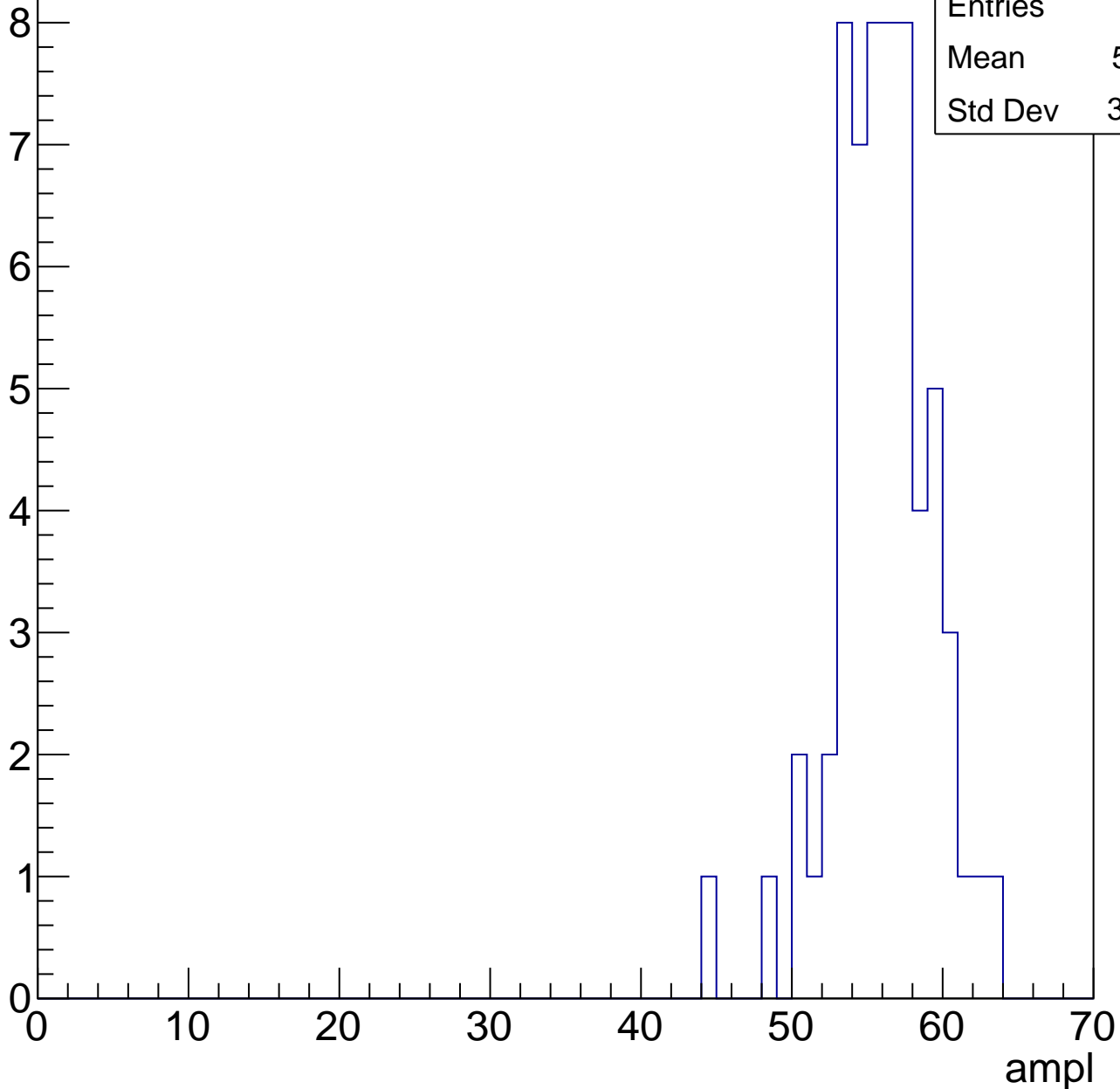


B1L100S, U11-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

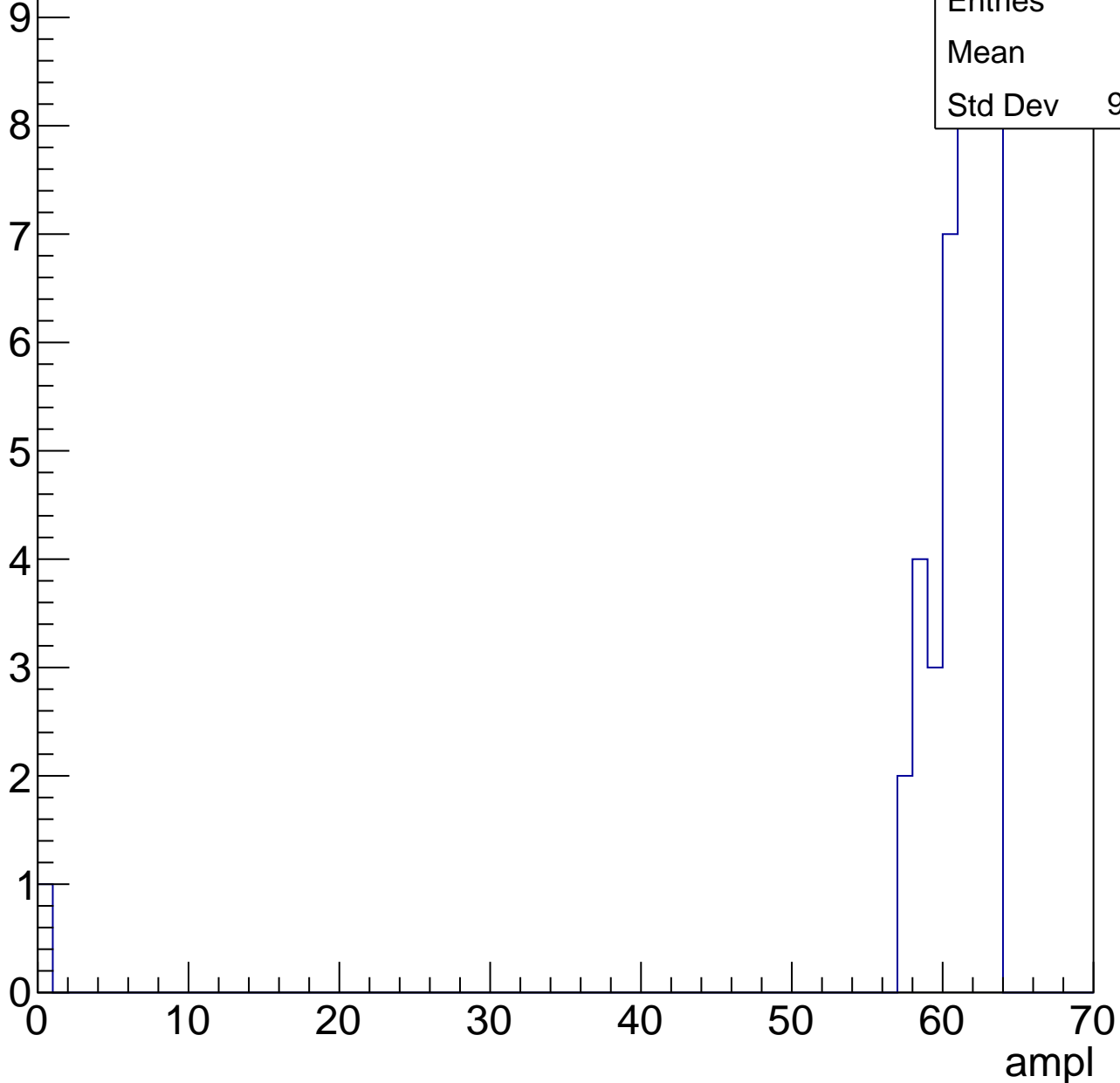
Entries	61
Mean	55.51
Std Dev	3.282



B1L100S, U11-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

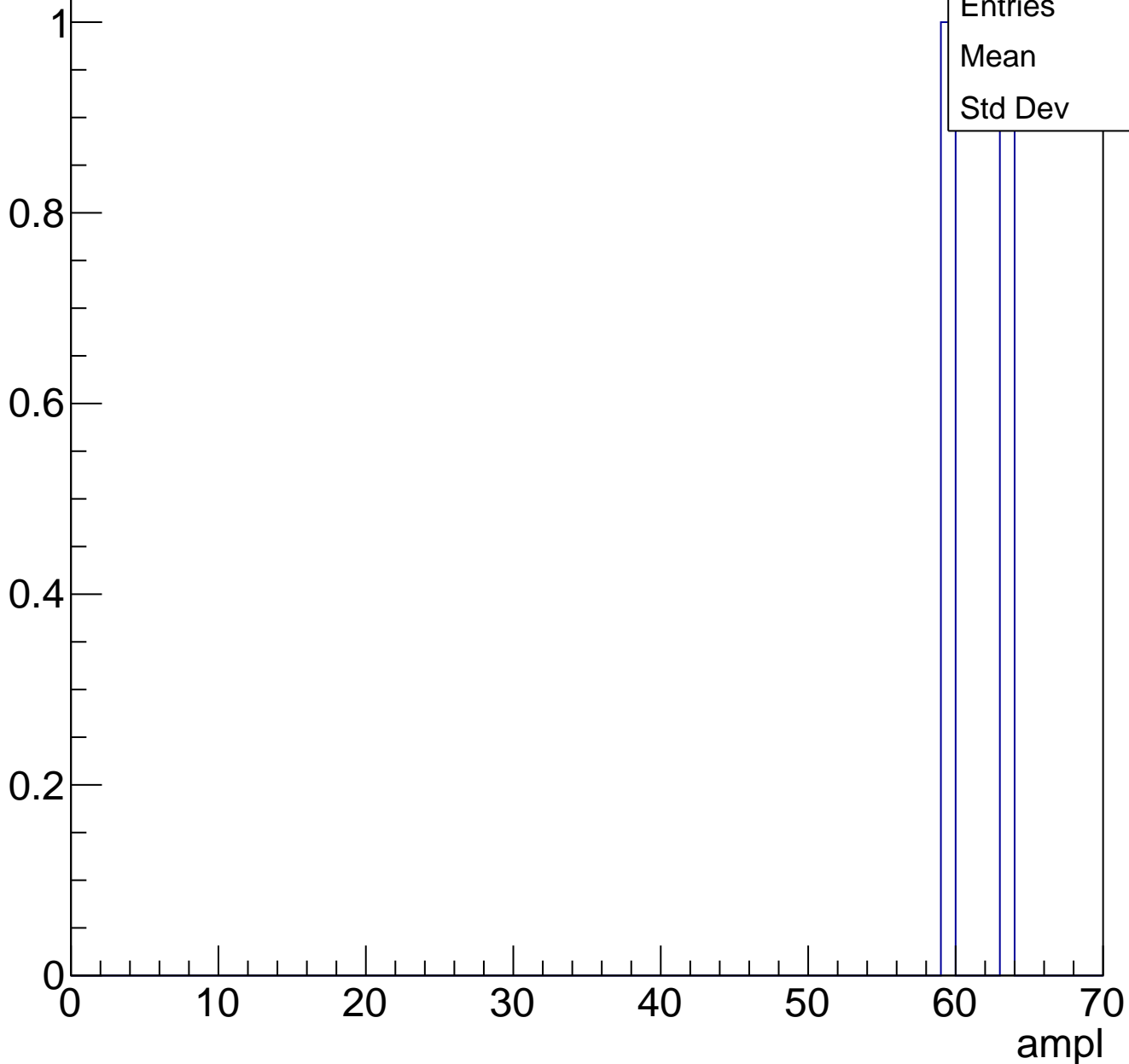
Entry



B1L100S, U11-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch79, adc0

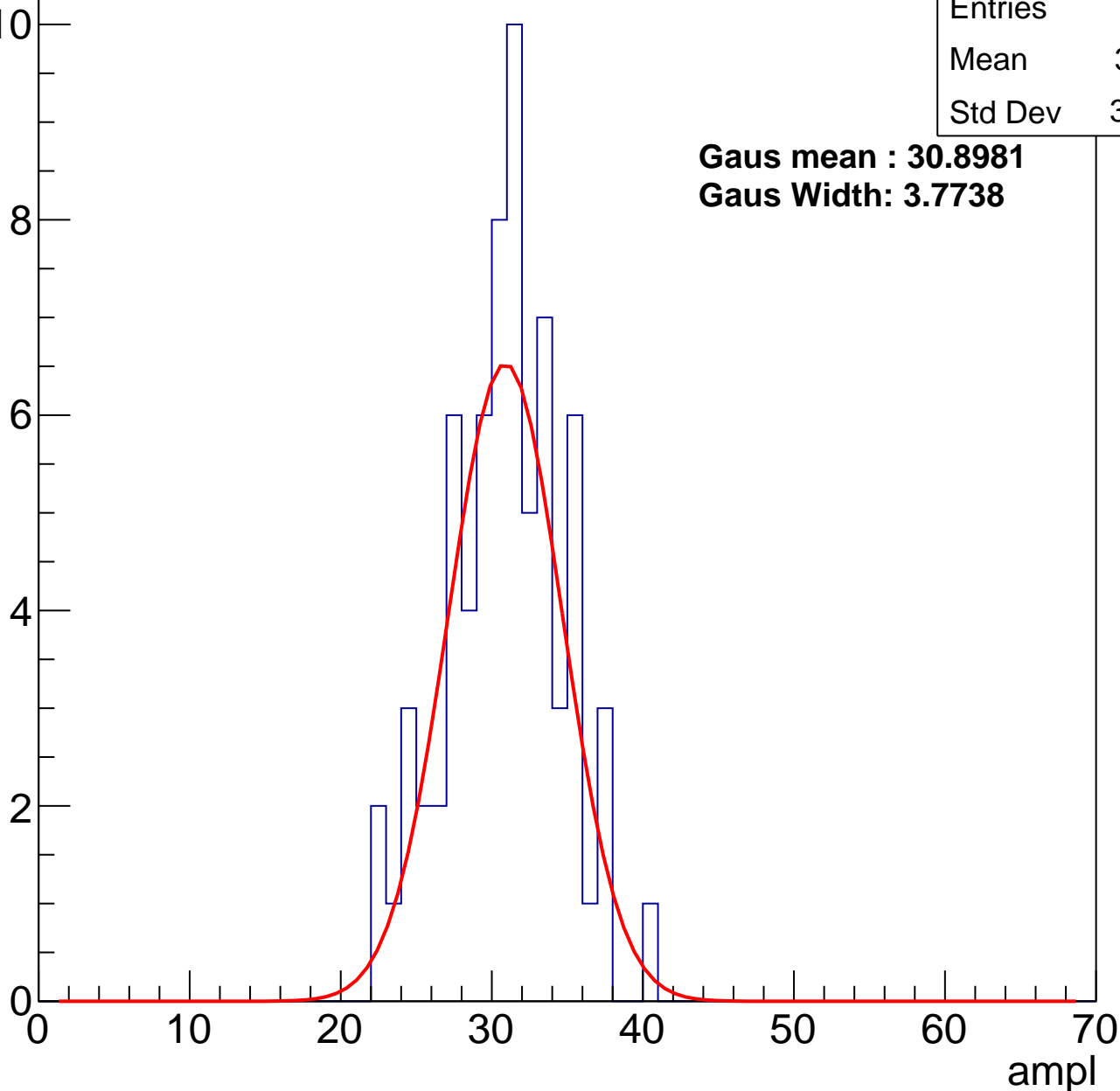
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.41
Std Dev	3.789

Gaus mean : 30.8981

Gaus Width: 3.7738



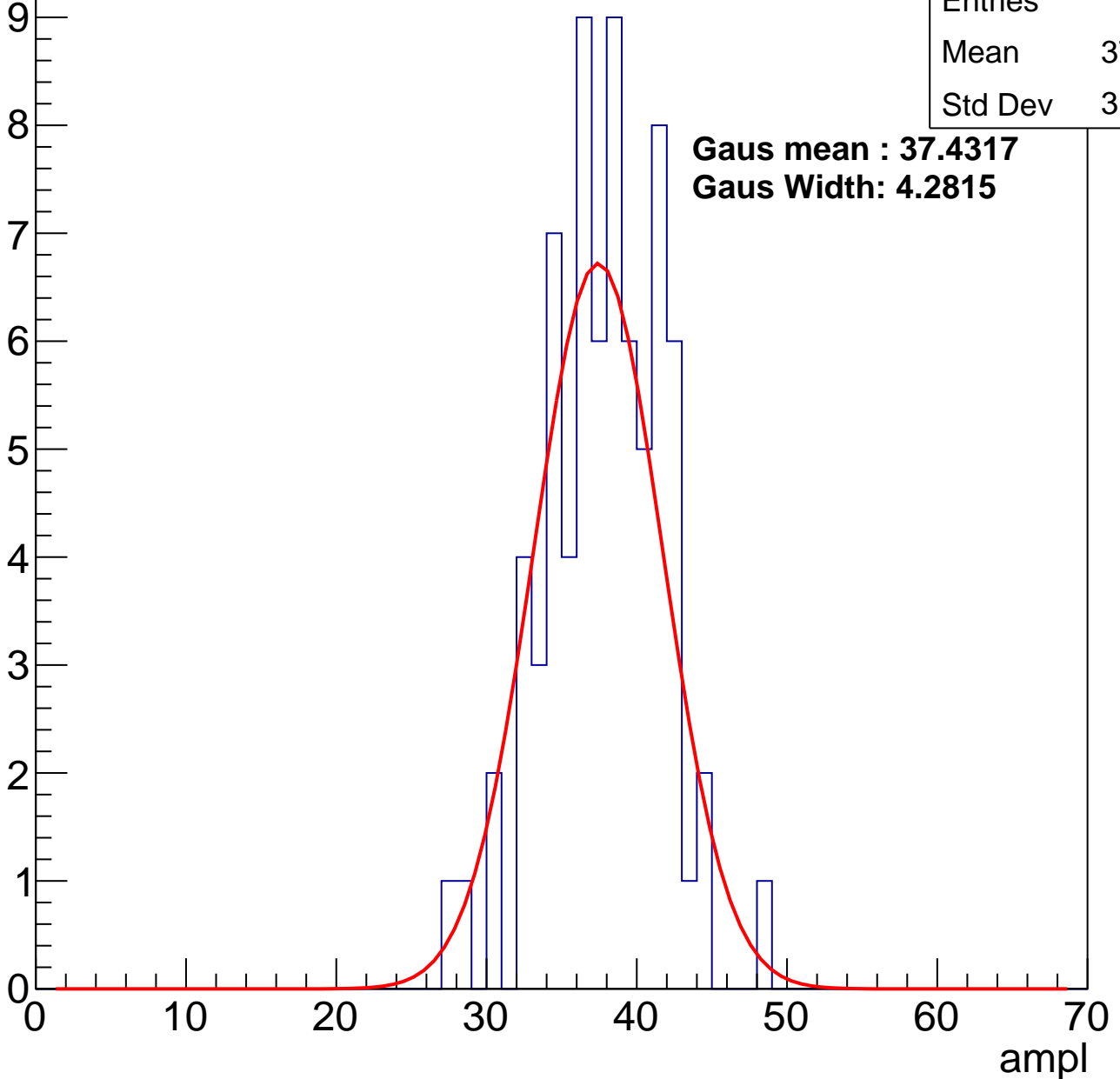
B1L100S, U11-ch79, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.35
Std Dev	3.859

Gaus mean : 37.4317
Gaus Width: 4.2815



B1L100S, U11-ch79, adc2

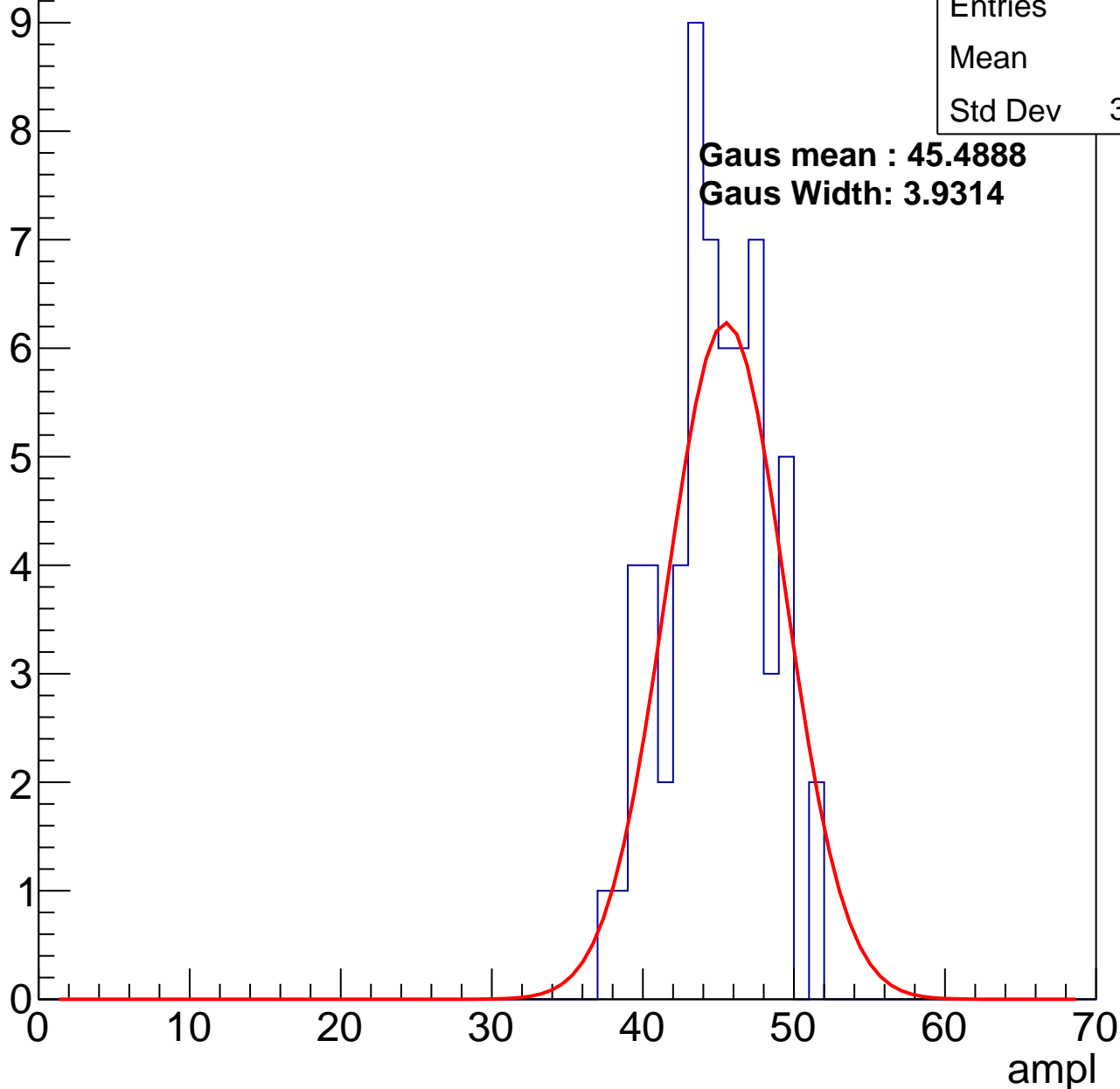
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.3
Std Dev	3.266

Gaus mean : 45.4888

Gaus Width: 3.9314



B1L100S, U11-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

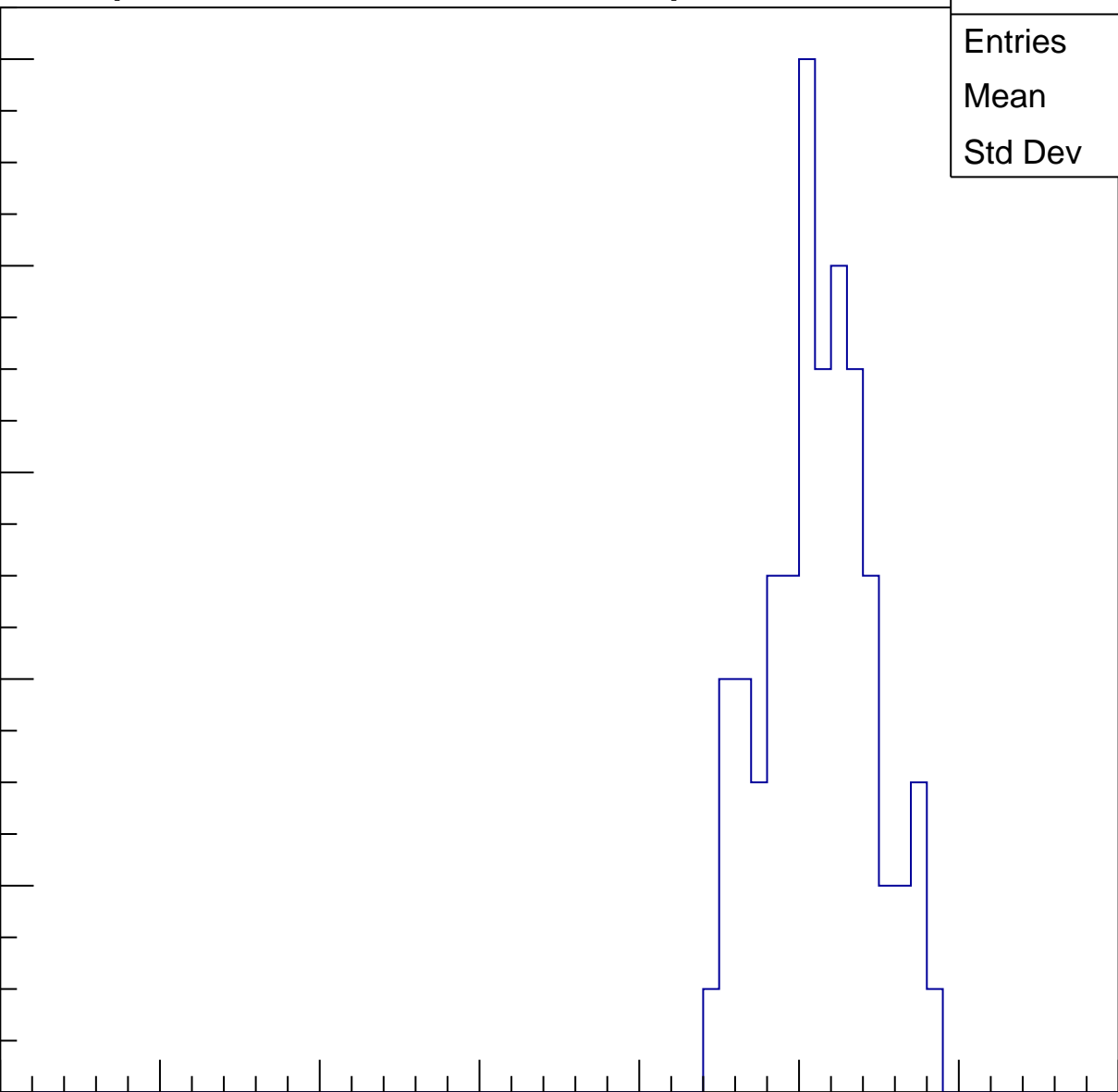
Entries	67
Mean	50.73
Std Dev	3.303

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

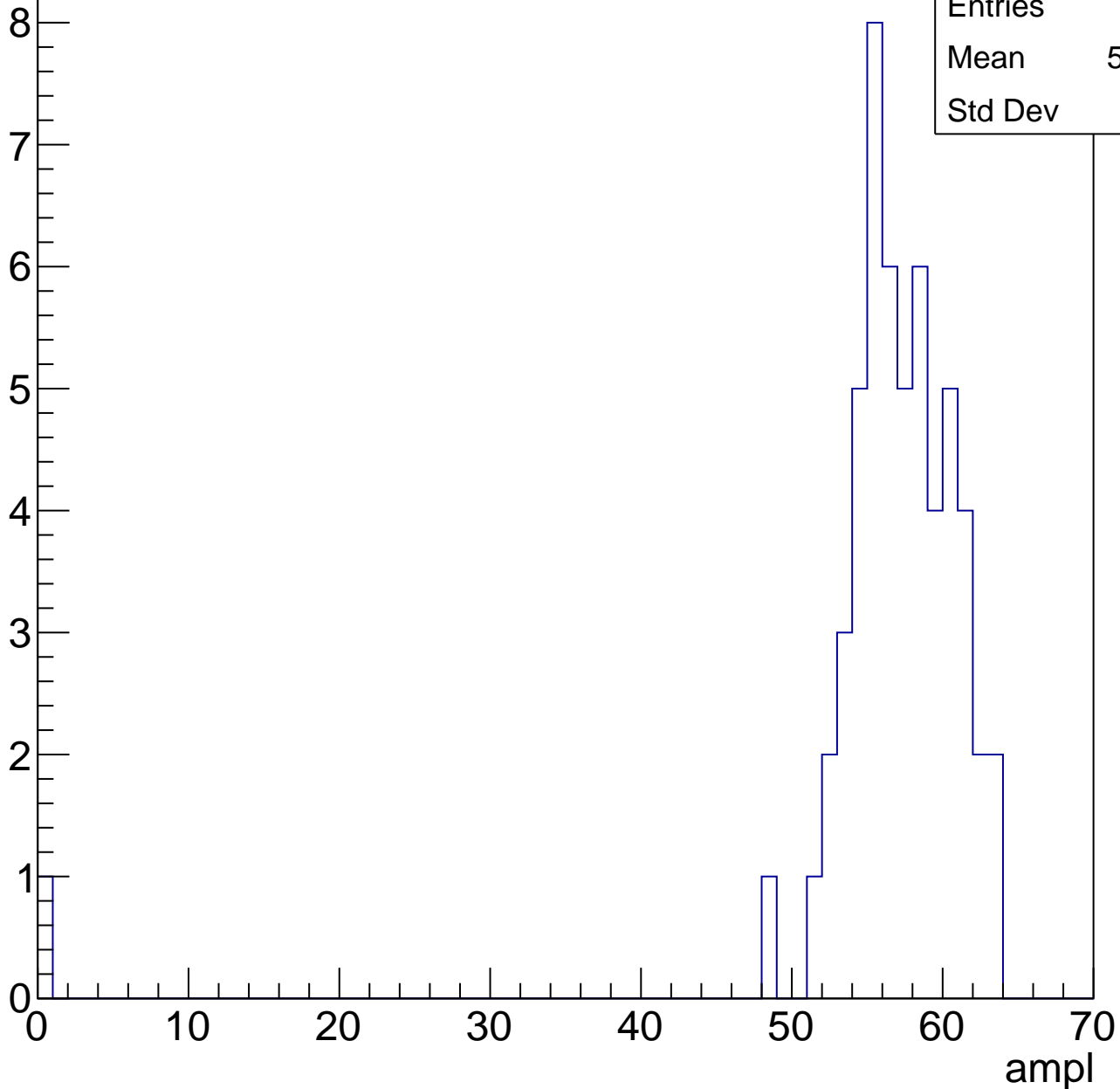


B1L100S, U11-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	55.84
Std Dev	8.23

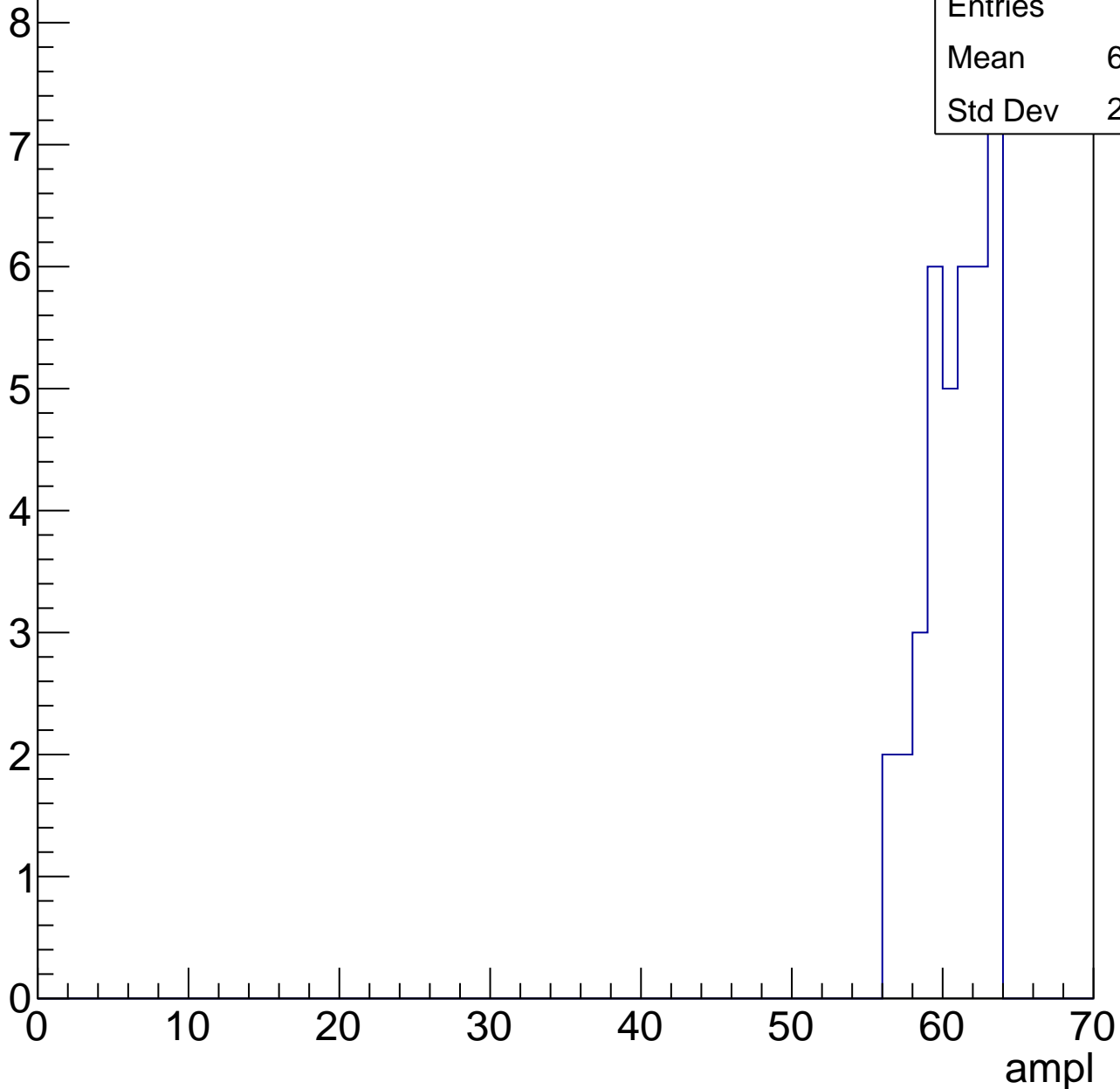


B1L100S, U11-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	60.42
Std Dev	2.073



B1L100S, U11-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L100S, U11-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch80, adc0

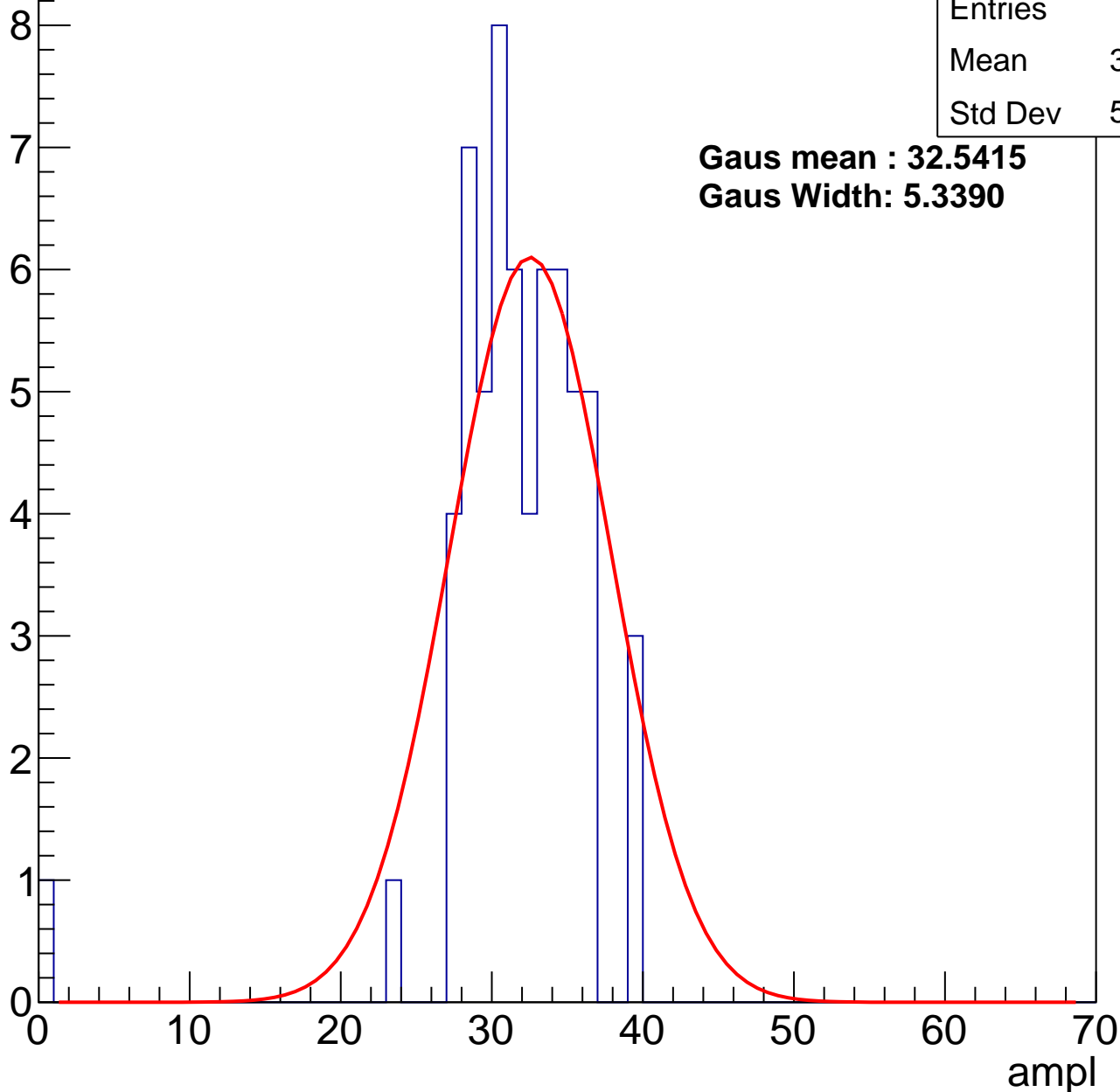
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	31.15
Std Dev	5.213

Gaus mean : 32.5415

Gaus Width: 5.3390



B1L100S, U11-ch80, adc1

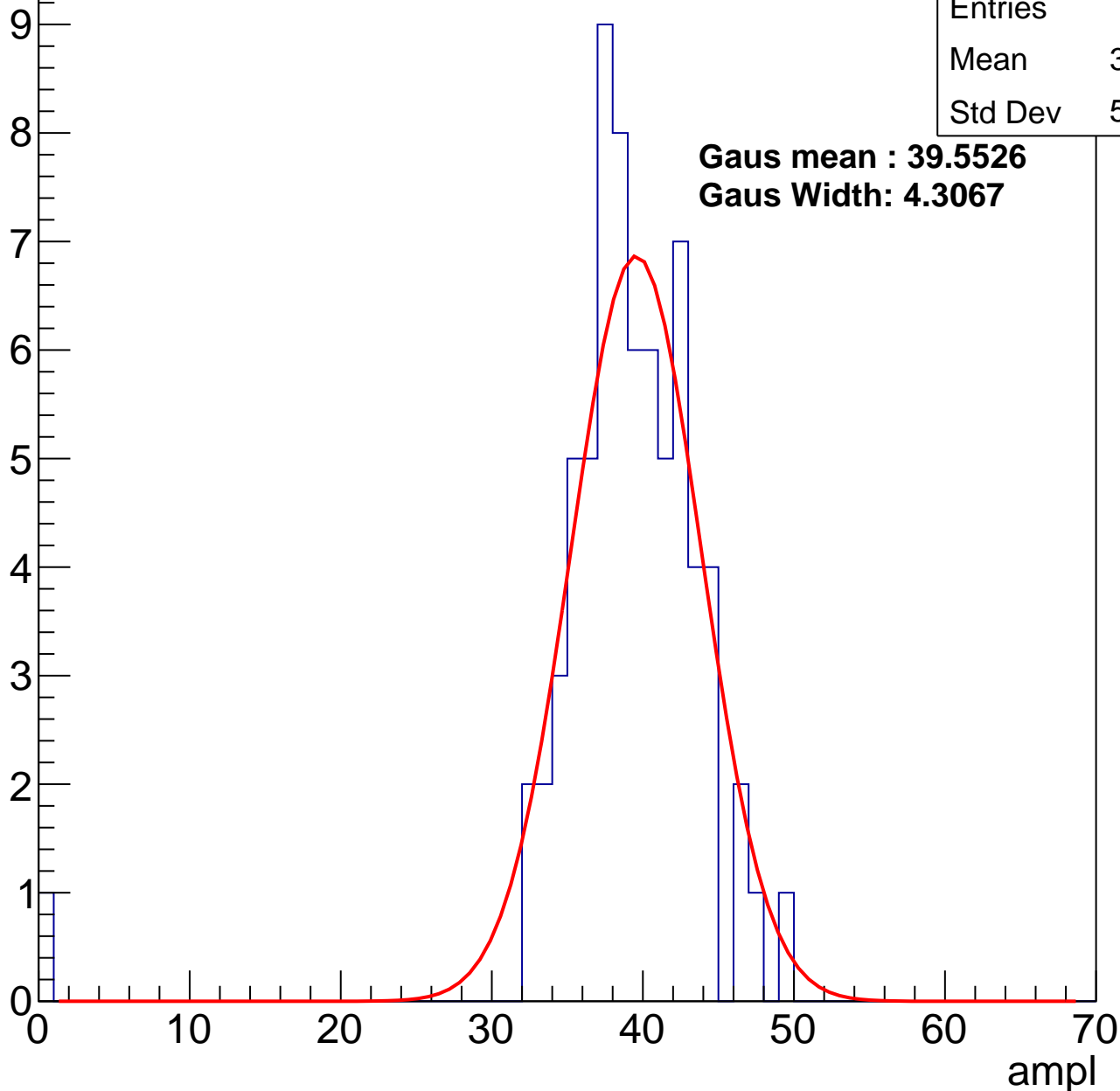
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	38.49
Std Dev	5.852

Gaus mean : 39.5526

Gaus Width: 4.3067



B1L100S, U11-ch80, adc2

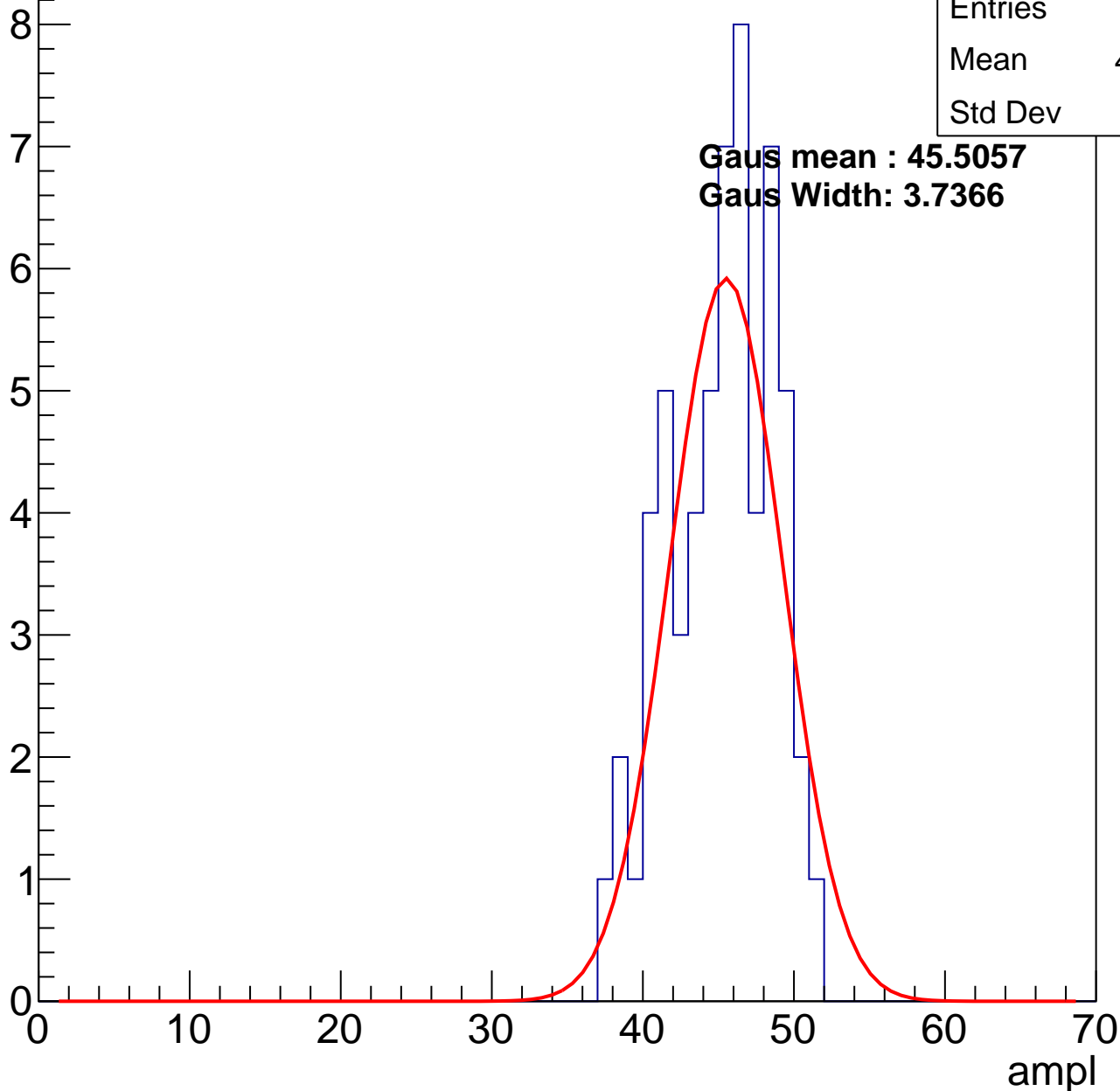
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.71
Std Dev	3.38

Gaus mean : 45.5057

Gaus Width: 3.7366



B1L100S, U11-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

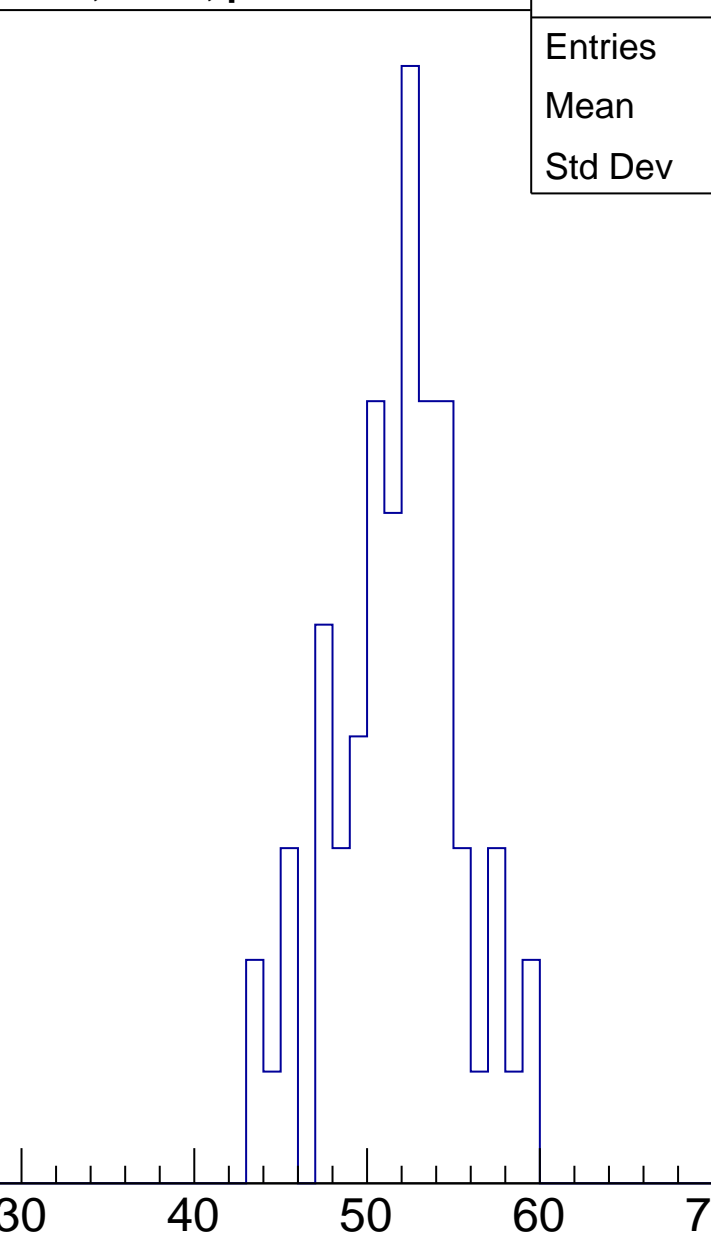
Entries	65
Mean	51.28
Std Dev	3.656

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

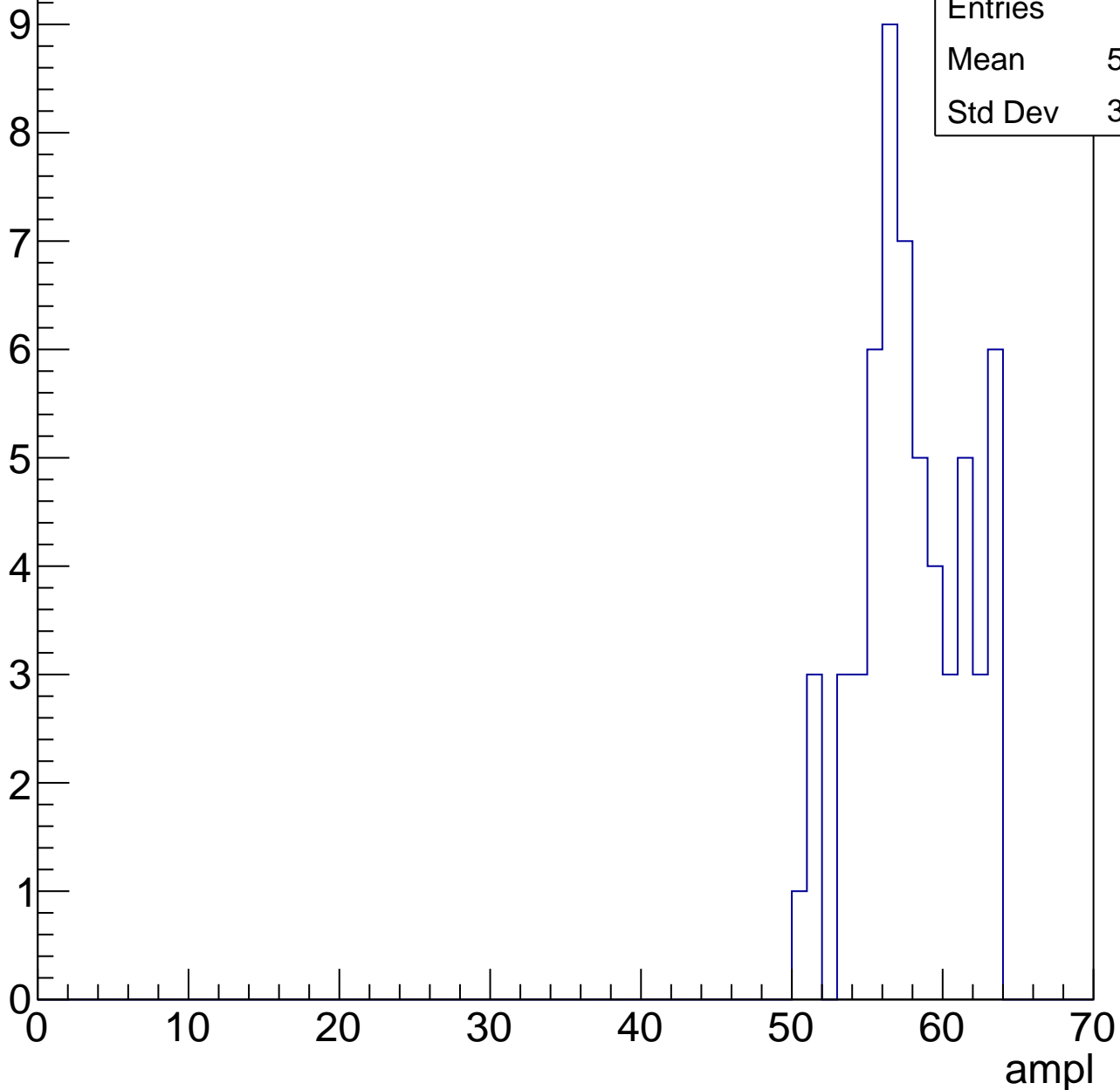


B1L100S, U11-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	57.45
Std Dev	3.405



B1L100S, U11-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 33

Mean 58.61

Std Dev 10.52

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

7

8

9

10

B1L100S, U11-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch81, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	30.15
Std Dev	3.394

Gaus mean : 31.0492

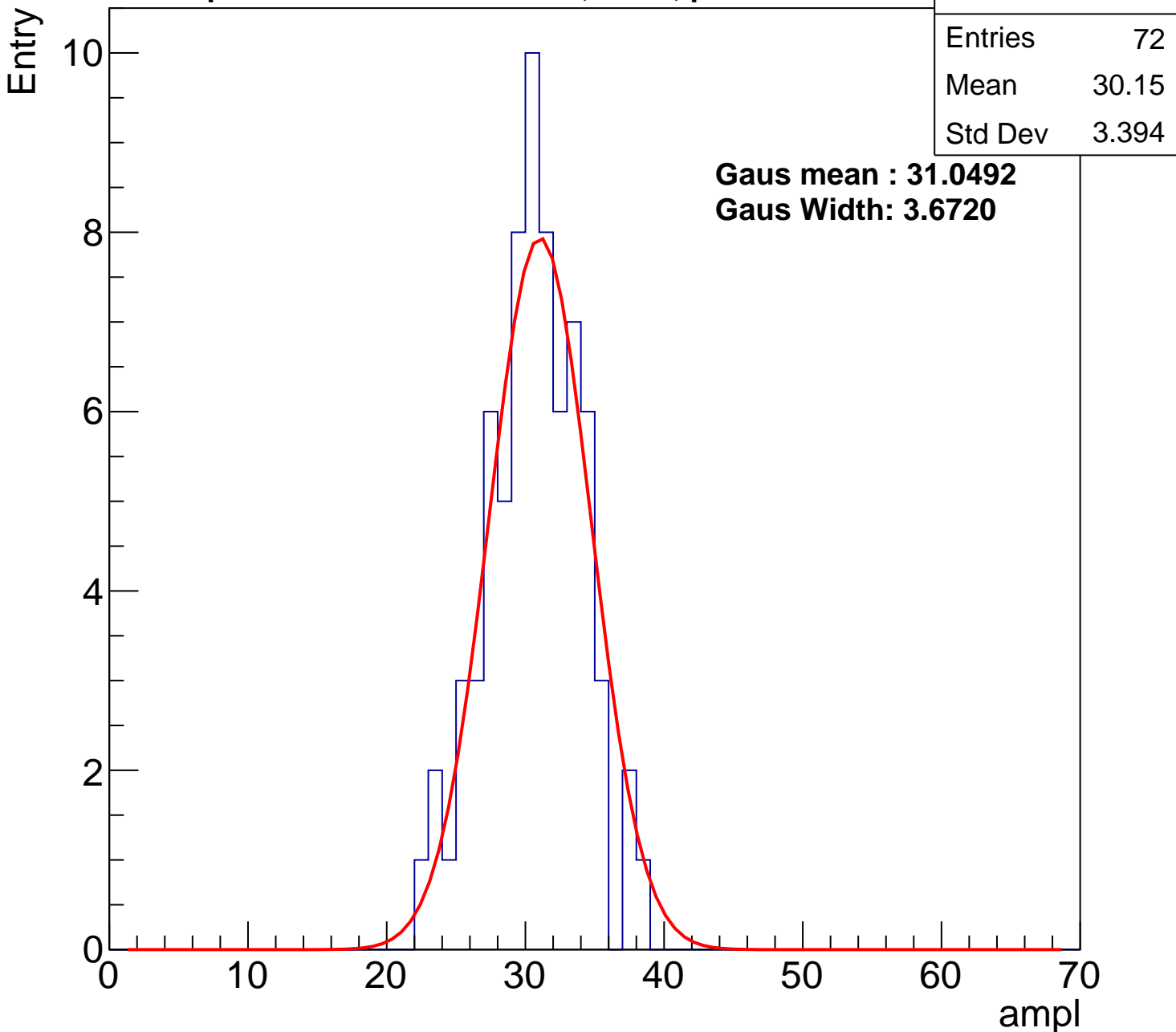
Gaus Width: 3.6720

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch81, adc1

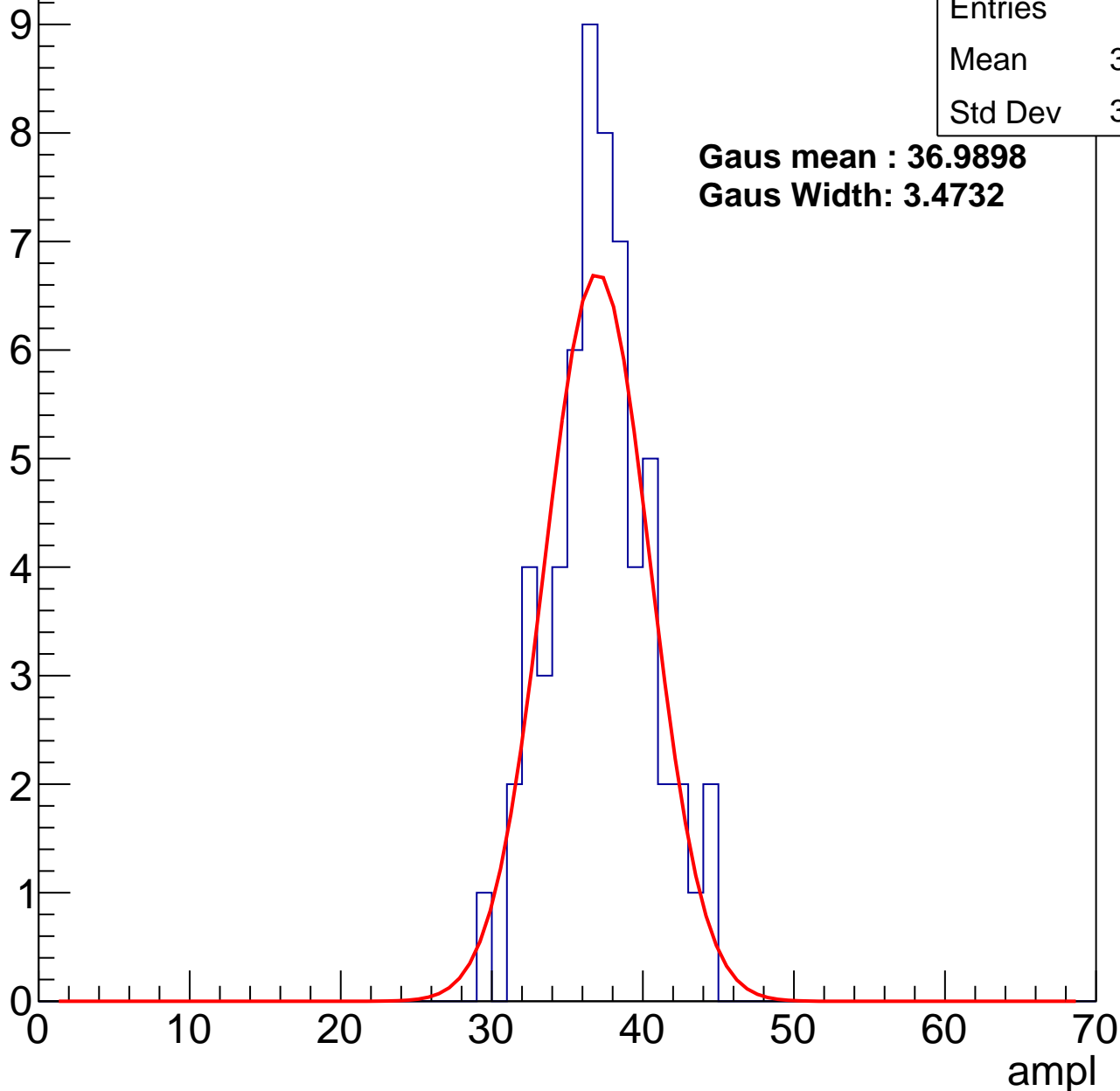
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	36.72
Std Dev	3.246

Gaus mean : 36.9898

Gaus Width: 3.4732



B1L100S, U11-ch81, adc2

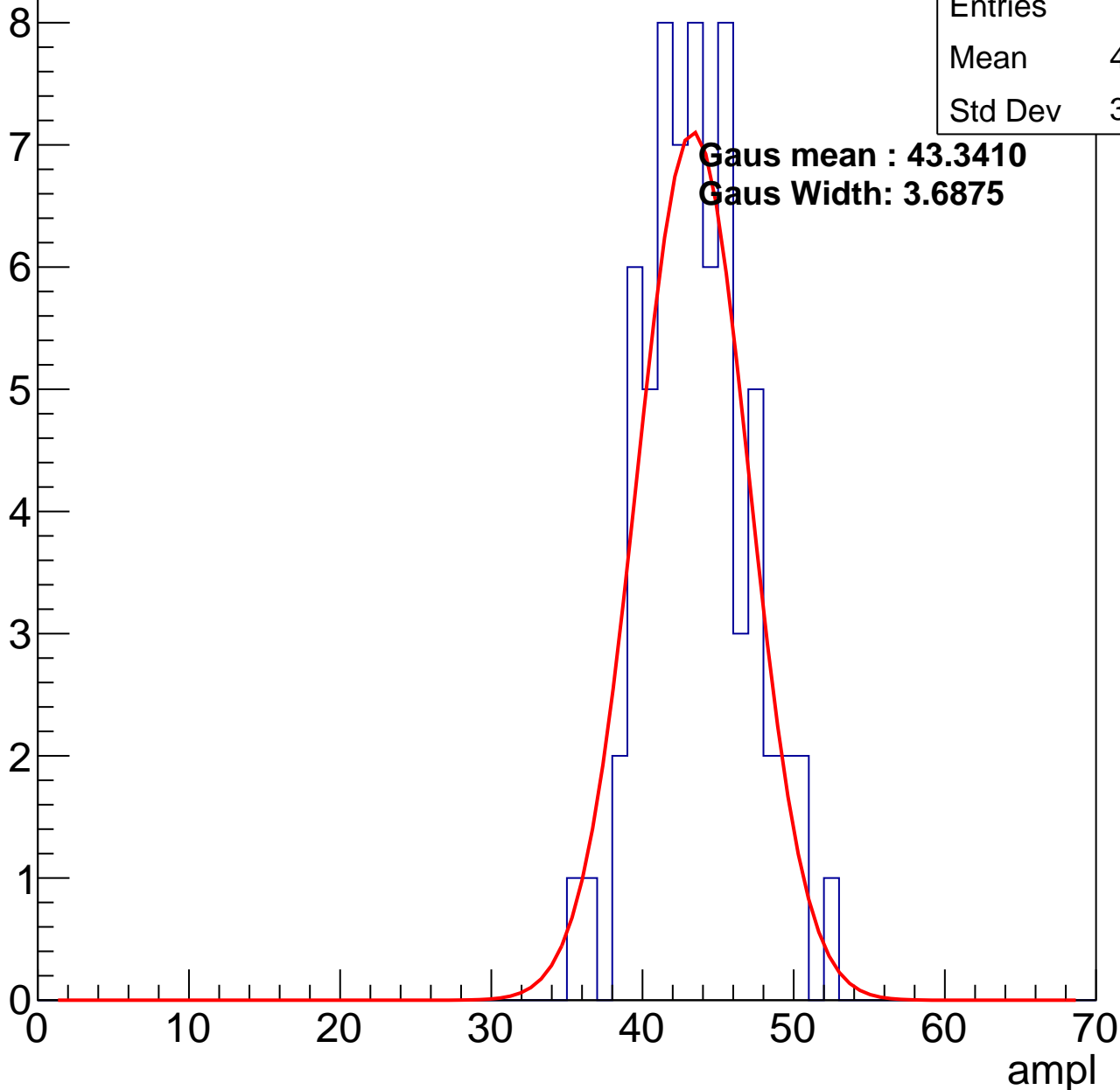
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.13
Std Dev	3.429

Gaus mean : 43.3410

Gaus Width: 3.6875

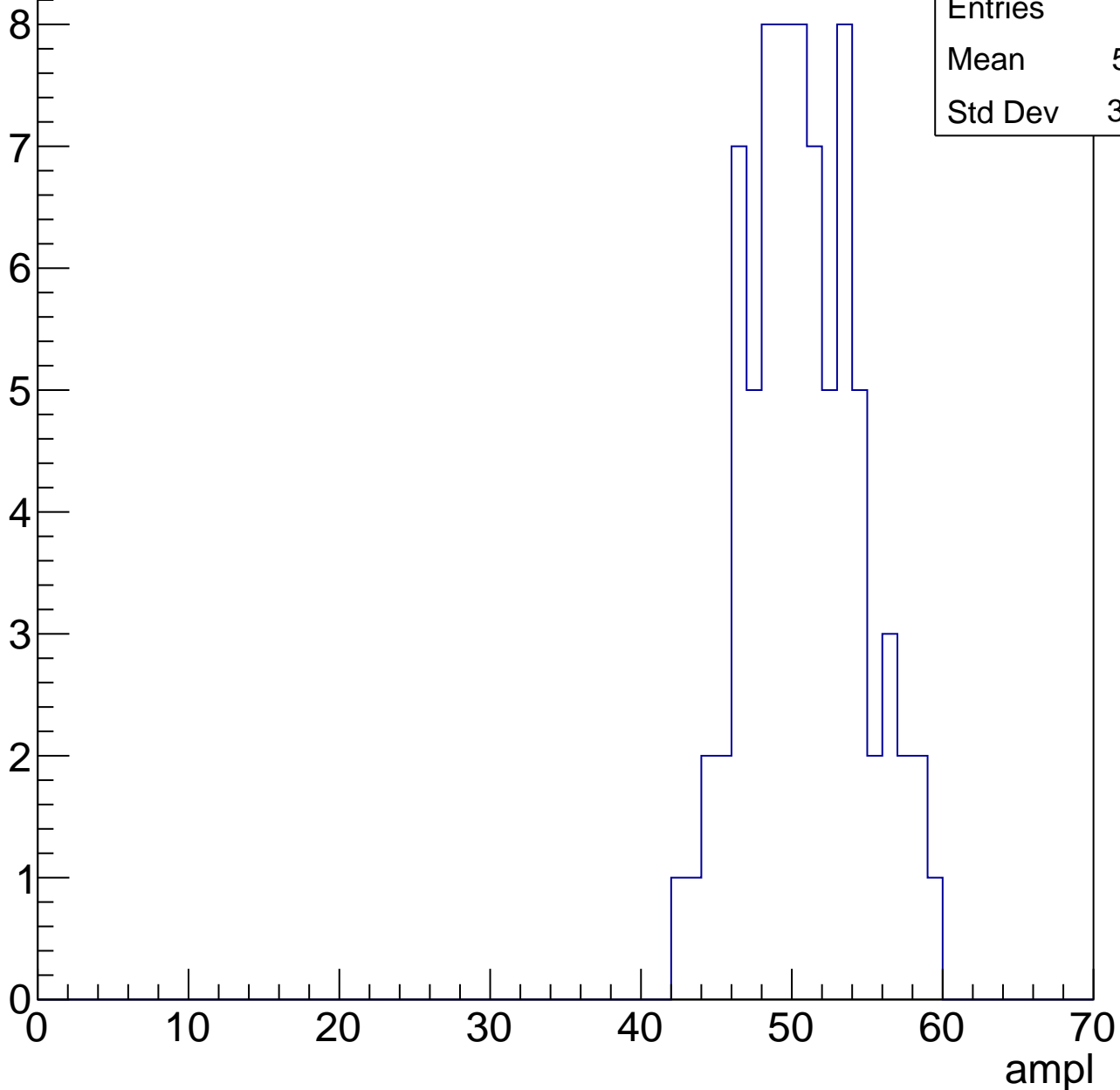


B1L100S, U11-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

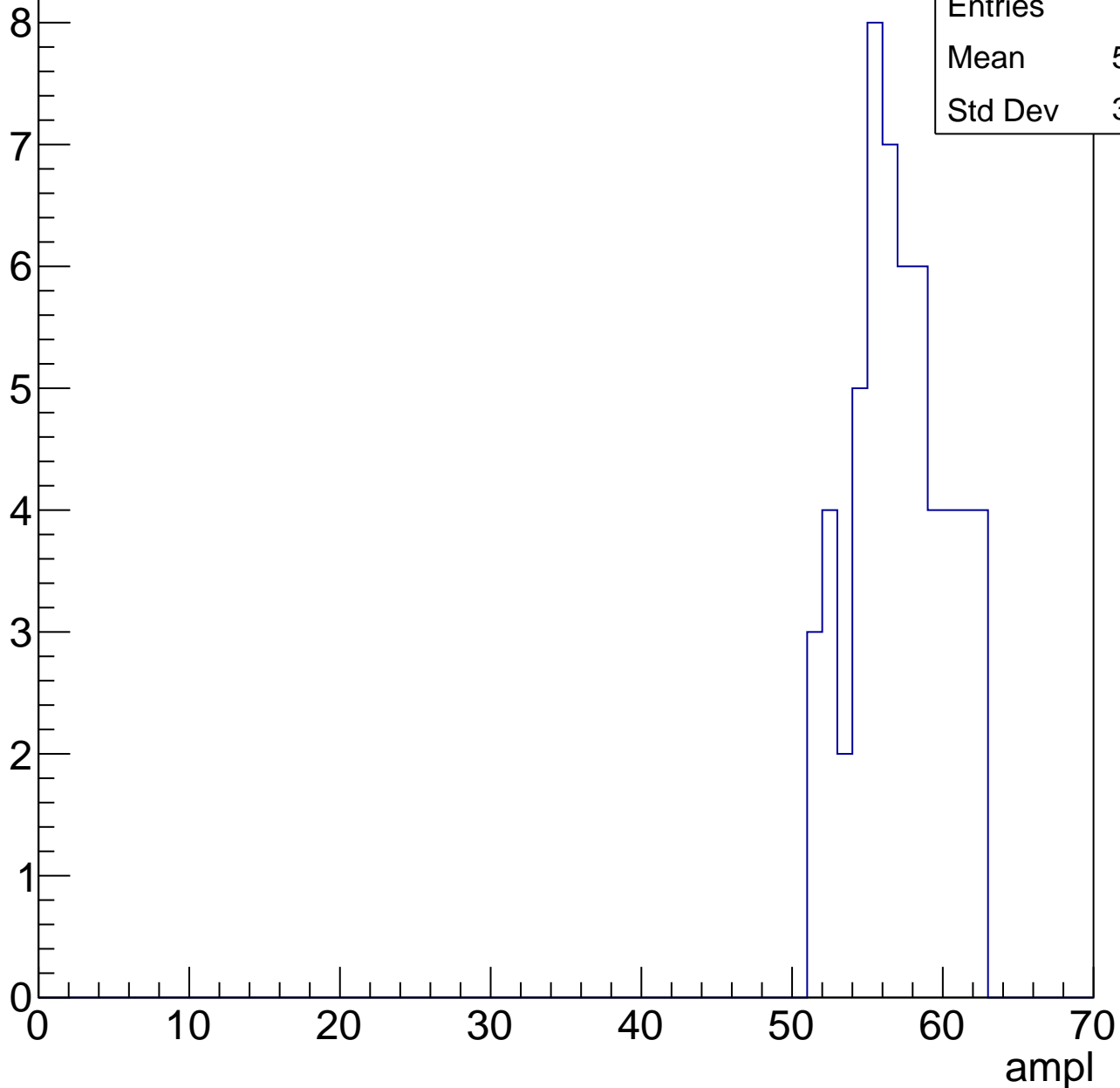
Entries	77
Mean	50.31
Std Dev	3.725



B1L100S, U11-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



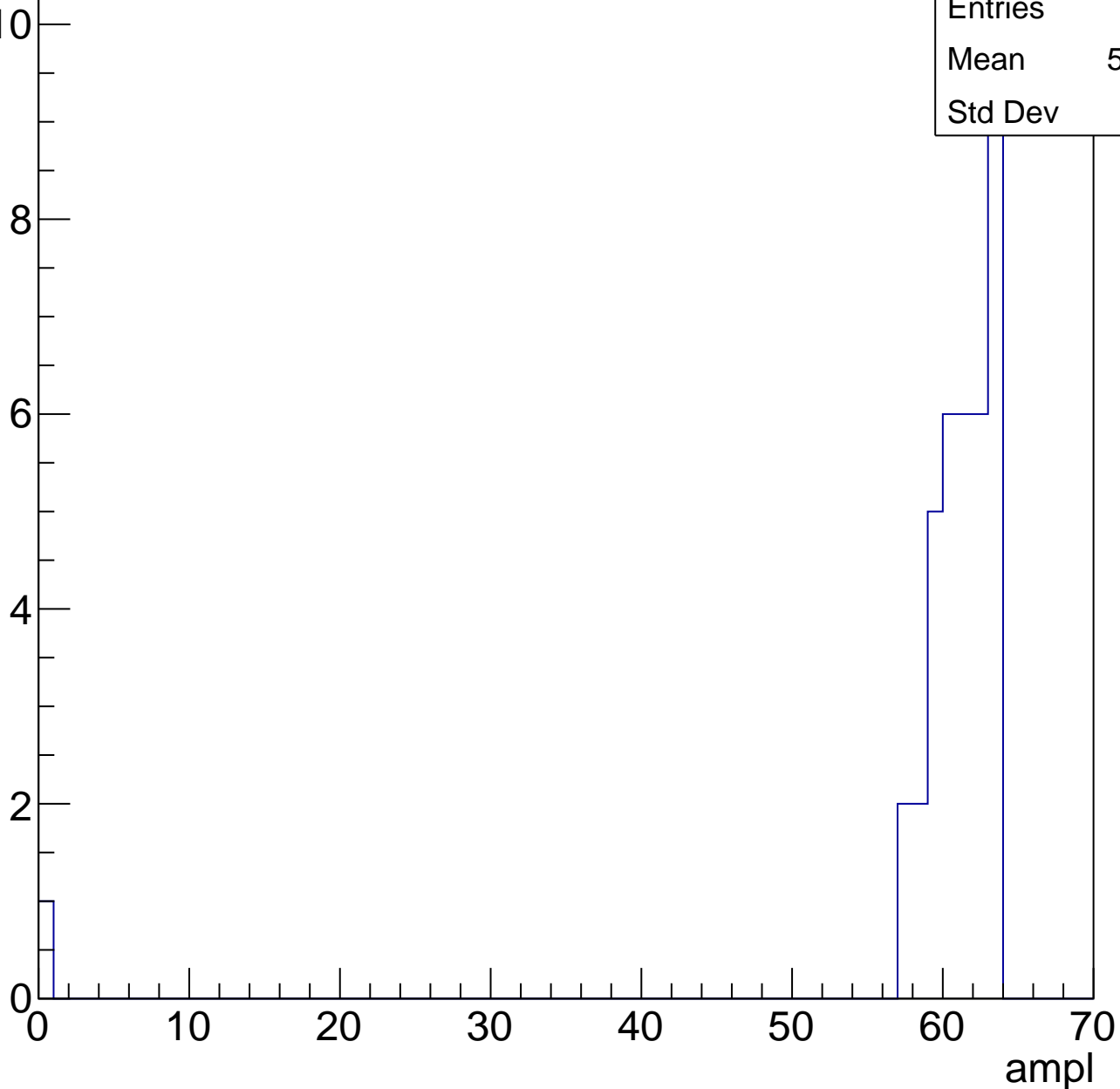
Entries	57
Mean	56.61
Std Dev	3.071

B1L100S, U11-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	59.29
Std Dev	9.91



B1L100S, U11-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch82, adc0

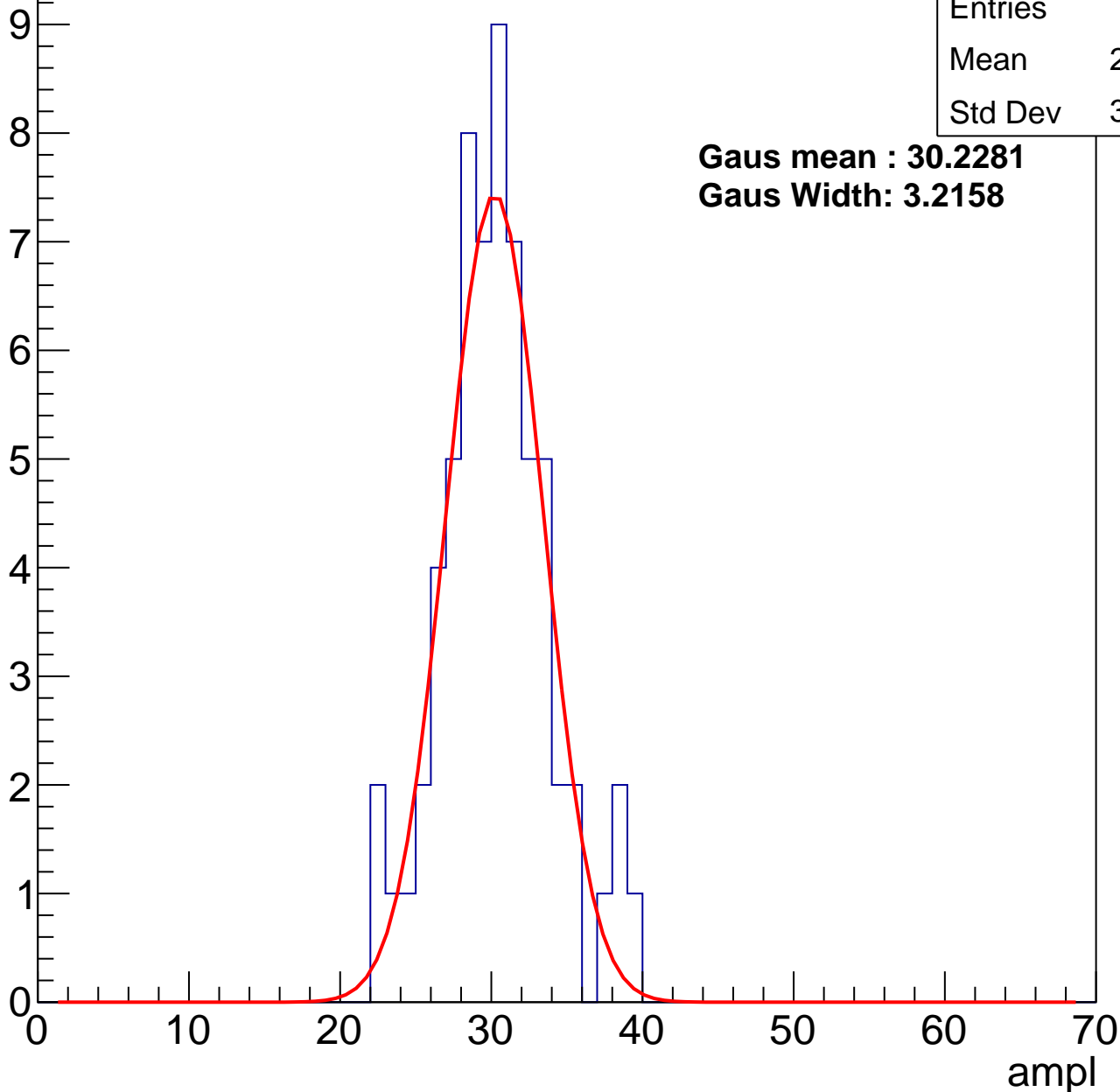
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.83
Std Dev	3.582

Gaus mean : 30.2281

Gaus Width: 3.2158



B1L100S, U11-ch82, adc1

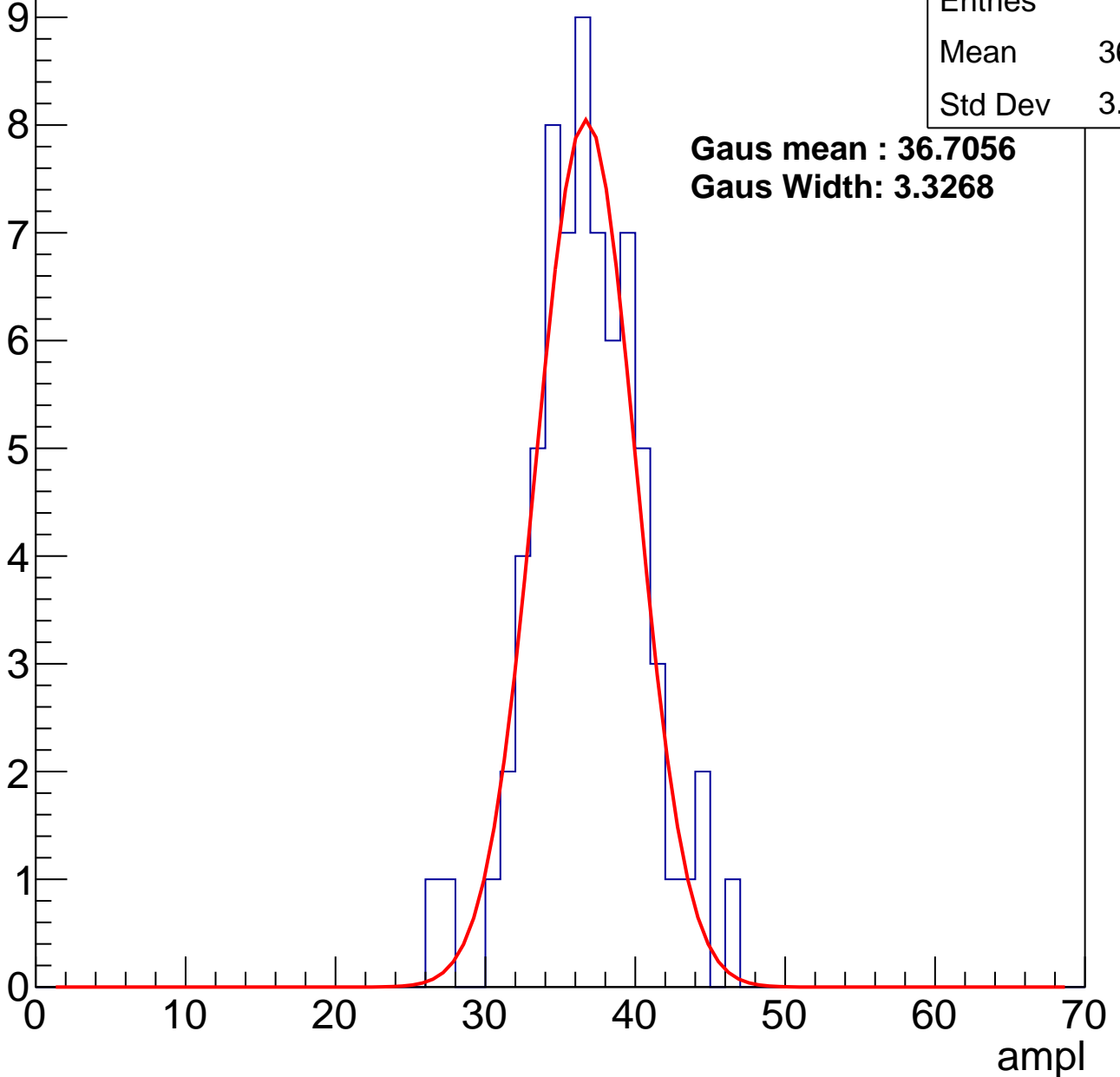
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.35
Std Dev	3.693

Gaus mean : 36.7056

Gaus Width: 3.3268



B1L100S, U11-ch82, adc2

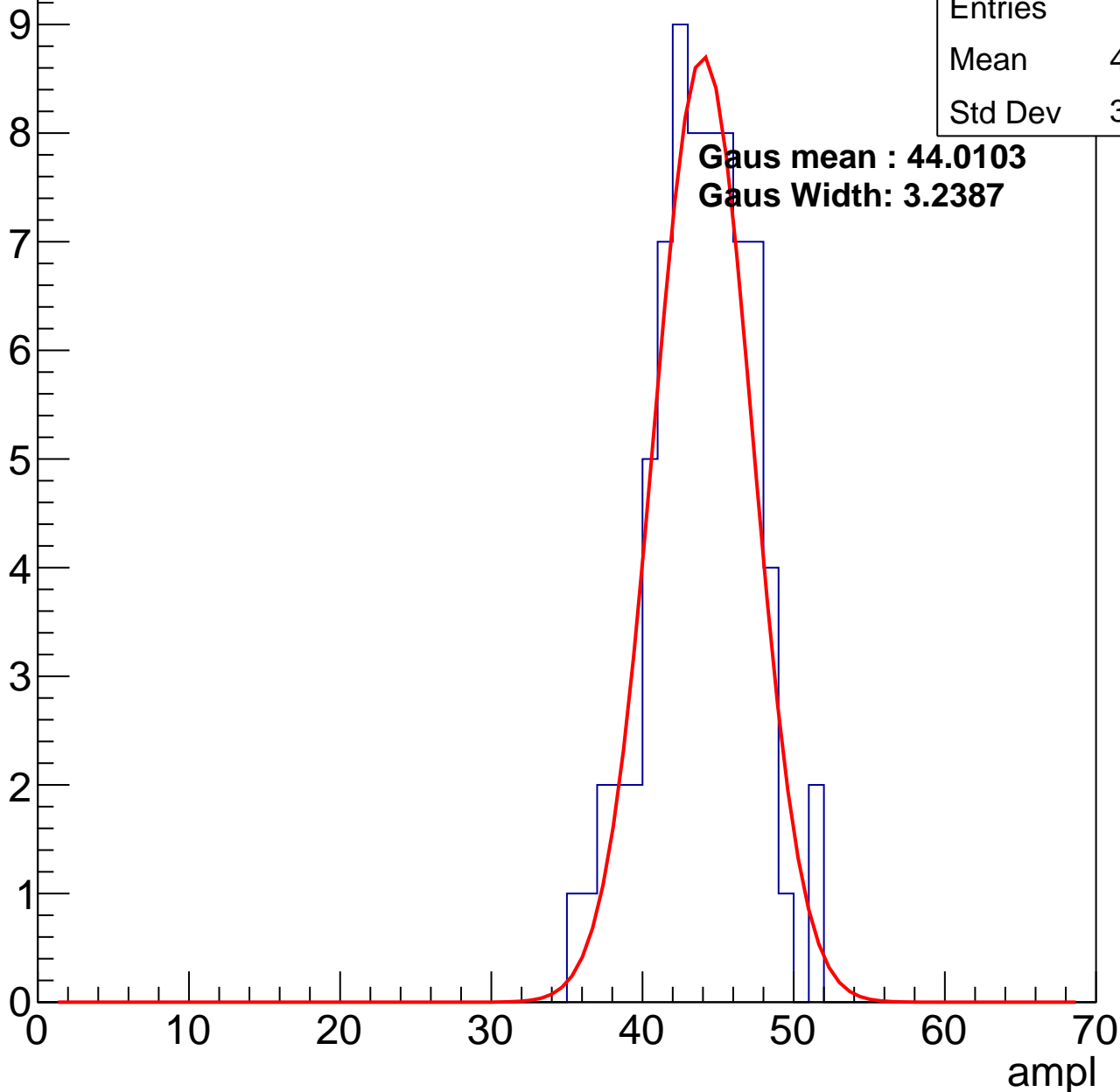
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	43.43
Std Dev	3.317

Gaus mean : 44.0103

Gaus Width: 3.2387

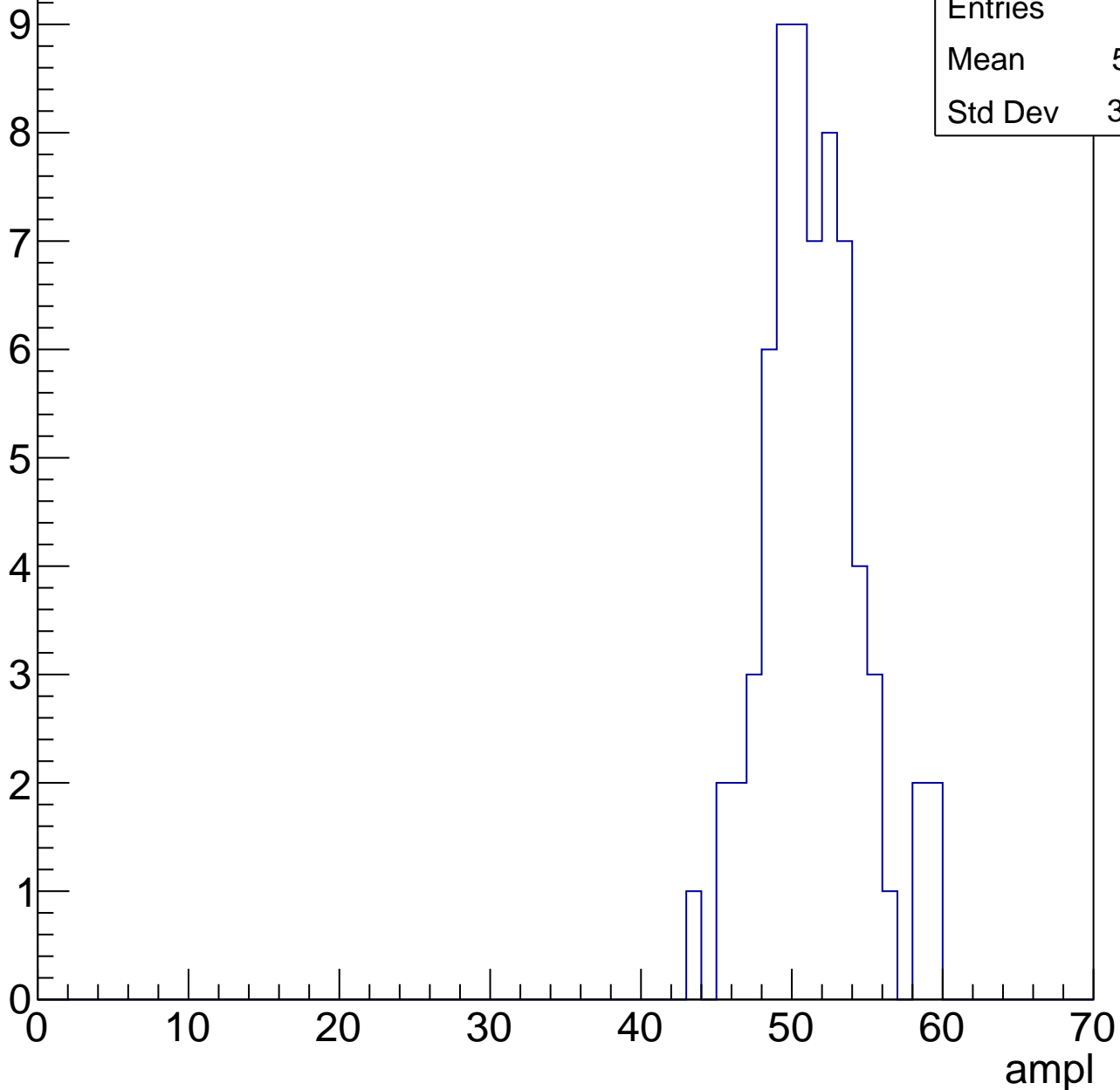


B1L100S, U11-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

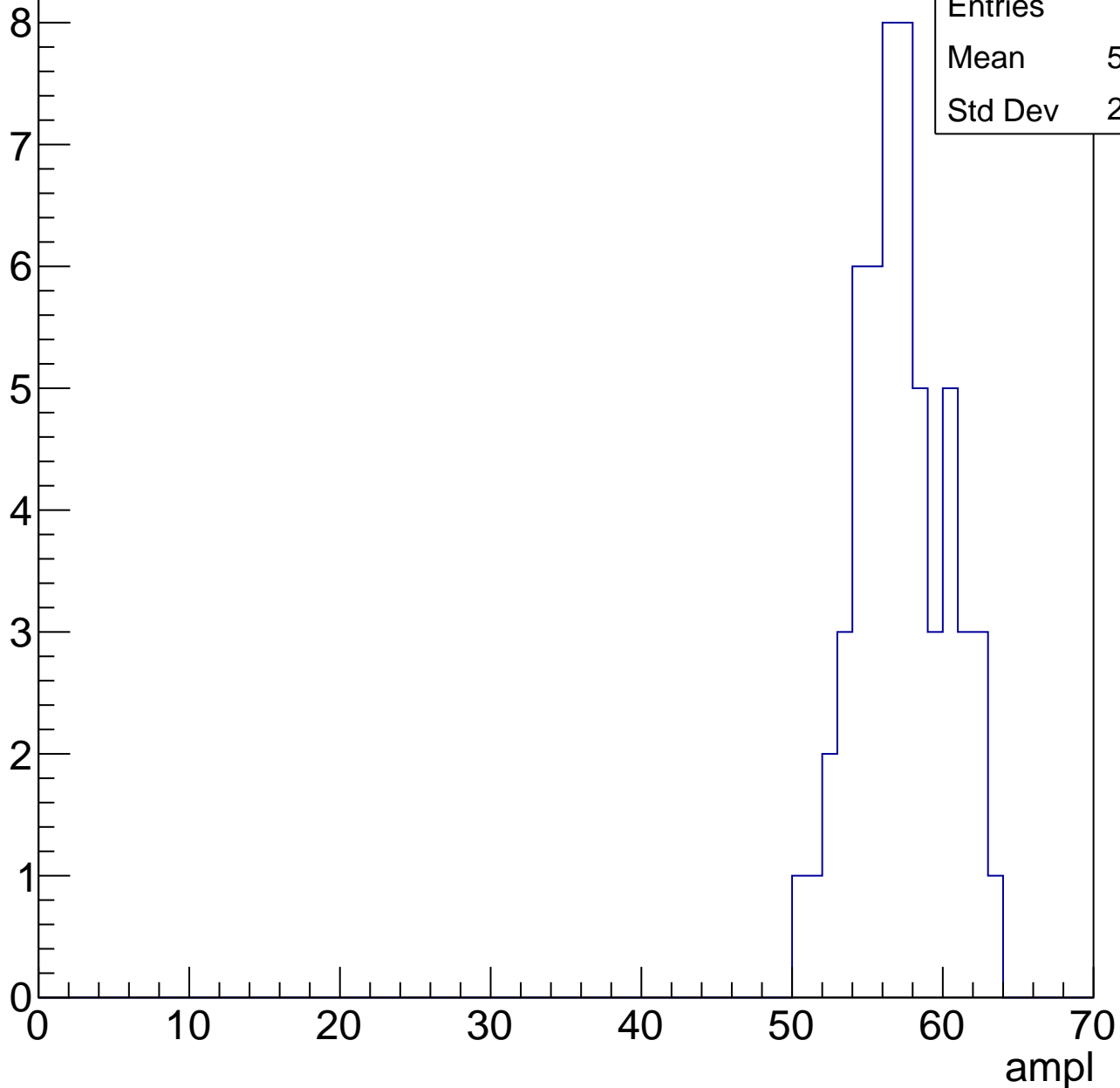
Entries	66
Mean	50.91
Std Dev	3.265



B1L100S, U11-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

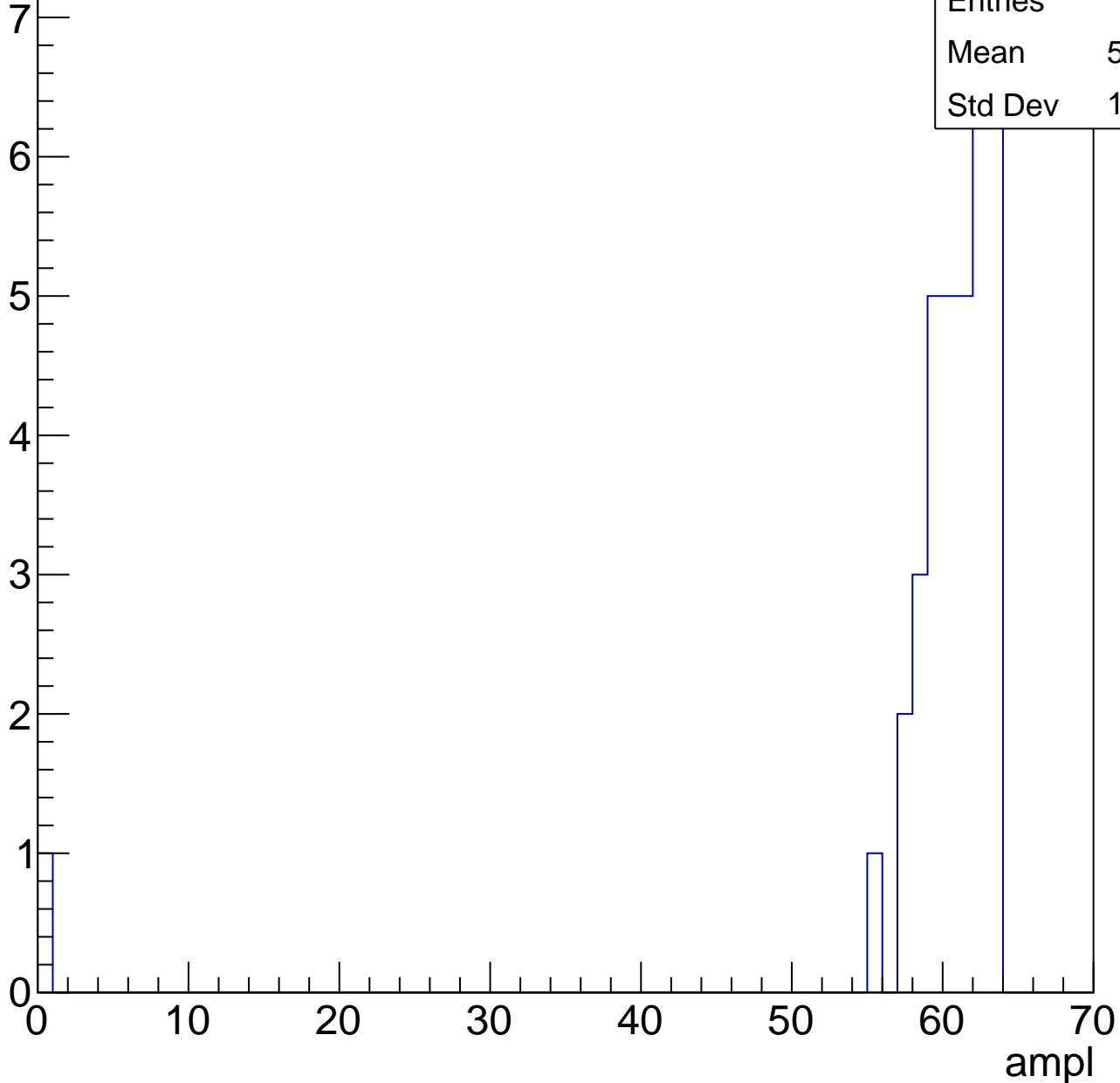


B1L100S, U11-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

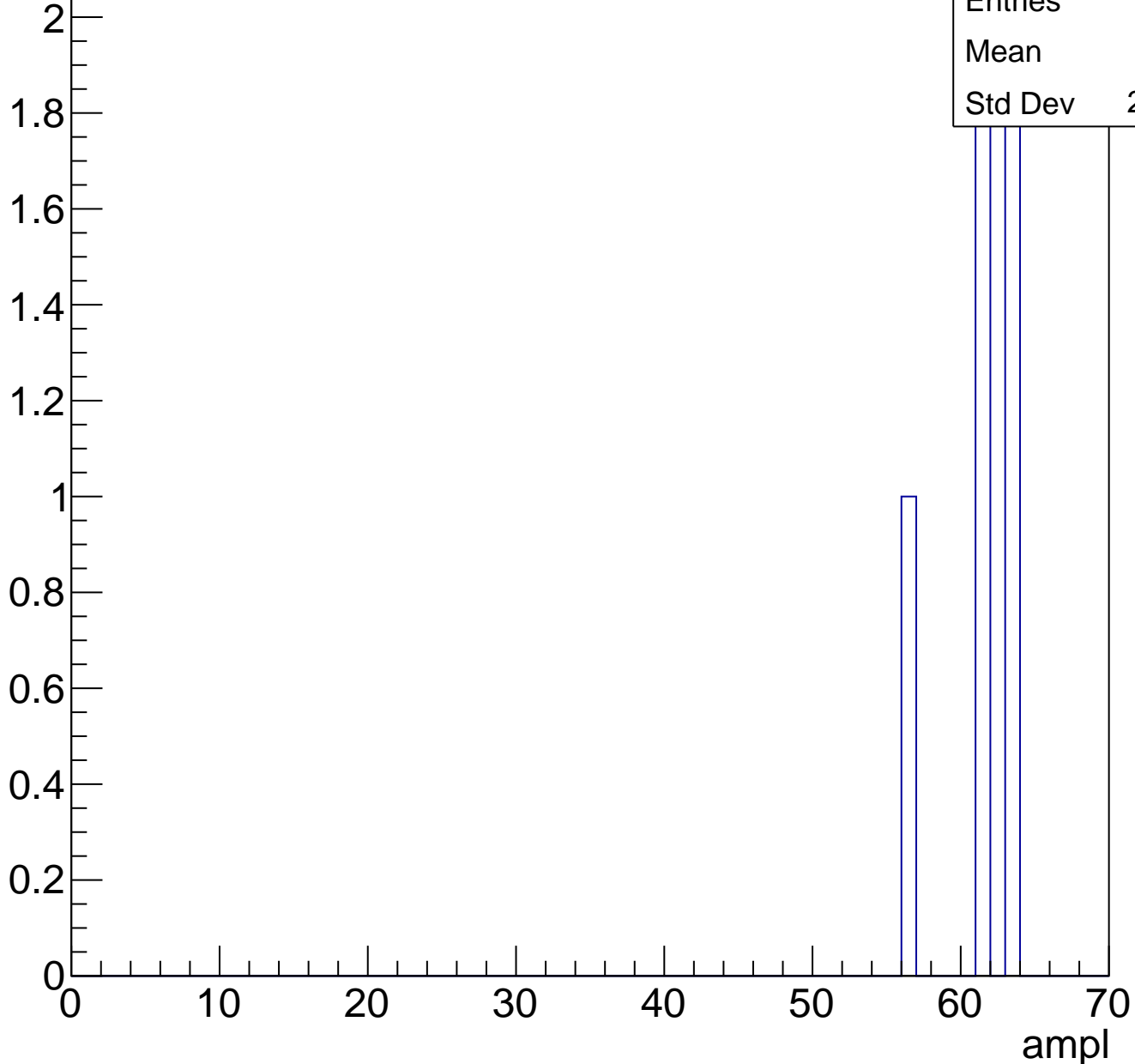
Entries	36
Mean	58.83
Std Dev	10.15



B1L100S, U11-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch83, adc0

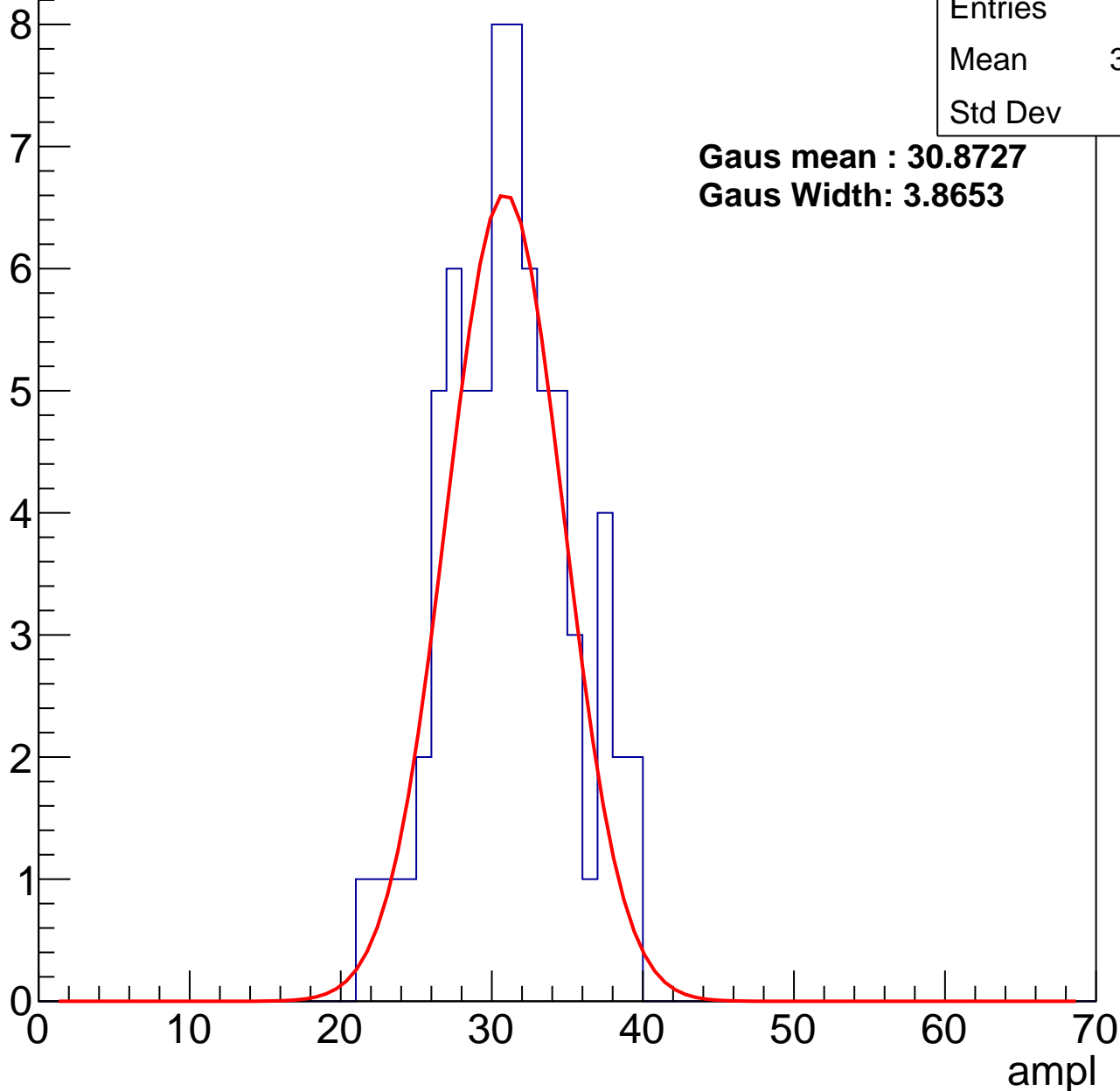
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.63
Std Dev	4.05

Gaus mean : 30.8727

Gaus Width: 3.8653



B1L100S, U11-ch83, adc1

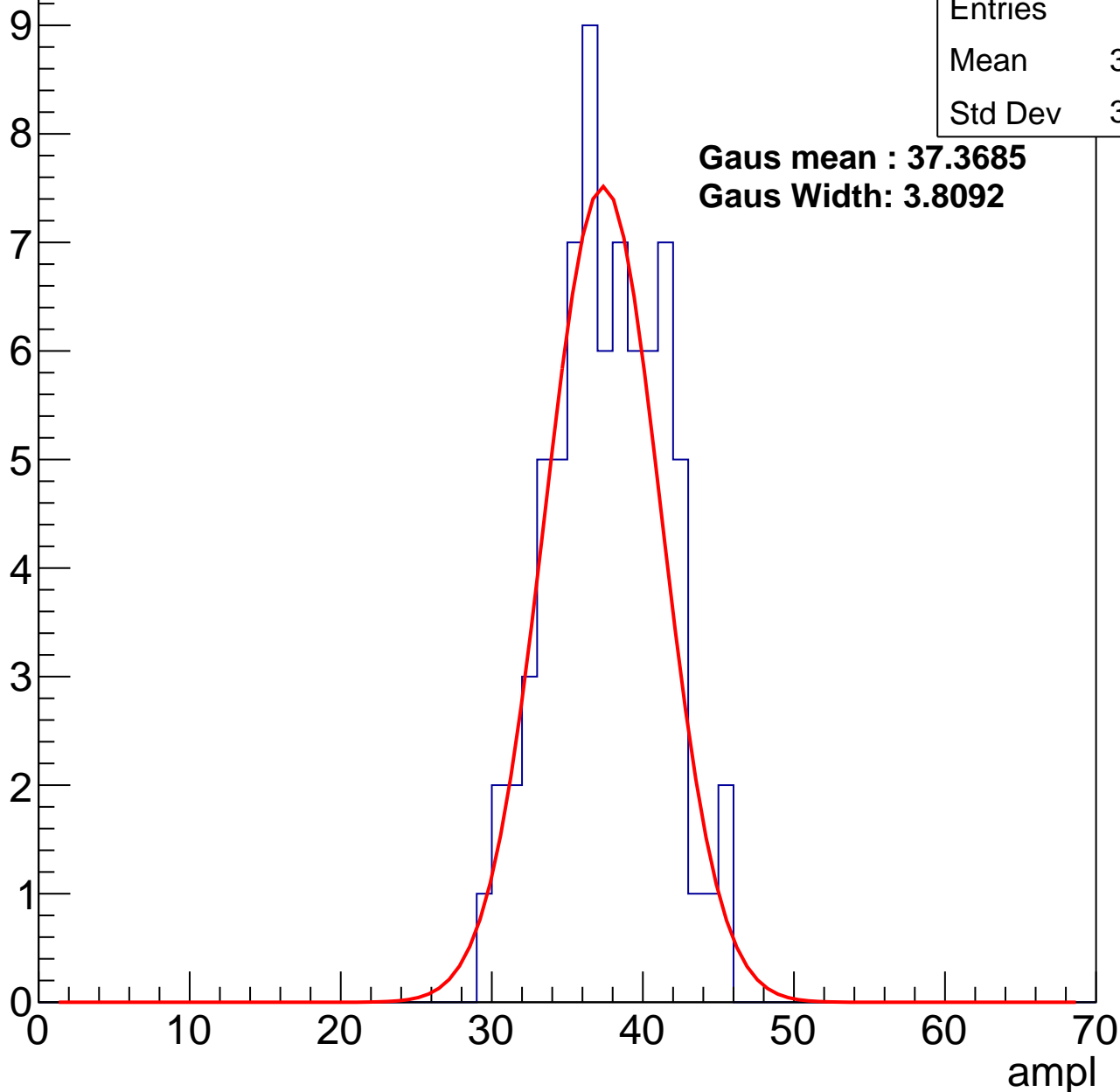
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	37.16
Std Dev	3.659

Gaus mean : 37.3685

Gaus Width: 3.8092



B1L100S, U11-ch83, adc2

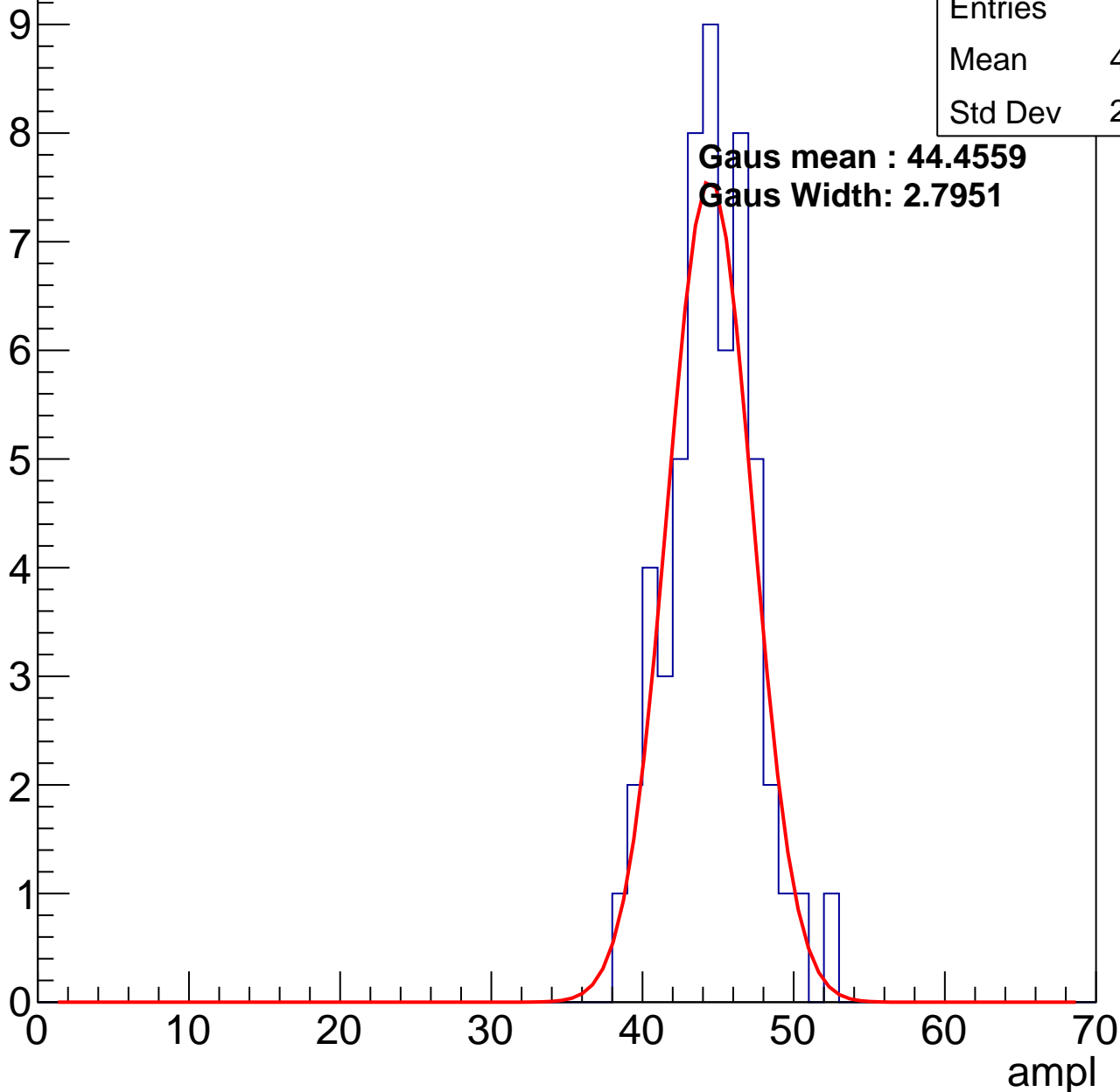
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.09
Std Dev	2.818

Gaus mean : 44.4559

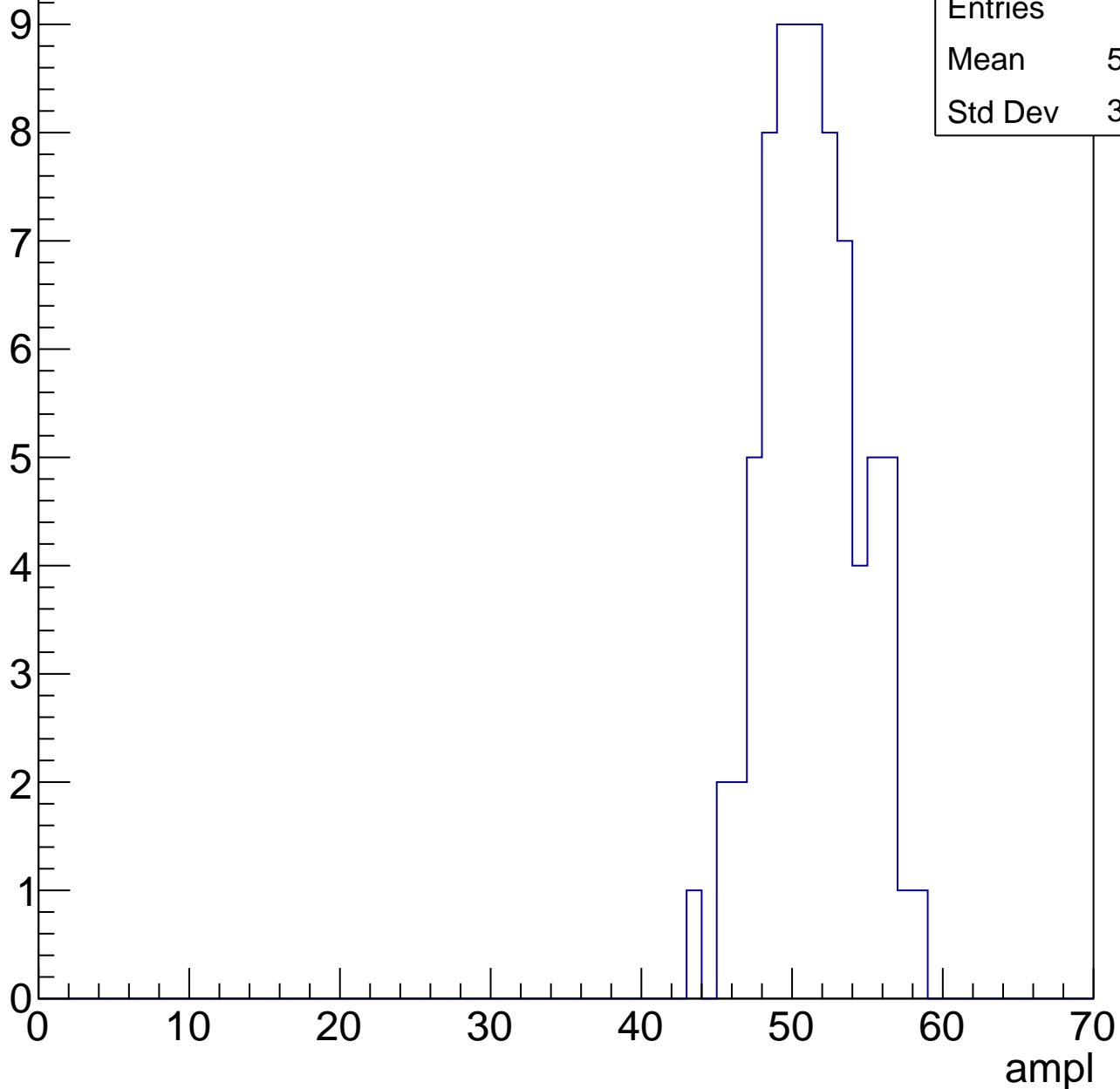
Gaus Width: 2.7951



B1L100S, U11-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



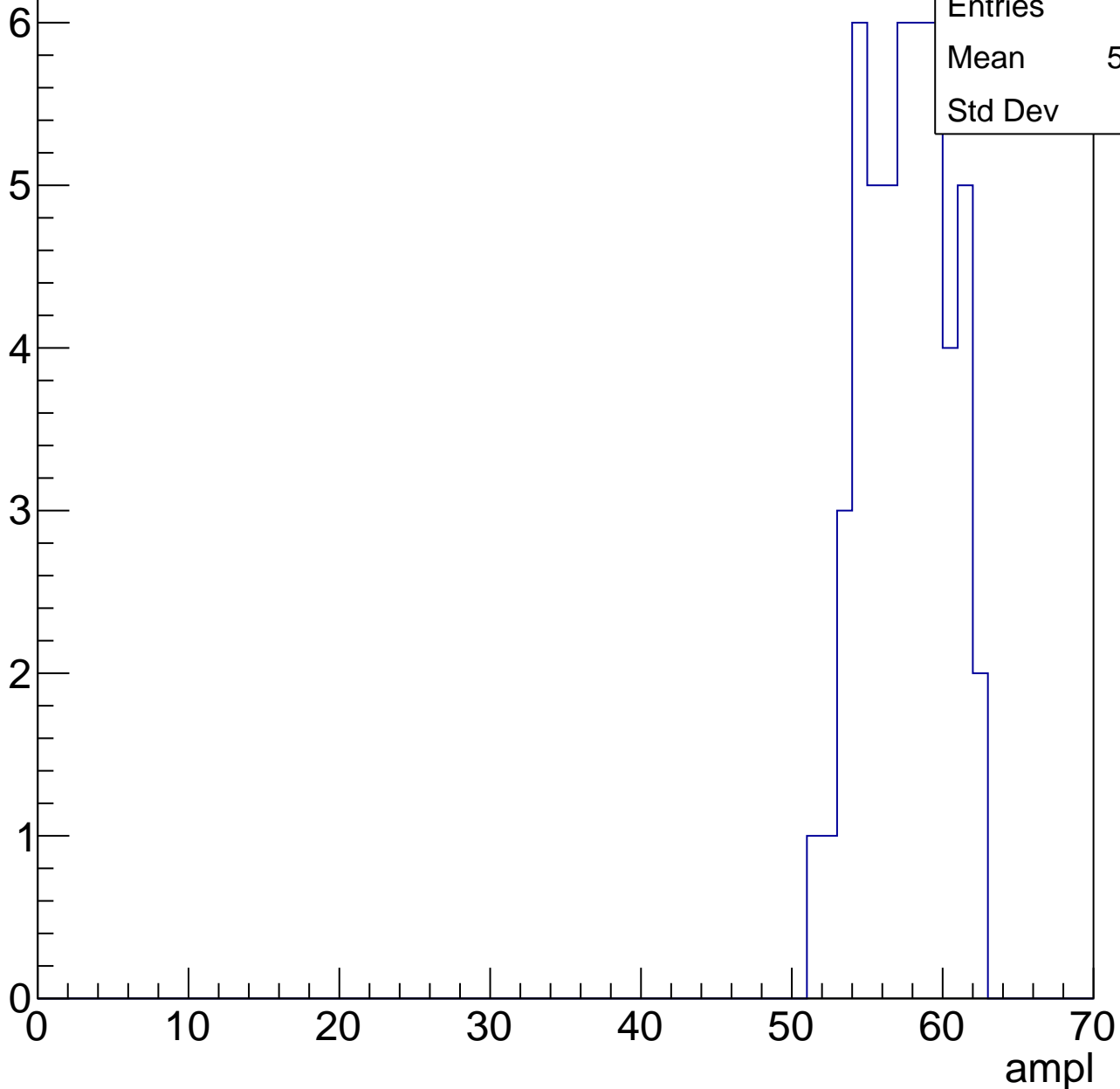
Entries	76
Mean	50.88
Std Dev	3.129

B1L100S, U11-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	57.08
Std Dev	2.77



B1L100S, U11-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	38
Mean	59.16
Std Dev	9.912

ampl

0

10

20

30

40

50

60

70

B1L100S, U11-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch84, adc0

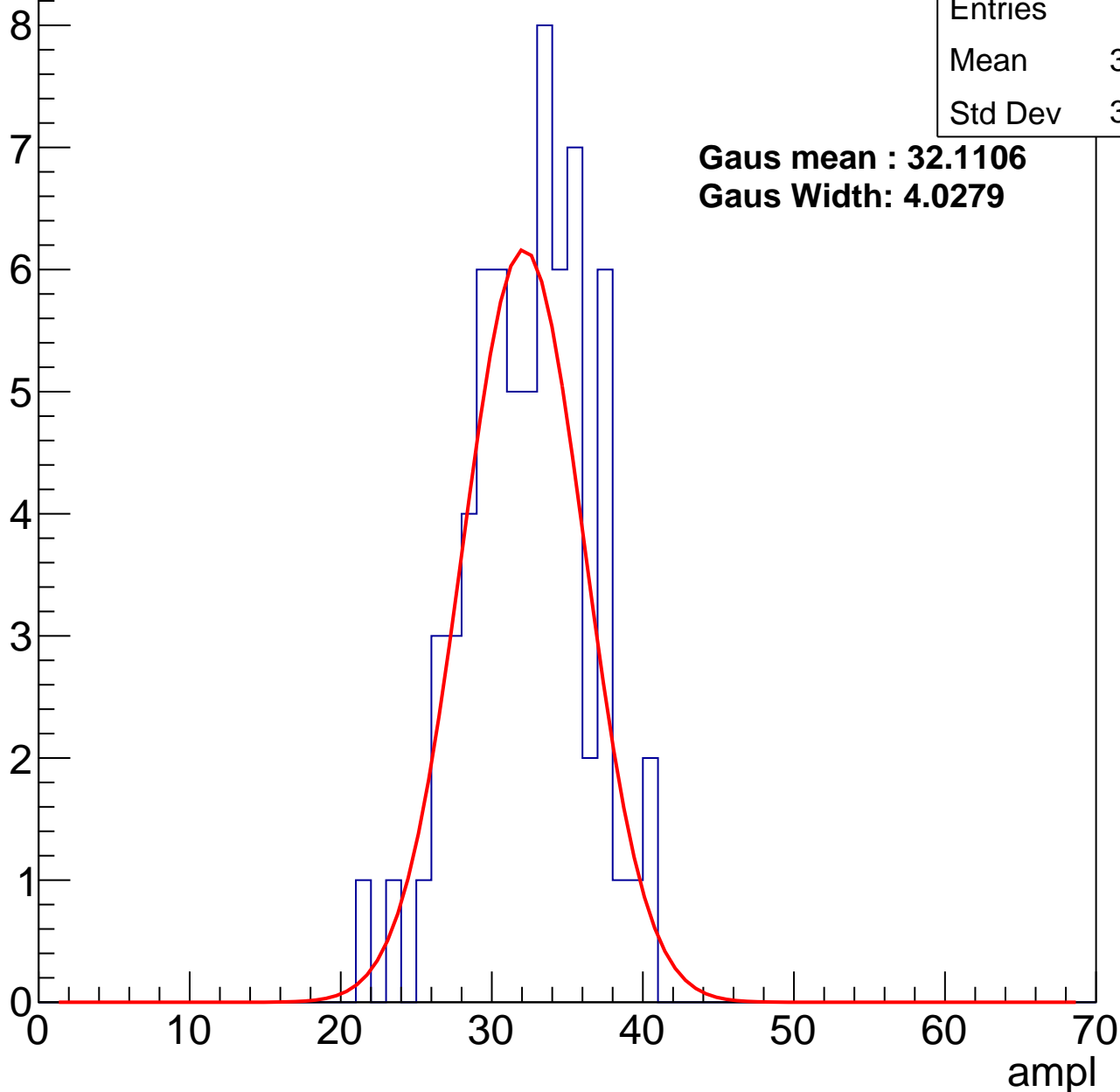
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	31.96
Std Dev	3.968

Gaus mean : 32.1106

Gaus Width: 4.0279



B1L100S, U11-ch84, adc1

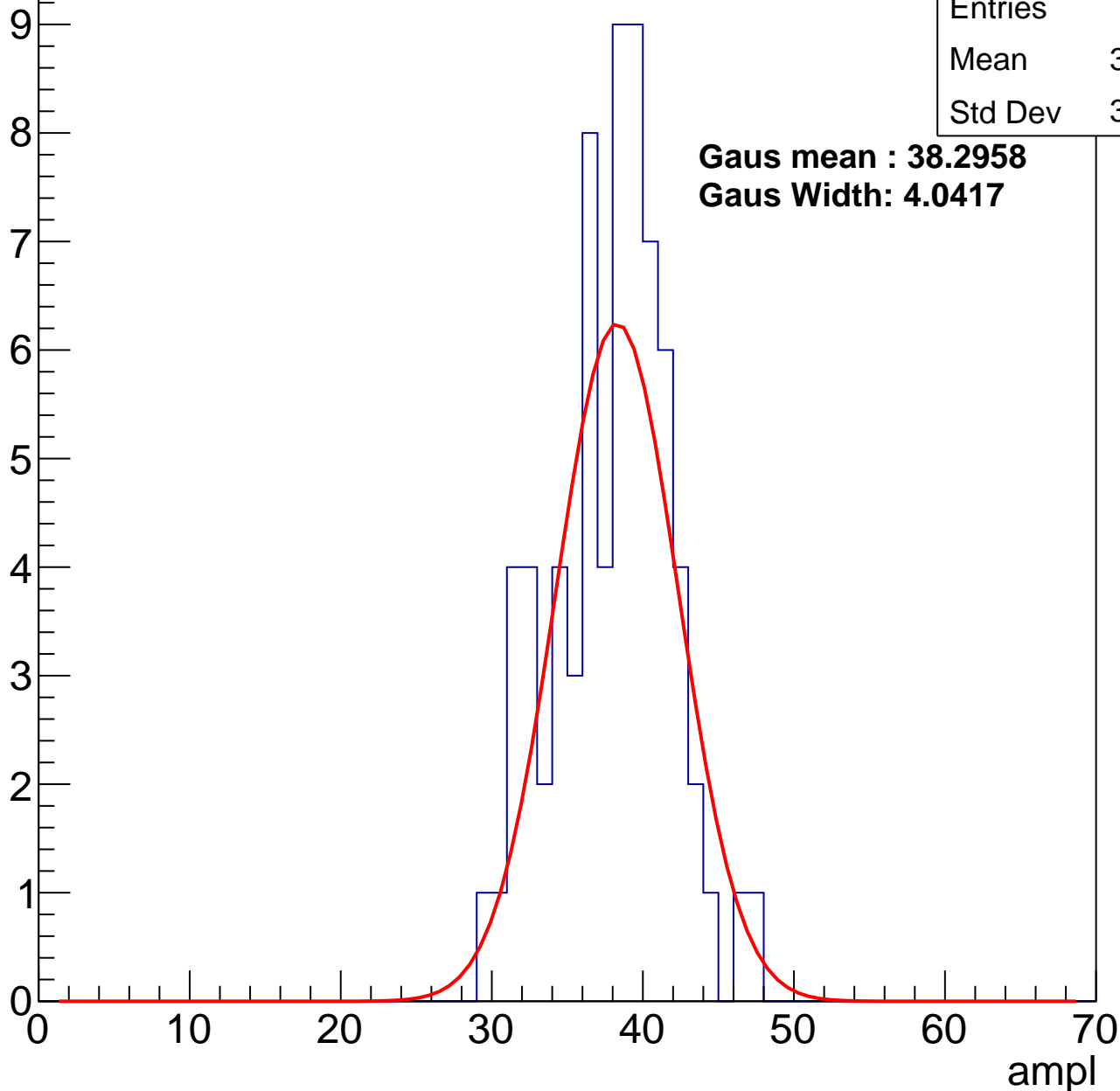
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	37.52
Std Dev	3.812

Gaus mean : 38.2958

Gaus Width: 4.0417



B1L100S, U11-ch84, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	45.86
Std Dev	3.066

Gaus mean : 46.2122

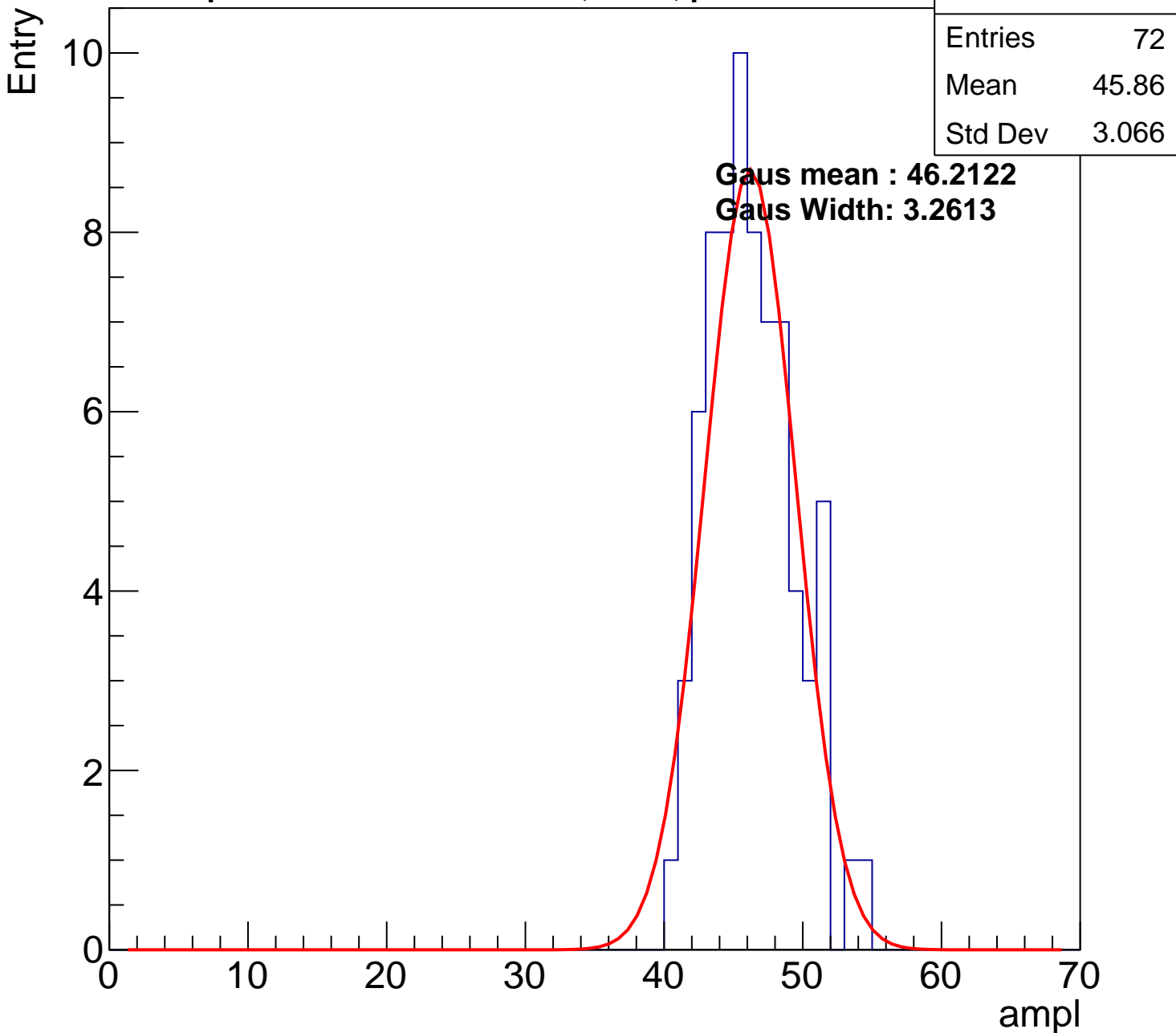
Gaus Width: 3.2613

Entry

10
8
6
4
2
0

ampl

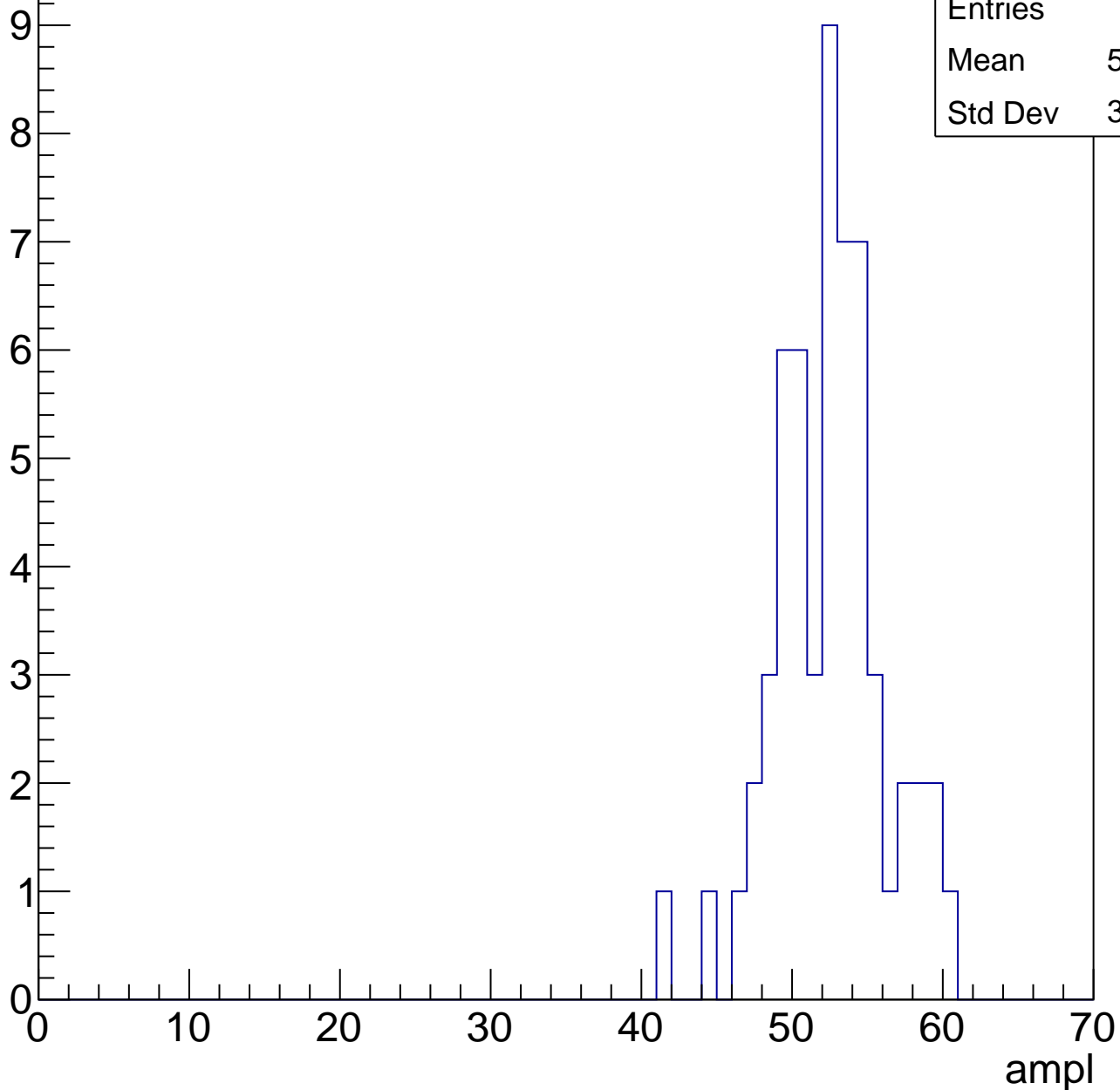
0 10 20 30 40 50 60 70



B1L100S, U11-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



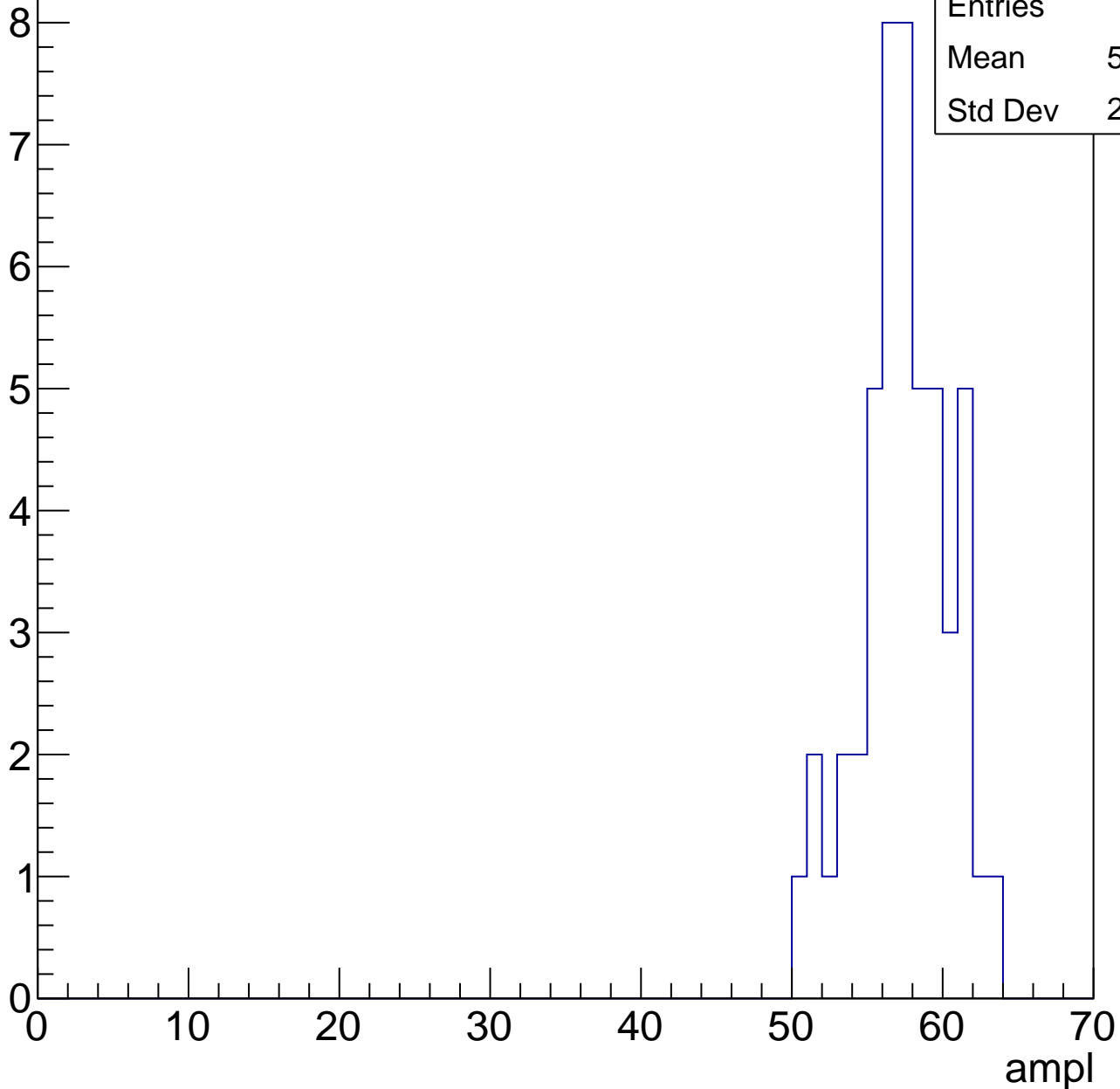
Entries	57
Mean	51.96
Std Dev	3.656

B1L100S, U11-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	56.98
Std Dev	2.917

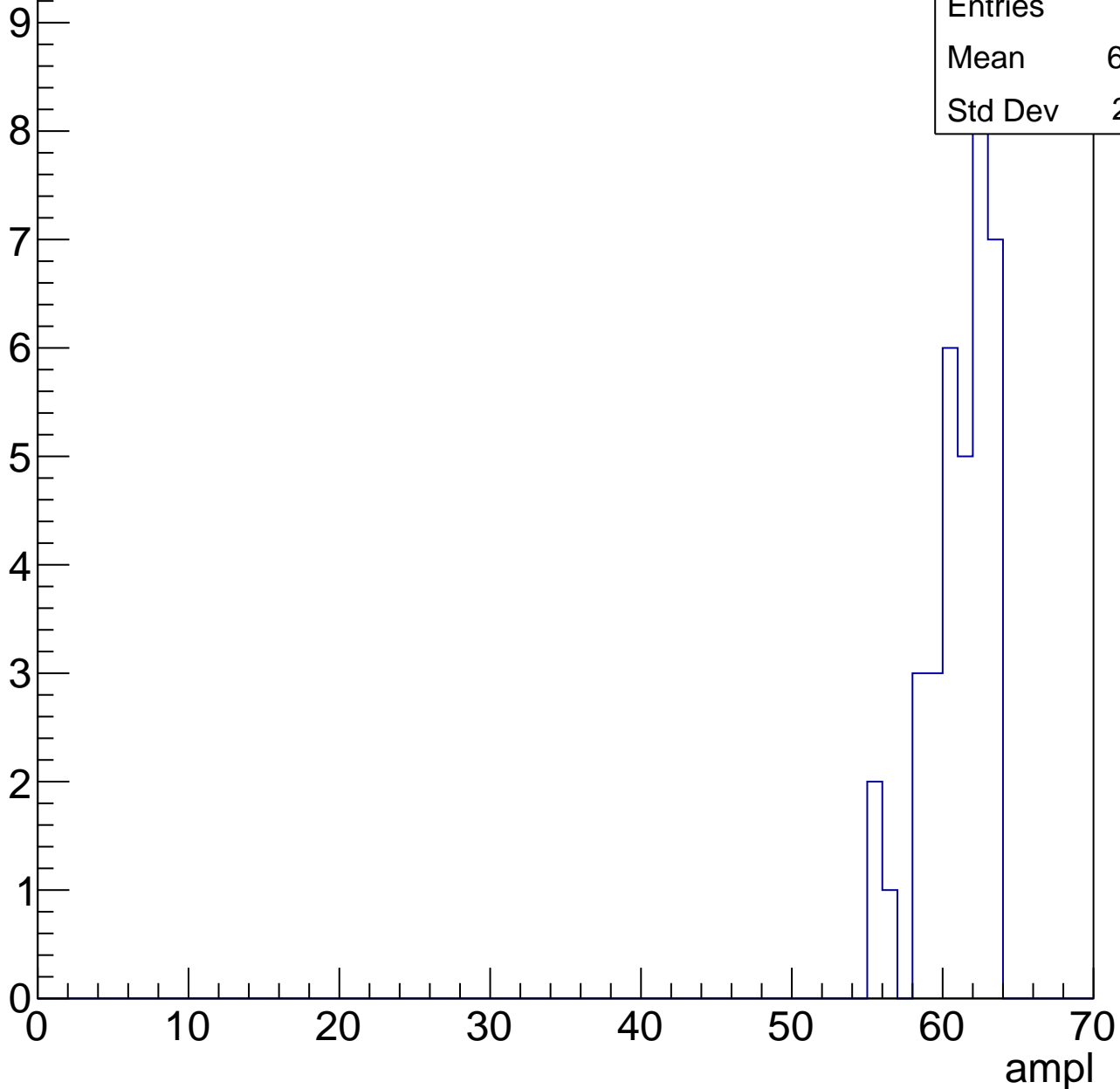


B1L100S, U11-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	60.58
Std Dev	2.191



B1L100S, U11-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch85, adc0

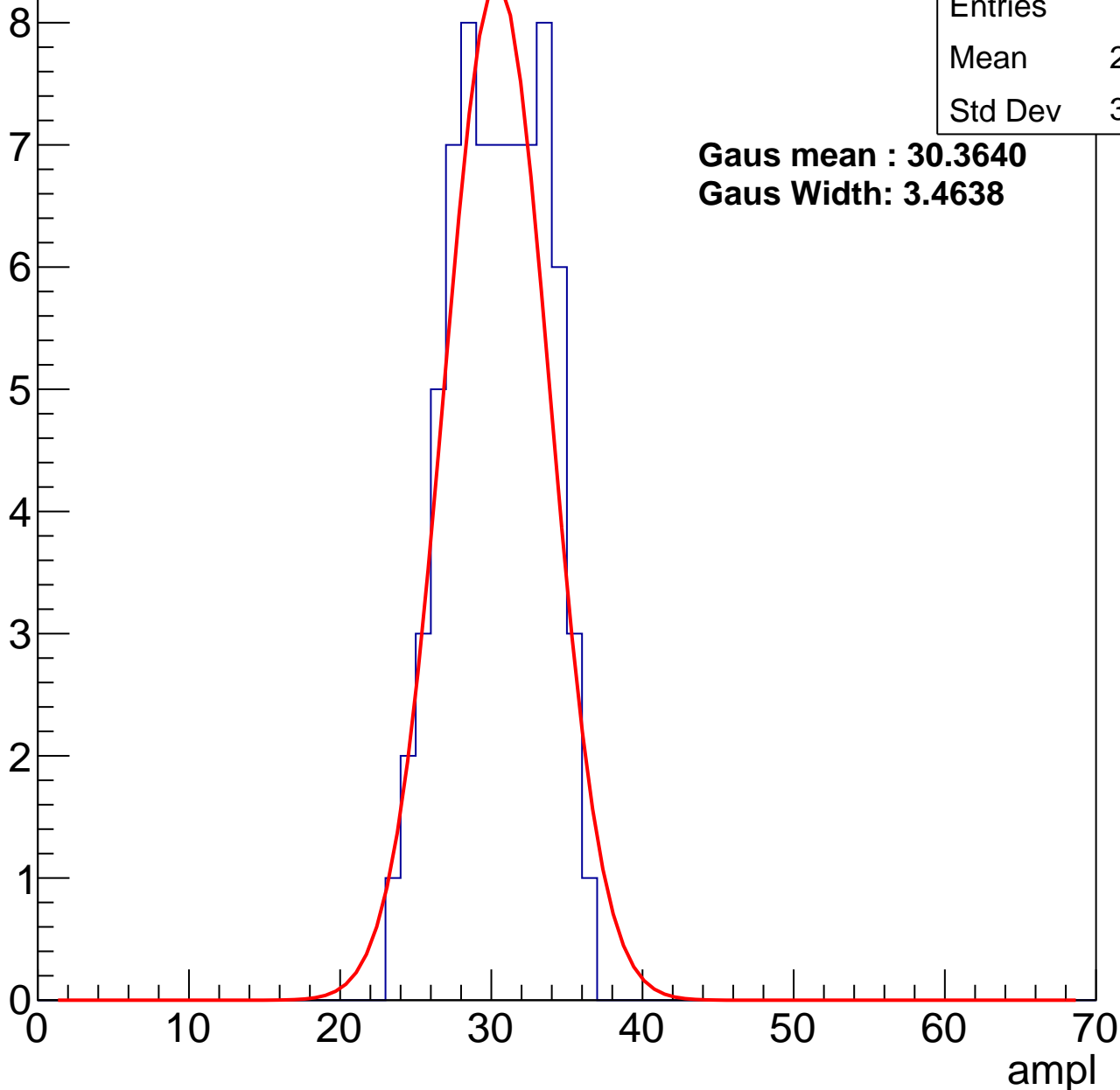
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	29.89
Std Dev	3.098

Gaus mean : 30.3640

Gaus Width: 3.4638



B1L100S, U11-ch85, adc1

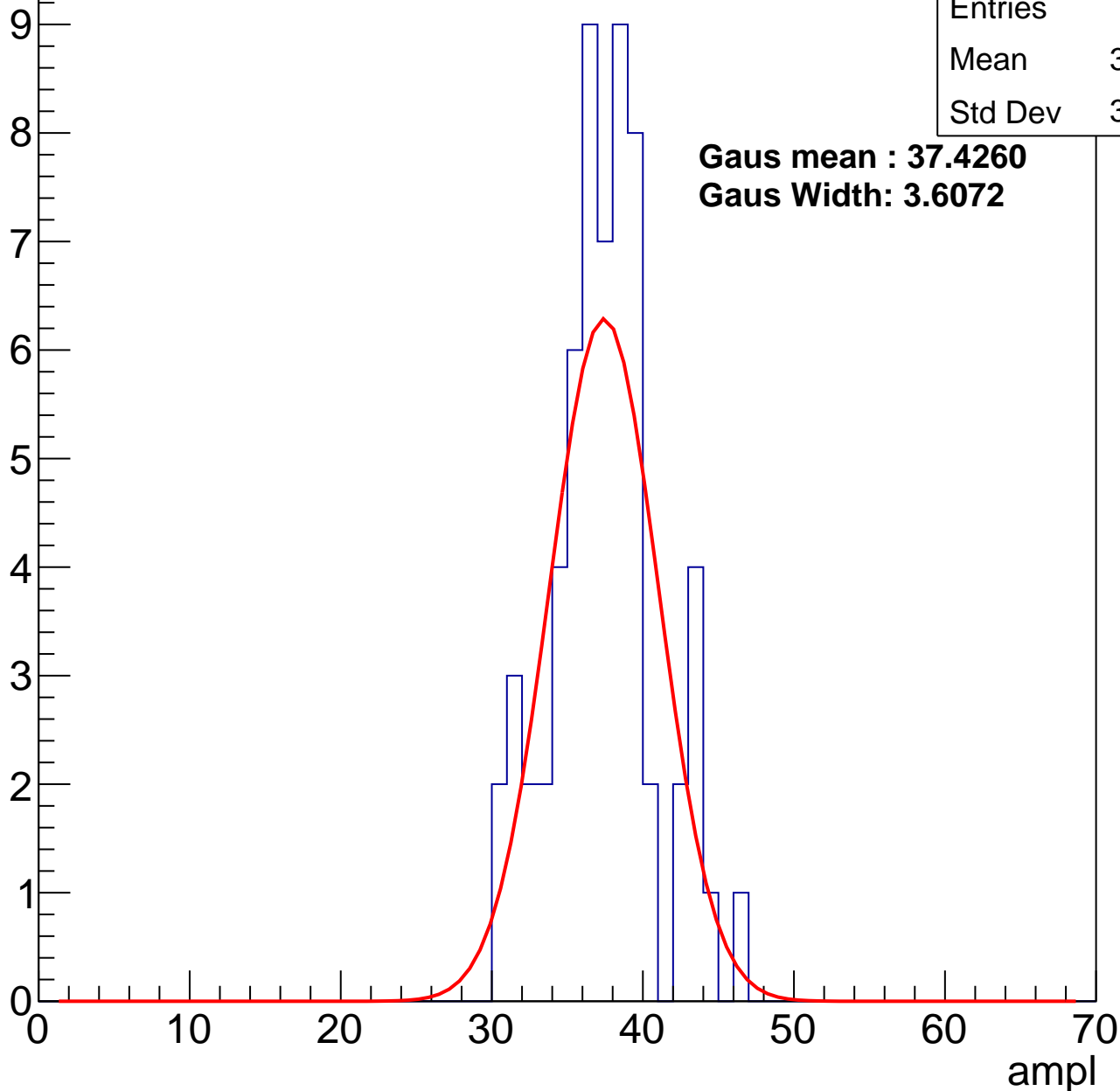
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.97
Std Dev	3.455

Gaus mean : 37.4260

Gaus Width: 3.6072



B1L100S, U11-ch85, adc2

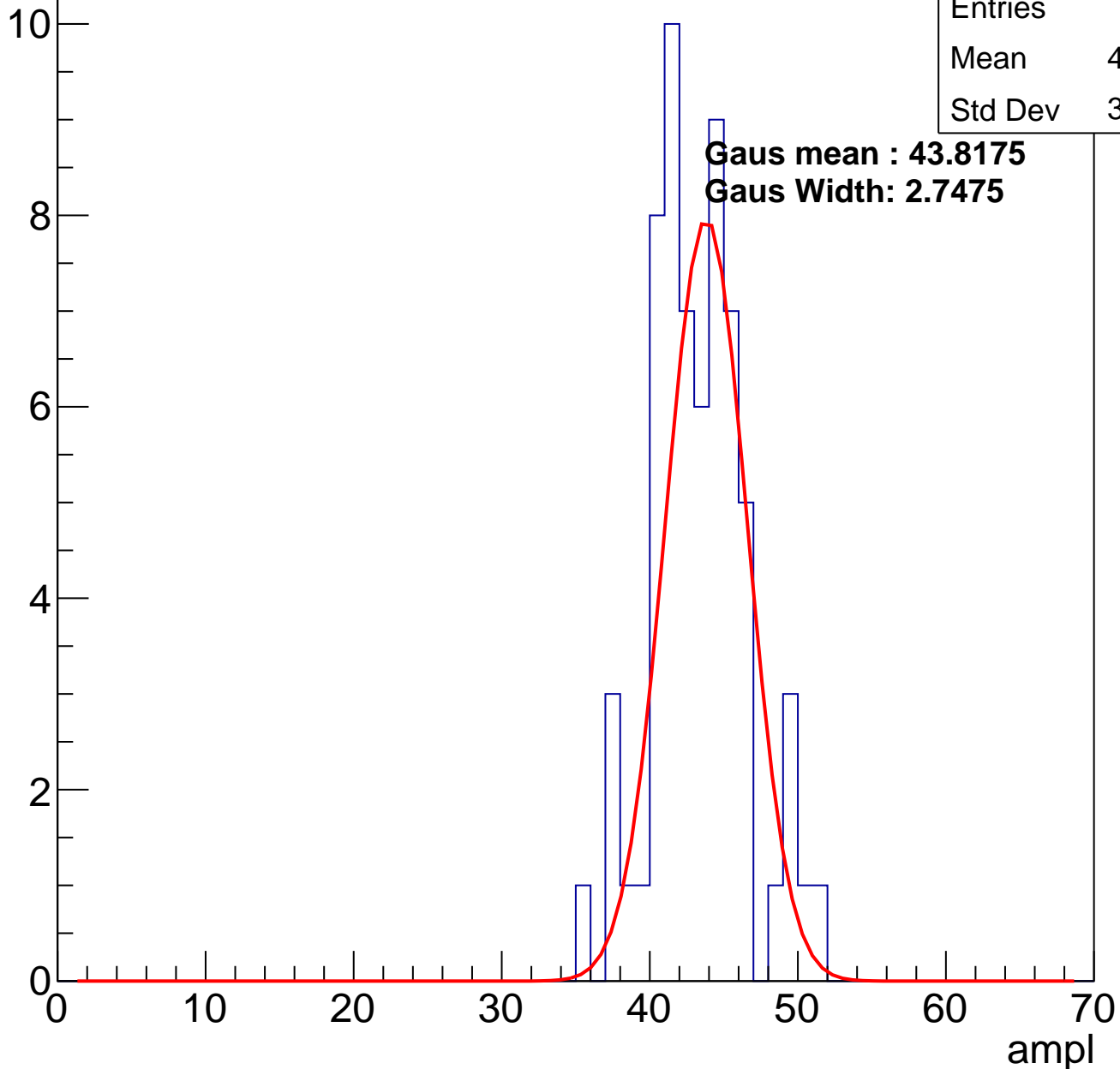
calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	42.84
Std Dev	3.208

Gaus mean : 43.8175

Gaus Width: 2.7475

Entry



B1L100S, U11-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	49.78
Std Dev	3.267

Entry

10

8

6

4

2

0

0

10

20

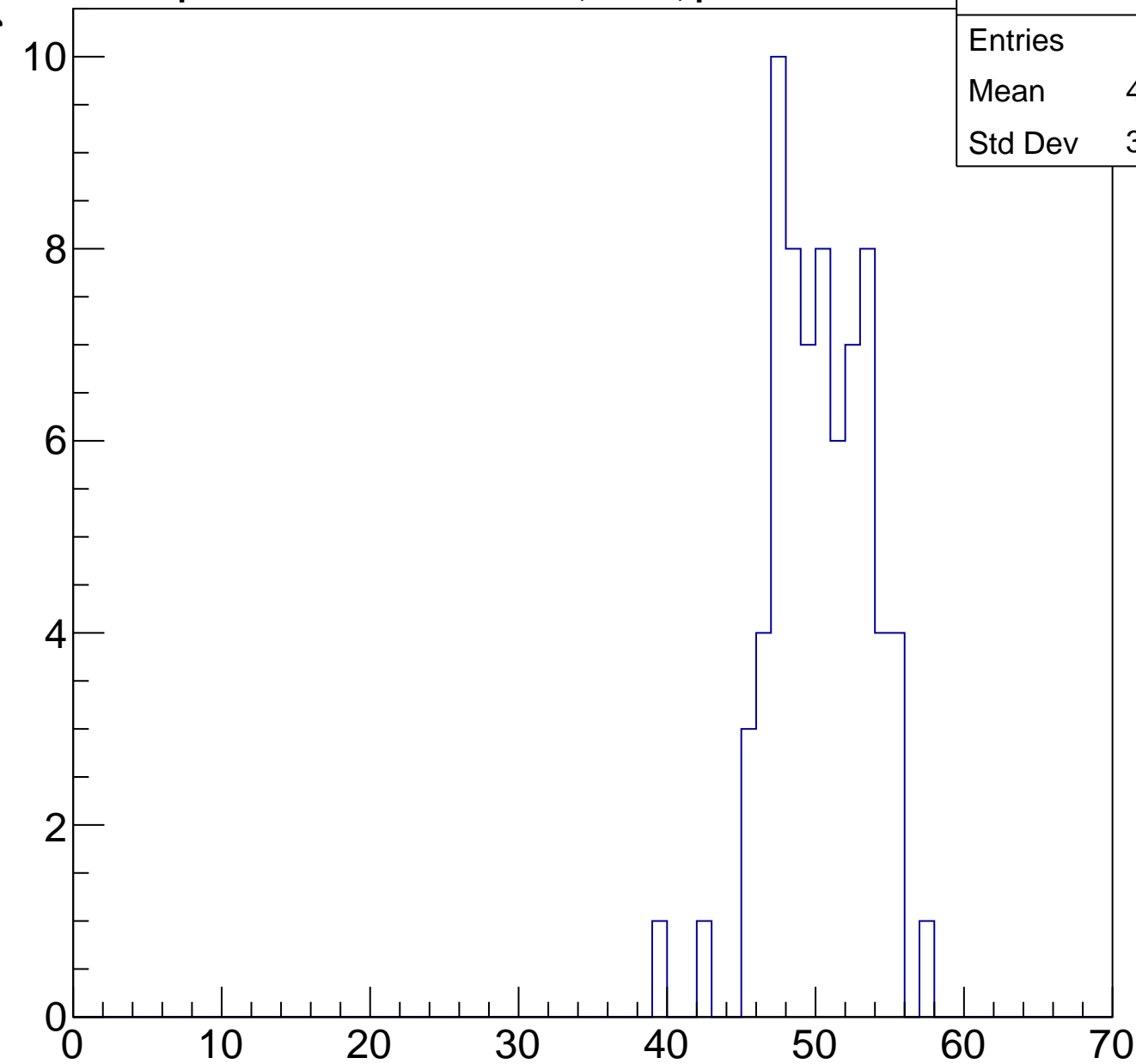
30

40

50

60

ampl

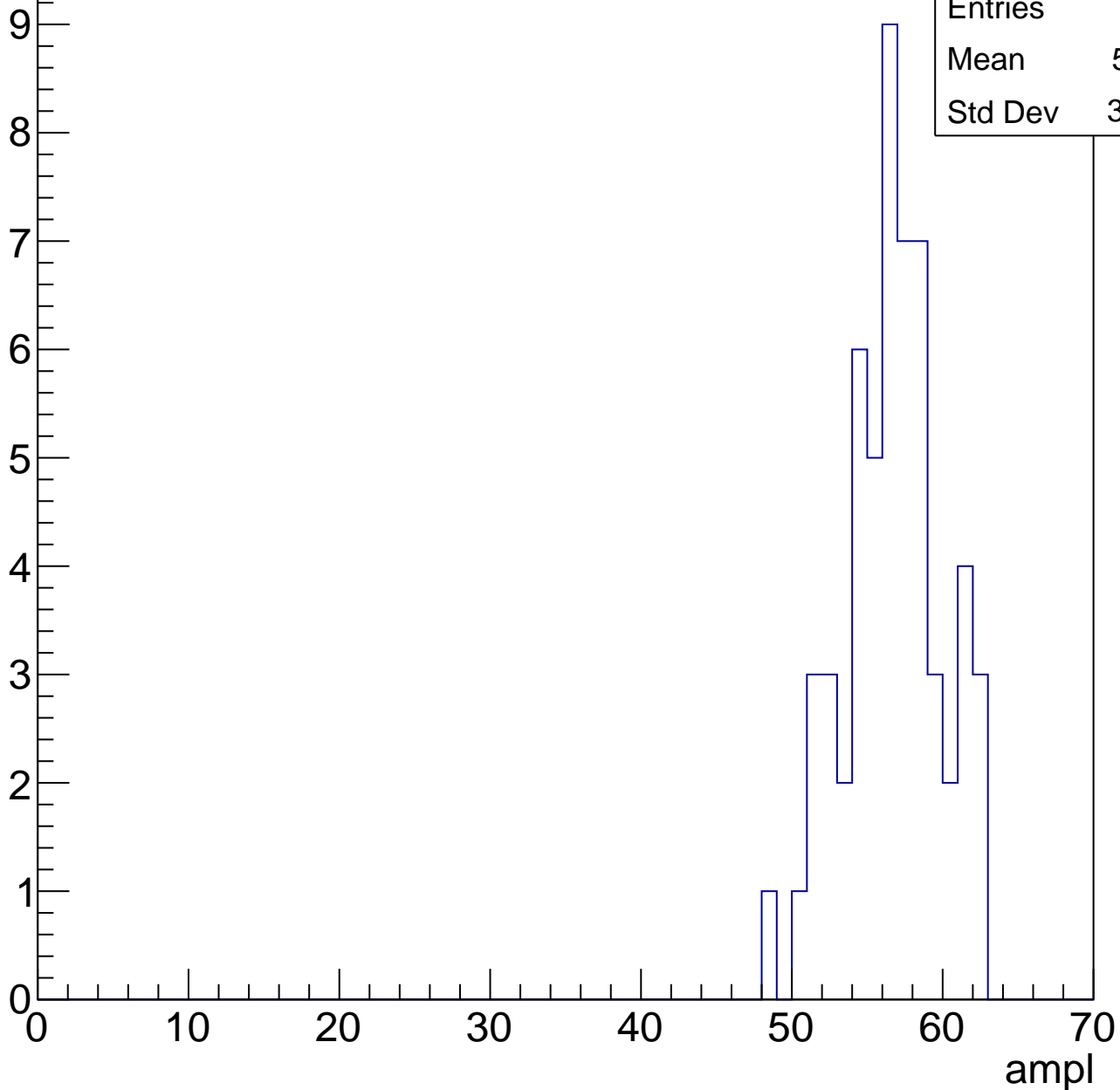


B1L100S, U11-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.21
Std Dev	3.189

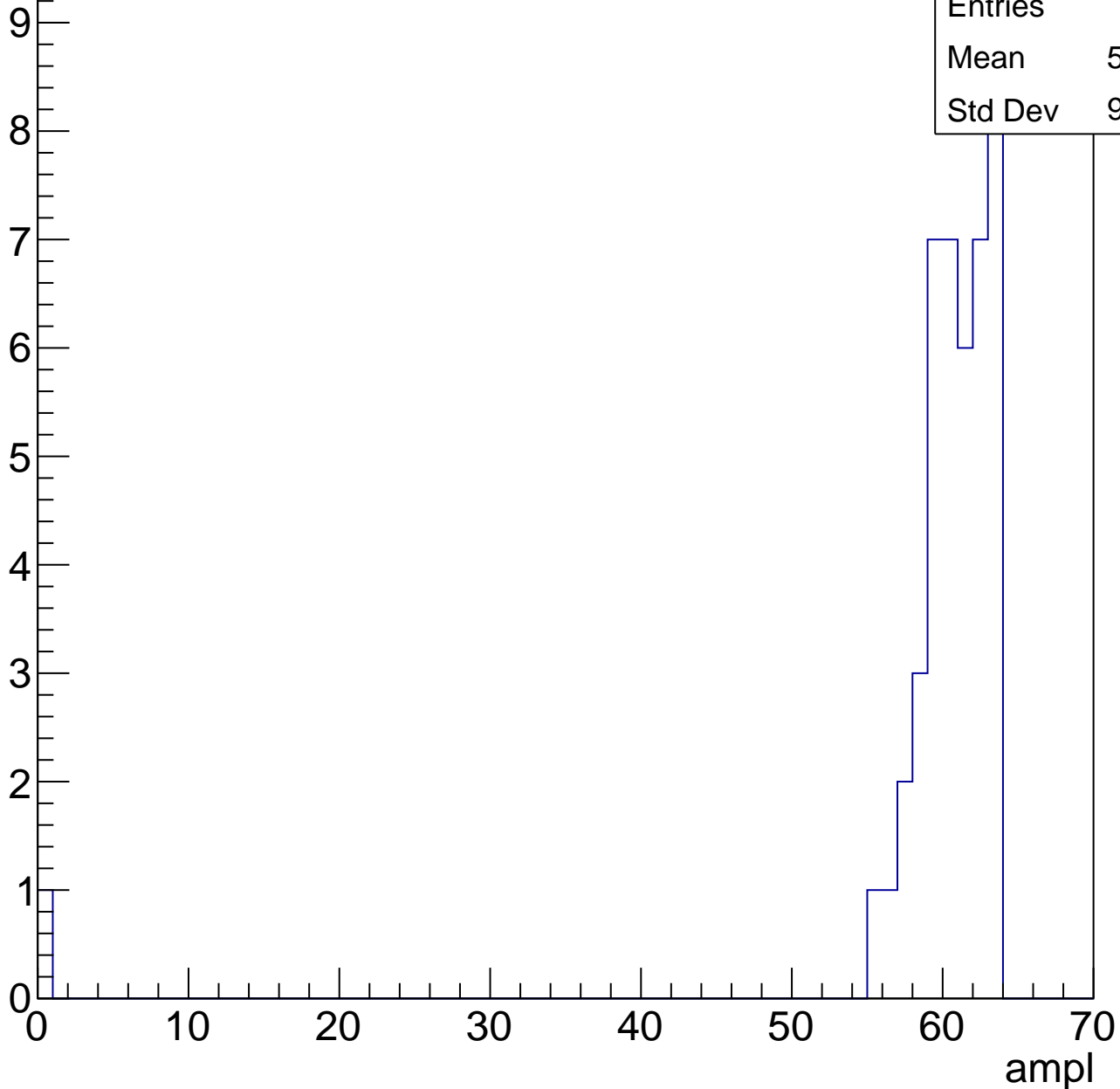


B1L100S, U11-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	59.07
Std Dev	9.238



B1L100S, U11-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch86, adc0

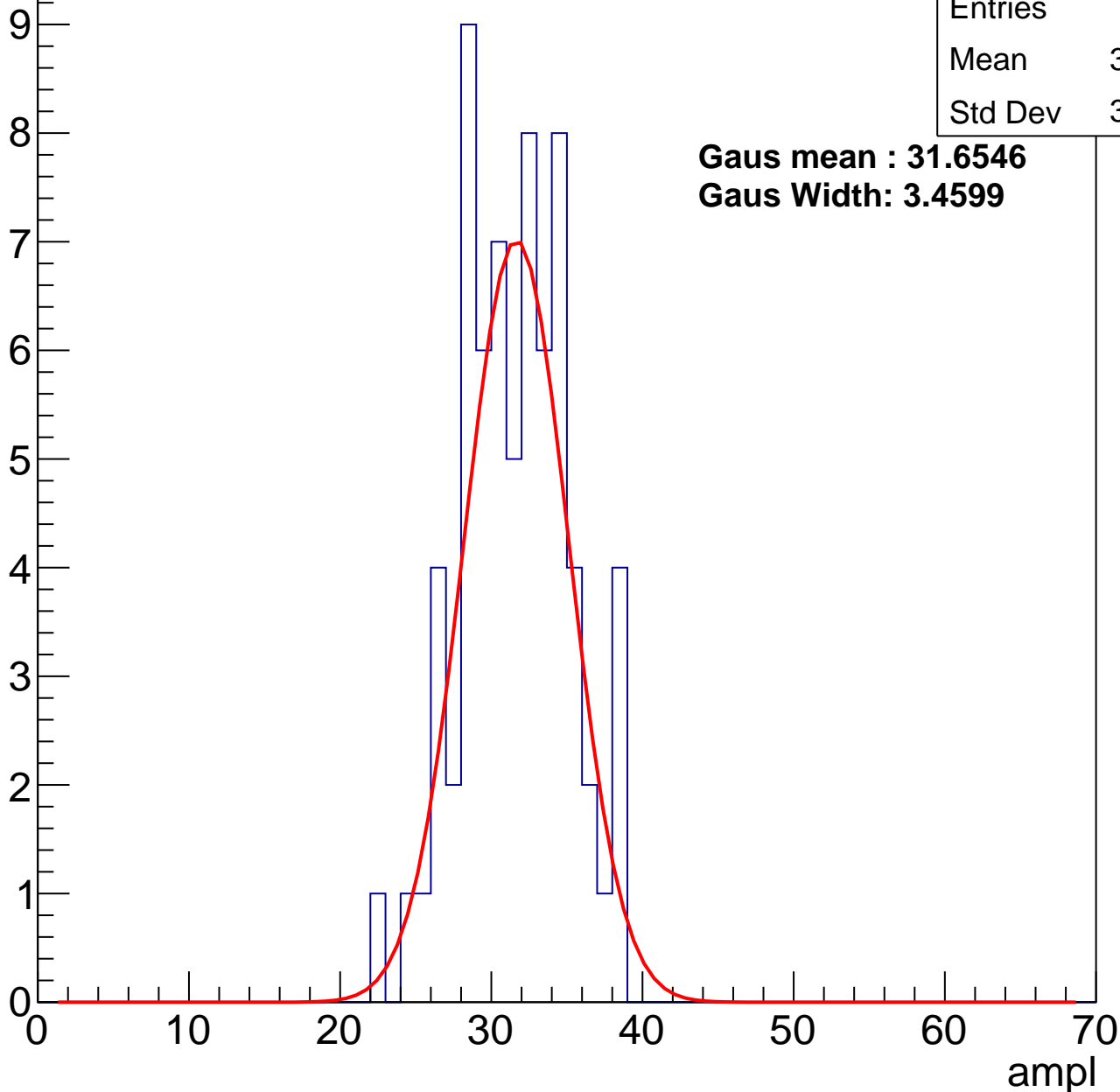
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	31.12
Std Dev	3.529

Gaus mean : 31.6546

Gaus Width: 3.4599



B1L100S, U11-ch86, adc1

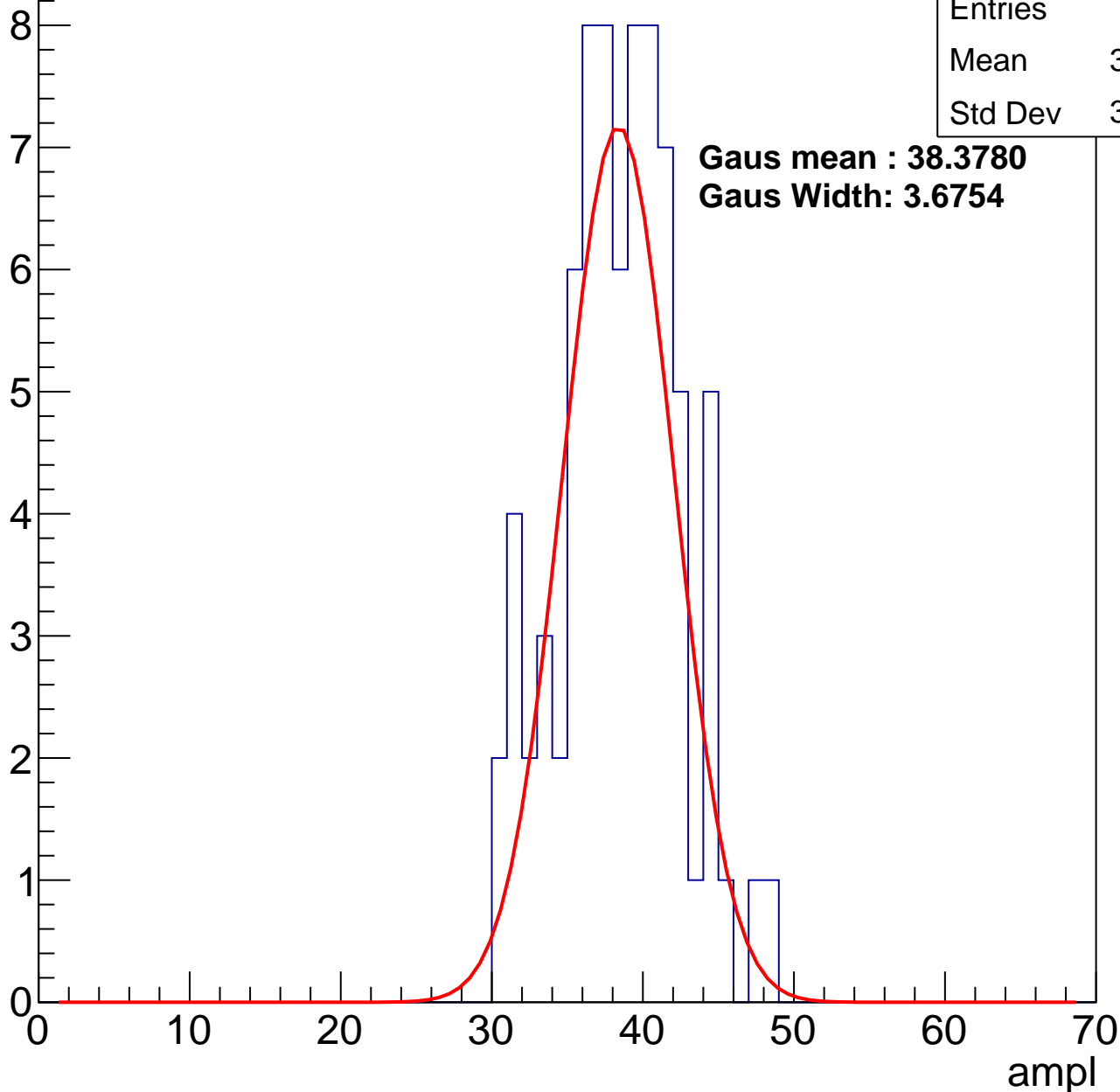
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	38.06
Std Dev	3.946

Gaus mean : 38.3780

Gaus Width: 3.6754



B1L100S, U11-ch86, adc2

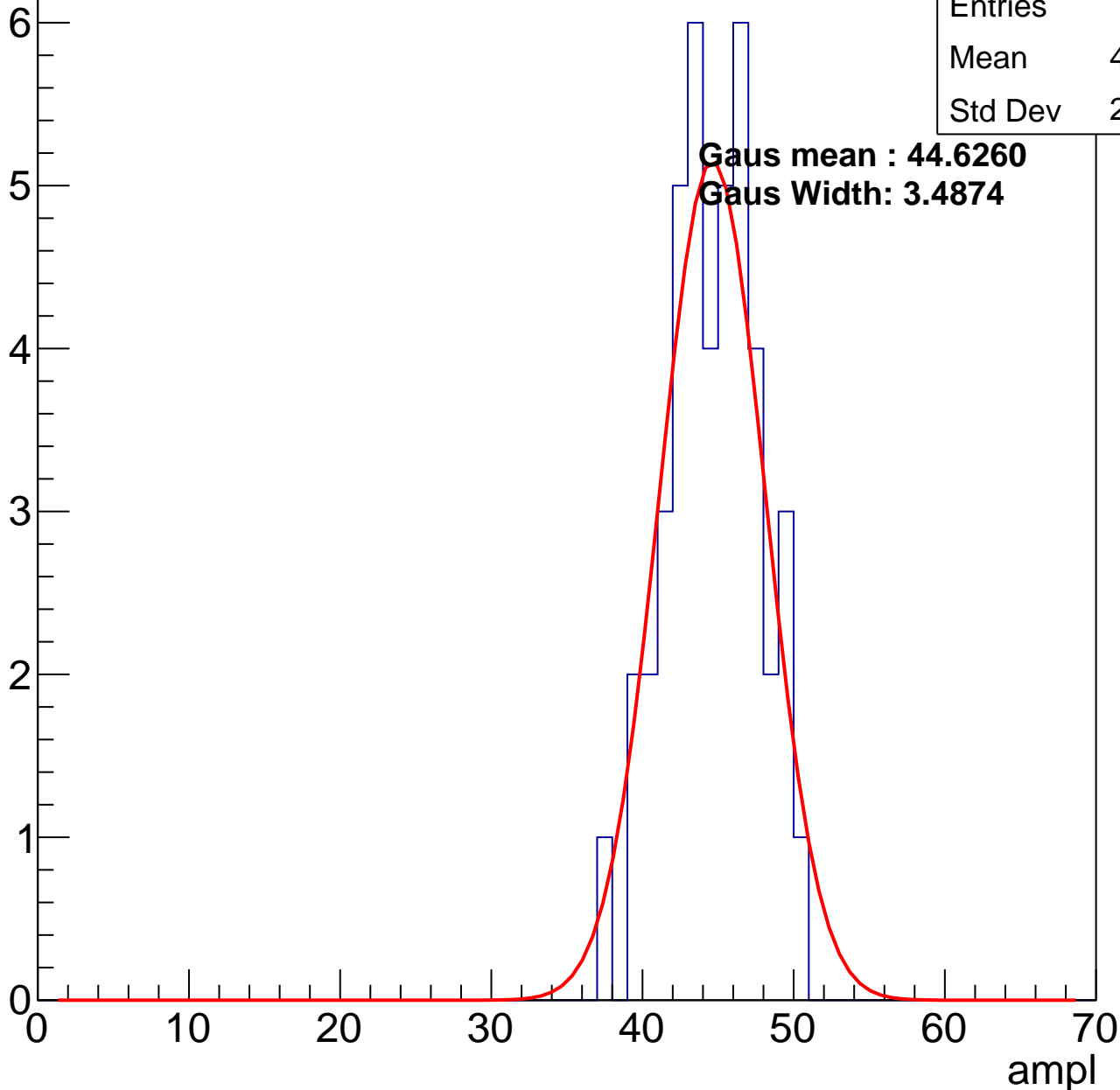
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	44.18
Std Dev	2.979

Gaus mean : 44.6260

Gaus Width: 3.4874

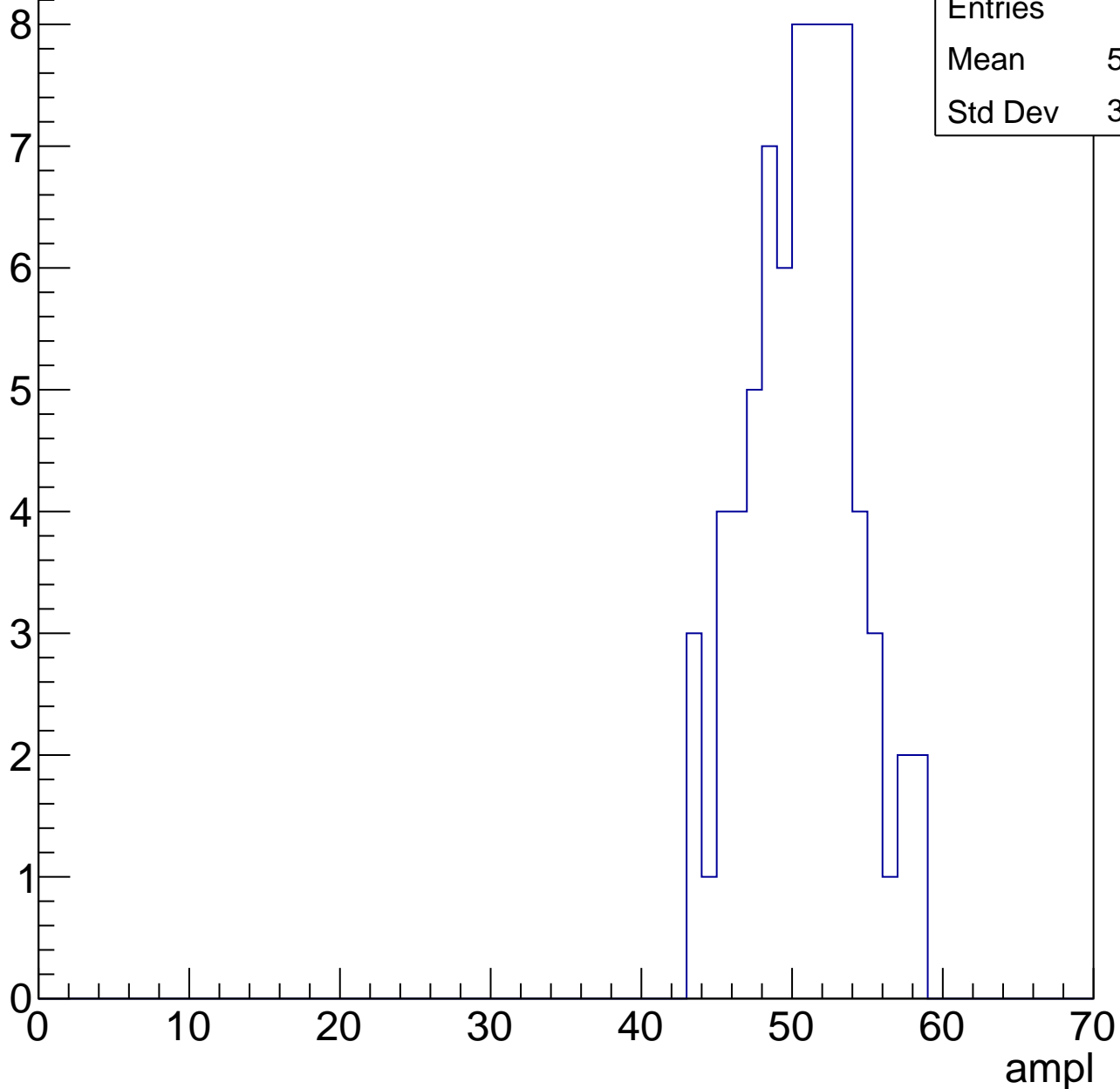


B1L100S, U11-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.23
Std Dev	3.555

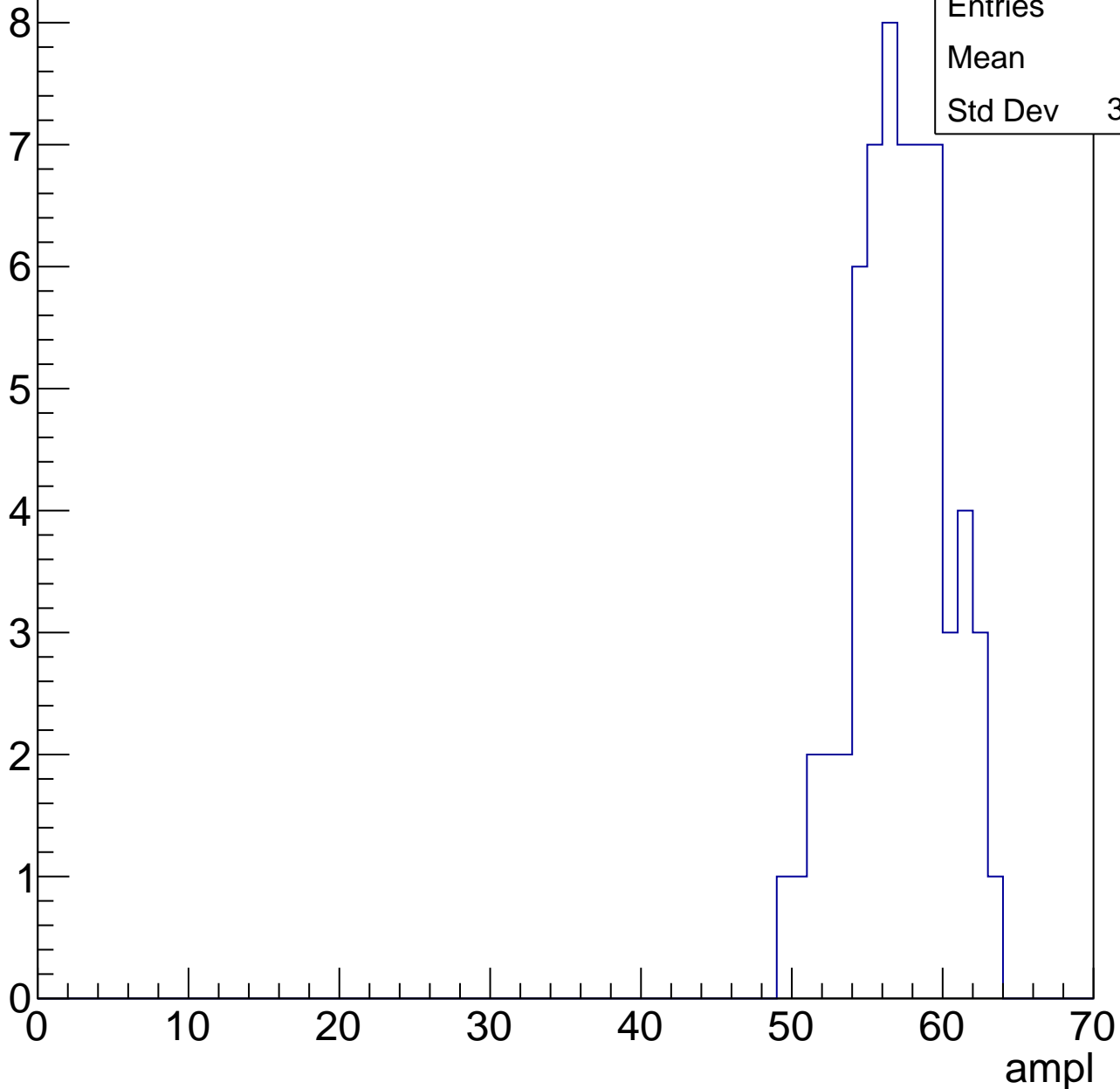


B1L100S, U11-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

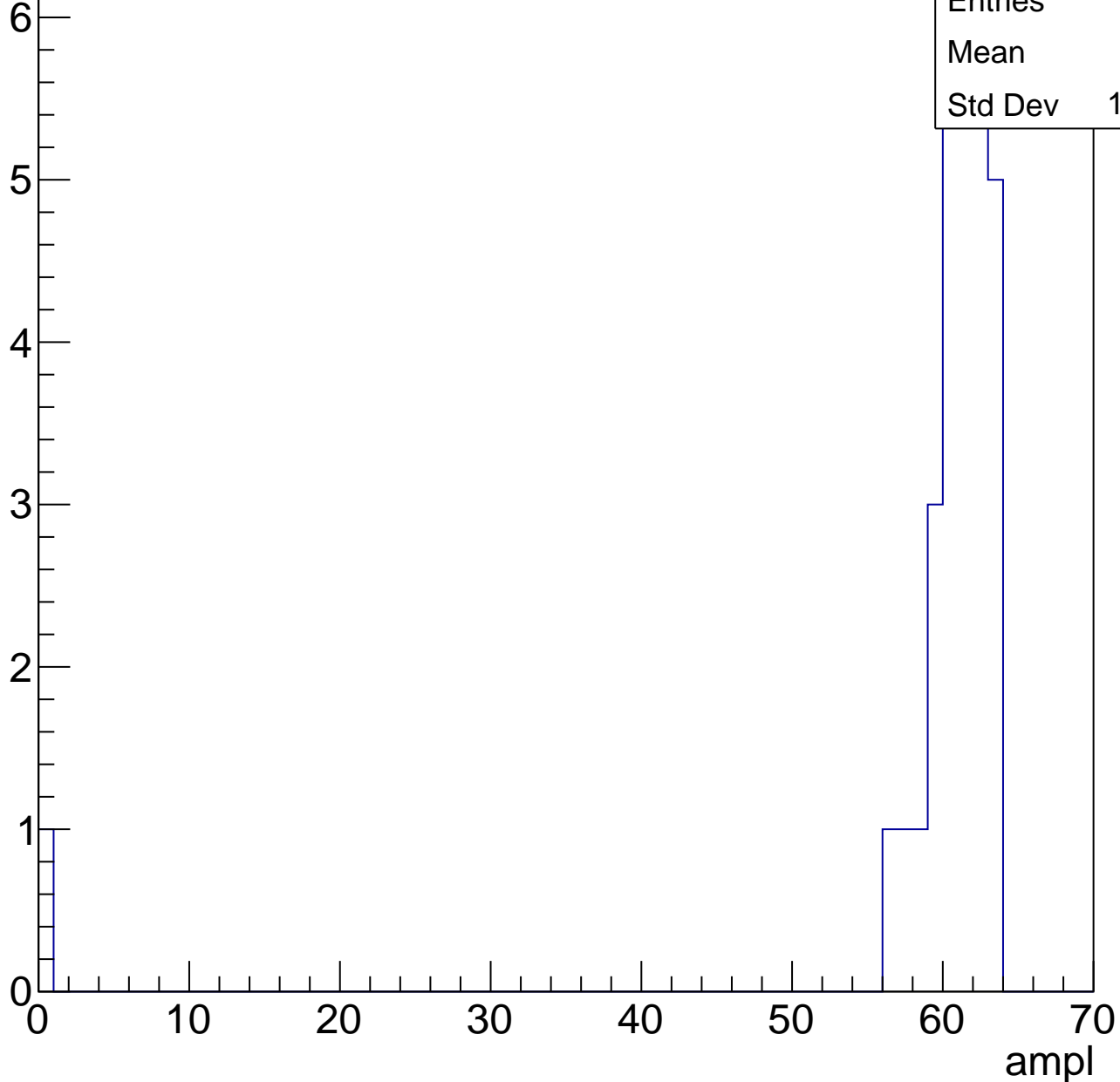
Entries	61
Mean	56.7
Std Dev	3.107



B1L100S, U11-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

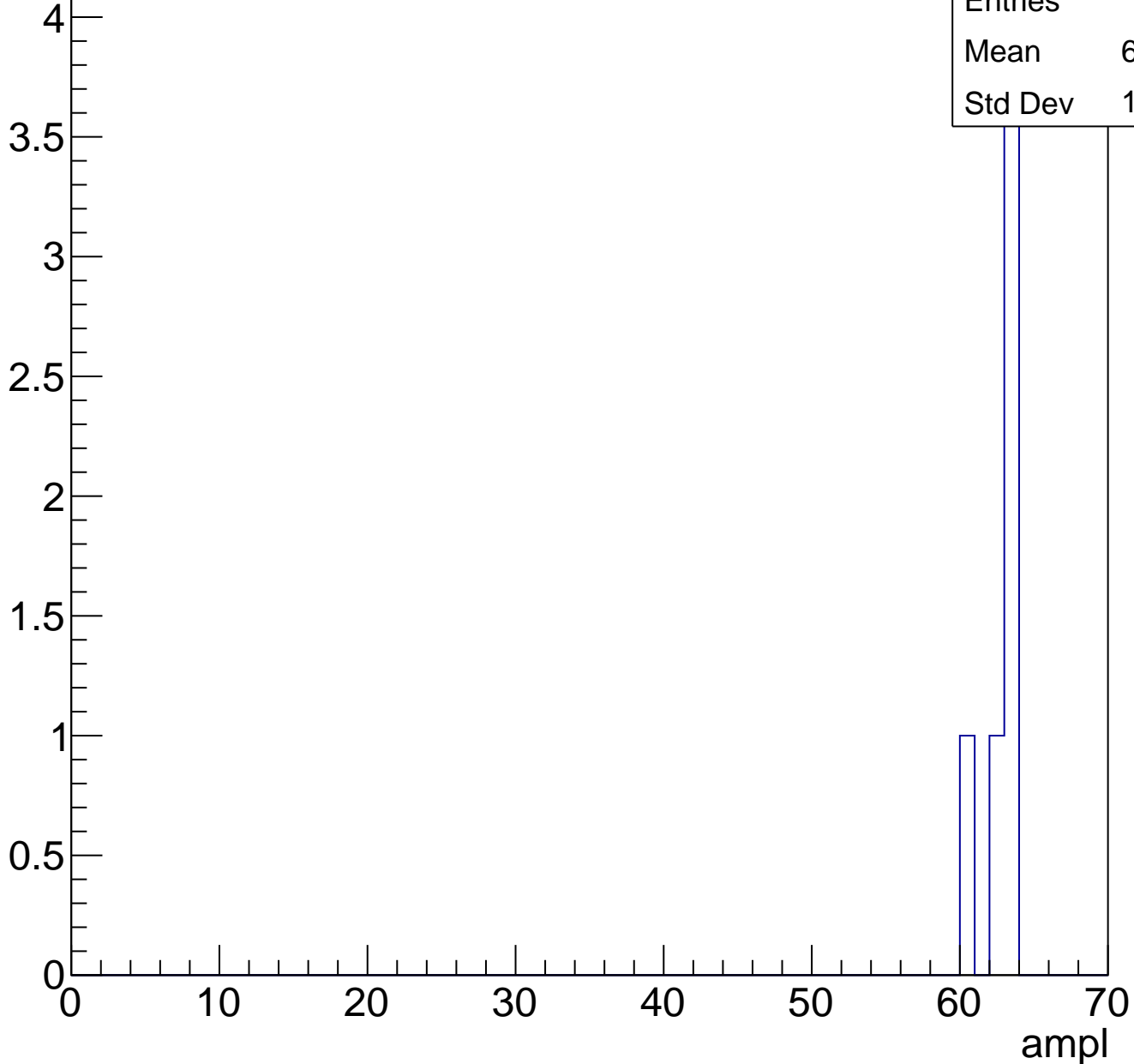
Entry



B1L100S, U11-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch87, adc0

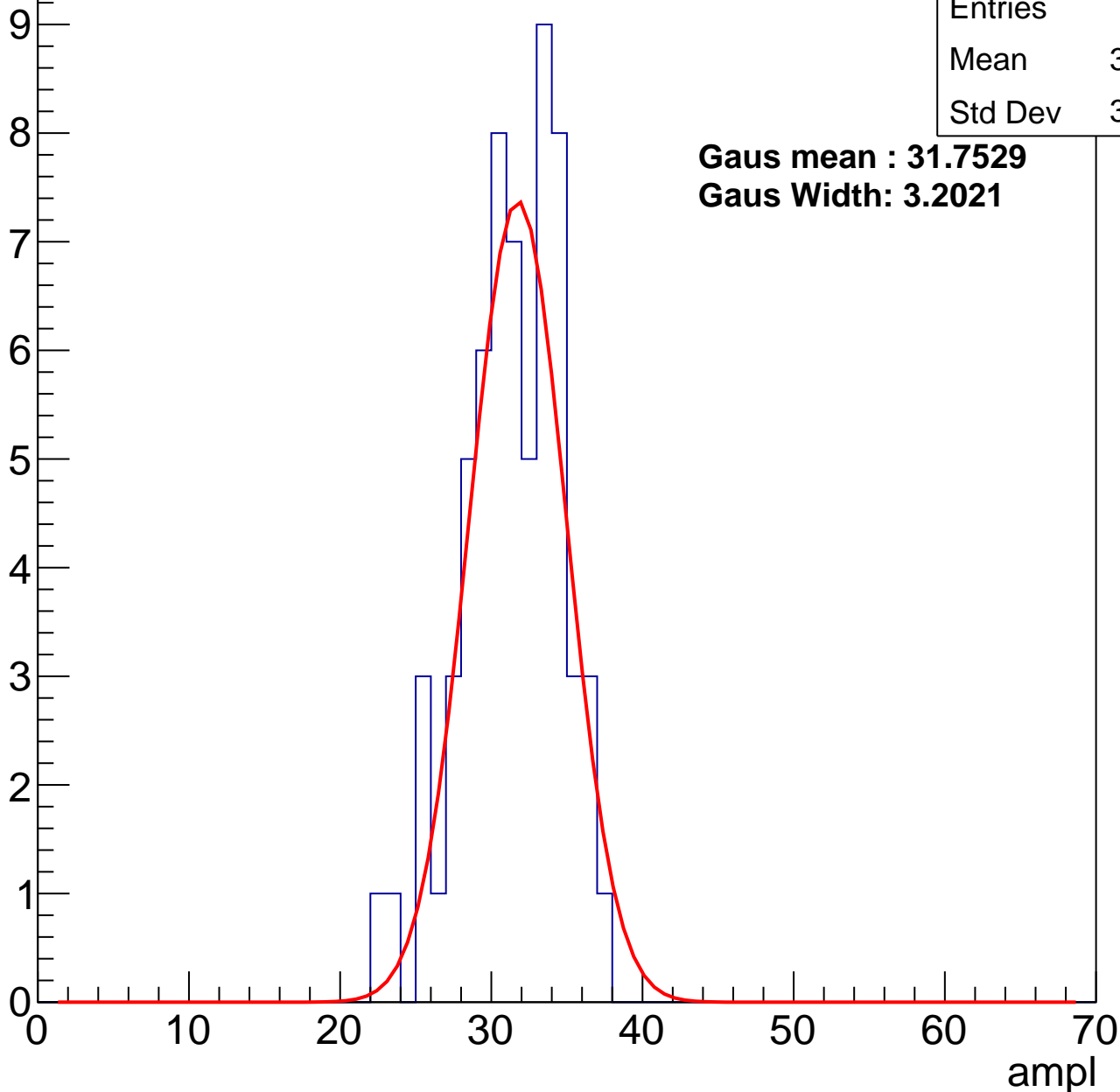
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.89
Std Dev	3.246

Gaus mean : 31.7529

Gaus Width: 3.2021



B1L100S, U11-ch87, adc1

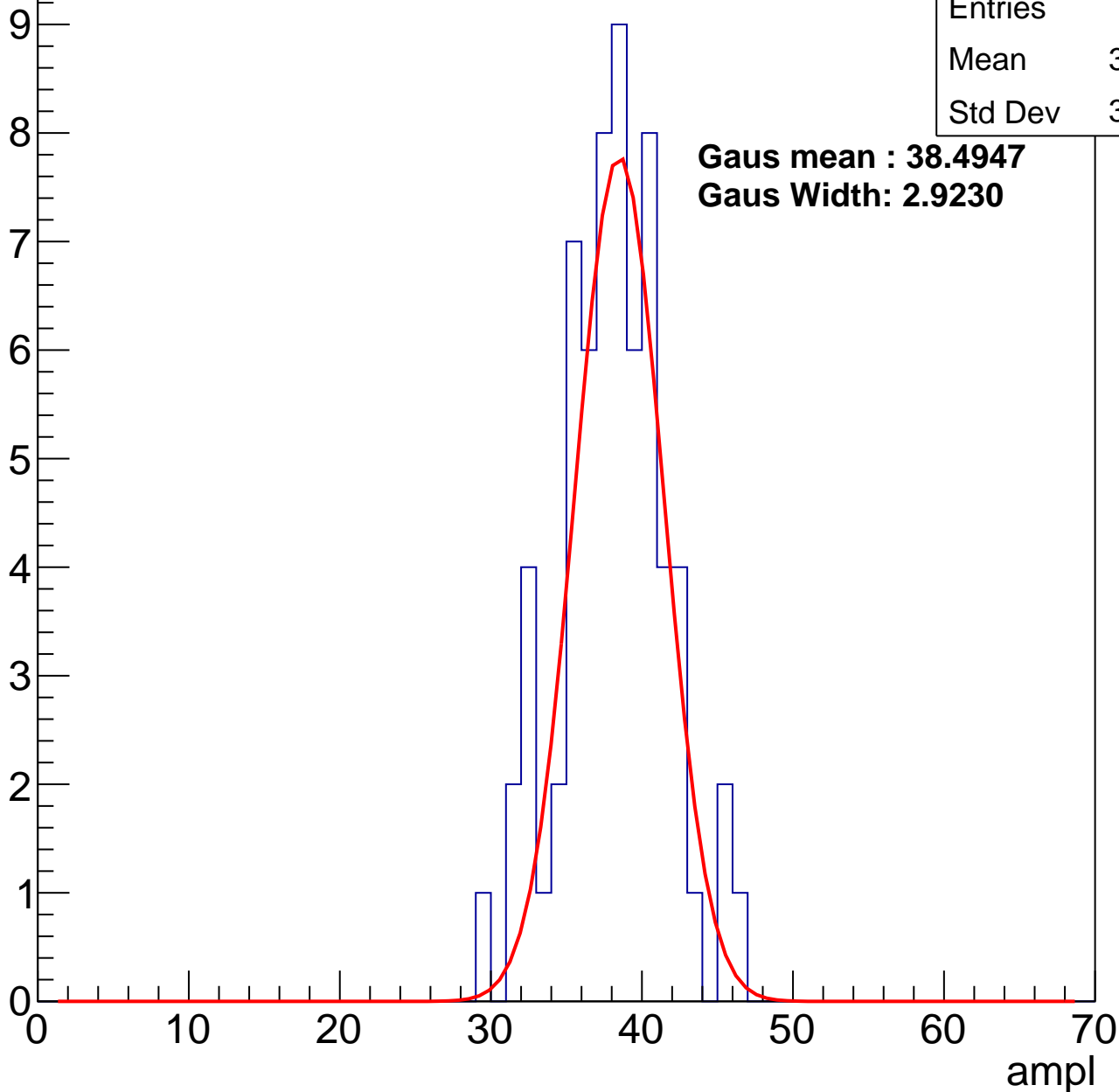
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	37.64
Std Dev	3.463

Gaus mean : 38.4947

Gaus Width: 2.9230



B1L100S, U11-ch87, adc2

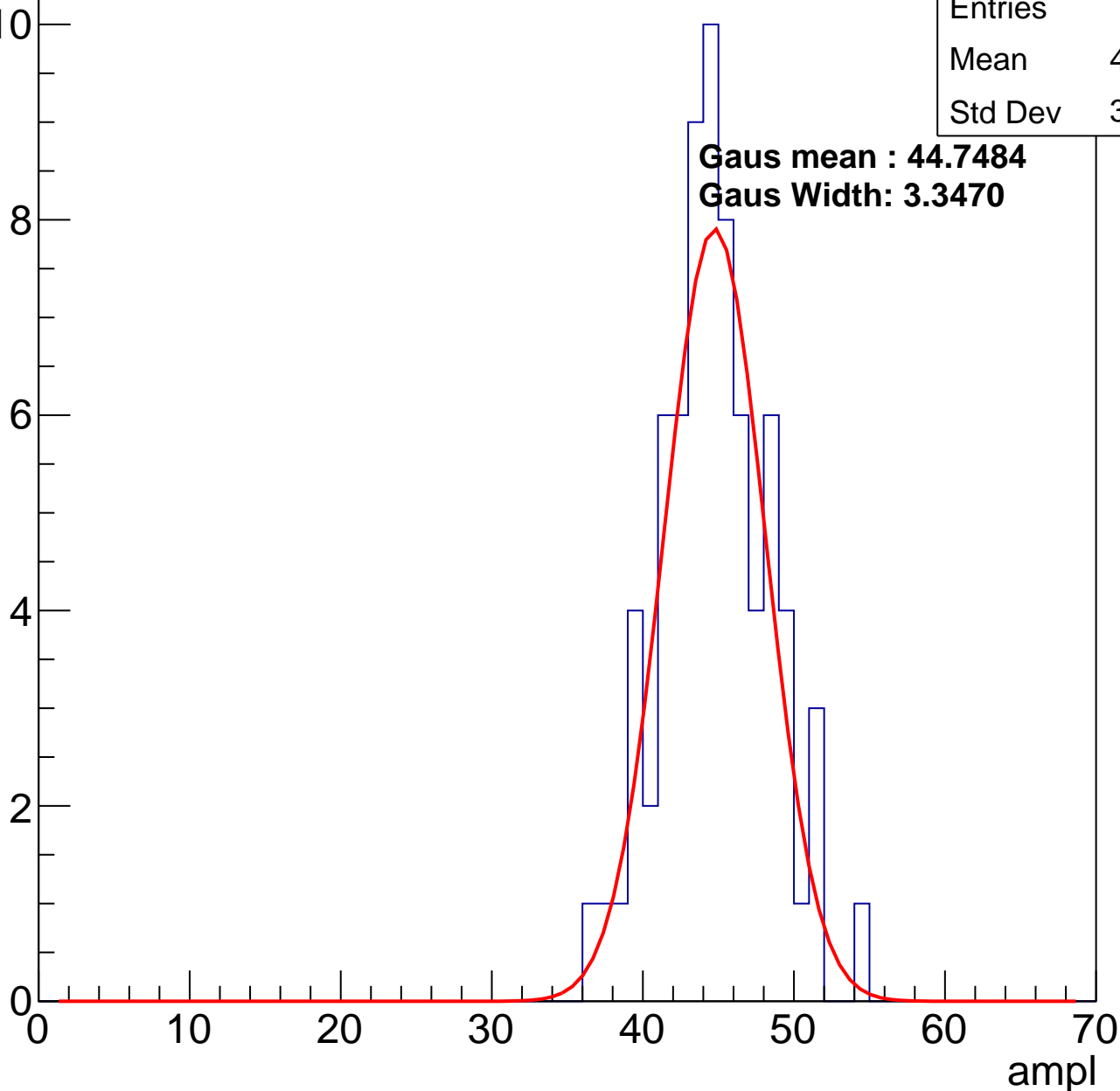
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	44.34
Std Dev	3.524

Gaus mean : 44.7484

Gaus Width: 3.3470

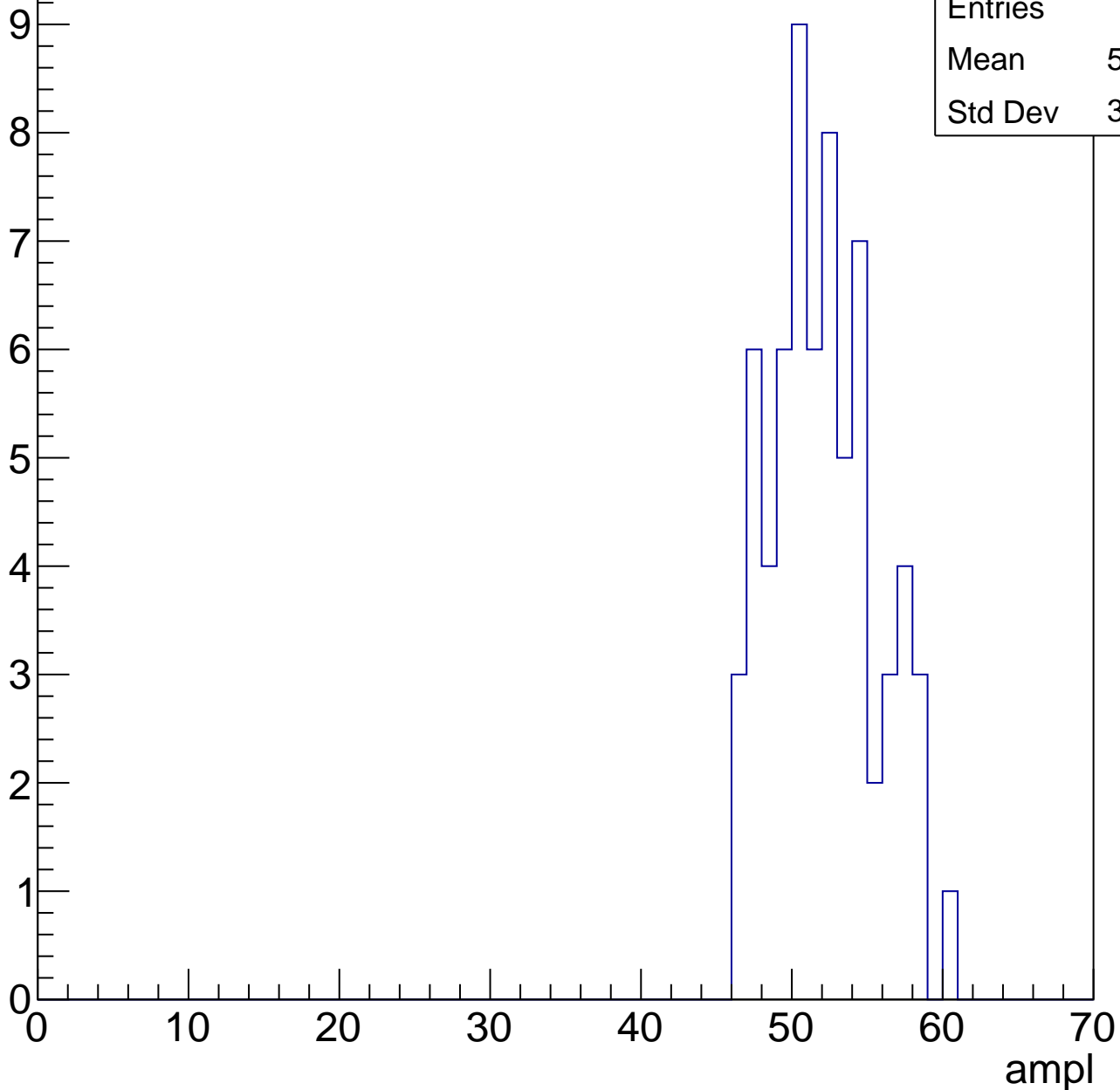


B1L100S, U11-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

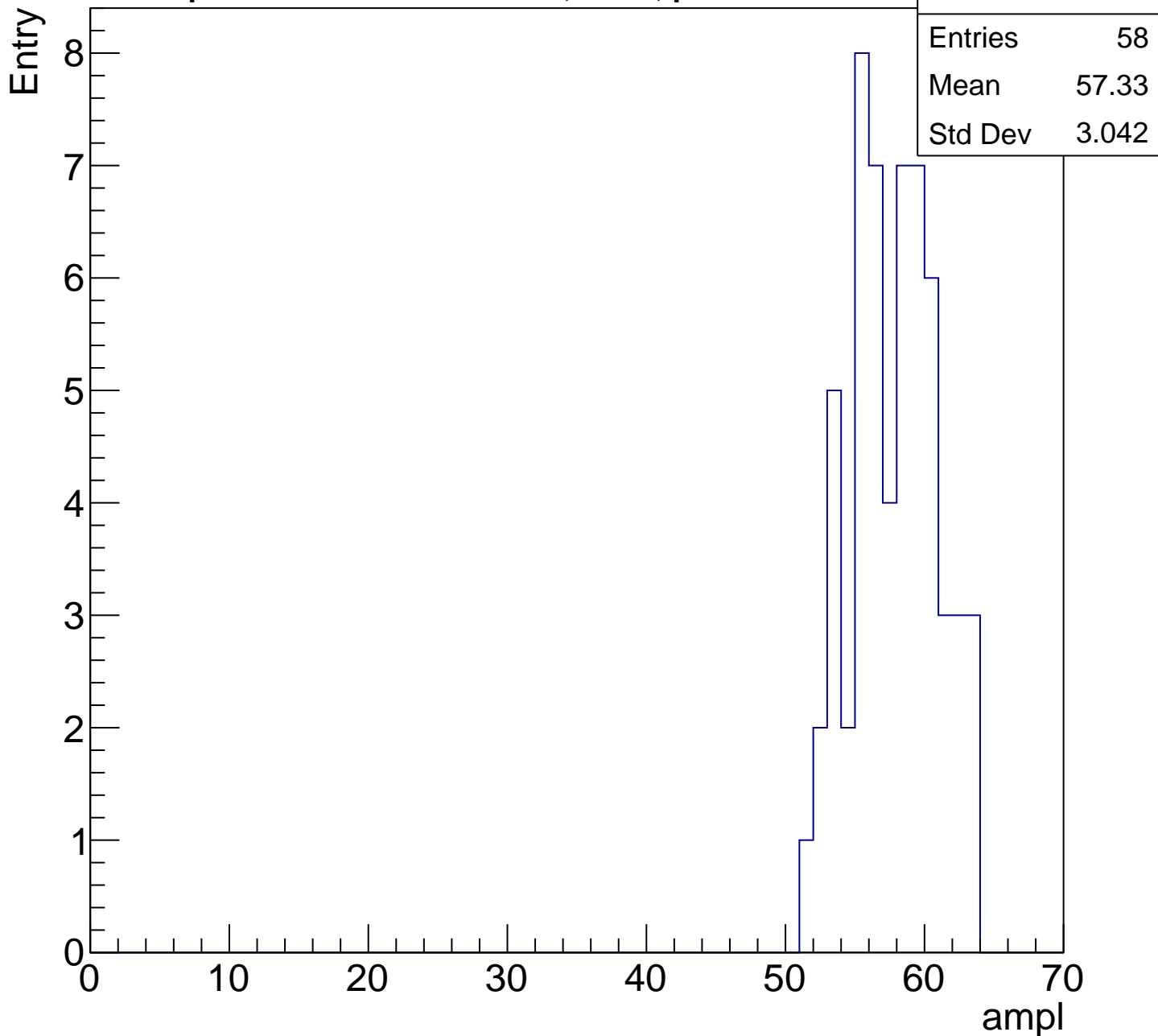
Entry

Entries	67
Mean	51.66
Std Dev	3.414



B1L100S, U11-ch87, adc4

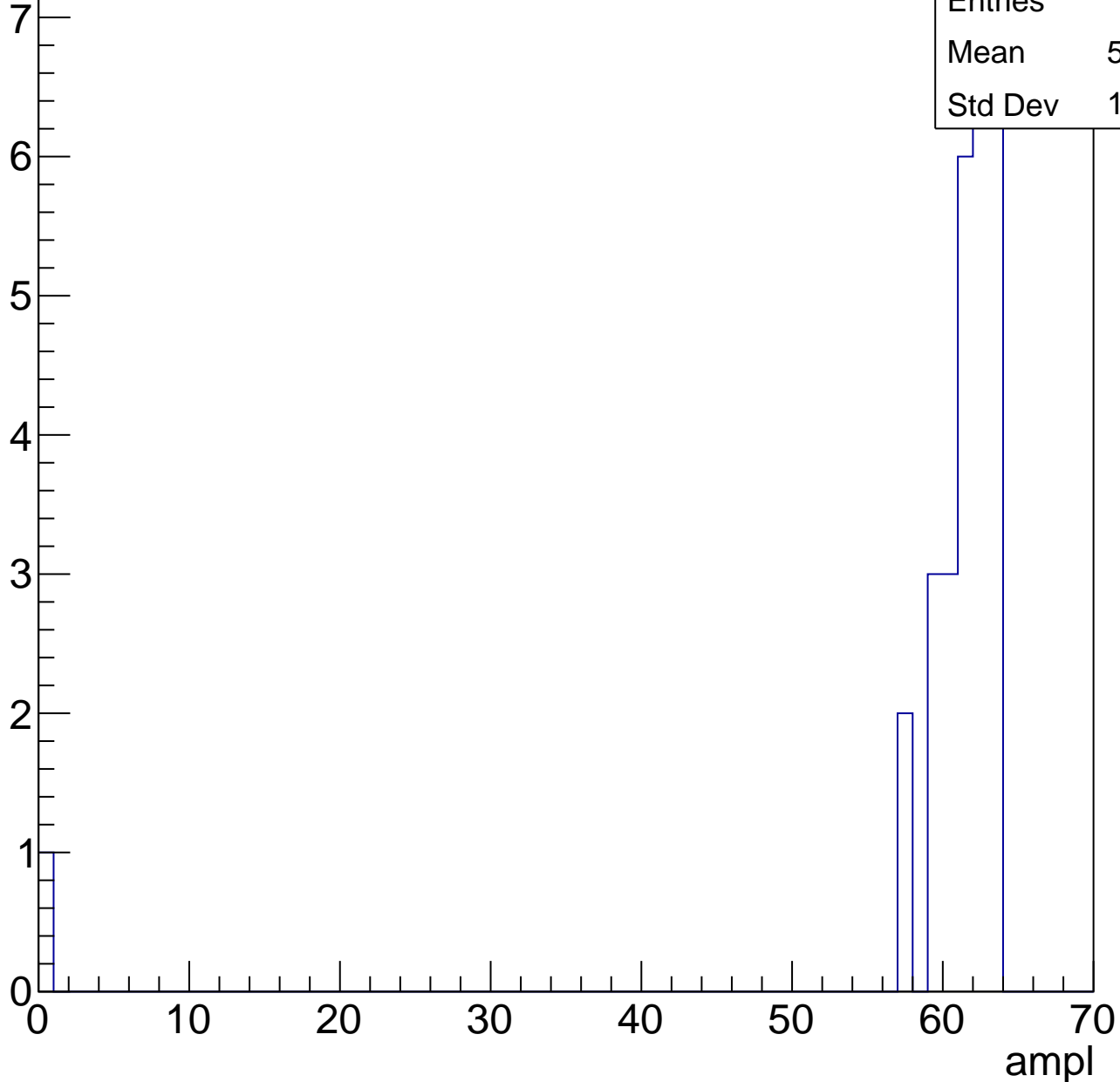
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U11-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch88, adc0

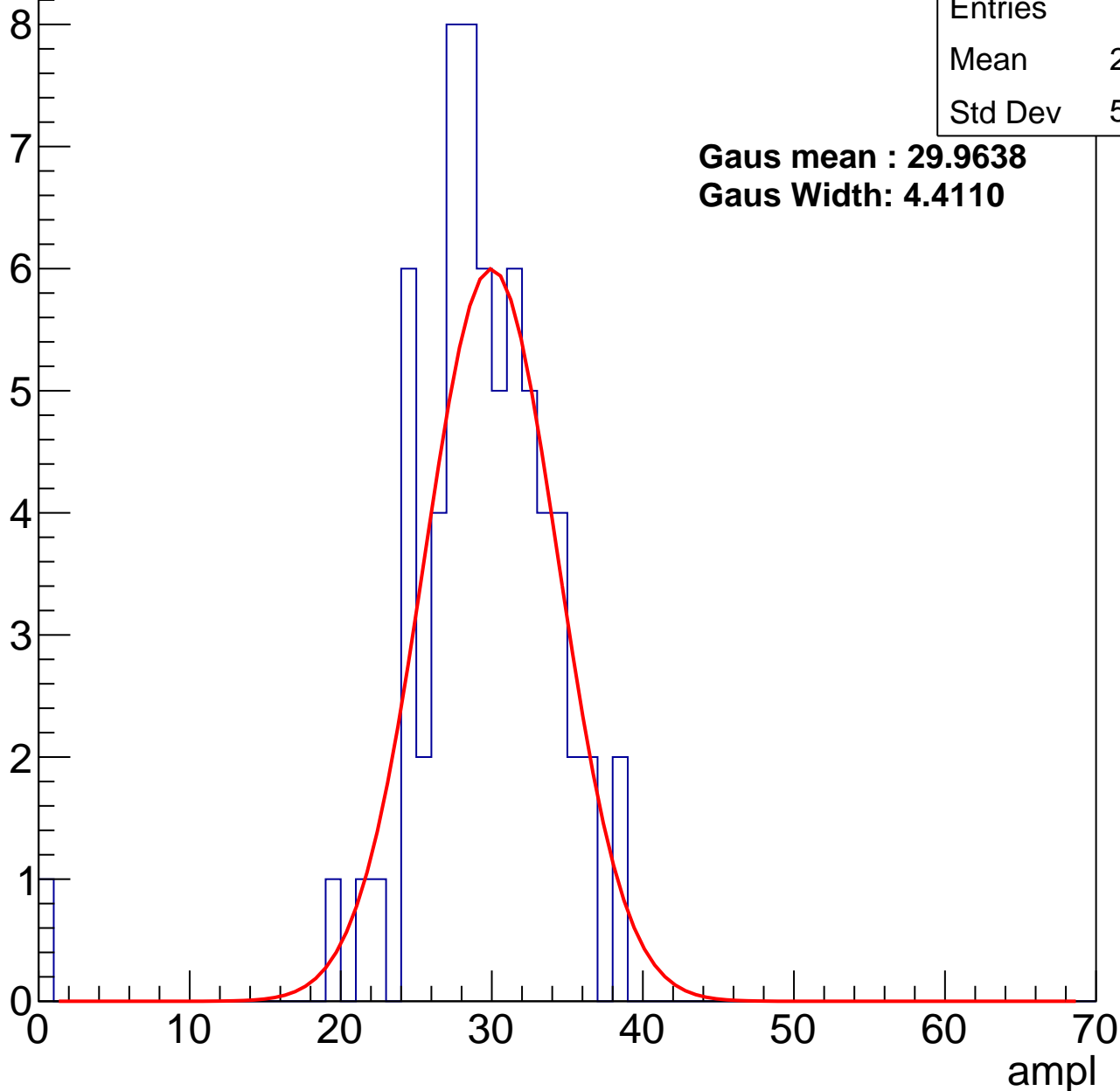
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.76
Std Dev	5.256

Gaus mean : 29.9638

Gaus Width: 4.4110



B1L100S, U11-ch88, adc1

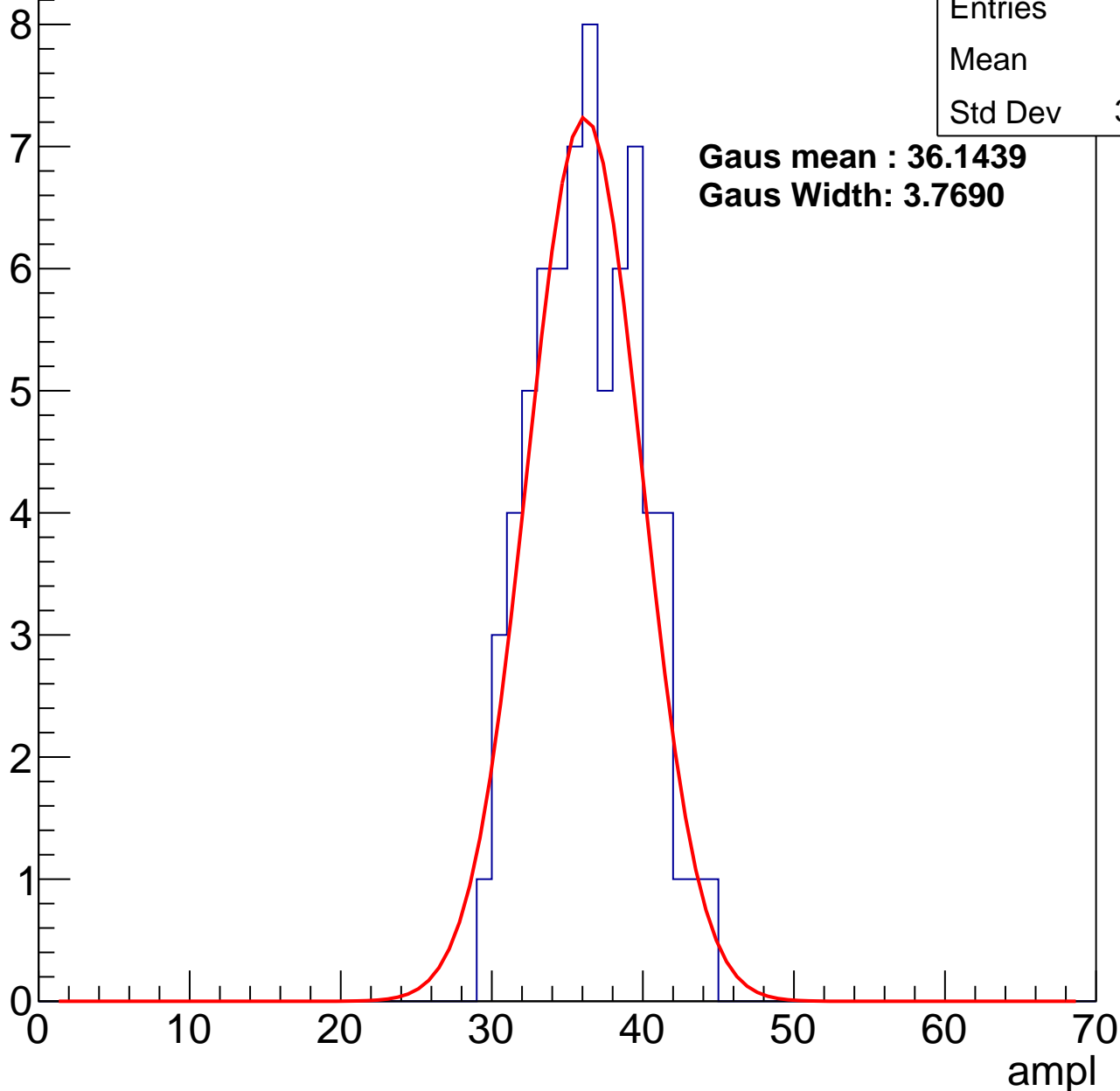
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	35.9
Std Dev	3.461

Gaus mean : 36.1439

Gaus Width: 3.7690



B1L100S, U11-ch88, adc2

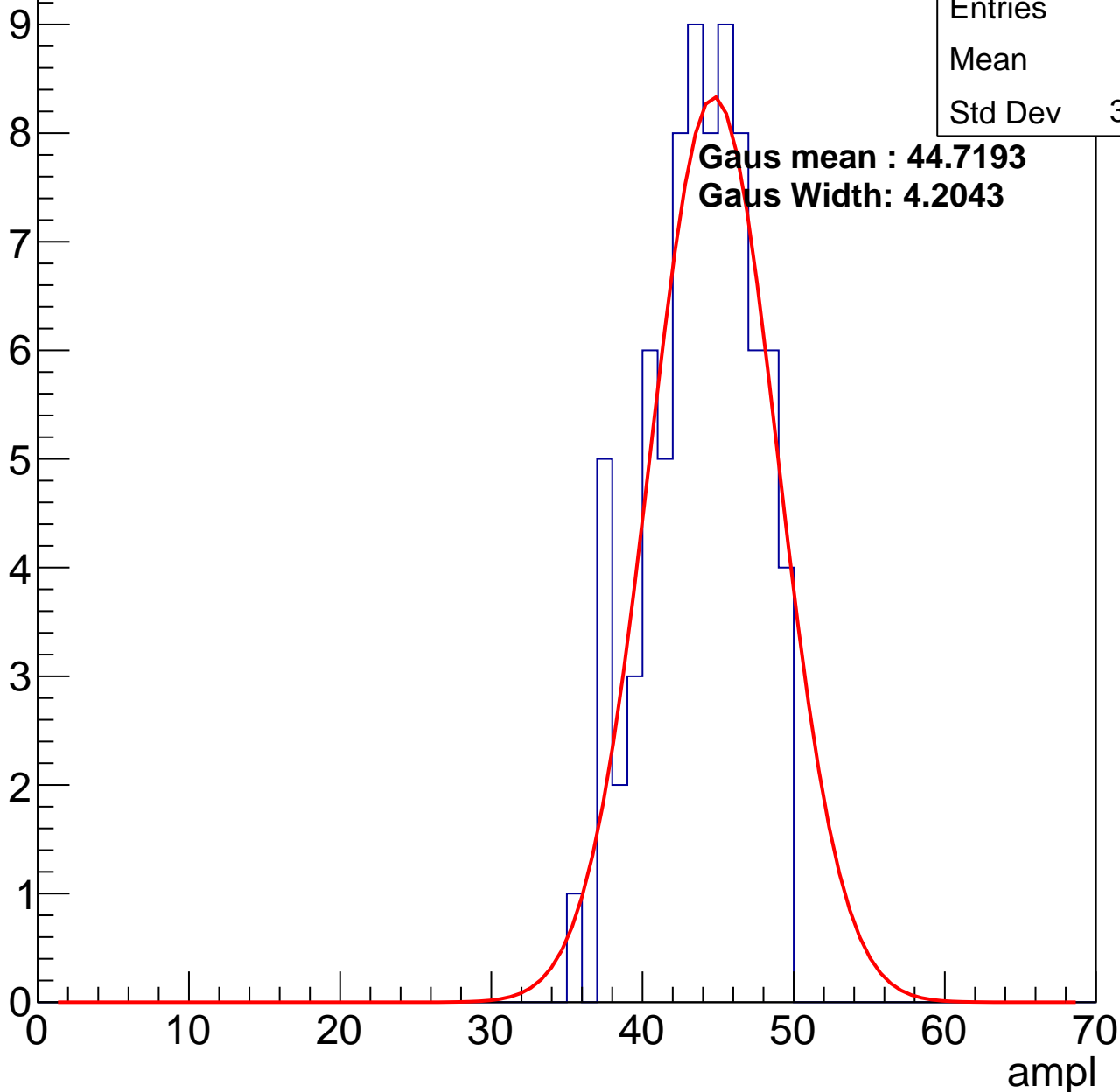
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	43.4
Std Dev	3.386

Gaus mean : 44.7193

Gaus Width: 4.2043

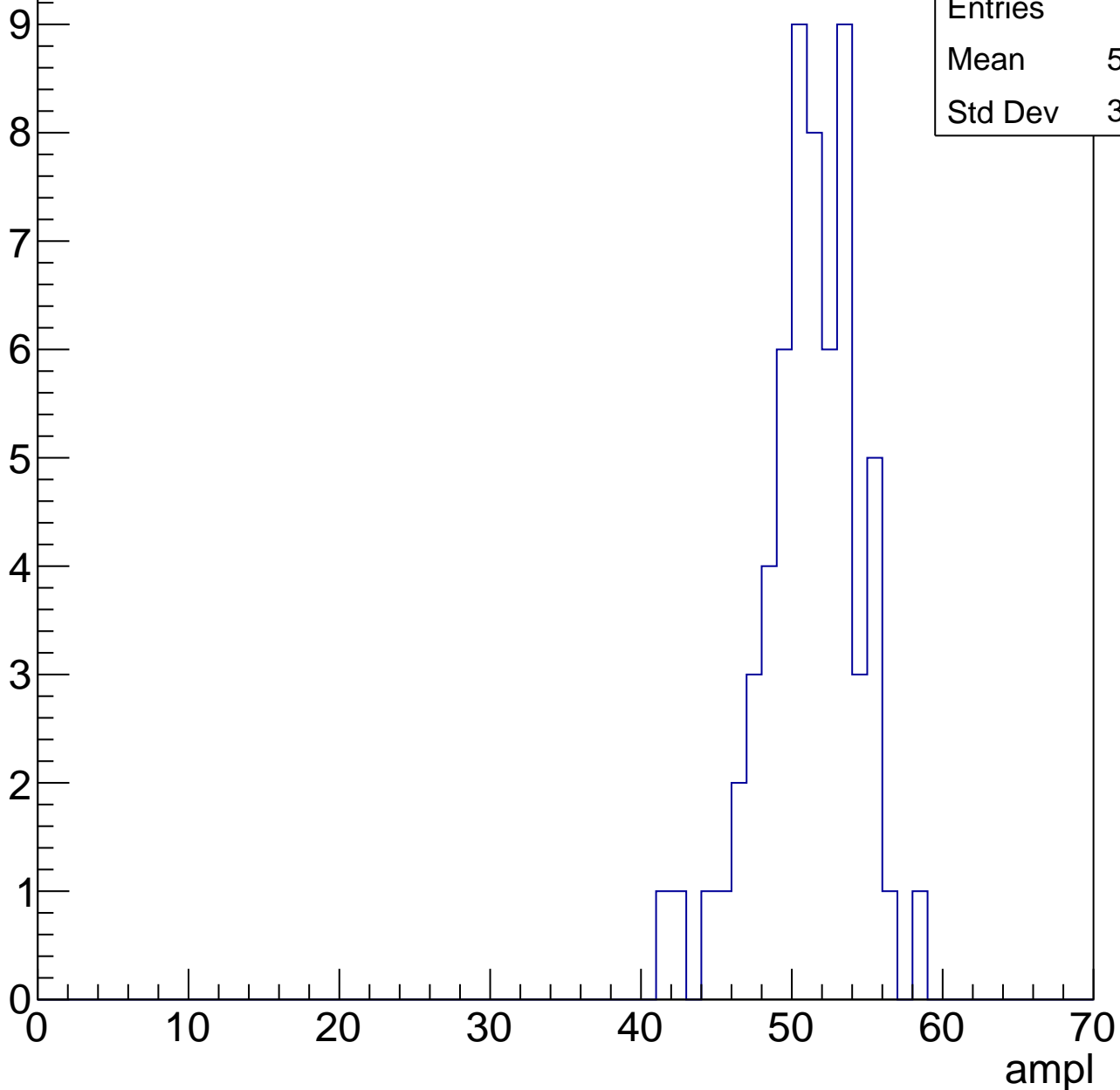


B1L100S, U11-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

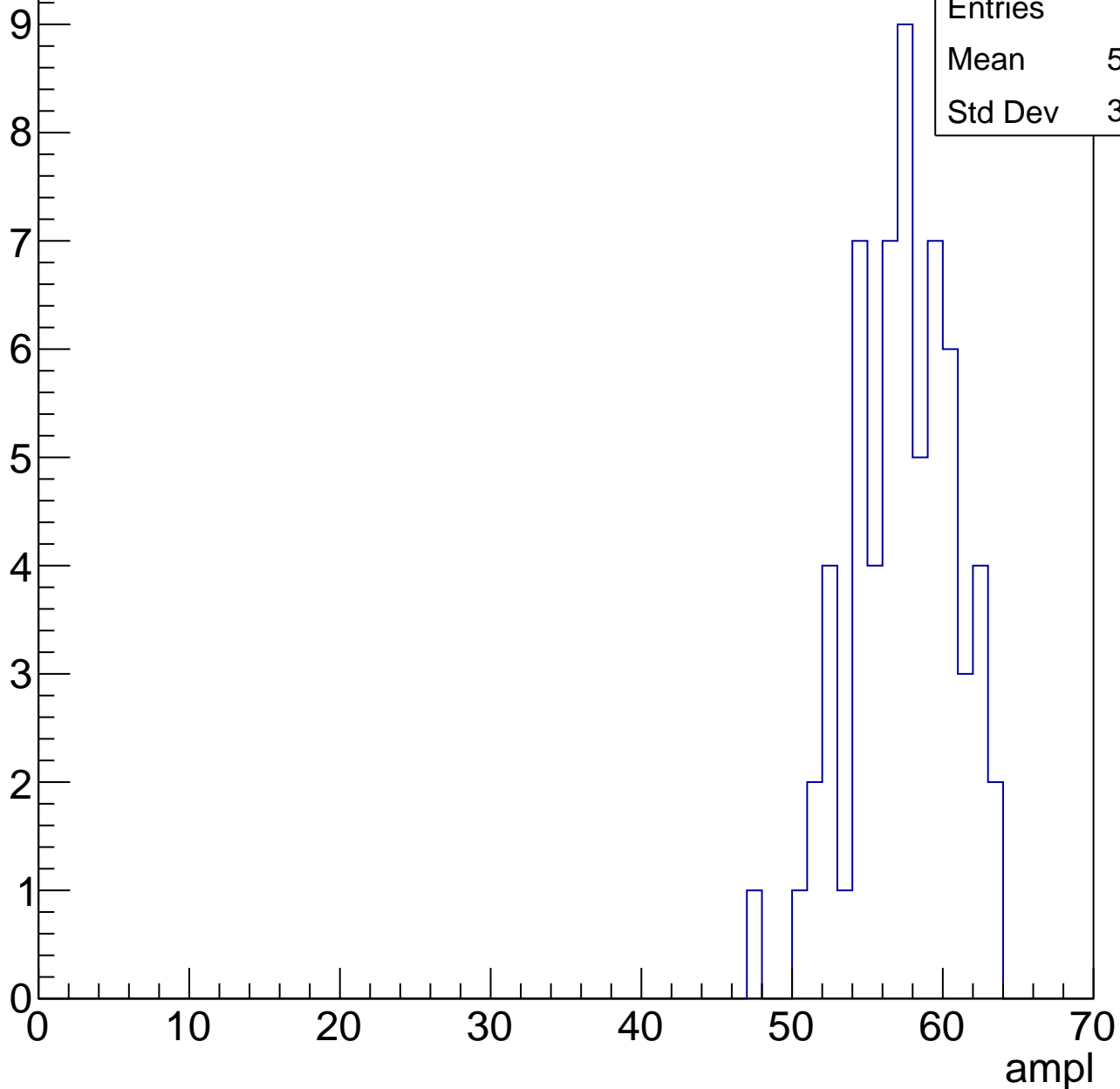
Entries	61
Mean	50.64
Std Dev	3.259



B1L100S, U11-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch88, adc5

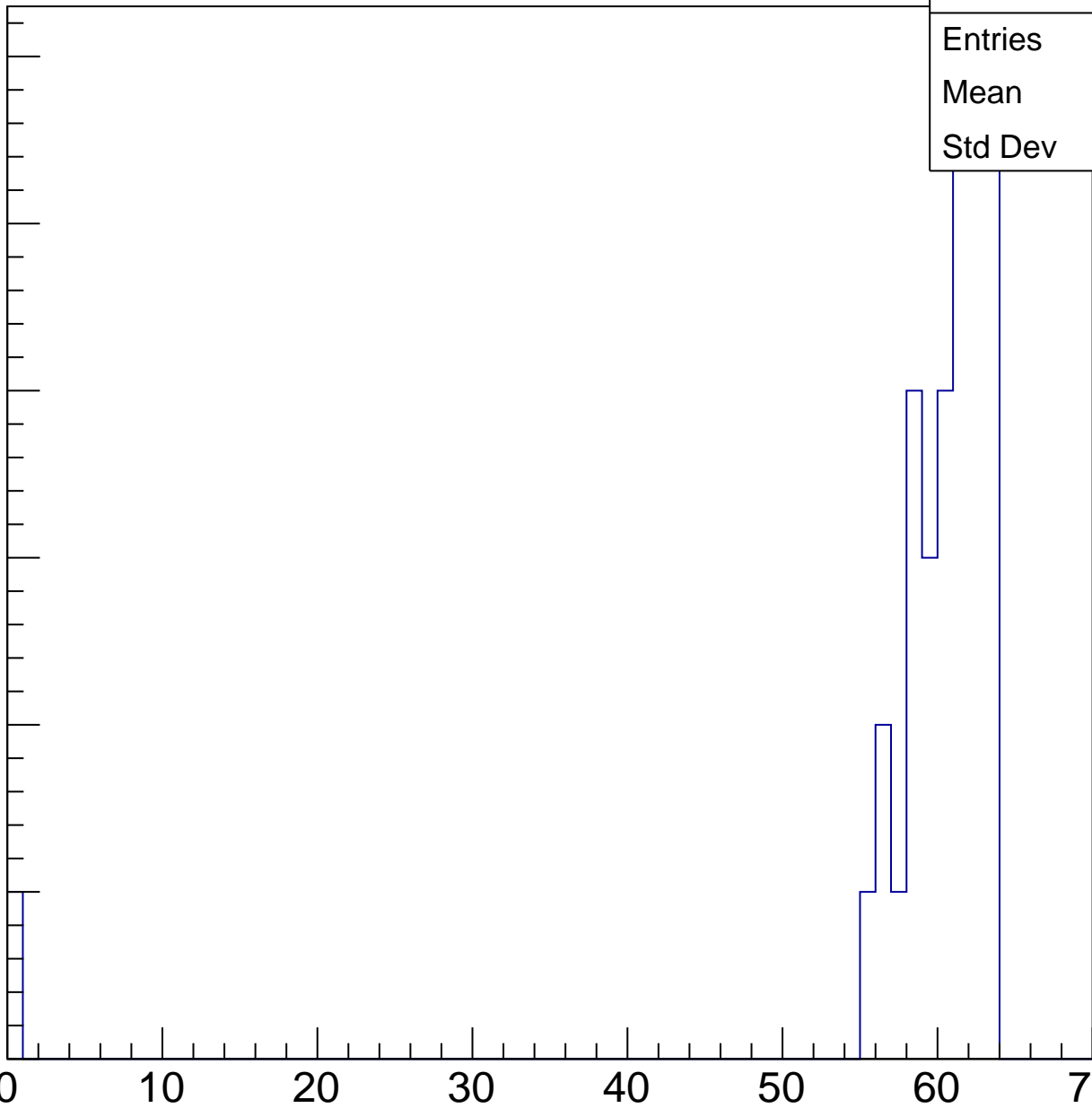
calib_packv5_042523_0143.root, FC#4, port A2

Entry

6
5
4
3
2
1
0

Entries	34
Mean	58.5
Std Dev	10.42

ampl



B1L100S, U11-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch89, adc0

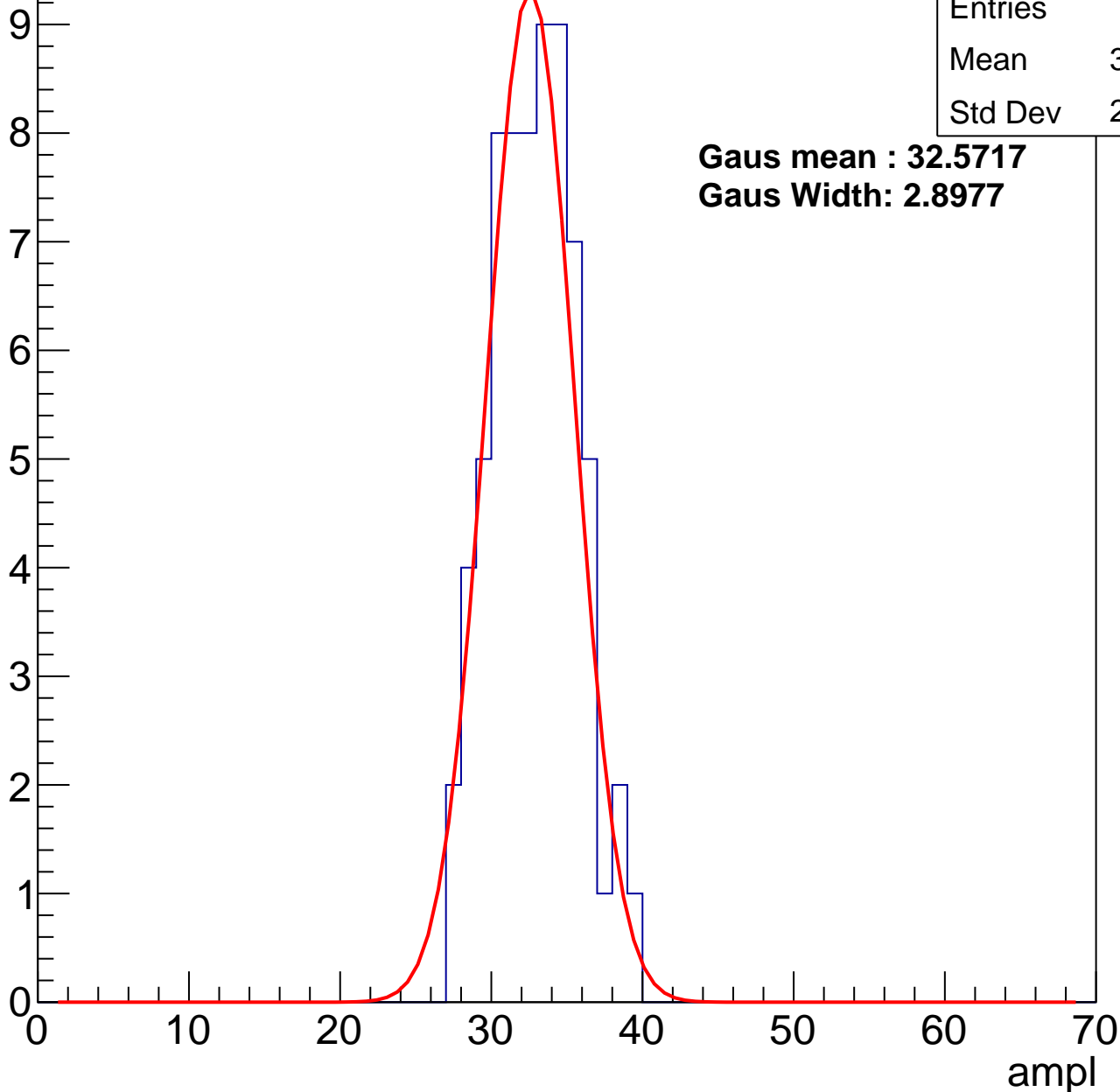
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	32.39
Std Dev	2.752

Gaus mean : 32.5717

Gaus Width: 2.8977



B1L100S, U11-ch89, adc1

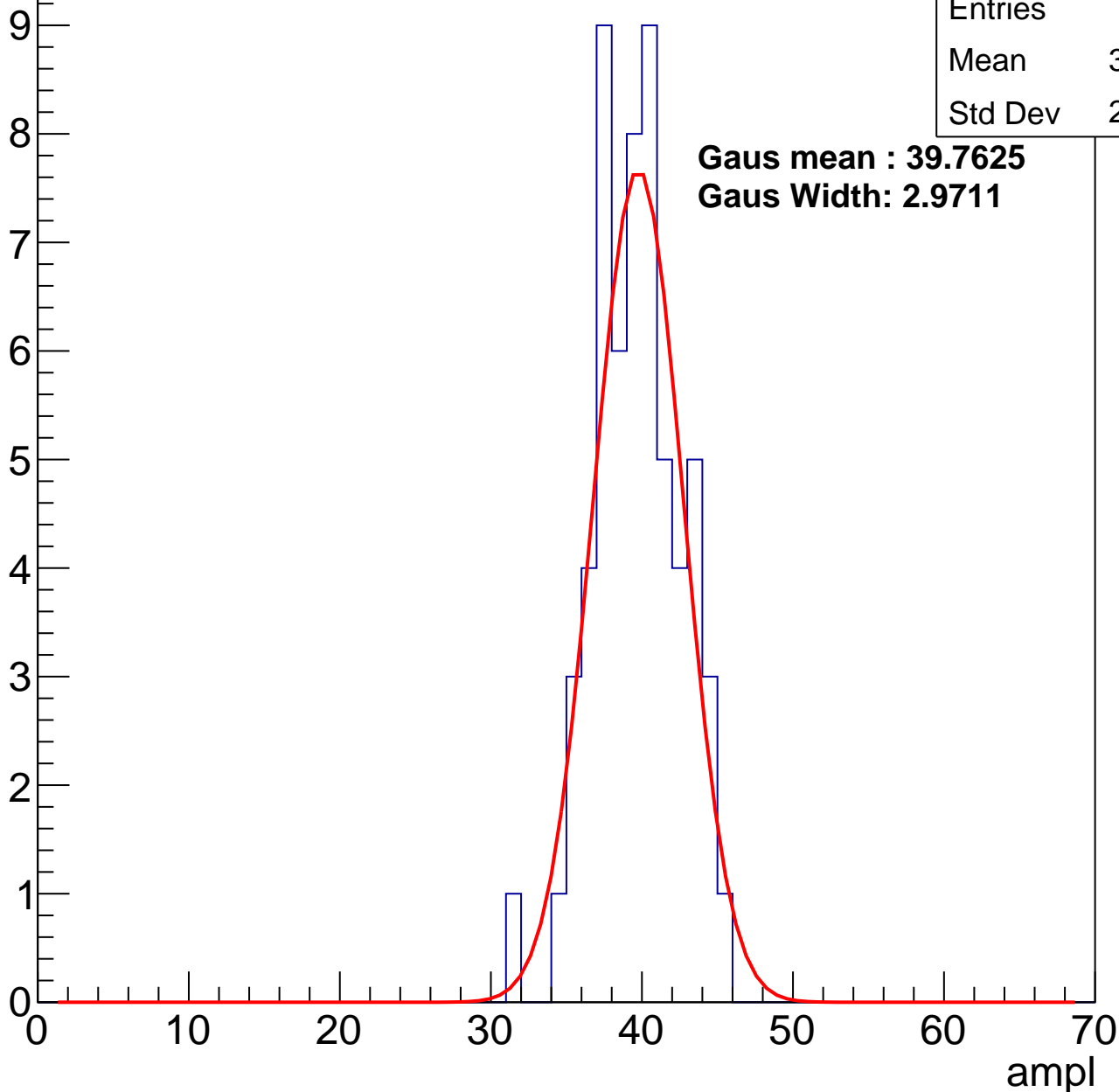
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	39.19
Std Dev	2.819

Gaus mean : 39.7625

Gaus Width: 2.9711



B1L100S, U11-ch89, adc2

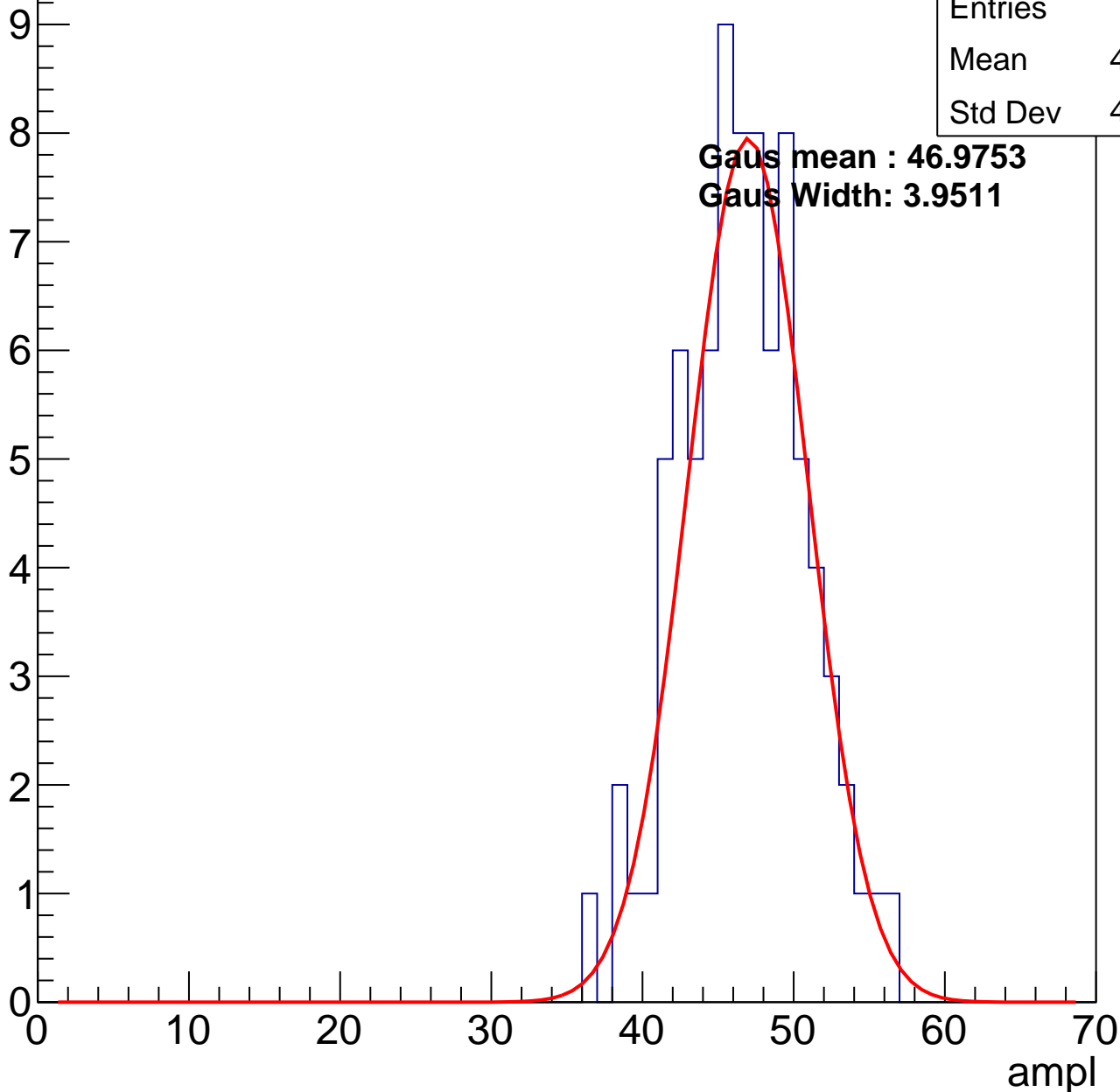
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	46.23
Std Dev	4.022

Gaus mean : 46.9753

Gaus Width: 3.9511

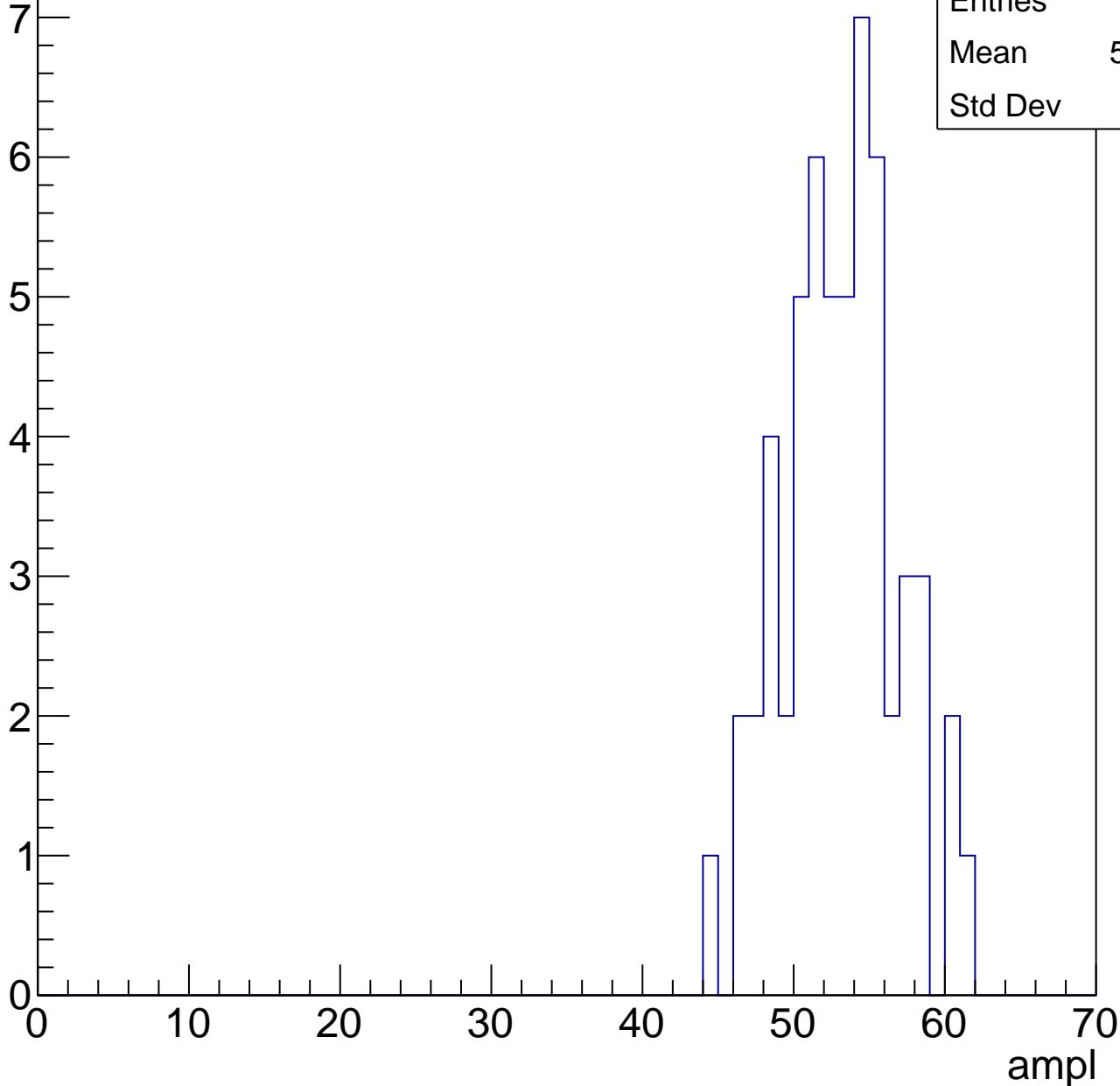


B1L100S, U11-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

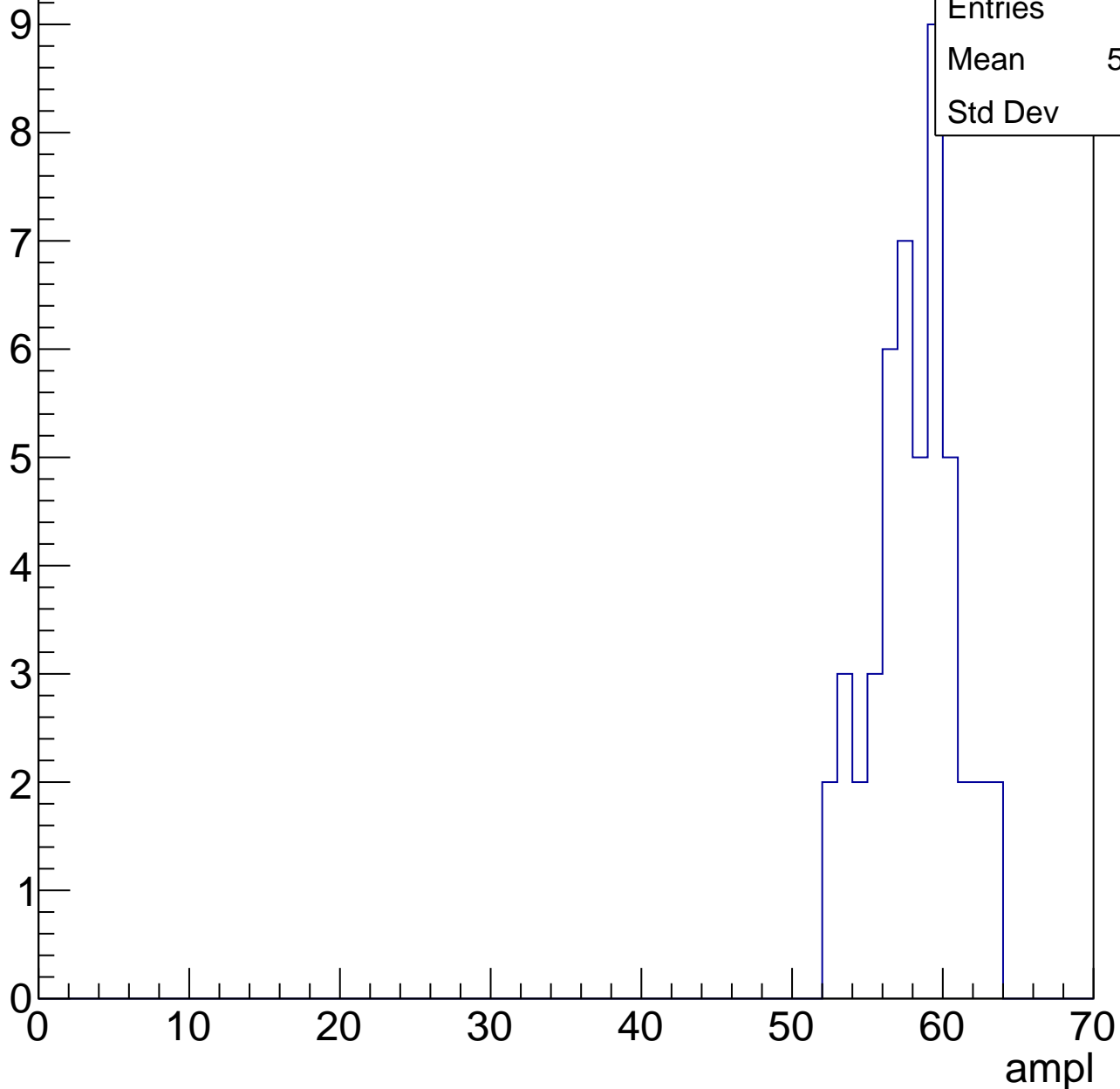
Entries	56
Mean	52.62
Std Dev	3.74



B1L100S, U11-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

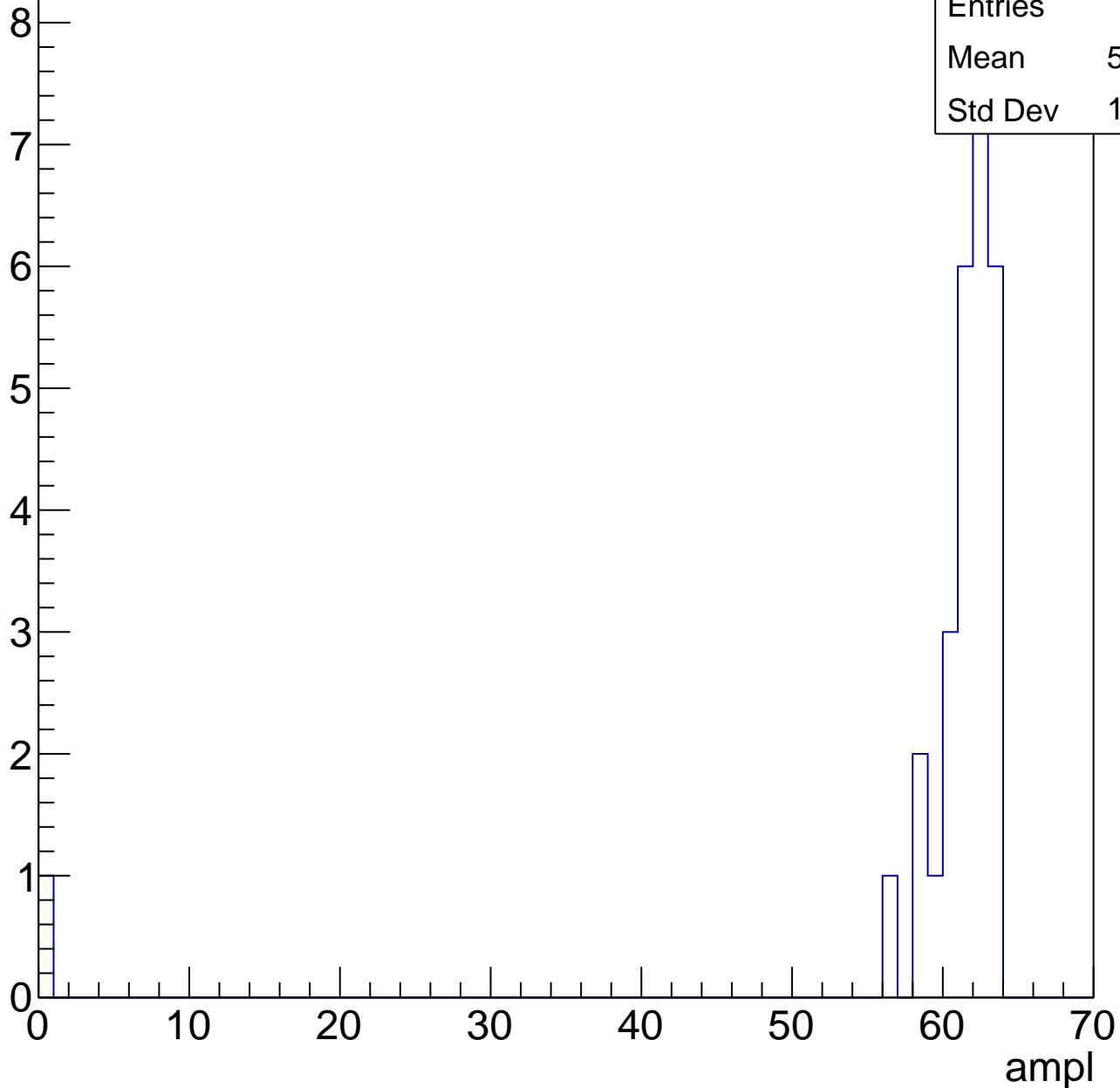


B1L100S, U11-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	58.96
Std Dev	11.48



B1L100S, U11-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

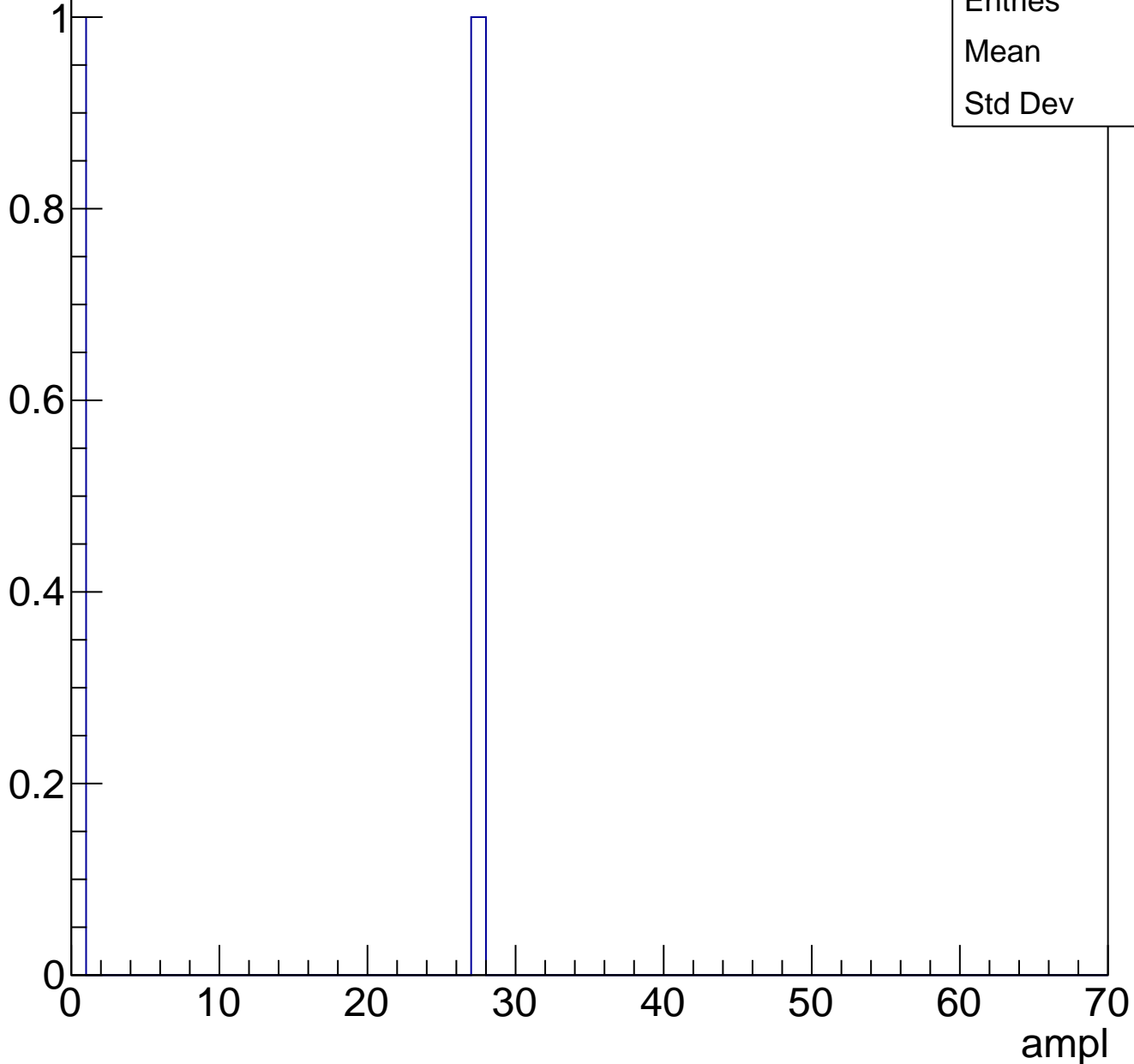
Entry



B1L100S, U11-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch90, adc0

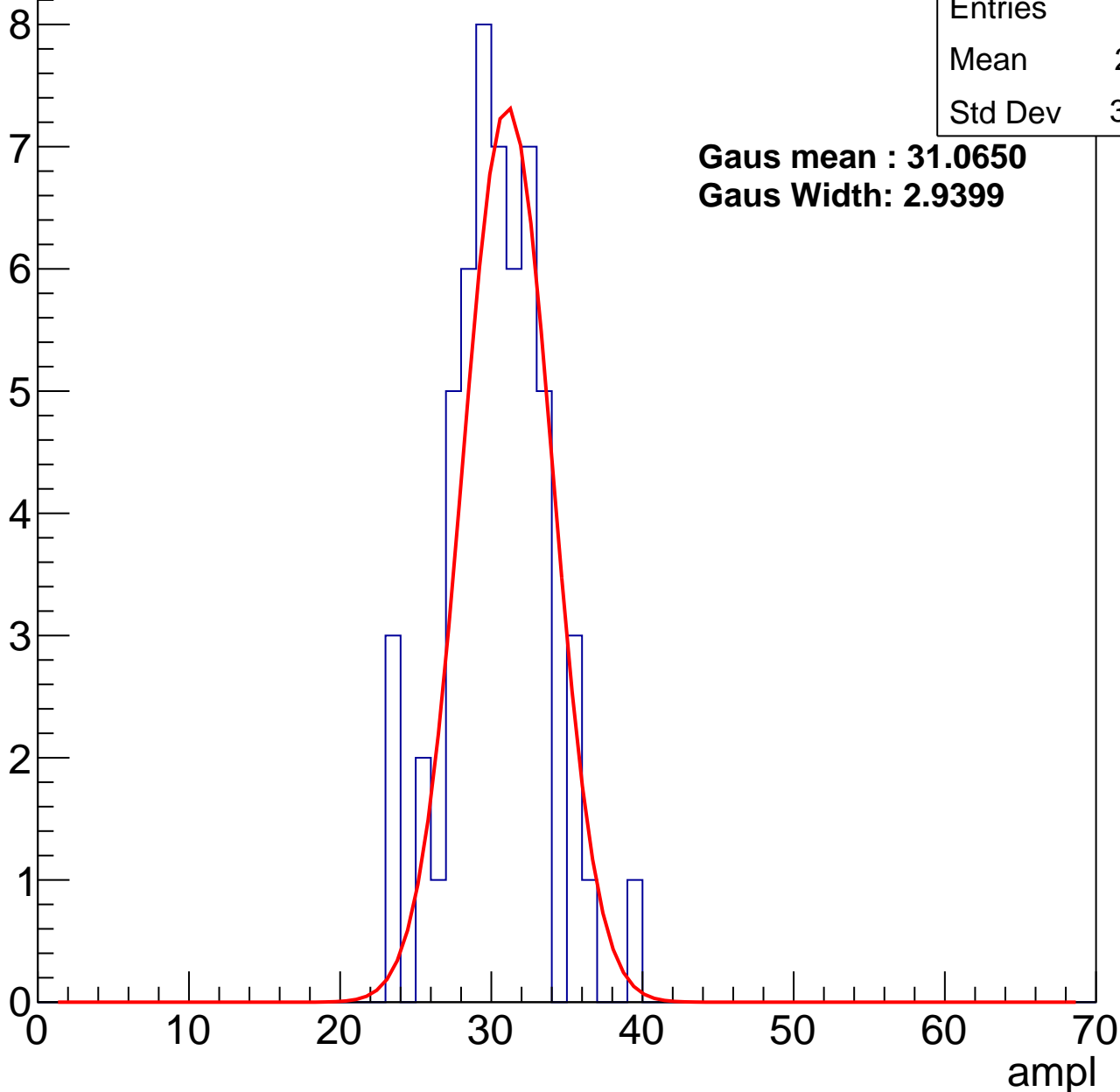
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	29.91
Std Dev	3.192

Gaus mean : 31.0650

Gaus Width: 2.9399



B1L100S, U11-ch90, adc1

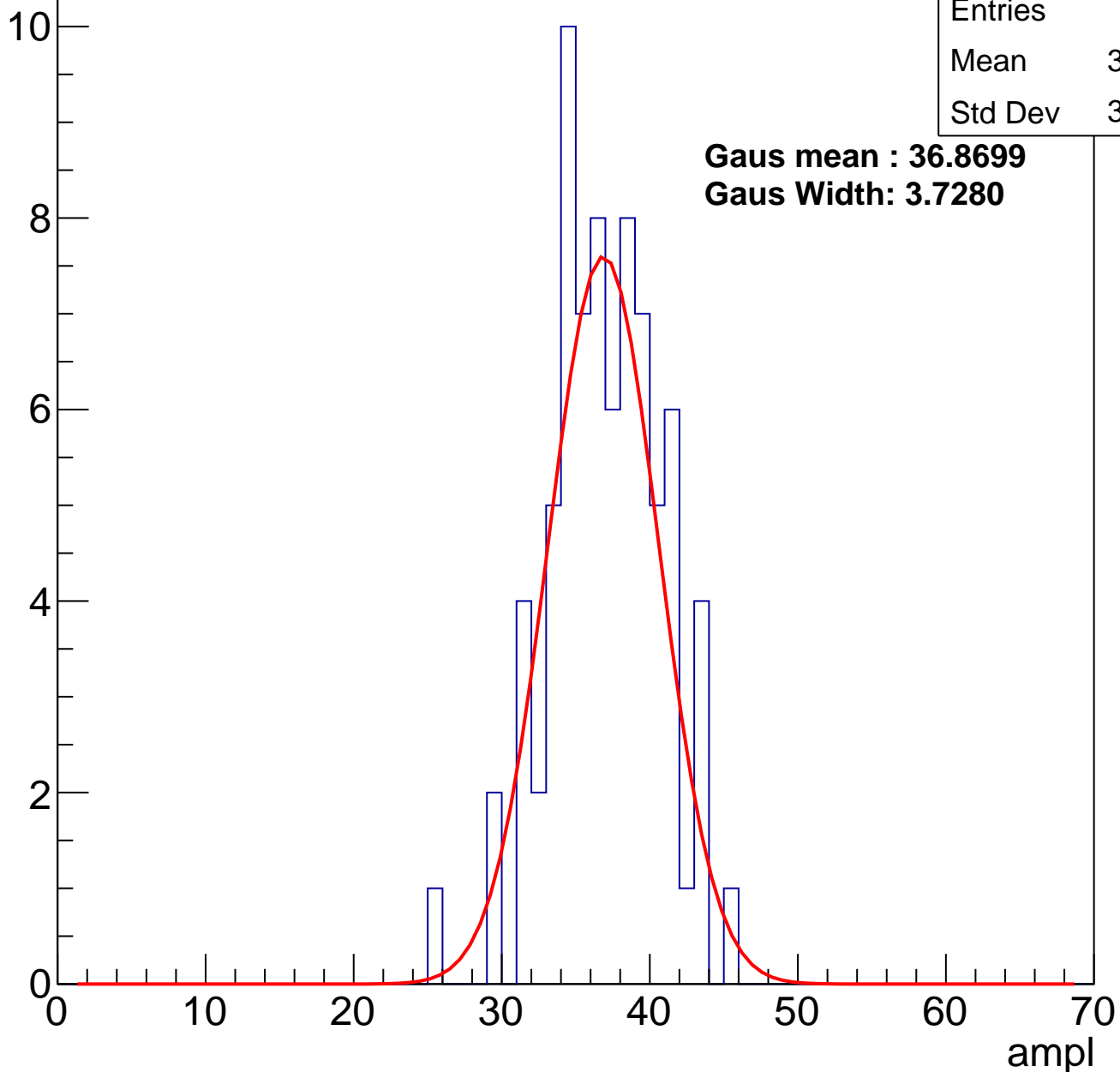
calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	36.53
Std Dev	3.719

Gaus mean : 36.8699

Gaus Width: 3.7280

Entry



B1L100S, U11-ch90, adc2

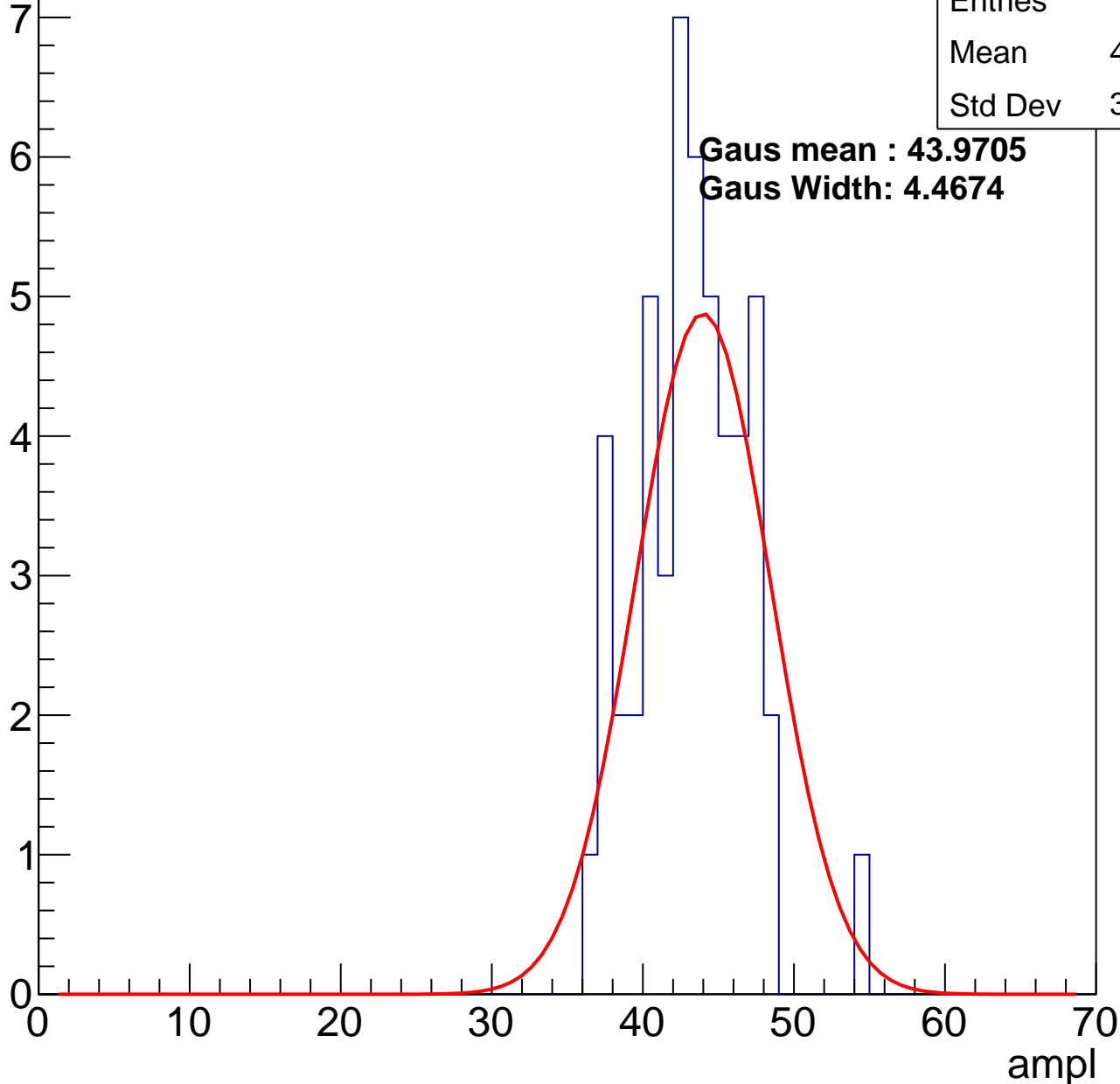
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	42.78
Std Dev	3.555

Gaus mean : 43.9705

Gaus Width: 4.4674

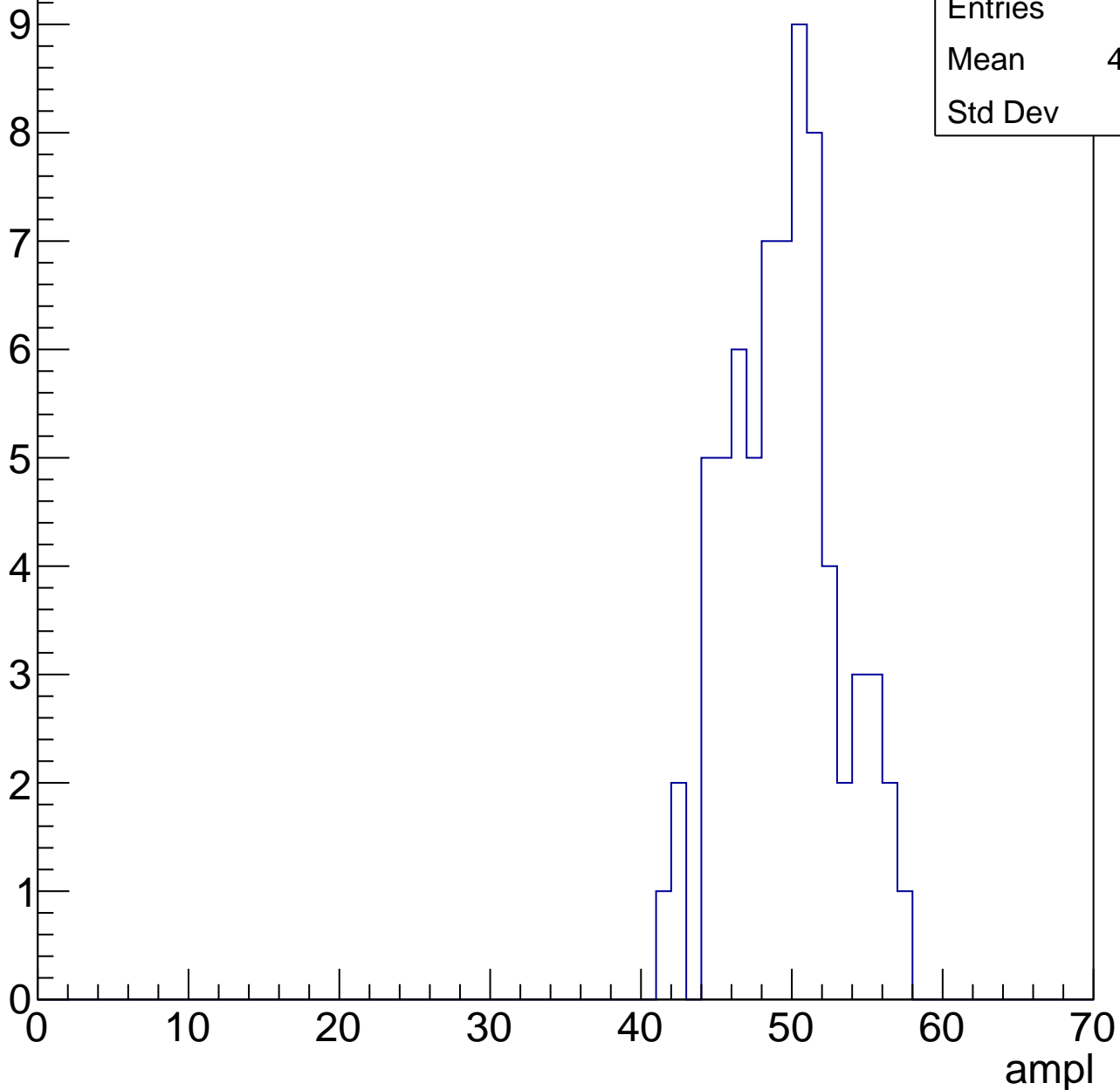


B1L100S, U11-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	48.97
Std Dev	3.59

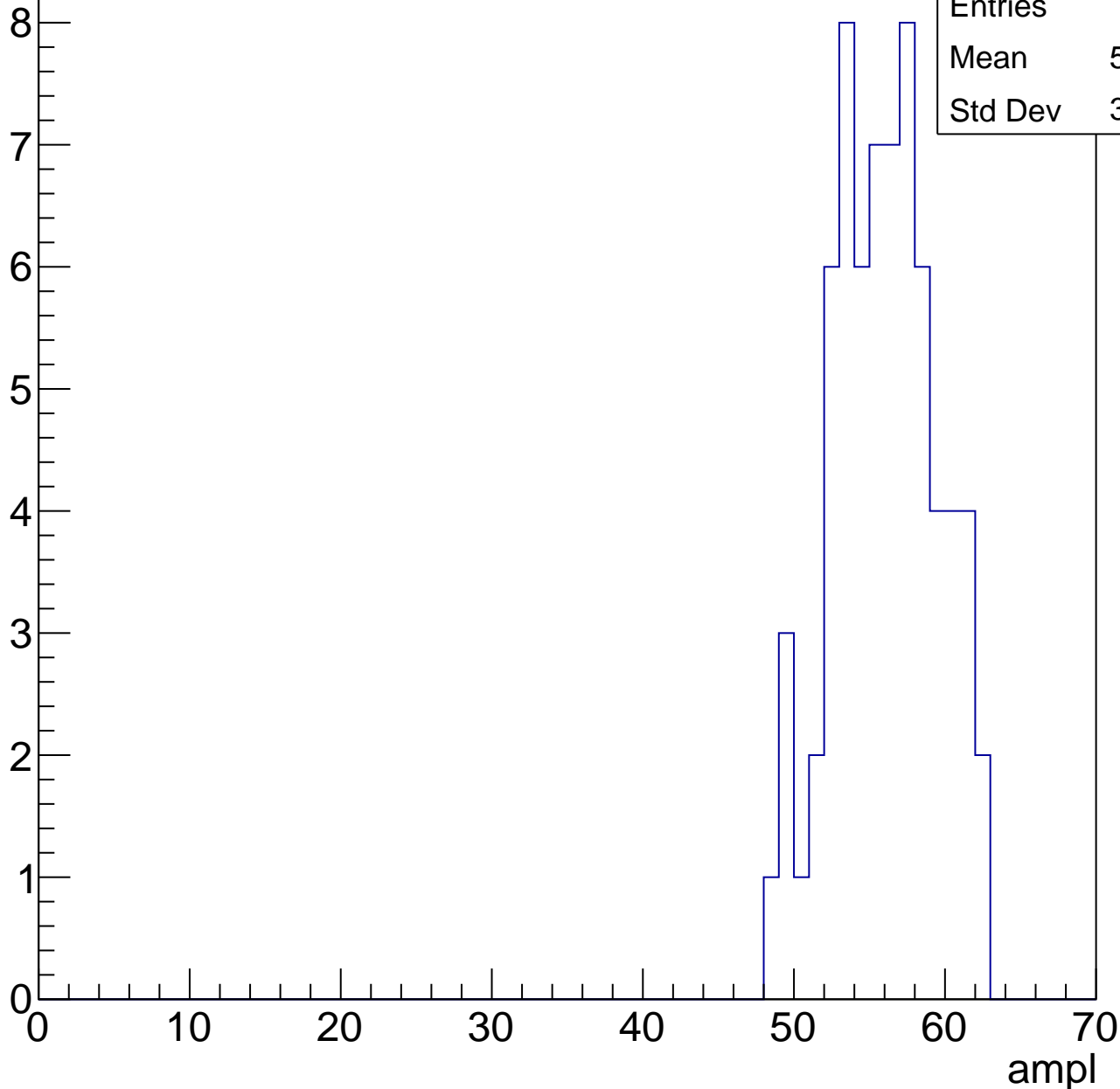


B1L100S, U11-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

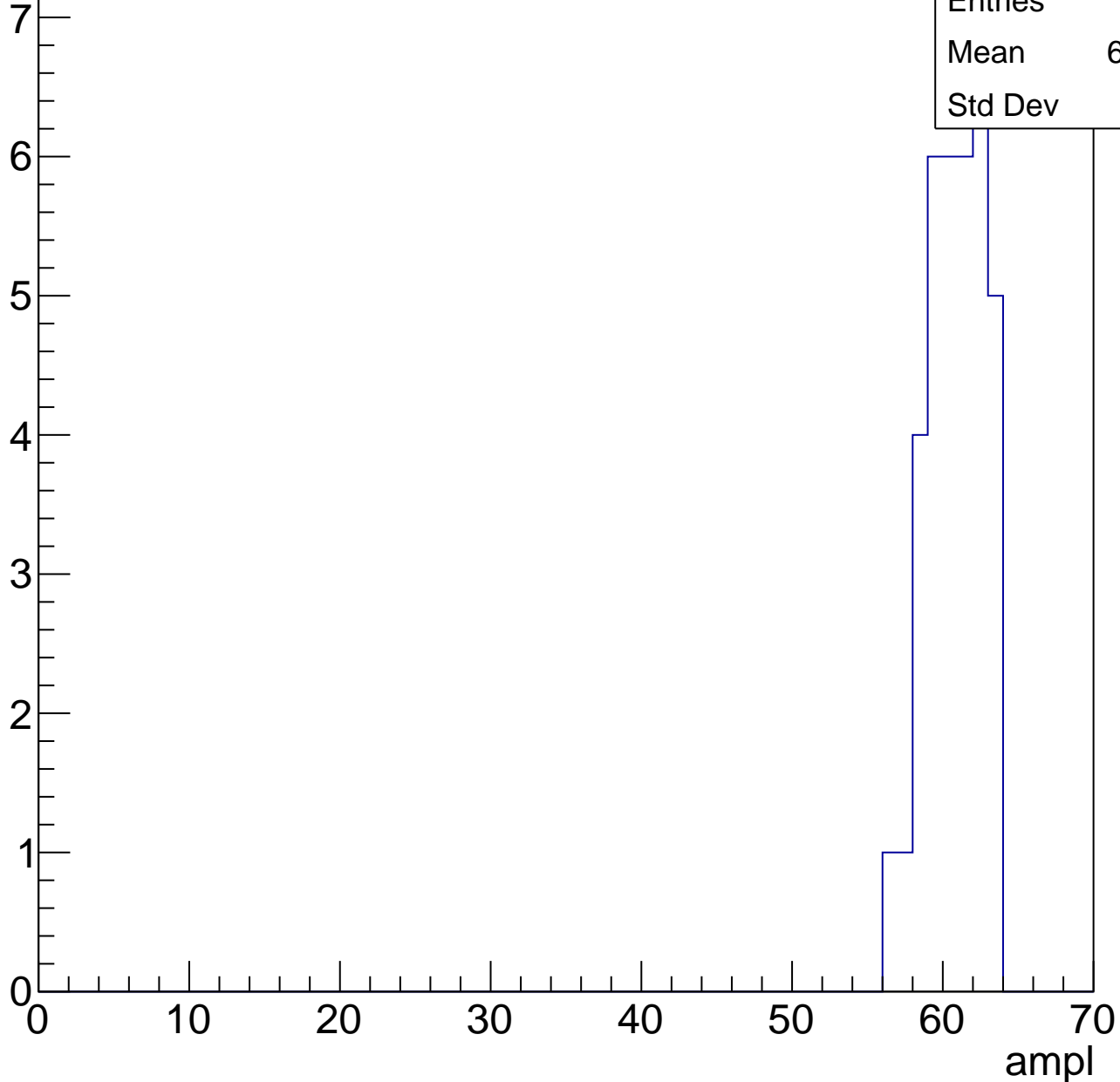
Entries	69
Mean	55.54
Std Dev	3.373



B1L100S, U11-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

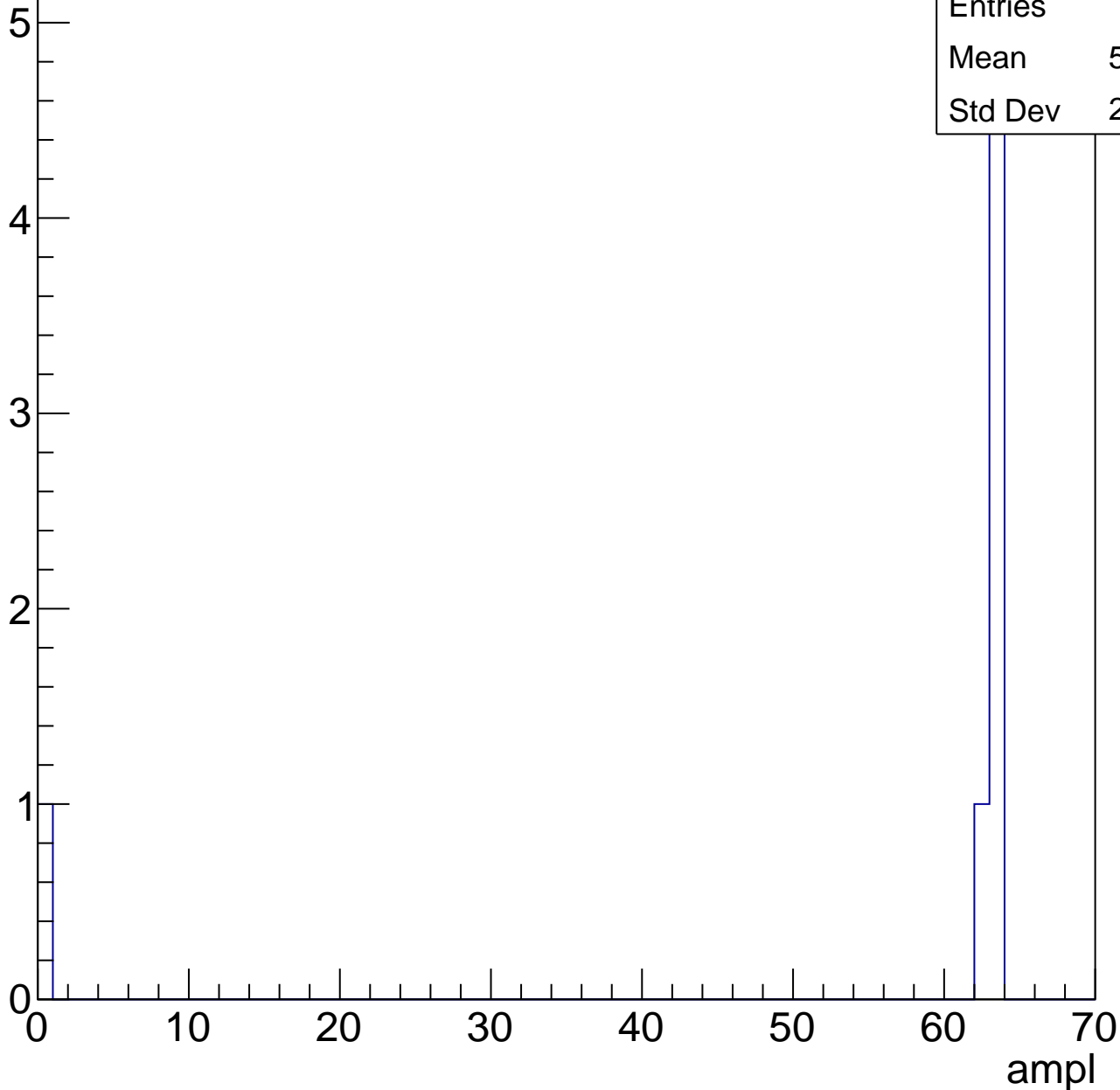


B1L100S, U11-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	7
Mean	53.86
Std Dev	21.99



B1L100S, U11-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch91, adc0

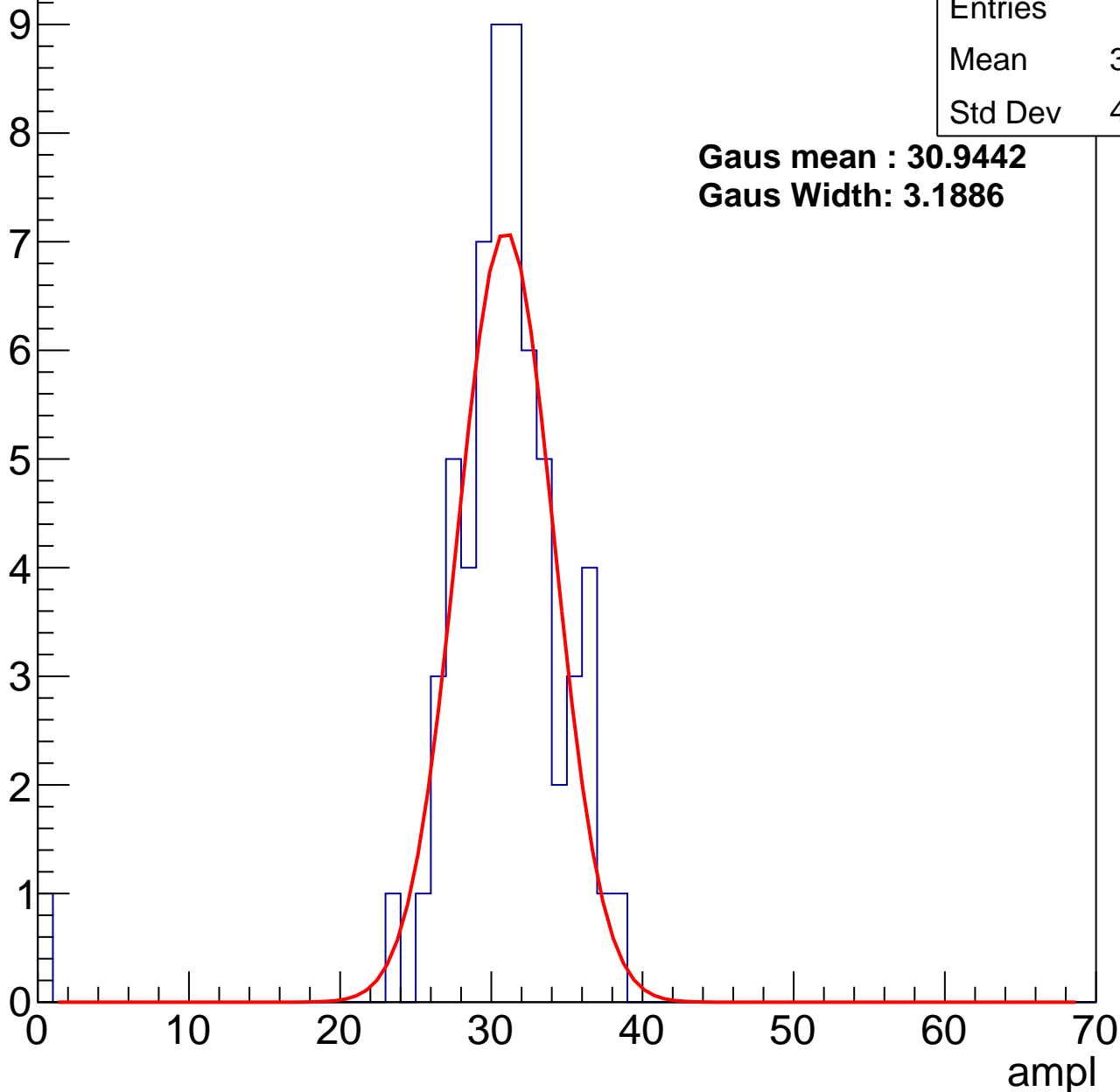
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.23
Std Dev	4.966

Gaus mean : 30.9442

Gaus Width: 3.1886



B1L100S, U11-ch91, adc1

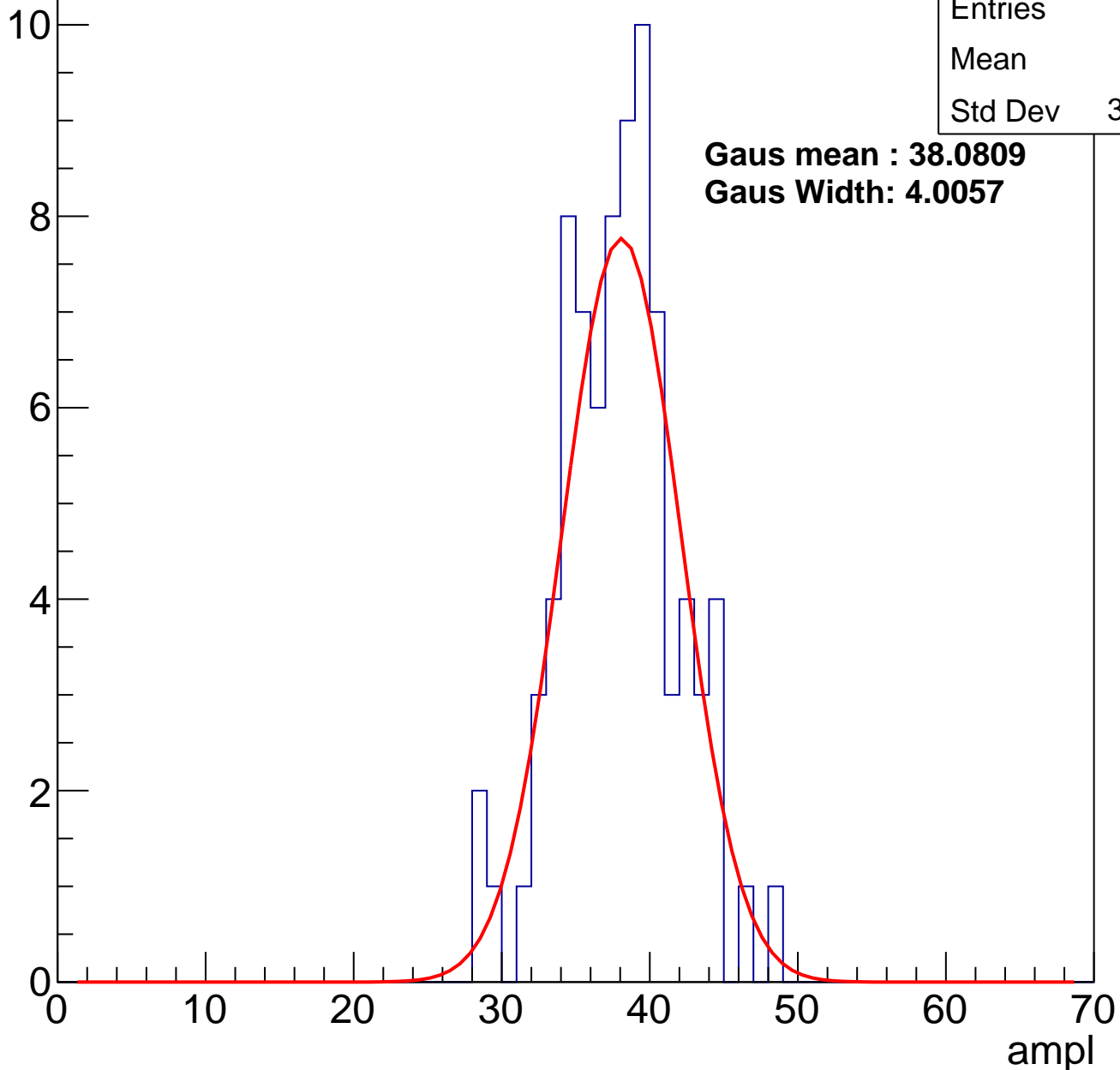
calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	37.5
Std Dev	3.908

Gaus mean : 38.0809

Gaus Width: 4.0057

Entry

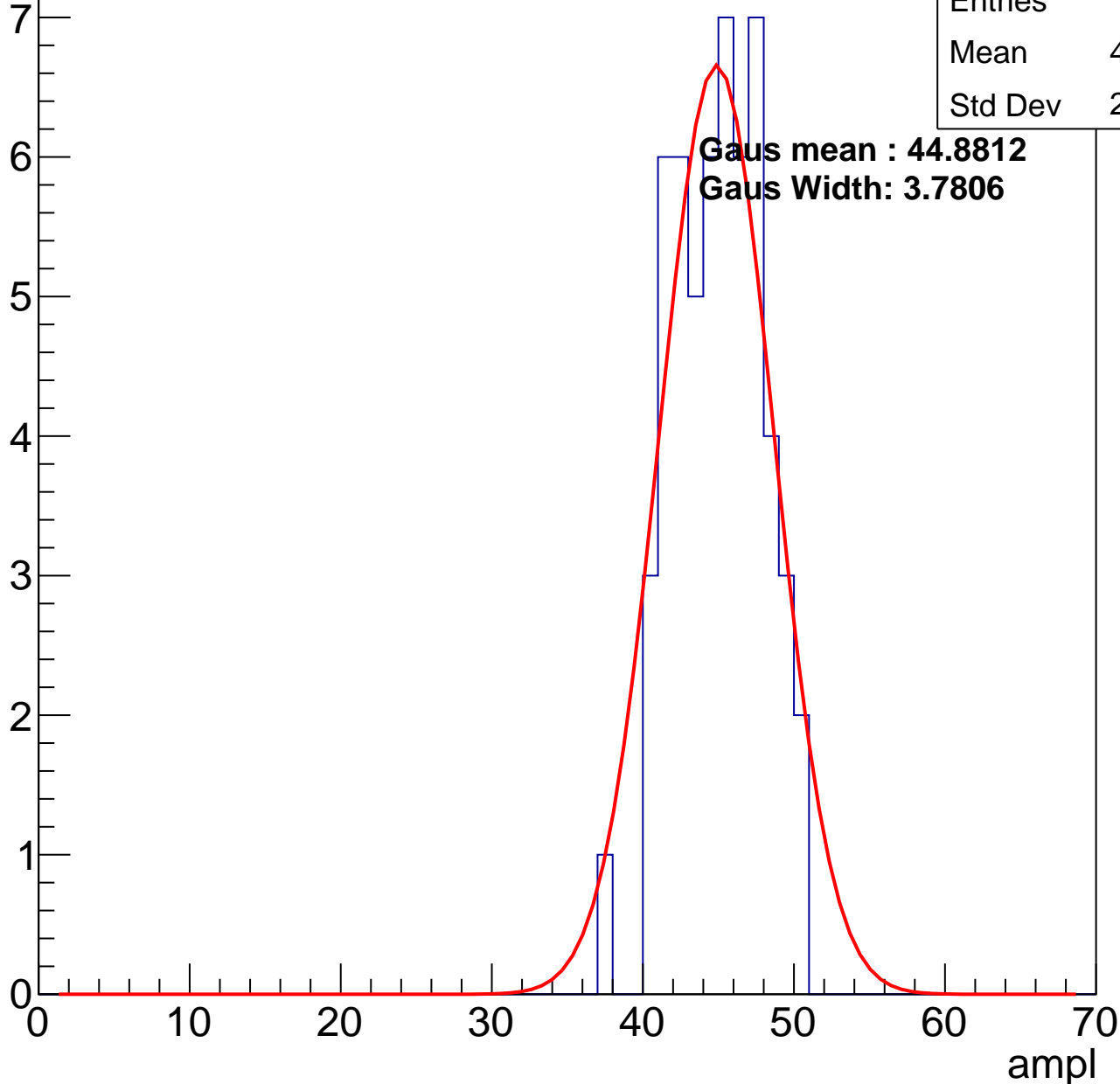


B1L100S, U11-ch91, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.52
Std Dev	2.897

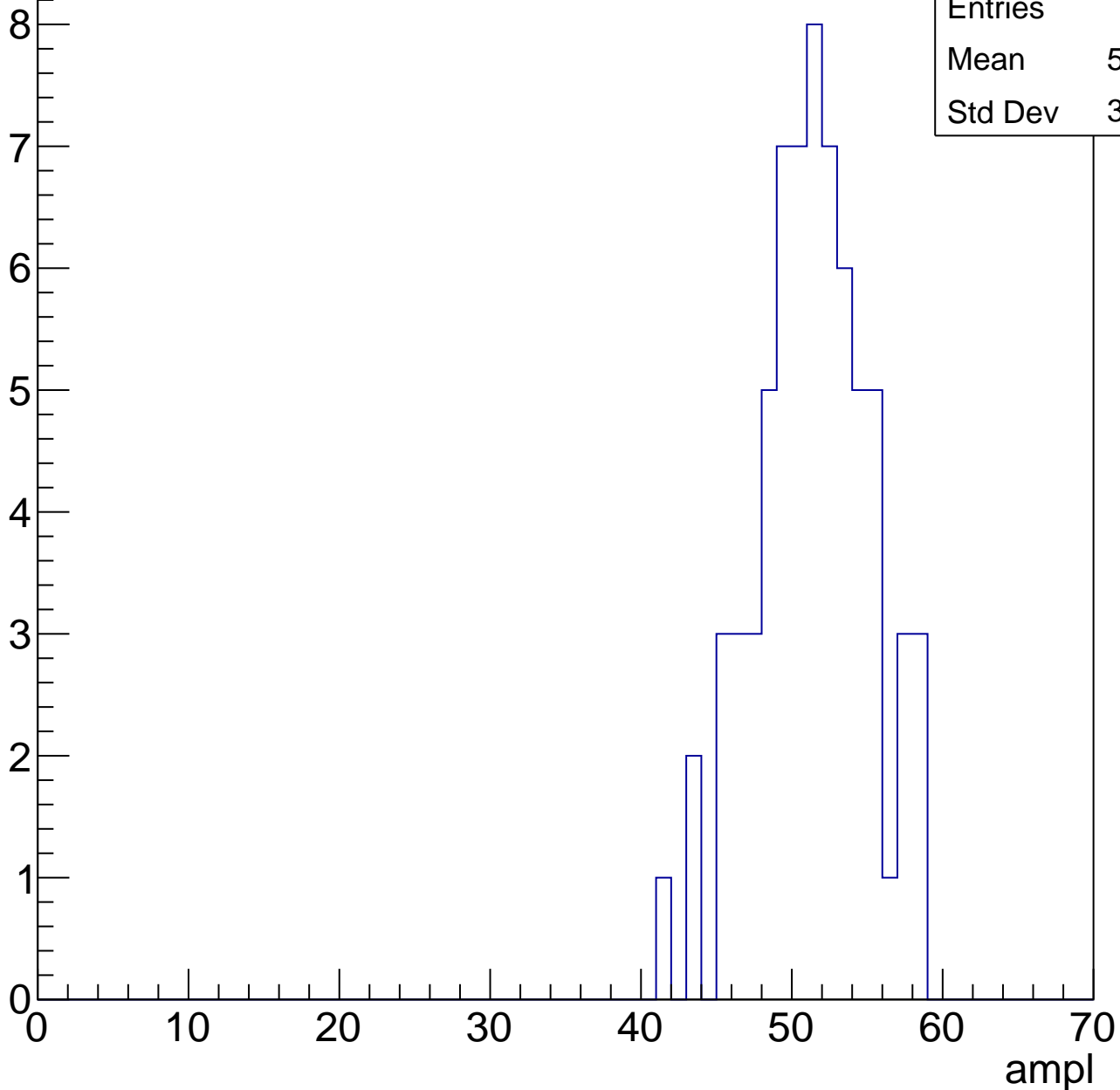


B1L100S, U11-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	50.87
Std Dev	3.757

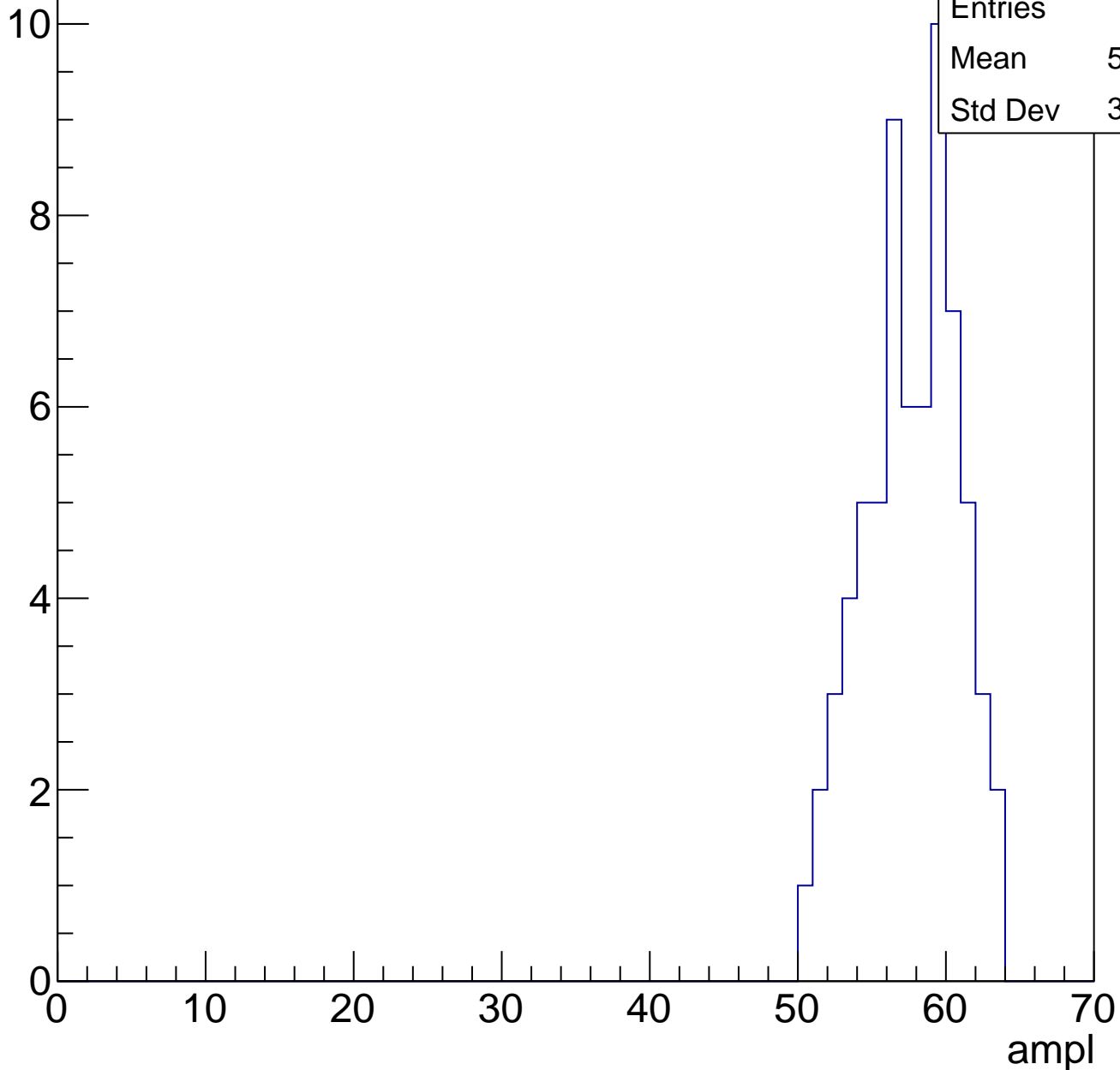


B1L100S, U11-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	57.15
Std Dev	3.135

Entry

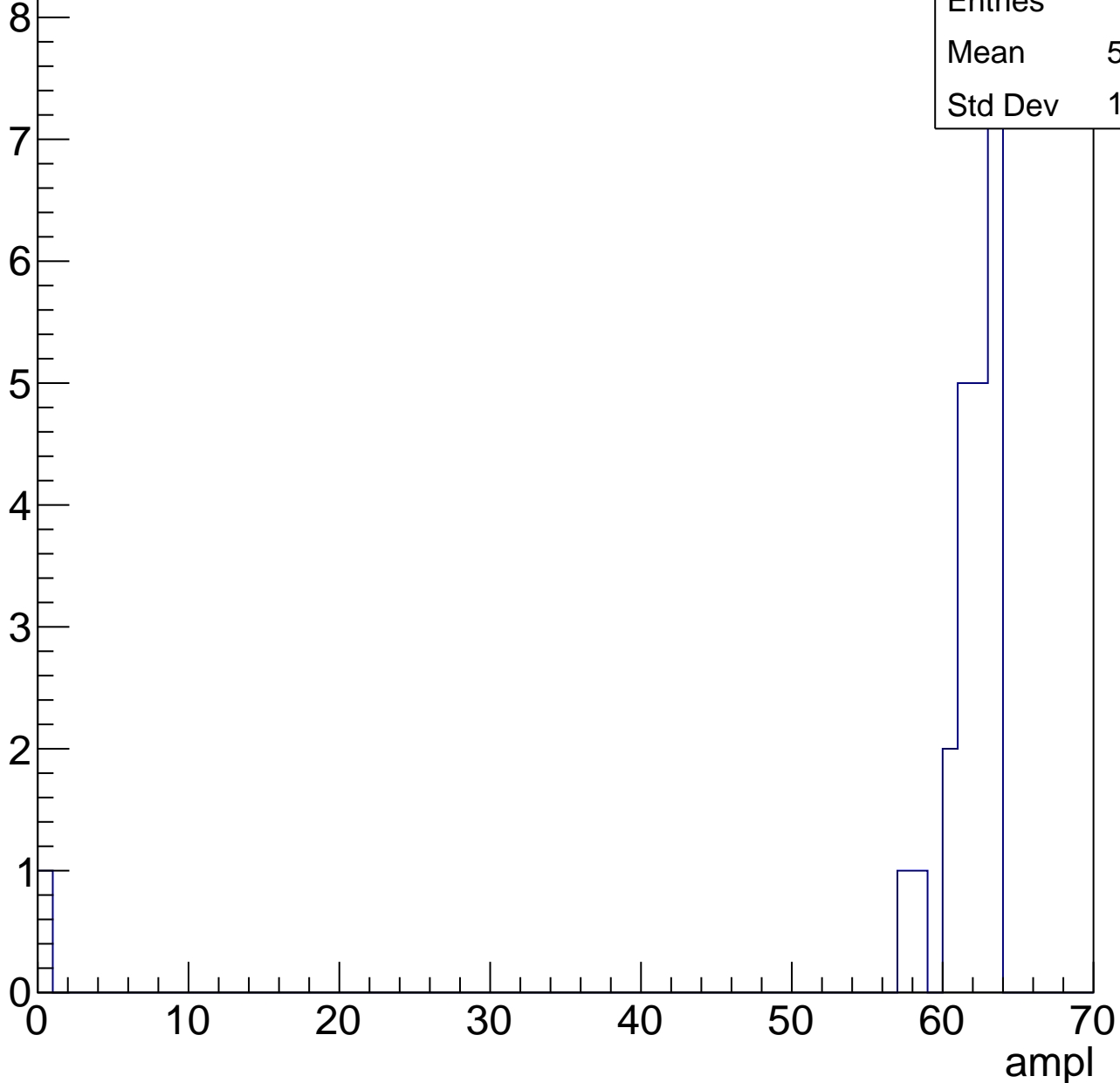


B1L100S, U11-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

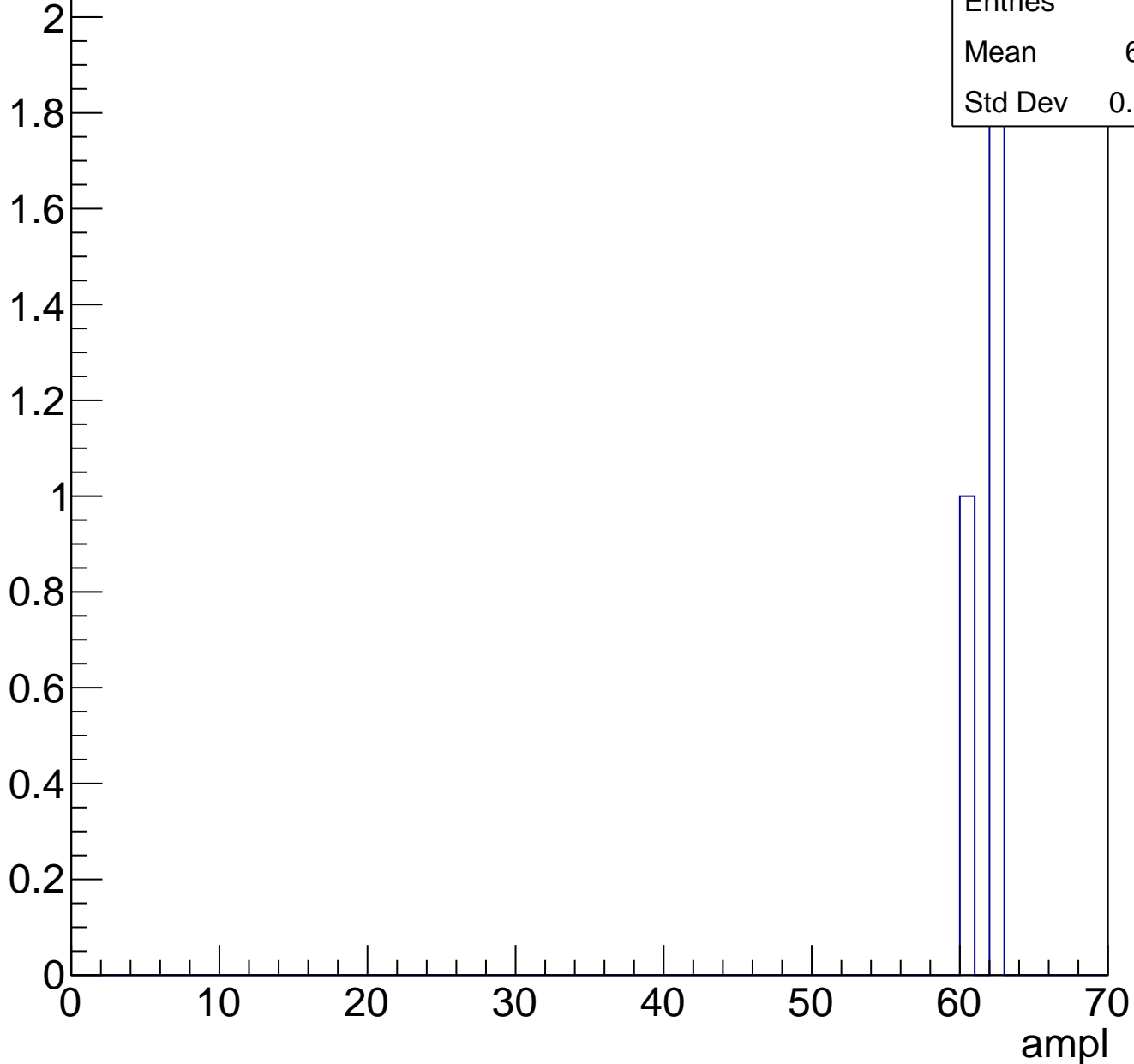
Entries	23
Mean	58.87
Std Dev	12.65



B1L100S, U11-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



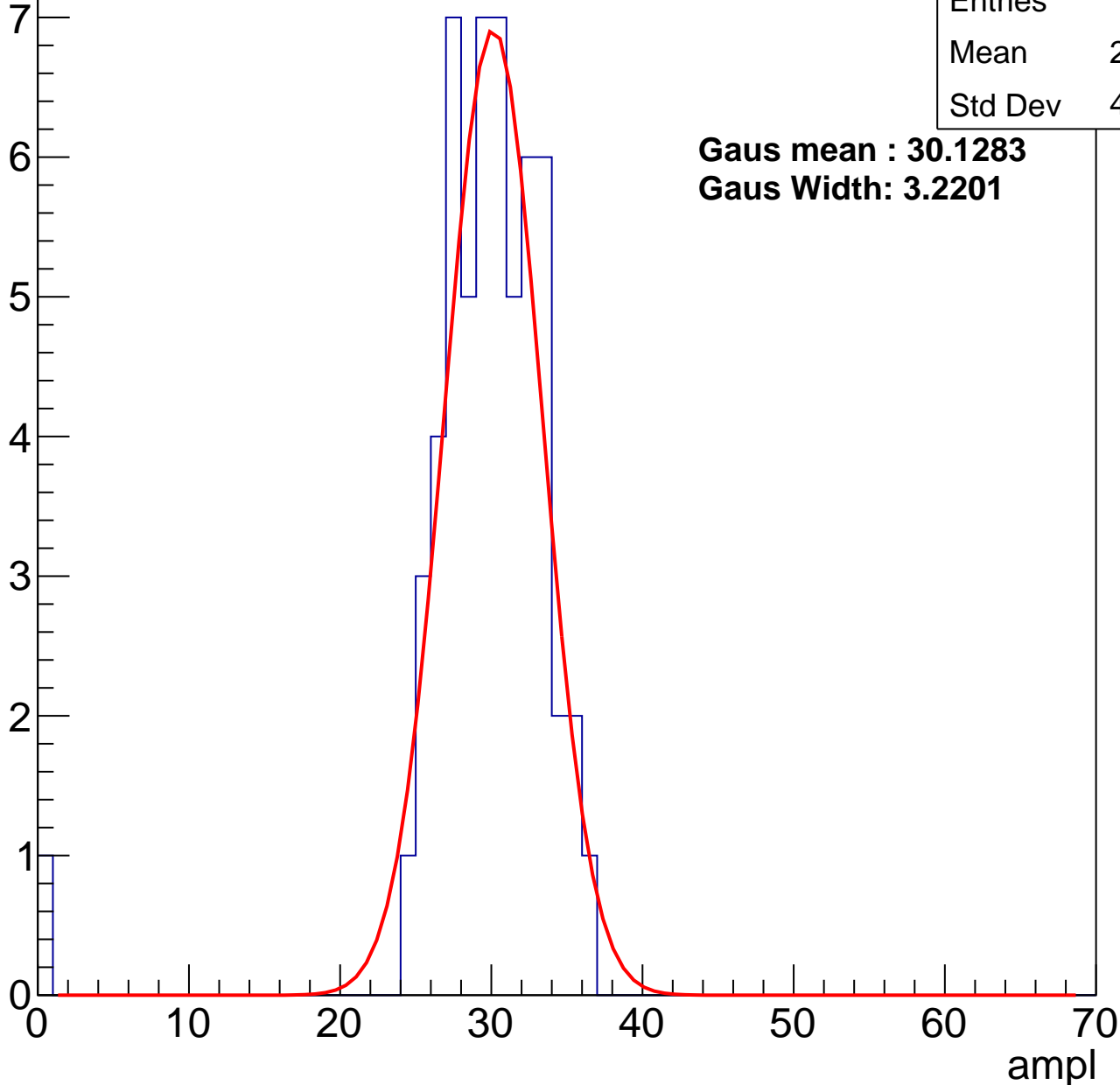
B1L100S, U11-ch92, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	29.19
Std Dev	4.828

Gaus mean : 30.1283
Gaus Width: 3.2201



B1L100S, U11-ch92, adc1

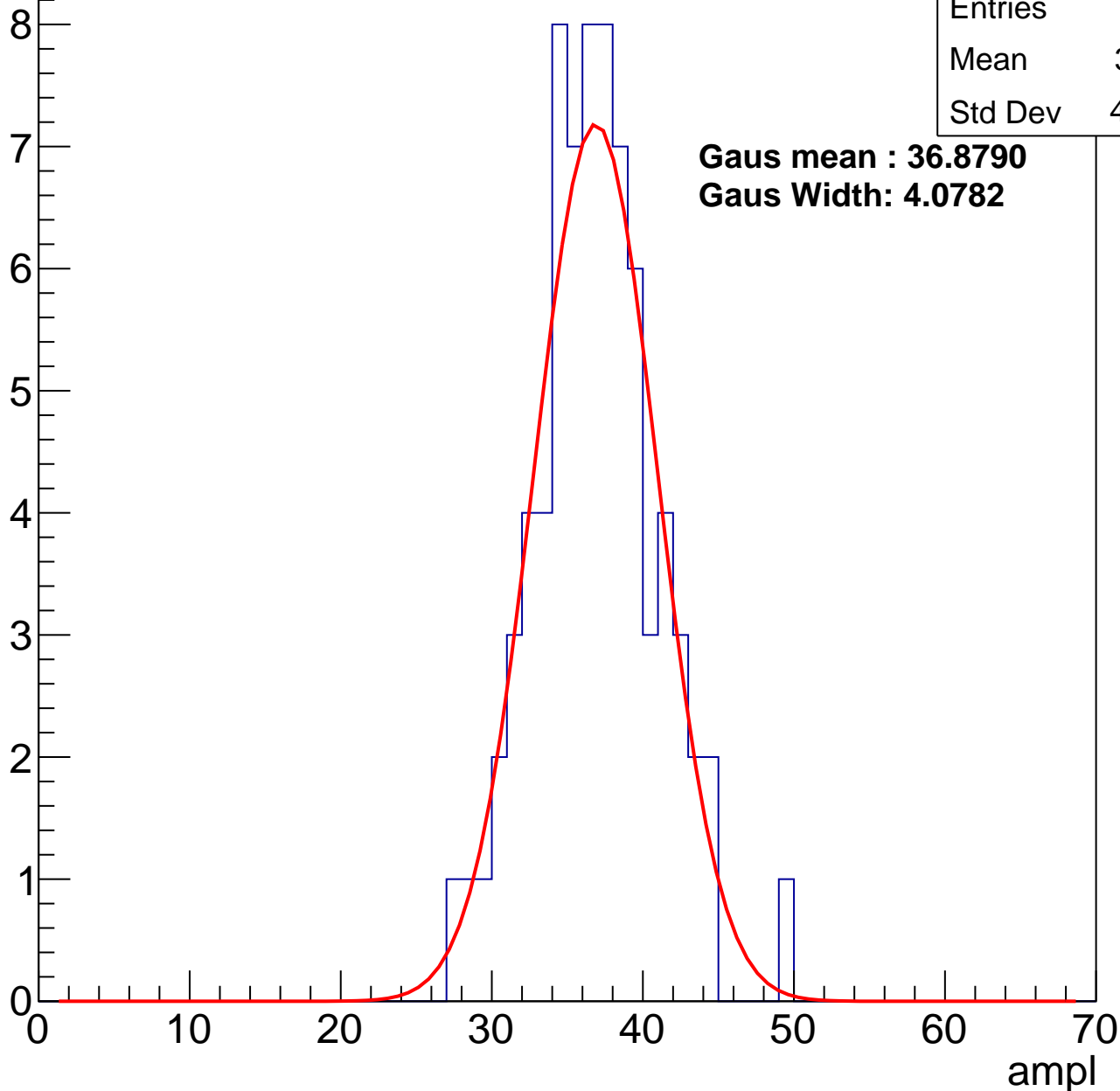
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.41
Std Dev	4.014

Gaus mean : 36.8790

Gaus Width: 4.0782



B1L100S, U11-ch92, adc2

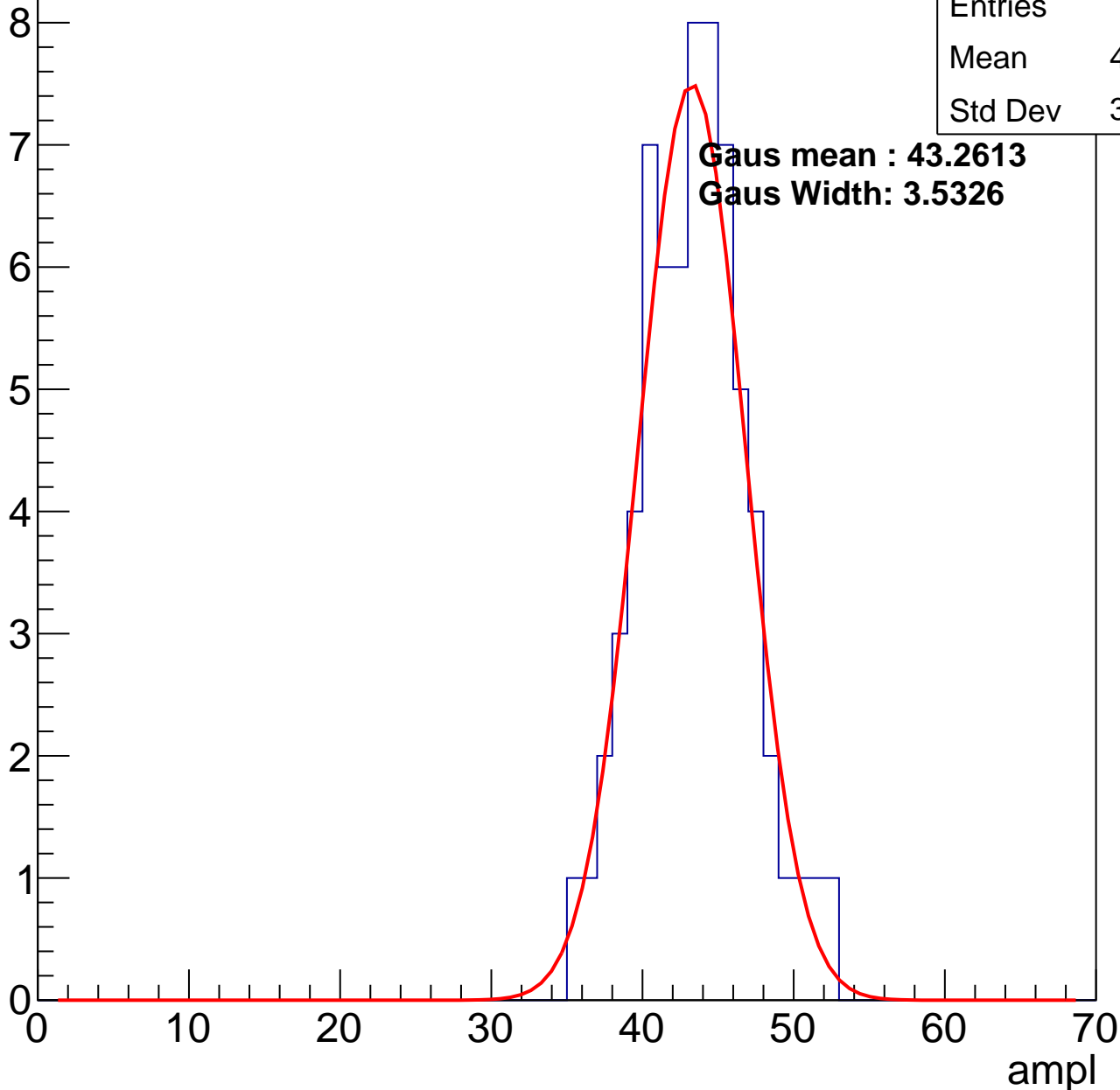
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.94
Std Dev	3.518

Gaus mean : 43.2613

Gaus Width: 3.5326

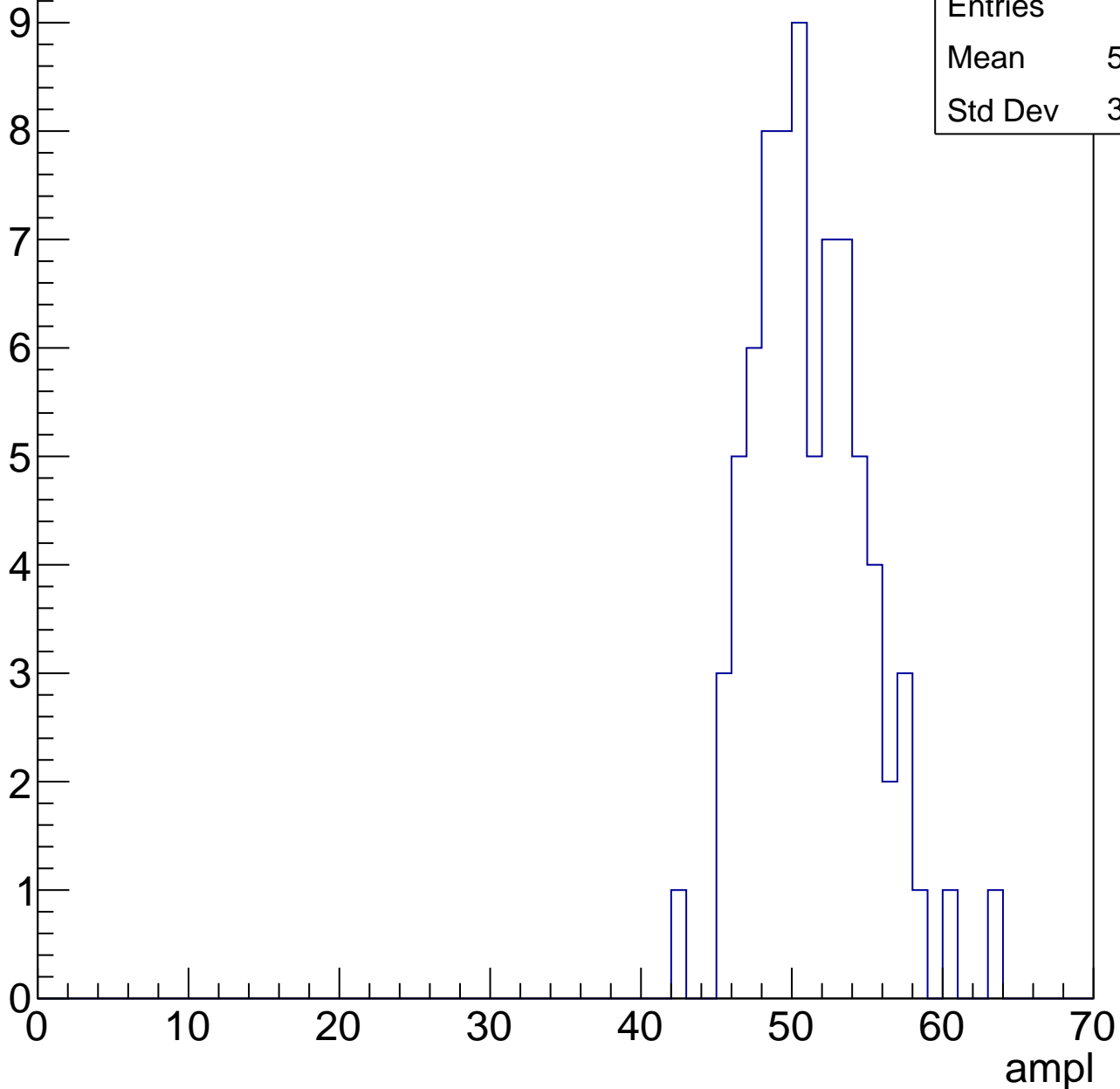


B1L100S, U11-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	50.78
Std Dev	3.796

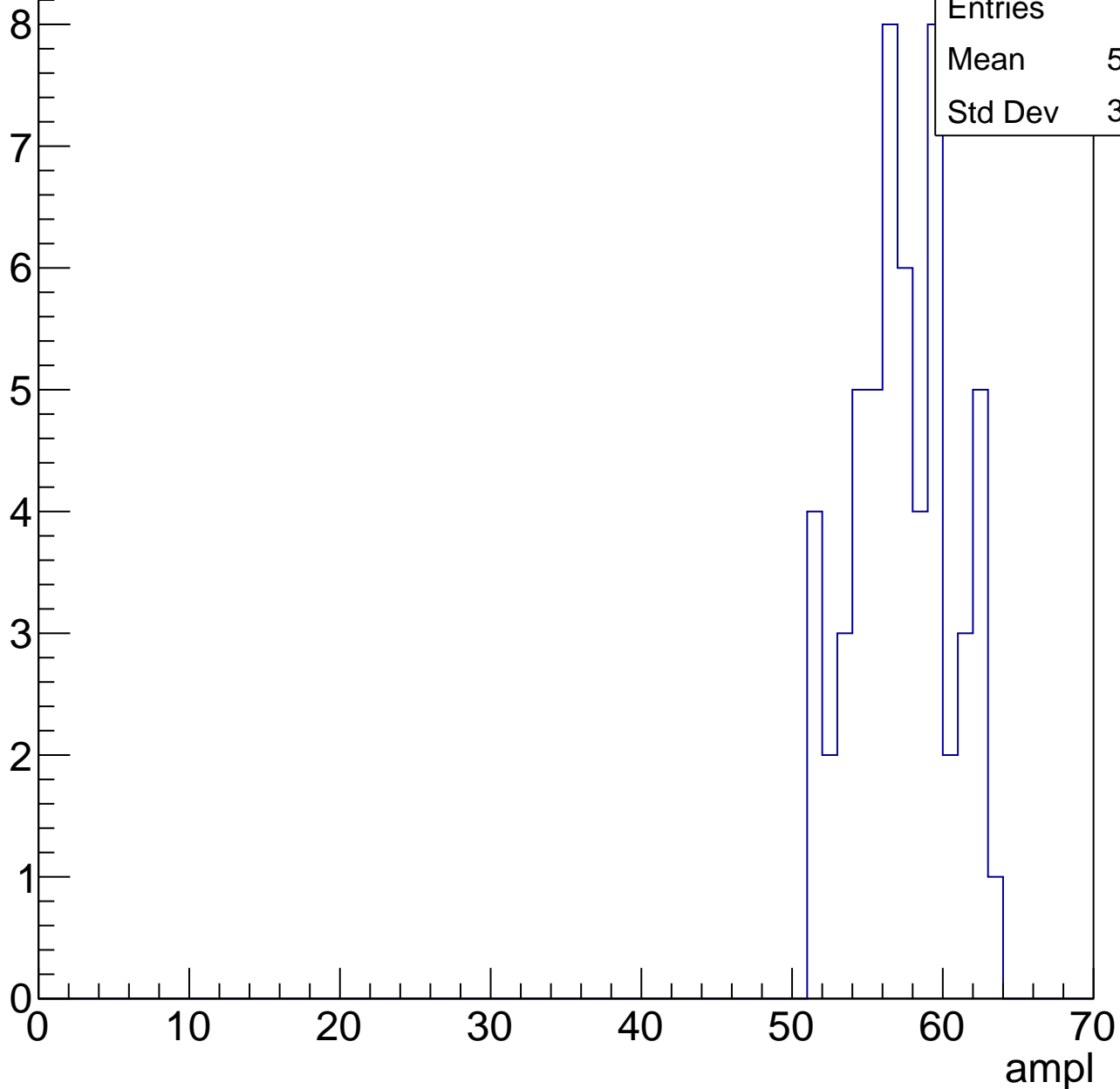


B1L100S, U11-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.82
Std Dev	3.208



B1L100S, U11-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	36
Mean	58.83
Std Dev	10.13

ampl

B1L100S, U11-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L100S, U11-ch93, adc0

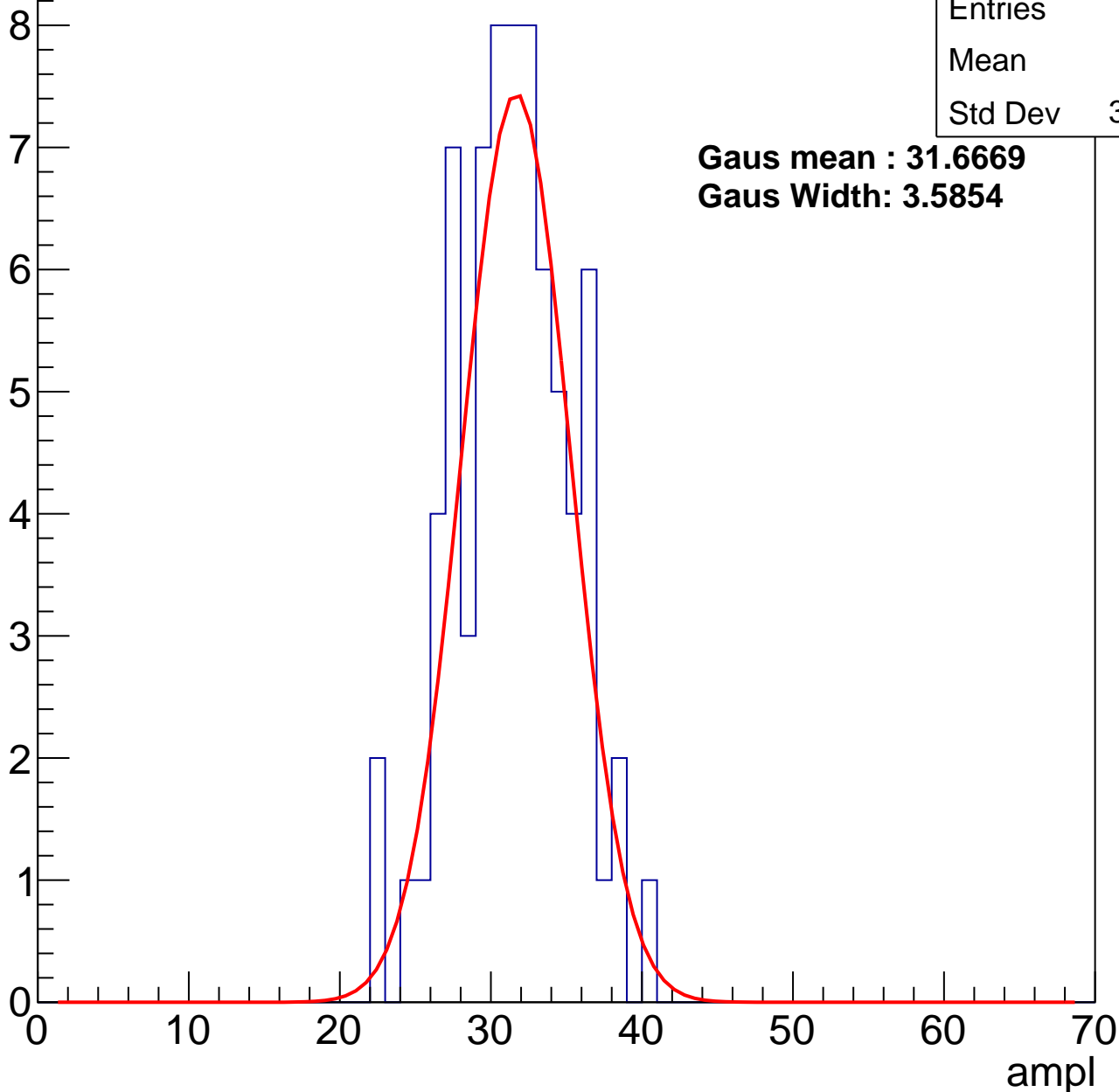
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	31
Std Dev	3.727

Gaus mean : 31.6669

Gaus Width: 3.5854



B1L100S, U11-ch93, adc1

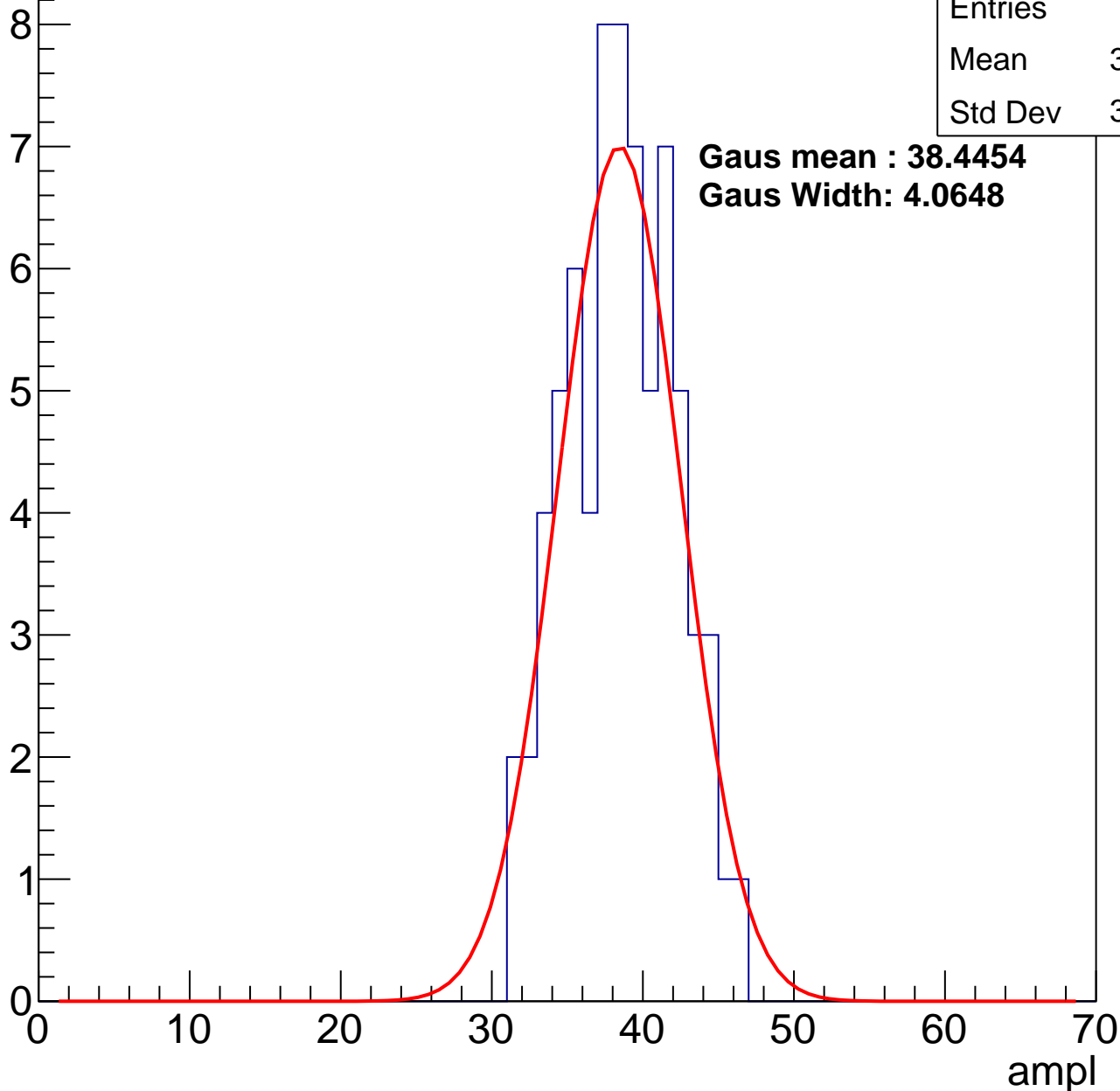
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	38.08
Std Dev	3.547

Gaus mean : 38.4454

Gaus Width: 4.0648



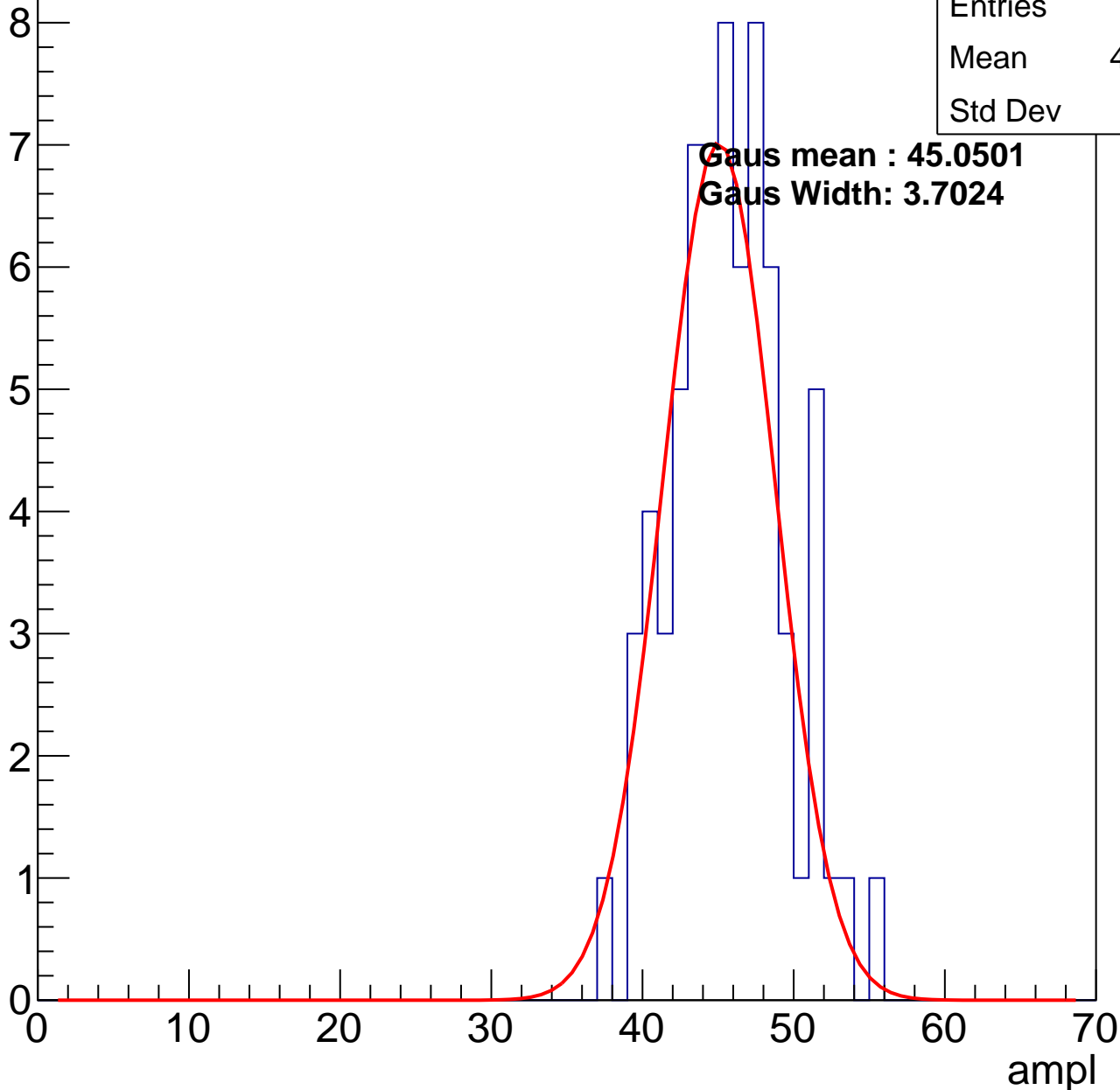
B1L100S, U11-ch93, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	45.26
Std Dev	3.69

Gaus mean : 45.0501
Gaus Width: 3.7024

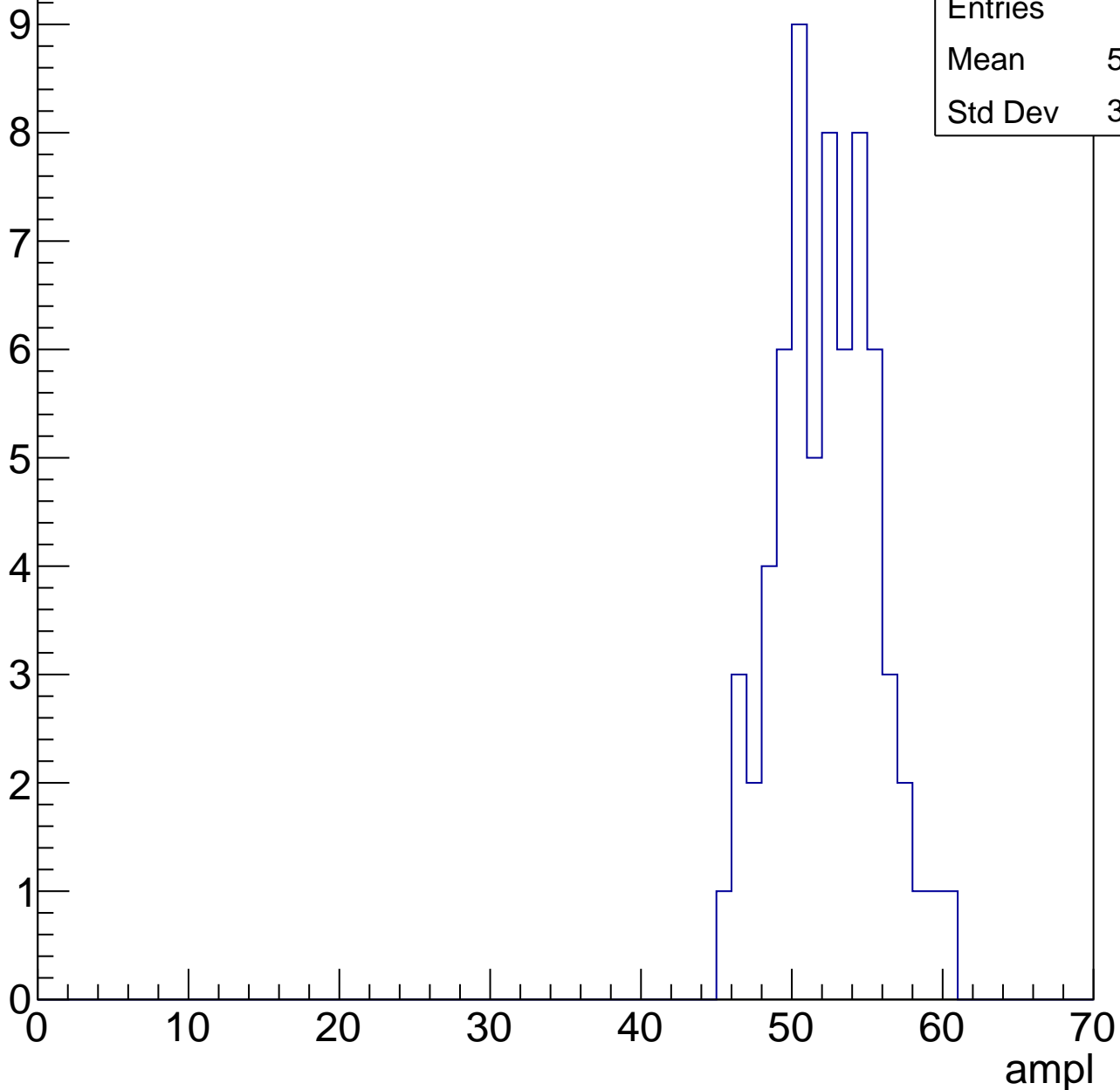


B1L100S, U11-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

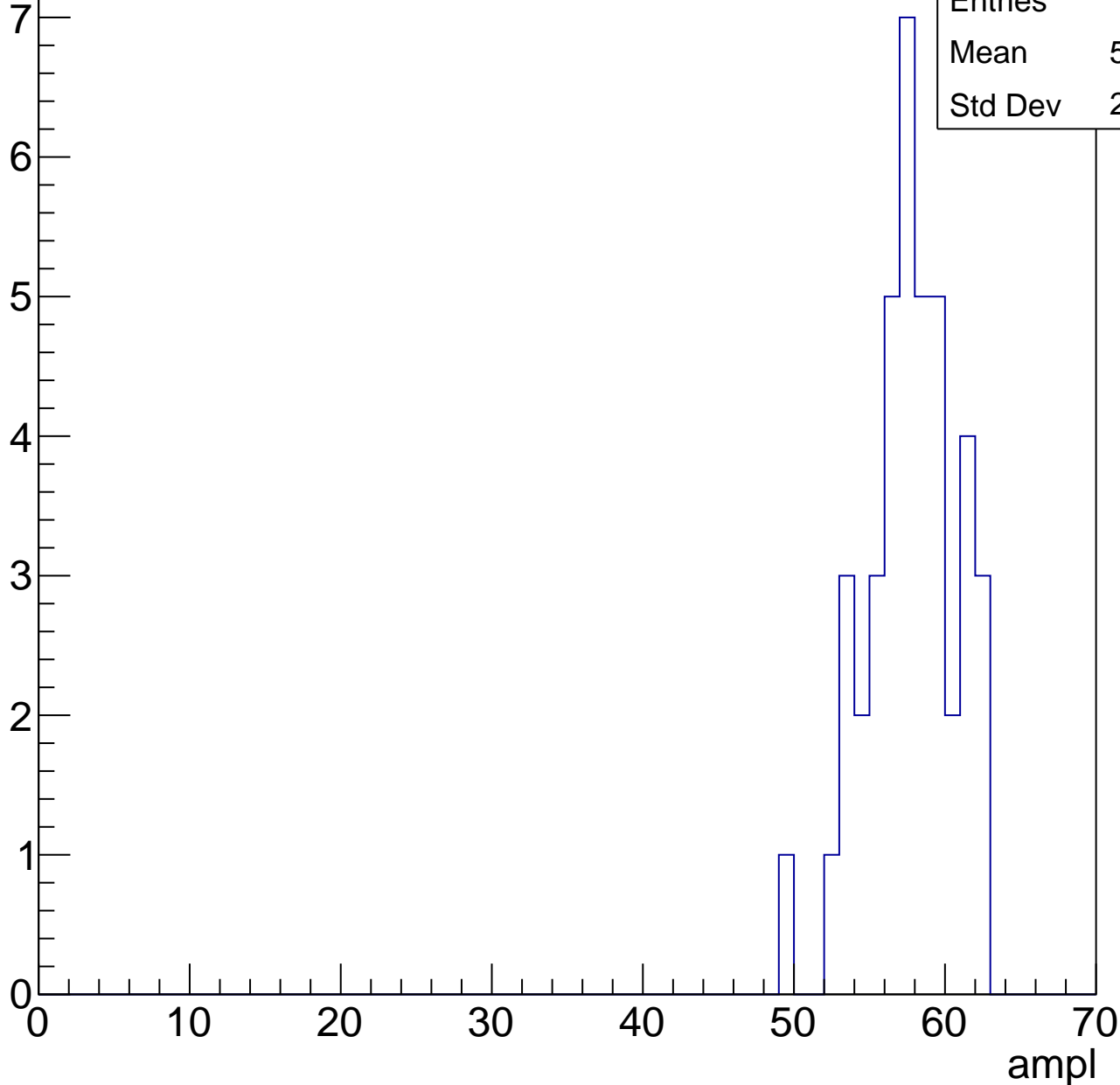
Entries	66
Mean	51.86
Std Dev	3.265



B1L100S, U11-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

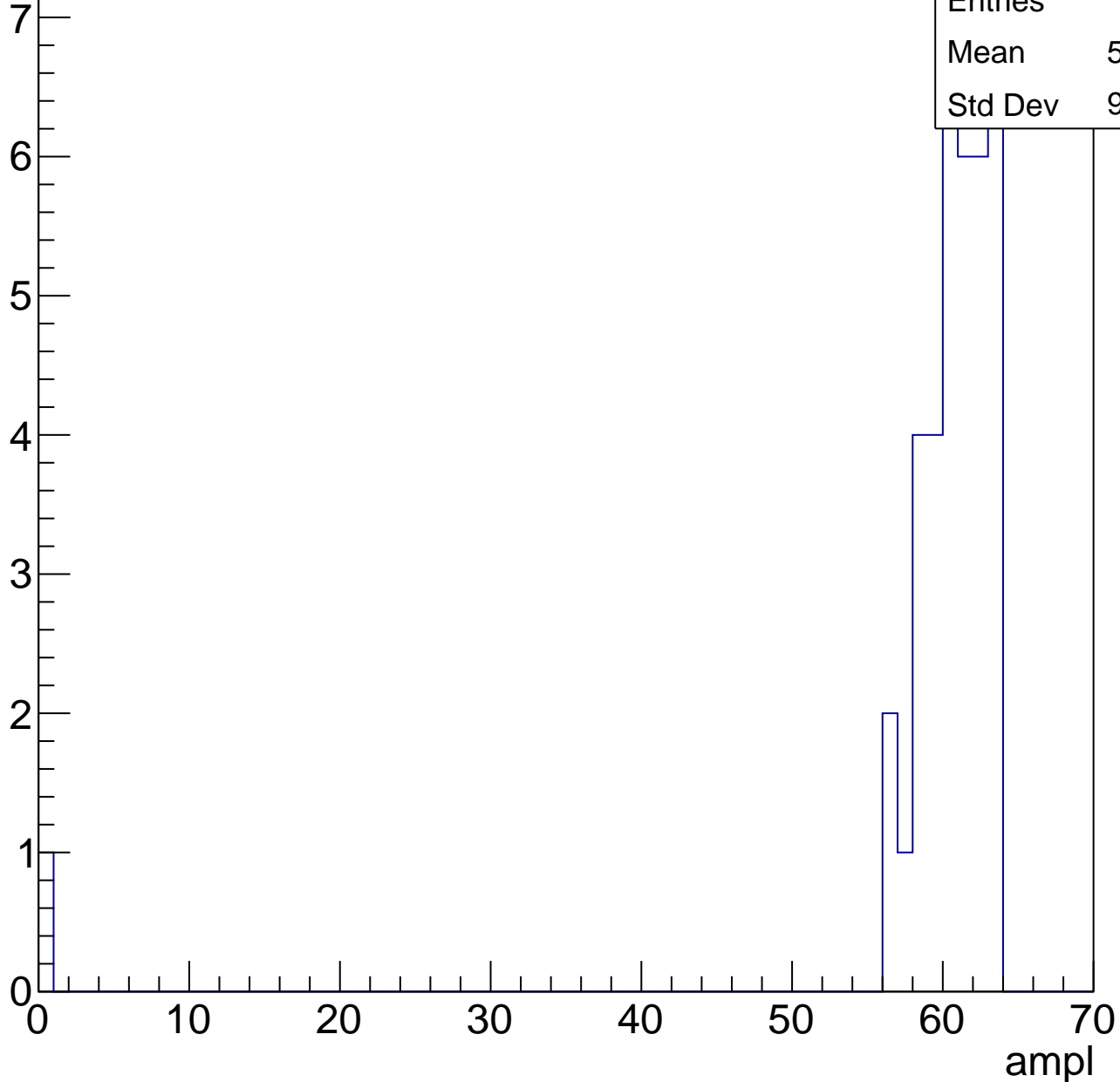
Entry



B1L100S, U11-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L100S, U11-ch94, adc0

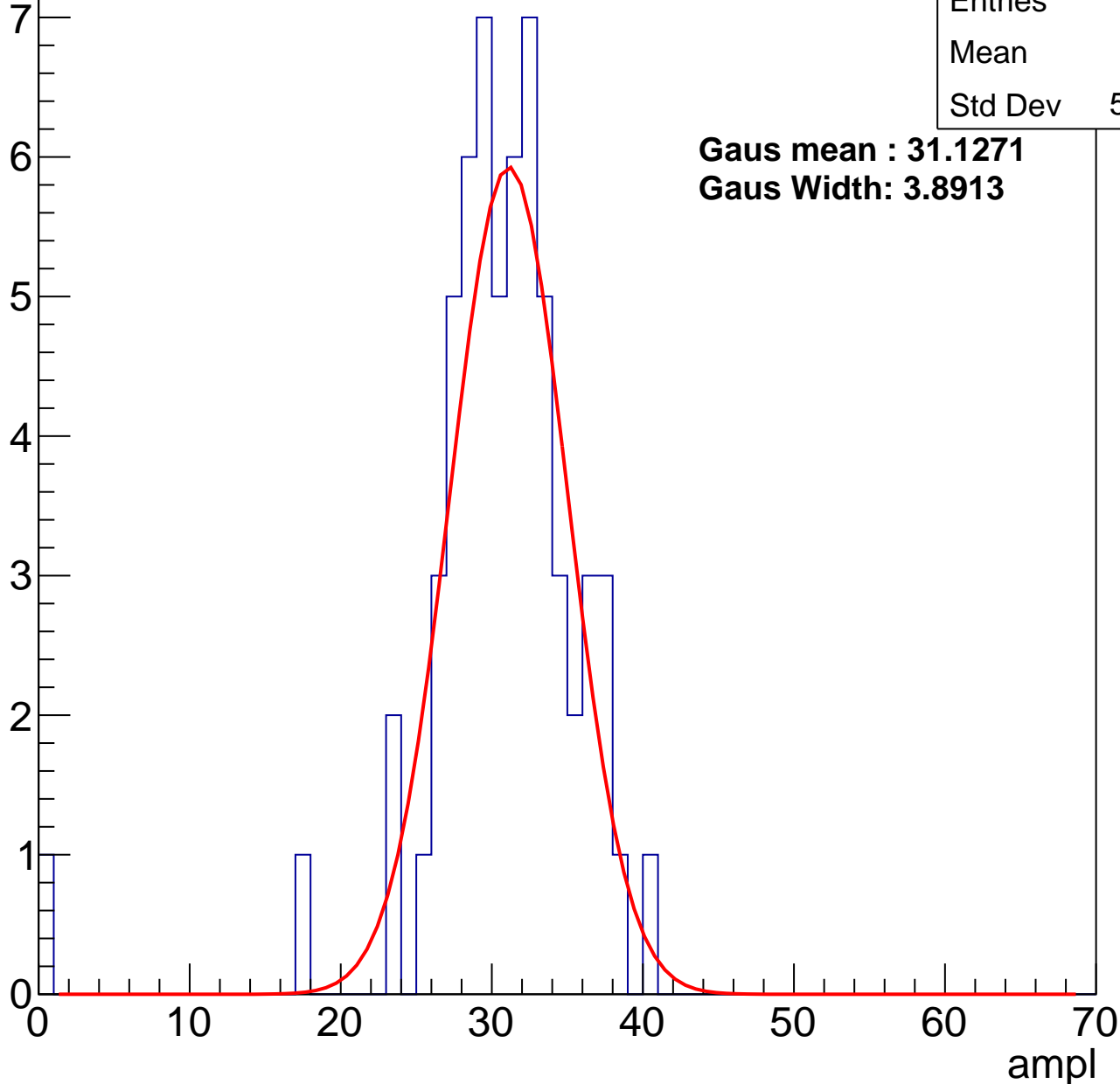
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.1
Std Dev	5.552

Gaus mean : 31.1271

Gaus Width: 3.8913



B1L100S, U11-ch94, adc1

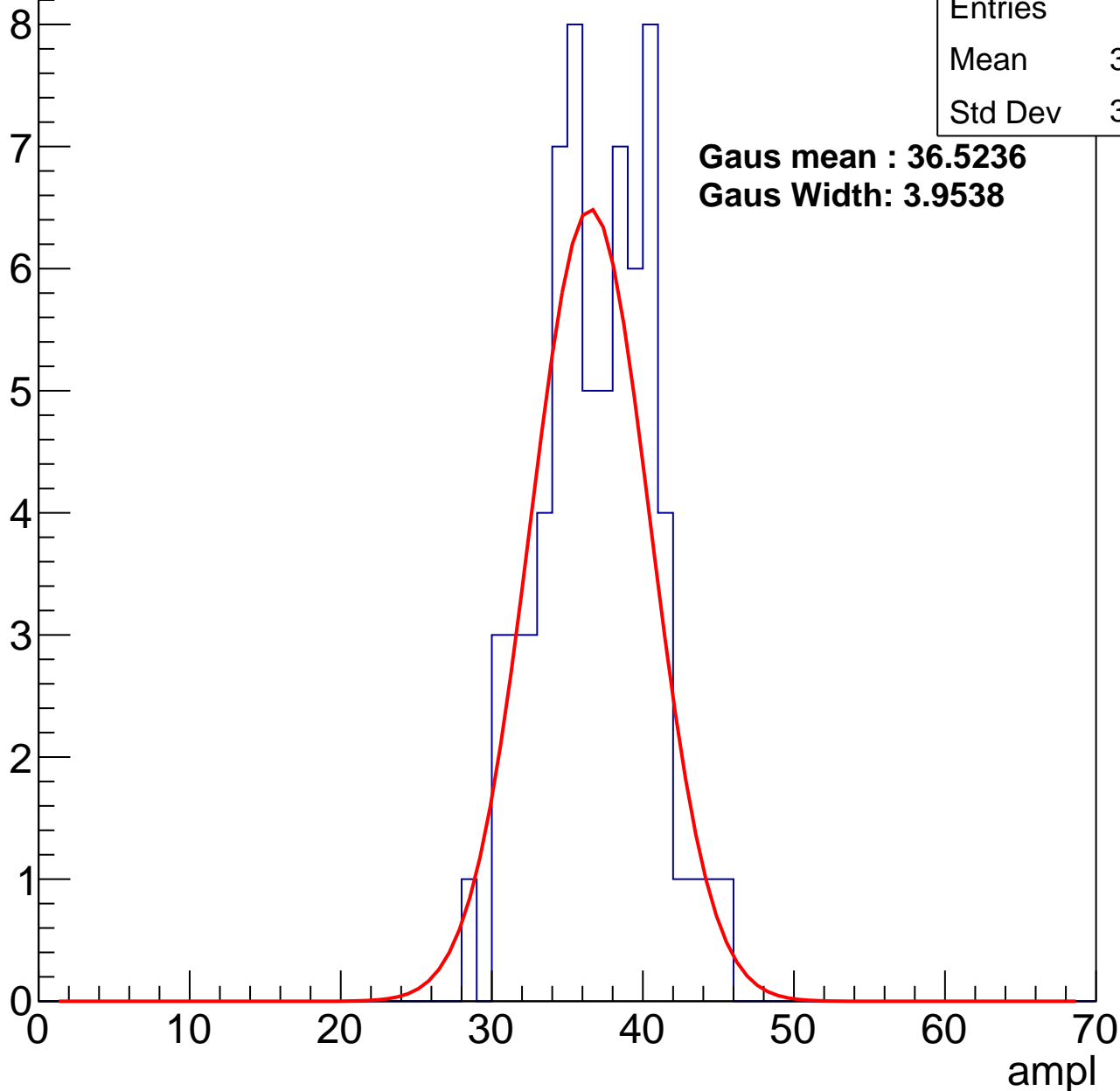
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.47
Std Dev	3.632

Gaus mean : 36.5236

Gaus Width: 3.9538



B1L100S, U11-ch94, adc2

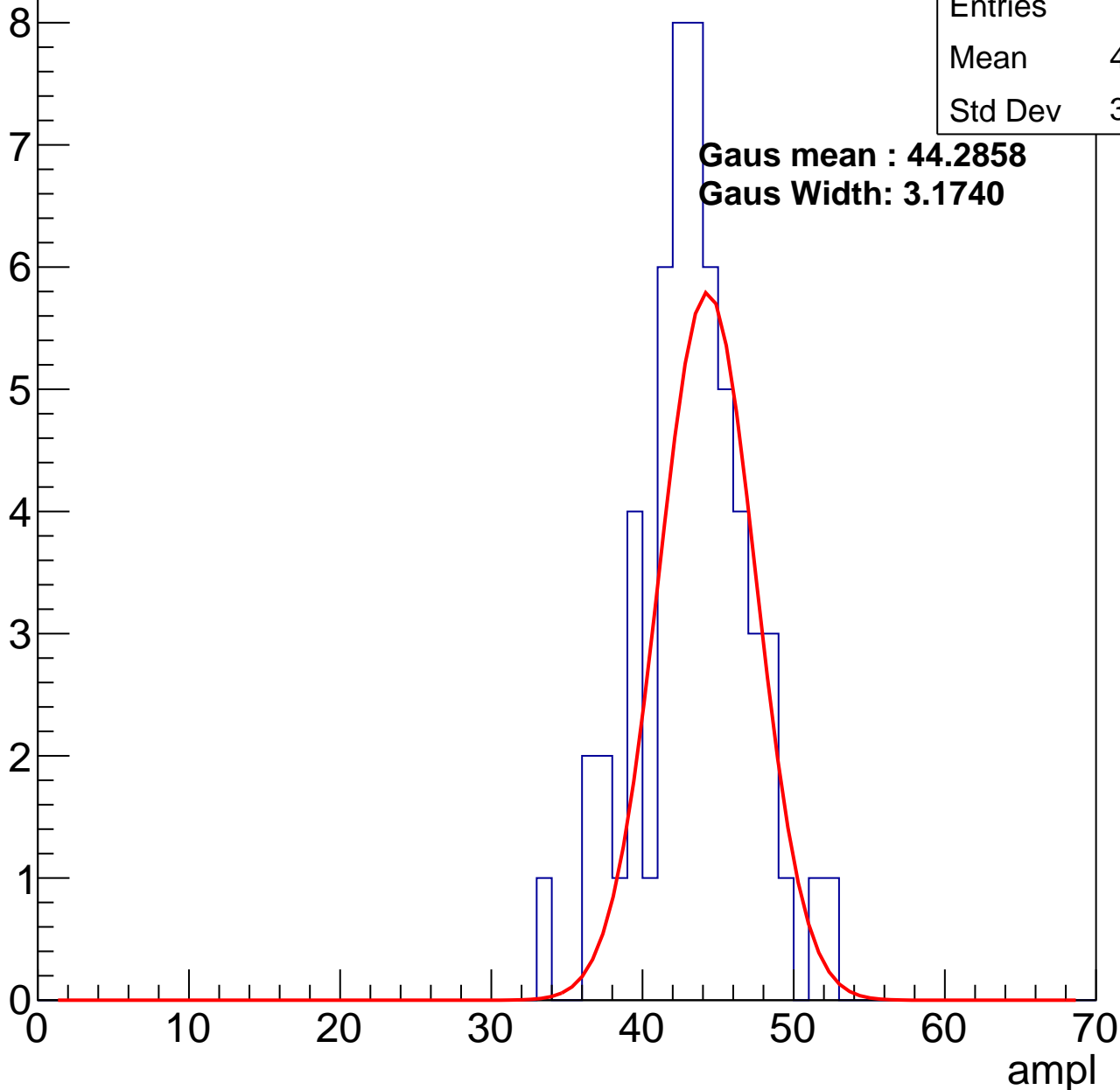
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.96
Std Dev	3.666

Gaus mean : 44.2858

Gaus Width: 3.1740

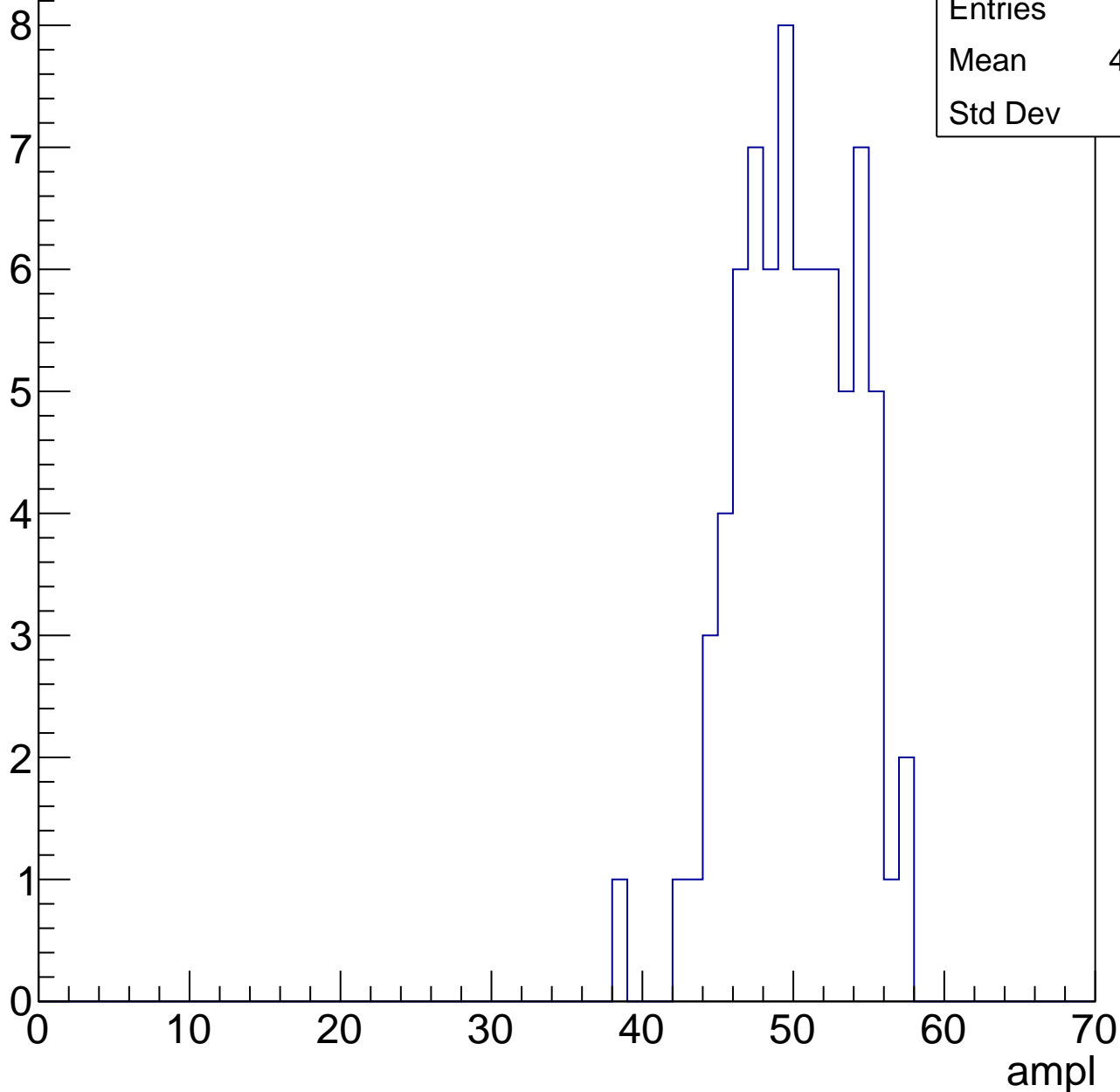


B1L100S, U11-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

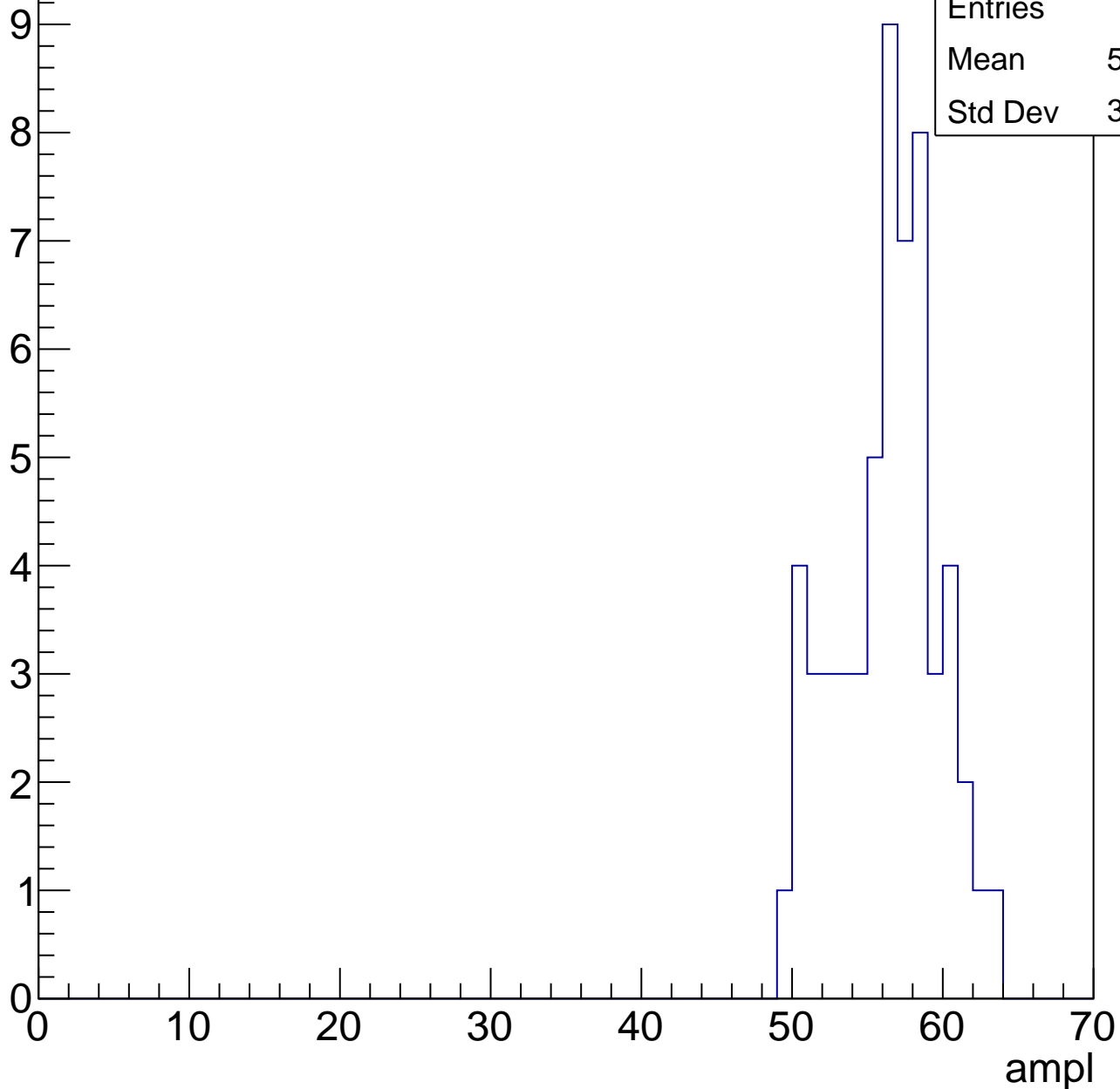
Entries	75
Mean	49.68
Std Dev	3.82



B1L100S, U11-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

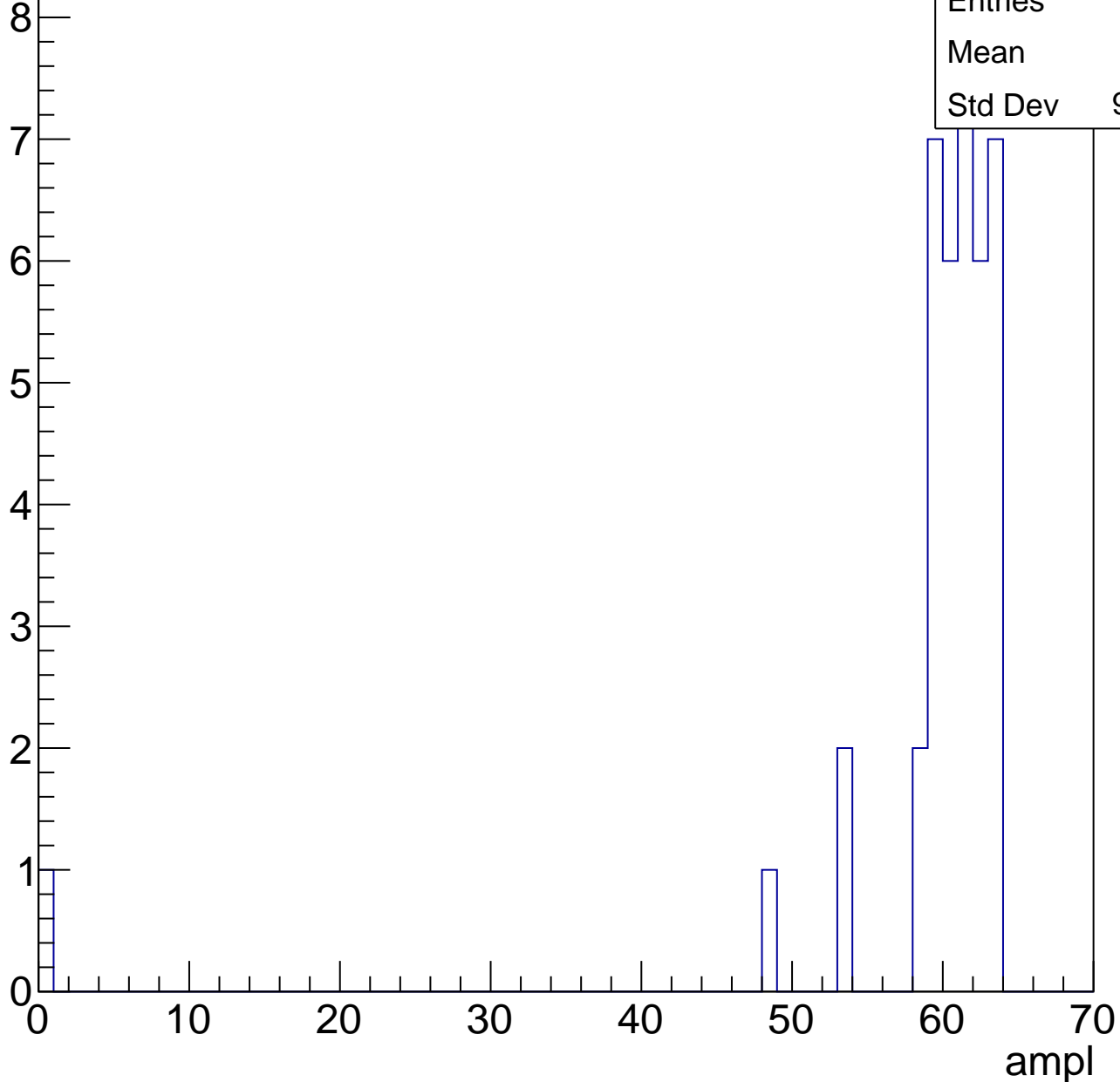


Entries	57
Mean	55.88
Std Dev	3.309

B1L100S, U11-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

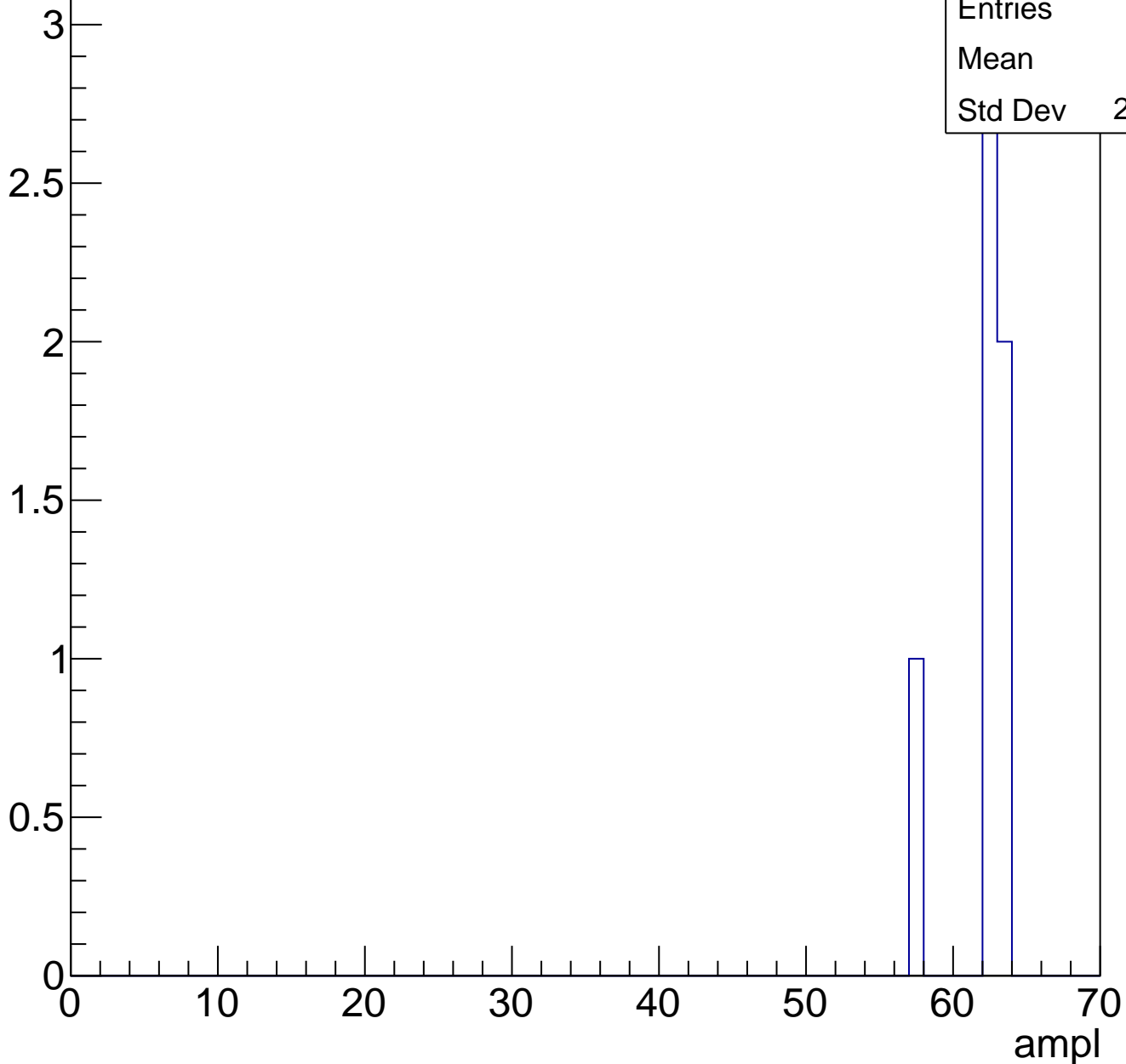
Entry



B1L100S, U11-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch95, adc0

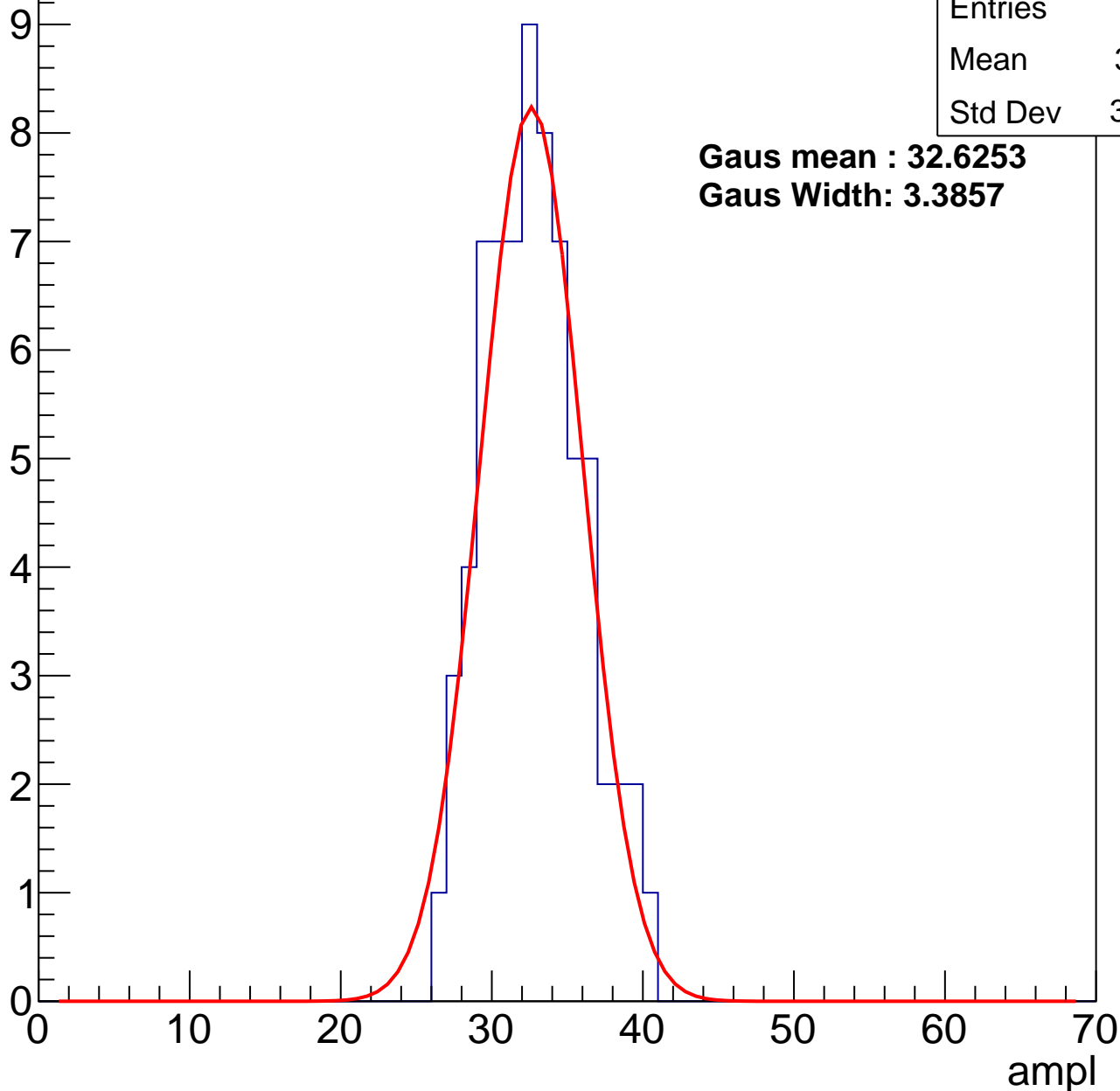
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	32.31
Std Dev	3.187

Gaus mean : 32.6253

Gaus Width: 3.3857



B1L100S, U11-ch95, adc1

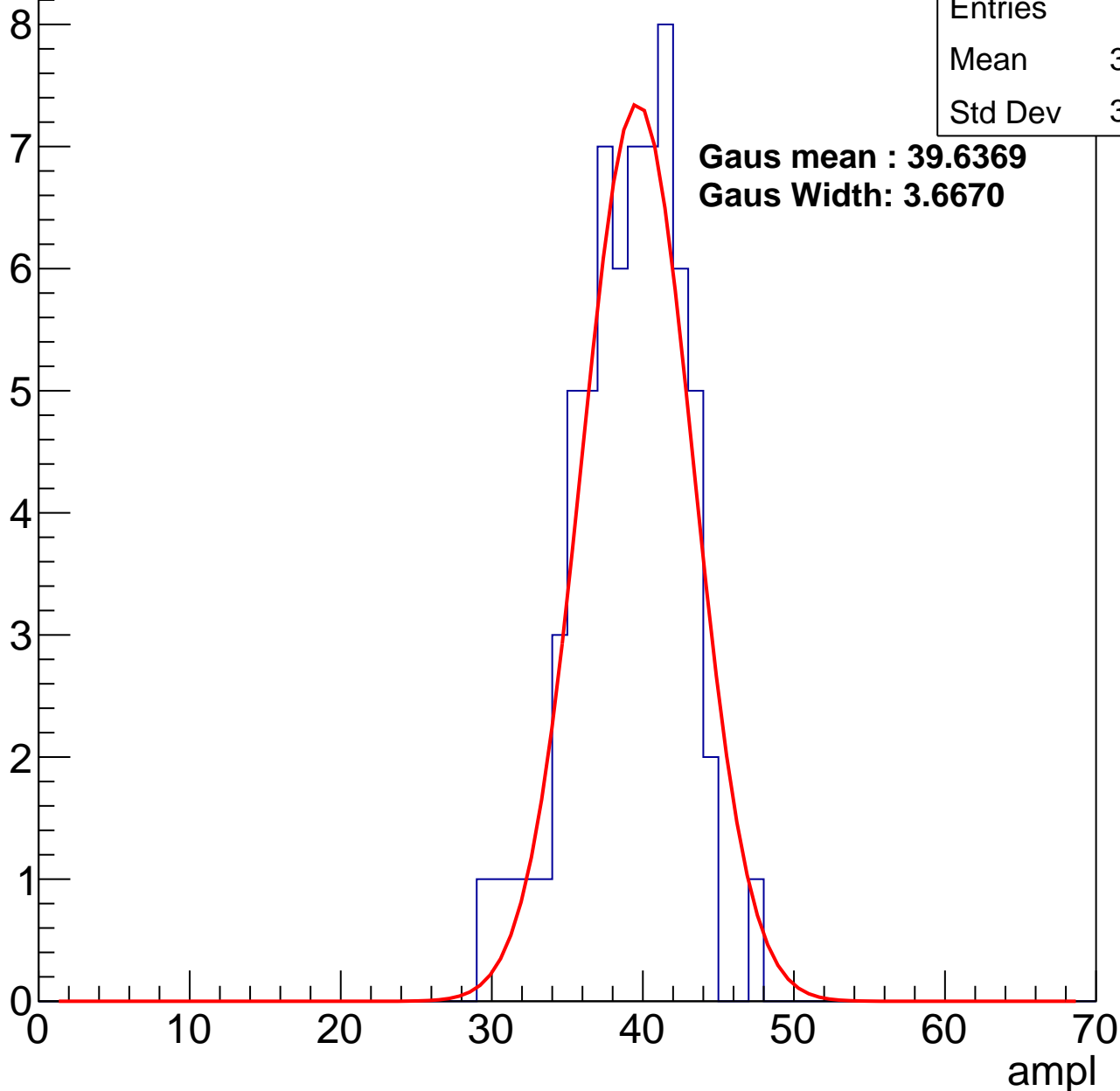
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	38.54
Std Dev	3.534

Gaus mean : 39.6369

Gaus Width: 3.6670



B1L100S, U11-ch95, adc2

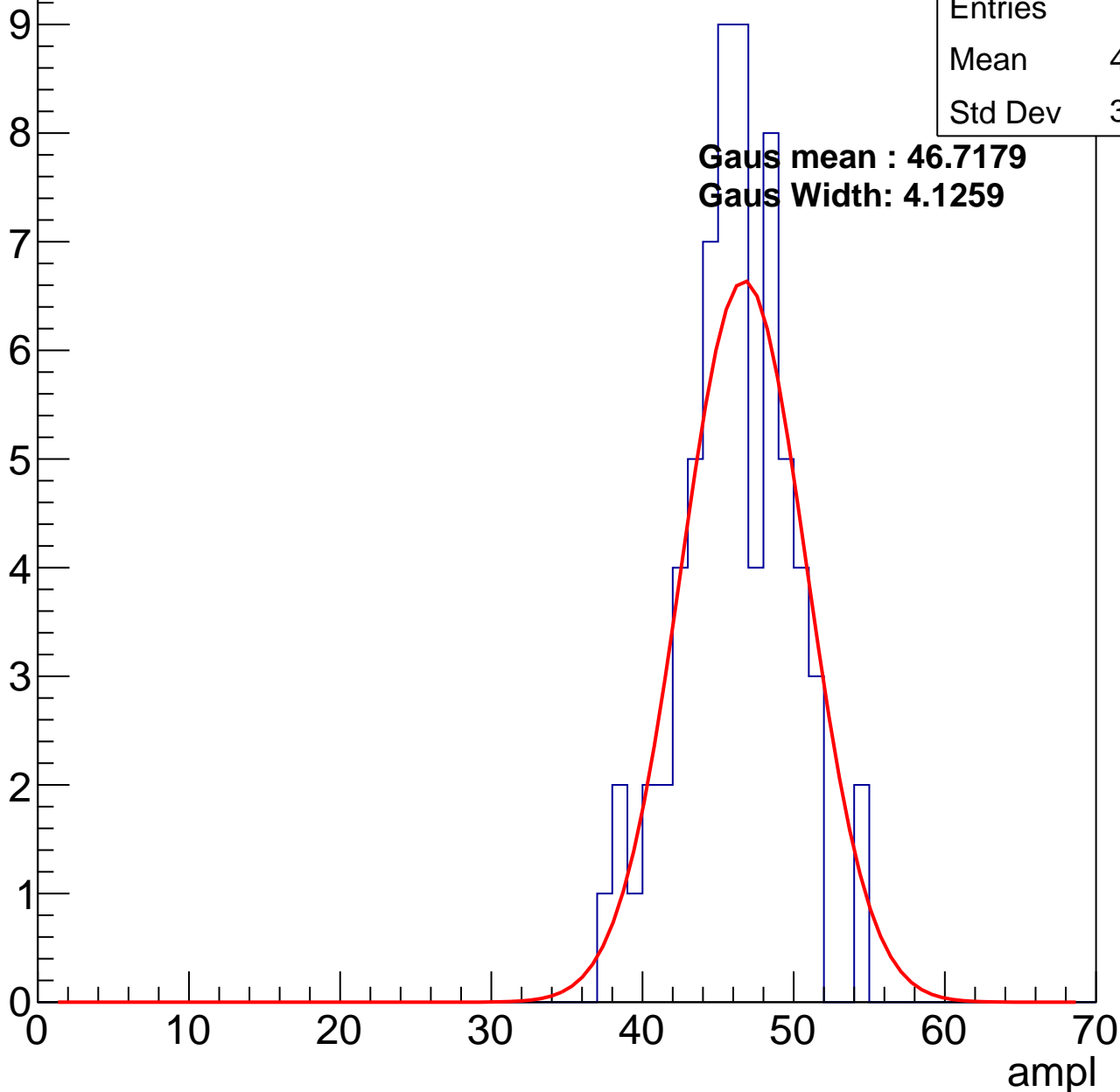
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	45.62
Std Dev	3.565

Gaus mean : 46.7179

Gaus Width: 4.1259

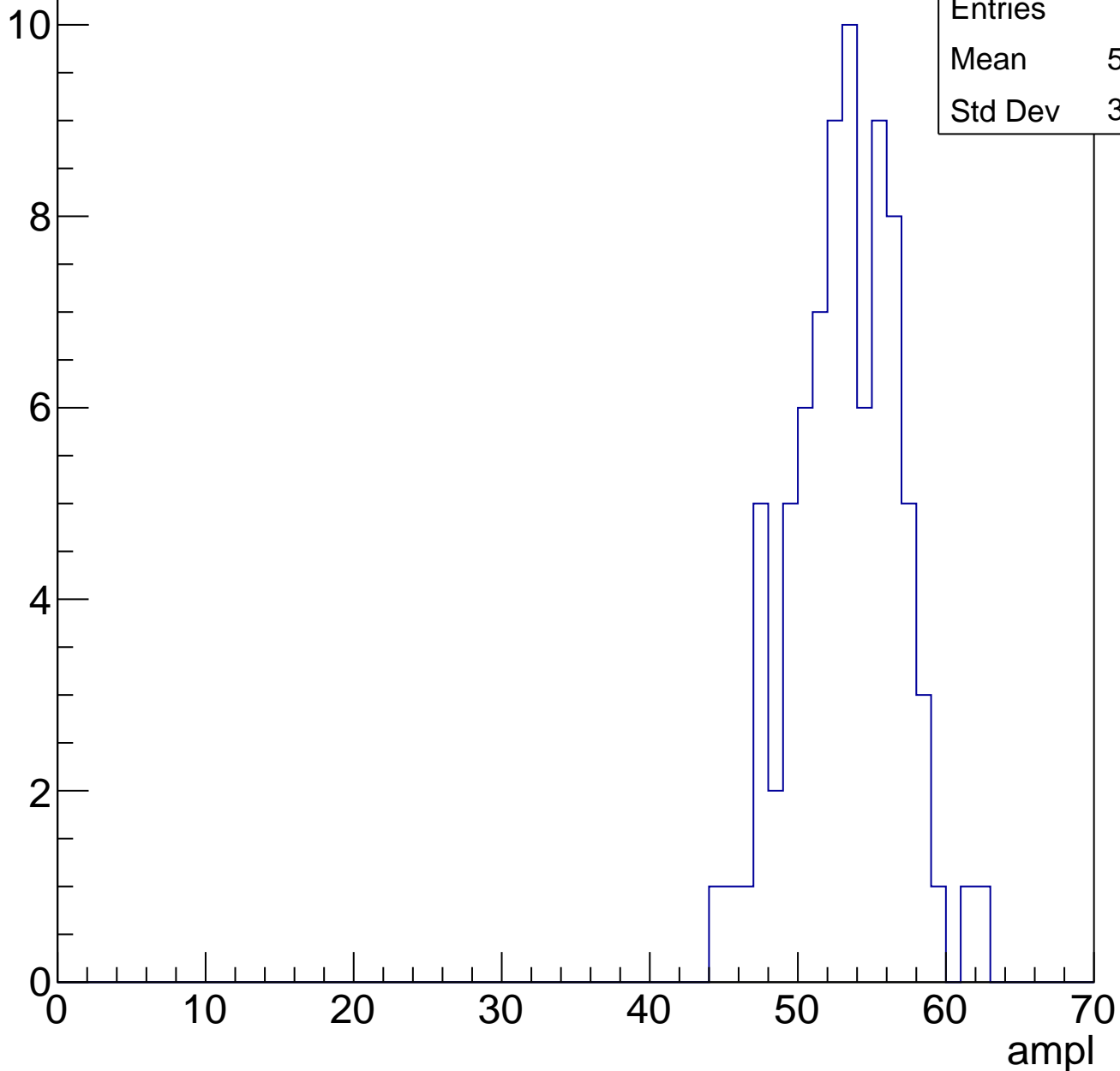


B1L100S, U11-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

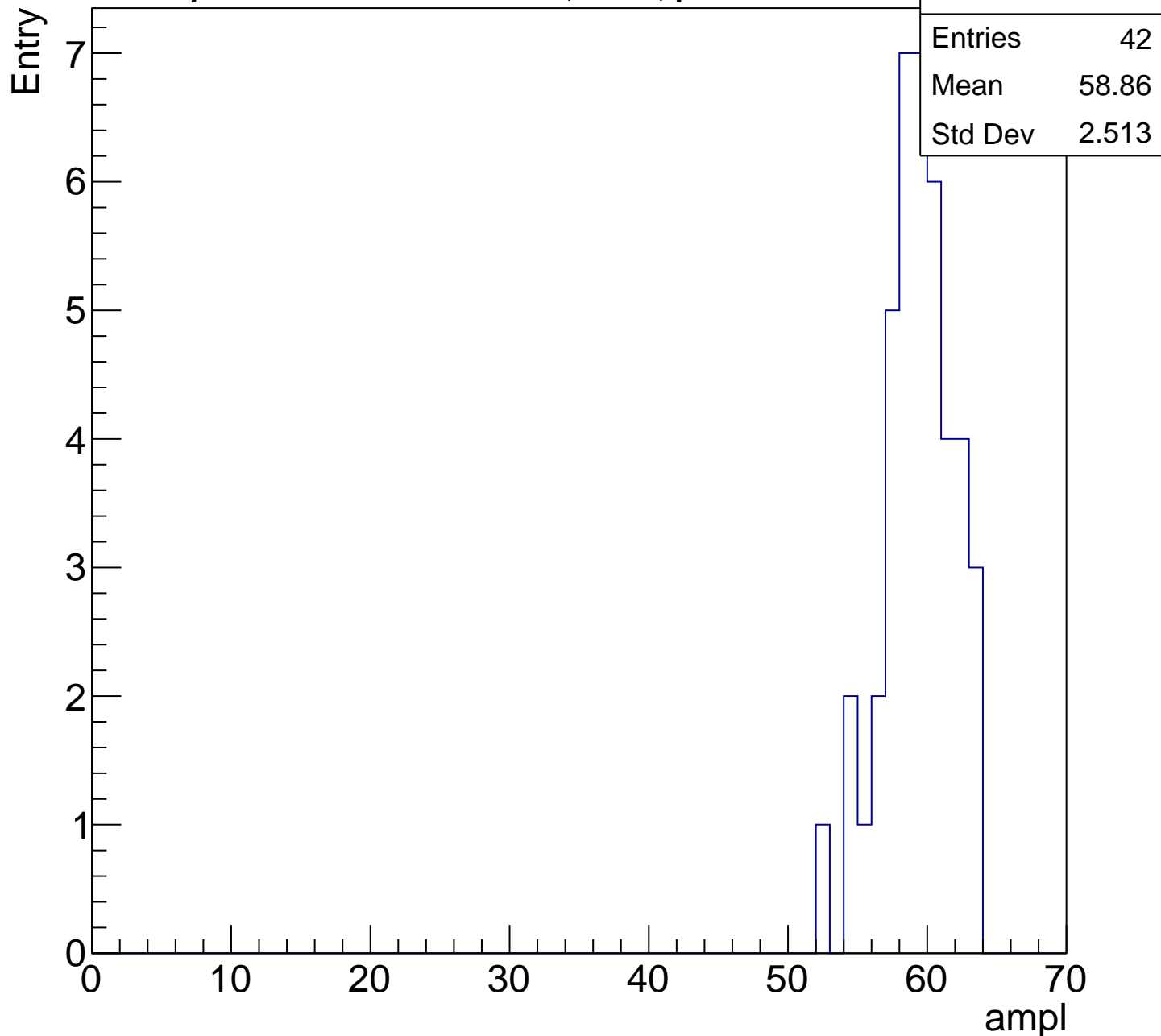
Entries	81
Mean	52.77
Std Dev	3.584

Entry



B1L100S, U11-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

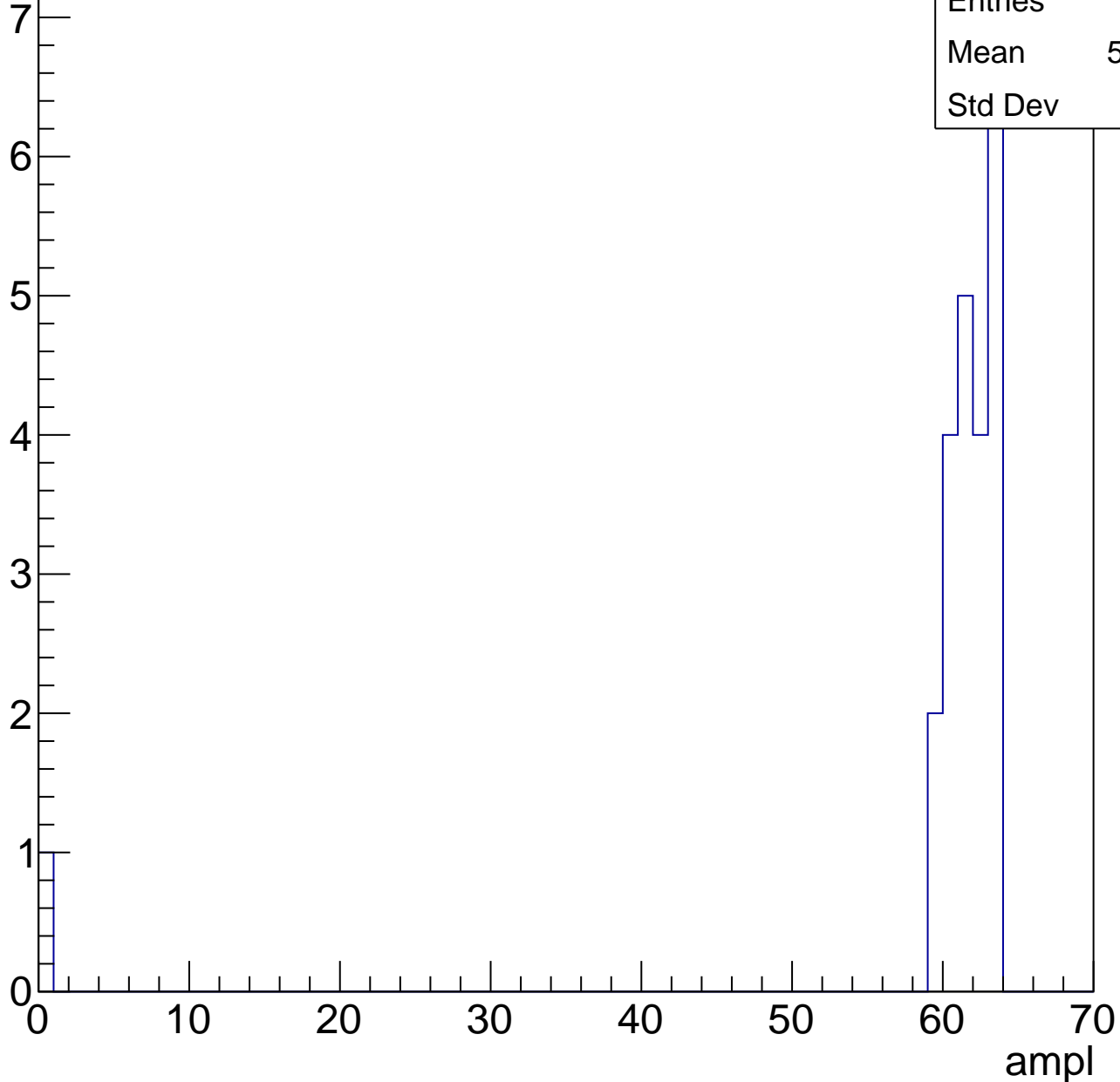


B1L100S, U11-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	58.78
Std Dev	12.6



B1L100S, U11-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch95, adc7

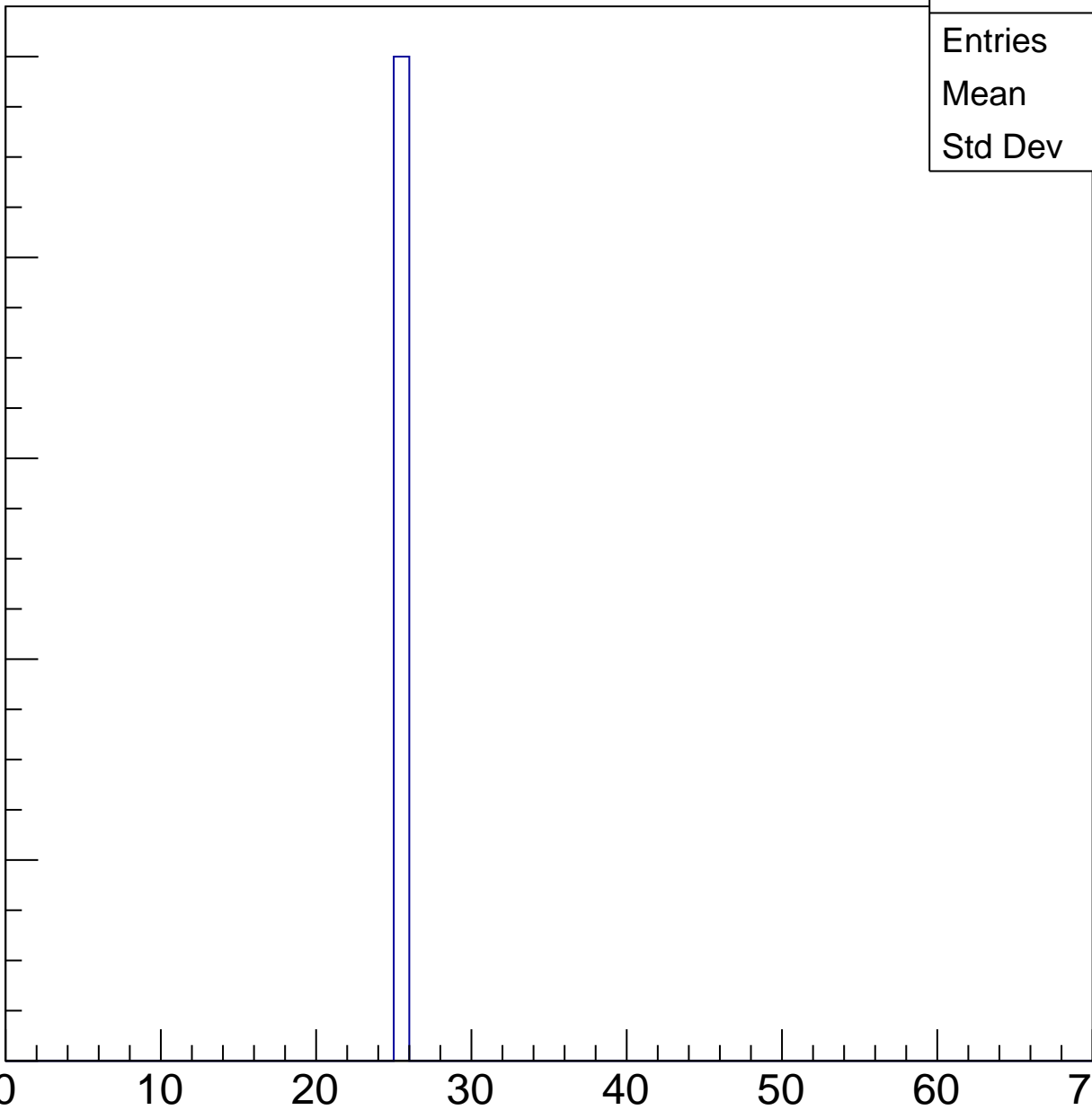
calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L100S, U11-ch96, adc0

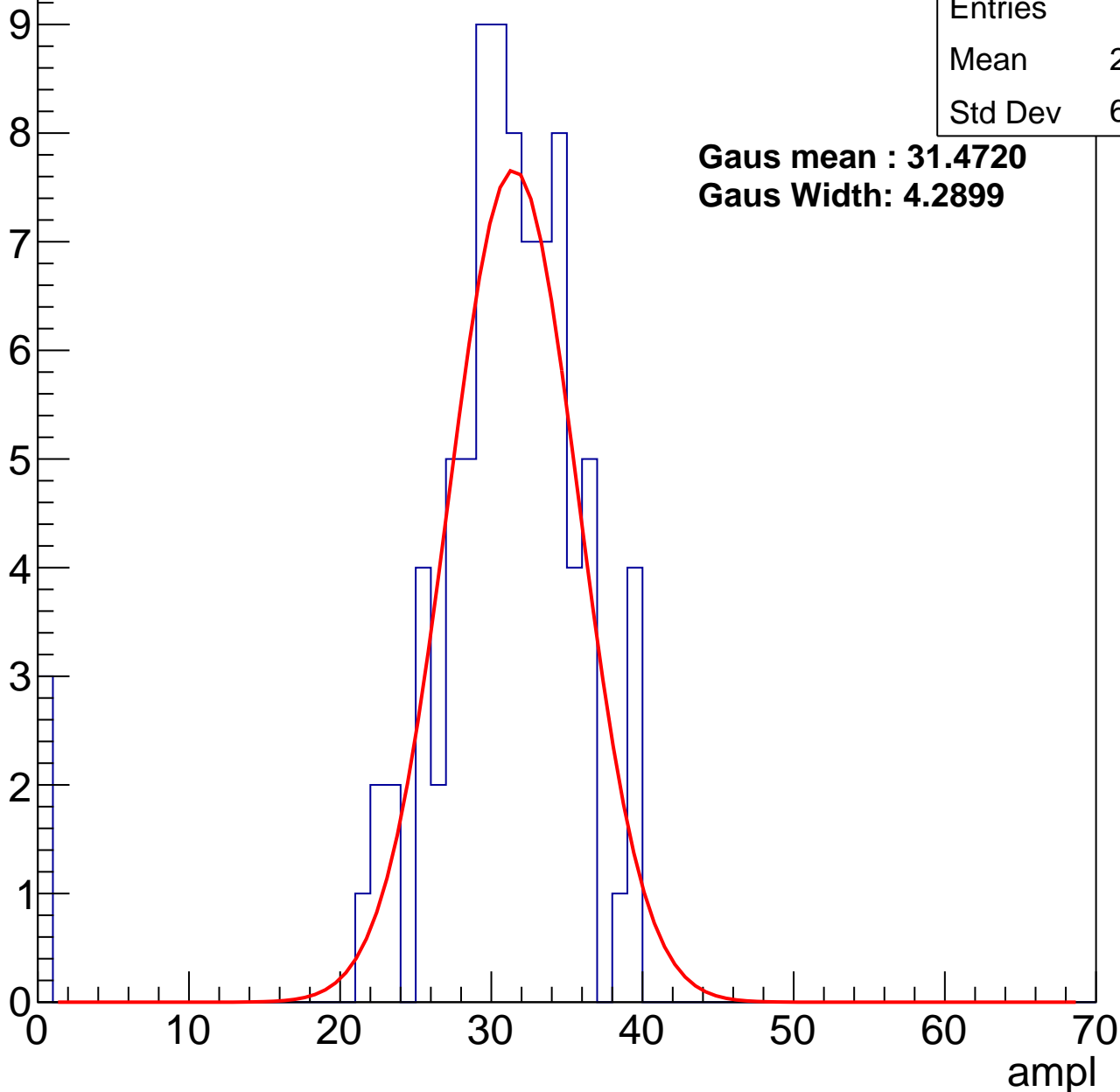
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	29.74
Std Dev	6.907

Gaus mean : 31.4720

Gaus Width: 4.2899



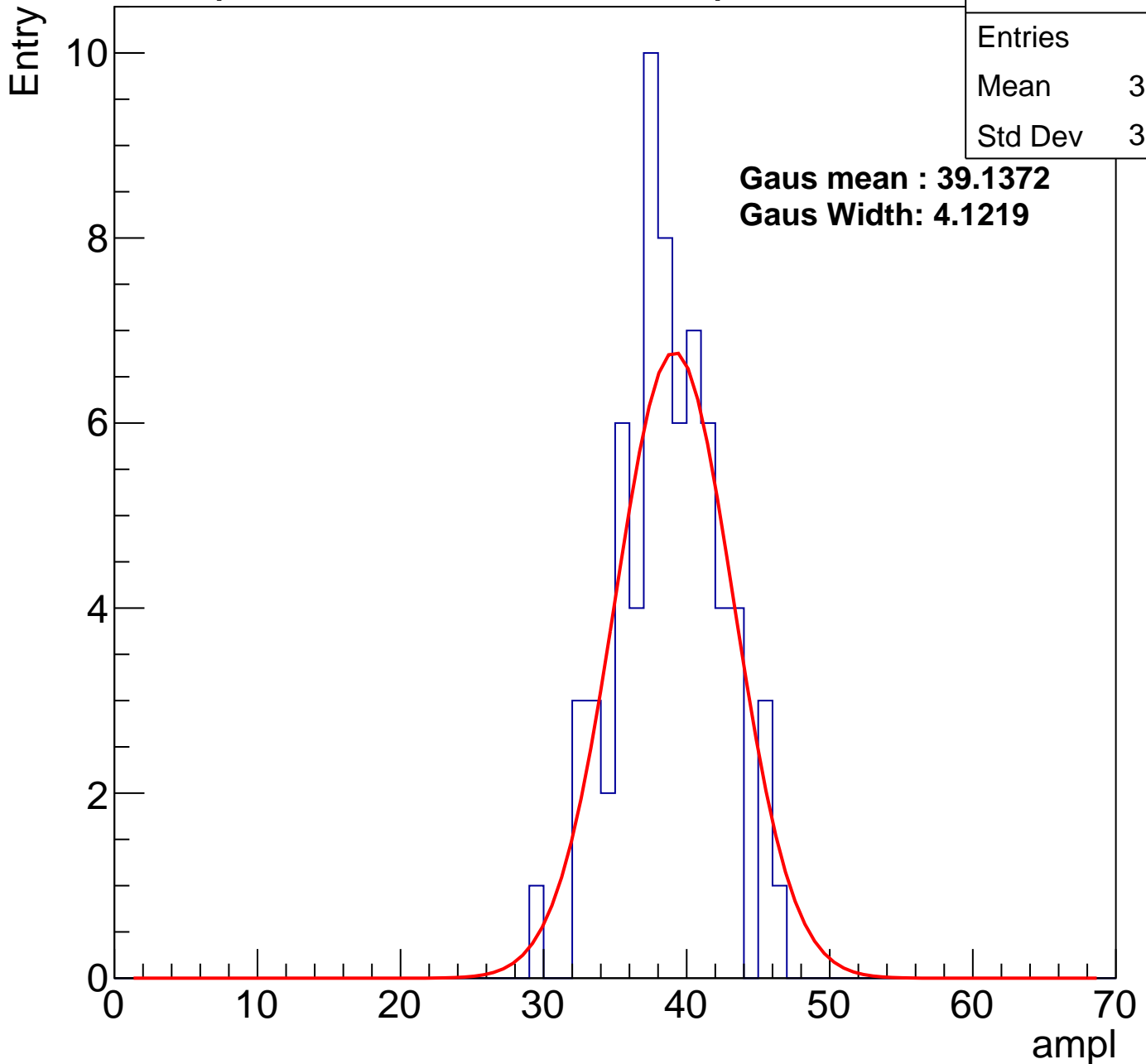
B1L100S, U11-ch96, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	38.25
Std Dev	3.504

Gaus mean : 39.1372

Gaus Width: 4.1219



B1L100S, U11-ch96, adc2

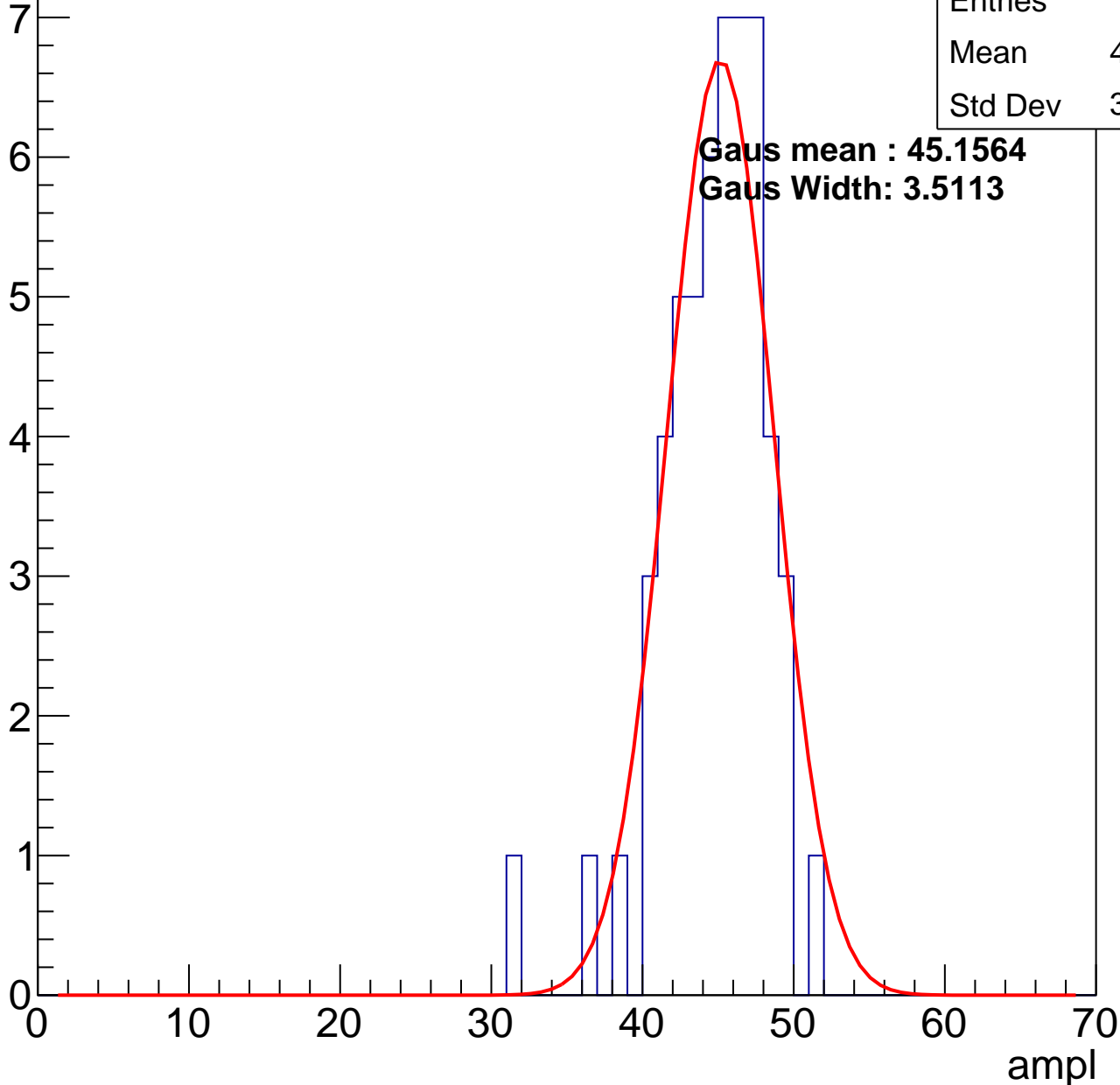
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	44.25
Std Dev	3.465

Gaus mean : 45.1564

Gaus Width: 3.5113

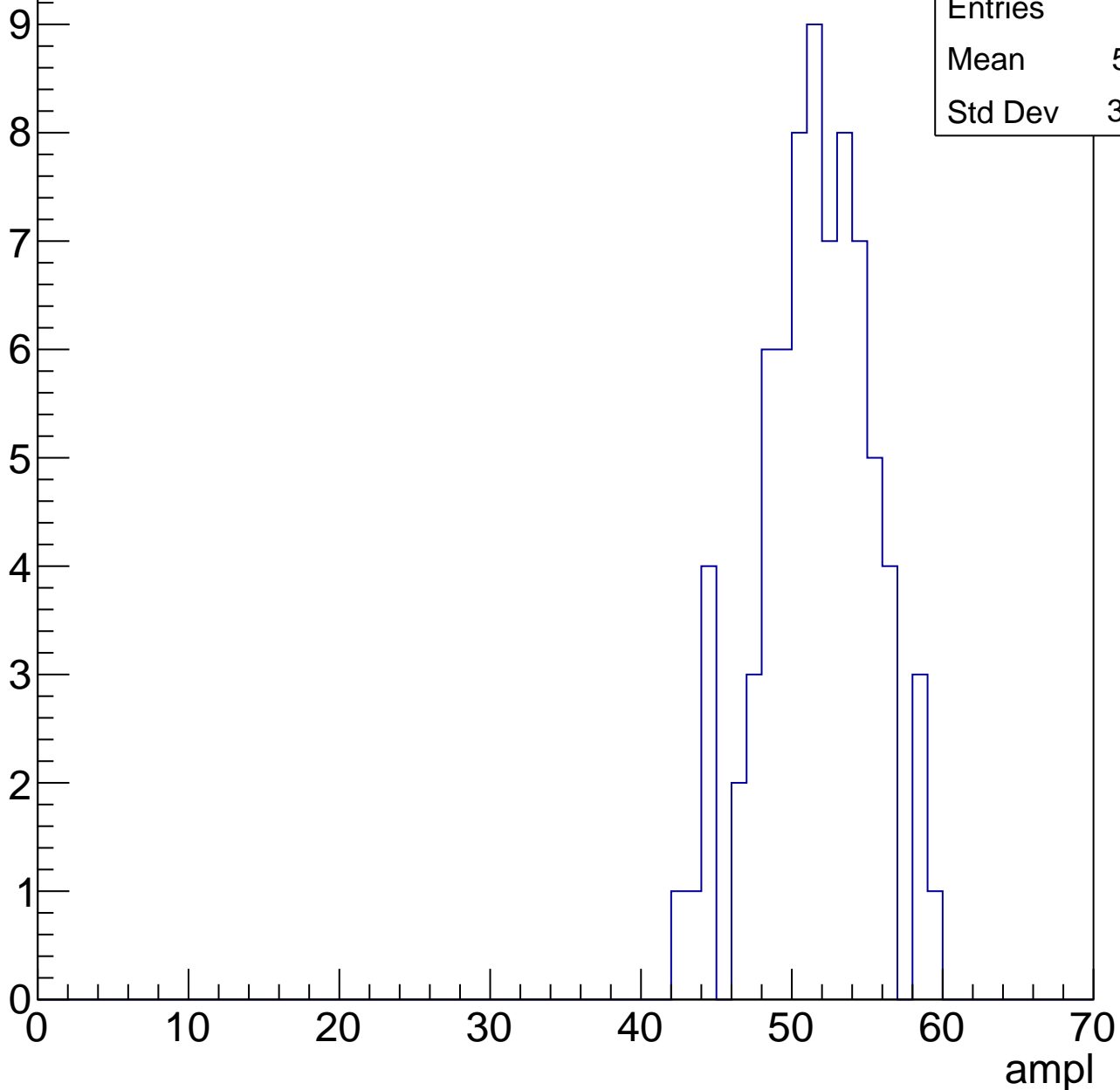


B1L100S, U11-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	51.11
Std Dev	3.683

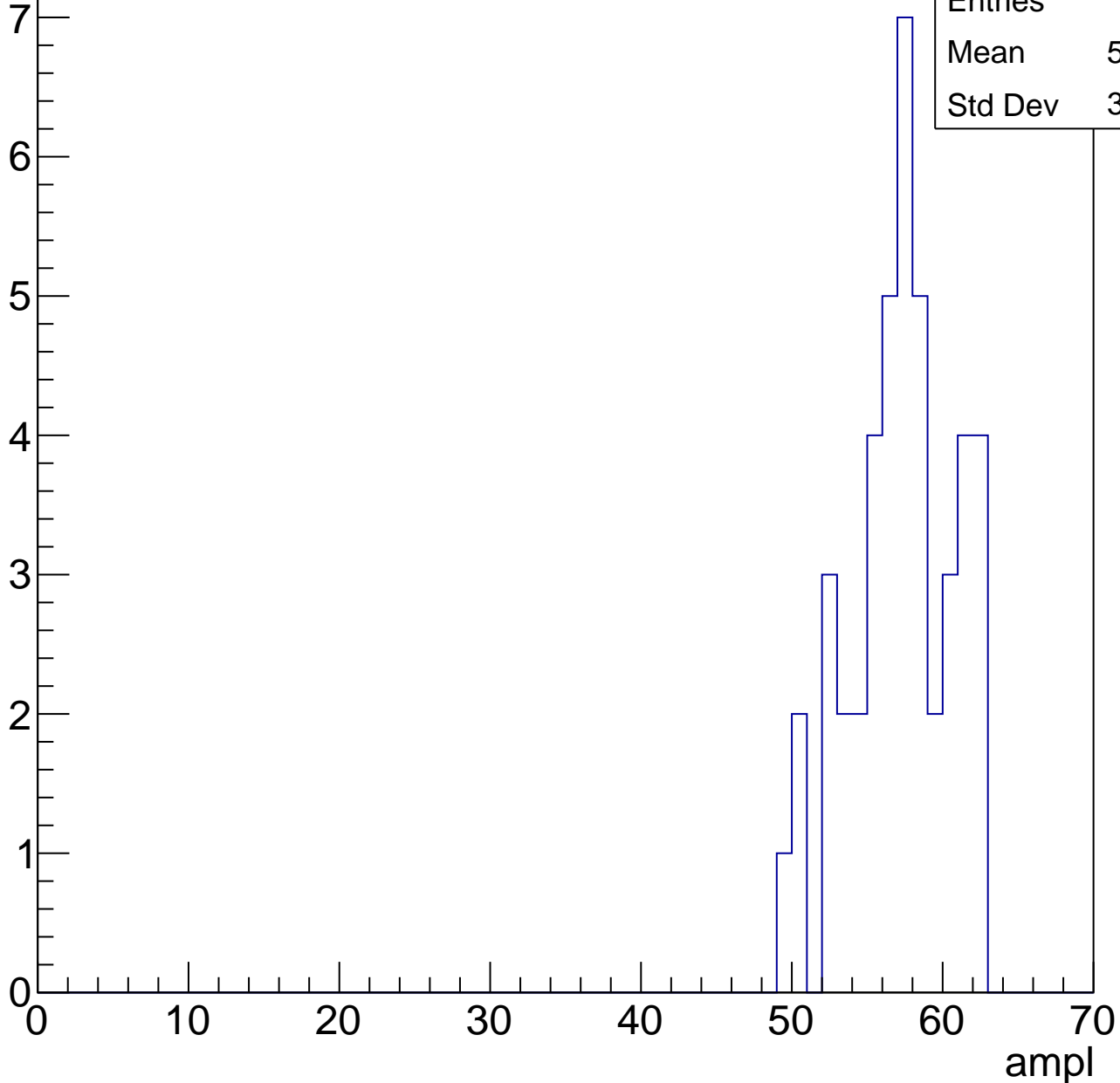


B1L100S, U11-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	56.77
Std Dev	3.404

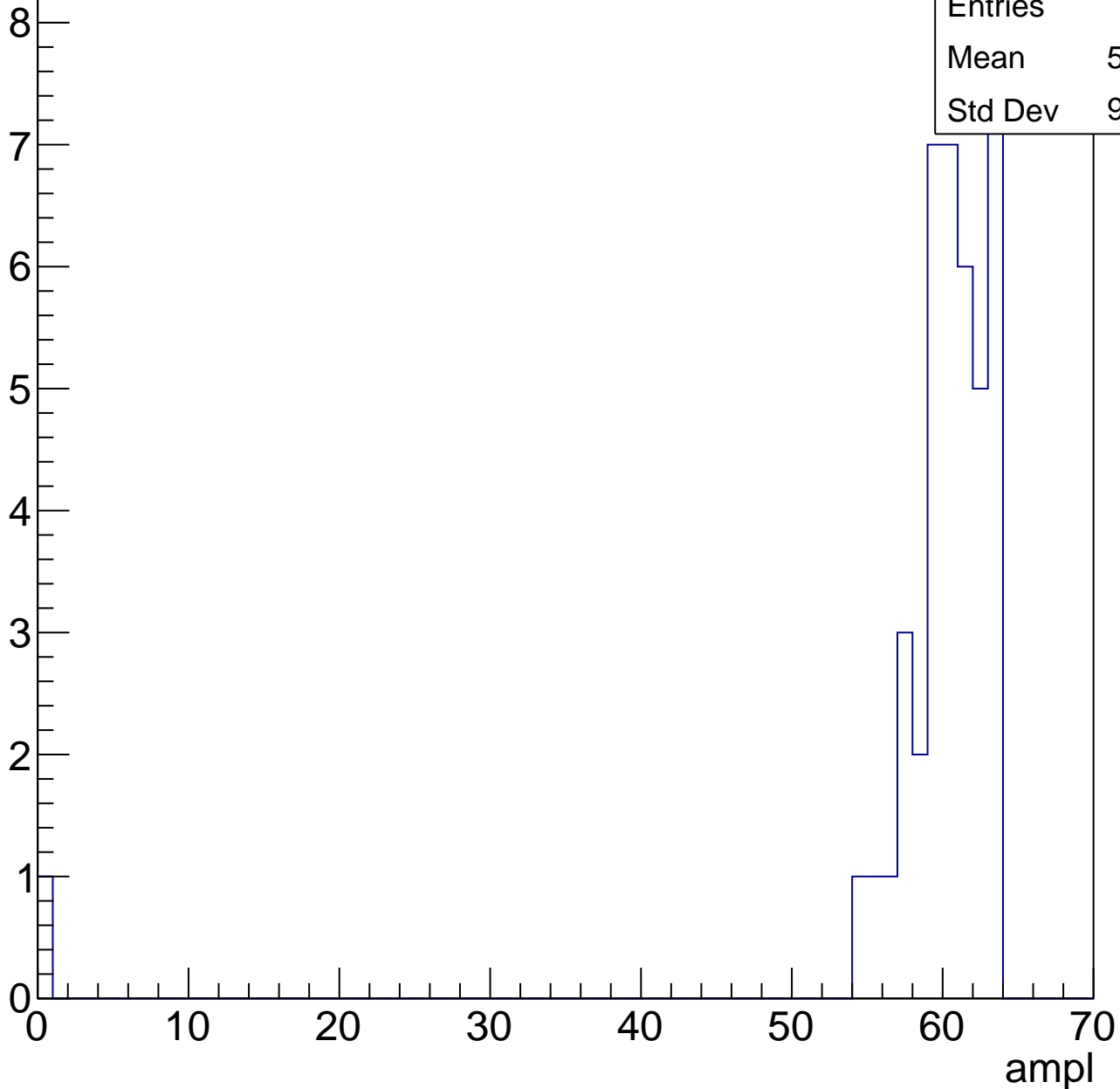


B1L100S, U11-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	58.69
Std Dev	9.443



B1L100S, U11-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L100S, U11-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch97, adc0

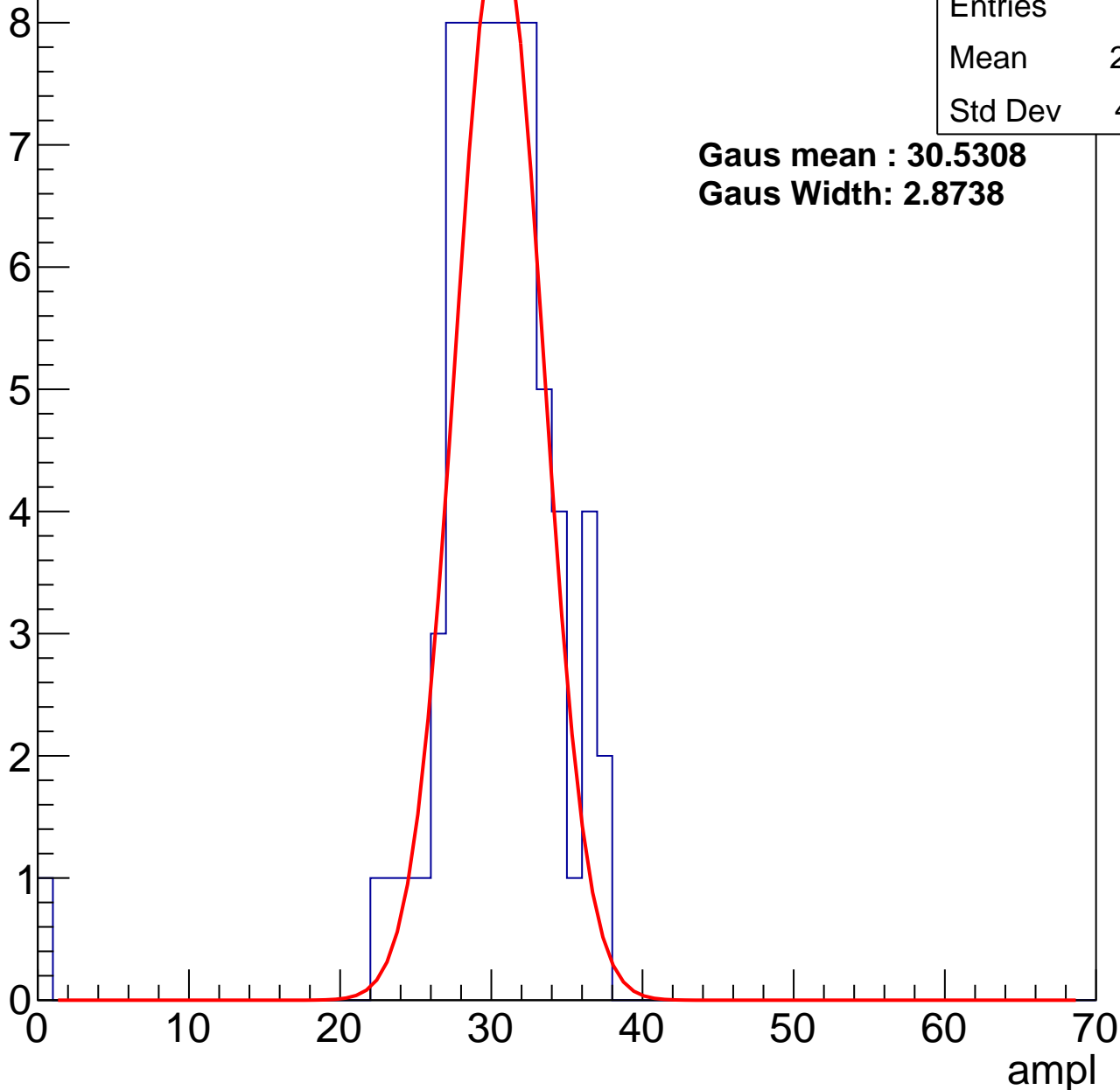
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	29.75
Std Dev	4.781

Gaus mean : 30.5308

Gaus Width: 2.8738



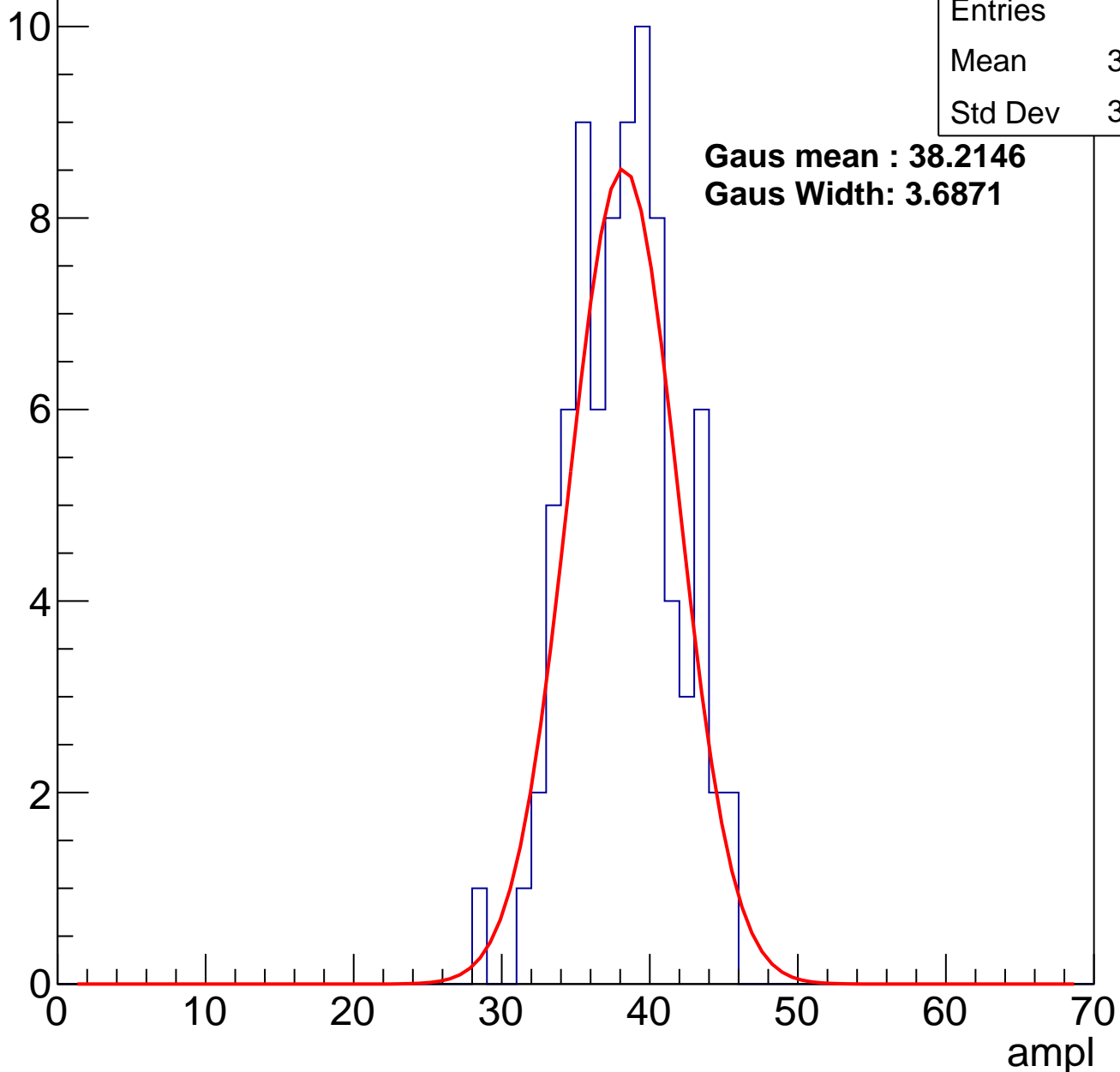
B1L100S, U11-ch97, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	37.77
Std Dev	3.486

Gaus mean : 38.2146
Gaus Width: 3.6871

Entry



B1L100S, U11-ch97, adc2

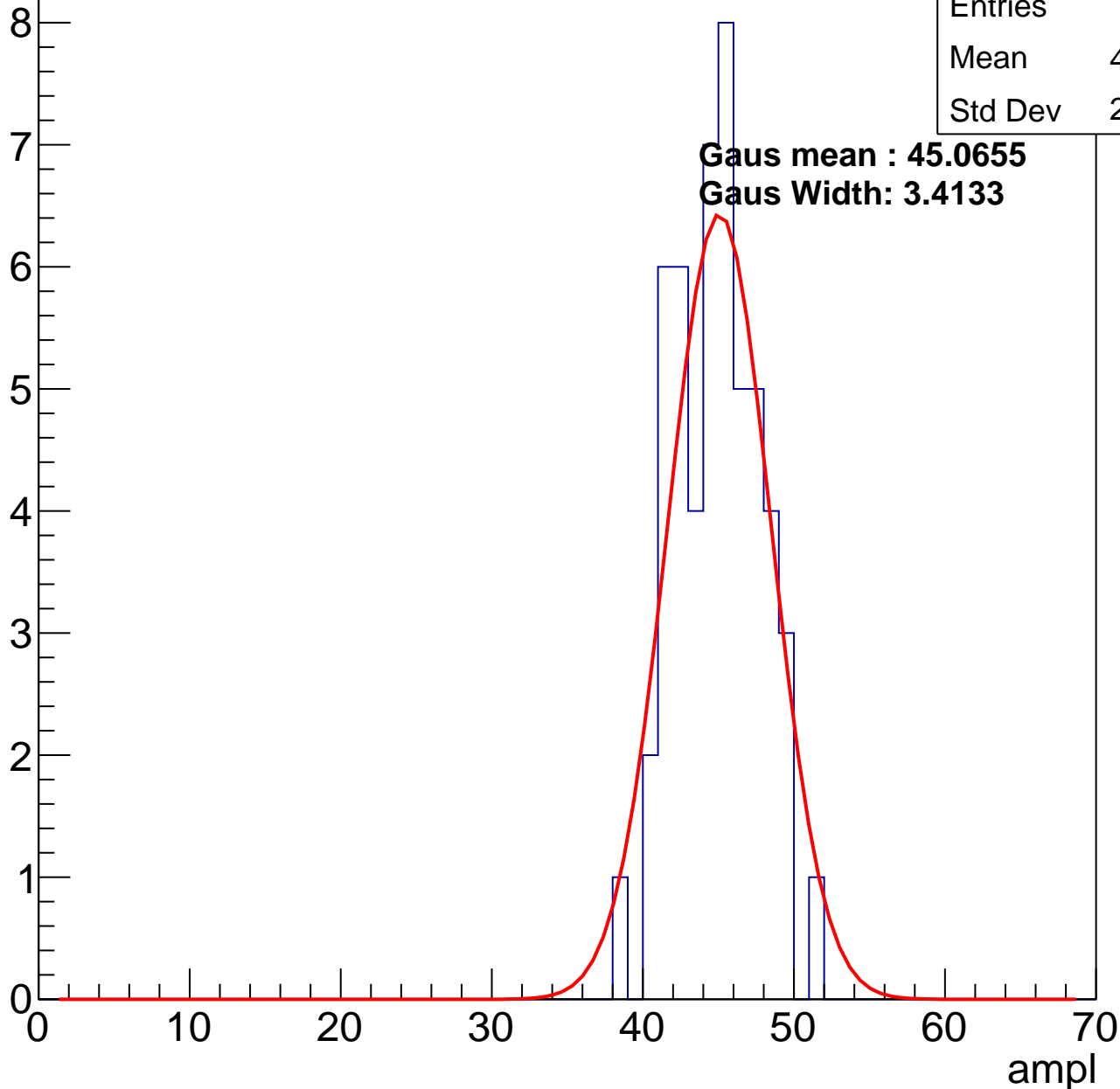
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	44.44
Std Dev	2.776

Gaus mean : 45.0655

Gaus Width: 3.4133

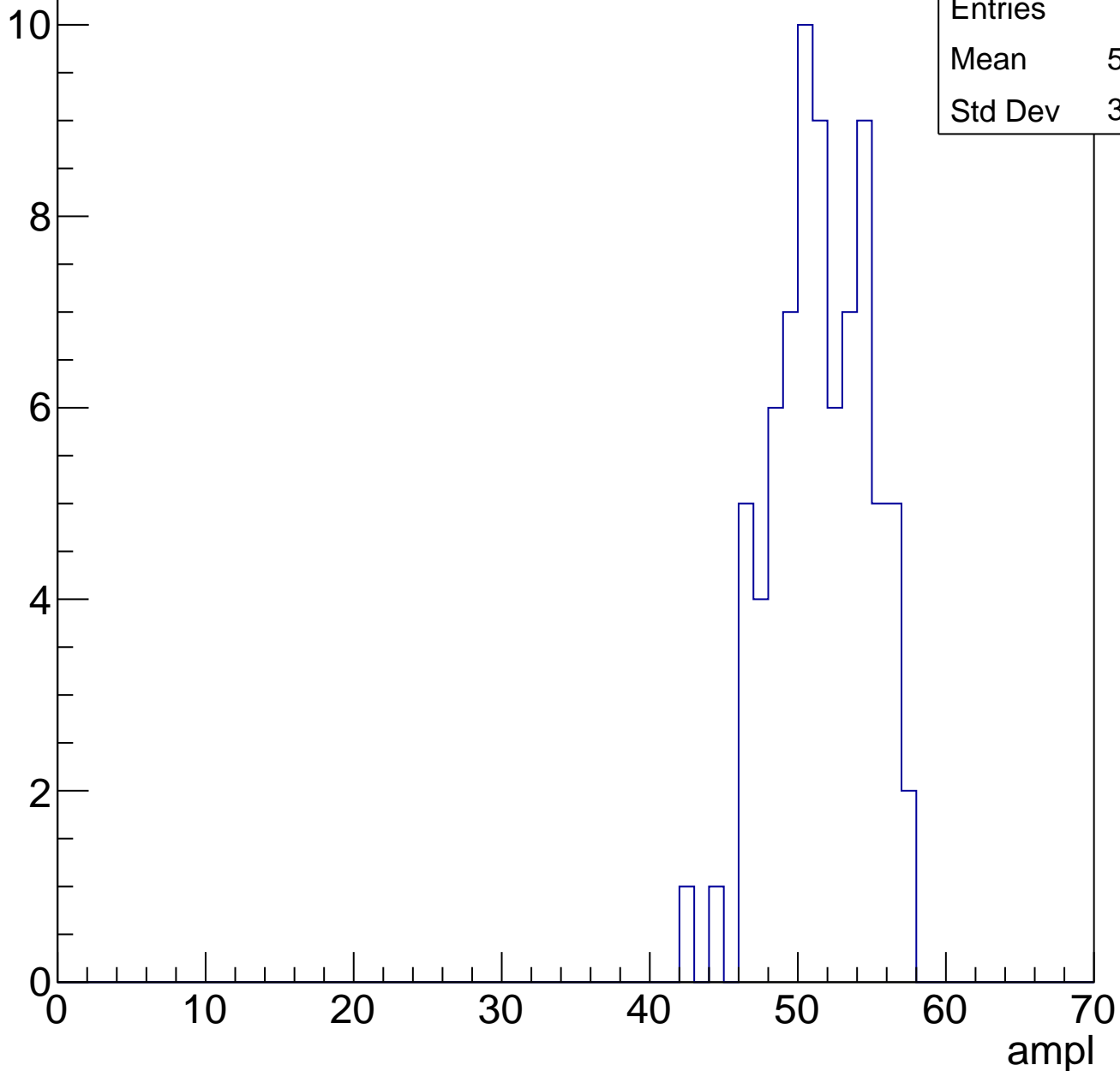


B1L100S, U11-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	51.06
Std Dev	3.229

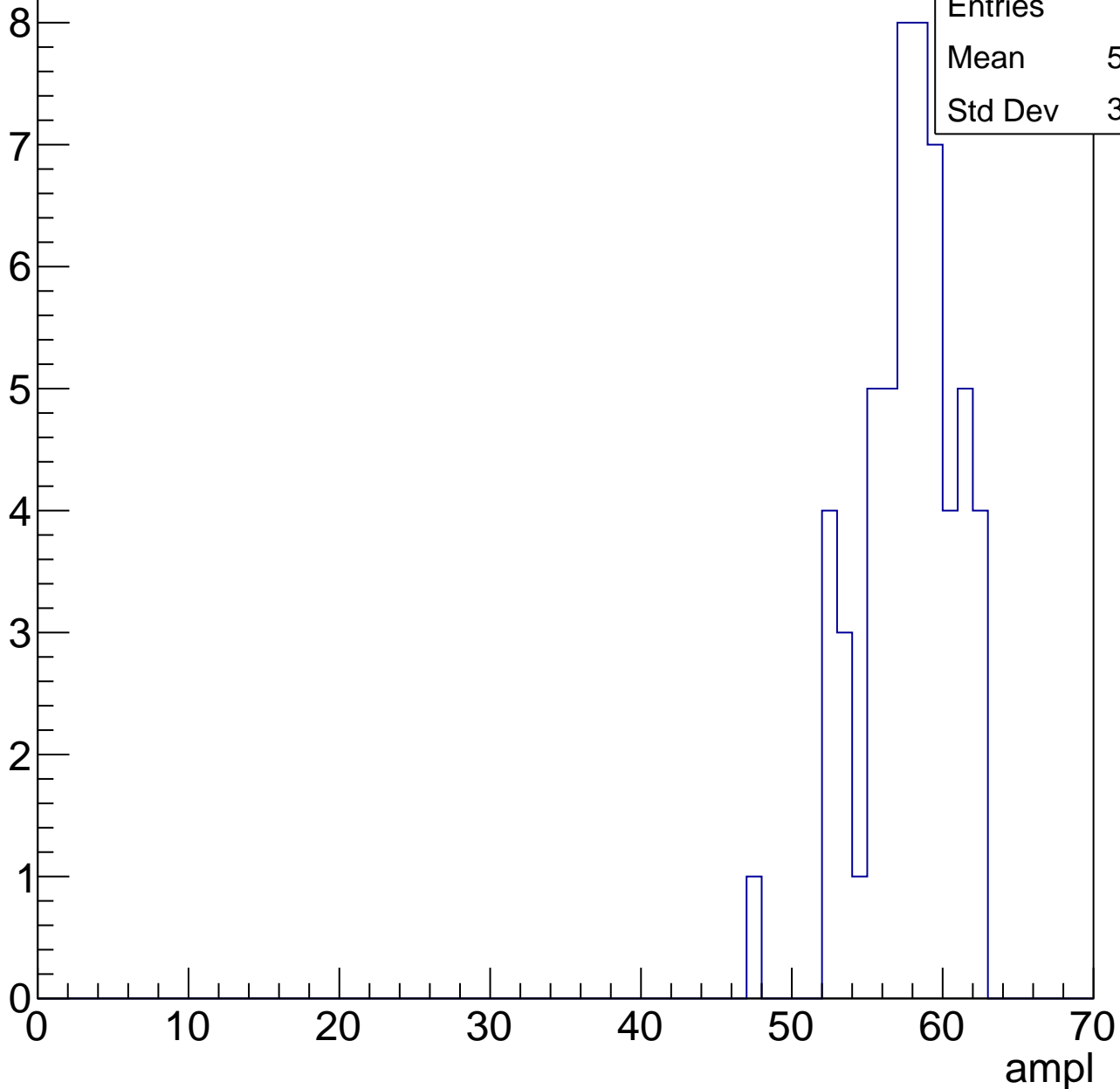


B1L100S, U11-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	57.25
Std Dev	3.106

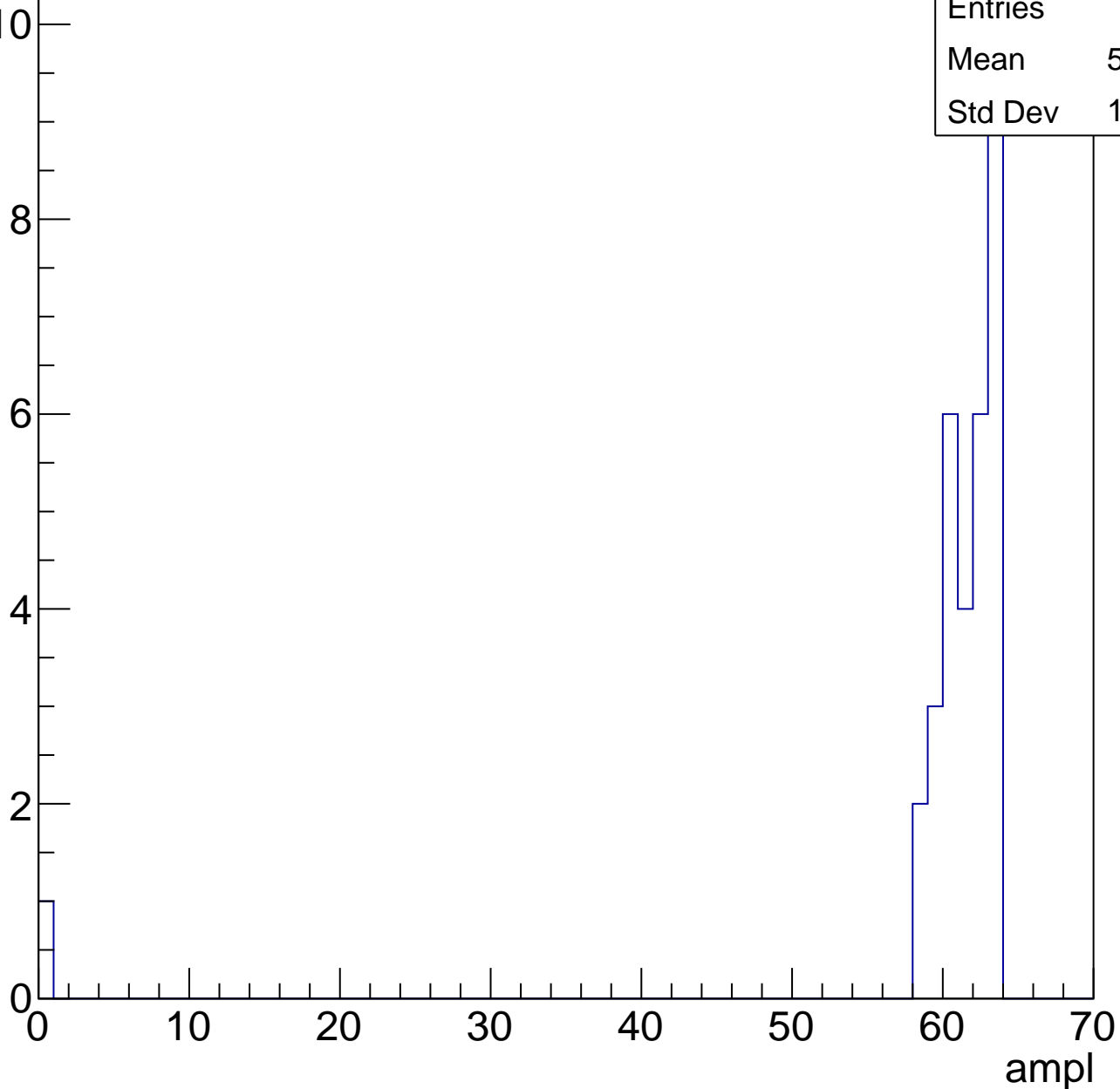


B1L100S, U11-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	59.34
Std Dev	10.77



B1L100S, U11-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch98, adc0

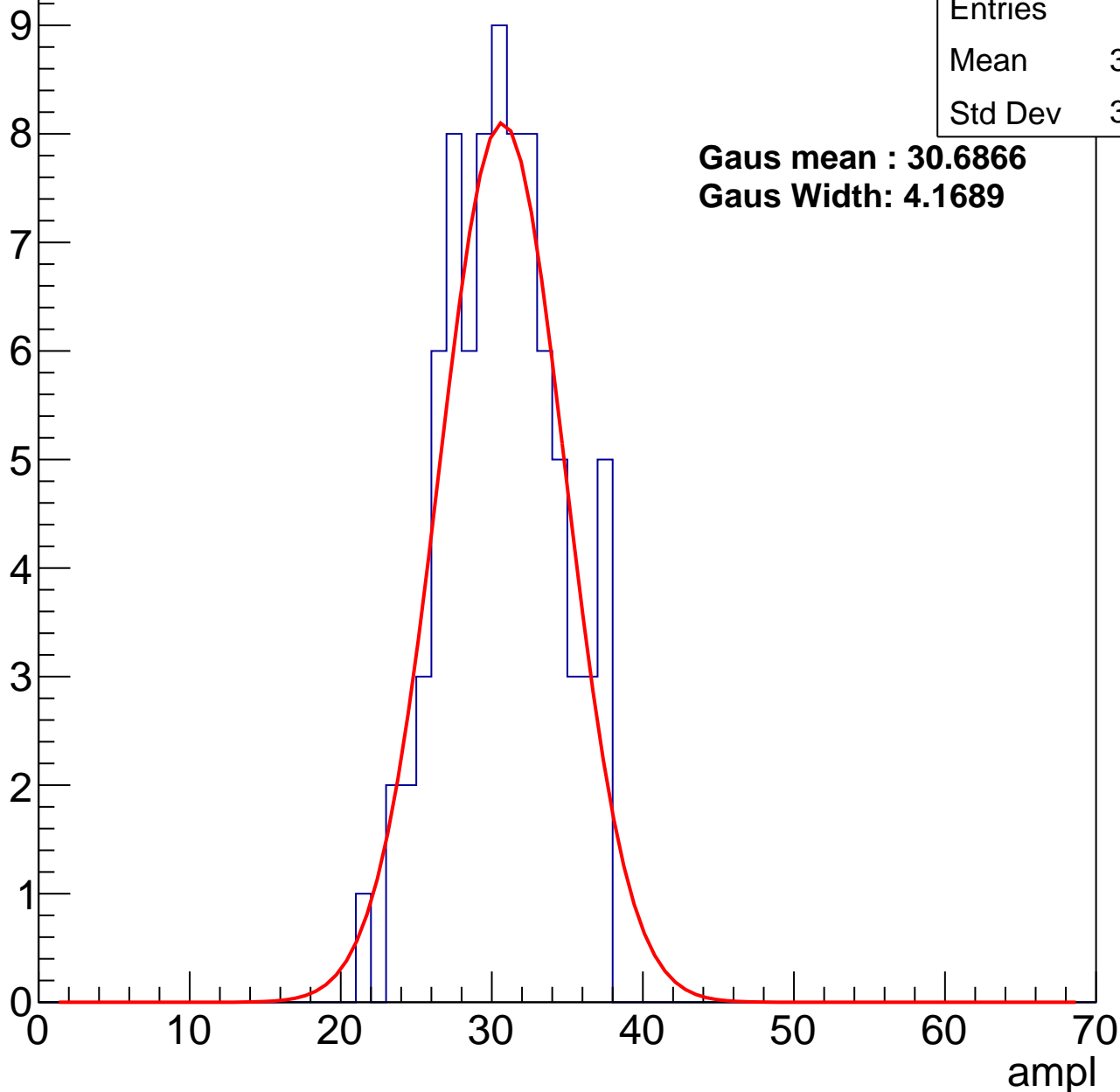
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	30.14
Std Dev	3.684

Gaus mean : 30.6866

Gaus Width: 4.1689



B1L100S, U11-ch98, adc1

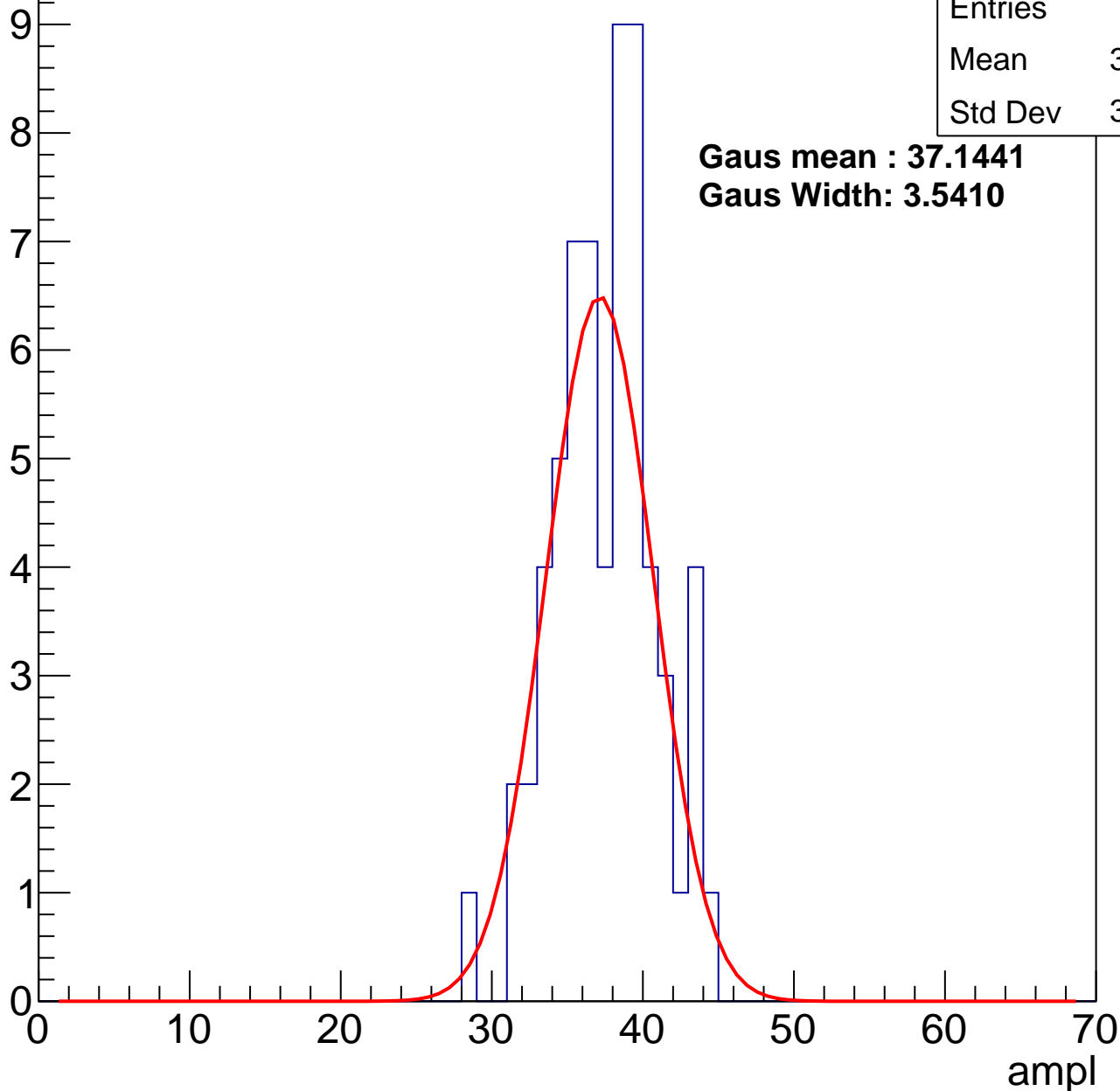
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.06
Std Dev	3.318

Gaus mean : 37.1441

Gaus Width: 3.5410



B1L100S, U11-ch98, adc2

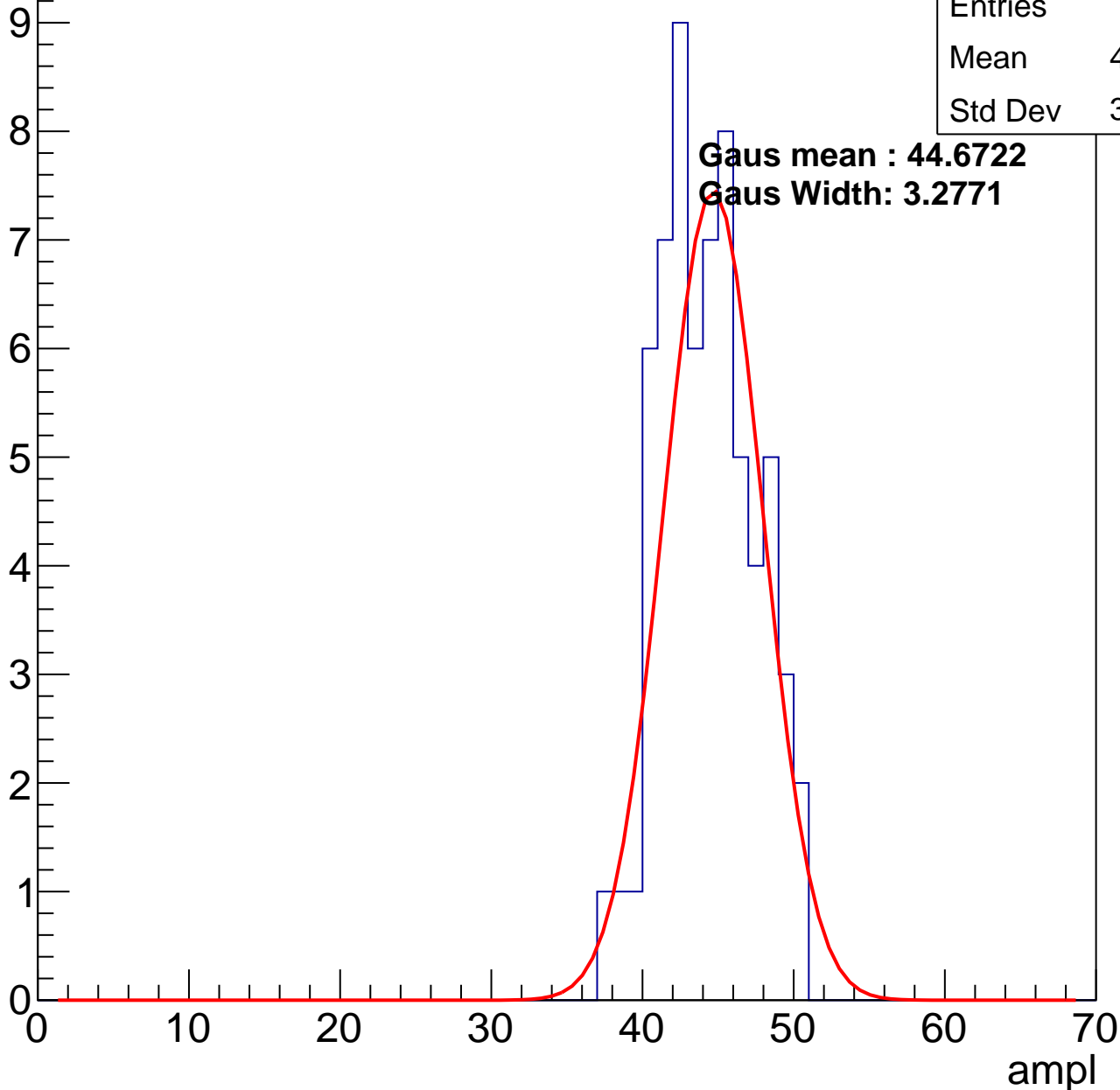
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.85
Std Dev	3.039

Gaus mean : 44.6722

Gaus Width: 3.2771



B1L100S, U11-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	50.79
Std Dev	3.34

Entry

10

8

6

4

2

0

0

10

20

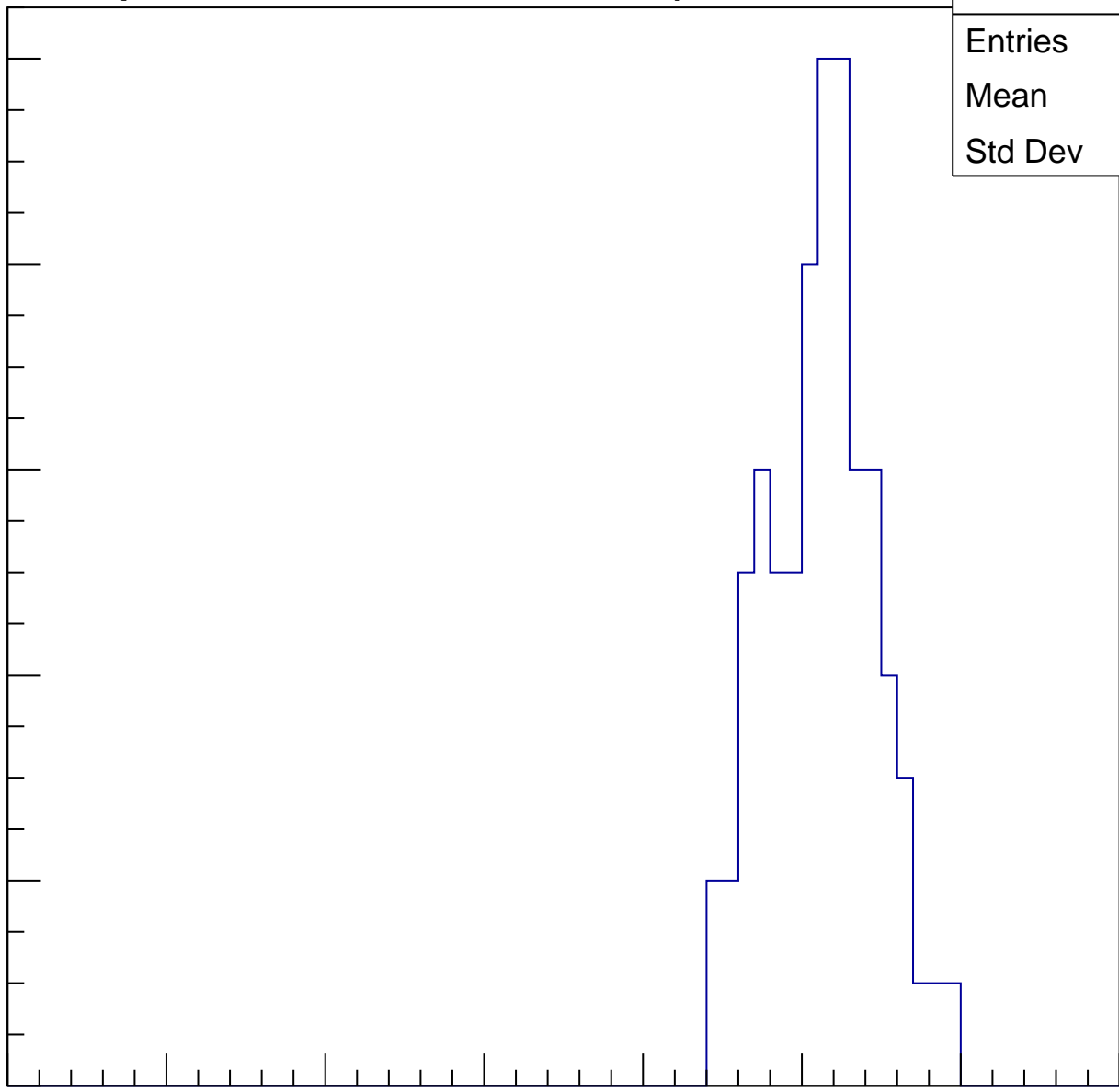
30

40

50

60

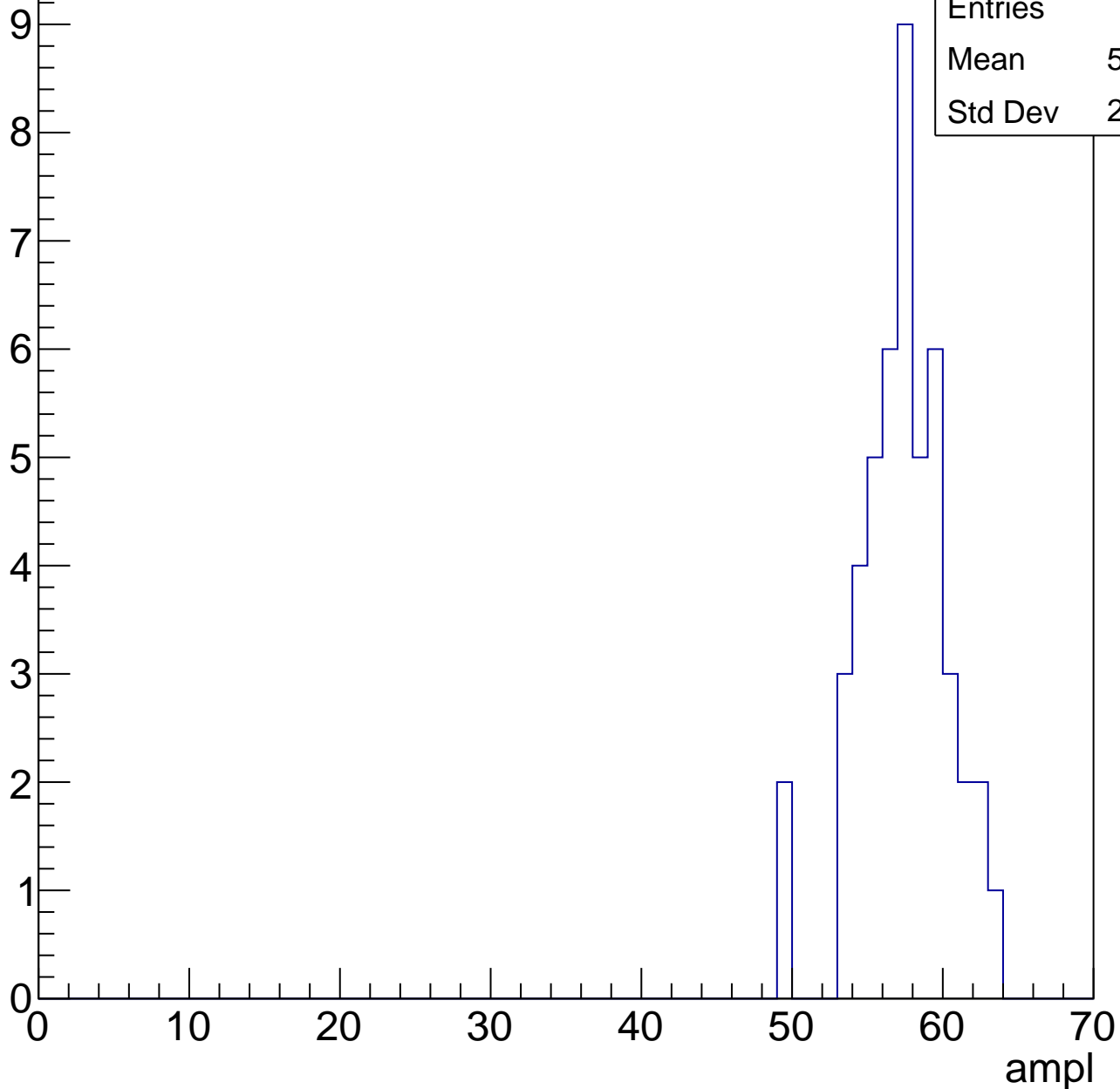
ampl



B1L100S, U11-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



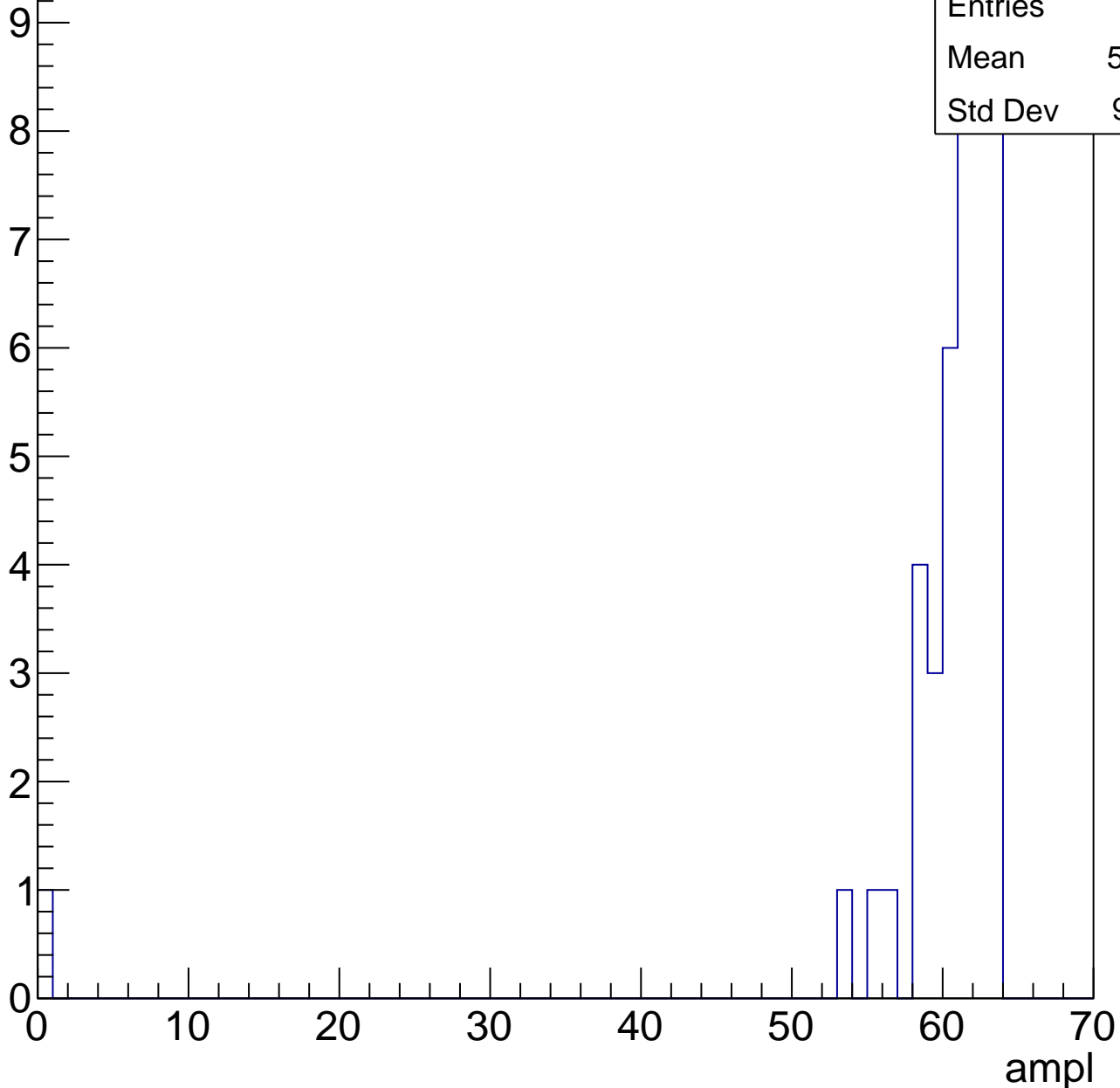
Entries	48
Mean	56.88
Std Dev	2.927

B1L100S, U11-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	59.14
Std Dev	9.511



B1L100S, U11-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

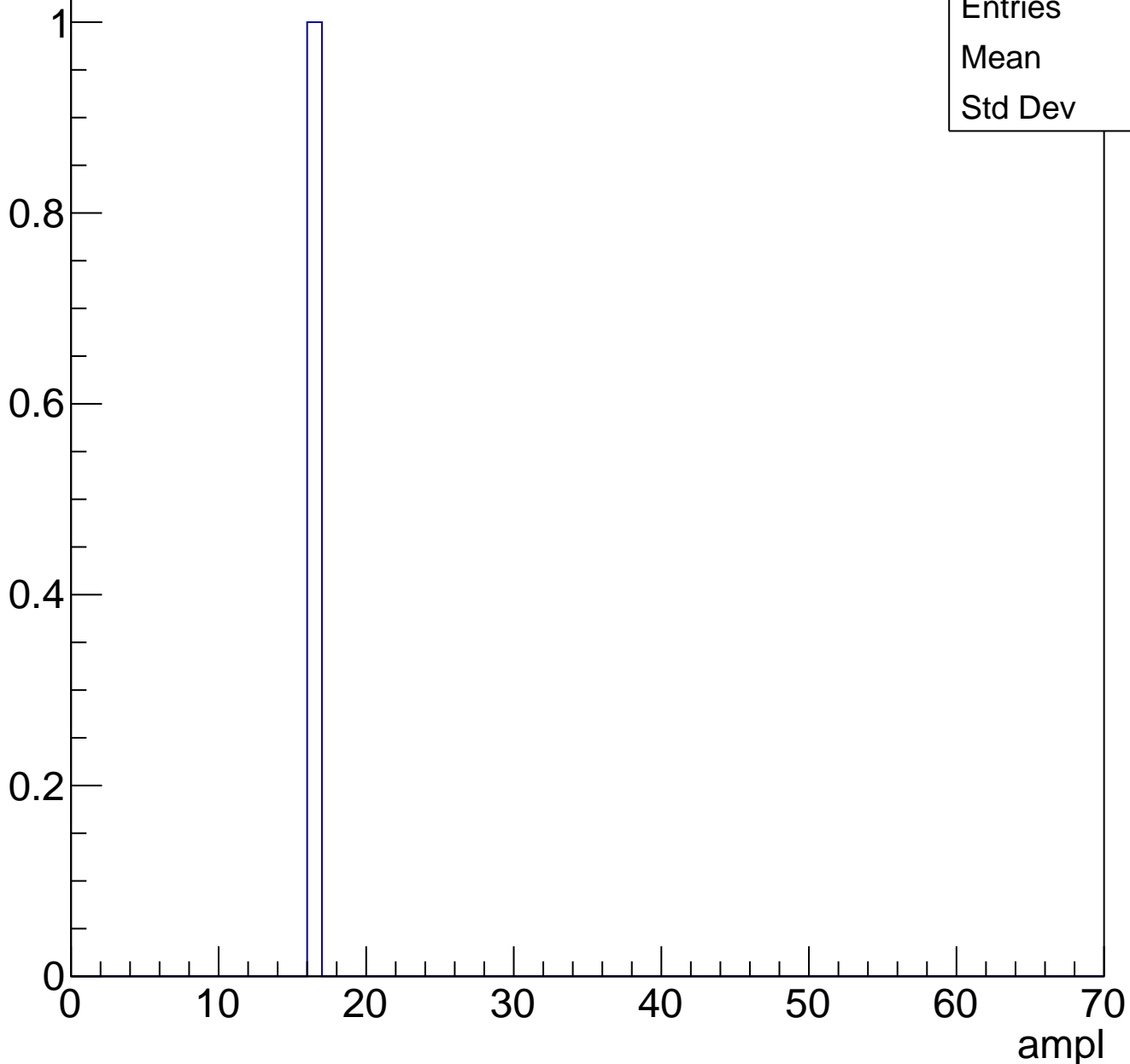
Entry



B1L100S, U11-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch99, adc0

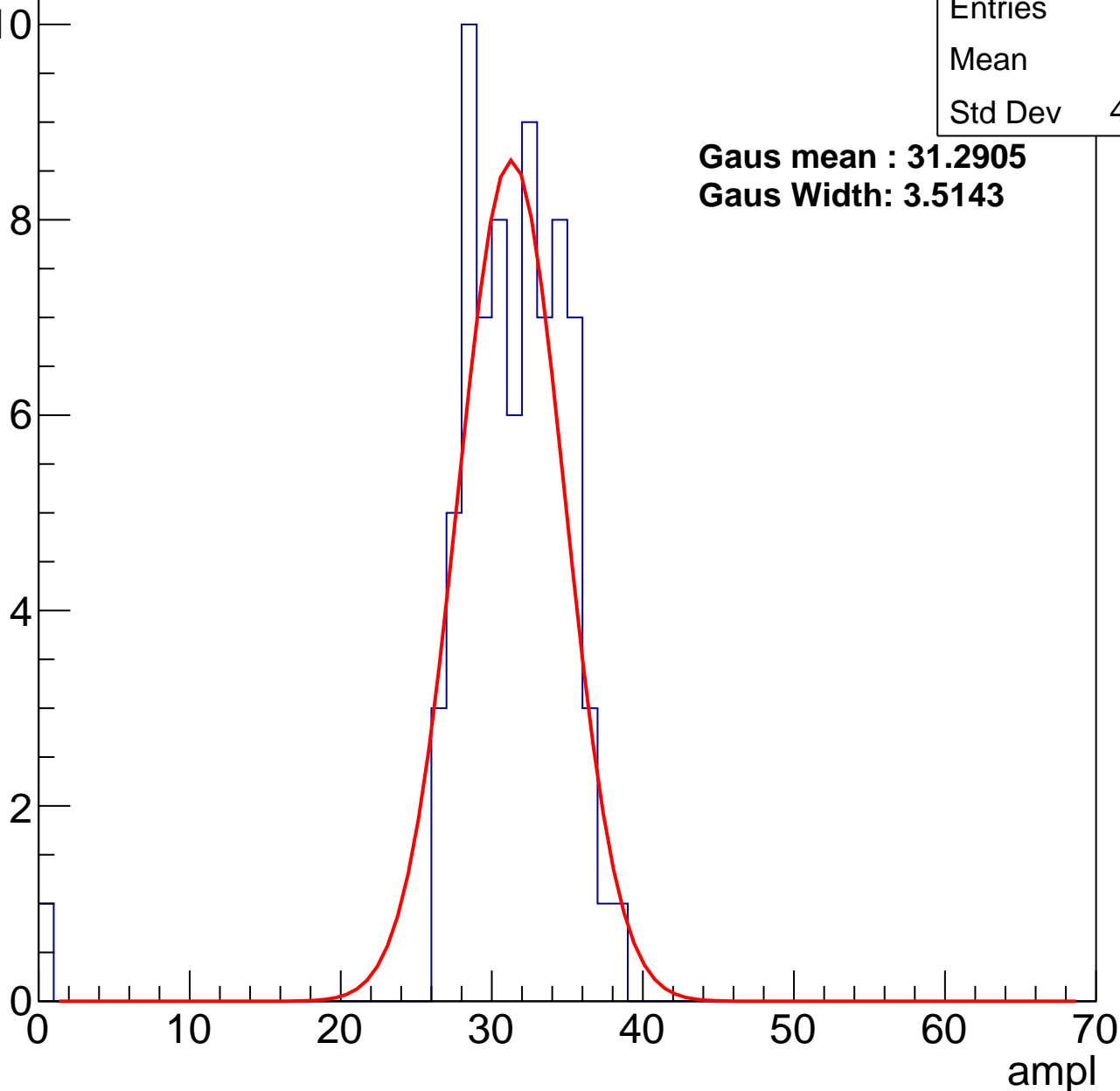
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	30.8
Std Dev	4.617

Gaus mean : 31.2905

Gaus Width: 3.5143



B1L100S, U11-ch99, adc1

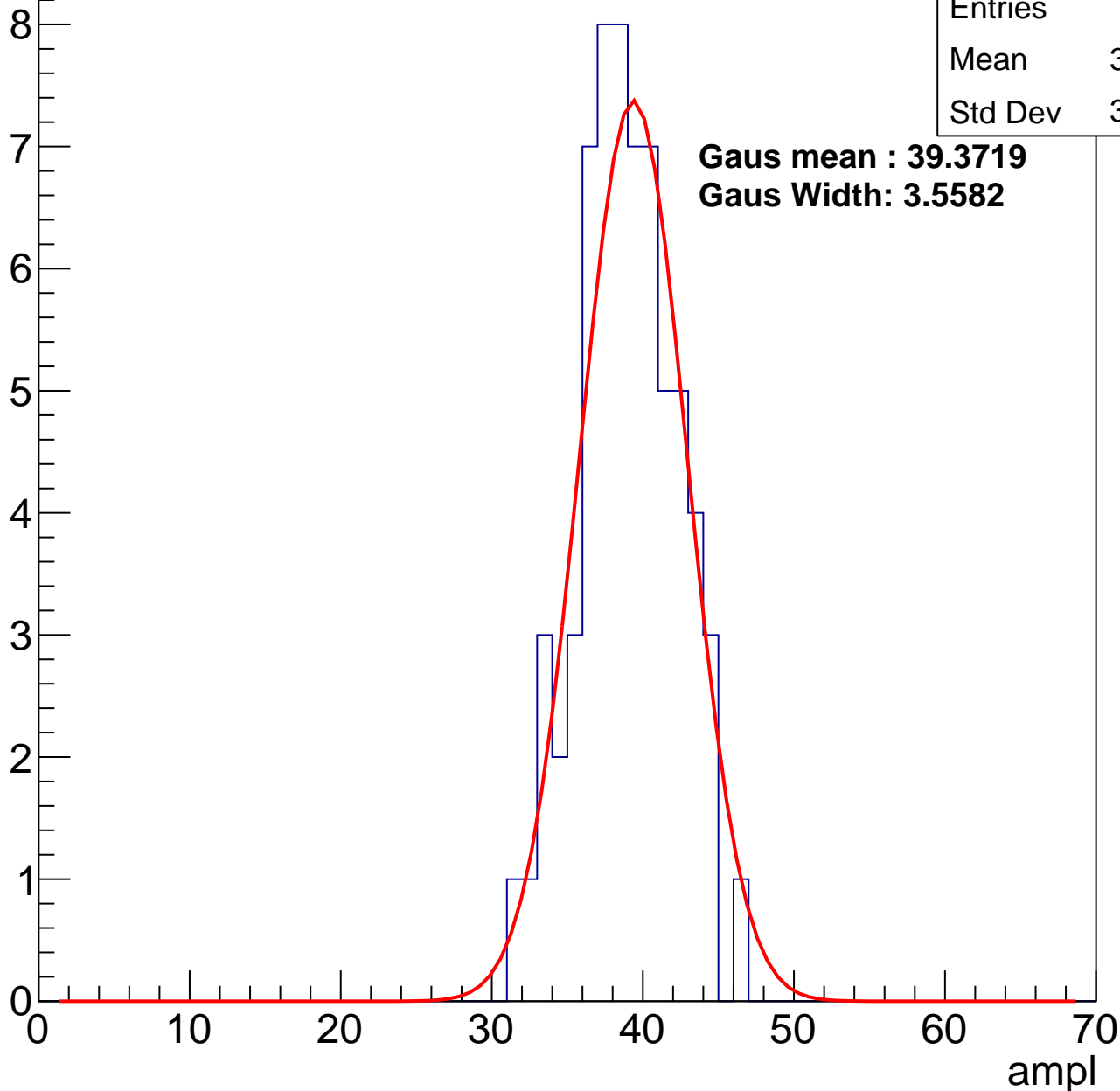
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	38.54
Std Dev	3.206

Gaus mean : 39.3719

Gaus Width: 3.5582



B1L100S, U11-ch99, adc2

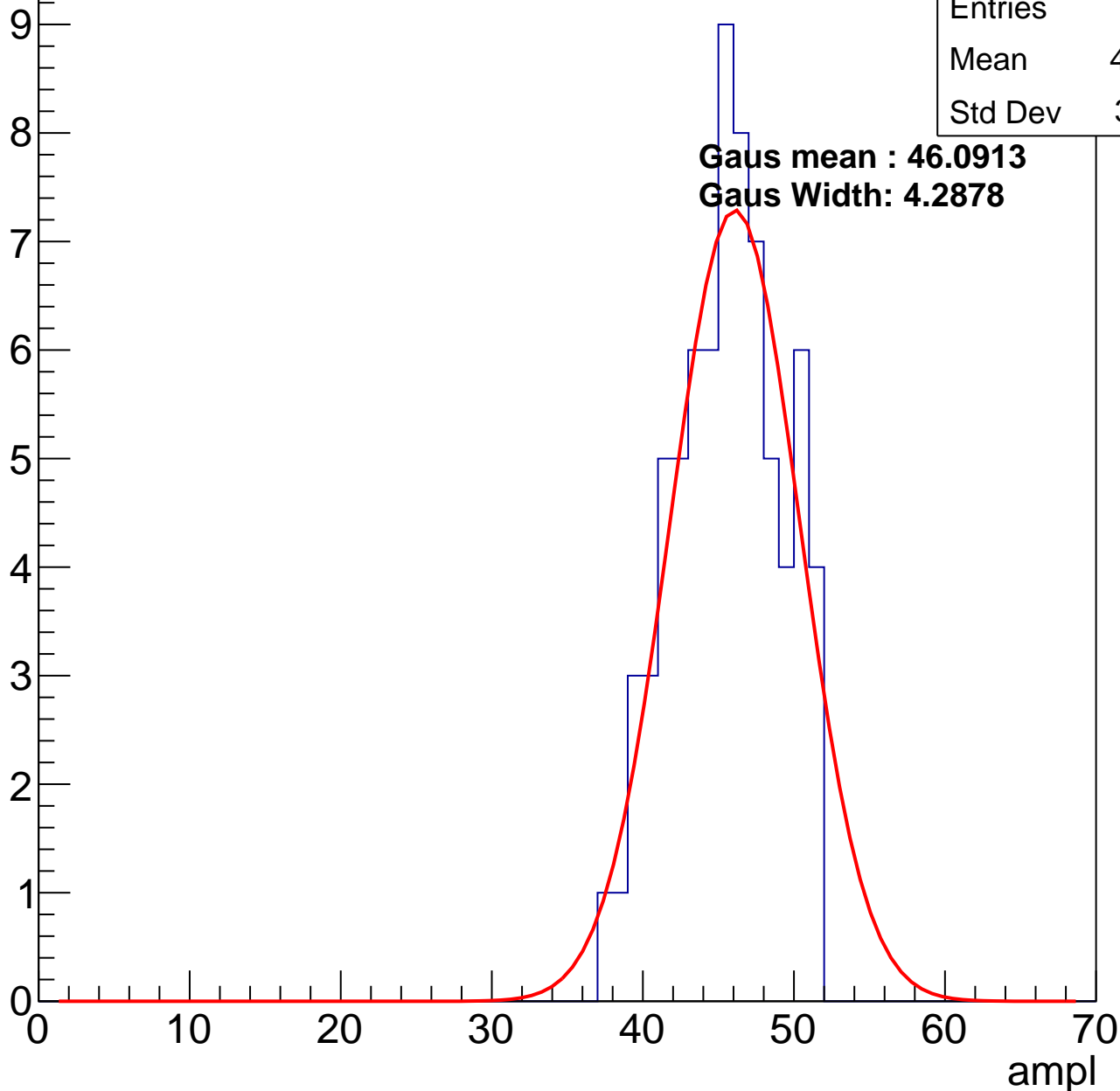
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	45.08
Std Dev	3.491

Gaus mean : 46.0913

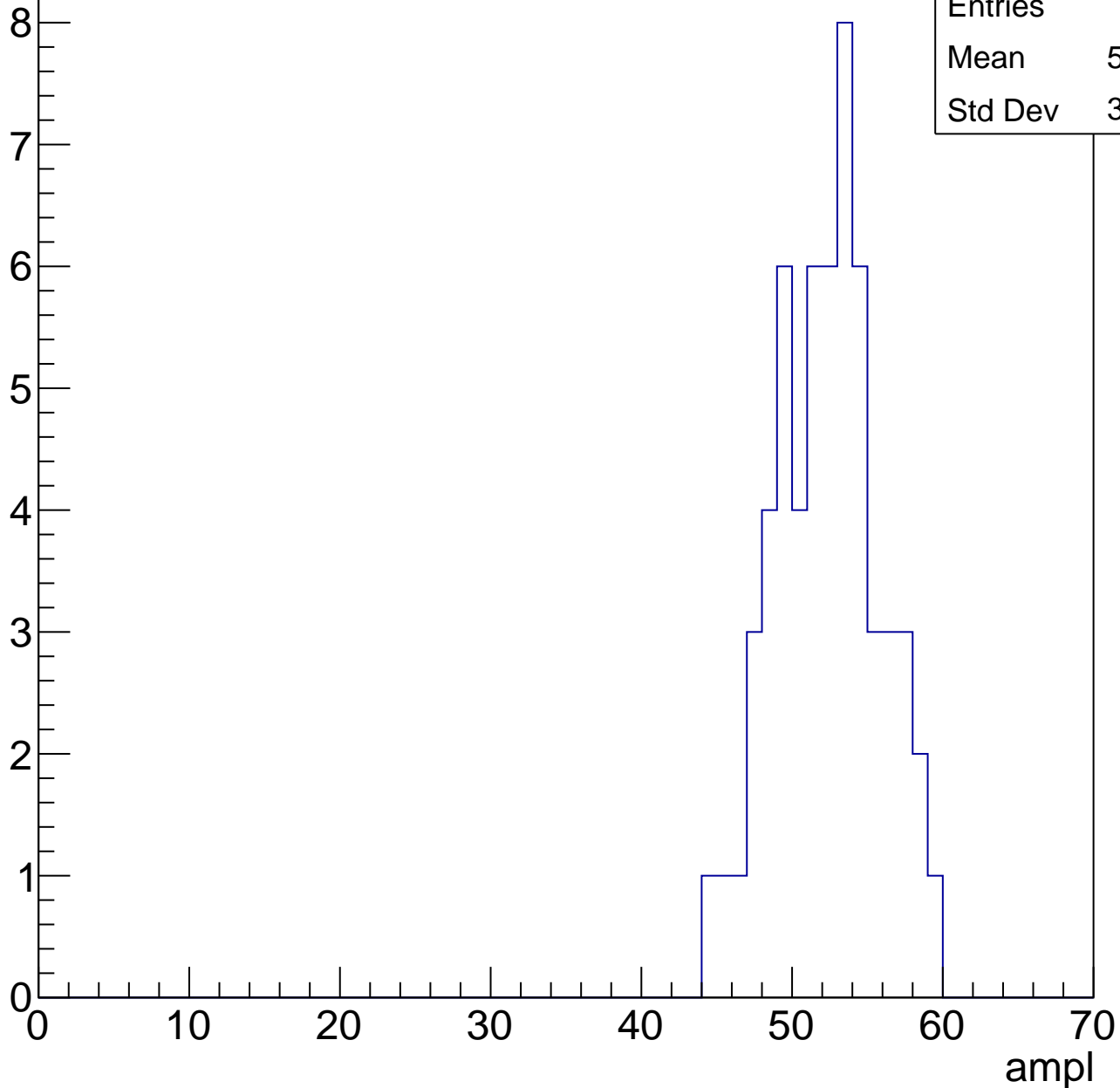
Gaus Width: 4.2878



B1L100S, U11-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



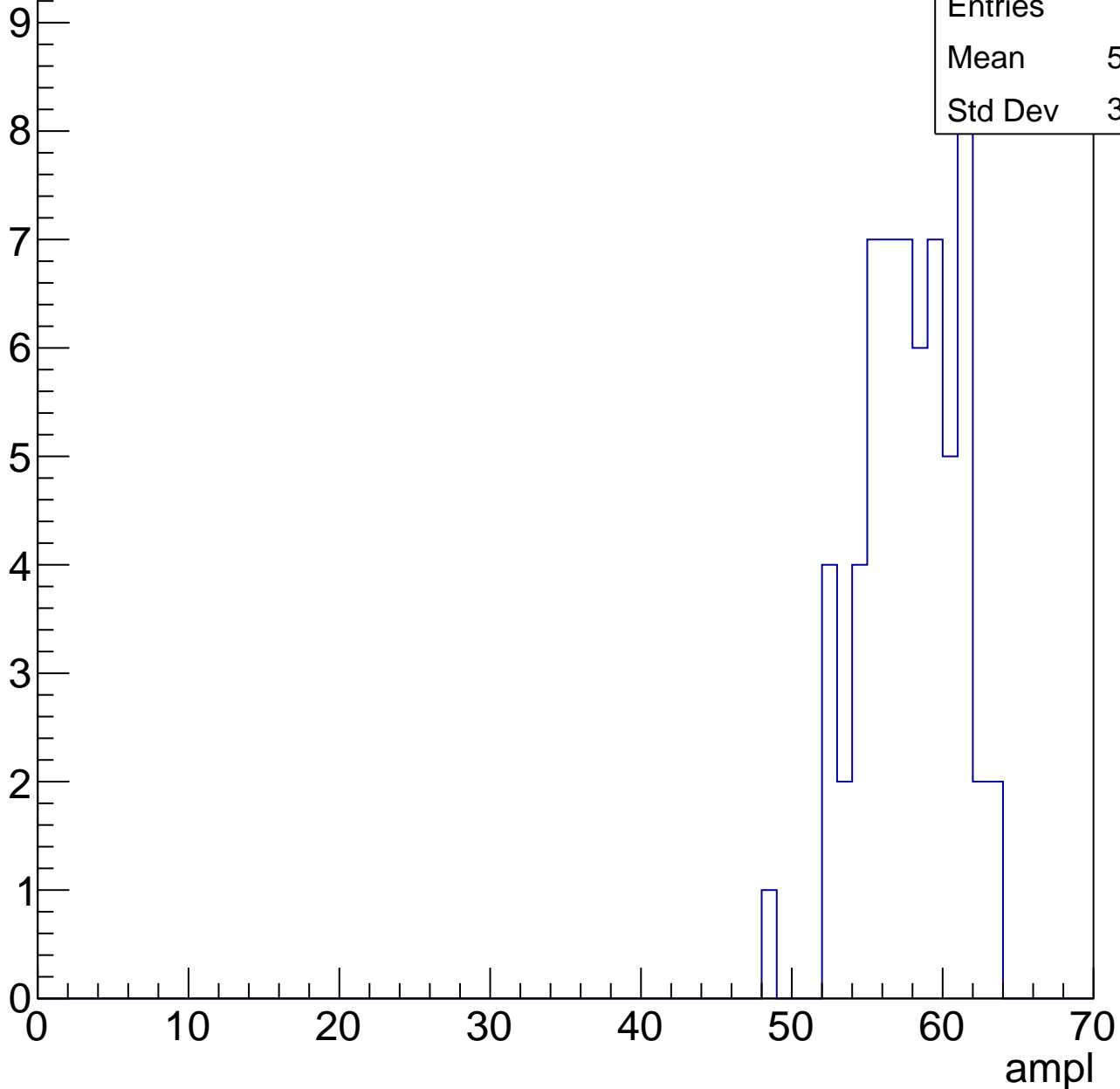
Entries	58
Mean	51.84
Std Dev	3.388

B1L100S, U11-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

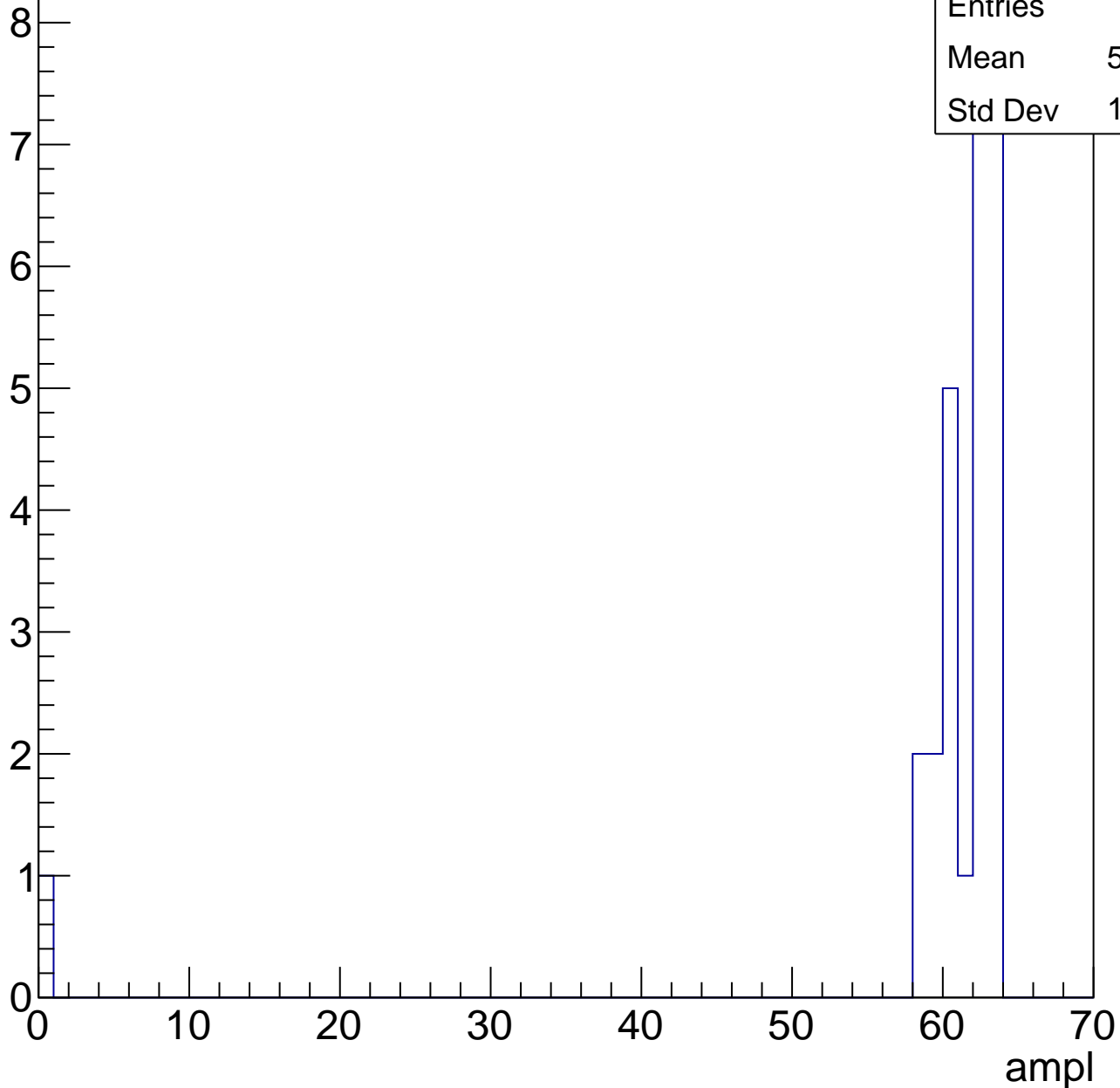
Entries	63
Mean	57.37
Std Dev	3.139



B1L100S, U11-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U11-ch100, adc0

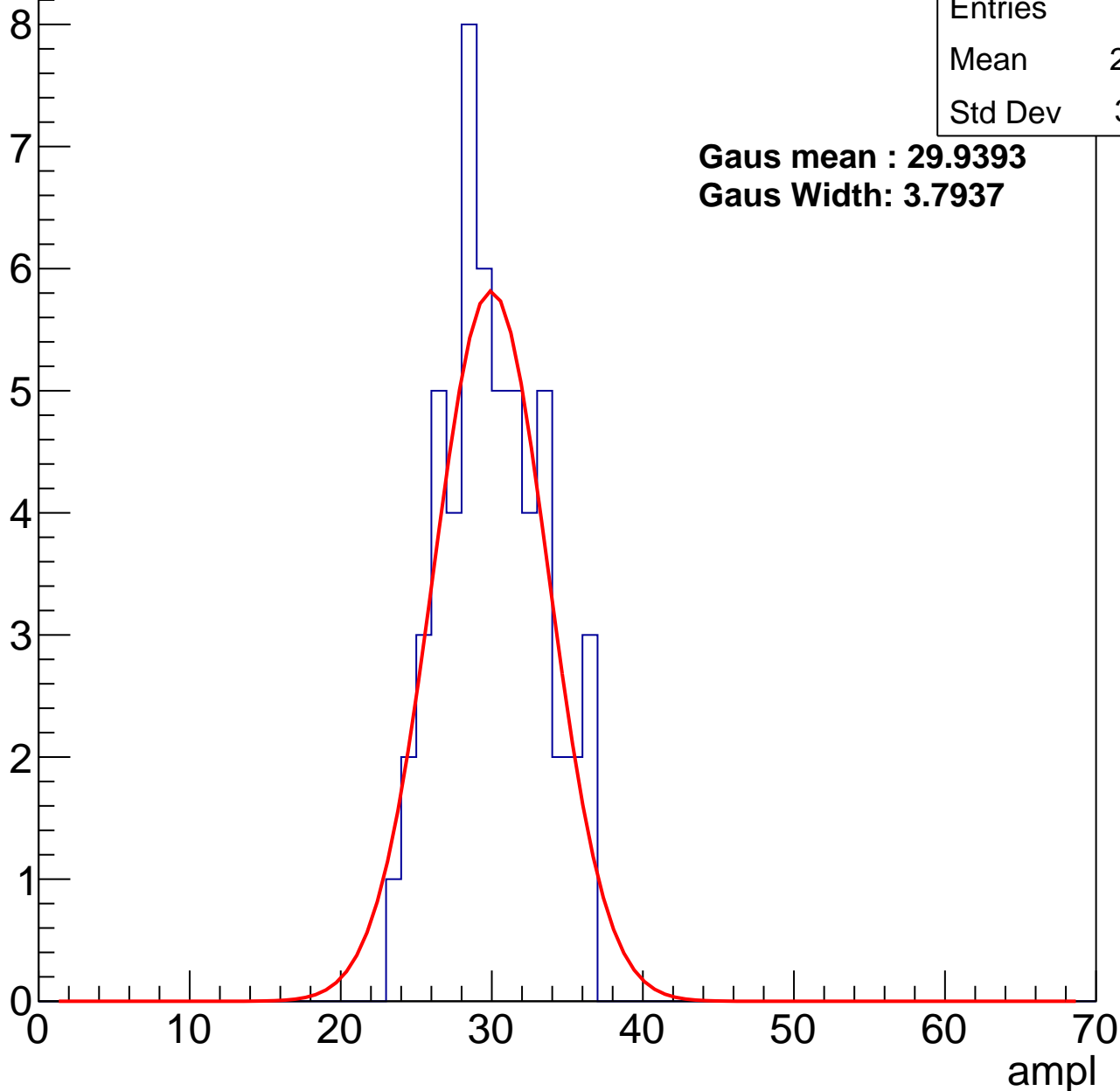
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	29.56
Std Dev	3.291

Gaus mean : 29.9393

Gaus Width: 3.7937



B1L100S, U11-ch100, adc1

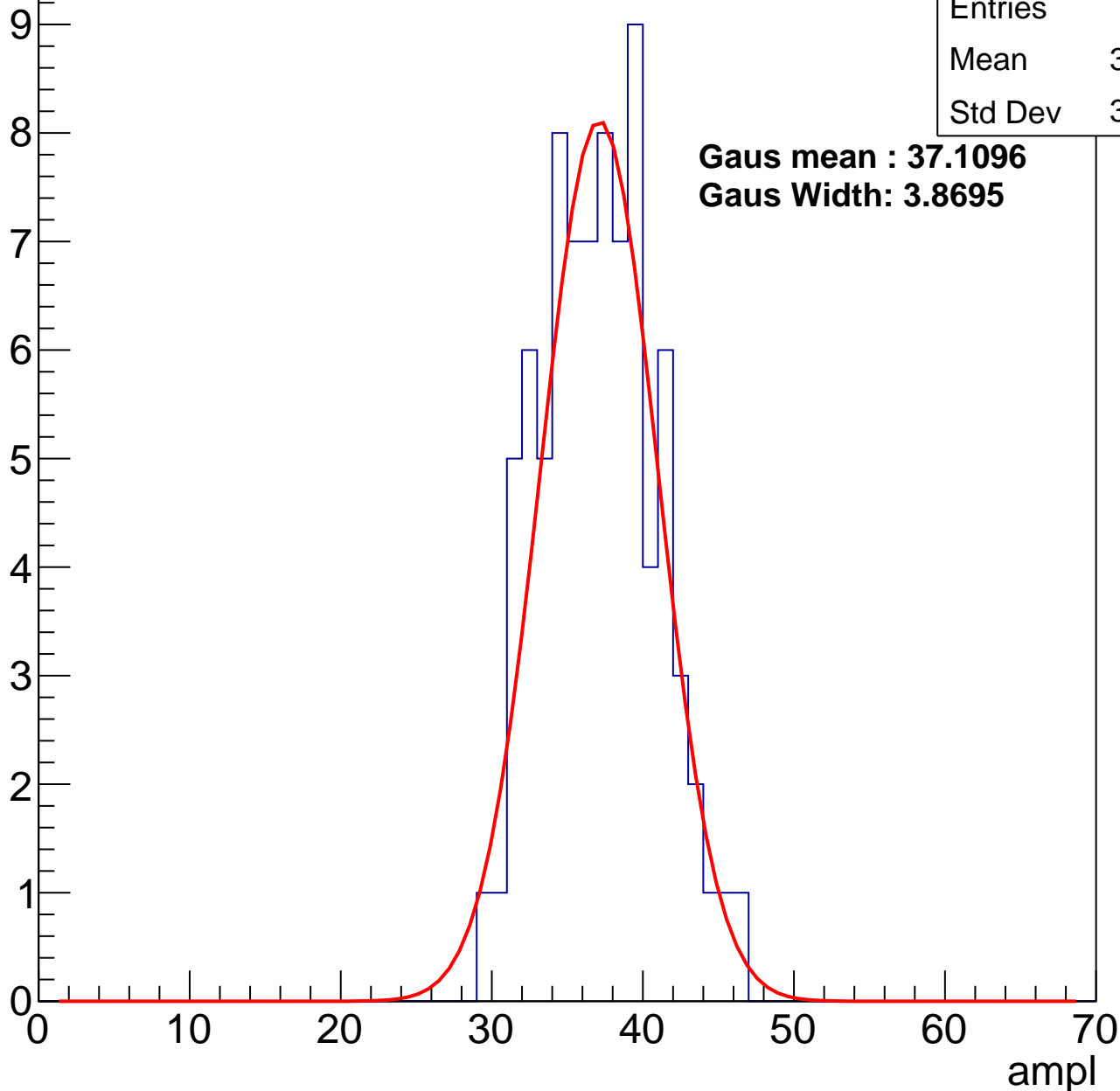
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	36.66
Std Dev	3.723

Gaus mean : 37.1096

Gaus Width: 3.8695



B1L100S, U11-ch100, adc2

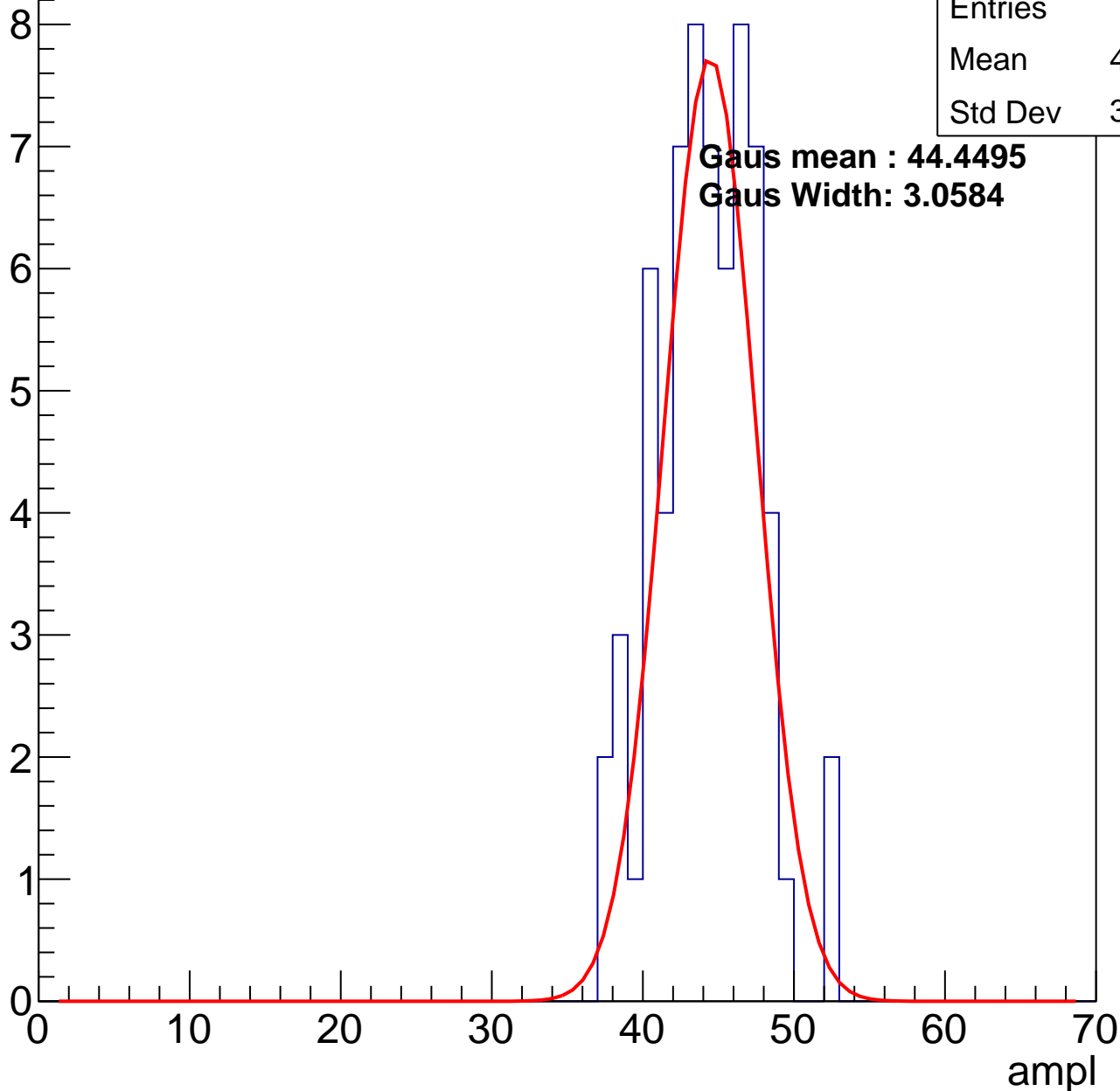
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.77
Std Dev	3.288

Gaus mean : 44.4495

Gaus Width: 3.0584

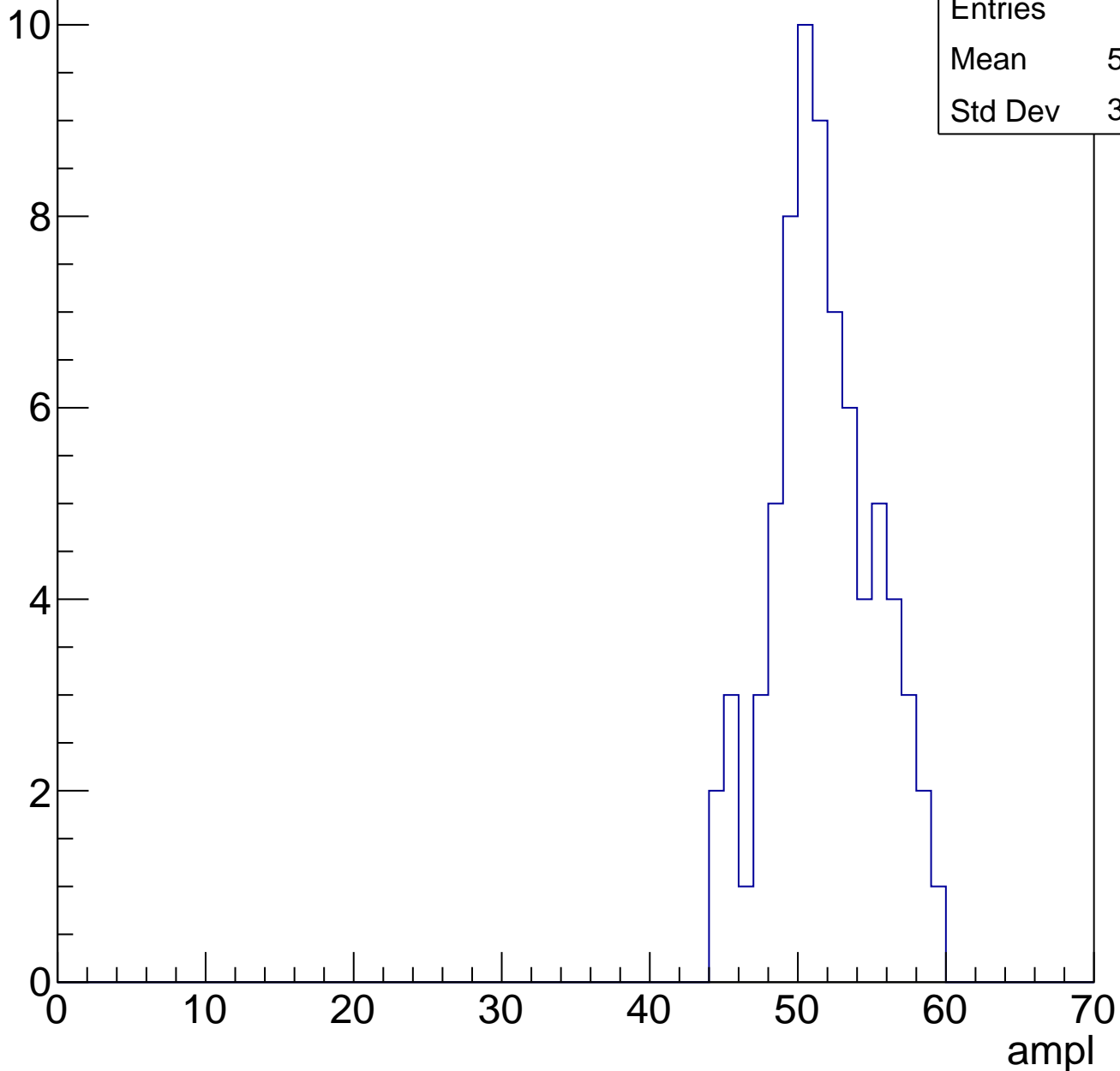


B1L100S, U11-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	51.29
Std Dev	3.466

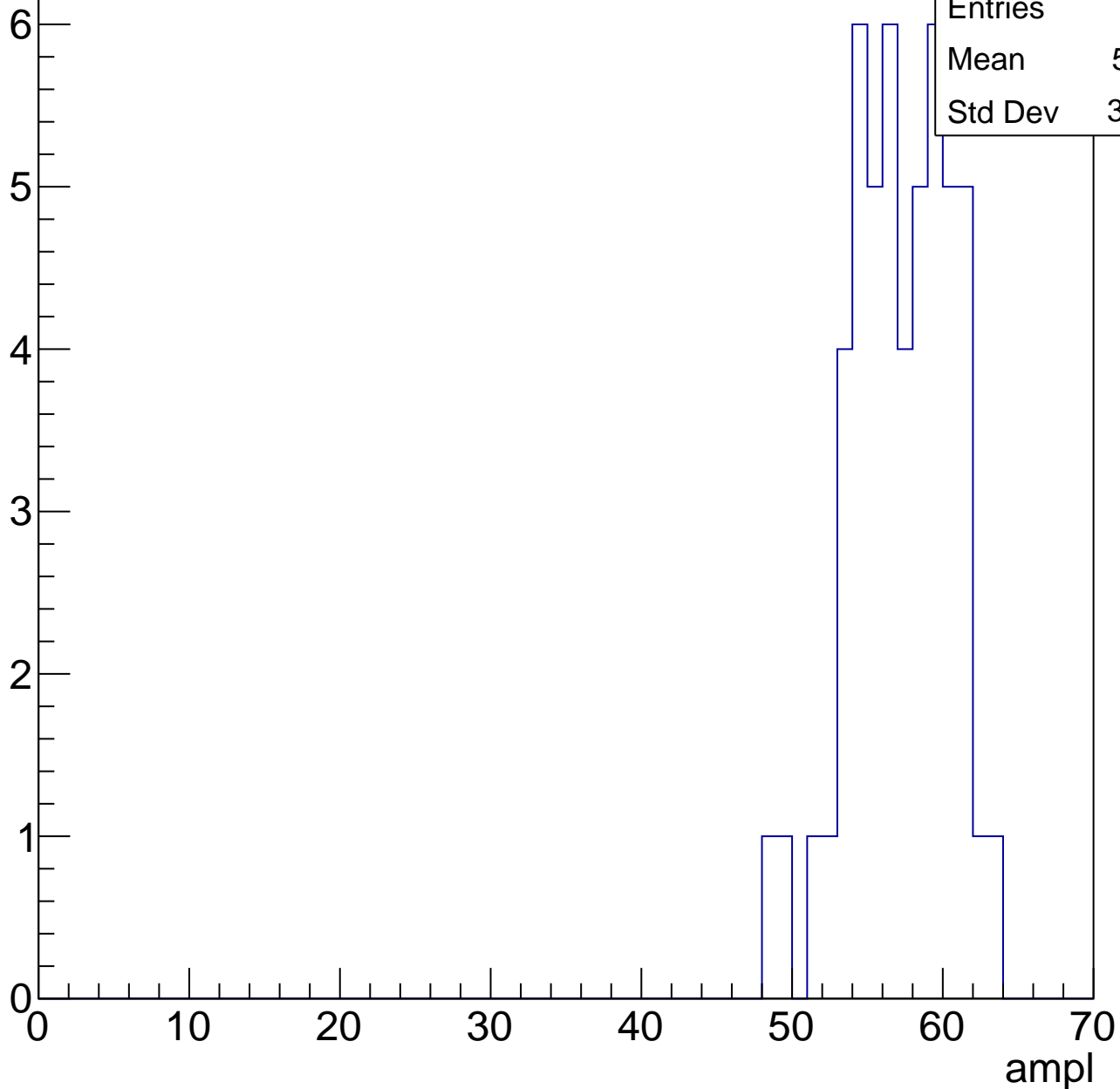


B1L100S, U11-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	56.71
Std Dev	3.284

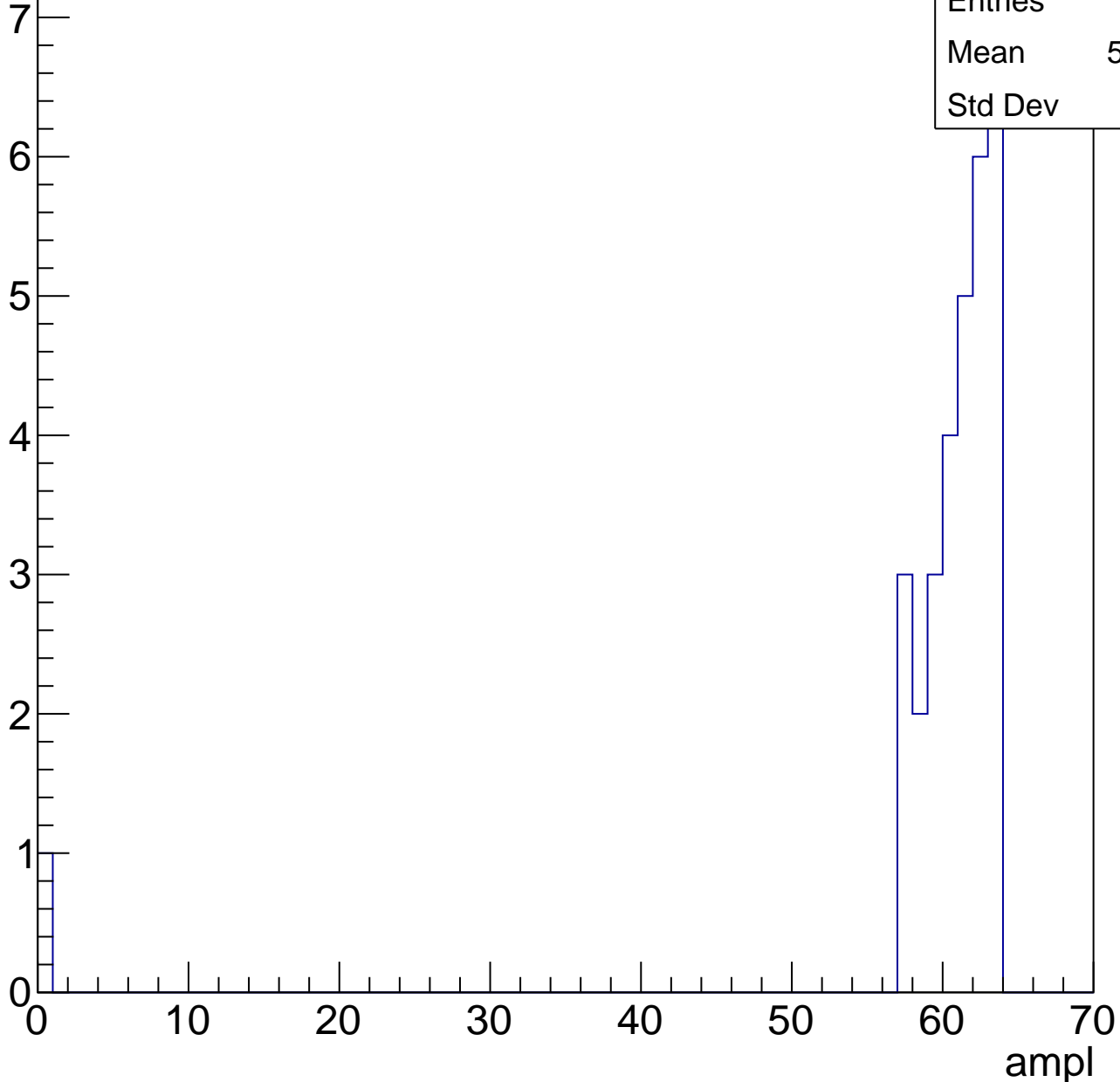


B1L100S, U11-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

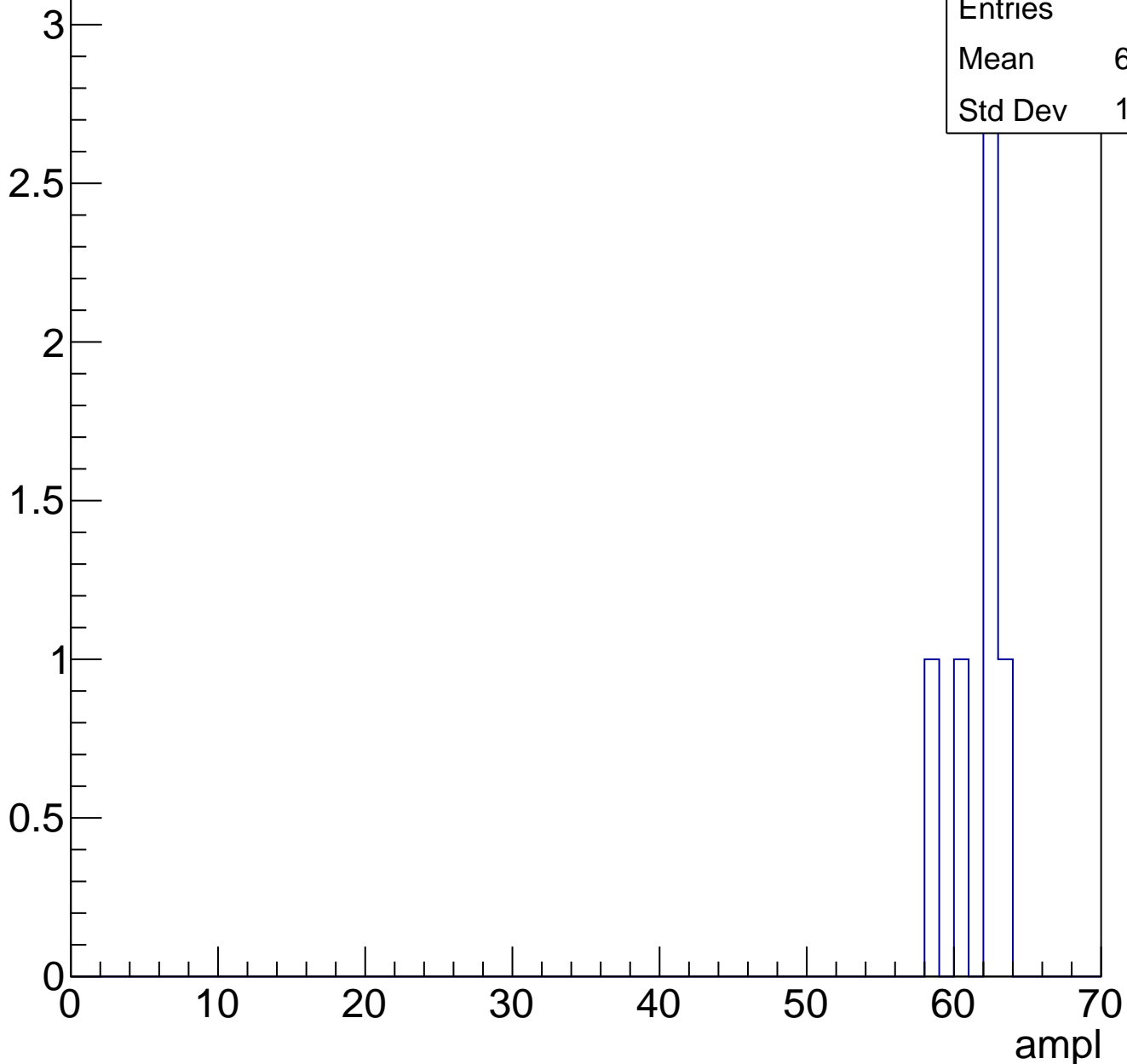
Entries	31
Mean	58.77
Std Dev	10.9



B1L100S, U11-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	6
Mean	61.17
Std Dev	1.675

B1L100S, U11-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch101, adc0

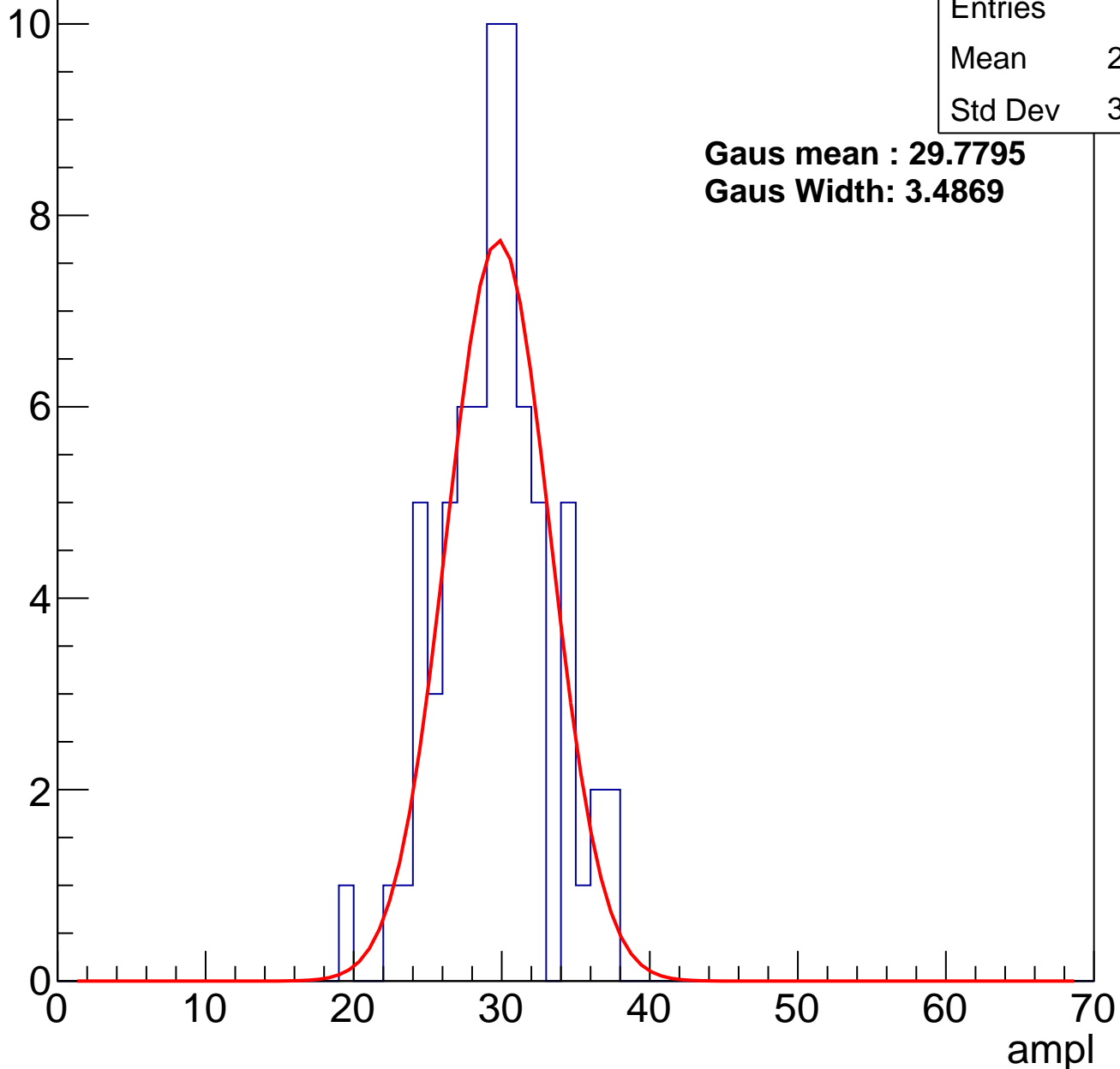
calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	29.07
Std Dev	3.609

Gaus mean : 29.7795

Gaus Width: 3.4869

Entry



B1L100S, U11-ch101, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	36.77
Std Dev	3.448

Gaus mean : 36.7462

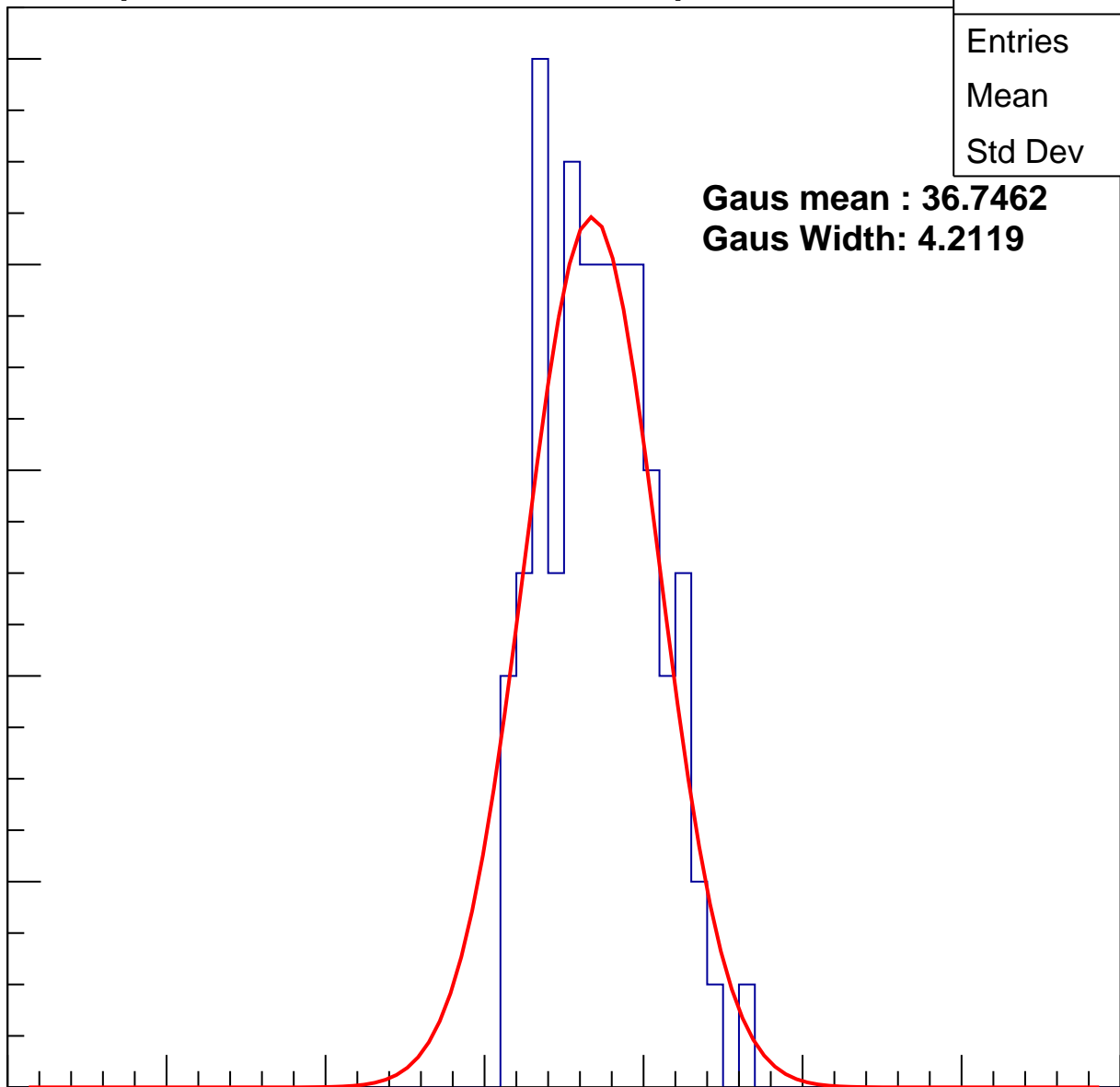
Gaus Width: 4.2119

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U11-ch101, adc2

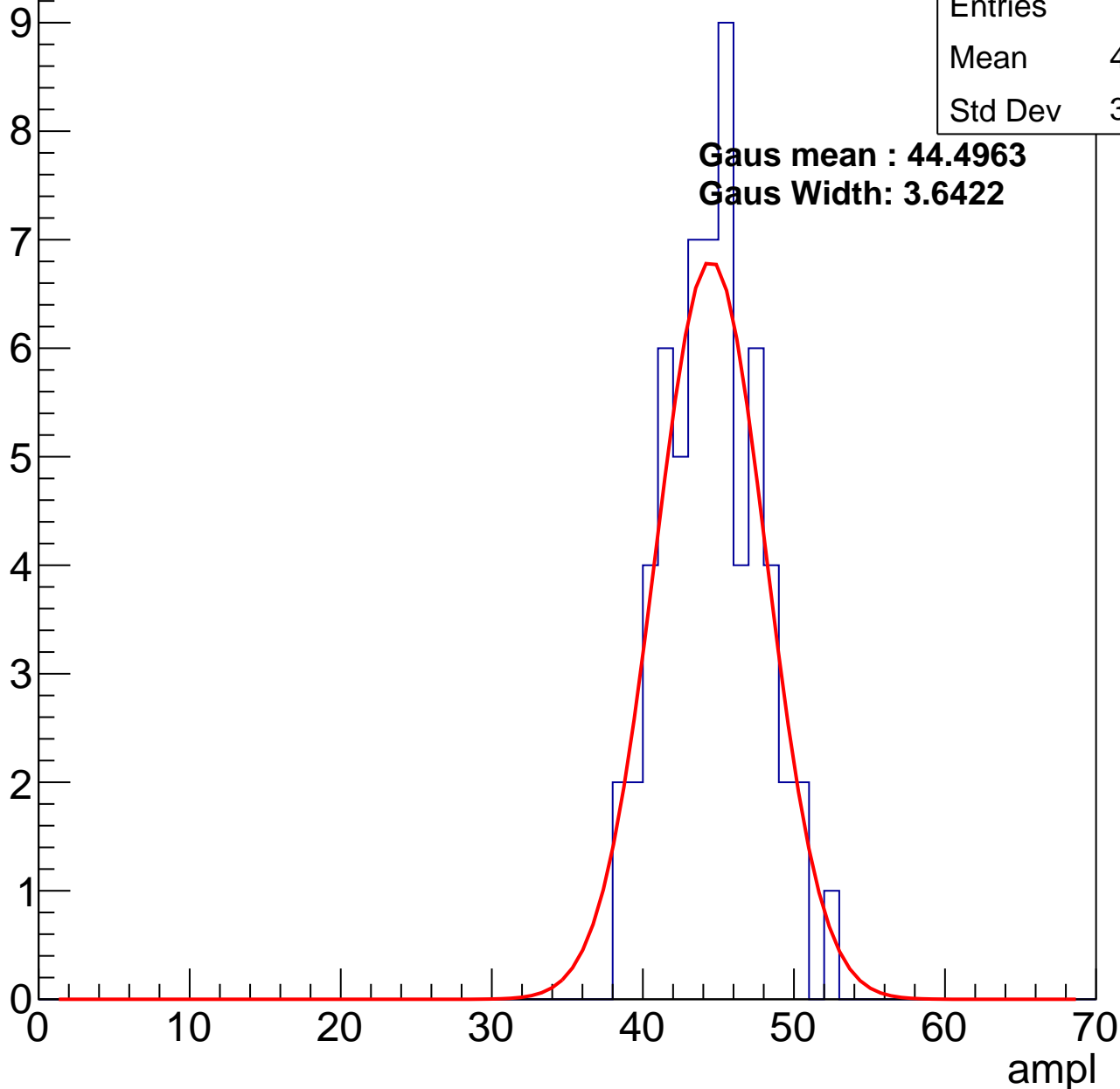
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.13
Std Dev	3.123

Gaus mean : 44.4963

Gaus Width: 3.6422

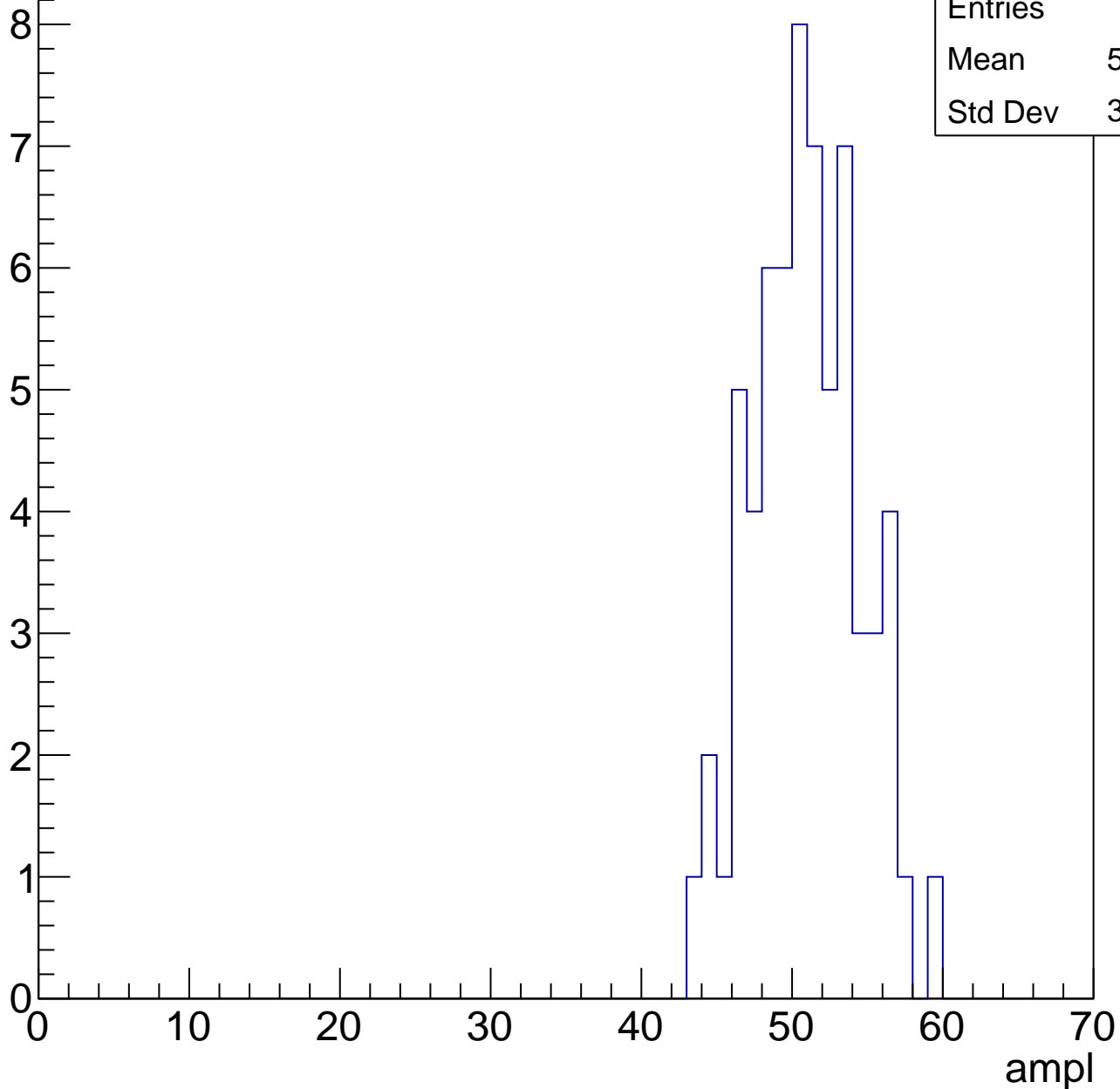


B1L100S, U11-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	50.48
Std Dev	3.455

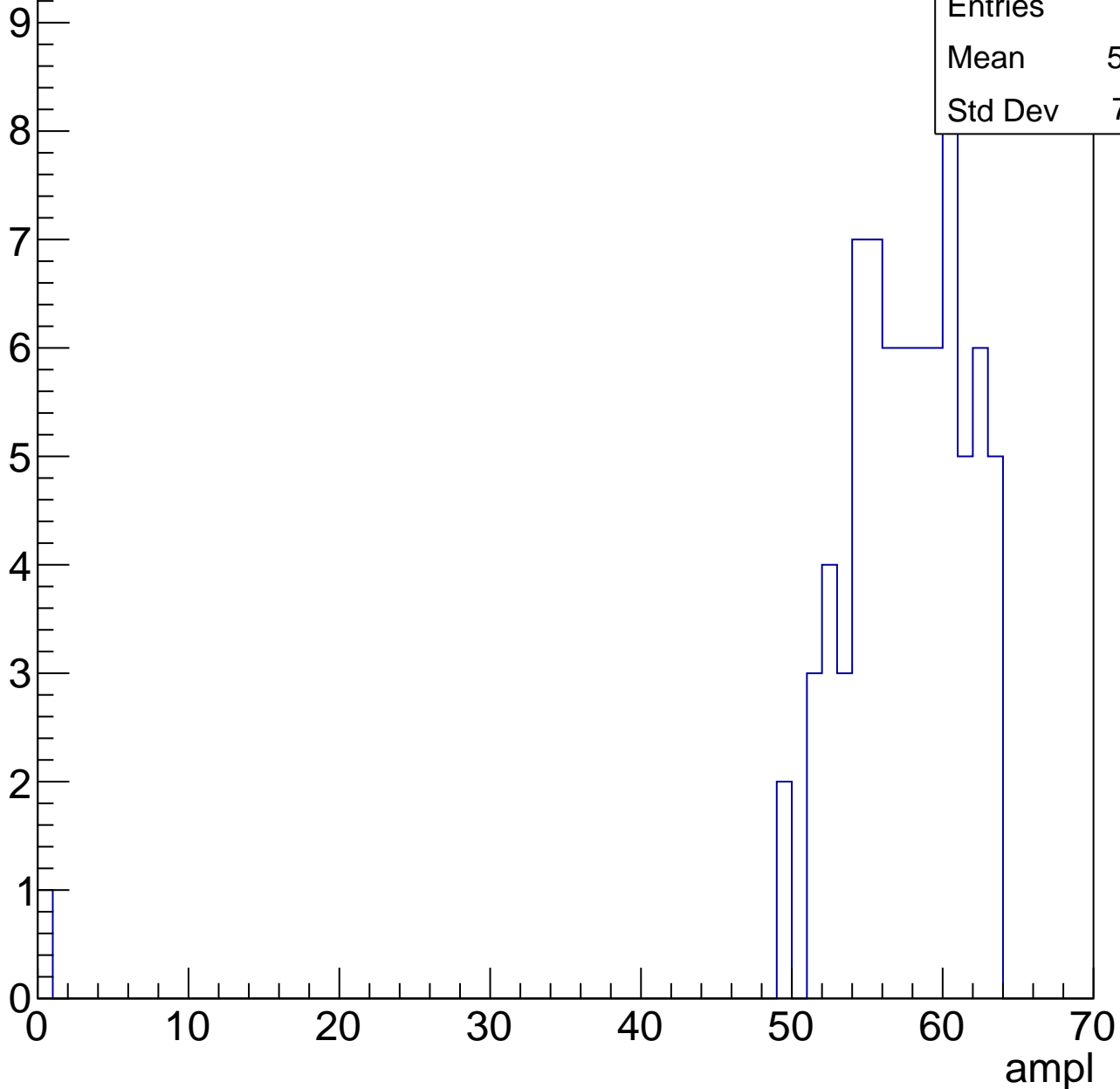


B1L100S, U11-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	56.49
Std Dev	7.461

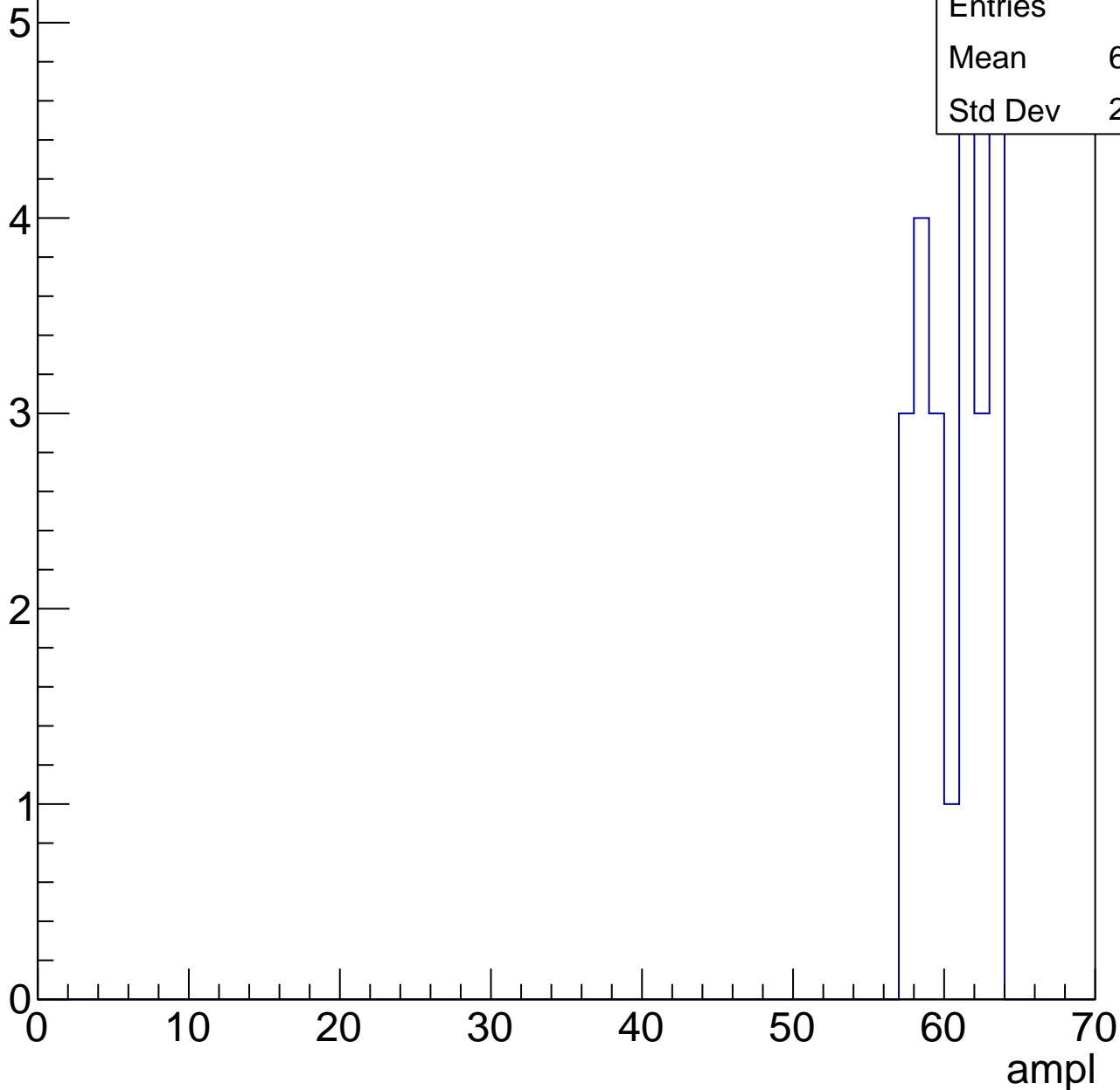


B1L100S, U11-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	60.25
Std Dev	2.107



B1L100S, U11-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch102, adc0

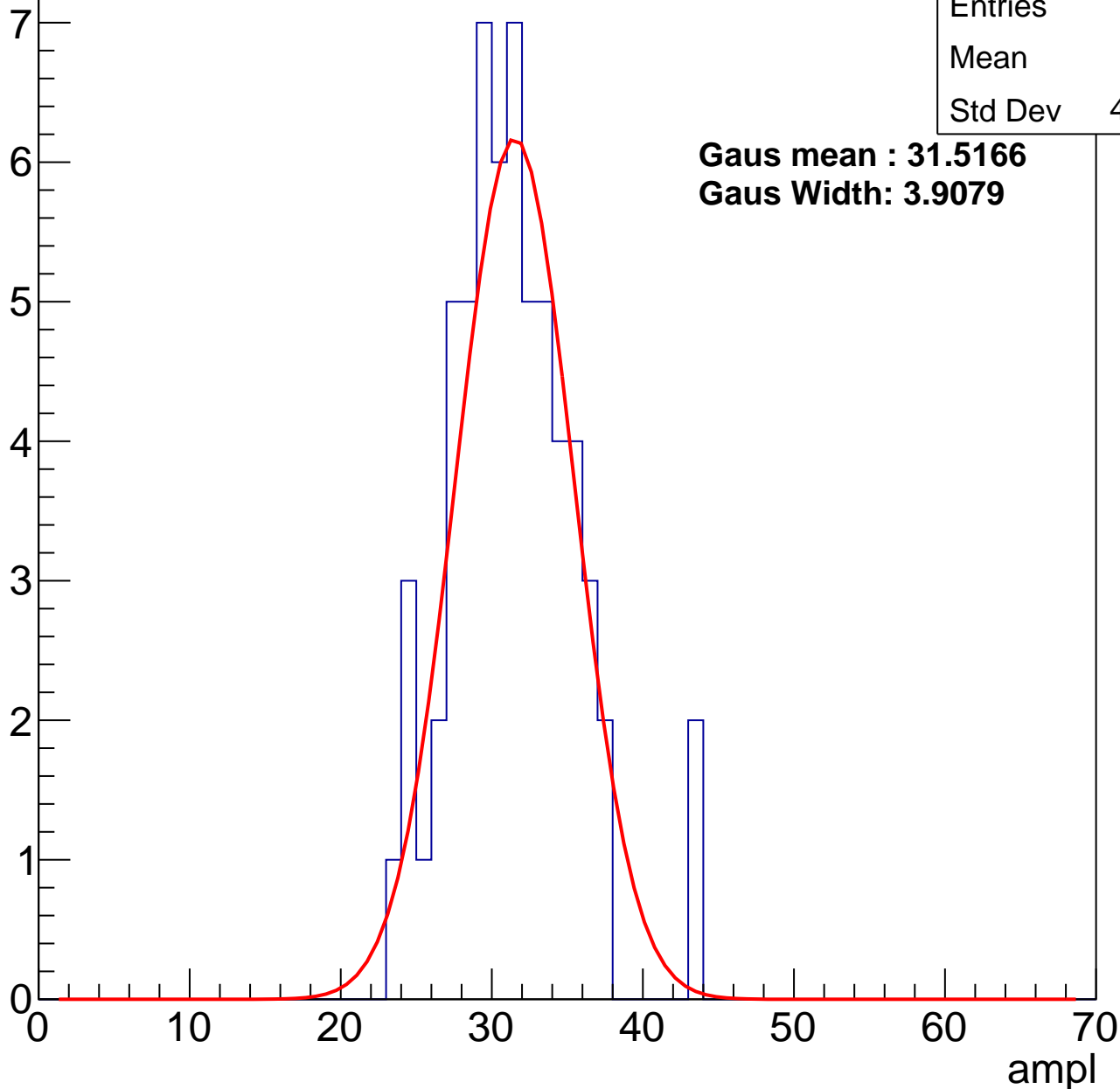
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.9
Std Dev	4.047

Gaus mean : 31.5166

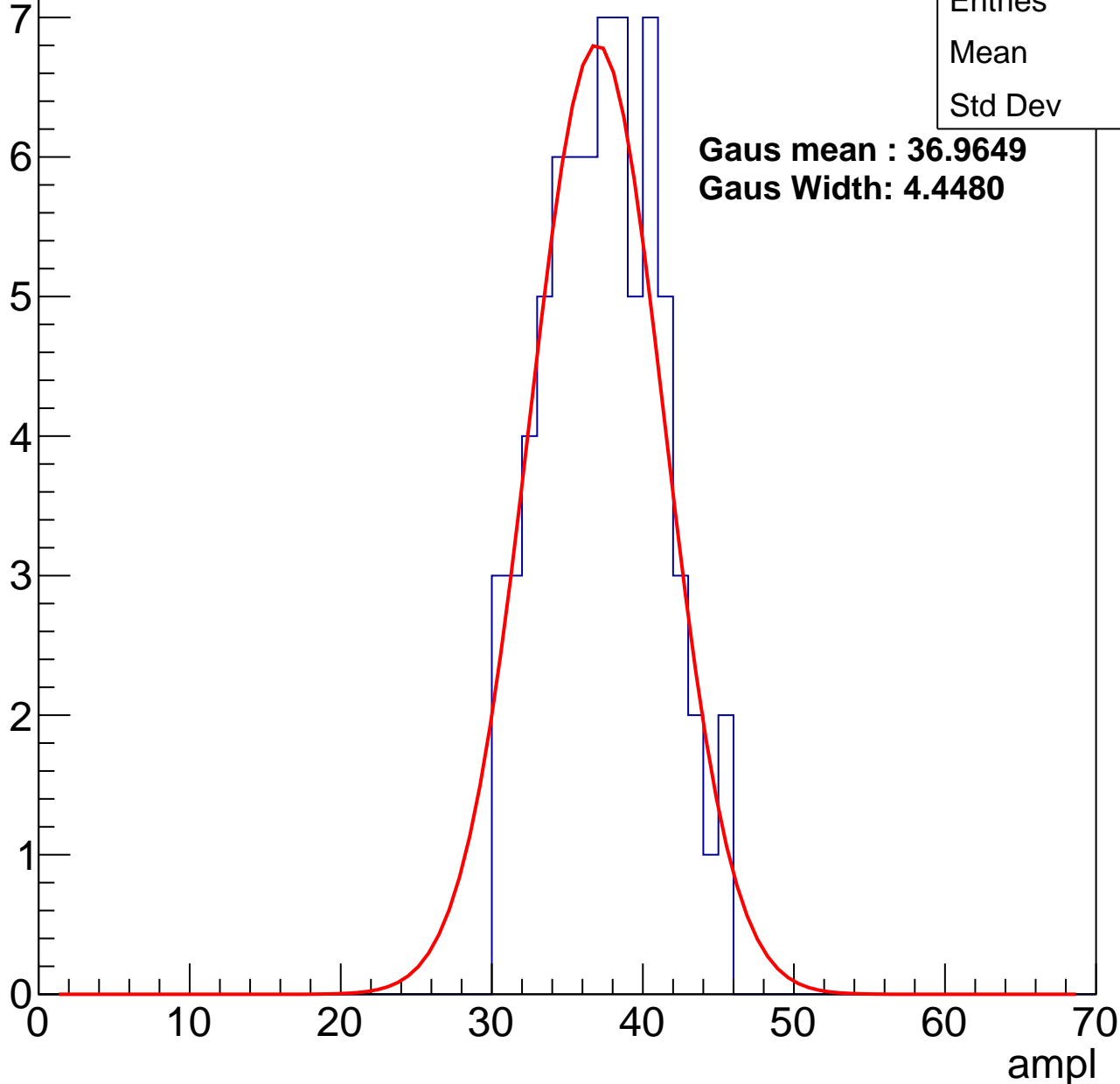
Gaus Width: 3.9079



B1L100S, U11-ch102, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch102, adc2

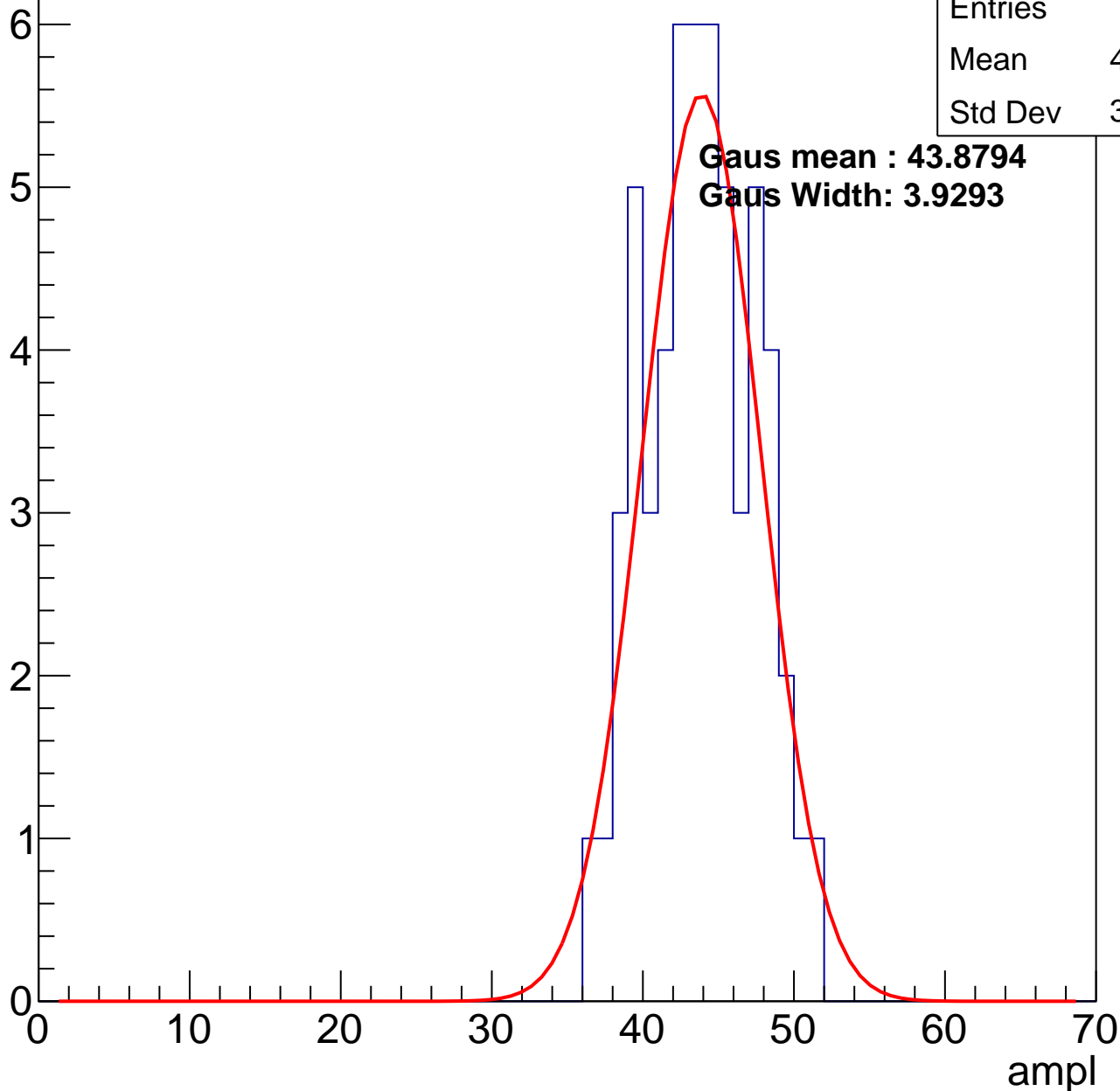
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	43.38
Std Dev	3.533

Gaus mean : 43.8794

Gaus Width: 3.9293

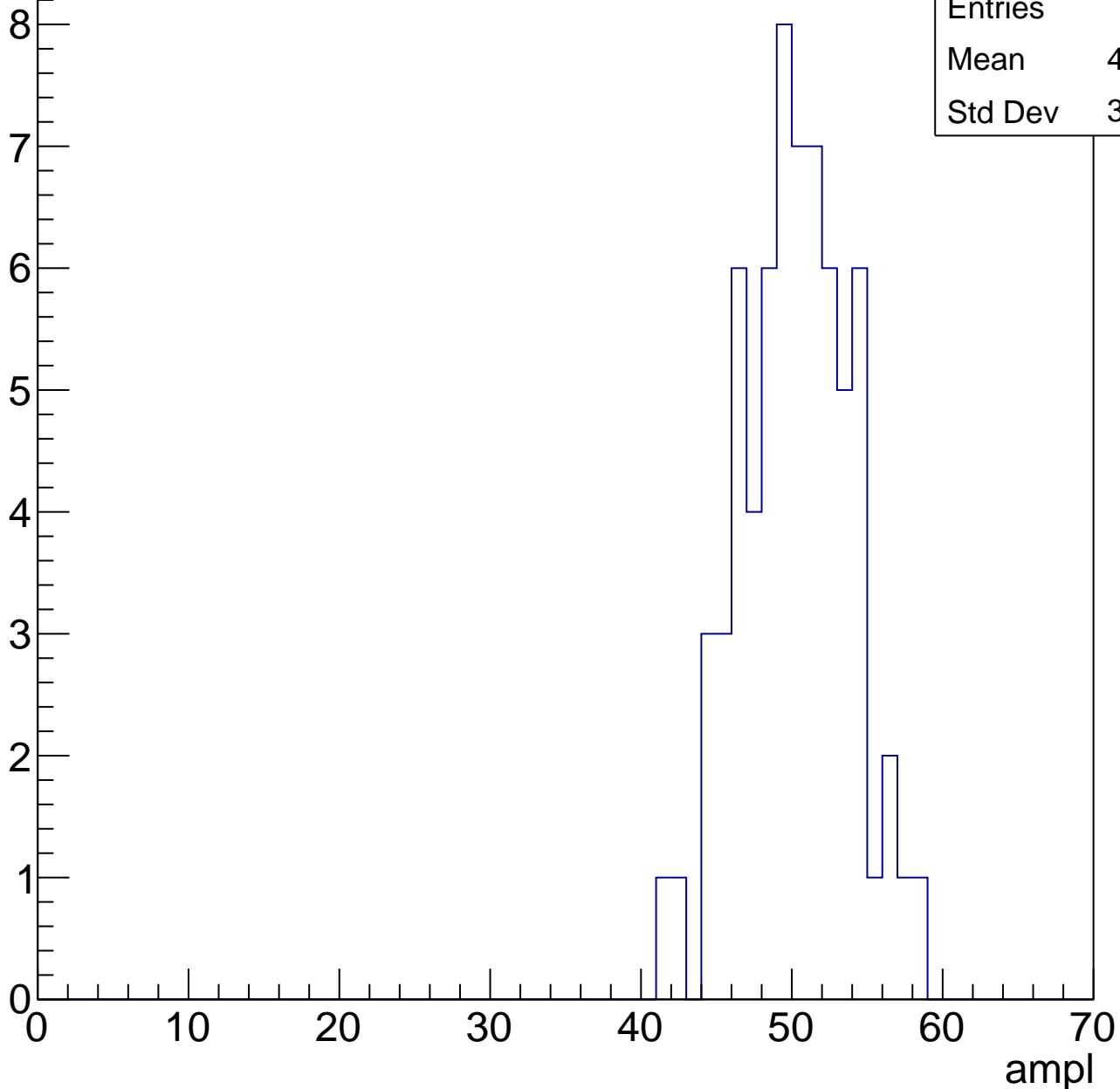


B1L100S, U11-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	49.76
Std Dev	3.577

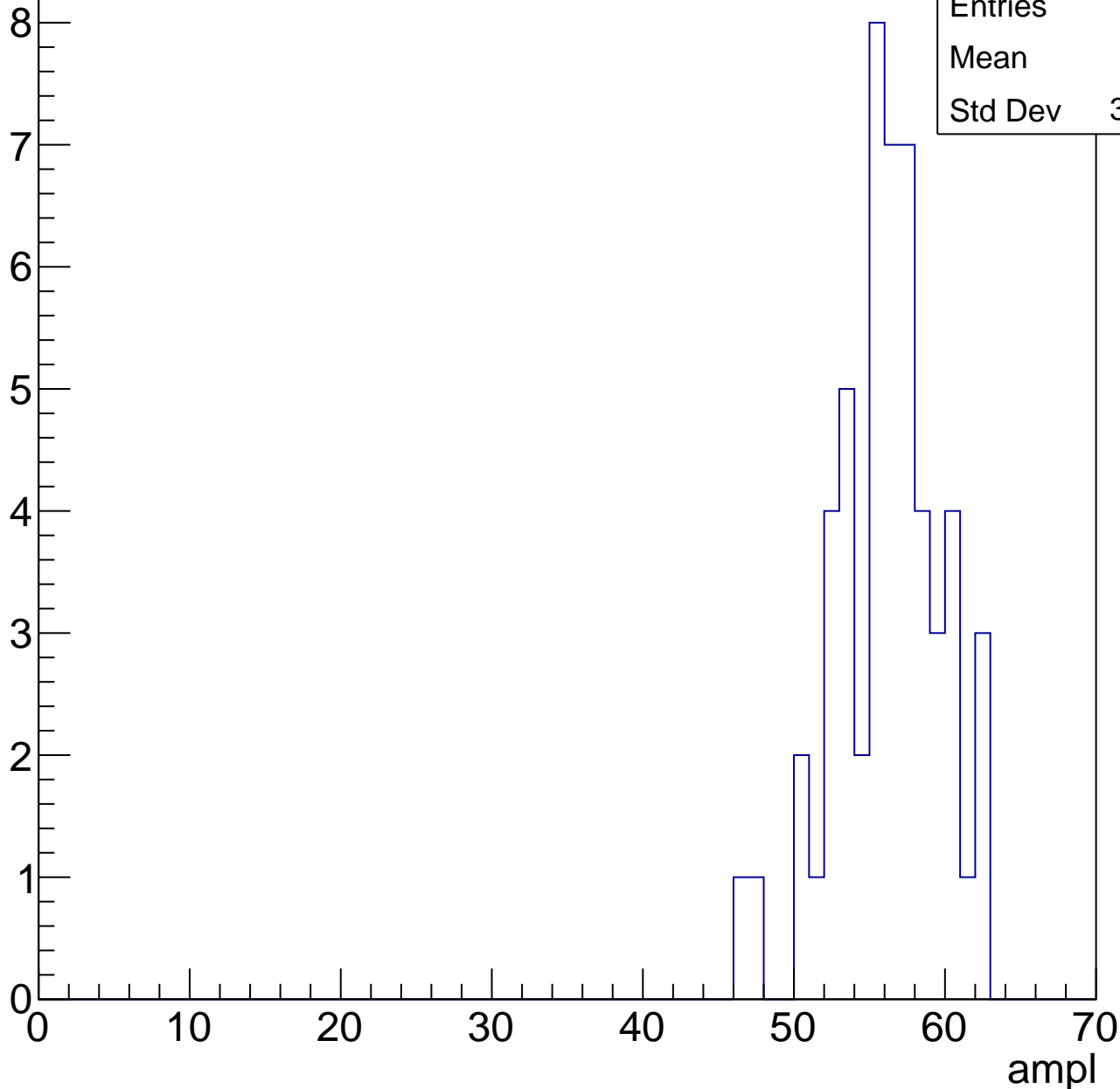


B1L100S, U11-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	55.7
Std Dev	3.489

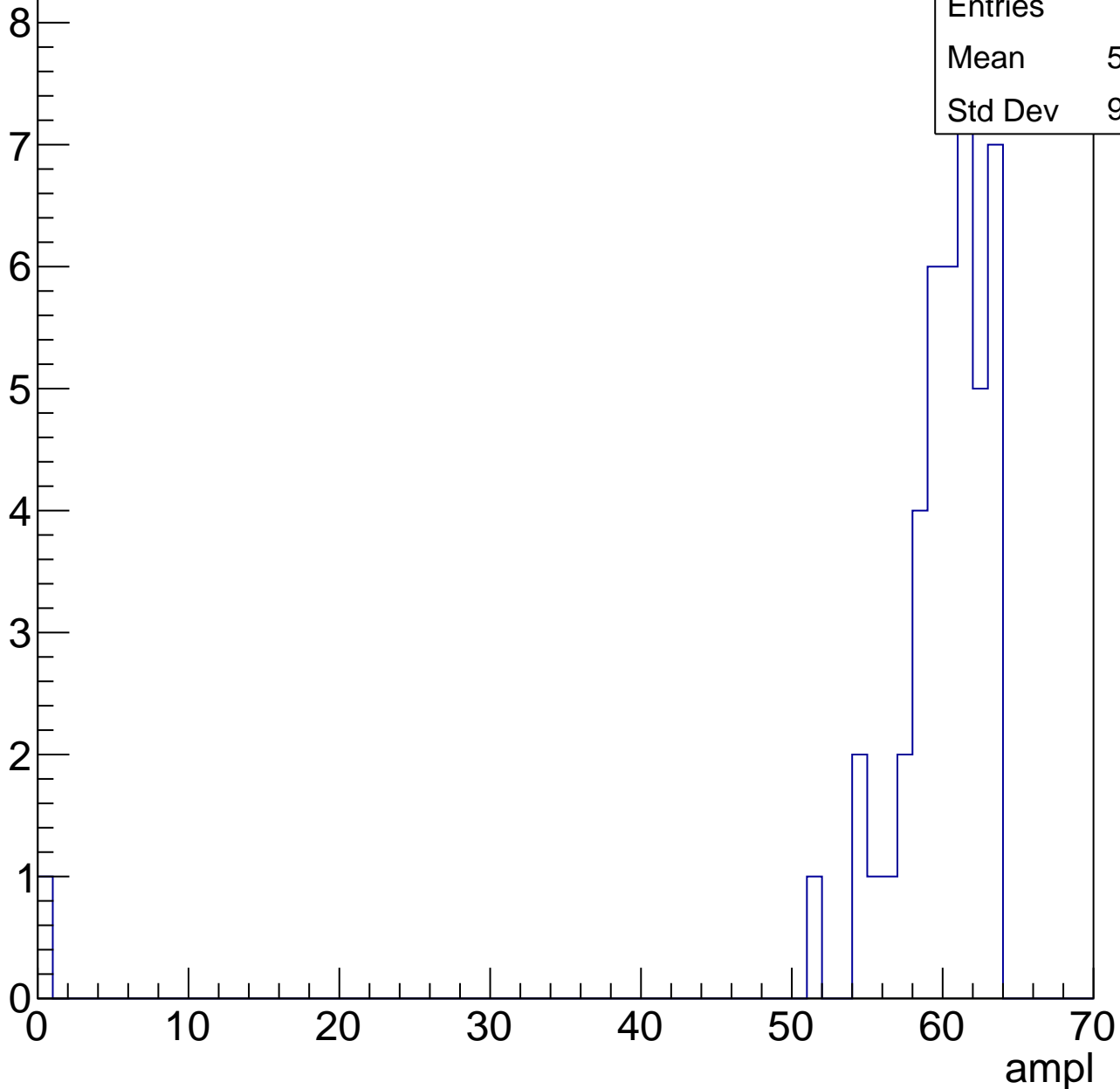


B1L100S, U11-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

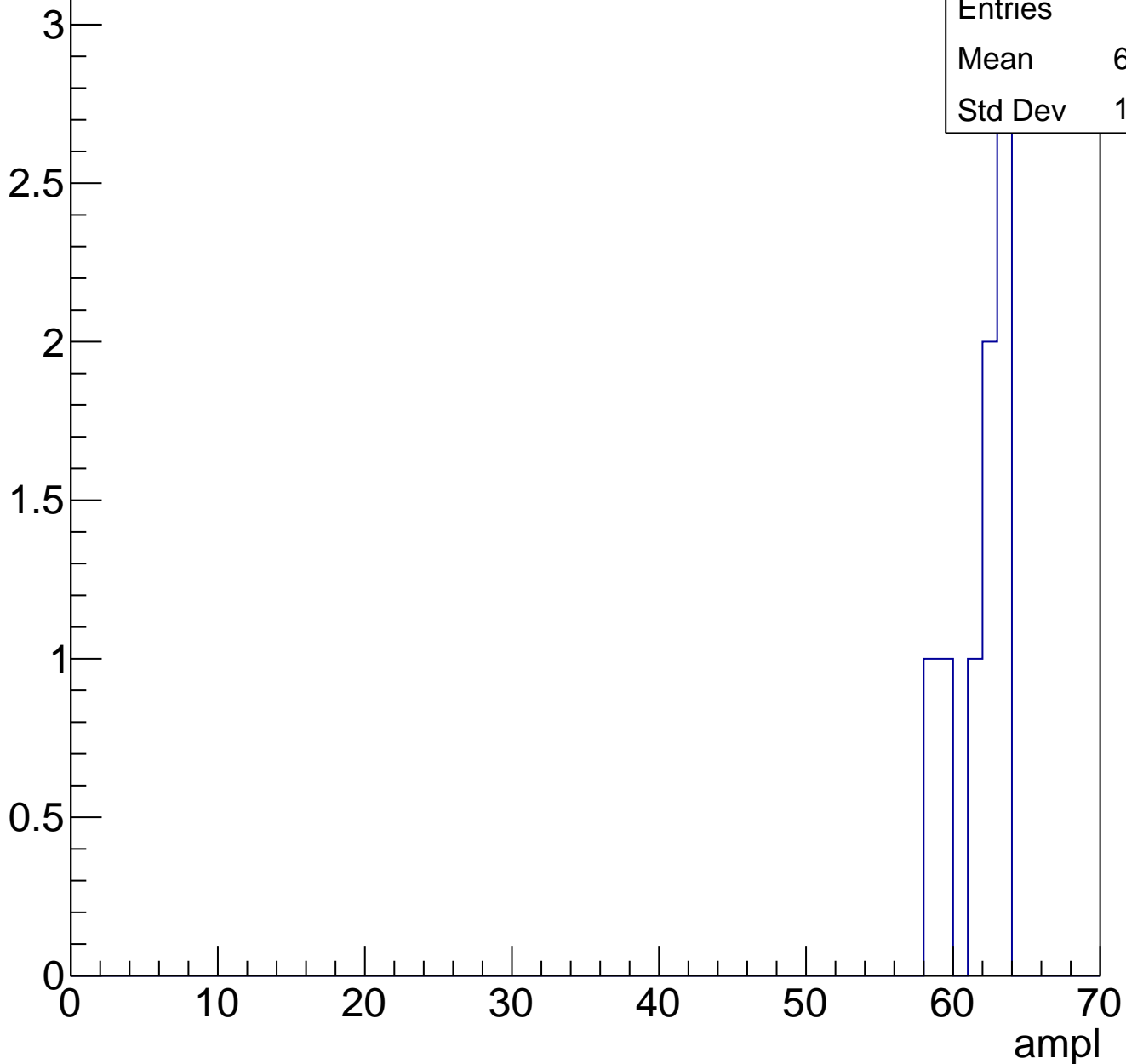
Entries	44
Mean	58.39
Std Dev	9.306



B1L100S, U11-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch103, adc0

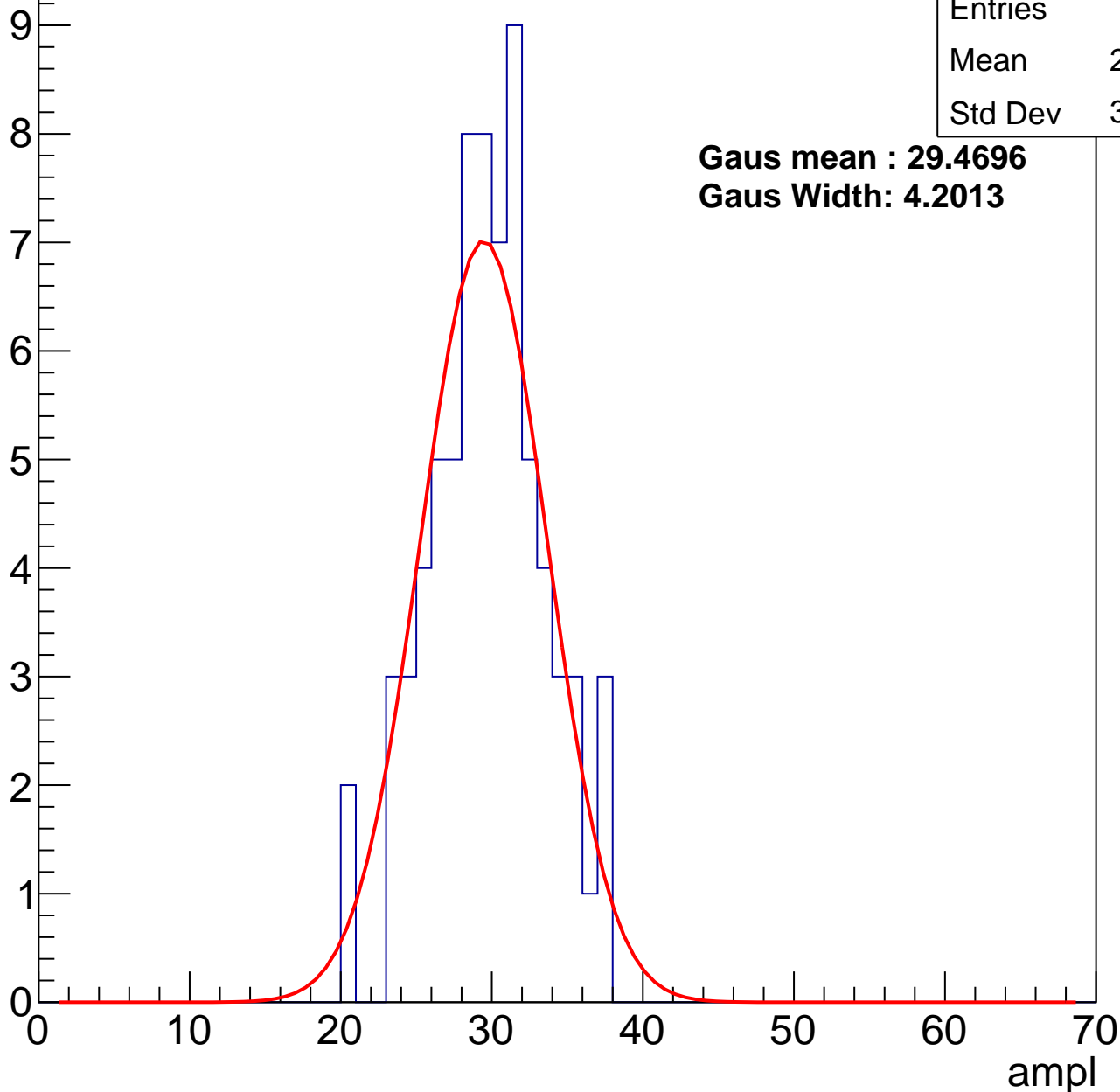
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	29.27
Std Dev	3.797

Gaus mean : 29.4696

Gaus Width: 4.2013



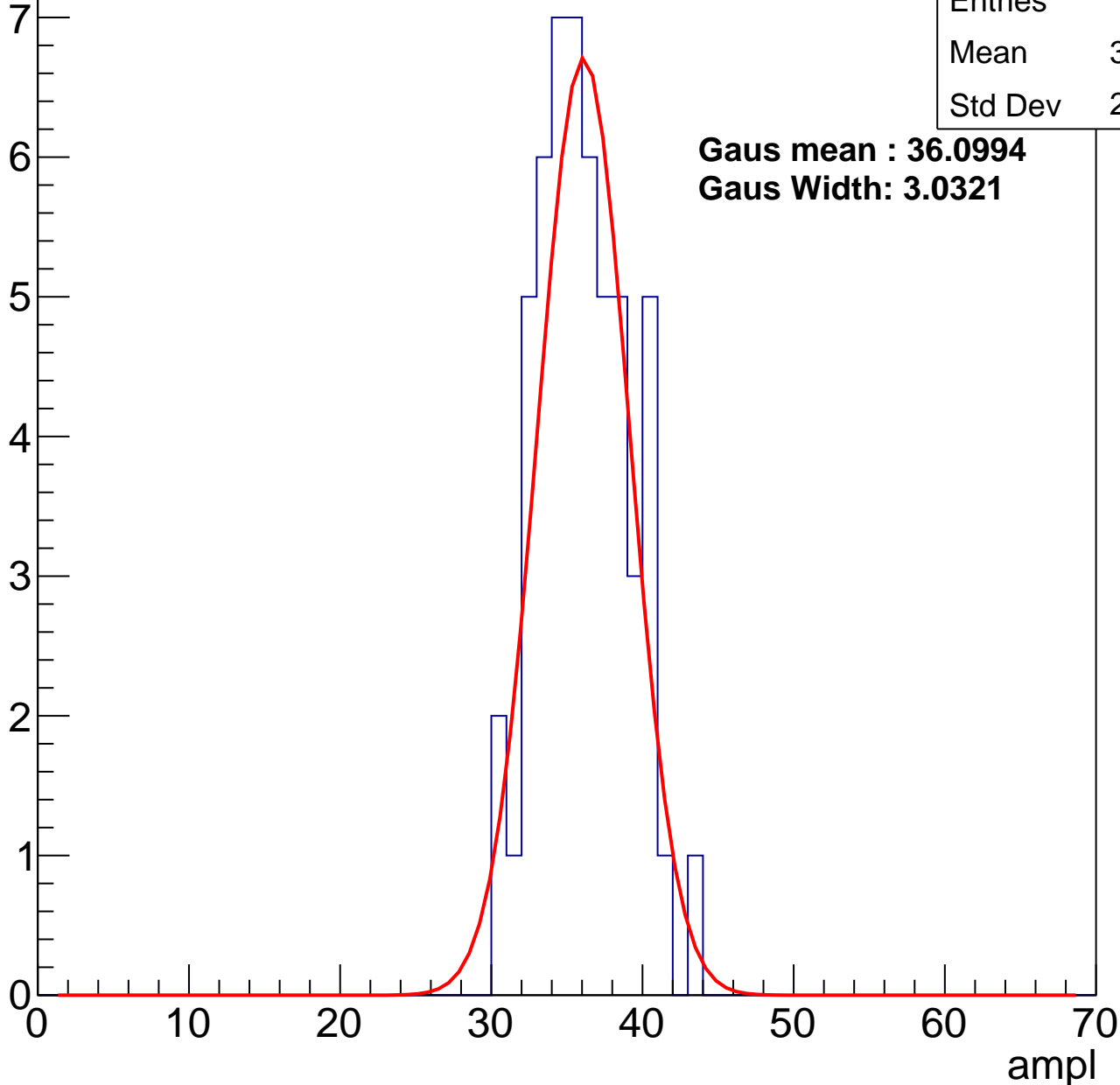
B1L100S, U11-ch103, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	35.63
Std Dev	2.933

Gaus mean : 36.0994
Gaus Width: 3.0321



B1L100S, U11-ch103, adc2

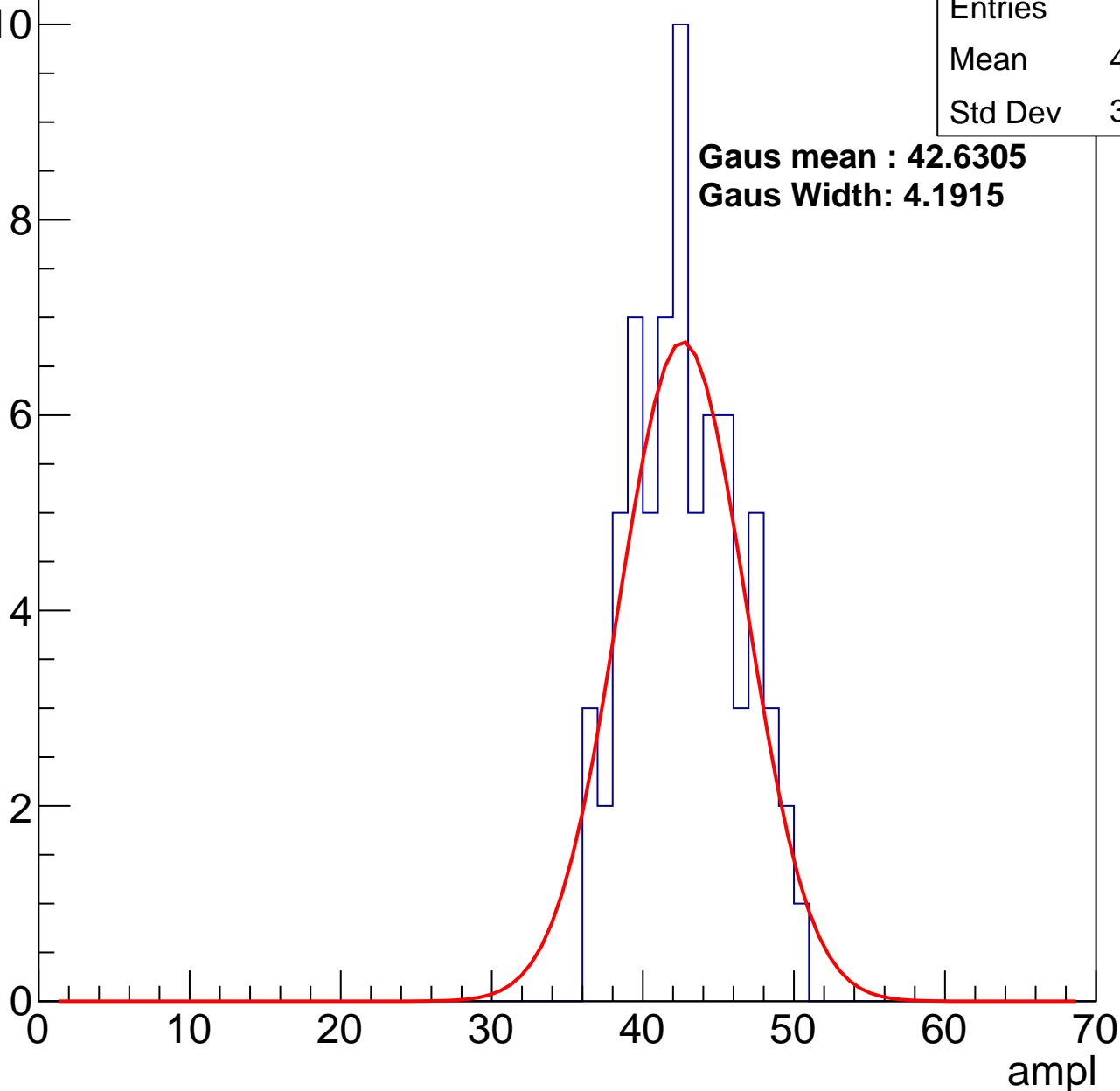
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.37
Std Dev	3.473

Gaus mean : 42.6305

Gaus Width: 4.1915

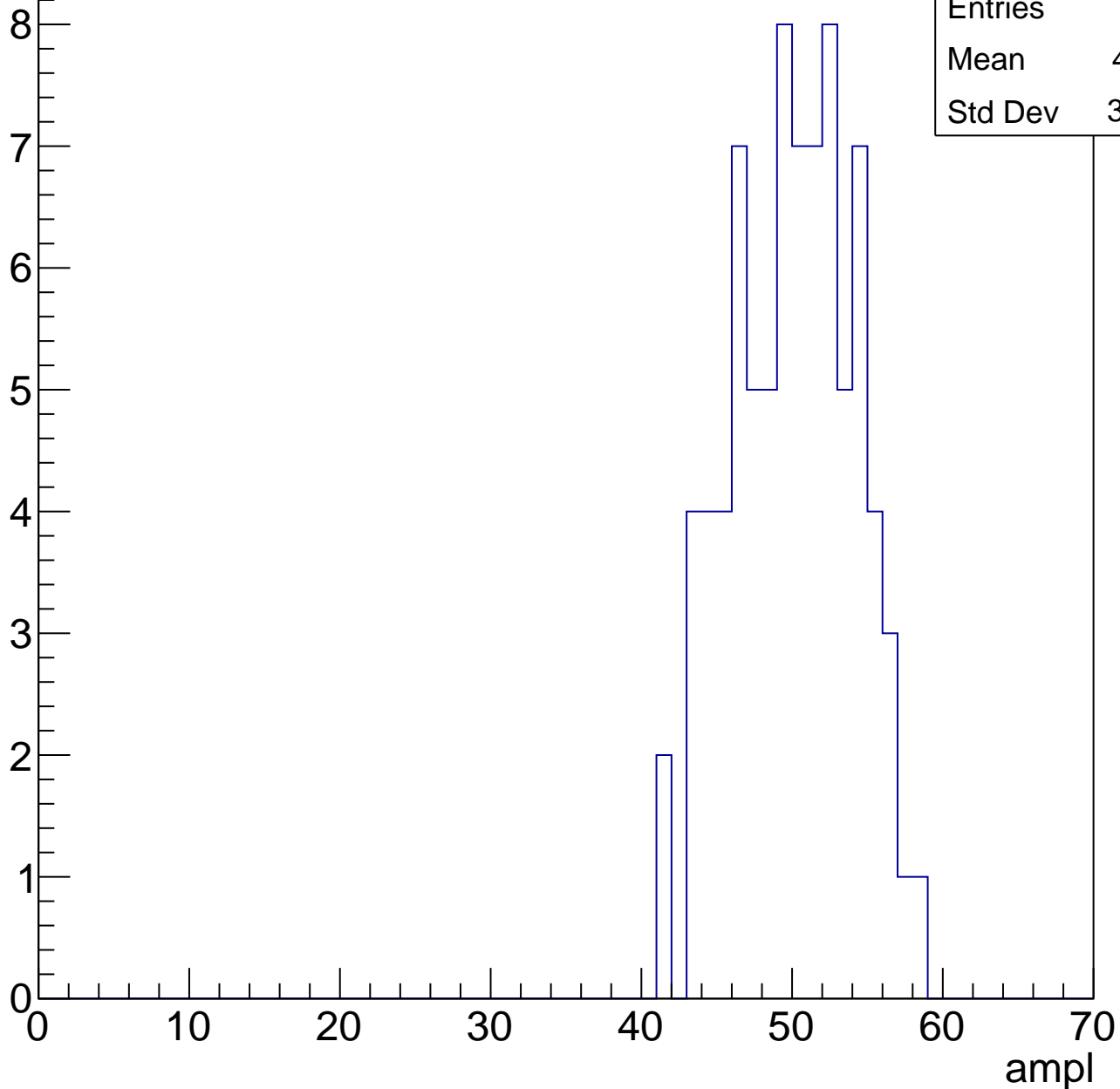


B1L100S, U11-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	49.61
Std Dev	3.969

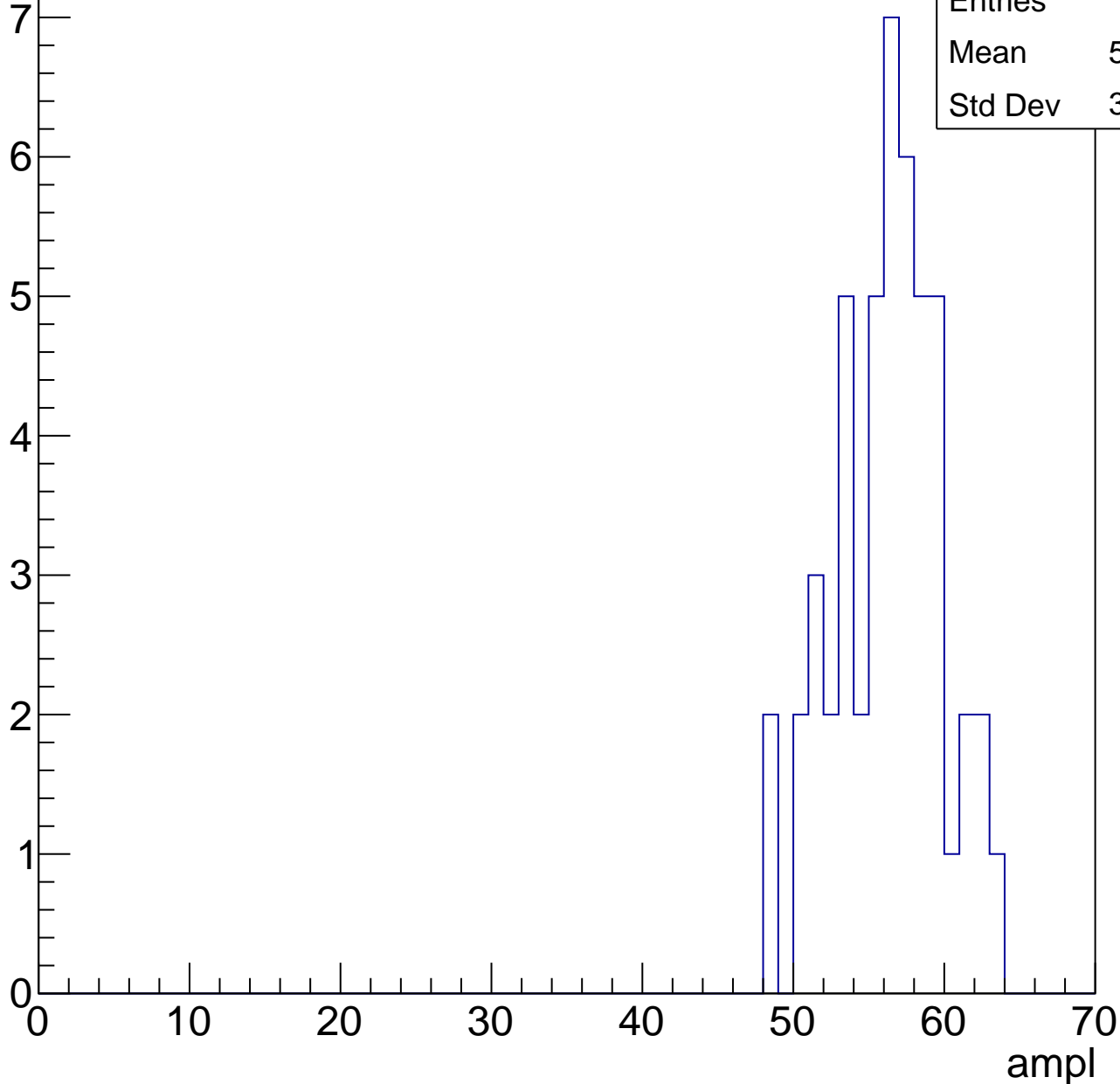


B1L100S, U11-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	55.78
Std Dev	3.523

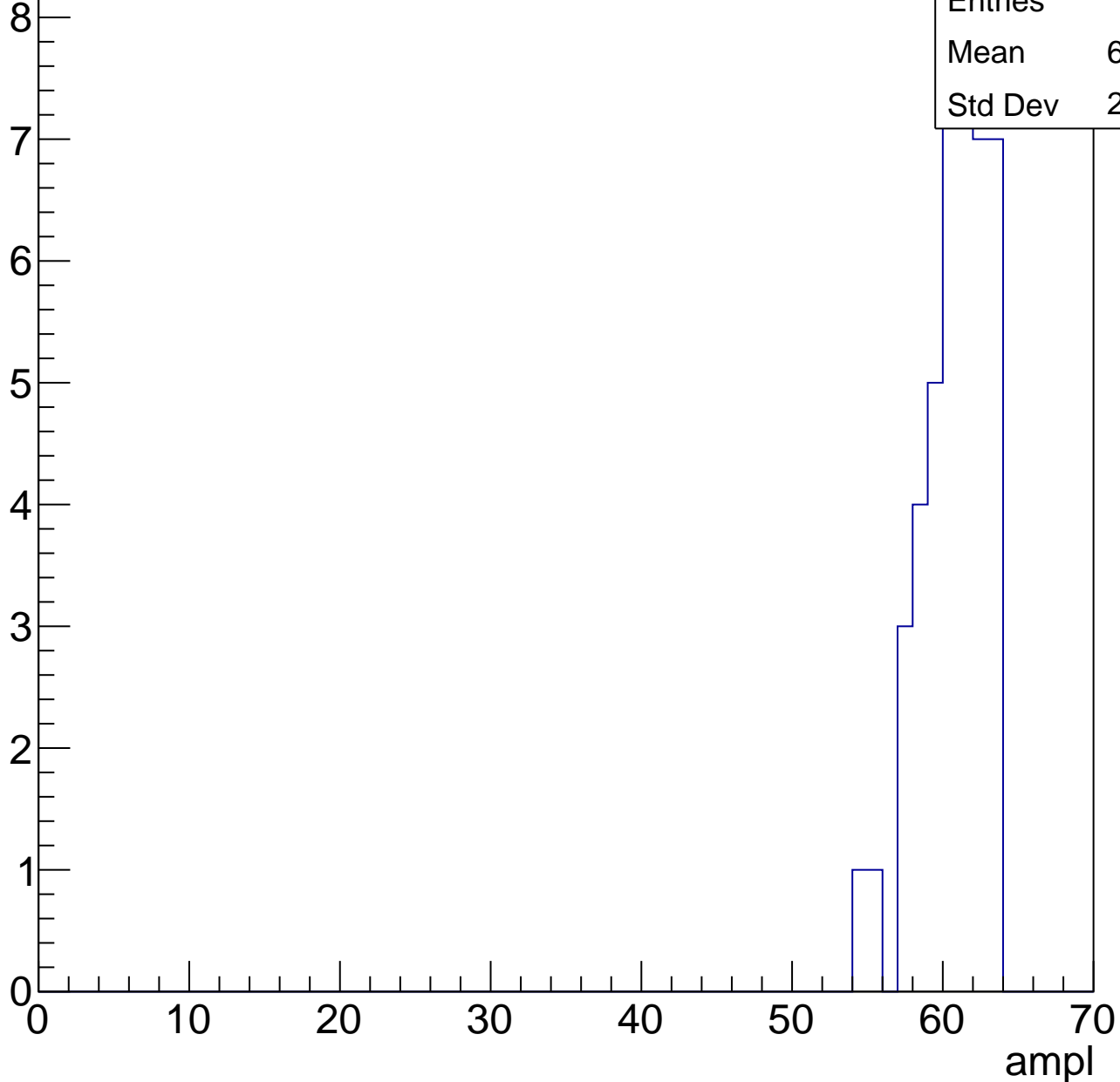


B1L100S, U11-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

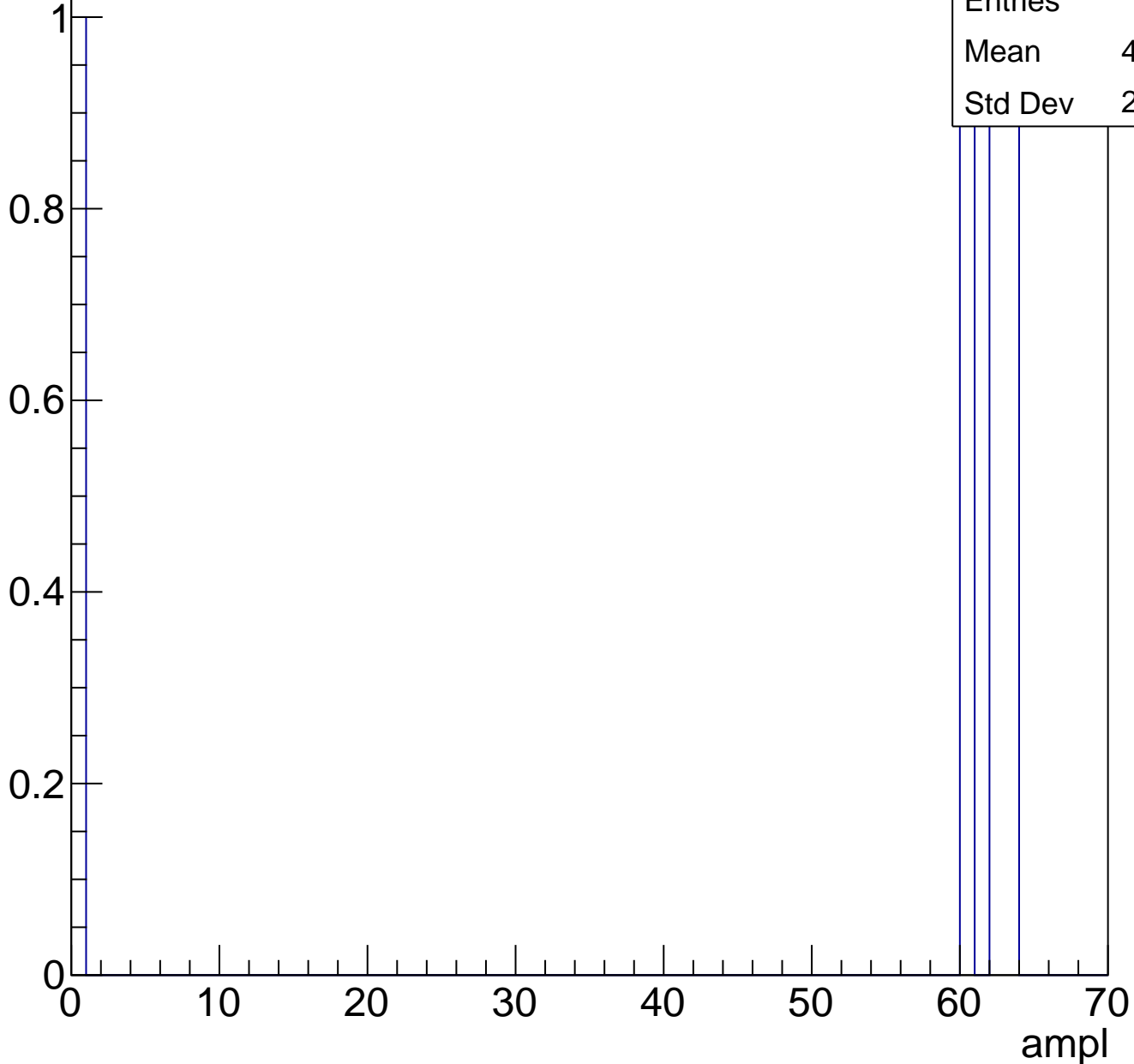
Entries	44
Mean	60.23
Std Dev	2.162



B1L100S, U11-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

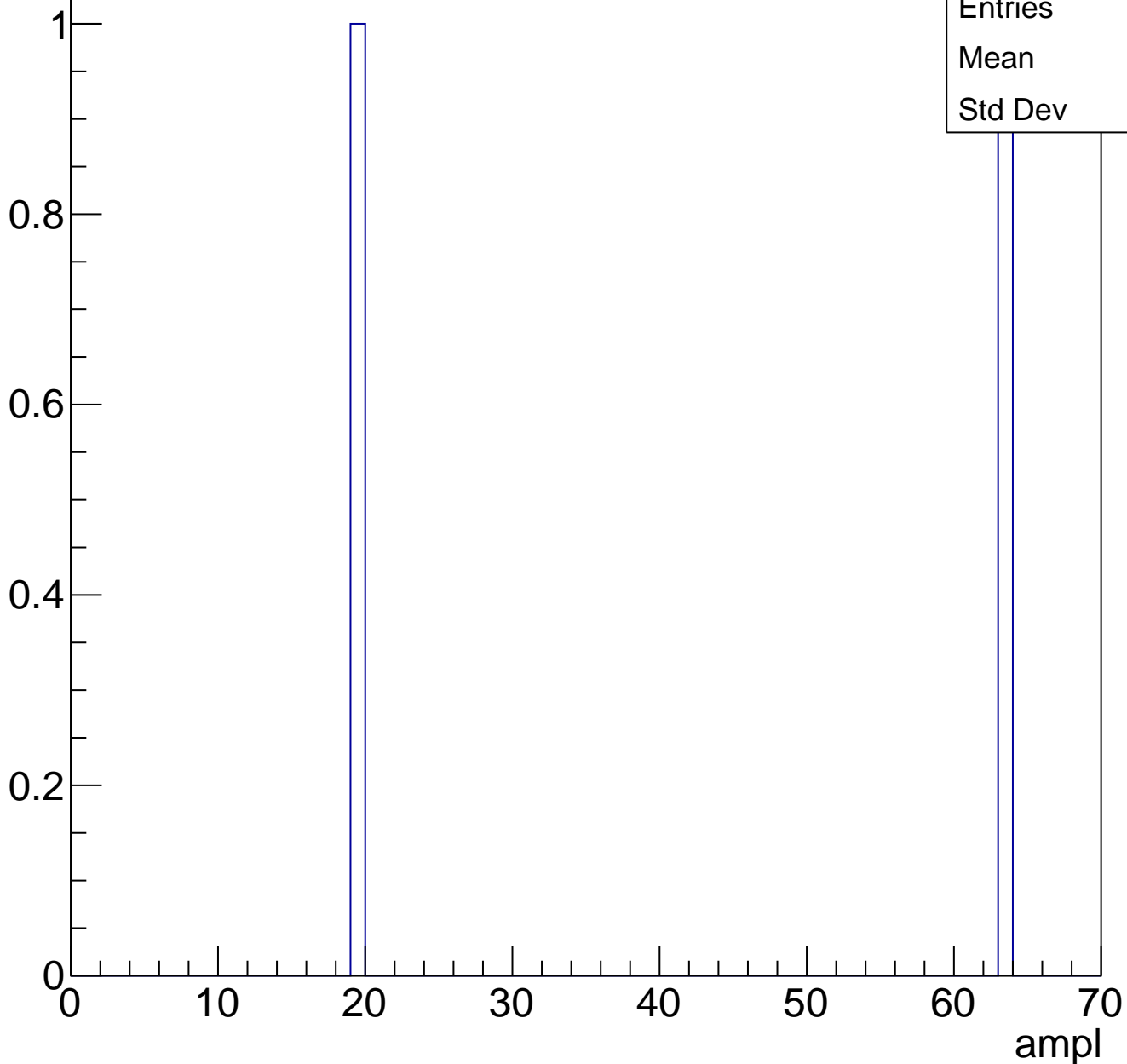
Entry



B1L100S, U11-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch104, adc0

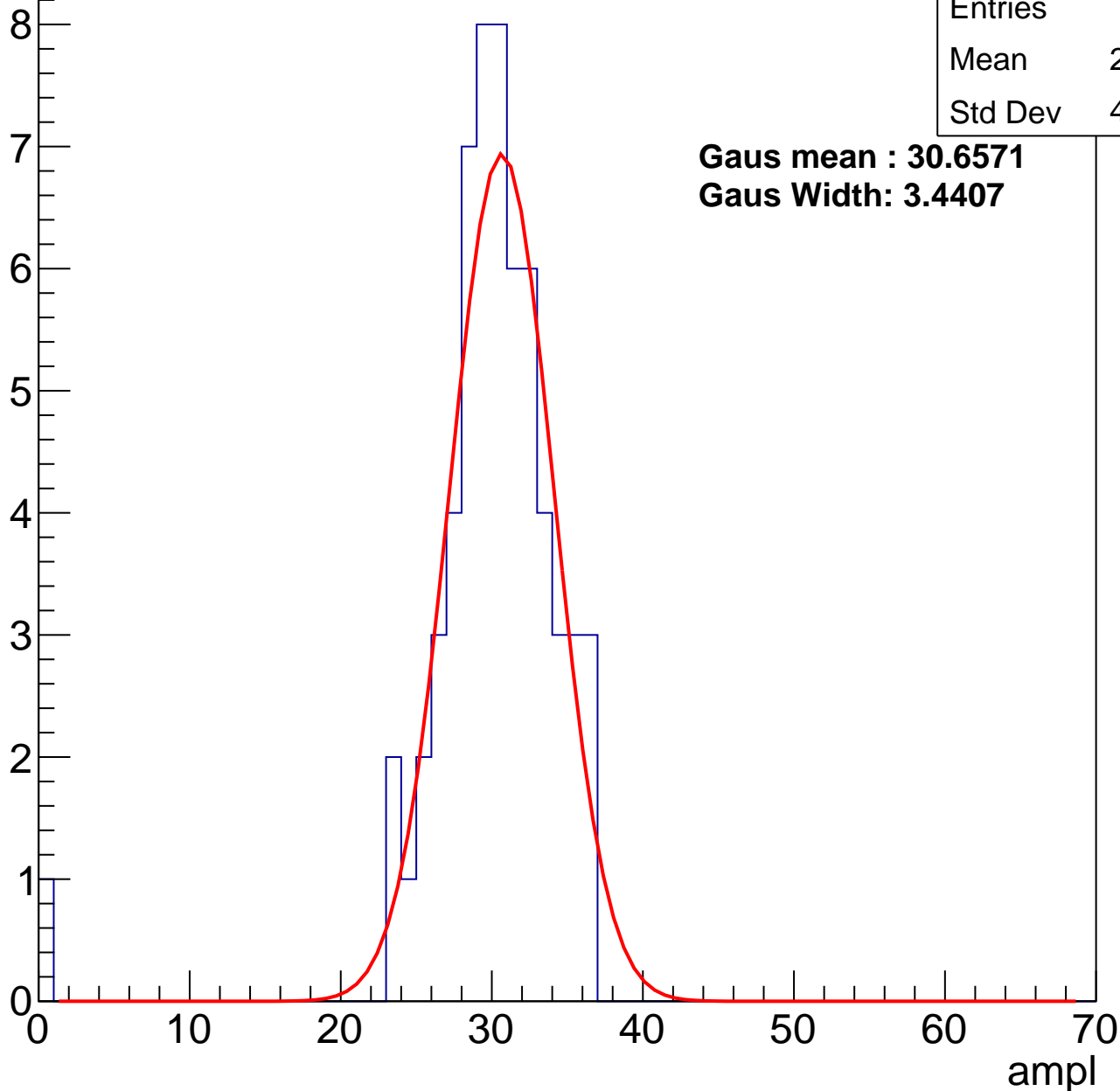
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.49
Std Dev	4.934

Gaus mean : 30.6571

Gaus Width: 3.4407



B1L100S, U11-ch104, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	36.18
Std Dev	3.43

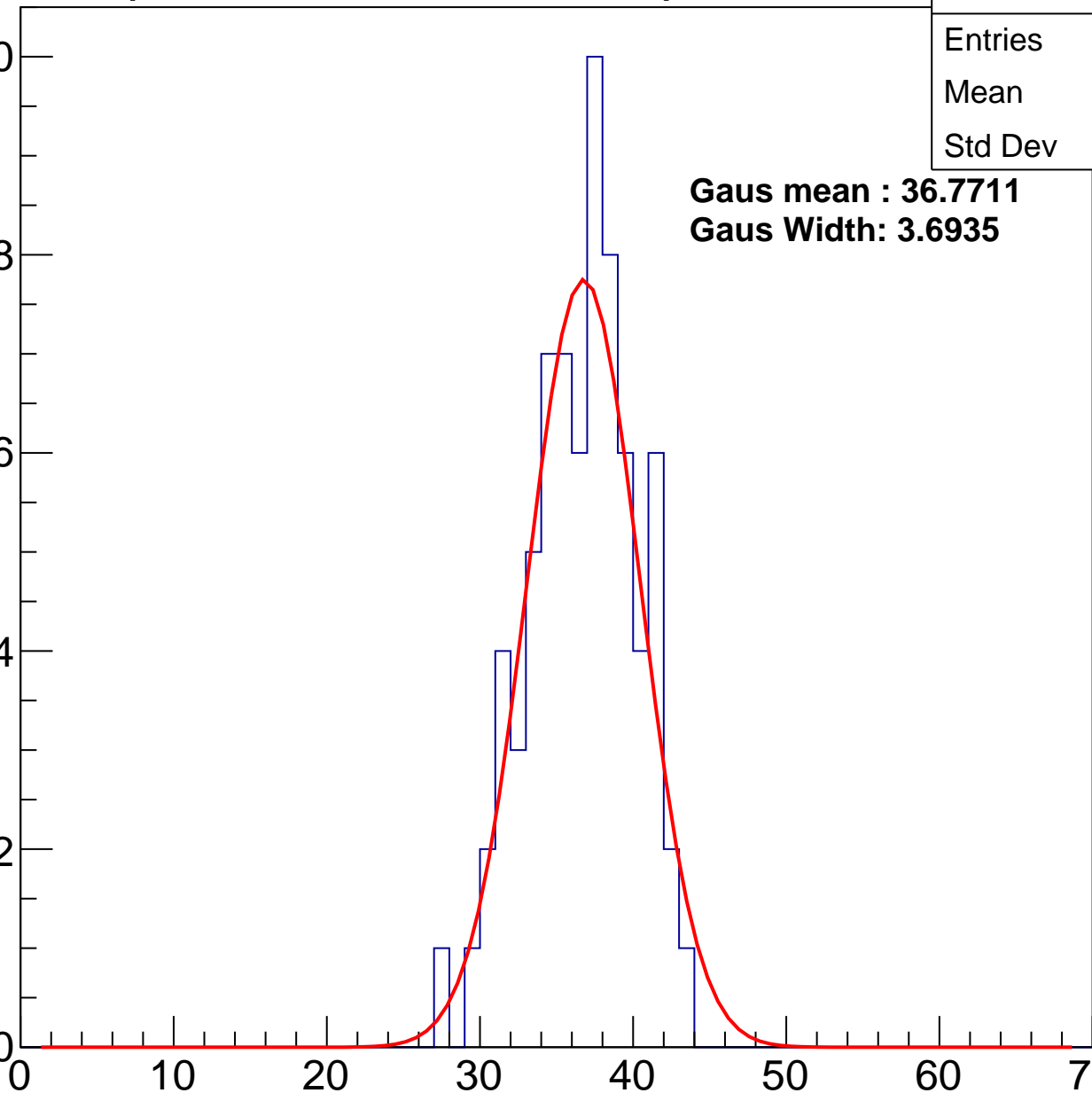
Gaus mean : 36.7711

Gaus Width: 3.6935

Entry

10
8
6
4
2
0

ampl



B1L100S, U11-ch104, adc2

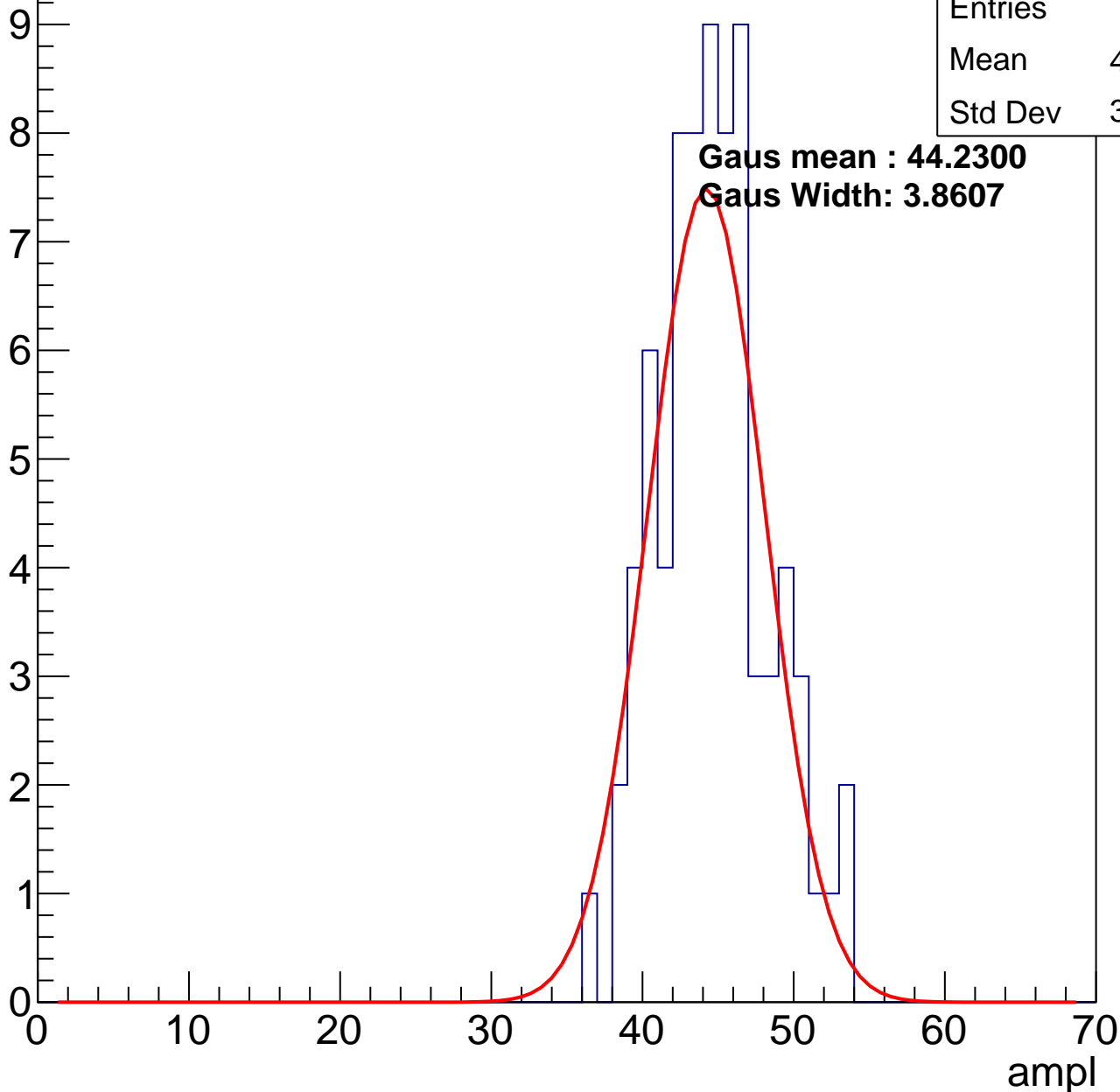
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	44.24
Std Dev	3.638

Gaus mean : 44.2300

Gaus Width: 3.8607

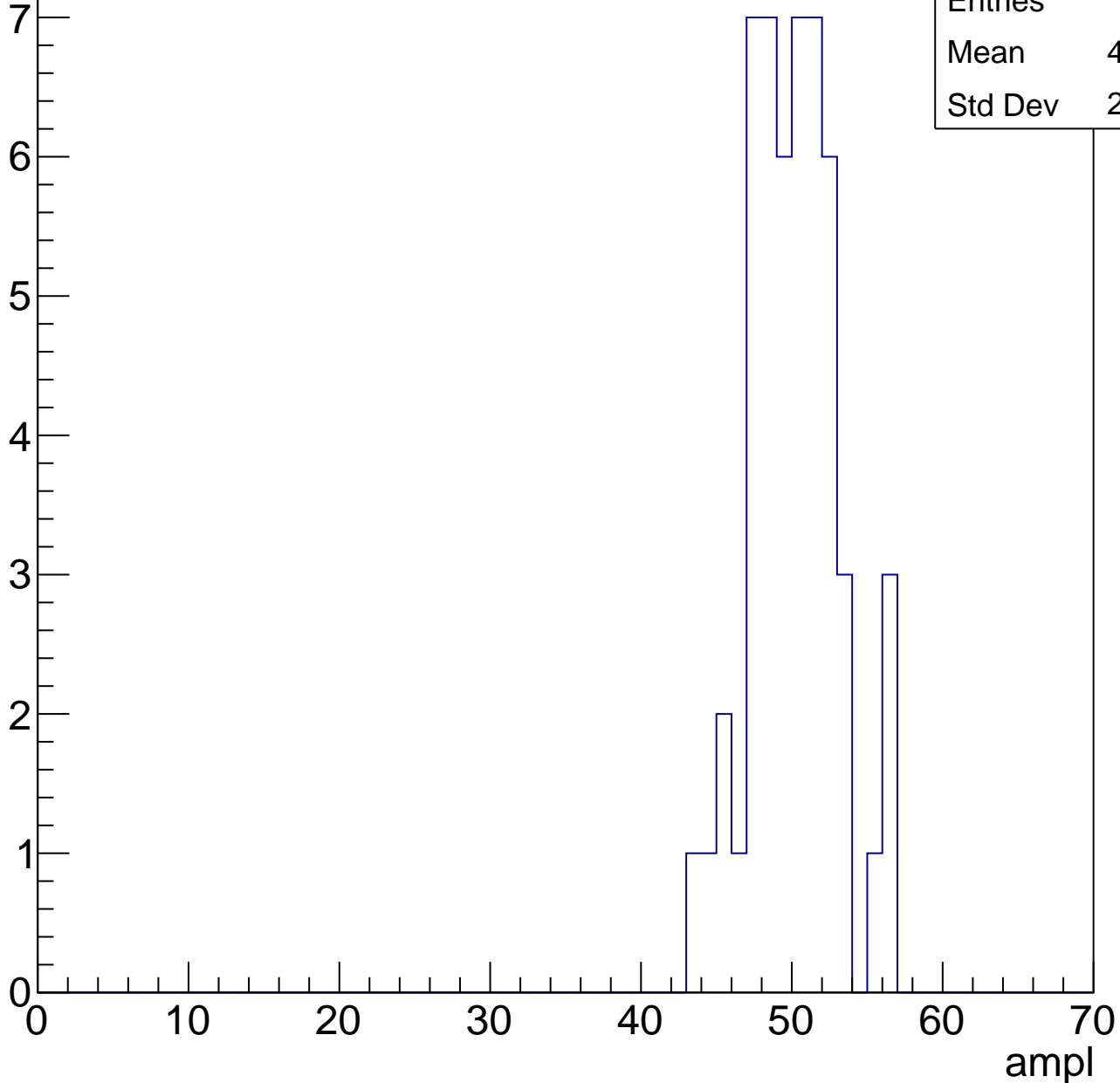


B1L100S, U11-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	49.67
Std Dev	2.887

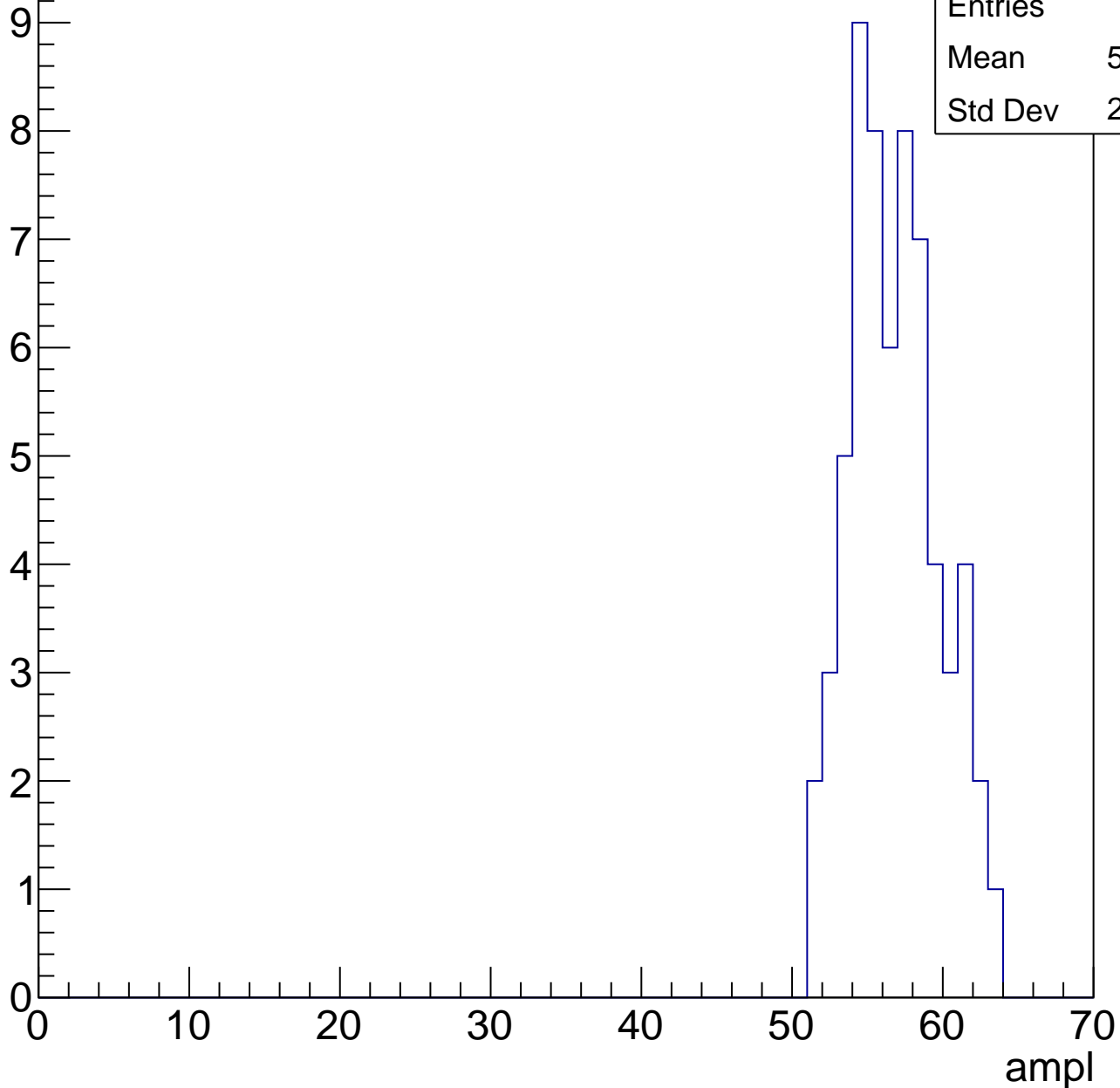


B1L100S, U11-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	56.35
Std Dev	2.897



B1L100S, U11-ch104, adc5

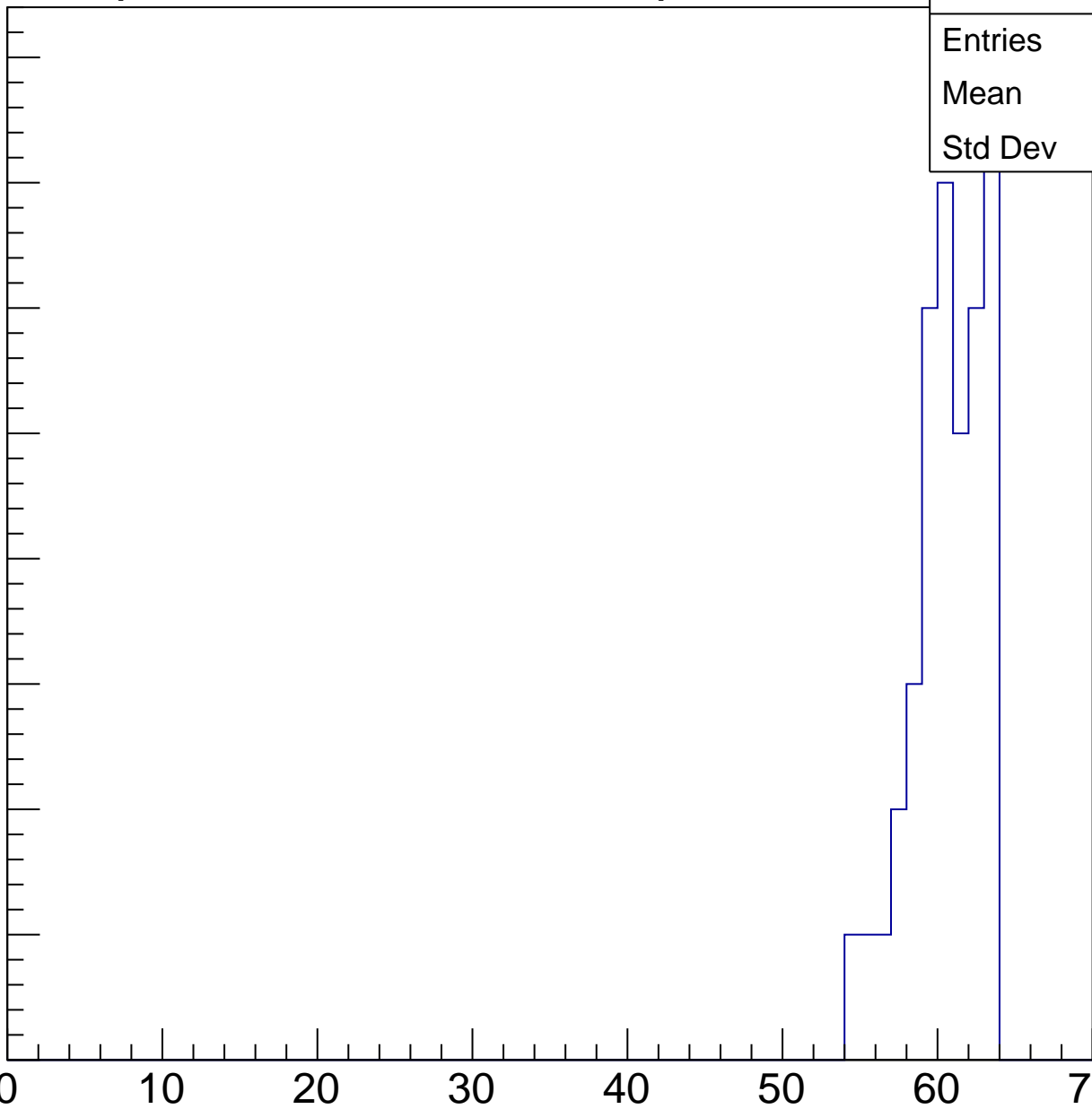
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.2
Std Dev	2.304

ampl



B1L100S, U11-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch105, adc0

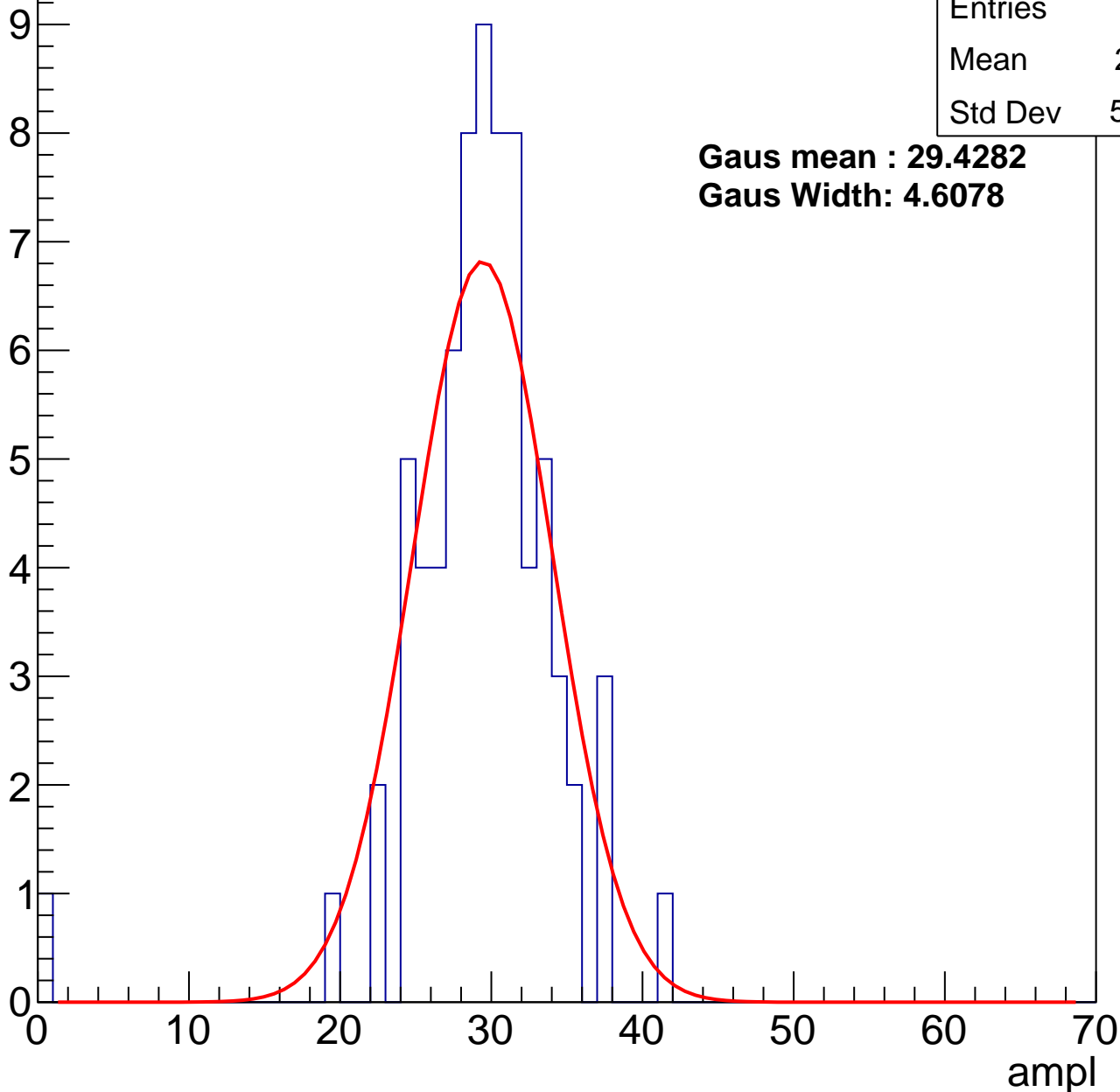
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.91
Std Dev	5.113

Gaus mean : 29.4282

Gaus Width: 4.6078



B1L100S, U11-ch105, adc1

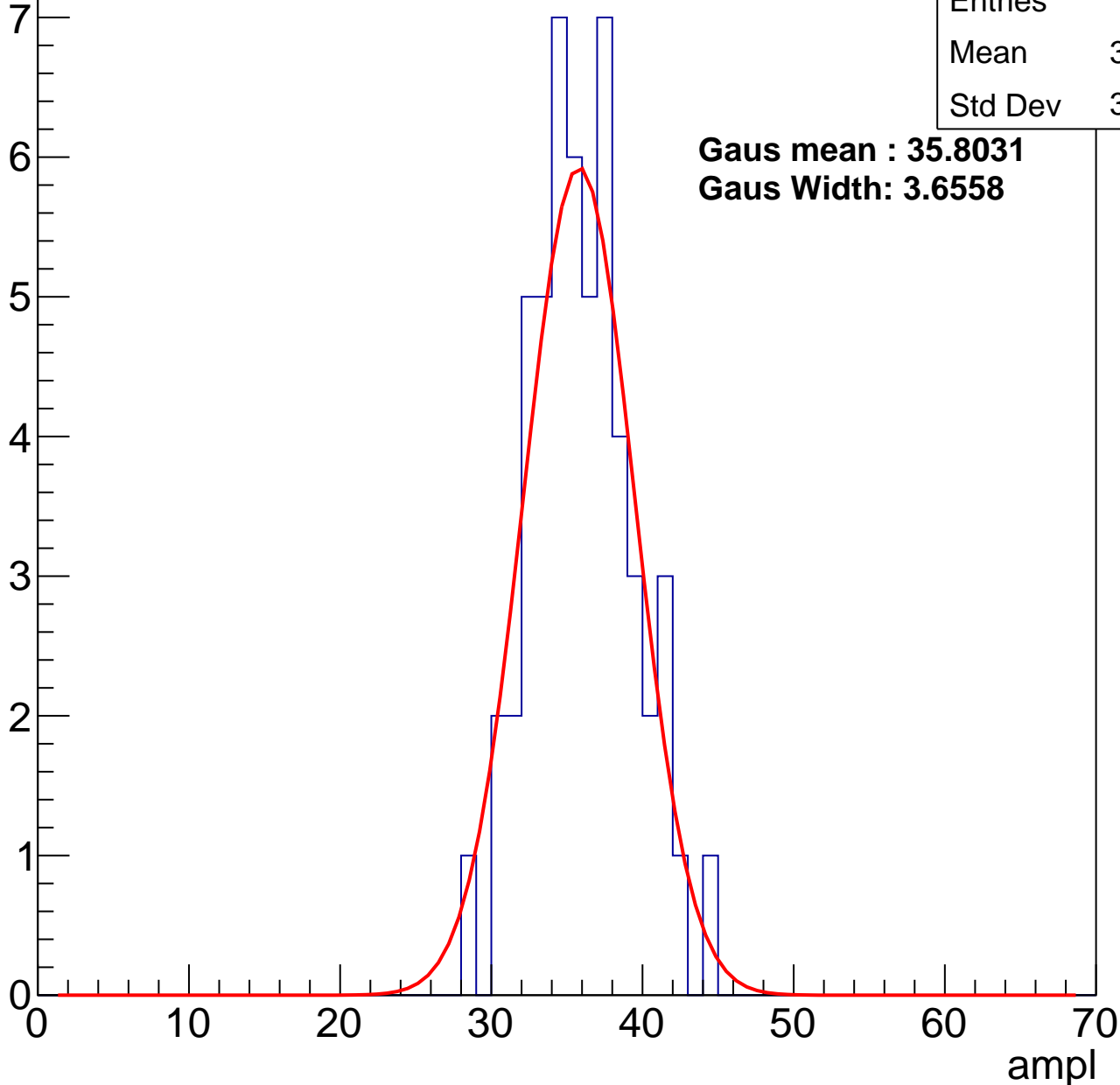
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	35.56
Std Dev	3.309

Gaus mean : 35.8031

Gaus Width: 3.6558



B1L100S, U11-ch105, adc2

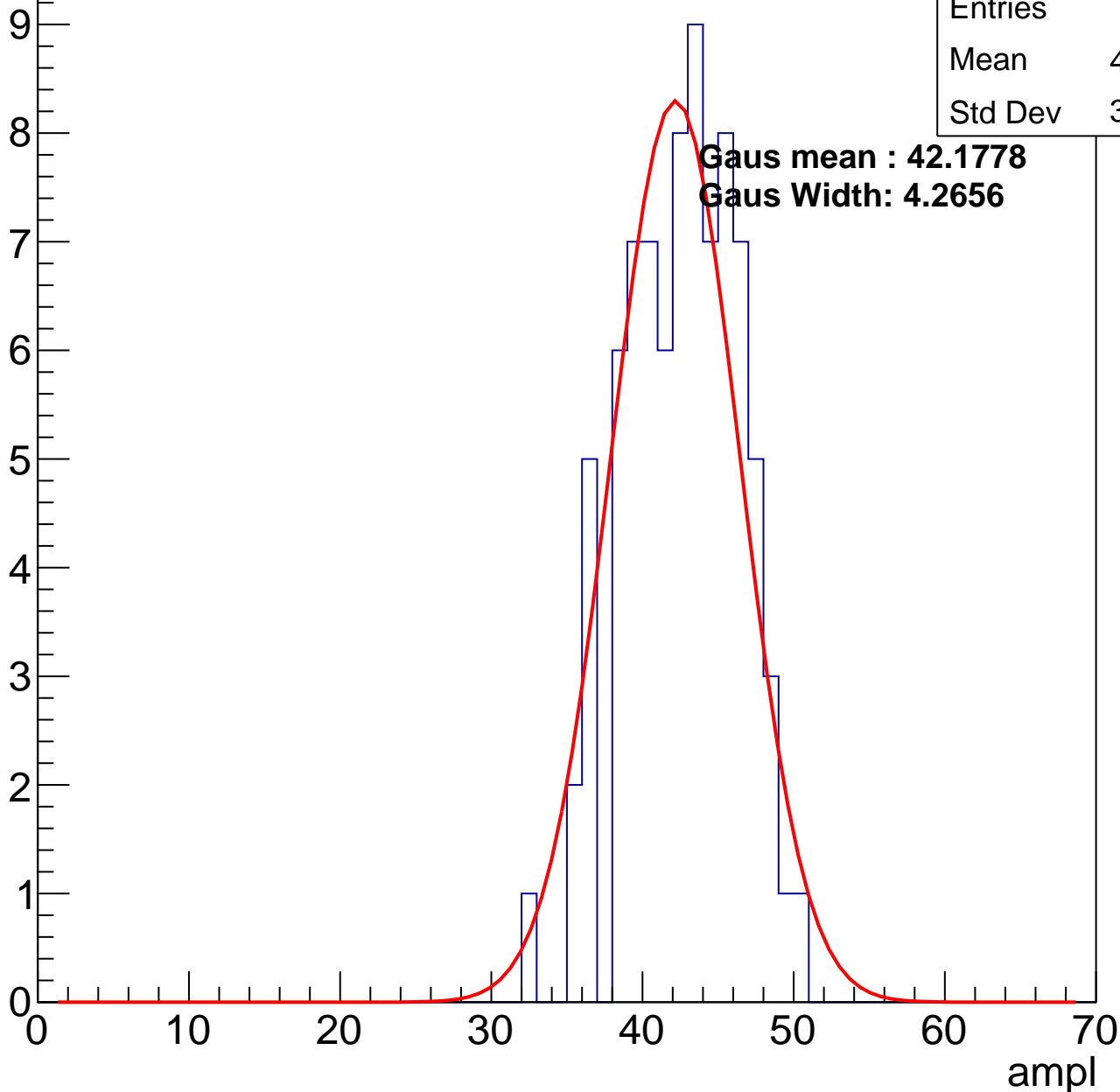
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	42.17
Std Dev	3.699

Gaus mean : 42.1778

Gaus Width: 4.2656

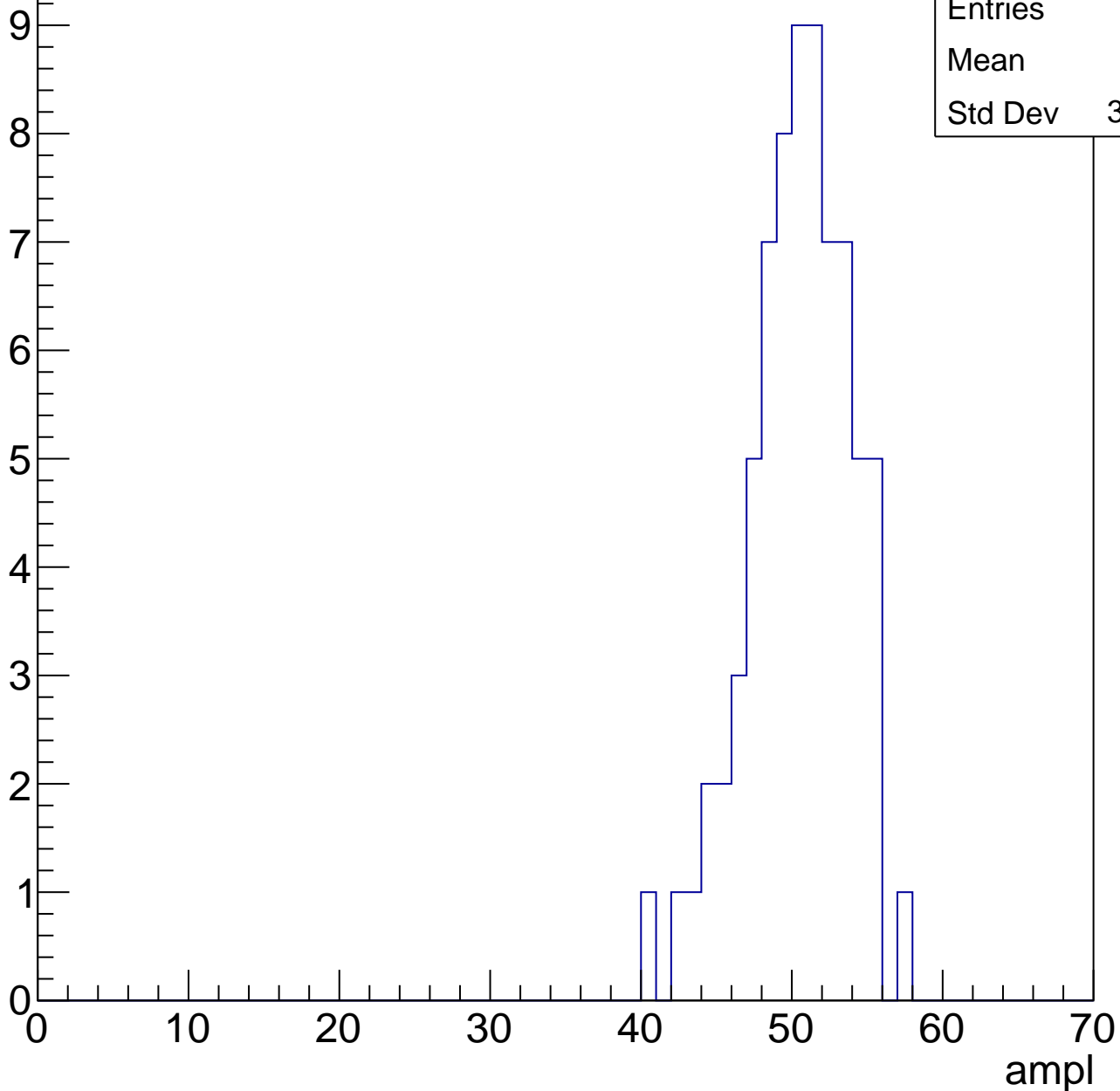


B1L100S, U11-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

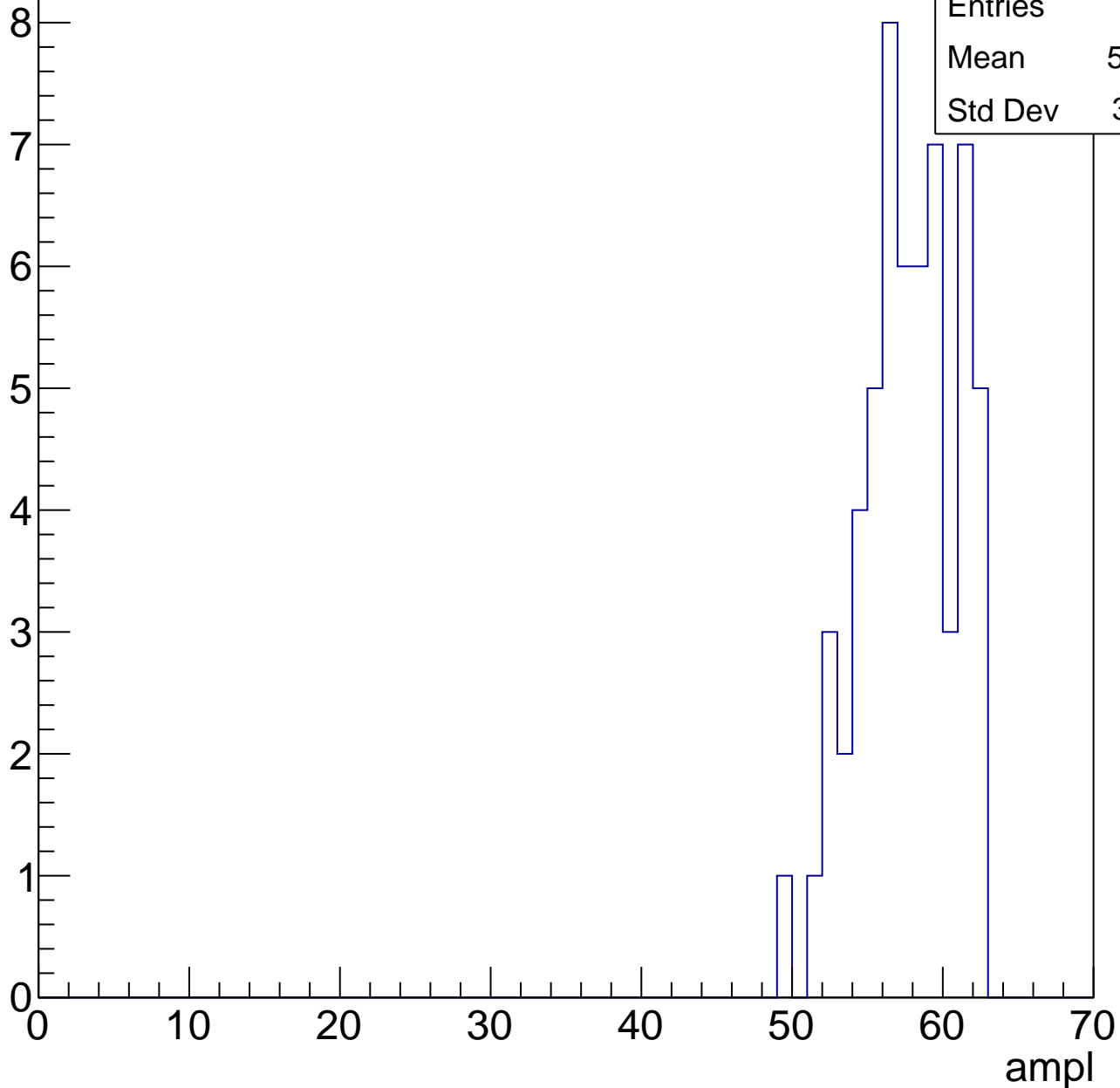
Entries	73
Mean	50
Std Dev	3.347



B1L100S, U11-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	58
Mean	57.26
Std Dev	3.121

B1L100S, U11-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	40
Mean	58.55
Std Dev	9.736

Entry

10

8

6

4

2

0

0

10

20

30

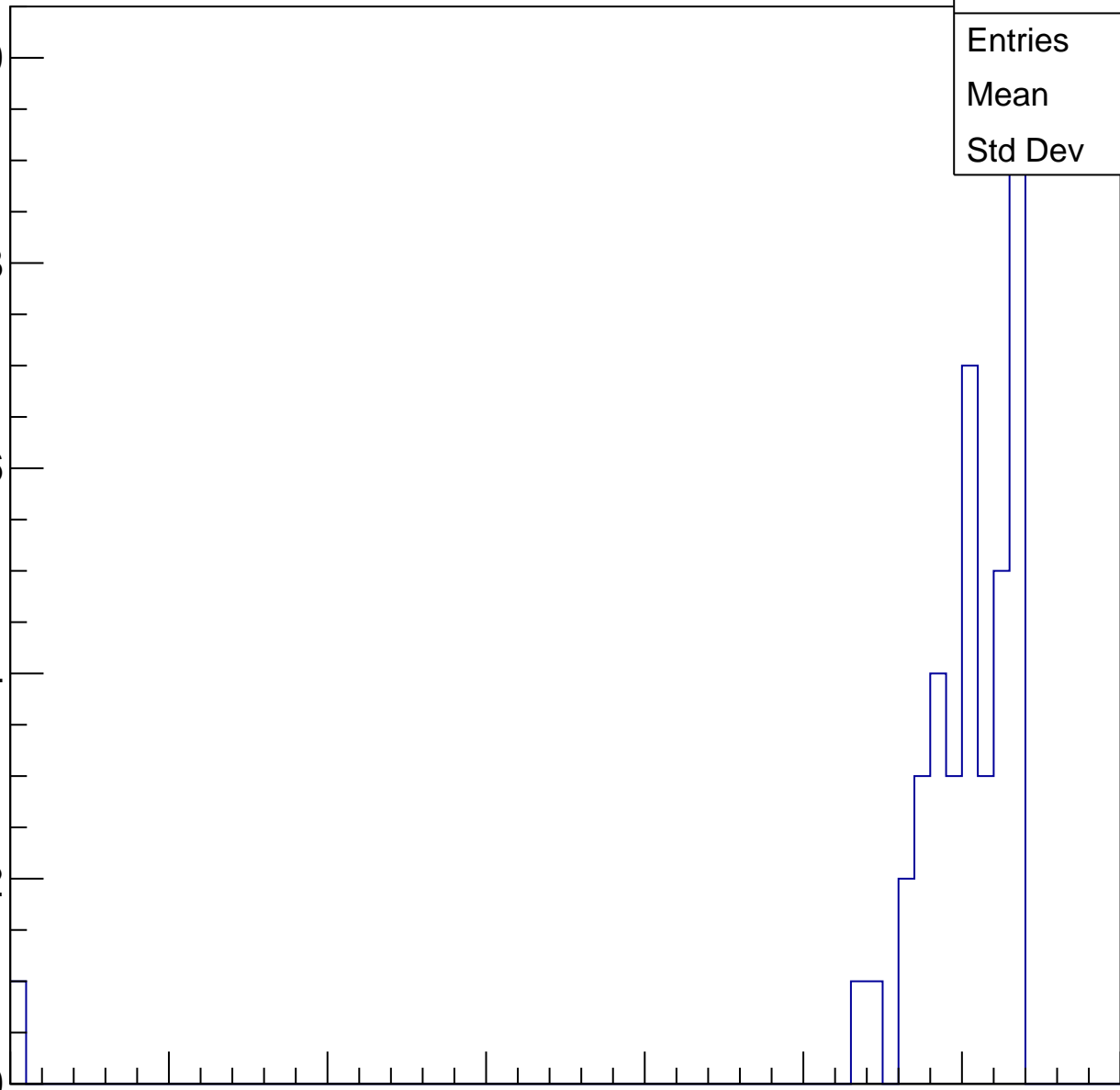
40

50

ampl

60

70



B1L100S, U11-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch106, adc0

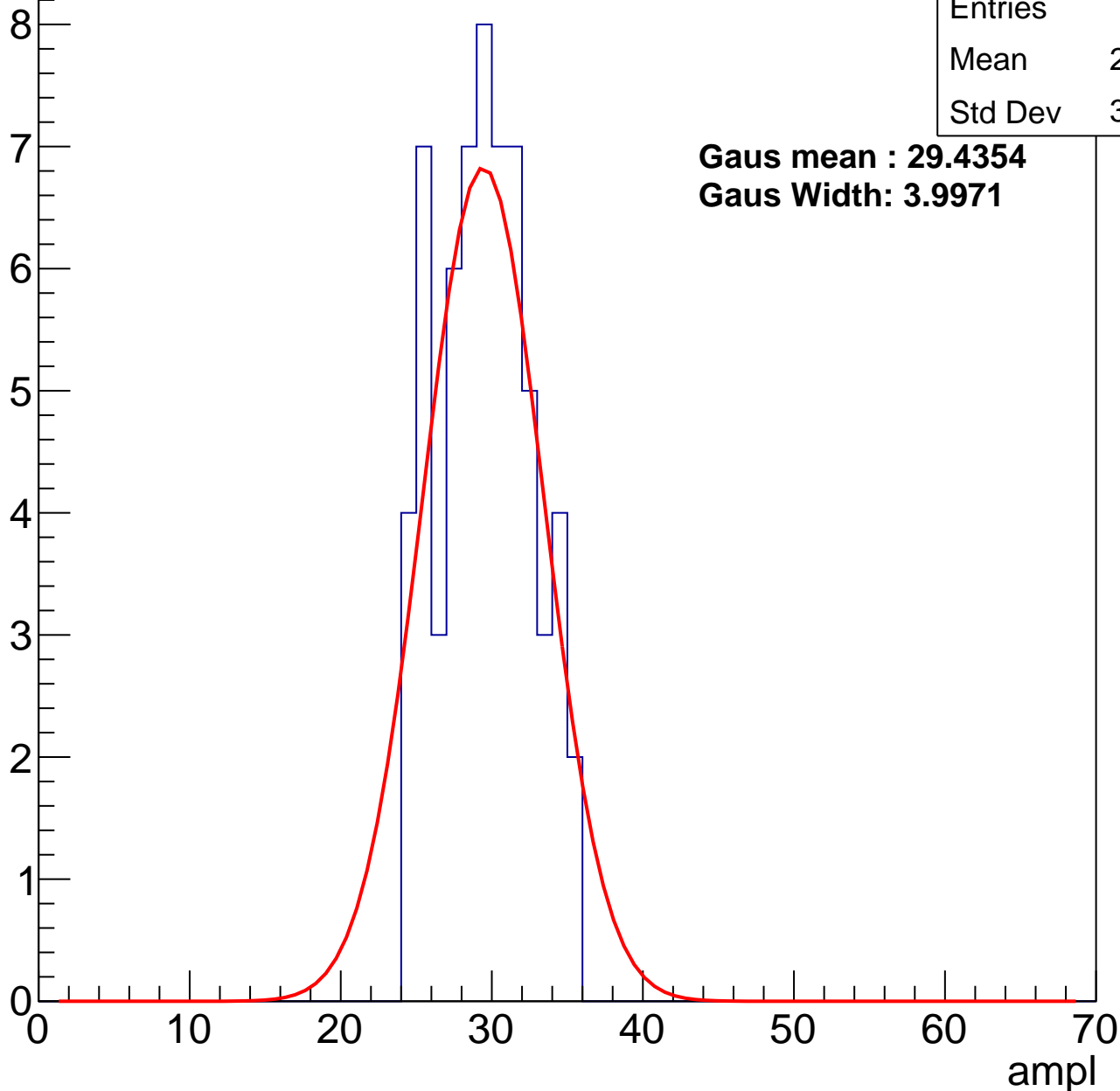
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	29.06
Std Dev	3.007

Gaus mean : 29.4354

Gaus Width: 3.9971



B1L100S, U11-ch106, adc1

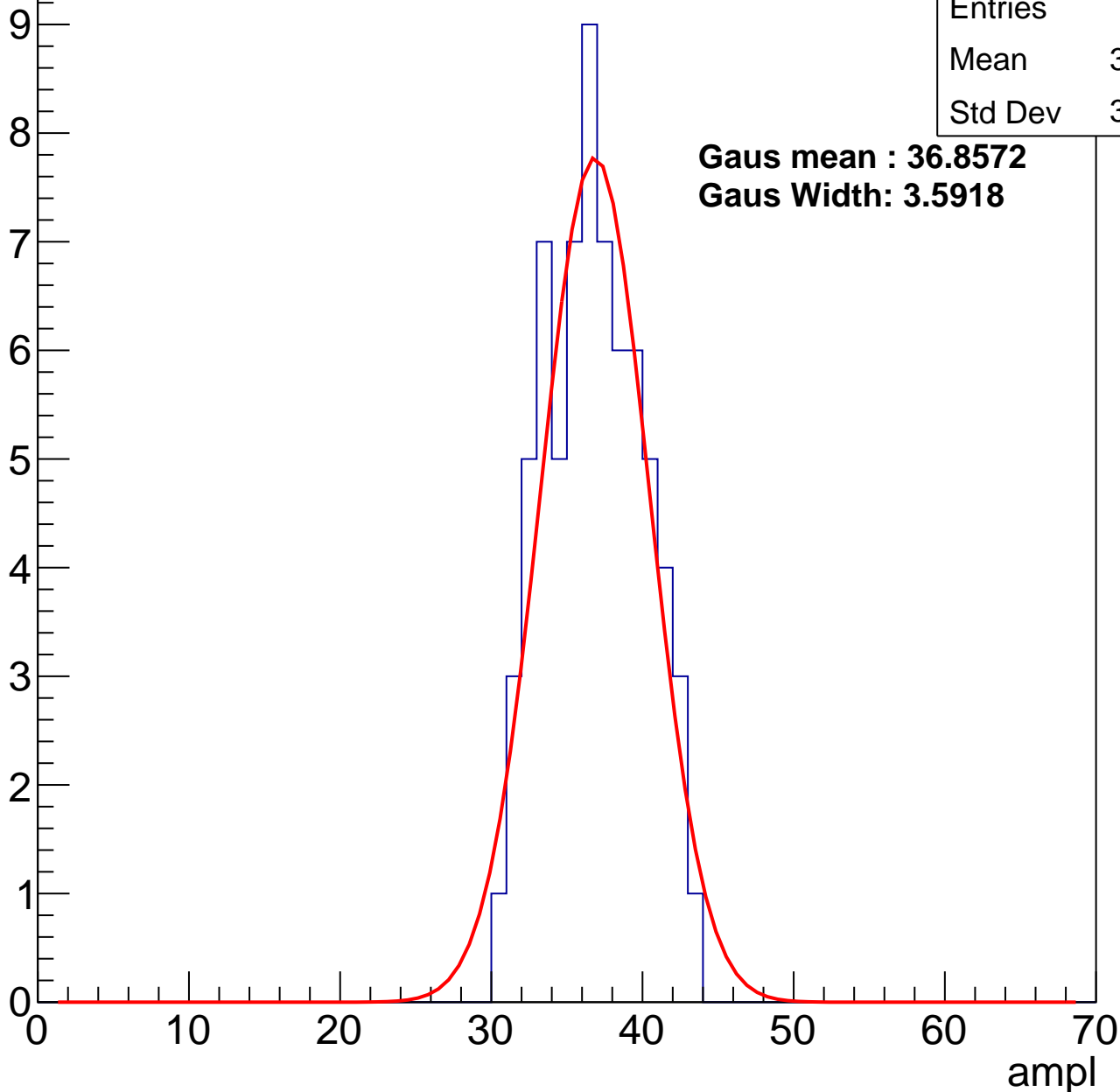
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	36.33
Std Dev	3.174

Gaus mean : 36.8572

Gaus Width: 3.5918



B1L100S, U11-ch106, adc2

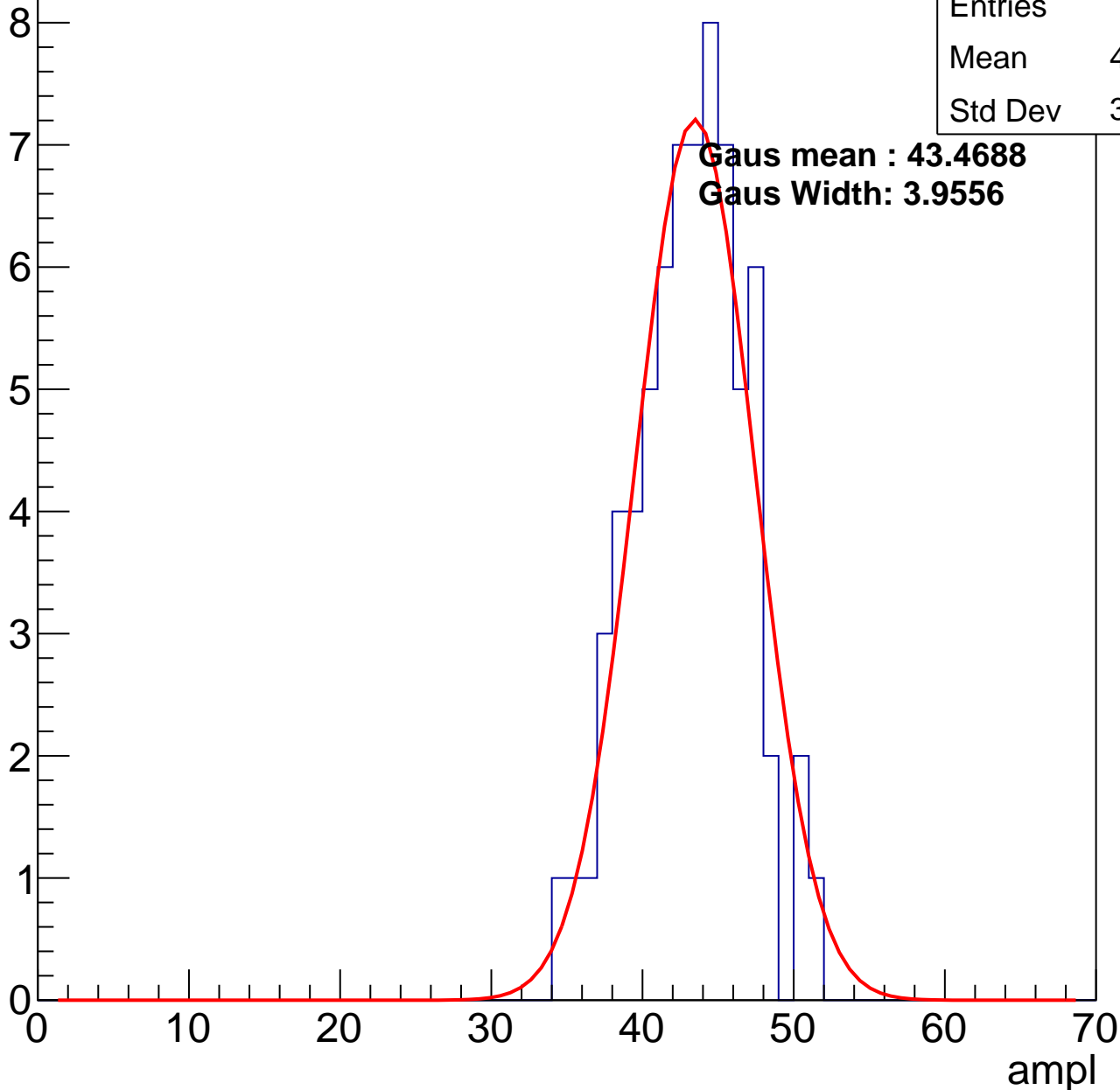
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.73
Std Dev	3.637

Gaus mean : 43.4688

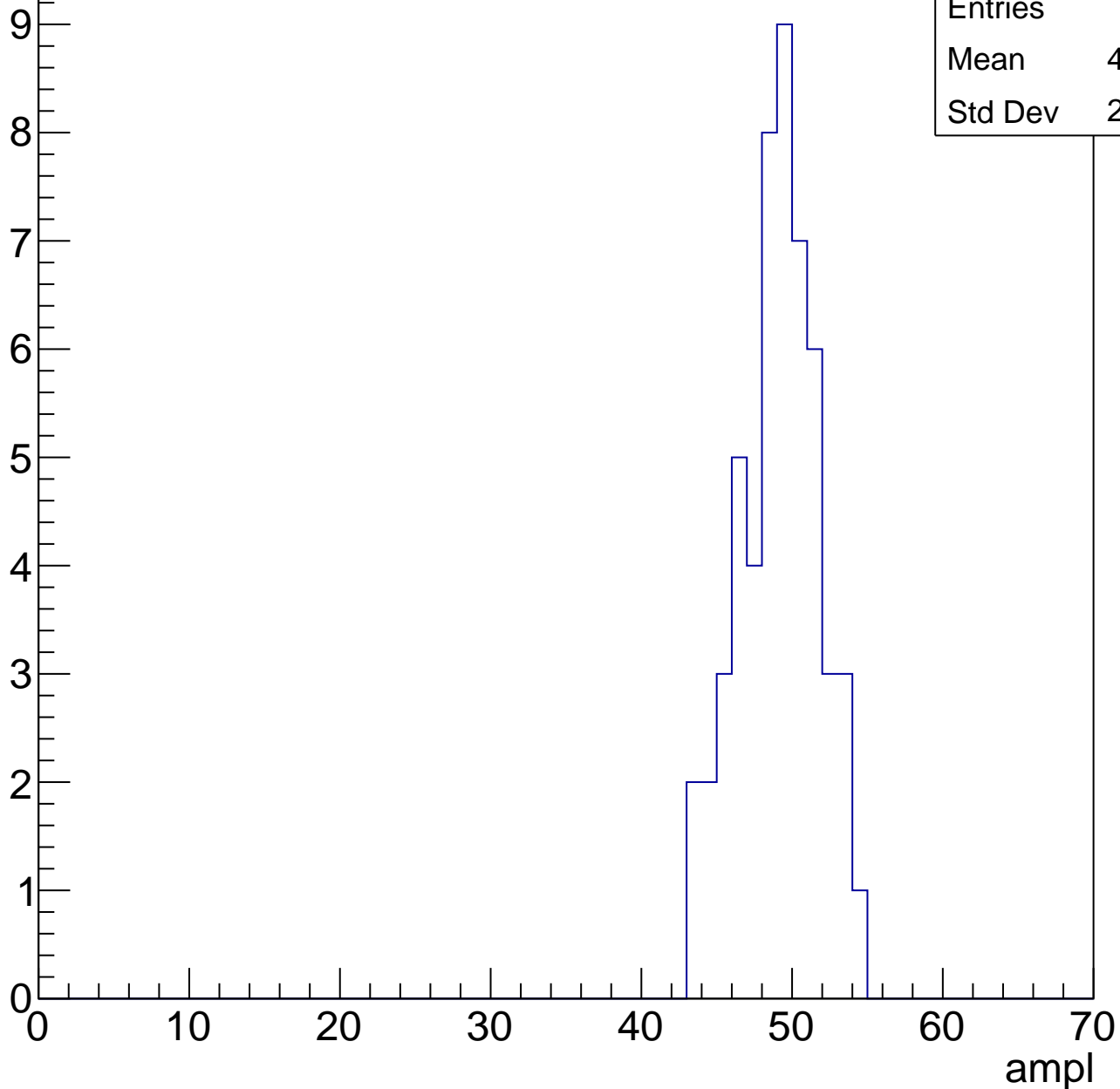
Gaus Width: 3.9556



B1L100S, U11-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

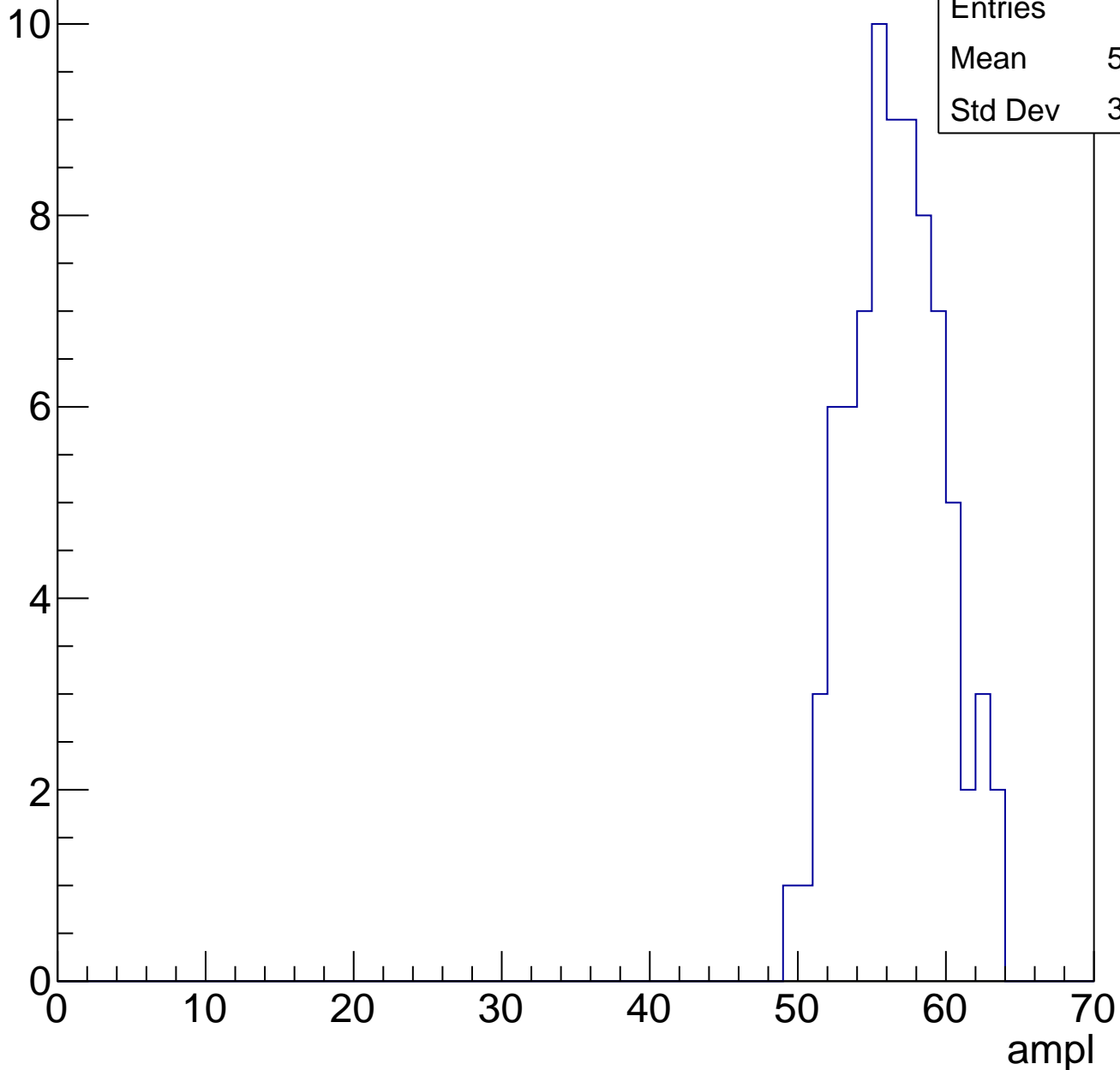


B1L100S, U11-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	56.18
Std Dev	3.153

Entry

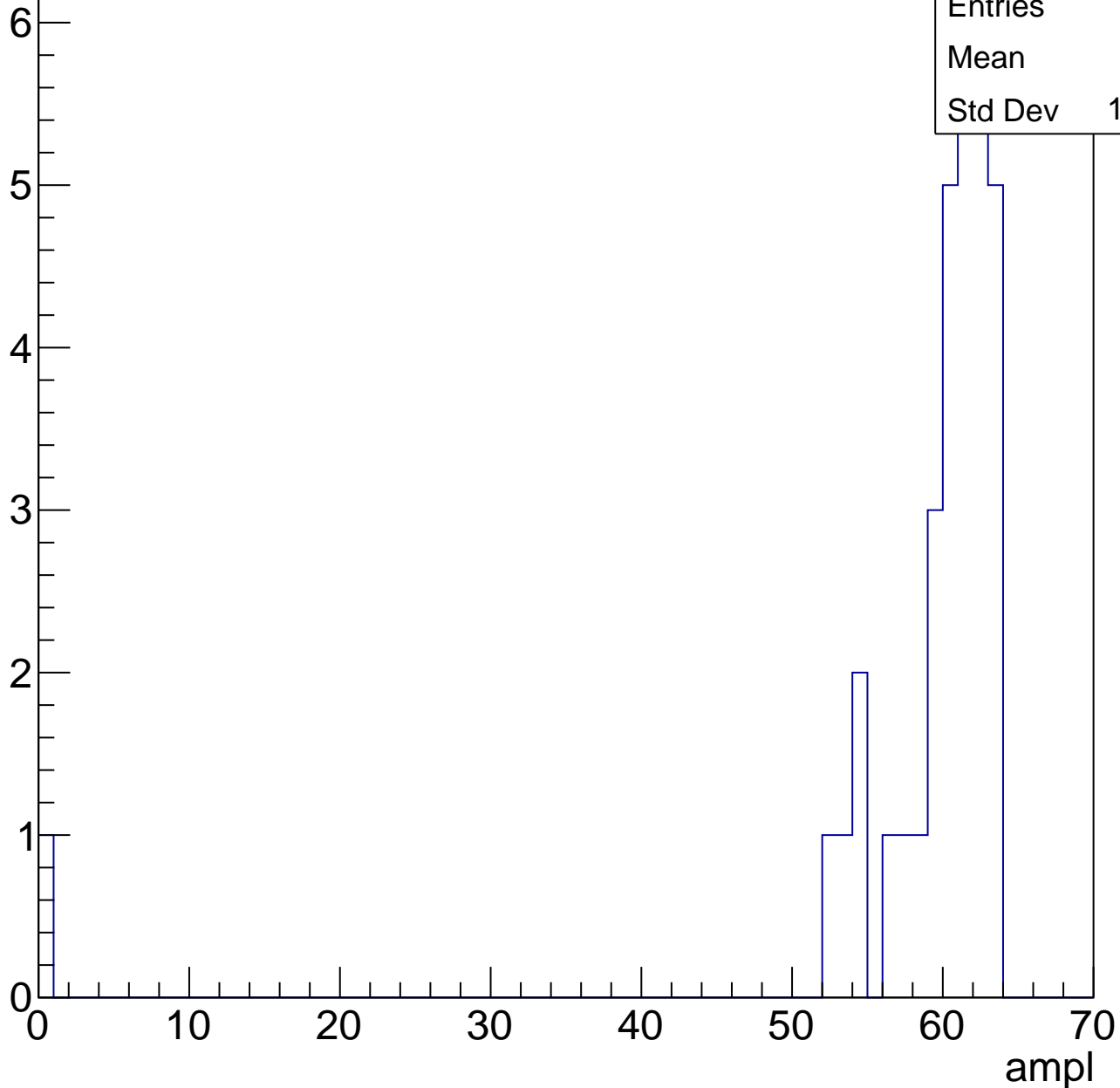


B1L100S, U11-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

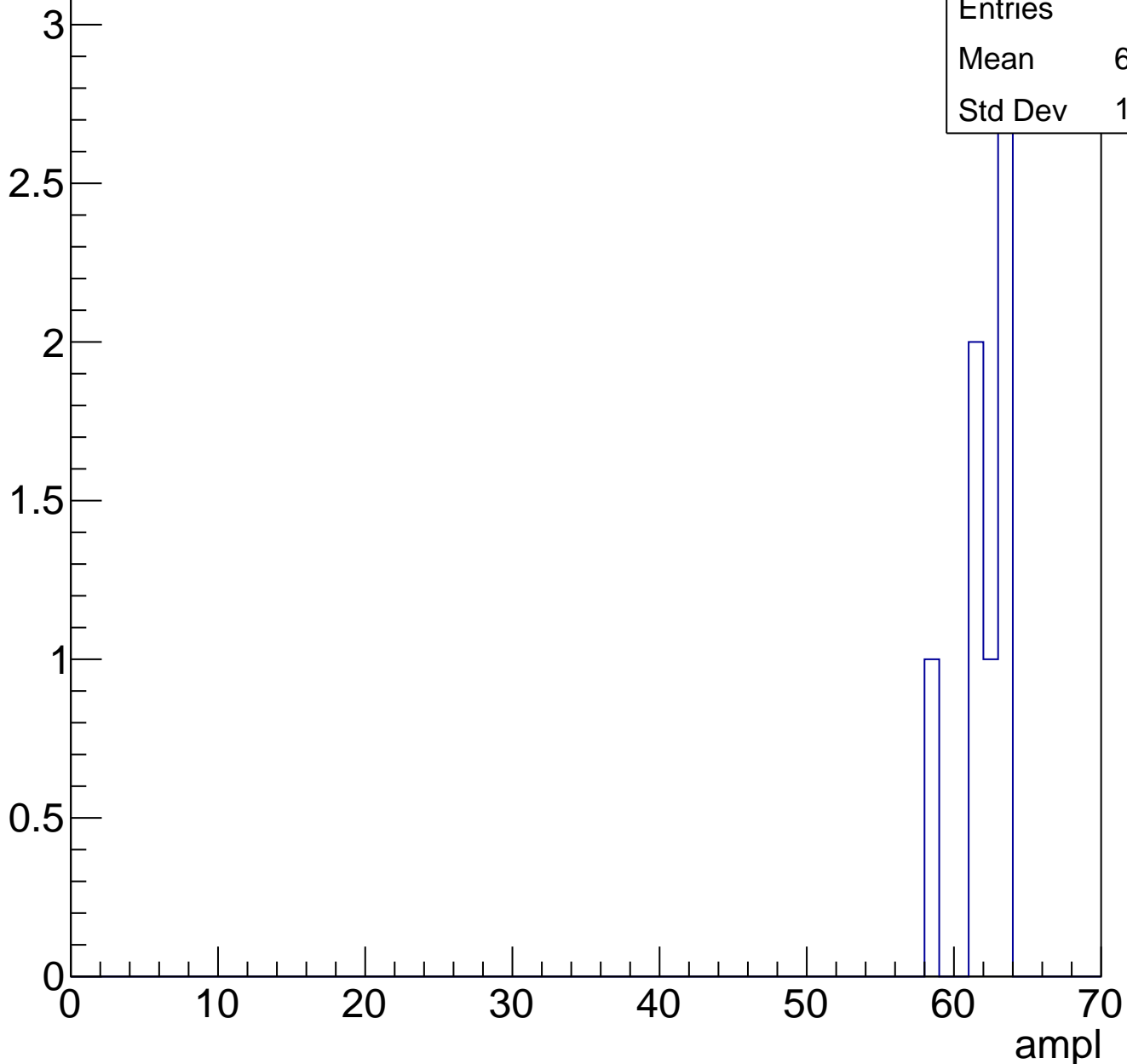
Entries	33
Mean	58
Std Dev	10.67



B1L100S, U11-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	7
Mean	61.57
Std Dev	1.678

B1L100S, U11-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch107, adc0

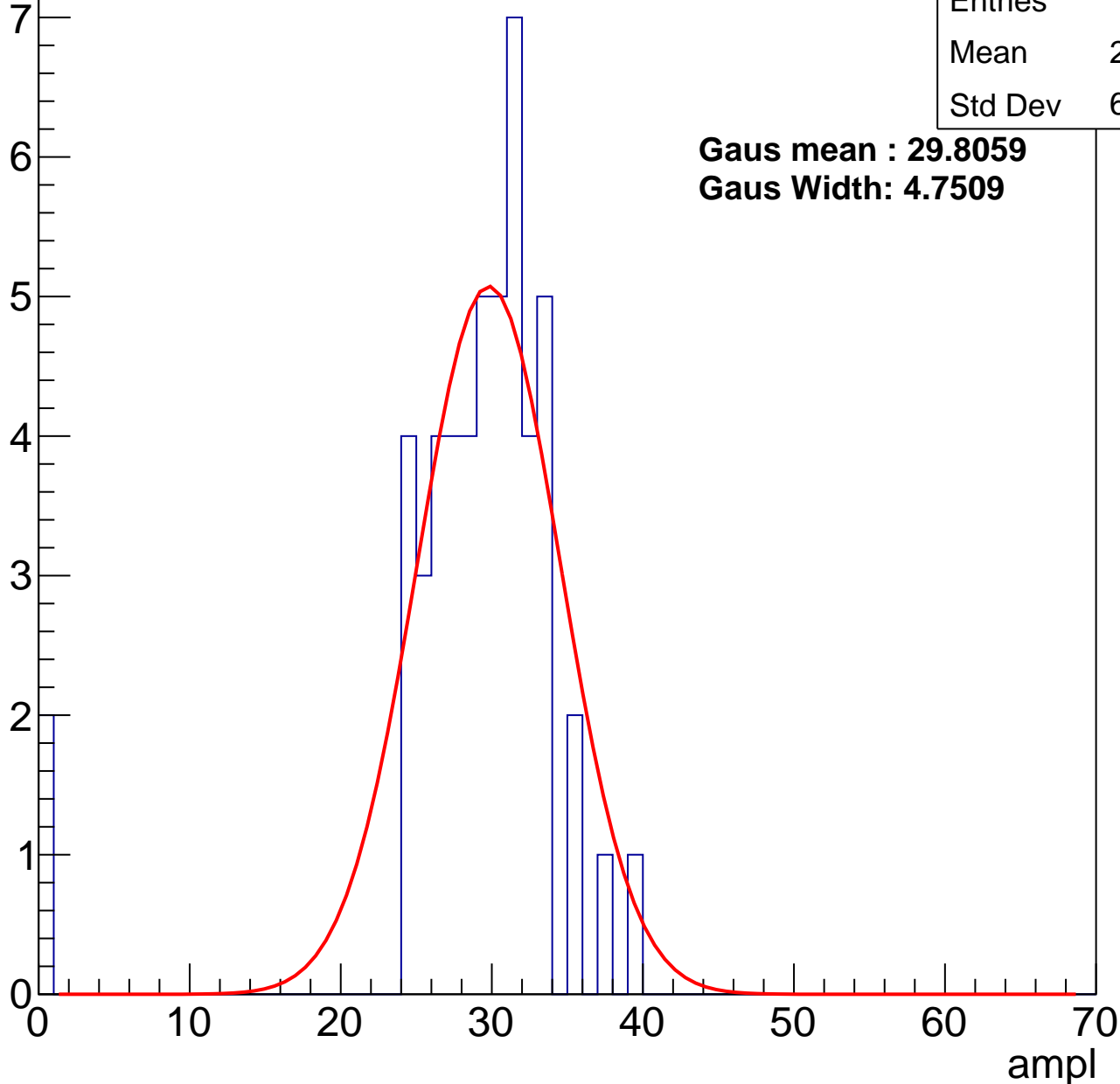
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	28.35
Std Dev	6.642

Gaus mean : 29.8059

Gaus Width: 4.7509



B1L100S, U11-ch107, adc1

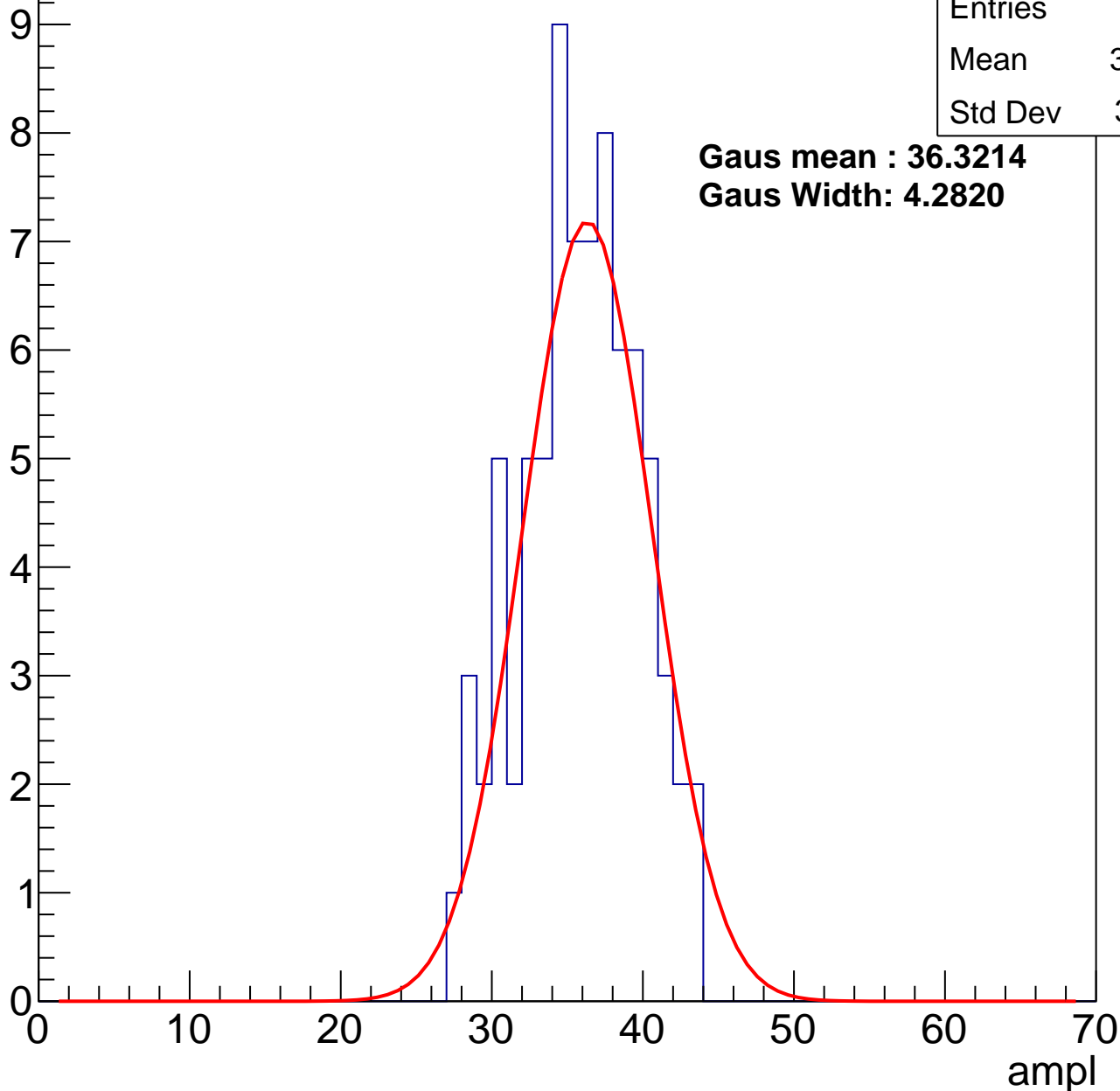
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	35.38
Std Dev	3.841

Gaus mean : 36.3214

Gaus Width: 4.2820



B1L100S, U11-ch107, adc2

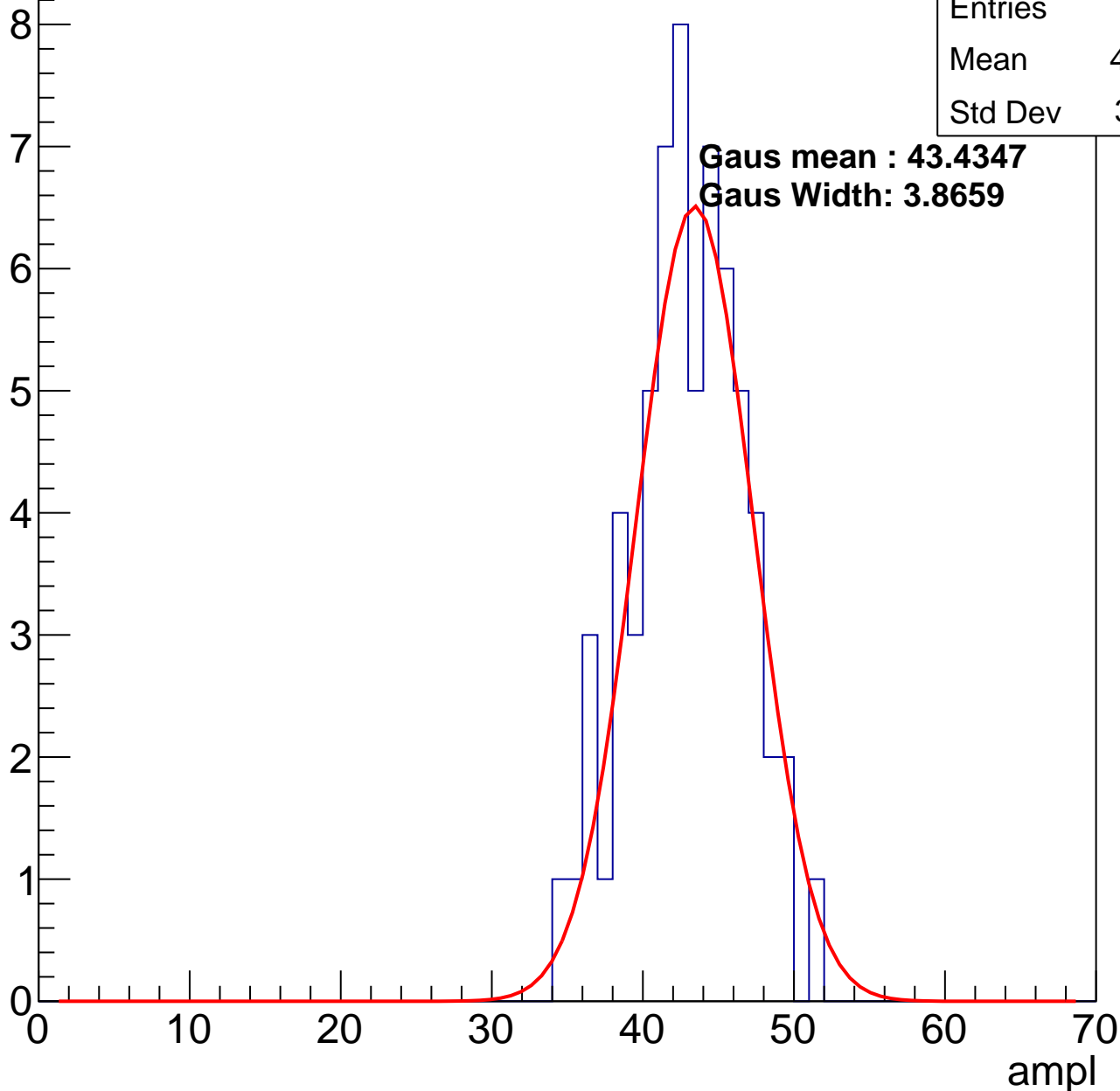
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.49
Std Dev	3.651

Gaus mean : 43.4347

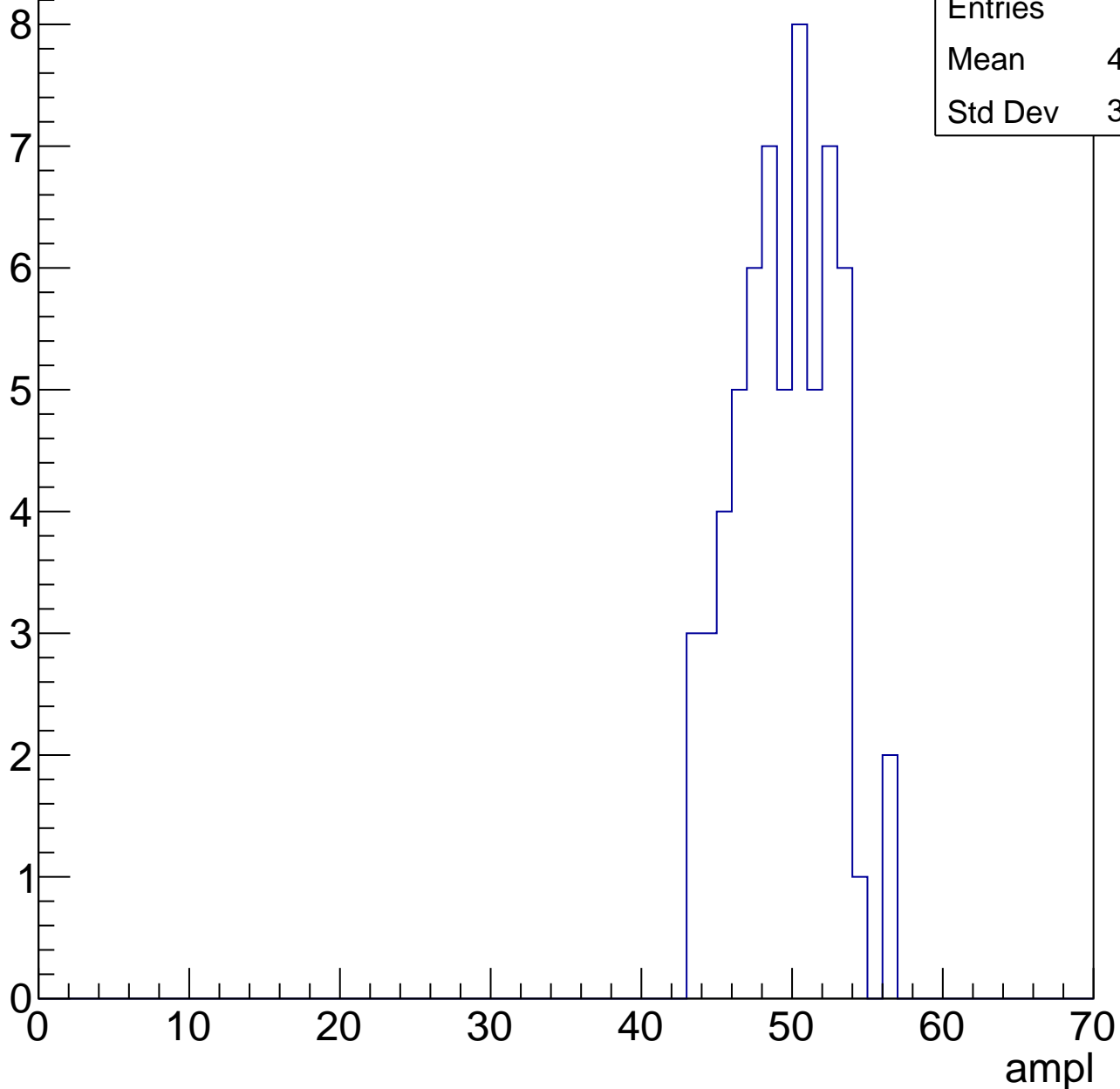
Gaus Width: 3.8659



B1L100S, U11-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

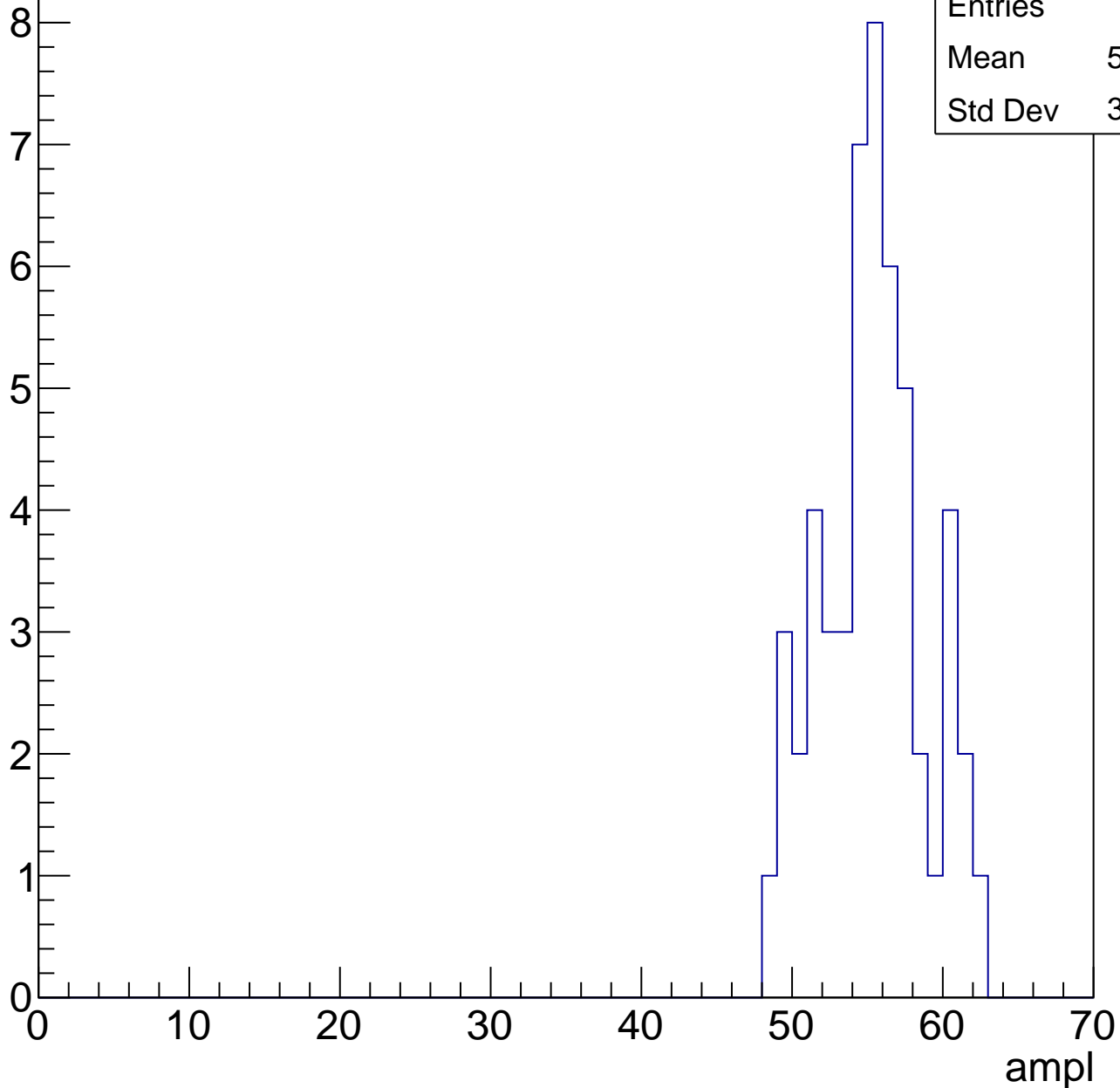
Entry



B1L100S, U11-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch107, adc5

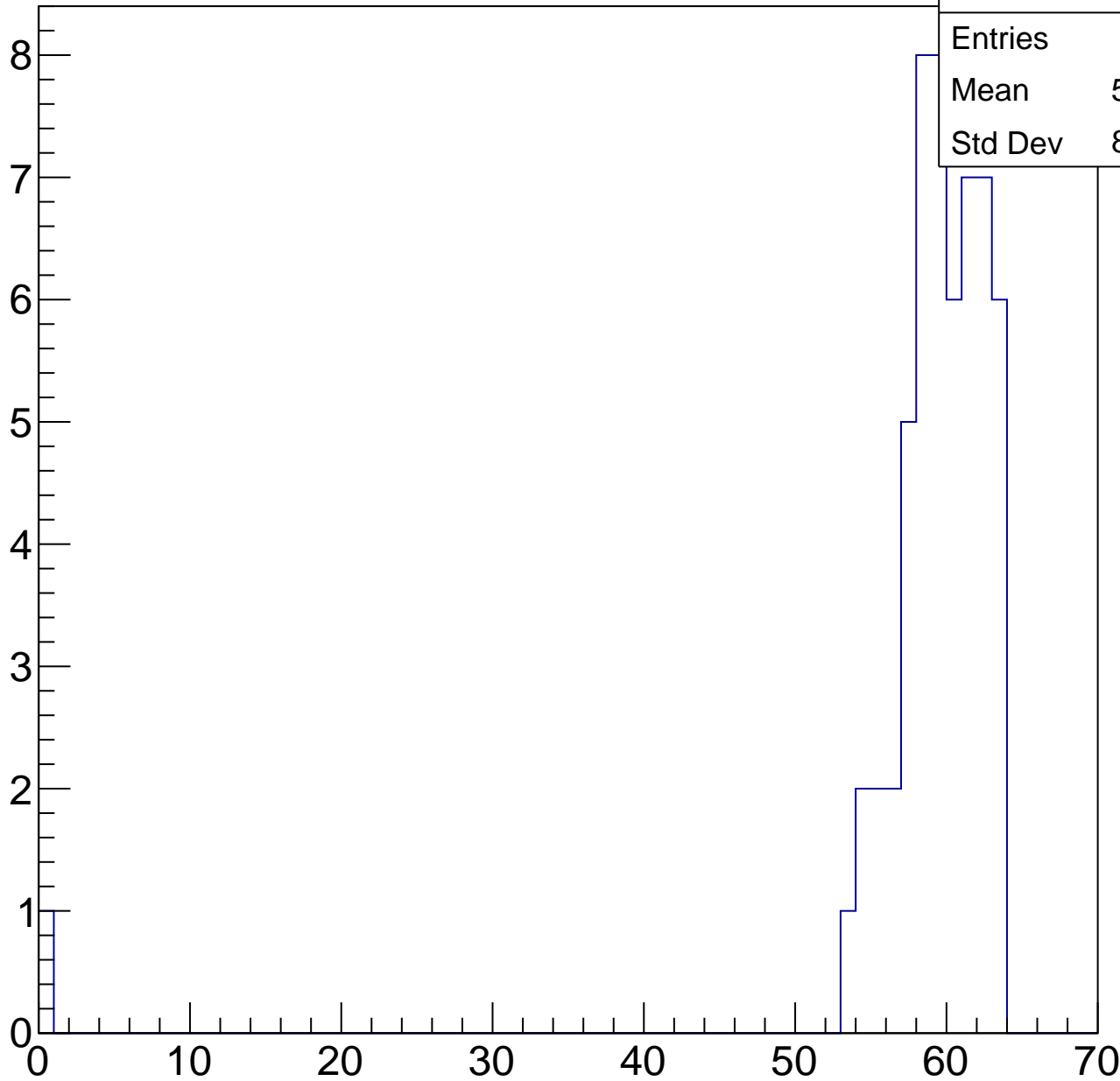
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	55
Mean	58.24
Std Dev	8.319

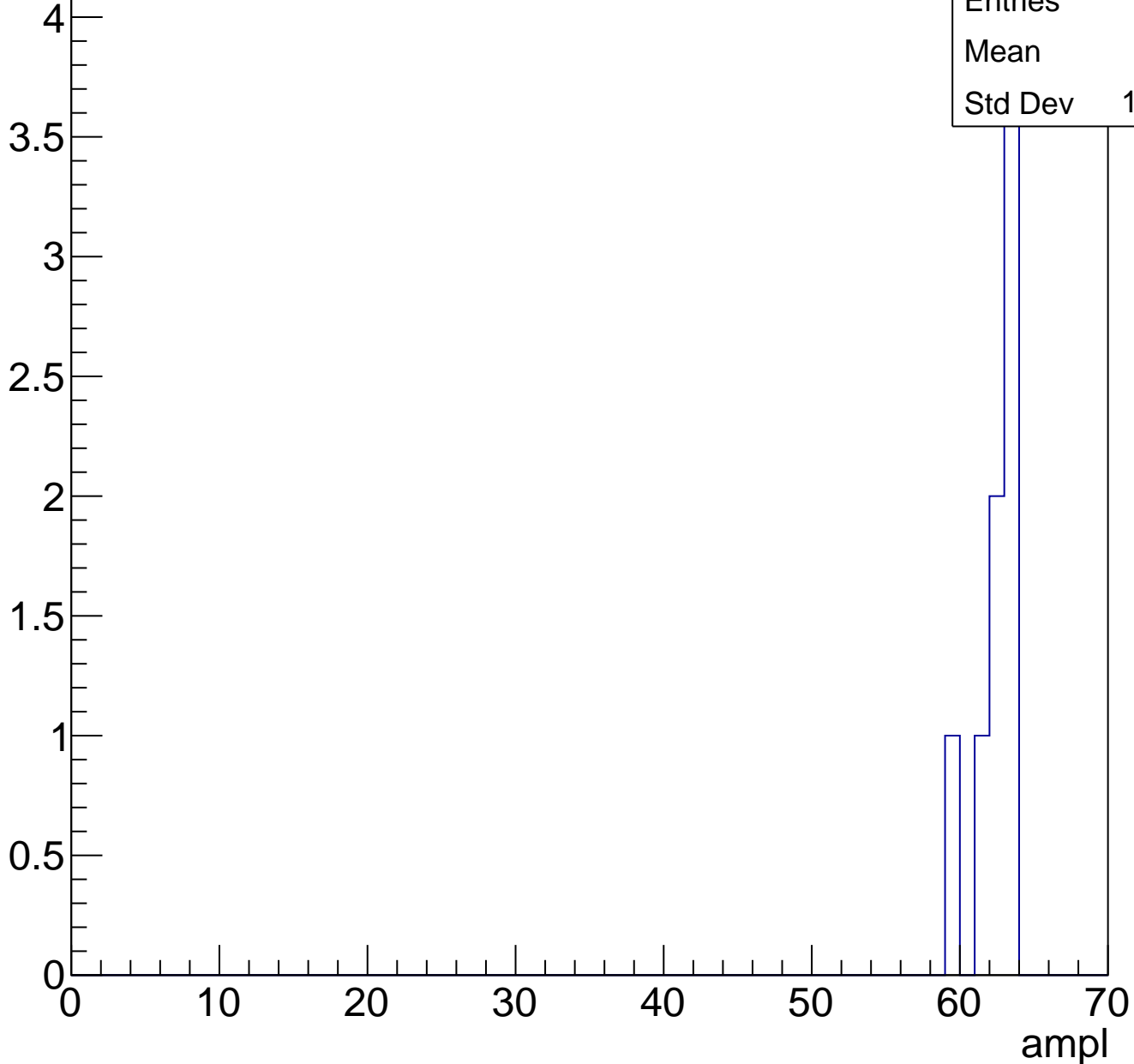
ampl



B1L100S, U11-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch108, adc0

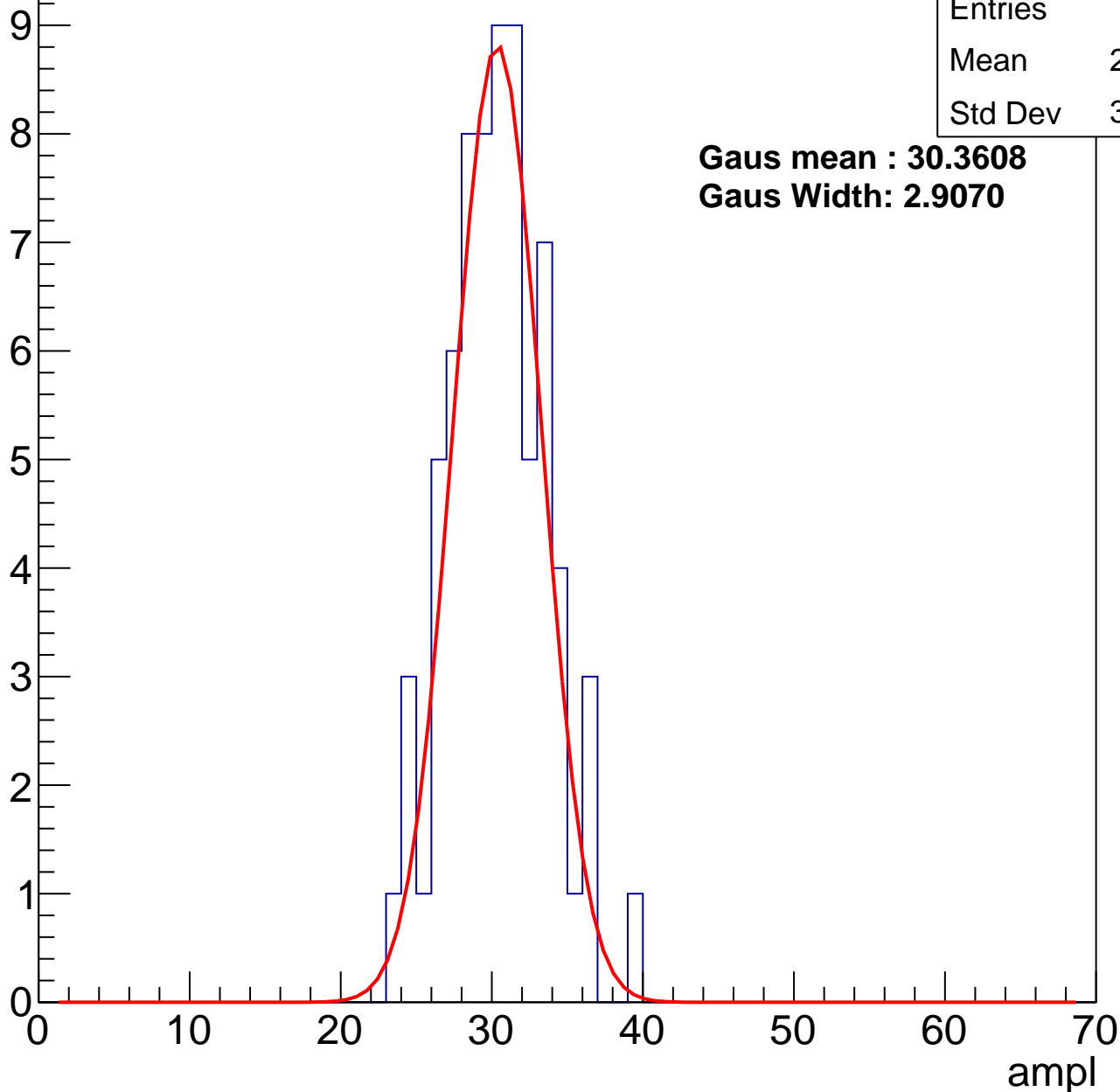
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.94
Std Dev	3.197

Gaus mean : 30.3608

Gaus Width: 2.9070



B1L100S, U11-ch108, adc1

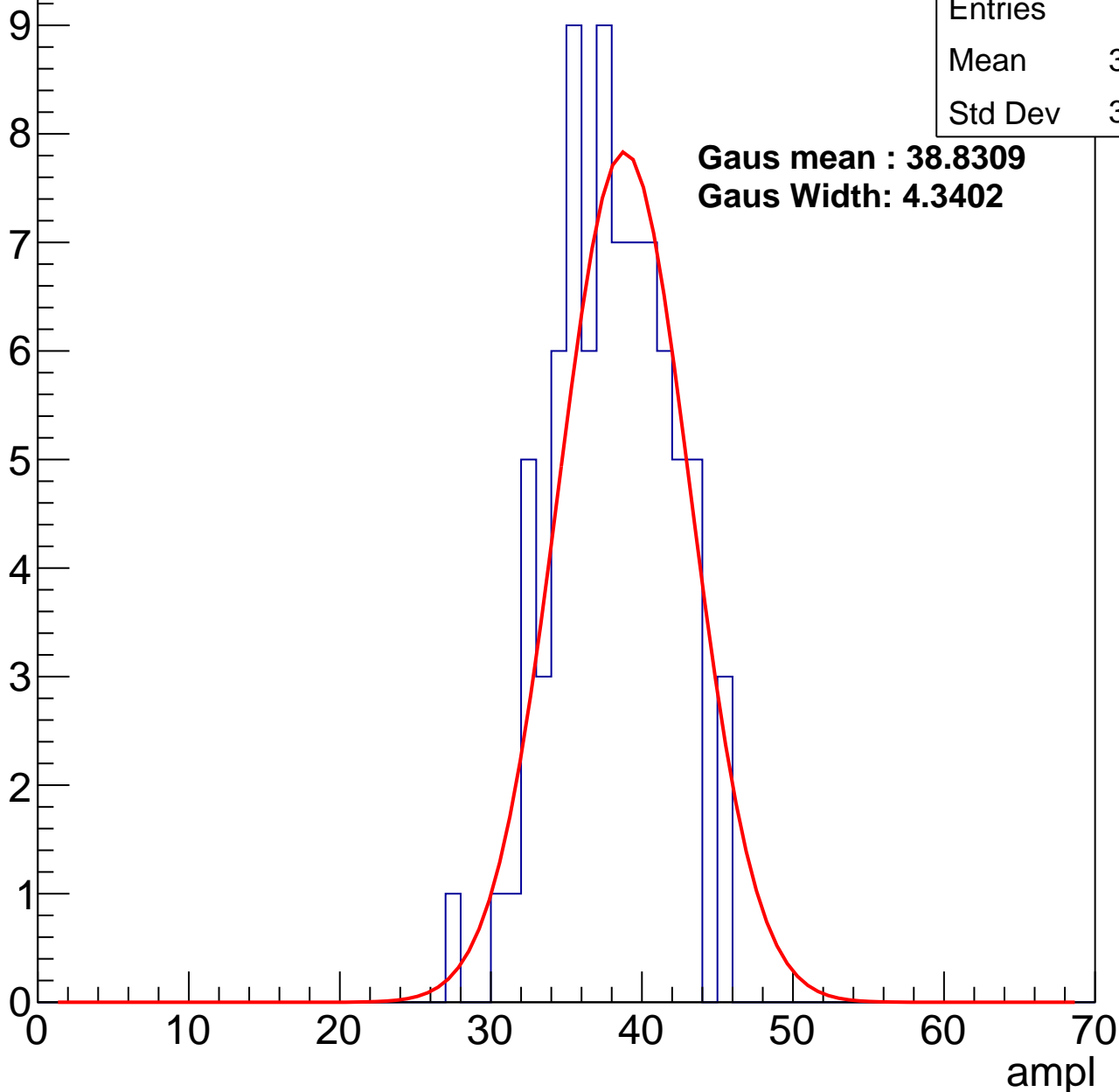
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	37.53
Std Dev	3.722

Gaus mean : 38.8309

Gaus Width: 4.3402



B1L100S, U11-ch108, adc2

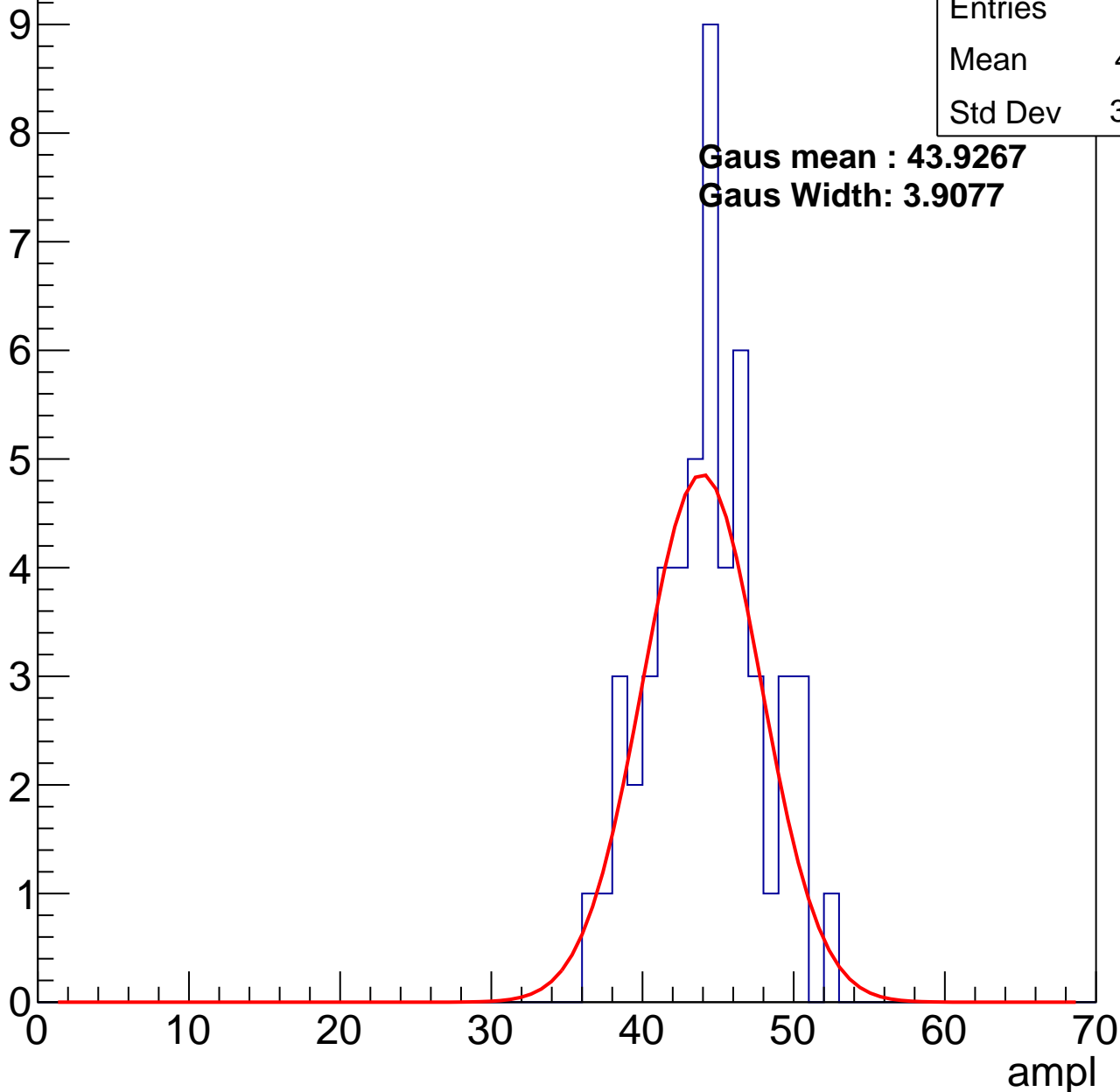
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	43.81
Std Dev	3.614

Gaus mean : 43.9267

Gaus Width: 3.9077

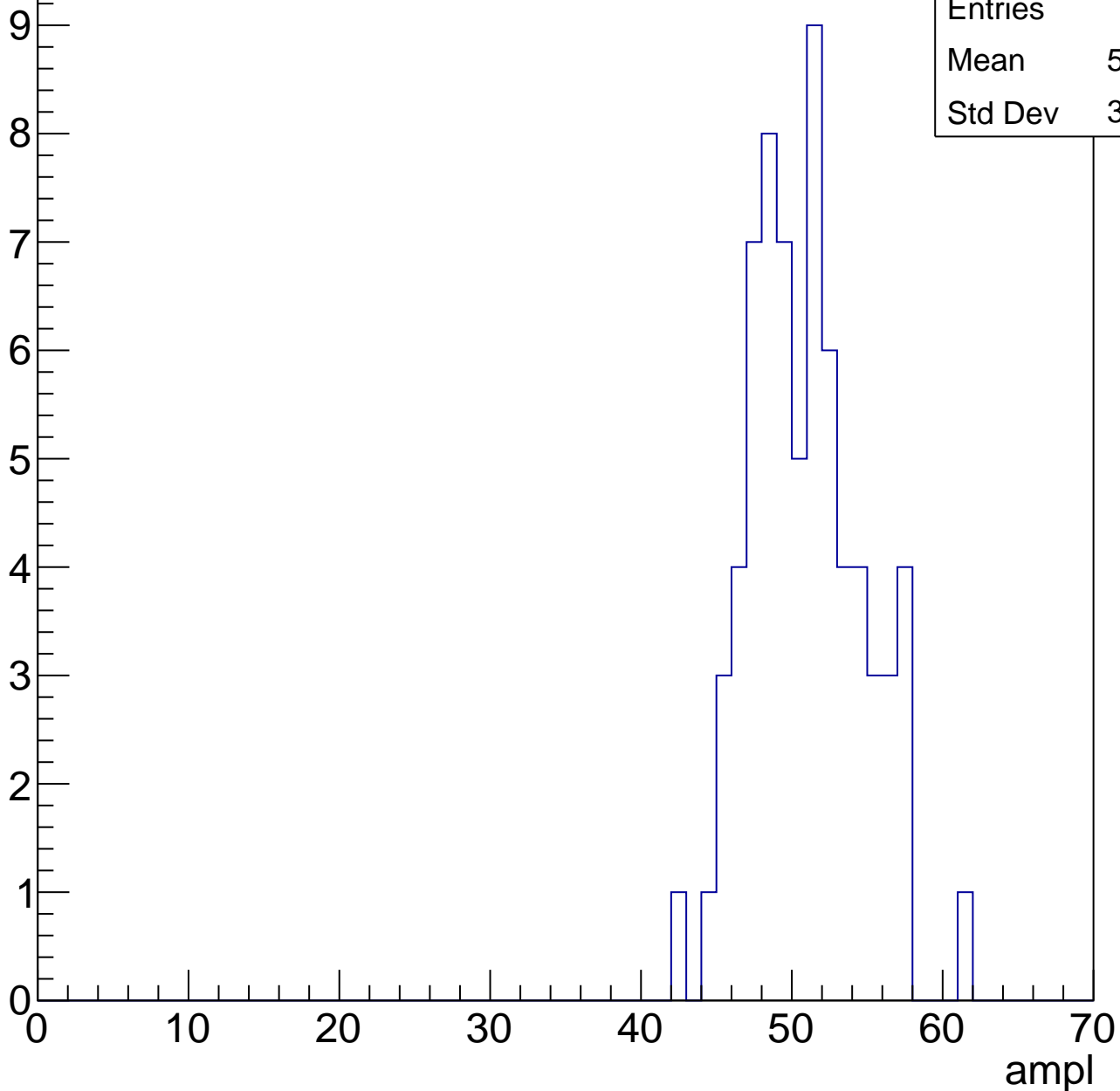


B1L100S, U11-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	50.46
Std Dev	3.702

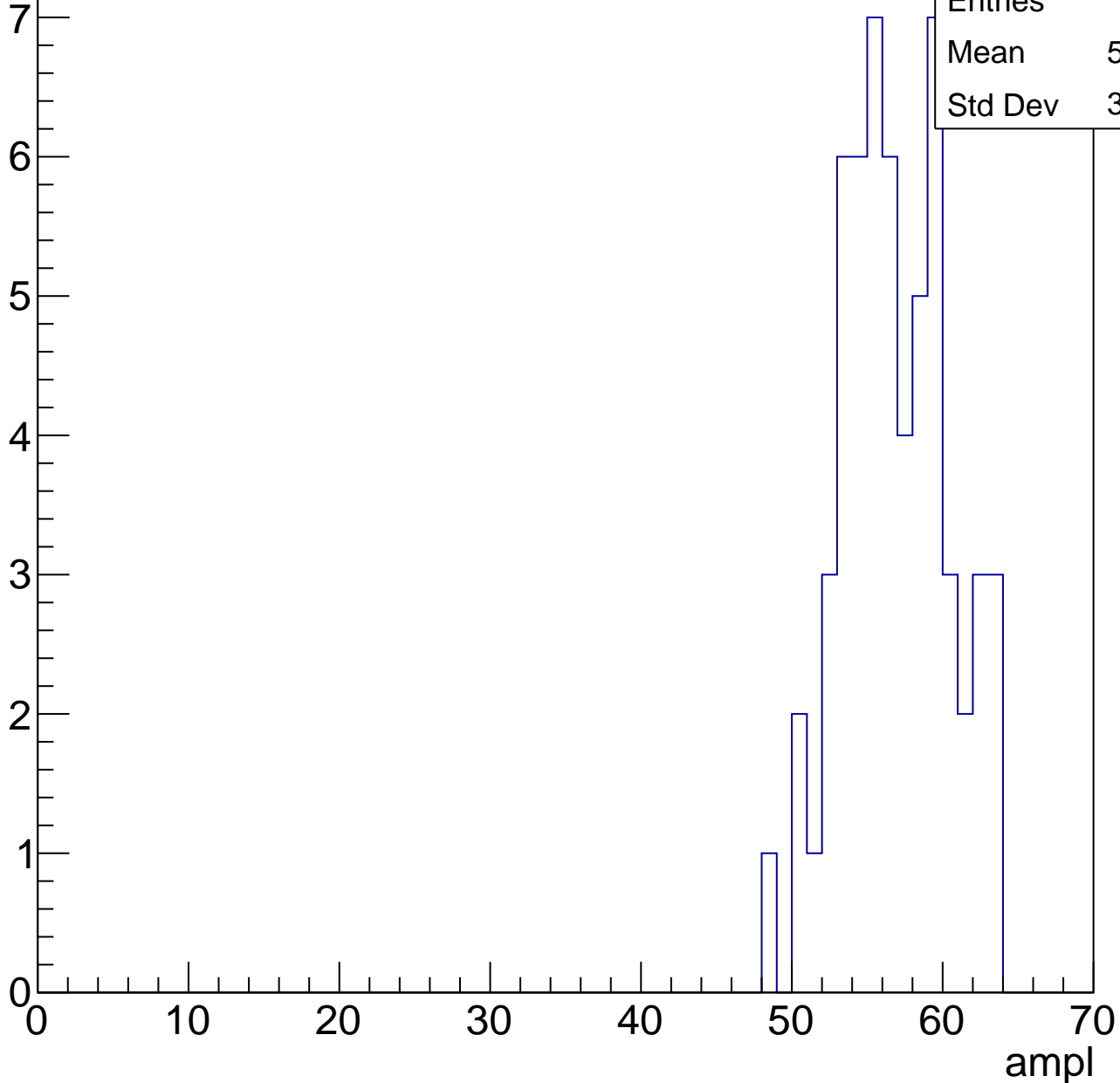


B1L100S, U11-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.37
Std Dev	3.507

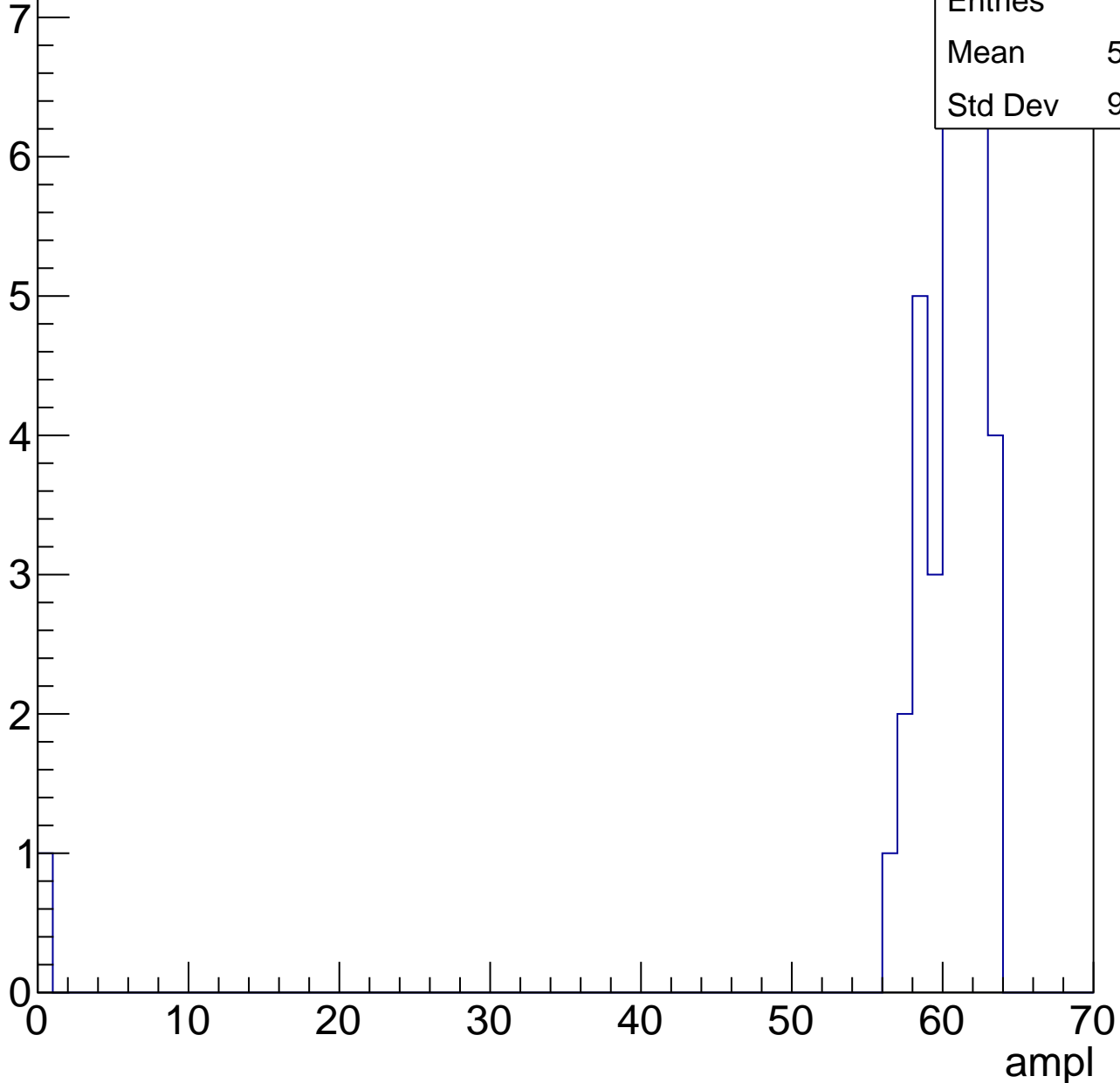


B1L100S, U11-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.65
Std Dev	9.946



B1L100S, U11-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

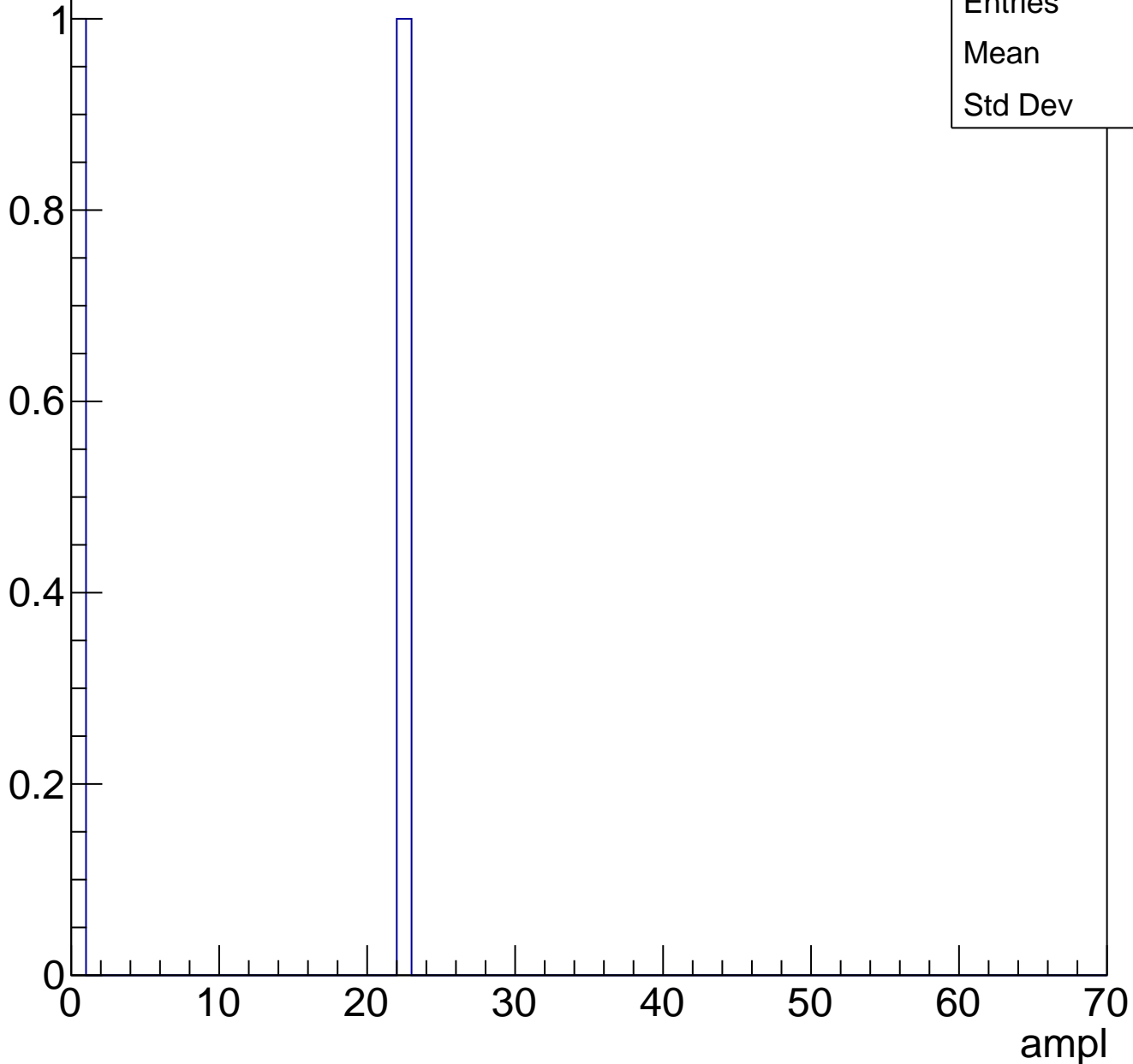
Entry



B1L100S, U11-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch109, adc0

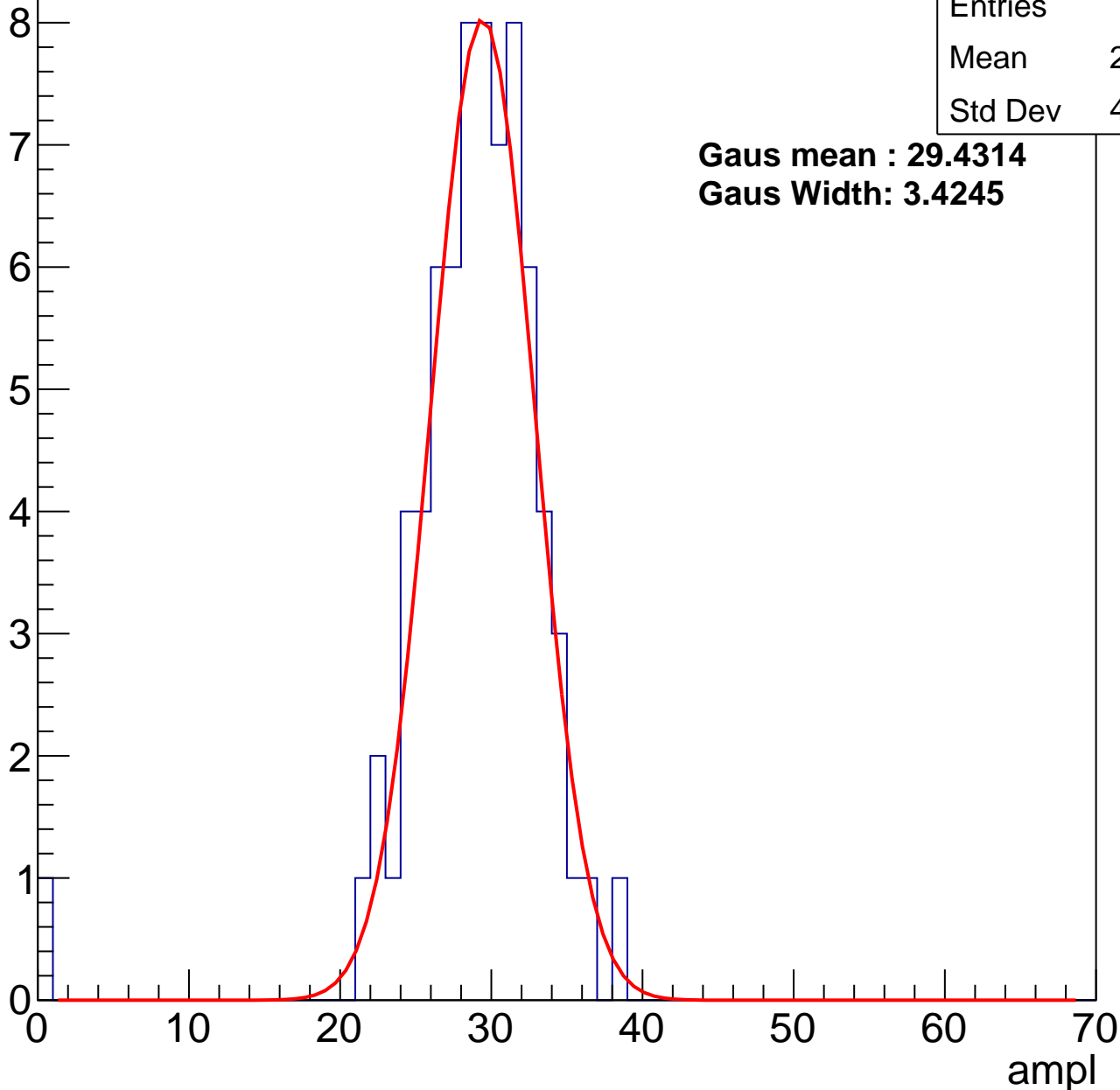
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.49
Std Dev	4.813

Gaus mean : 29.4314

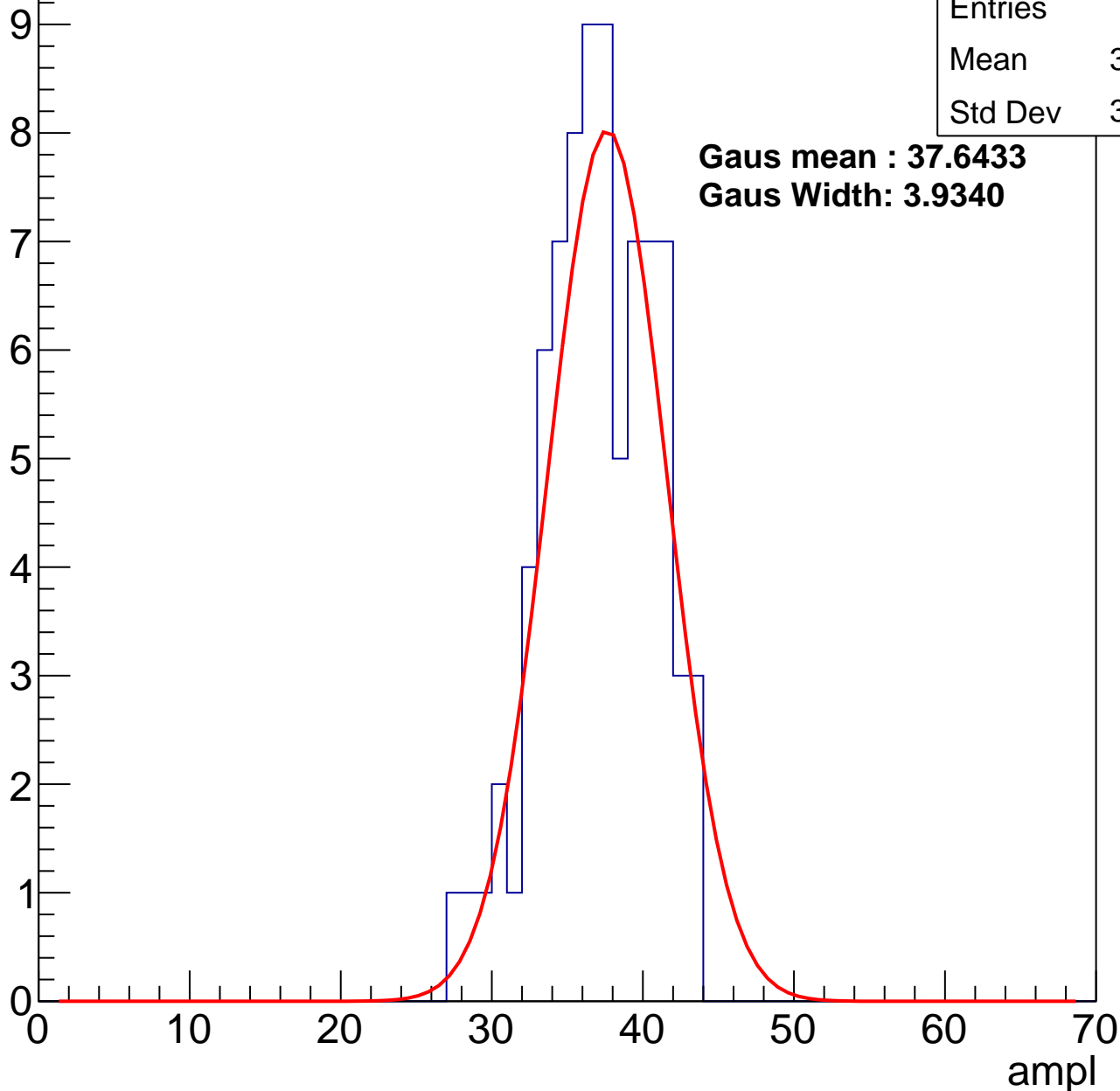
Gaus Width: 3.4245



B1L100S, U11-ch109, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch109, adc2

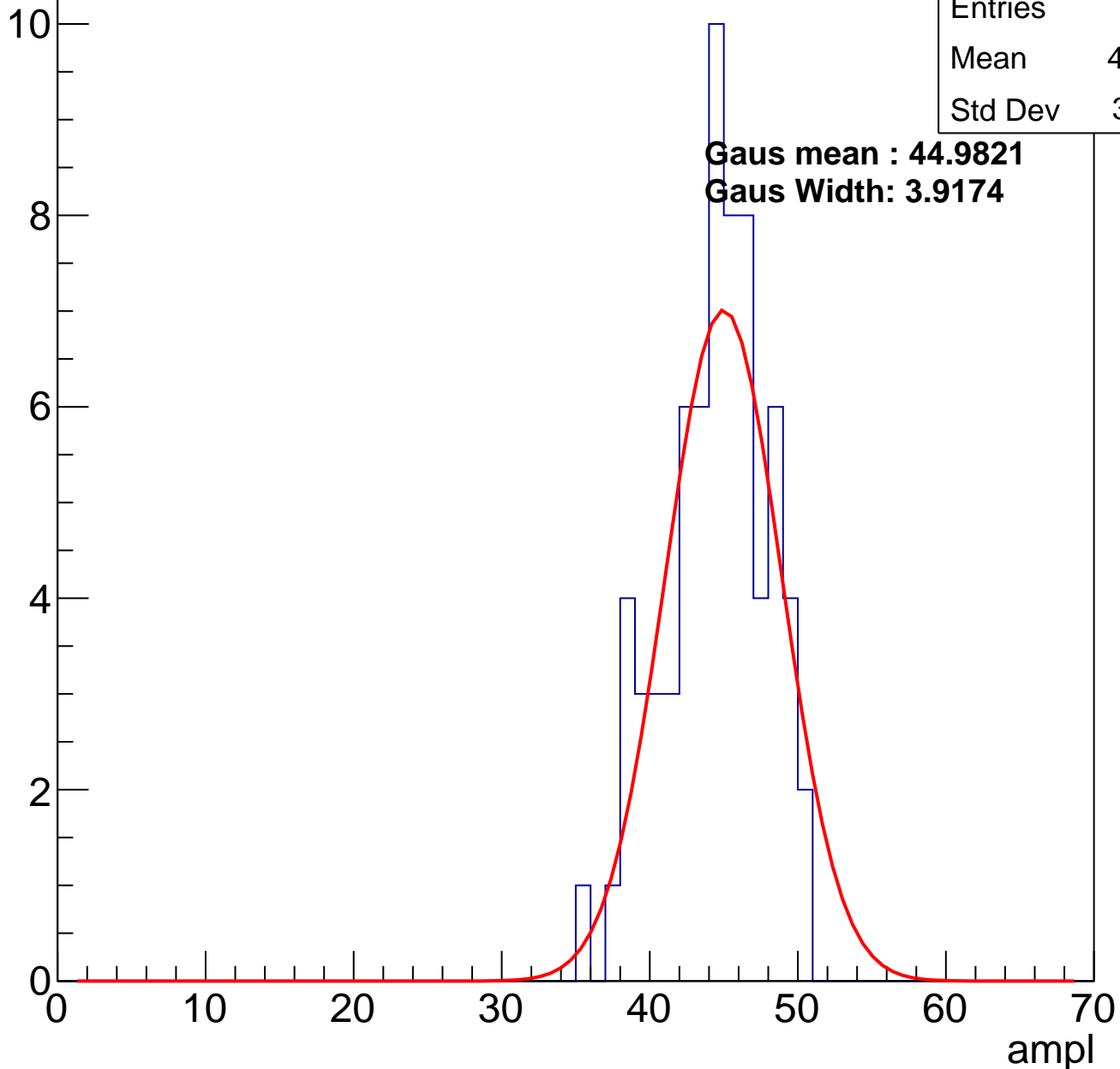
calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	43.97
Std Dev	3.401

Gaus mean : 44.9821

Gaus Width: 3.9174

Entry

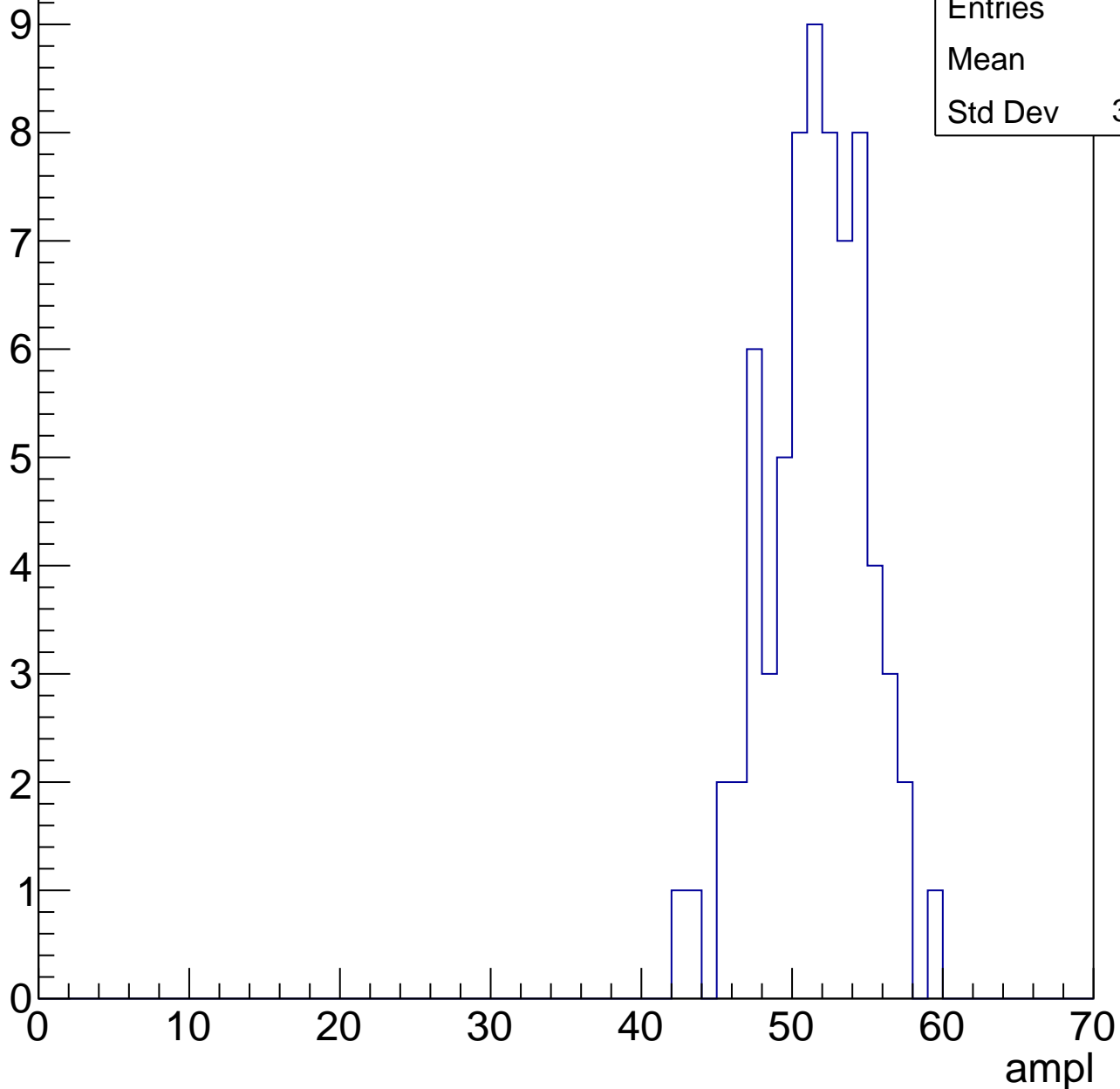


B1L100S, U11-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	51.1
Std Dev	3.381

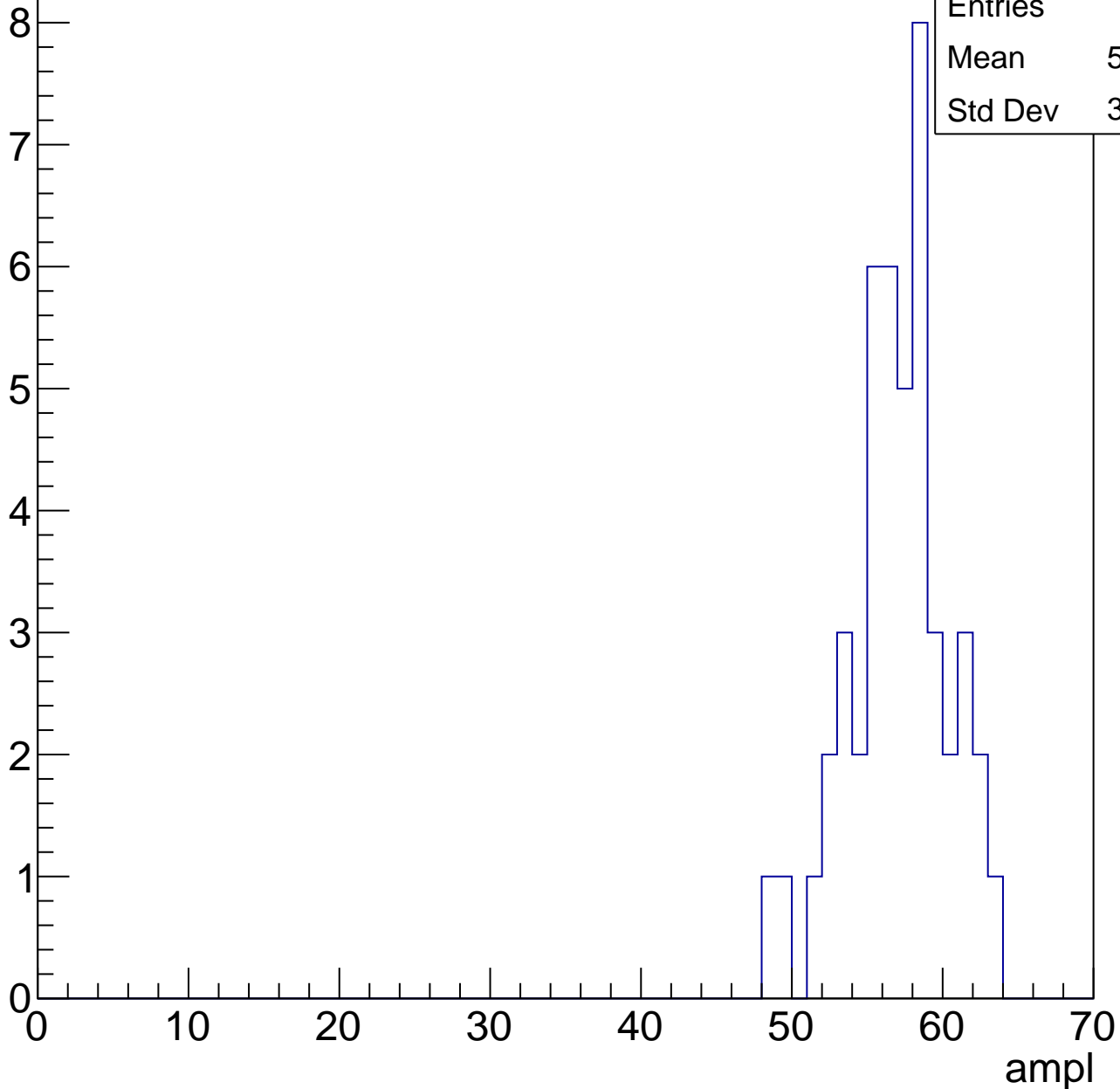


B1L100S, U11-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	56.54
Std Dev	3.262



B1L100S, U11-ch109, adc5

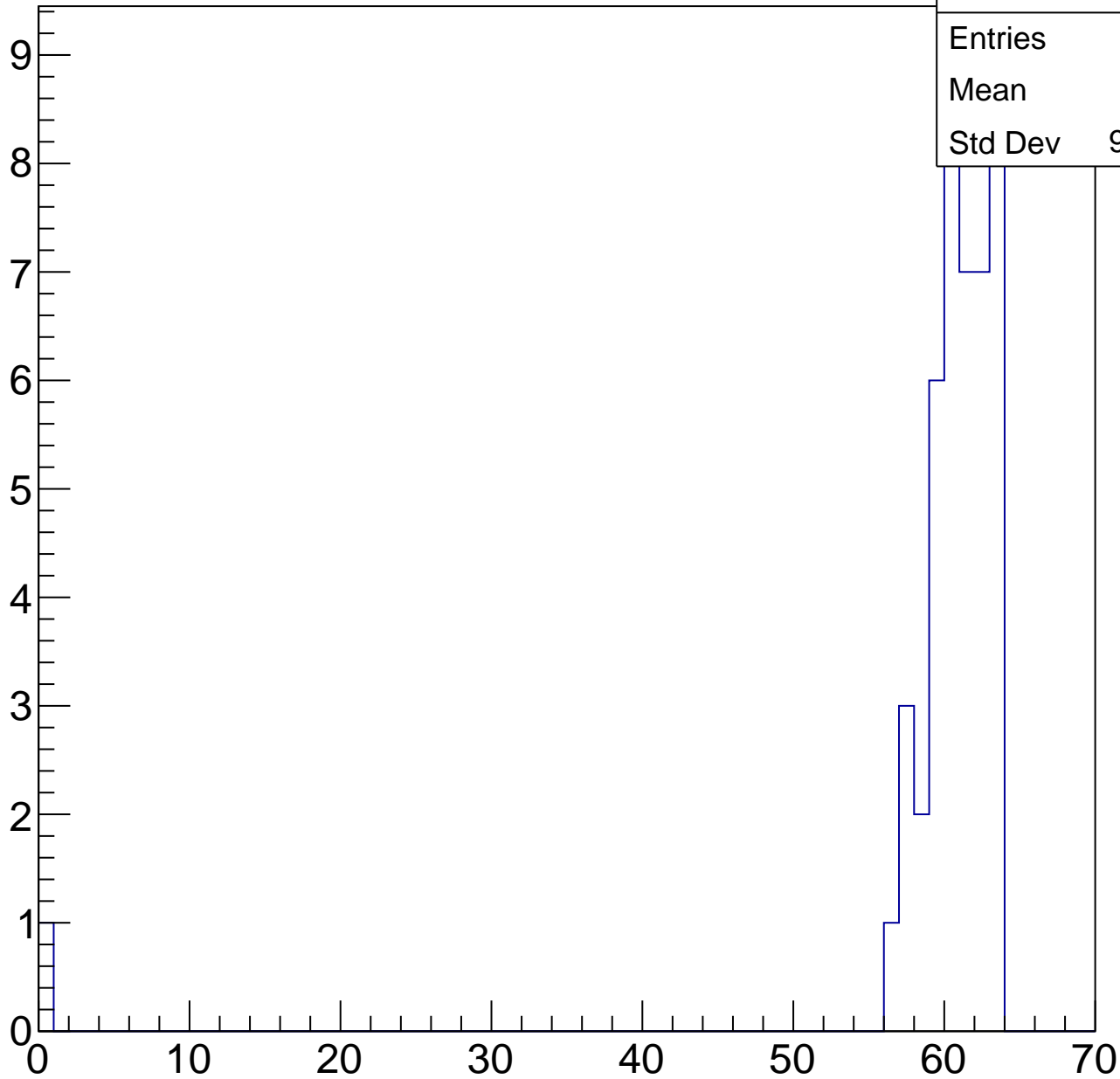
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	59.2
Std Dev	9.226

ampl



B1L100S, U11-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U11-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch110, adc0

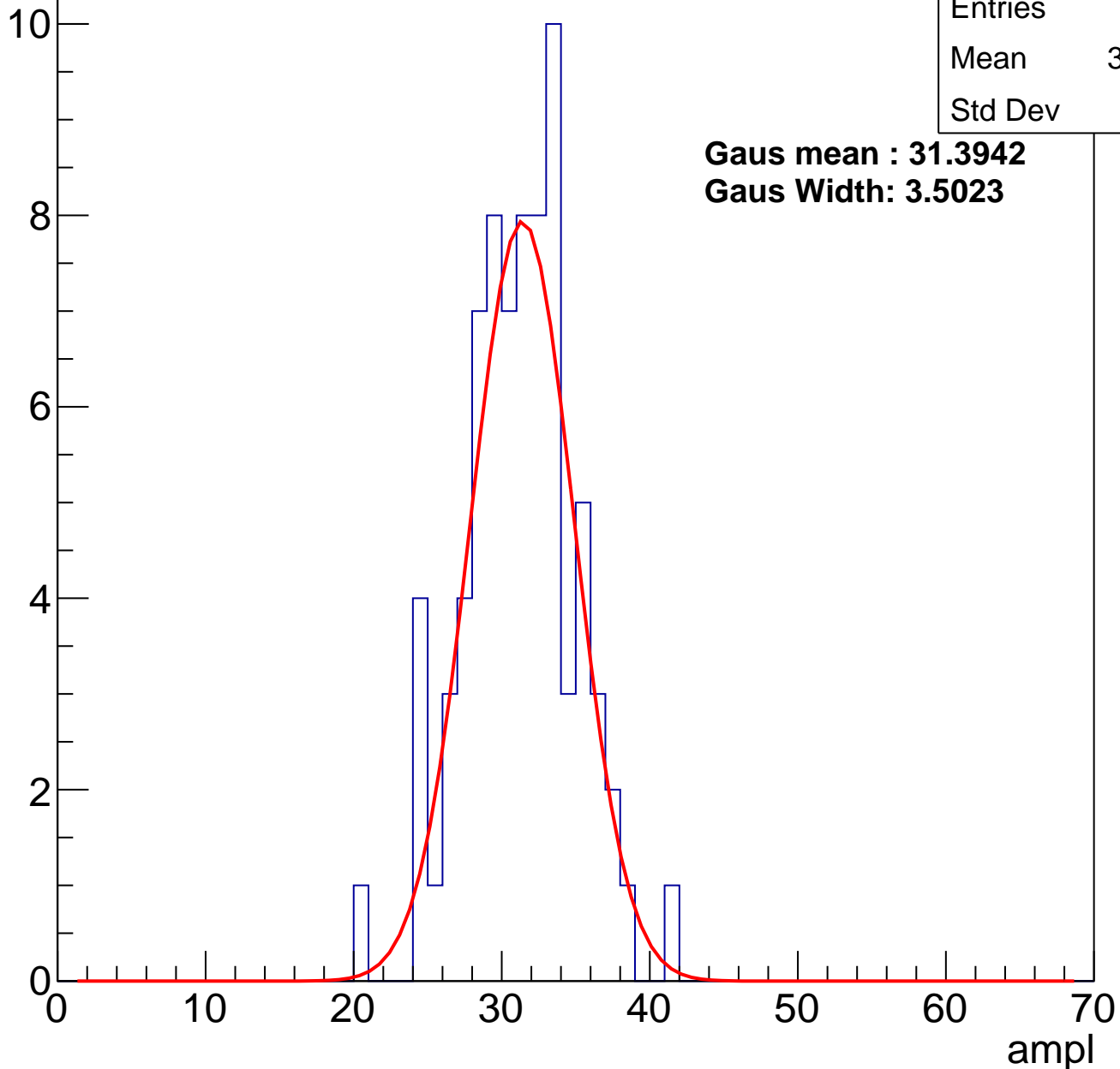
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	30.75
Std Dev	3.7

Gaus mean : 31.3942

Gaus Width: 3.5023

Entry



B1L100S, U11-ch110, adc1

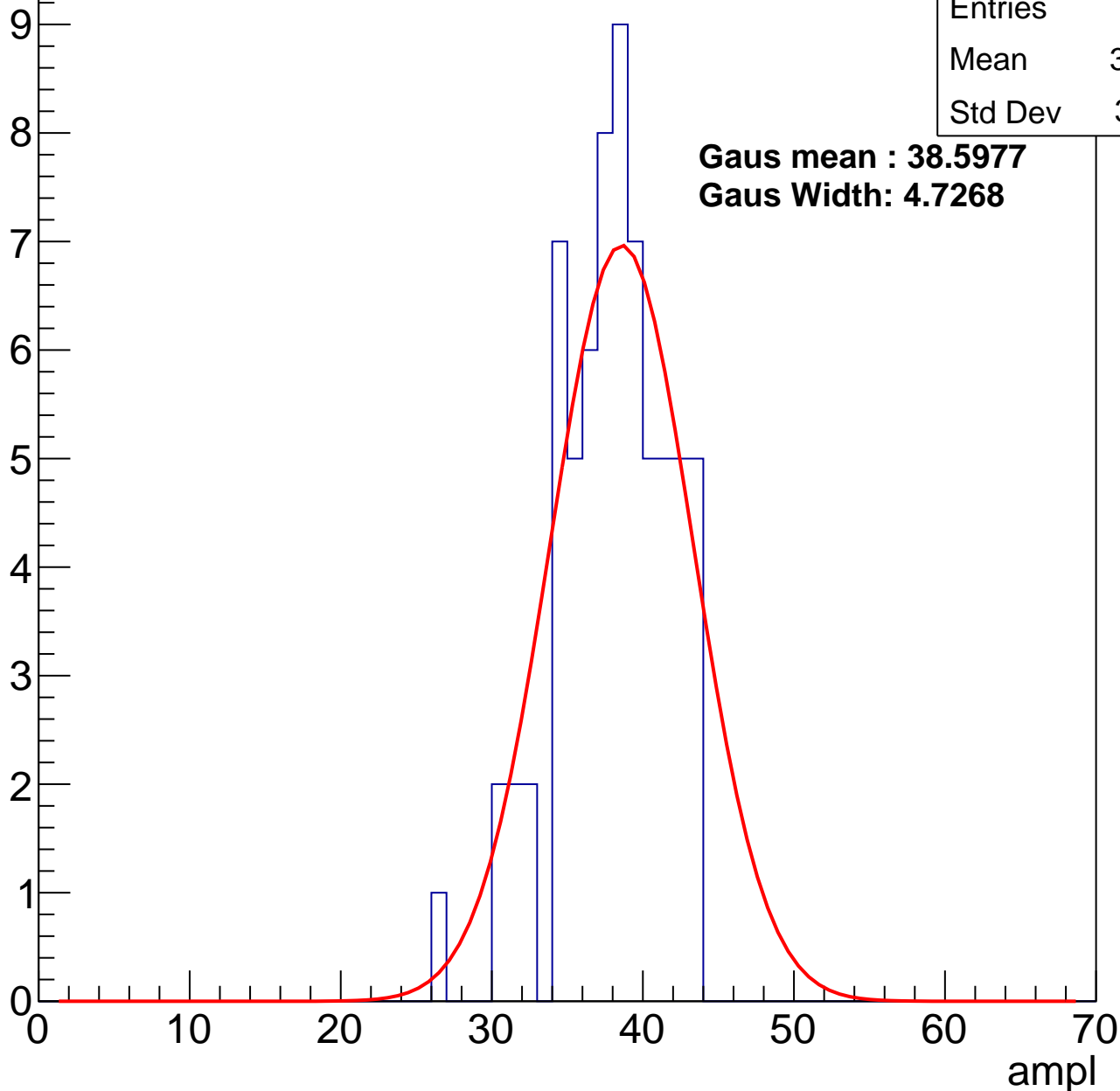
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	37.42
Std Dev	3.581

Gaus mean : 38.5977

Gaus Width: 4.7268



B1L100S, U11-ch110, adc2

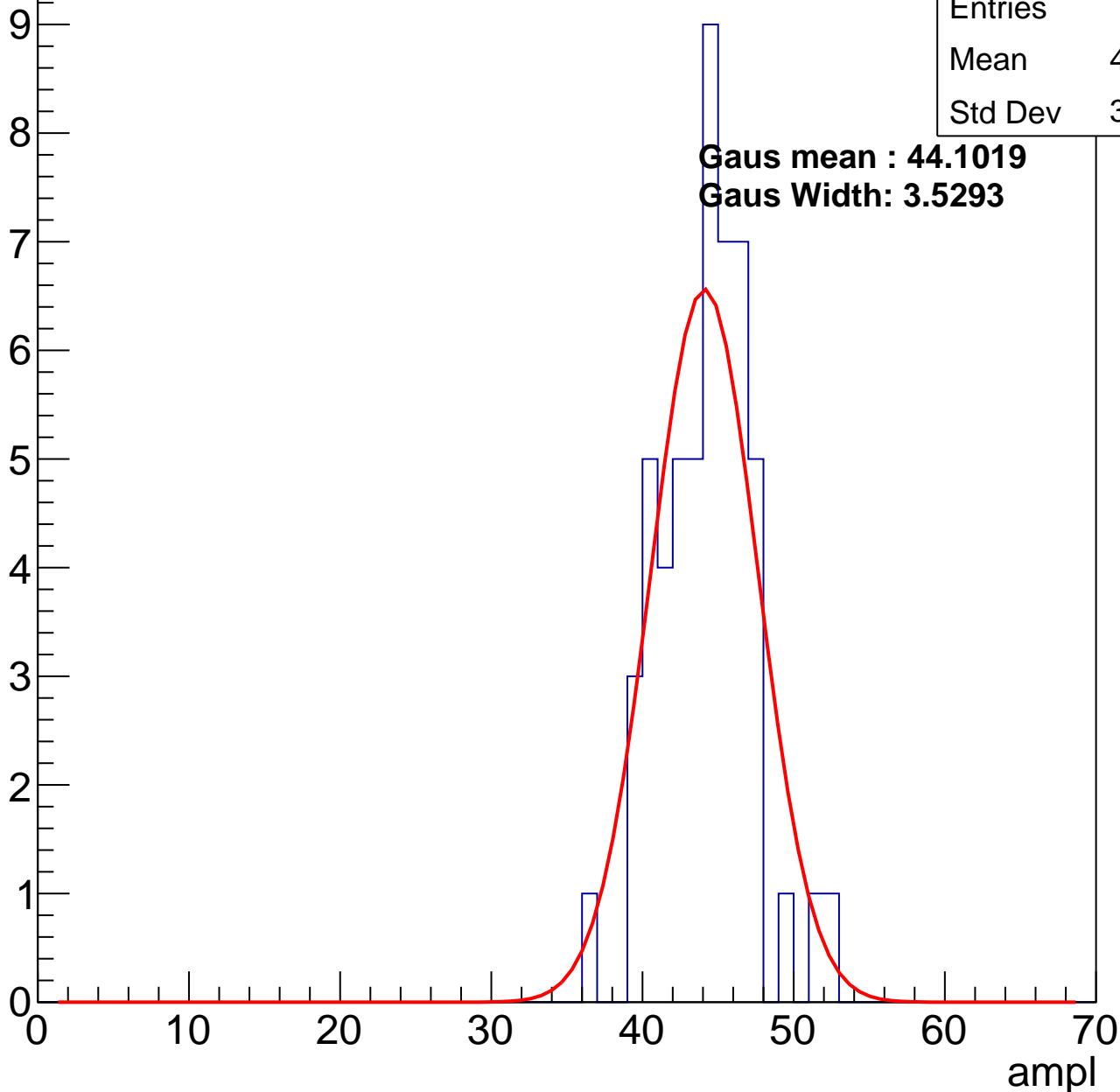
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.74
Std Dev	3.026

Gaus mean : 44.1019

Gaus Width: 3.5293



B1L100S, U11-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	50.34
Std Dev	3.074

Entry

10

8

6

4

2

0

0

10

20

30

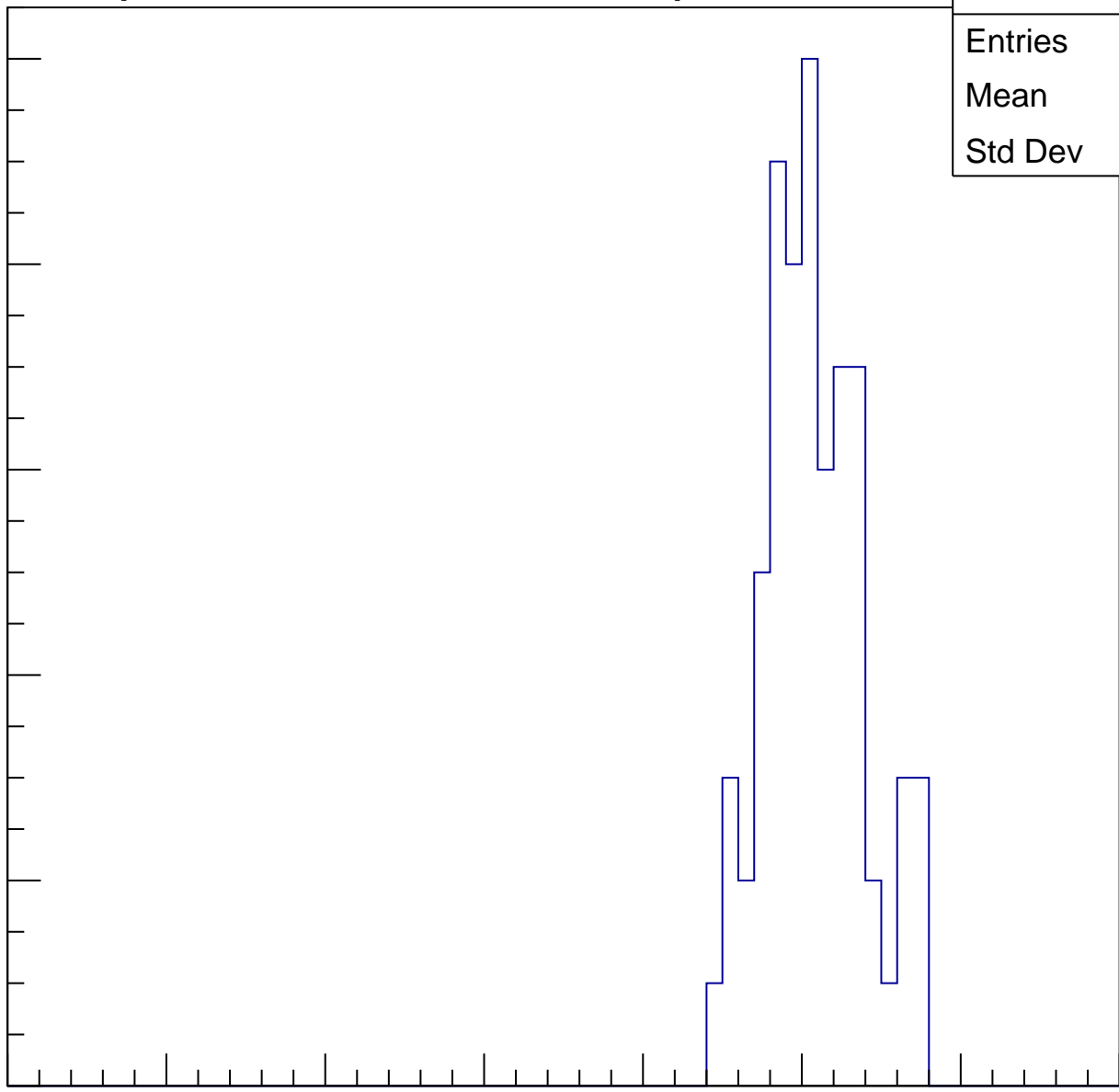
40

50

60

70

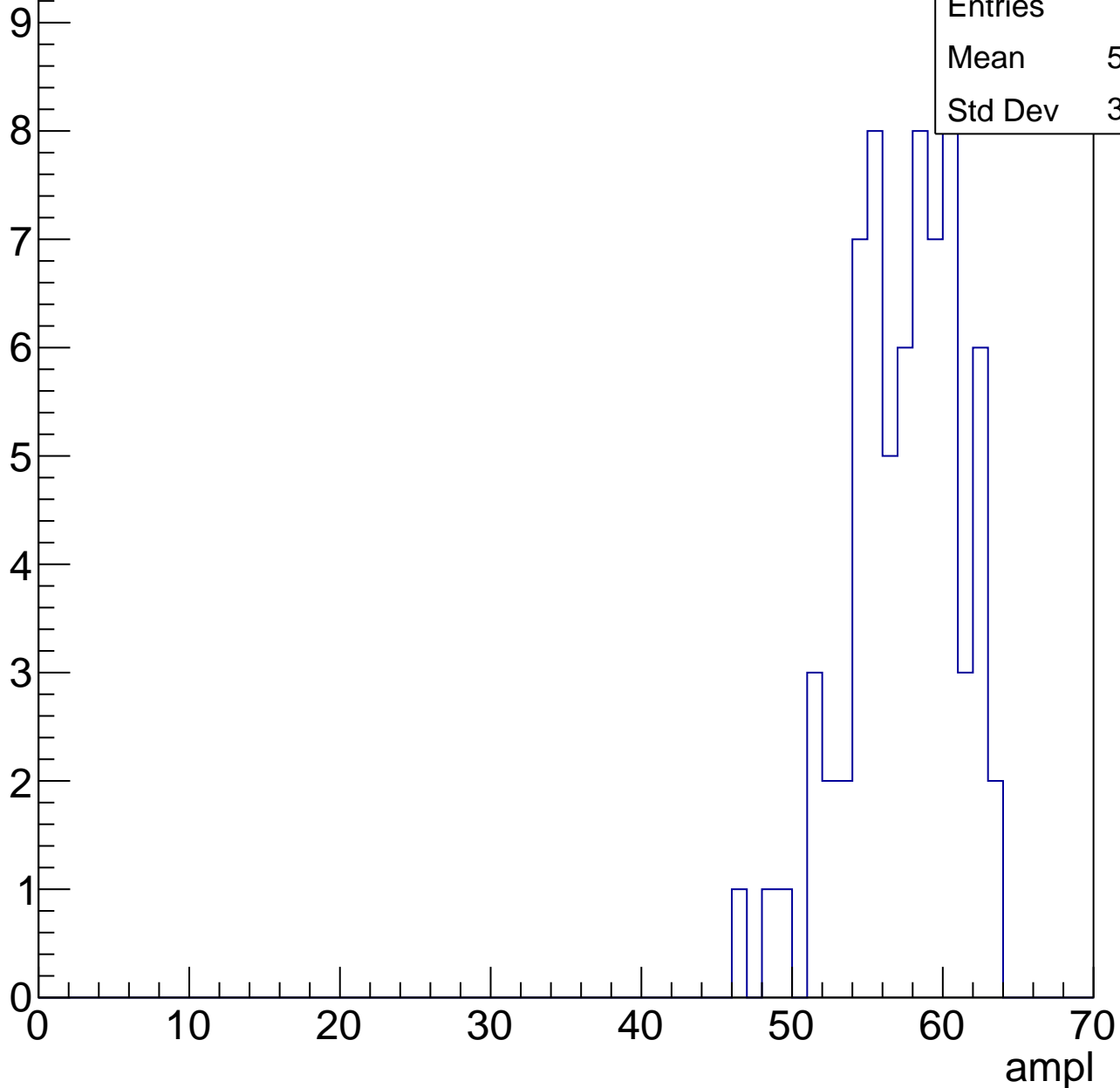
ampl



B1L100S, U11-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

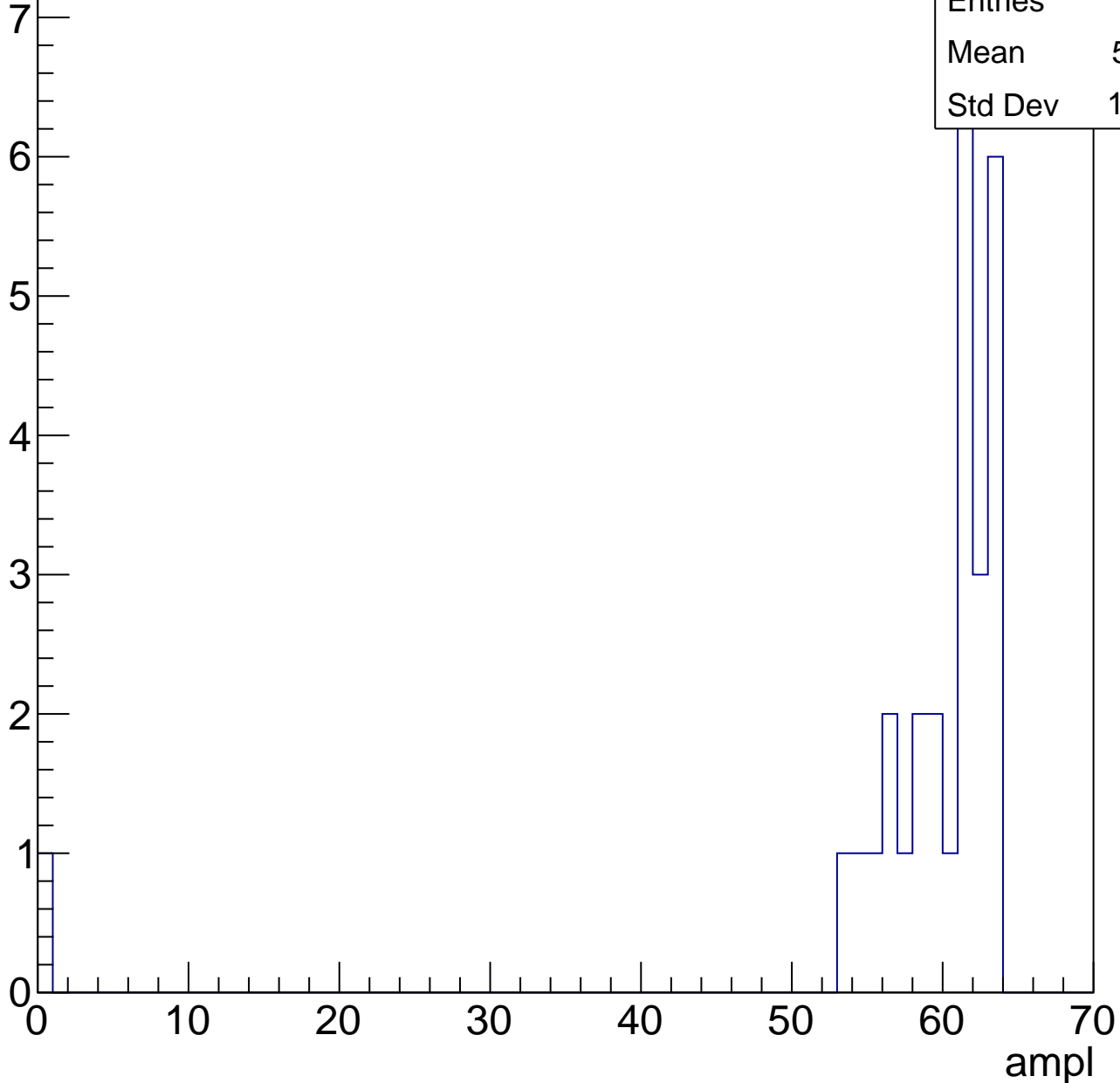


B1L100S, U11-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

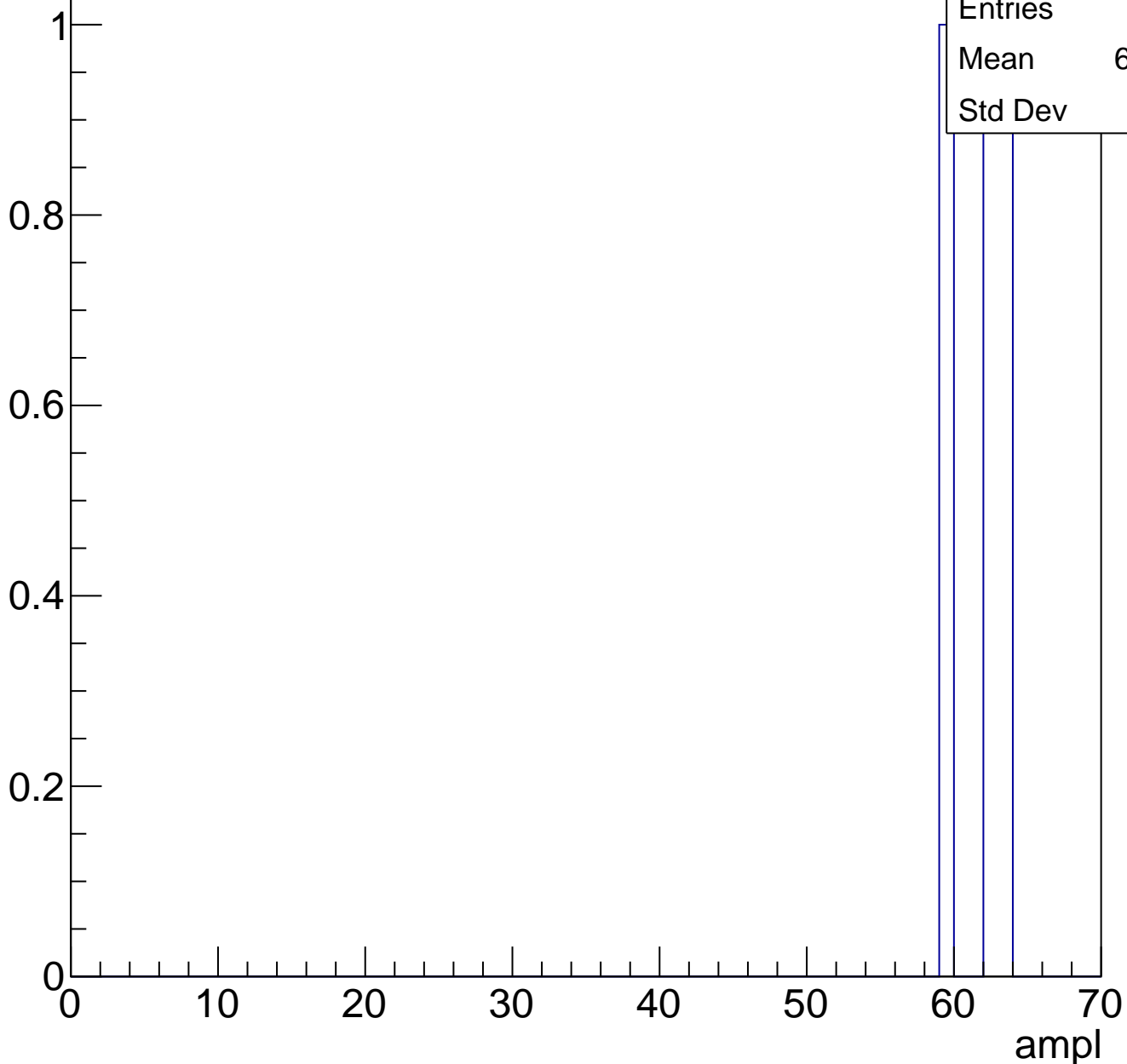
Entries	28
Mean	57.71
Std Dev	11.48



B1L100S, U11-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U11-ch111, adc0

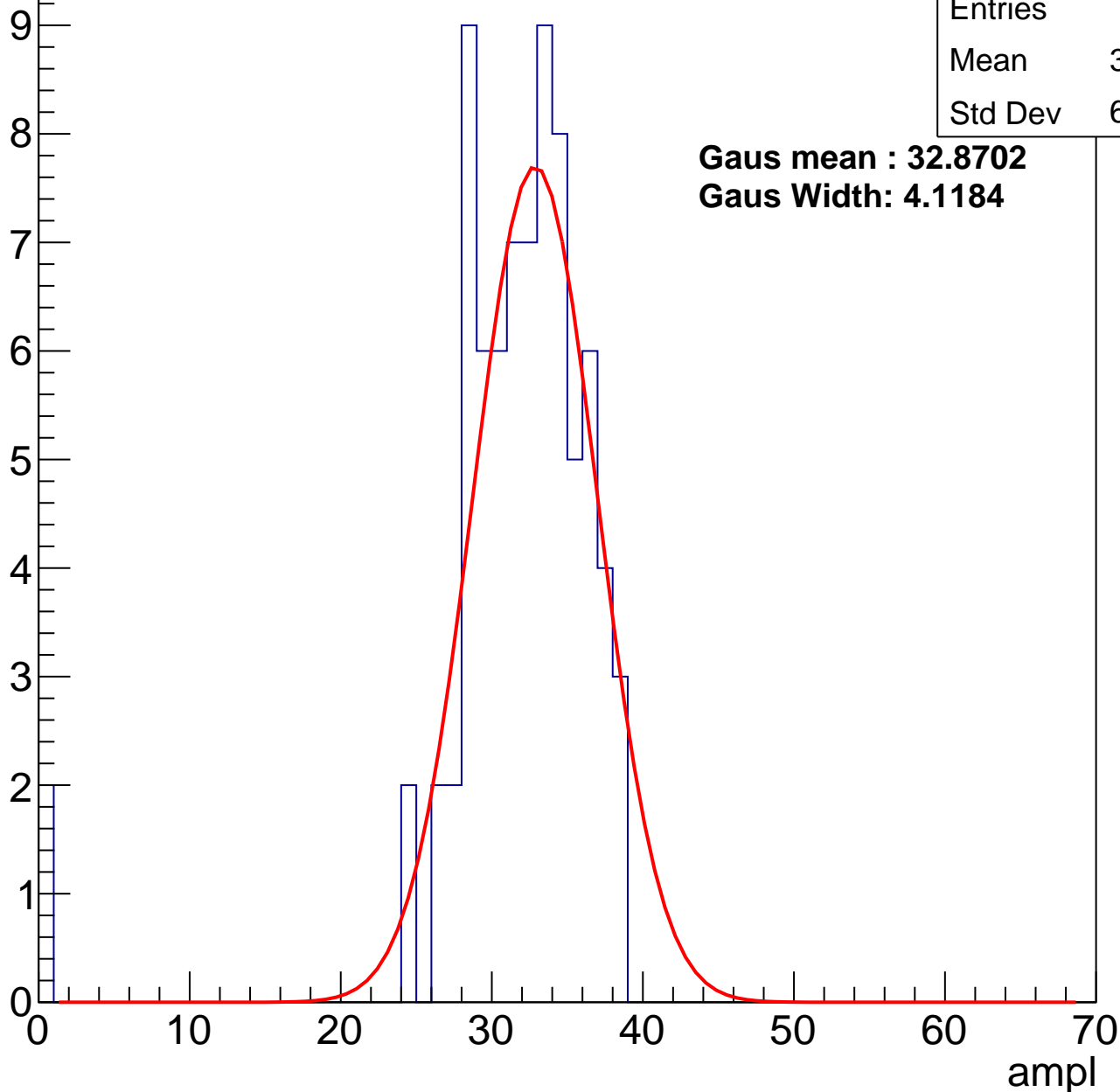
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	31.06
Std Dev	6.043

Gaus mean : 32.8702

Gaus Width: 4.1184



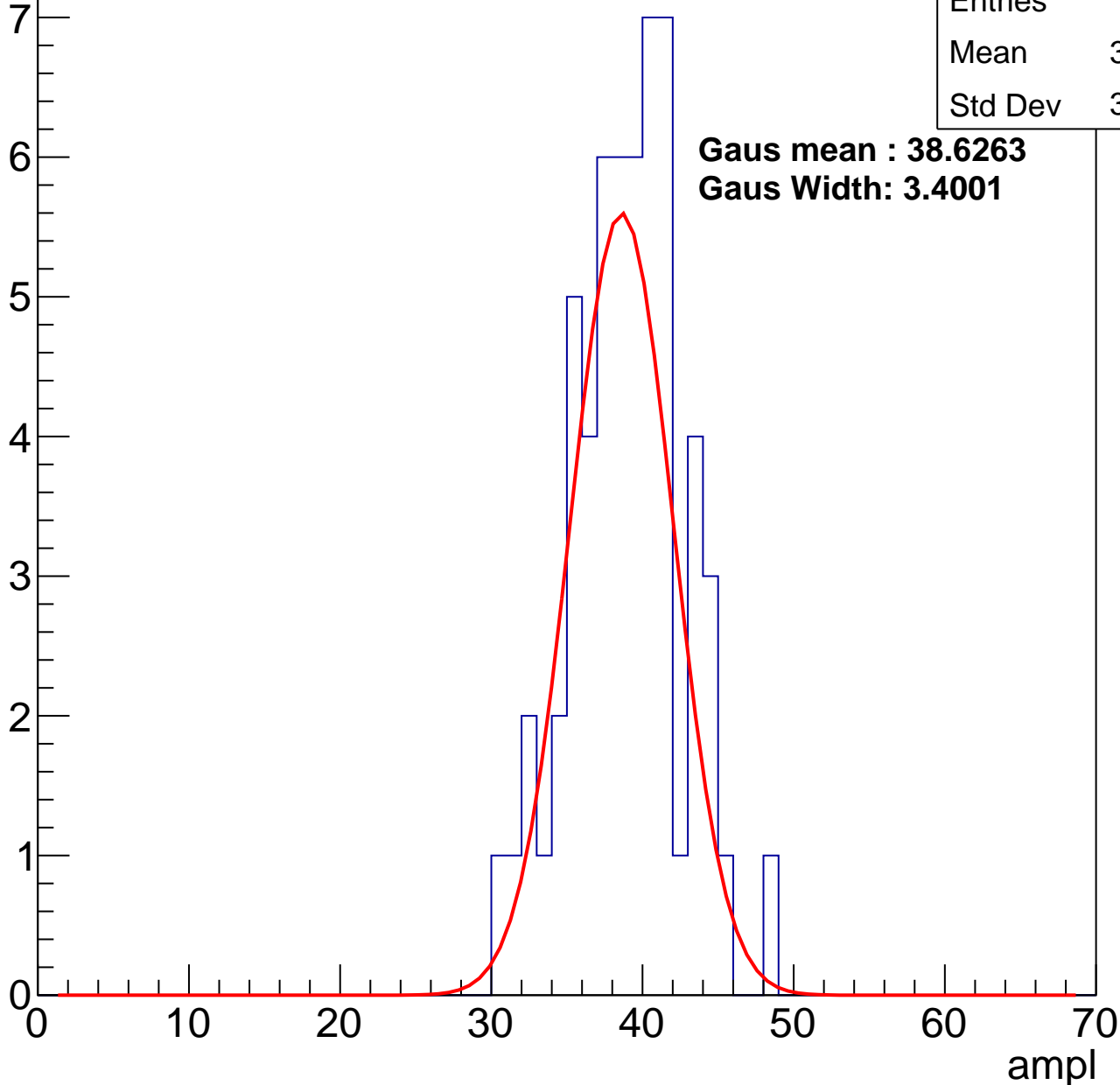
B1L100S, U11-ch111, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	38.53
Std Dev	3.626

Gaus mean : 38.6263
Gaus Width: 3.4001



B1L100S, U11-ch111, adc2

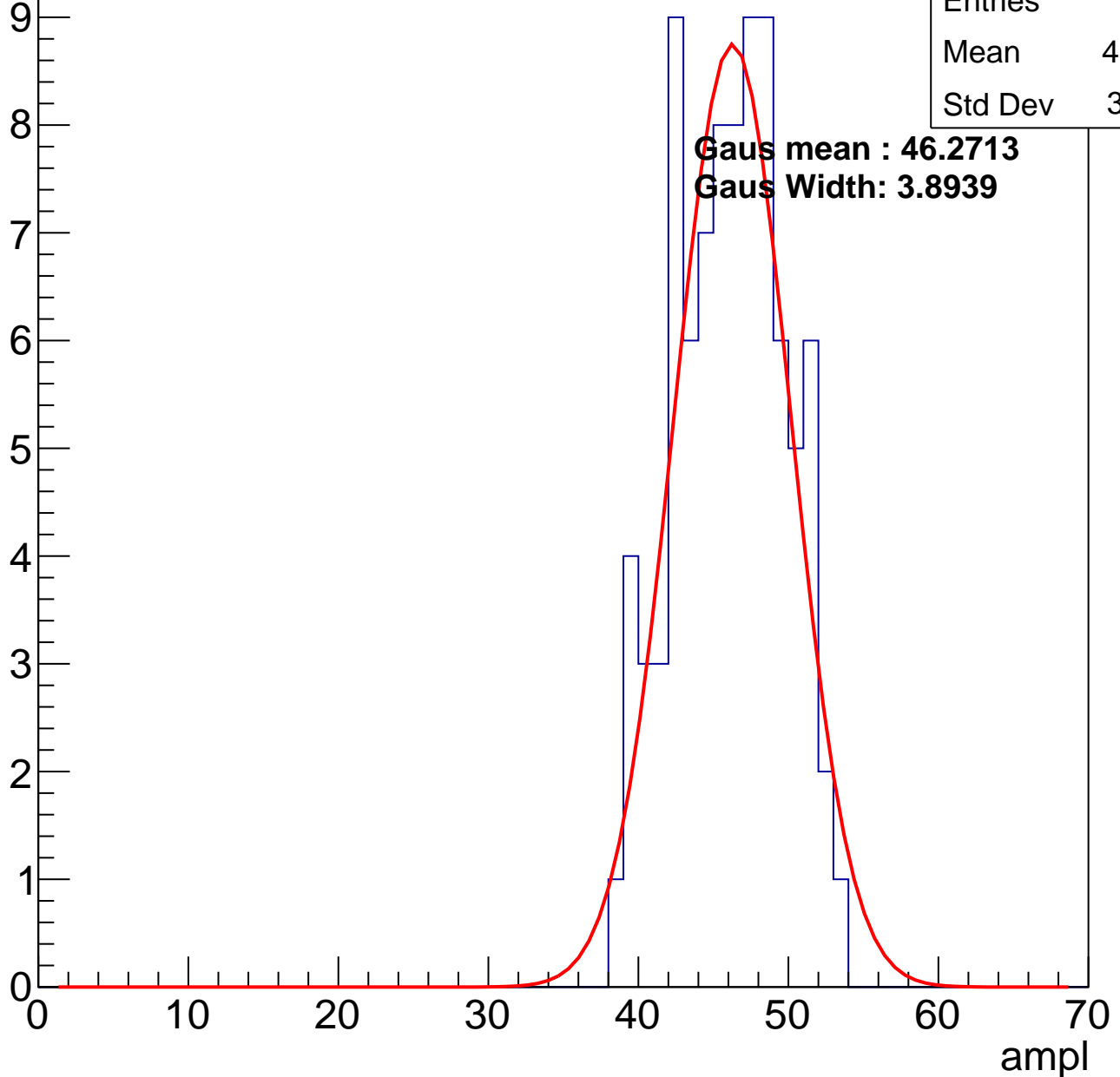
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	45.64
Std Dev	3.581

Gaus mean : 46.2713

Gaus Width: 3.8939

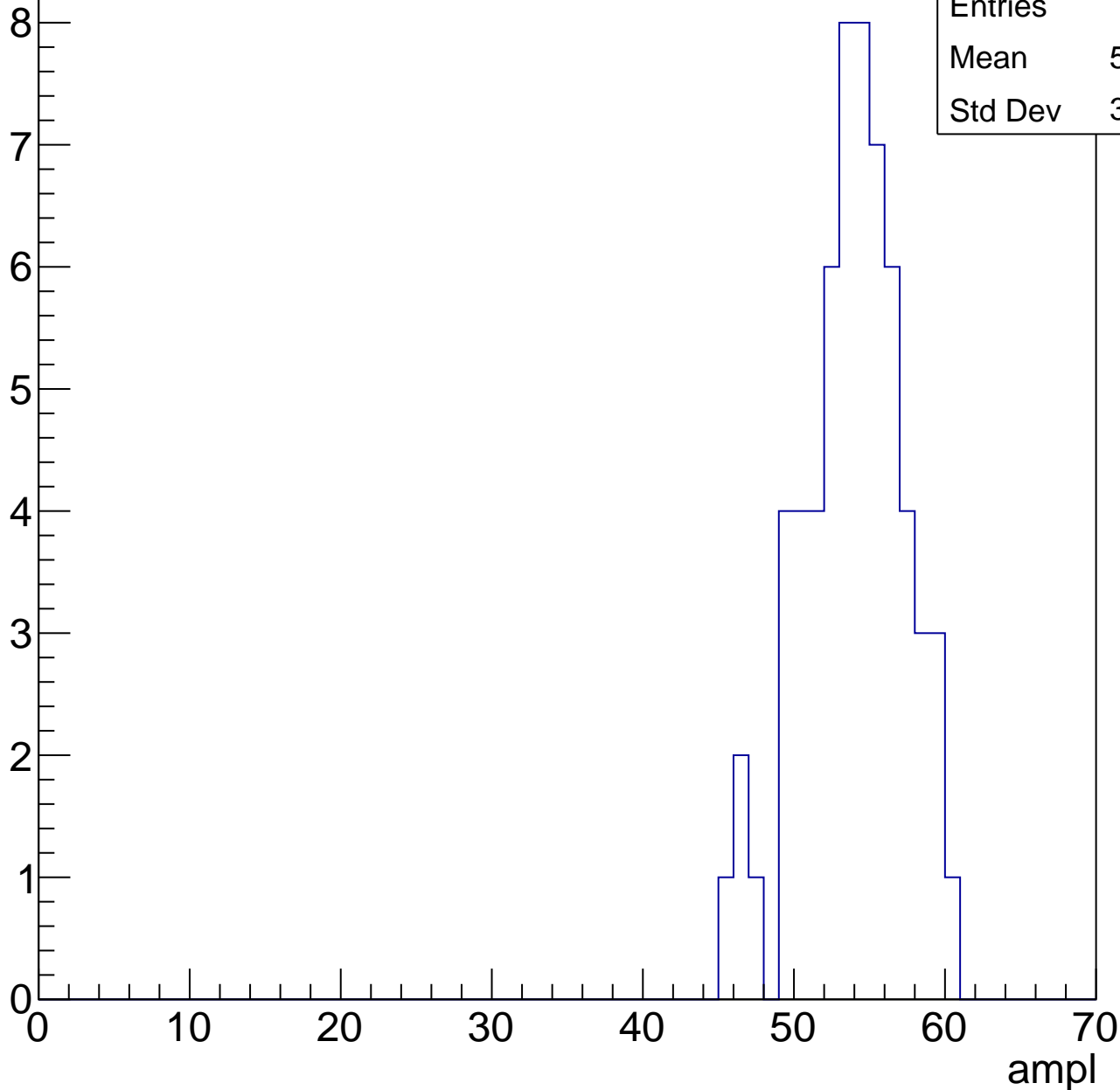


B1L100S, U11-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	53.42
Std Dev	3.348

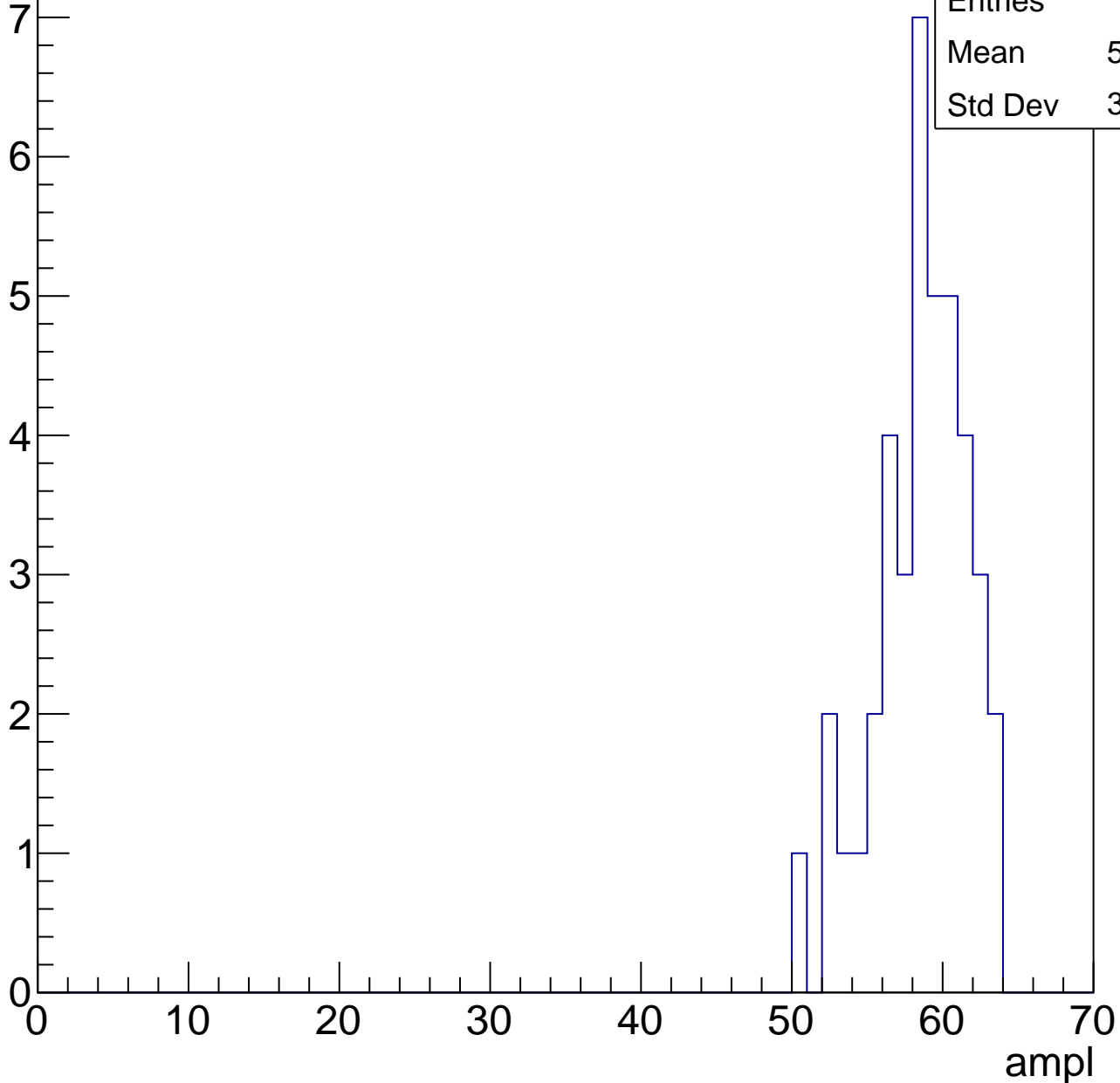


B1L100S, U11-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	58.08
Std Dev	3.053

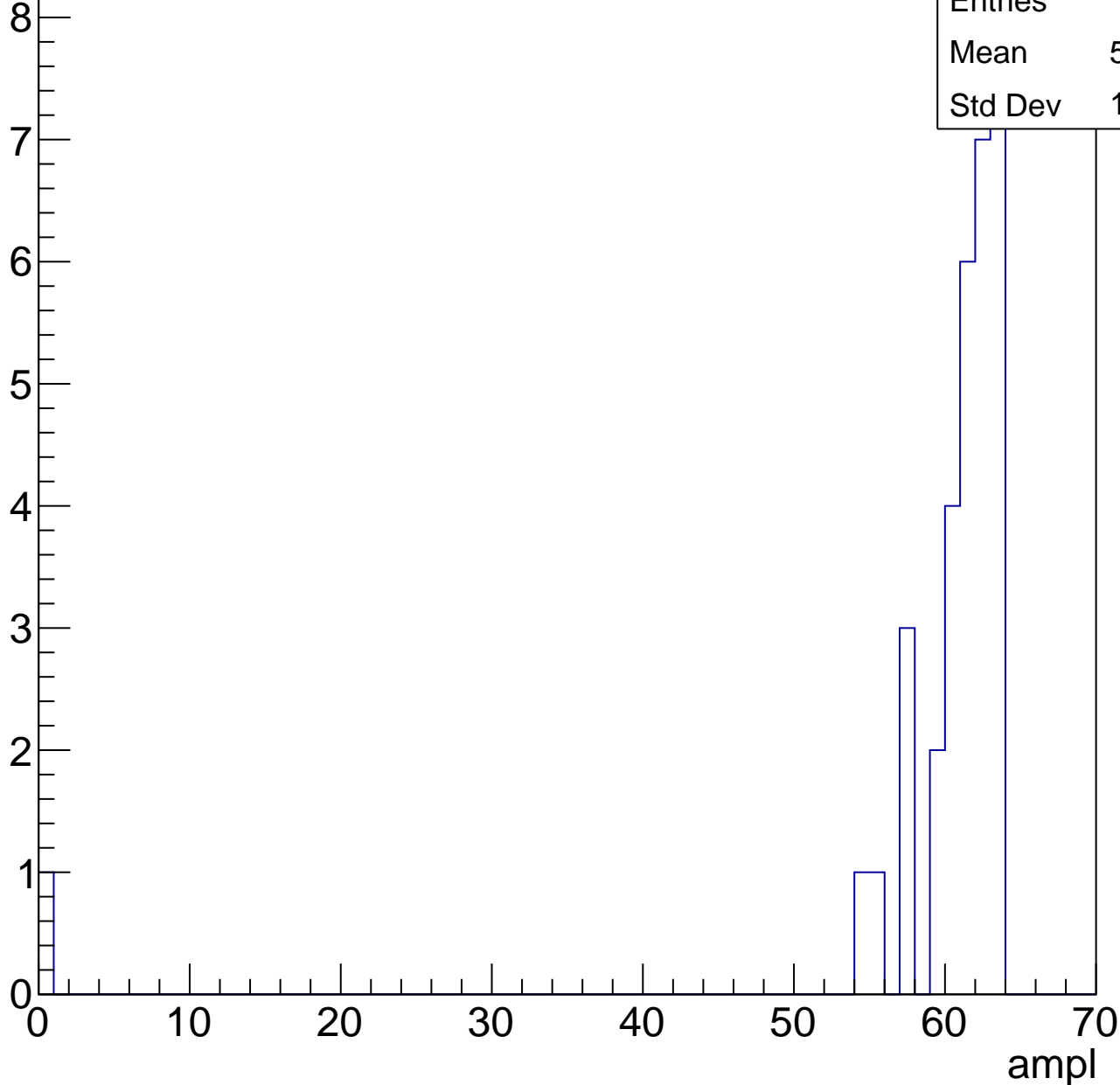


B1L100S, U11-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.85
Std Dev	10.66



B1L100S, U11-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch112, adc0

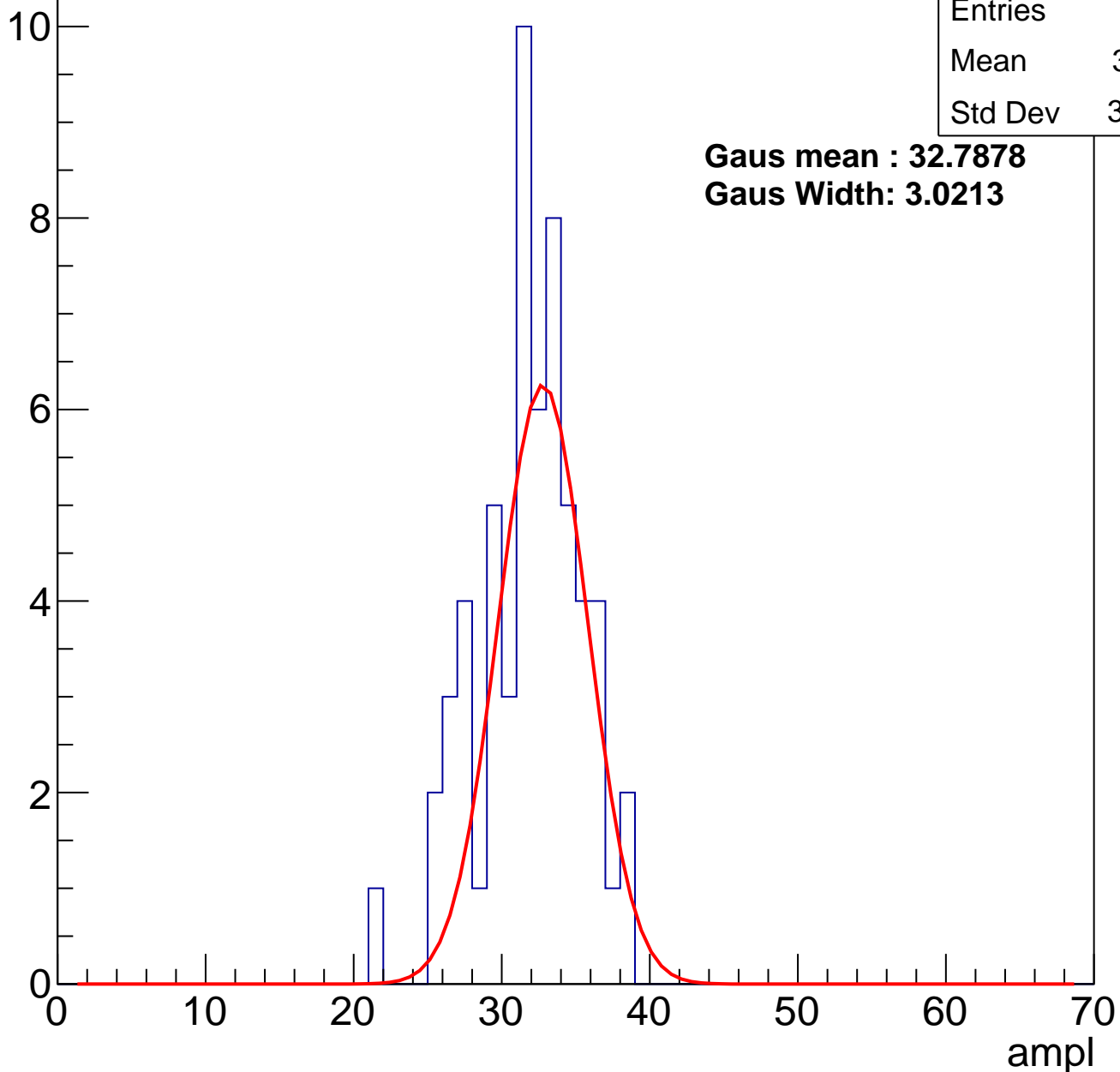
calib_packv5_042523_0143.root, FC#4, port A2

Entries	59
Mean	31.41
Std Dev	3.479

Gaus mean : 32.7878

Gaus Width: 3.0213

Entry



B1L100S, U11-ch112, adc1

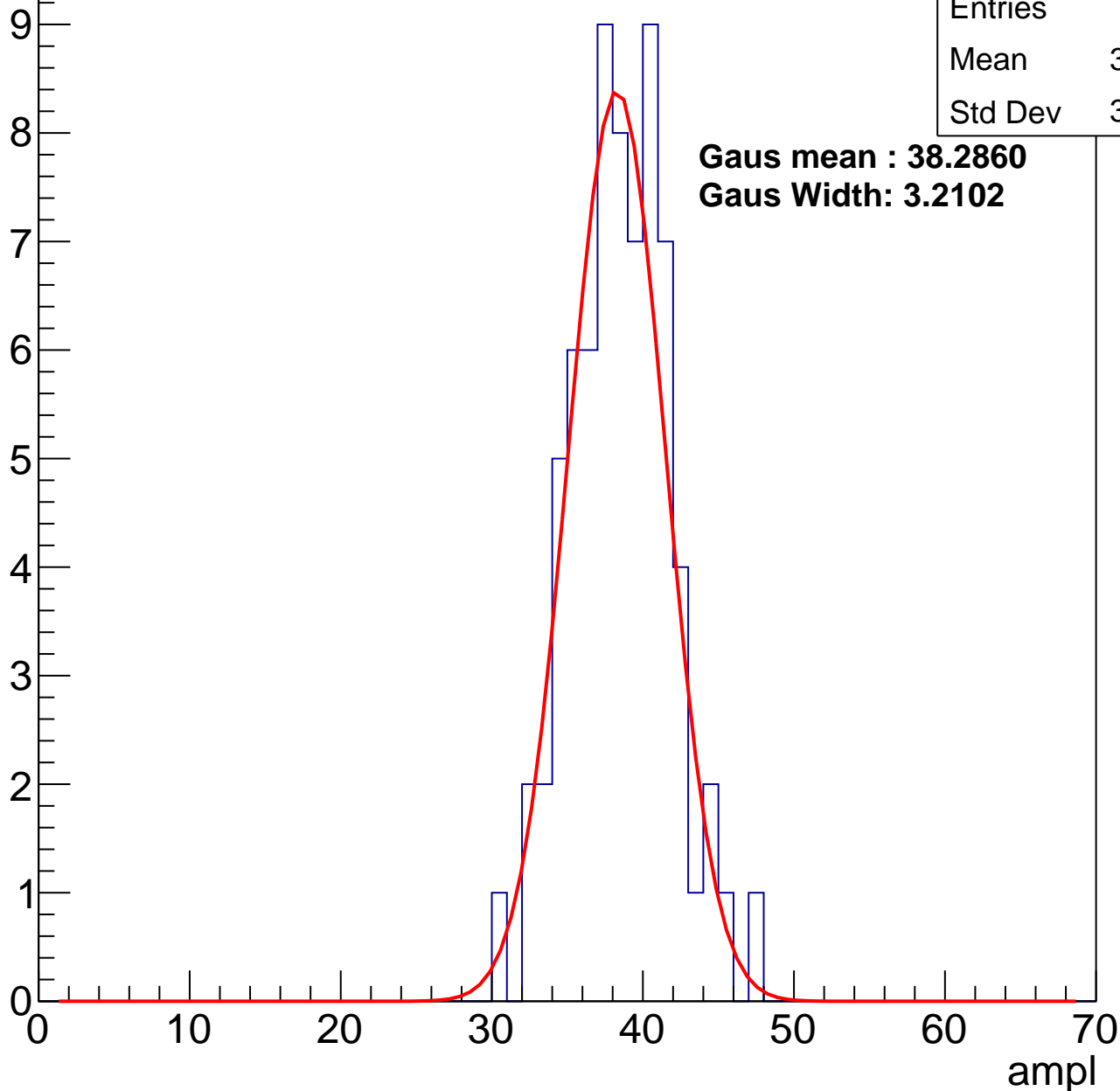
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	38.08
Std Dev	3.249

Gaus mean : 38.2860

Gaus Width: 3.2102



B1L100S, U11-ch112, adc2

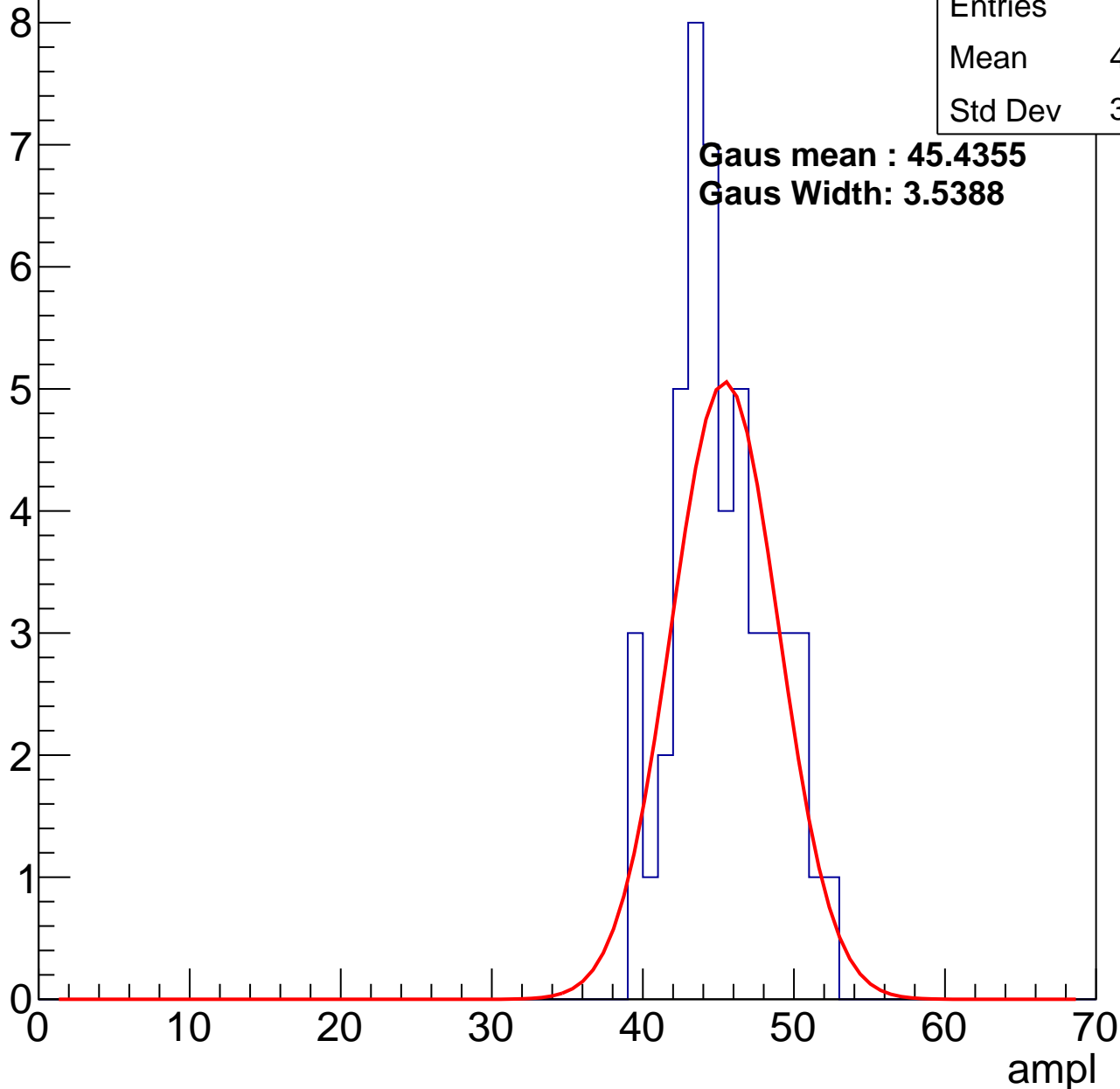
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	44.82
Std Dev	3.192

Gaus mean : 45.4355

Gaus Width: 3.5388

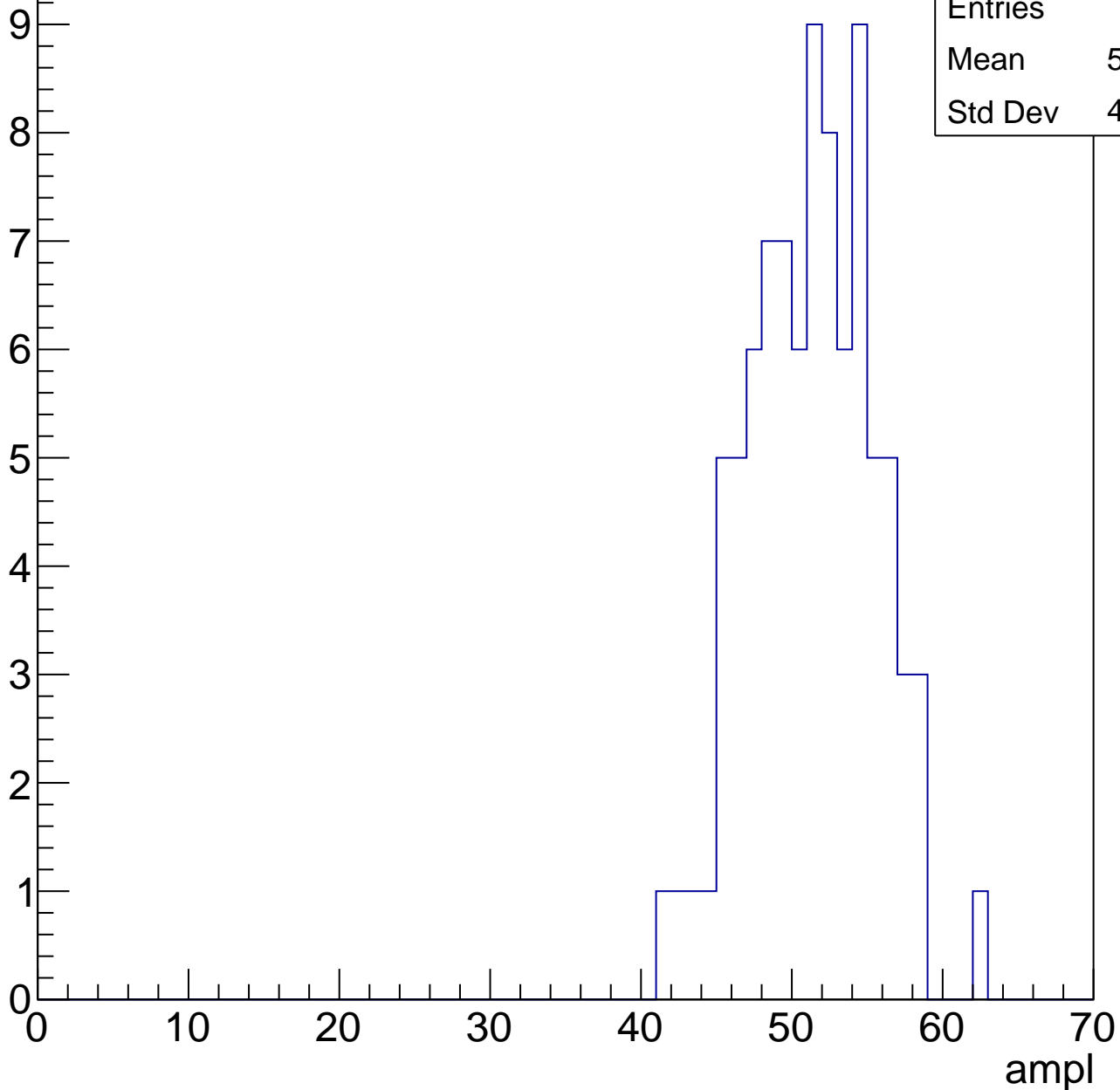


B1L100S, U11-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	50.87
Std Dev	4.084

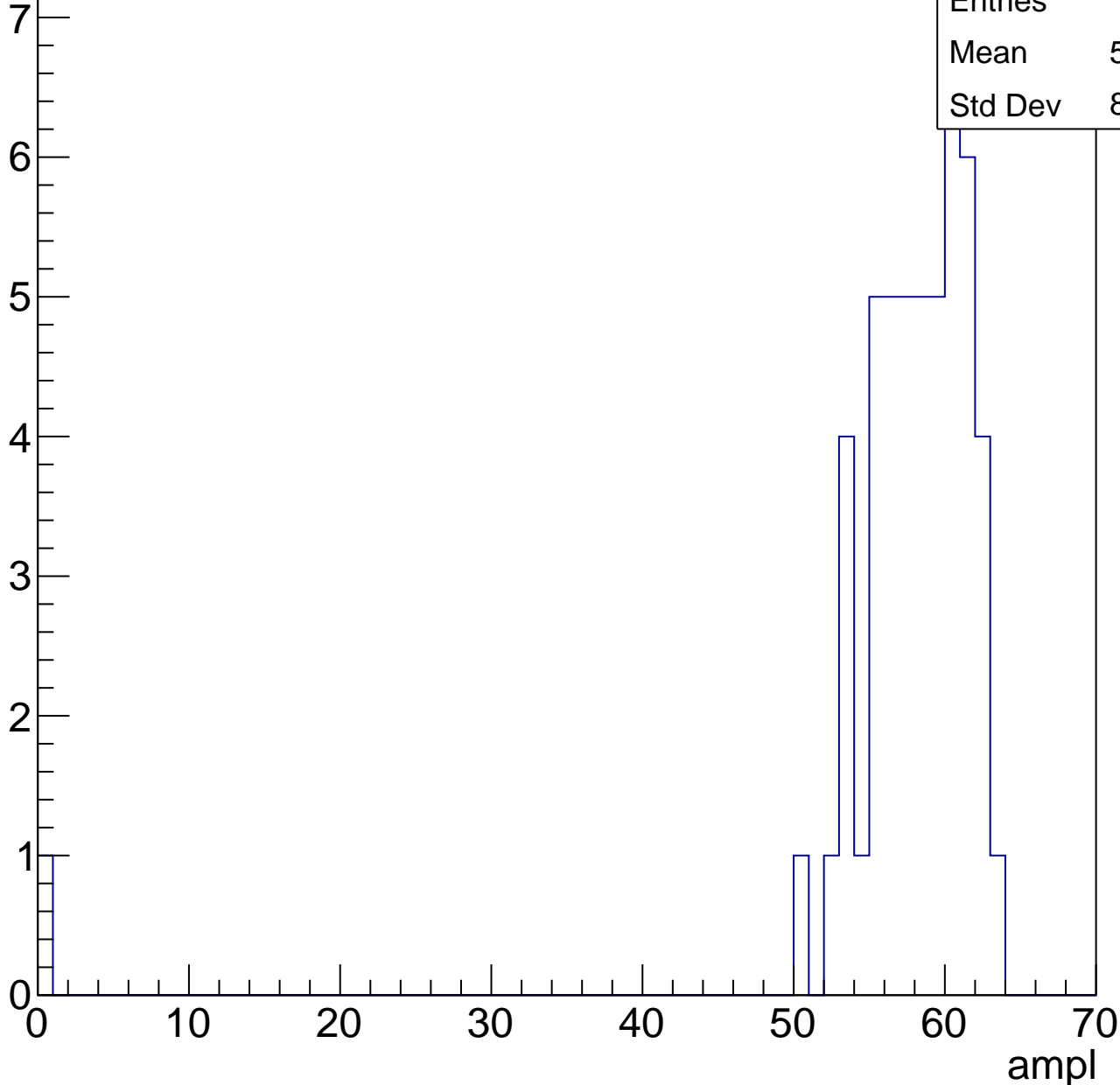


B1L100S, U11-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	56.67
Std Dev	8.558

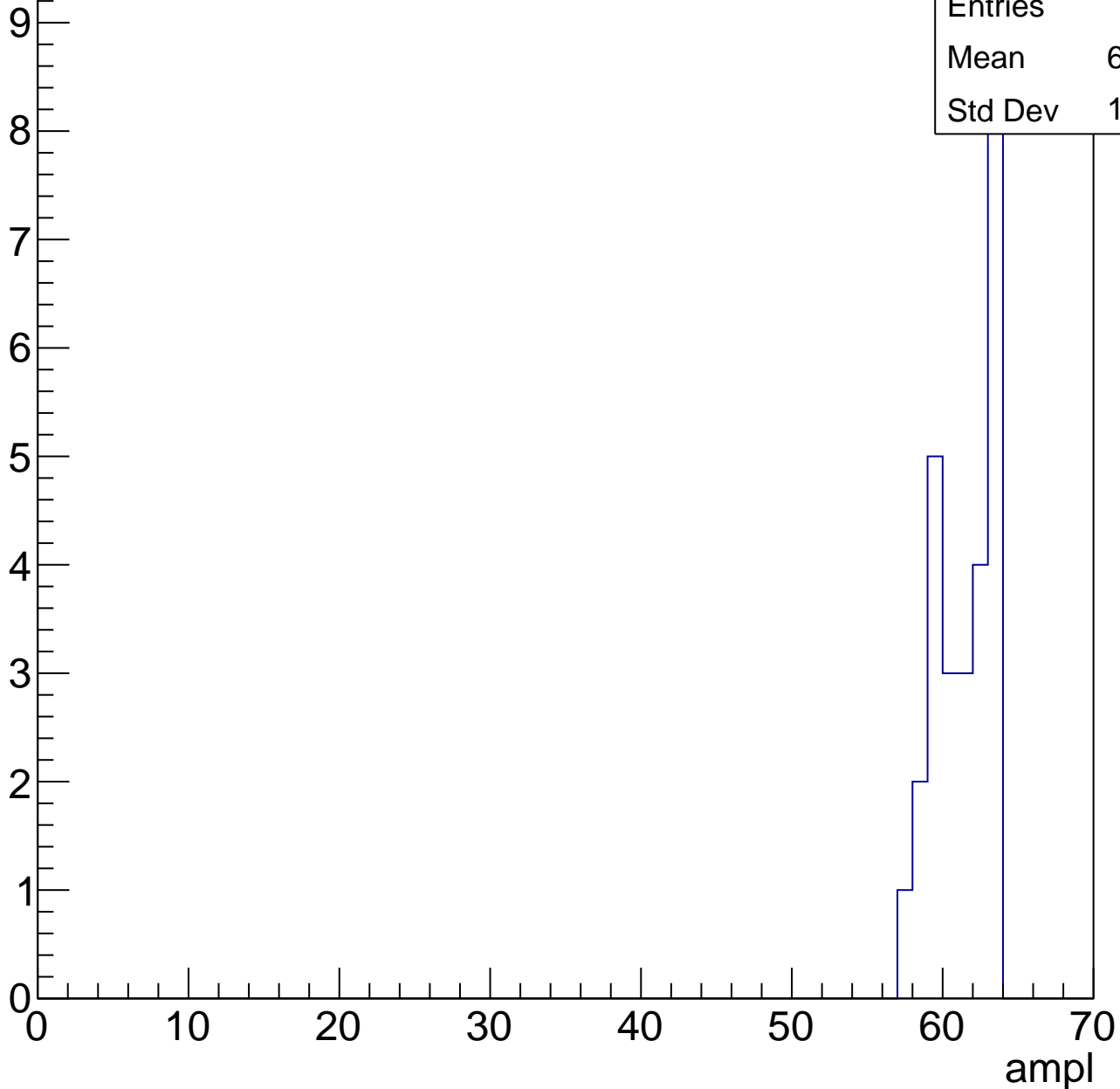


B1L100S, U11-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

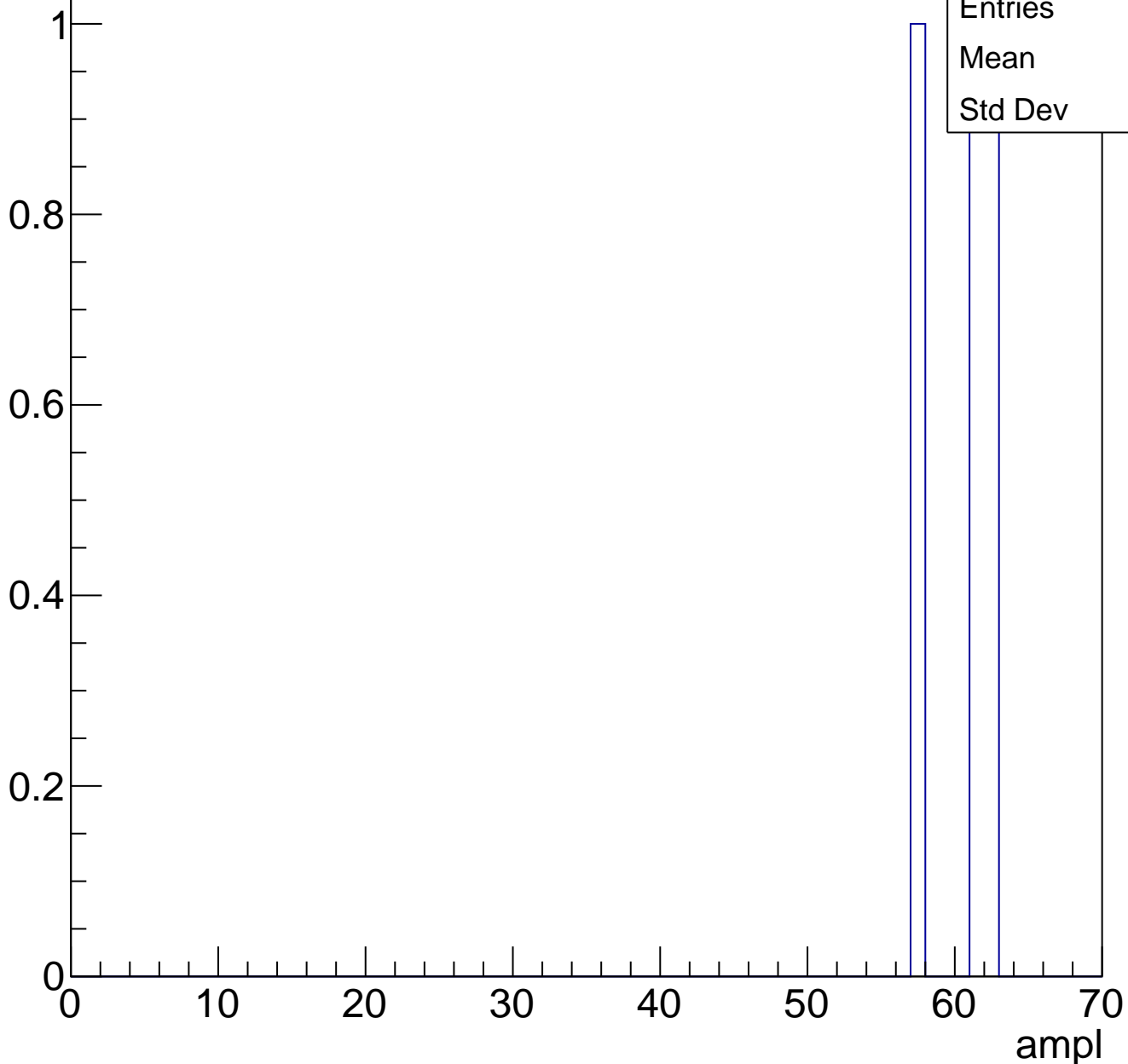
Entries	27
Mean	60.96
Std Dev	1.895



B1L100S, U11-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

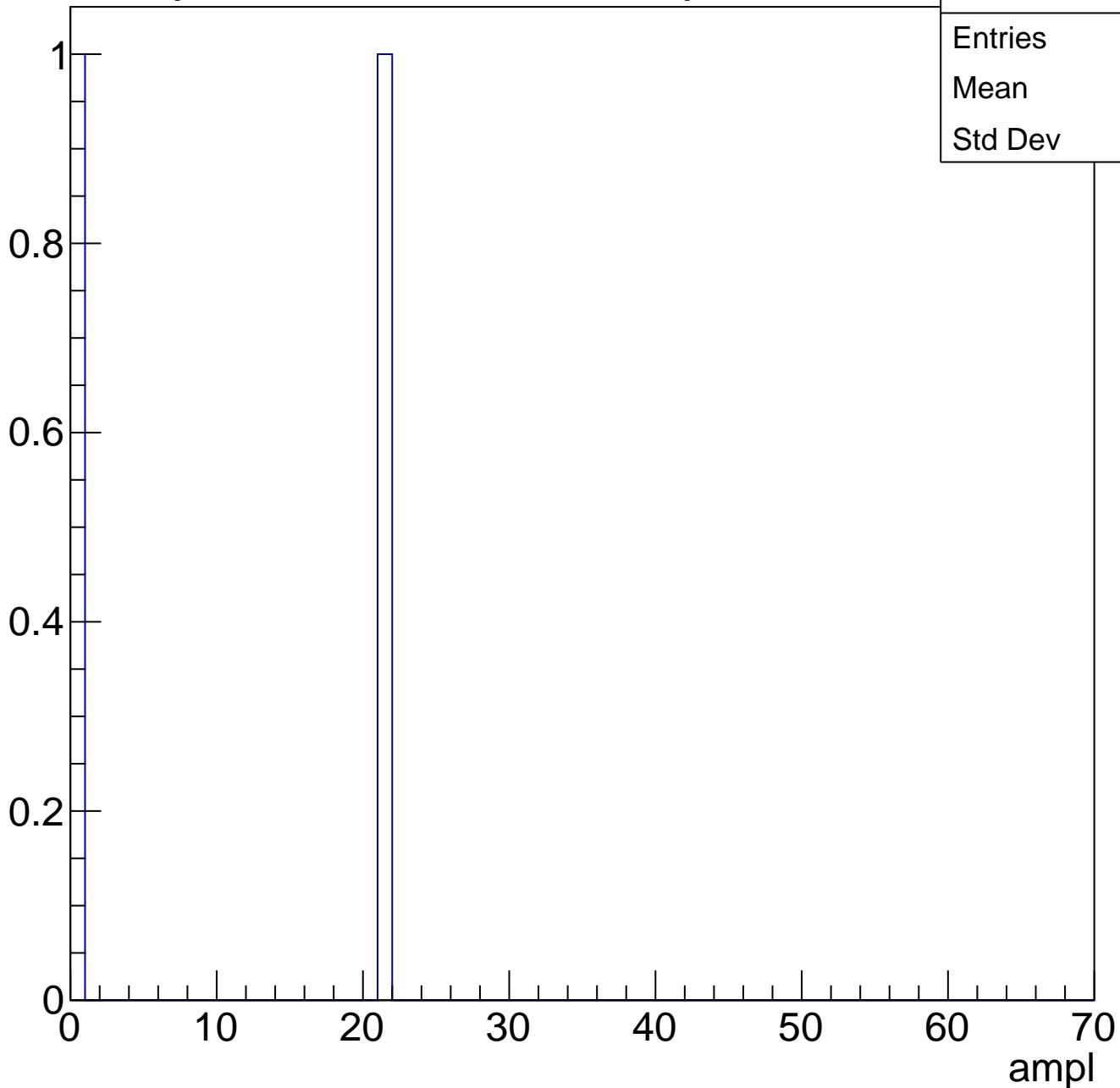
Entry



B1L100S, U11-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch113, adc0

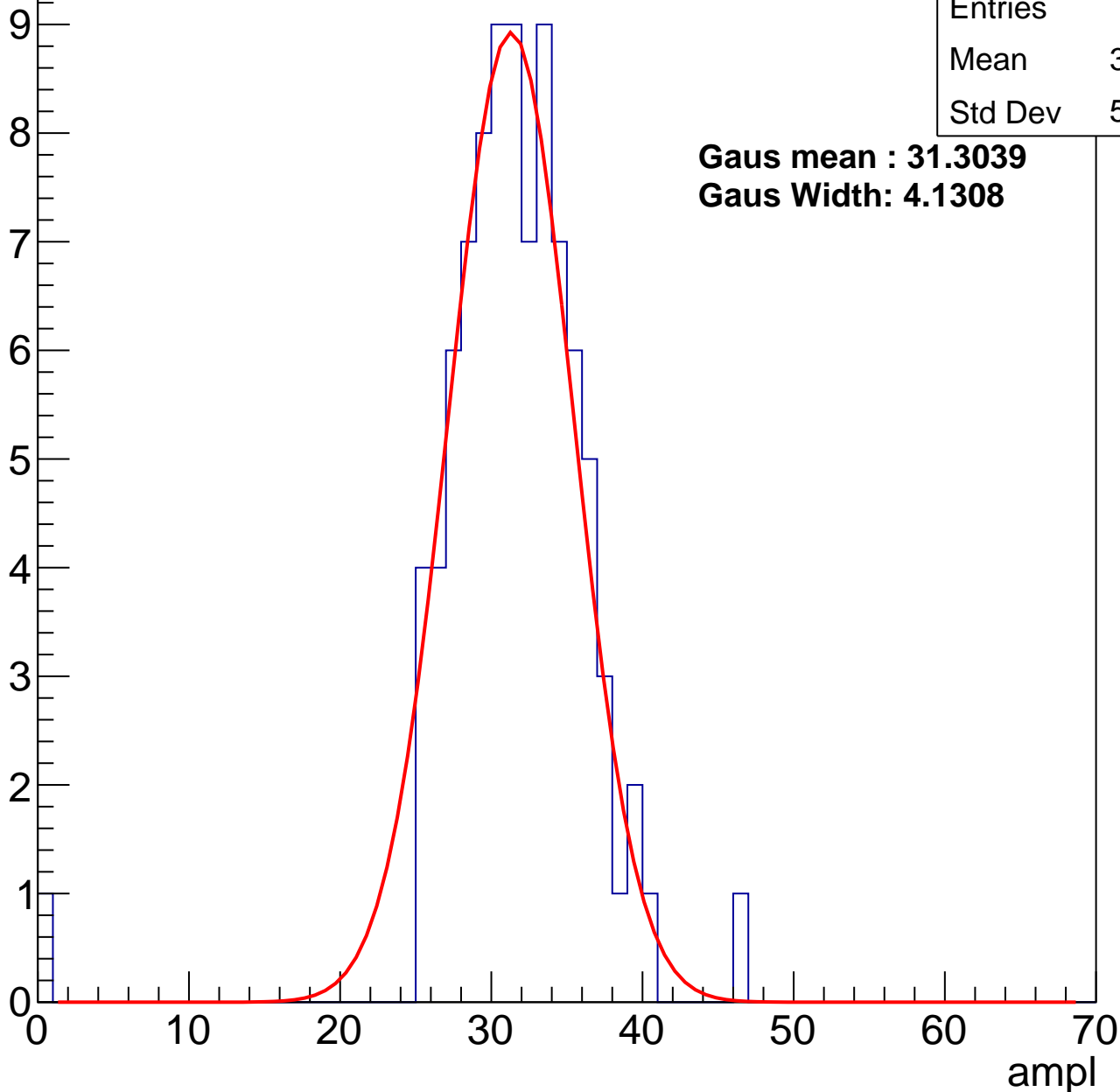
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	90
Mean	31.17
Std Dev	5.078

Gaus mean : 31.3039

Gaus Width: 4.1308



B1L100S, U11-ch113, adc1

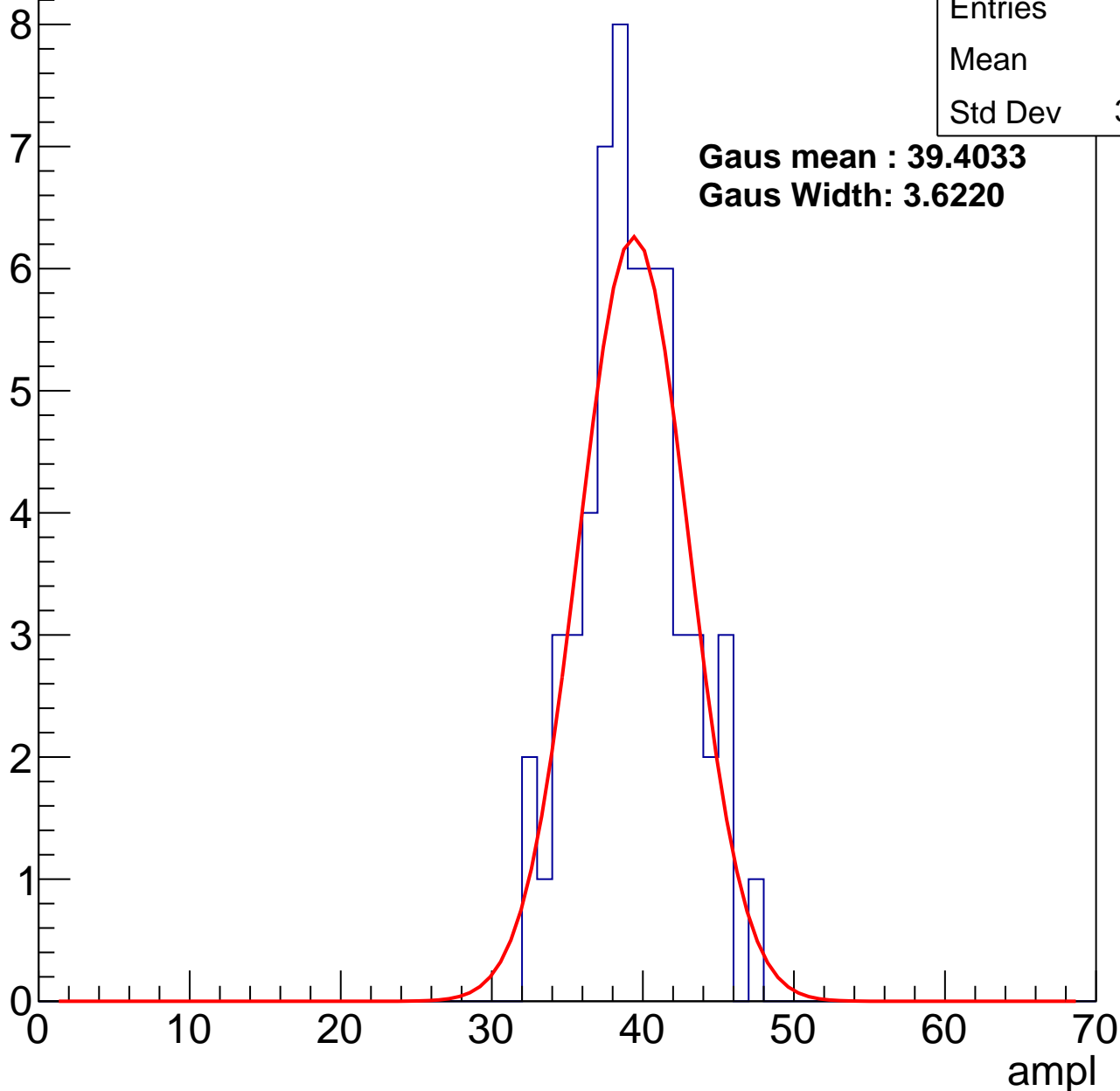
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	38.9
Std Dev	3.361

Gaus mean : 39.4033

Gaus Width: 3.6220



B1L100S, U11-ch113, adc2

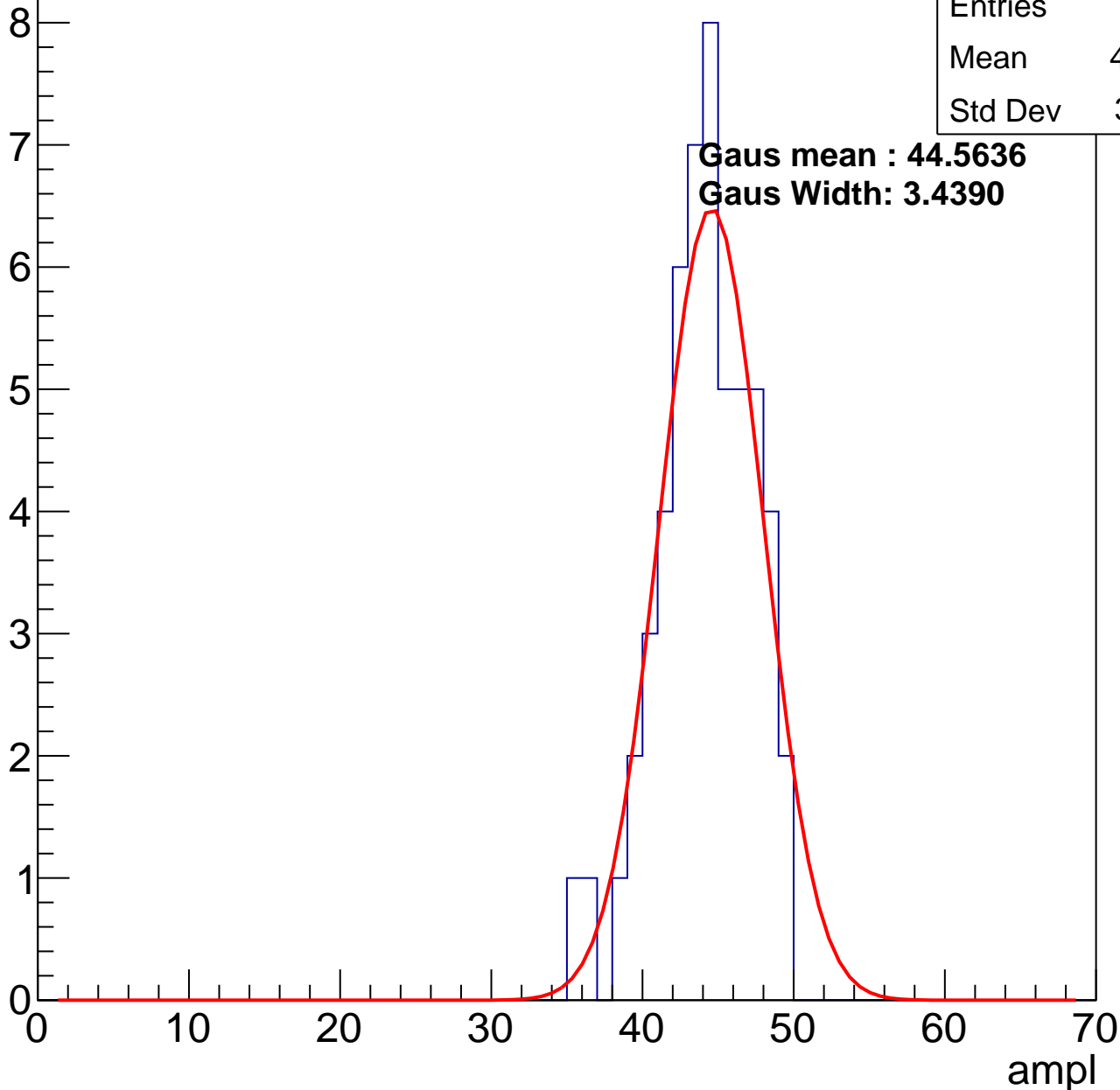
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.63
Std Dev	3.111

Gaus mean : 44.5636

Gaus Width: 3.4390



B1L100S, U11-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

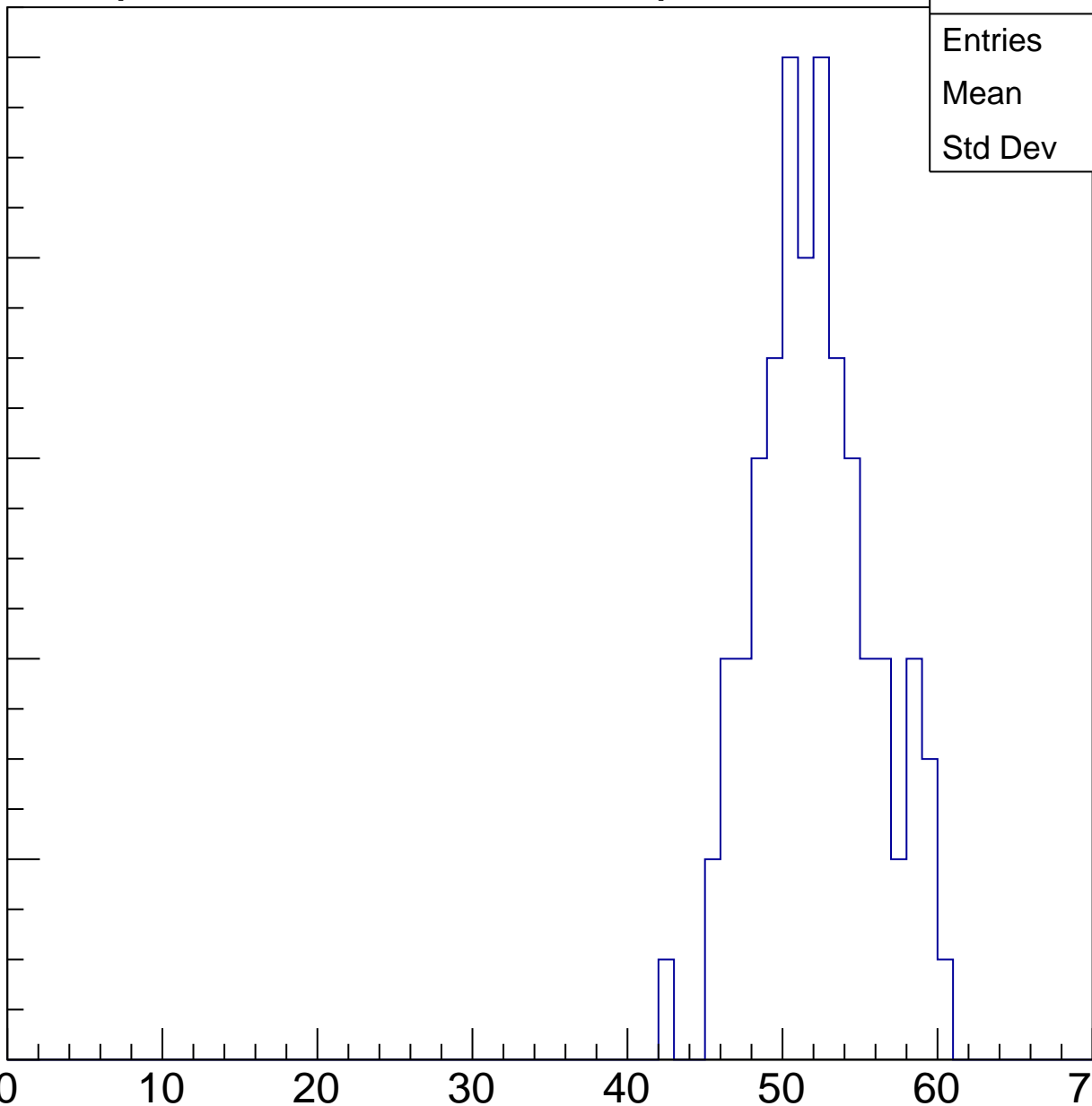
Entries	83
Mean	51.63
Std Dev	3.76

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

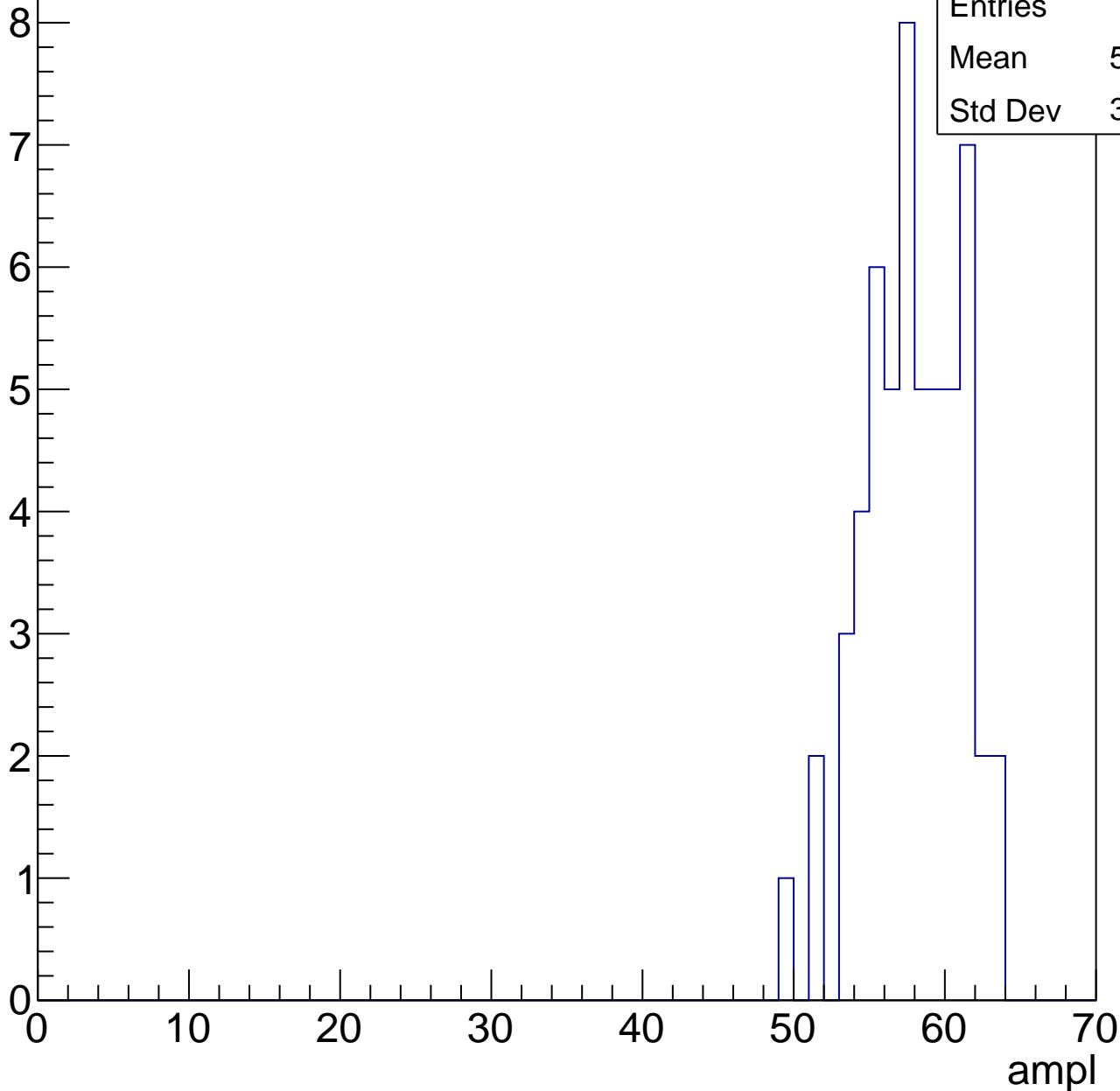


B1L100S, U11-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	57.35
Std Dev	3.152

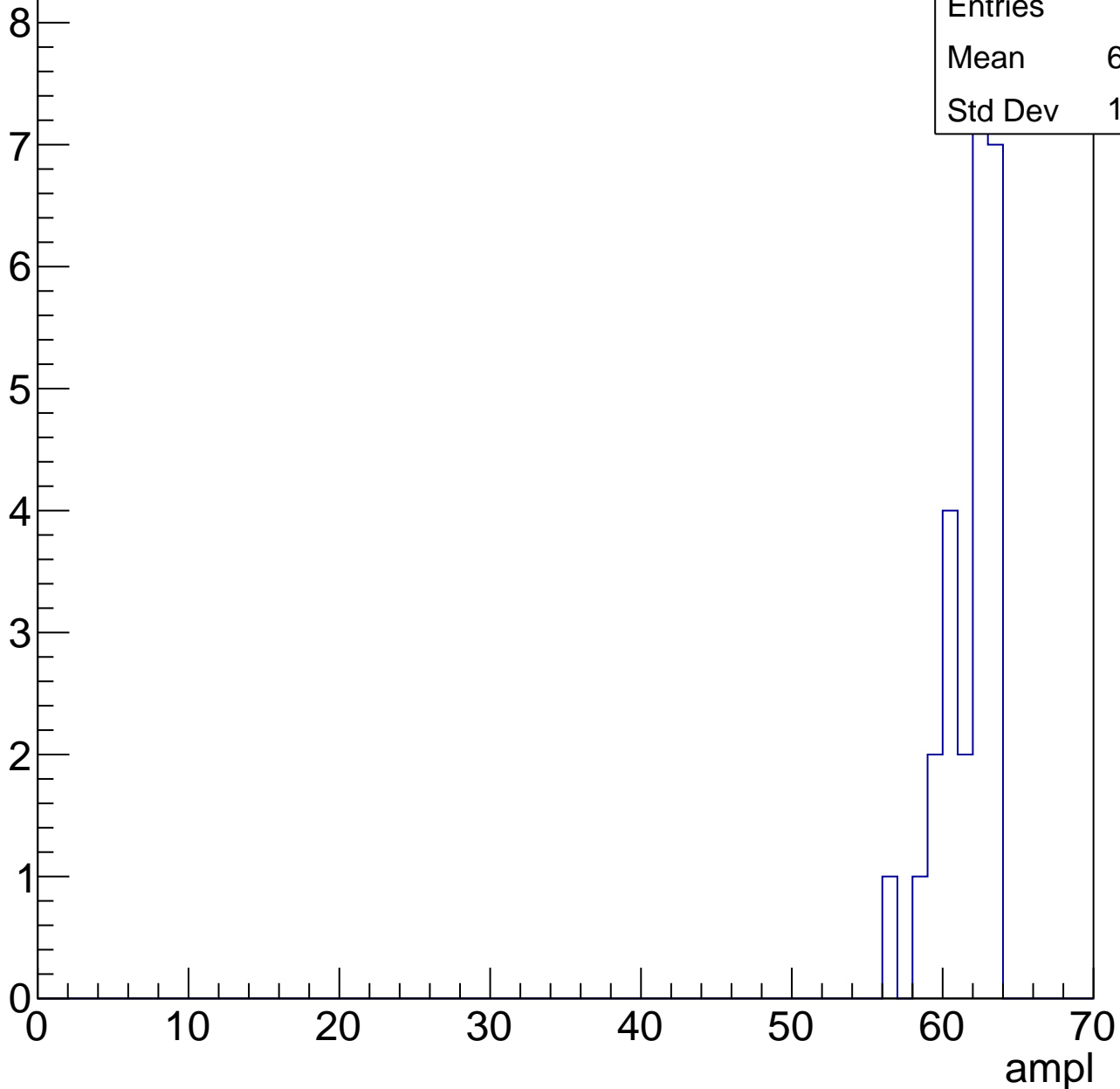


B1L100S, U11-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	61.24
Std Dev	1.795



B1L100S, U11-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch114, adc0

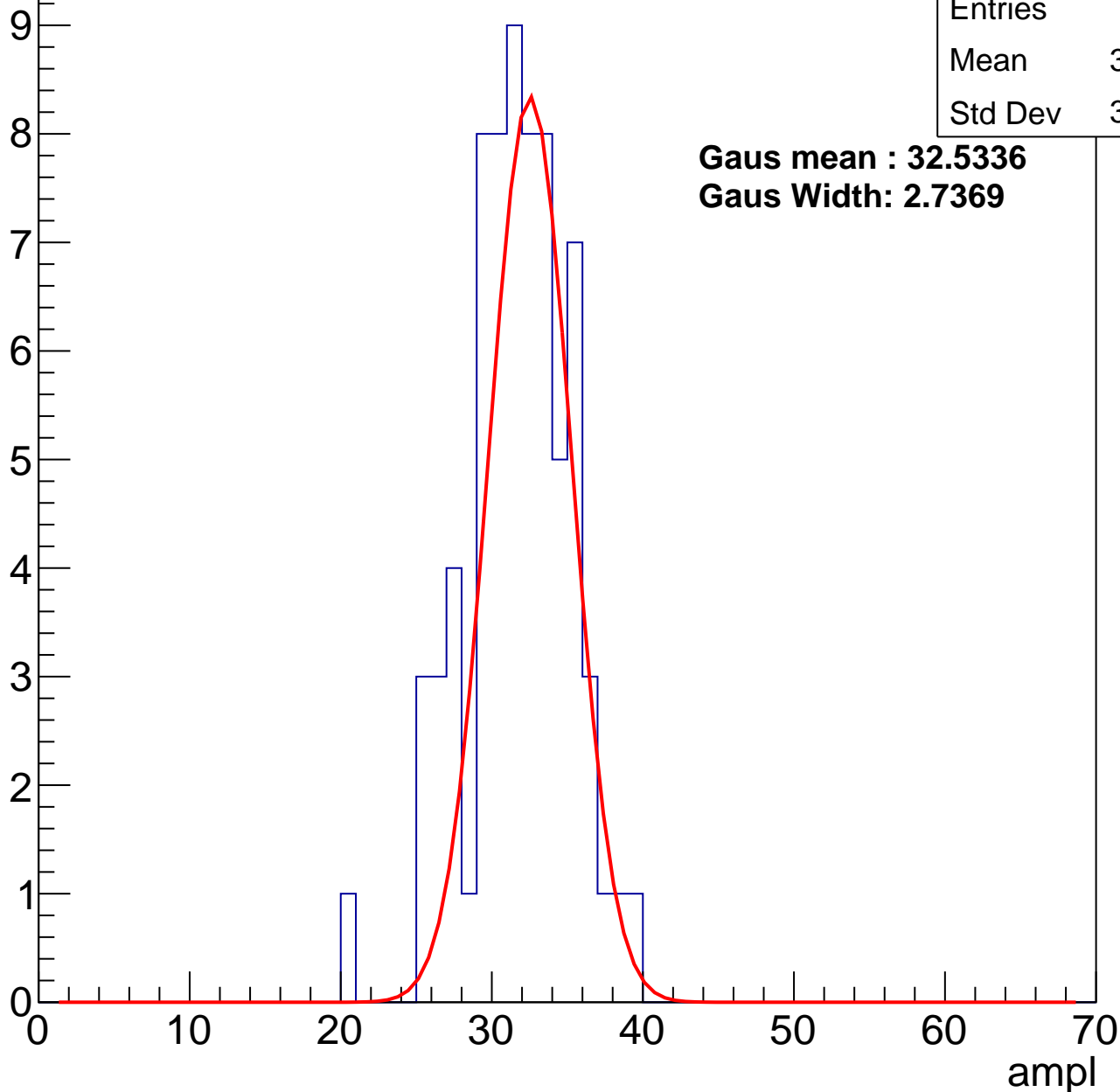
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	31.23
Std Dev	3.428

Gaus mean : 32.5336

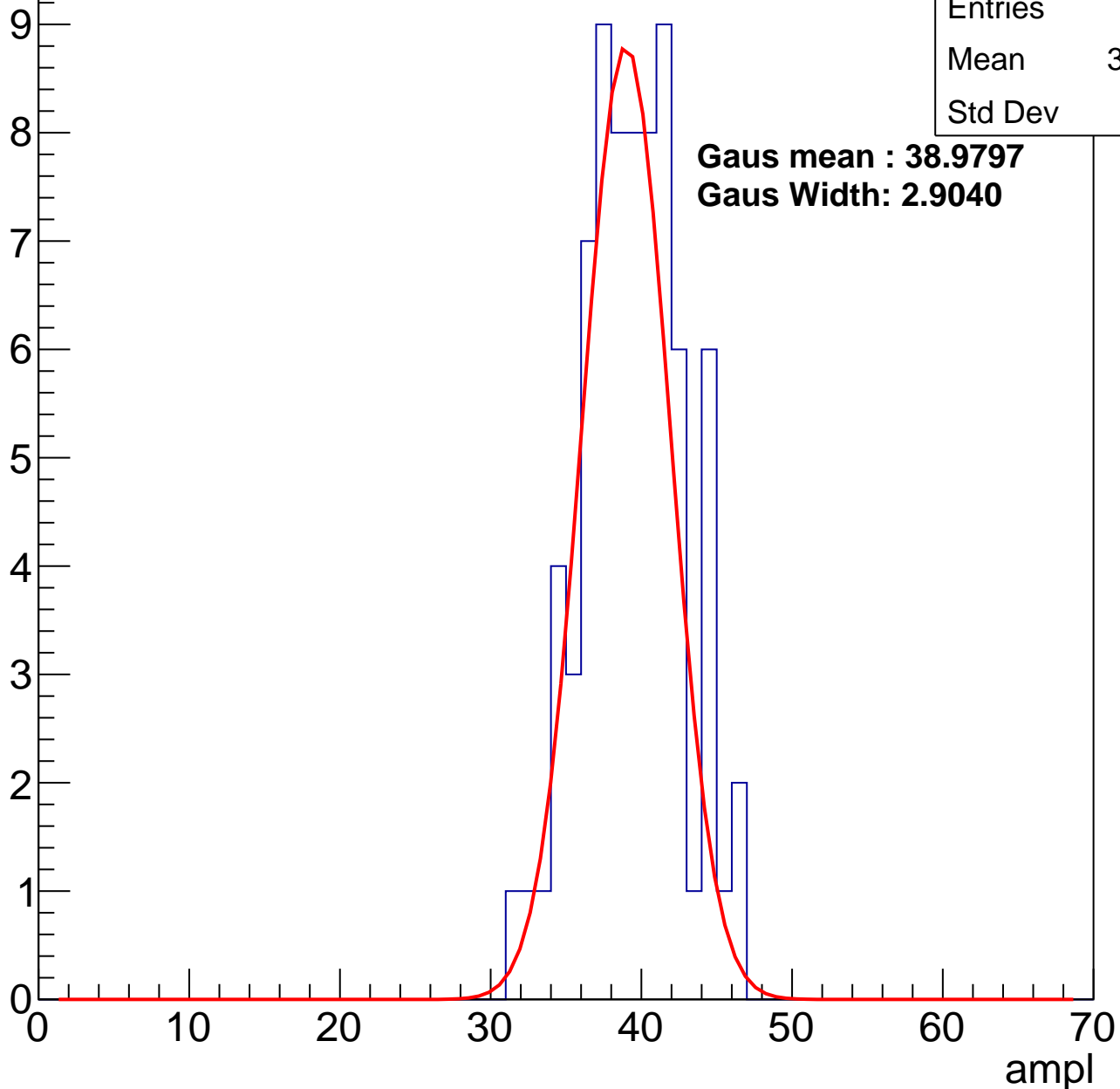
Gaus Width: 2.7369



B1L100S, U11-ch114, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch114, adc2

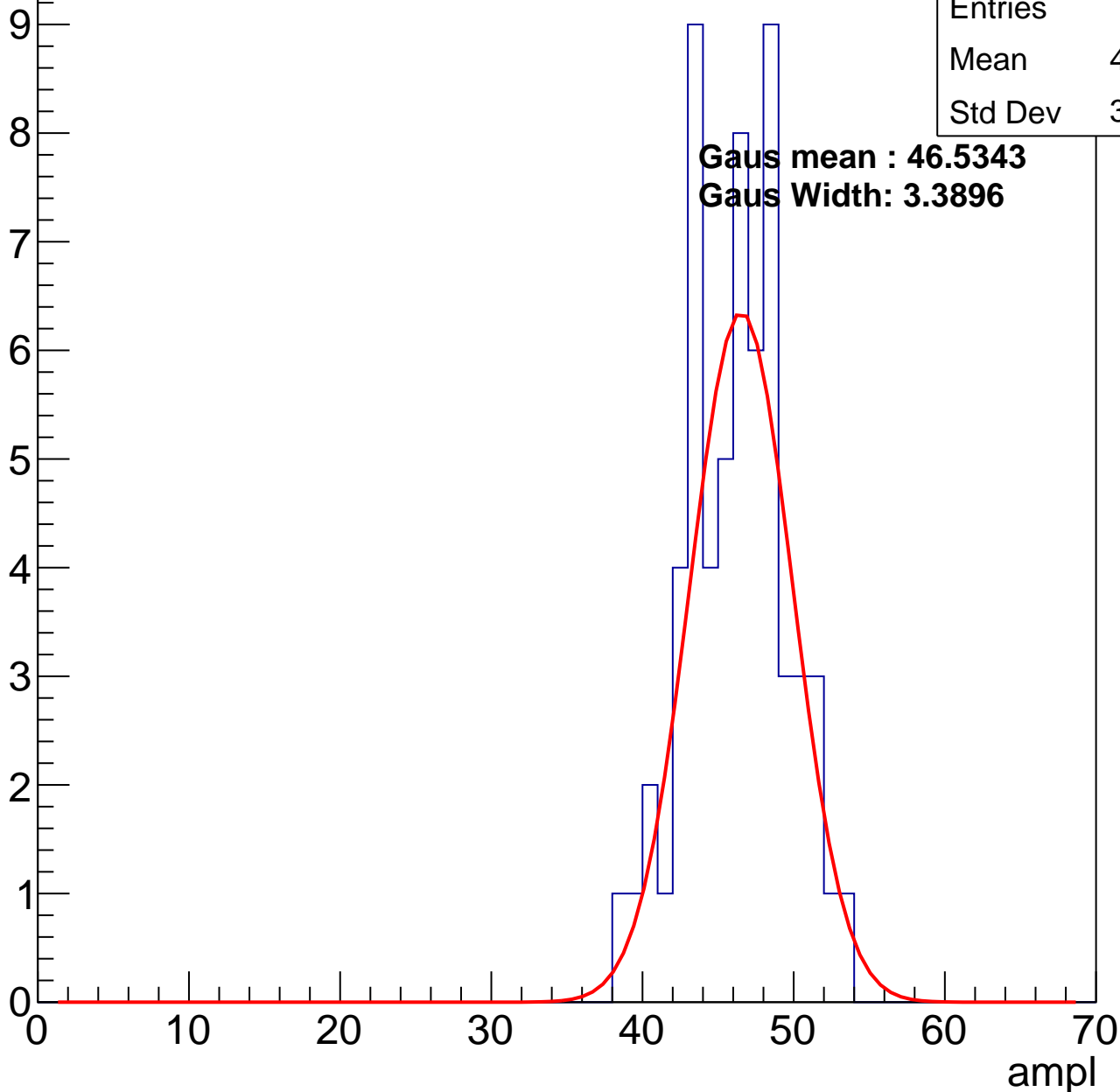
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	45.75
Std Dev	3.263

Gaus mean : 46.5343

Gaus Width: 3.3896

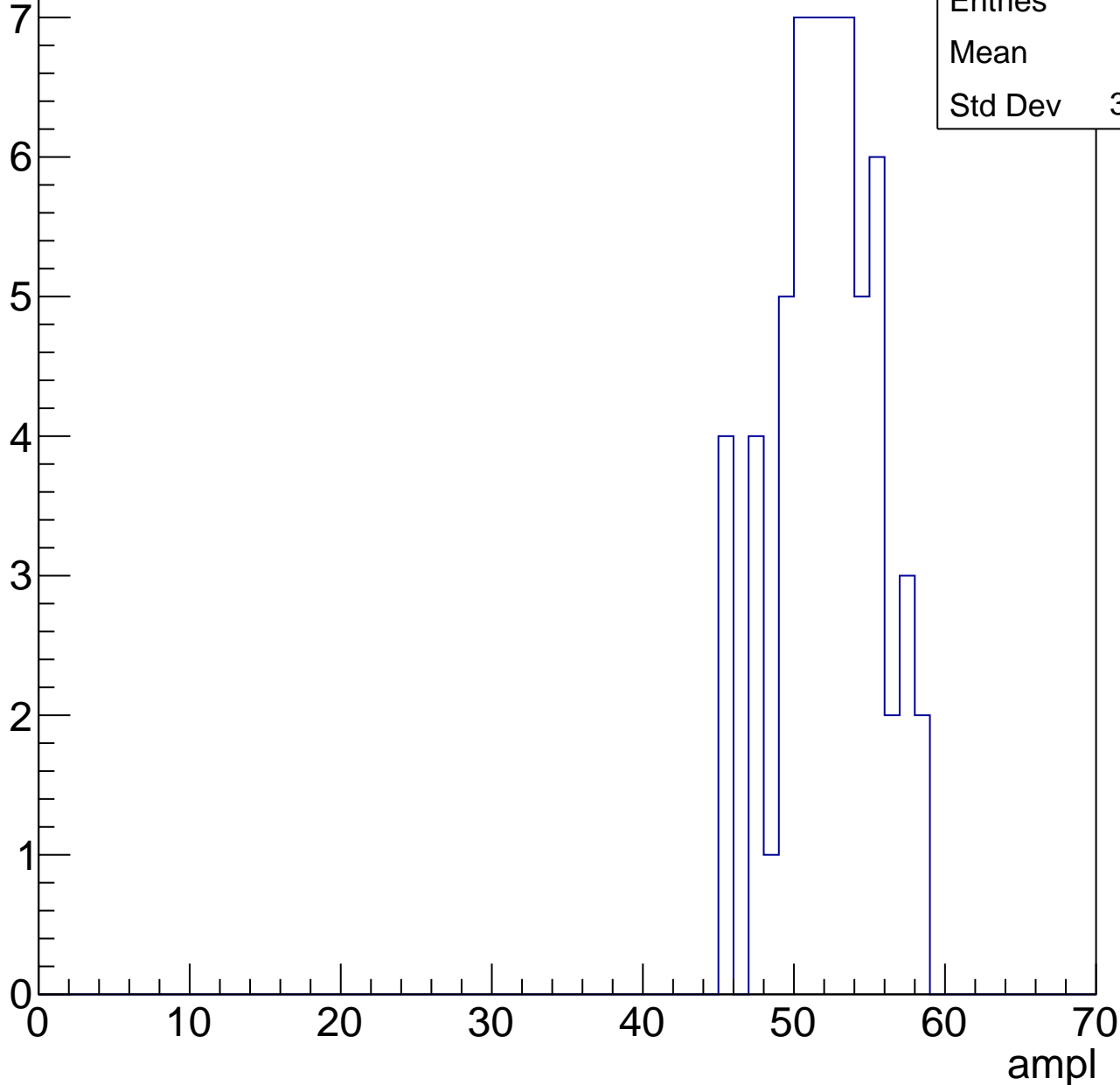


B1L100S, U11-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	51.7
Std Dev	3.283

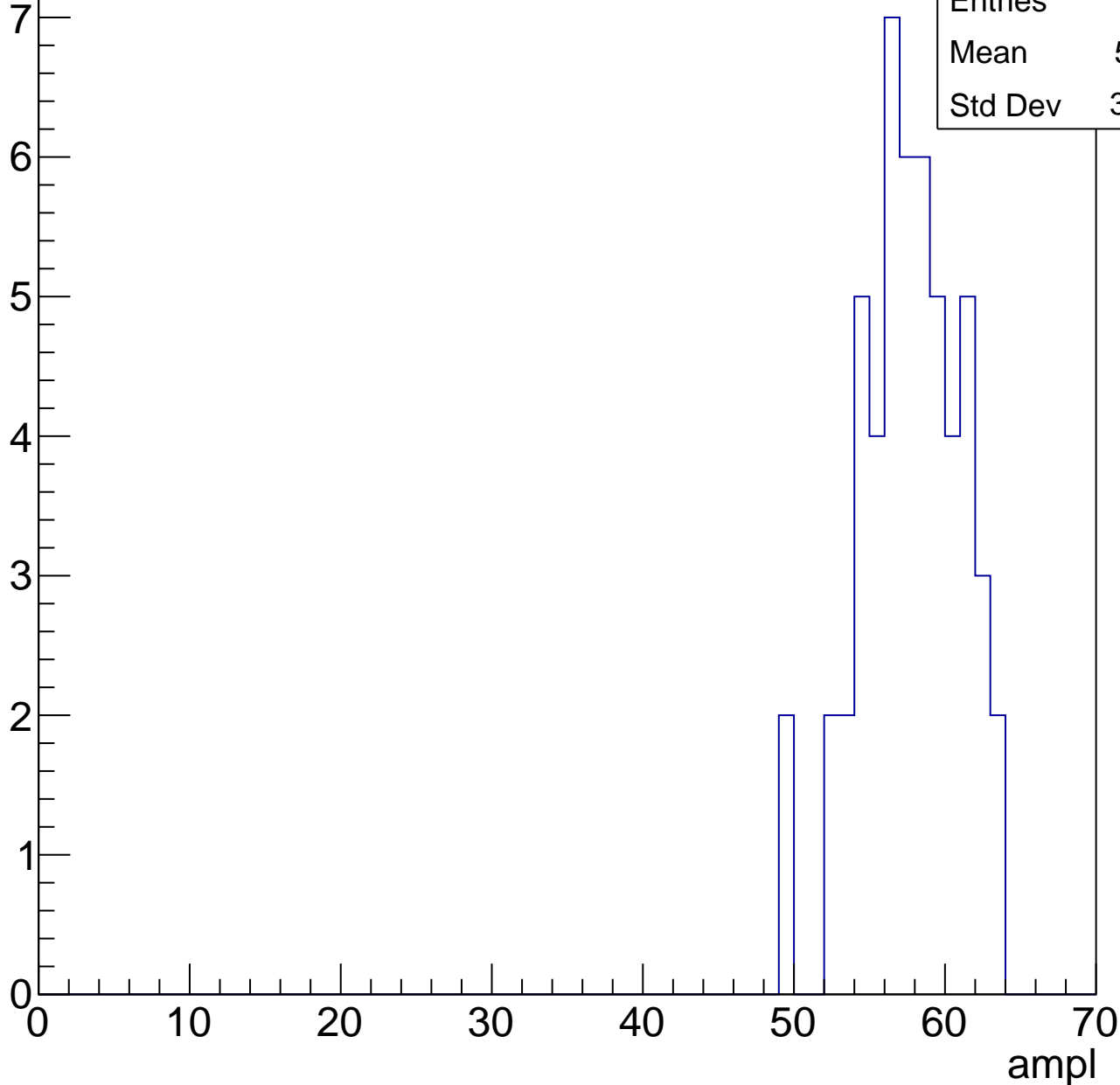


B1L100S, U11-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	57.21
Std Dev	3.264

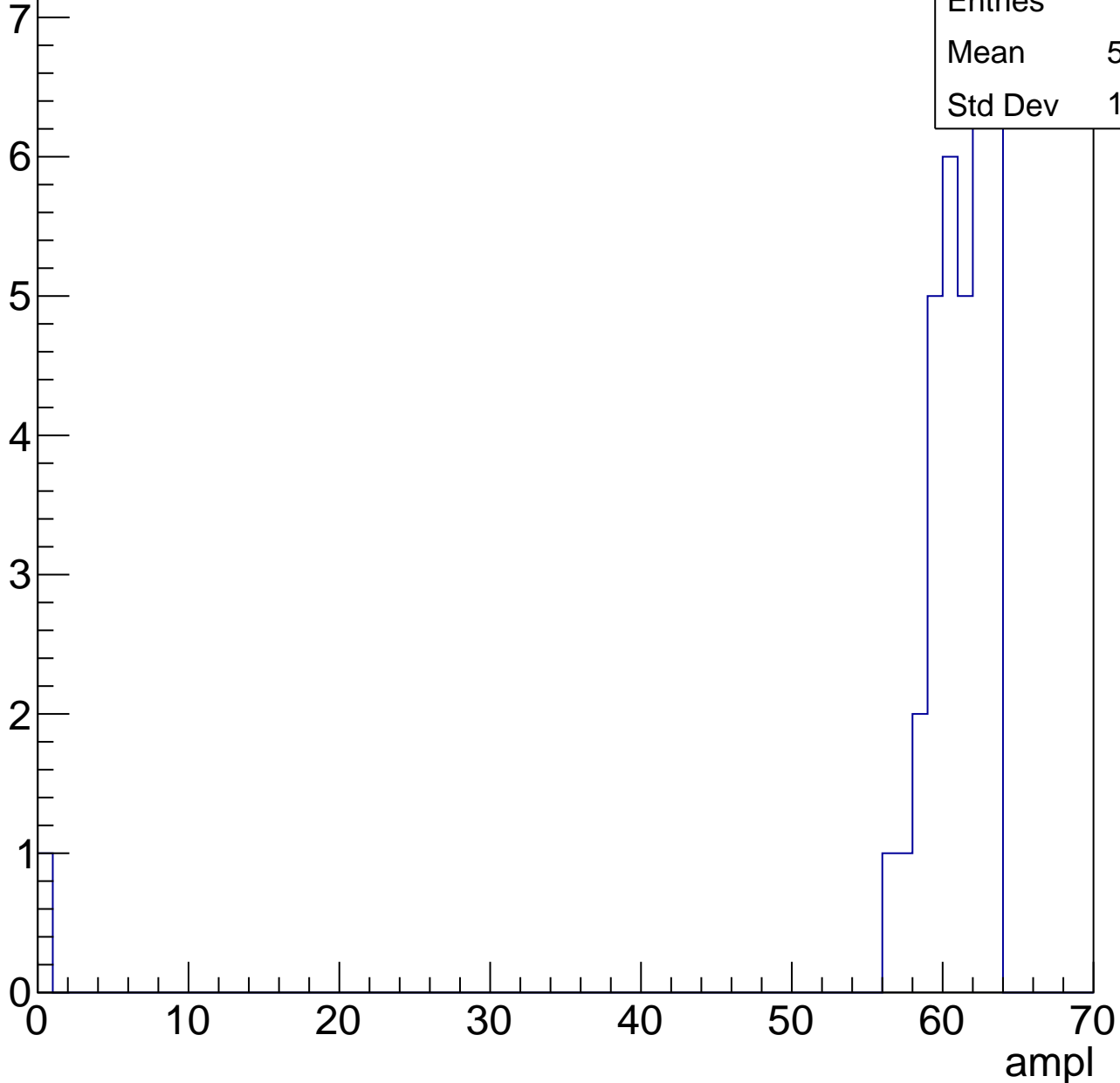


B1L100S, U11-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	58.97
Std Dev	10.28



B1L100S, U11-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch115, adc0

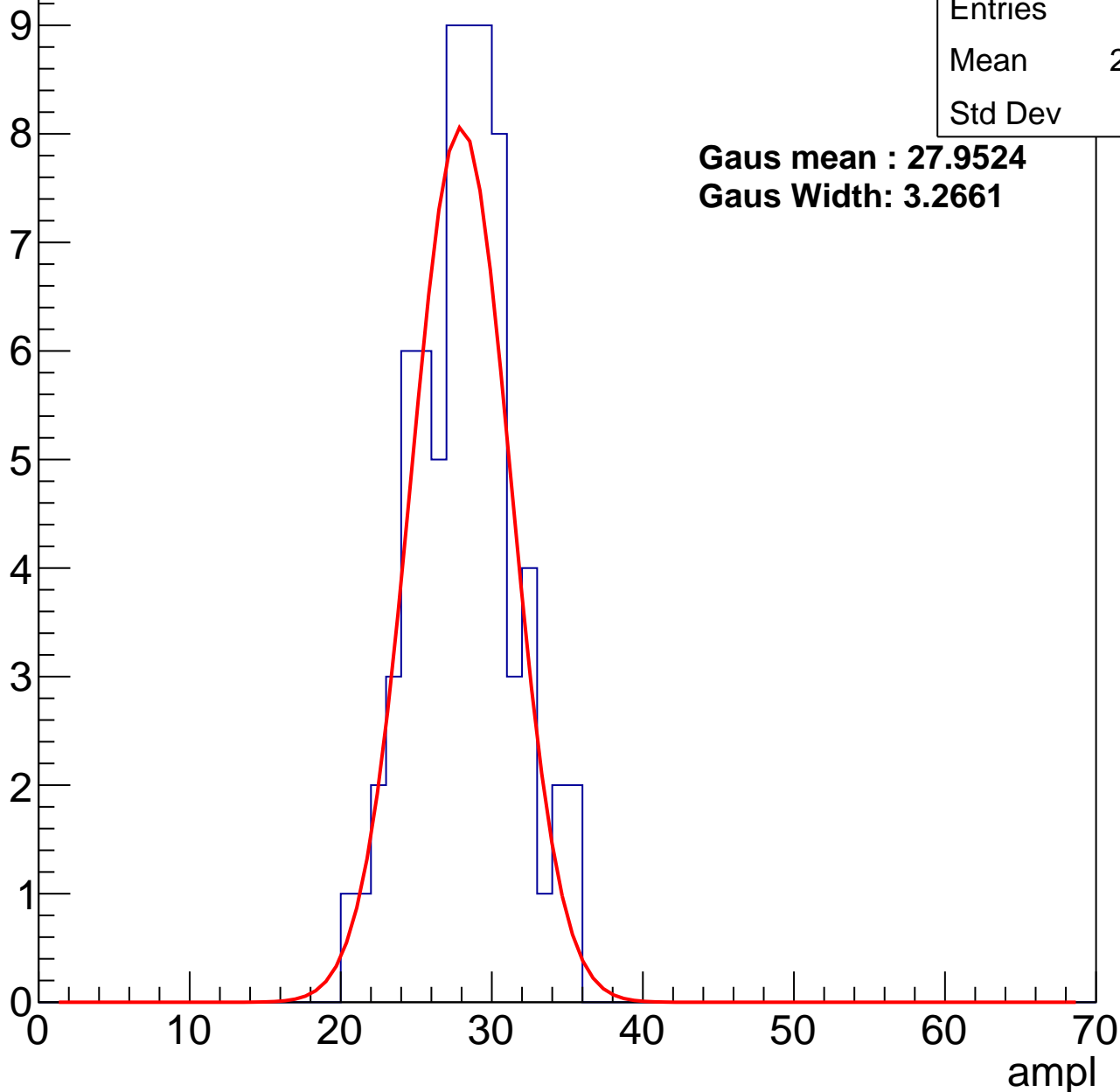
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	27.69
Std Dev	3.27

Gaus mean : 27.9524

Gaus Width: 3.2661



B1L100S, U11-ch115, adc1

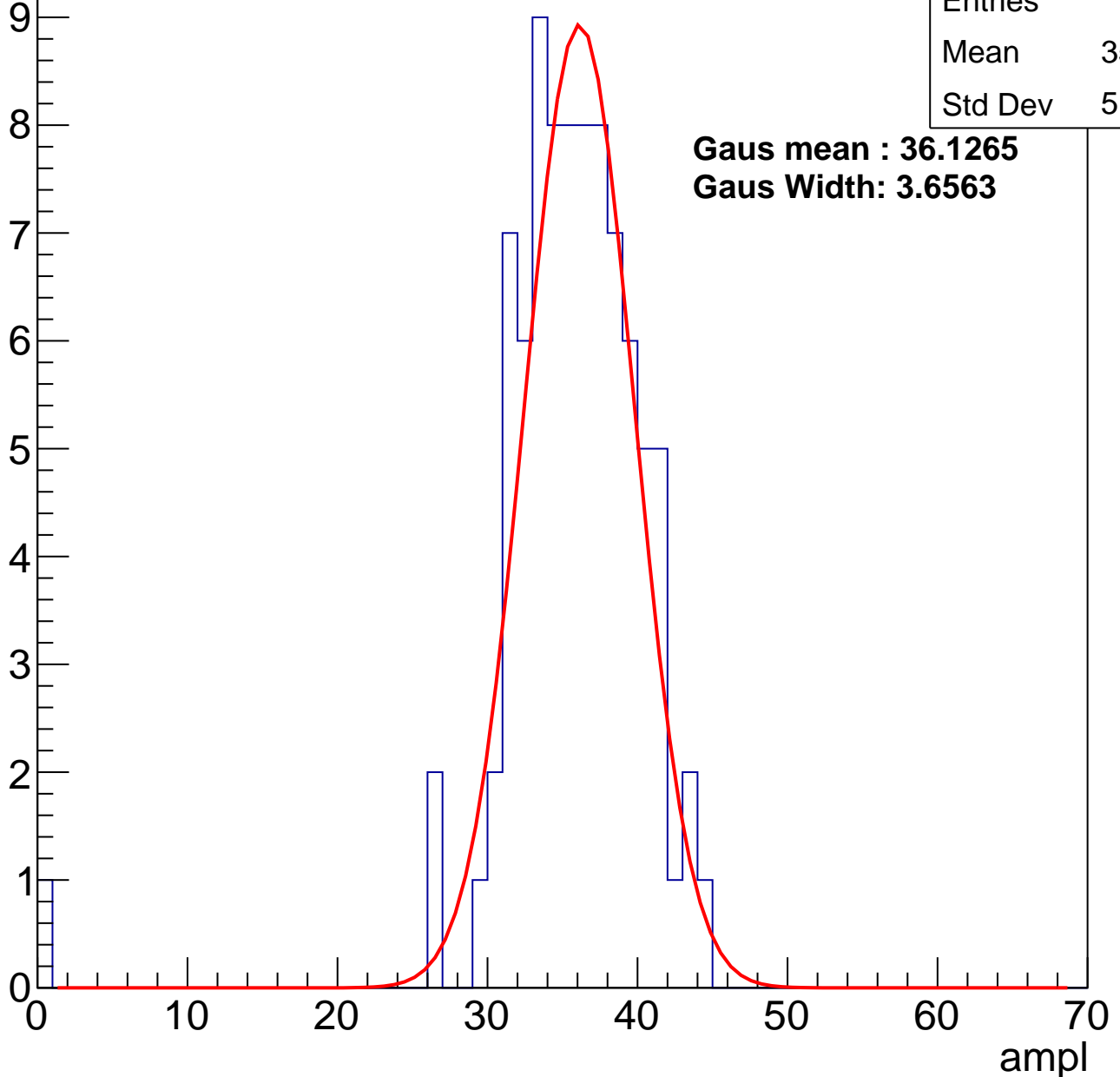
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	35.17
Std Dev	5.298

Gaus mean : 36.1265

Gaus Width: 3.6563



B1L100S, U11-ch115, adc2

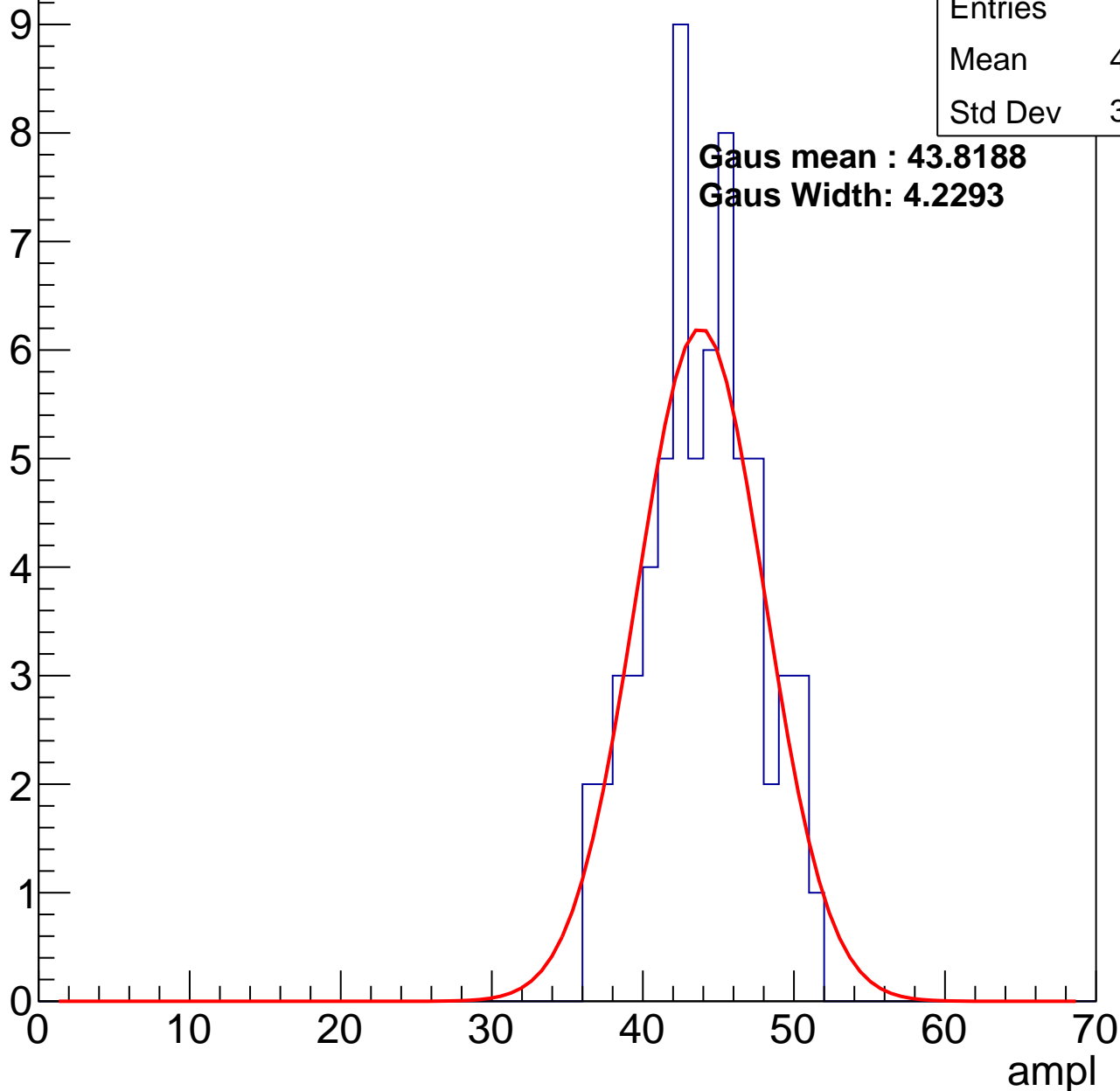
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.45
Std Dev	3.644

Gaus mean : 43.8188

Gaus Width: 4.2293

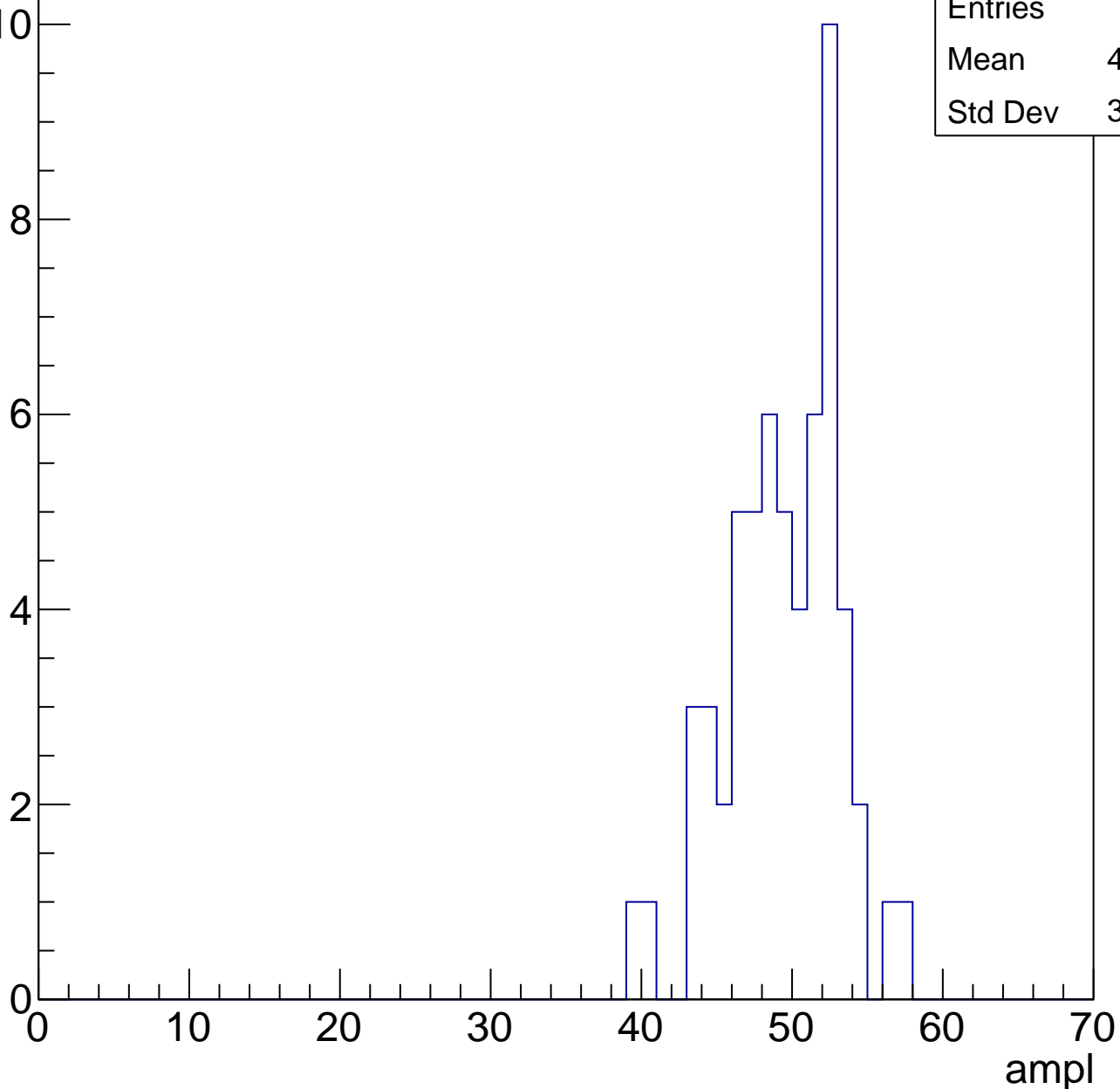


B1L100S, U11-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	48.93
Std Dev	3.709

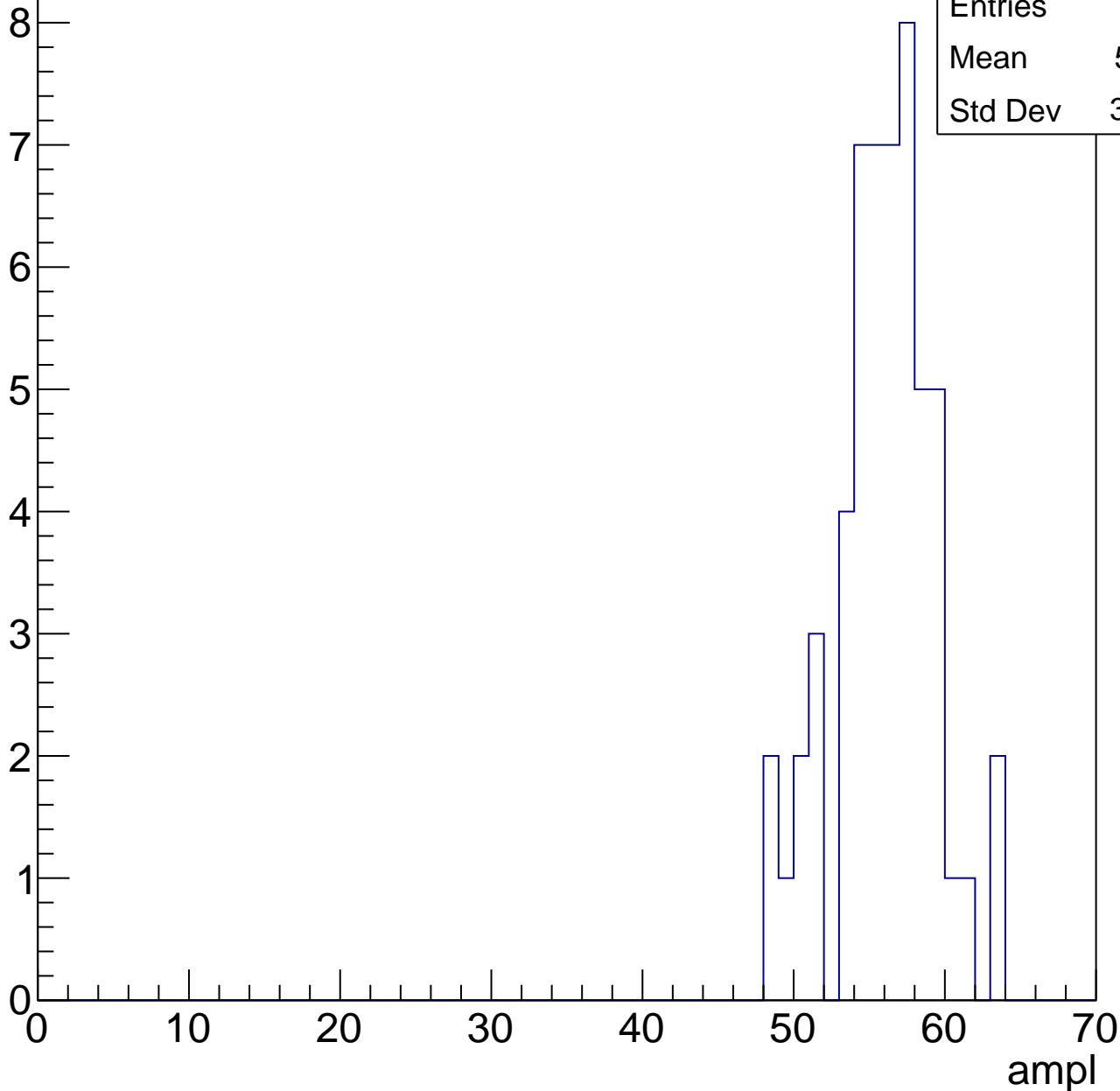


B1L100S, U11-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	55.51
Std Dev	3.286



B1L100S, U11-ch115, adc5

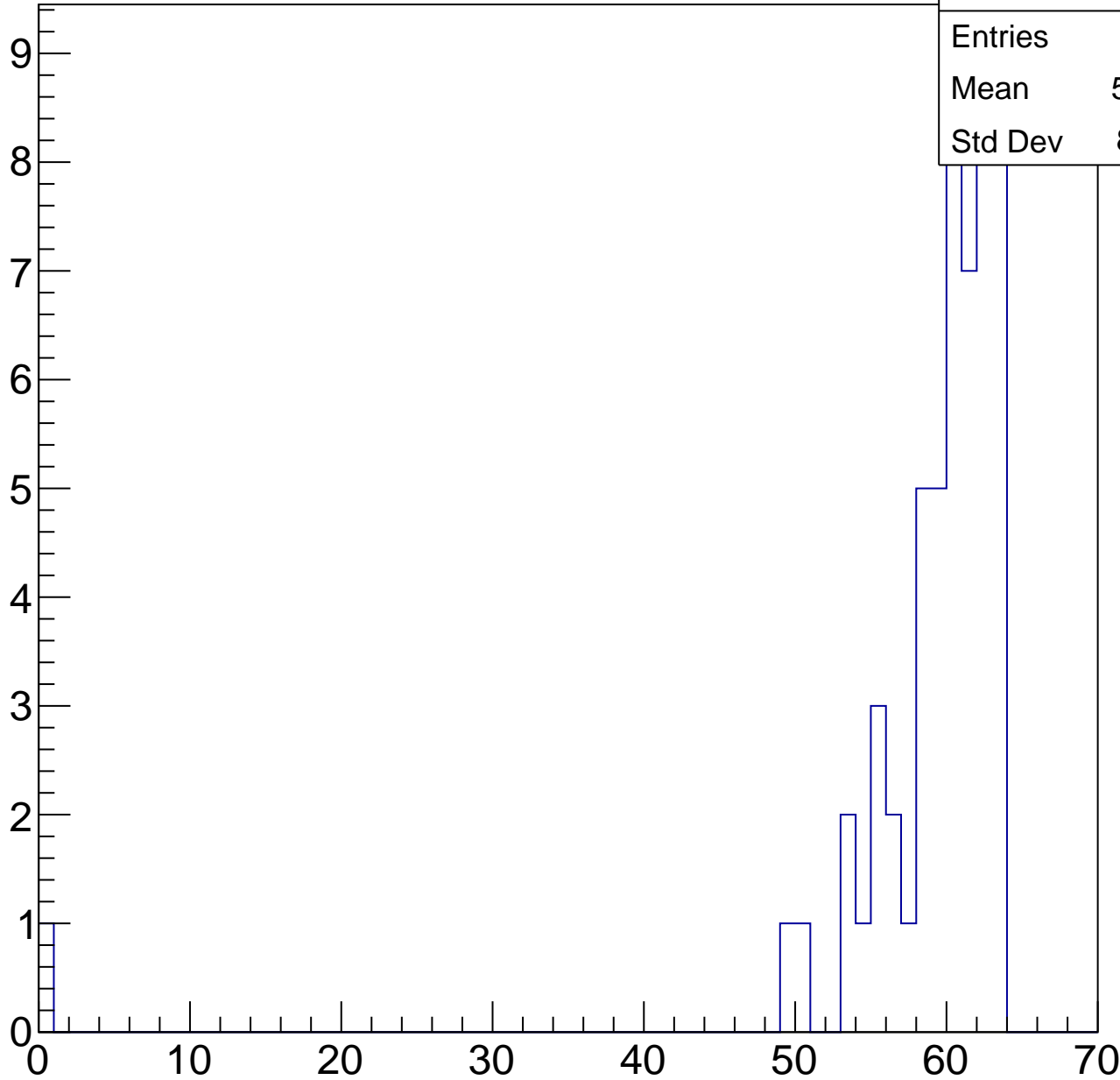
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.28
Std Dev	8.661

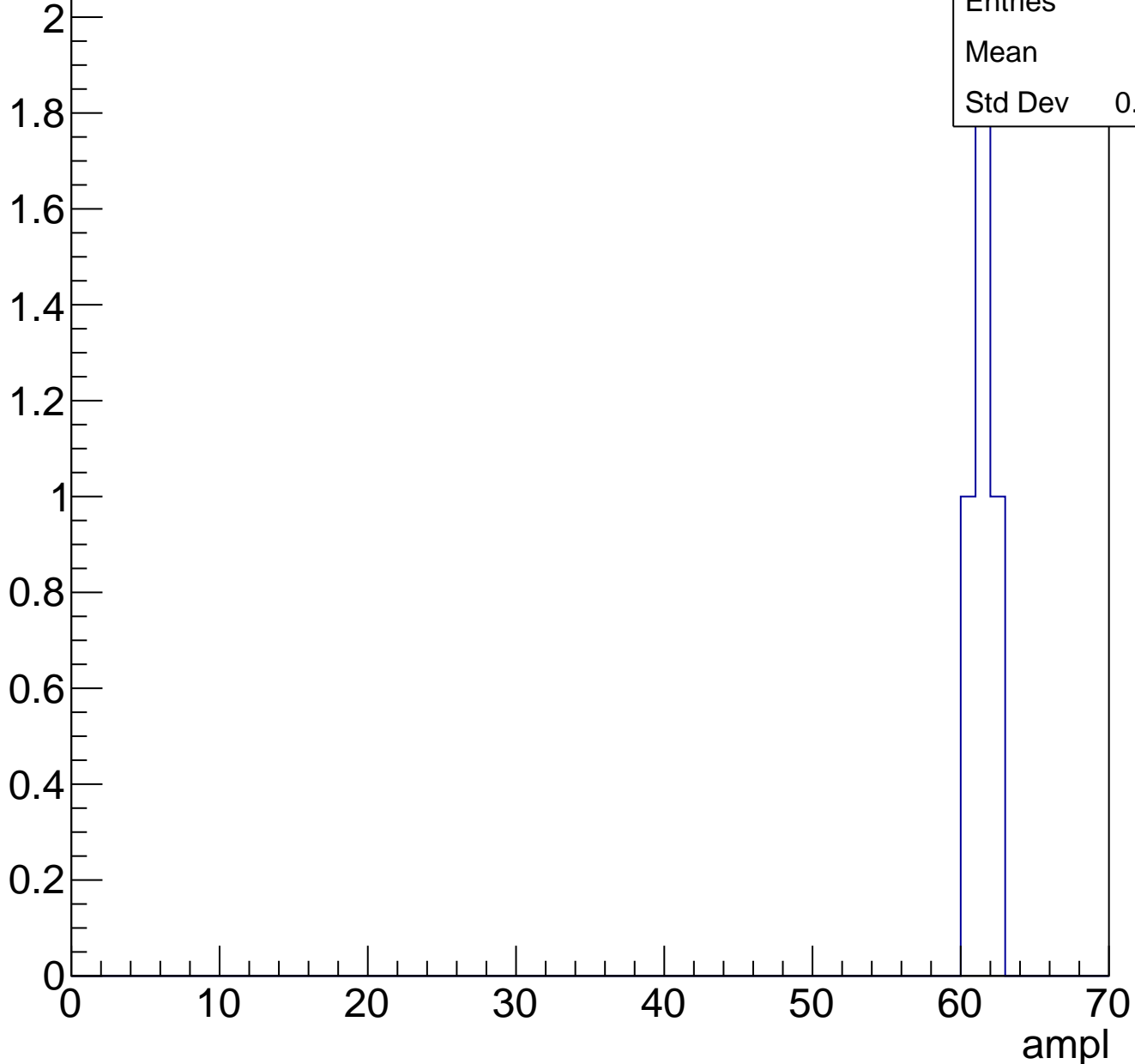
ampl



B1L100S, U11-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	61
Std Dev	0.7071

B1L100S, U11-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch116, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	29.57
Std Dev	3.637

Gaus mean : 29.8751

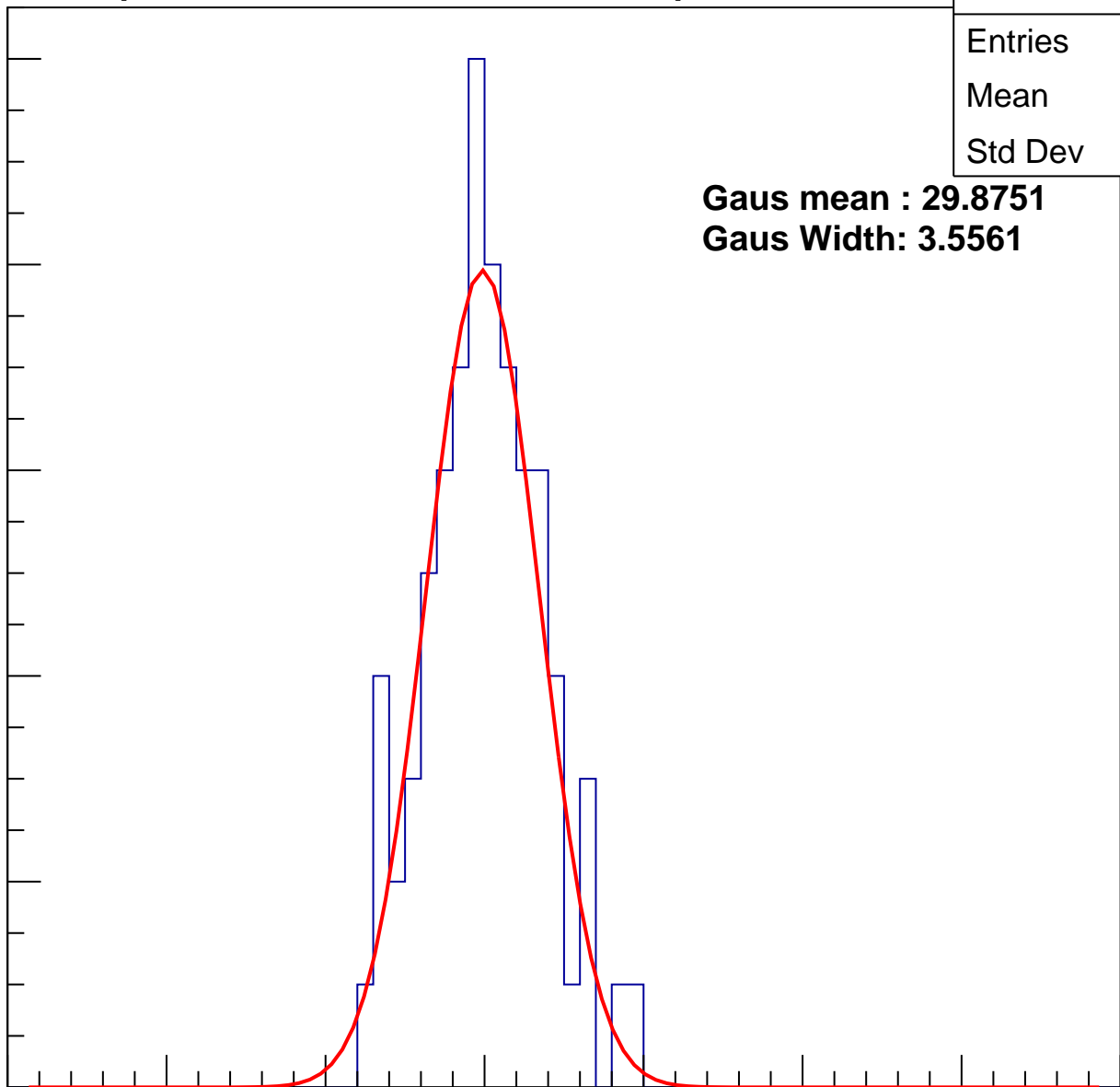
Gaus Width: 3.5561

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch116, adc1

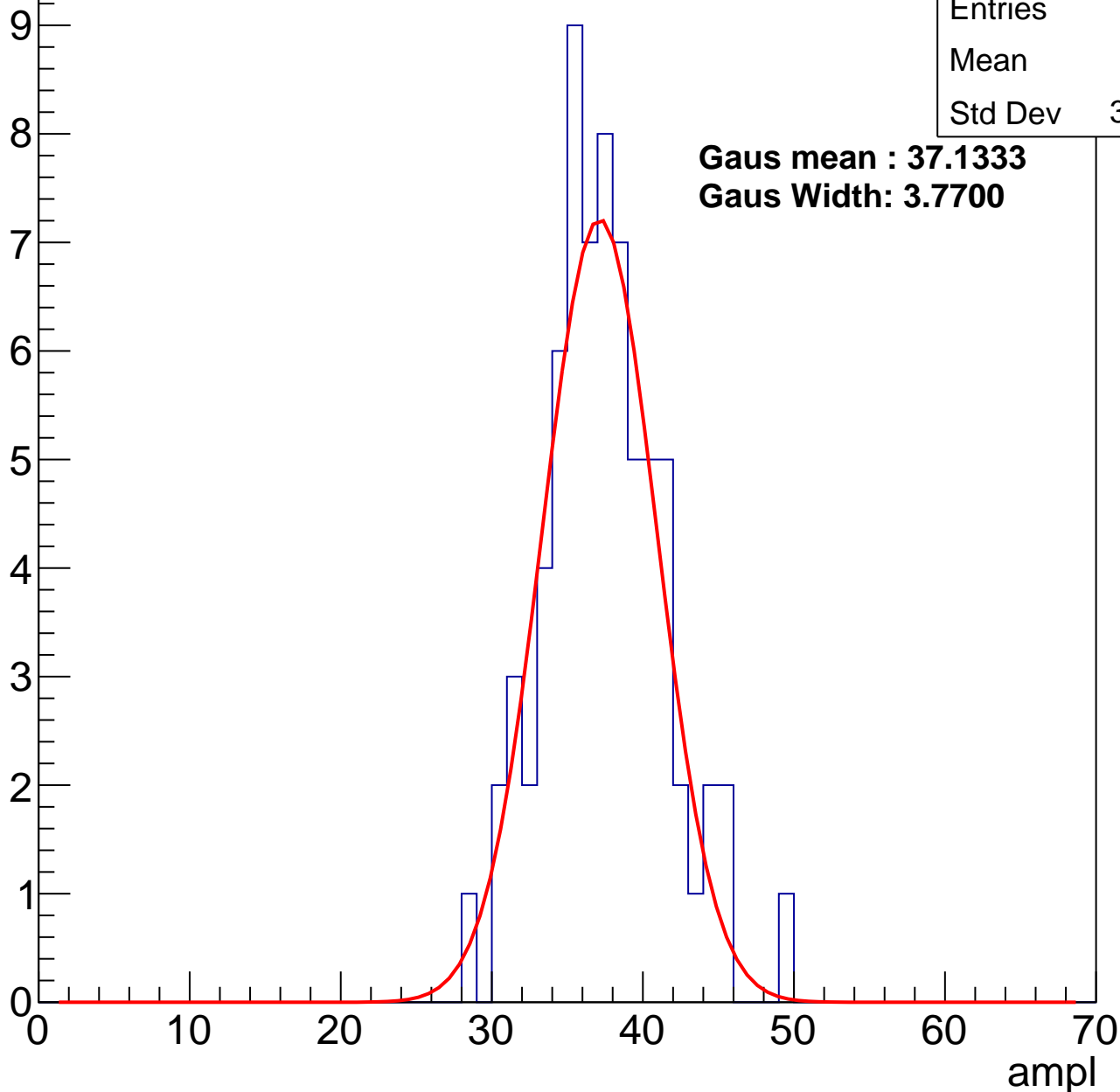
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37
Std Dev	3.919

Gaus mean : 37.1333

Gaus Width: 3.7700



B1L100S, U11-ch116, adc2

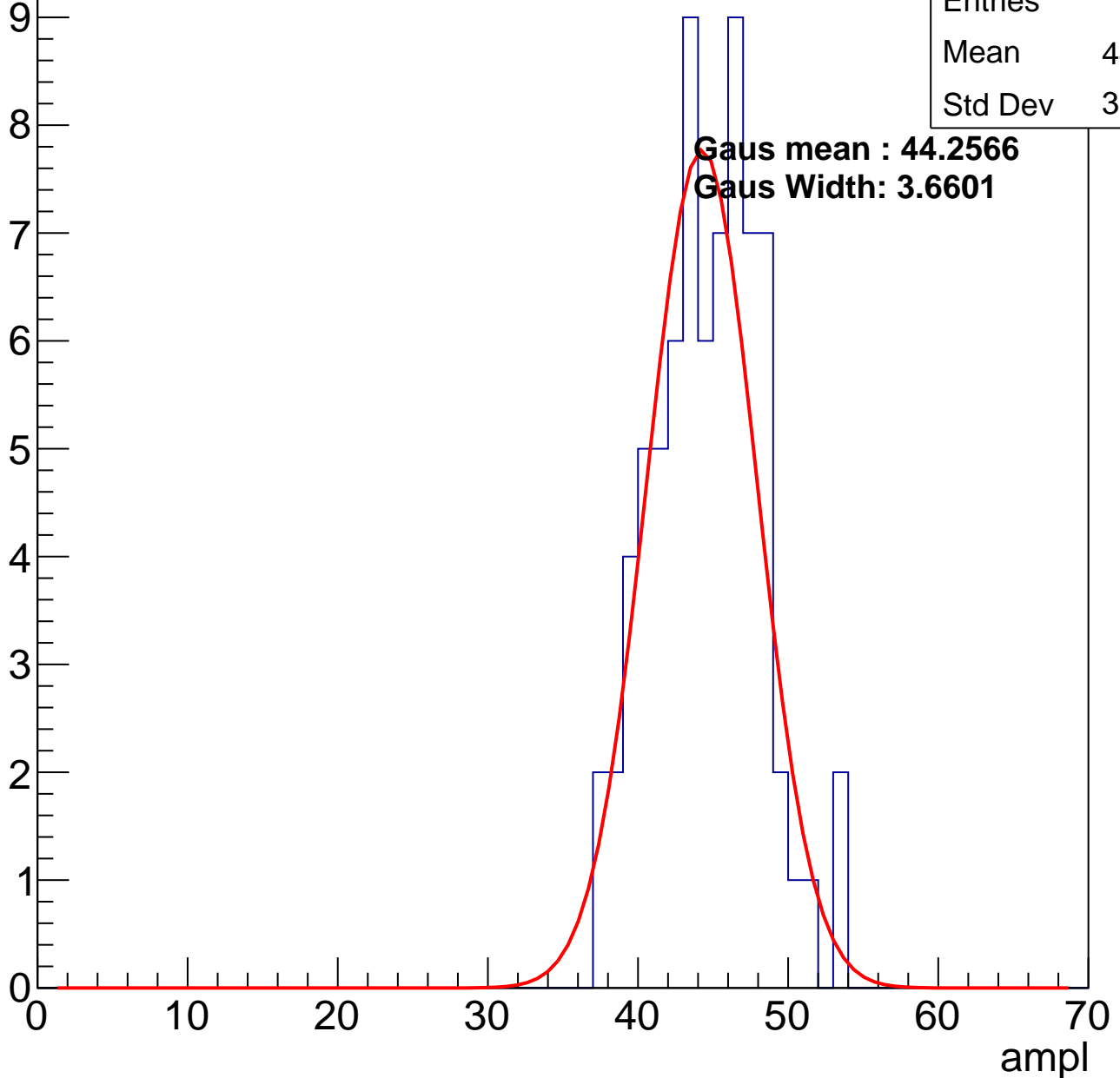
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	44.17
Std Dev	3.542

Gaus mean : 44.2566

Gaus Width: 3.6601

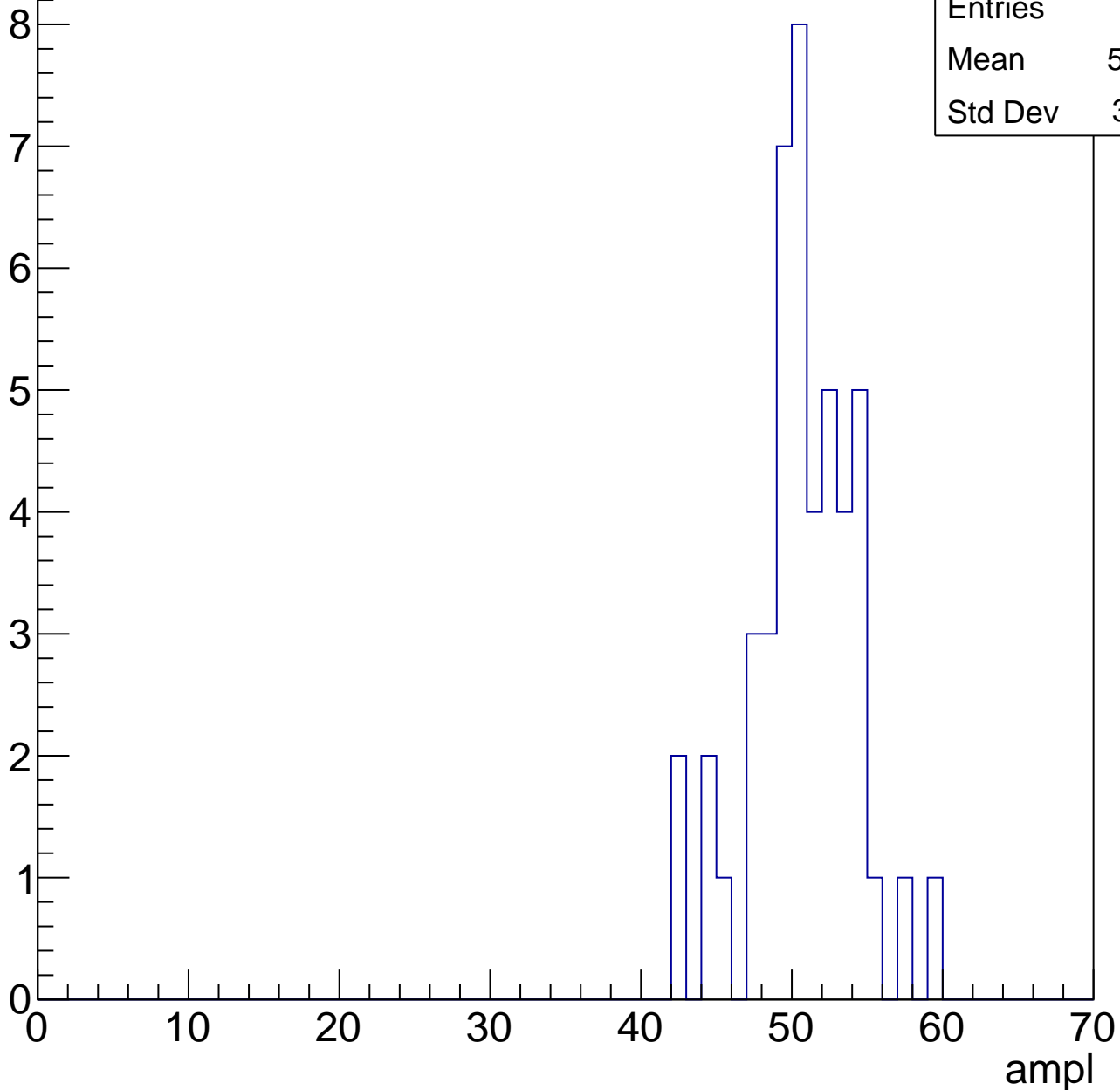


B1L100S, U11-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	50.26
Std Dev	3.461



B1L100S, U11-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

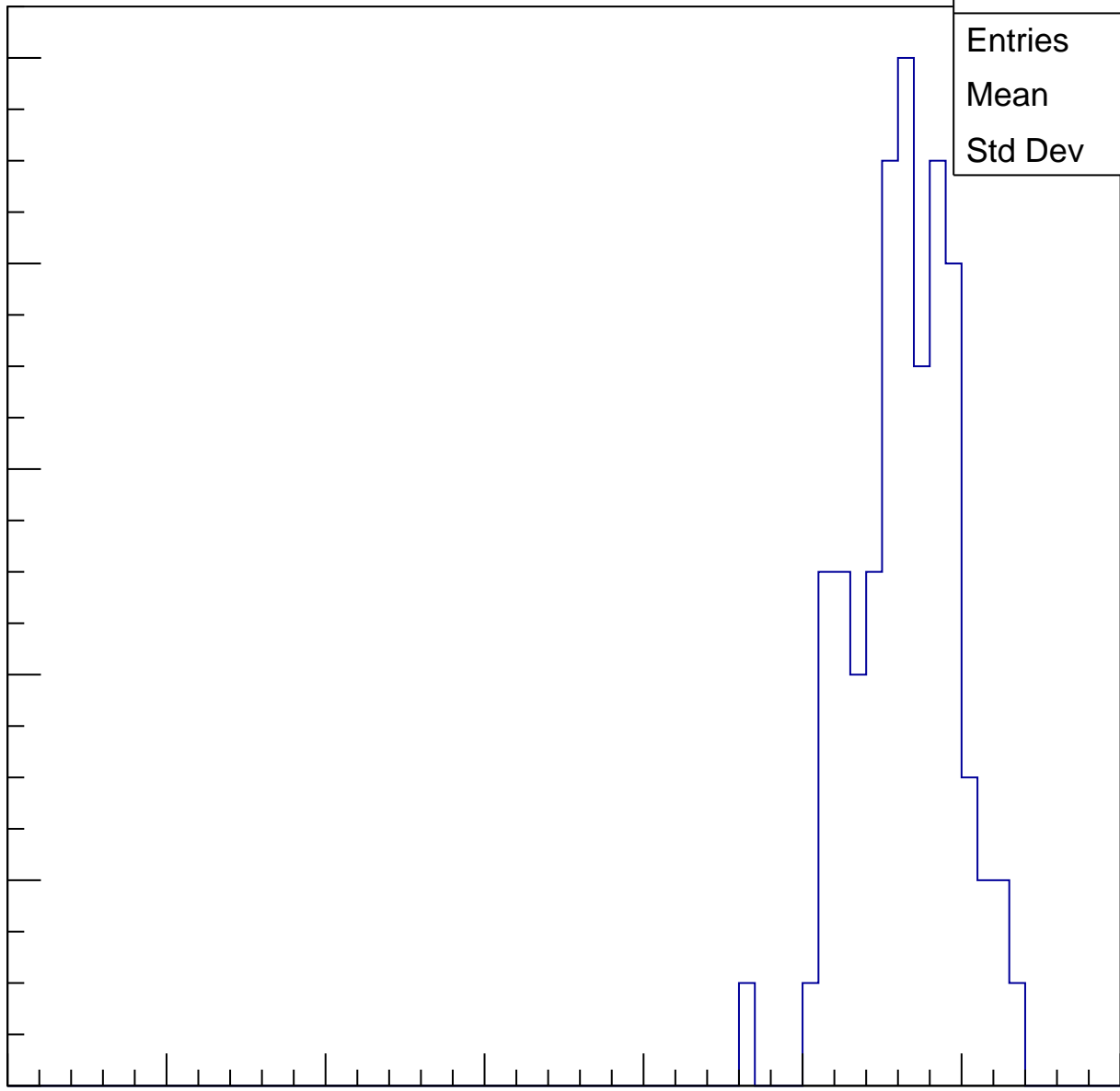
Entries	72
Mean	55.97
Std Dev	3.206

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

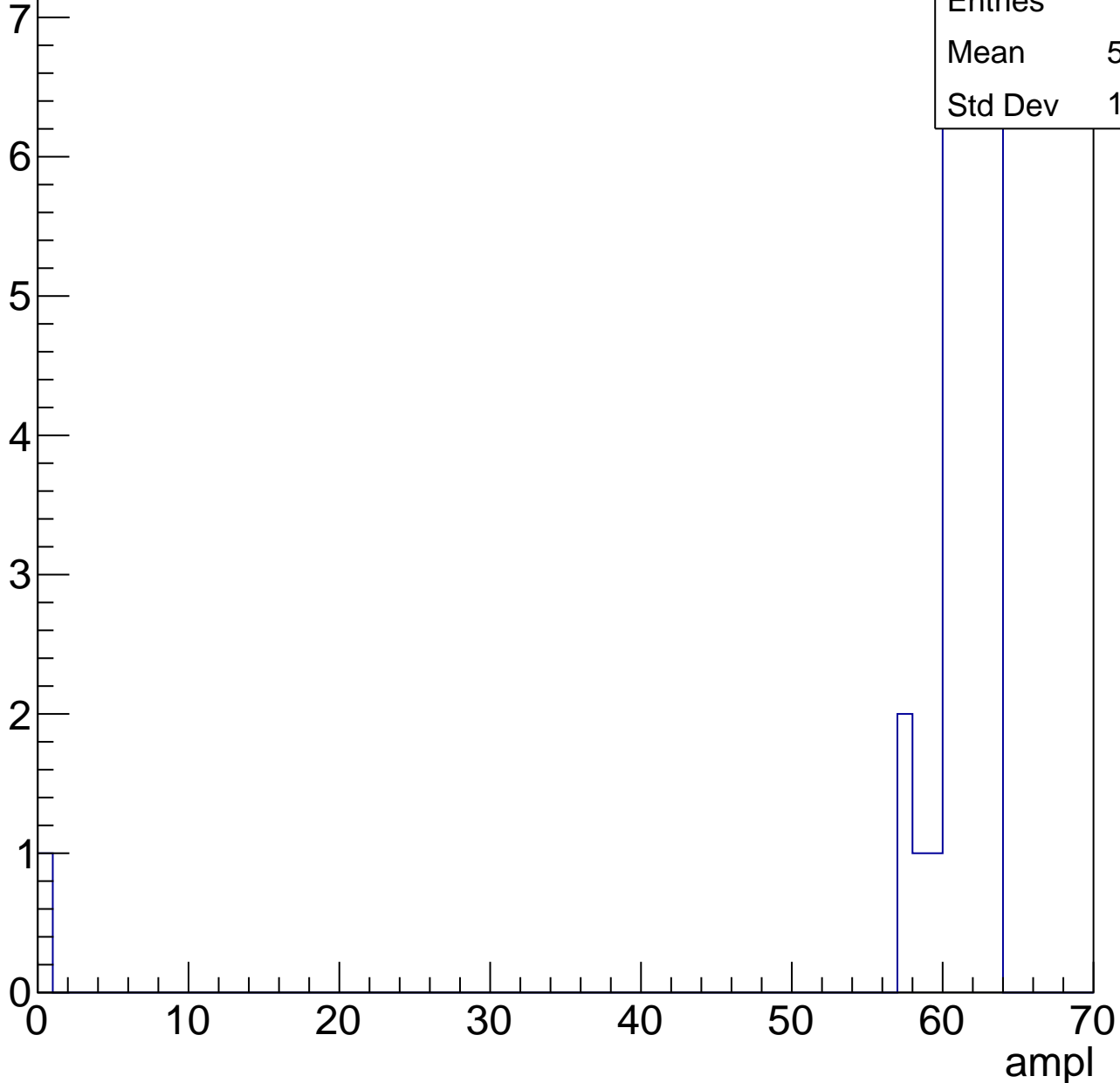


B1L100S, U11-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	59.18
Std Dev	10.59



B1L100S, U11-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U11-ch117, adc0

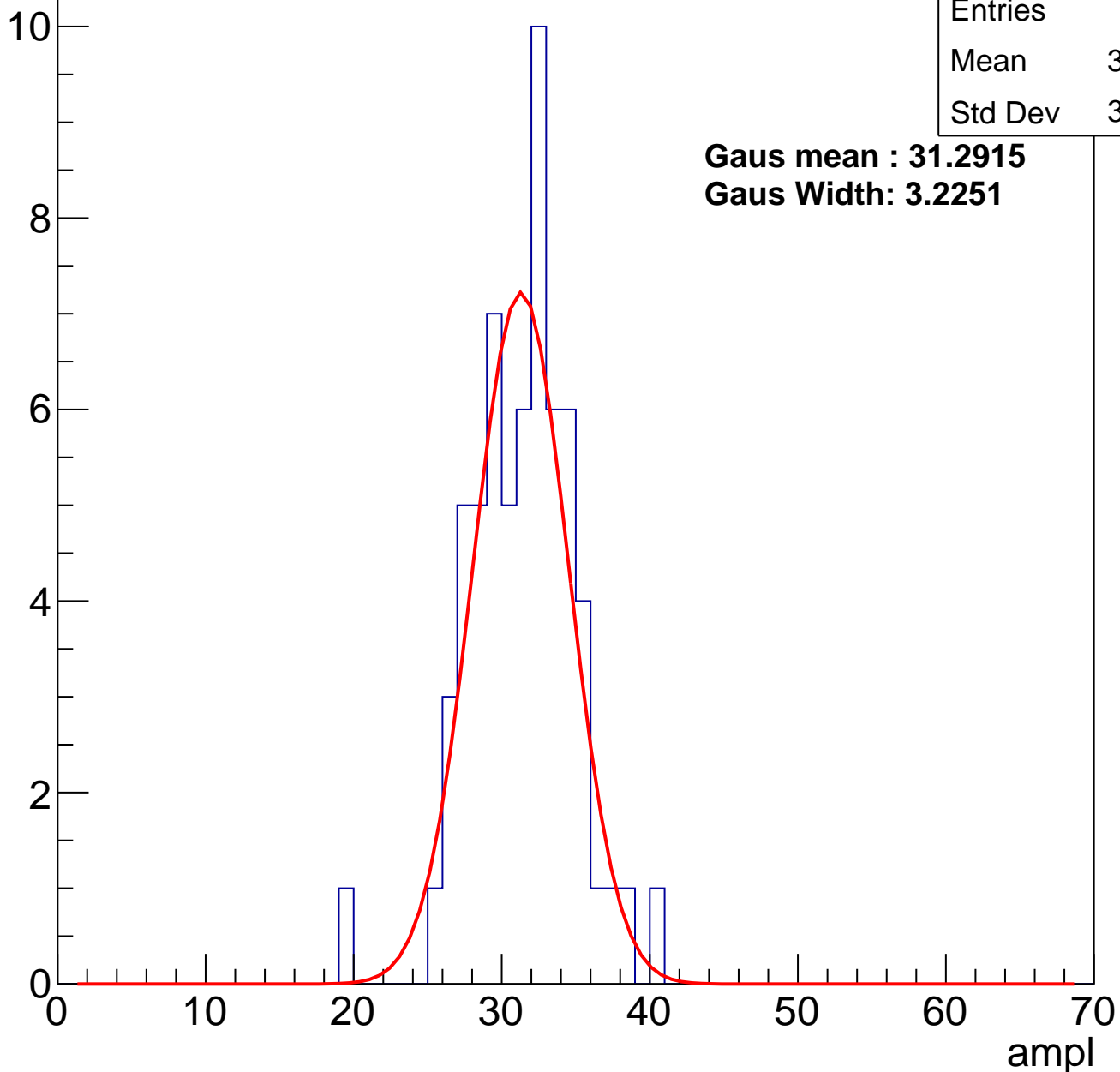
calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	30.94
Std Dev	3.464

Gaus mean : 31.2915

Gaus Width: 3.2251

Entry



B1L100S, U11-ch117, adc1

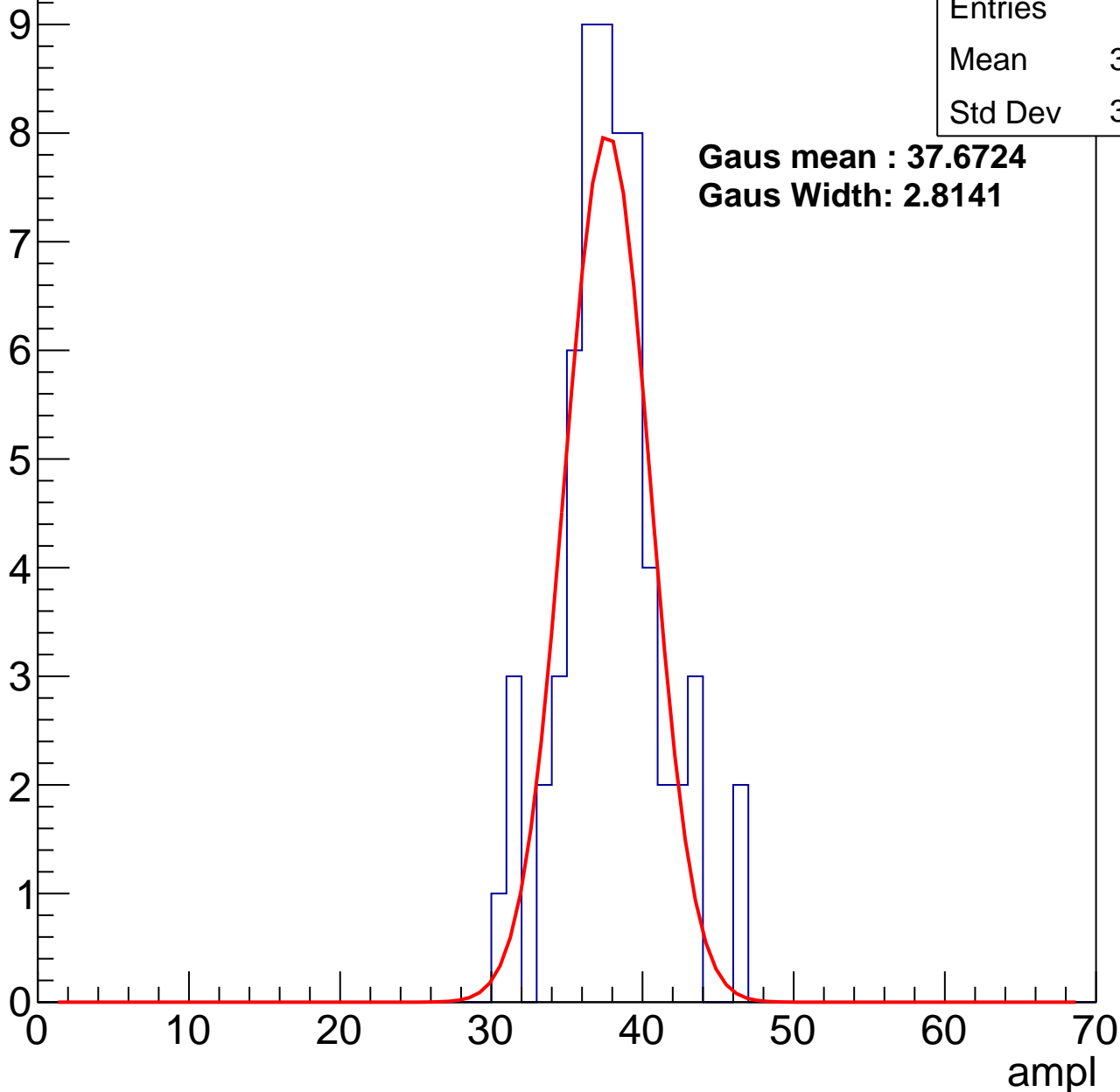
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	37.44
Std Dev	3.276

Gaus mean : 37.6724

Gaus Width: 2.8141



B1L100S, U11-ch117, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	92
Mean	45.21
Std Dev	3.911

Gaus mean : 45.5984

Gaus Width: 4.0474

Entry

10

8

6

4

2

0

0

10

20

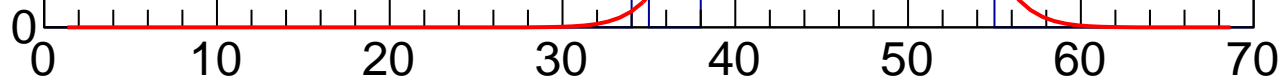
30

40

50

60

ampl

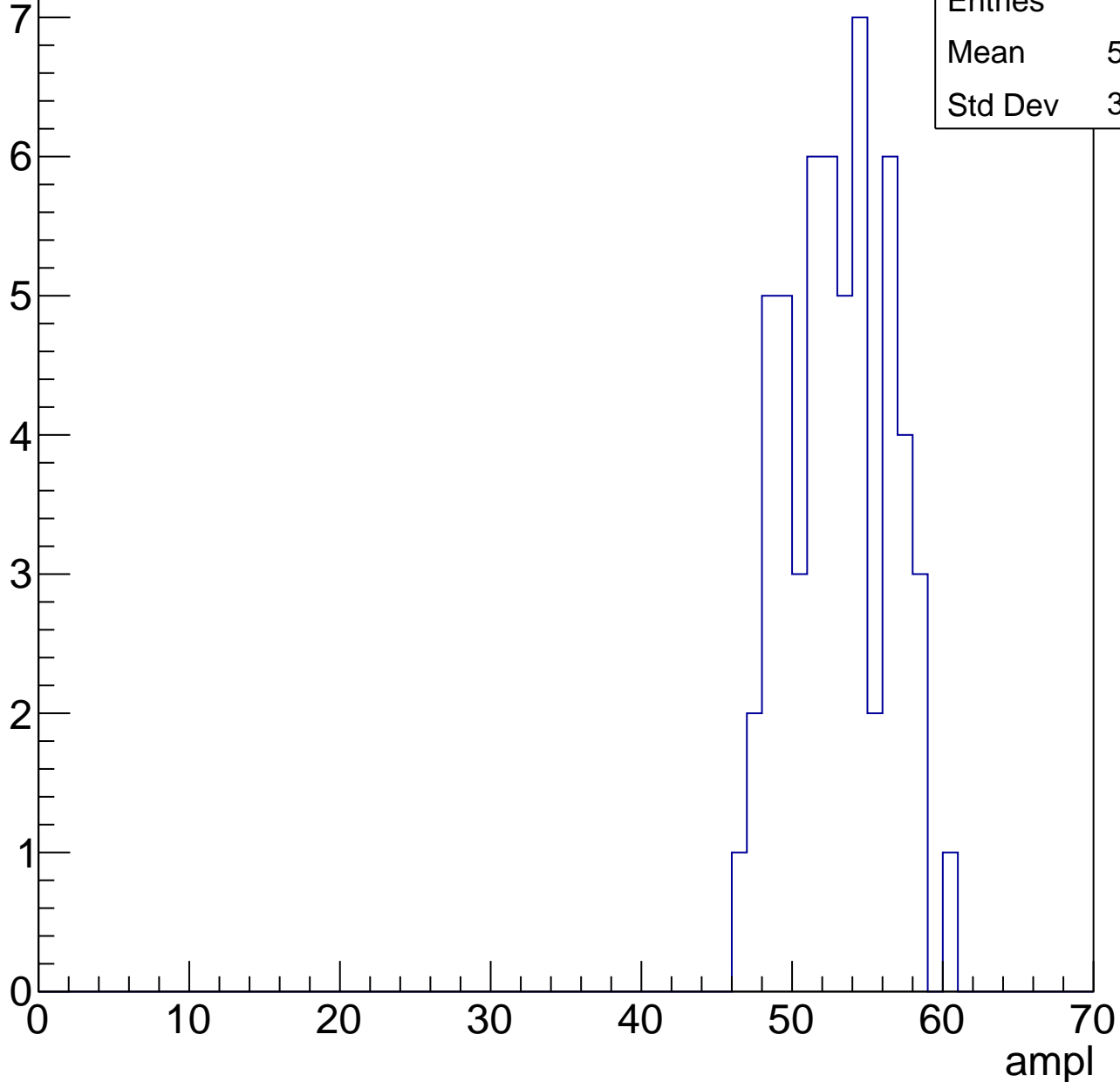


B1L100S, U11-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	52.57
Std Dev	3.353

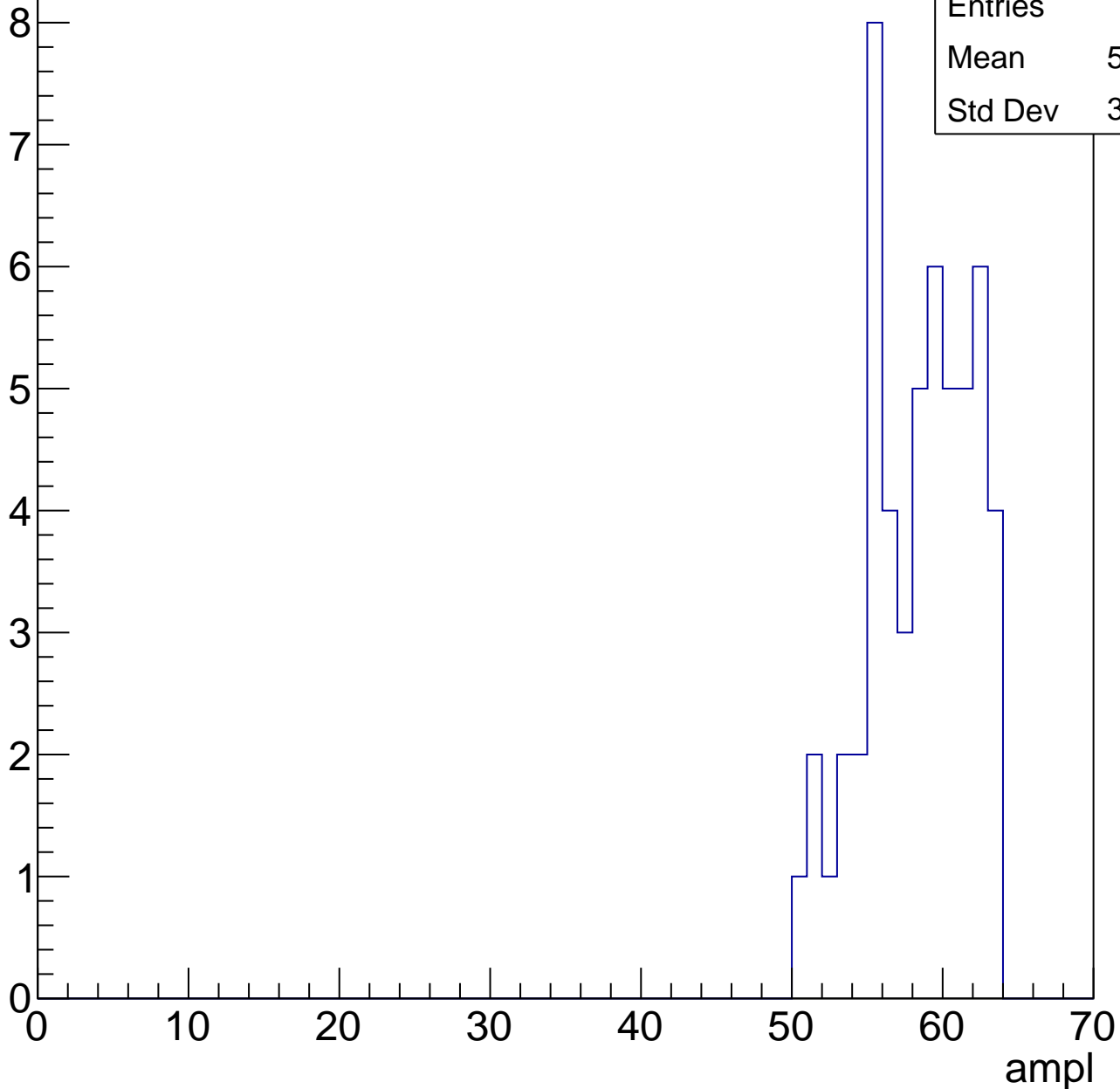


B1L100S, U11-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.89
Std Dev	3.435

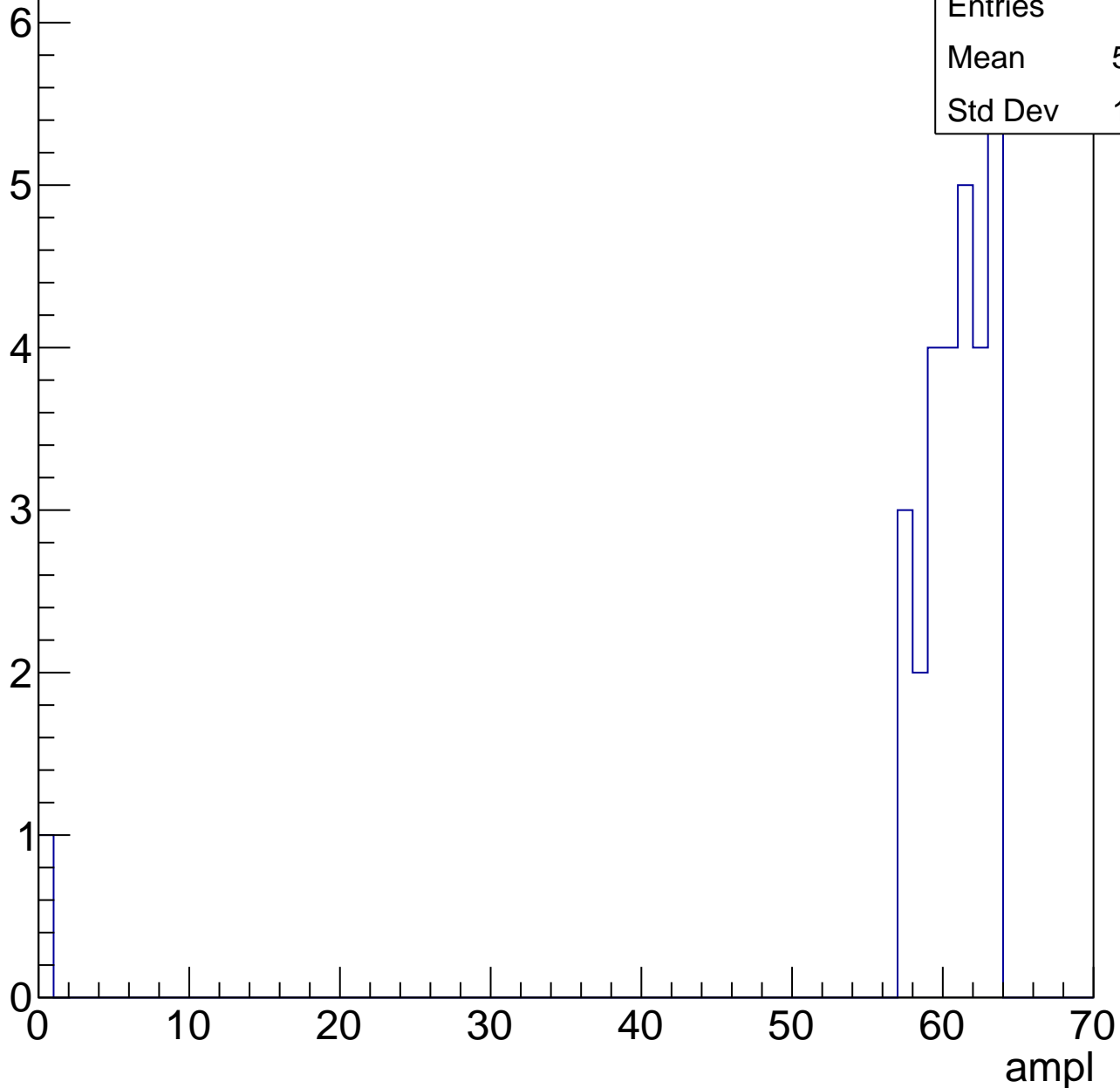


B1L100S, U11-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.41
Std Dev	11.21



B1L100S, U11-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch118, adc0

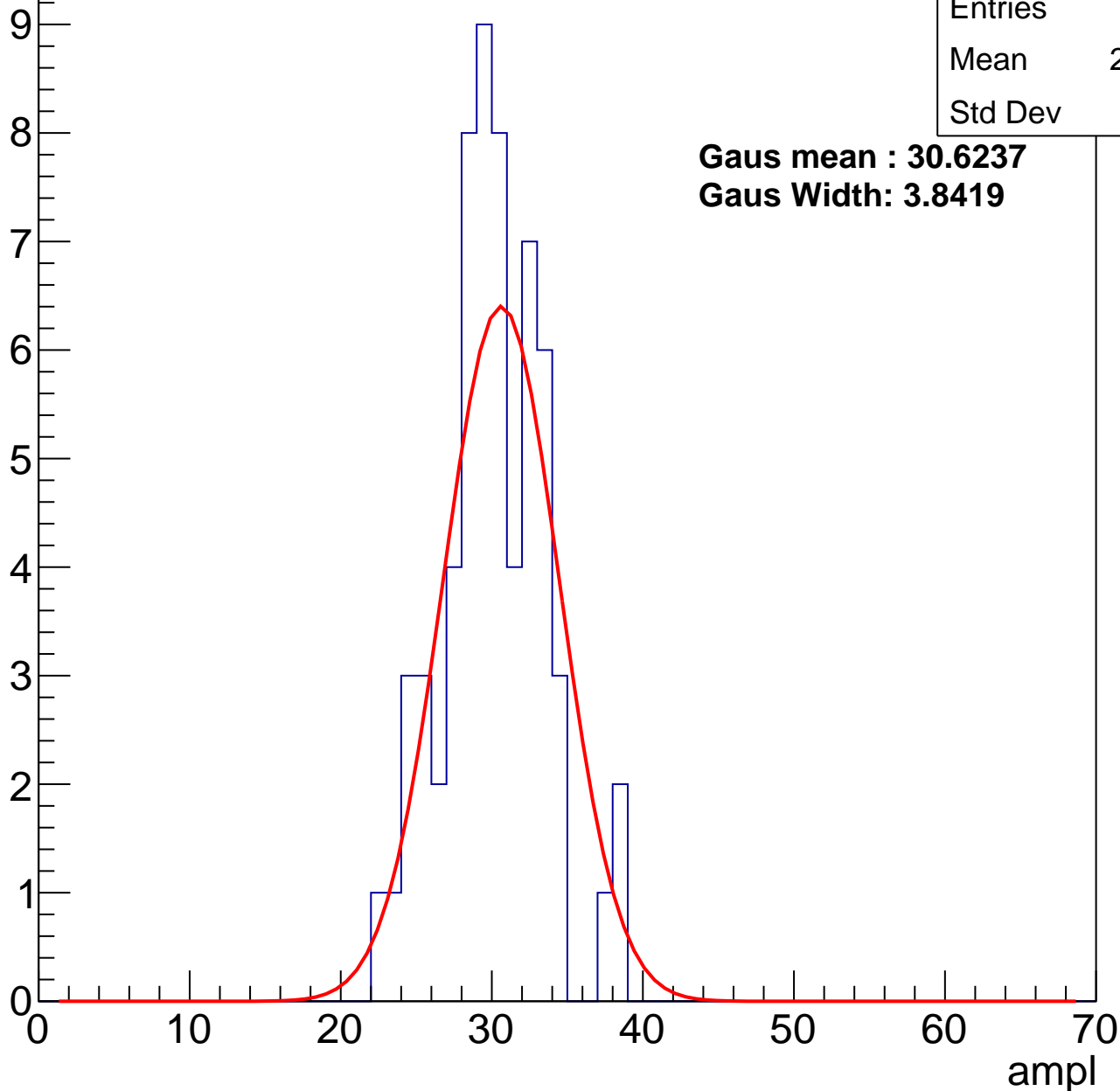
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	29.65
Std Dev	3.38

Gaus mean : 30.6237

Gaus Width: 3.8419



B1L100S, U11-ch118, adc1

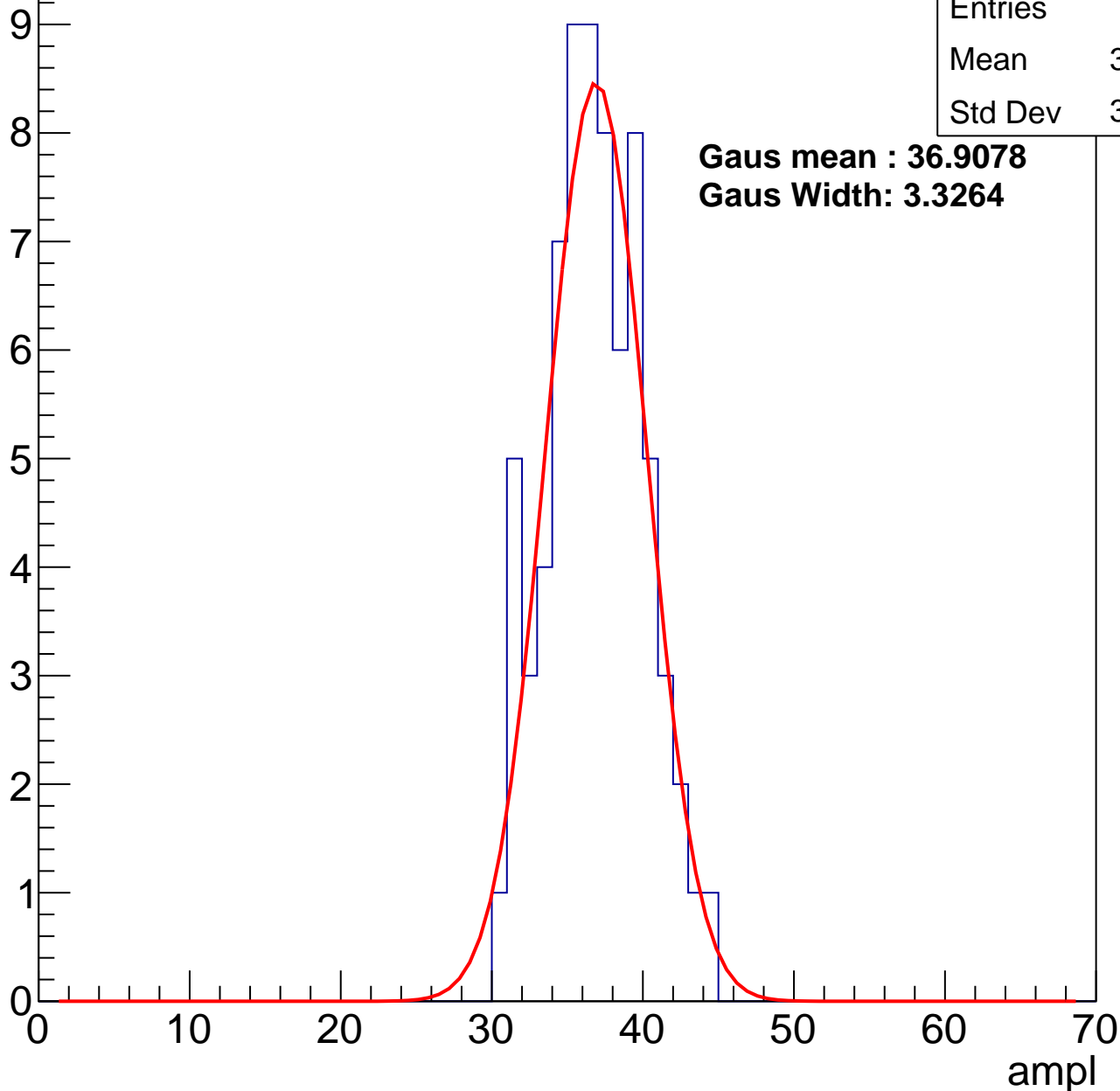
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	36.39
Std Dev	3.152

Gaus mean : 36.9078

Gaus Width: 3.3264



B1L100S, U11-ch118, adc2

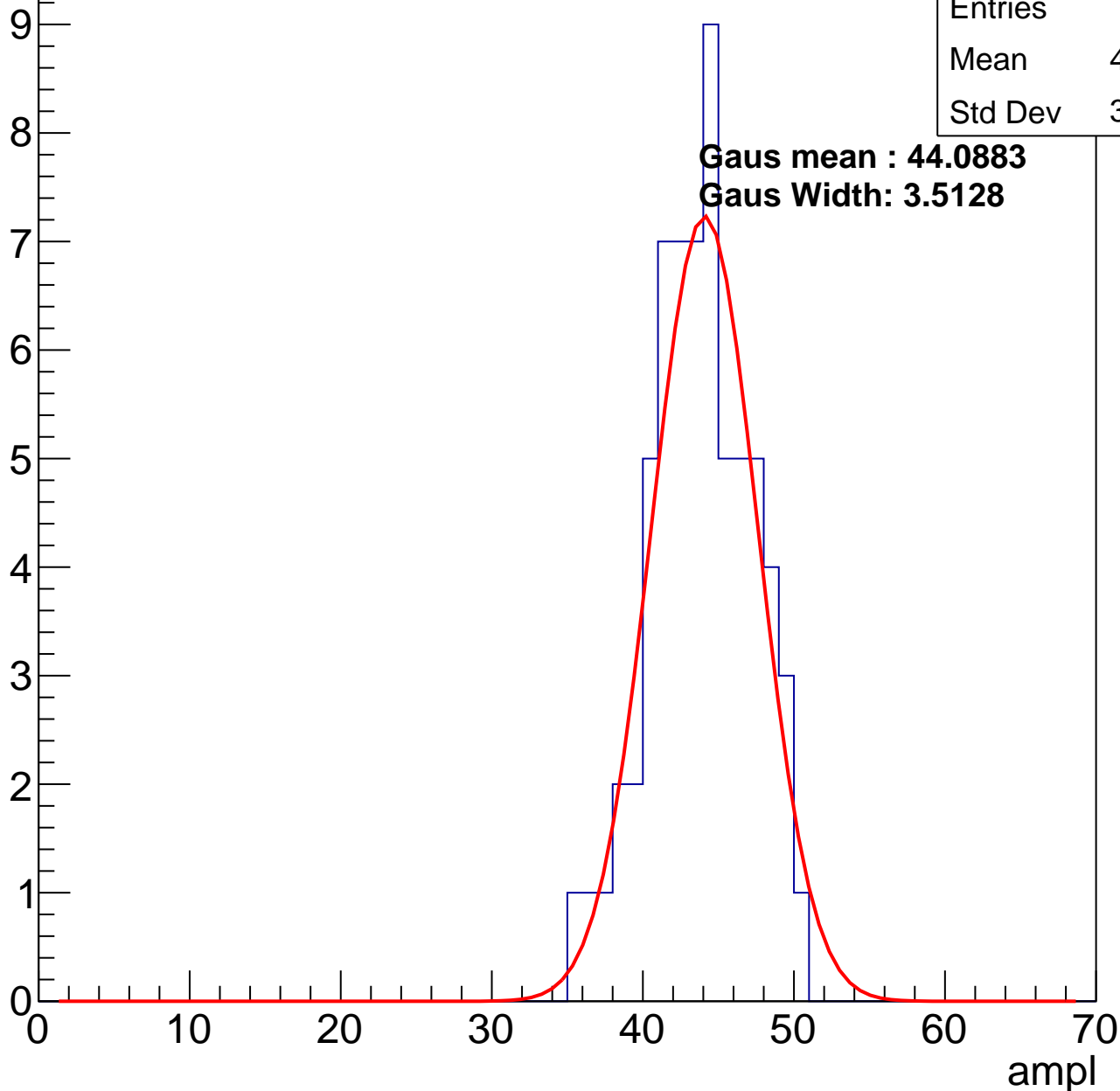
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.37
Std Dev	3.312

Gaus mean : 44.0883

Gaus Width: 3.5128

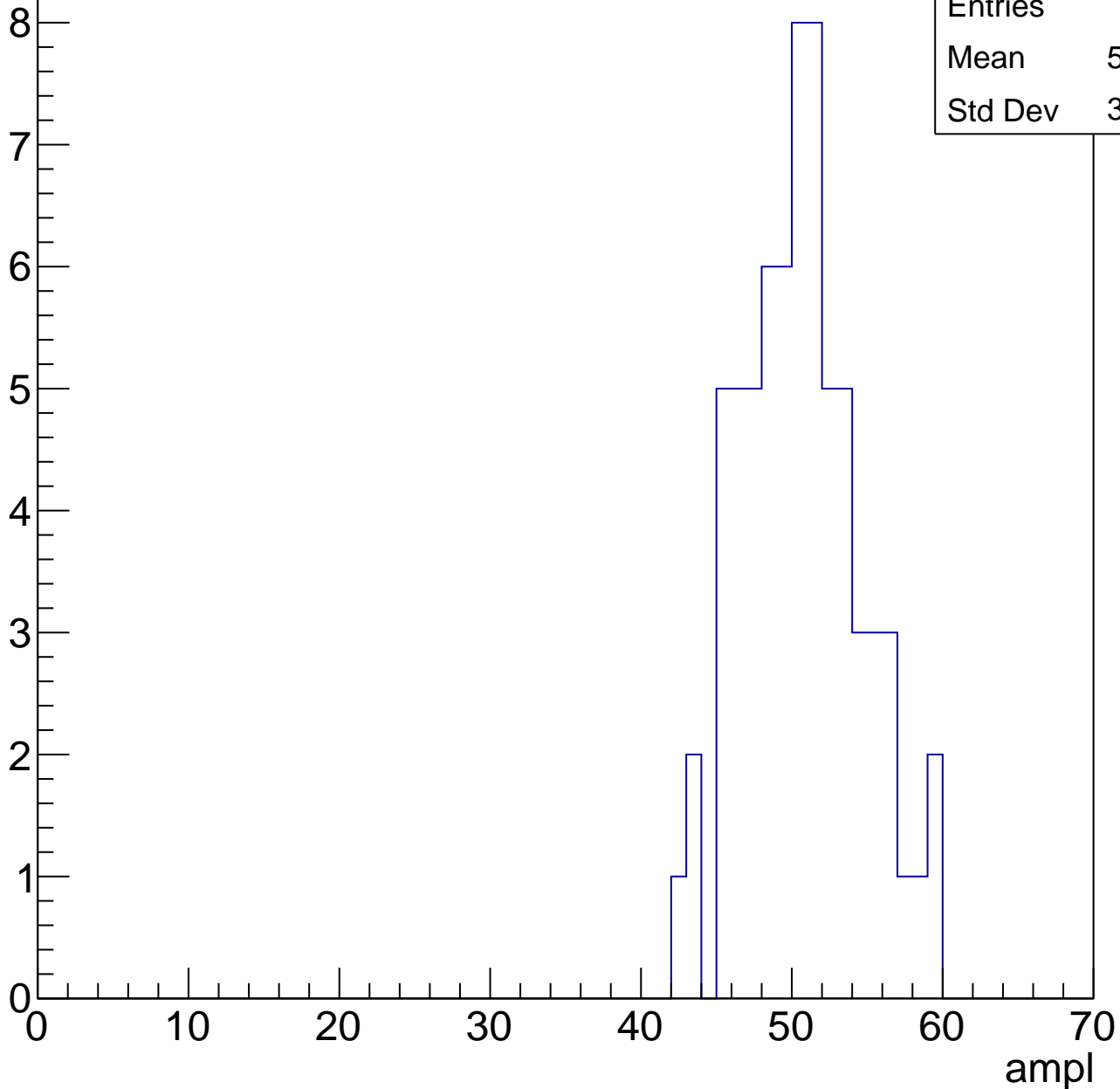


B1L100S, U11-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	50.16
Std Dev	3.843

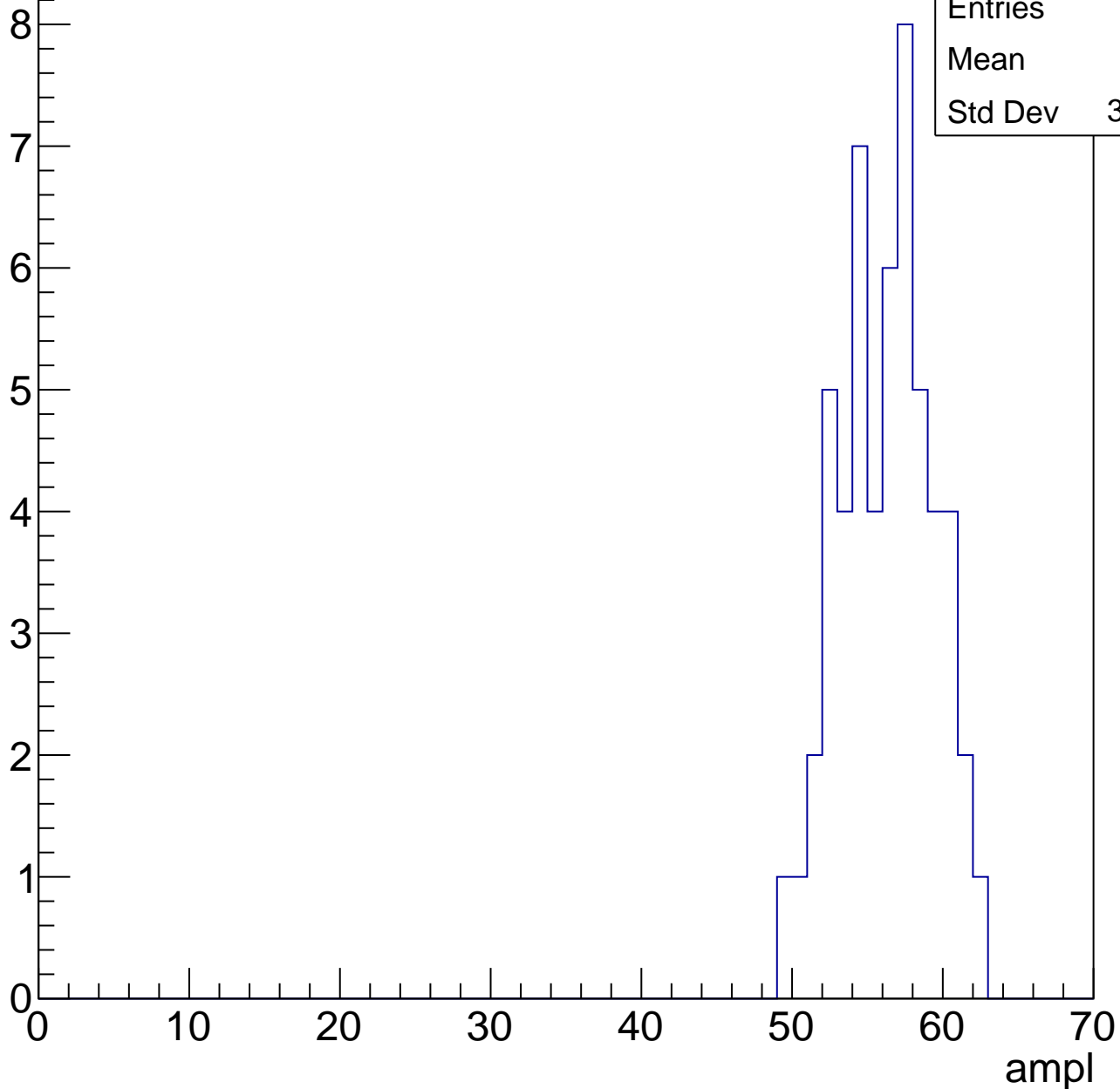


B1L100S, U11-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	55.8
Std Dev	3.027

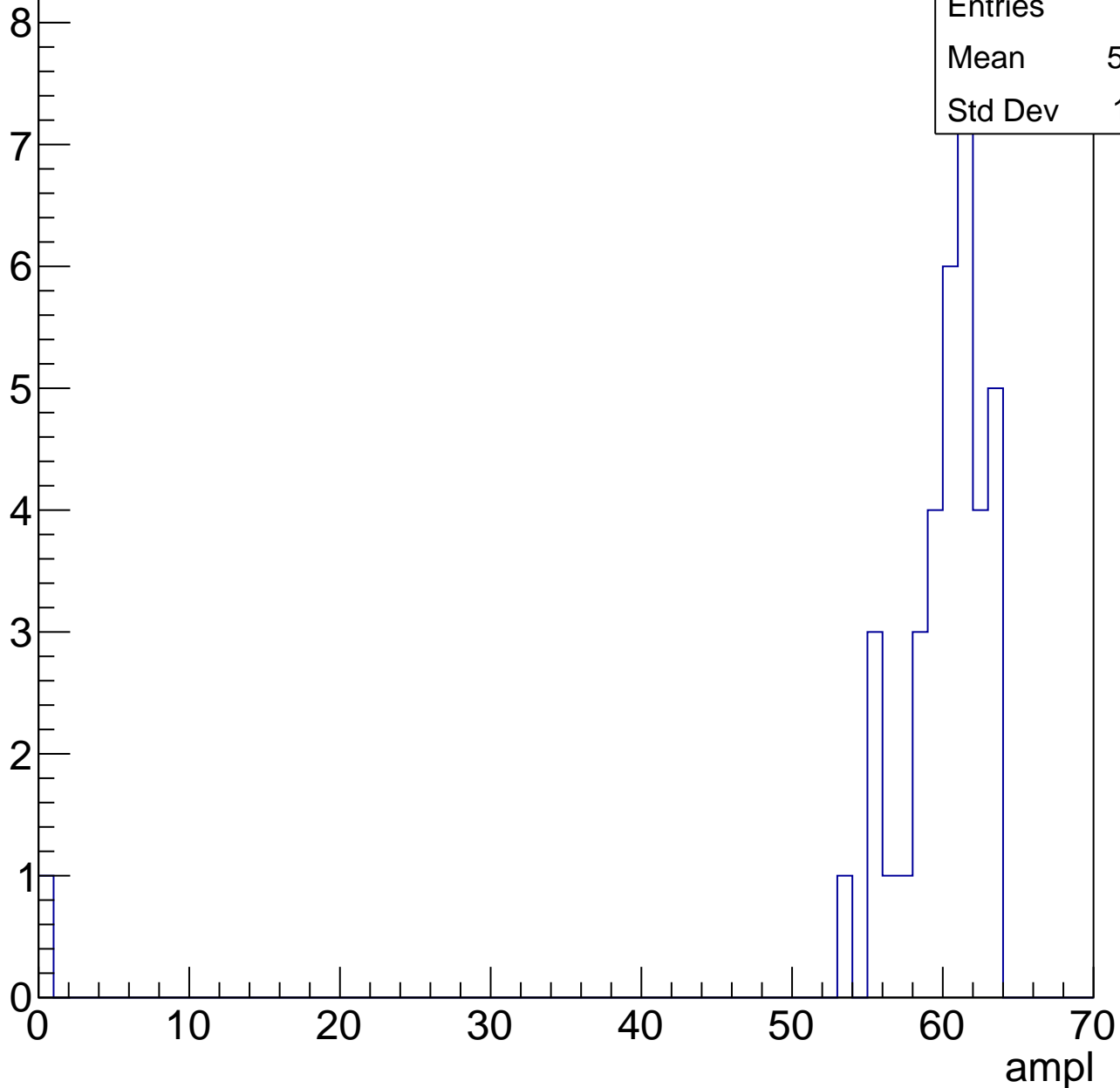


B1L100S, U11-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.16
Std Dev	10.01

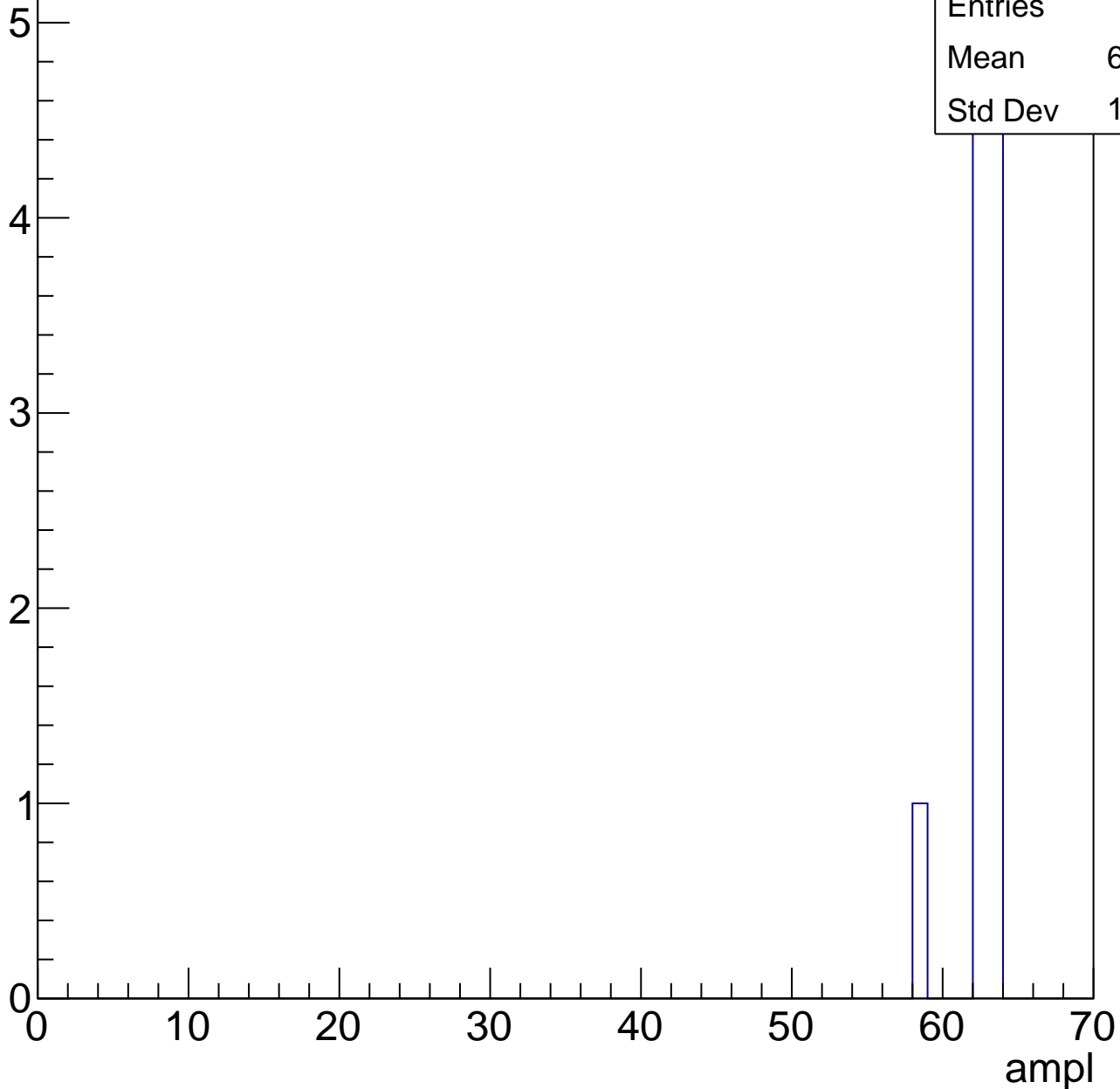


B1L100S, U11-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

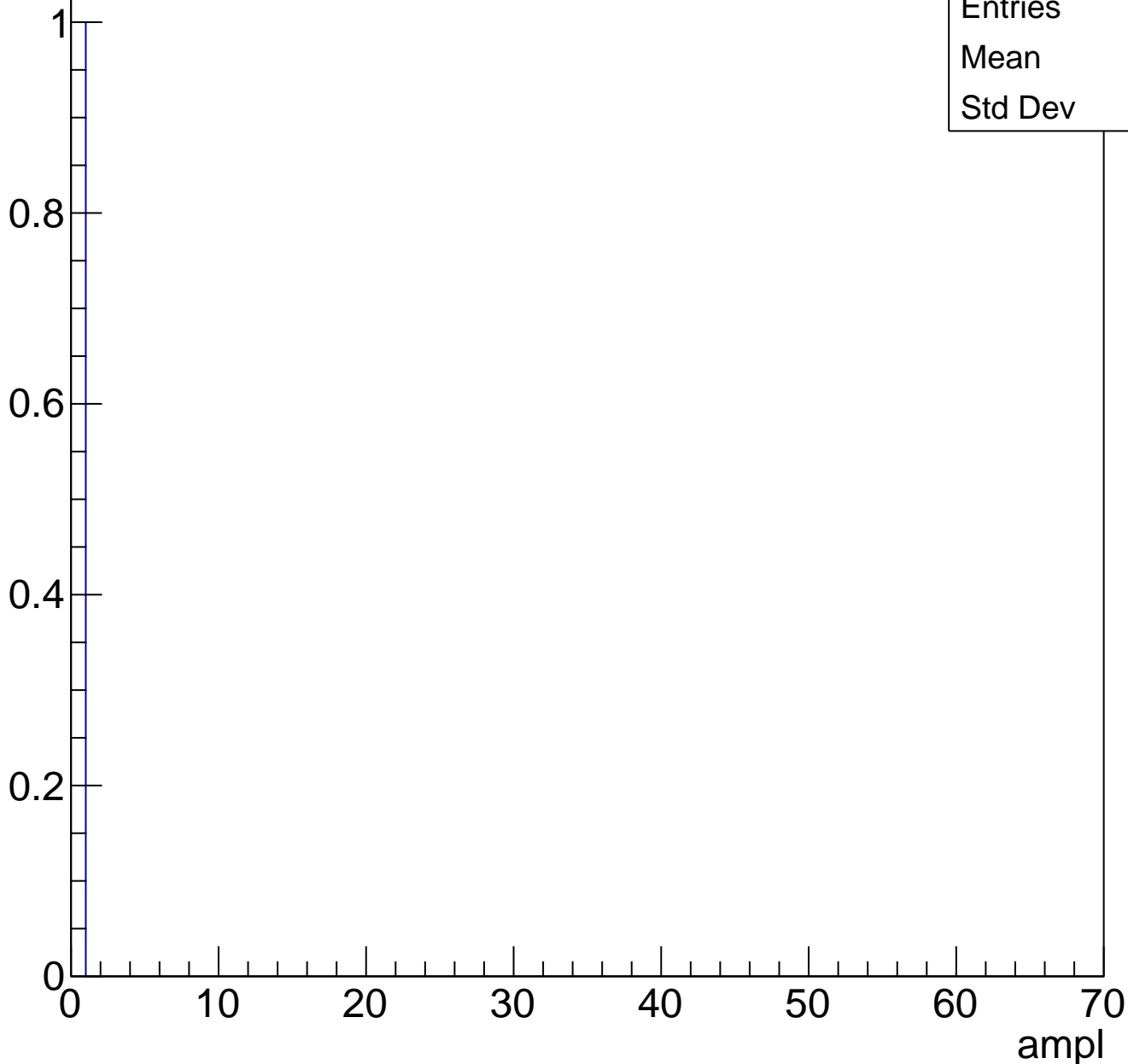
Entries	11
Mean	62.09
Std Dev	1.379



B1L100S, U11-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch119, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	30.35
Std Dev	3.431

Gaus mean : 30.5386

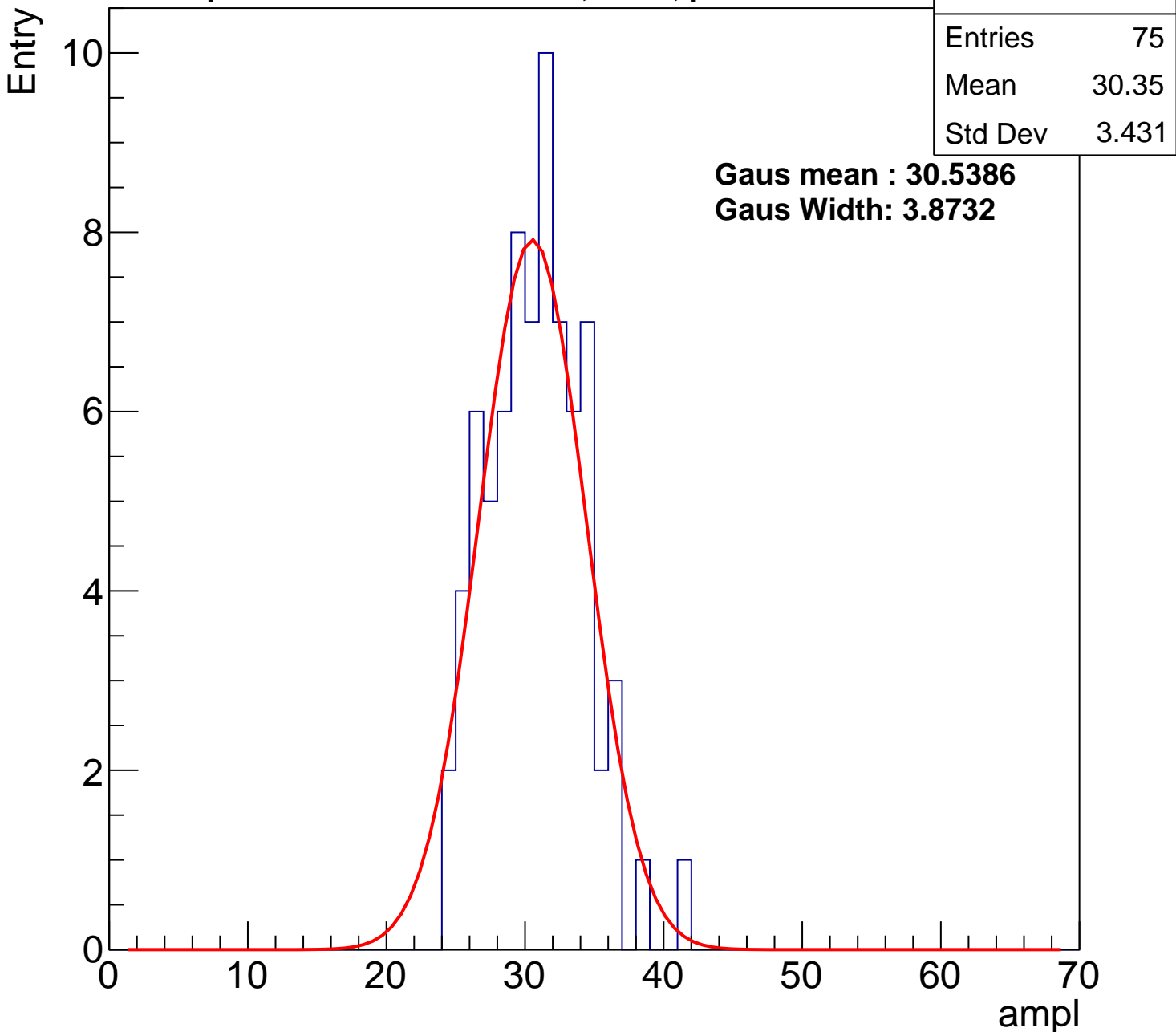
Gaus Width: 3.8732

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch119, adc1

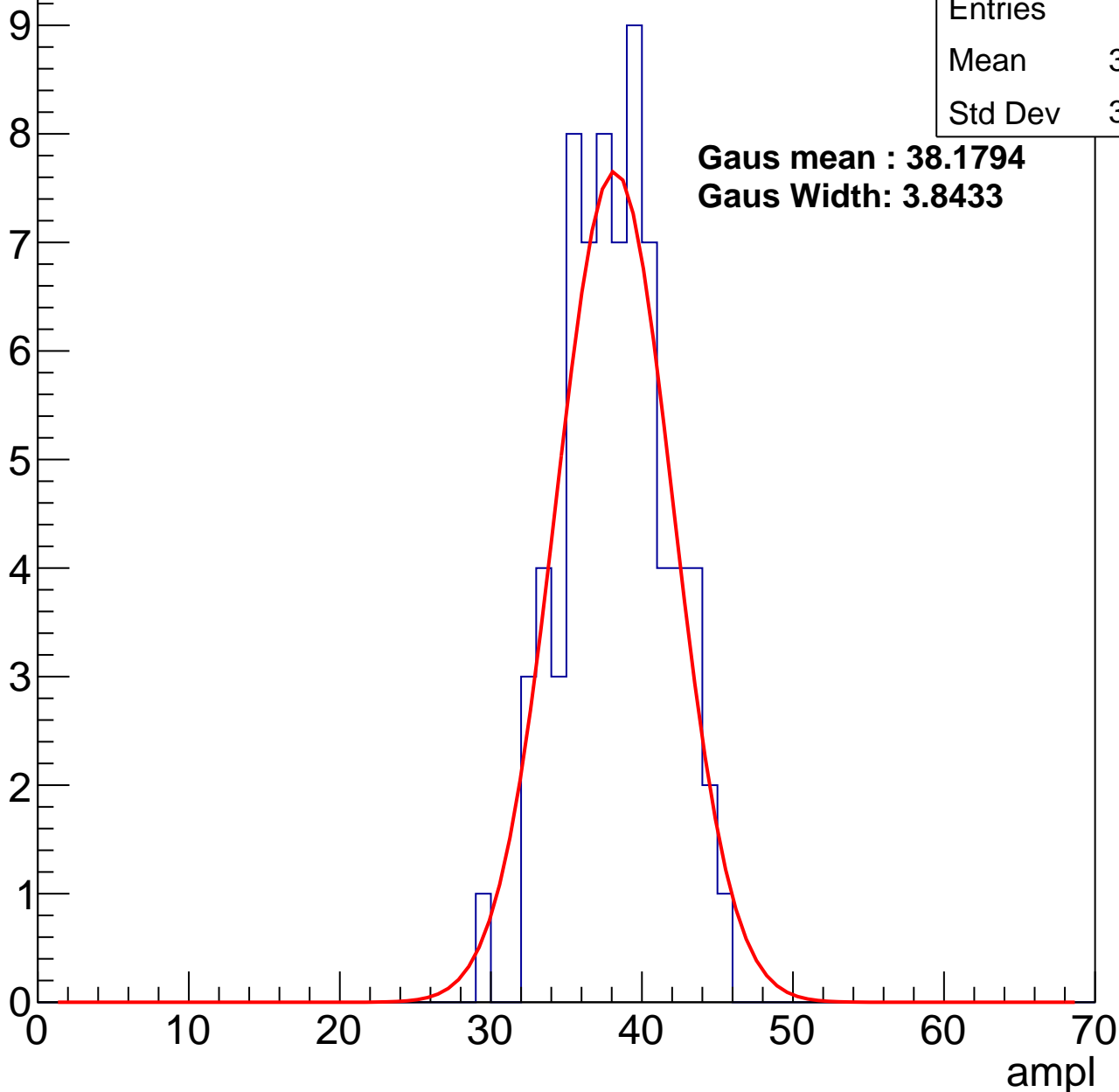
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.79
Std Dev	3.333

Gaus mean : 38.1794

Gaus Width: 3.8433



B1L100S, U11-ch119, adc2

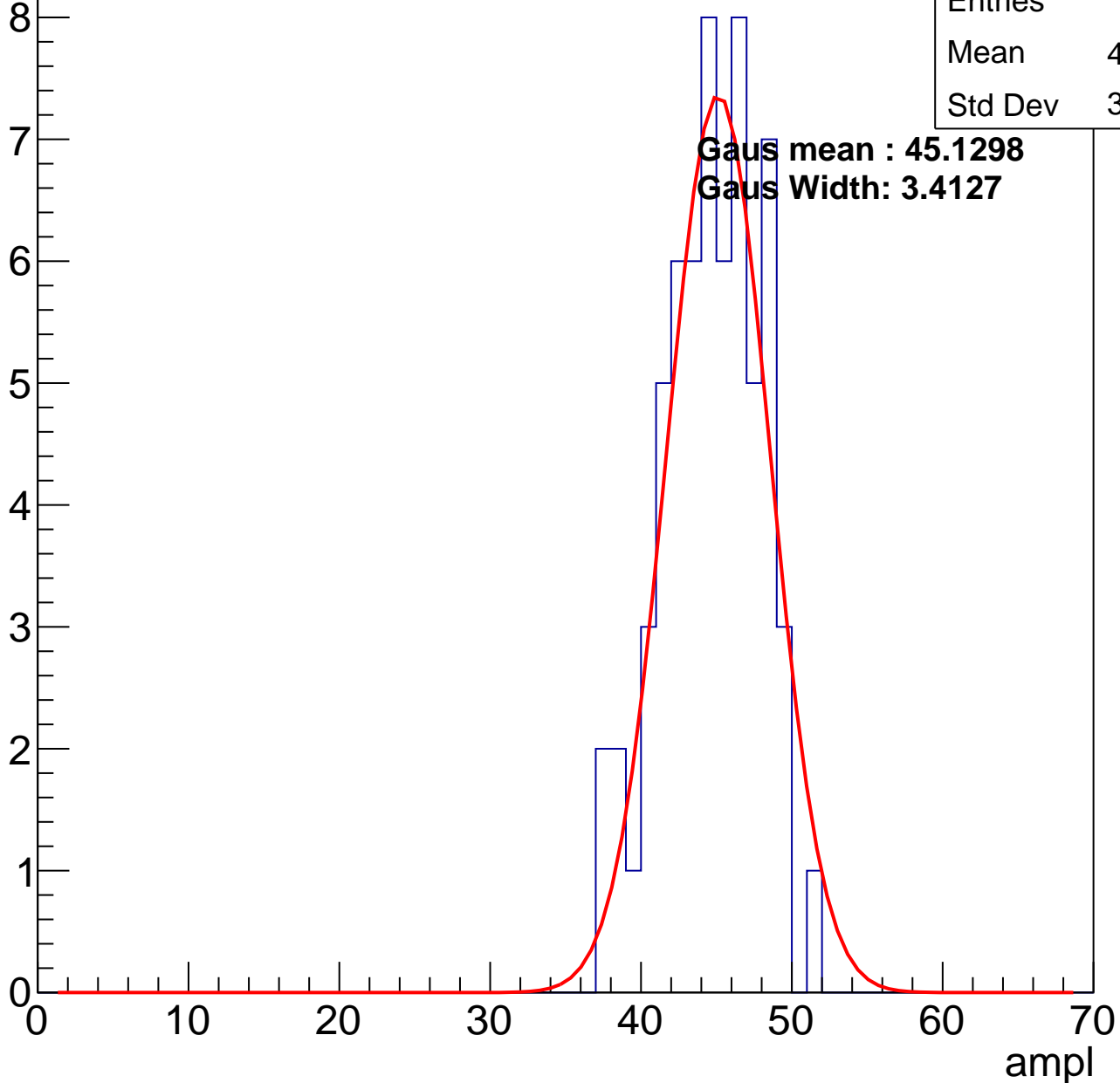
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.17
Std Dev	3.175

Gaus mean : 45.1298

Gaus Width: 3.4127

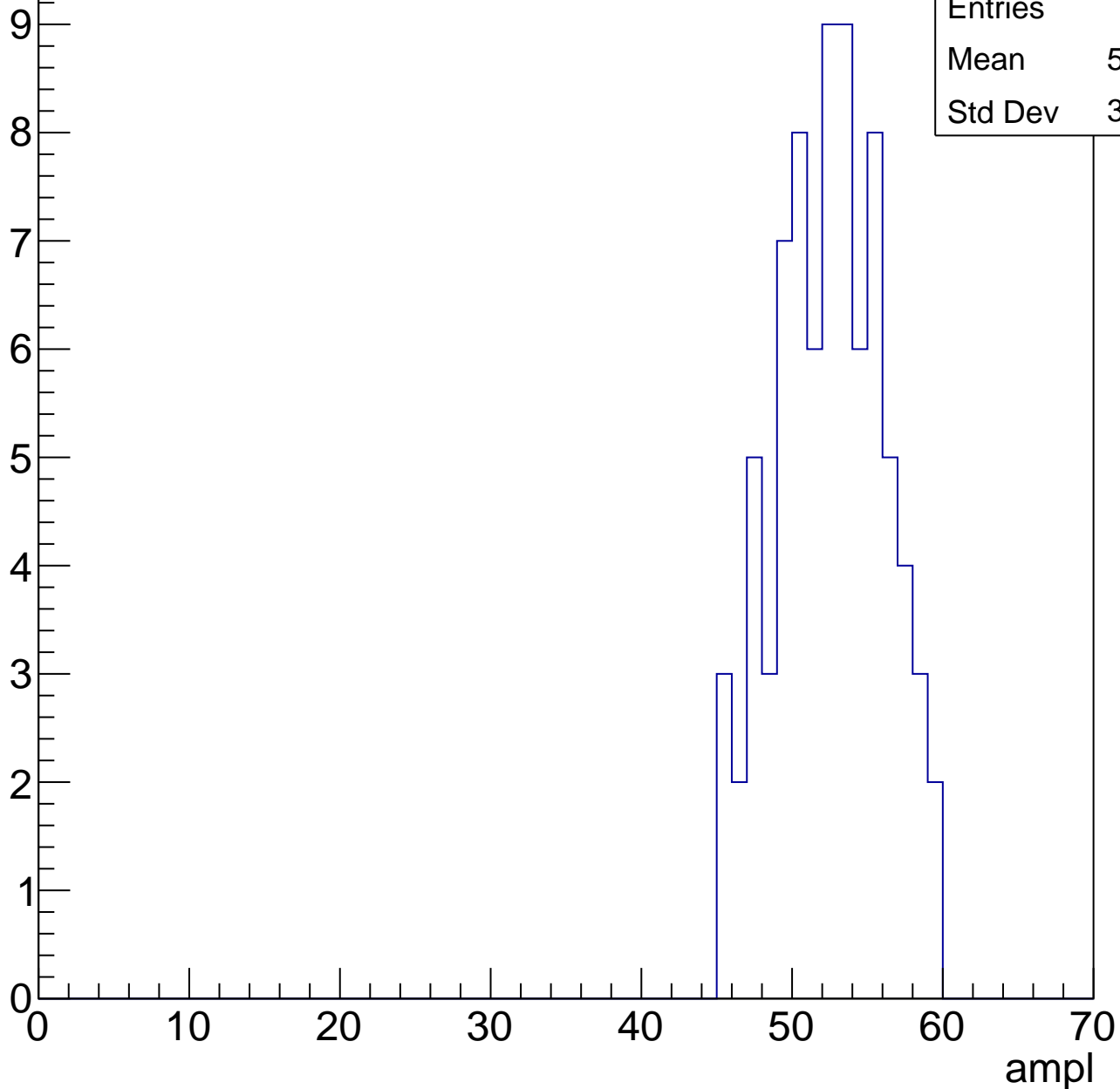


B1L100S, U11-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	52.05
Std Dev	3.507

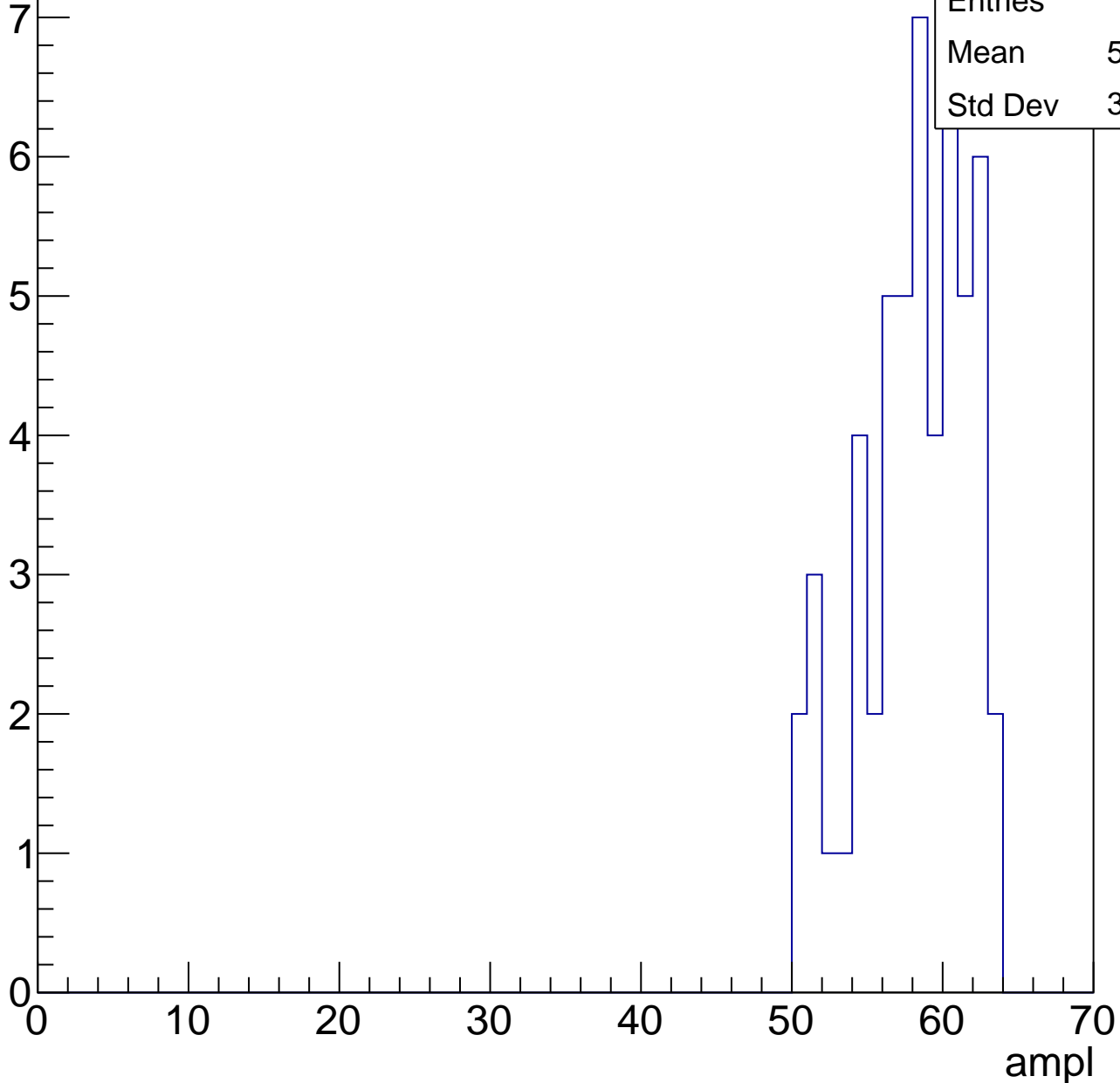


B1L100S, U11-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.67
Std Dev	3.496

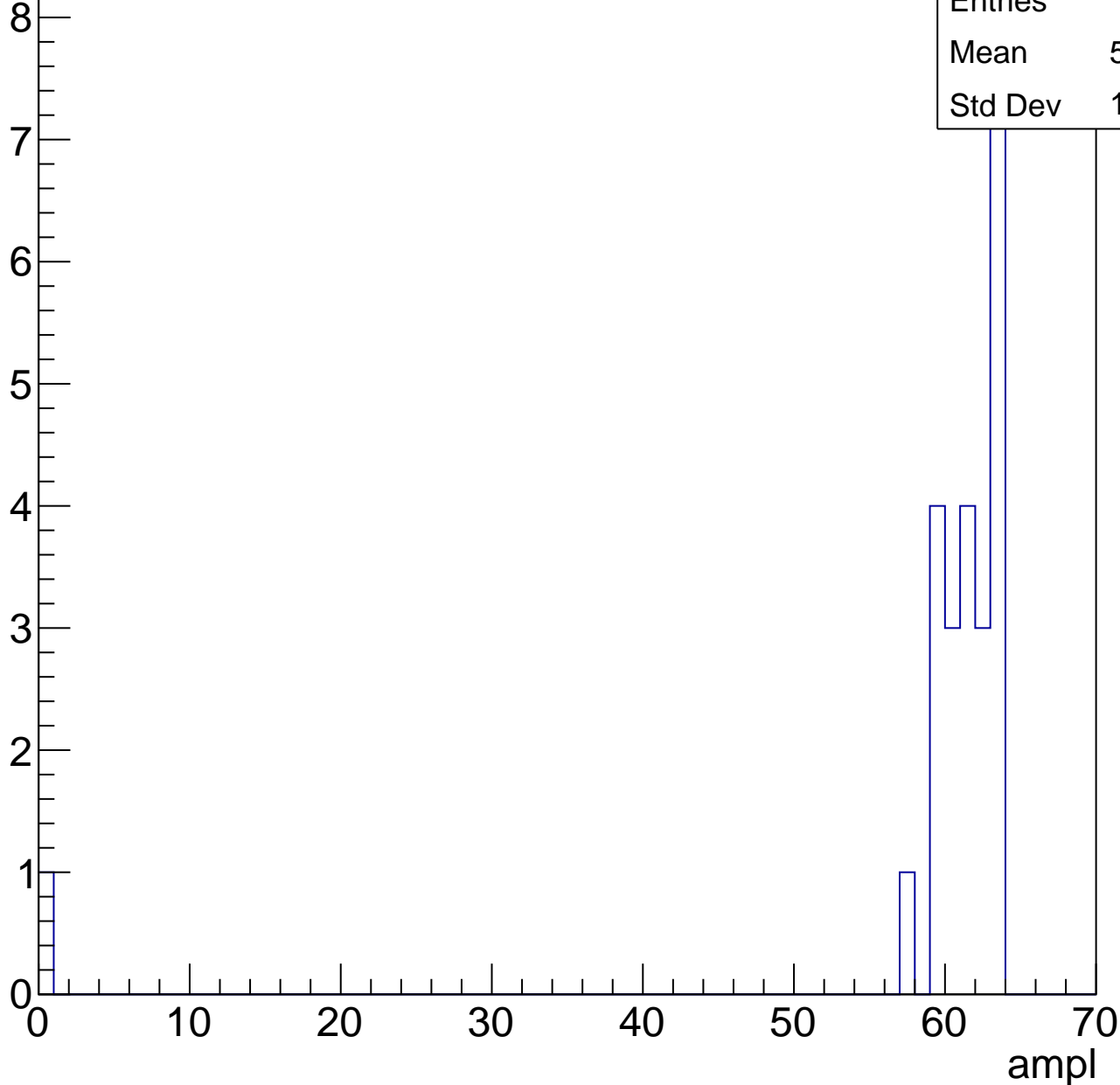


B1L100S, U11-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	58.62
Std Dev	12.34



B1L100S, U11-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

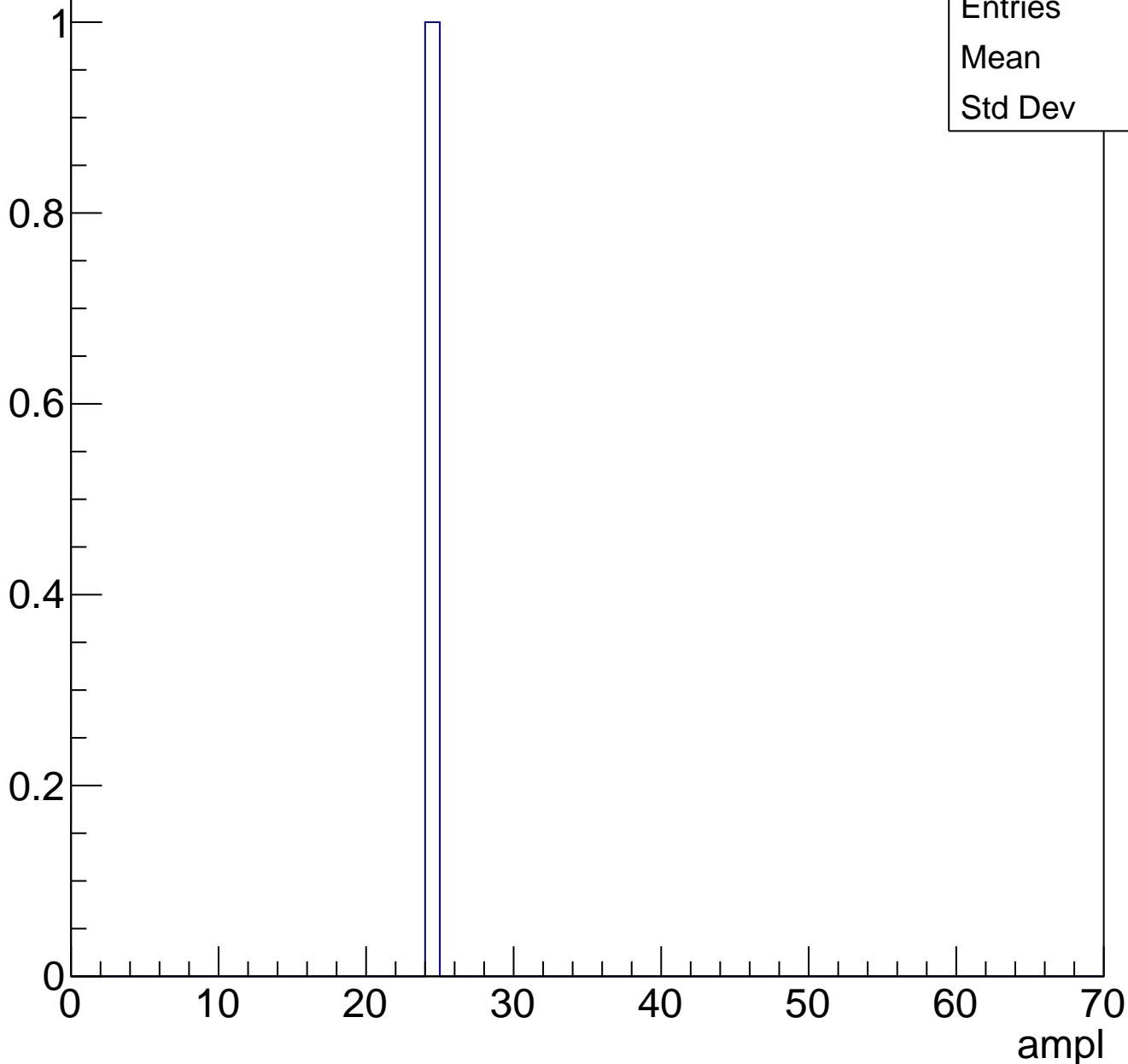
Entry



B1L100S, U11-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch120, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	30.22
Std Dev	3.334

Gaus mean : 30.6924

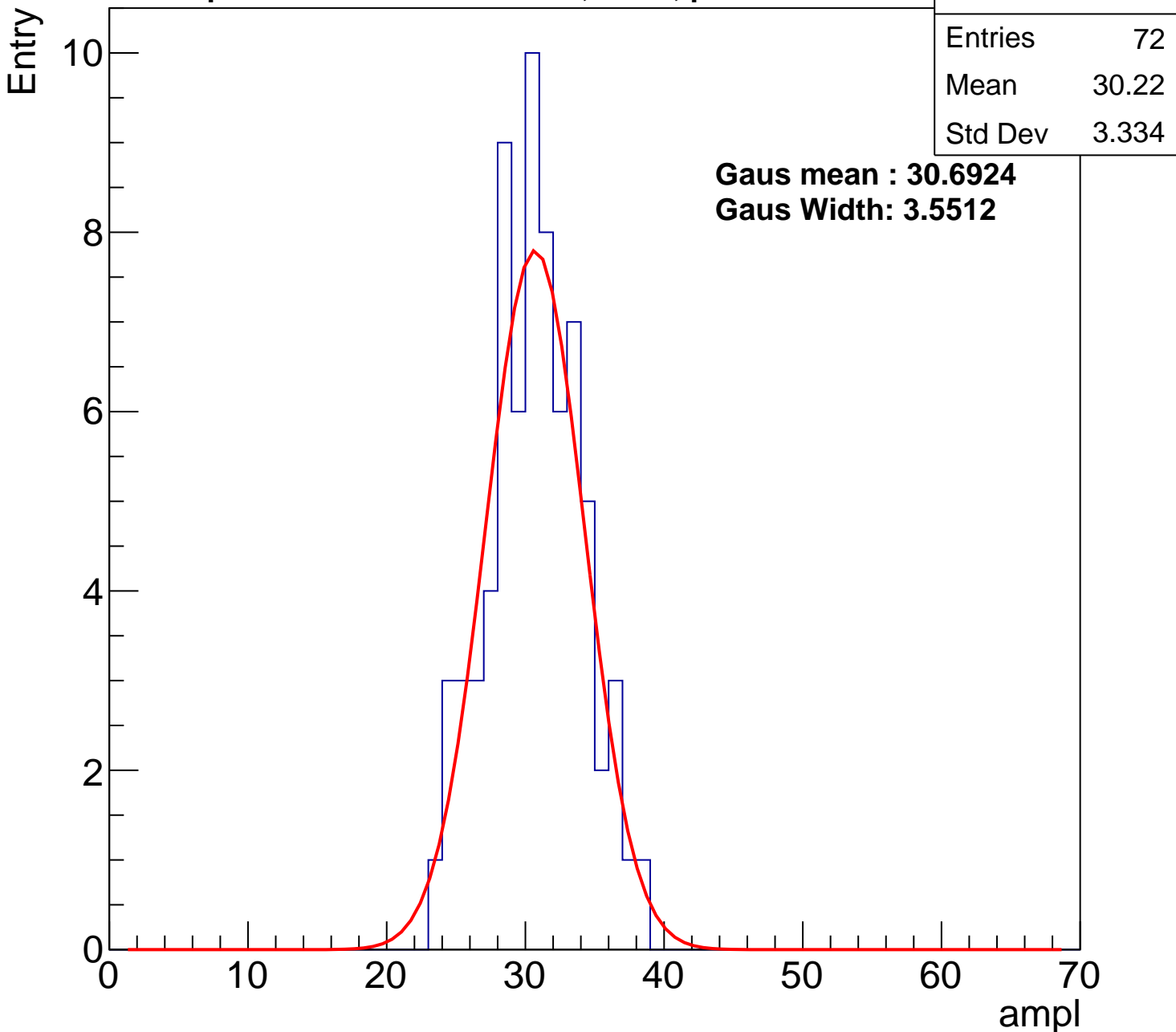
Gaus Width: 3.5512

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch120, adc1

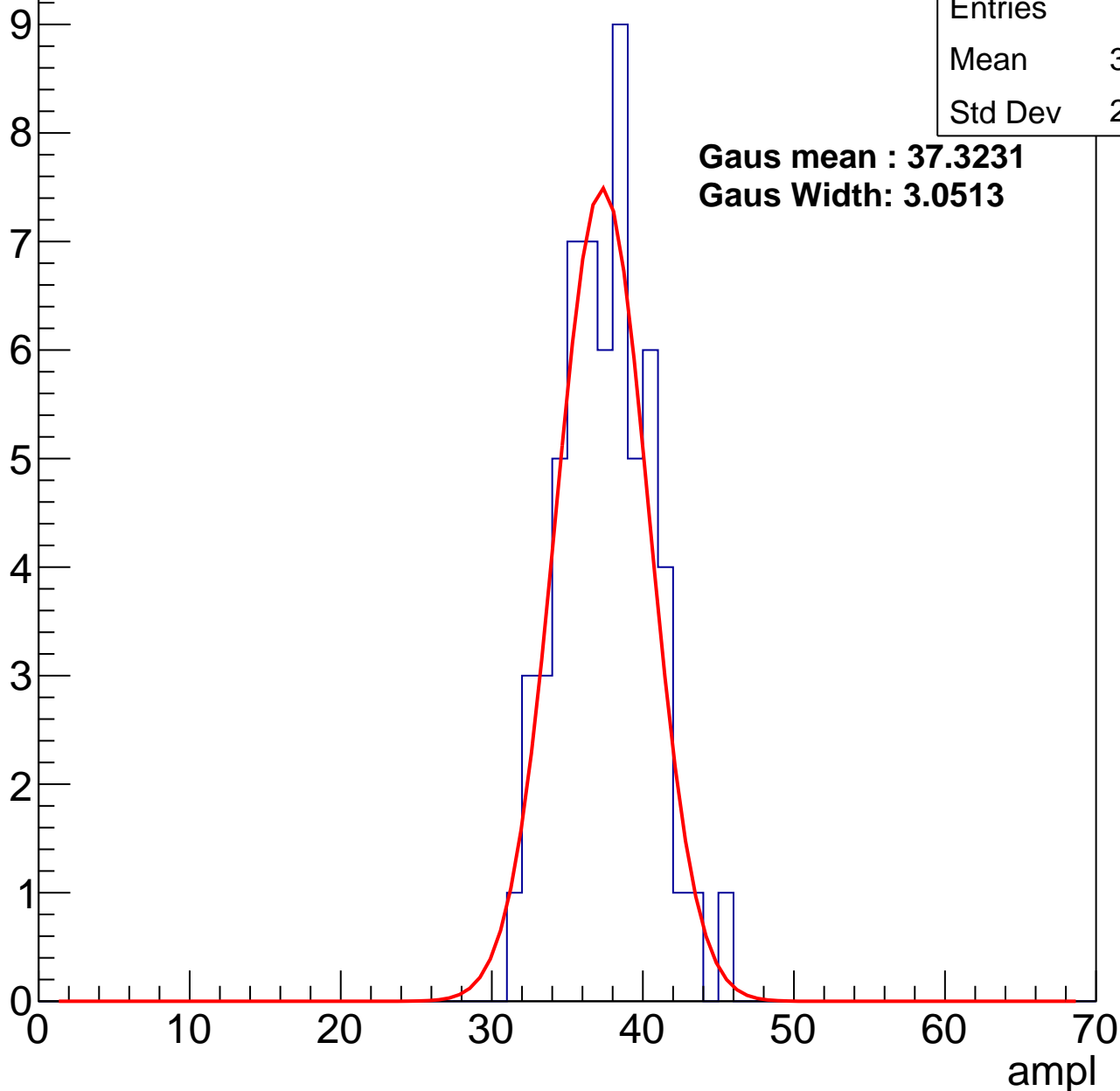
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	37.05
Std Dev	2.943

Gaus mean : 37.3231

Gaus Width: 3.0513



B1L100S, U11-ch120, adc2

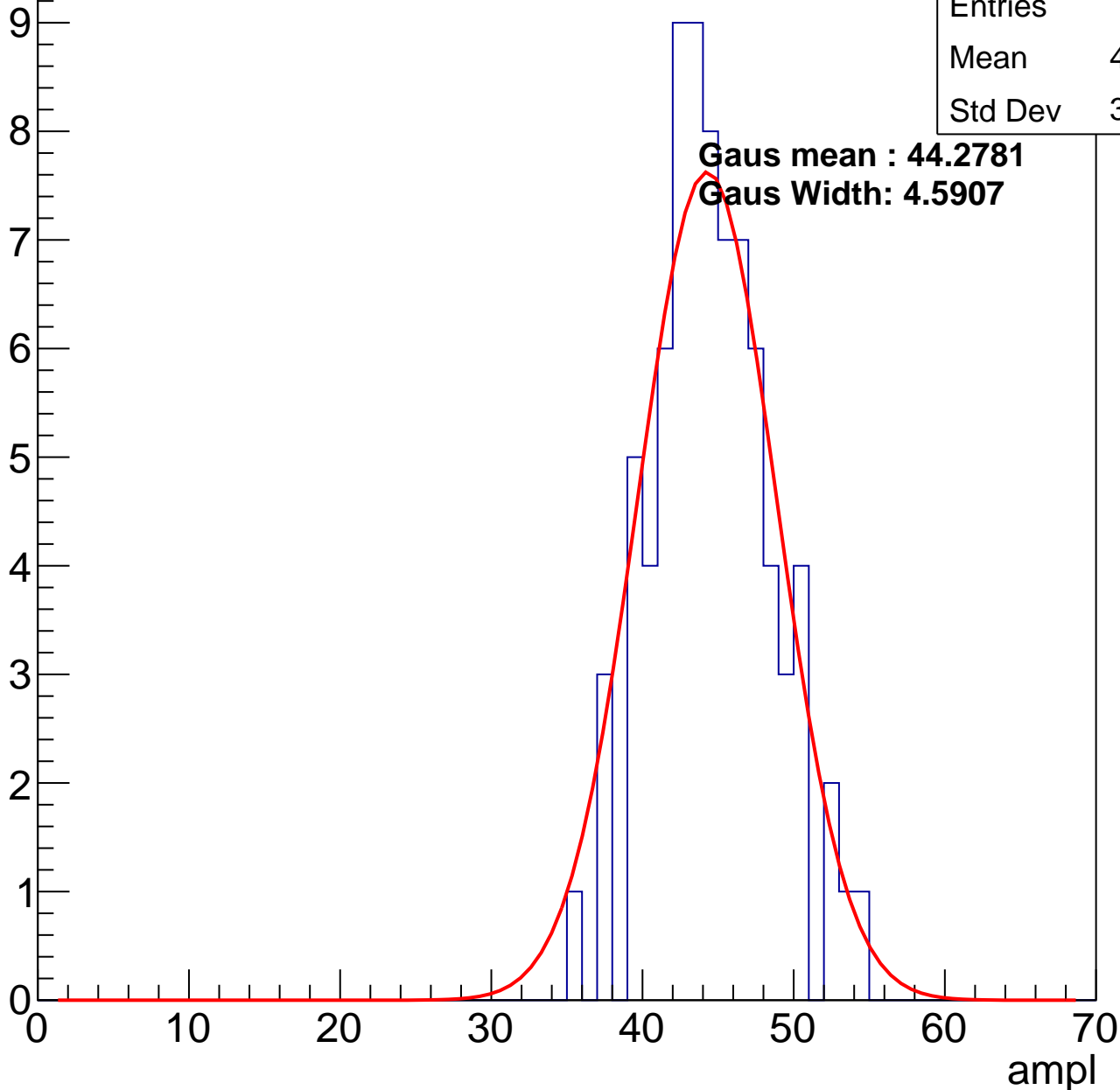
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	44.16
Std Dev	3.865

Gaus mean : 44.2781

Gaus Width: 4.5907

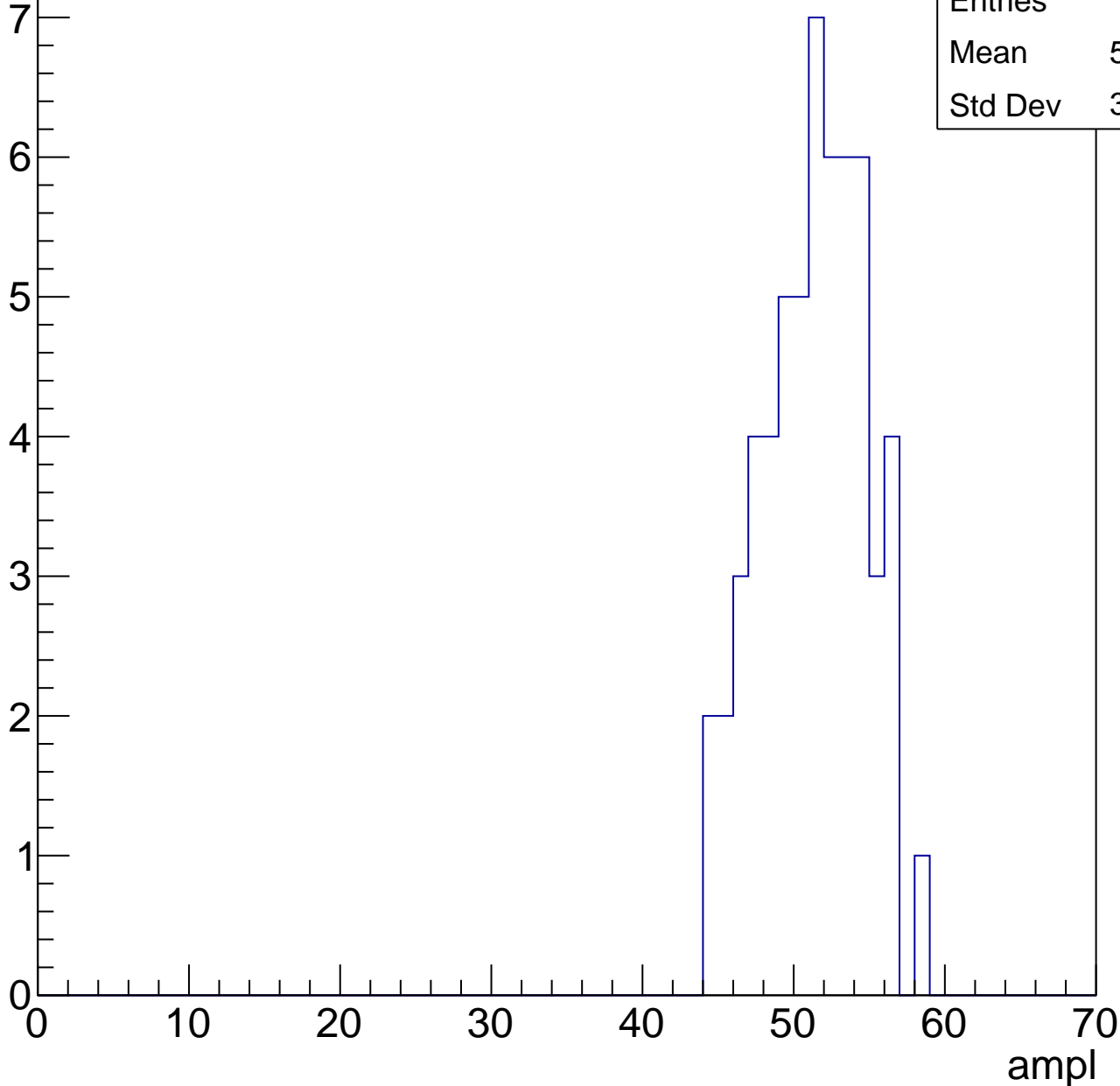


B1L100S, U11-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

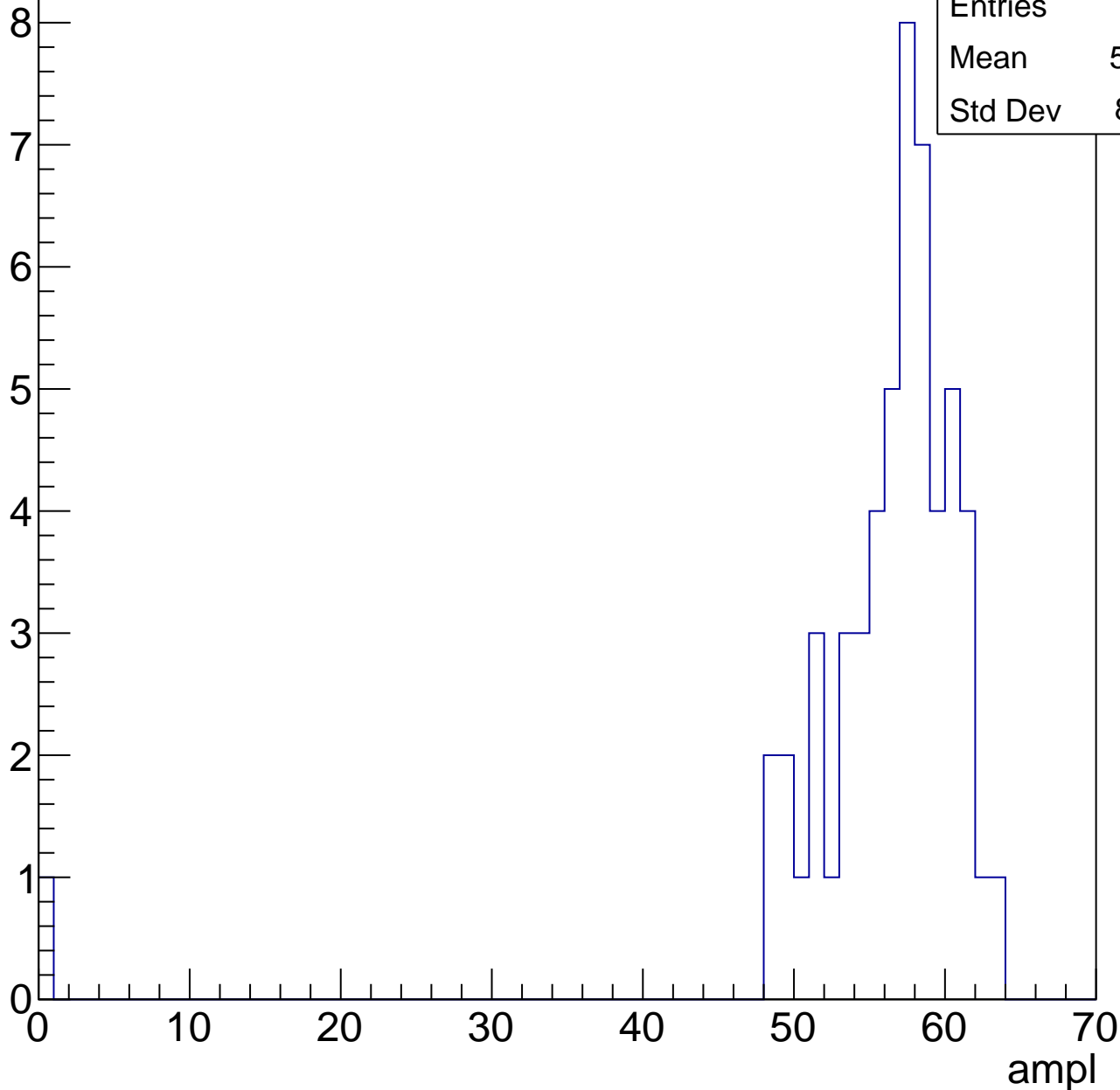
Entries	58
Mean	50.84
Std Dev	3.347



B1L100S, U11-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

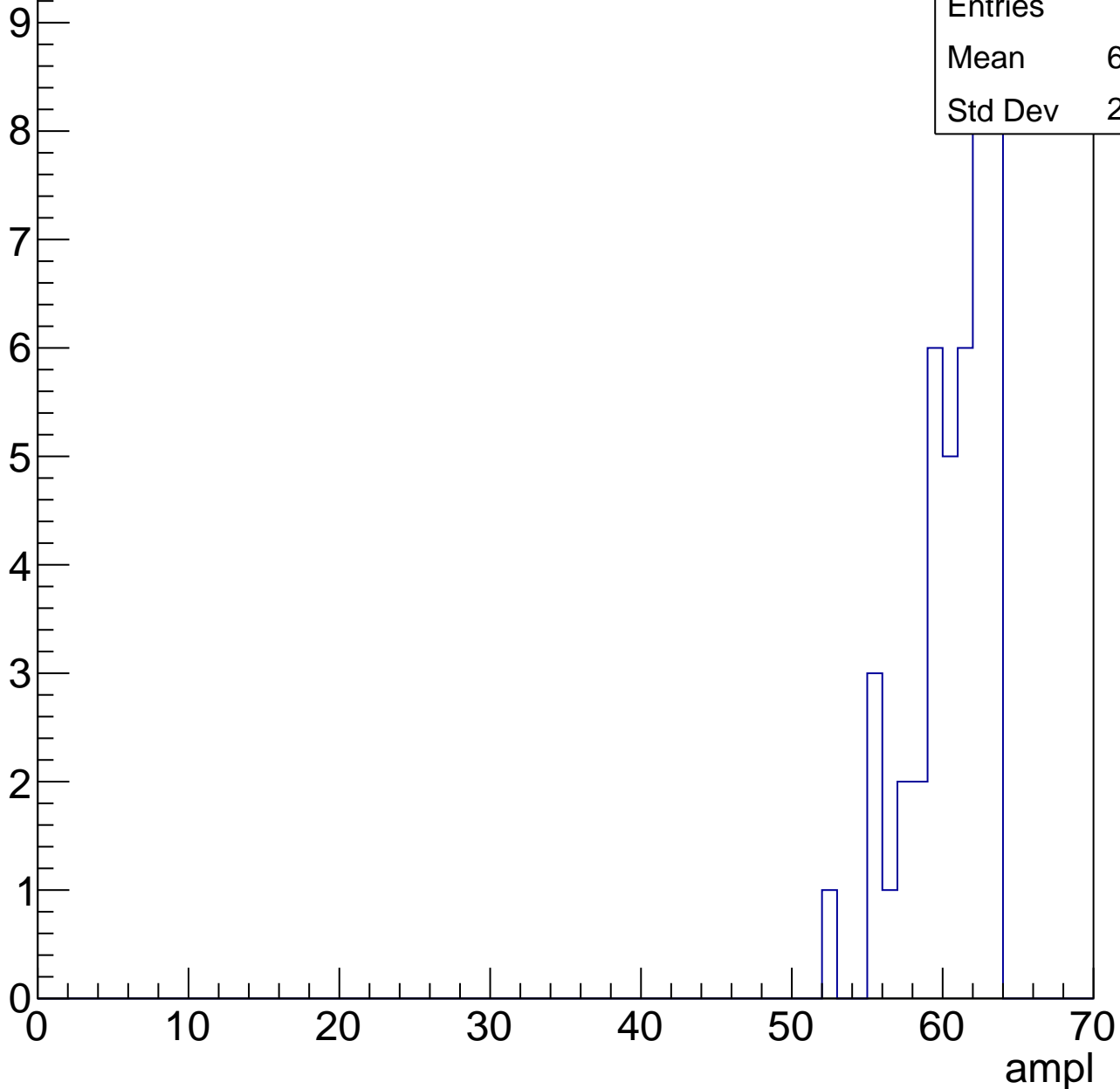


B1L100S, U11-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	60.14
Std Dev	2.664



B1L100S, U11-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L100S, U11-ch121, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	29.39
Std Dev	3.79

Gaus mean : 29.2889

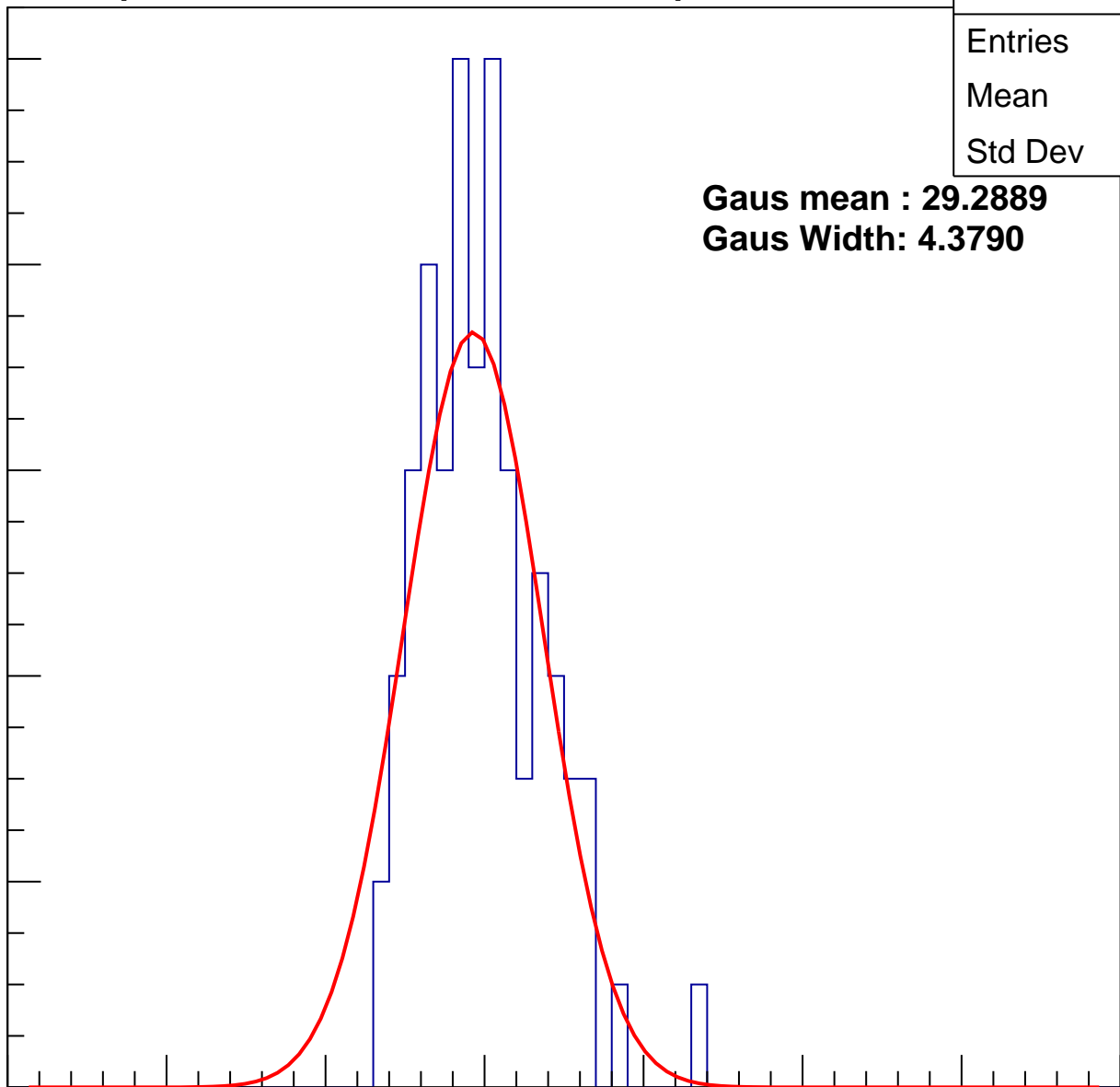
Gaus Width: 4.3790

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch121, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	36.52
Std Dev	3.765

Gaus mean : 36.8846

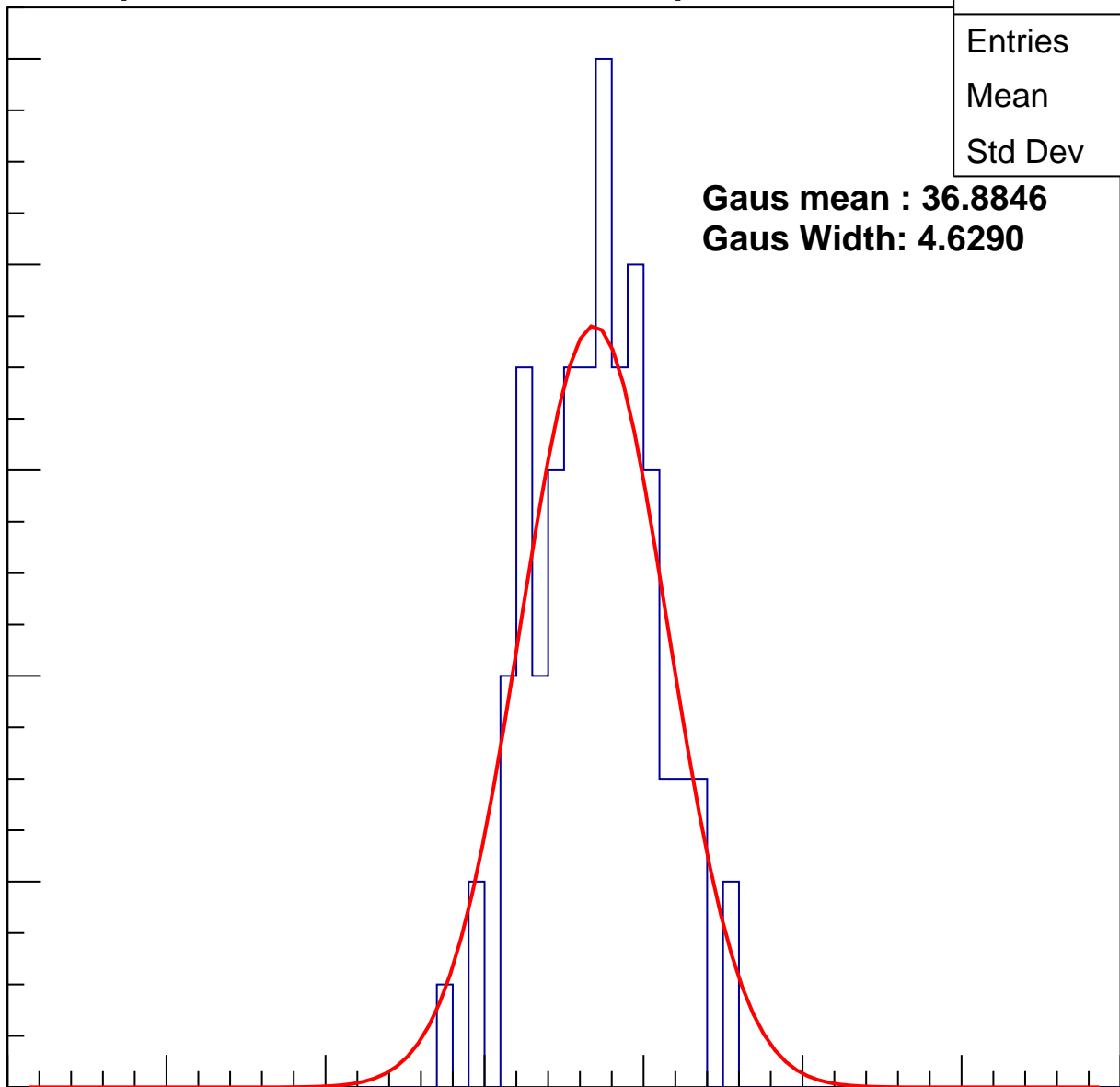
Gaus Width: 4.6290

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch121, adc2

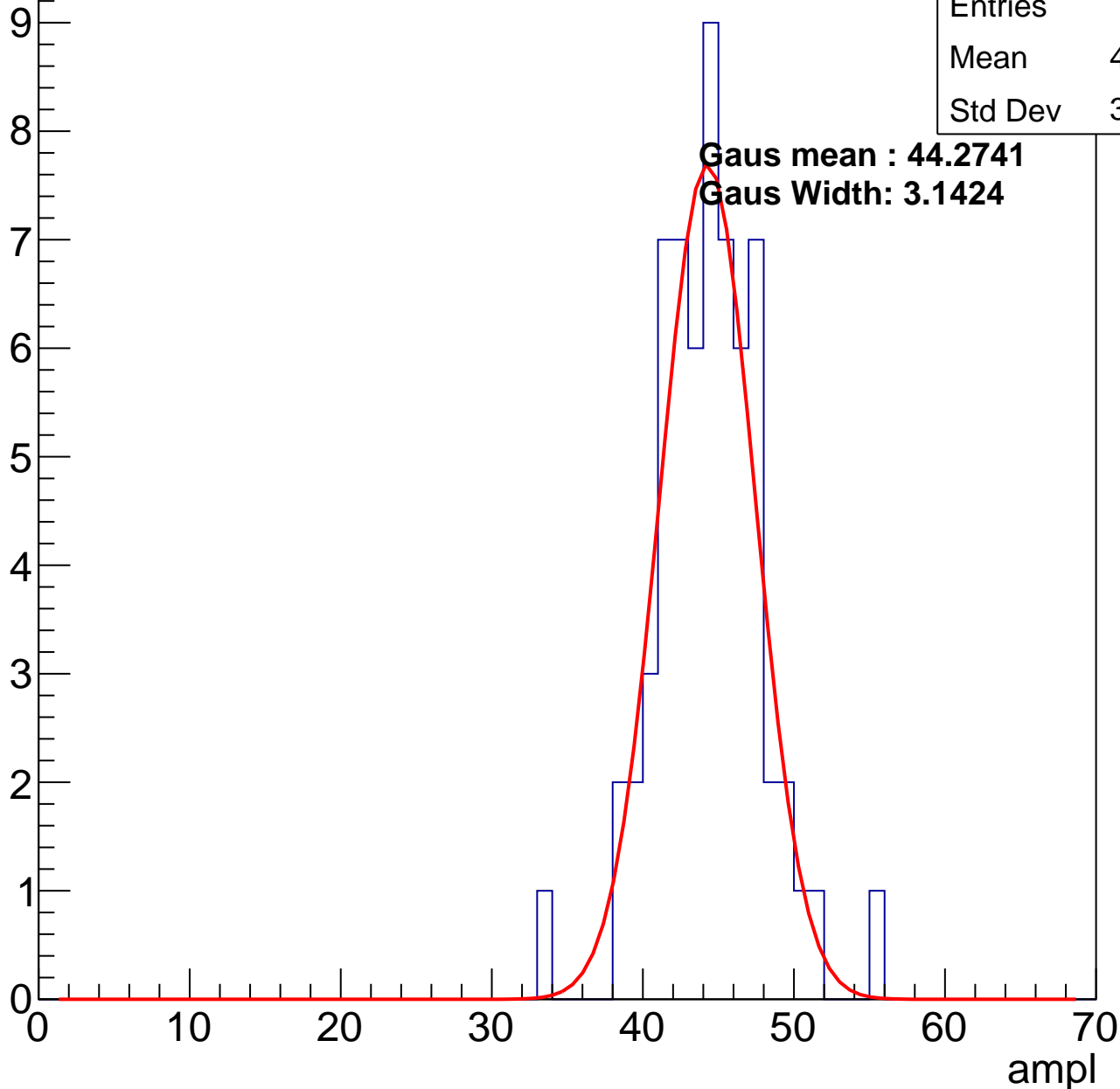
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.94
Std Dev	3.468

Gaus mean : 44.2741

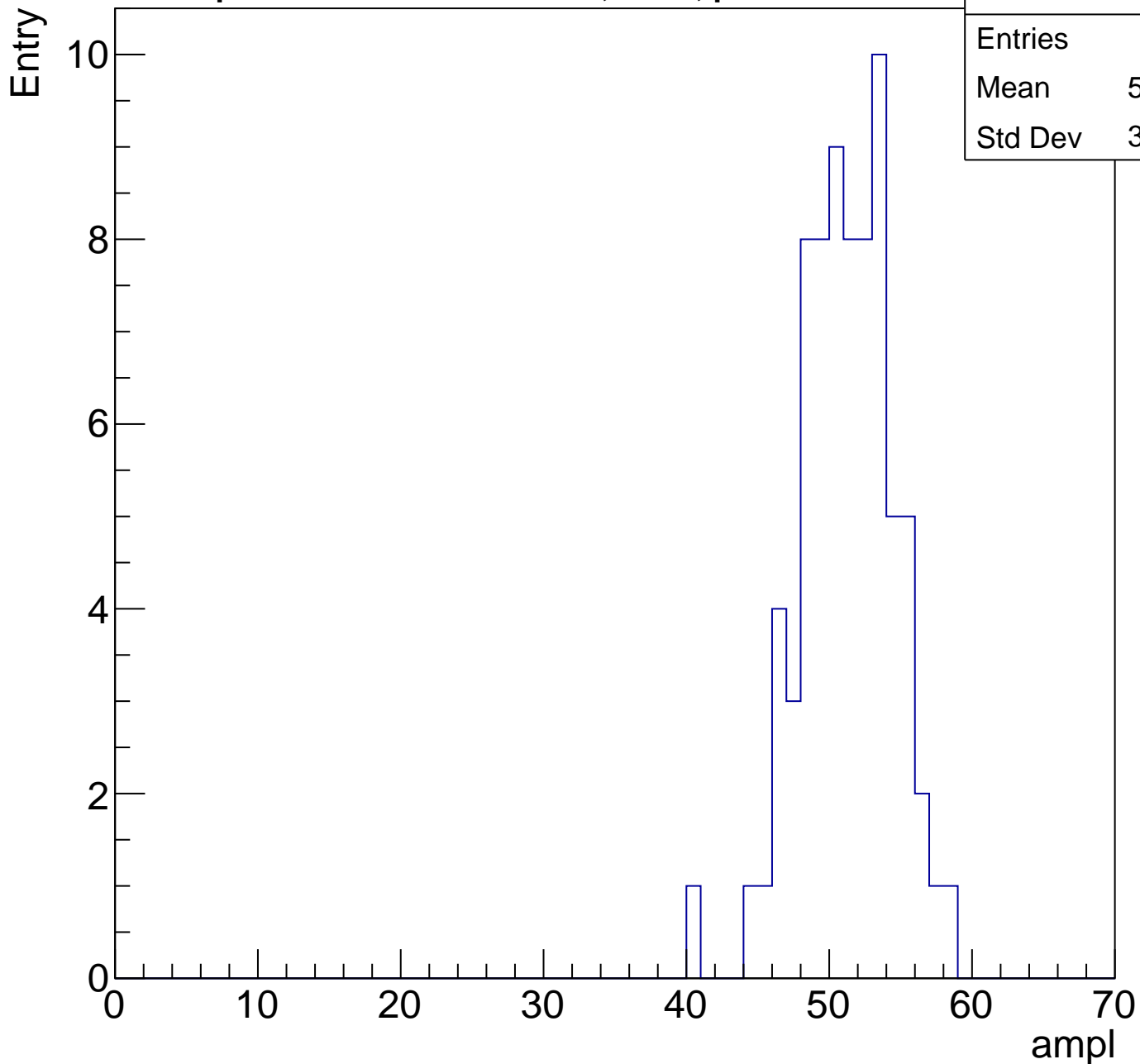
Gaus Width: 3.1424



B1L100S, U11-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

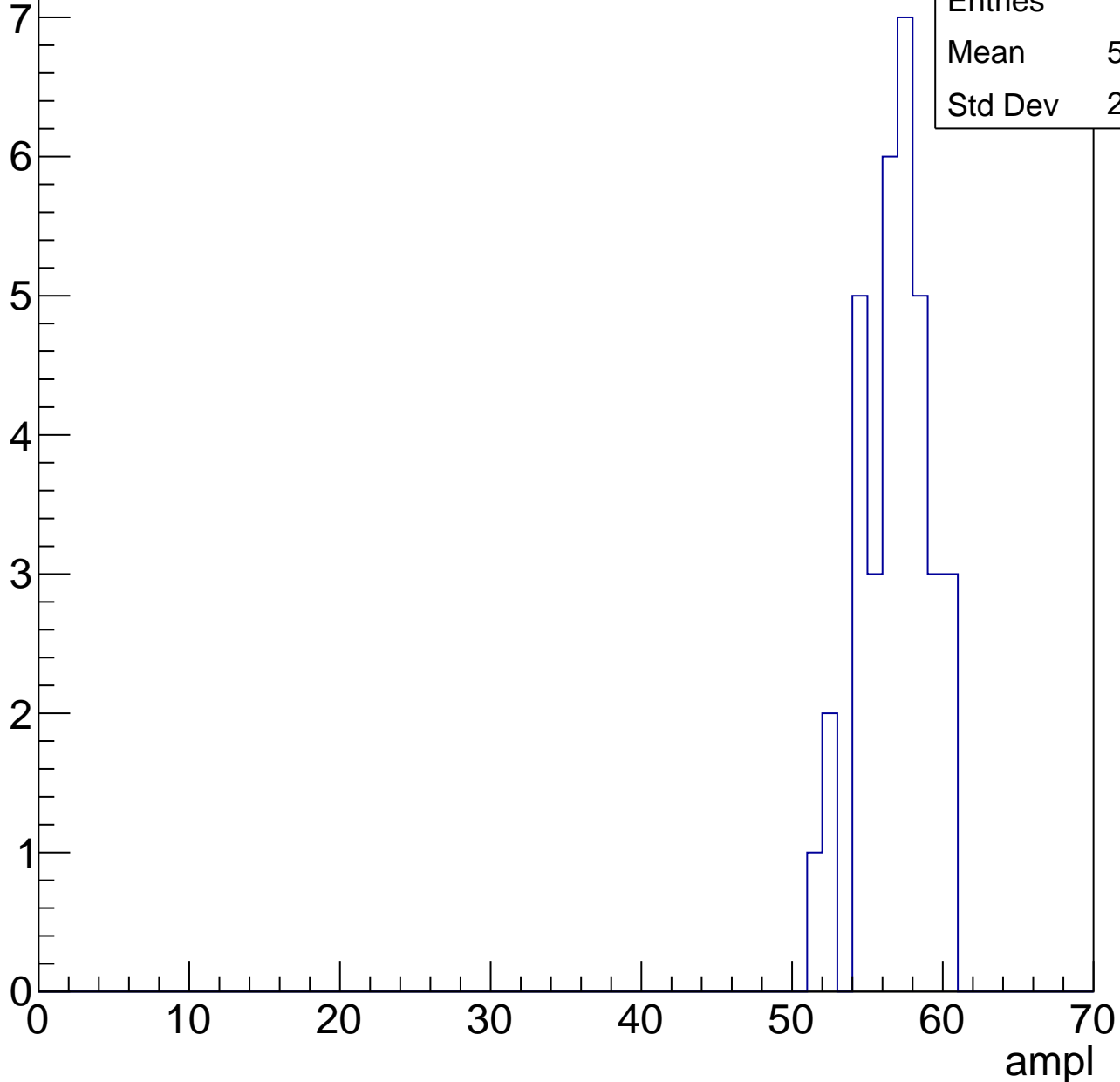
Entries	75
Mean	50.75
Std Dev	3.205



B1L100S, U11-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

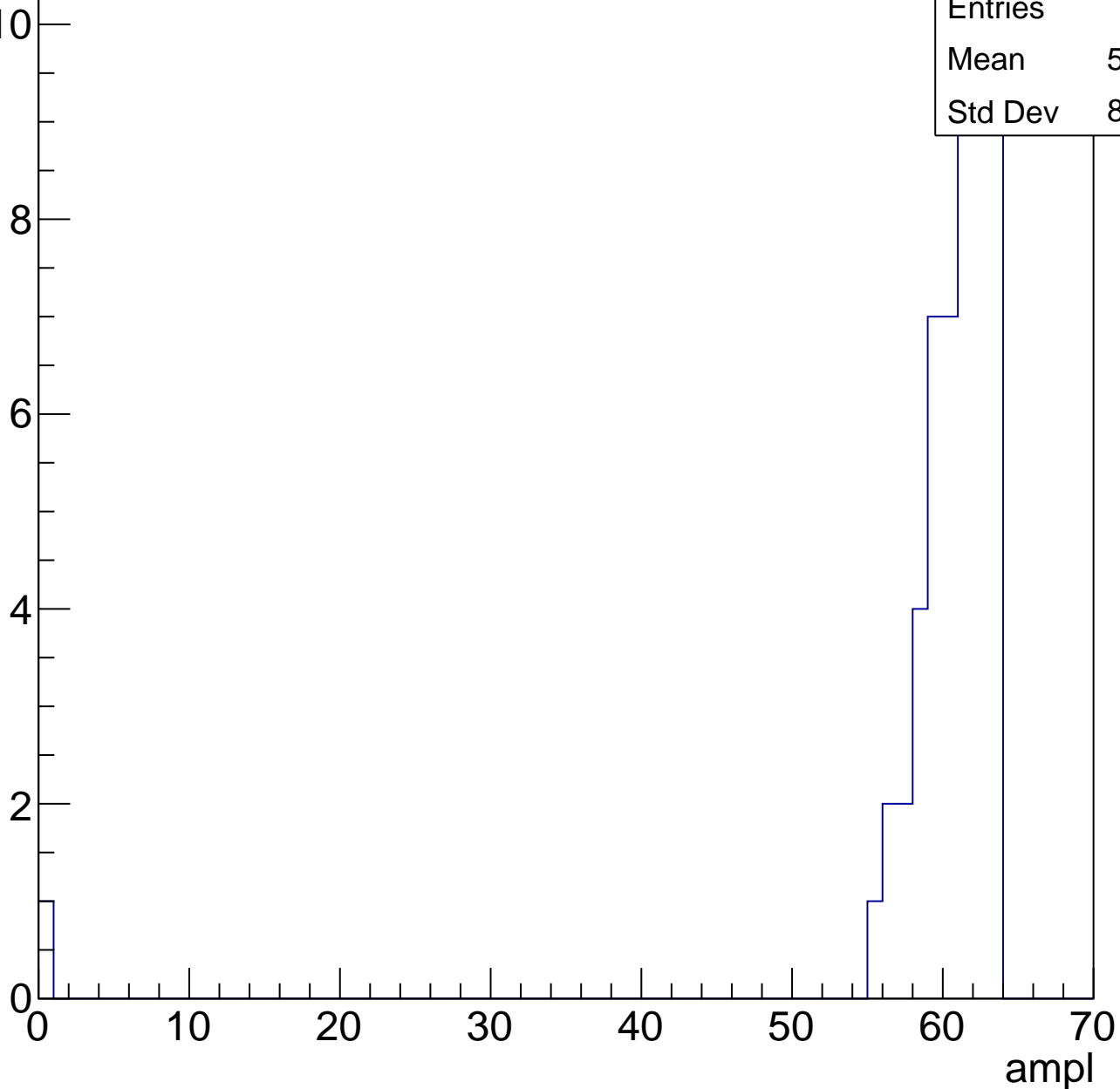


B1L100S, U11-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	59.25
Std Dev	8.544



B1L100S, U11-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

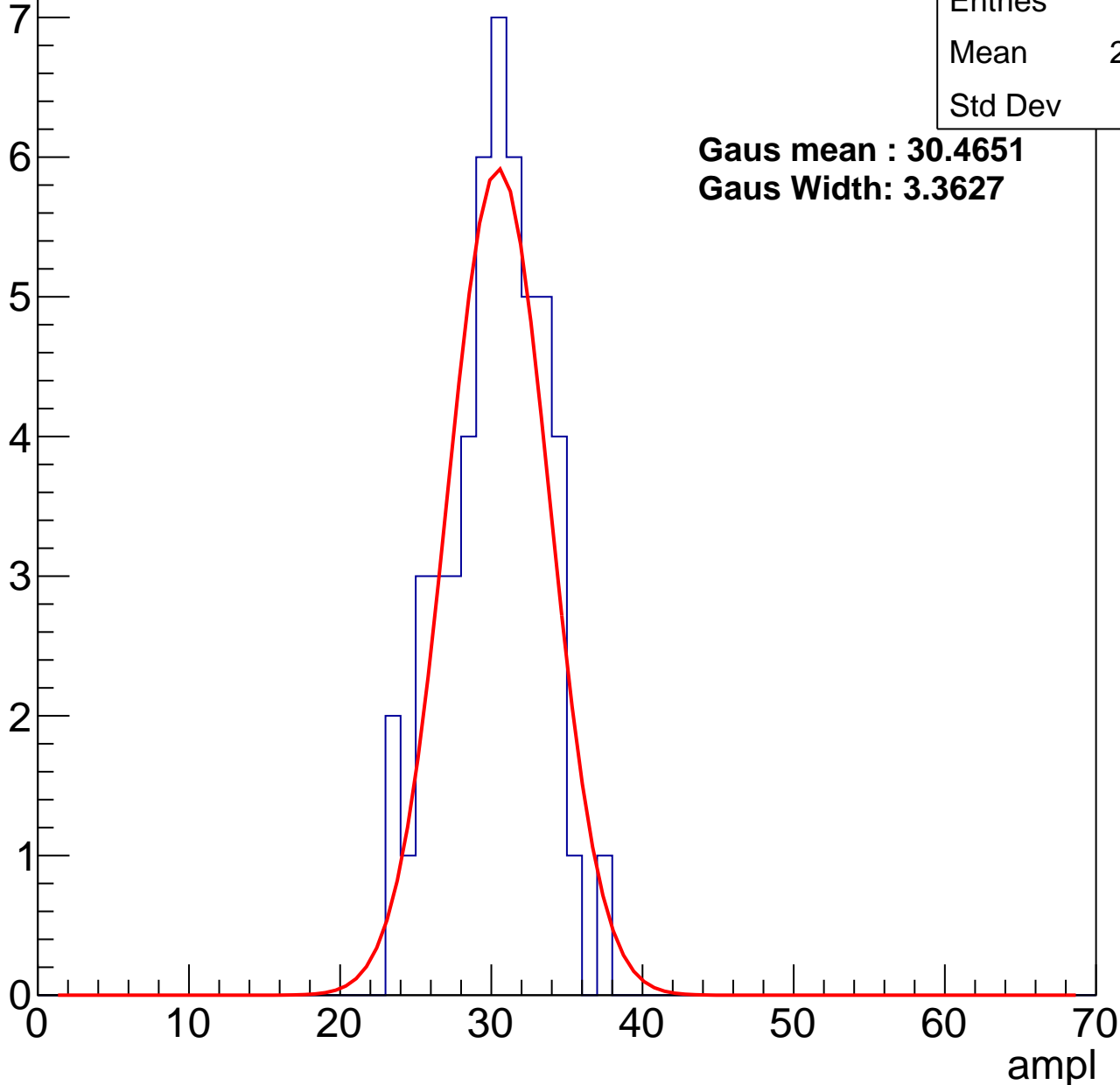
B1L100S, U11-ch122, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	29.78
Std Dev	3.17

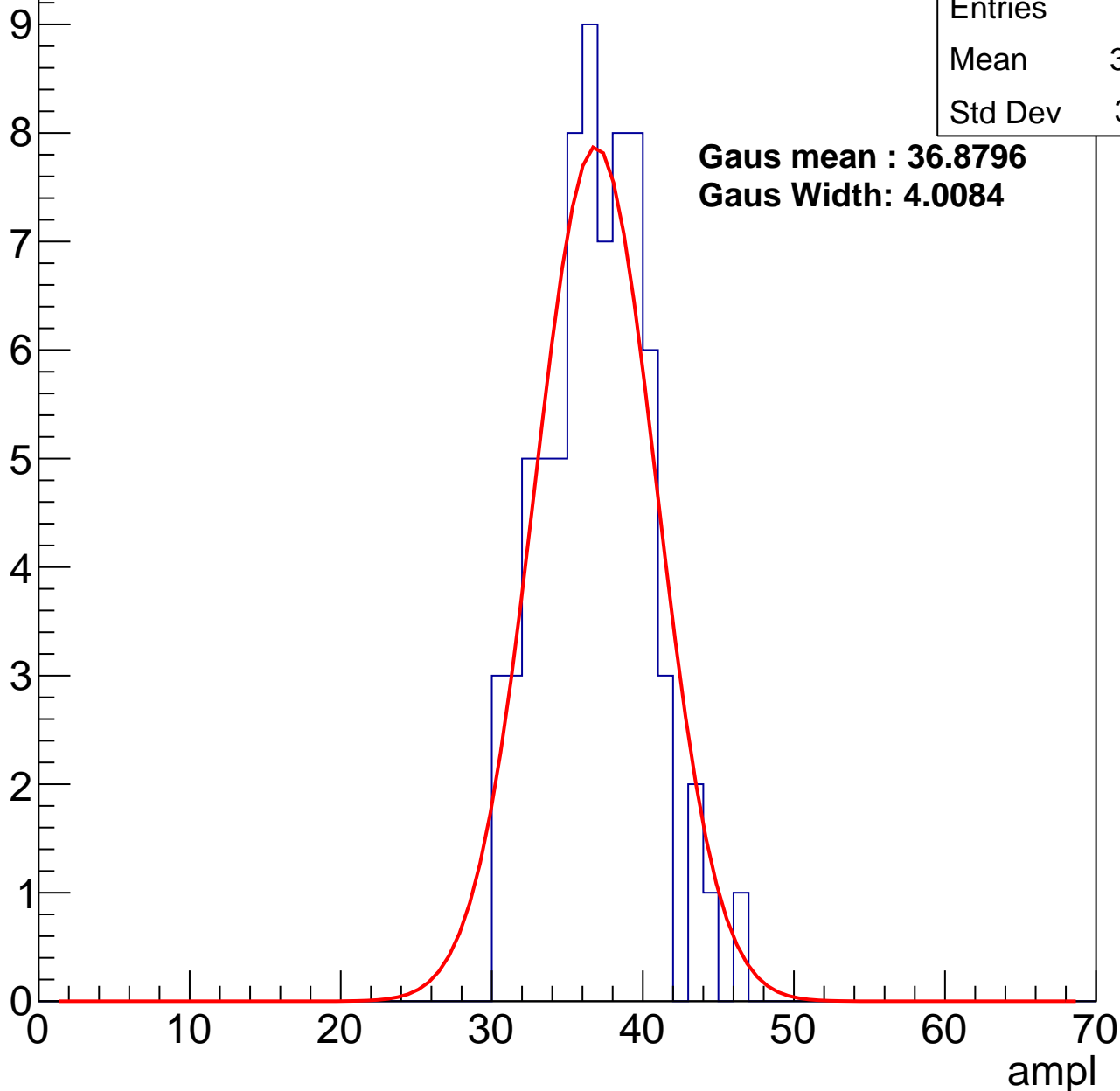
Gaus mean : 30.4651
Gaus Width: 3.3627



B1L100S, U11-ch122, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch122, adc2

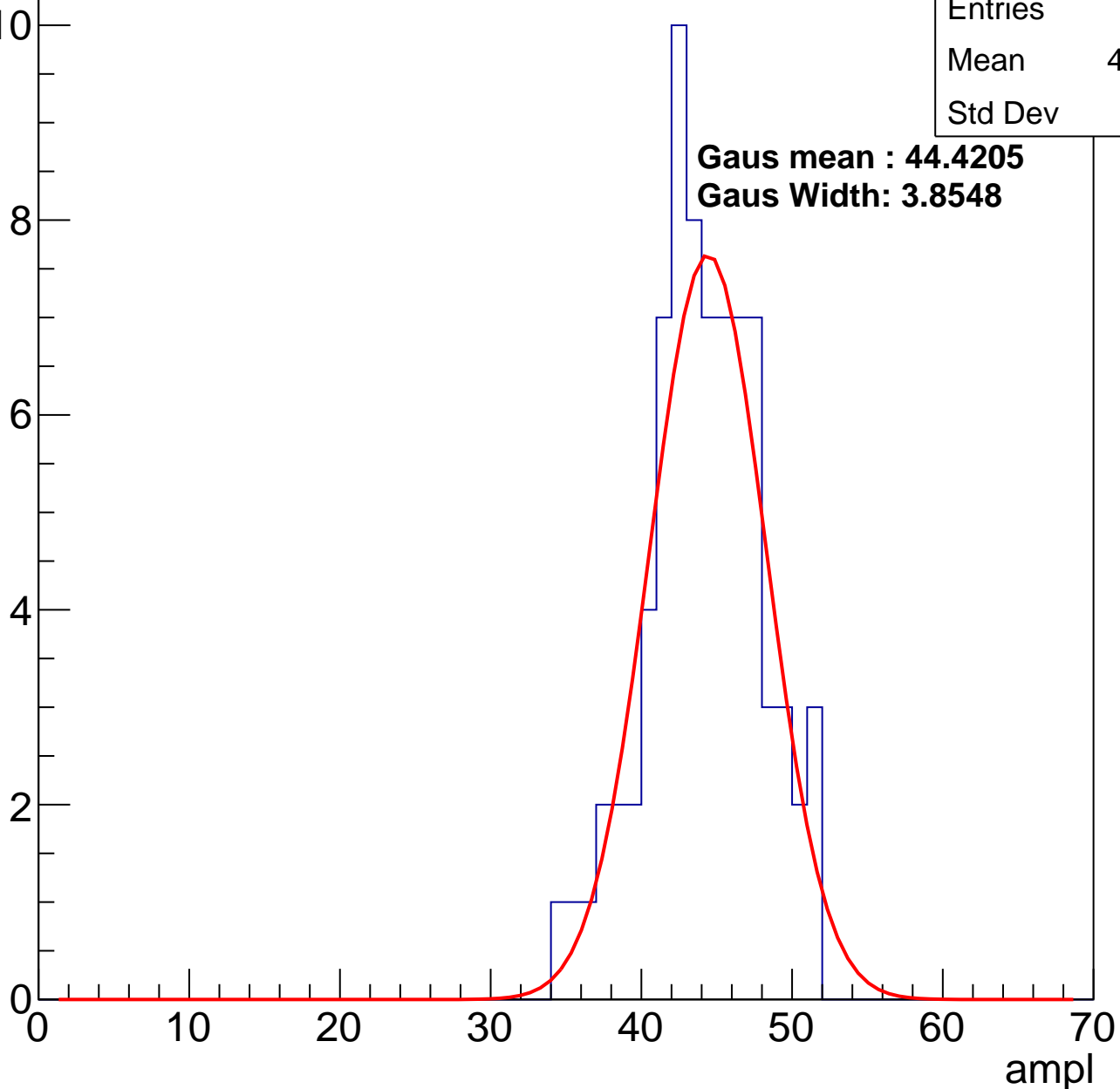
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	43.66
Std Dev	3.73

Gaus mean : 44.4205

Gaus Width: 3.8548

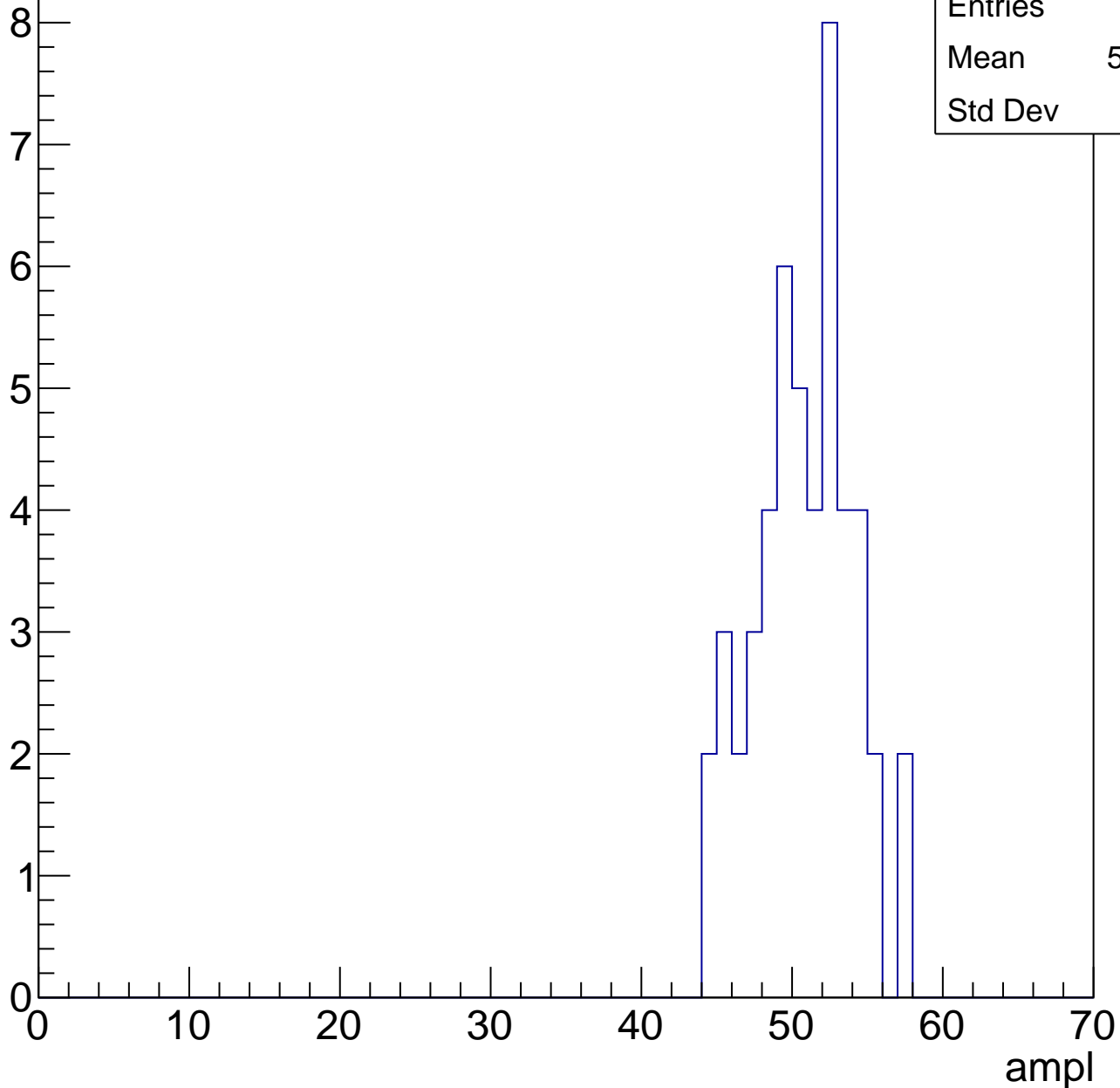


B1L100S, U11-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	50.29
Std Dev	3.22

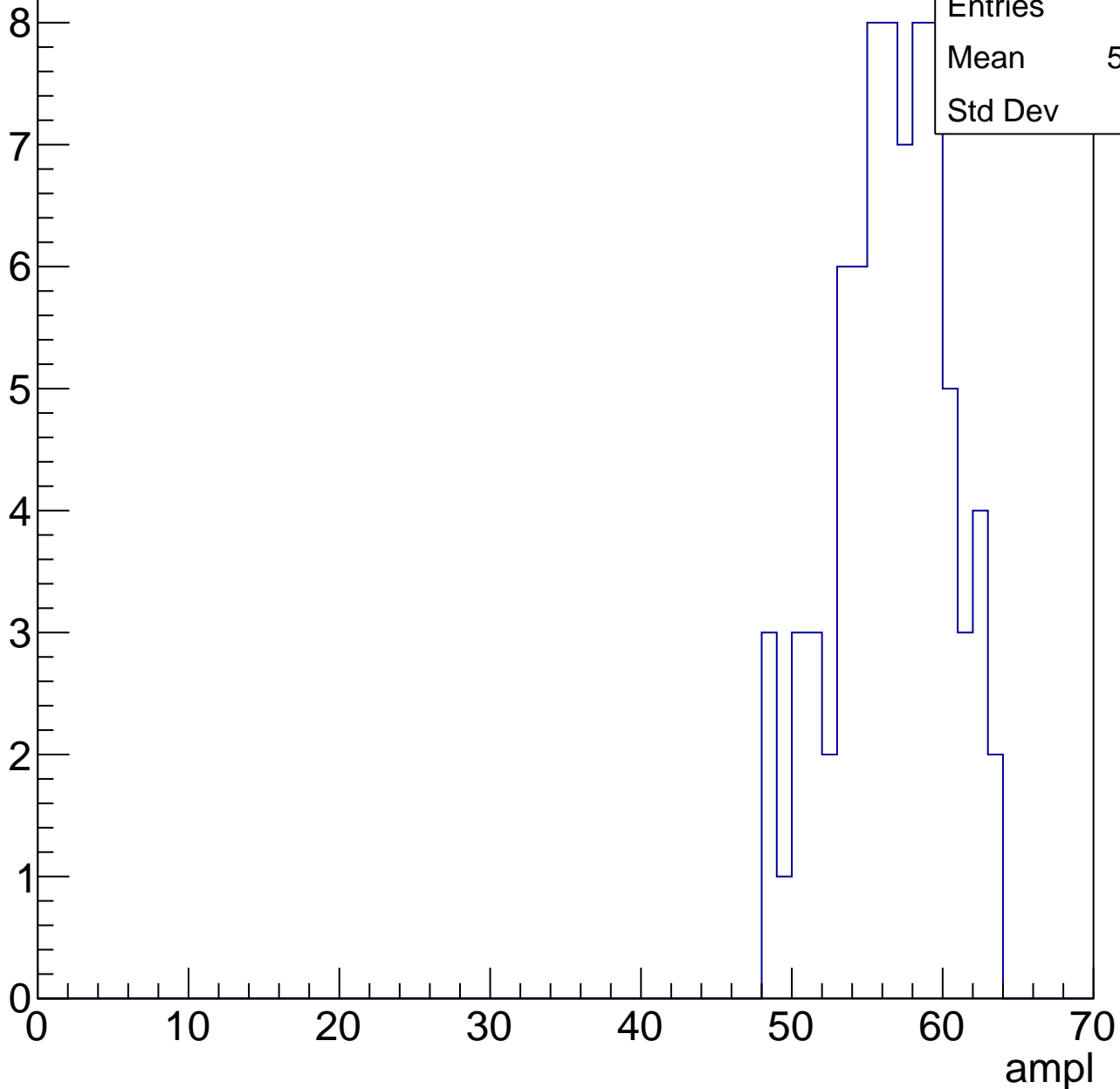


B1L100S, U11-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	56.13
Std Dev	3.69

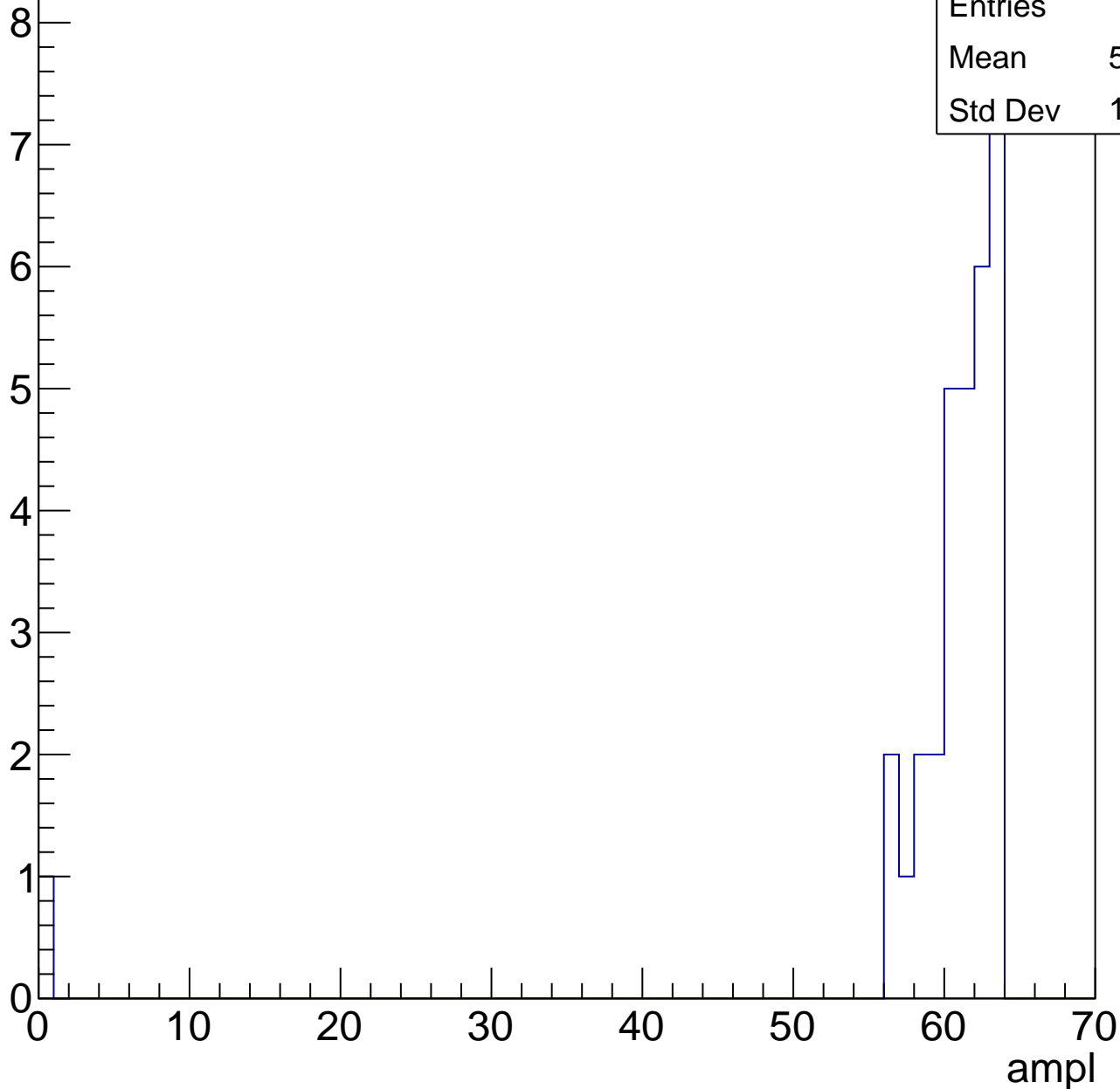


B1L100S, U11-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.88
Std Dev	10.77



B1L100S, U11-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	61
Std Dev	0

ampl

B1L100S, U11-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch123, adc0

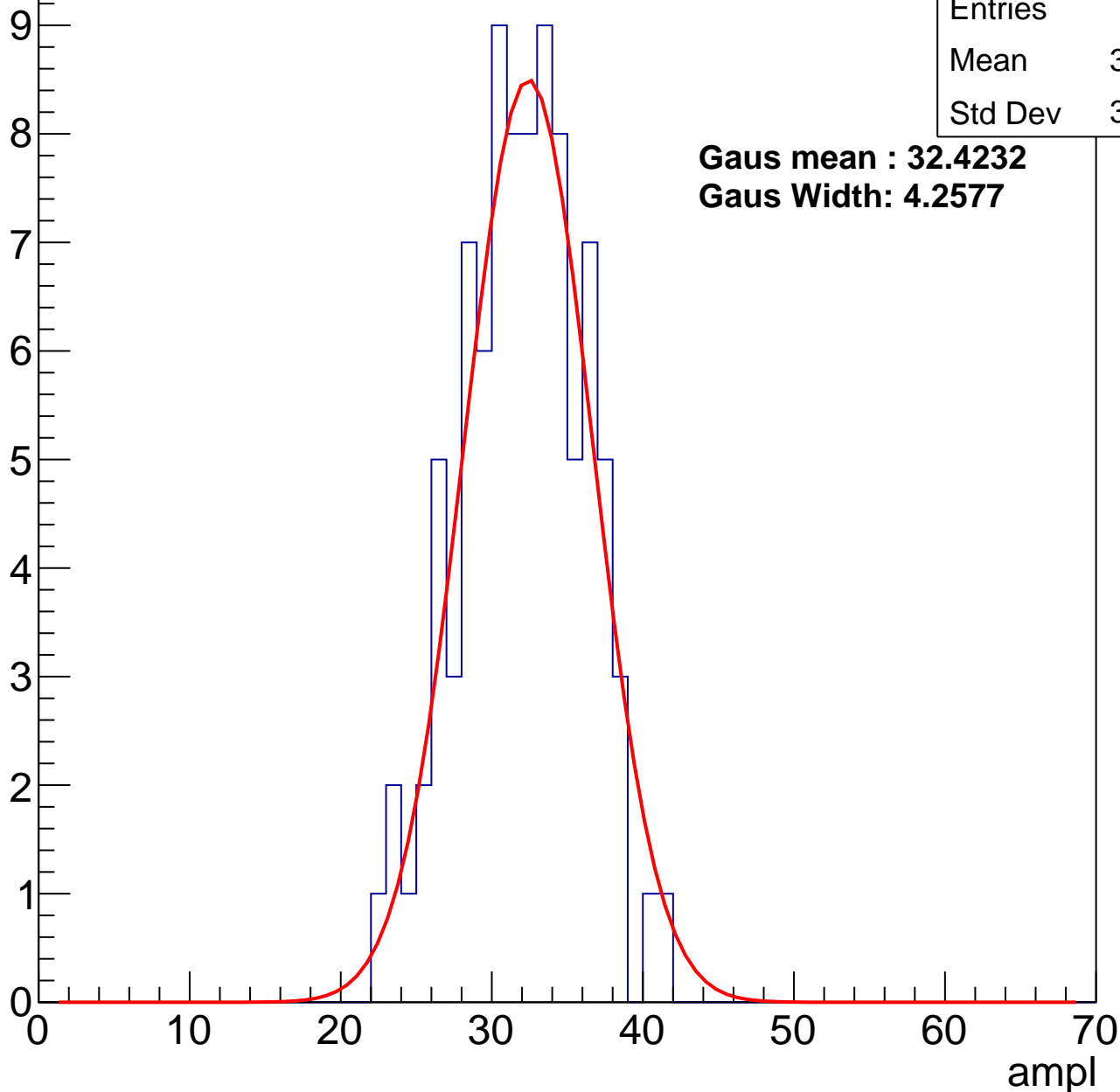
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	91
Mean	31.57
Std Dev	3.989

Gaus mean : 32.4232

Gaus Width: 4.2577



B1L100S, U11-ch123, adc1

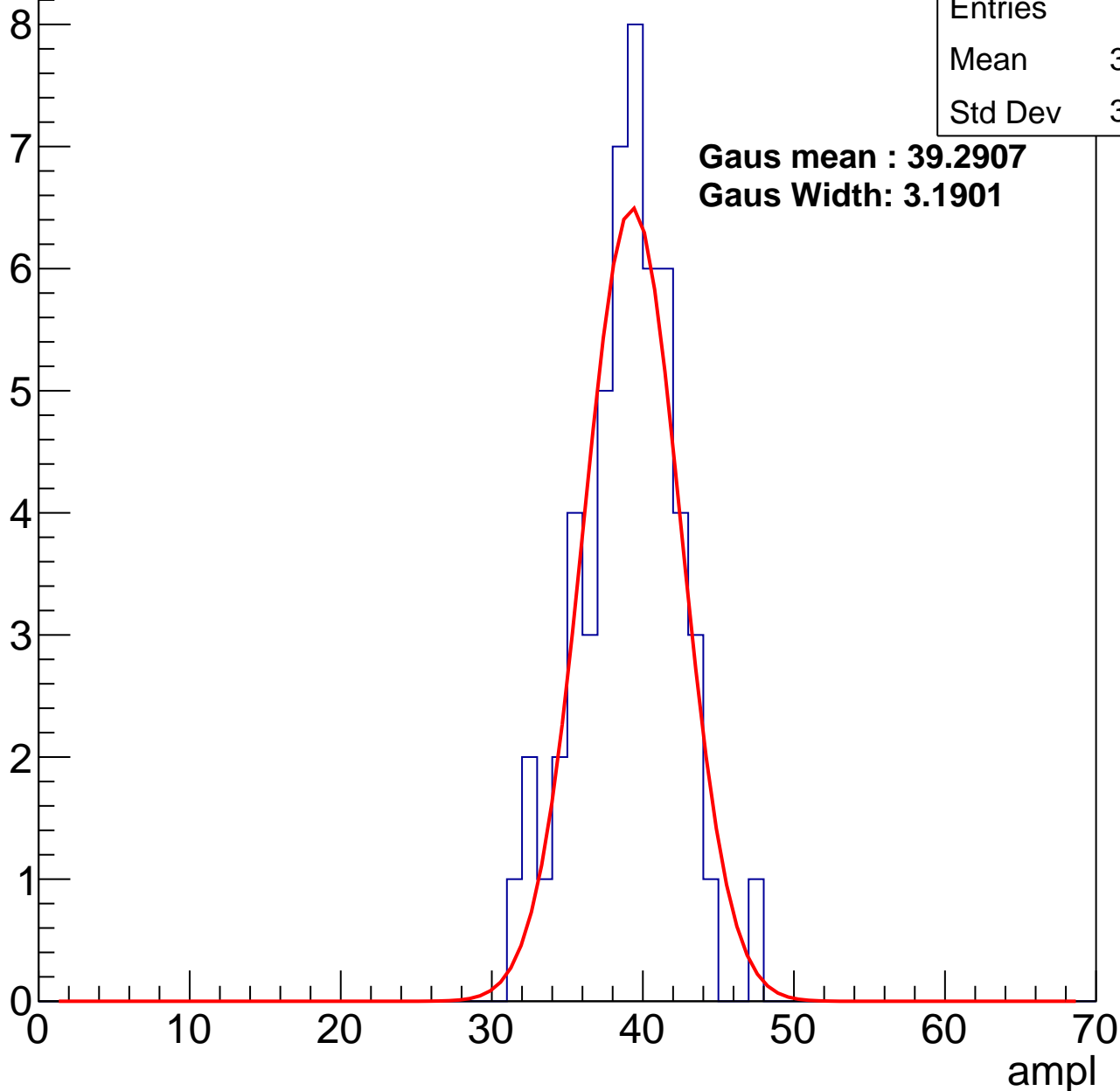
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	38.54
Std Dev	3.213

Gaus mean : 39.2907

Gaus Width: 3.1901



B1L100S, U11-ch123, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	45.06
Std Dev	3.268

Gaus mean : 45.3557

Gaus Width: 3.1580

Entry

10

8

6

4

2

0

0

10

20

30

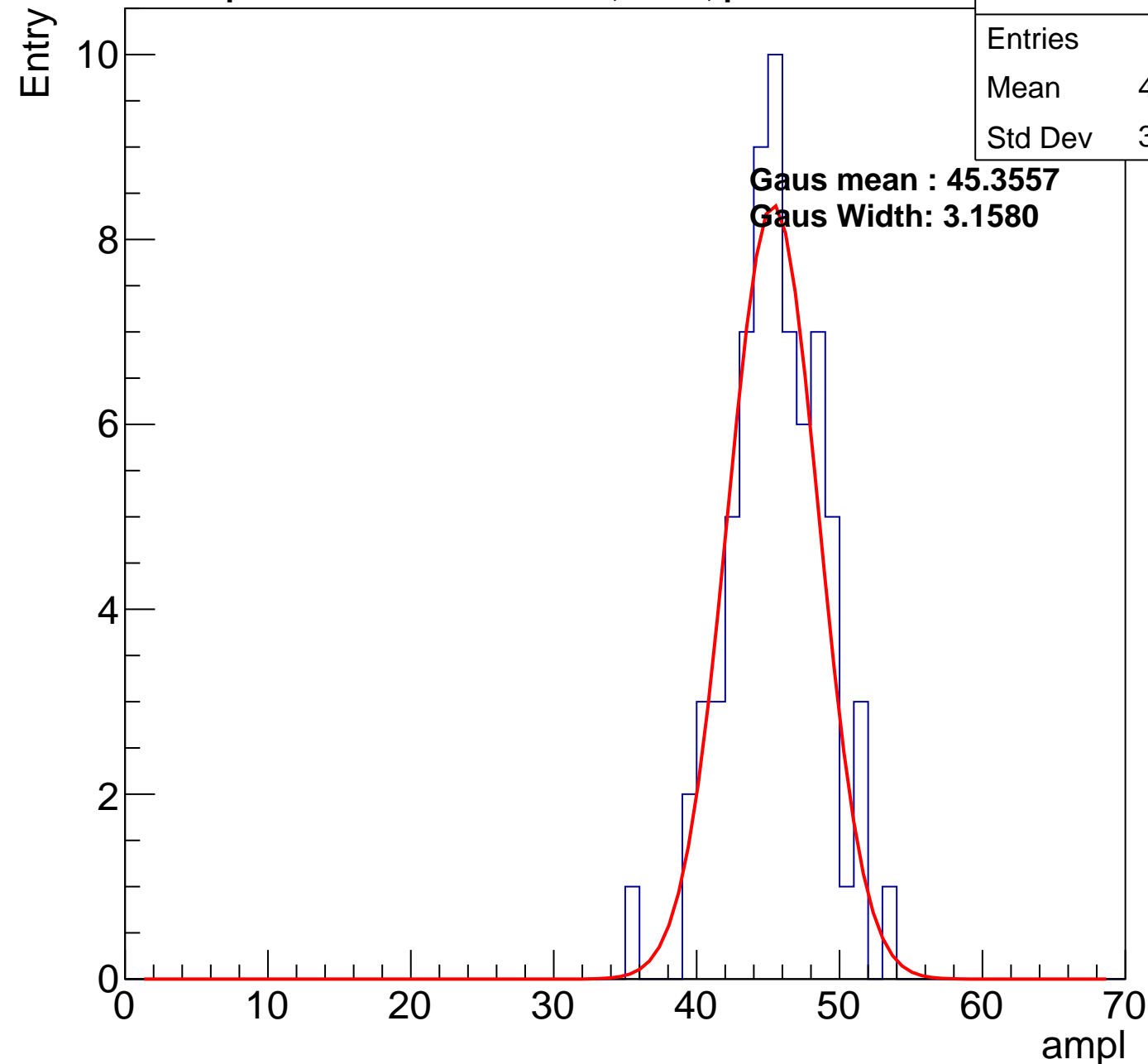
40

50

60

70

ampl



B1L100S, U11-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	52.26
Std Dev	3.663

Entry

10

8

6

4

2

0

0

10

20

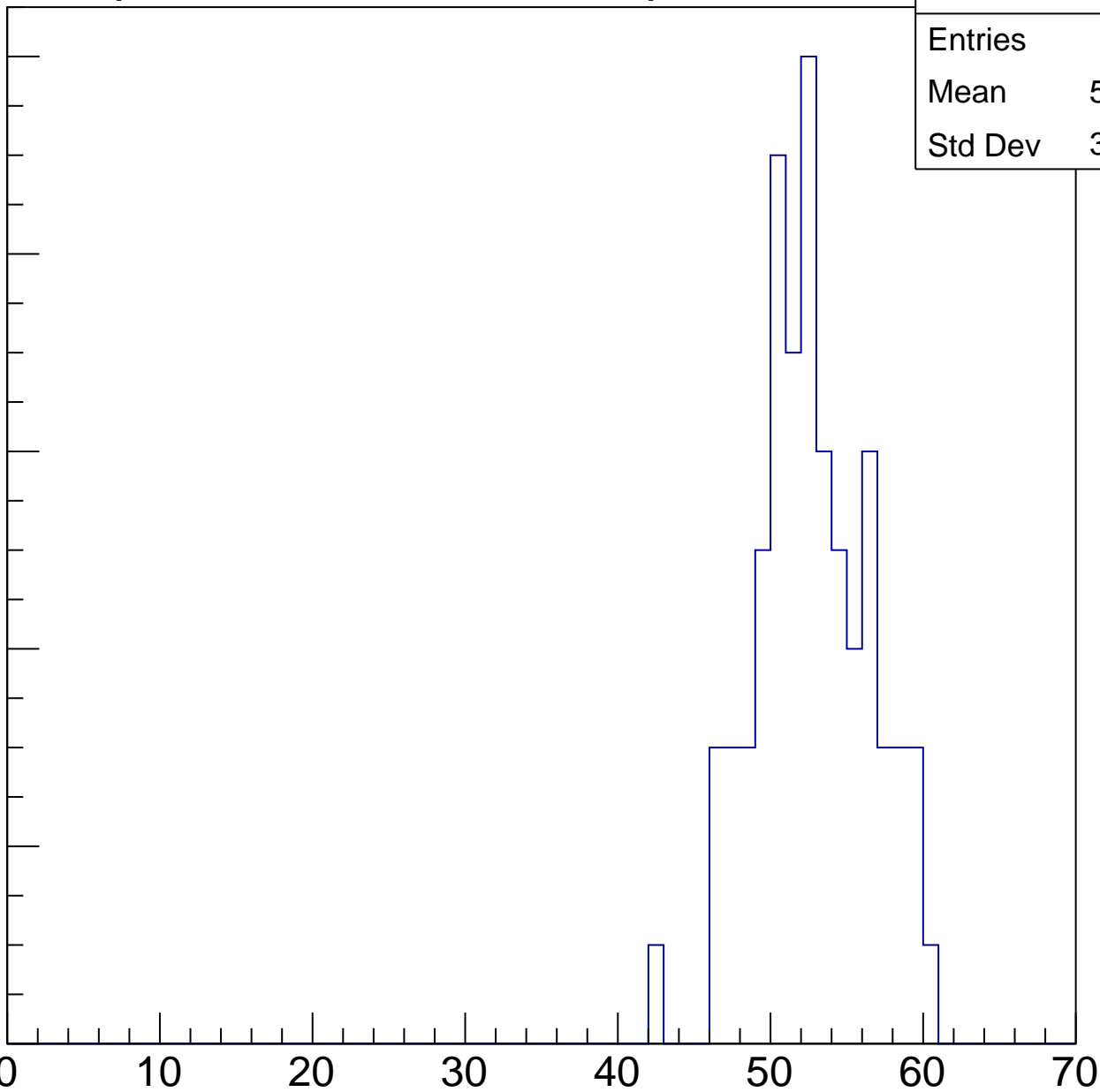
30

40

50

60

ampl

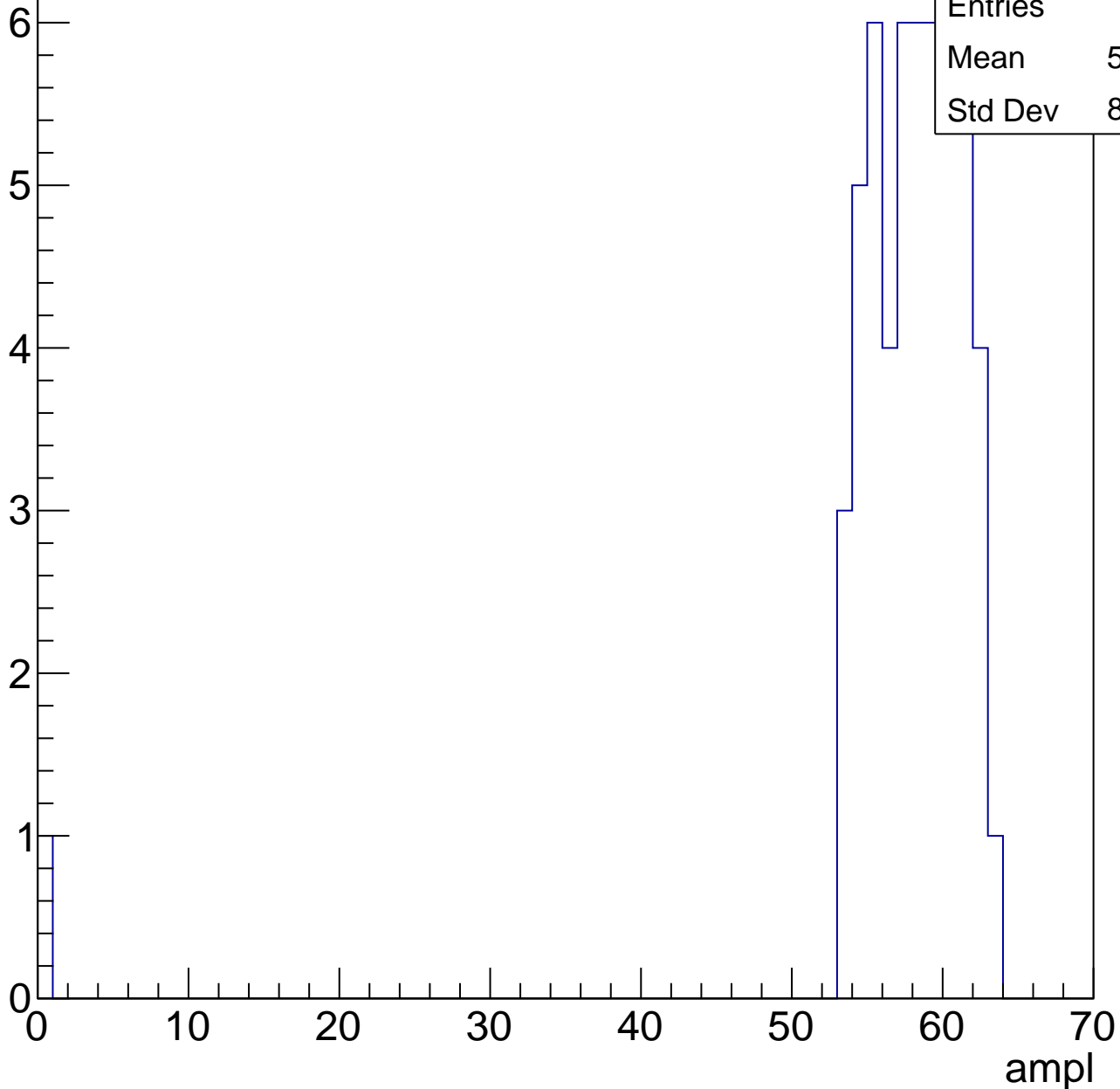


B1L100S, U11-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	56.74
Std Dev	8.258

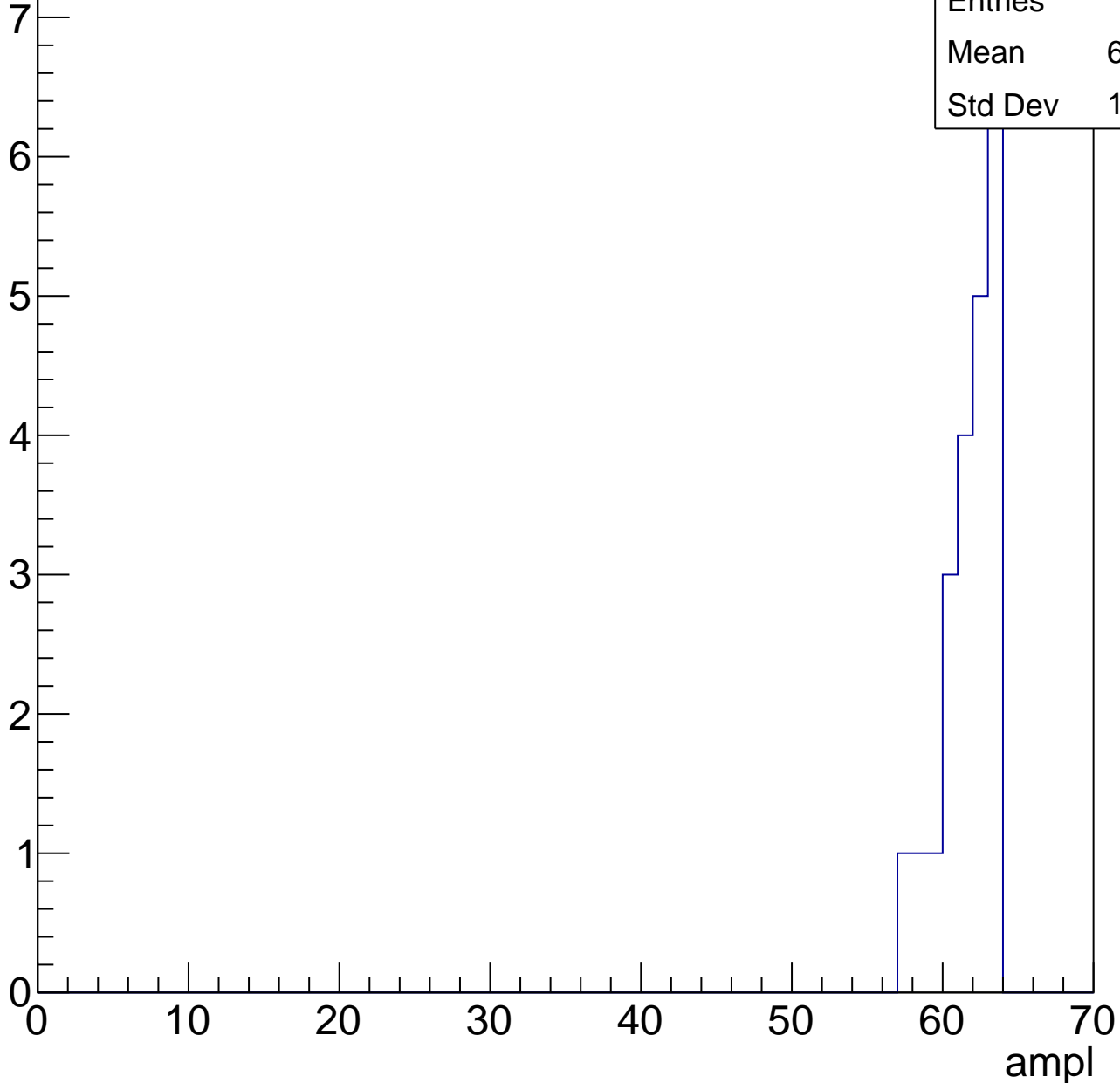


B1L100S, U11-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	61.32
Std Dev	1.689



B1L100S, U11-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

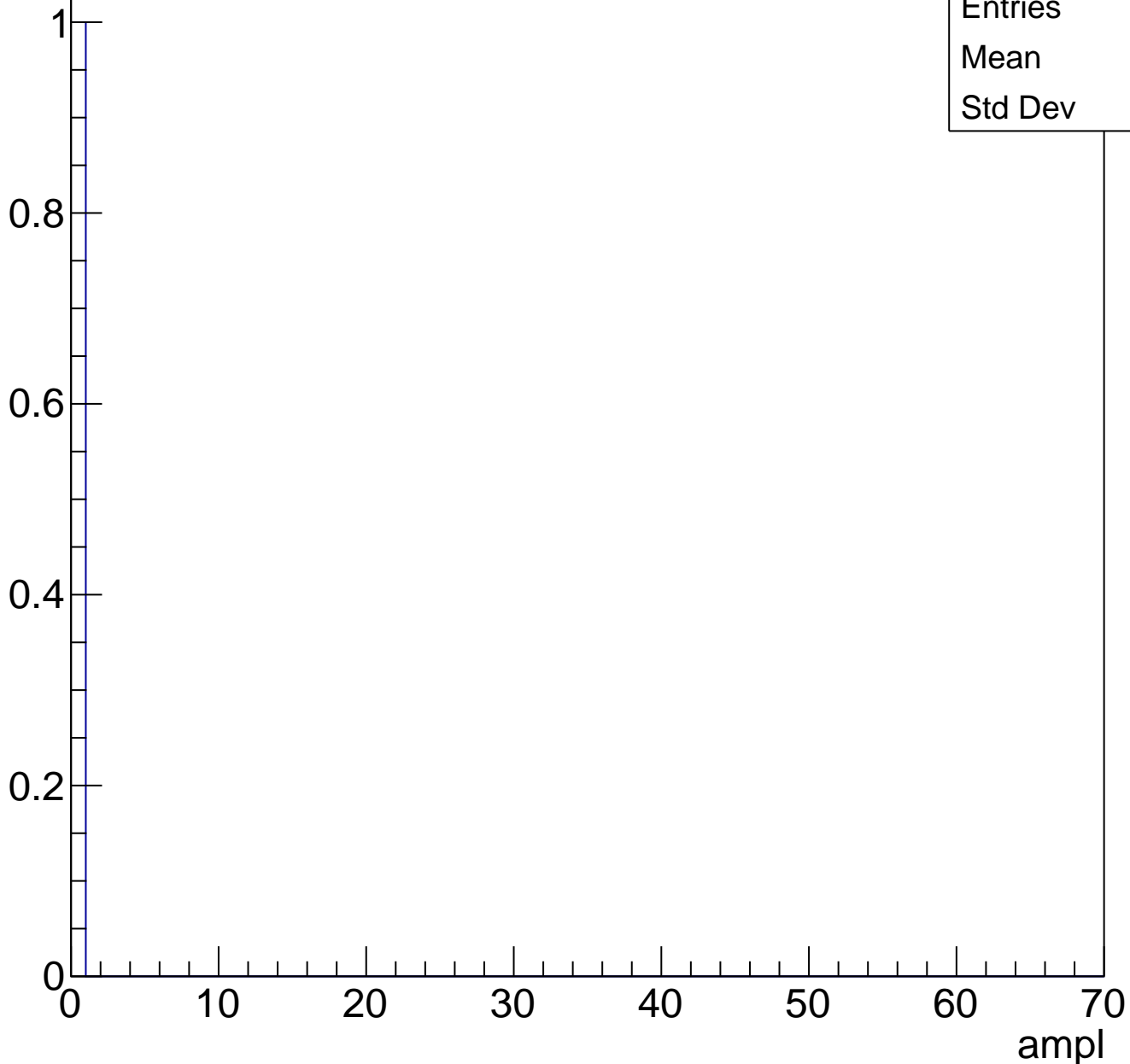
Entry



B1L100S, U11-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch124, adc0

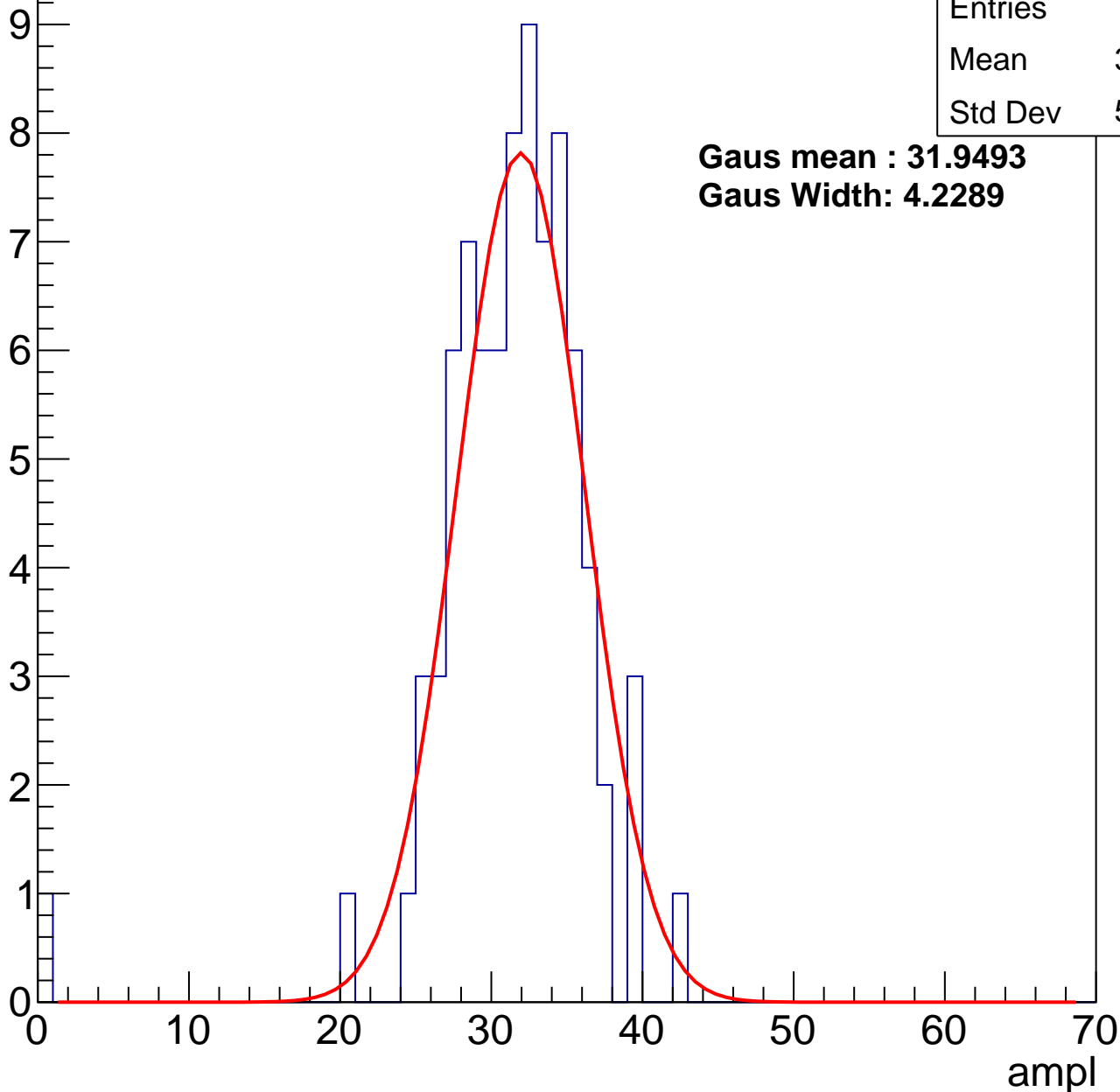
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	30.91
Std Dev	5.171

Gaus mean : 31.9493

Gaus Width: 4.2289



B1L100S, U11-ch124, adc1

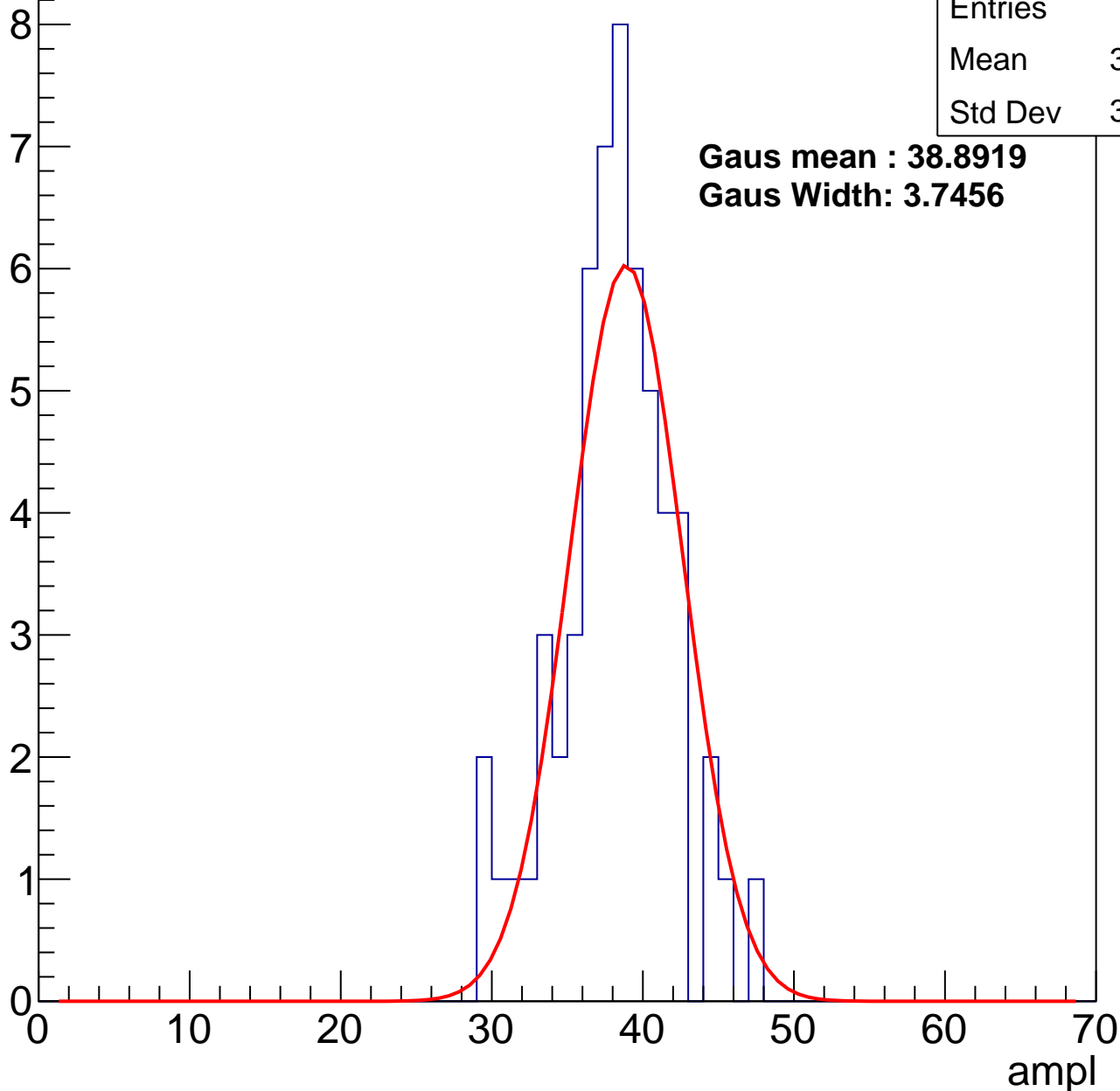
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	37.68
Std Dev	3.742

Gaus mean : 38.8919

Gaus Width: 3.7456



B1L100S, U11-ch124, adc2

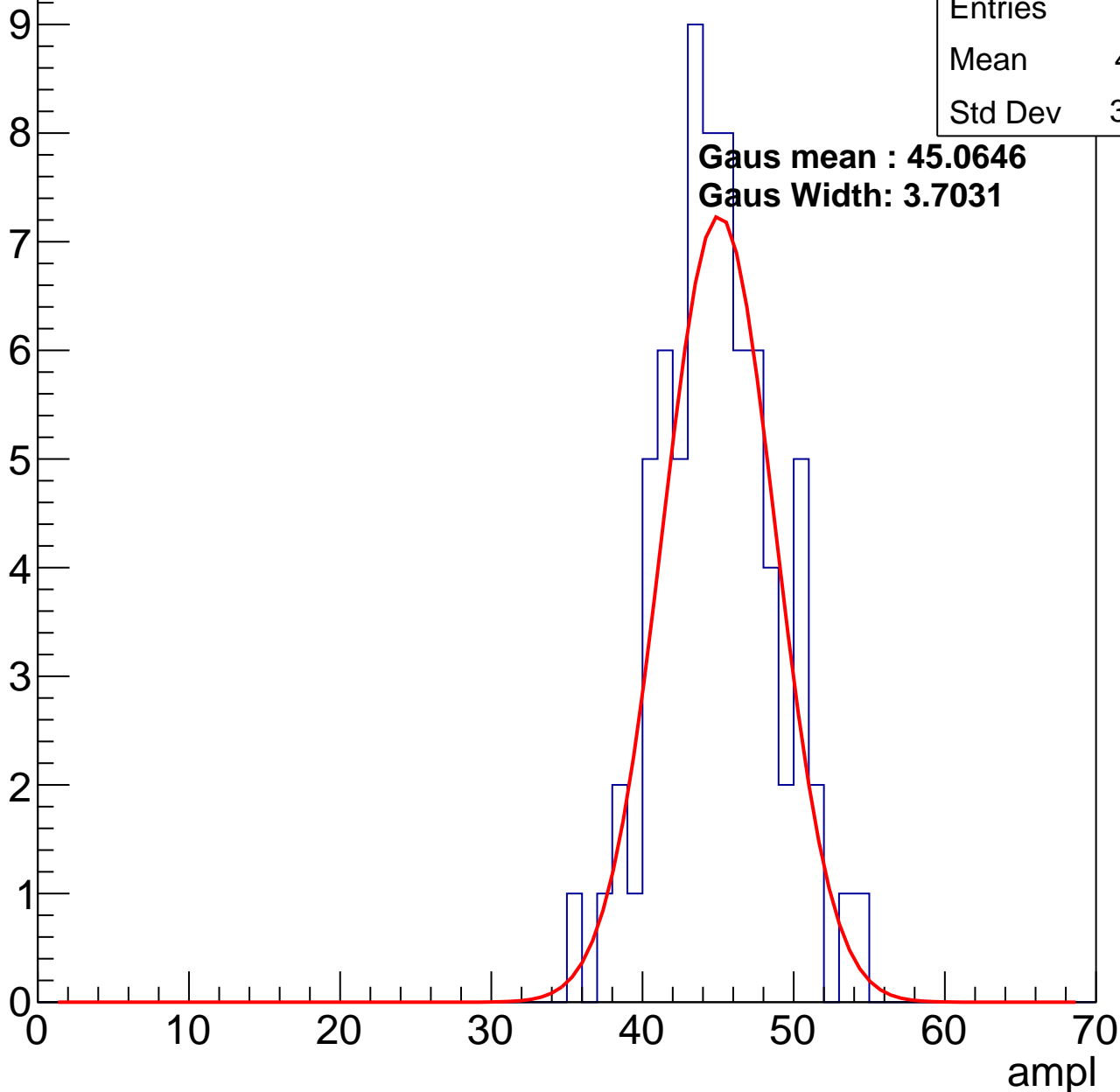
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	44.51
Std Dev	3.757

Gaus mean : 45.0646

Gaus Width: 3.7031

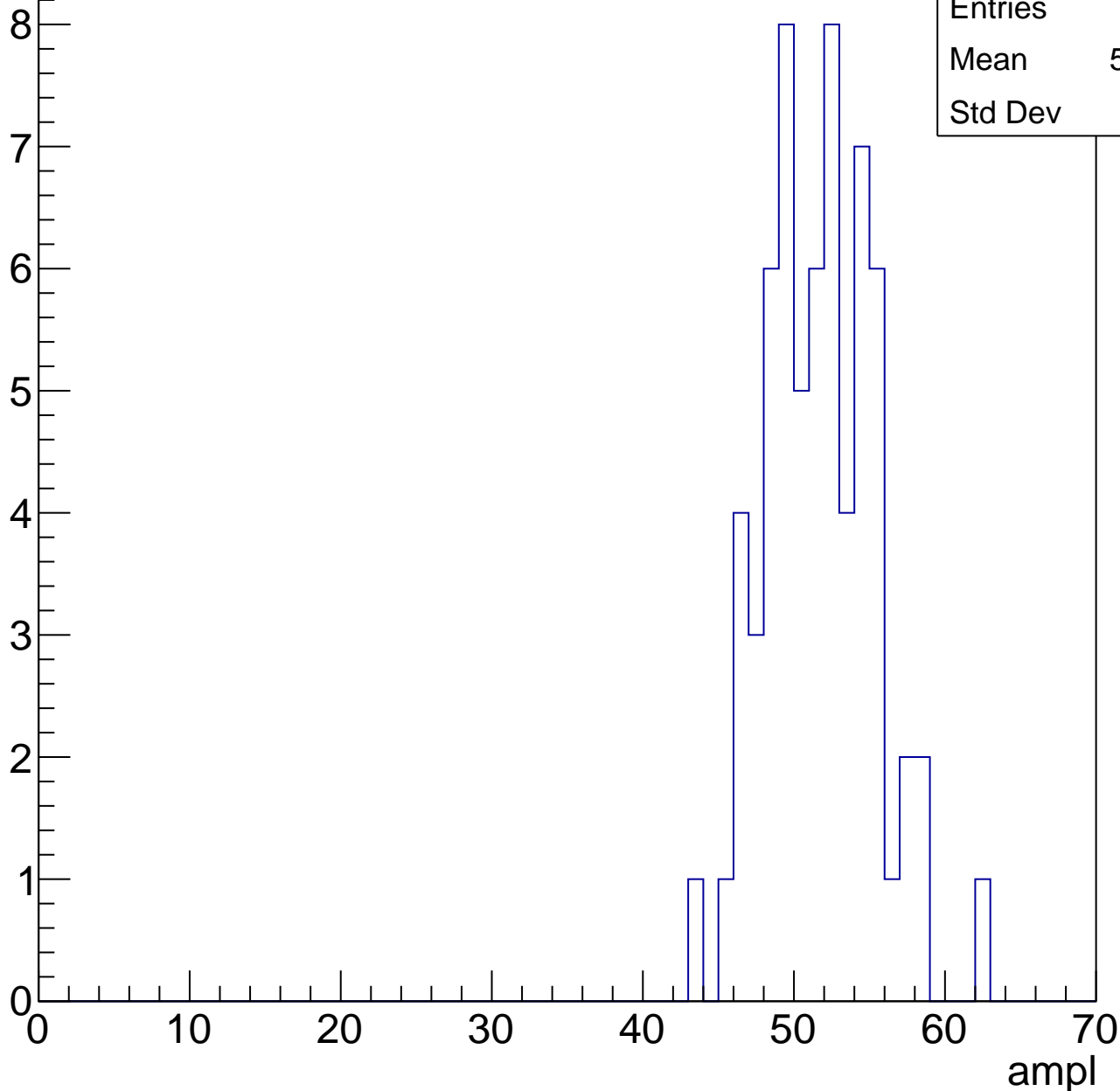


B1L100S, U11-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

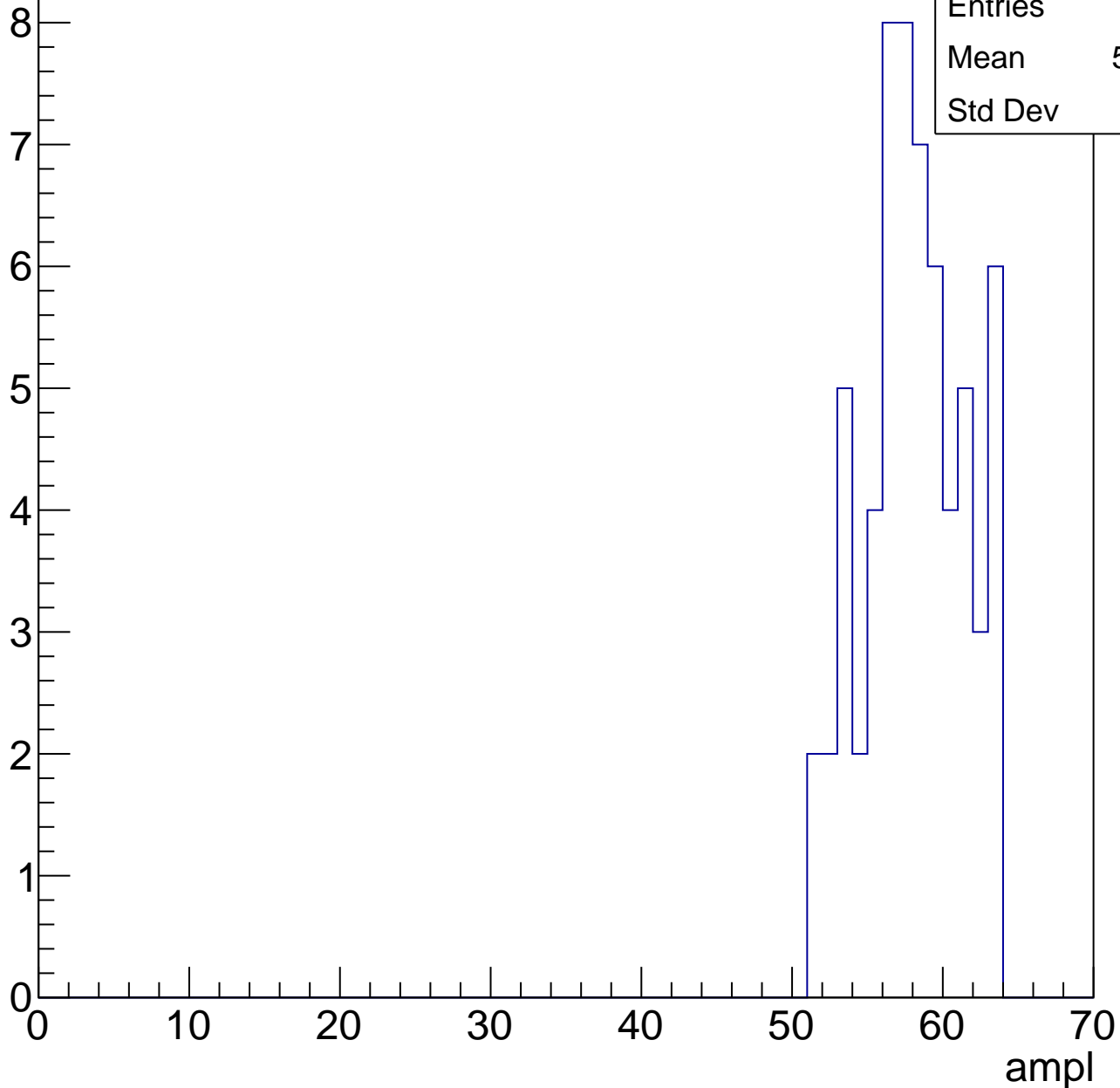
Entries	65
Mean	51.28
Std Dev	3.58



B1L100S, U11-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

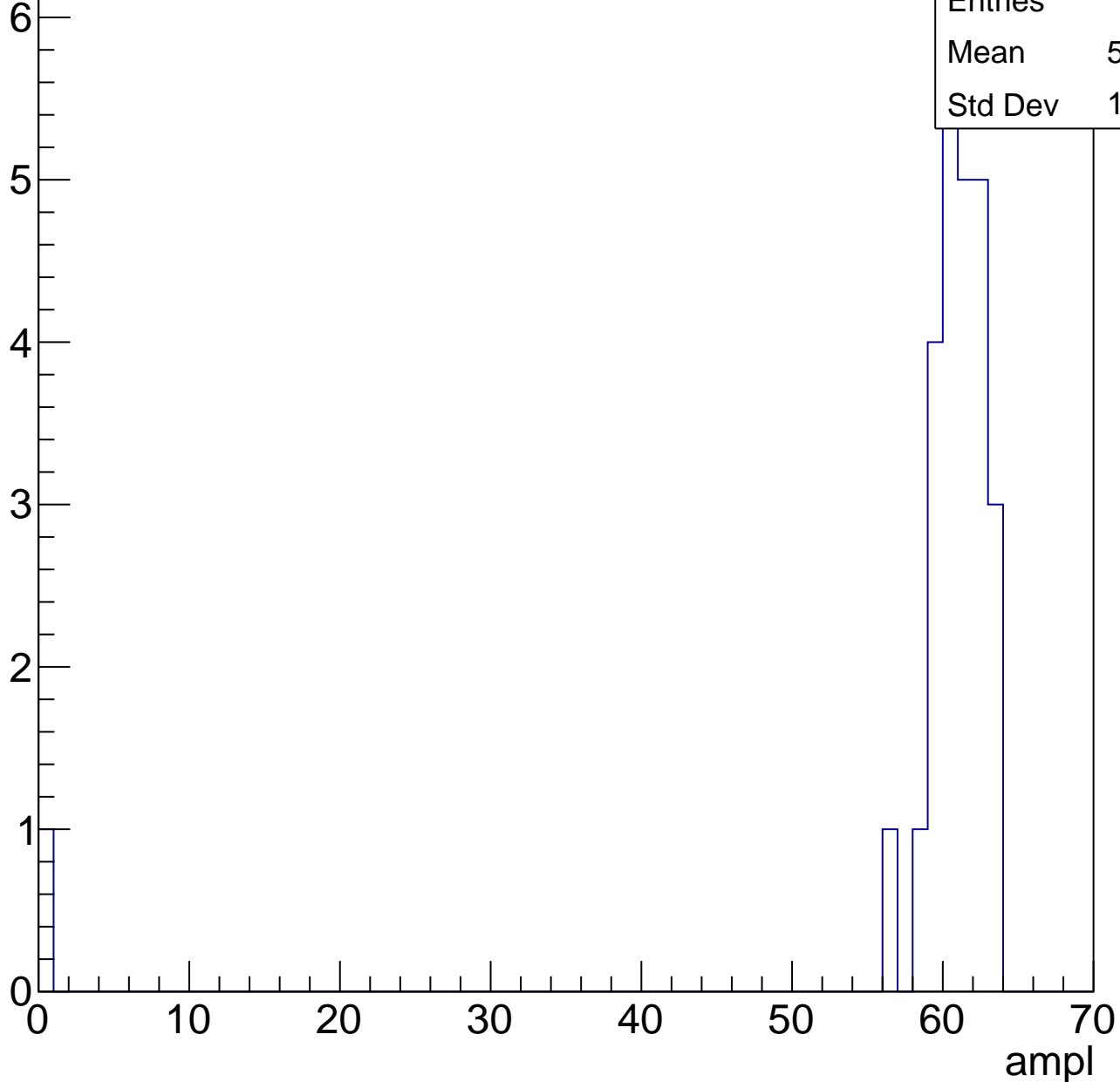


Entries	62
Mean	57.61
Std Dev	3.26

B1L100S, U11-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U11-ch125, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	28.88
Std Dev	5.058

Gaus mean : 30.6032

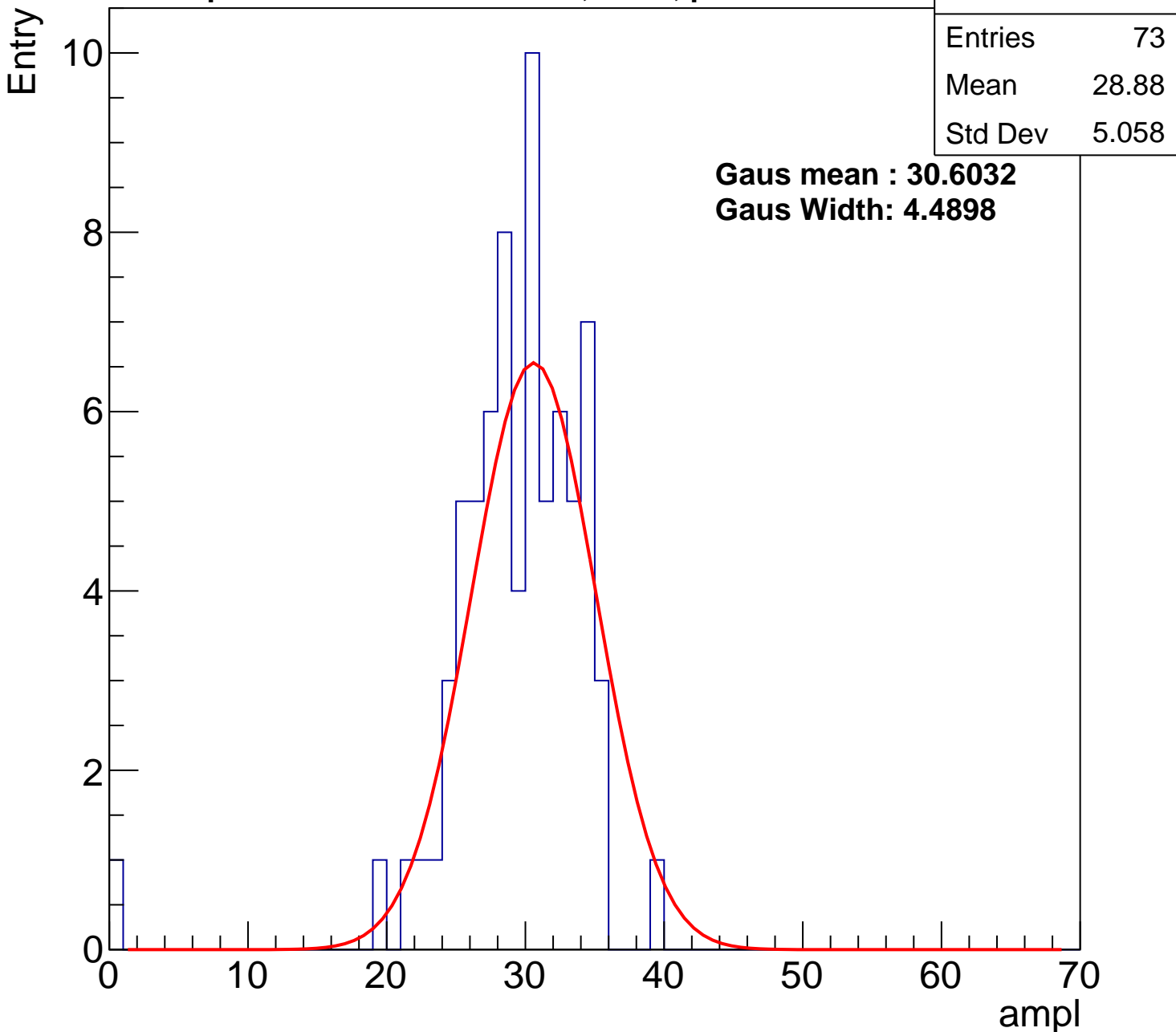
Gaus Width: 4.4898

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U11-ch125, adc1

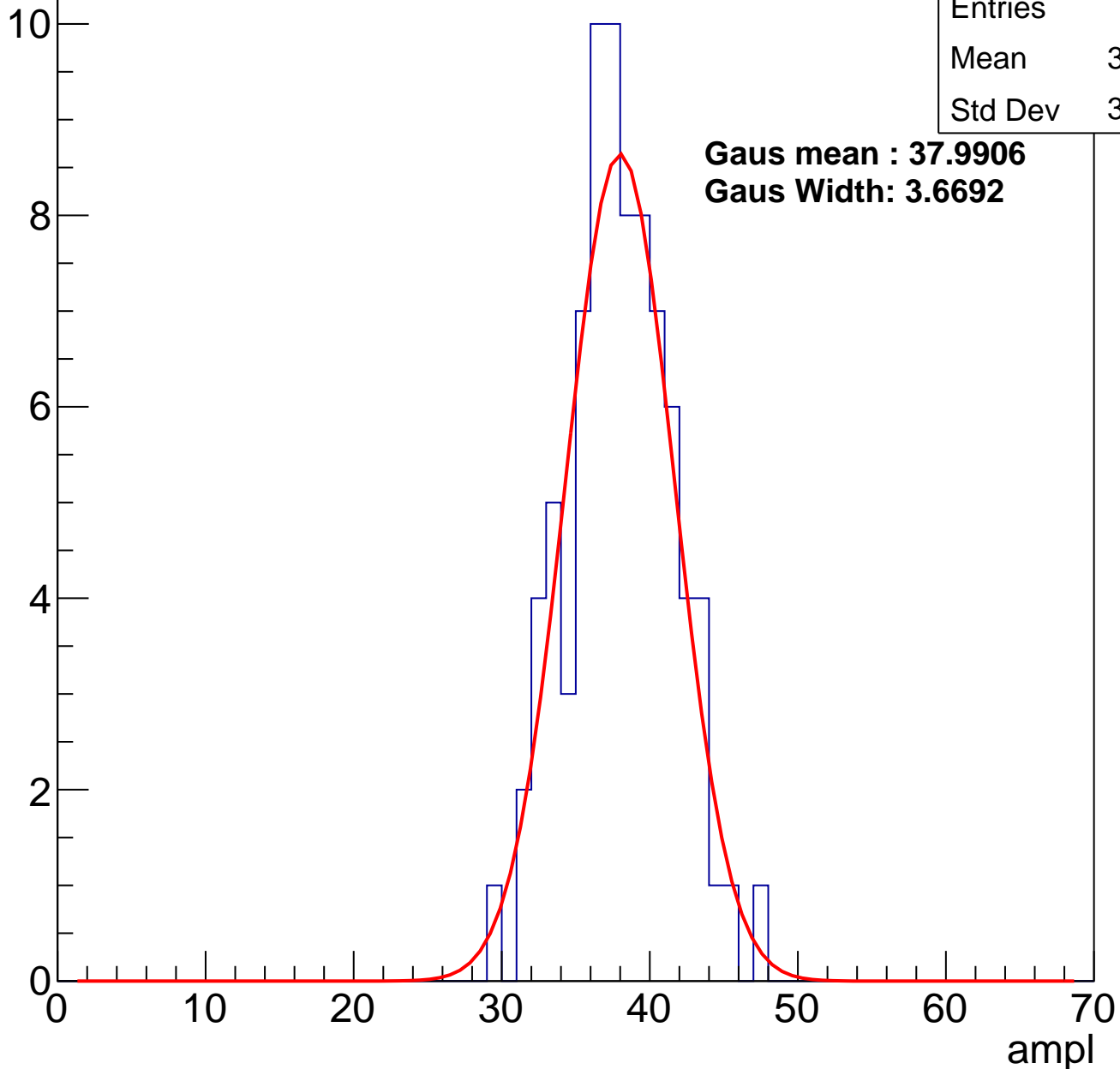
calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	37.55
Std Dev	3.514

Gaus mean : 37.9906

Gaus Width: 3.6692

Entry



B1L100S, U11-ch125, adc2

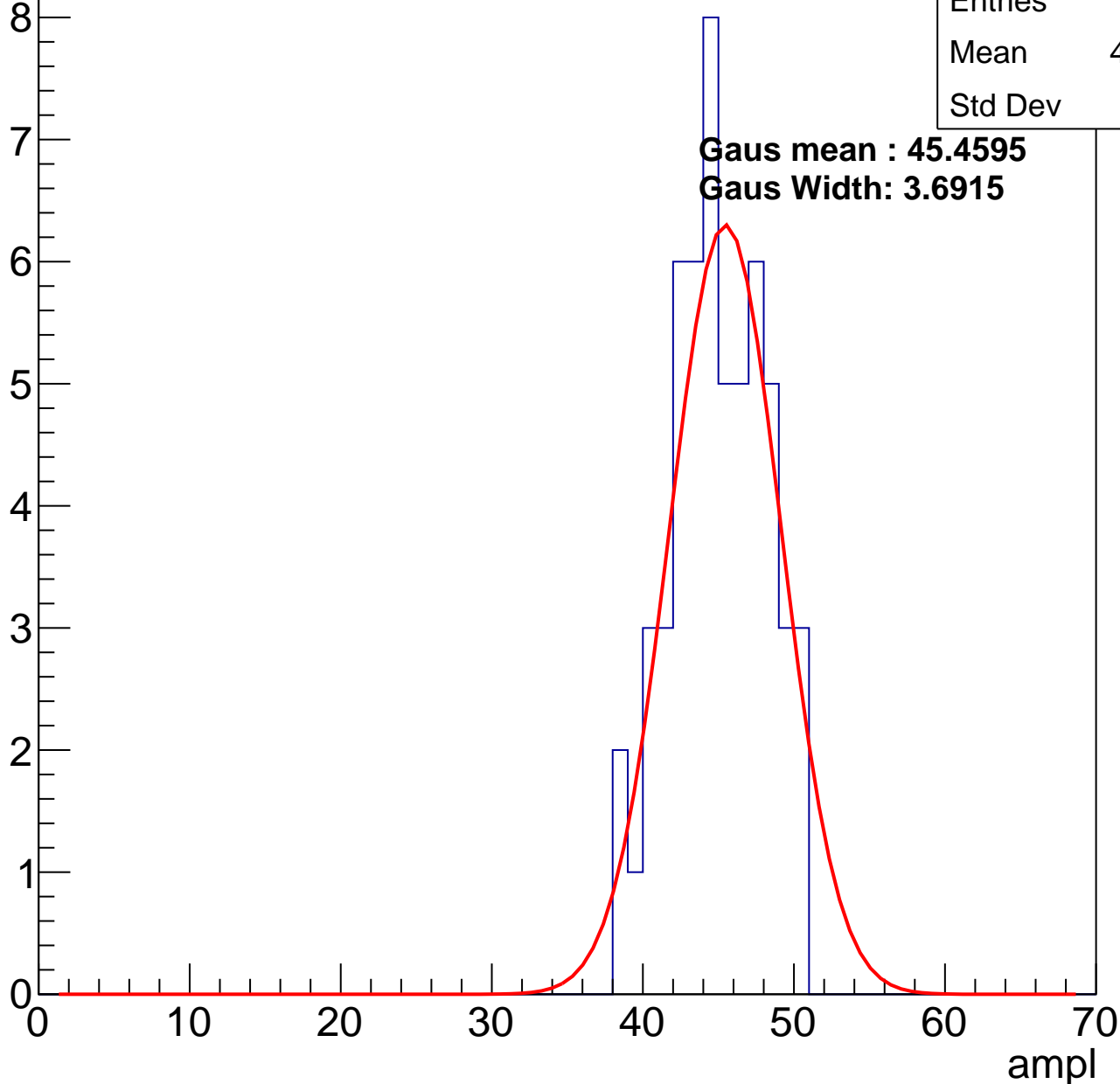
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.54
Std Dev	3.07

Gaus mean : 45.4595

Gaus Width: 3.6915

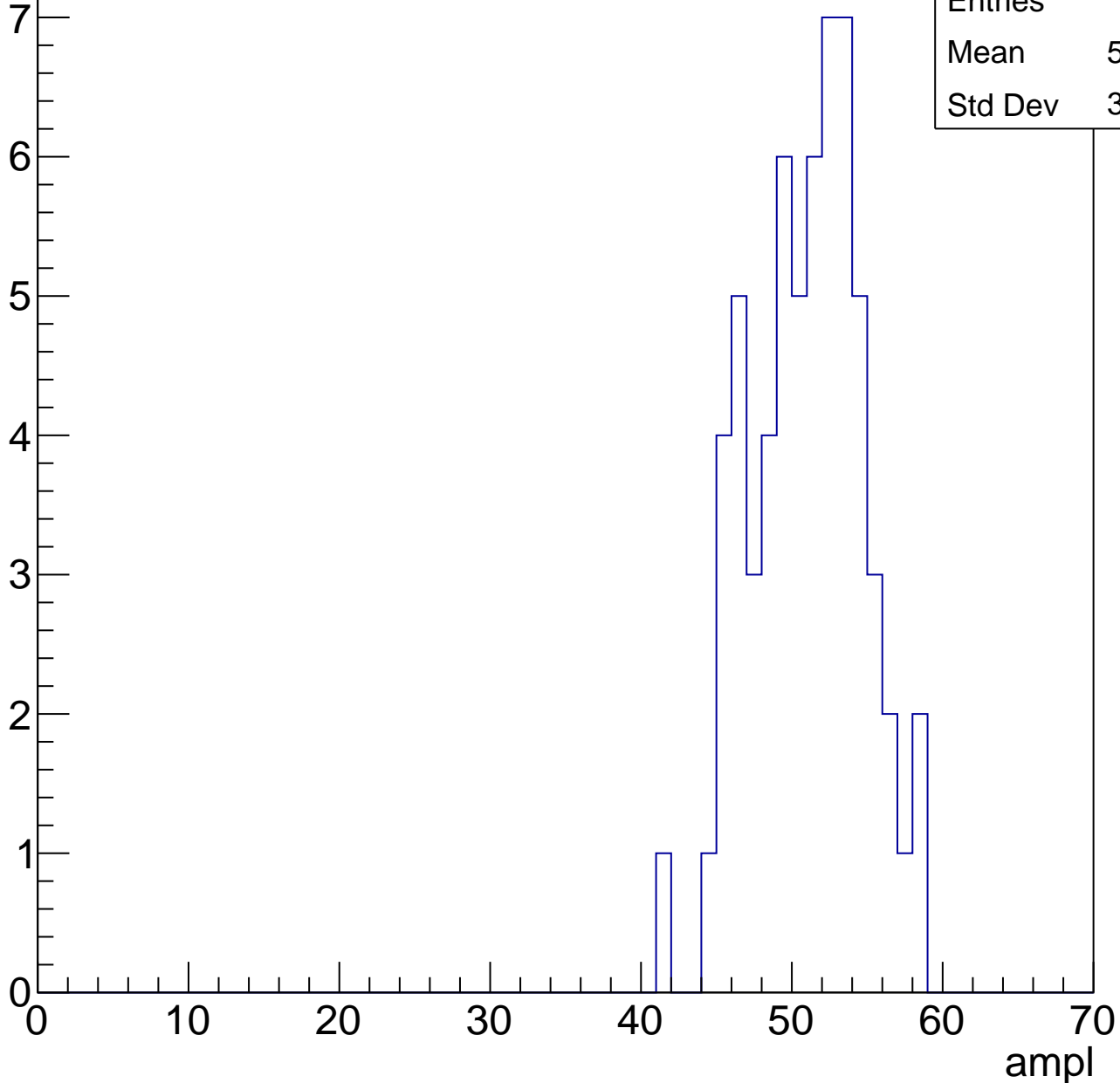


B1L100S, U11-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

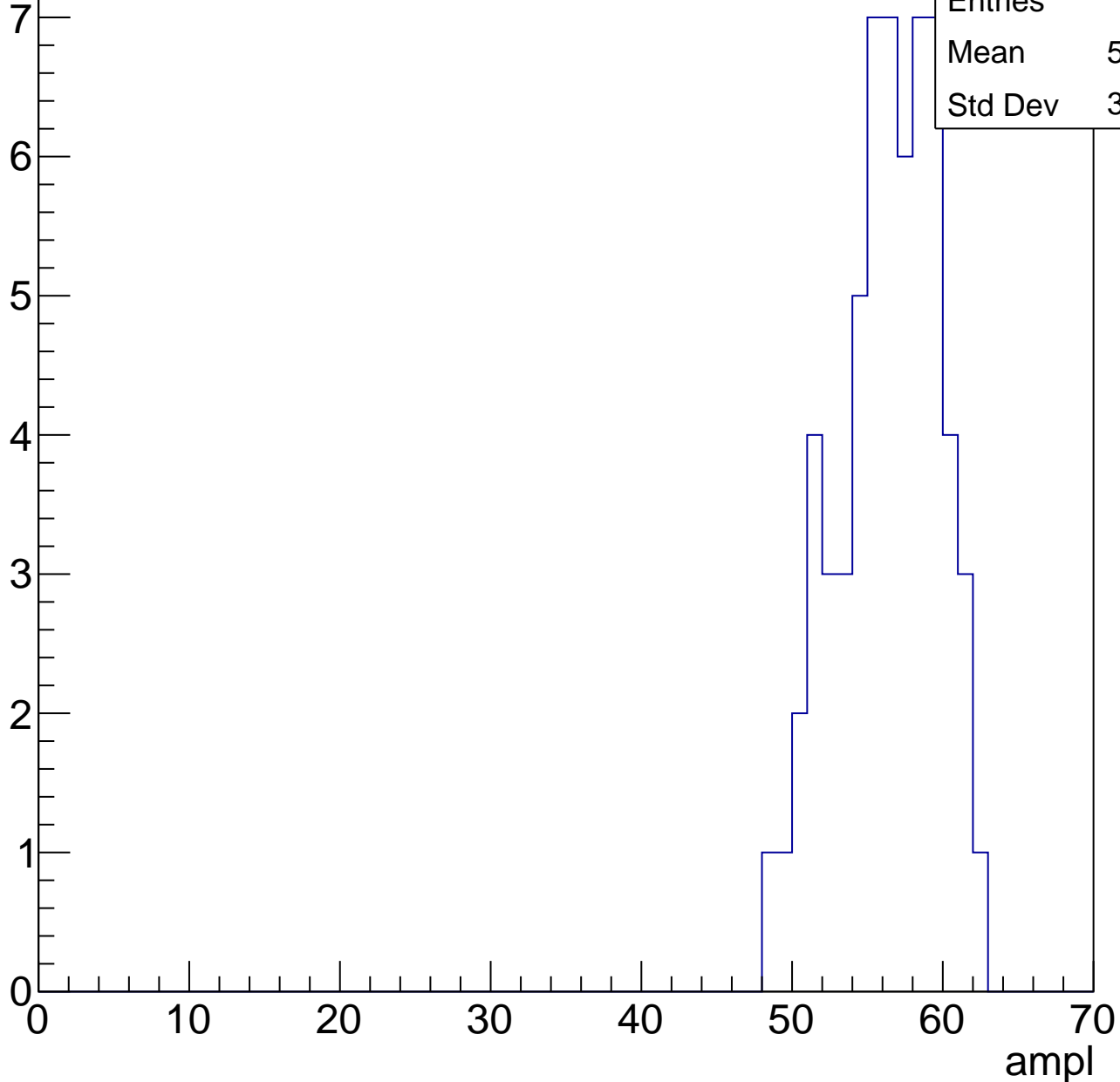
Entries	62
Mean	50.53
Std Dev	3.658



B1L100S, U11-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



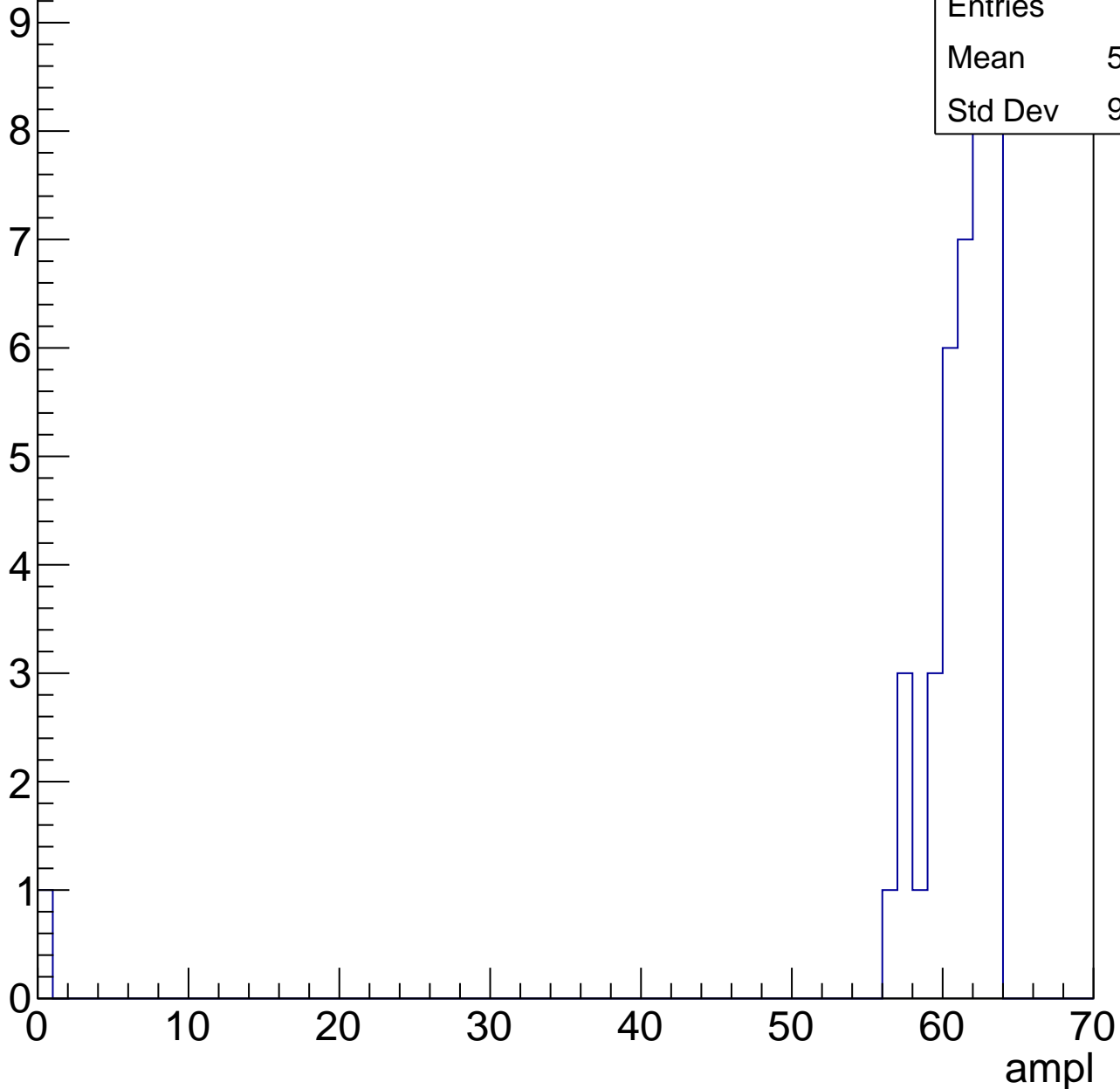
Entries	61
Mean	55.89
Std Dev	3.285

B1L100S, U11-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	59.26
Std Dev	9.797



B1L100S, U11-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U11-ch126, adc0

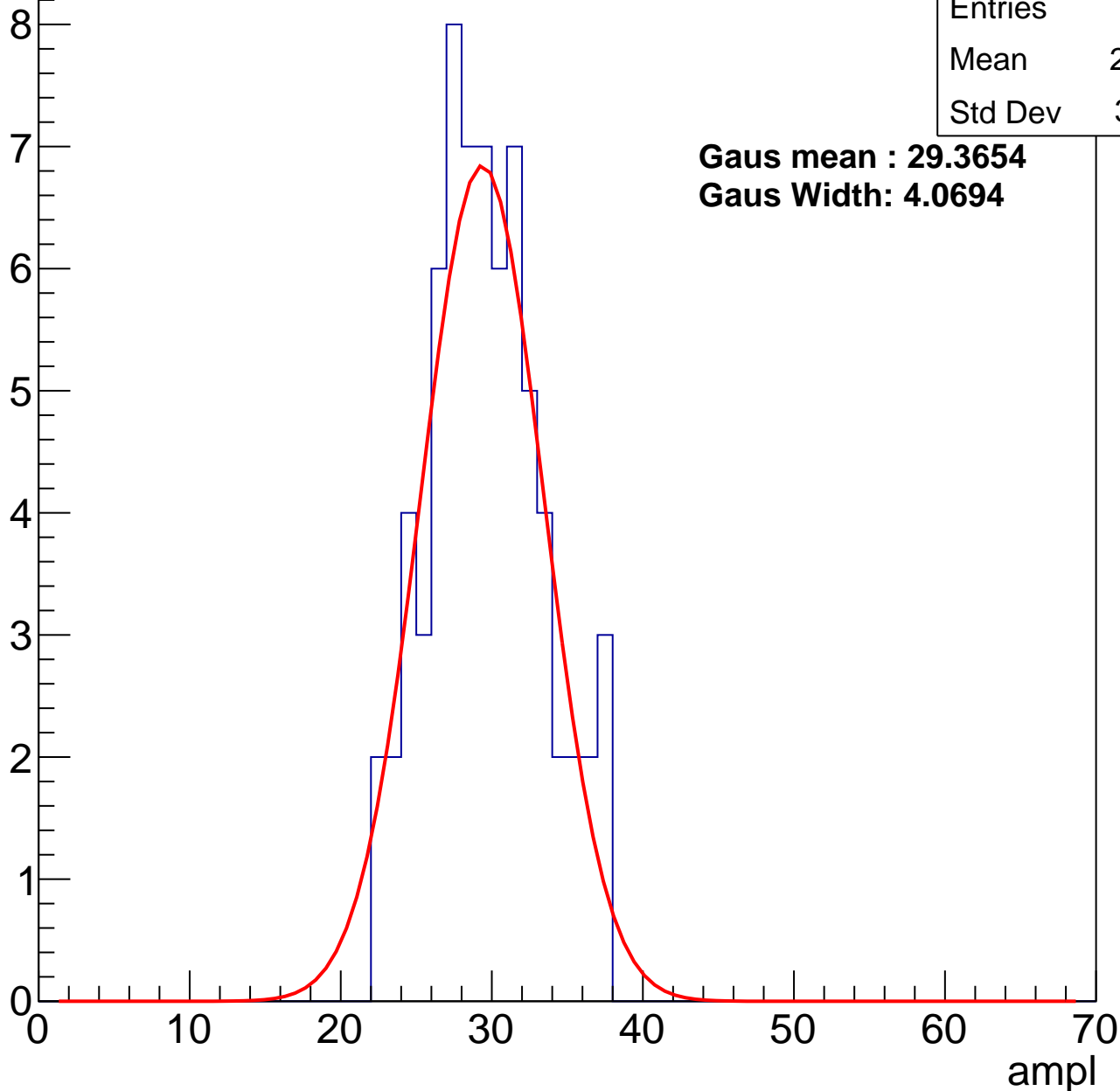
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.17
Std Dev	3.711

Gaus mean : 29.3654

Gaus Width: 4.0694



B1L100S, U11-ch126, adc1

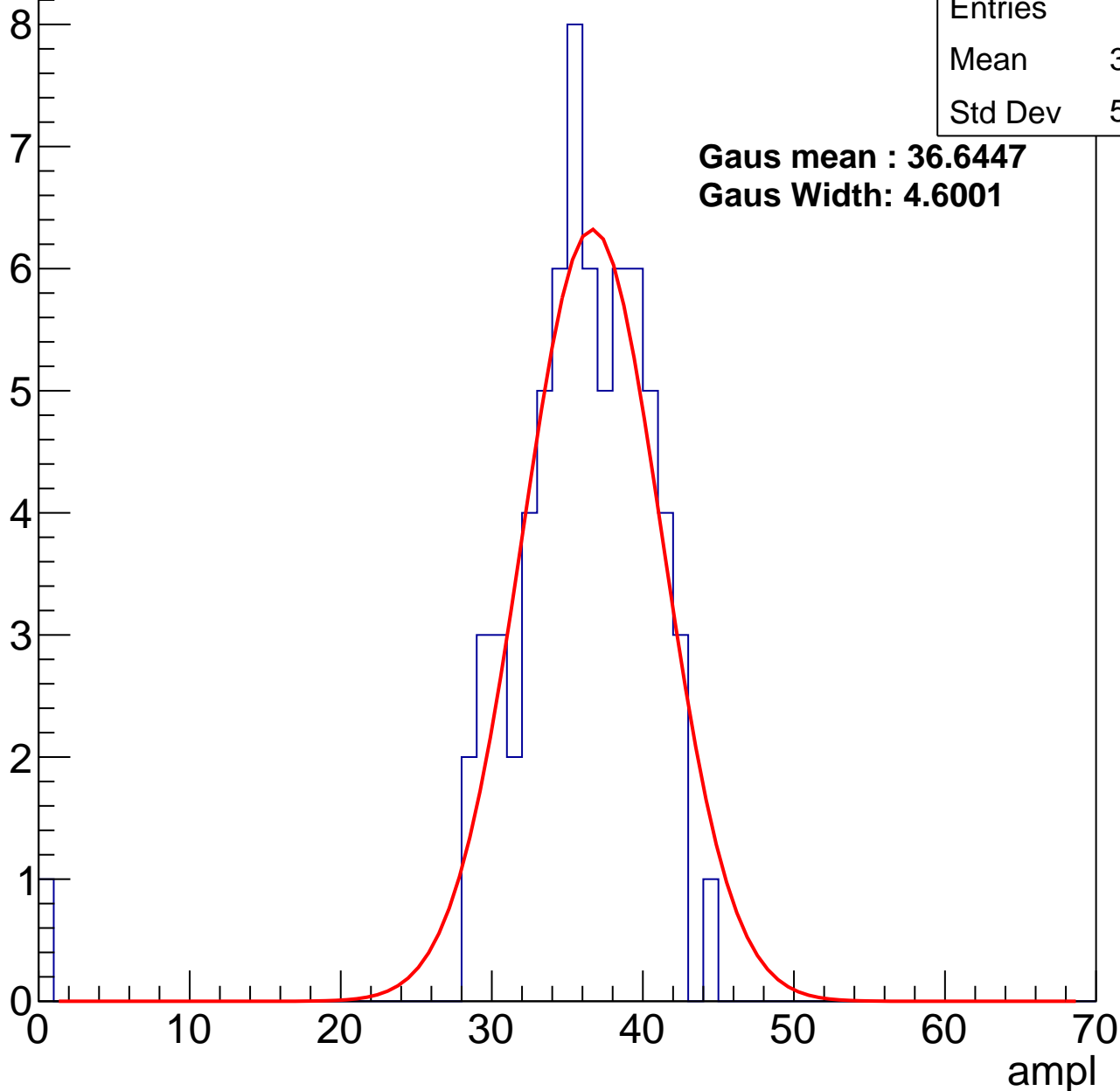
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.27
Std Dev	5.692

Gaus mean : 36.6447

Gaus Width: 4.6001



B1L100S, U11-ch126, adc2

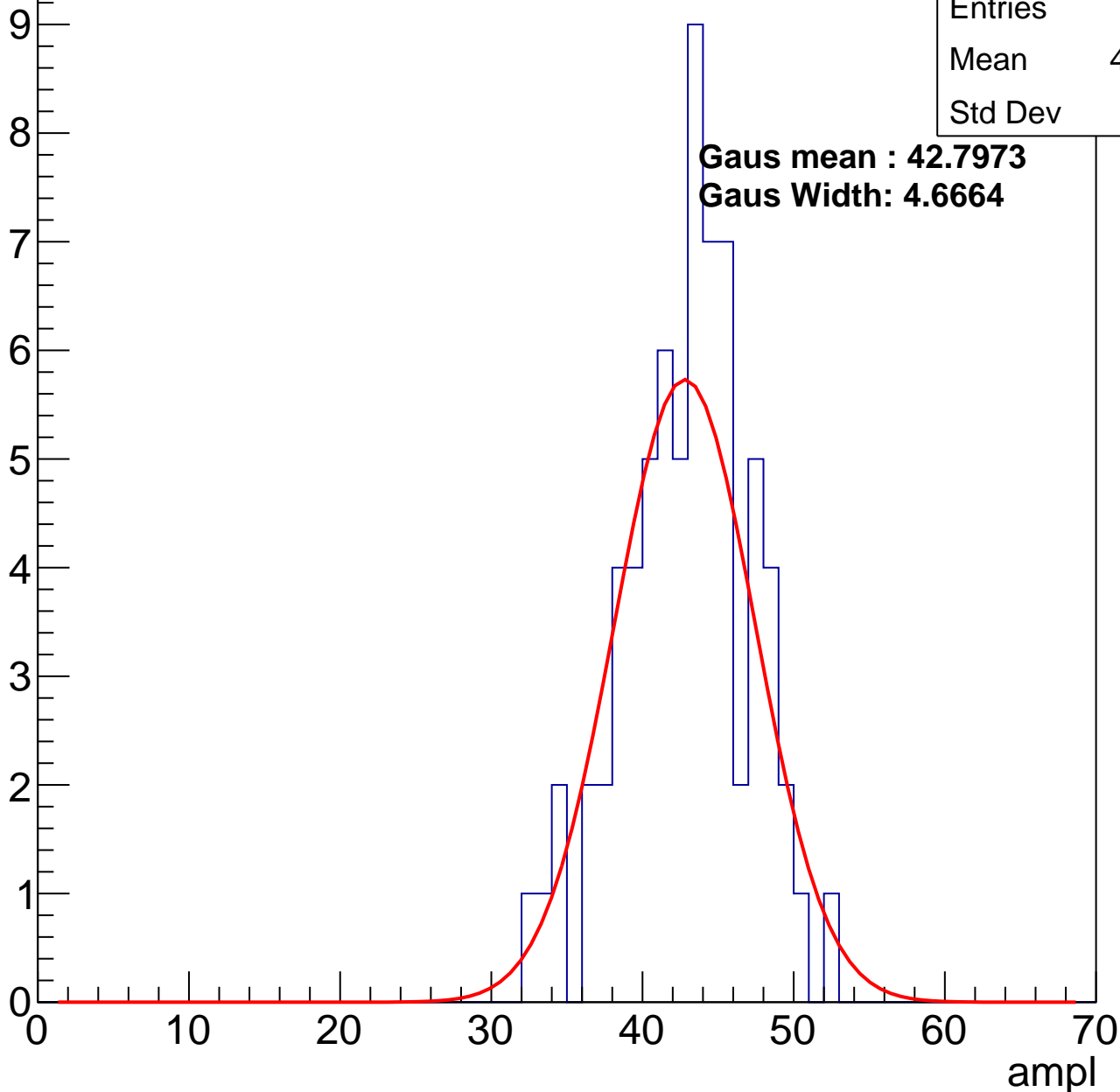
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.46
Std Dev	4.15

Gaus mean : 42.7973

Gaus Width: 4.6664

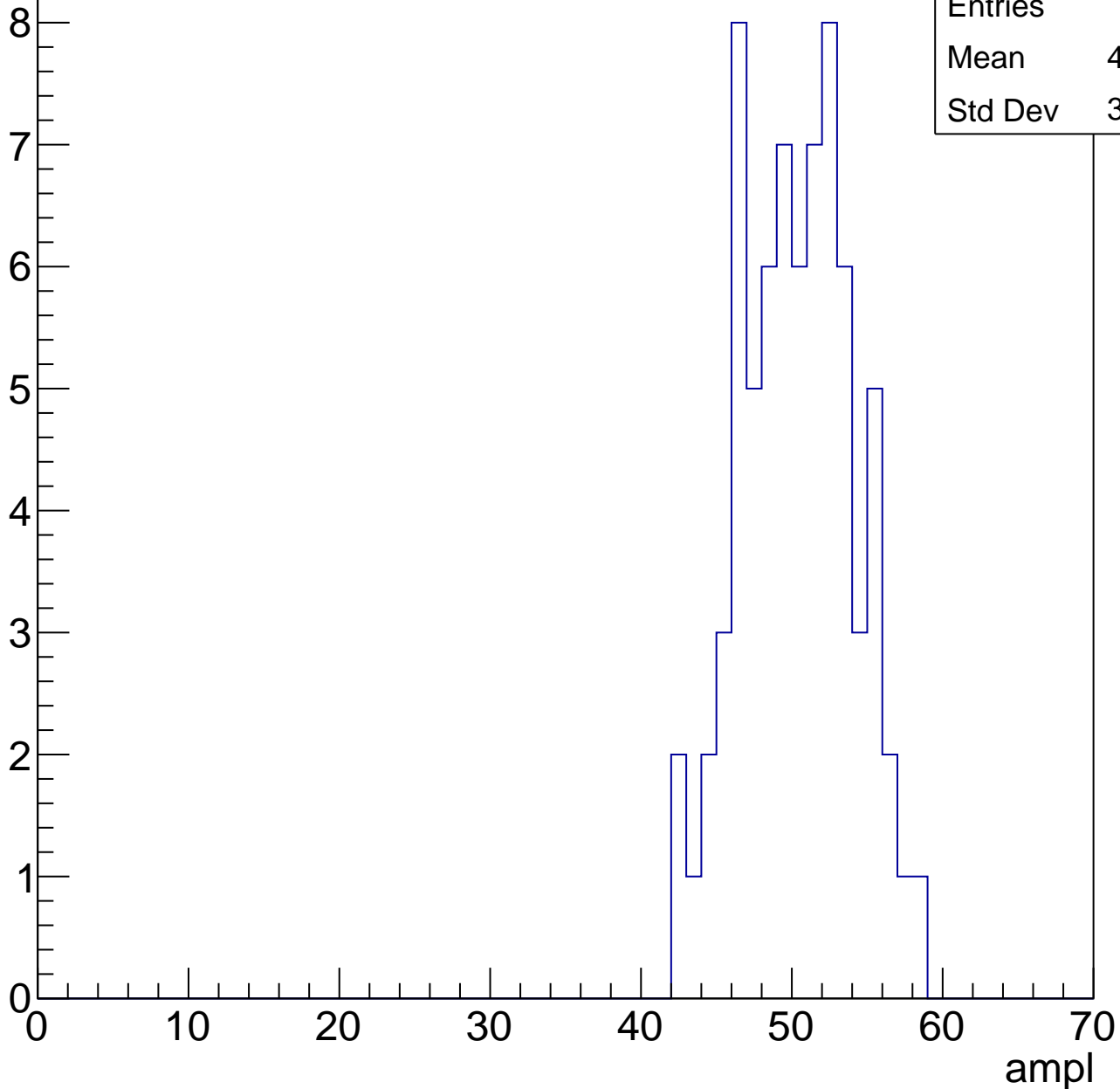


B1L100S, U11-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	49.85
Std Dev	3.659

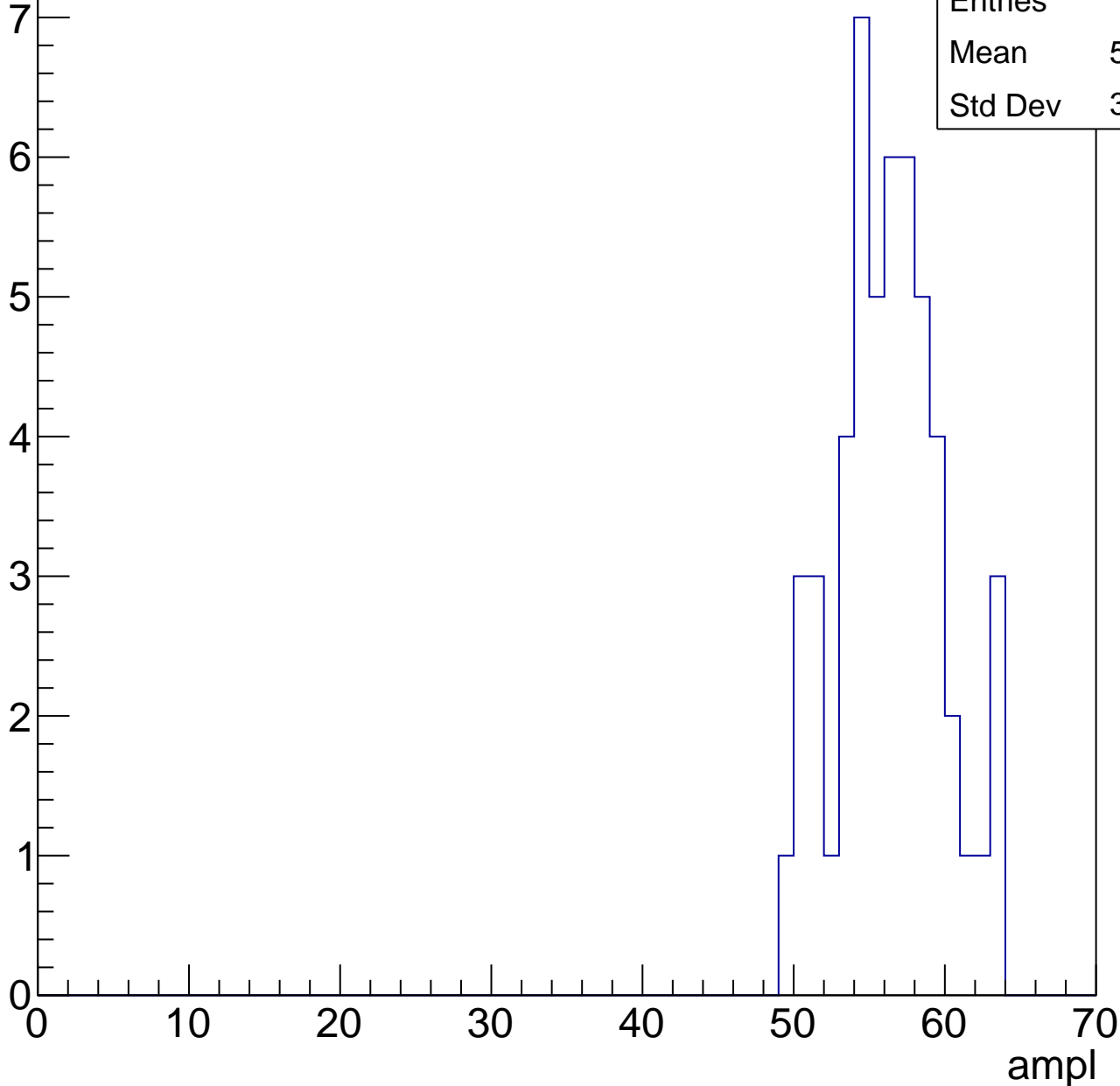


B1L100S, U11-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	55.87
Std Dev	3.448

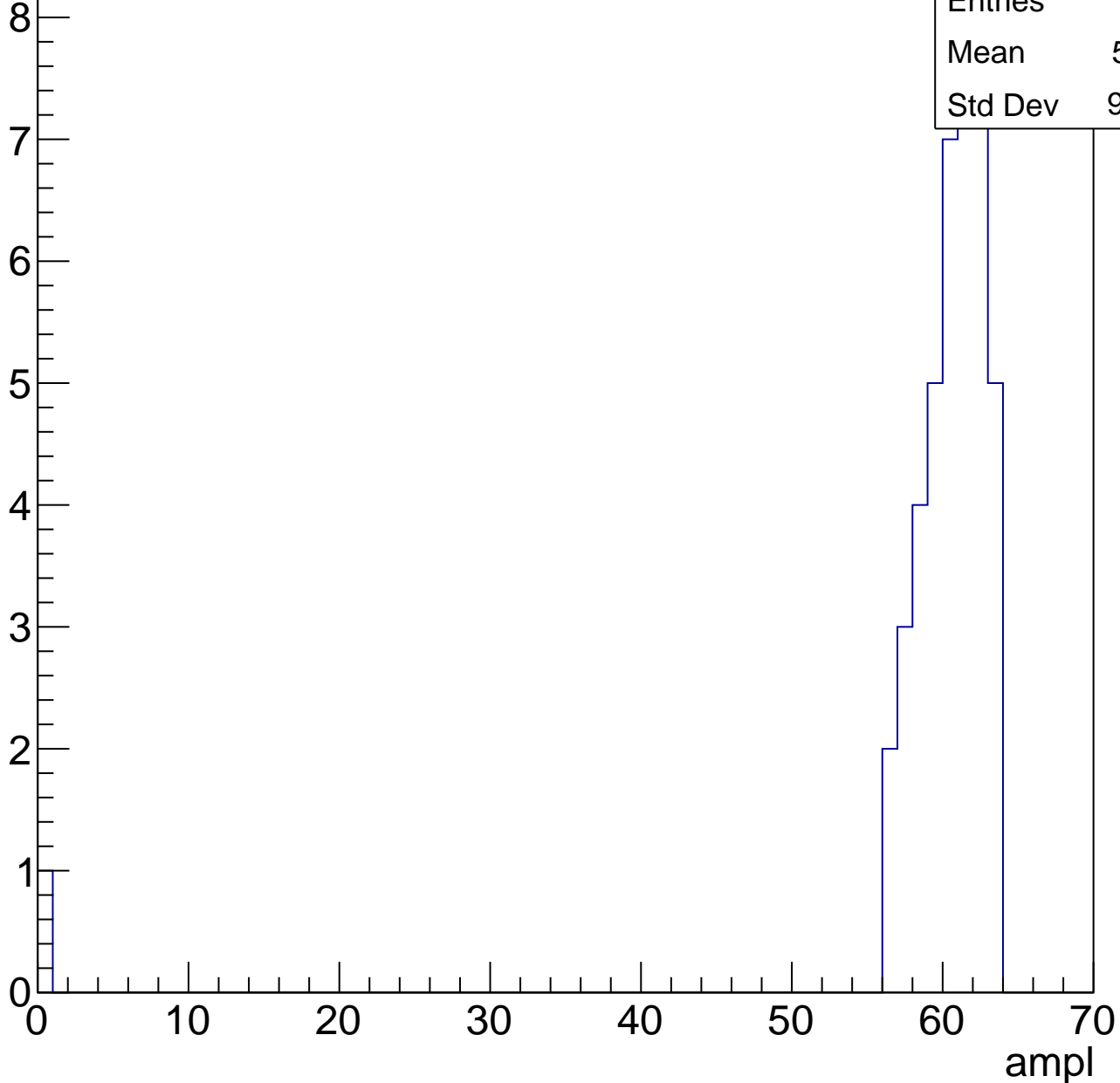


B1L100S, U11-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

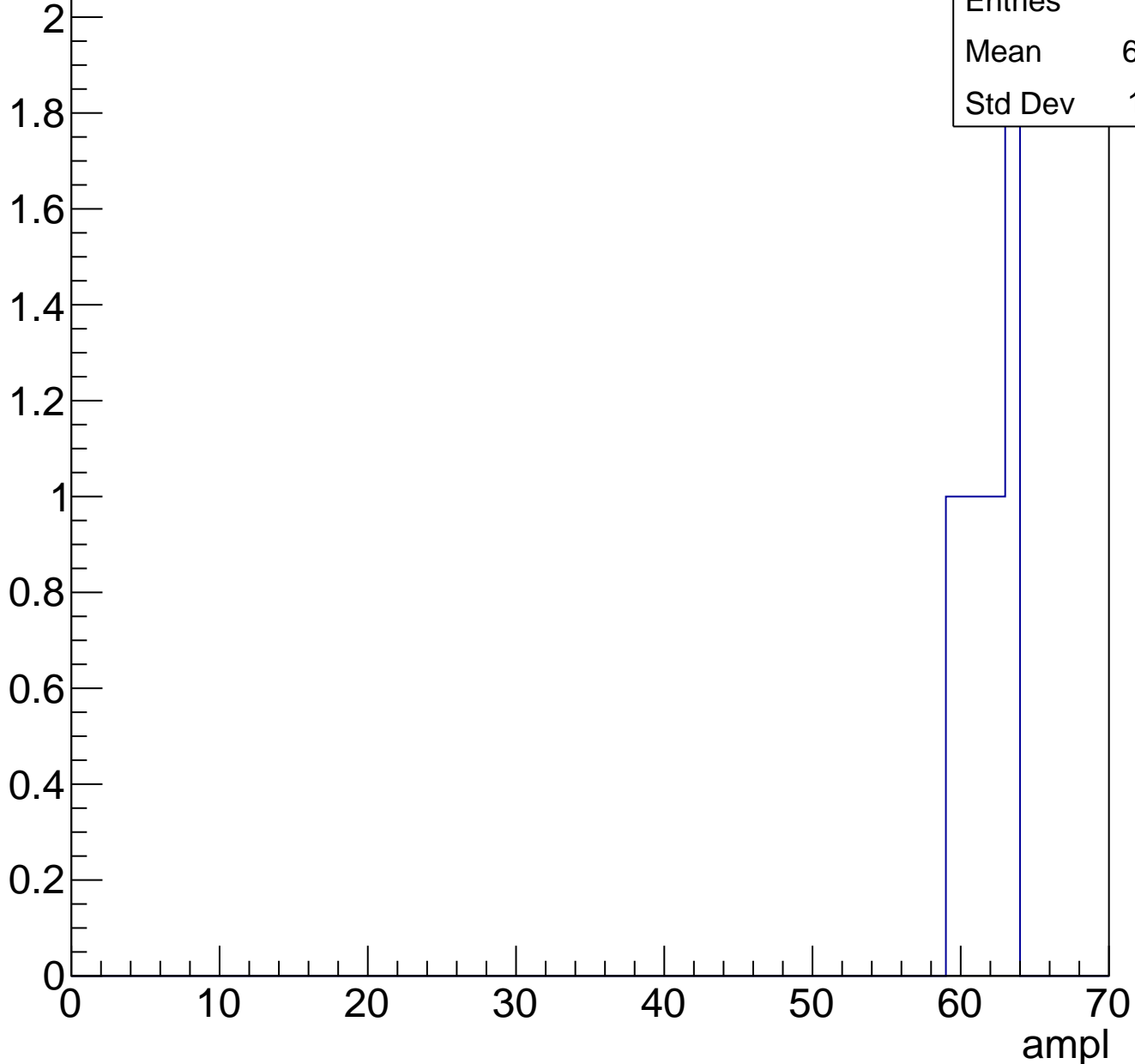
Entries	43
Mean	58.81
Std Dev	9.282



B1L100S, U11-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	6
Mean	61.33
Std Dev	1.491

B1L100S, U11-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch127, adc0

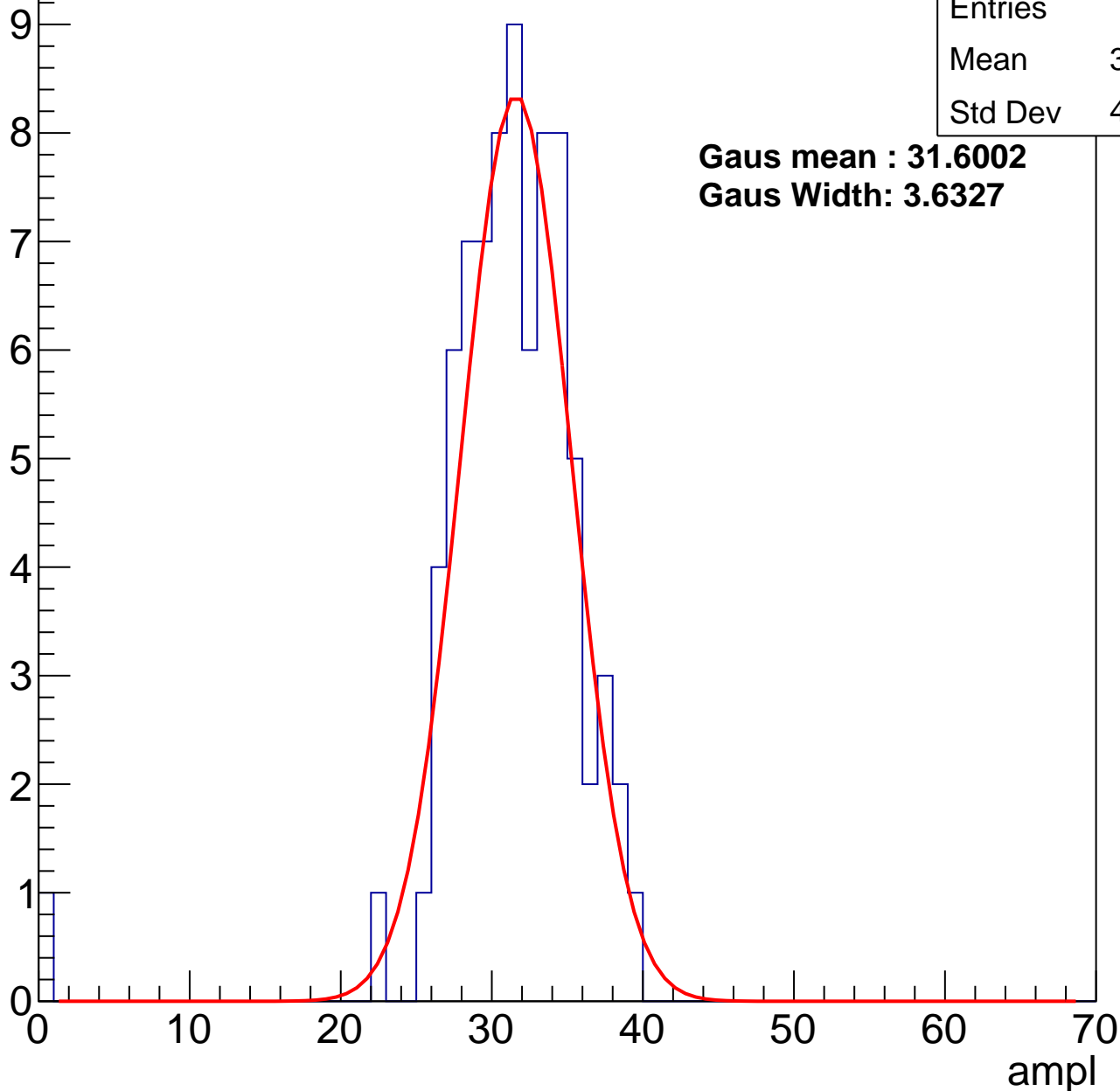
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	30.78
Std Dev	4.877

Gaus mean : 31.6002

Gaus Width: 3.6327



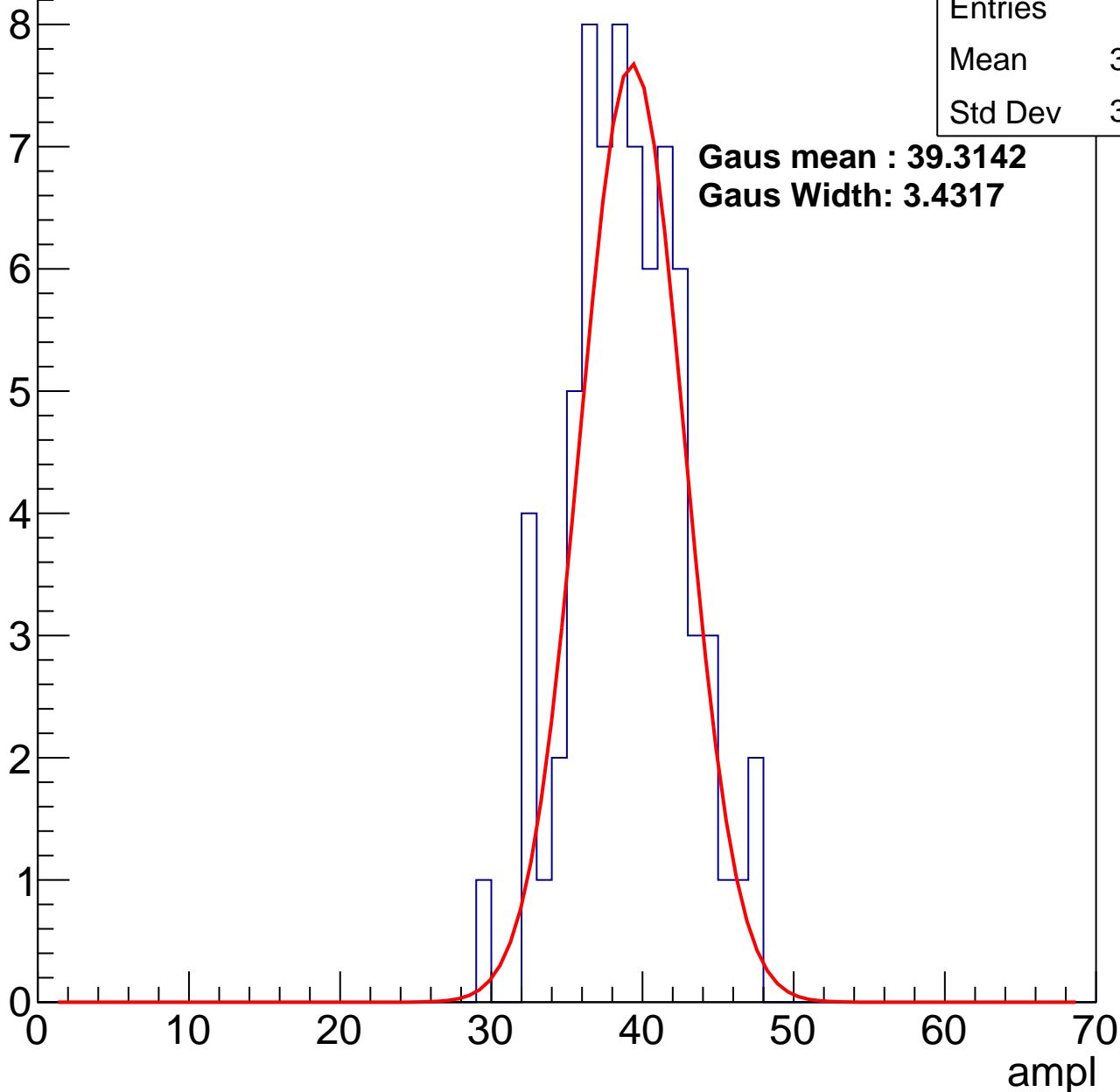
B1L100S, U11-ch127, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	38.64
Std Dev	3.698

Gaus mean : 39.3142
Gaus Width: 3.4317



B1L100S, U11-ch127, adc2

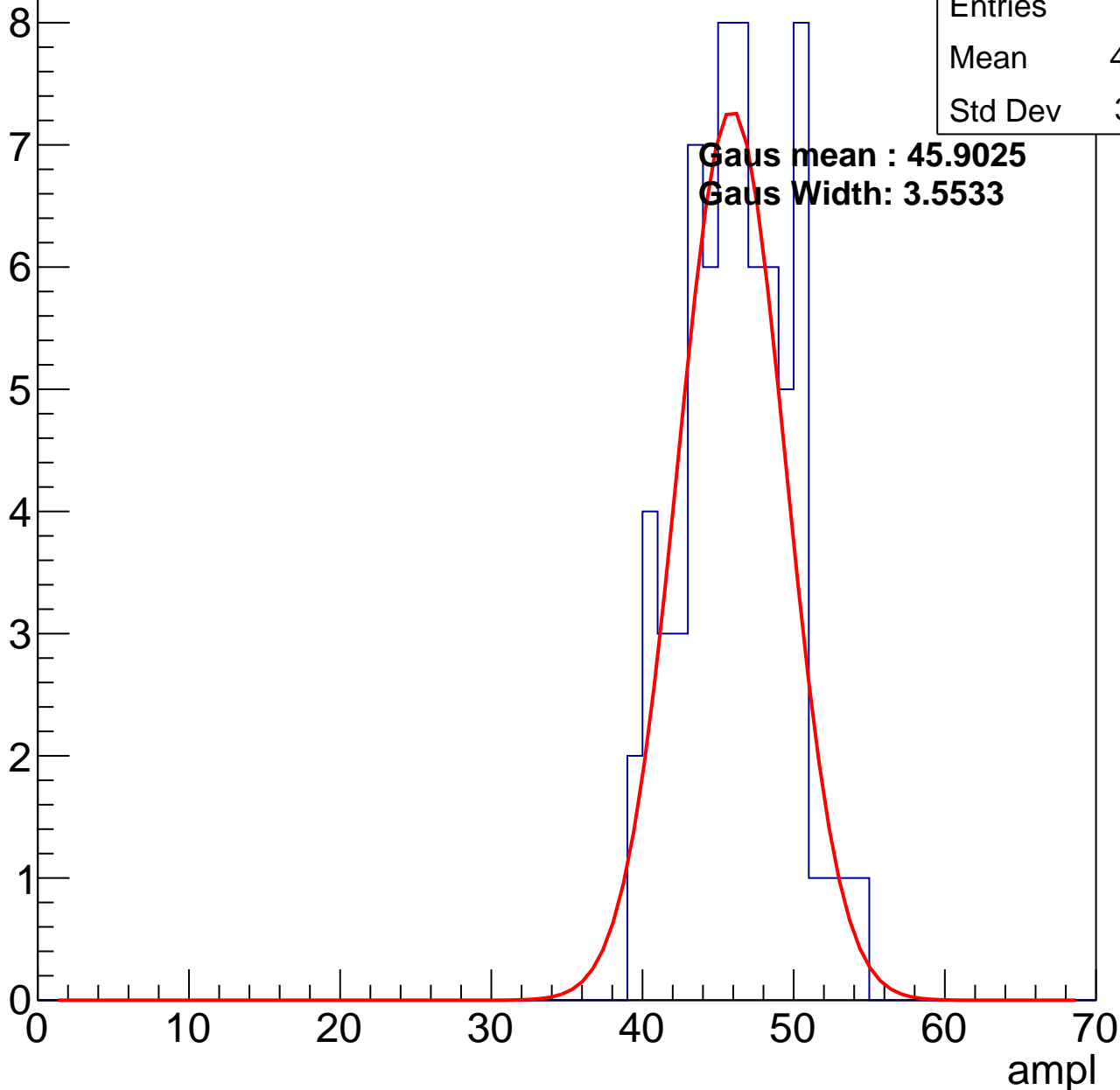
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	45.79
Std Dev	3.451

Gaus mean : 45.9025

Gaus Width: 3.5533

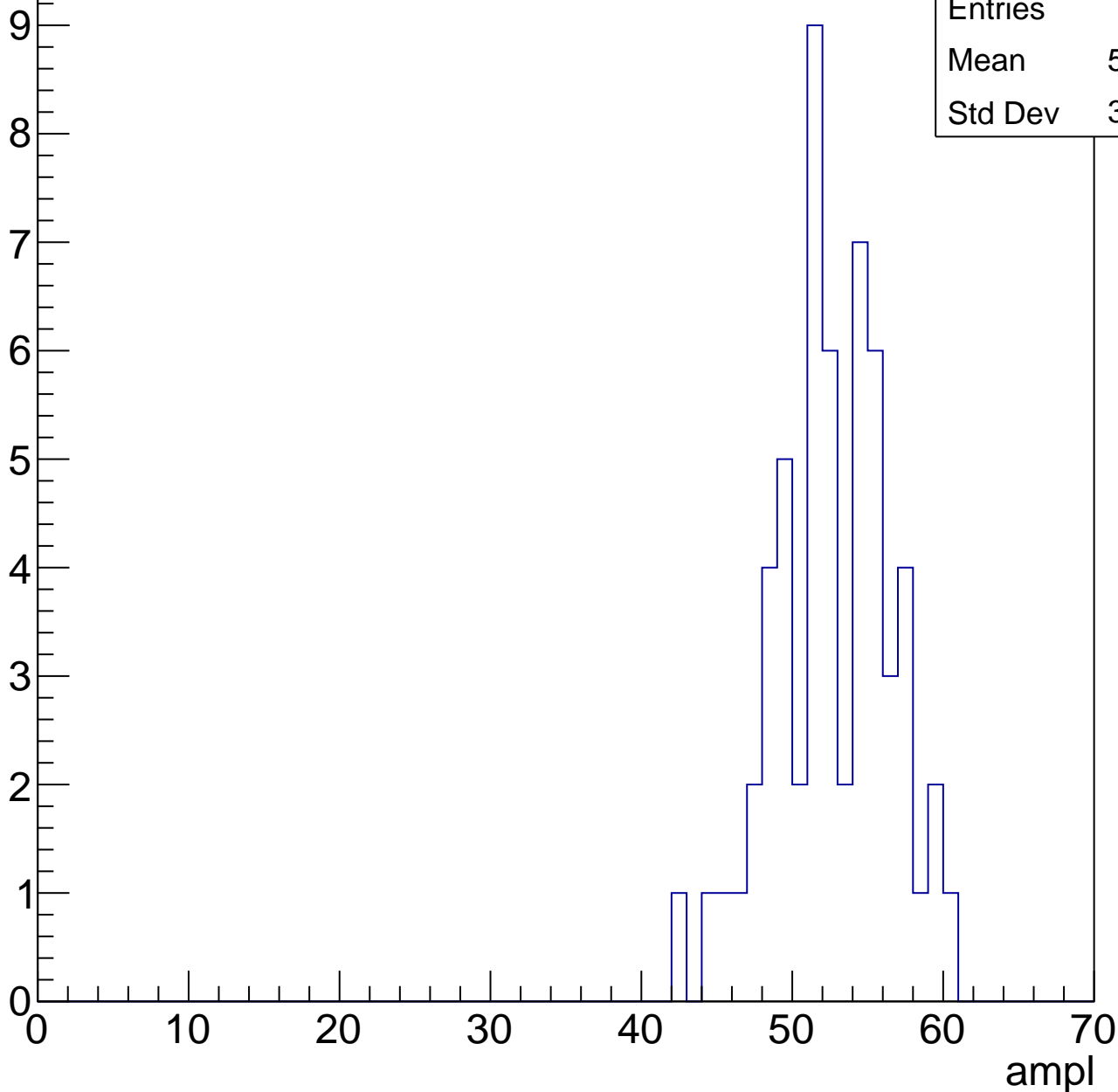


B1L100S, U11-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

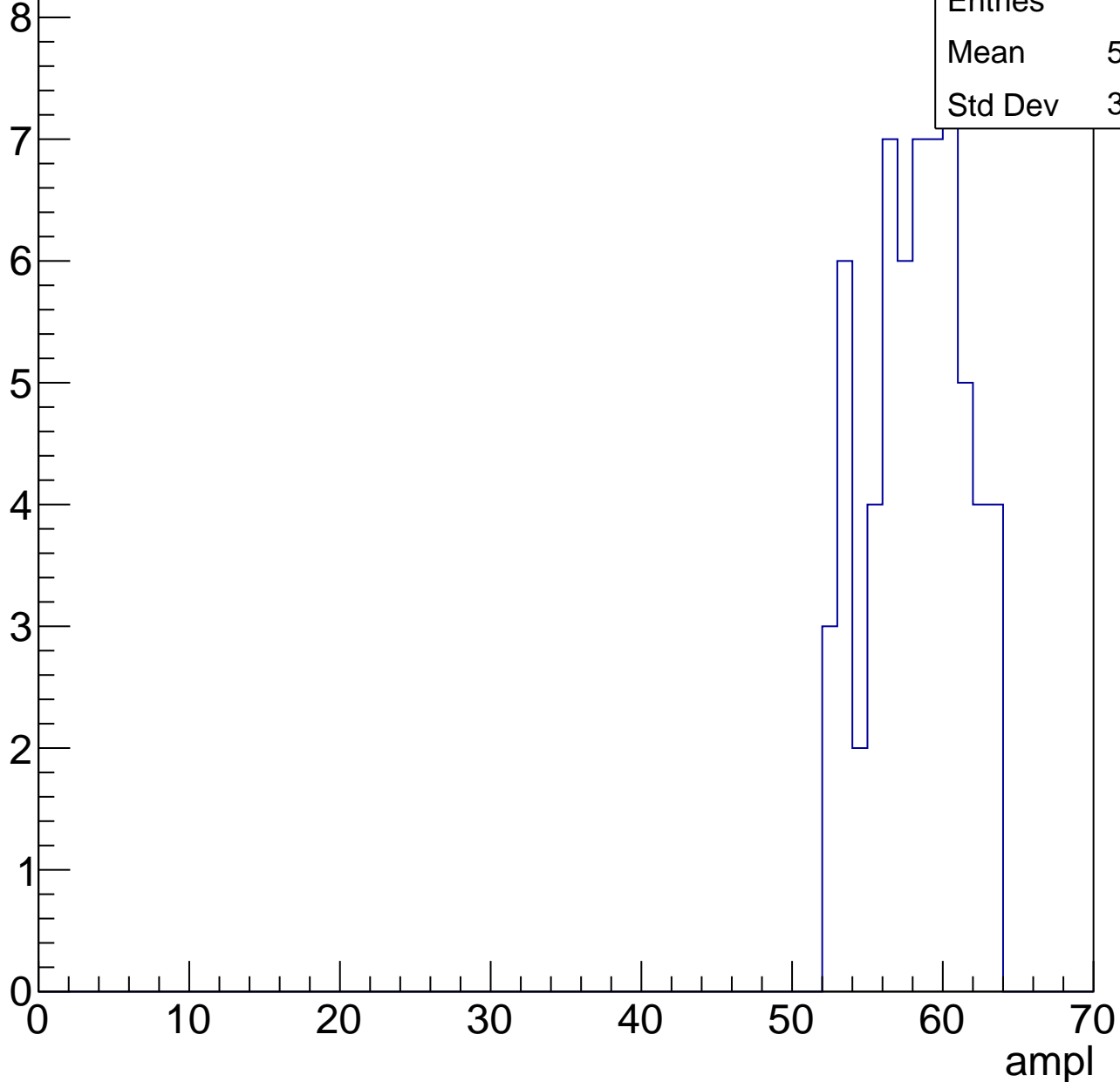
Entries	58
Mean	52.16
Std Dev	3.836



B1L100S, U11-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



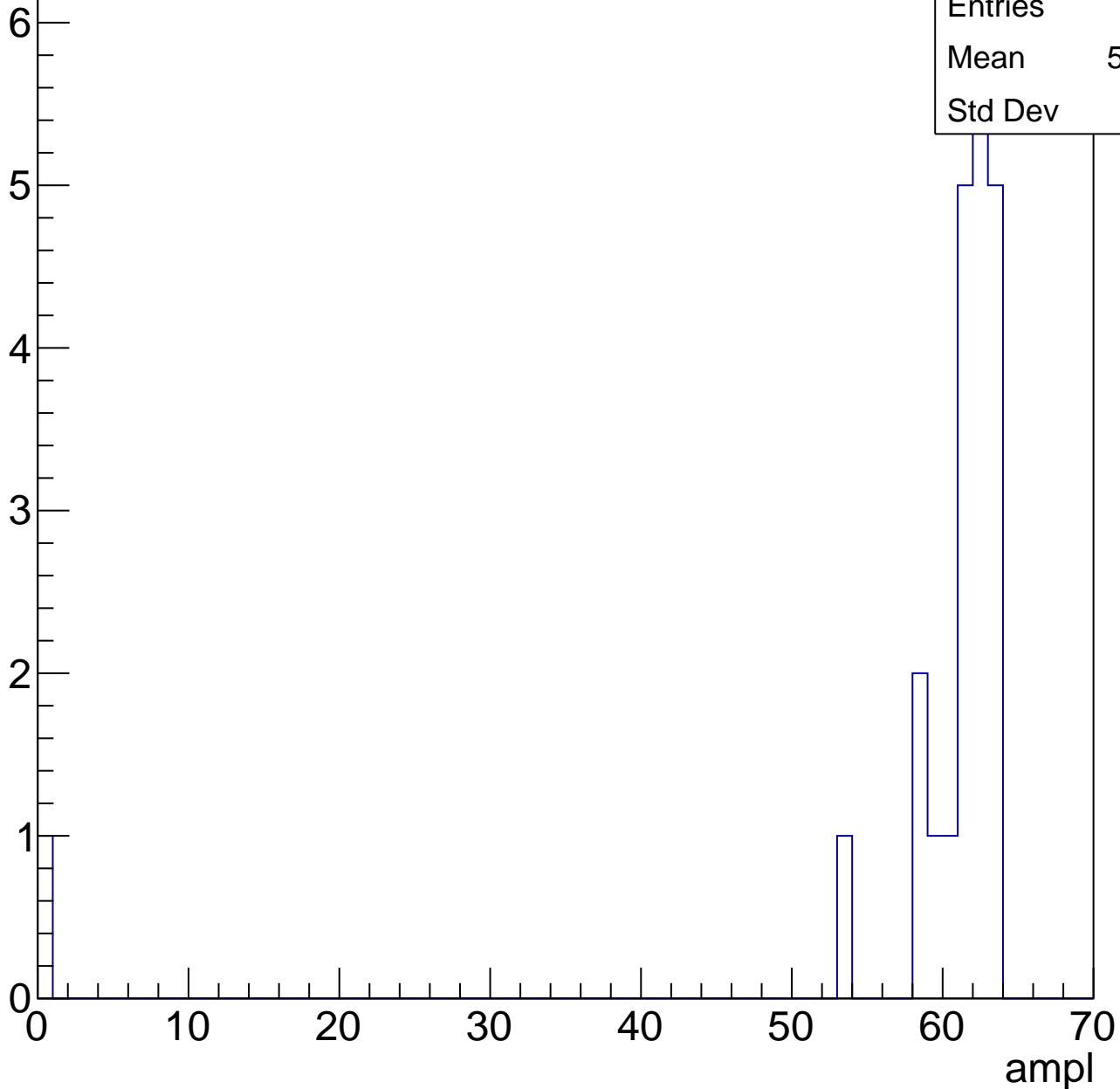
Entries	63
Mean	57.78
Std Dev	3.099

B1L100S, U11-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.18
Std Dev	12.9



B1L100S, U11-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U11-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U11-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0