

B0L002S, U9-ch0, adc0

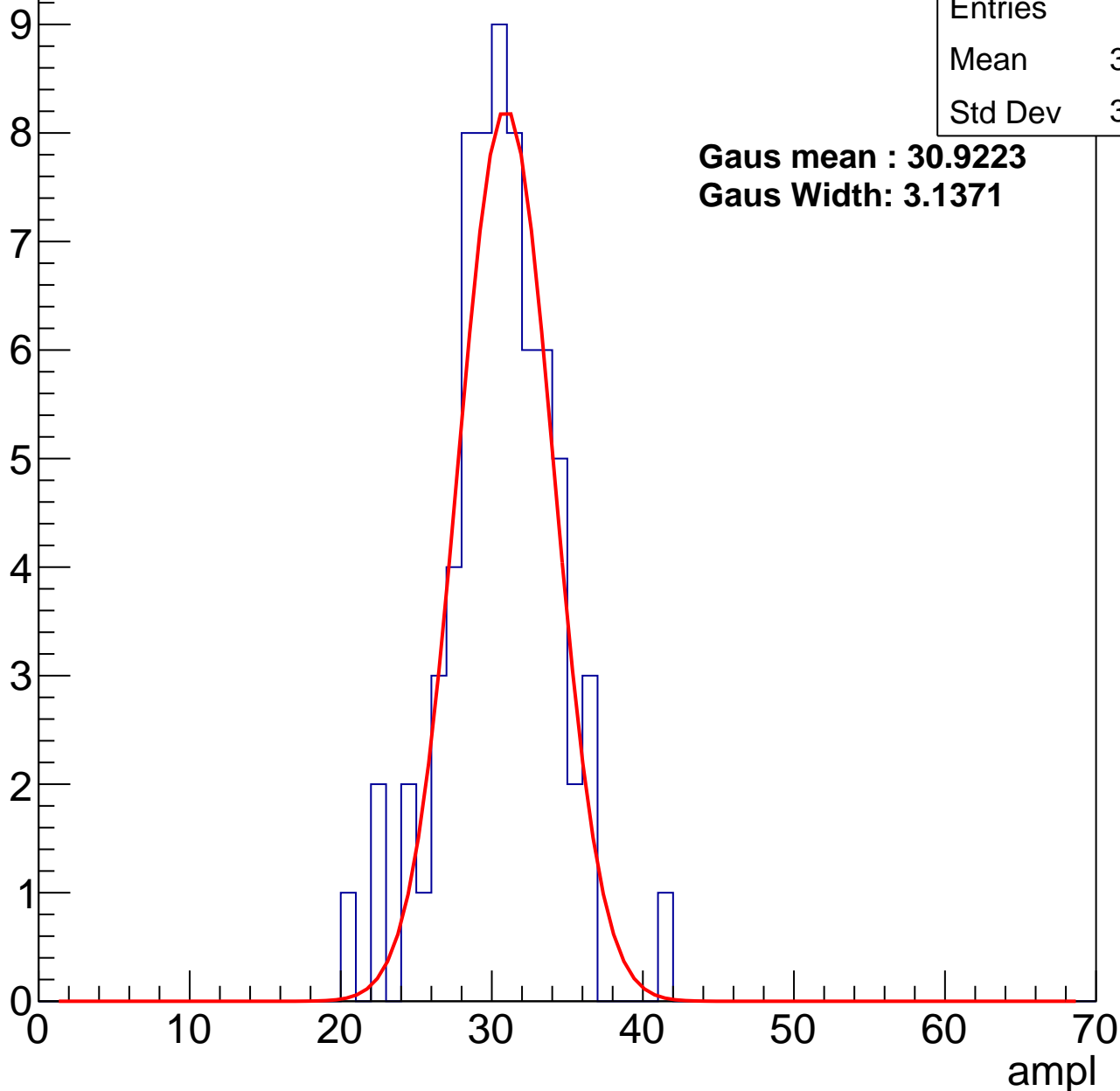
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.09
Std Dev	3.598

Gaus mean : 30.9223

Gaus Width: 3.1371



B0L002S, U9-ch0, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	37.53
Std Dev	3.432

Gaus mean : 37.3196

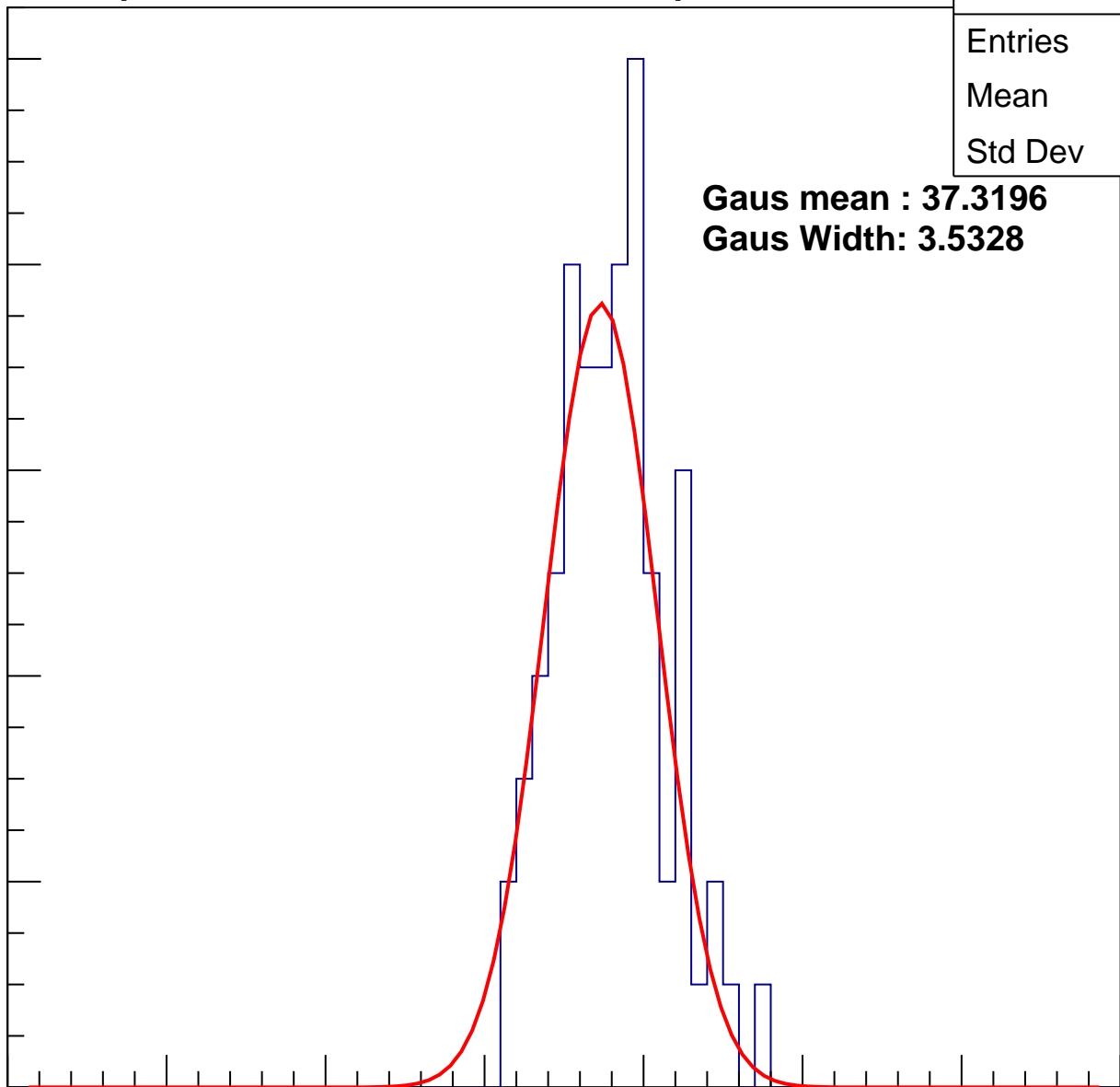
Gaus Width: 3.5328

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch0, adc2

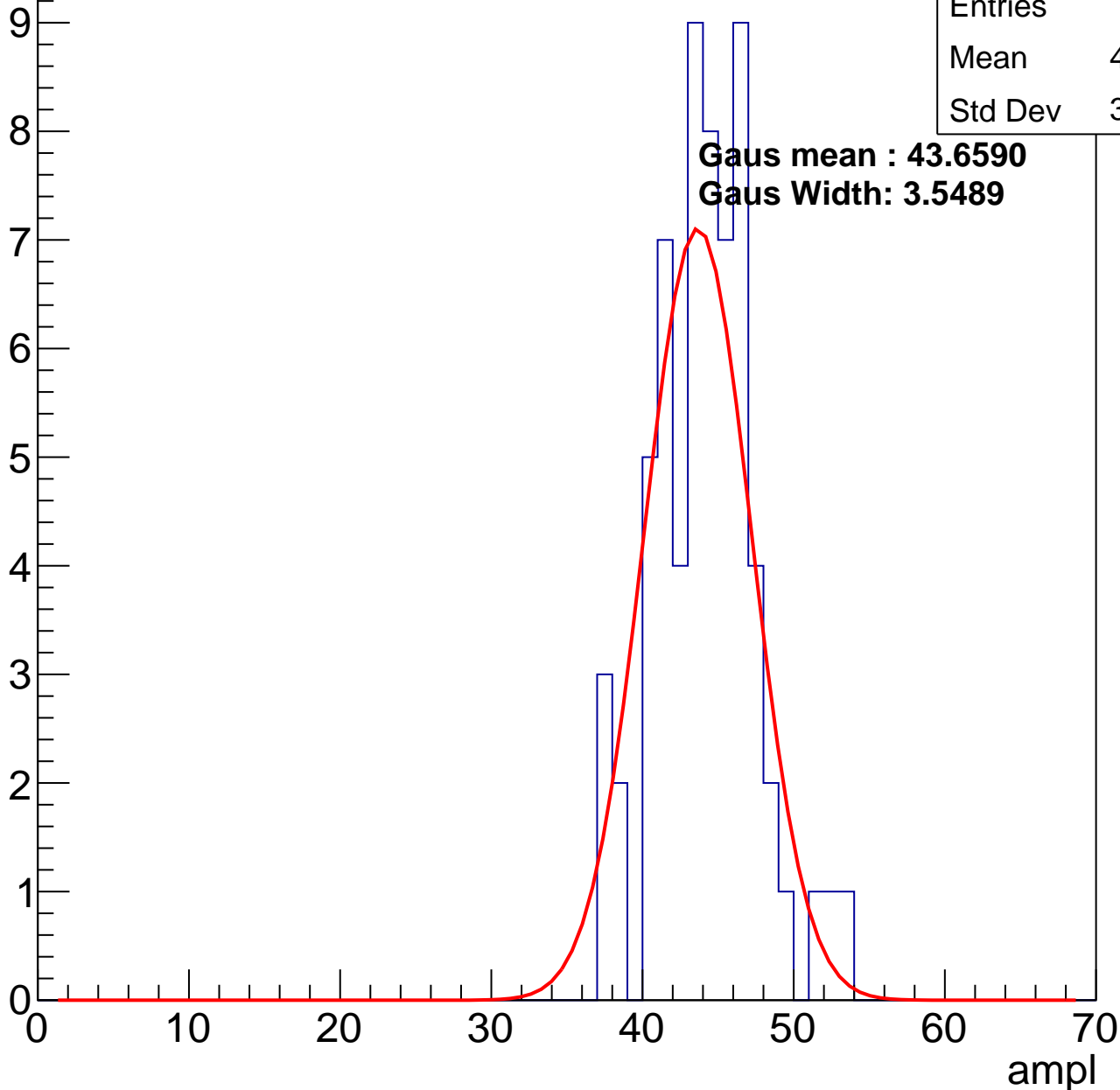
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.73
Std Dev	3.337

Gaus mean : 43.6590

Gaus Width: 3.5489

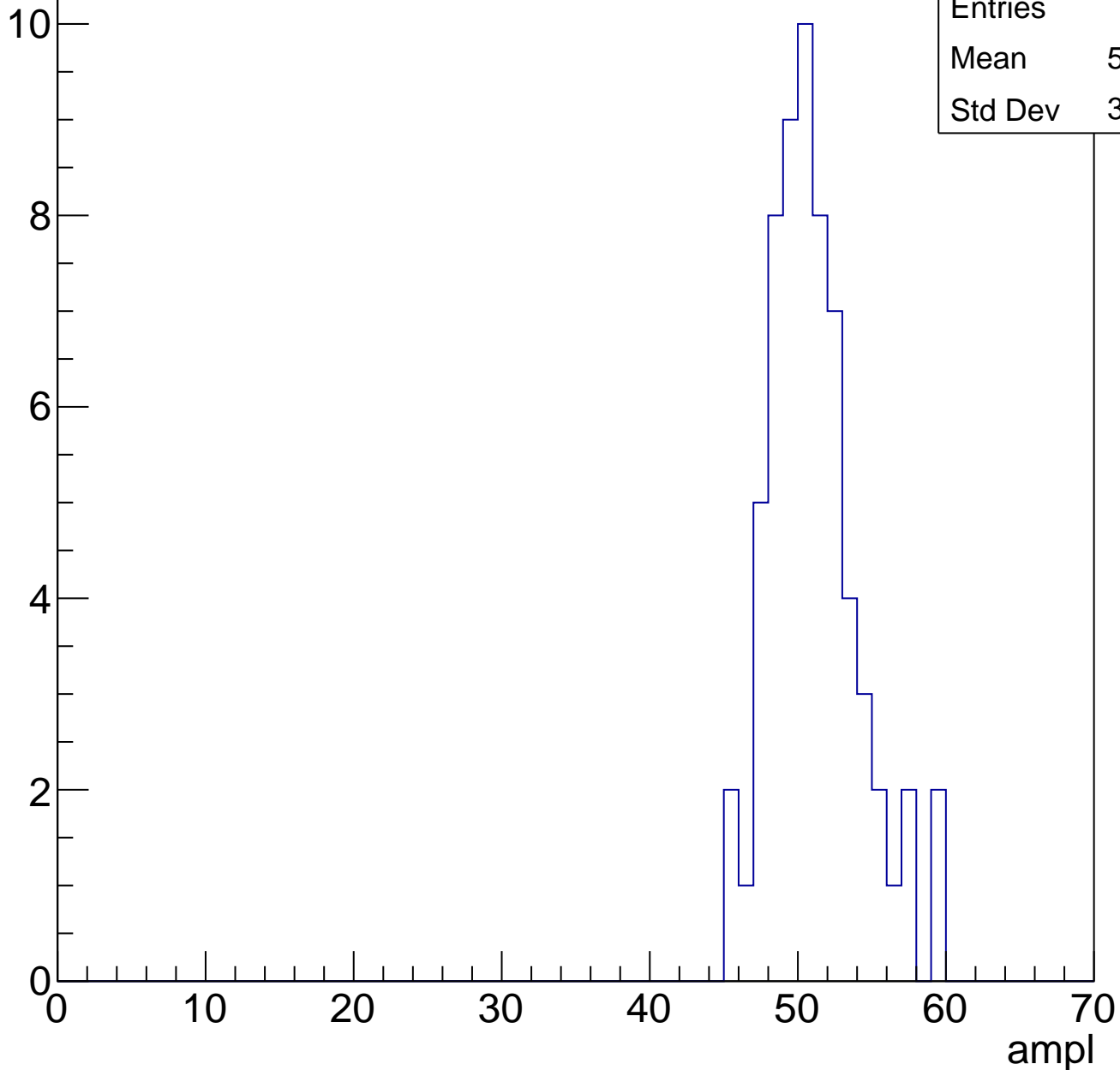


B0L002S, U9-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	50.62
Std Dev	3.044

Entry



B0L002S, U9-ch0, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	56.93
Std Dev	2.778

Entry

10

8

6

4

2

0

0

10

20

30

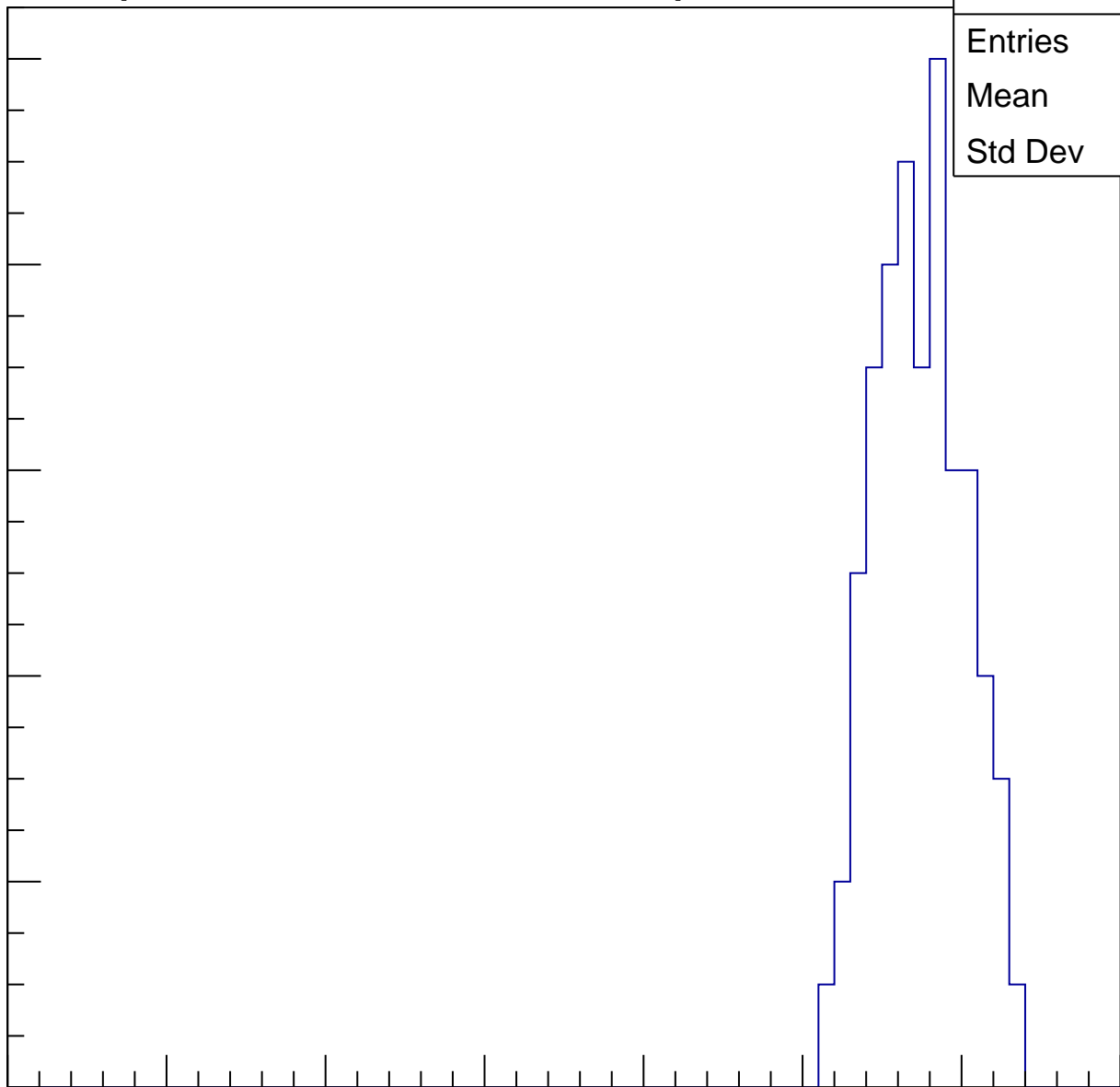
40

50

60

70

ampl

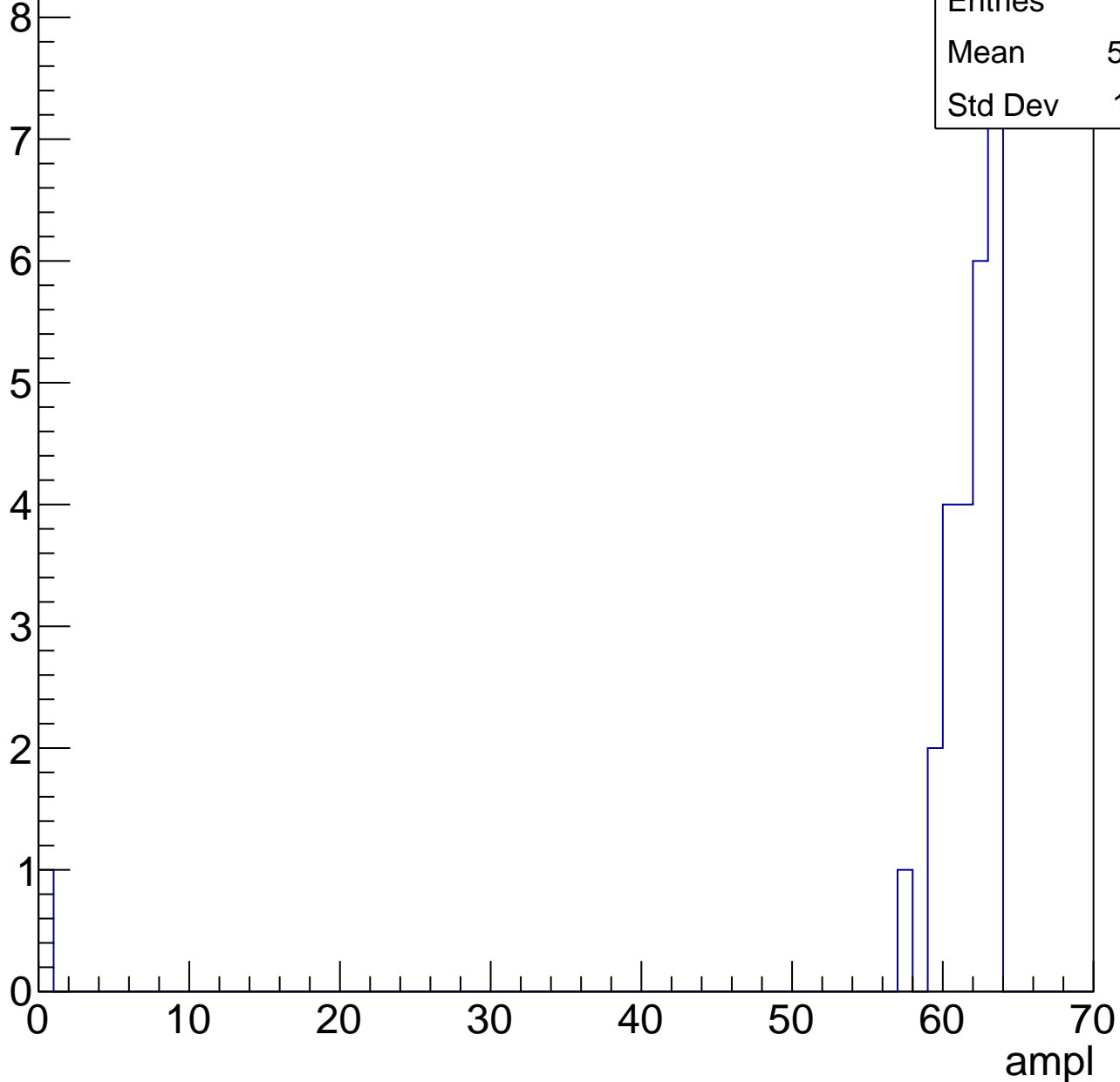


B0L002S, U9-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	59.04
Std Dev	11.91



B0L002S, U9-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

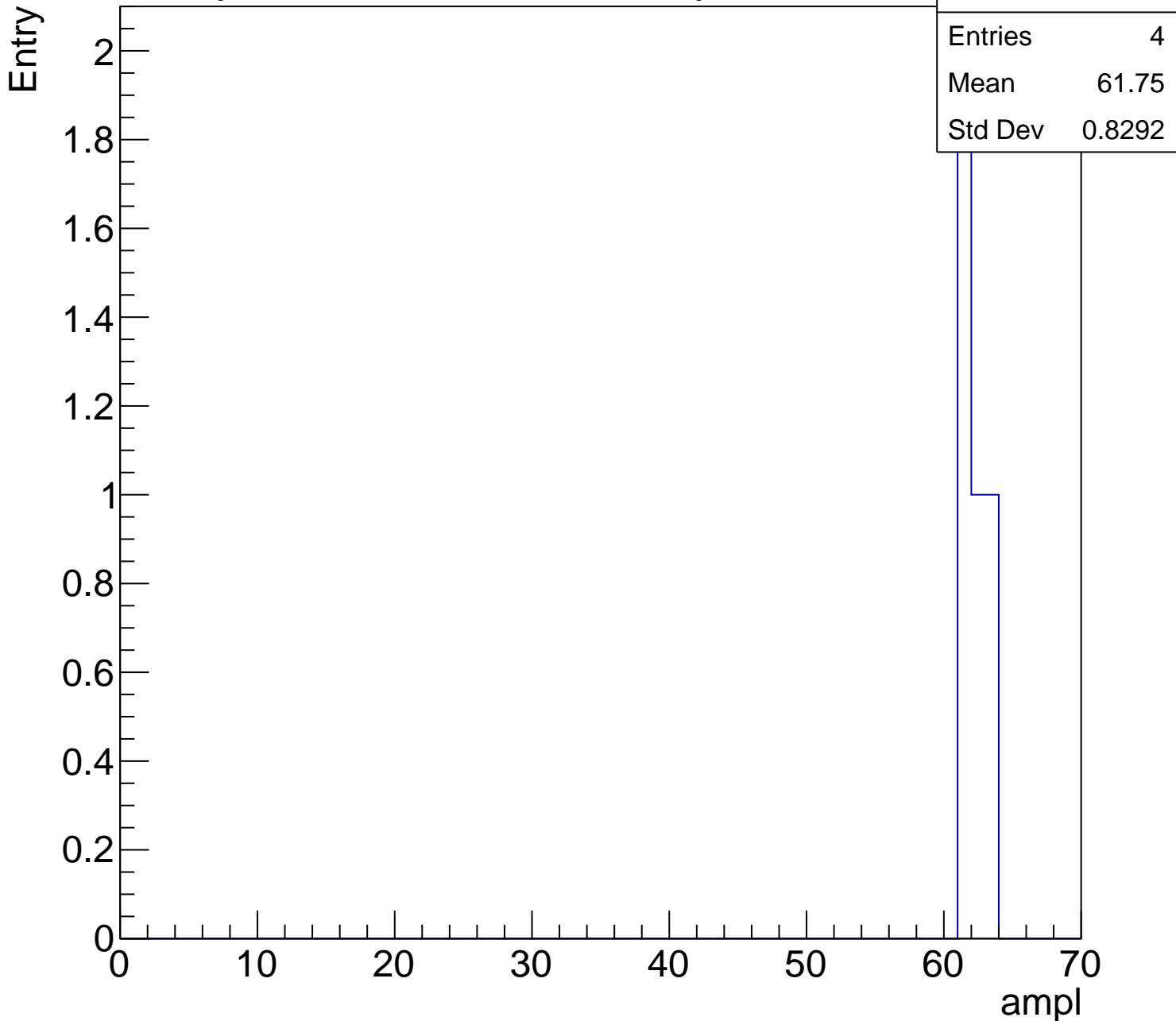
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch1, adc0

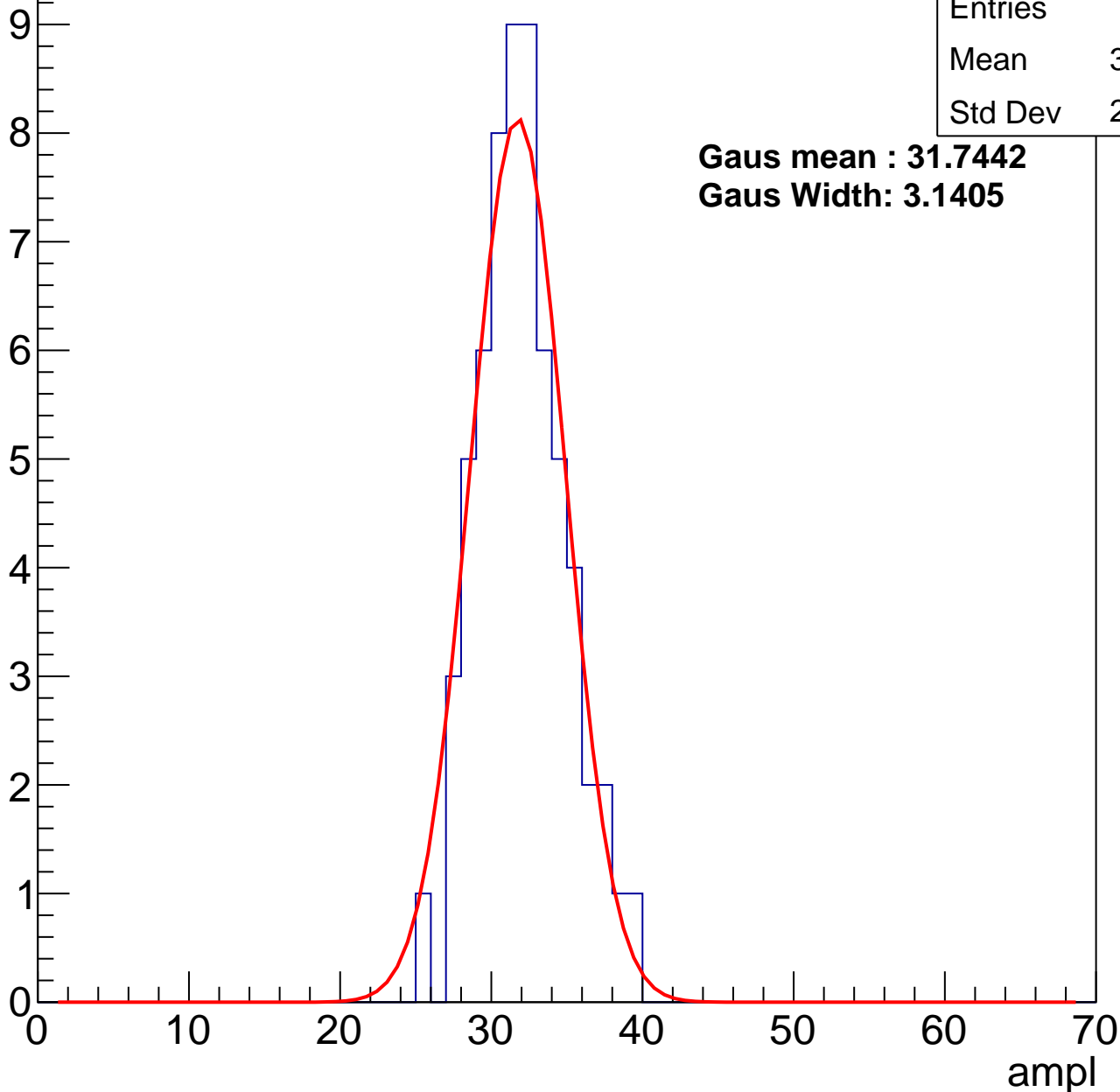
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.58
Std Dev	2.888

Gaus mean : 31.7442

Gaus Width: 3.1405



B0L002S, U9-ch1, adc1

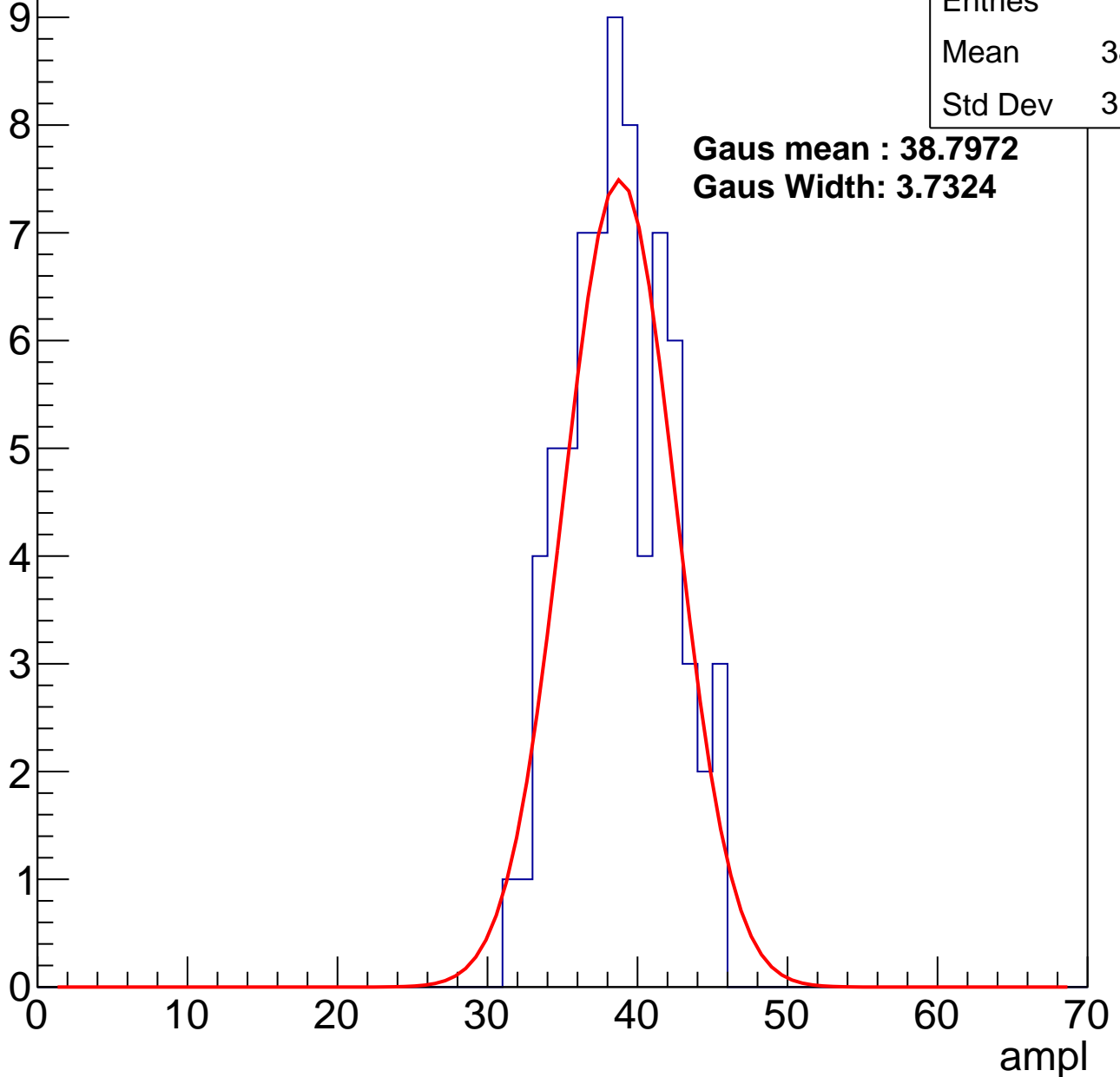
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	38.28
Std Dev	3.367

Gaus mean : 38.7972

Gaus Width: 3.7324



B0L002S, U9-ch1, adc2

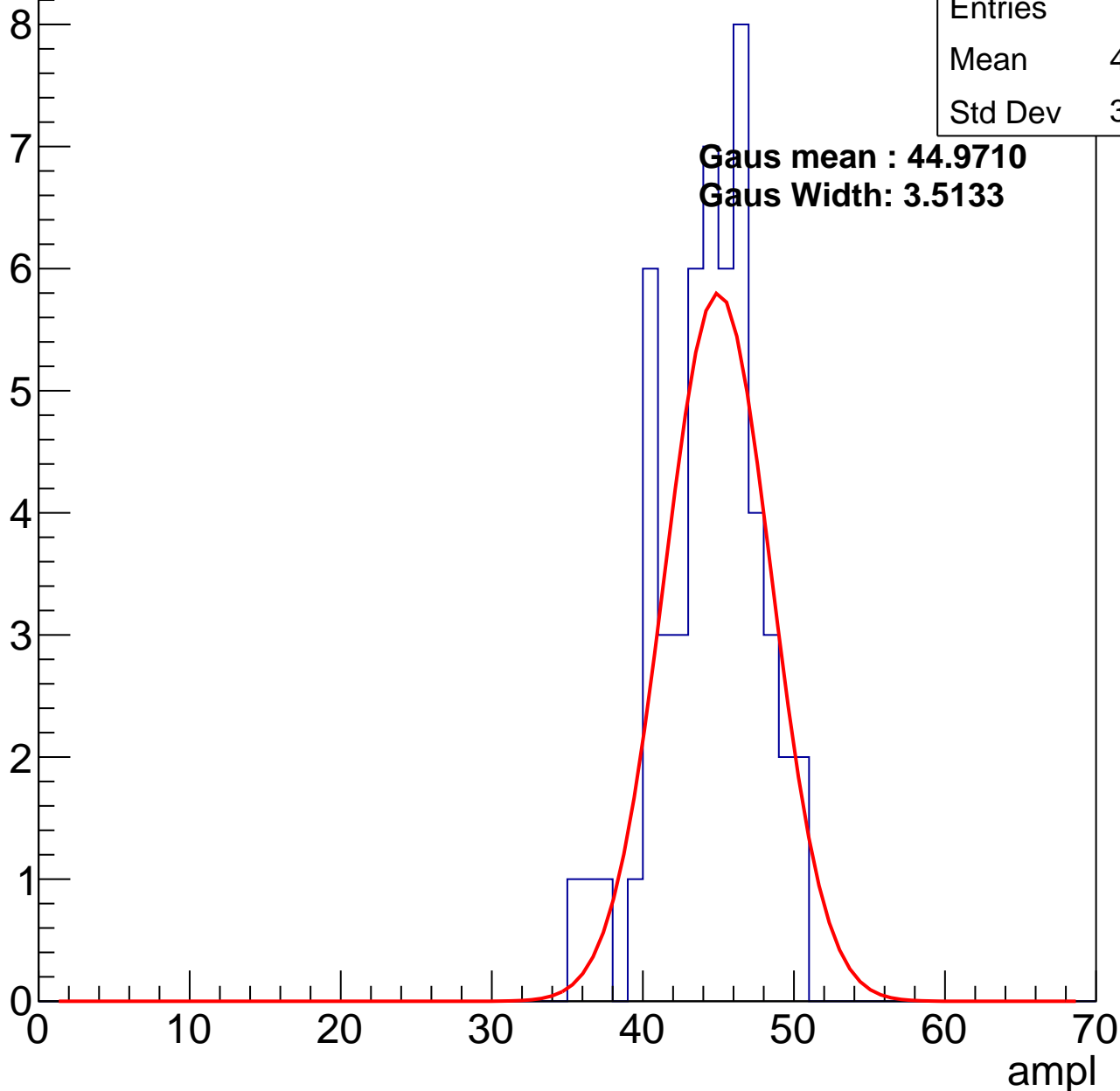
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.89
Std Dev	3.343

Gaus mean : 44.9710

Gaus Width: 3.5133

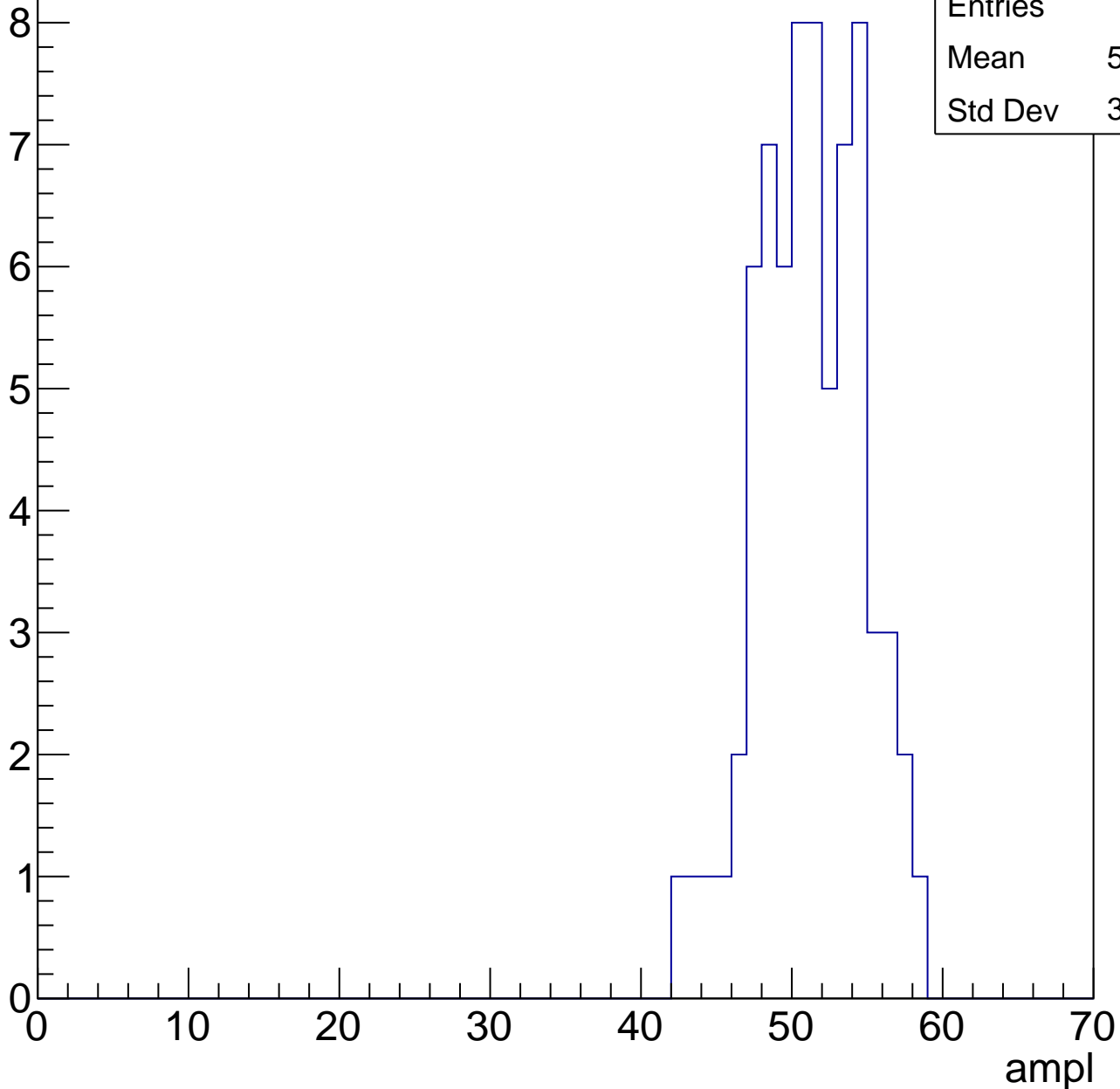


B0L002S, U9-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	50.77
Std Dev	3.415



B0L002S, U9-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	56.38
Std Dev	7.42

Entry

10

8

6

4

2

0

0

10

20

30

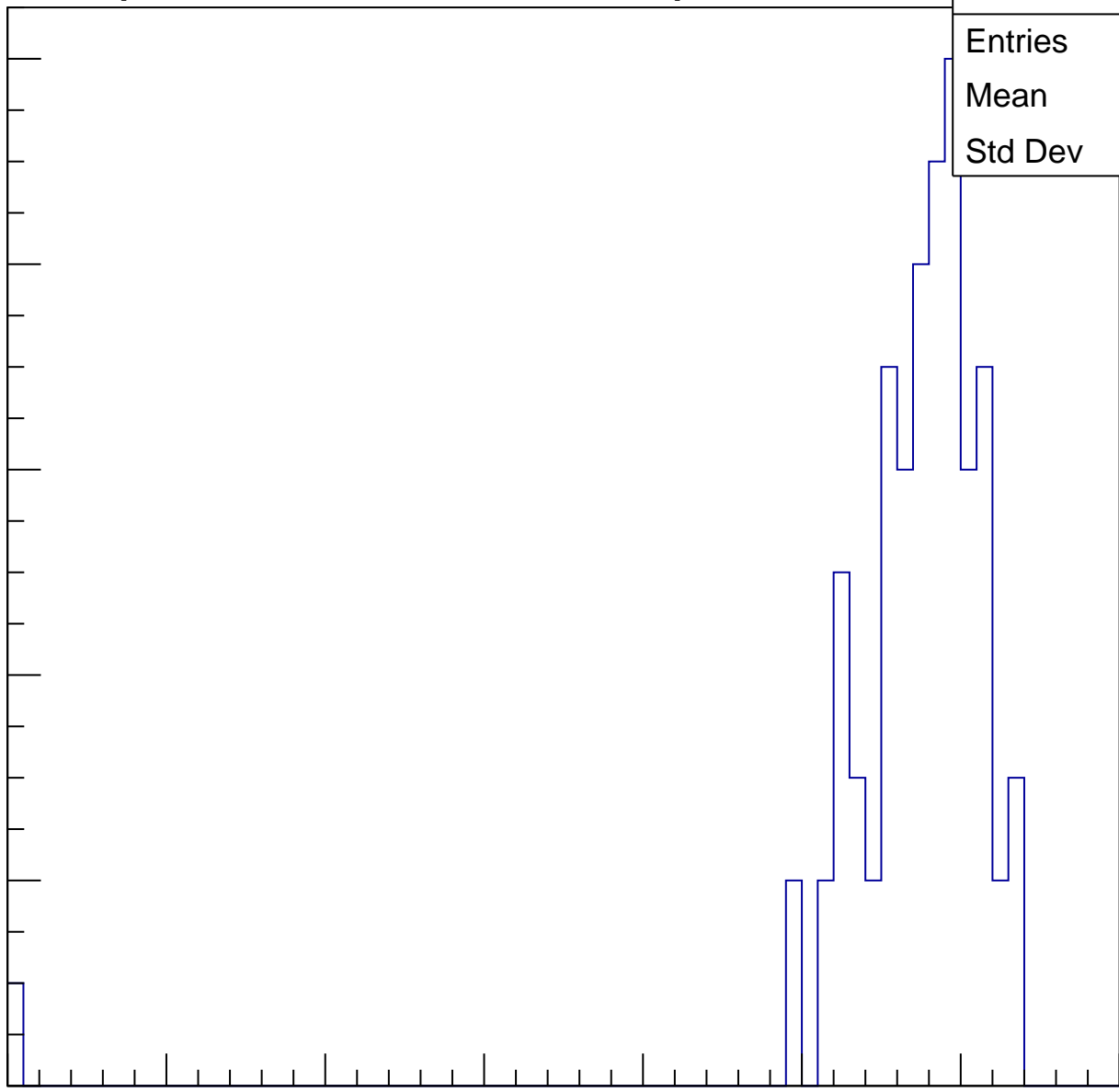
40

50

60

70

ampl

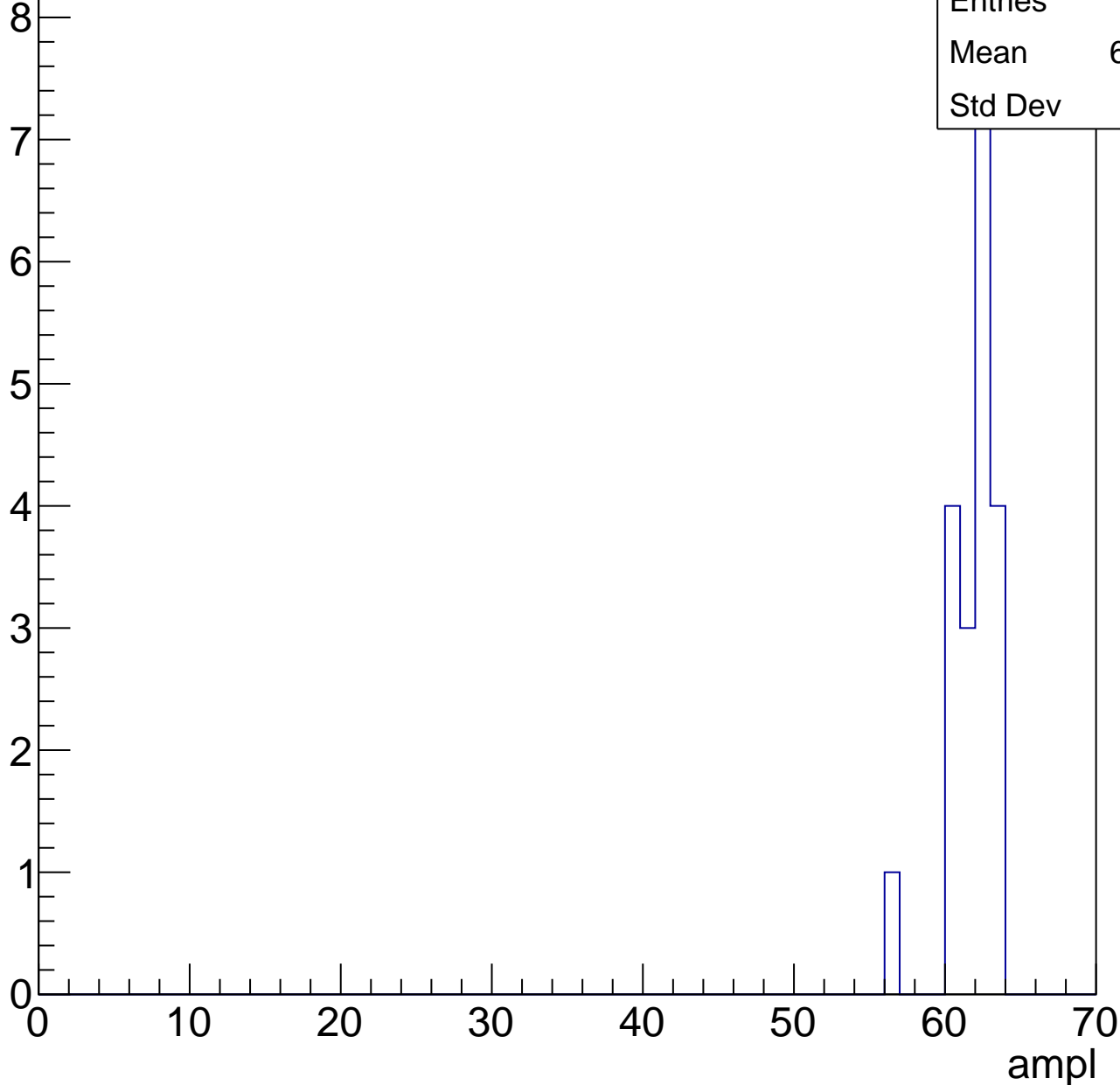


B0L002S, U9-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	61.35
Std Dev	1.59



B0L002S, U9-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch2, adc0

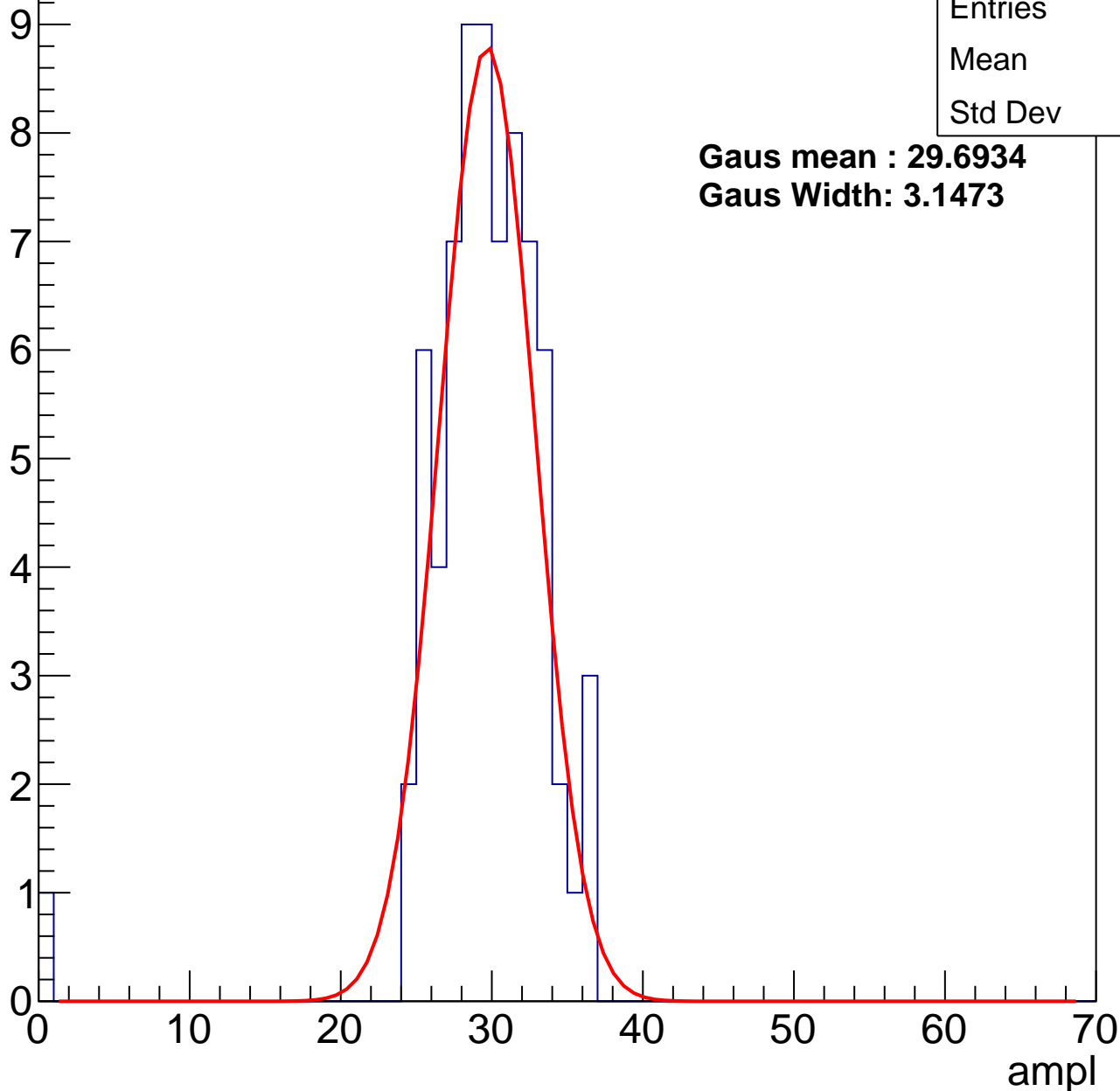
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	29.1
Std Dev	4.55

Gaus mean : 29.6934

Gaus Width: 3.1473

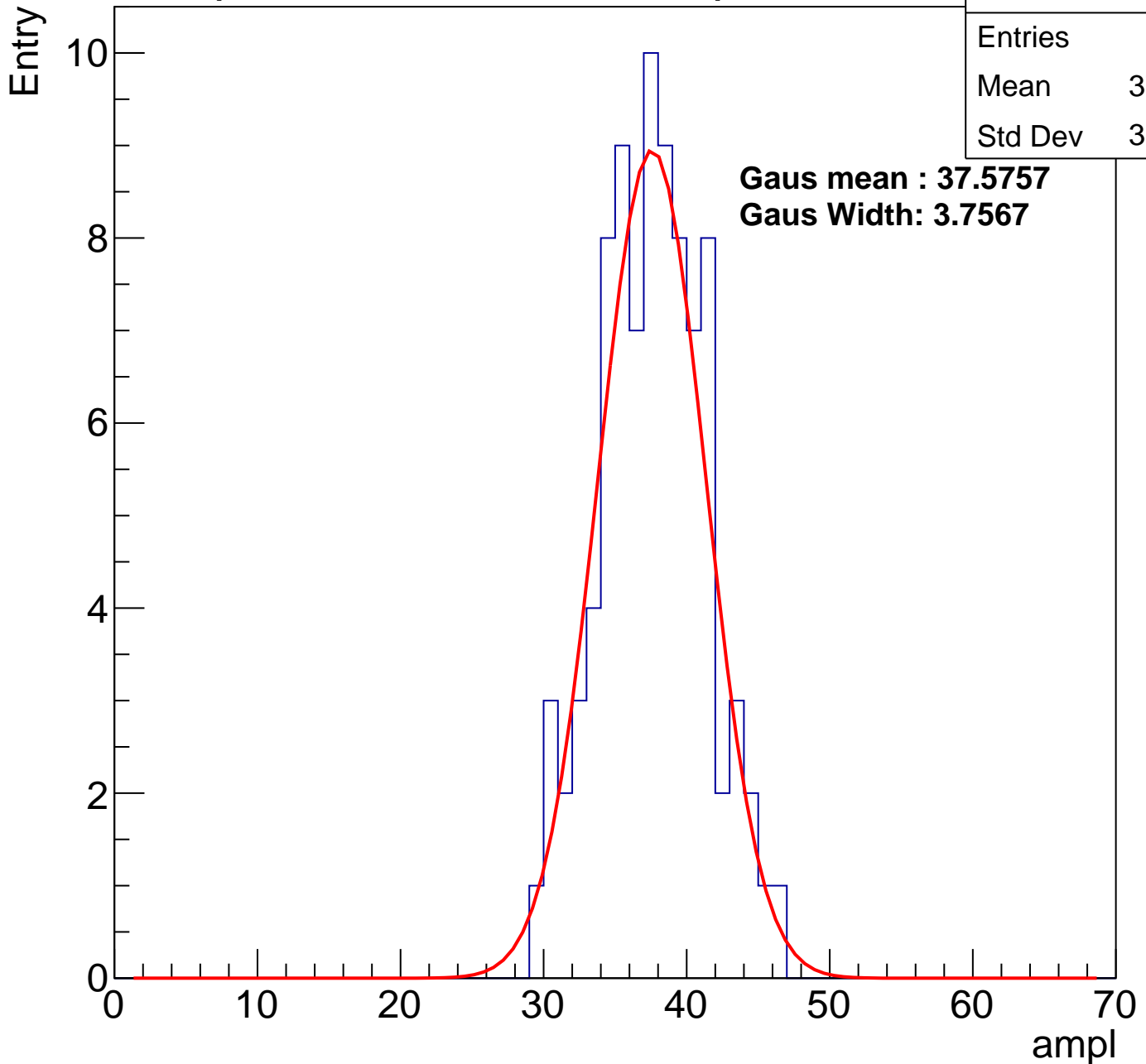


B0L002S, U9-ch2, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	88
Mean	37.18
Std Dev	3.642

Gaus mean : 37.5757
Gaus Width: 3.7567



B0L002S, U9-ch2, adc2

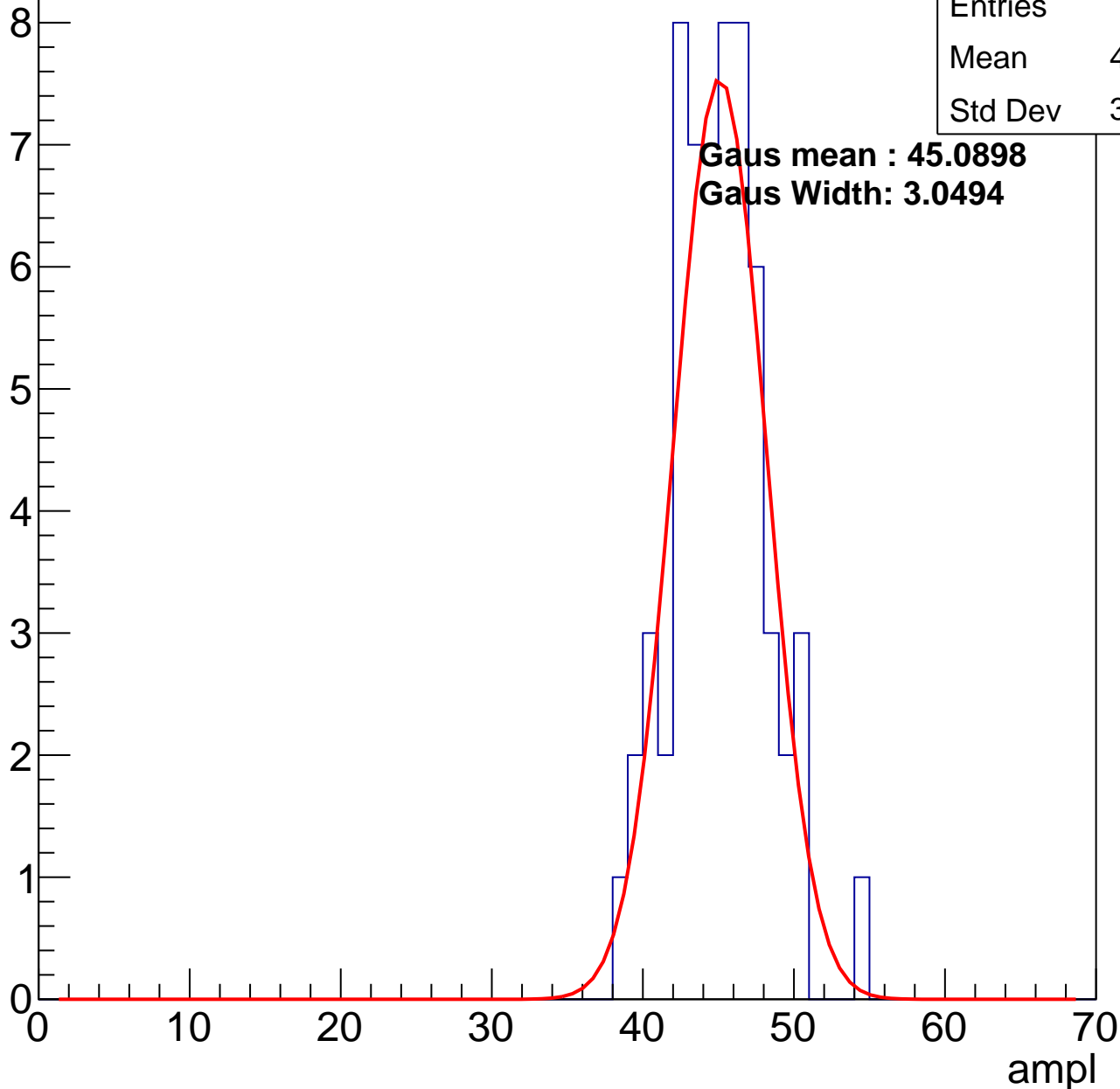
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.57
Std Dev	3.059

Gaus mean : 45.0898

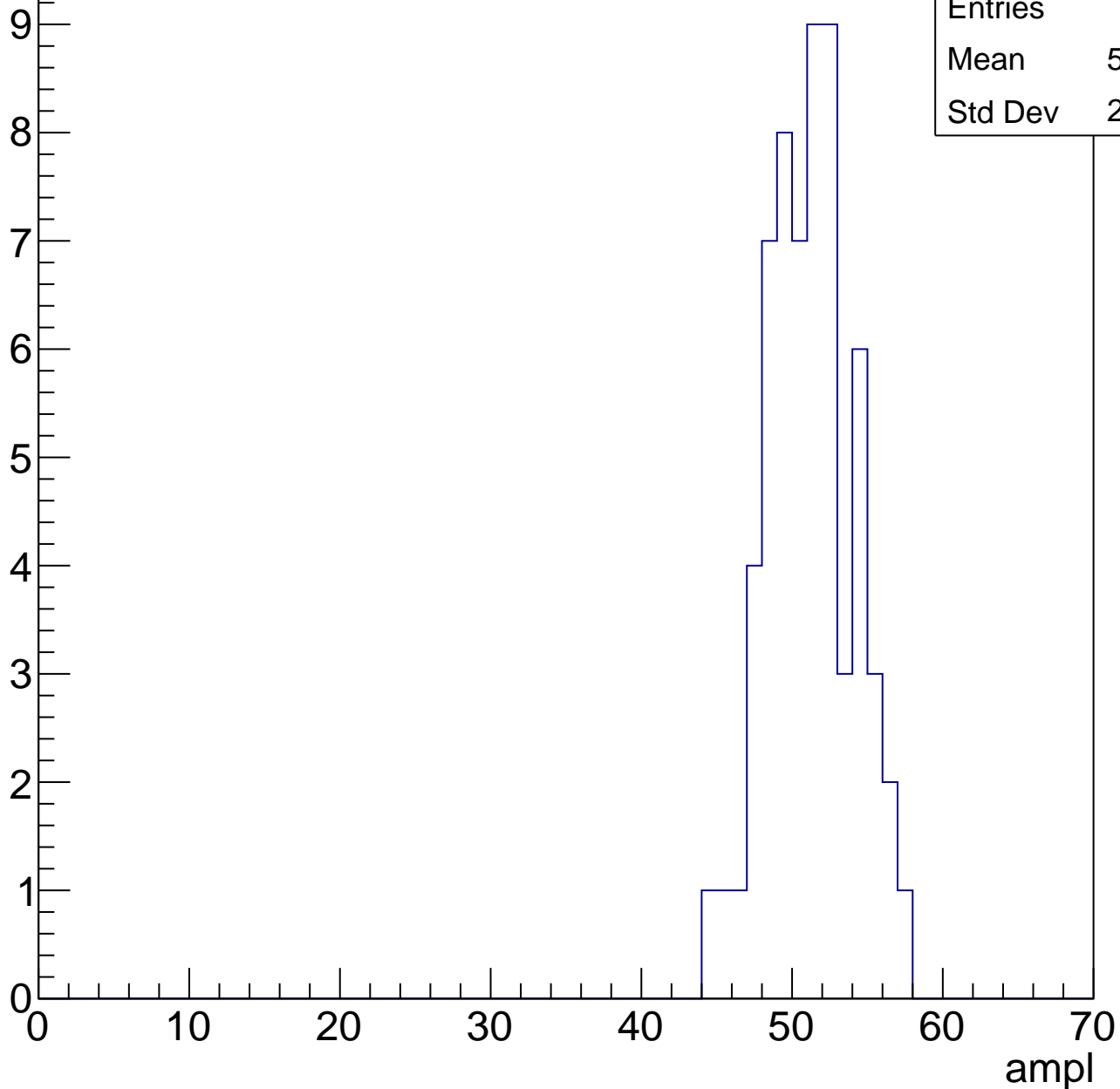
Gaus Width: 3.0494



B0L002S, U9-ch2, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

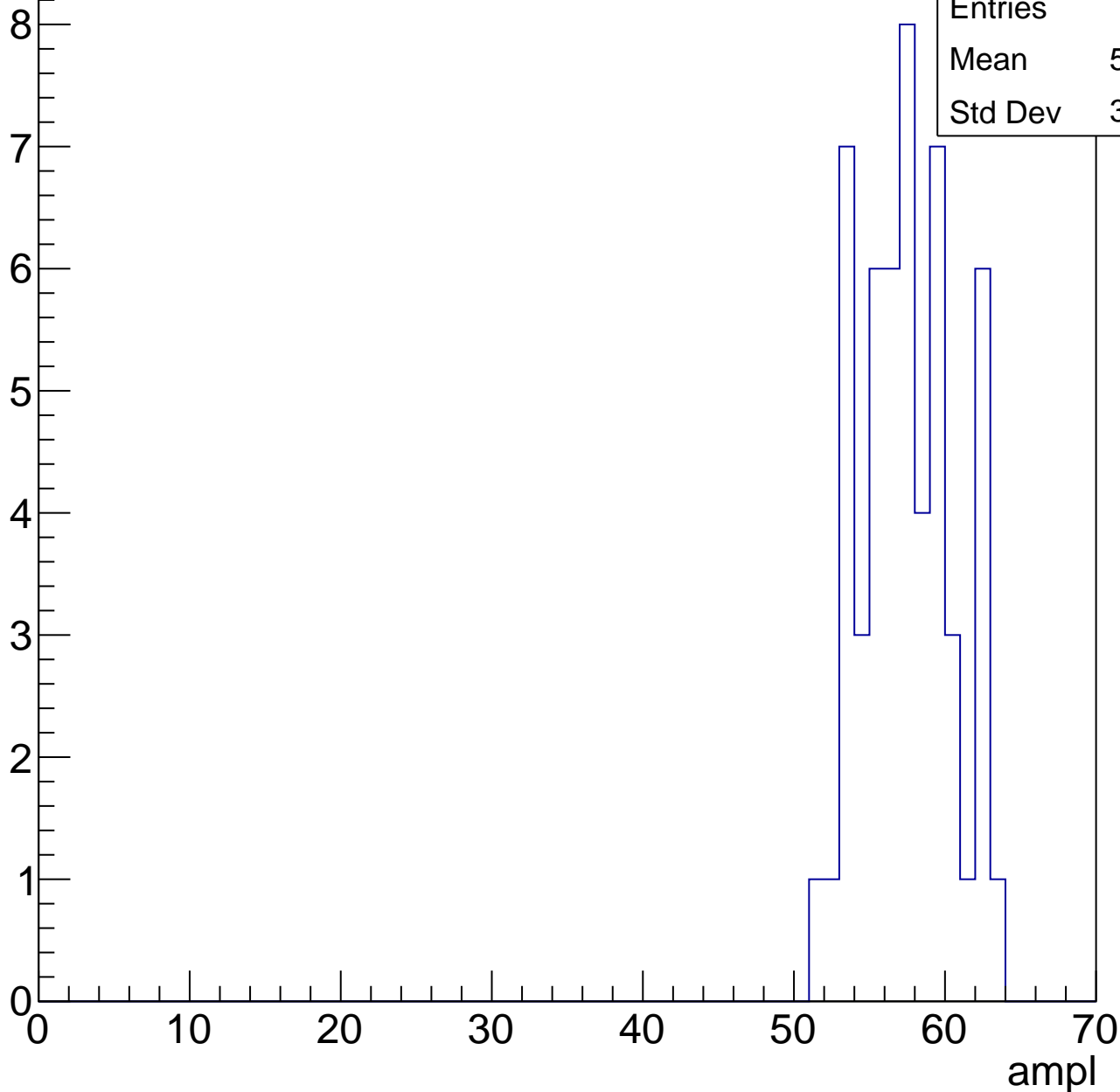


B0L002S, U9-ch2, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

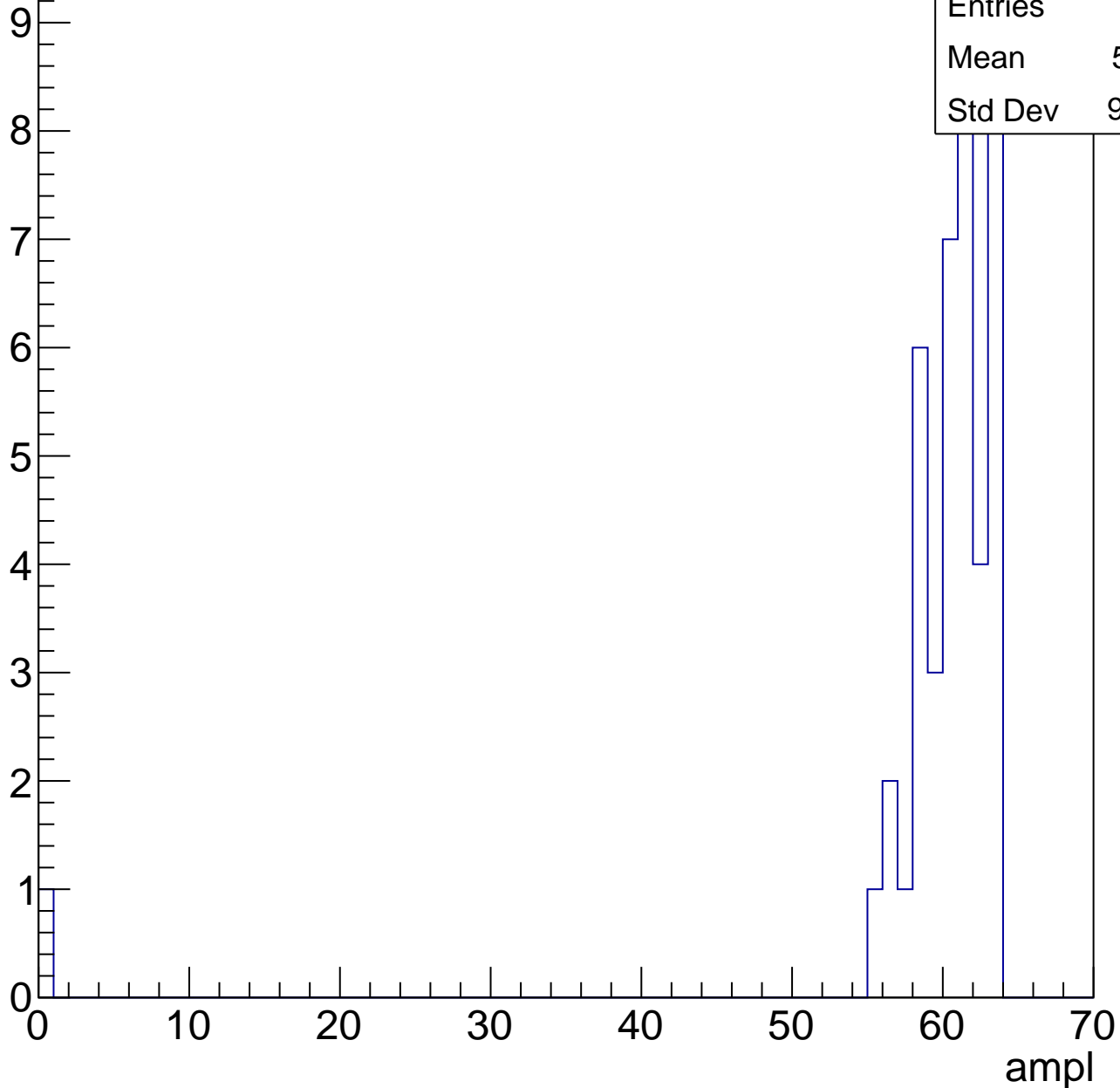
Entries	54
Mean	57.02
Std Dev	3.015



B0L002S, U9-ch2, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch3, adc0

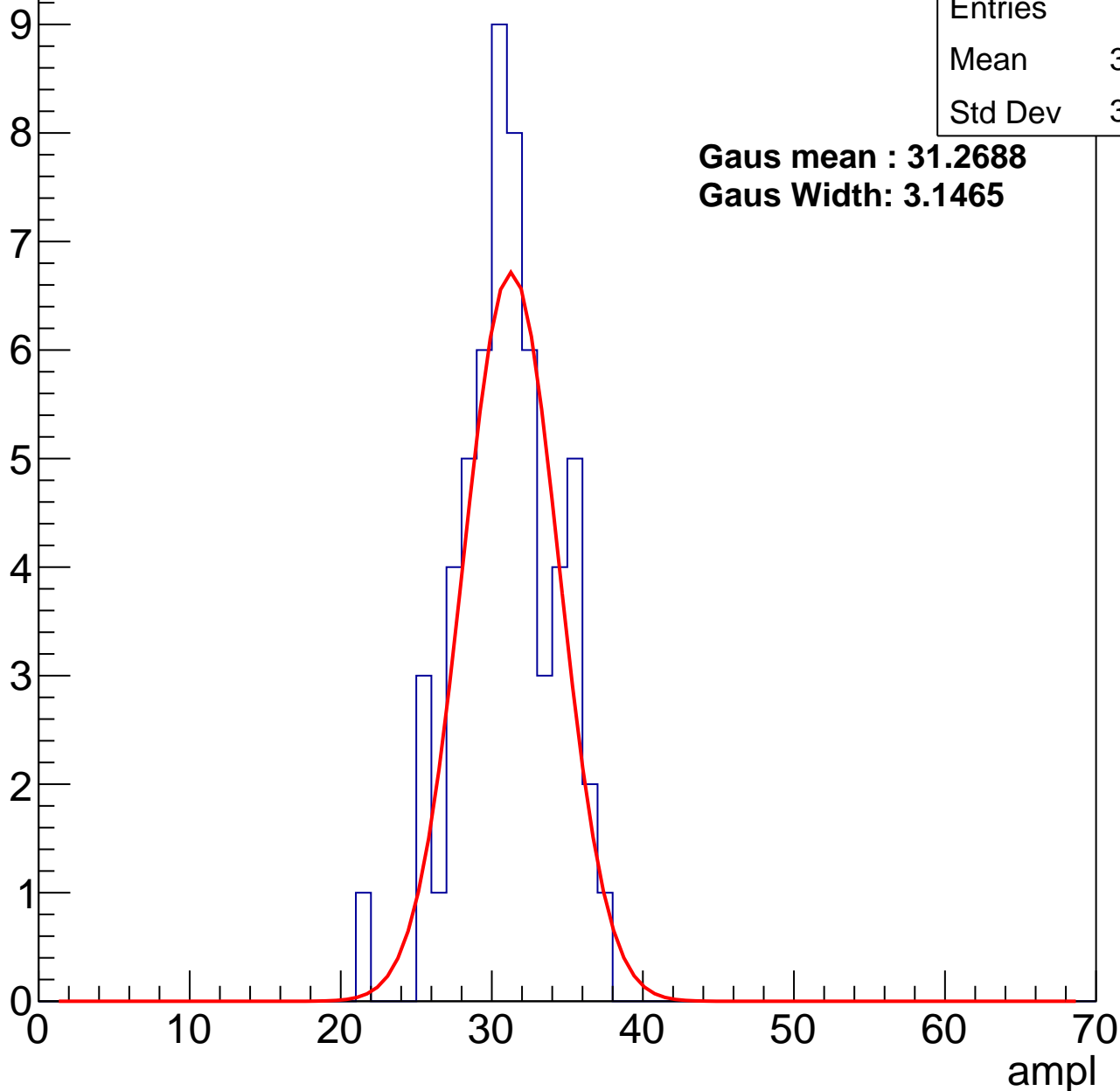
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	30.57
Std Dev	3.174

Gaus mean : 31.2688

Gaus Width: 3.1465



B0L002S, U9-ch3, adc1

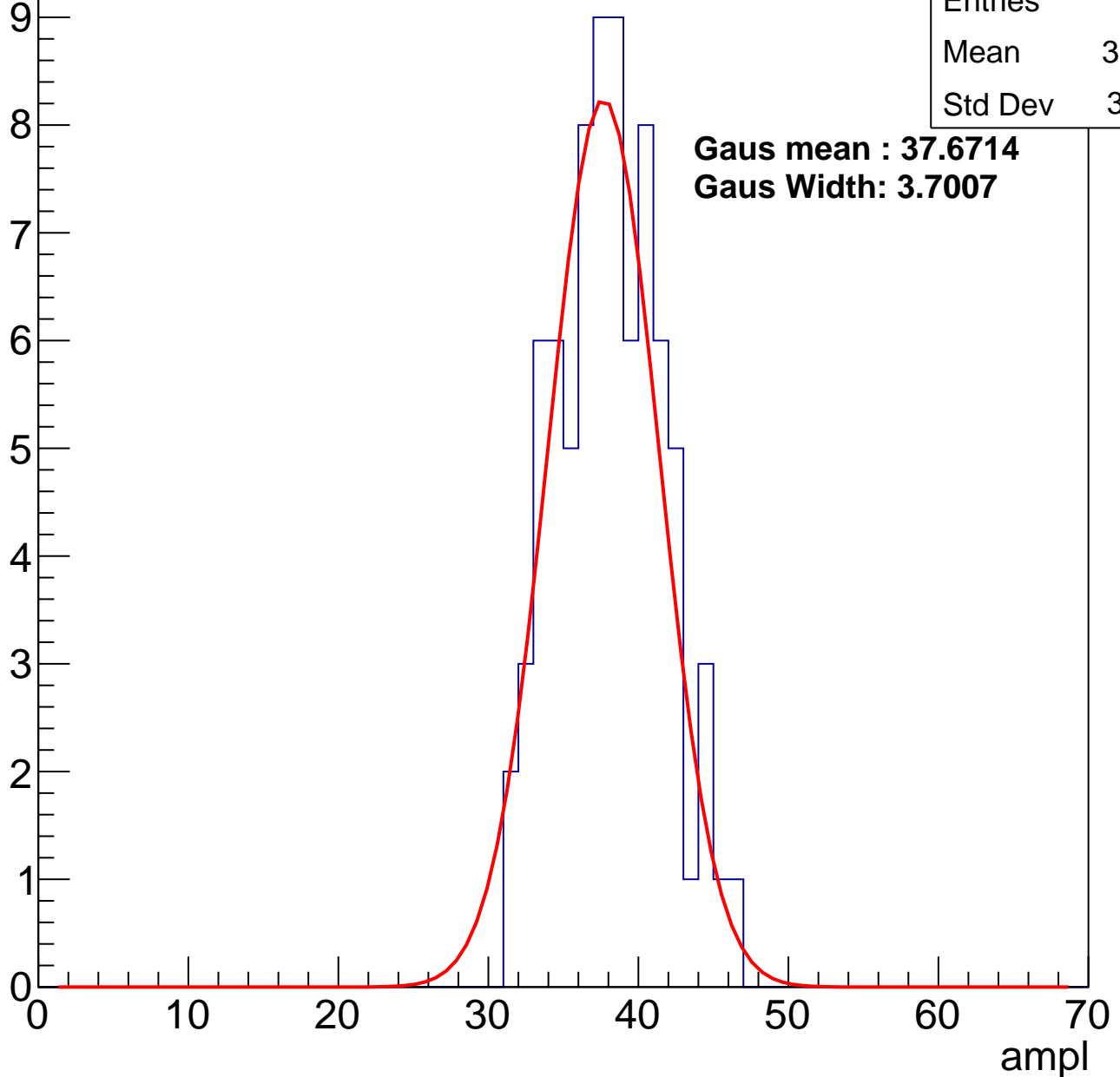
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	37.65
Std Dev	3.461

Gaus mean : 37.6714

Gaus Width: 3.7007



B0L002S, U9-ch3, adc2

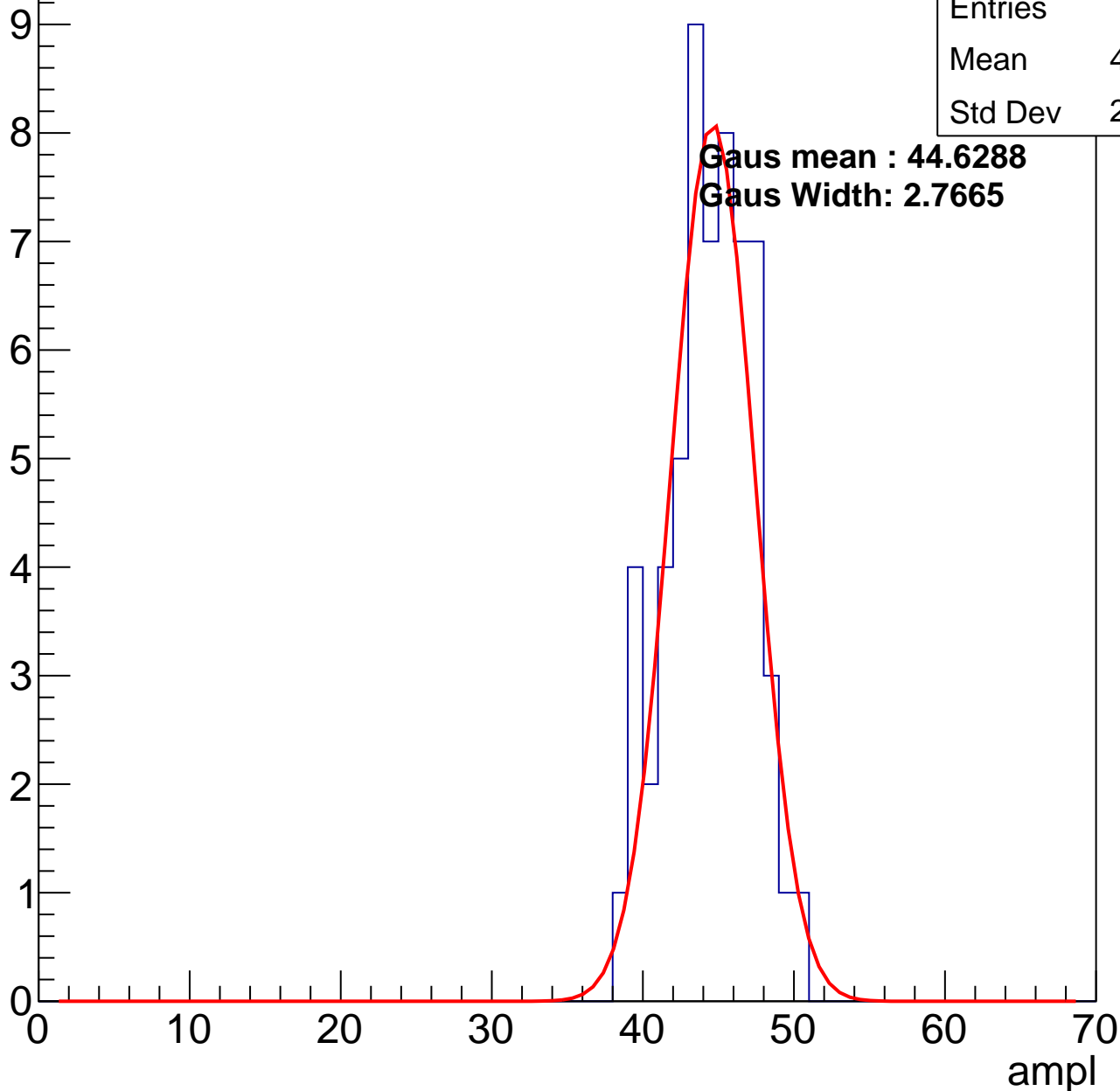
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44.02
Std Dev	2.734

Gaus mean : 44.6288

Gaus Width: 2.7665

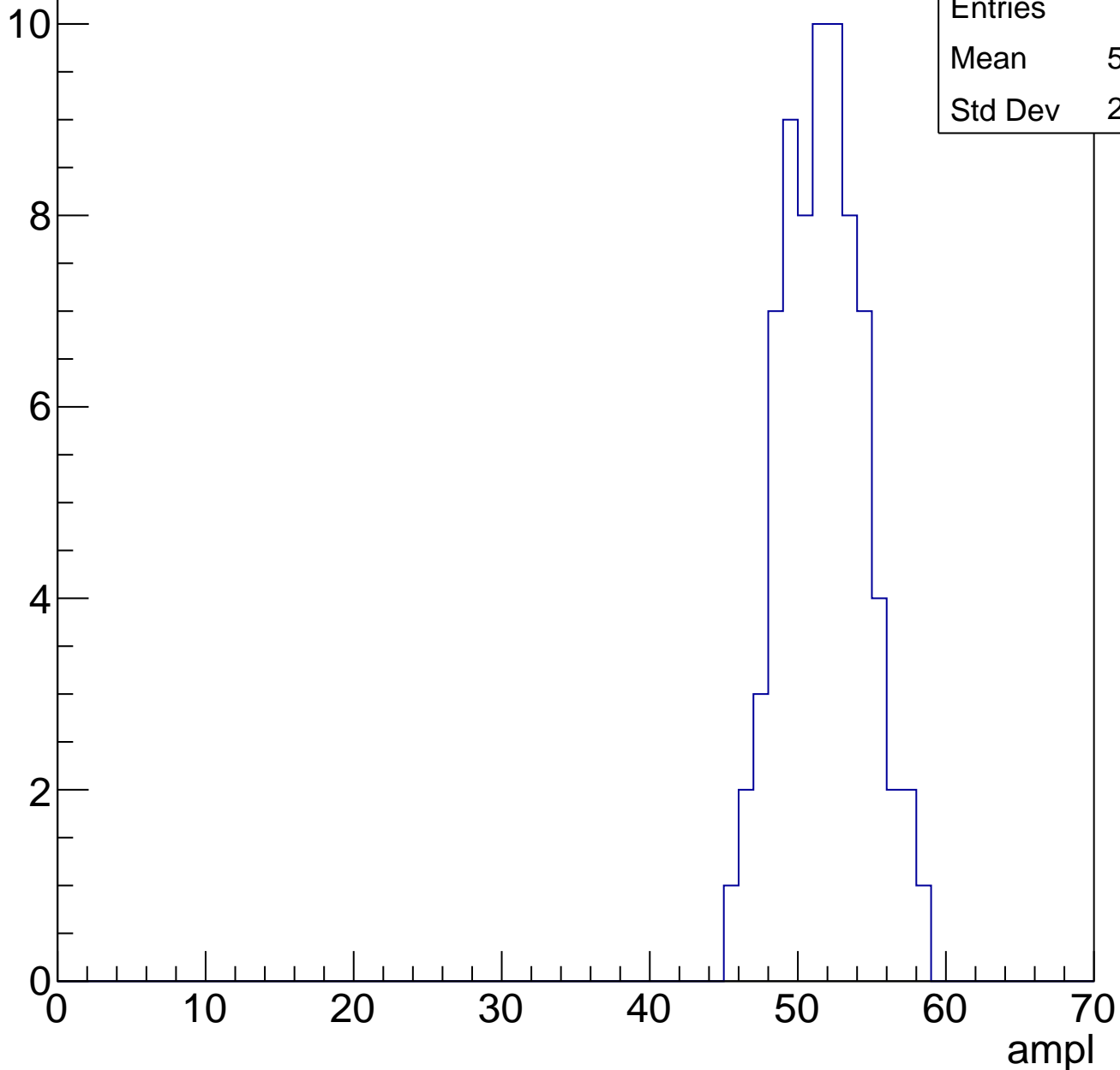


B0L002S, U9-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	51.23
Std Dev	2.793

Entry

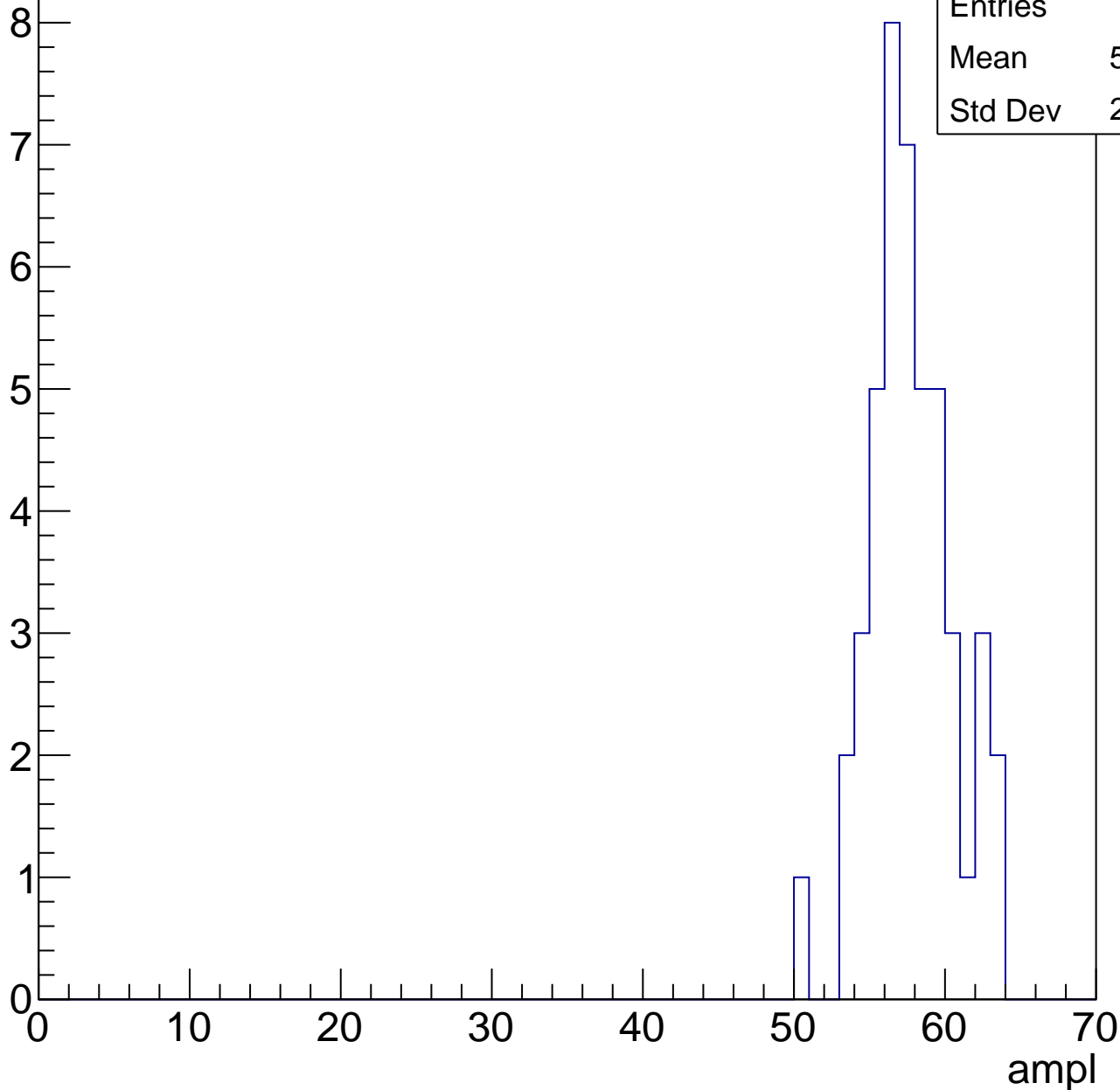


B0L002S, U9-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

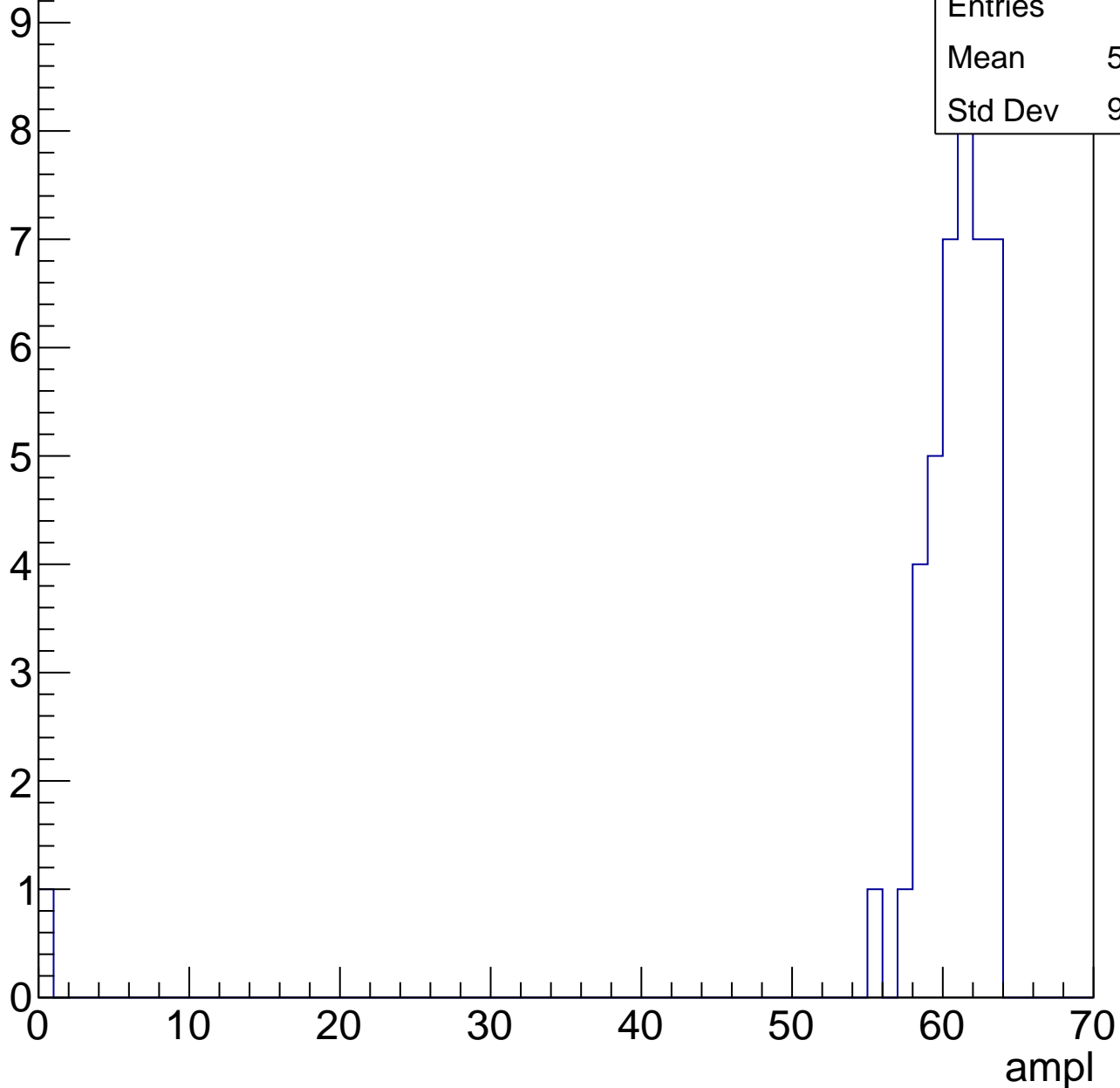
Entries	45
Mean	57.29
Std Dev	2.778



B0L002S, U9-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

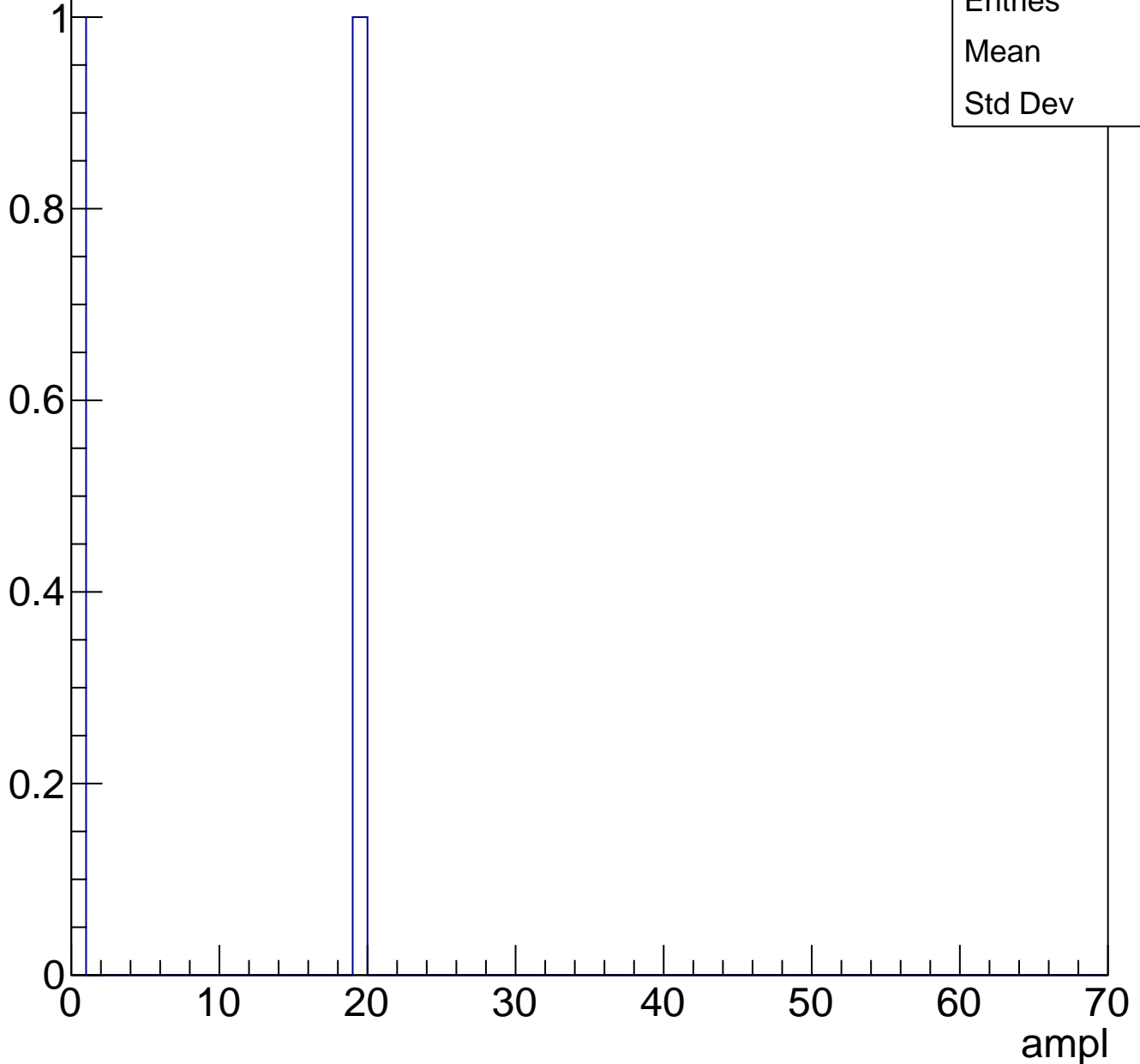
Entry



B0L002S, U9-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L002S, U9-ch4, adc0

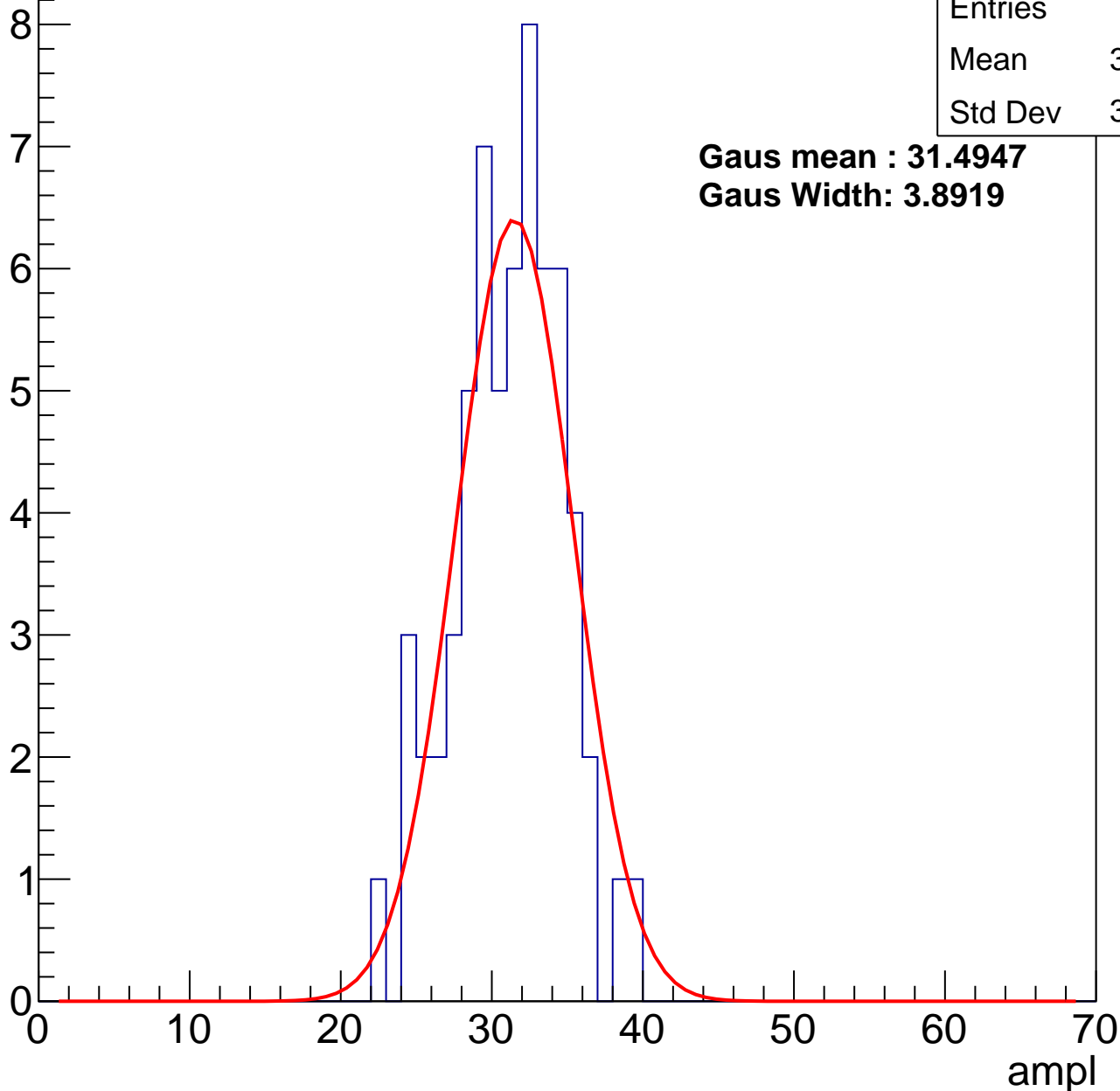
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.69
Std Dev	3.554

Gaus mean : 31.4947

Gaus Width: 3.8919



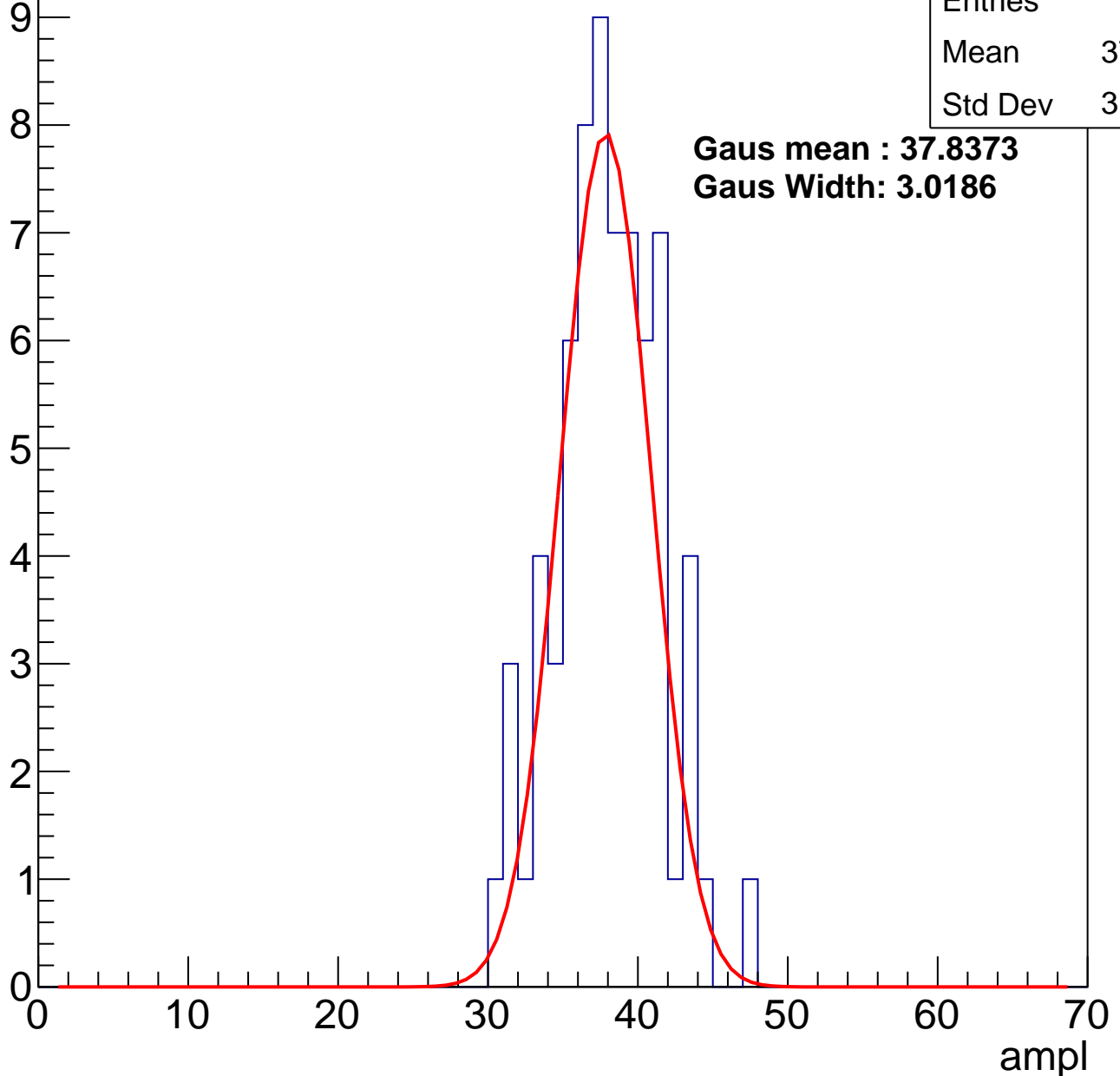
B0L002S, U9-ch4, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.55
Std Dev	3.412

Gaus mean : 37.8373
Gaus Width: 3.0186



B0L002S, U9-ch4, adc2

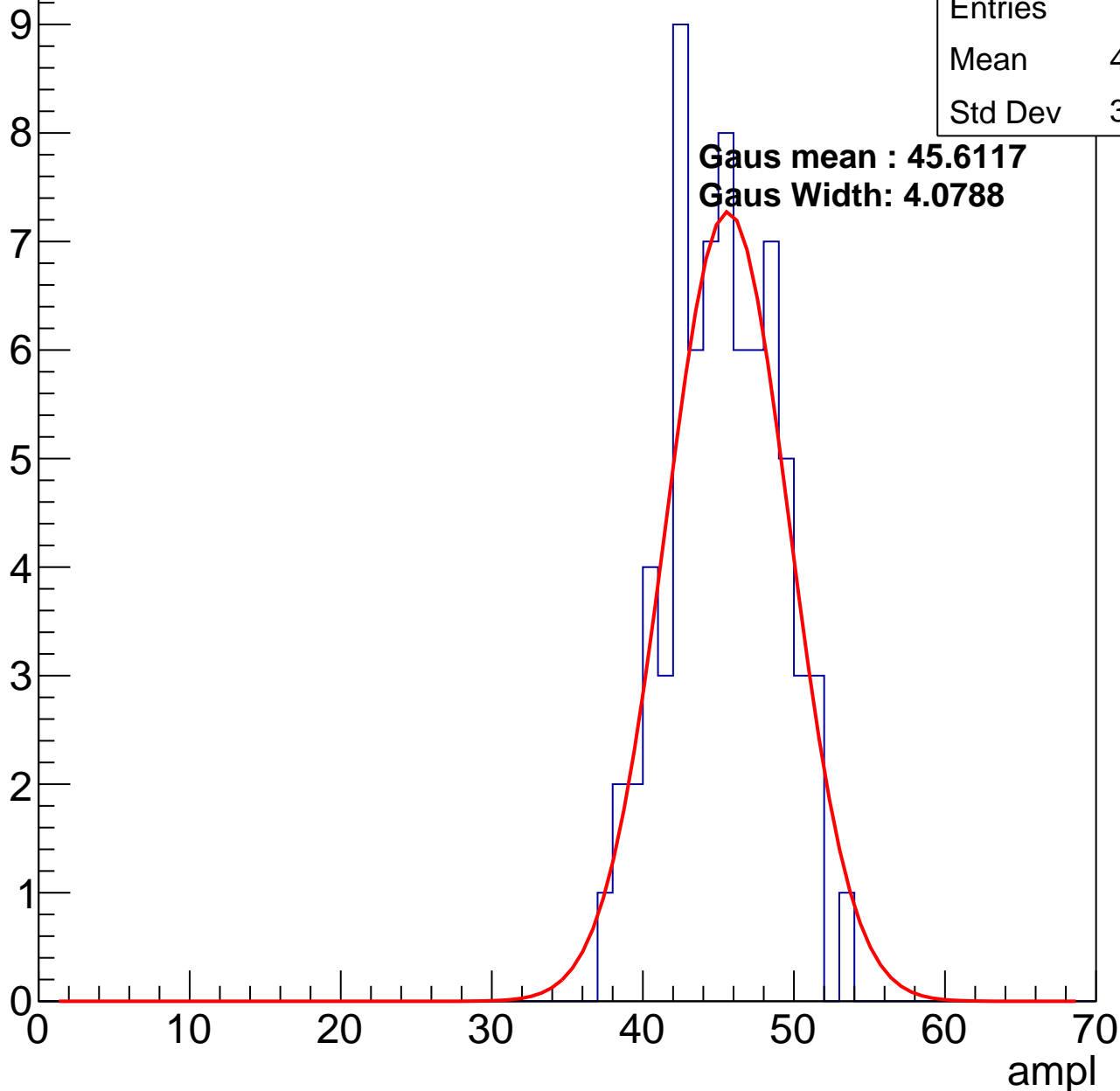
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.84
Std Dev	3.535

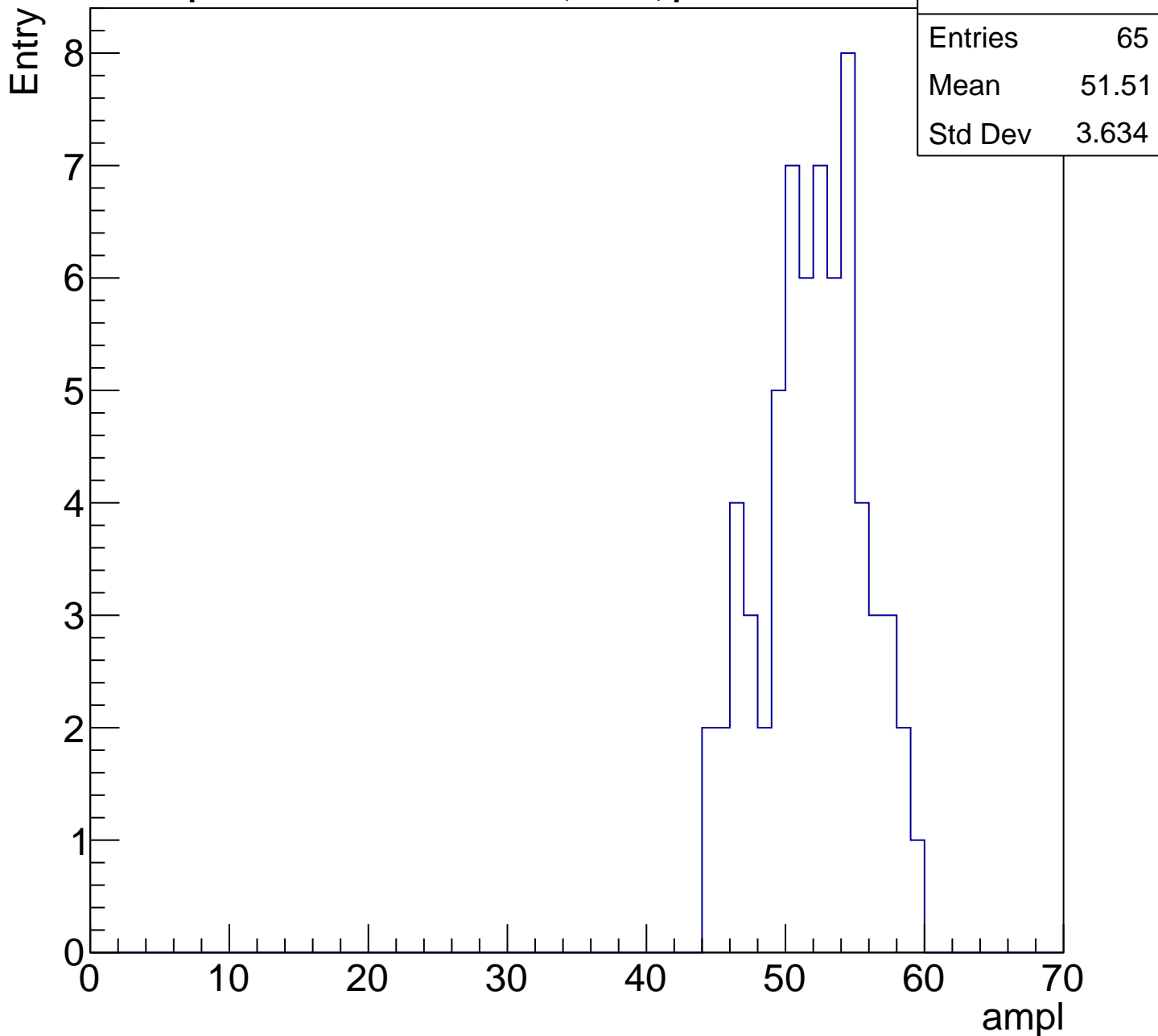
Gaus mean : 45.6117

Gaus Width: 4.0788



B0L002S, U9-ch4, adc3

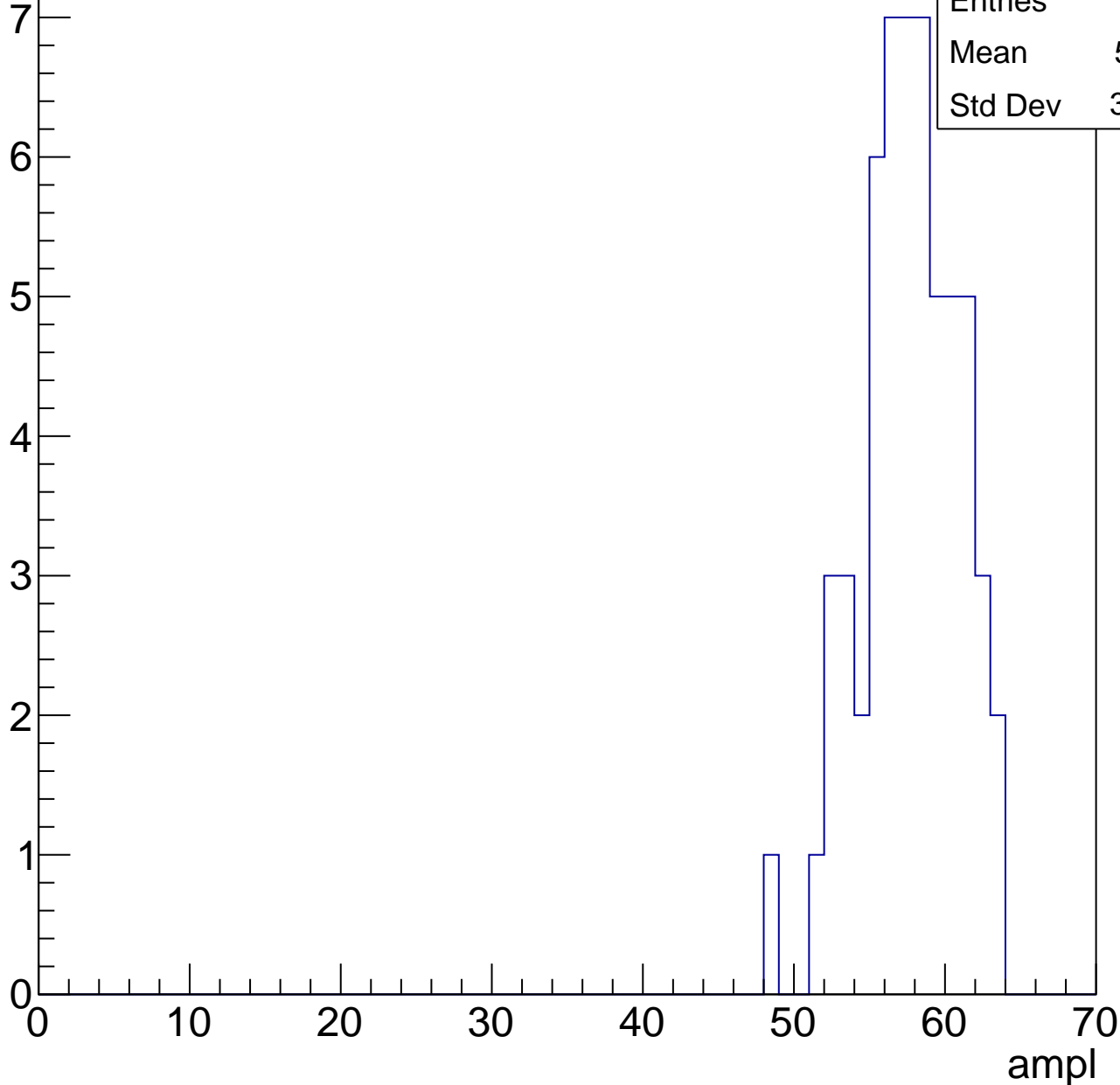
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



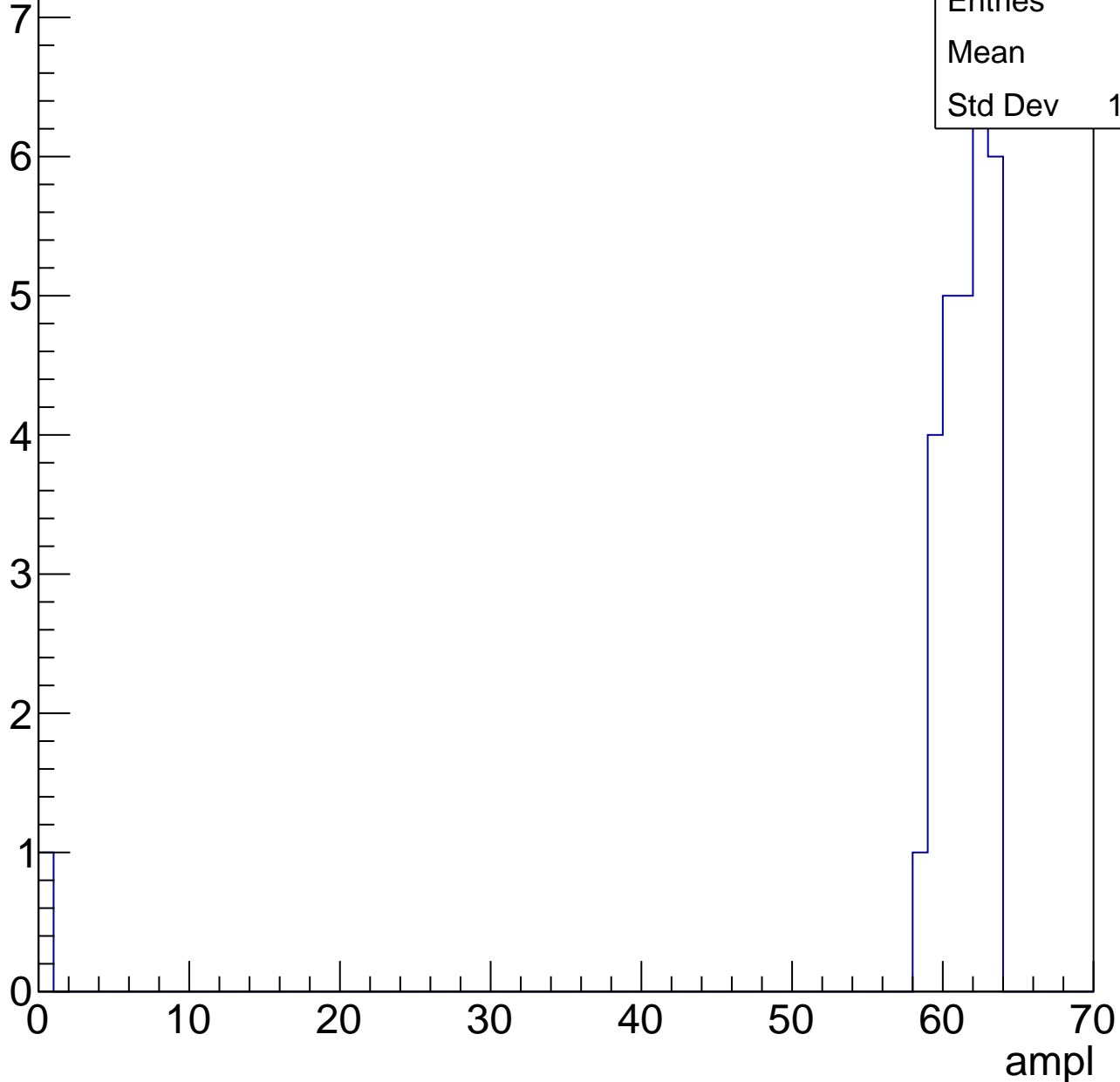
Entries	57
Mean	57.21
Std Dev	3.205

B0L002S, U9-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59
Std Dev	11.24



B0L002S, U9-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L002S, U9-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch5, adc0

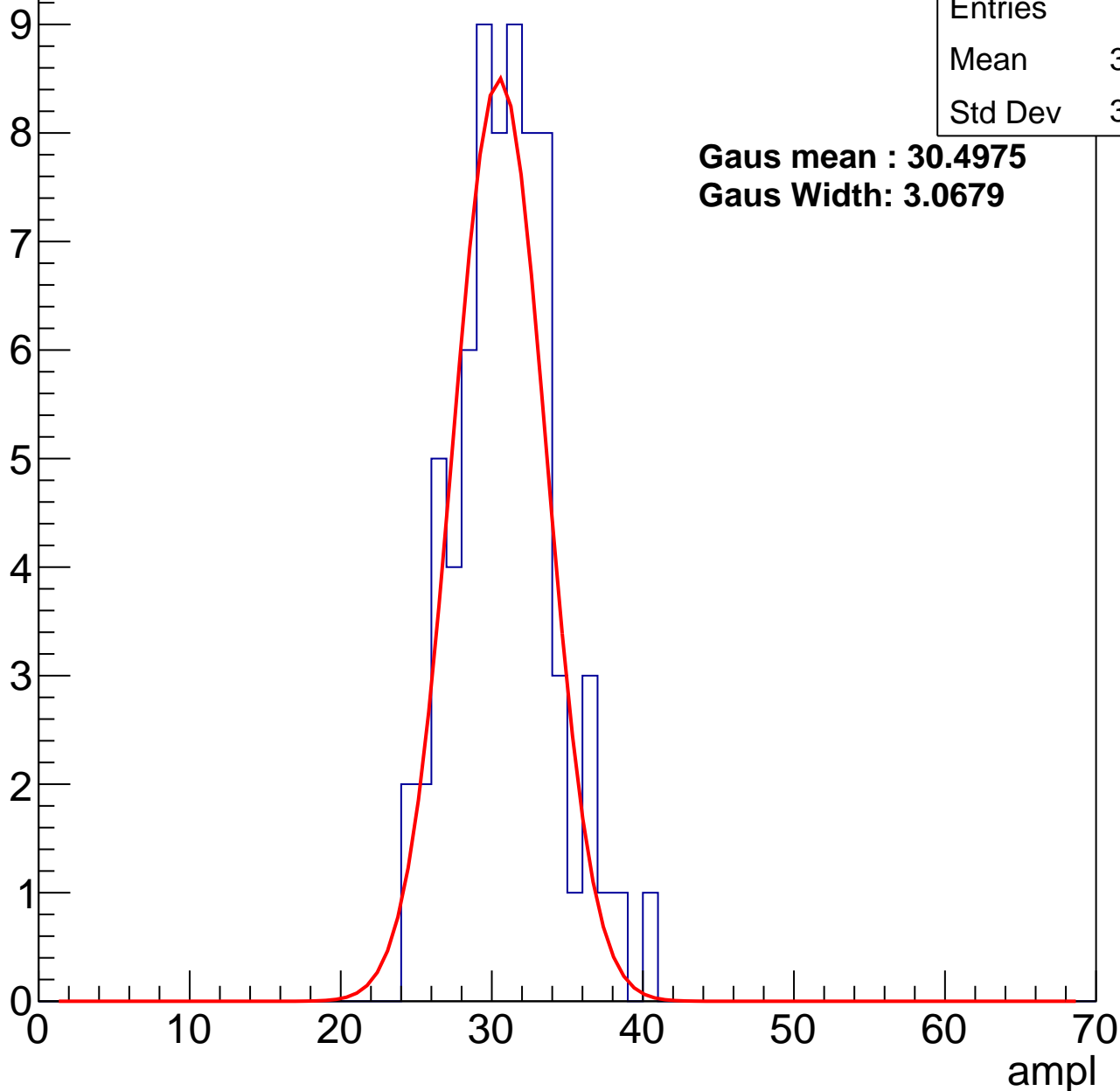
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.48
Std Dev	3.275

Gaus mean : 30.4975

Gaus Width: 3.0679



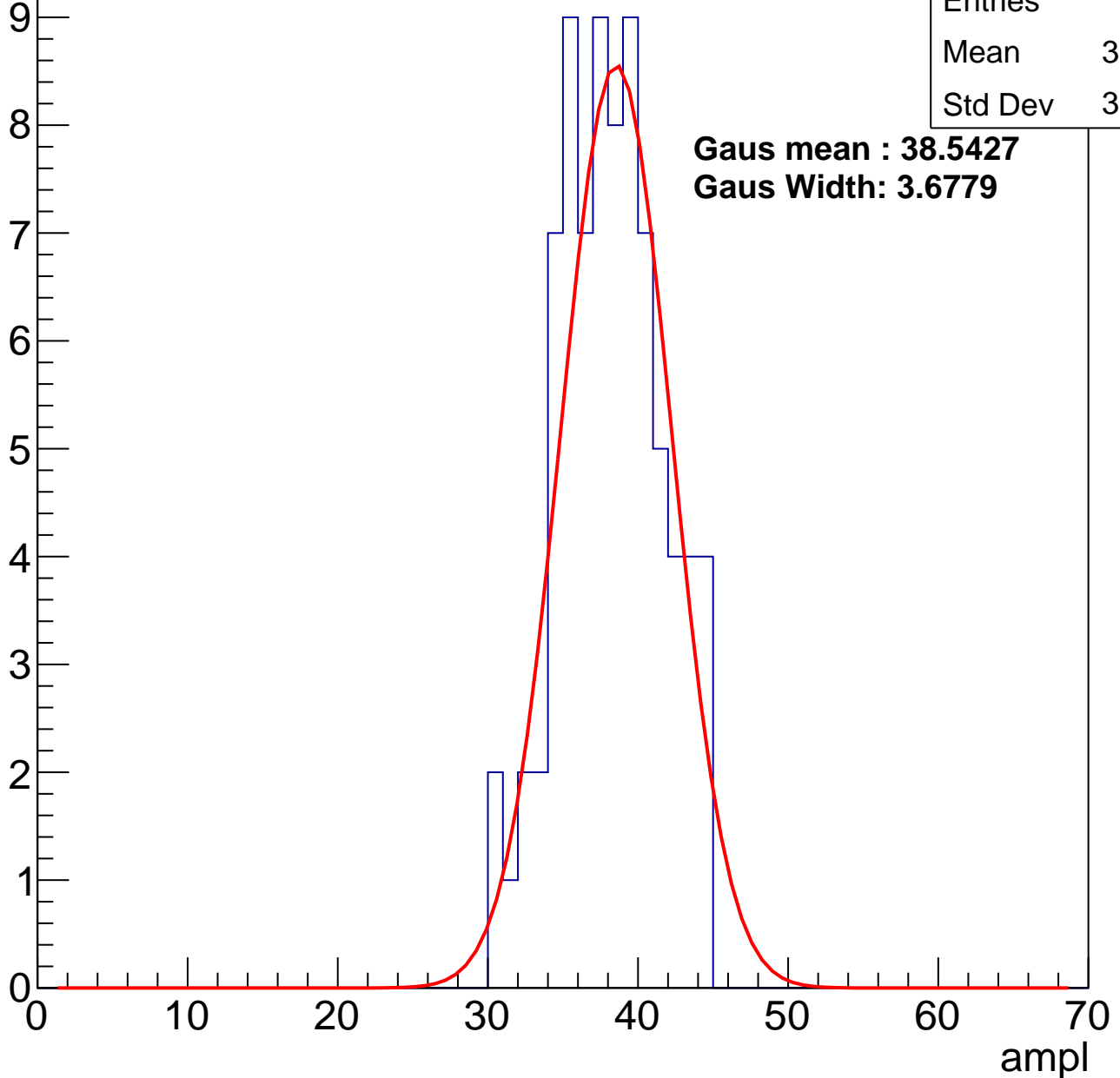
B0L002S, U9-ch5, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	37.69
Std Dev	3.375

Gaus mean : 38.5427
Gaus Width: 3.6779



B0L002S, U9-ch5, adc2

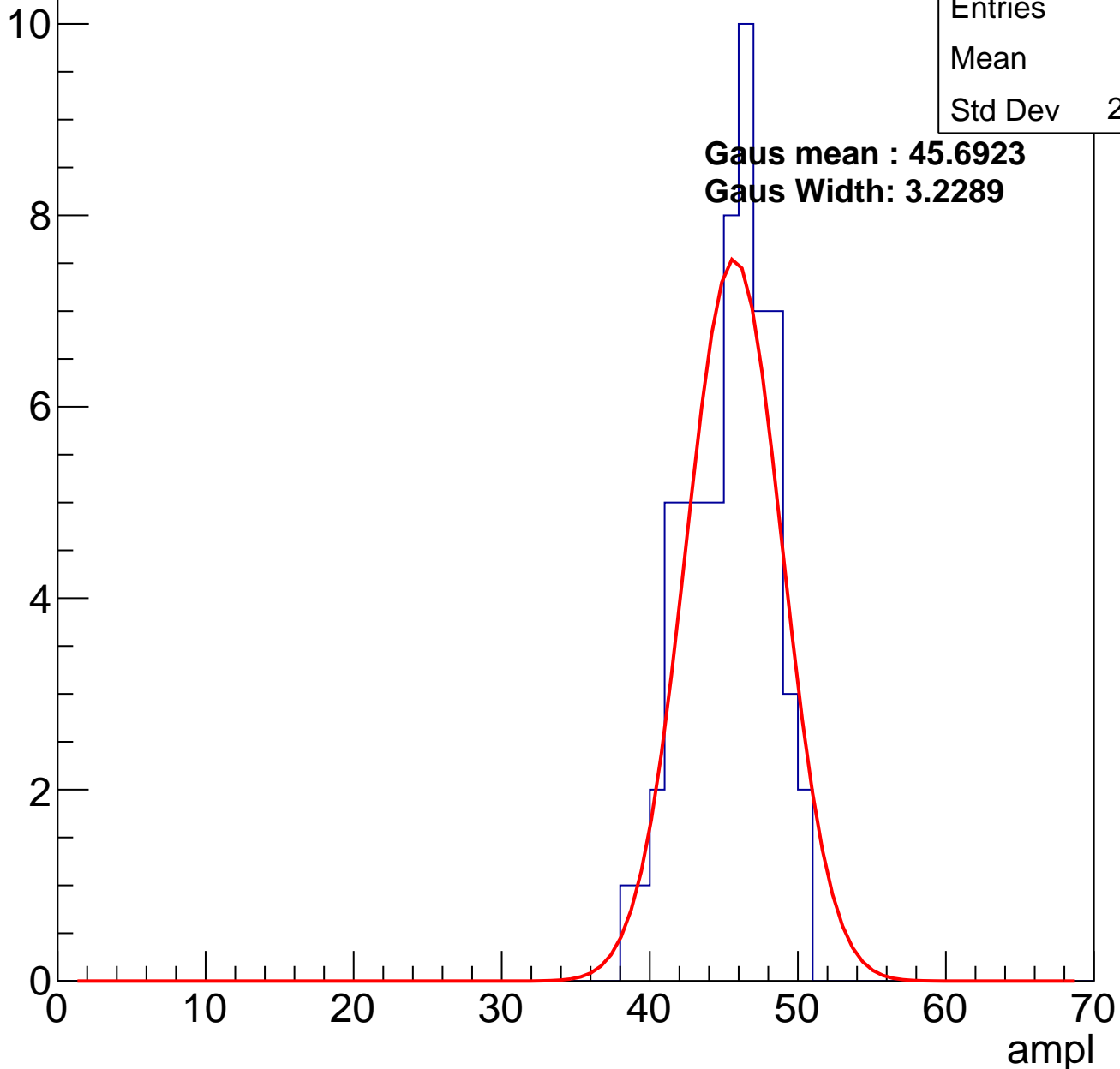
calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	44.9
Std Dev	2.815

Gaus mean : 45.6923

Gaus Width: 3.2289

Entry

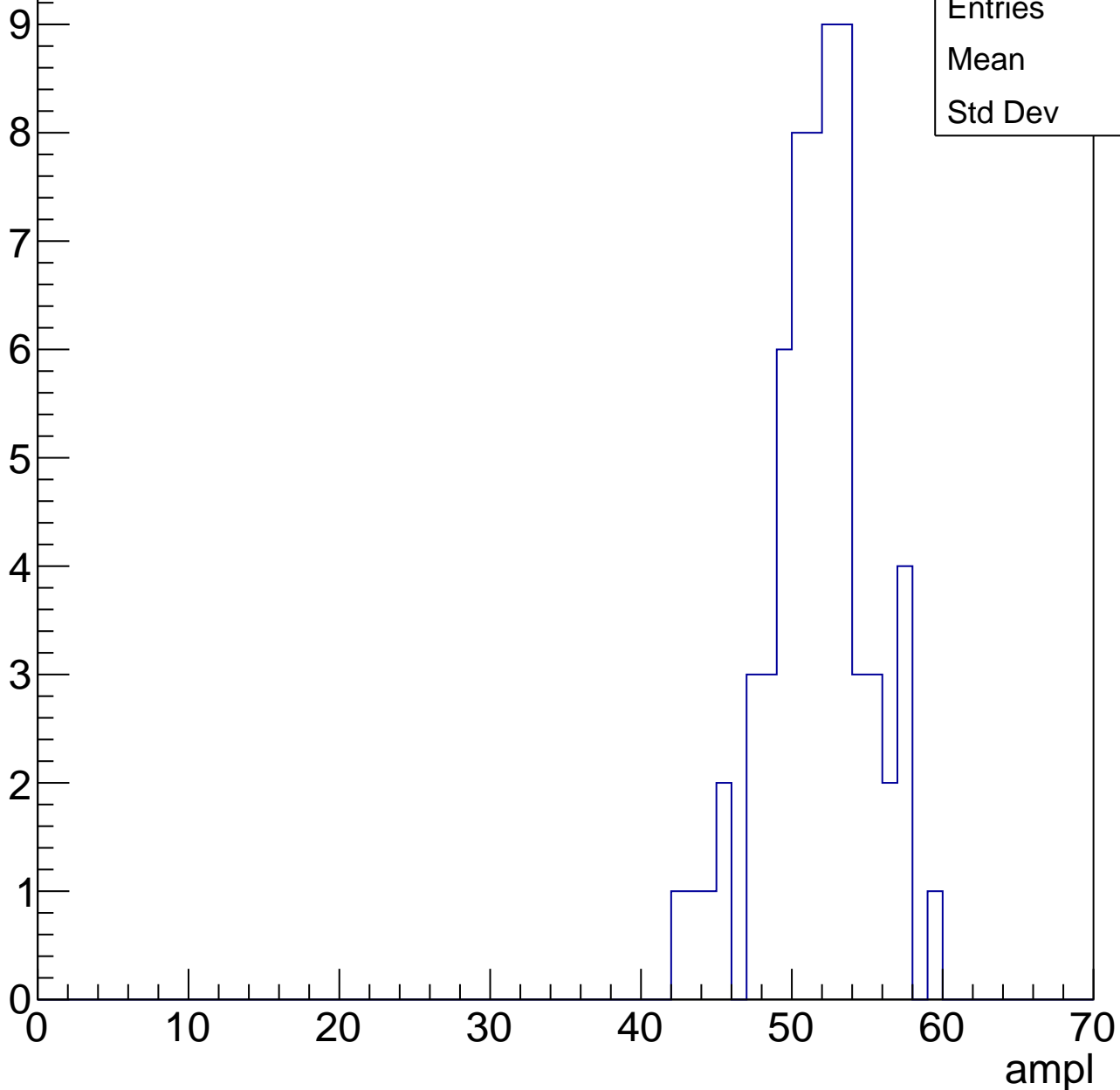


B0L002S, U9-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

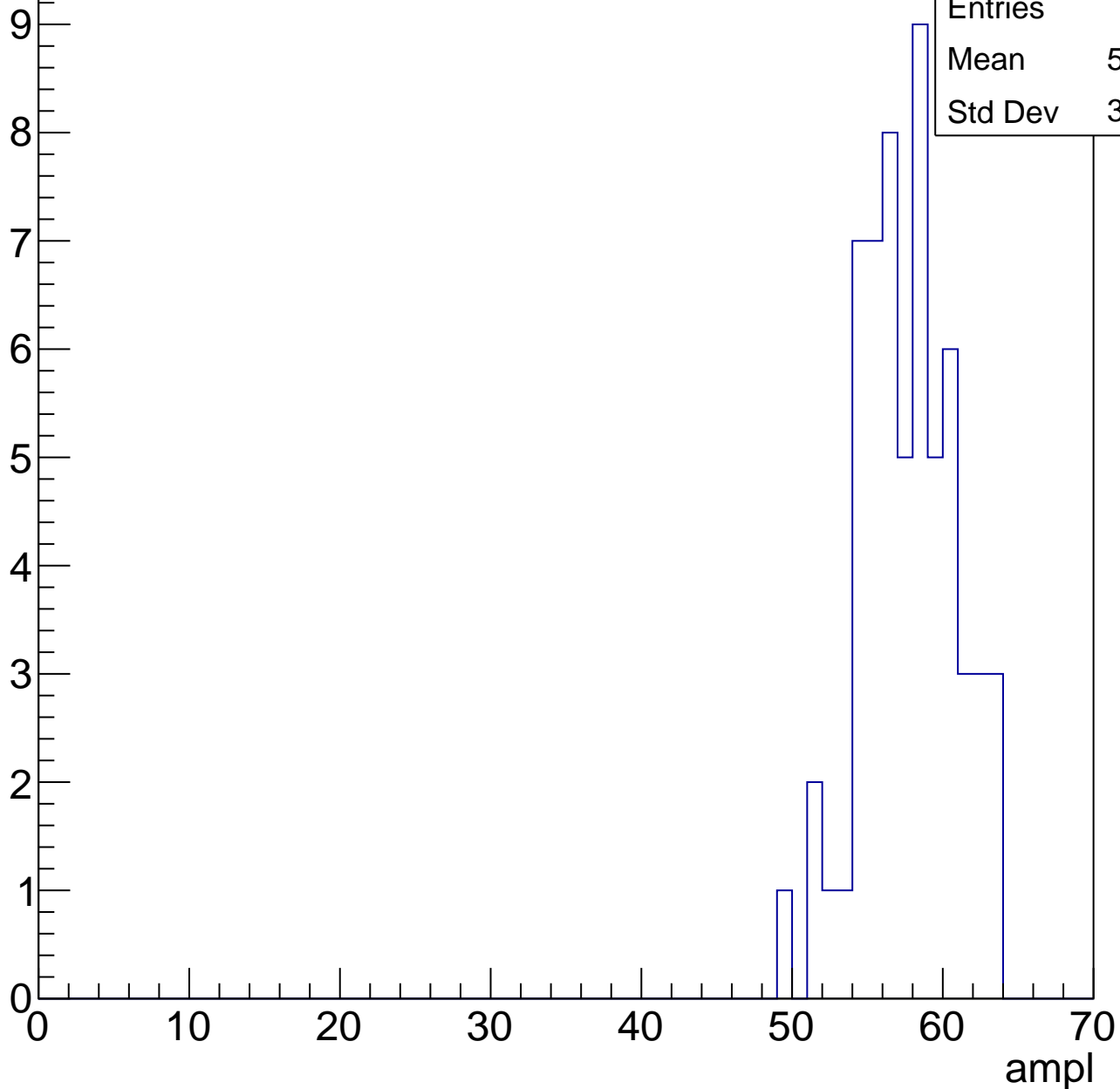
Entries	64
Mean	51.2
Std Dev	3.42



B0L002S, U9-ch5, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

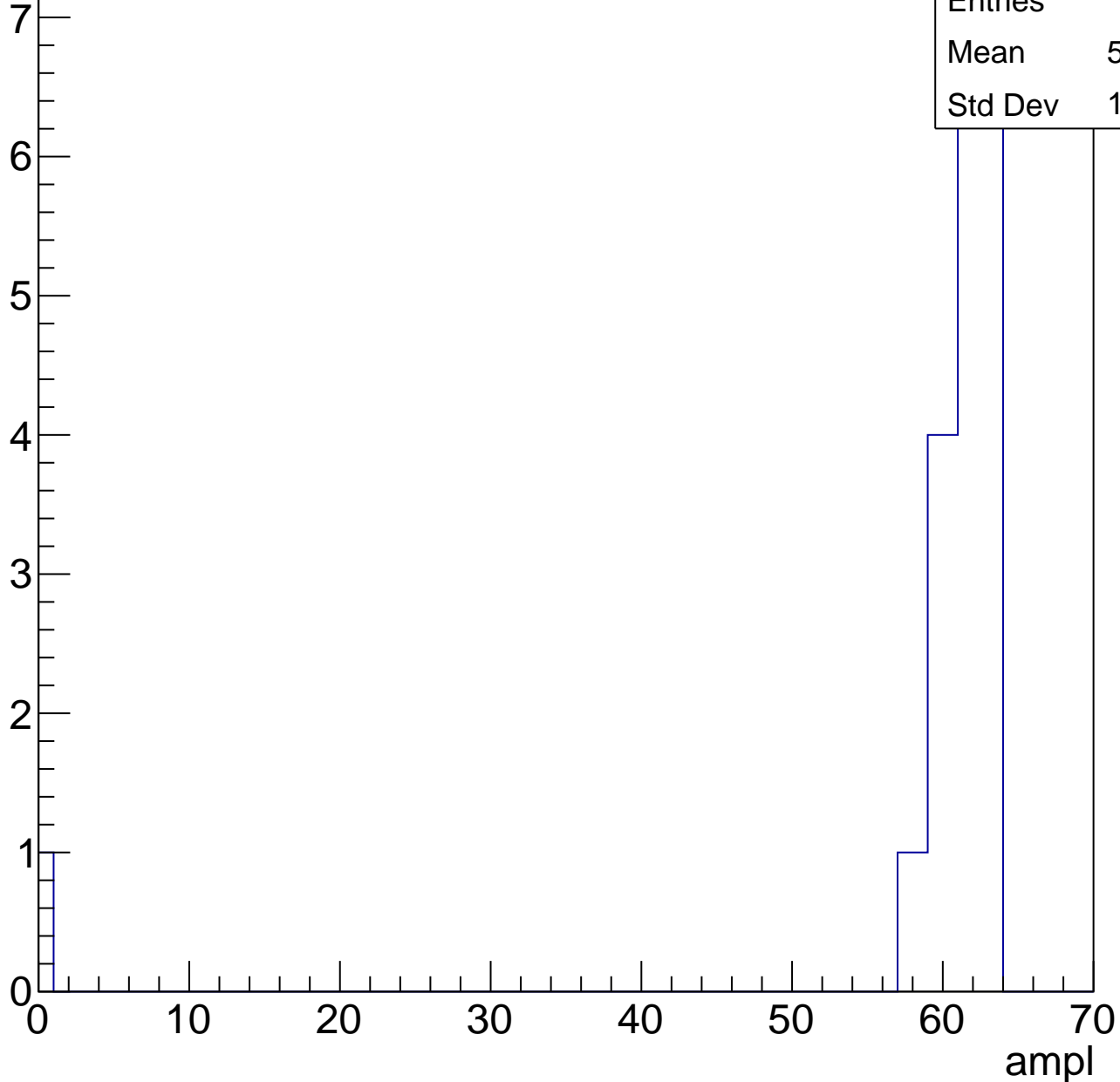


Entries	61
Mean	57.16
Std Dev	3.106

B0L002S, U9-ch5, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L002S, U9-ch6, adc0

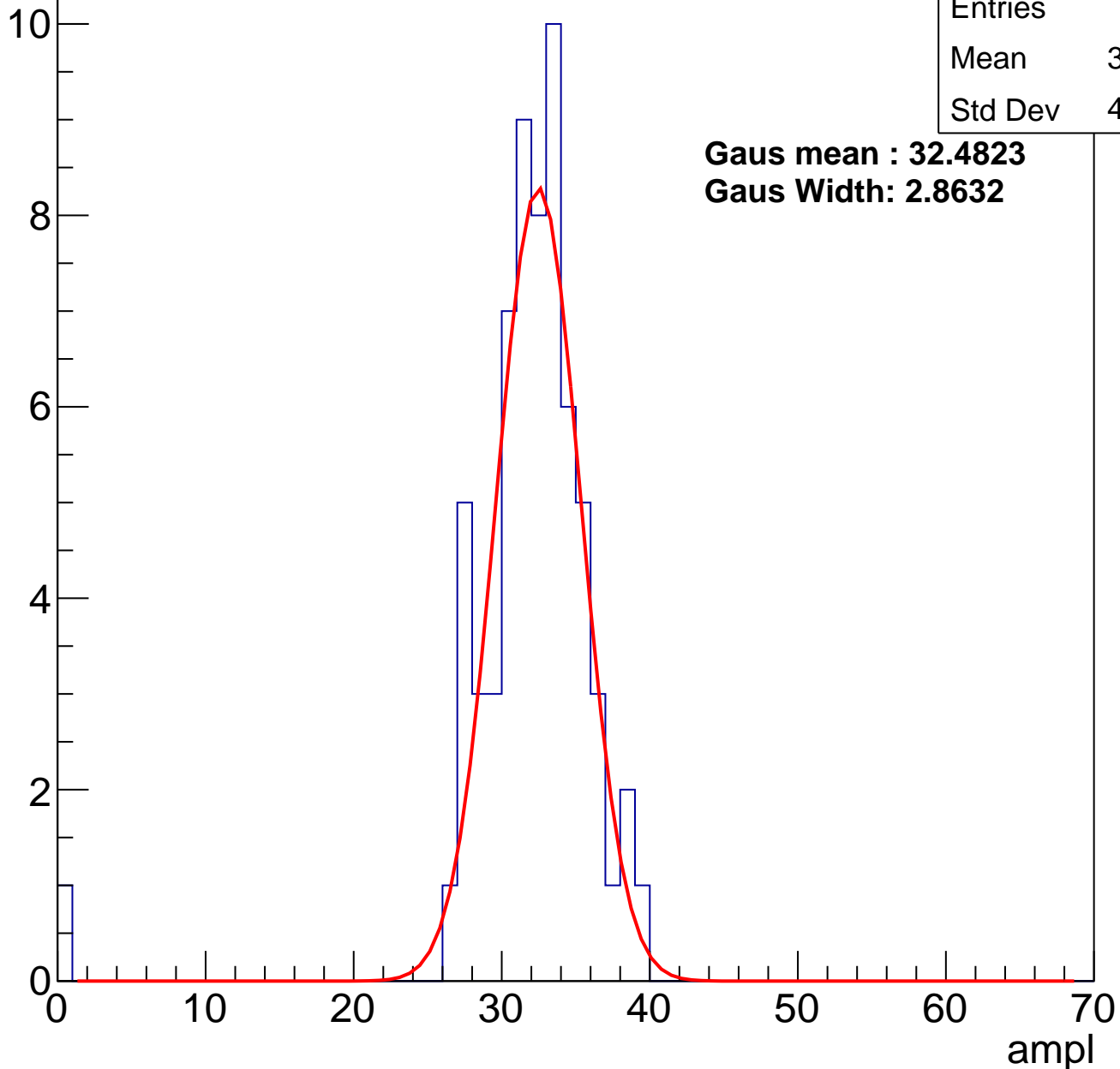
calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	31.48
Std Dev	4.887

Gaus mean : 32.4823

Gaus Width: 2.8632

Entry



B0L002S, U9-ch6, adc1

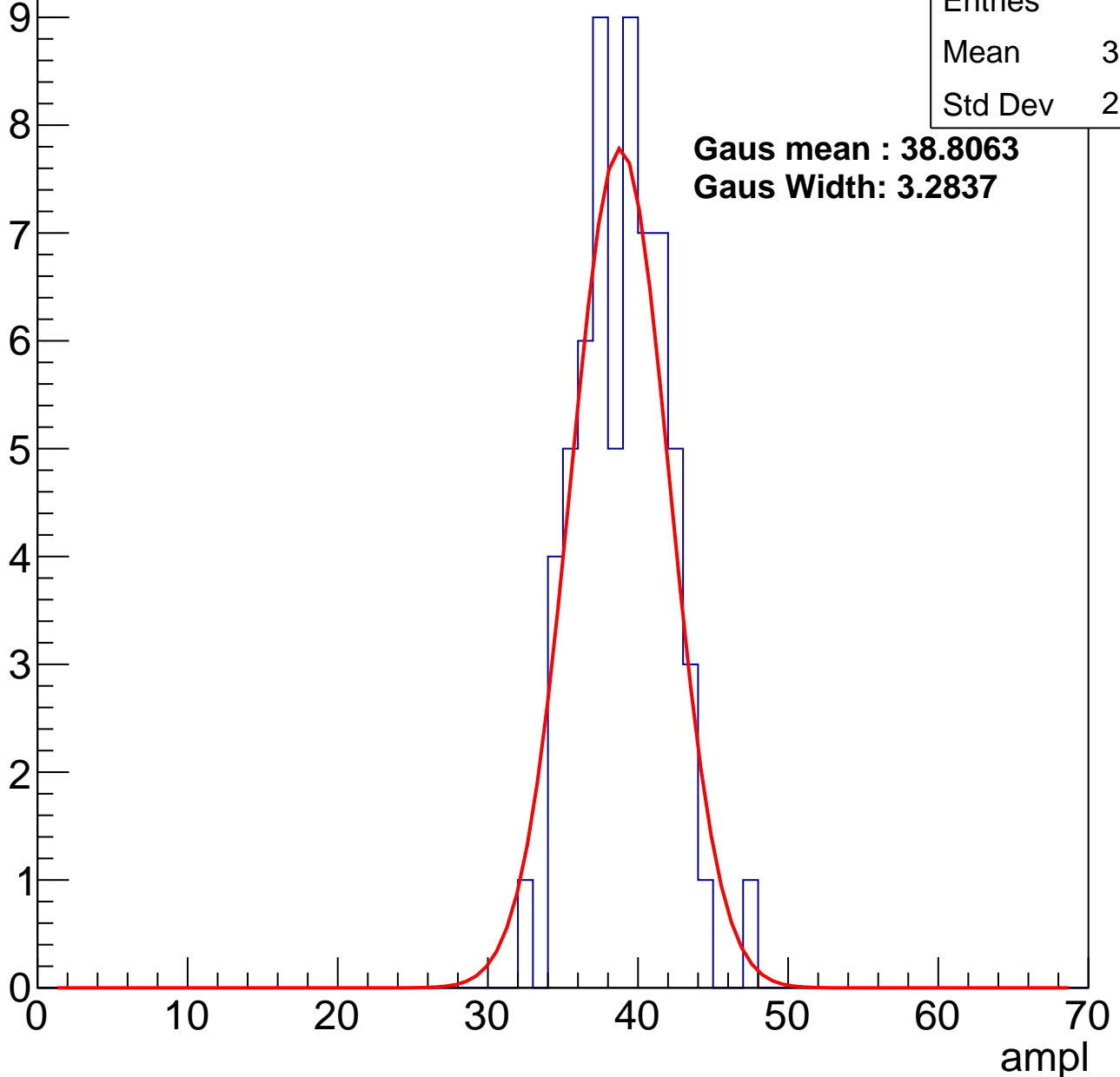
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.57
Std Dev	2.899

Gaus mean : 38.8063

Gaus Width: 3.2837



B0L002S, U9-ch6, adc2

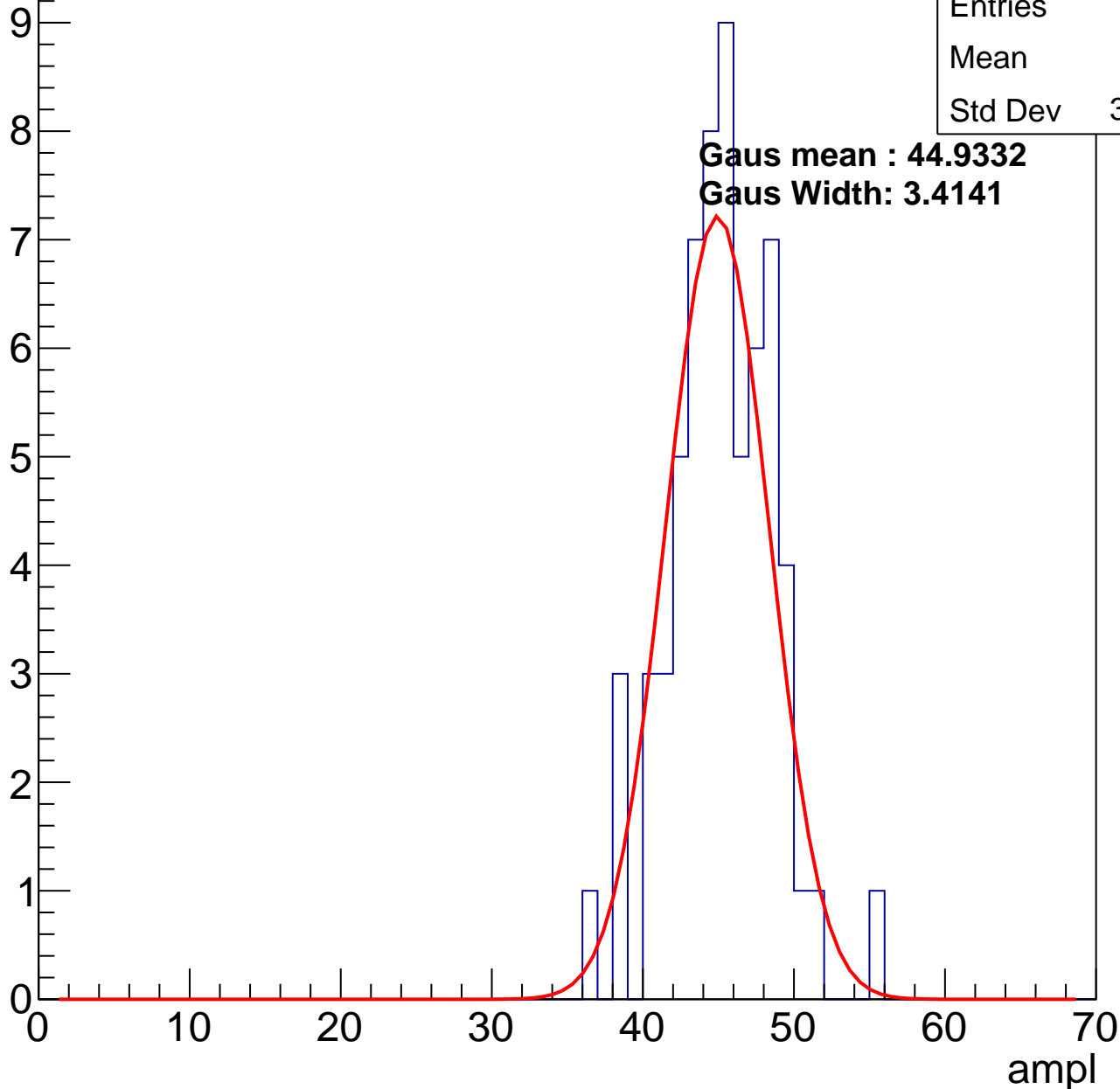
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.7
Std Dev	3.408

Gaus mean : 44.9332

Gaus Width: 3.4141

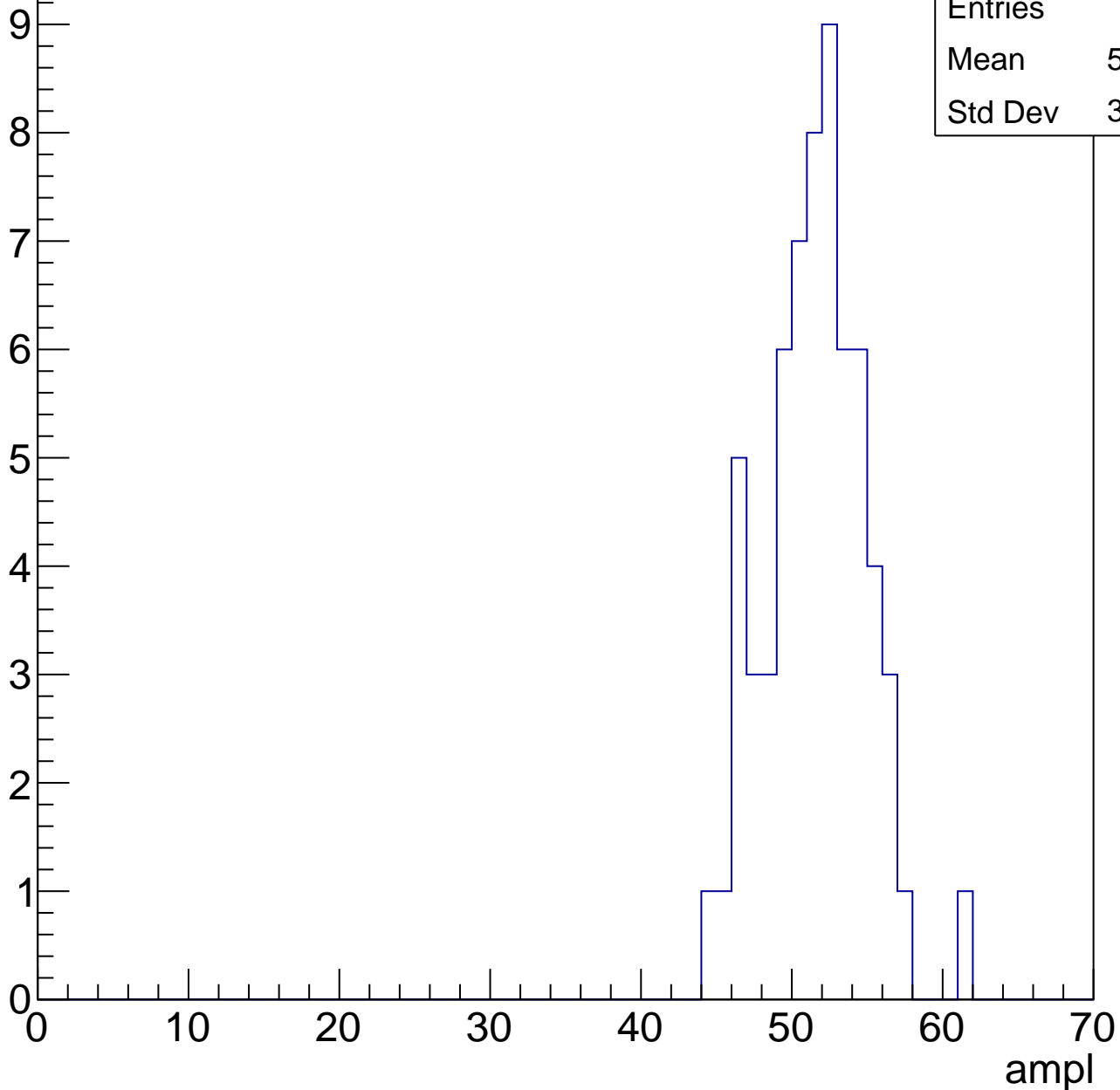


B0L002S, U9-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

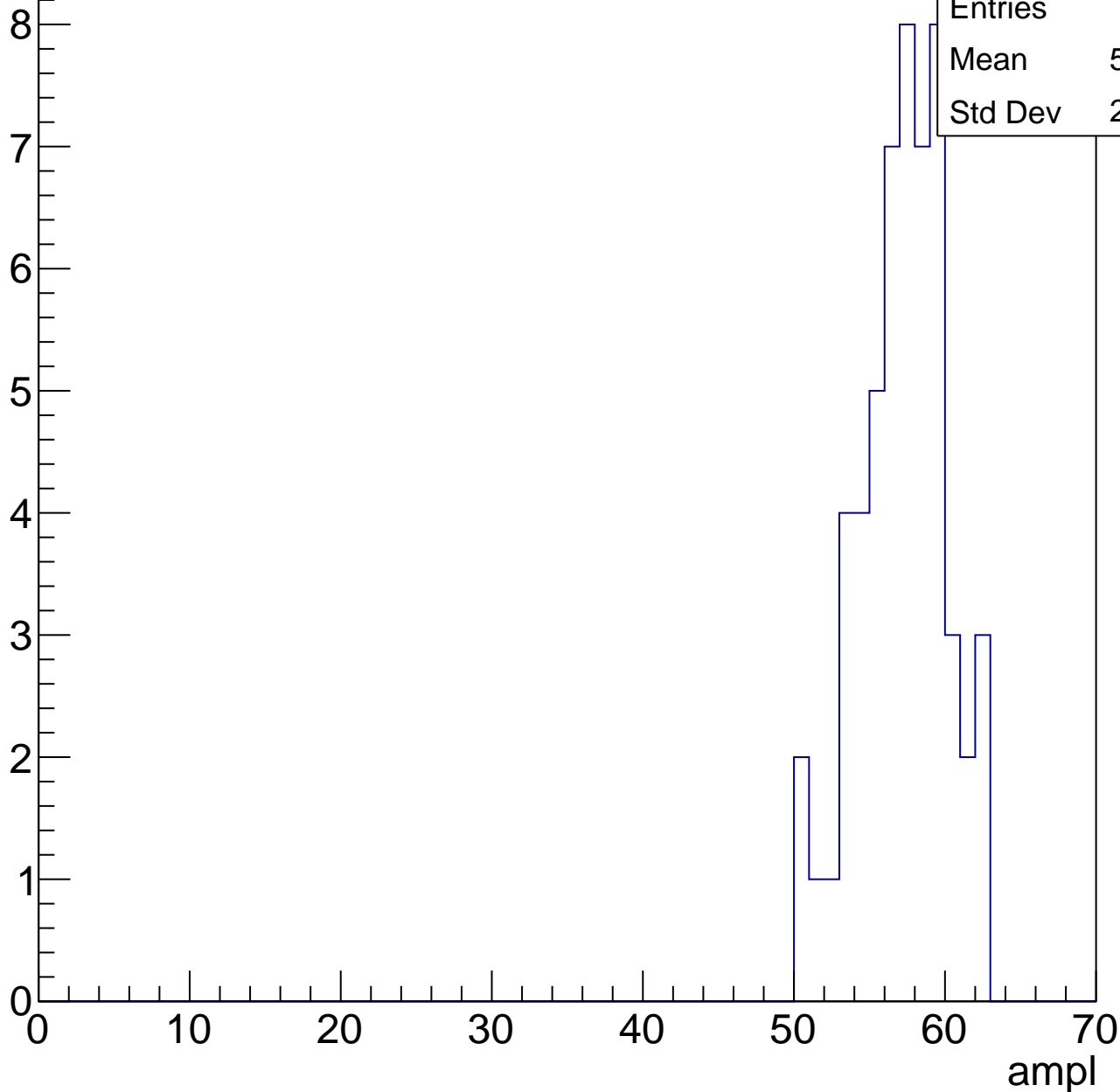
Entries	64
Mean	51.12
Std Dev	3.252



B0L002S, U9-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

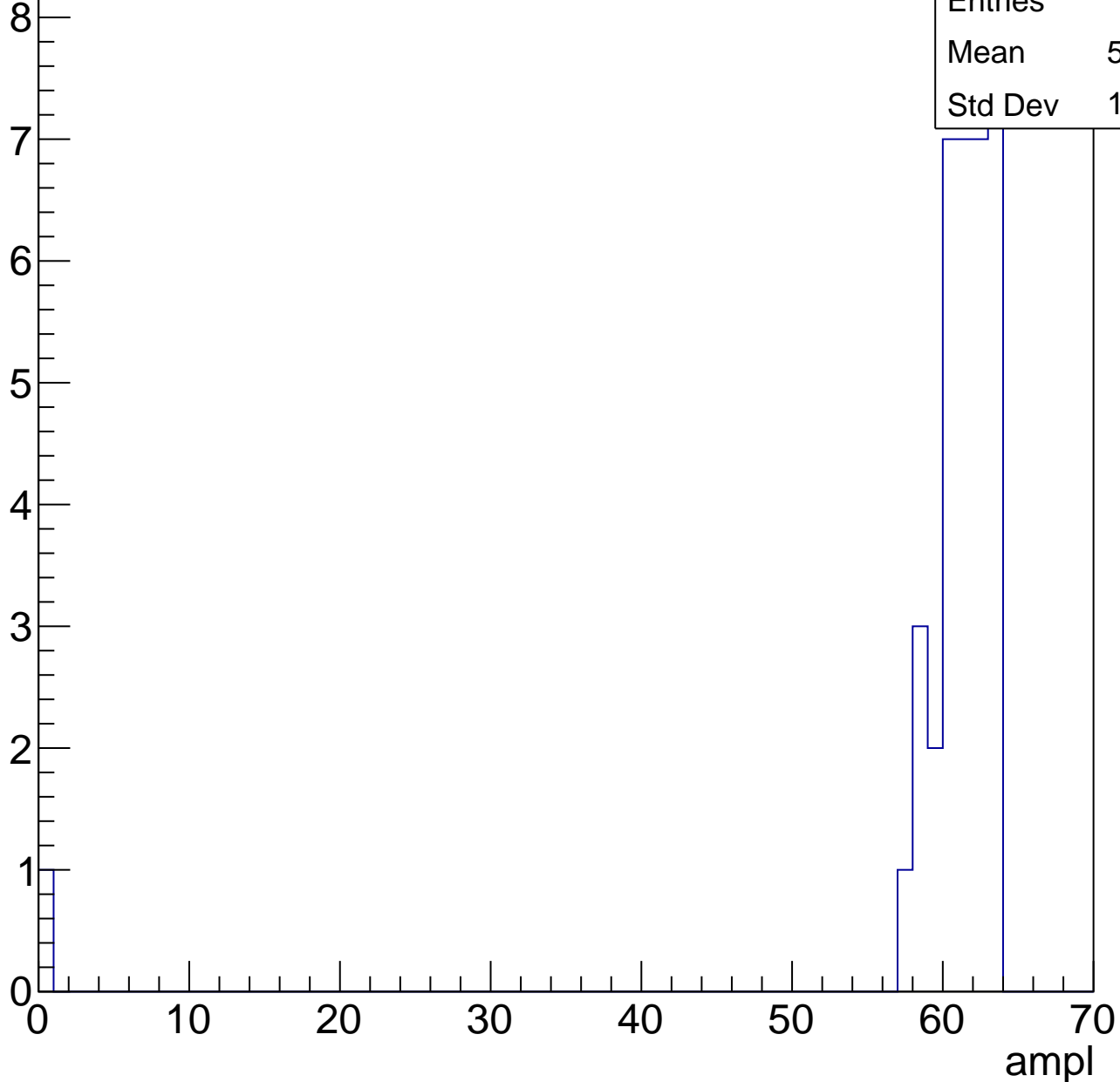
Entry



B0L002S, U9-ch6, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

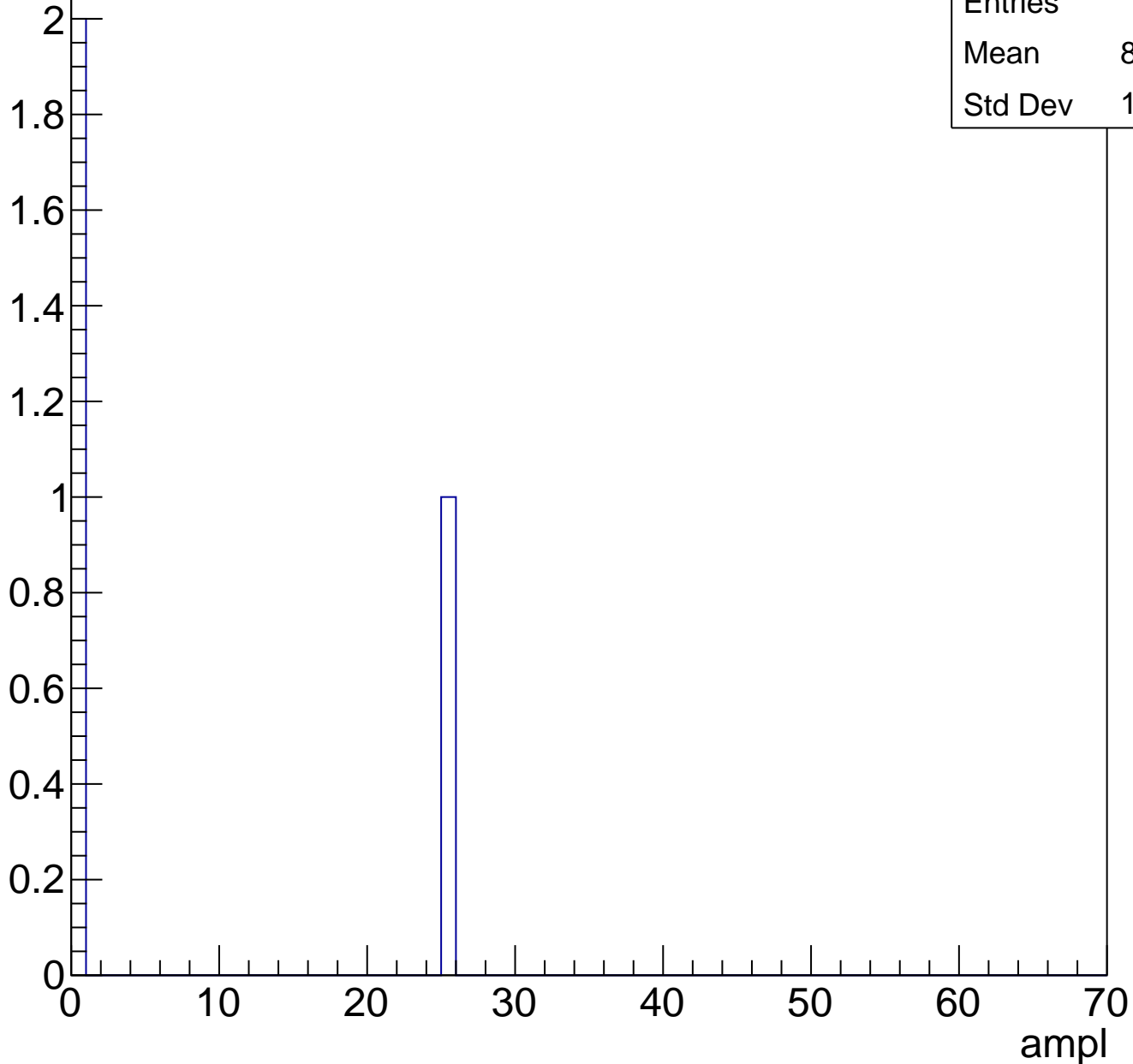
0 10 20 30 40 50 60 70

ampl

B0L002S, U9-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L002S, U9-ch7, adc0

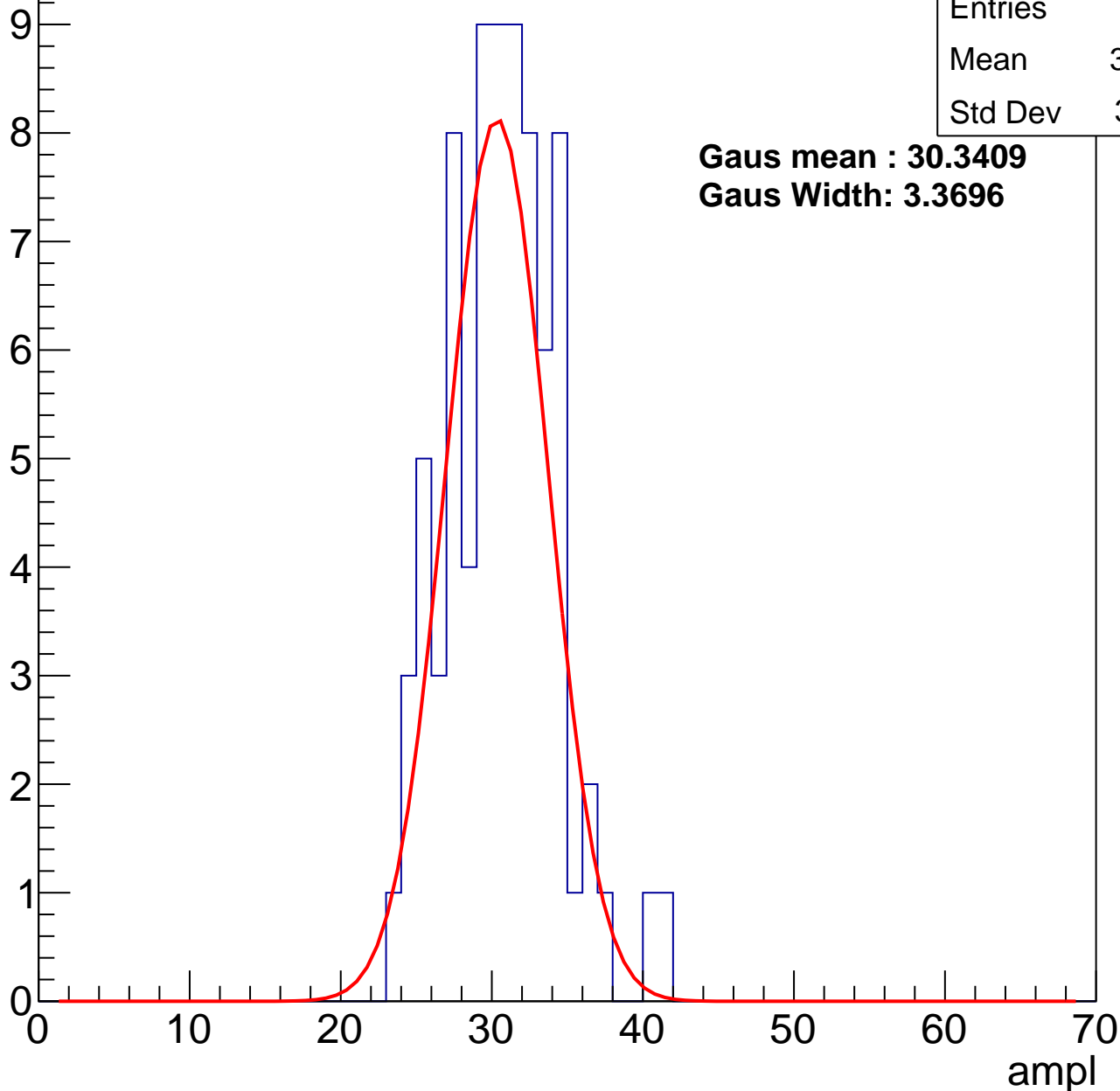
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.22
Std Dev	3.581

Gaus mean : 30.3409

Gaus Width: 3.3696



B0L002S, U9-ch7, adc1

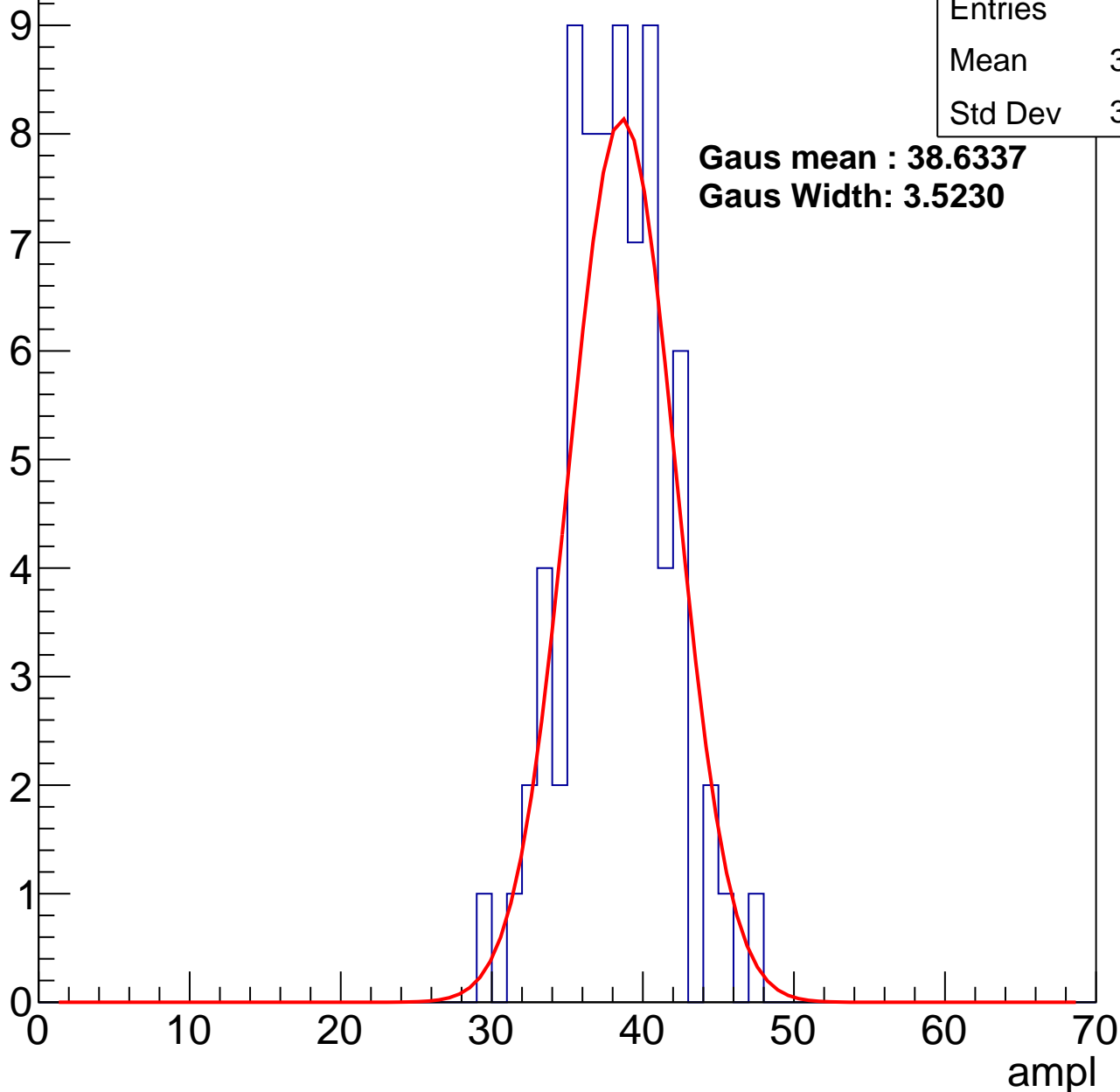
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.76
Std Dev	3.344

Gaus mean : 38.6337

Gaus Width: 3.5230



B0L002S, U9-ch7, adc2

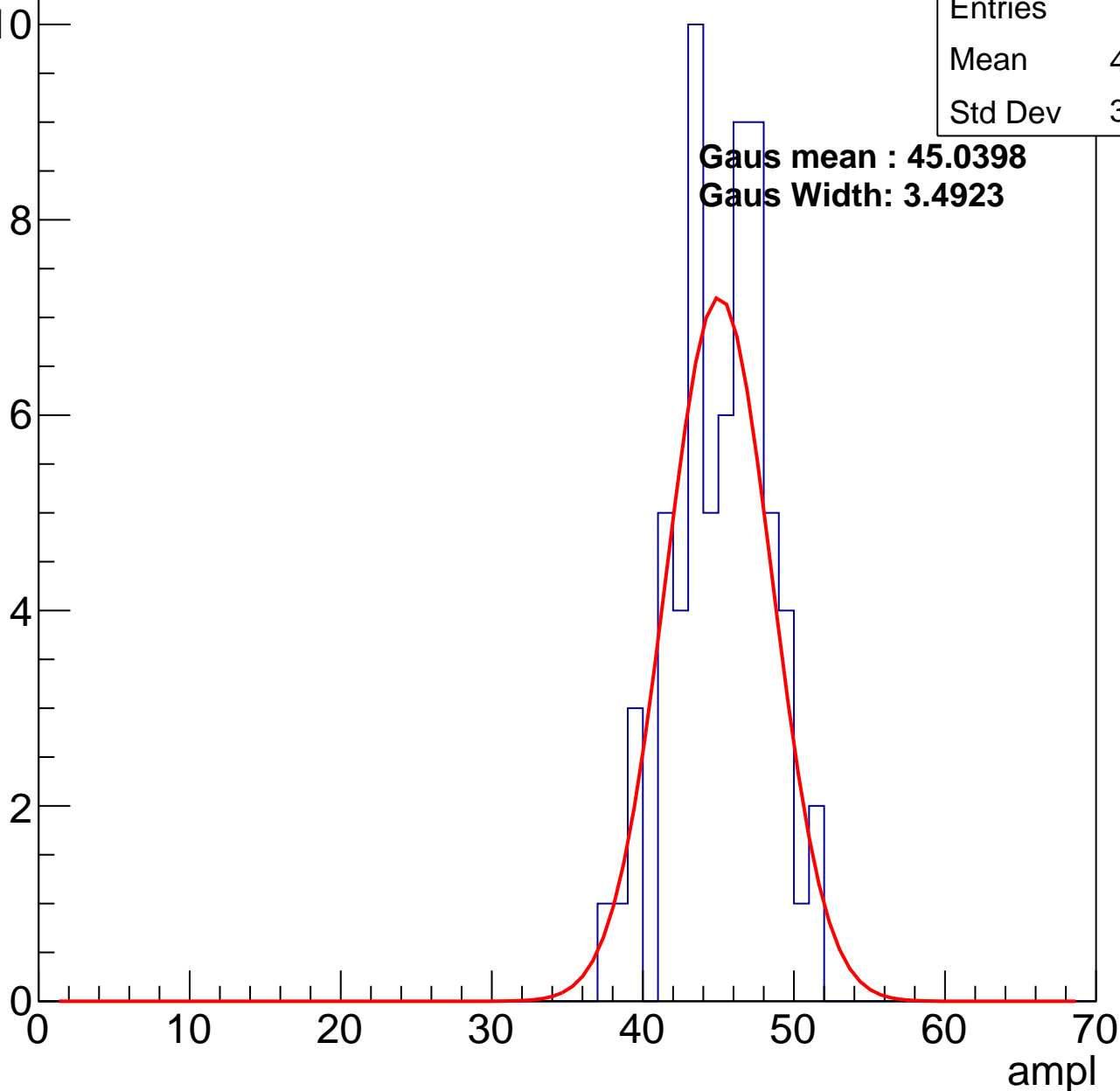
calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	44.77
Std Dev	3.112

Gaus mean : 45.0398

Gaus Width: 3.4923

Entry

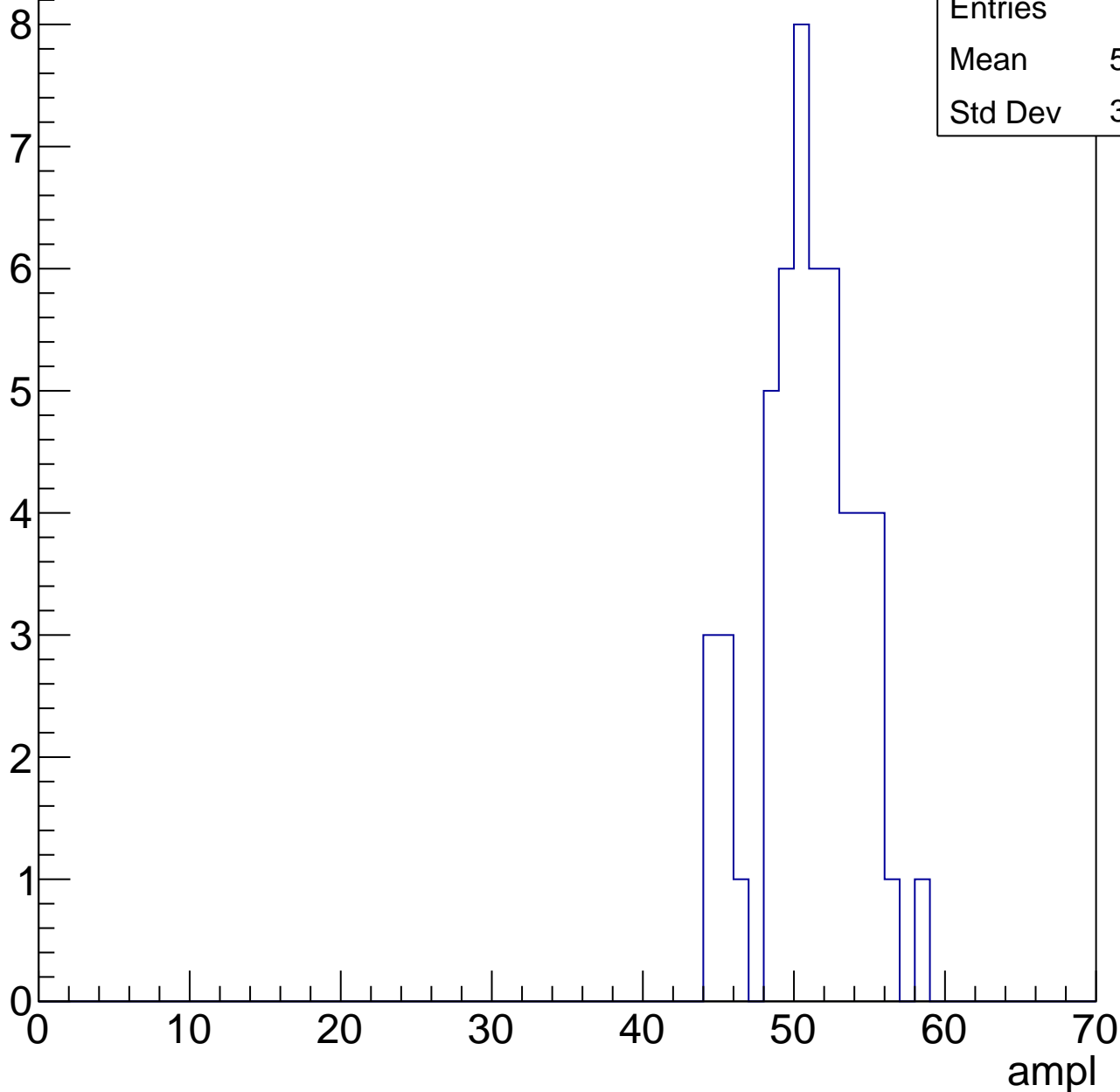


B0L002S, U9-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	50.52
Std Dev	3.225

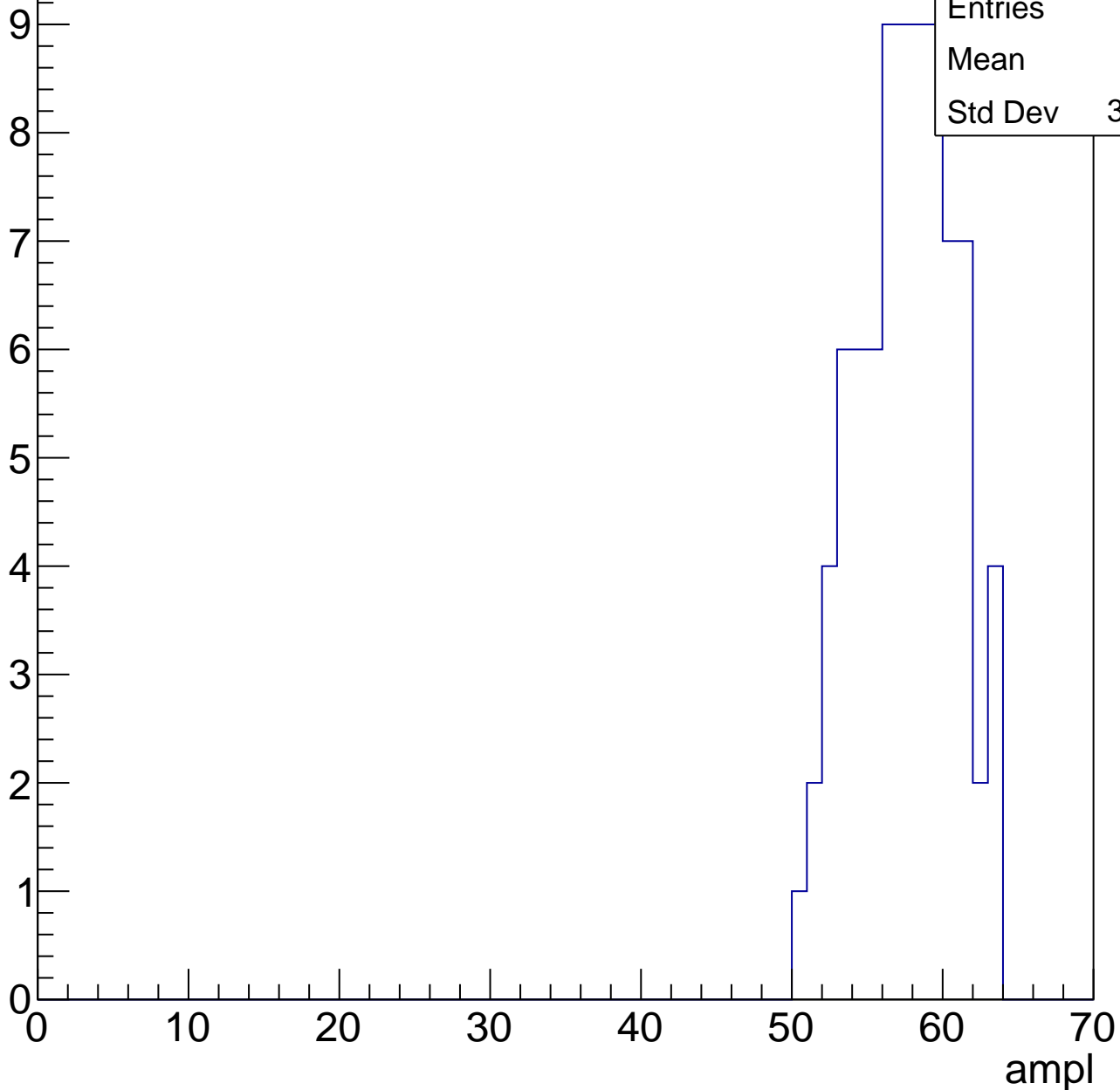


B0L002S, U9-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	57.1
Std Dev	3.176

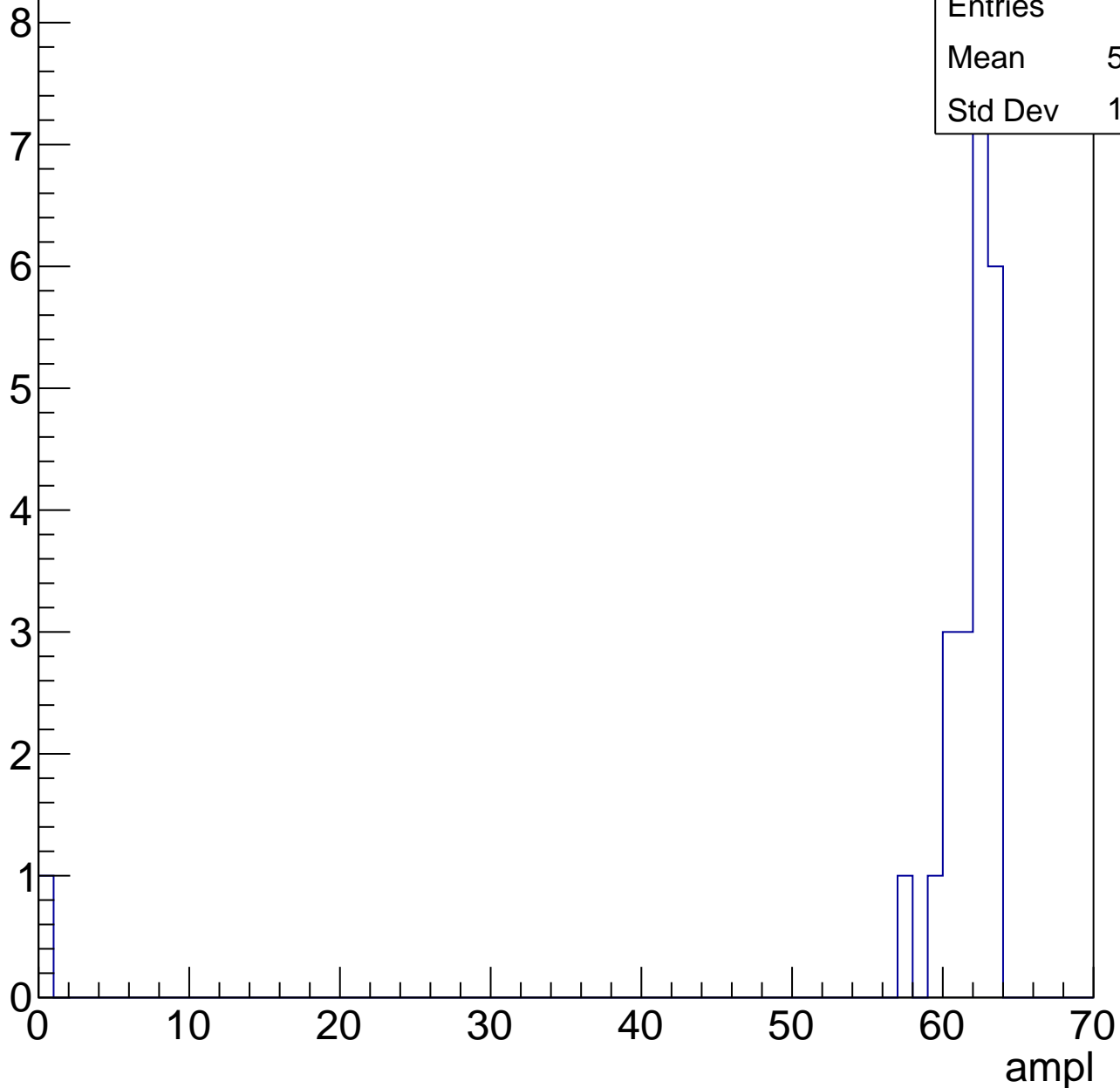


B0L002S, U9-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.83
Std Dev	12.63



B0L002S, U9-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L002S, U9-ch8, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	30.44
Std Dev	4.697

Gaus mean : 31.3893

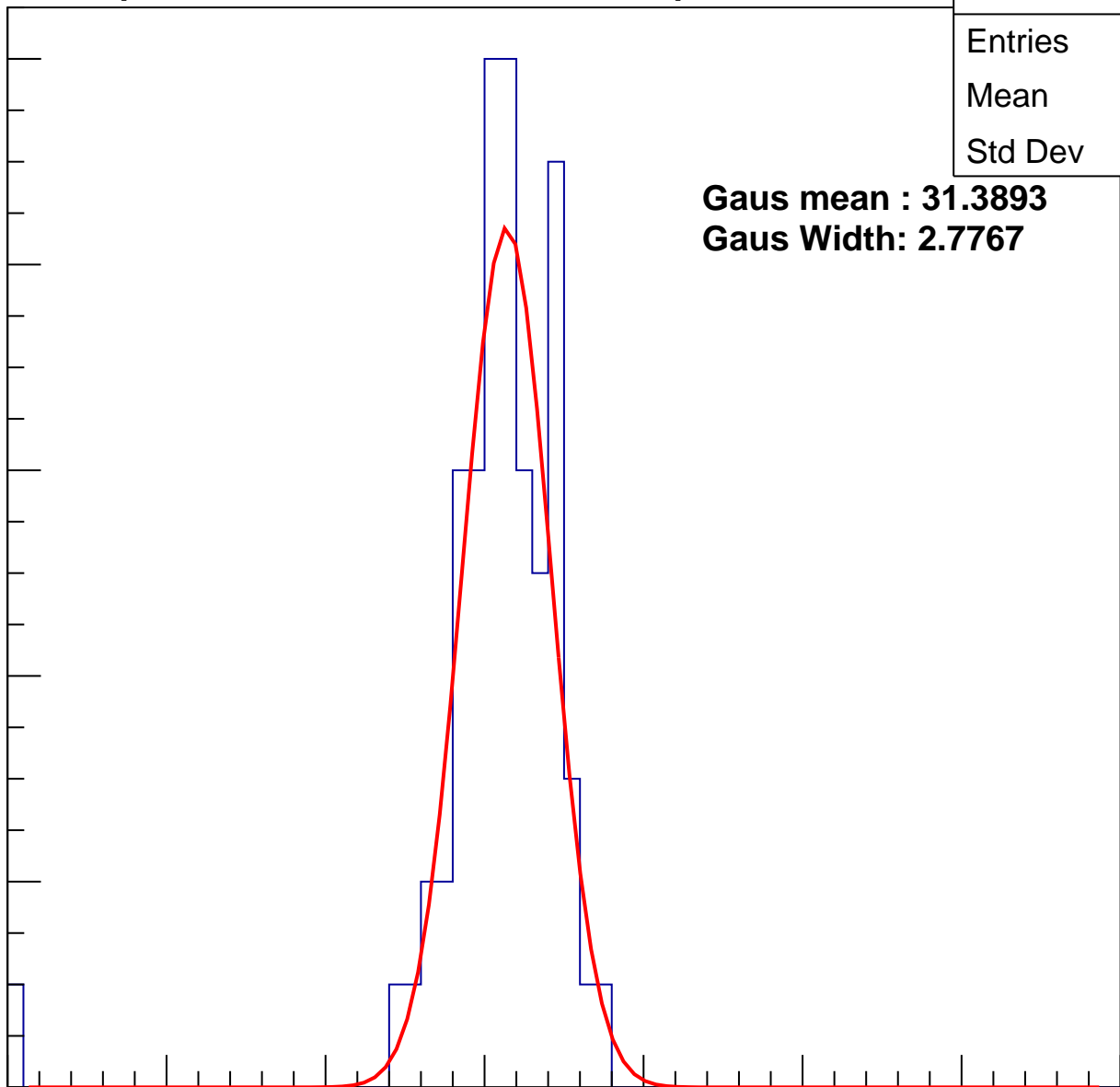
Gaus Width: 2.7767

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

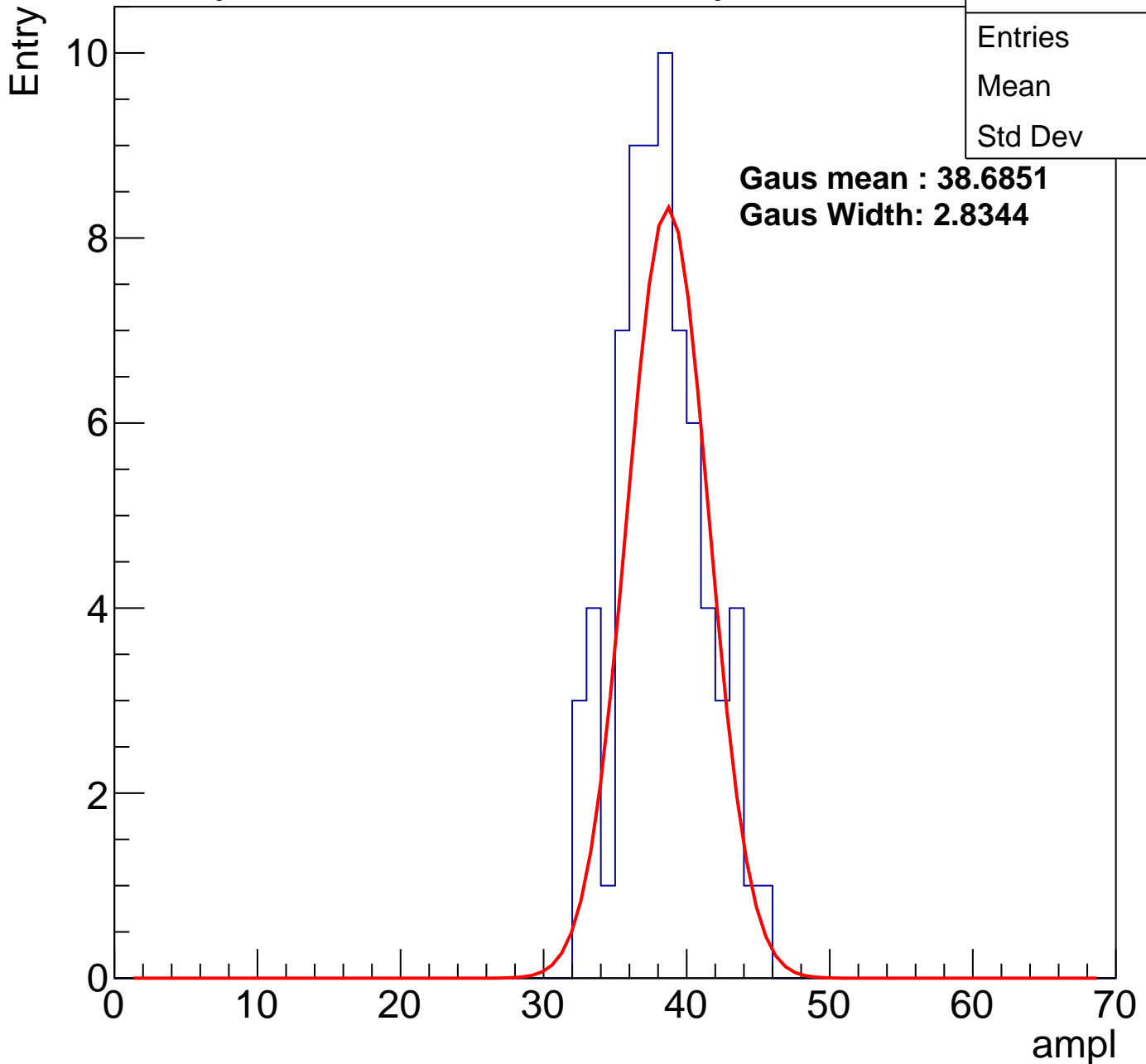


B0L002S, U9-ch8, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	37.8
Std Dev	3.02

Gaus mean : 38.6851
Gaus Width: 2.8344



B0L002S, U9-ch8, adc2

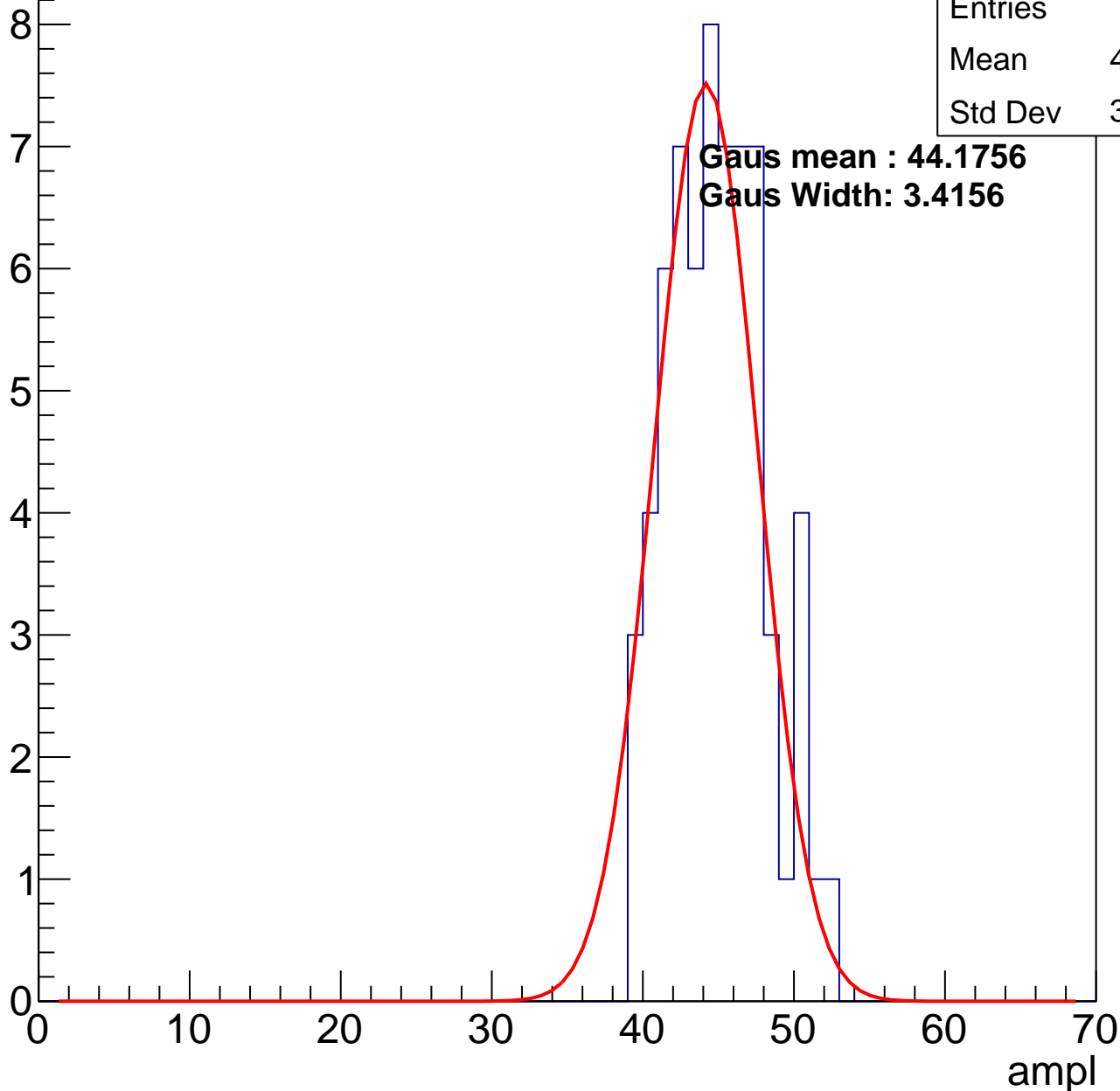
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.45
Std Dev	3.143

Gaus mean : 44.1756

Gaus Width: 3.4156

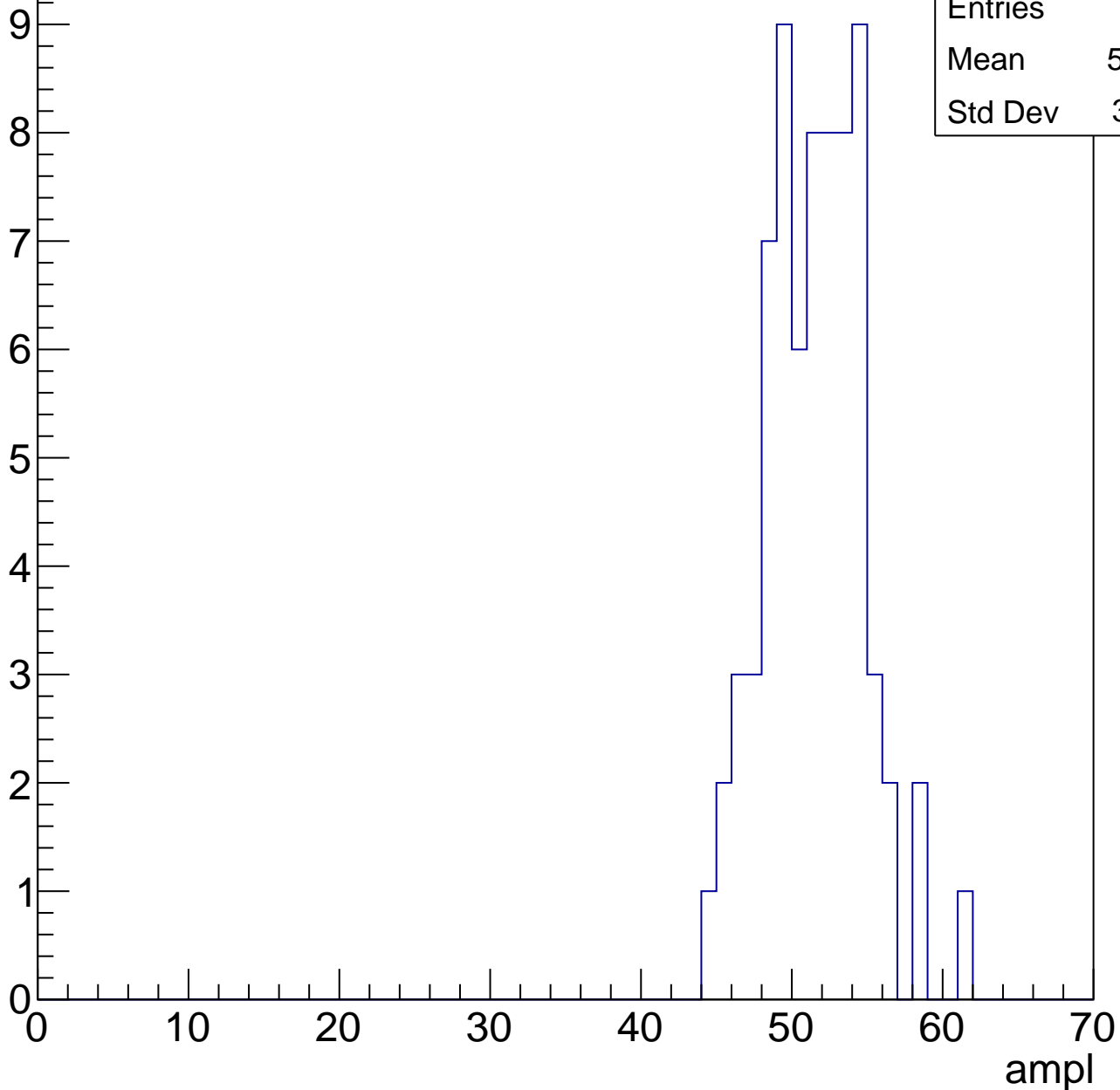


B0L002S, U9-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

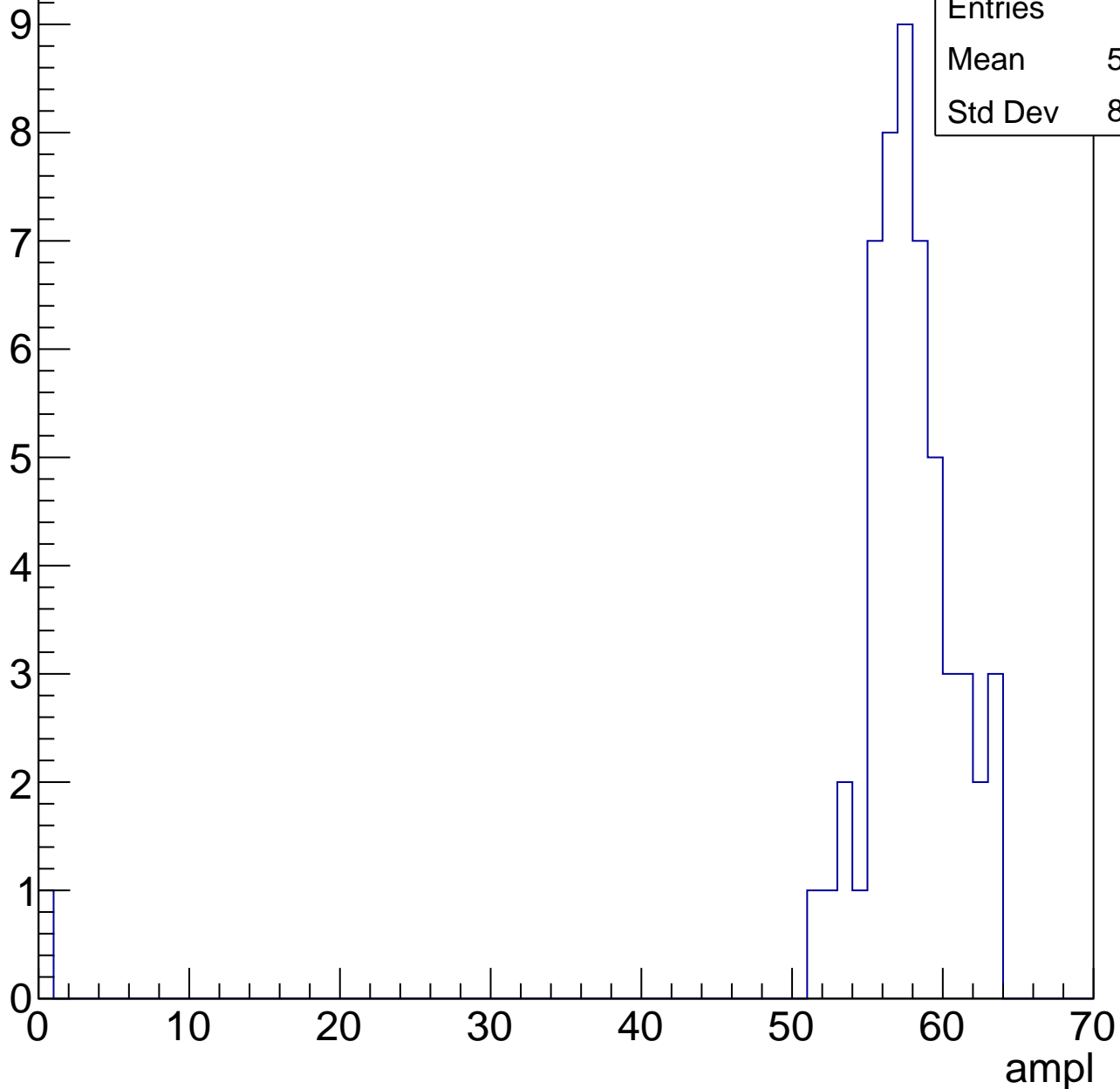
Entries	72
Mean	51.08
Std Dev	3.261



B0L002S, U9-ch8, adc4

calib_packv5_042523_0143.root, FC#8, port C1

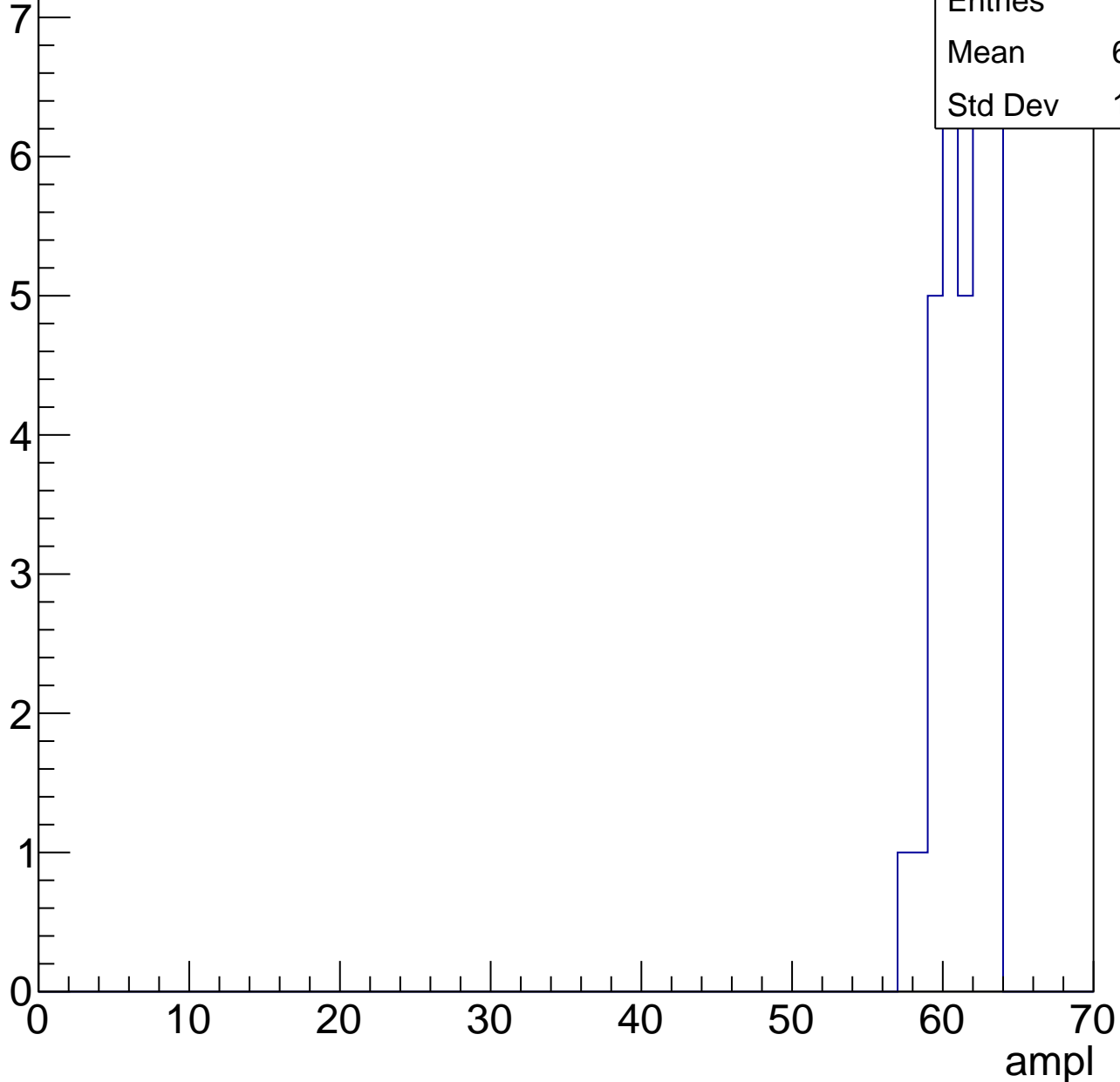
Entry



B0L002S, U9-ch8, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch9, adc0

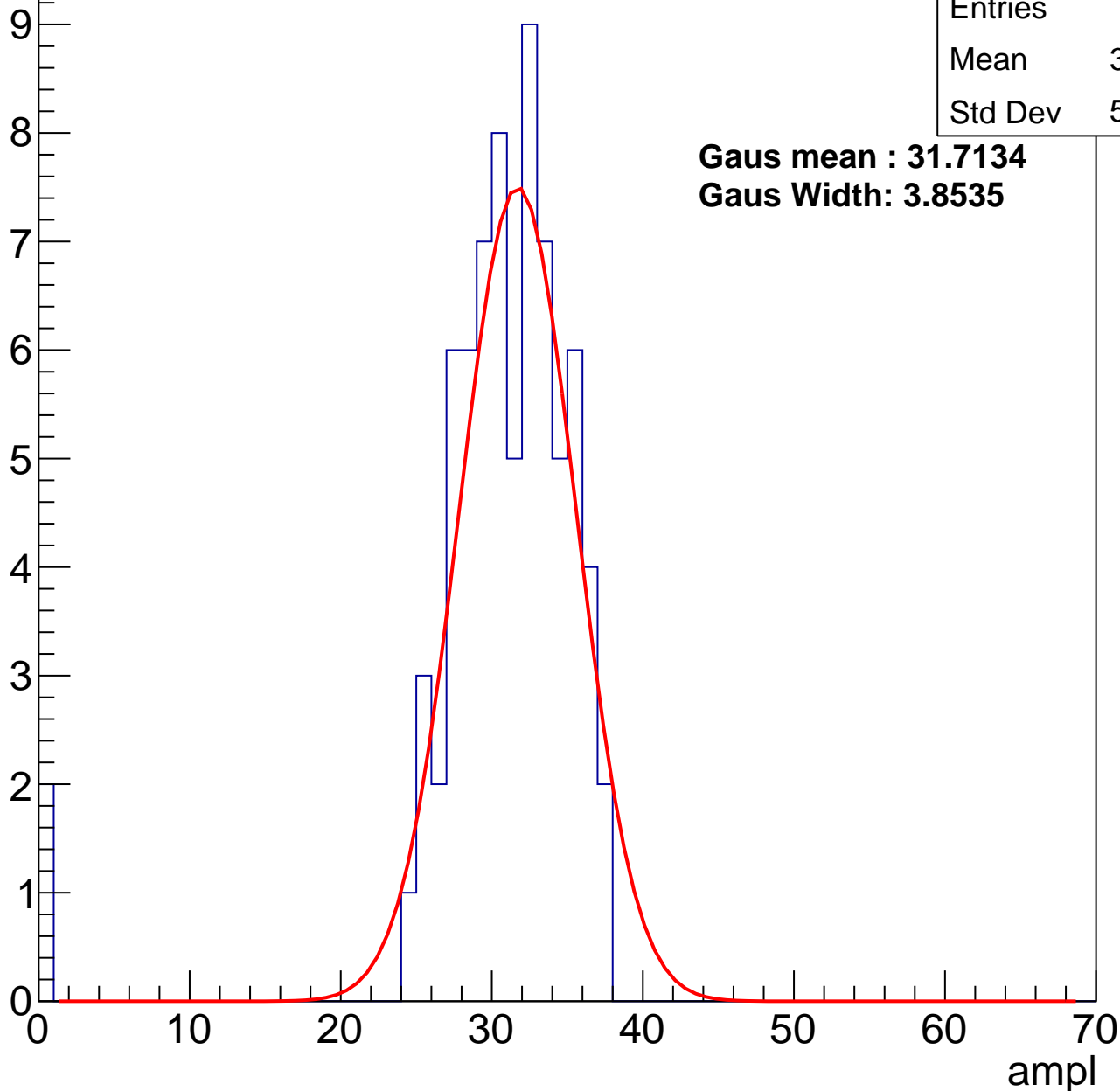
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.08
Std Dev	5.972

Gaus mean : 31.7134

Gaus Width: 3.8535



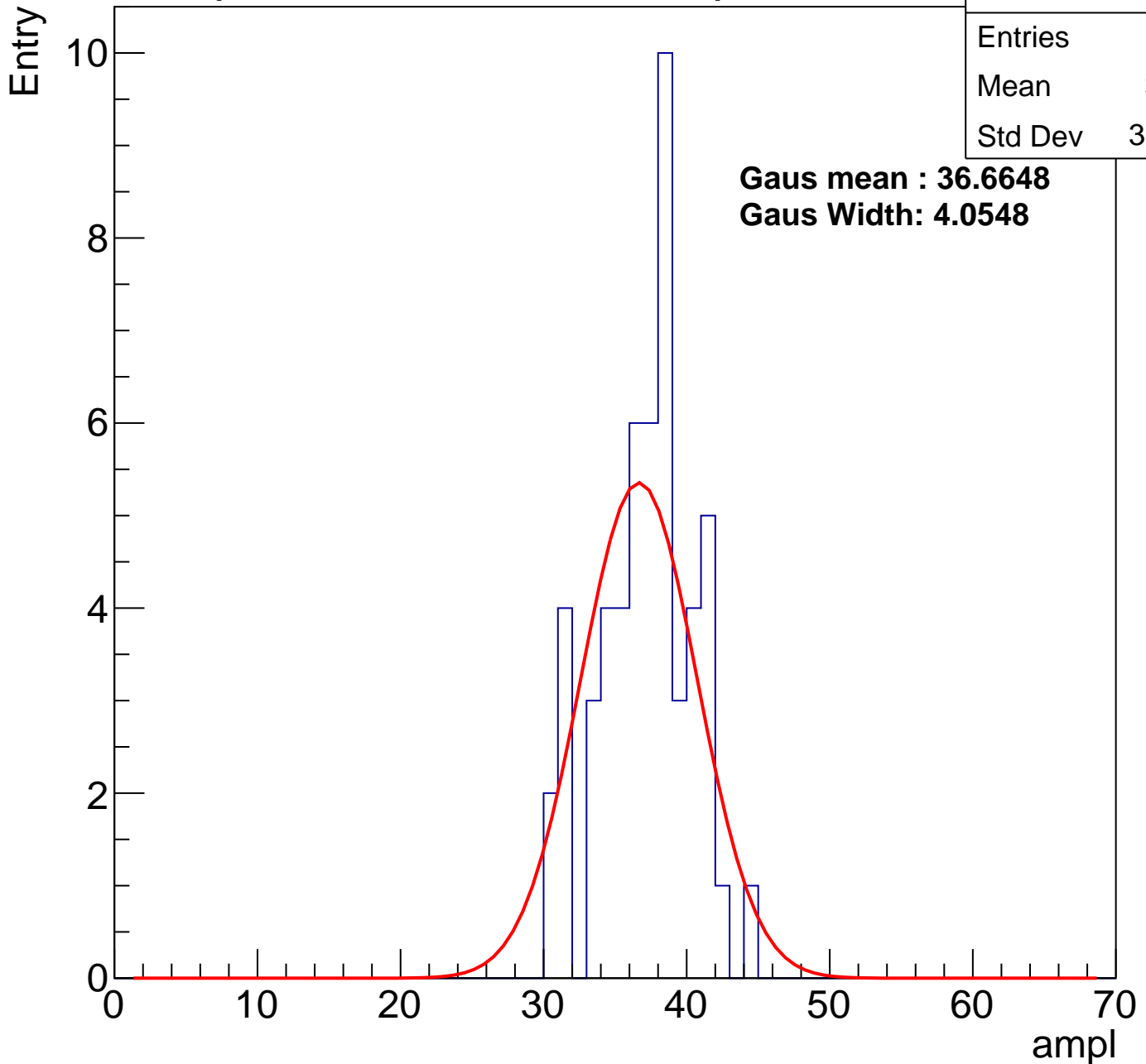
B0L002S, U9-ch9, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	53
Mean	36.7
Std Dev	3.236

Gaus mean : 36.6648

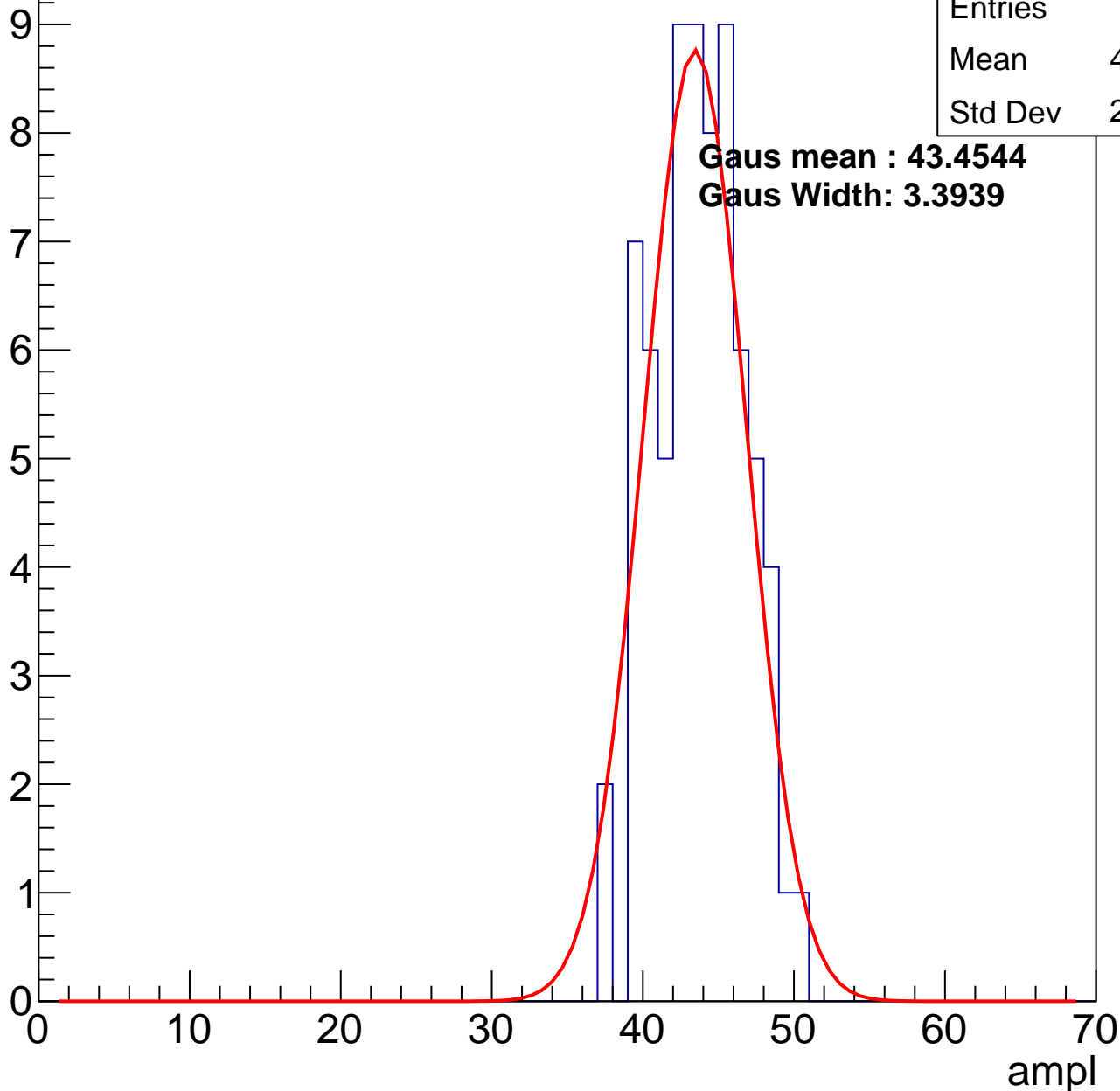
Gaus Width: 4.0548



B0L002S, U9-ch9, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

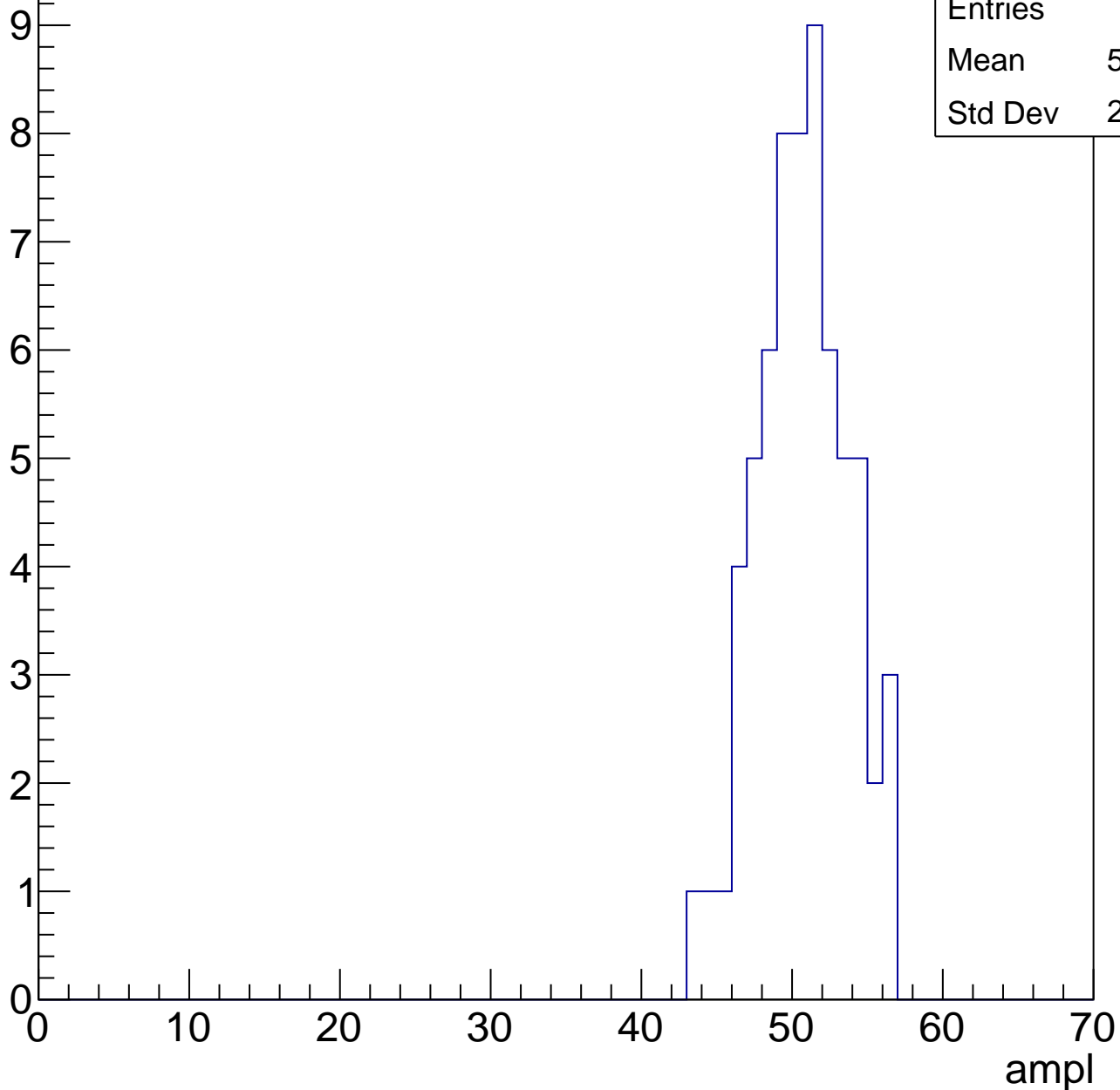


Entries	72
Mean	43.28
Std Dev	2.945

B0L002S, U9-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

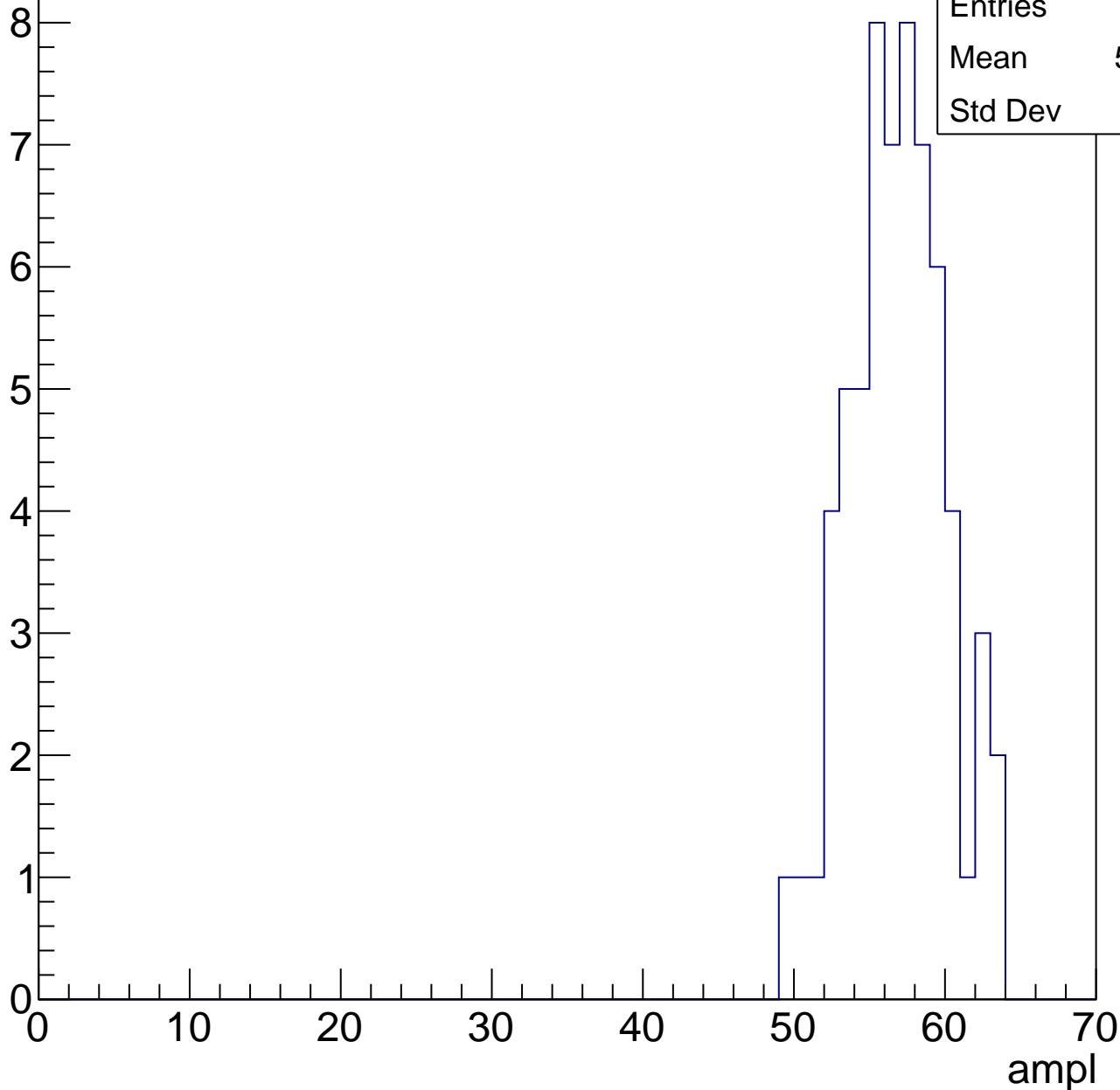


Entries	64
Mean	50.23
Std Dev	2.967

B0L002S, U9-ch9, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

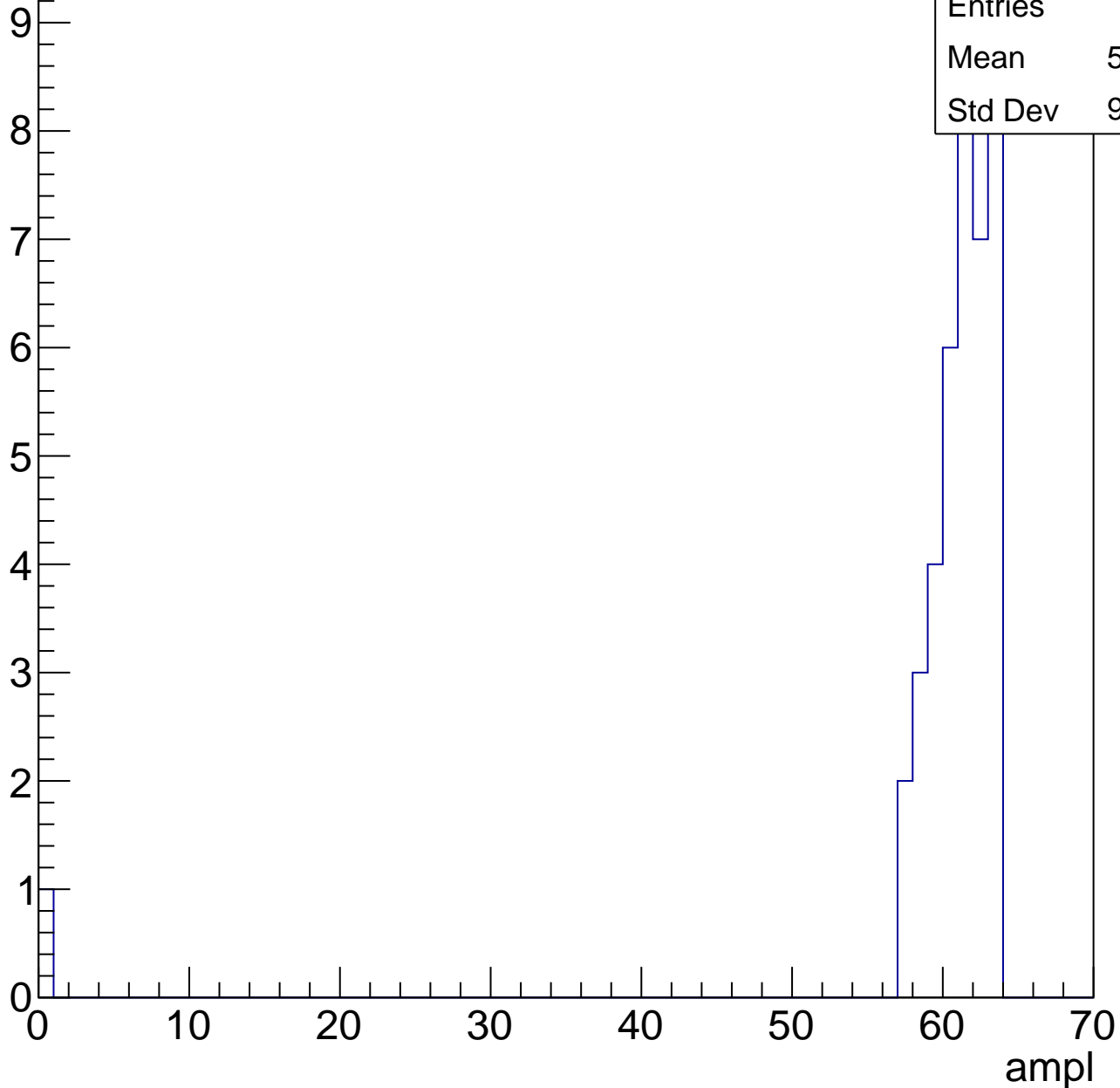


Entries	63
Mean	56.41
Std Dev	3.14

B0L002S, U9-ch9, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch10, adc0

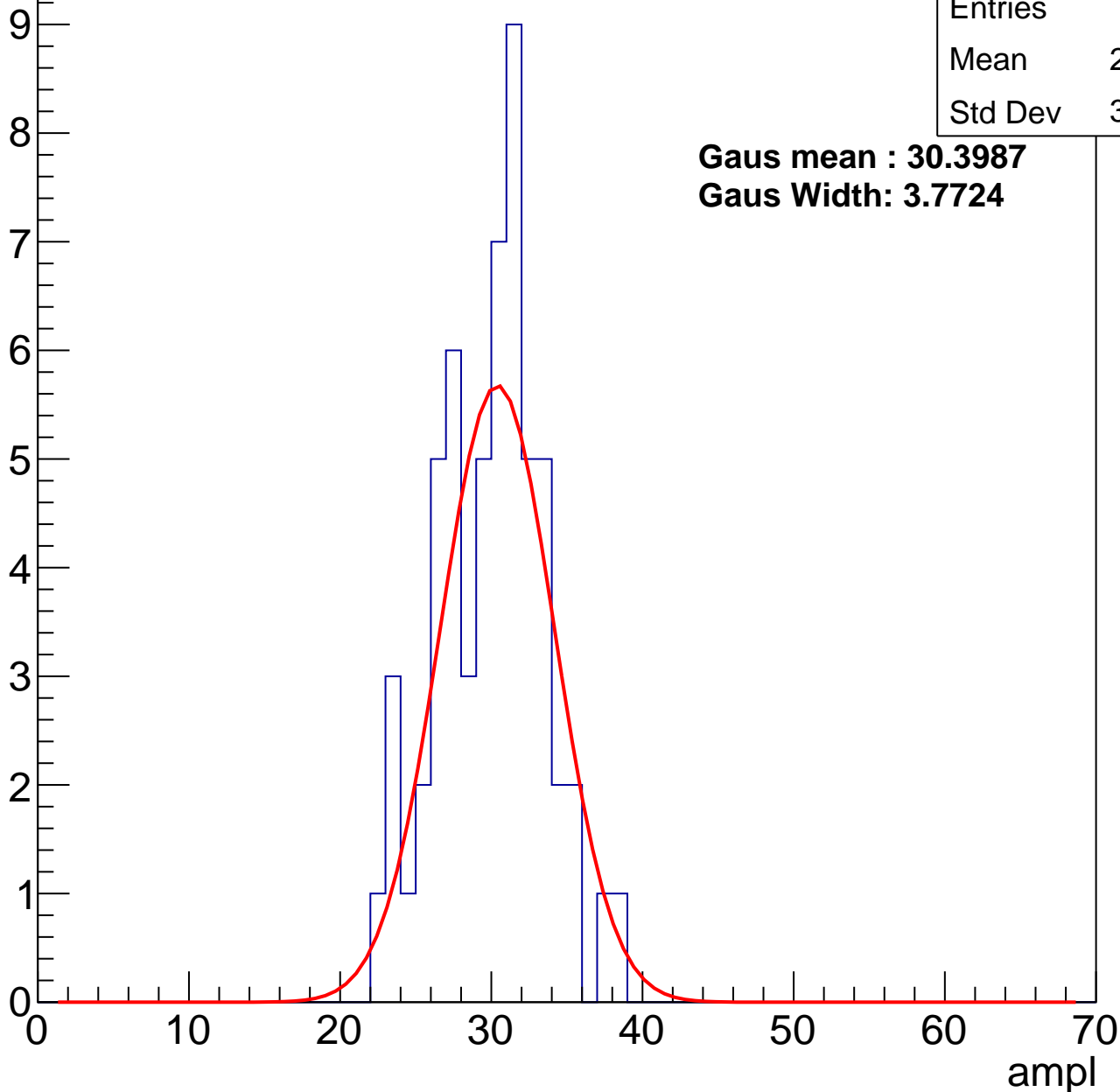
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.53
Std Dev	3.485

Gaus mean : 30.3987

Gaus Width: 3.7724



B0L002S, U9-ch10, adc1

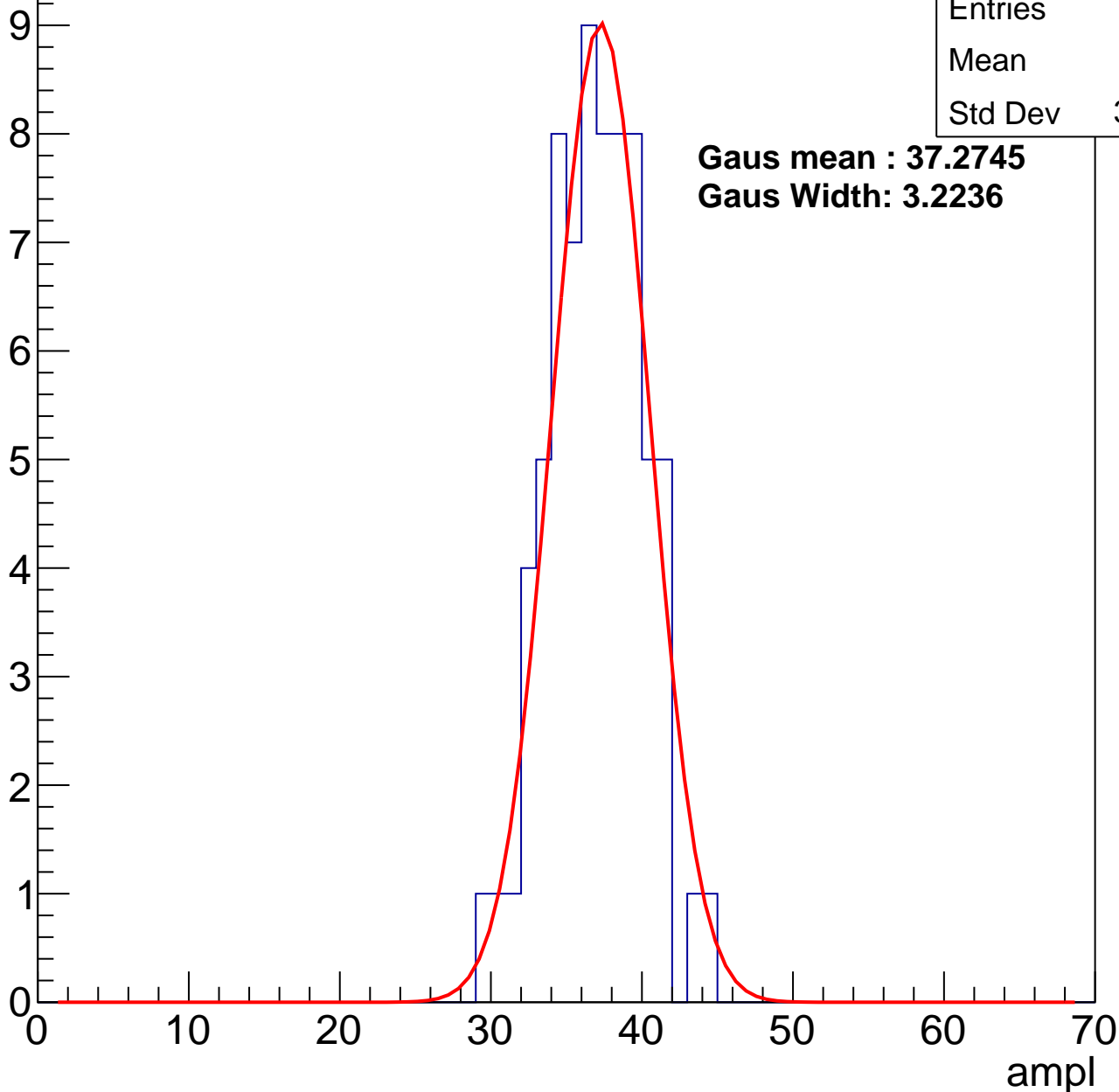
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.5
Std Dev	3.051

Gaus mean : 37.2745

Gaus Width: 3.2236



B0L002S, U9-ch10, adc2

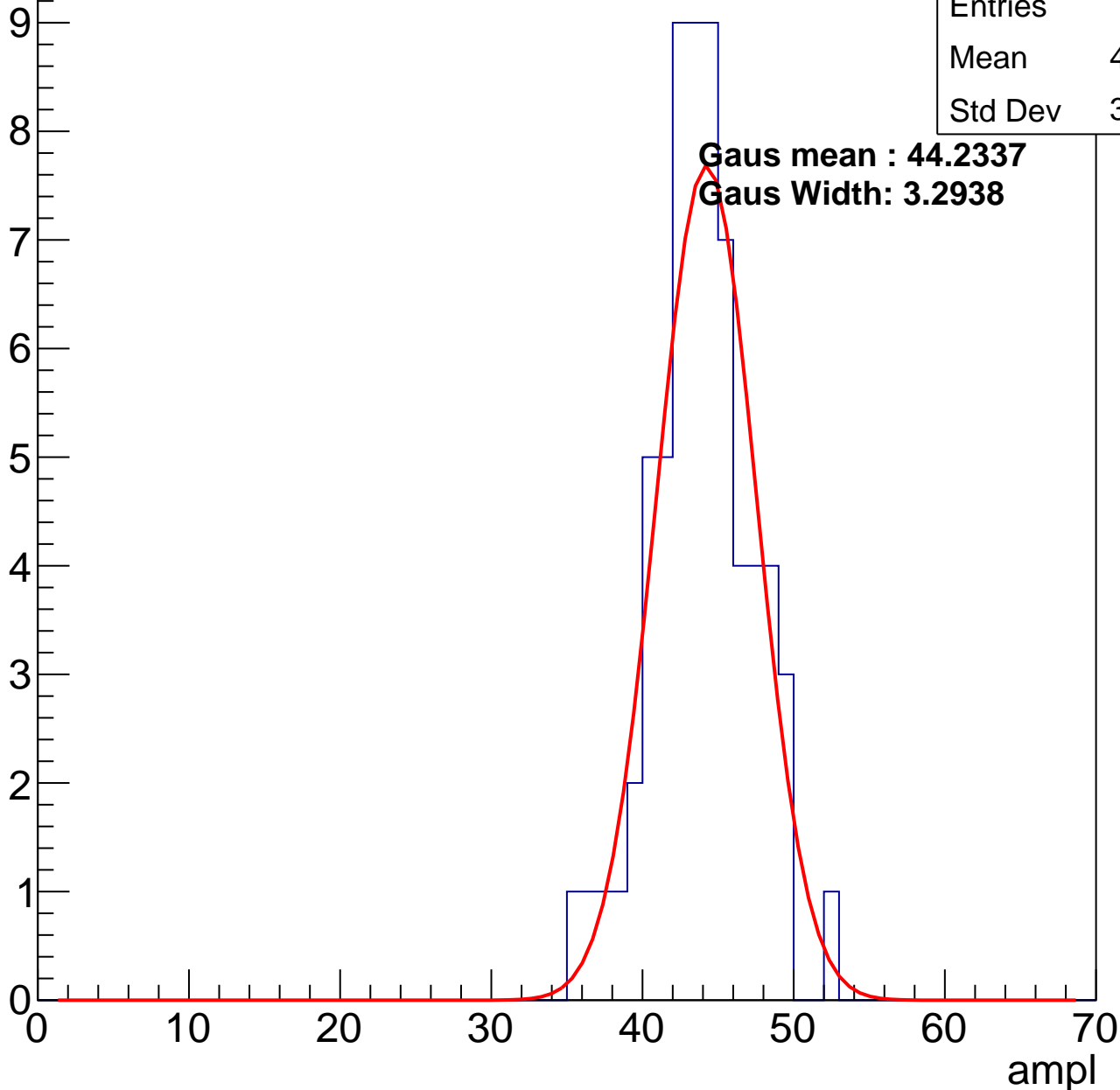
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.45
Std Dev	3.239

Gaus mean : 44.2337

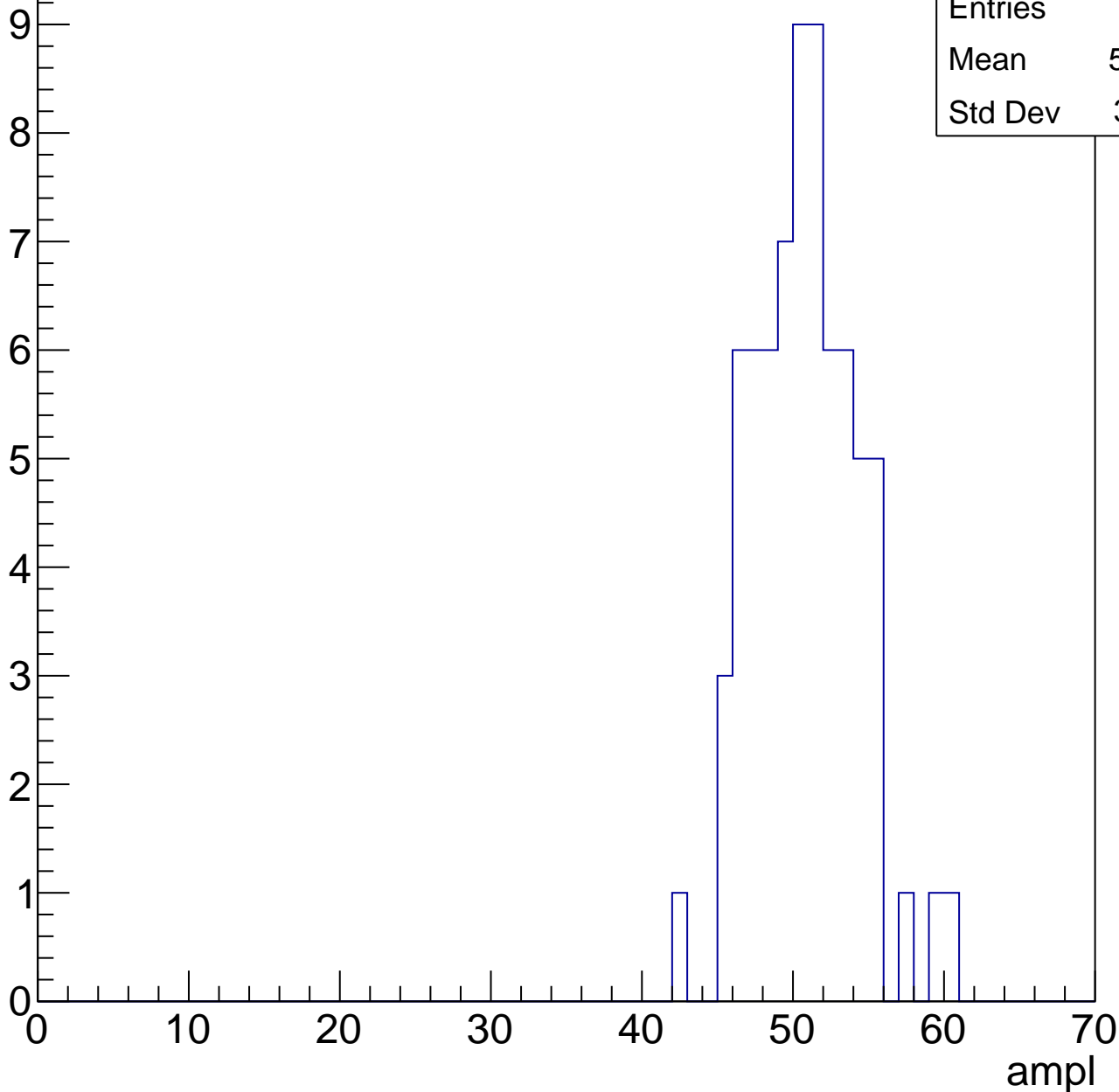
Gaus Width: 3.2938



B0L002S, U9-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	72
Mean	50.36
Std Dev	3.401

B0L002S, U9-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

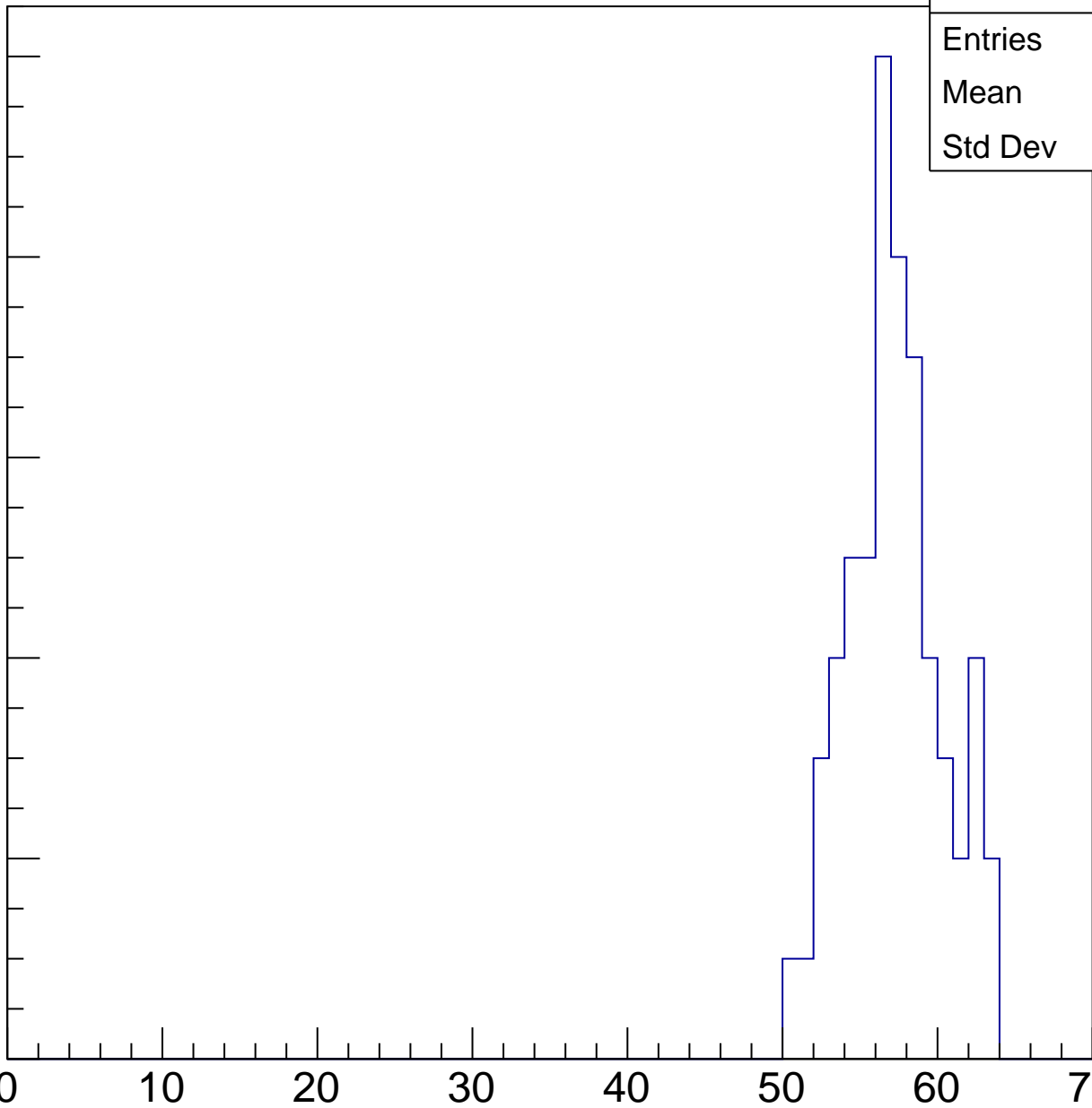
Entries	59
Mean	56.75
Std Dev	3.051

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

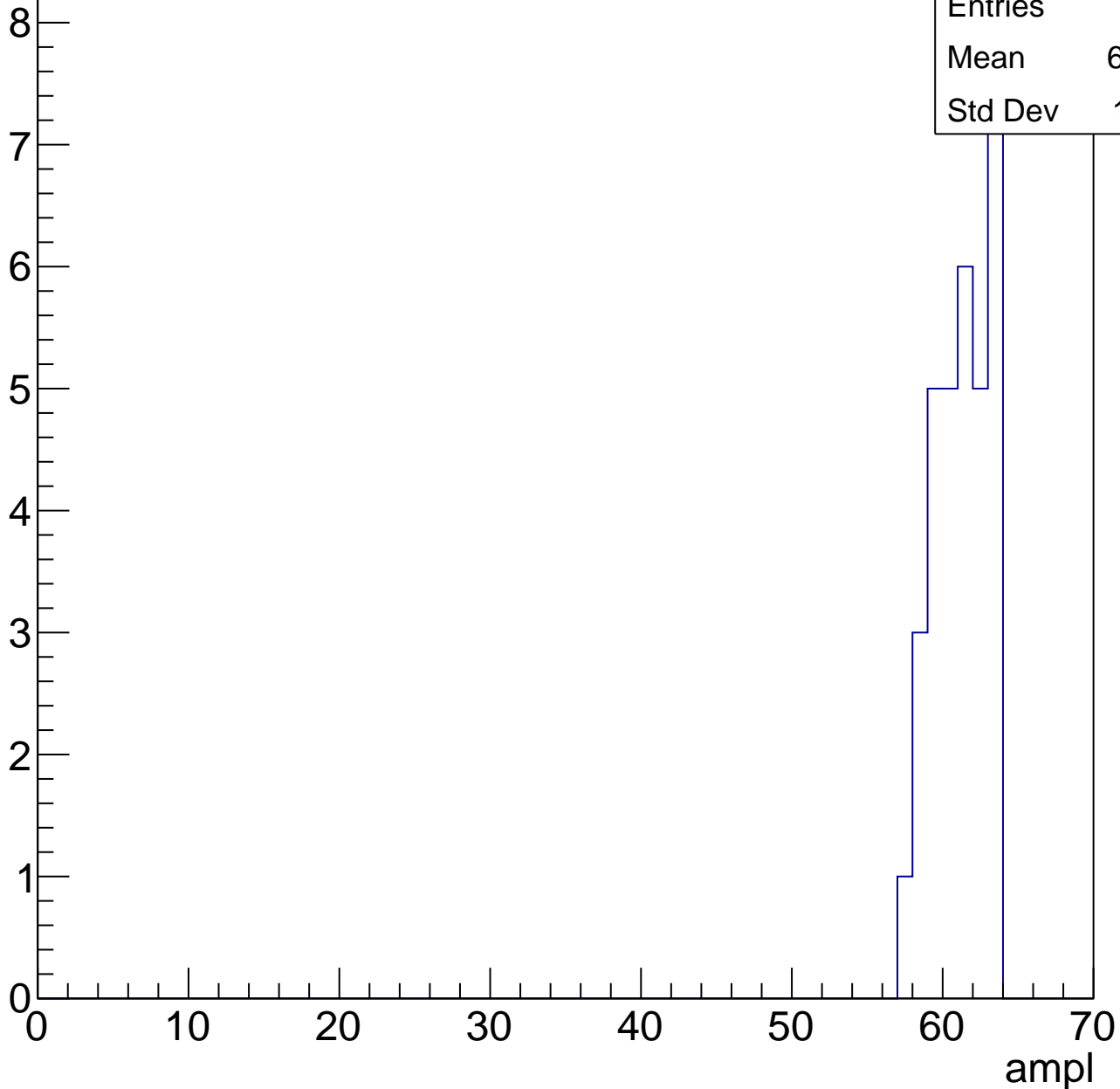


B0L002S, U9-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

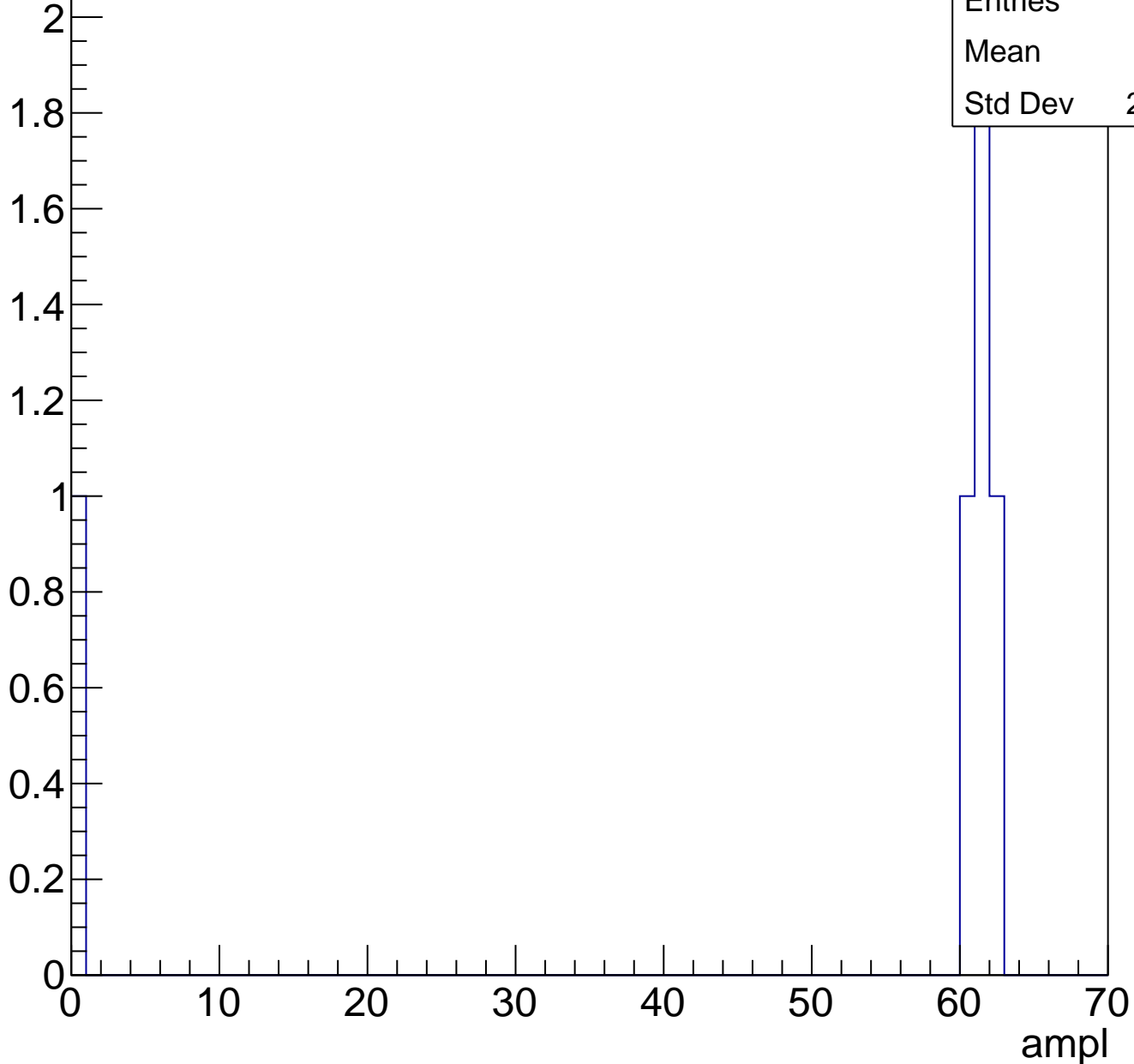
Entries	33
Mean	60.79
Std Dev	1.771



B0L002S, U9-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch11, adc0

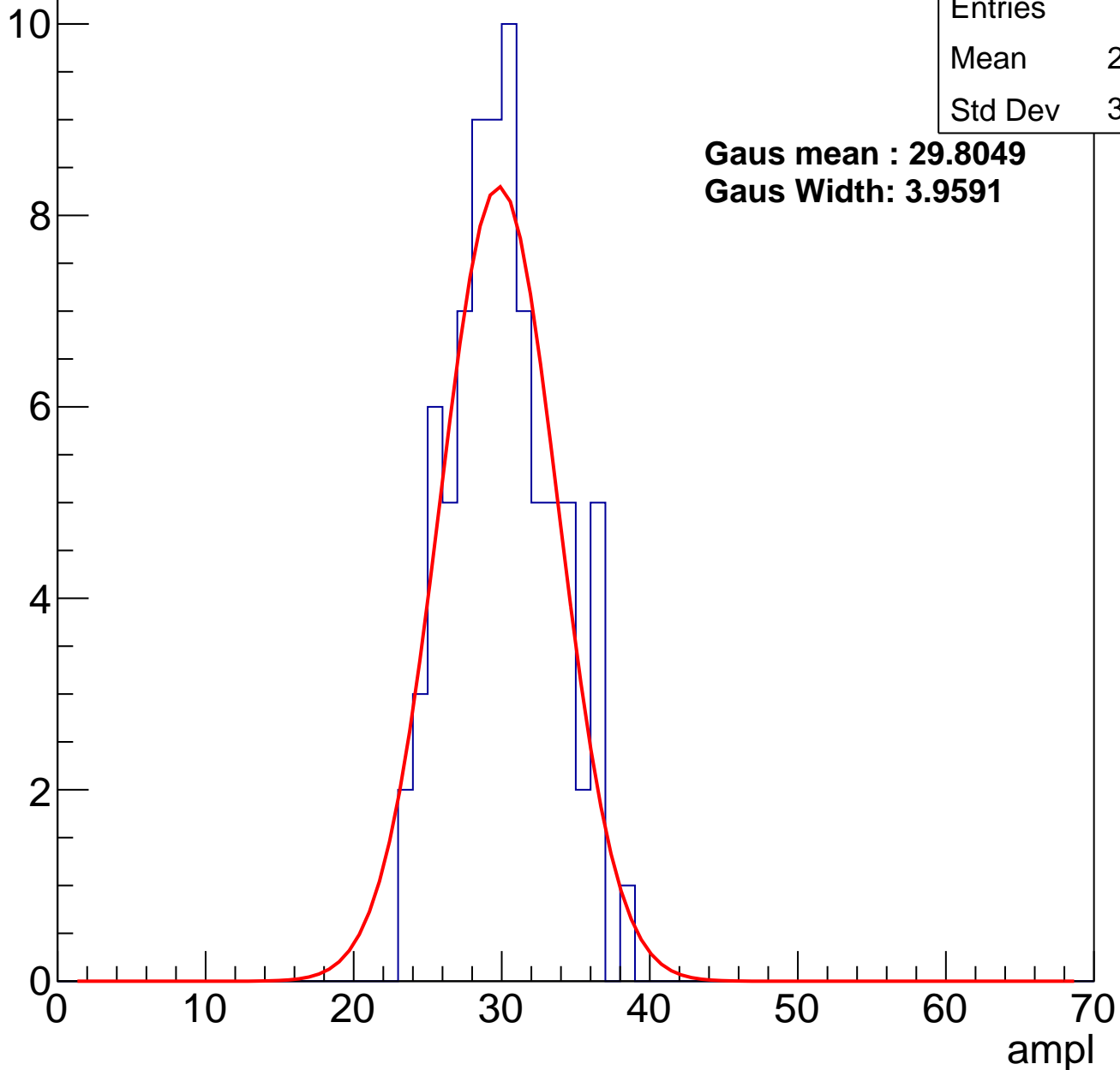
calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	29.63
Std Dev	3.476

Gaus mean : 29.8049

Gaus Width: 3.9591

Entry



B0L002S, U9-ch11, adc1

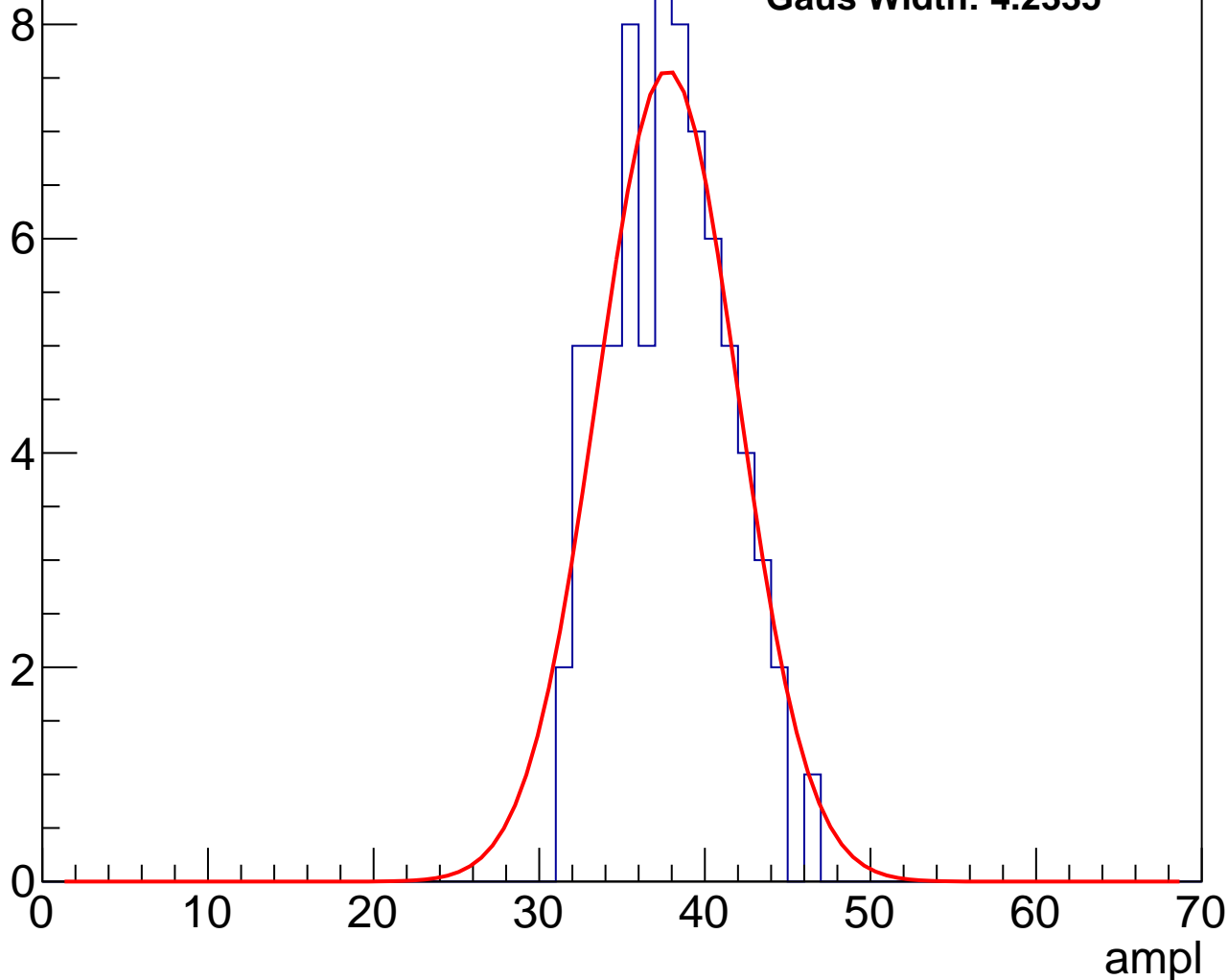
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.37
Std Dev	3.452

Gaus mean : 37.7477

Gaus Width: 4.2335



B0L002S, U9-ch11, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	44.56
Std Dev	3.127

Gaus mean : 44.8999

Gaus Width: 3.2843

10

8

6

4

2

0

0

10

20

30

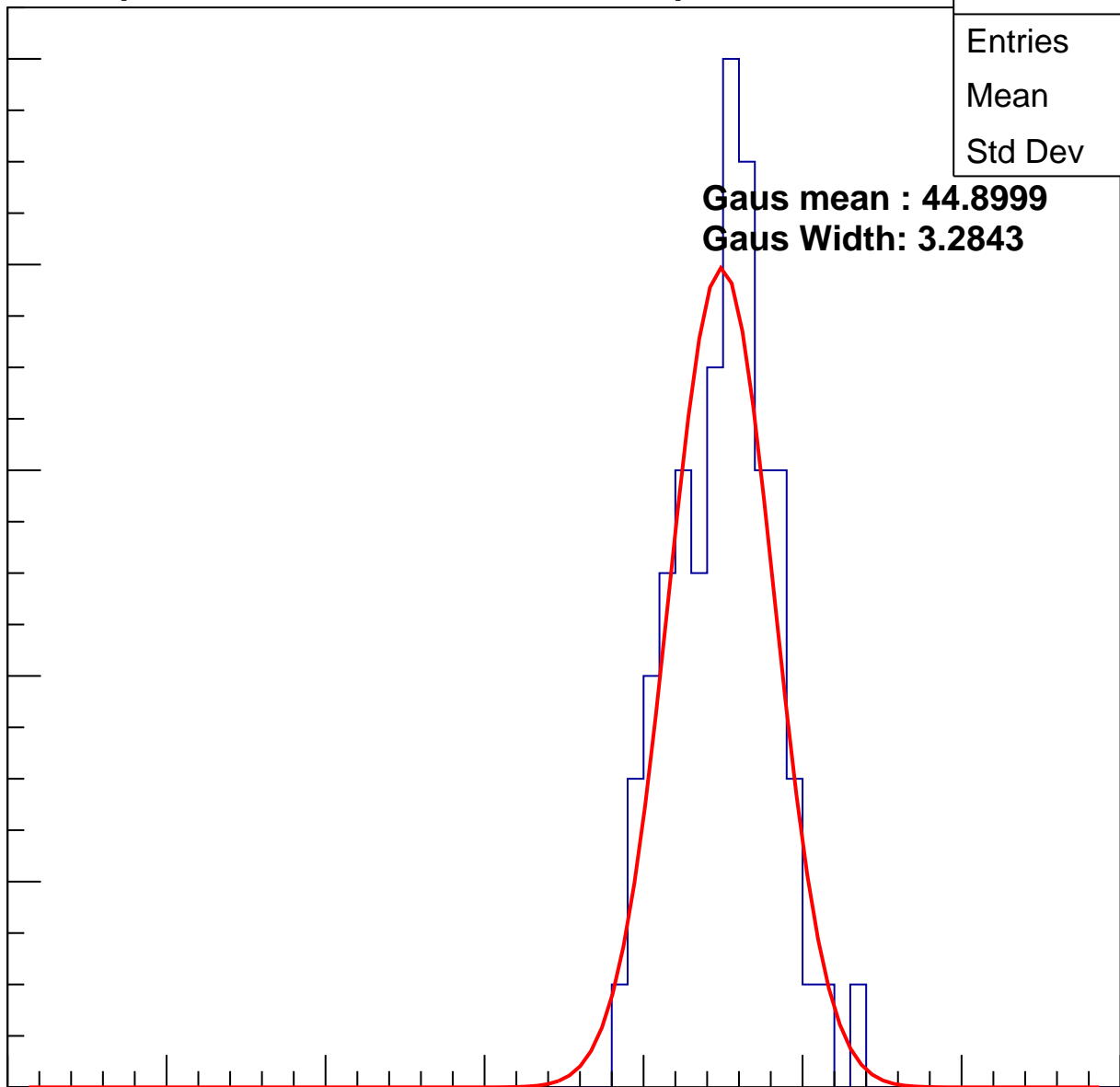
40

50

60

70

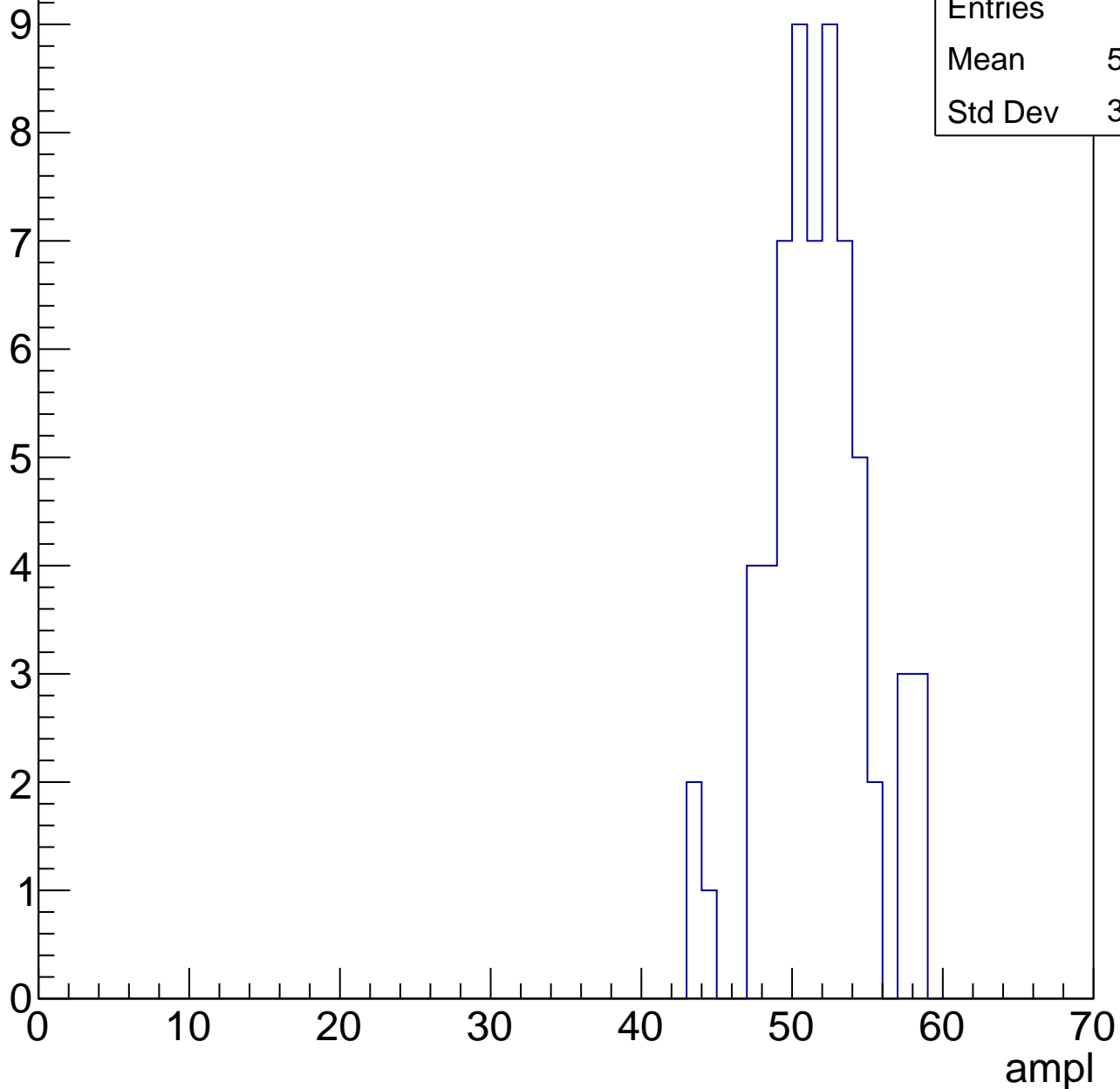
ampl



B0L002S, U9-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

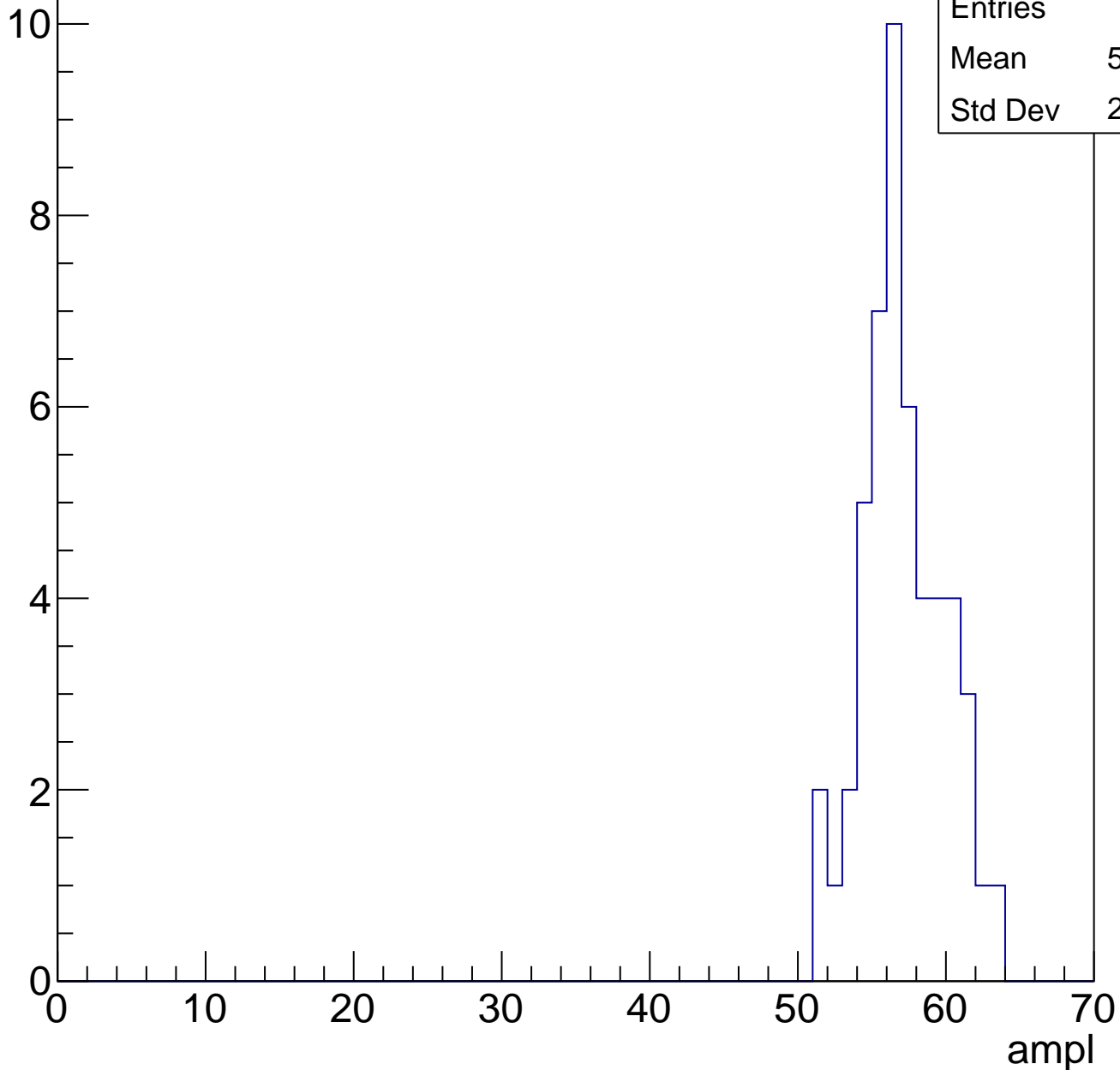


B0L002S, U9-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	50
Mean	56.66
Std Dev	2.725

Entry

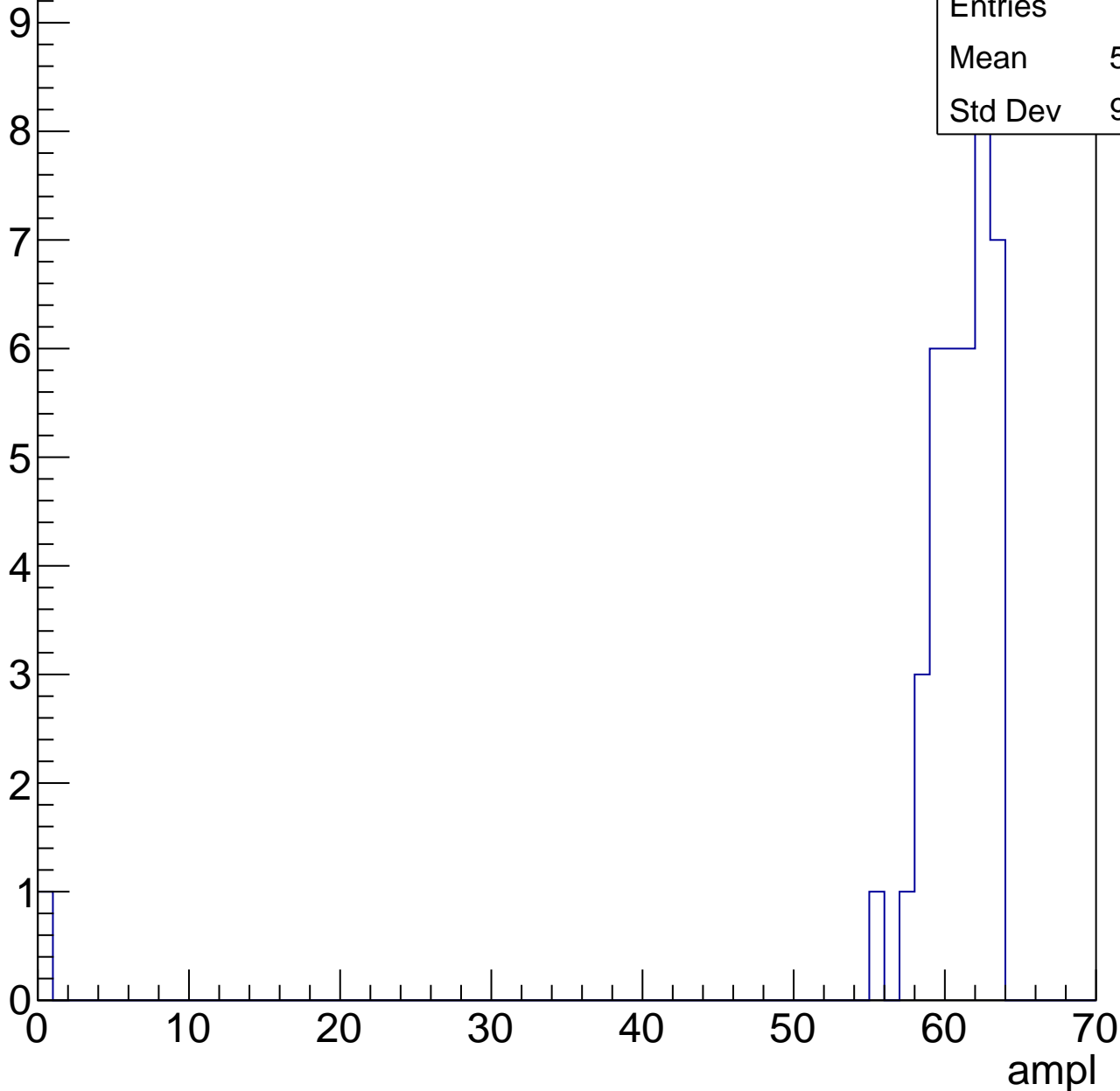


B0L002S, U9-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.12
Std Dev	9.652



B0L002S, U9-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U9-ch12, adc0

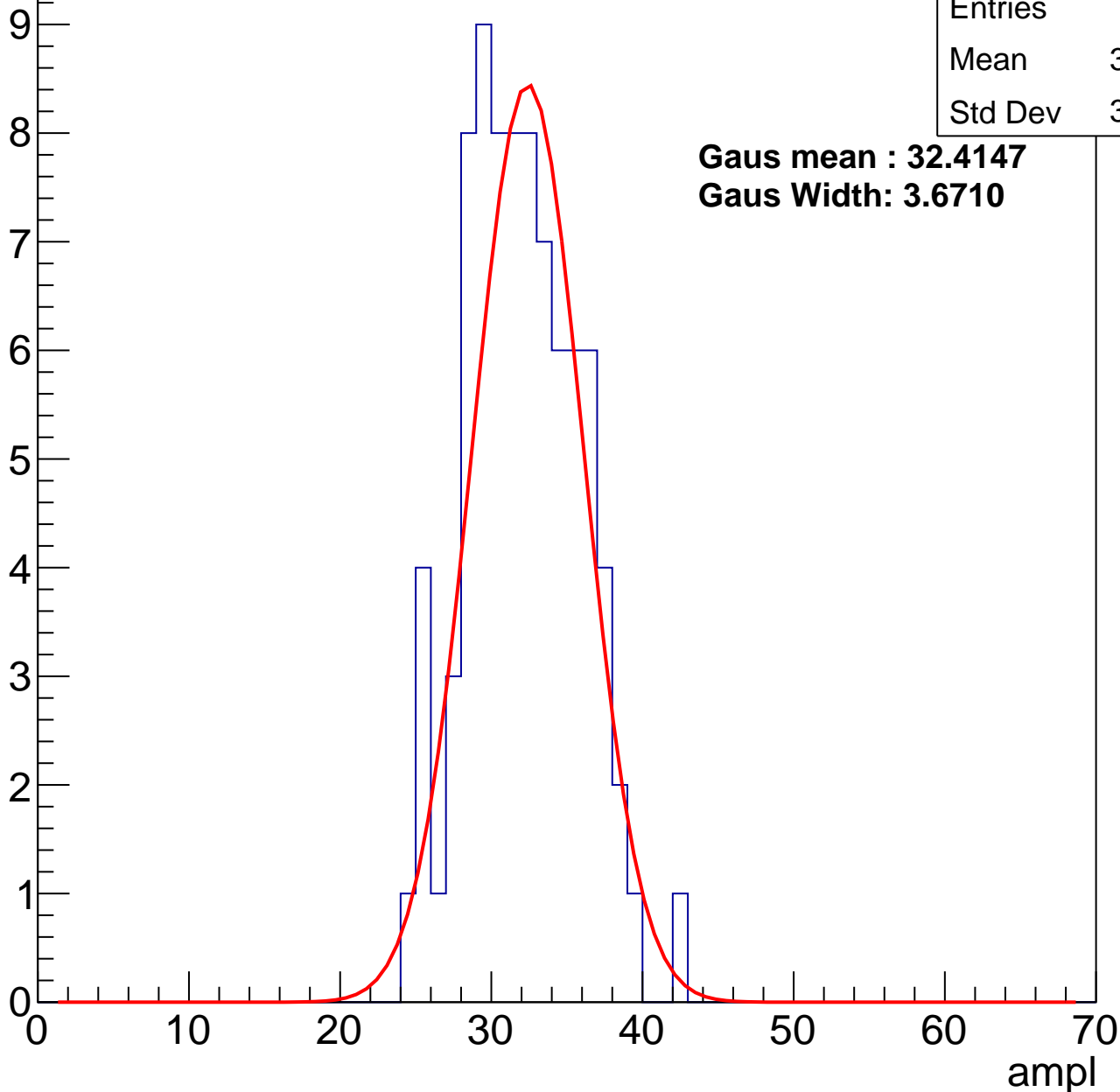
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	31.64
Std Dev	3.659

Gaus mean : 32.4147

Gaus Width: 3.6710



B0L002S, U9-ch12, adc1

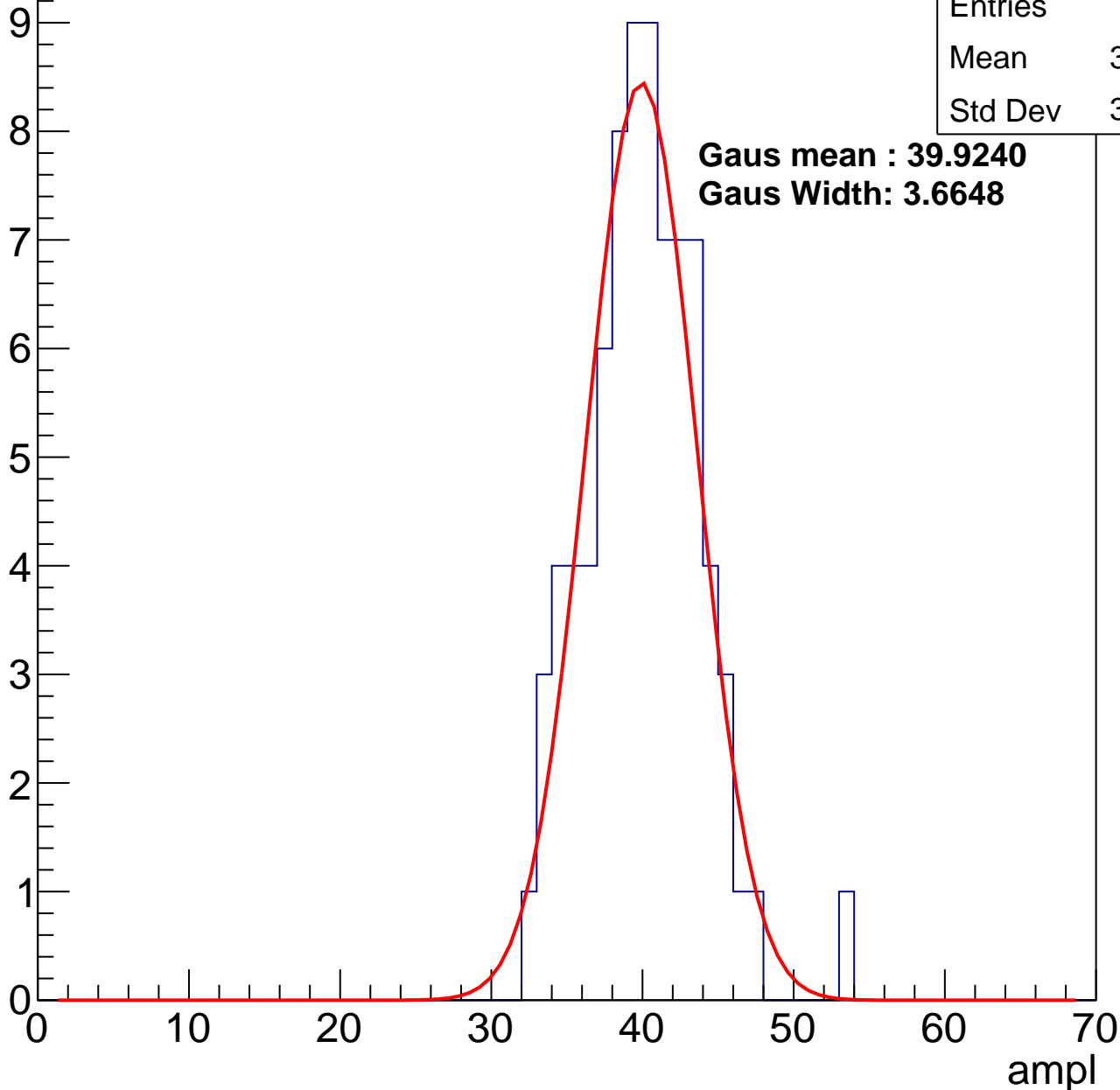
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	39.58
Std Dev	3.717

Gaus mean : 39.9240

Gaus Width: 3.6648



B0L002S, U9-ch12, adc2

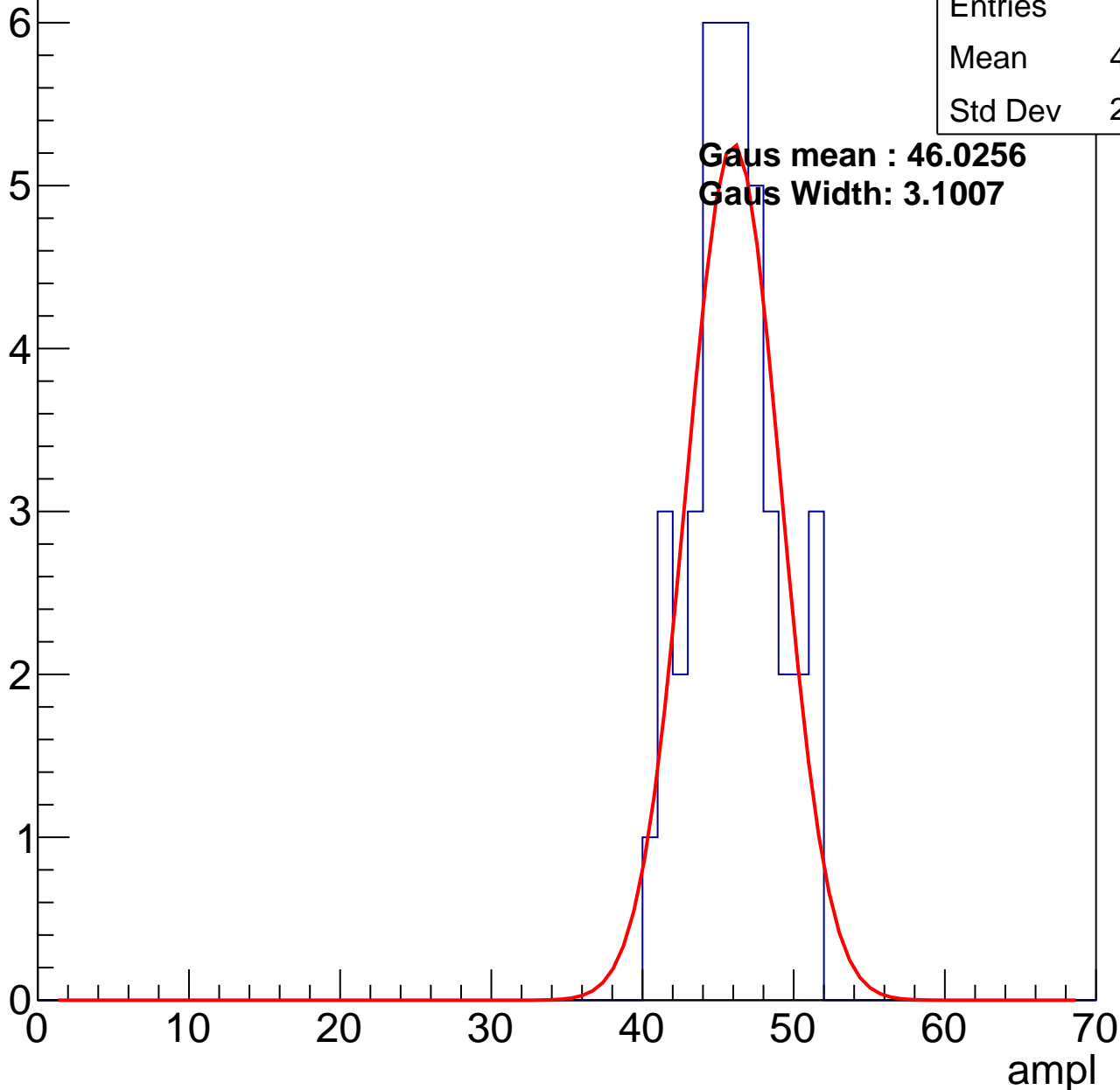
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	45.62
Std Dev	2.828

Gaus mean : 46.0256

Gaus Width: 3.1007

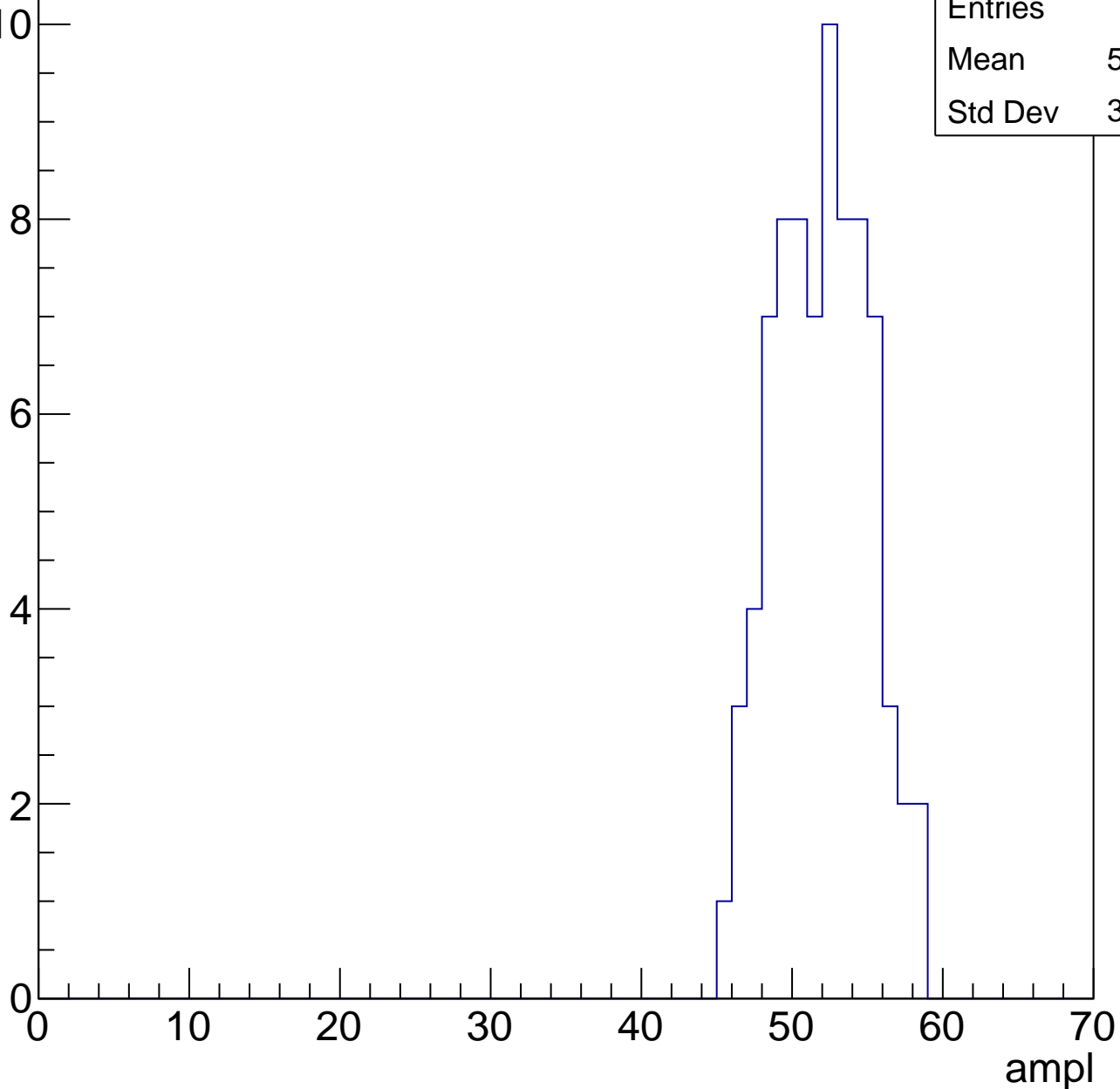


B0L002S, U9-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	51.47
Std Dev	3.062



B0L002S, U9-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

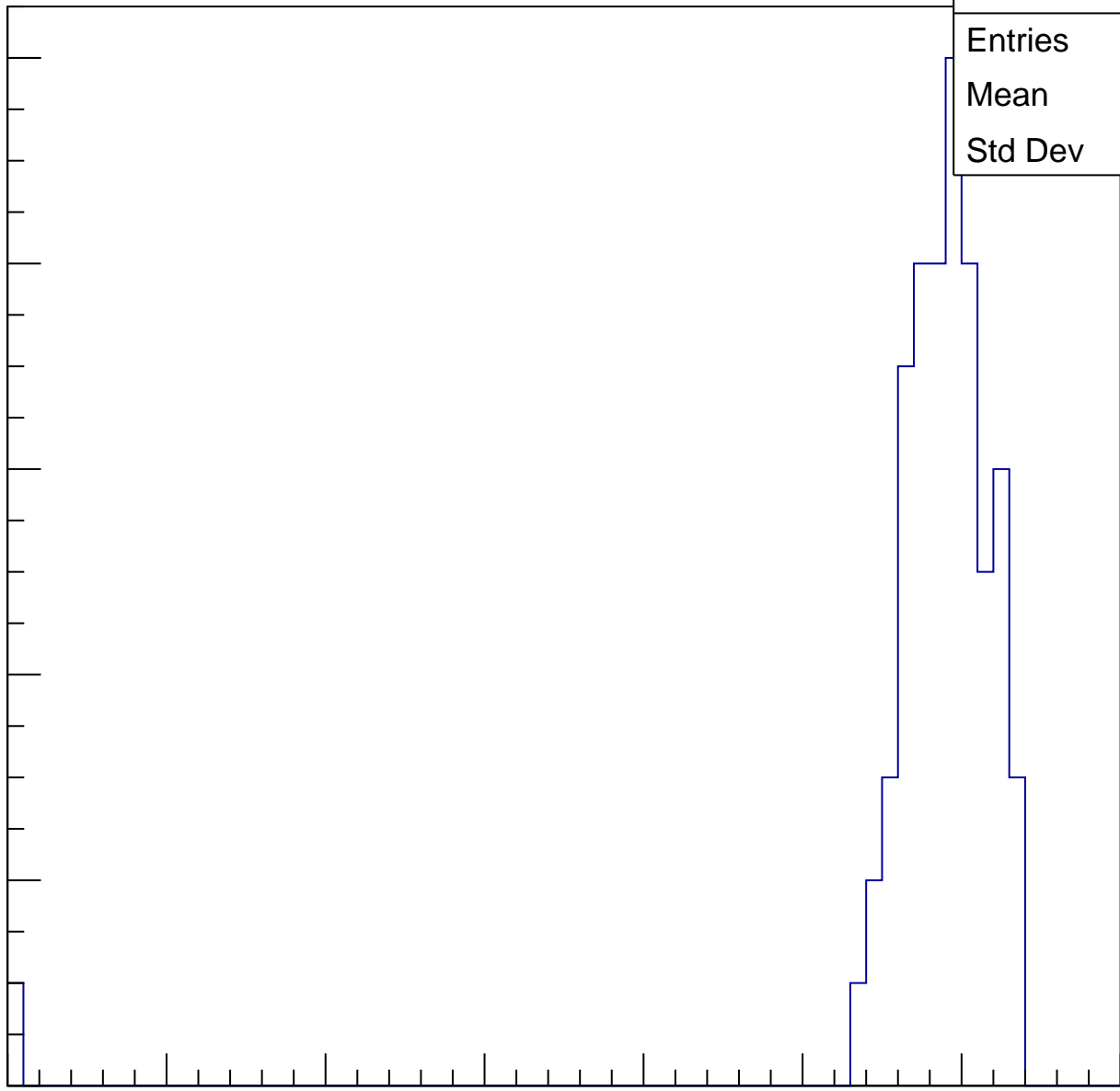
Entries	62
Mean	57.65
Std Dev	7.761

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

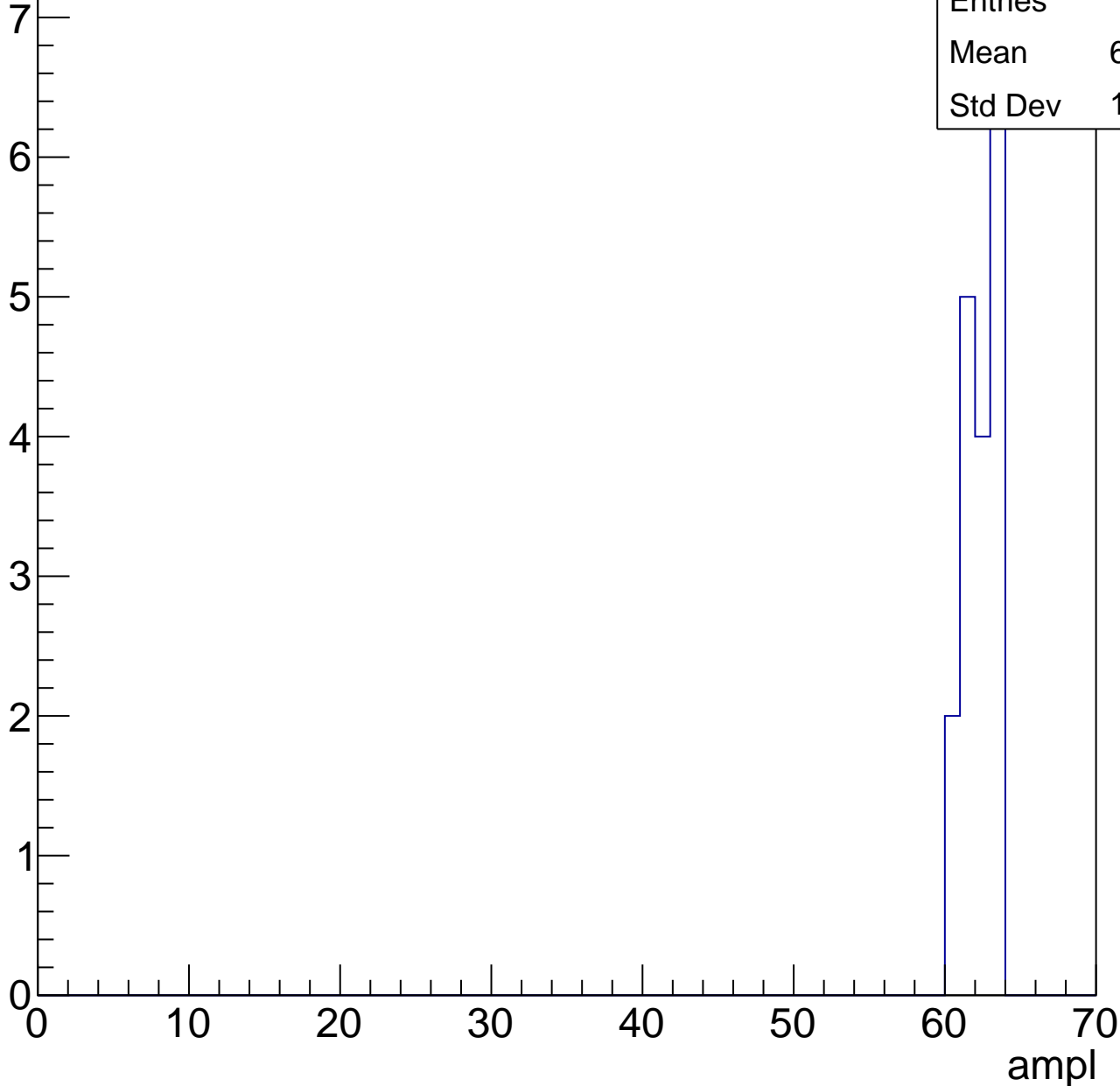


B0L002S, U9-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	18
Mean	61.89
Std Dev	1.048



B0L002S, U9-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

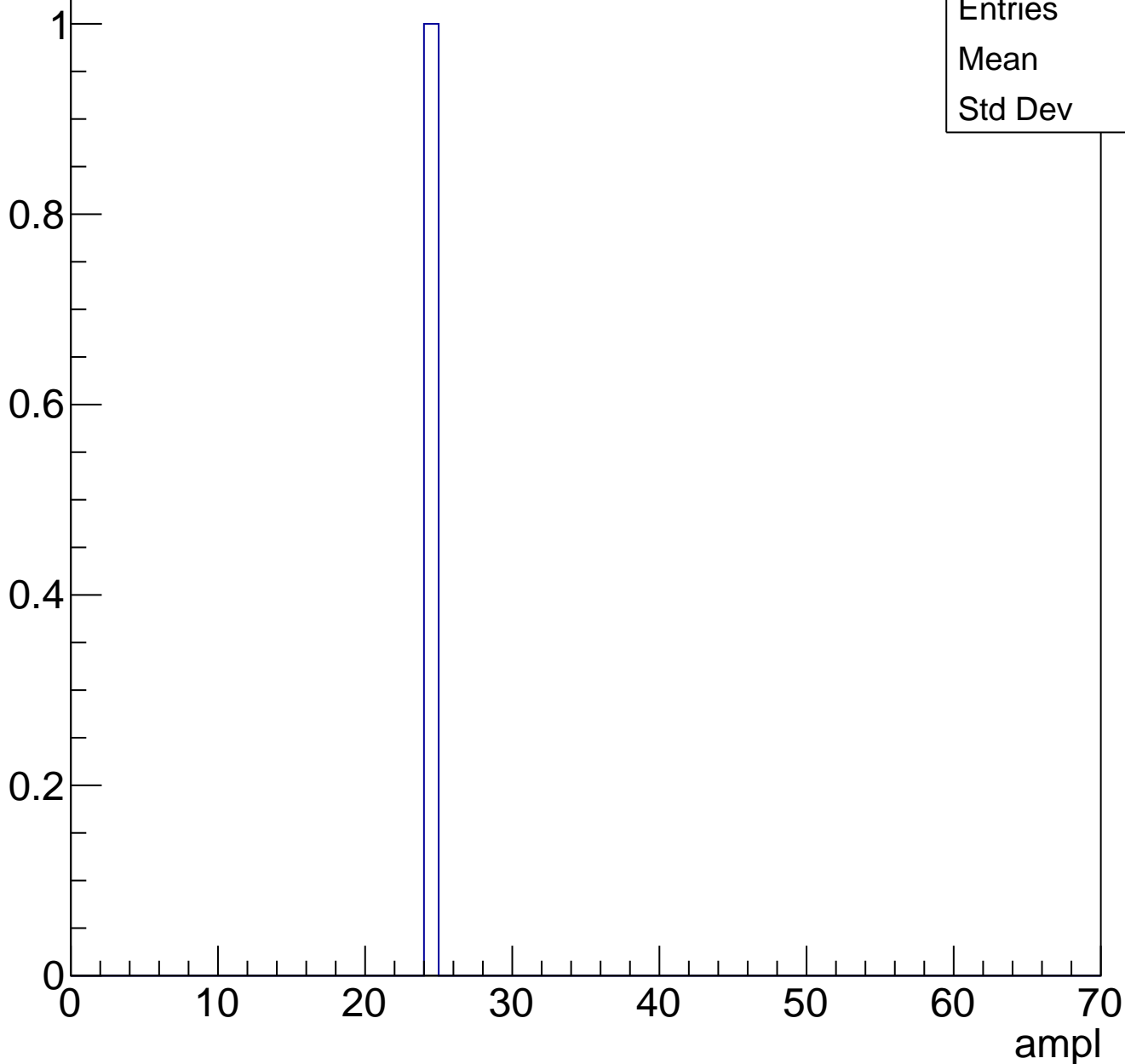


Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch13, adc0

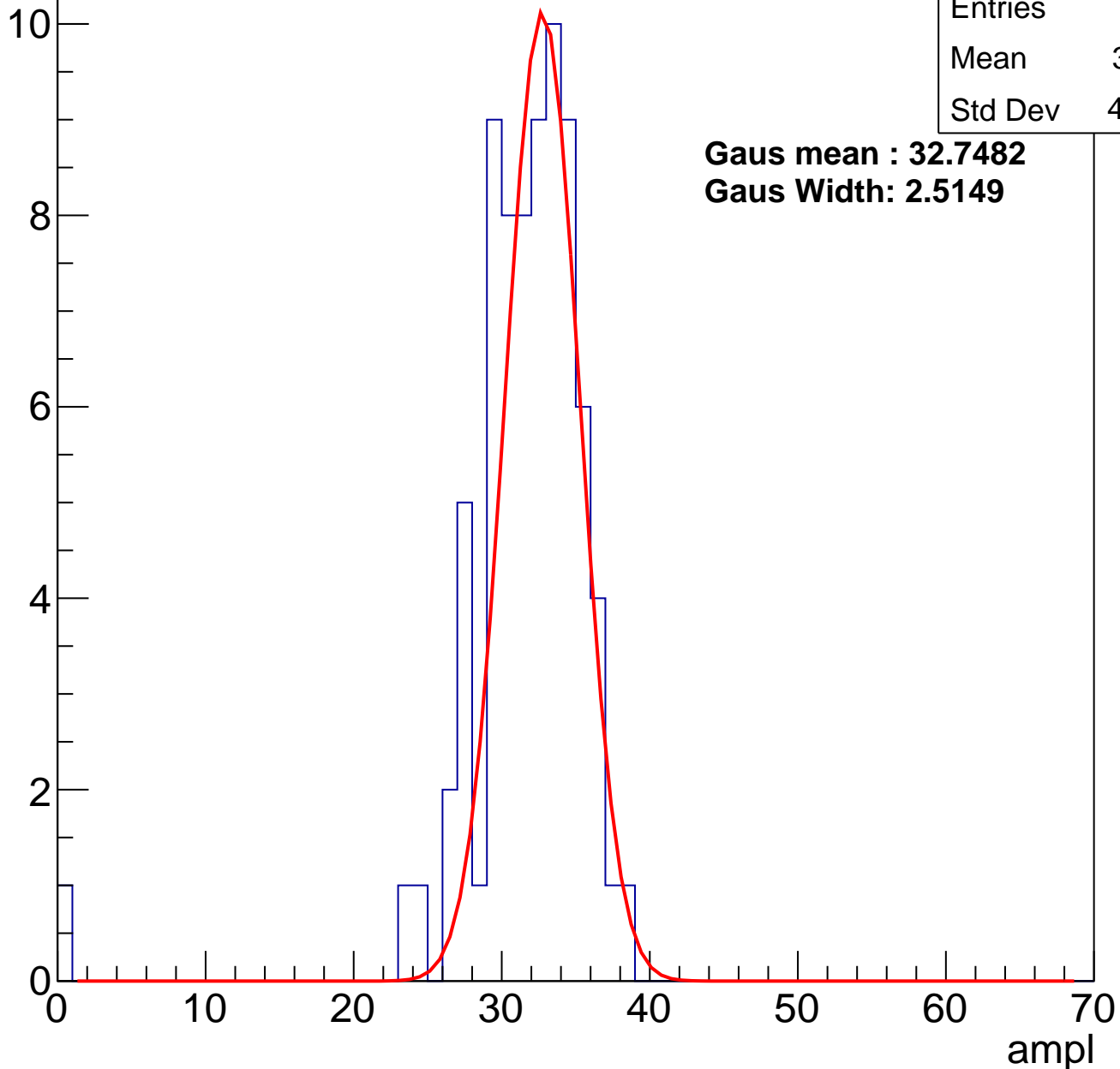
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	31.11
Std Dev	4.684

Gaus mean : 32.7482

Gaus Width: 2.5149

Entry



B0L002S, U9-ch13, adc1

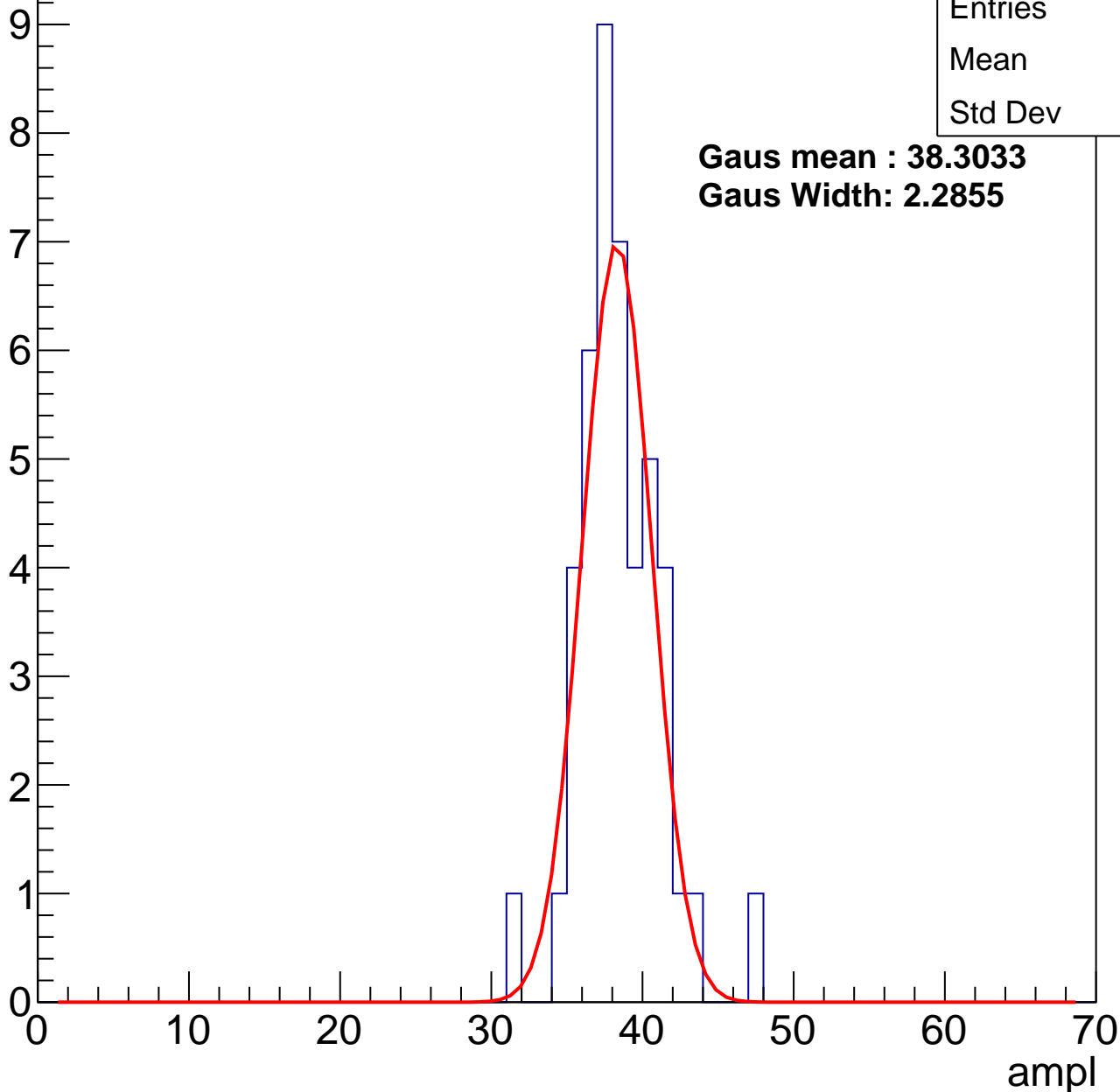
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	38
Std Dev	2.68

Gaus mean : 38.3033

Gaus Width: 2.2855



B0L002S, U9-ch13, adc2

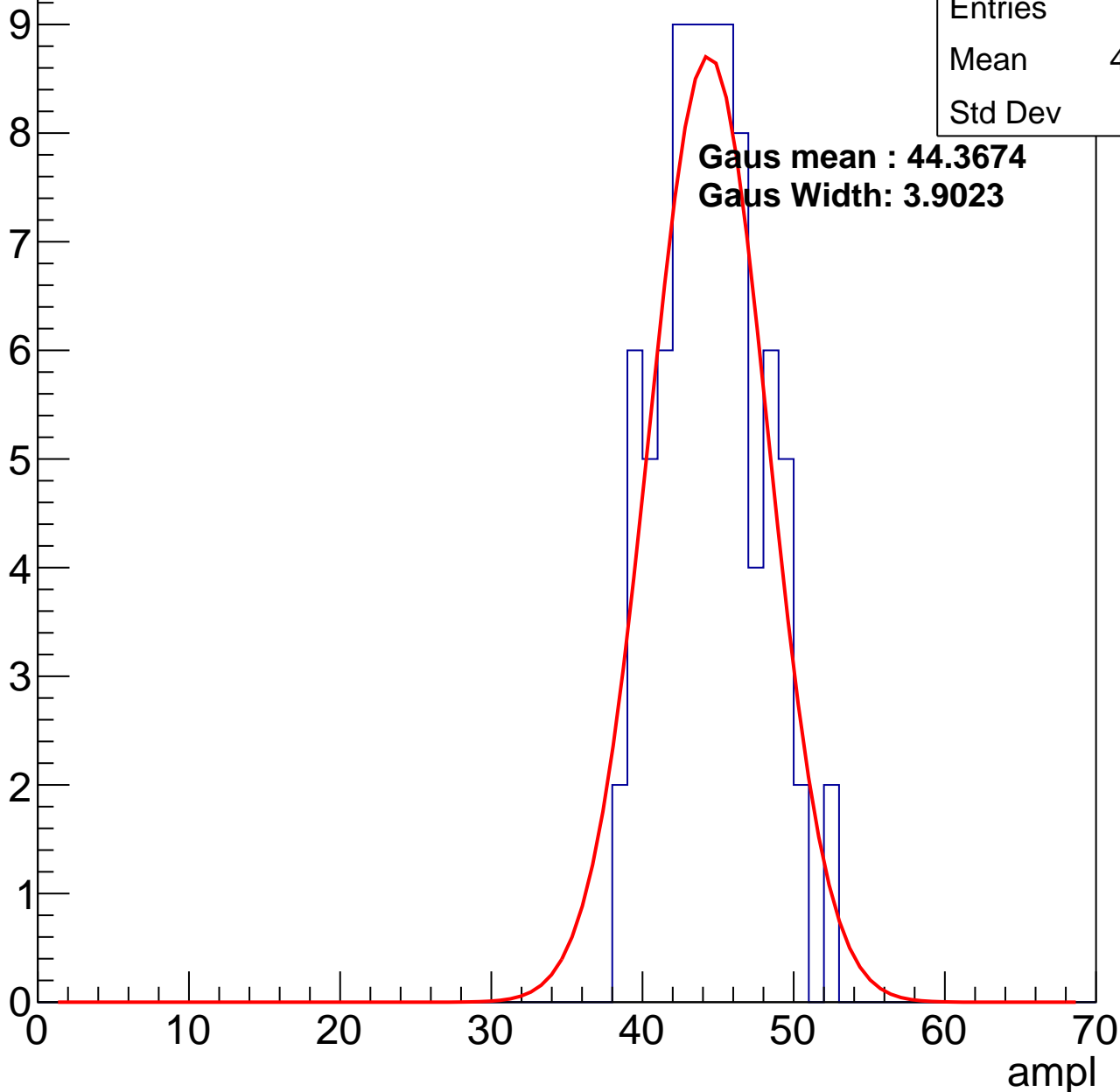
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	44.09
Std Dev	3.31

Gaus mean : 44.3674

Gaus Width: 3.9023

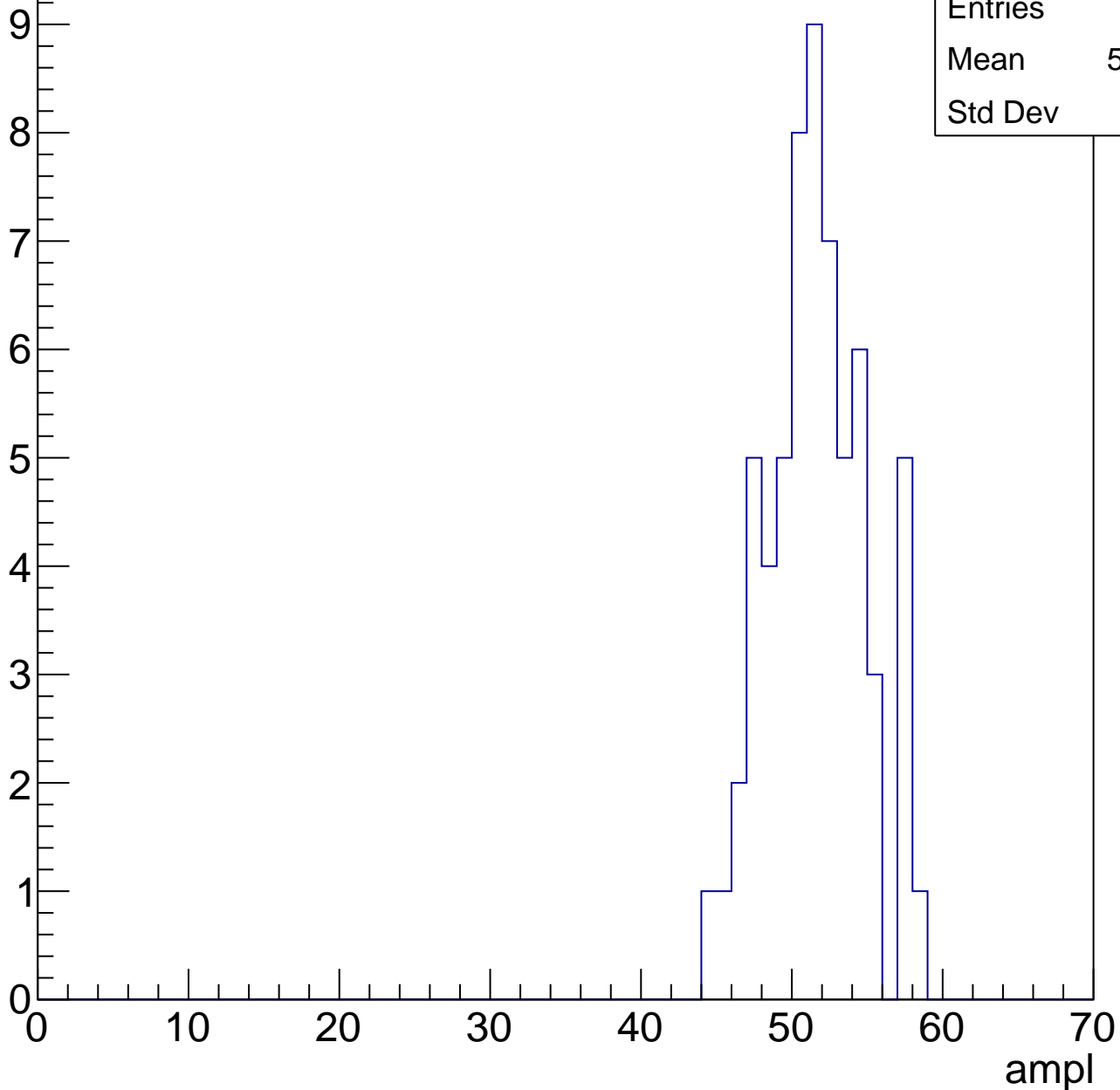


B0L002S, U9-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

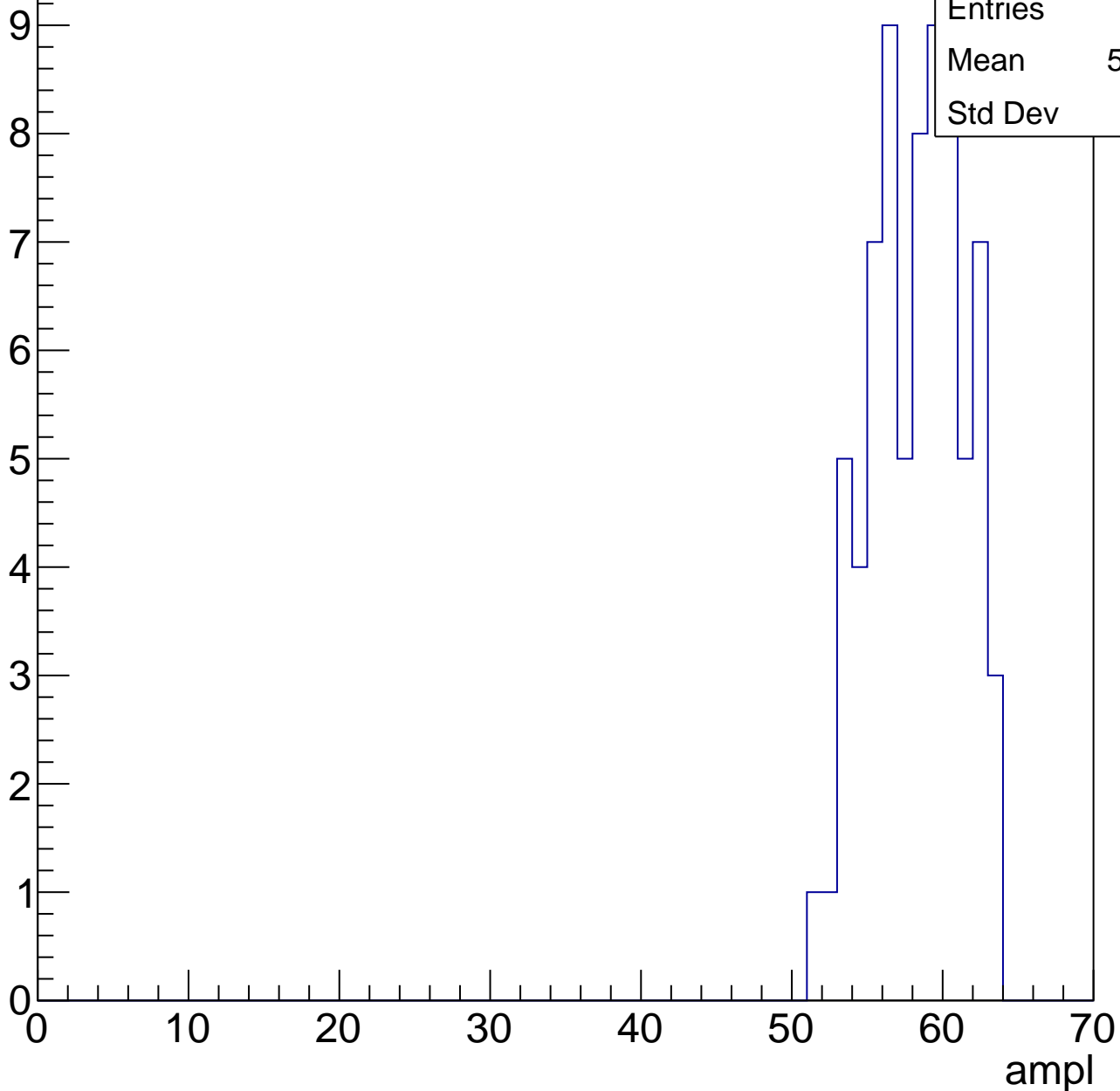
Entries	62
Mean	51.18
Std Dev	3.2



B0L002S, U9-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

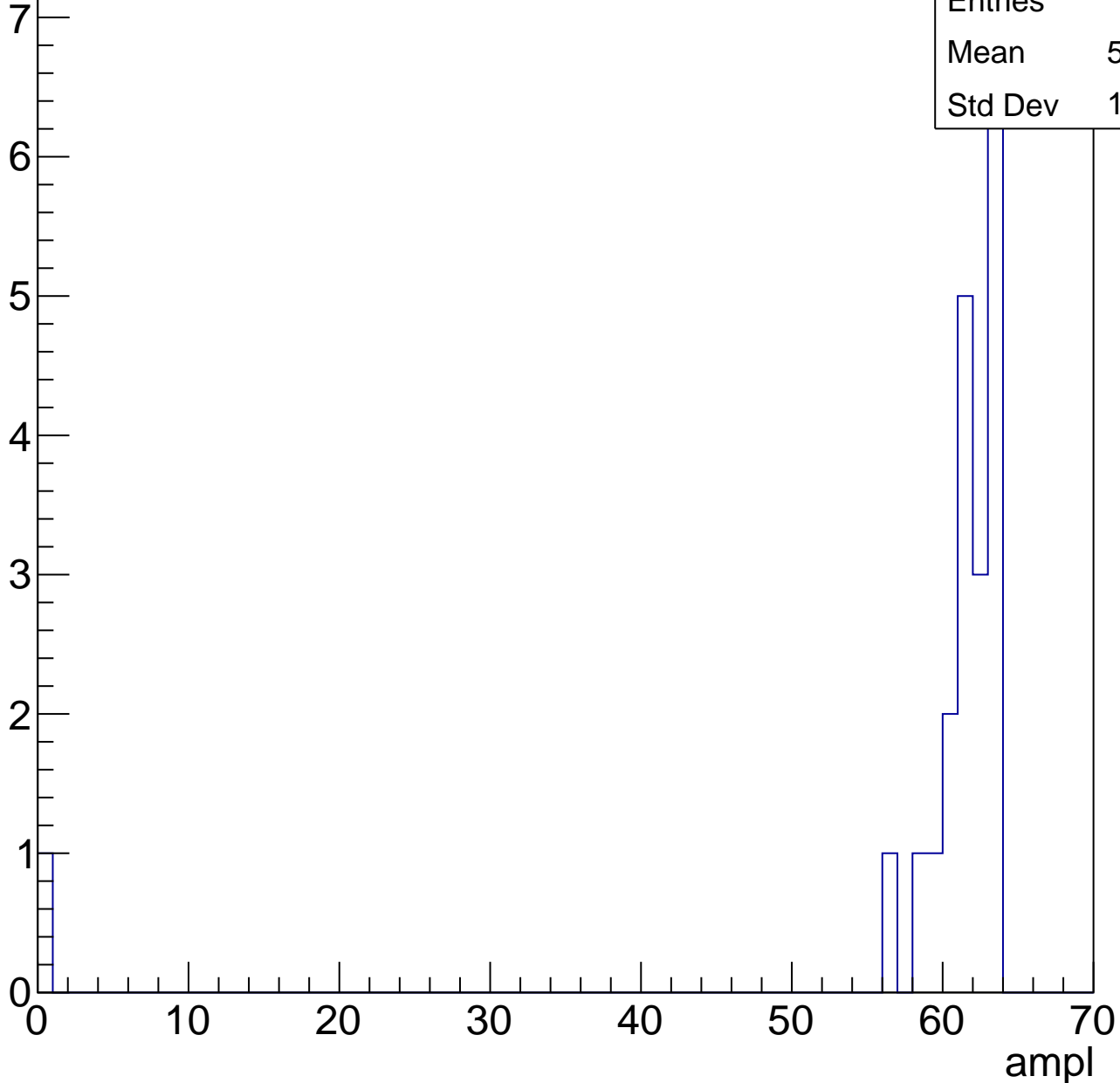


B0L002S, U9-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.33
Std Dev	13.17



B0L002S, U9-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch14, adc0

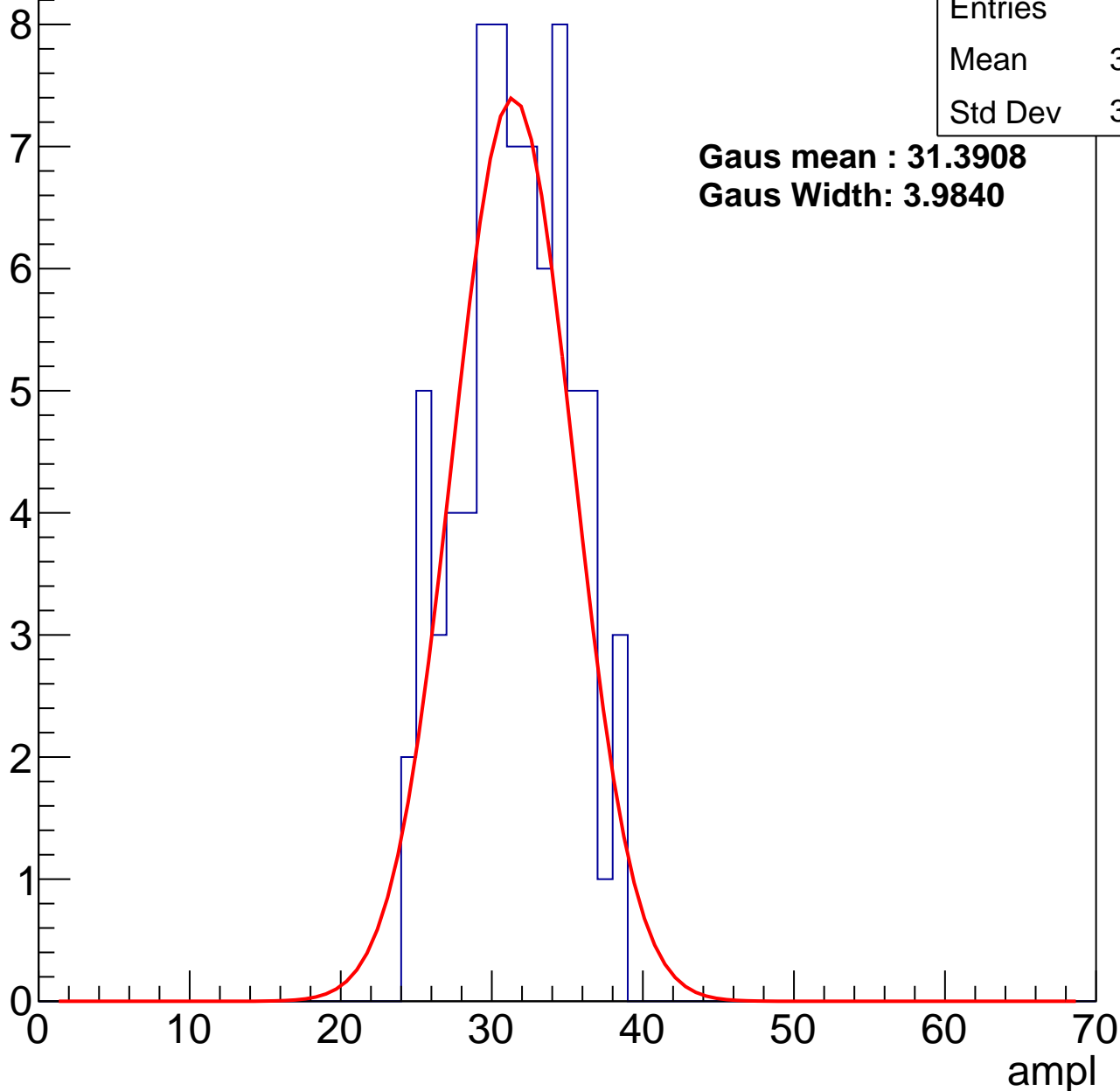
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.05
Std Dev	3.598

Gaus mean : 31.3908

Gaus Width: 3.9840



B0L002S, U9-ch14, adc1

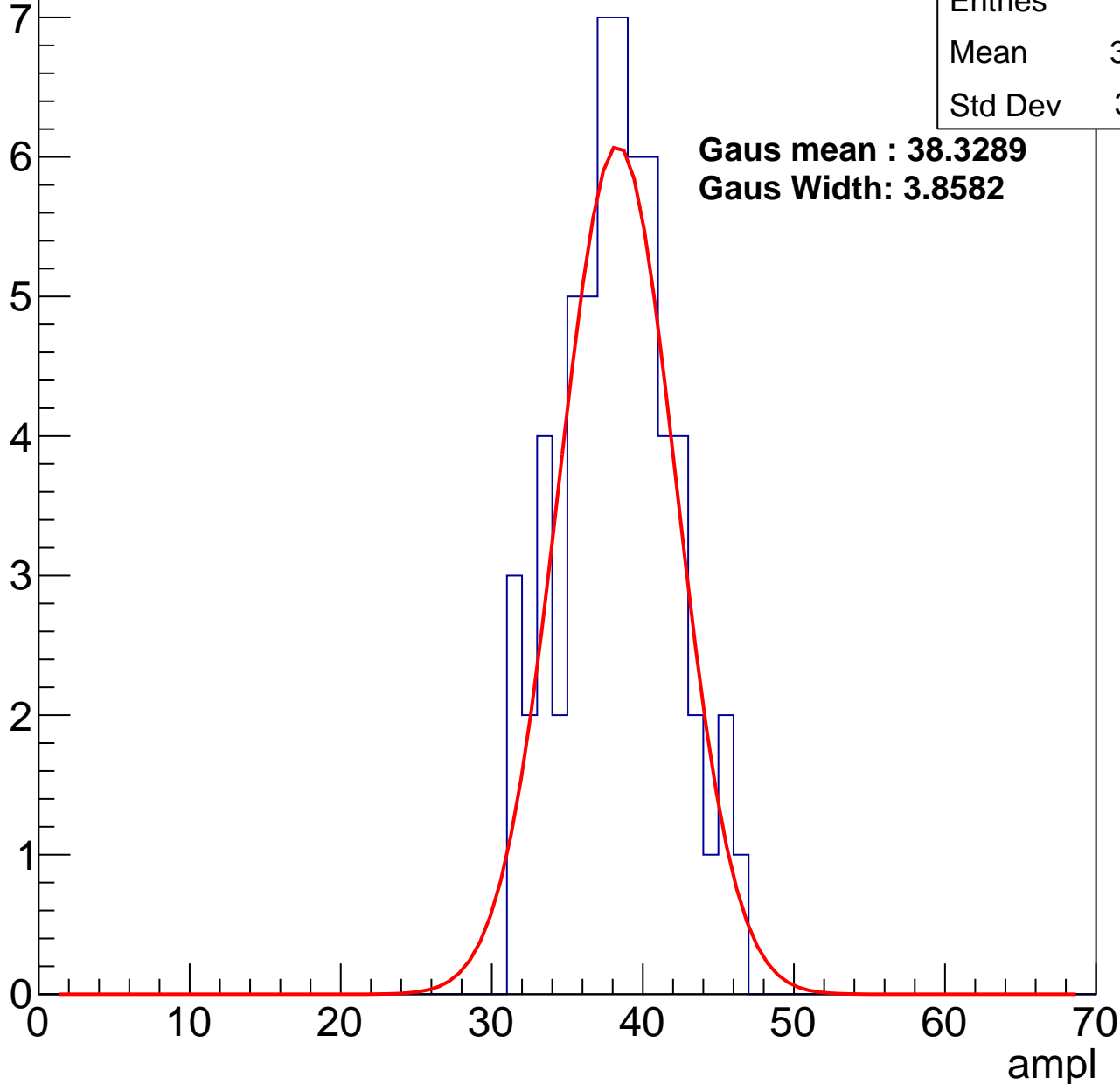
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.85
Std Dev	3.621

Gaus mean : 38.3289

Gaus Width: 3.8582



B0L002S, U9-ch14, adc2

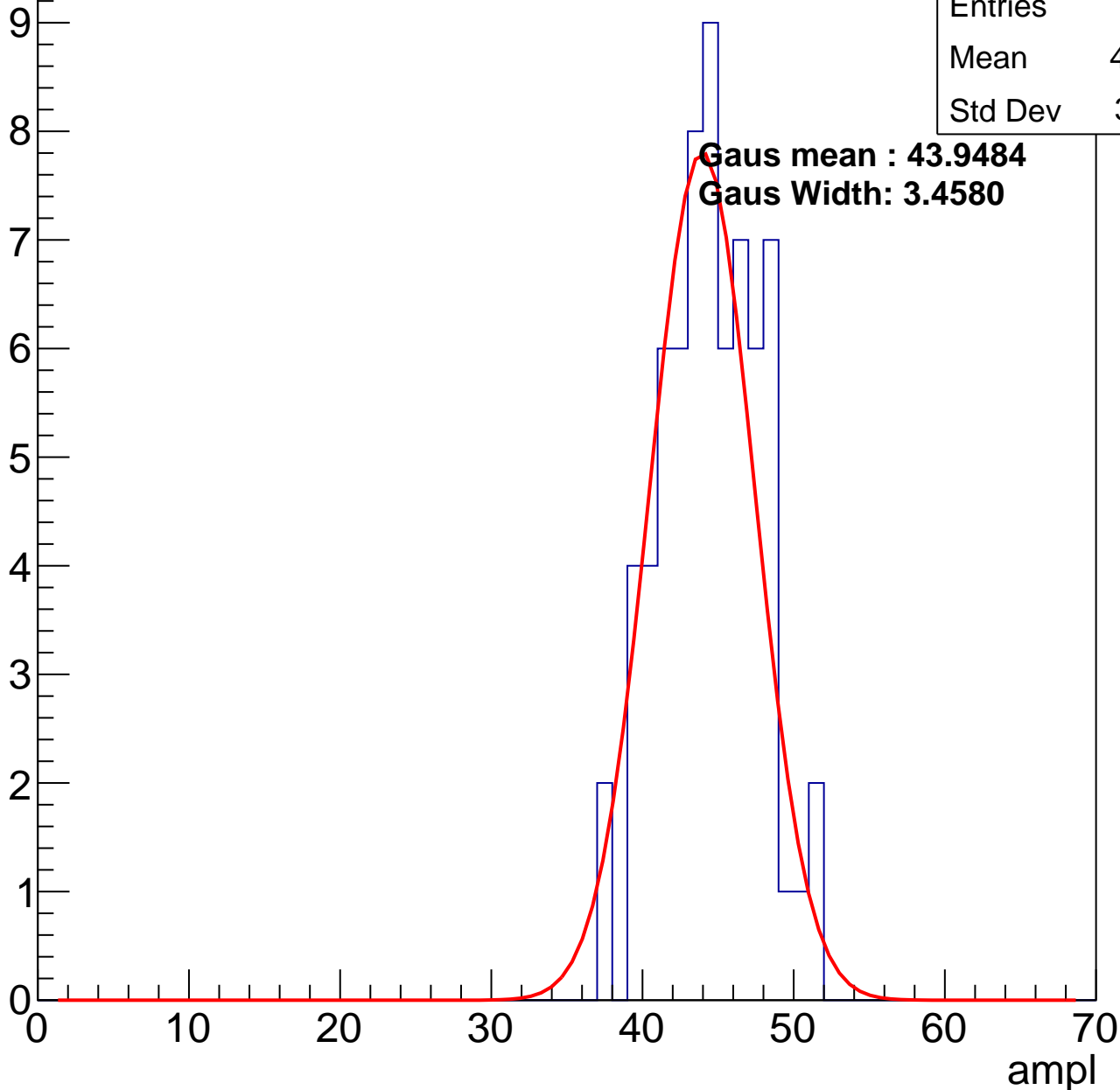
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.04
Std Dev	3.201

Gaus mean : 43.9484

Gaus Width: 3.4580

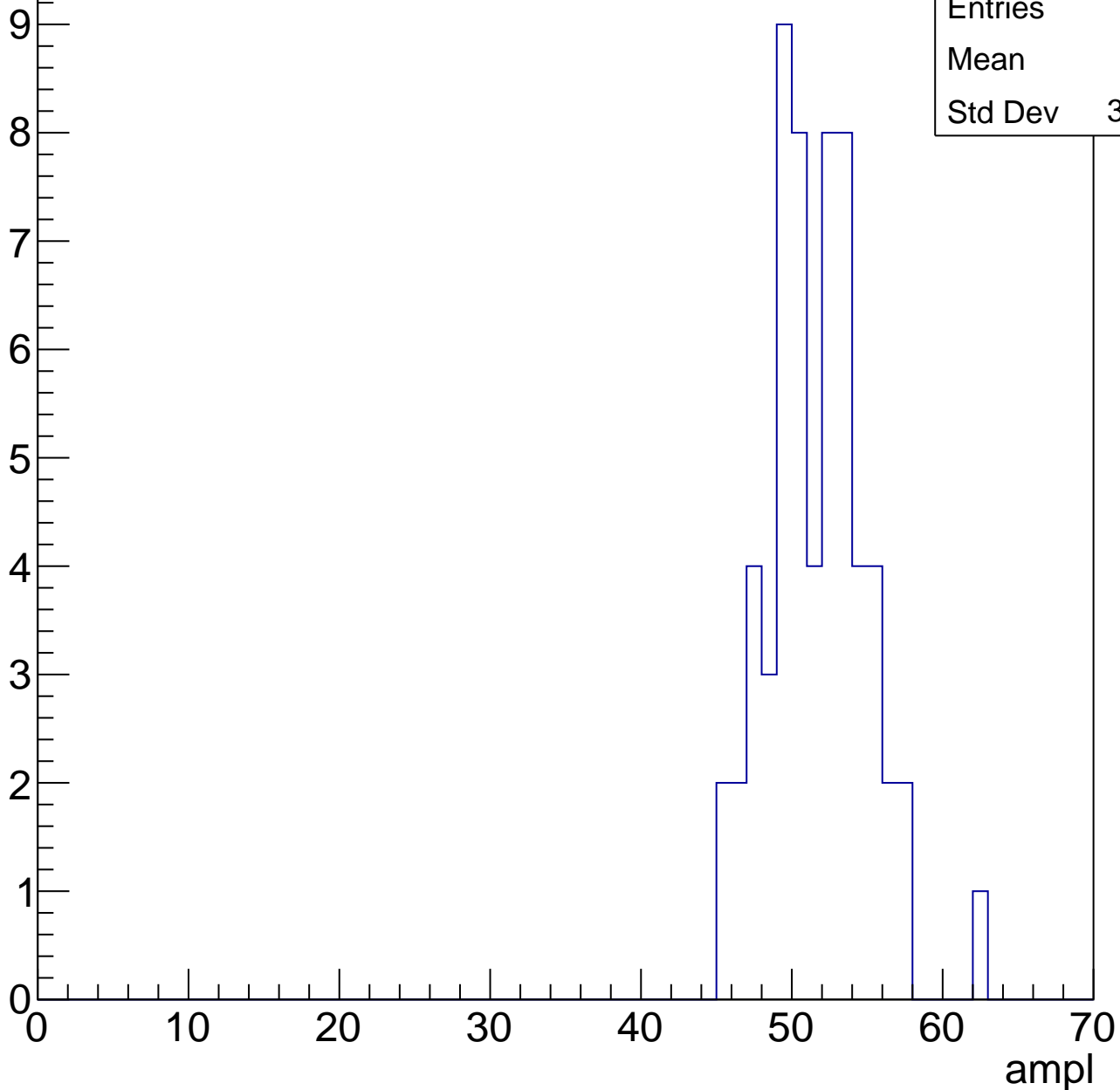


B0L002S, U9-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	51.2
Std Dev	3.233

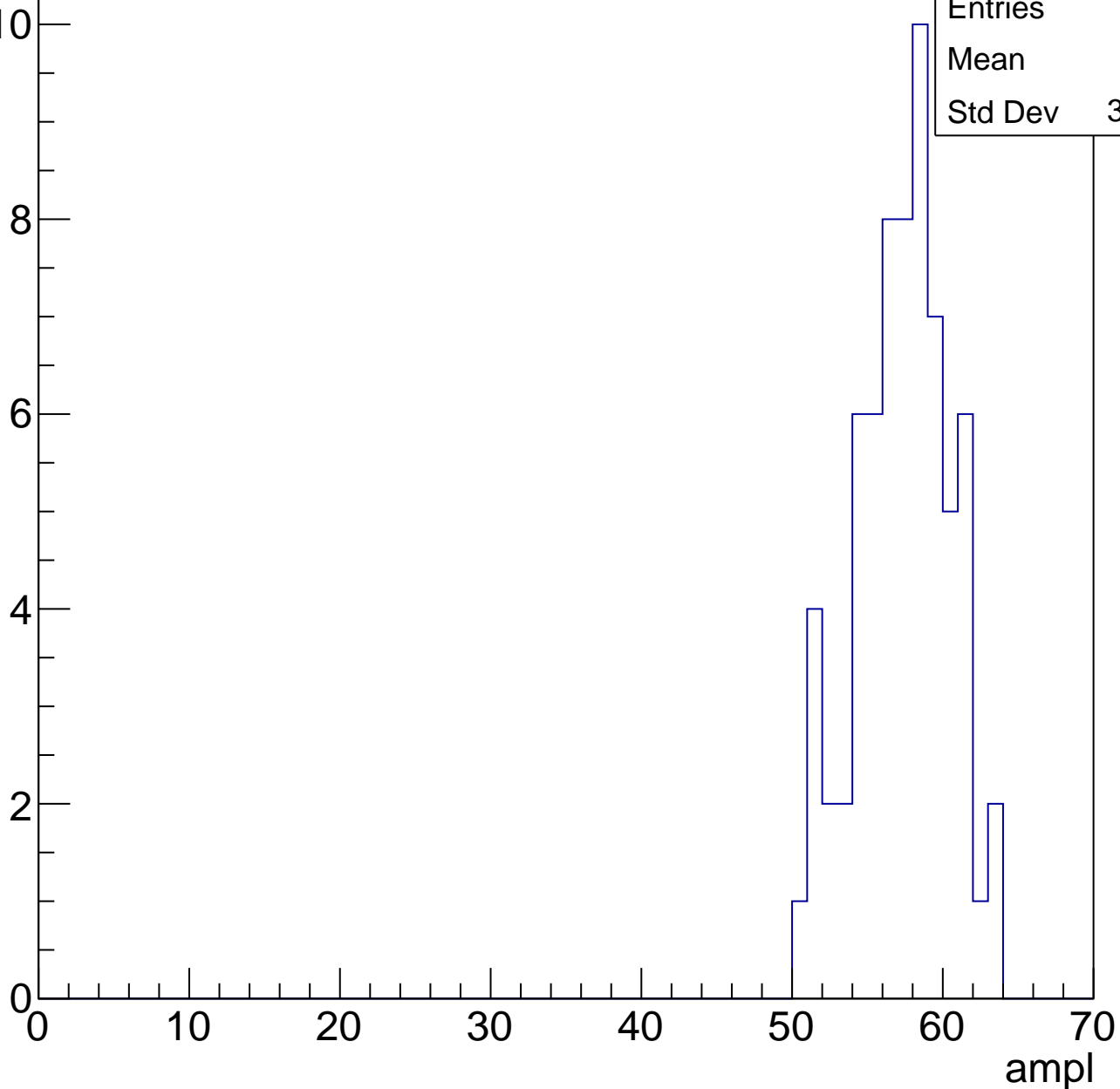


B0L002S, U9-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	56.9
Std Dev	3.059



B0L002S, U9-ch14, adc5

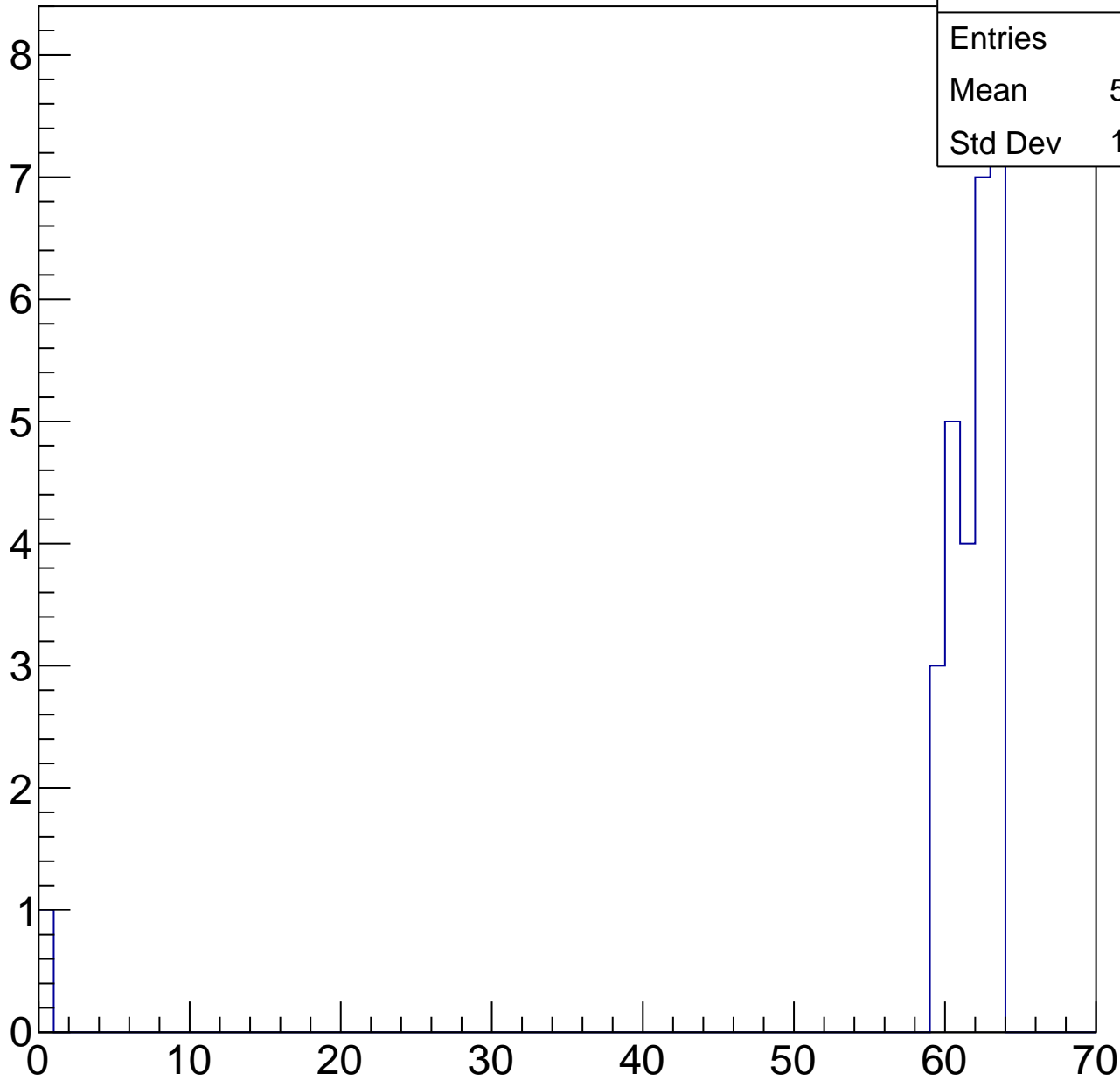
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	28
Mean	59.25
Std Dev	11.48

ampl



B0L002S, U9-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

62

Std Dev

0

B0L002S, U9-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch15, adc0

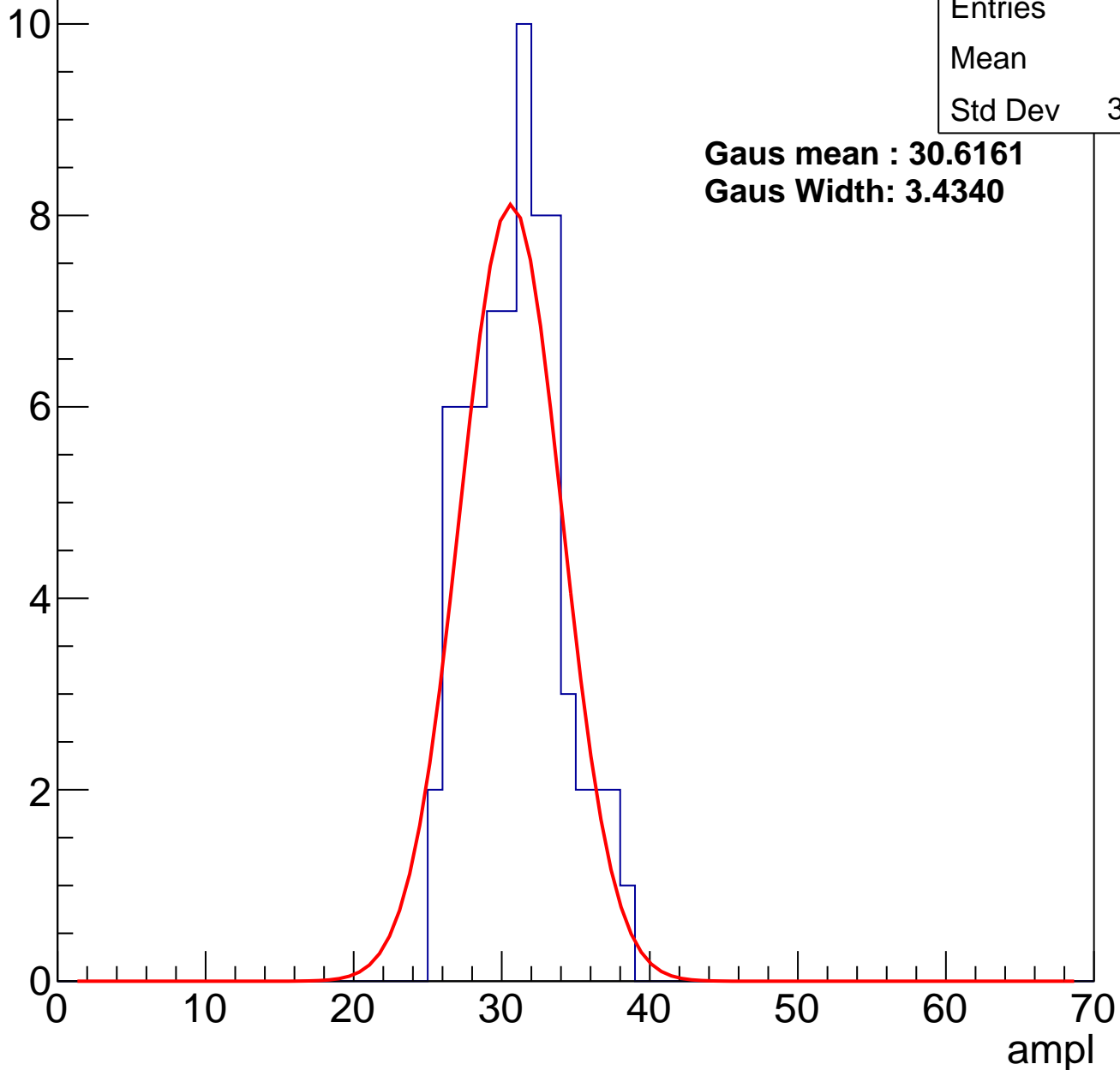
calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	30.5
Std Dev	3.069

Gaus mean : 30.6161

Gaus Width: 3.4340

Entry



B0L002S, U9-ch15, adc1

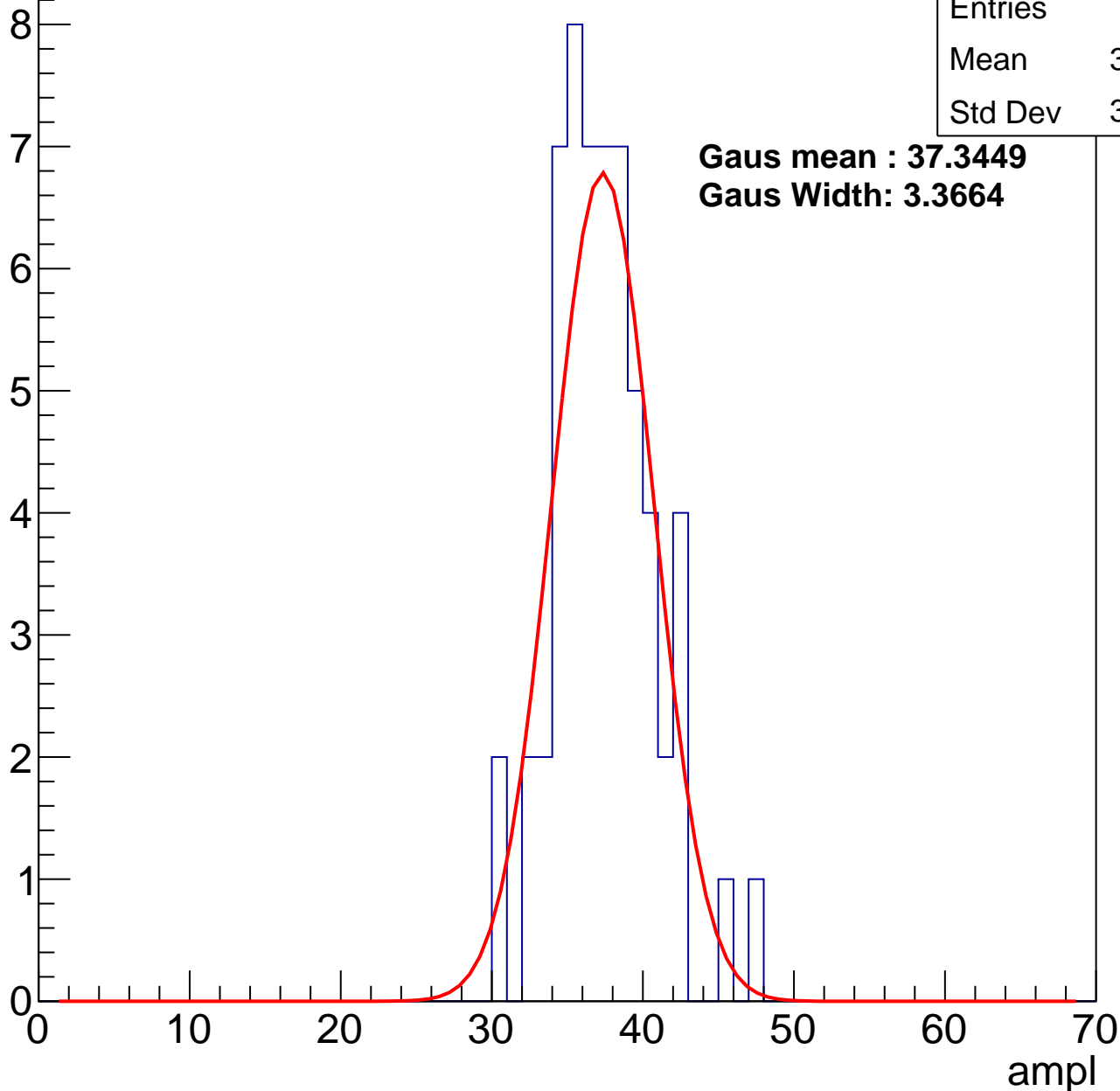
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.98
Std Dev	3.296

Gaus mean : 37.3449

Gaus Width: 3.3664



B0L002S, U9-ch15, adc2

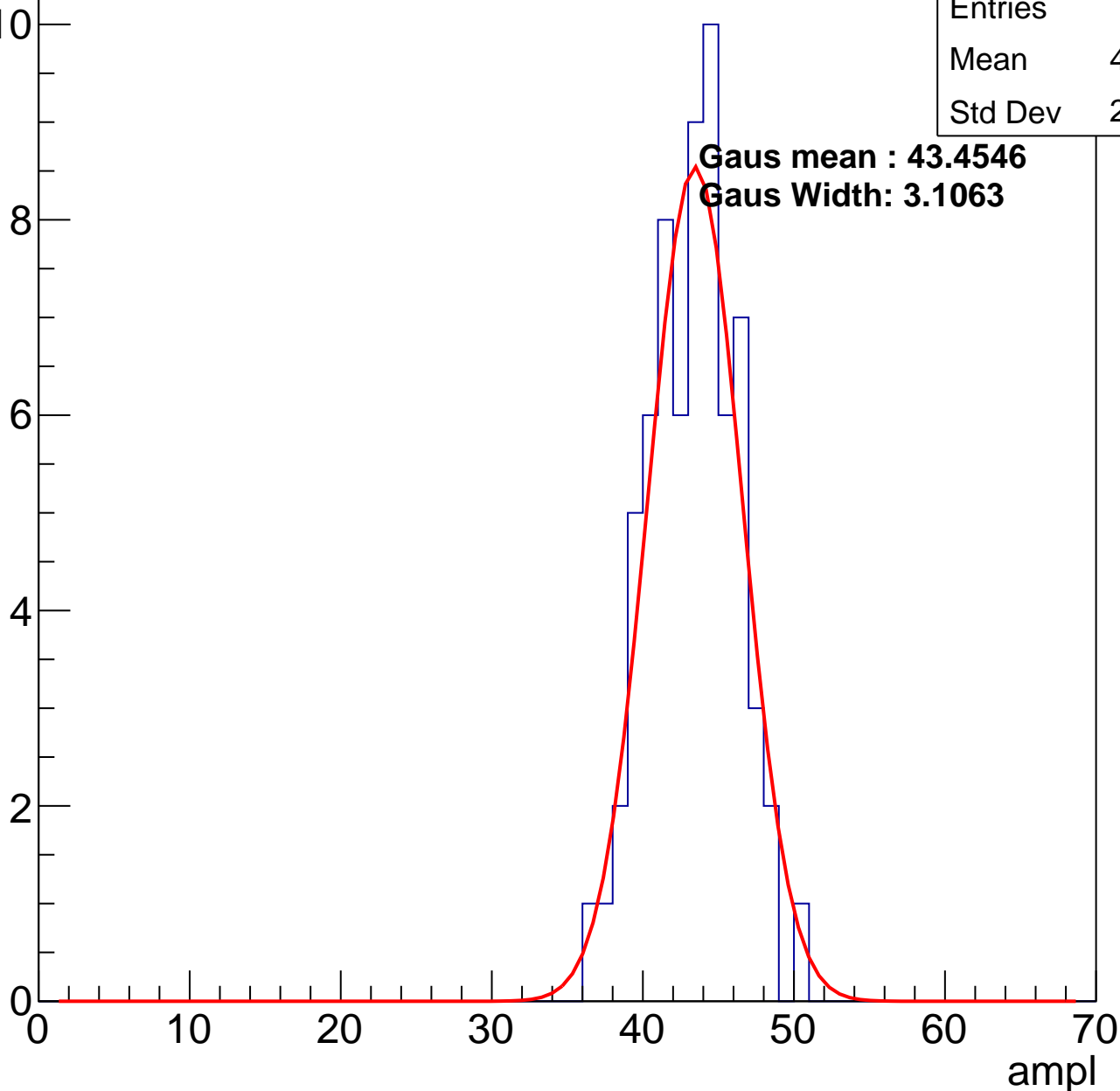
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.84
Std Dev	2.863

Gaus mean : 43.4546

Gaus Width: 3.1063

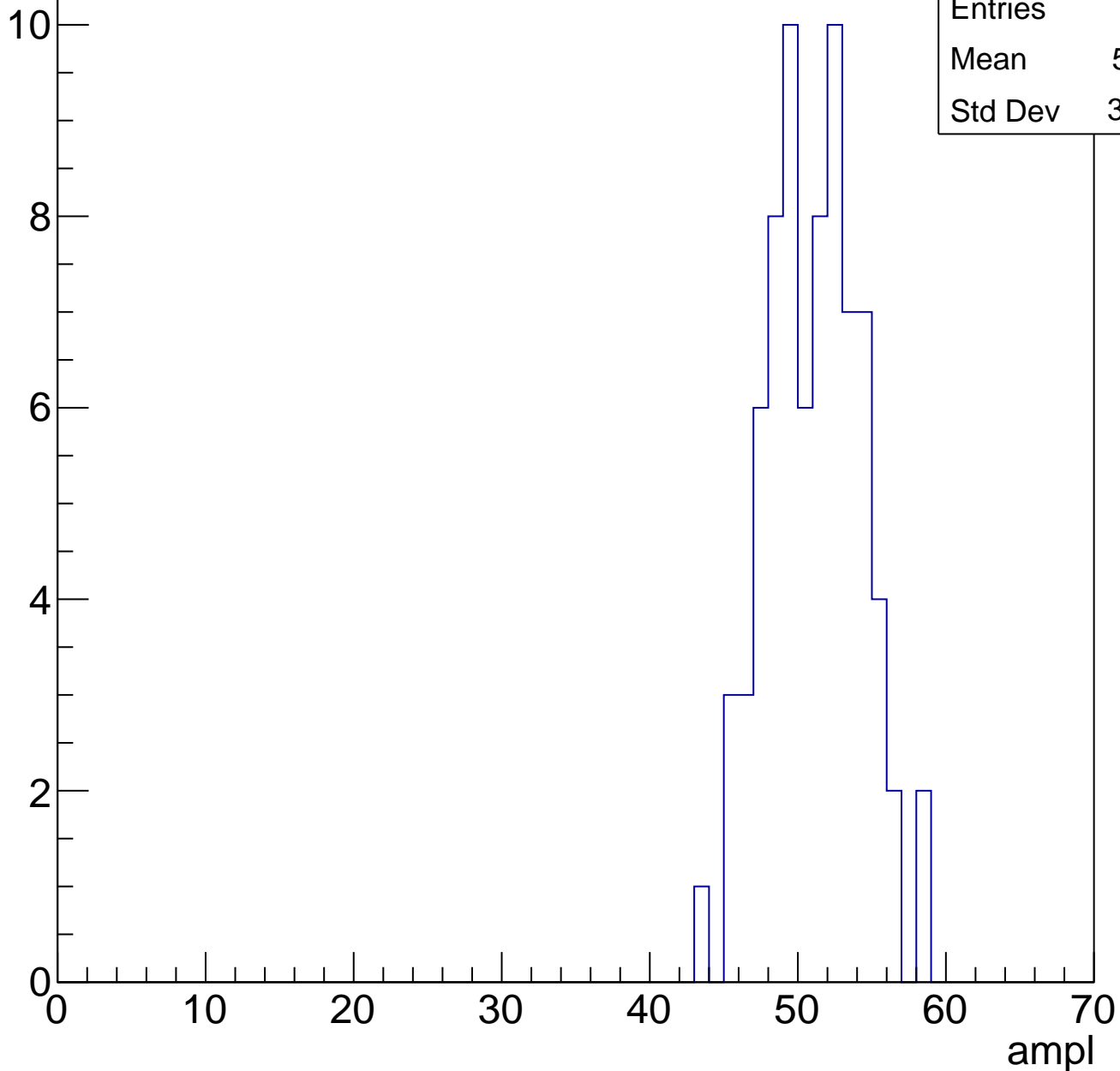


B0L002S, U9-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	50.61
Std Dev	3.146

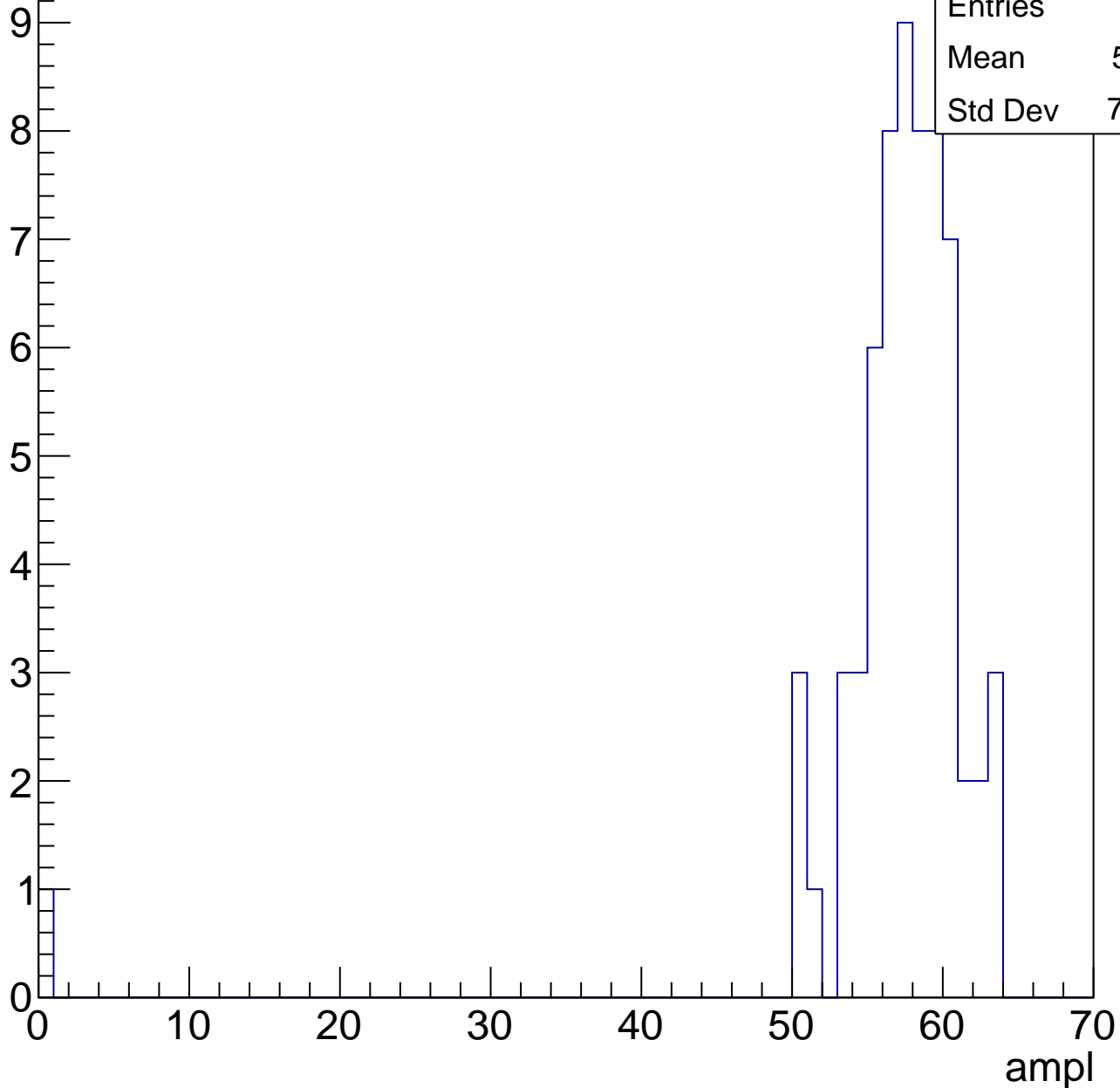
Entry



B0L002S, U9-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

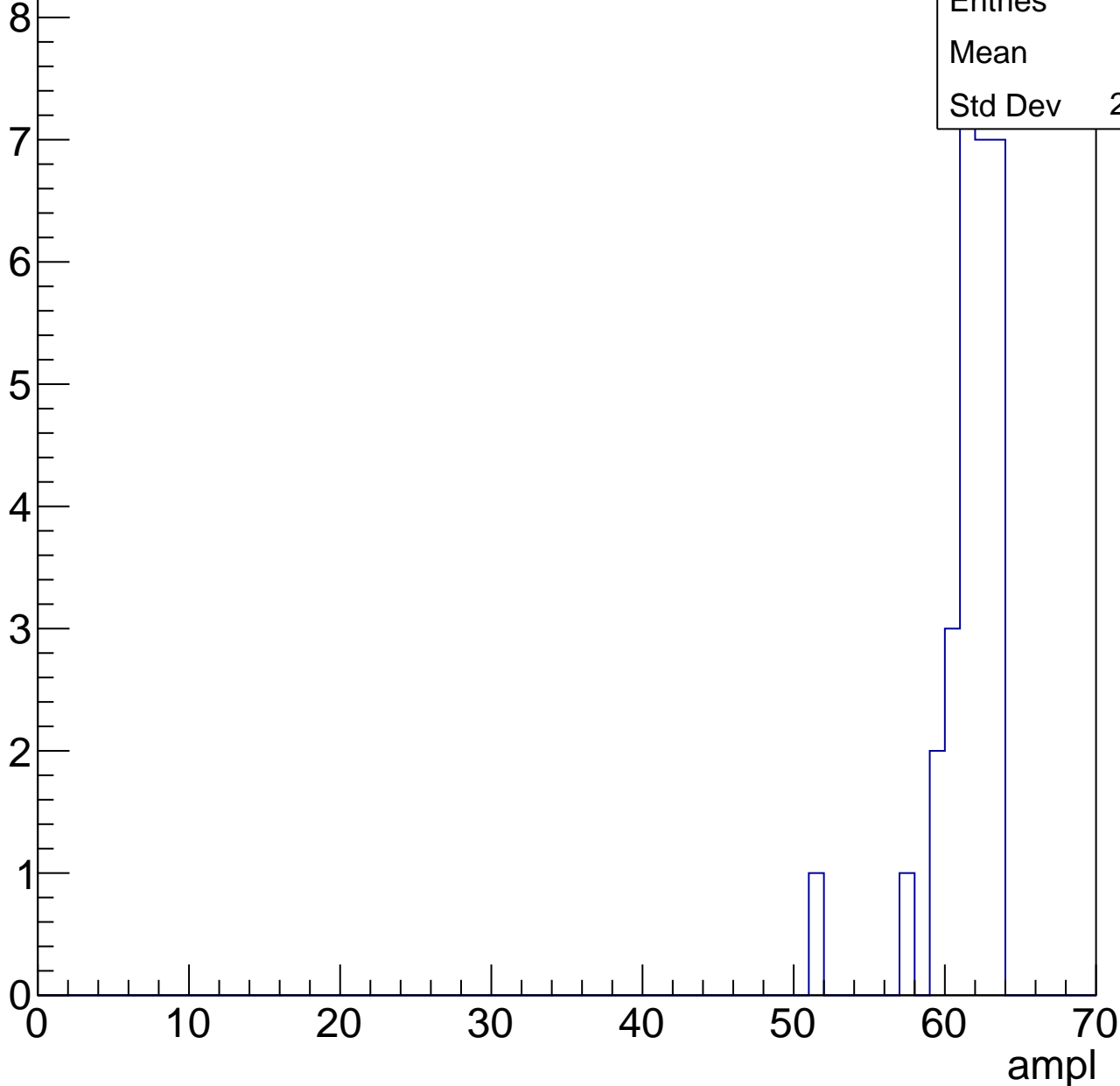


B0L002S, U9-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	61
Std Dev	2.364



B0L002S, U9-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch16, adc0

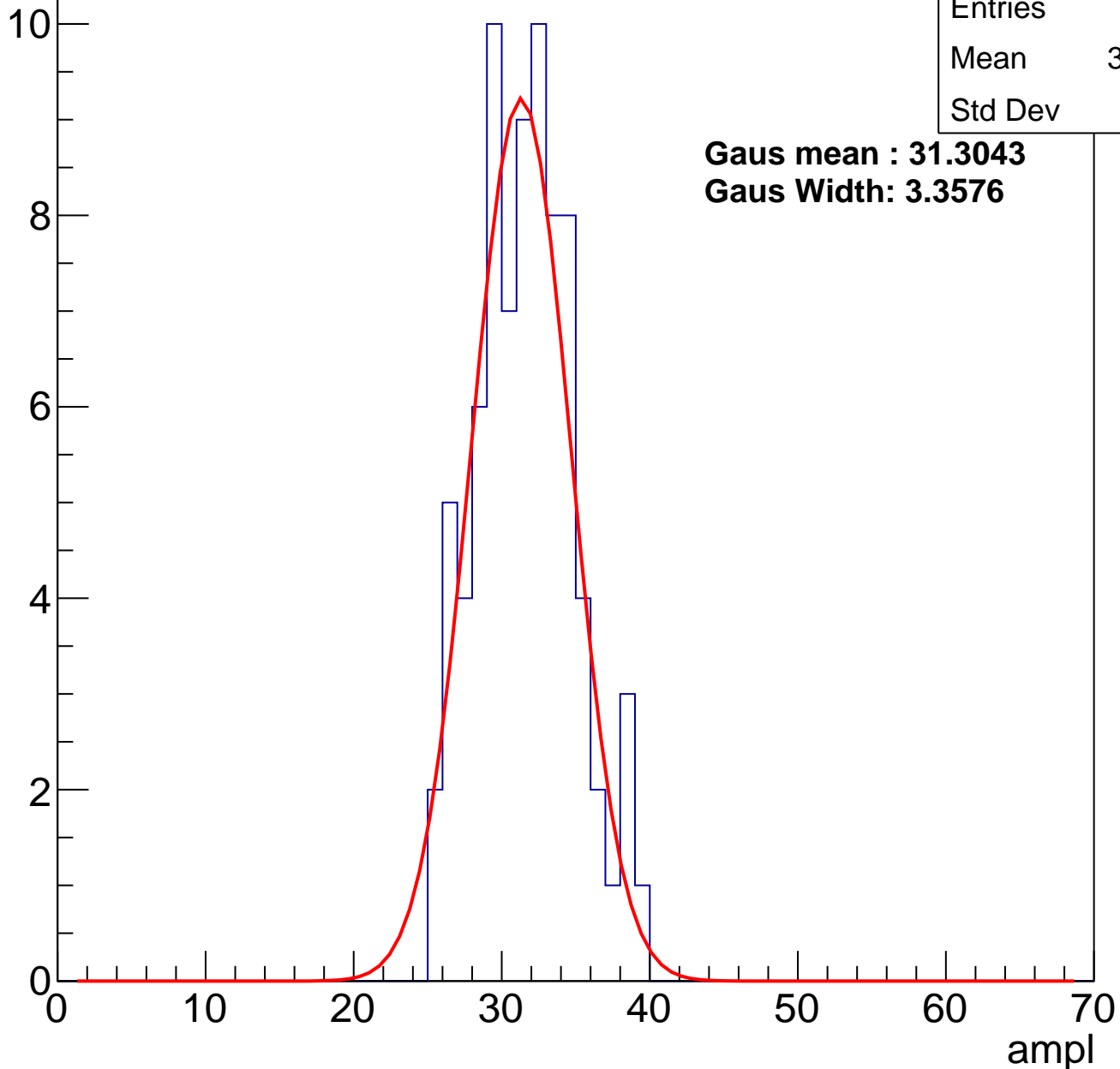
calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	31.16
Std Dev	3.23

Gaus mean : 31.3043

Gaus Width: 3.3576

Entry



B0L002S, U9-ch16, adc1

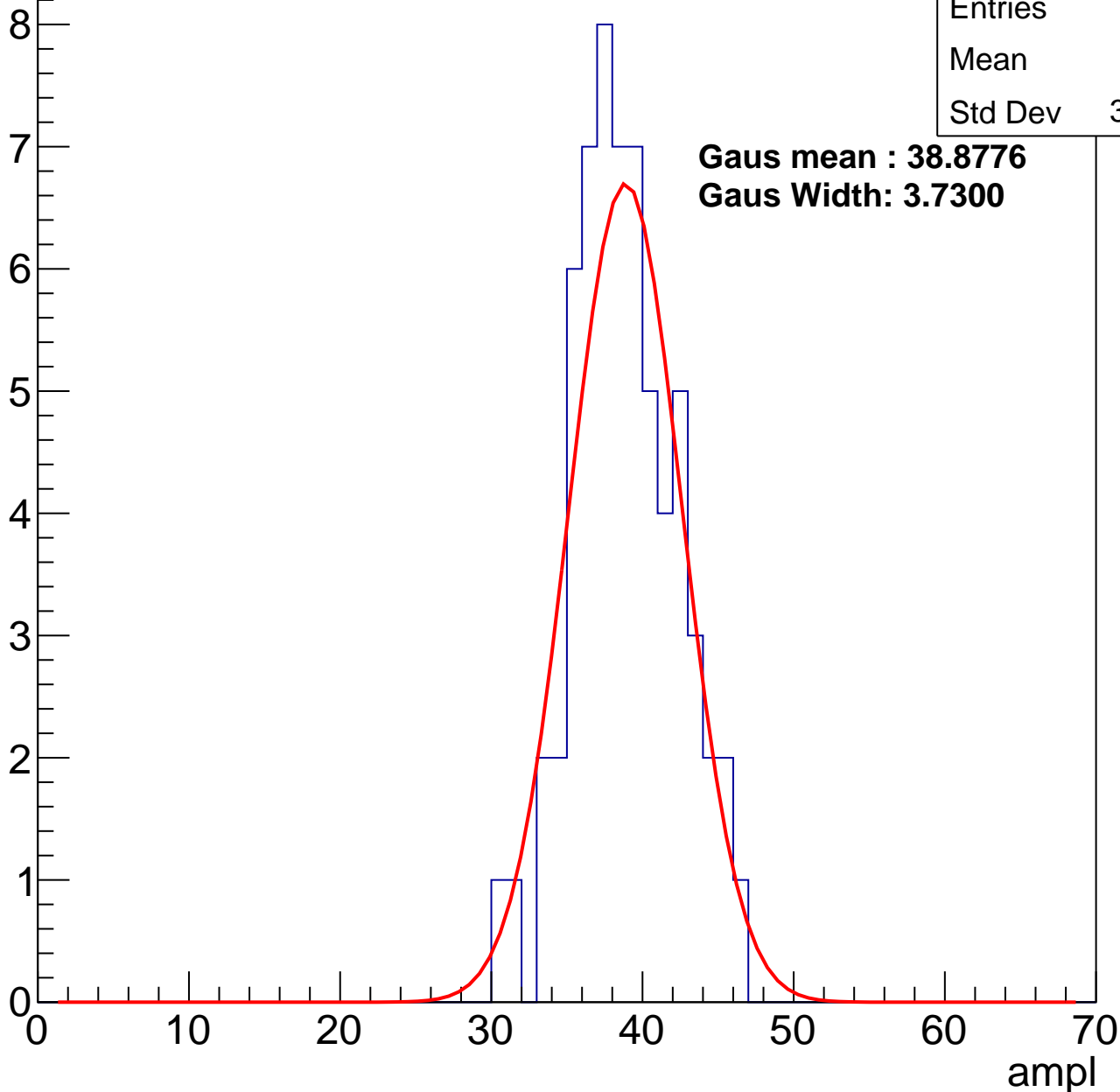
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.4
Std Dev	3.397

Gaus mean : 38.8776

Gaus Width: 3.7300



B0L002S, U9-ch16, adc2

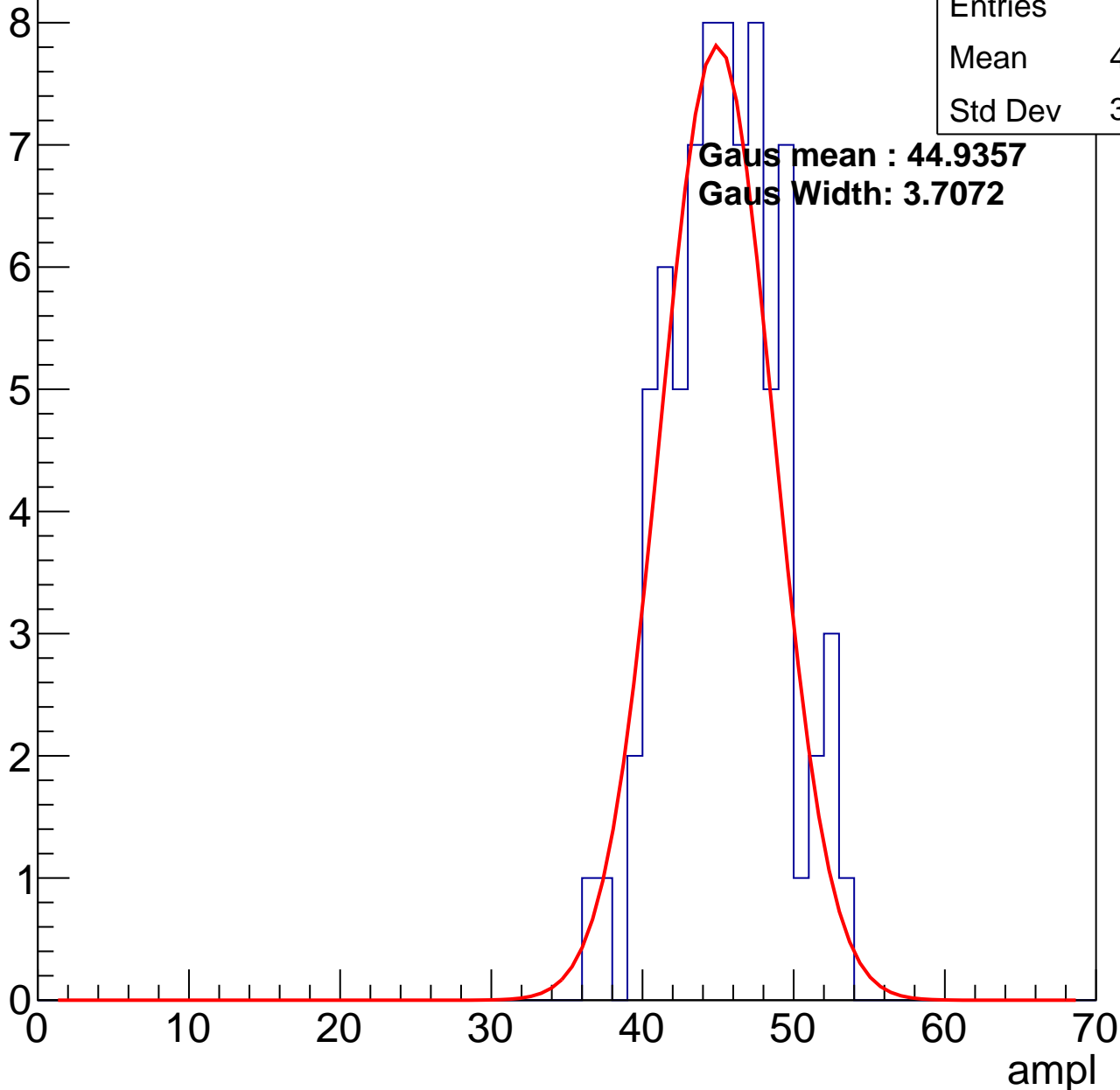
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	44.96
Std Dev	3.645

Gaus mean : 44.9357

Gaus Width: 3.7072

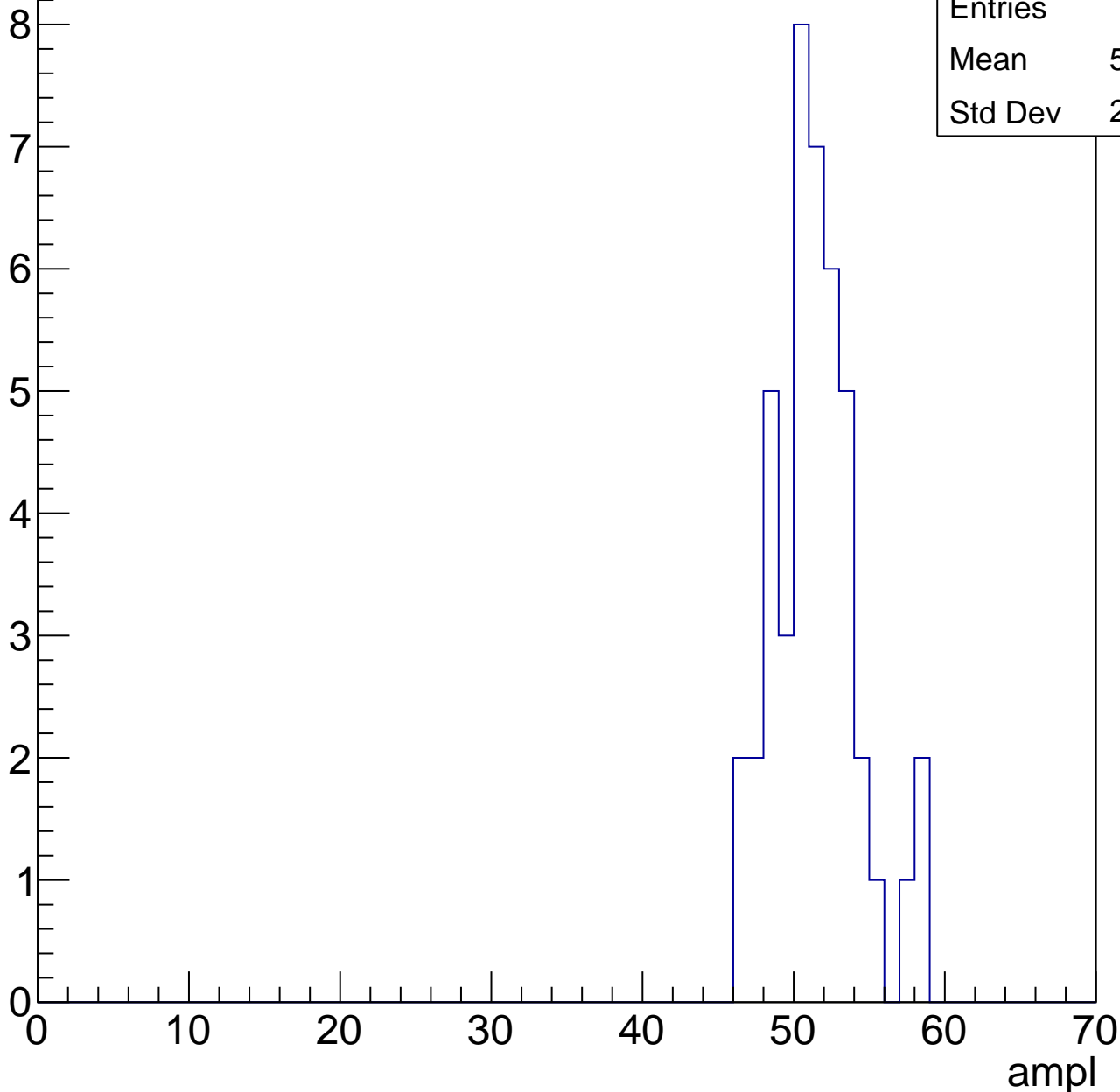


B0L002S, U9-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	50.98
Std Dev	2.784



B0L002S, U9-ch16, adc4

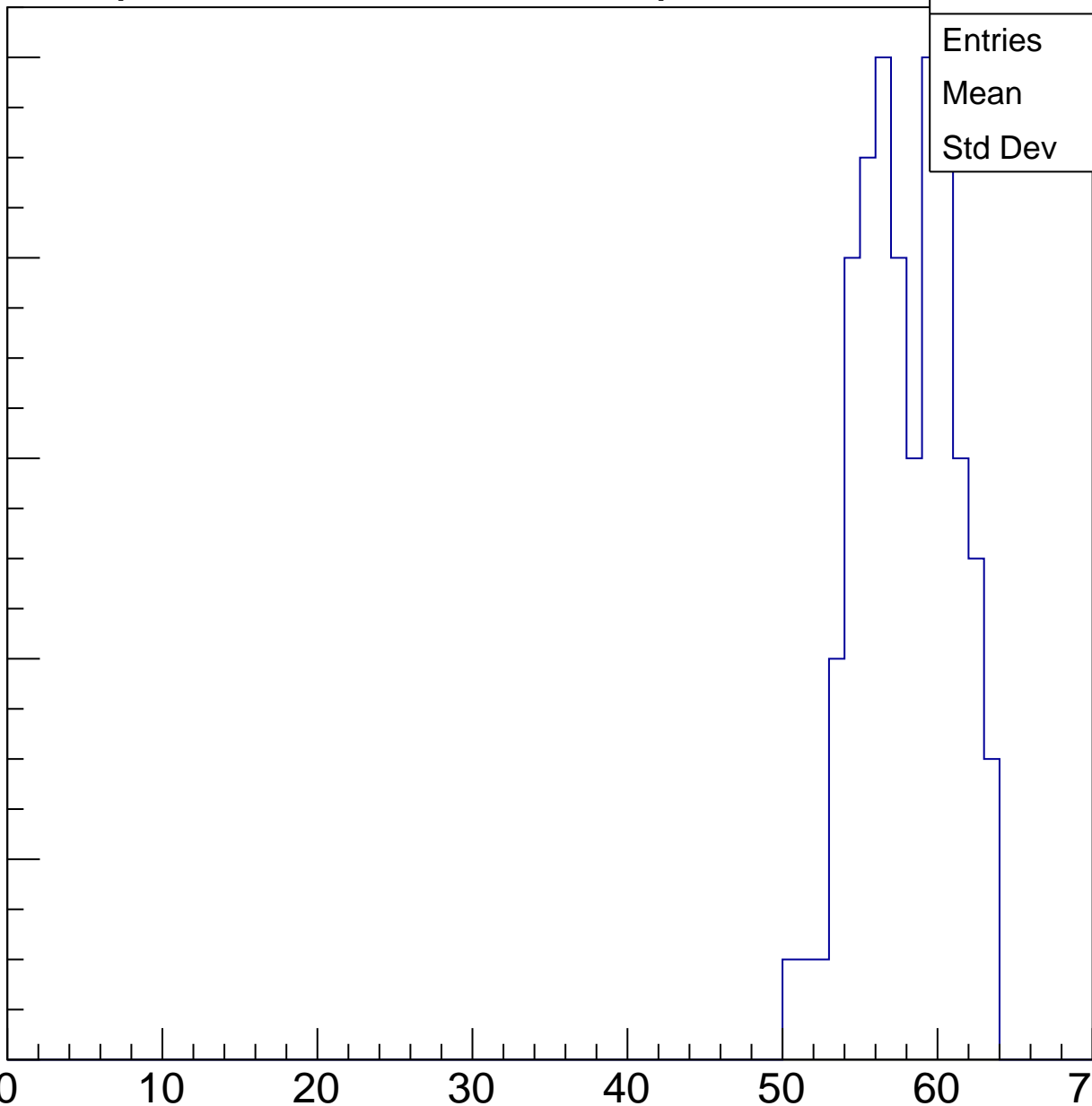
calib_packv5_042523_0143.root, FC#8, port C1

Entry

10
8
6
4
2
0

Entries	81
Mean	57.42
Std Dev	3.01

ampl

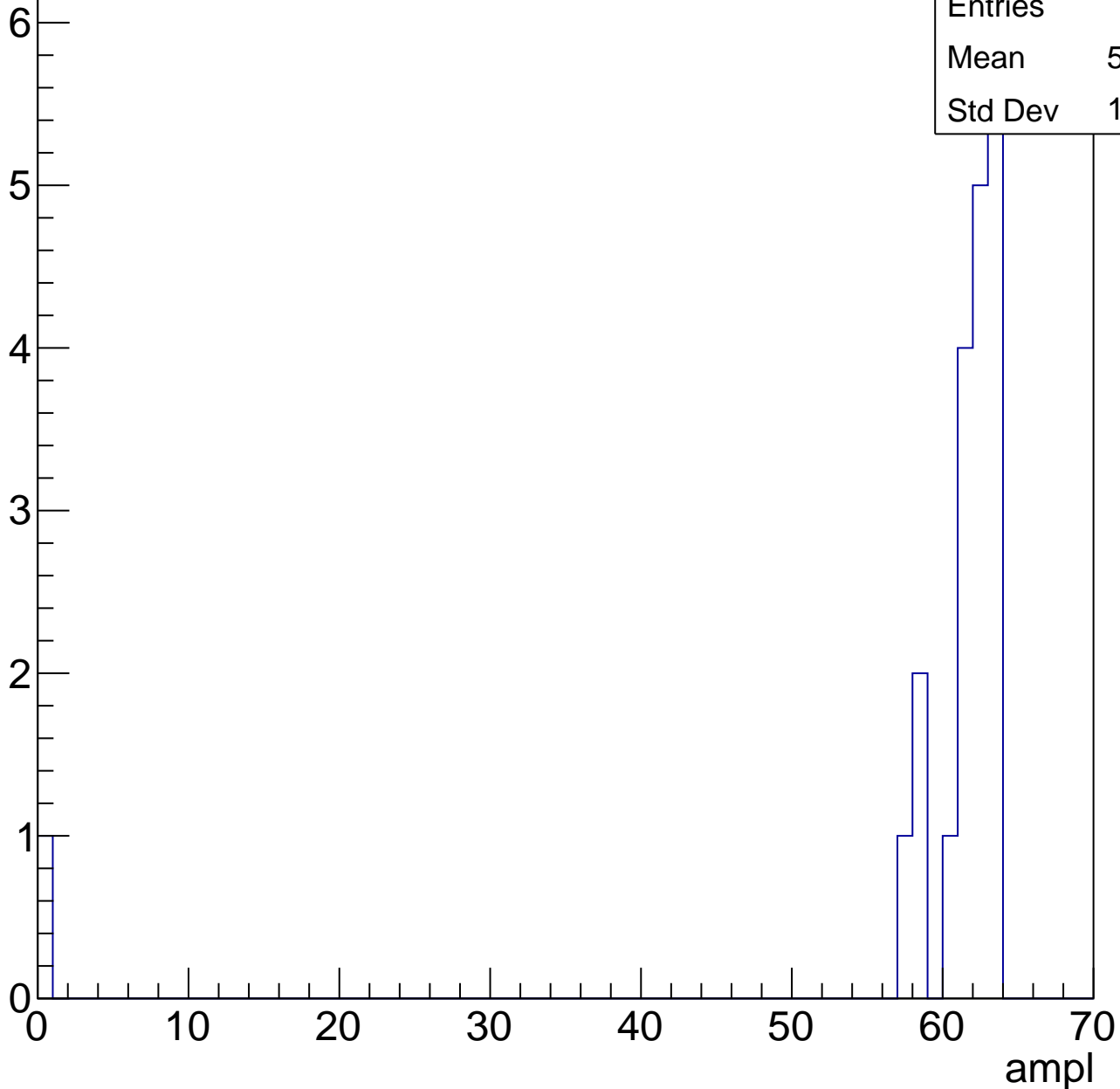


B0L002S, U9-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

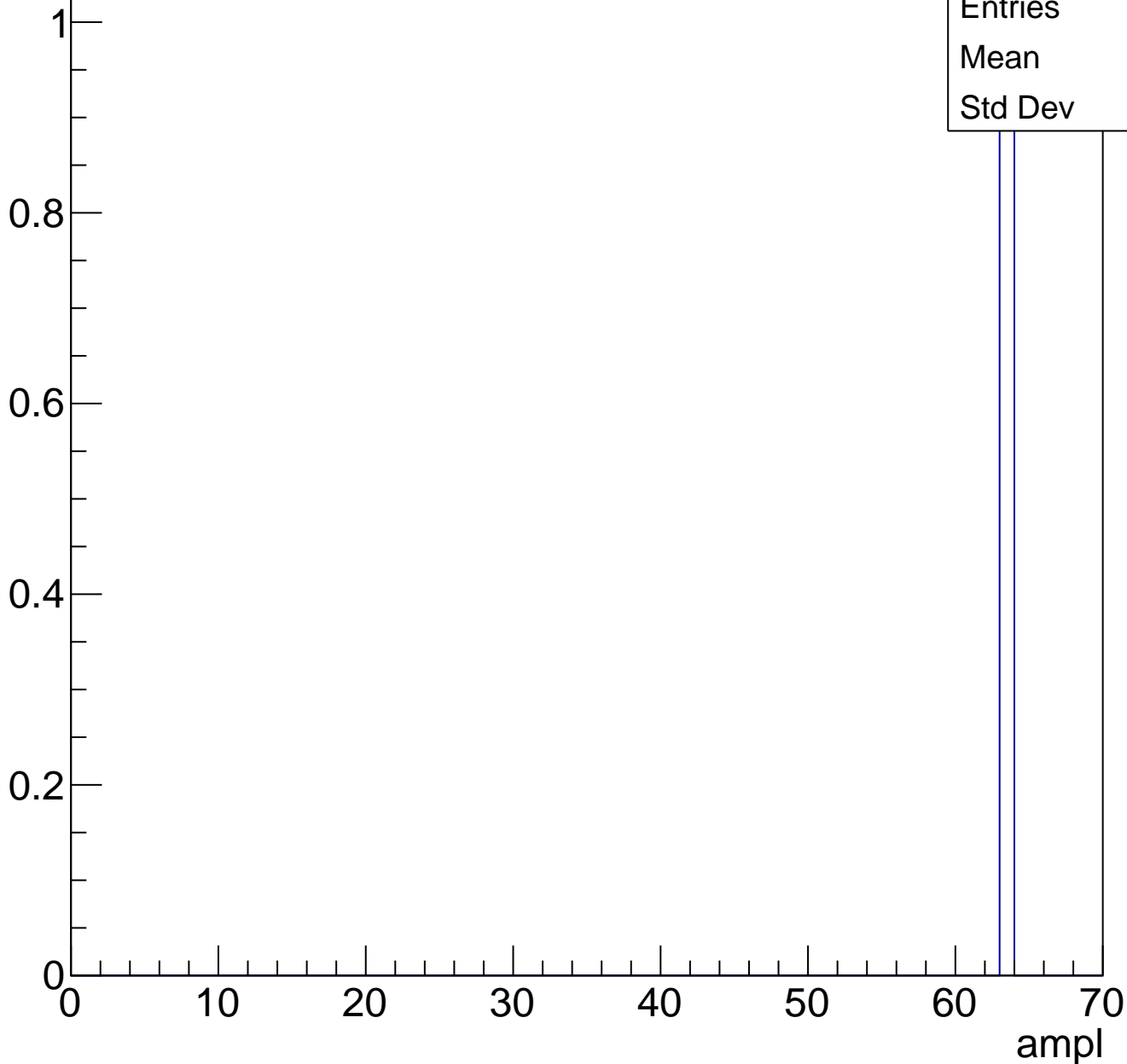
Entries	20
Mean	58.25
Std Dev	13.48



B0L002S, U9-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U9-ch17, adc0

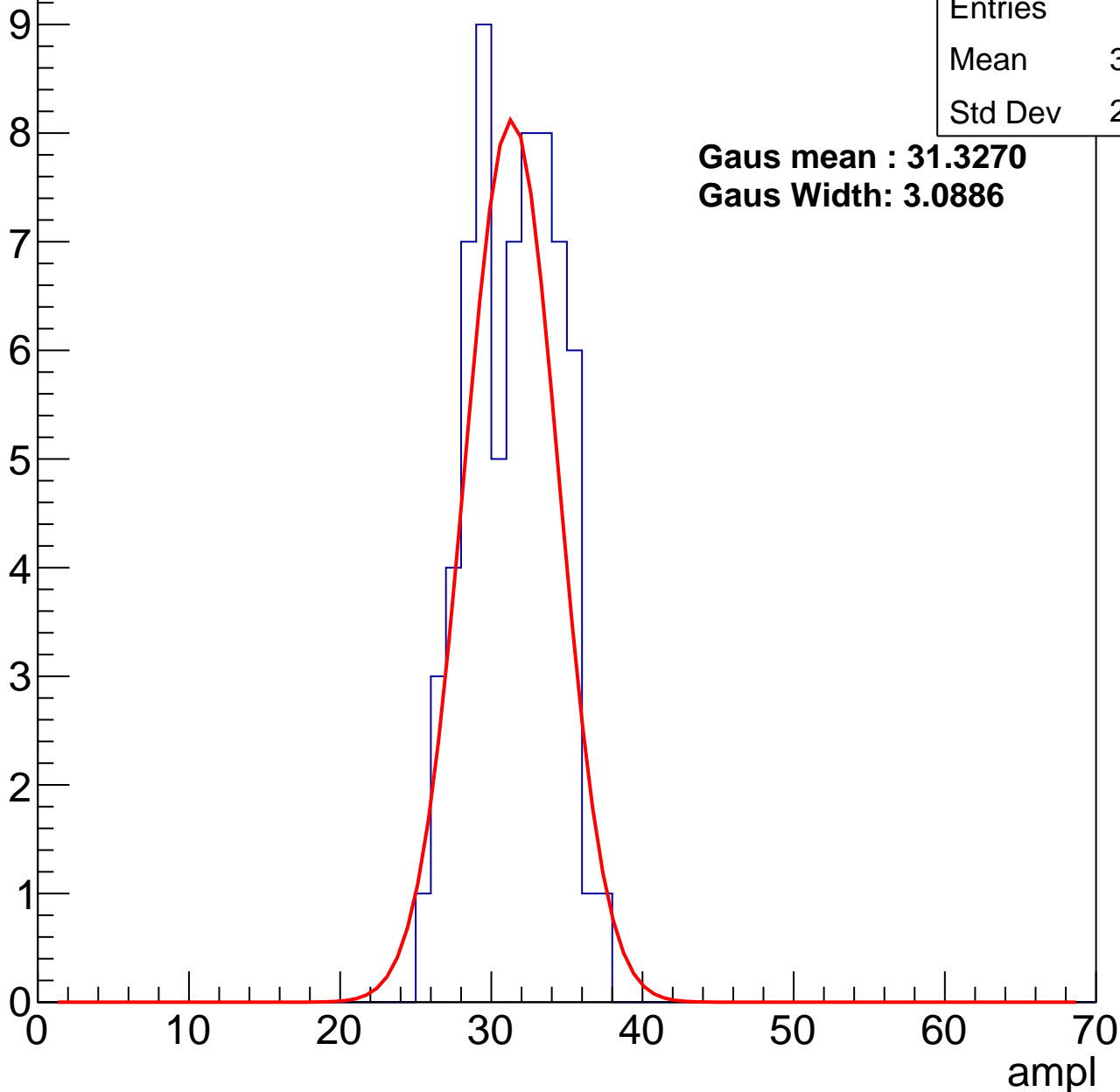
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.99
Std Dev	2.836

Gaus mean : 31.3270

Gaus Width: 3.0886



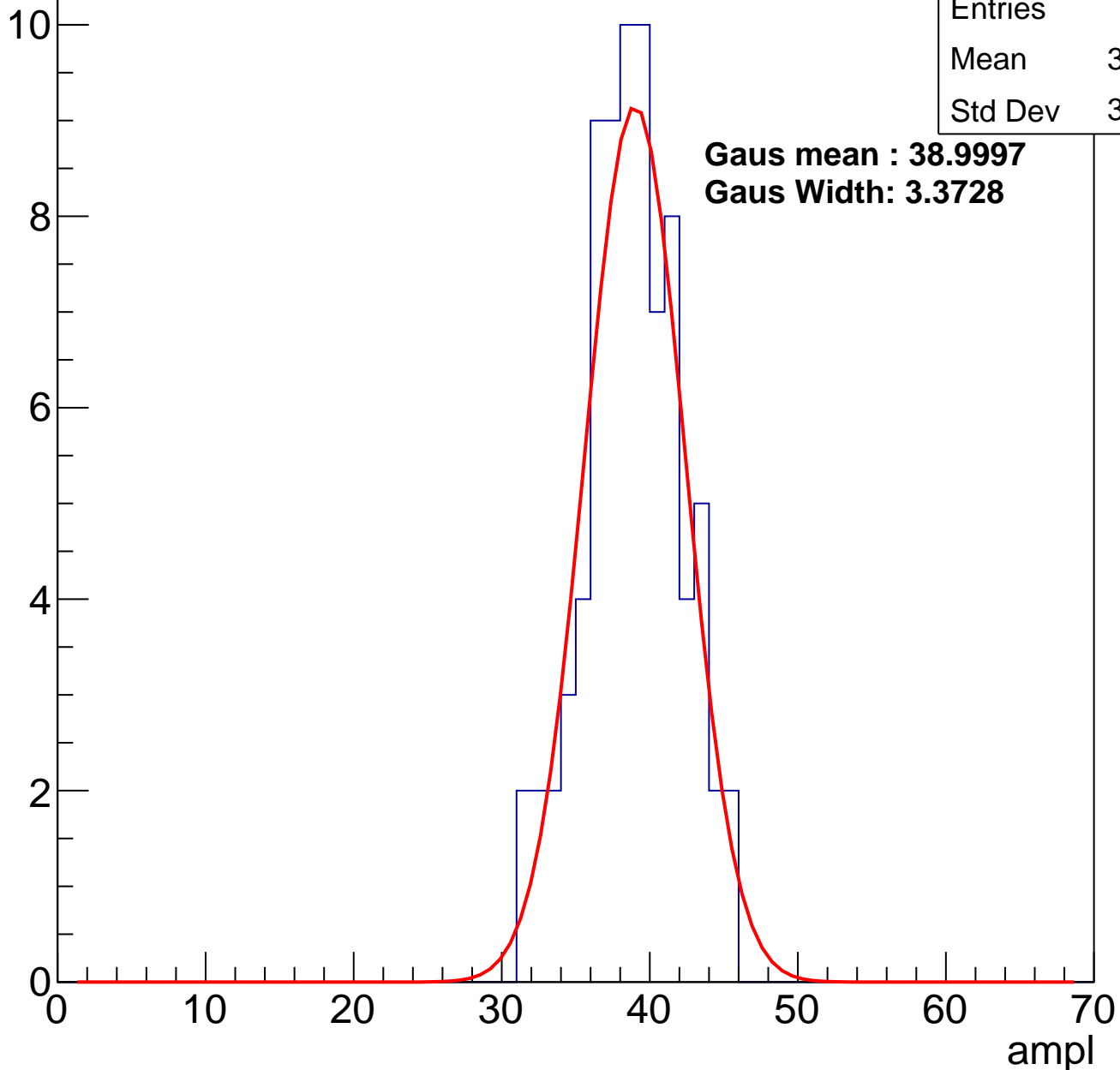
B0L002S, U9-ch17, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	38.35
Std Dev	3.198

Gaus mean : 38.9997
Gaus Width: 3.3728

Entry



B0L002S, U9-ch17, adc2

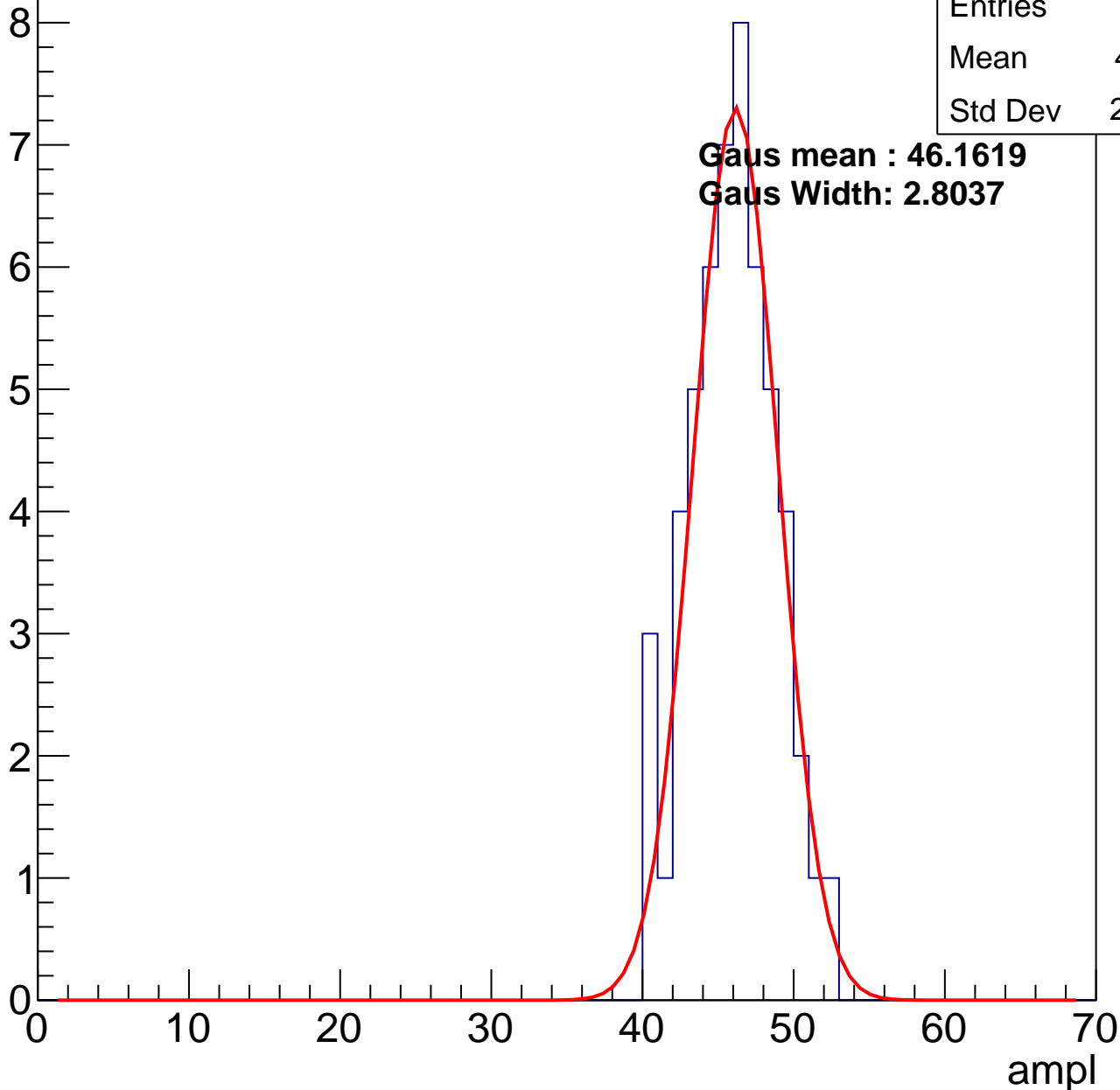
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	45.51
Std Dev	2.799

Gaus mean : 46.1619

Gaus Width: 2.8037

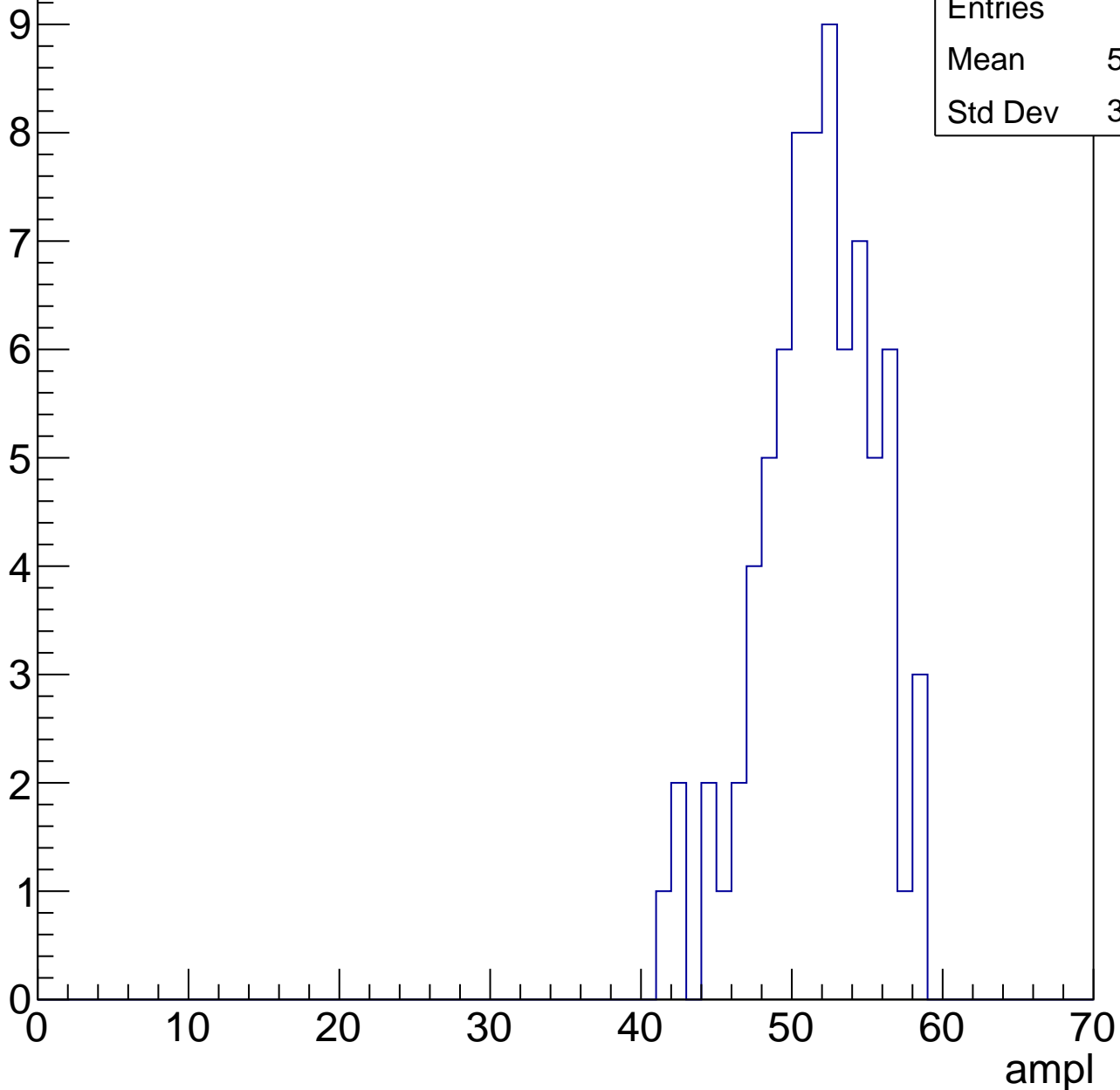


B0L002S, U9-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

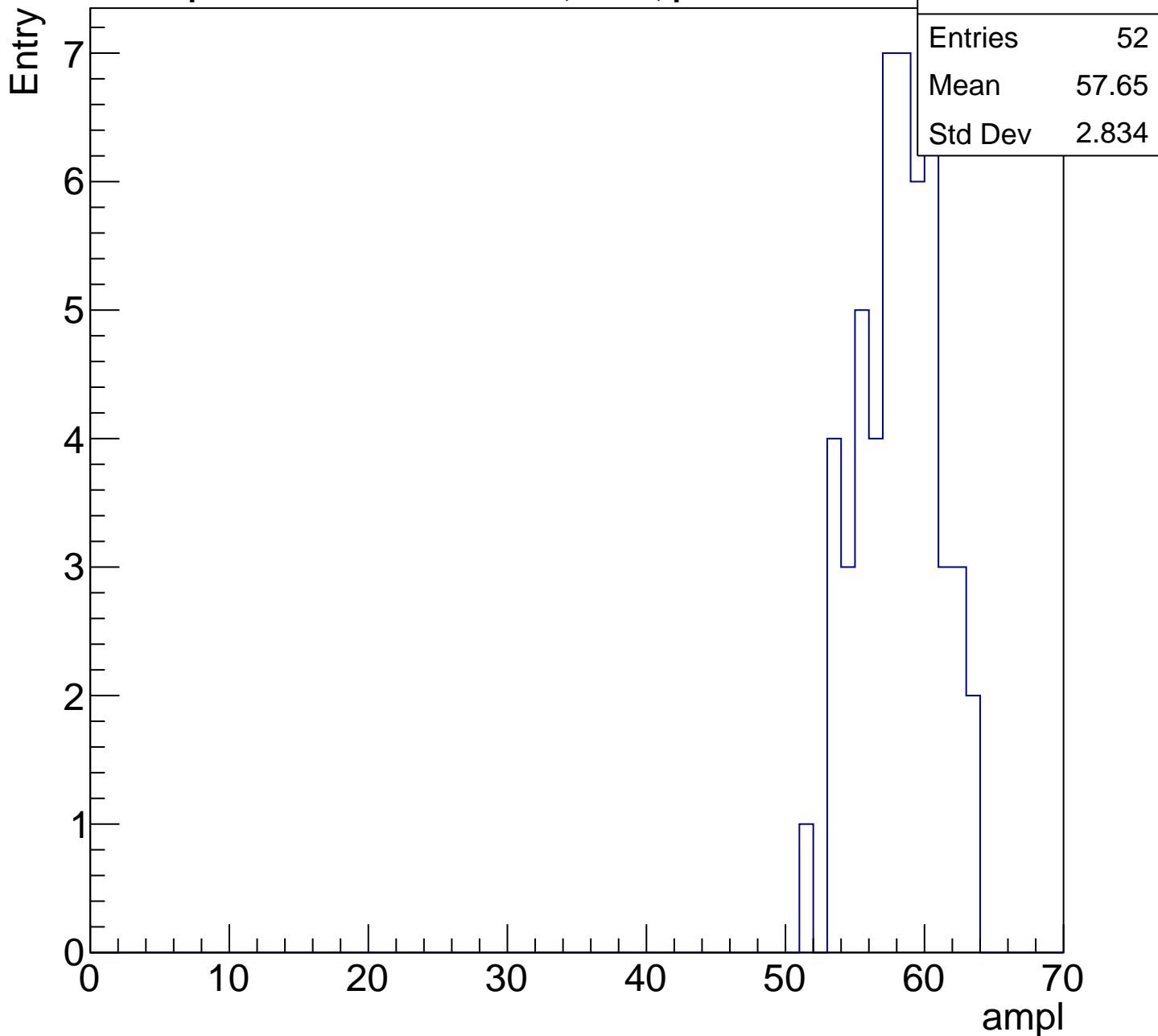
Entry

Entries	76
Mean	51.13
Std Dev	3.795



B0L002S, U9-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

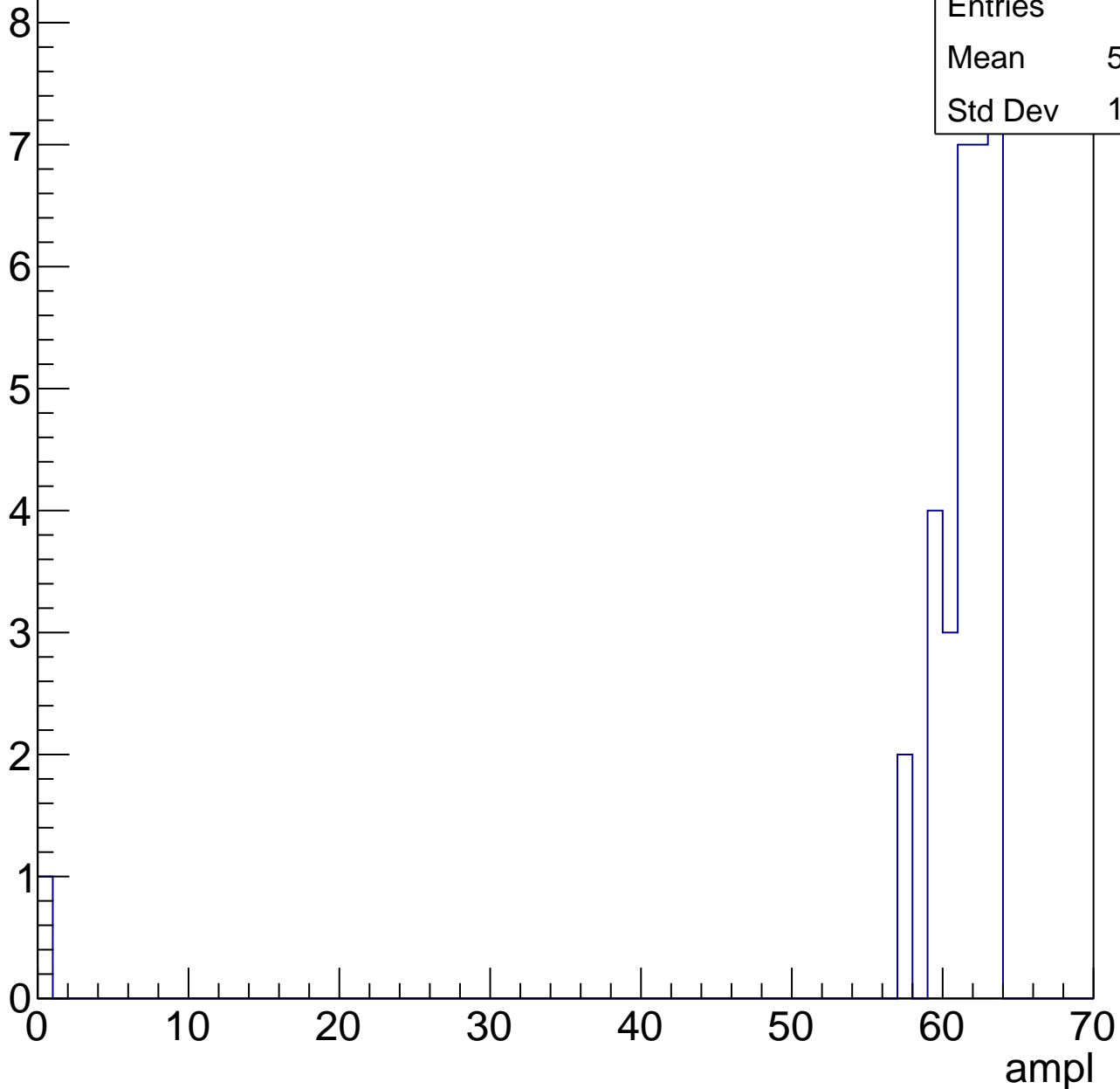


B0L002S, U9-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59.22
Std Dev	10.77



B0L002S, U9-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U9-ch18, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	96
Mean	30.48
Std Dev	4.67

Gaus mean : 31.2383

Gaus Width: 3.5901

10

8

6

4

2

0

ampl

0

10

20

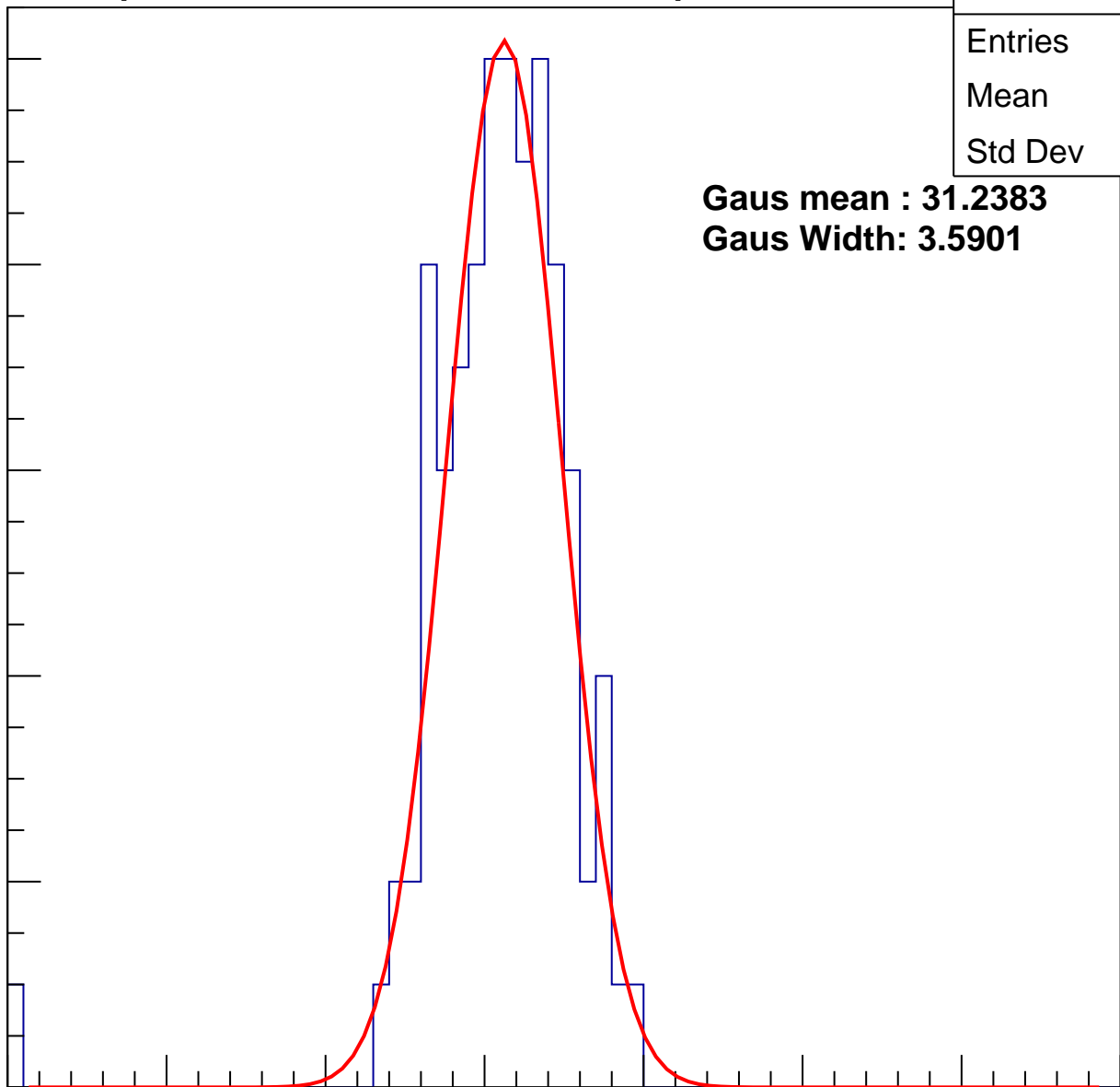
30

40

50

60

70



B0L002S, U9-ch18, adc1

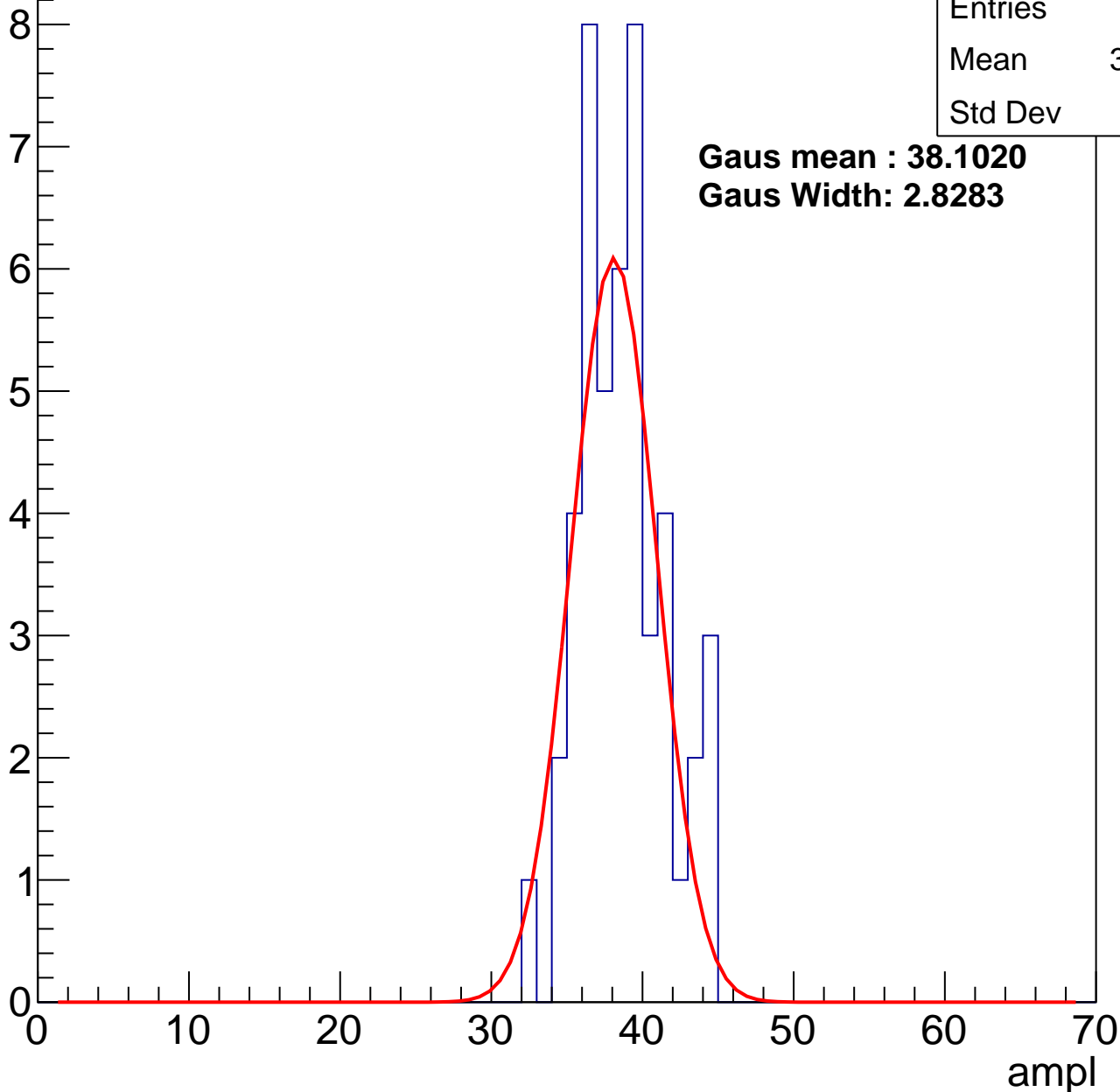
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	38.23
Std Dev	2.8

Gaus mean : 38.1020

Gaus Width: 2.8283



B0L002S, U9-ch18, adc2

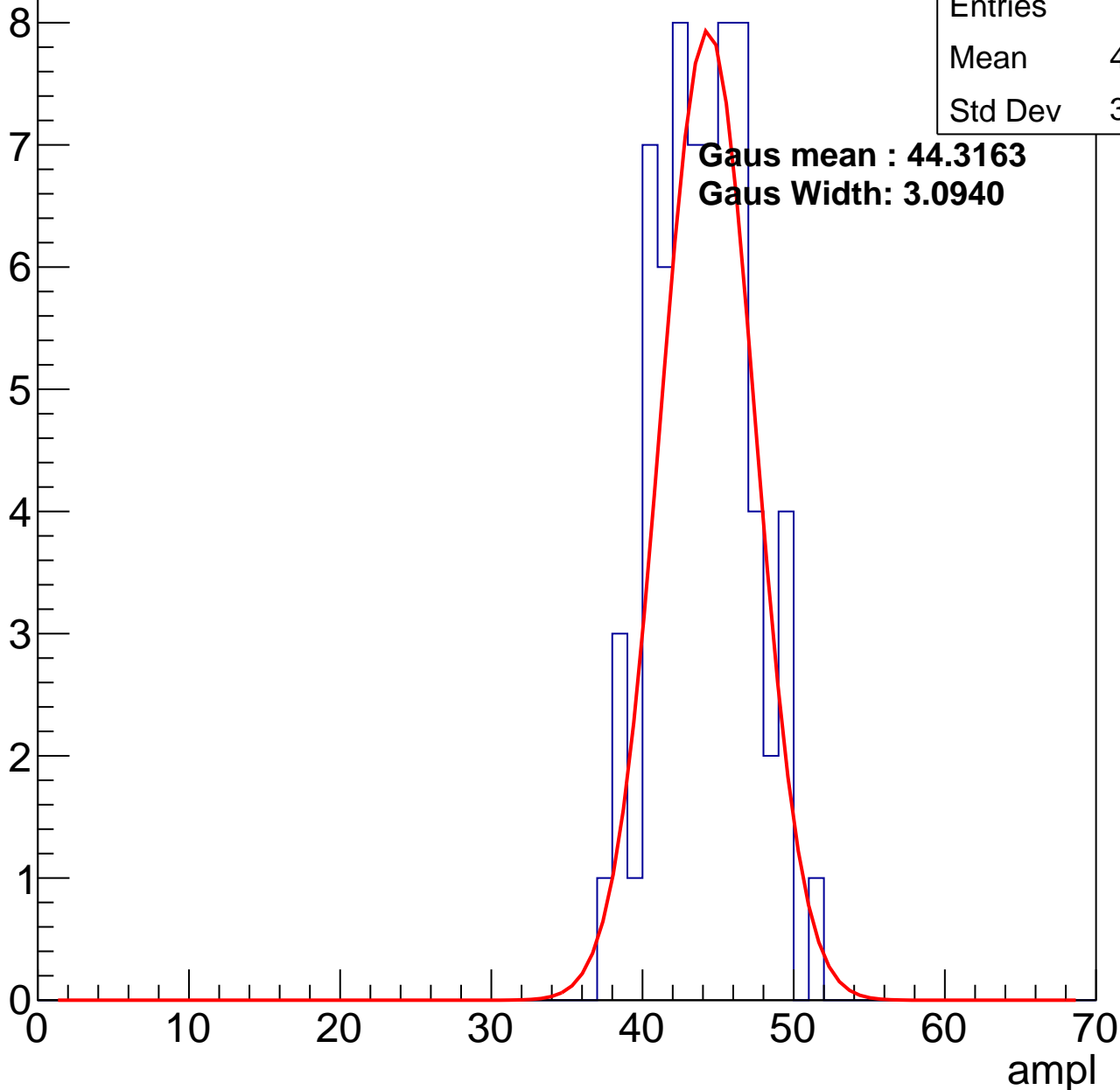
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.58
Std Dev	3.072

Gaus mean : 44.3163

Gaus Width: 3.0940



B0L002S, U9-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

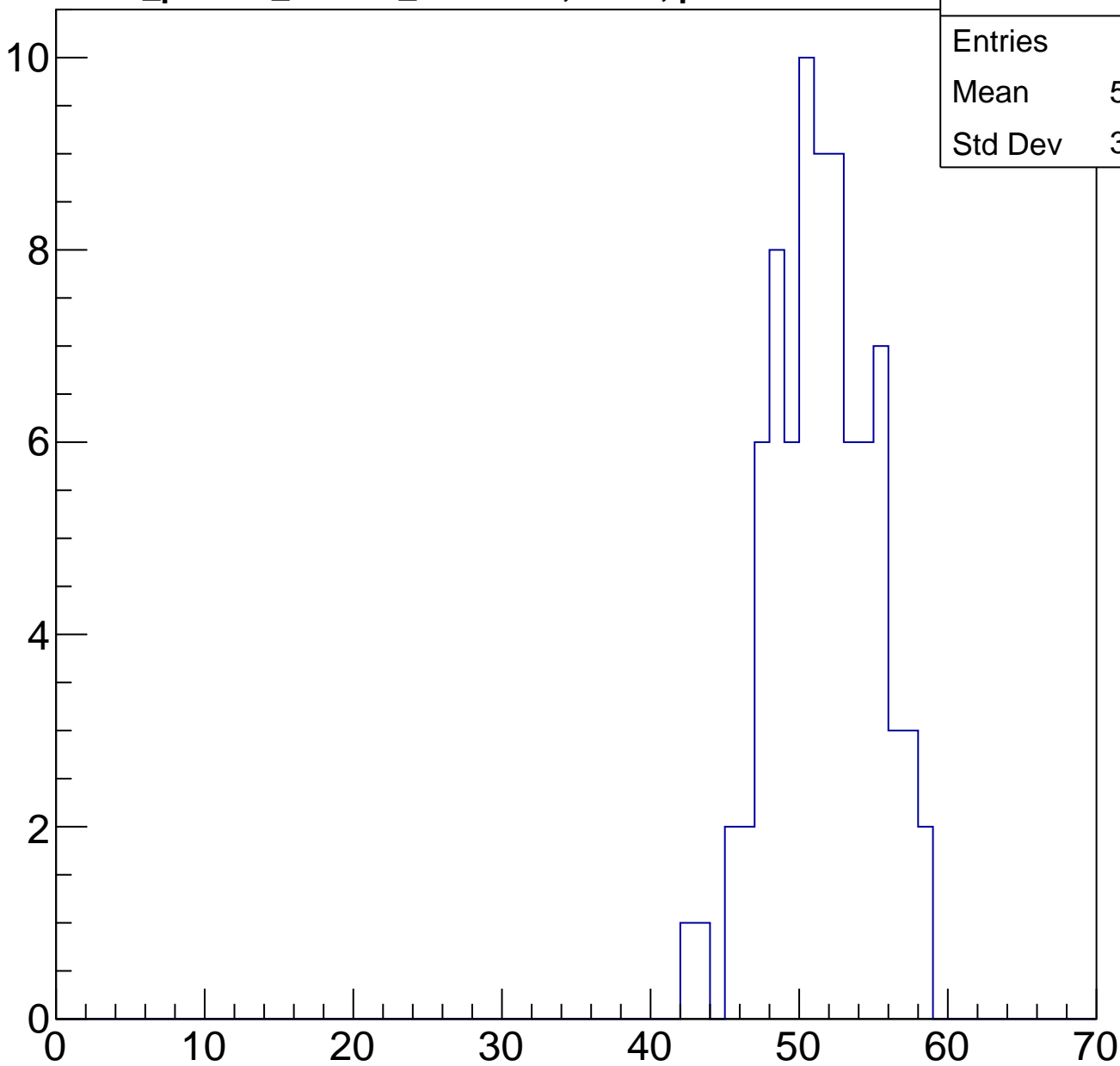
Entries	81
Mean	51.06
Std Dev	3.422

Entry

10
8
6
4
2
0

ampl

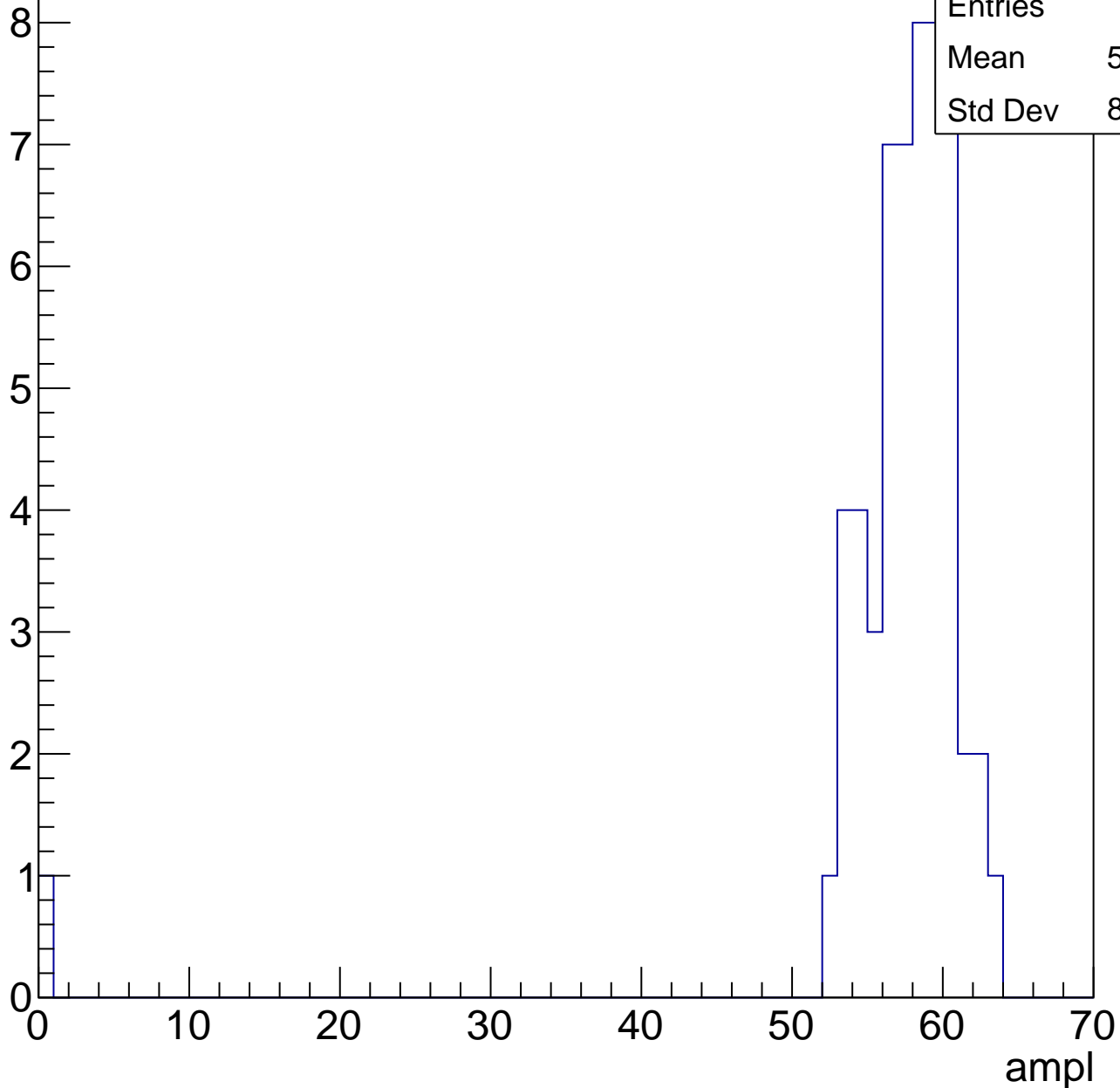
0 10 20 30 40 50 60 70



B0L002S, U9-ch18, adc4

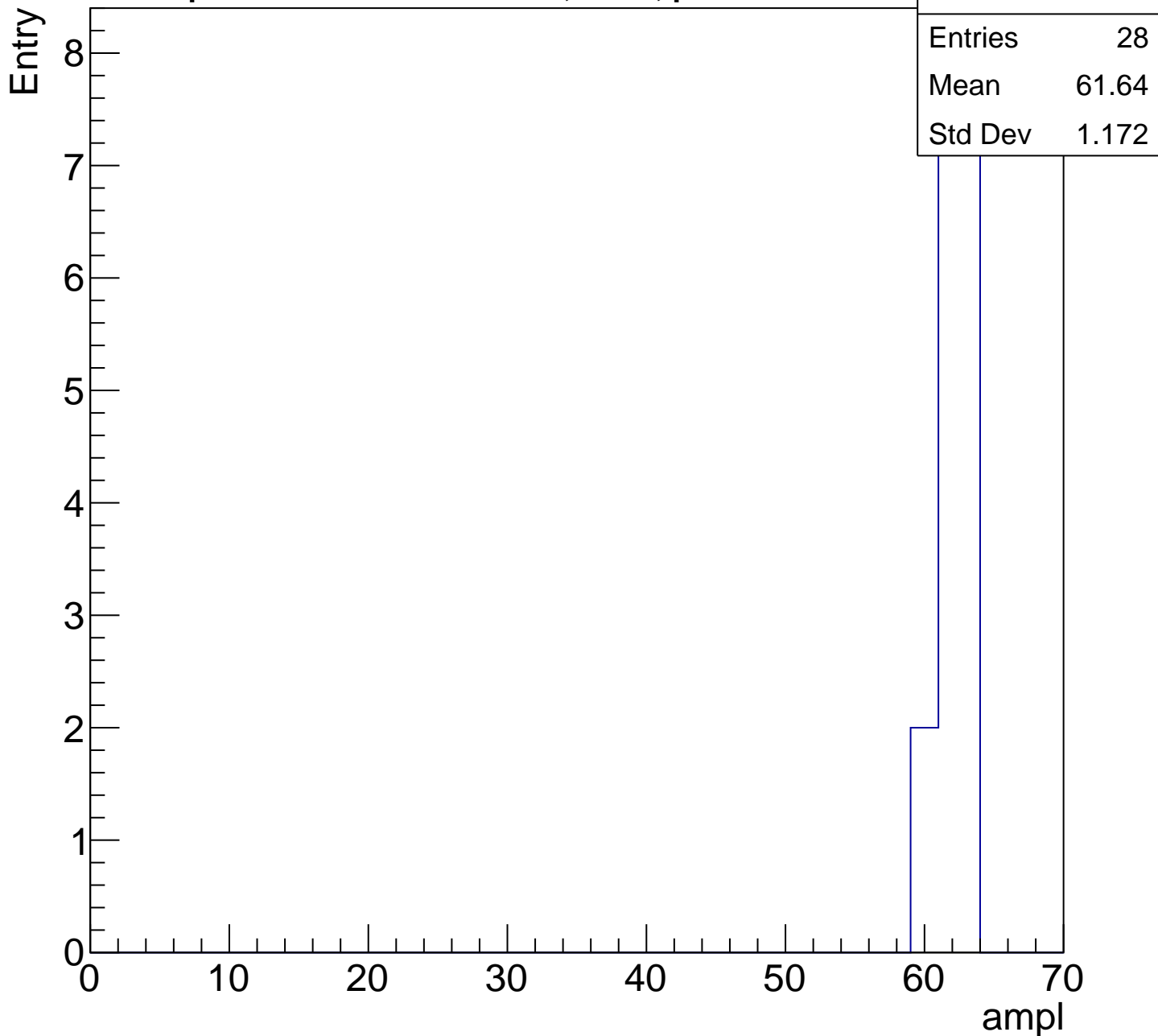
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch19, adc0

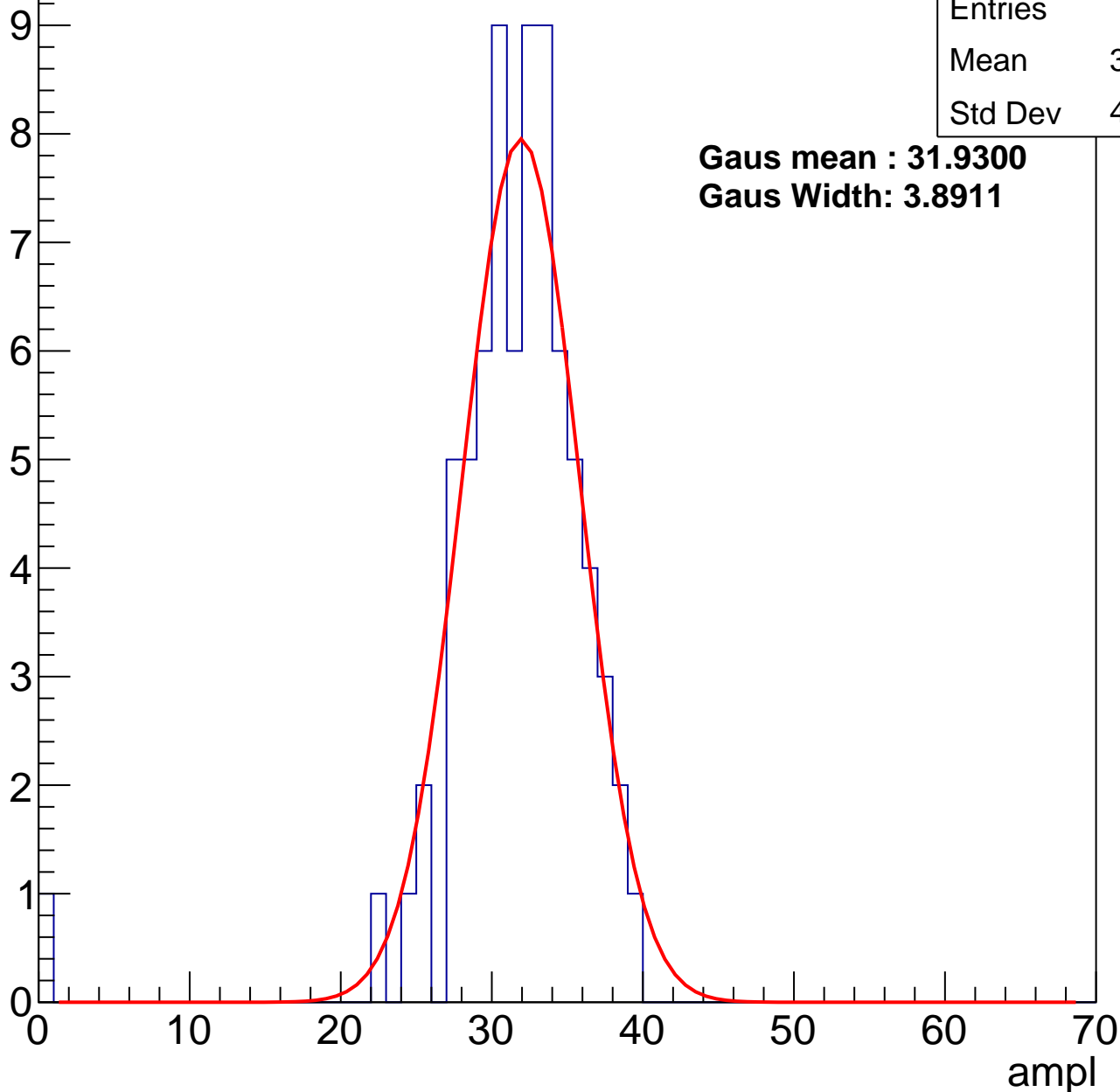
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.13
Std Dev	4.992

Gaus mean : 31.9300

Gaus Width: 3.8911



B0L002S, U9-ch19, adc1

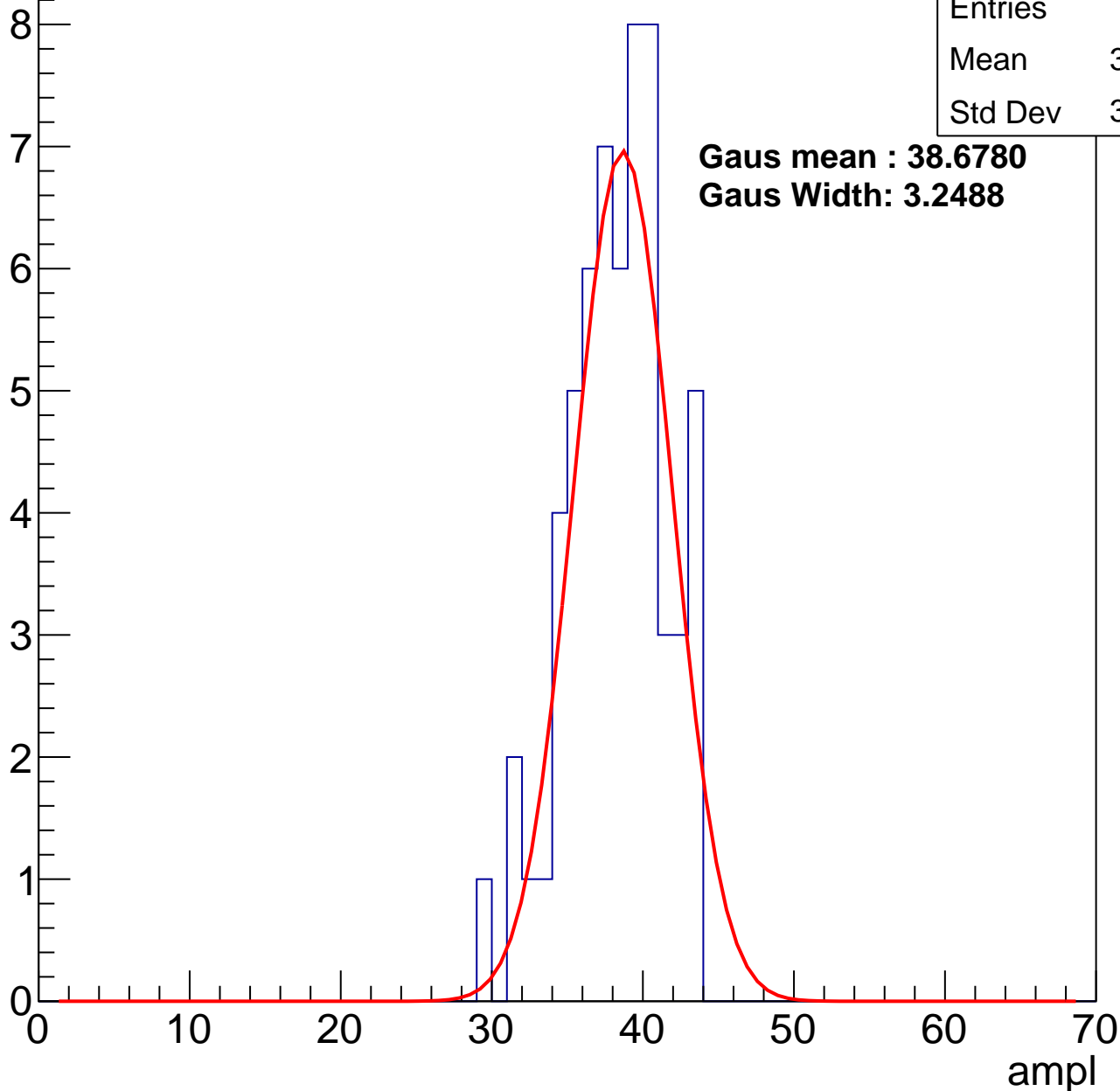
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	37.77
Std Dev	3.206

Gaus mean : 38.6780

Gaus Width: 3.2488



B0L002S, U9-ch19, adc2

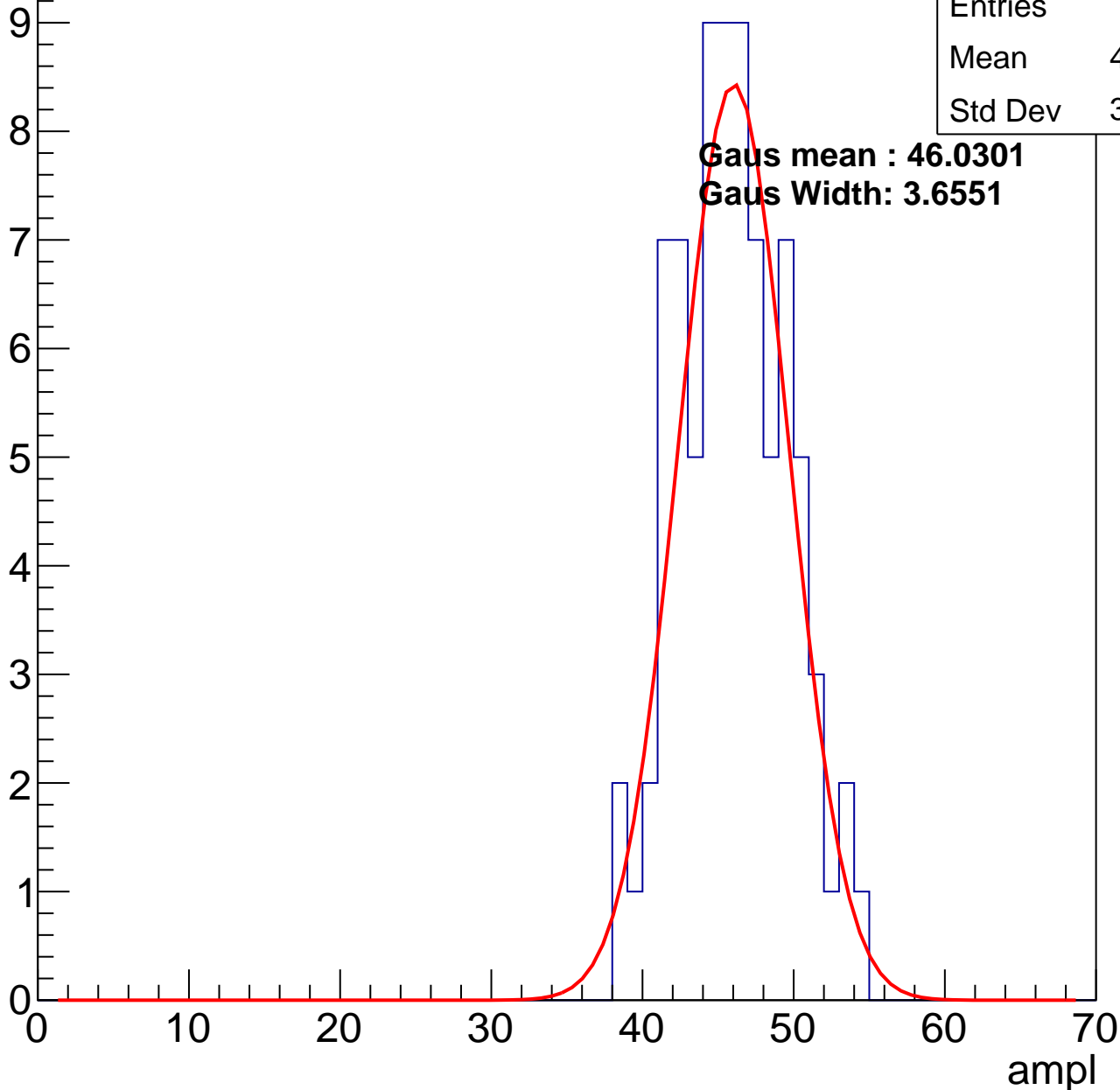
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	45.52
Std Dev	3.579

Gaus mean : 46.0301

Gaus Width: 3.6551

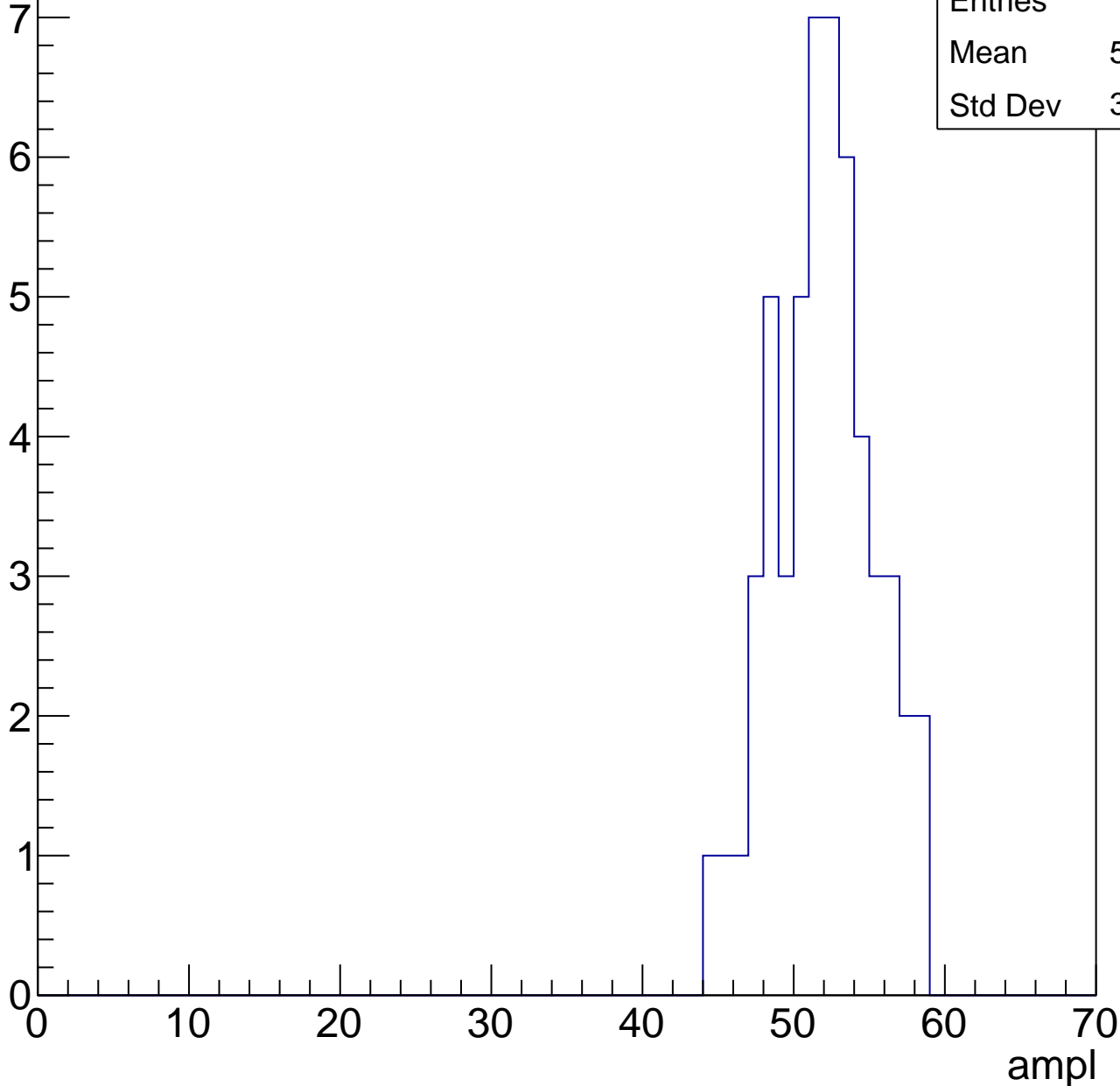


B0L002S, U9-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	51.53
Std Dev	3.266

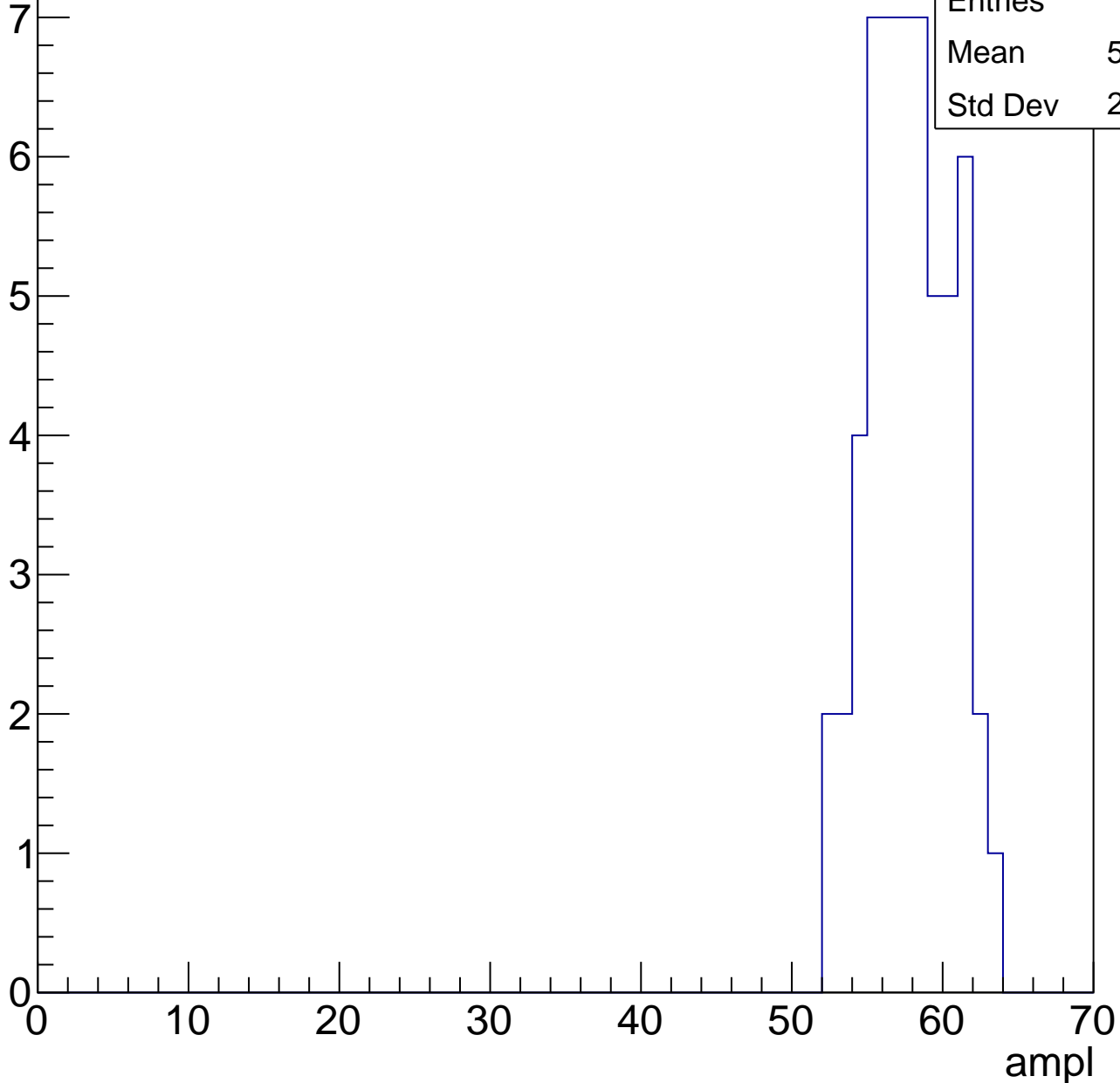


B0L002S, U9-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.38
Std Dev	2.693

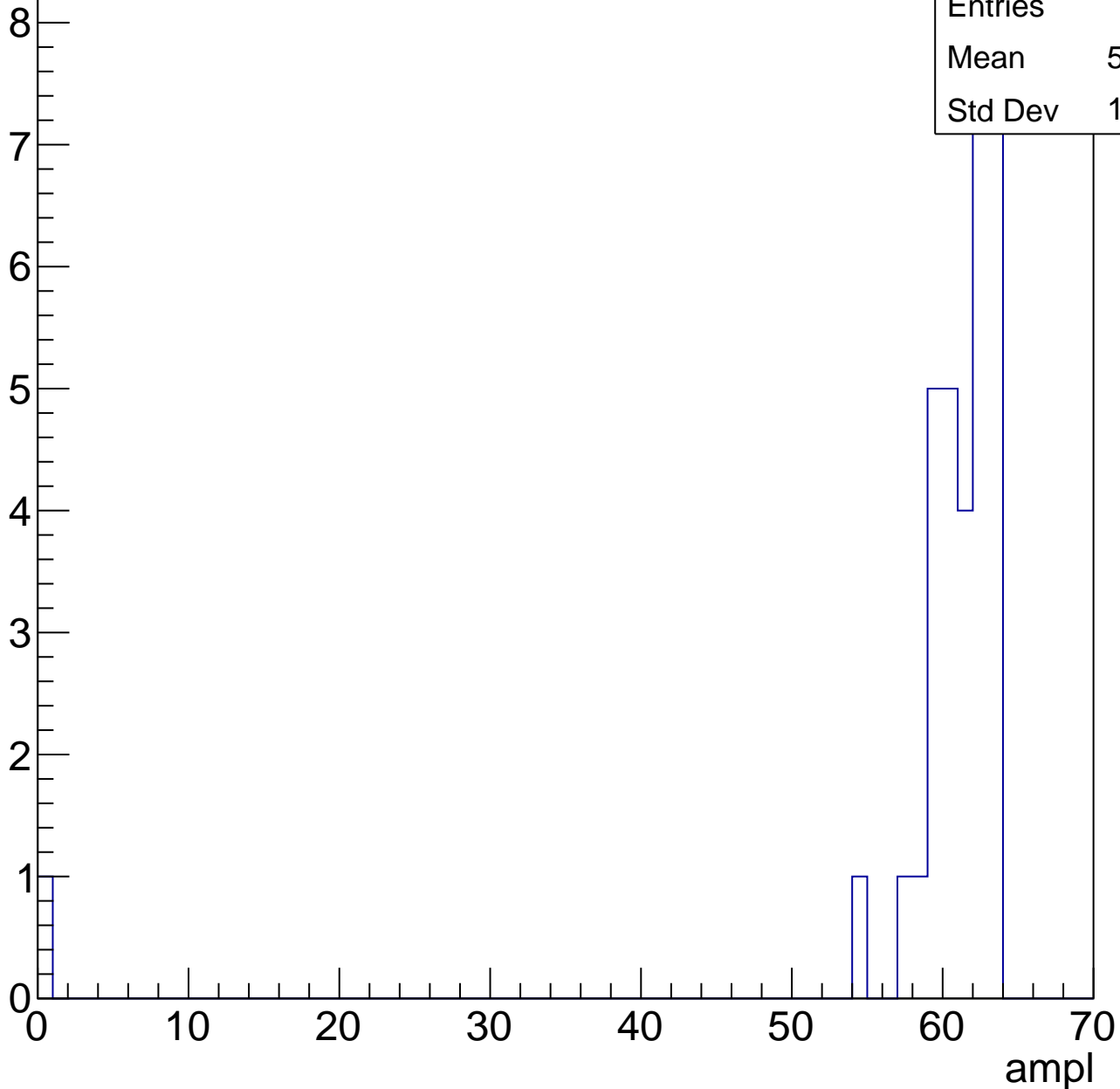


B0L002S, U9-ch19, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.06
Std Dev	10.48



B0L002S, U9-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch20, adc0

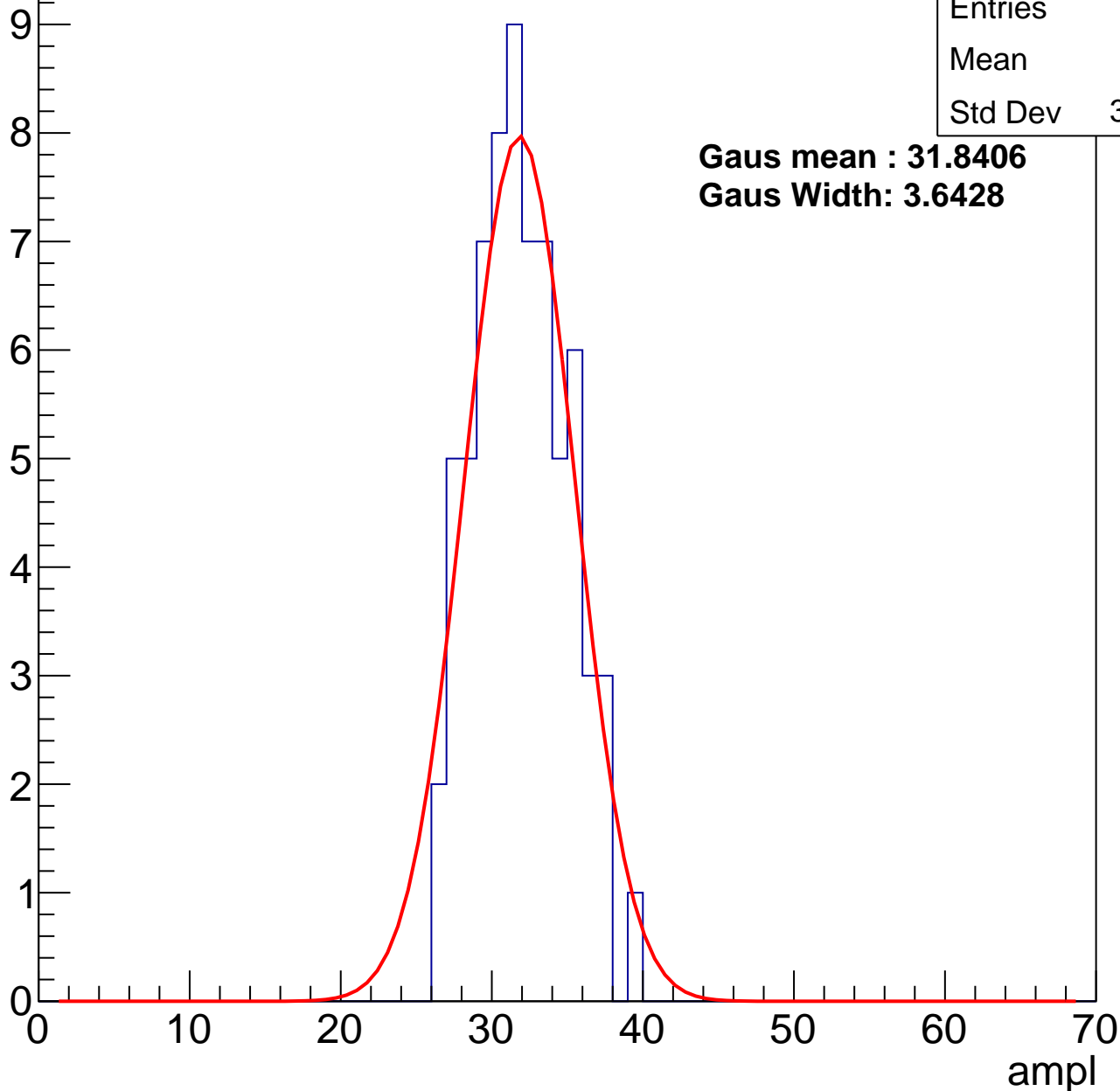
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.5
Std Dev	3.012

Gaus mean : 31.8406

Gaus Width: 3.6428



B0L002S, U9-ch20, adc1

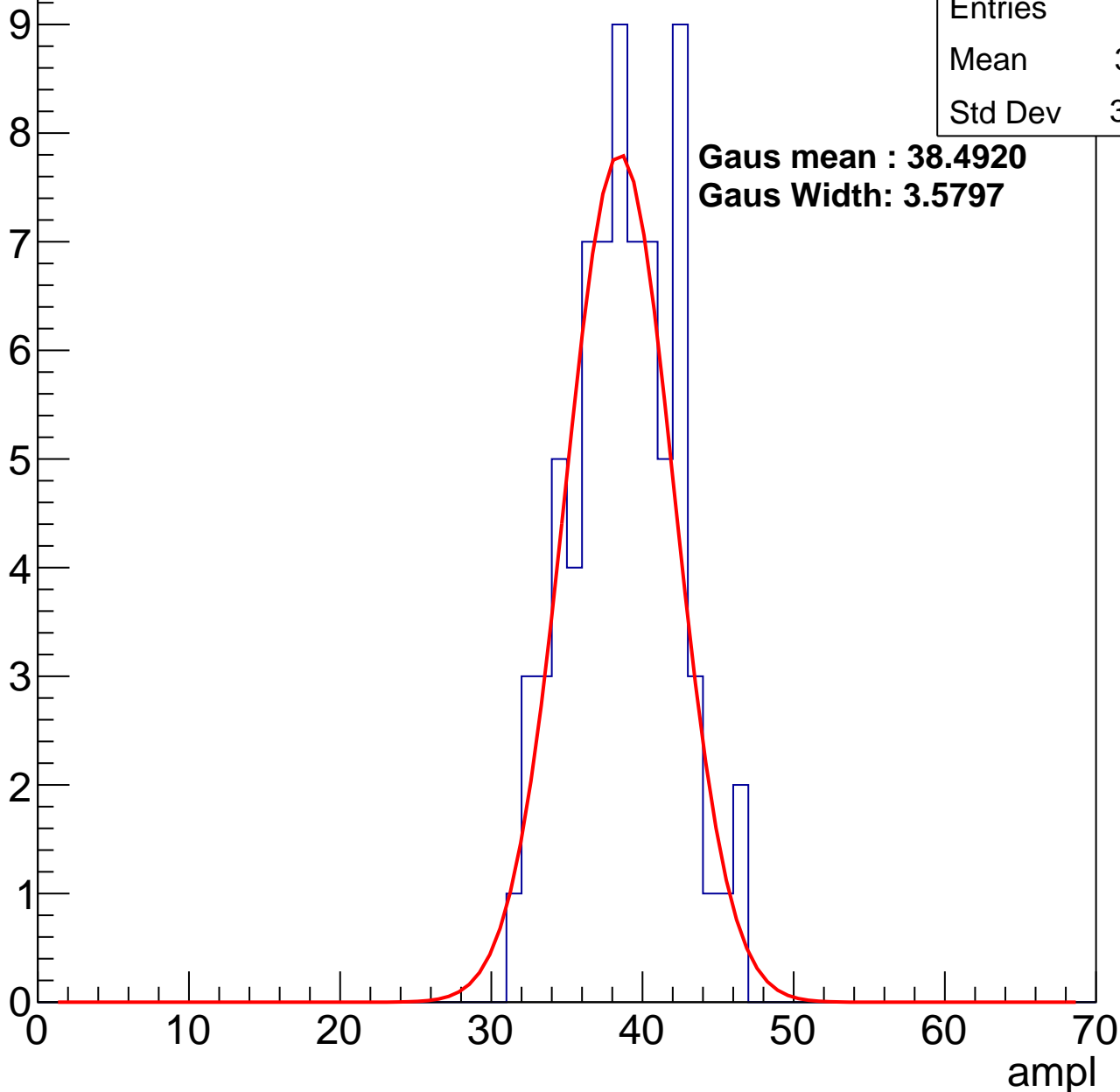
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	38.31
Std Dev	3.464

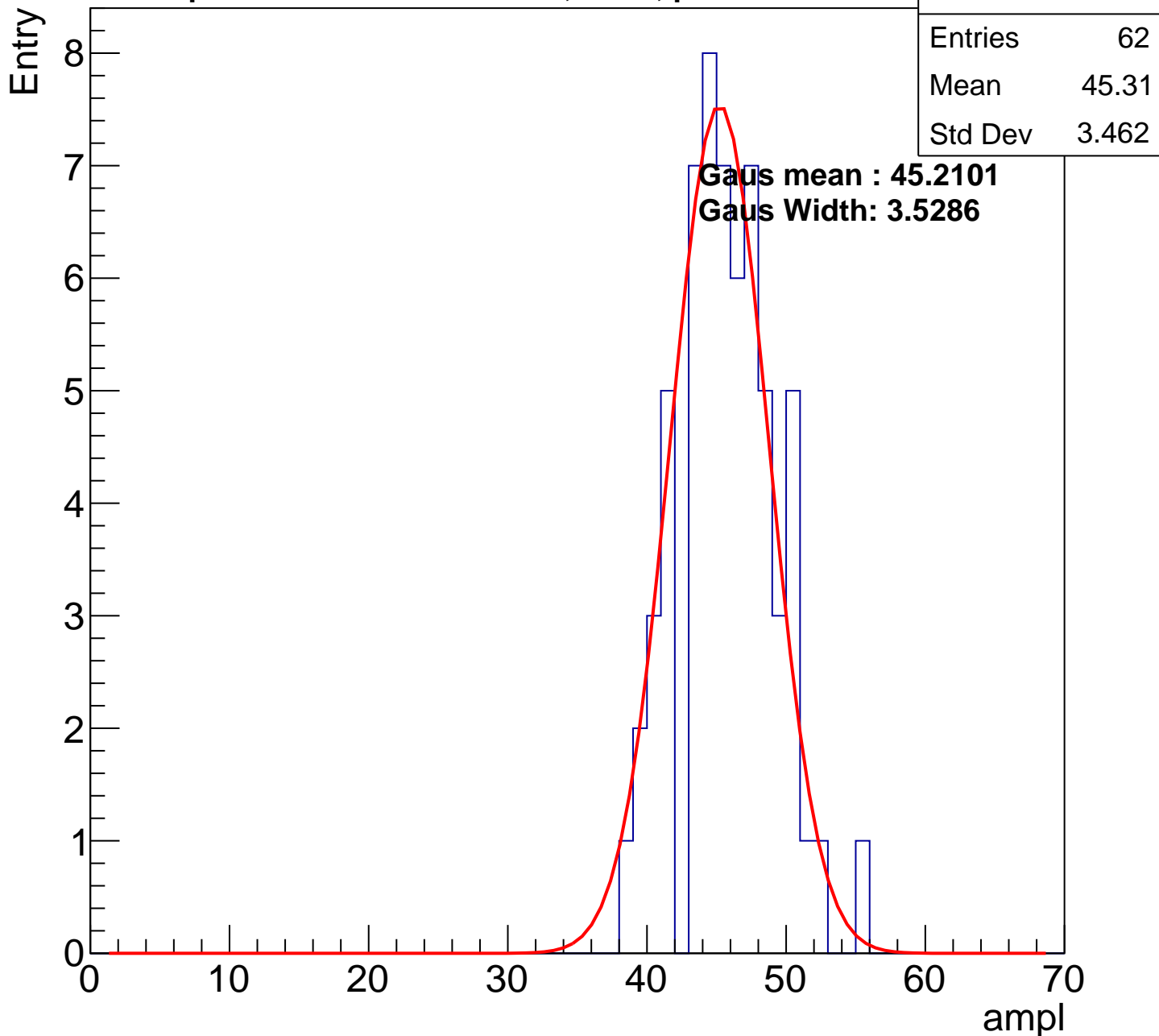
Gaus mean : 38.4920

Gaus Width: 3.5797



B0L002S, U9-ch20, adc2

calib_packv5_042523_0143.root, FC#8, port C1

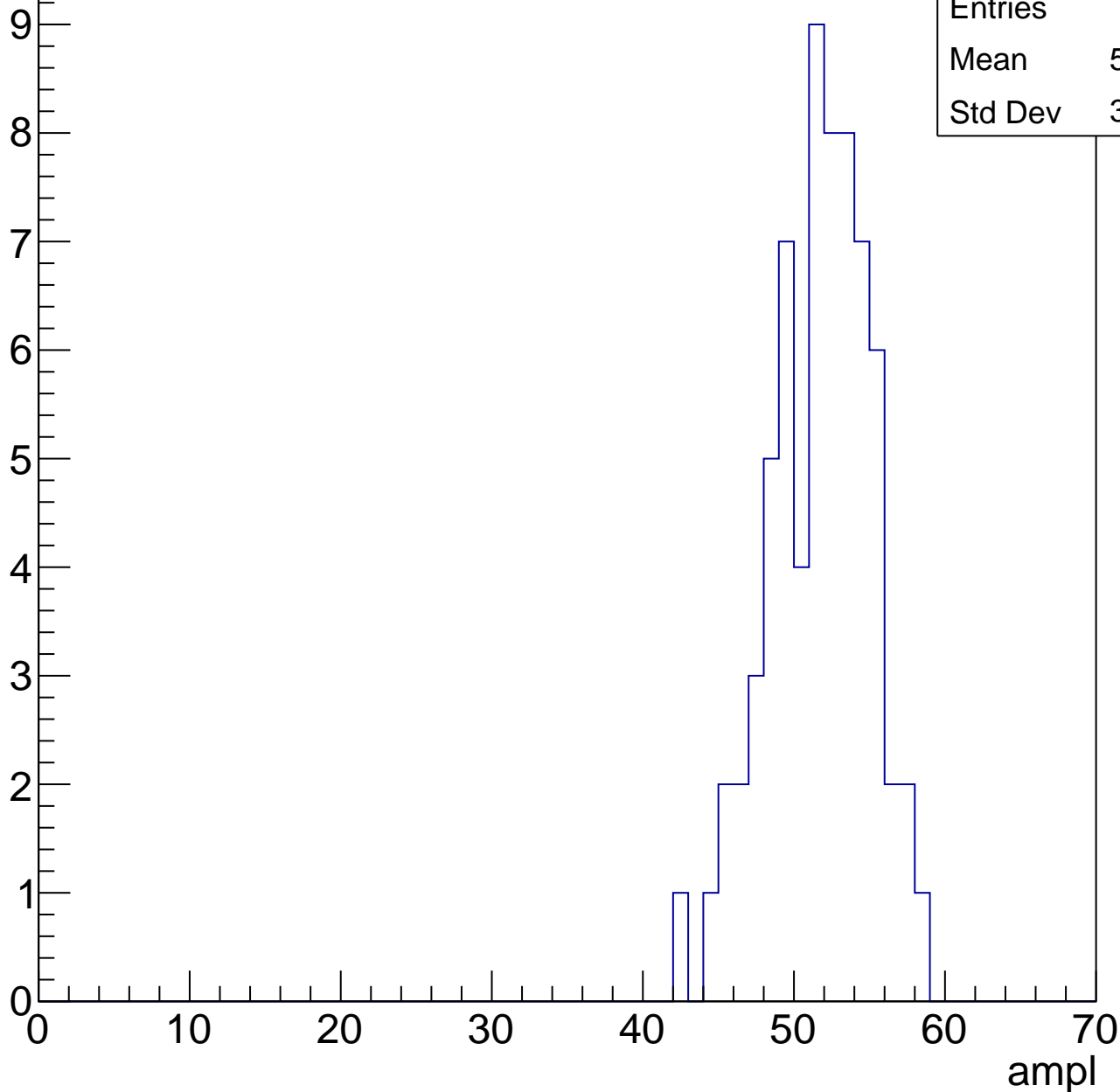


B0L002S, U9-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	51.22
Std Dev	3.307

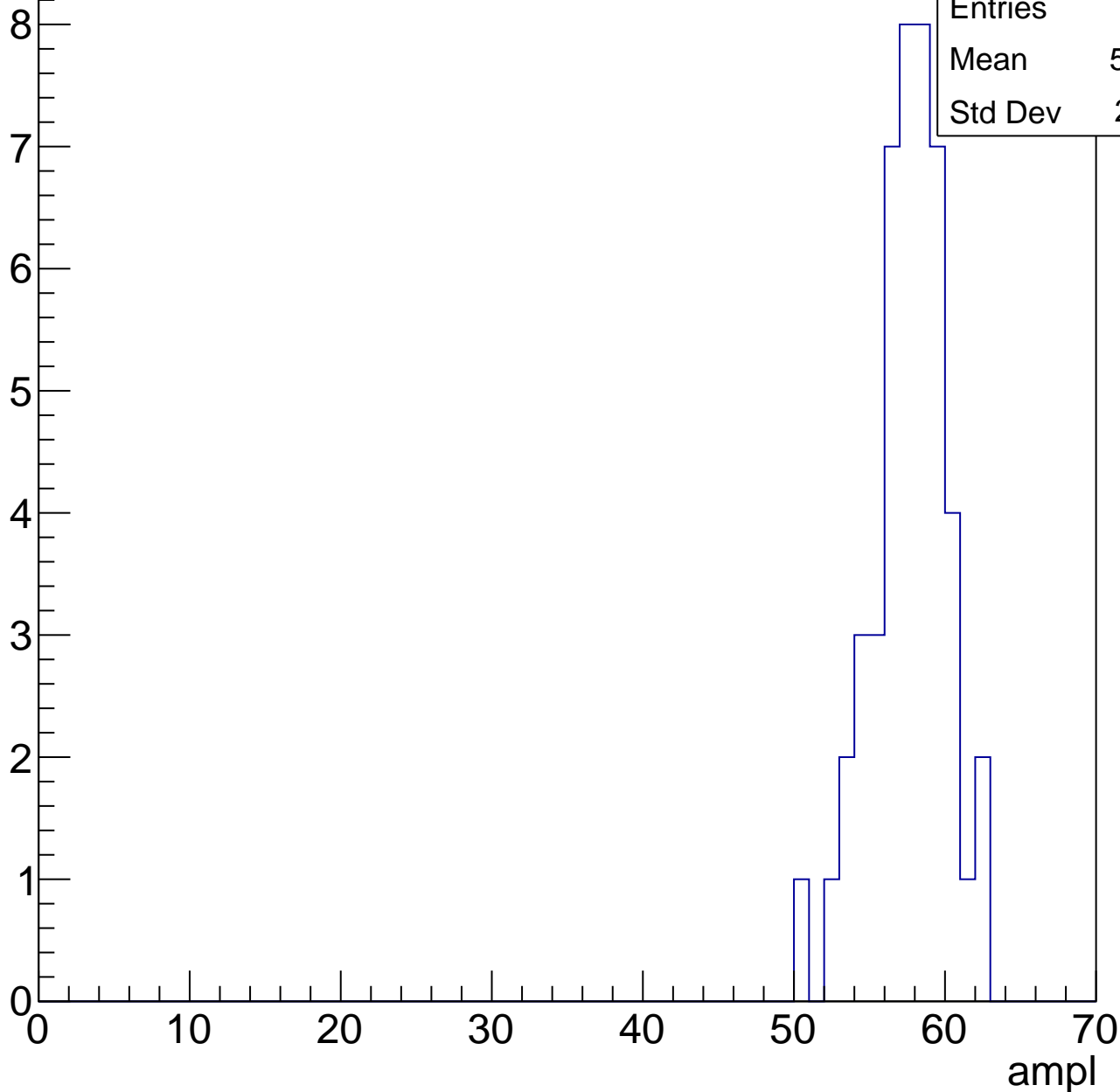


B0L002S, U9-ch20, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

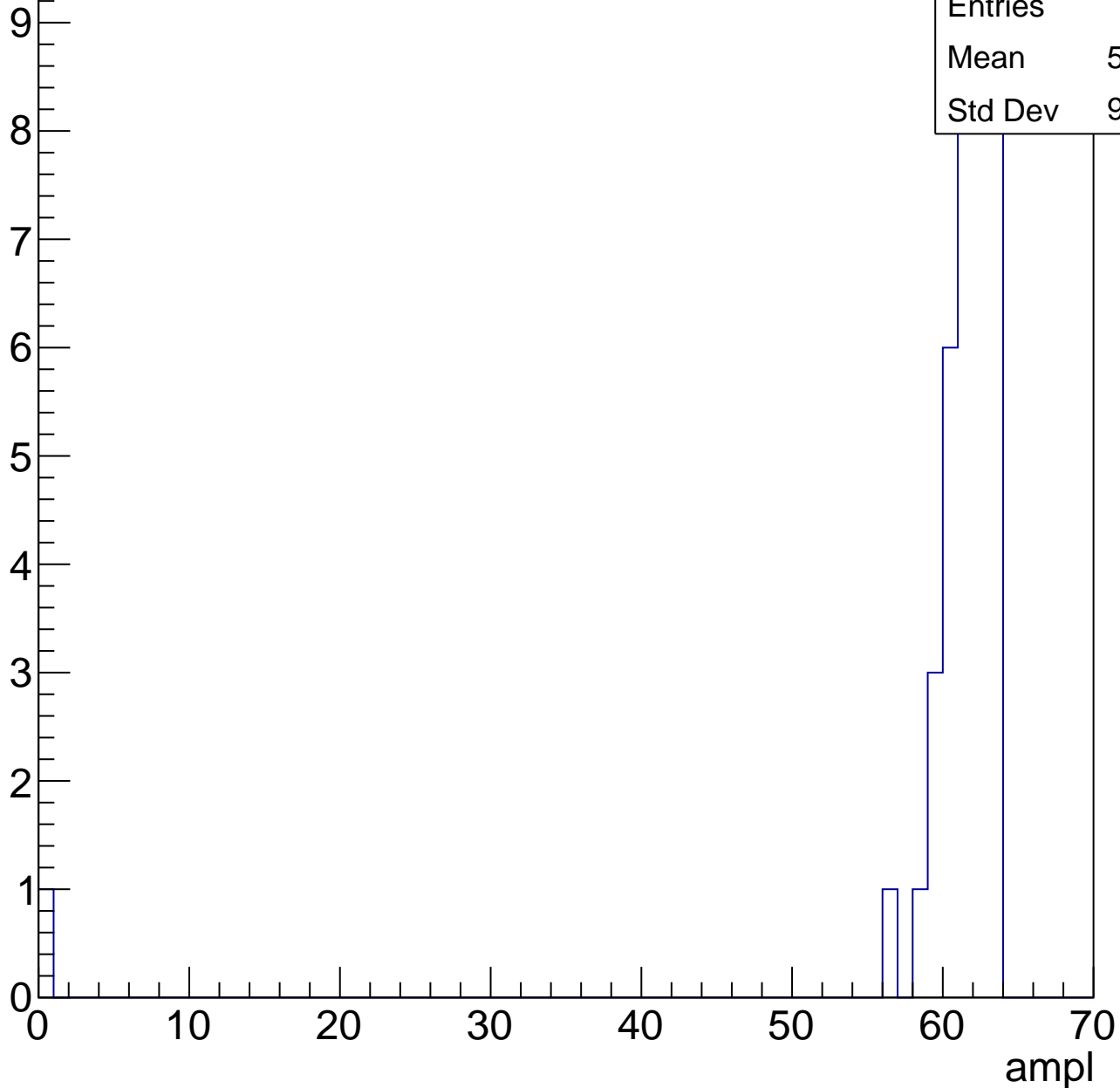
Entries	47
Mean	57.13
Std Dev	2.481



B0L002S, U9-ch20, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch21, adc0

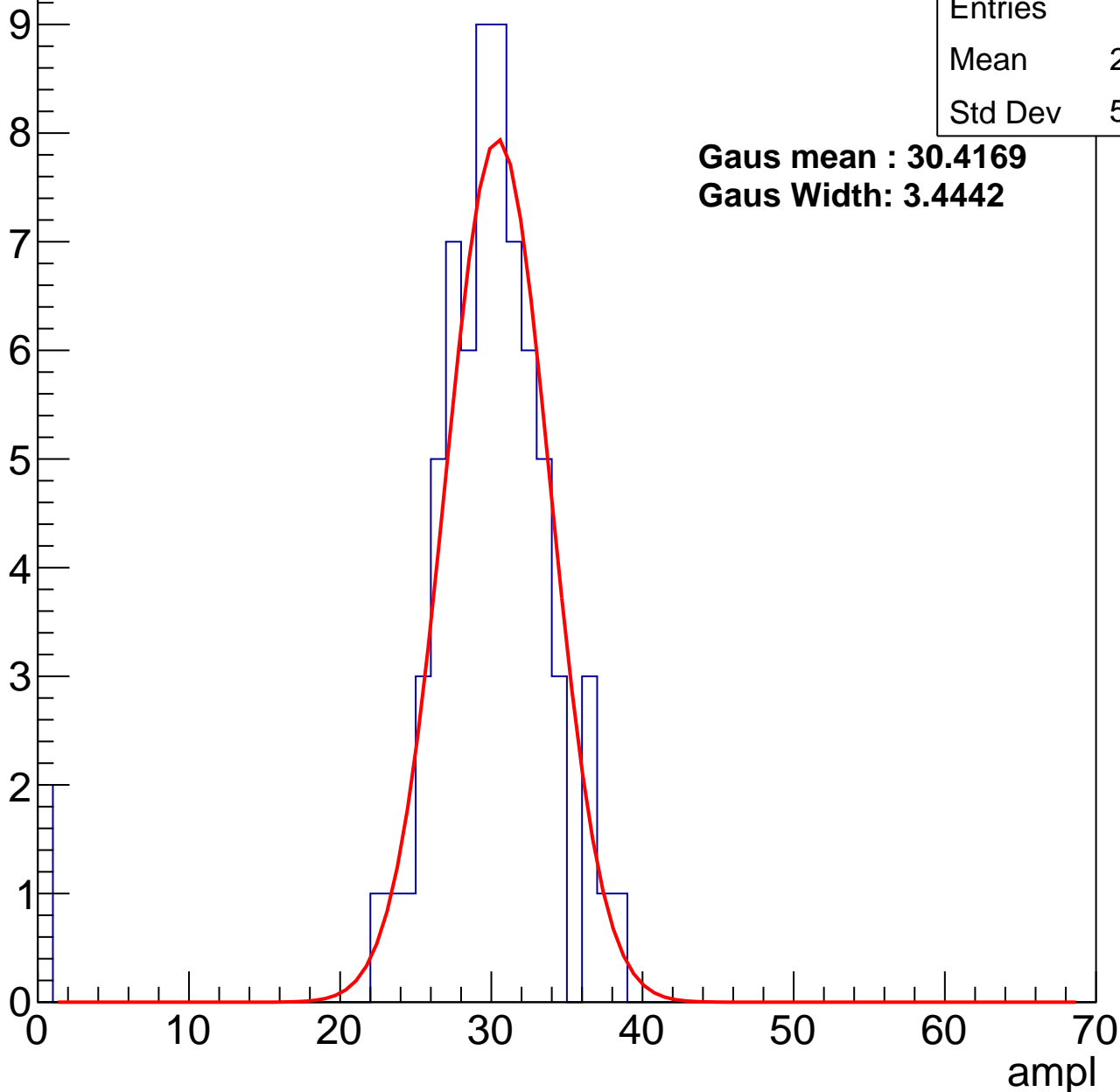
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	28.87
Std Dev	5.923

Gaus mean : 30.4169

Gaus Width: 3.4442



B0L002S, U9-ch21, adc1

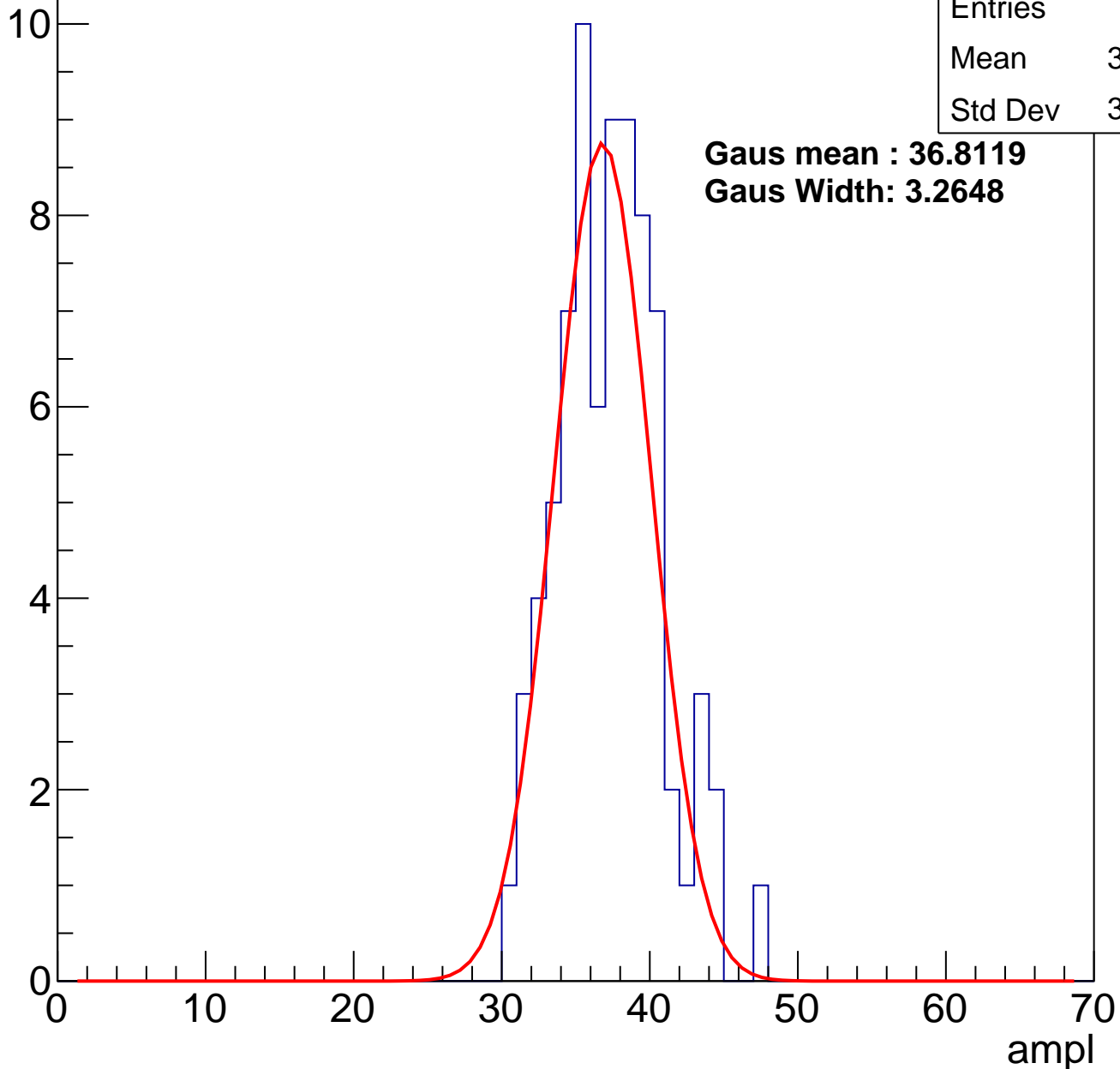
calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	36.86
Std Dev	3.418

Gaus mean : 36.8119

Gaus Width: 3.2648

Entry



B0L002S, U9-ch21, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	44.68
Std Dev	3.476

Gaus mean : 44.5368

Gaus Width: 3.5581

10

8

6

4

2

0

0

10

20

30

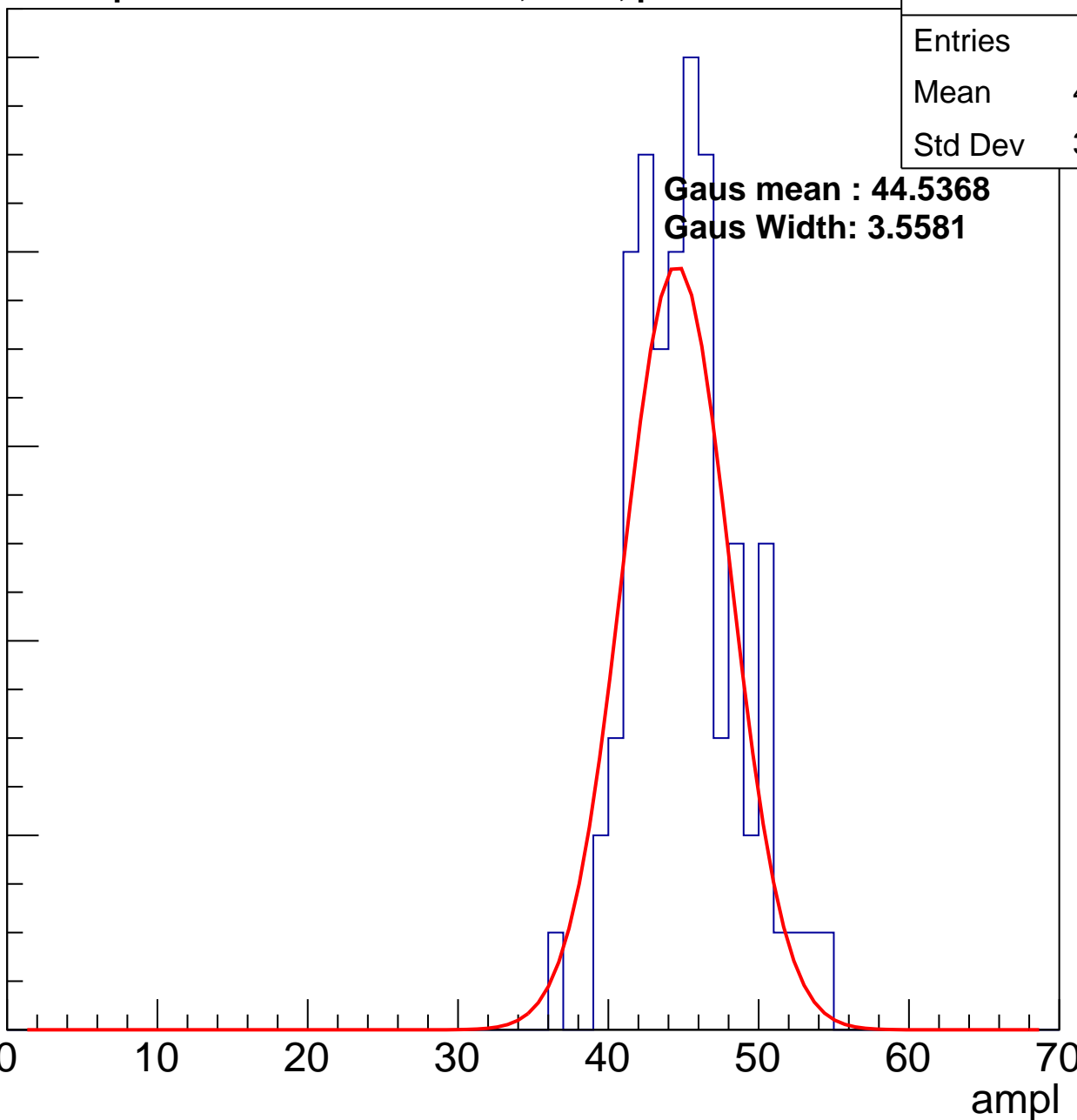
40

50

60

70

ampl

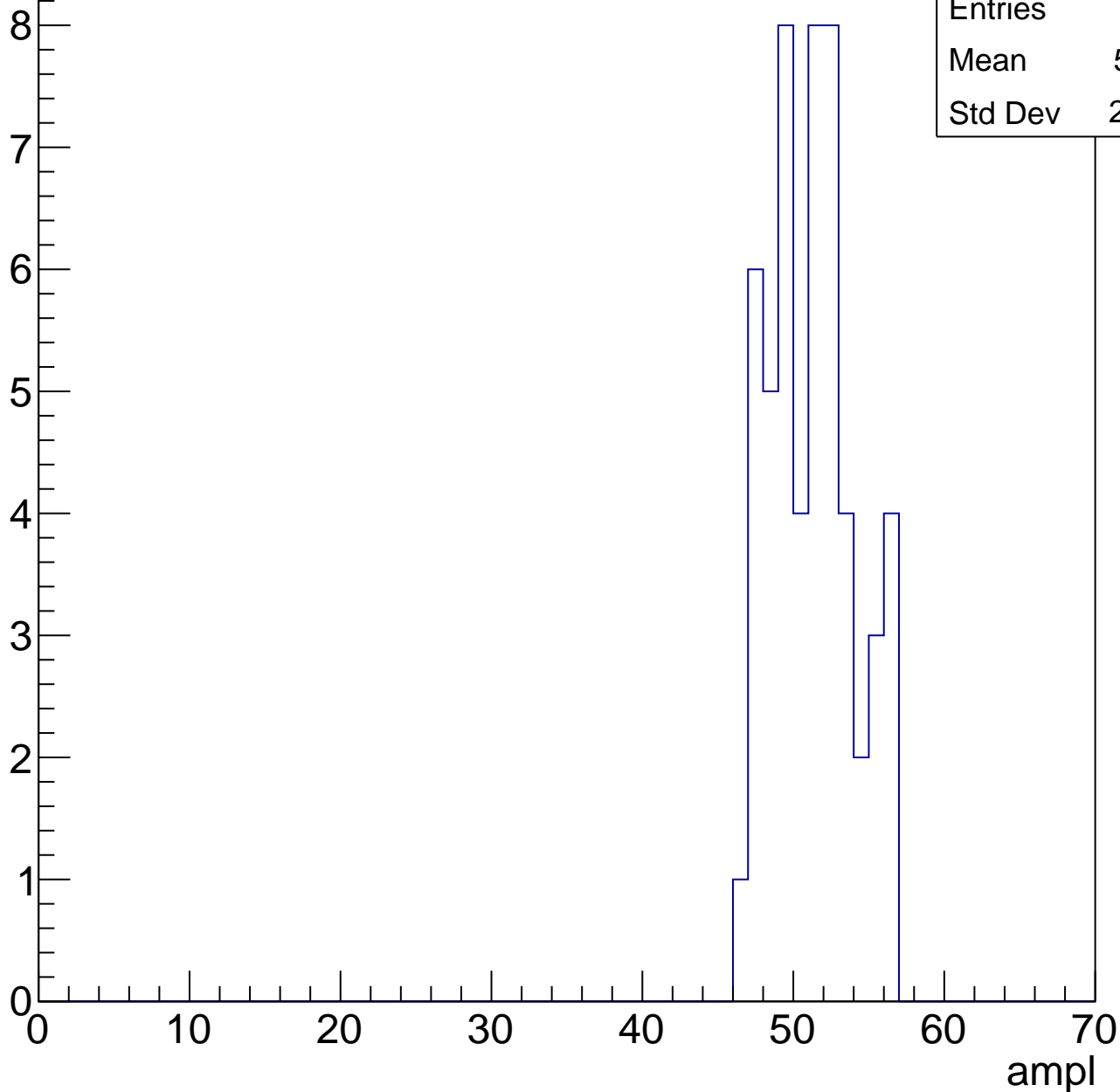


B0L002S, U9-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	50.81
Std Dev	2.713

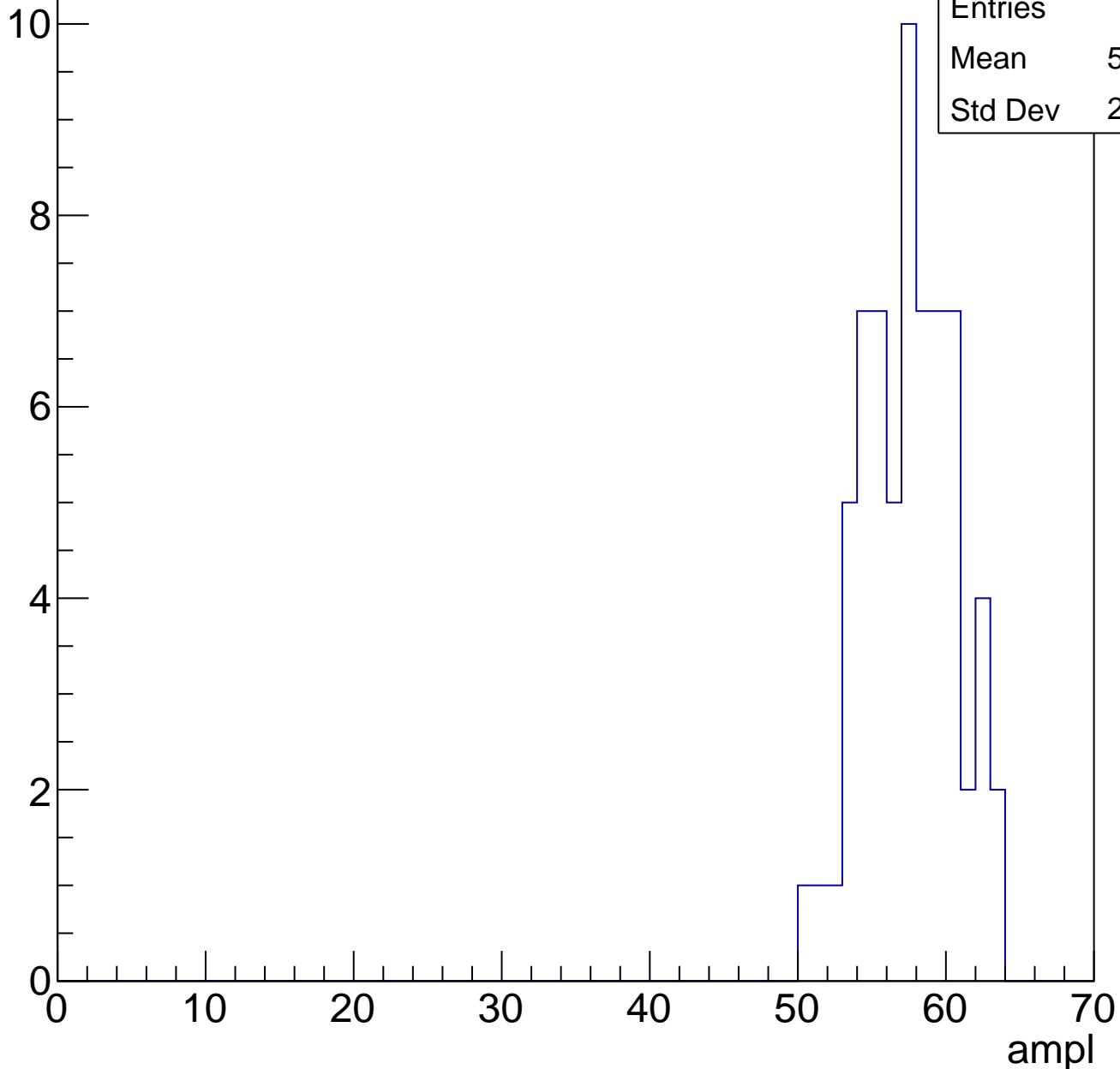


B0L002S, U9-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	57.06
Std Dev	2.984

Entry

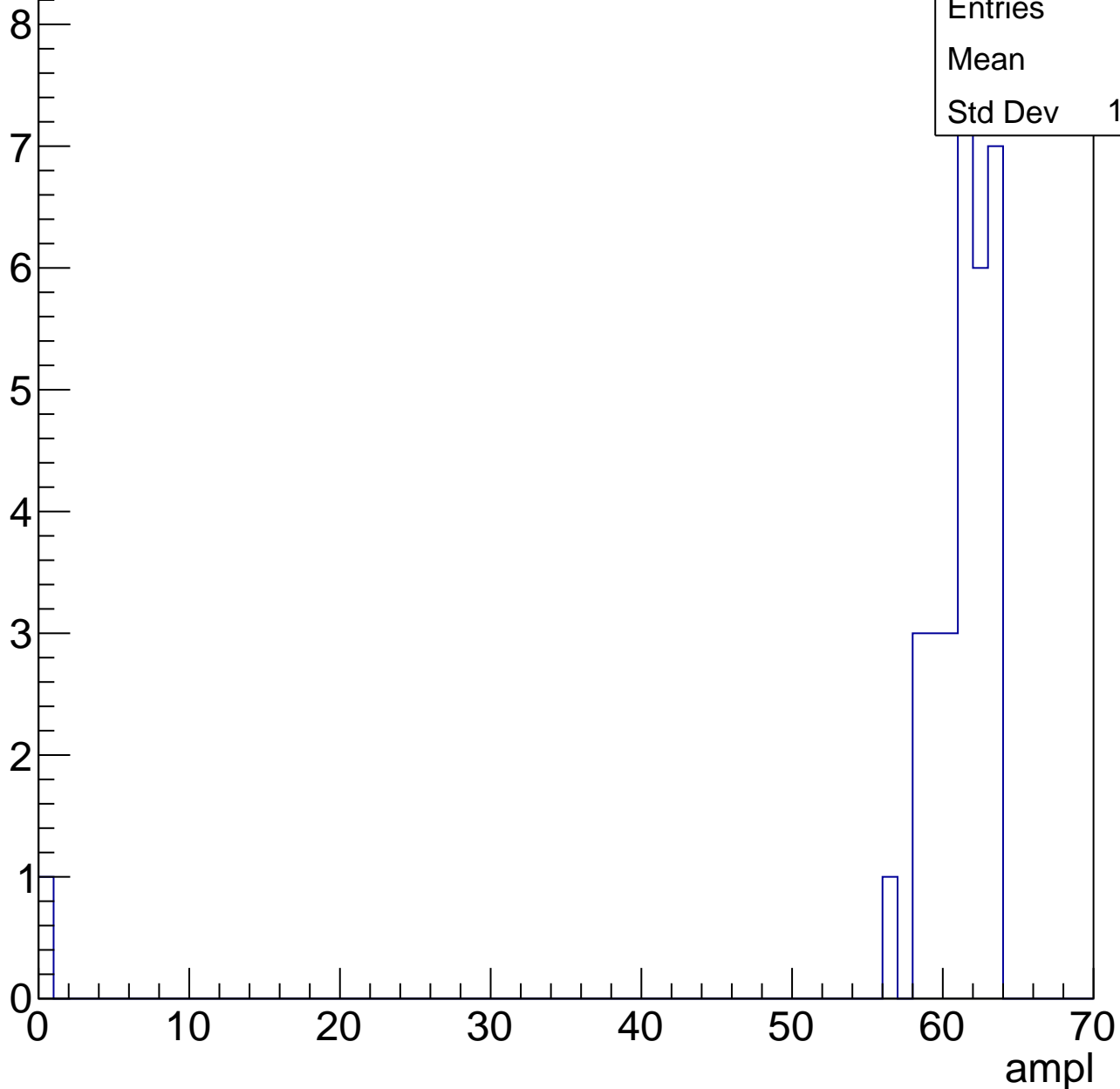


B0L002S, U9-ch21, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59
Std Dev	10.74



B0L002S, U9-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch22, adc0

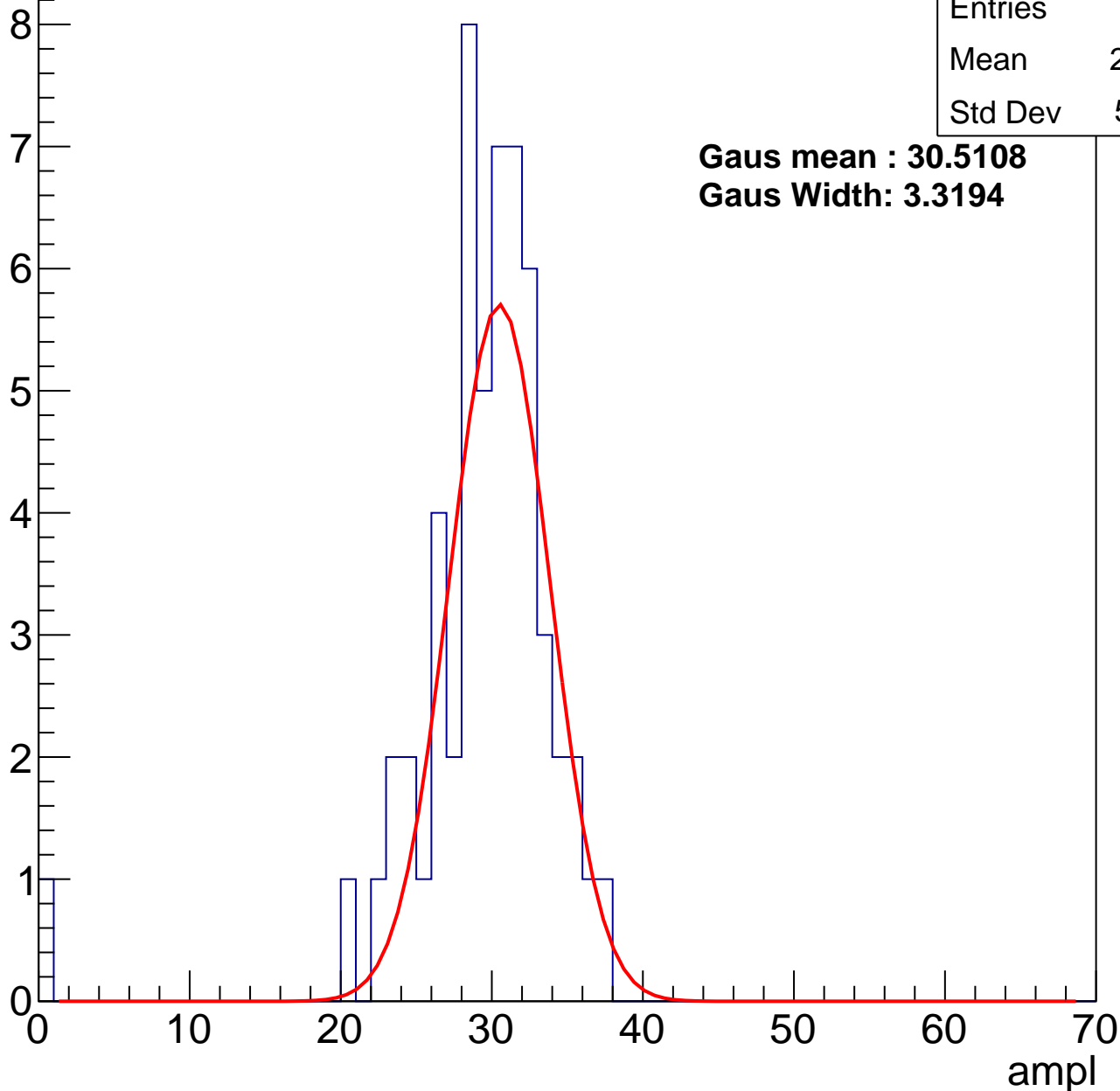
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	28.88
Std Dev	5.231

Gaus mean : 30.5108

Gaus Width: 3.3194



B0L002S, U9-ch22, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	35.83
Std Dev	3.131

Gaus mean : 36.9428

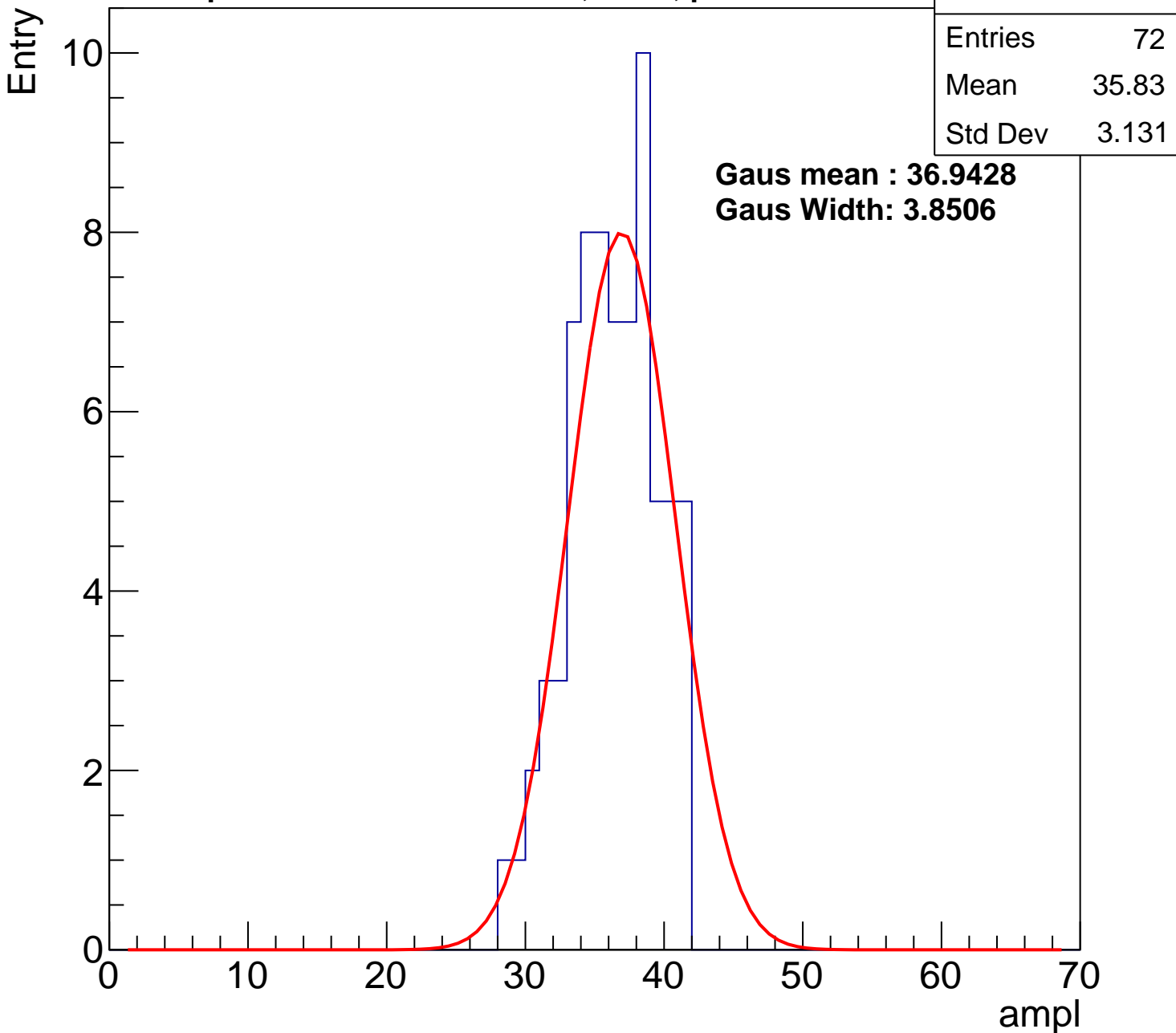
Gaus Width: 3.8506

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch22, adc2

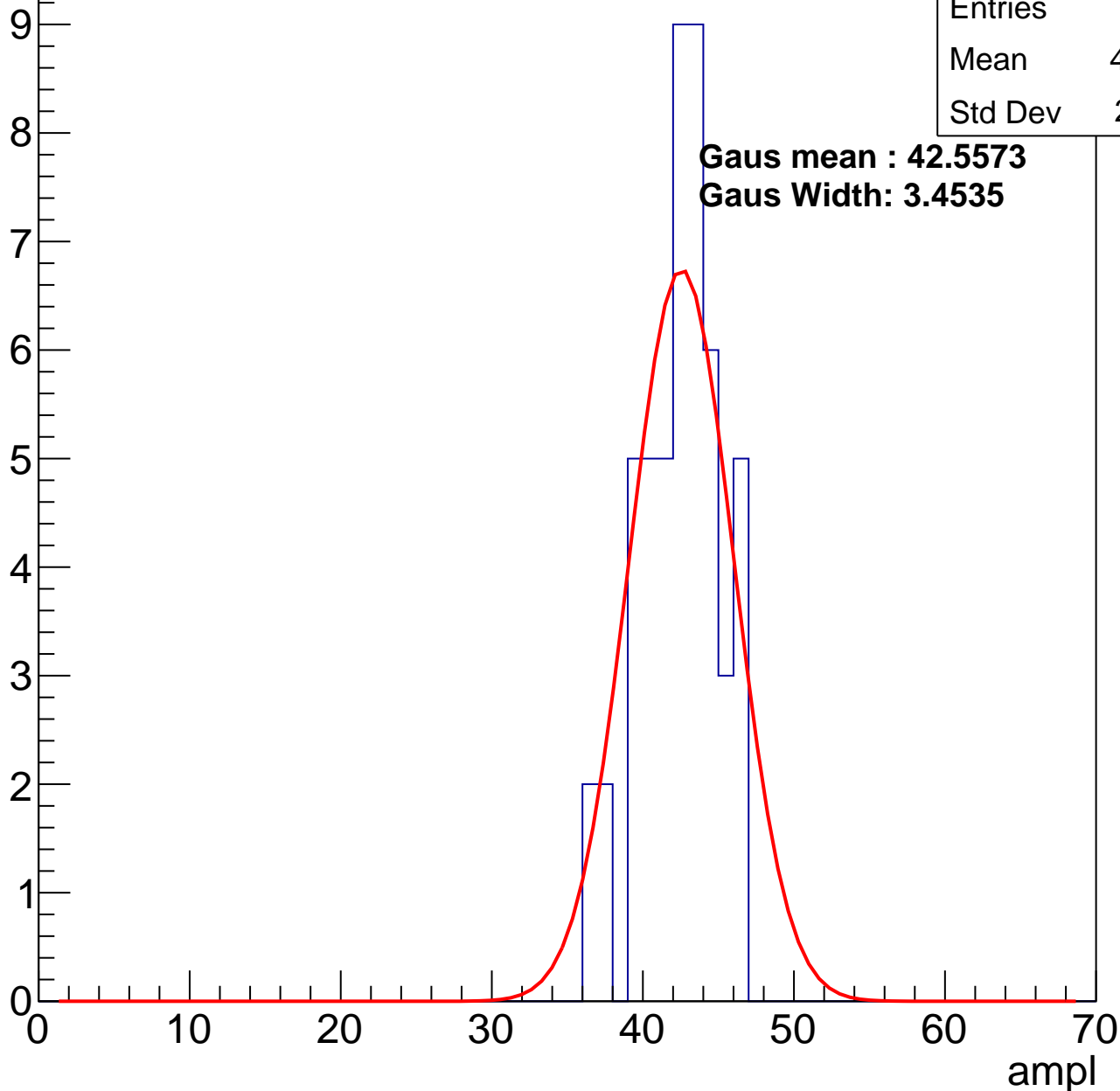
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	41.96
Std Dev	2.551

Gaus mean : 42.5573

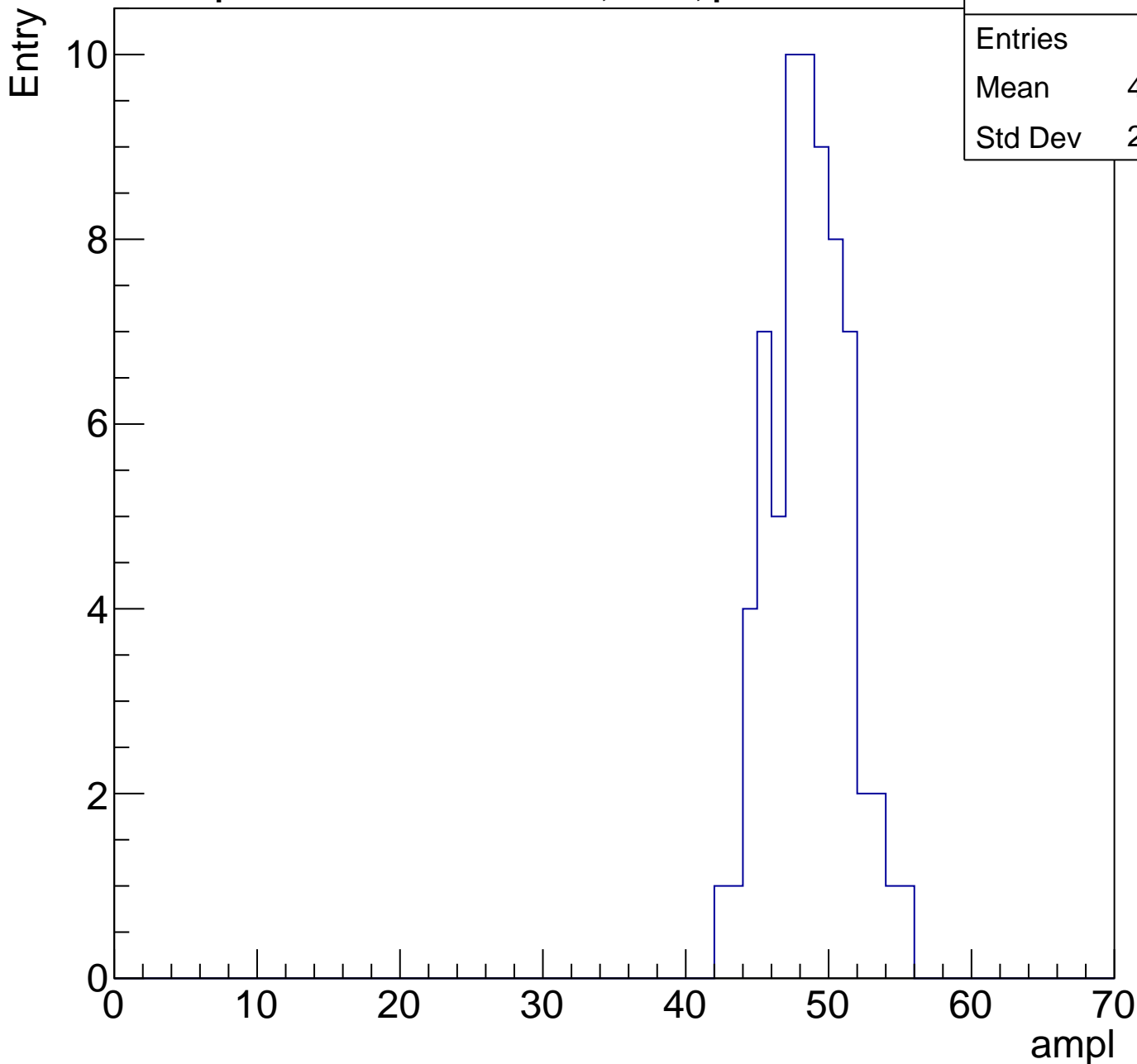
Gaus Width: 3.4535



B0L002S, U9-ch22, adc3

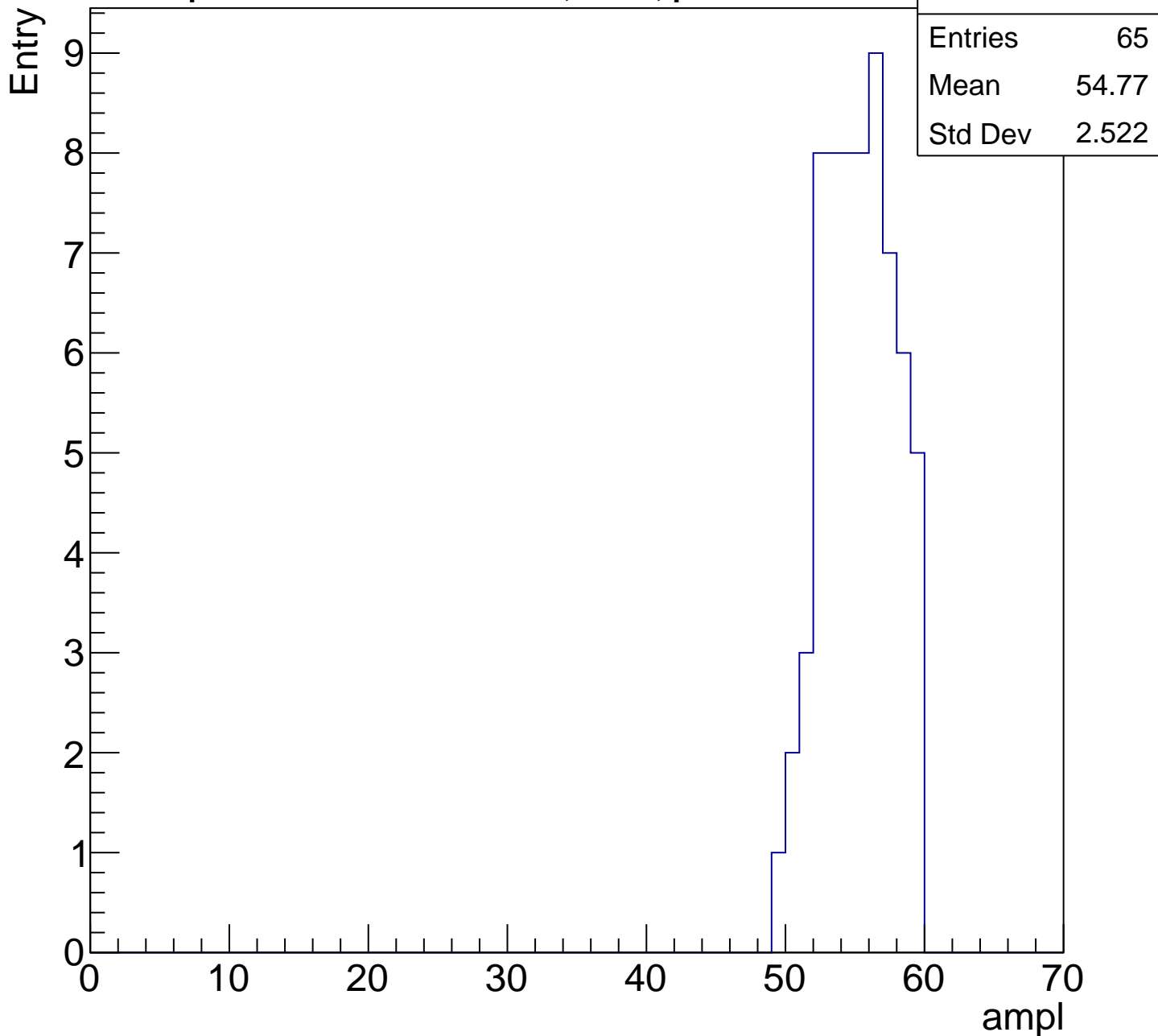
calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	48.13
Std Dev	2.678



B0L002S, U9-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

Entries	50
Mean	58.94
Std Dev	8.677

ampl

0

10

20

30

40

50

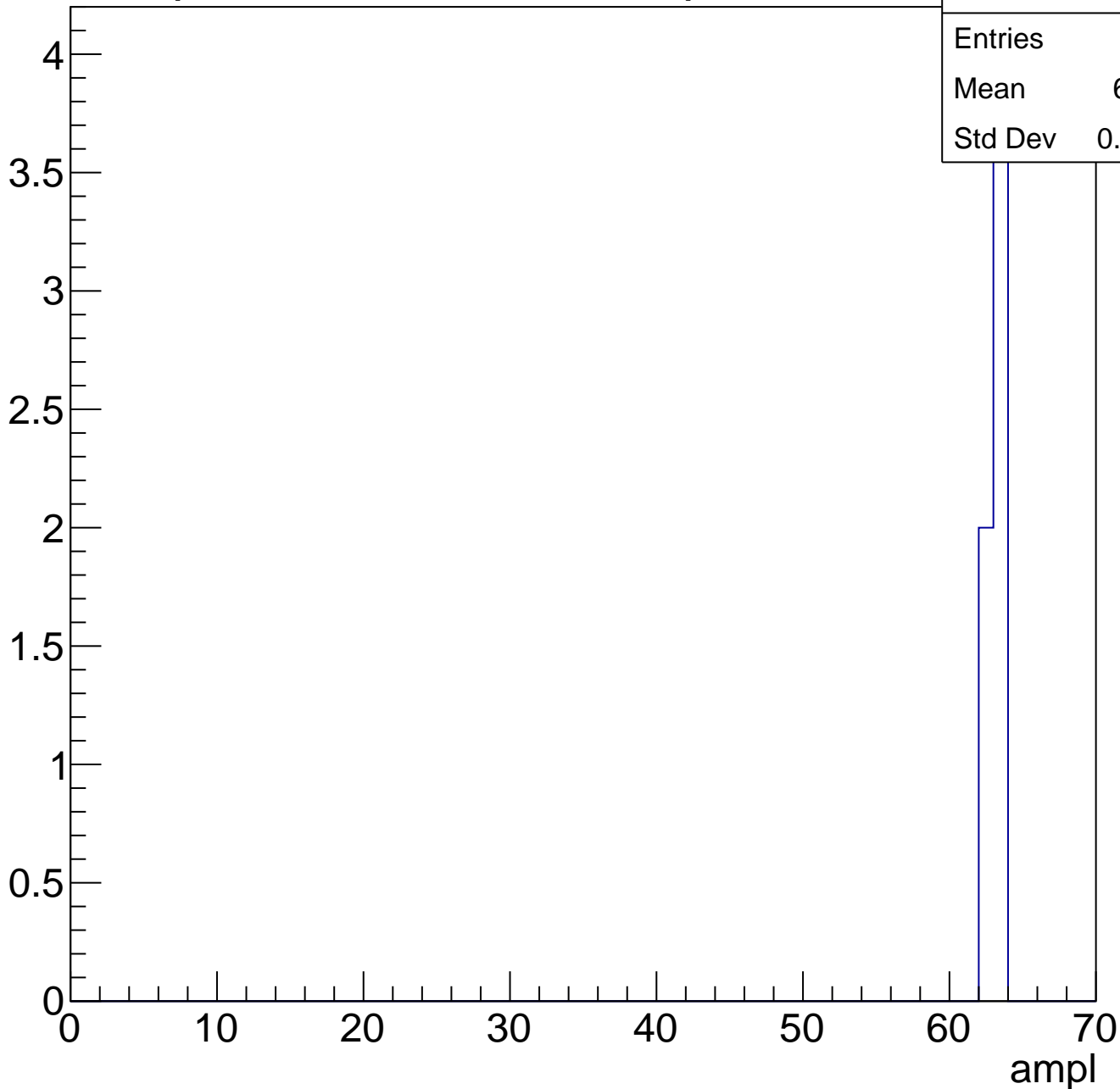
60

70

B0L002S, U9-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch23, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	29.43
Std Dev	3.34

Gaus mean : 30.0575

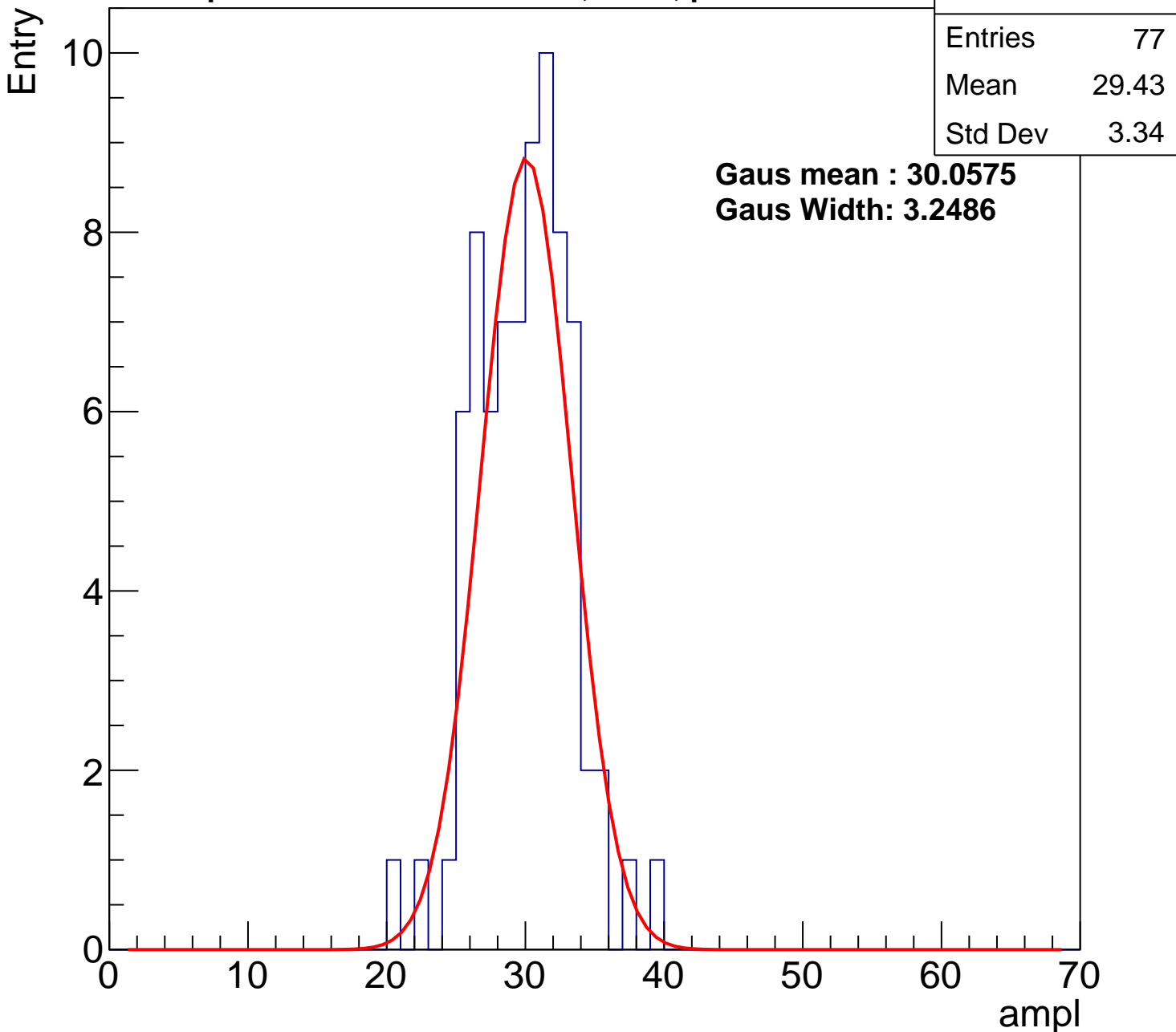
Gaus Width: 3.2486

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch23, adc1

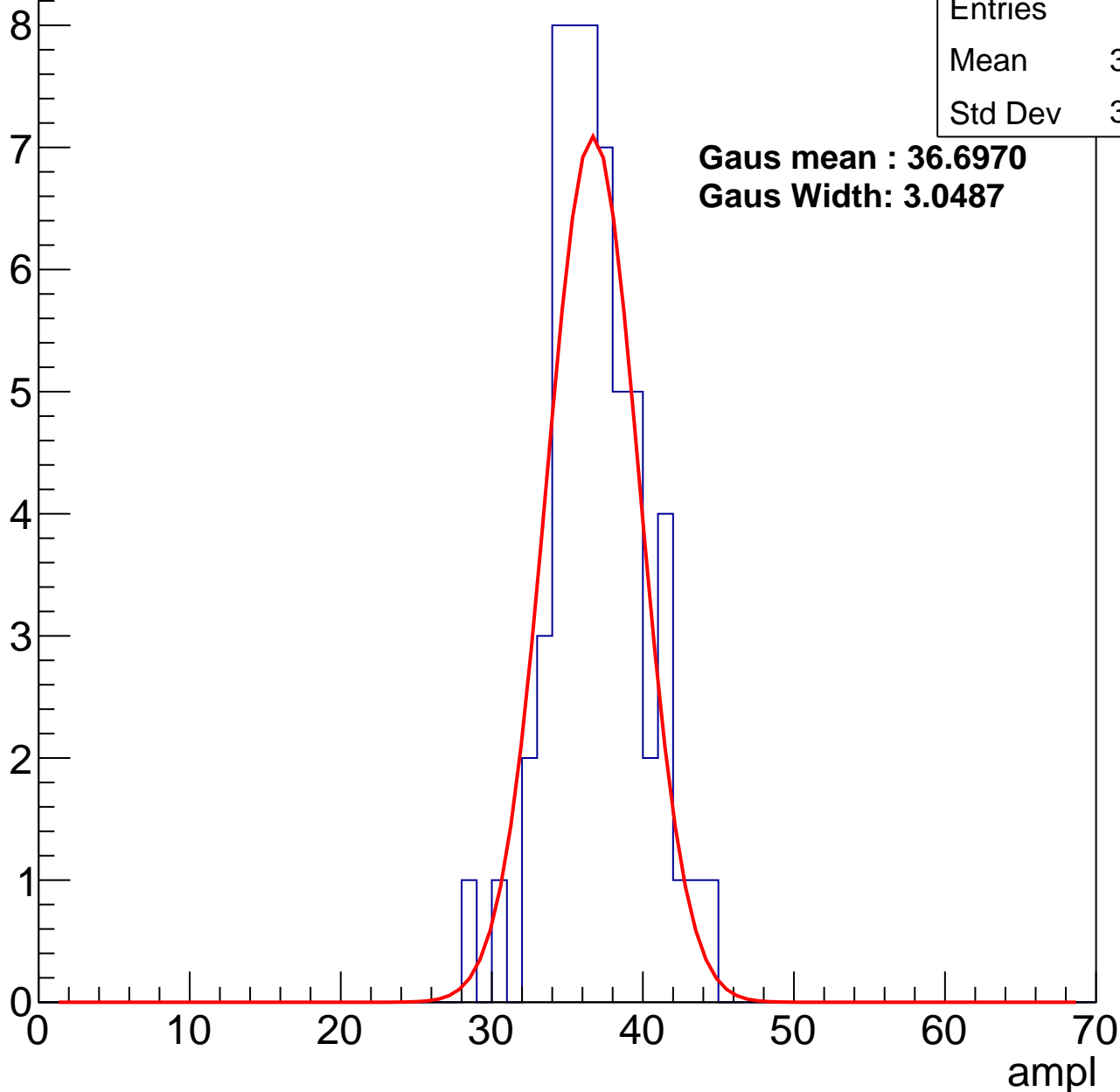
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.46
Std Dev	3.078

Gaus mean : 36.6970

Gaus Width: 3.0487

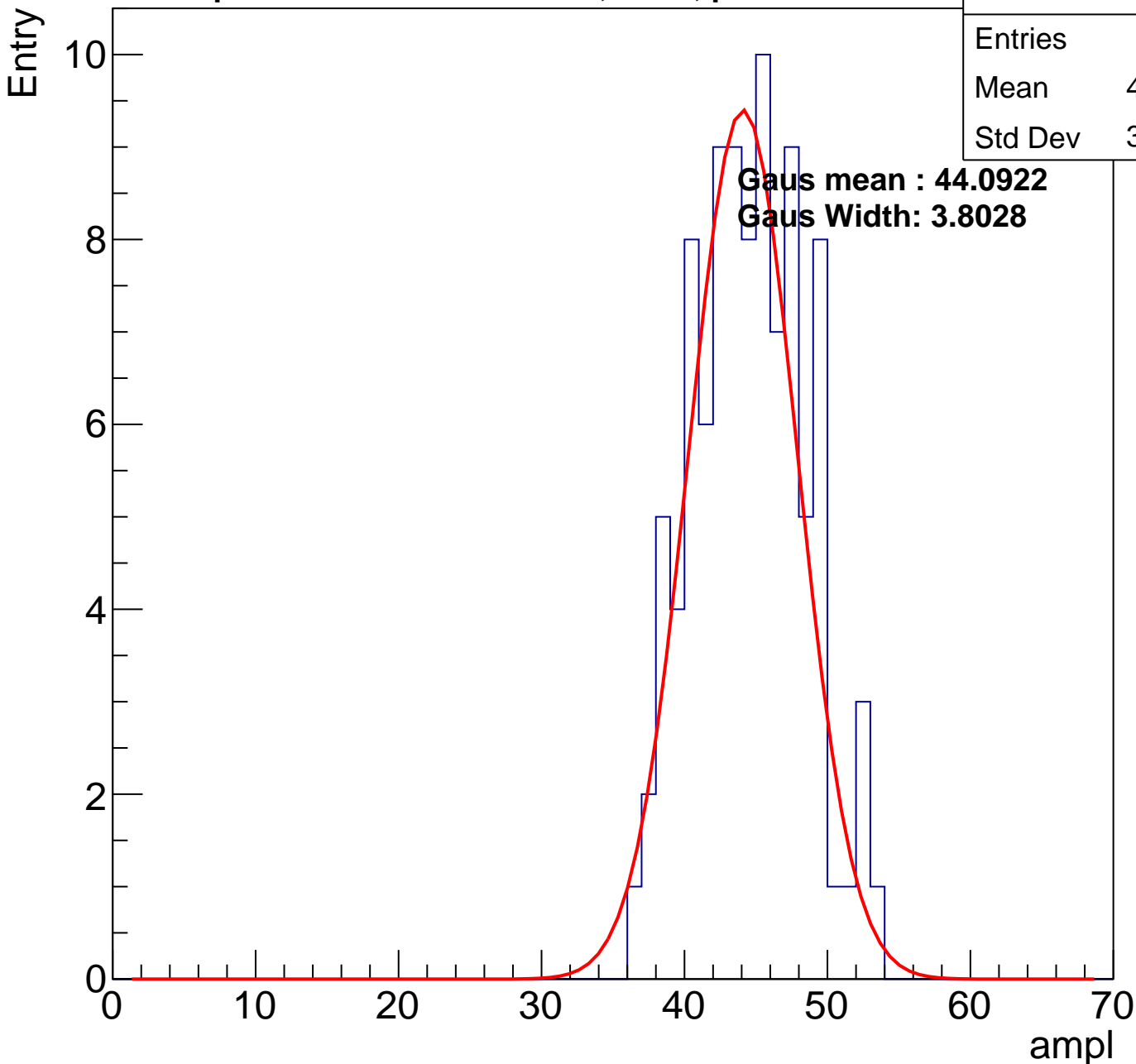


B0L002S, U9-ch23, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	97
Mean	44.08
Std Dev	3.833

Gaus mean : 44.0922
Gaus Width: 3.8028

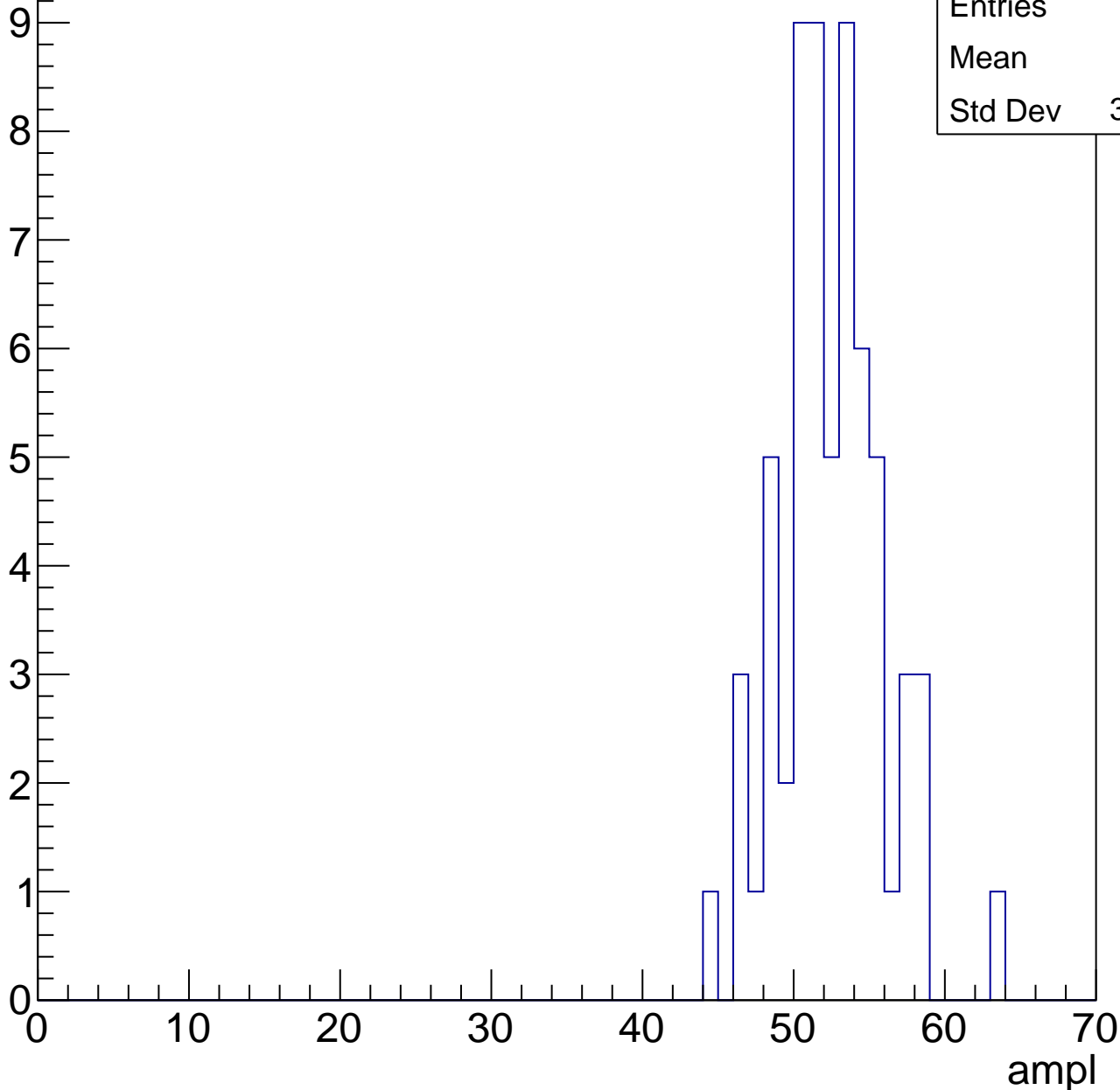


B0L002S, U9-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

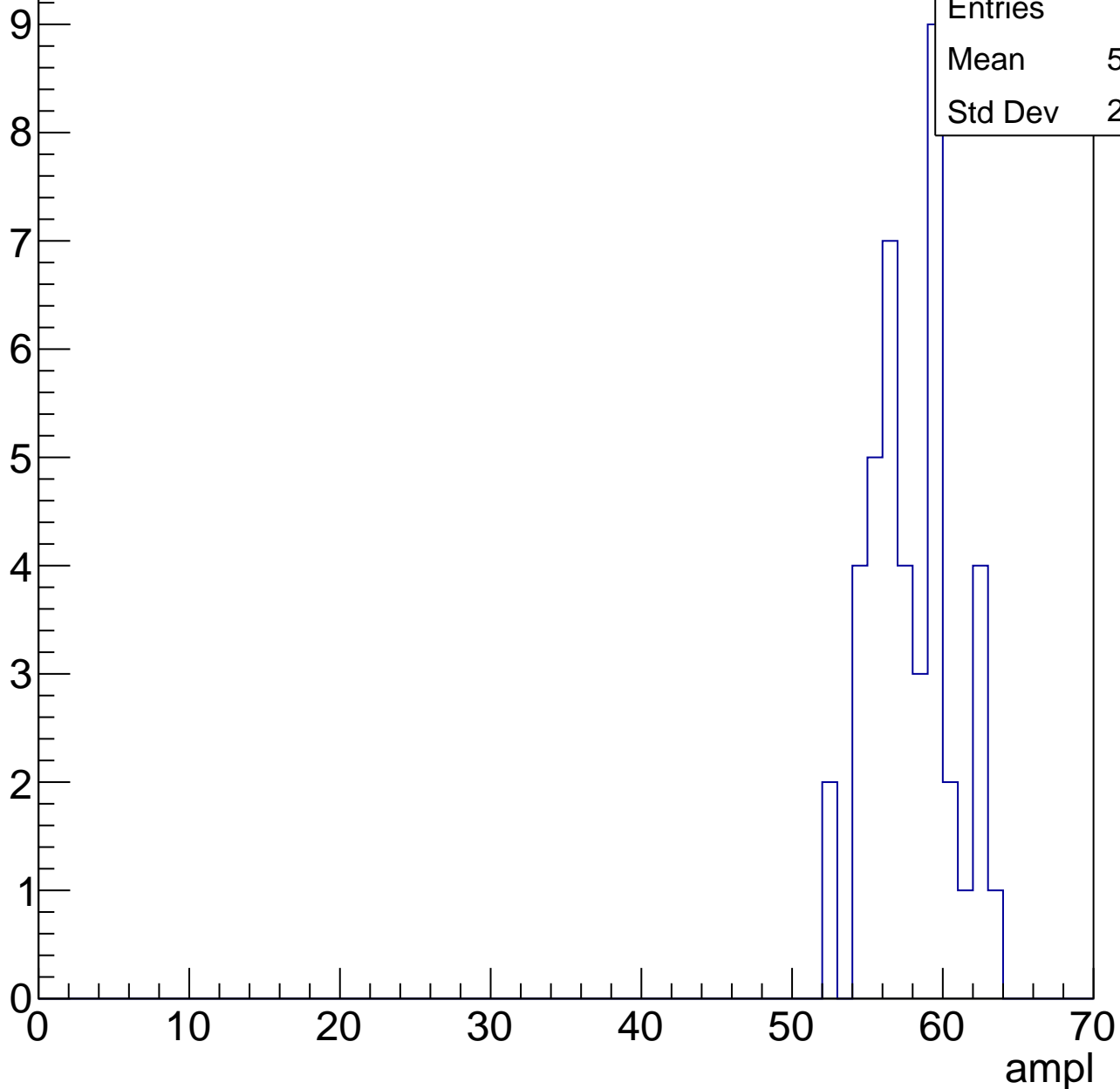
Entries	63
Mean	52
Std Dev	3.423



B0L002S, U9-ch23, adc4

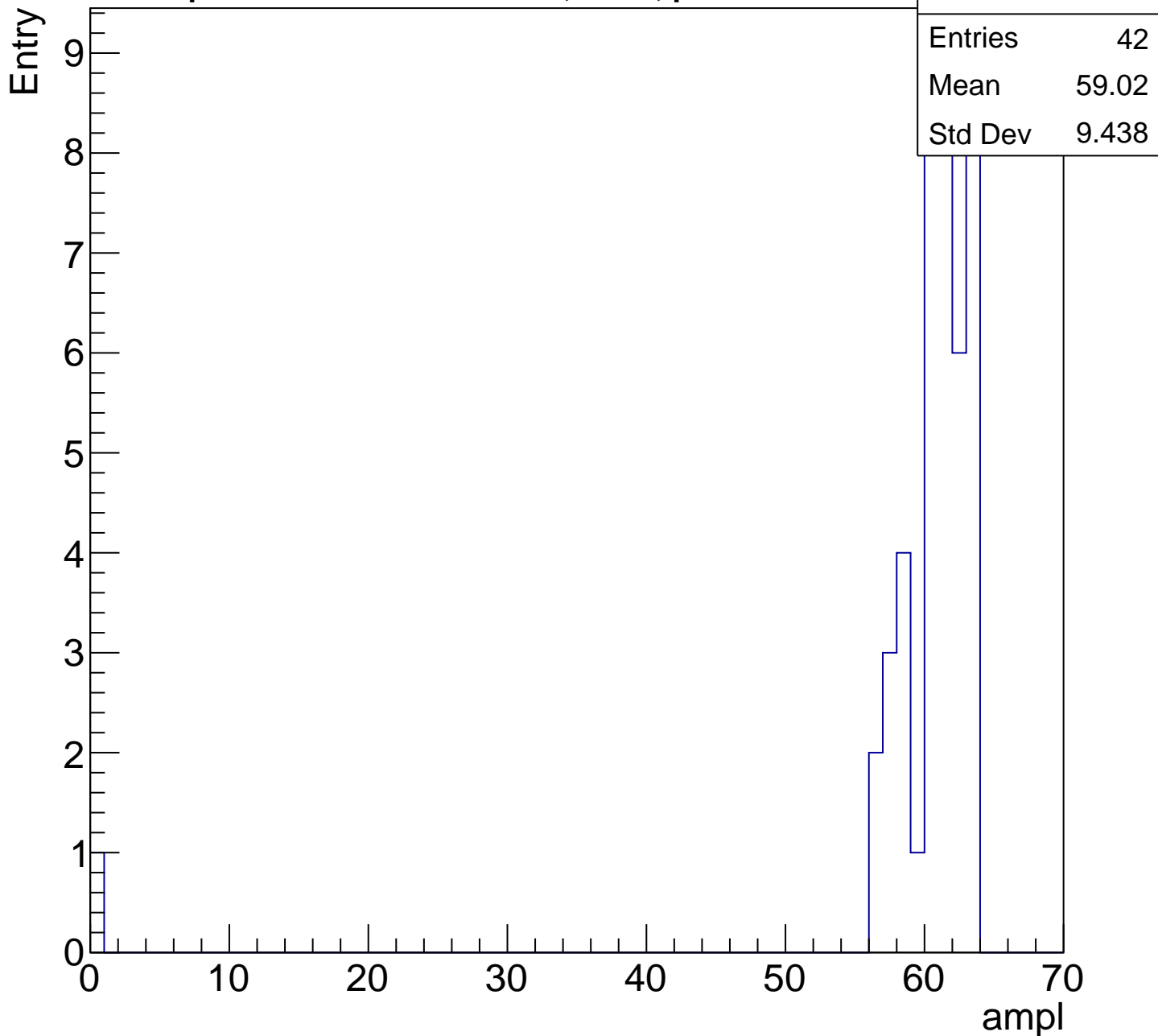
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch23, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch24, adc0

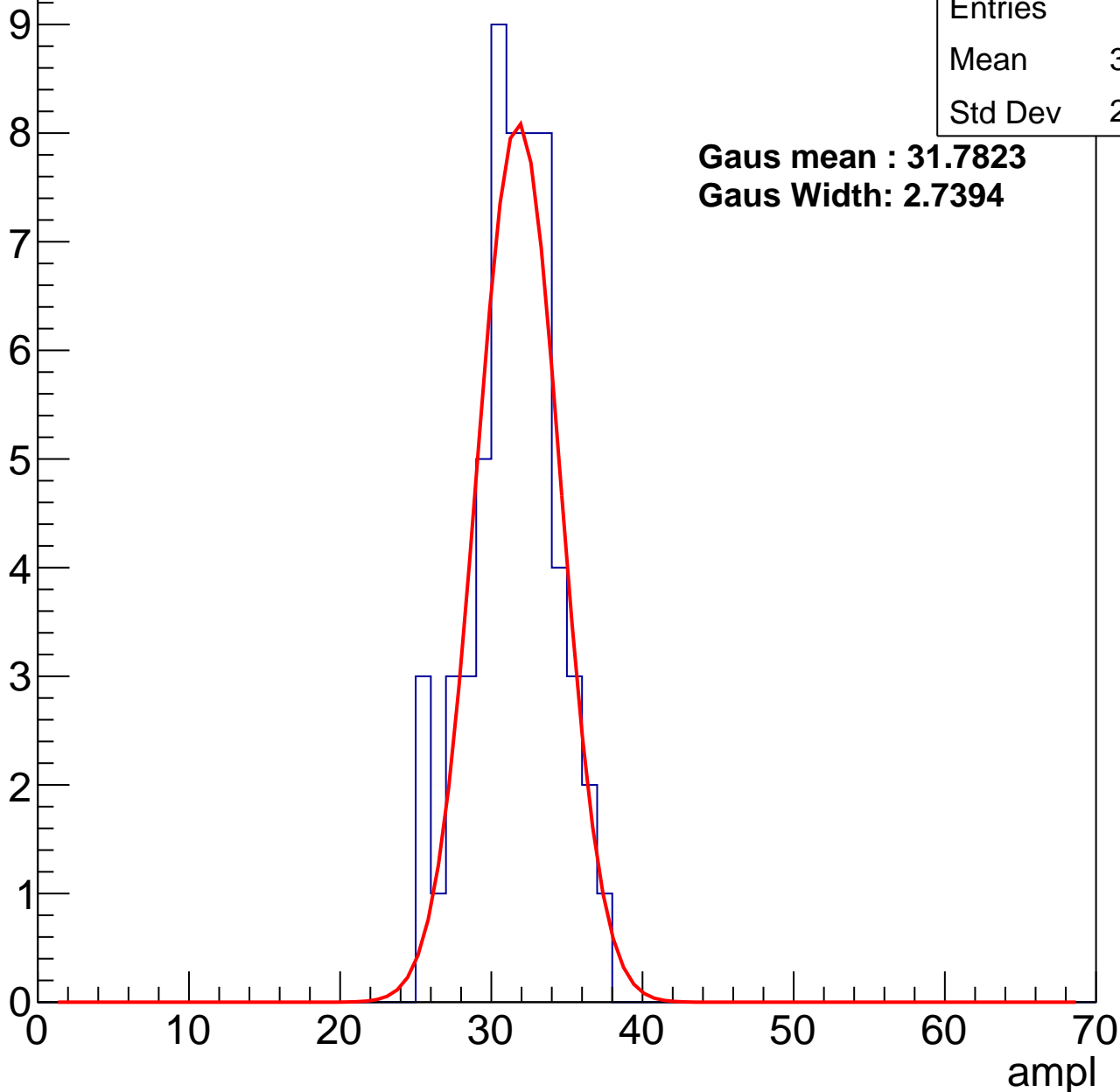
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.02
Std Dev	2.776

Gaus mean : 31.7823

Gaus Width: 2.7394



B0L002S, U9-ch24, adc1

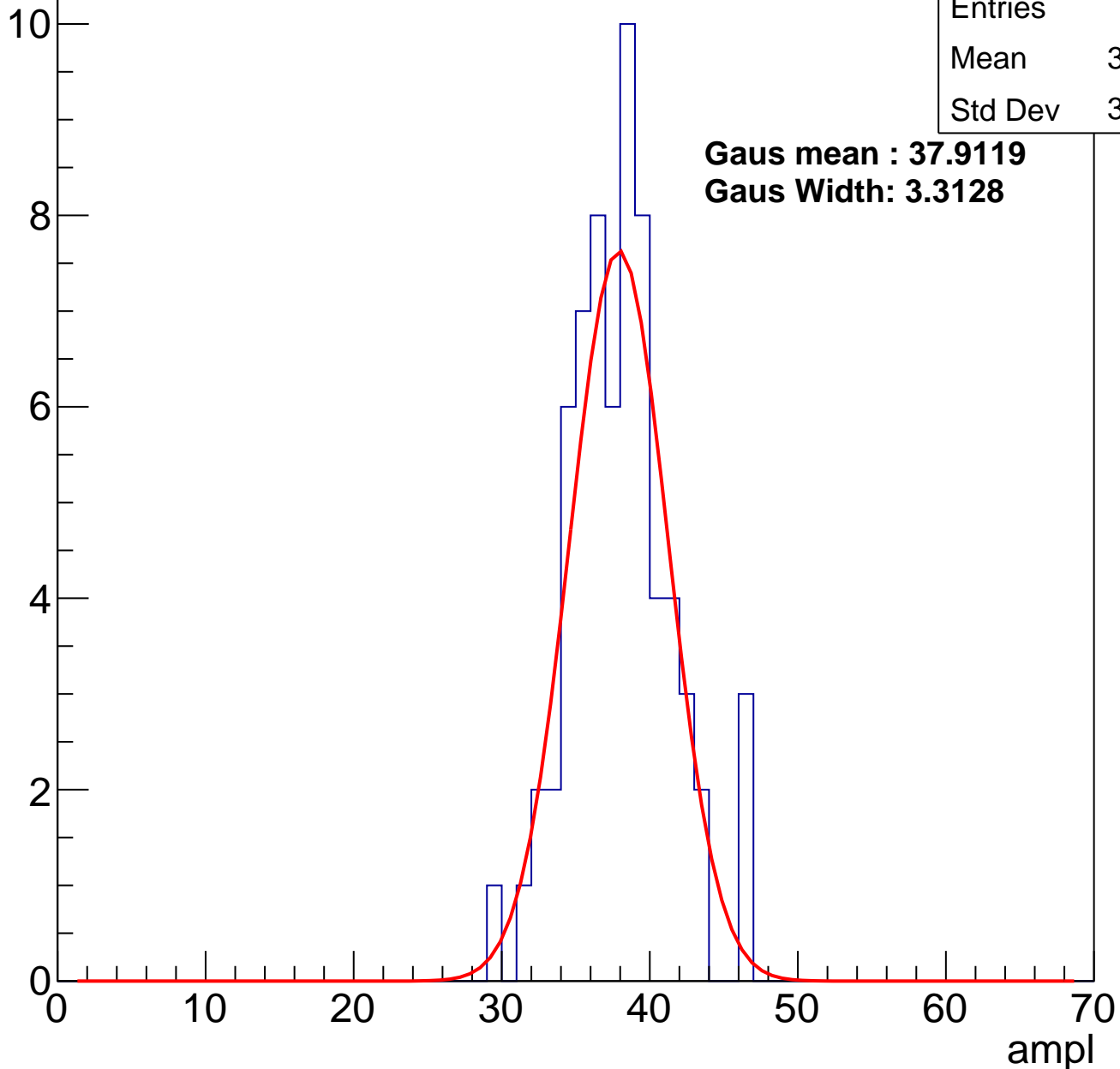
calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	37.54
Std Dev	3.422

Gaus mean : 37.9119

Gaus Width: 3.3128

Entry



B0L002S, U9-ch24, adc2

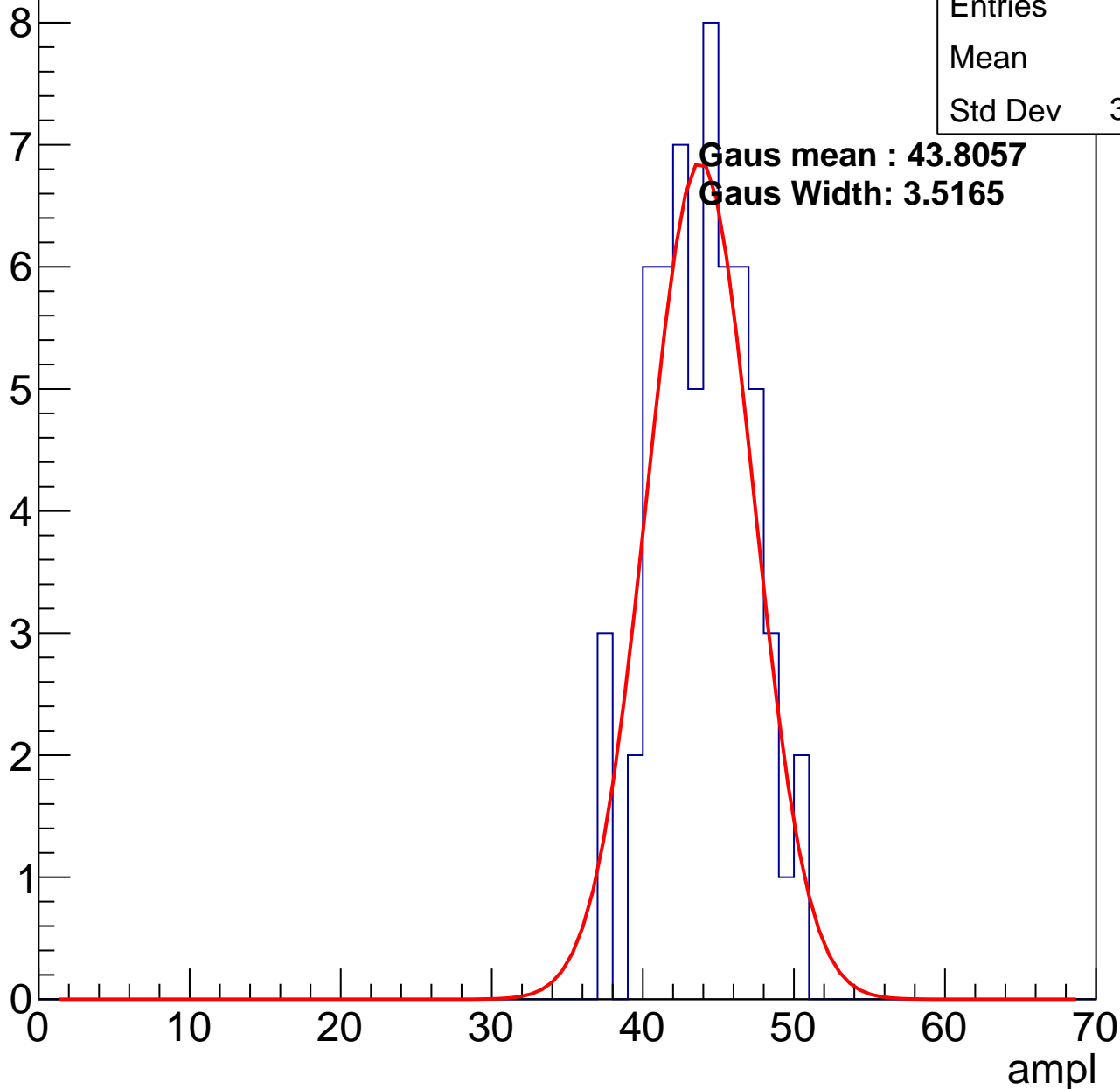
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.5
Std Dev	3.122

Gaus mean : 43.8057

Gaus Width: 3.5165

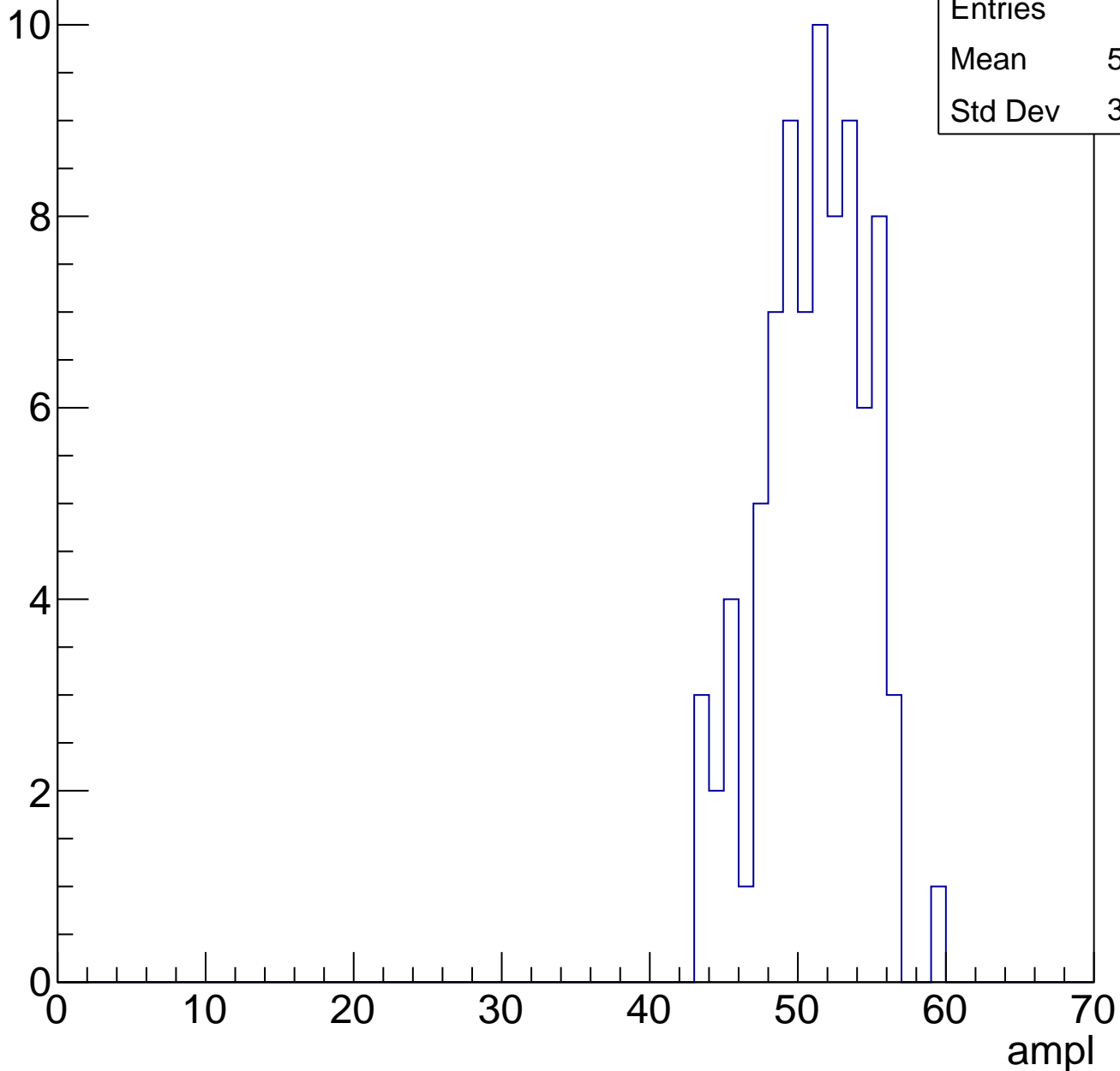


B0L002S, U9-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	50.59
Std Dev	3.464

Entry

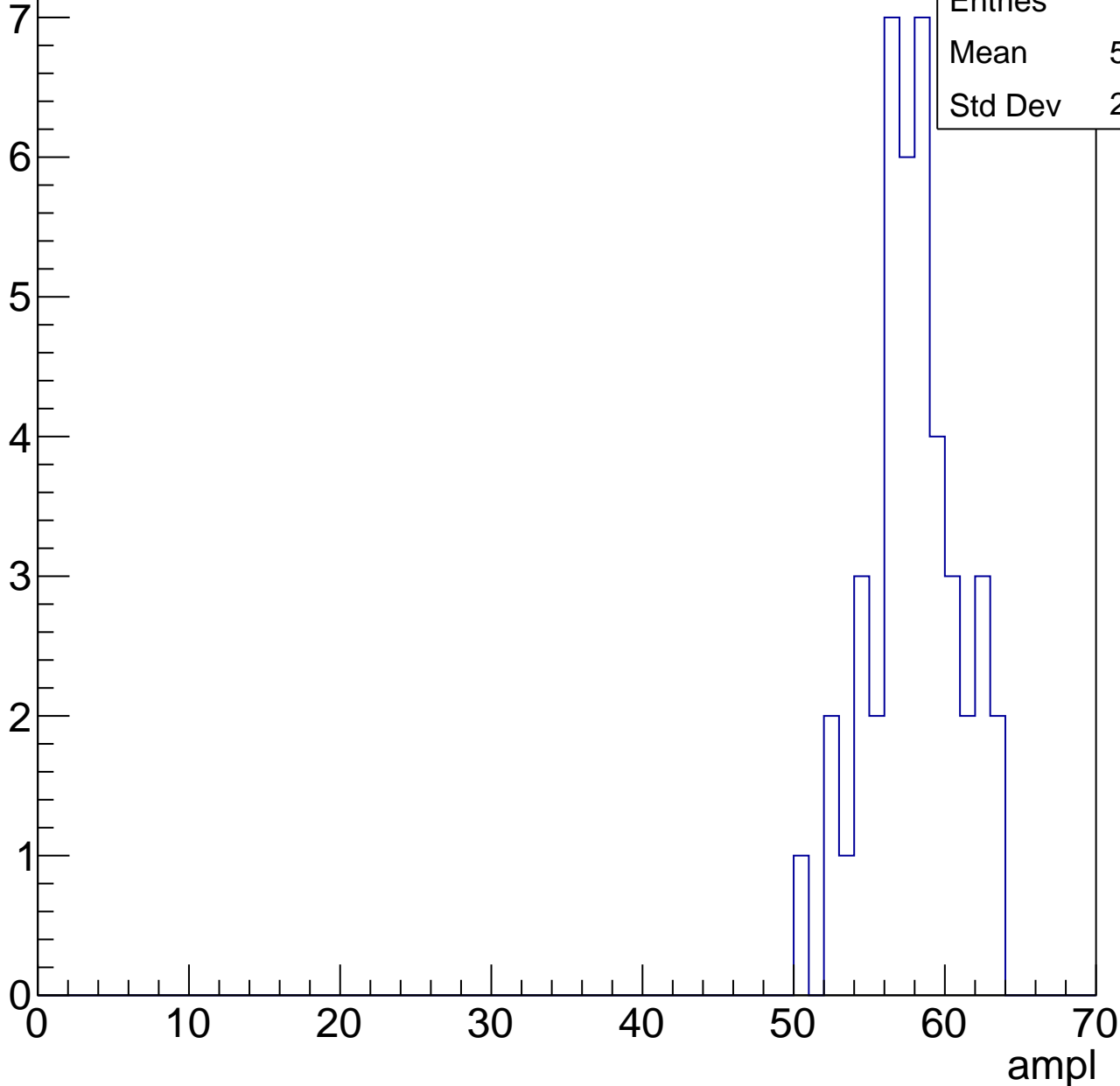


B0L002S, U9-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	57.42
Std Dev	2.967

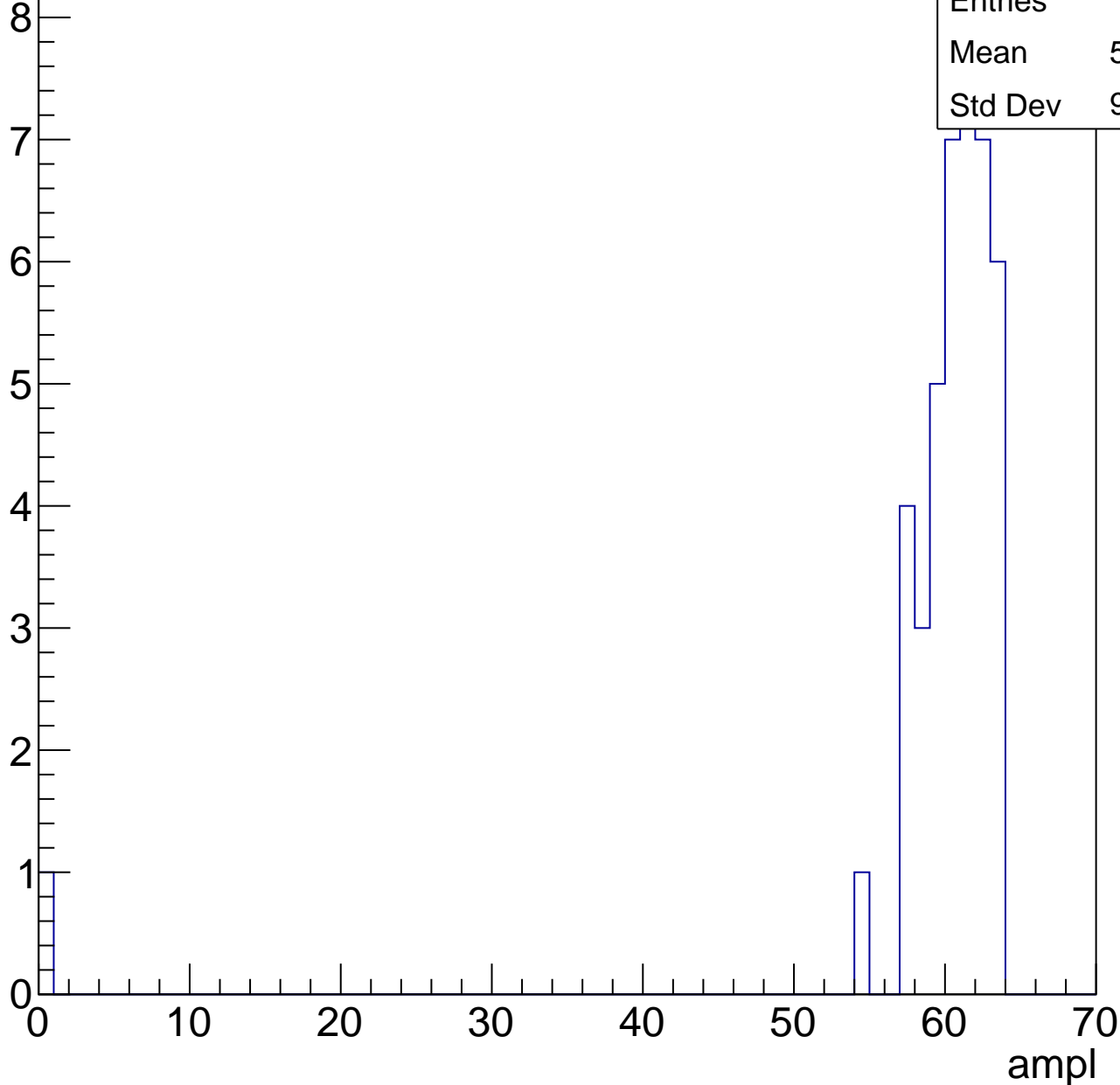


B0L002S, U9-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	58.83
Std Dev	9.414



B0L002S, U9-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch25, adc0

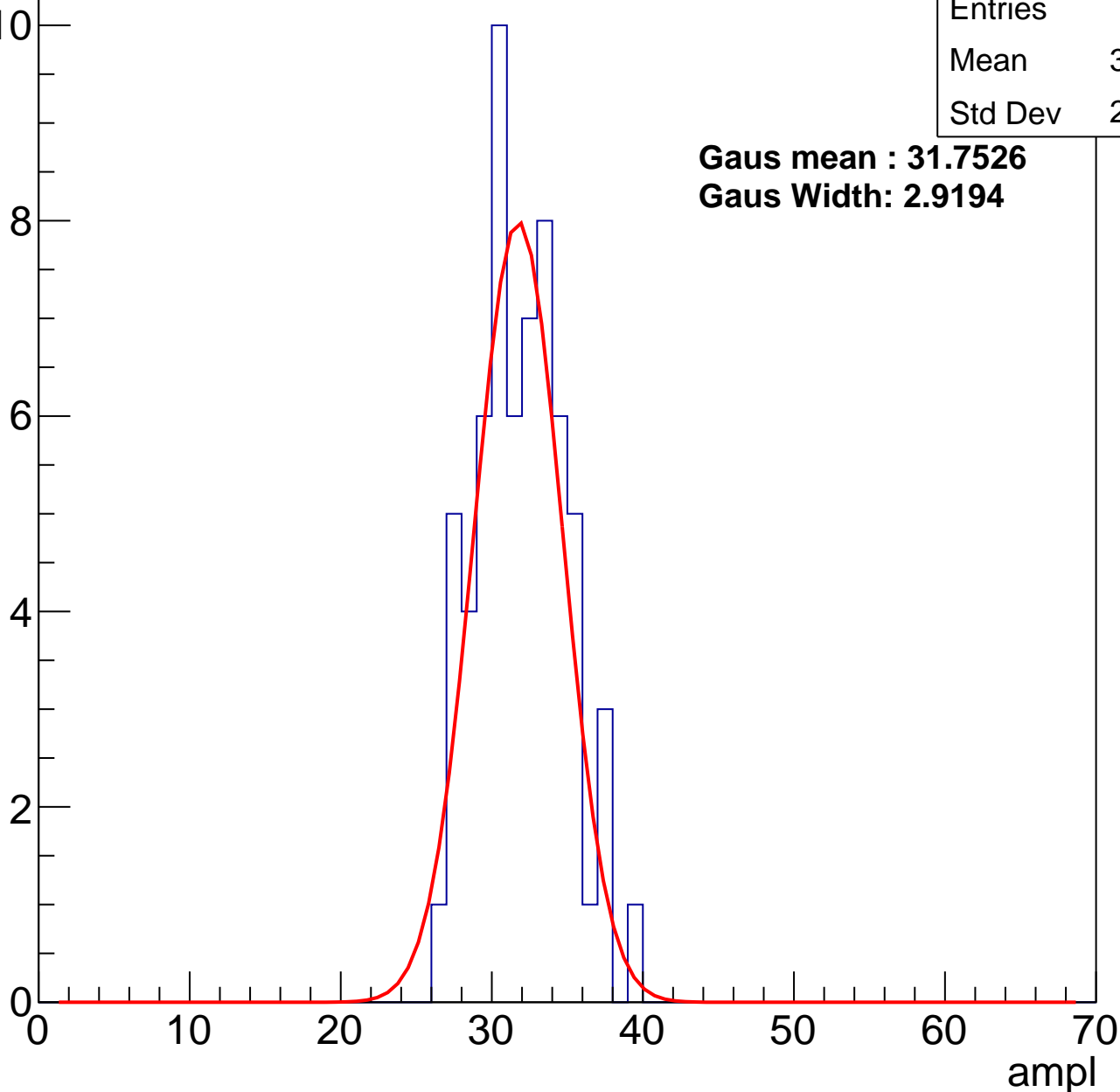
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	31.52
Std Dev	2.894

Gaus mean : 31.7526

Gaus Width: 2.9194



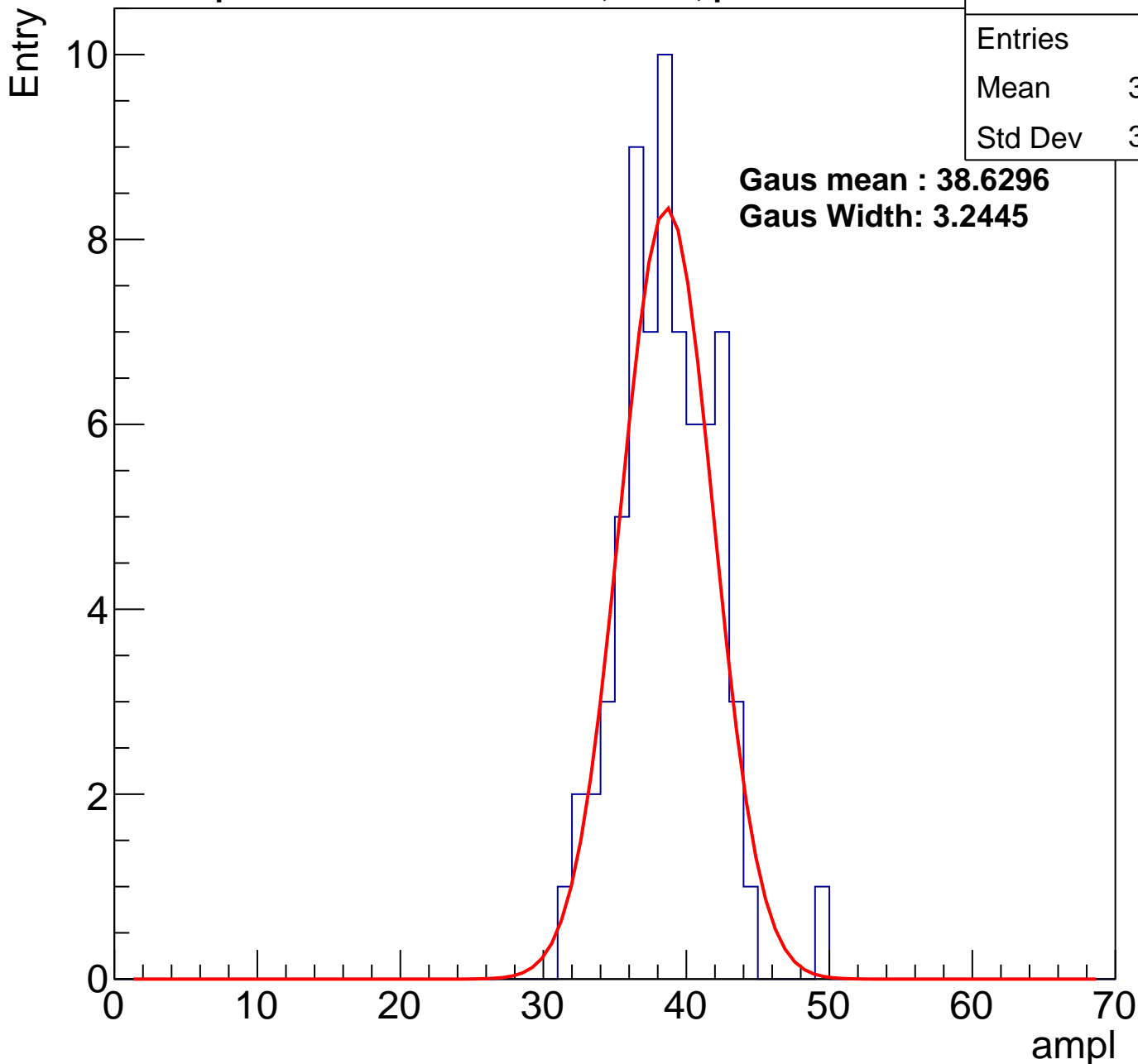
B0L002S, U9-ch25, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	38.23
Std Dev	3.235

Gaus mean : 38.6296

Gaus Width: 3.2445



B0L002S, U9-ch25, adc2

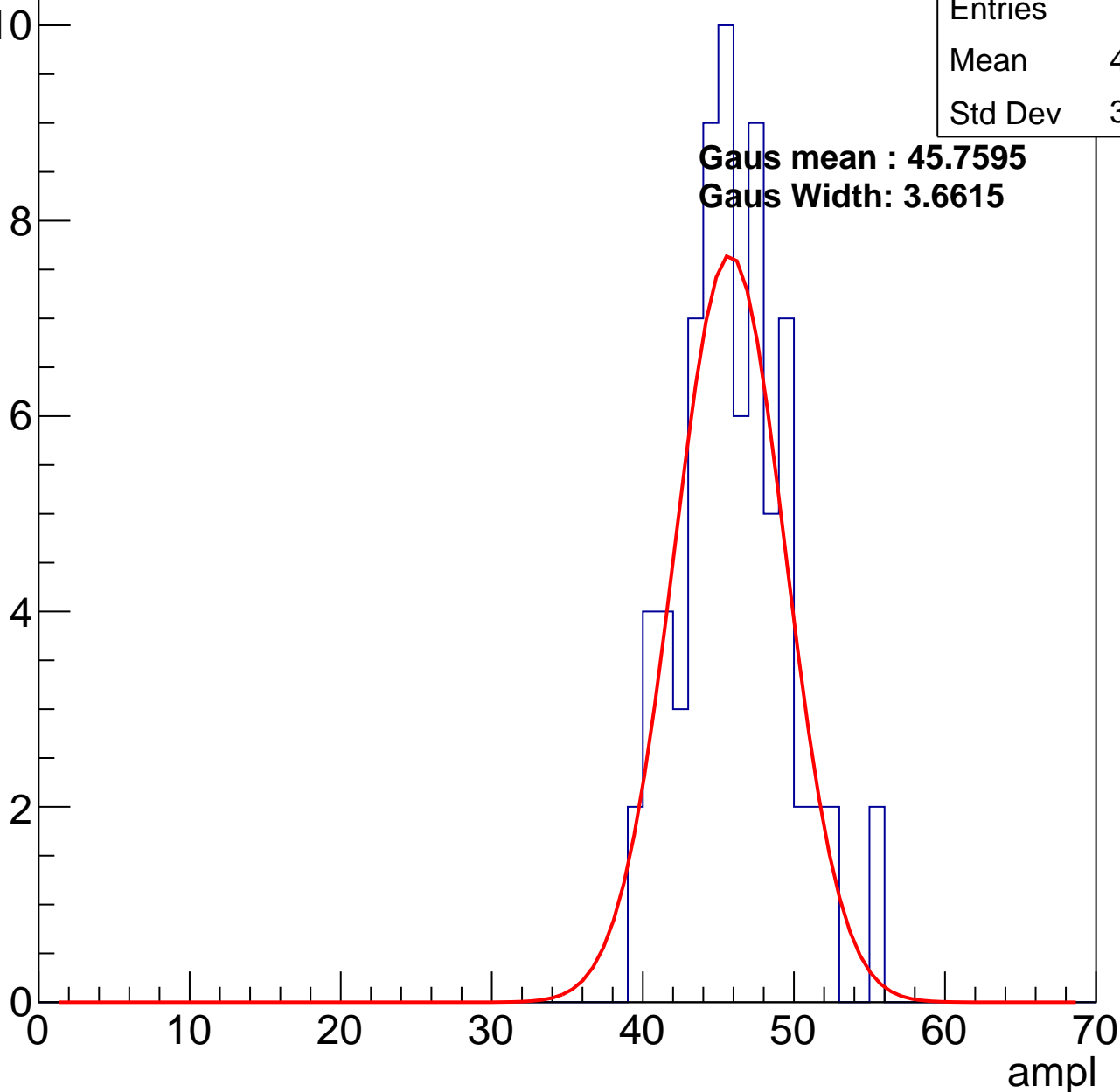
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	45.58
Std Dev	3.476

Gaus mean : 45.7595

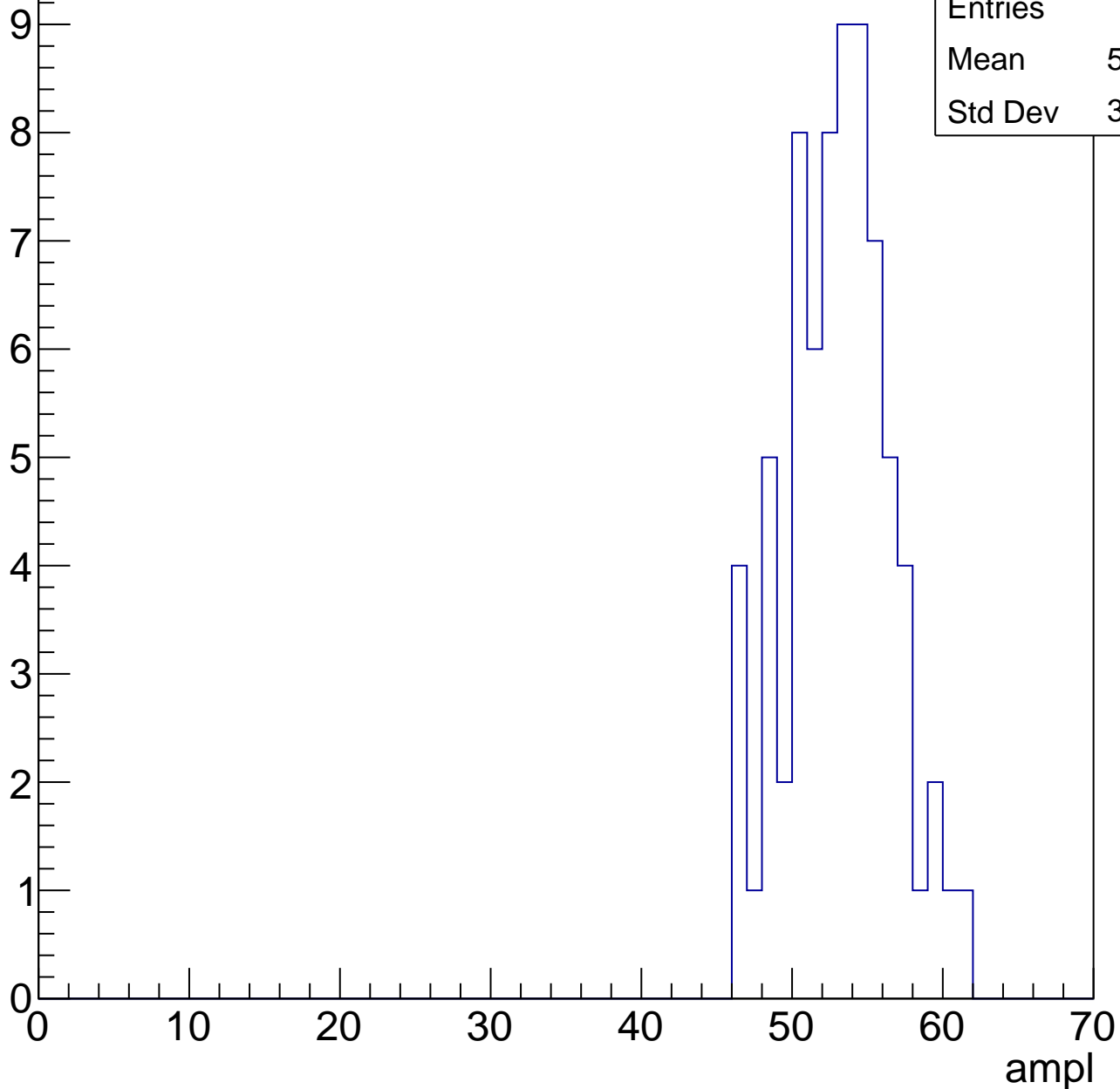
Gaus Width: 3.6615



B0L002S, U9-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

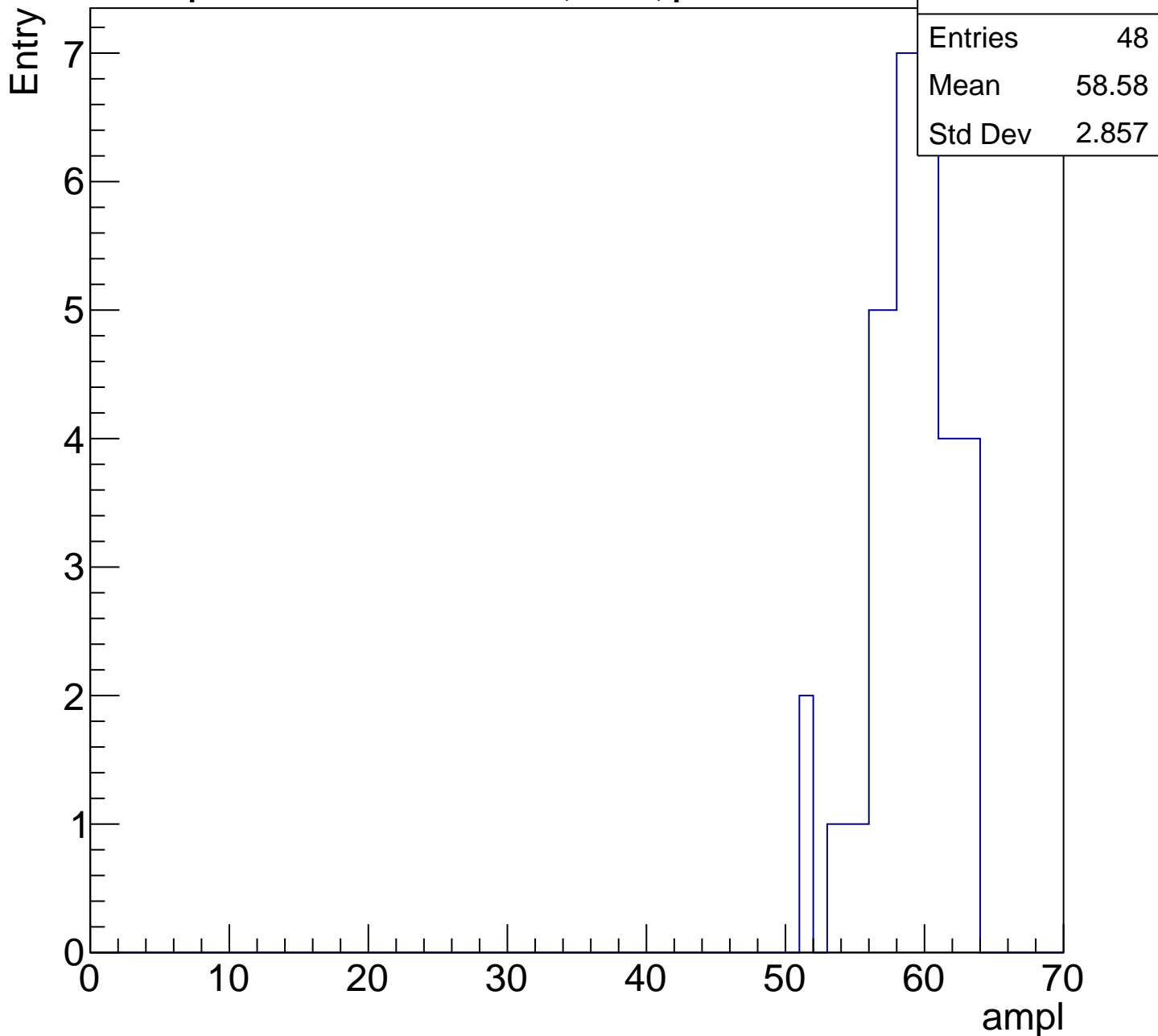
Entry



Entries	73
Mean	52.66
Std Dev	3.393

B0L002S, U9-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

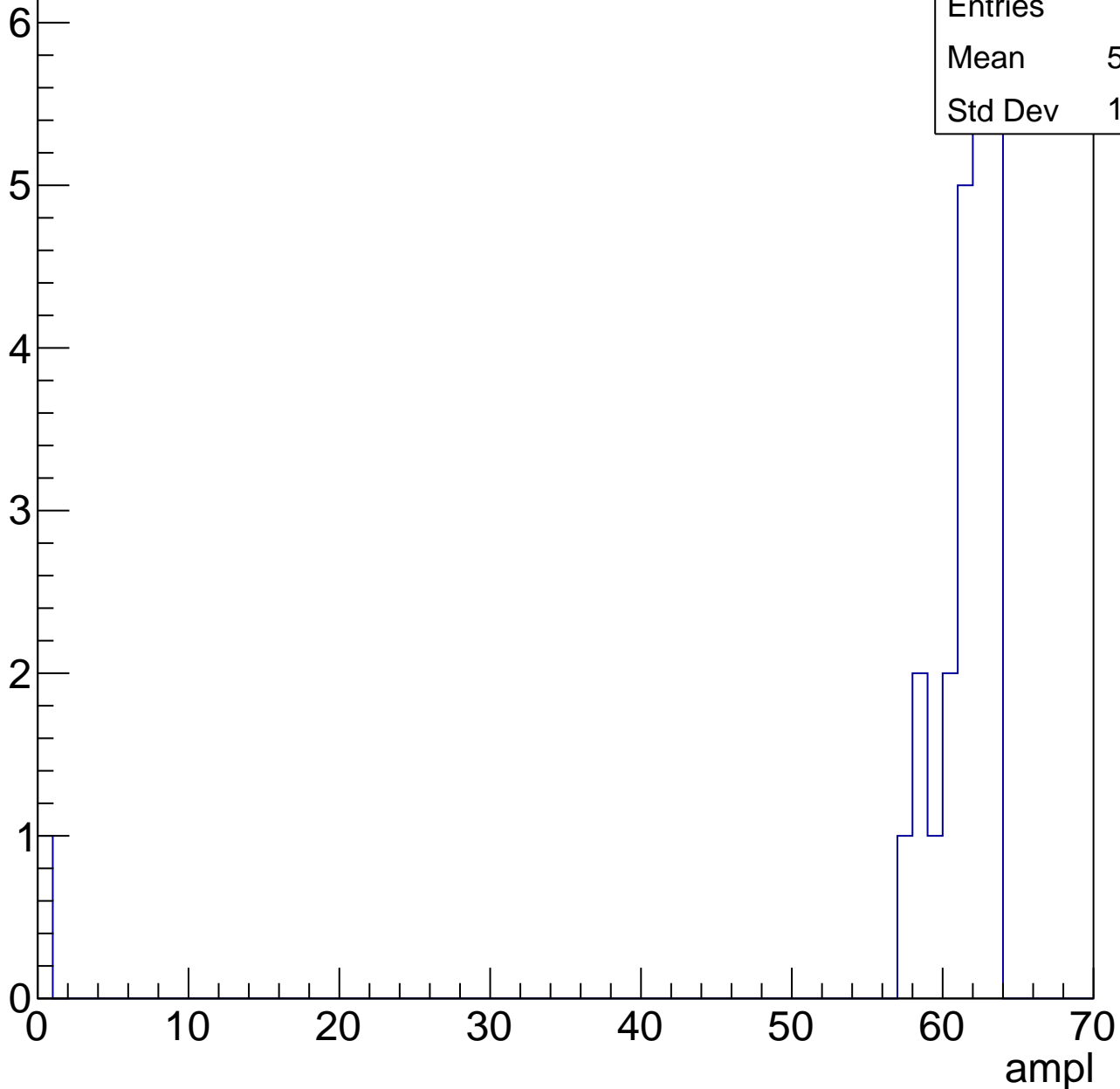


B0L002S, U9-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.62
Std Dev	12.34



B0L002S, U9-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch25, adc7

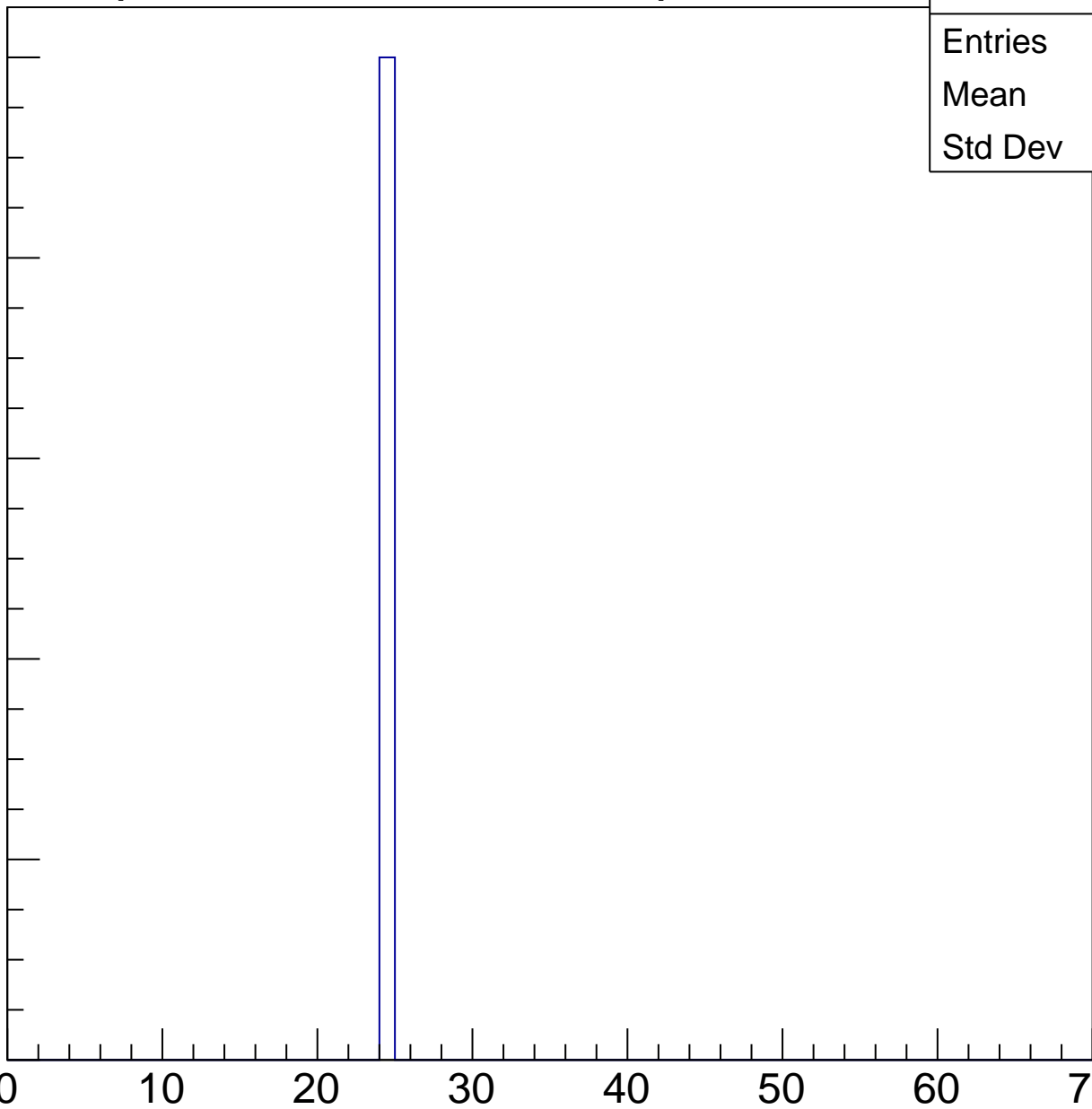
calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B0L002S, U9-ch26, adc0

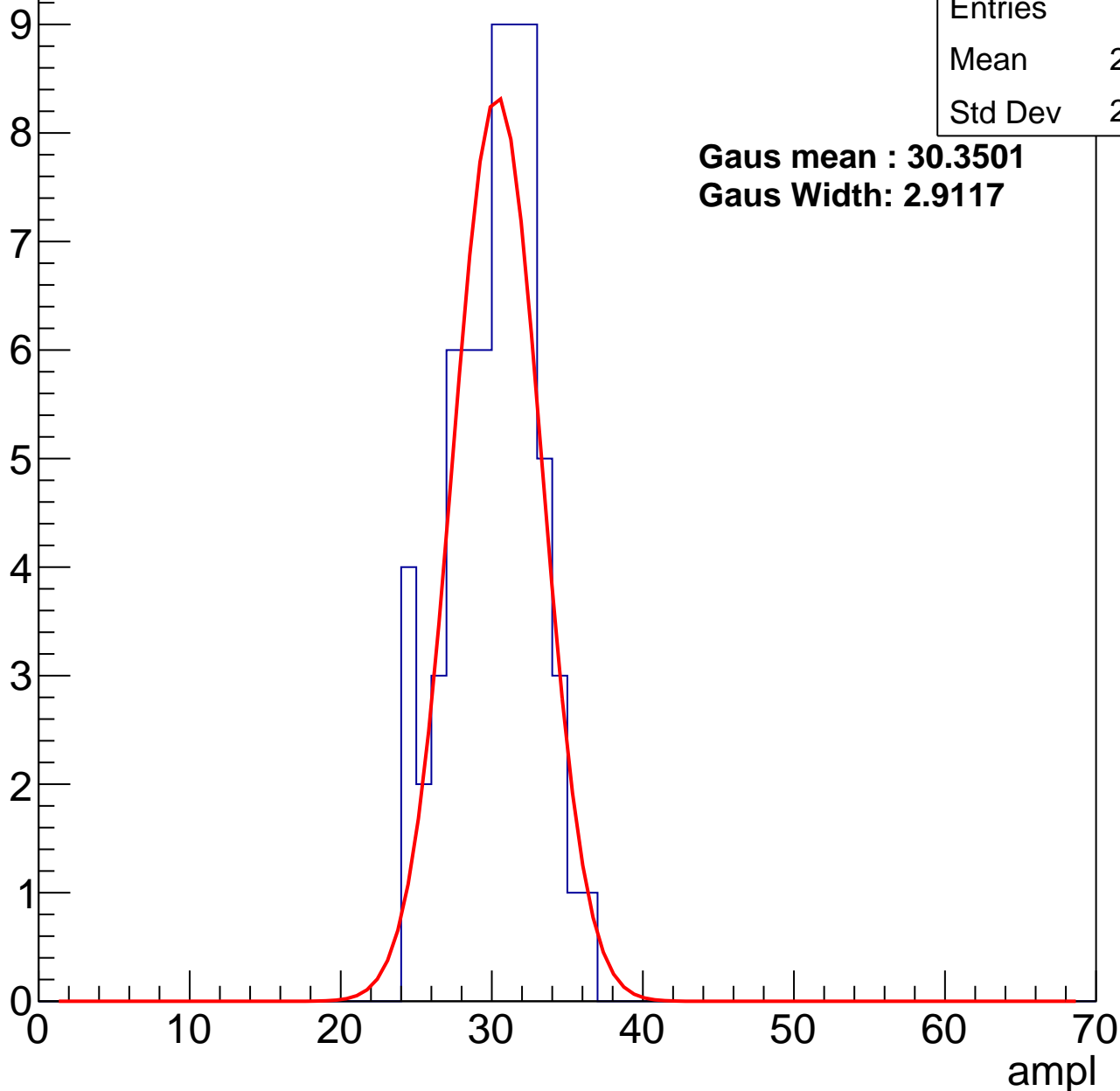
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.73
Std Dev	2.852

Gaus mean : 30.3501

Gaus Width: 2.9117



B0L002S, U9-ch26, adc1

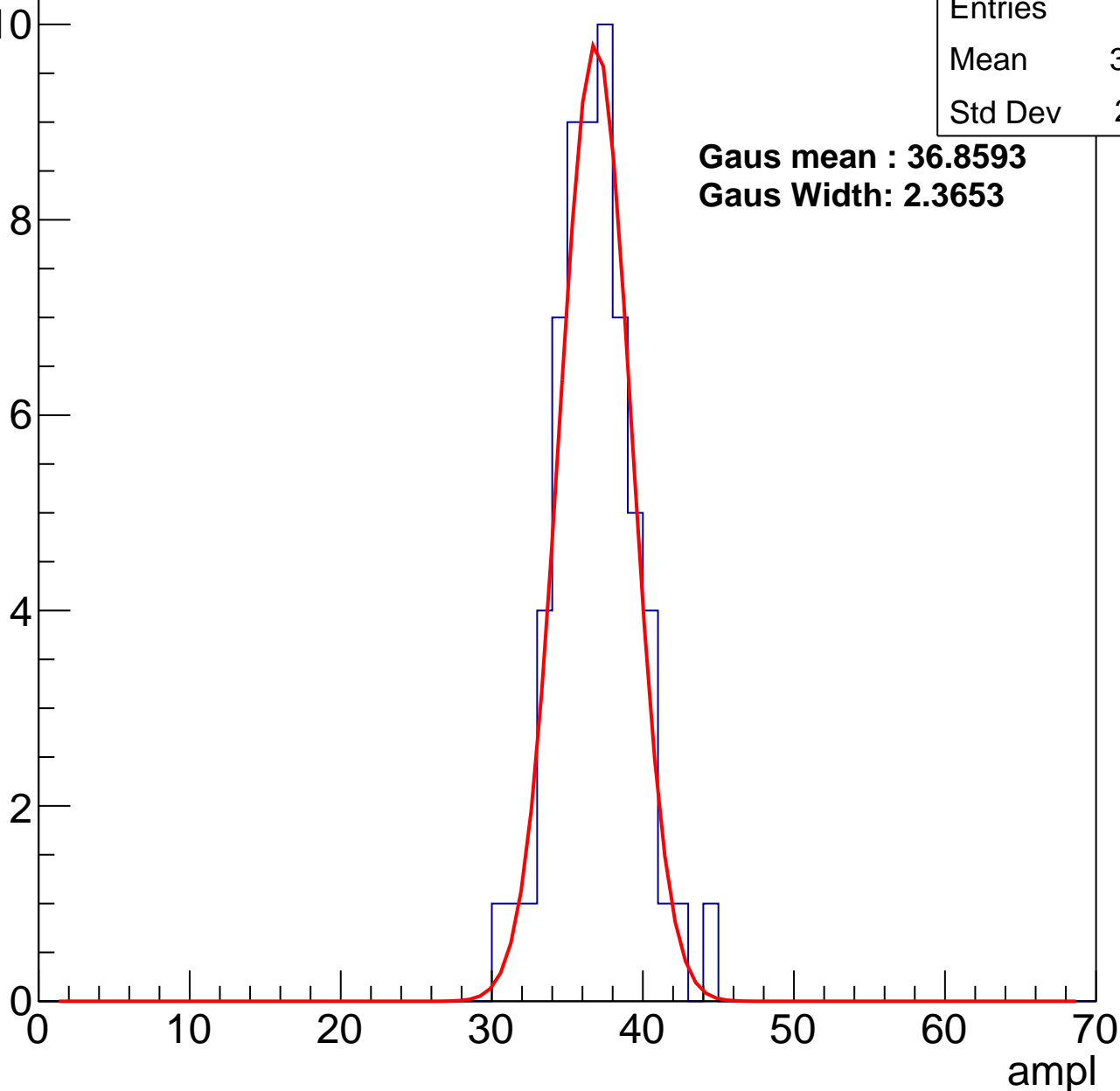
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.39
Std Dev	2.601

Gaus mean : 36.8593

Gaus Width: 2.3653



B0L002S, U9-ch26, adc2

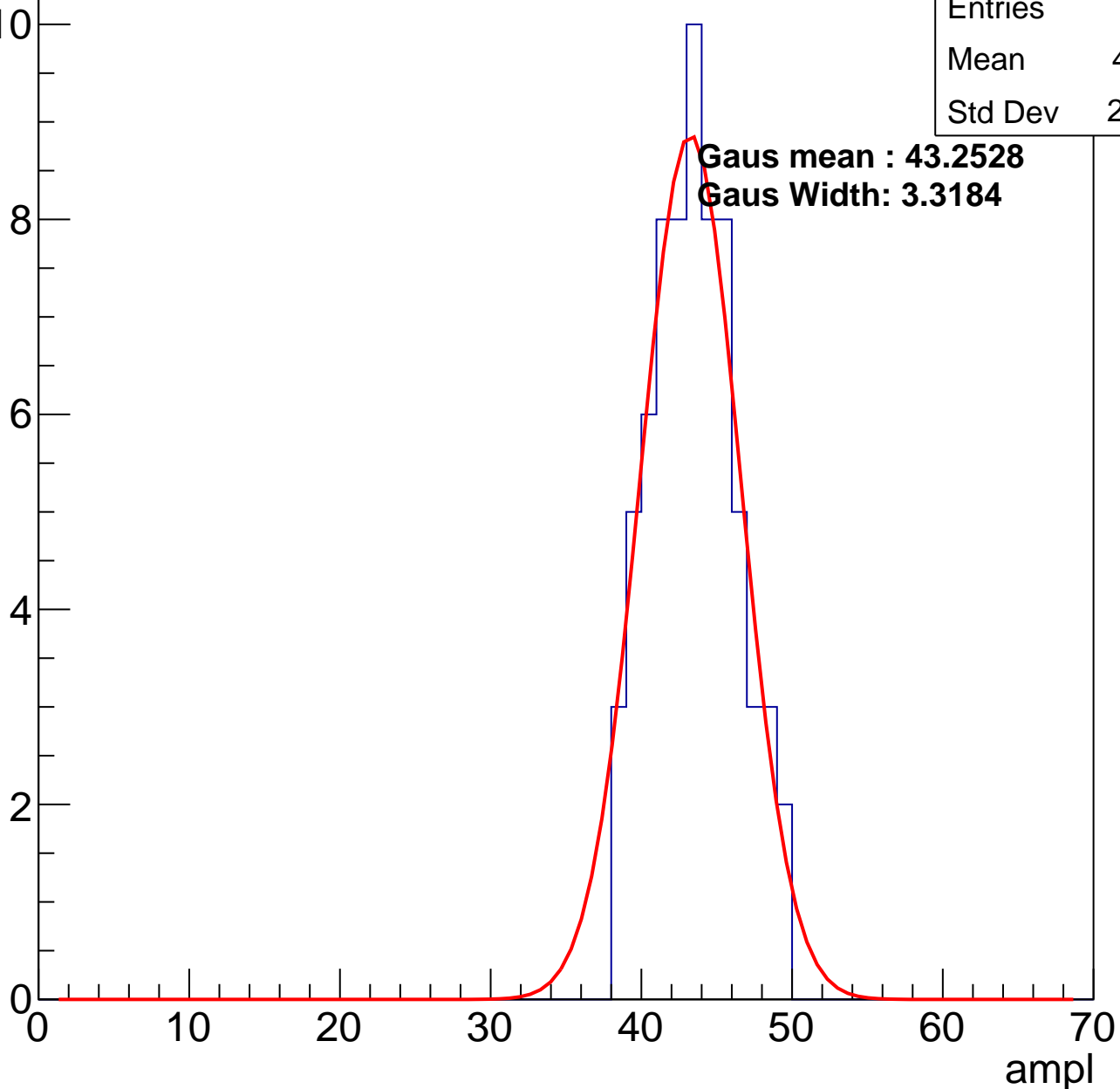
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.01
Std Dev	2.769

Gaus mean : 43.2528

Gaus Width: 3.3184

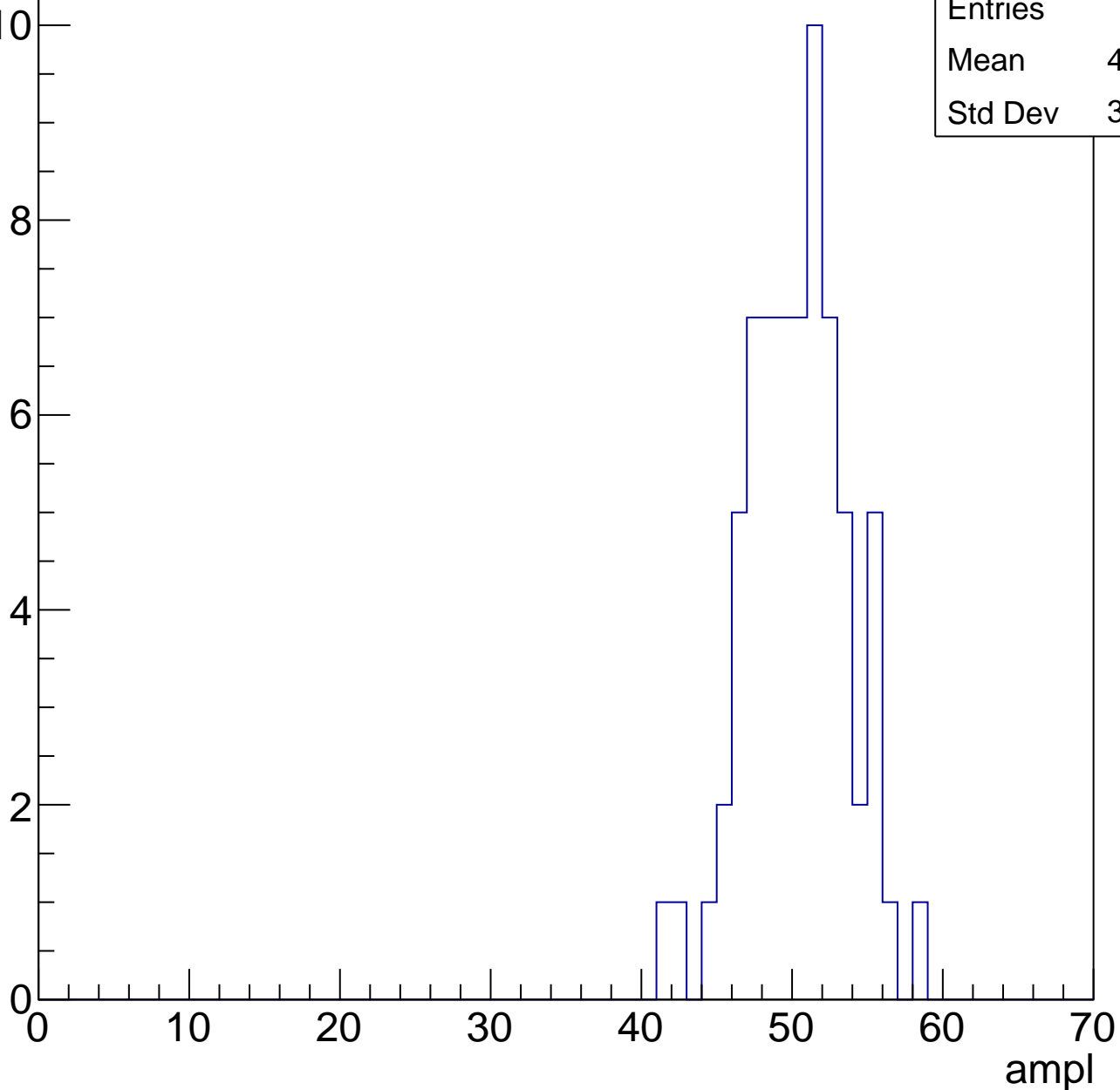


B0L002S, U9-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.87
Std Dev	3.292

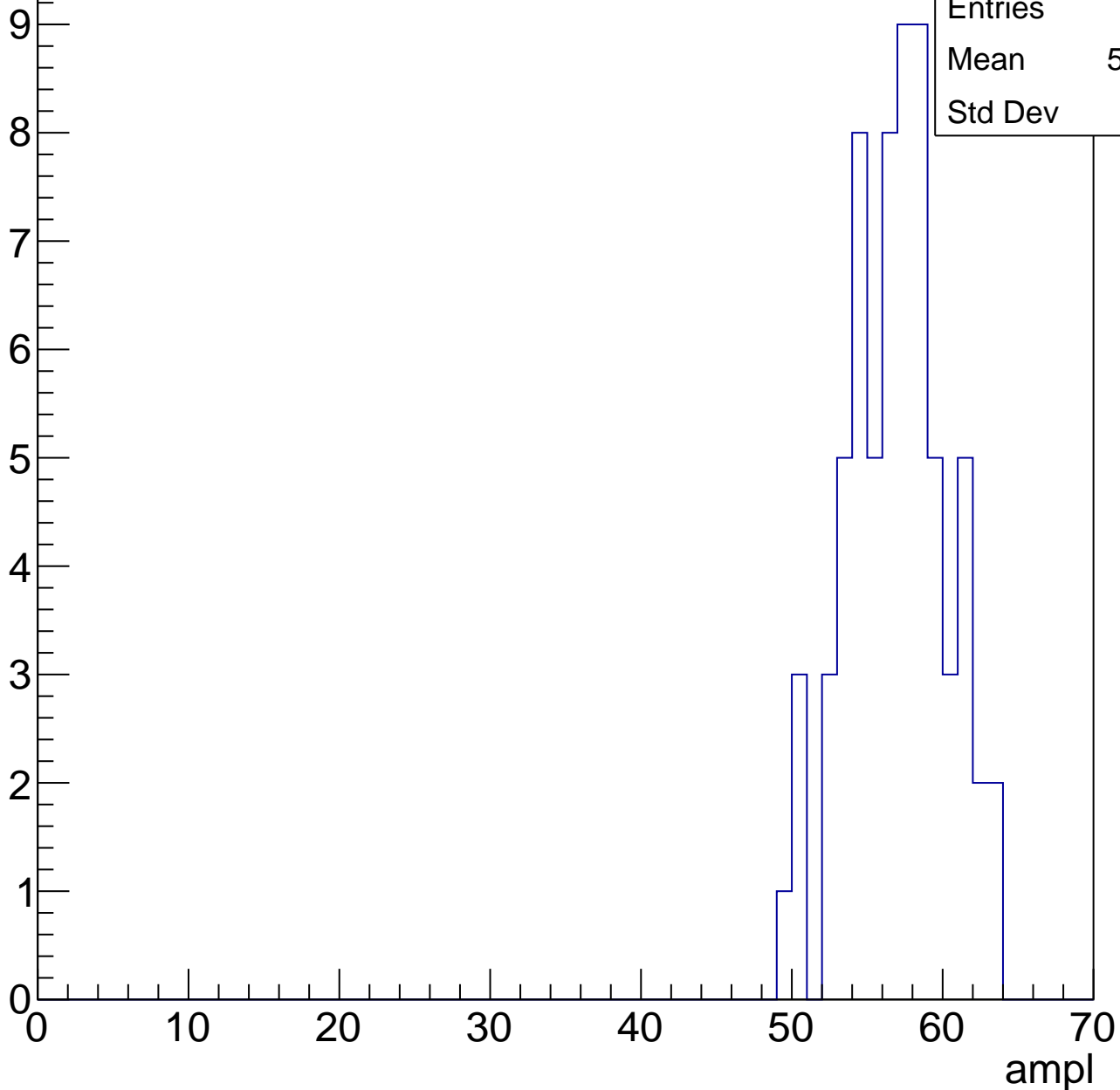


B0L002S, U9-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1

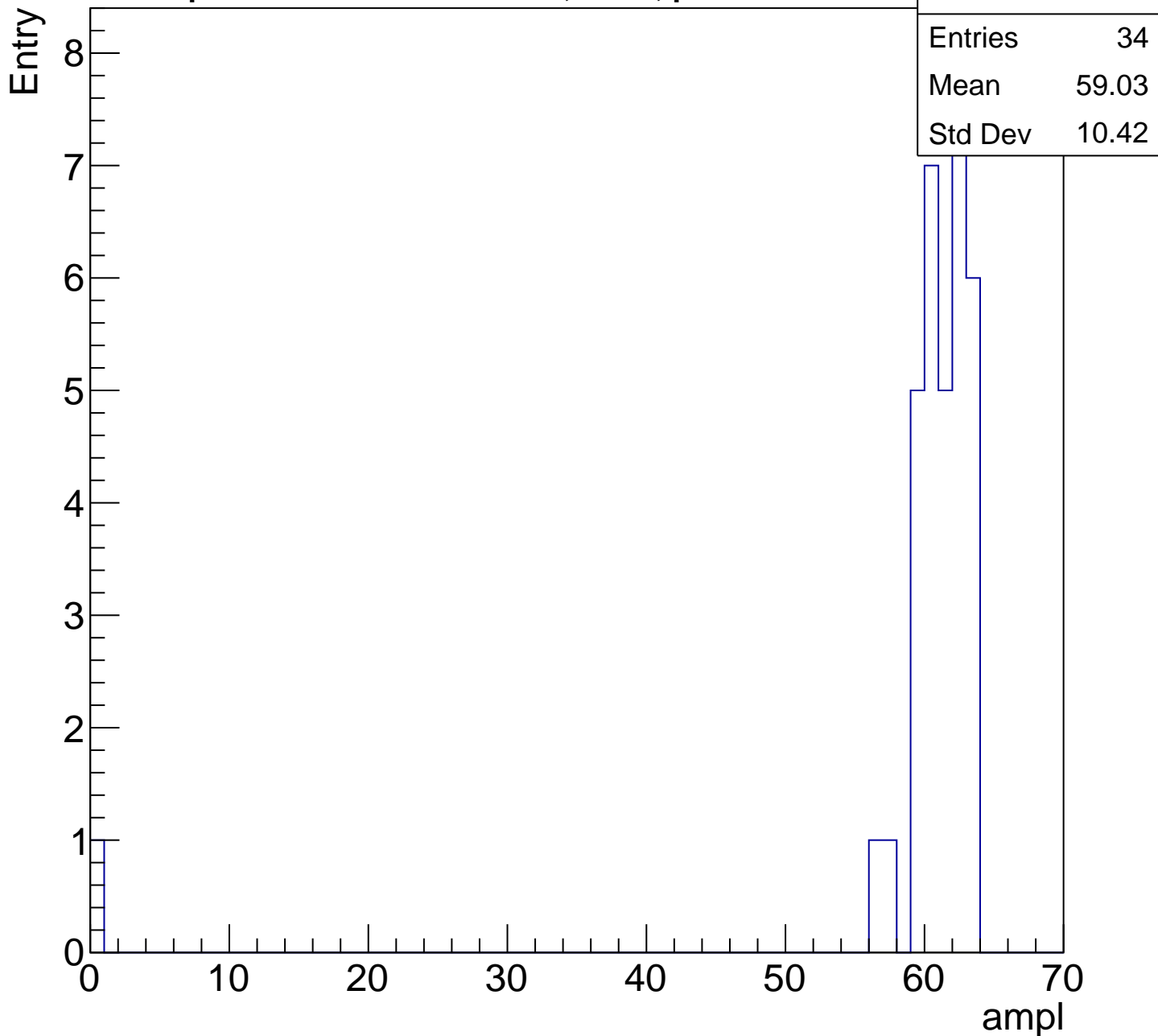
Entry

Entries	68
Mean	56.47
Std Dev	3.22



B0L002S, U9-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U9-ch27, adc0

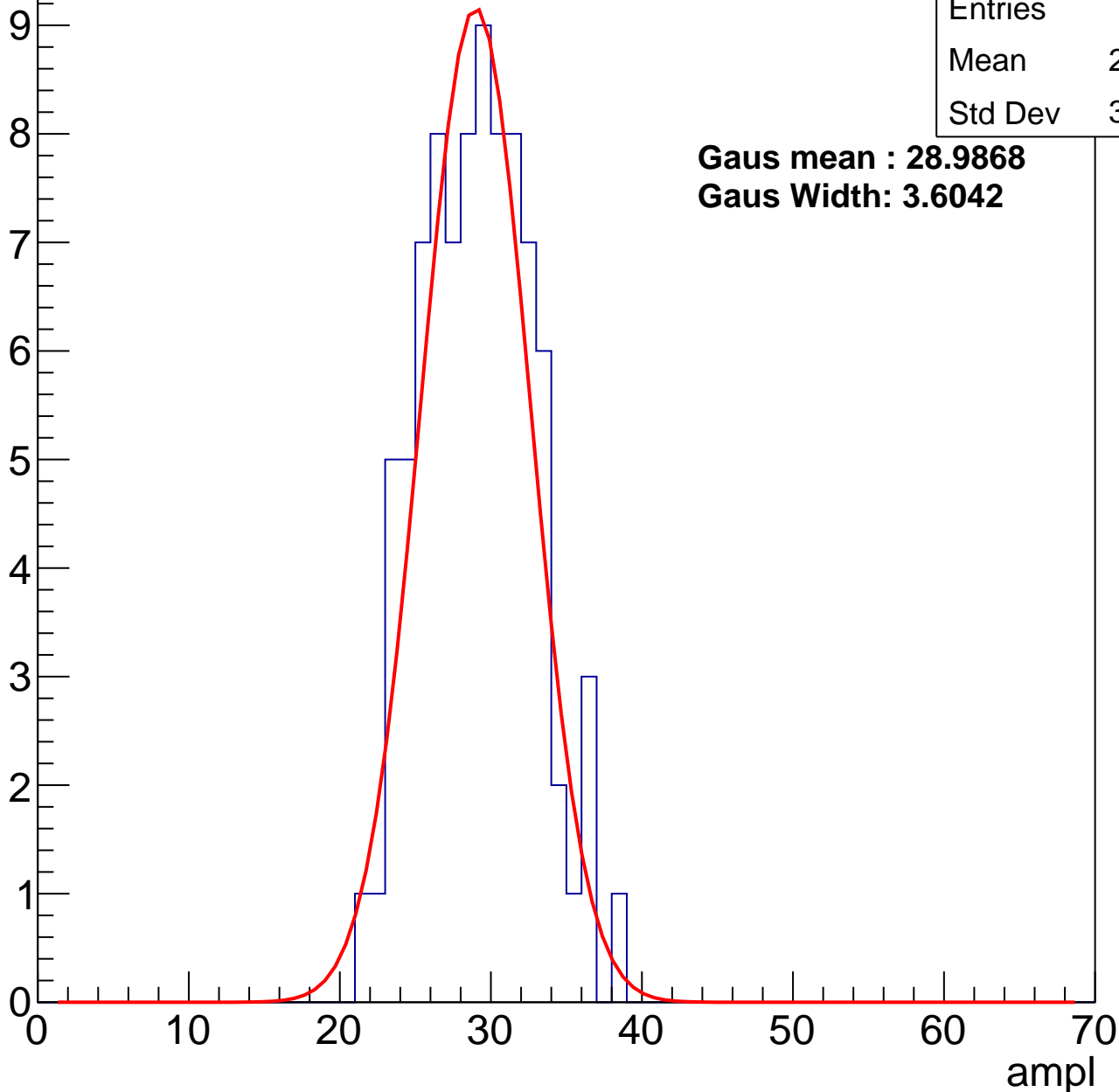
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	28.67
Std Dev	3.622

Gaus mean : 28.9868

Gaus Width: 3.6042



B0L002S, U9-ch27, adc1

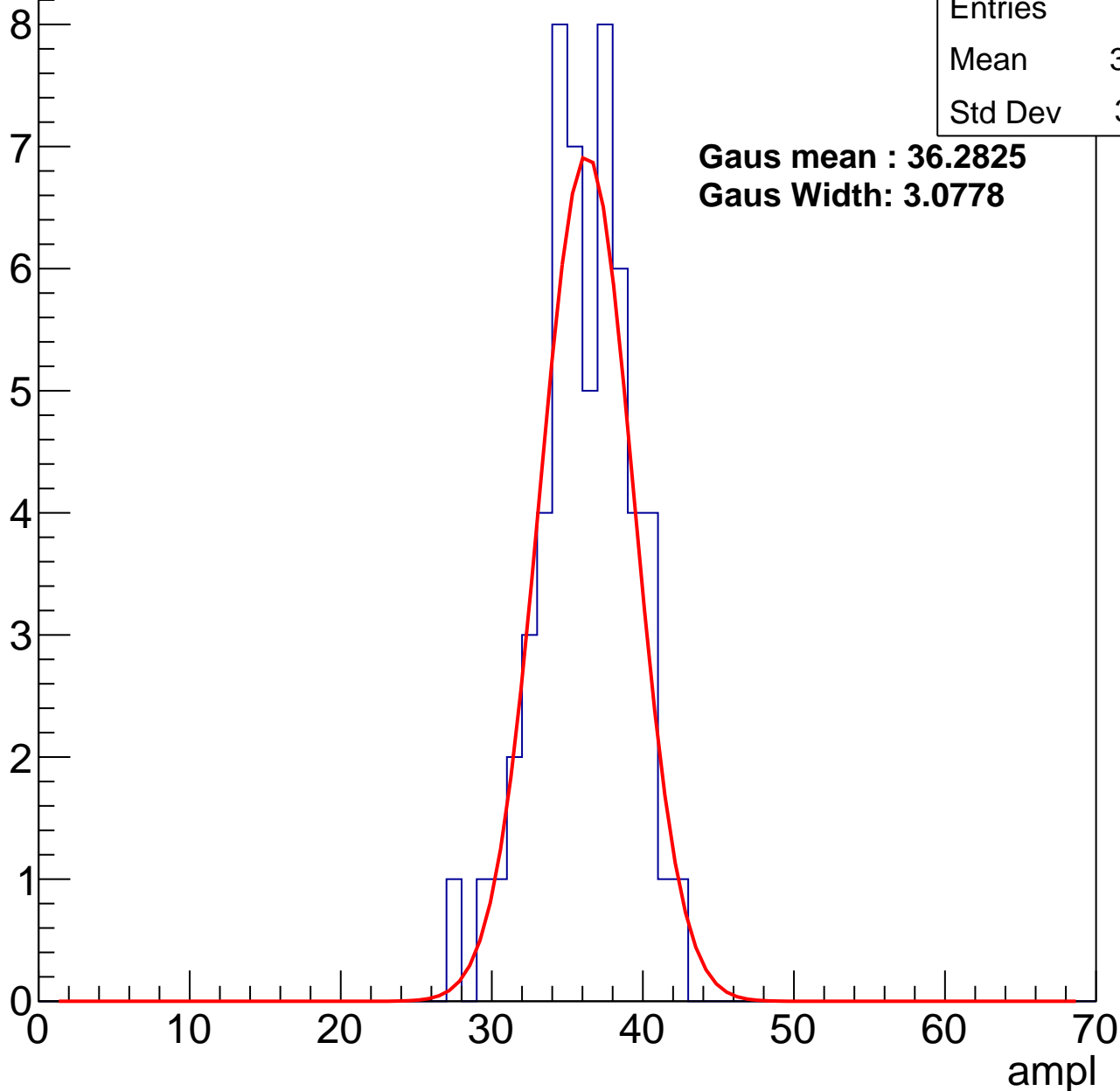
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	35.64
Std Dev	3.061

Gaus mean : 36.2825

Gaus Width: 3.0778



B0L002S, U9-ch27, adc2

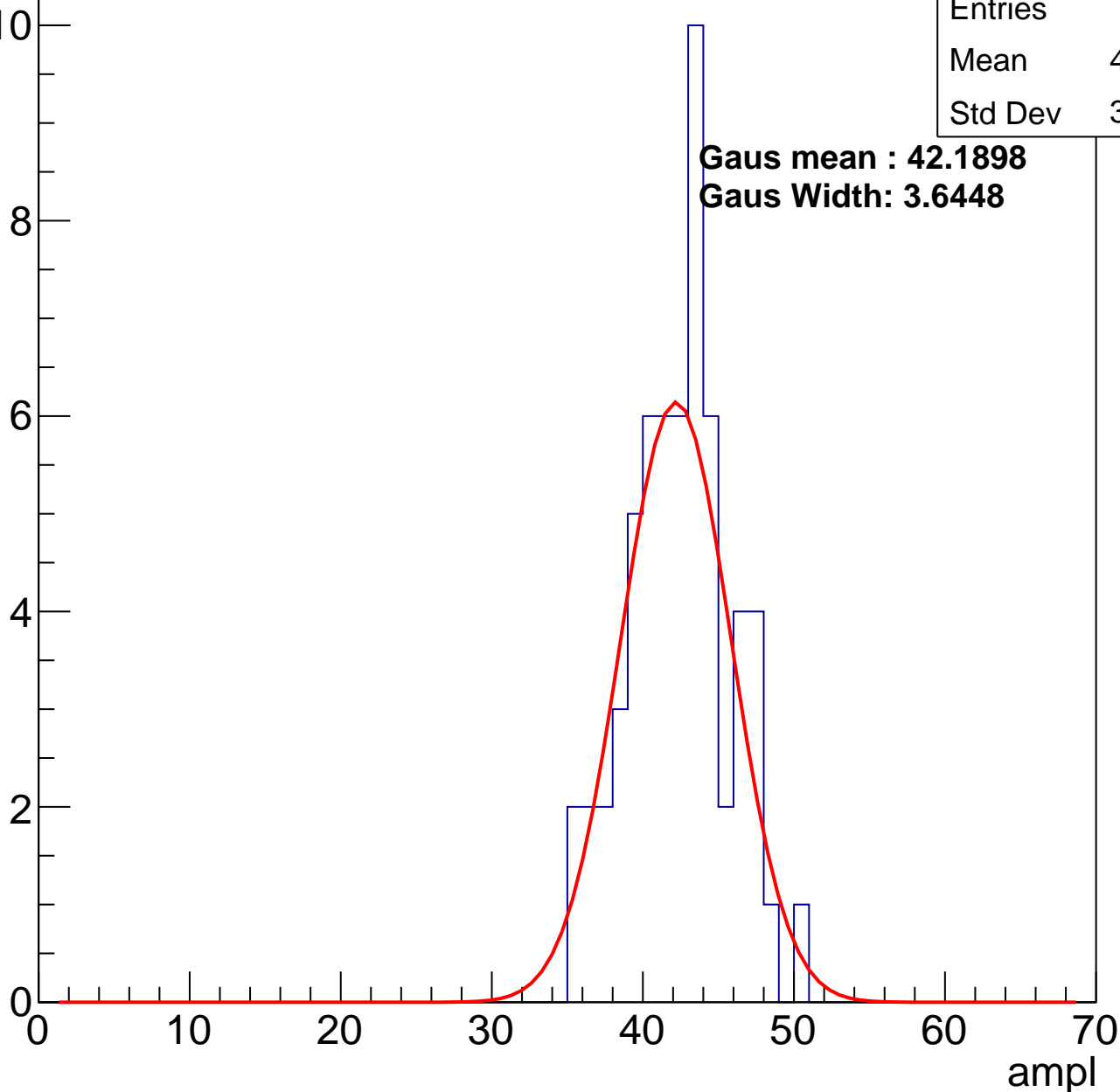
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	41.95
Std Dev	3.314

Gaus mean : 42.1898

Gaus Width: 3.6448

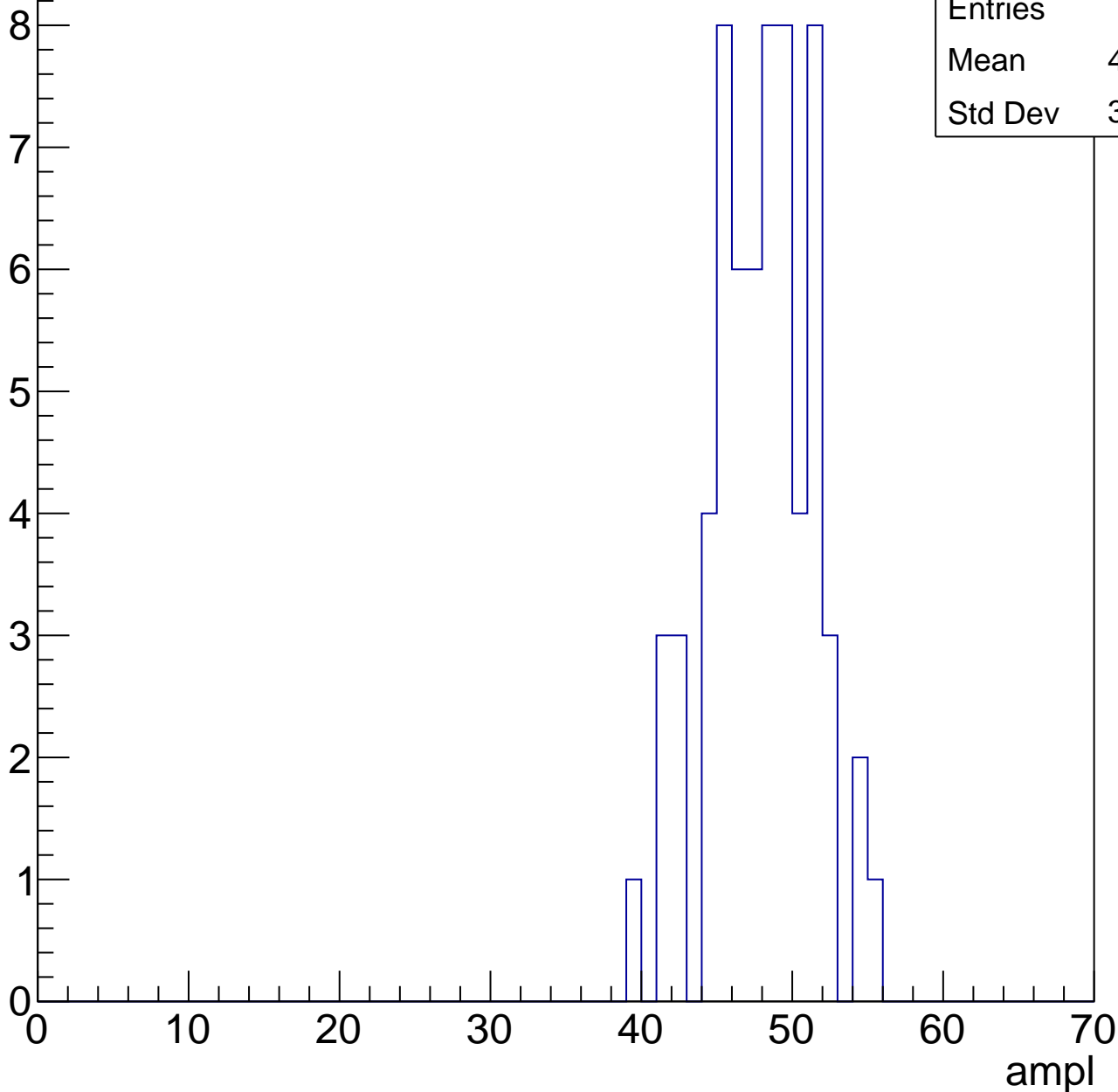


B0L002S, U9-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	47.46
Std Dev	3.393



B0L002S, U9-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

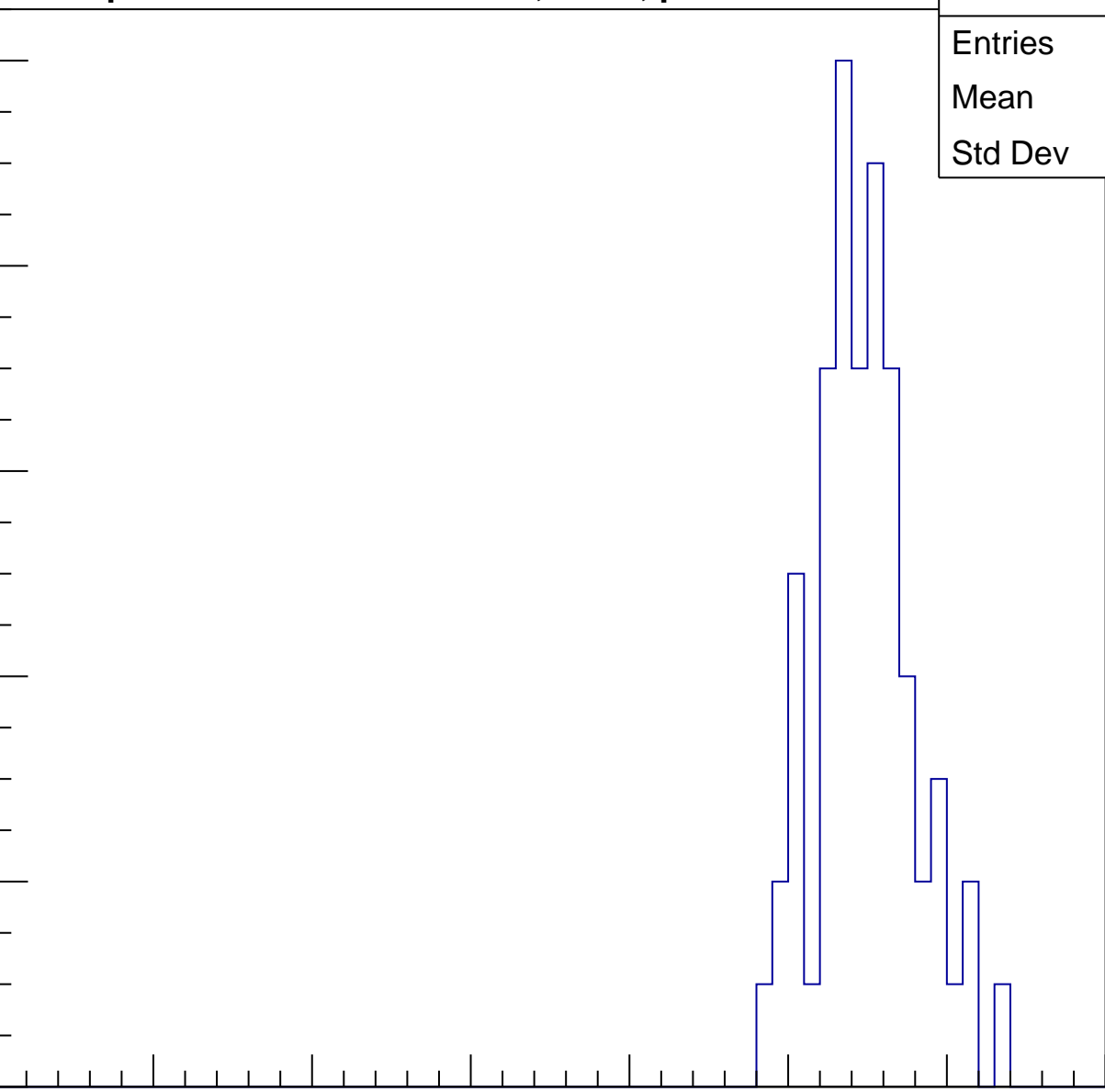
Entries	62
Mean	54.39
Std Dev	3.108

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

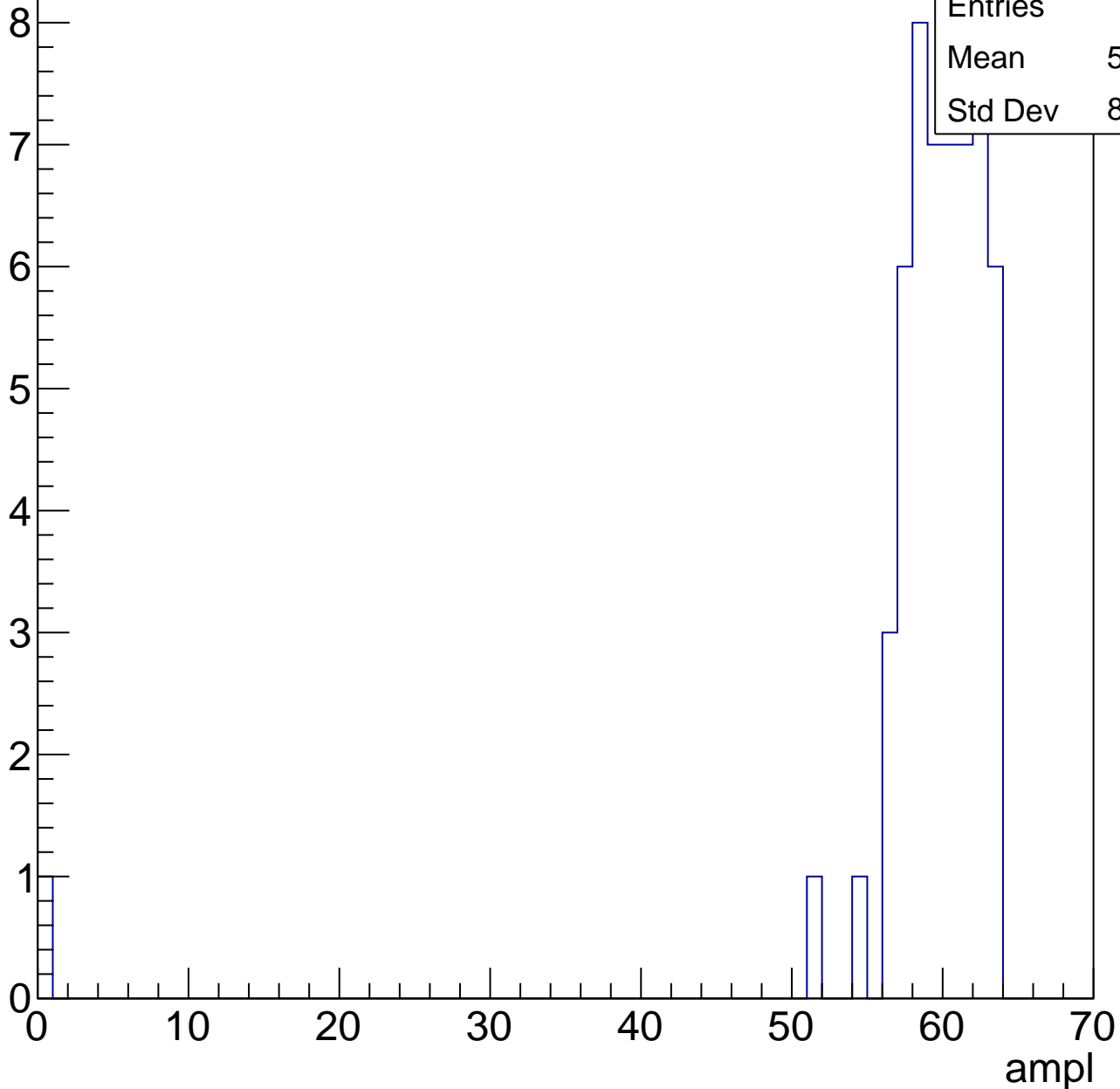


B0L002S, U9-ch27, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

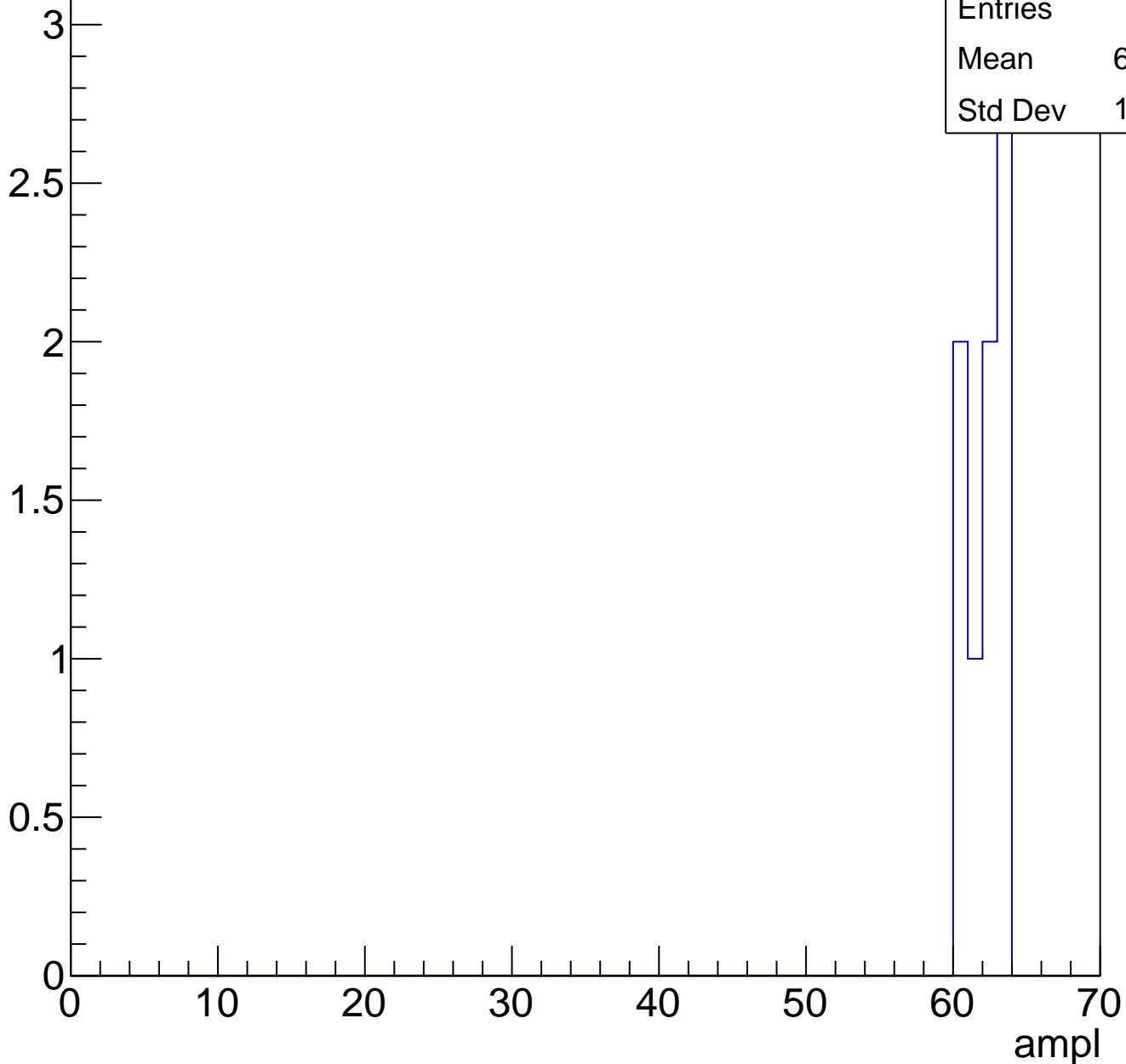
Entries	55
Mean	58.42
Std Dev	8.327



B0L002S, U9-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

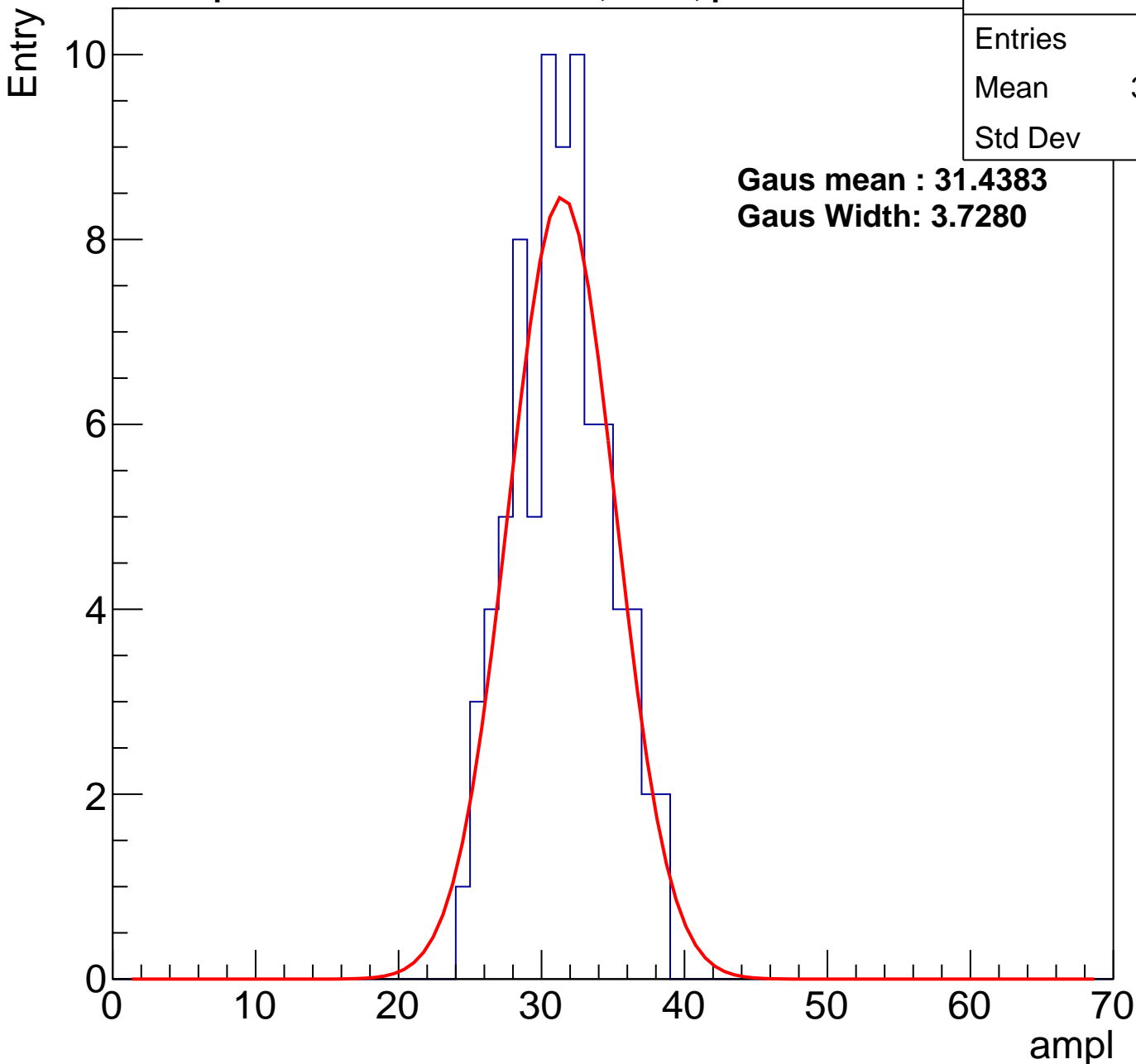
B0L002S, U9-ch28, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	30.91
Std Dev	3.3

Gaus mean : 31.4383

Gaus Width: 3.7280



B0L002S, U9-ch28, adc1

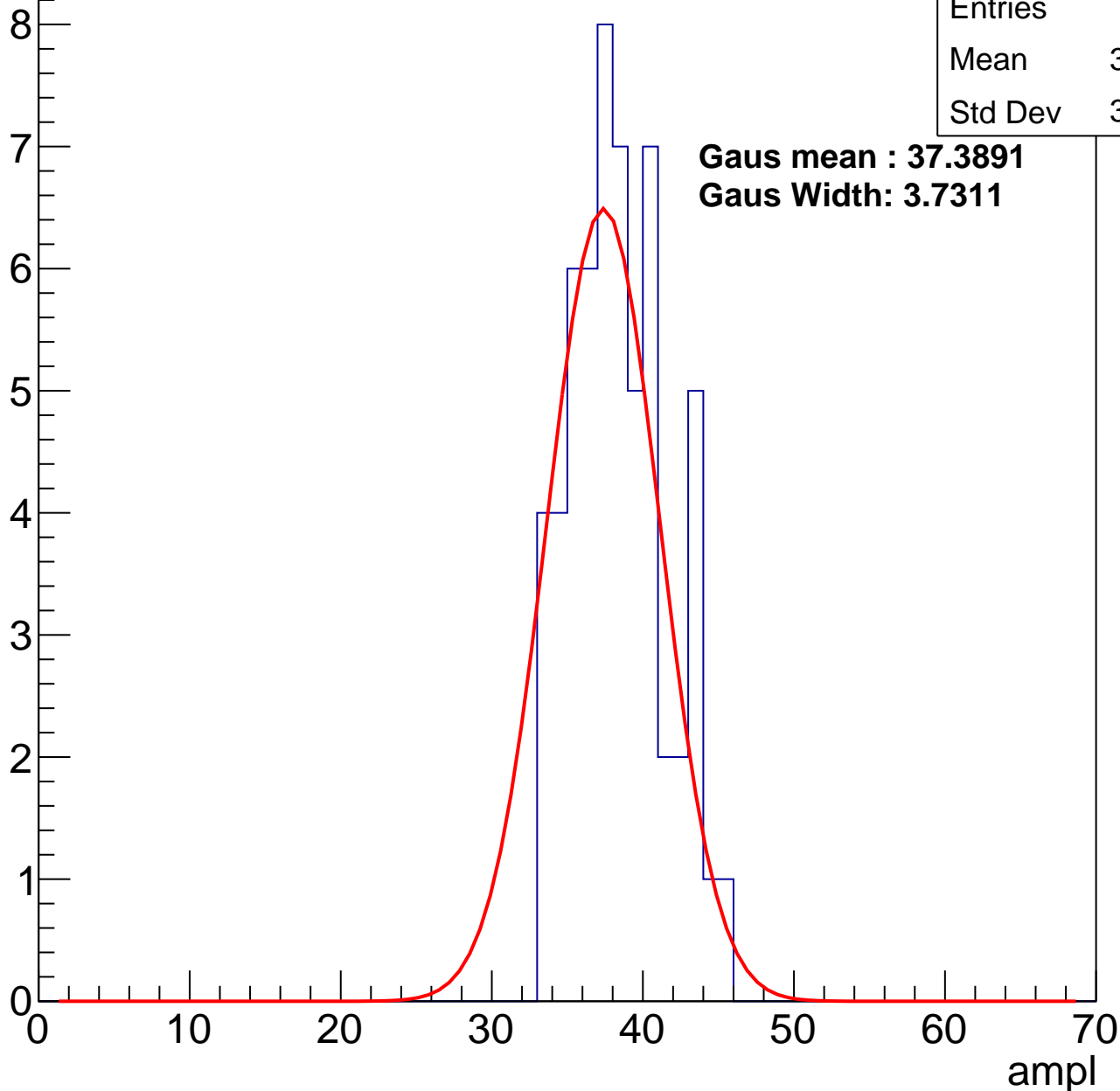
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.95
Std Dev	3.059

Gaus mean : 37.3891

Gaus Width: 3.7311



B0L002S, U9-ch28, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.08
Std Dev	2.98

Gaus mean : 44.7969

Gaus Width: 3.3890

10

8

6

4

2

0

0

2

4

6

8

ampl

0

10

20

30

40

50

60

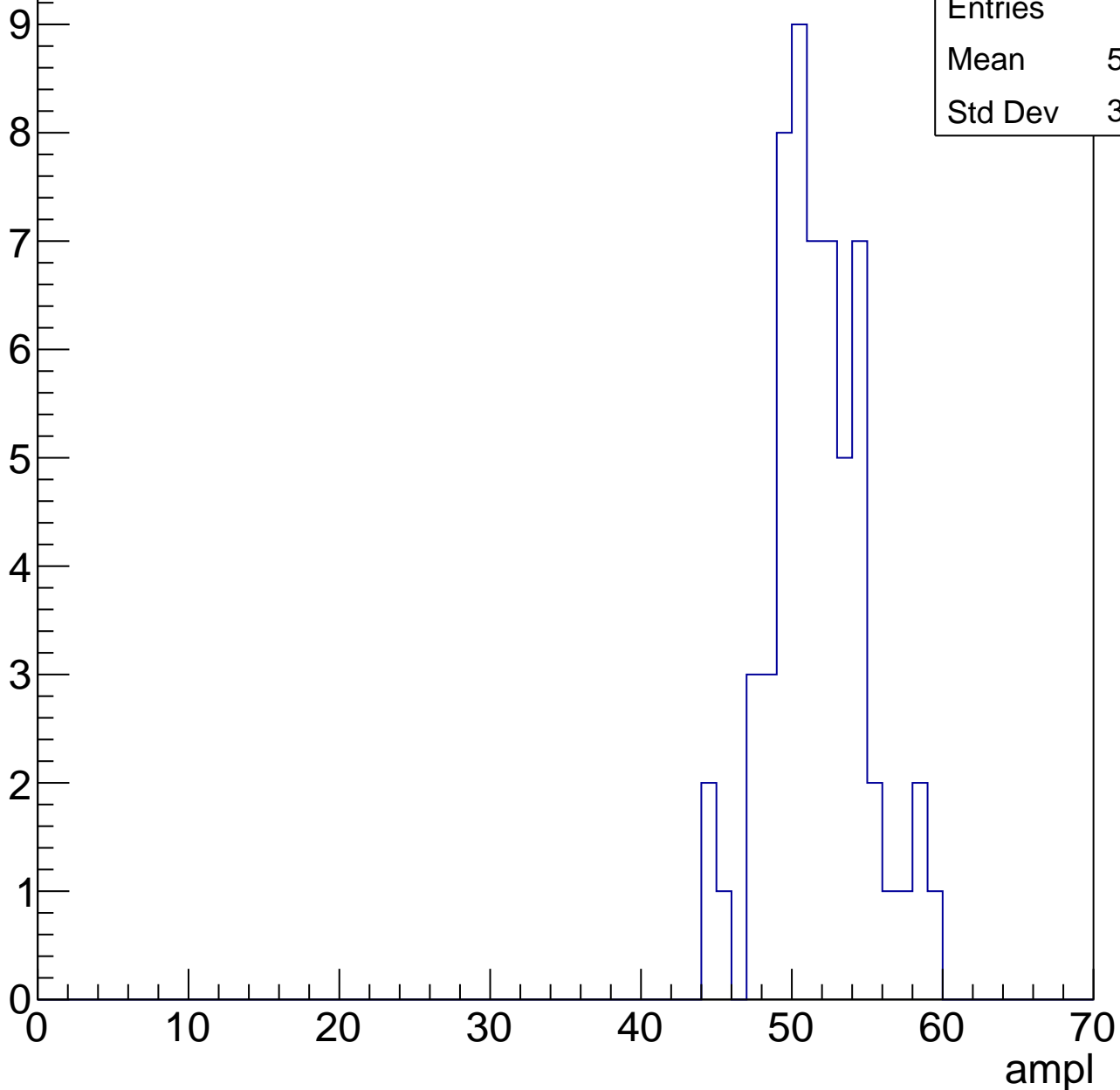
70

B0L002S, U9-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

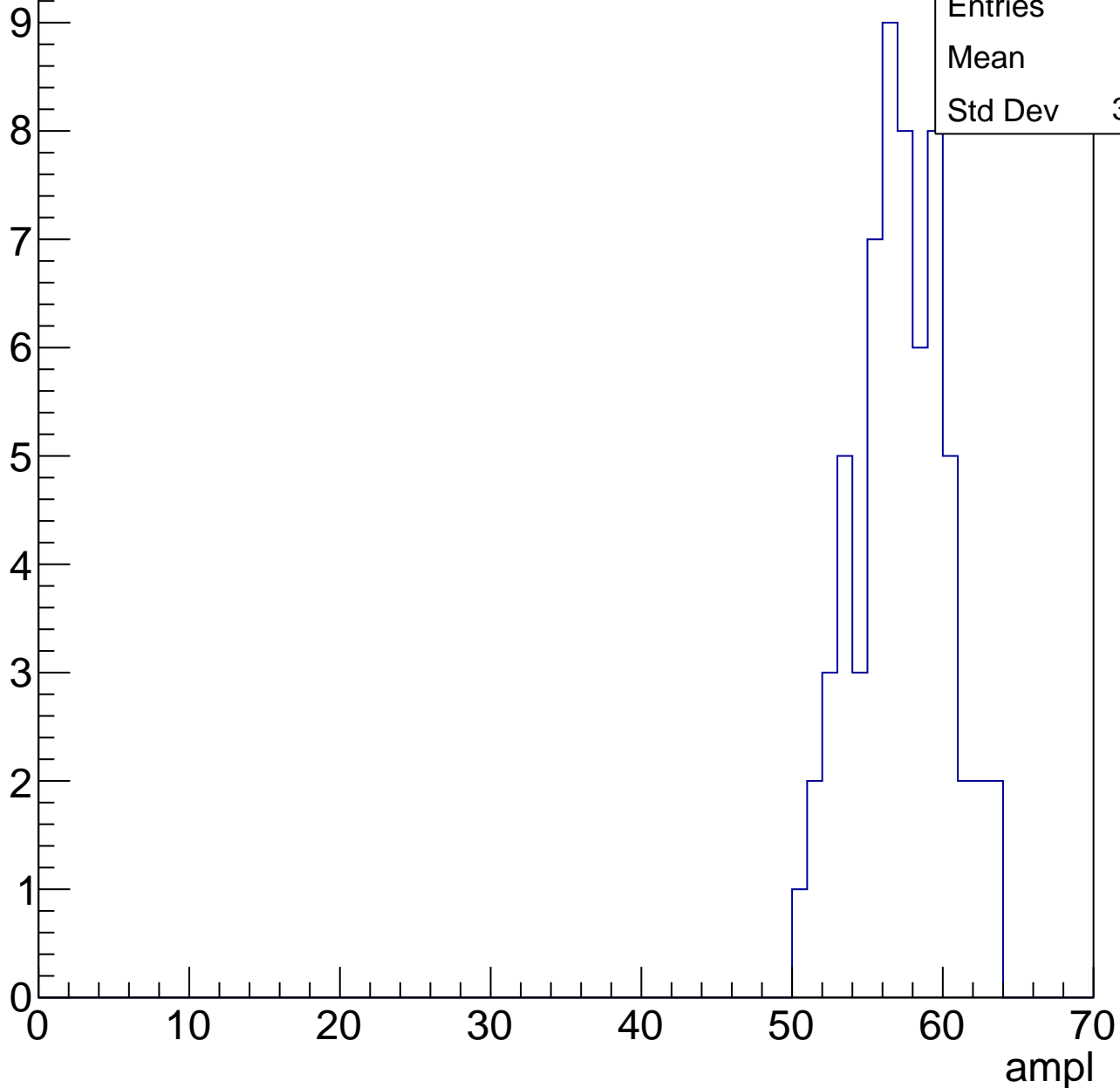
Entries	59
Mean	51.22
Std Dev	3.168



B0L002S, U9-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

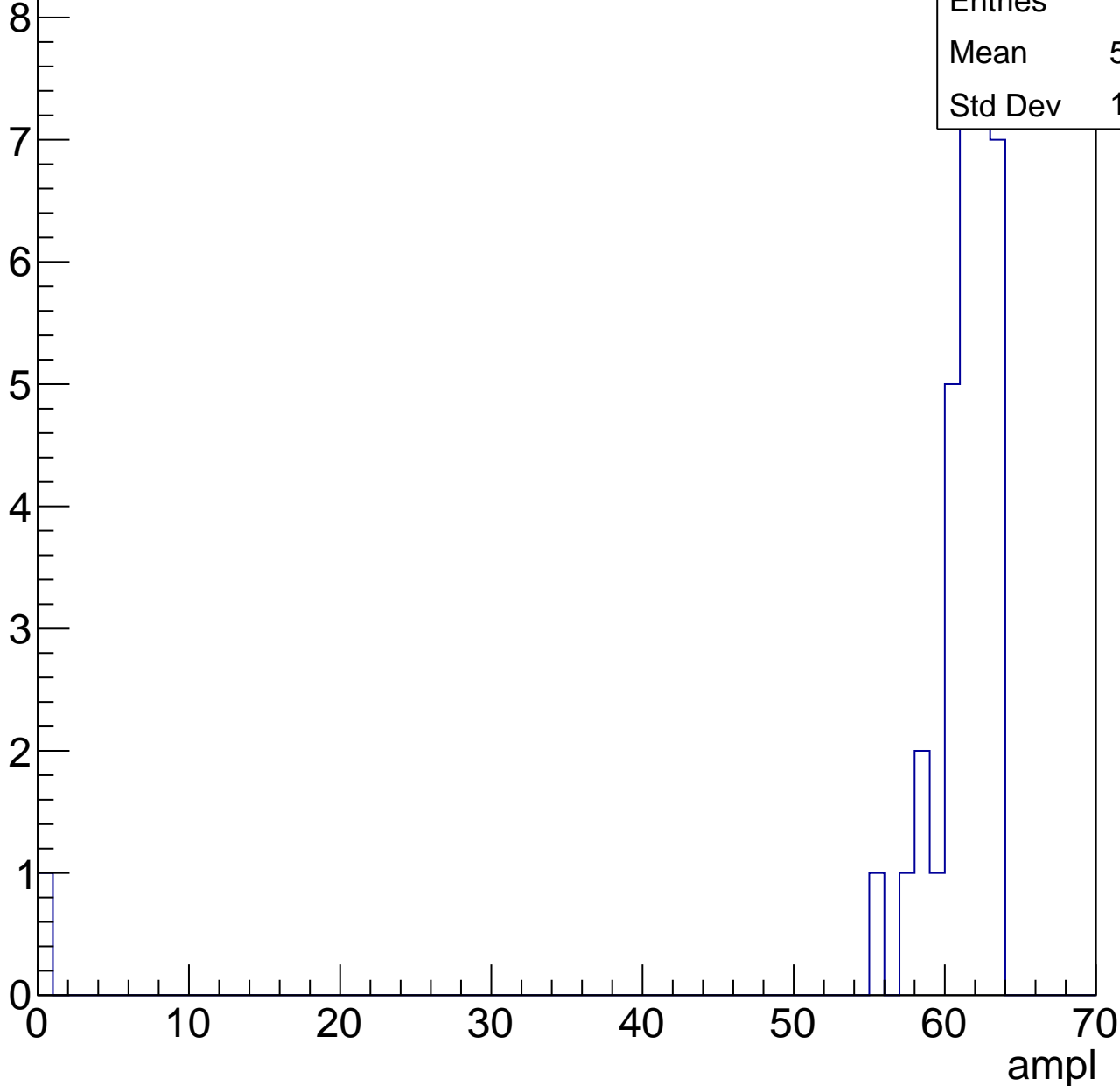


B0L002S, U9-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59.18
Std Dev	10.46



B0L002S, U9-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U9-ch29, adc0

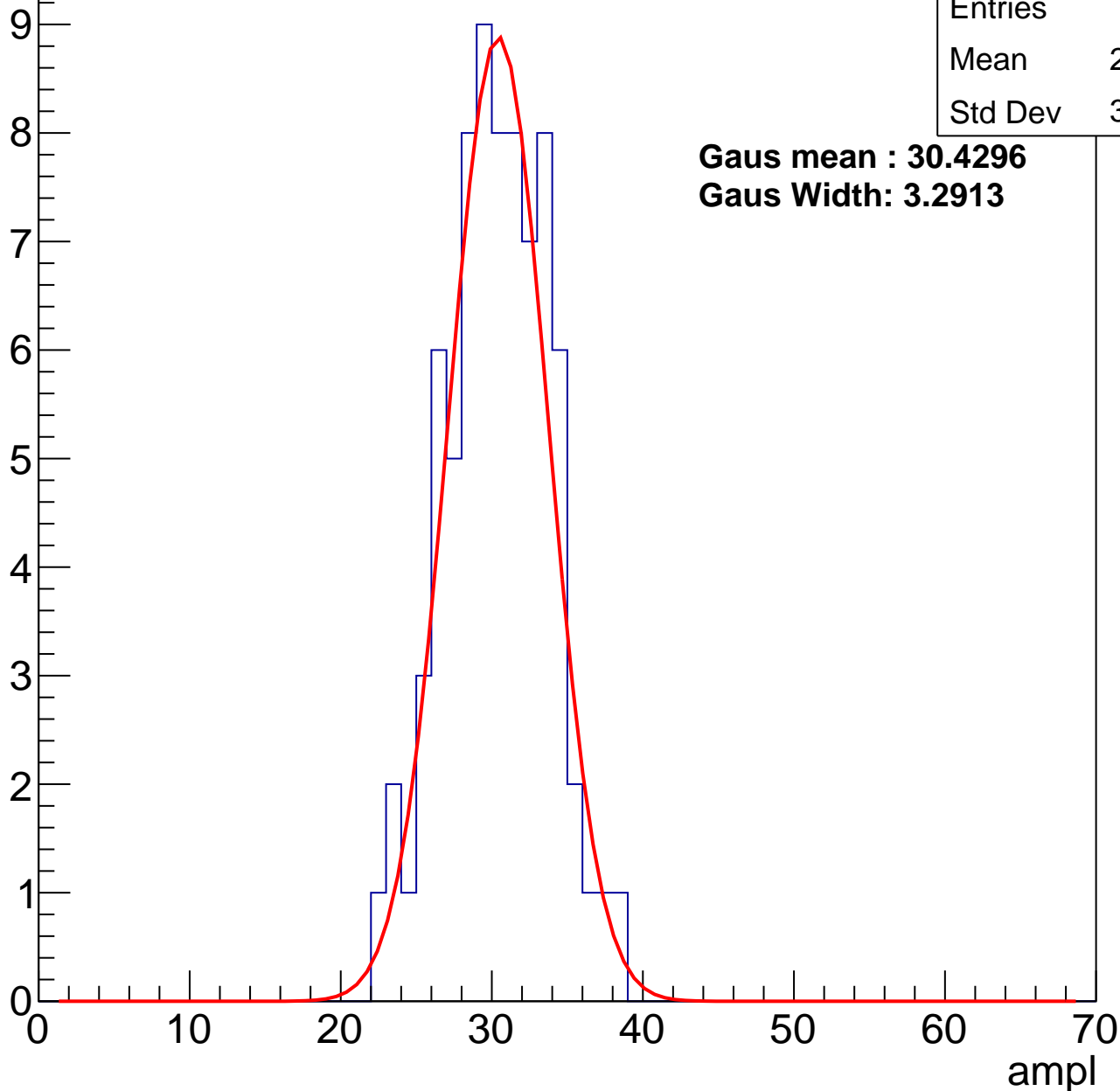
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	29.92
Std Dev	3.337

Gaus mean : 30.4296

Gaus Width: 3.2913



B0L002S, U9-ch29, adc1

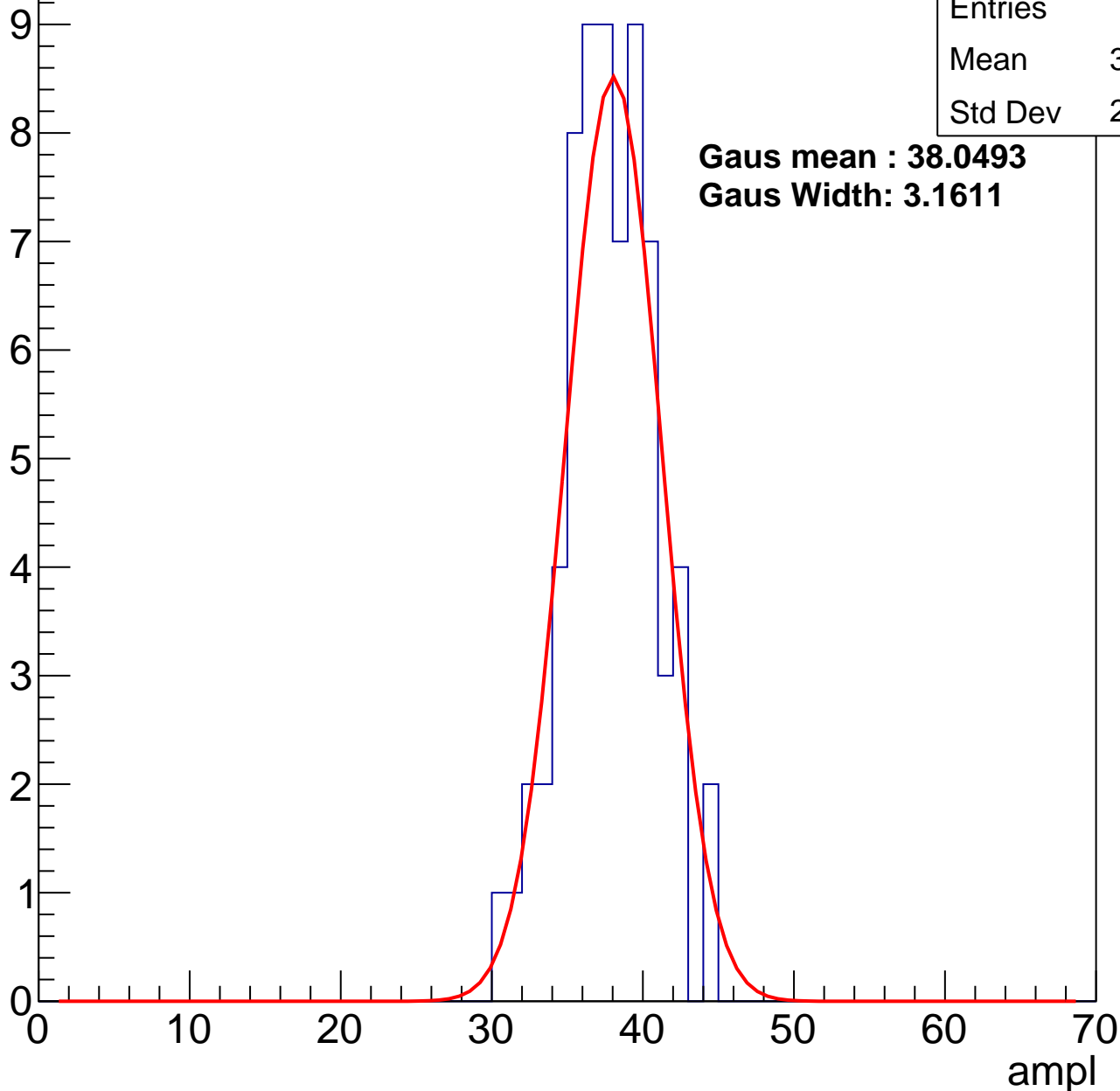
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.35
Std Dev	2.939

Gaus mean : 38.0493

Gaus Width: 3.1611



B0L002S, U9-ch29, adc2

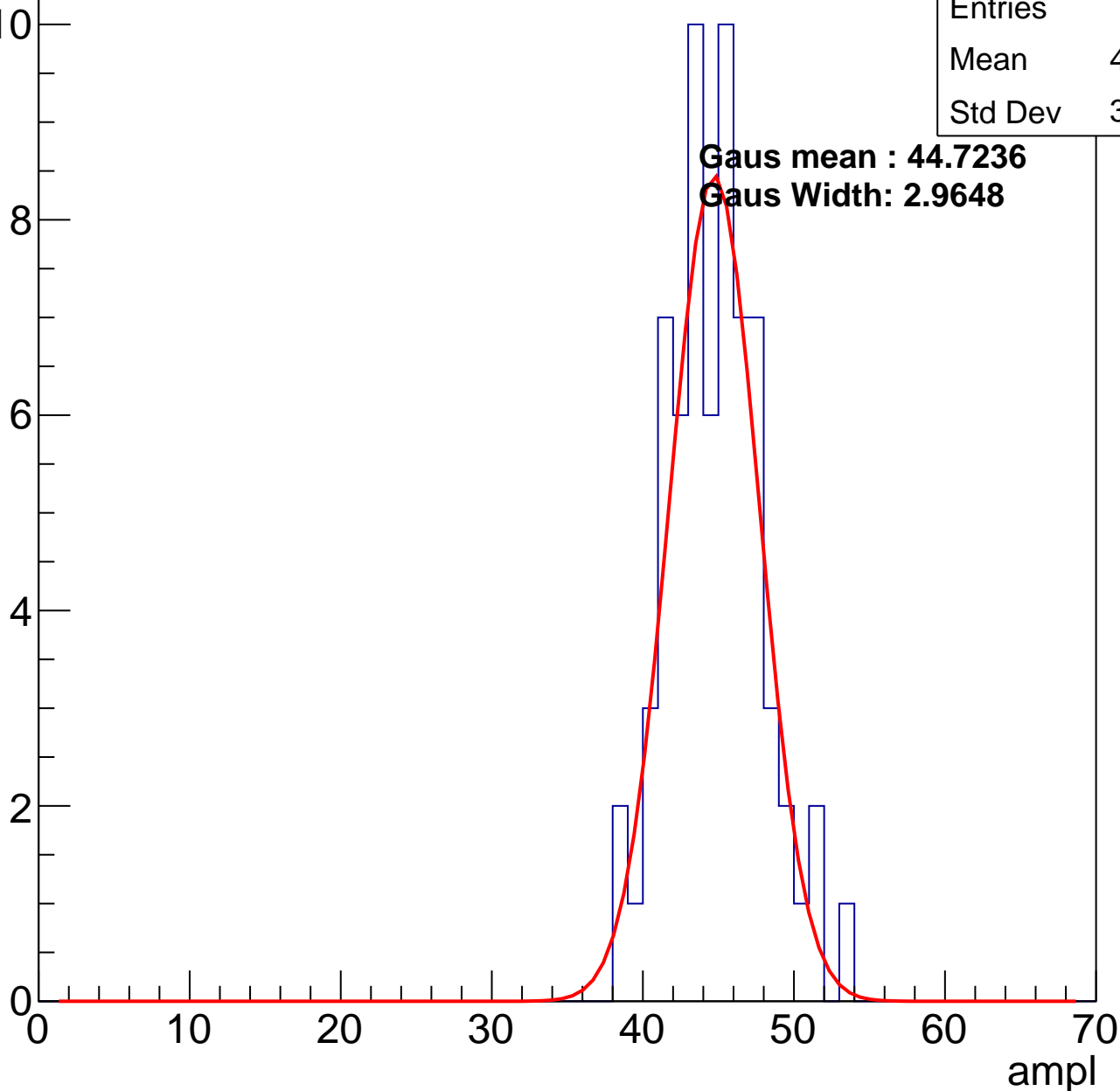
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	44.35
Std Dev	3.086

Gaus mean : 44.7236

Gaus Width: 2.9648

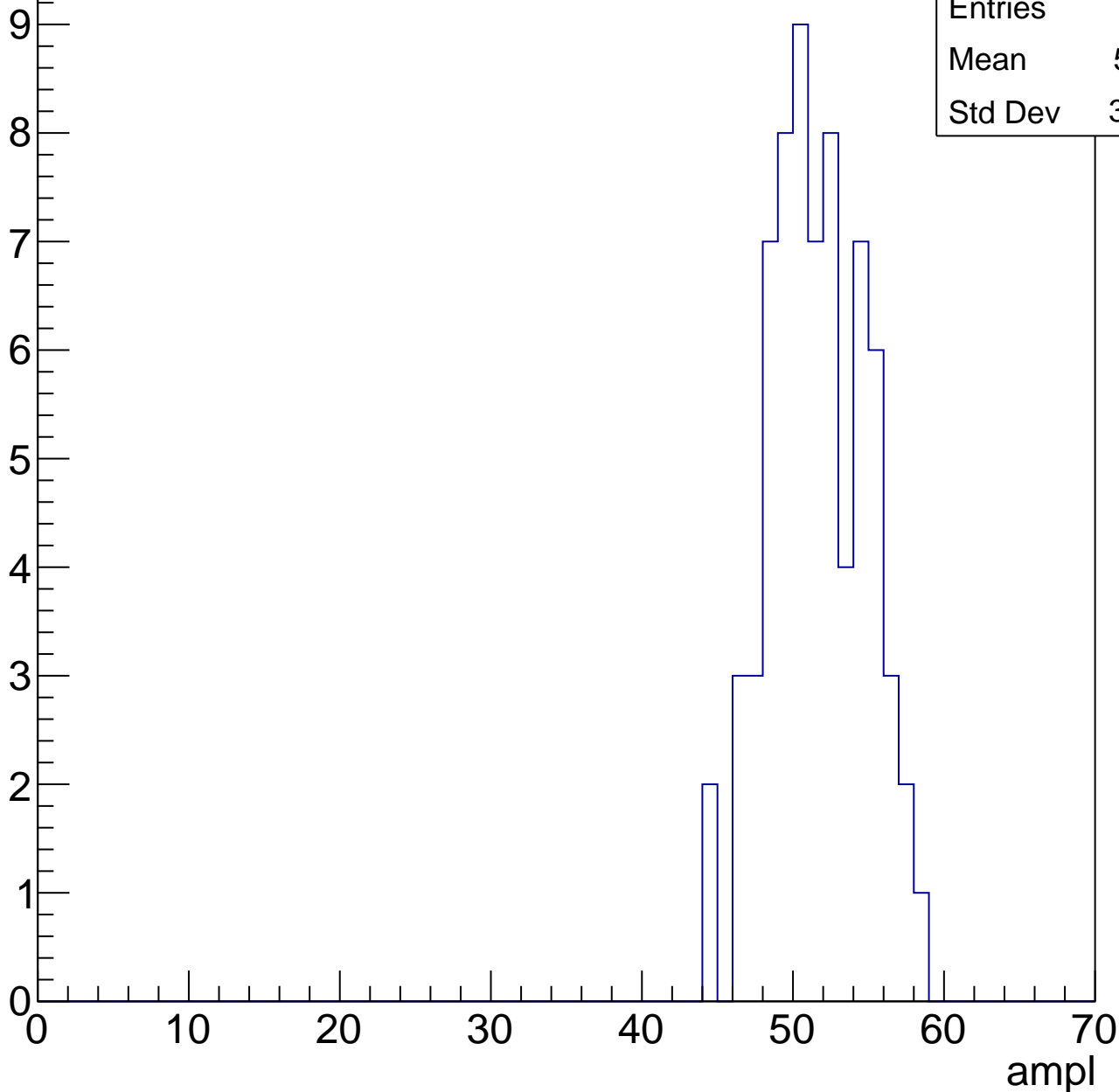


B0L002S, U9-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

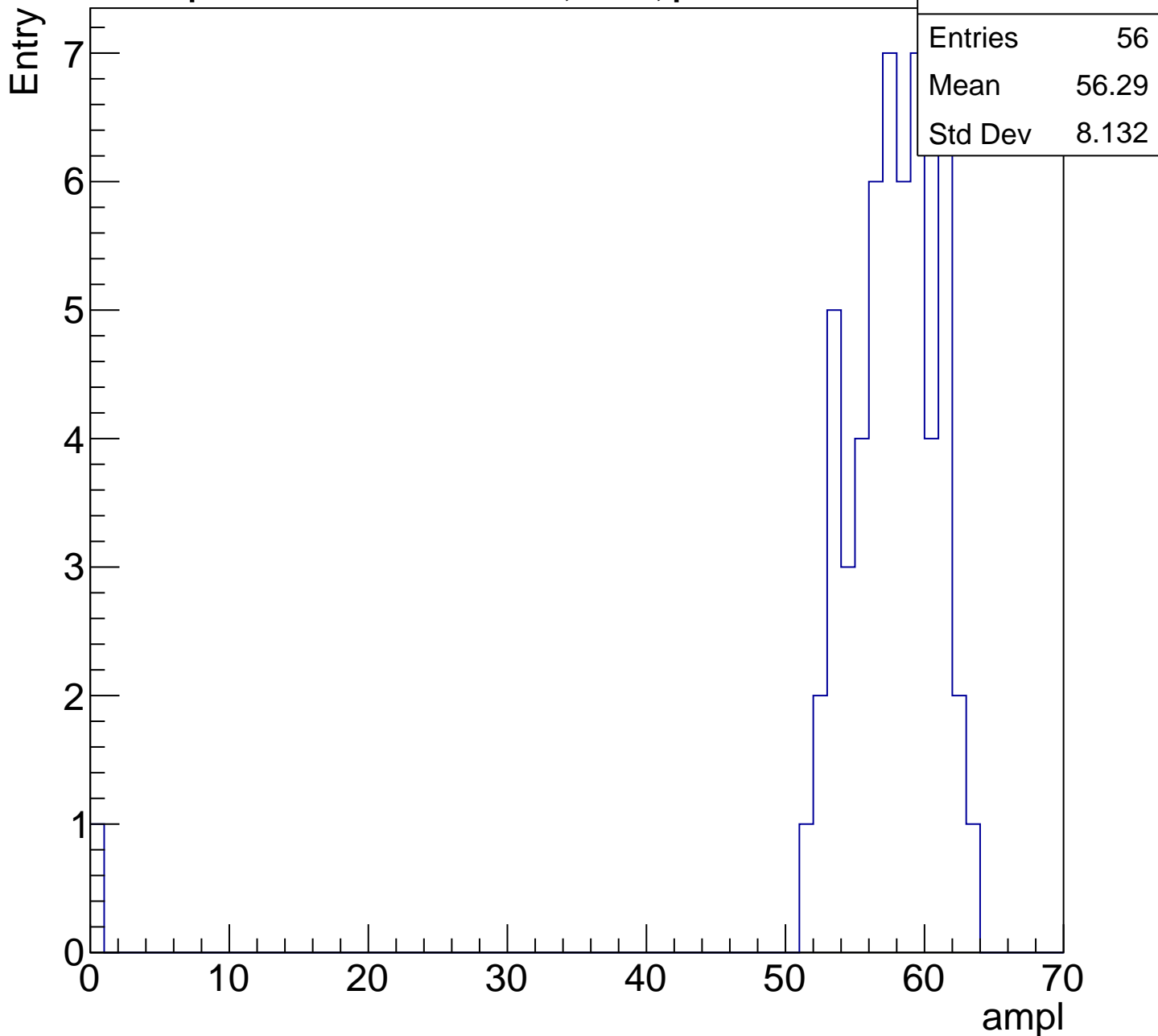
Entry

Entries	70
Mean	51.11
Std Dev	3.169



B0L002S, U9-ch29, adc4

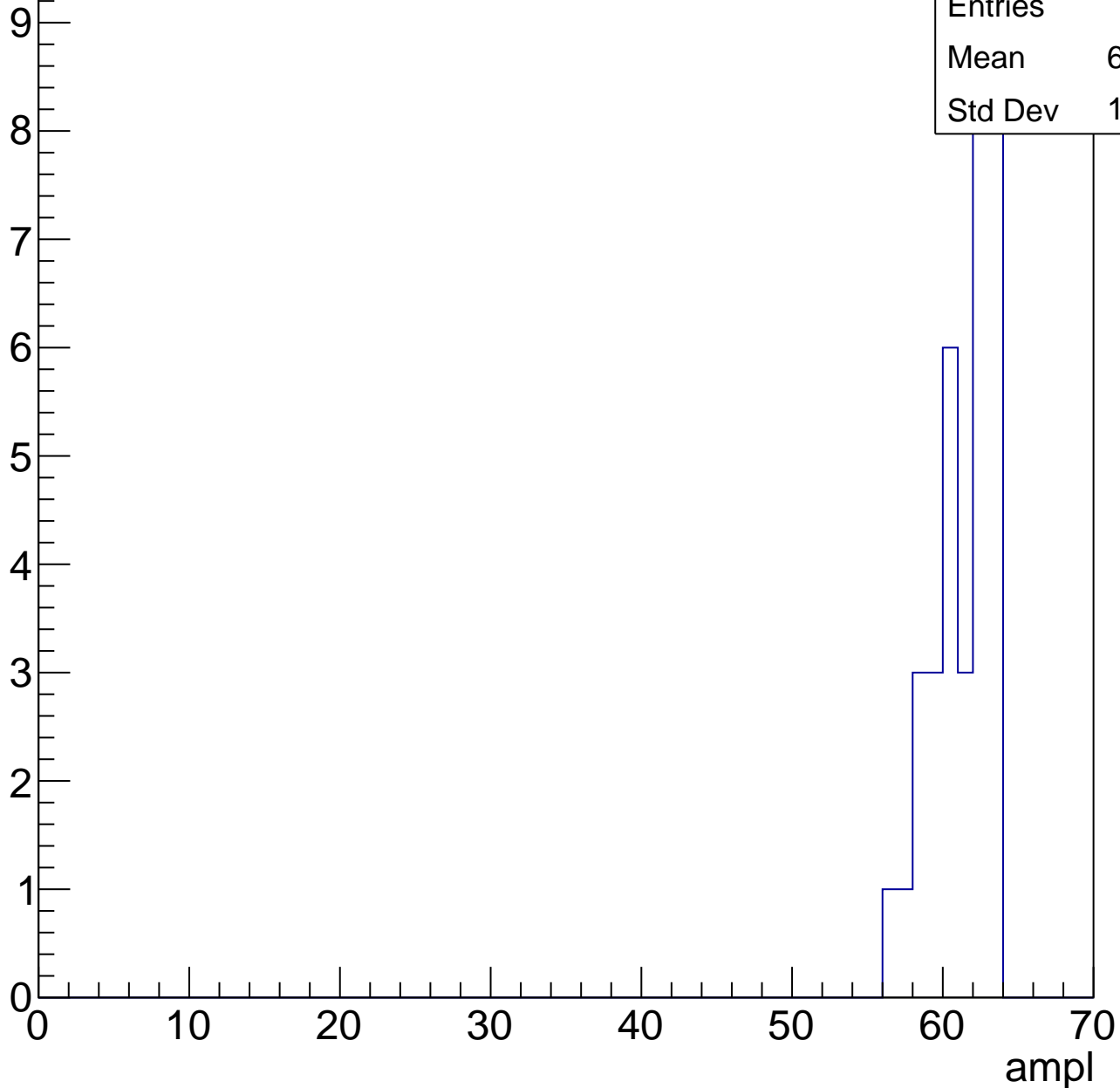
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch30, adc0

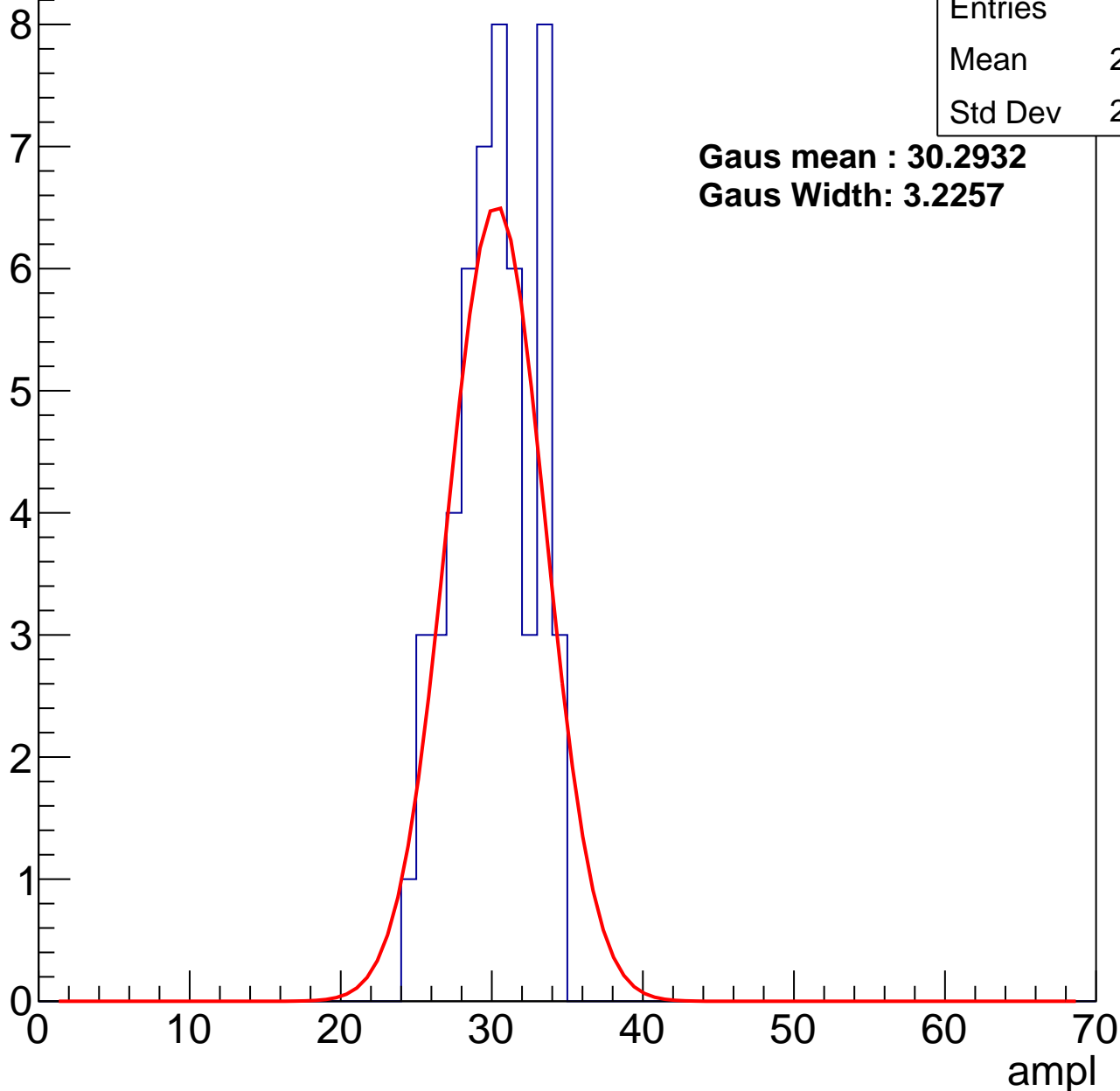
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	29.69
Std Dev	2.628

Gaus mean : 30.2932

Gaus Width: 3.2257



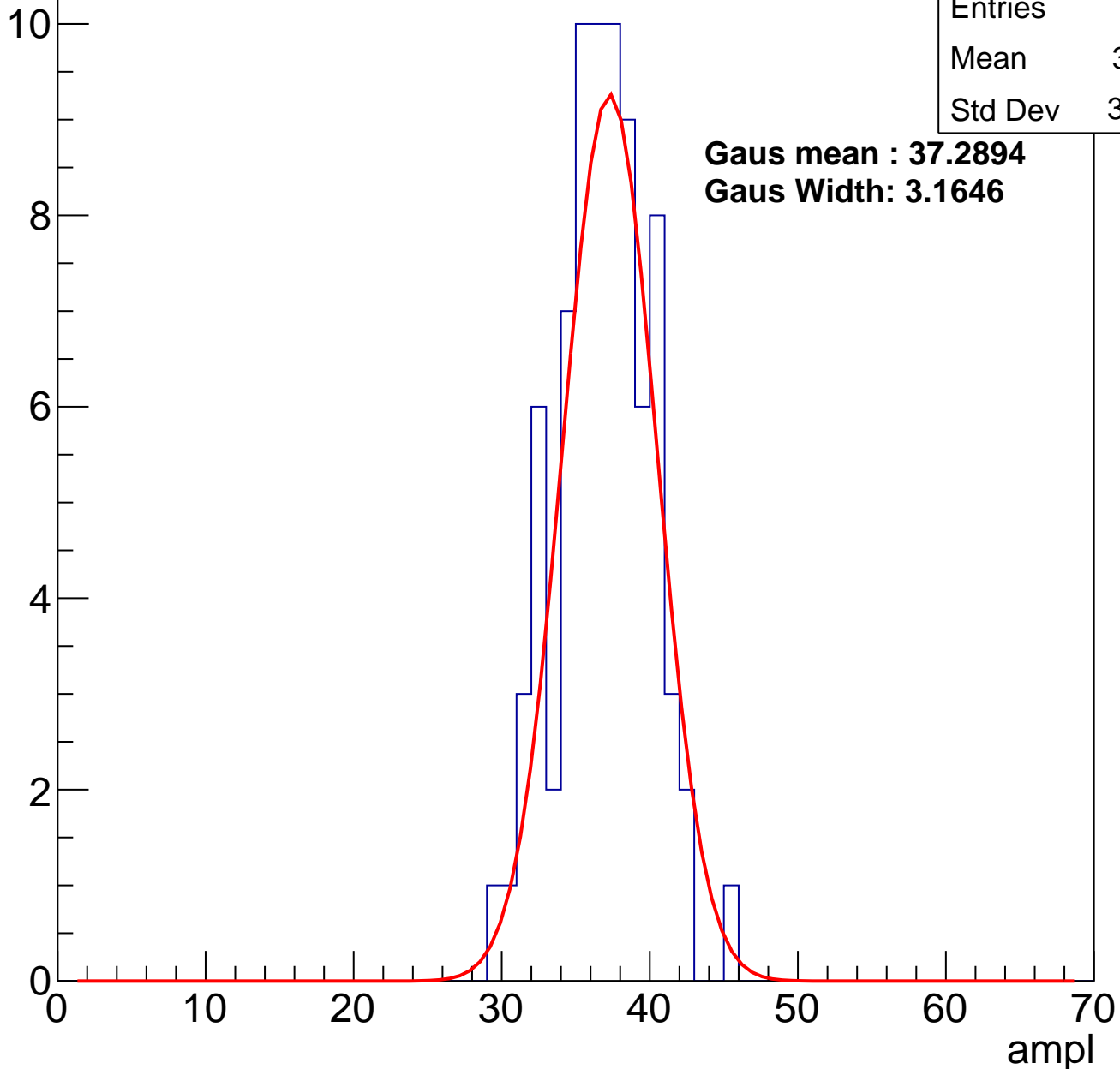
B0L002S, U9-ch30, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	36.41
Std Dev	3.092

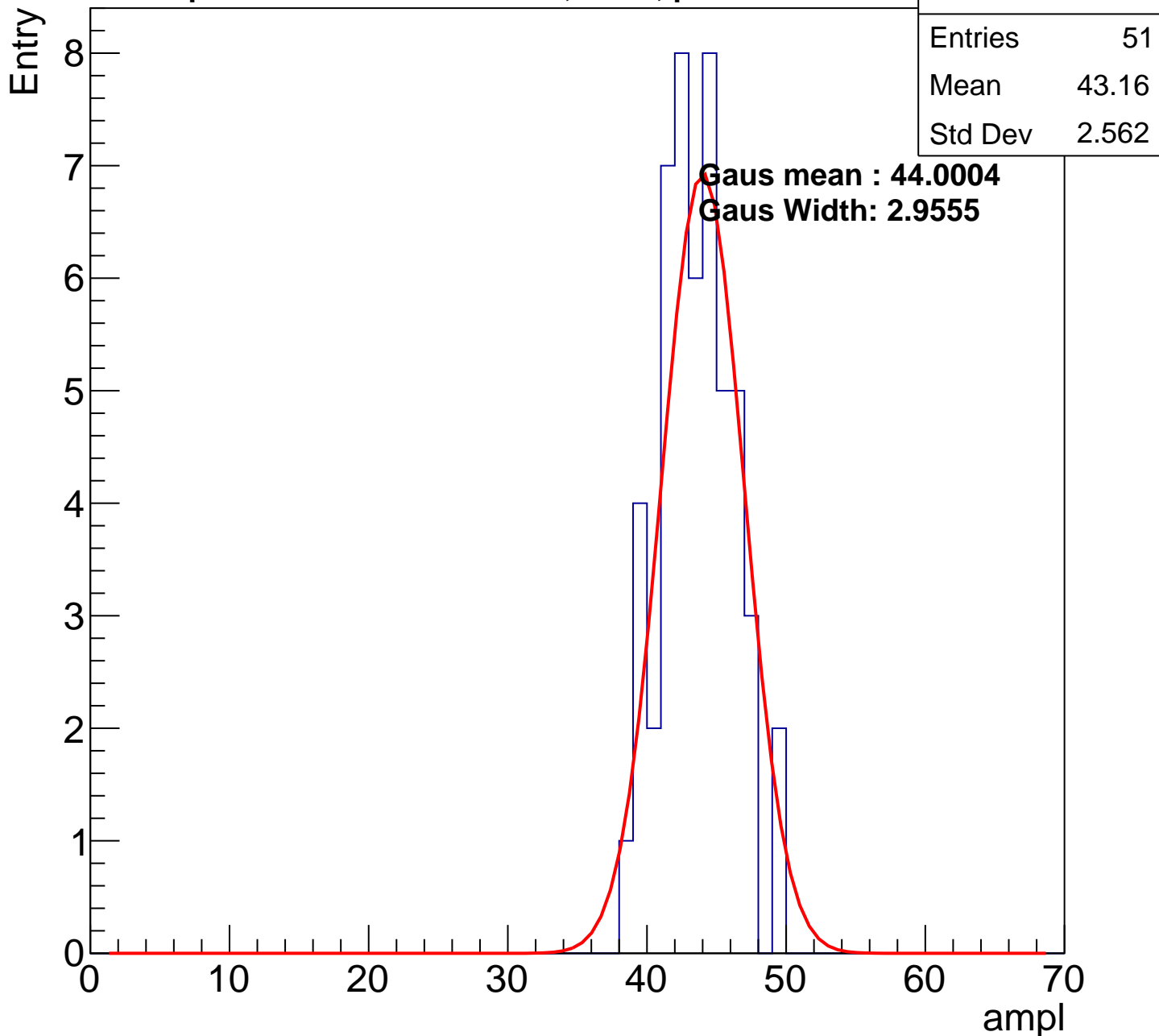
Gaus mean : 37.2894
Gaus Width: 3.1646

Entry



B0L002S, U9-ch30, adc2

calib_packv5_042523_0143.root, FC#8, port C1

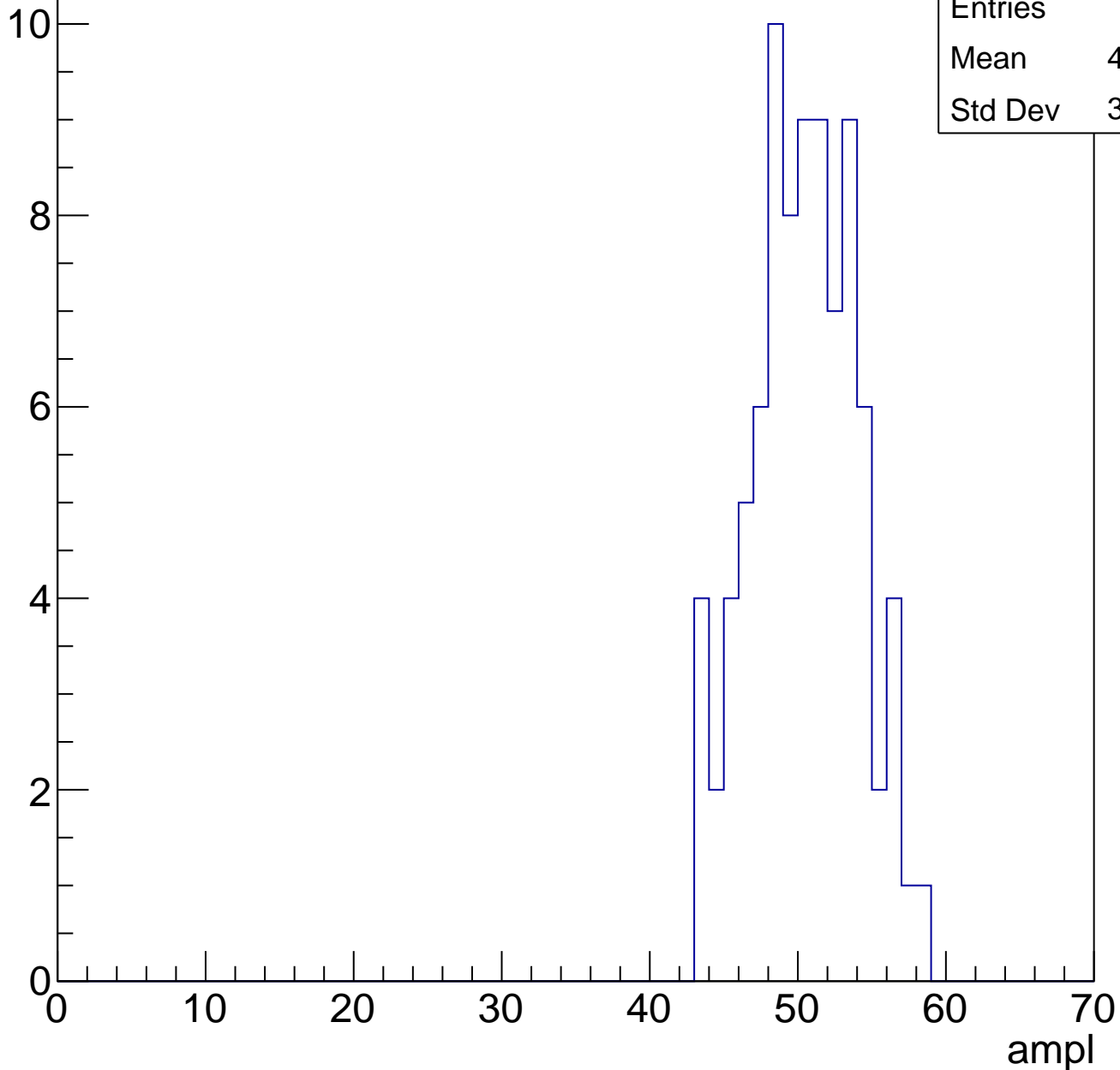


B0L002S, U9-ch30, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	49.97
Std Dev	3.508

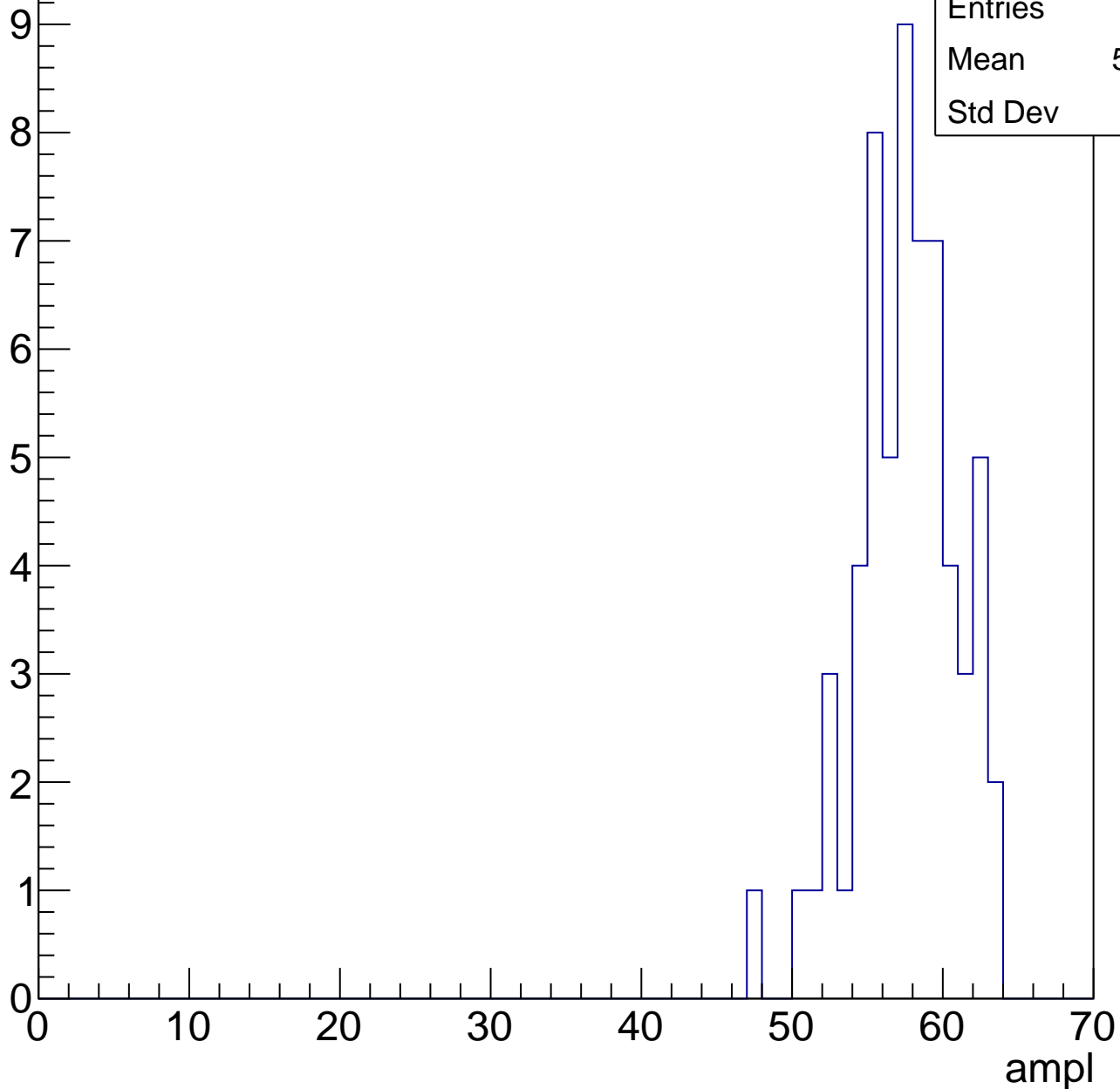
Entry



B0L002S, U9-ch30, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



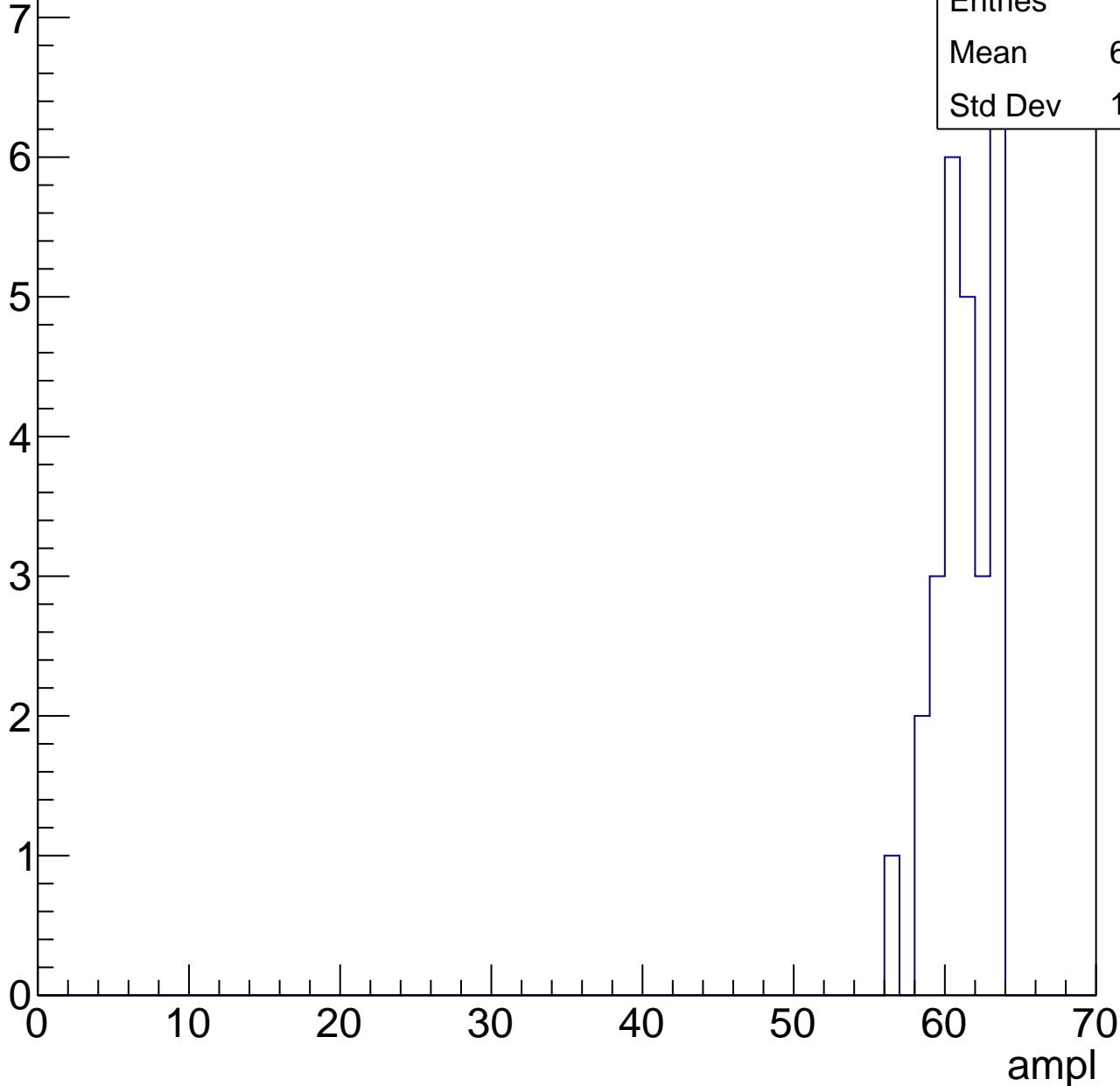
Entries	61
Mean	57.11
Std Dev	3.3

B0L002S, U9-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	60.78
Std Dev	1.832



B0L002S, U9-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

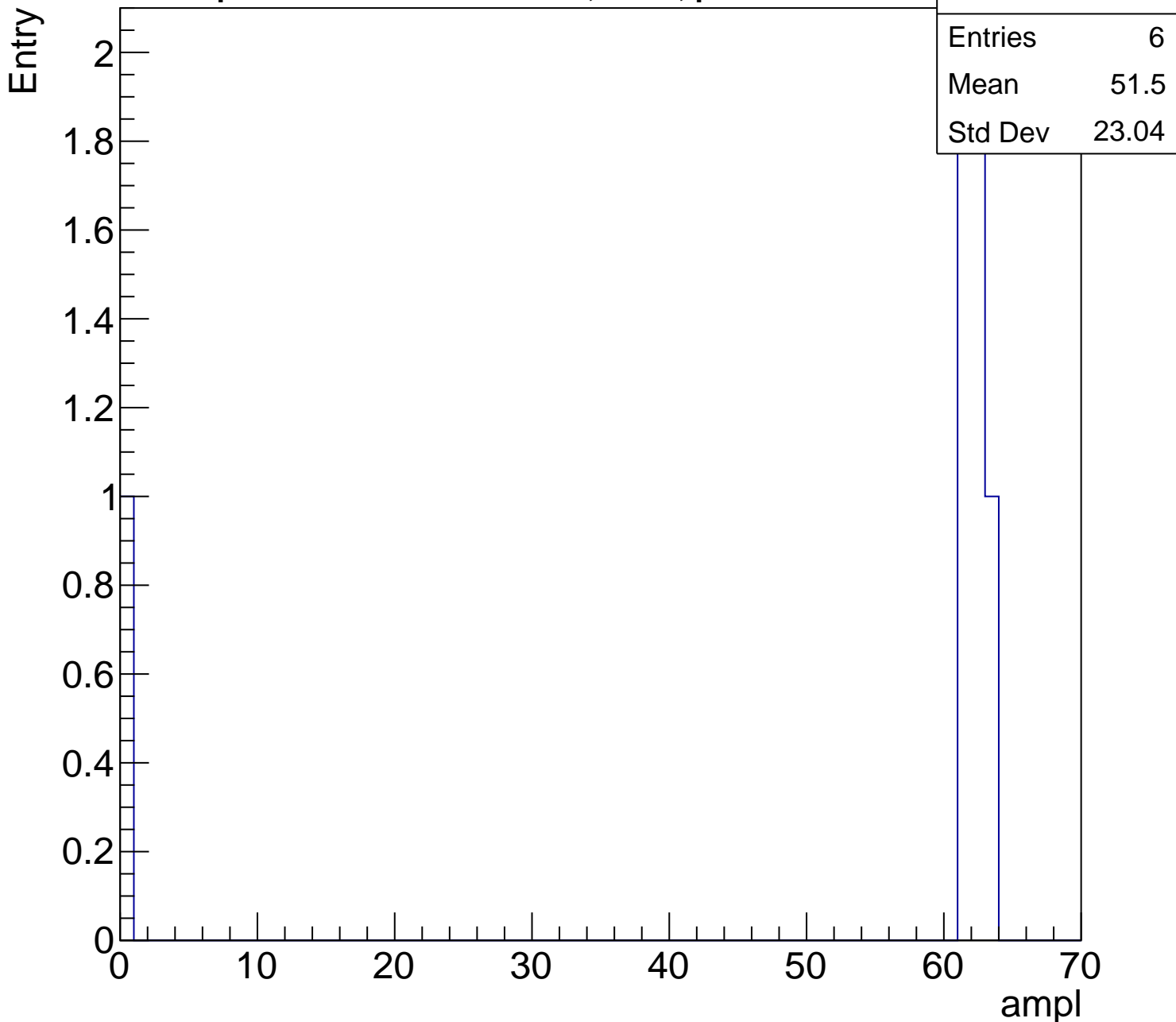
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.5
Std Dev	23.04

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch31, adc0

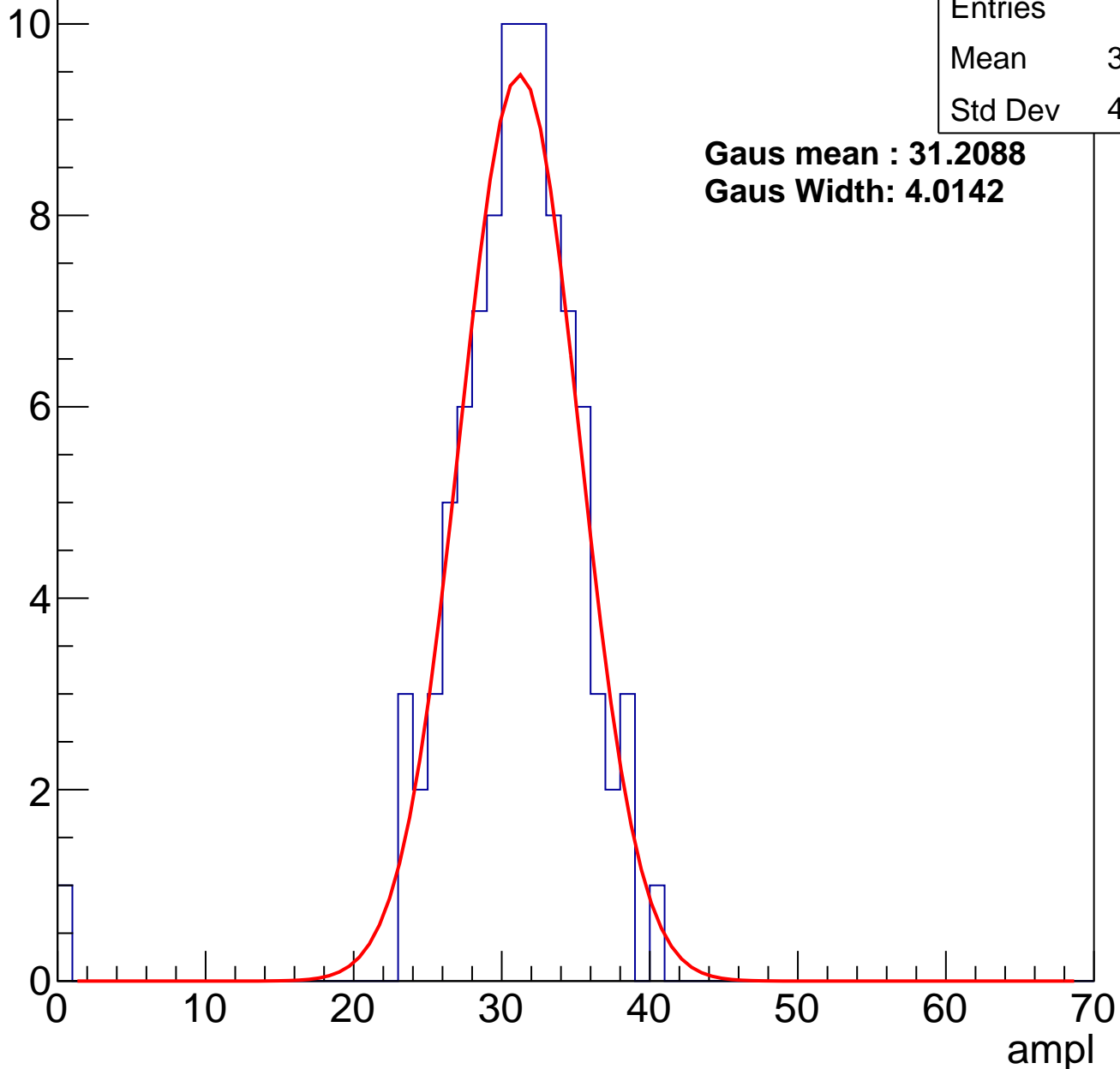
calib_packv5_042523_0143.root, FC#8, port C1

Entries	95
Mean	30.42
Std Dev	4.844

Gaus mean : 31.2088

Gaus Width: 4.0142

Entry



B0L002S, U9-ch31, adc1

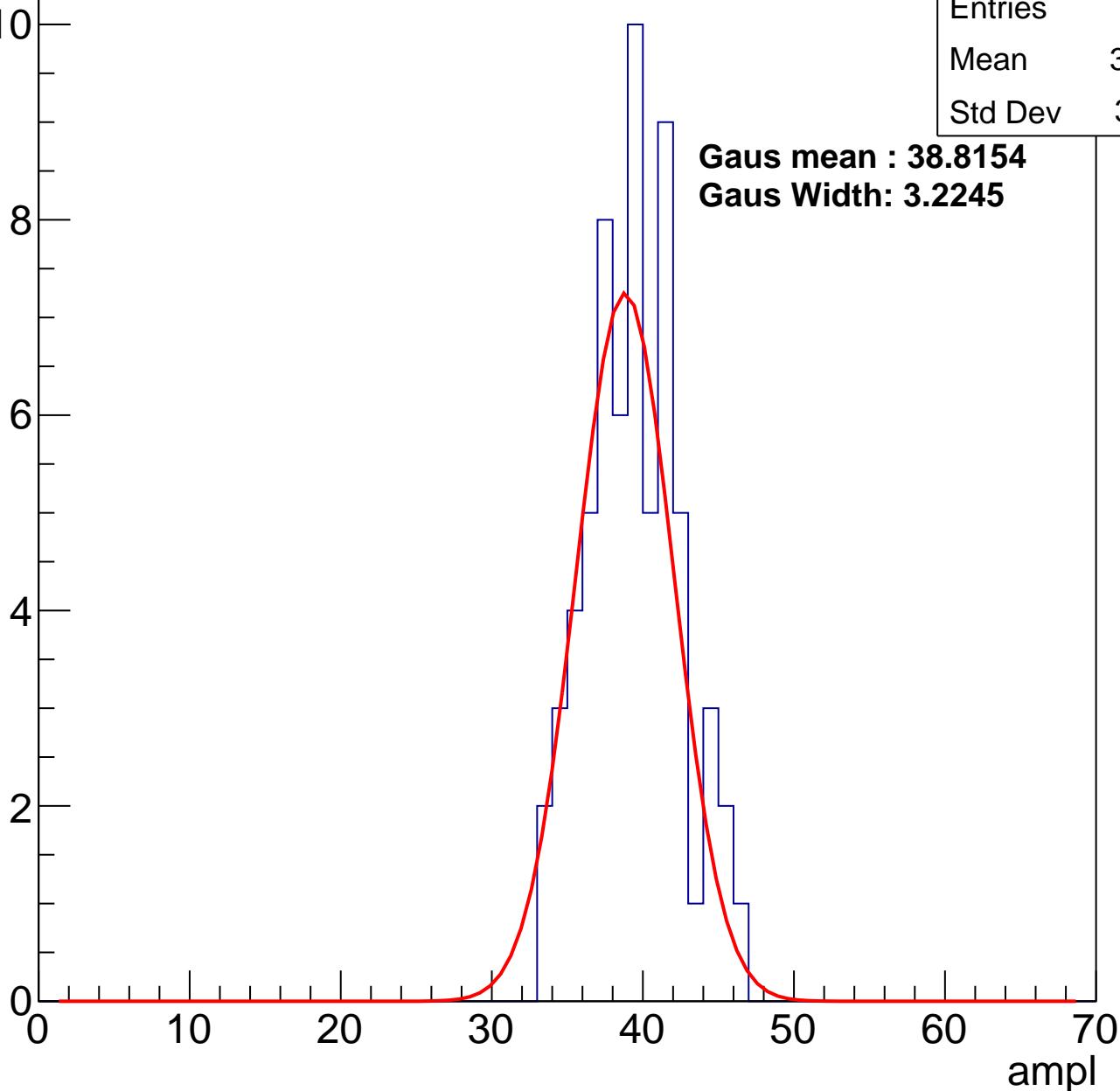
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.94
Std Dev	3.041

Gaus mean : 38.8154

Gaus Width: 3.2245



B0L002S, U9-ch31, adc2

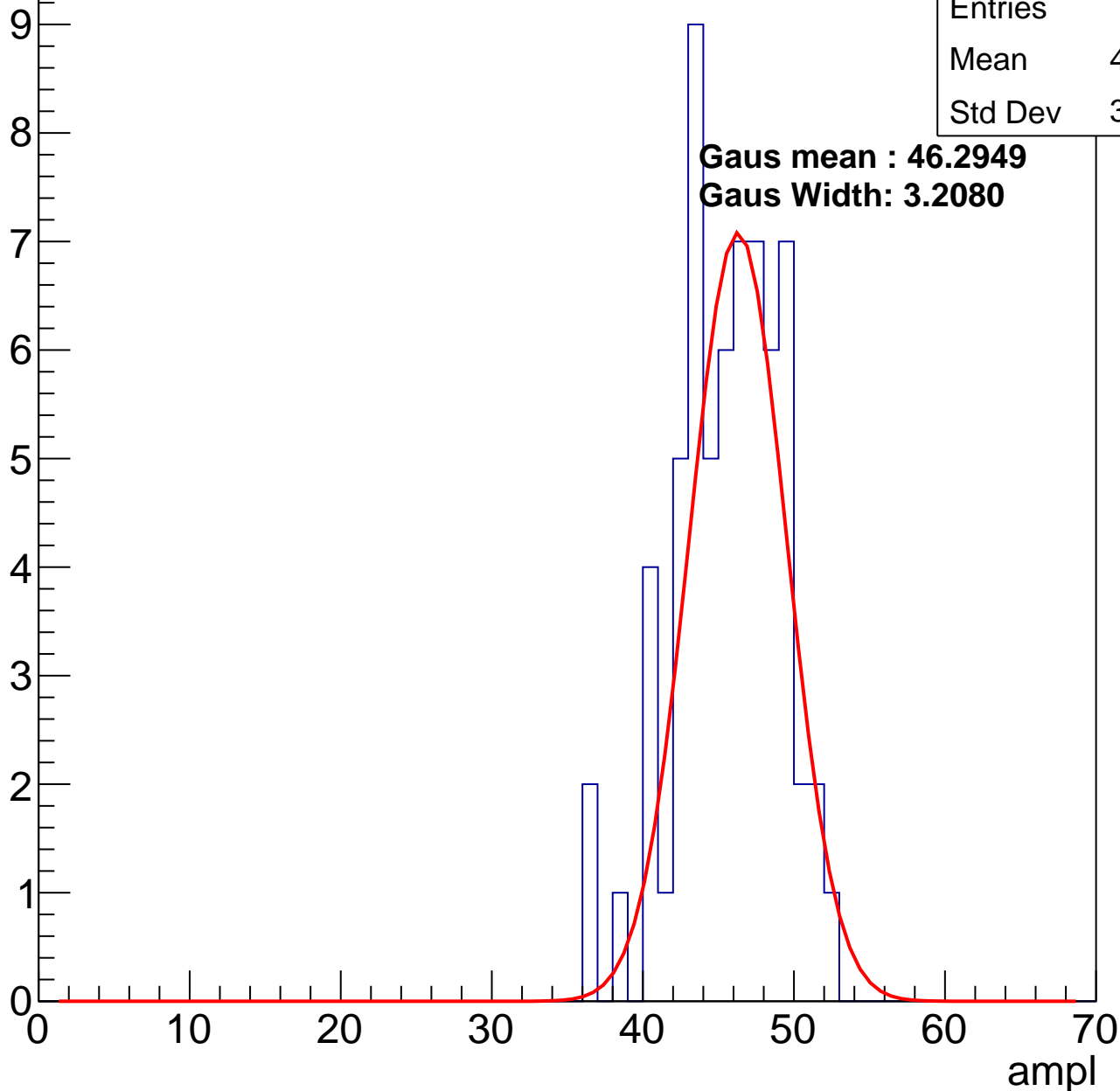
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	45.14
Std Dev	3.472

Gaus mean : 46.2949

Gaus Width: 3.2080

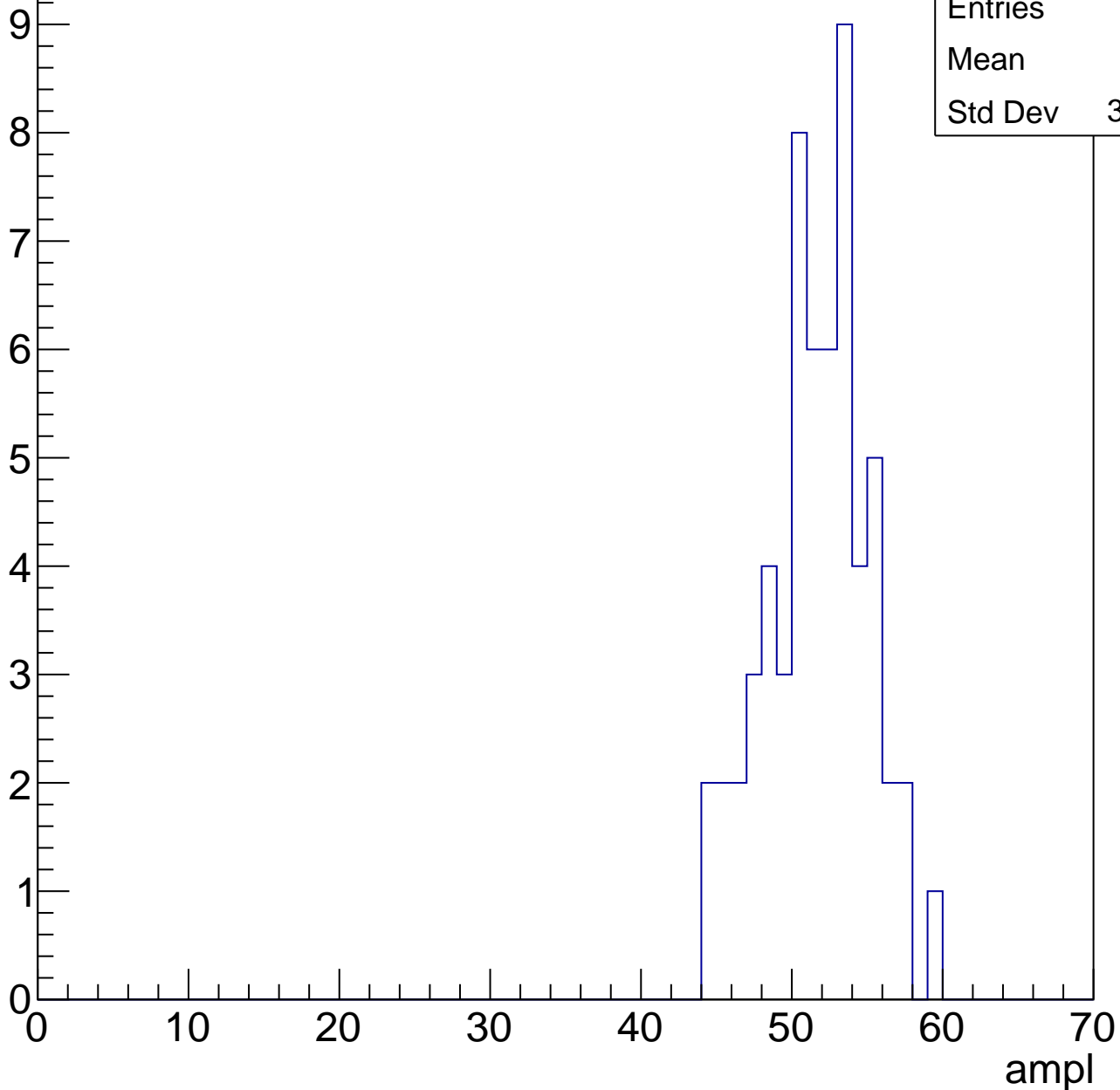


B0L002S, U9-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

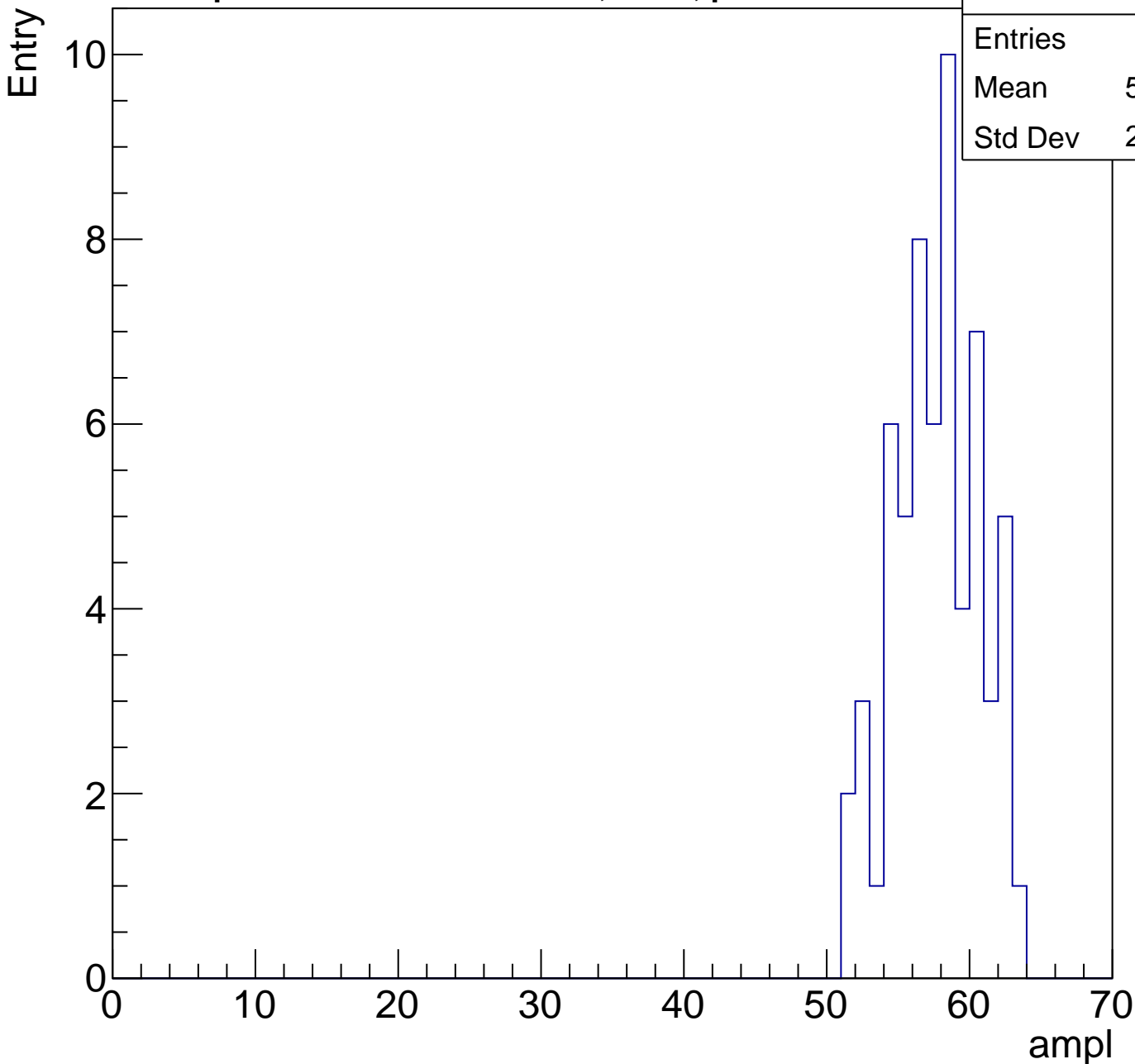
Entries	59
Mean	51.2
Std Dev	3.359



B0L002S, U9-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	57.25
Std Dev	2.973

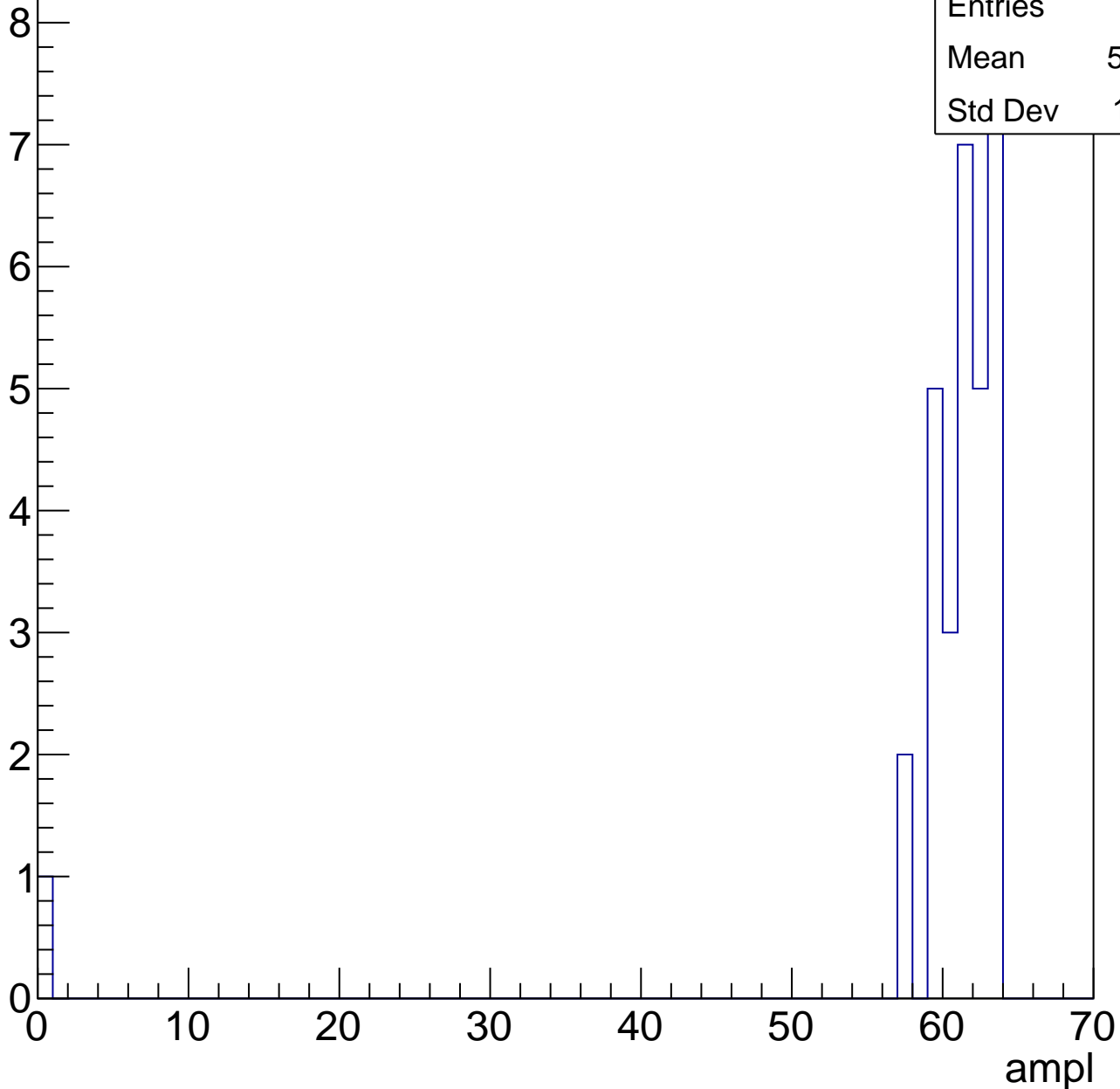


B0L002S, U9-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.03
Std Dev	10.91



B0L002S, U9-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch32, adc0

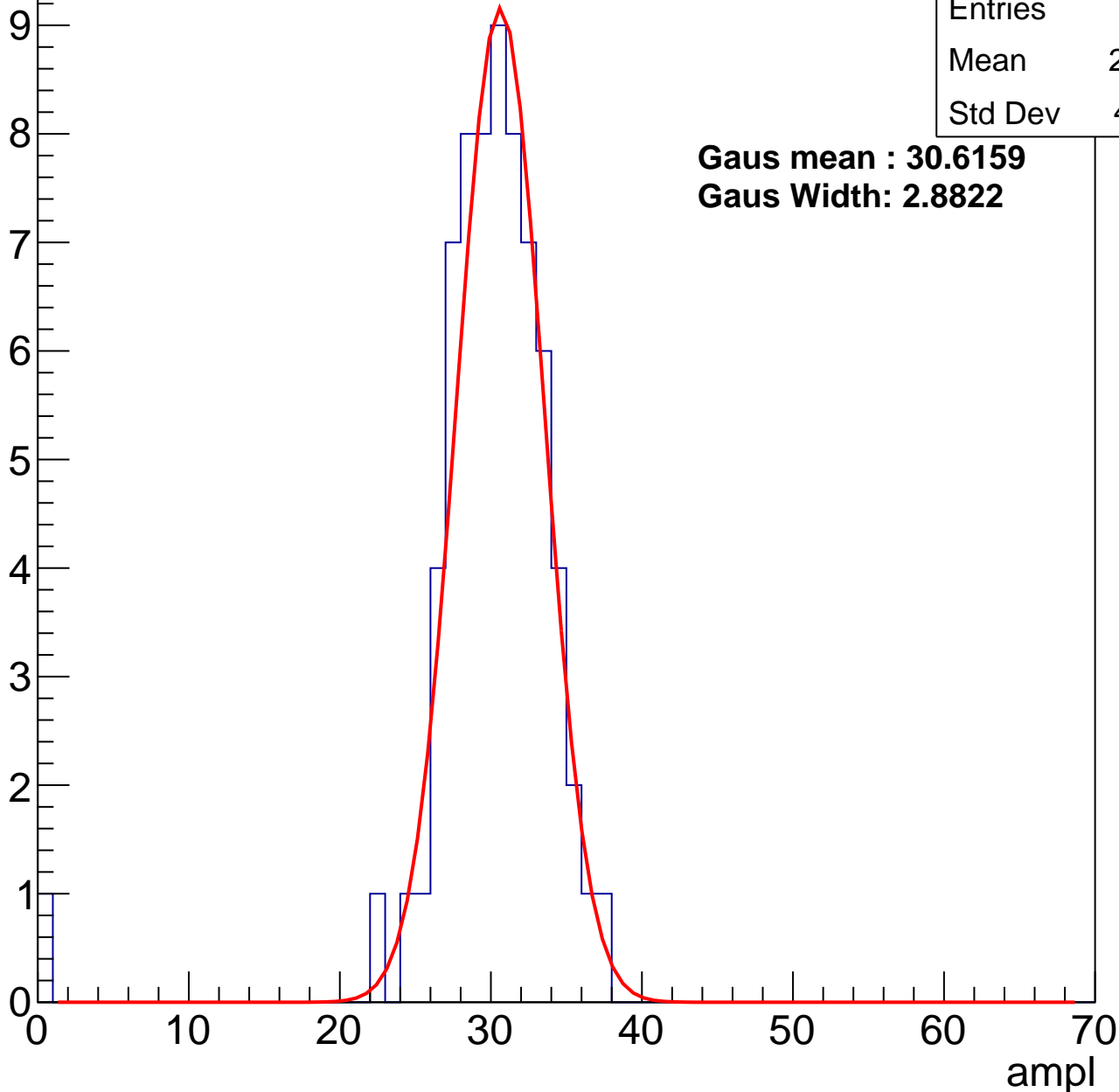
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.55
Std Dev	4.611

Gaus mean : 30.6159

Gaus Width: 2.8822



B0L002S, U9-ch32, adc1

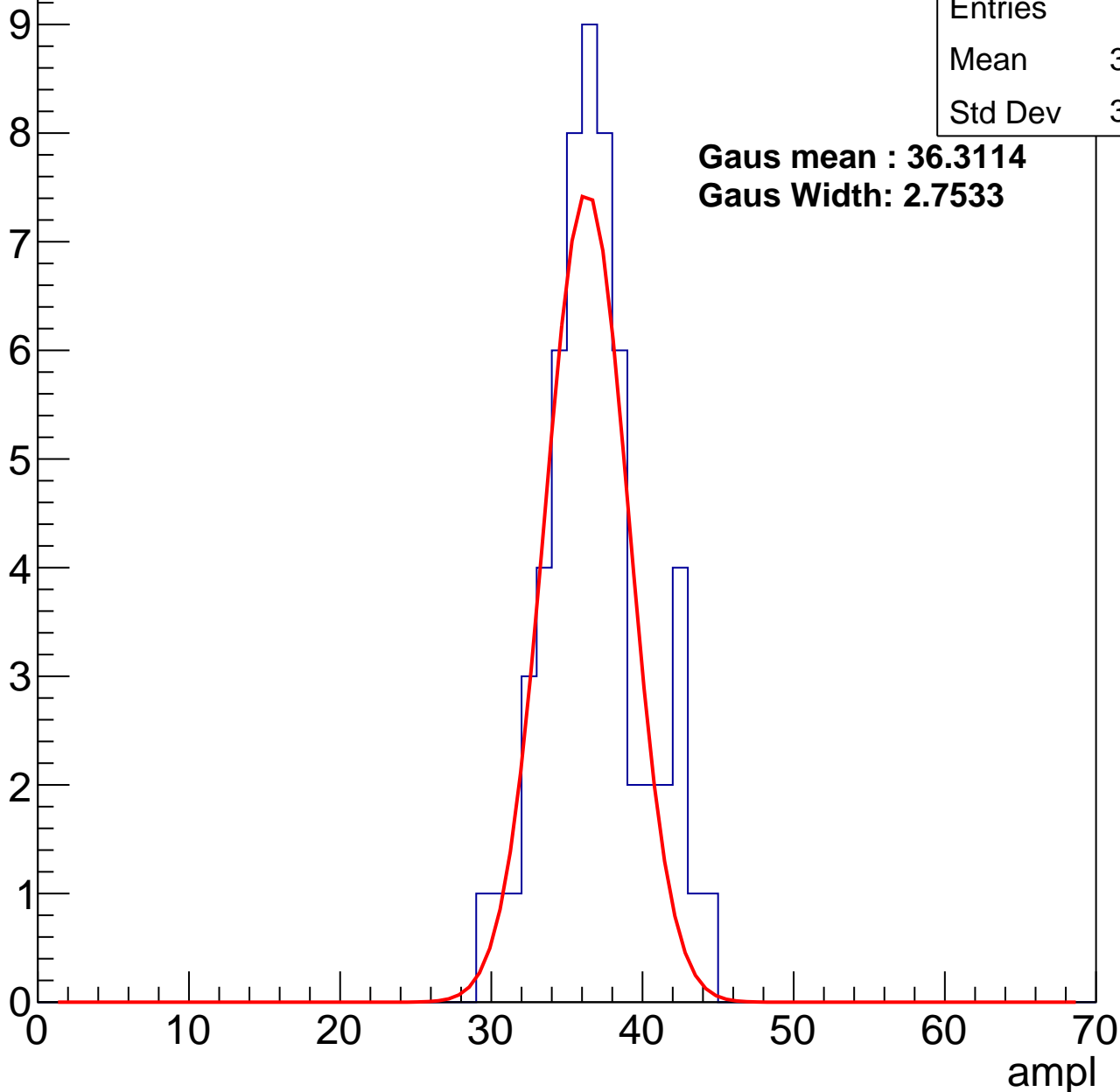
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.36
Std Dev	3.209

Gaus mean : 36.3114

Gaus Width: 2.7533



B0L002S, U9-ch32, adc2

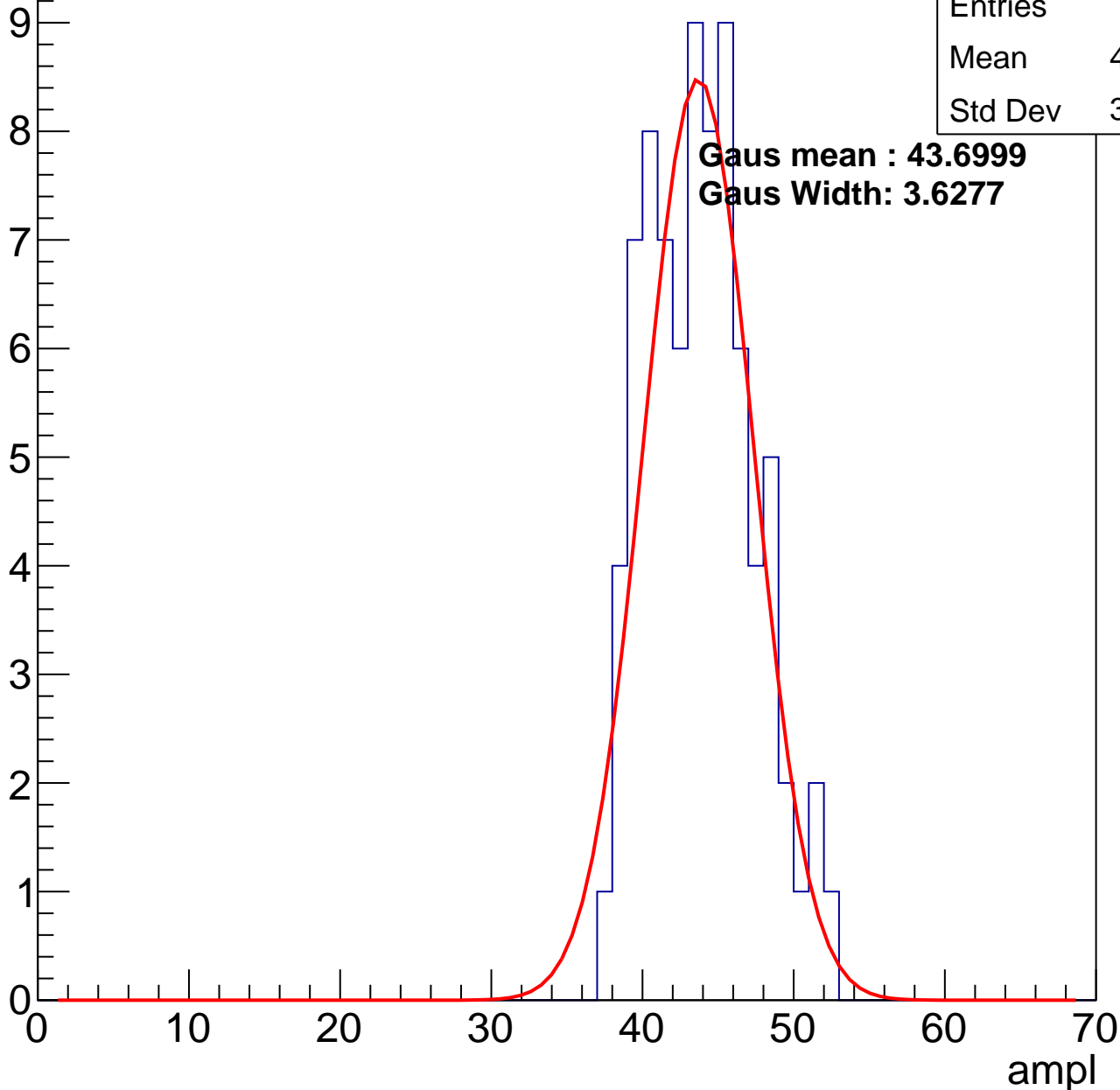
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	43.39
Std Dev	3.462

Gaus mean : 43.6999

Gaus Width: 3.6277

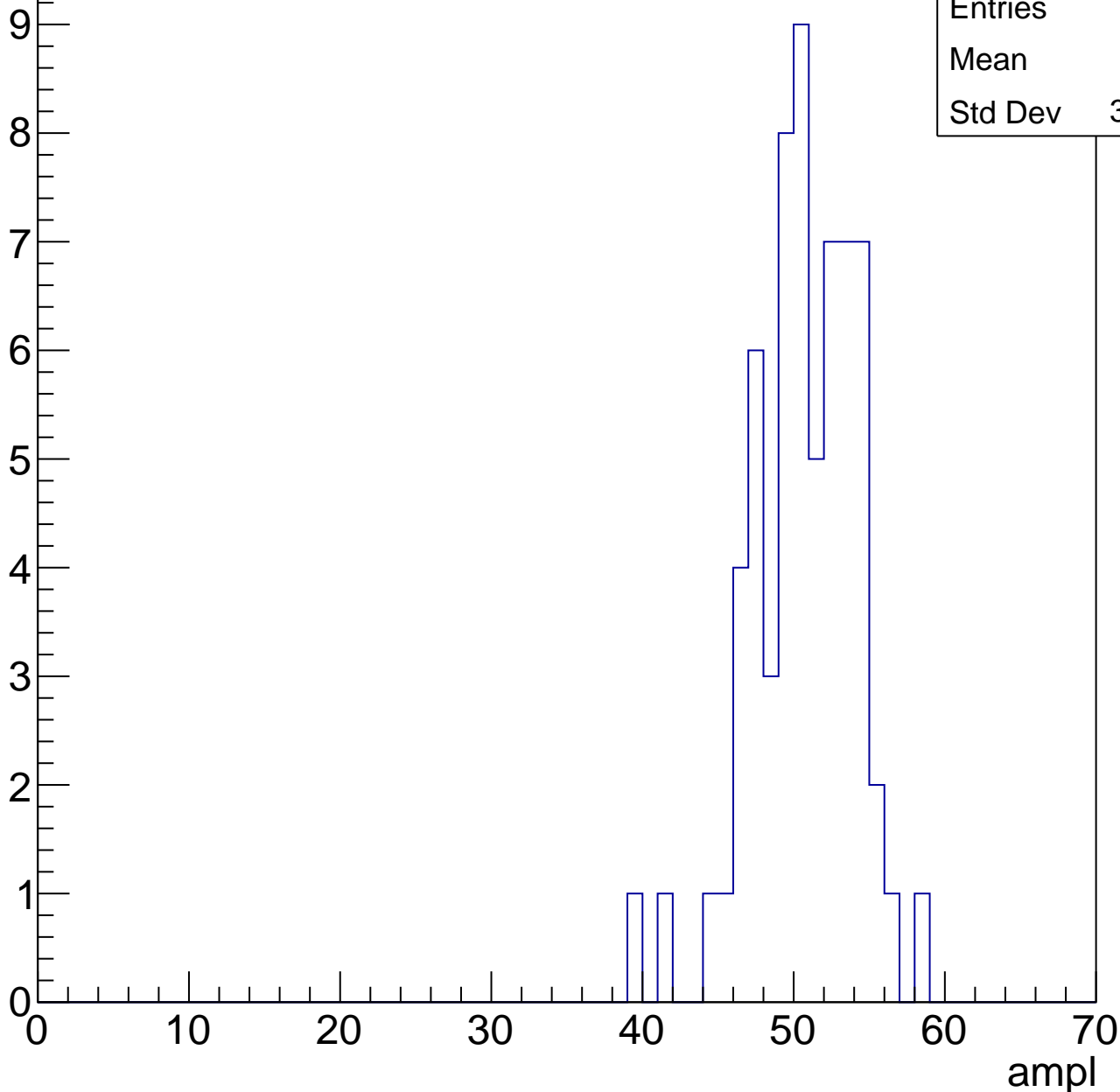


B0L002S, U9-ch32, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	50.2
Std Dev	3.438

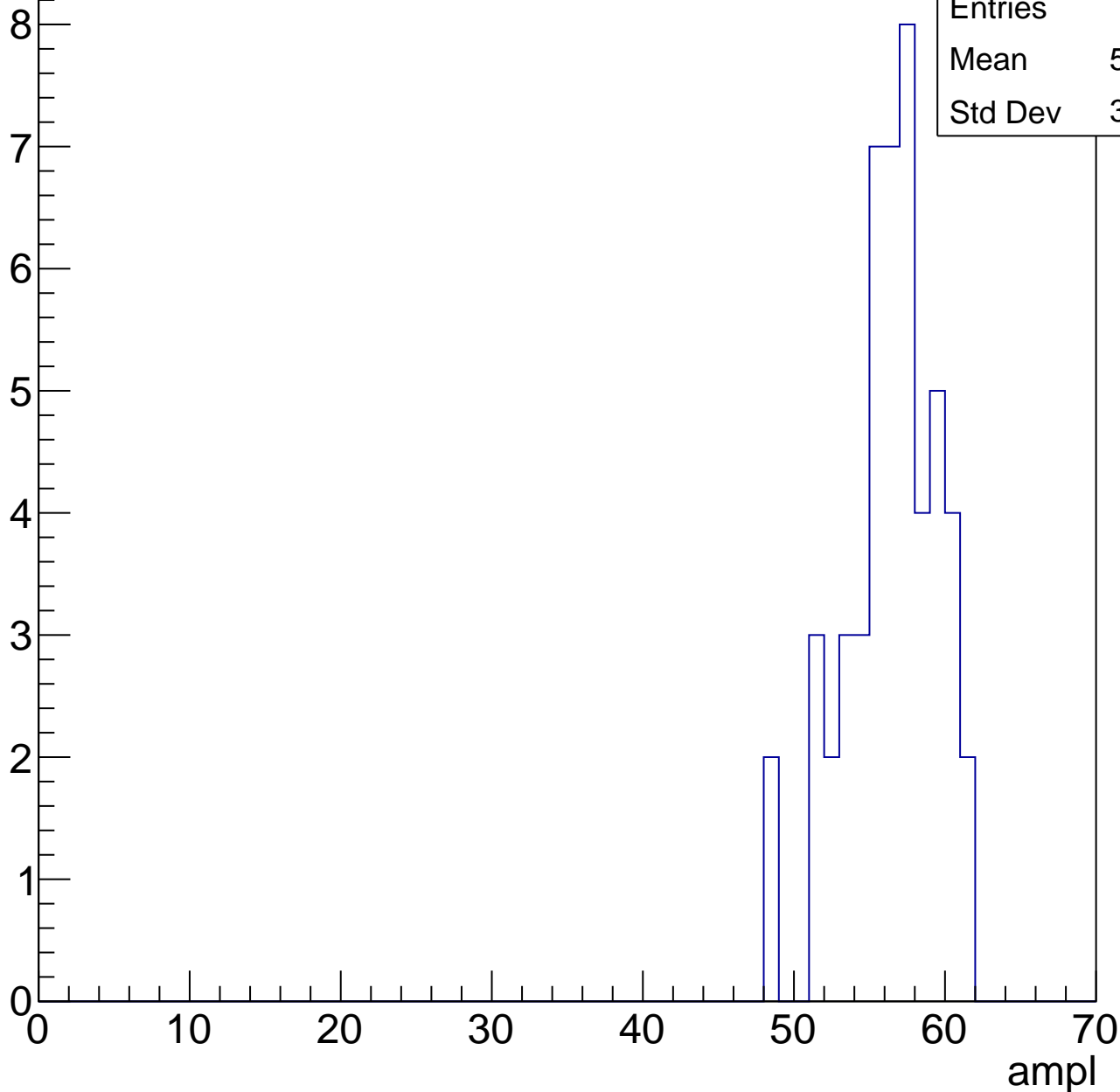


B0L002S, U9-ch32, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	55.92
Std Dev	3.045

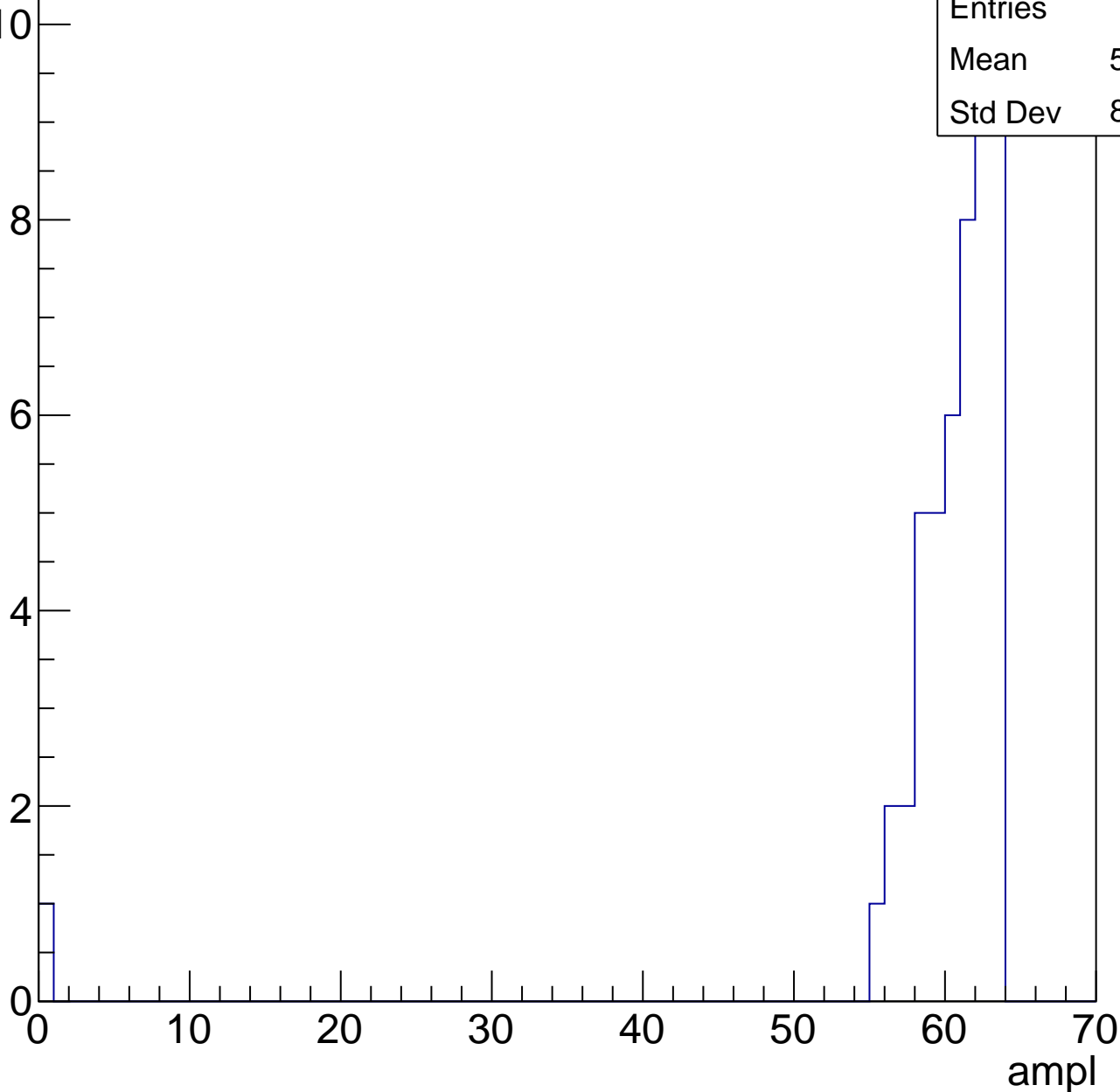


B0L002S, U9-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	59.28
Std Dev	8.732



B0L002S, U9-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch33, adc0

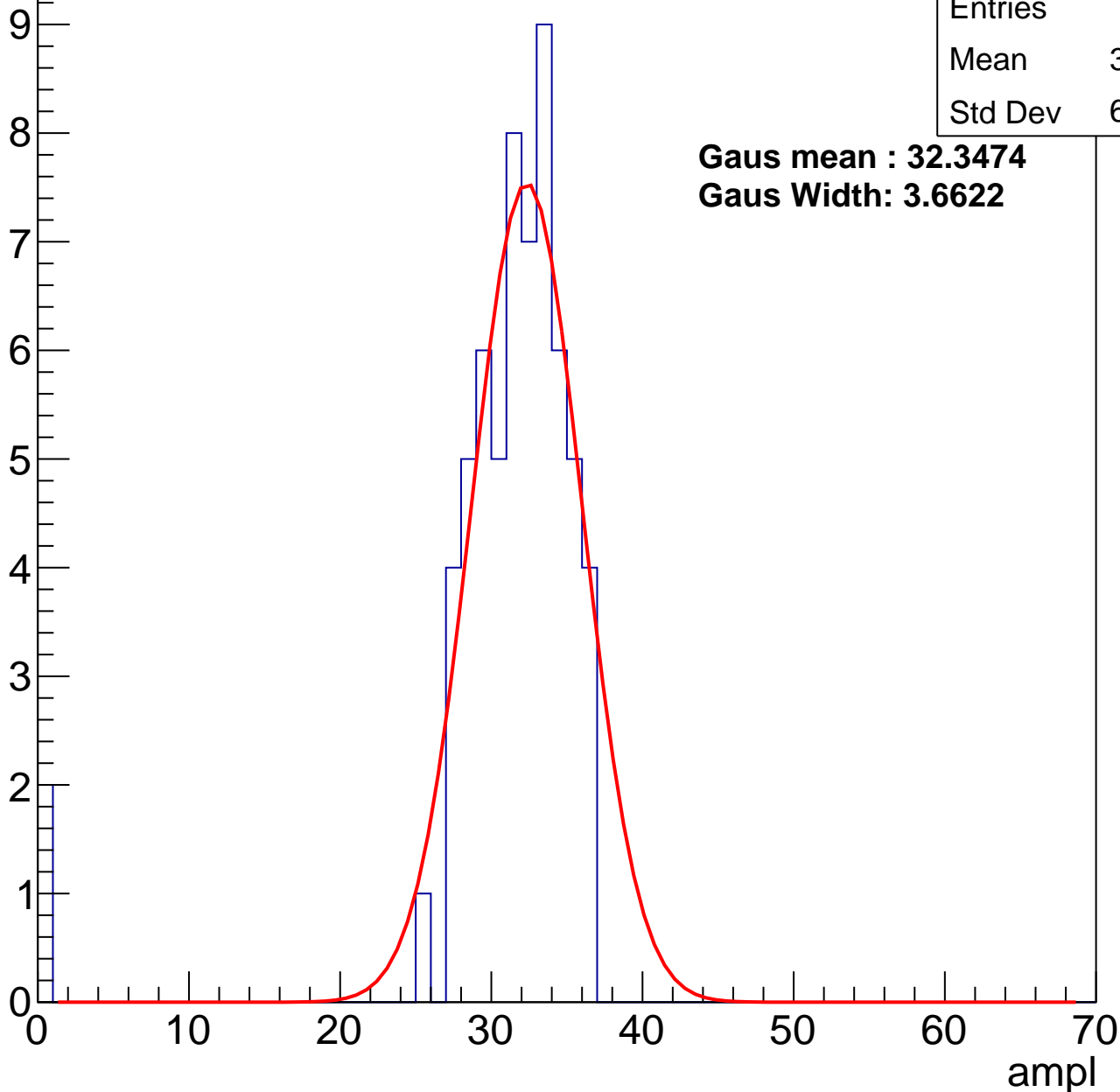
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.47
Std Dev	6.164

Gaus mean : 32.3474

Gaus Width: 3.6622



B0L002S, U9-ch33, adc1

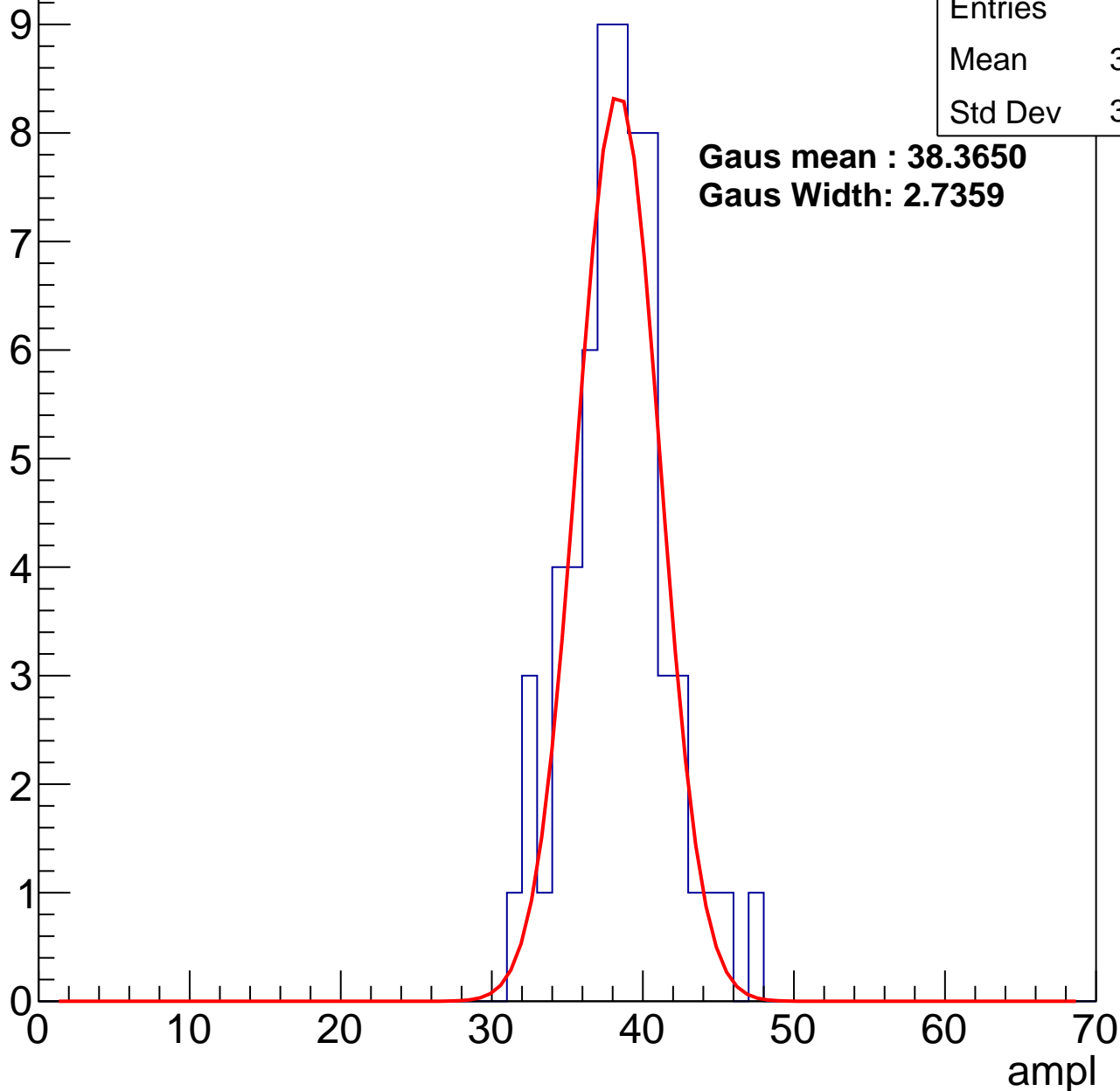
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.89
Std Dev	3.138

Gaus mean : 38.3650

Gaus Width: 2.7359



B0L002S, U9-ch33, adc2

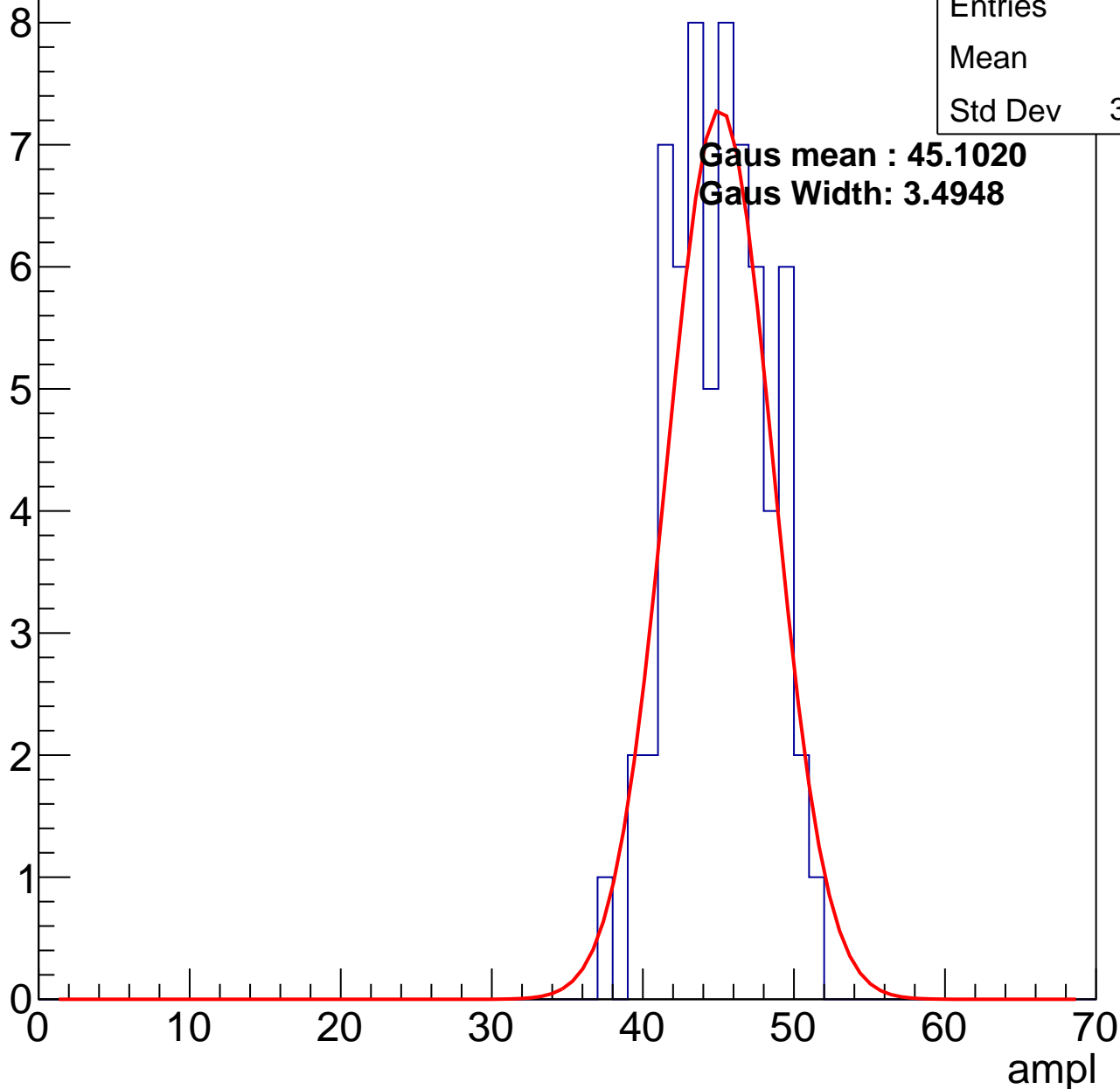
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.6
Std Dev	3.107

Gaus mean : 45.1020

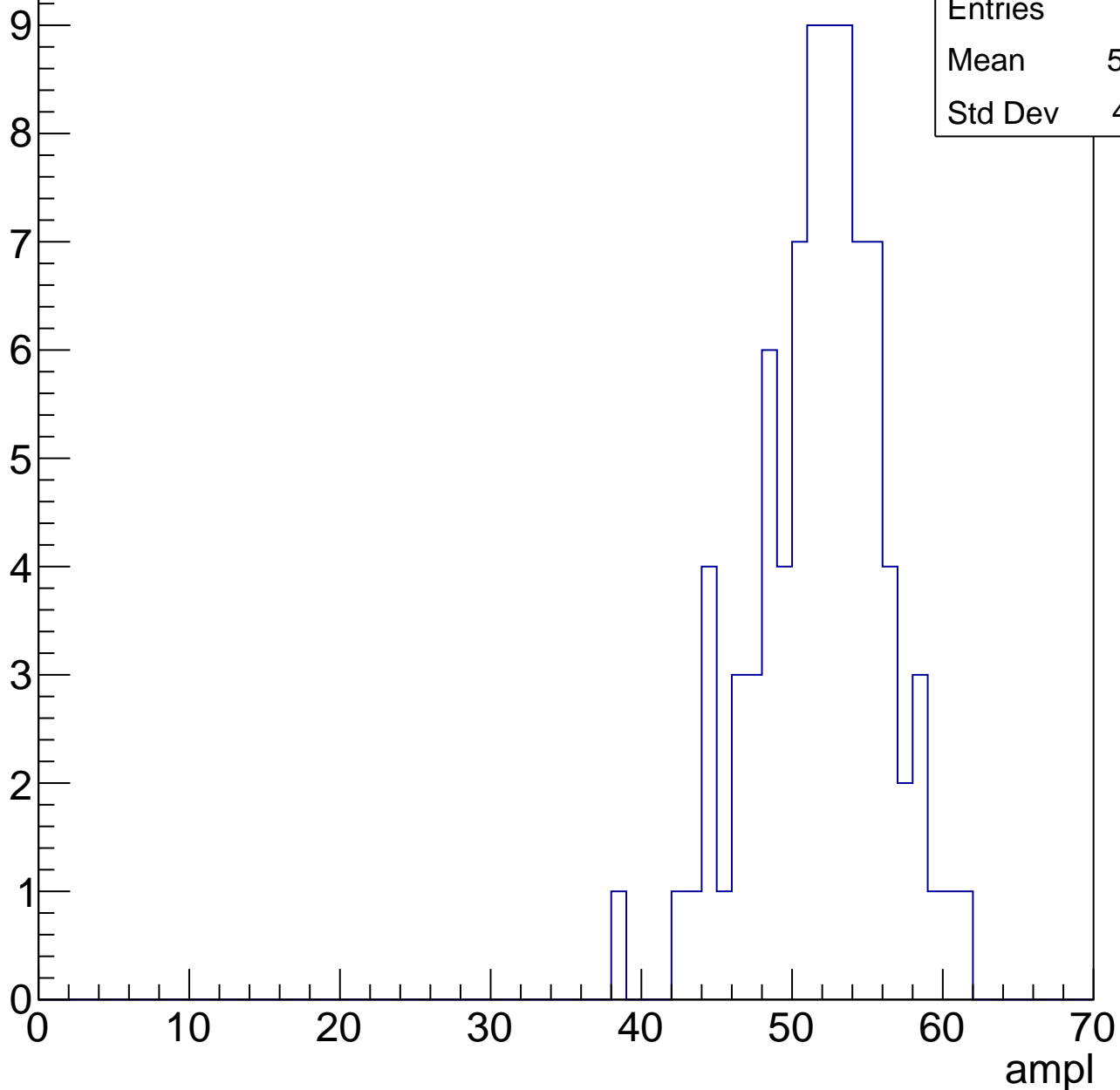
Gaus Width: 3.4948



B0L002S, U9-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

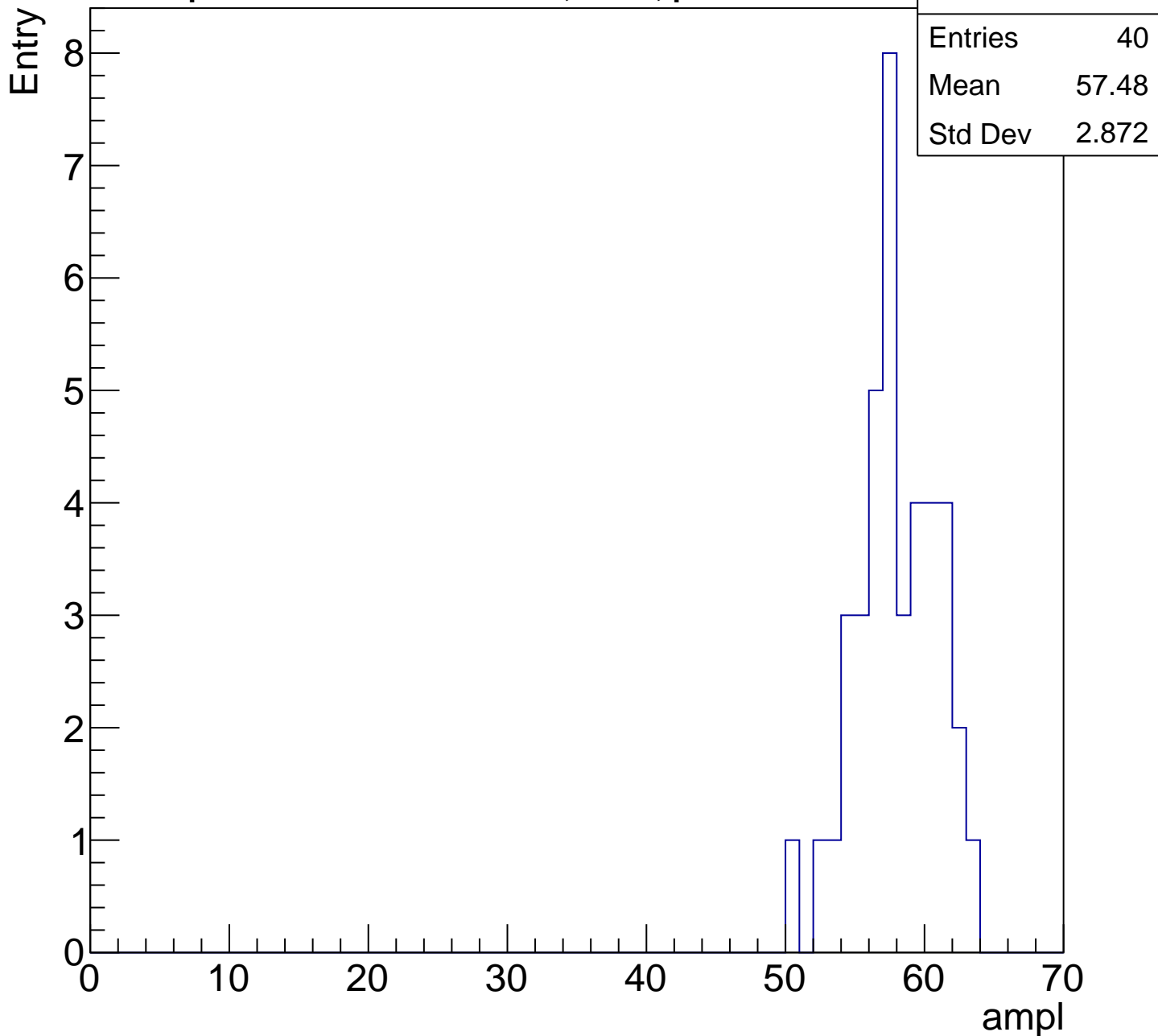
Entry



Entries	84
Mean	51.38
Std Dev	4.251

B0L002S, U9-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

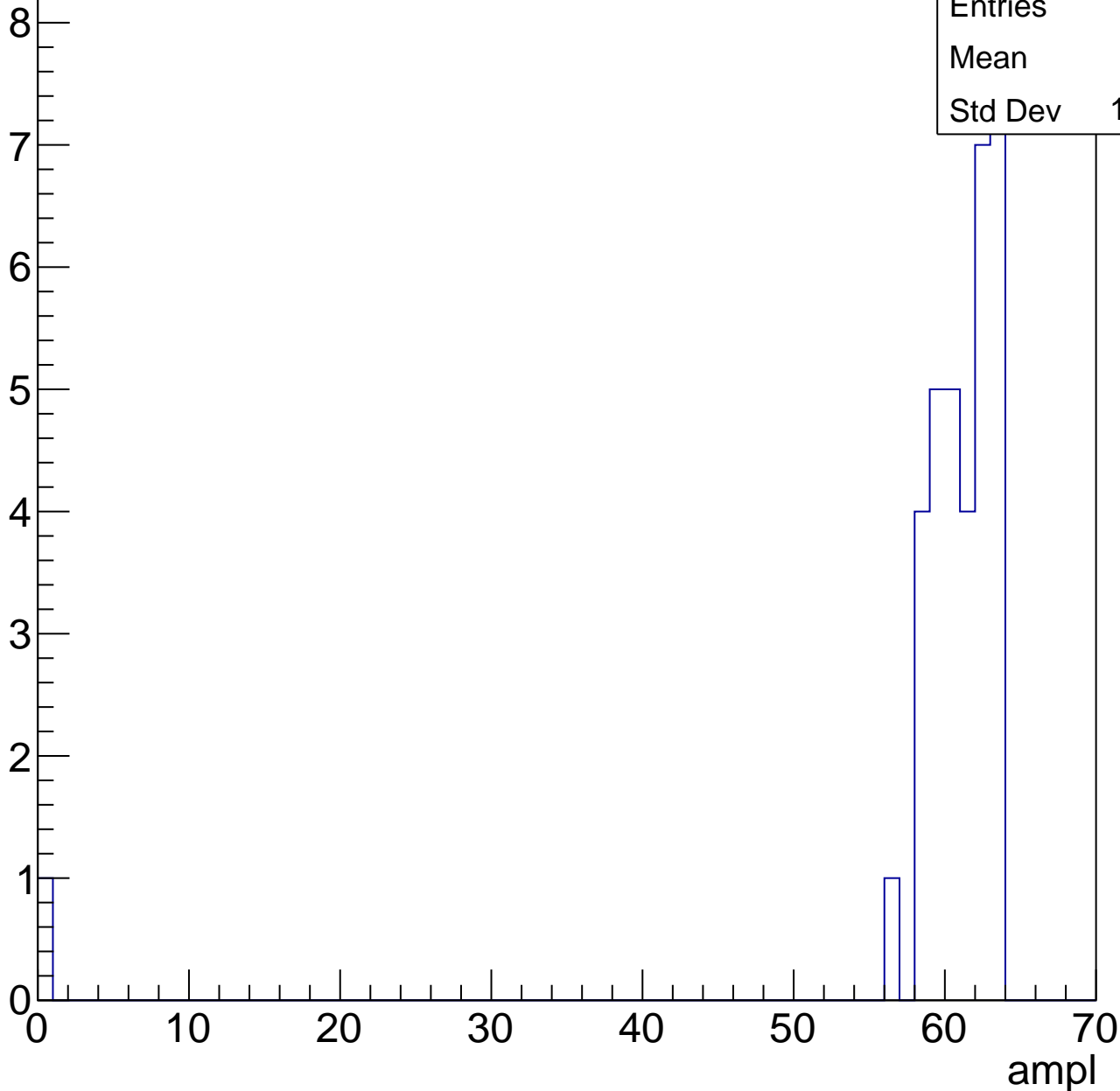


B0L002S, U9-ch33, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

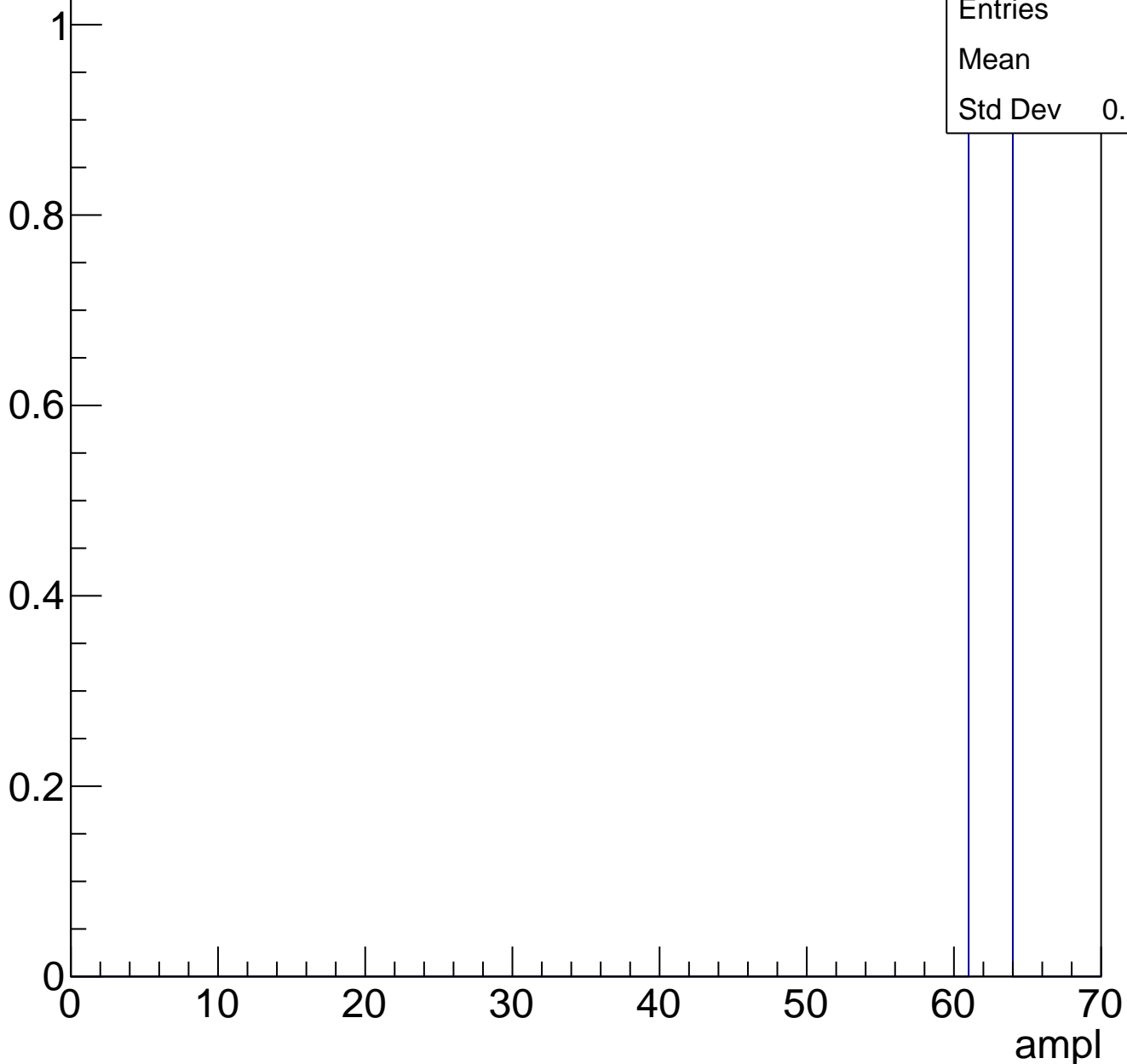
Entries	35
Mean	59
Std Dev	10.29



B0L002S, U9-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch34, adc0

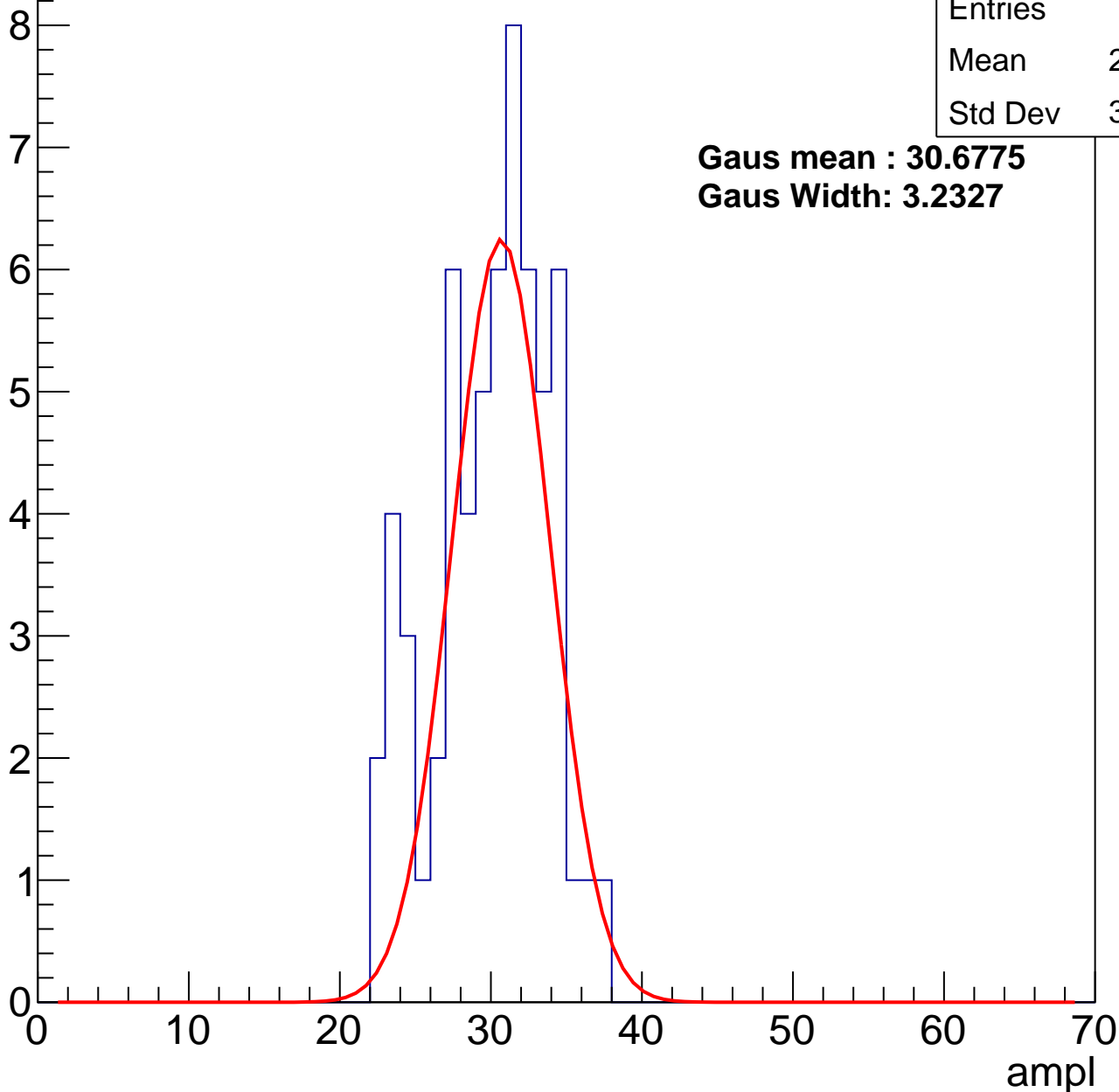
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.52
Std Dev	3.696

Gaus mean : 30.6775

Gaus Width: 3.2327



B0L002S, U9-ch34, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	37.29
Std Dev	3.257

Gaus mean : 37.8037

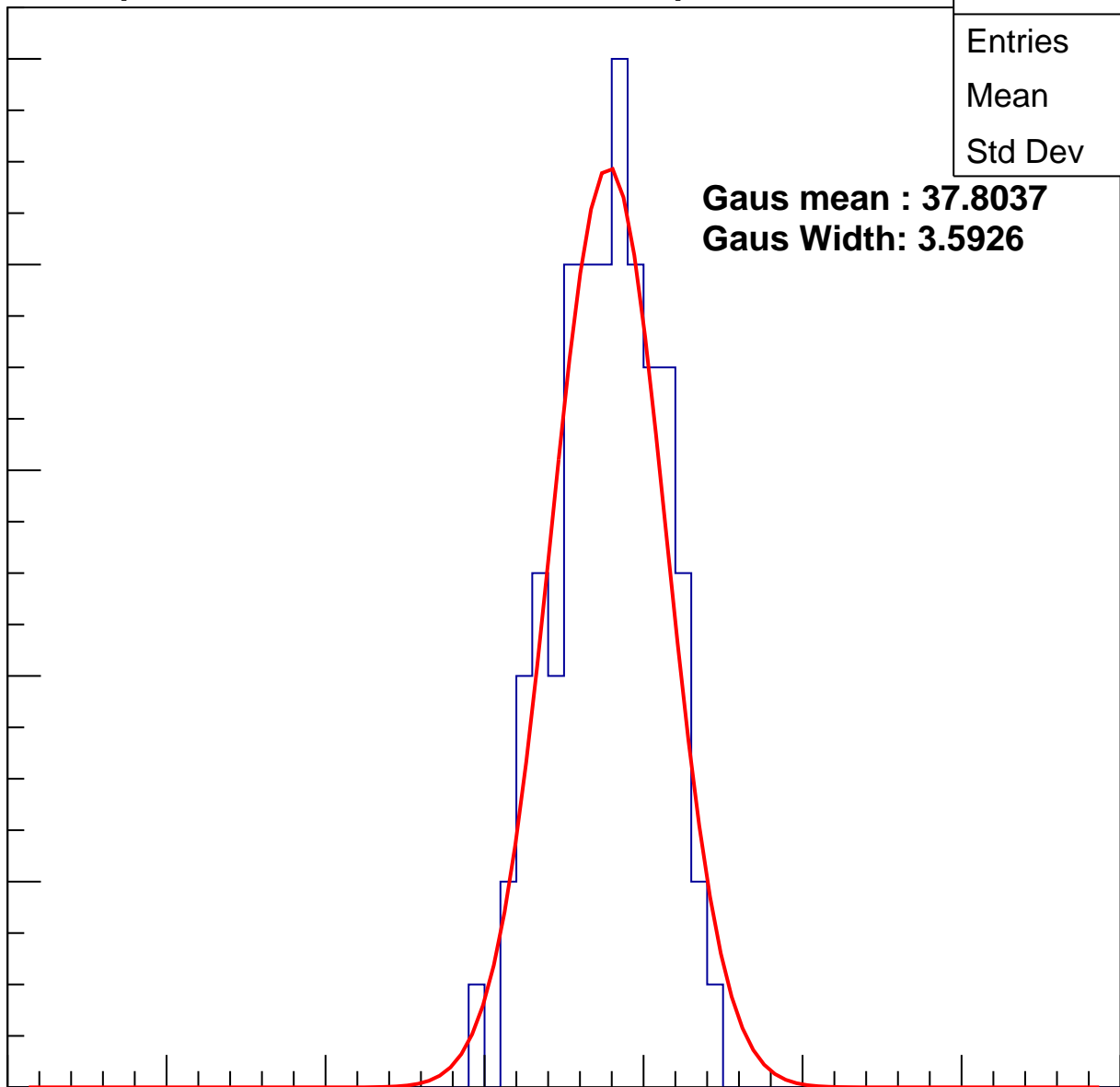
Gaus Width: 3.5926

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch34, adc2

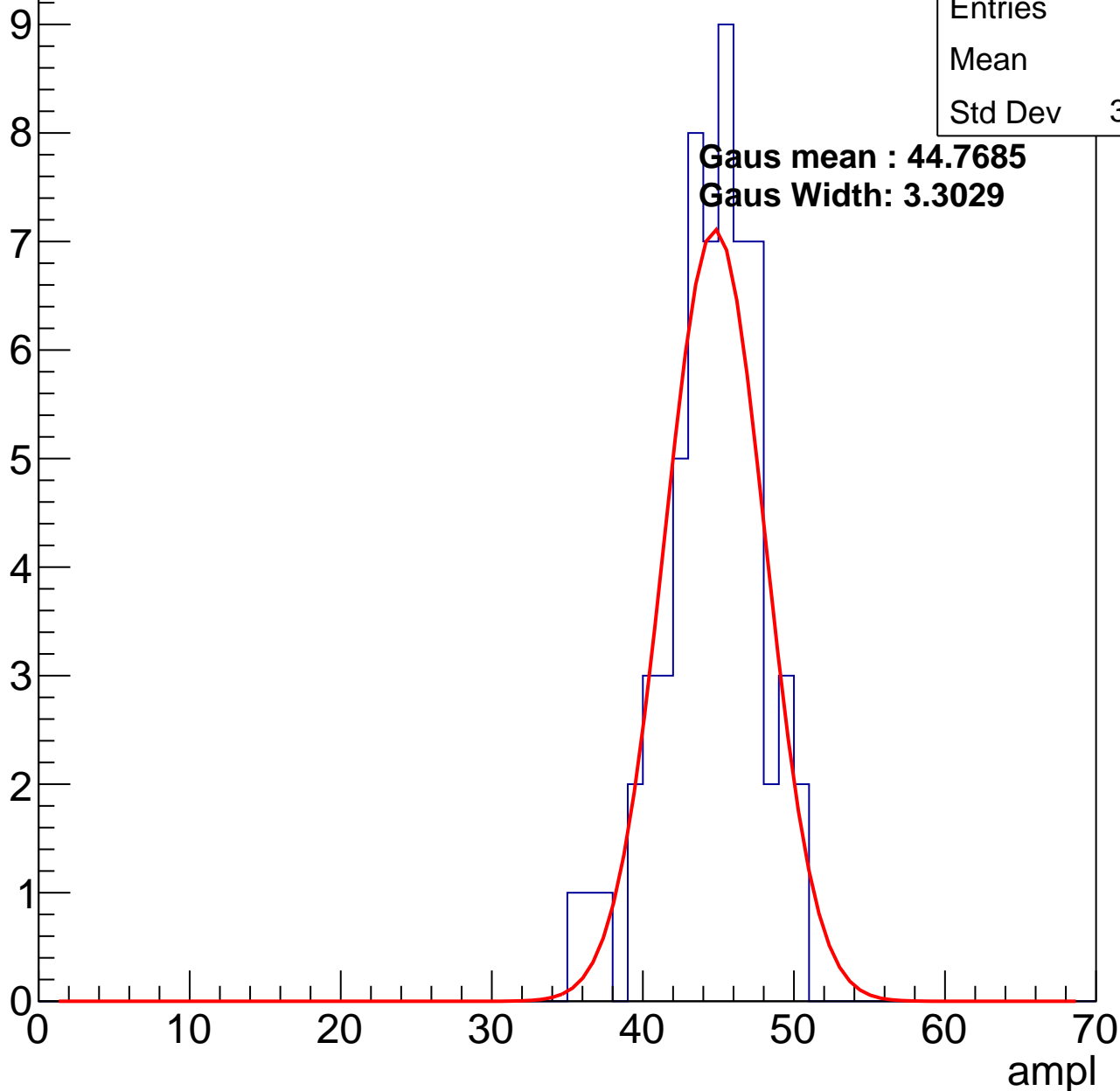
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.1
Std Dev	3.202

Gaus mean : 44.7685

Gaus Width: 3.3029

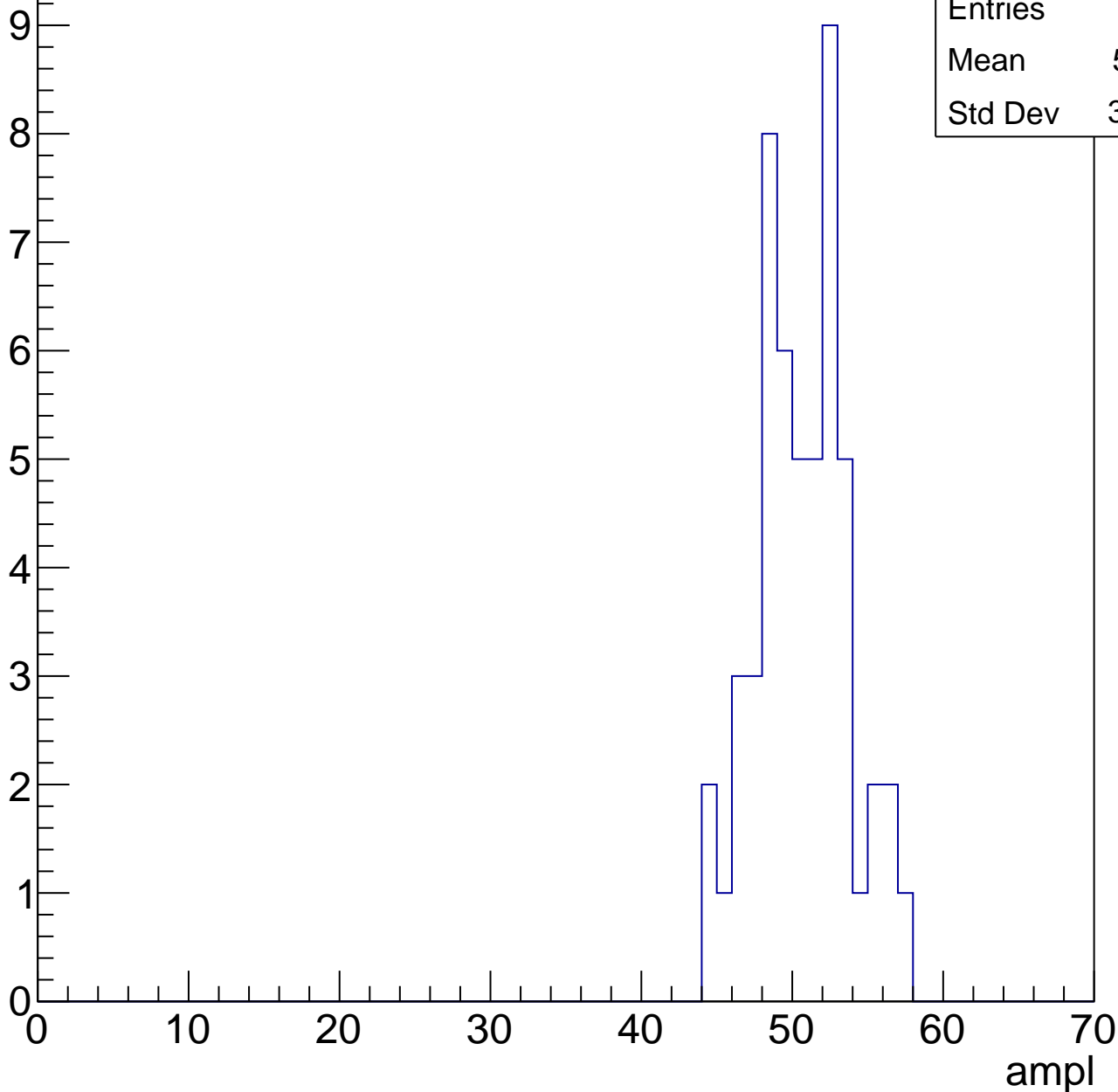


B0L002S, U9-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	50.21
Std Dev	3.012

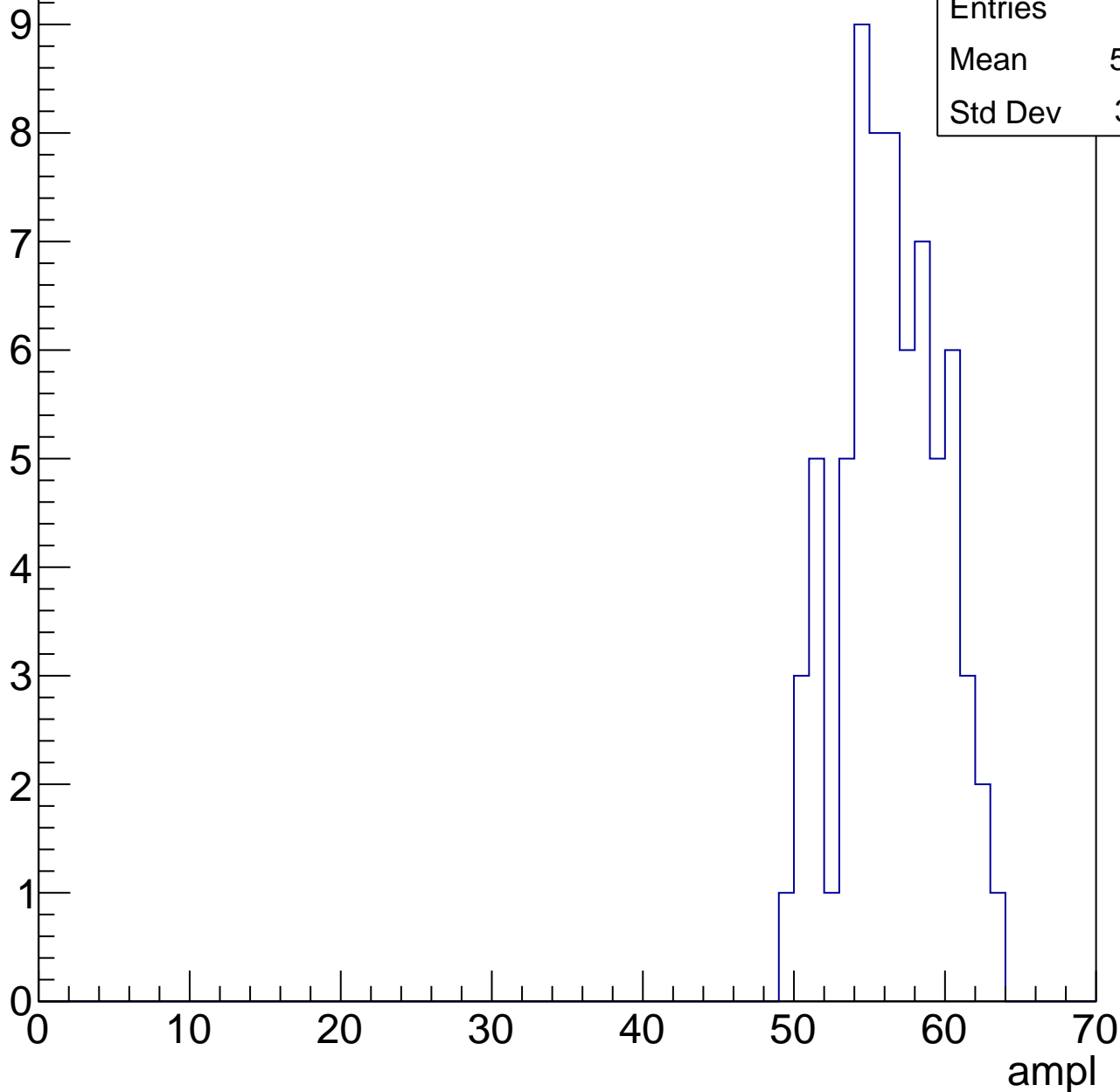


B0L002S, U9-ch34, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	55.97
Std Dev	3.291

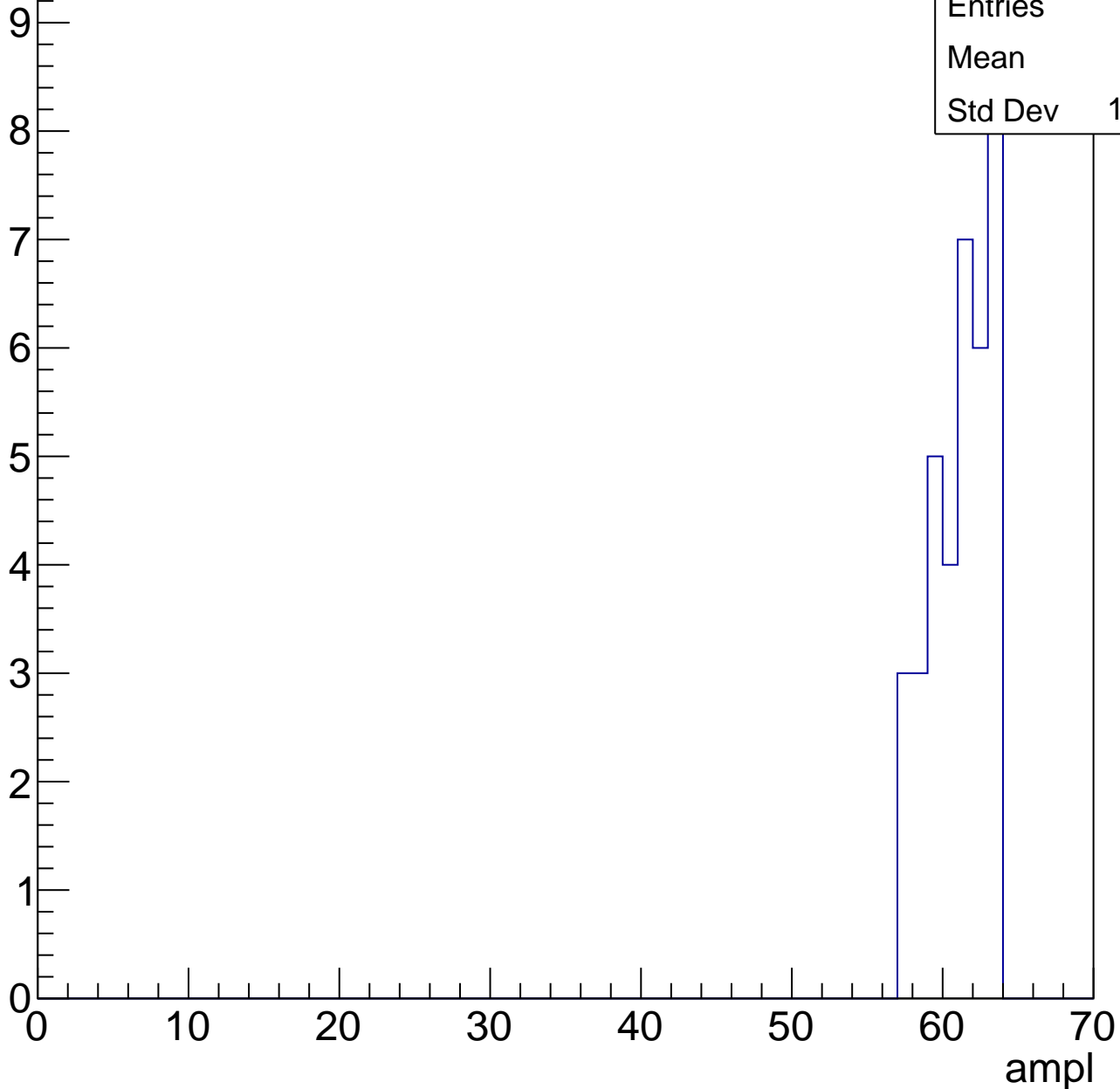


B0L002S, U9-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

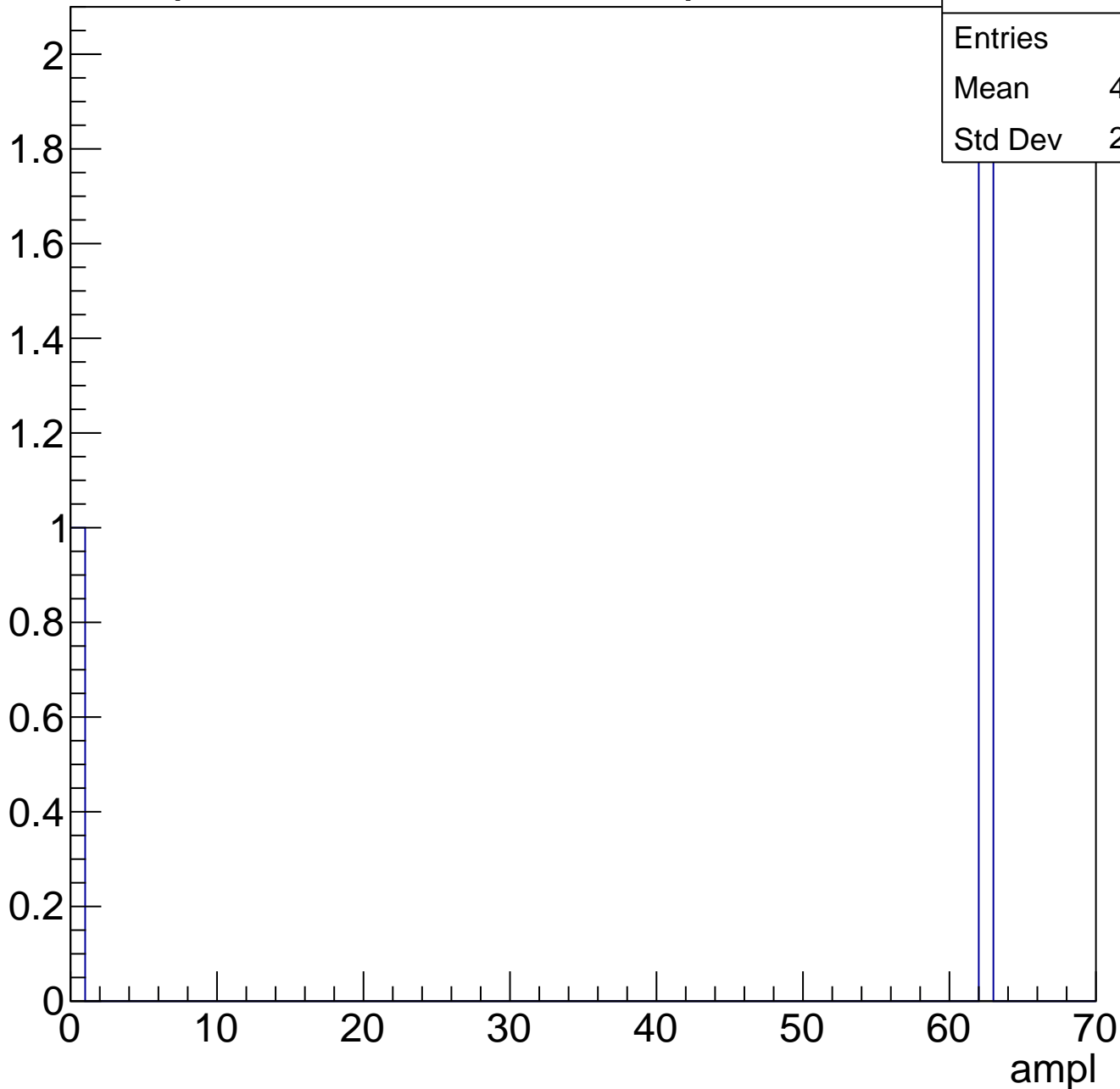
Entries	37
Mean	60.7
Std Dev	1.929



B0L002S, U9-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch35, adc0

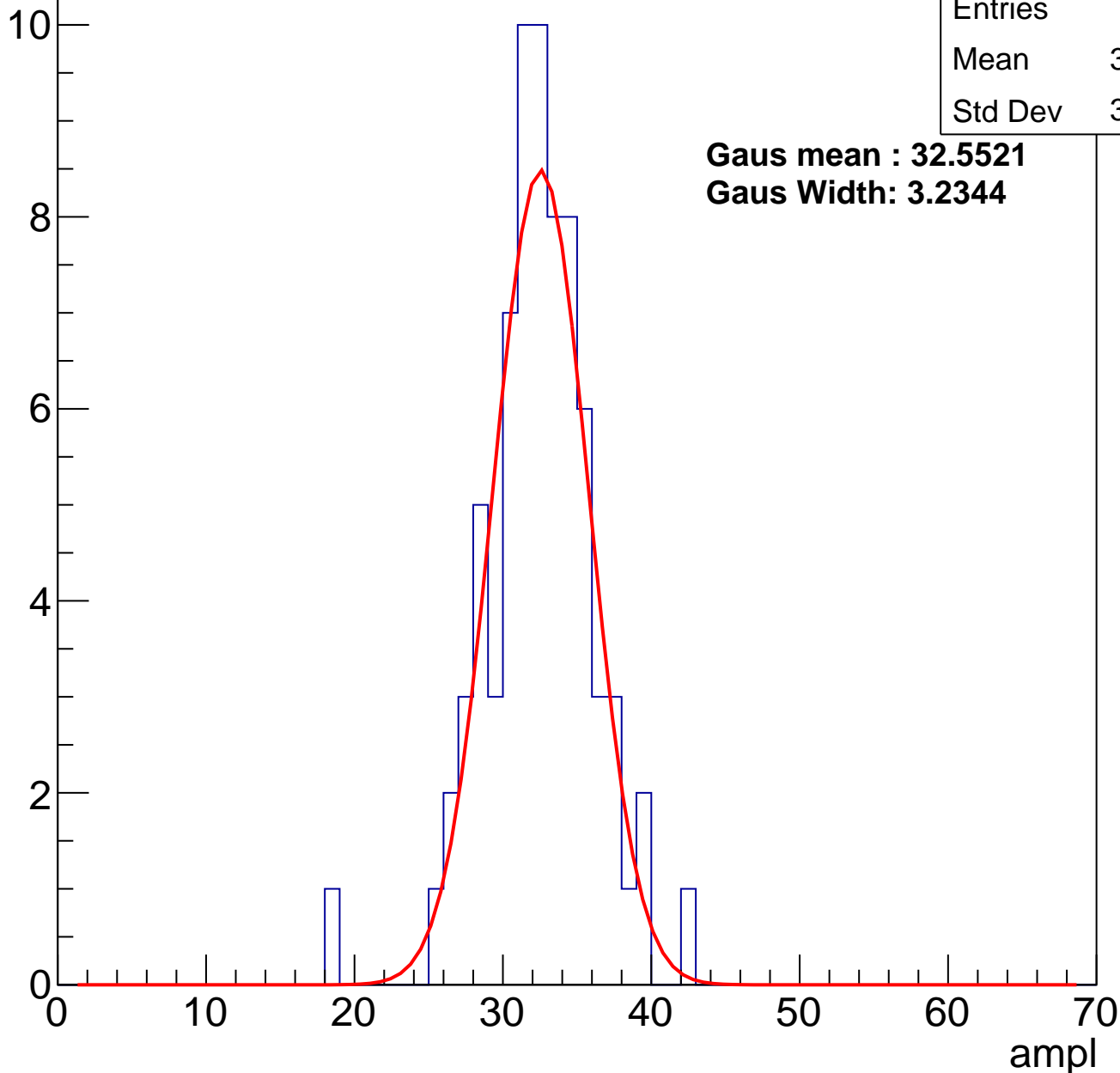
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	31.97
Std Dev	3.654

Gaus mean : 32.5521

Gaus Width: 3.2344

Entry



B0L002S, U9-ch35, adc1

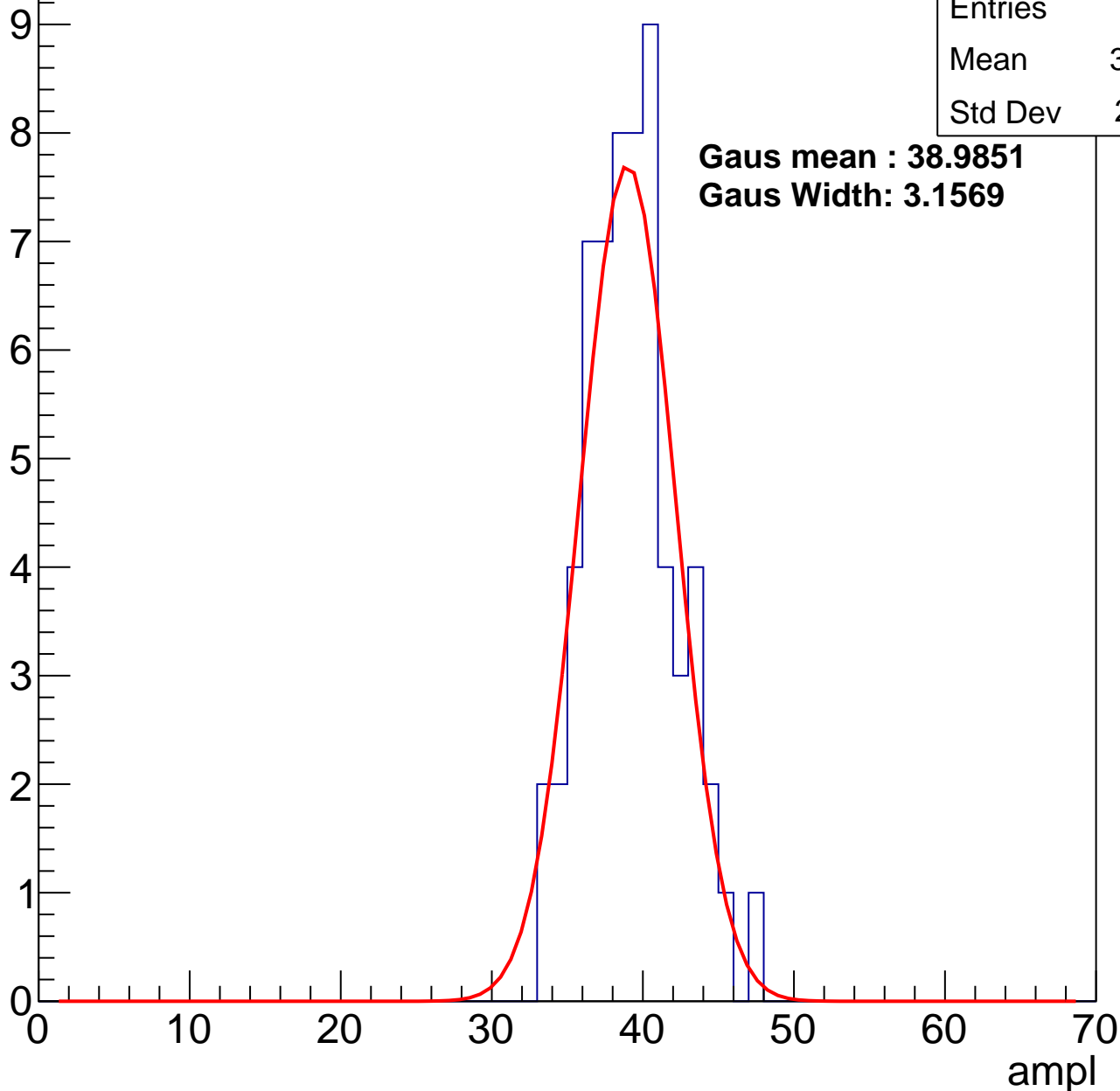
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.76
Std Dev	2.971

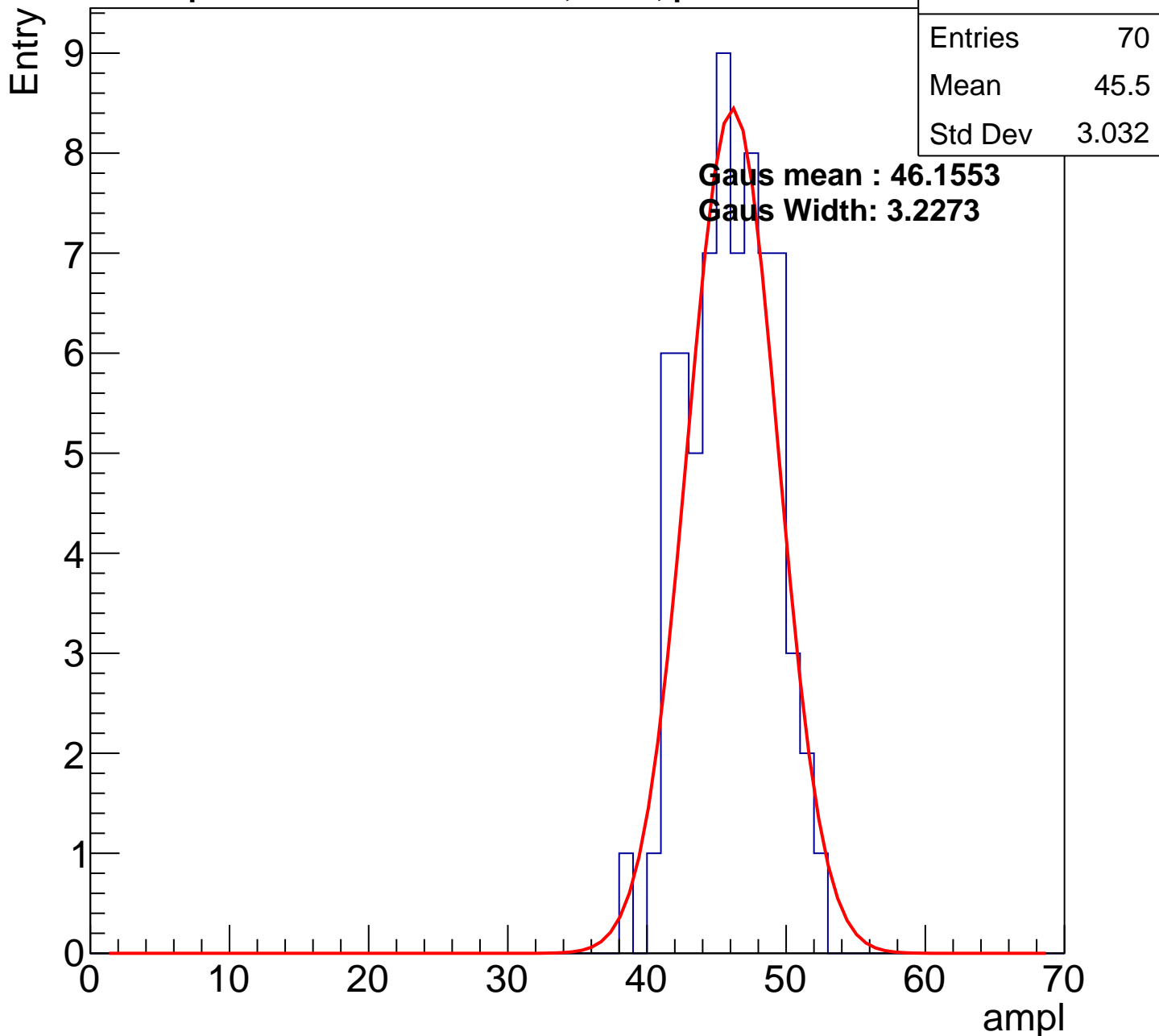
Gaus mean : 38.9851

Gaus Width: 3.1569



B0L002S, U9-ch35, adc2

calib_packv5_042523_0143.root, FC#8, port C1

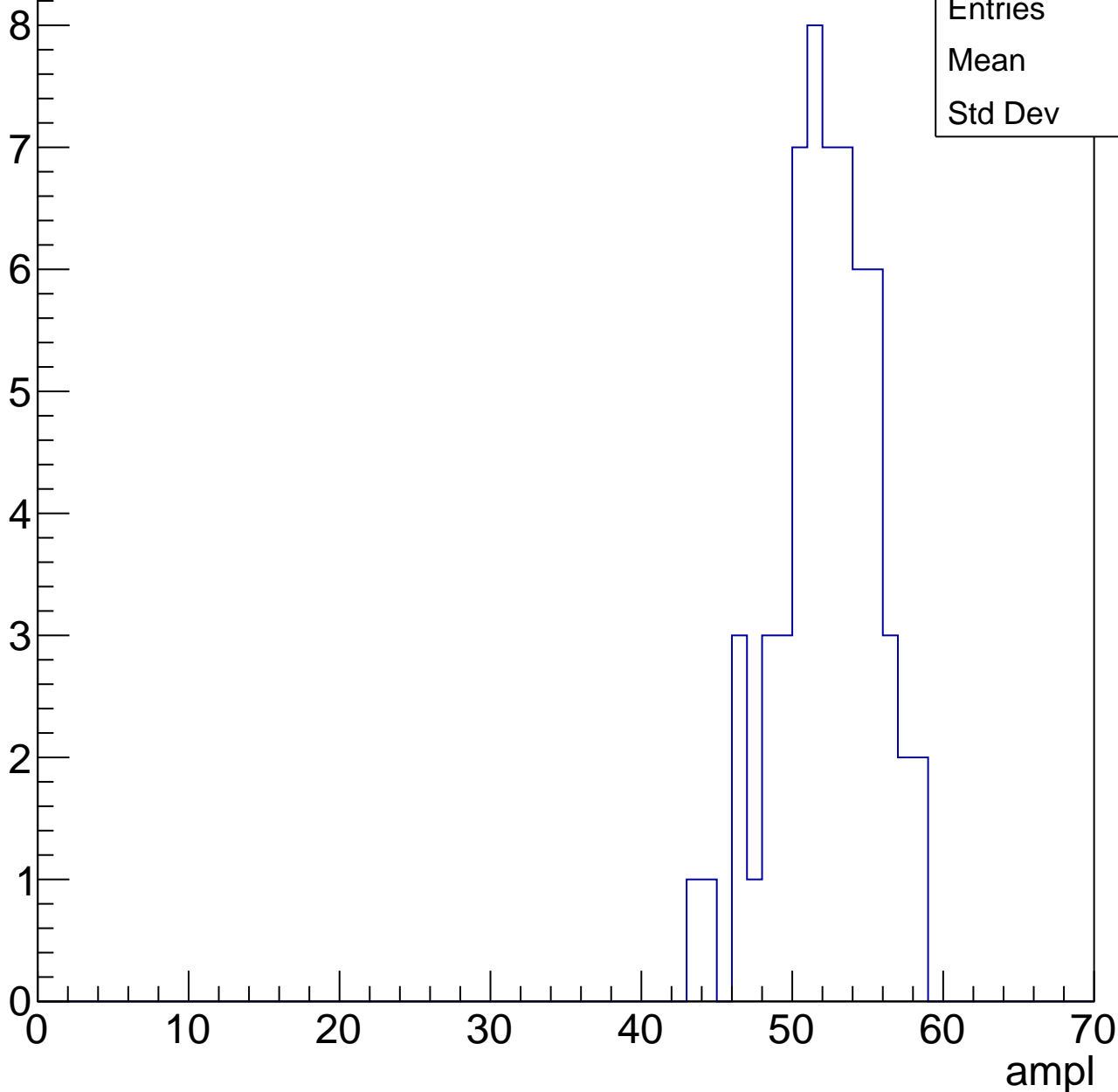


B0L002S, U9-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

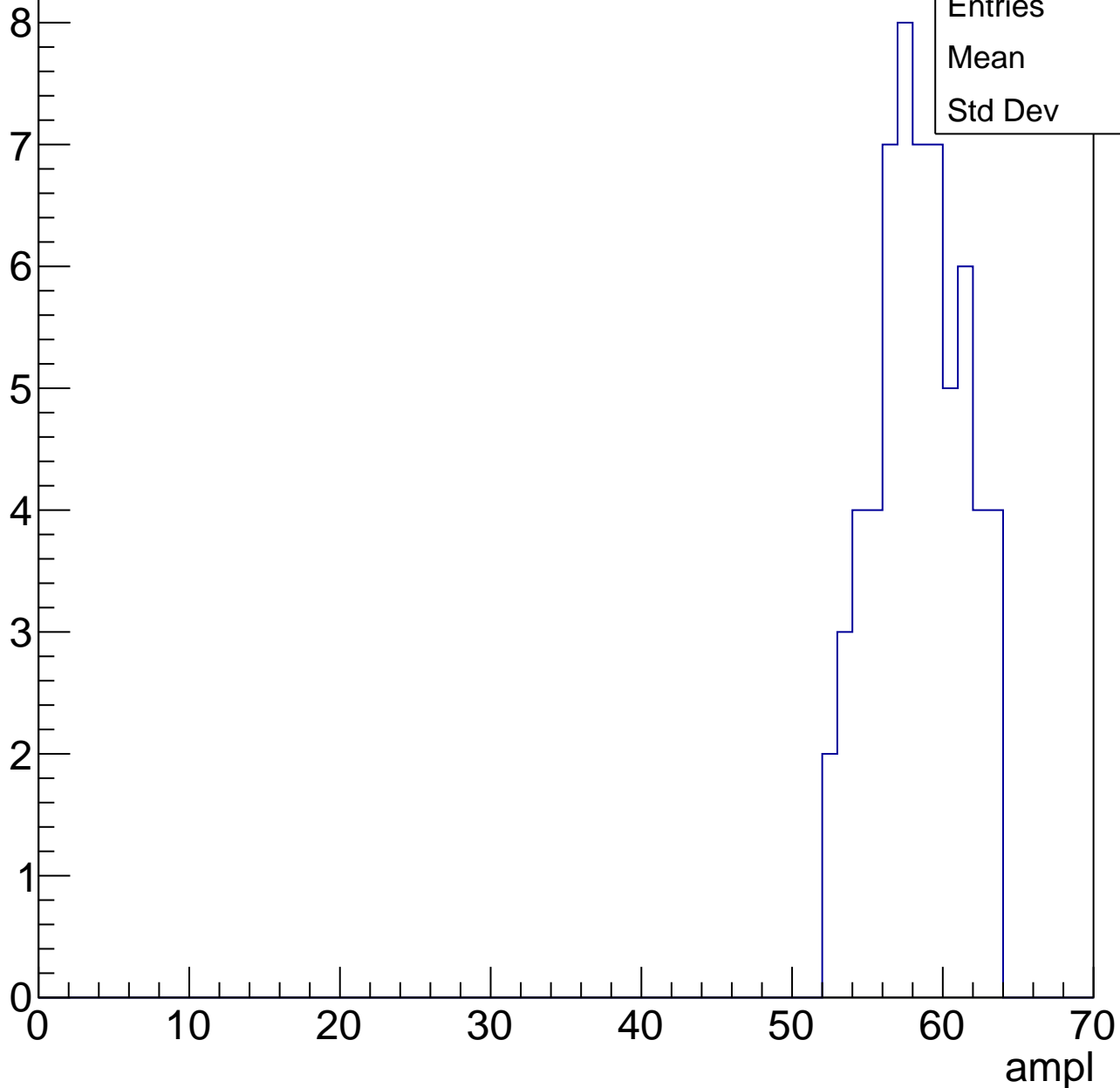
Entries	60
Mean	51.8
Std Dev	3.27



B0L002S, U9-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

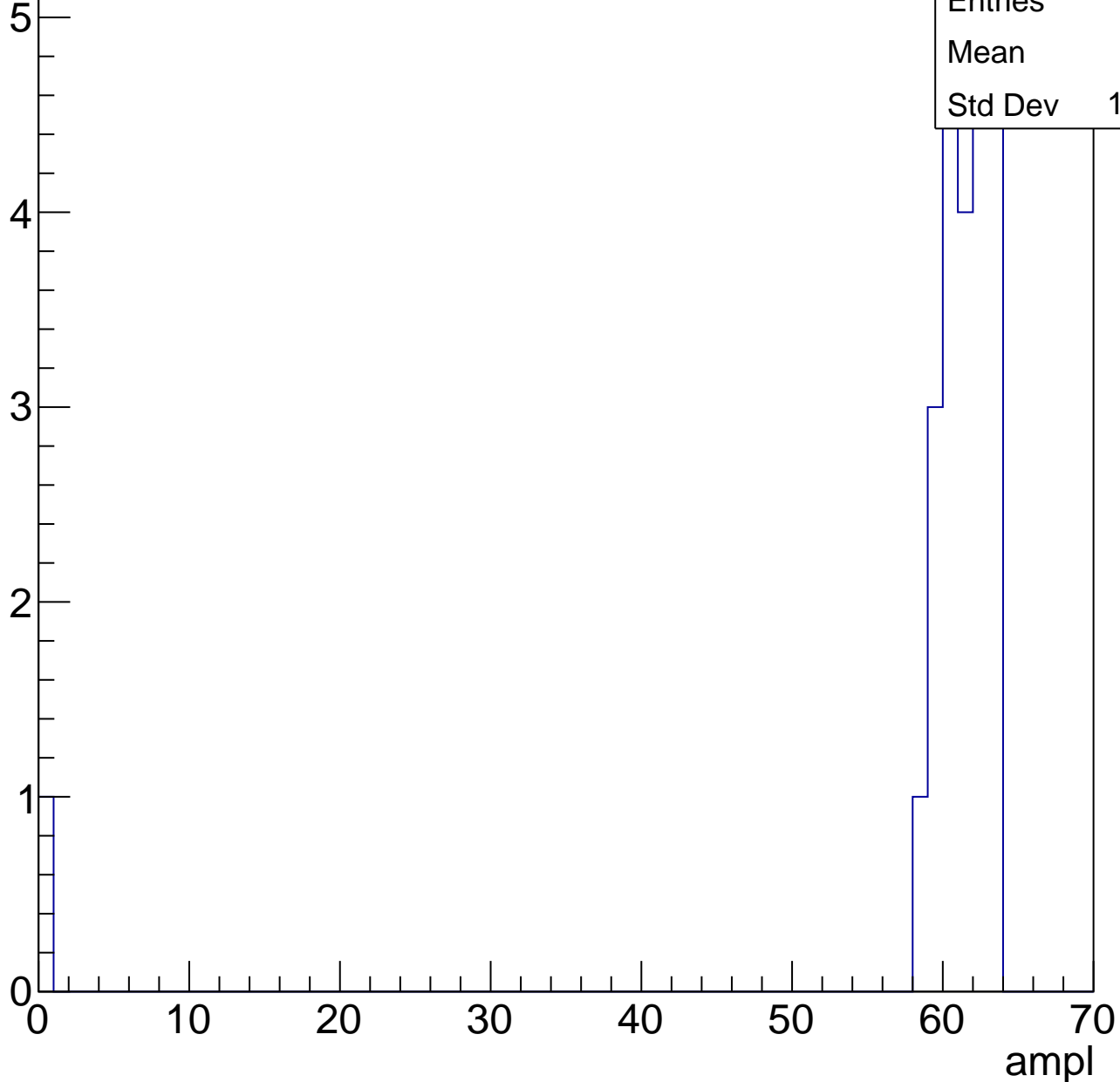


Entries	61
Mean	57.9
Std Dev	2.94

B0L002S, U9-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch36, adc0

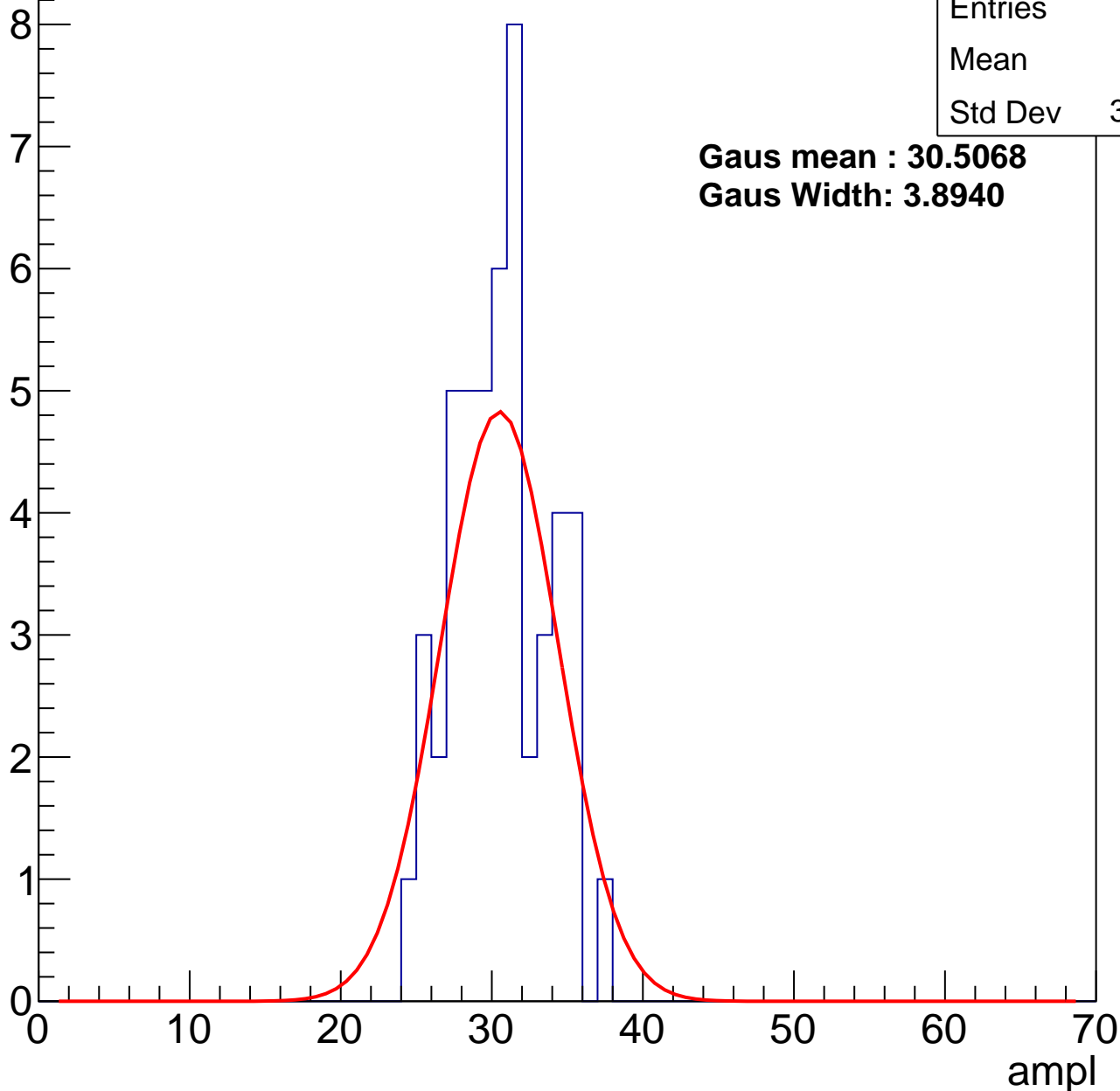
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	30.1
Std Dev	3.092

Gaus mean : 30.5068

Gaus Width: 3.8940



B0L002S, U9-ch36, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	36.49
Std Dev	3.596

Gaus mean : 37.0818

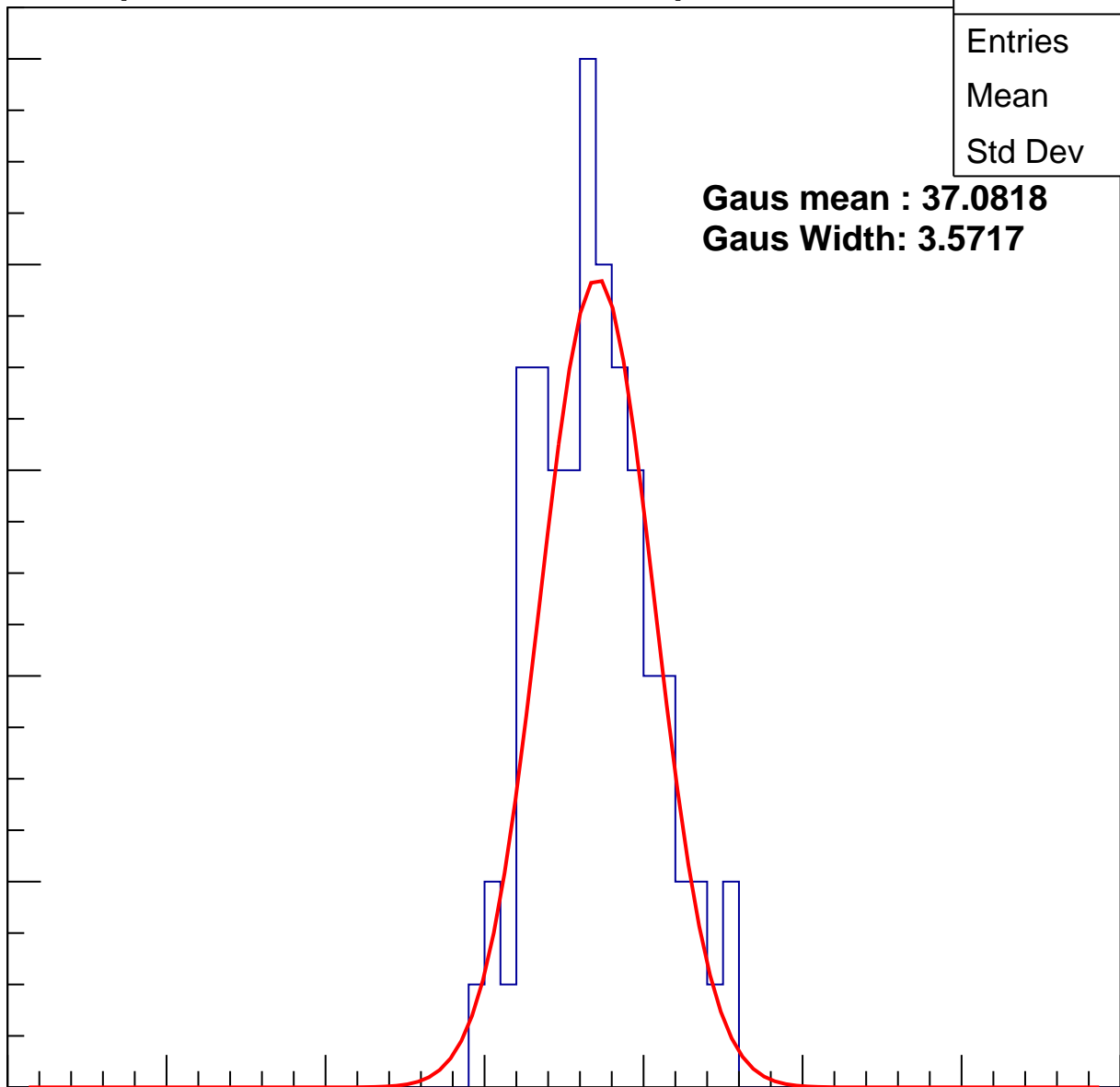
Gaus Width: 3.5717

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch36, adc2

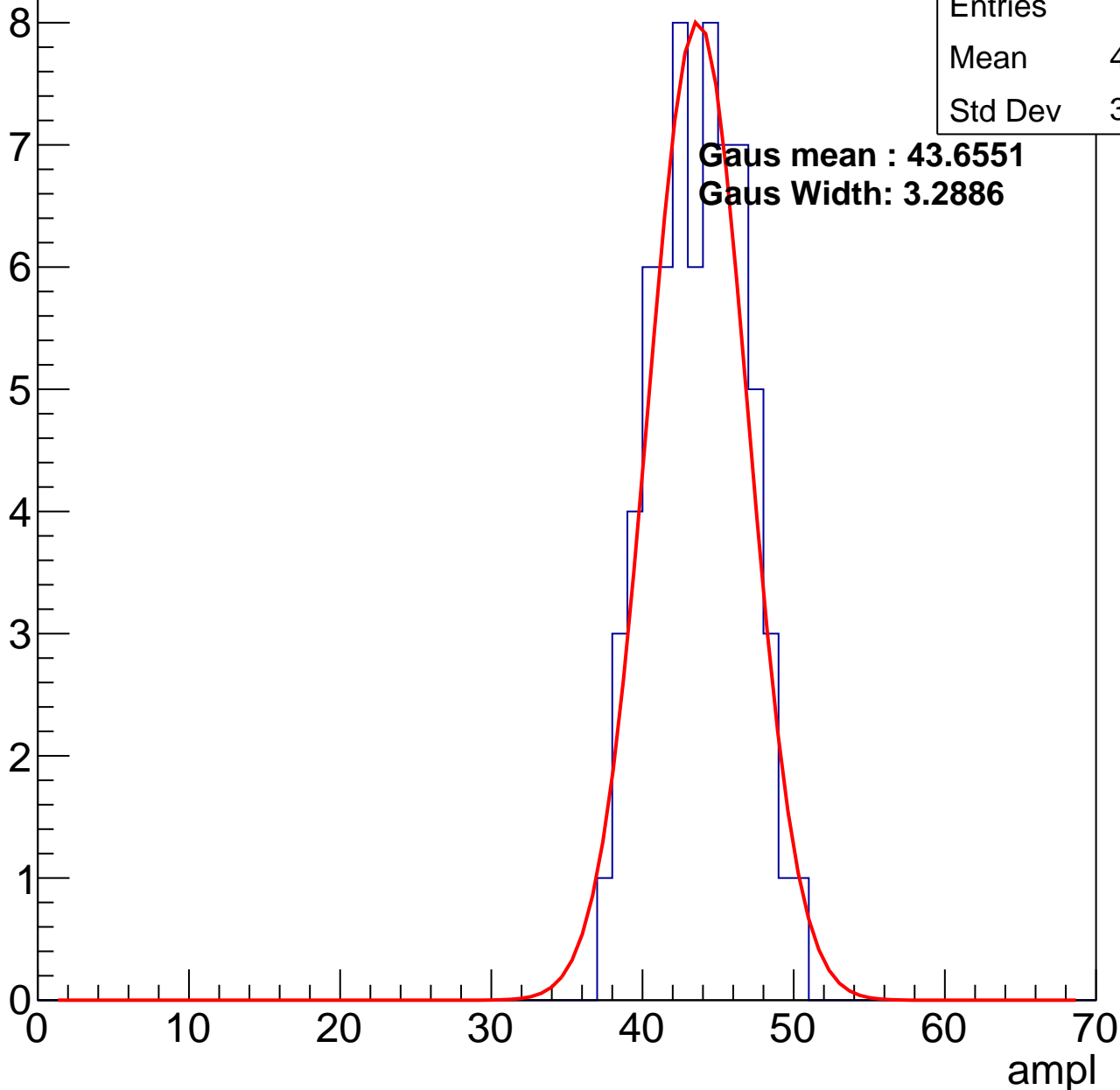
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.24
Std Dev	3.005

Gaus mean : 43.6551

Gaus Width: 3.2886



B0L002S, U9-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

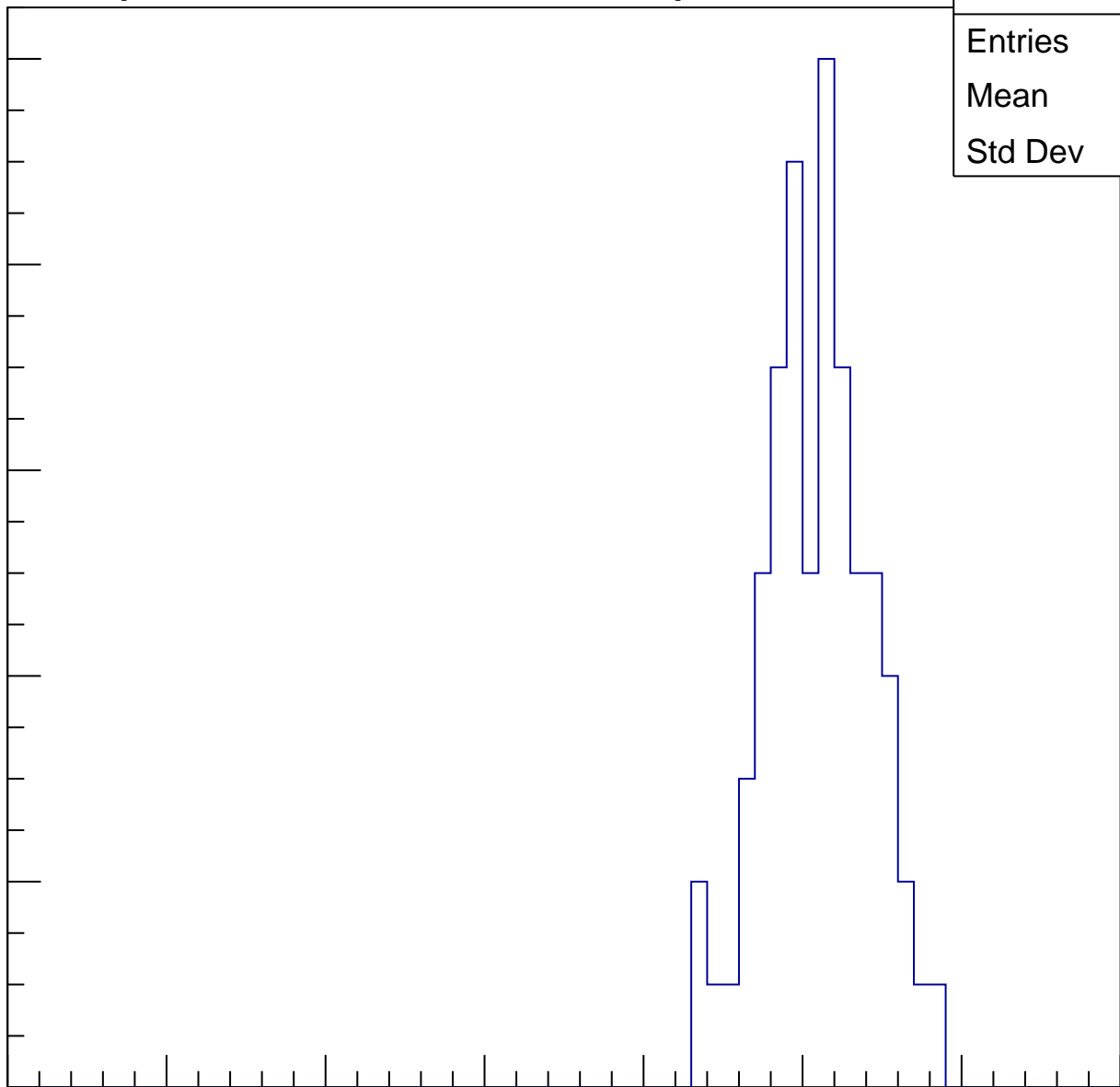
Entries	68
Mean	50.46
Std Dev	3.26

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

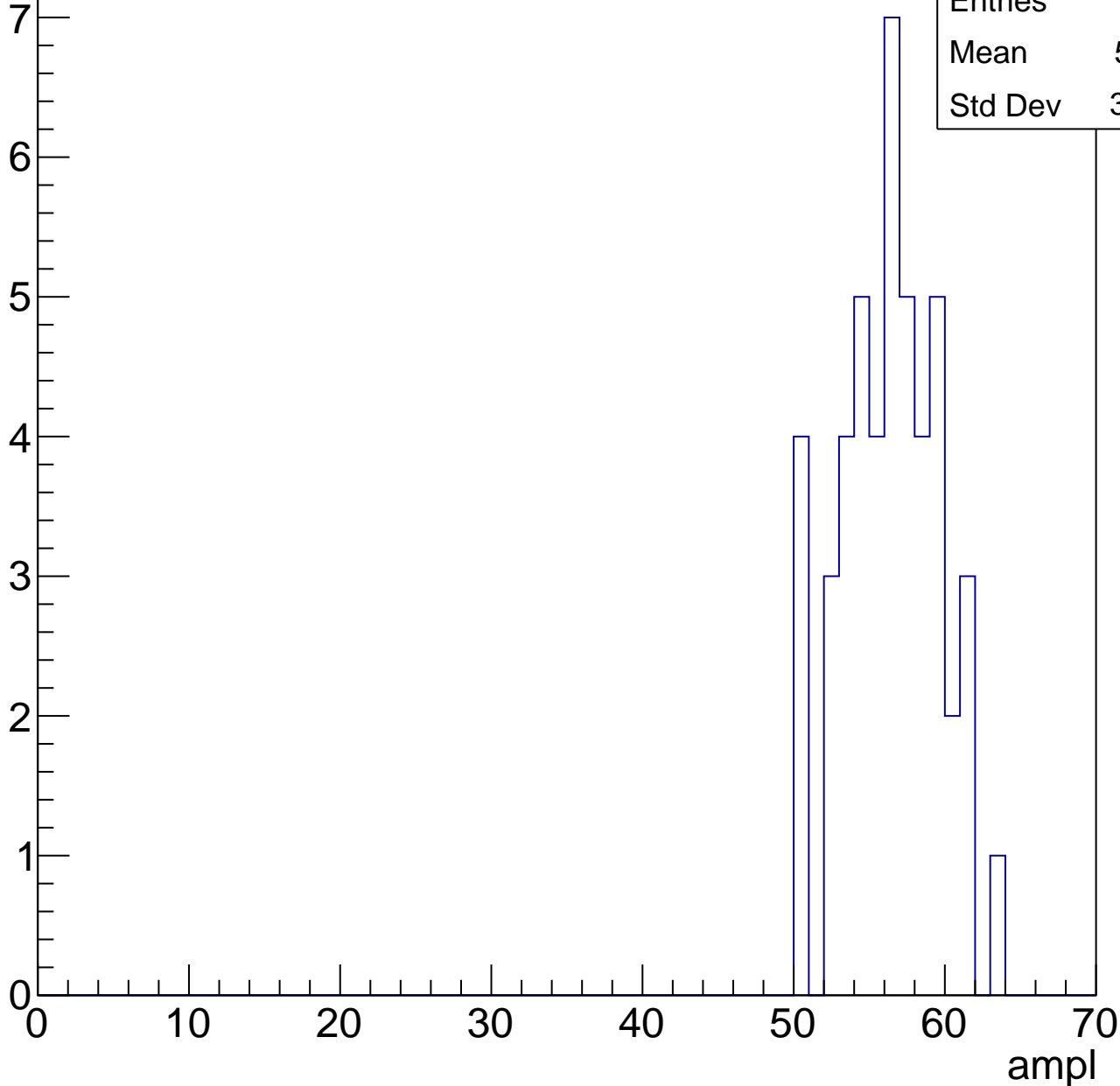


B0L002S, U9-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	55.91
Std Dev	3.175

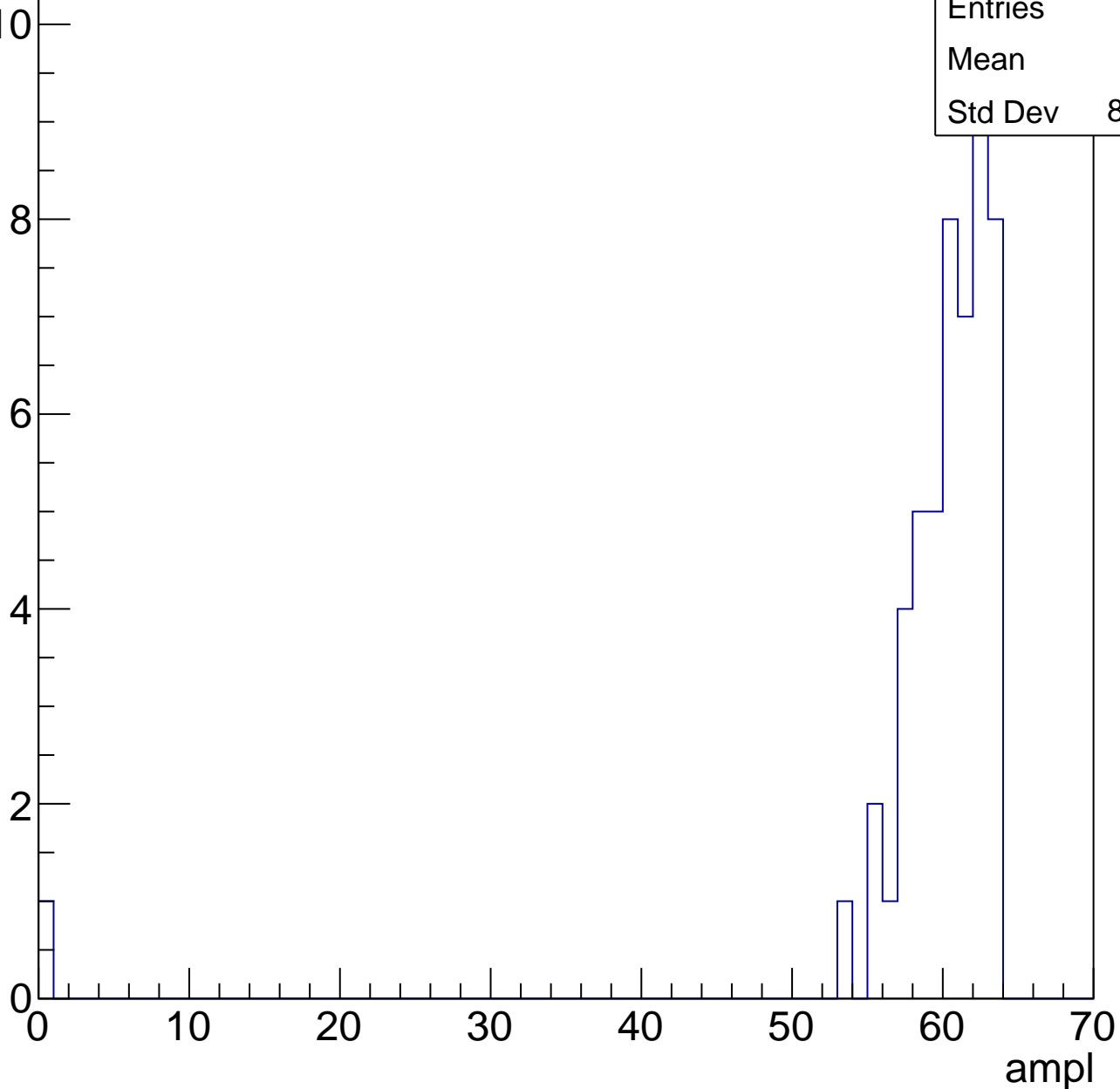


B0L002S, U9-ch36, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

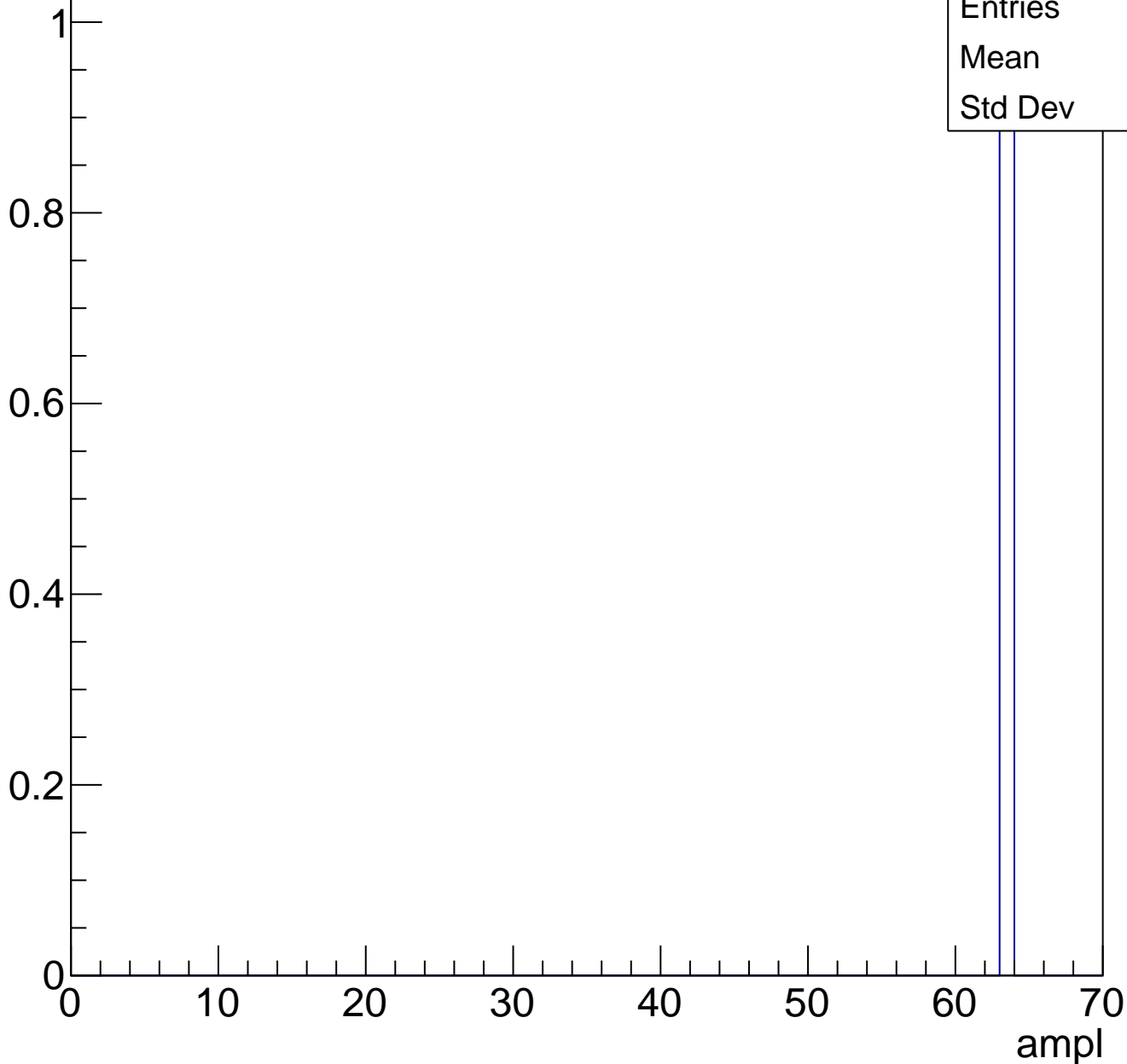
Entries	52
Mean	58.9
Std Dev	8.585



B0L002S, U9-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8
Std Dev	11.31

B0L002S, U9-ch37, adc0

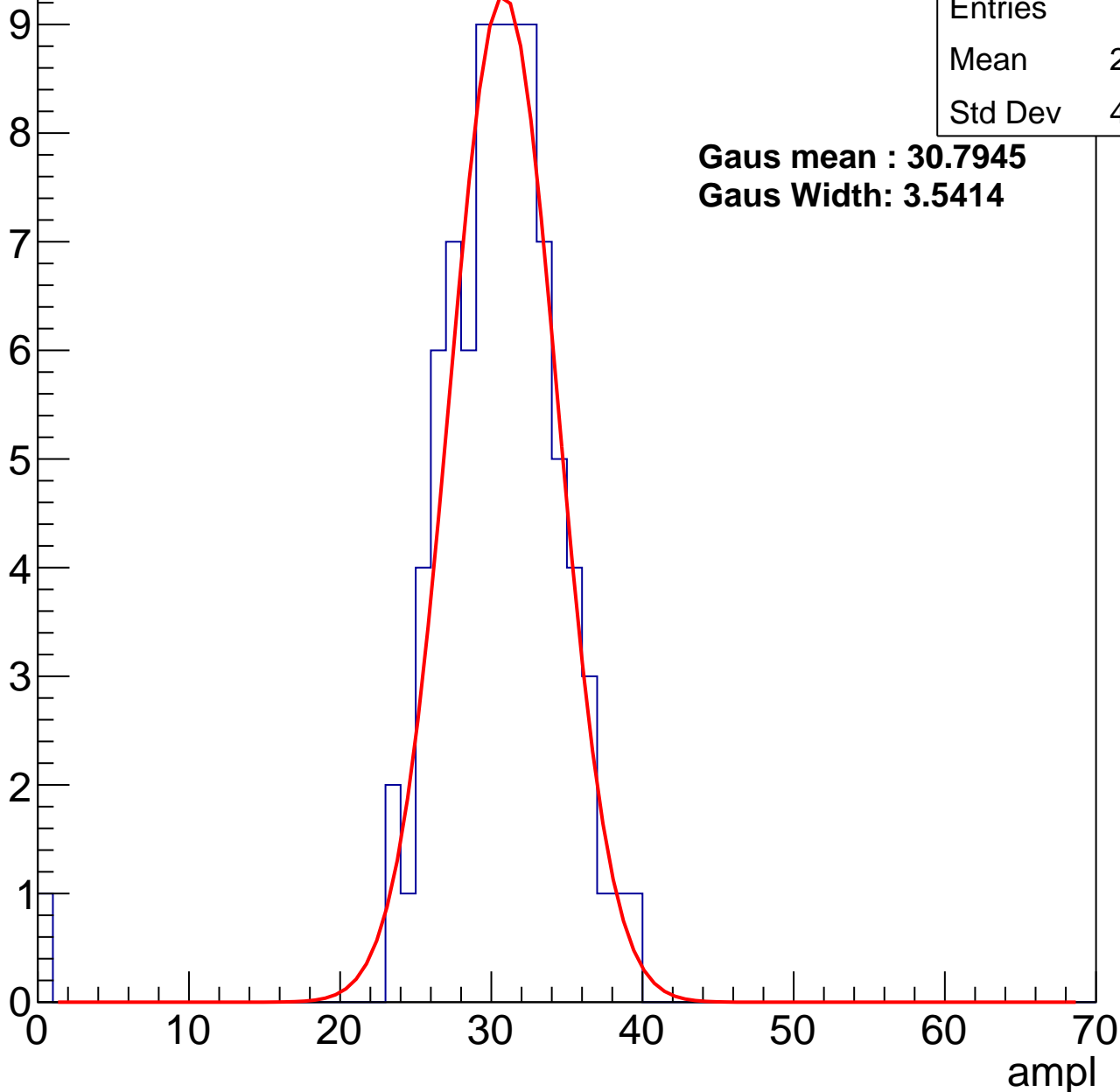
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	29.93
Std Dev	4.747

Gaus mean : 30.7945

Gaus Width: 3.5414



B0L002S, U9-ch37, adc1

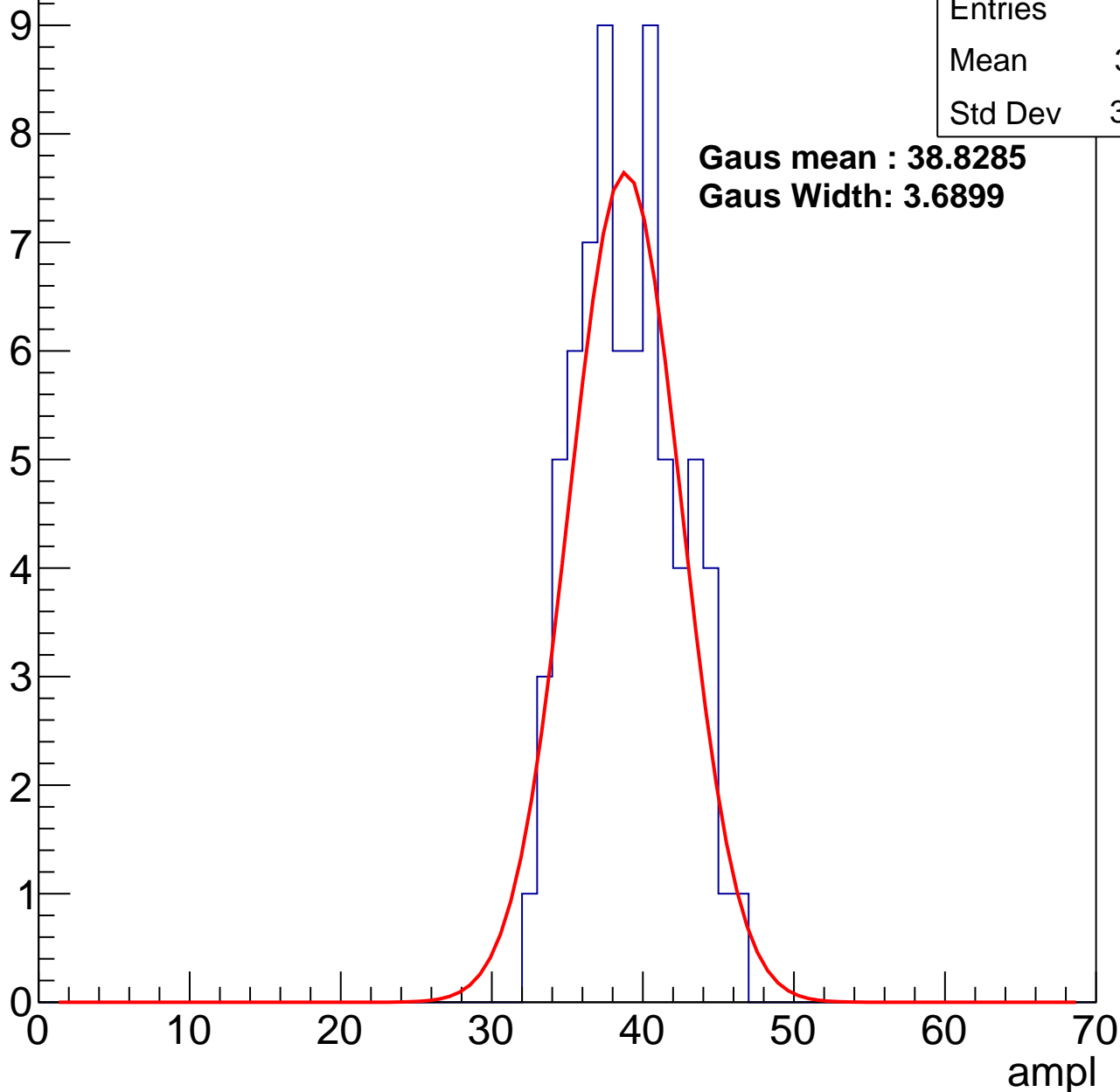
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	38.51
Std Dev	3.325

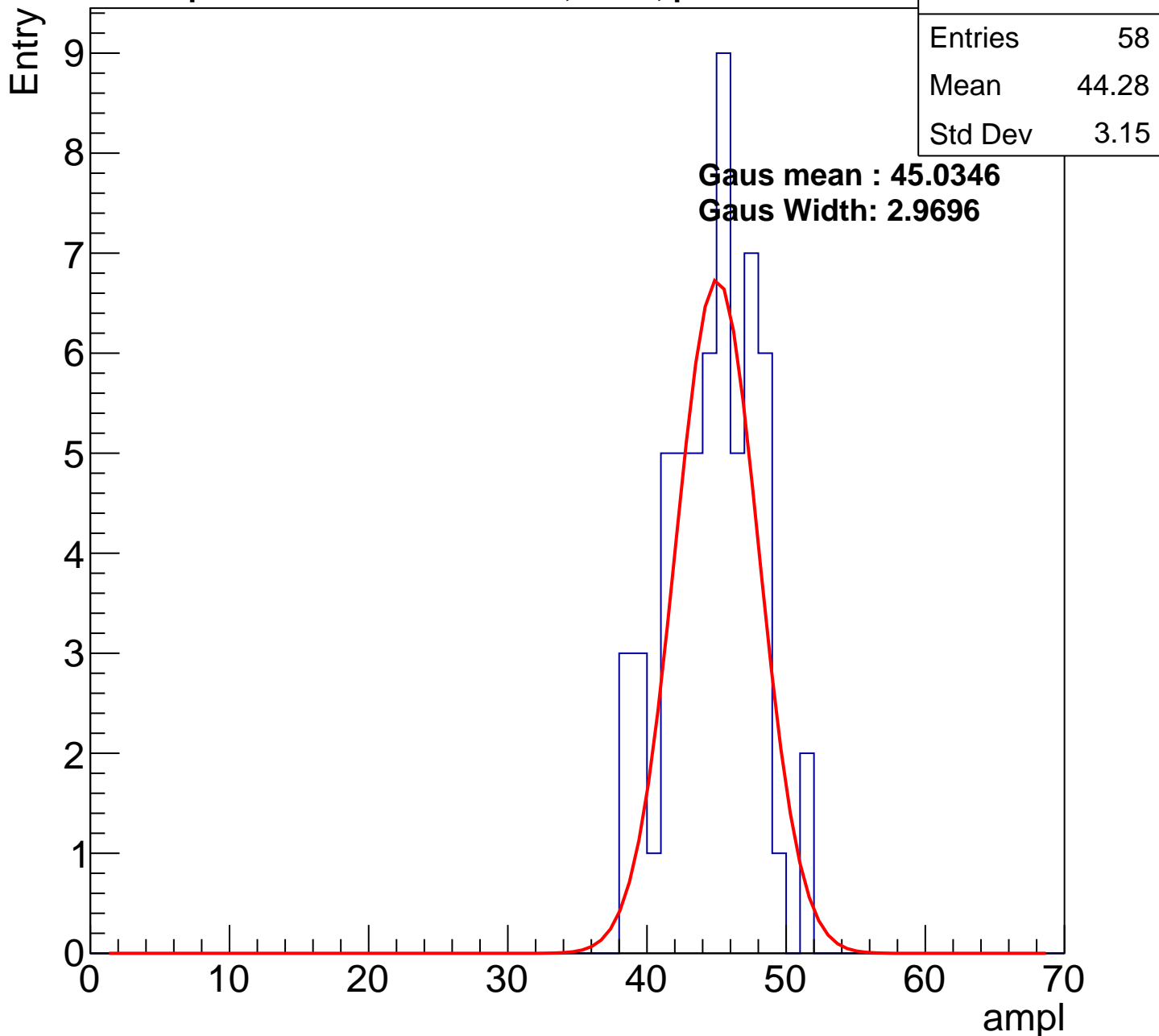
Gaus mean : 38.8285

Gaus Width: 3.6899



B0L002S, U9-ch37, adc2

calib_packv5_042523_0143.root, FC#8, port C1

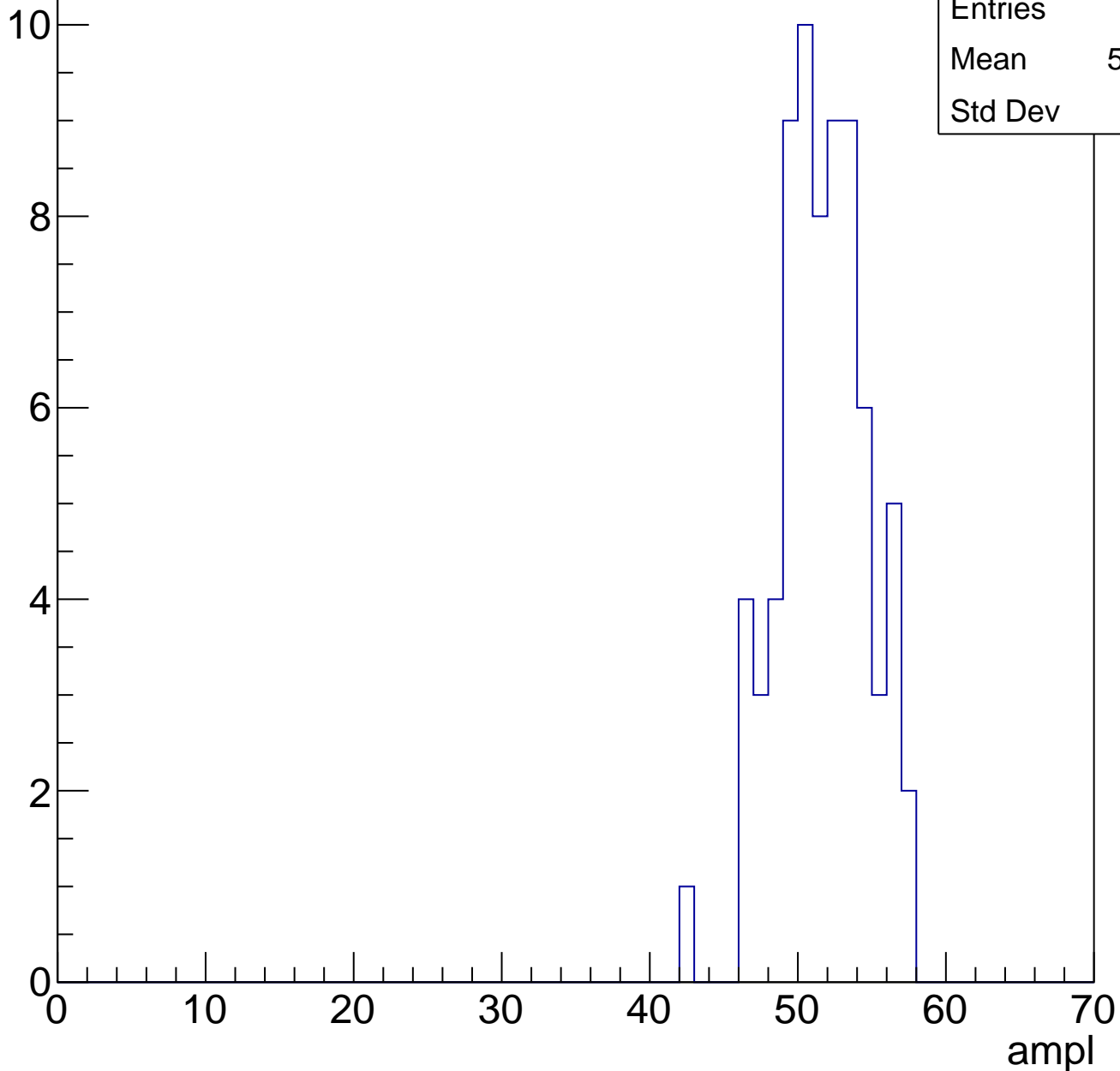


B0L002S, U9-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	51.18
Std Dev	2.99

Entry

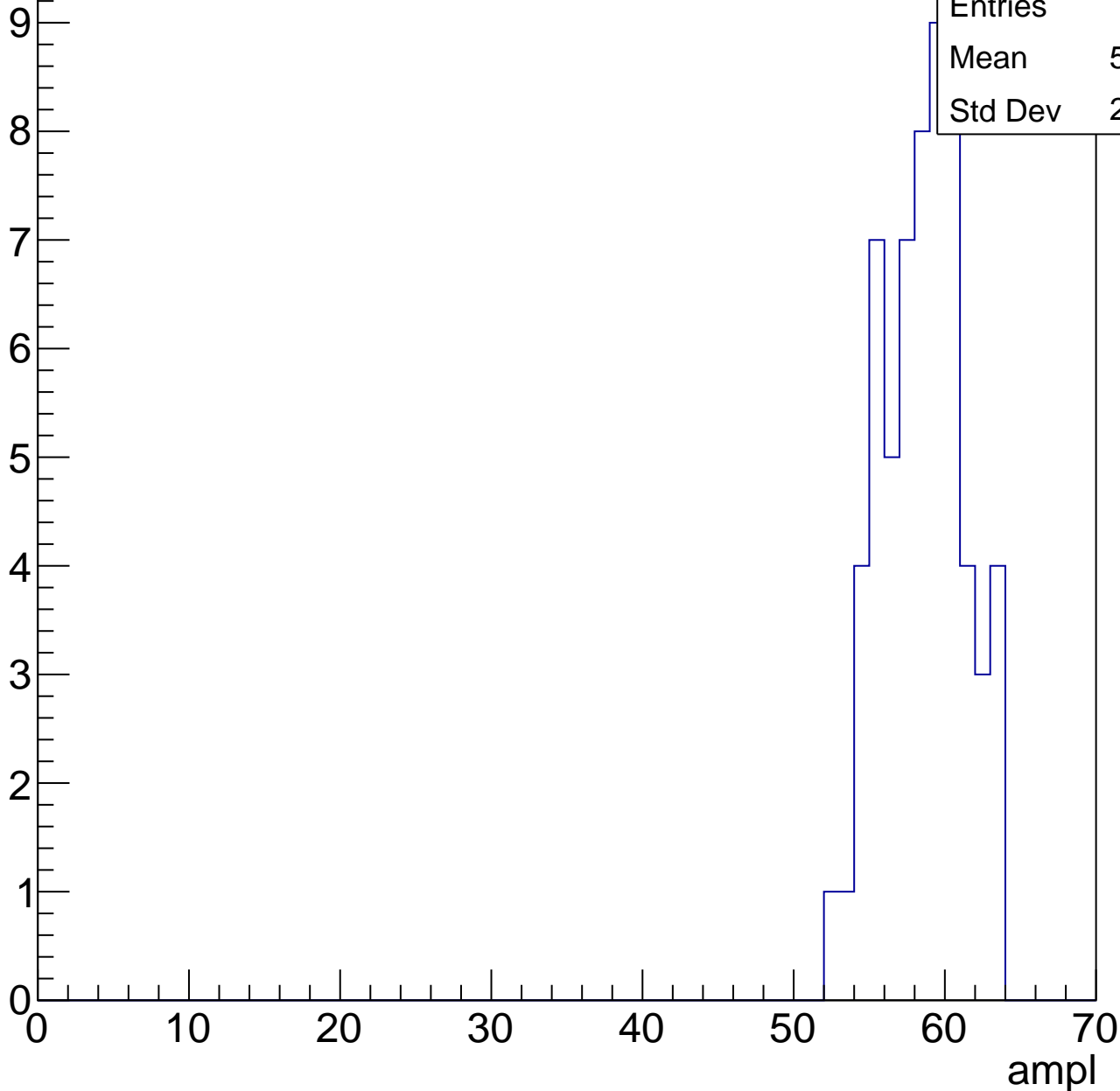


B0L002S, U9-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	58.07
Std Dev	2.685

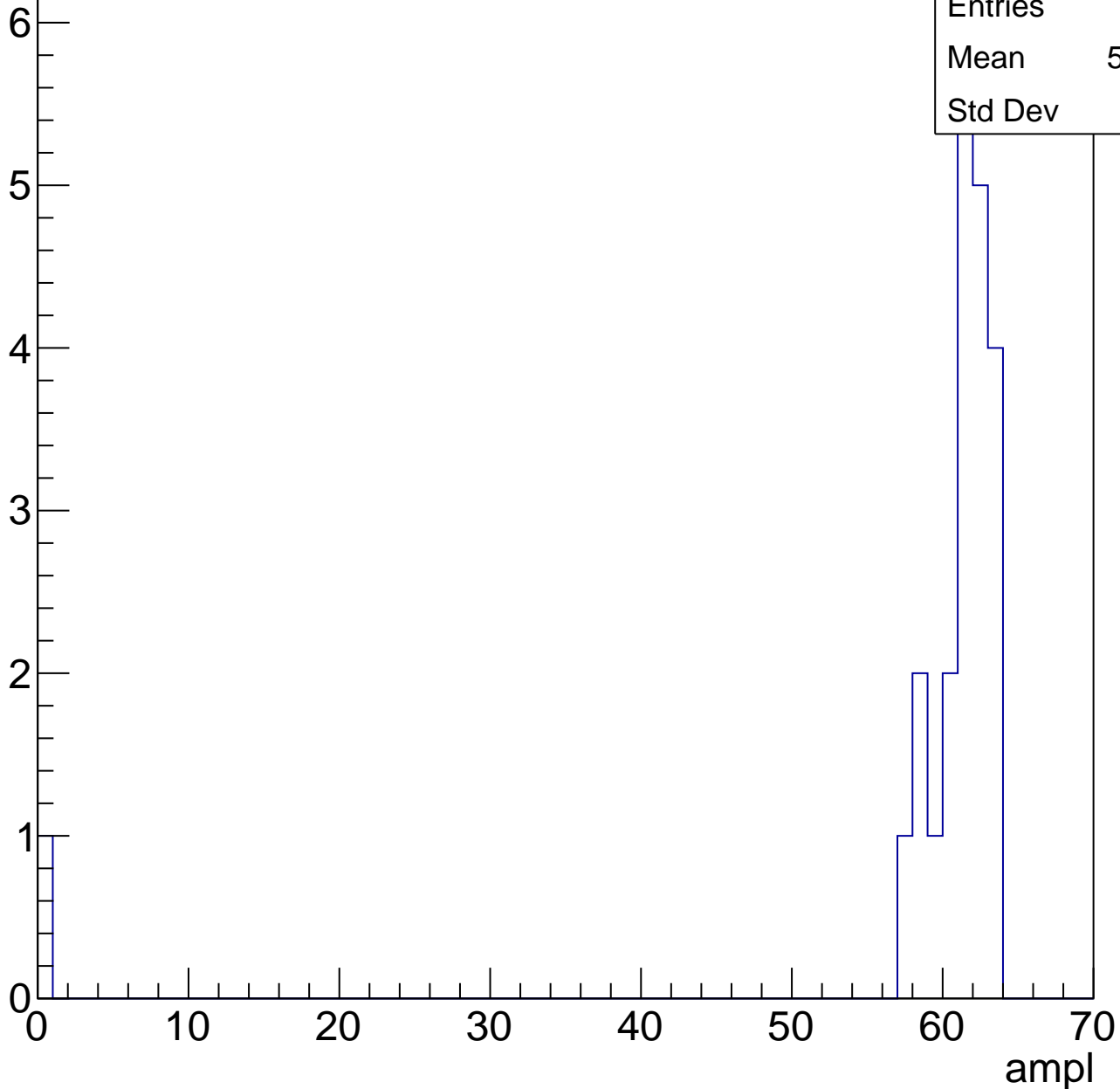


B0L002S, U9-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.18
Std Dev	12.8



B0L002S, U9-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62.5
Std Dev	0.5

B0L002S, U9-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch38, adc0

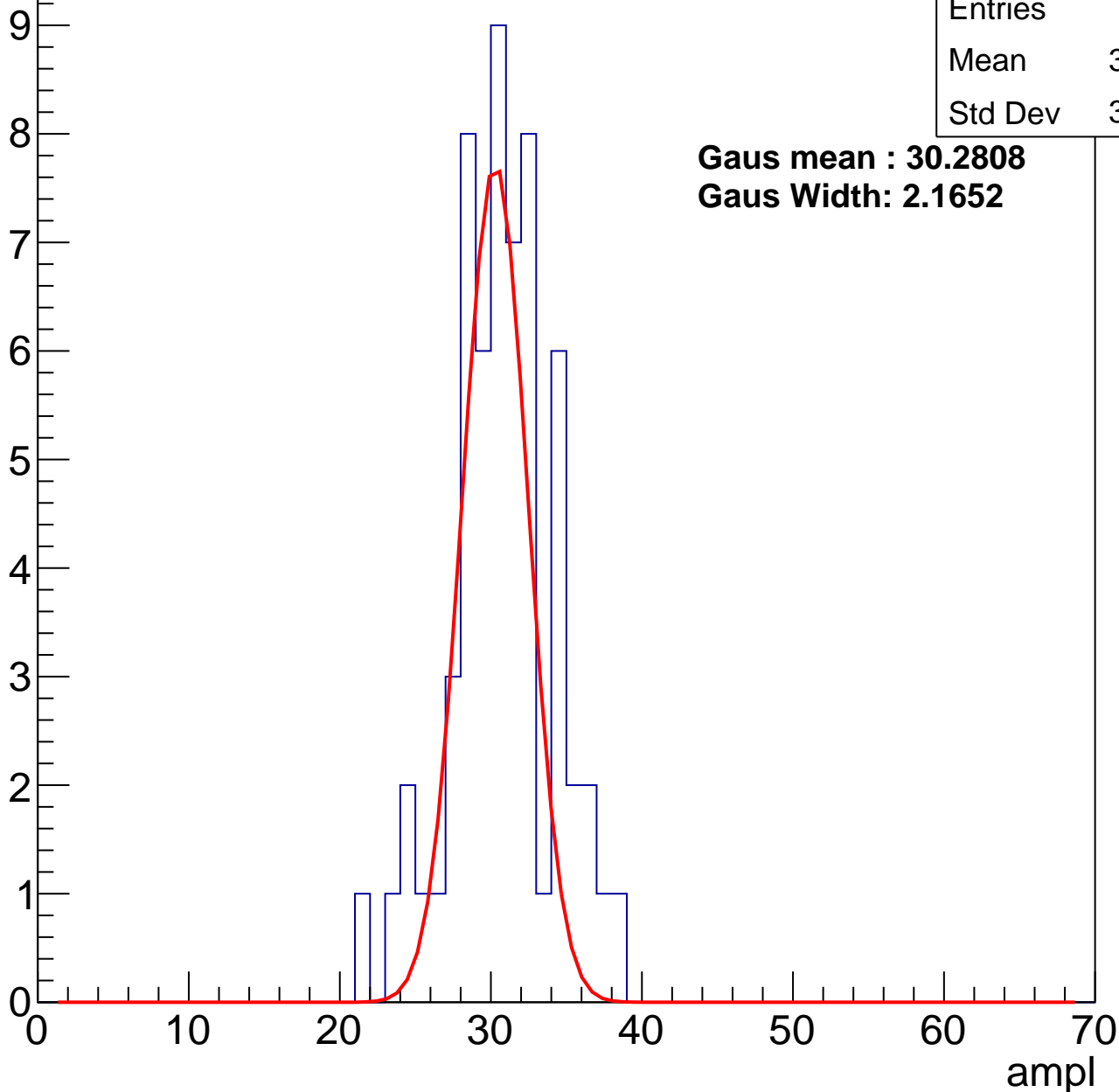
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	30.32
Std Dev	3.369

Gaus mean : 30.2808

Gaus Width: 2.1652



B0L002S, U9-ch38, adc1

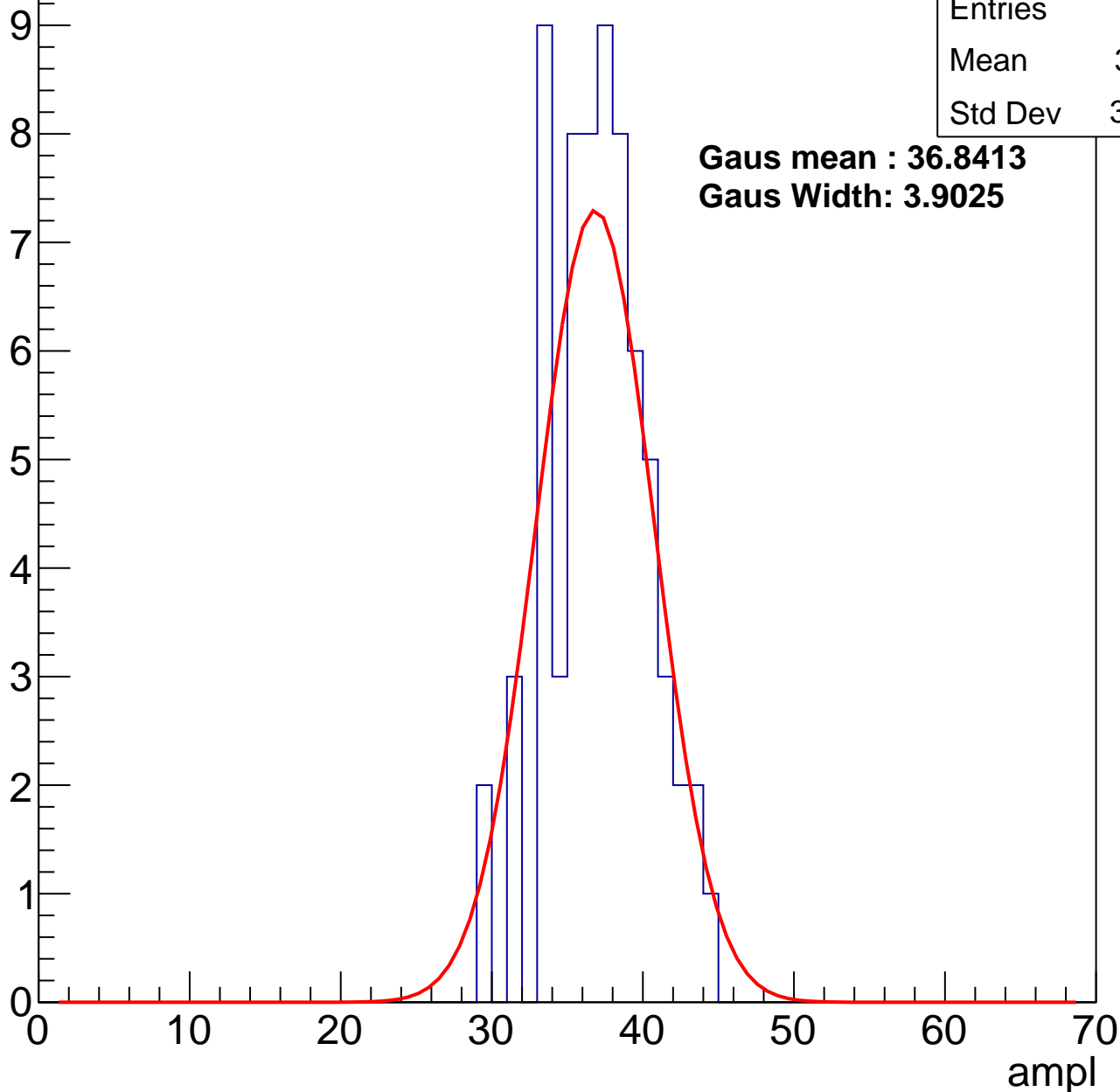
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.61
Std Dev	3.258

Gaus mean : 36.8413

Gaus Width: 3.9025



B0L002S, U9-ch38, adc2

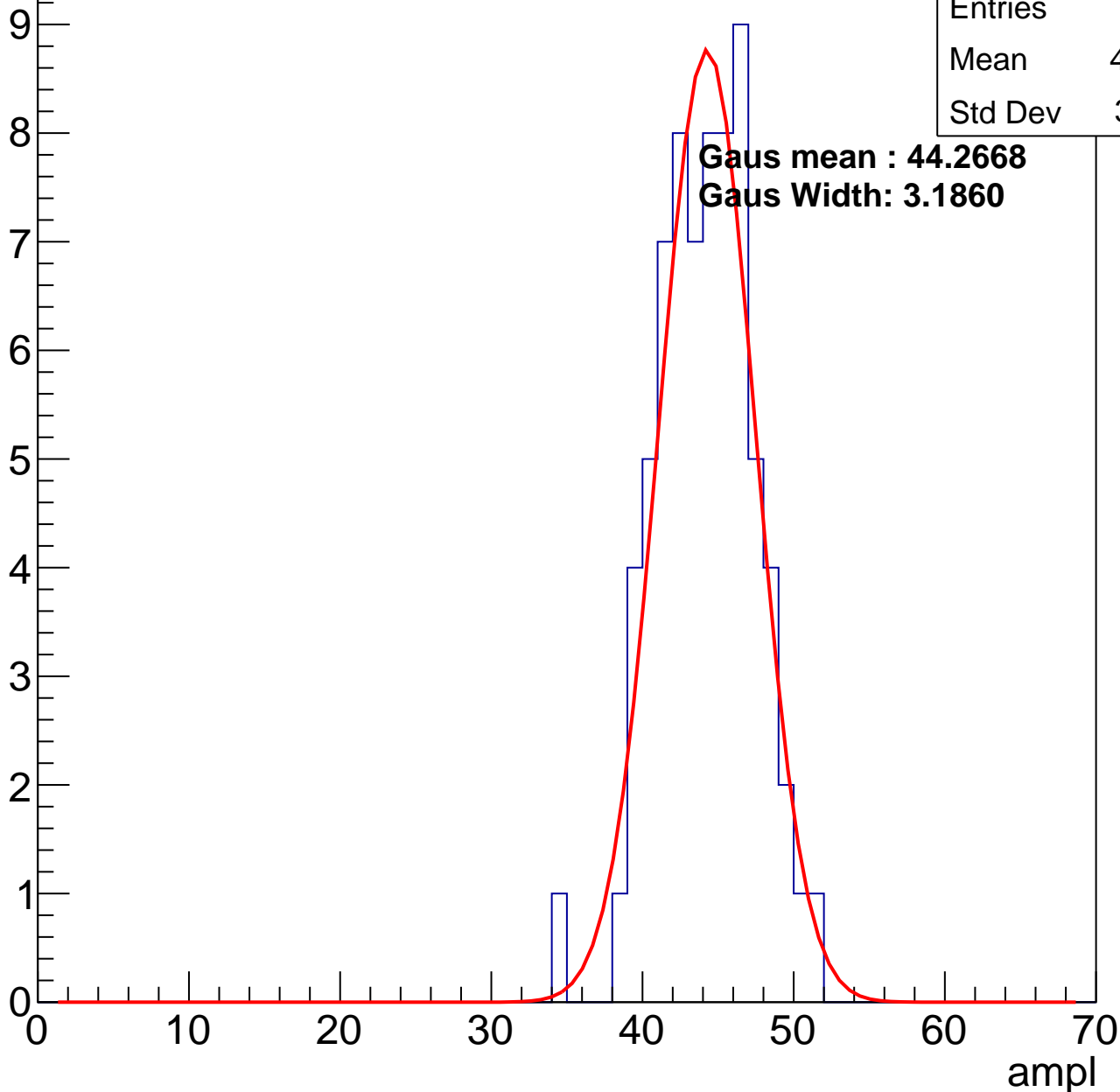
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.72
Std Dev	3.141

Gaus mean : 44.2668

Gaus Width: 3.1860

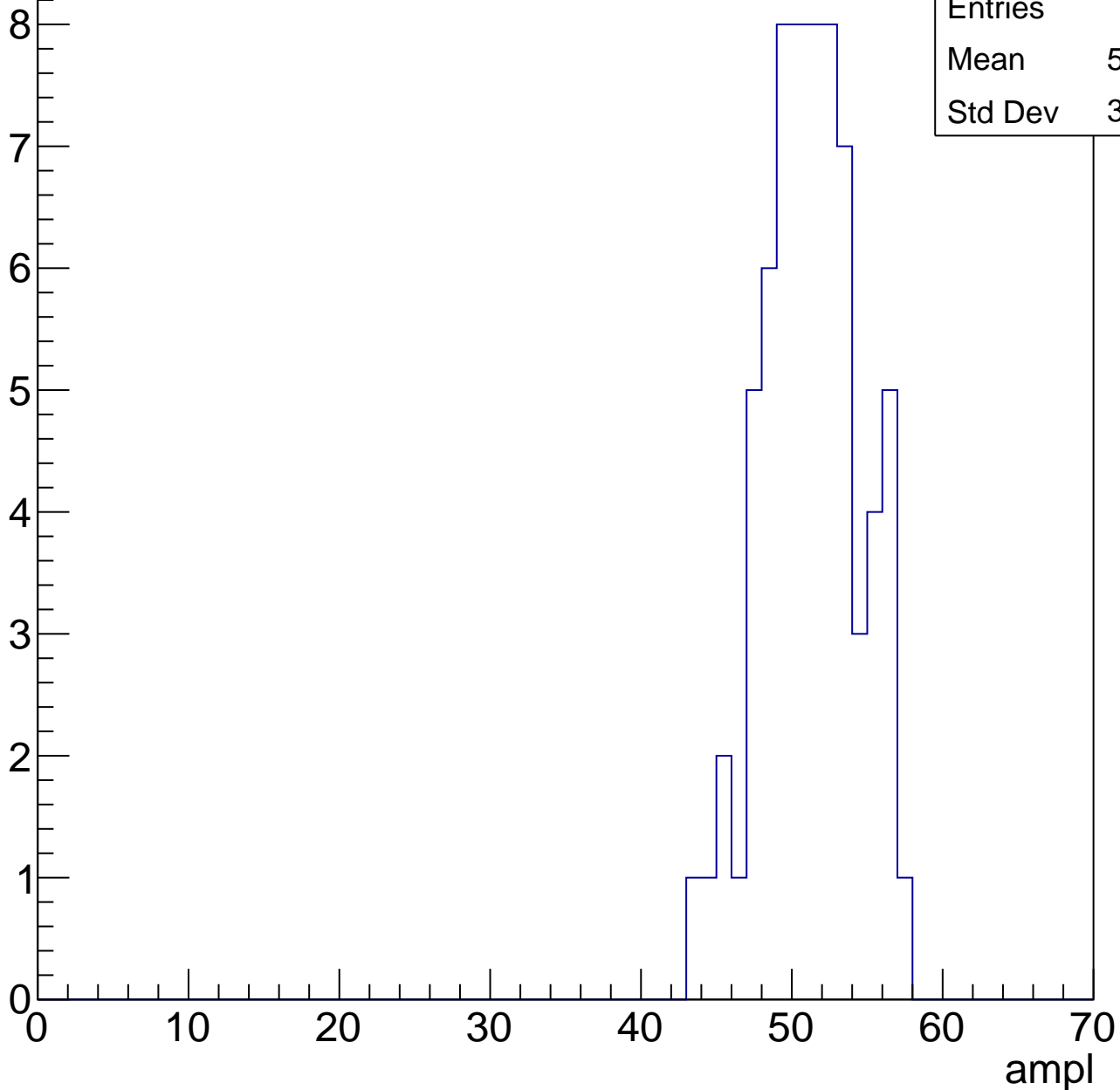


B0L002S, U9-ch38, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.76
Std Dev	3.135

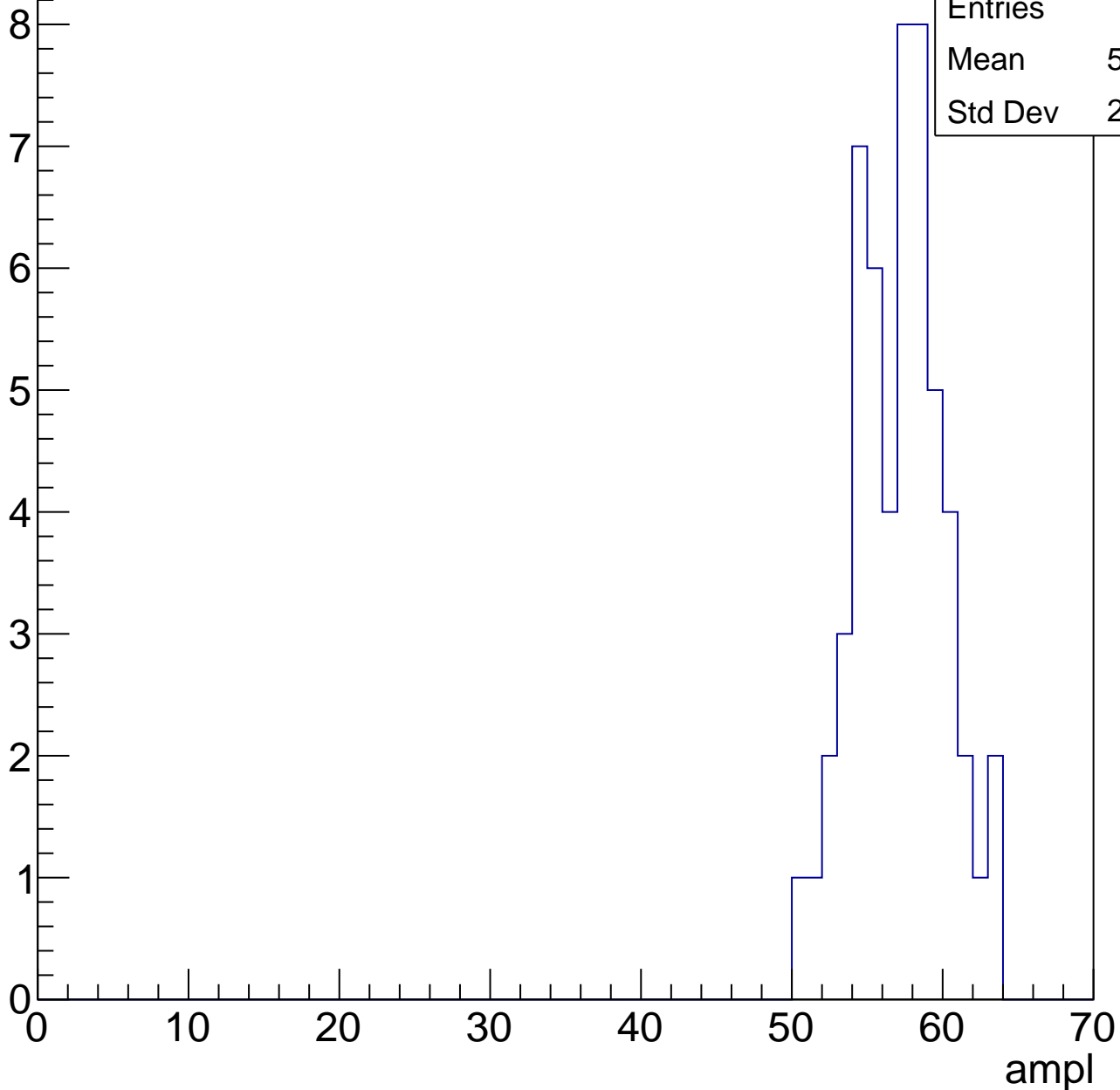


B0L002S, U9-ch38, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	56.69
Std Dev	2.924



B0L002S, U9-ch38, adc5

calib_packv5_042523_0143.root, FC#8, port C1

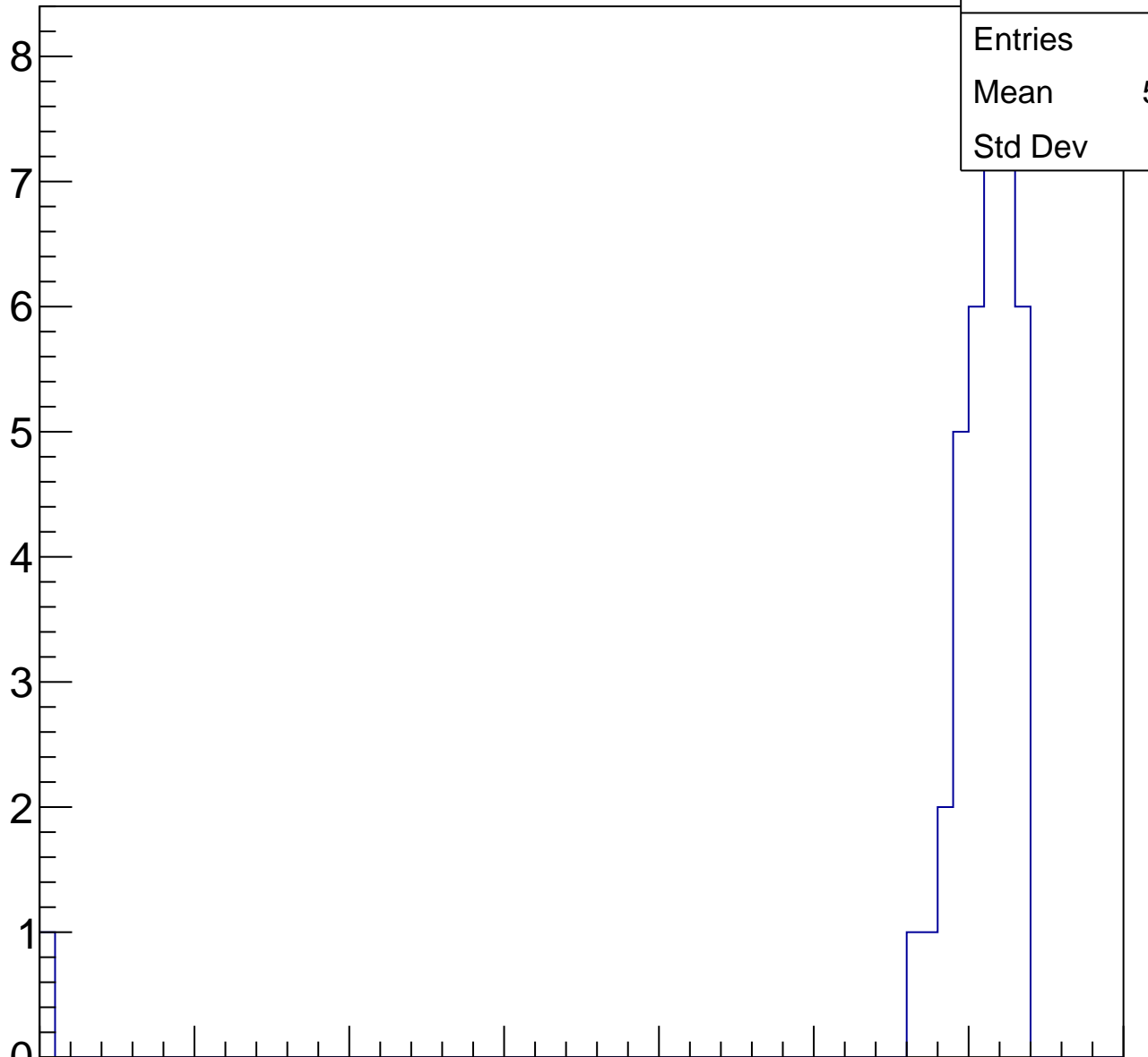
Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	59.11
Std Dev	9.87

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch39, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	29.57
Std Dev	4.94

Gaus mean : 30.2986

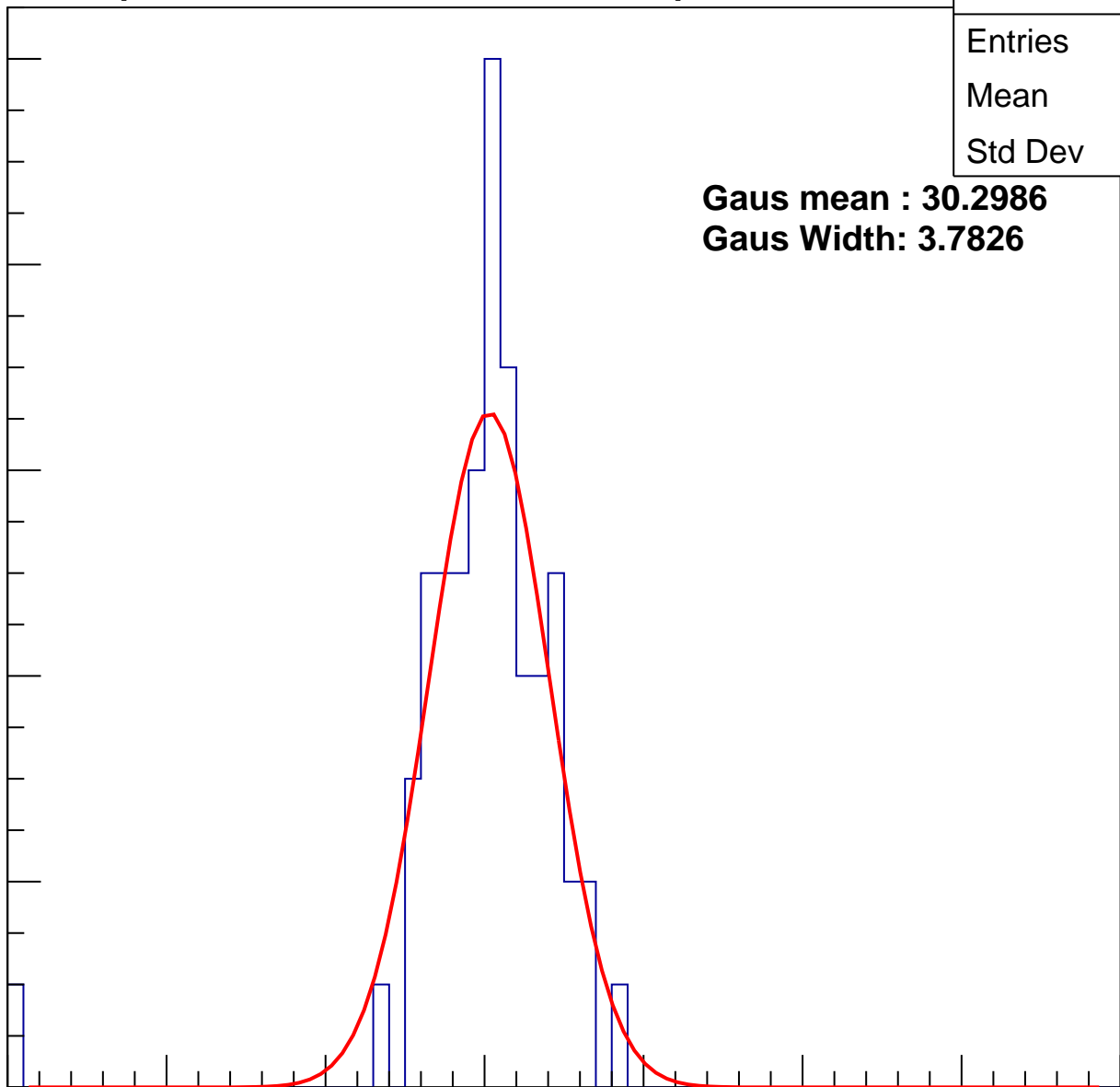
Gaus Width: 3.7826

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch39, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	37.76
Std Dev	3.666

Gaus mean : 38.0478

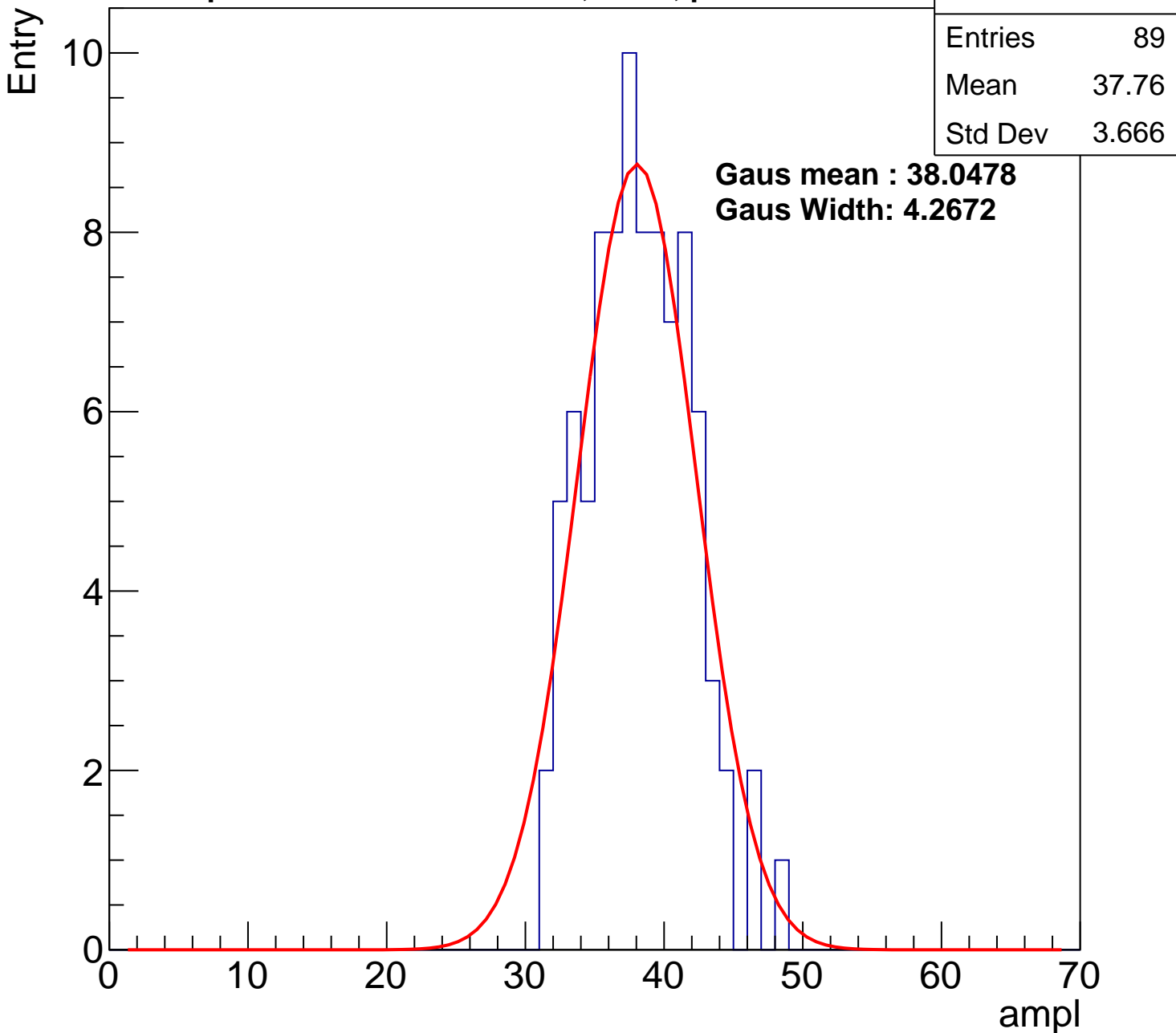
Gaus Width: 4.2672

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch39, adc2

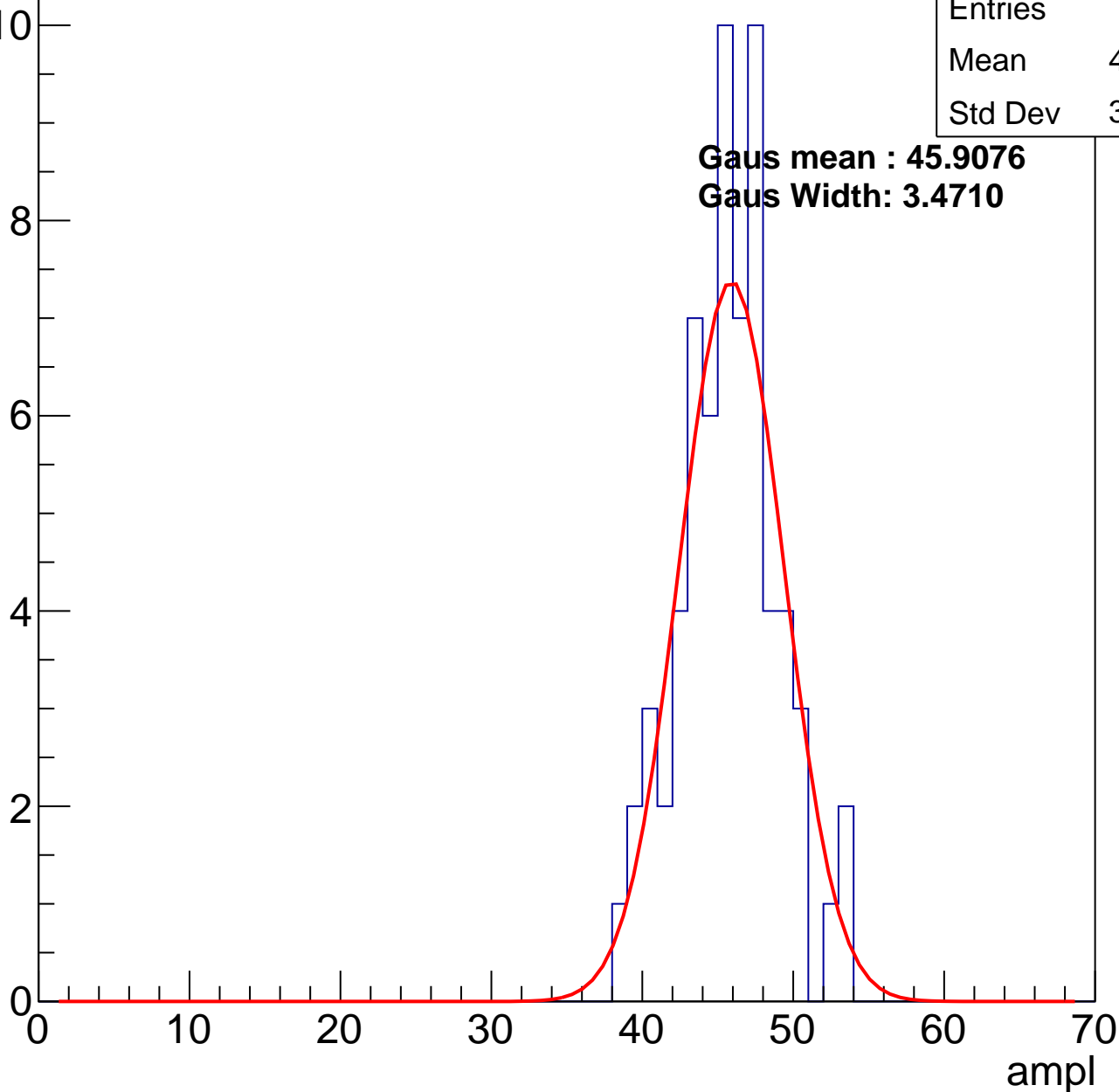
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	45.29
Std Dev	3.237

Gaus mean : 45.9076

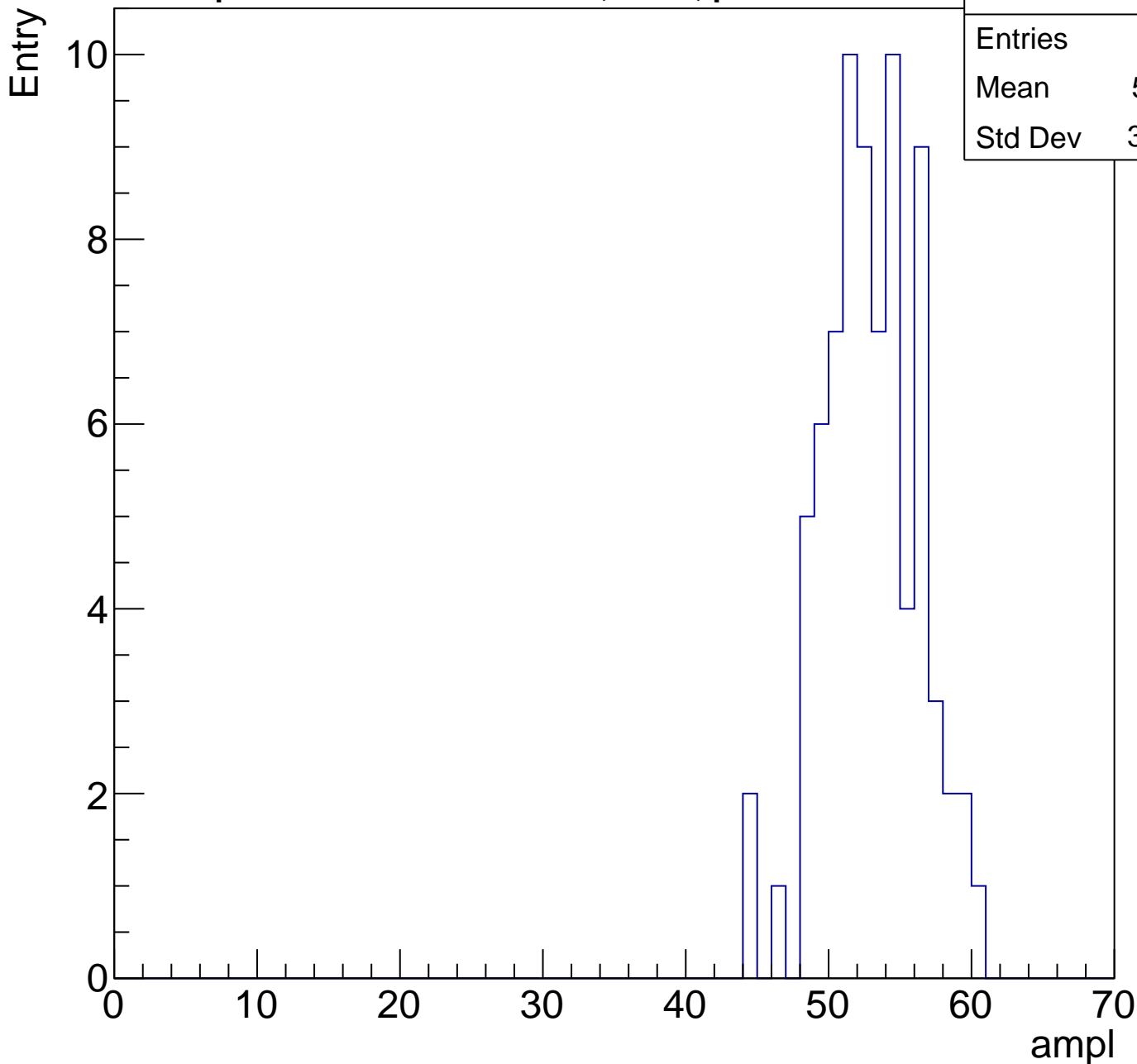
Gaus Width: 3.4710



B0L002S, U9-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	52.51
Std Dev	3.304



B0L002S, U9-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 58.76

Std Dev 2.547

ampl

0

10

20

30

40

50

60

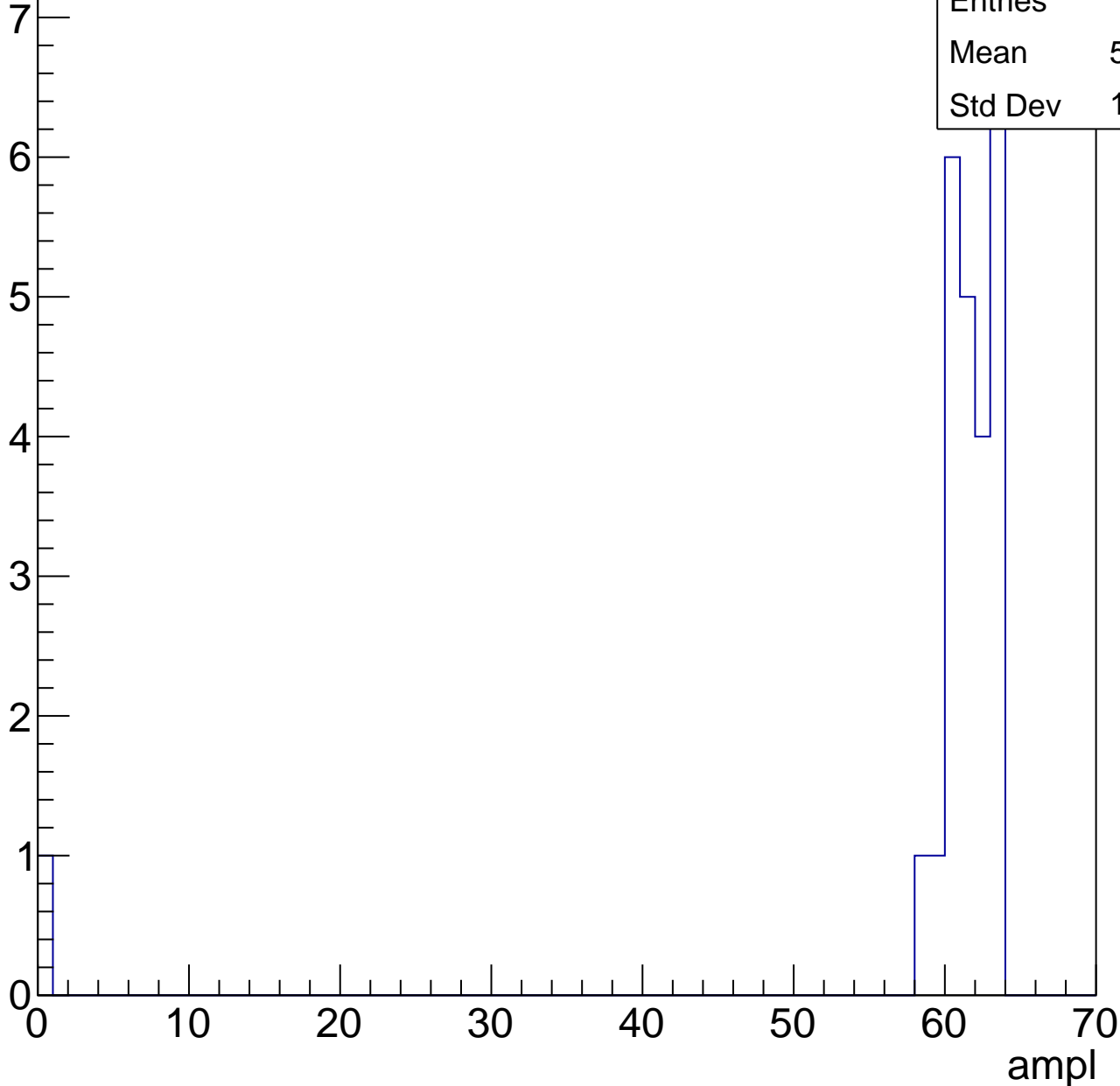
70

B0L002S, U9-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.84
Std Dev	12.09



B0L002S, U9-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch40, adc0

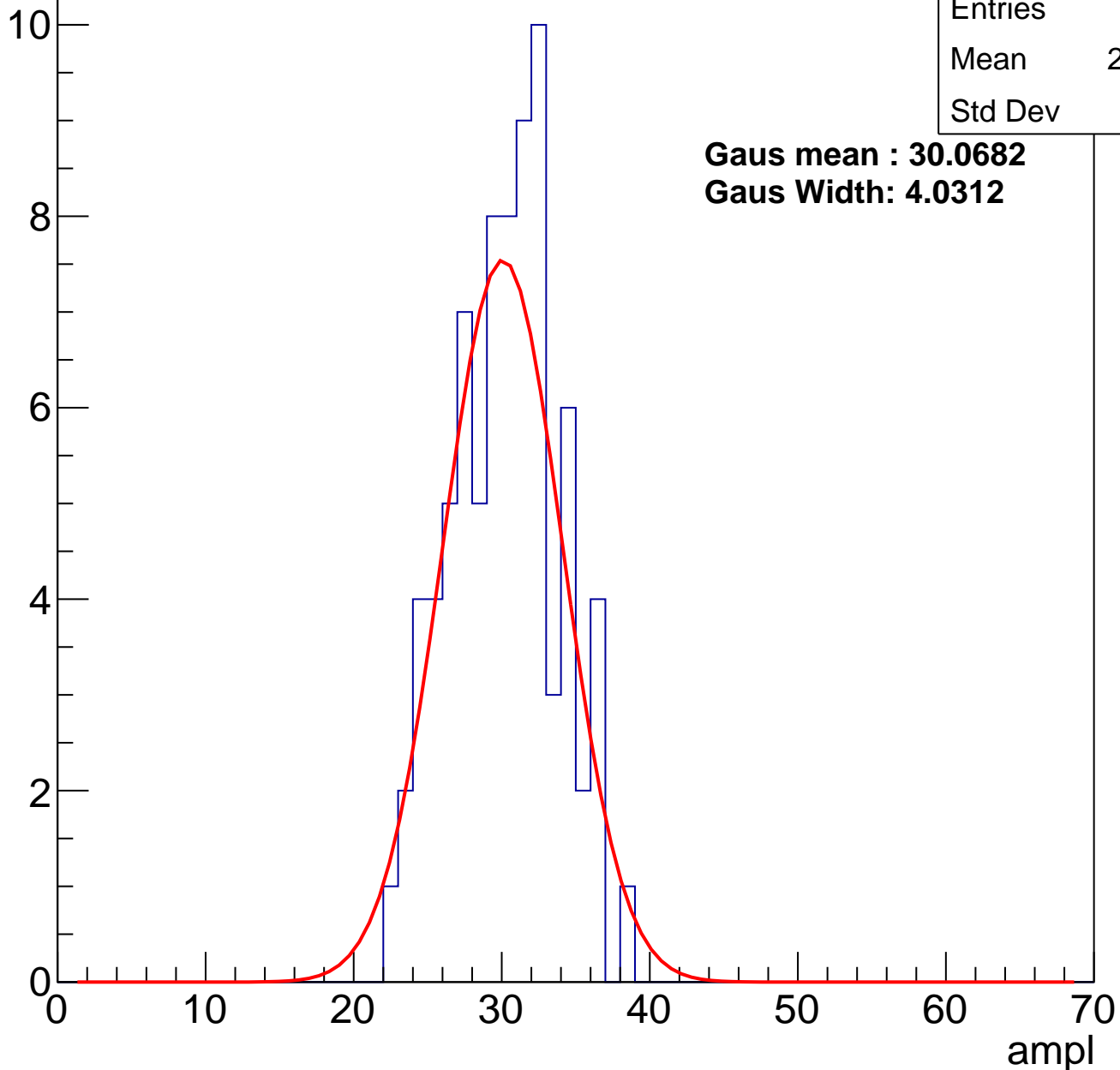
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	29.73
Std Dev	3.56

Gaus mean : 30.0682

Gaus Width: 4.0312

Entry



B0L002S, U9-ch40, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	37.44
Std Dev	3.171

Gaus mean : 37.9676

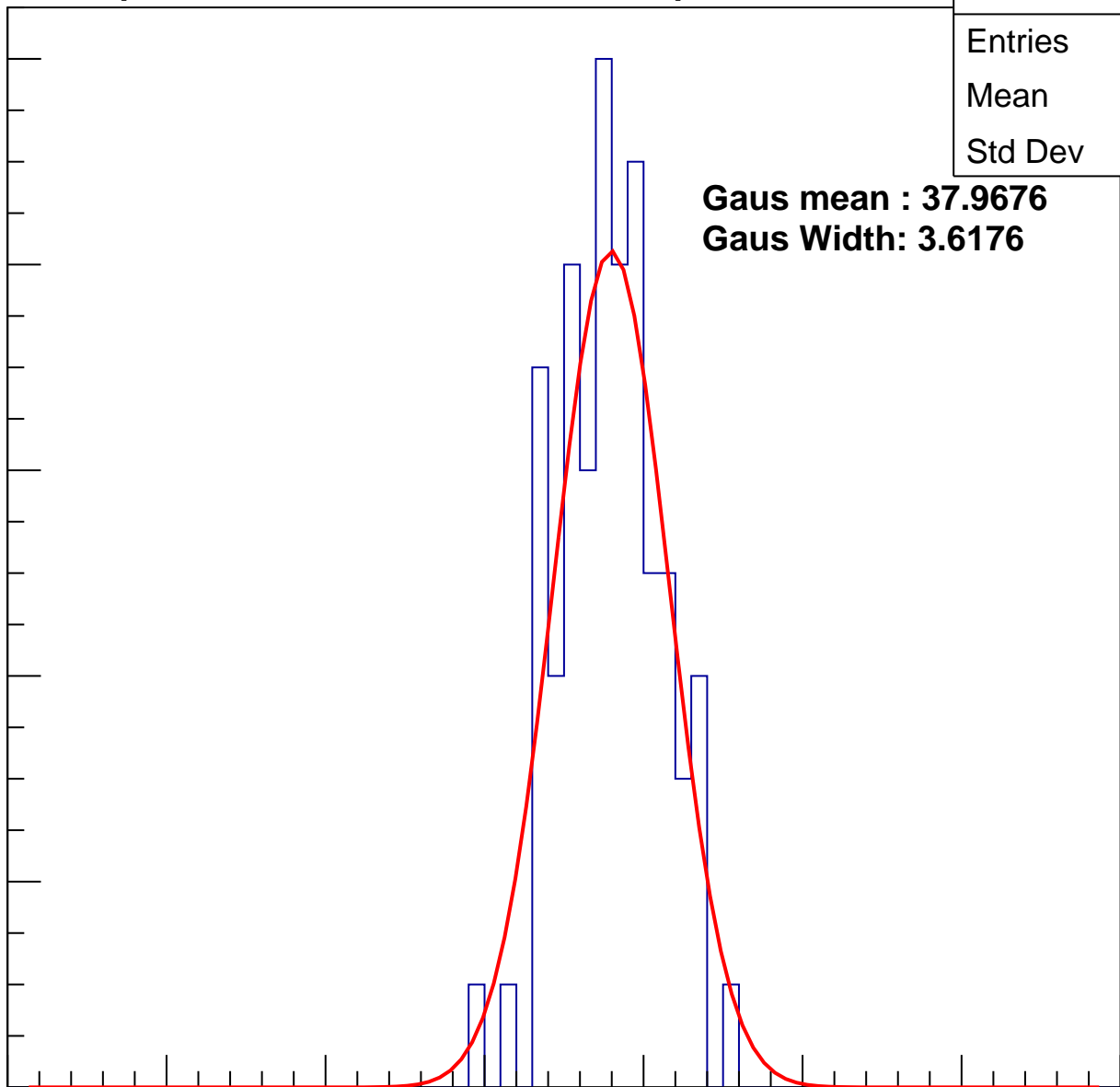
Gaus Width: 3.6176

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch40, adc2

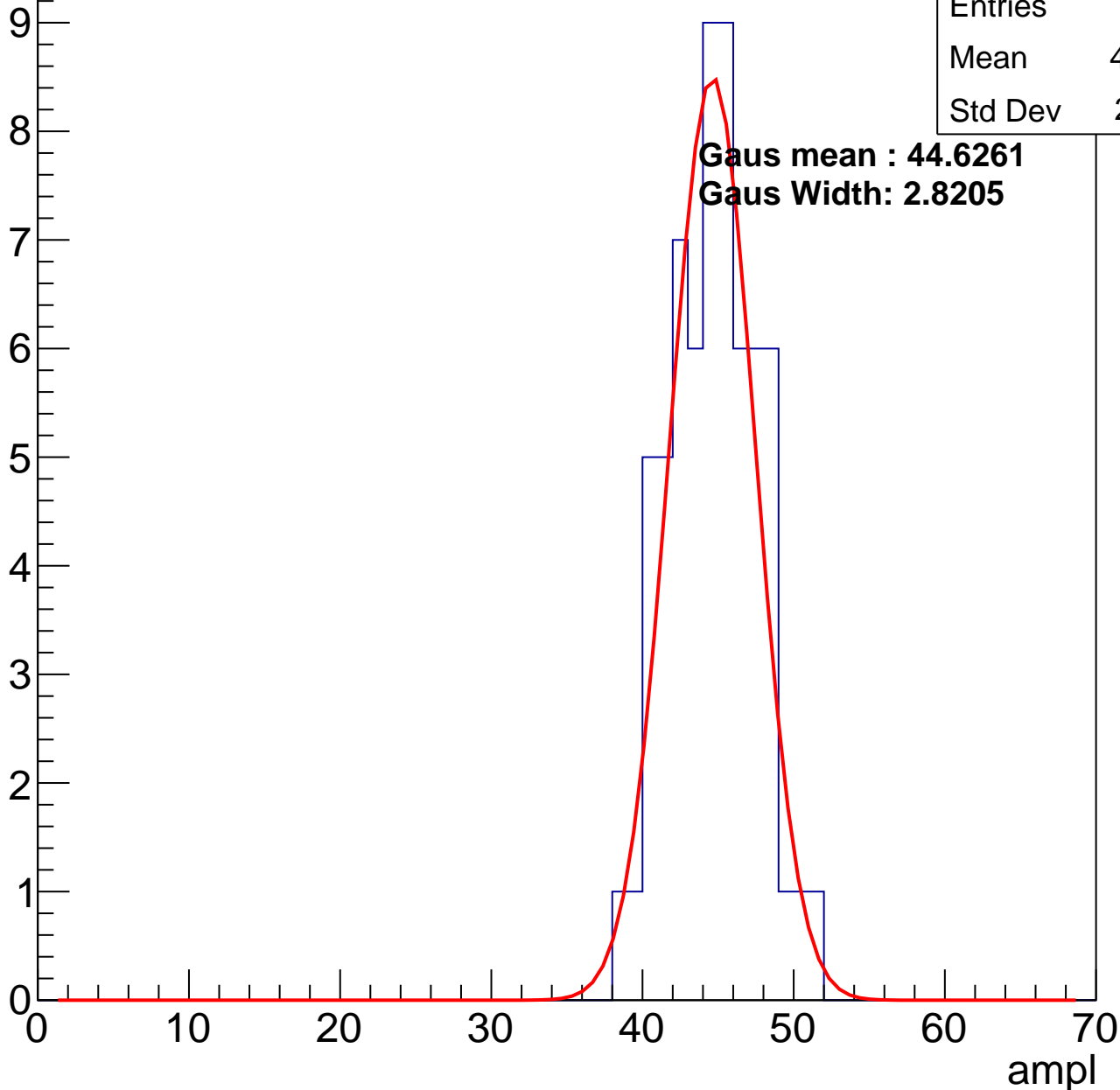
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.23
Std Dev	2.821

Gaus mean : 44.6261

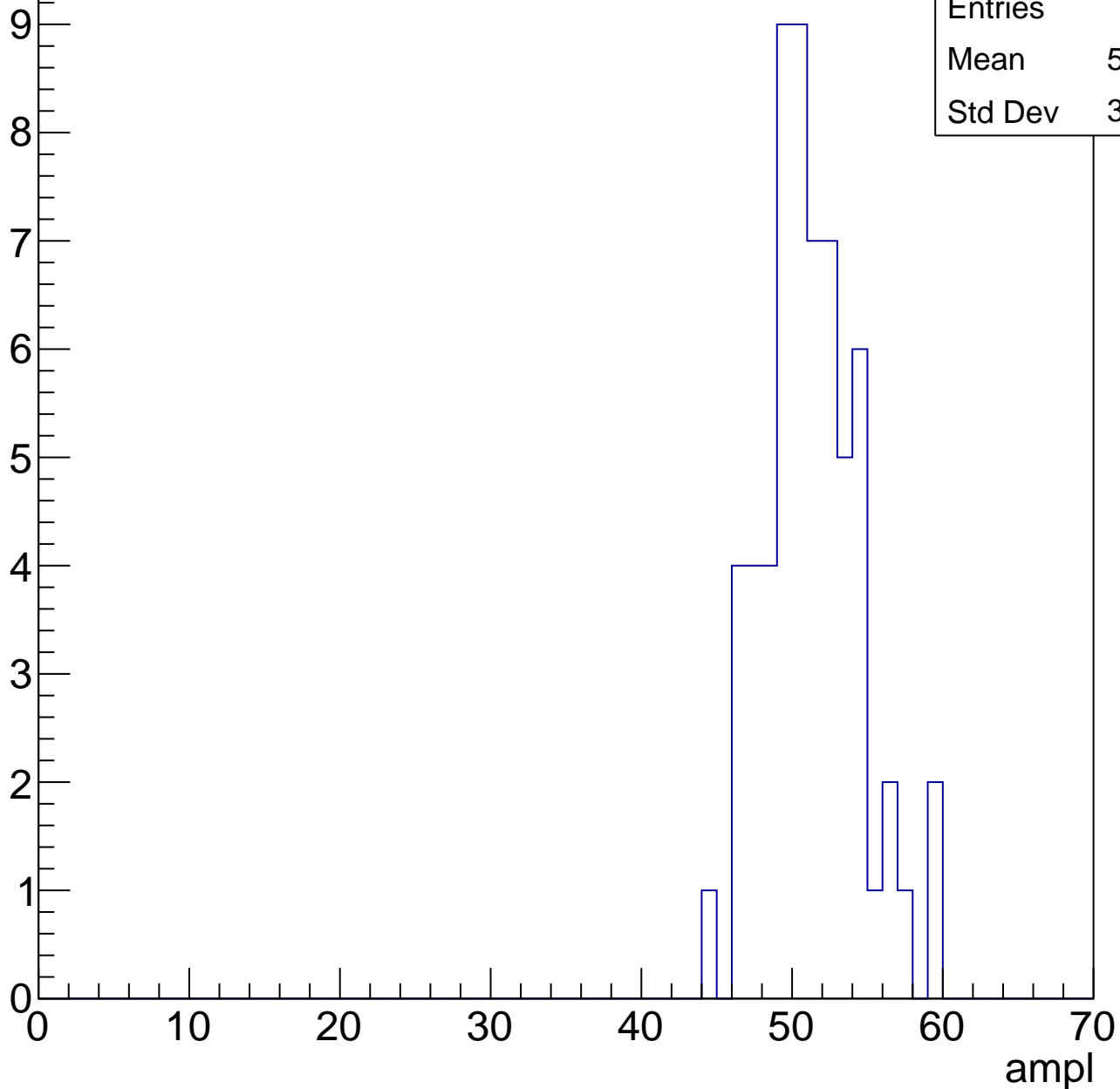
Gaus Width: 2.8205



B0L002S, U9-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

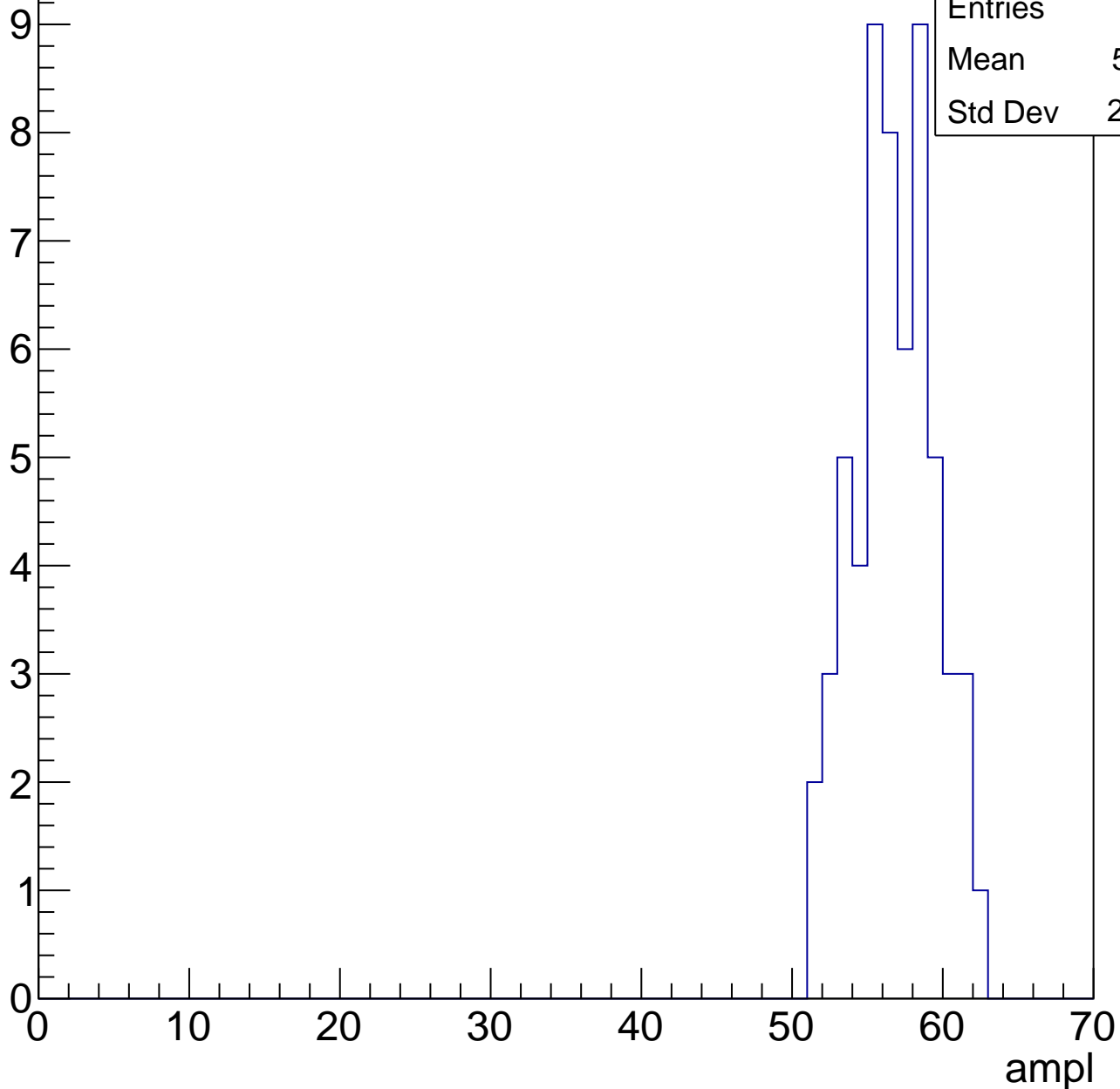


Entries	62
Mean	50.82
Std Dev	3.119

B0L002S, U9-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



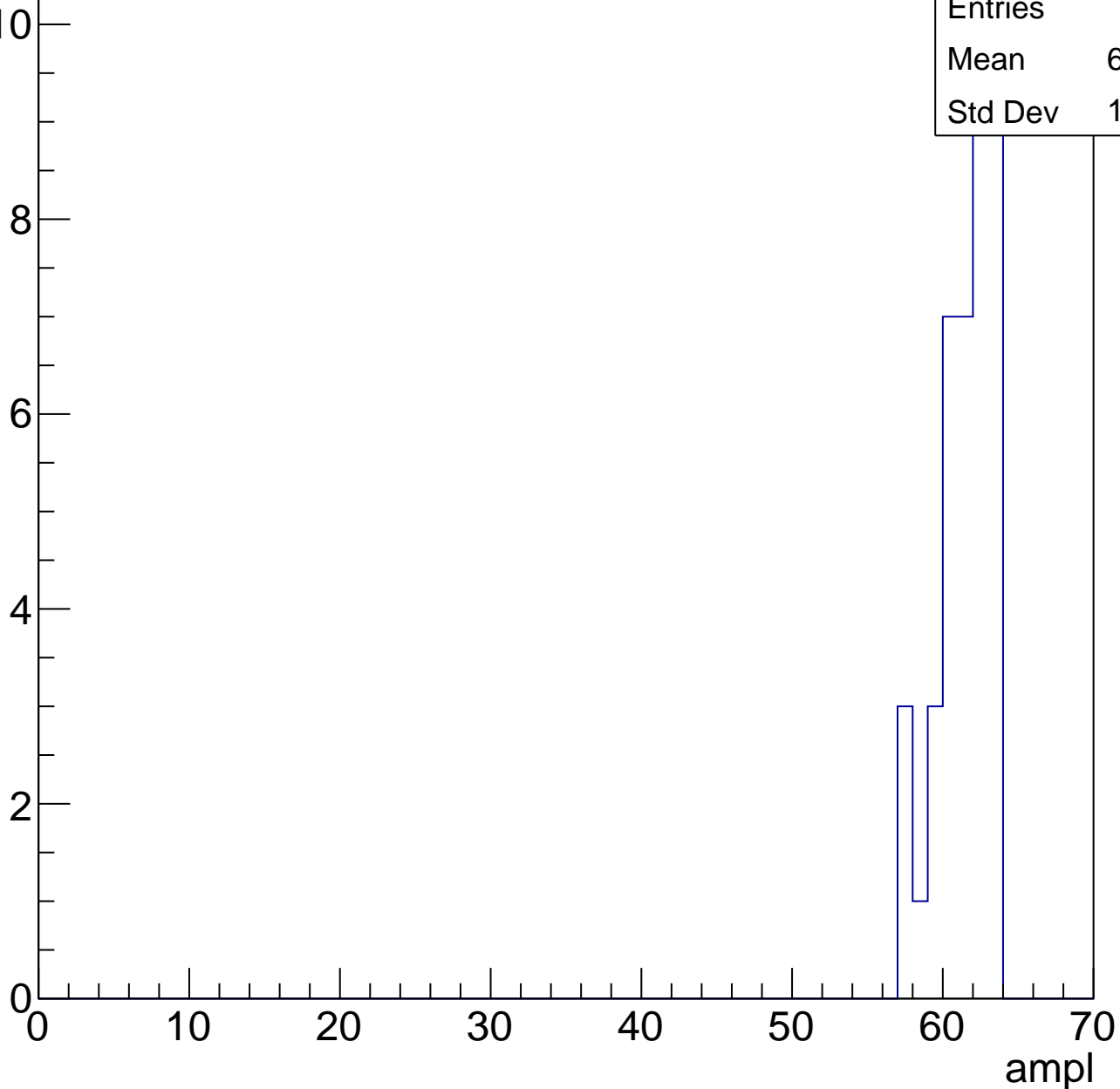
Entries	58
Mean	56.31
Std Dev	2.654

B0L002S, U9-ch40, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

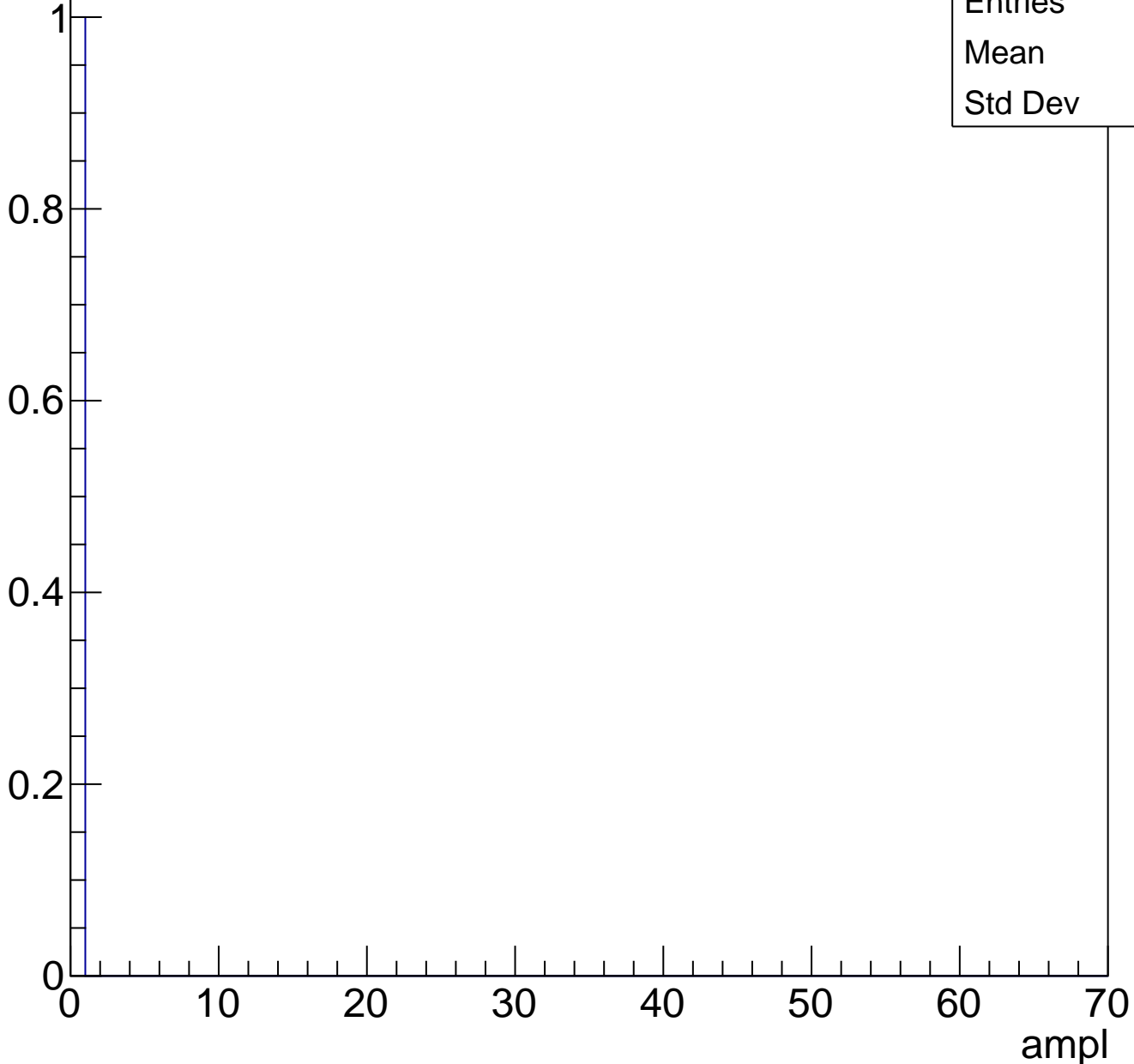
Entries	40
Mean	61.02
Std Dev	1.768



B0L002S, U9-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L002S, U9-ch41, adc0

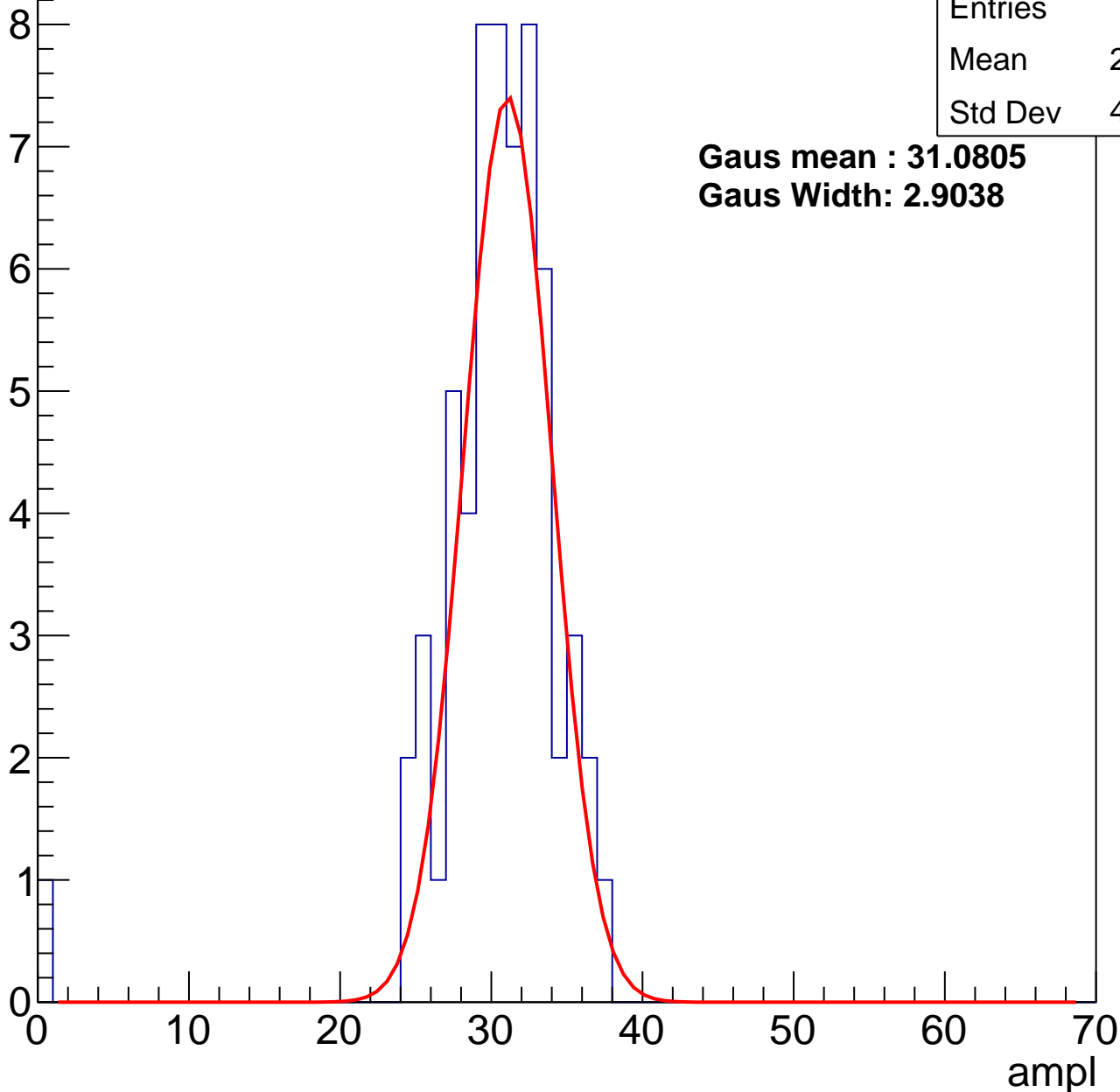
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.85
Std Dev	4.878

Gaus mean : 31.0805

Gaus Width: 2.9038



B0L002S, U9-ch41, adc1

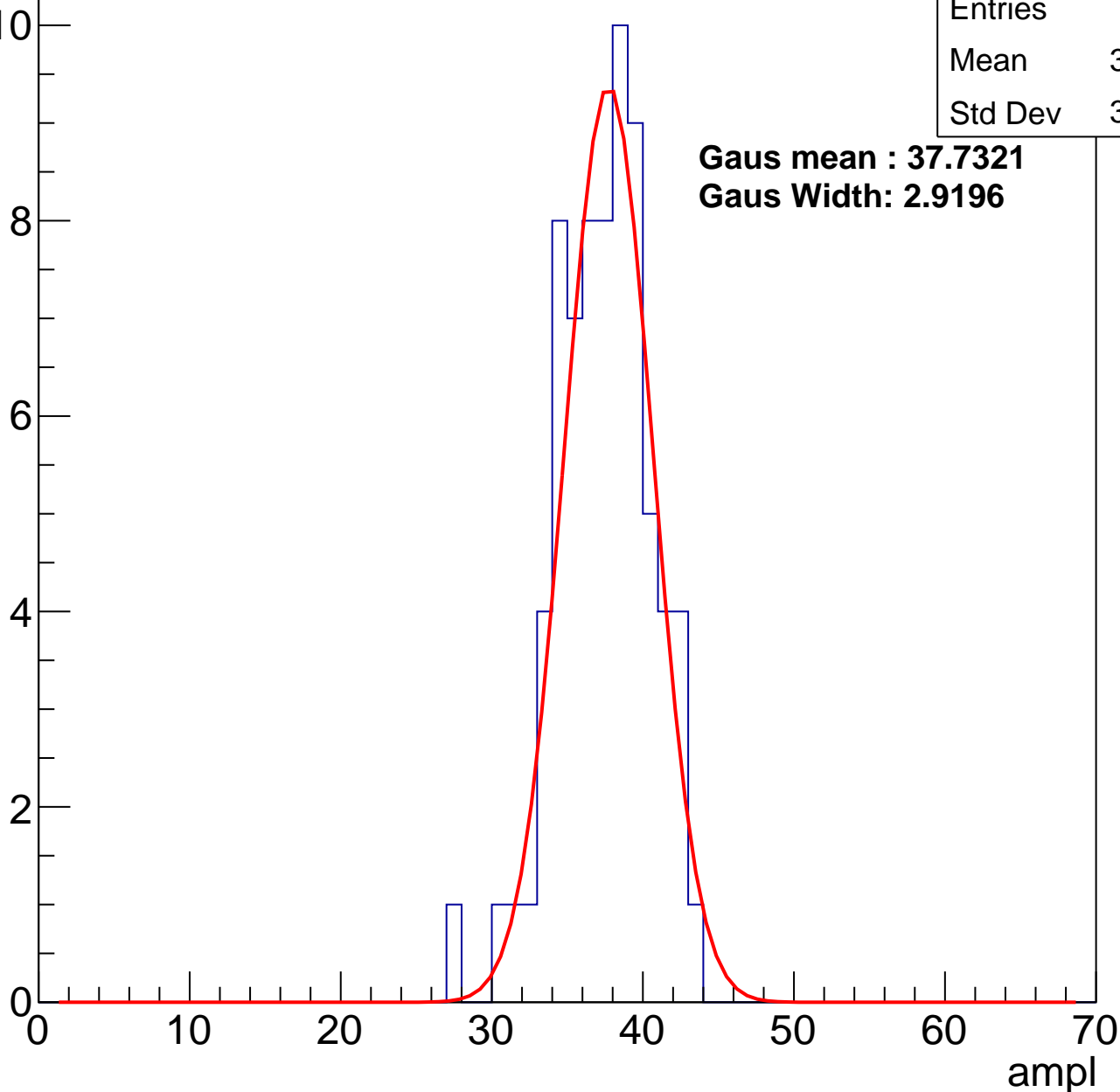
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.93
Std Dev	3.057

Gaus mean : 37.7321

Gaus Width: 2.9196



B0L002S, U9-ch41, adc2

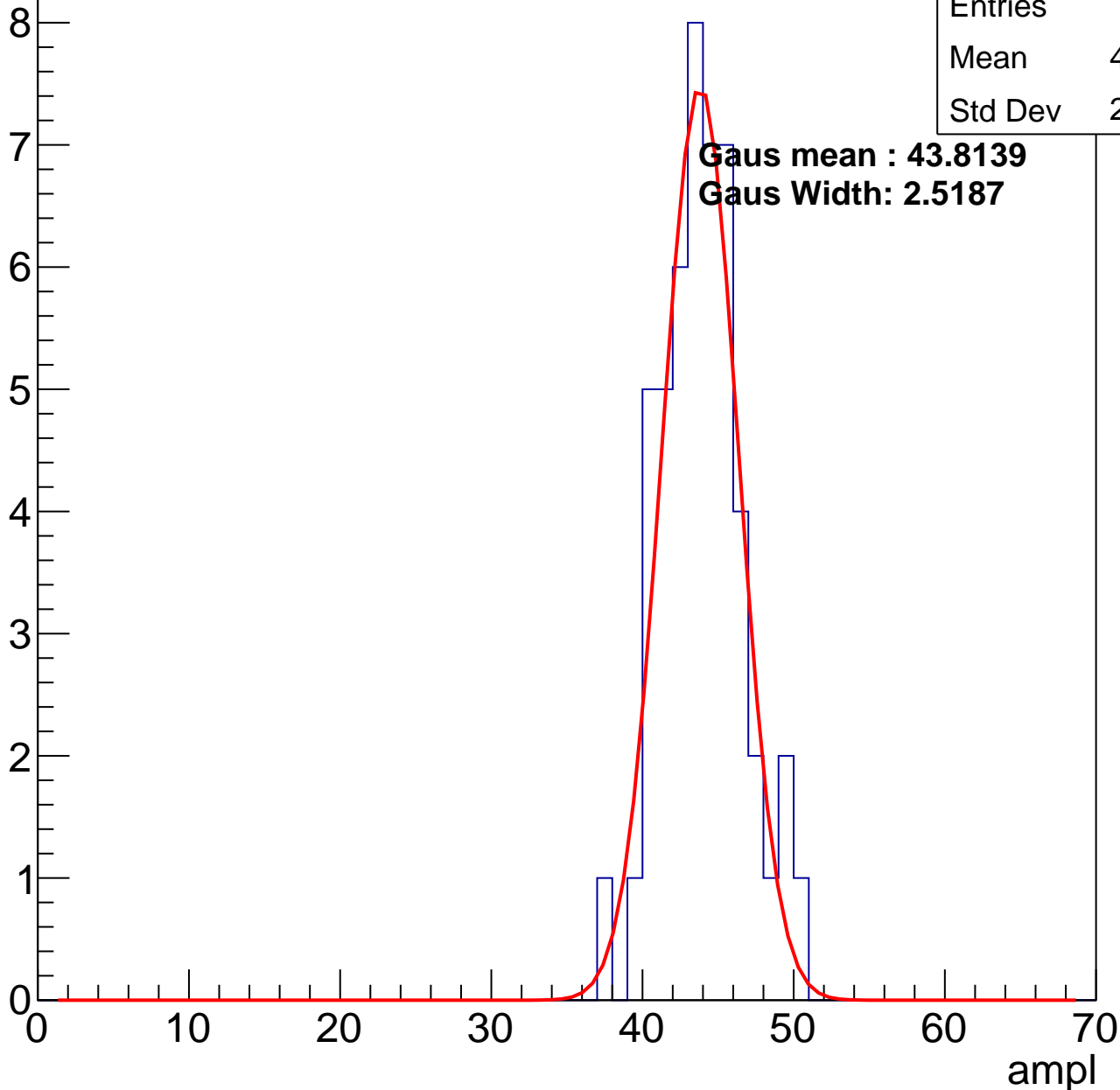
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	43.48
Std Dev	2.685

Gaus mean : 43.8139

Gaus Width: 2.5187



B0L002S, U9-ch41, adc3

calib_packv5_042523_0143.root, FC#8, port C1

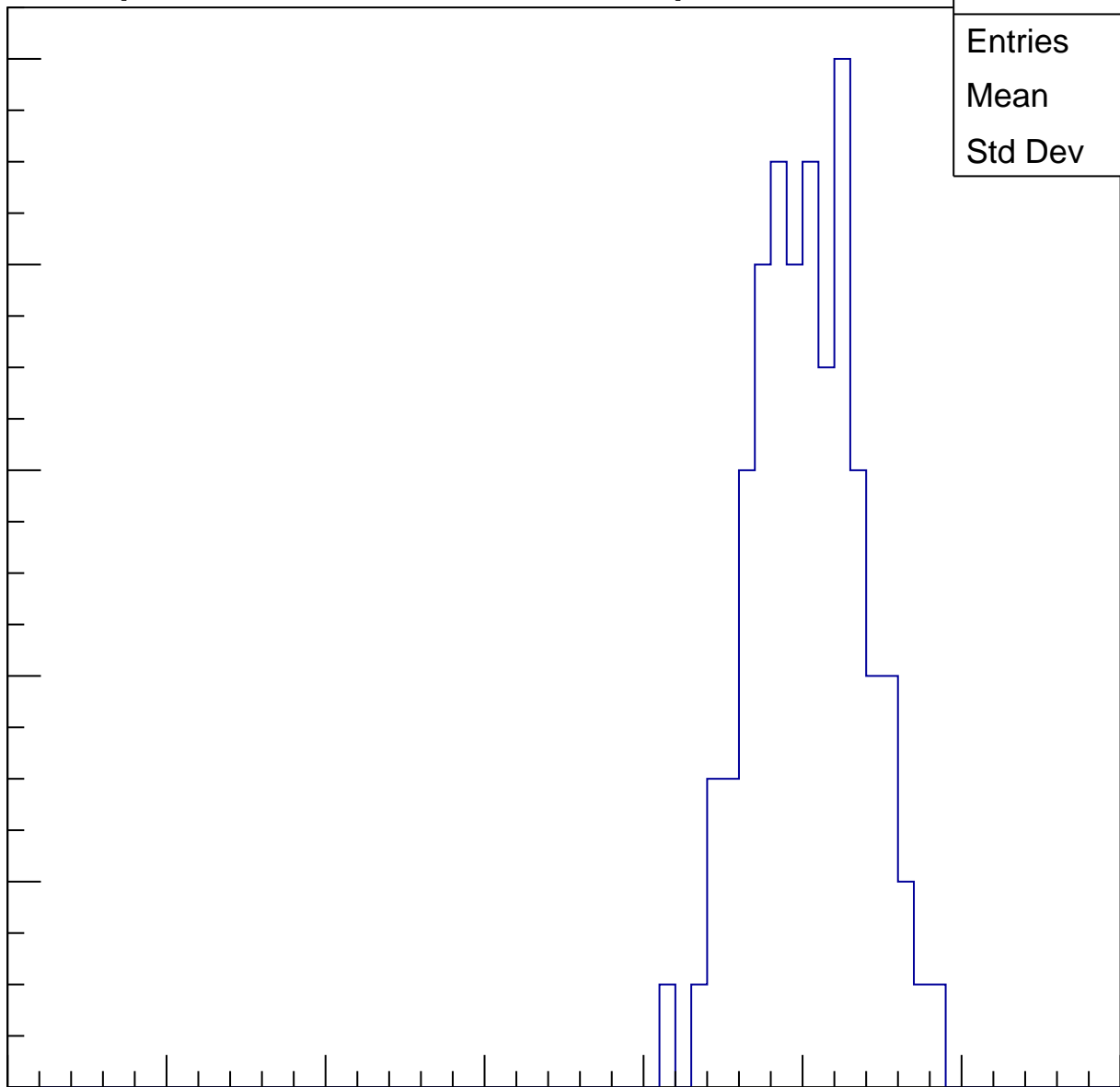
Entries	83
Mean	49.82
Std Dev	3.423

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

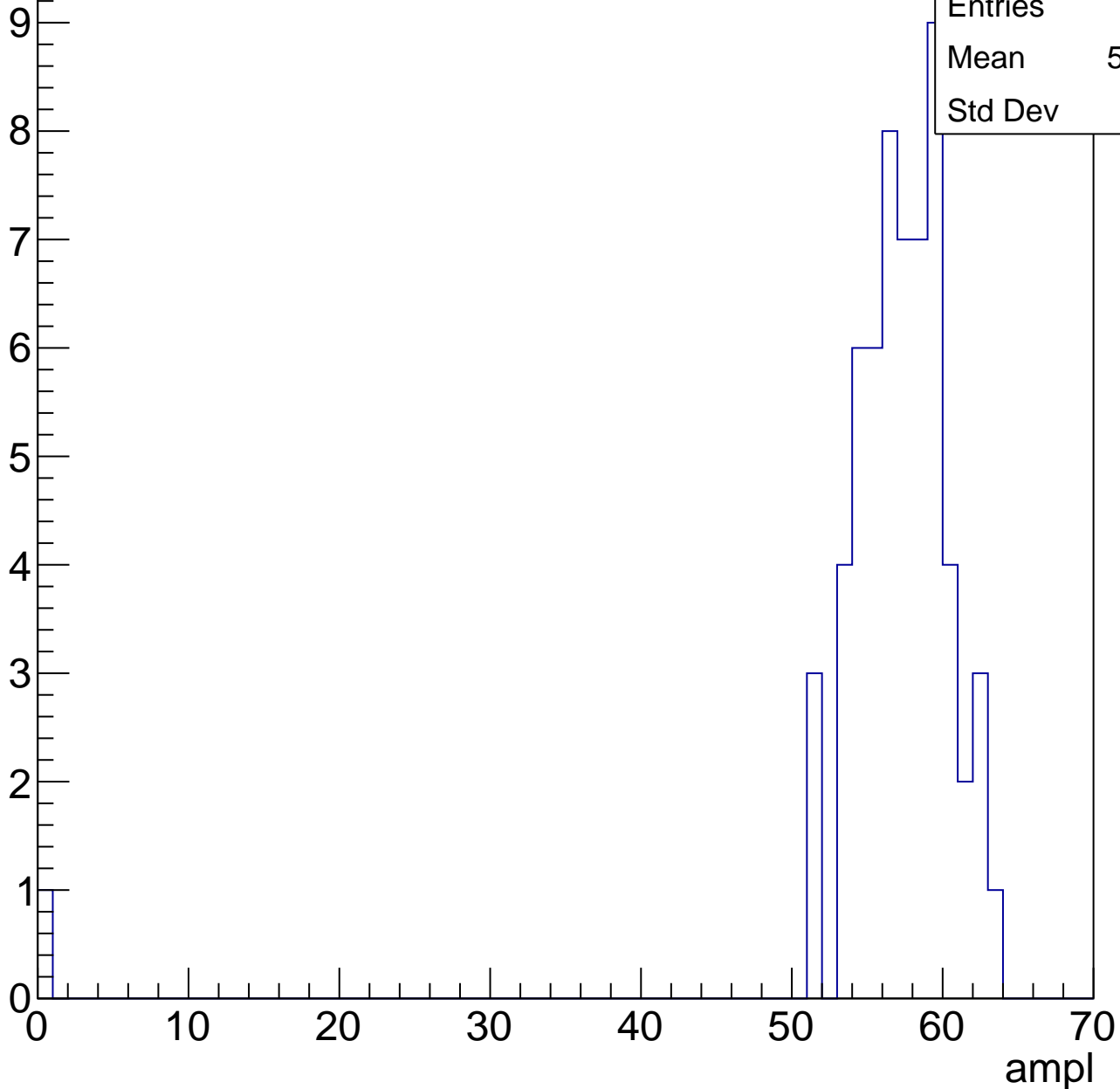


B0L002S, U9-ch41, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	55.97
Std Dev	7.75



B0L002S, U9-ch41, adc5

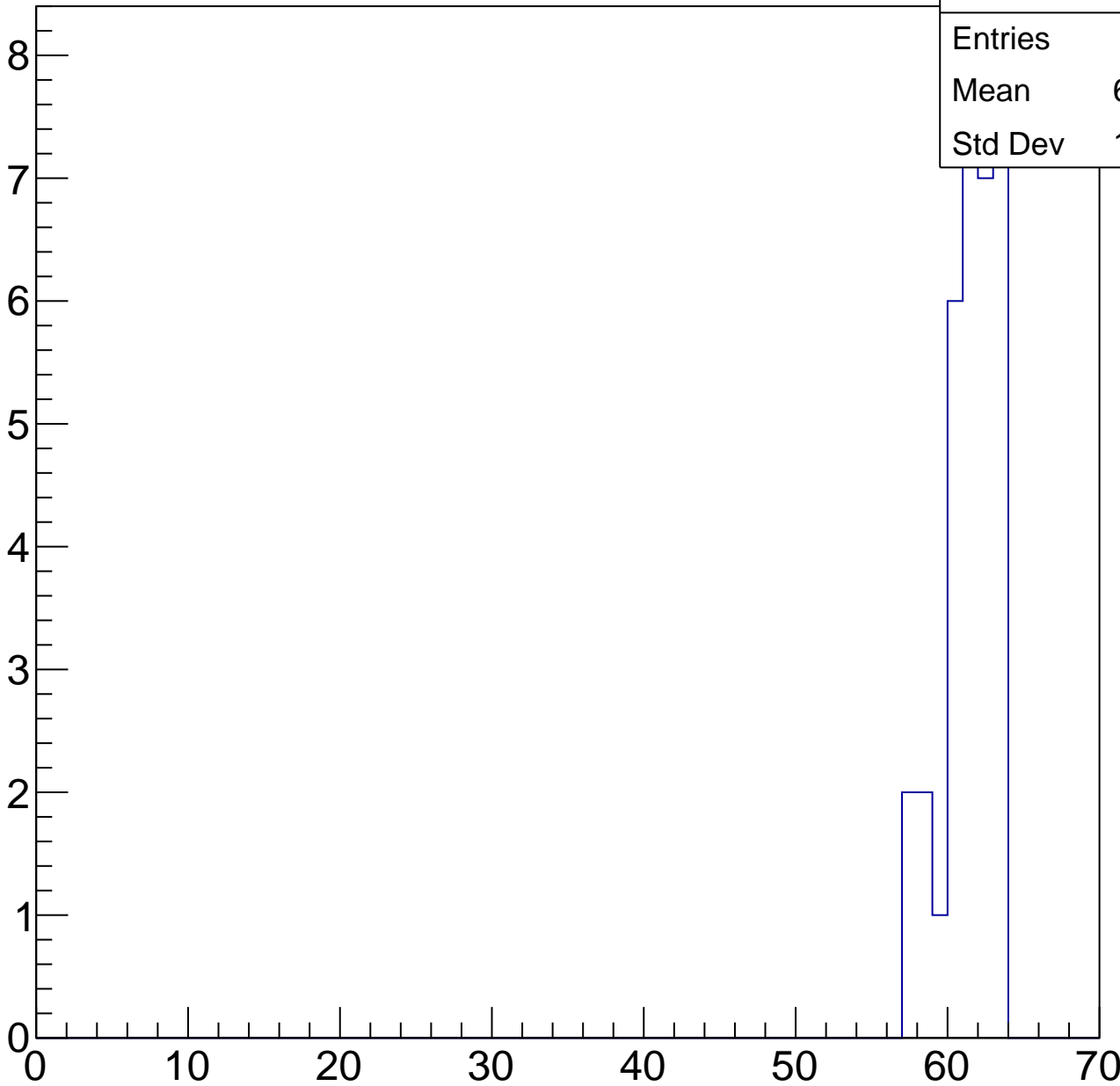
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	34
Mean	61.03
Std Dev	1.706

ampl



B0L002S, U9-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch42, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.95
Std Dev	4.858

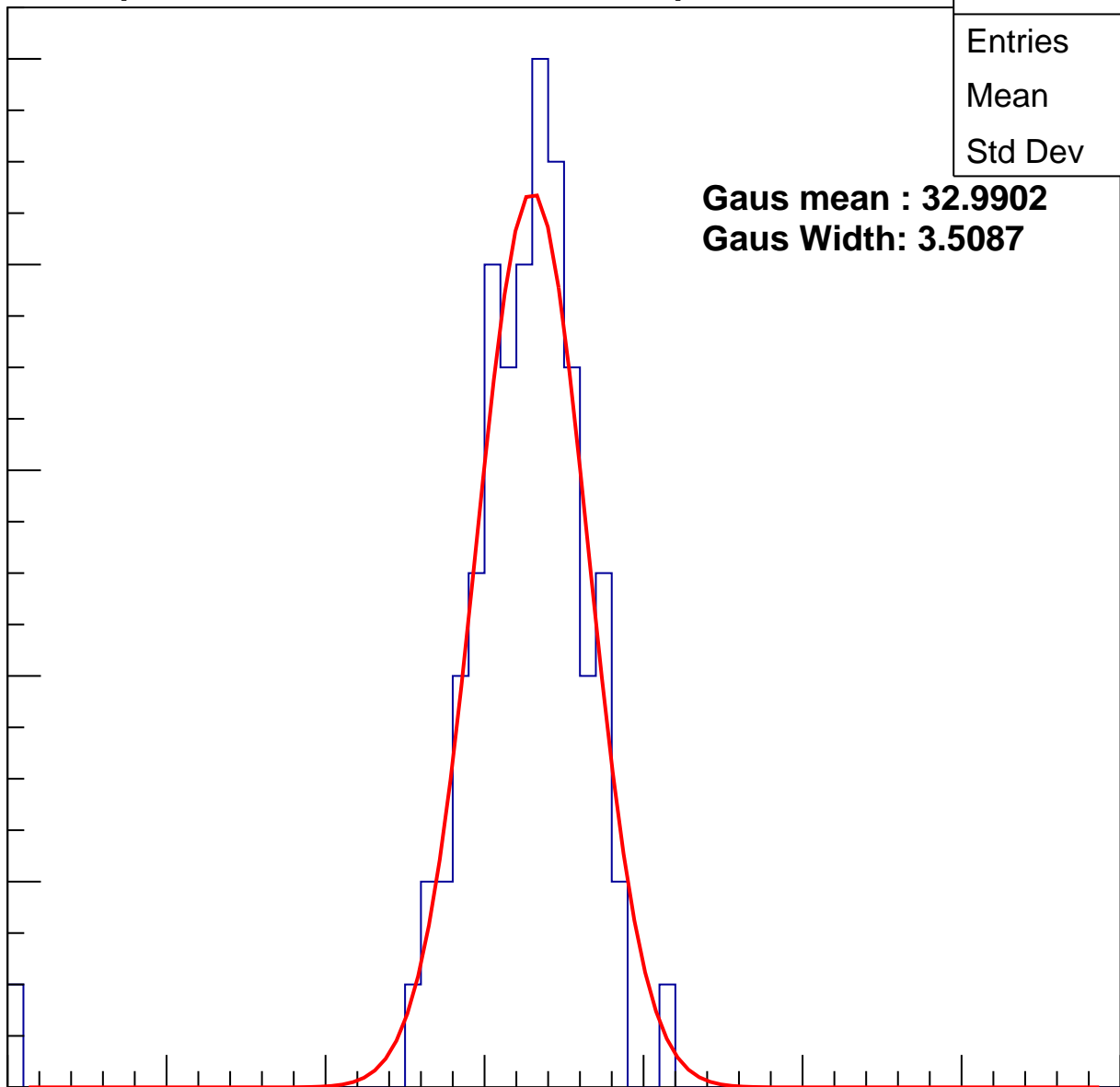
Gaus mean : 32.9902

Gaus Width: 3.5087

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch42, adc1

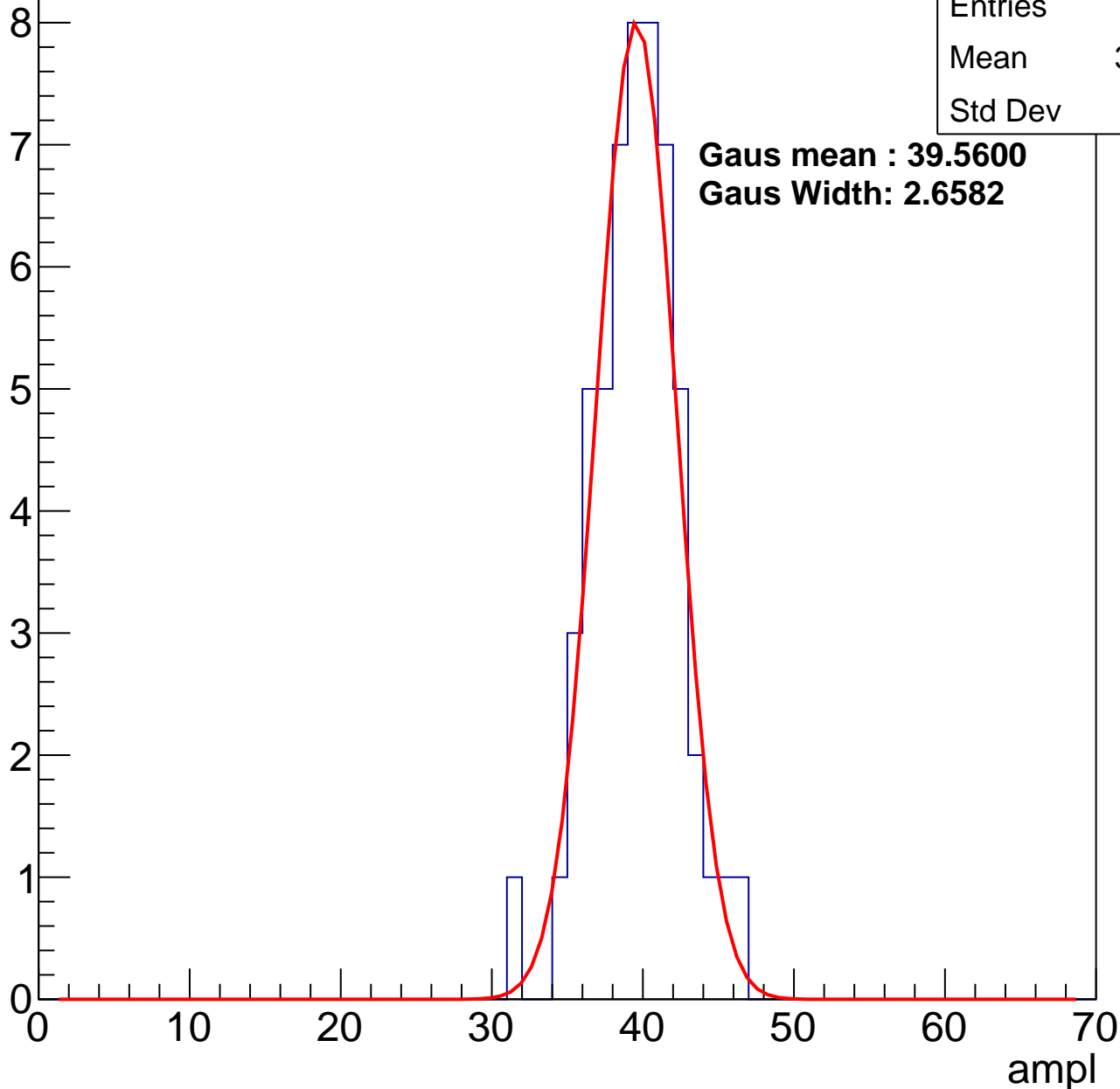
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	39.11
Std Dev	2.8

Gaus mean : 39.5600

Gaus Width: 2.6582



B0L002S, U9-ch42, adc2

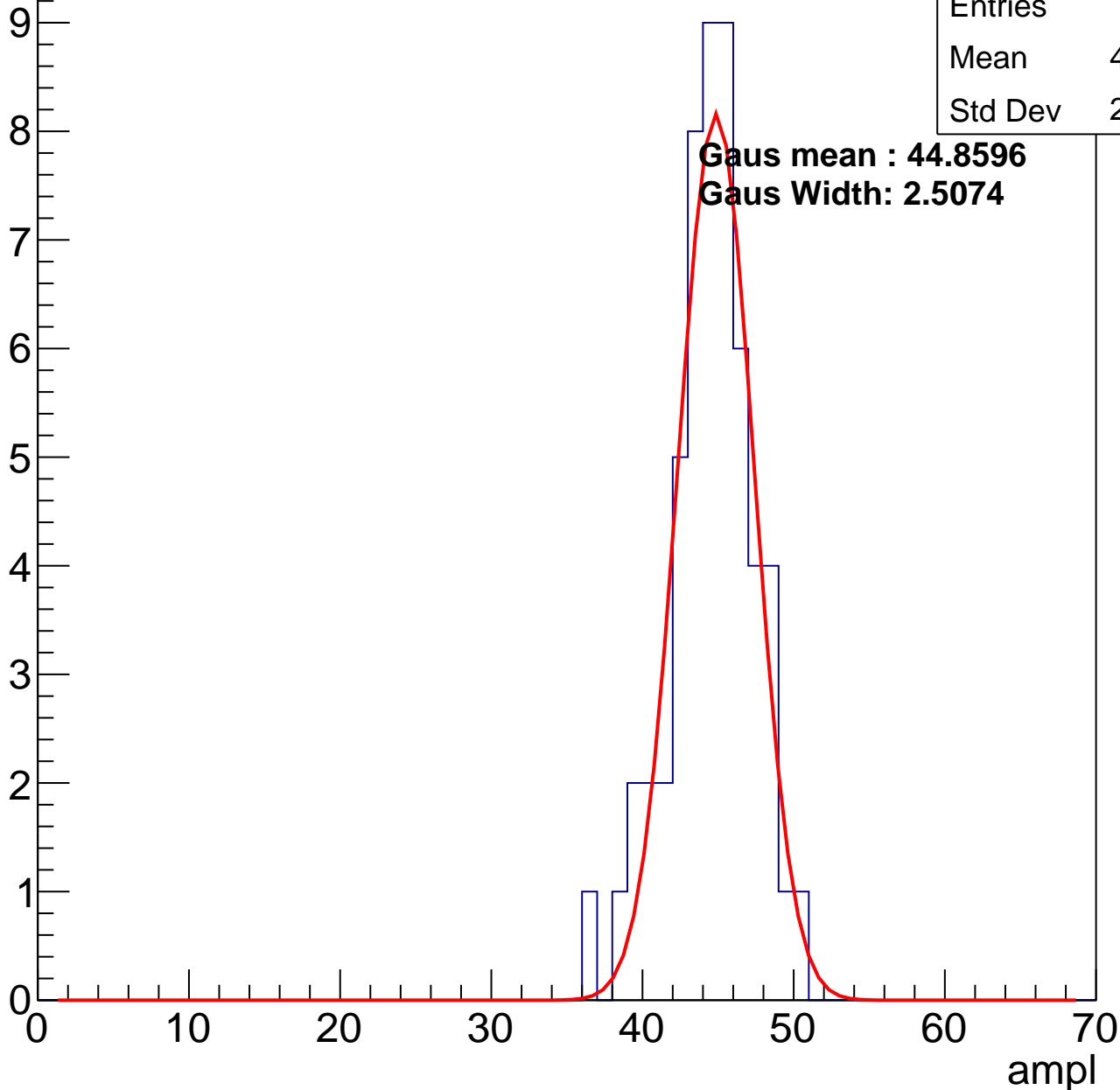
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.07
Std Dev	2.769

Gaus mean : 44.8596

Gaus Width: 2.5074

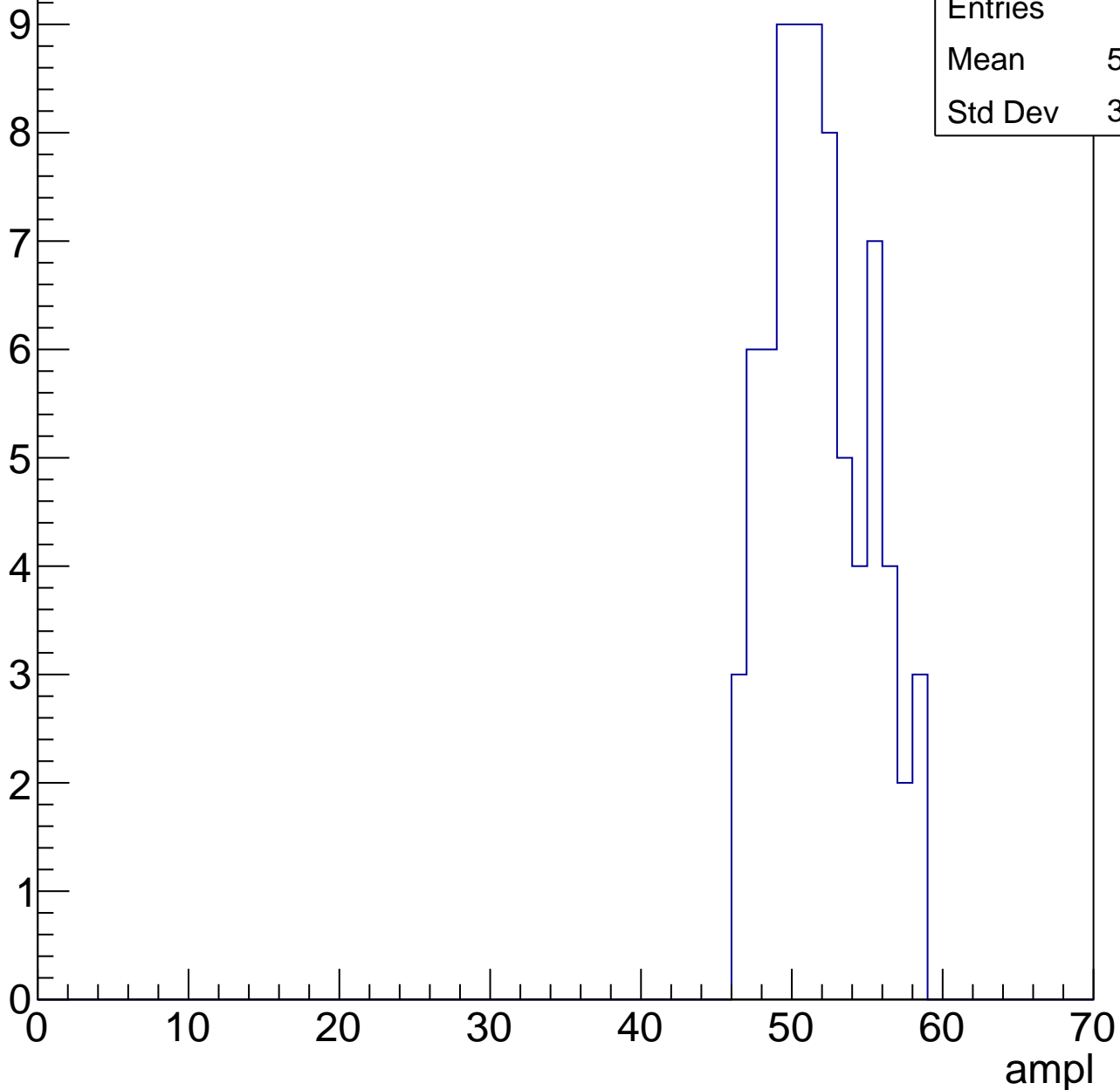


B0L002S, U9-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

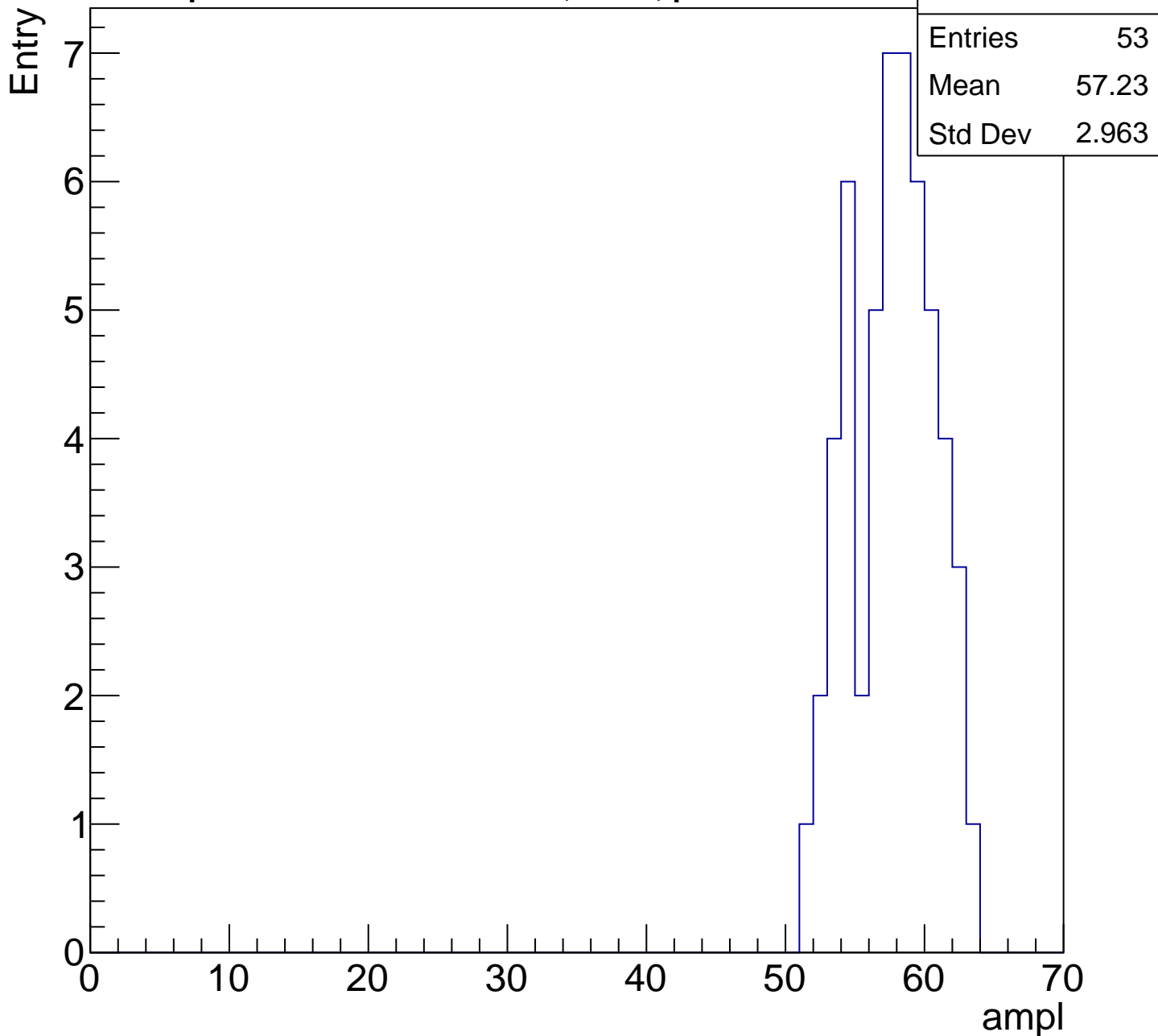
Entry

Entries	75
Mean	51.36
Std Dev	3.173



B0L002S, U9-ch42, adc4

calib_packv5_042523_0143.root, FC#8, port C1

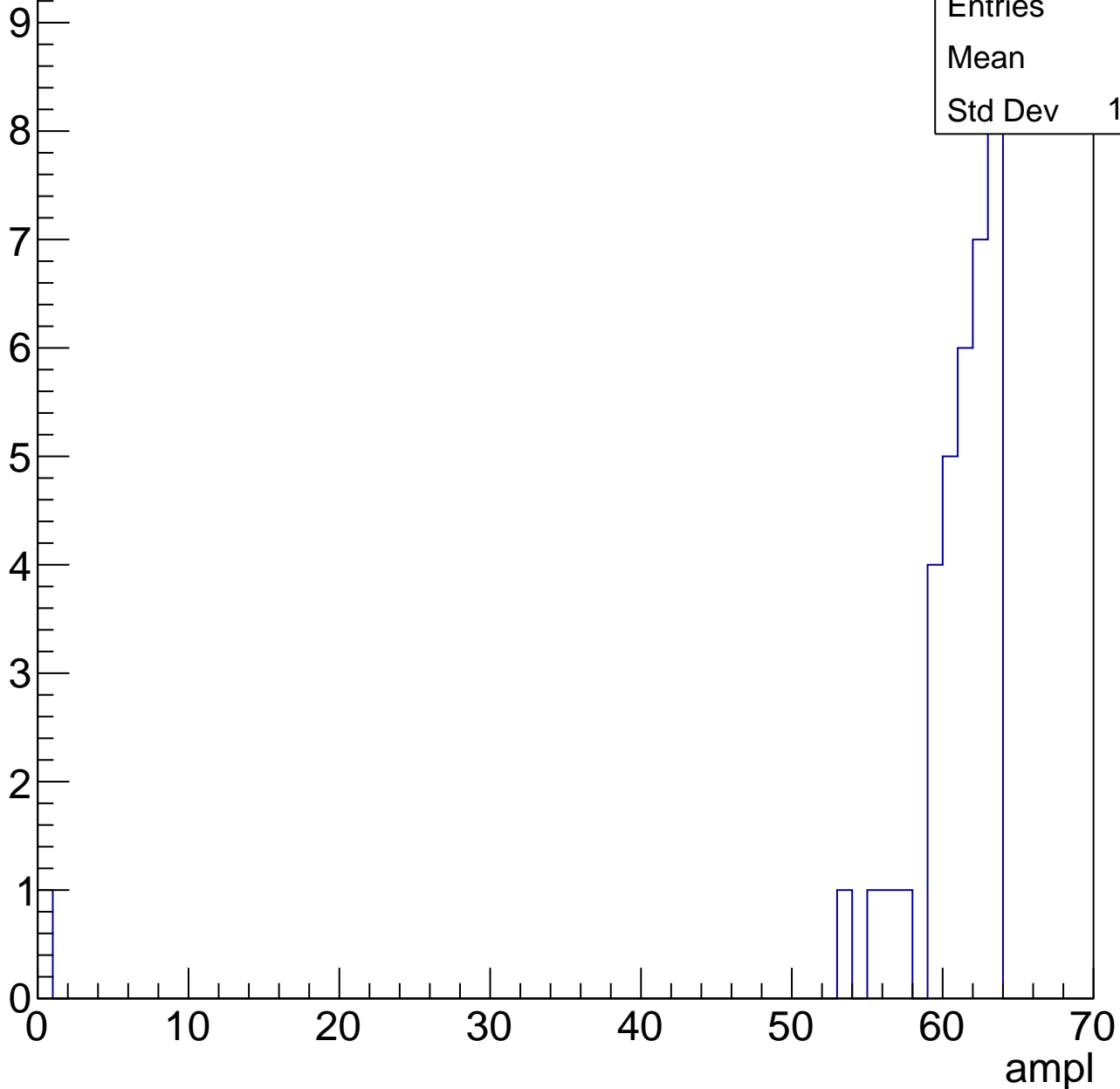


B0L002S, U9-ch42, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59
Std Dev	10.25



B0L002S, U9-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U9-ch43, adc0

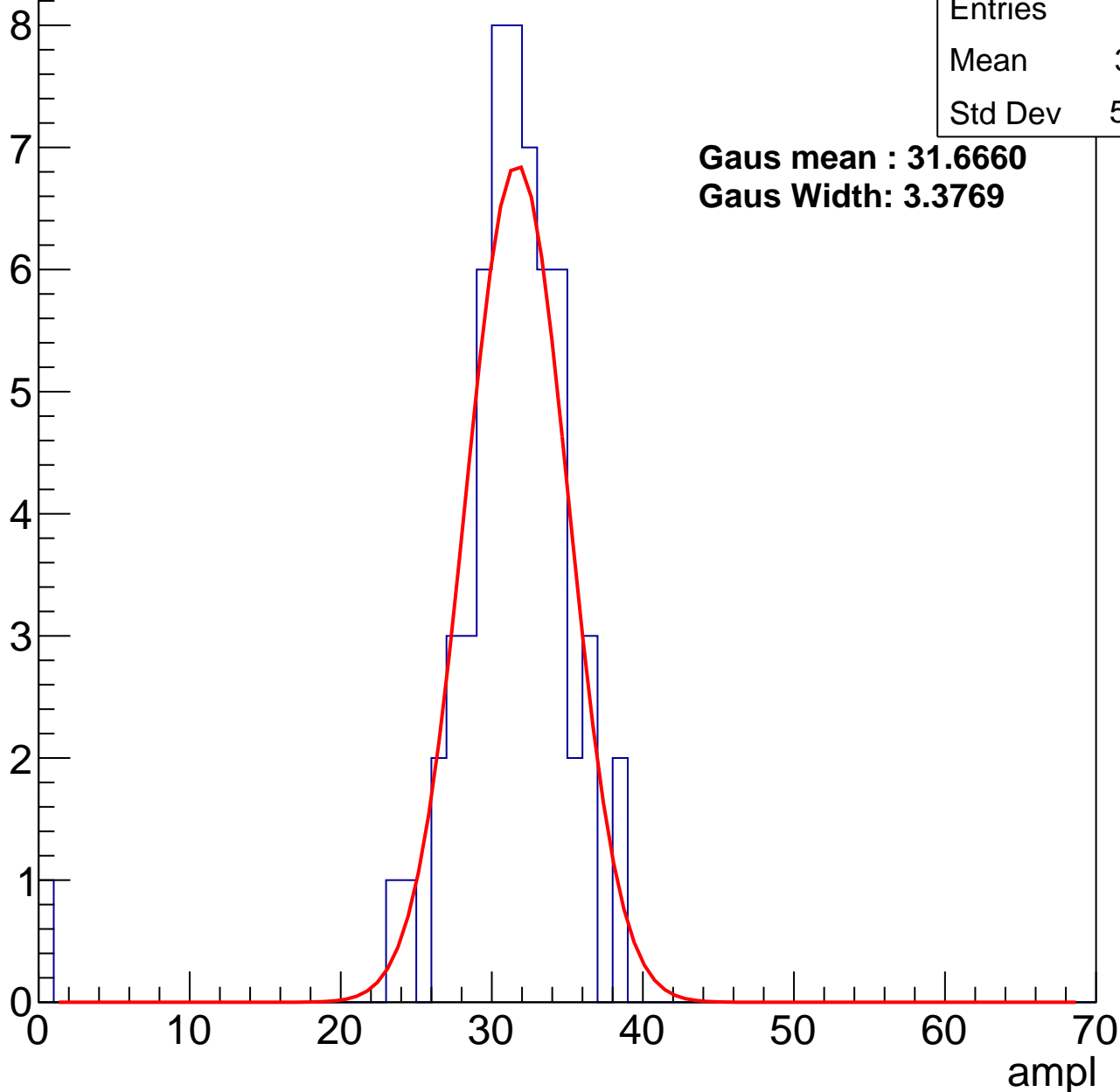
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.61
Std Dev	5.062

Gaus mean : 31.6660

Gaus Width: 3.3769



B0L002S, U9-ch43, adc1

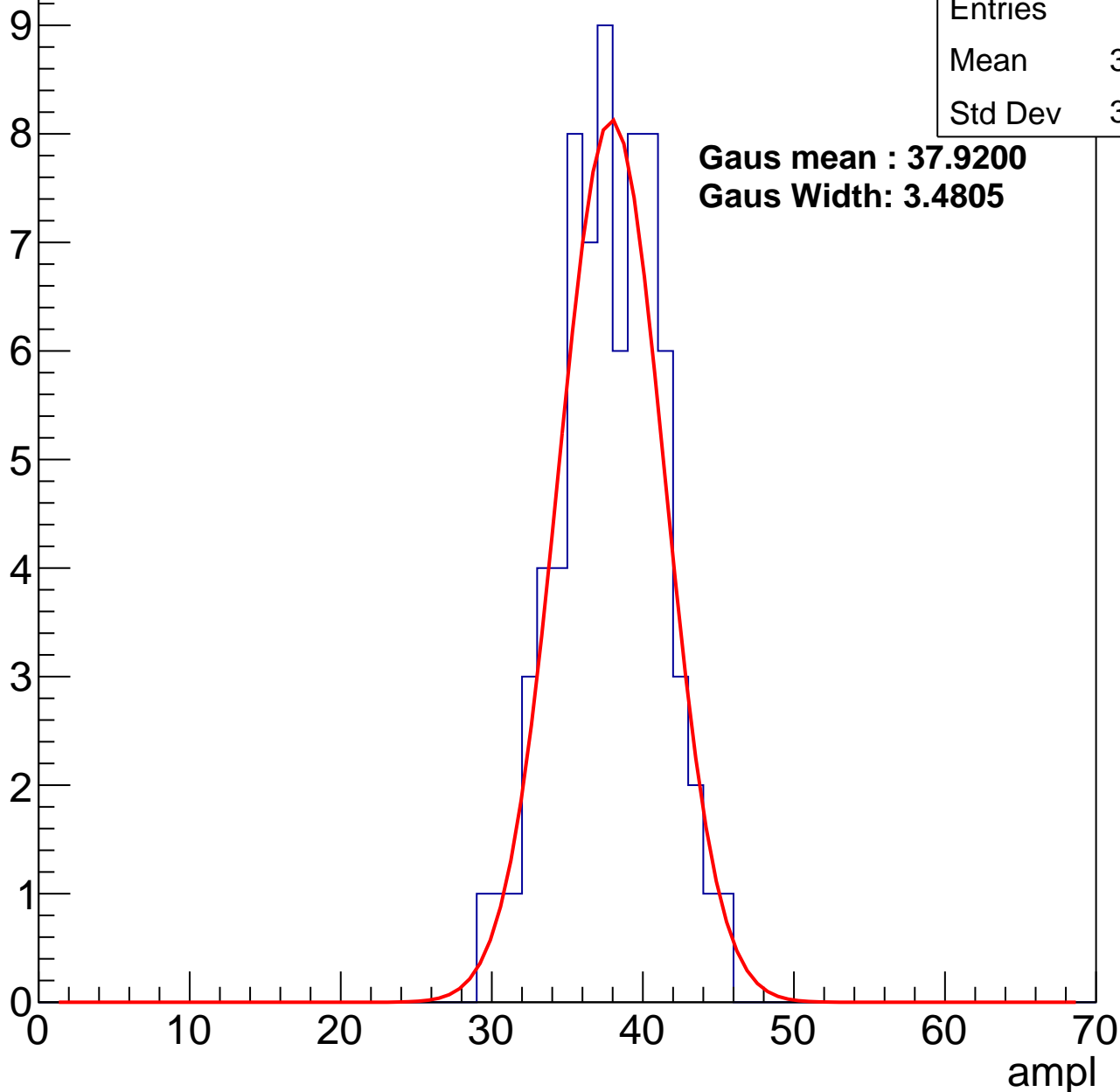
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.34
Std Dev	3.352

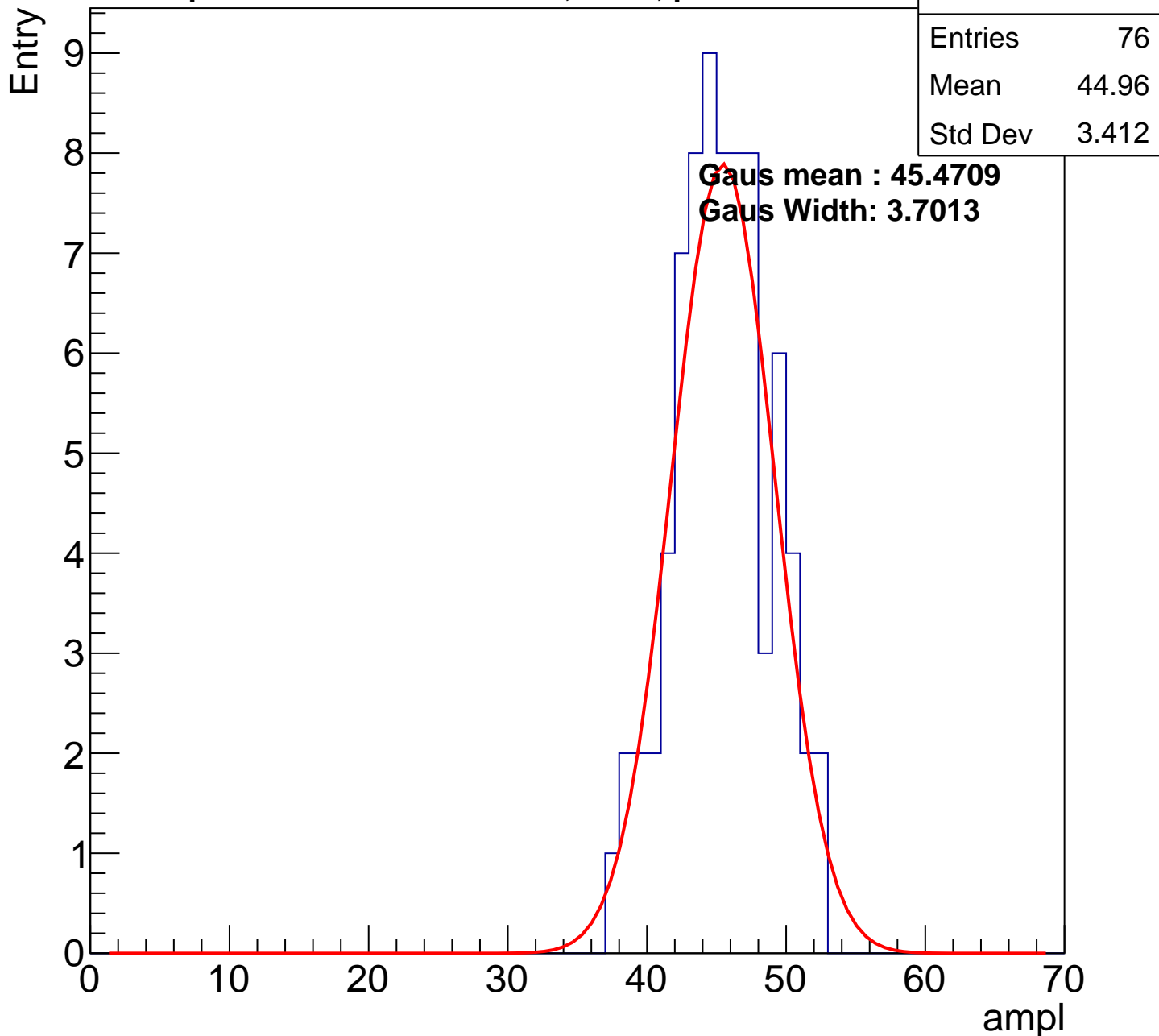
Gaus mean : 37.9200

Gaus Width: 3.4805



B0L002S, U9-ch43, adc2

calib_packv5_042523_0143.root, FC#8, port C1

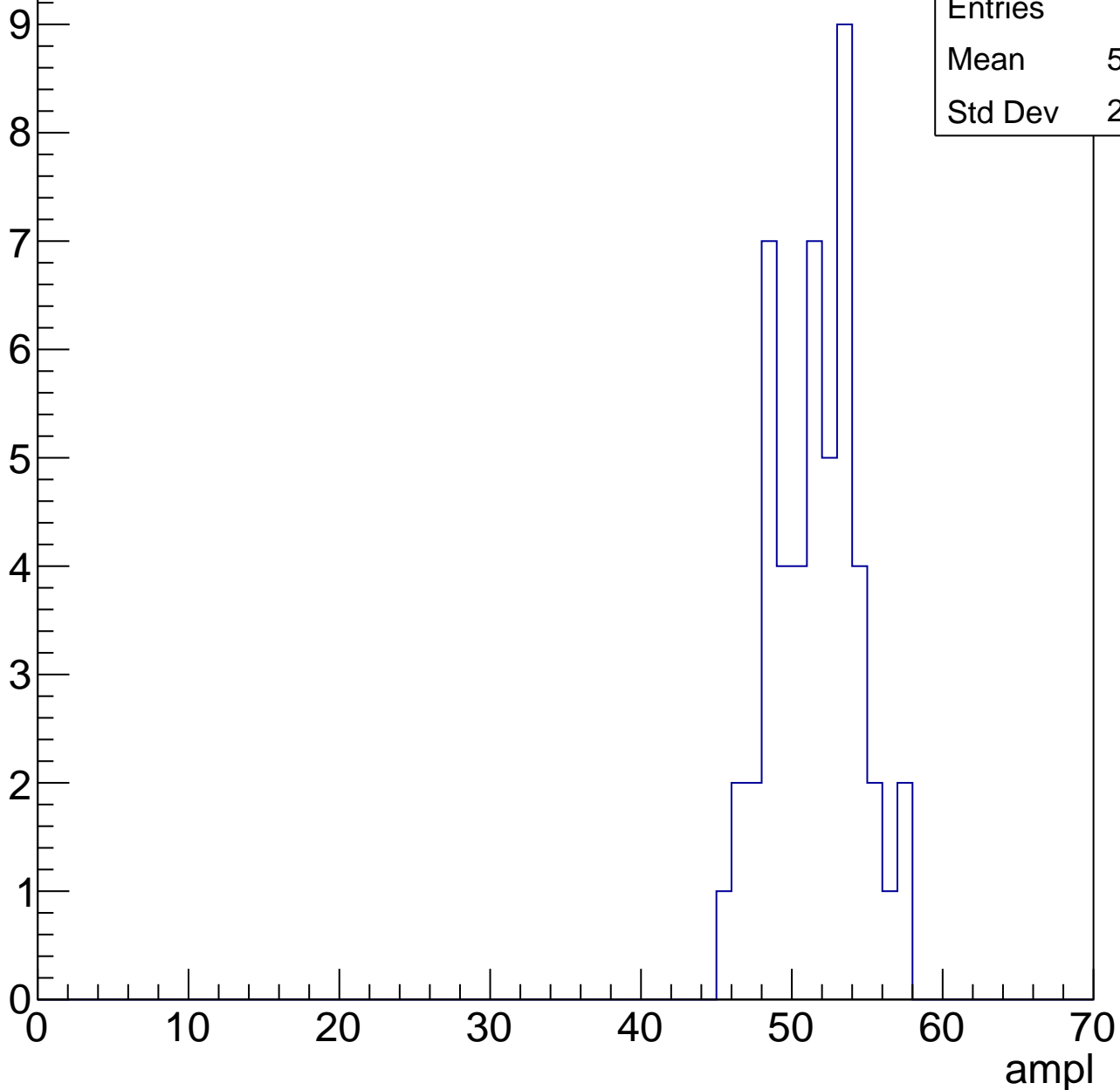


B0L002S, U9-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	51.06
Std Dev	2.852



B0L002S, U9-ch43, adc4

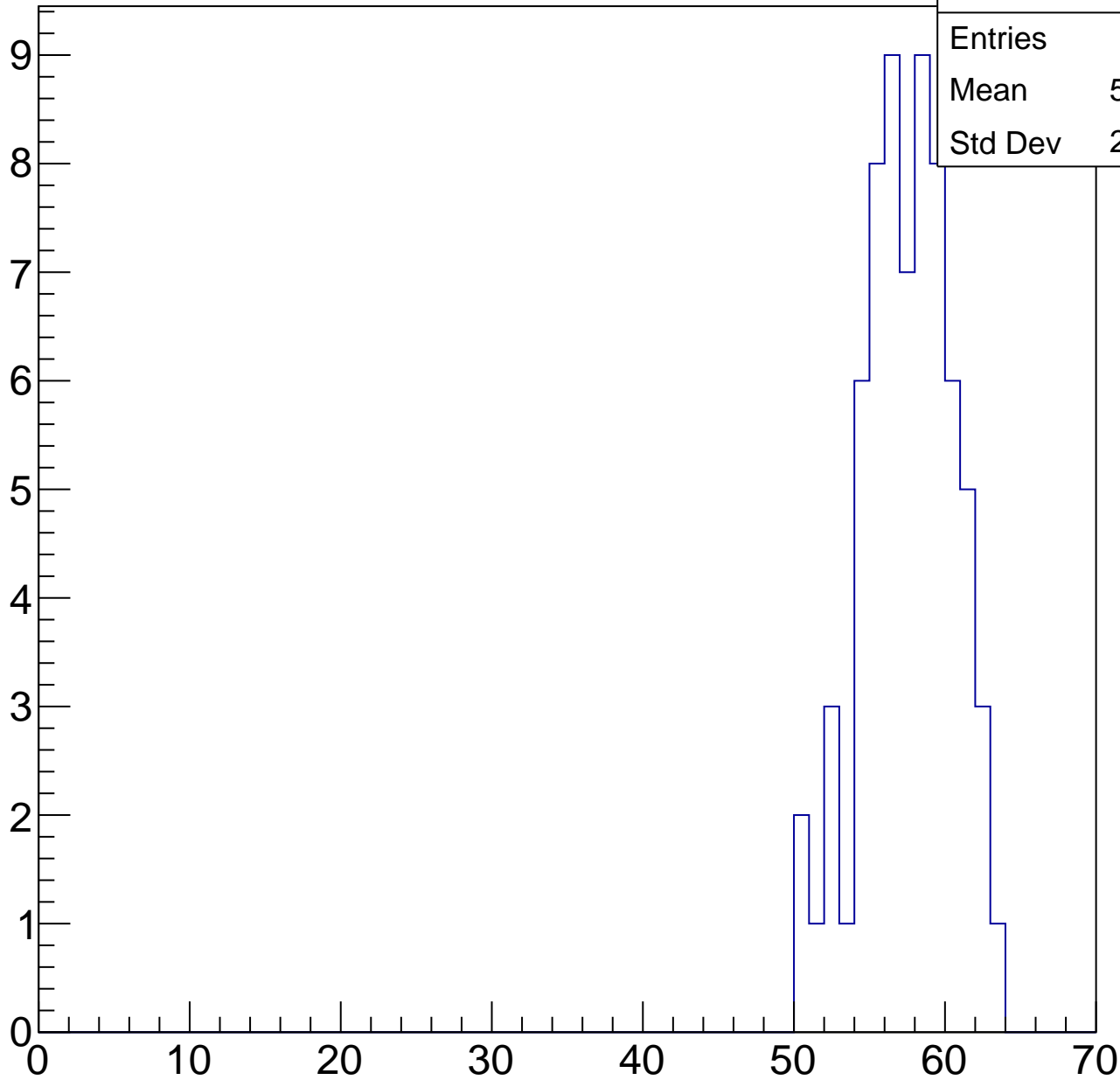
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	57.03
Std Dev	2.963

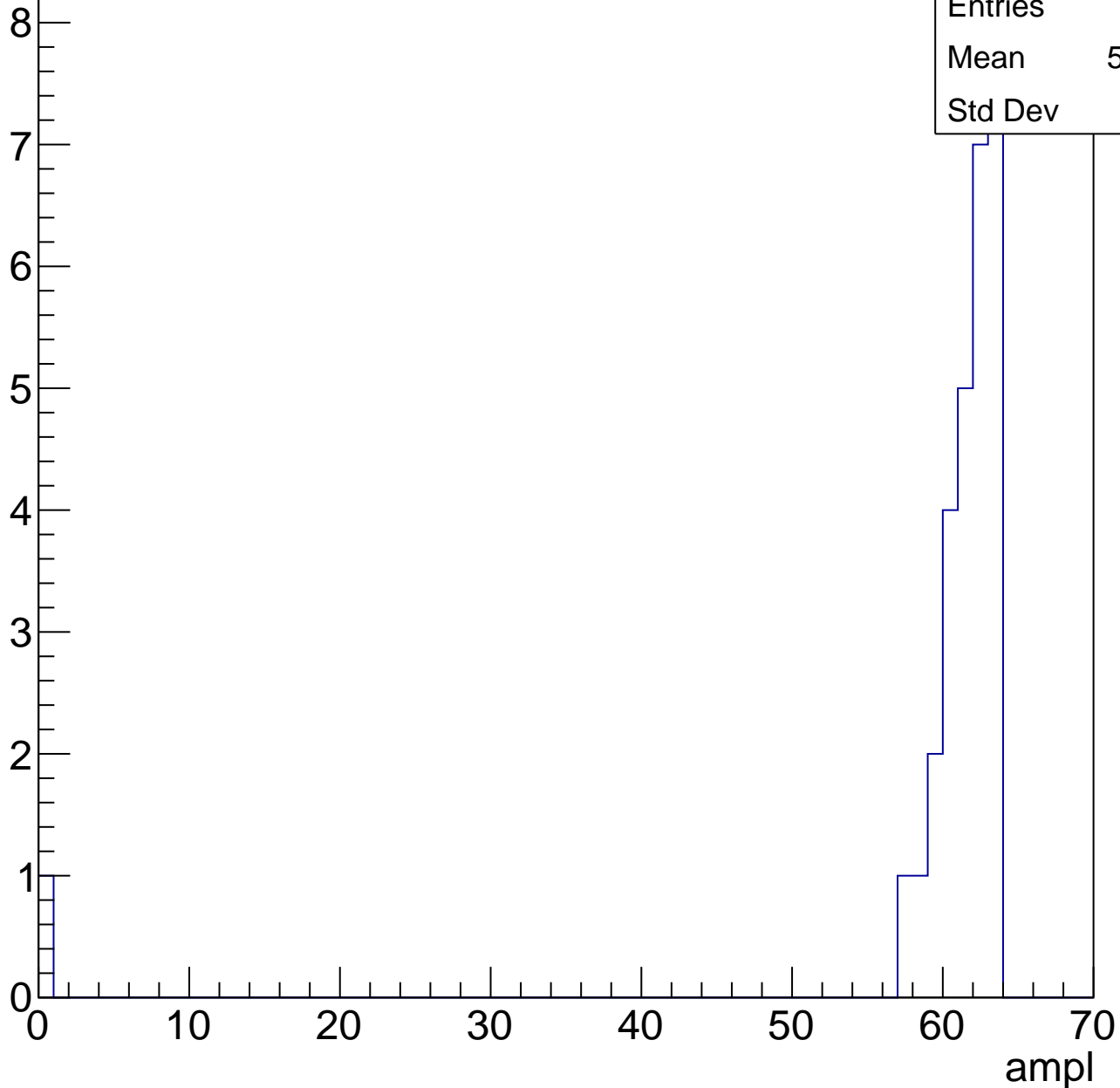
ampl



B0L002S, U9-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

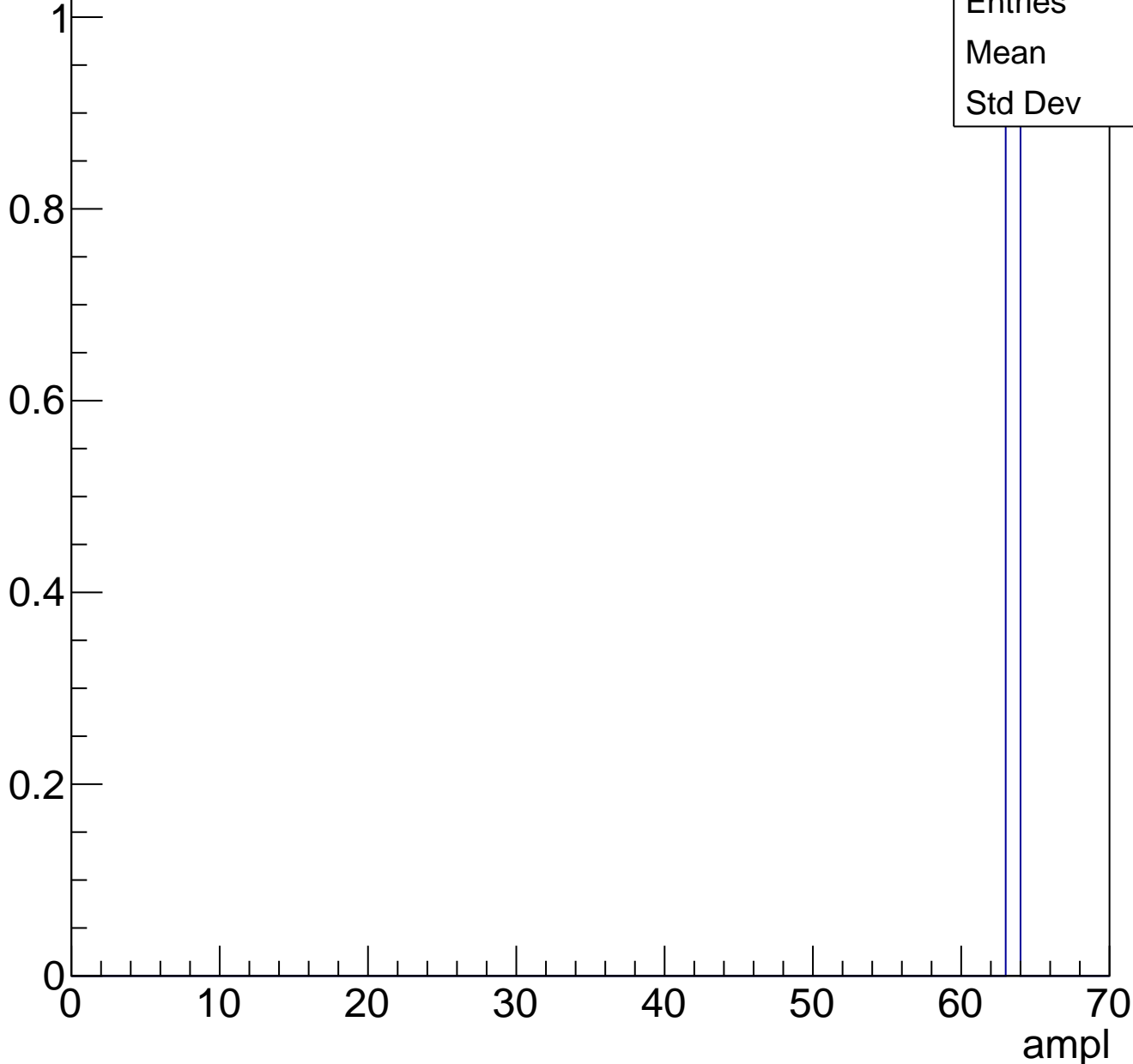
Entry



B0L002S, U9-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch44, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	31.62
Std Dev	3.343

Gaus mean : 32.0994

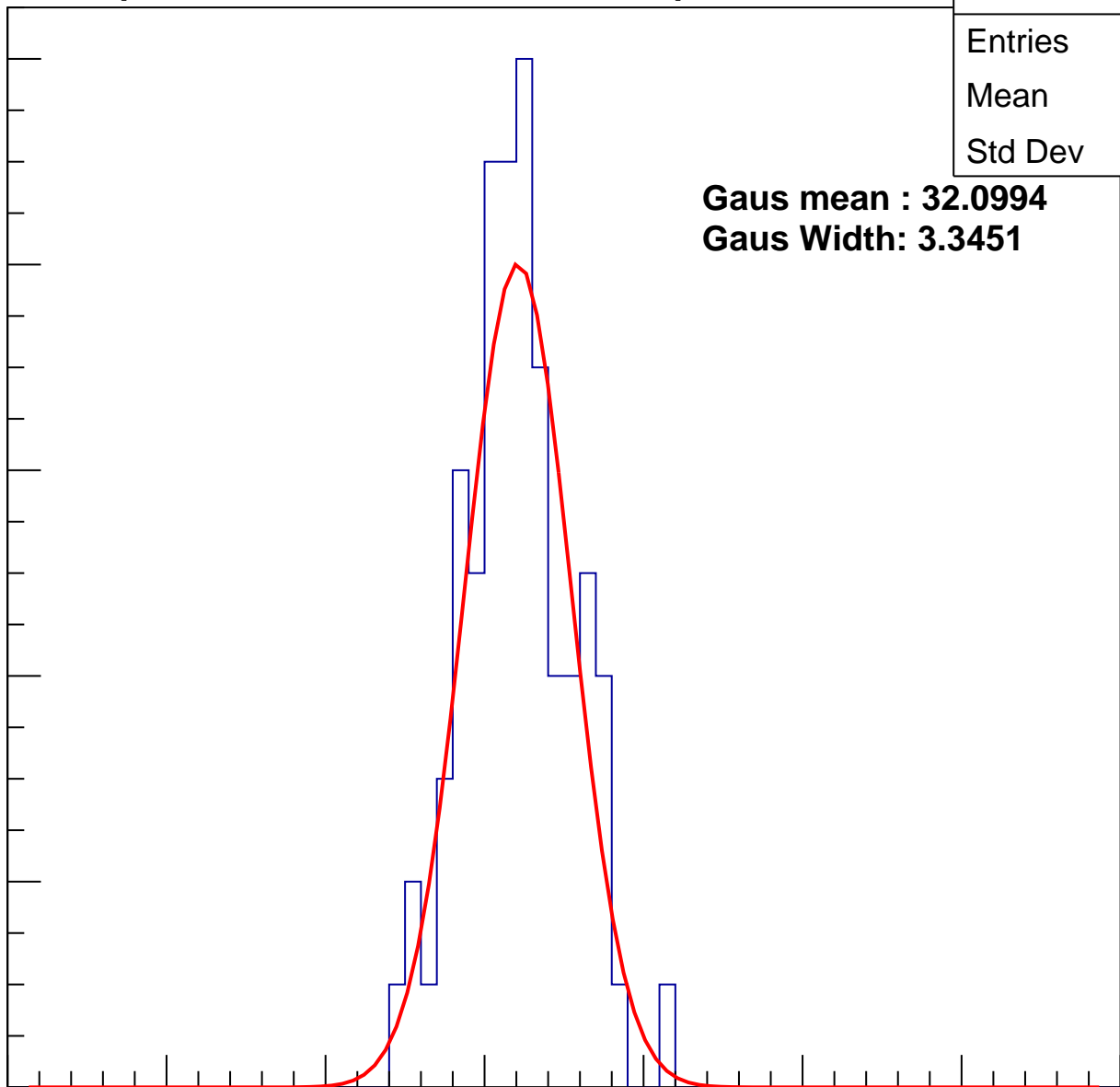
Gaus Width: 3.3451

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch44, adc1

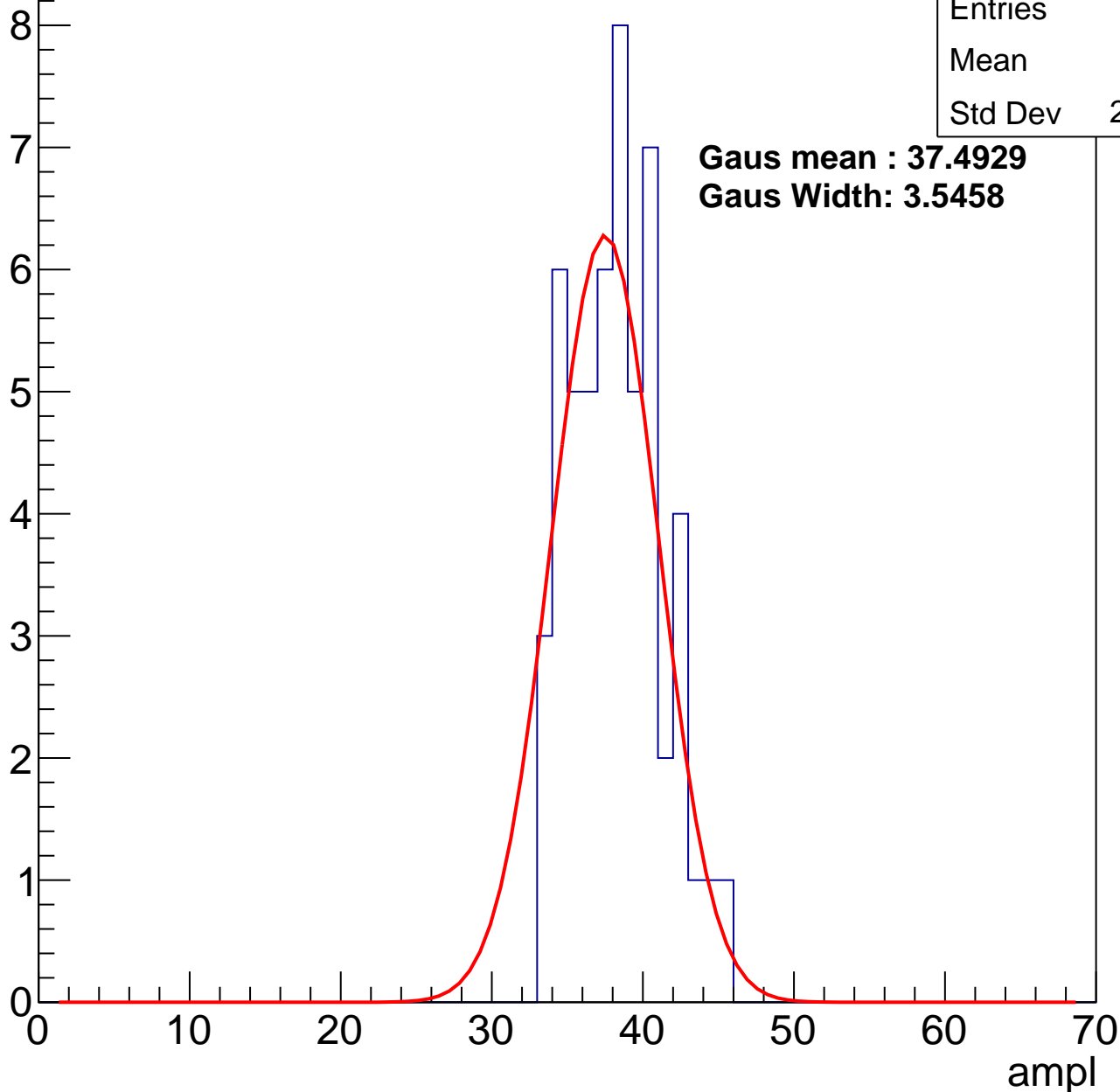
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	37.8
Std Dev	2.934

Gaus mean : 37.4929

Gaus Width: 3.5458



B0L002S, U9-ch44, adc2

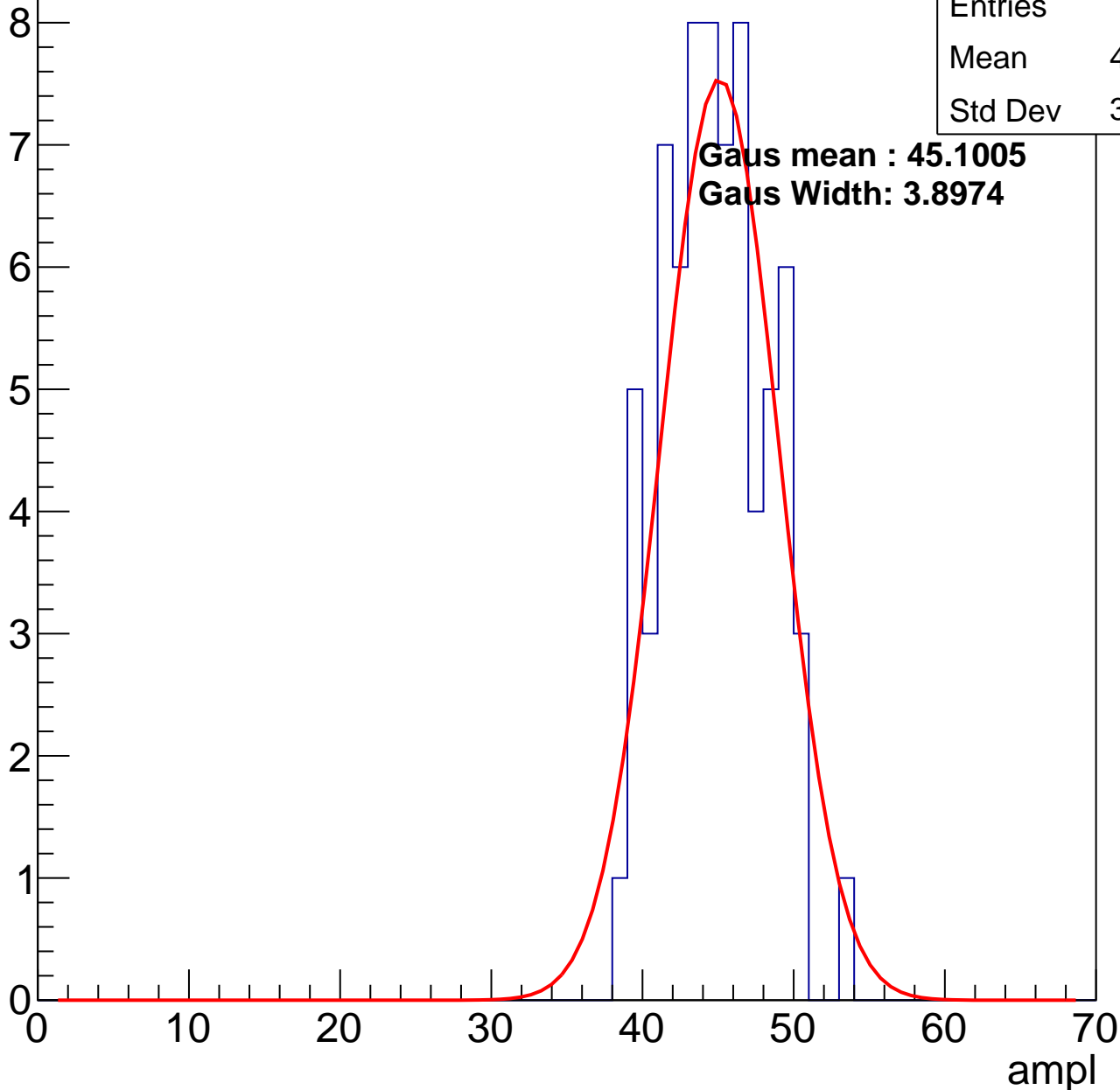
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.39
Std Dev	3.306

Gaus mean : 45.1005

Gaus Width: 3.8974



B0L002S, U9-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

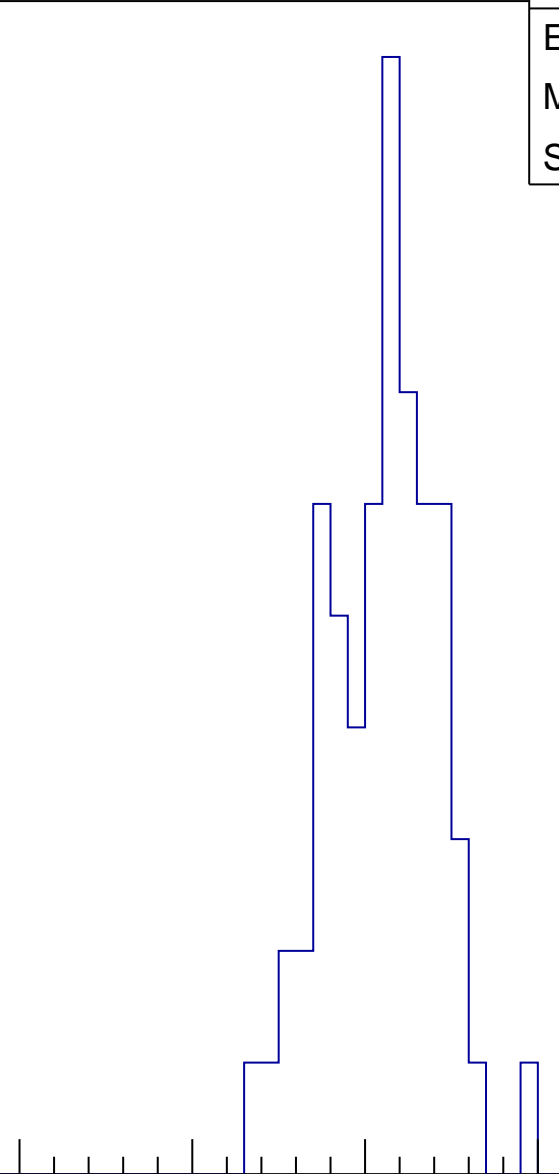
Entries	61
Mean	50.54
Std Dev	3.155

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch44, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries 76

Mean 57.5

Std Dev 2.812

8

6

4

2

0

0

10

20

30

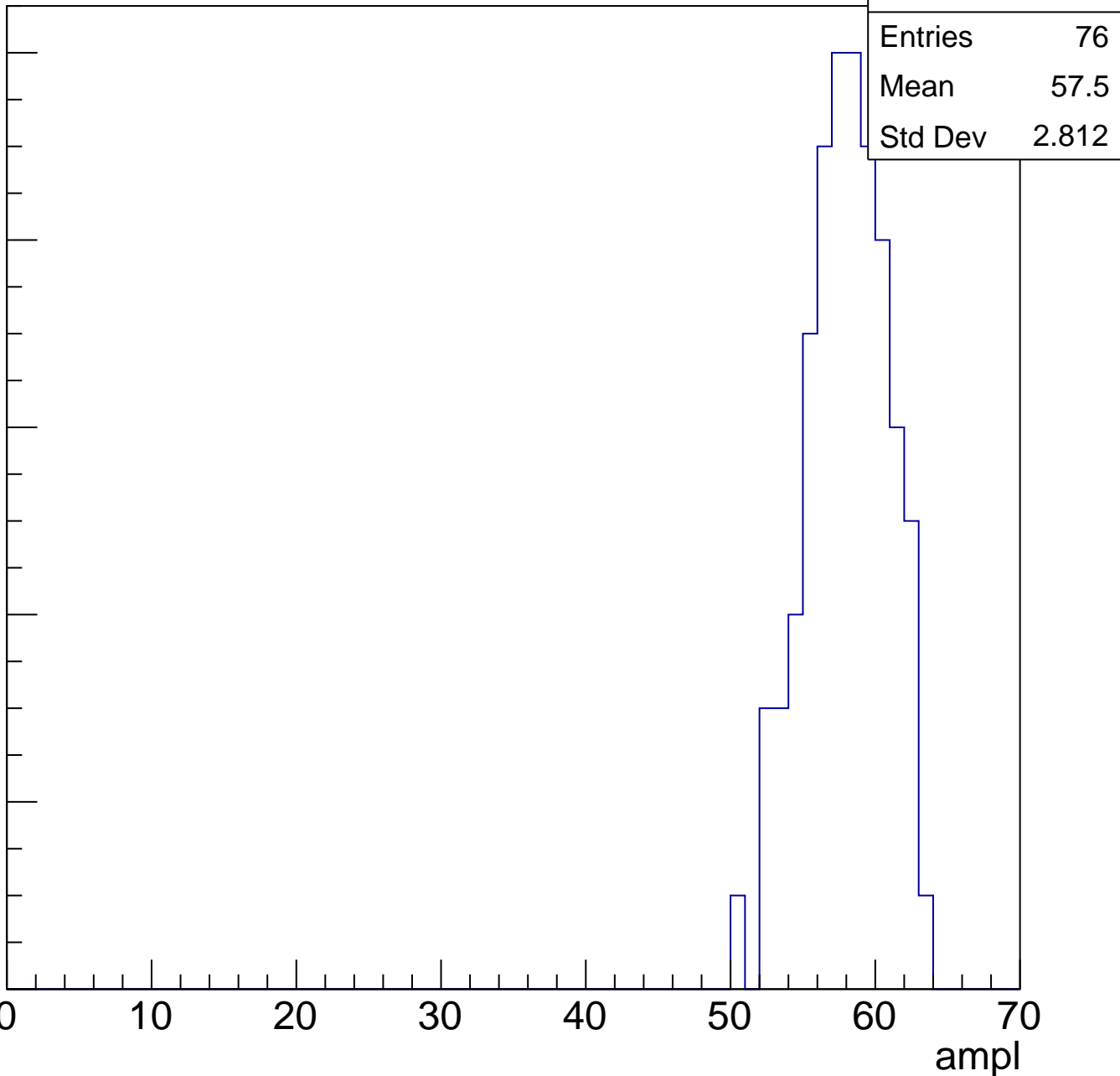
40

50

60

70

ampl

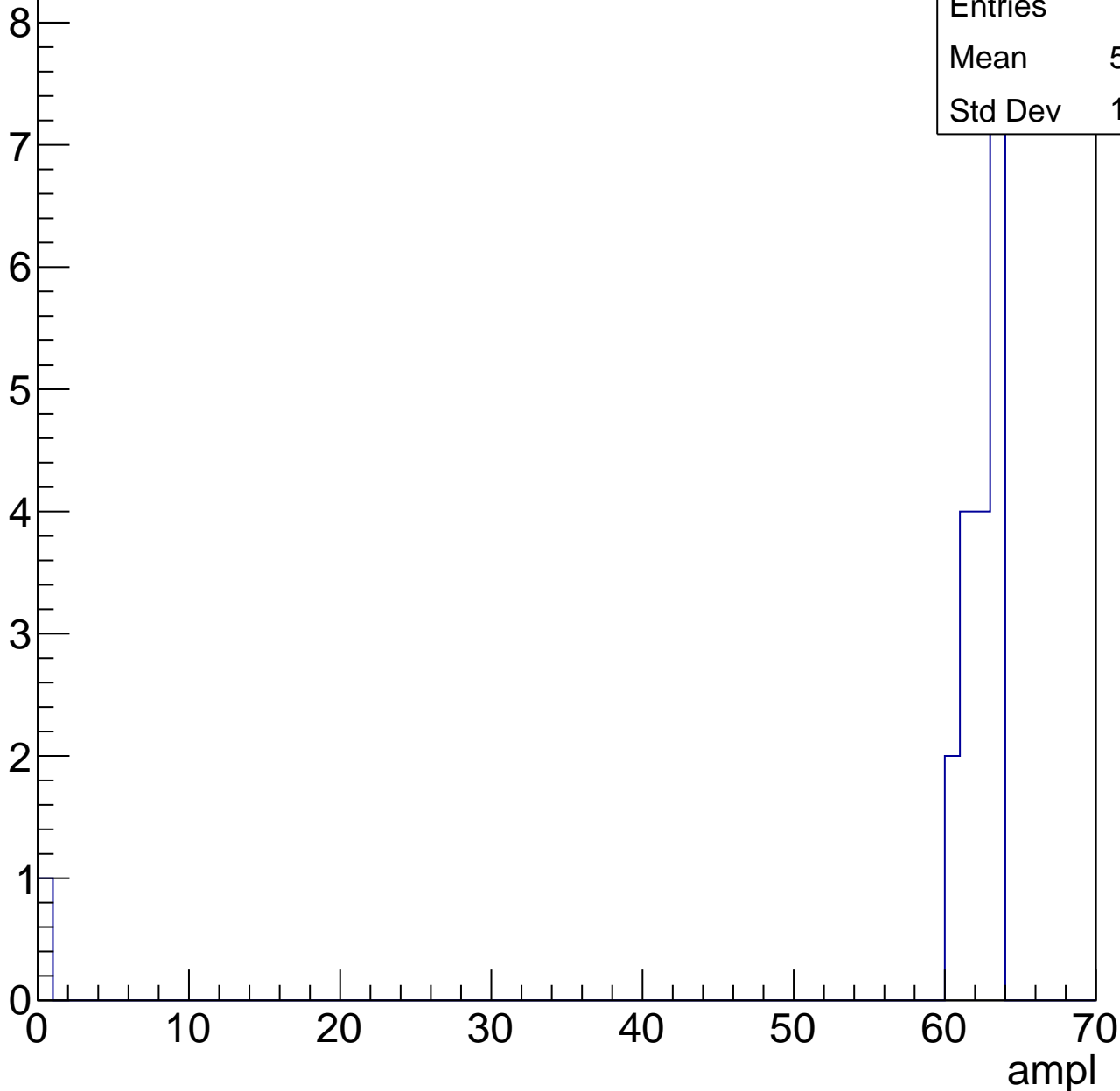


B0L002S, U9-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	58.74
Std Dev	13.88



B0L002S, U9-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch45, adc0

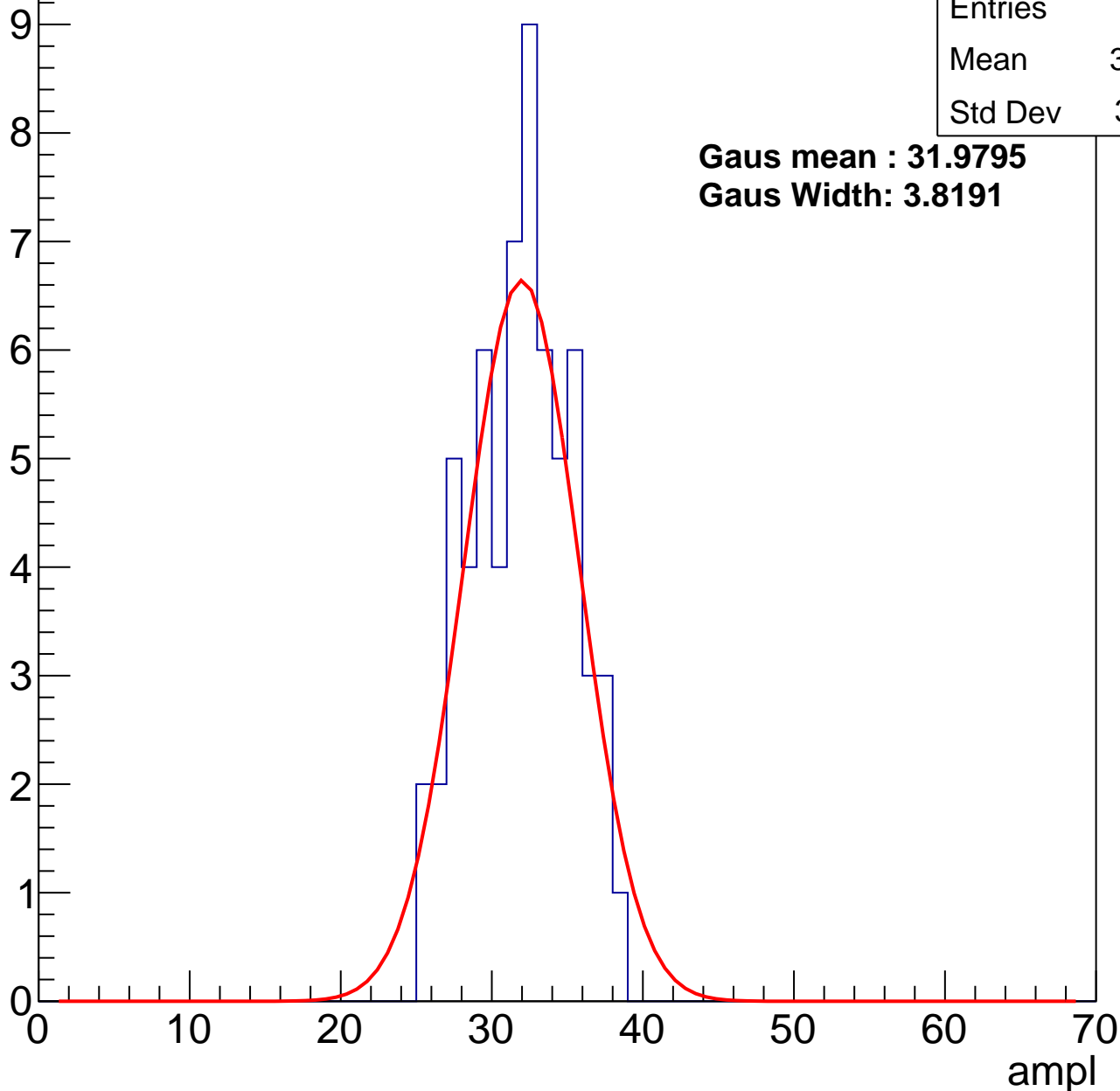
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	31.48
Std Dev	3.231

Gaus mean : 31.9795

Gaus Width: 3.8191



B0L002S, U9-ch45, adc1

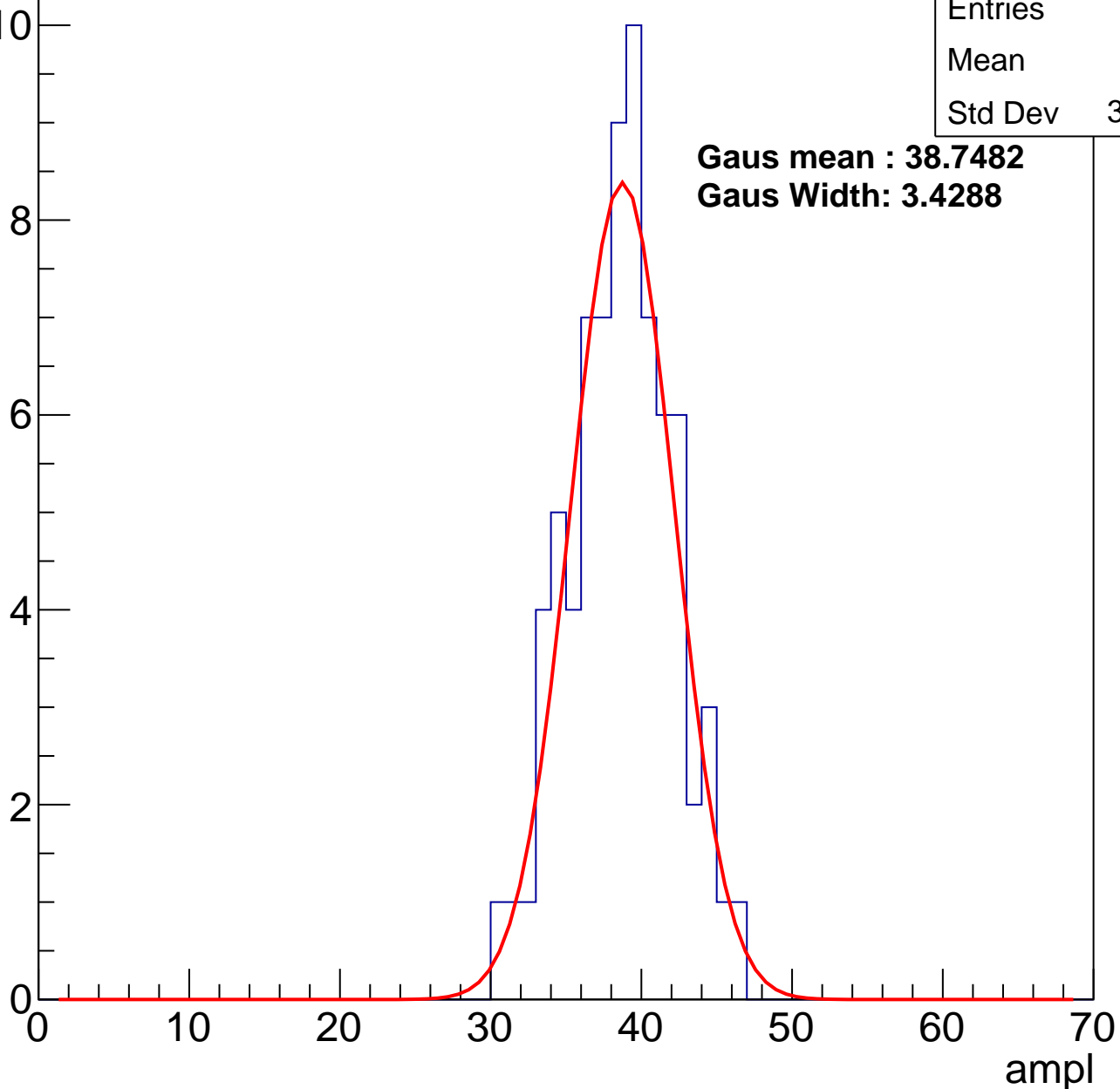
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.2
Std Dev	3.378

Gaus mean : 38.7482

Gaus Width: 3.4288



B0L002S, U9-ch45, adc2

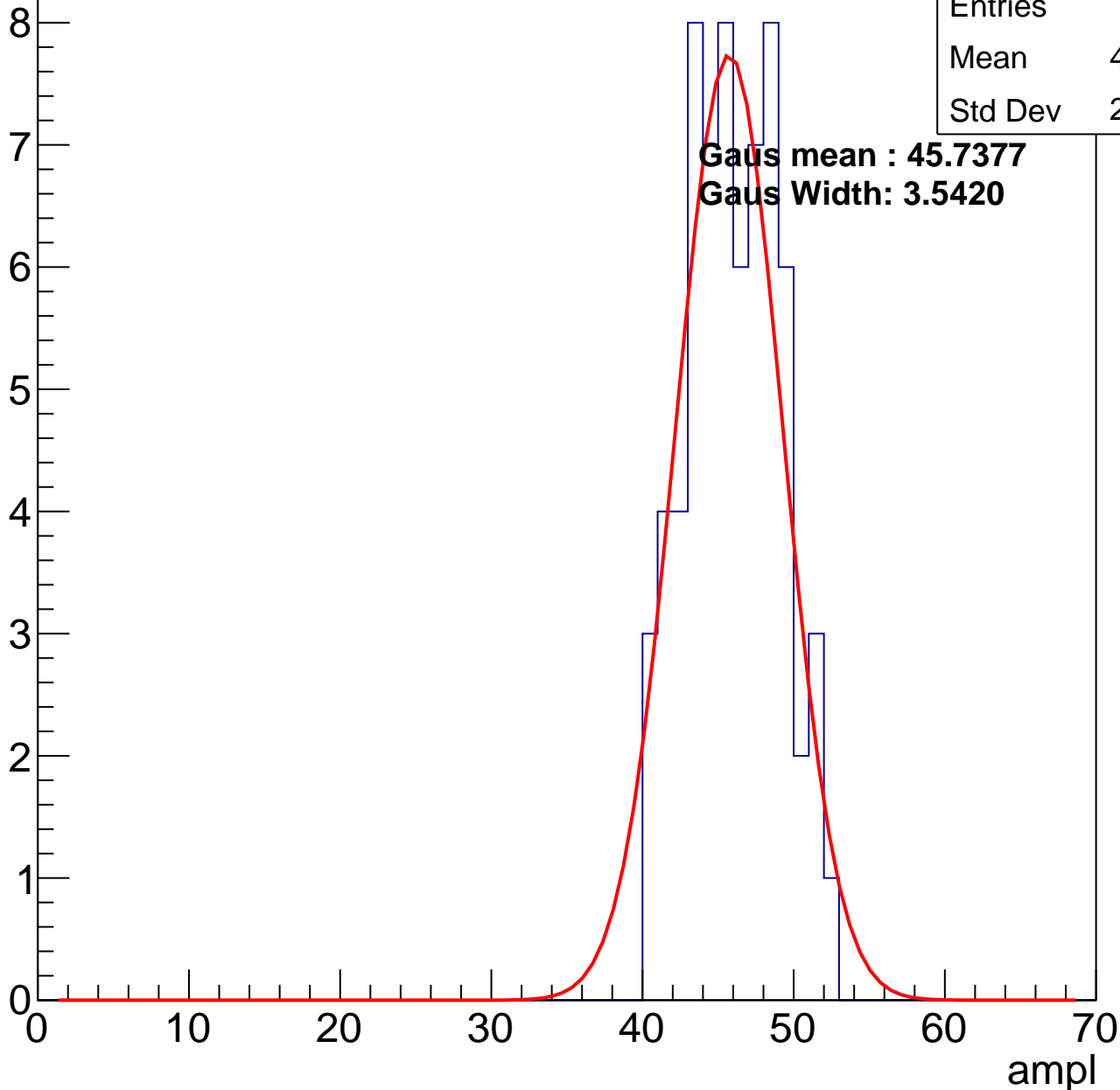
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	45.55
Std Dev	2.999

Gaus mean : 45.7377

Gaus Width: 3.5420

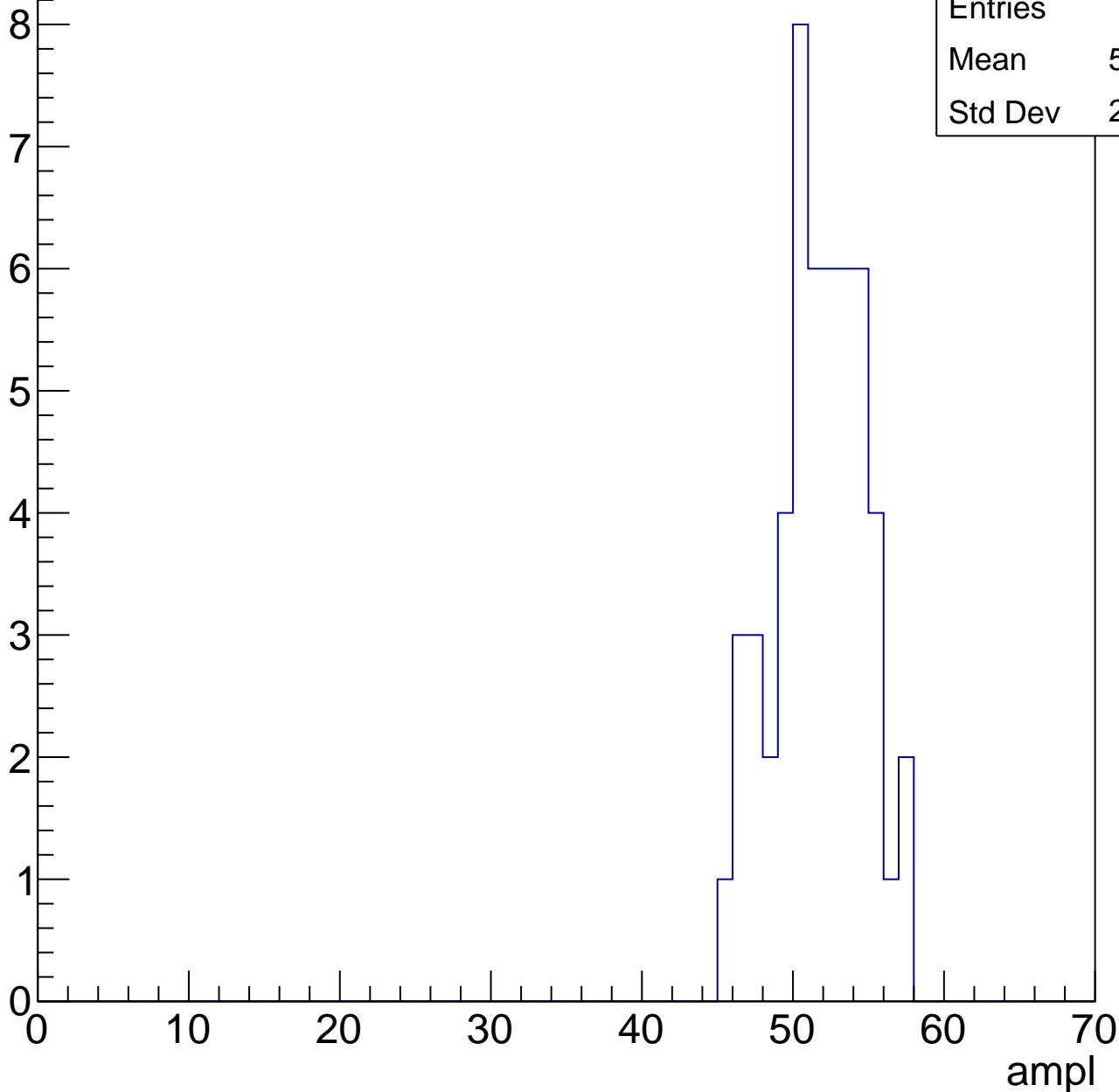


B0L002S, U9-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	51.27
Std Dev	2.916



B0L002S, U9-ch45, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	57.46
Std Dev	2.975

Entry

10

8

6

4

2

0

0

10

20

30

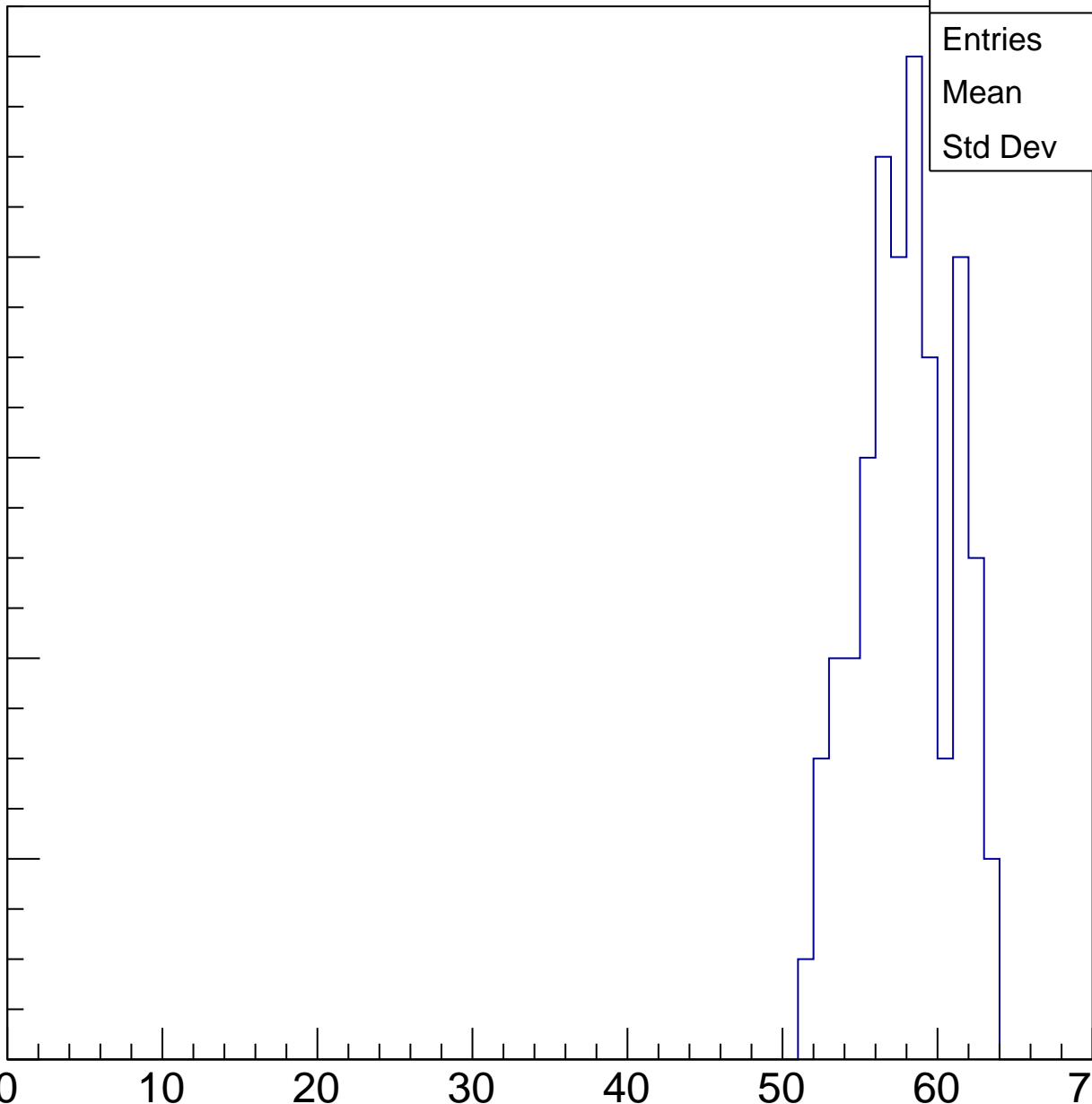
40

50

60

70

ampl



B0L002S, U9-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

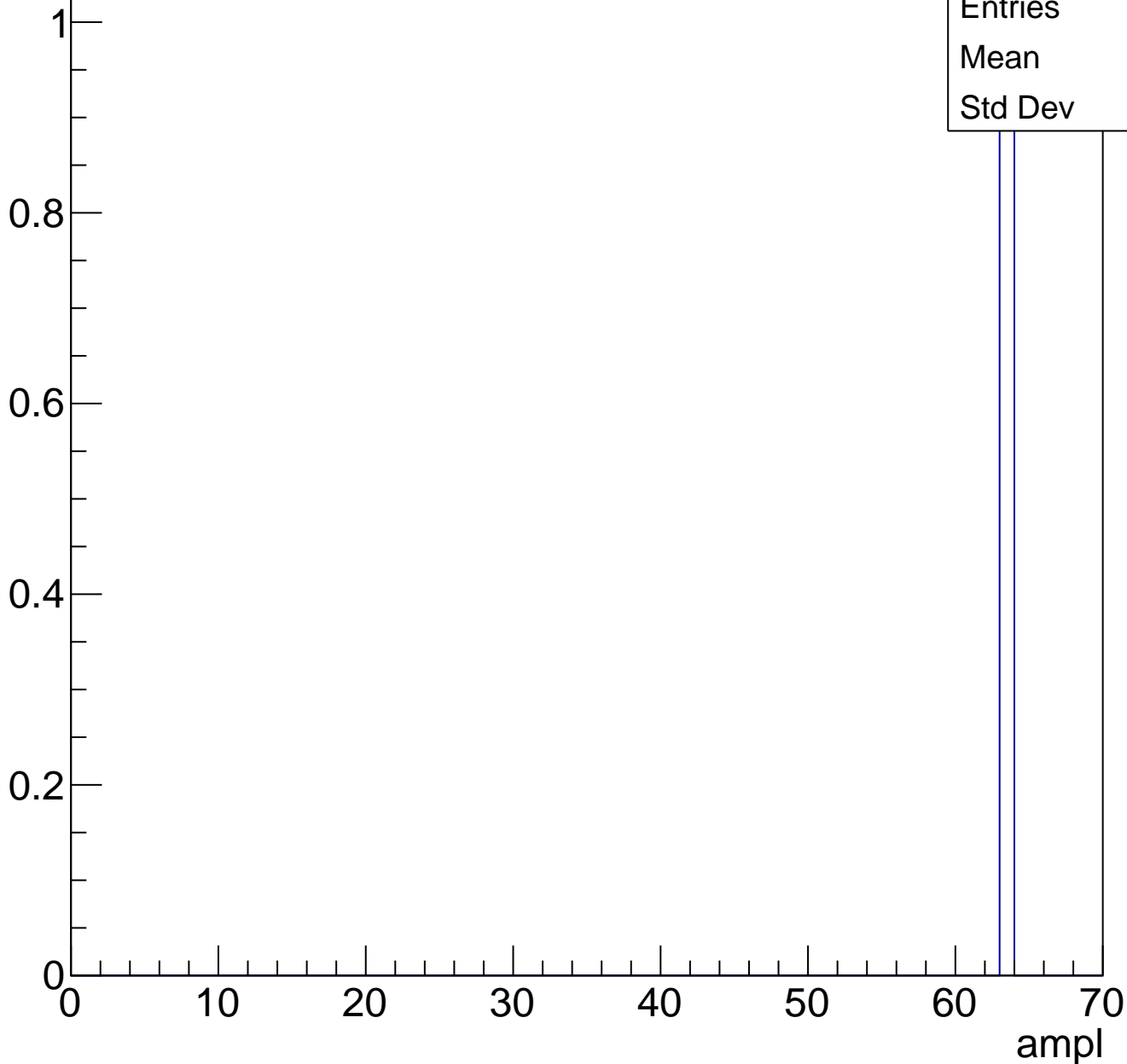
Entries	25
Mean	58.8
Std Dev	12.09

ampl

B0L002S, U9-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U9-ch46, adc0

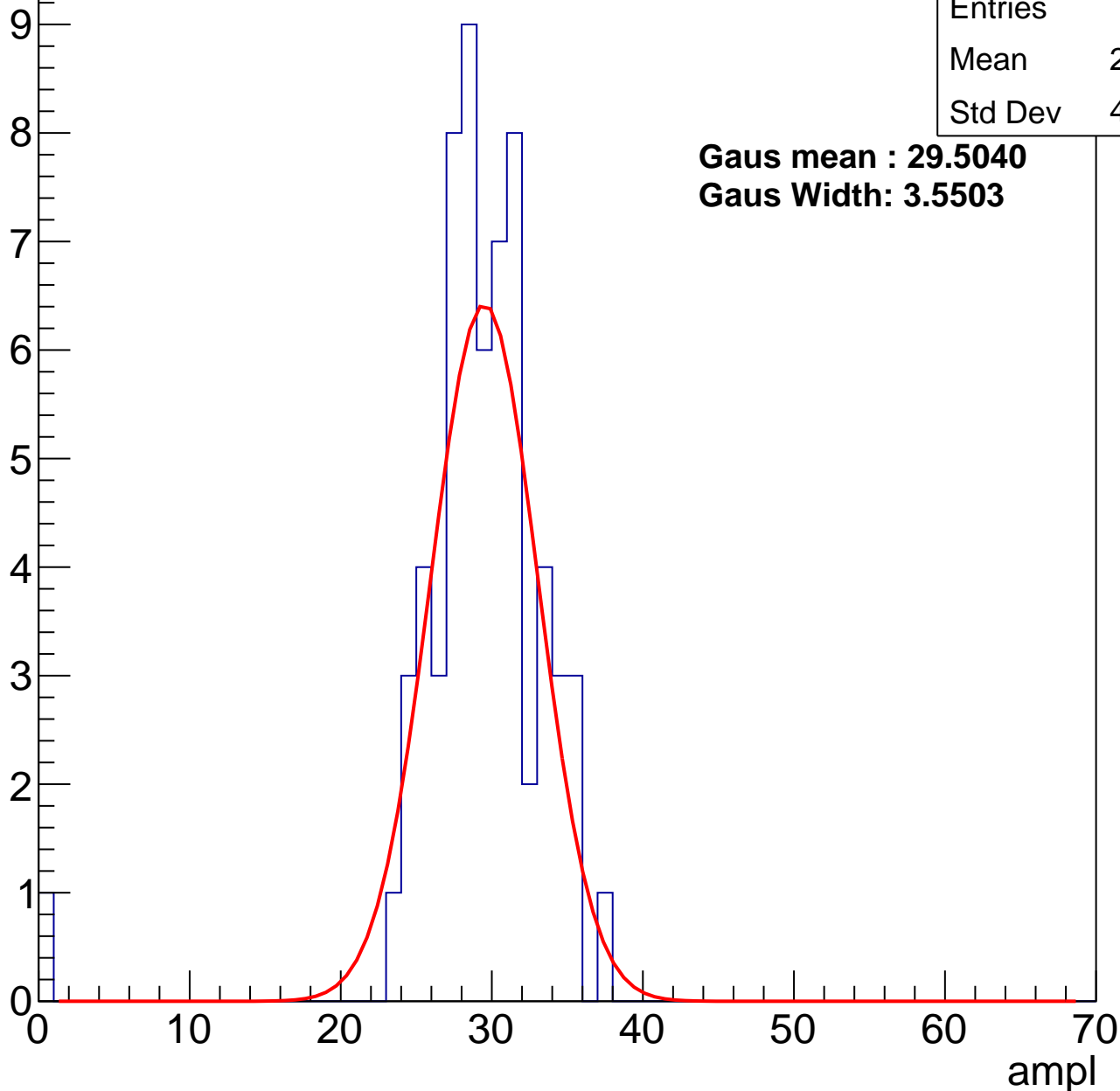
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	28.78
Std Dev	4.796

Gaus mean : 29.5040

Gaus Width: 3.5503



B0L002S, U9-ch46, adc1

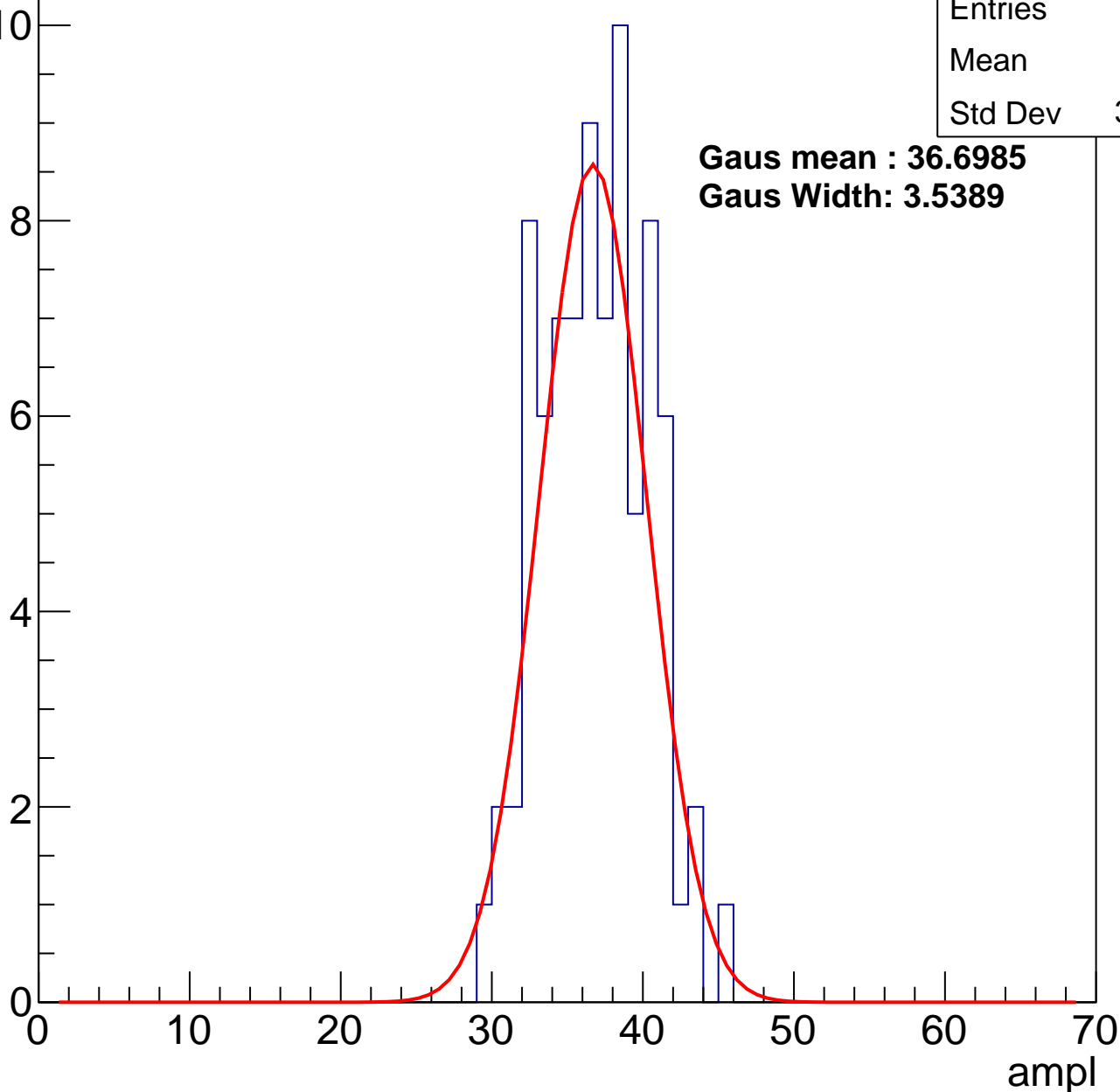
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	36.4
Std Dev	3.421

Gaus mean : 36.6985

Gaus Width: 3.5389



B0L002S, U9-ch46, adc2

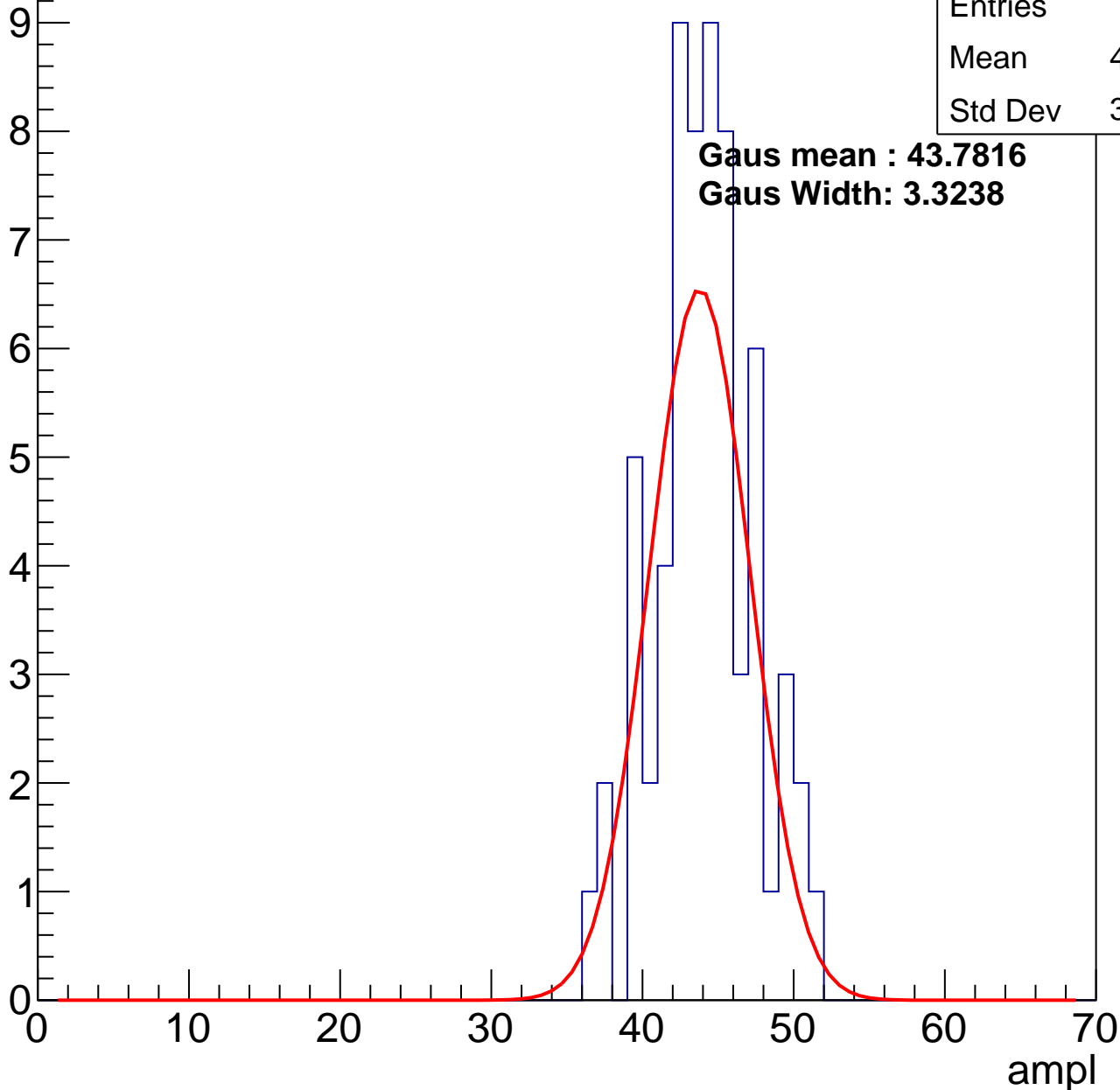
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.64
Std Dev	3.252

Gaus mean : 43.7816

Gaus Width: 3.3238

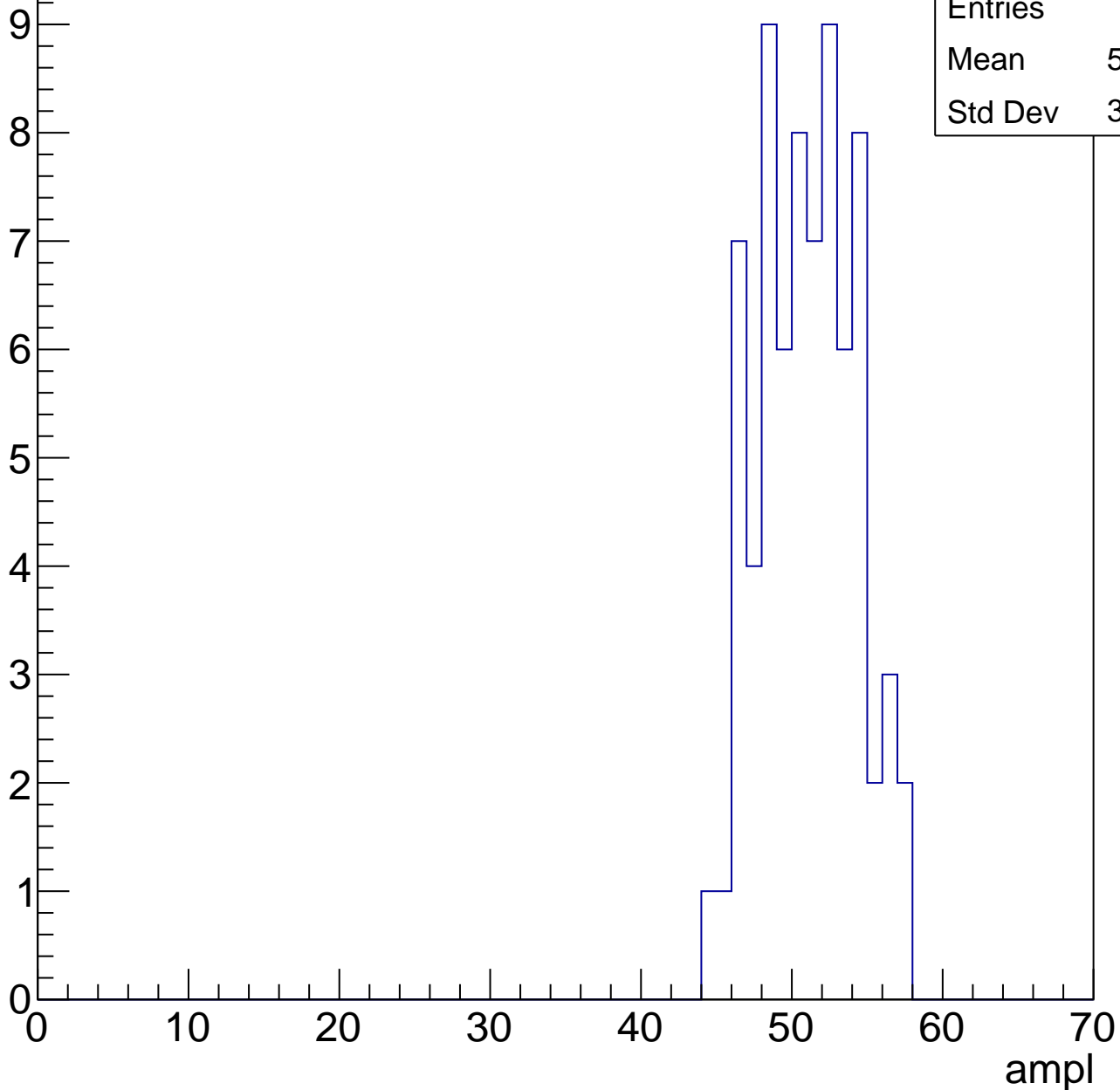


B0L002S, U9-ch46, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

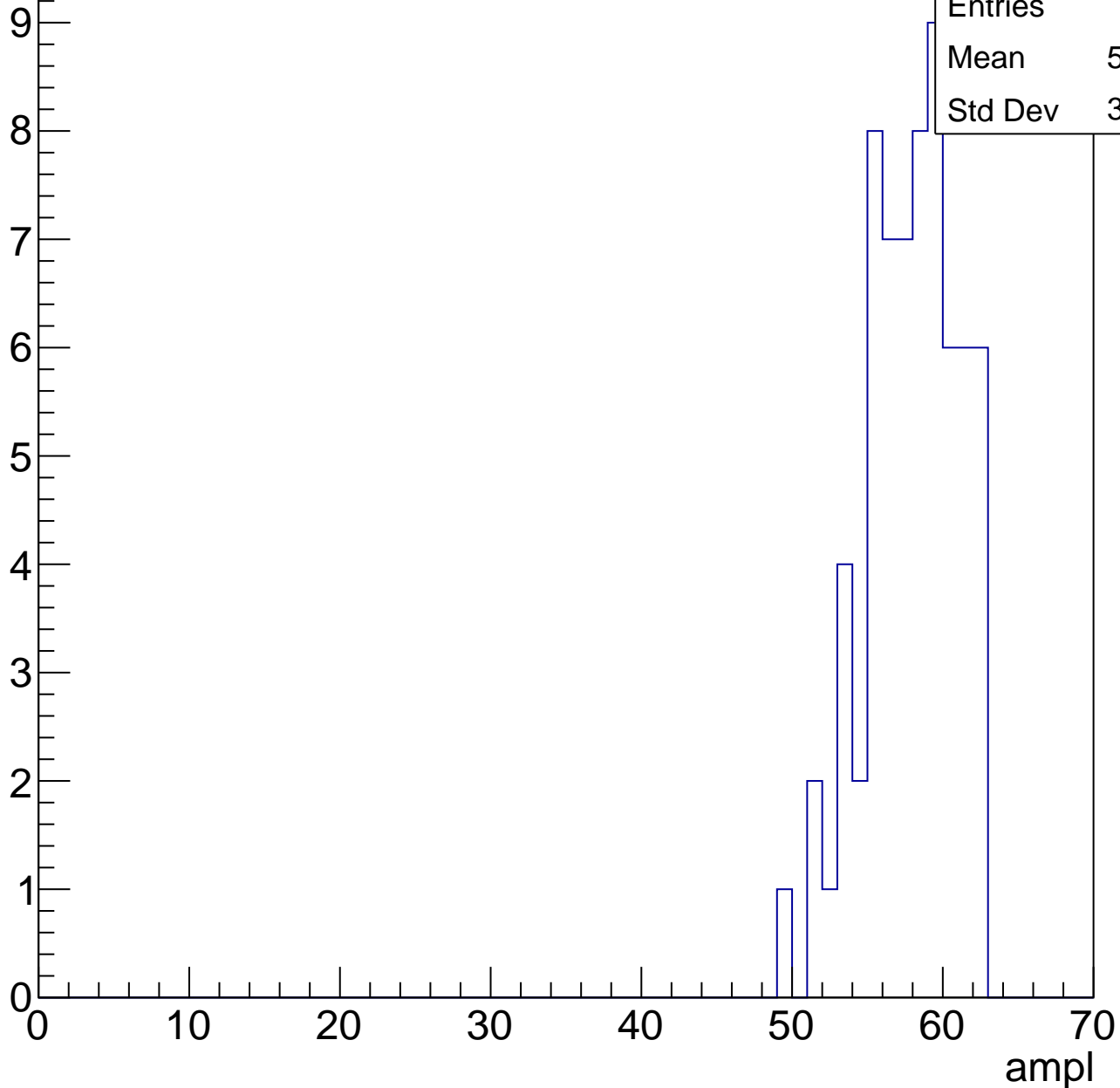
Entries	73
Mean	50.58
Std Dev	3.114



B0L002S, U9-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

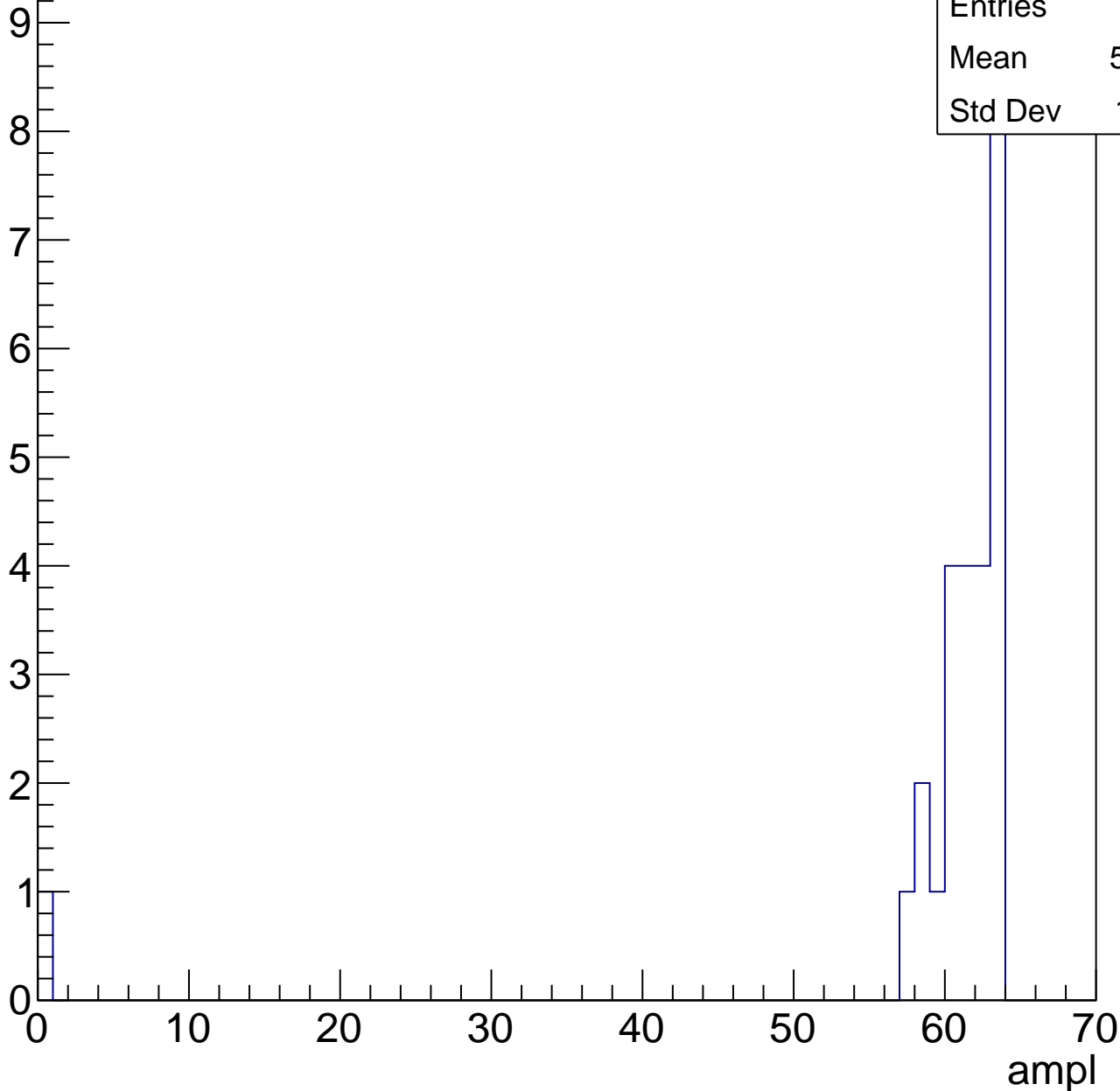


B0L002S, U9-ch46, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	58.88
Std Dev	11.91



B0L002S, U9-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch47, adc0

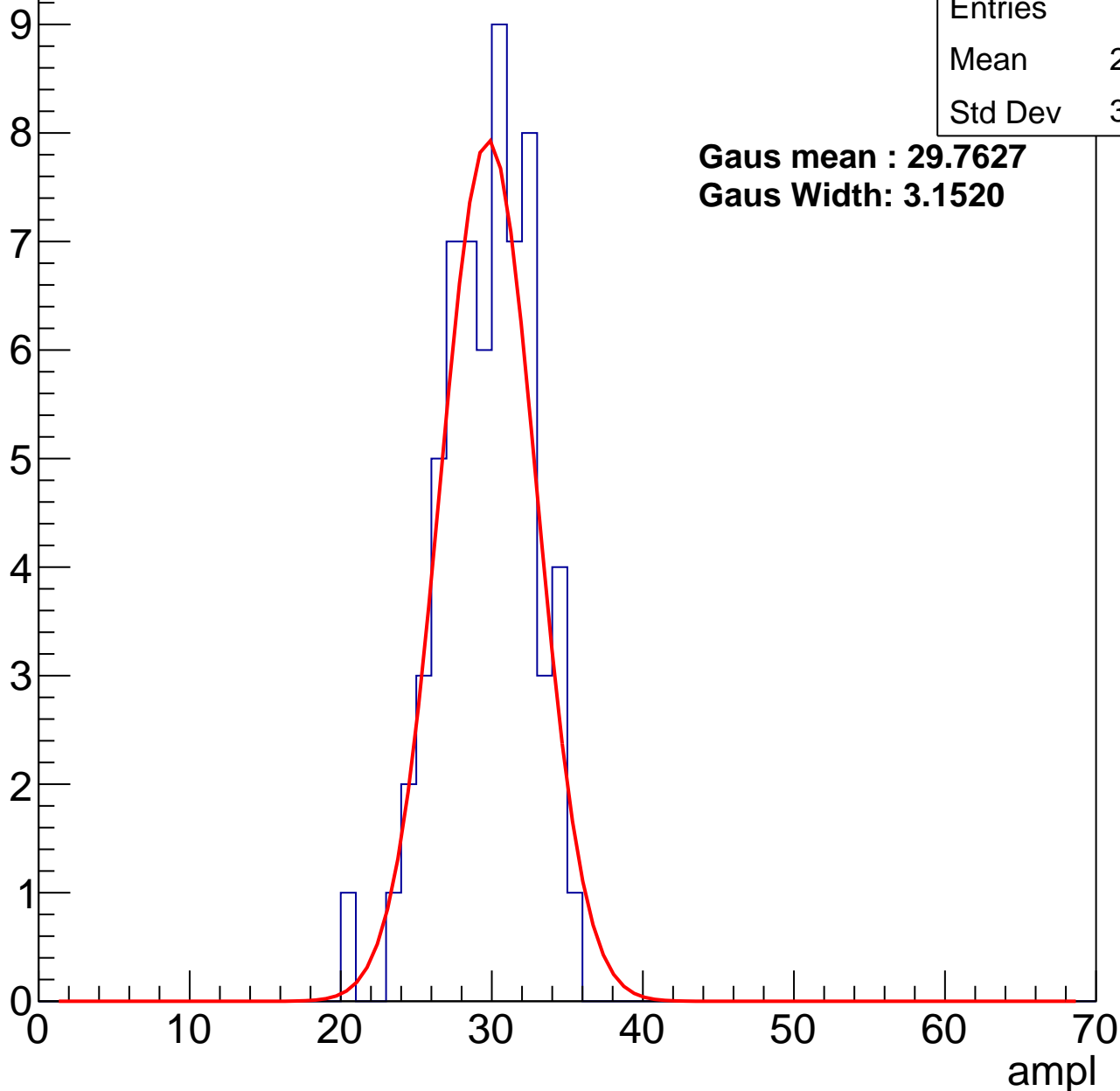
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	29.19
Std Dev	3.025

Gaus mean : 29.7627

Gaus Width: 3.1520

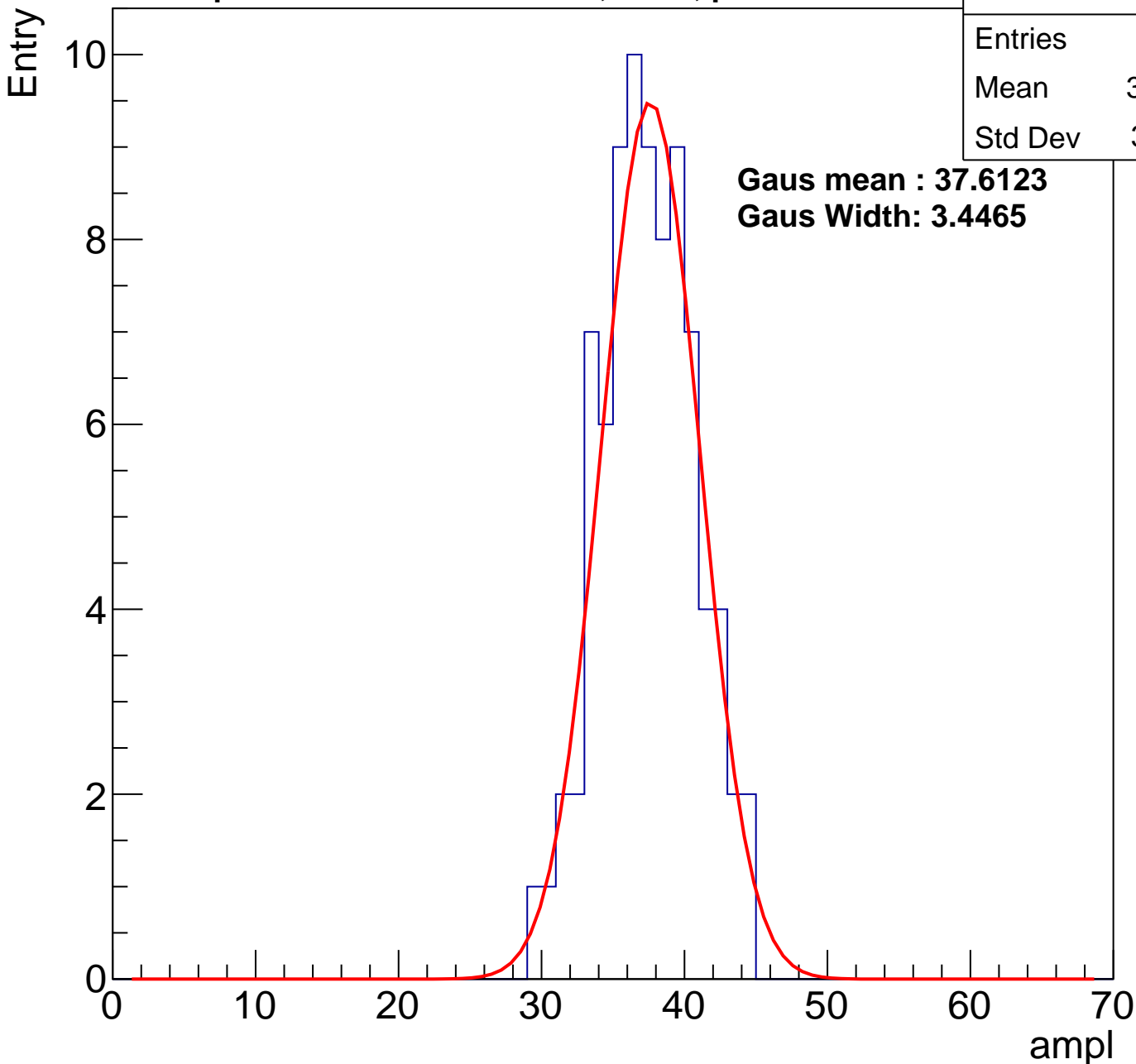


B0L002S, U9-ch47, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	36.98
Std Dev	3.271

Gaus mean : 37.6123
Gaus Width: 3.4465



B0L002S, U9-ch47, adc2

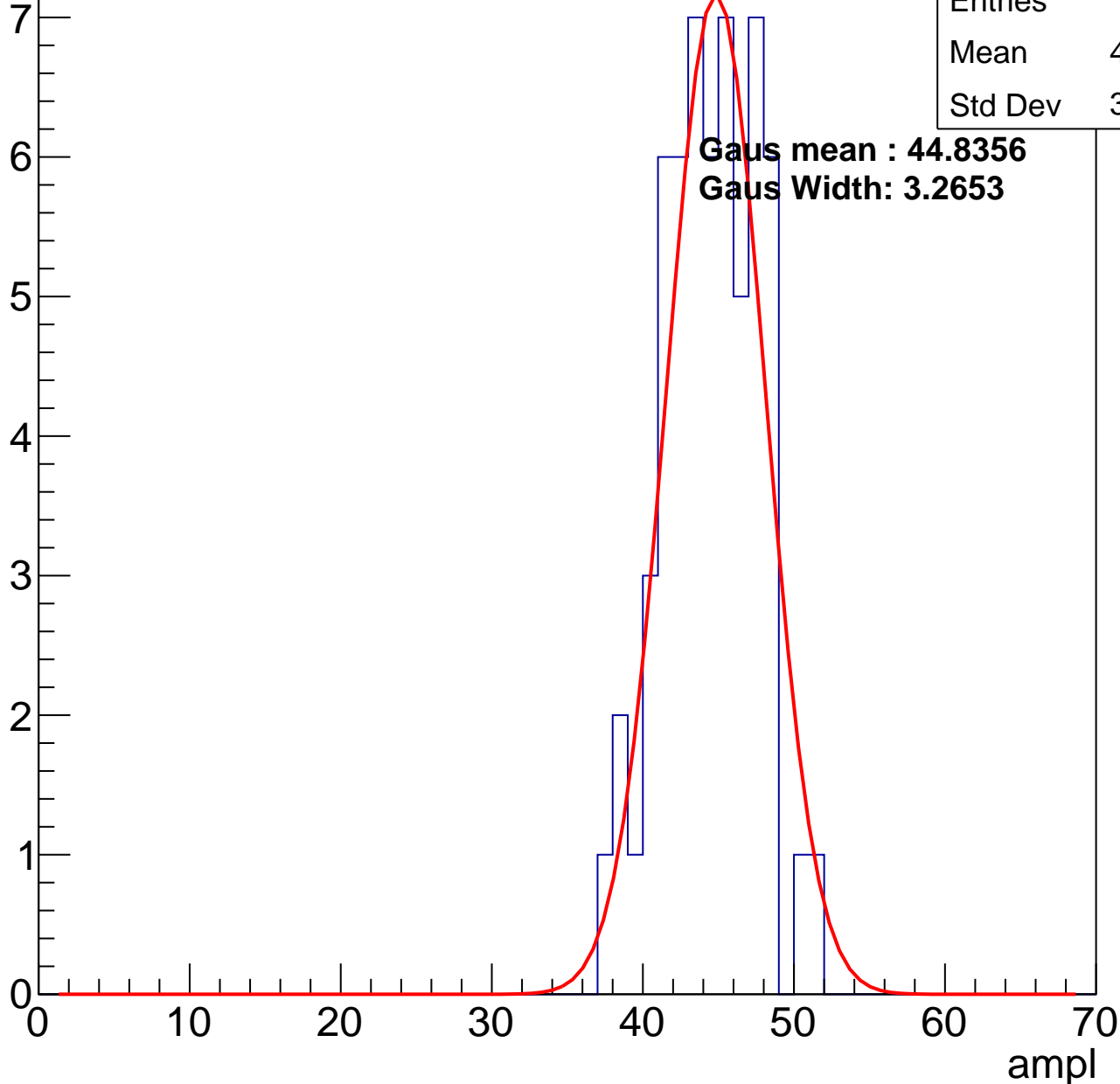
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44.03
Std Dev	3.053

Gaus mean : 44.8356

Gaus Width: 3.2653

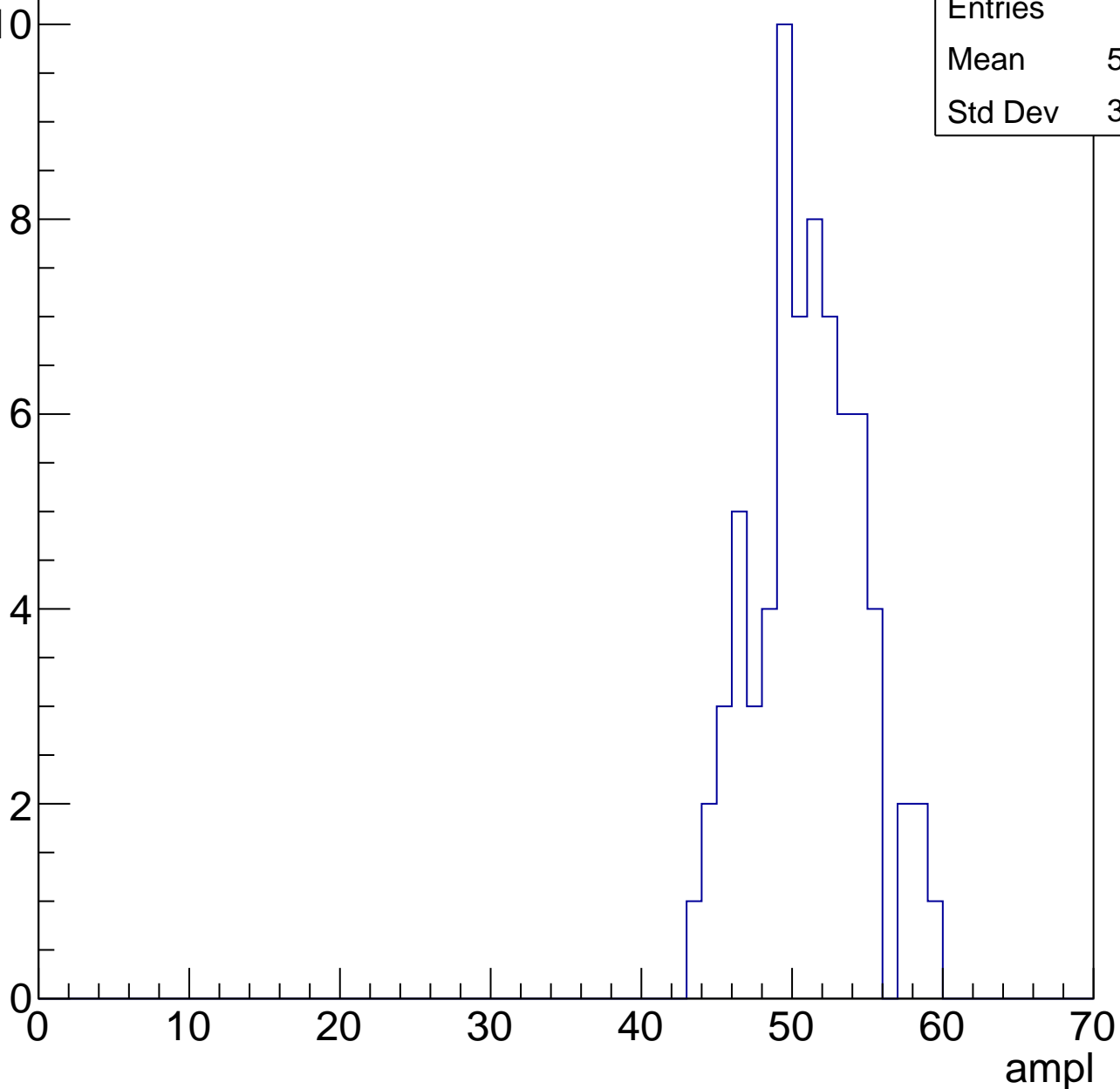


B0L002S, U9-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	50.59
Std Dev	3.555

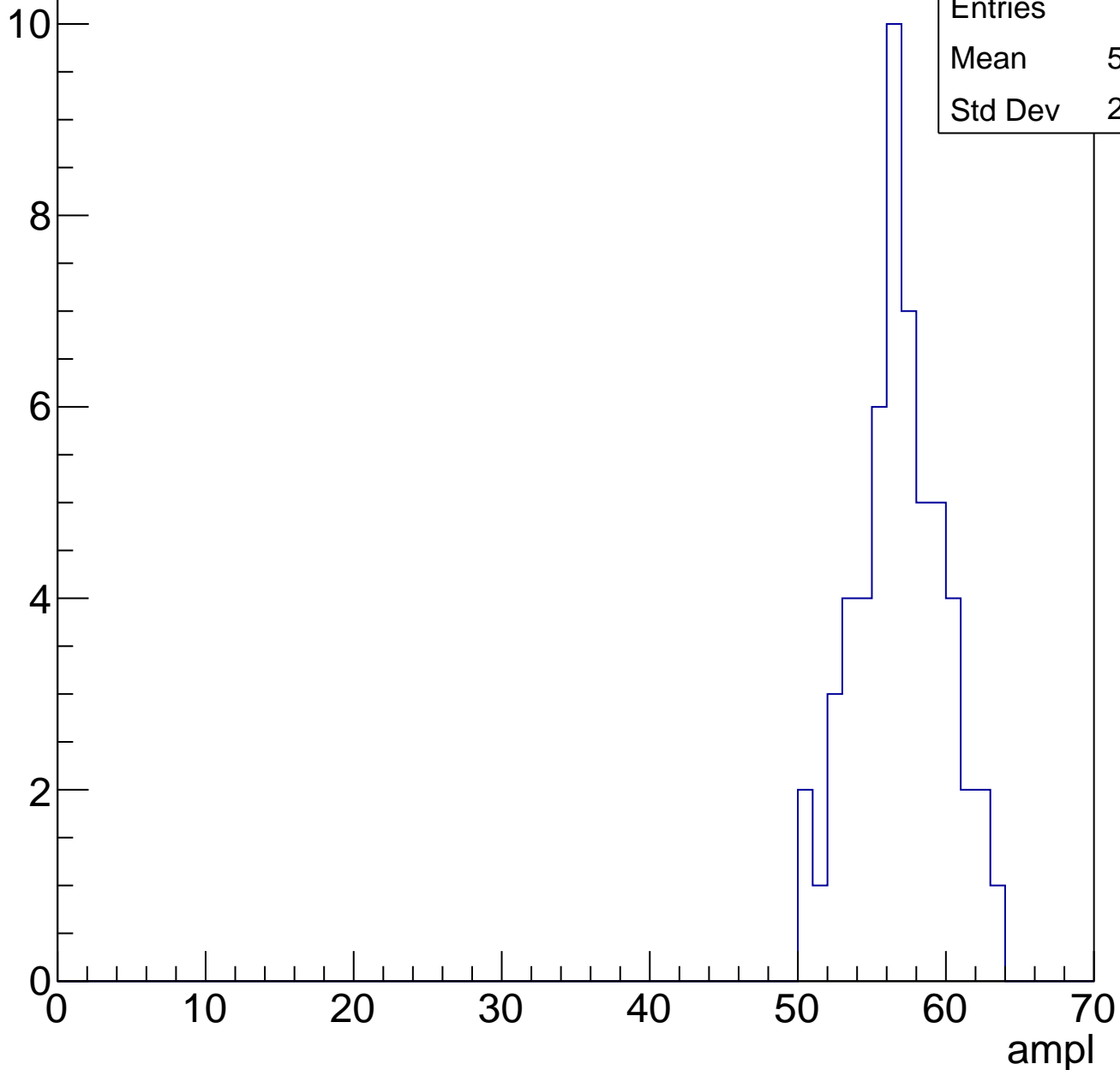


B0L002S, U9-ch47, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	56
Mean	56.39
Std Dev	2.992

Entry



B0L002S, U9-ch47, adc5

calib_packv5_042523_0143.root, FC#8, port C1

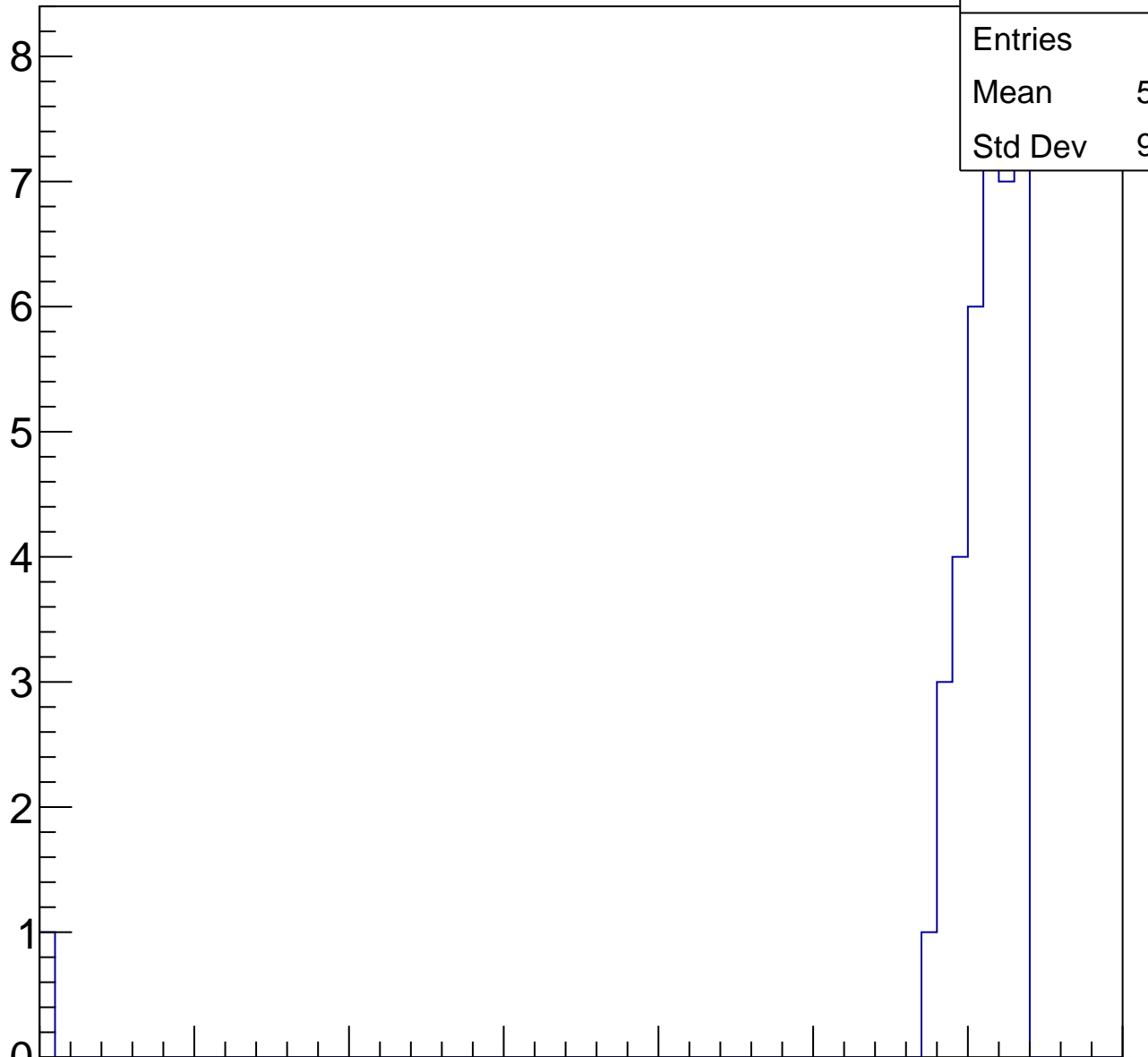
Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	59.29
Std Dev	9.886

ampl

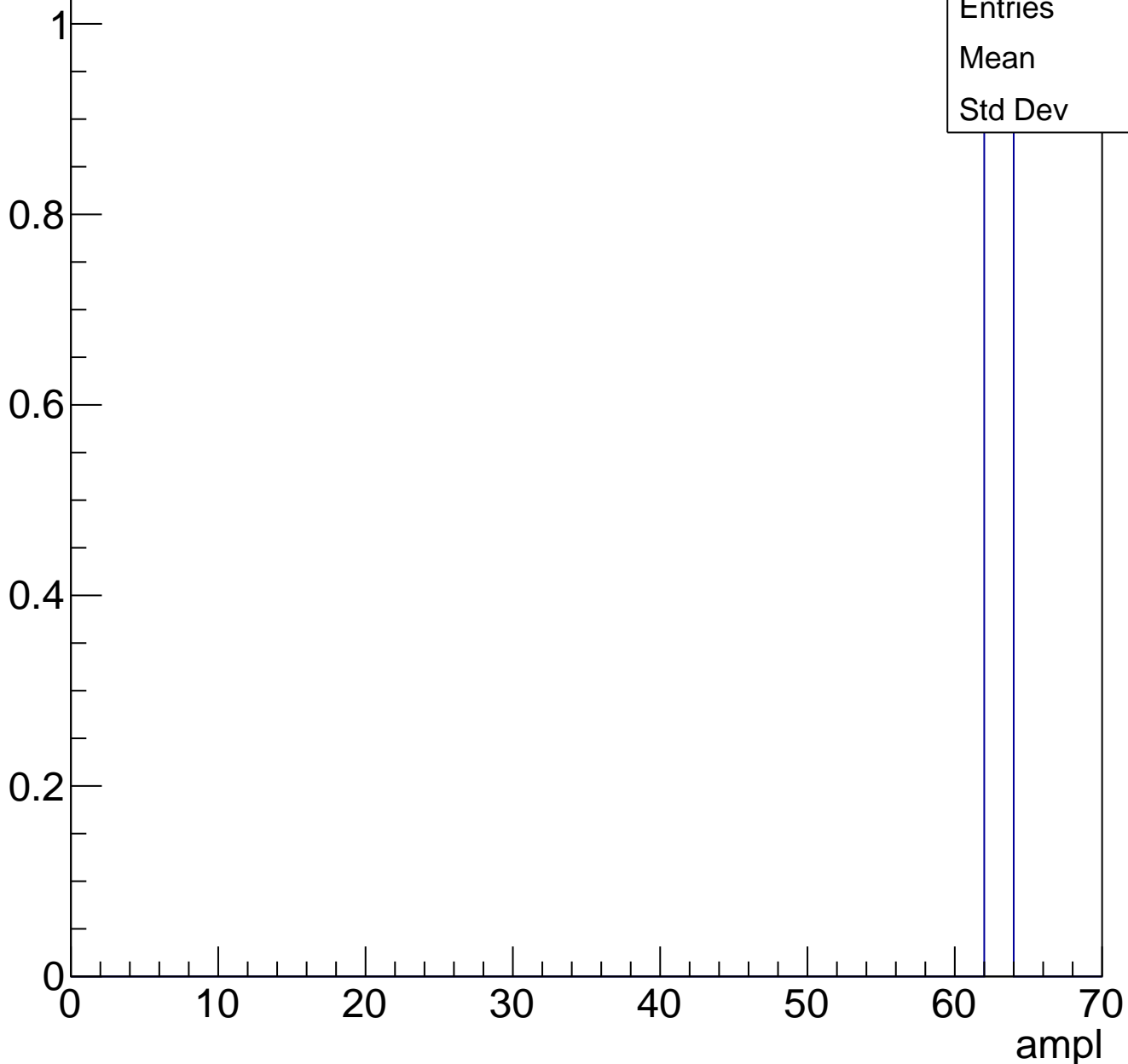
0 10 20 30 40 50 60 70



B0L002S, U9-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch48, adc0

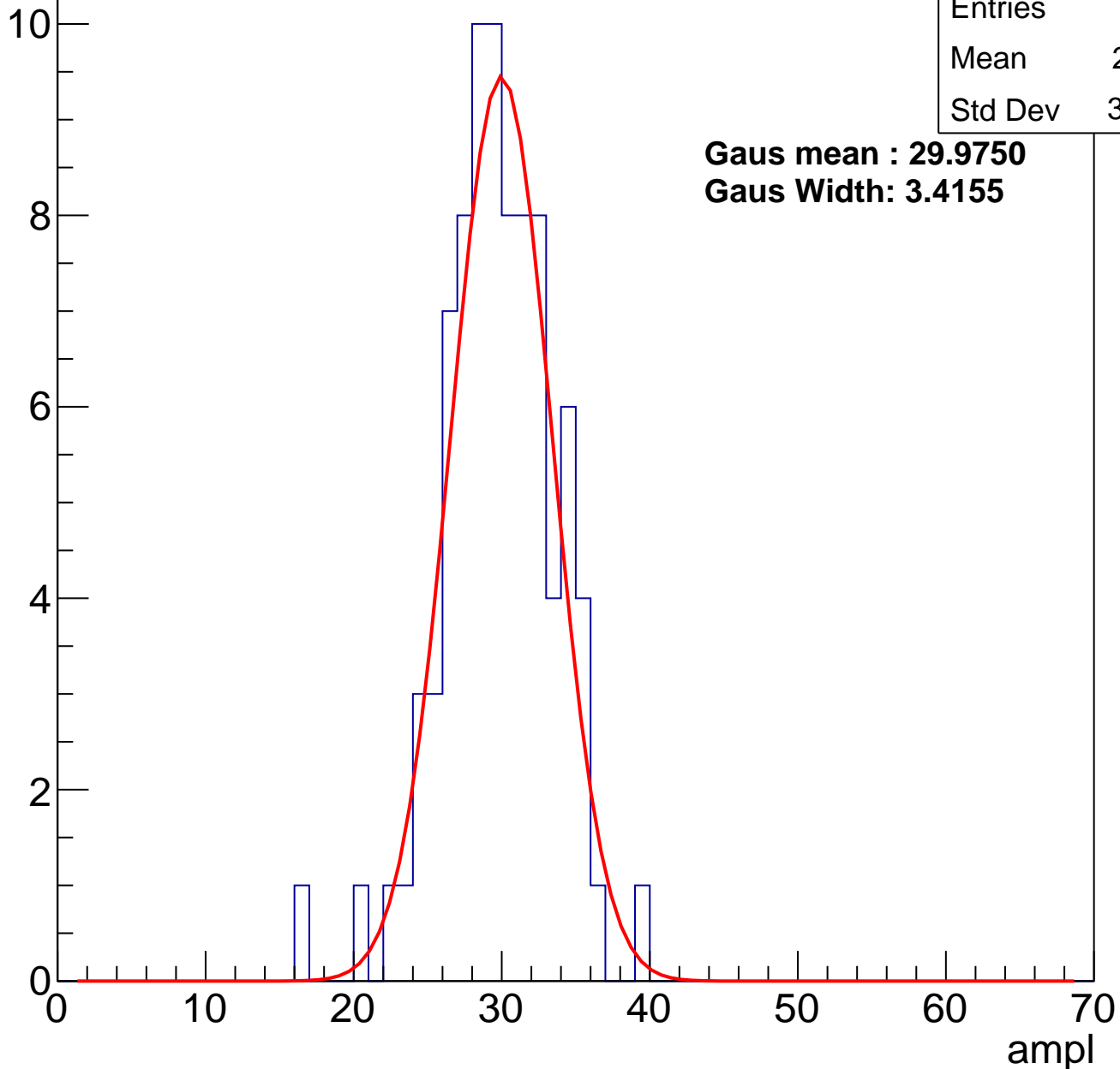
calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	29.31
Std Dev	3.716

Gaus mean : 29.9750

Gaus Width: 3.4155

Entry



B0L002S, U9-ch48, adc1

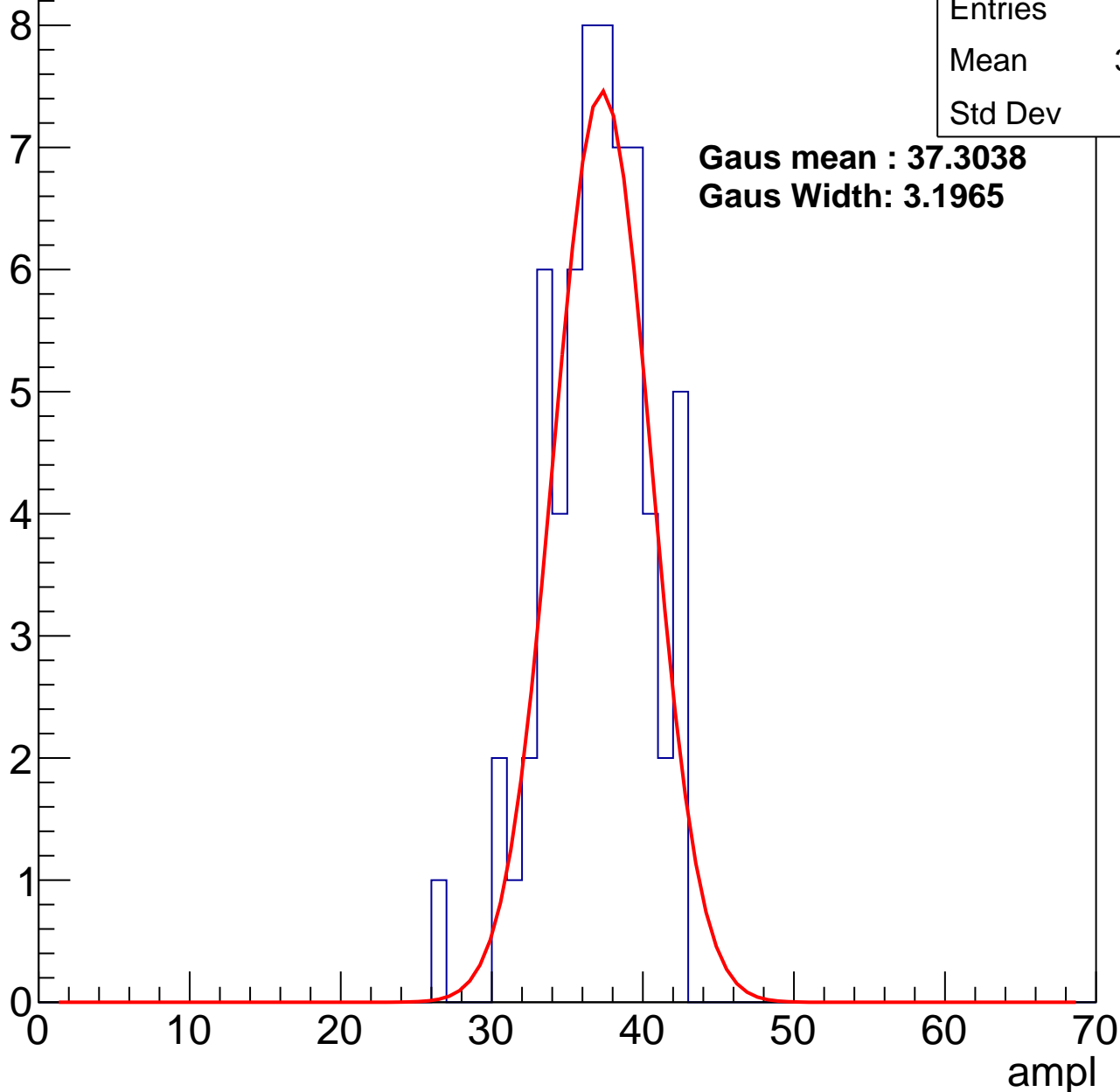
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.51
Std Dev	3.29

Gaus mean : 37.3038

Gaus Width: 3.1965



B0L002S, U9-ch48, adc2

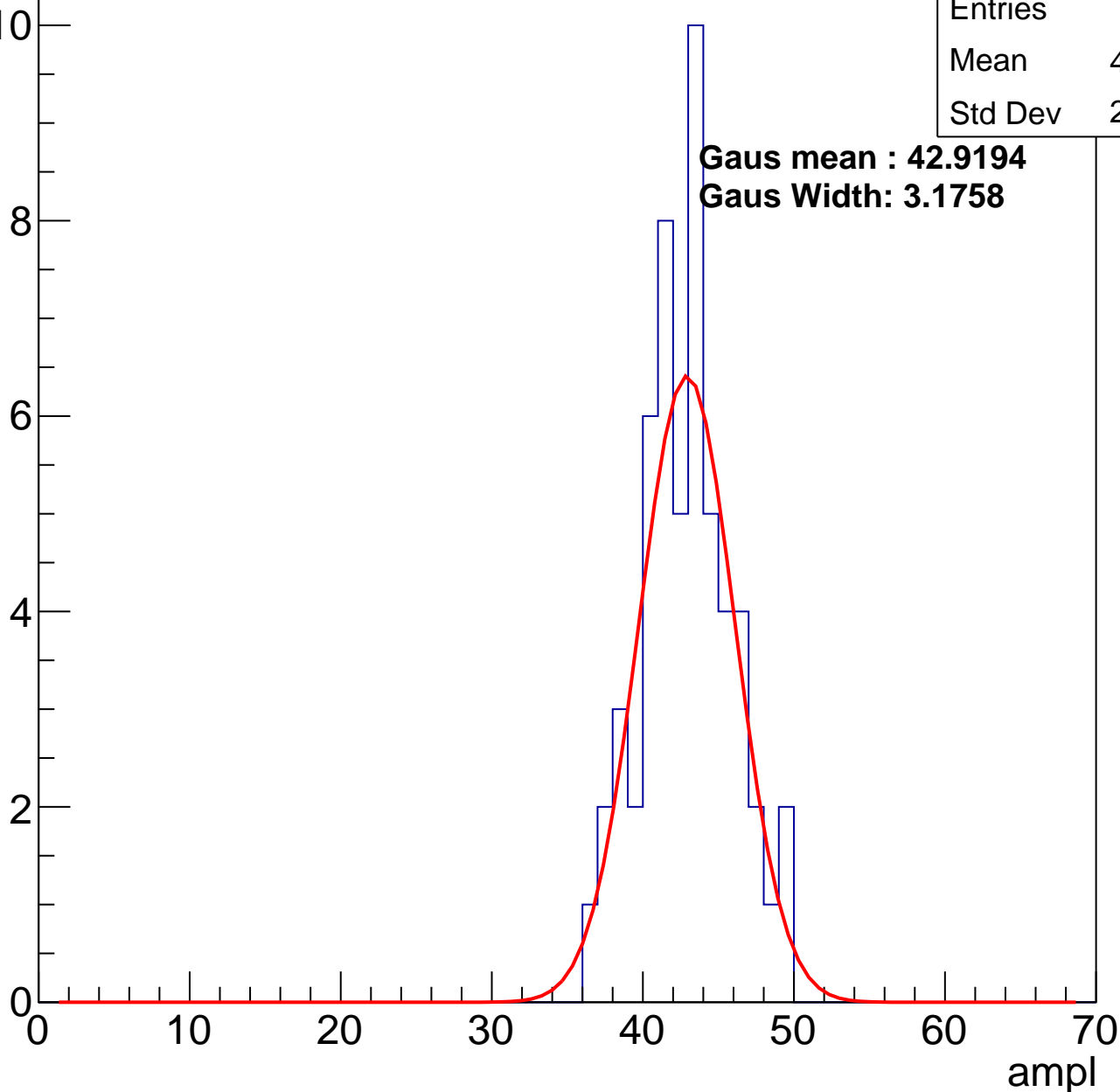
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	42.44
Std Dev	2.977

Gaus mean : 42.9194

Gaus Width: 3.1758

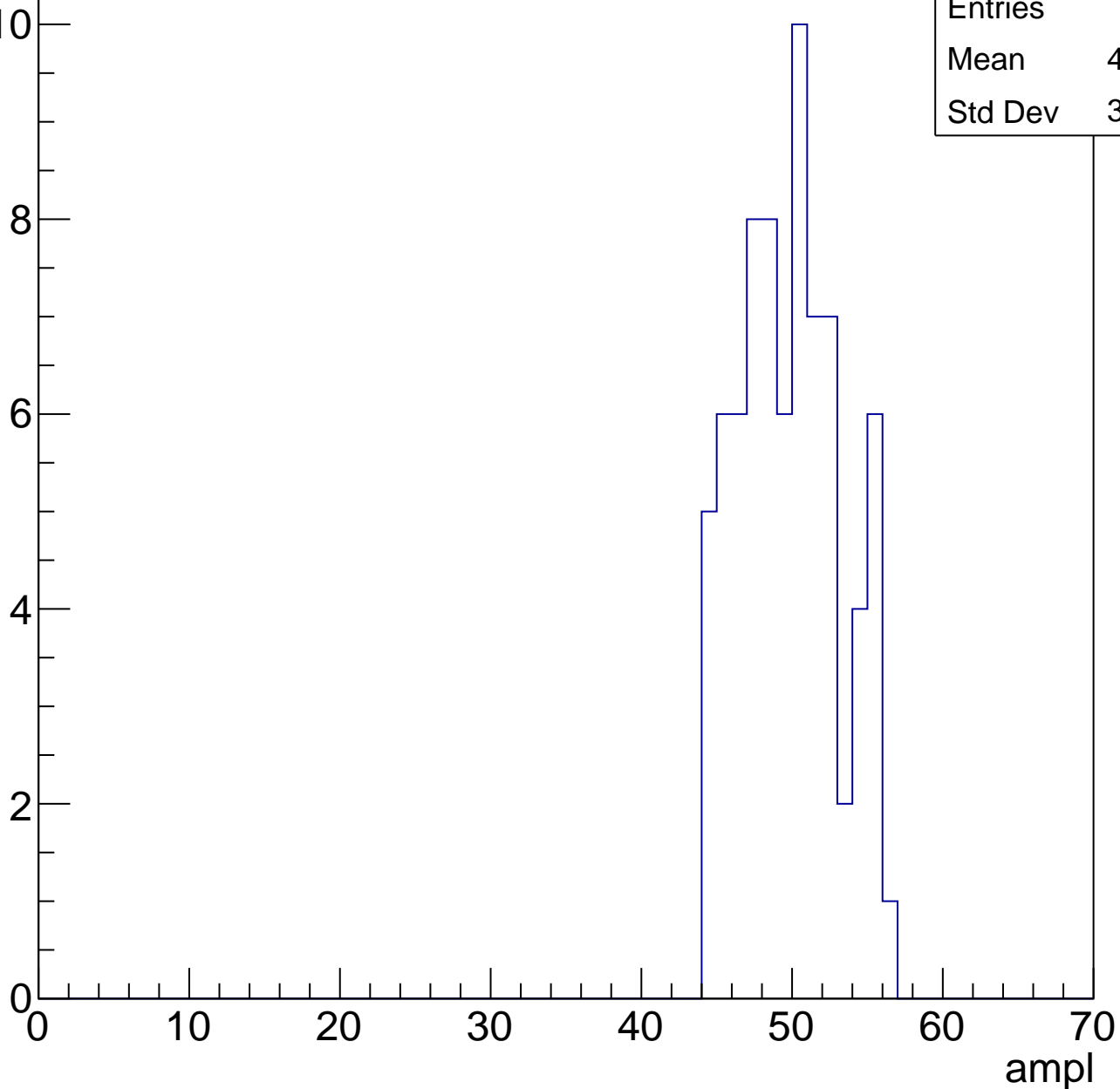


B0L002S, U9-ch48, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	49.33
Std Dev	3.254

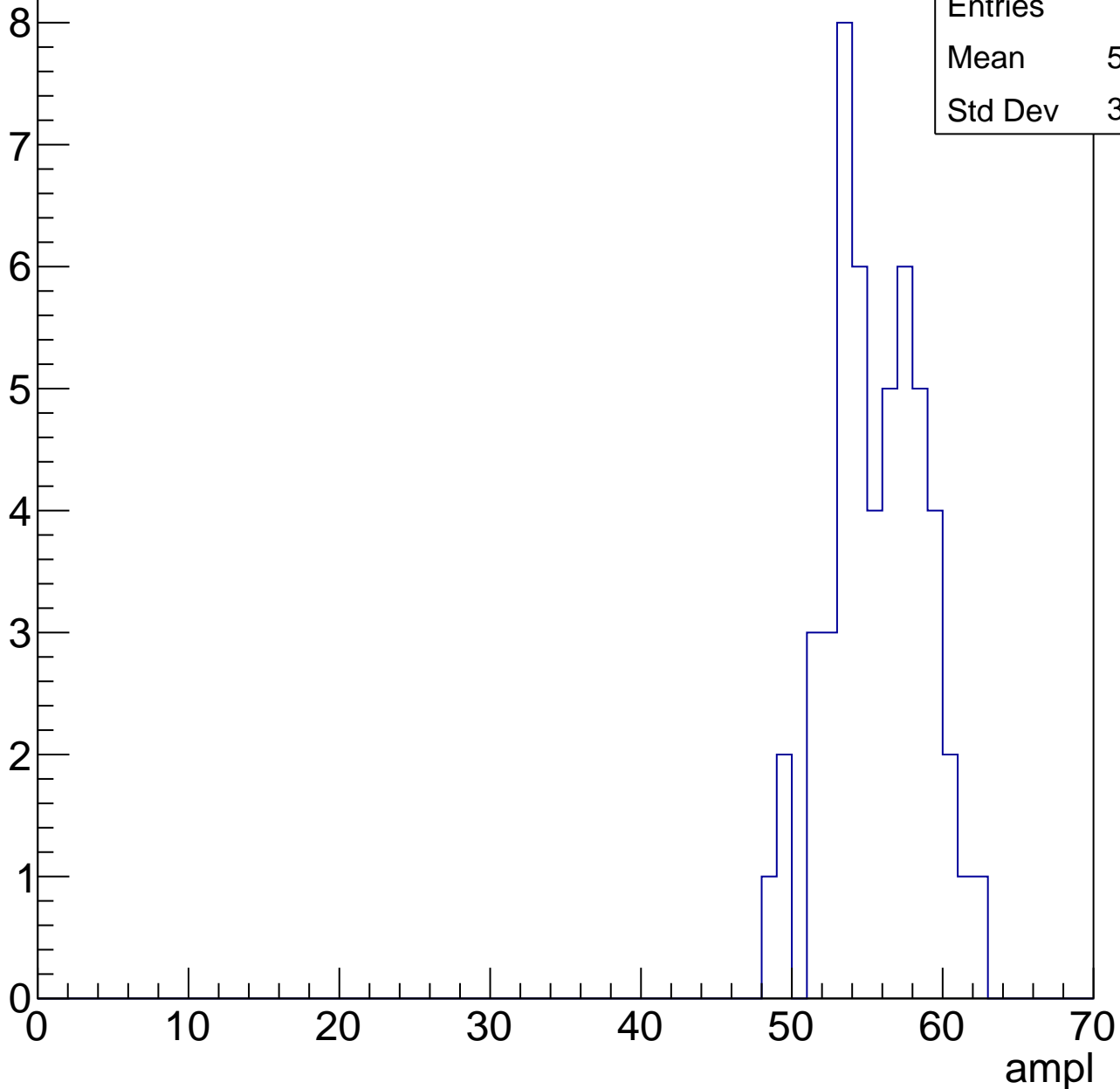


B0L002S, U9-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.18
Std Dev	3.148

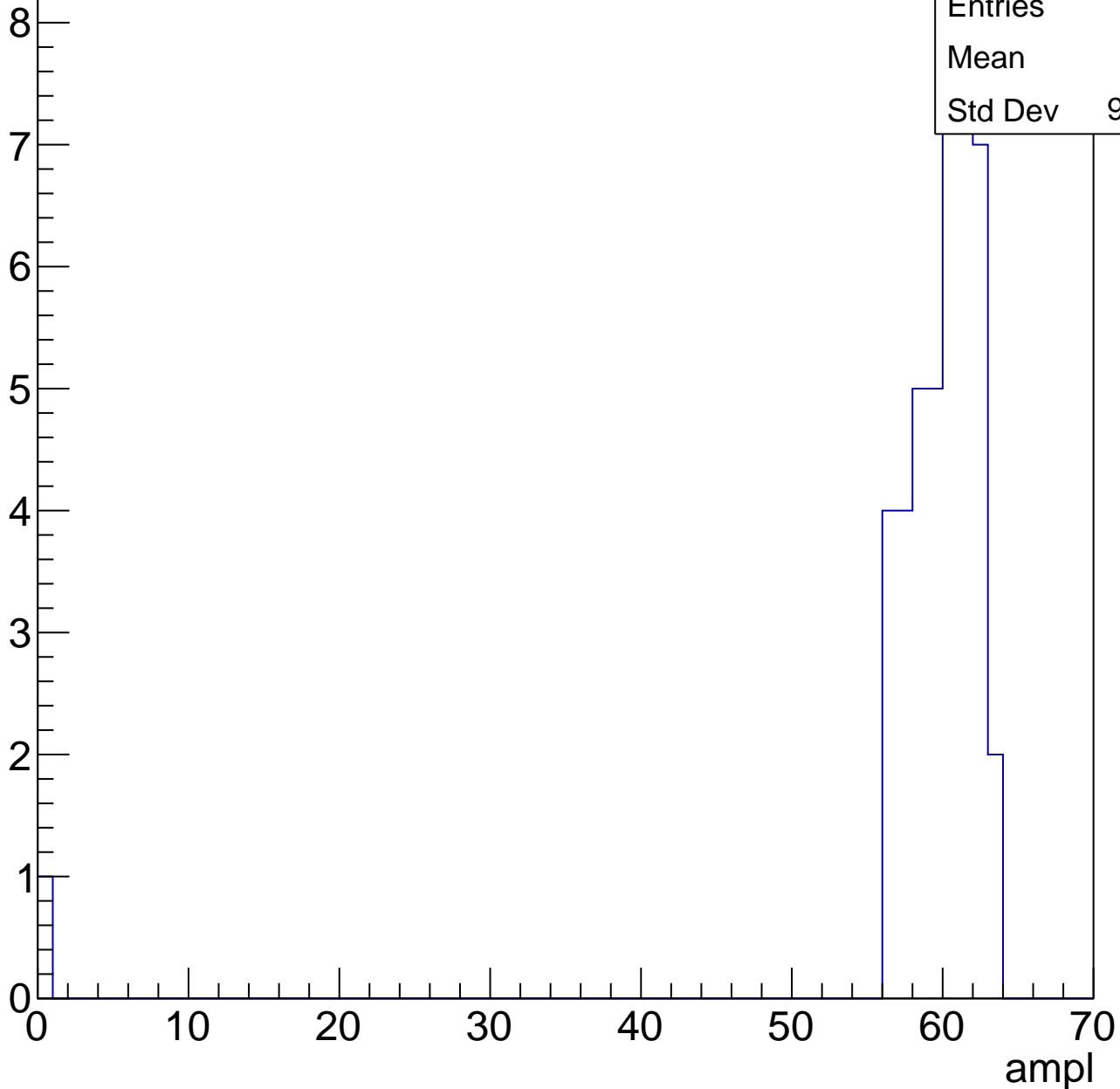


B0L002S, U9-ch48, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	58.3
Std Dev	9.109

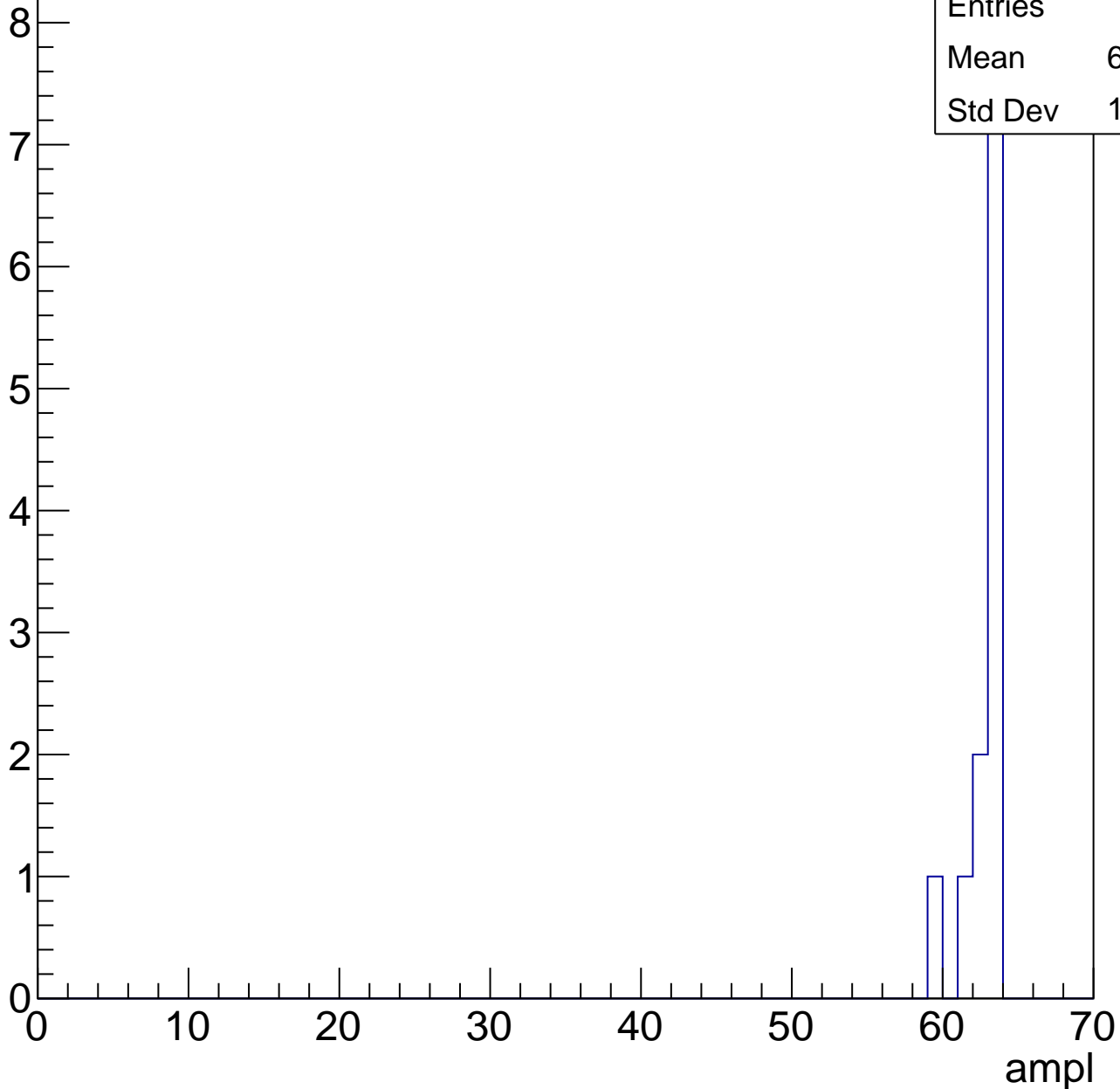


B0L002S, U9-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	62.33
Std Dev	1.179



B0L002S, U9-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch49, adc0

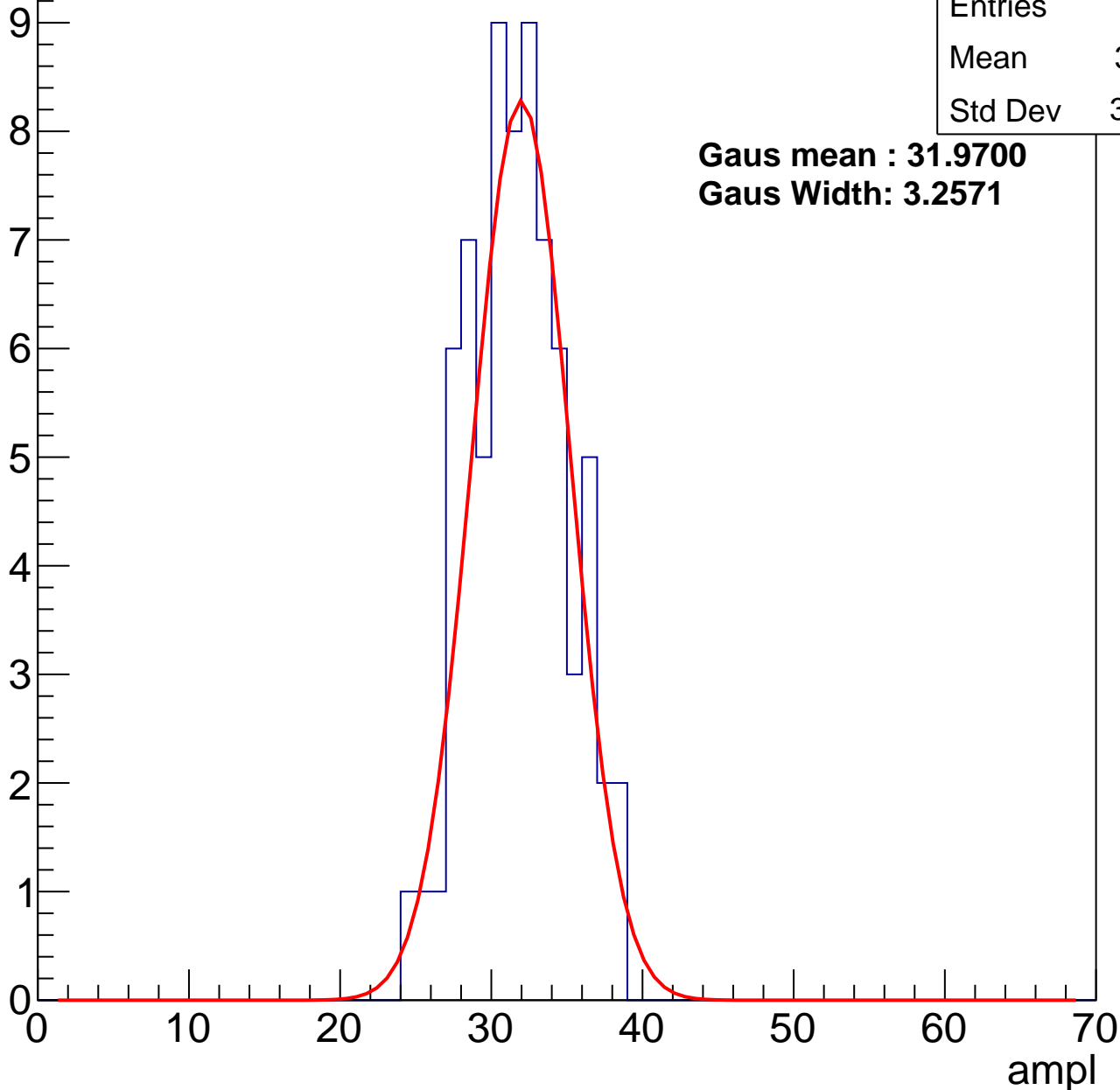
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	31.31
Std Dev	3.174

Gaus mean : 31.9700

Gaus Width: 3.2571



B0L002S, U9-ch49, adc1

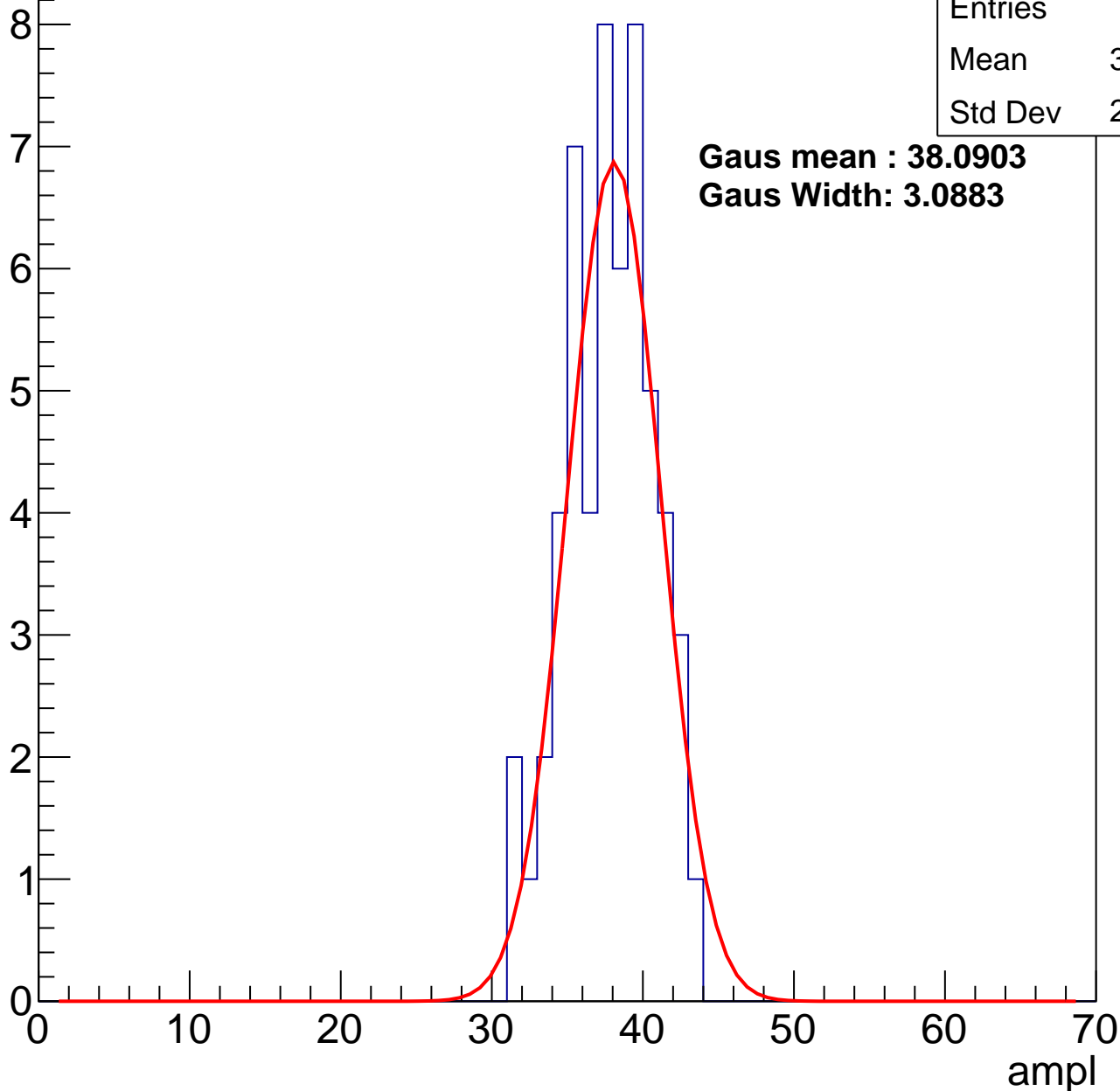
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	37.35
Std Dev	2.855

Gaus mean : 38.0903

Gaus Width: 3.0883



B0L002S, U9-ch49, adc2

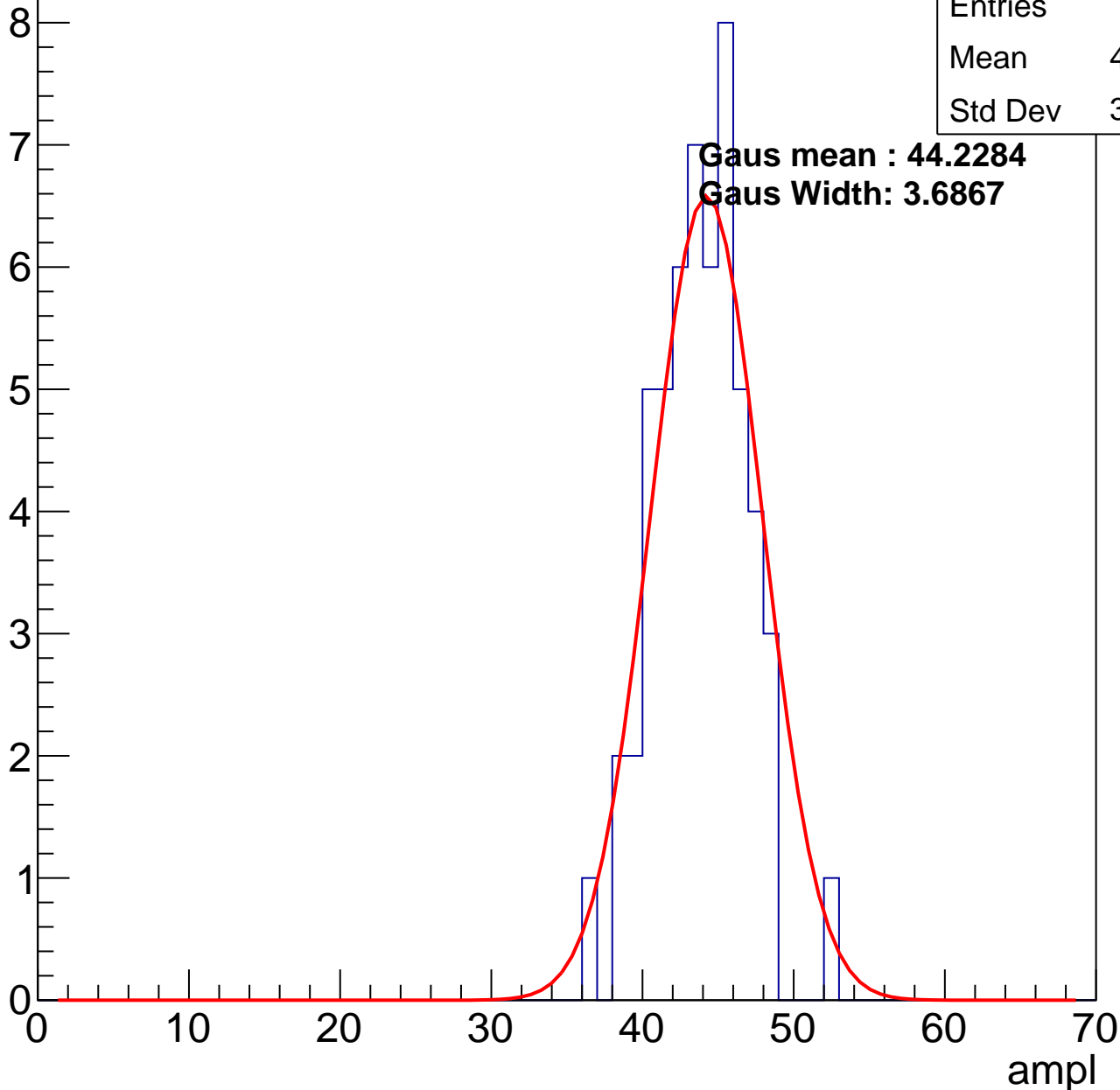
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	43.38
Std Dev	3.006

Gaus mean : 44.2284

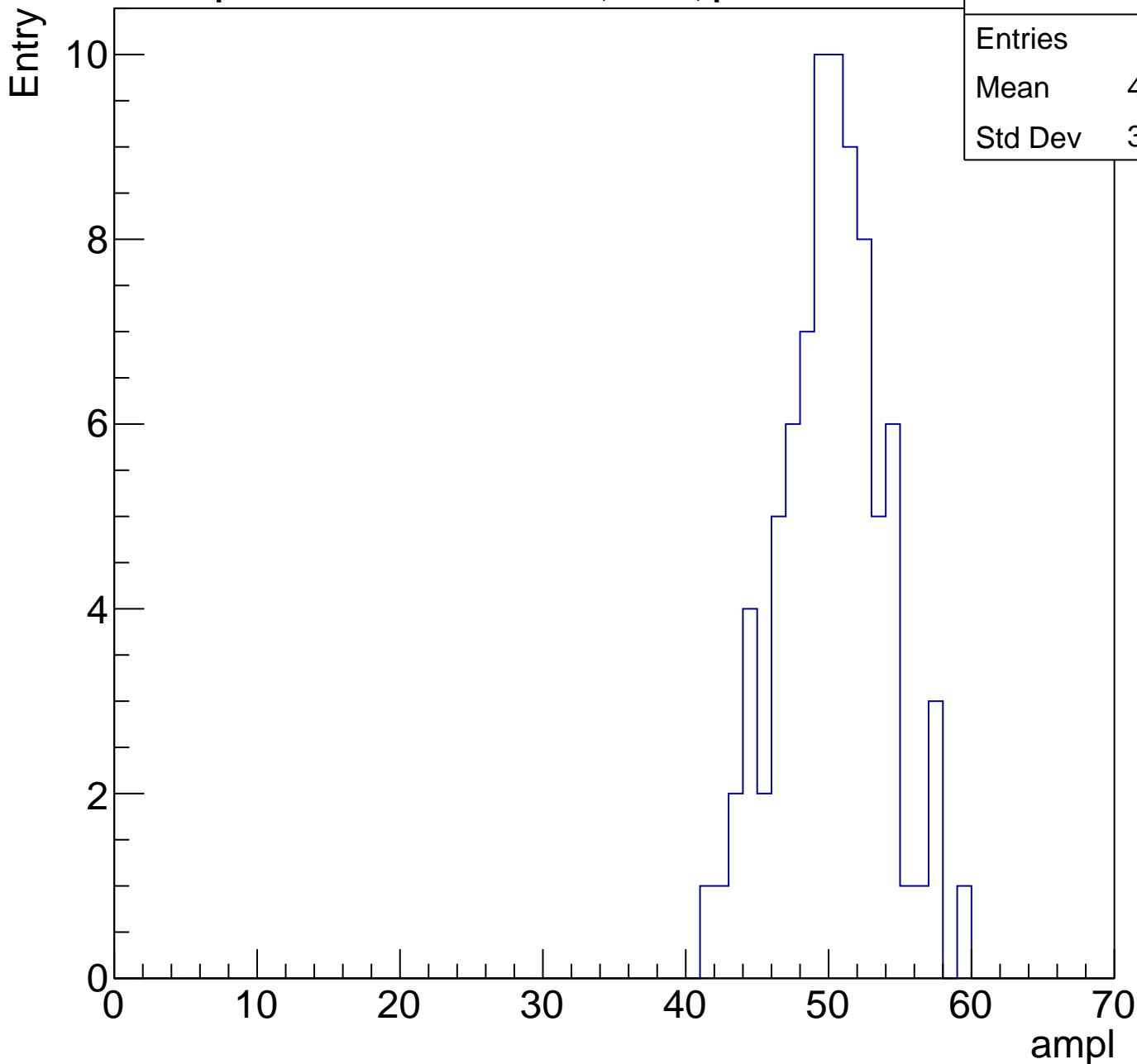
Gaus Width: 3.6867



B0L002S, U9-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

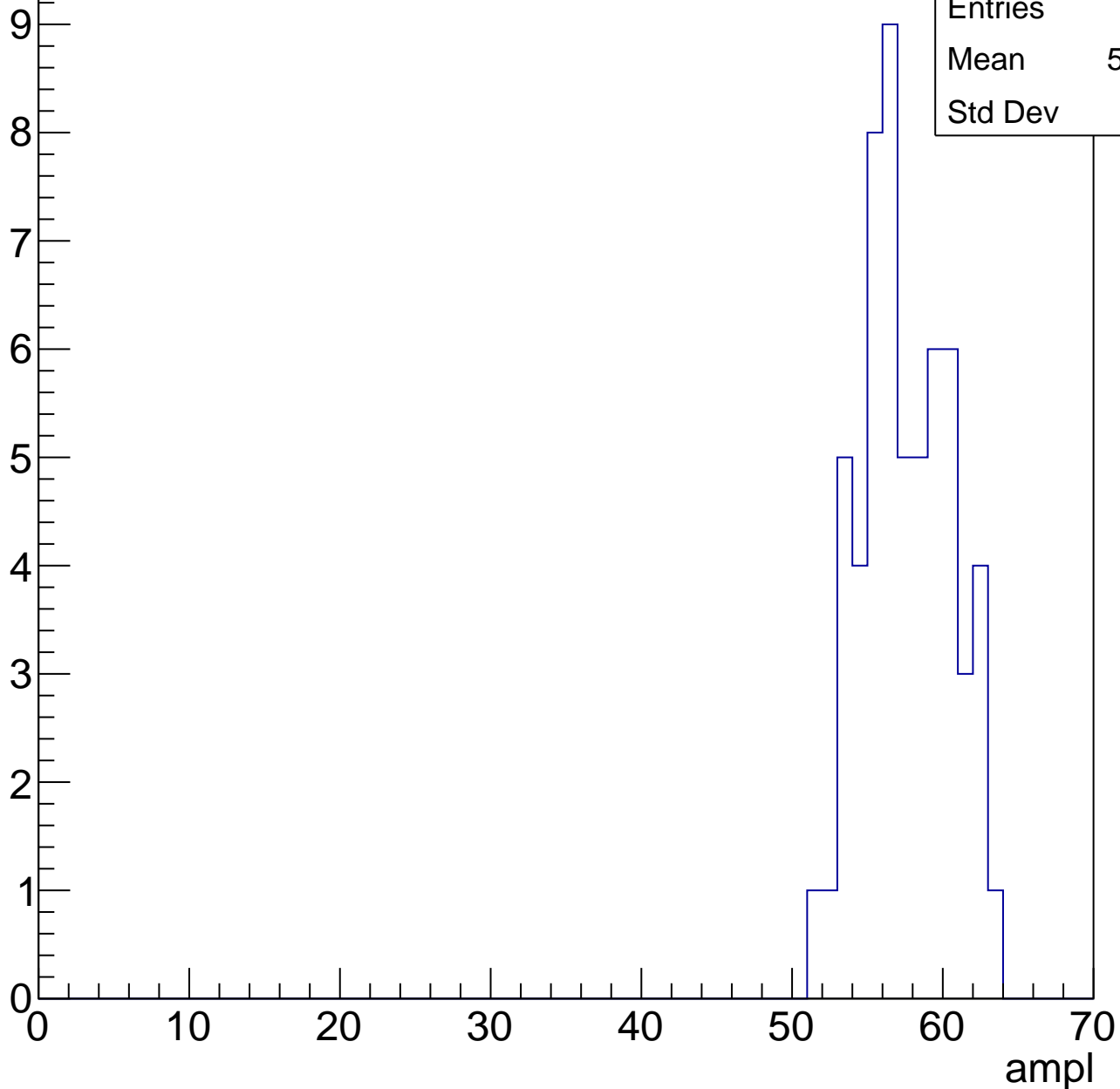
Entries	82
Mean	49.73
Std Dev	3.619



B0L002S, U9-ch49, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



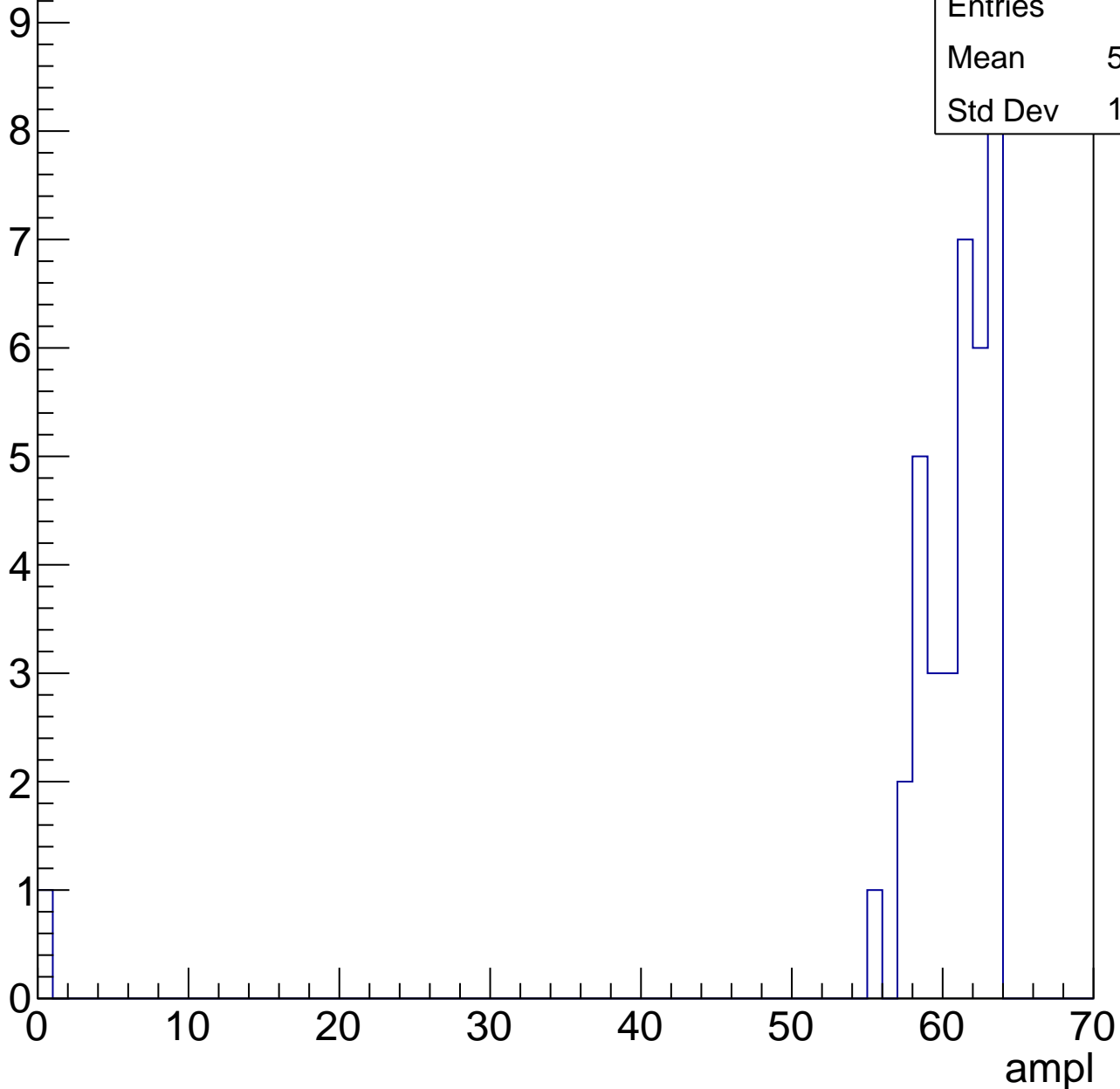
Entries	58
Mean	57.09
Std Dev	2.89

B0L002S, U9-ch49, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.97
Std Dev	10.05



B0L002S, U9-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	60
Std Dev	0

B0L002S, U9-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch50, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	30.28
Std Dev	3.331

Gaus mean : 30.5363

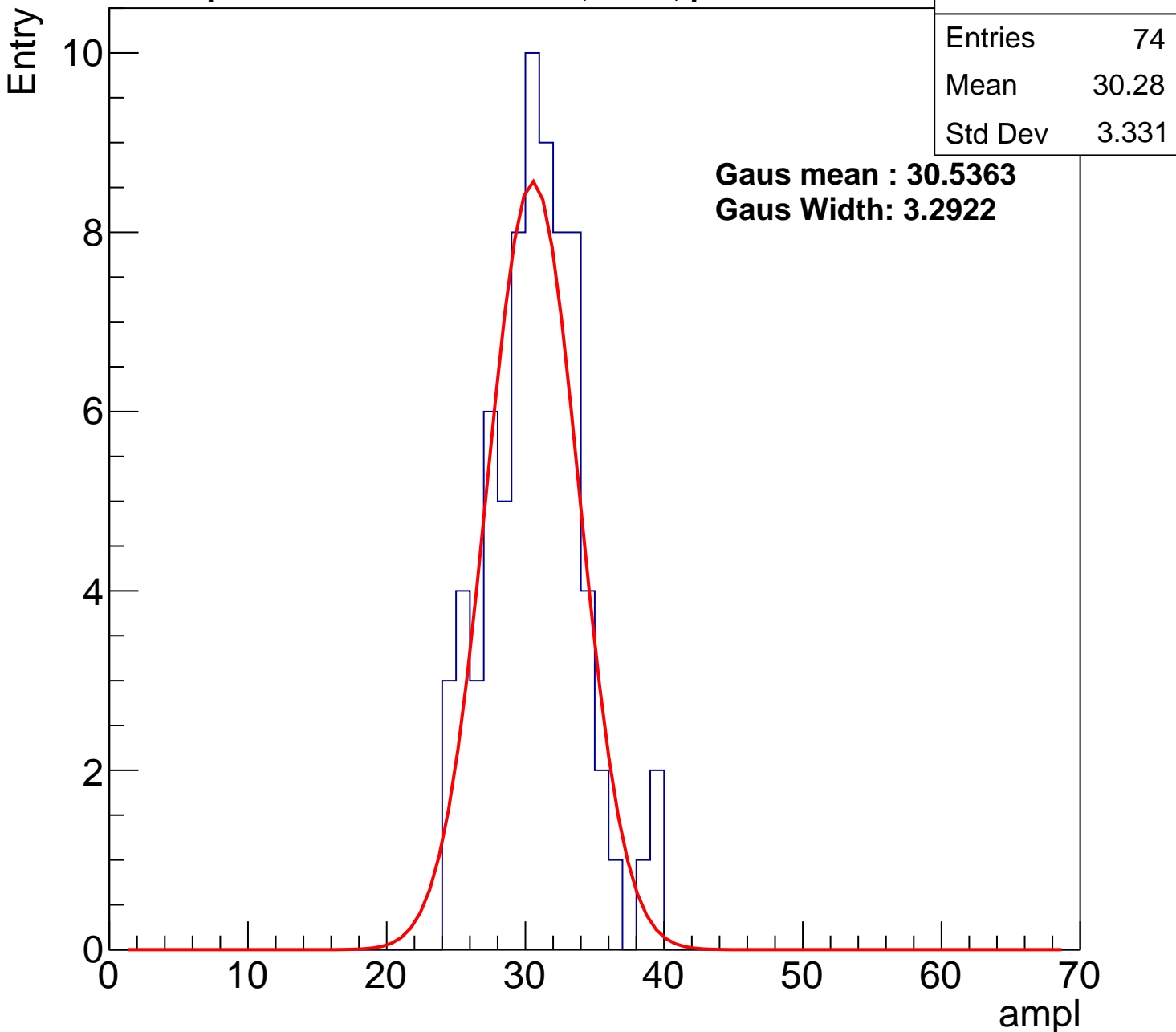
Gaus Width: 3.2922

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch50, adc1

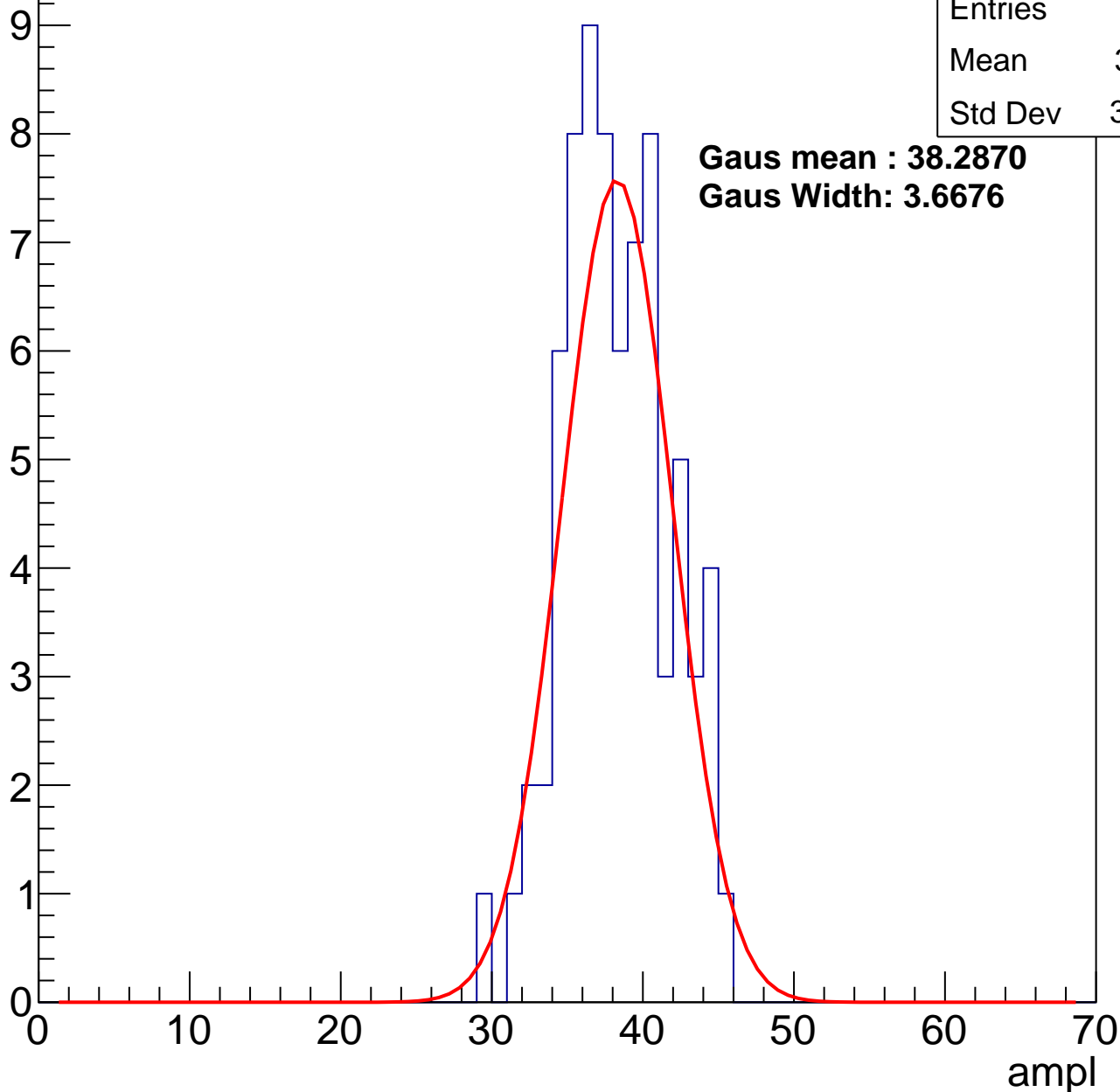
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.81
Std Dev	3.439

Gaus mean : 38.2870

Gaus Width: 3.6676



B0L002S, U9-ch50, adc2

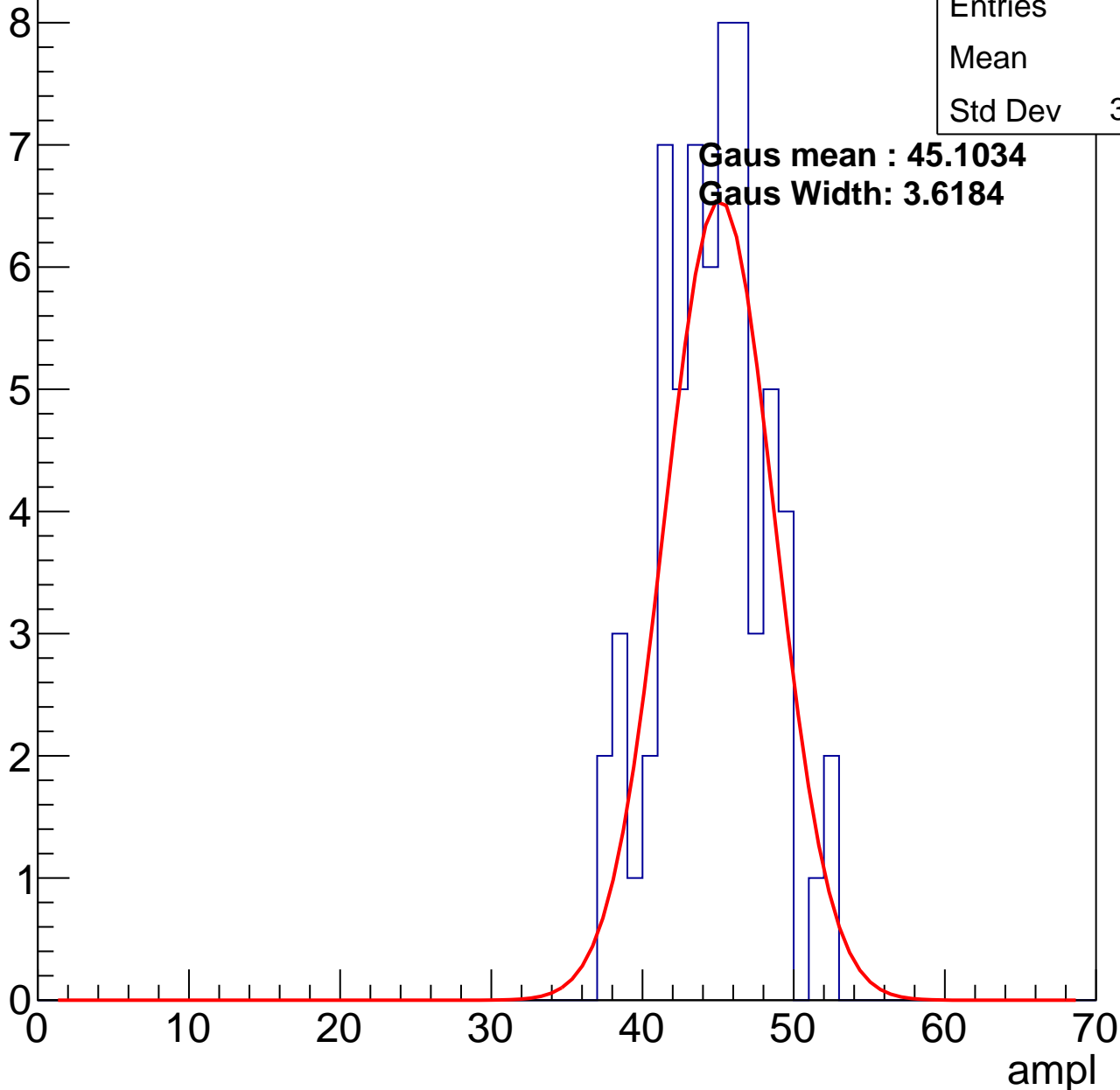
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.2
Std Dev	3.478

Gaus mean : 45.1034

Gaus Width: 3.6184

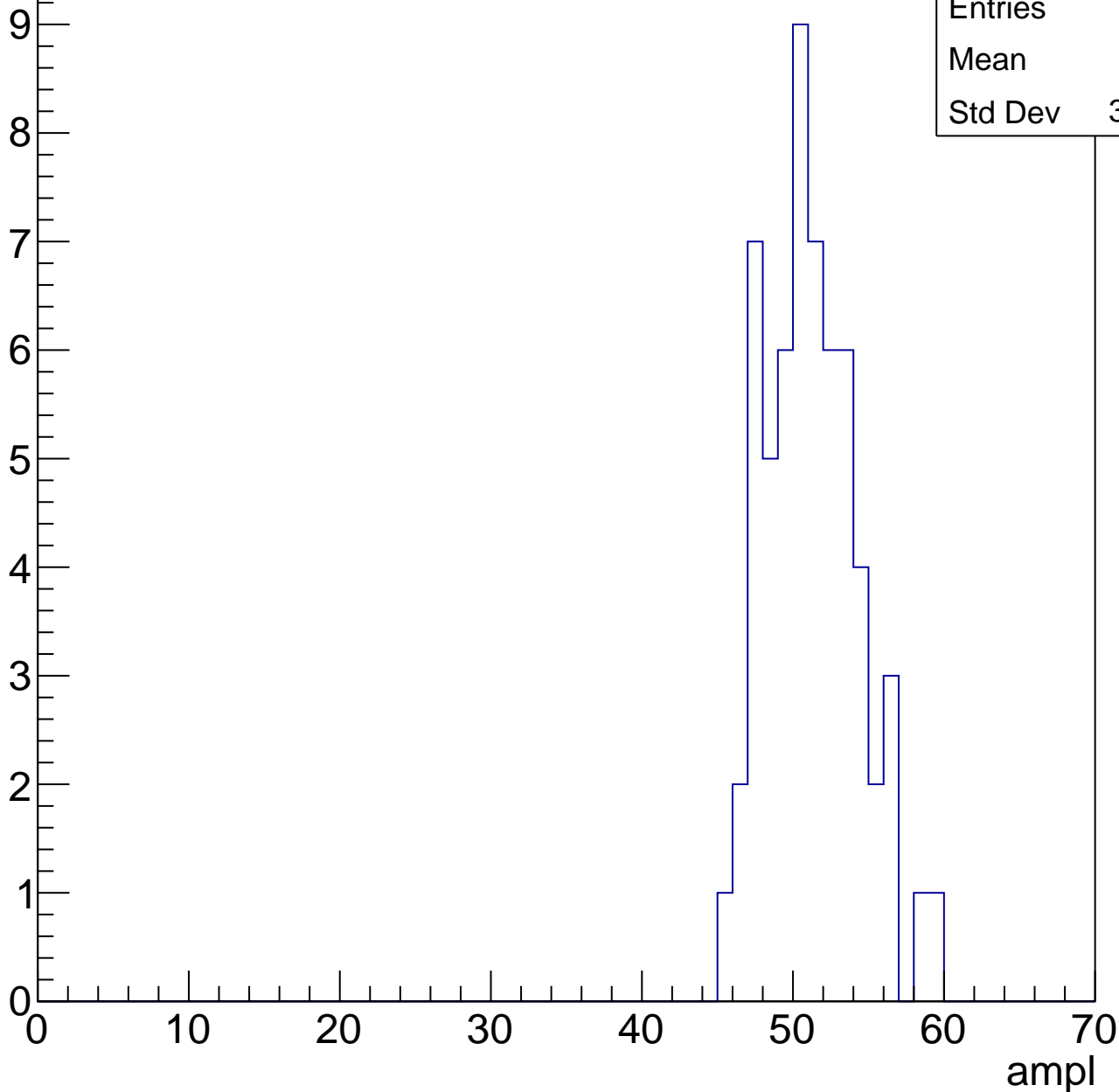


B0L002S, U9-ch50, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.8
Std Dev	3.054



B0L002S, U9-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

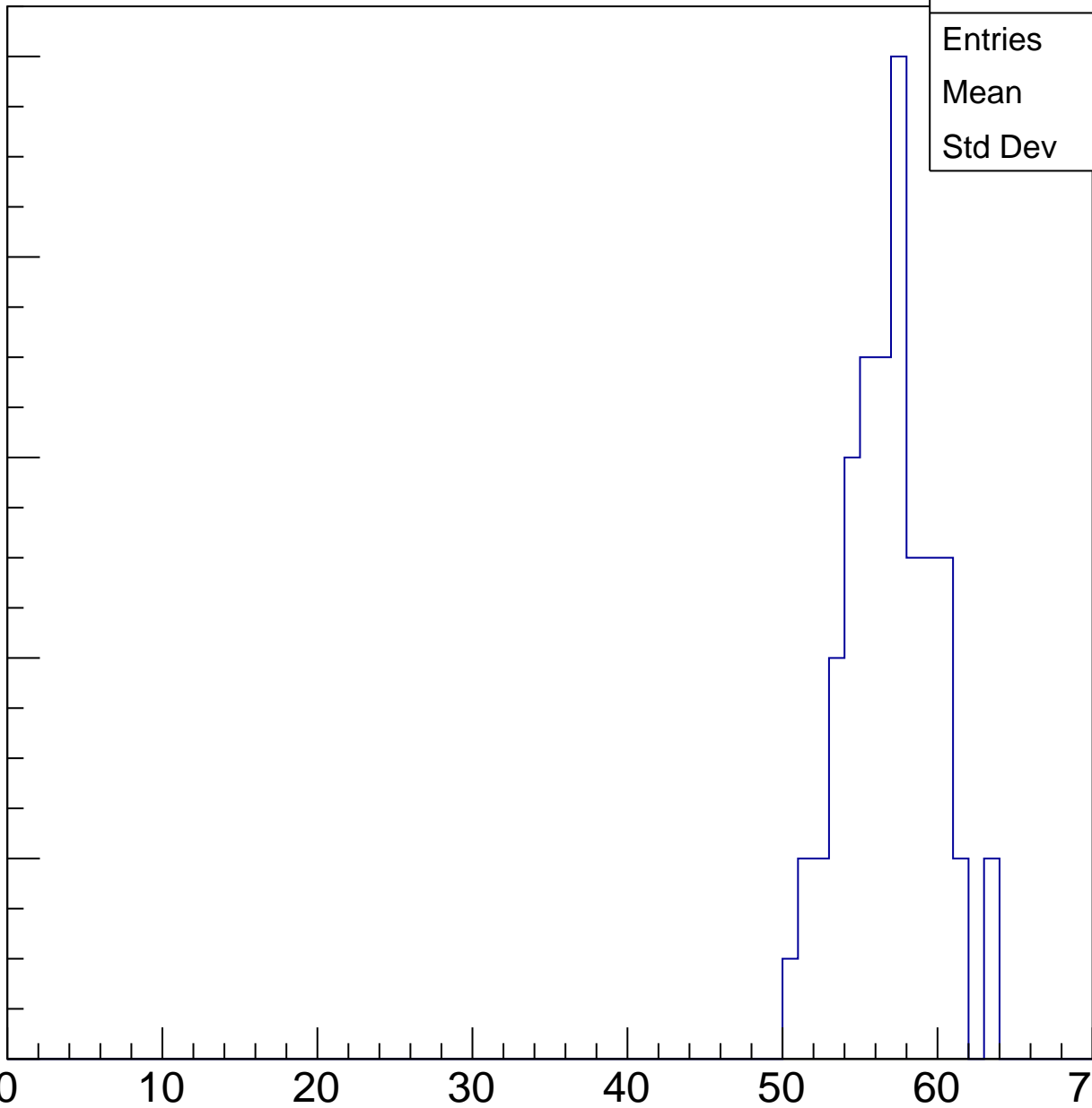
Entries	58
Mean	56.41
Std Dev	2.871

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

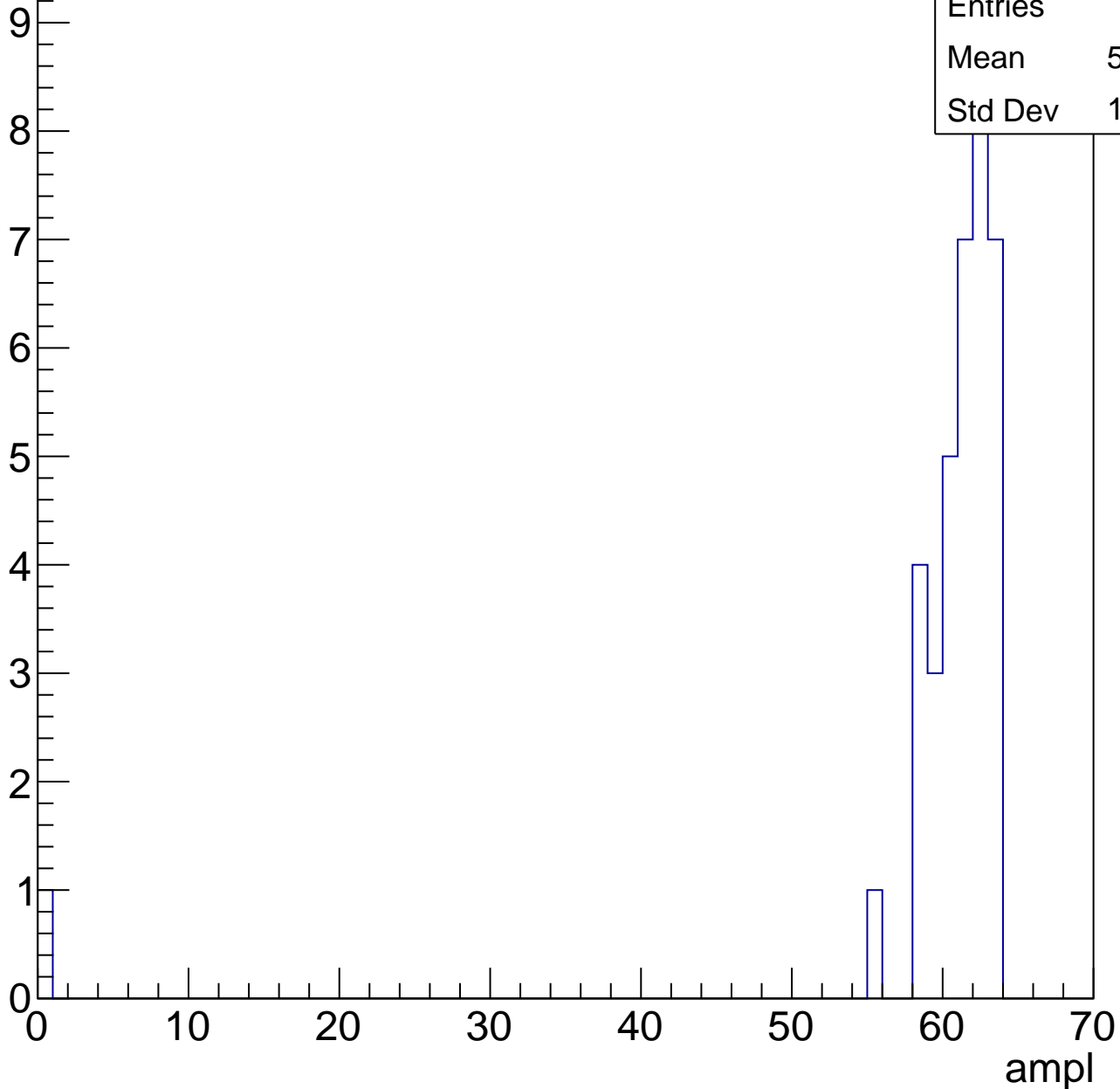


B0L002S, U9-ch50, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

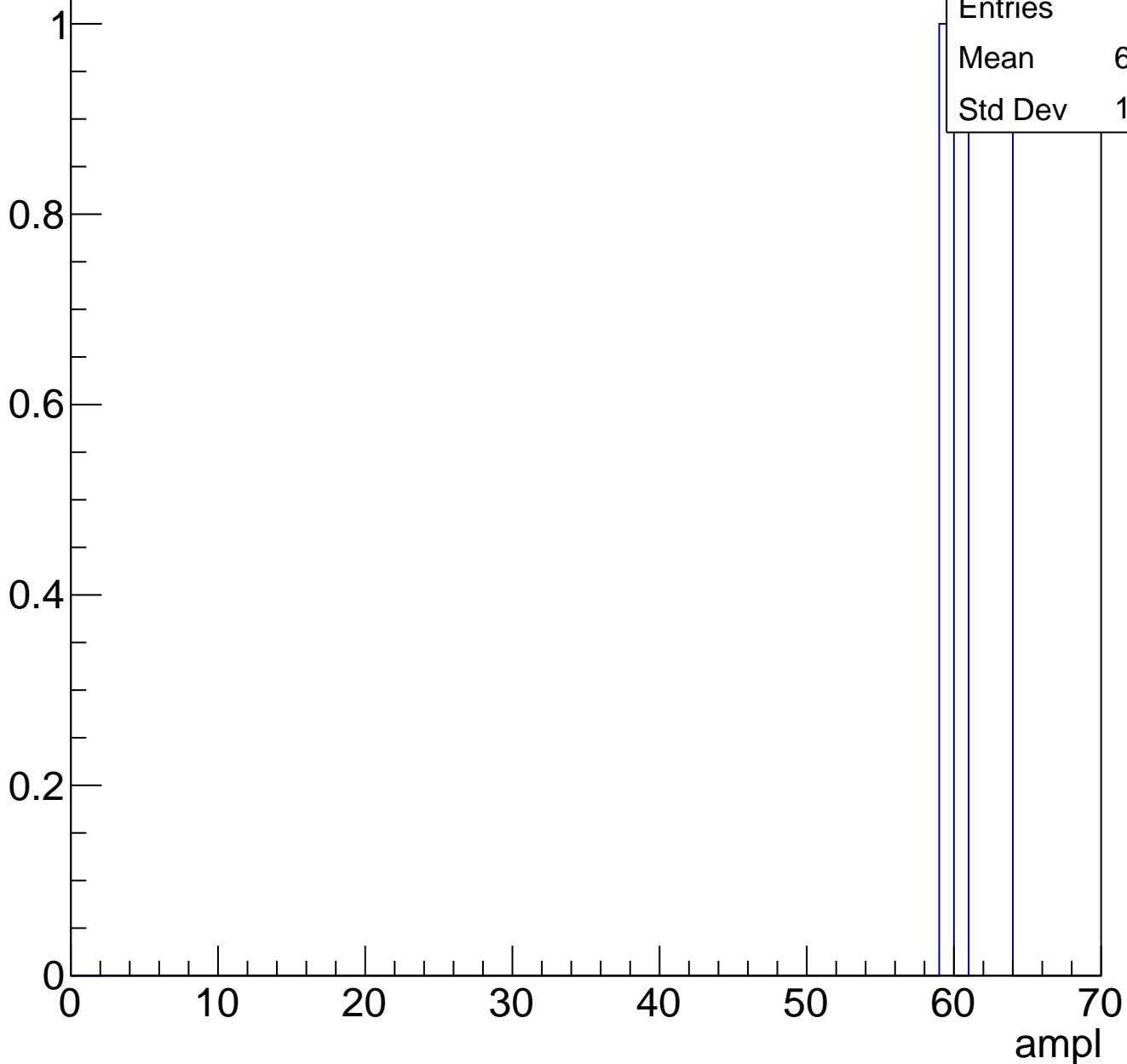
Entries	37
Mean	59.19
Std Dev	10.03



B0L002S, U9-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch51, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	29.91
Std Dev	3.562

Gaus mean : 30.6553

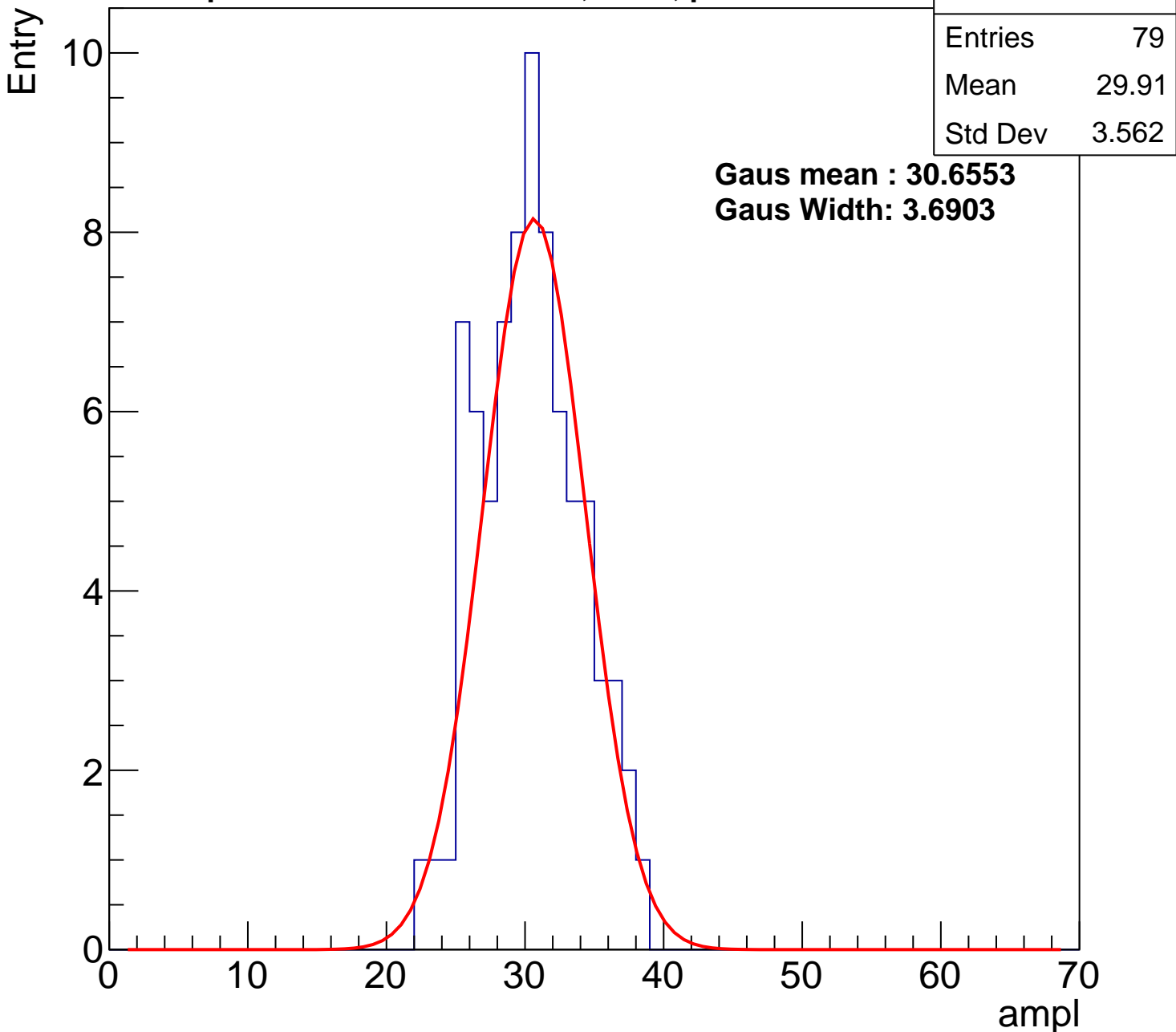
Gaus Width: 3.6903

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch51, adc1

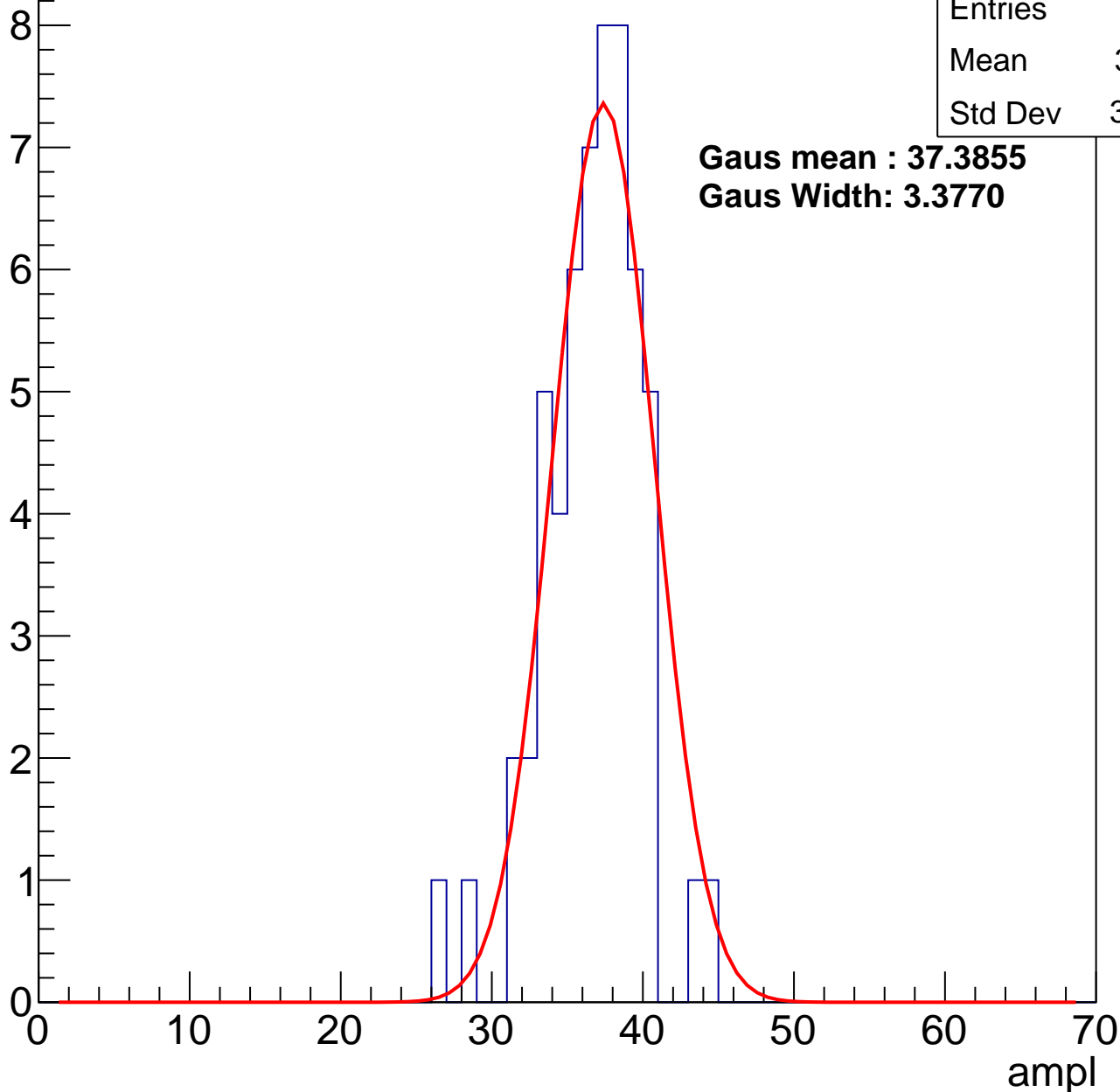
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.21
Std Dev	3.238

Gaus mean : 37.3855

Gaus Width: 3.3770



B0L002S, U9-ch51, adc2

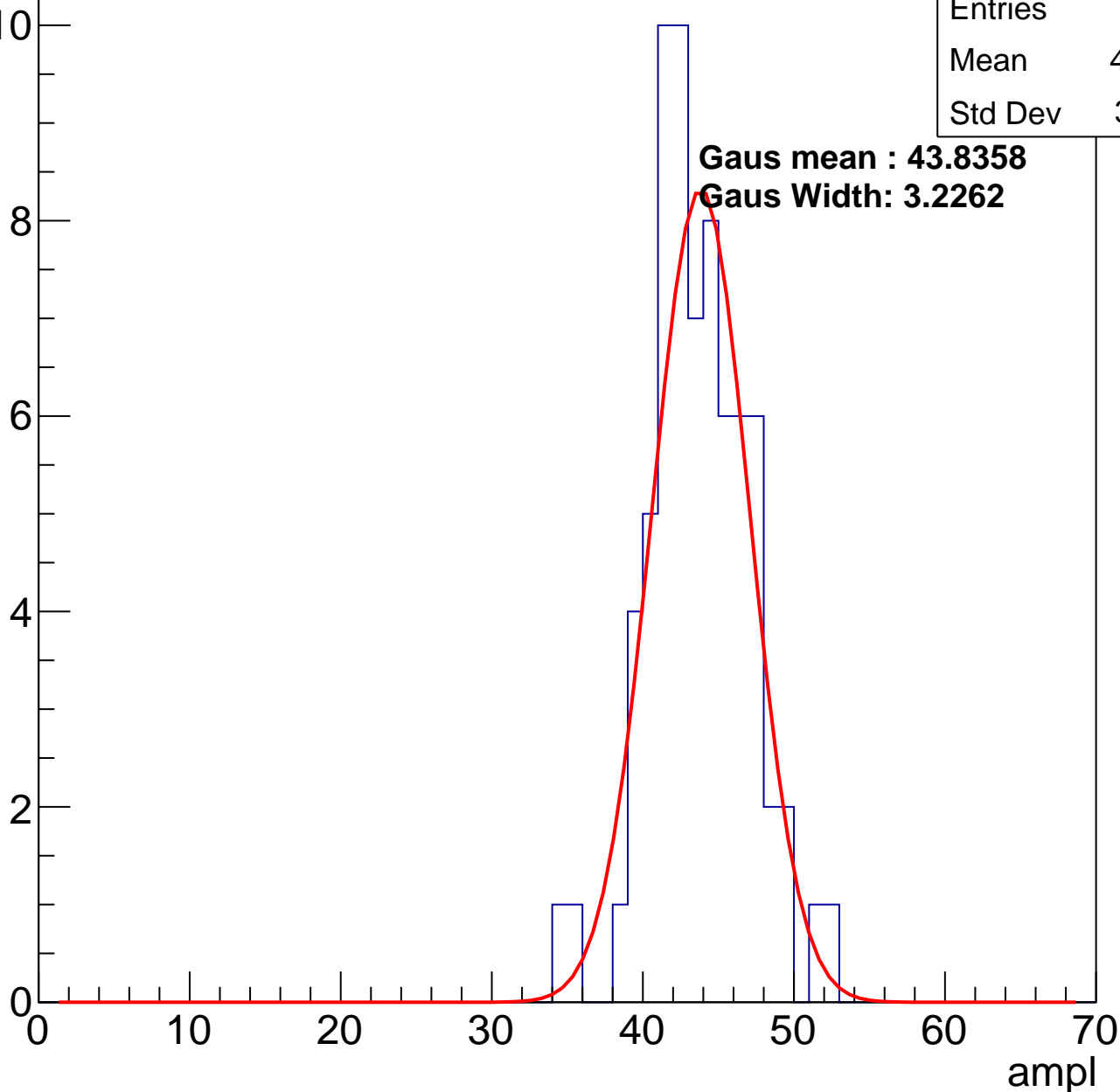
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.25
Std Dev	3.301

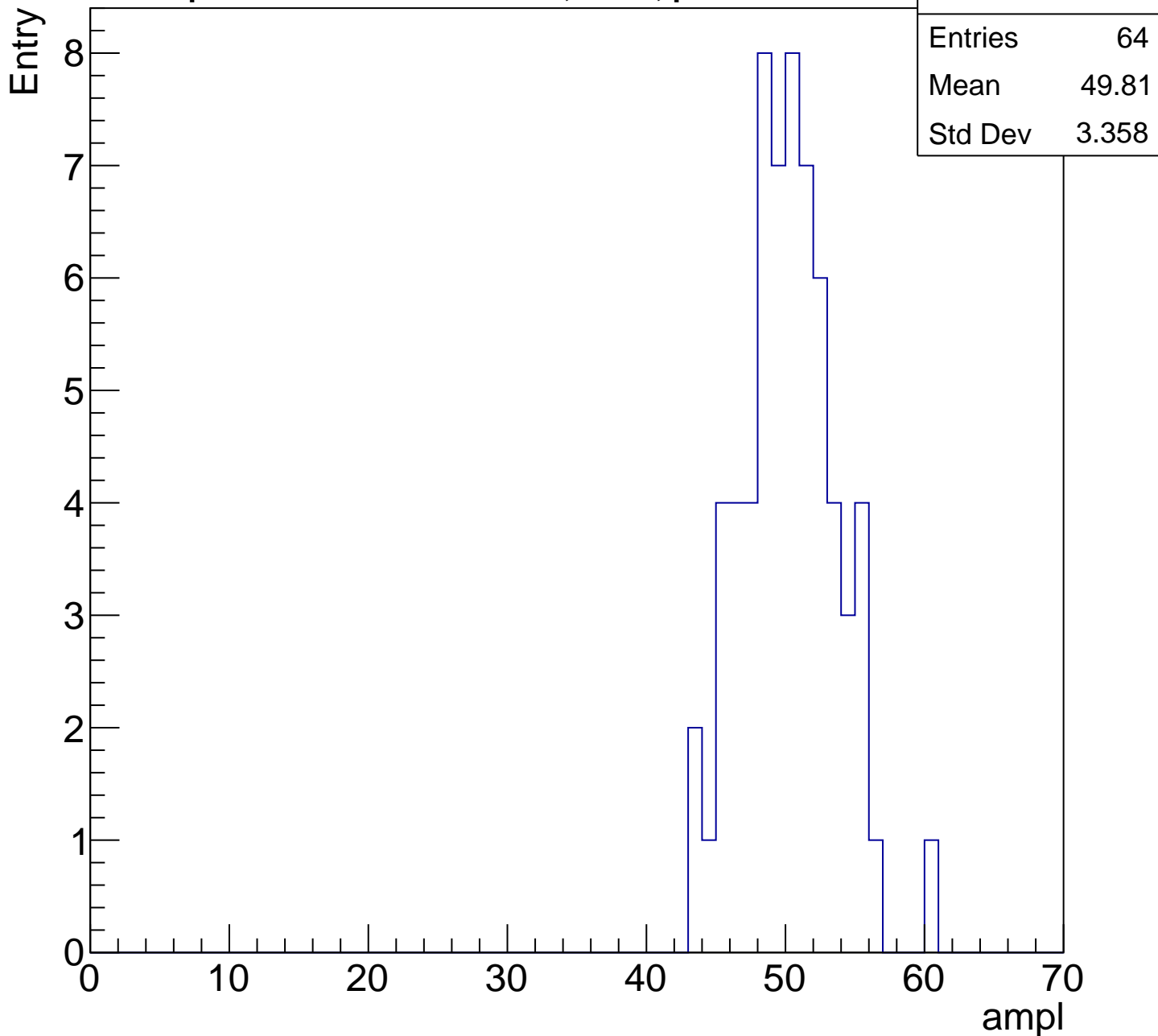
Gaus mean : 43.8358

Gaus Width: 3.2262



B0L002S, U9-ch51, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch51, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

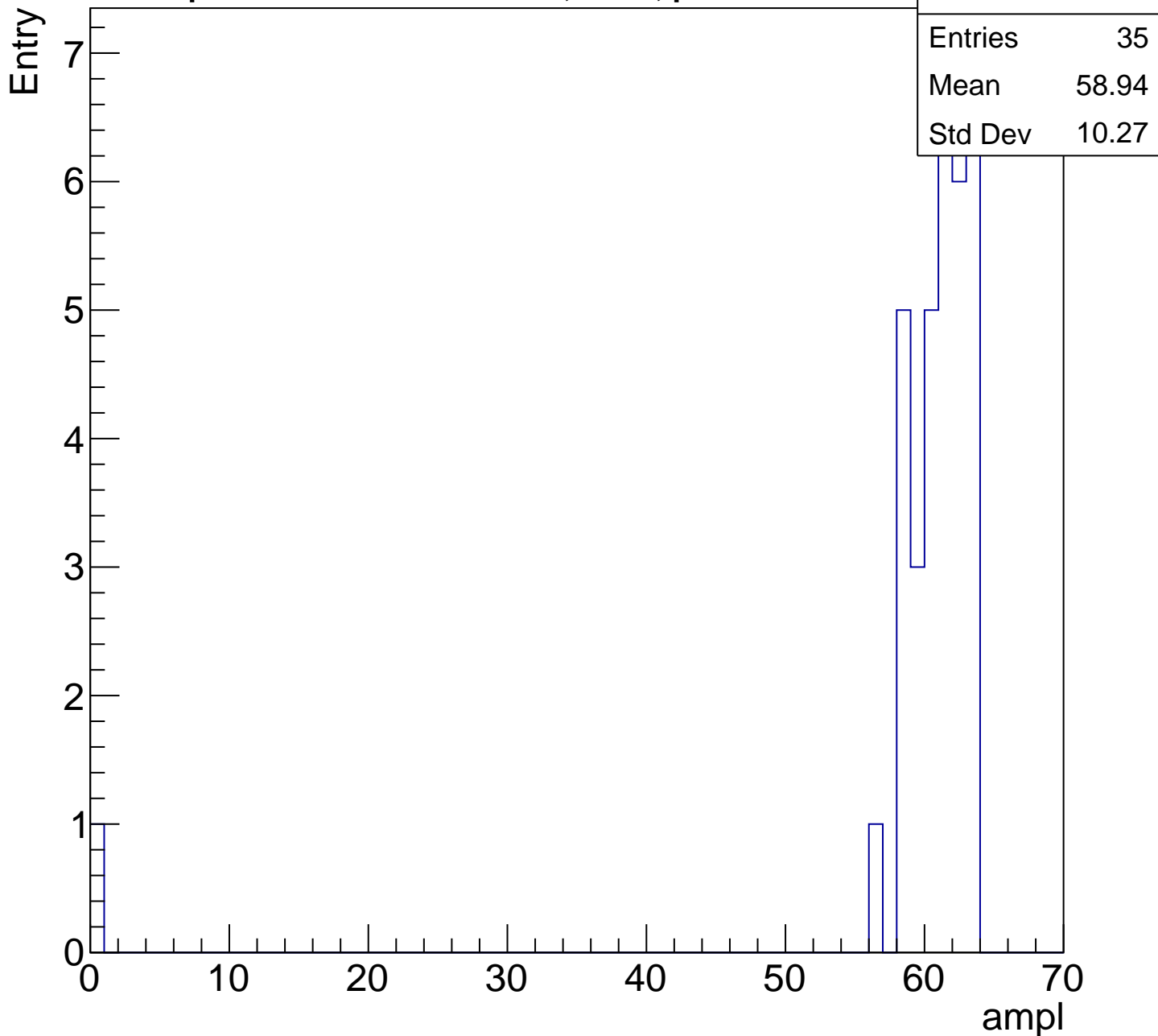
70

ampl

Entries	69
Mean	56.43
Std Dev	3.356

B0L002S, U9-ch51, adc5

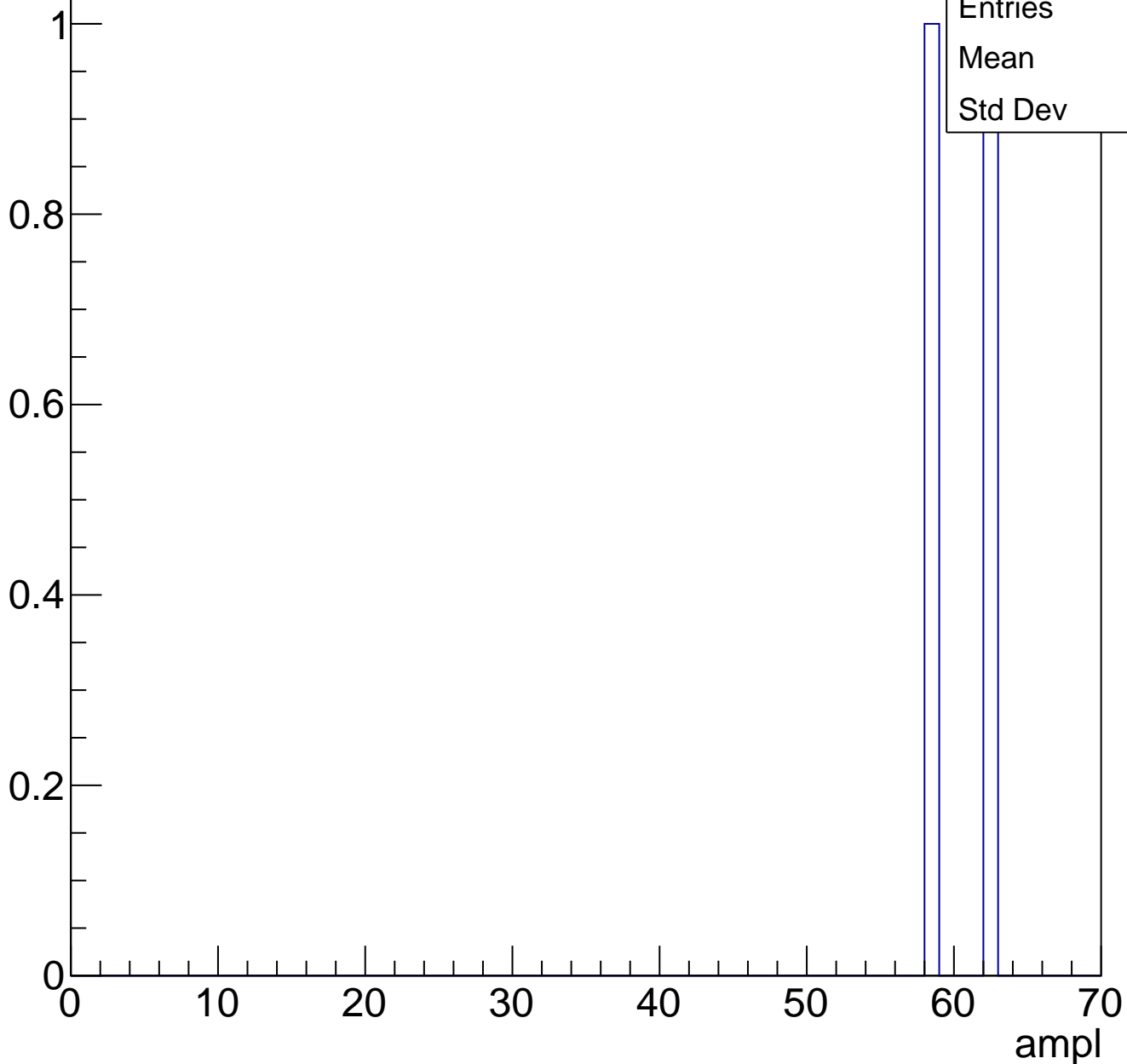
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch52, adc0

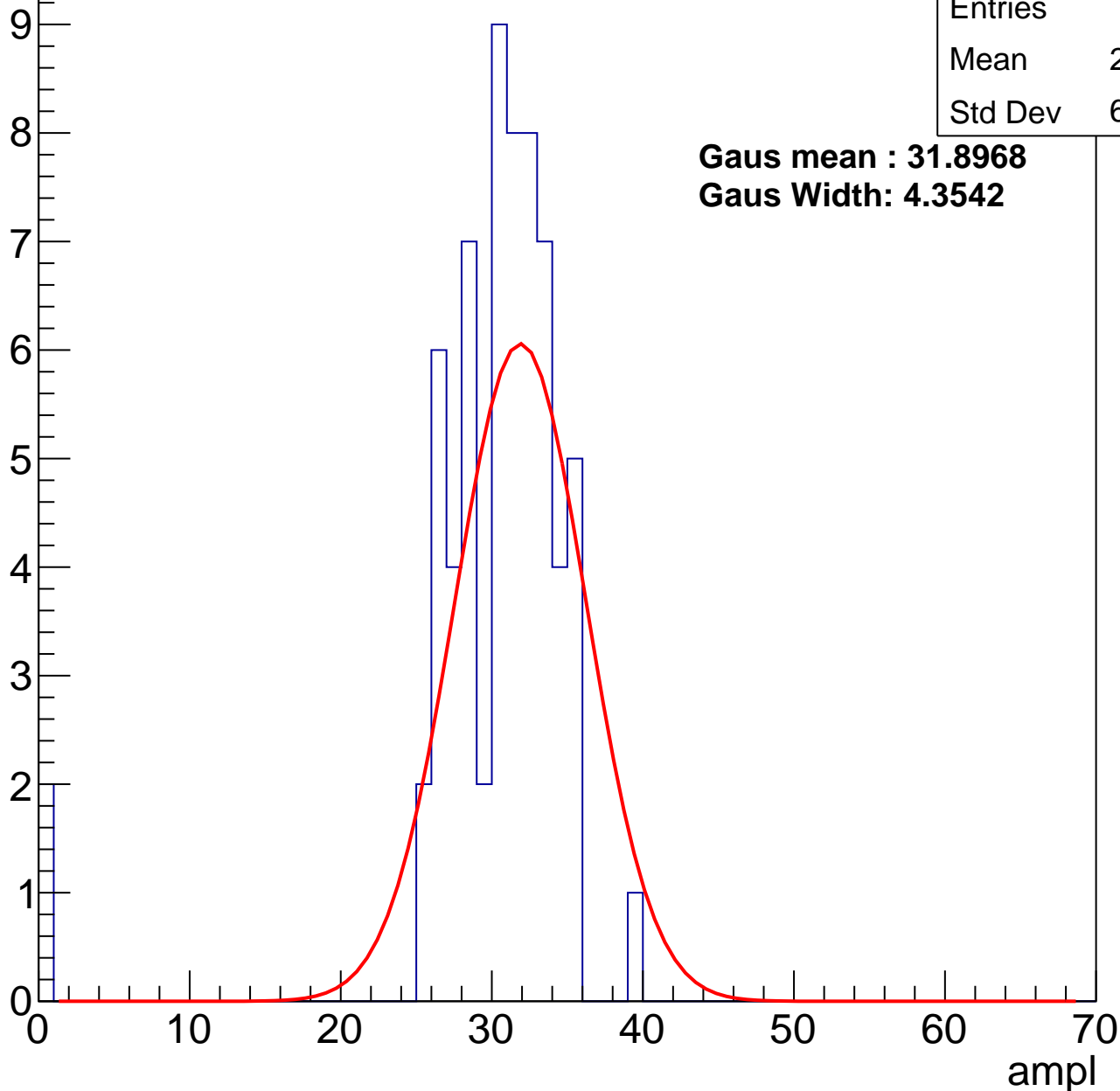
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.58
Std Dev	6.043

Gaus mean : 31.8968

Gaus Width: 4.3542



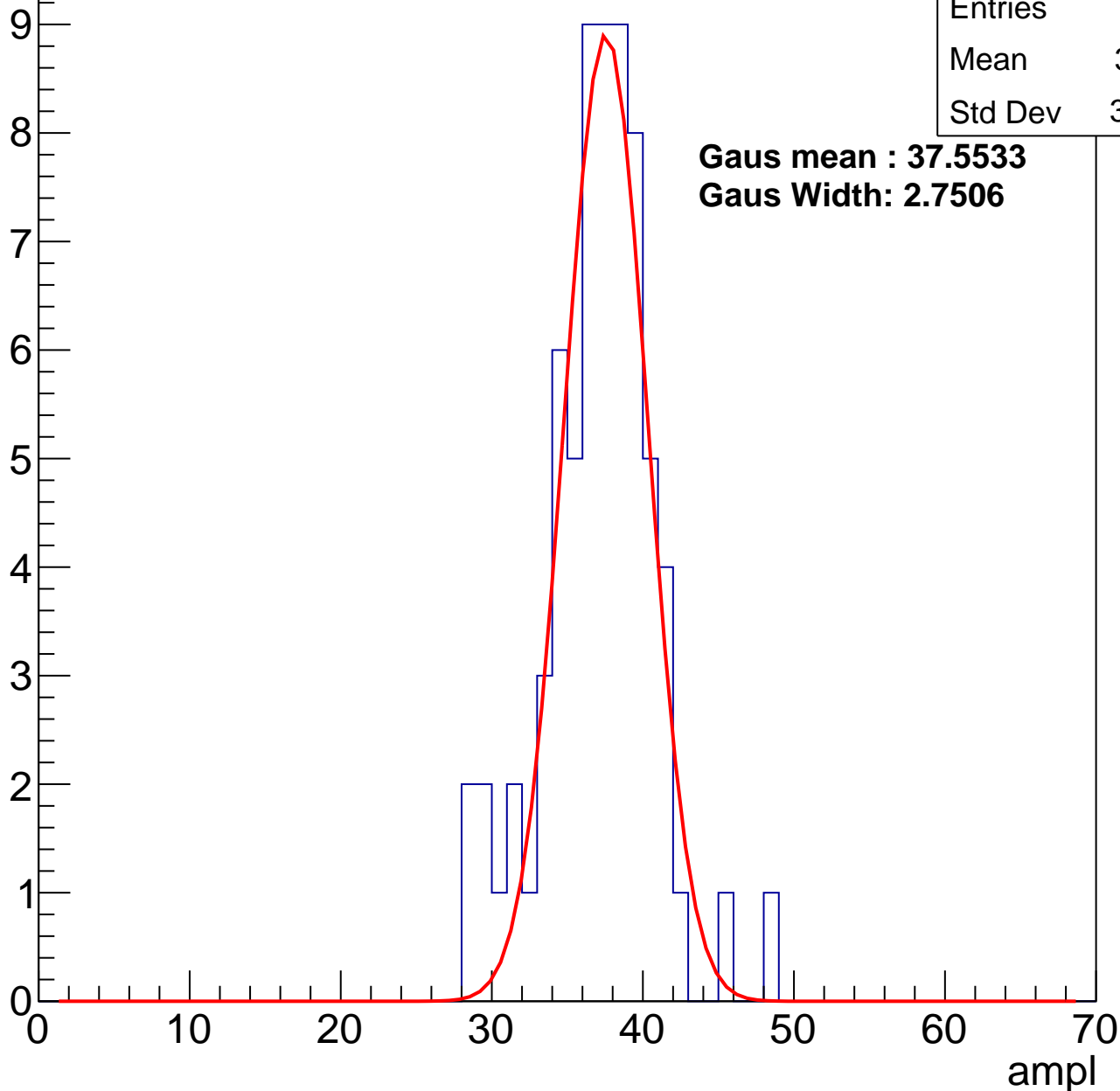
B0L002S, U9-ch52, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.61
Std Dev	3.644

Gaus mean : 37.5533
Gaus Width: 2.7506



B0L002S, U9-ch52, adc2

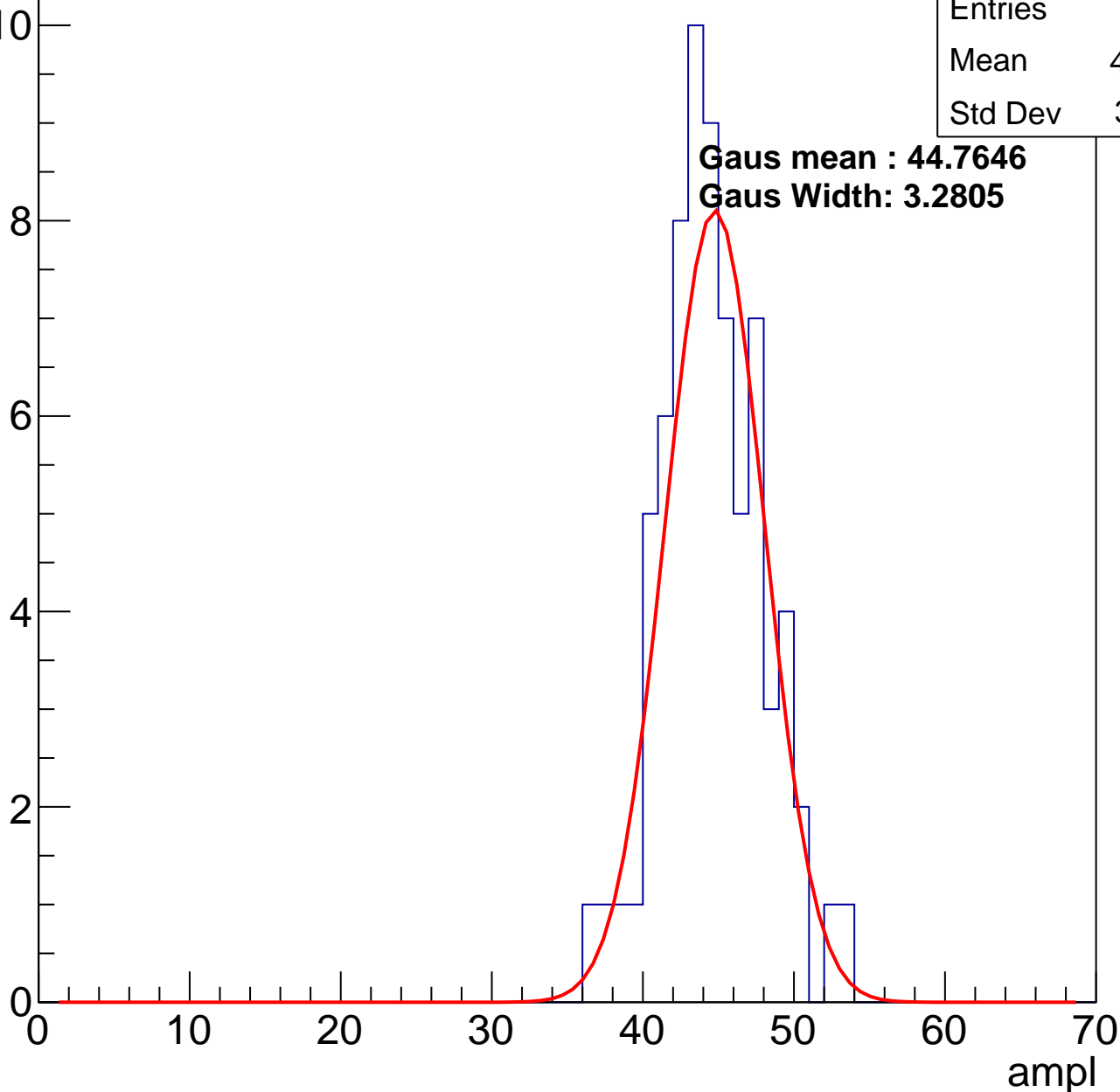
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.12
Std Dev	3.341

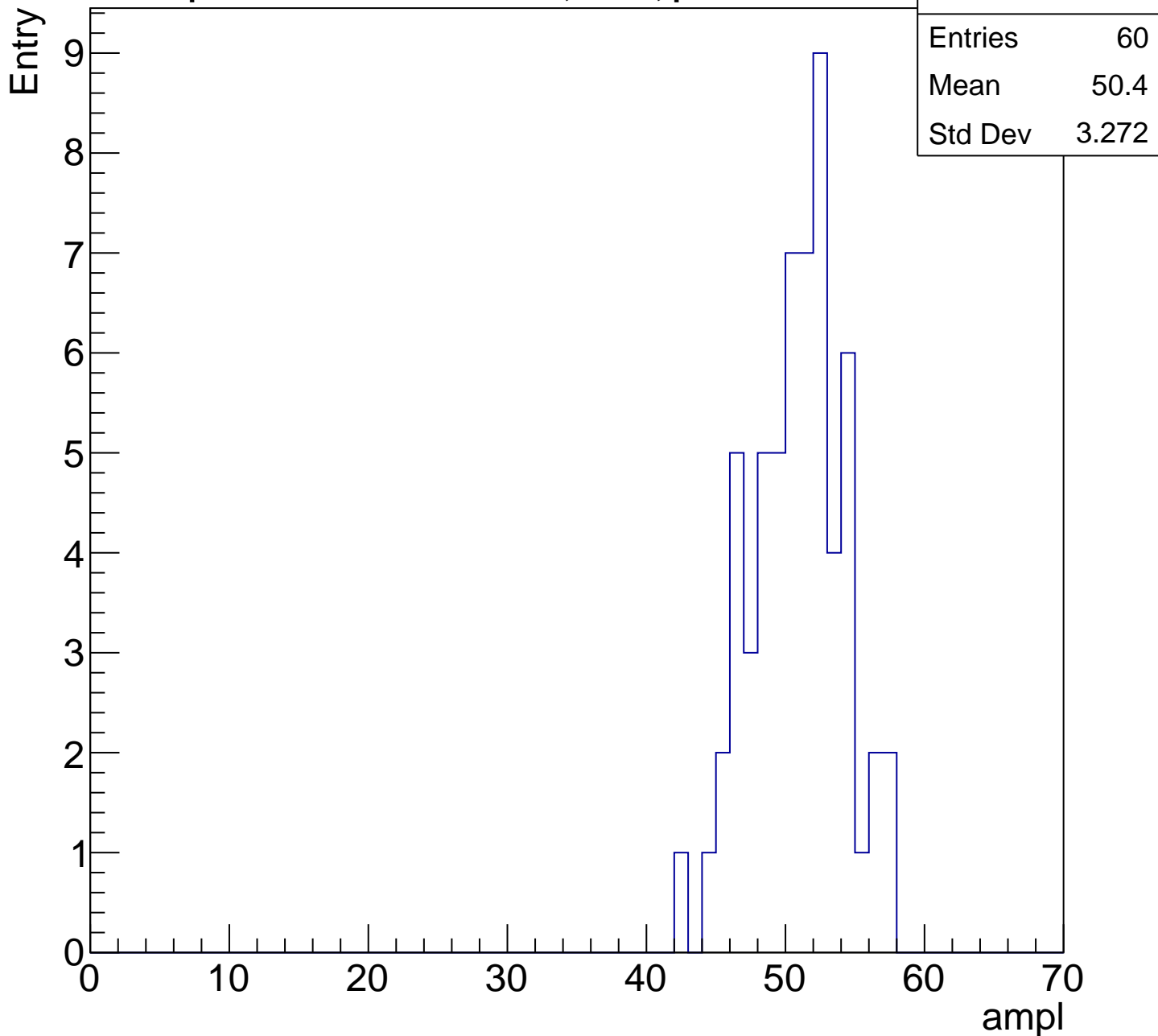
Gaus mean : 44.7646

Gaus Width: 3.2805



B0L002S, U9-ch52, adc3

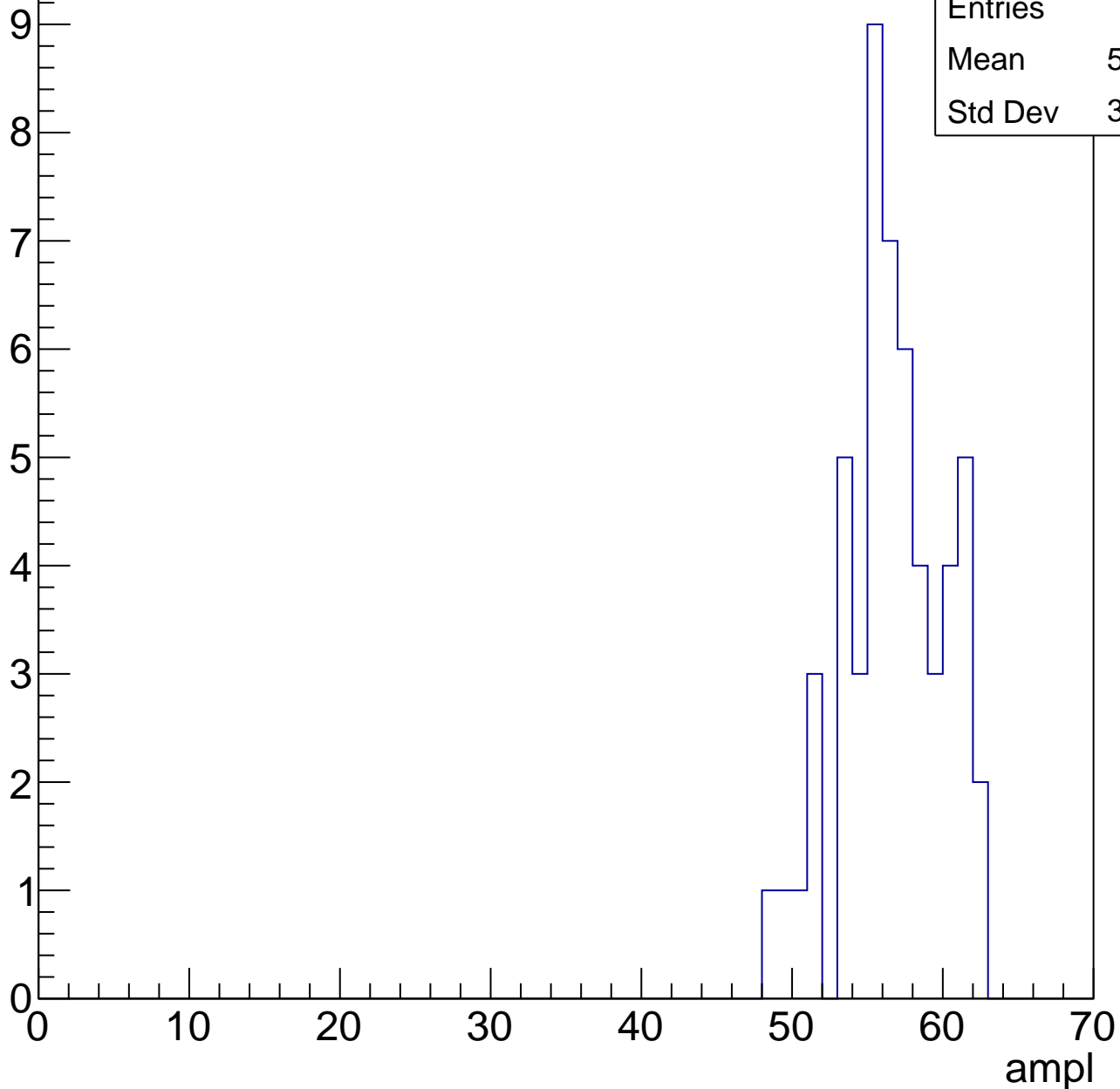
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	54
Mean	56.19
Std Dev	3.328

B0L002S, U9-ch52, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 40

Mean 58.52

Std Dev 9.62

ampl

0

10

20

30

40

50

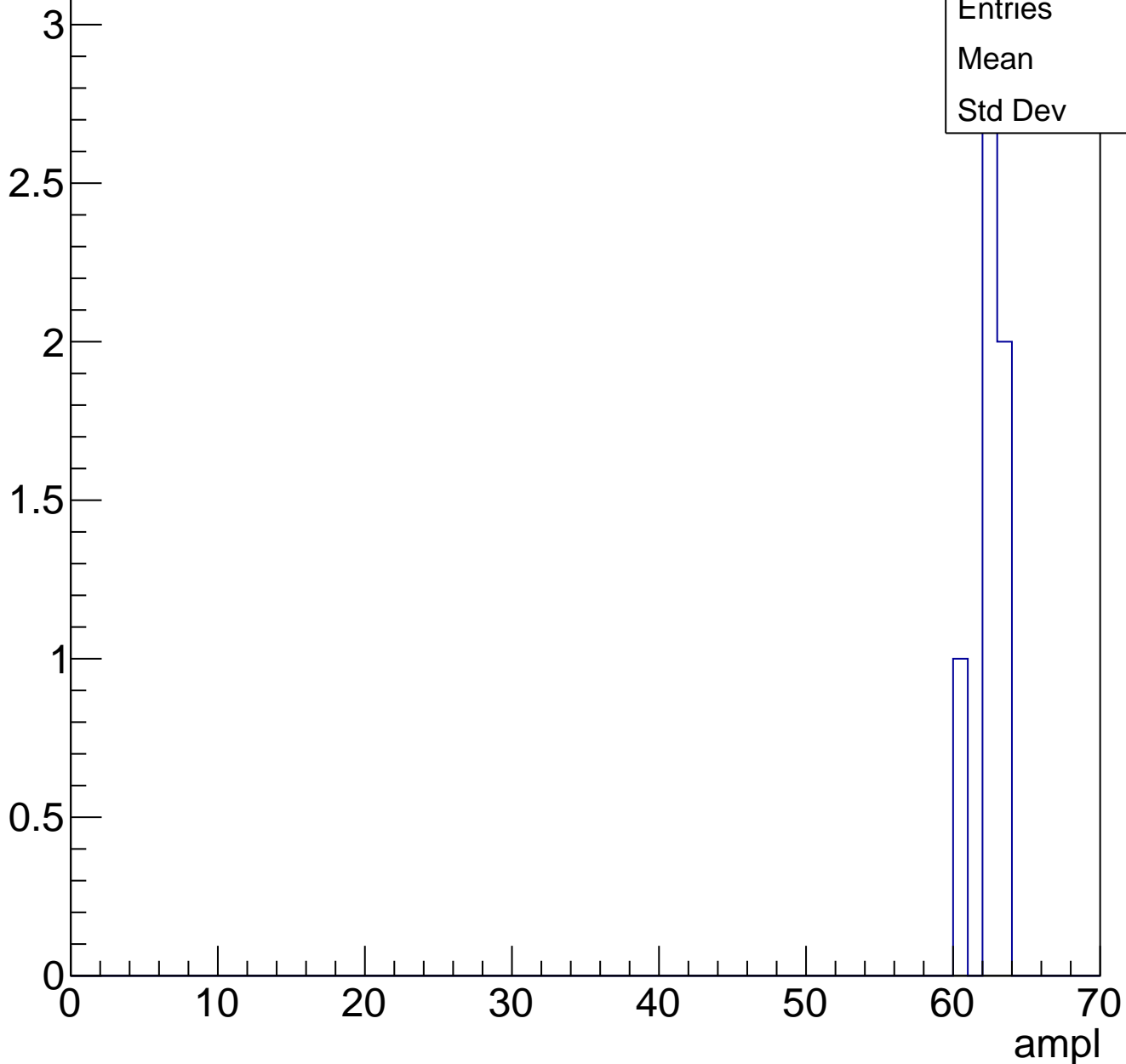
60

70

B0L002S, U9-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch52, adc7

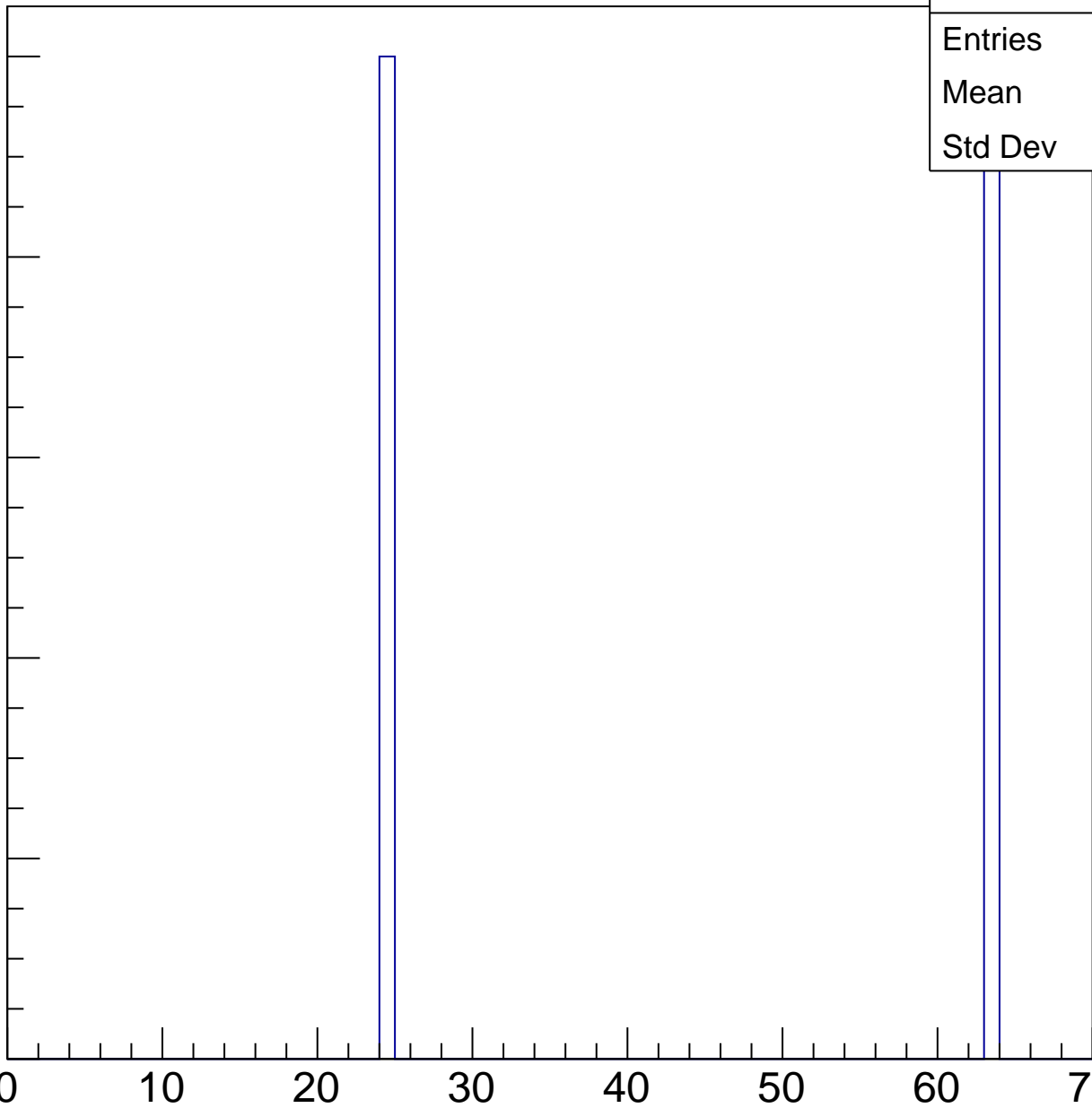
calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43.5
Std Dev	19.5

ampl



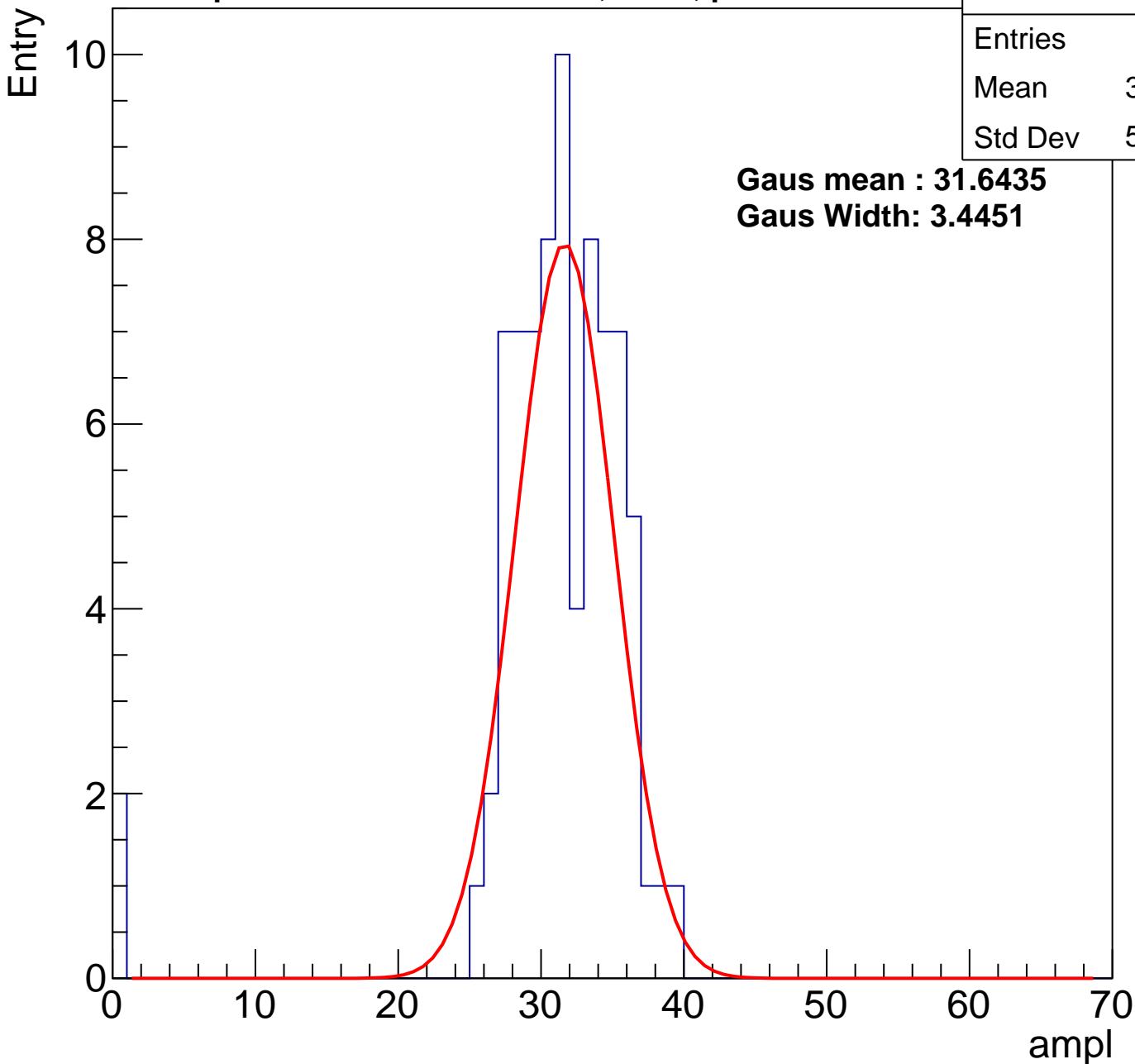
B0L002S, U9-ch53, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	30.56
Std Dev	5.872

Gaus mean : 31.6435

Gaus Width: 3.4451



B0L002S, U9-ch53, adc1

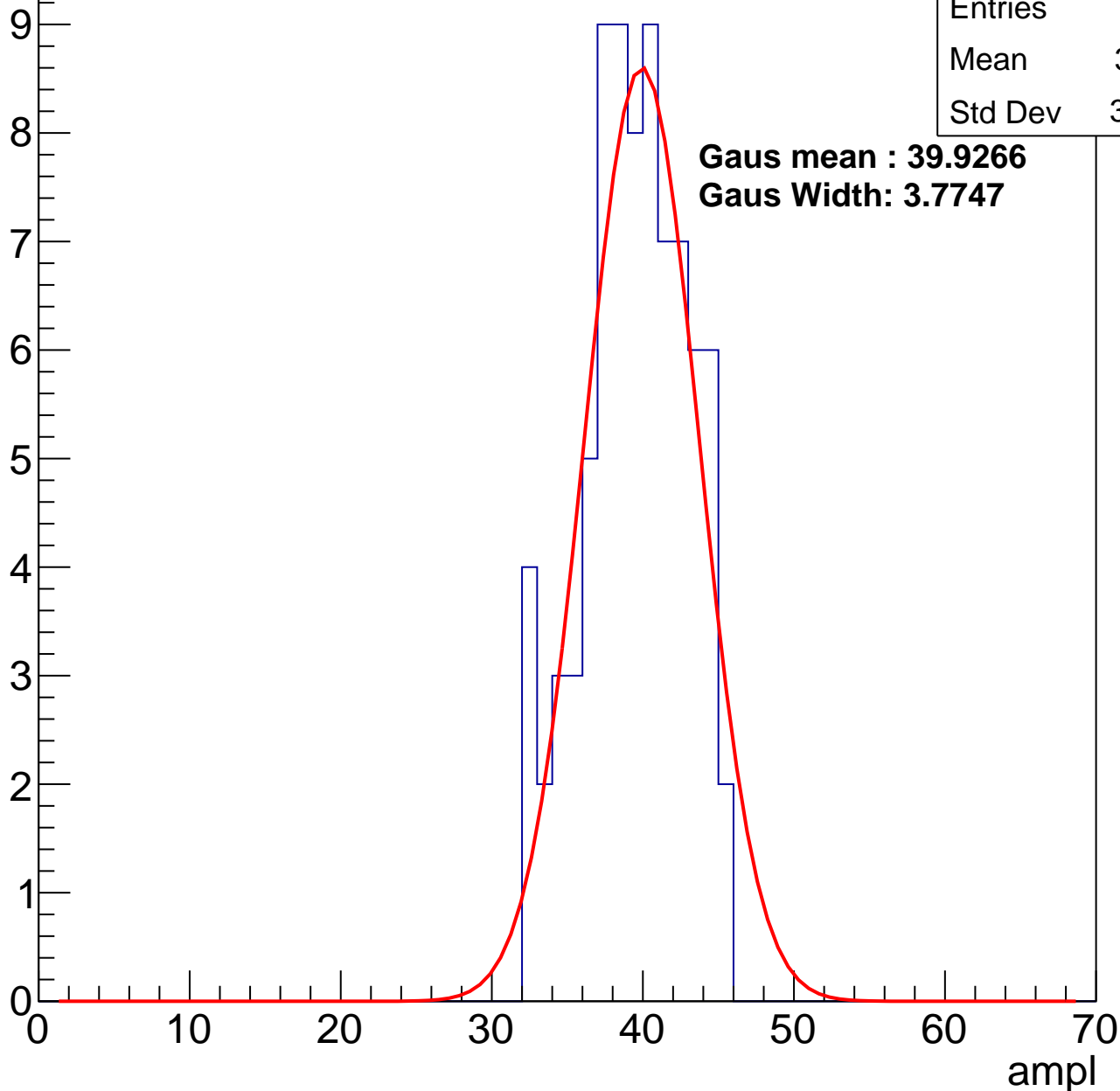
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	39.01
Std Dev	3.352

Gaus mean : 39.9266

Gaus Width: 3.7747



B0L002S, U9-ch53, adc2

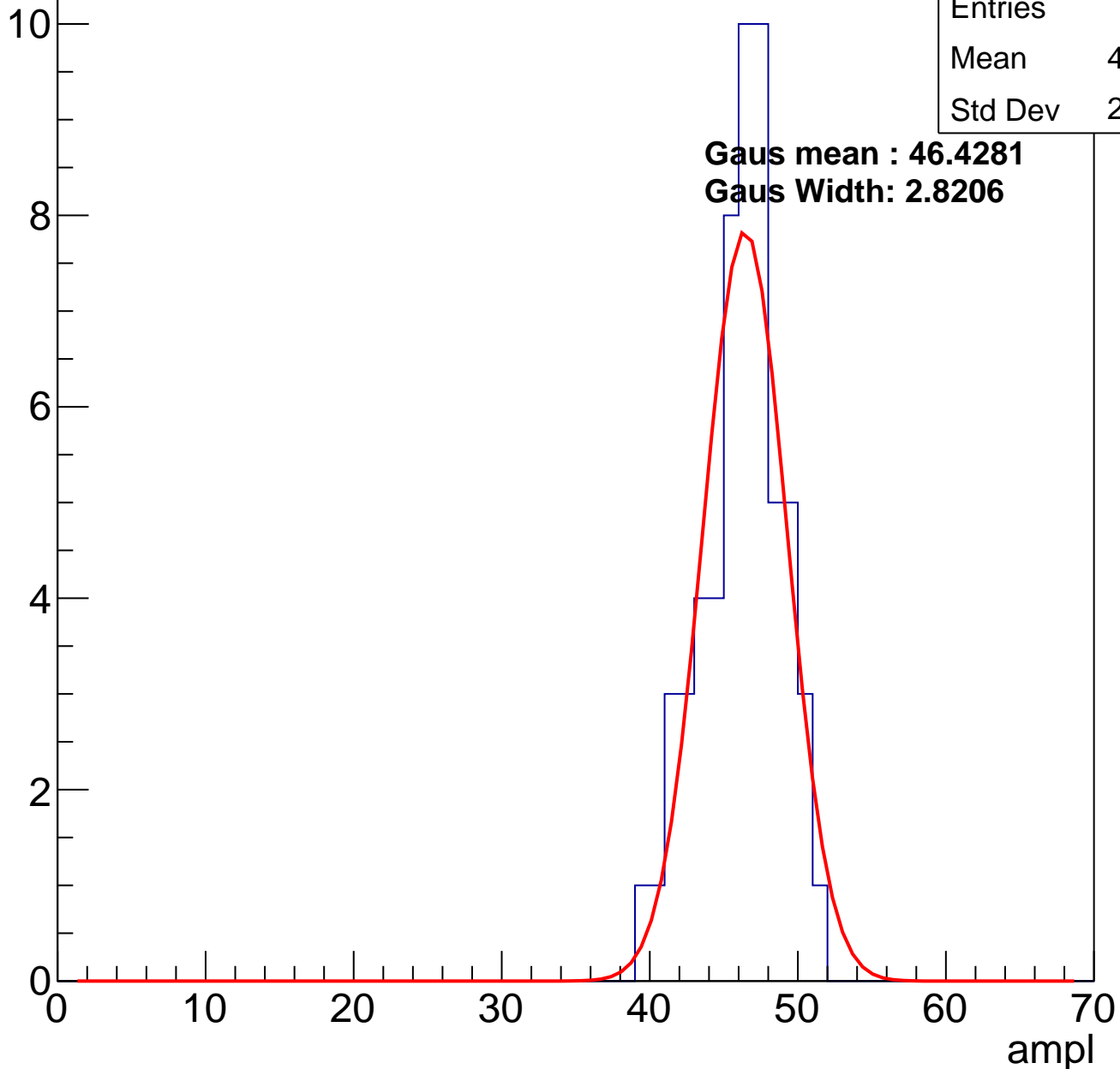
calib_packv5_042523_0143.root, FC#8, port C1

Entries	58
Mean	45.72
Std Dev	2.664

Gaus mean : 46.4281

Gaus Width: 2.8206

Entry

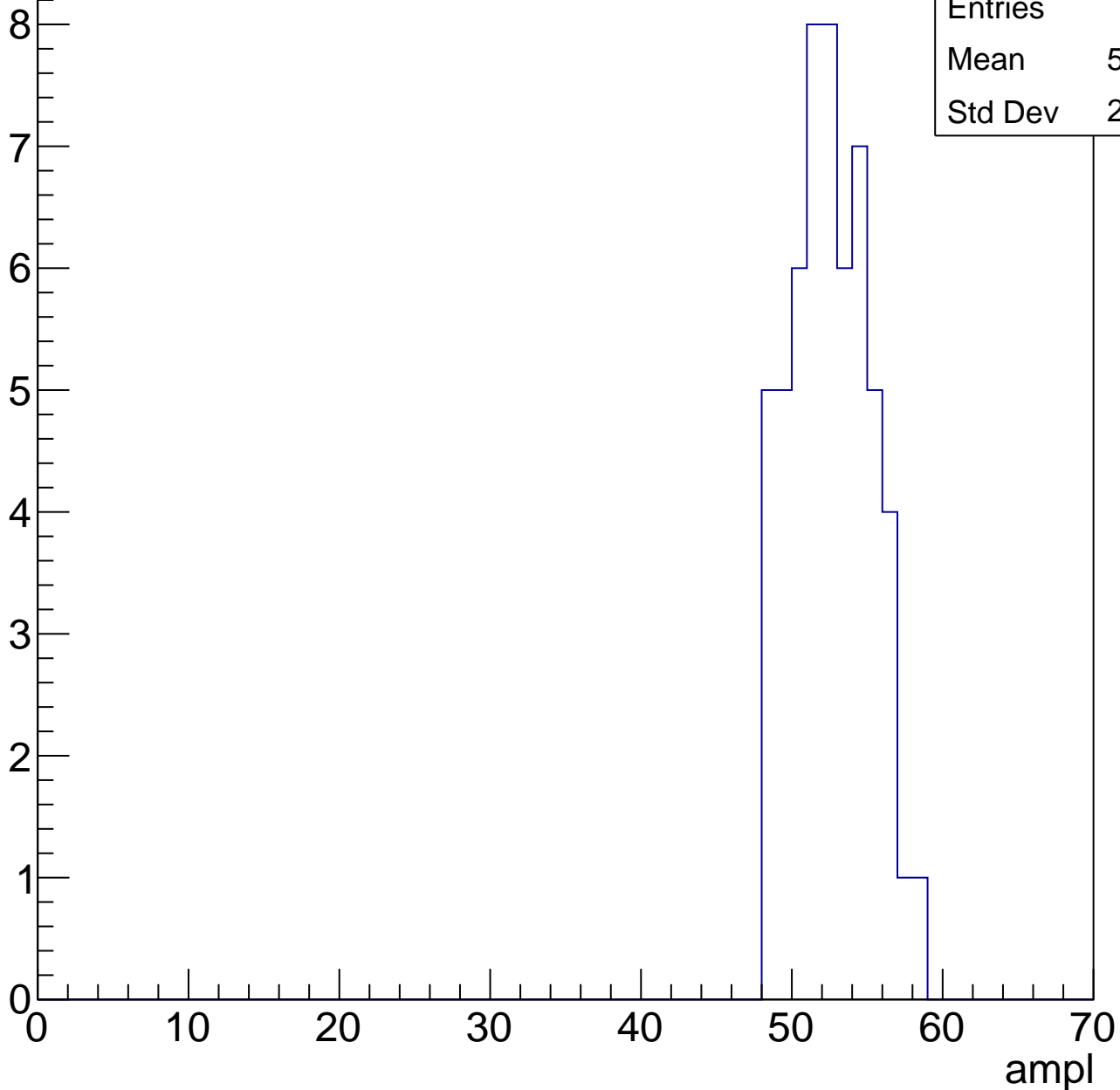


B0L002S, U9-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

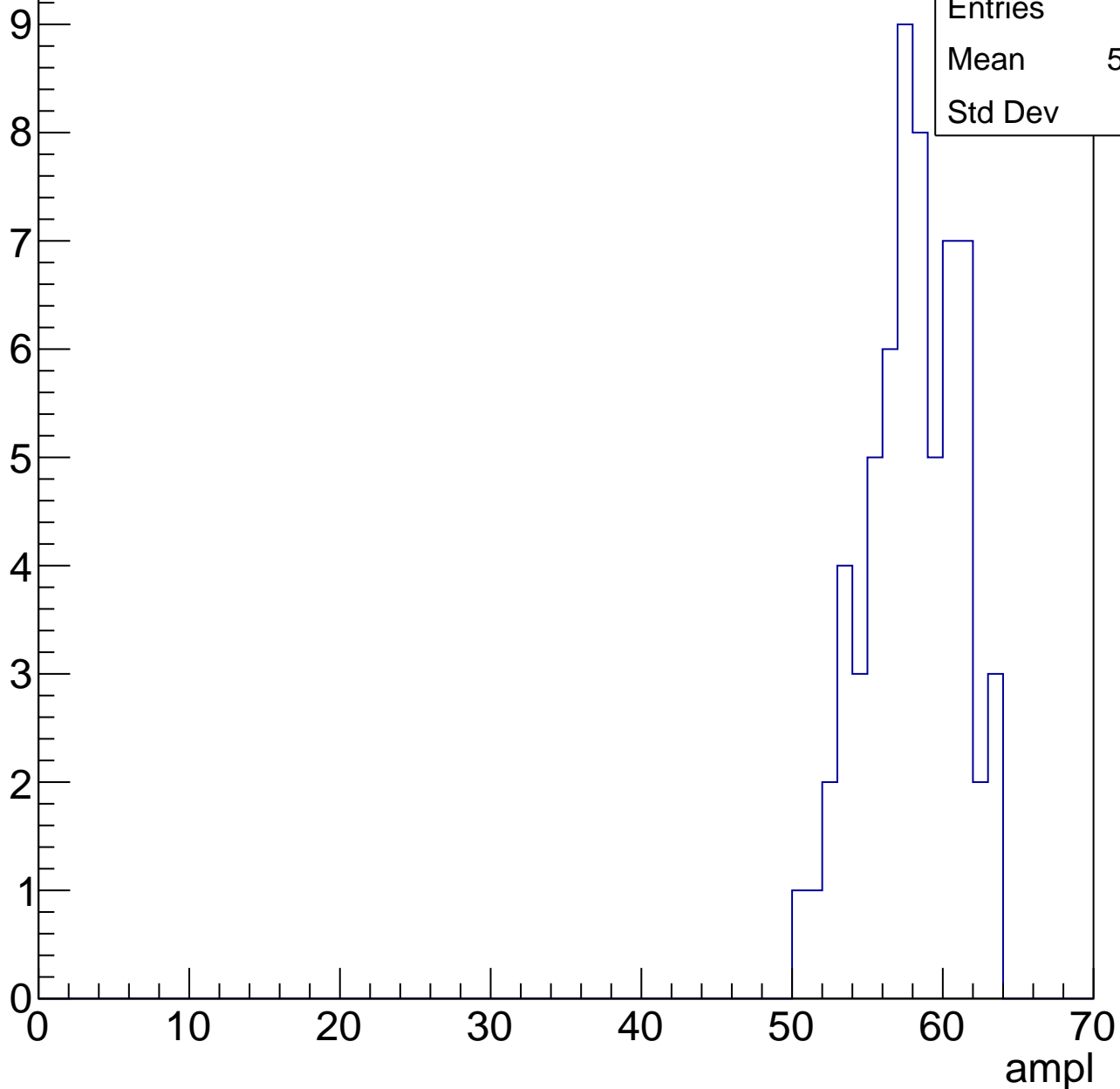
Entries	56
Mean	52.12
Std Dev	2.536



B0L002S, U9-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

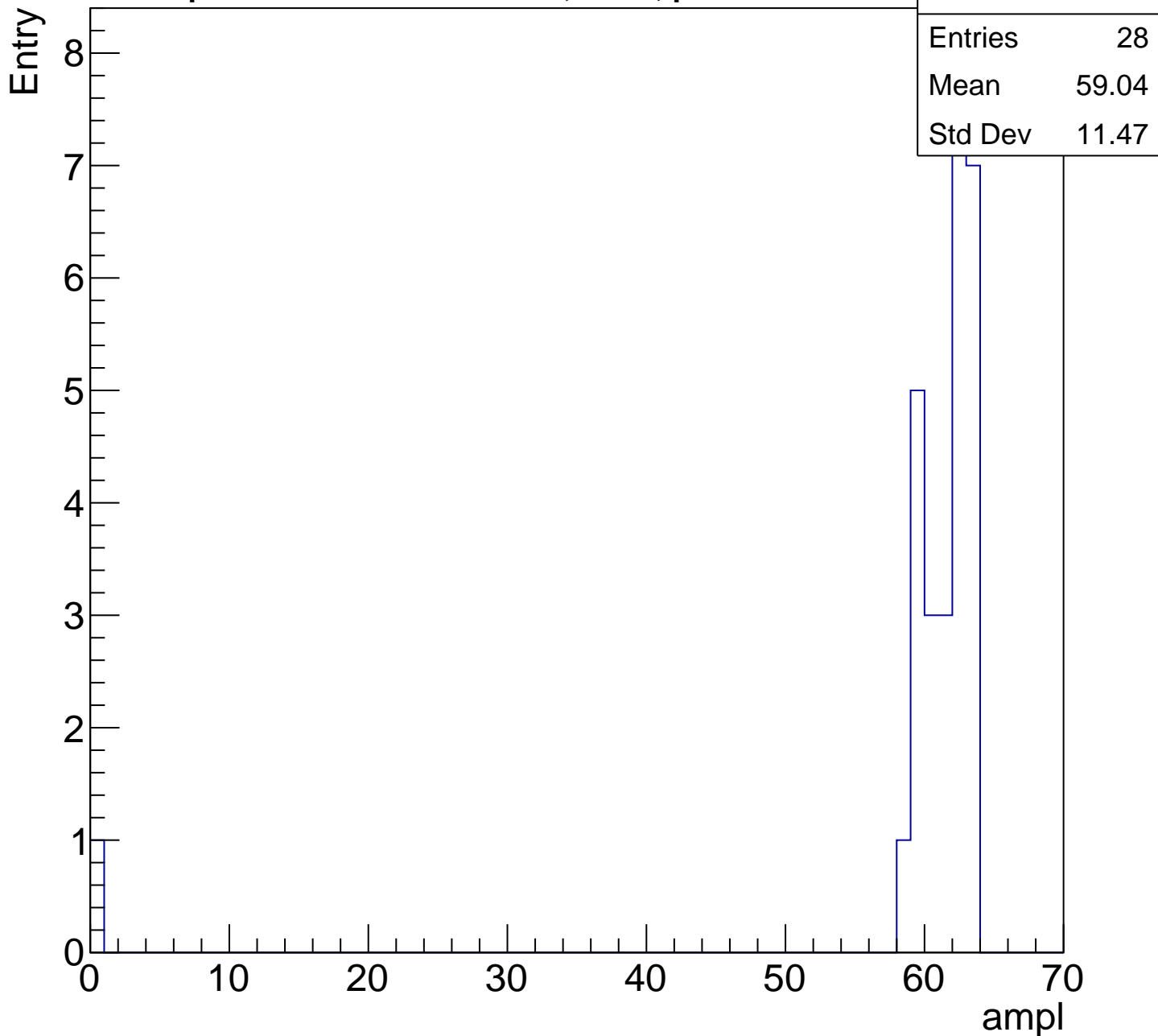
Entry



Entries	63
Mean	57.49
Std Dev	3.08

B0L002S, U9-ch53, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch54, adc0

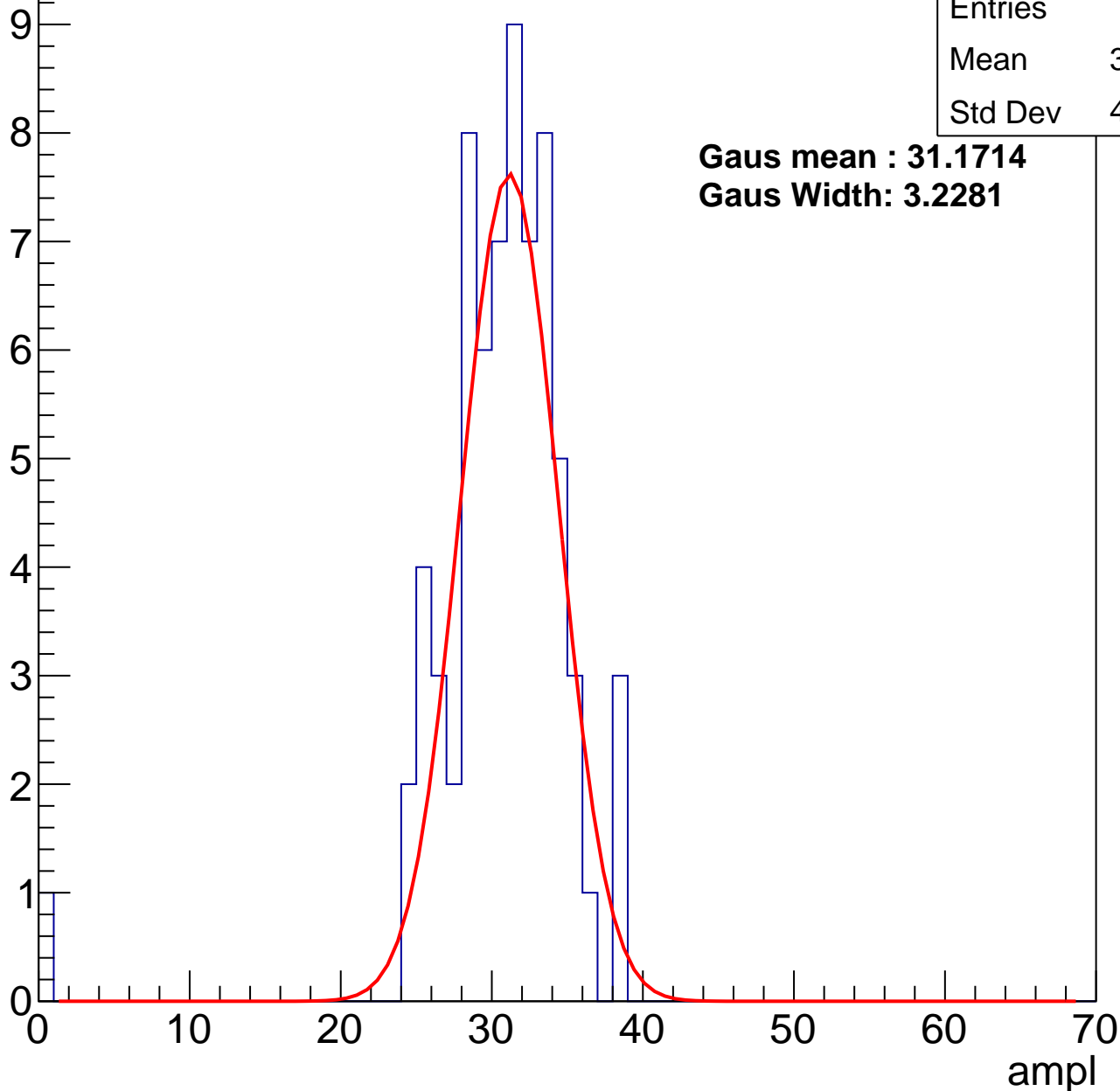
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.14
Std Dev	4.915

Gaus mean : 31.1714

Gaus Width: 3.2281



B0L002S, U9-ch54, adc1

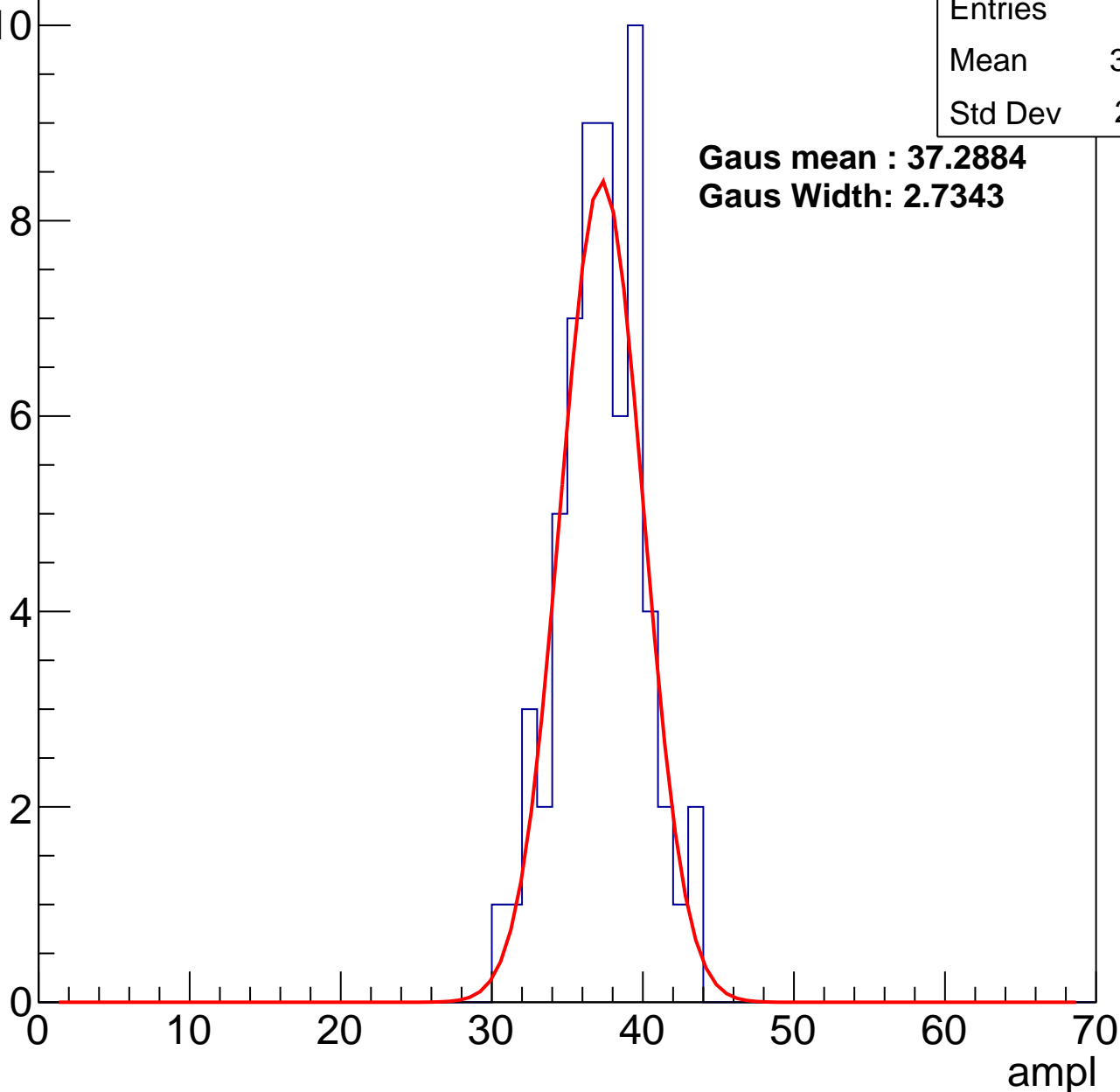
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.82
Std Dev	2.791

Gaus mean : 37.2884

Gaus Width: 2.7343



B0L002S, U9-ch54, adc2

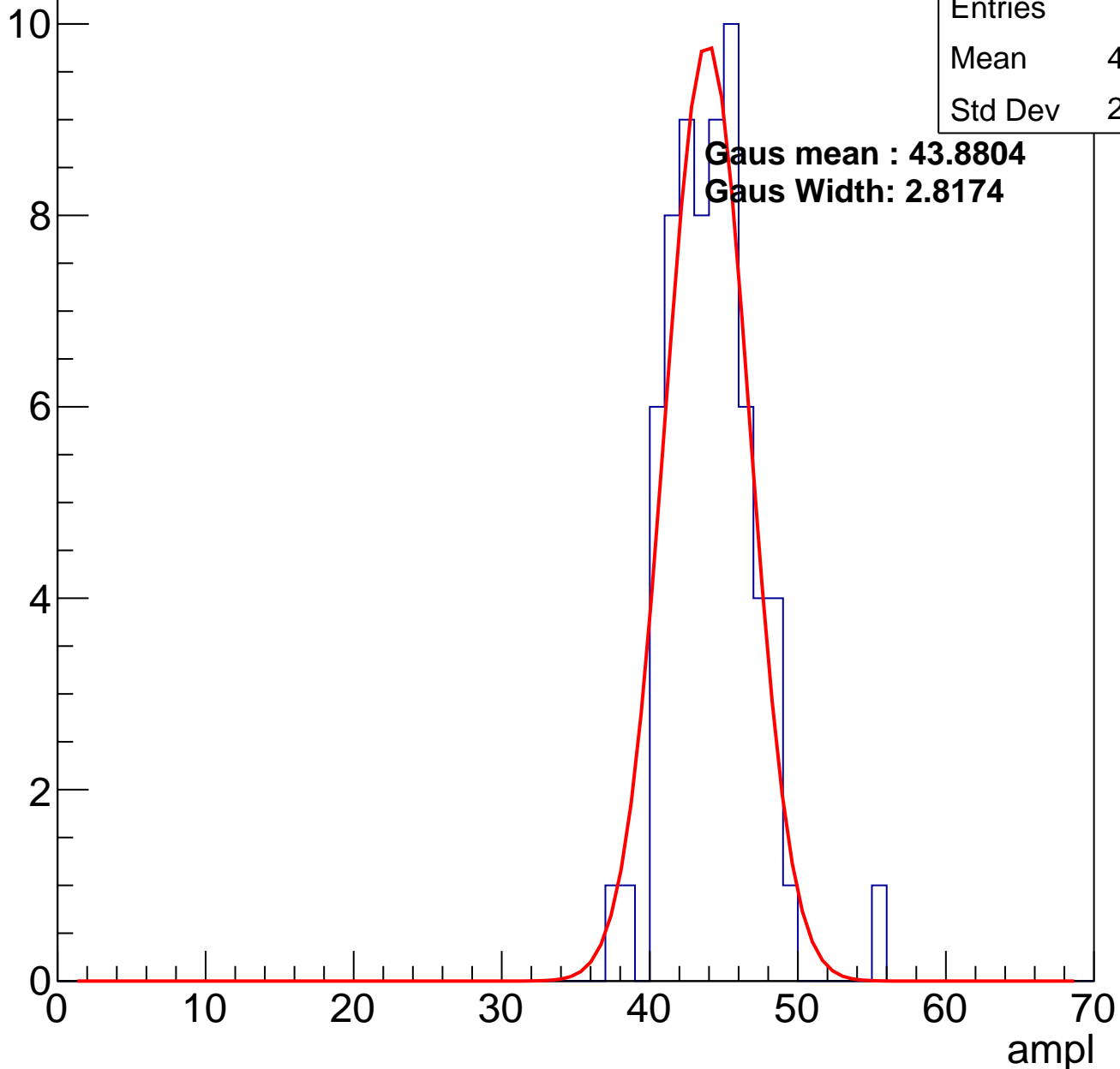
calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	43.69
Std Dev	2.897

Gaus mean : 43.8804

Gaus Width: 2.8174

Entry

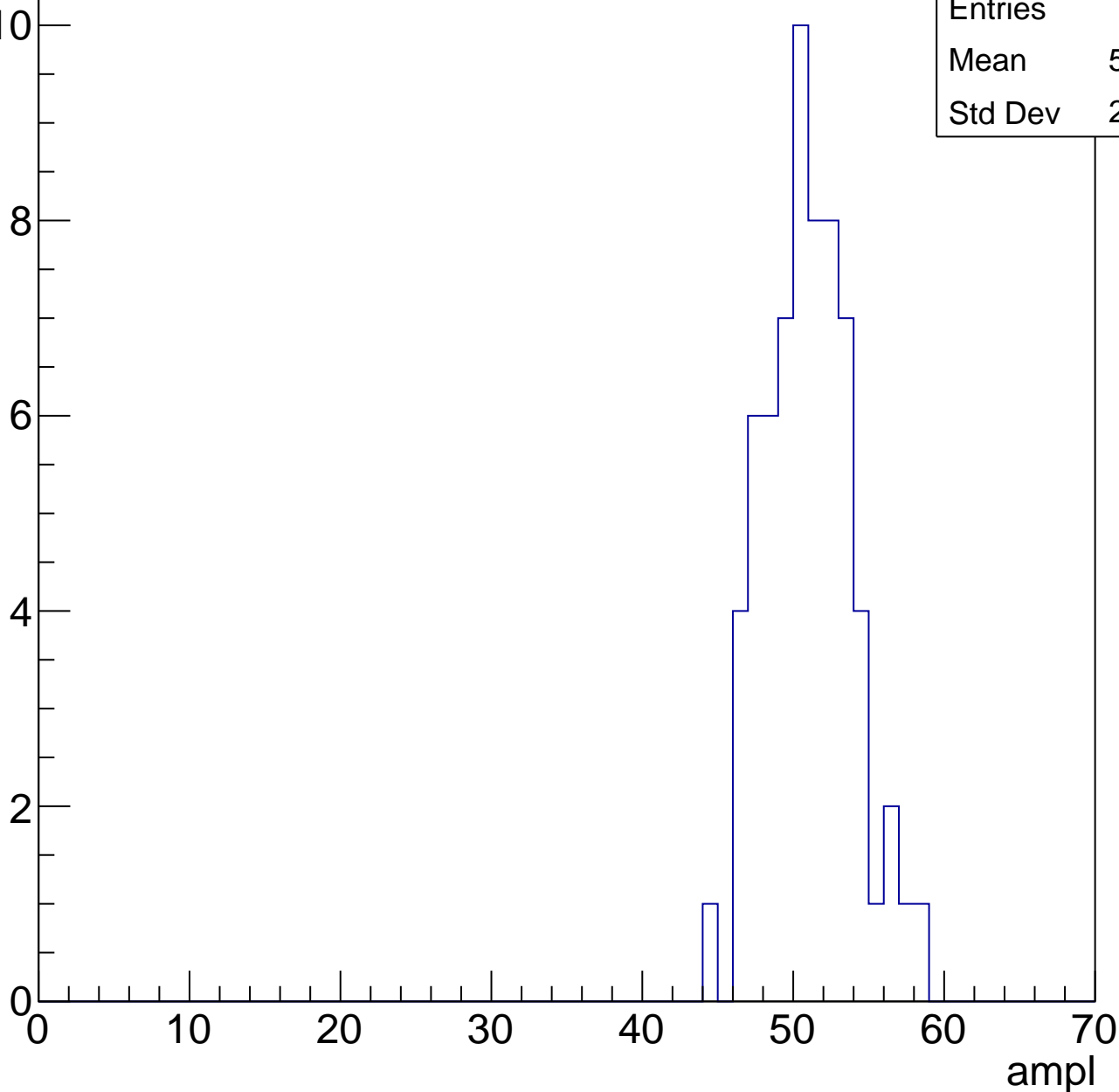


B0L002S, U9-ch54, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.52
Std Dev	2.872



B0L002S, U9-ch54, adc4

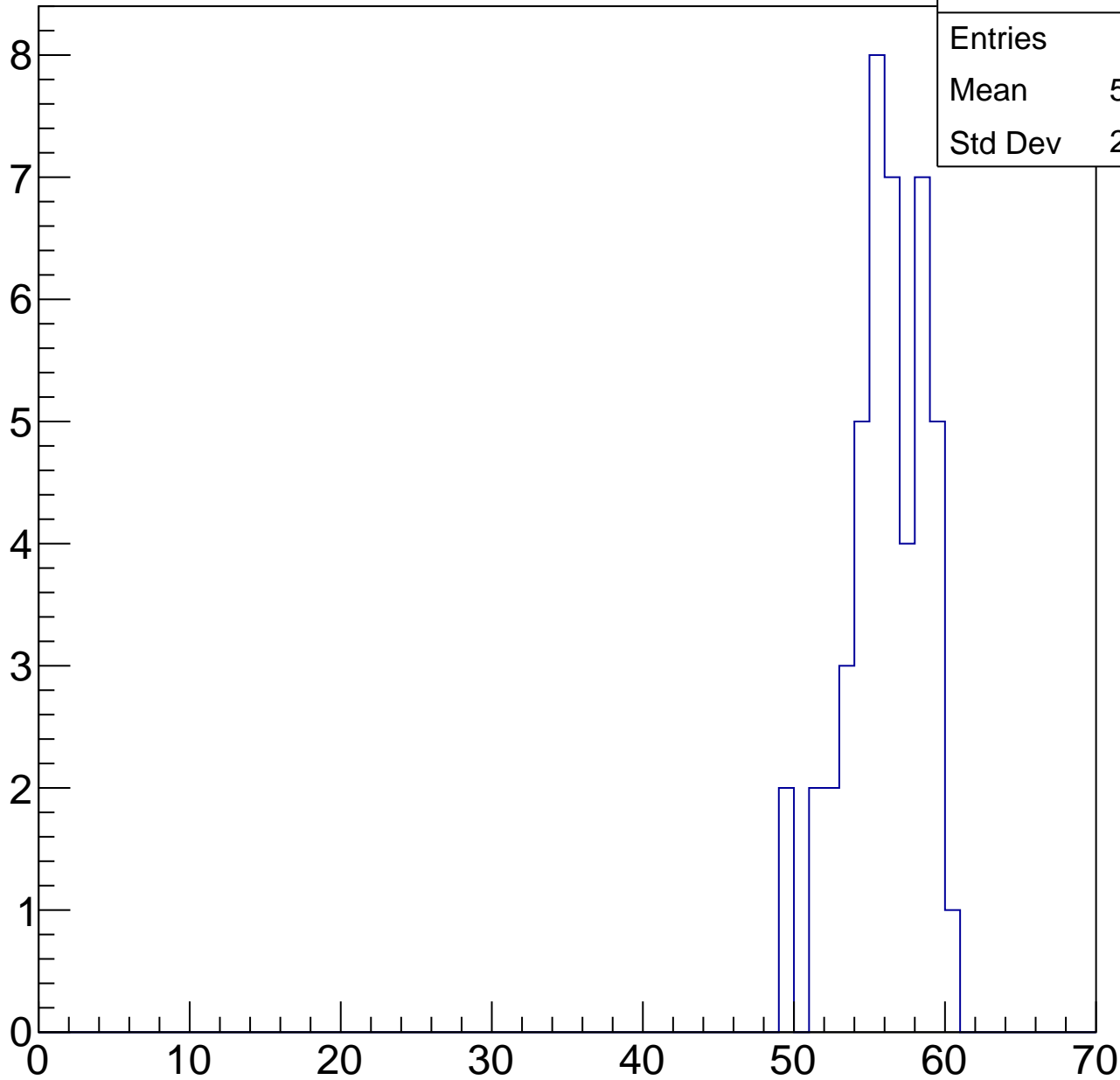
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	55.52
Std Dev	2.627

ampl

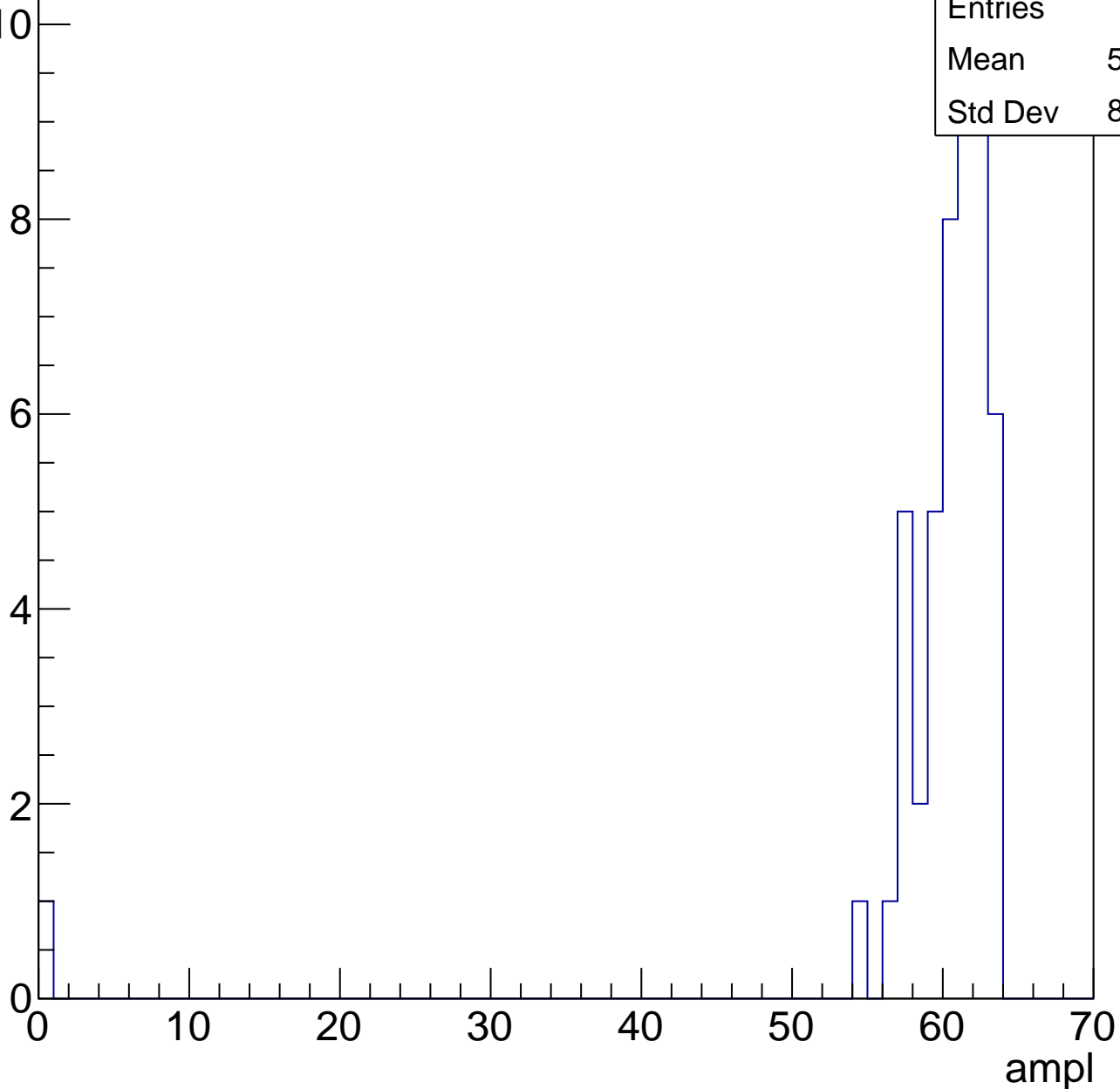


B0L002S, U9-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

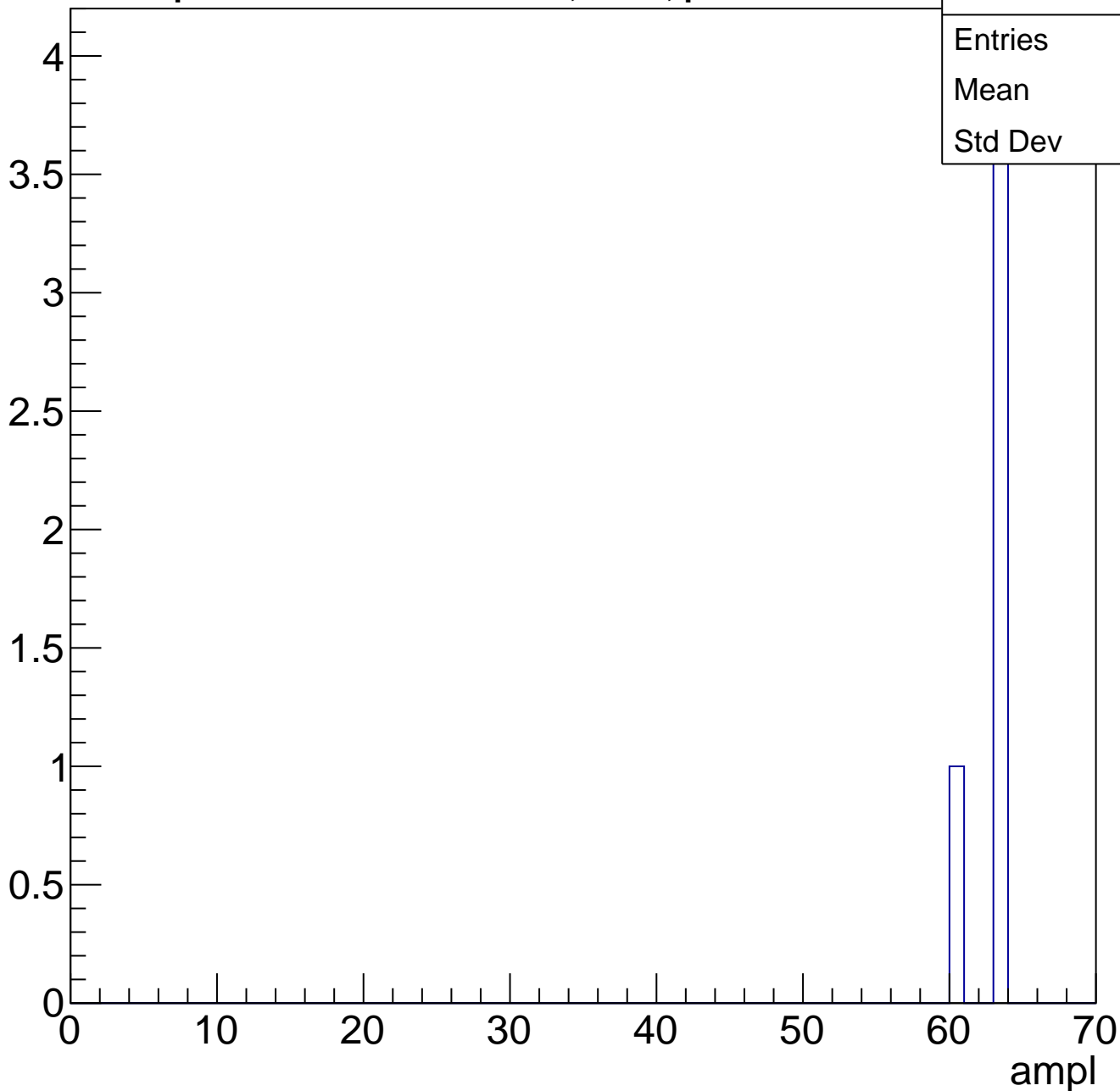
Entries	49
Mean	59.06
Std Dev	8.772



B0L002S, U9-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch55, adc0

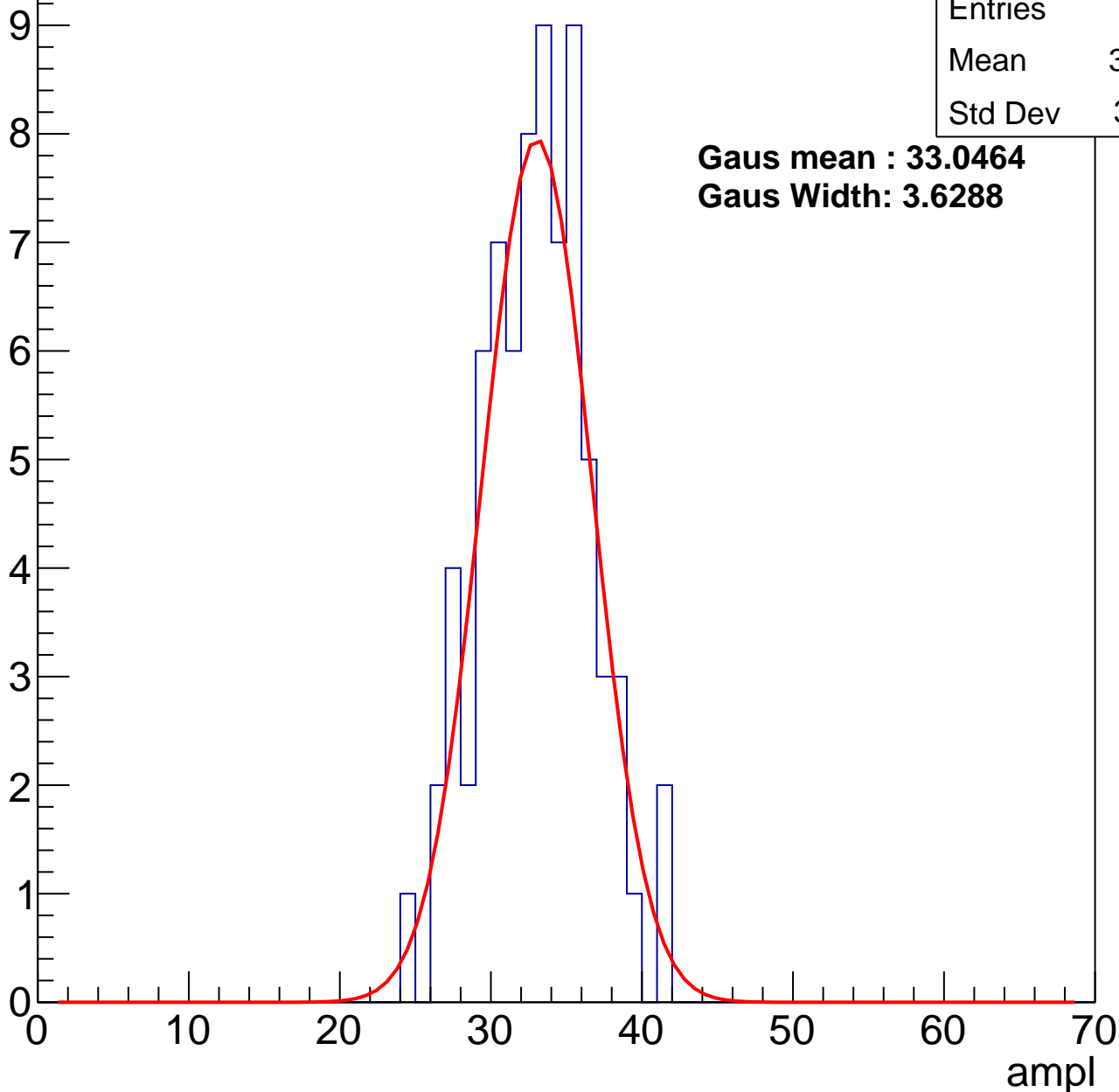
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	32.56
Std Dev	3.511

Gaus mean : 33.0464

Gaus Width: 3.6288



B0L002S, U9-ch55, adc1

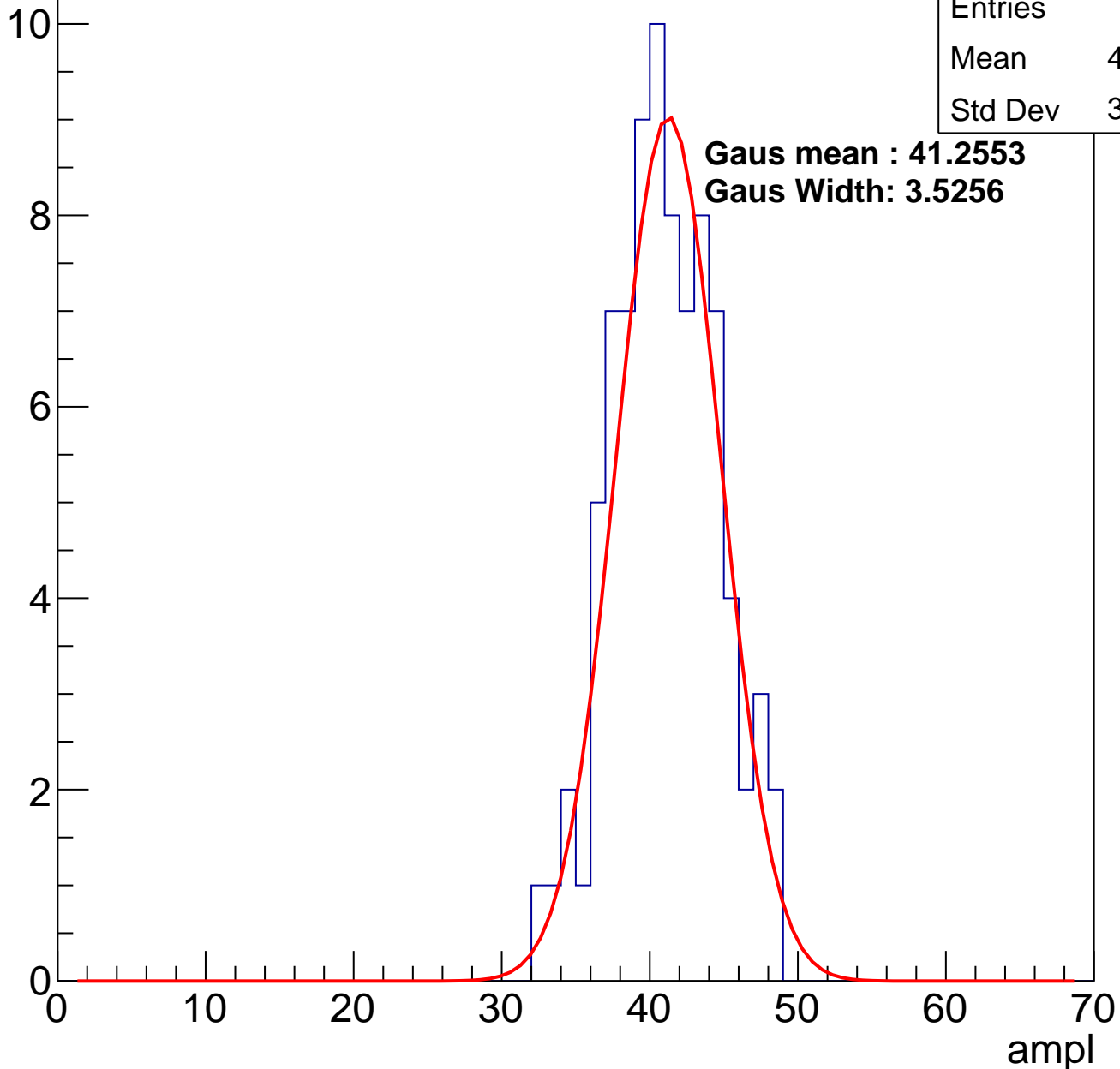
calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	40.56
Std Dev	3.503

Gaus mean : 41.2553

Gaus Width: 3.5256

Entry



B0L002S, U9-ch55, adc2

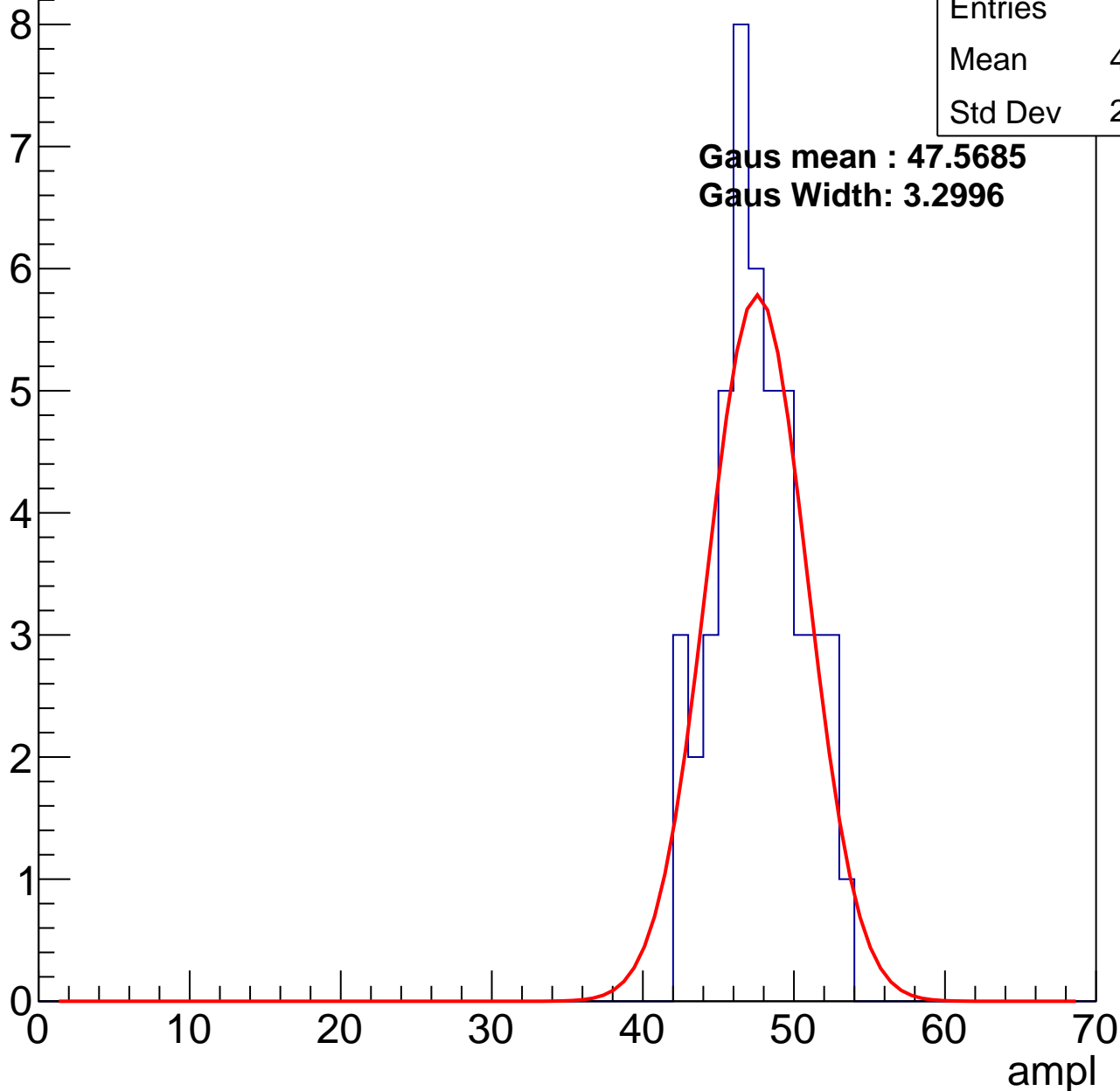
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	47.15
Std Dev	2.813

Gaus mean : 47.5685

Gaus Width: 3.2996

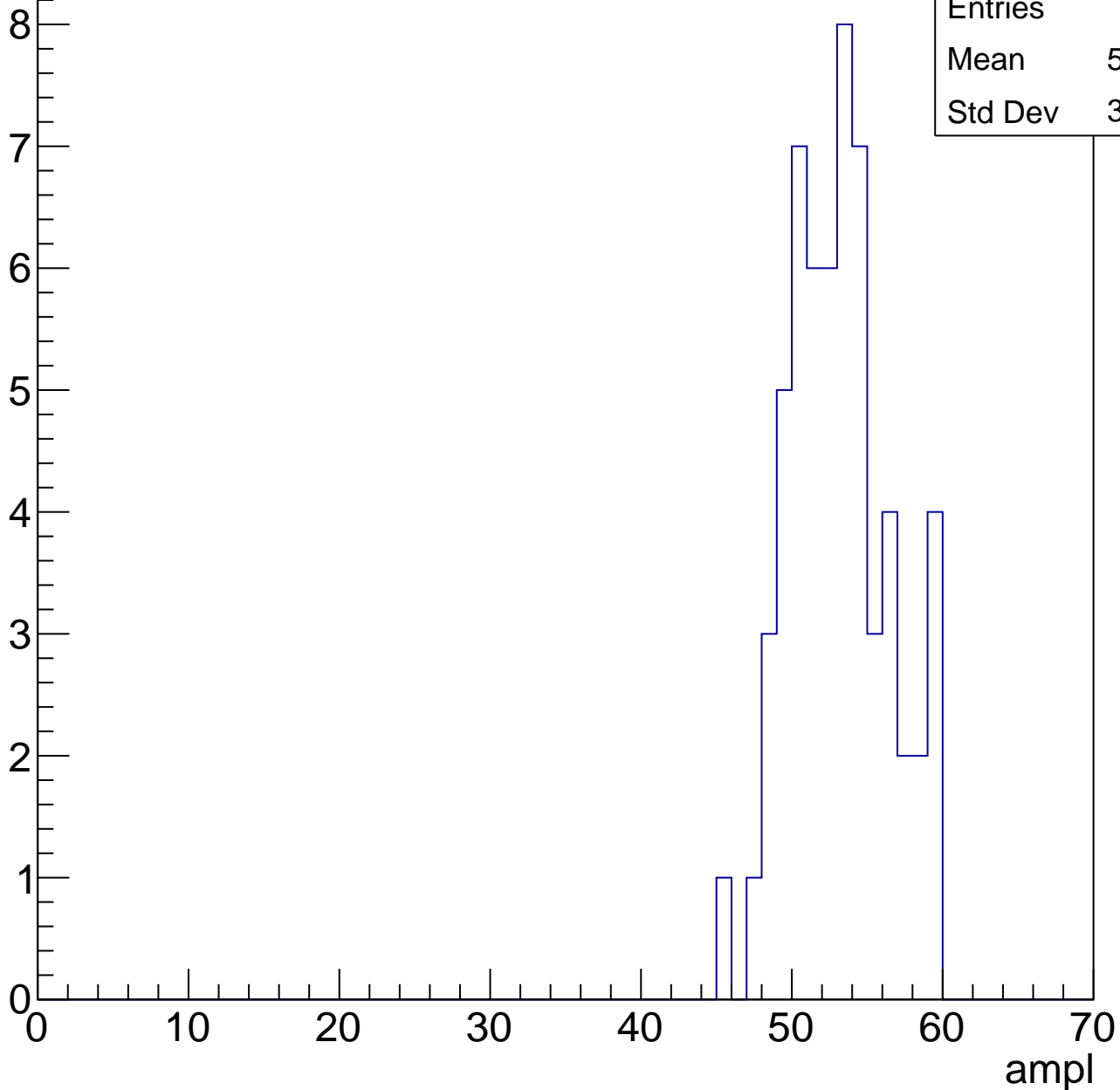


B0L002S, U9-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	52.64
Std Dev	3.246

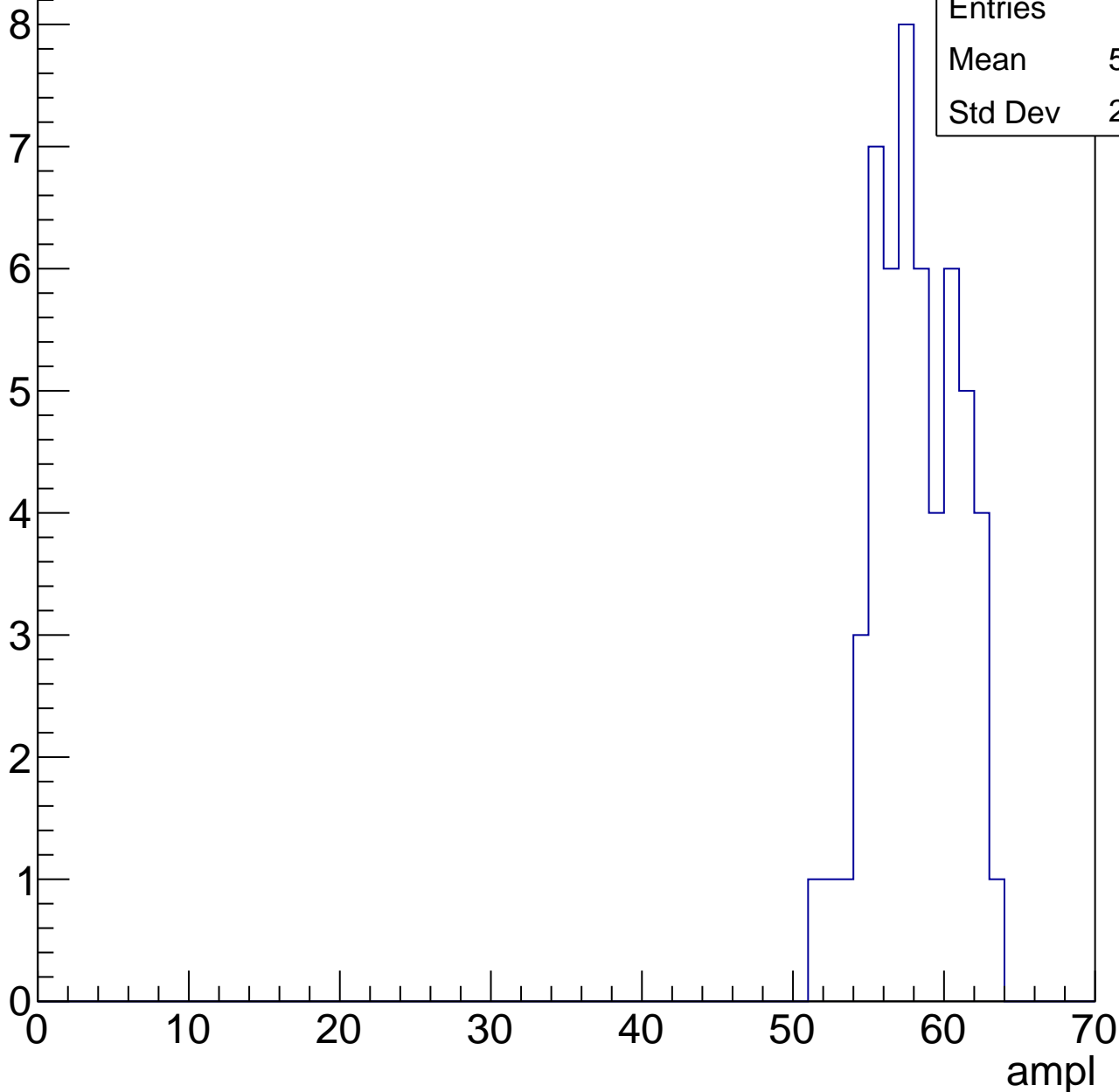


B0L002S, U9-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	57.64
Std Dev	2.775

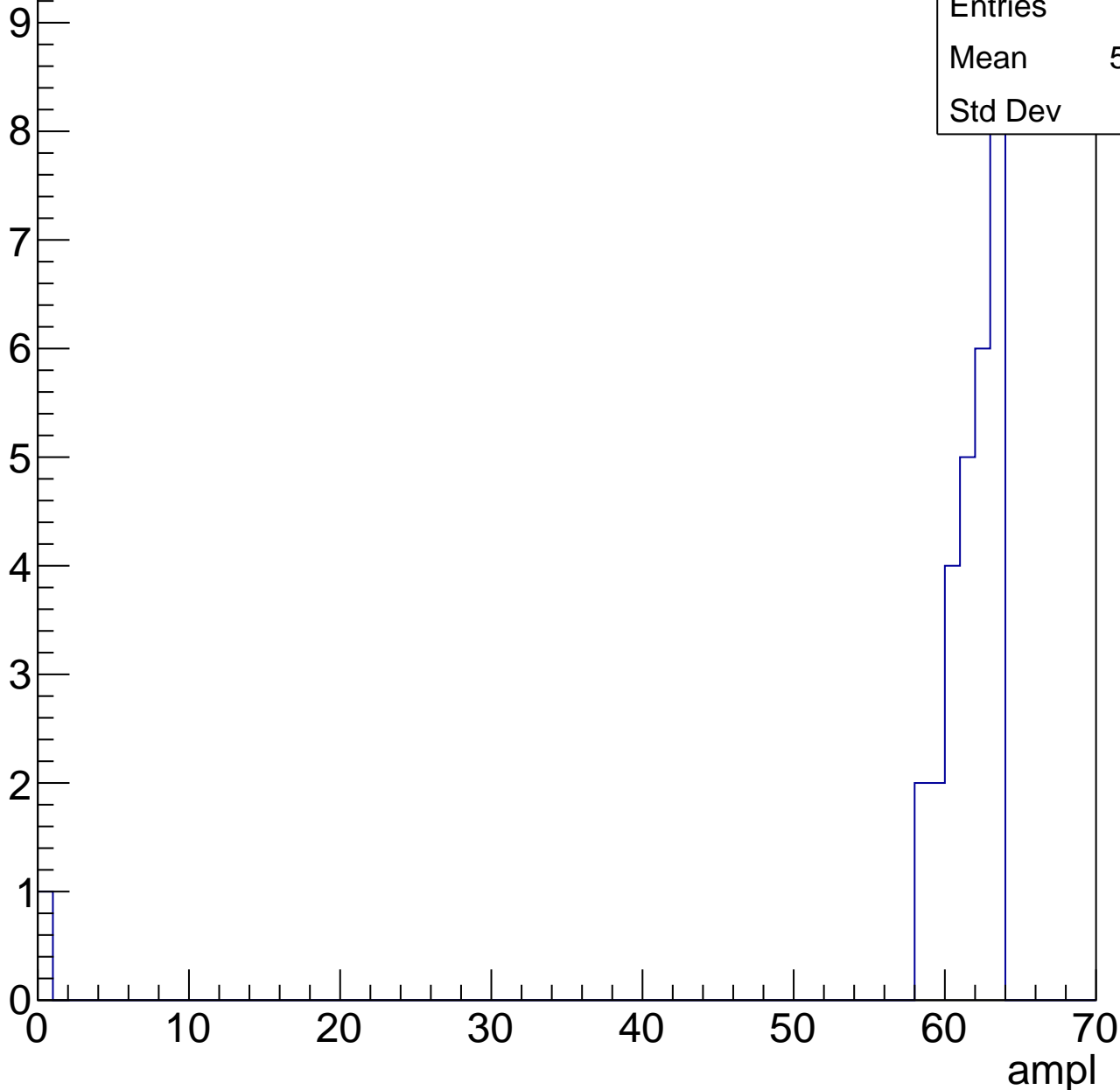


B0L002S, U9-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59.24
Std Dev	11.3



B0L002S, U9-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U9-ch56, adc0

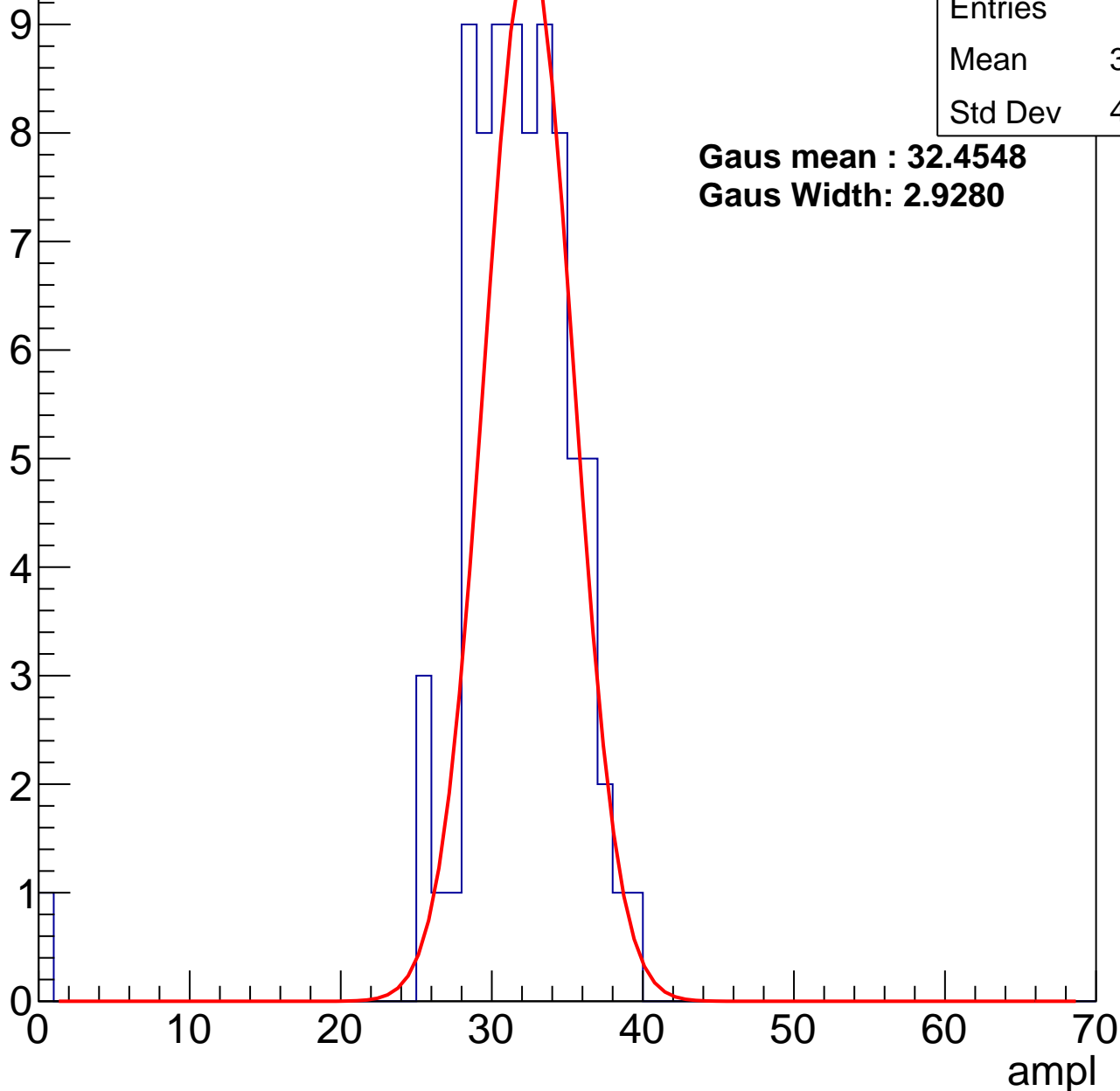
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	31.15
Std Dev	4.659

Gaus mean : 32.4548

Gaus Width: 2.9280



B0L002S, U9-ch56, adc1

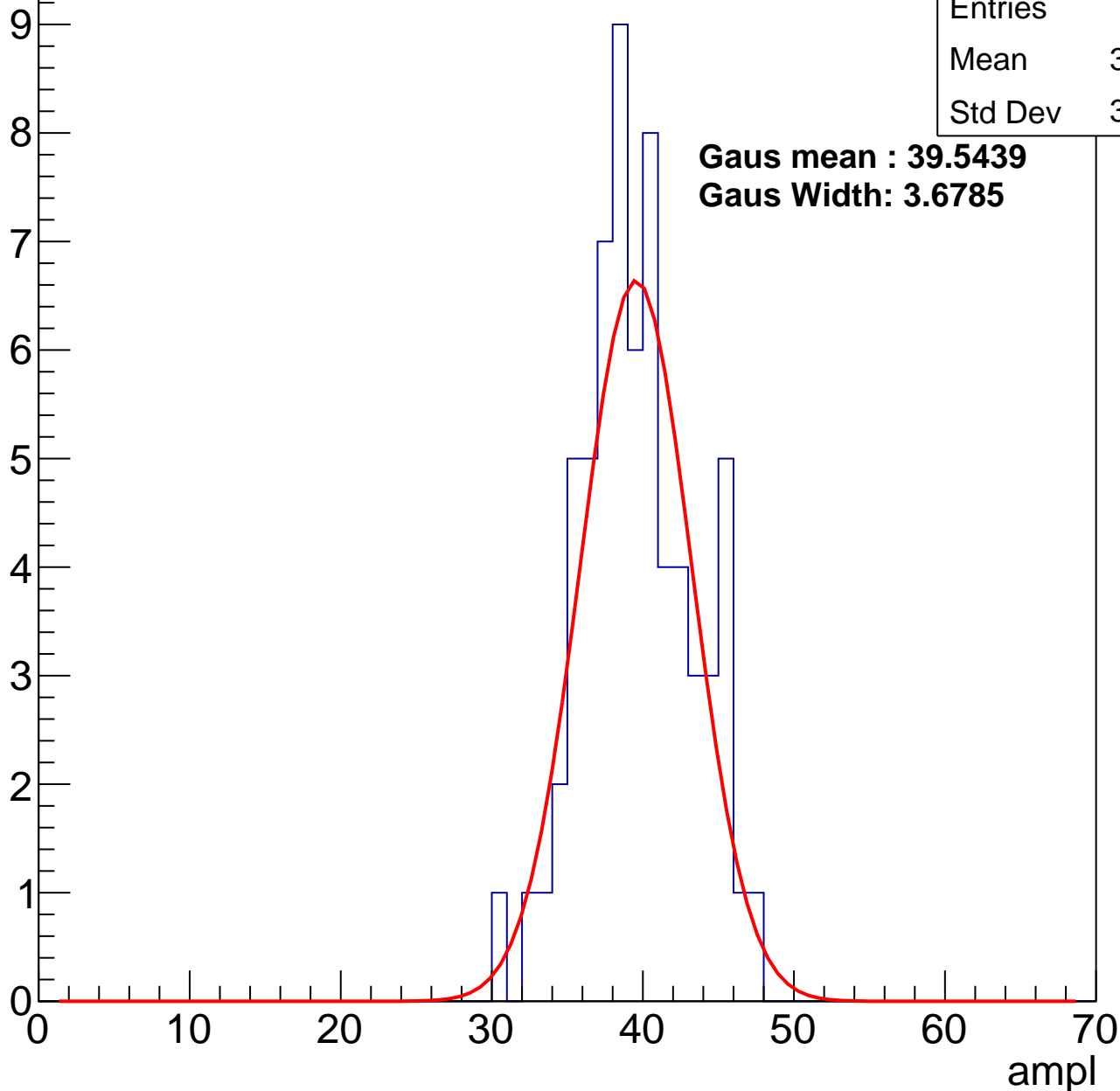
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	39.15
Std Dev	3.594

Gaus mean : 39.5439

Gaus Width: 3.6785



B0L002S, U9-ch56, adc2

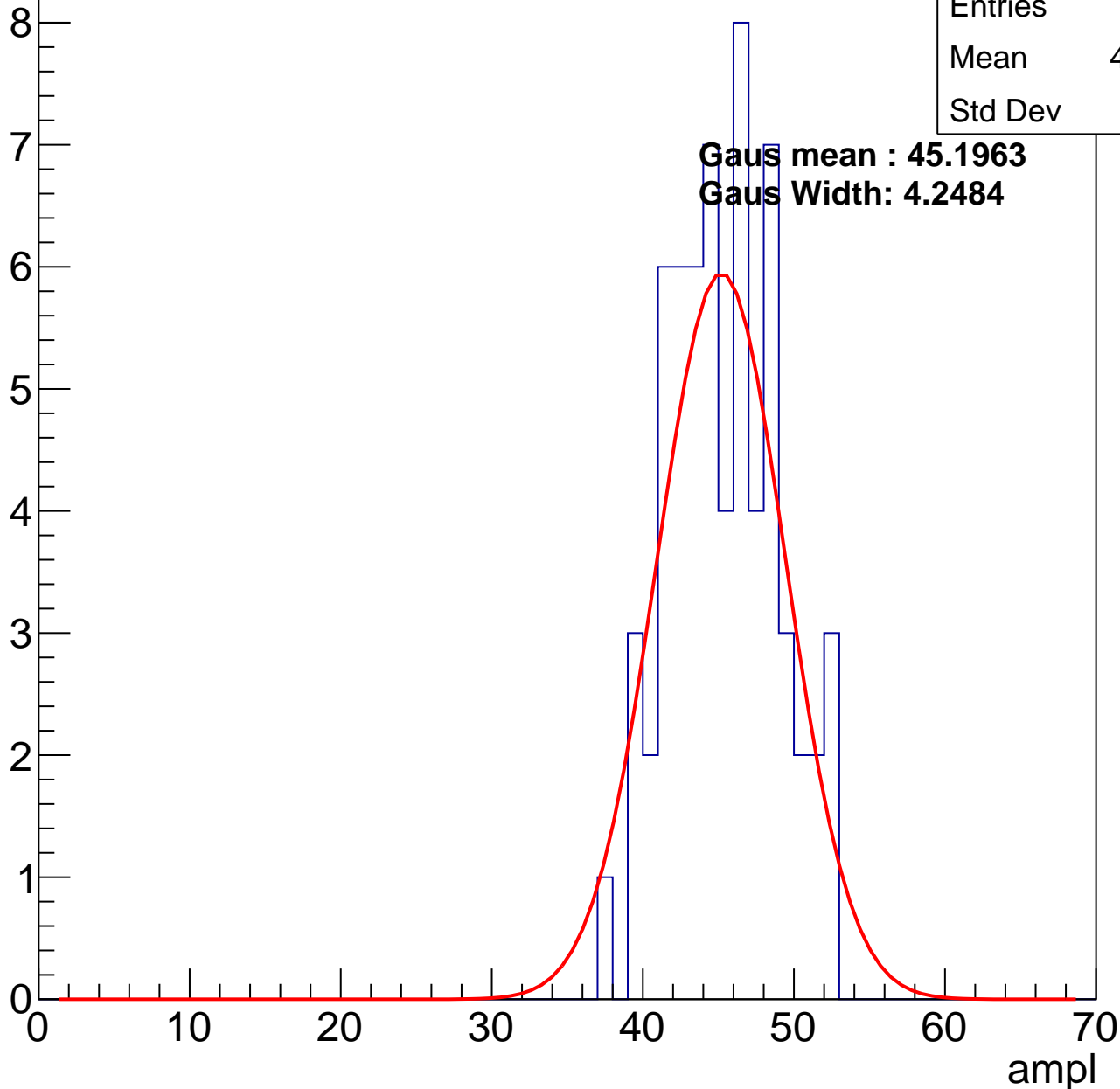
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.92
Std Dev	3.55

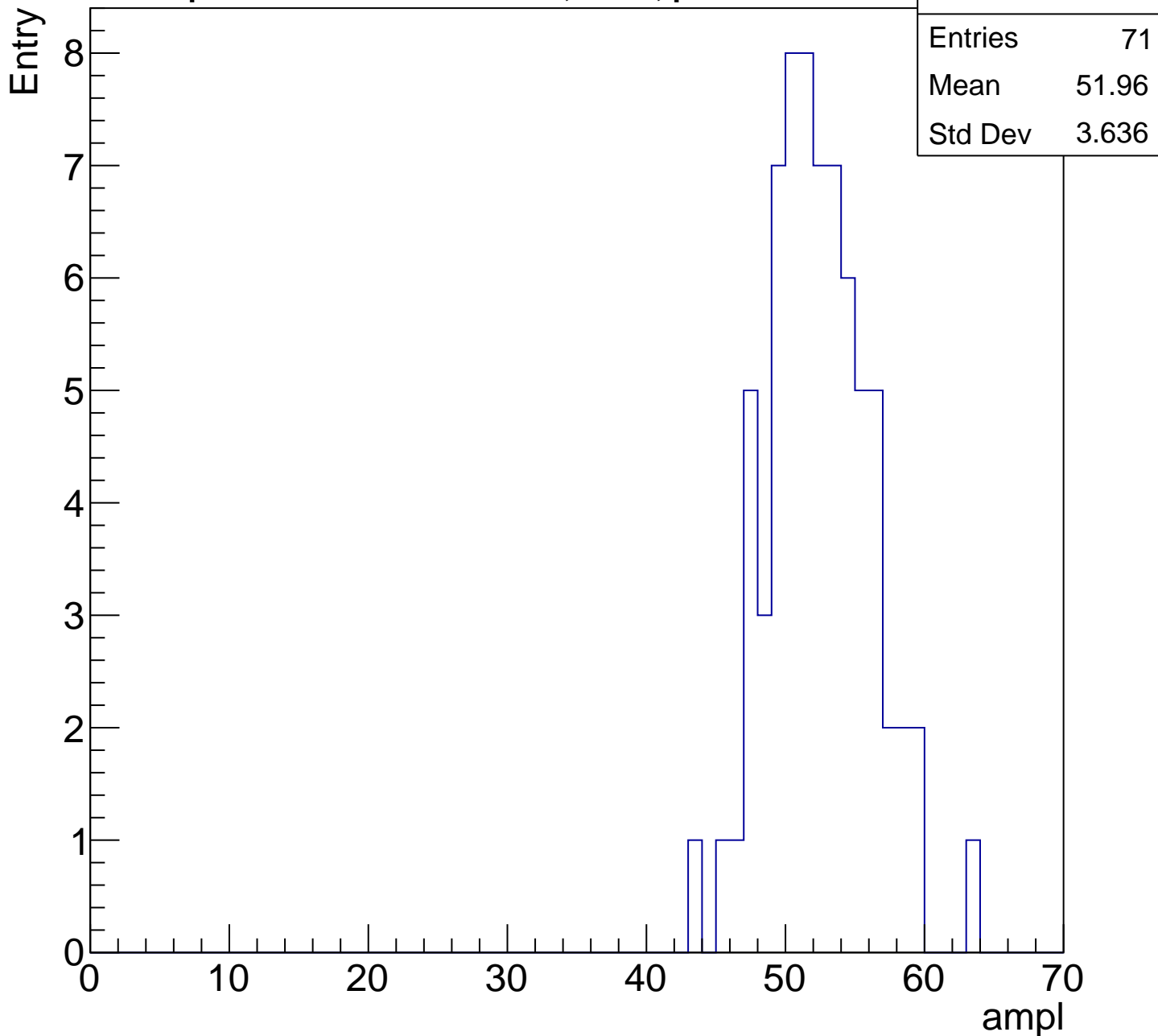
Gaus mean : 45.1663

Gaus Width: 4.2484



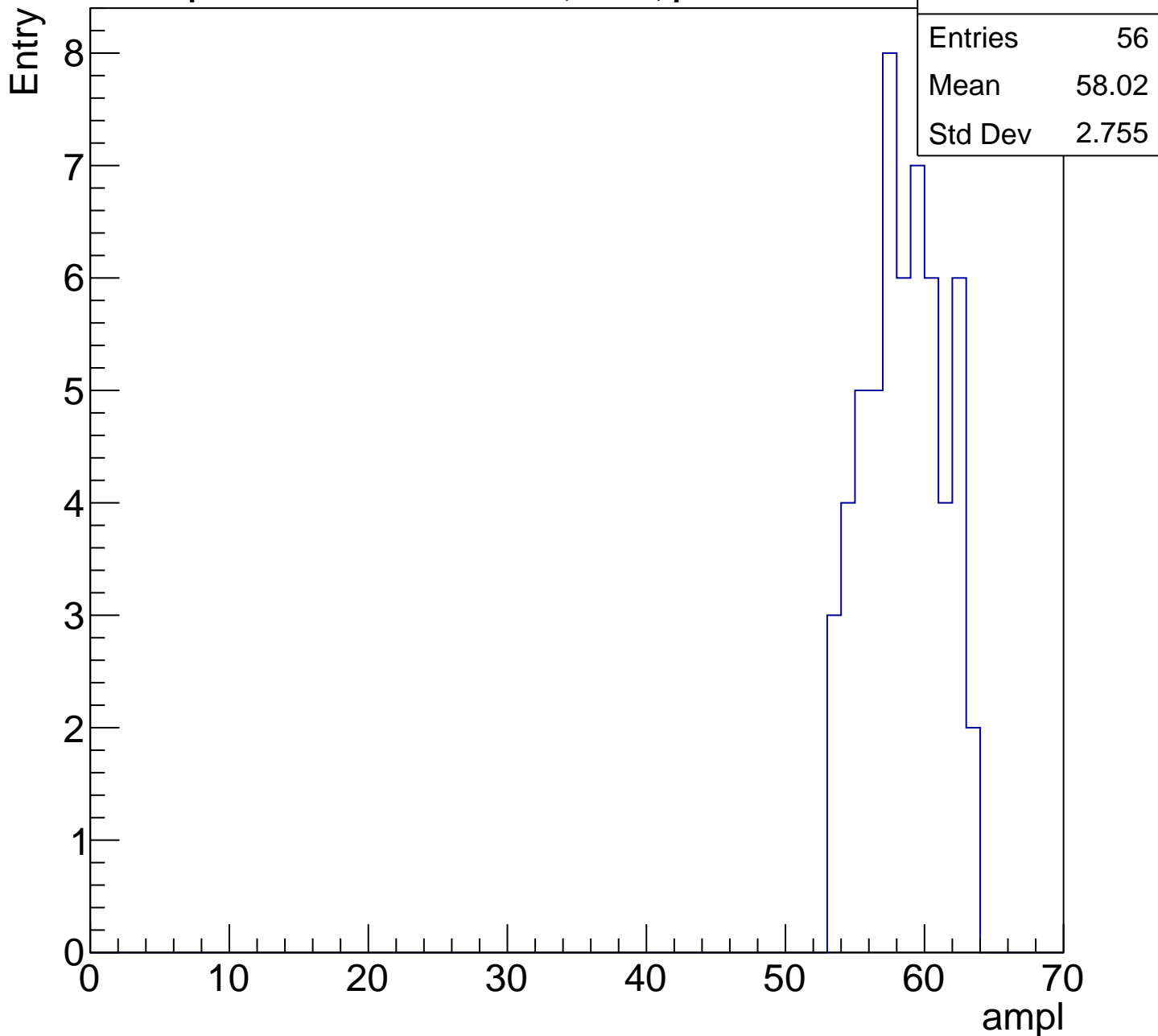
B0L002S, U9-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

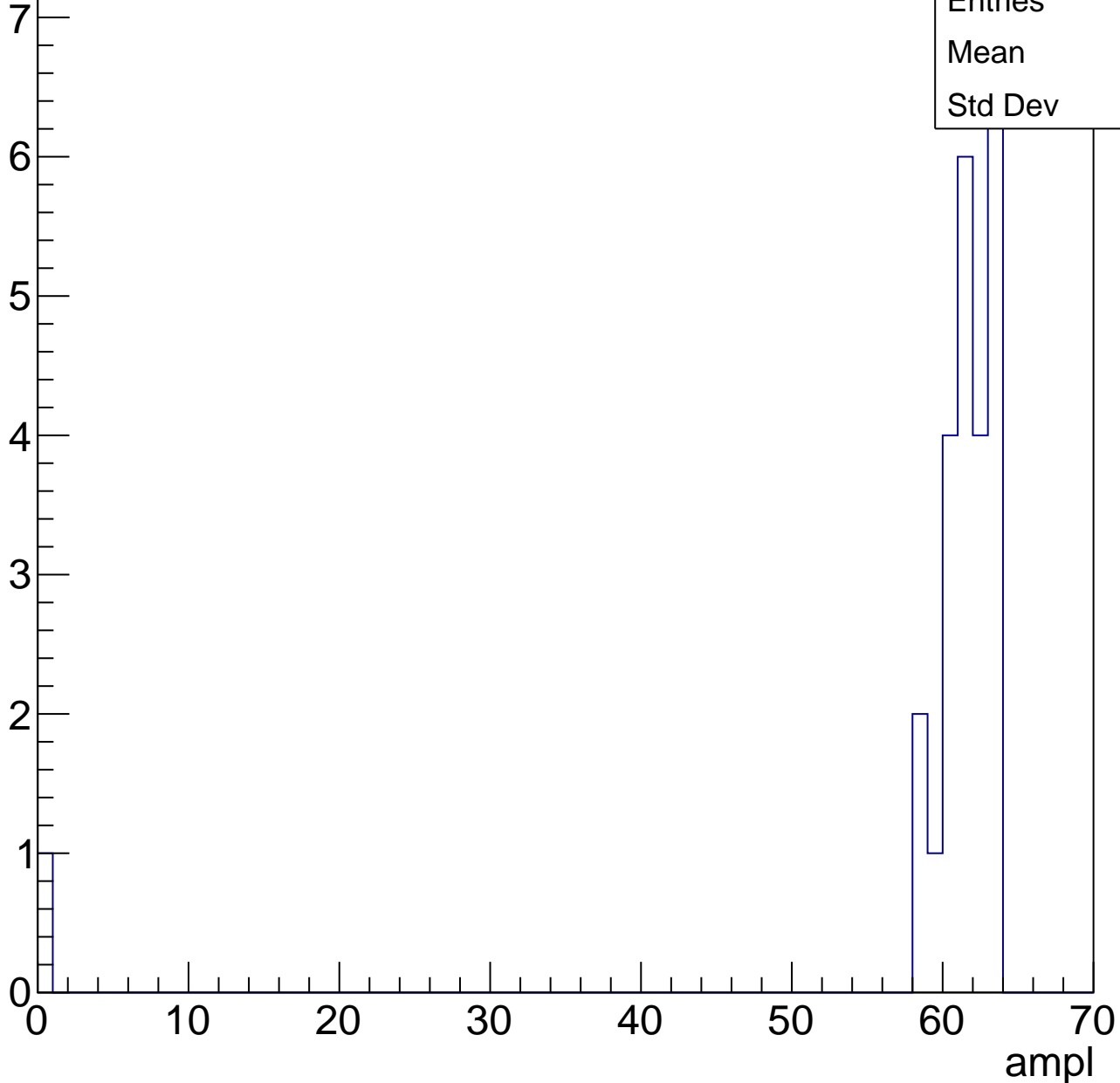


B0L002S, U9-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.8
Std Dev	12.1



B0L002S, U9-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch57, adc0

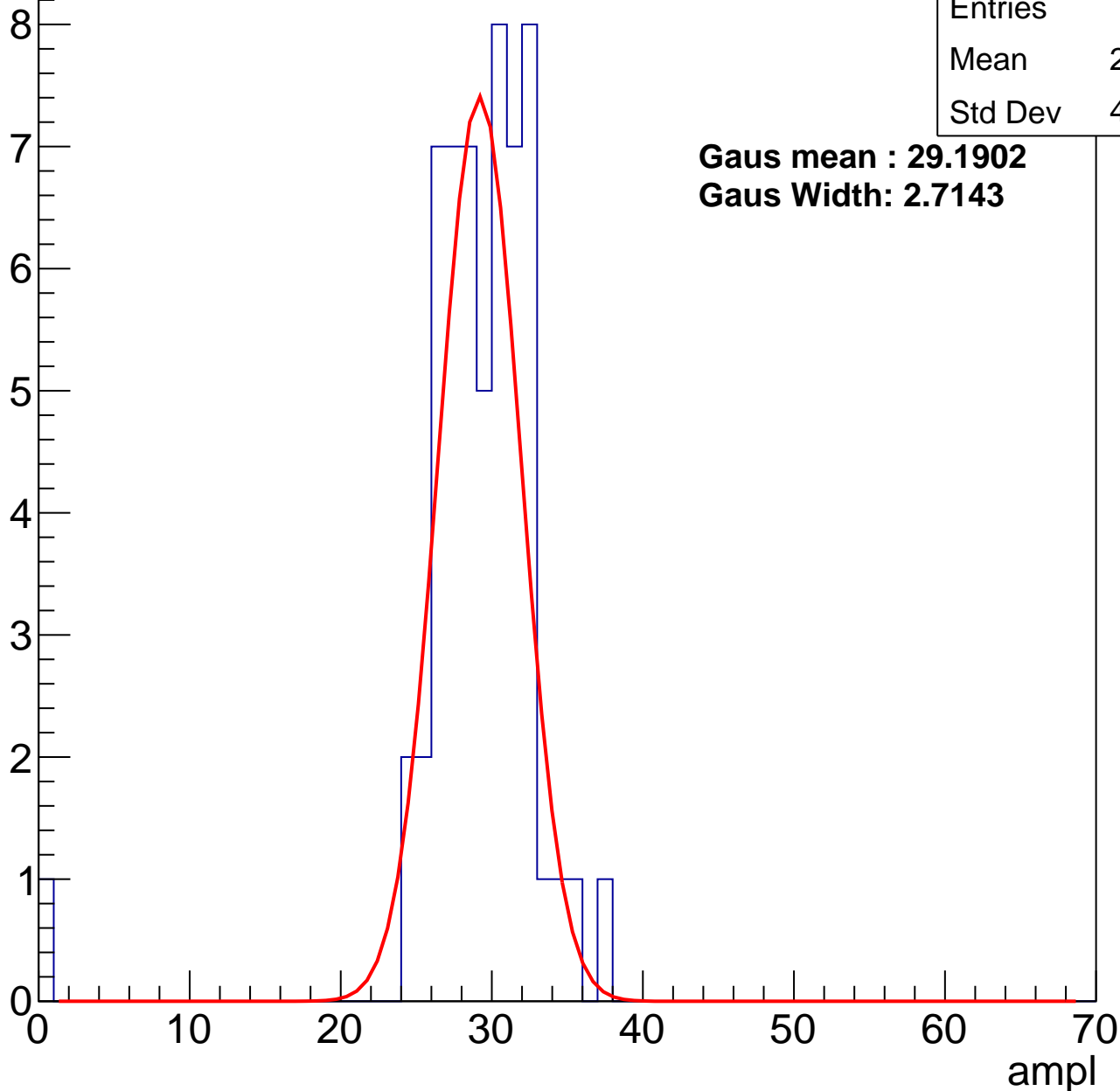
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	28.66
Std Dev	4.667

Gaus mean : 29.1902

Gaus Width: 2.7143



B0L002S, U9-ch57, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	35.72
Std Dev	3.257

Gaus mean : 36.4353

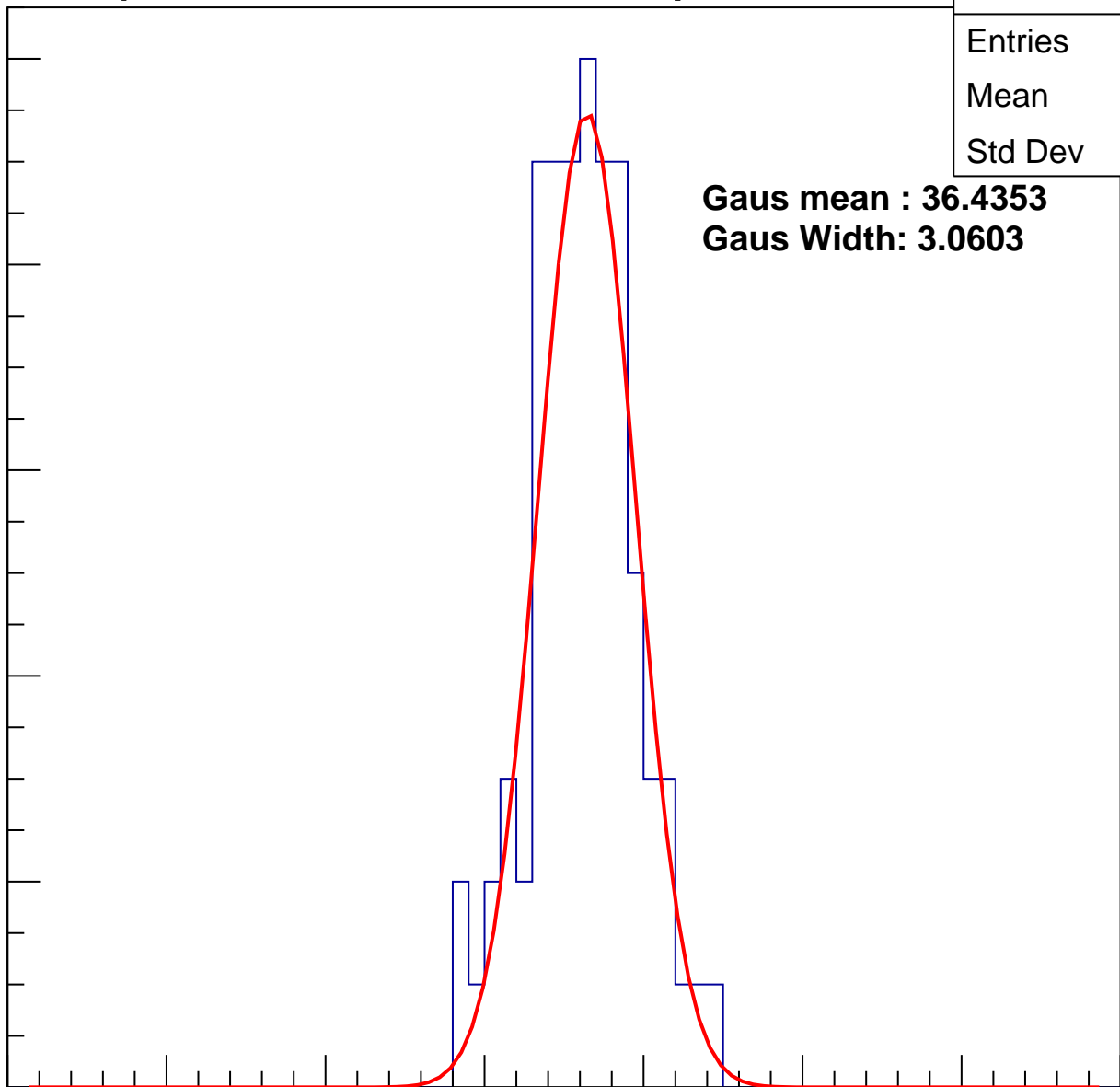
Gaus Width: 3.0603

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch57, adc2

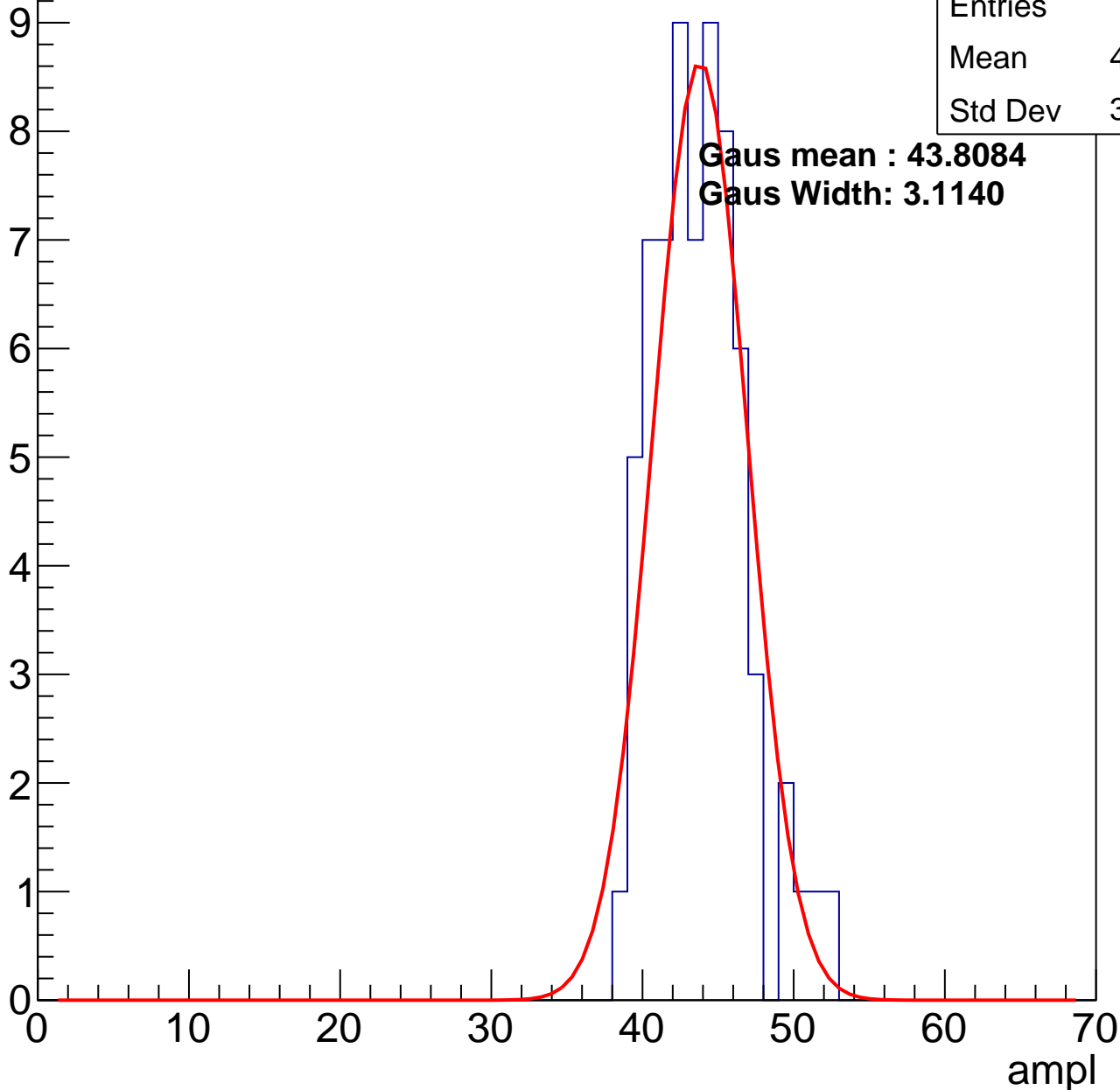
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.33
Std Dev	3.009

Gaus mean : 43.8084

Gaus Width: 3.1140



B0L002S, U9-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1

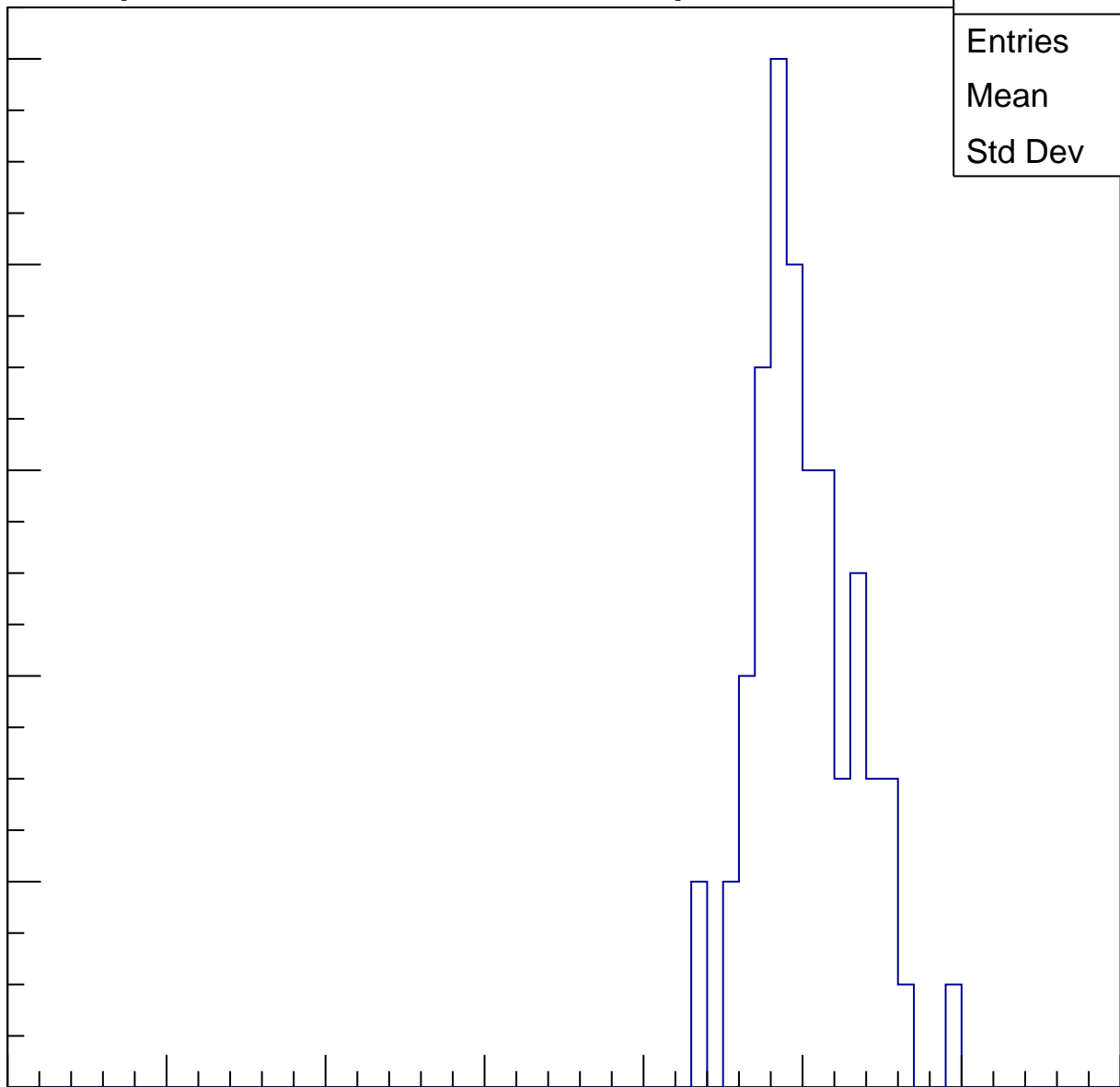
Entries	61
Mean	49.67
Std Dev	3.187

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	55.11
Std Dev	7.132

Entry

10

8

6

4

2

0

0

10

20

30

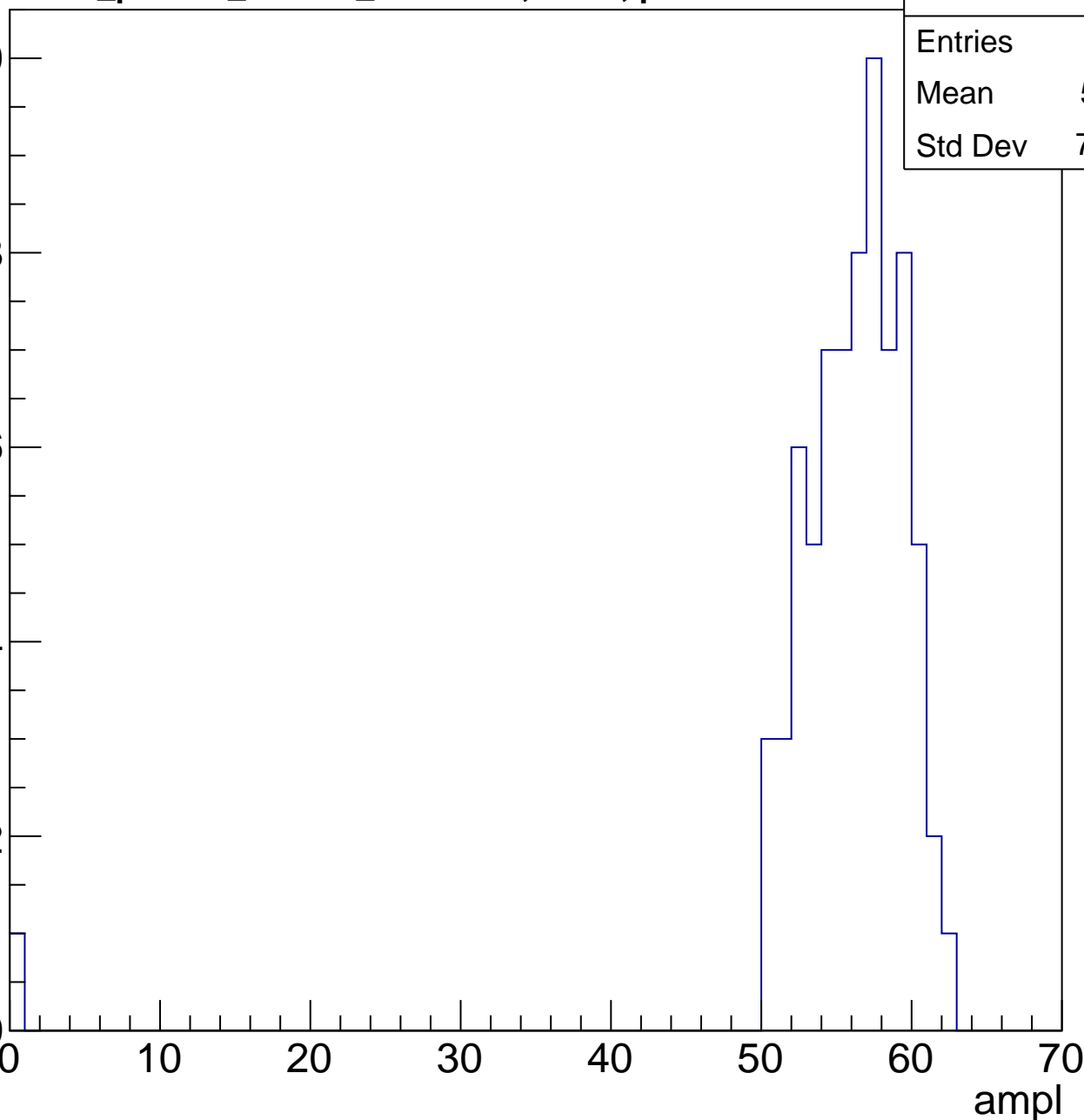
40

50

ampl

60

70



B0L002S, U9-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	37
Mean	61.22
Std Dev	1.711

B0L002S, U9-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch58, adc0

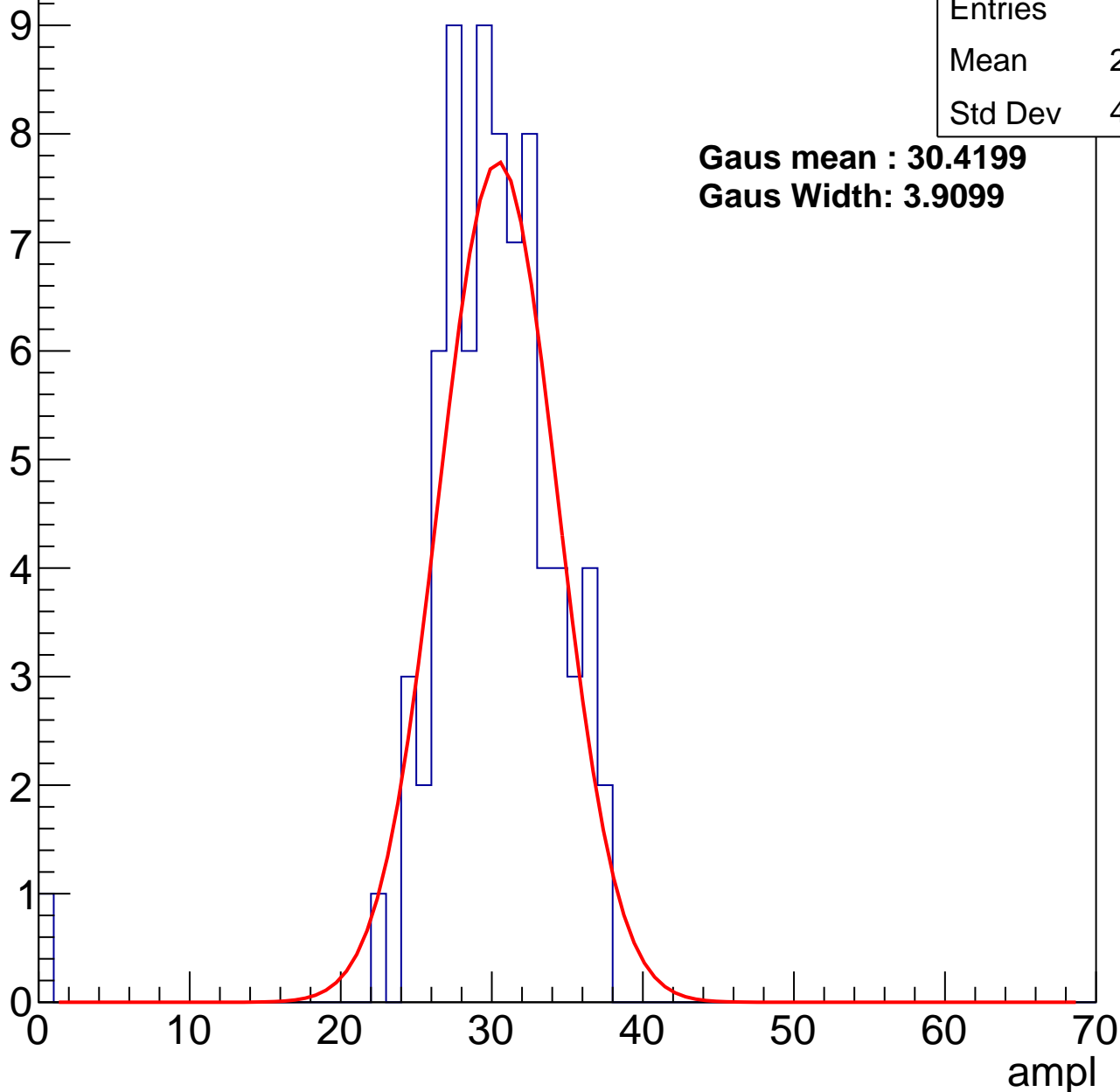
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	29.56
Std Dev	4.799

Gaus mean : 30.4199

Gaus Width: 3.9099



B0L002S, U9-ch58, adc1

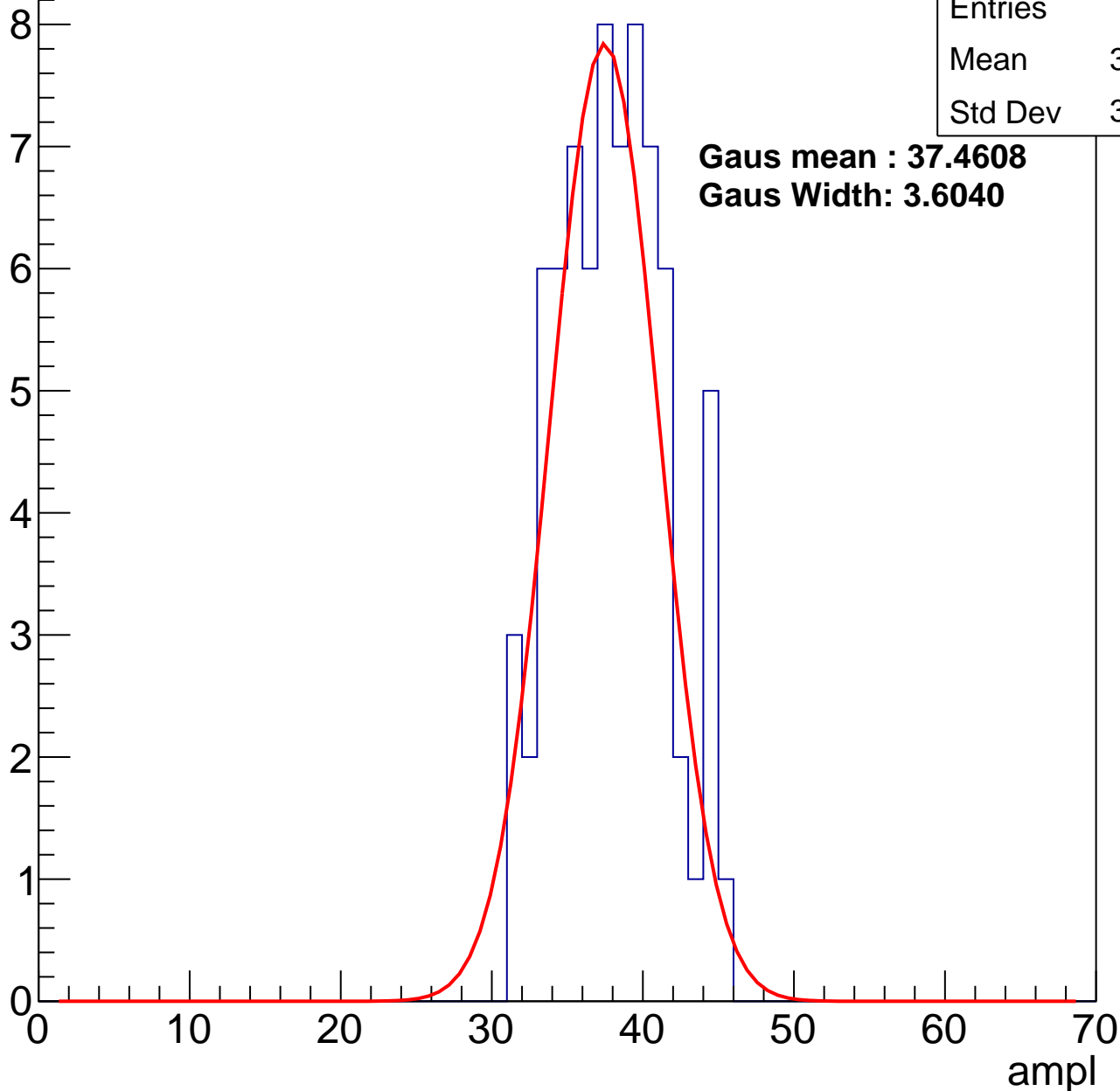
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.49
Std Dev	3.496

Gaus mean : 37.4608

Gaus Width: 3.6040



B0L002S, U9-ch58, adc2

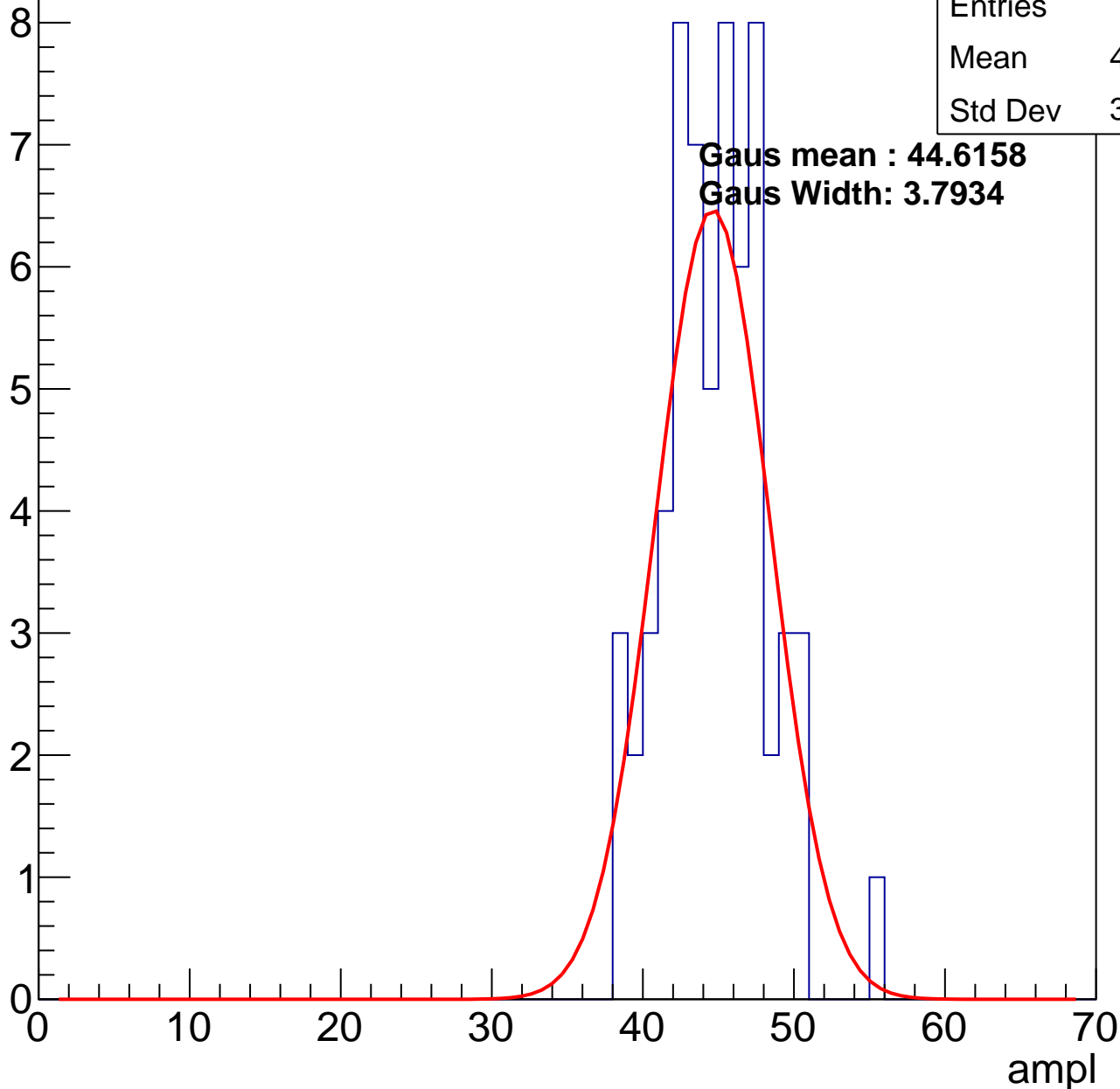
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.33
Std Dev	3.367

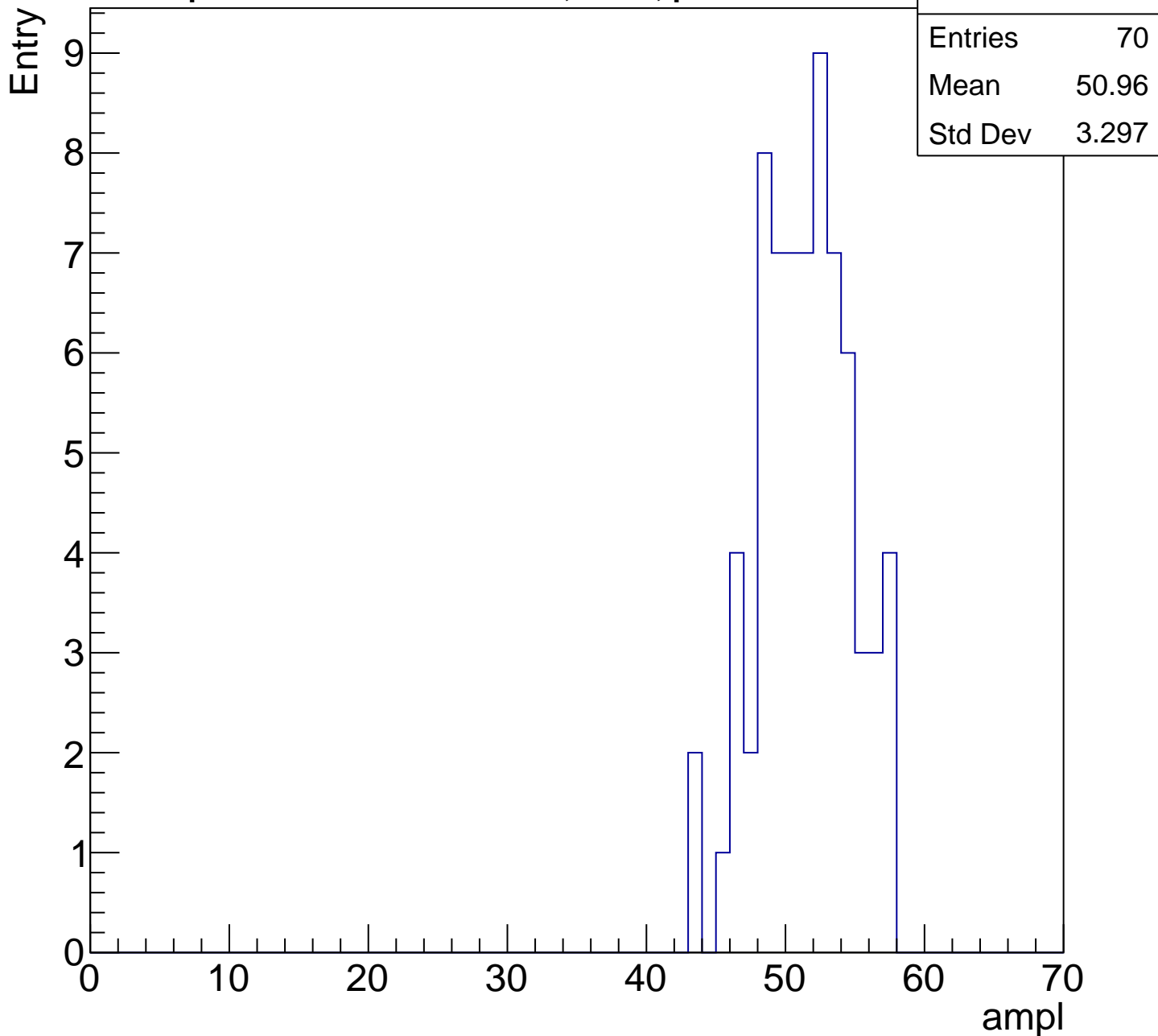
Gaus mean : 44.6158

Gaus Width: 3.7934



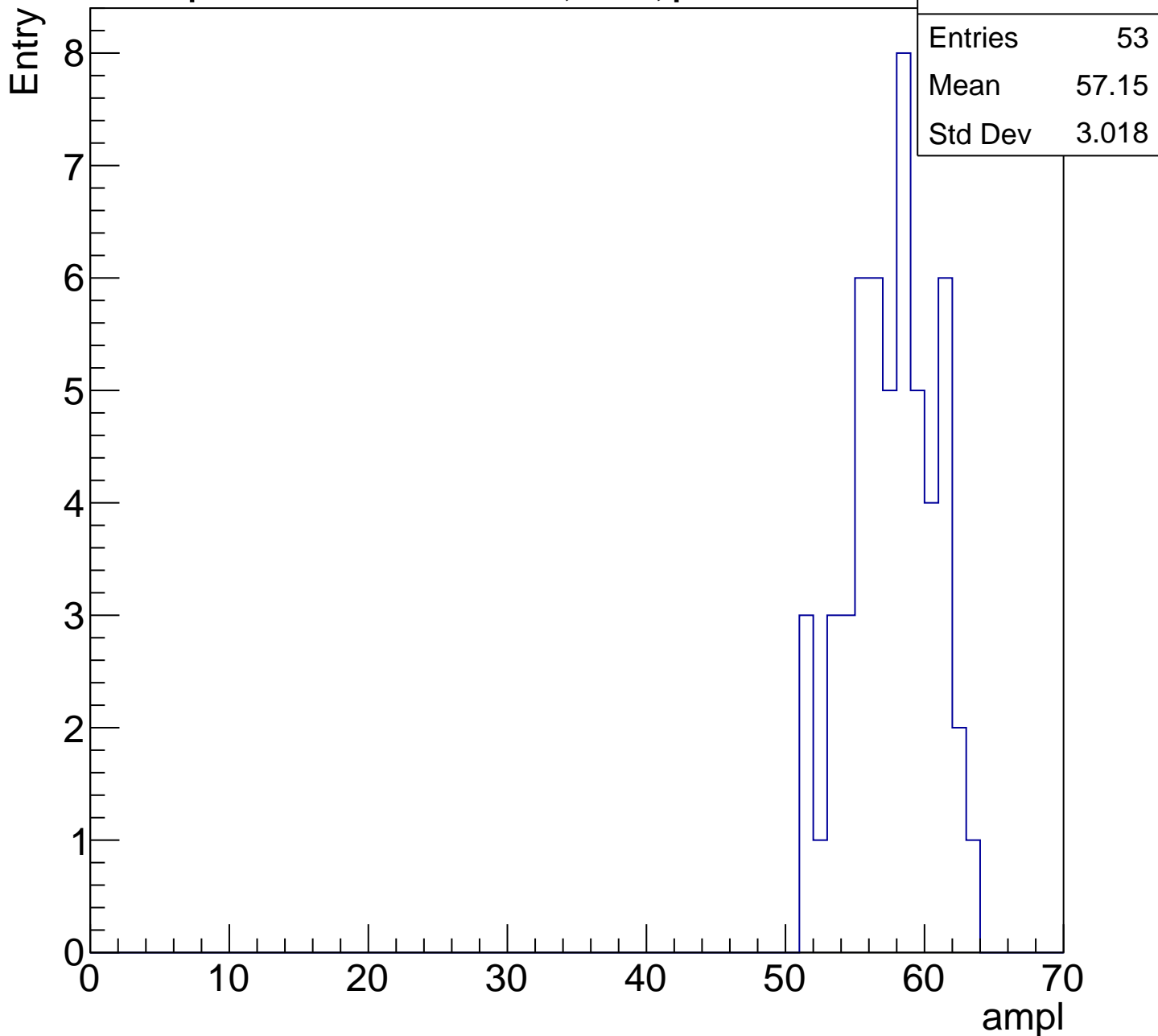
B0L002S, U9-ch58, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

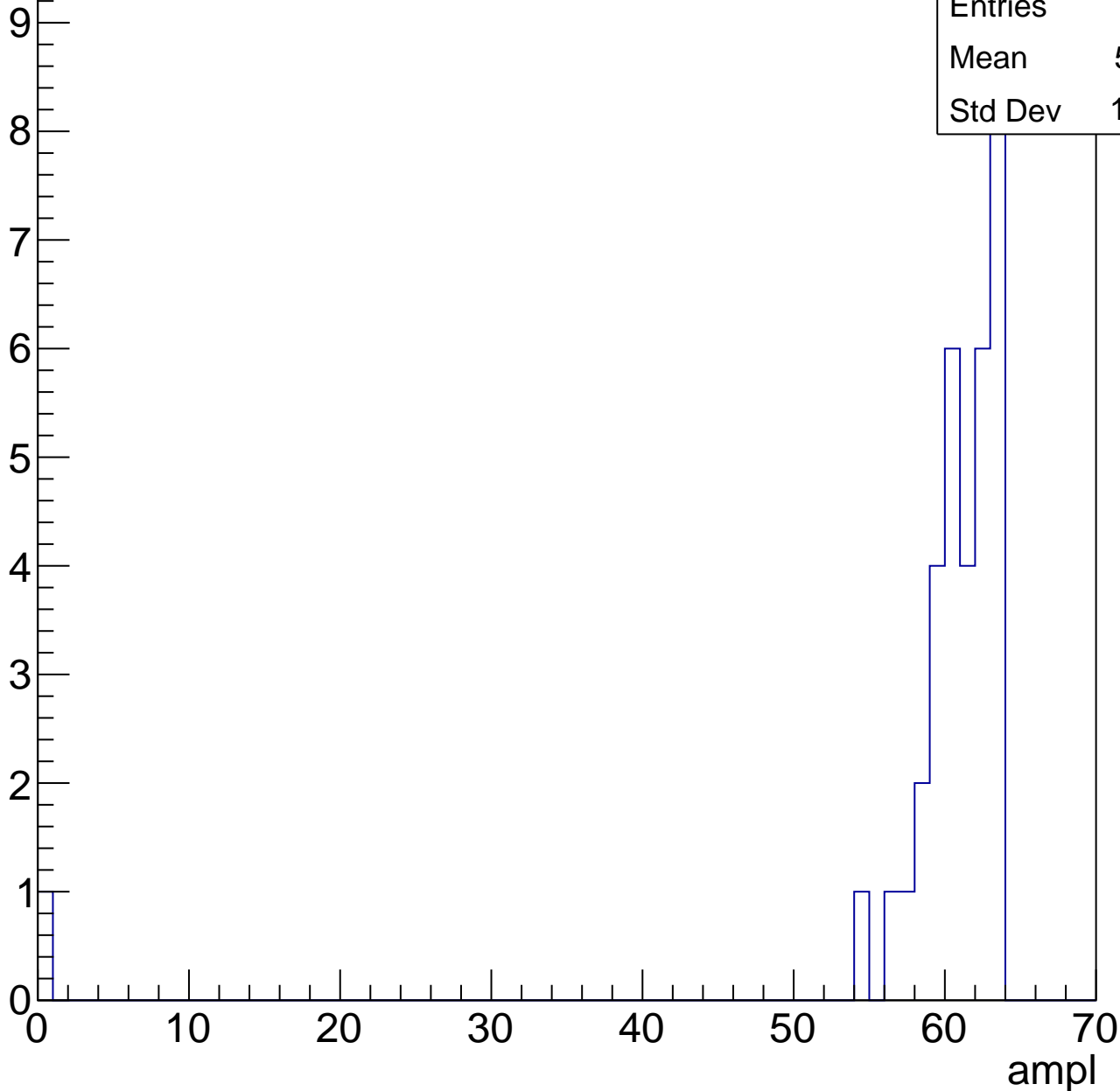


B0L002S, U9-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

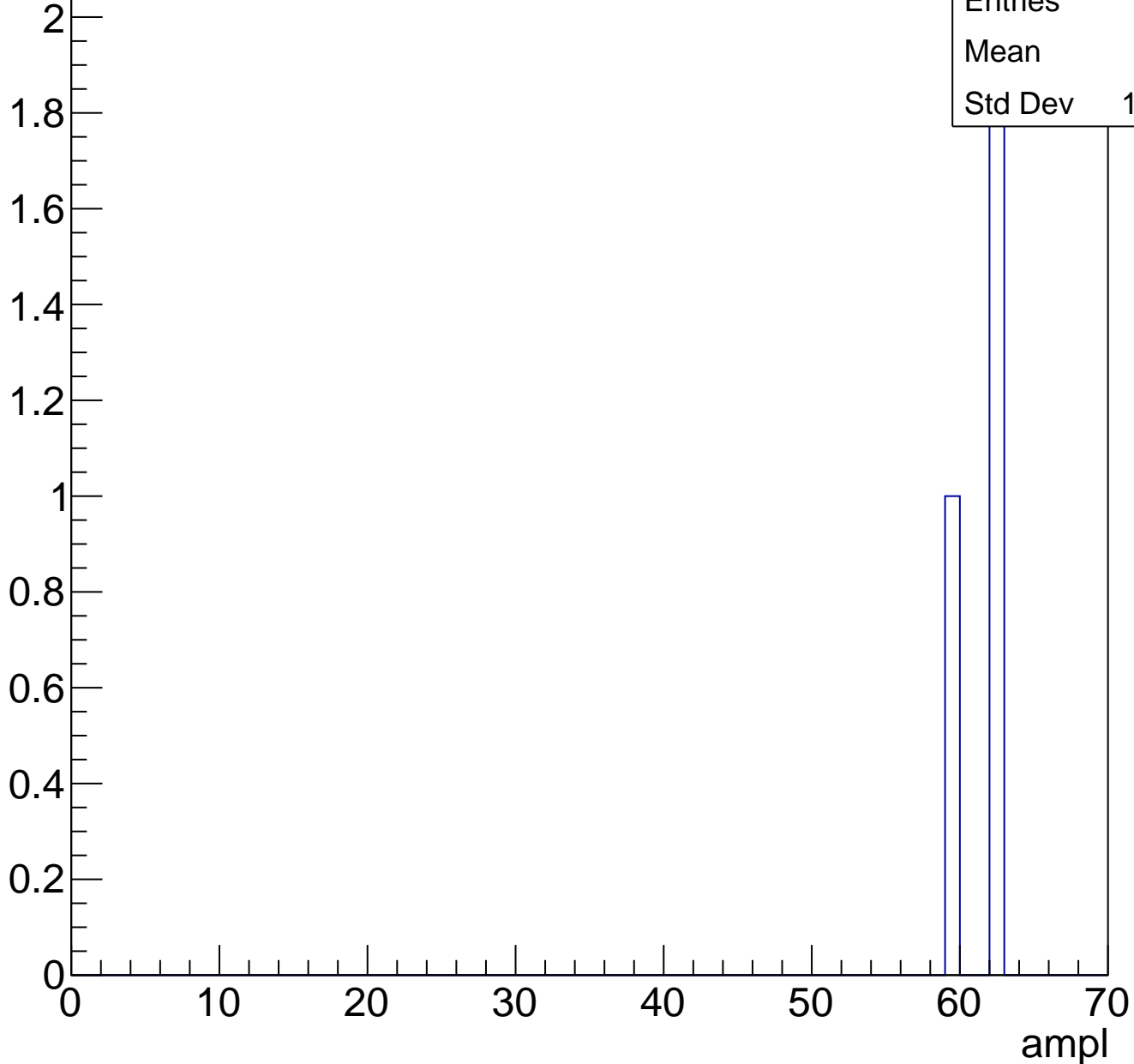
Entries	35
Mean	58.91
Std Dev	10.34



B0L002S, U9-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch59, adc0

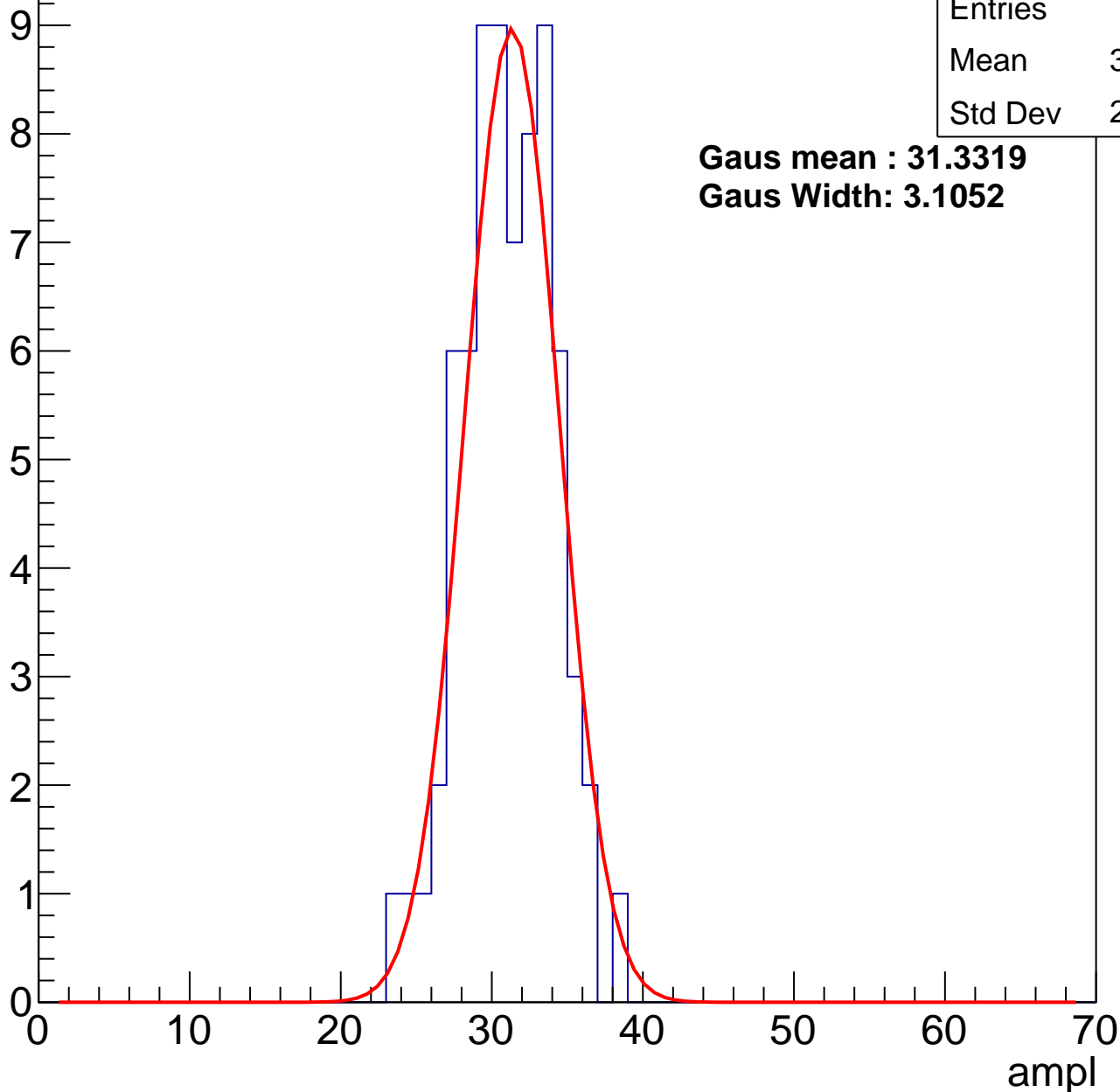
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.62
Std Dev	2.976

Gaus mean : 31.3319

Gaus Width: 3.1052

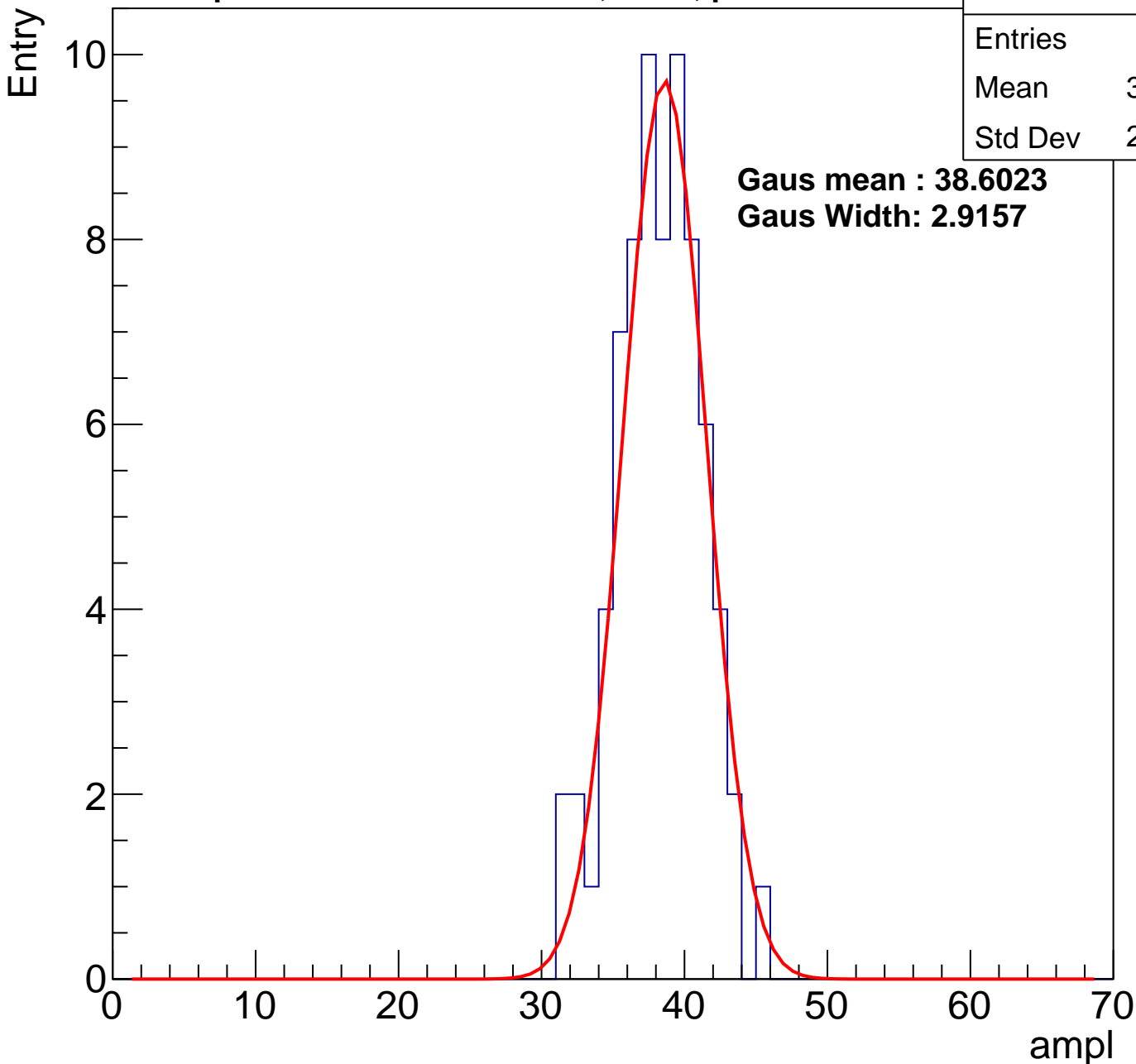


B0L002S, U9-ch59, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	37.77
Std Dev	2.912

Gaus mean : 38.6023
Gaus Width: 2.9157



B0L002S, U9-ch59, adc2

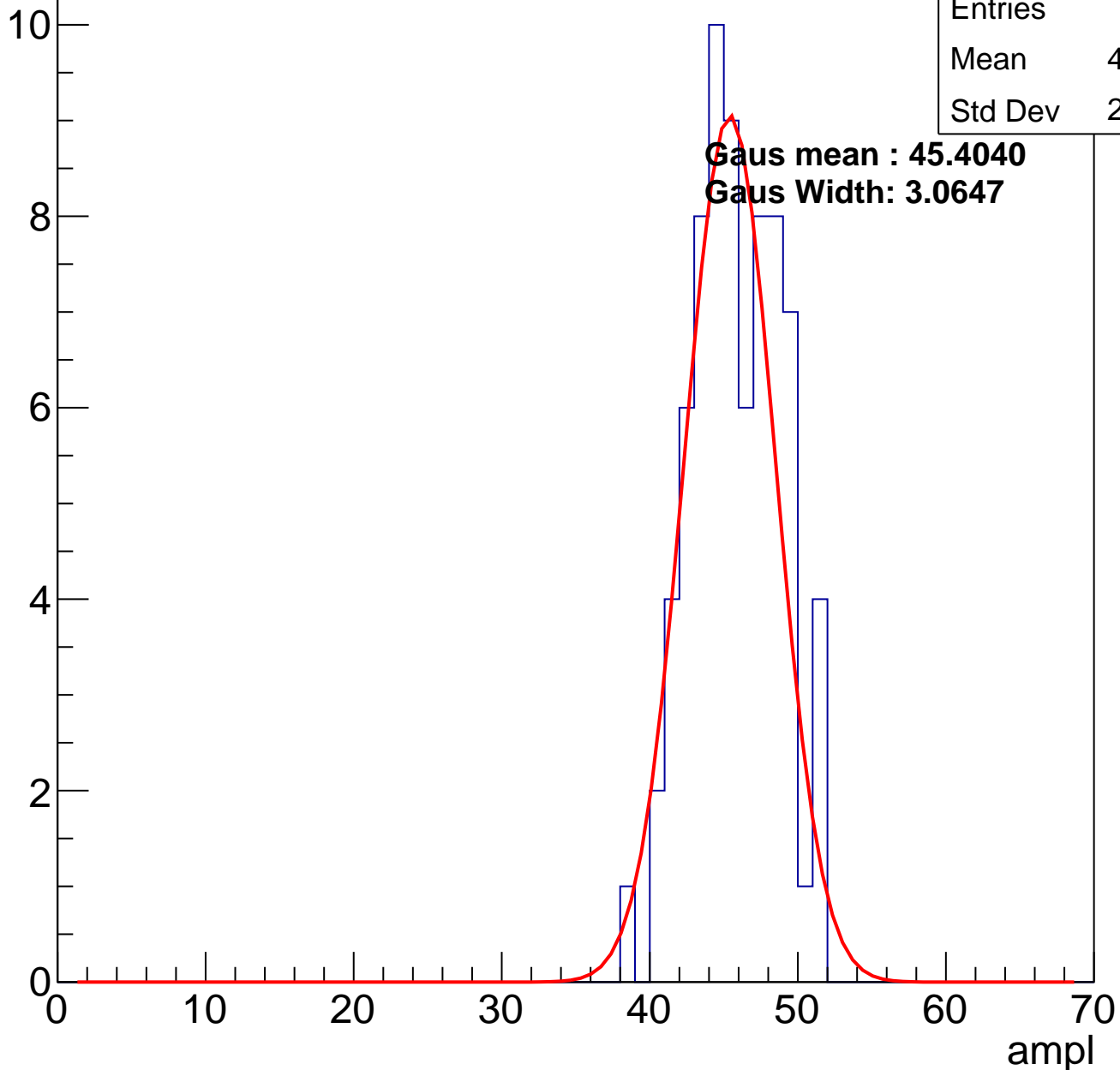
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	45.35
Std Dev	2.943

Gaus mean : 45.4040

Gaus Width: 3.0647

Entry

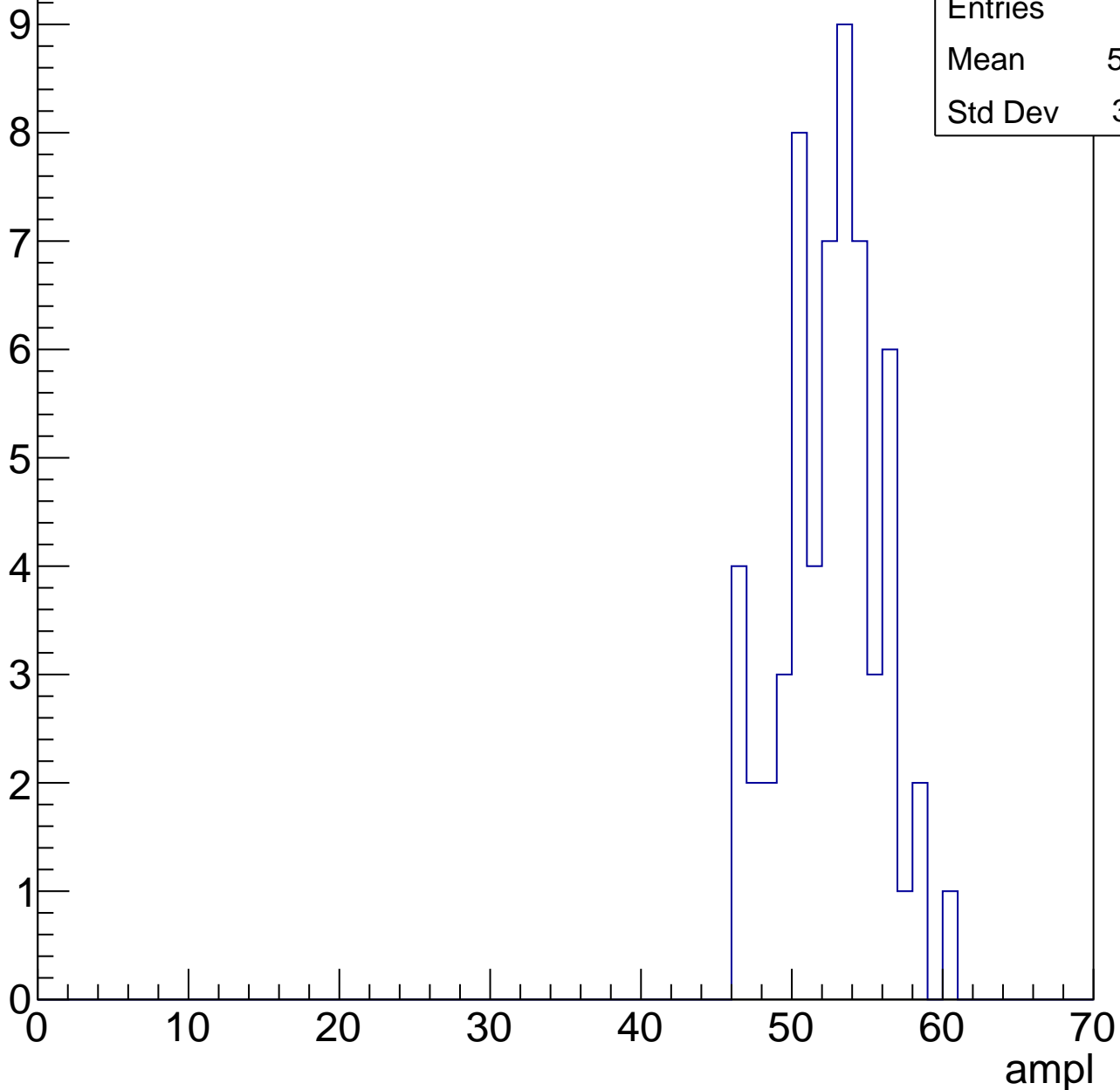


B0L002S, U9-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	52.17
Std Dev	3.211

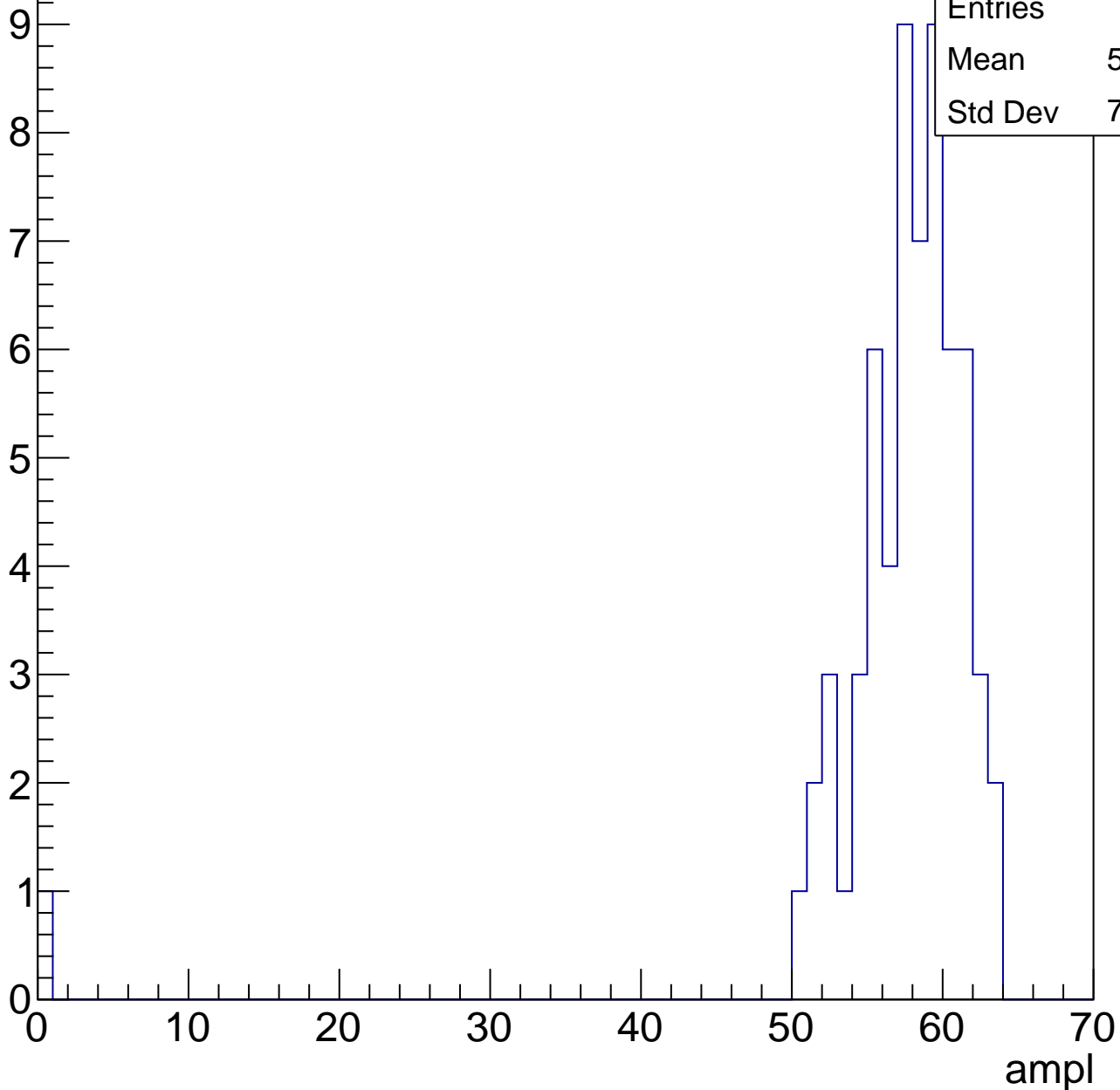


B0L002S, U9-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.59
Std Dev	7.813

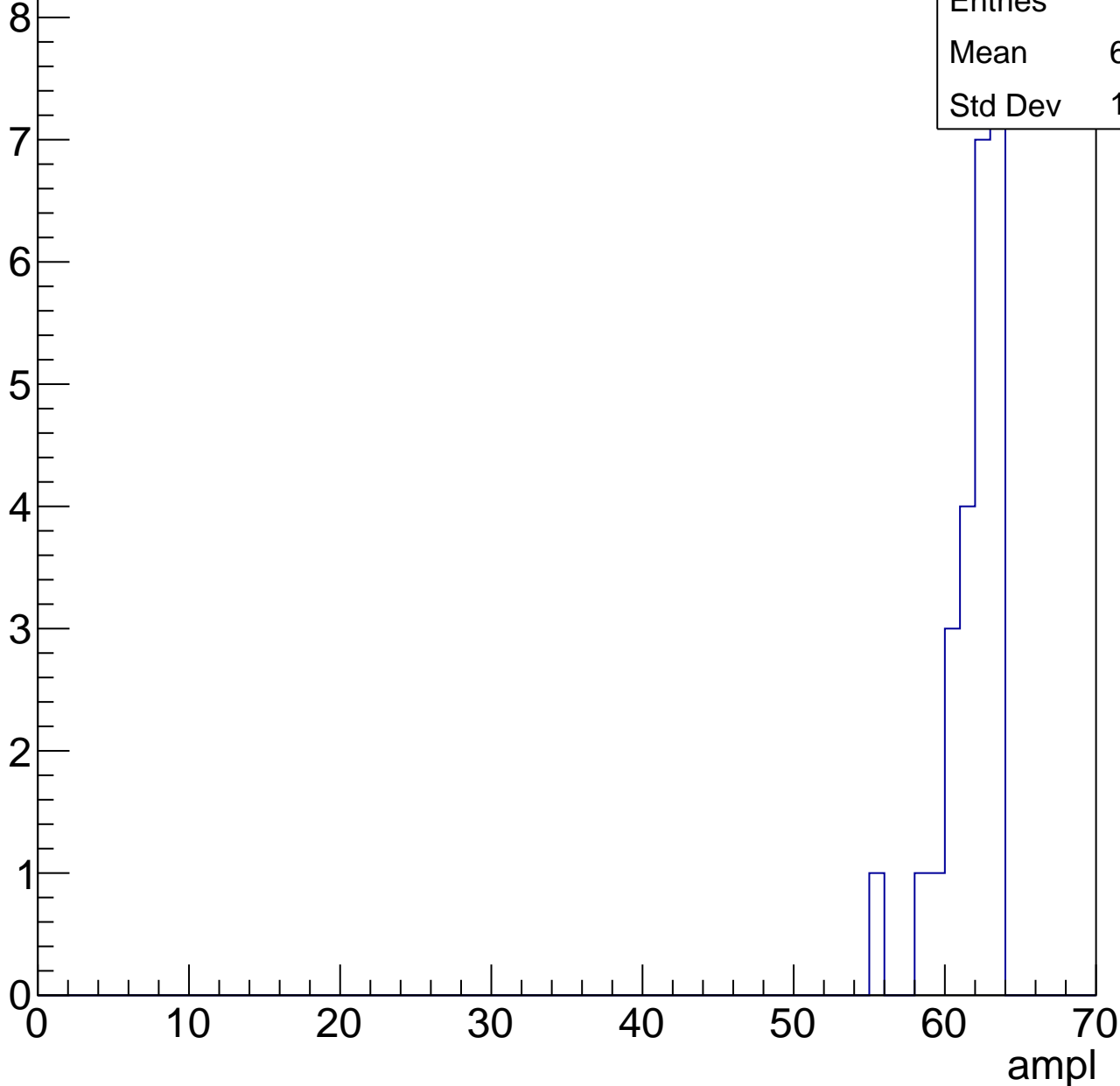


B0L002S, U9-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	61.36
Std Dev	1.874



B0L002S, U9-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U9-ch60, adc0

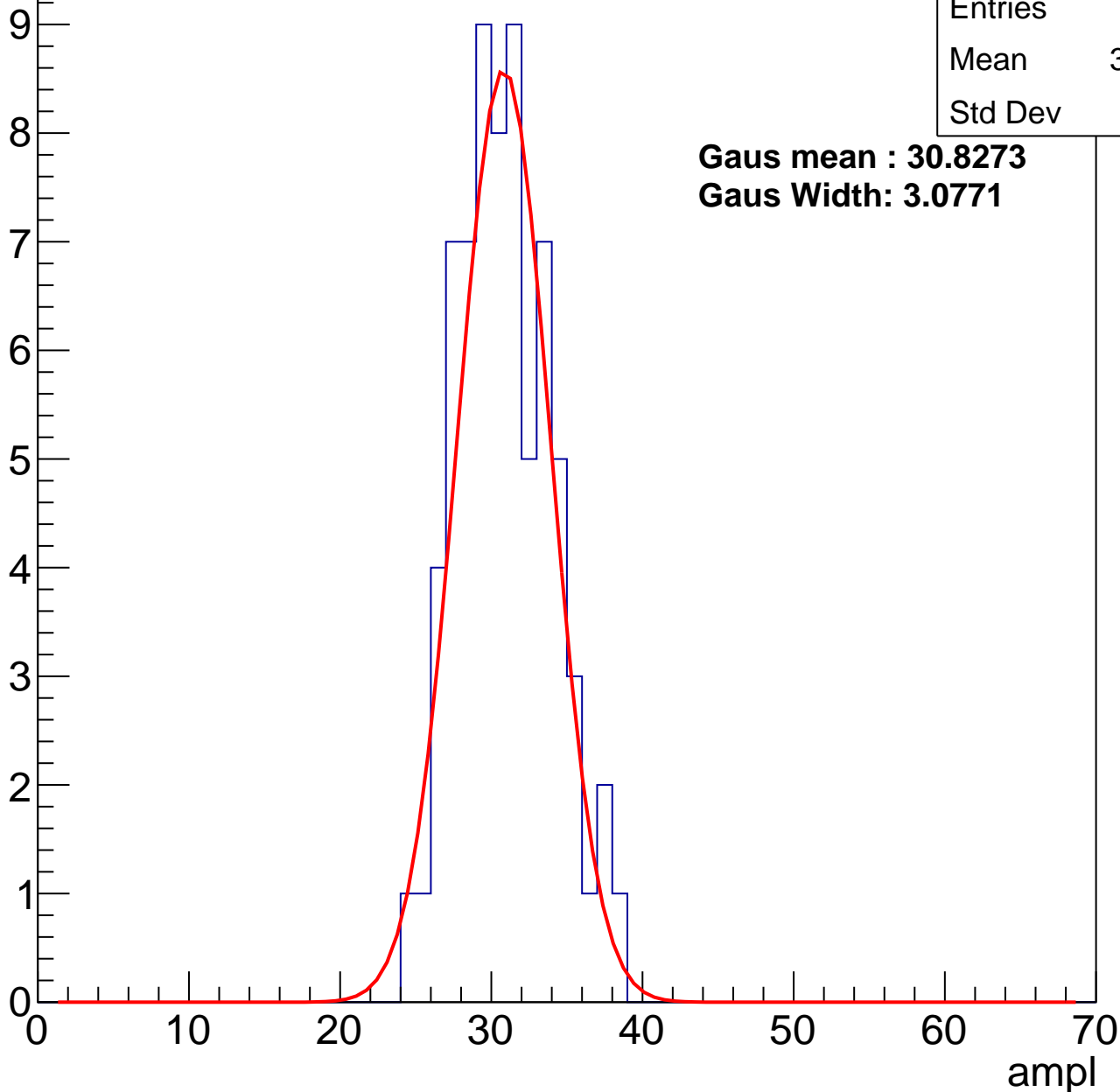
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.46
Std Dev	3.06

Gaus mean : 30.8273

Gaus Width: 3.0771



B0L002S, U9-ch60, adc1

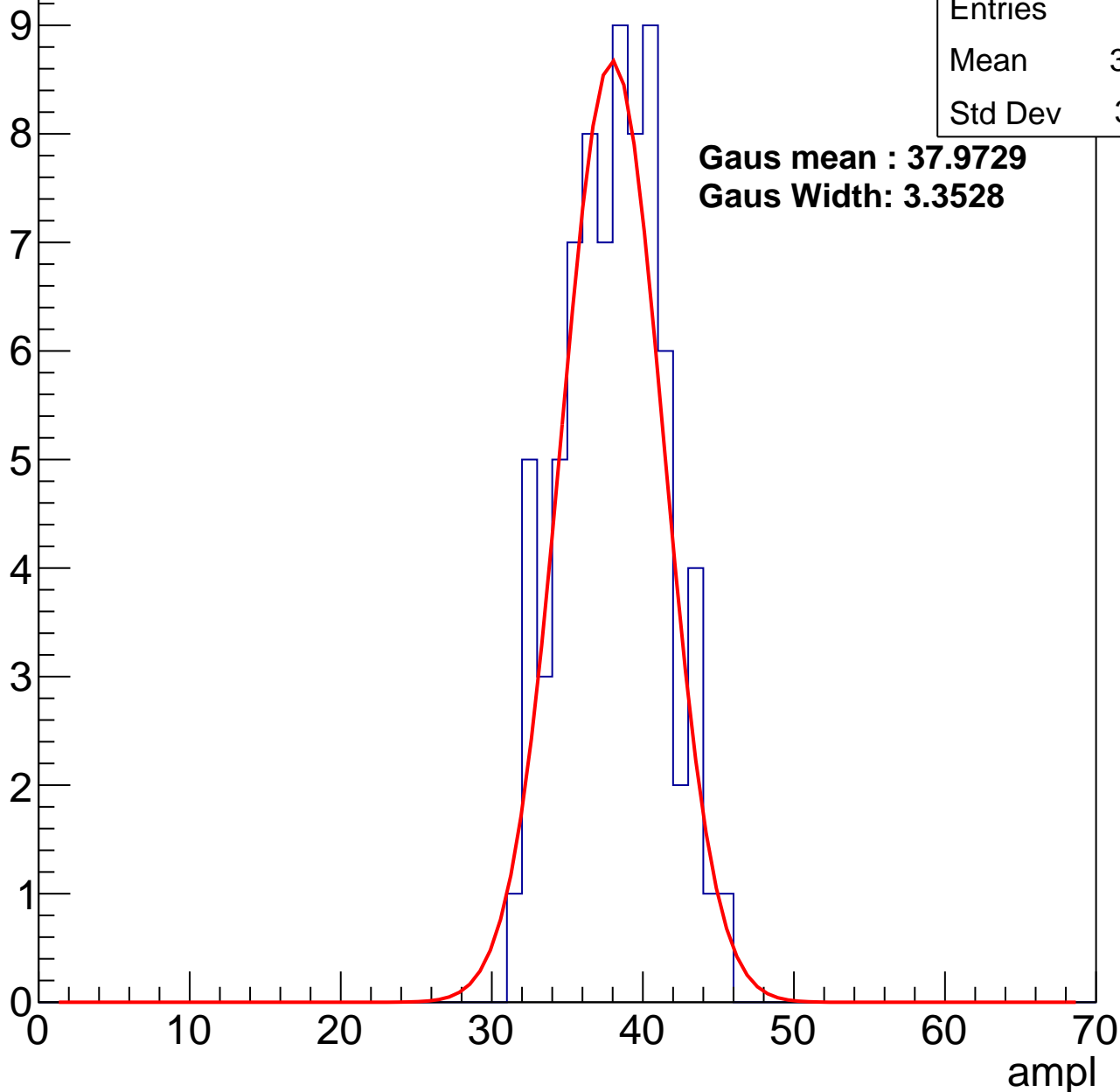
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.59
Std Dev	3.221

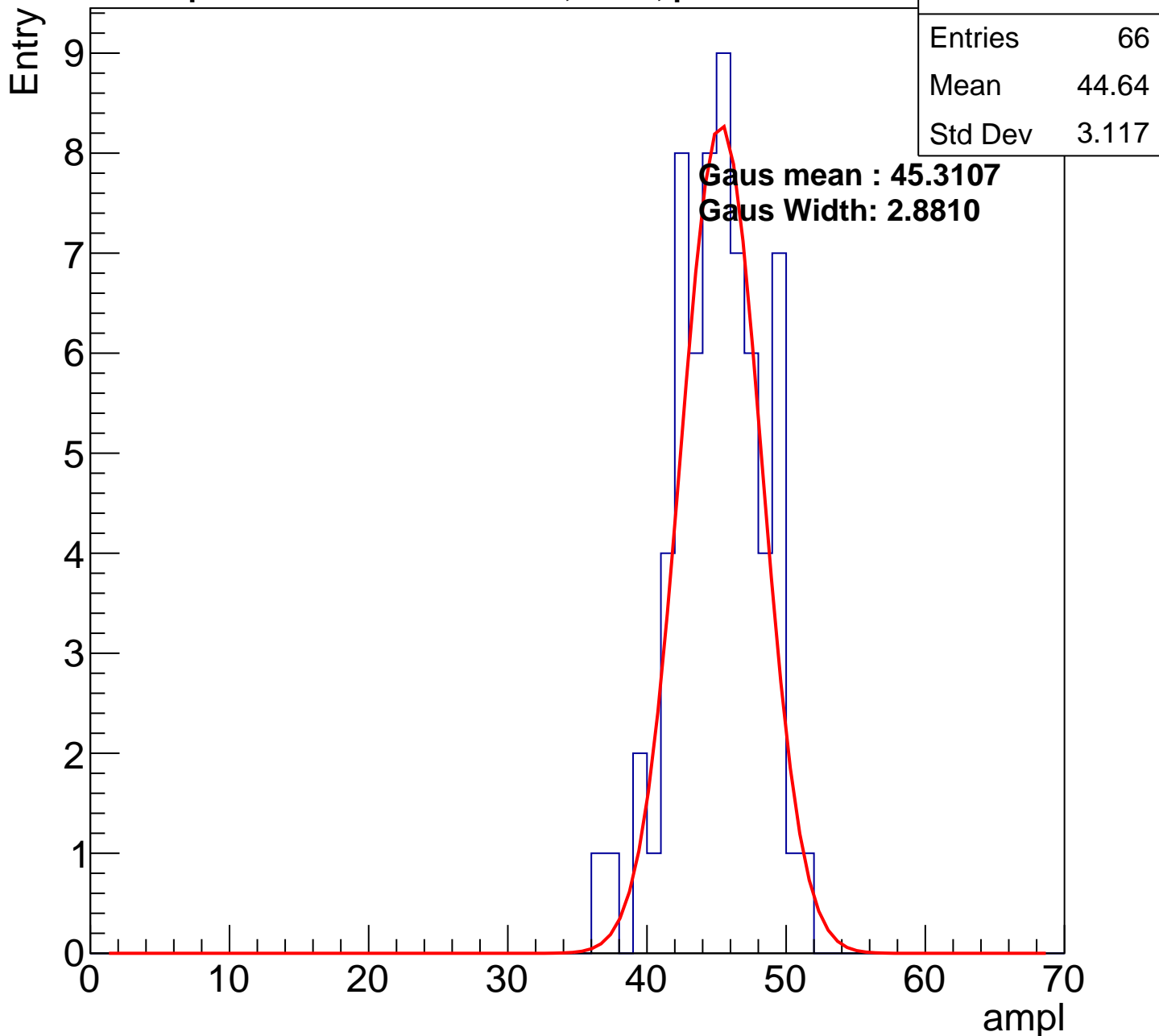
Gaus mean : 37.9729

Gaus Width: 3.3528



B0L002S, U9-ch60, adc2

calib_packv5_042523_0143.root, FC#8, port C1

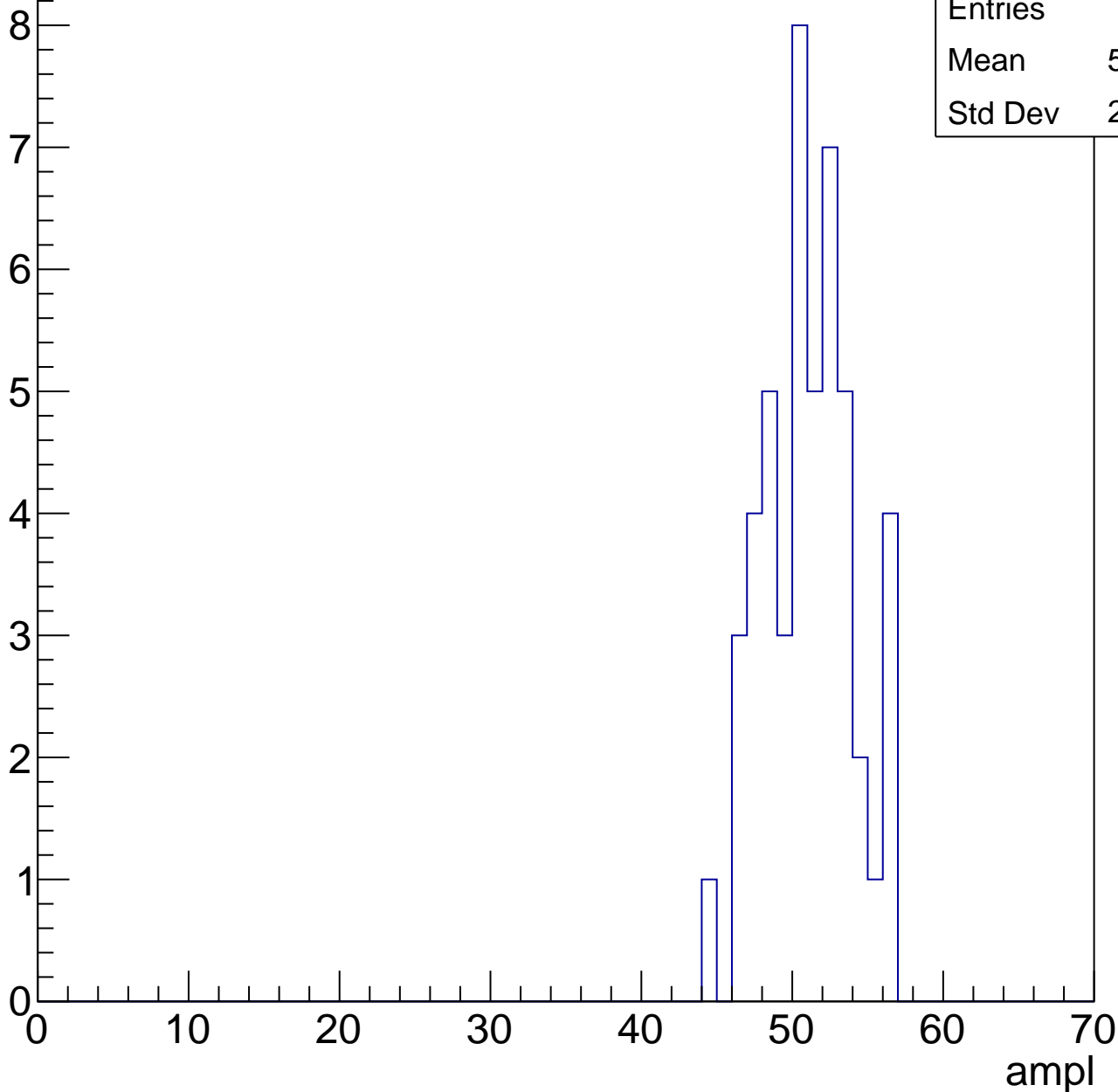


B0L002S, U9-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

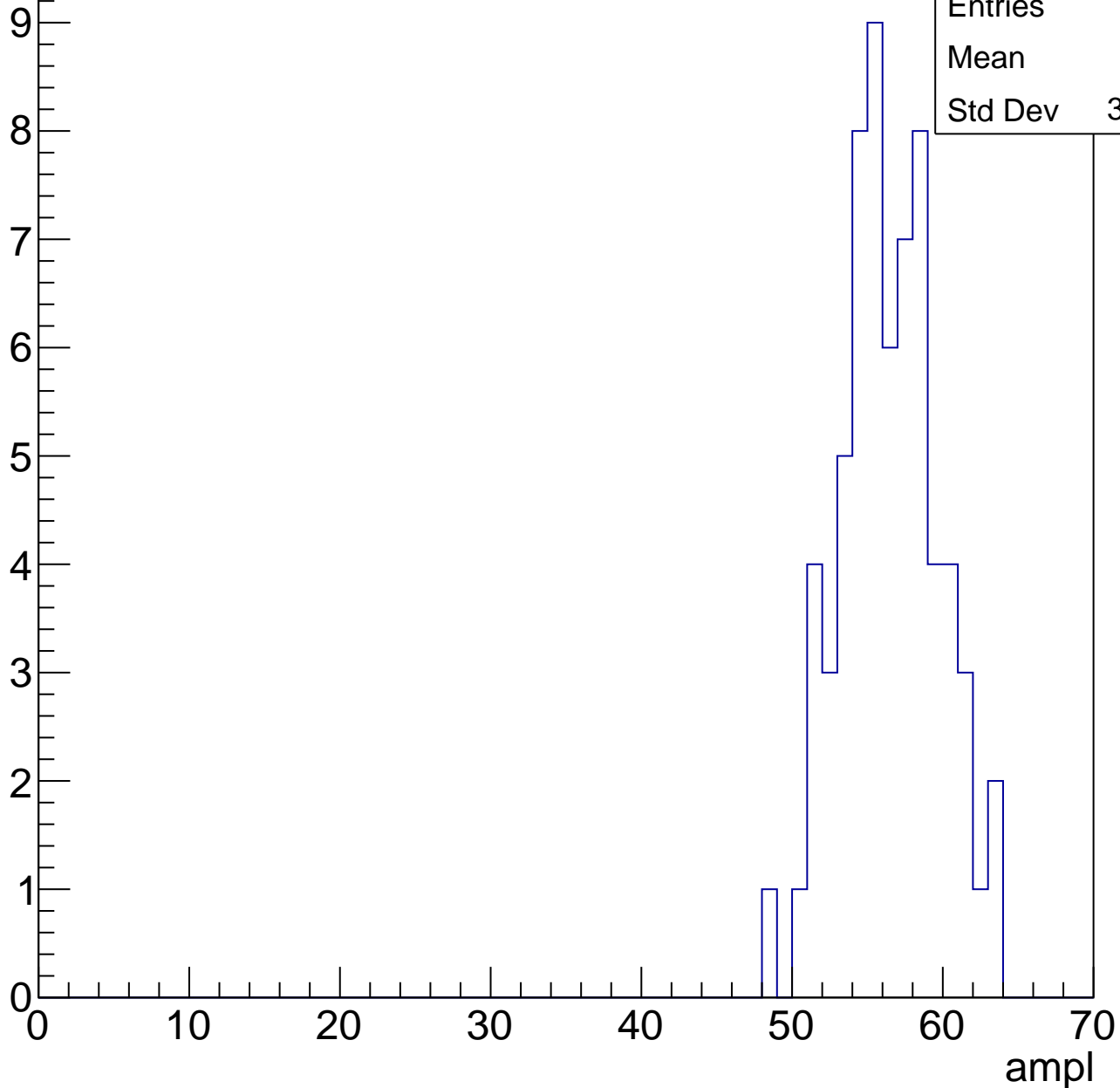
Entries	48
Mean	50.58
Std Dev	2.907



B0L002S, U9-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

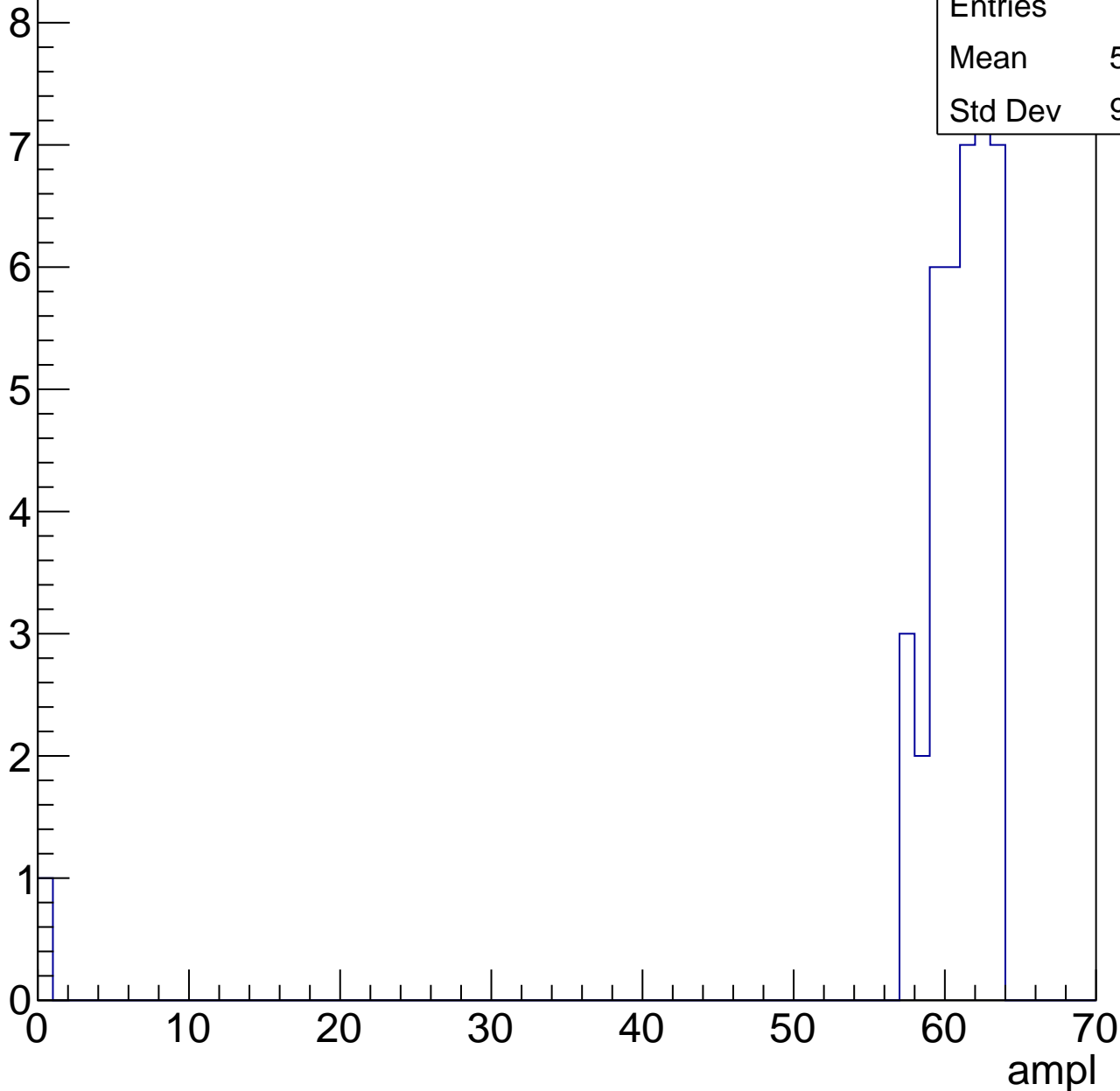


B0L002S, U9-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.12
Std Dev	9.634



B0L002S, U9-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch61, adc0

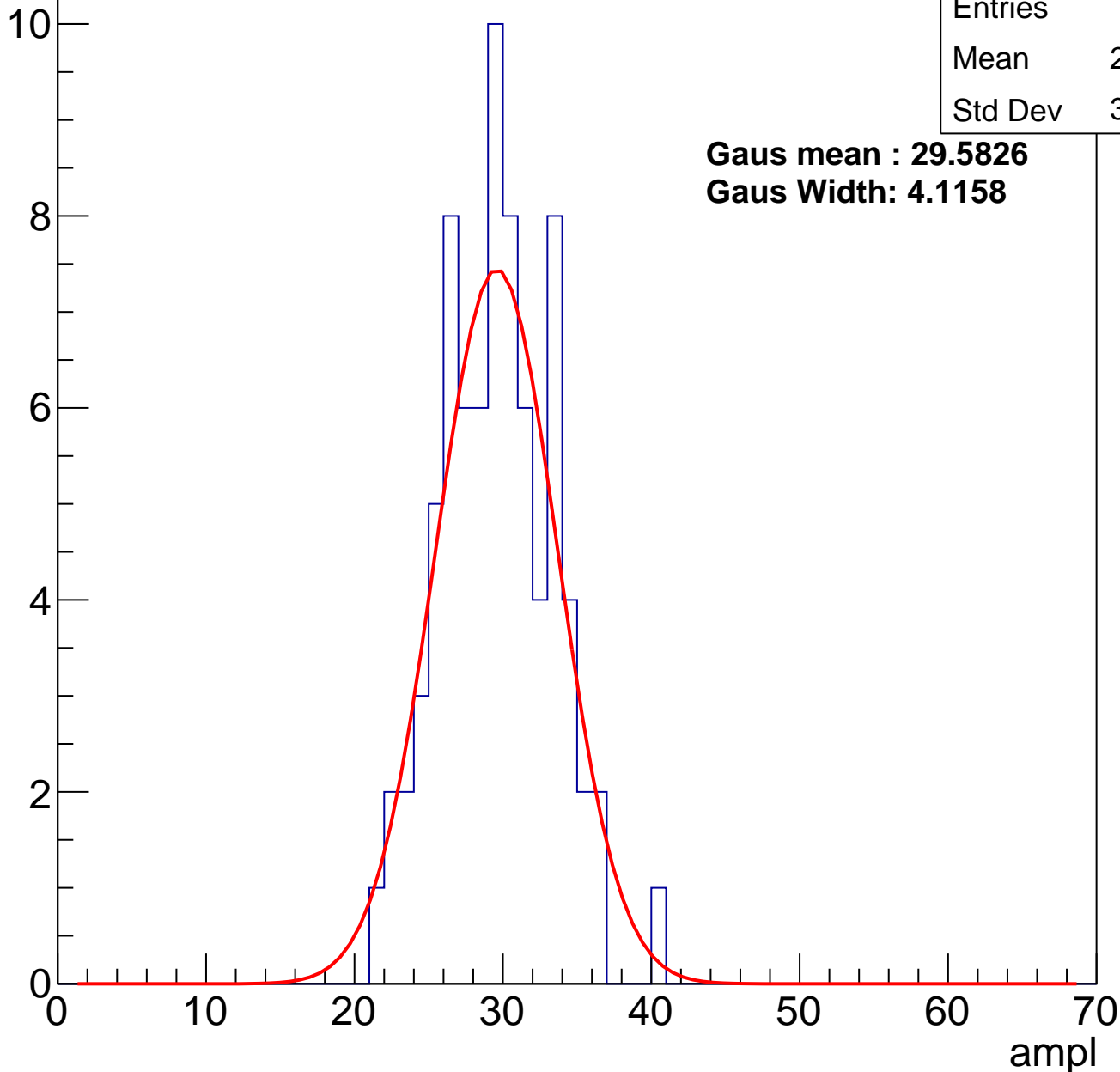
calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	29.13
Std Dev	3.715

Gaus mean : 29.5826

Gaus Width: 4.1158

Entry



B0L002S, U9-ch61, adc1

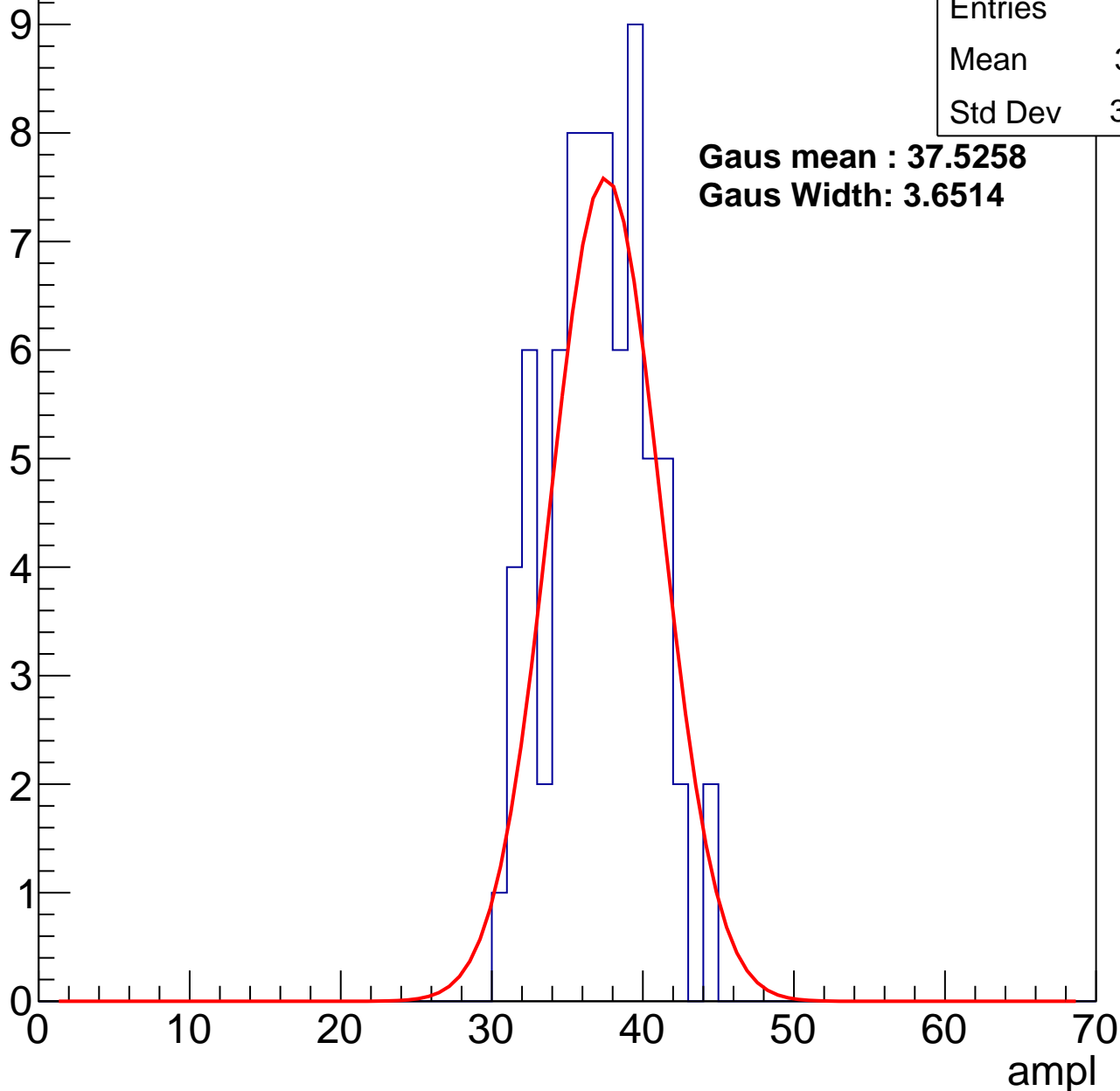
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.61
Std Dev	3.277

Gaus mean : 37.5258

Gaus Width: 3.6514



B0L002S, U9-ch61, adc2

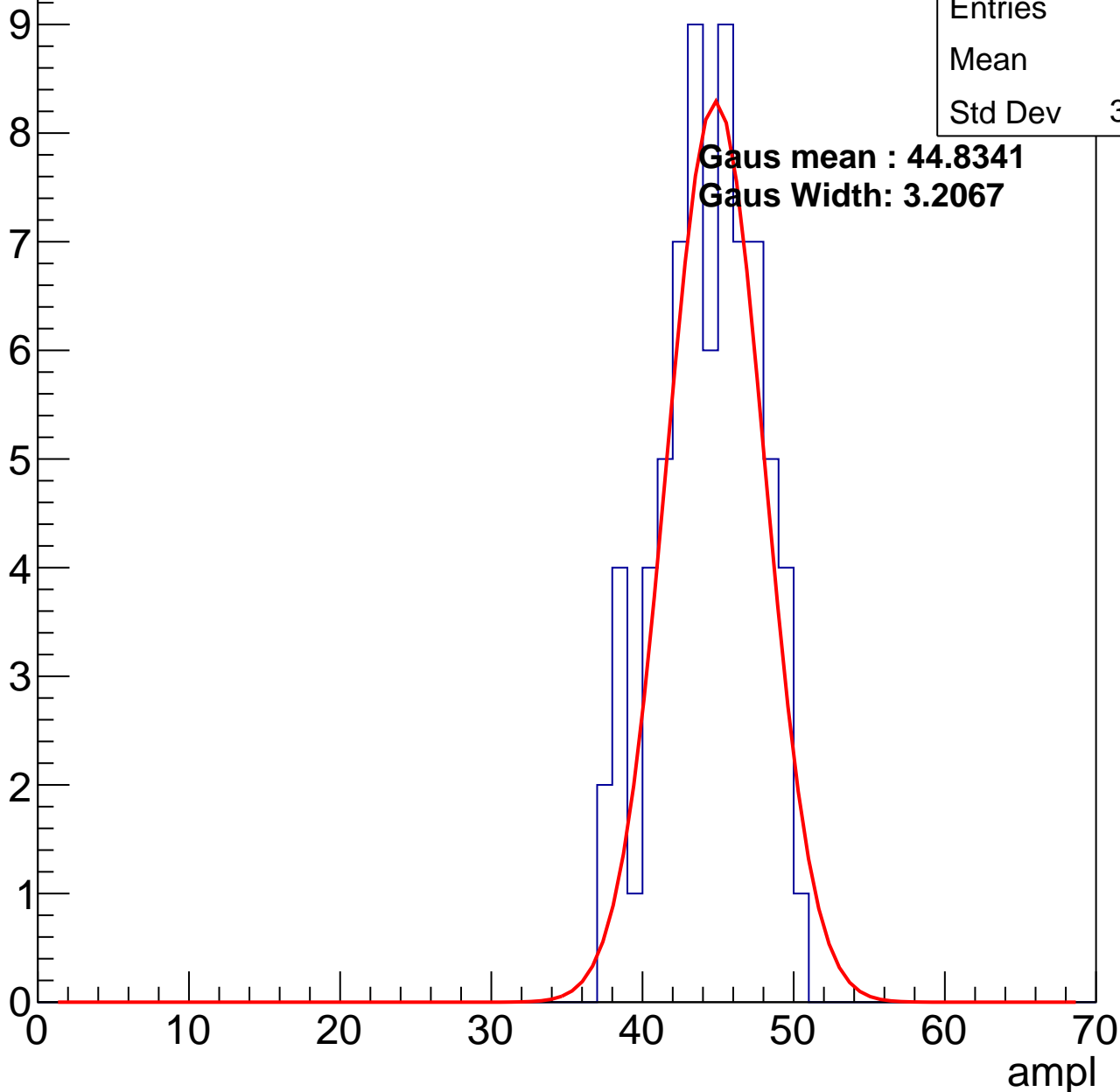
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.9
Std Dev	3.203

Gaus mean : 44.8341

Gaus Width: 3.2067

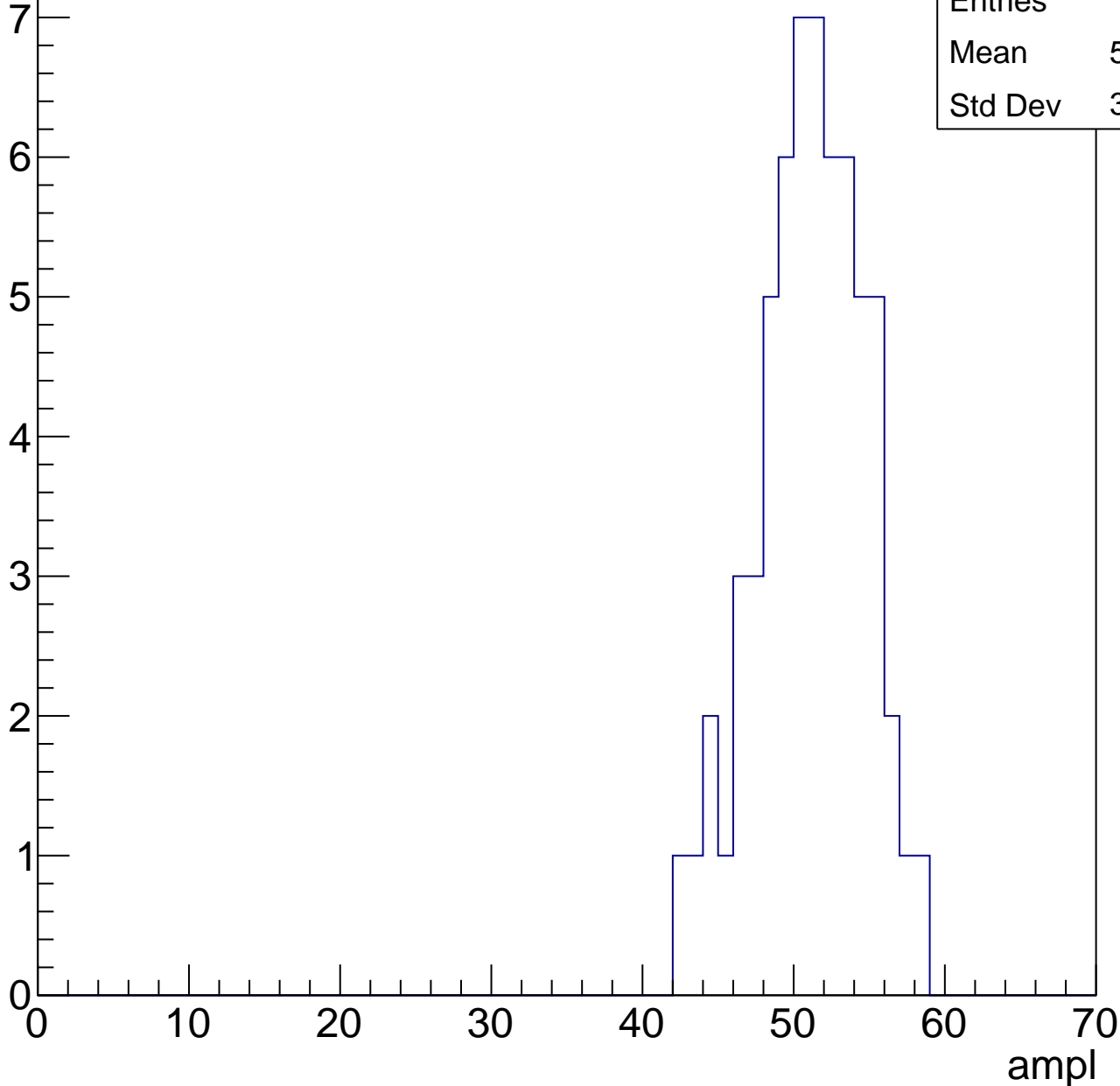


B0L002S, U9-ch61, adc3

calib_packv5_042523_0143.root, FC#8, port C1

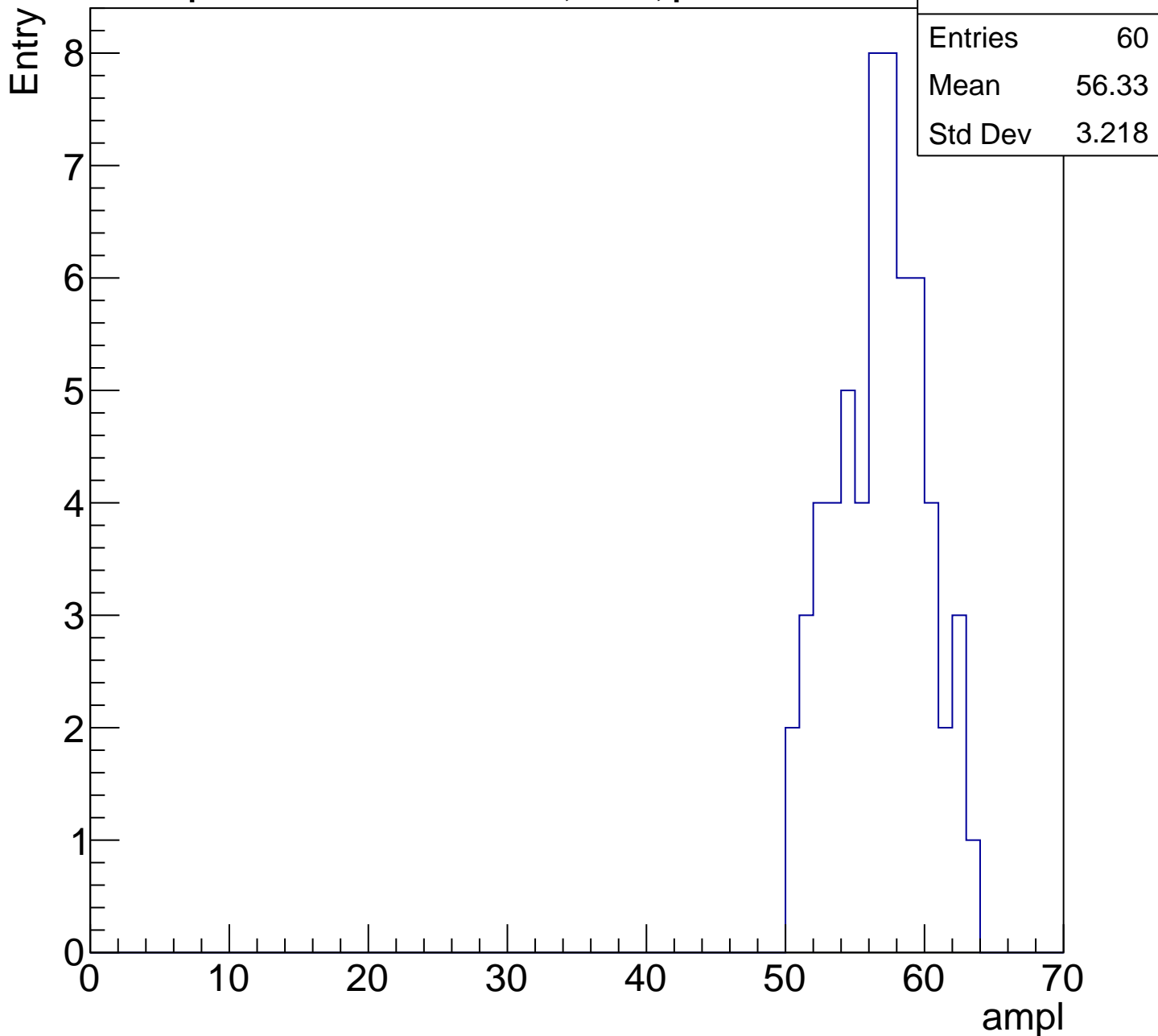
Entry

Entries	62
Mean	50.65
Std Dev	3.502



B0L002S, U9-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch61, adc5

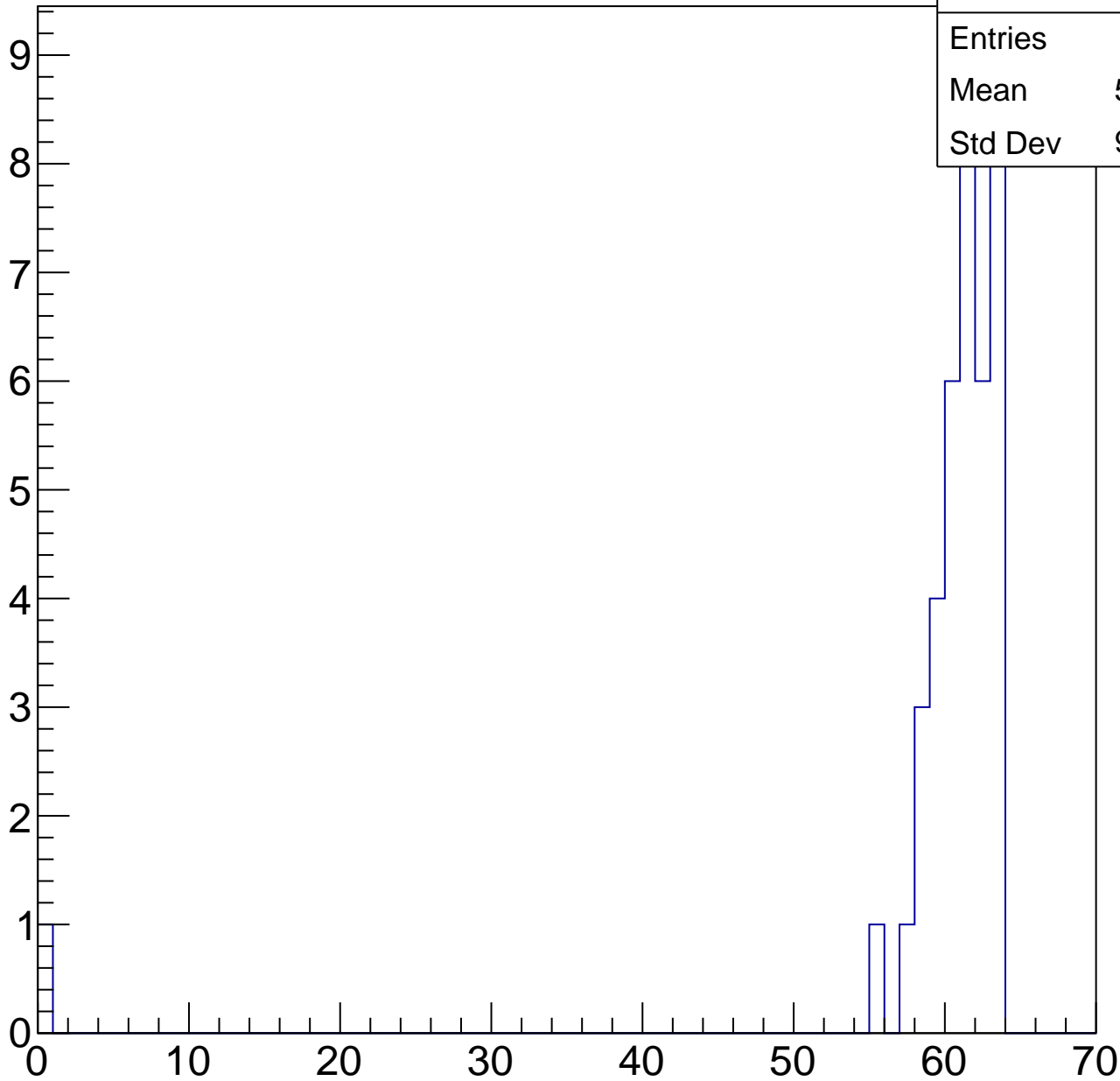
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.21
Std Dev	9.791

ampl



B0L002S, U9-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch62, adc0

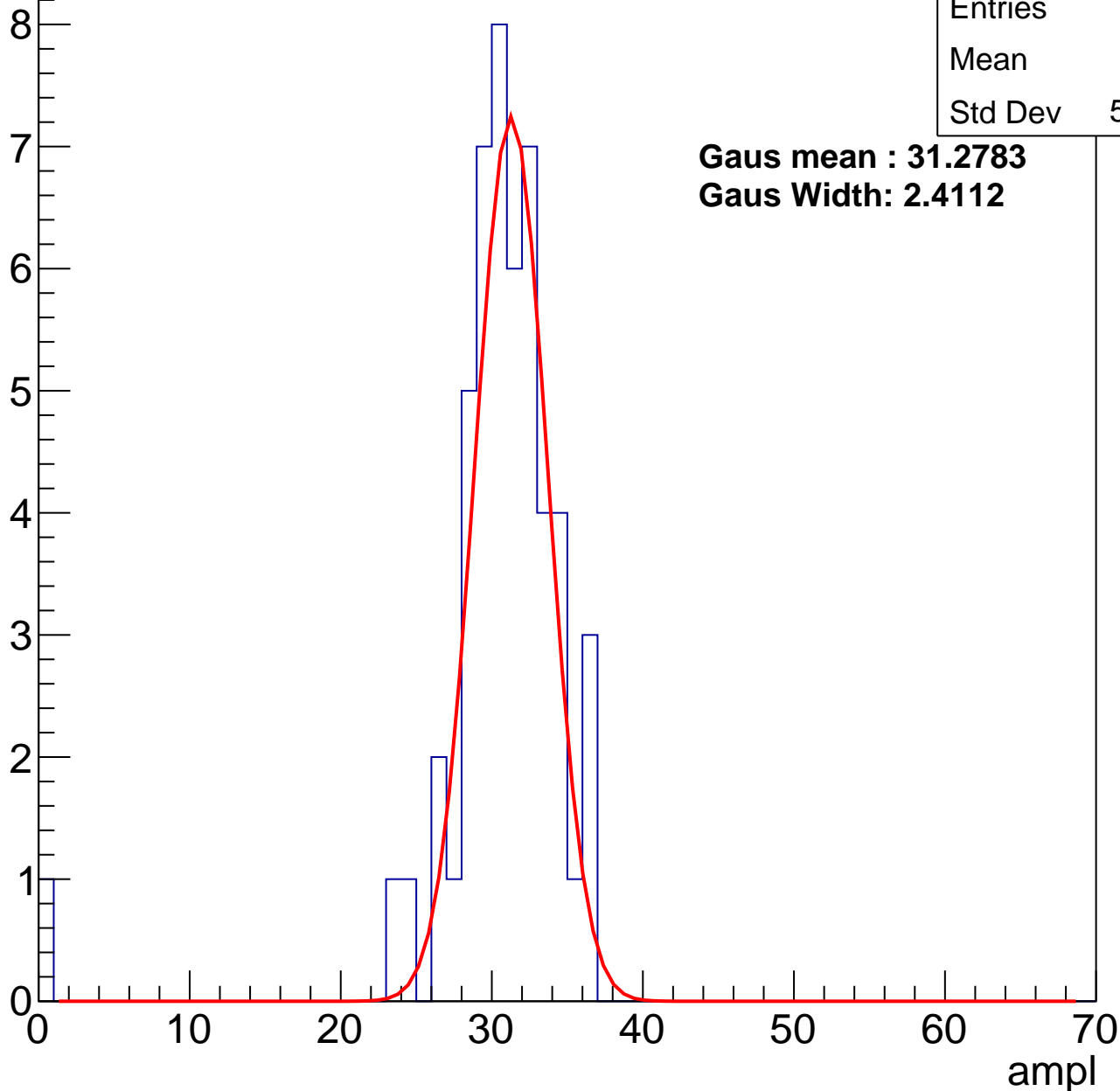
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	30
Std Dev	5.087

Gaus mean : 31.2783

Gaus Width: 2.4112

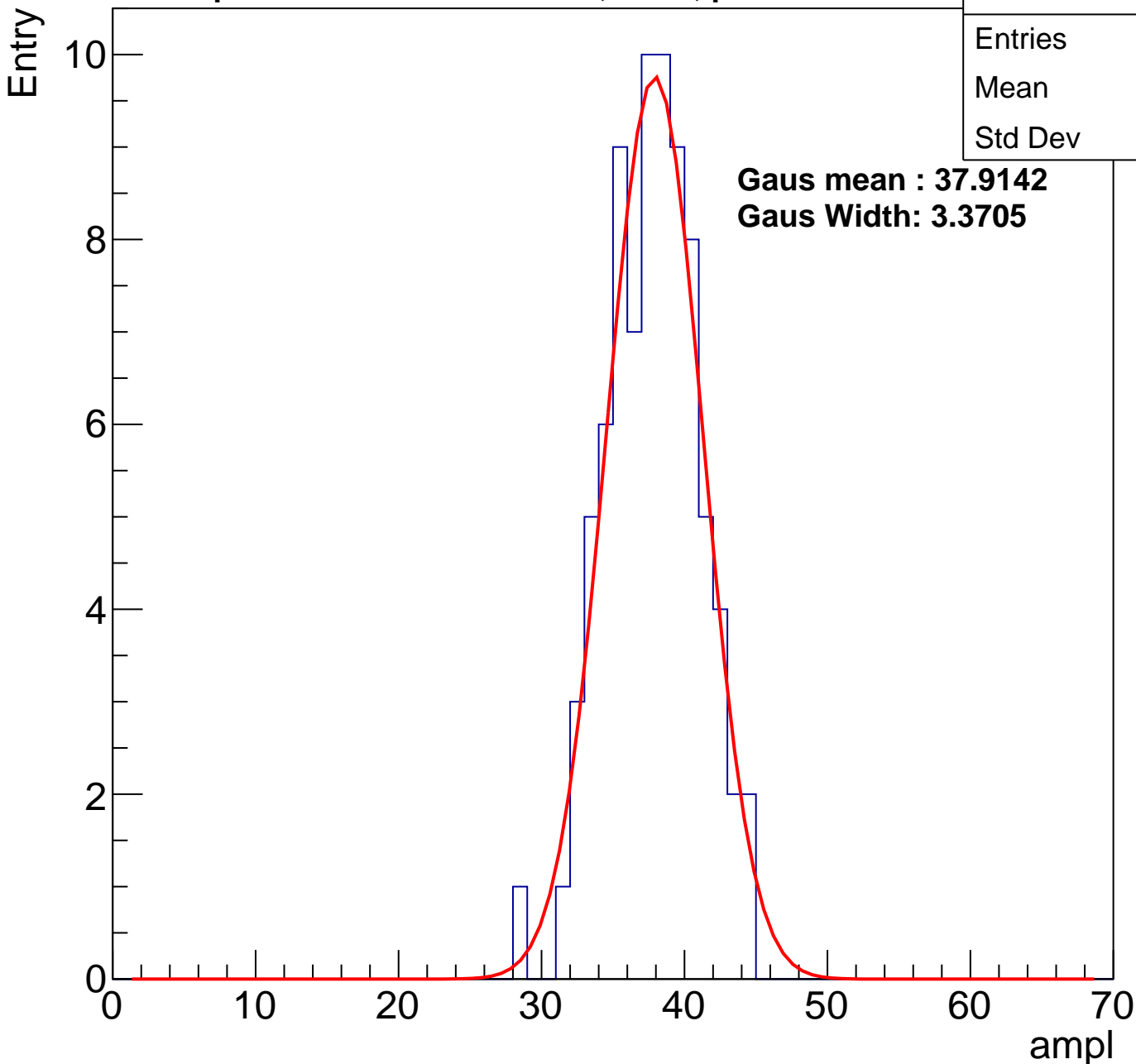


B0L002S, U9-ch62, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	37.3
Std Dev	3.18

Gaus mean : 37.9142
Gaus Width: 3.3705



B0L002S, U9-ch62, adc2

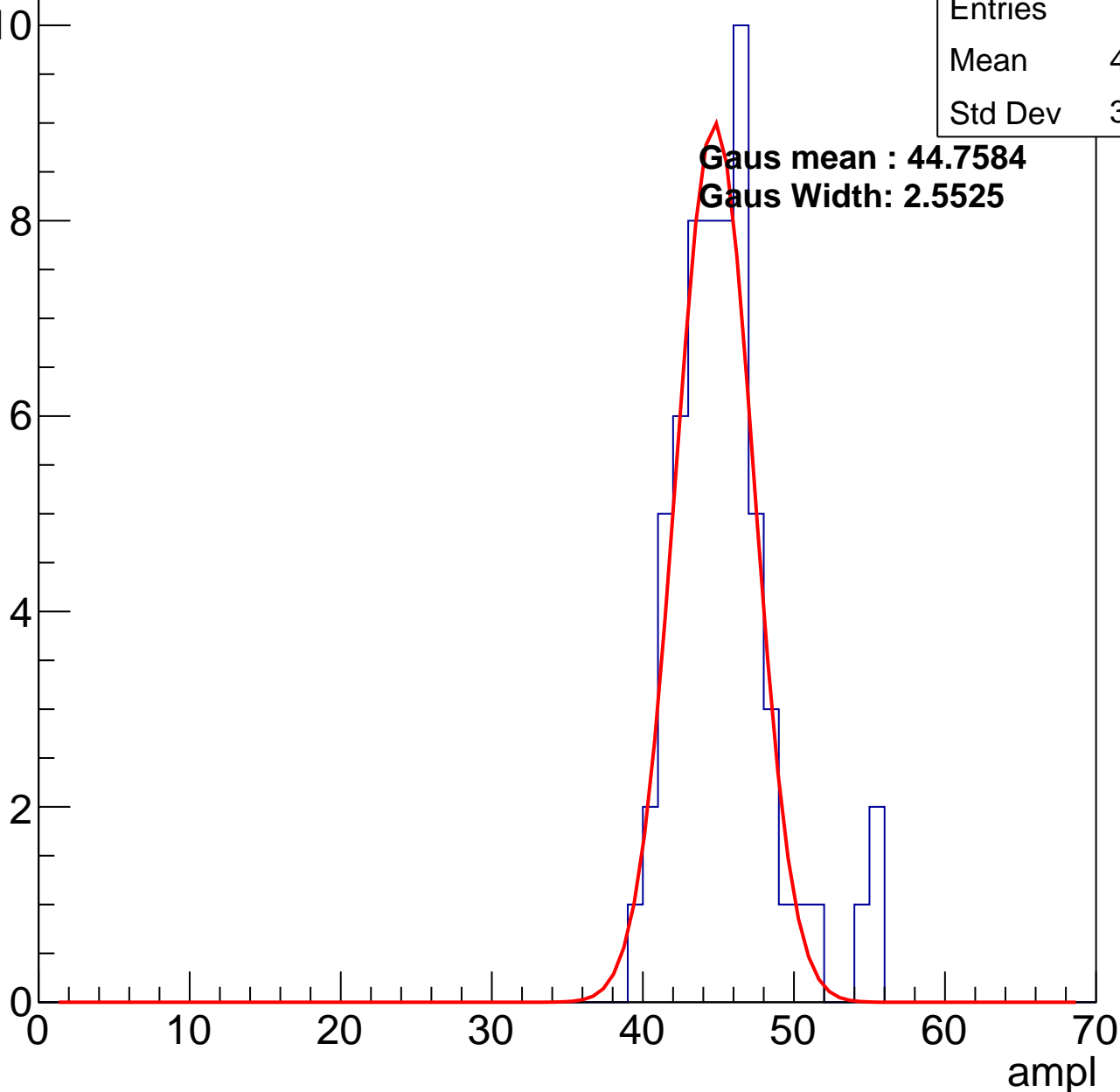
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.92
Std Dev	3.299

Gaus mean : 44.7584

Gaus Width: 2.5525

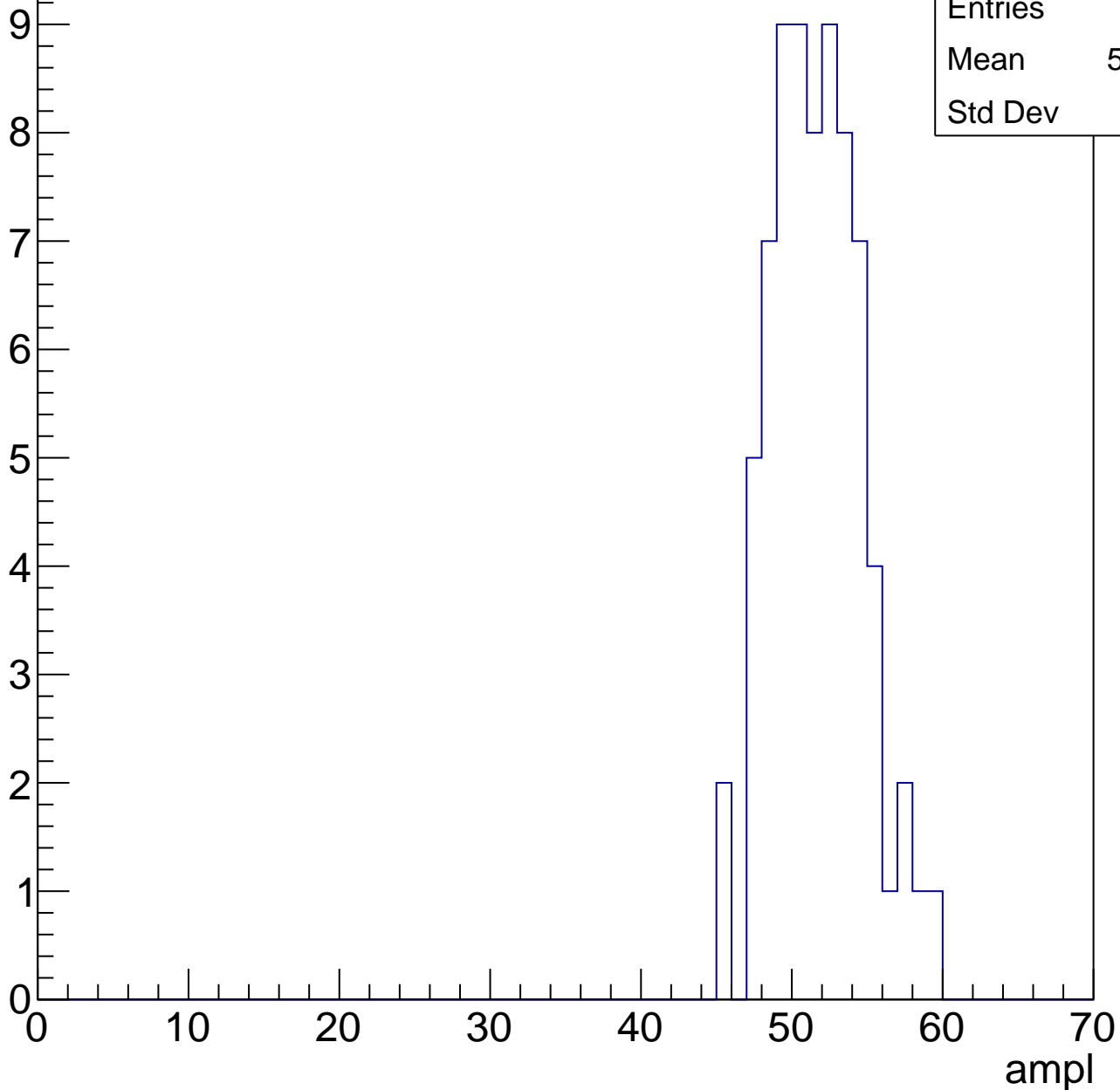


B0L002S, U9-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	51.19
Std Dev	2.95

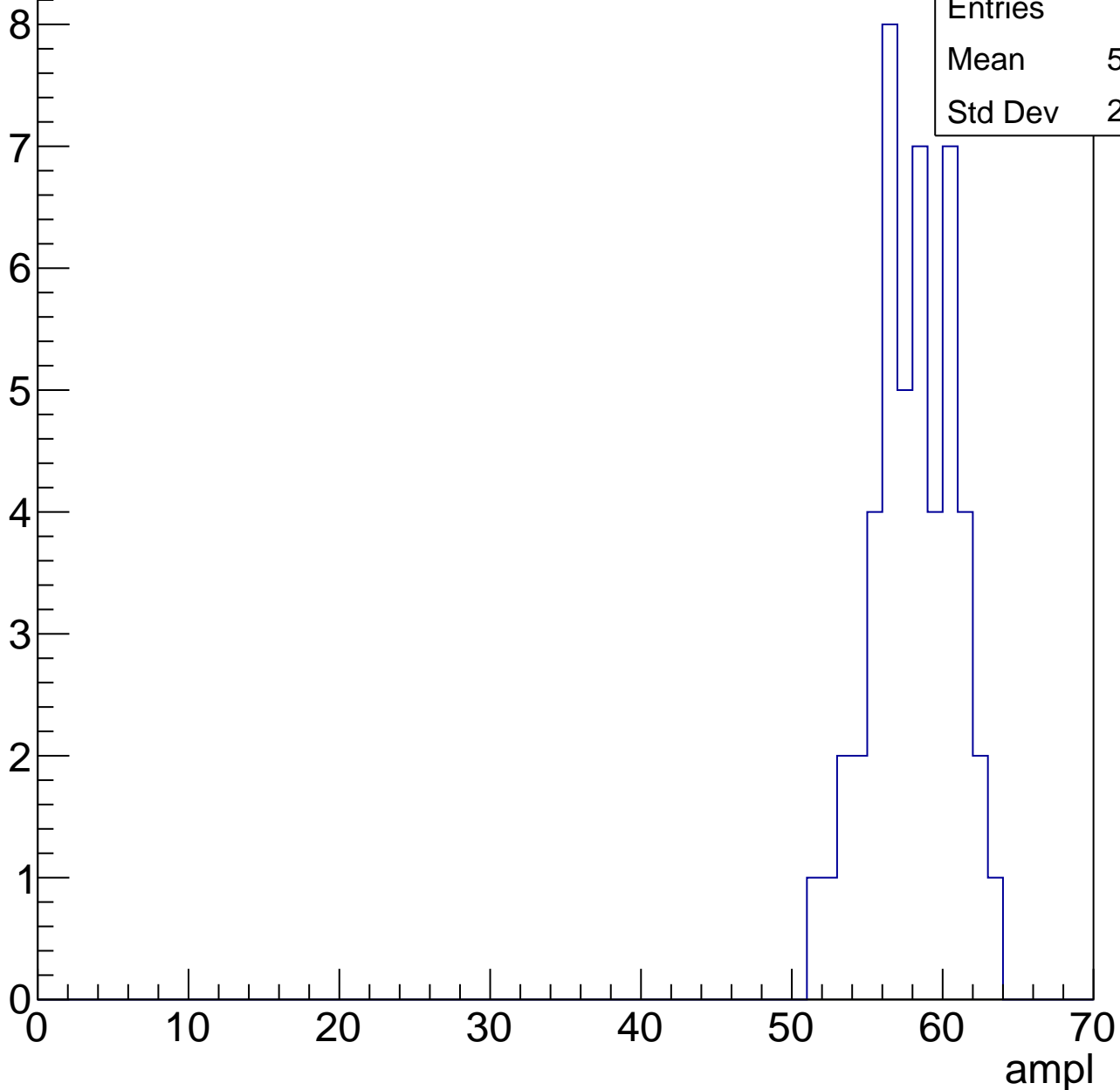


B0L002S, U9-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	57.56
Std Dev	2.723

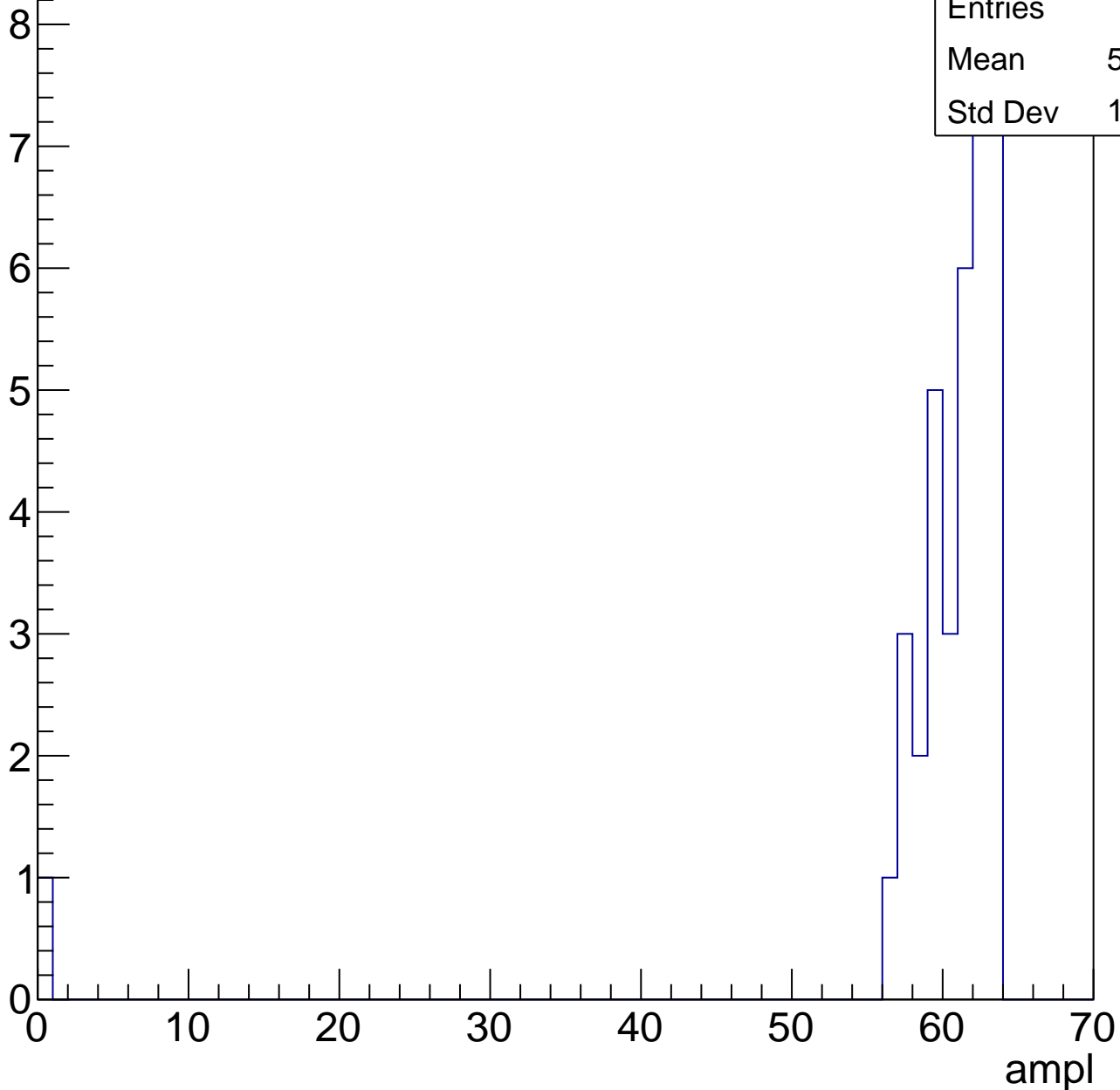


B0L002S, U9-ch62, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.03
Std Dev	10.04



B0L002S, U9-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch62, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch63, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	28.58
Std Dev	5.685

Gaus mean : 29.5435

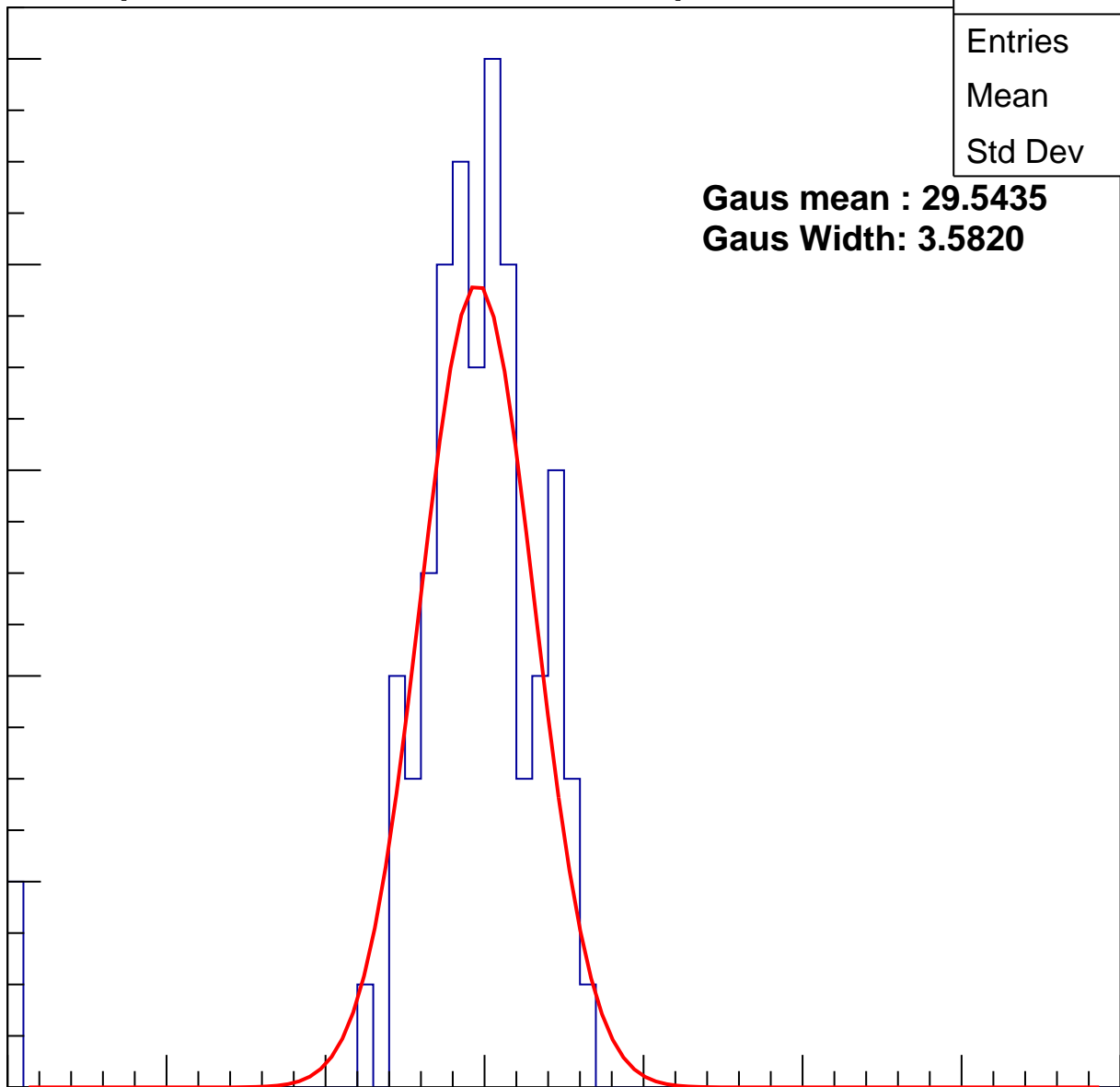
Gaus Width: 3.5820

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch63, adc1

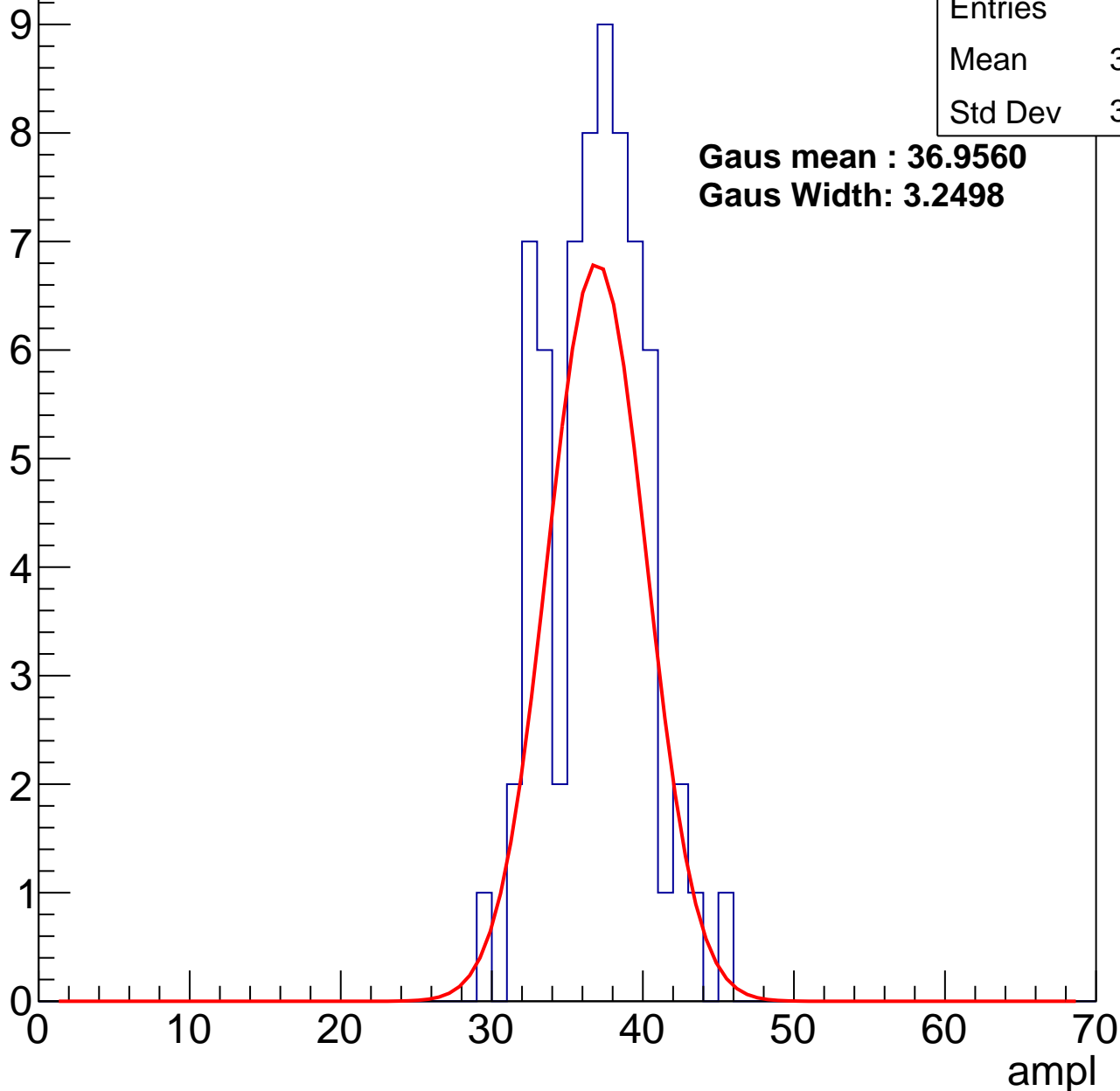
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	36.43
Std Dev	3.196

Gaus mean : 36.9560

Gaus Width: 3.2498



B0L002S, U9-ch63, adc2

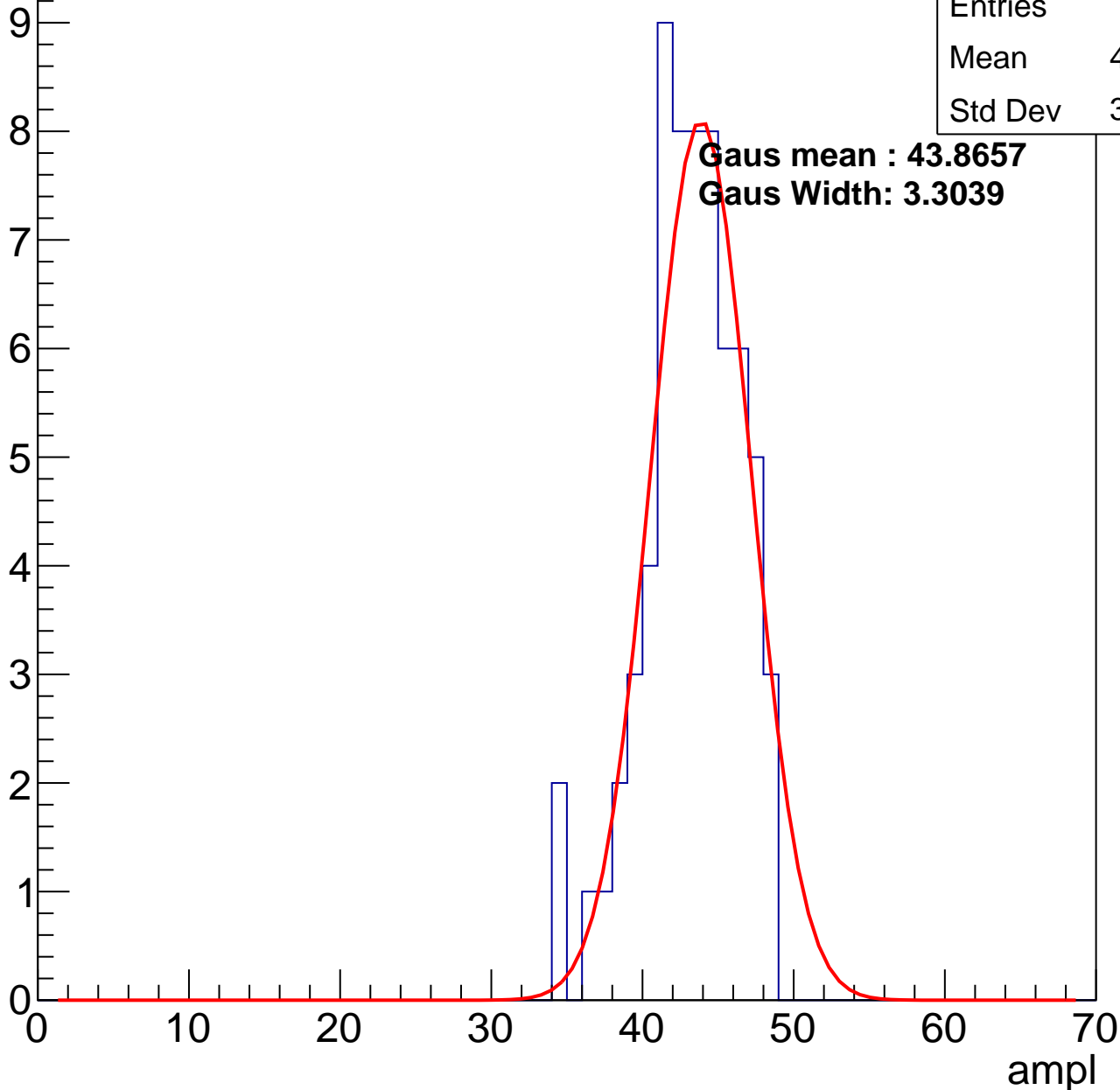
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.73
Std Dev	3.165

Gaus mean : 43.8657

Gaus Width: 3.3039



B0L002S, U9-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

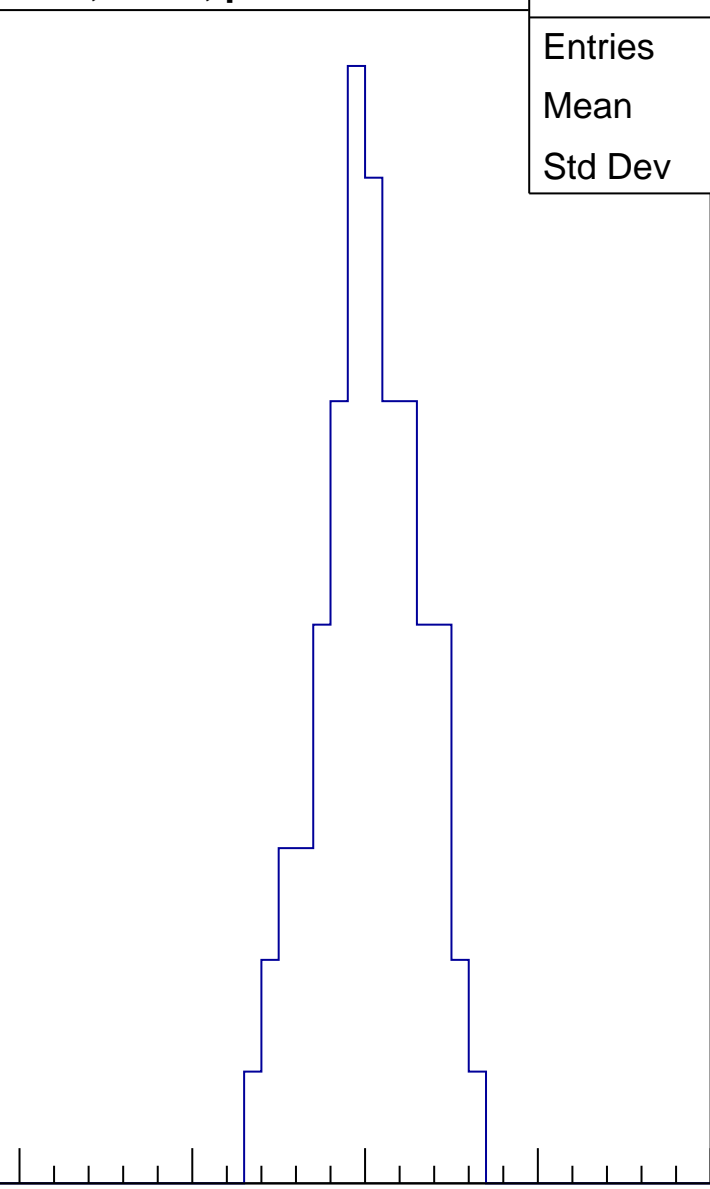
Entries	67
Mean	49.81
Std Dev	2.918

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

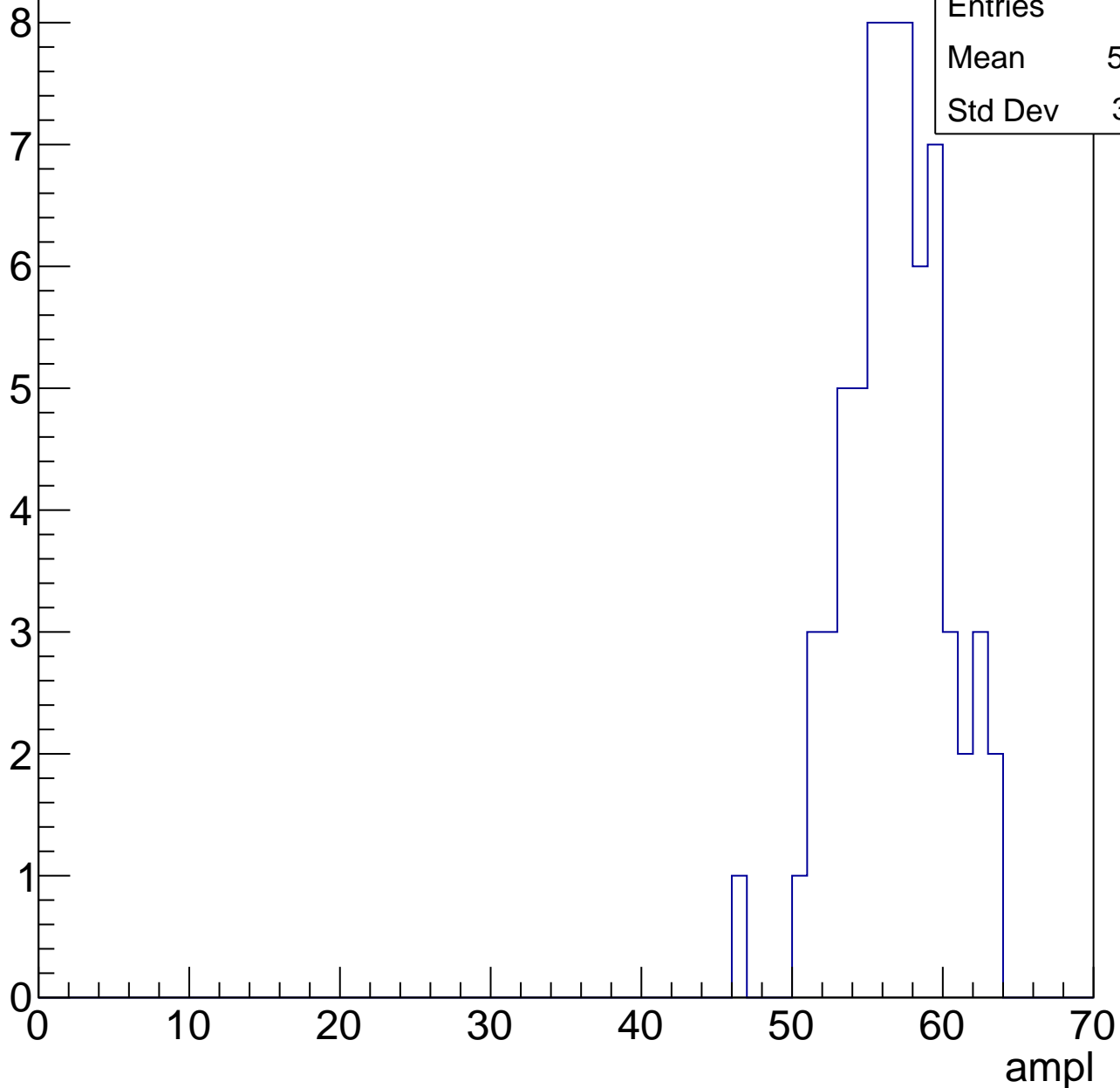
ampl



B0L002S, U9-ch63, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

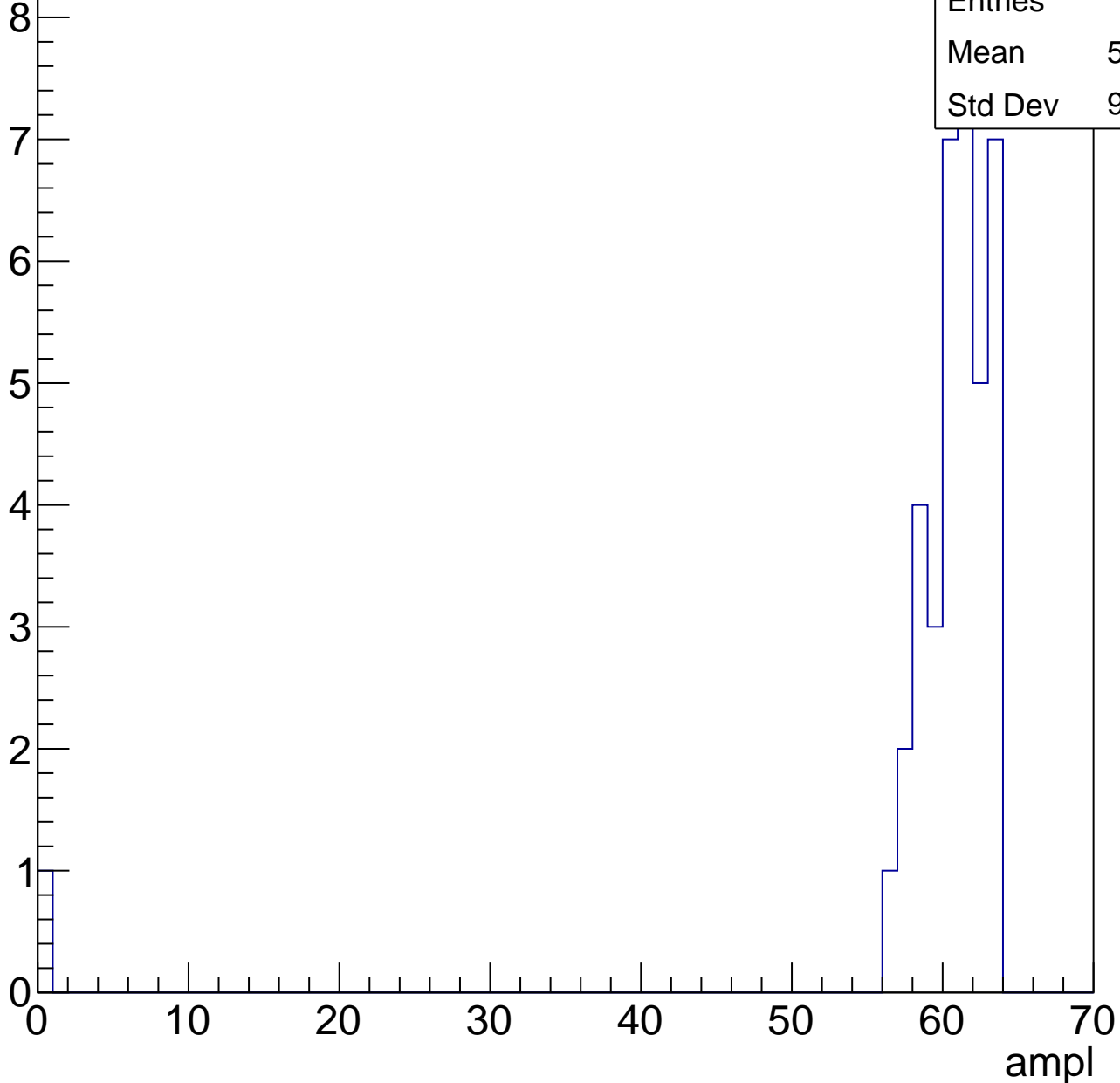


B0L002S, U9-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.89
Std Dev	9.864



B0L002S, U9-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L002S, U9-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch64, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	30.42
Std Dev	3.132

Gaus mean : 30.7869

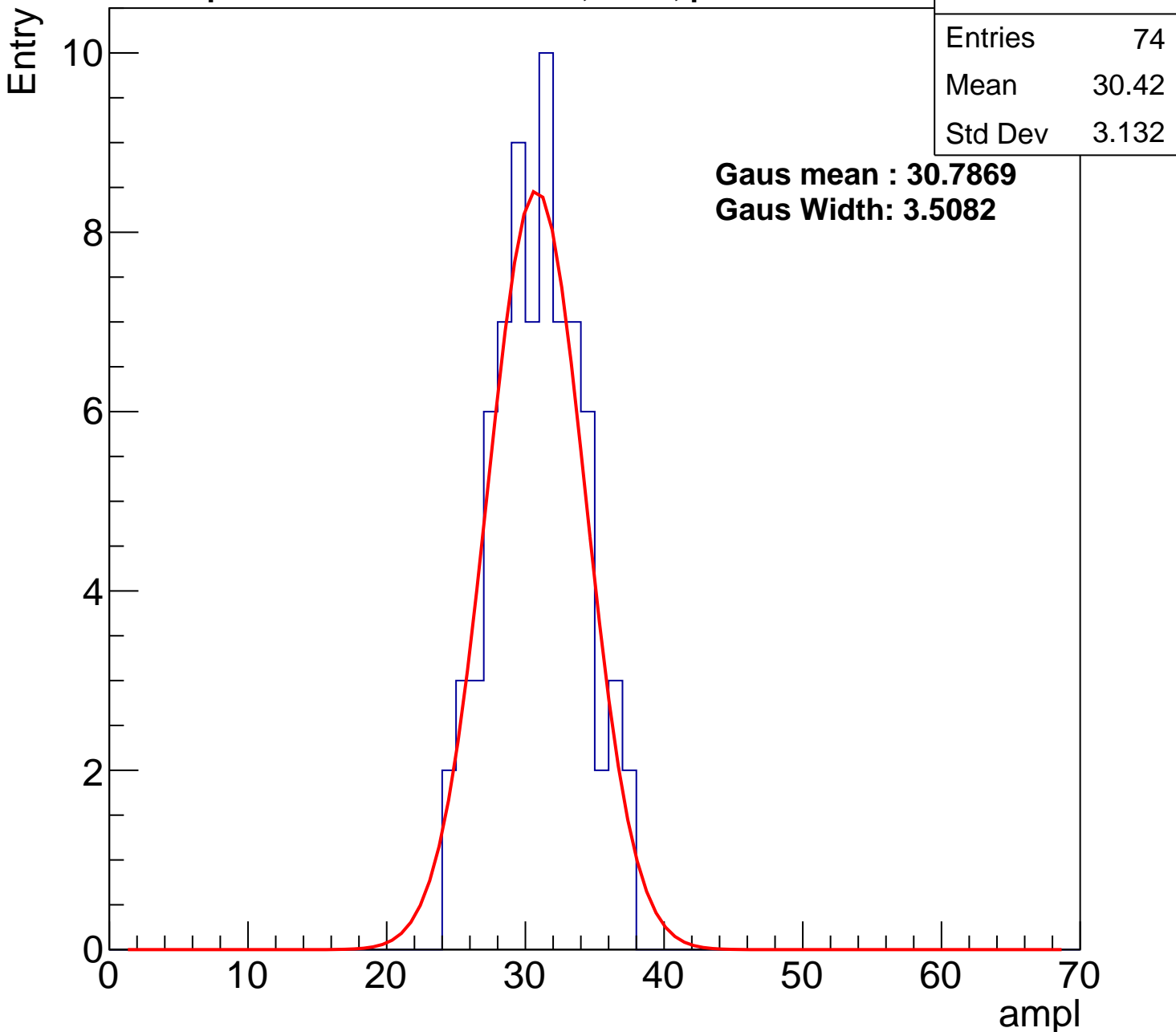
Gaus Width: 3.5082

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch64, adc1

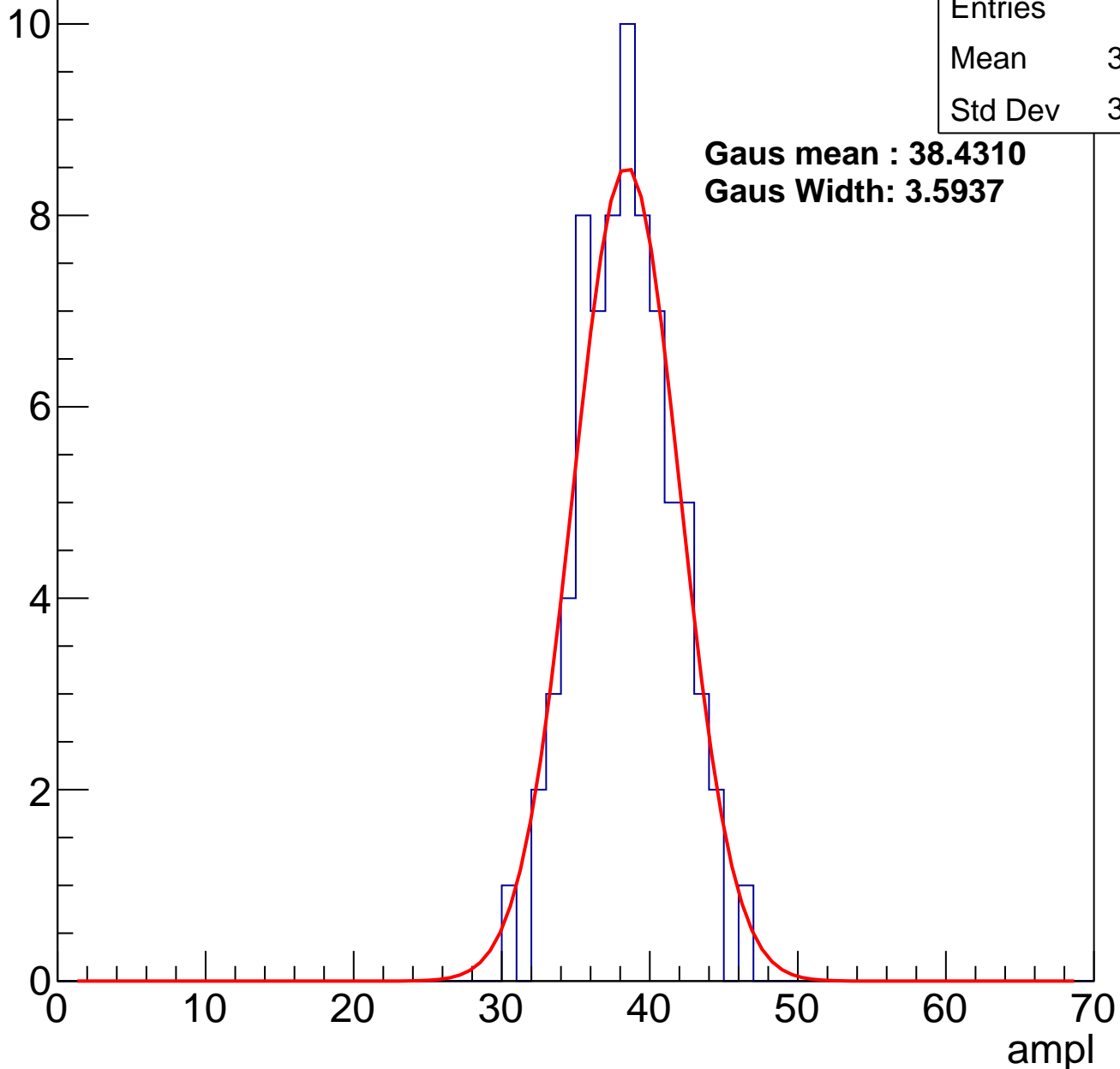
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	37.93
Std Dev	3.193

Gaus mean : 38.4310

Gaus Width: 3.5937

Entry



B0L002S, U9-ch64, adc2

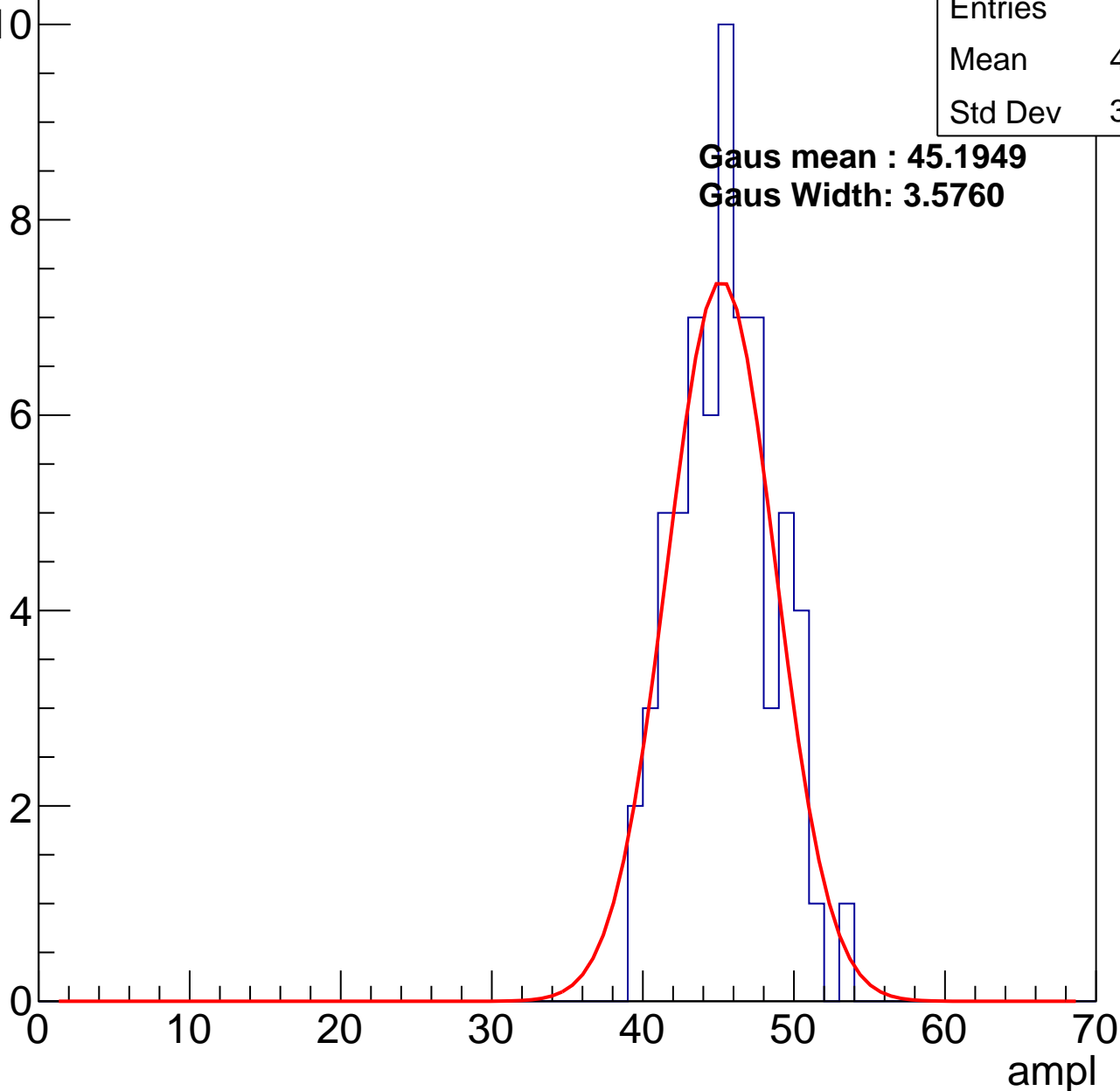
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	45.03
Std Dev	3.133

Gaus mean : 45.1949

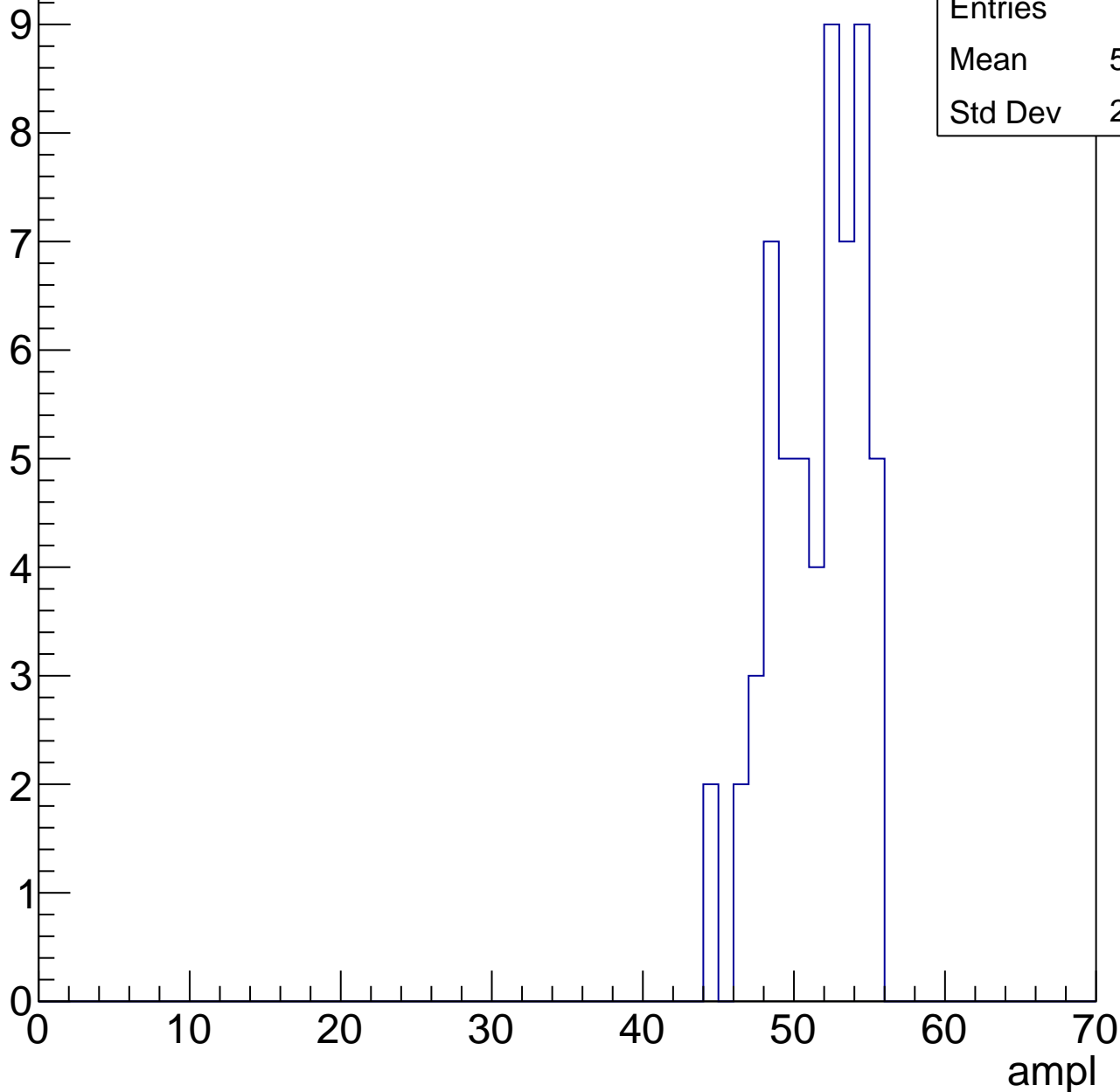
Gaus Width: 3.5760



B0L002S, U9-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

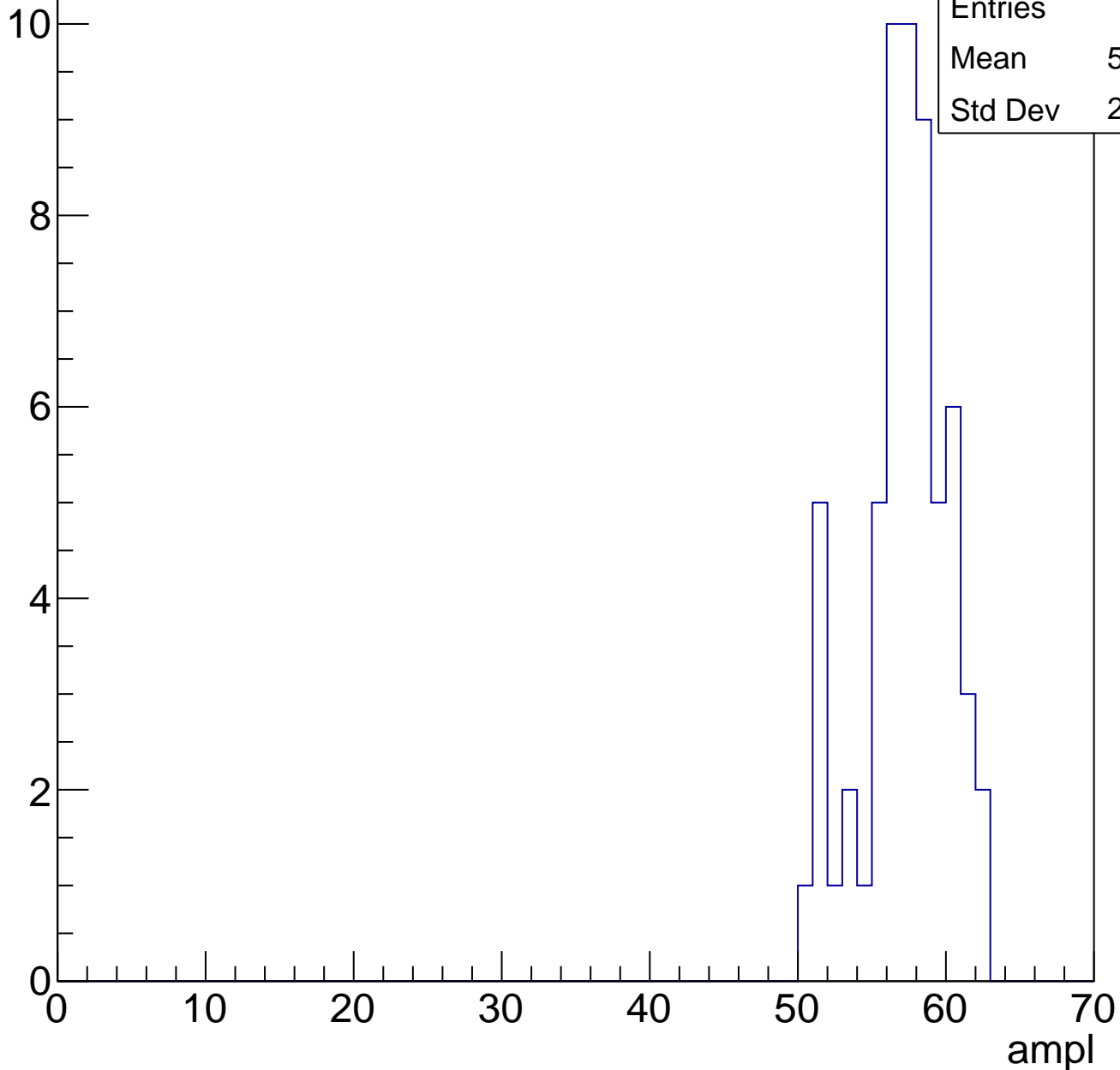


B0L002S, U9-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	56.77
Std Dev	2.895

Entry

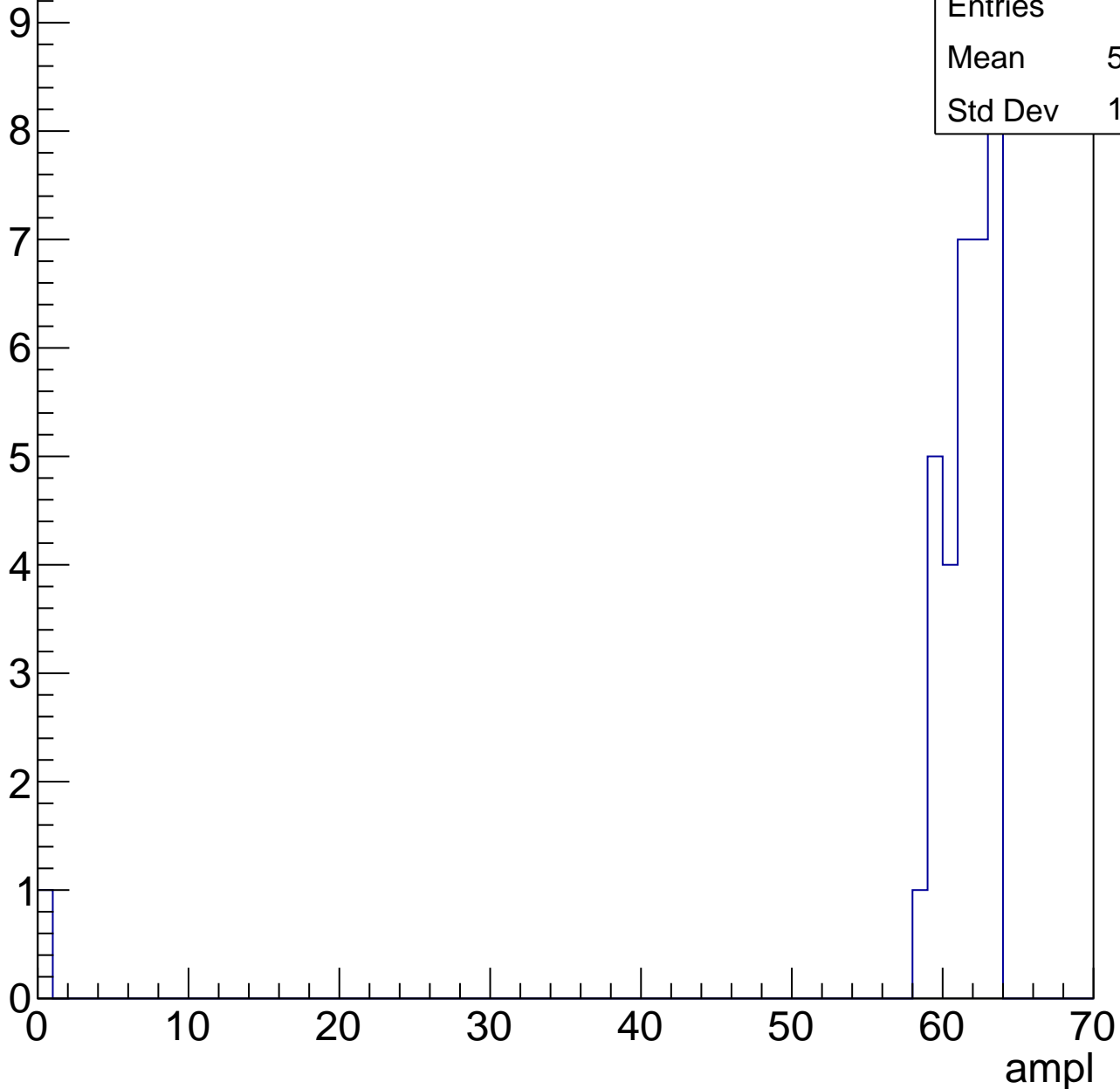


B0L002S, U9-ch64, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

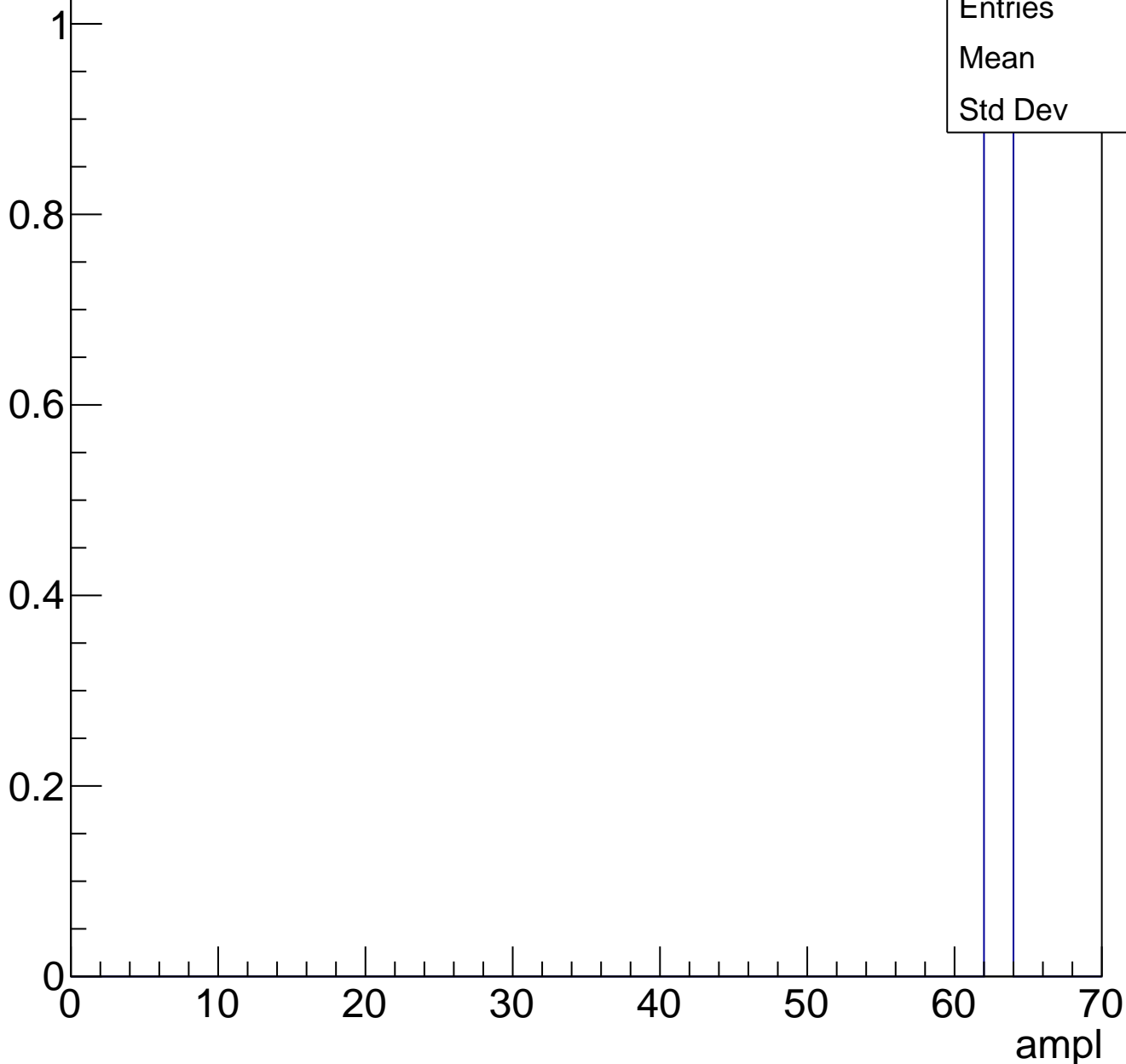
Entries	34
Mean	59.44
Std Dev	10.45



B0L002S, U9-ch64, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch65, adc0

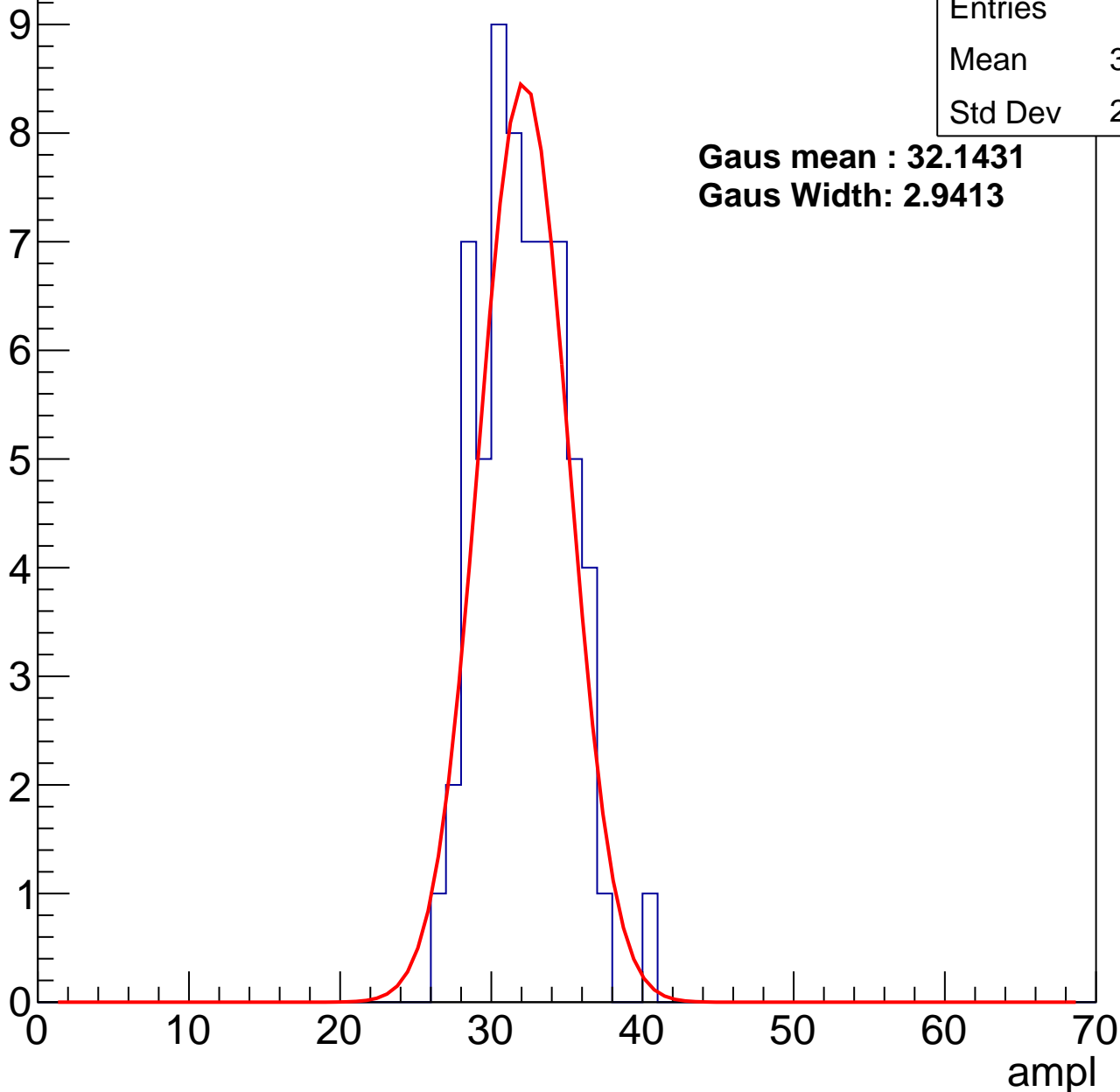
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.69
Std Dev	2.833

Gaus mean : 32.1431

Gaus Width: 2.9413



B0L002S, U9-ch65, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	86
Mean	39.22
Std Dev	3.617

Gaus mean : 39.5251

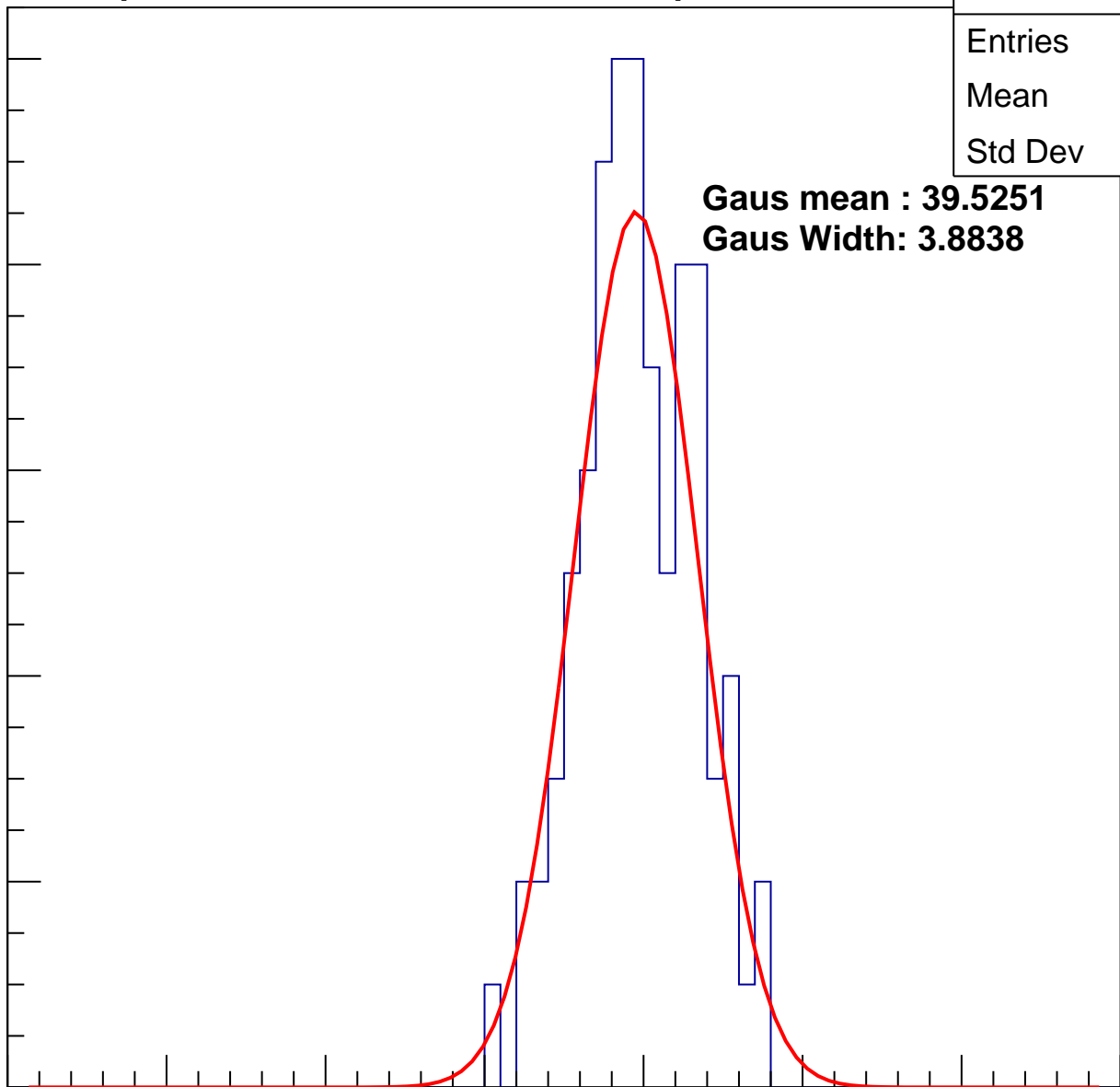
Gaus Width: 3.8838

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch65, adc2

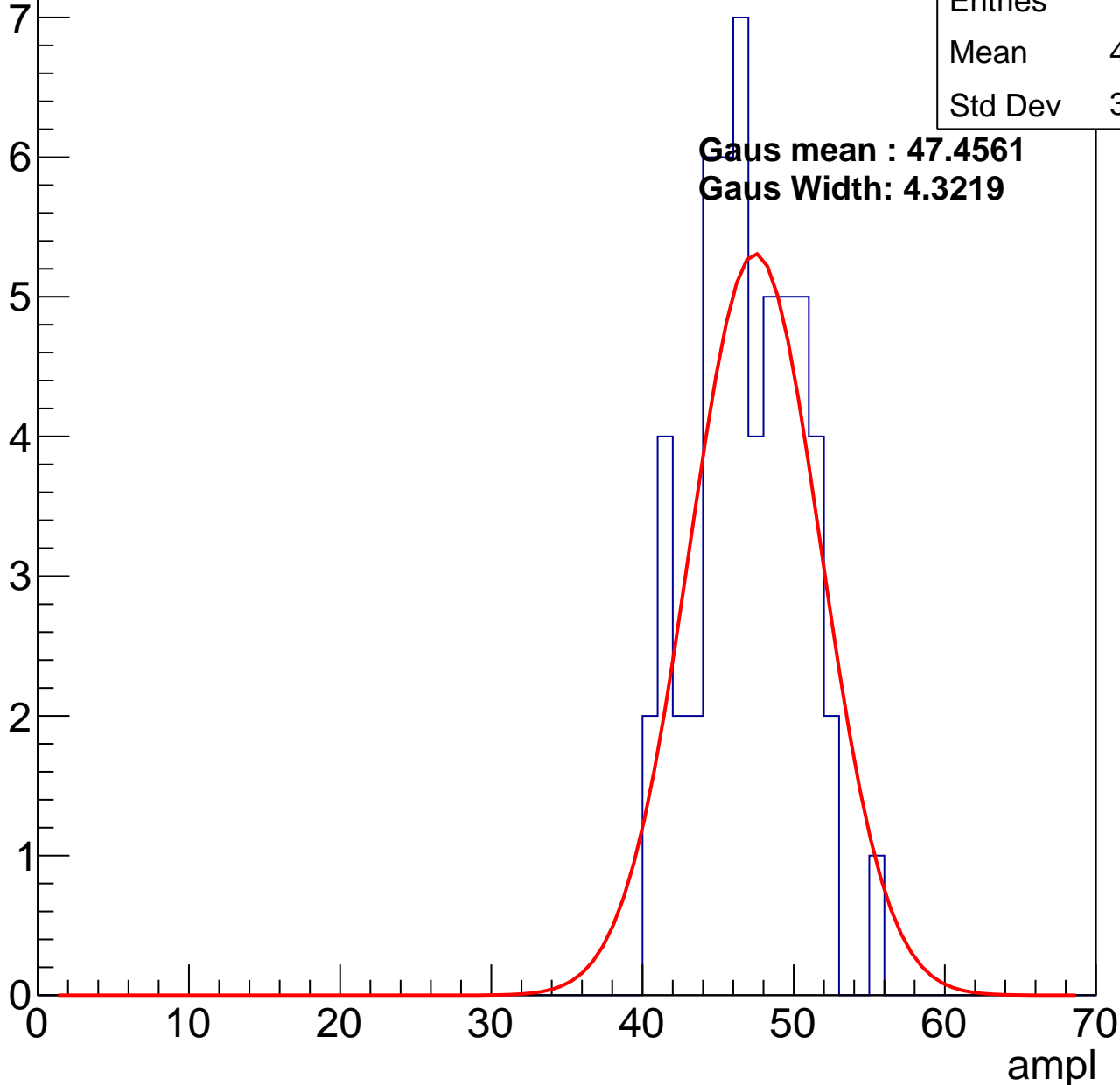
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	46.47
Std Dev	3.416

Gaus mean : 47.4561

Gaus Width: 4.3219

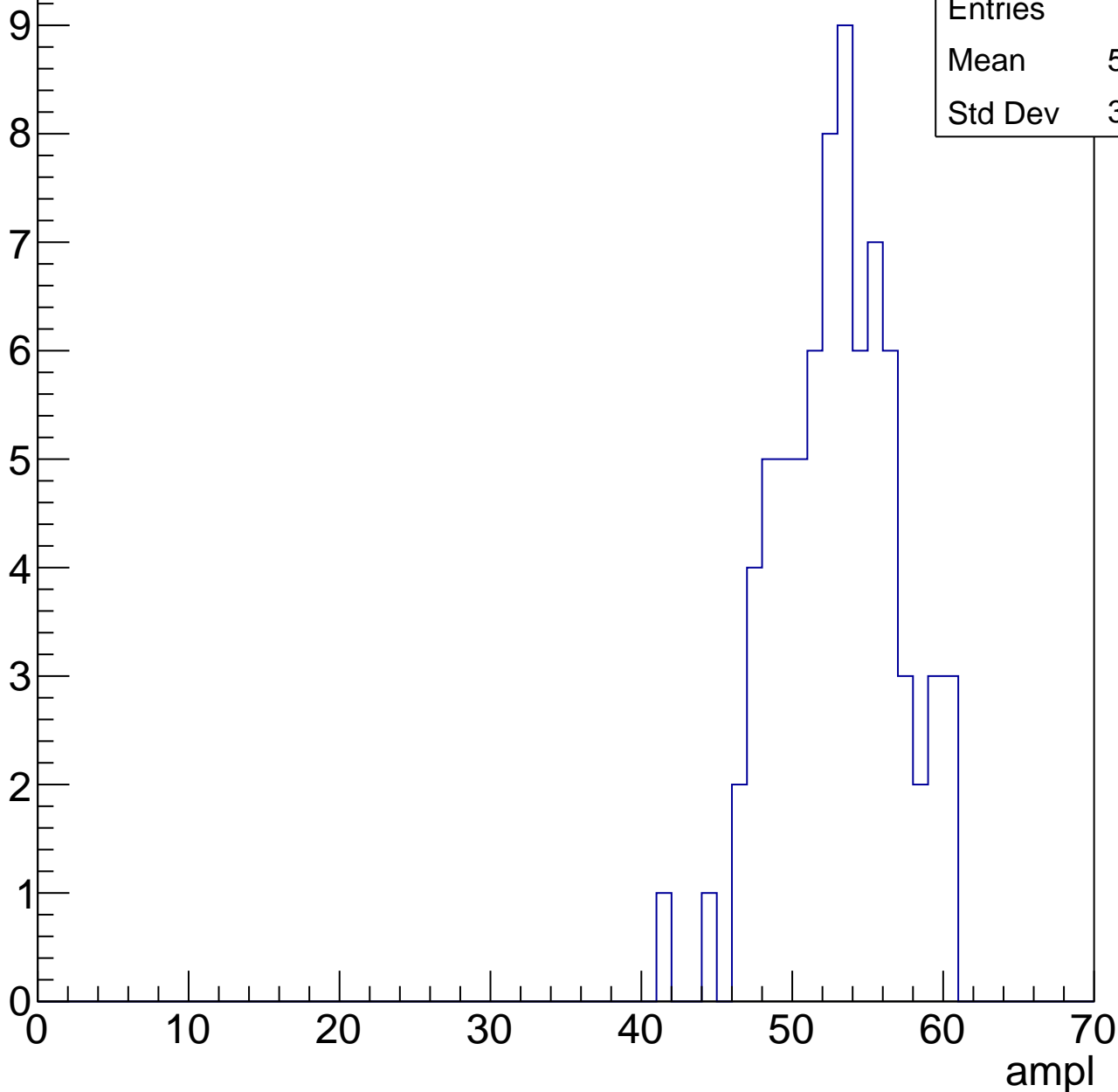


B0L002S, U9-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	52.47
Std Dev	3.922

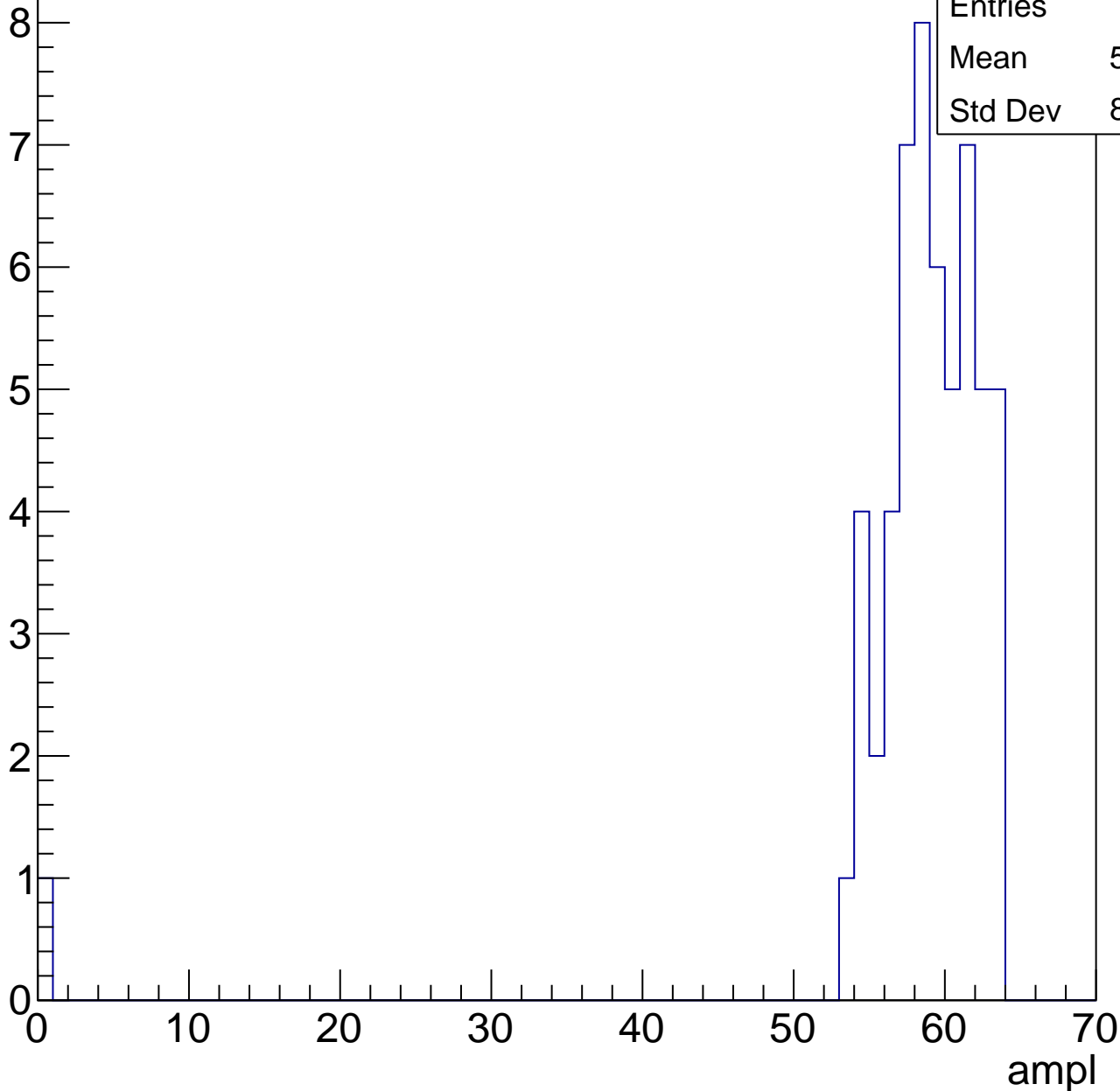


B0L002S, U9-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.67
Std Dev	8.293

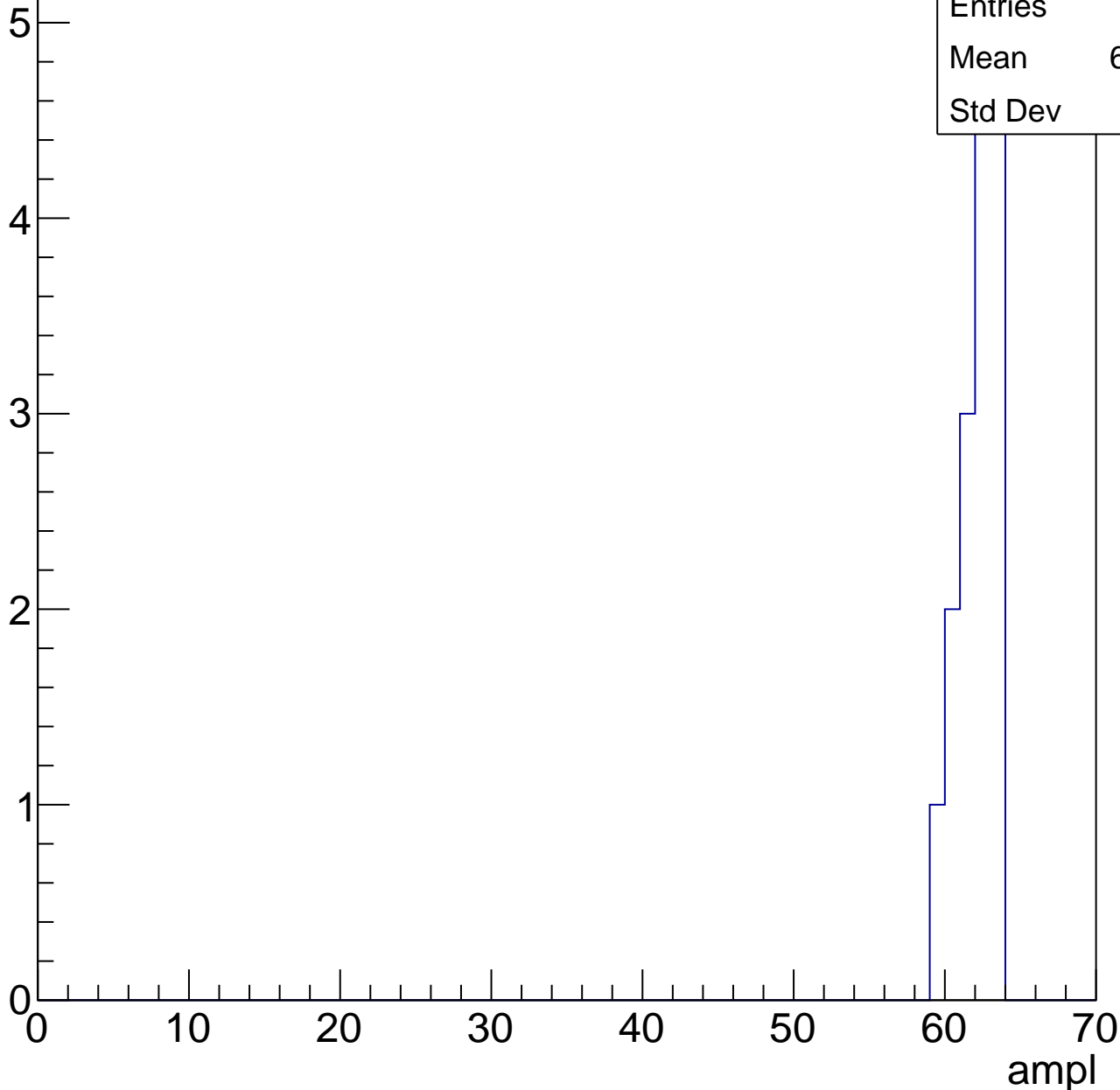


B0L002S, U9-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	16
Mean	61.69
Std Dev	1.21



B0L002S, U9-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L002S, U9-ch66, adc0

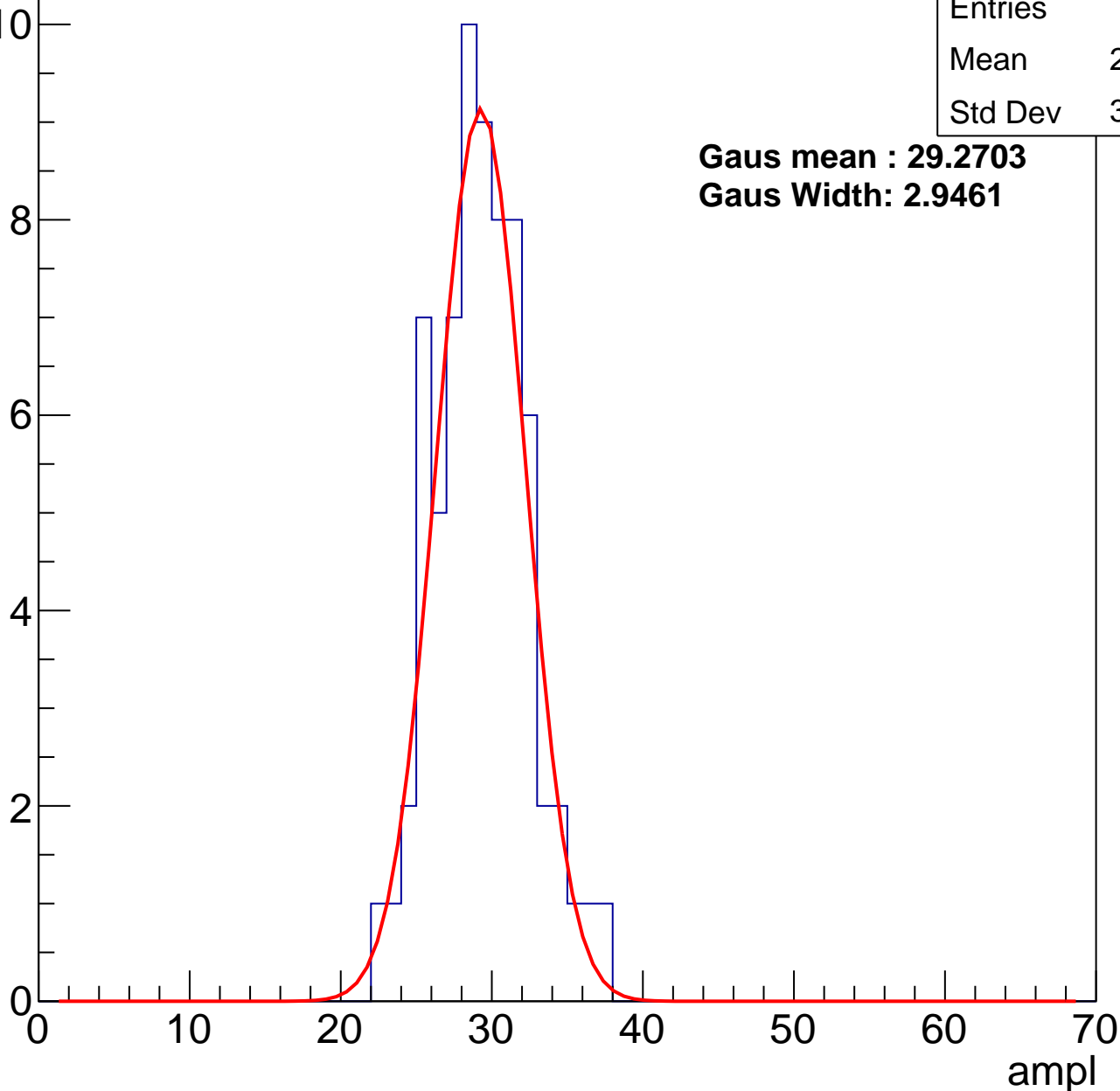
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	28.87
Std Dev	3.035

Gaus mean : 29.2703

Gaus Width: 2.9461



B0L002S, U9-ch66, adc1

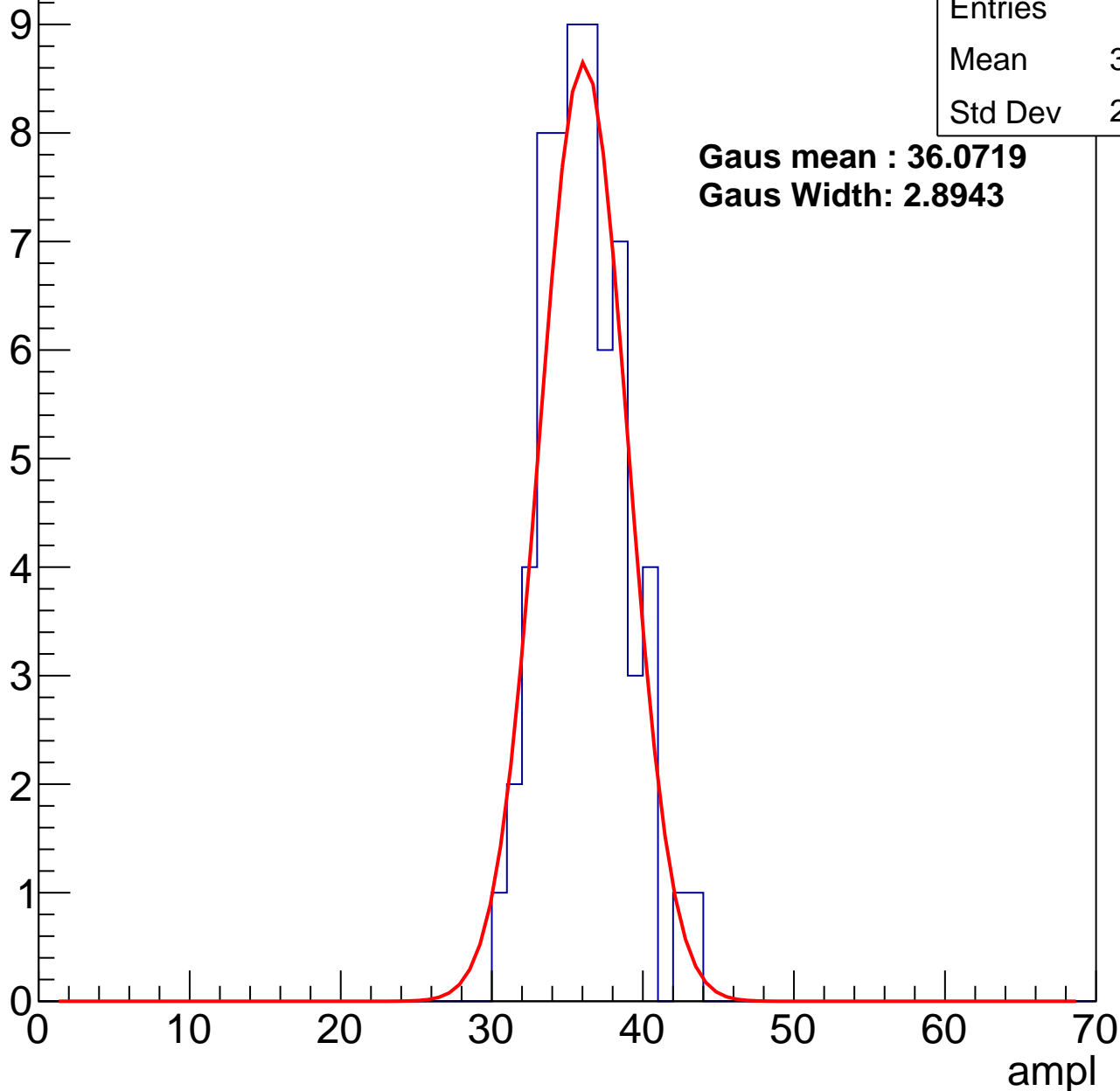
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	35.63
Std Dev	2.716

Gaus mean : 36.0719

Gaus Width: 2.8943



B0L002S, U9-ch66, adc2

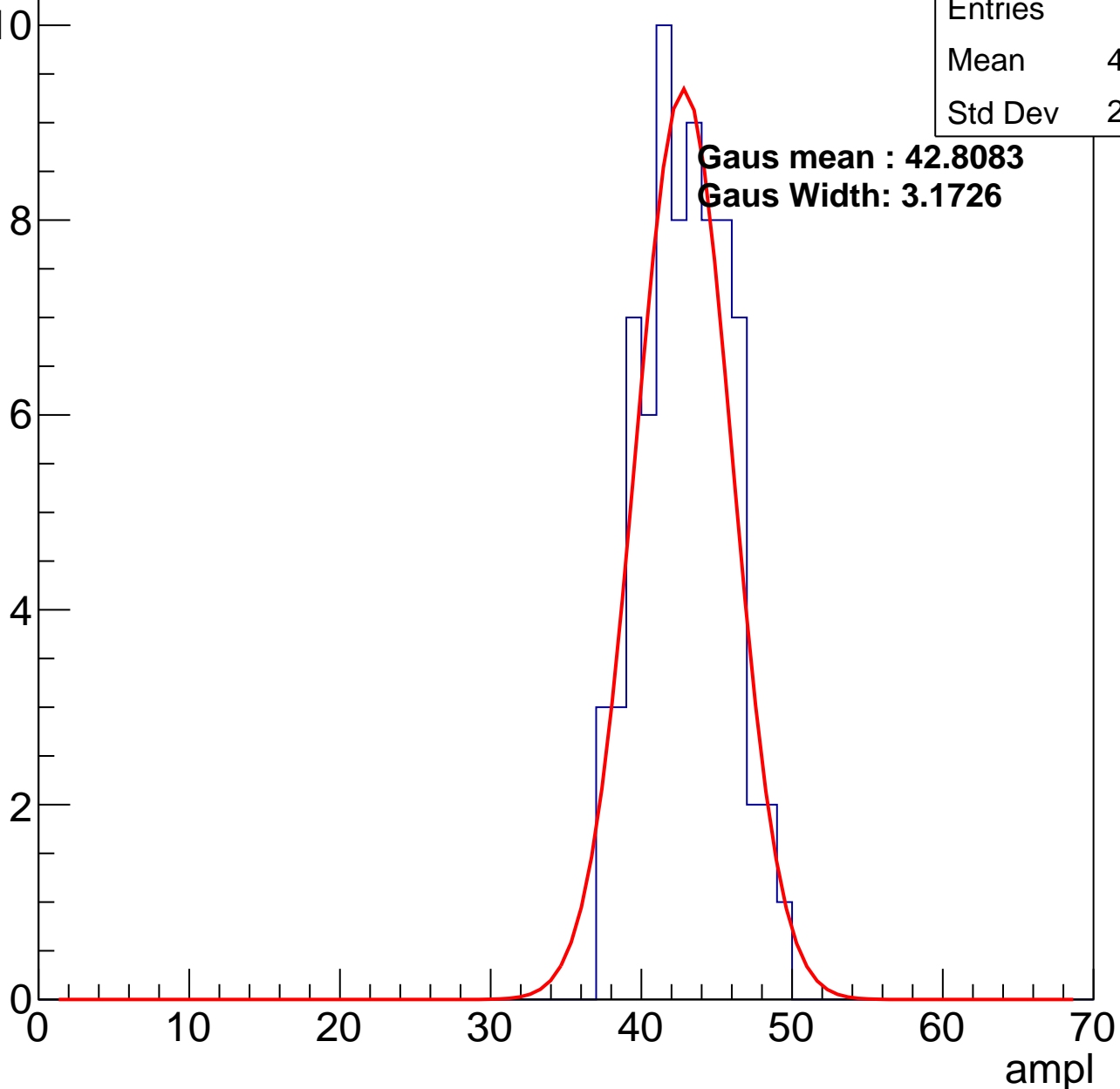
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	42.49
Std Dev	2.844

Gaus mean : 42.8083

Gaus Width: 3.1726

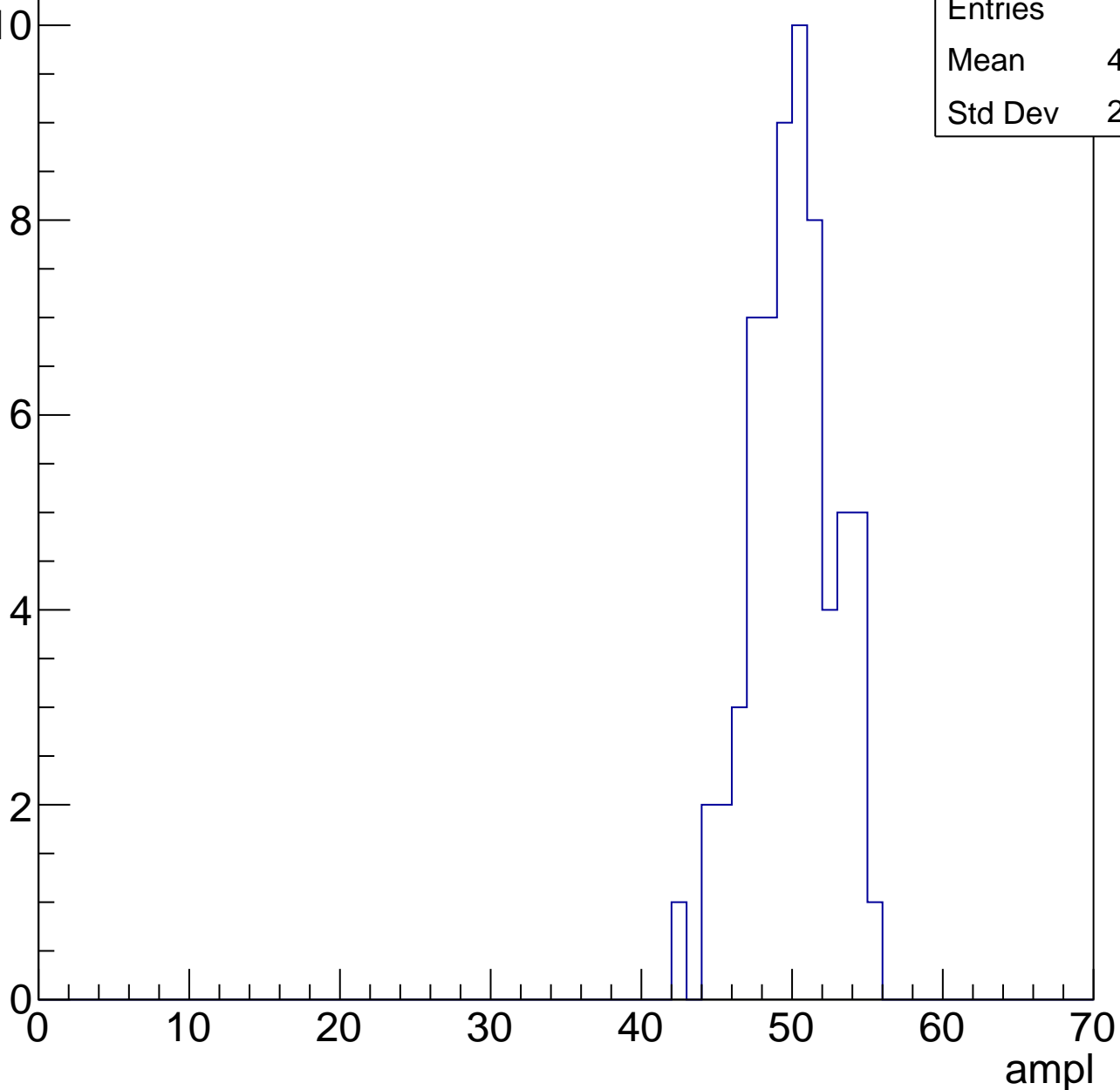


B0L002S, U9-ch66, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	49.53
Std Dev	2.778



B0L002S, U9-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1

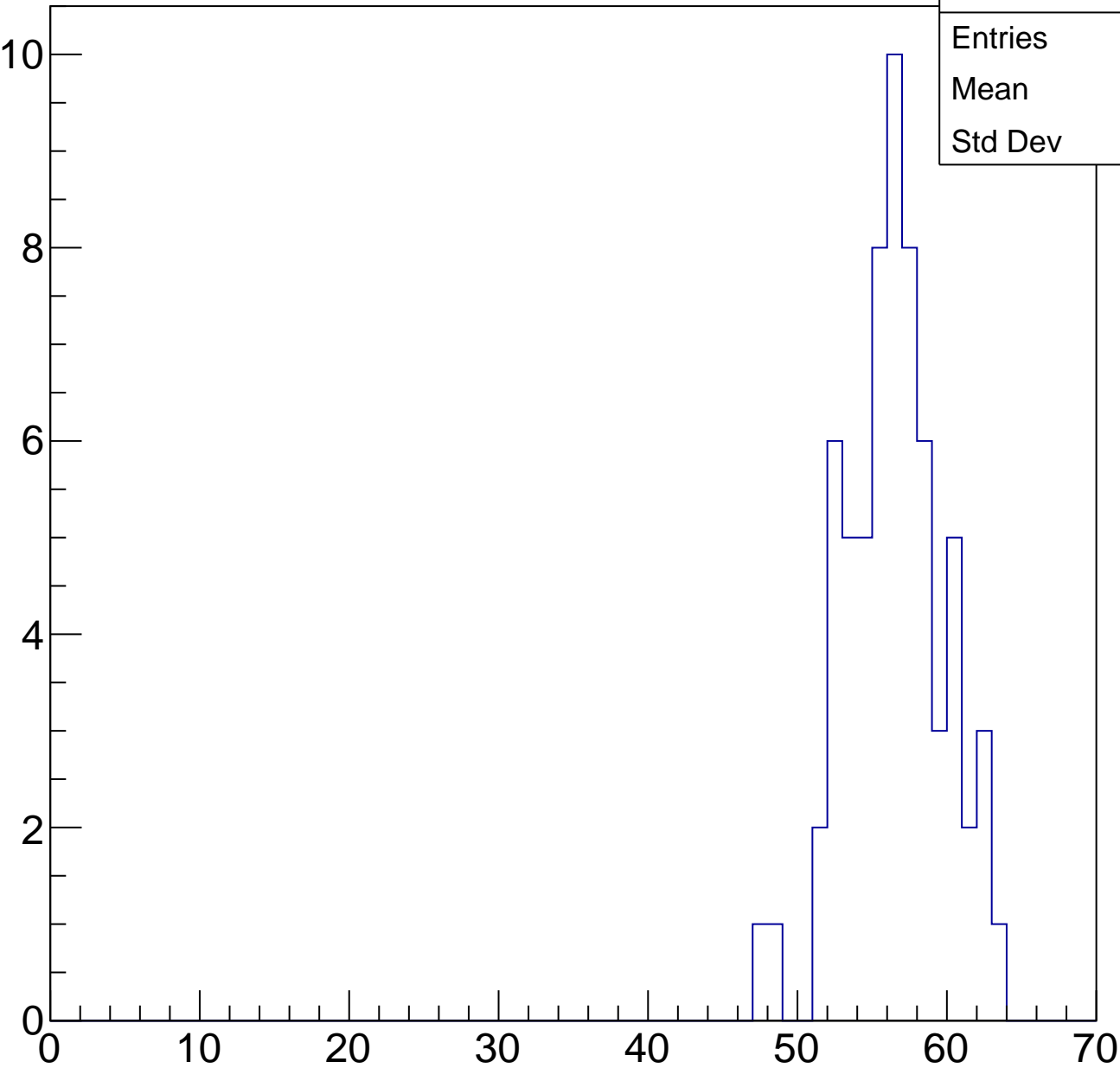
Entries	66
Mean	56
Std Dev	3.28

Entry

10
8
6
4
2
0

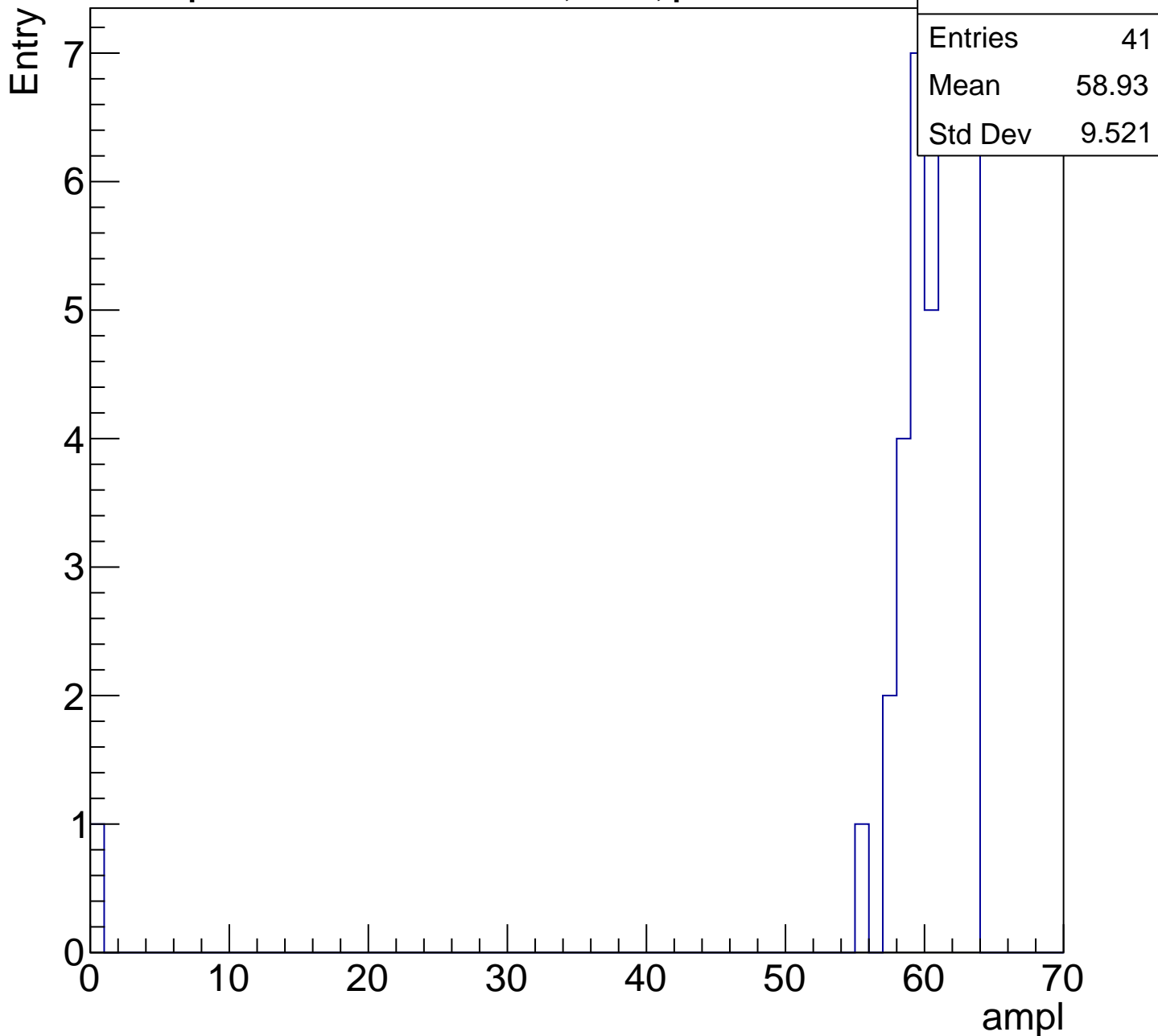
0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch67, adc0

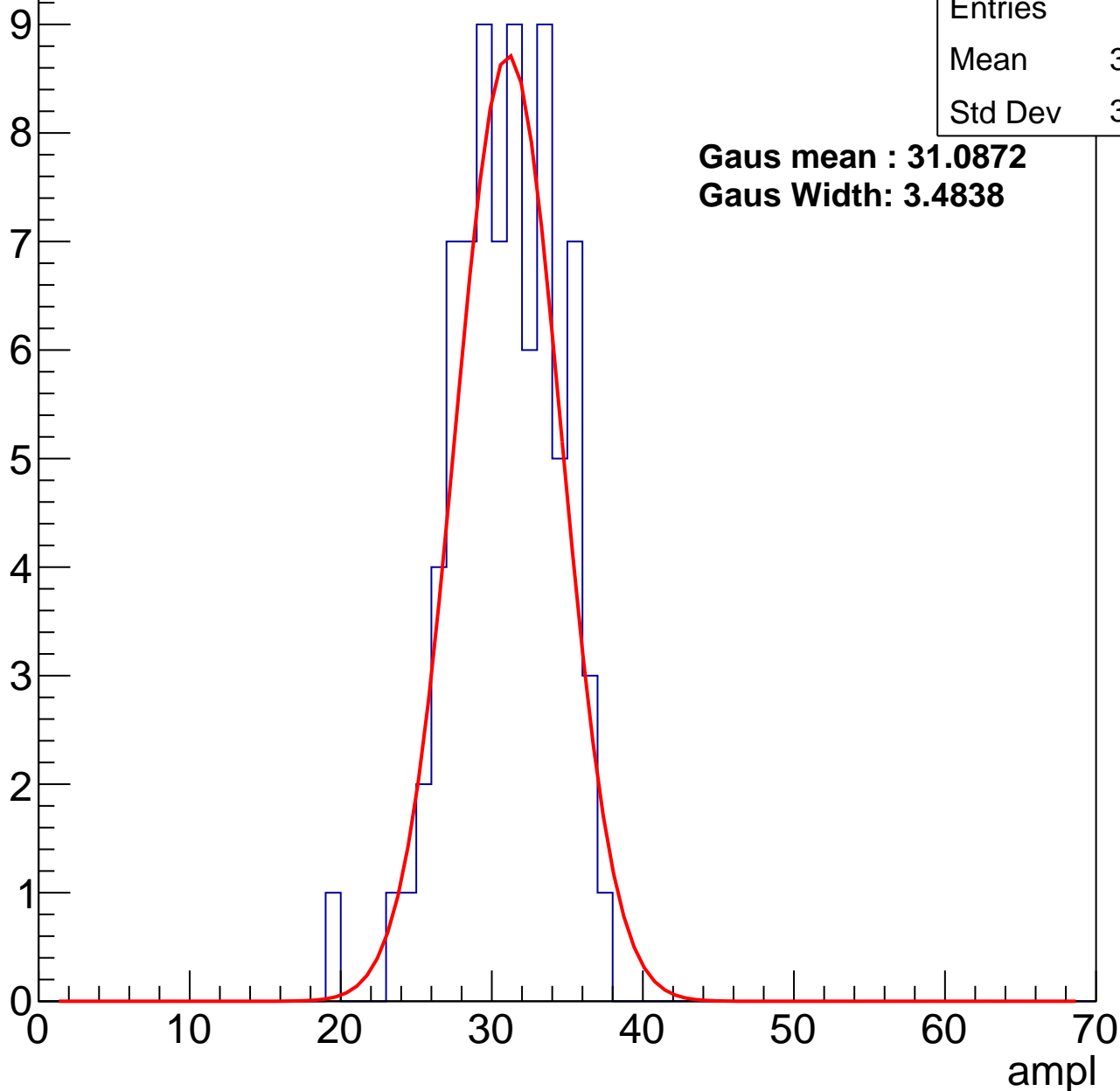
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.43
Std Dev	3.434

Gaus mean : 31.0872

Gaus Width: 3.4838



B0L002S, U9-ch67, adc1

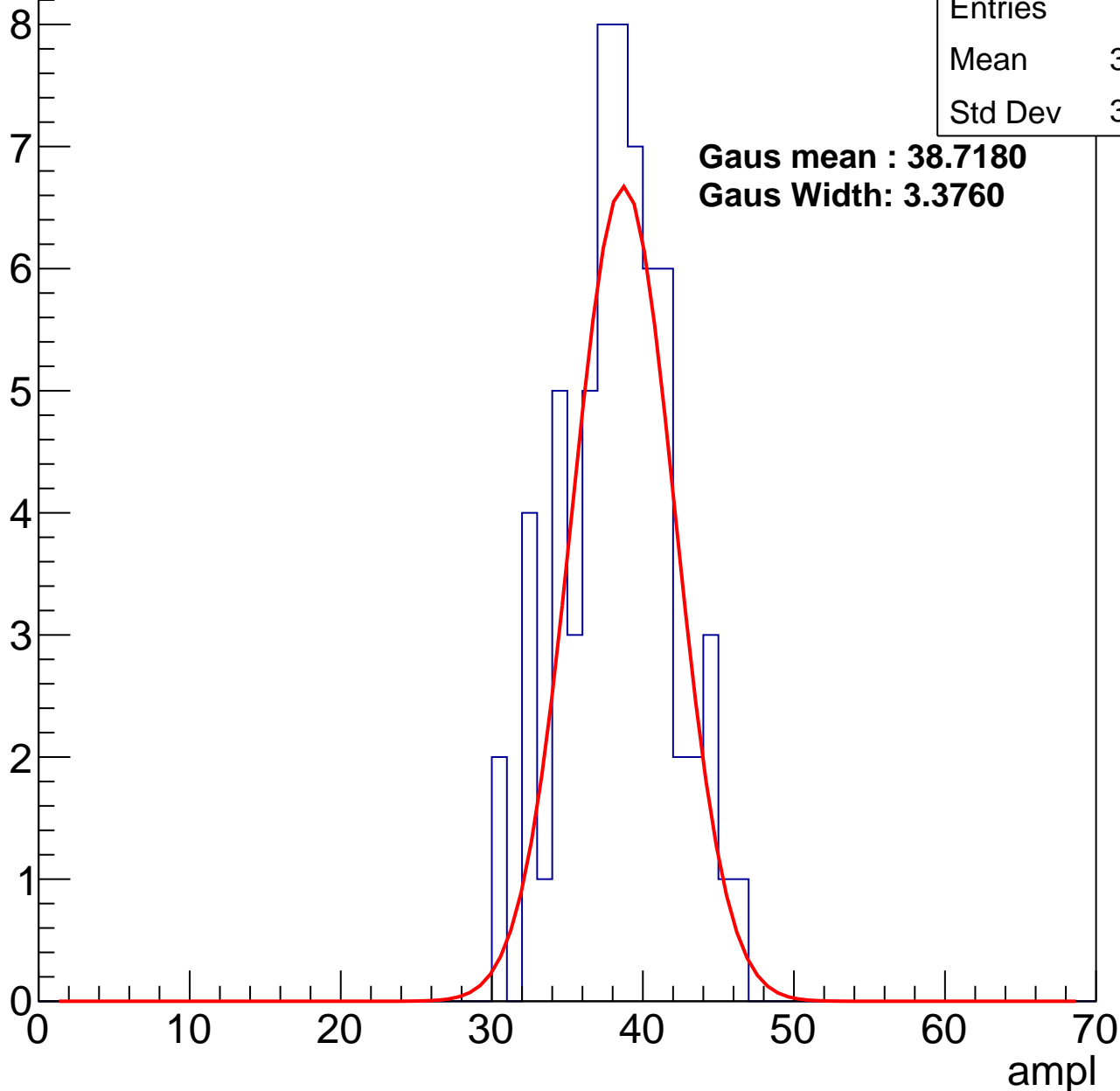
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.94
Std Dev	3.579

Gaus mean : 38.7180

Gaus Width: 3.3760



B0L002S, U9-ch67, adc2

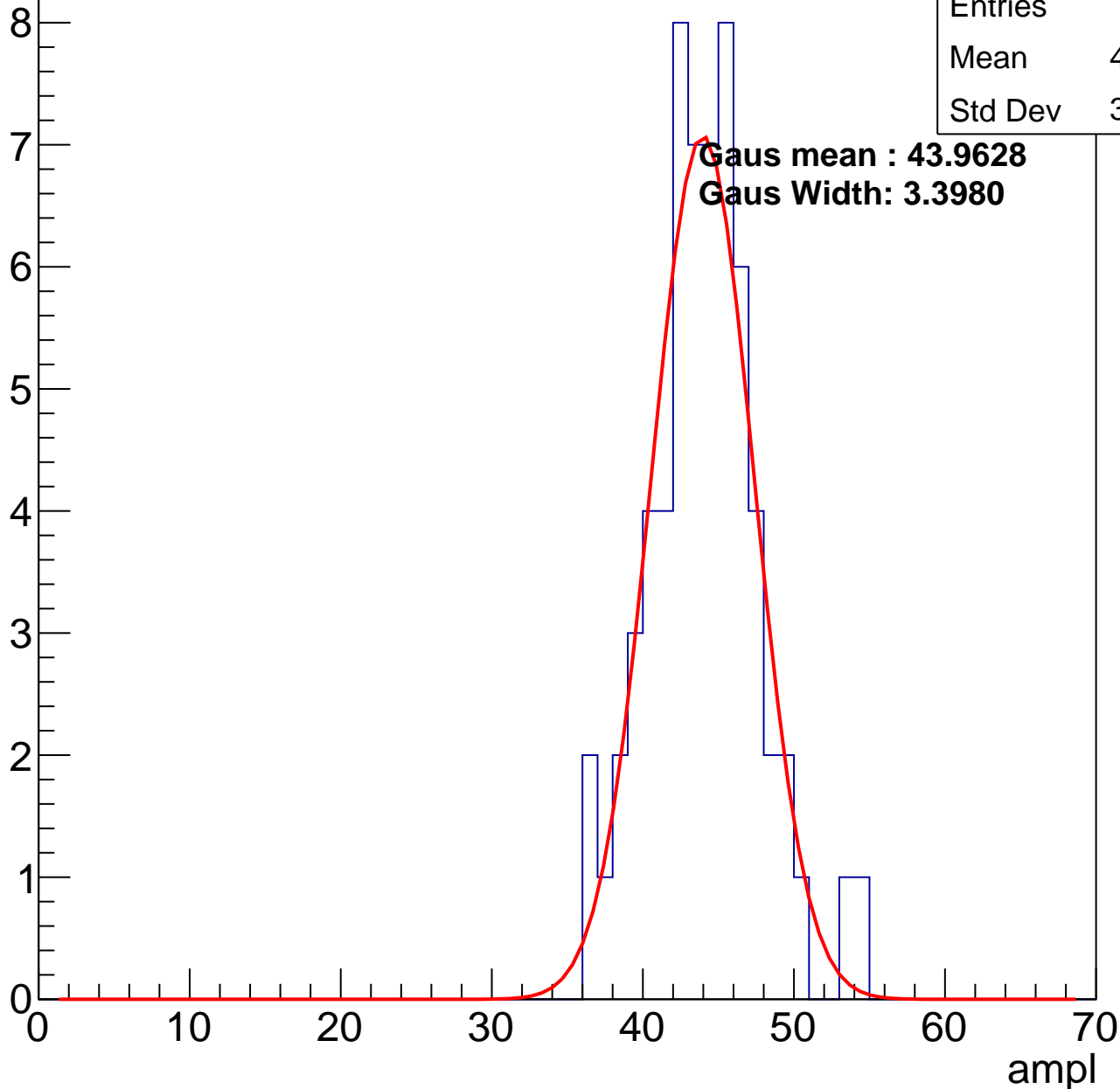
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.59
Std Dev	3.606

Gaus mean : 43.9628

Gaus Width: 3.3980

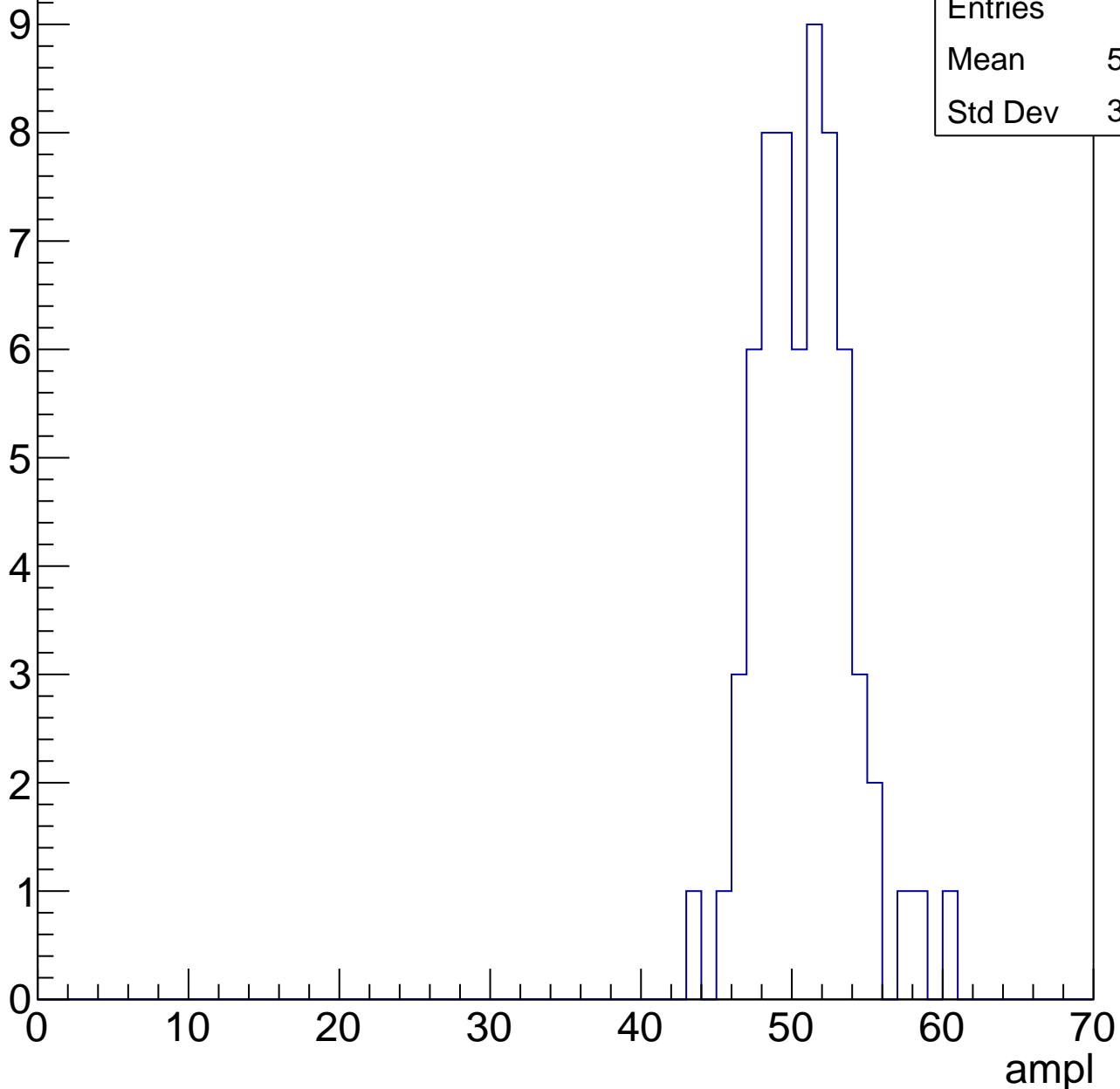


B0L002S, U9-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

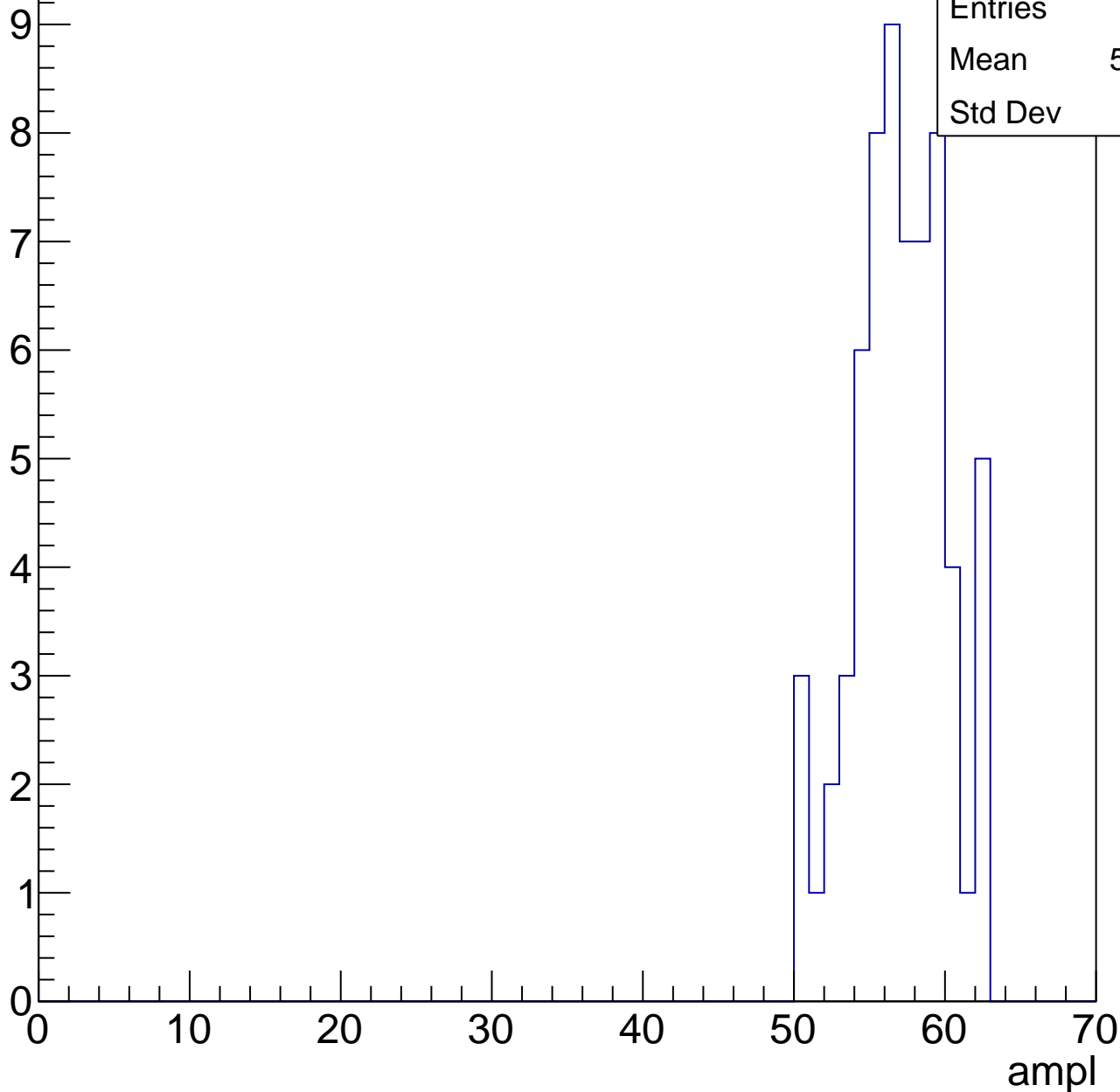
Entries	64
Mean	50.38
Std Dev	3.095



B0L002S, U9-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

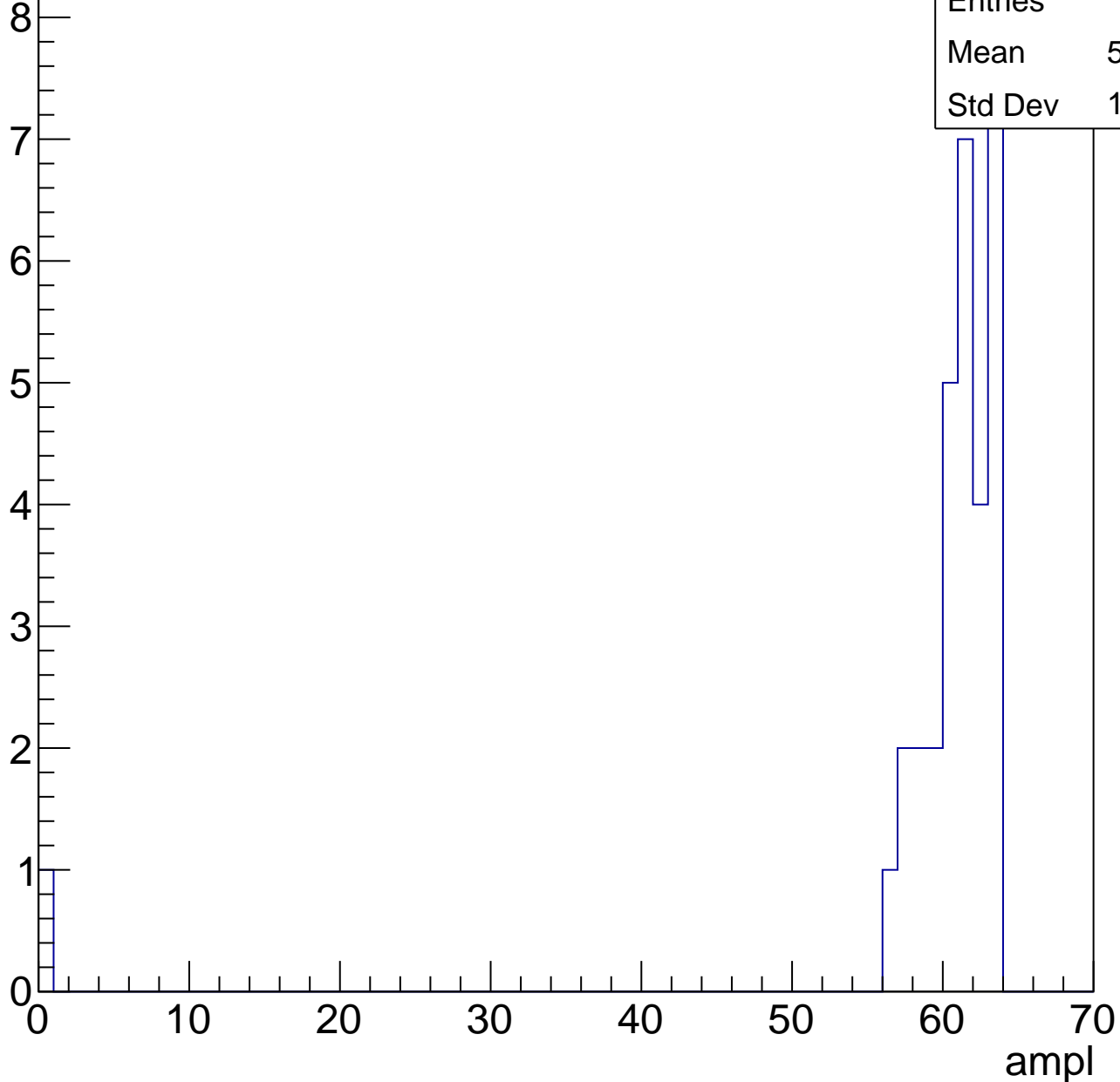
Entry



B0L002S, U9-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

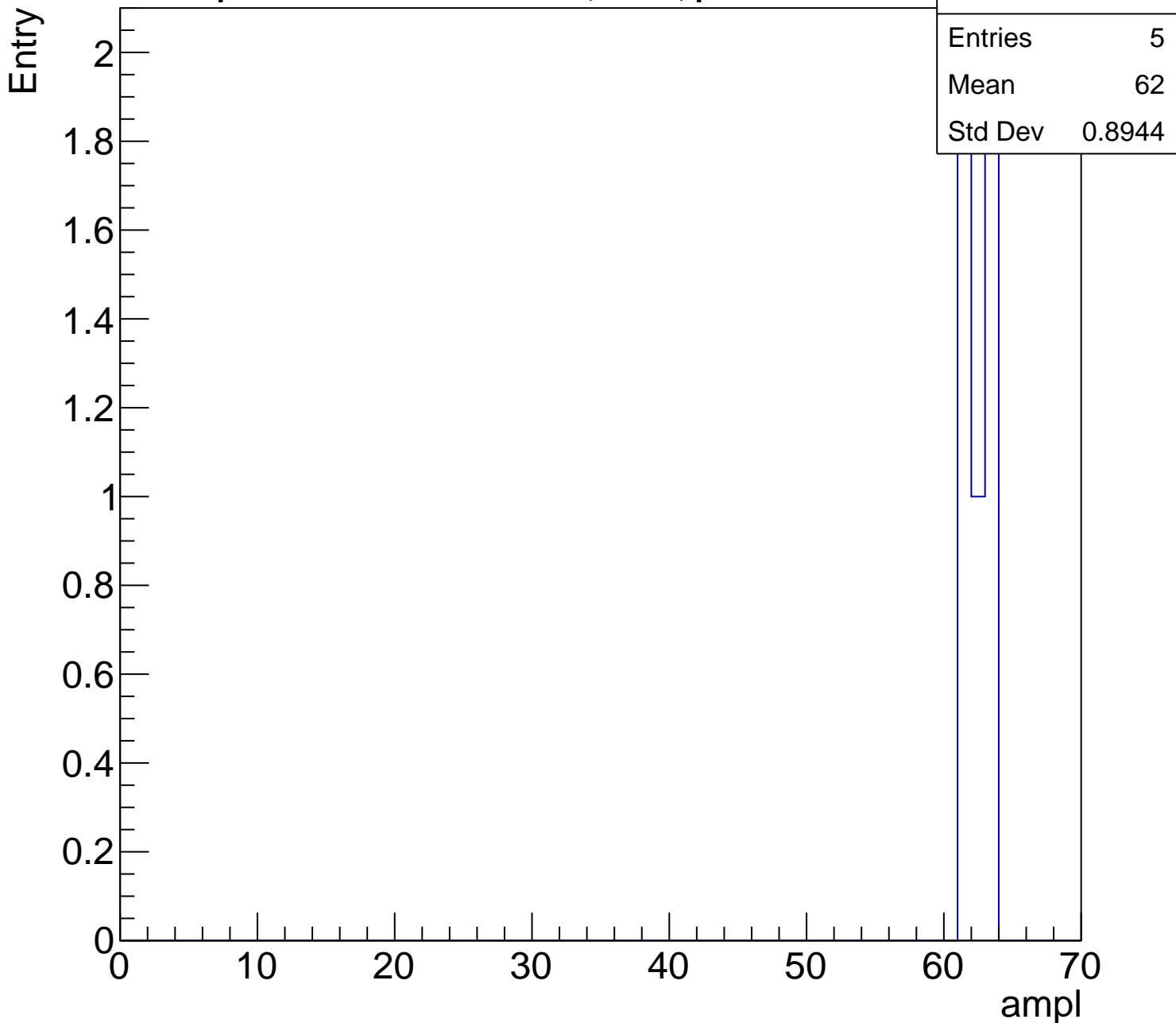
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch68, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	31.42
Std Dev	4.936

Gaus mean : 32.0781

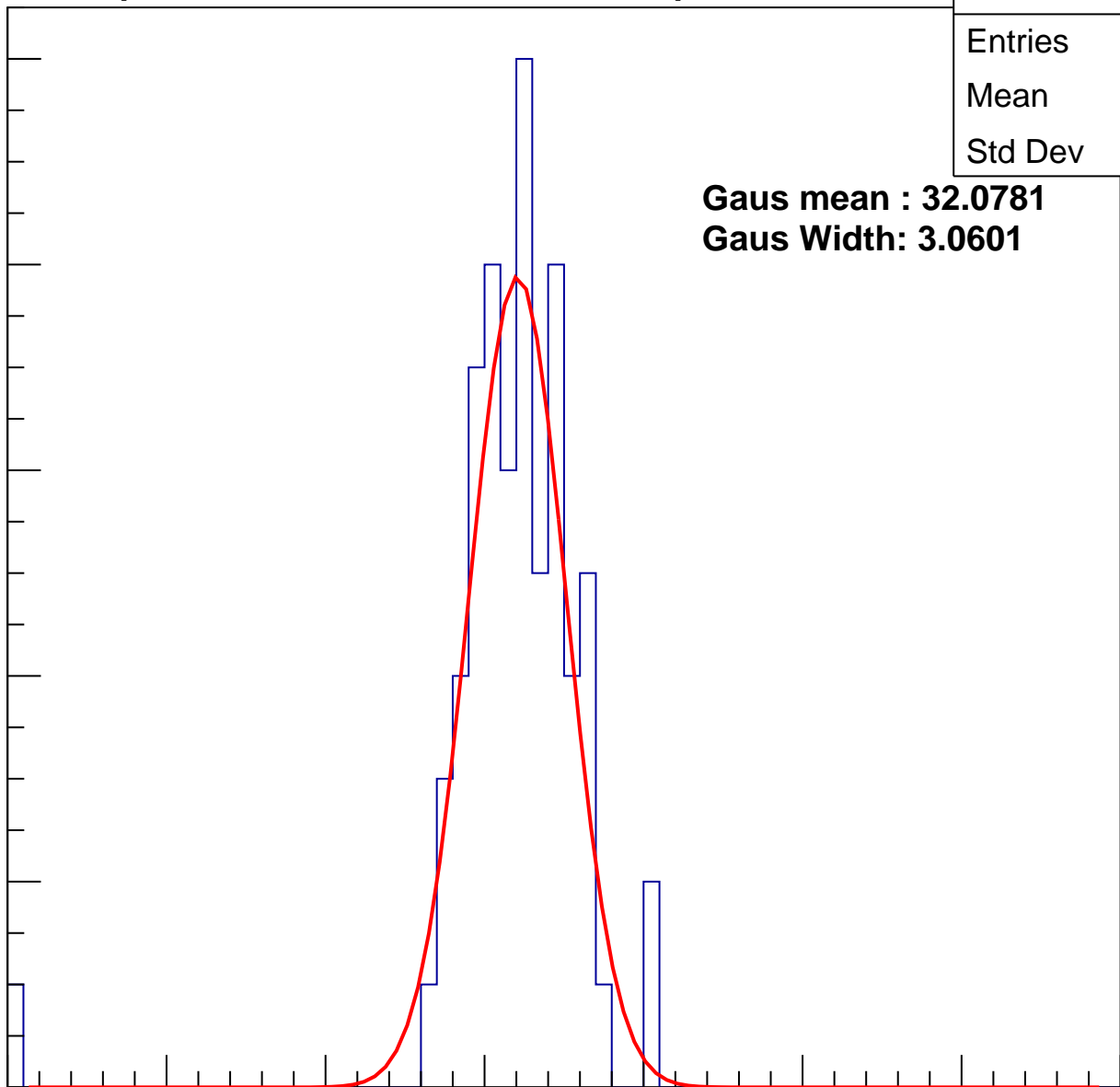
Gaus Width: 3.0601

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch68, adc1

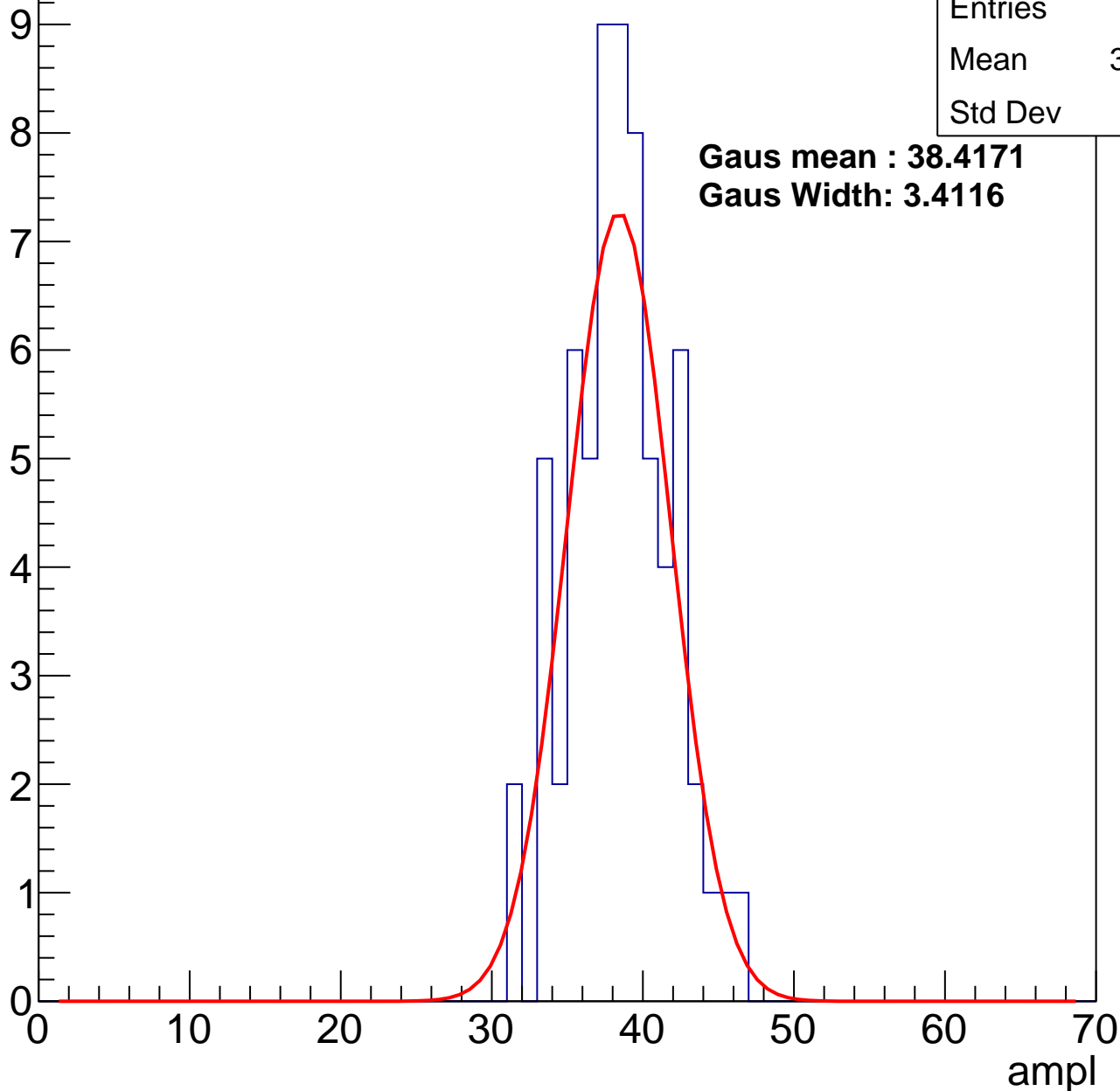
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.02
Std Dev	3.25

Gaus mean : 38.4171

Gaus Width: 3.4116



B0L002S, U9-ch68, adc2

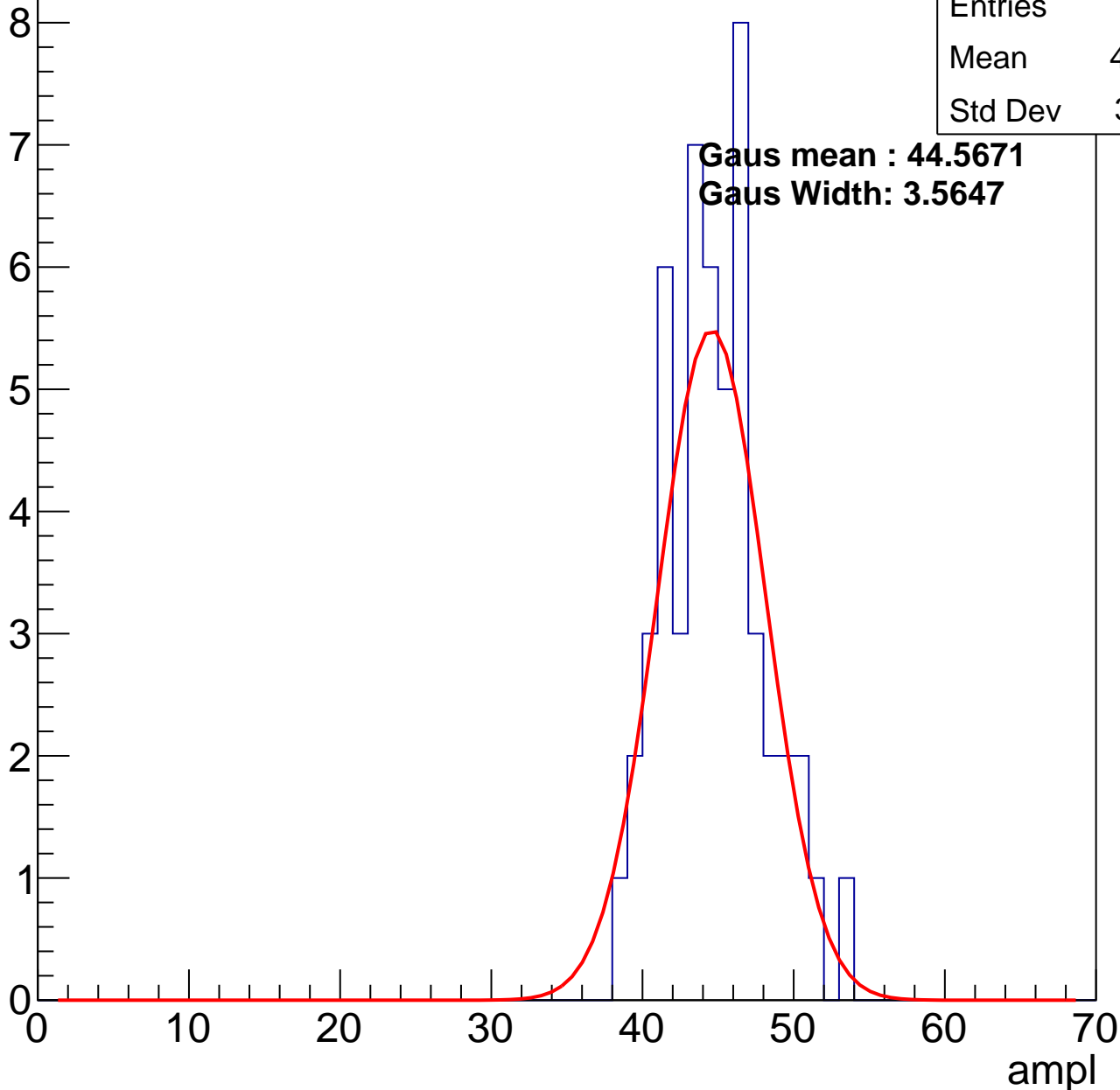
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	44.33
Std Dev	3.251

Gaus mean : 44.5671

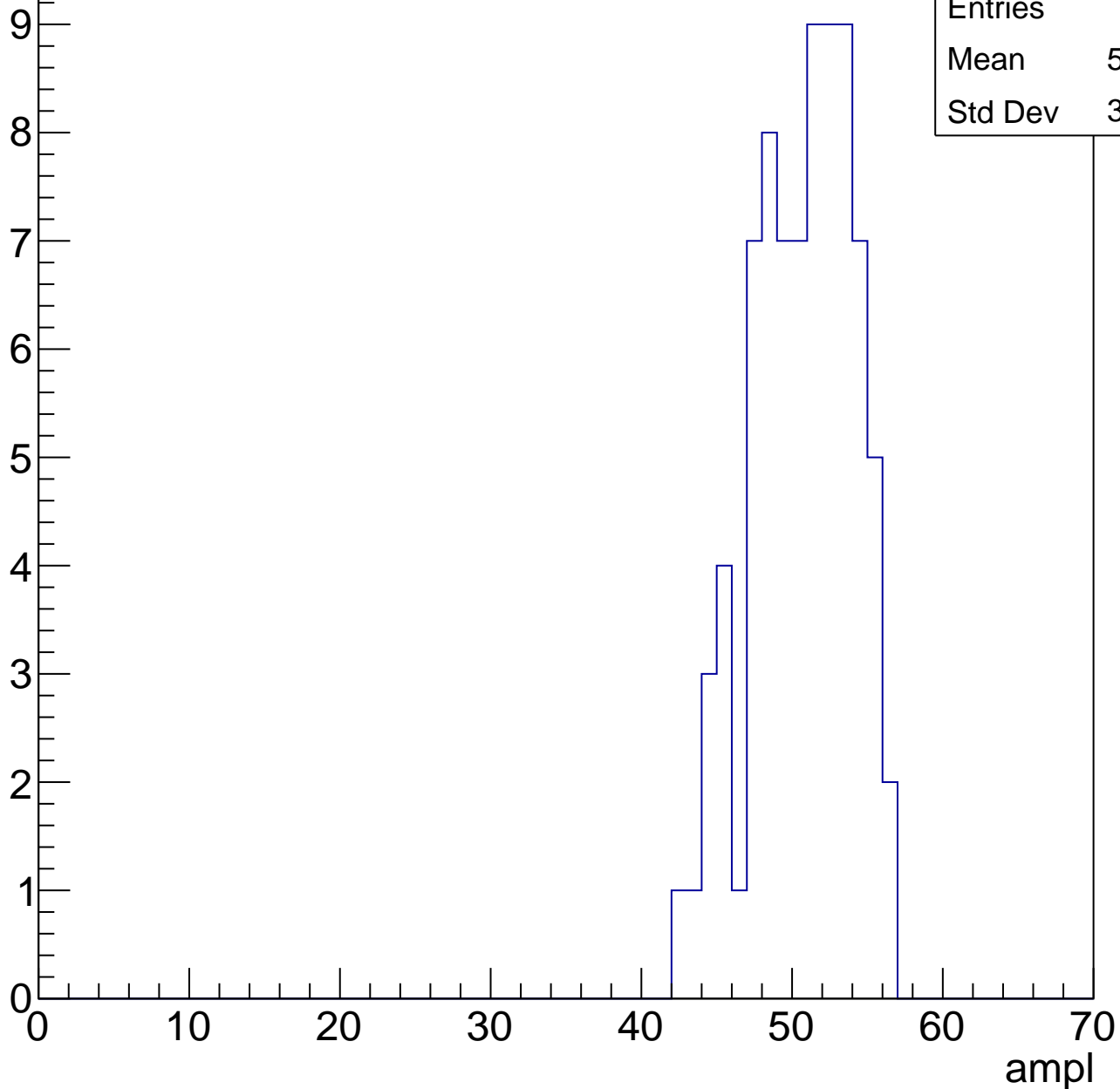
Gaus Width: 3.5647



B0L002S, U9-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



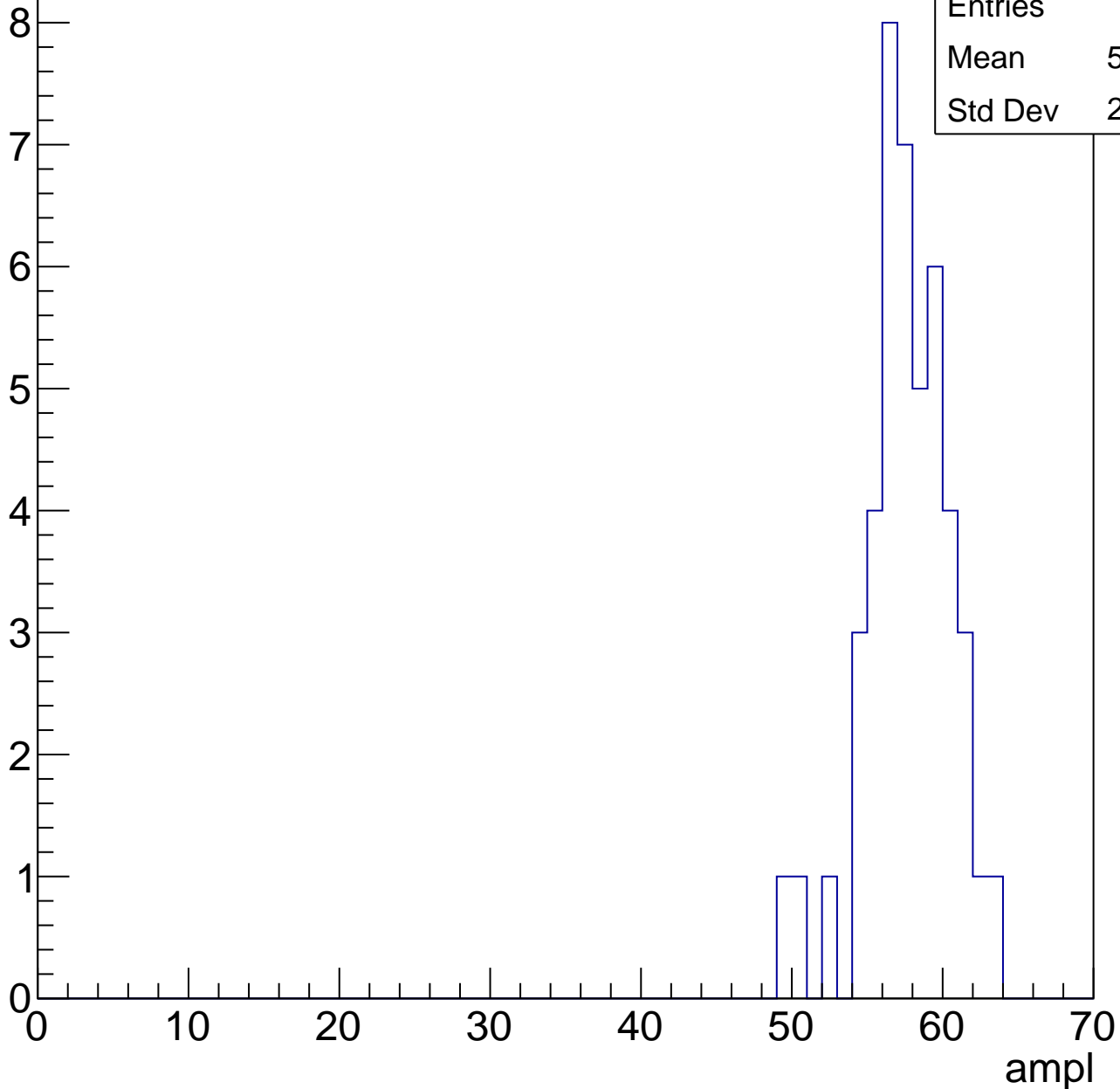
Entries	80
Mean	50.23
Std Dev	3.298

B0L002S, U9-ch68, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	57.16
Std Dev	2.836

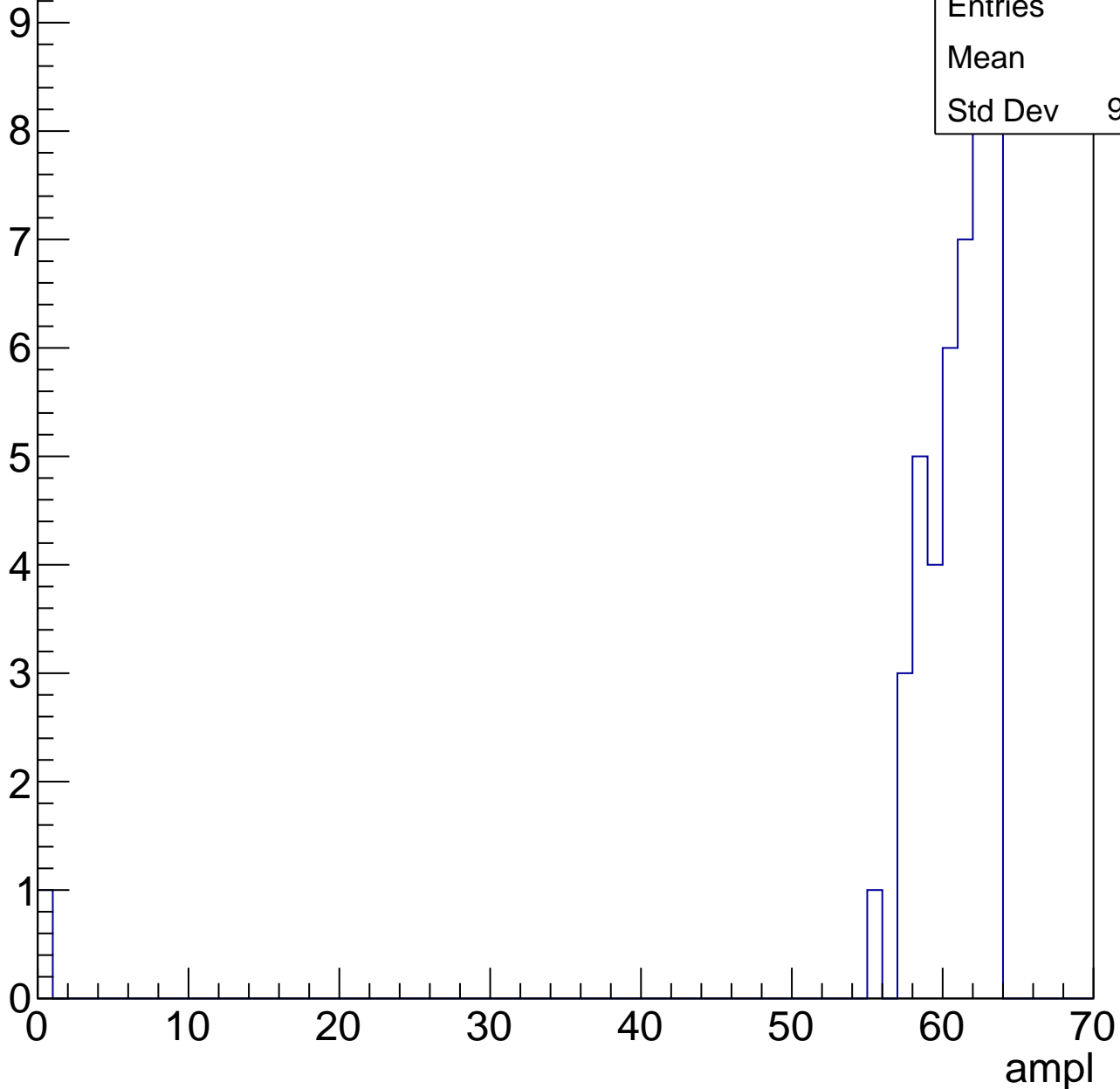


B0L002S, U9-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.2
Std Dev	9.154



B0L002S, U9-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch69, adc0

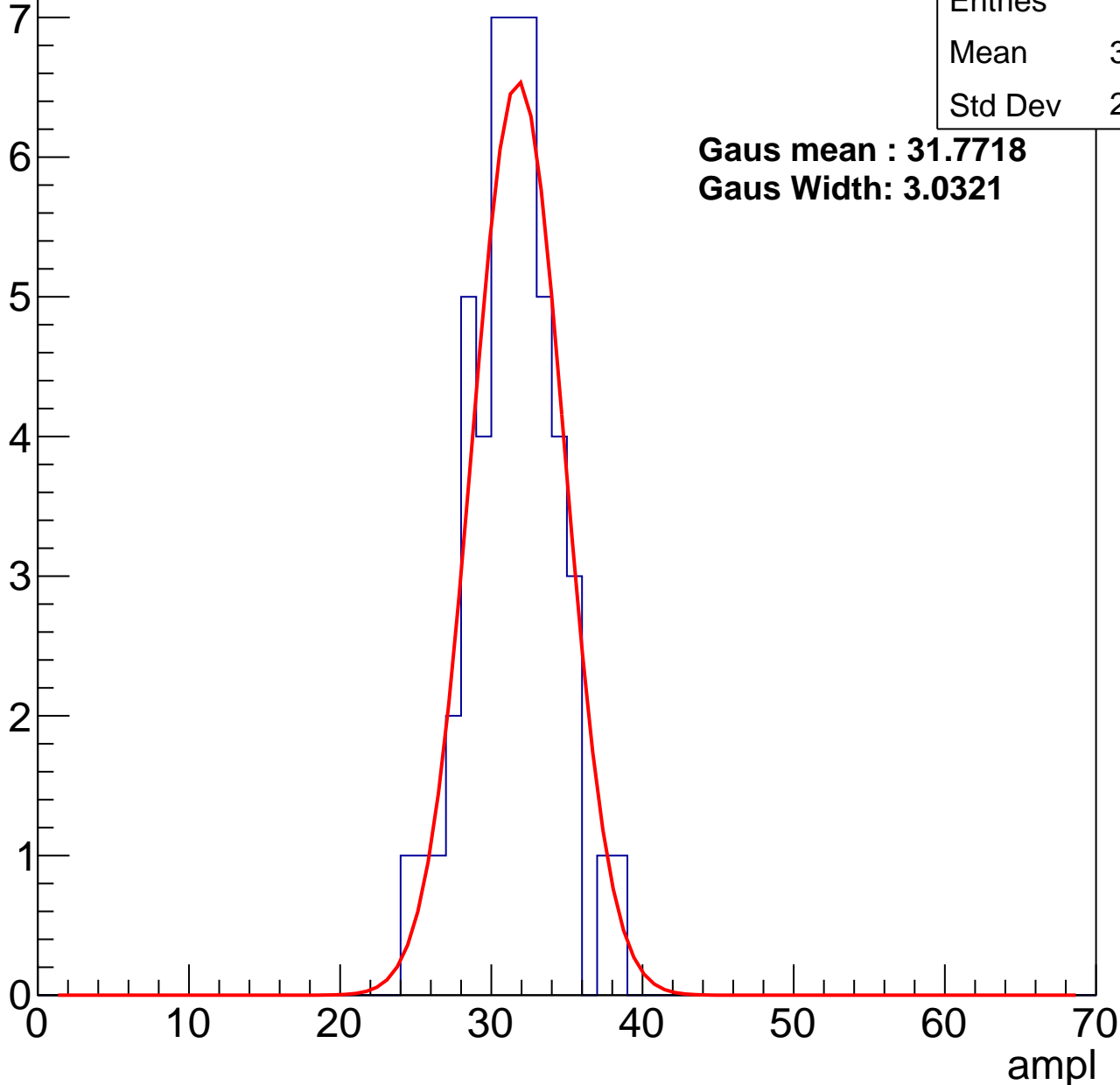
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	30.96
Std Dev	2.878

Gaus mean : 31.7718

Gaus Width: 3.0321



B0L002S, U9-ch69, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	37.03
Std Dev	3.687

Gaus mean : 37.7555

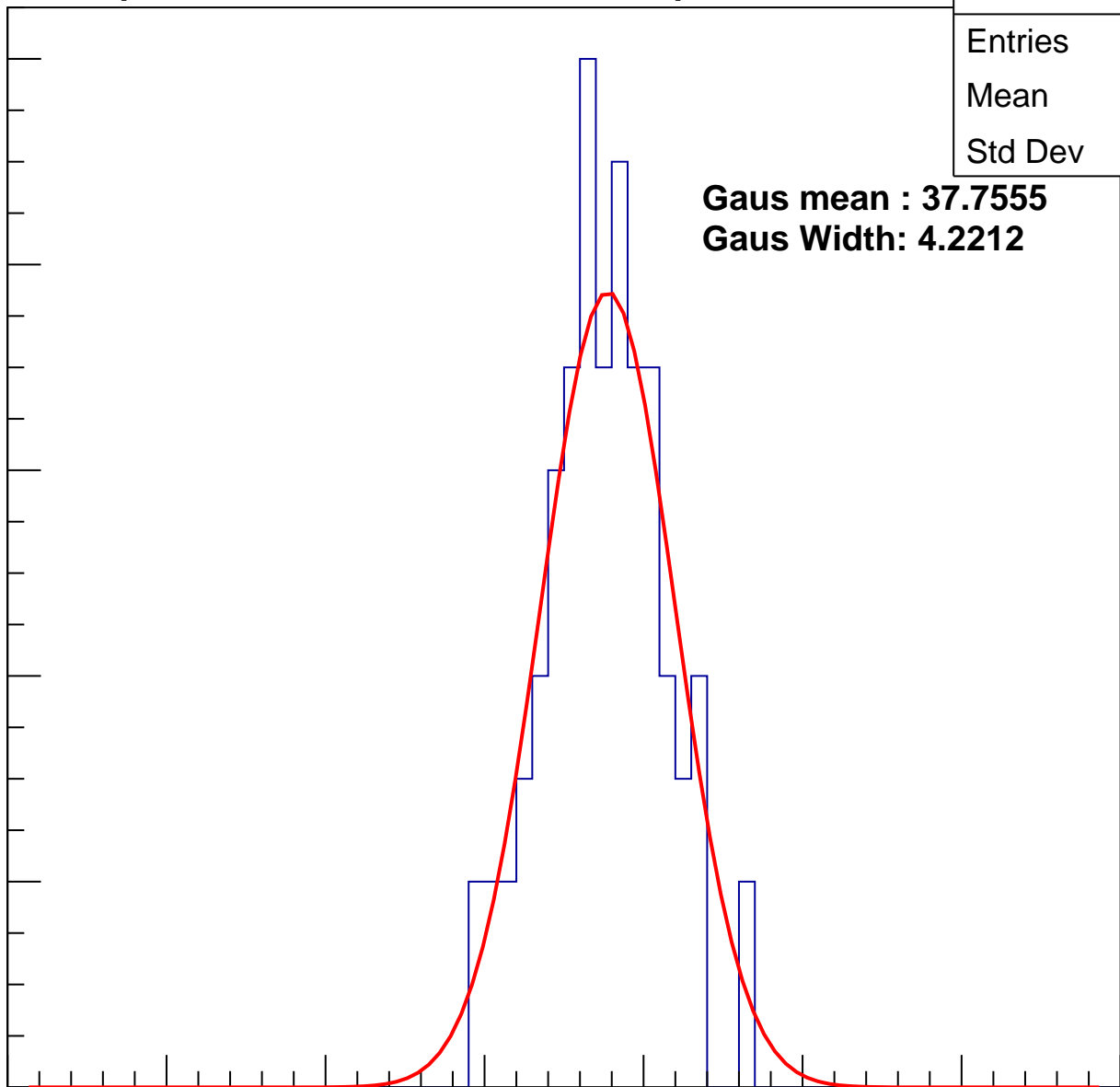
Gaus Width: 4.2212

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch69, adc2

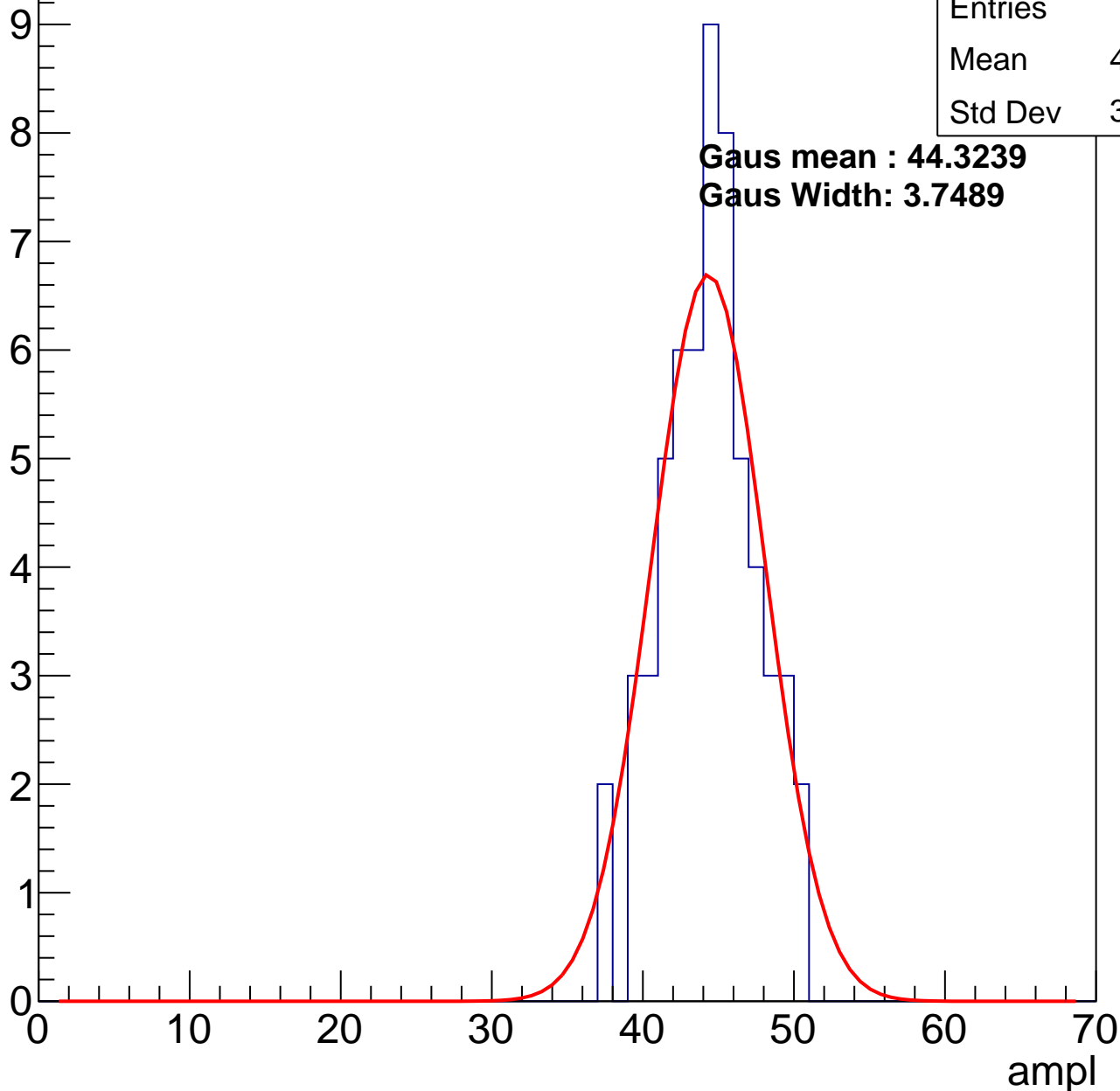
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.92
Std Dev	3.066

Gaus mean : 44.3239

Gaus Width: 3.7489

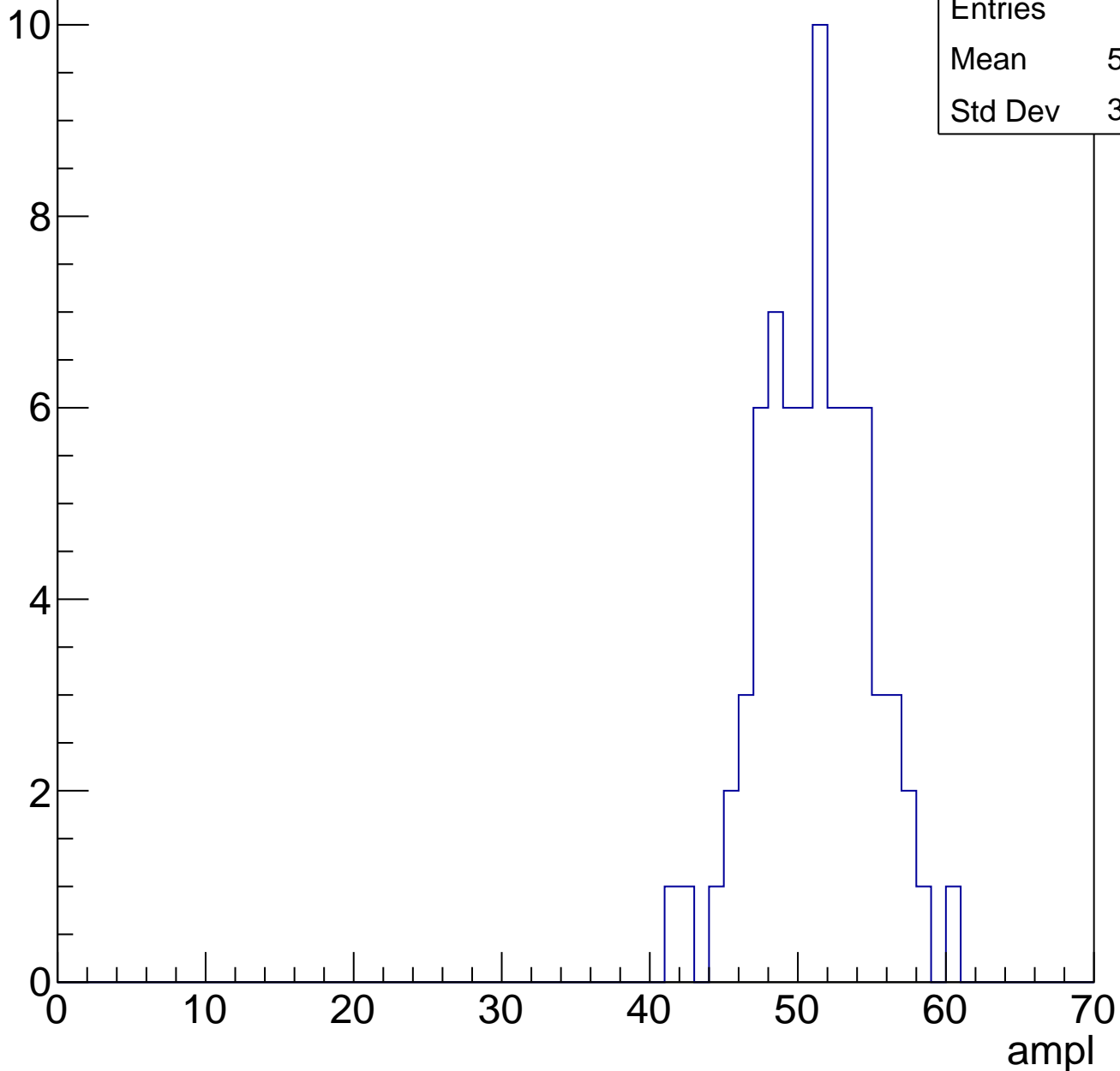


B0L002S, U9-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	50.65
Std Dev	3.689

Entry

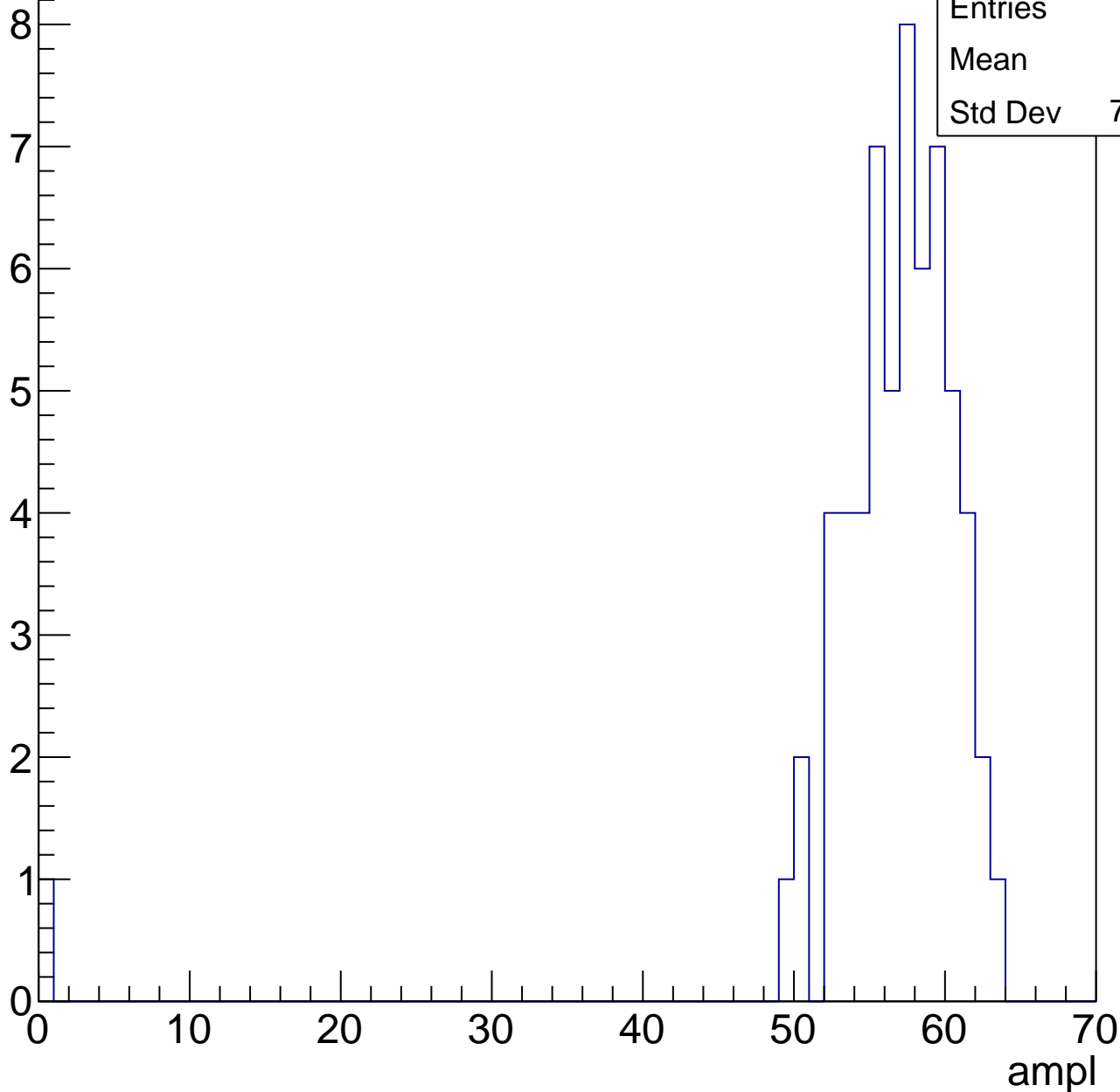


B0L002S, U9-ch69, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	55.7
Std Dev	7.862

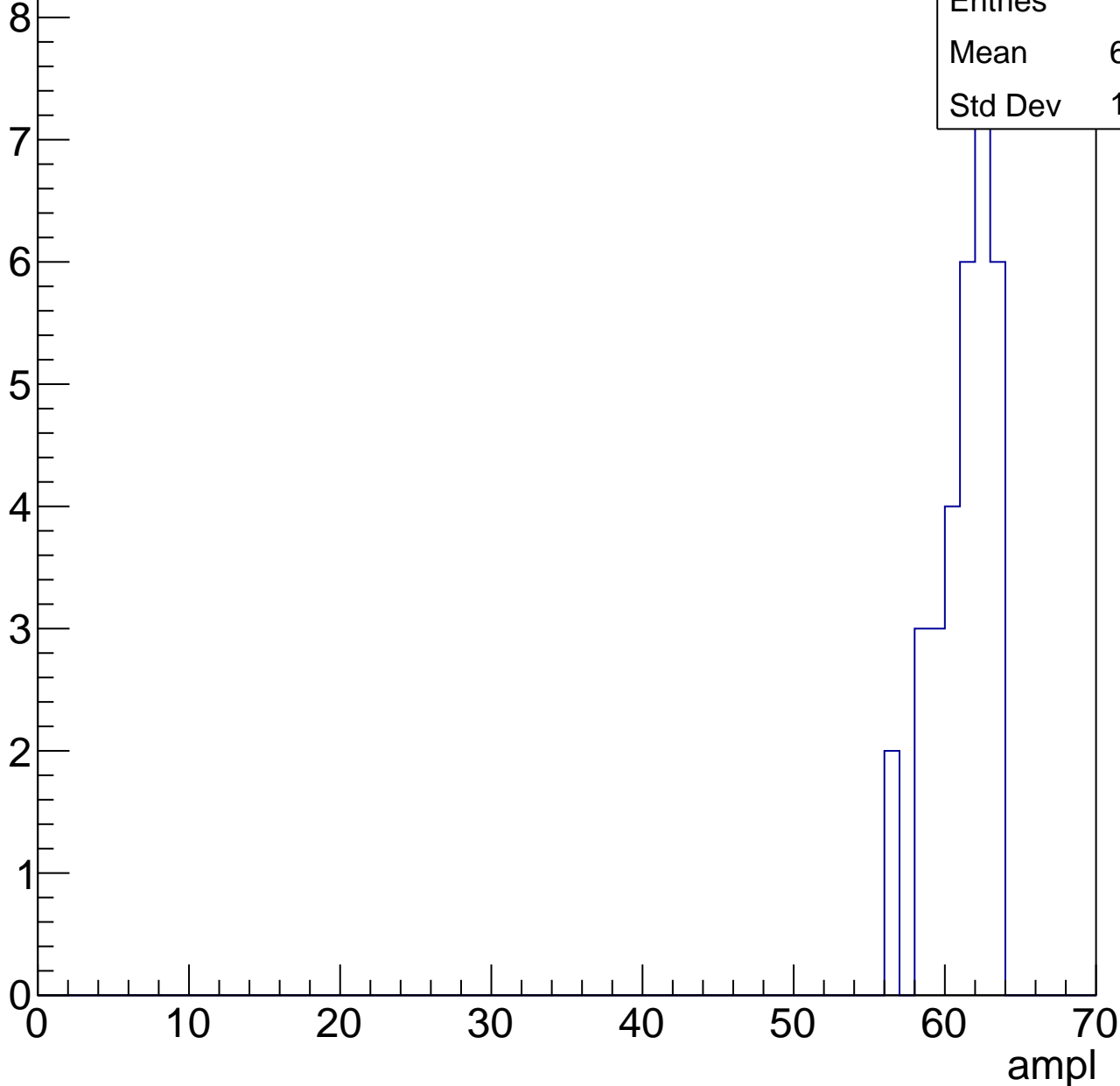


B0L002S, U9-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

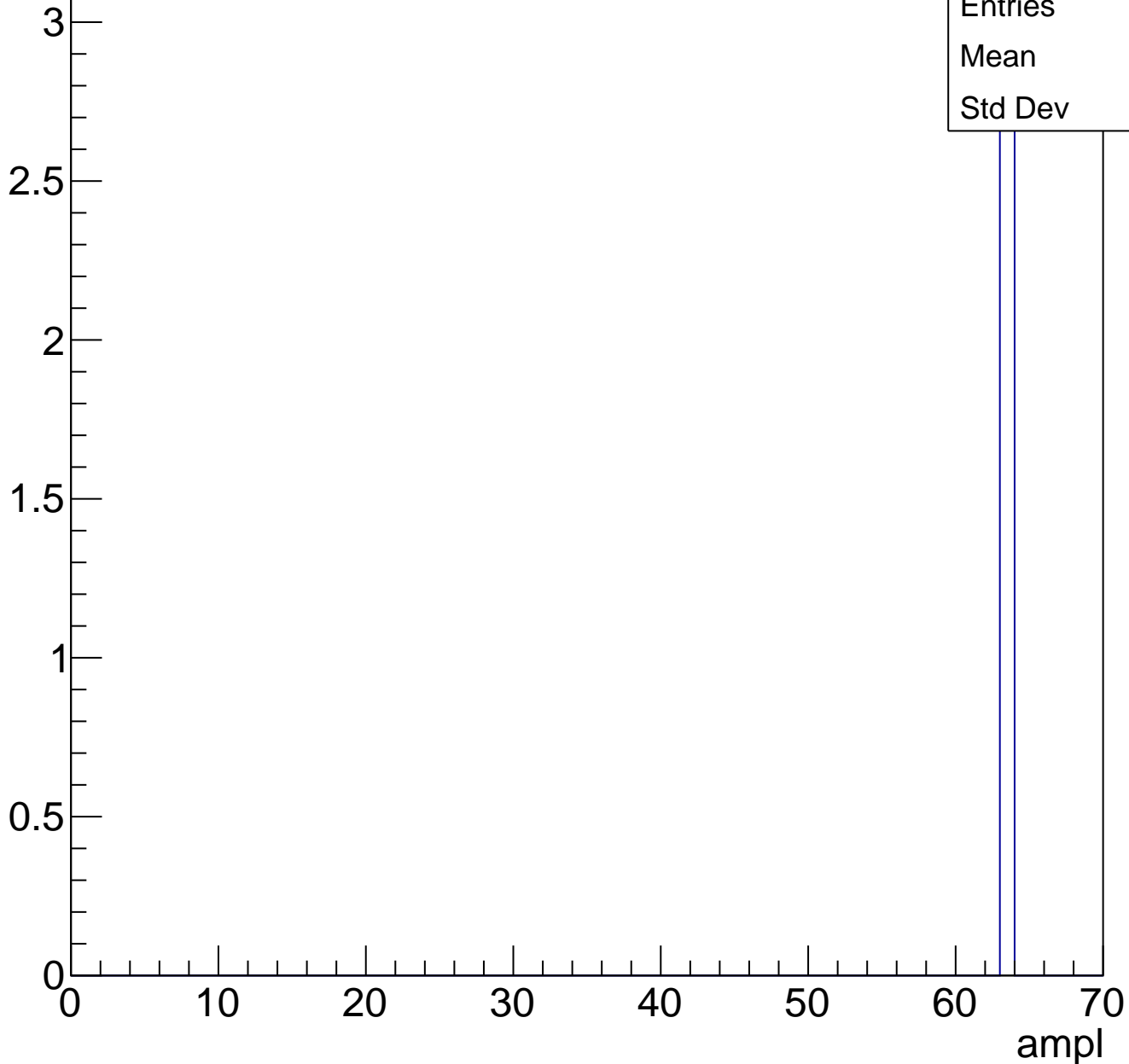
Entries	32
Mean	60.72
Std Dev	1.956



B0L002S, U9-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U9-ch70, adc0

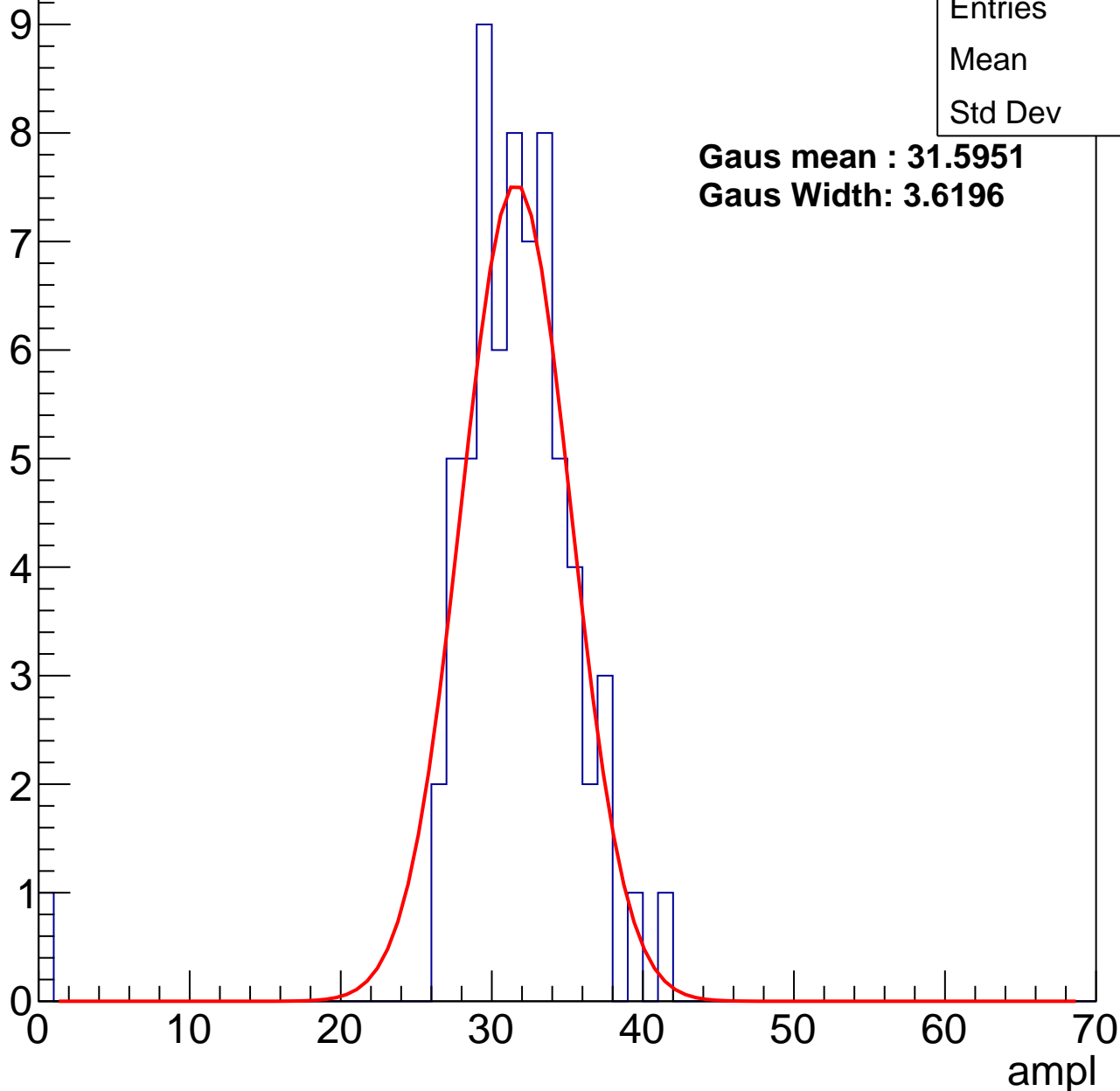
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	31
Std Dev	4.96

Gaus mean : 31.5951

Gaus Width: 3.6196



B0L002S, U9-ch70, adc1

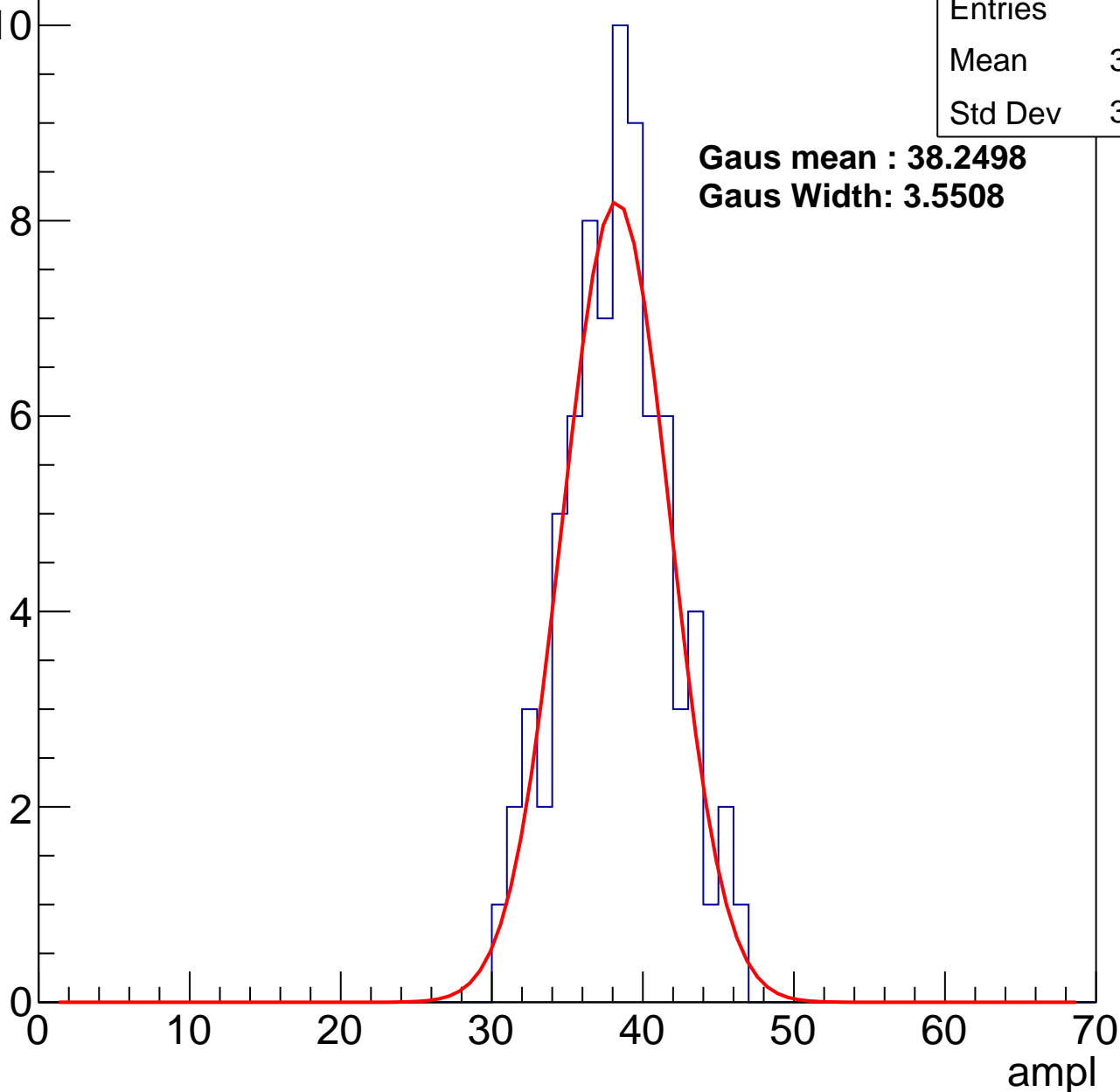
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.84
Std Dev	3.487

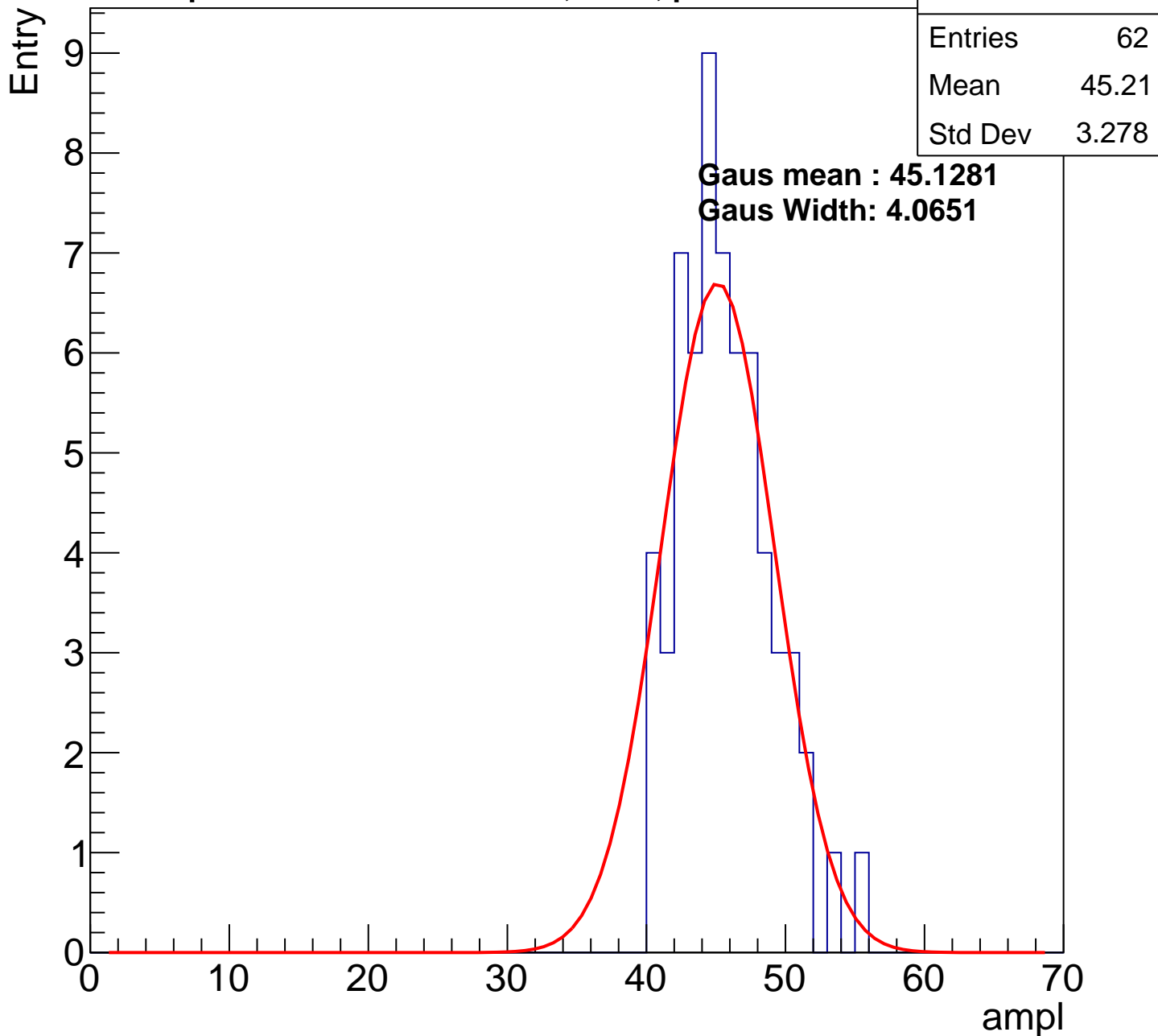
Gaus mean : 38.2498

Gaus Width: 3.5508



B0L002S, U9-ch70, adc2

calib_packv5_042523_0143.root, FC#8, port C1

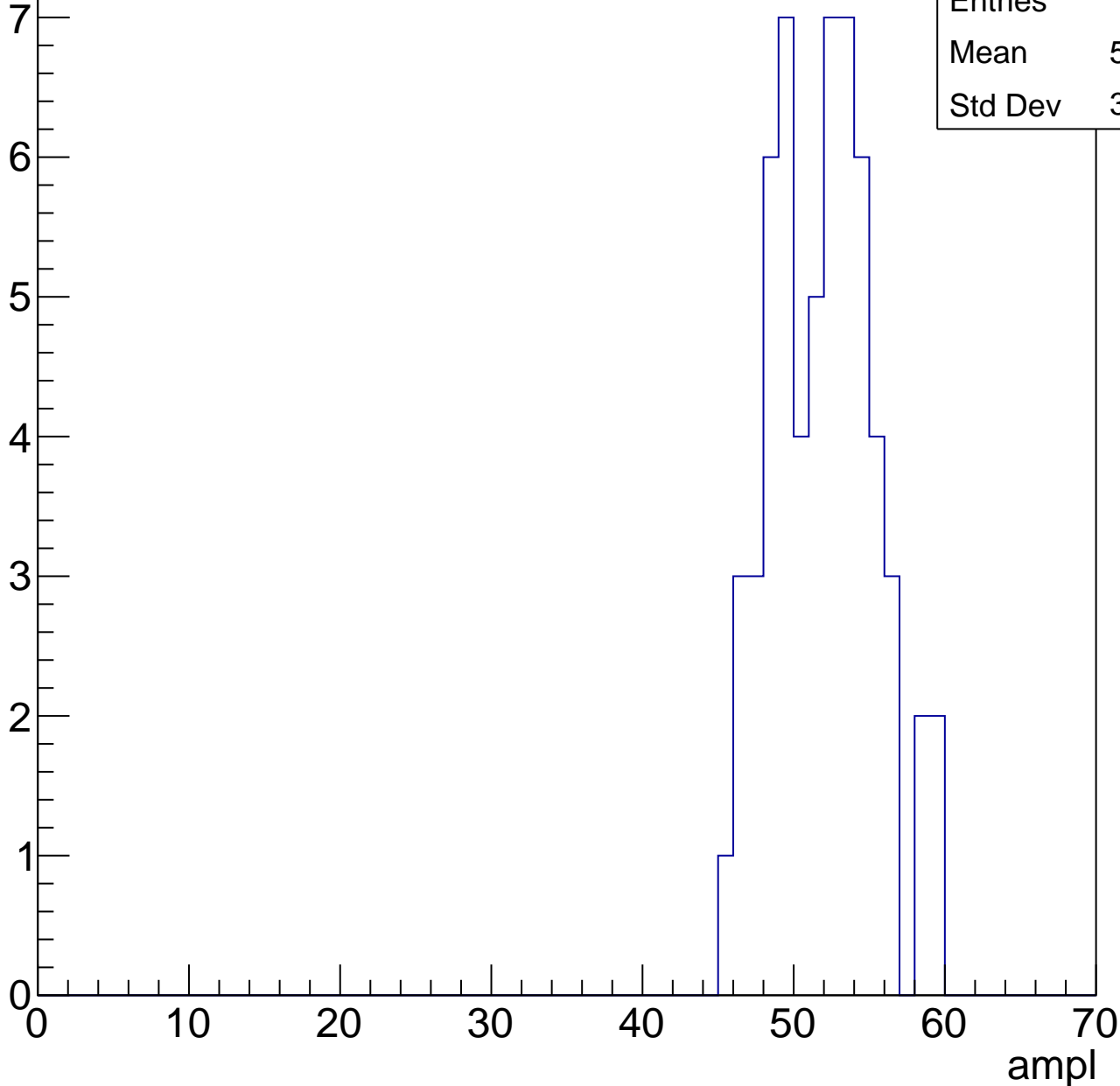


B0L002S, U9-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	51.52
Std Dev	3.374

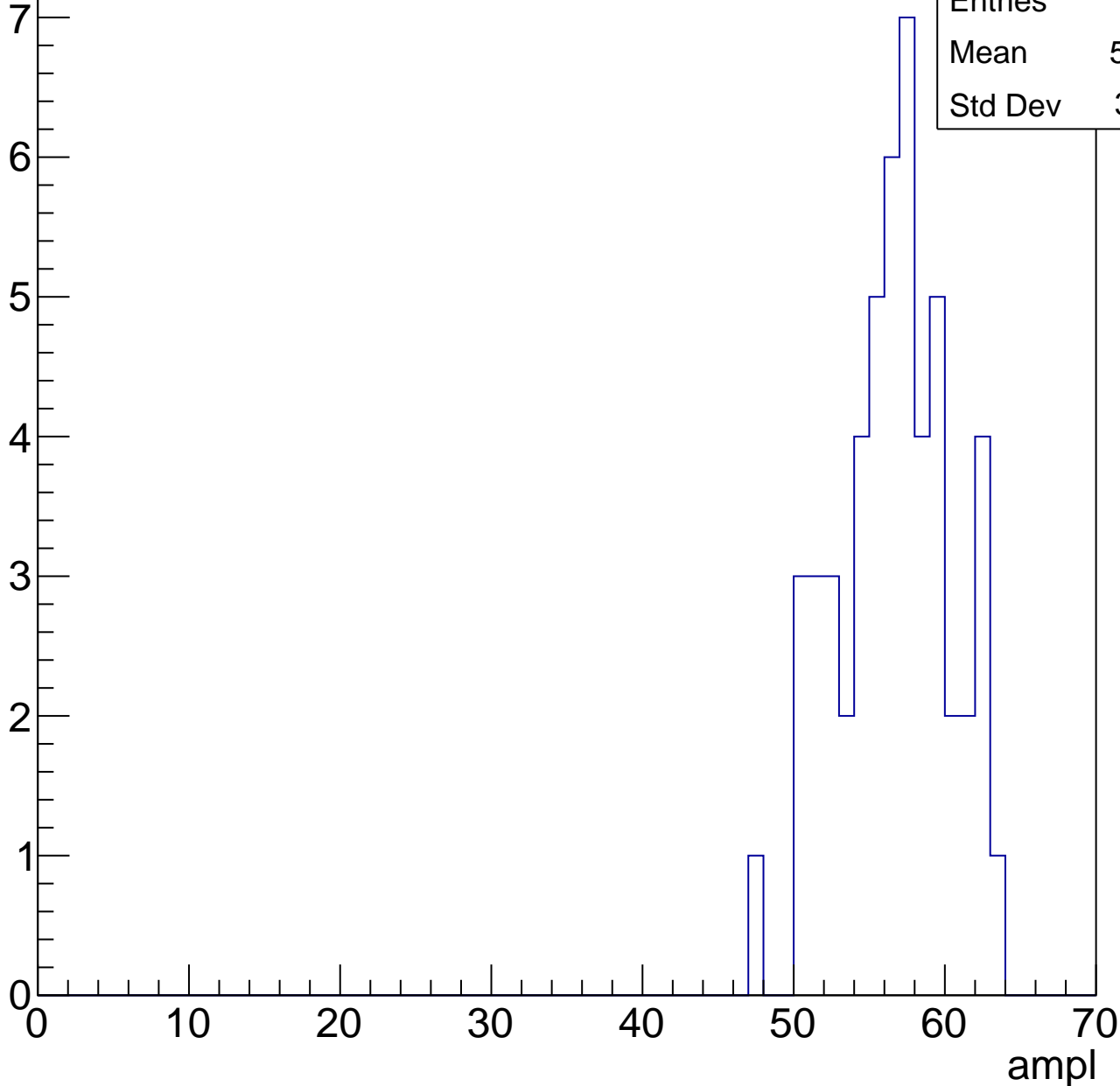


B0L002S, U9-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.12
Std Dev	3.651



B0L002S, U9-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	58.79
Std Dev	9.728

ampl

0

10

20

30

40

50

60

70

B0L002S, U9-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch71, adc0

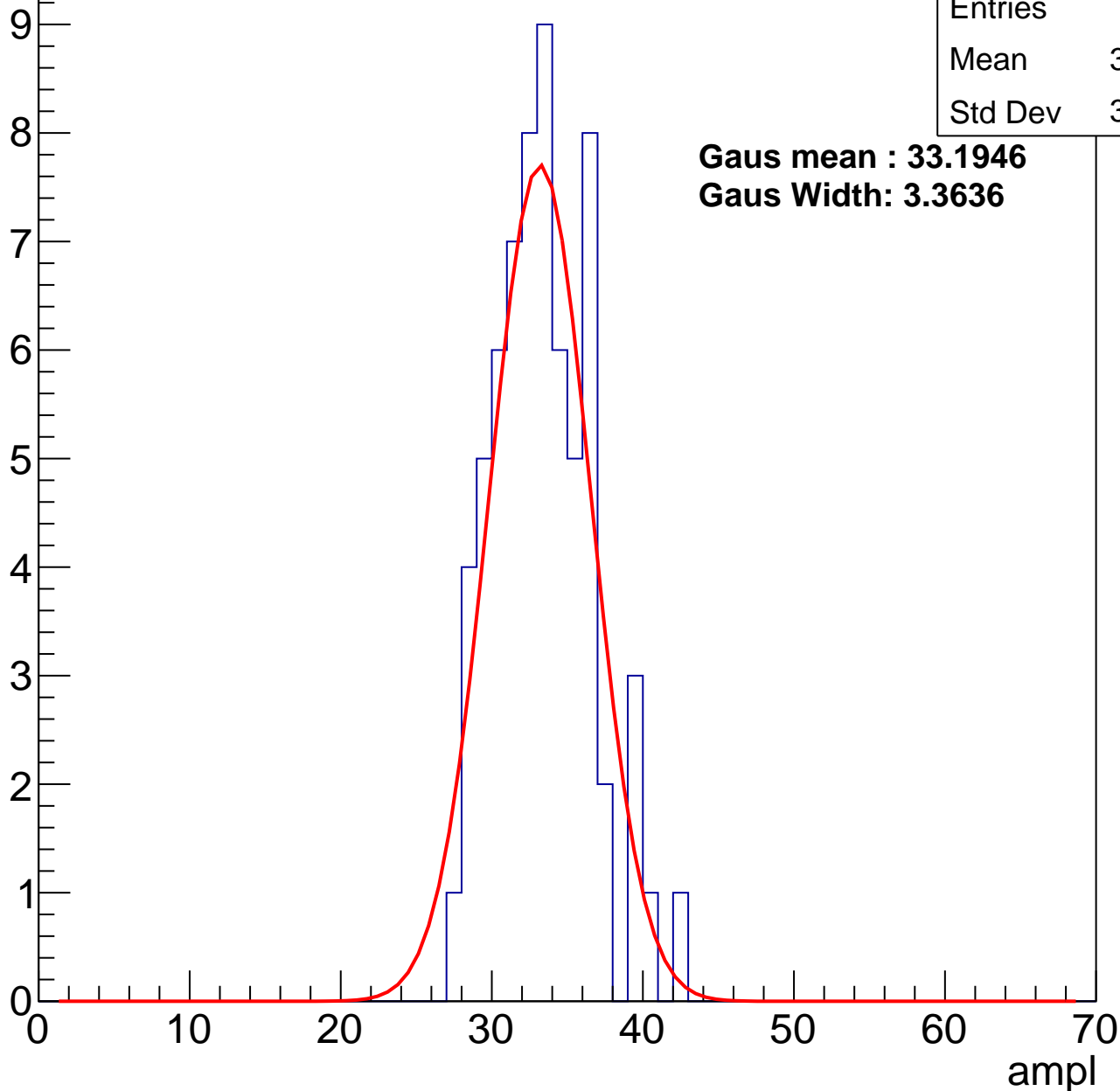
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	32.94
Std Dev	3.195

Gaus mean : 33.1946

Gaus Width: 3.3636



B0L002S, U9-ch71, adc1

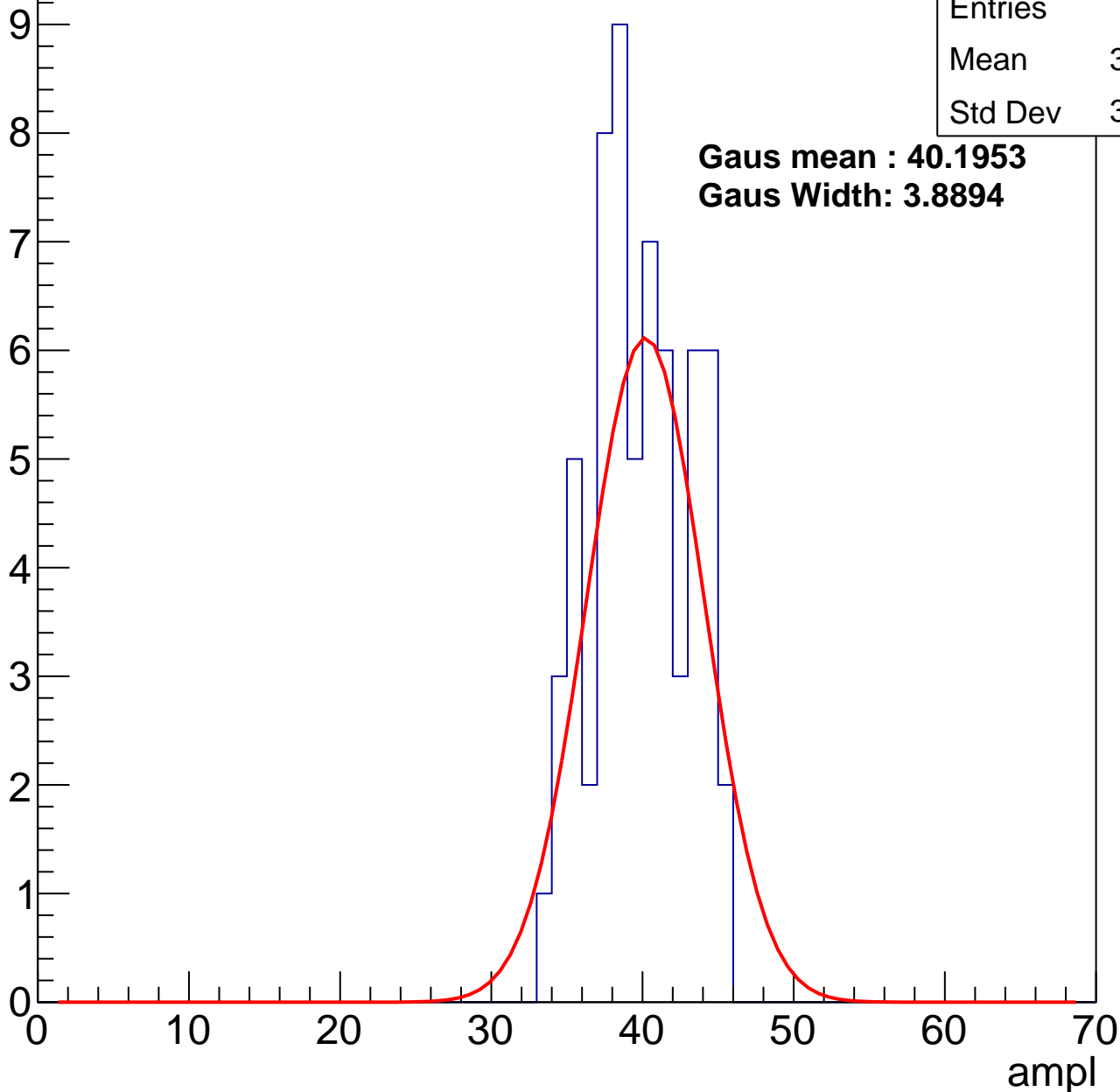
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	39.35
Std Dev	3.133

Gaus mean : 40.1953

Gaus Width: 3.8894



B0L002S, U9-ch71, adc2

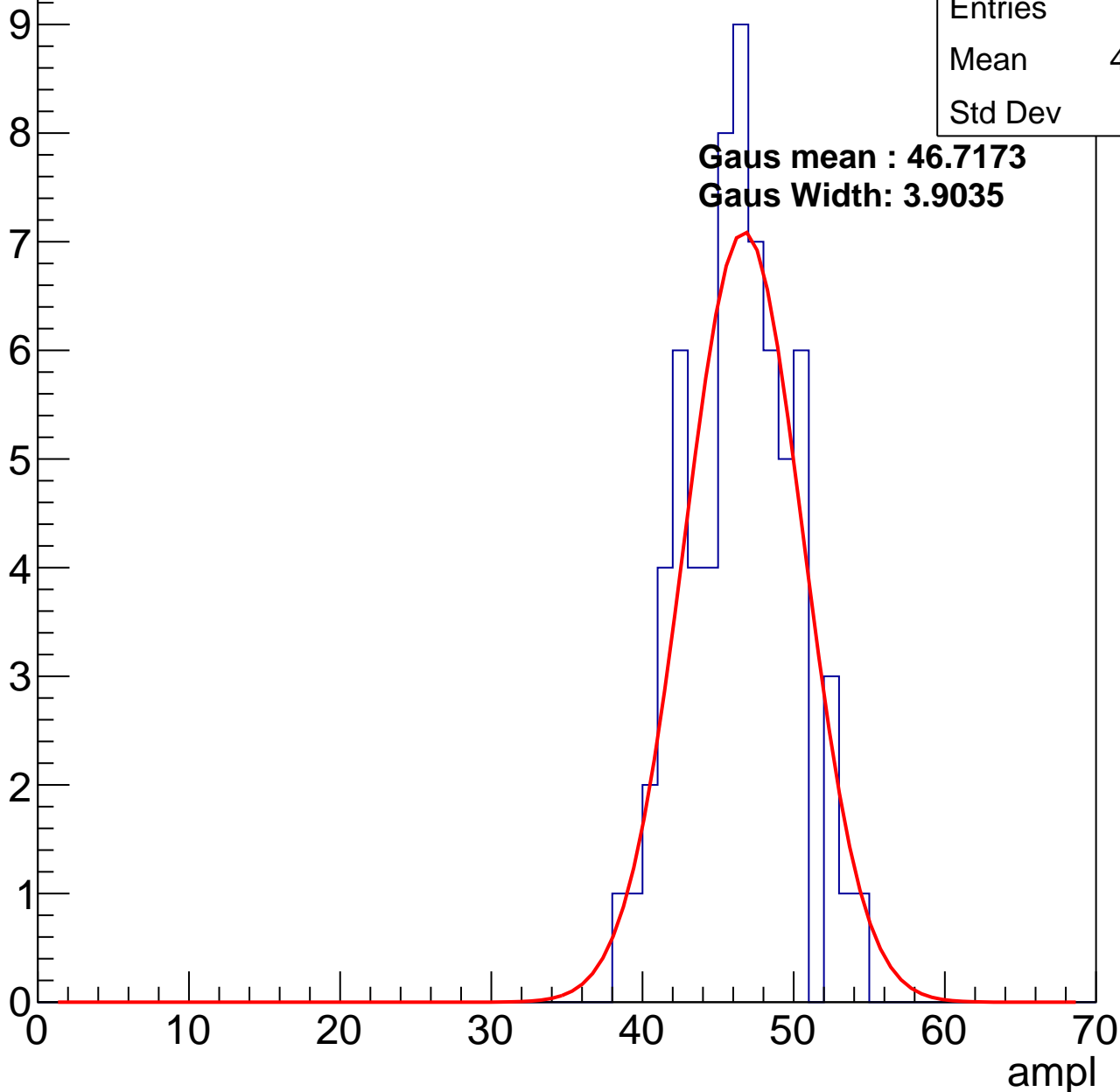
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.88
Std Dev	3.5

Gaus mean : 46.7173

Gaus Width: 3.9035

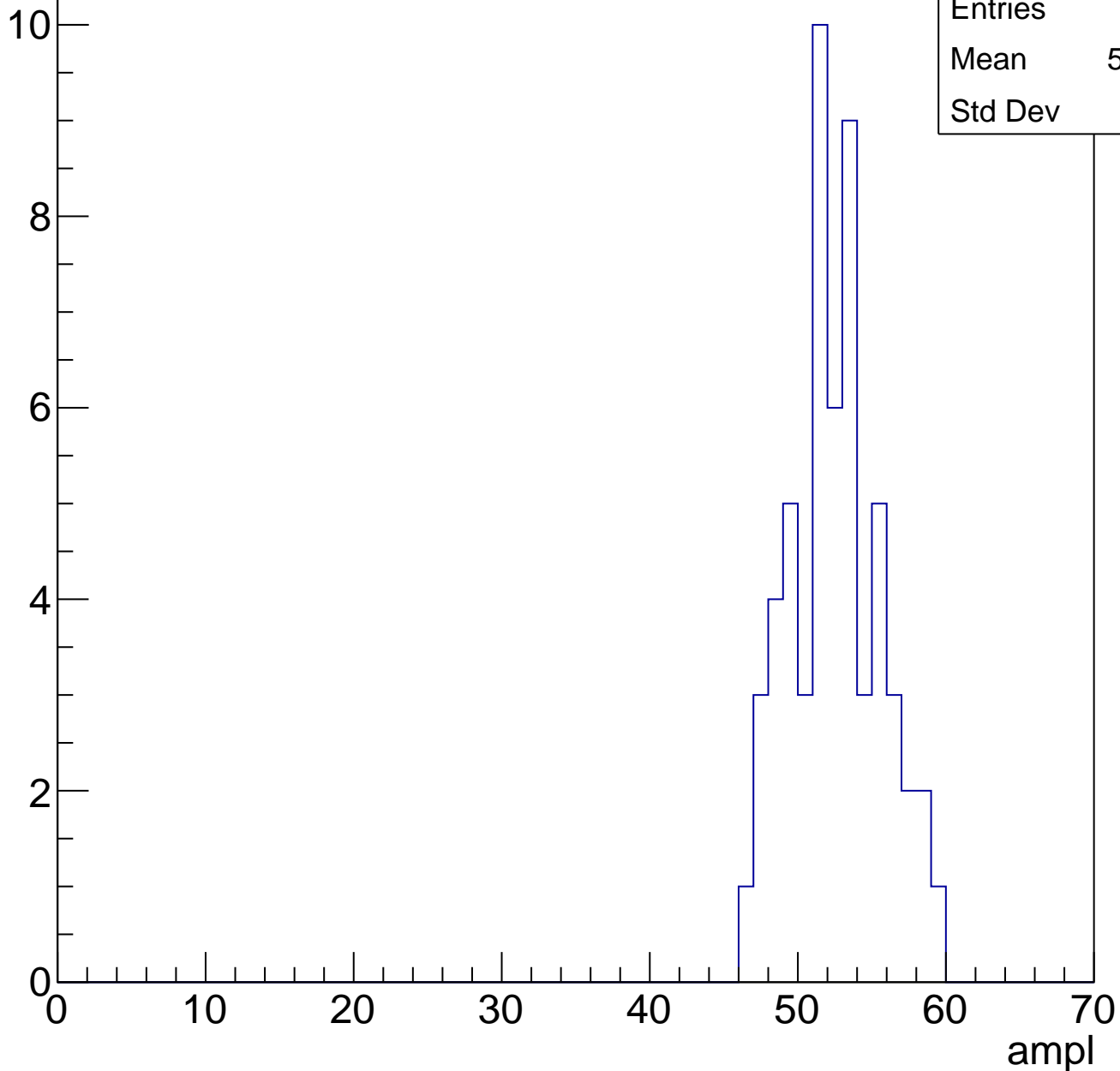


B0L002S, U9-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	57
Mean	52.05
Std Dev	3.04

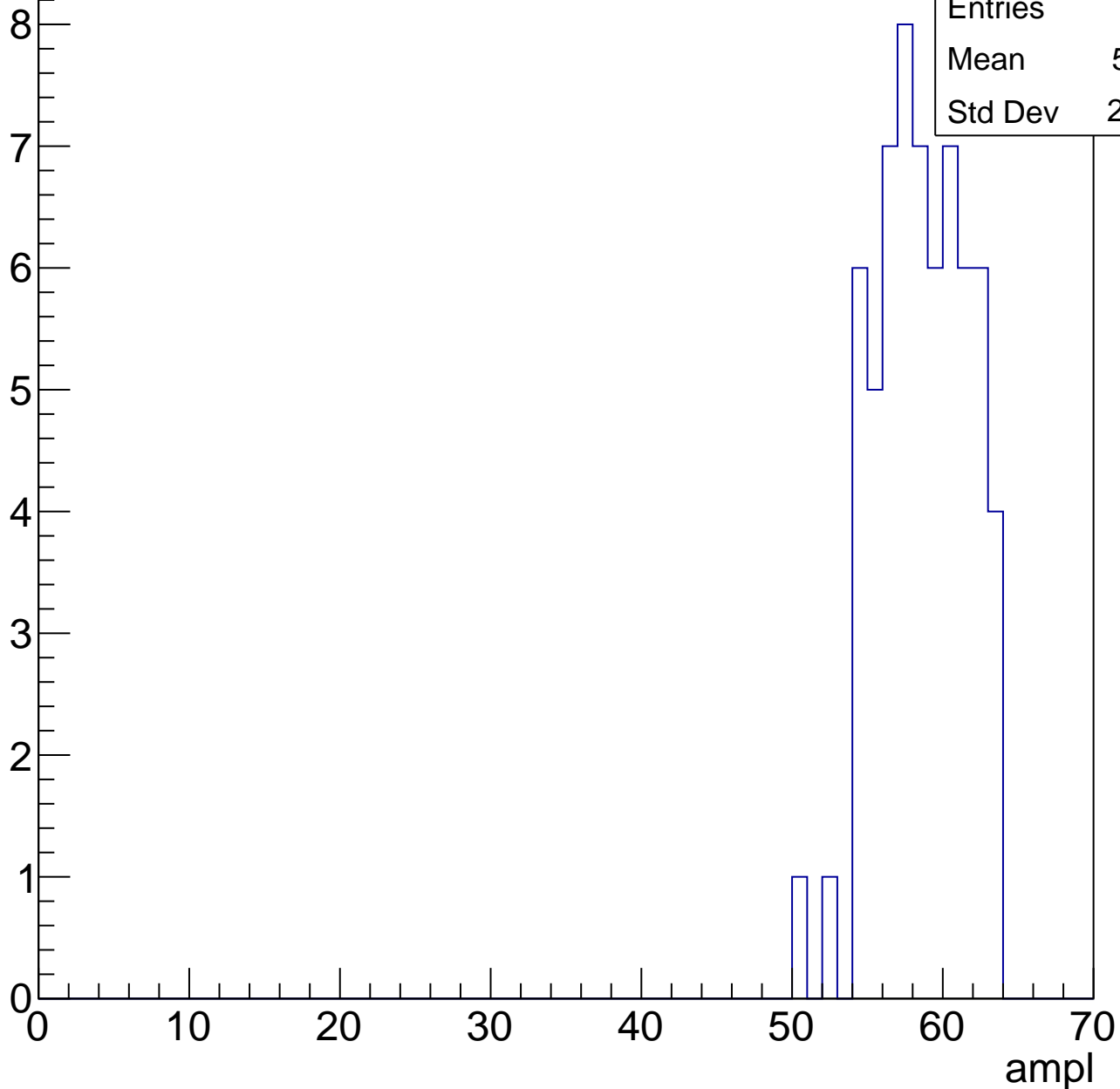
Entry



B0L002S, U9-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

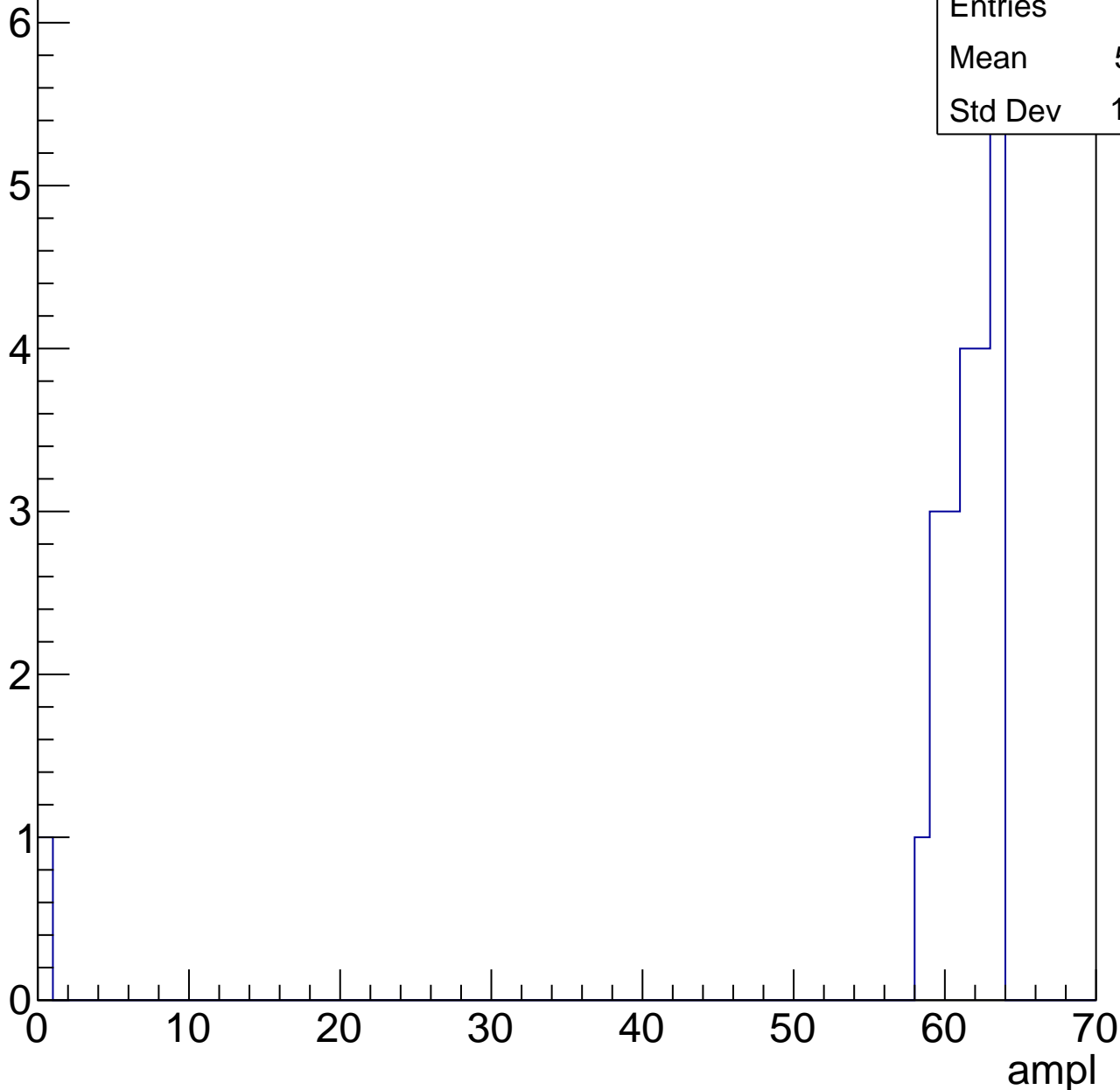


B0L002S, U9-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.41
Std Dev	12.84



B0L002S, U9-ch71, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch72, adc0

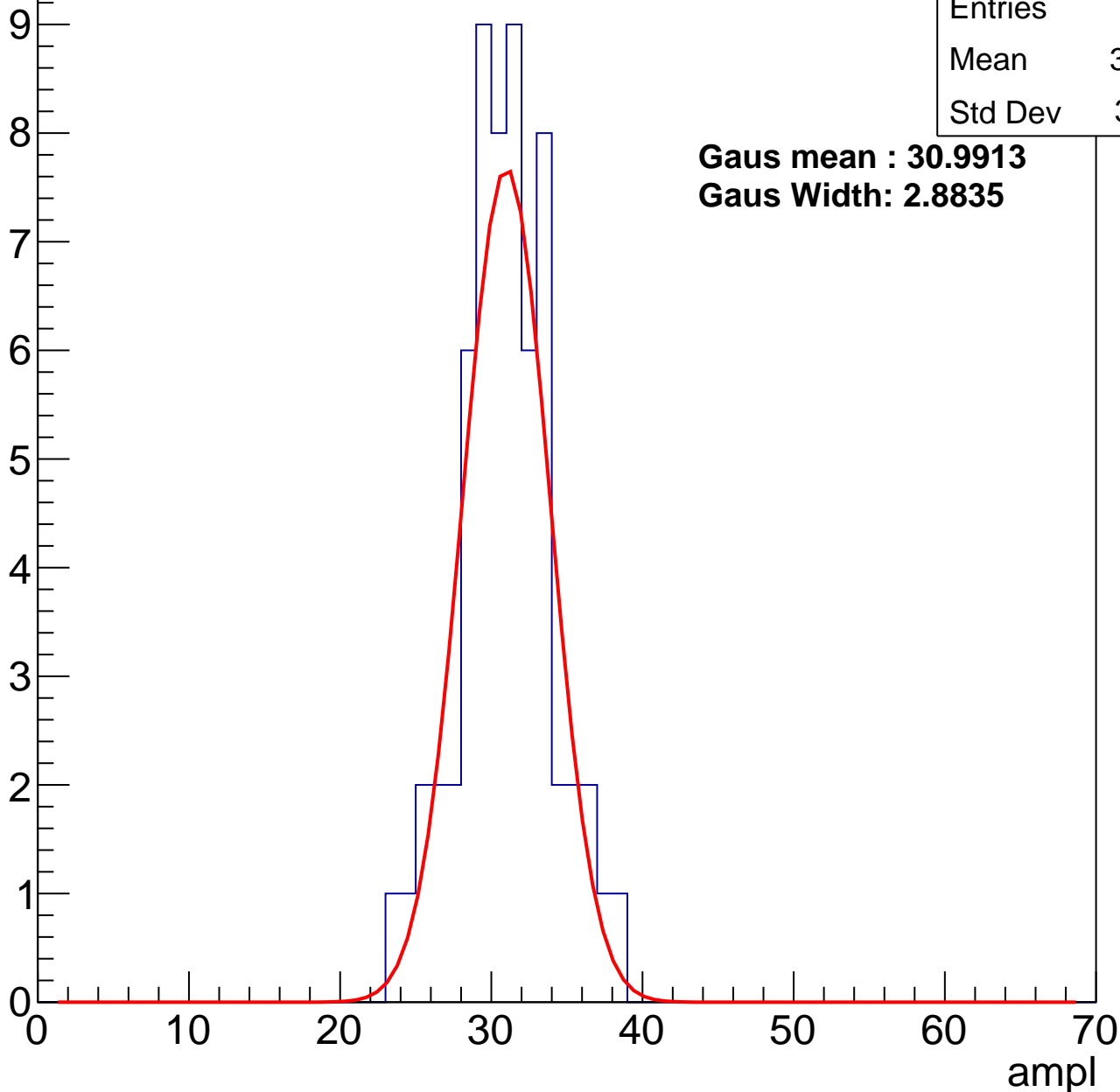
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.52
Std Dev	3.041

Gaus mean : 30.9913

Gaus Width: 2.8835



B0L002S, U9-ch72, adc1

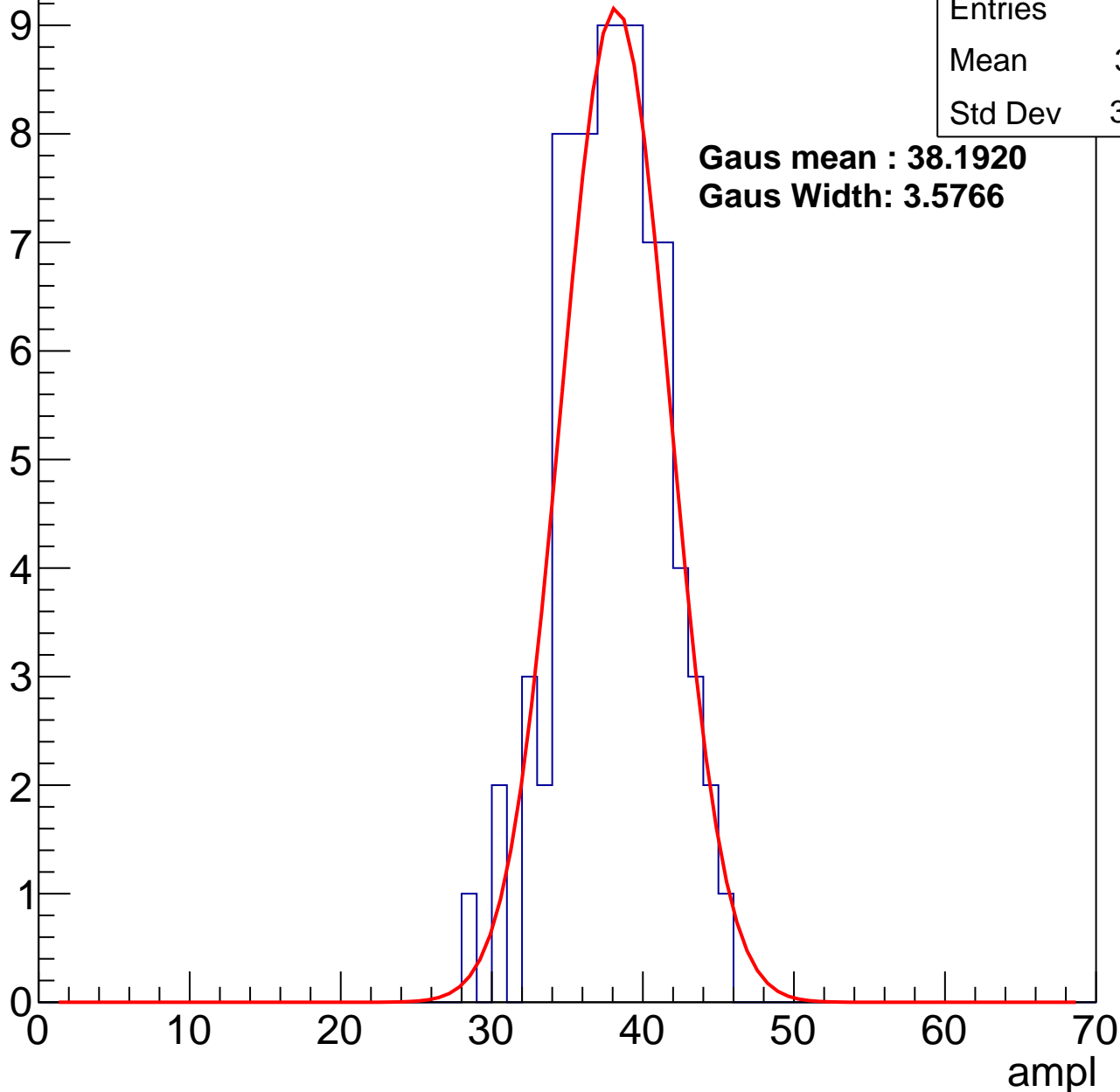
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	37.51
Std Dev	3.413

Gaus mean : 38.1920

Gaus Width: 3.5766



B0L002S, U9-ch72, adc2

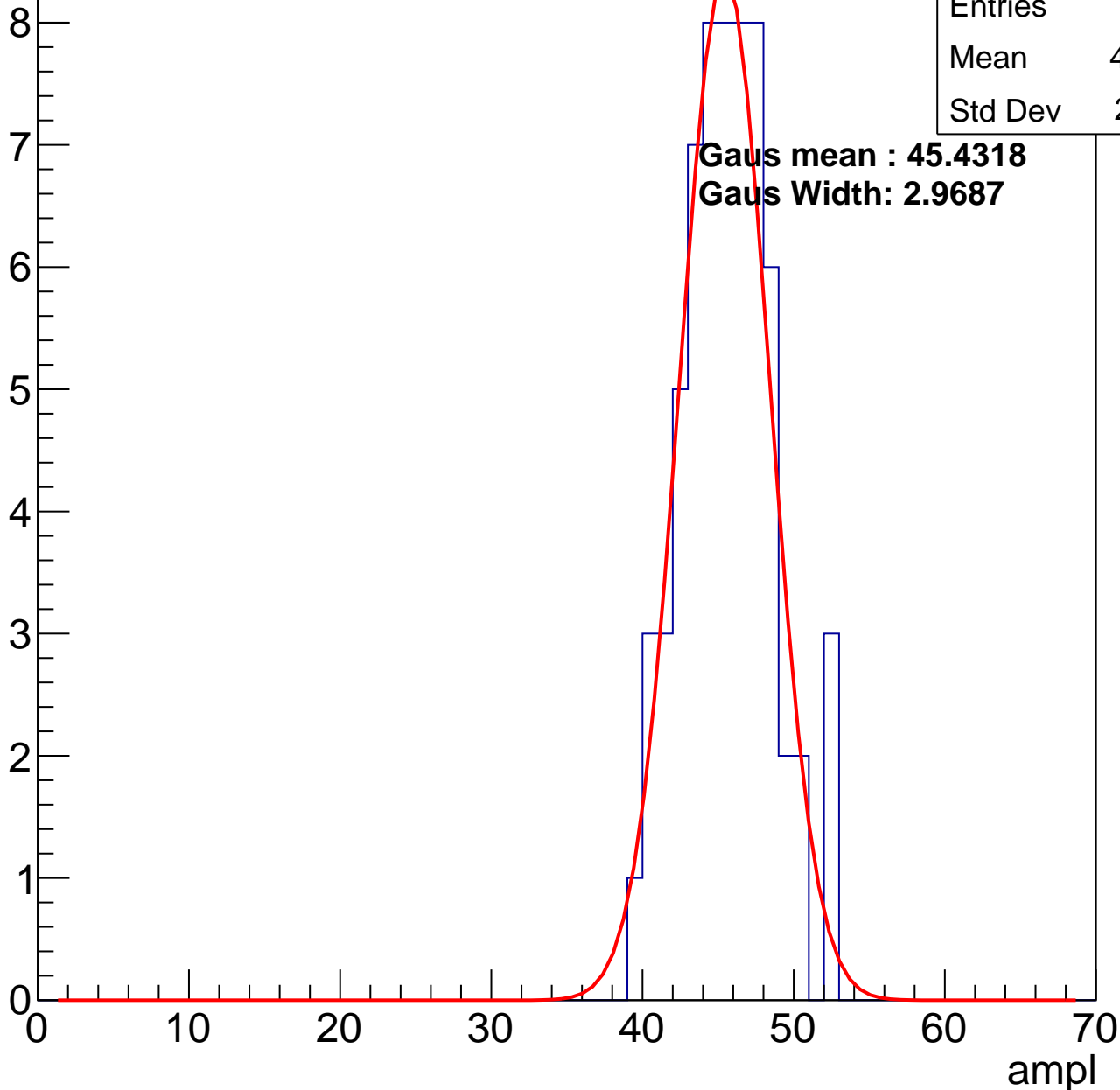
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	45.17
Std Dev	2.961

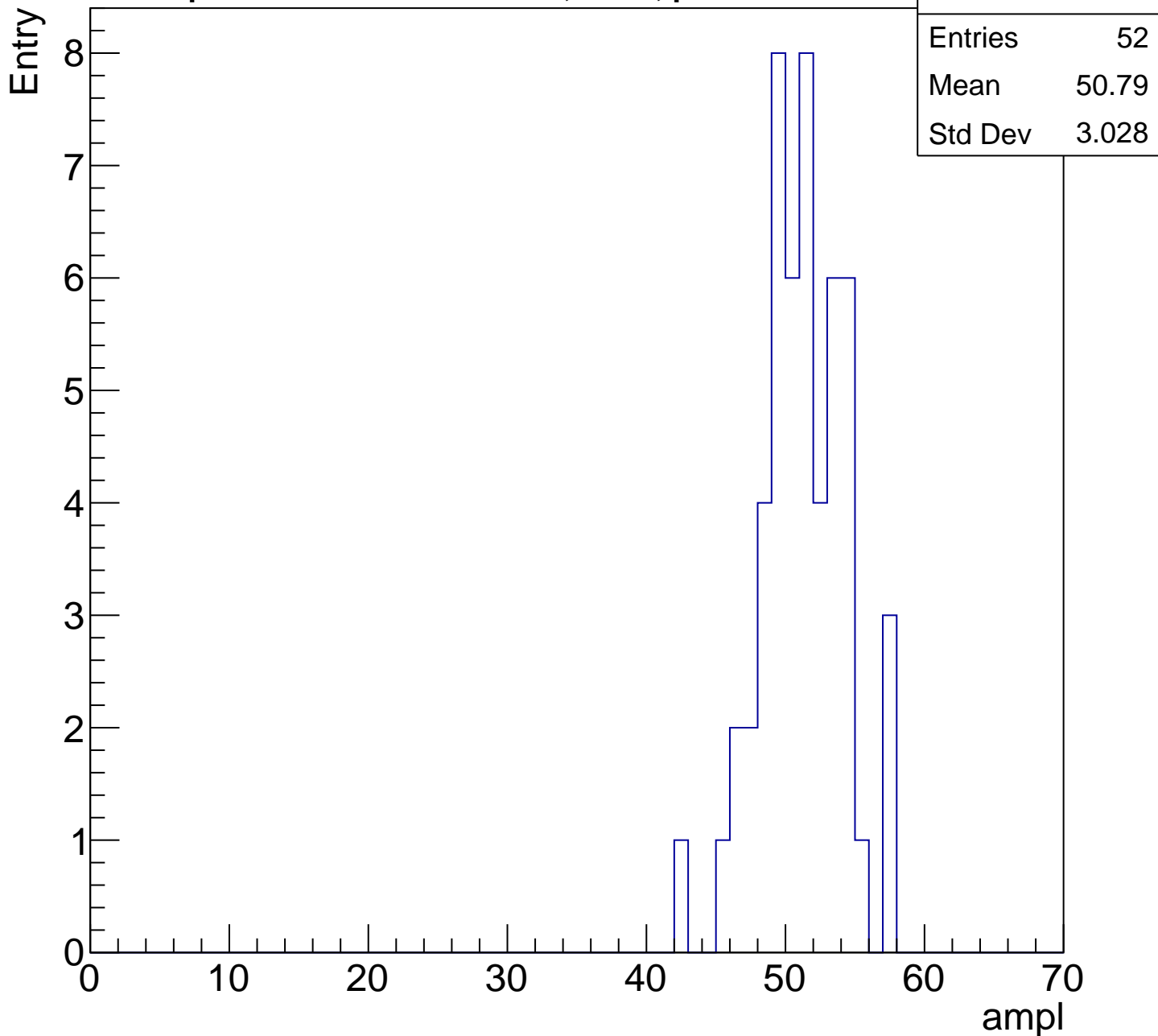
Gaus mean : 45.4318

Gaus Width: 2.9687



B0L002S, U9-ch72, adc3

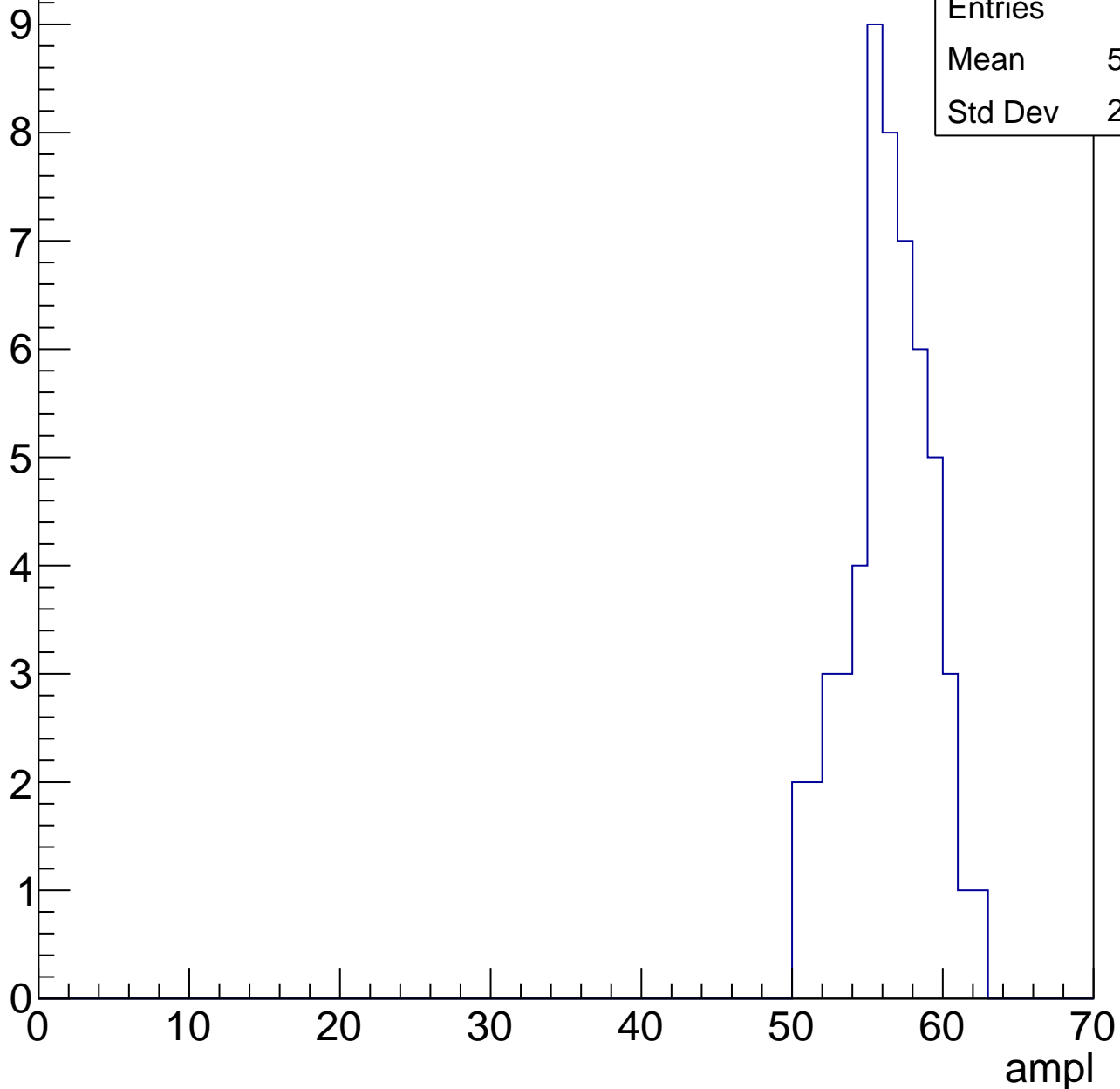
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



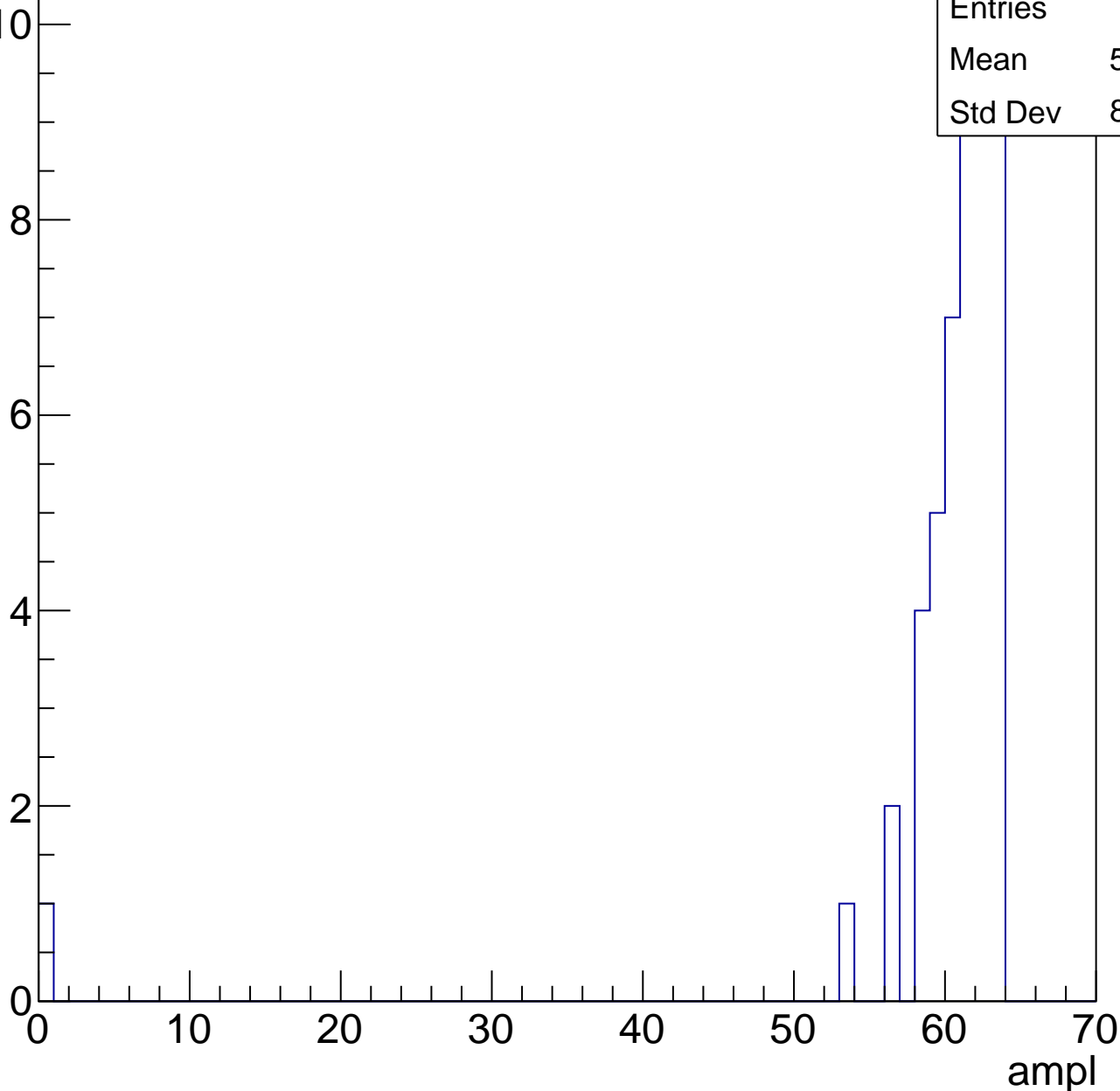
Entries	54
Mean	55.94
Std Dev	2.745

B0L002S, U9-ch72, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	59.35
Std Dev	8.917



B0L002S, U9-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch73, adc0

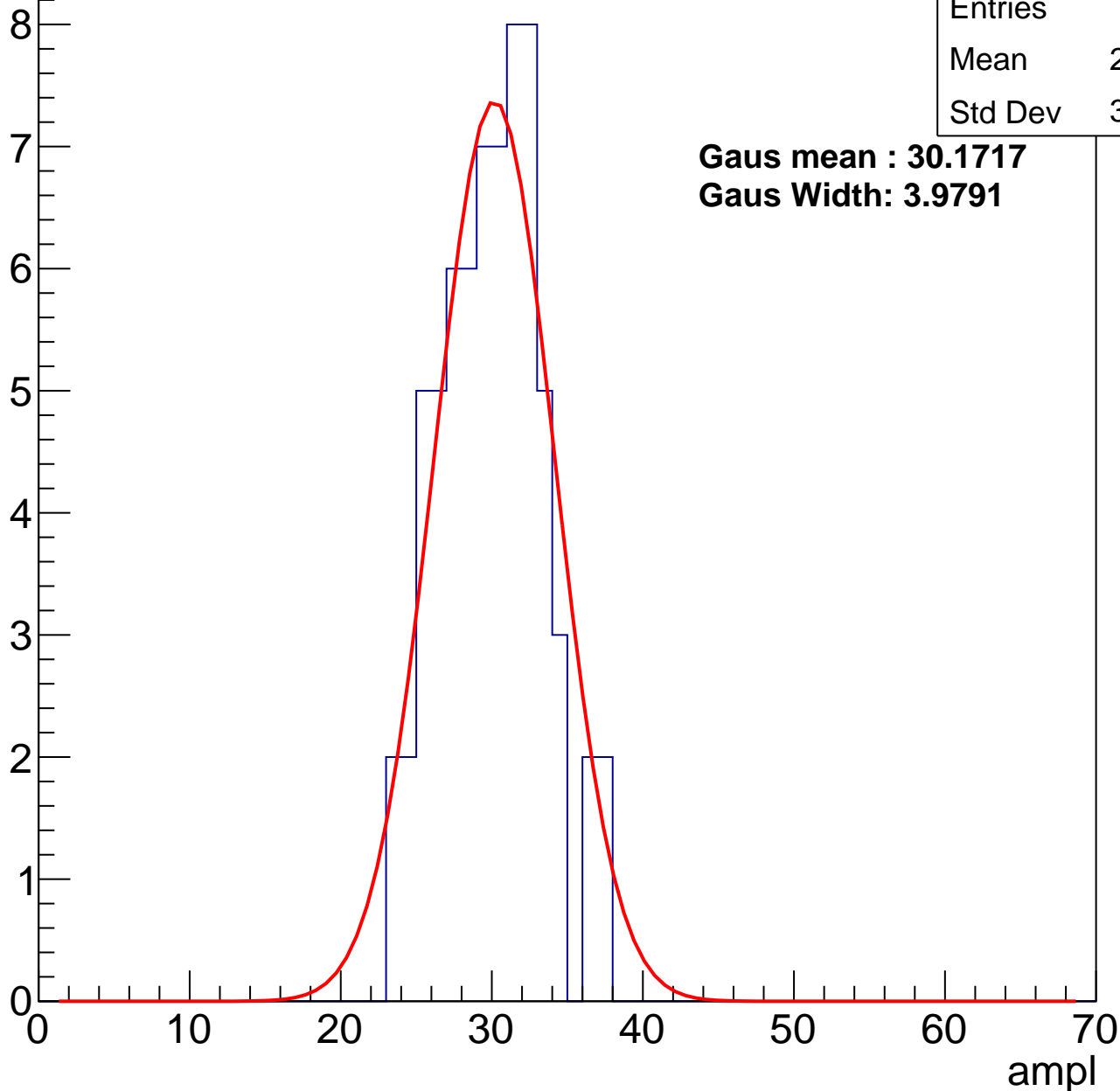
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.54
Std Dev	3.319

Gaus mean : 30.1717

Gaus Width: 3.9791



B0L002S, U9-ch73, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	37.35
Std Dev	3.345

Gaus mean : 37.4630

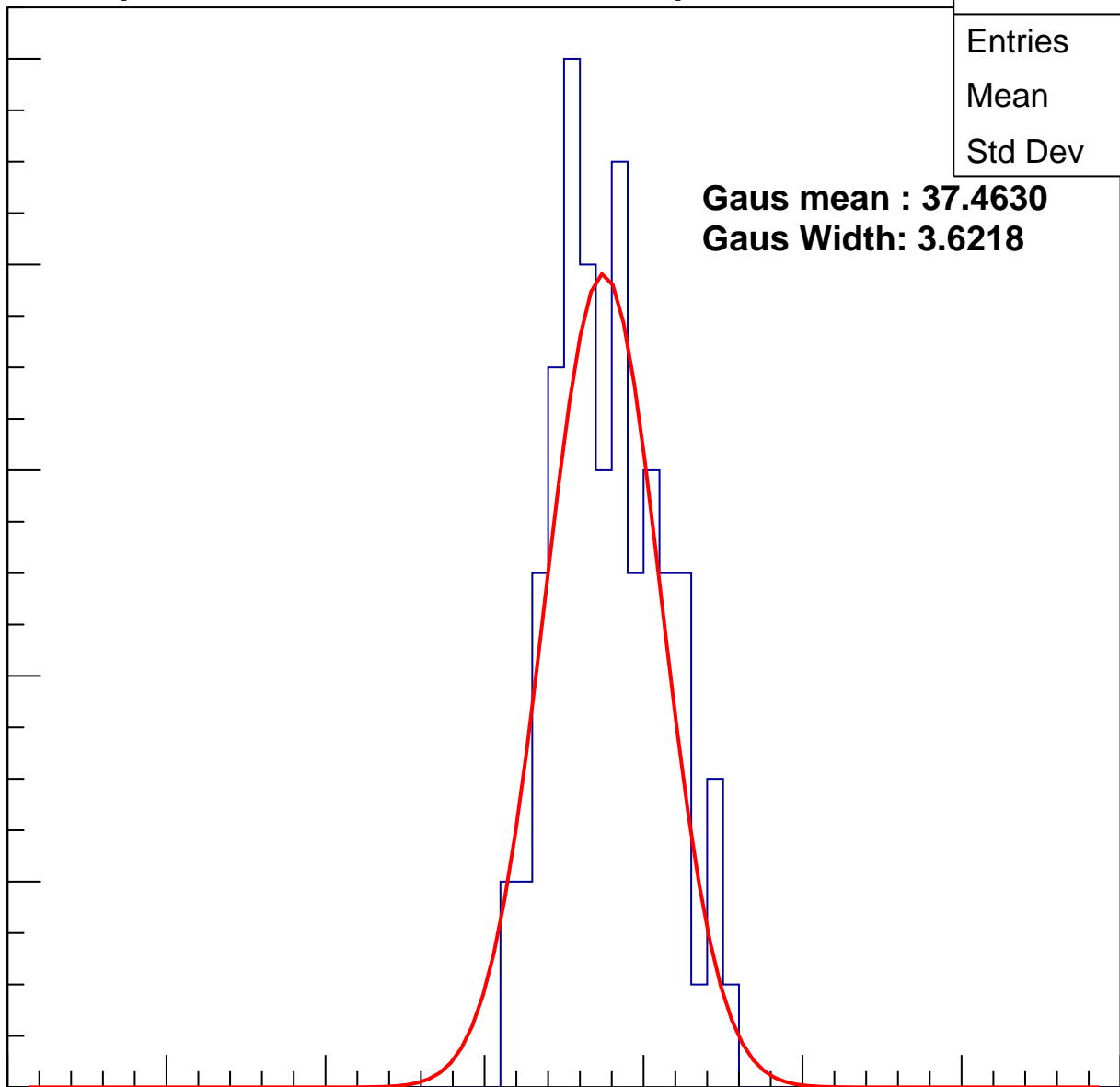
Gaus Width: 3.6218

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch73, adc2

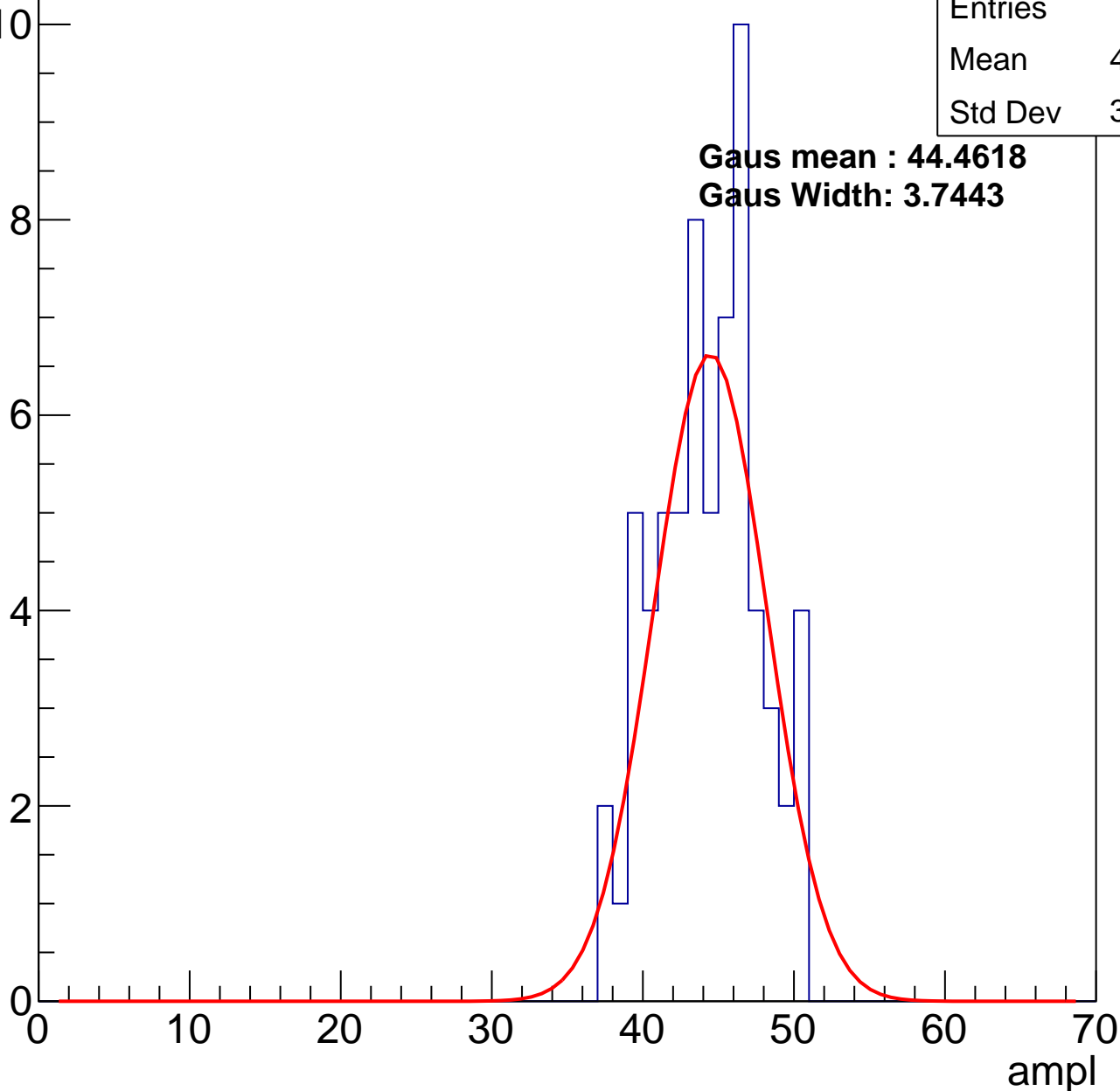
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.86
Std Dev	3.328

Gaus mean : 44.4618

Gaus Width: 3.7443

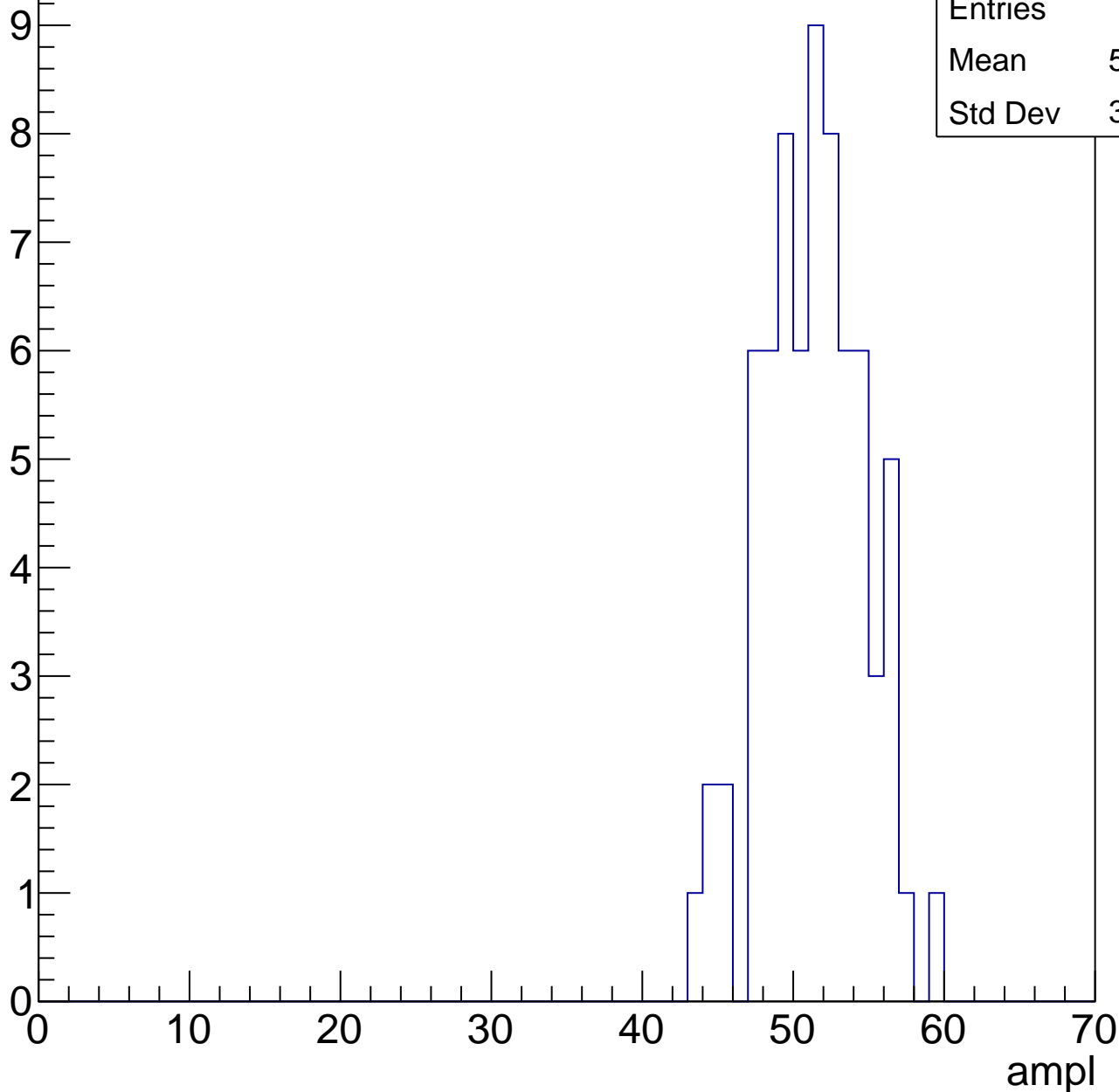


B0L002S, U9-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

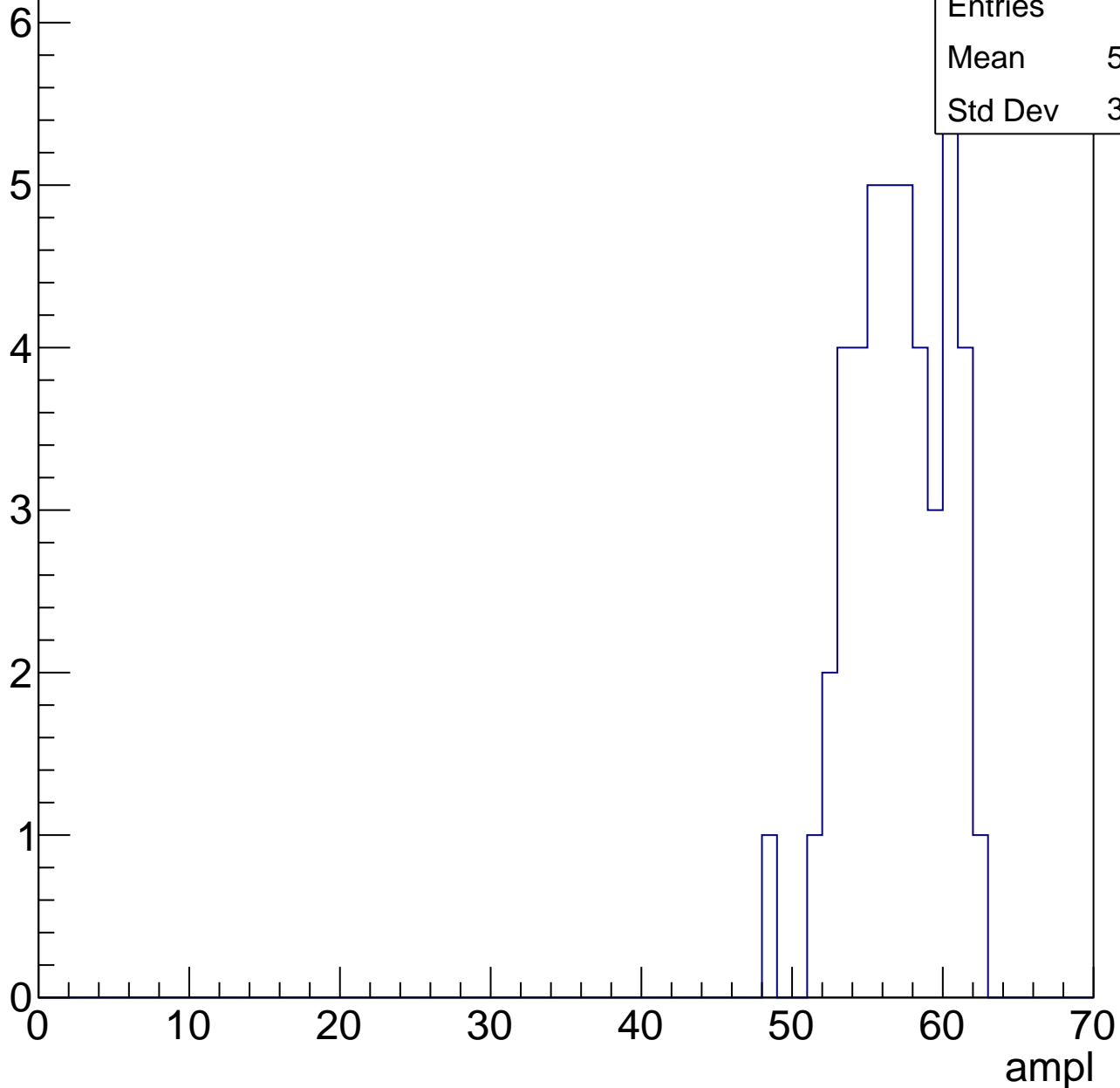
Entries	70
Mean	50.87
Std Dev	3.338



B0L002S, U9-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

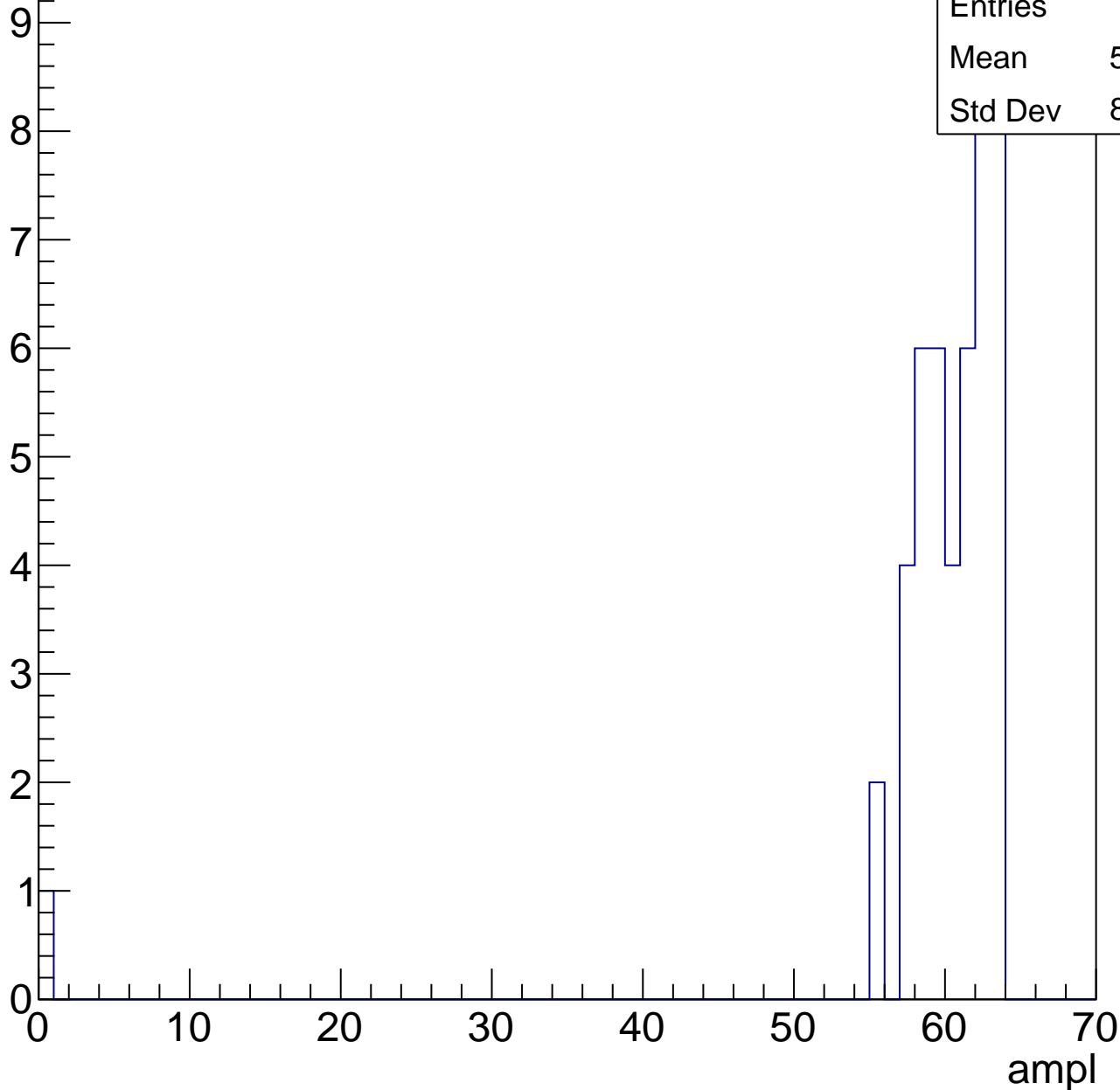


B0L002S, U9-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	58.96
Std Dev	8.977



B0L002S, U9-ch73, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch74, adc0

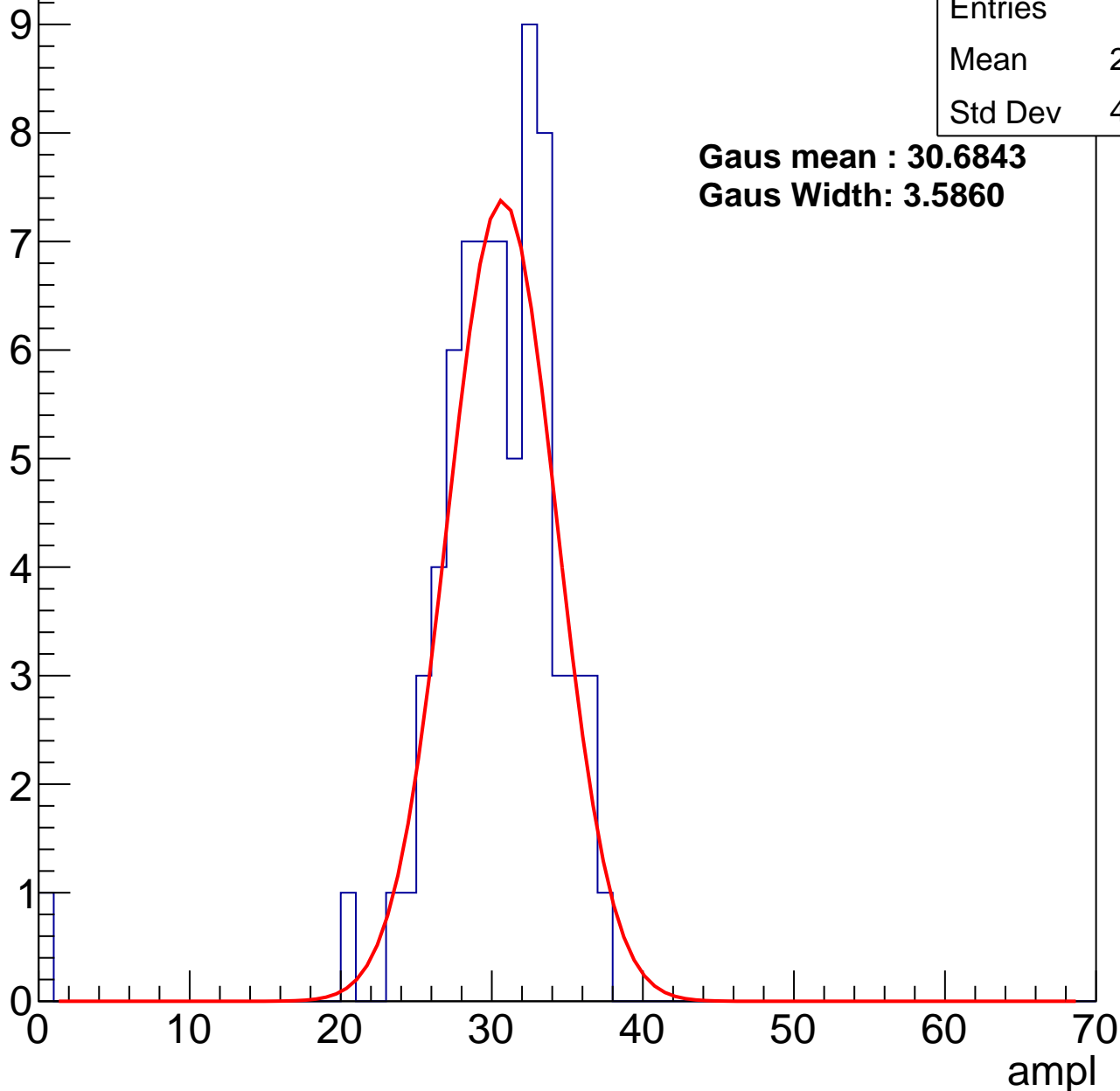
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.66
Std Dev	4.925

Gaus mean : 30.6843

Gaus Width: 3.5860



B0L002S, U9-ch74, adc1

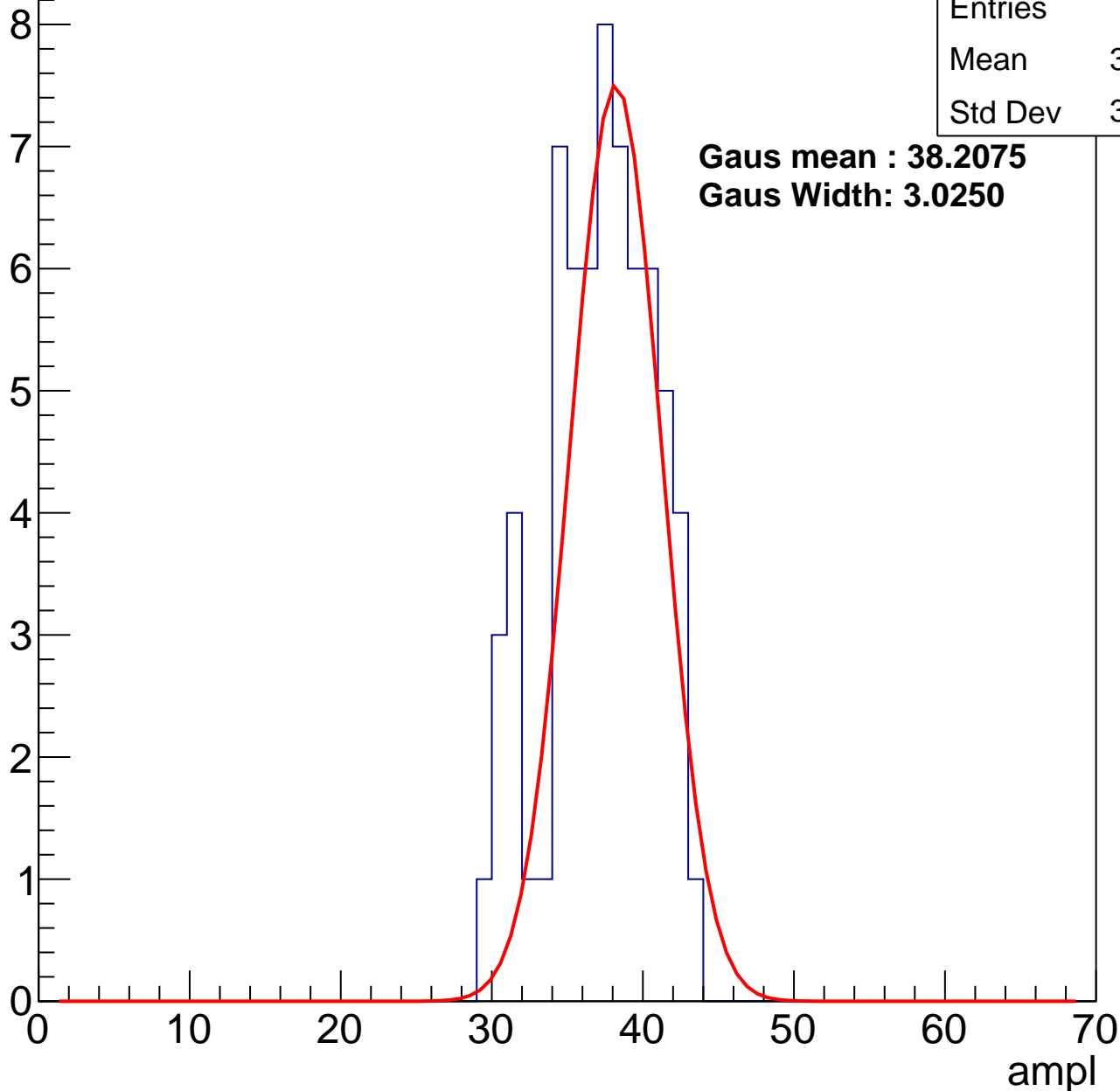
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.73
Std Dev	3.449

Gaus mean : 38.2075

Gaus Width: 3.0250



B0L002S, U9-ch74, adc2

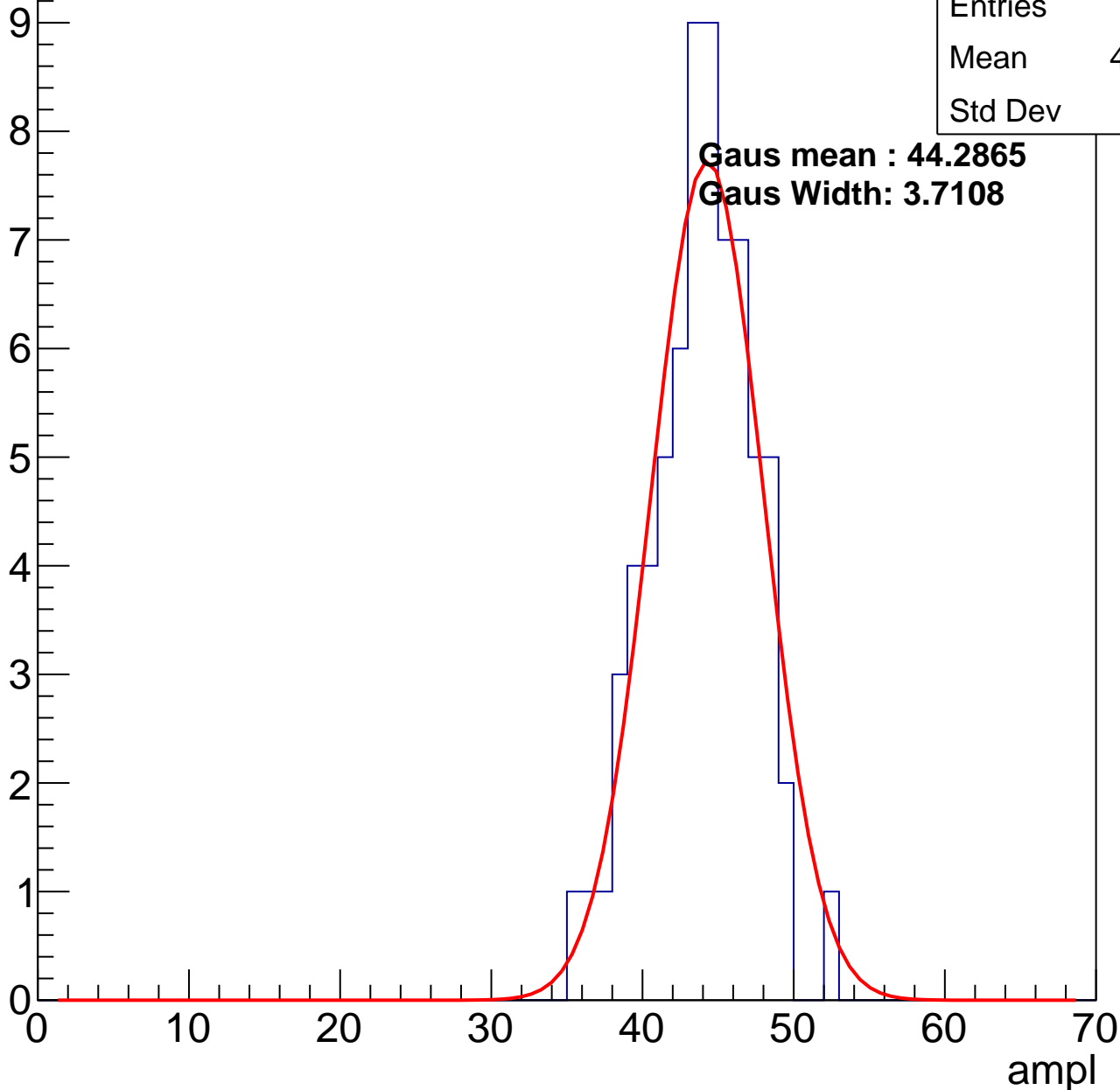
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.43
Std Dev	3.37

Gaus mean : 44.2865

Gaus Width: 3.7108

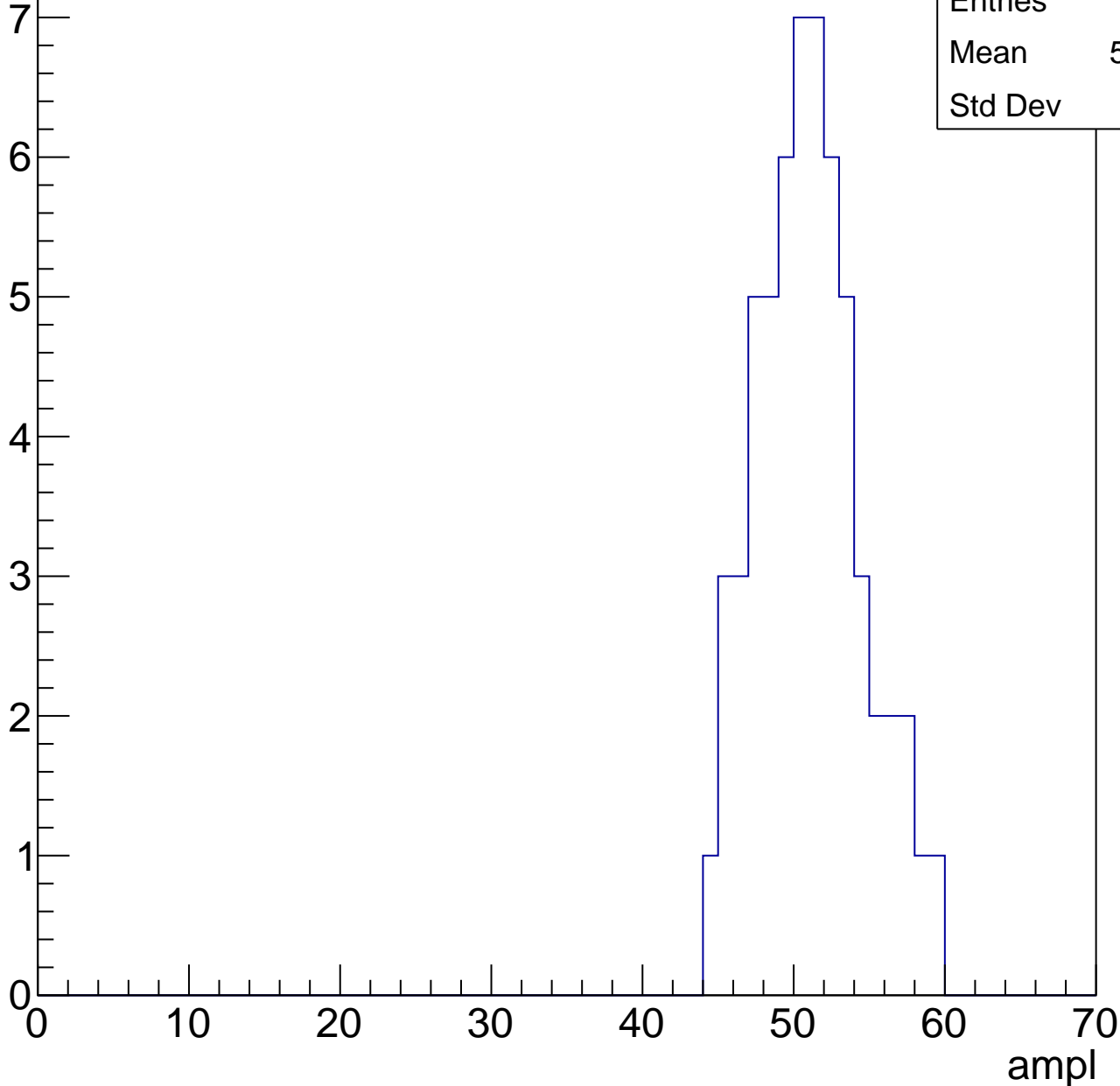


B0L002S, U9-ch74, adc3

calib_packv5_042523_0143.root, FC#8, port C1

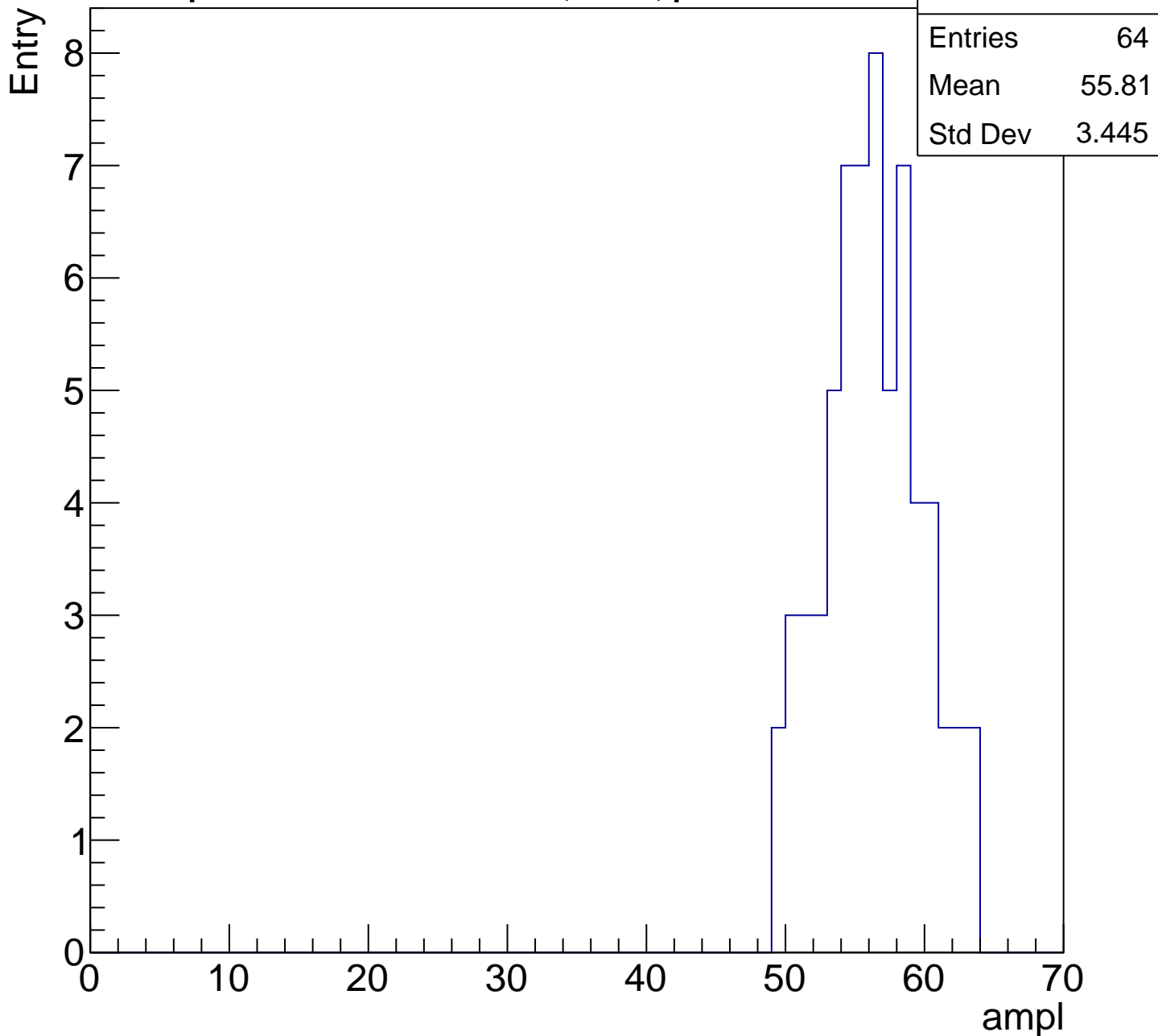
Entry

Entries	59
Mean	50.59
Std Dev	3.44



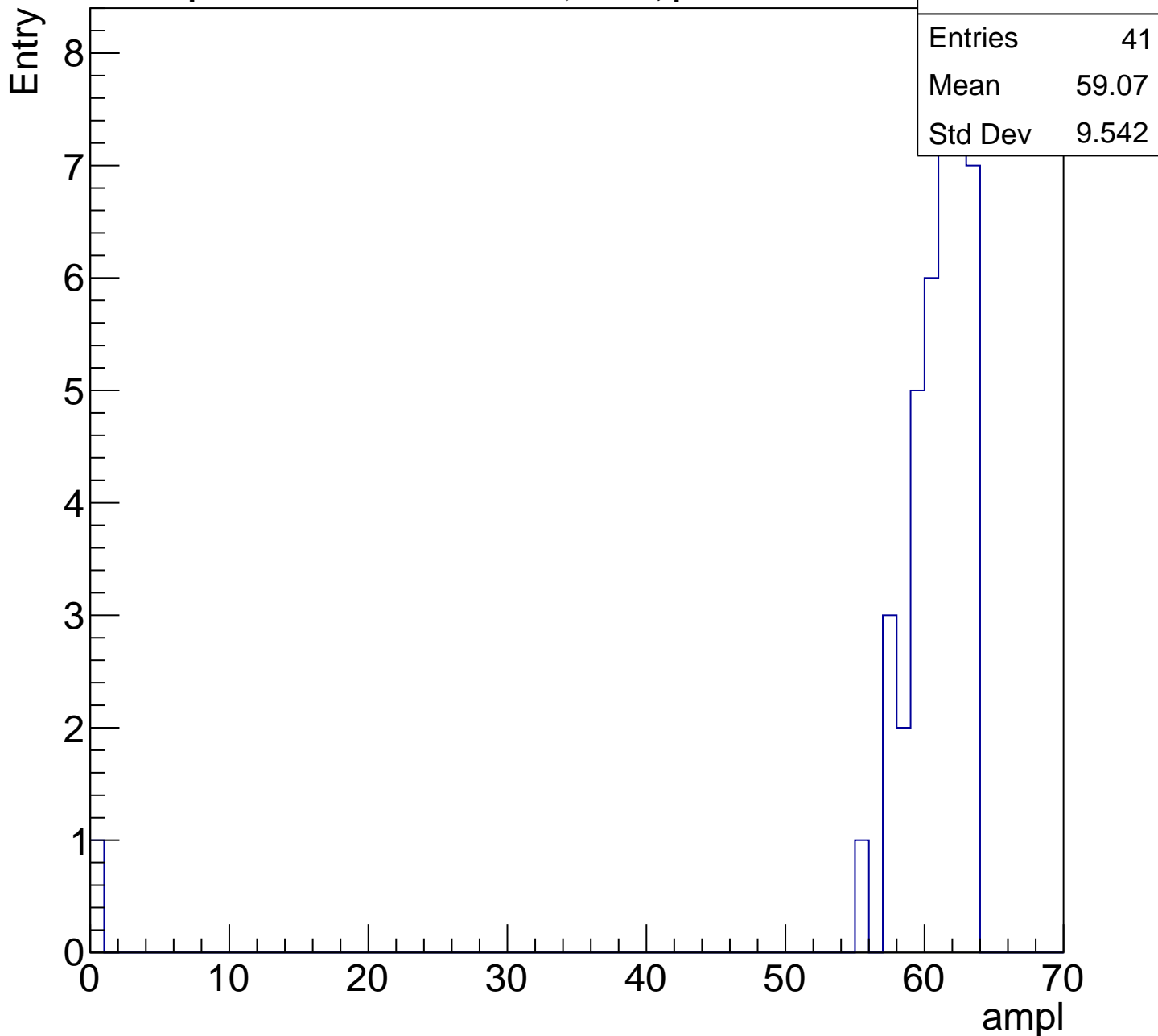
B0L002S, U9-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch75, adc0

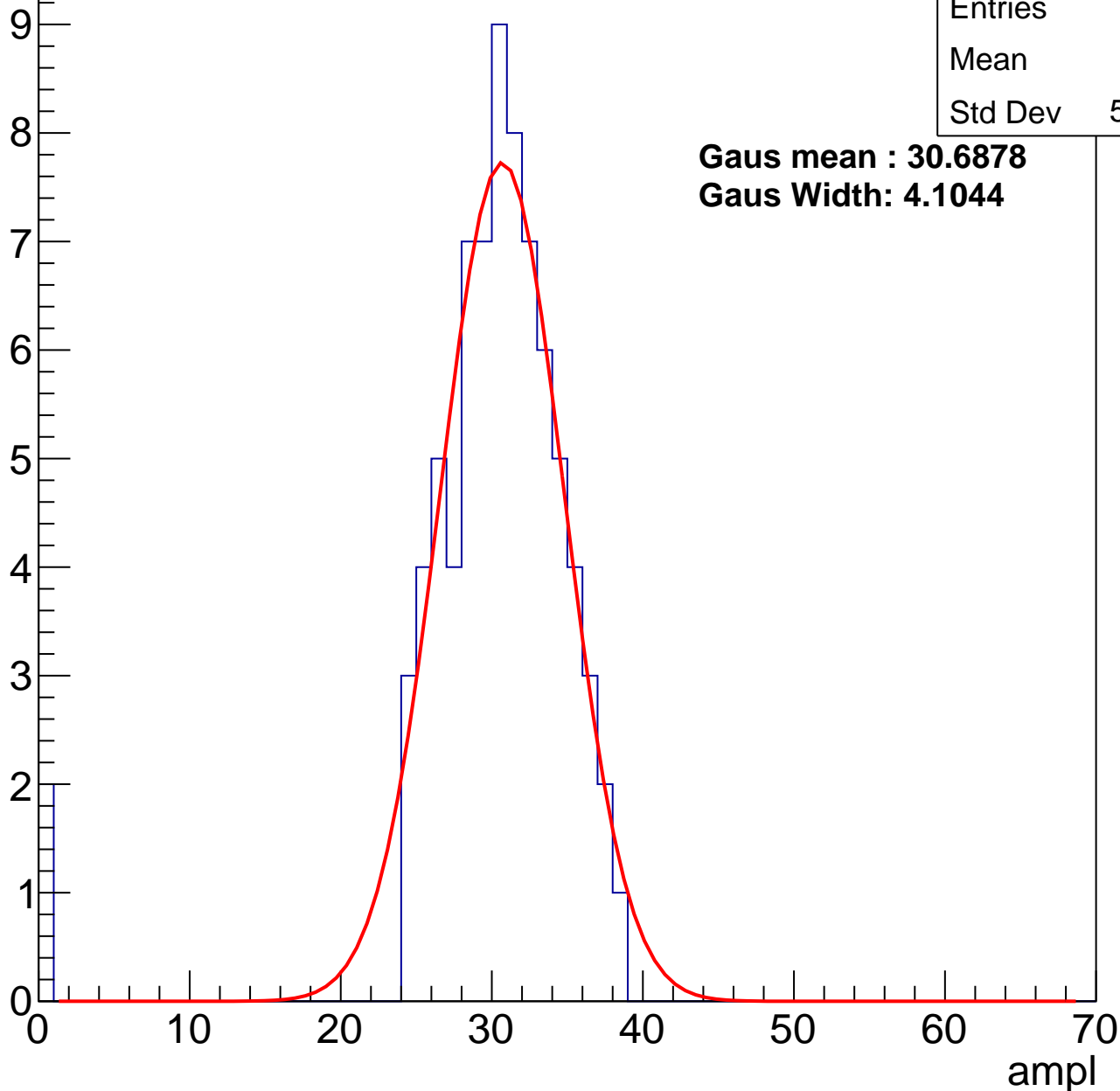
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	29.6
Std Dev	5.907

Gaus mean : 30.6878

Gaus Width: 4.1044



B0L002S, U9-ch75, adc1

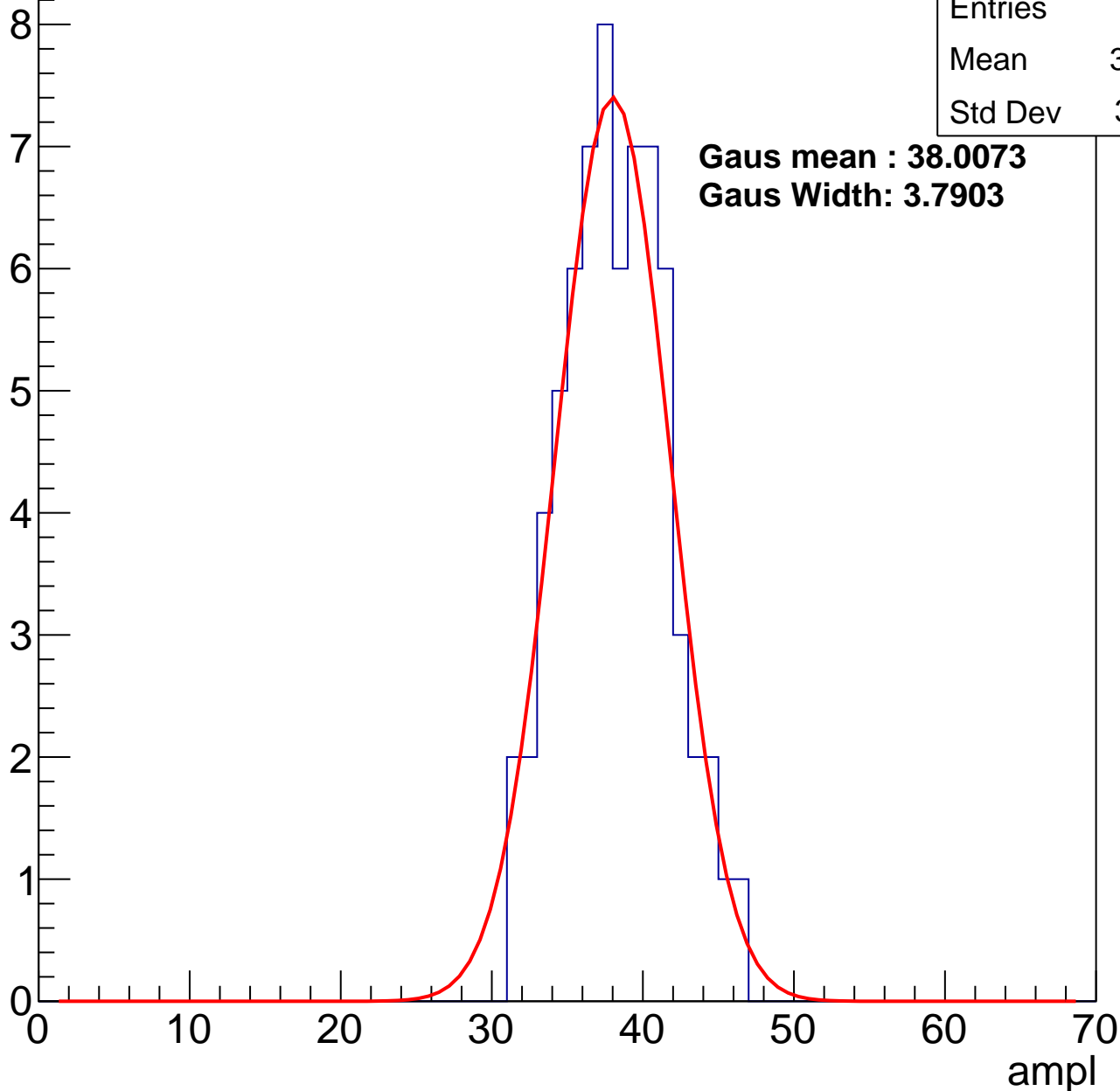
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.74
Std Dev	3.421

Gaus mean : 38.0073

Gaus Width: 3.7903



B0L002S, U9-ch75, adc2

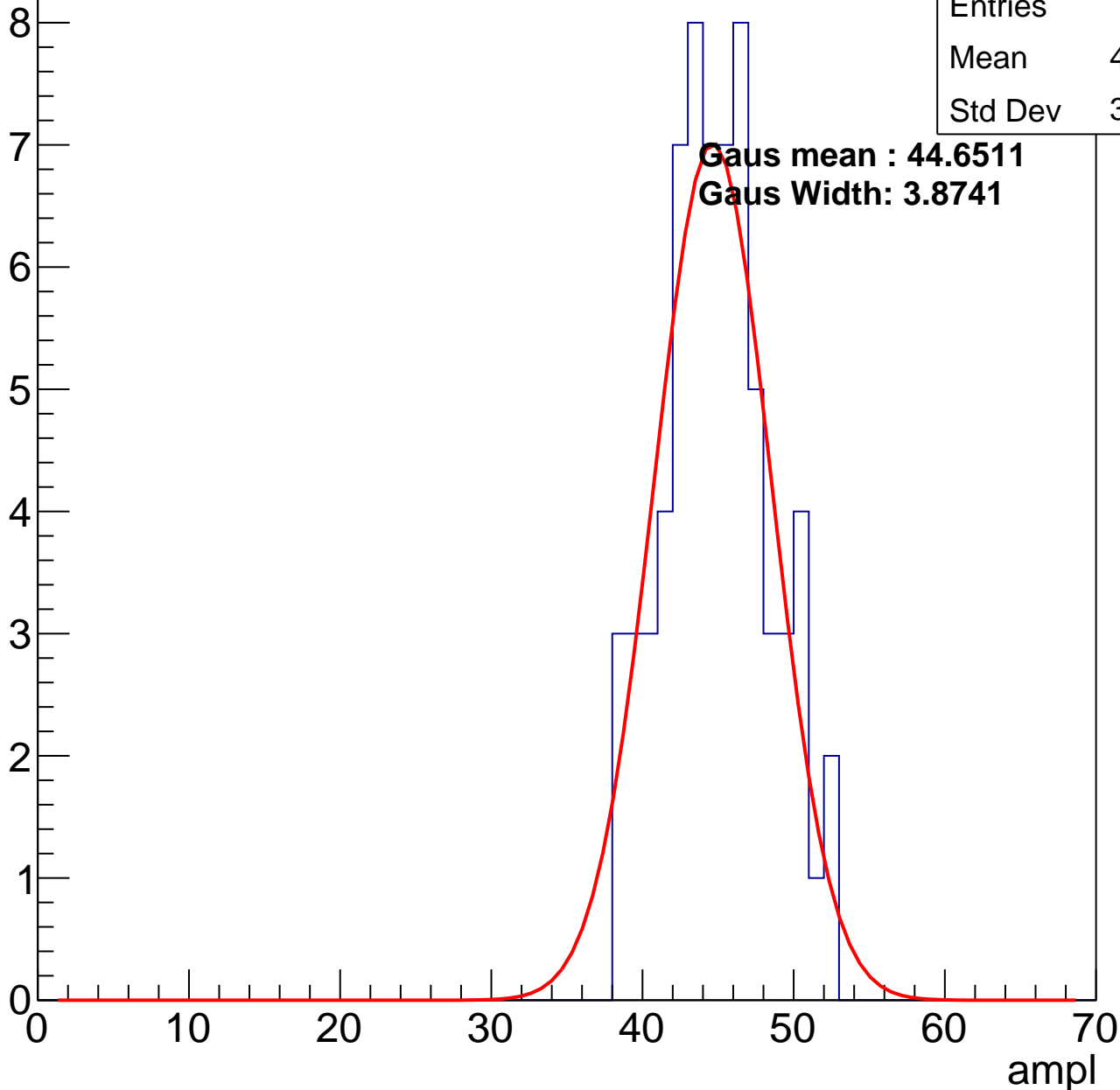
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	44.49
Std Dev	3.462

Gaus mean : 44.6511

Gaus Width: 3.8741

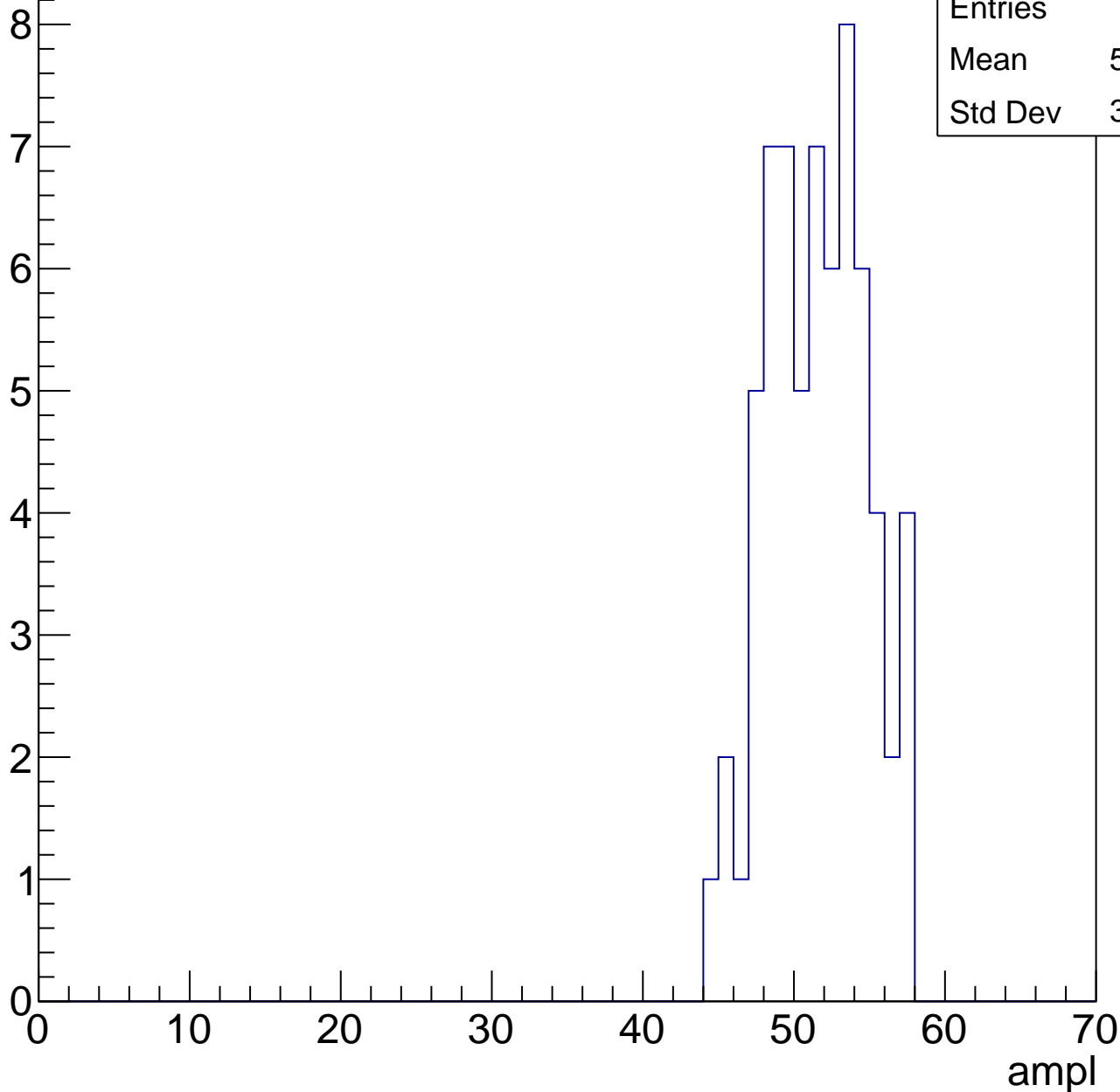


B0L002S, U9-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	51.09
Std Dev	3.214

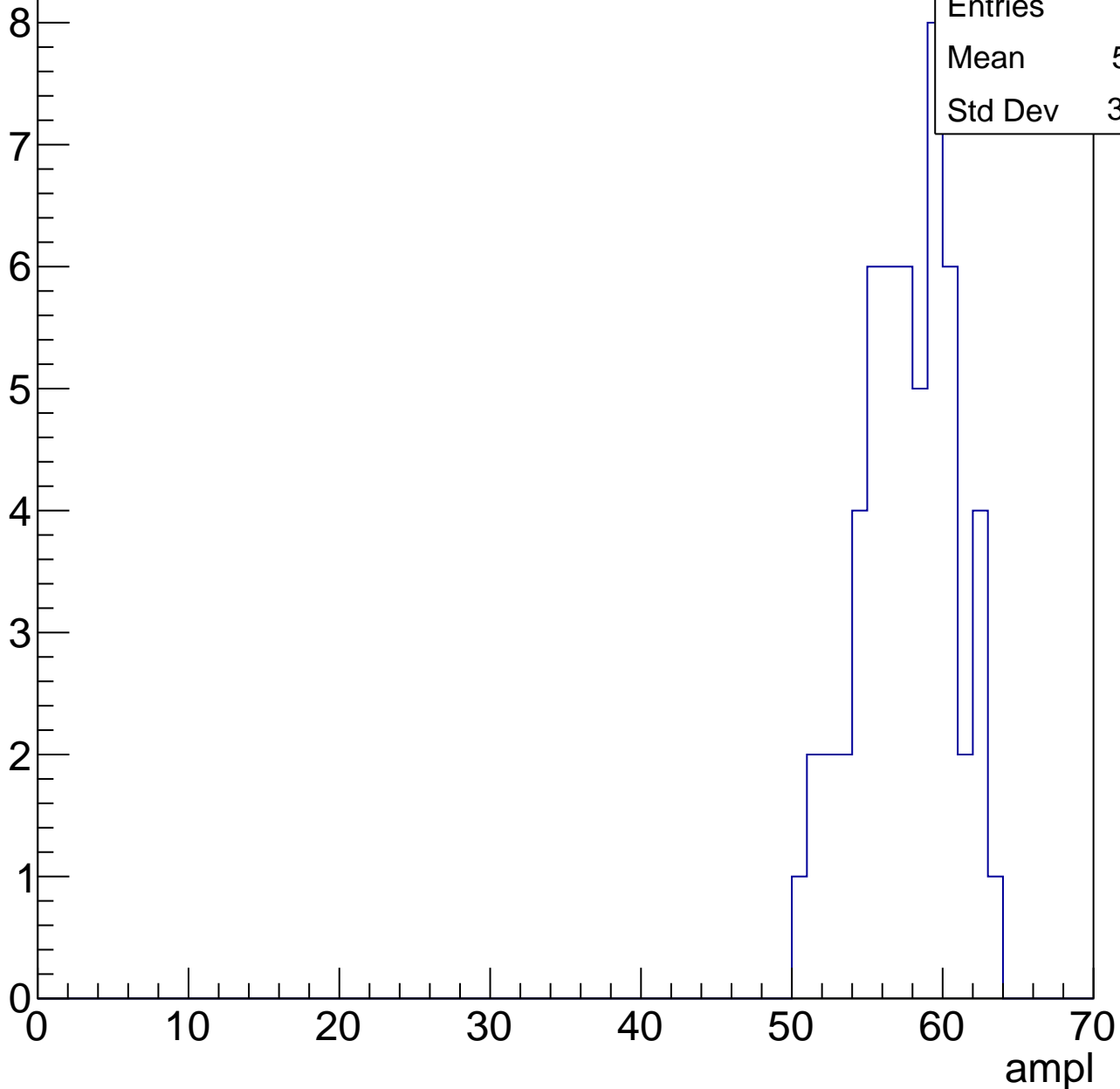


B0L002S, U9-ch75, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.11
Std Dev	3.096

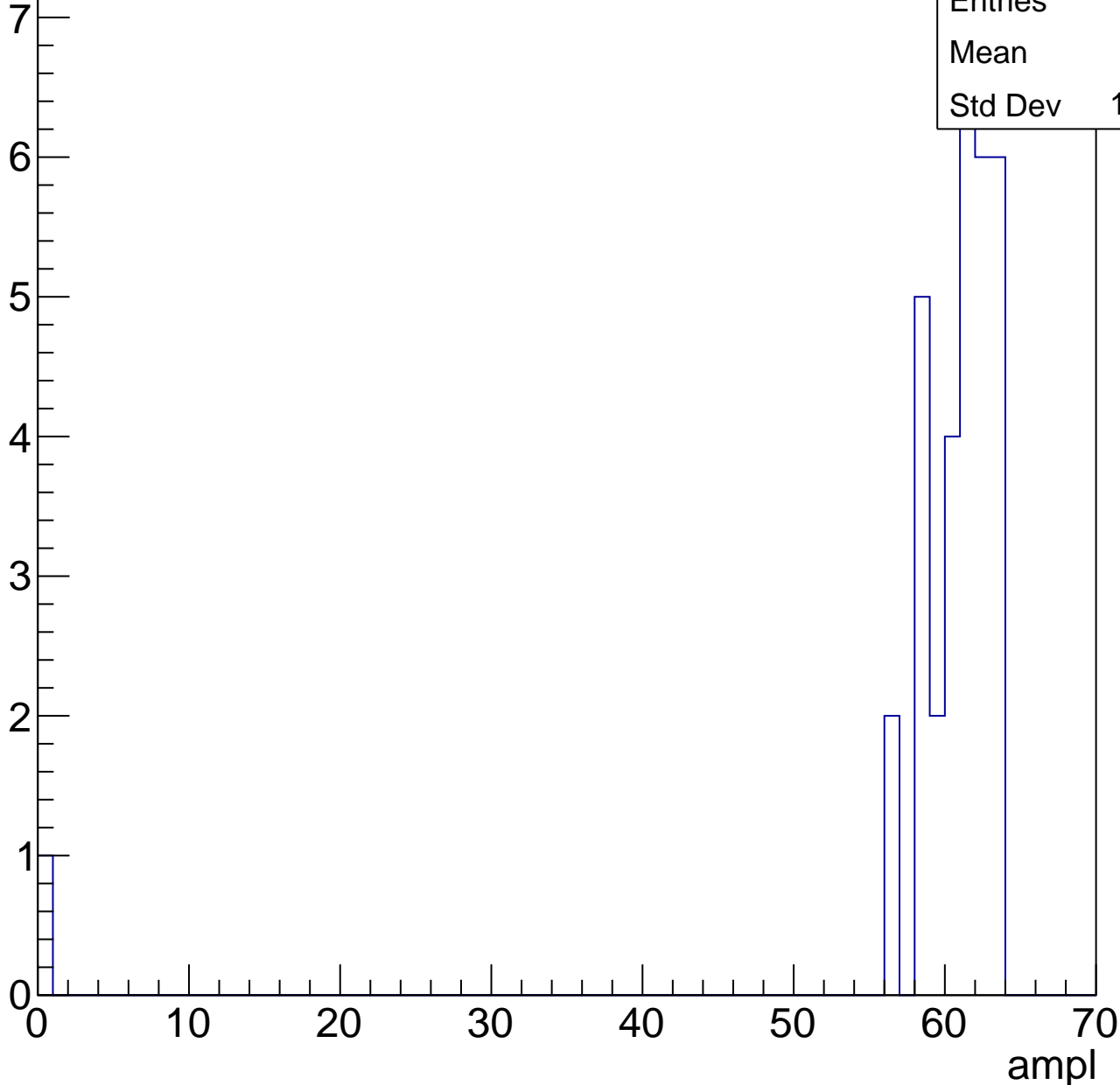


B0L002S, U9-ch75, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

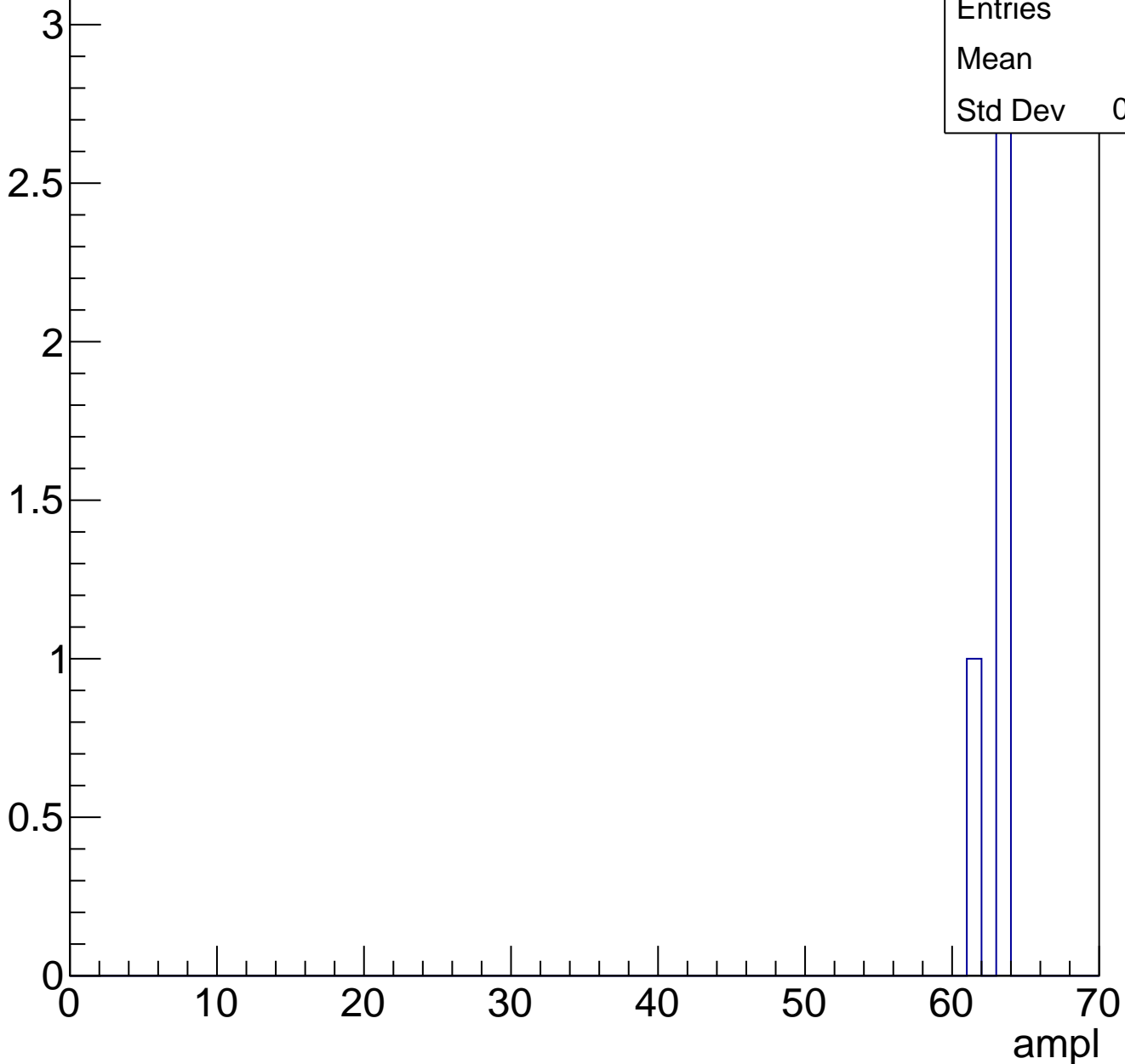
Entries	33
Mean	58.7
Std Dev	10.56



B0L002S, U9-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch76, adc0

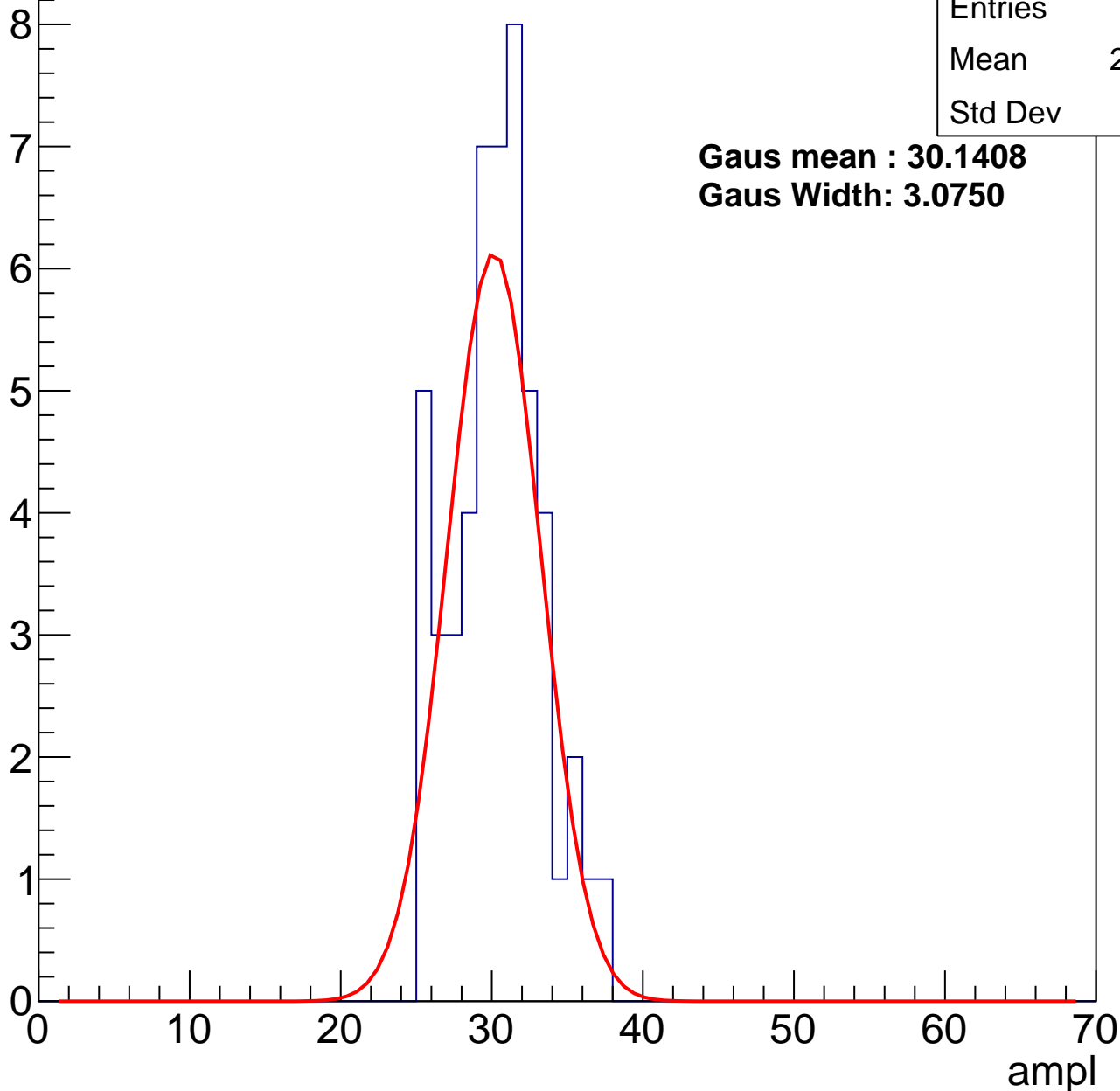
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	29.92
Std Dev	2.93

Gaus mean : 30.1408

Gaus Width: 3.0750



B0L002S, U9-ch76, adc1

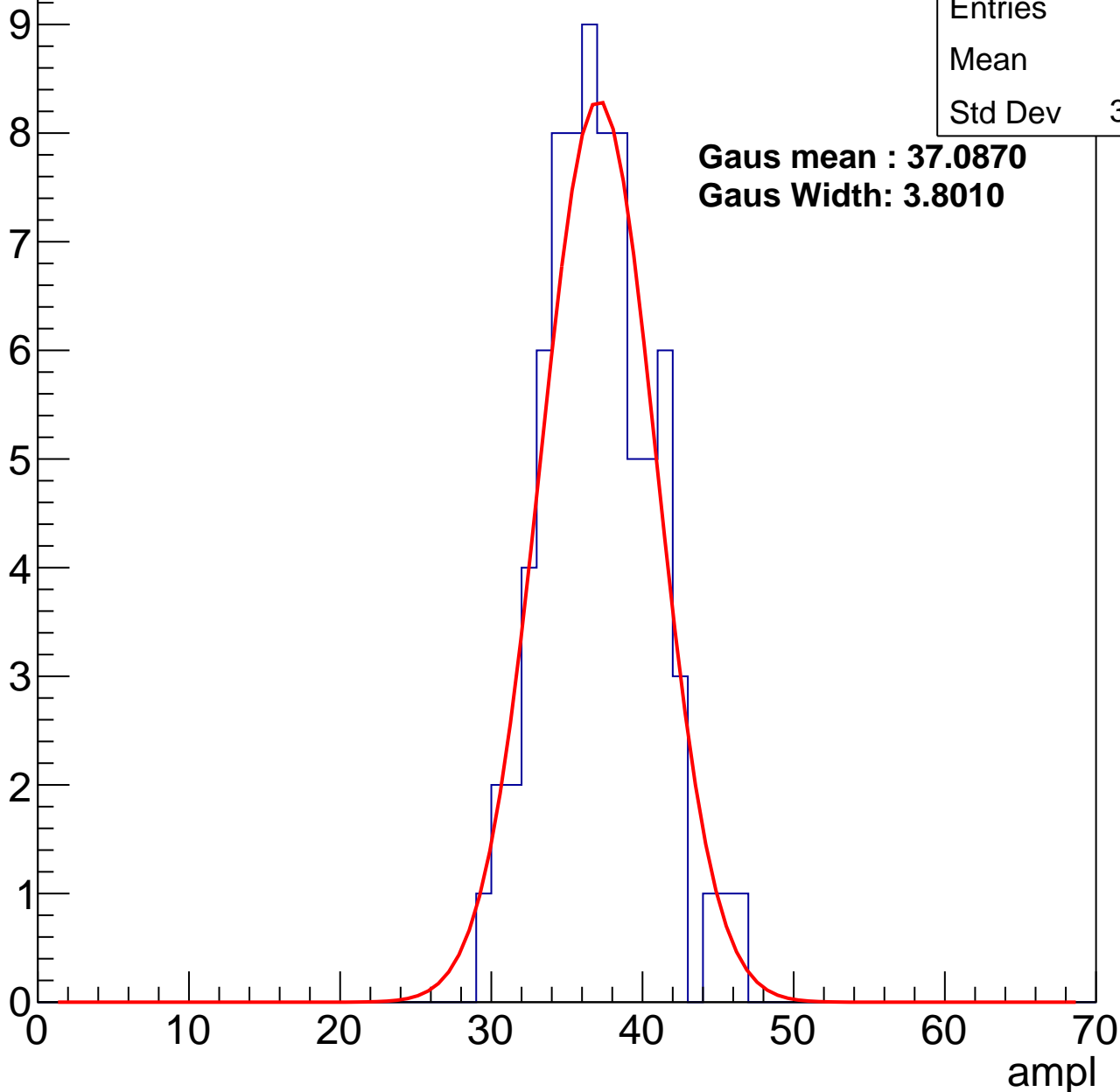
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	36.6
Std Dev	3.524

Gaus mean : 37.0870

Gaus Width: 3.8010



B0L002S, U9-ch76, adc2

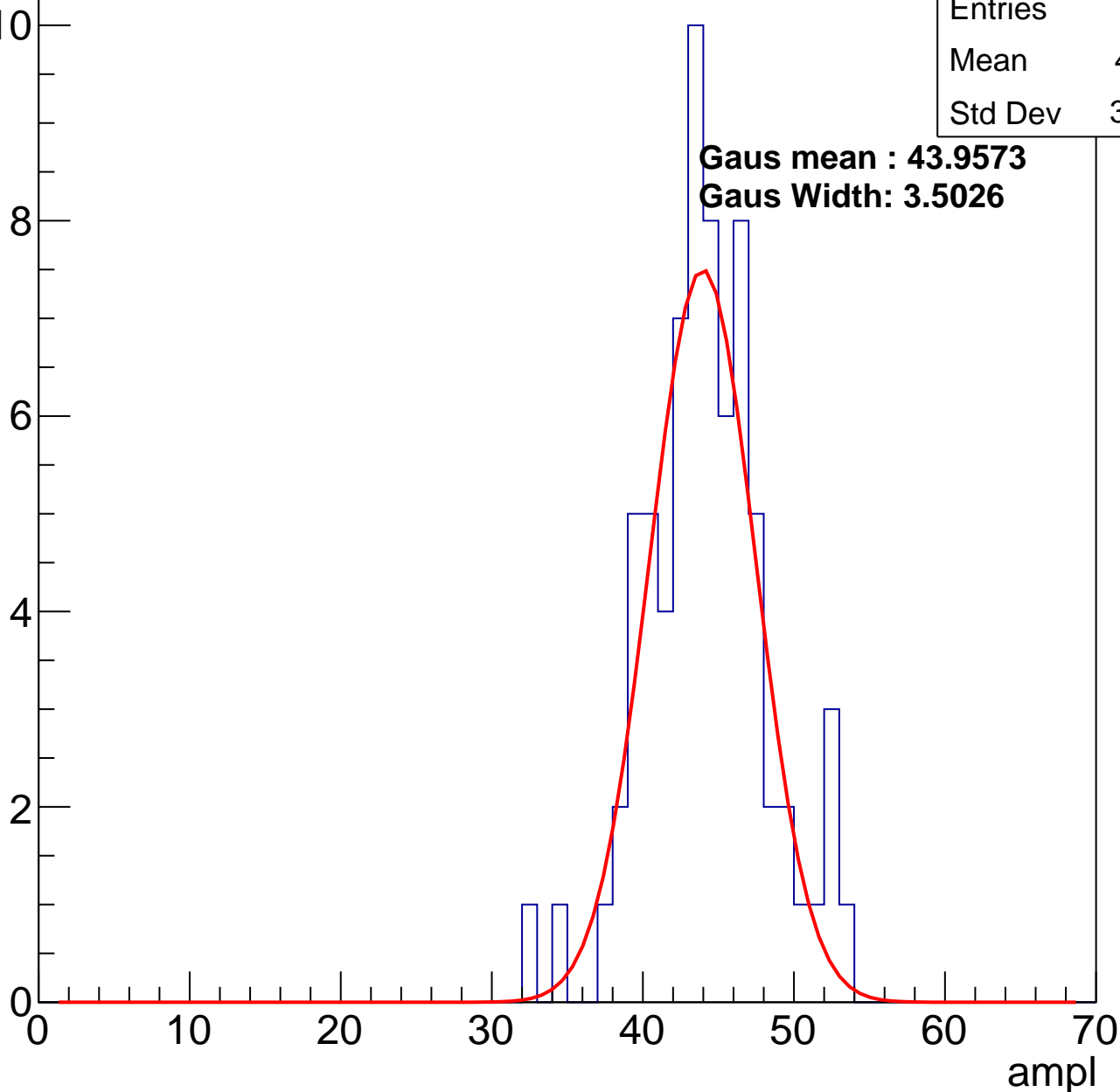
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	43.71
Std Dev	3.988

Gaus mean : 43.9573

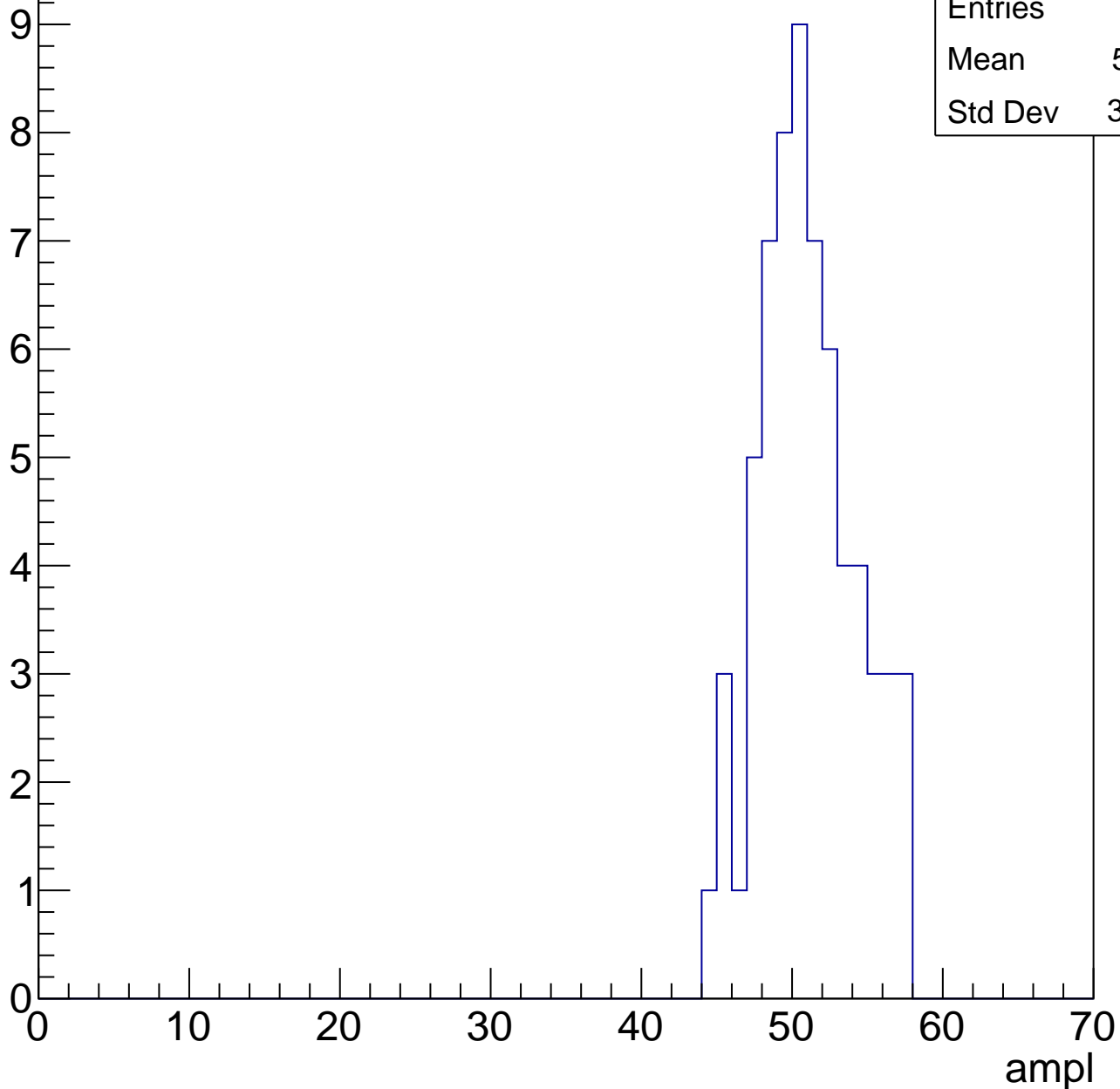
Gaus Width: 3.5026



B0L002S, U9-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



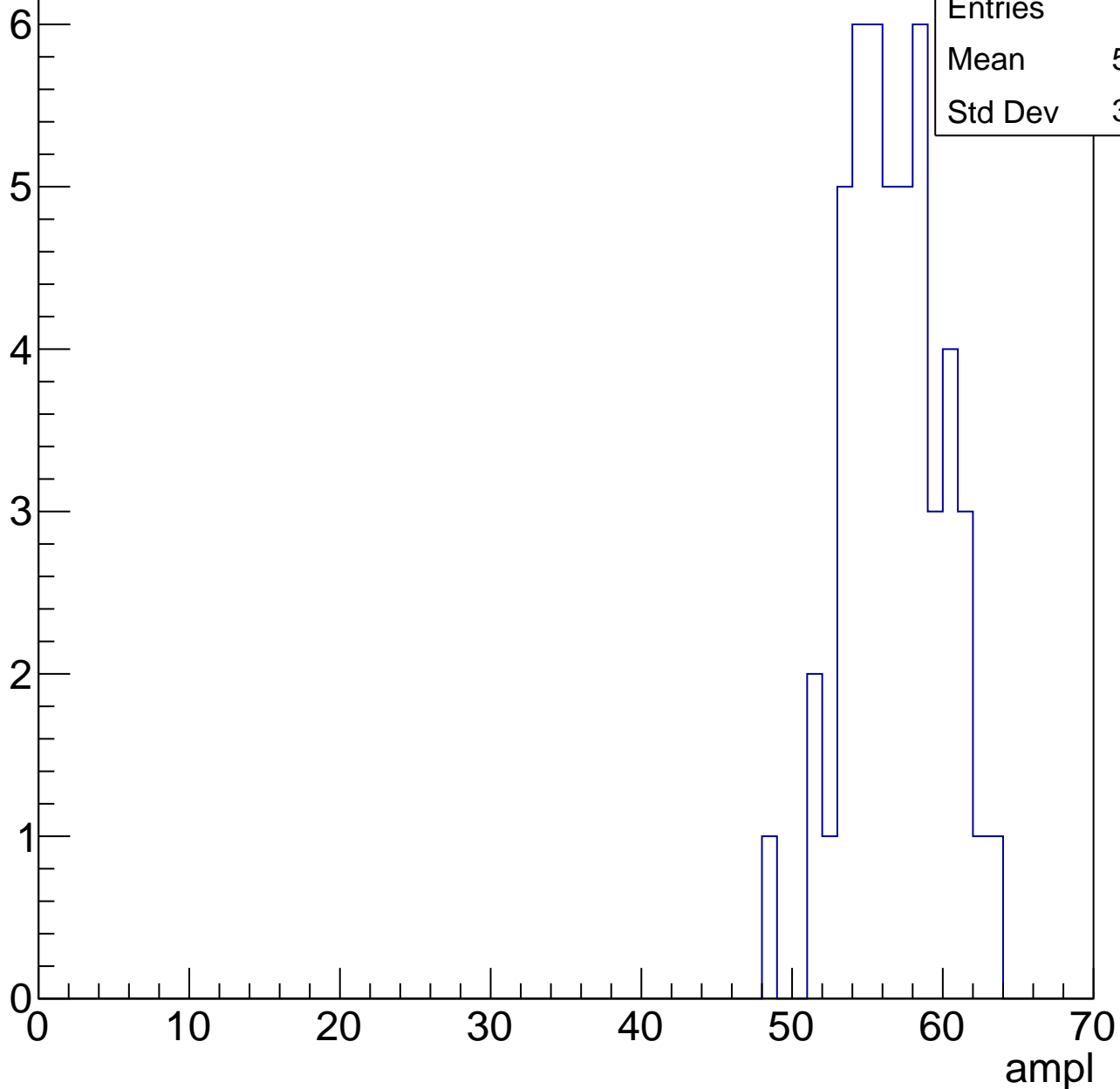
Entries	64
Mean	50.61
Std Dev	3.175

B0L002S, U9-ch76, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	56.31
Std Dev	3.131

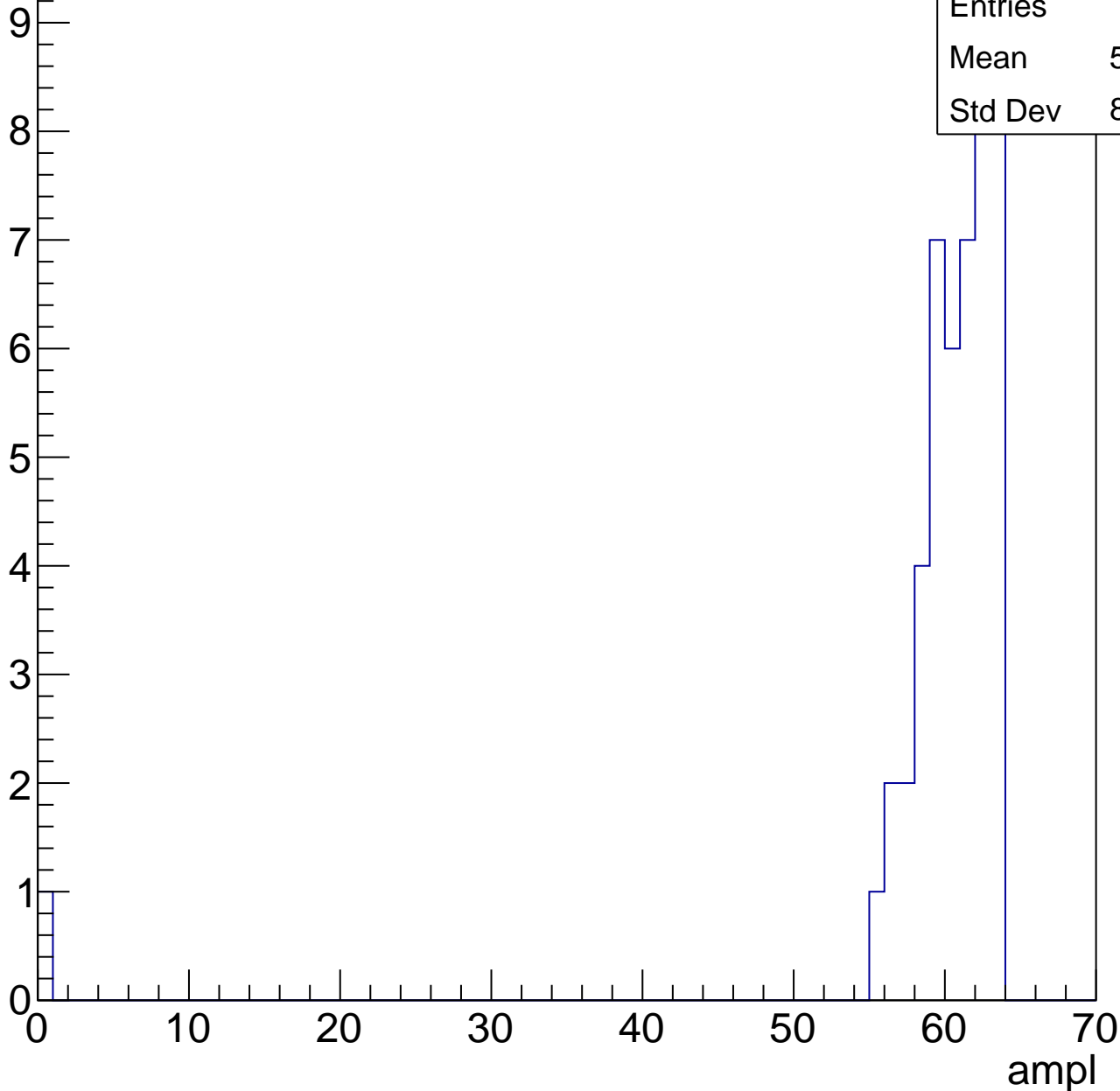


B0L002S, U9-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

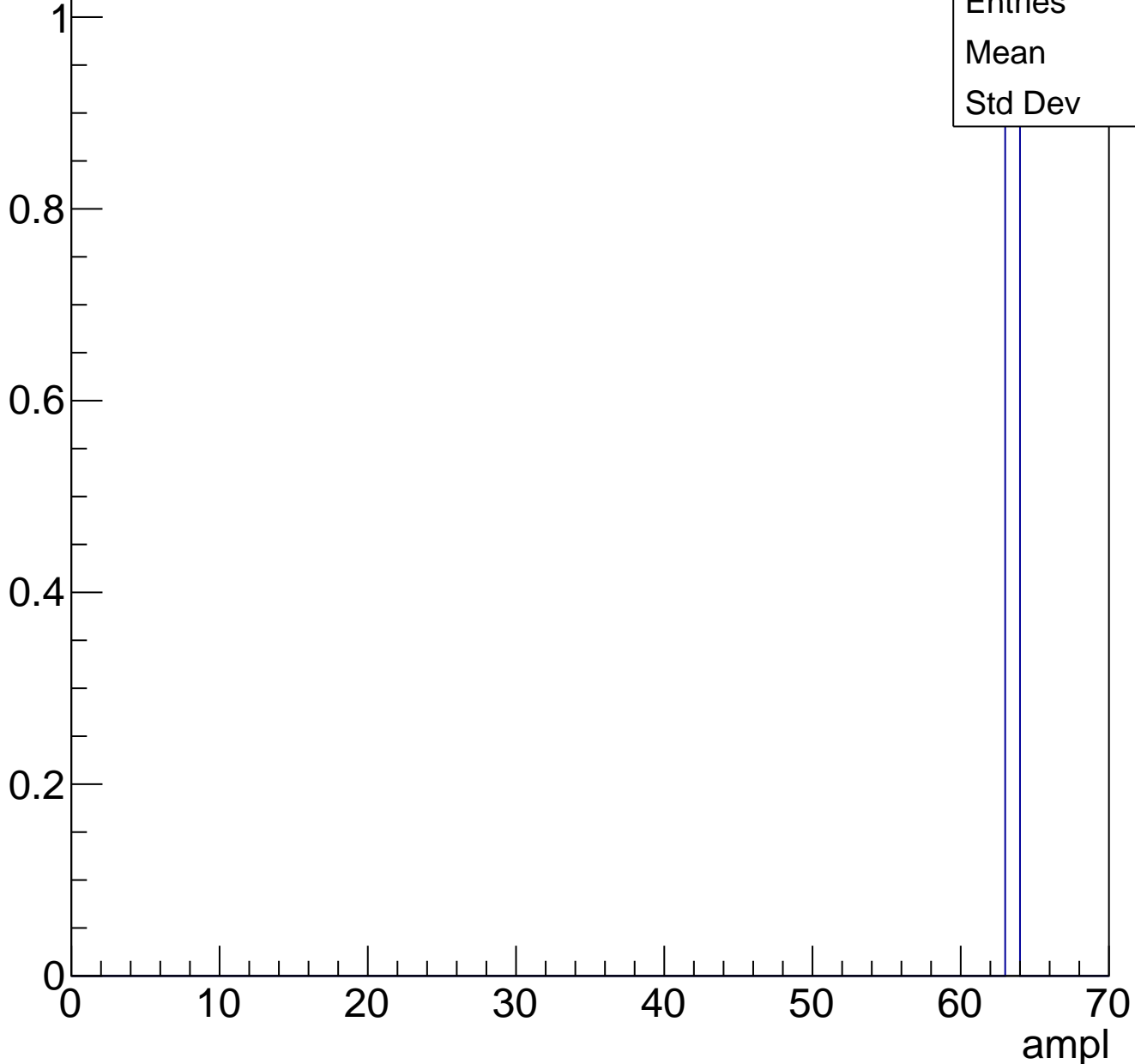
Entries	47
Mean	59.04
Std Dev	8.956



B0L002S, U9-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch77, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

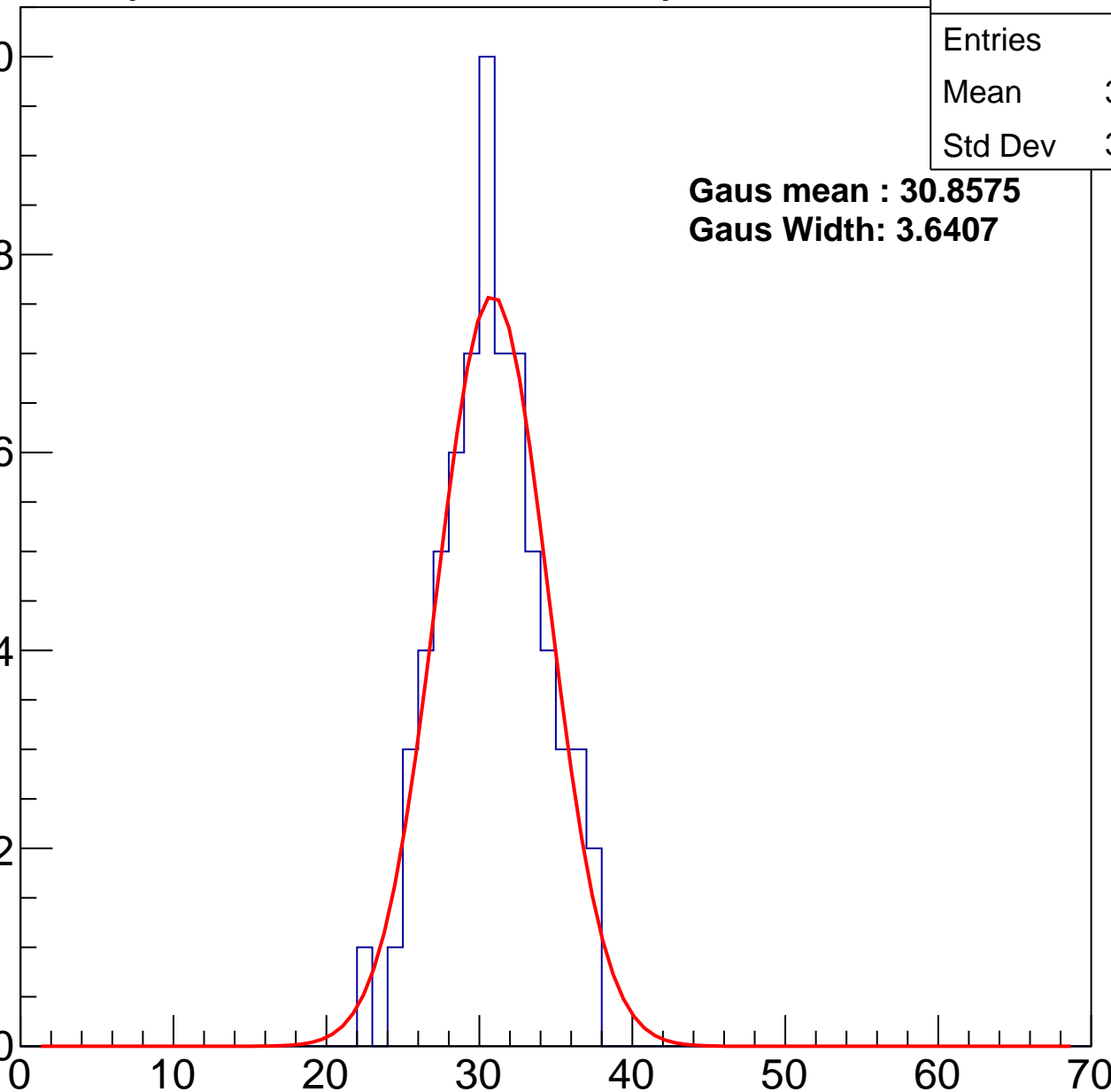
Entries	68
Mean	30.29
Std Dev	3.286

Gaus mean : 30.8575

Gaus Width: 3.6407

10
8
6
4
2
0

ampl



B0L002S, U9-ch77, adc1

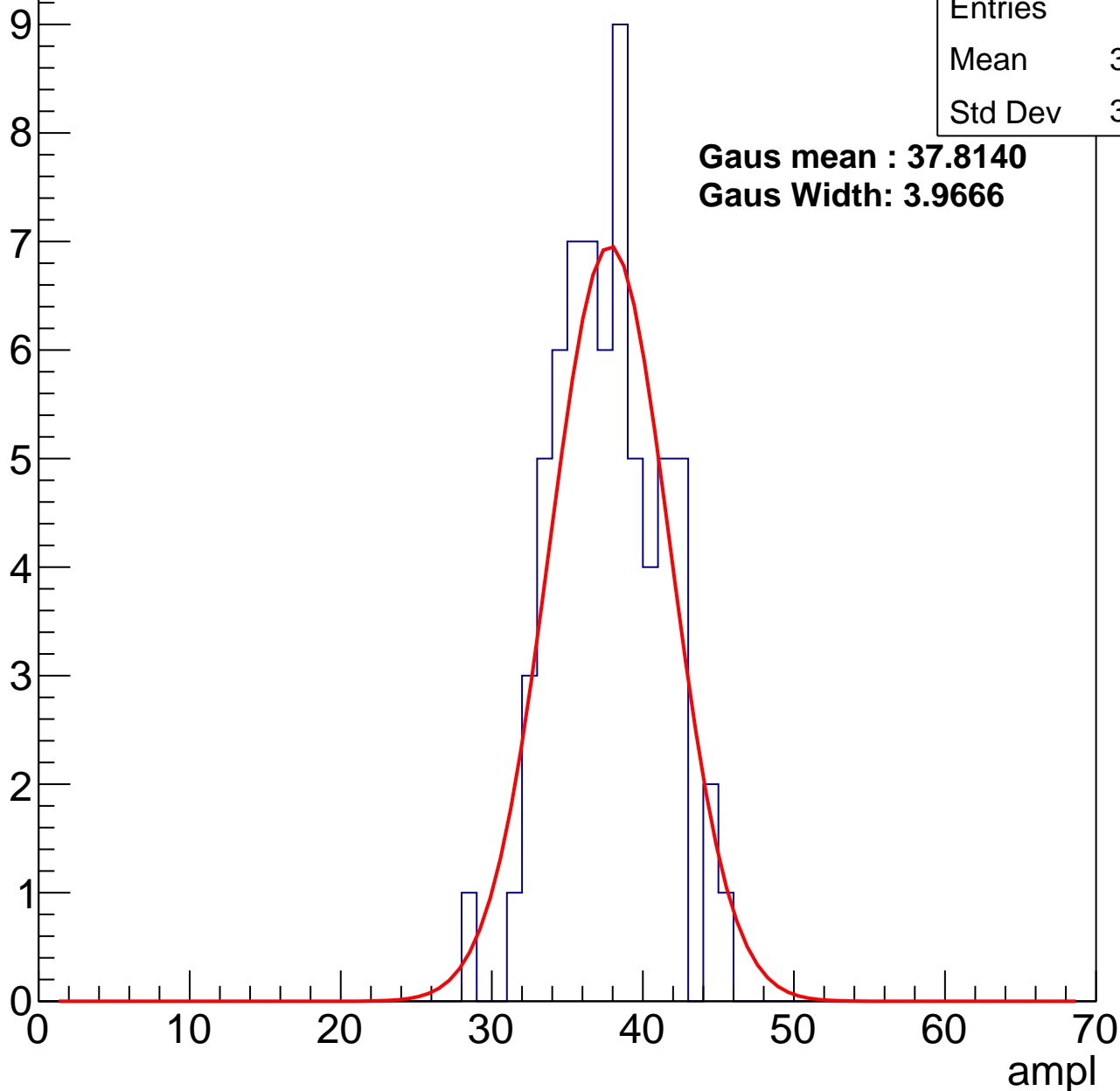
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.13
Std Dev	3.438

Gaus mean : 37.8140

Gaus Width: 3.9666



B0L002S, U9-ch77, adc2

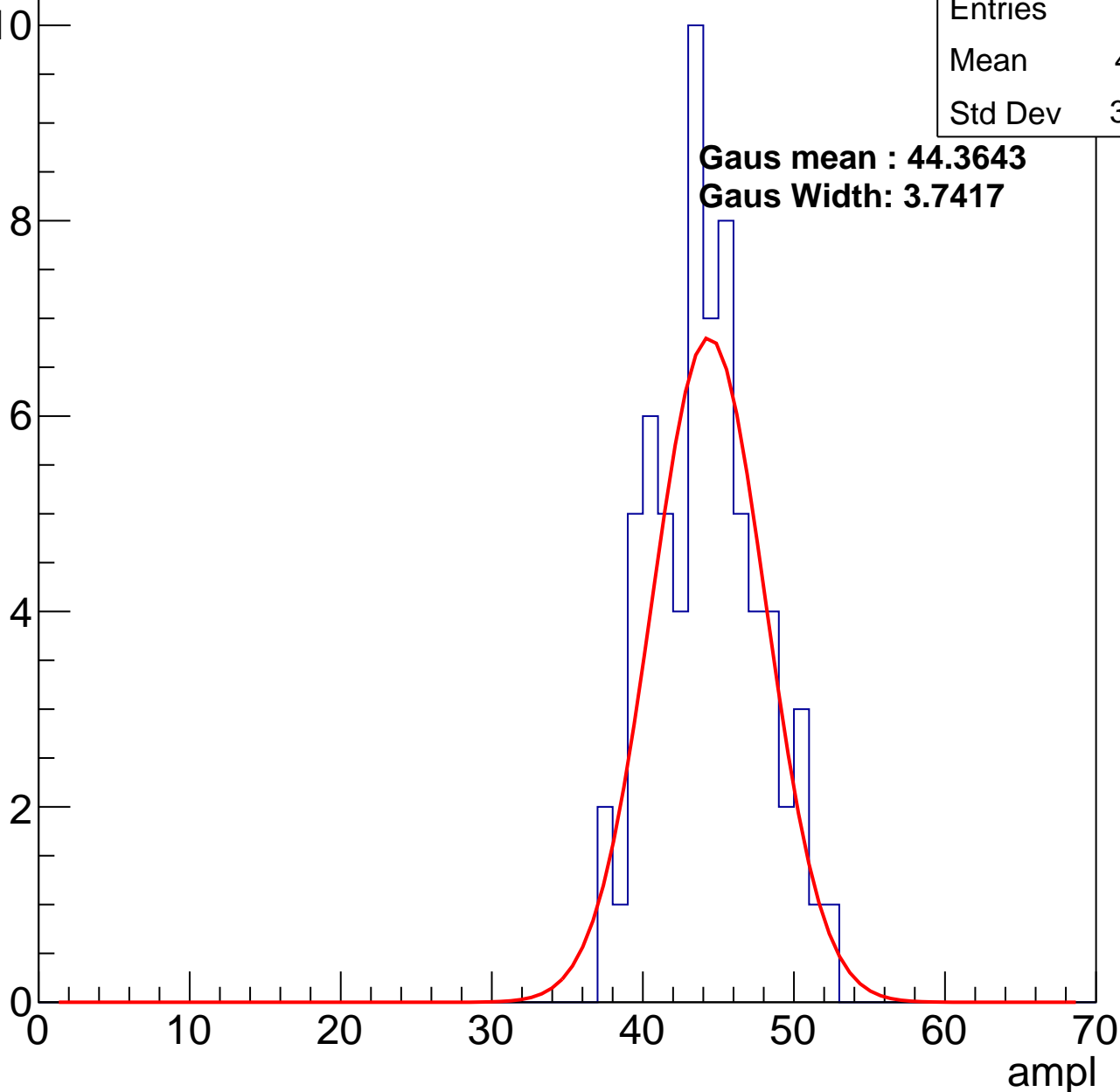
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.81
Std Dev	3.478

Gaus mean : 44.3643

Gaus Width: 3.7417

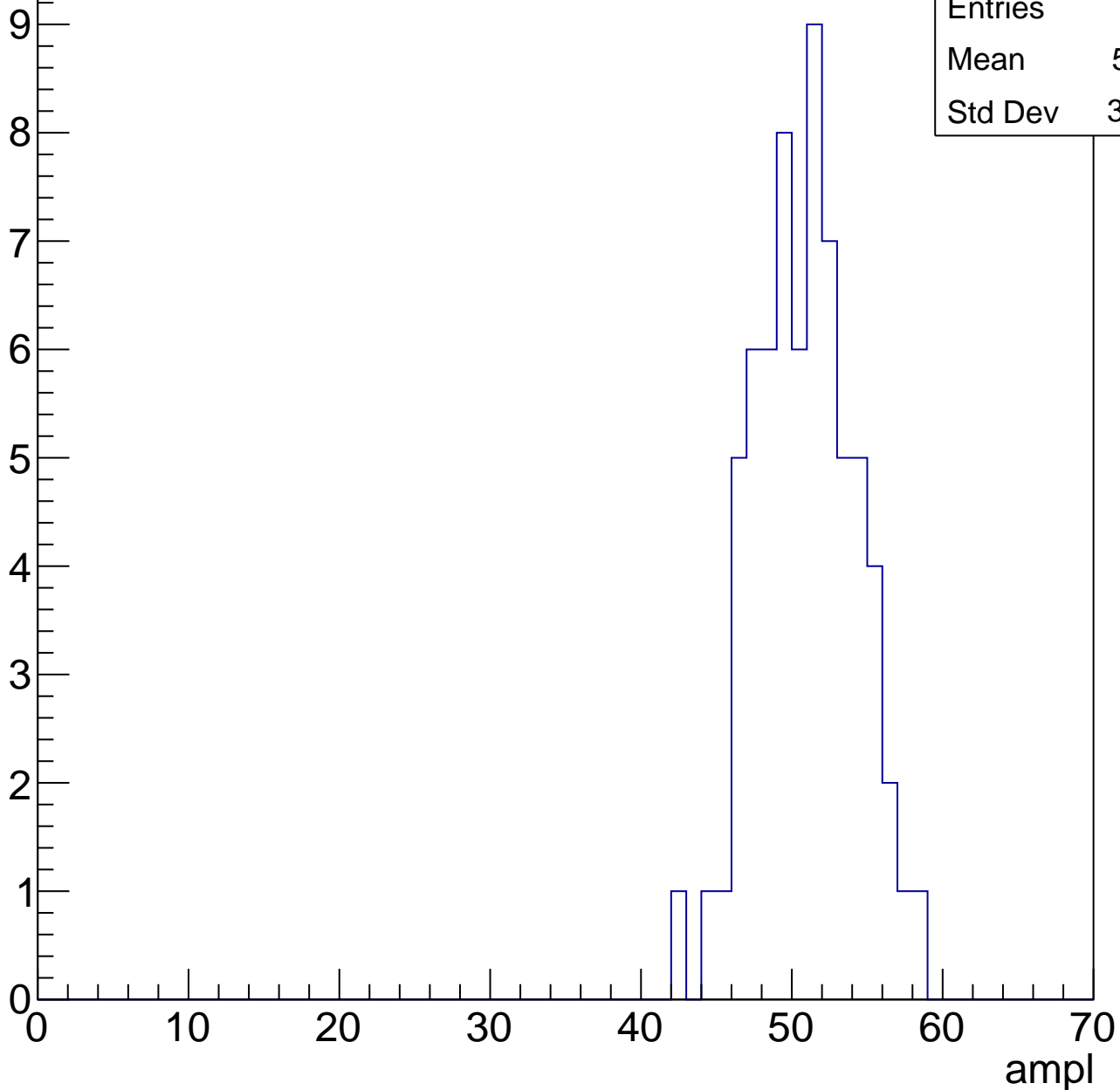


B0L002S, U9-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

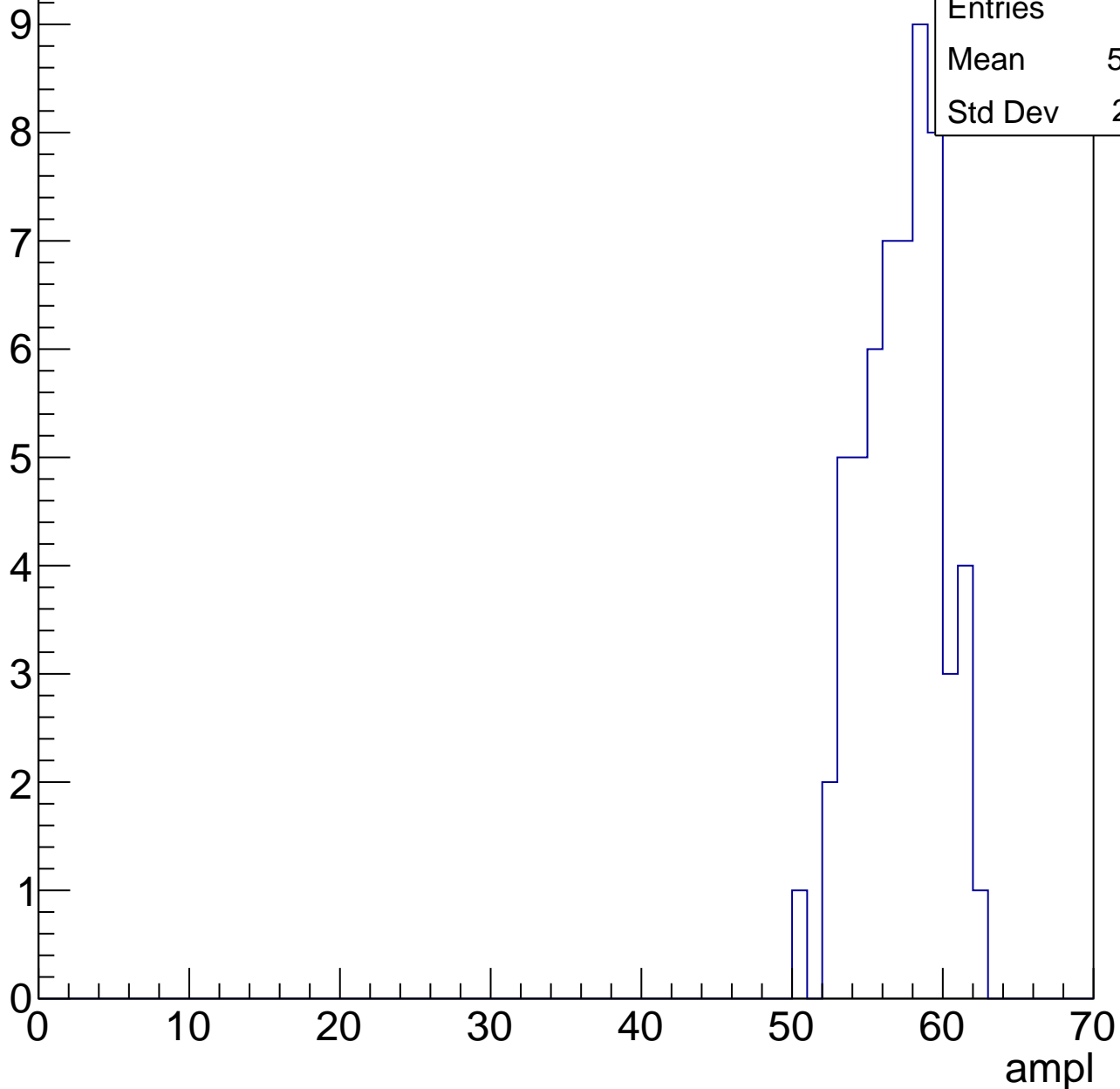
Entries	68
Mean	50.41
Std Dev	3.269



B0L002S, U9-ch77, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

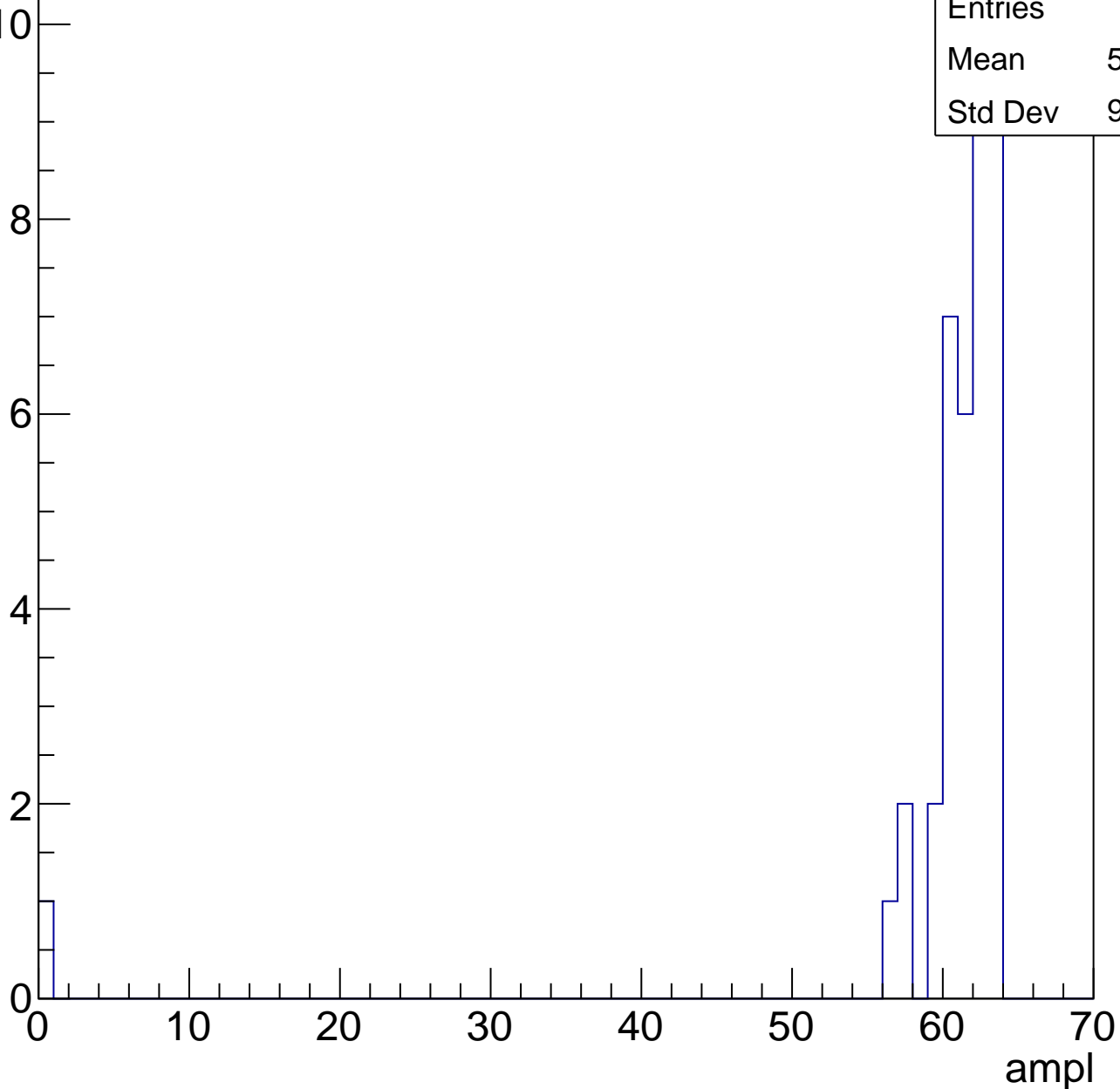


B0L002S, U9-ch77, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.53
Std Dev	9.947



B0L002S, U9-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch77, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch78, adc0

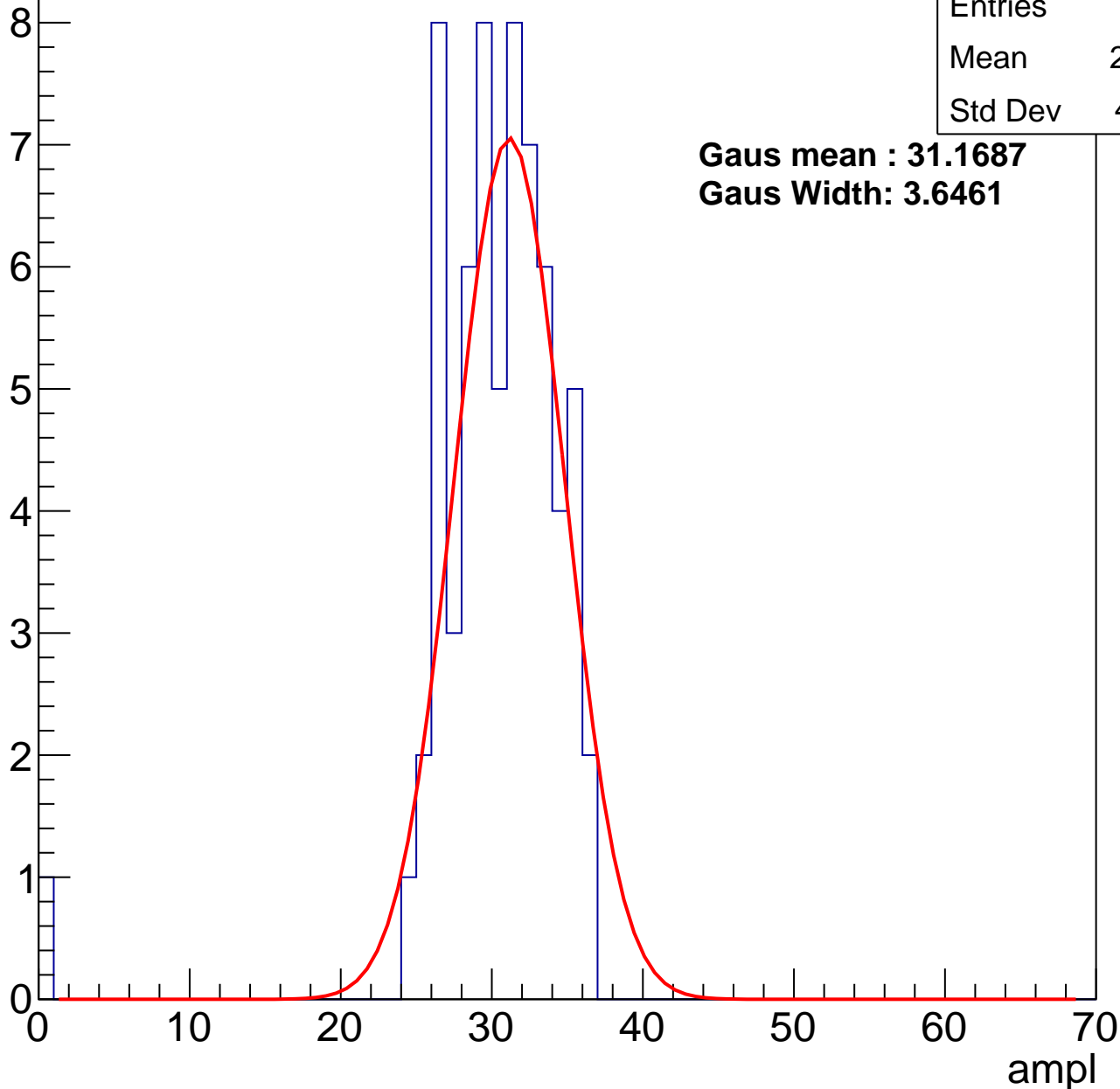
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.79
Std Dev	4.801

Gaus mean : 31.1687

Gaus Width: 3.6461



B0L002S, U9-ch78, adc1

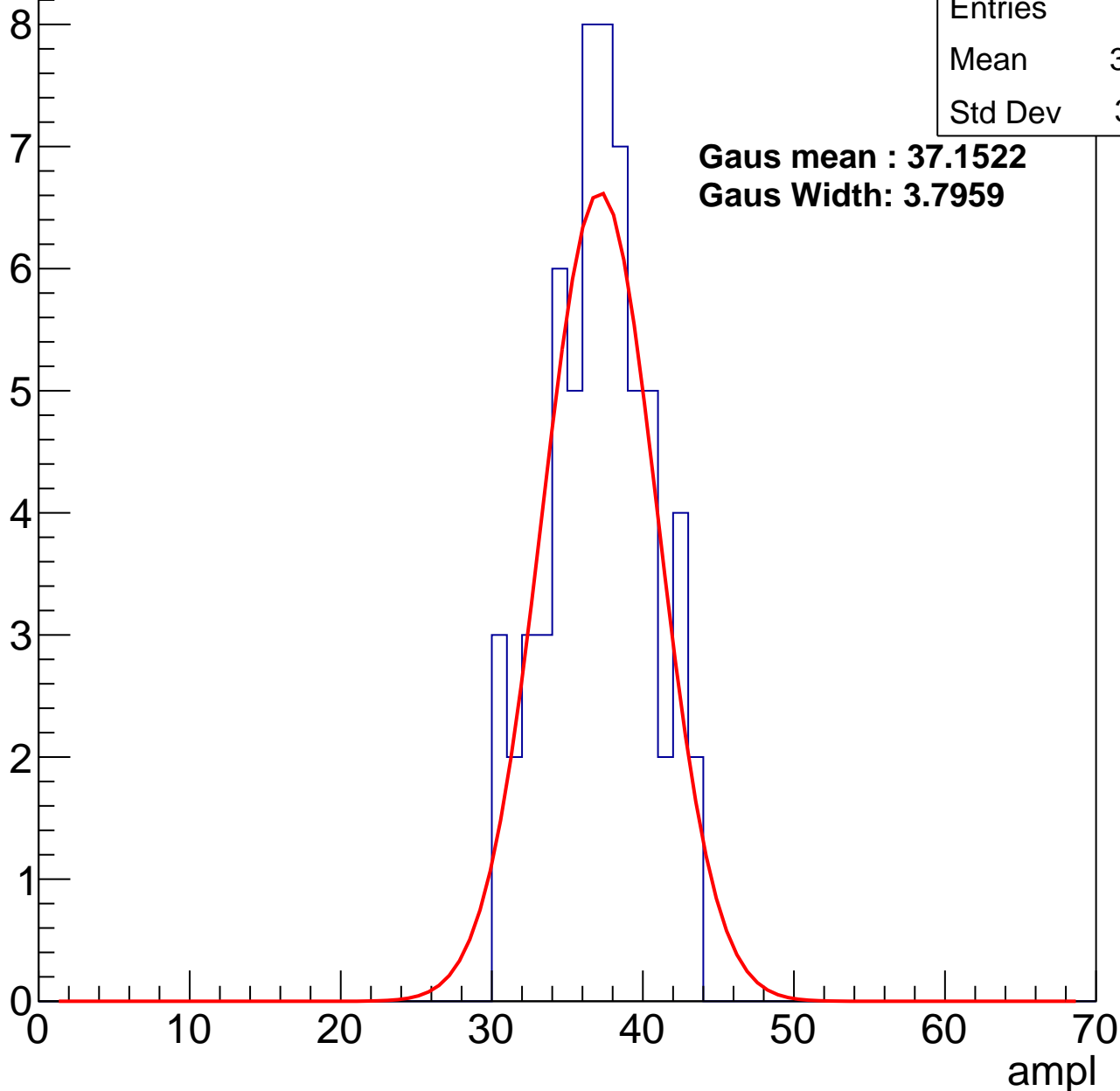
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.62
Std Dev	3.311

Gaus mean : 37.1522

Gaus Width: 3.7959



B0L002S, U9-ch78, adc2

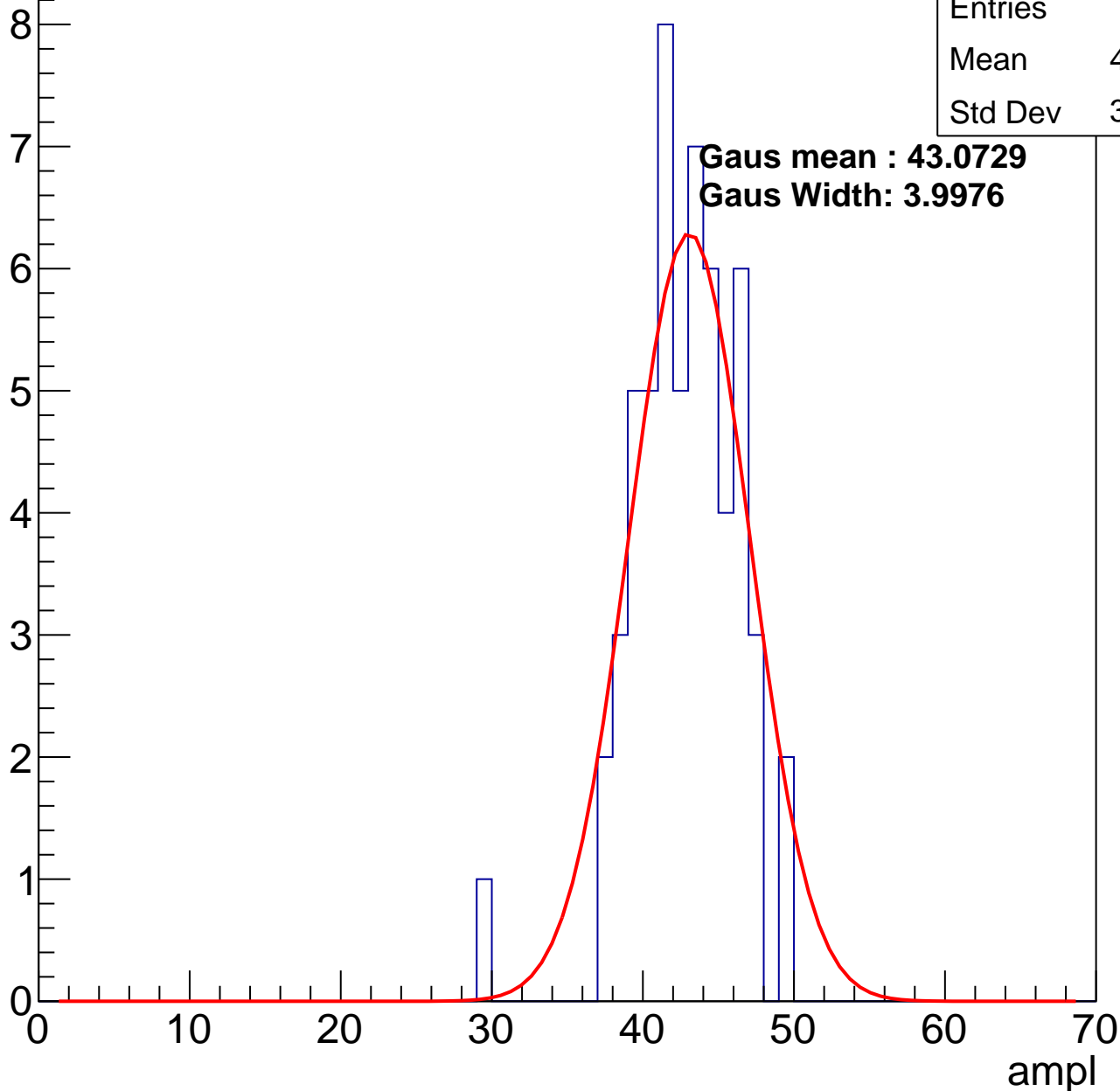
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.28
Std Dev	3.432

Gaus mean : 43.0729

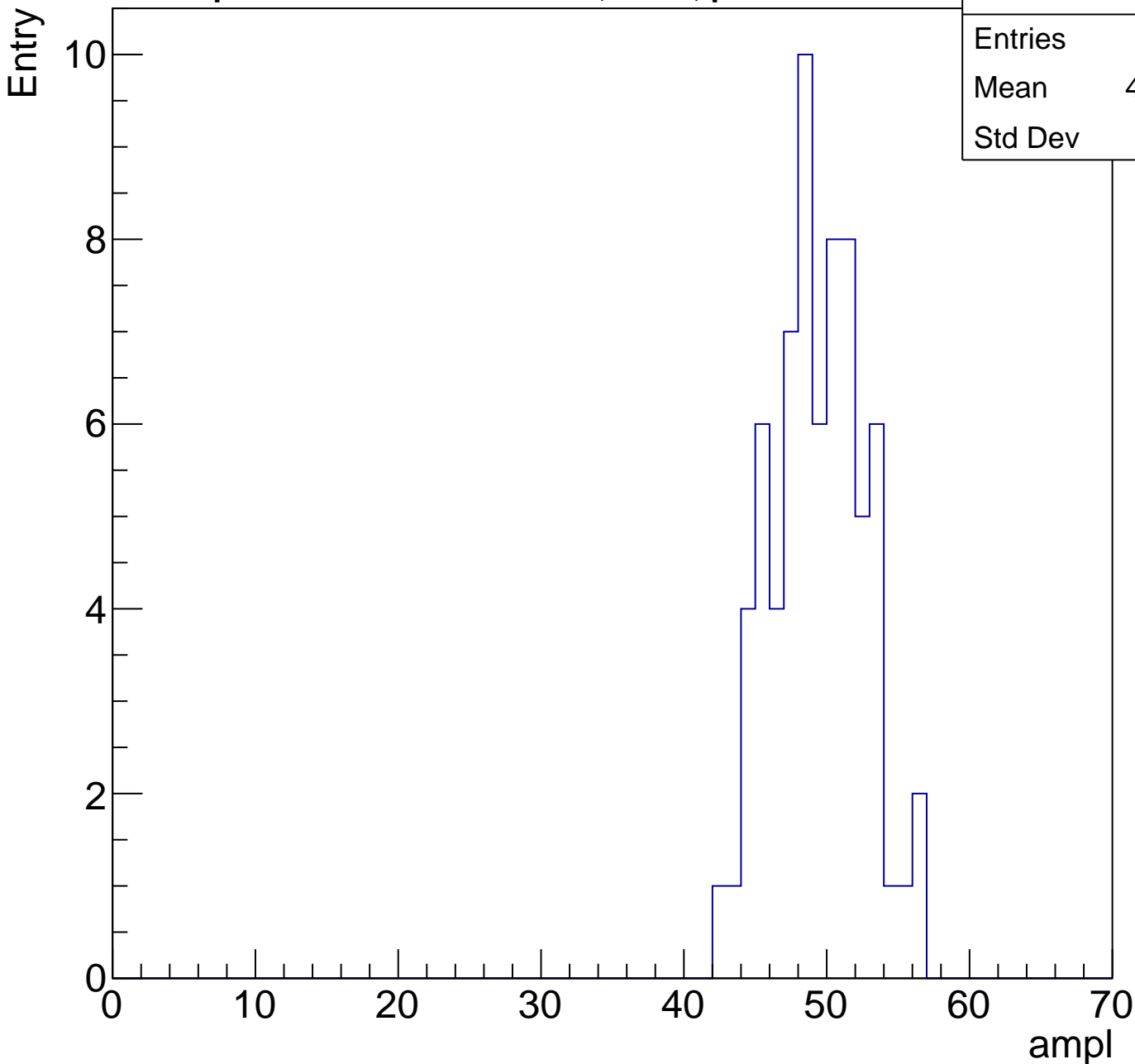
Gaus Width: 3.9976



B0L002S, U9-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	48.93
Std Dev	3.15



B0L002S, U9-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

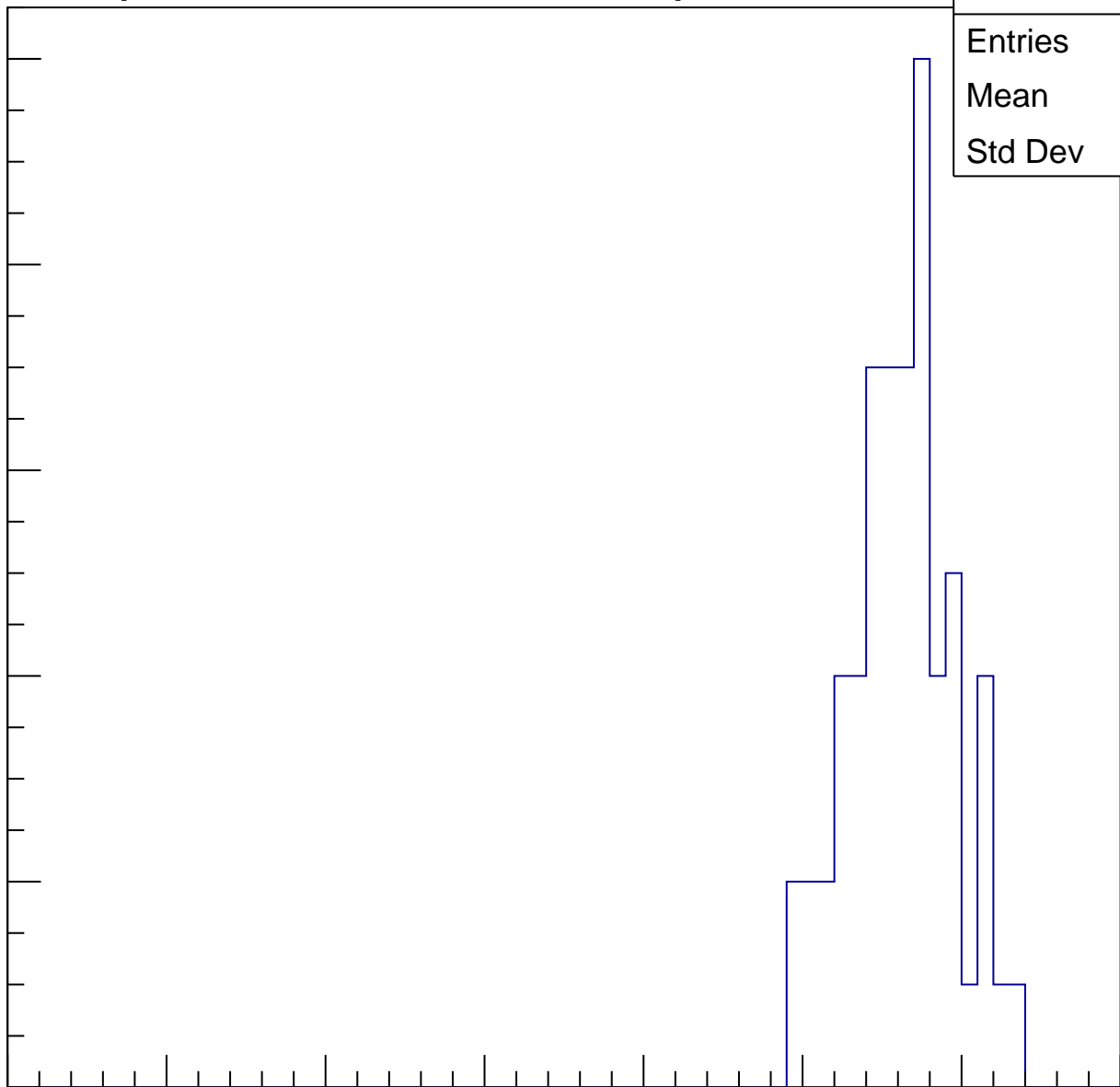
Entries	61
Mean	55.75
Std Dev	3.197

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

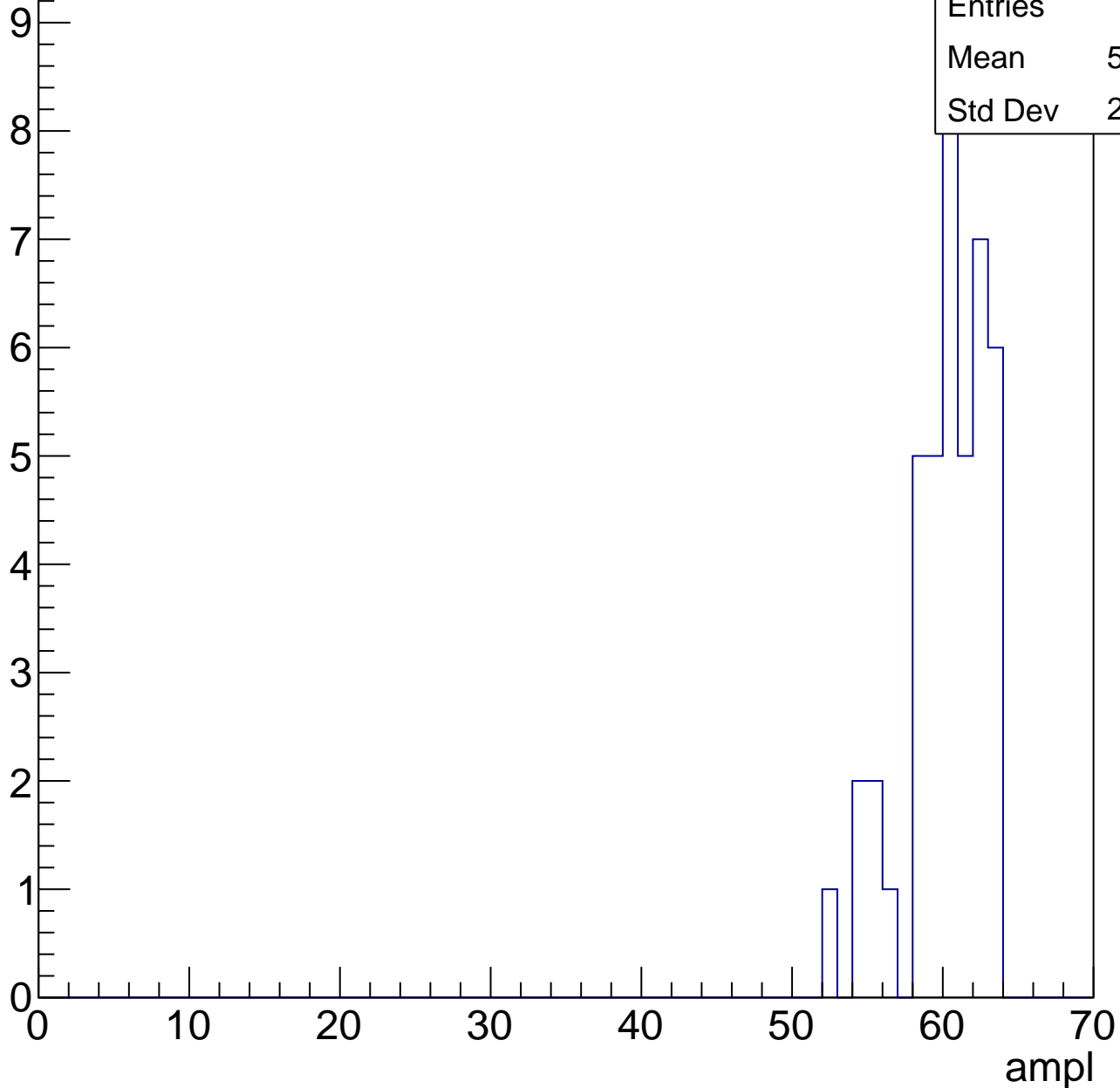
ampl



B0L002S, U9-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

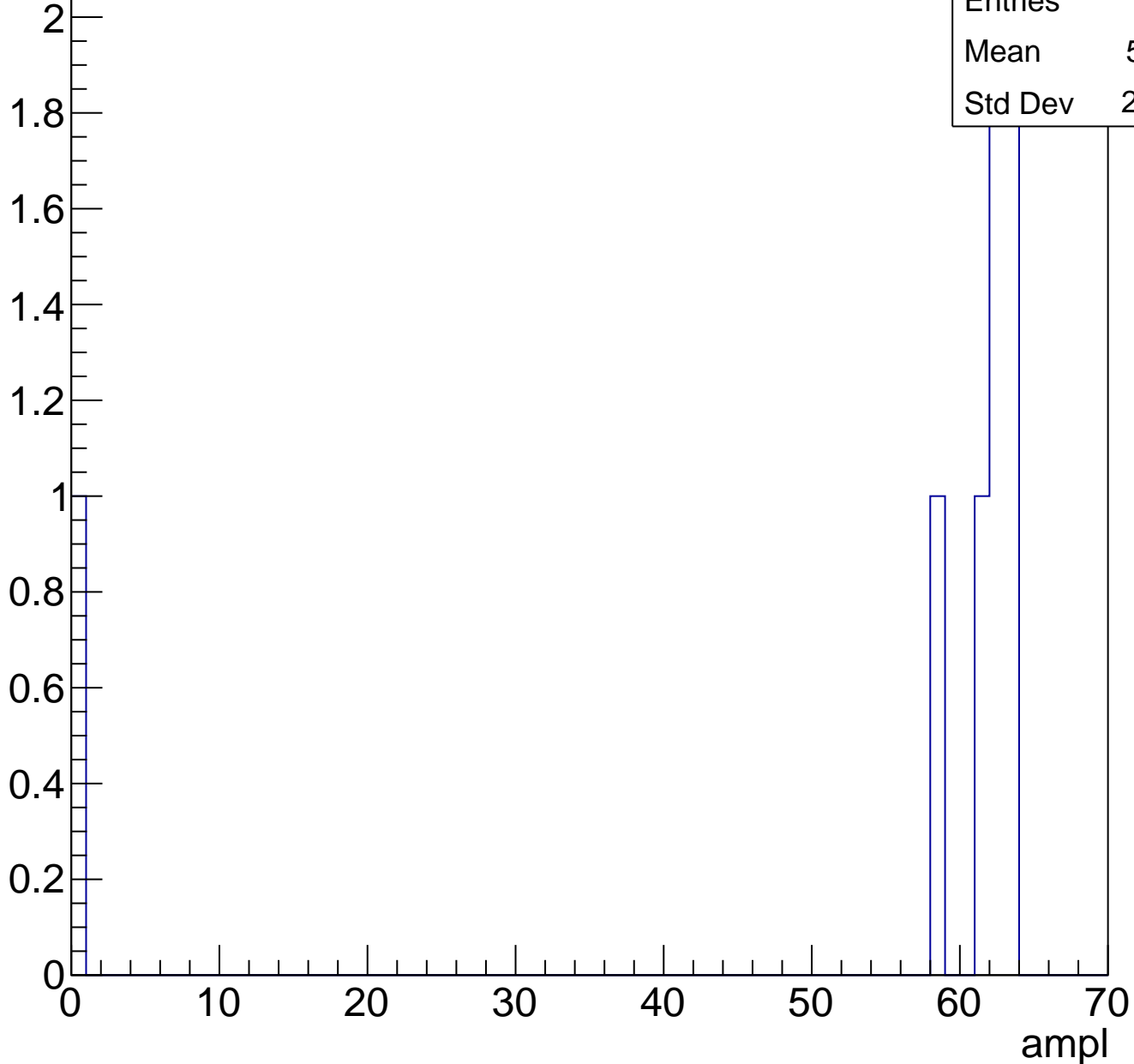
Entry



B0L002S, U9-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch79, adc0

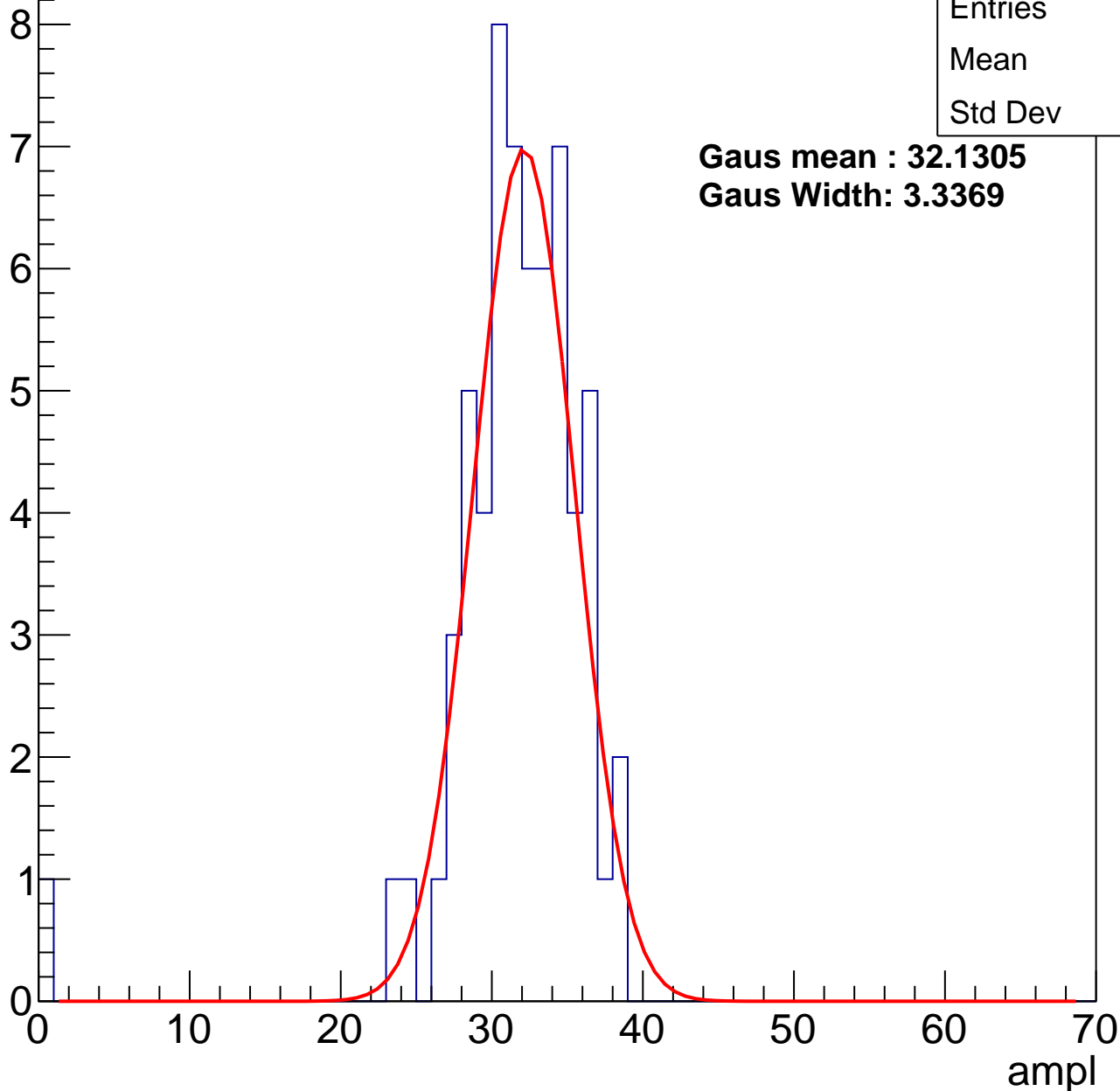
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.1
Std Dev	5.13

Gaus mean : 32.1305

Gaus Width: 3.3369



B0L002S, U9-ch79, adc1

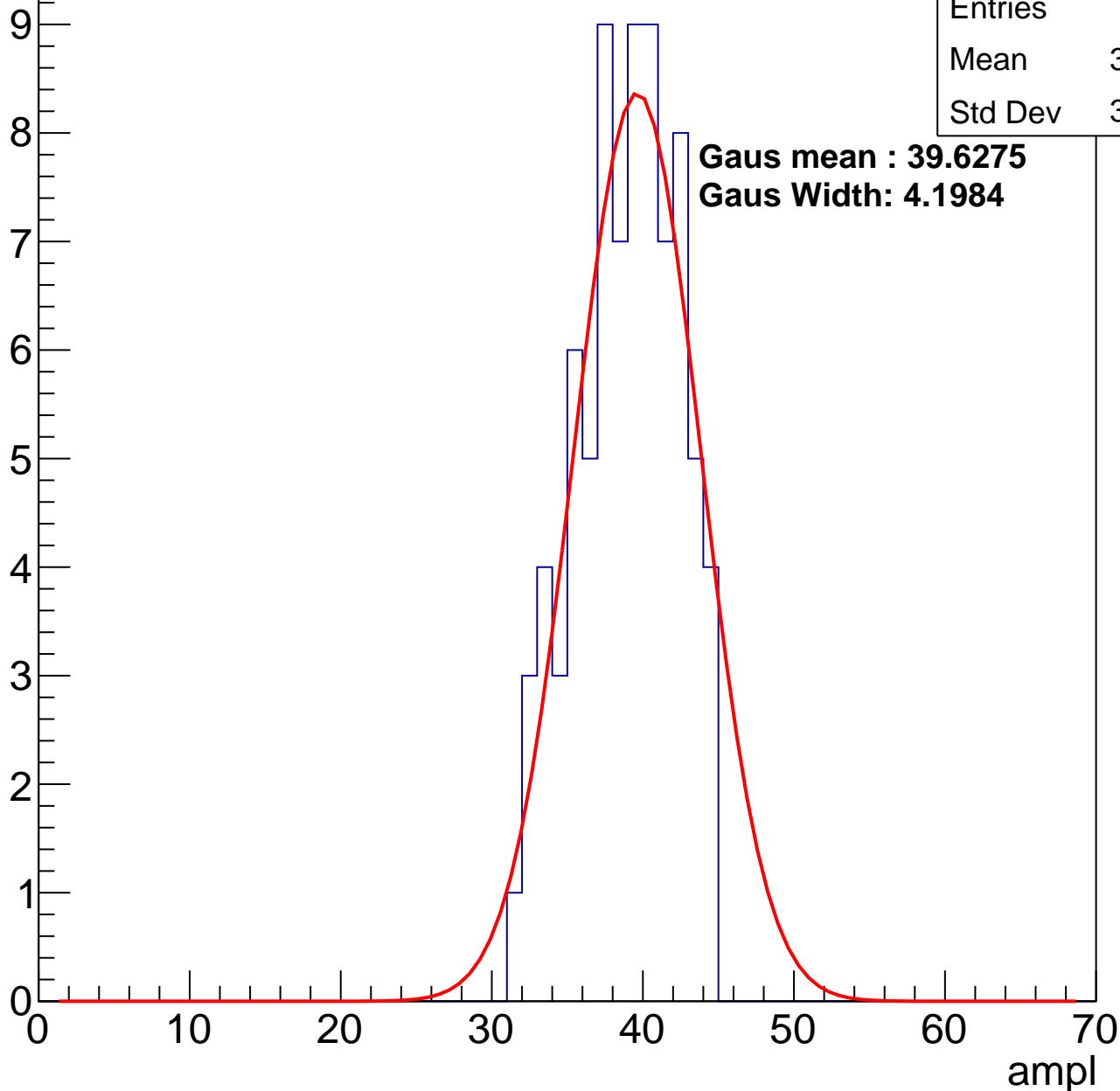
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	38.44
Std Dev	3.312

Gaus mean : 39.6275

Gaus Width: 4.1984



B0L002S, U9-ch79, adc2

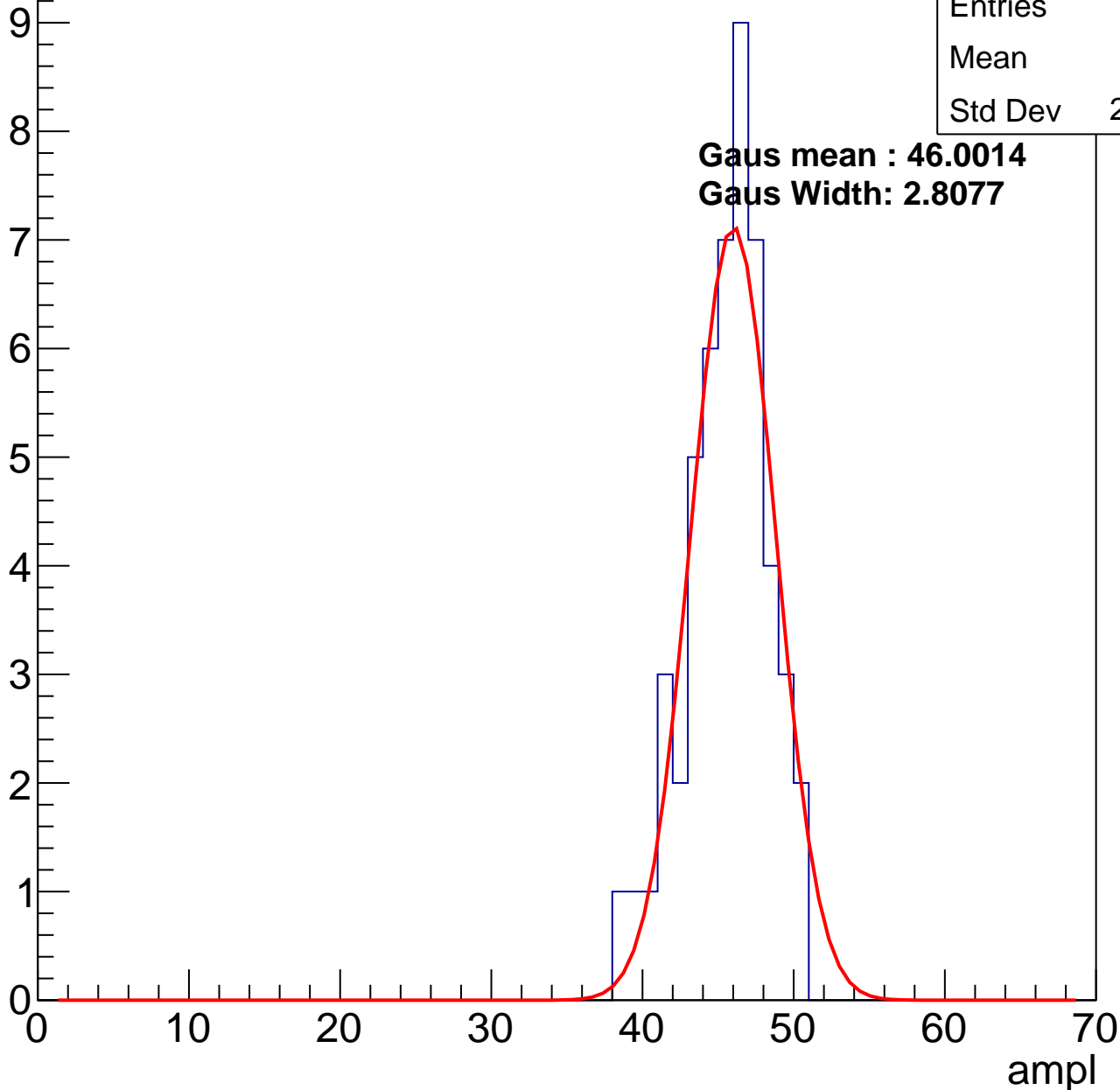
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.1
Std Dev	2.703

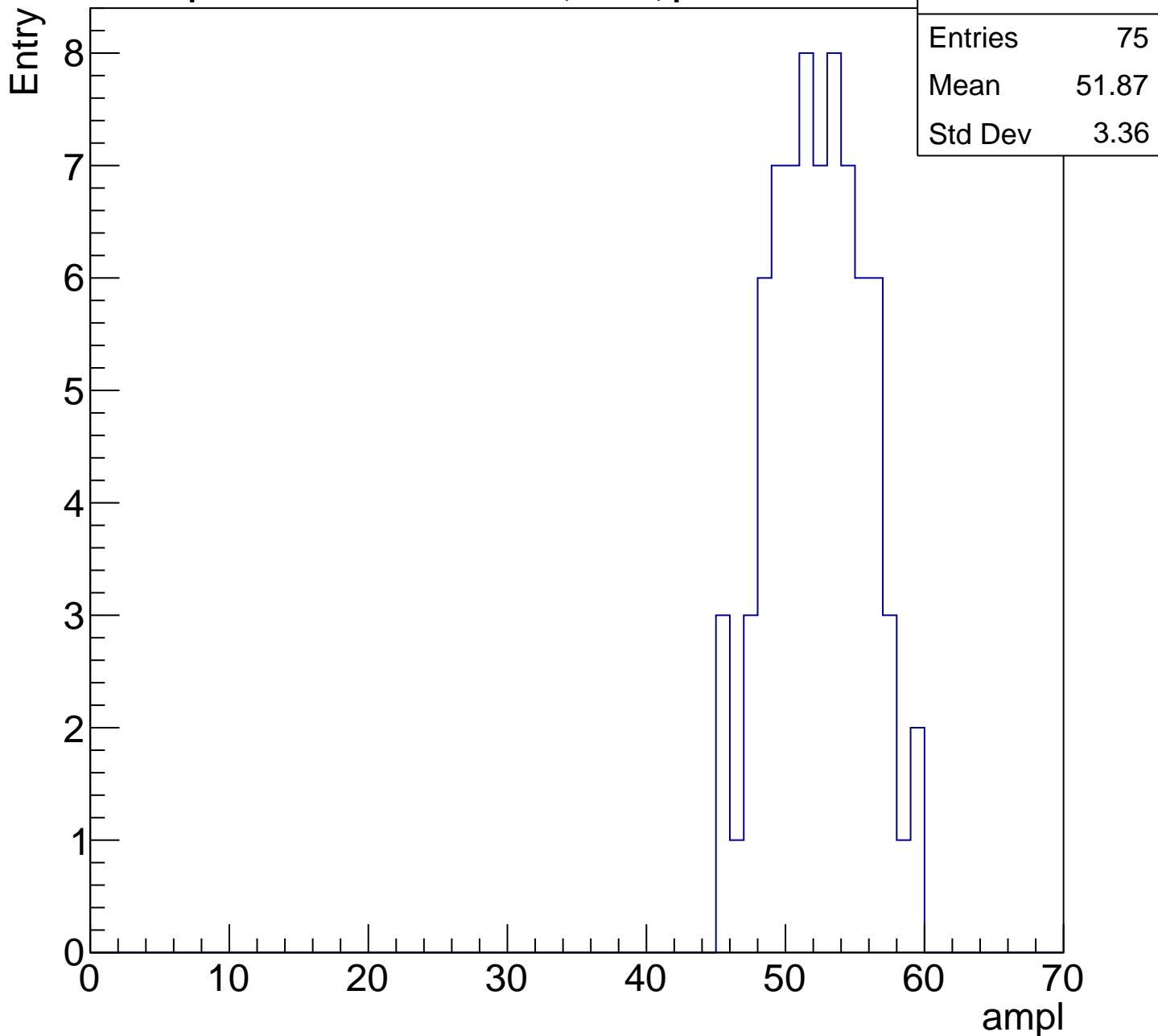
Gaus mean : 46.0014

Gaus Width: 2.8077



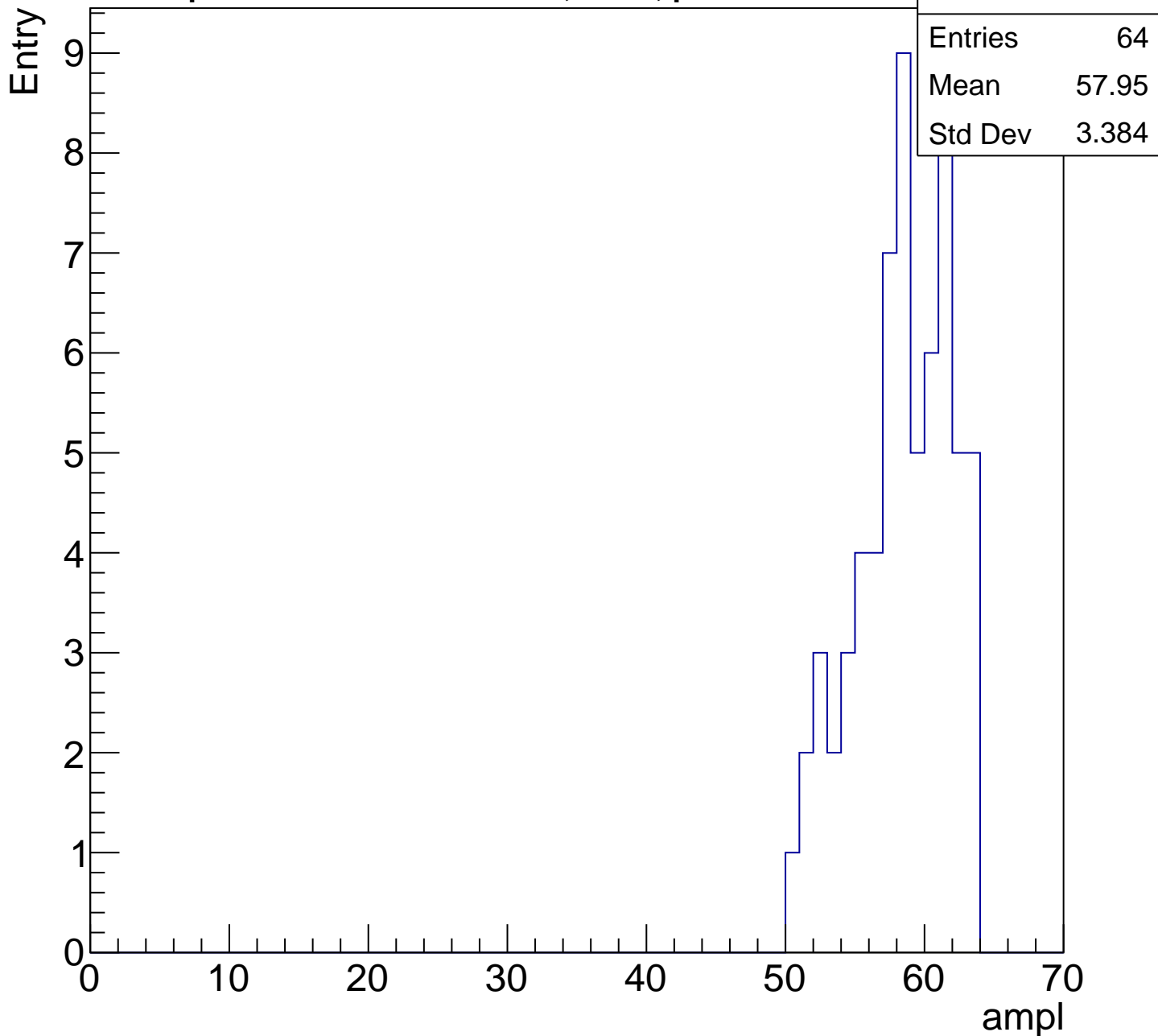
B0L002S, U9-ch79, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

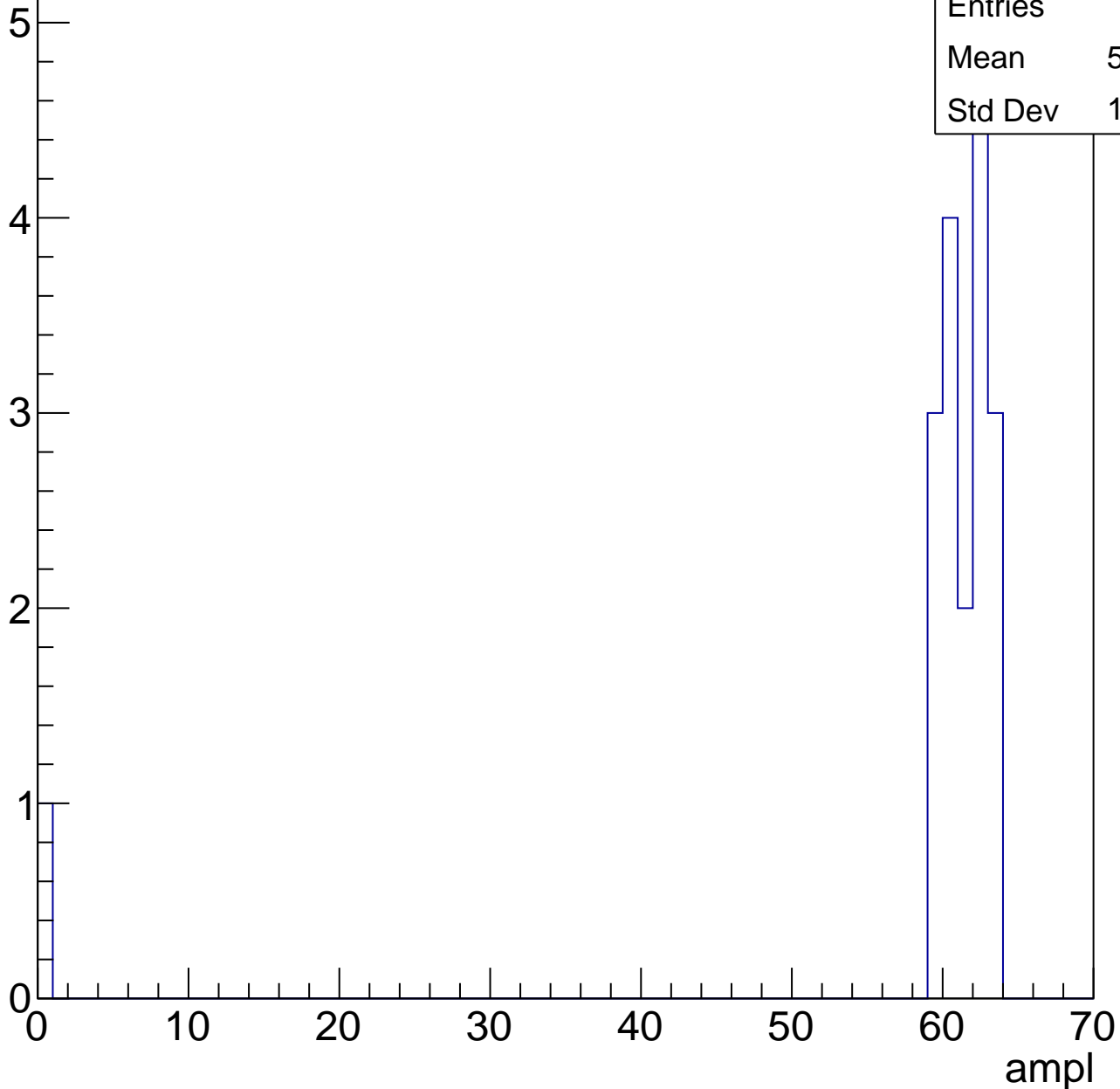


B0L002S, U9-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	18
Mean	57.67
Std Dev	14.05



B0L002S, U9-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch80, adc0

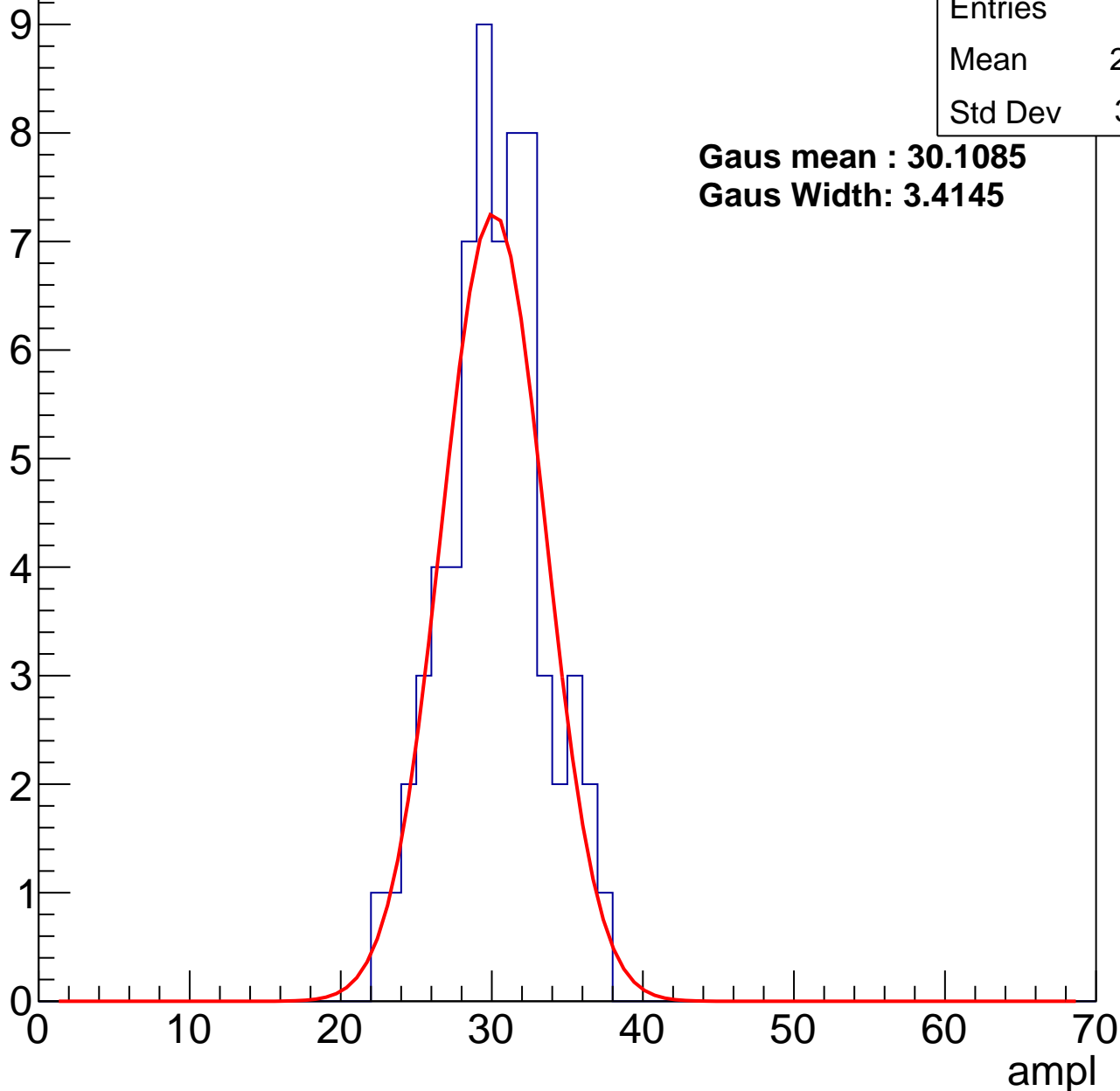
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	29.72
Std Dev	3.251

Gaus mean : 30.1085

Gaus Width: 3.4145



B0L002S, U9-ch80, adc1

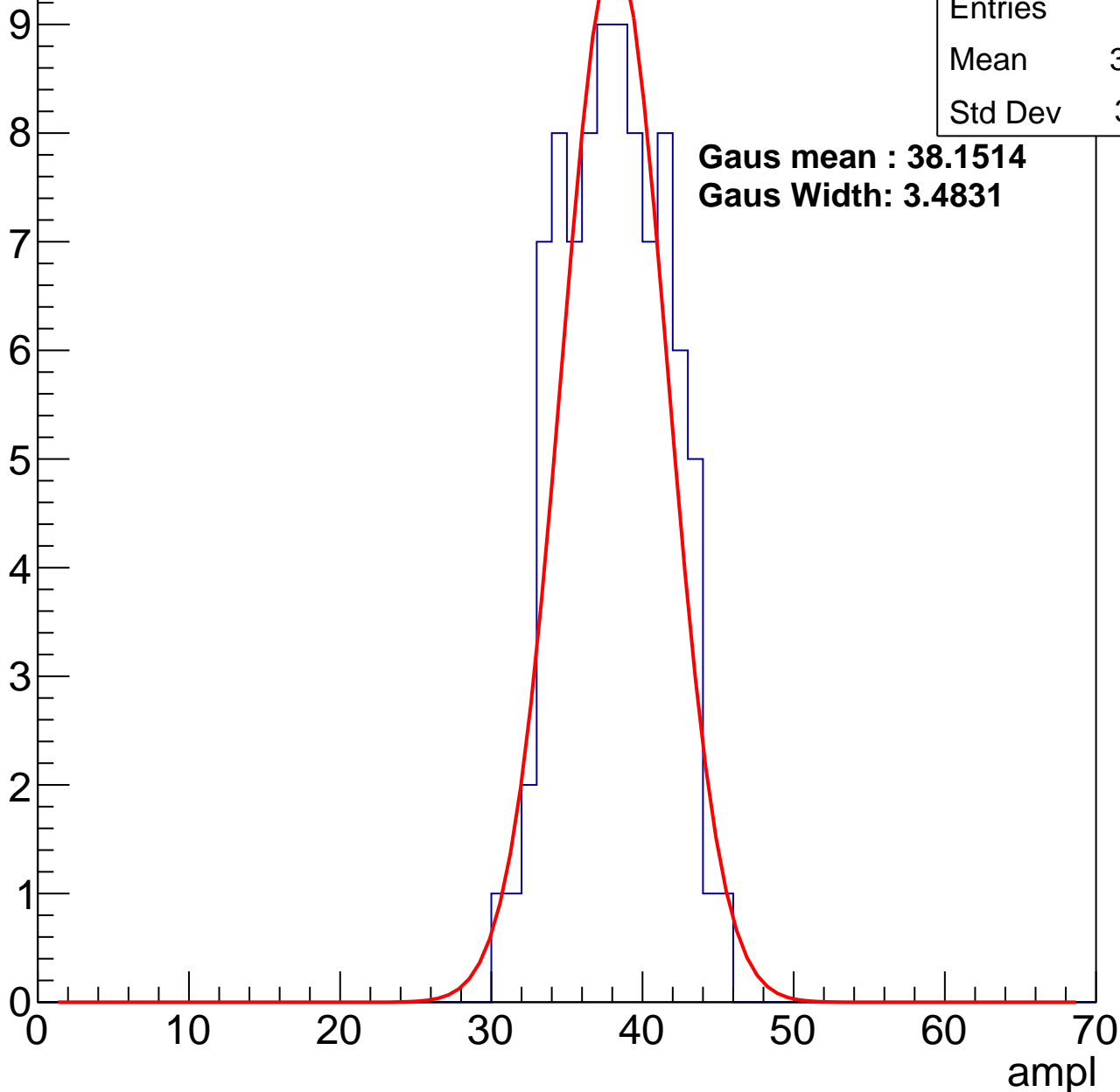
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	88
Mean	37.64
Std Dev	3.361

Gaus mean : 38.1514

Gaus Width: 3.4831



B0L002S, U9-ch80, adc2

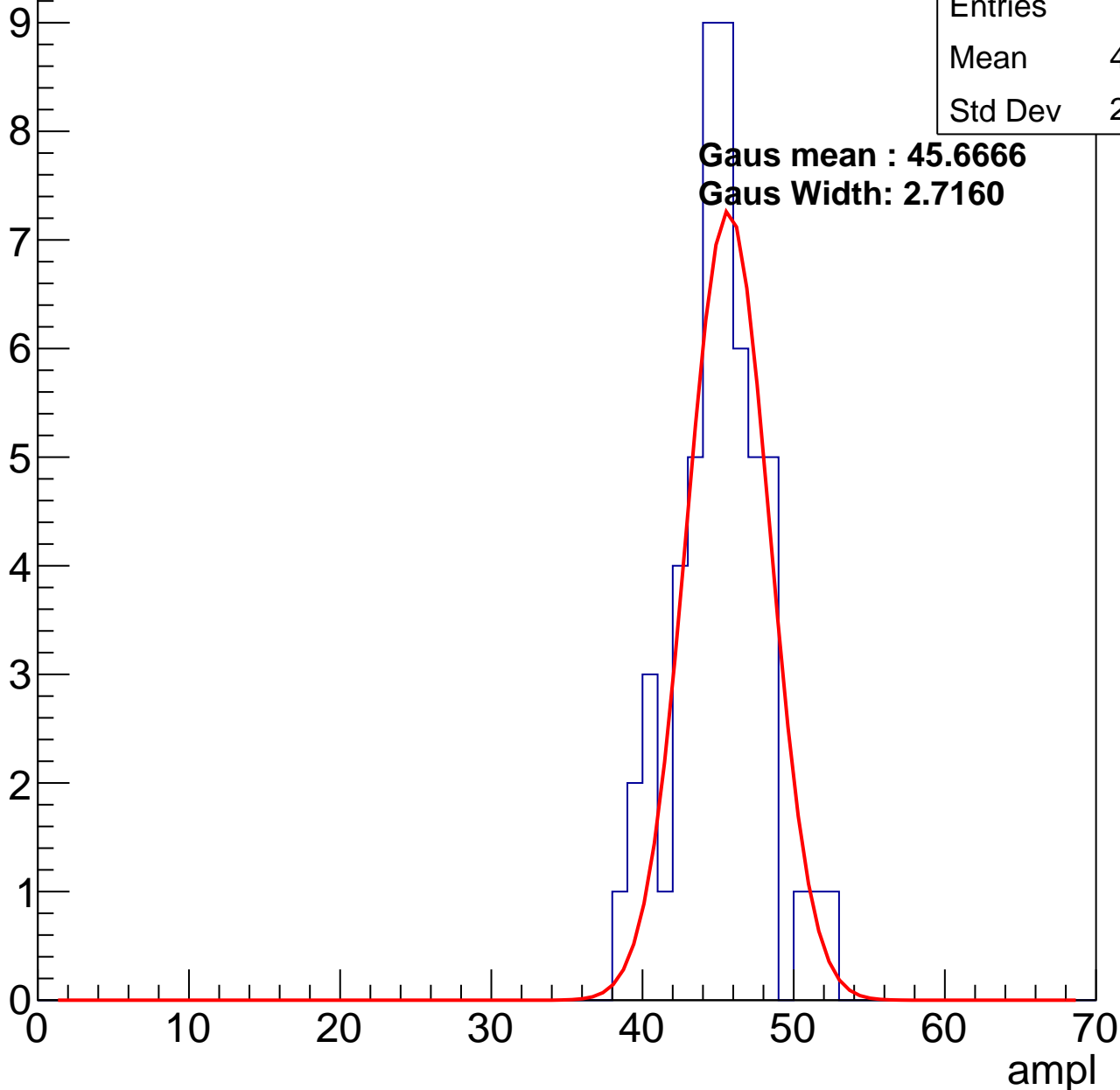
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.62
Std Dev	2.915

Gaus mean : 45.6666

Gaus Width: 2.7160



B0L002S, U9-ch80, adc3

calib_packv5_042523_0143.root, FC#8, port C1

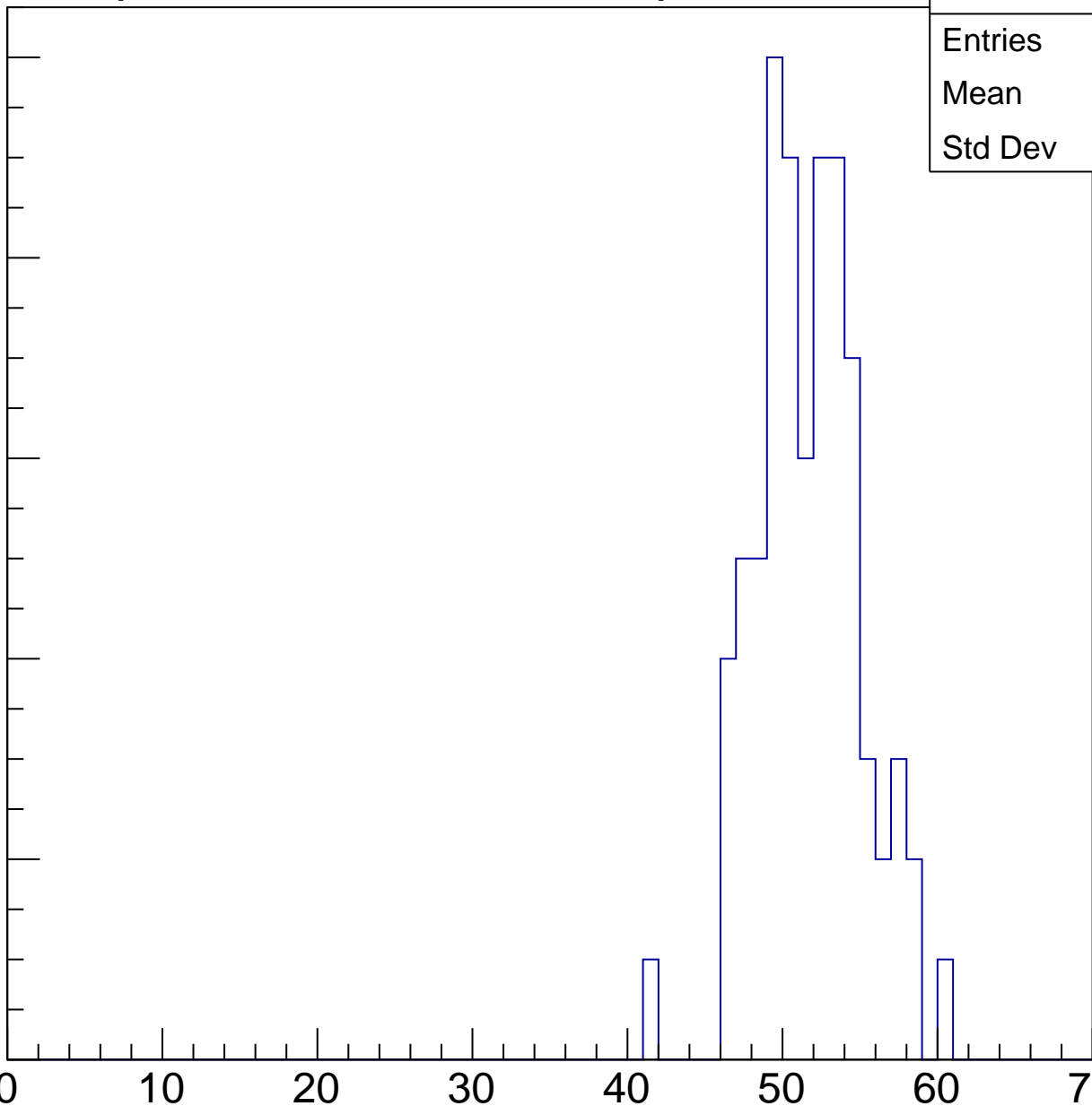
Entries	76
Mean	51.22
Std Dev	3.37

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

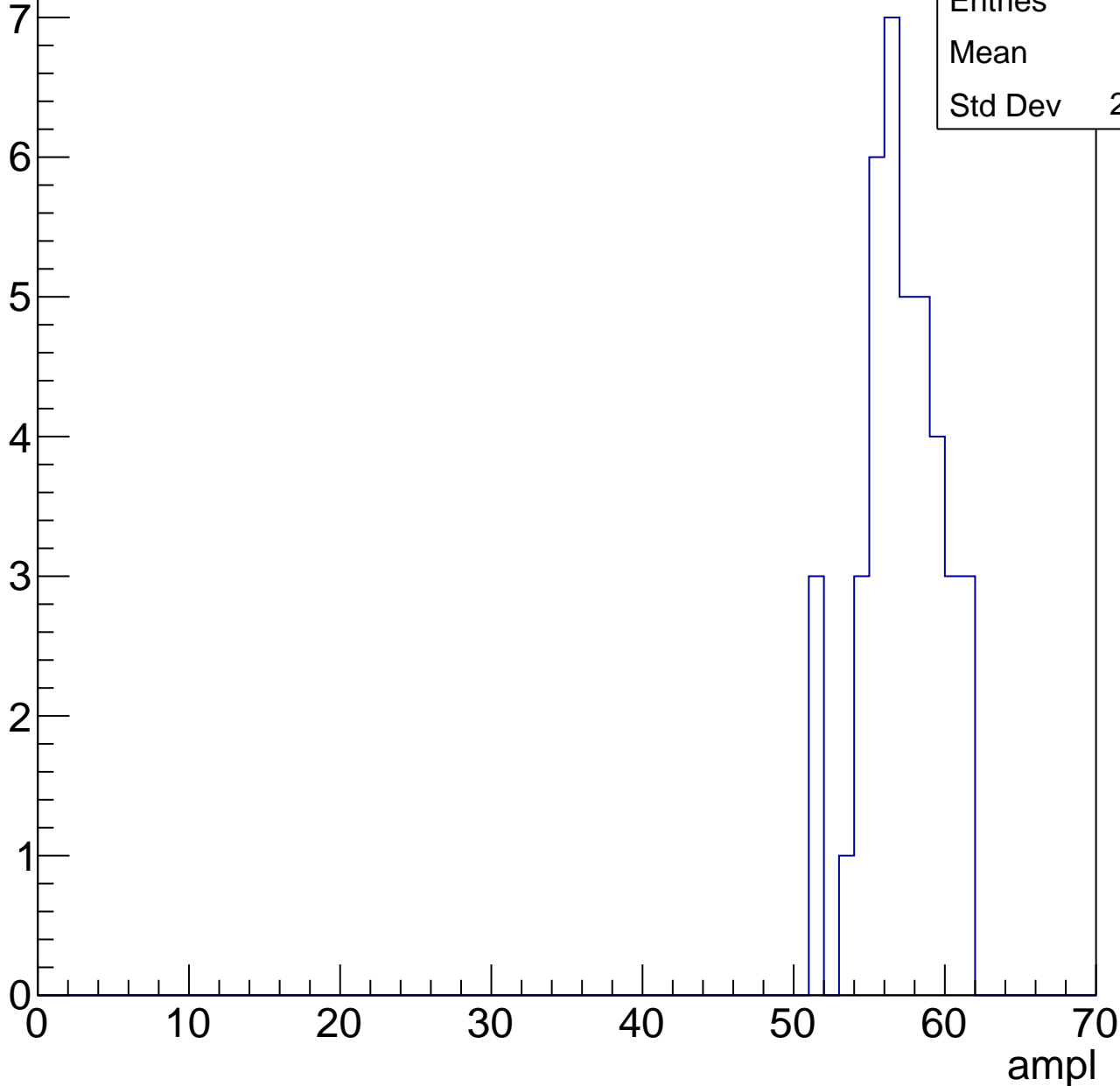


B0L002S, U9-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	56.6
Std Dev	2.606

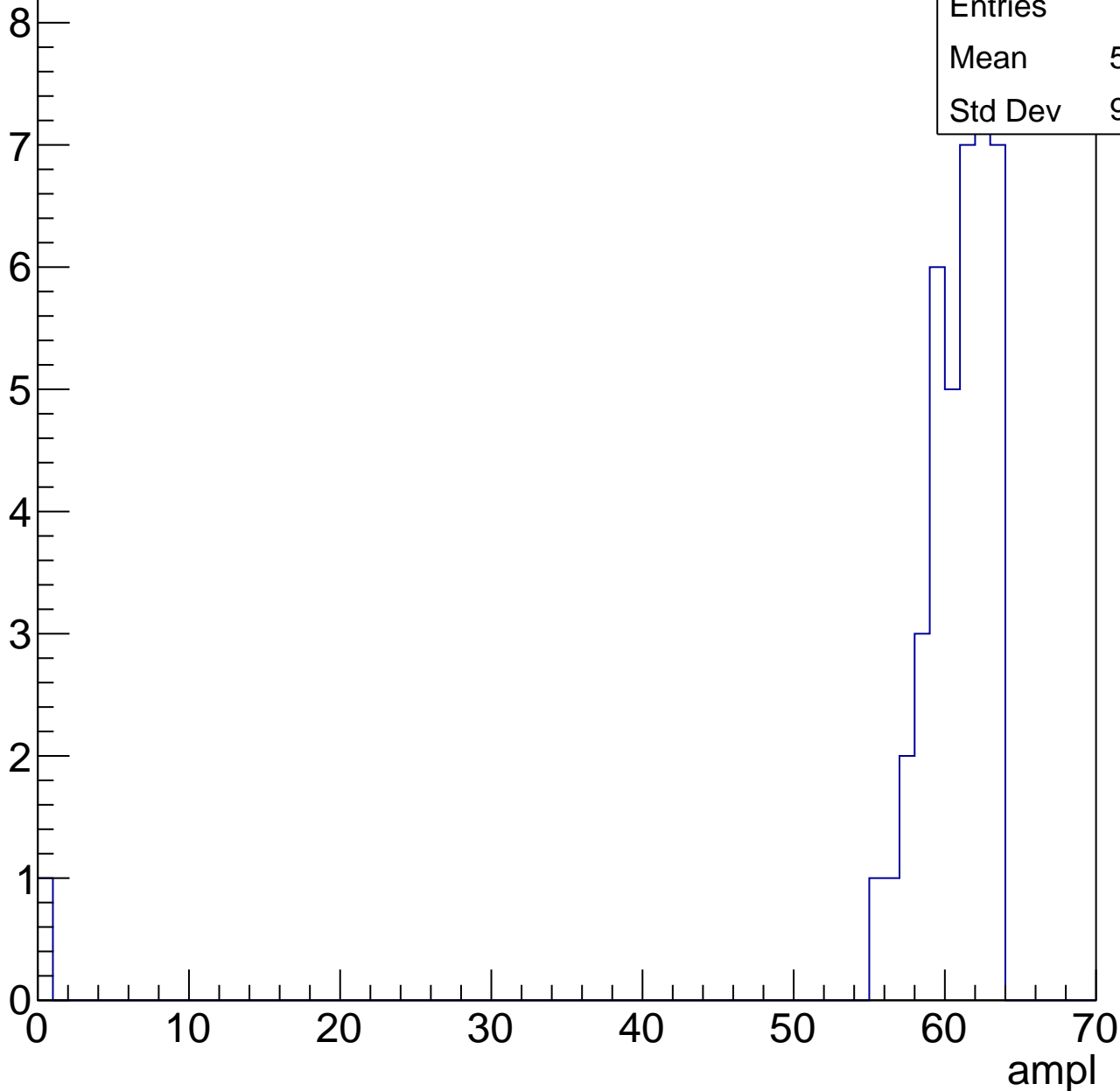


B0L002S, U9-ch80, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.95
Std Dev	9.543



B0L002S, U9-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch81, adc0

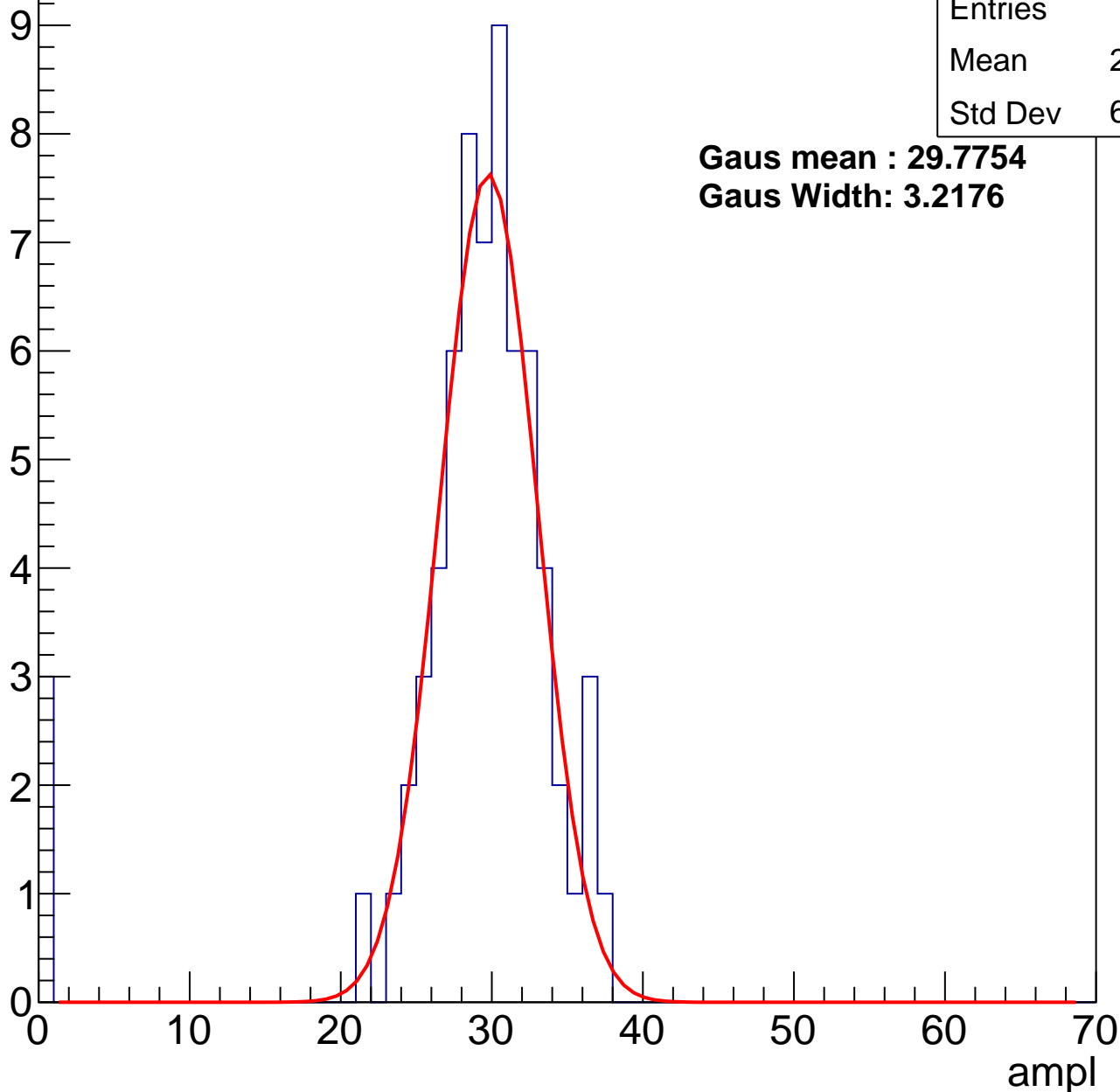
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	28.18
Std Dev	6.902

Gaus mean : 29.7754

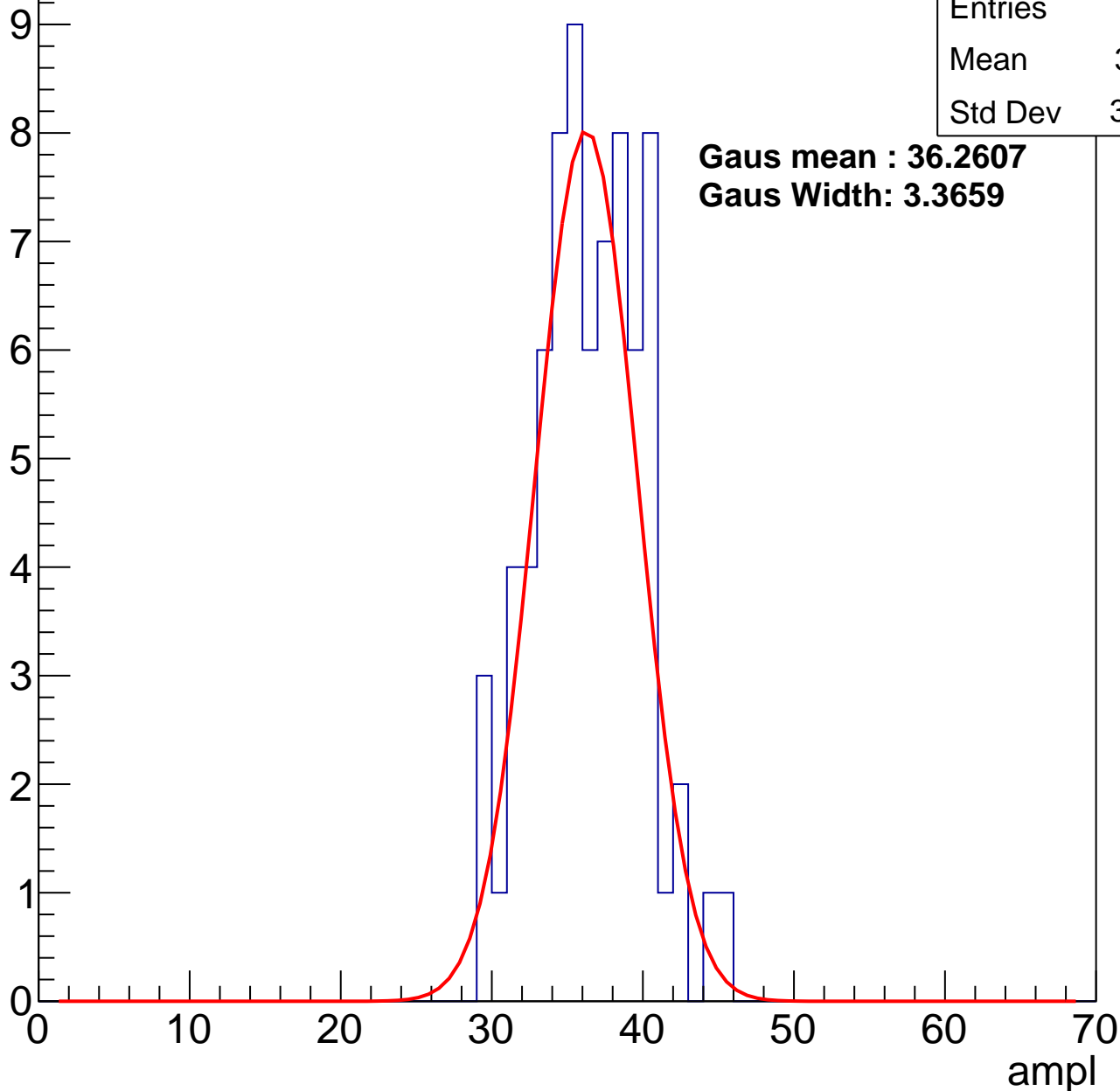
Gaus Width: 3.2176



B0L002S, U9-ch81, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch81, adc2

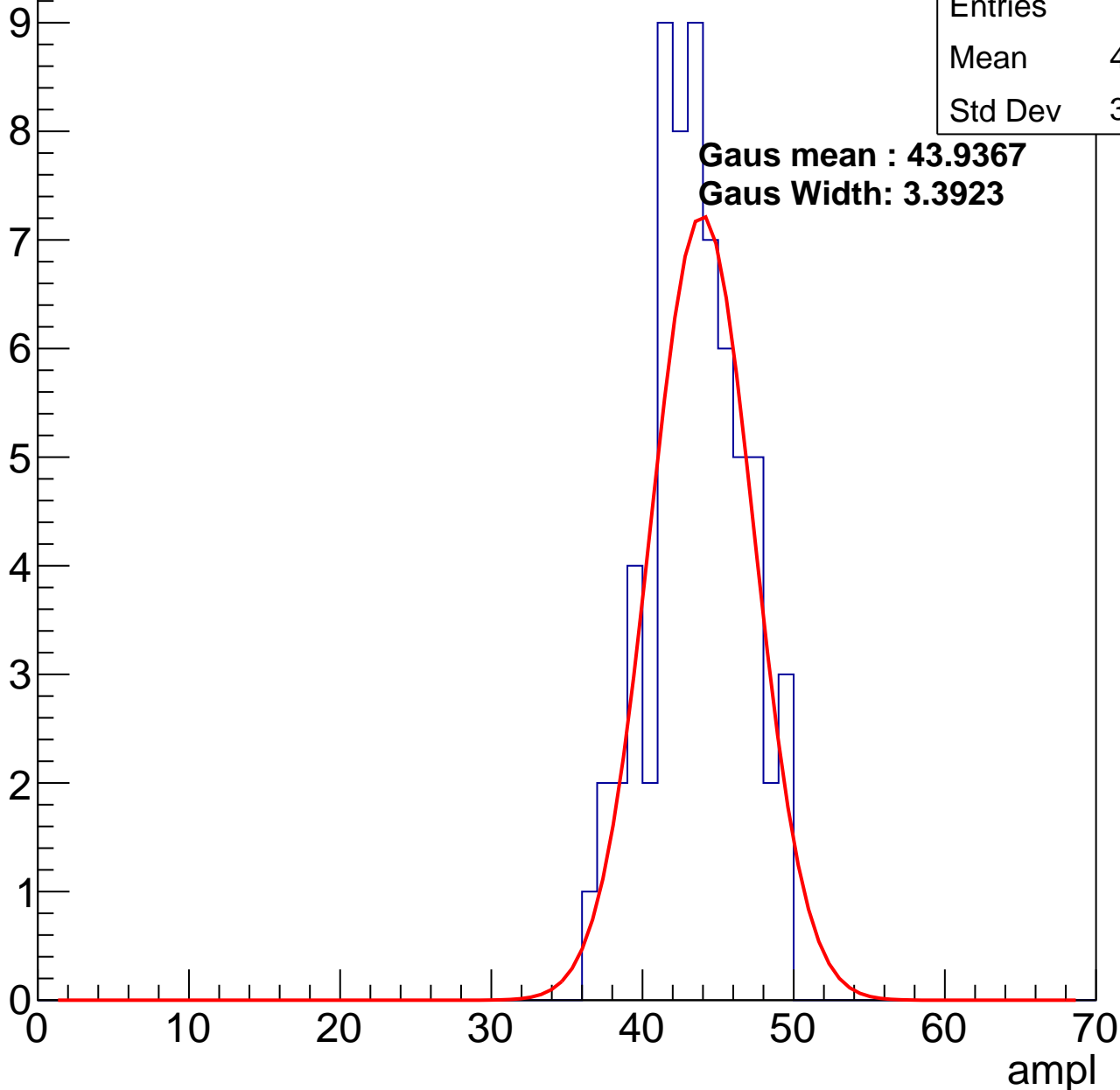
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.08
Std Dev	3.065

Gaus mean : 43.9367

Gaus Width: 3.3923

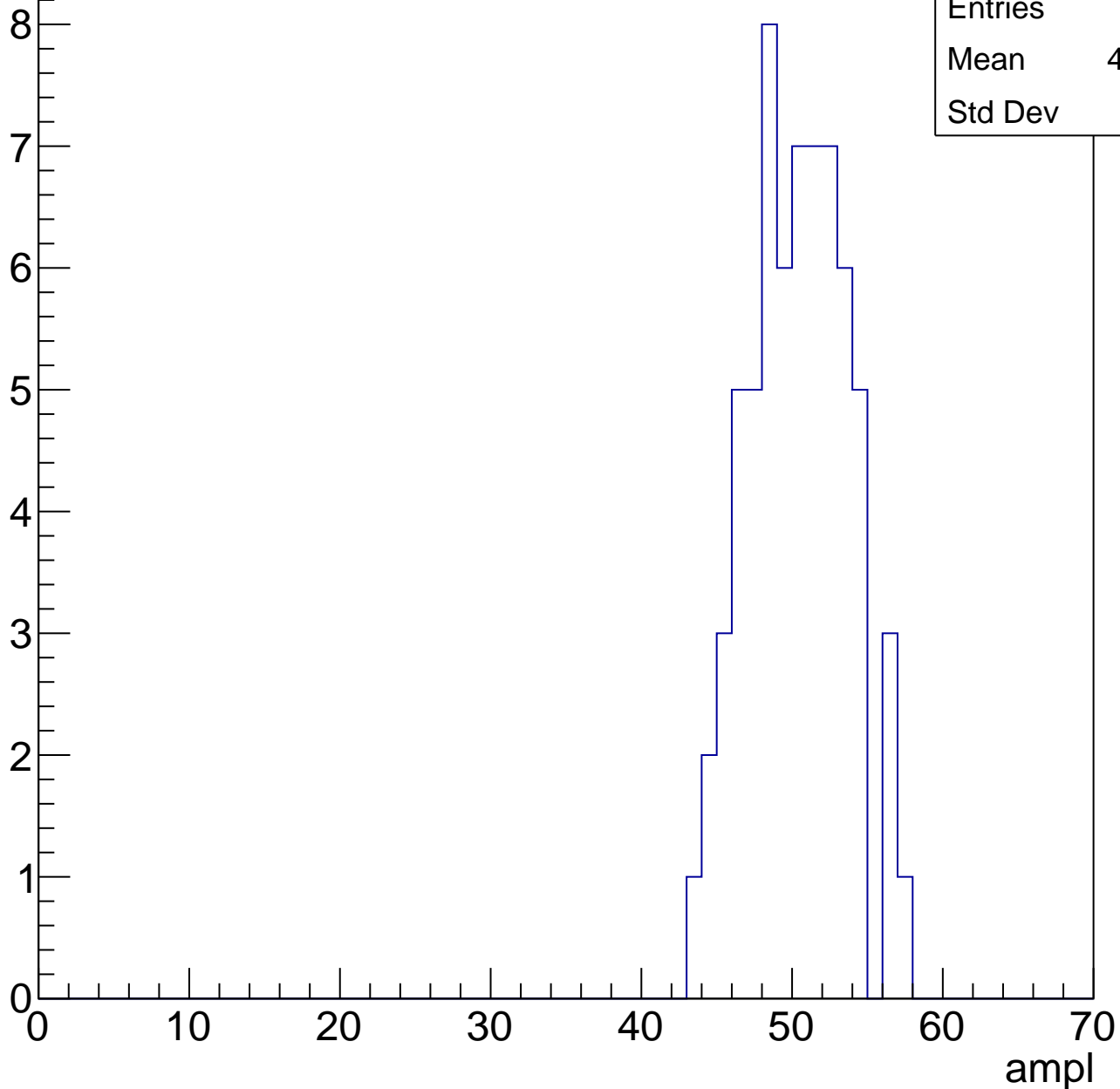


B0L002S, U9-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	49.89
Std Dev	3.22

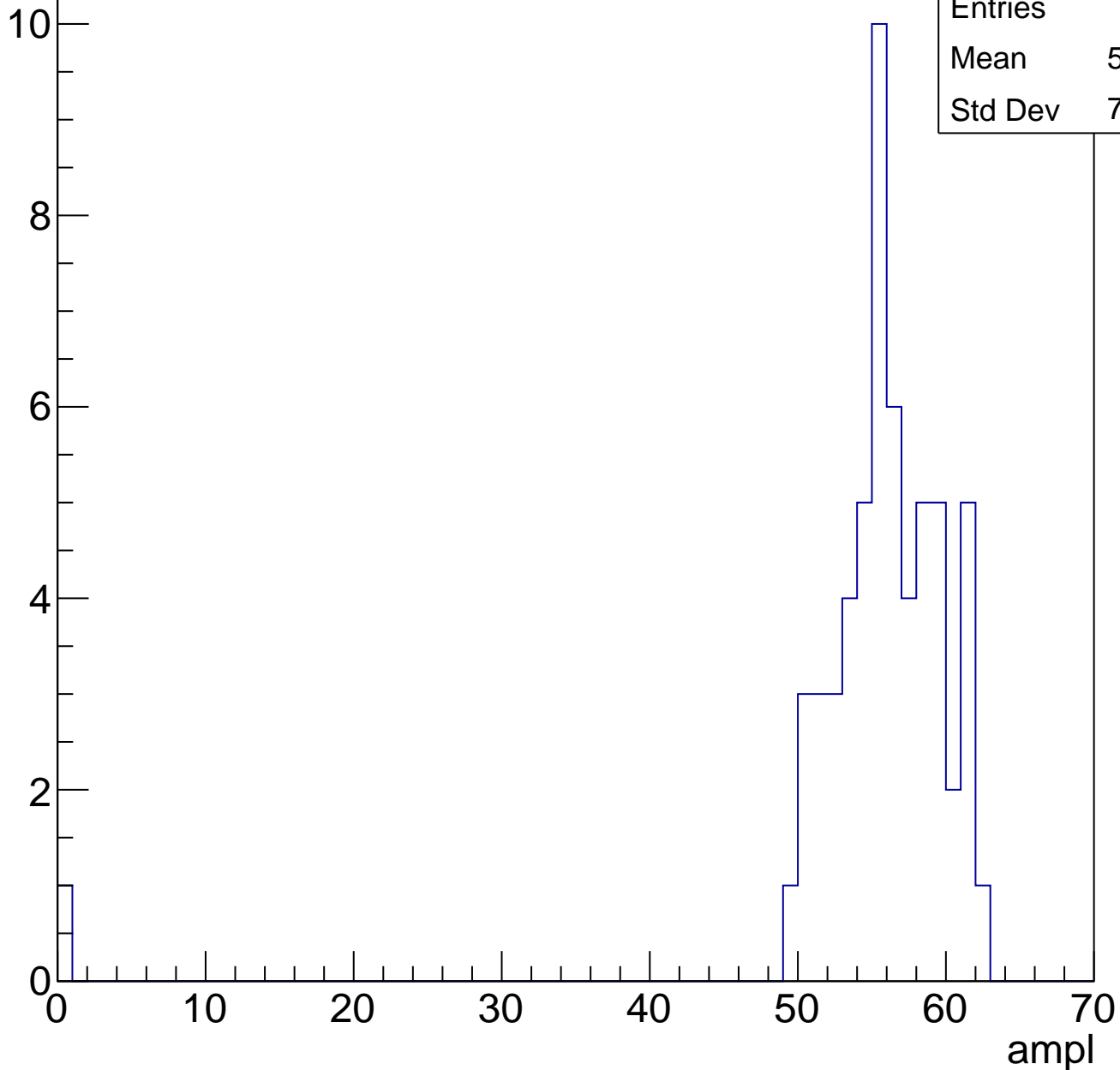


B0L002S, U9-ch81, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	58
Mean	54.76
Std Dev	7.938

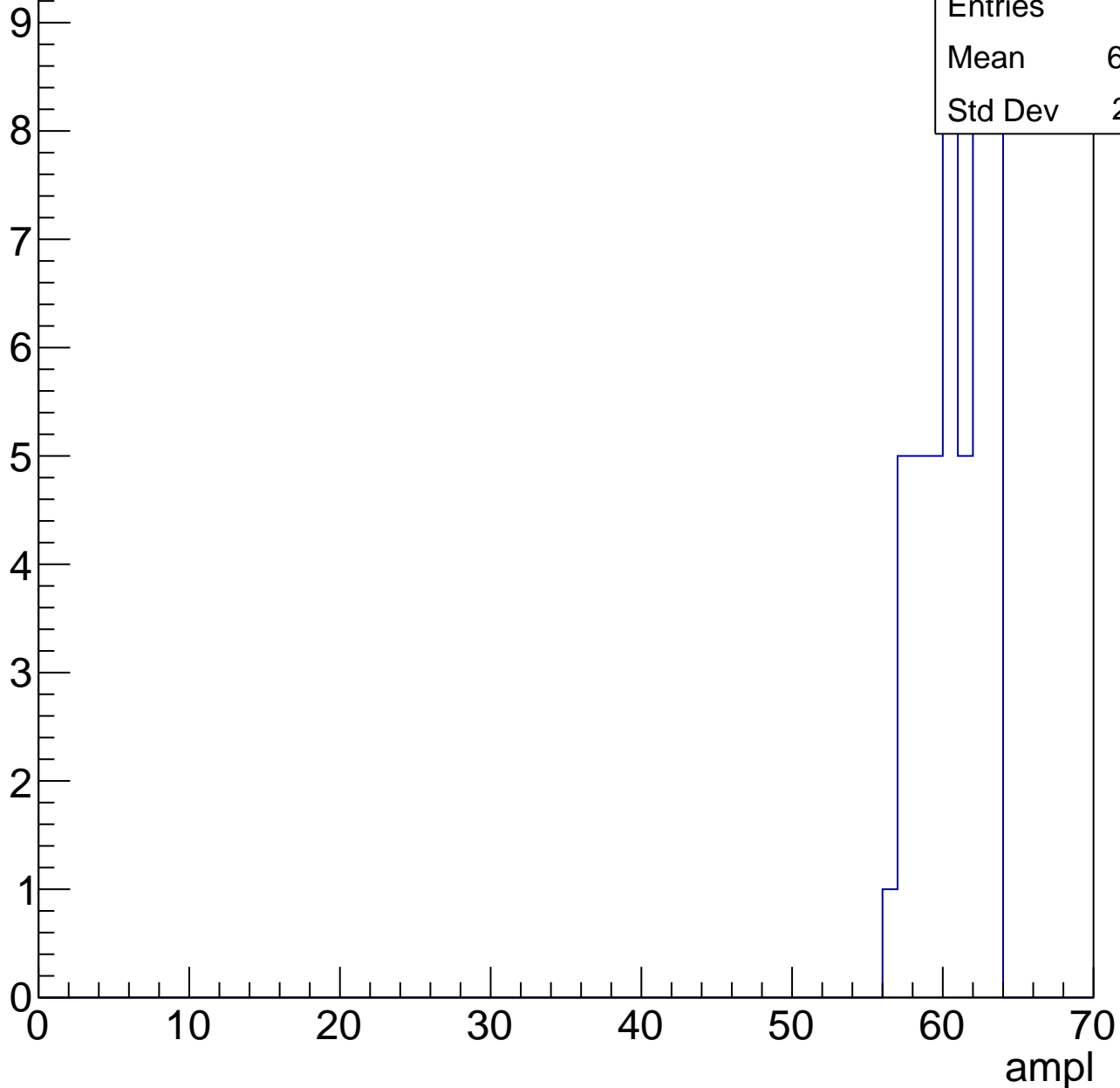
Entry



B0L002S, U9-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L002S, U9-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch82, adc0

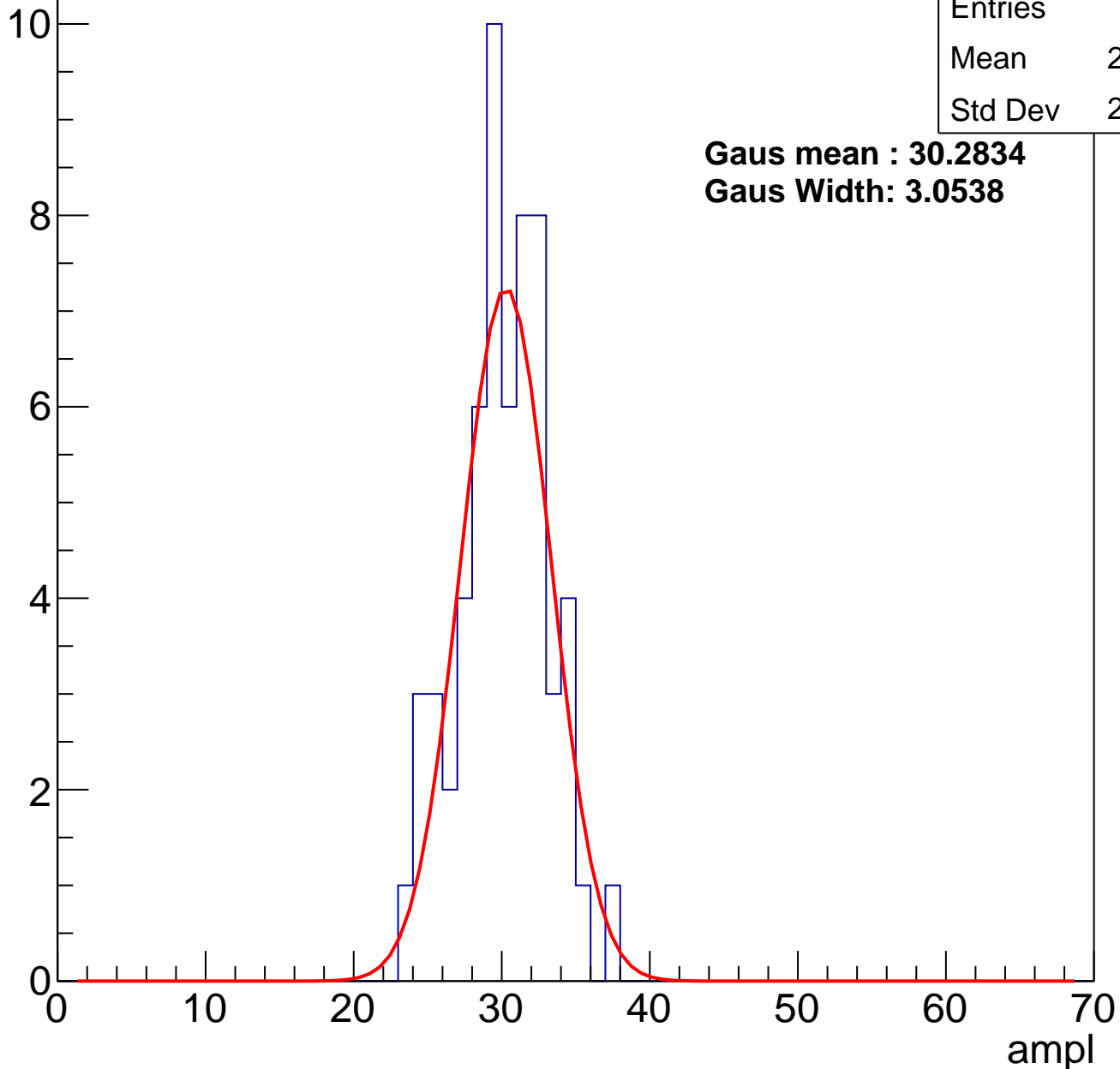
calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	29.65
Std Dev	2.977

Gaus mean : 30.2834

Gaus Width: 3.0538

Entry



B0L002S, U9-ch82, adc1

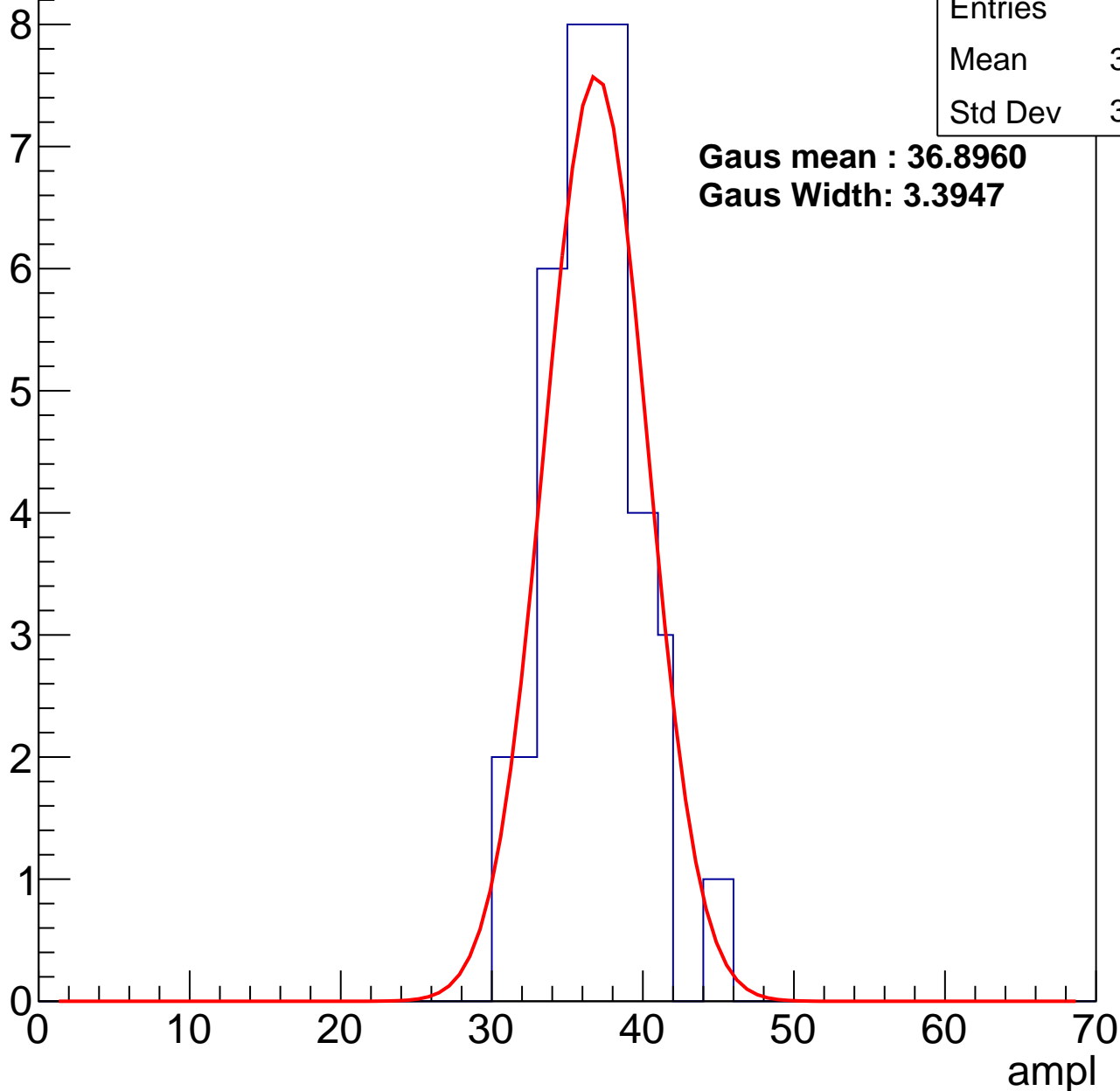
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.25
Std Dev	3.076

Gaus mean : 36.8960

Gaus Width: 3.3947



B0L002S, U9-ch82, adc2

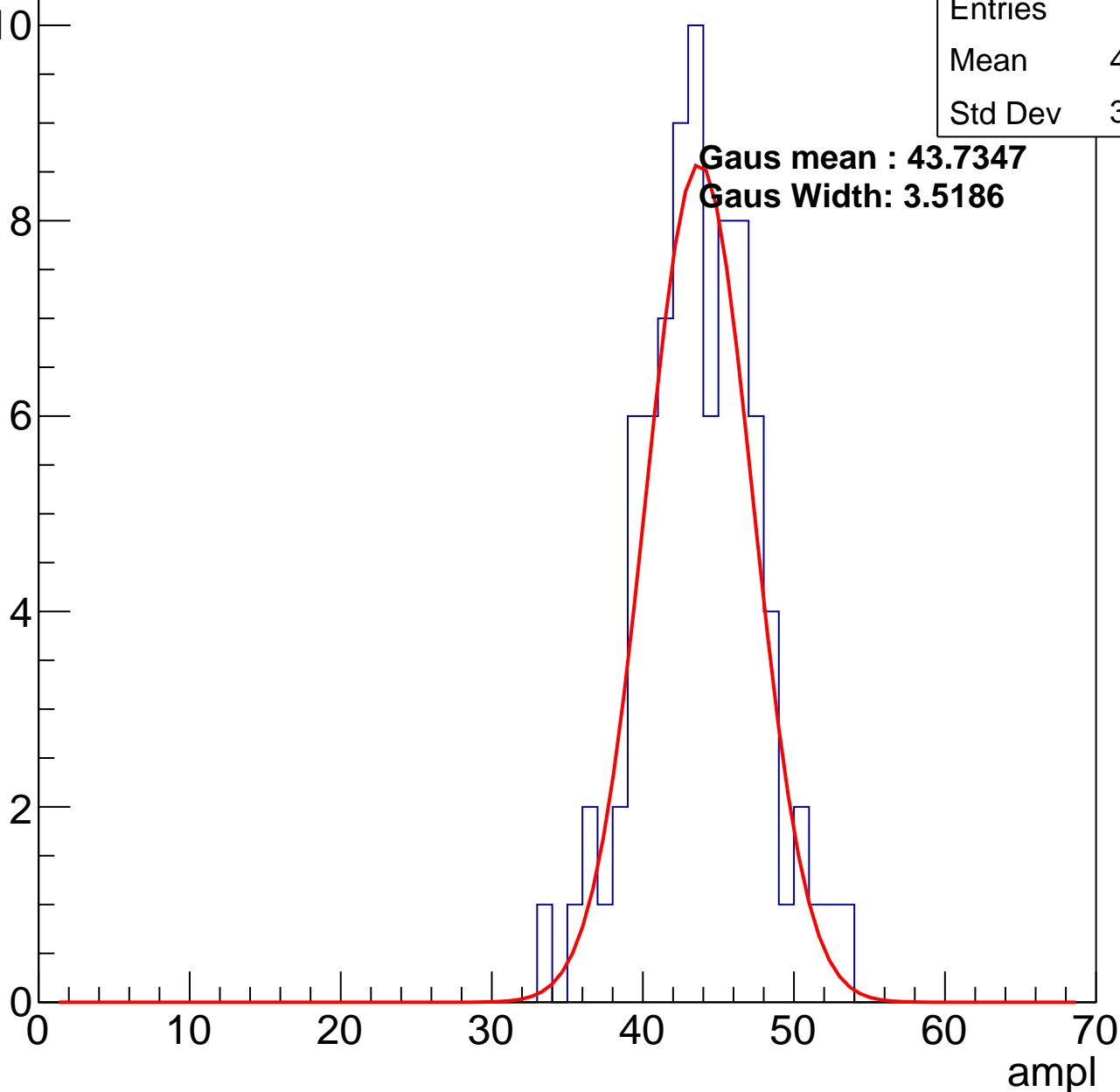
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	43.29
Std Dev	3.823

Gaus mean : 43.7347

Gaus Width: 3.5186

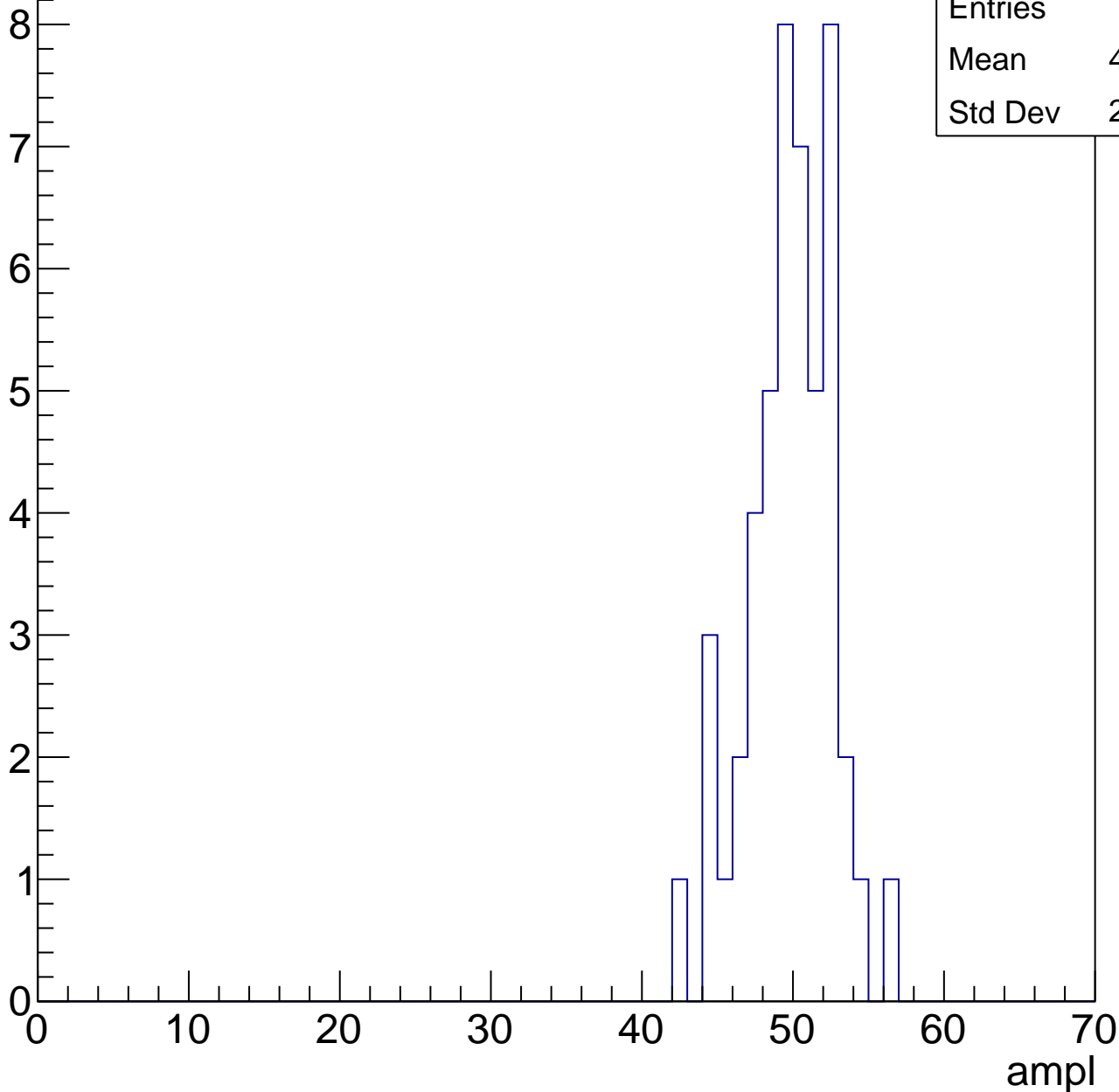


B0L002S, U9-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

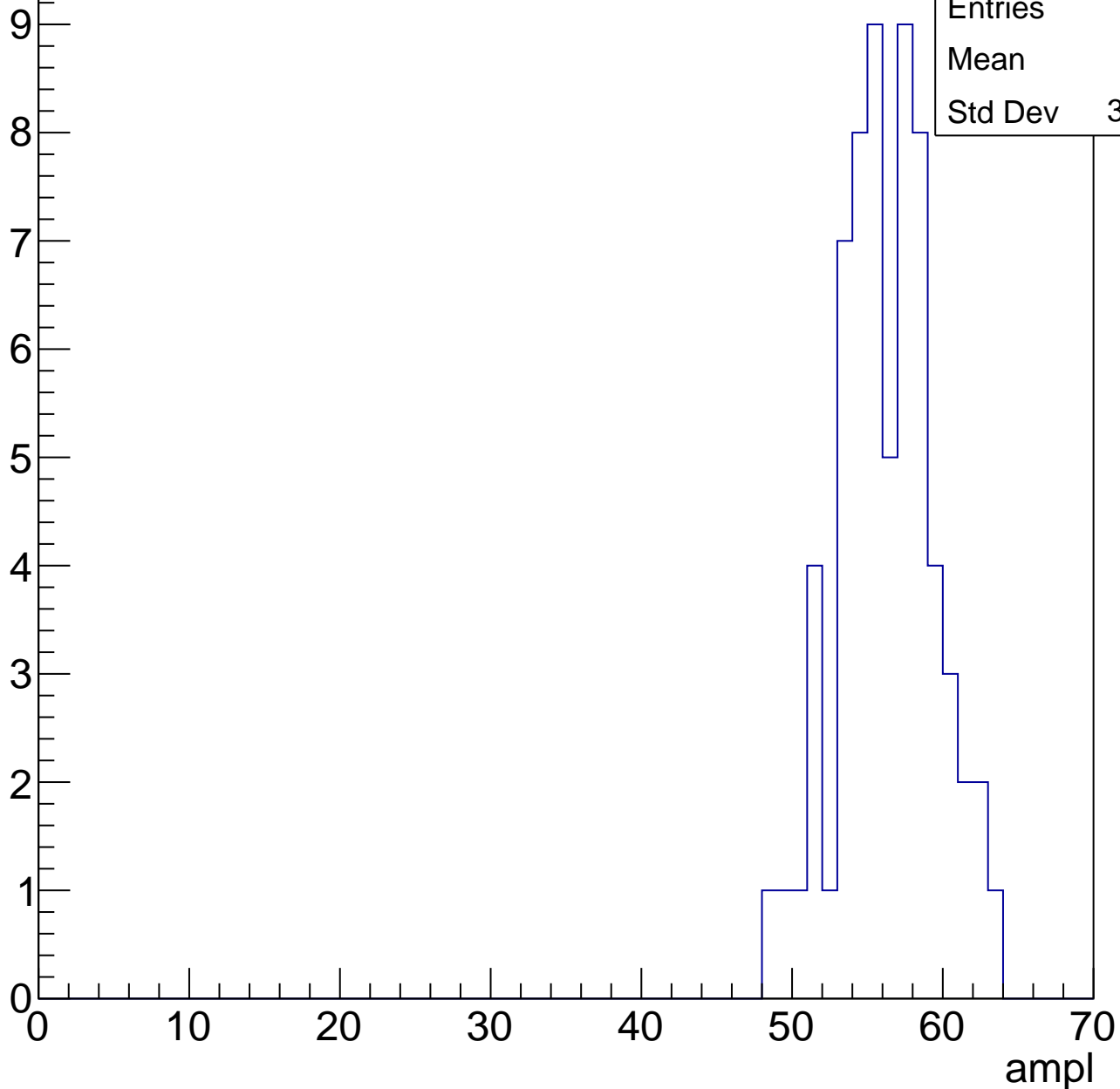
Entries	48
Mean	49.33
Std Dev	2.809



B0L002S, U9-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



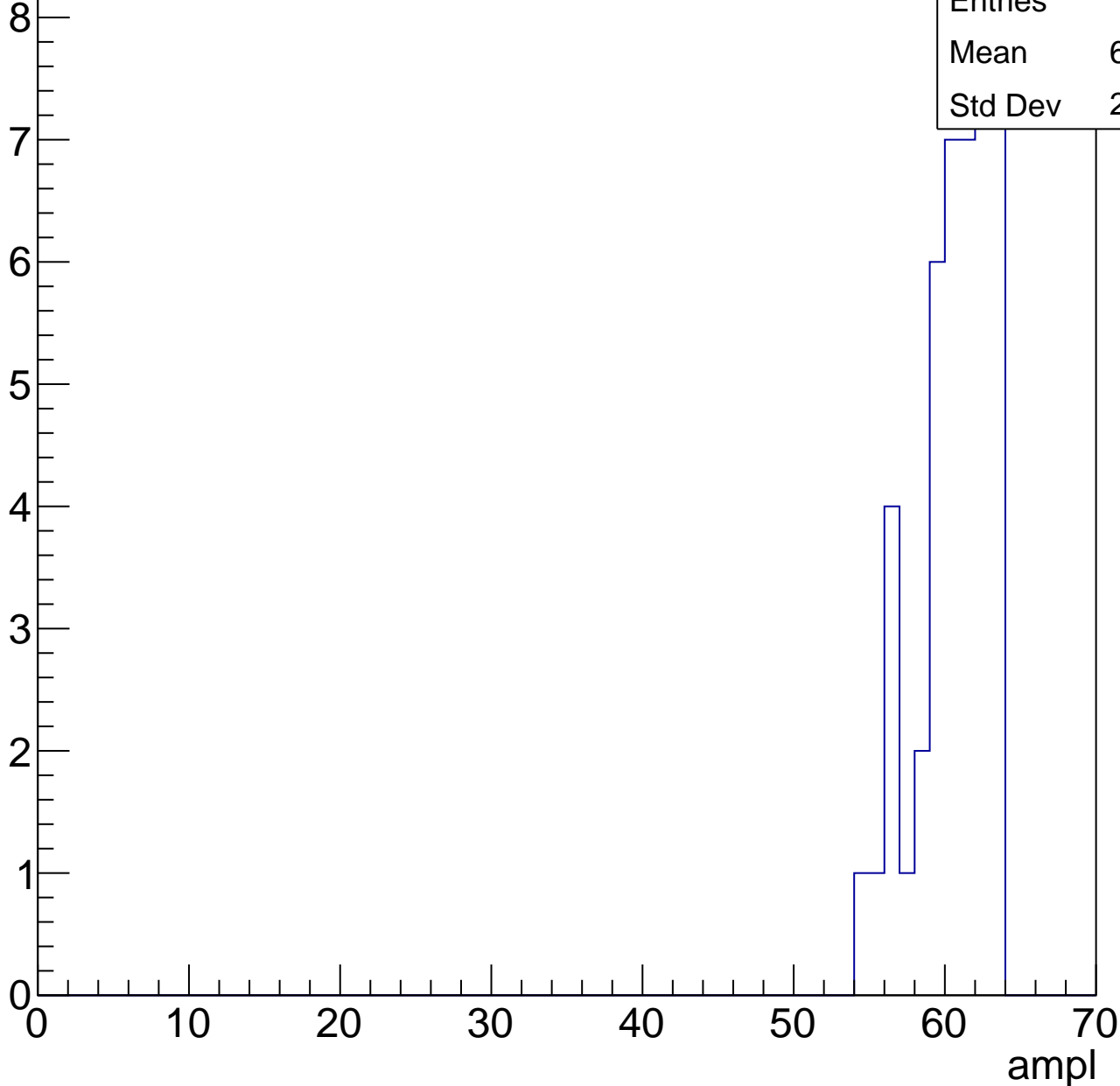
Entries	66
Mean	55.8
Std Dev	3.168

B0L002S, U9-ch82, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

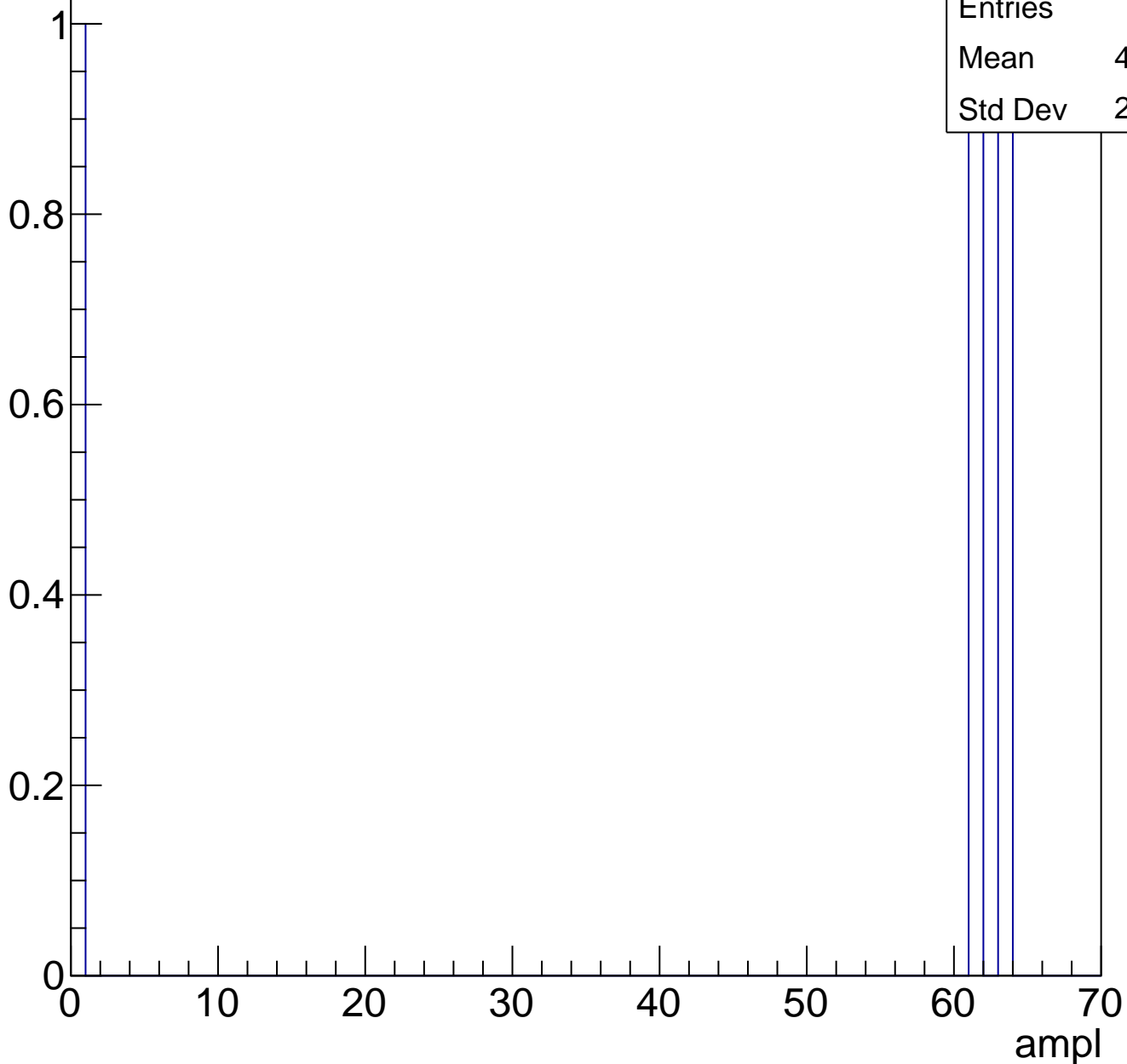
Entries	45
Mean	60.16
Std Dev	2.394



B0L002S, U9-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

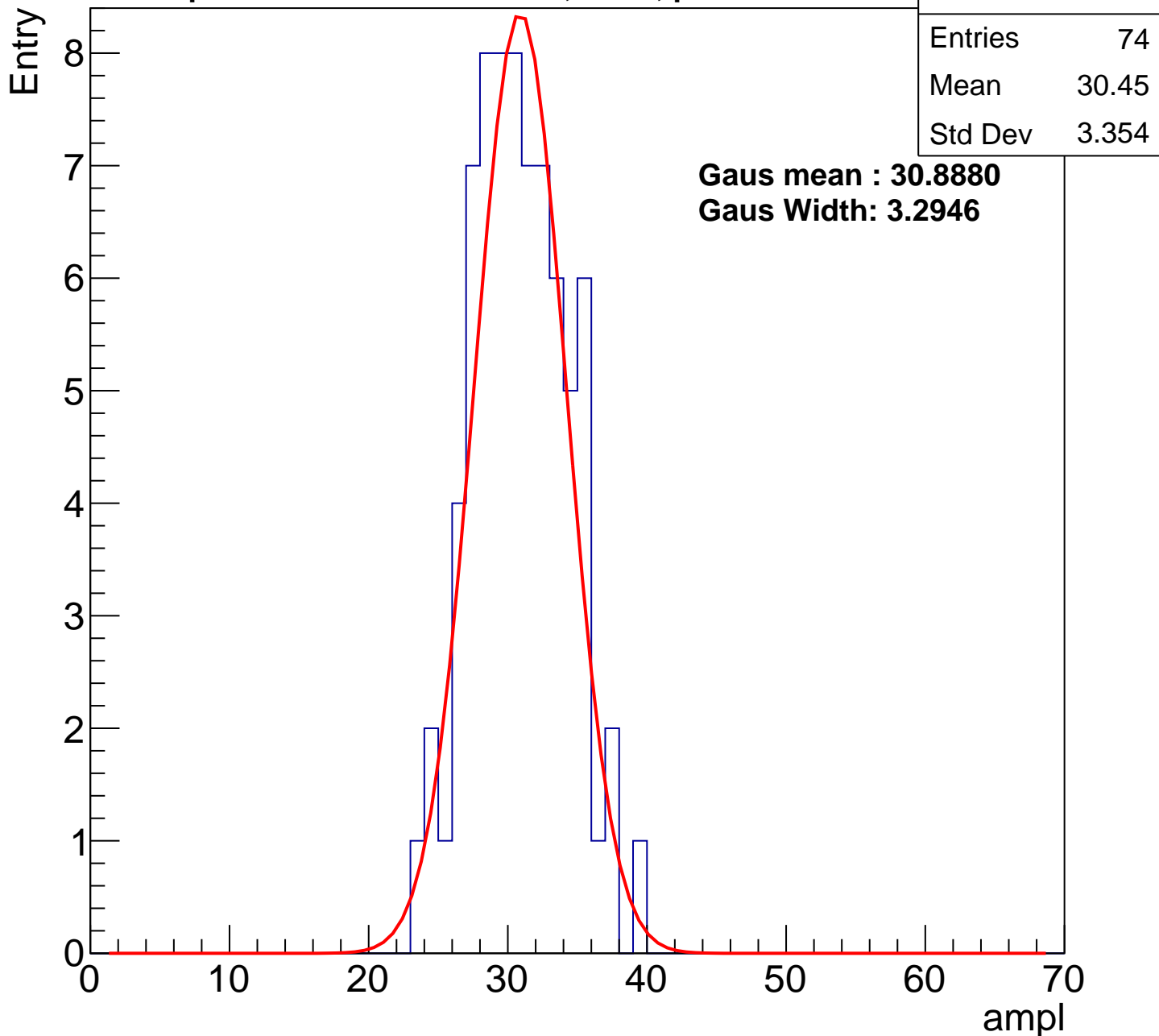
Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch83, adc0

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch83, adc1

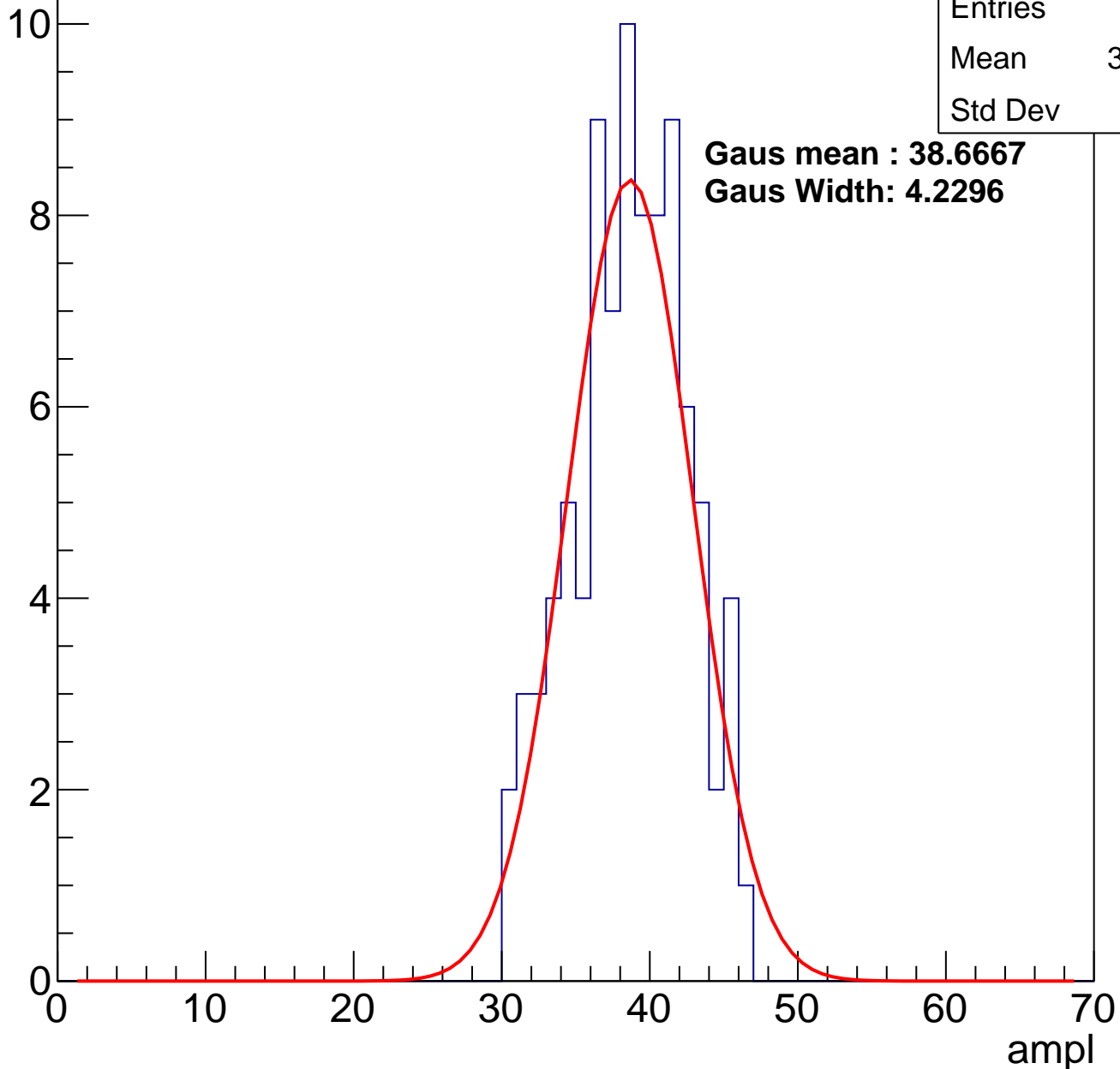
calib_packv5_042523_0143.root, FC#8, port C1

Entries	90
Mean	38.18
Std Dev	3.82

Gaus mean : 38.6667

Gaus Width: 4.2296

Entry



B0L002S, U9-ch83, adc2

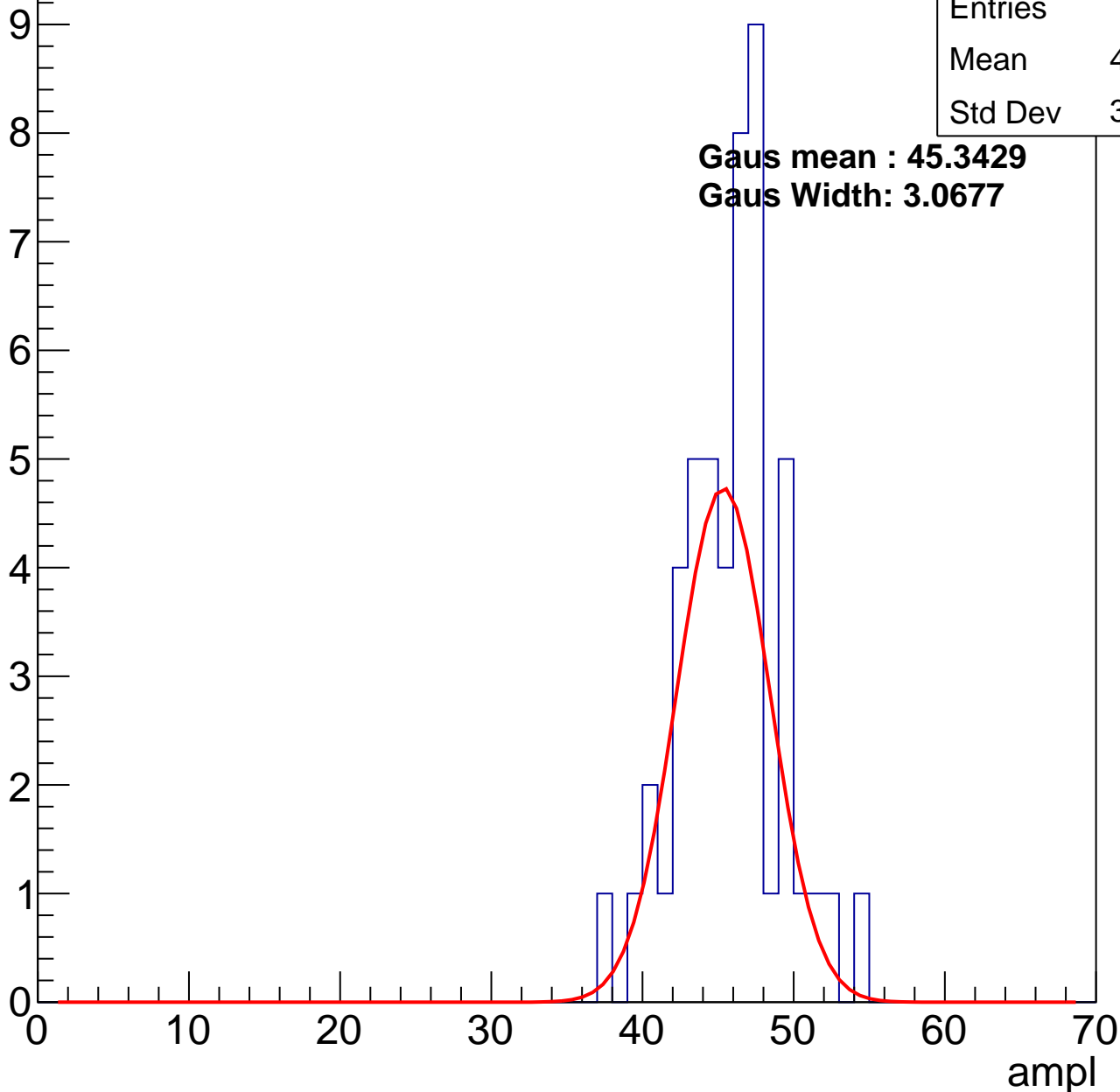
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	45.42
Std Dev	3.293

Gaus mean : 45.3429

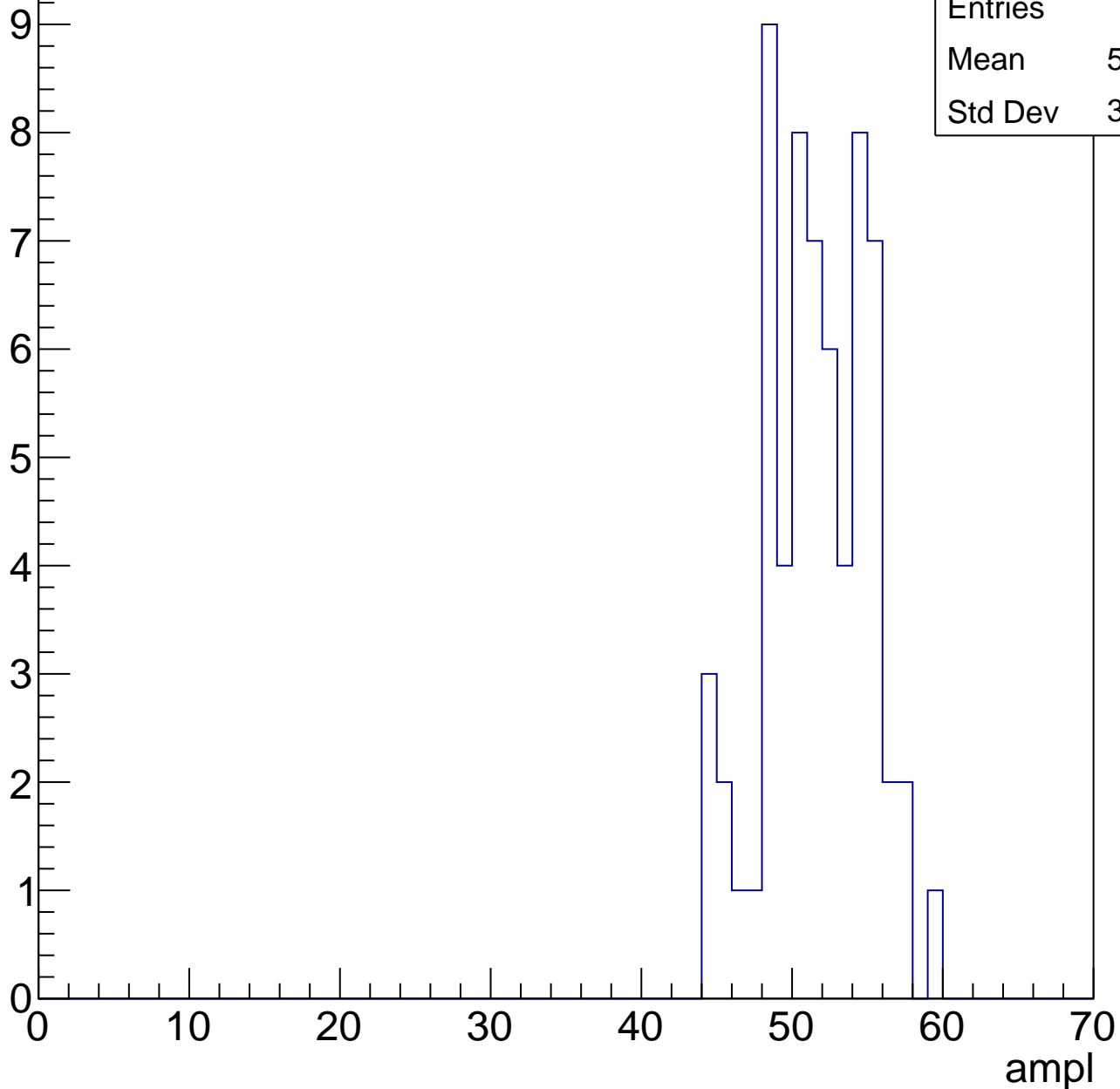
Gaus Width: 3.0677



B0L002S, U9-ch83, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

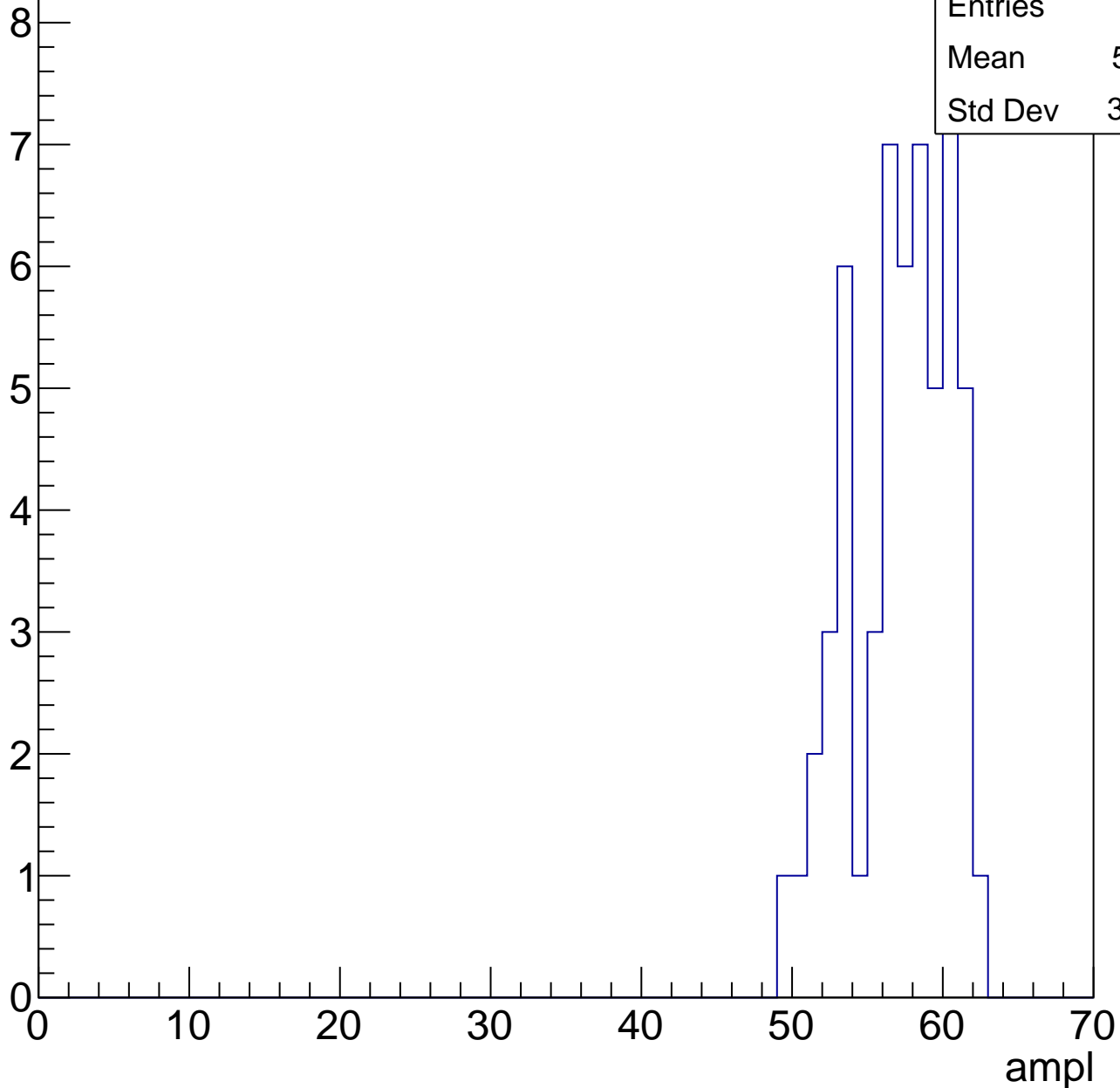


B0L002S, U9-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.71
Std Dev	3.228

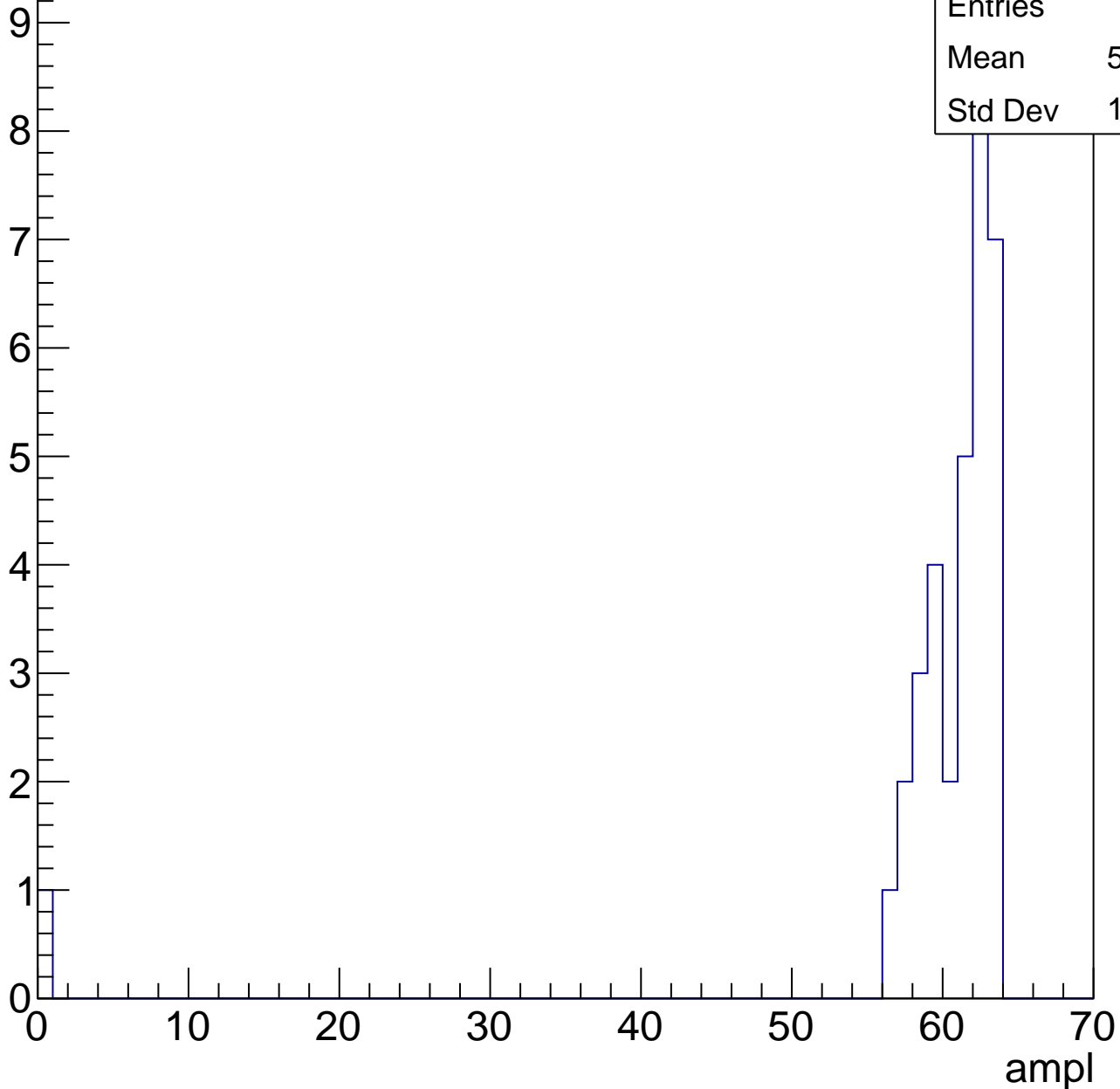


B0L002S, U9-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.94
Std Dev	10.45



B0L002S, U9-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch84, adc0

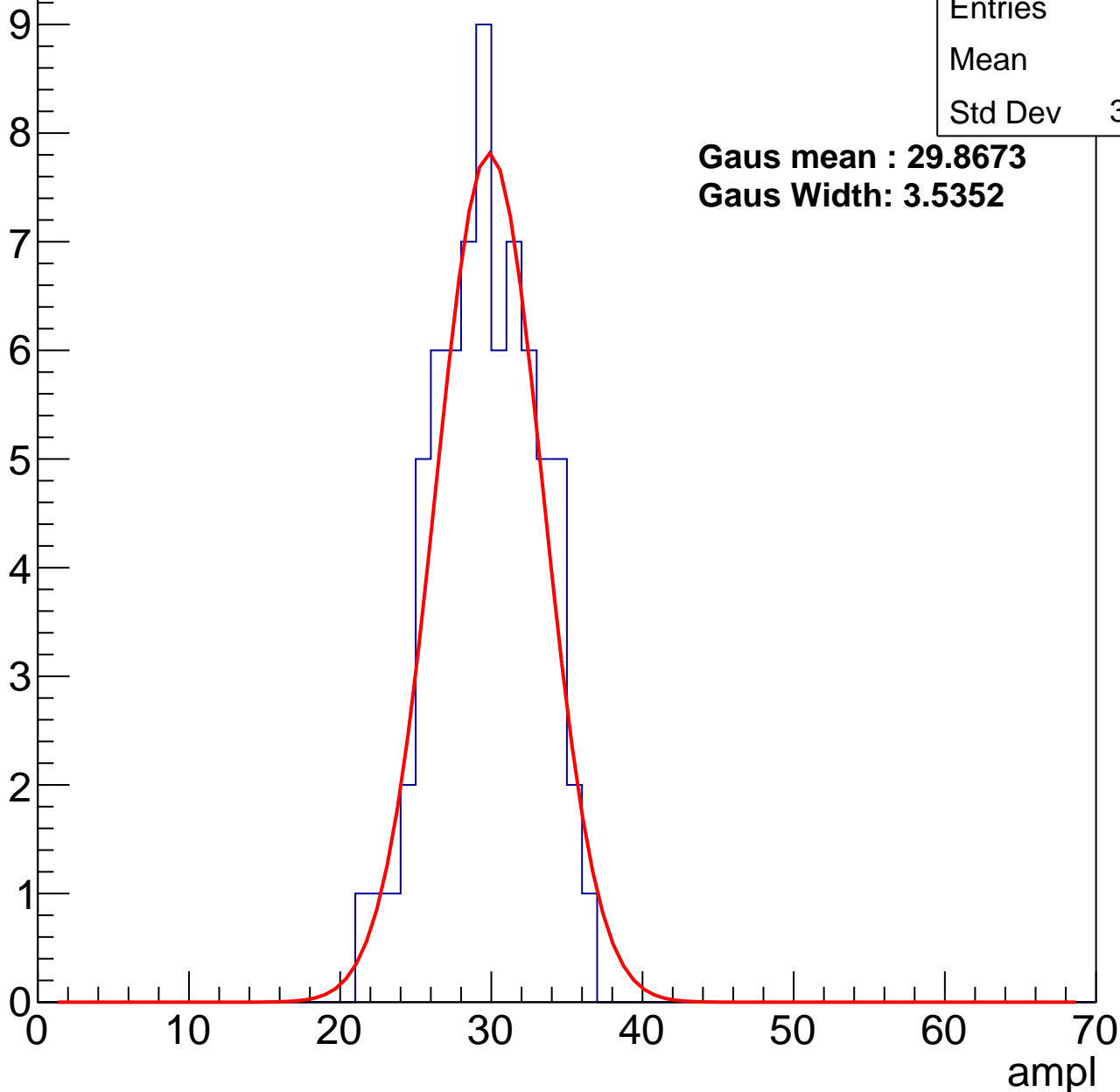
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.2
Std Dev	3.328

Gaus mean : 29.8673

Gaus Width: 3.5352



B0L002S, U9-ch84, adc1

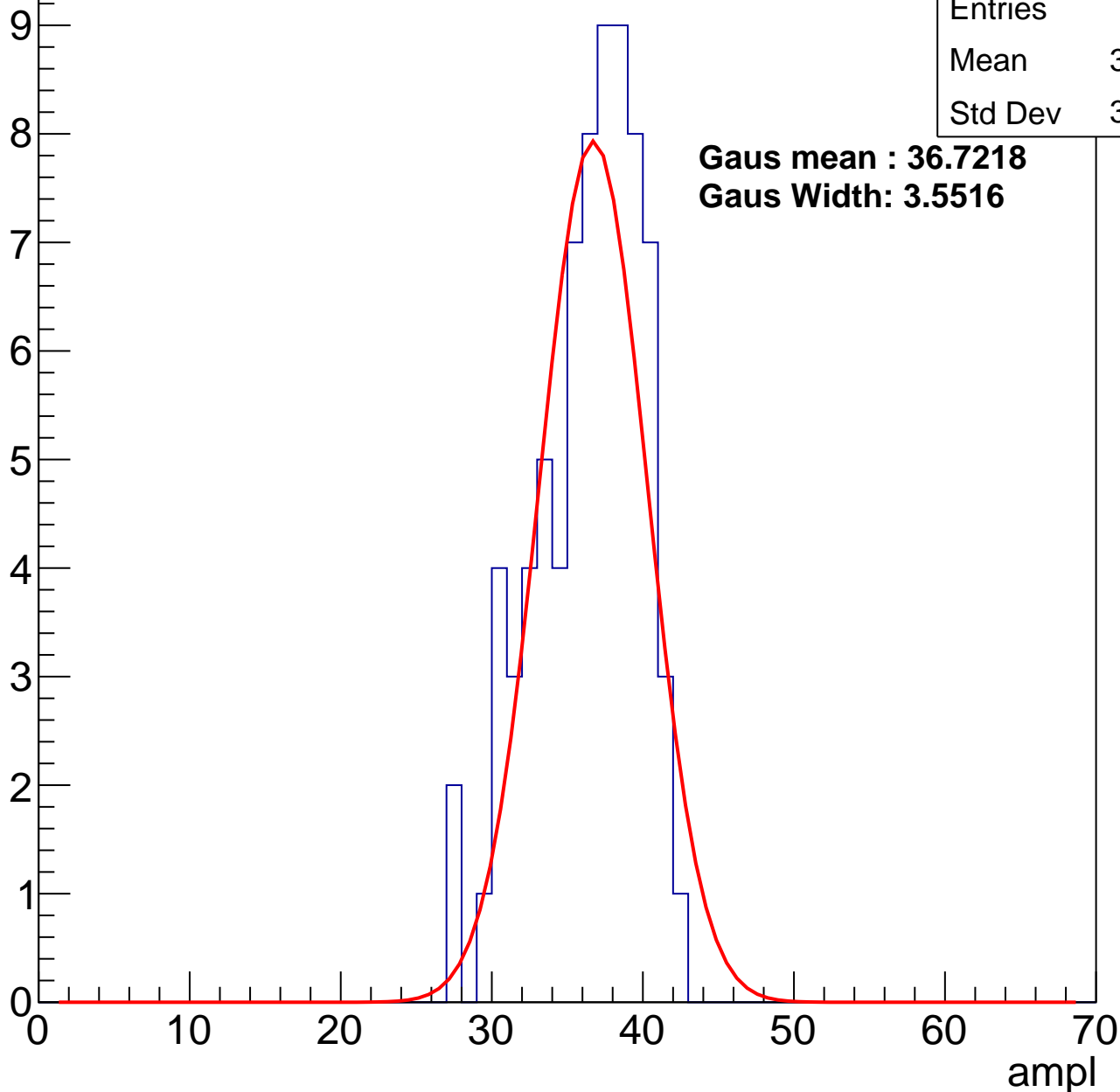
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	35.87
Std Dev	3.465

Gaus mean : 36.7218

Gaus Width: 3.5516



B0L002S, U9-ch84, adc2

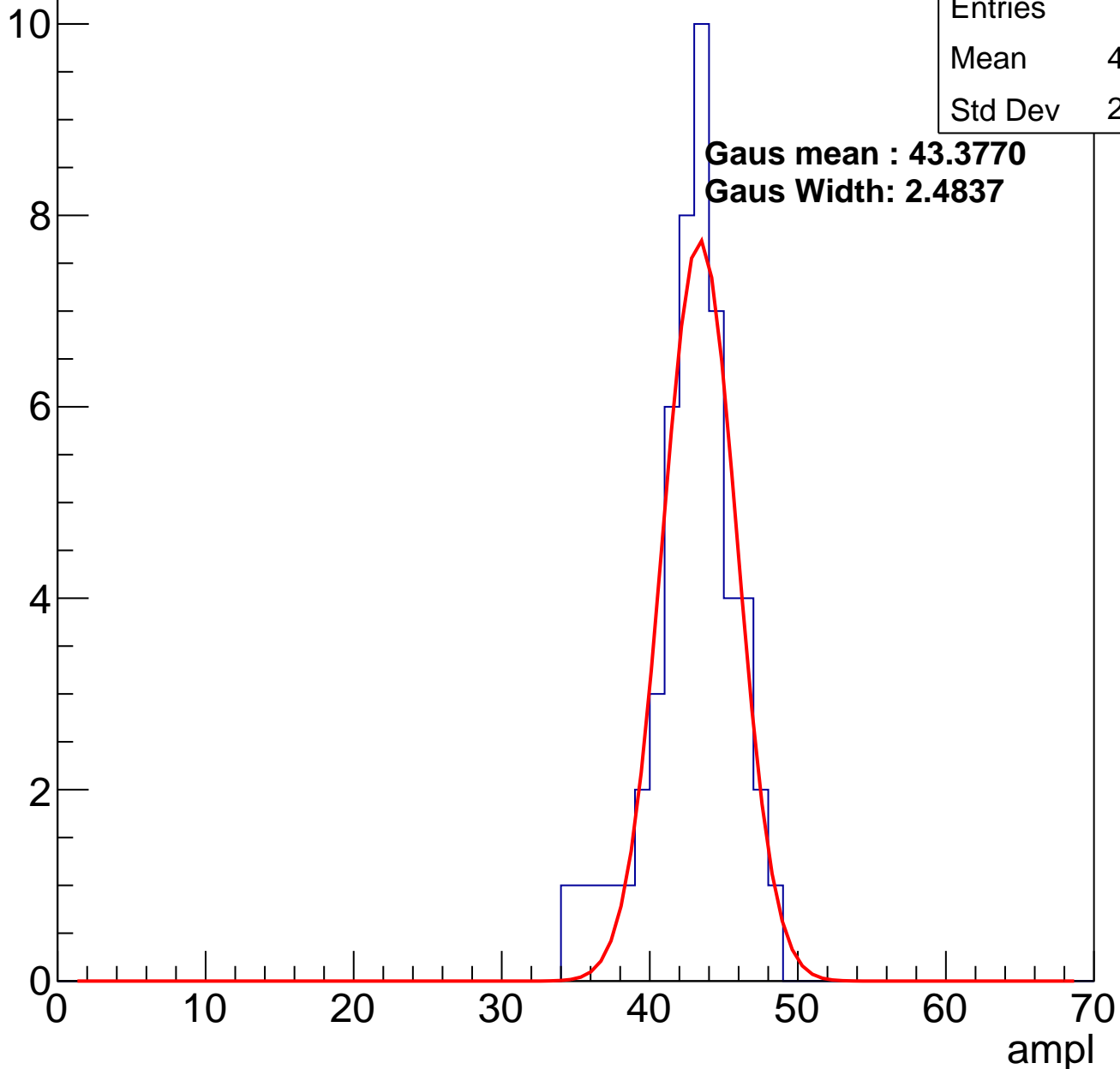
calib_packv5_042523_0143.root, FC#8, port C1

Entries	52
Mean	42.38
Std Dev	2.916

Gaus mean : 43.3770

Gaus Width: 2.4837

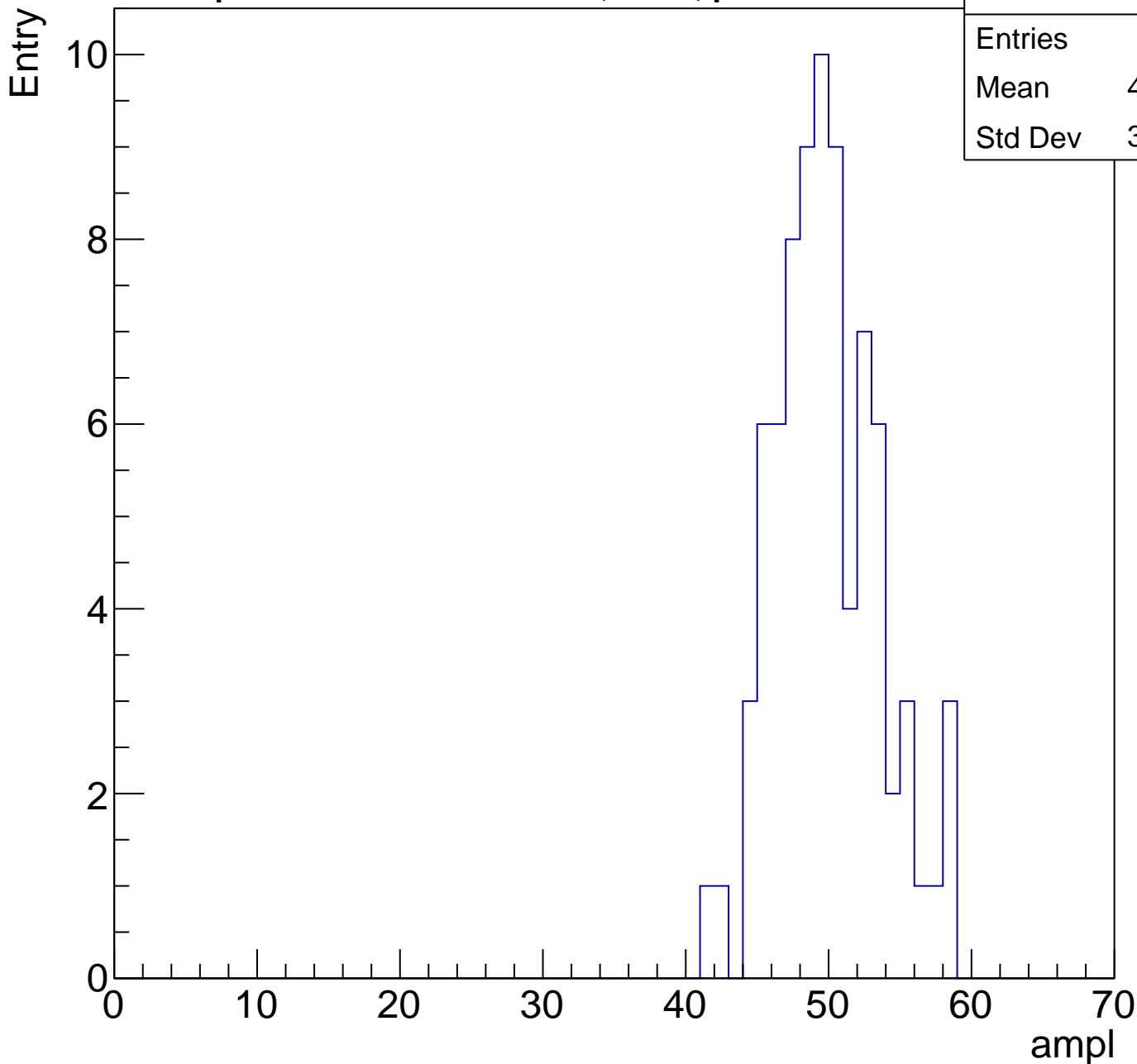
Entry



B0L002S, U9-ch84, adc3

calib_packv5_042523_0143.root, FC#8, port C1

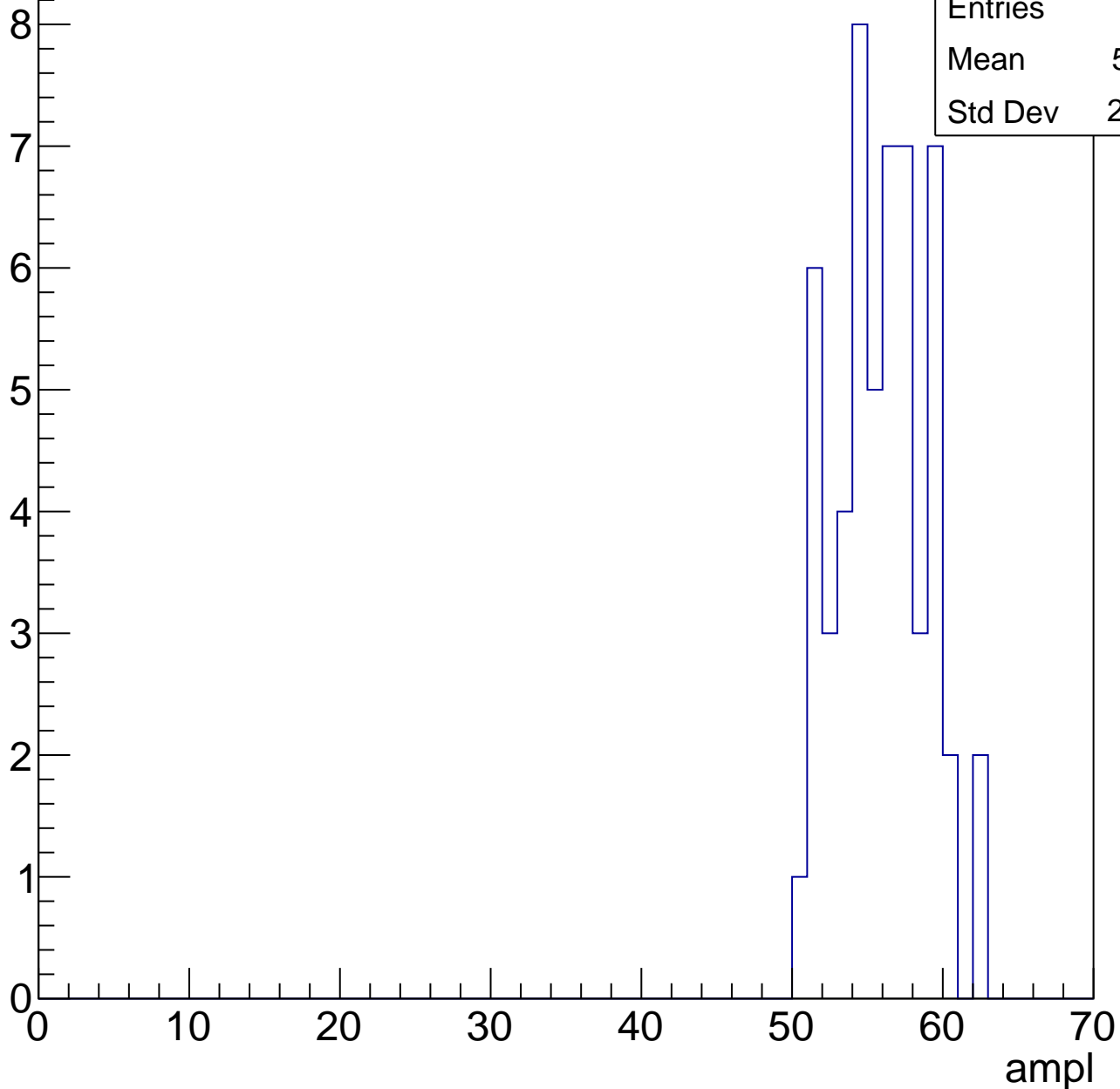
Entries	80
Mean	49.44
Std Dev	3.643



B0L002S, U9-ch84, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



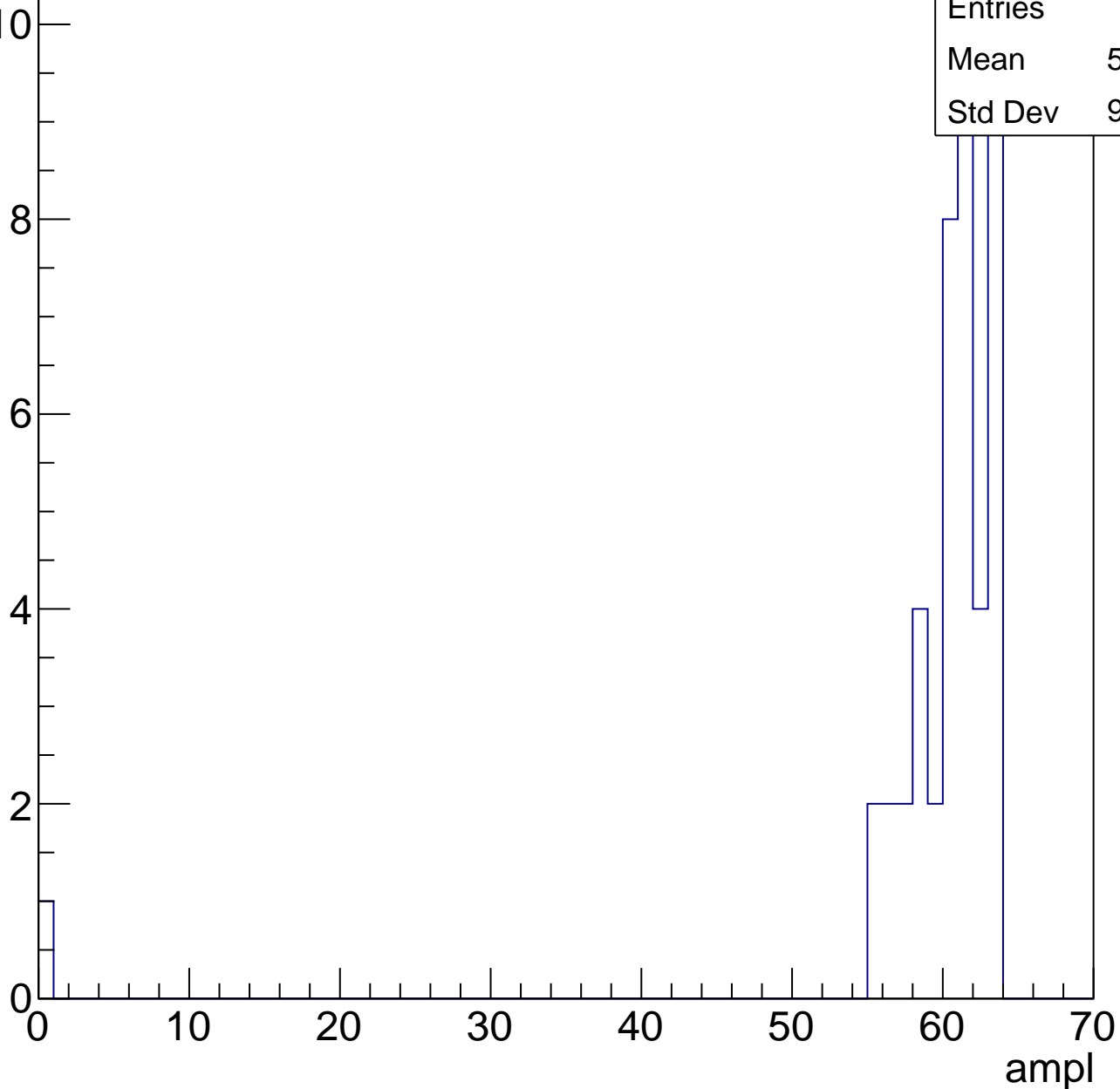
Entries	55
Mean	55.51
Std Dev	2.953

B0L002S, U9-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

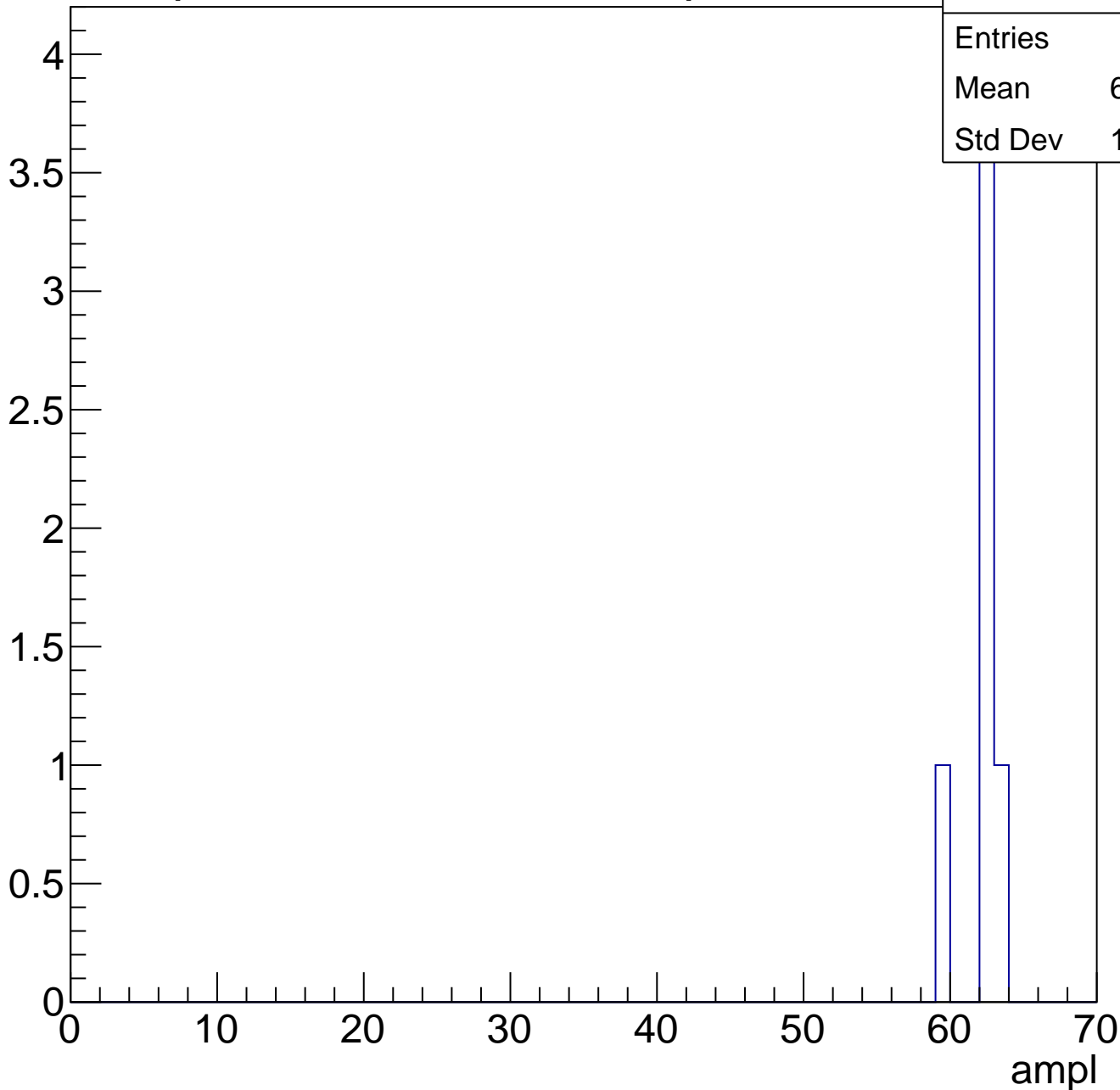
Entries	44
Mean	58.89
Std Dev	9.257



B0L002S, U9-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch85, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	29.27
Std Dev	5.79

Gaus mean : 30.2873

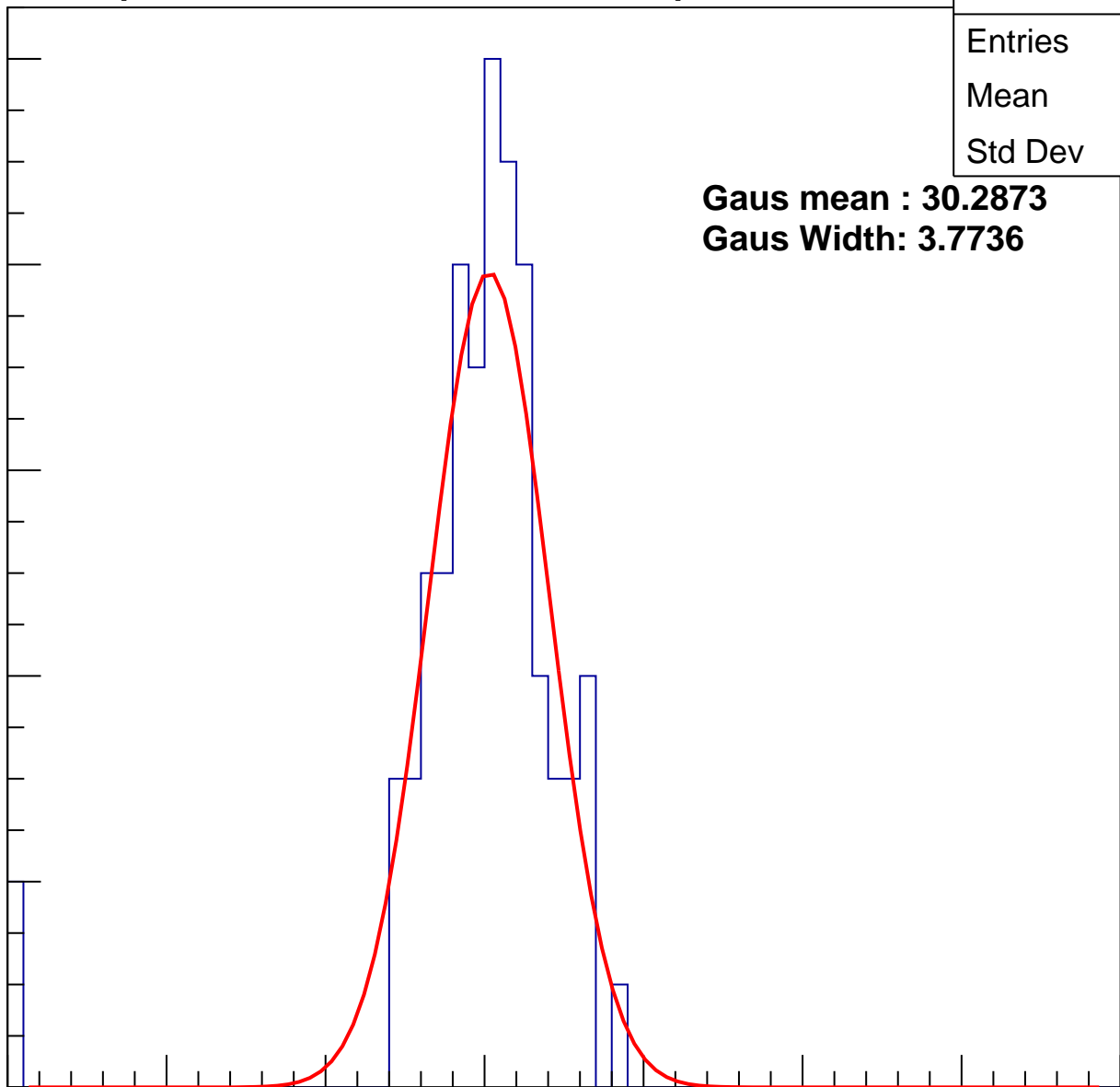
Gaus Width: 3.7736

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch85, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	91
Mean	38.53
Std Dev	3.449

Gaus mean : 38.9424

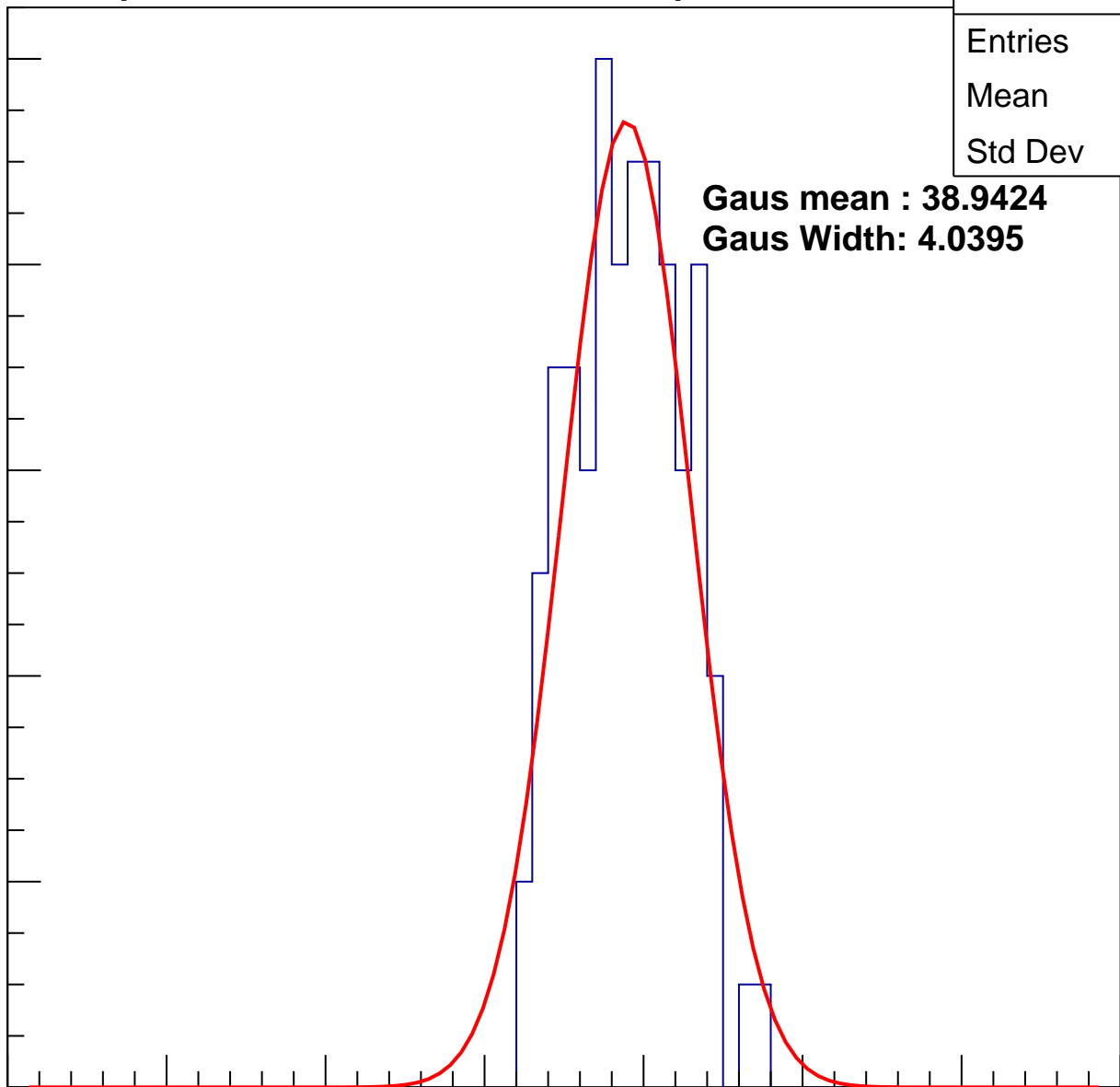
Gaus Width: 4.0395

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch85, adc2

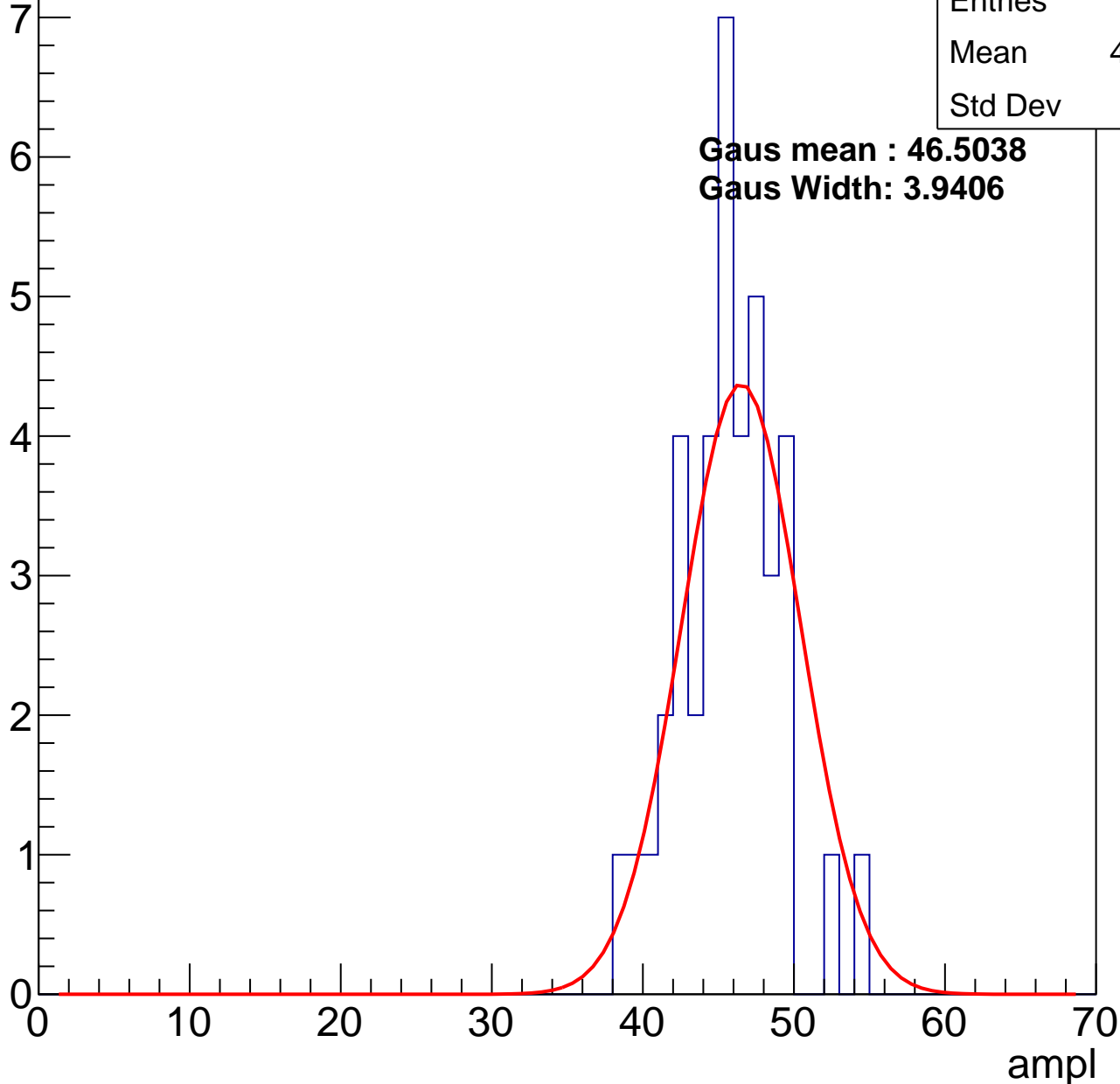
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	45.23
Std Dev	3.29

Gaus mean : 46.5038

Gaus Width: 3.9406



B0L002S, U9-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

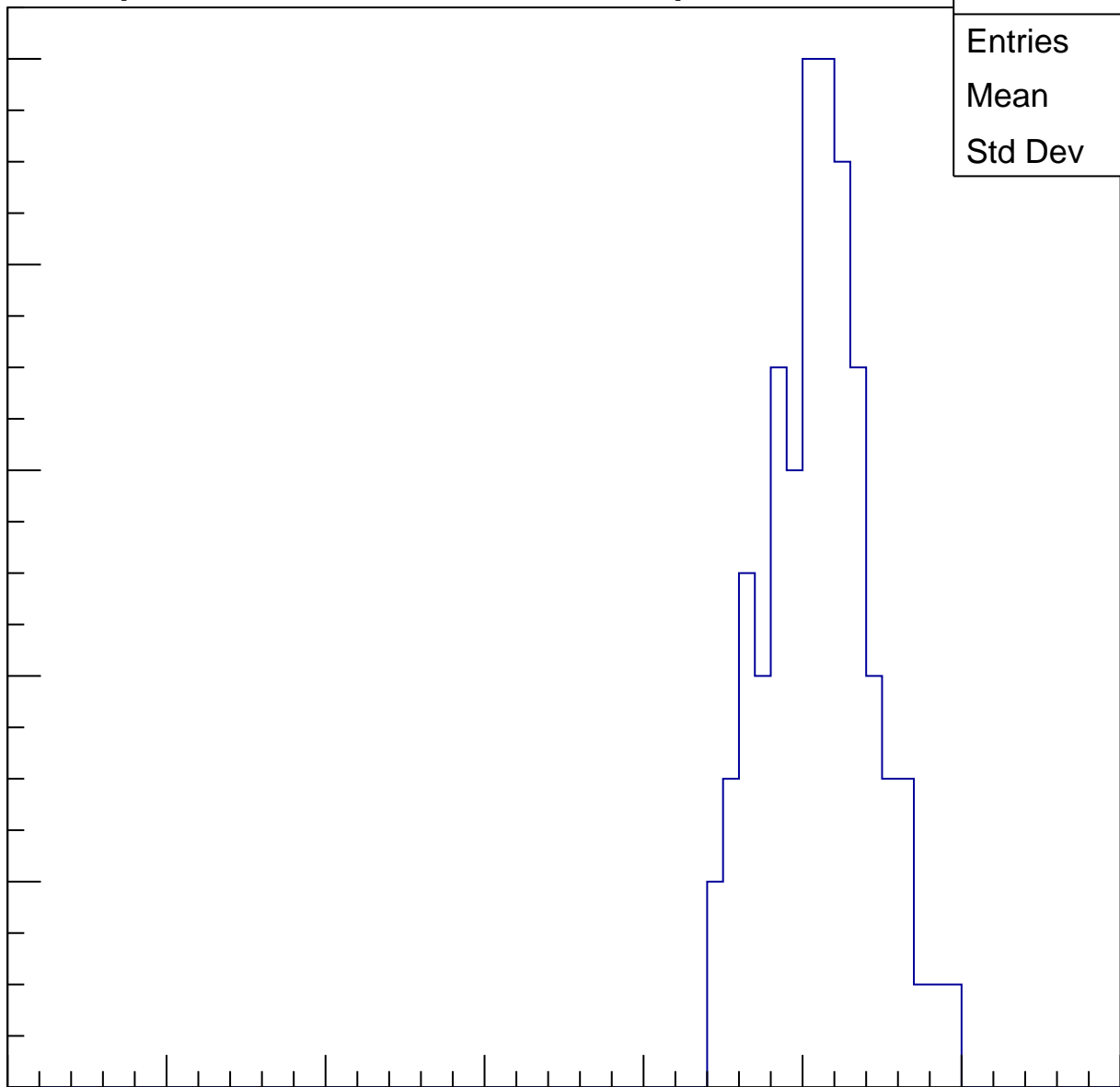
Entries	76
Mean	50.57
Std Dev	3.29

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

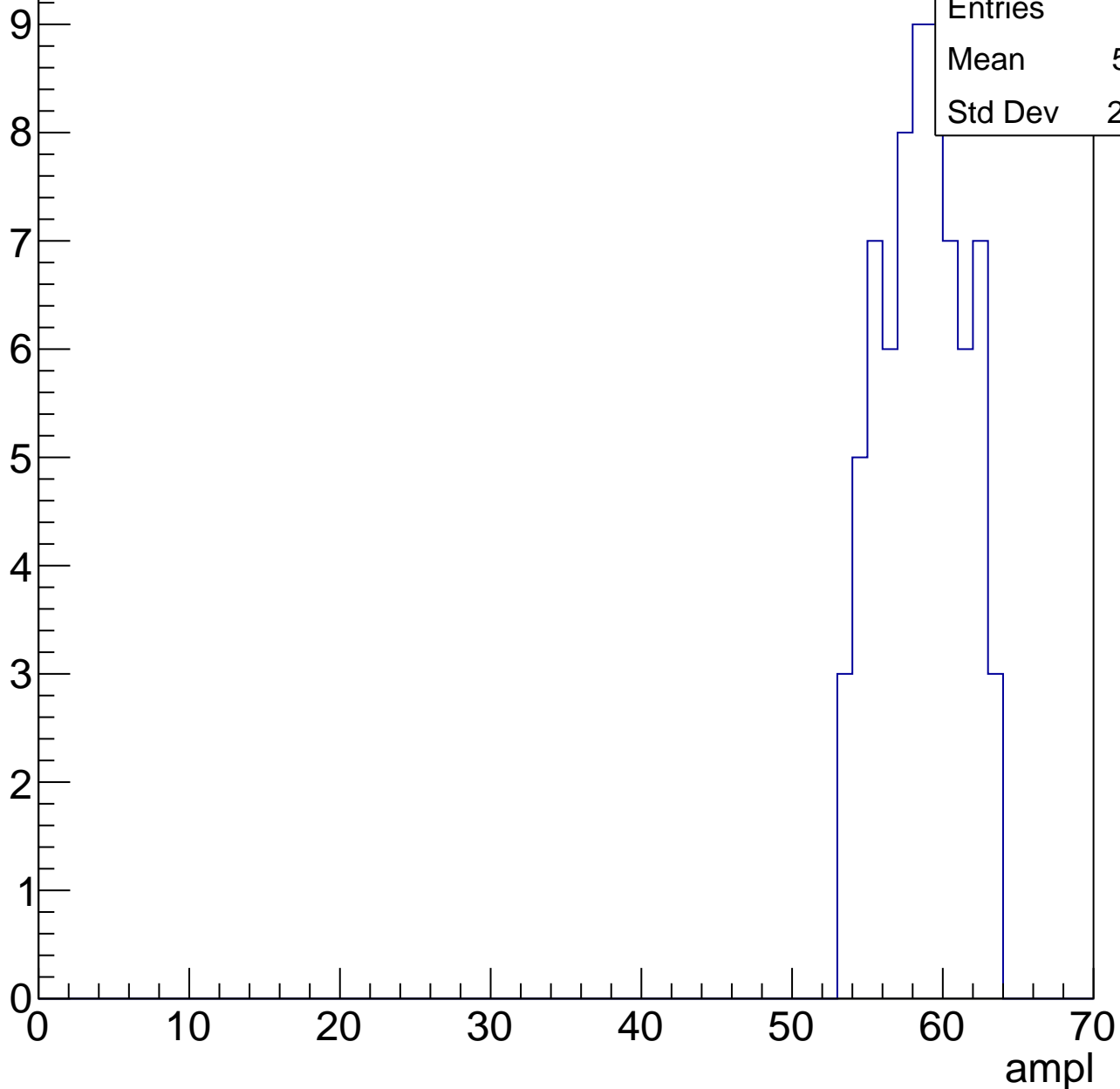
ampl



B0L002S, U9-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

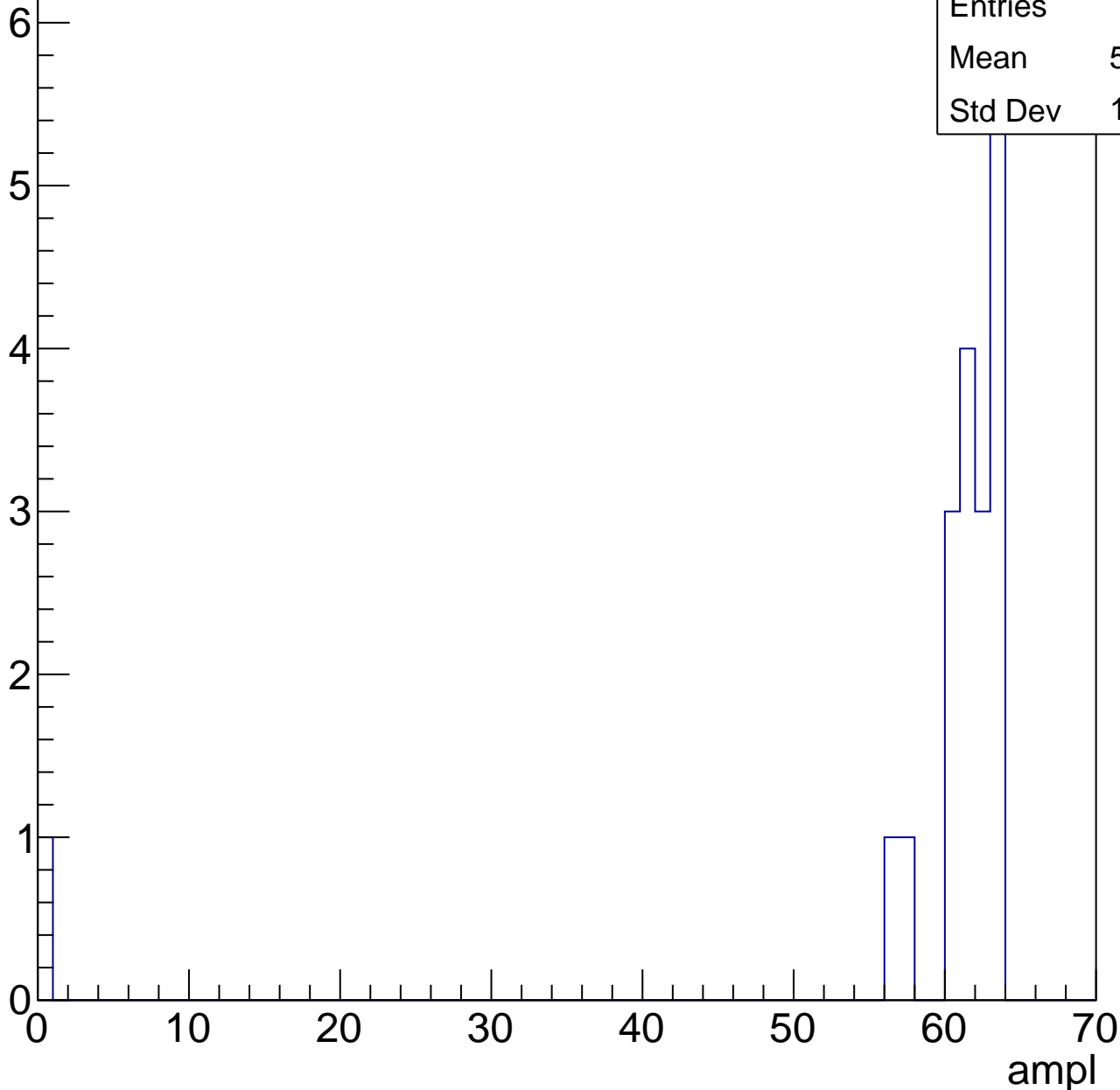


B0L002S, U9-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	57.95
Std Dev	13.79



B0L002S, U9-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch86, adc0

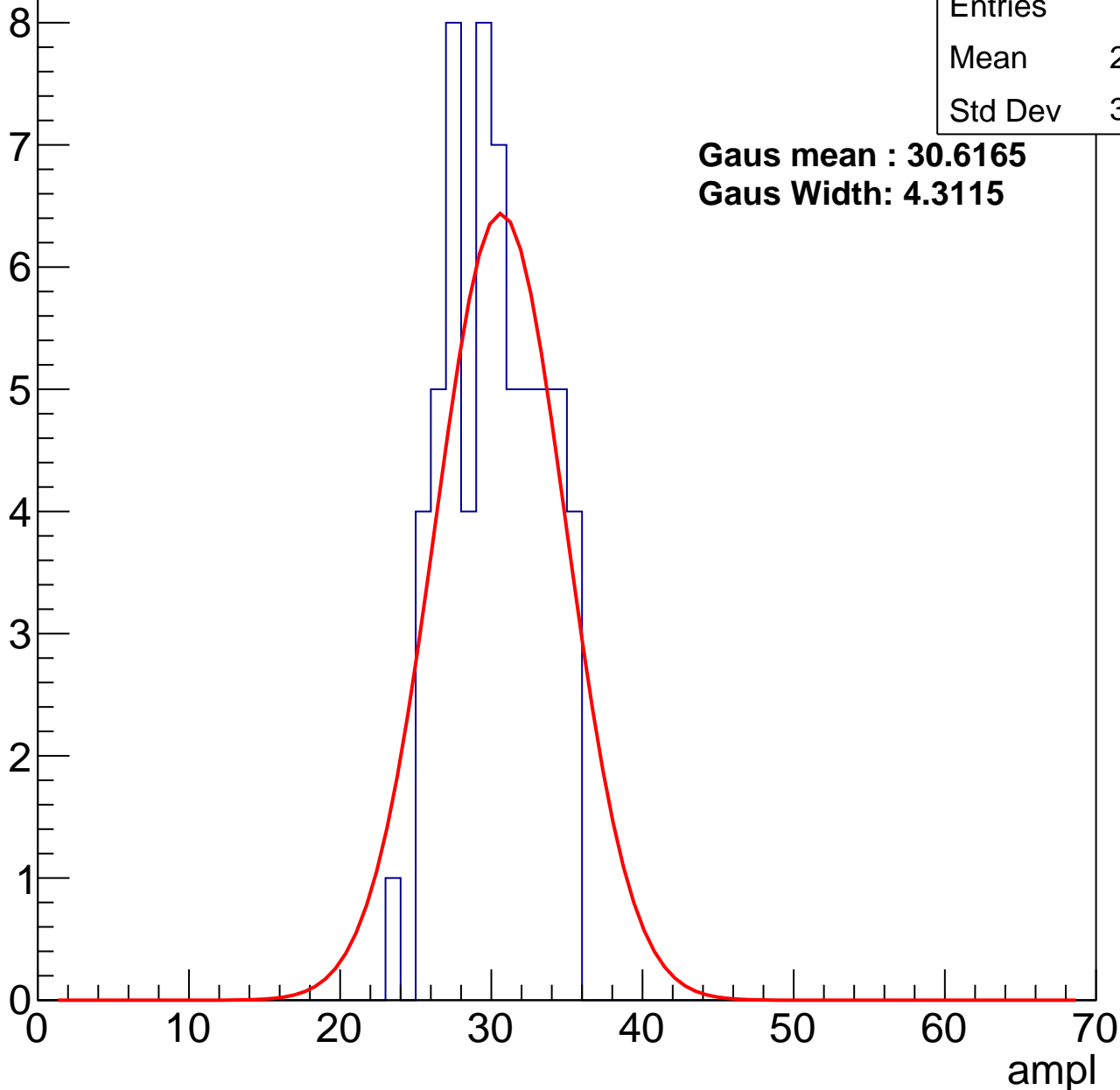
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.72
Std Dev	3.058

Gaus mean : 30.6165

Gaus Width: 4.3115



B0L002S, U9-ch86, adc1

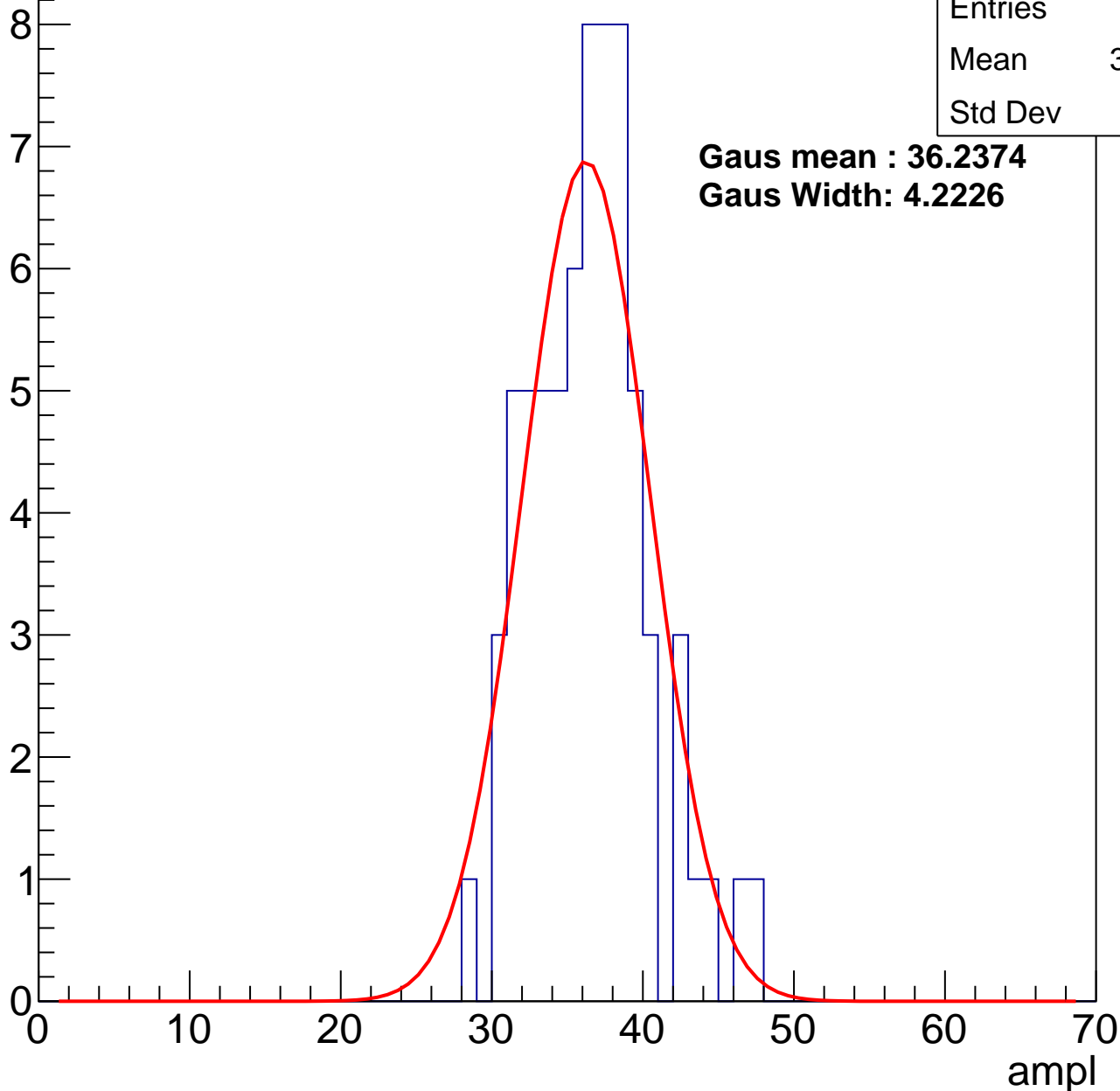
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.04
Std Dev	3.85

Gaus mean : 36.2374

Gaus Width: 4.2226



B0L002S, U9-ch86, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	42.78
Std Dev	3.185

Gaus mean : 43.2273

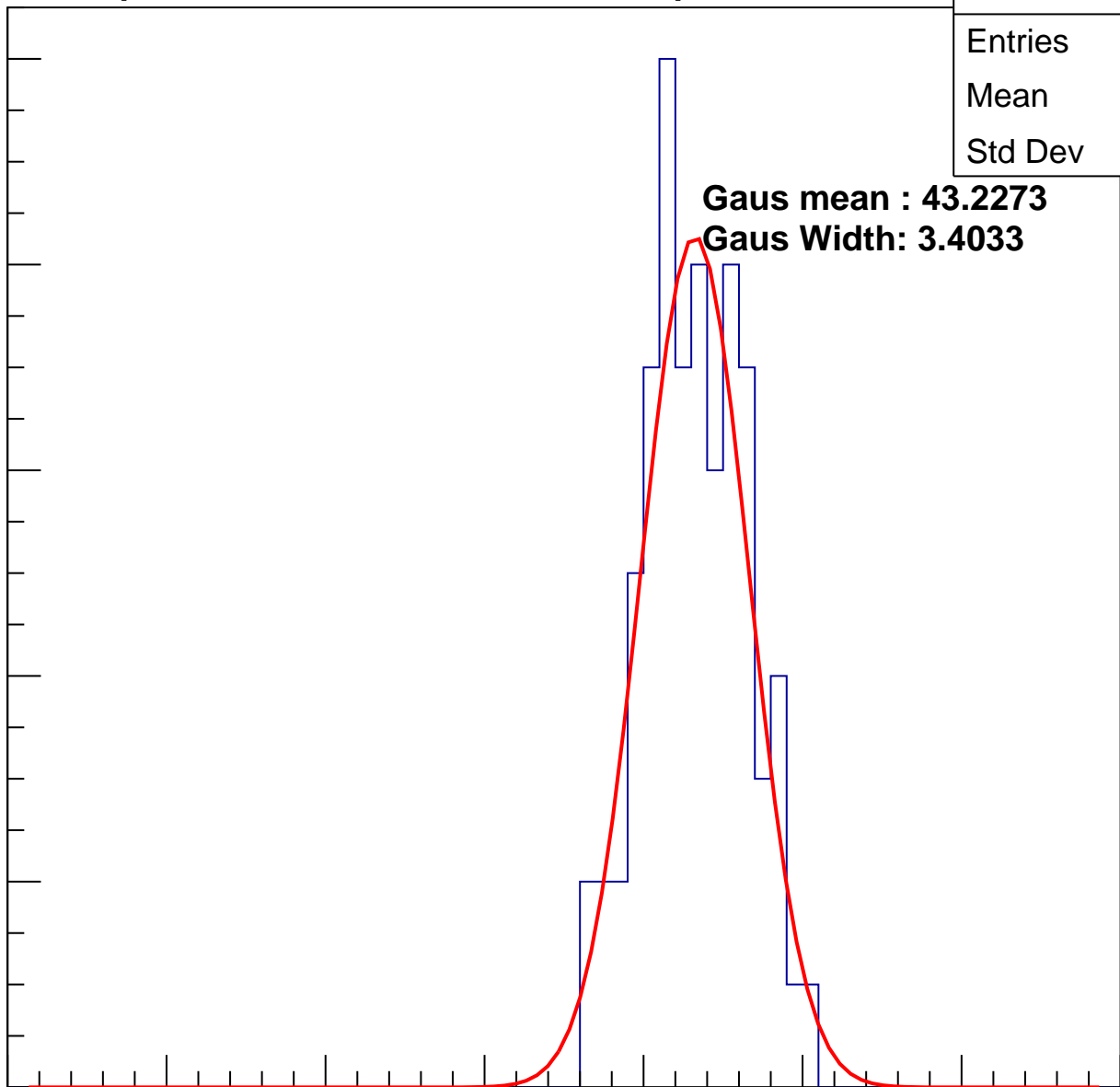
Gaus Width: 3.4033

Entry

10
8
6
4
2
0

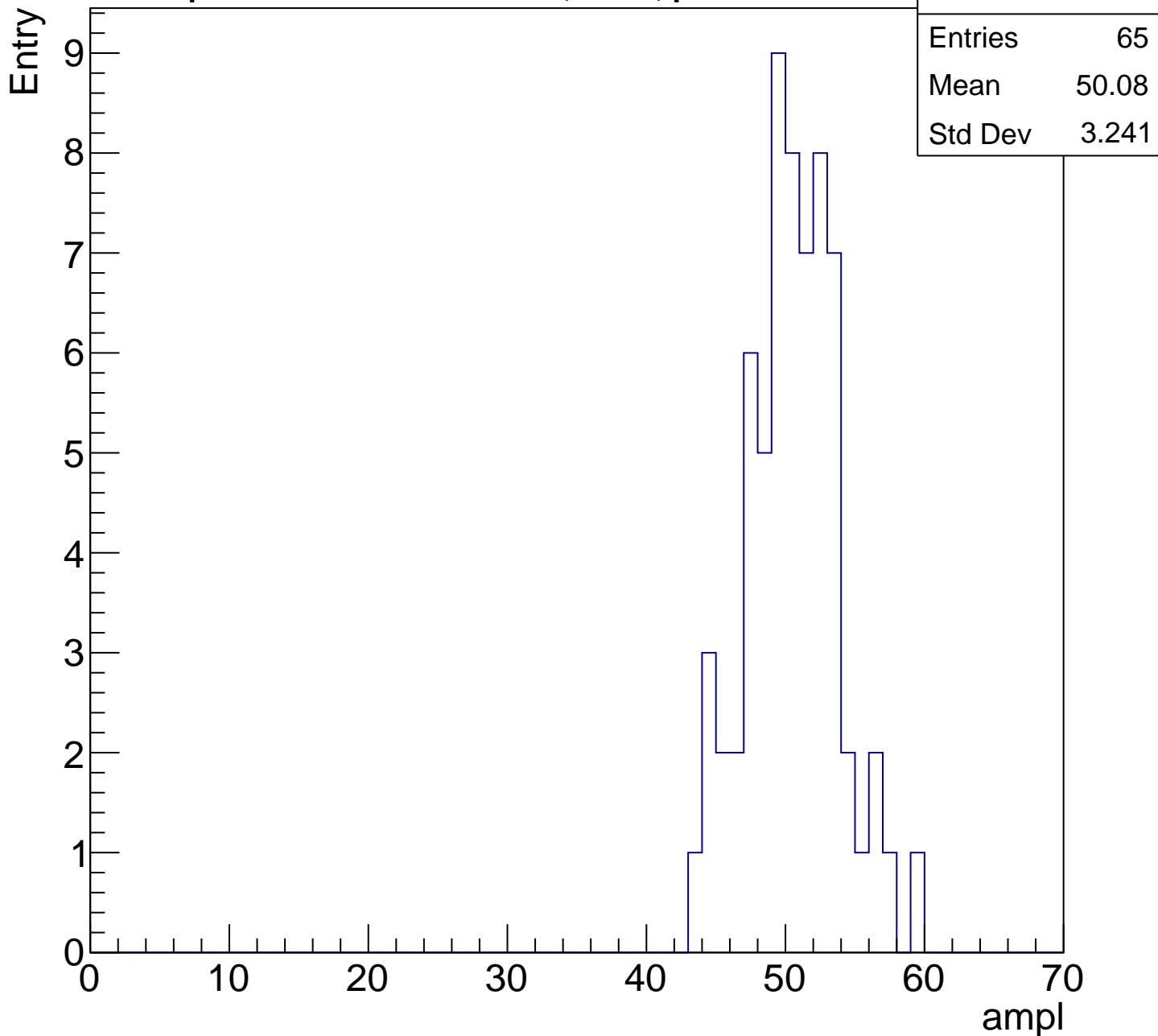
ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch86, adc3

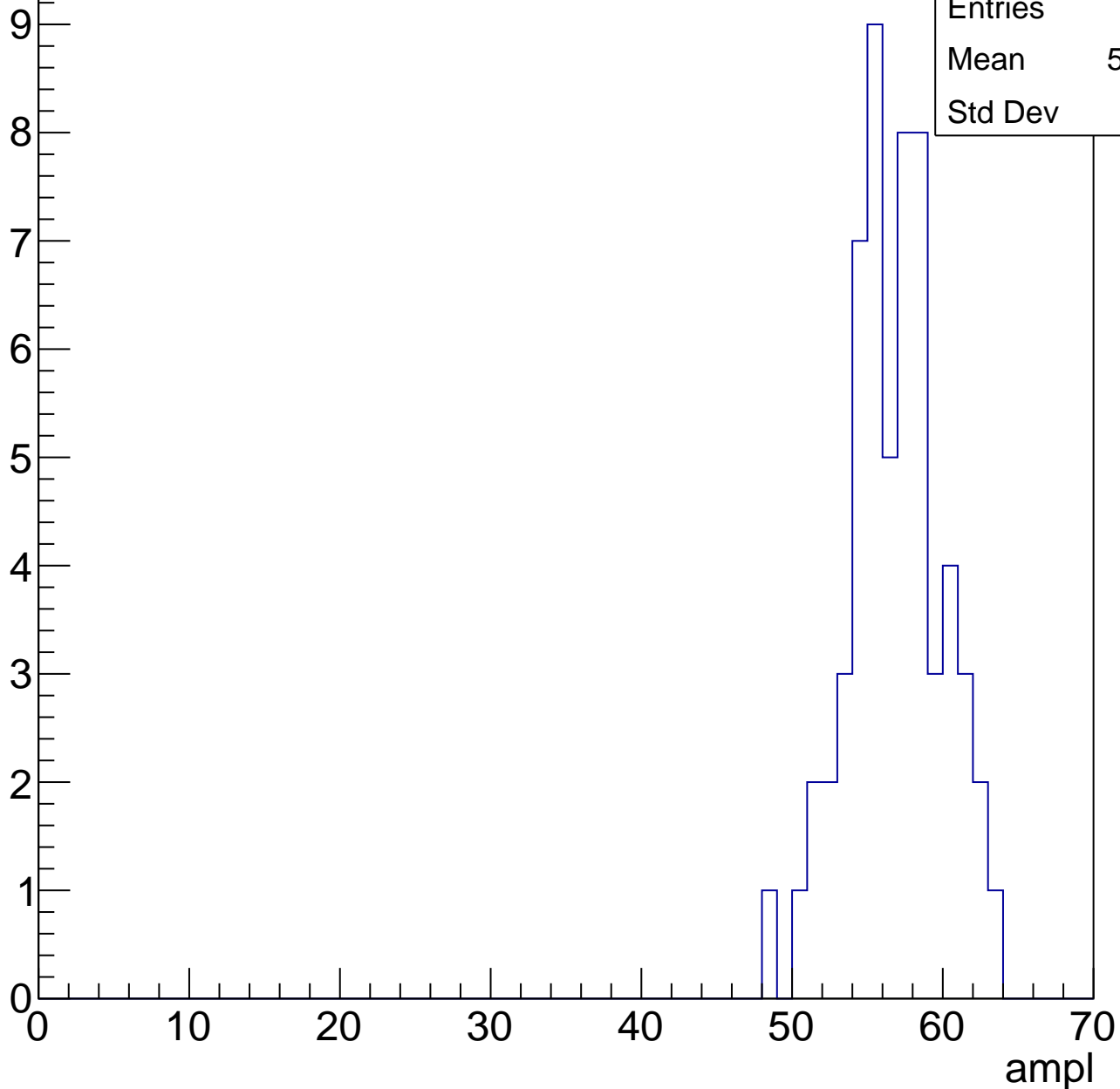
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch86, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



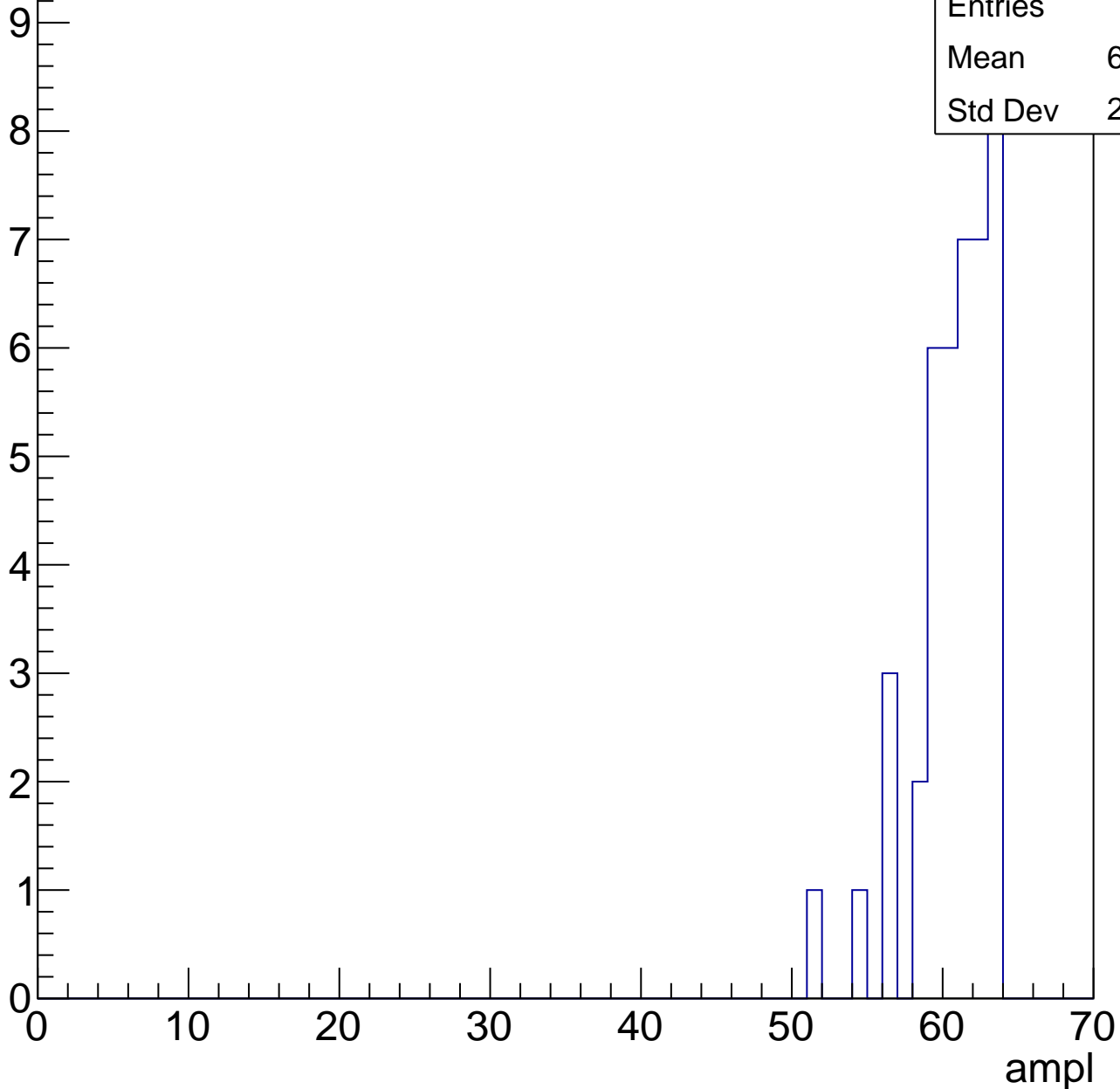
Entries	59
Mean	56.32
Std Dev	3.1

B0L002S, U9-ch86, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

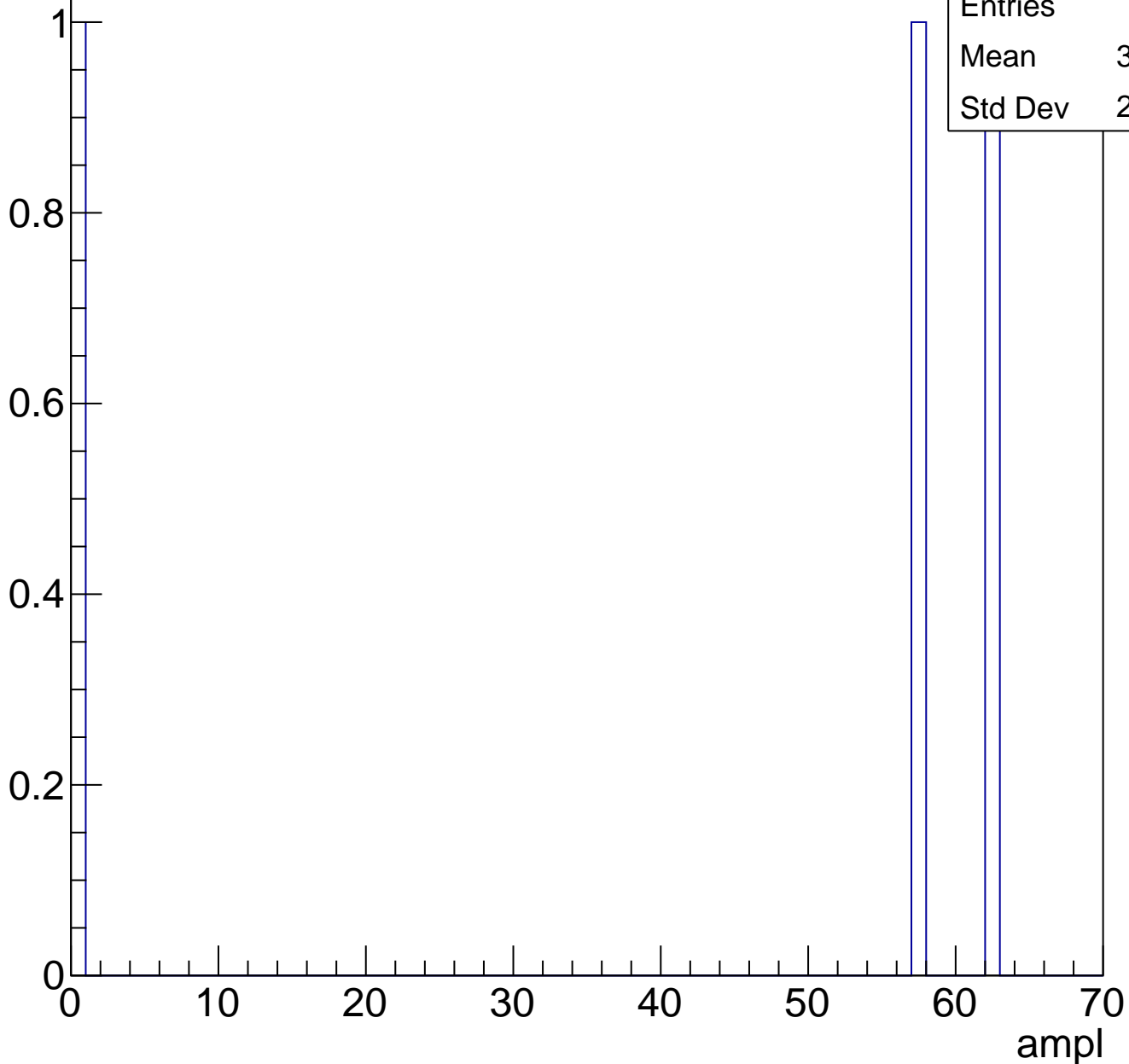
Entries	42
Mean	60.26
Std Dev	2.637



B0L002S, U9-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U9-ch87, adc0

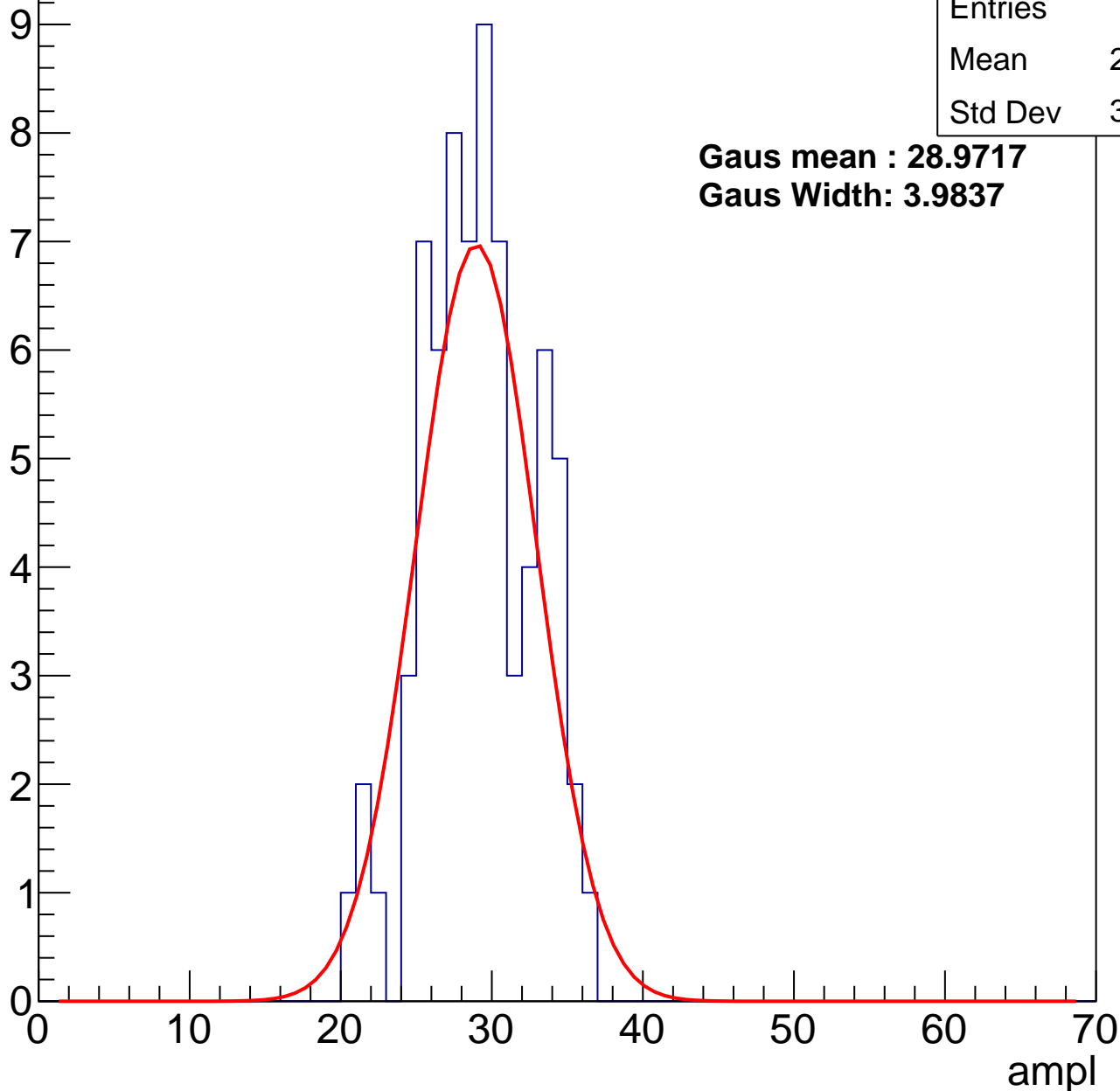
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	28.68
Std Dev	3.589

Gaus mean : 28.9717

Gaus Width: 3.9837



B0L002S, U9-ch87, adc1

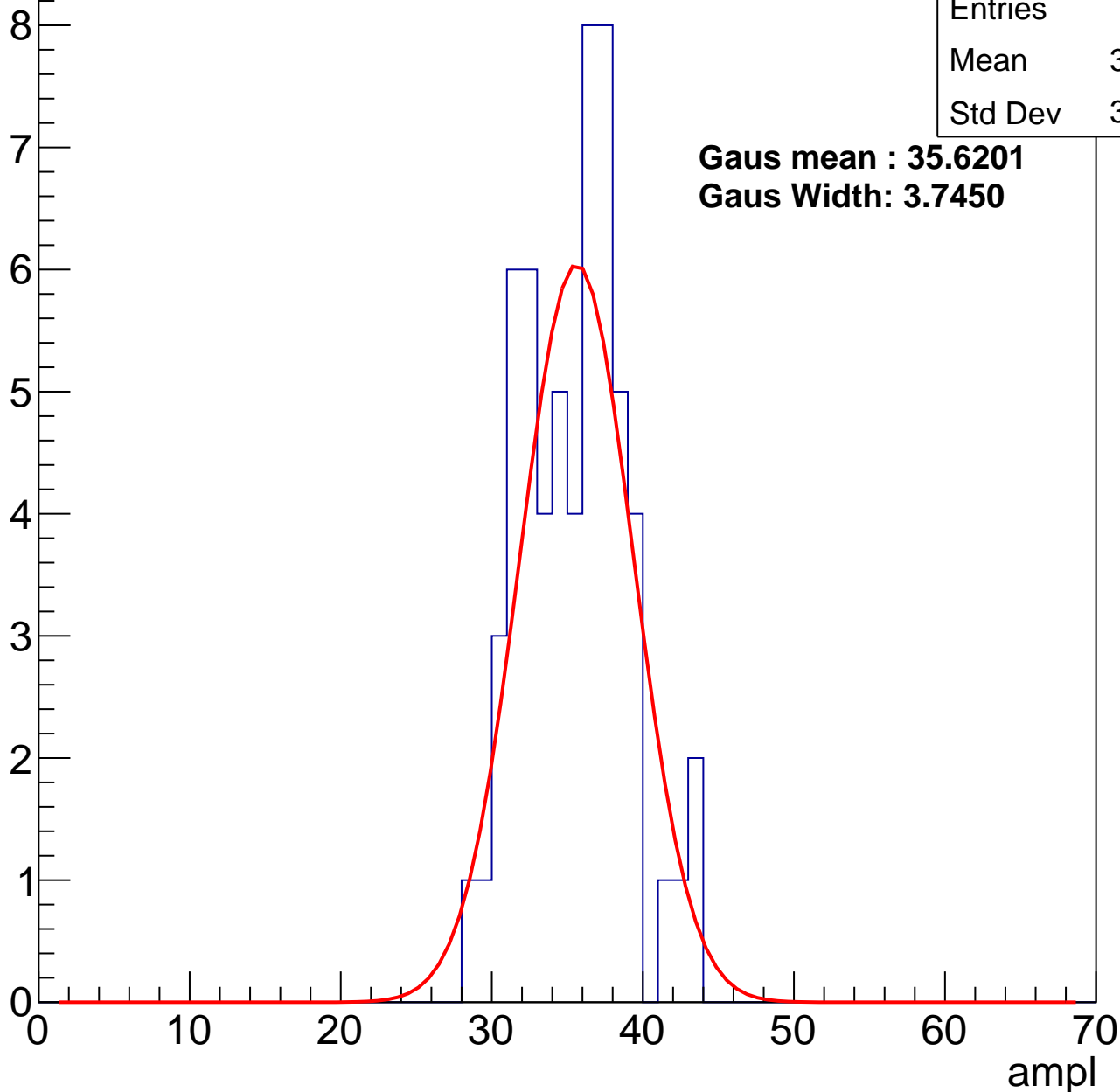
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	35.02
Std Dev	3.427

Gaus mean : 35.6201

Gaus Width: 3.7450



B0L002S, U9-ch87, adc2

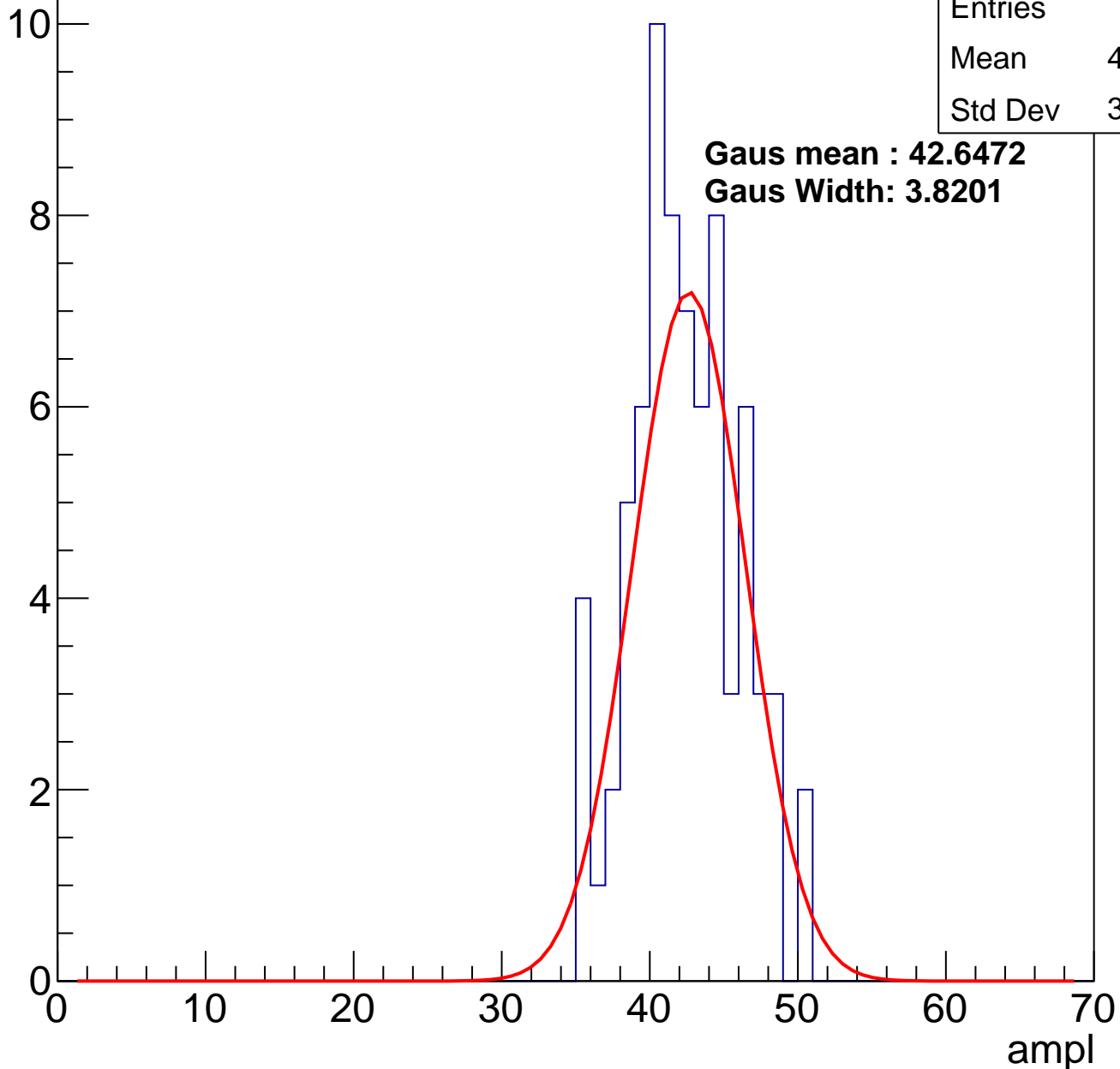
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	41.92
Std Dev	3.556

Gaus mean : 42.6472

Gaus Width: 3.8201

Entry

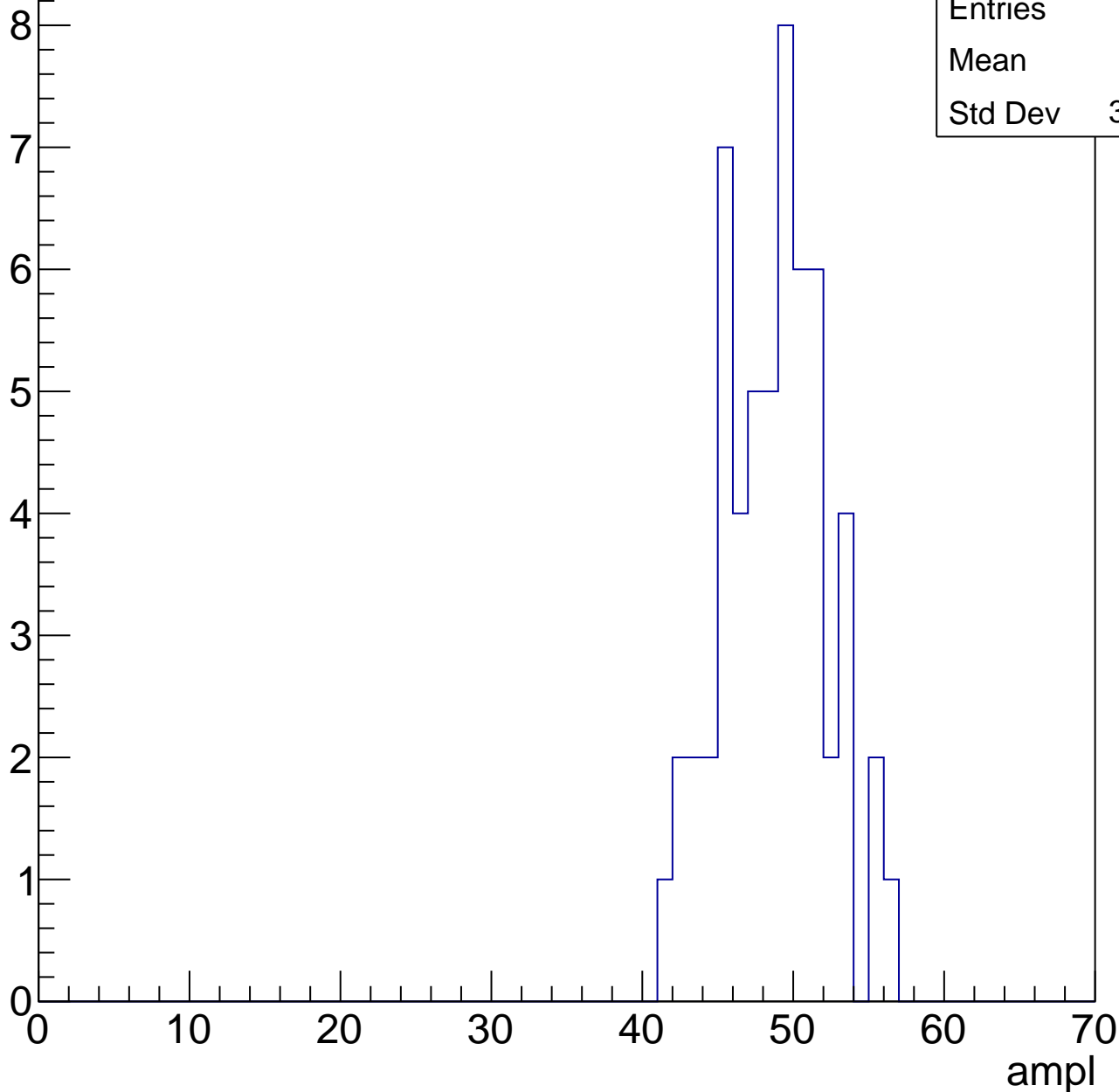


B0L002S, U9-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

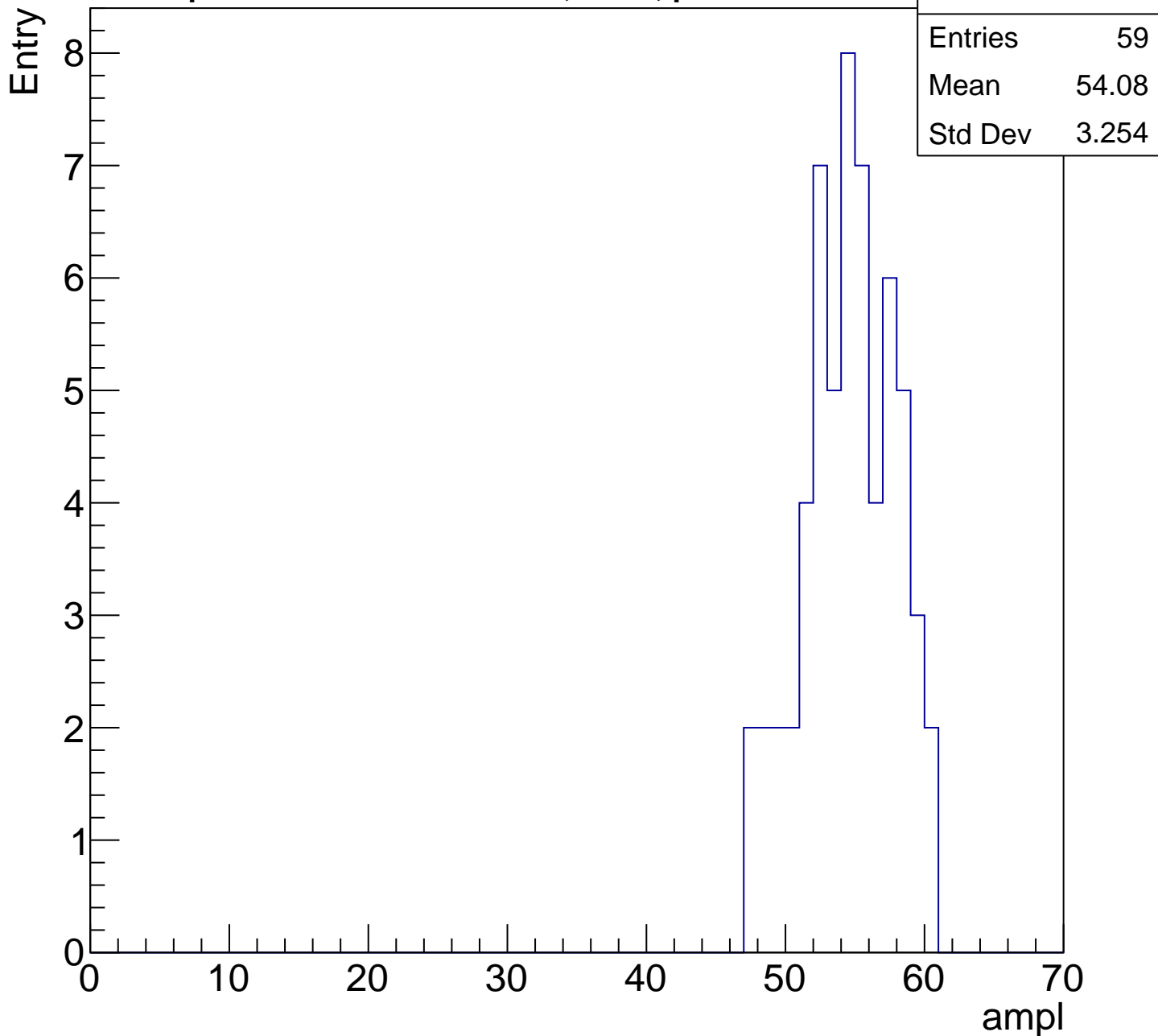
Entry

Entries	57
Mean	48.3
Std Dev	3.408



B0L002S, U9-ch87, adc4

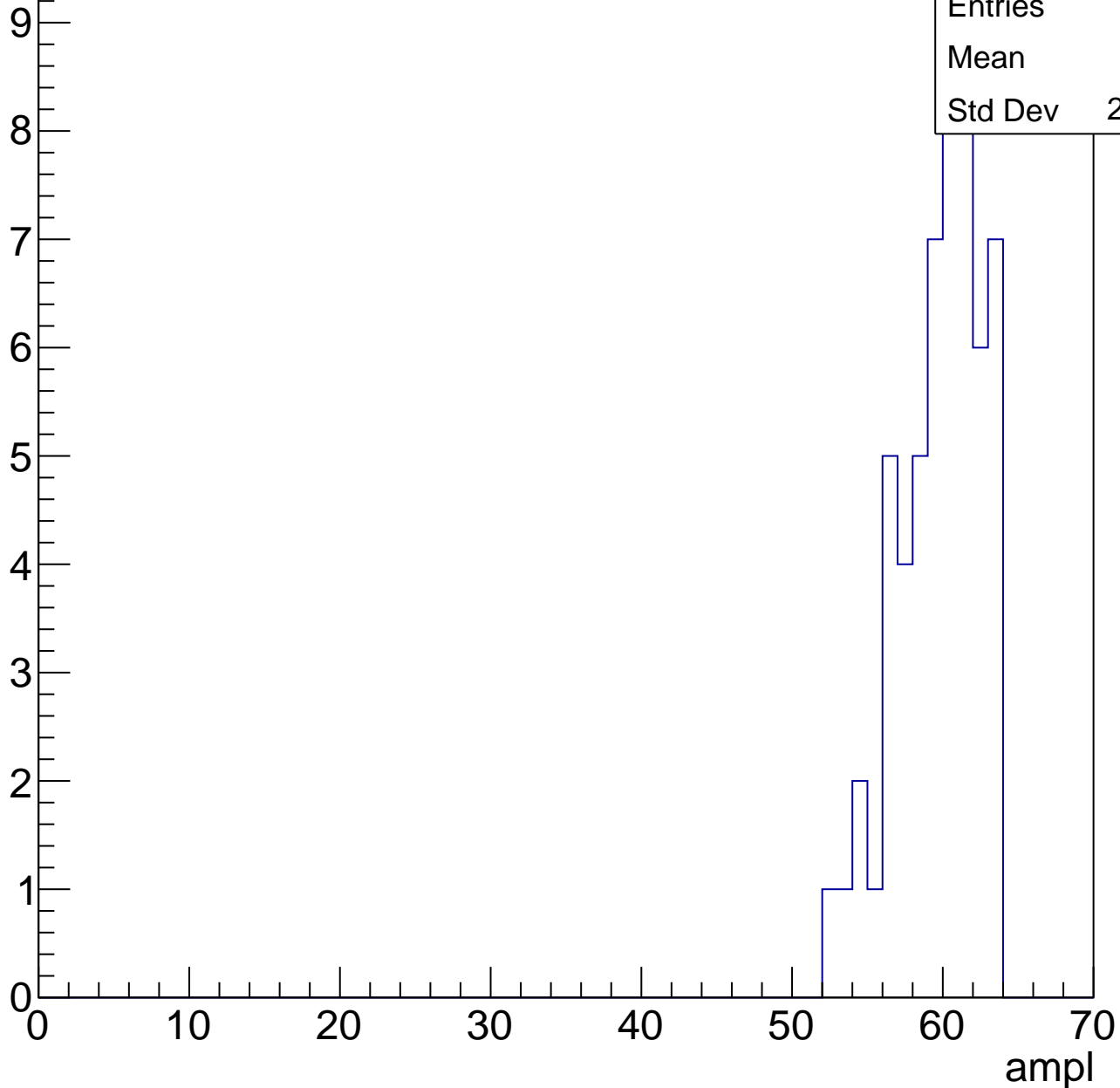
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

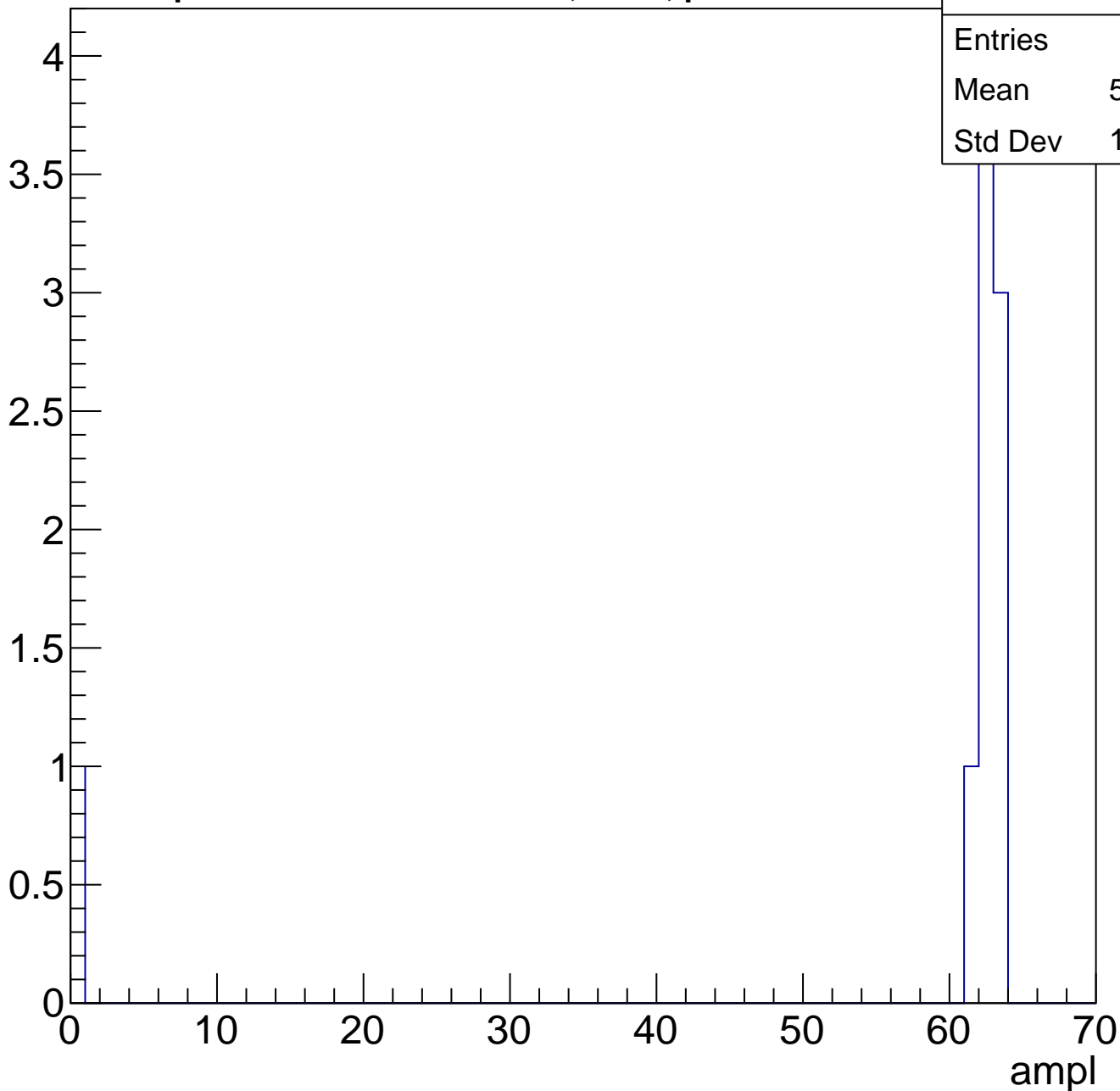
Entry



B0L002S, U9-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch88, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	29.16
Std Dev	4.907

Gaus mean : 30.4483

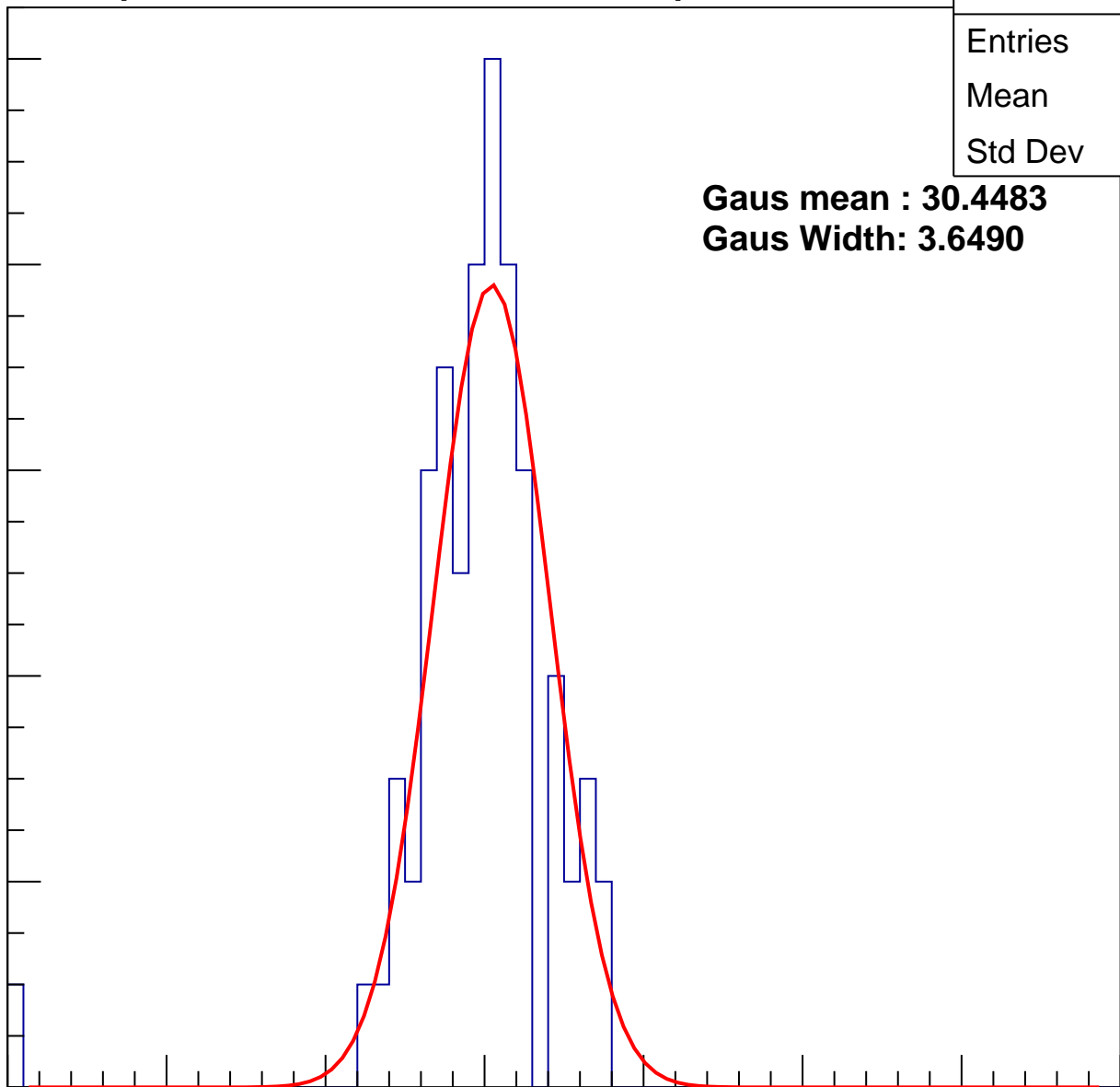
Gaus Width: 3.6490

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch88, adc1

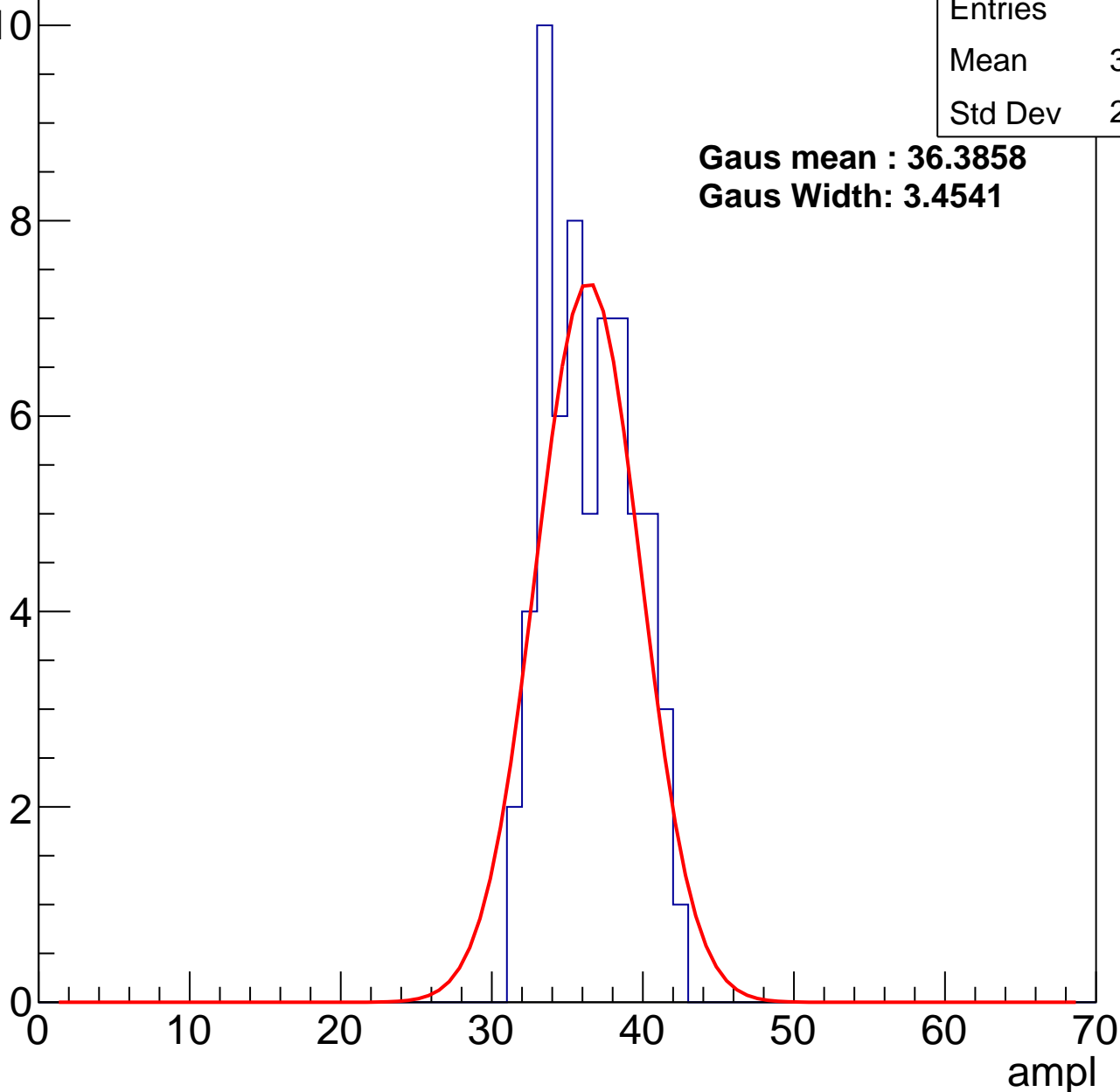
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.02
Std Dev	2.837

Gaus mean : 36.3858

Gaus Width: 3.4541



B0L002S, U9-ch88, adc2

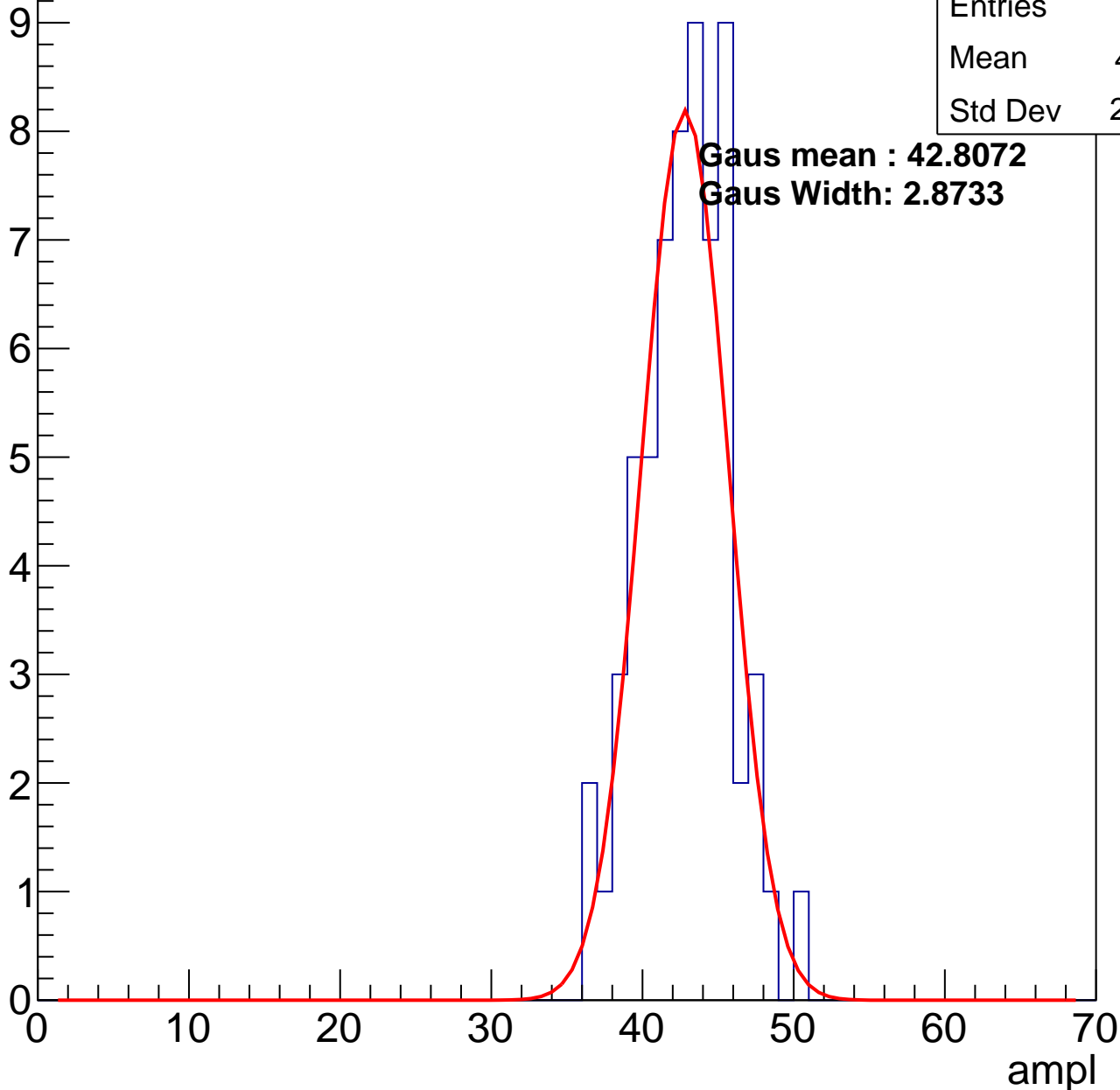
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.41
Std Dev	2.904

Gaus mean : 42.8072

Gaus Width: 2.8733

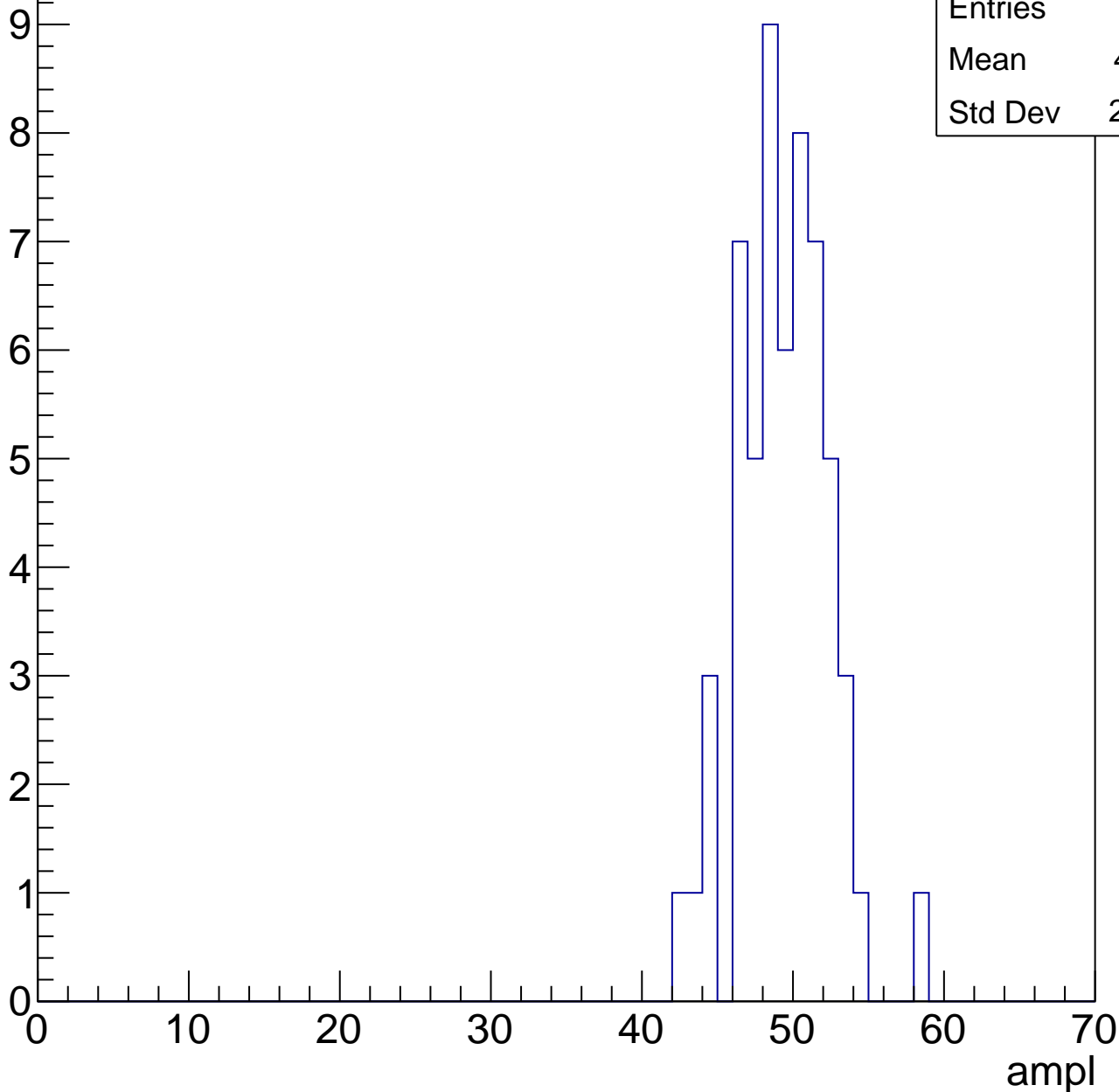


B0L002S, U9-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	48.91
Std Dev	2.922

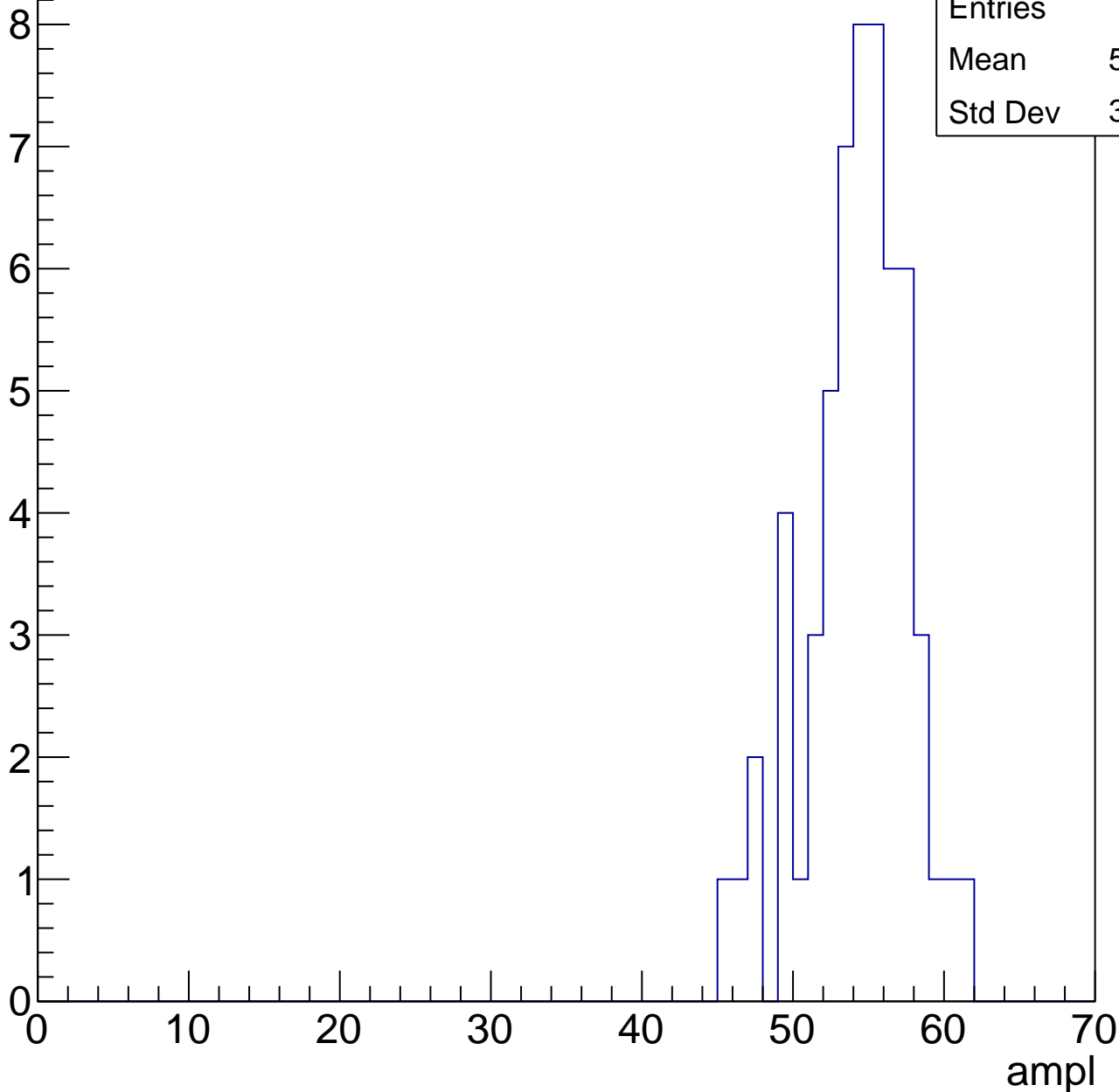


B0L002S, U9-ch88, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

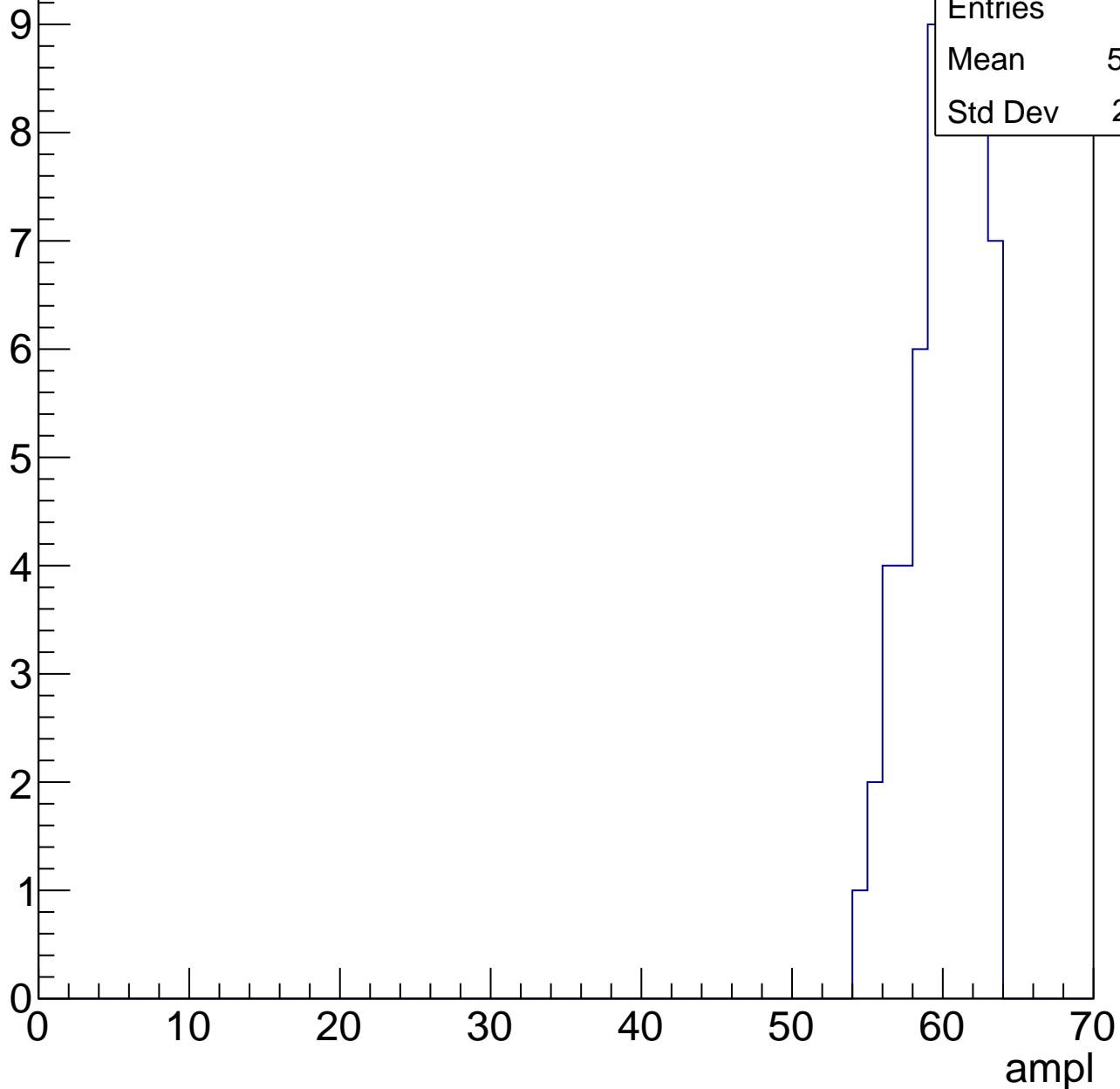
Entries	58
Mean	53.78
Std Dev	3.358



B0L002S, U9-ch88, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

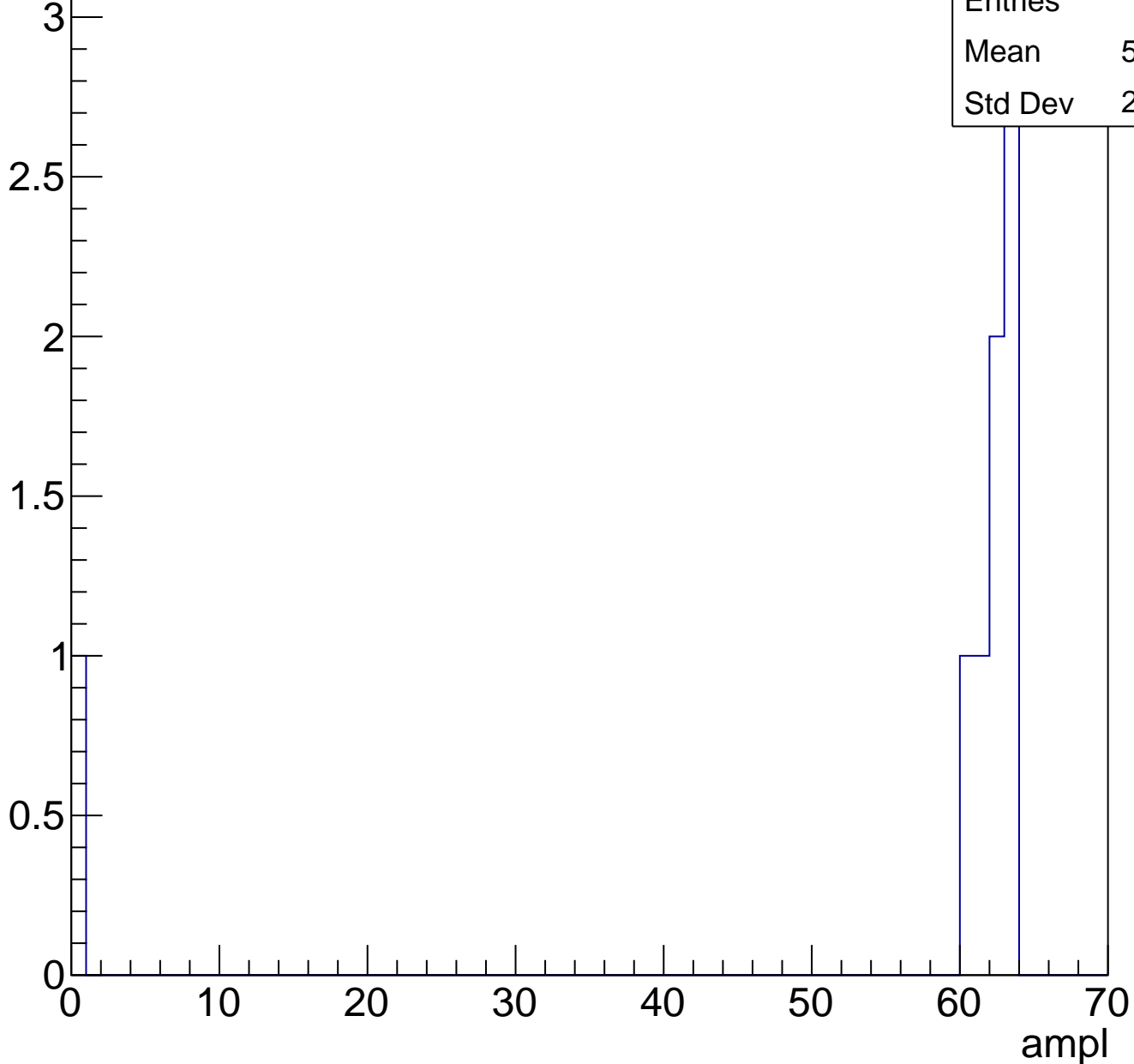


Entries	57
Mean	59.65
Std Dev	2.351

B0L002S, U9-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	8
Mean	54.25
Std Dev	20.53

B0L002S, U9-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch89, adc0

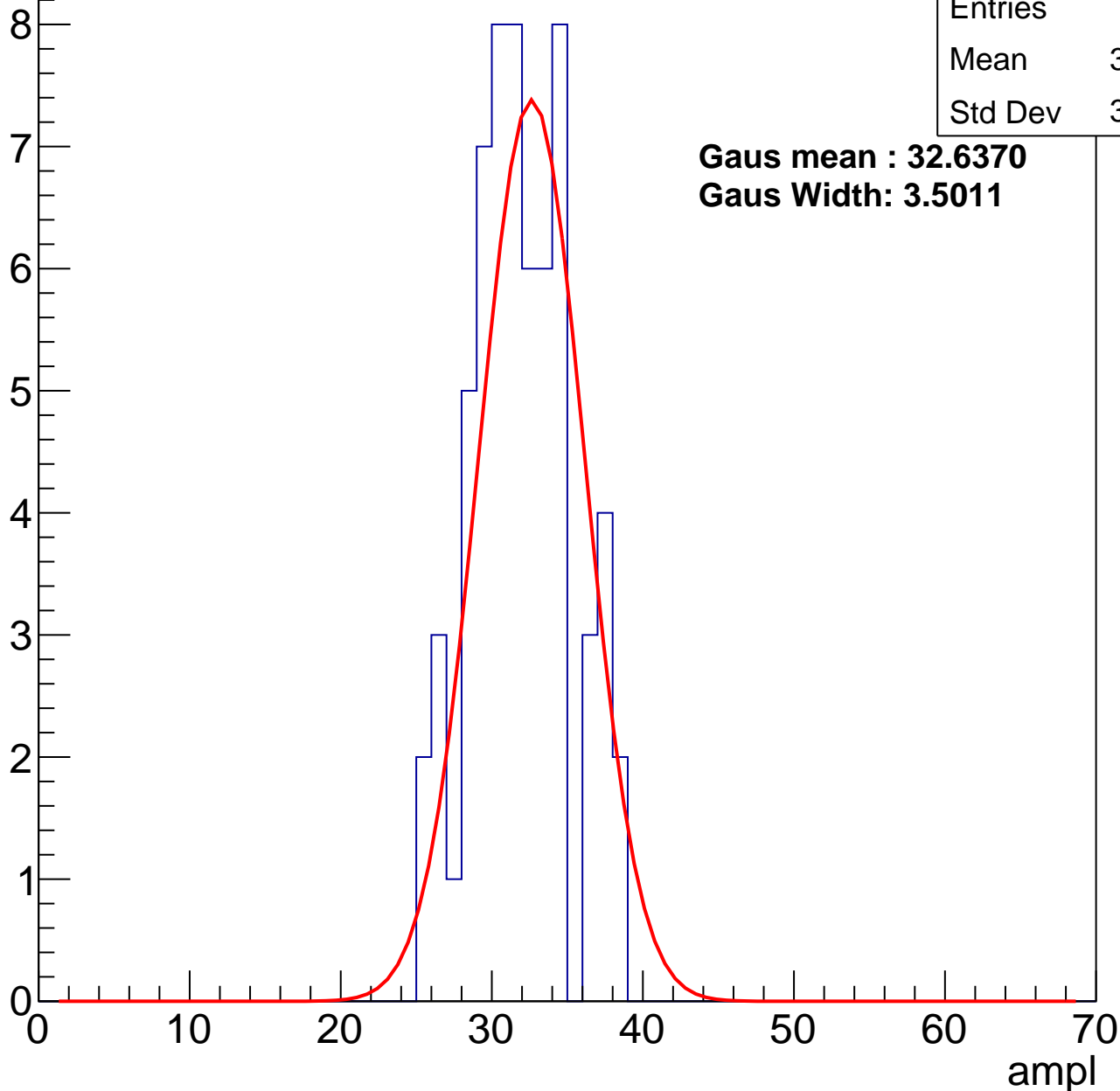
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	31.43
Std Dev	3.216

Gaus mean : 32.6370

Gaus Width: 3.5011



B0L002S, U9-ch89, adc1

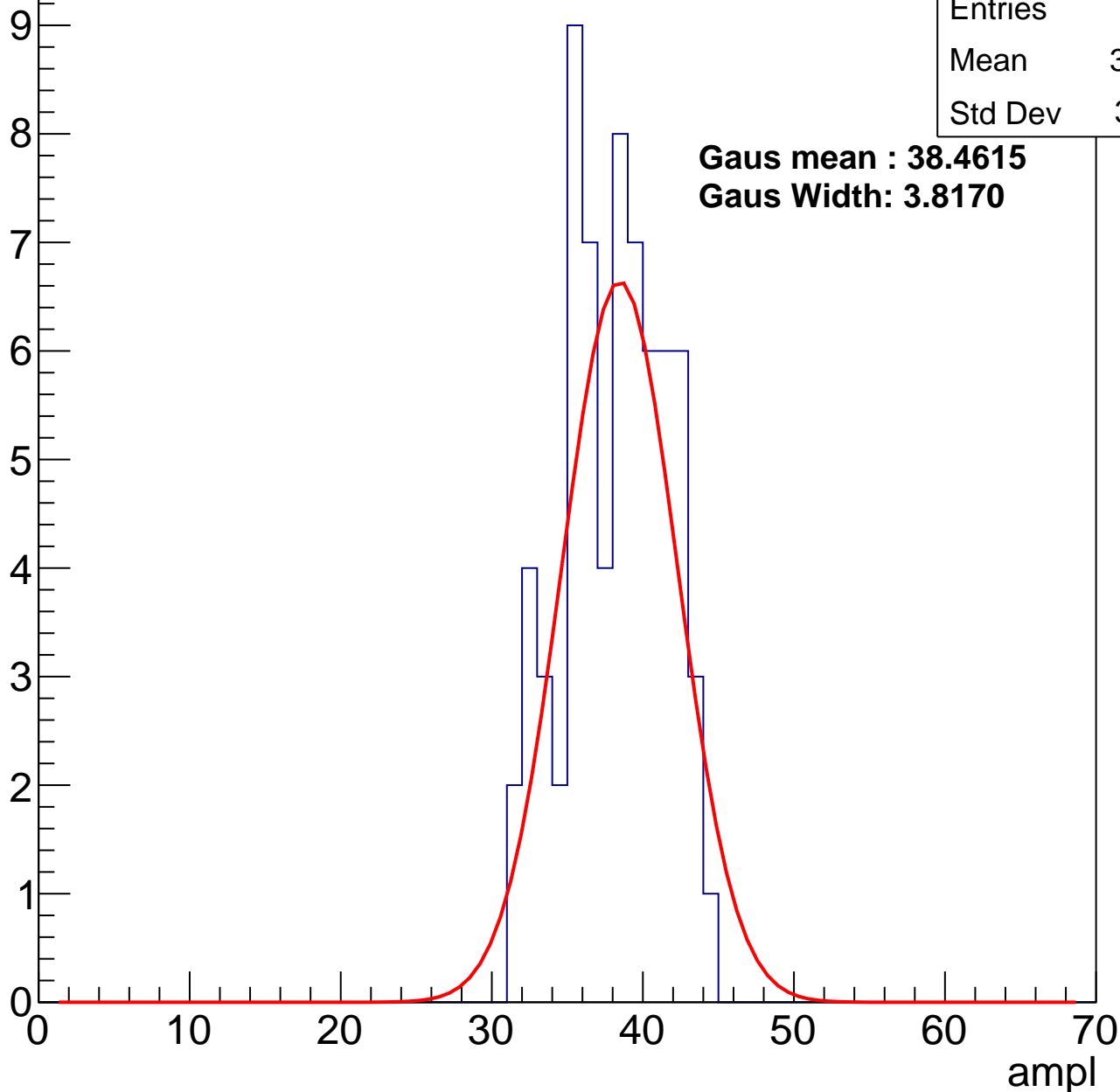
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.65
Std Dev	3.311

Gaus mean : 38.4615

Gaus Width: 3.8170



B0L002S, U9-ch89, adc2

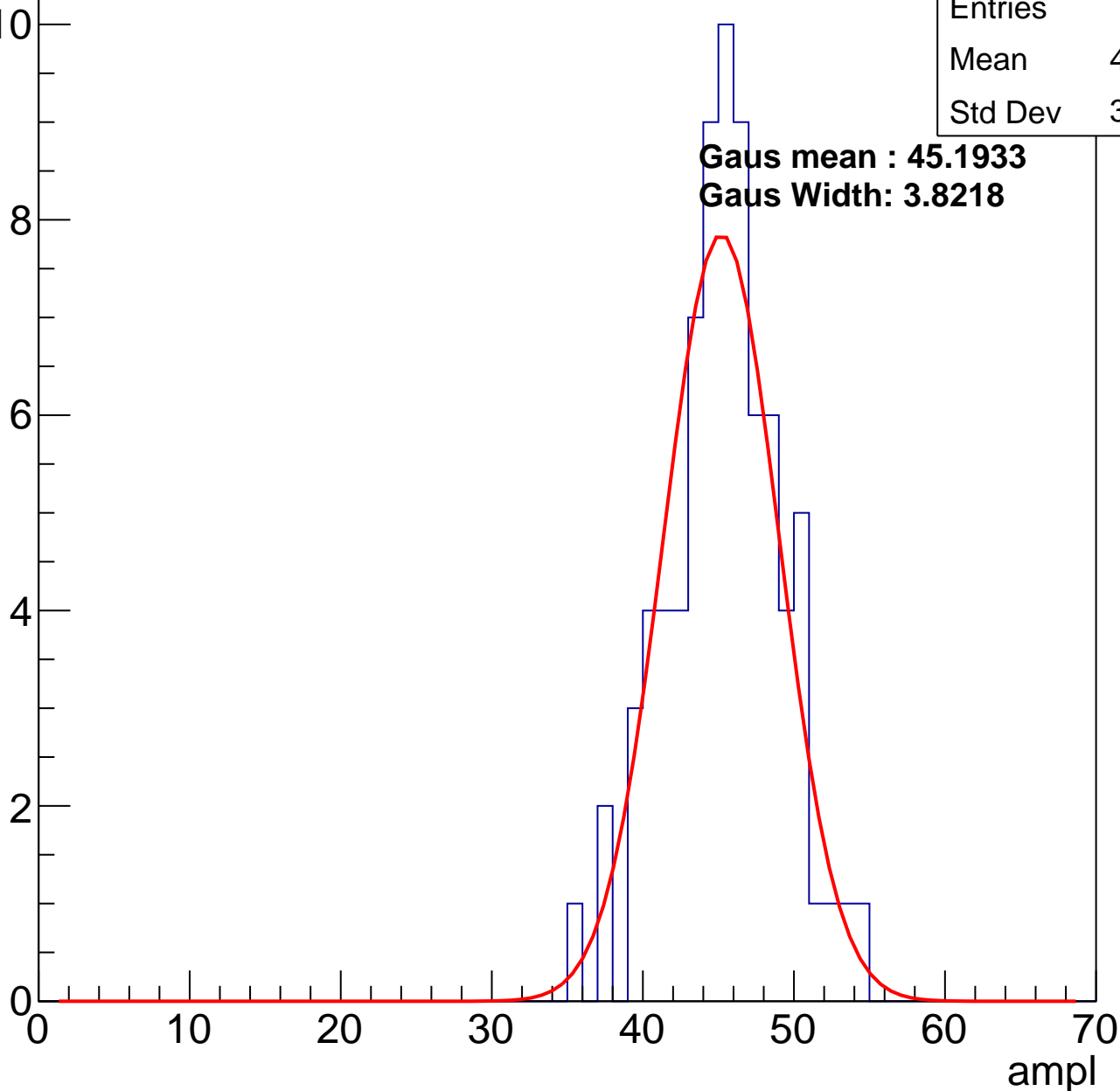
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	44.94
Std Dev	3.719

Gaus mean : 45.1933

Gaus Width: 3.8218

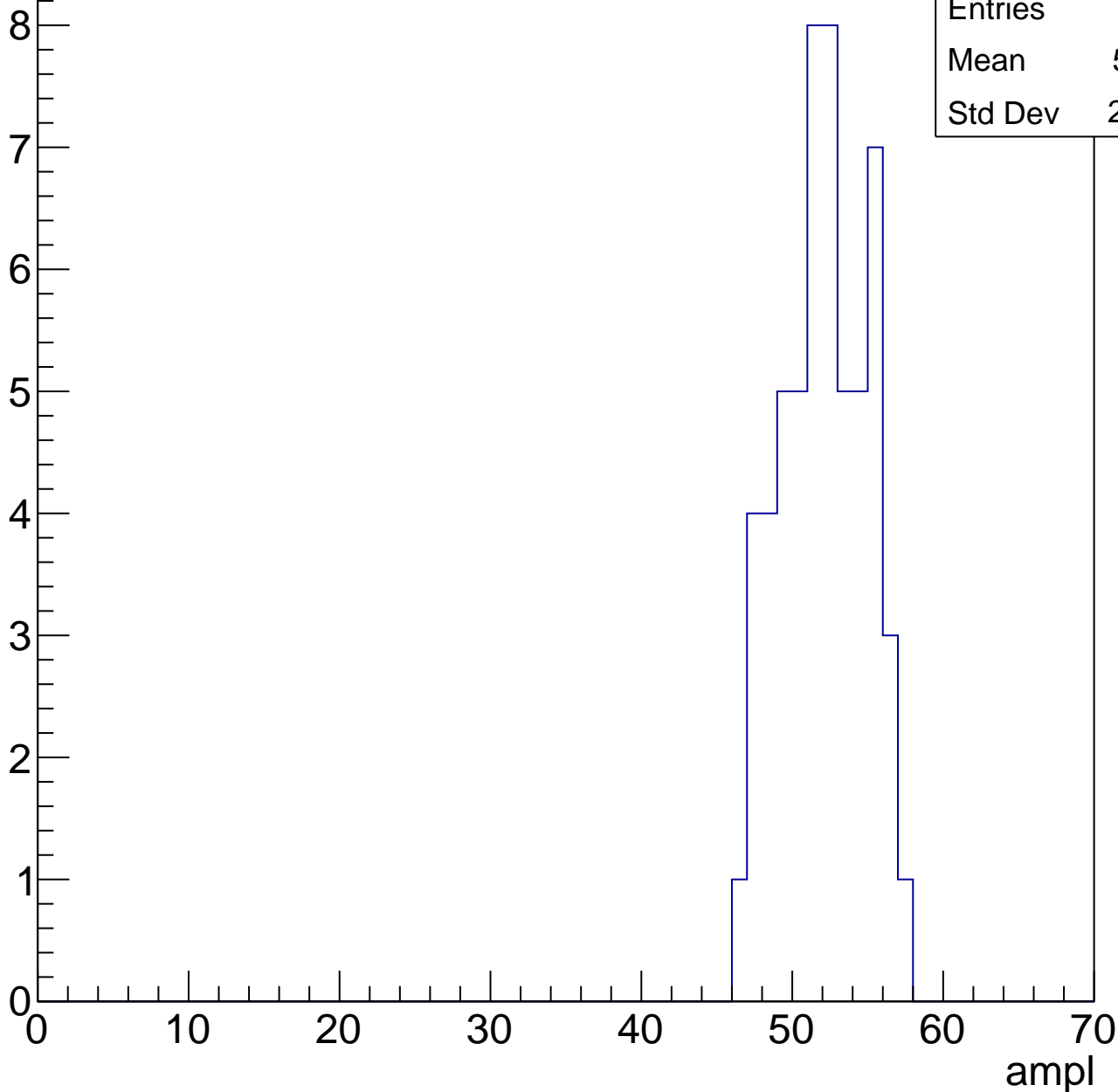


B0L002S, U9-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	51.61
Std Dev	2.756

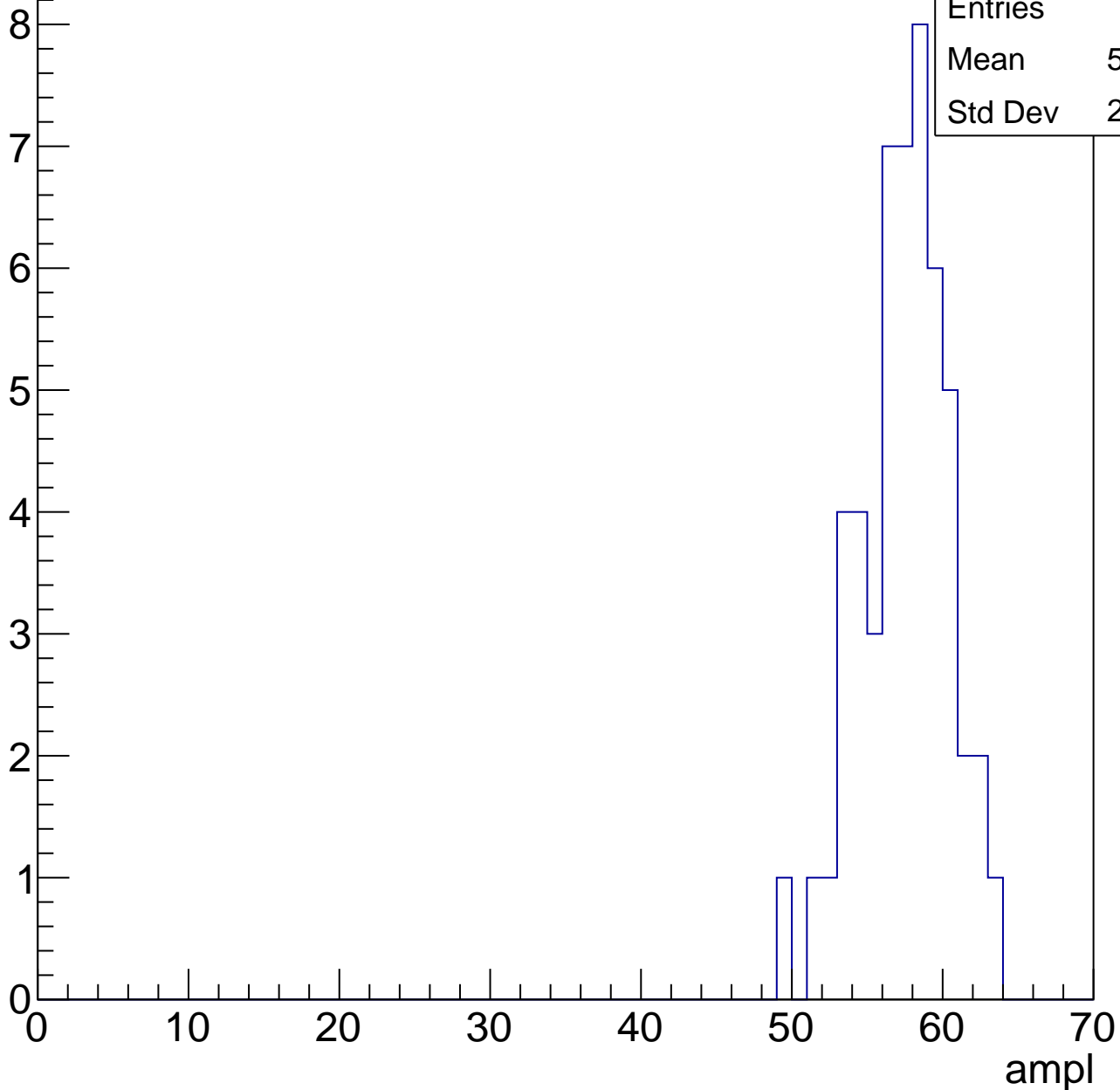


B0L002S, U9-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

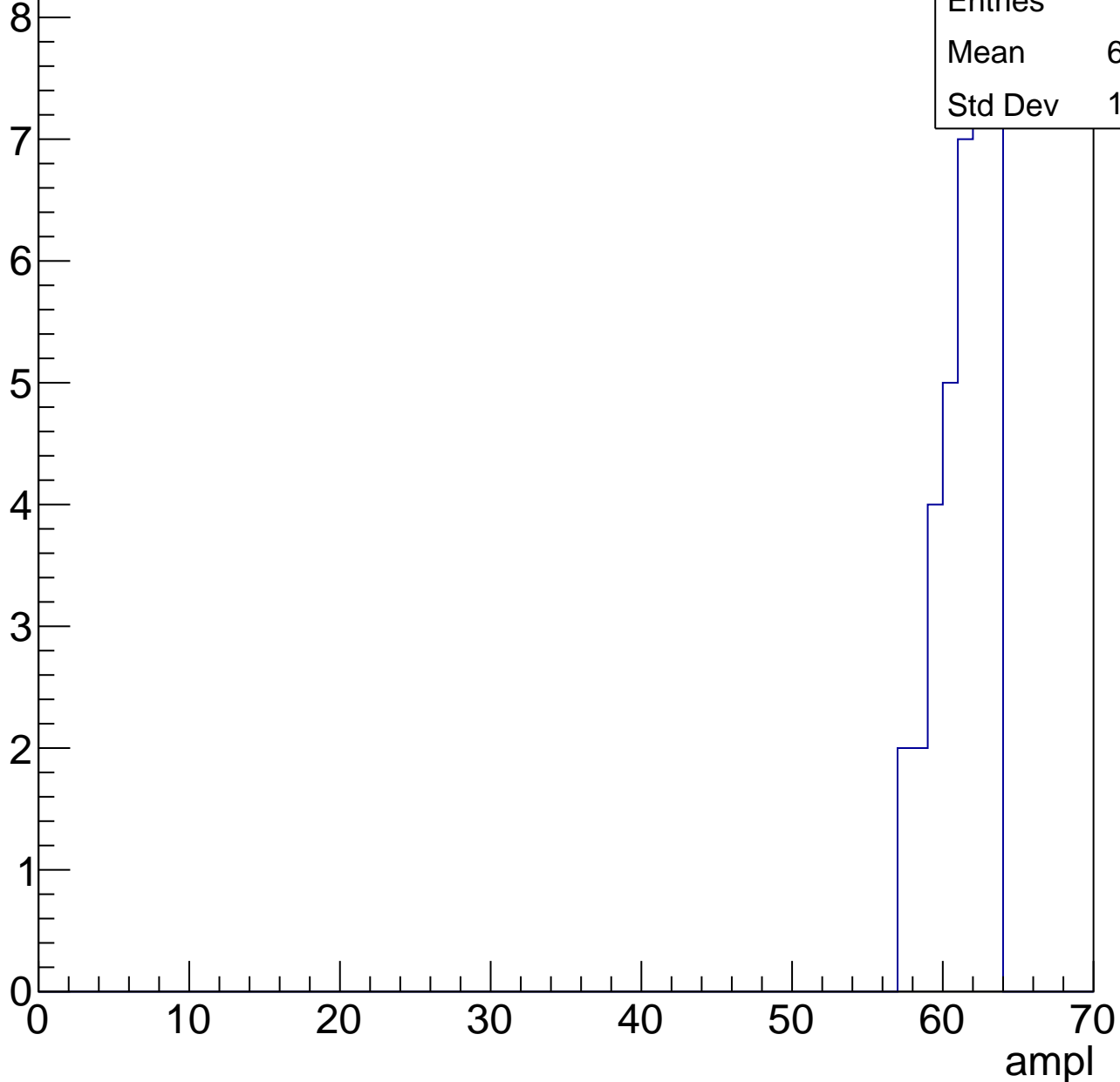
Entries	52
Mean	56.98
Std Dev	2.906



B0L002S, U9-ch89, adc5

calib_packv5_042523_0143.root, FC#8, port C1

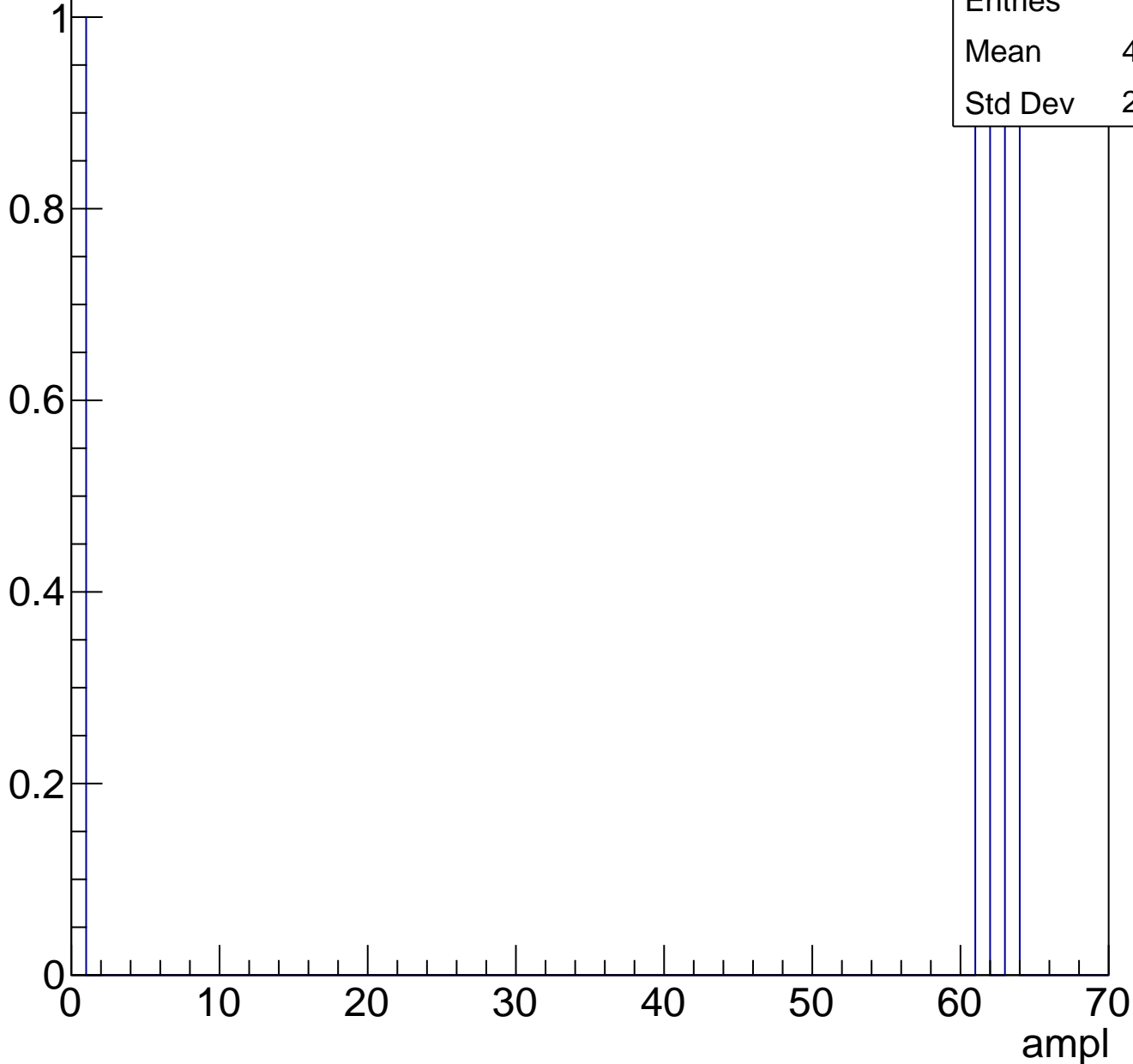
Entry



B0L002S, U9-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch90, adc0

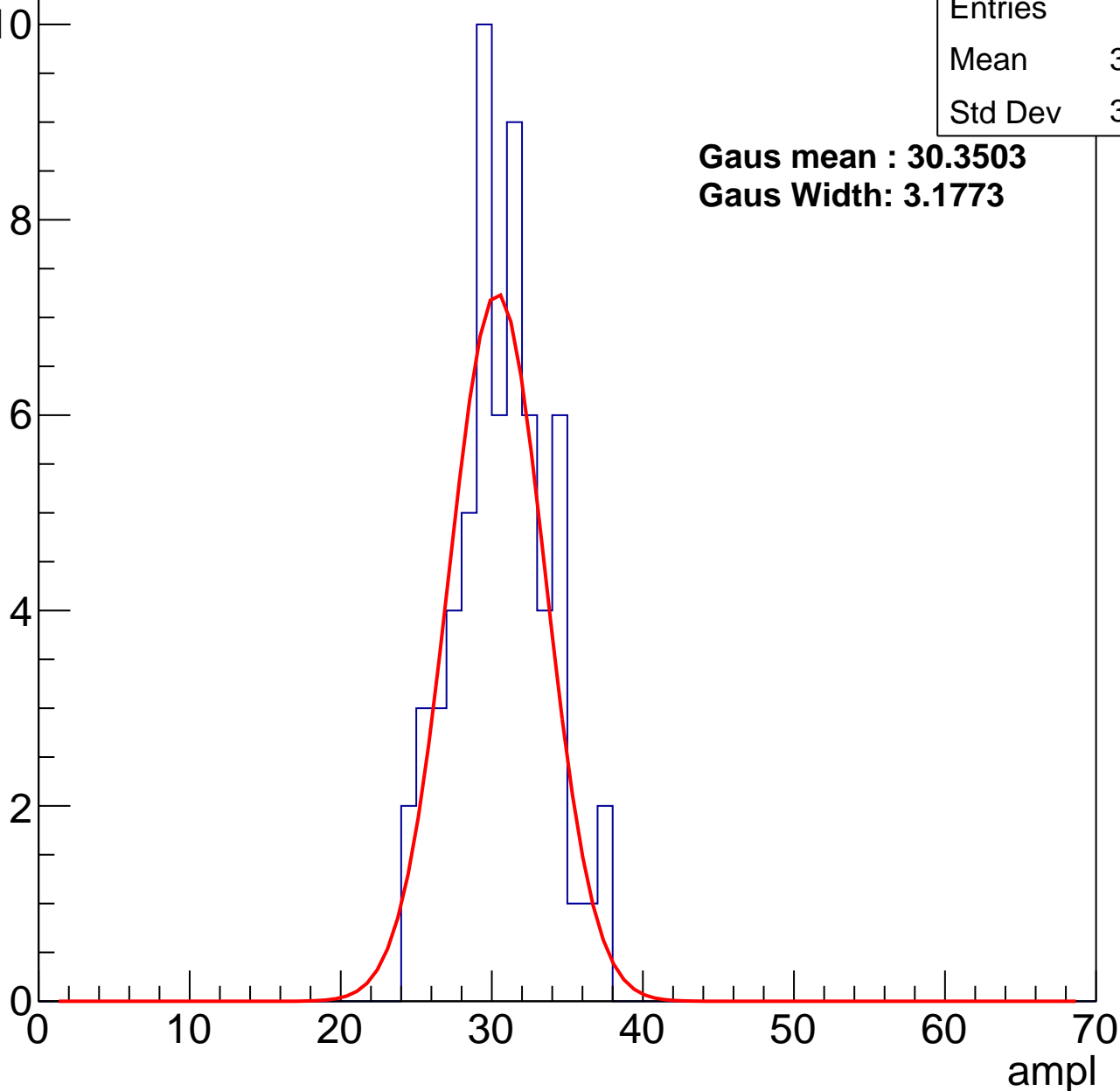
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.18
Std Dev	3.067

Gaus mean : 30.3503

Gaus Width: 3.1773



B0L002S, U9-ch90, adc1

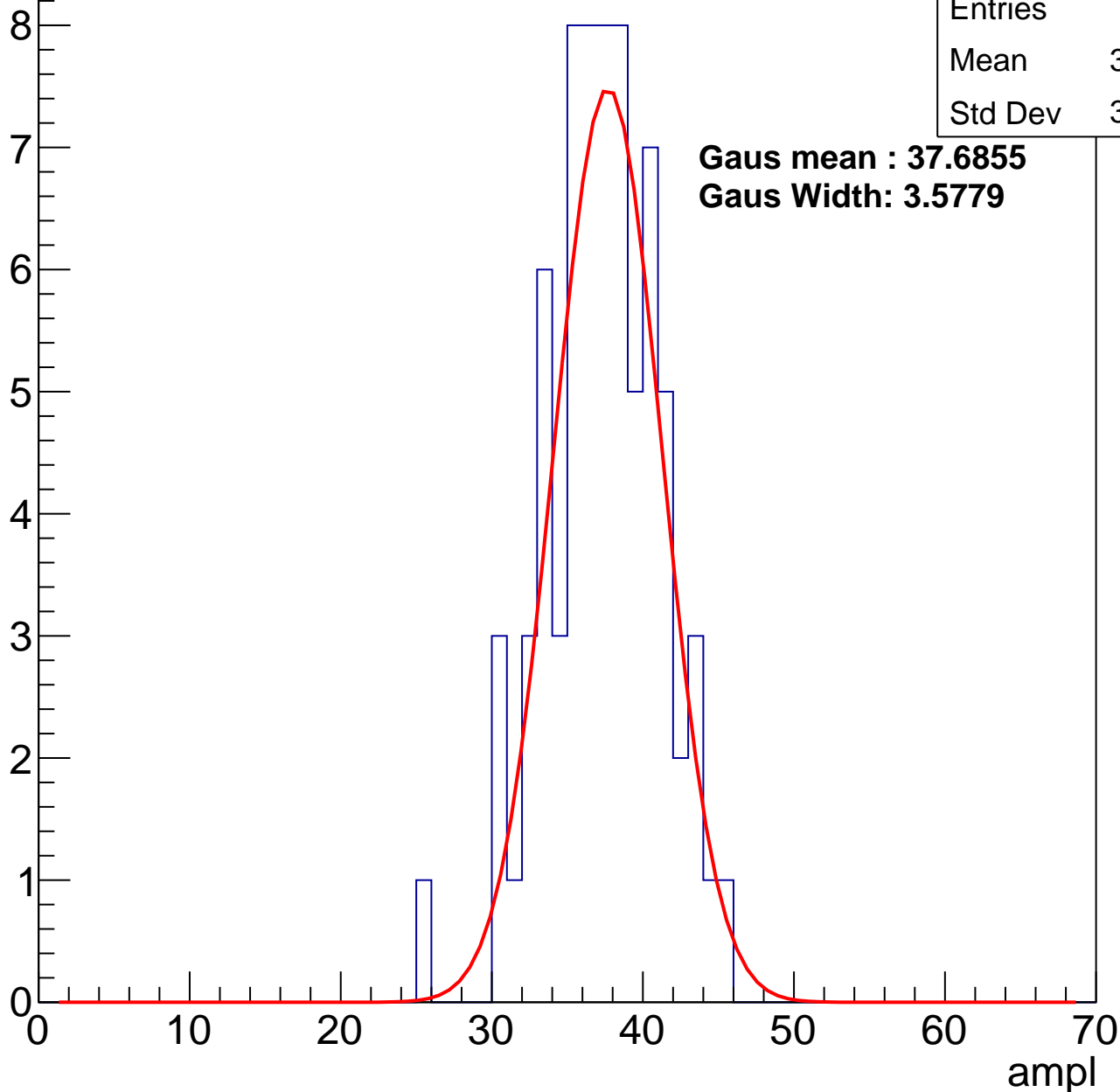
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	36.88
Std Dev	3.712

Gaus mean : 37.6855

Gaus Width: 3.5779



B0L002S, U9-ch90, adc2

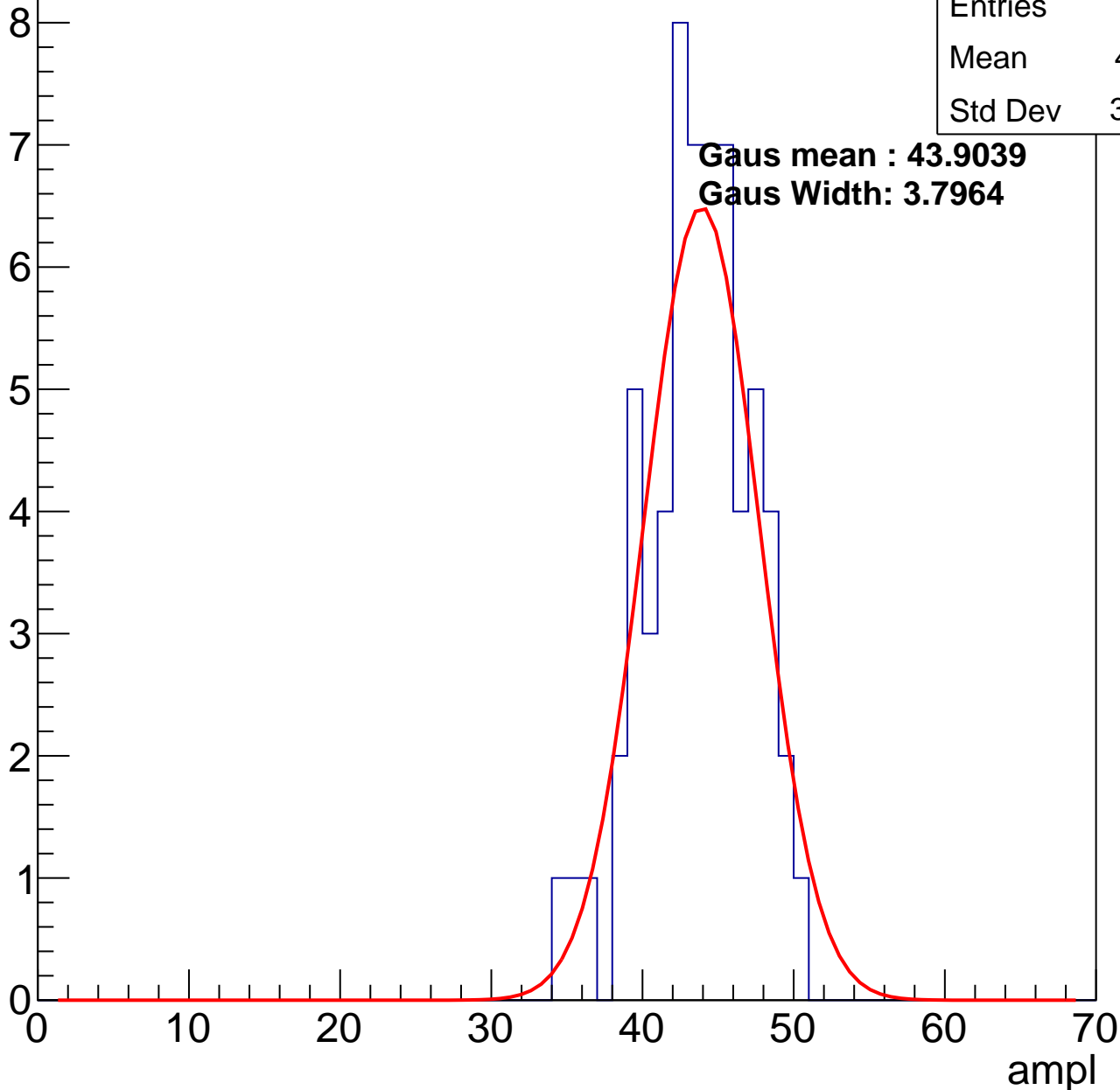
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.21
Std Dev	3.465

Gaus mean : 43.9039

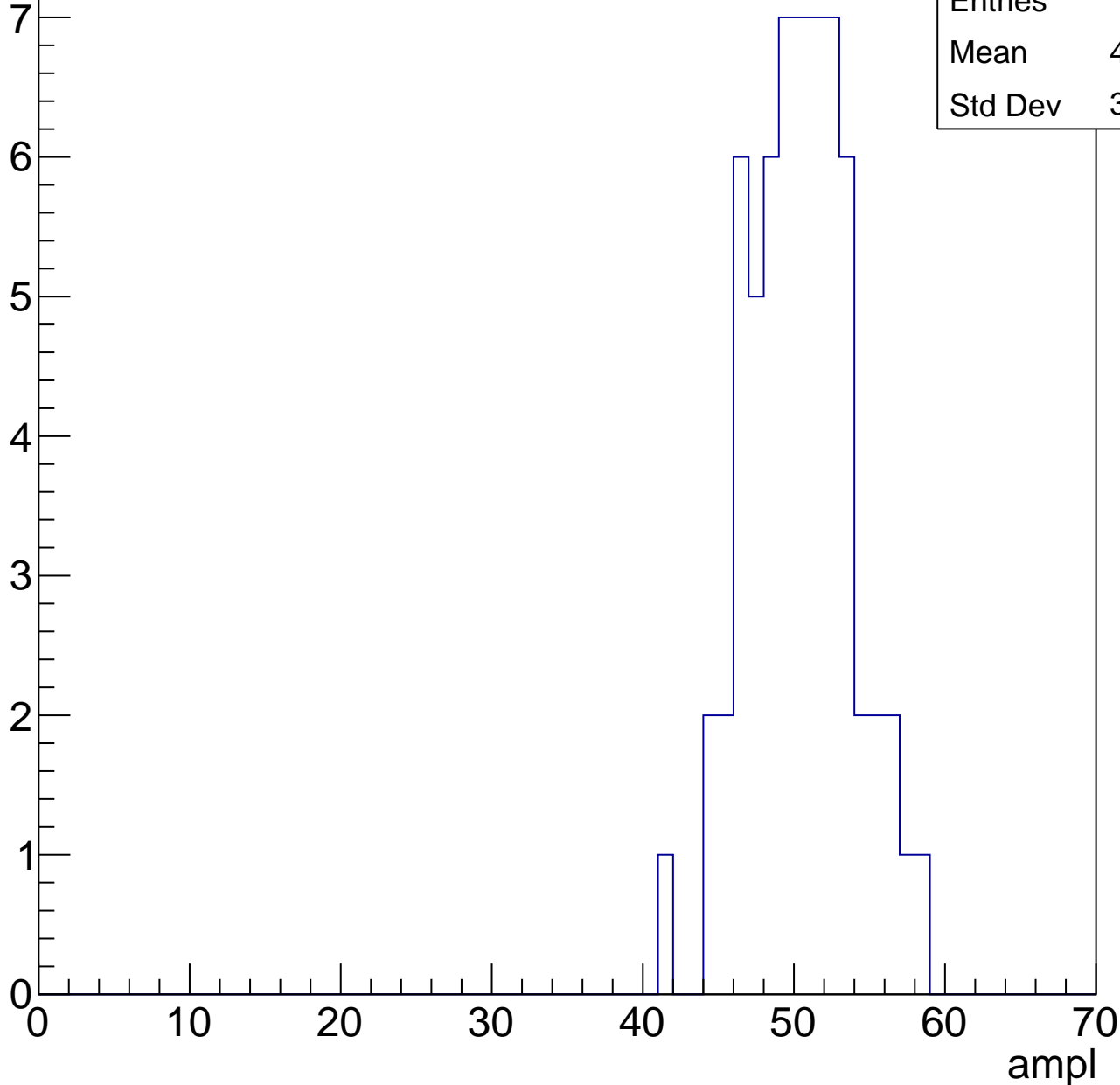
Gaus Width: 3.7964



B0L002S, U9-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

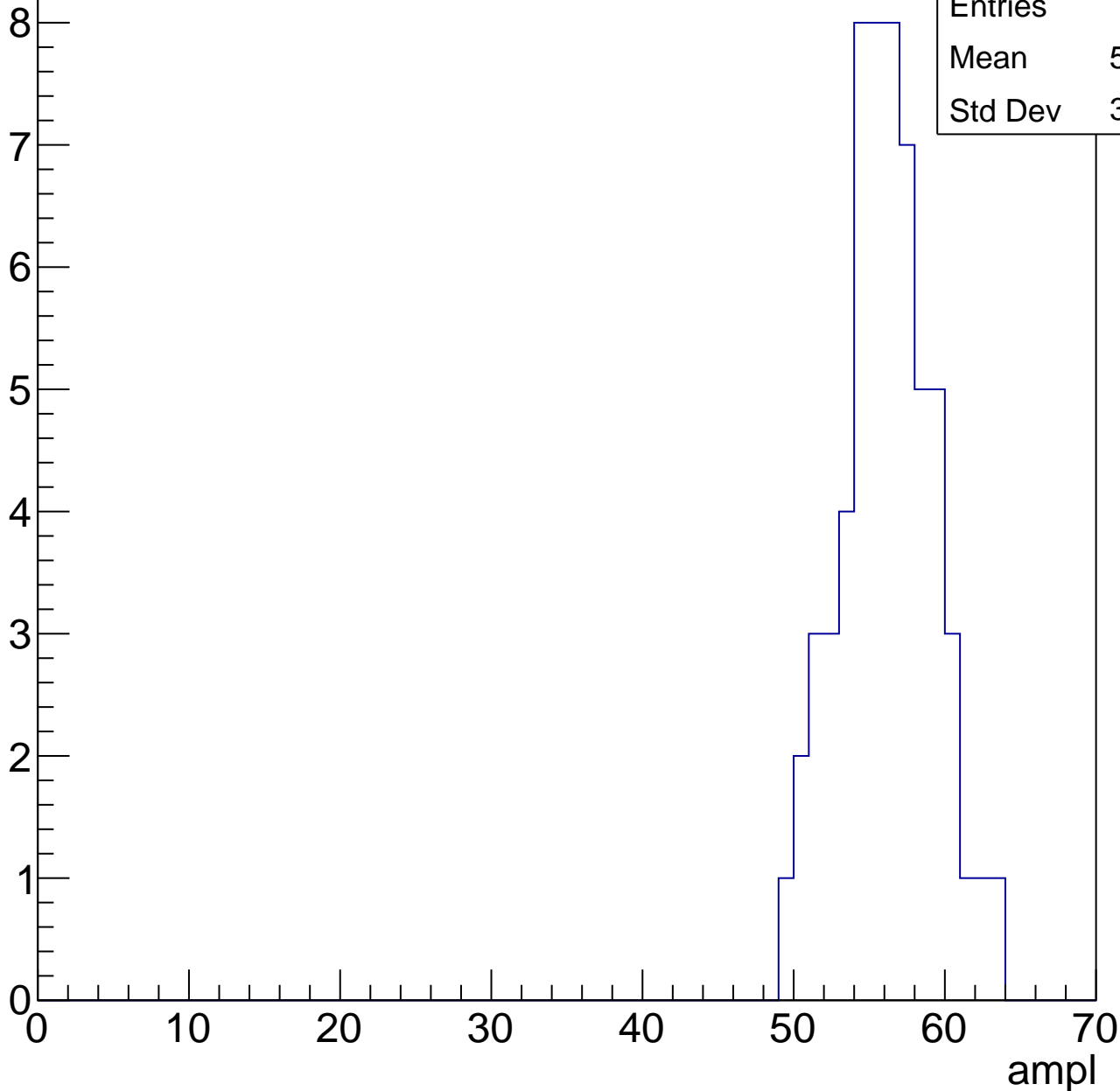


B0L002S, U9-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	55.67
Std Dev	3.009



B0L002S, U9-ch90, adc5

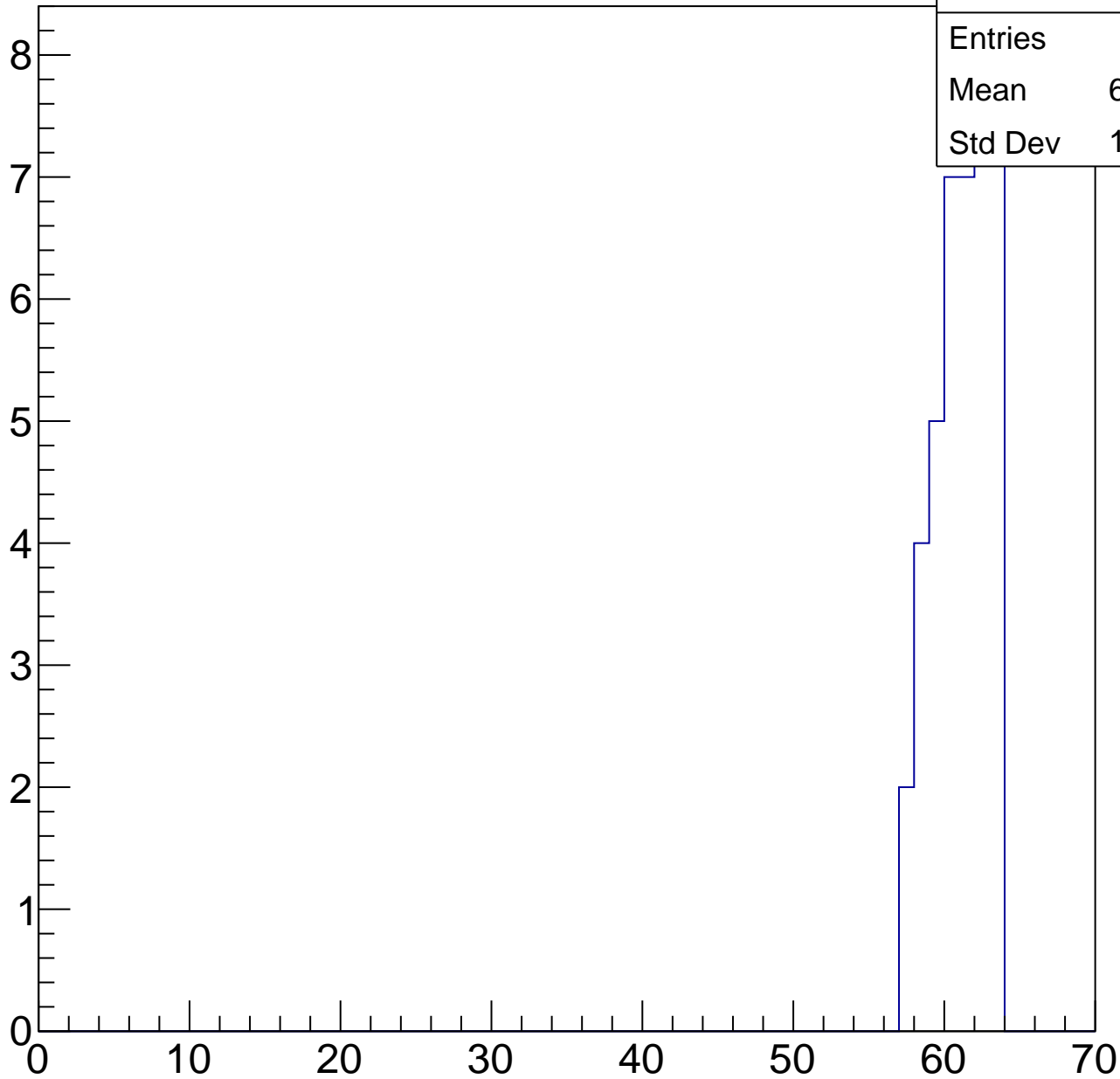
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.68
Std Dev	1.787

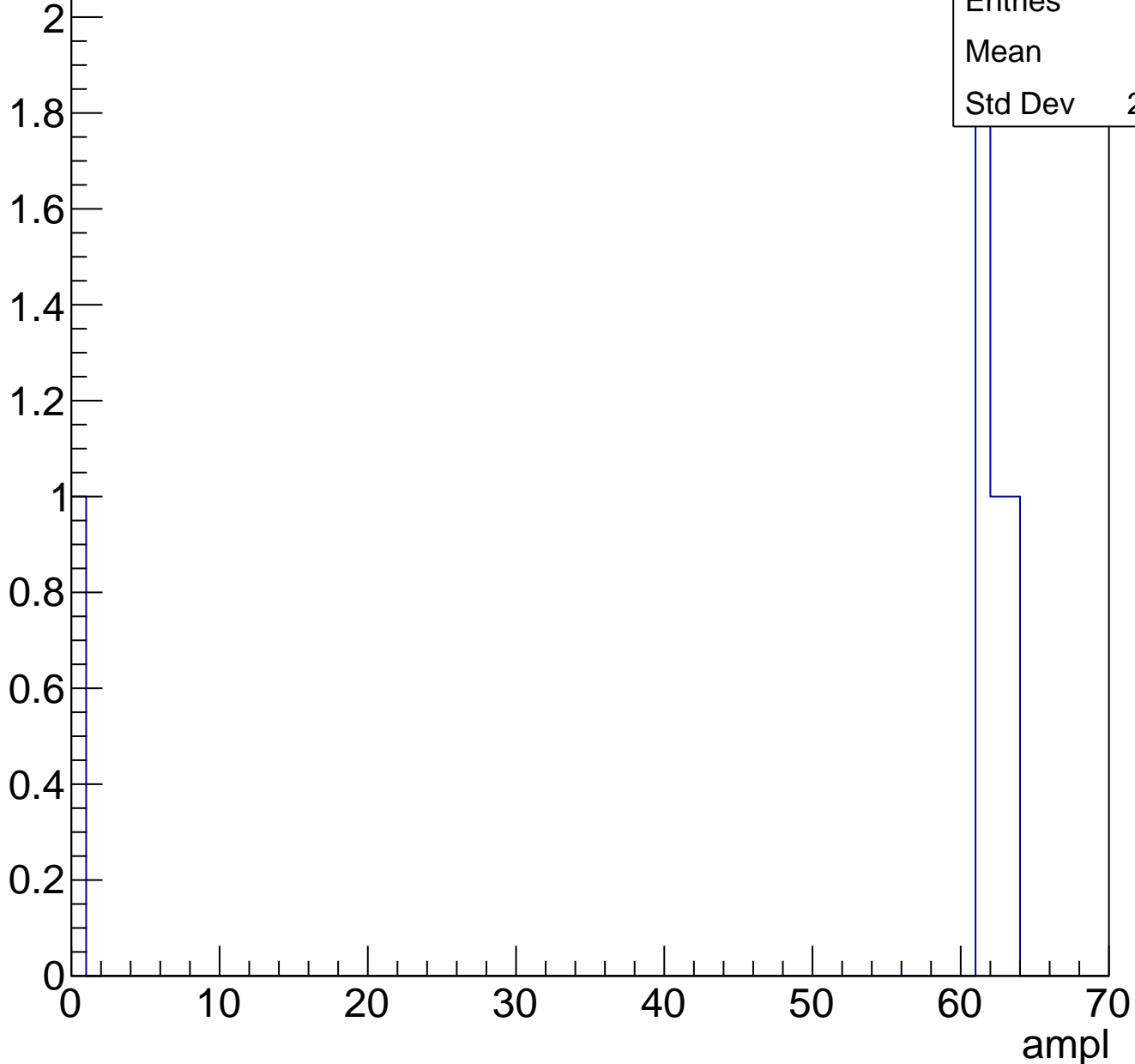
ampl



B0L002S, U9-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	49.4
Std Dev	24.71

B0L002S, U9-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch91, adc0

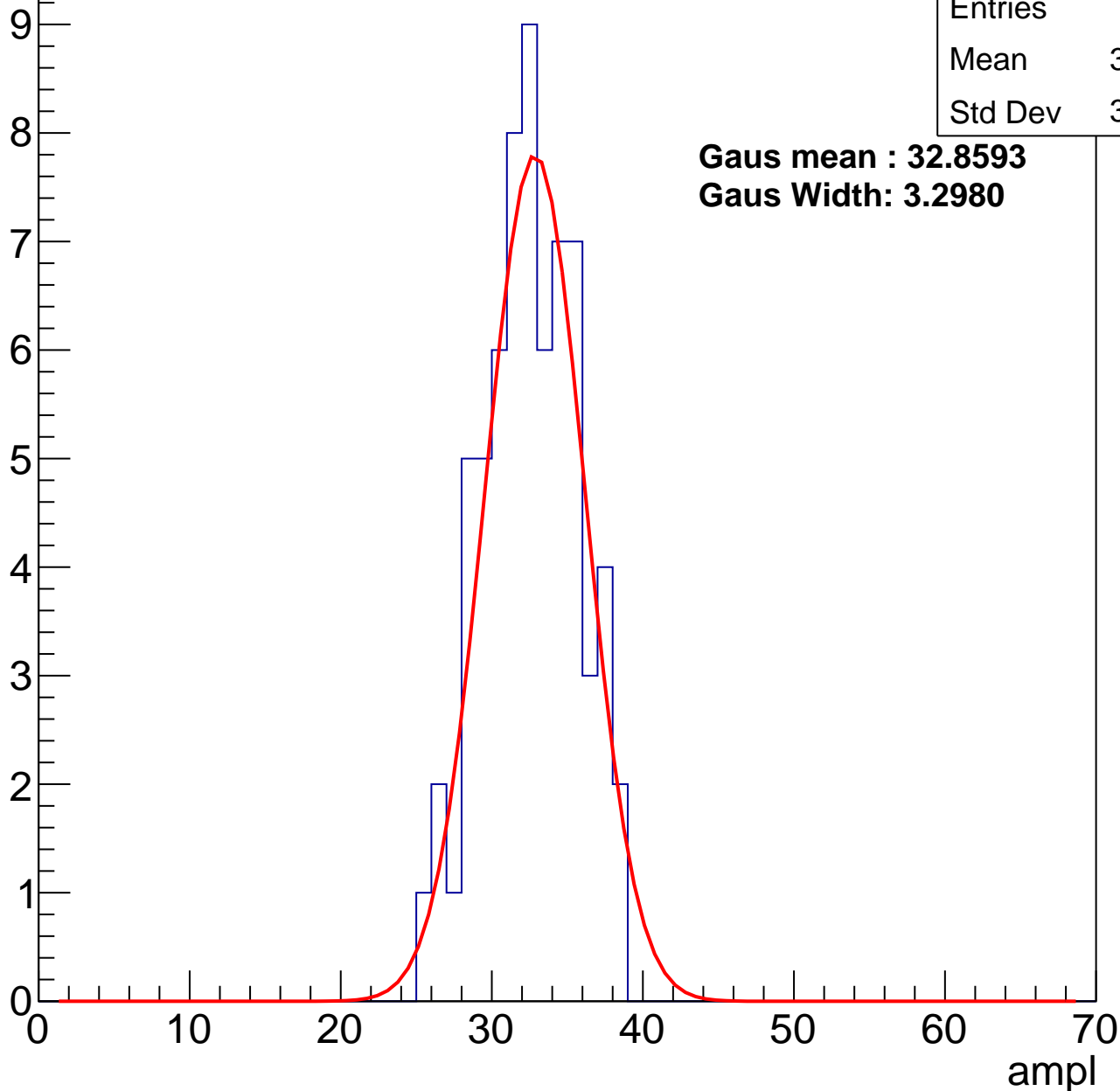
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	32.09
Std Dev	3.064

Gaus mean : 32.8593

Gaus Width: 3.2980



B0L002S, U9-ch91, adc1

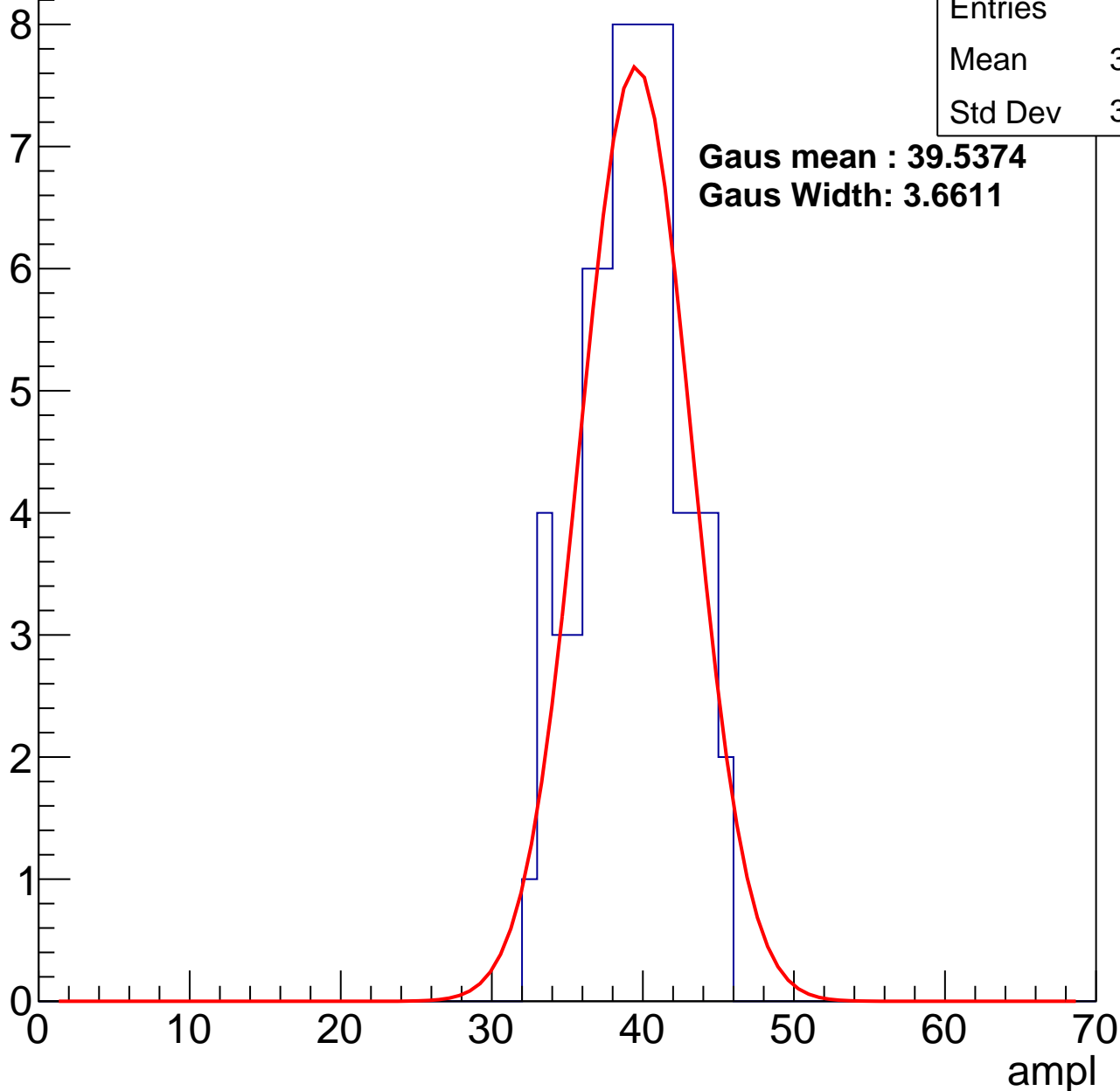
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	38.83
Std Dev	3.212

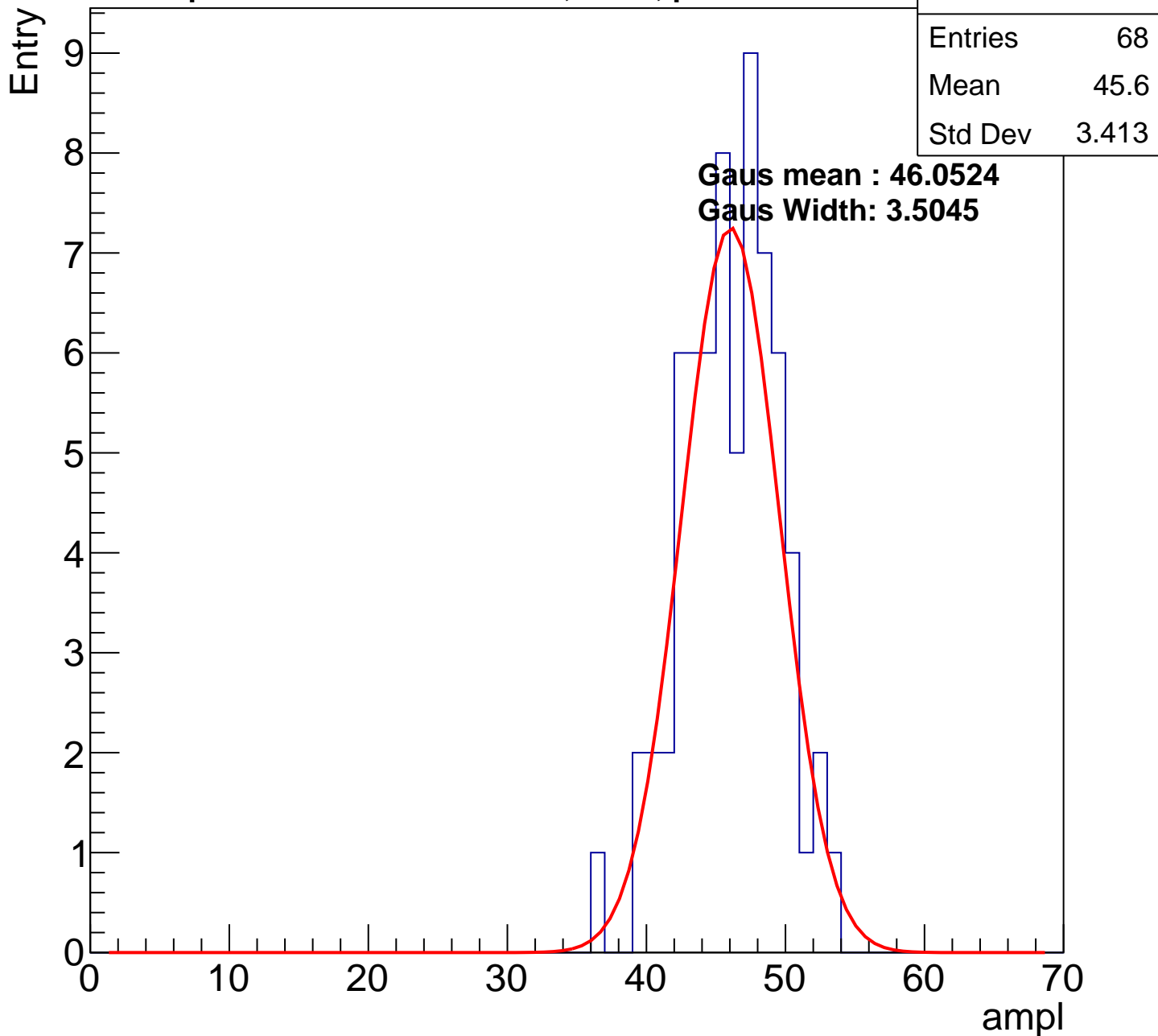
Gaus mean : 39.5374

Gaus Width: 3.6611



B0L002S, U9-ch91, adc2

calib_packv5_042523_0143.root, FC#8, port C1

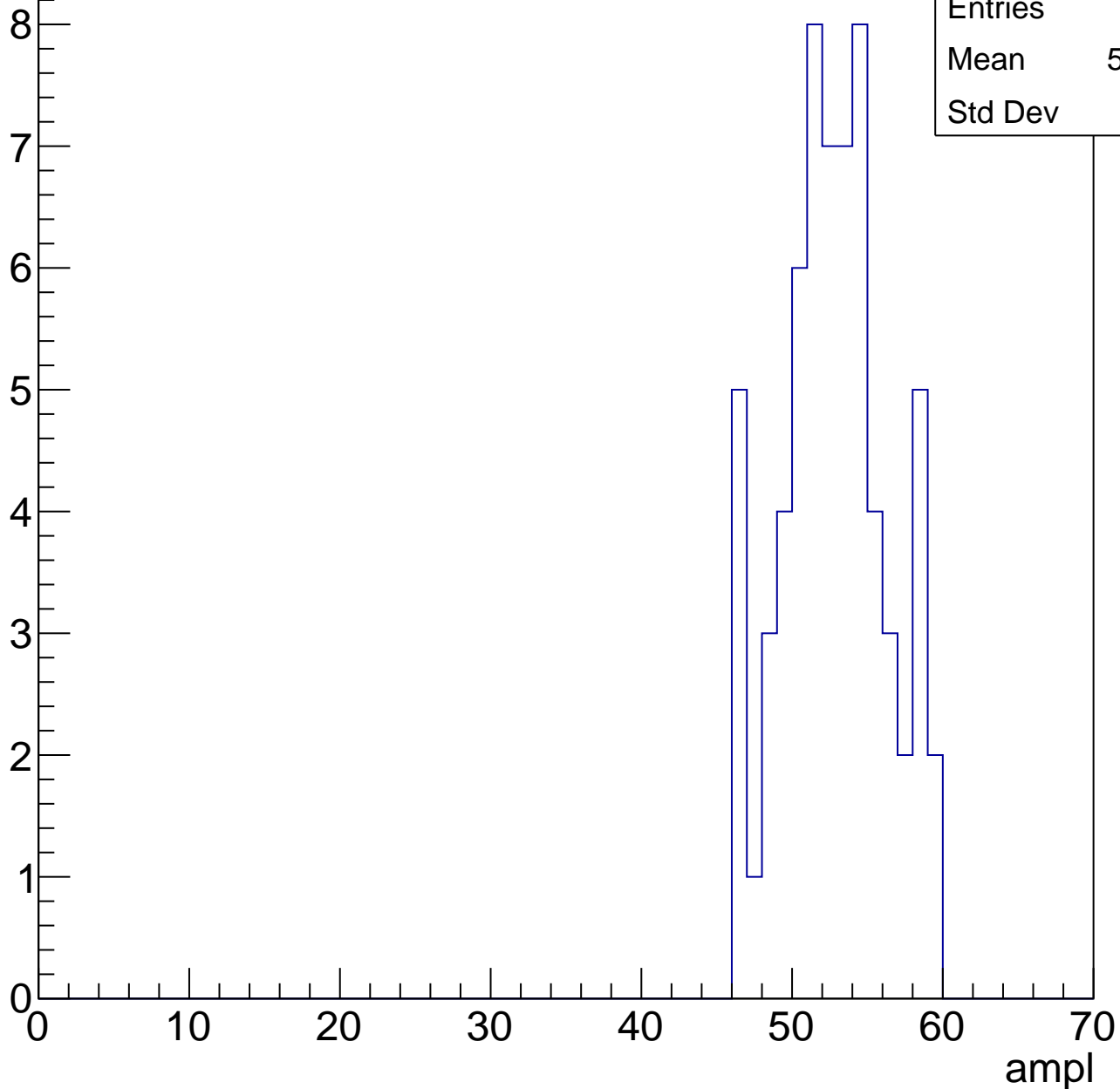


B0L002S, U9-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

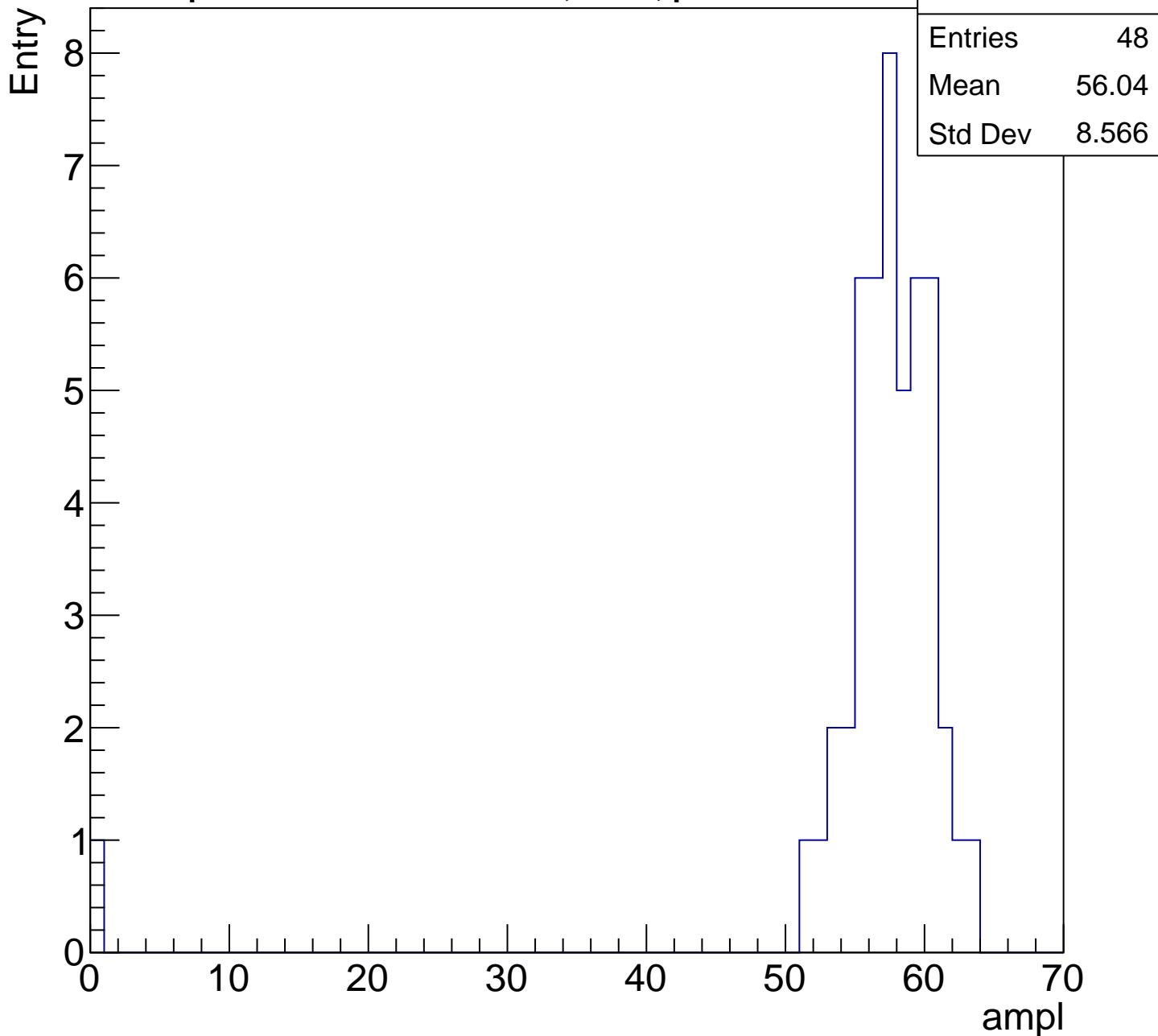
Entry

Entries	65
Mean	52.34
Std Dev	3.43



B0L002S, U9-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch91, adc5

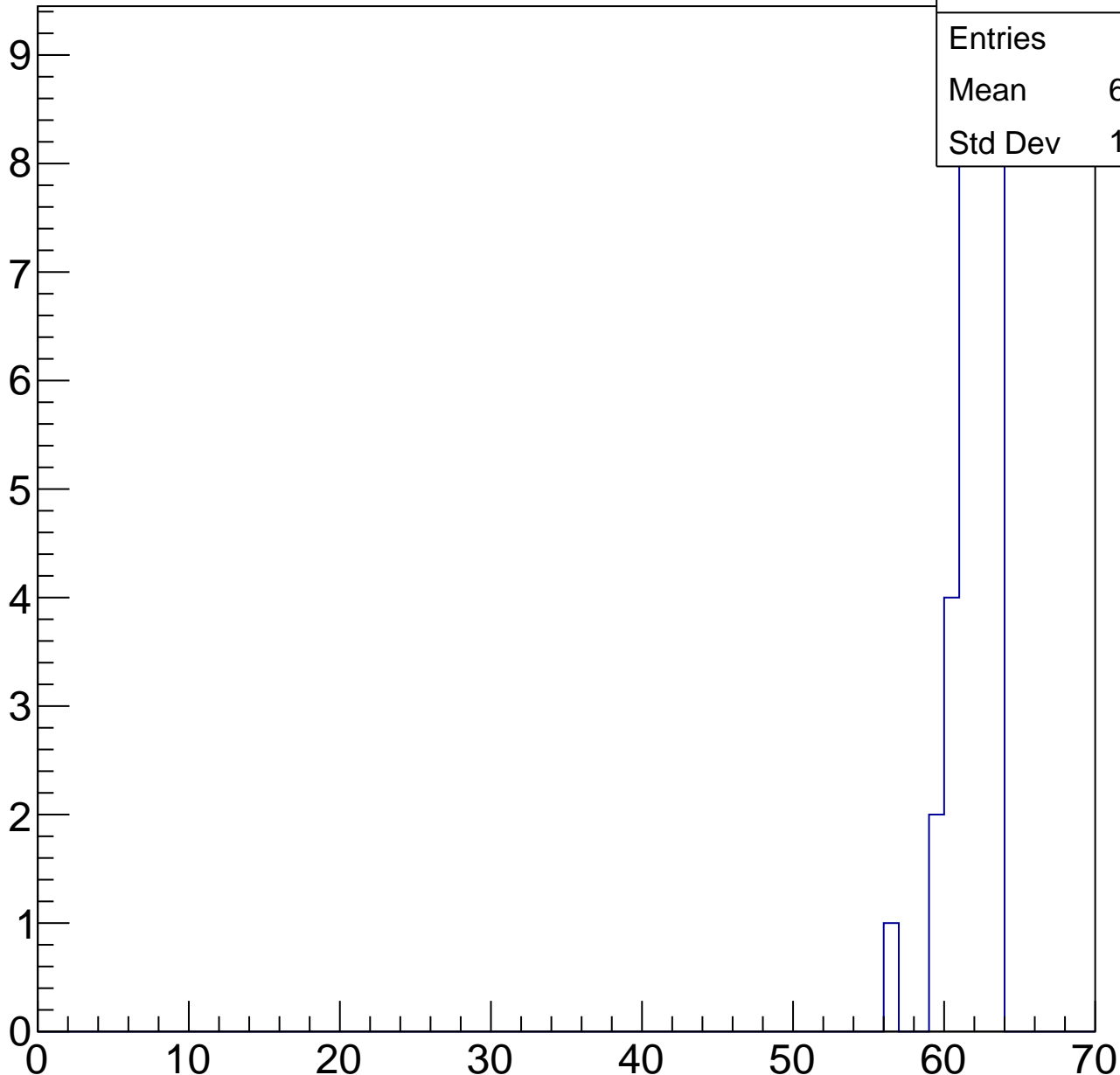
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	32
Mean	61.38
Std Dev	1.516

ampl



B0L002S, U9-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch92, adc0

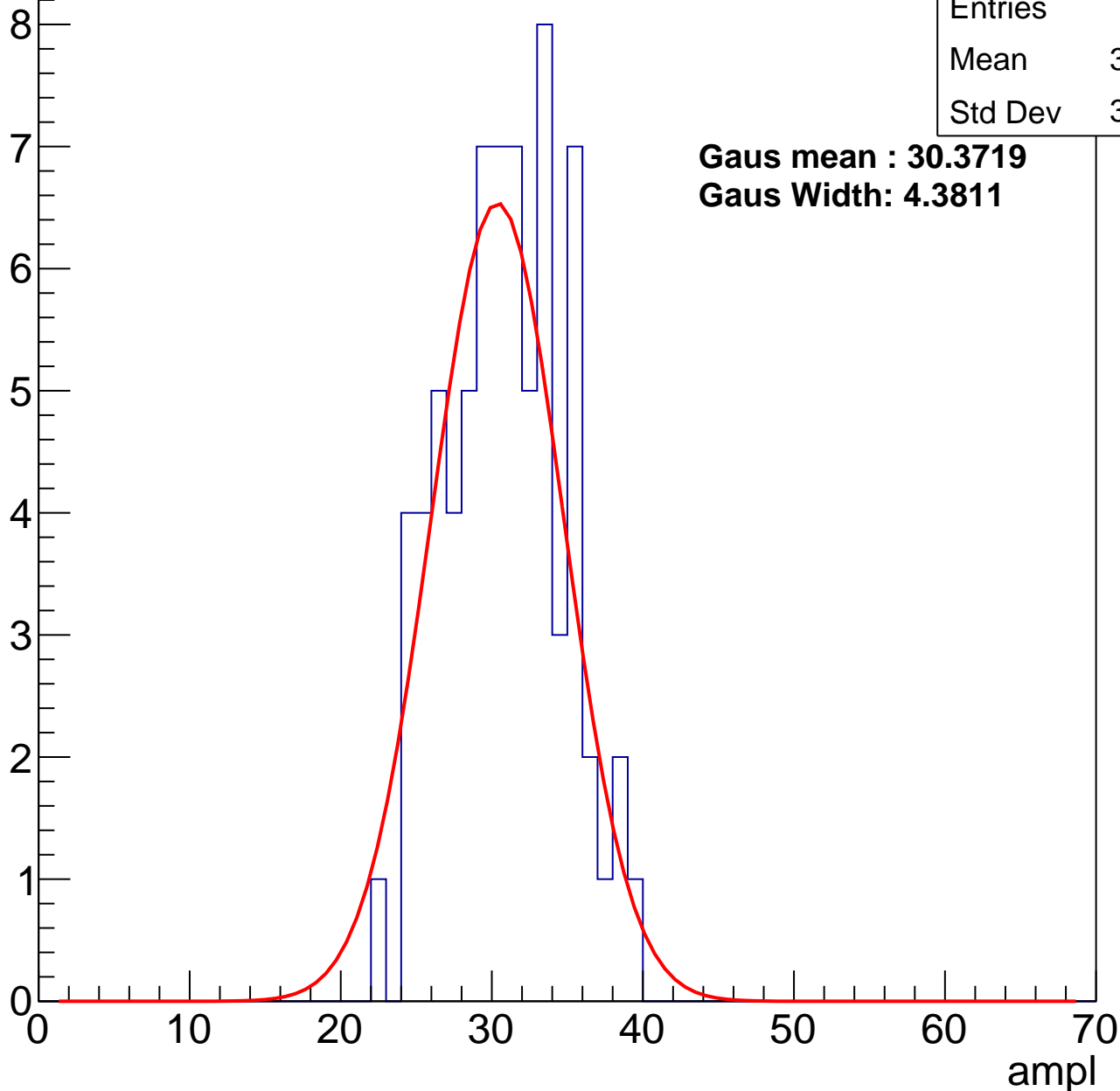
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.42
Std Dev	3.864

Gaus mean : 30.3719

Gaus Width: 4.3811



B0L002S, U9-ch92, adc1

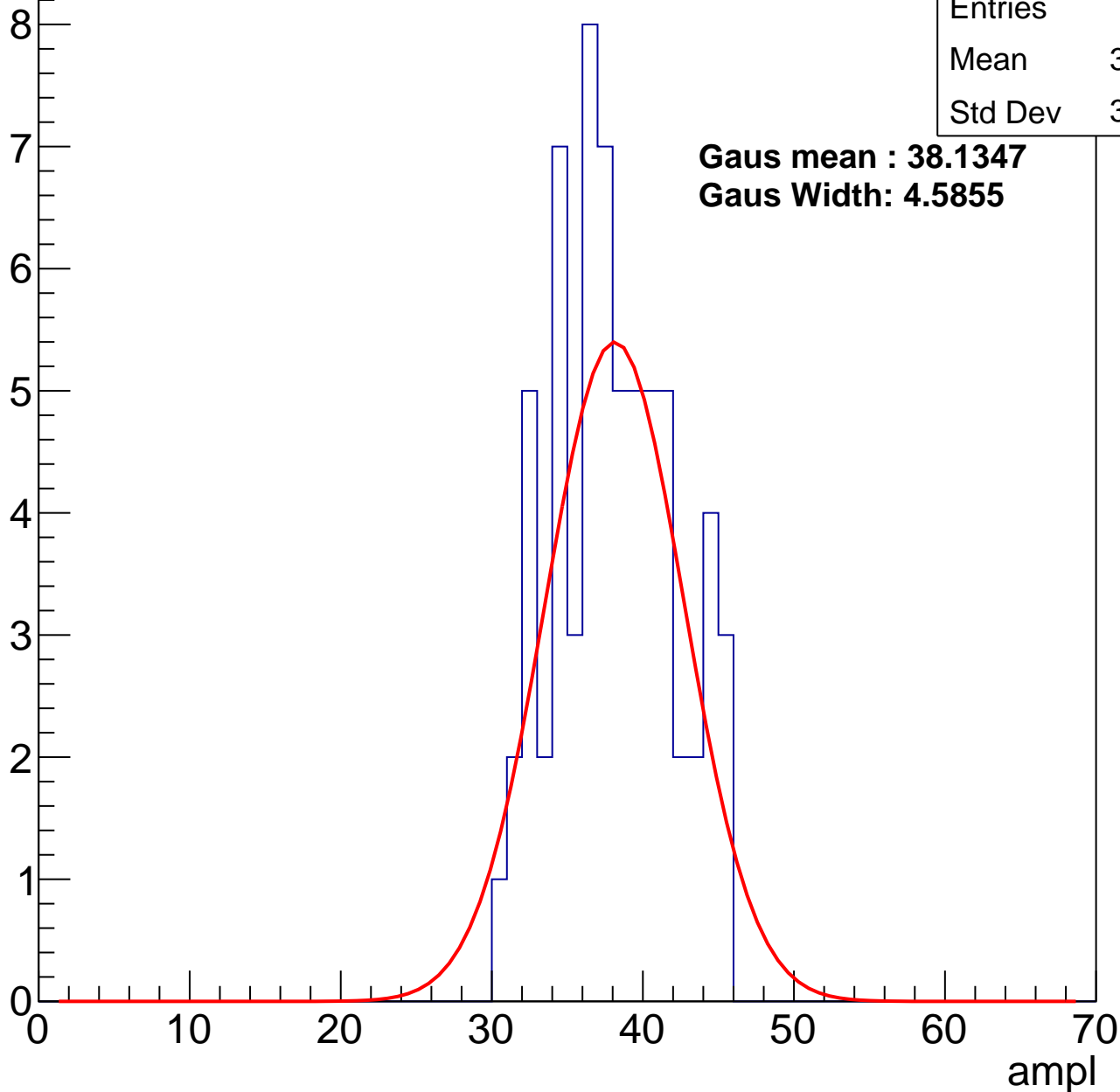
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.56
Std Dev	3.893

Gaus mean : 38.1347

Gaus Width: 4.5855



B0L002S, U9-ch92, adc2

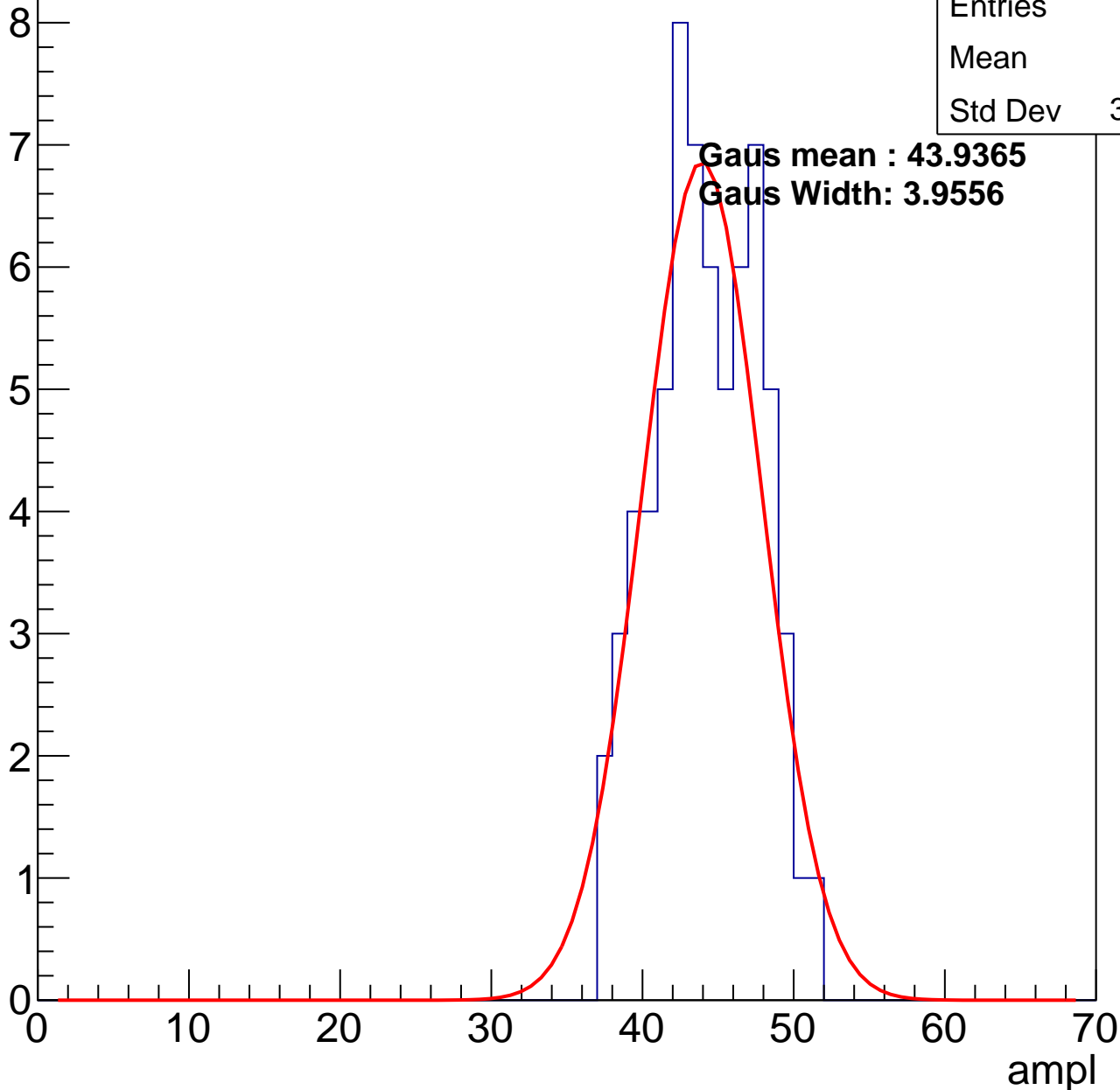
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.7
Std Dev	3.412

Gaus mean : 43.9365

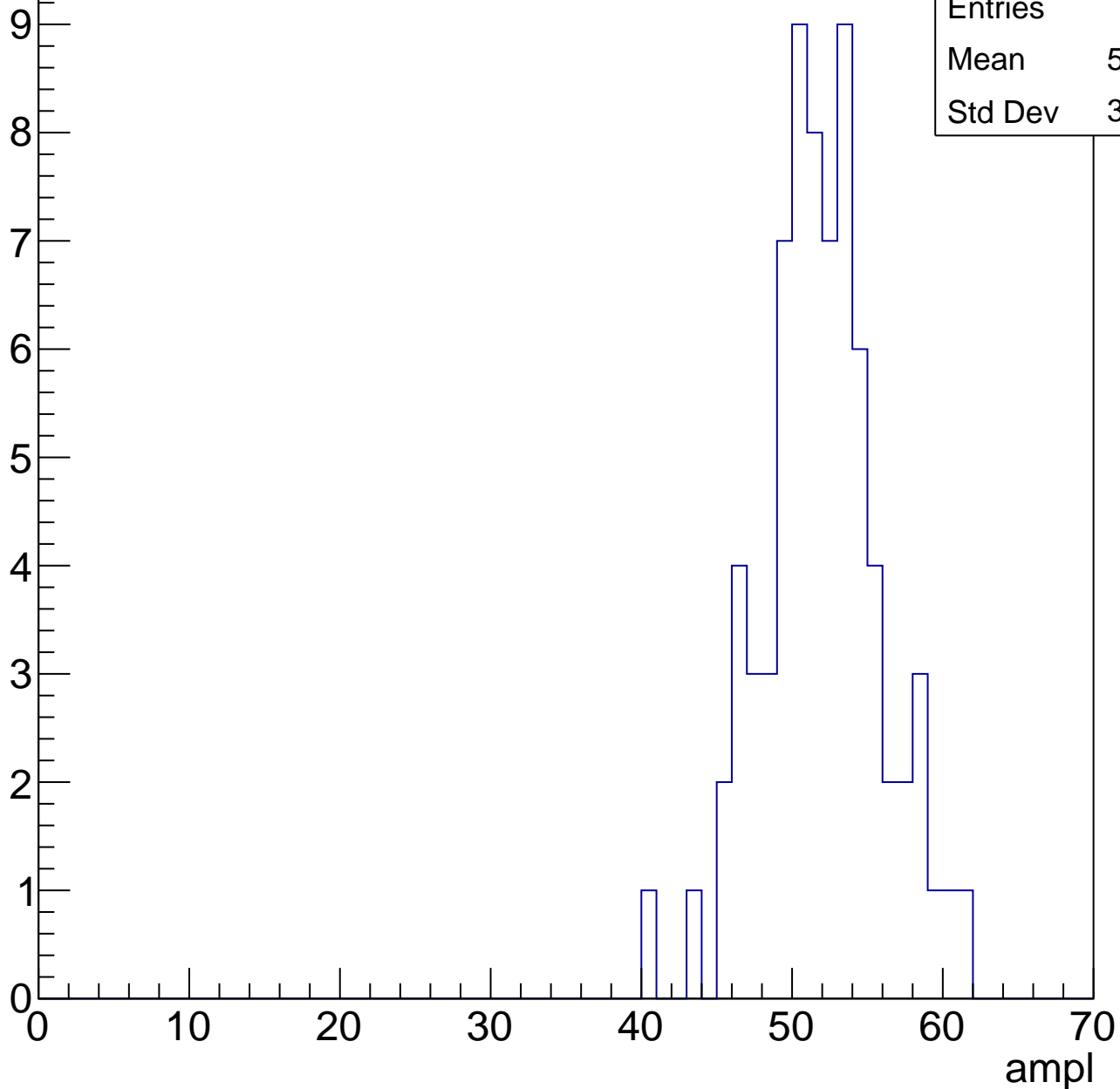
Gaus Width: 3.9556



B0L002S, U9-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



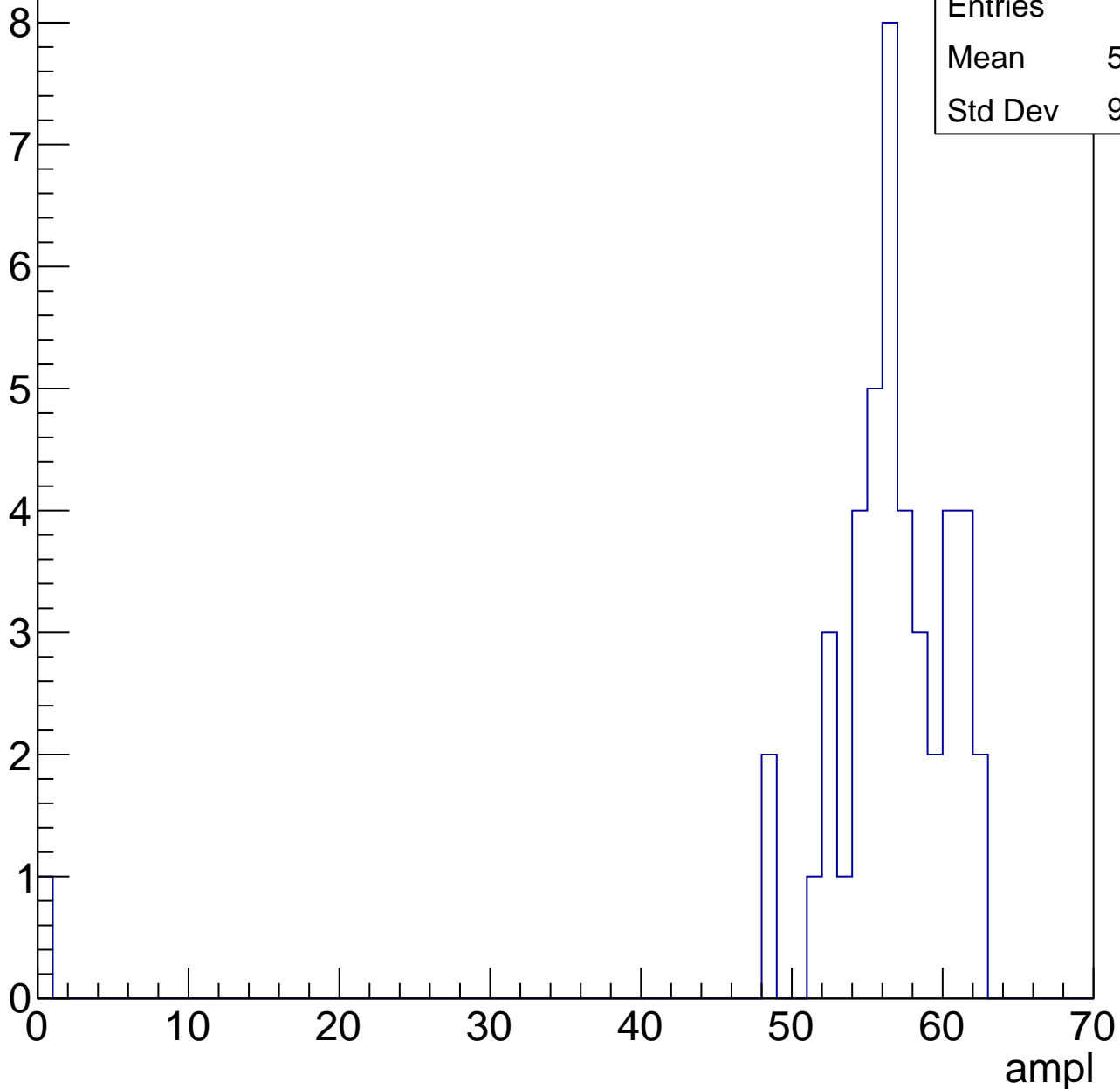
Entries	74
Mean	51.46
Std Dev	3.905

B0L002S, U9-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	55.07
Std Dev	9.044

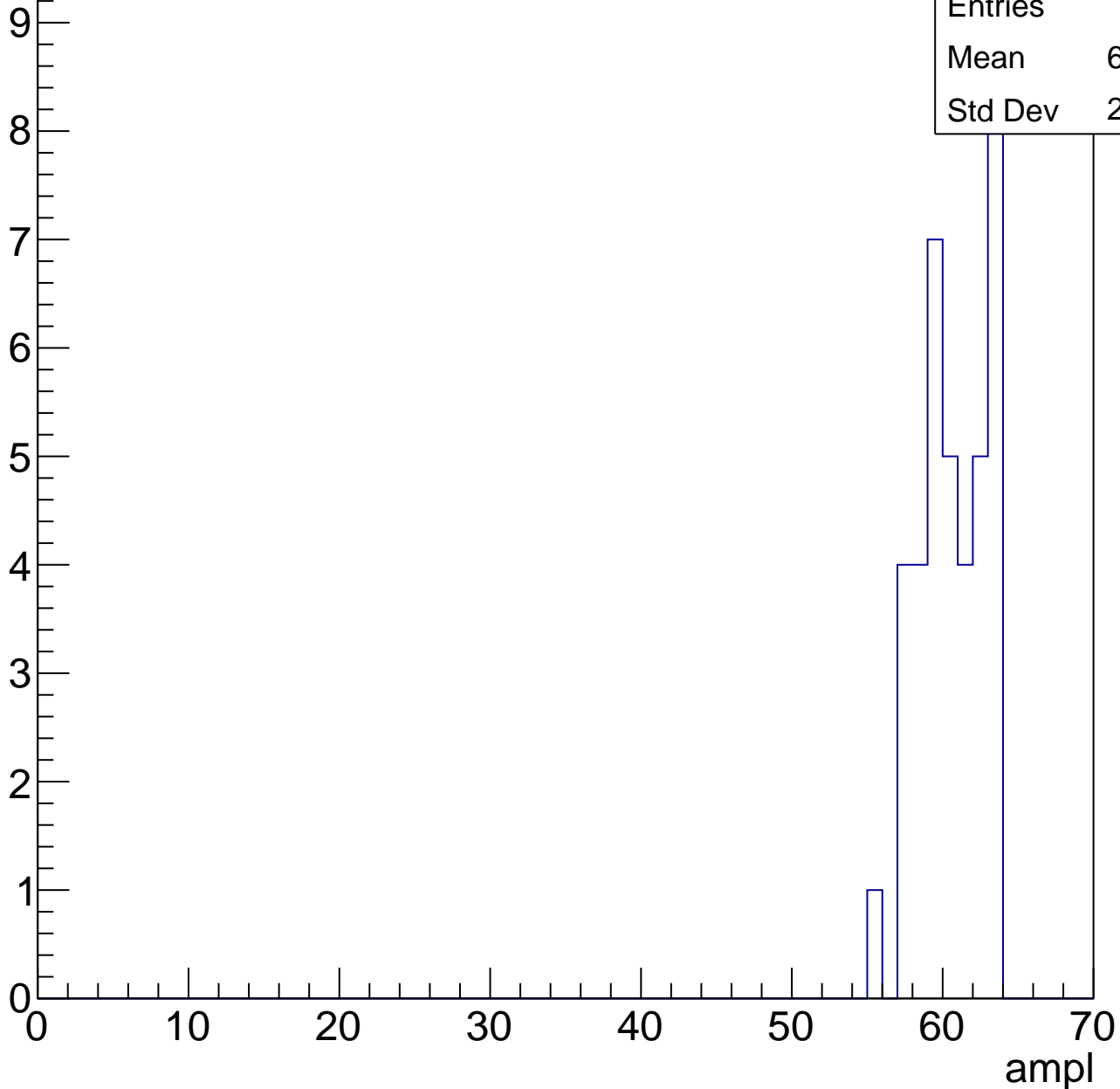


B0L002S, U9-ch92, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

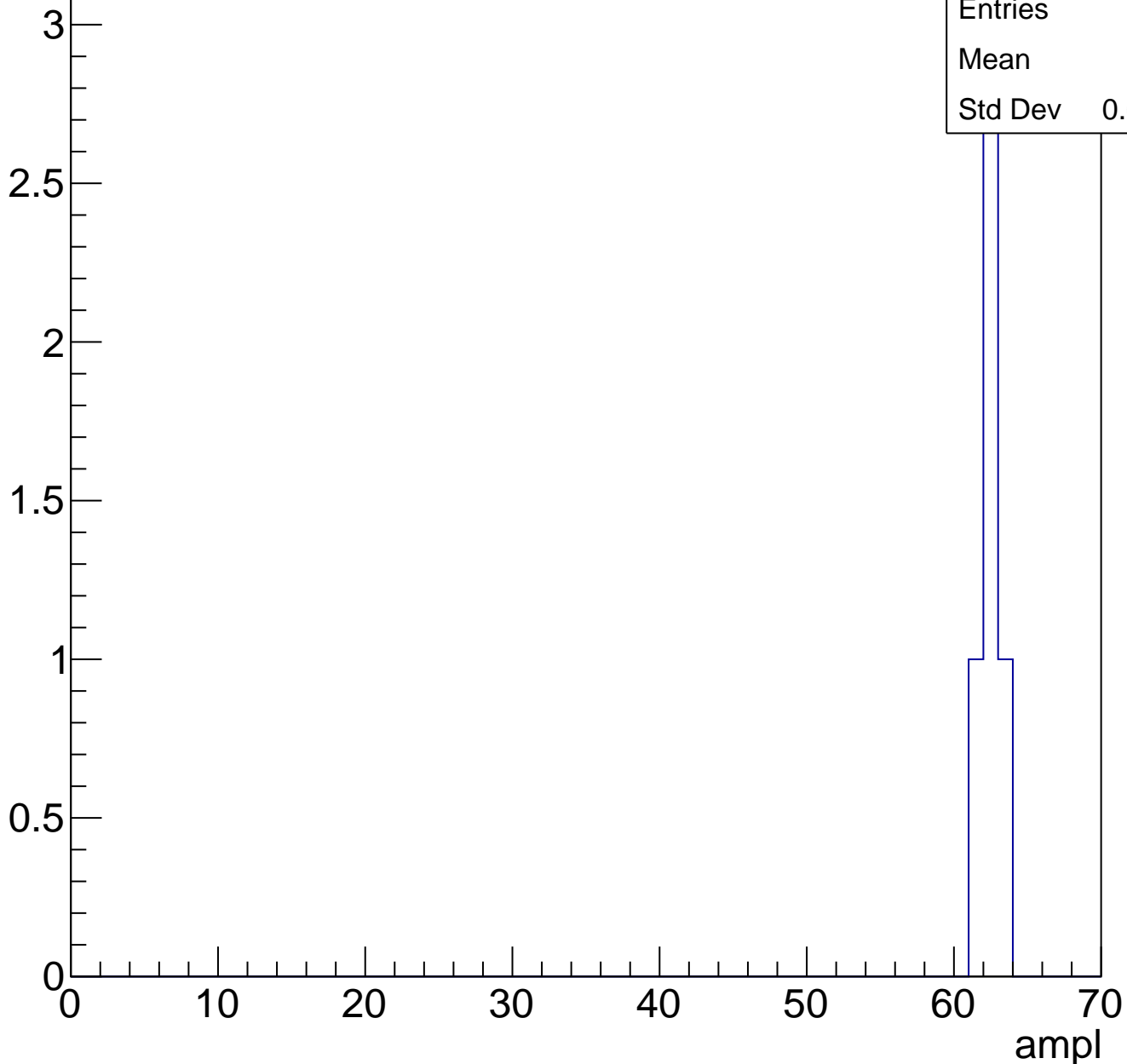
Entries	39
Mean	60.23
Std Dev	2.189



B0L002S, U9-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	62
Std Dev	0.6325

B0L002S, U9-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch93, adc0

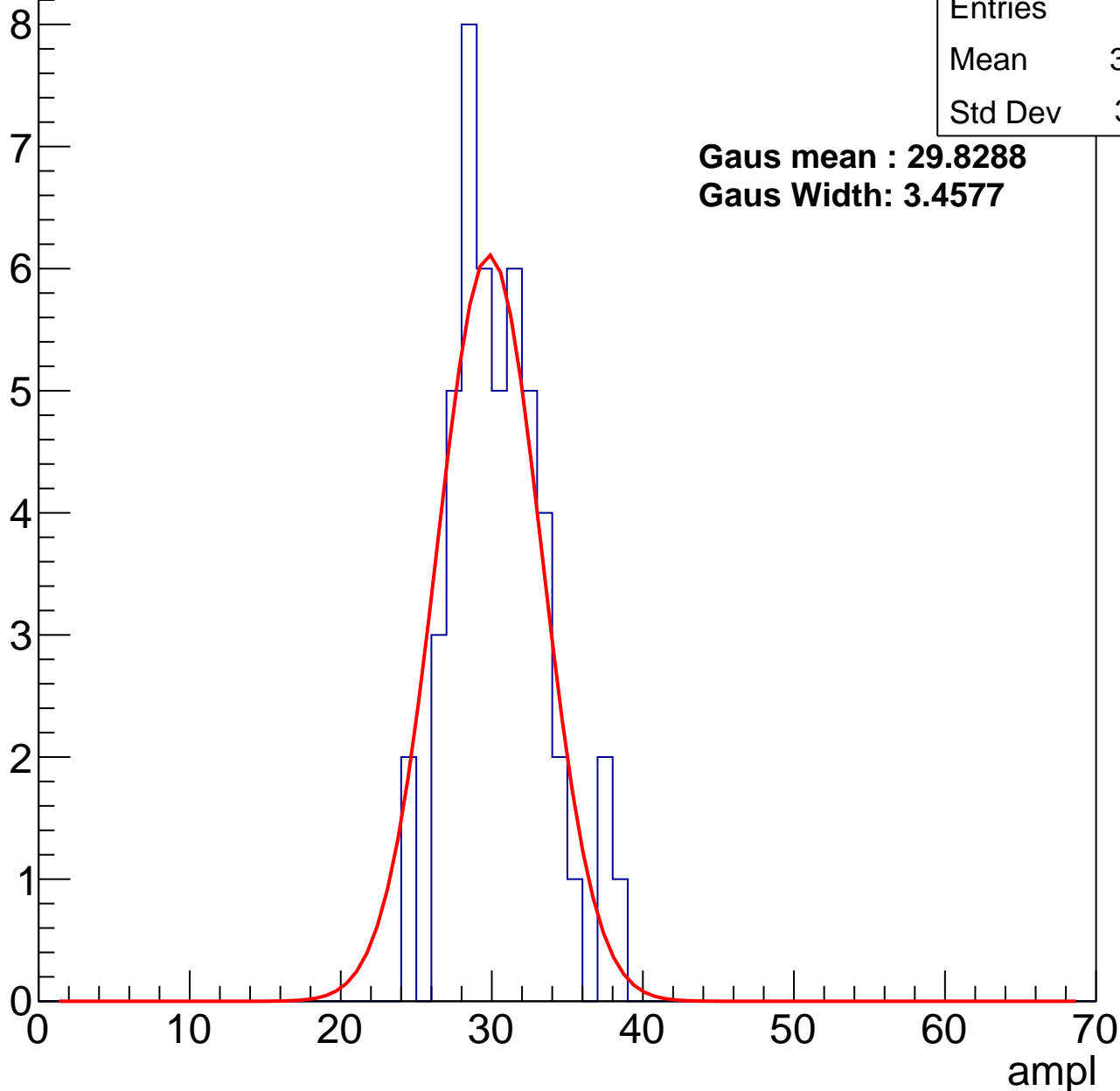
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	30.04
Std Dev	3.111

Gaus mean : 29.8288

Gaus Width: 3.4577



B0L002S, U9-ch93, adc1

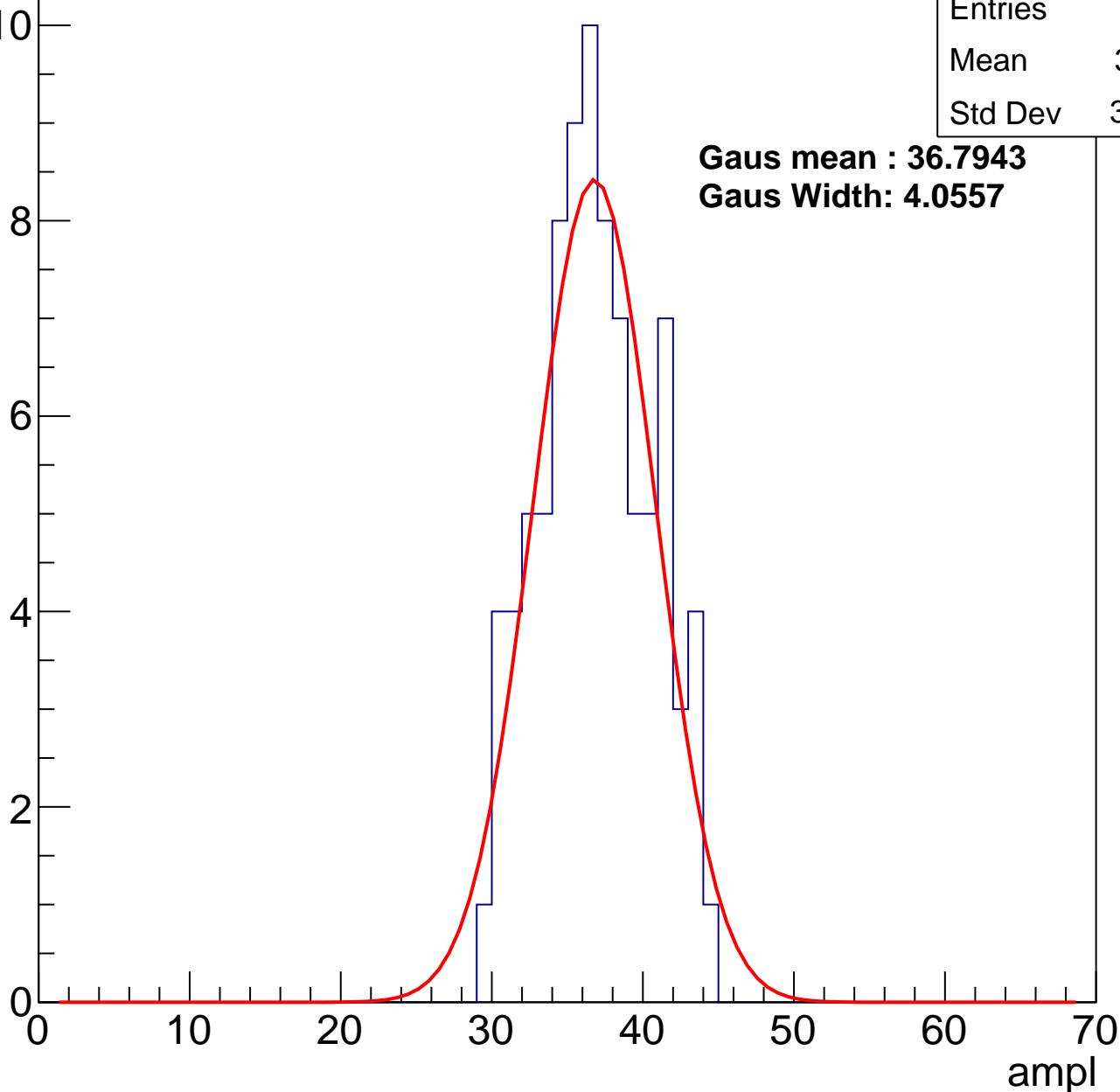
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	36.41
Std Dev	3.655

Gaus mean : 36.7943

Gaus Width: 4.0557



B0L002S, U9-ch93, adc2

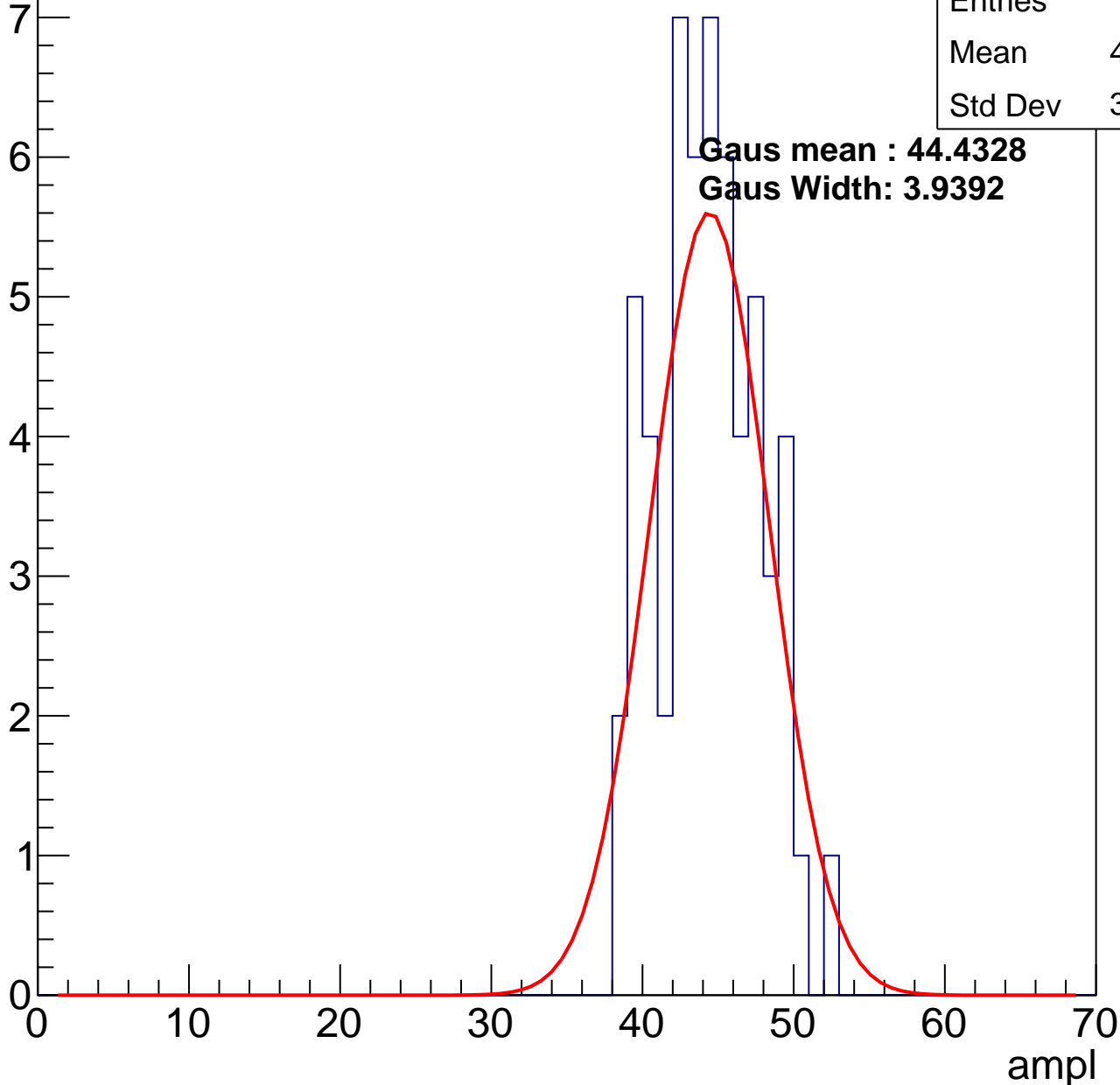
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.93
Std Dev	3.319

Gaus mean : 44.4328

Gaus Width: 3.9392



B0L002S, U9-ch93, adc3

calib_packv5_042523_0143.root, FC#8, port C1

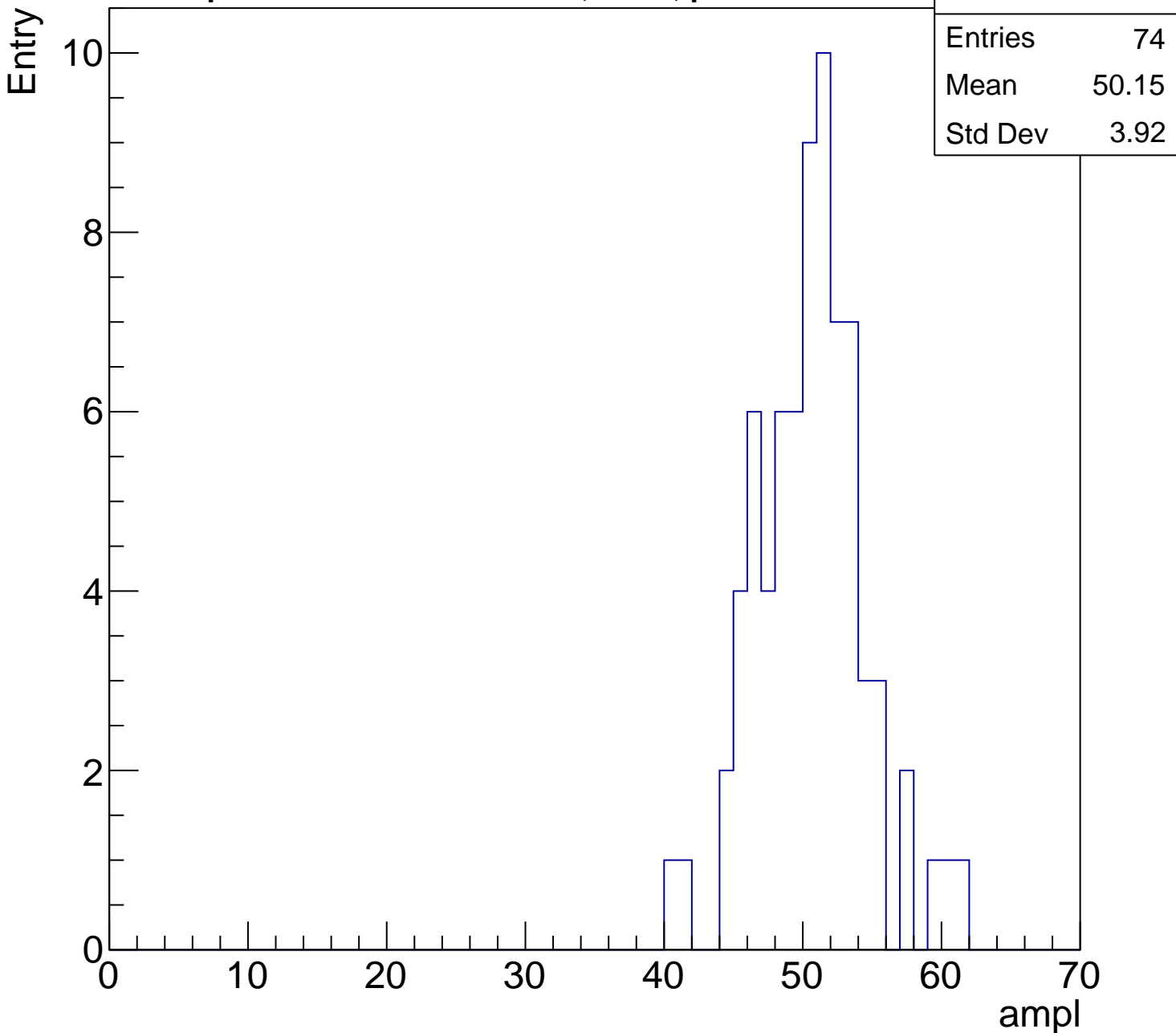
Entries	74
Mean	50.15
Std Dev	3.92

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

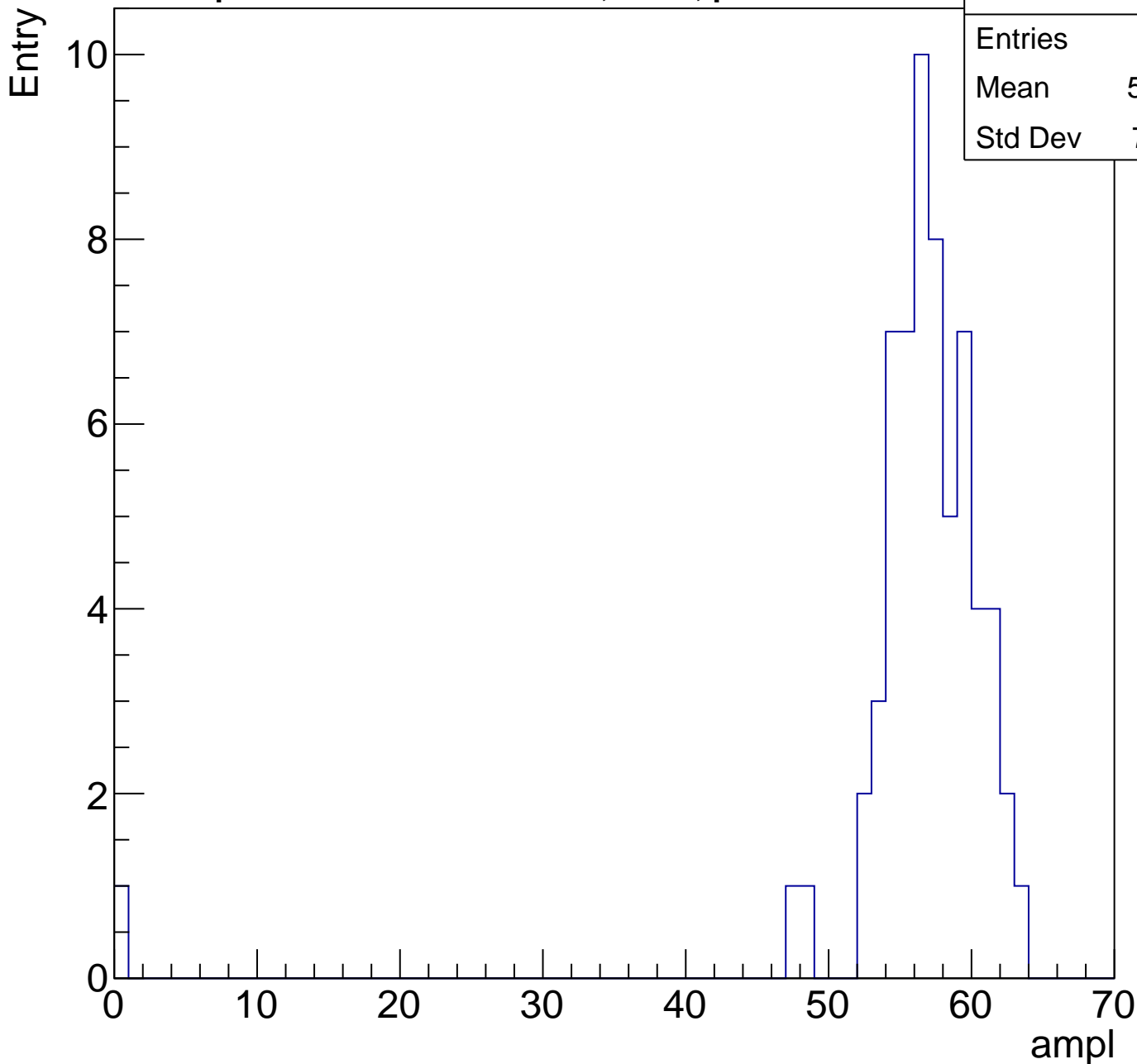
ampl



B0L002S, U9-ch93, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	63
Mean	55.73
Std Dev	7.711



B0L002S, U9-ch93, adc5

calib_packv5_042523_0143.root, FC#8, port C1

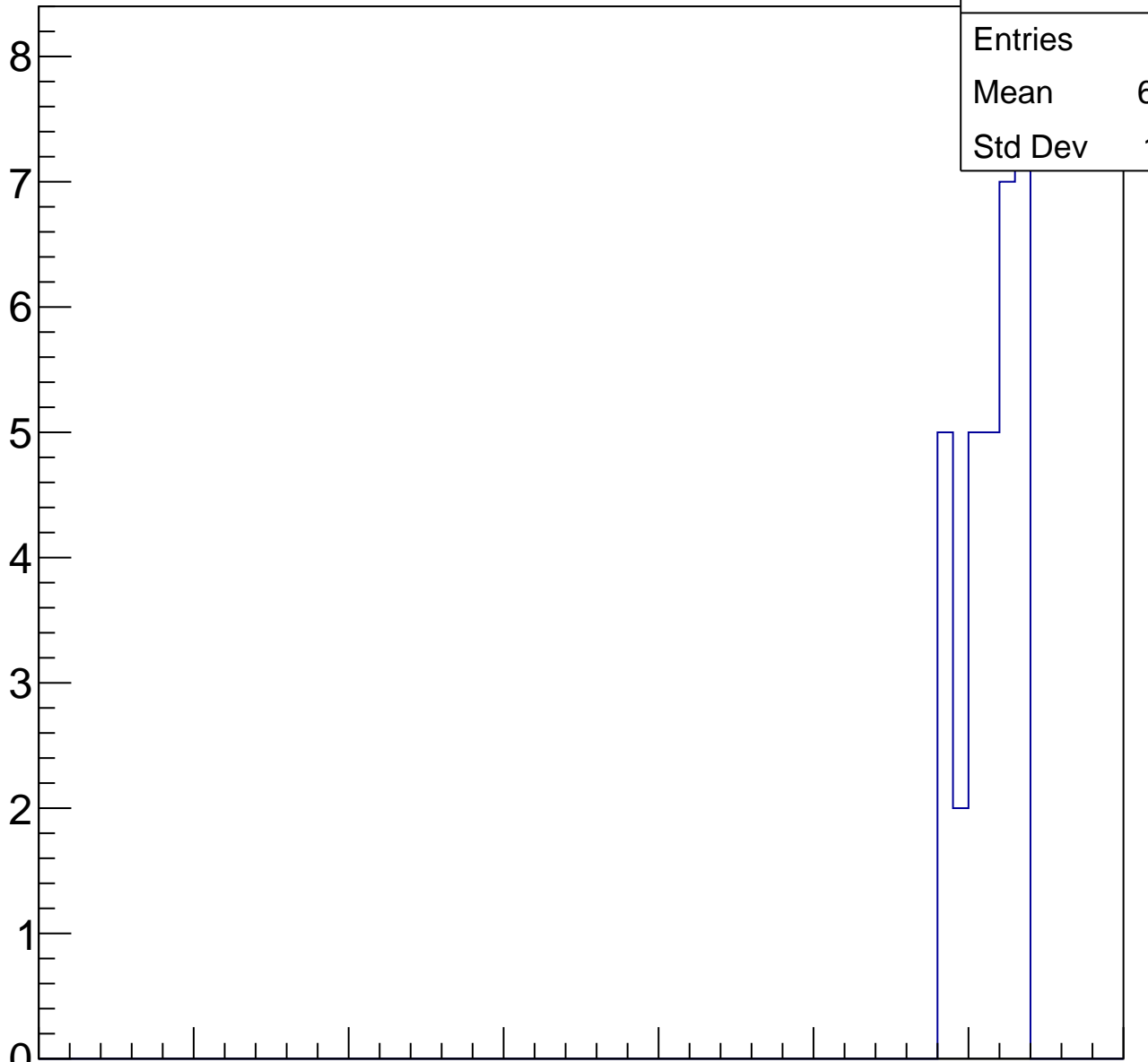
Entry

8
7
6
5
4
3
2
1
0

Entries	32
Mean	60.97
Std Dev	1.741

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch94, adc0

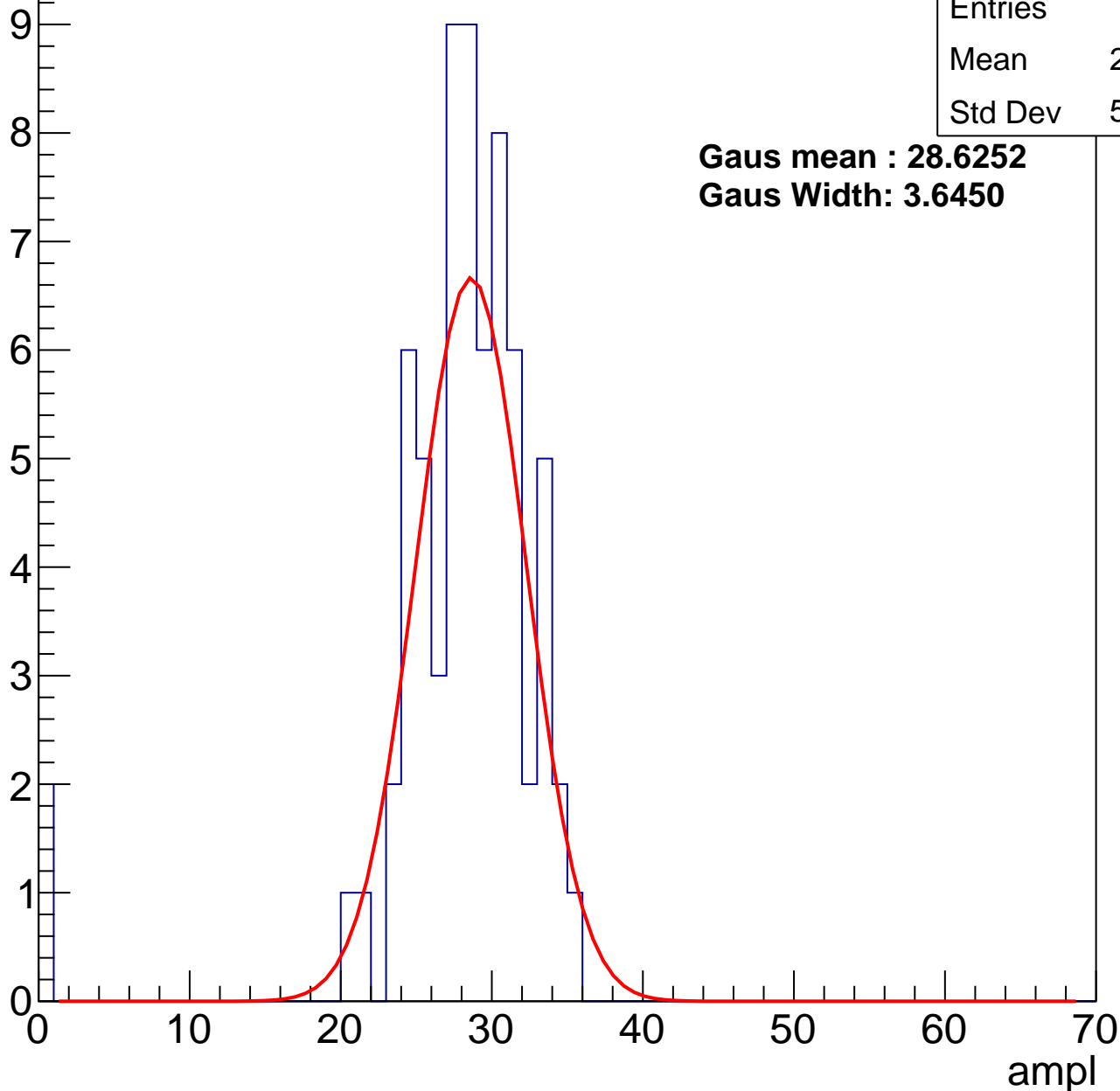
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	27.37
Std Dev	5.734

Gaus mean : 28.6252

Gaus Width: 3.6450



B0L002S, U9-ch94, adc1

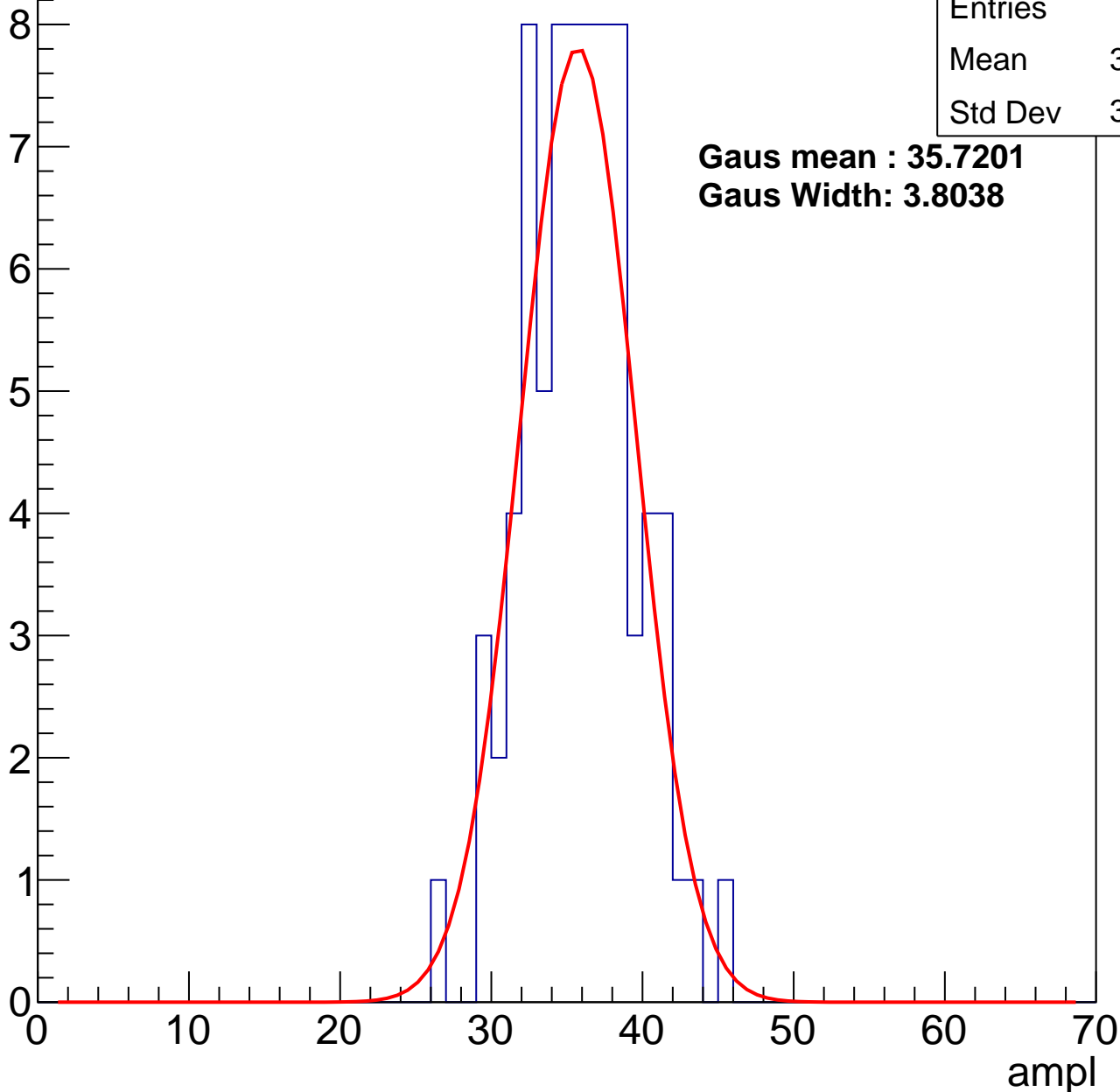
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	35.44
Std Dev	3.613

Gaus mean : 35.7201

Gaus Width: 3.8038



B0L002S, U9-ch94, adc2

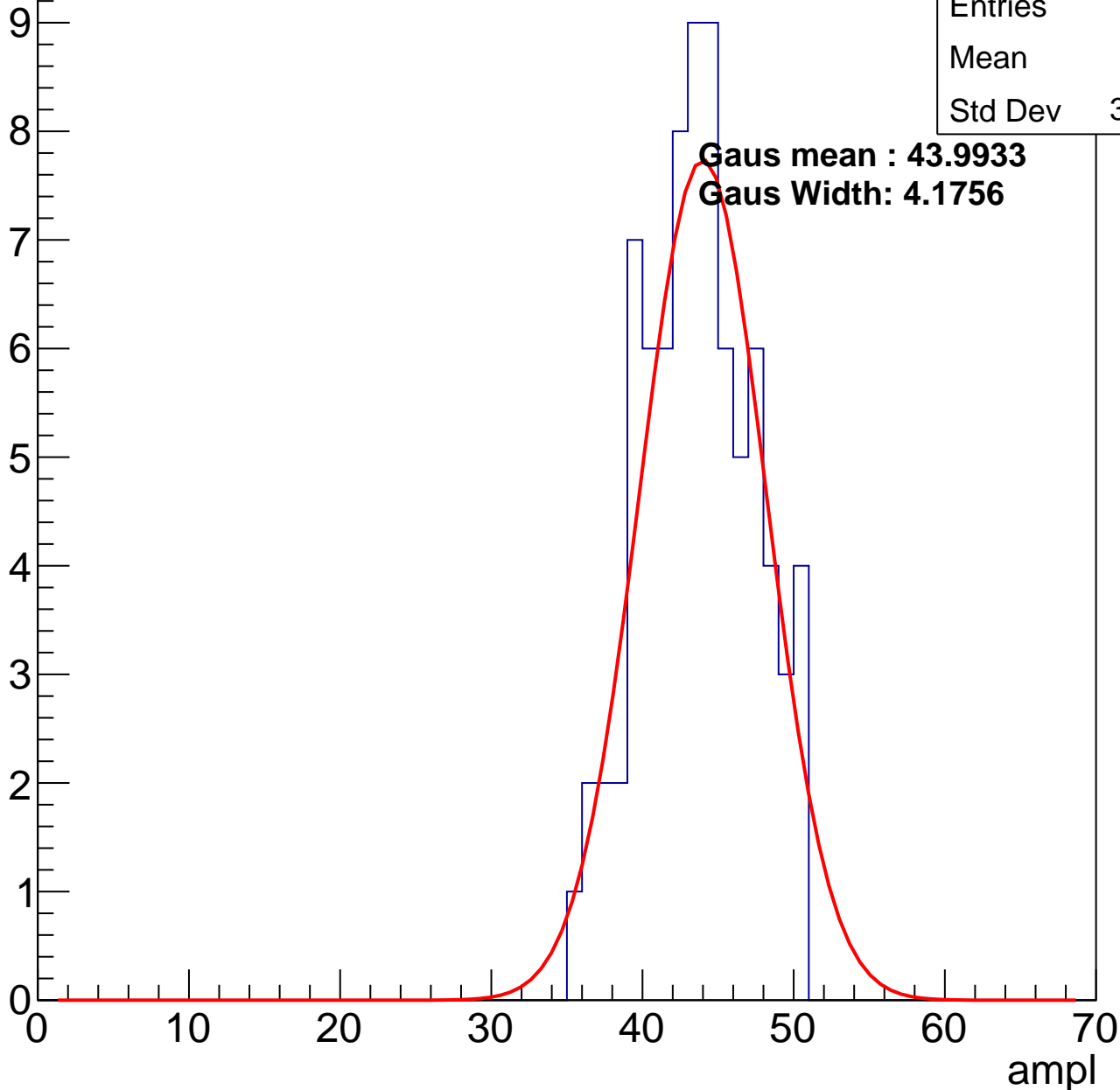
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	43.2
Std Dev	3.635

Gaus mean : 43.9933

Gaus Width: 4.1756

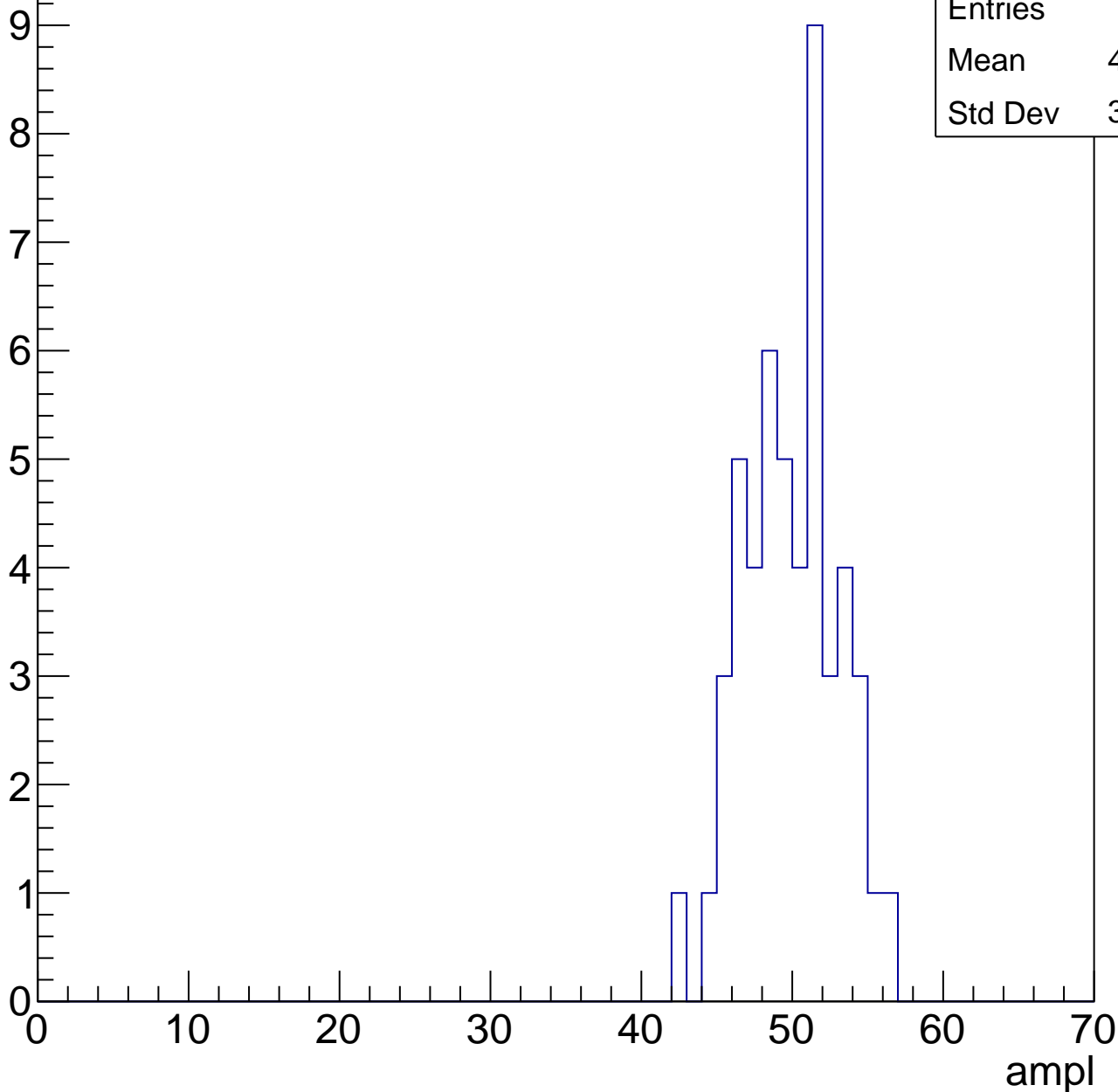


B0L002S, U9-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	49.44
Std Dev	3.067

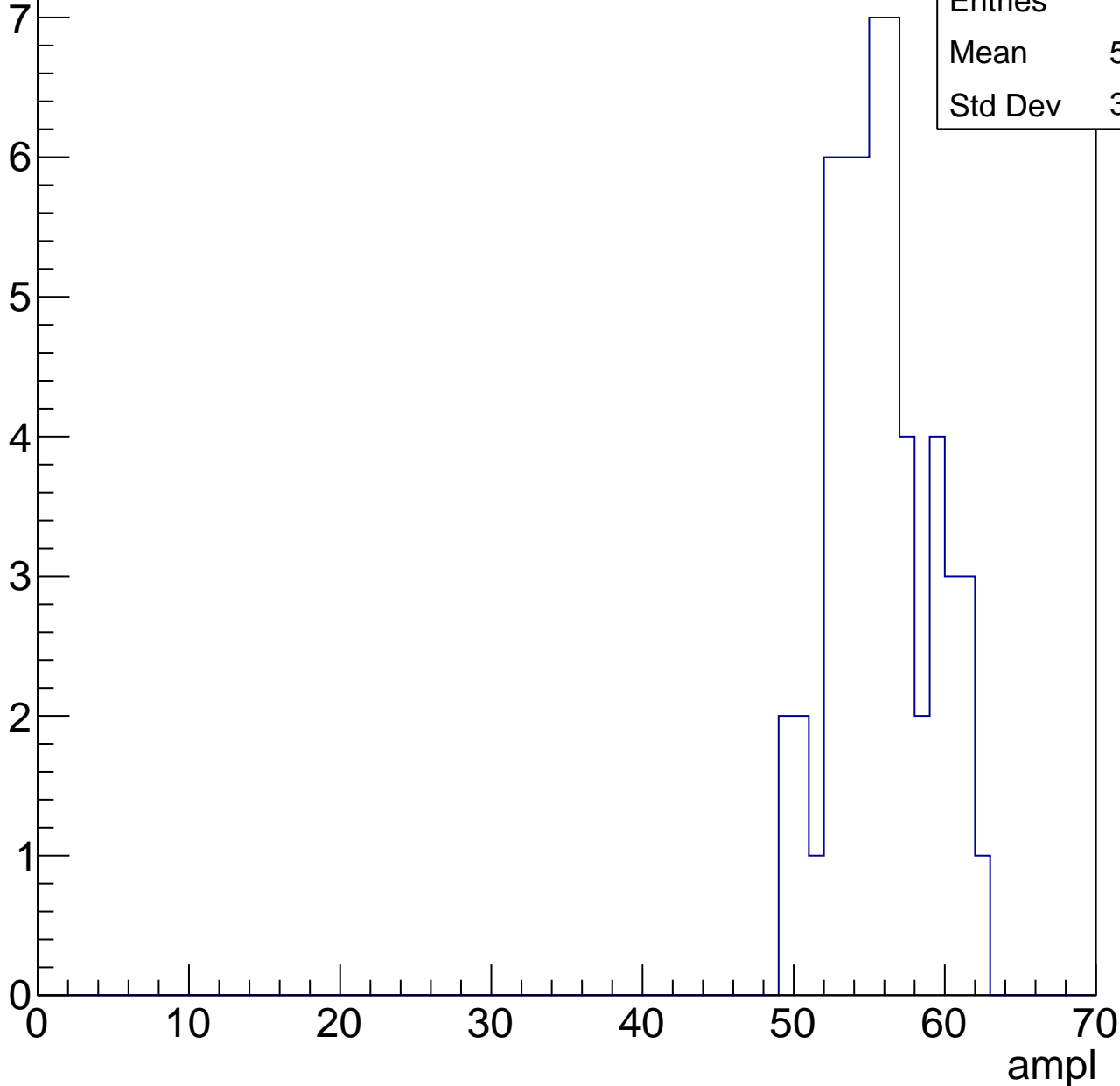


B0L002S, U9-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	55.28
Std Dev	3.205

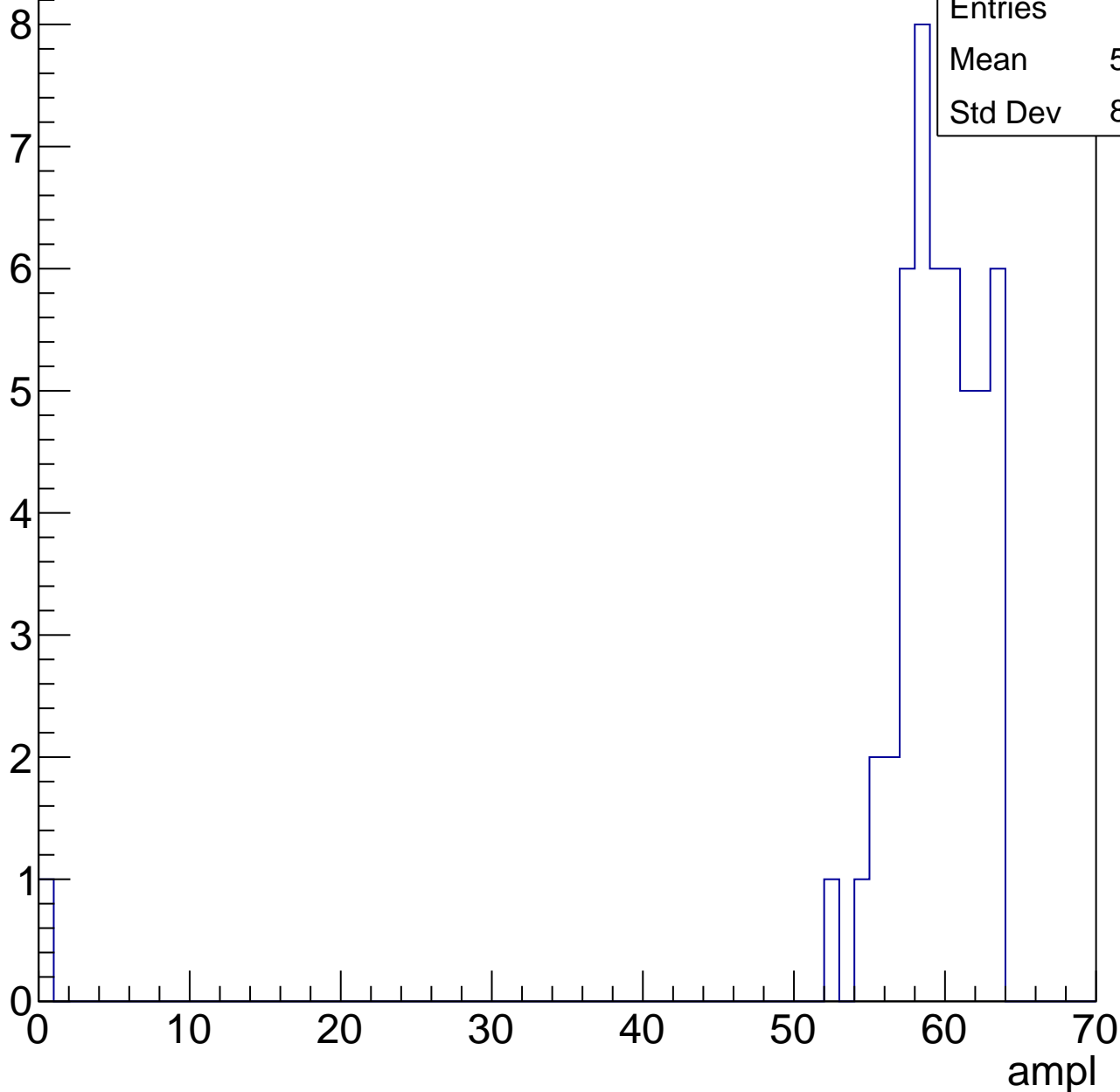


B0L002S, U9-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

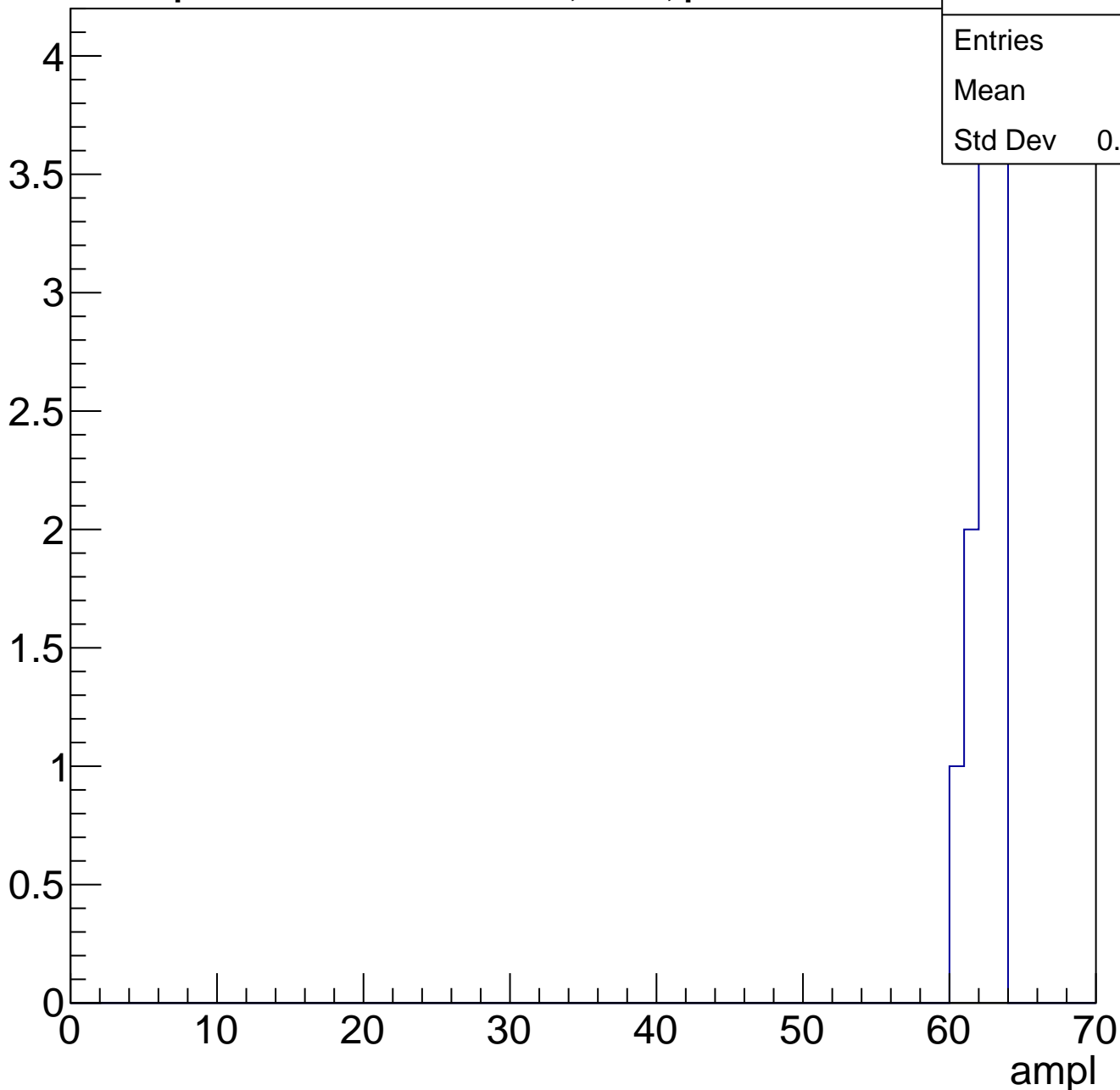
Entries	49
Mean	57.98
Std Dev	8.752



B0L002S, U9-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch95, adc0

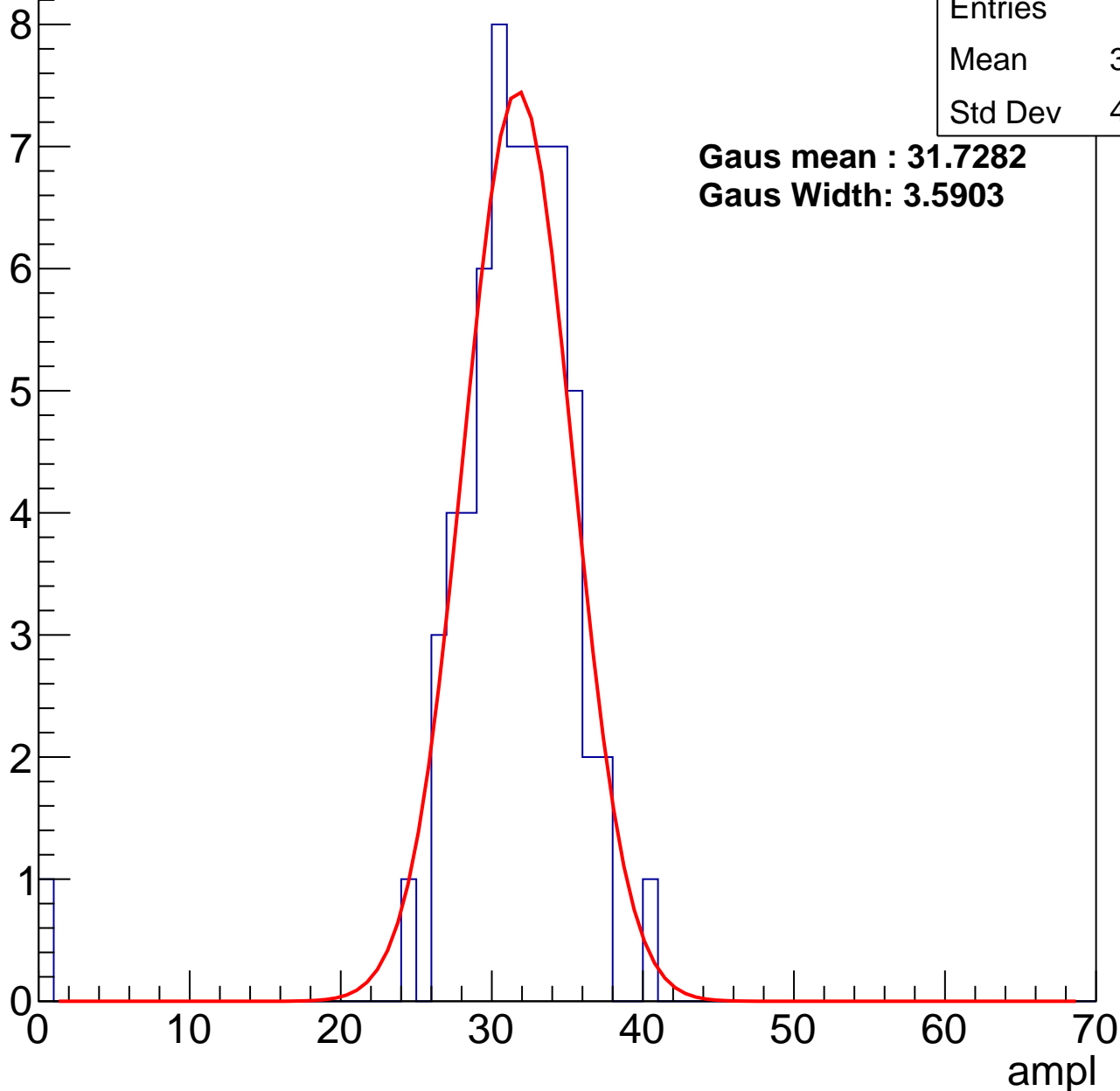
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.88
Std Dev	4.957

Gaus mean : 31.7282

Gaus Width: 3.5903



B0L002S, U9-ch95, adc1

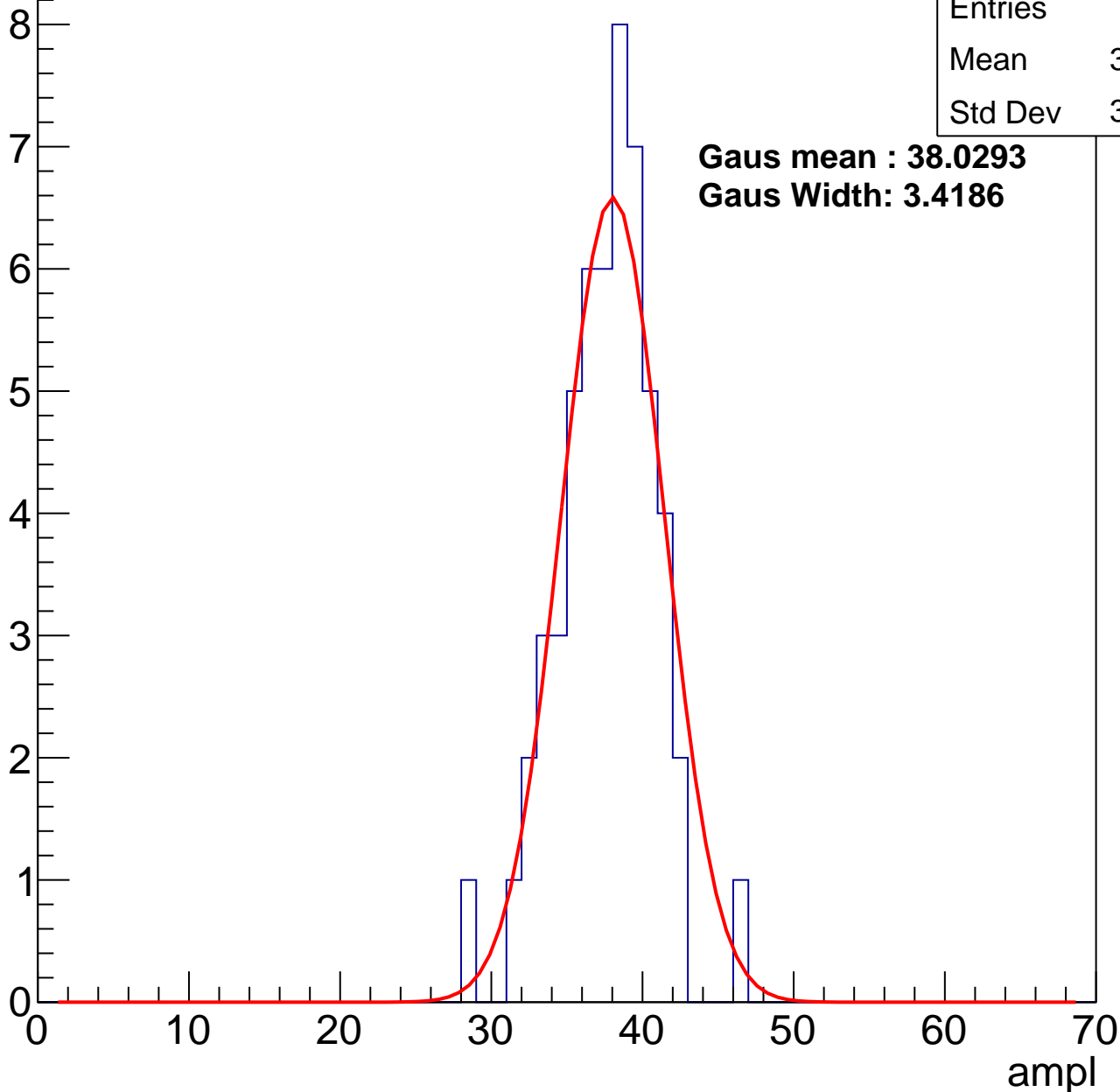
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	37.19
Std Dev	3.169

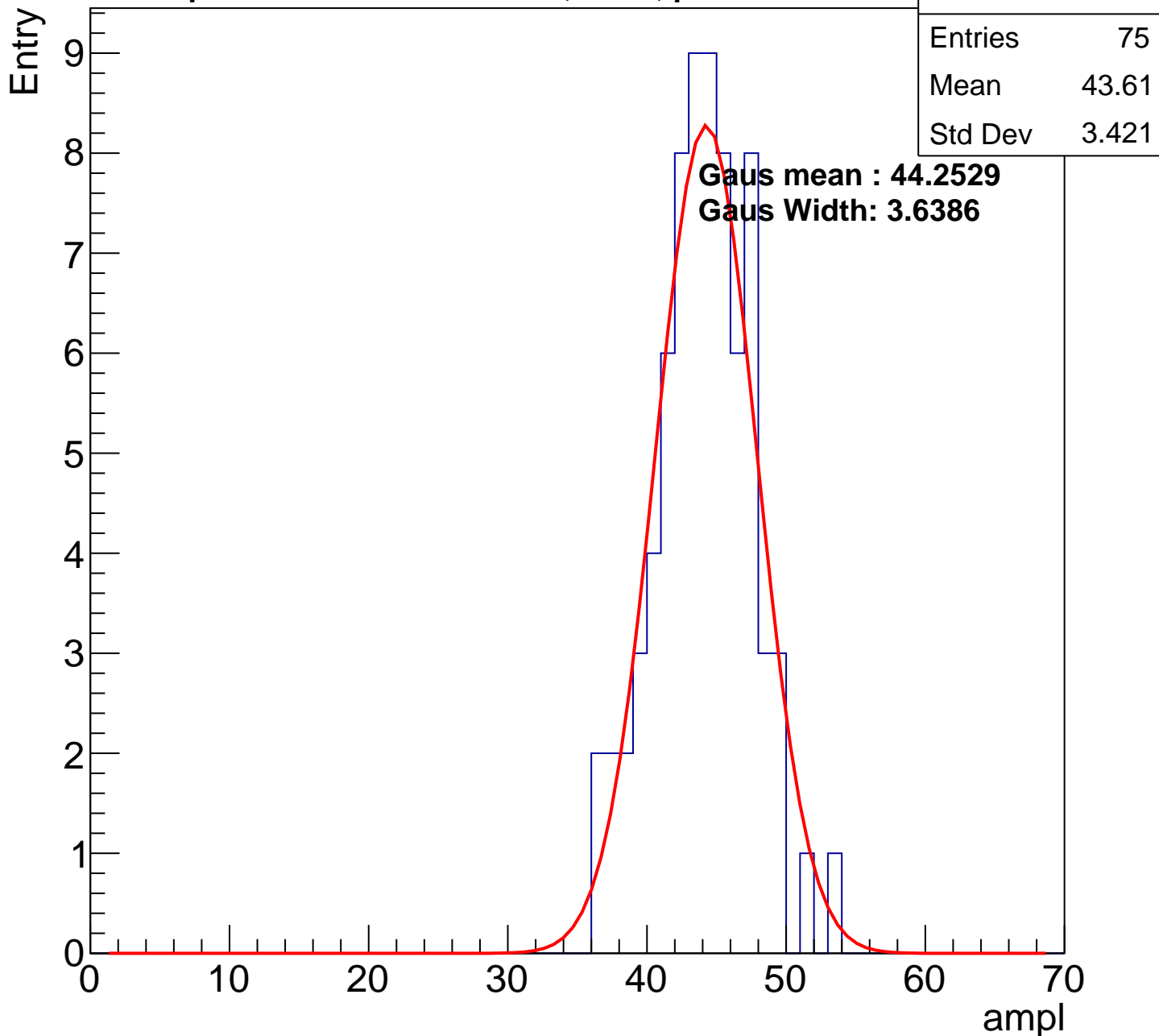
Gaus mean : 38.0293

Gaus Width: 3.4186



B0L002S, U9-ch95, adc2

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

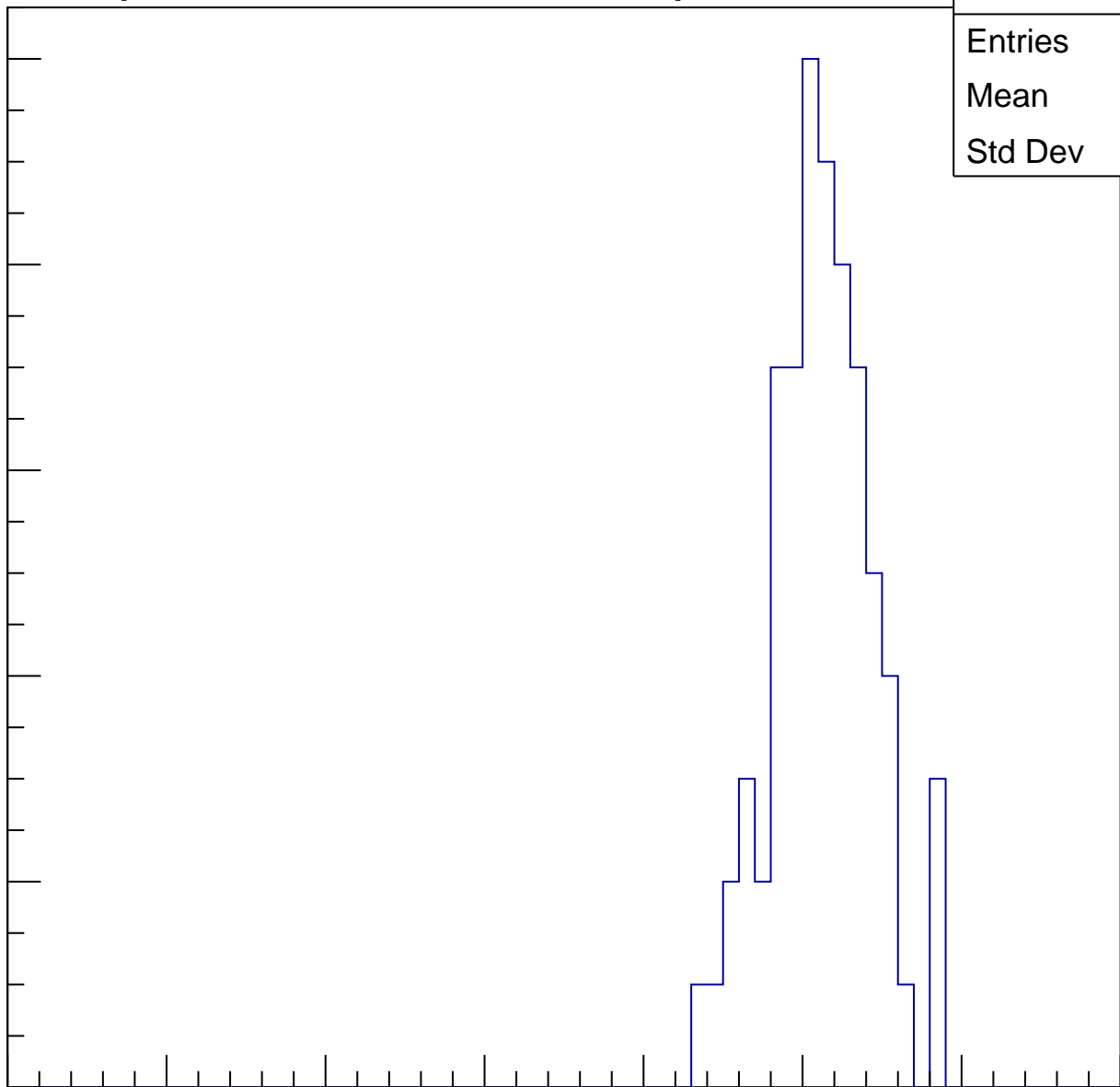
Entries	70
Mean	50.77
Std Dev	3.177

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

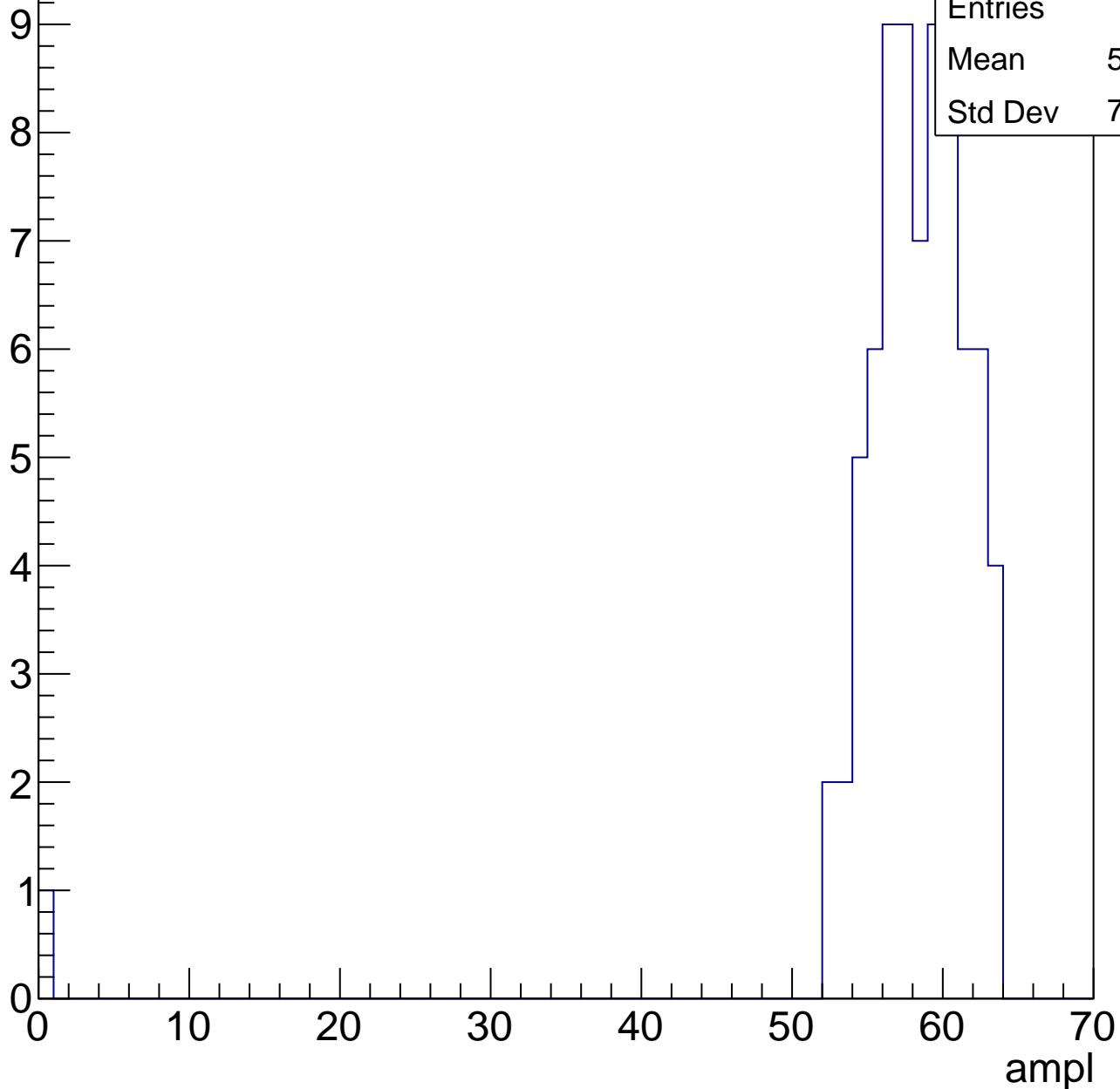
ampl



B0L002S, U9-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



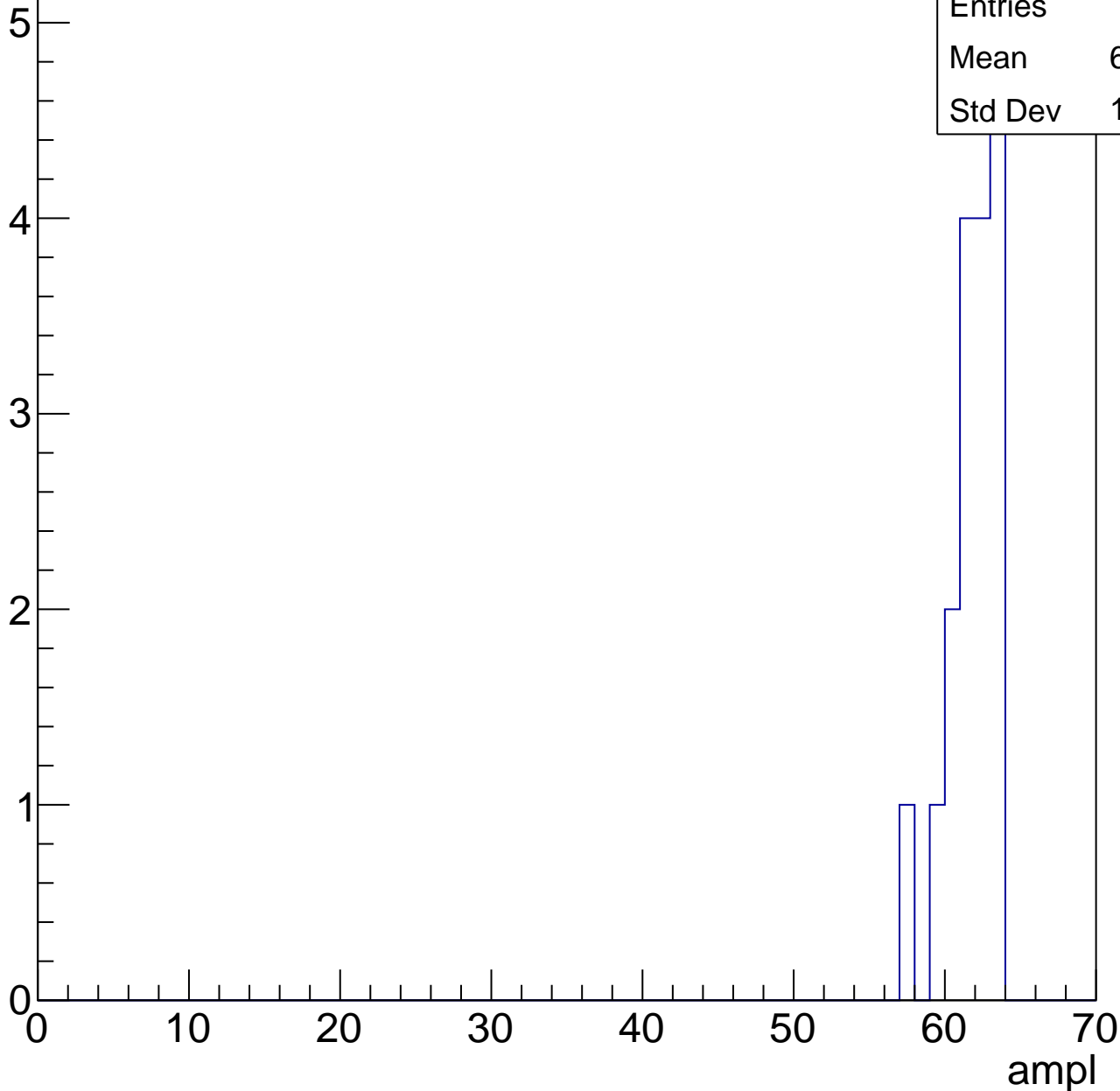
Entries	74
Mean	57.22
Std Dev	7.269

B0L002S, U9-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

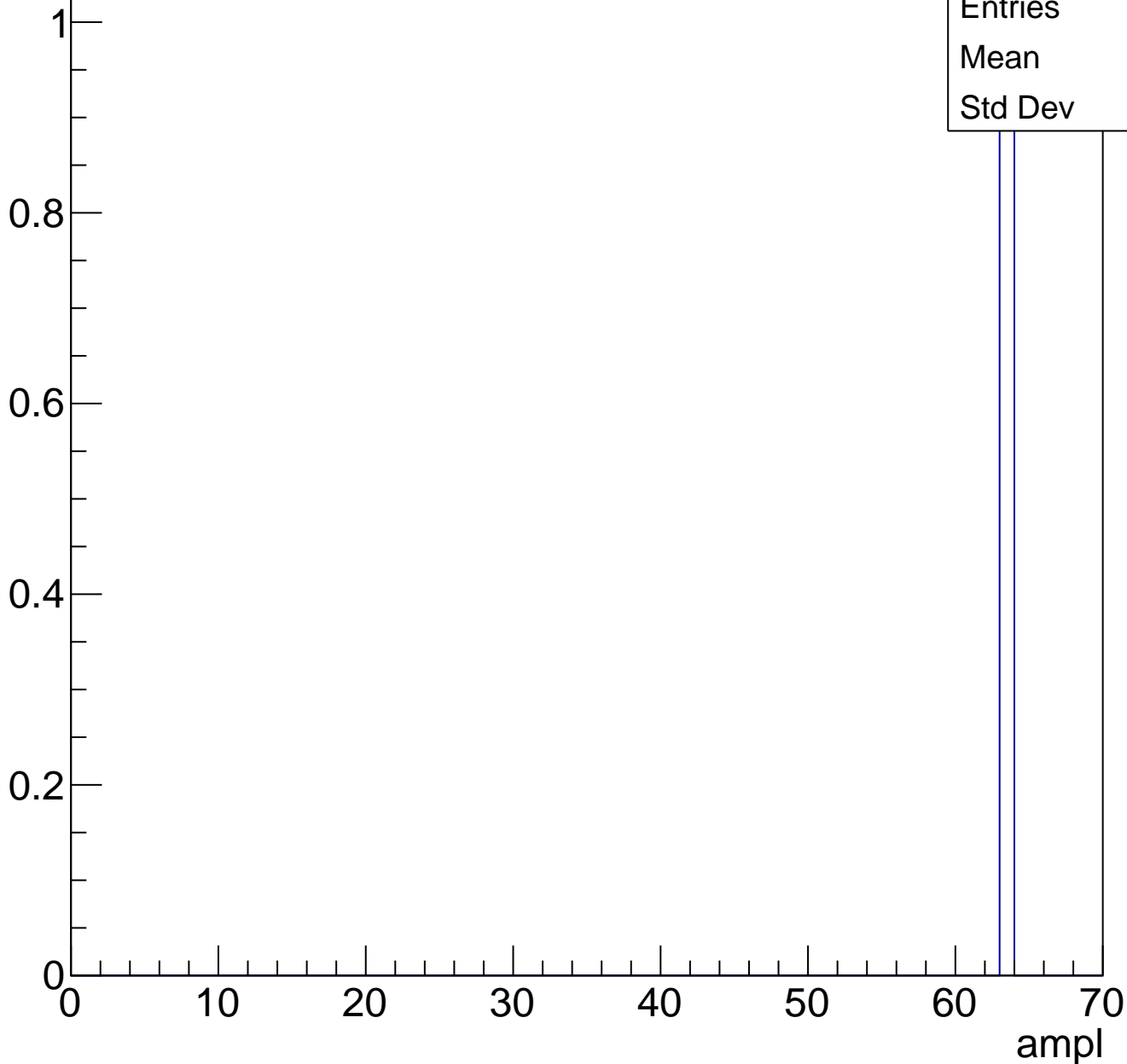
Entries	17
Mean	61.35
Std Dev	1.607



B0L002S, U9-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch96, adc0

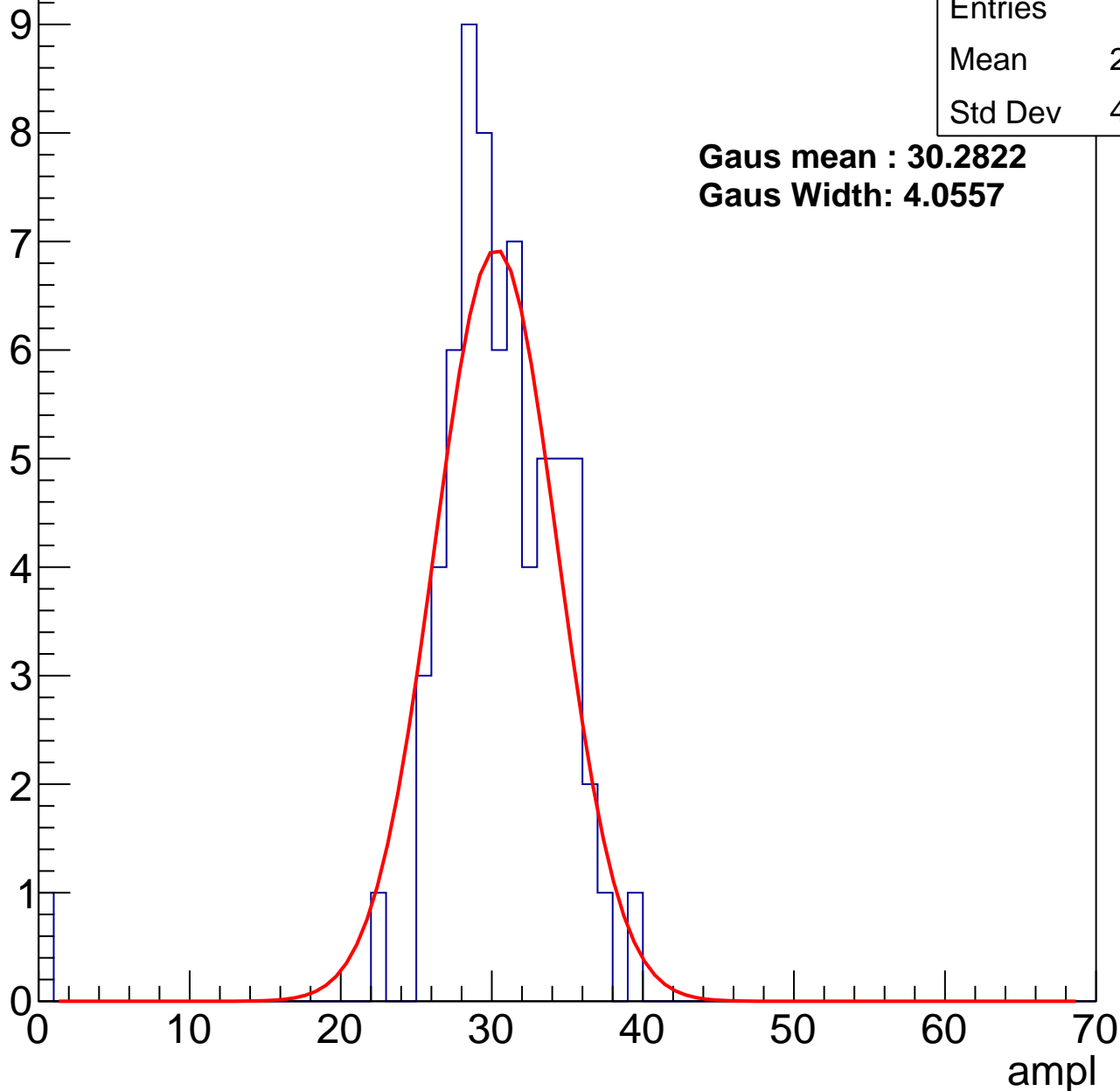
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.85
Std Dev	4.962

Gaus mean : 30.2822

Gaus Width: 4.0557



B0L002S, U9-ch96, adc1

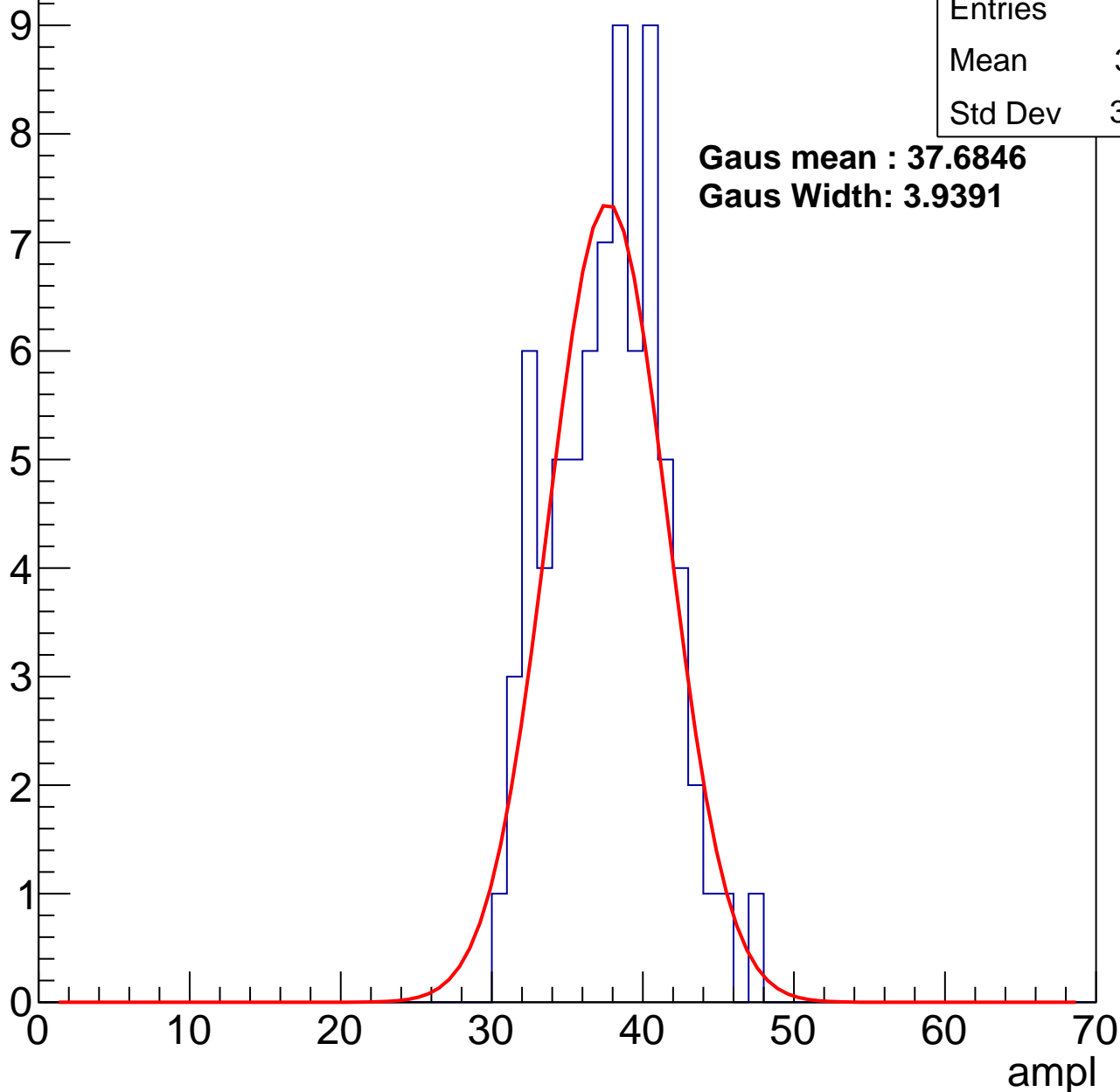
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.31
Std Dev	3.677

Gaus mean : 37.6846

Gaus Width: 3.9391



B0L002S, U9-ch96, adc2

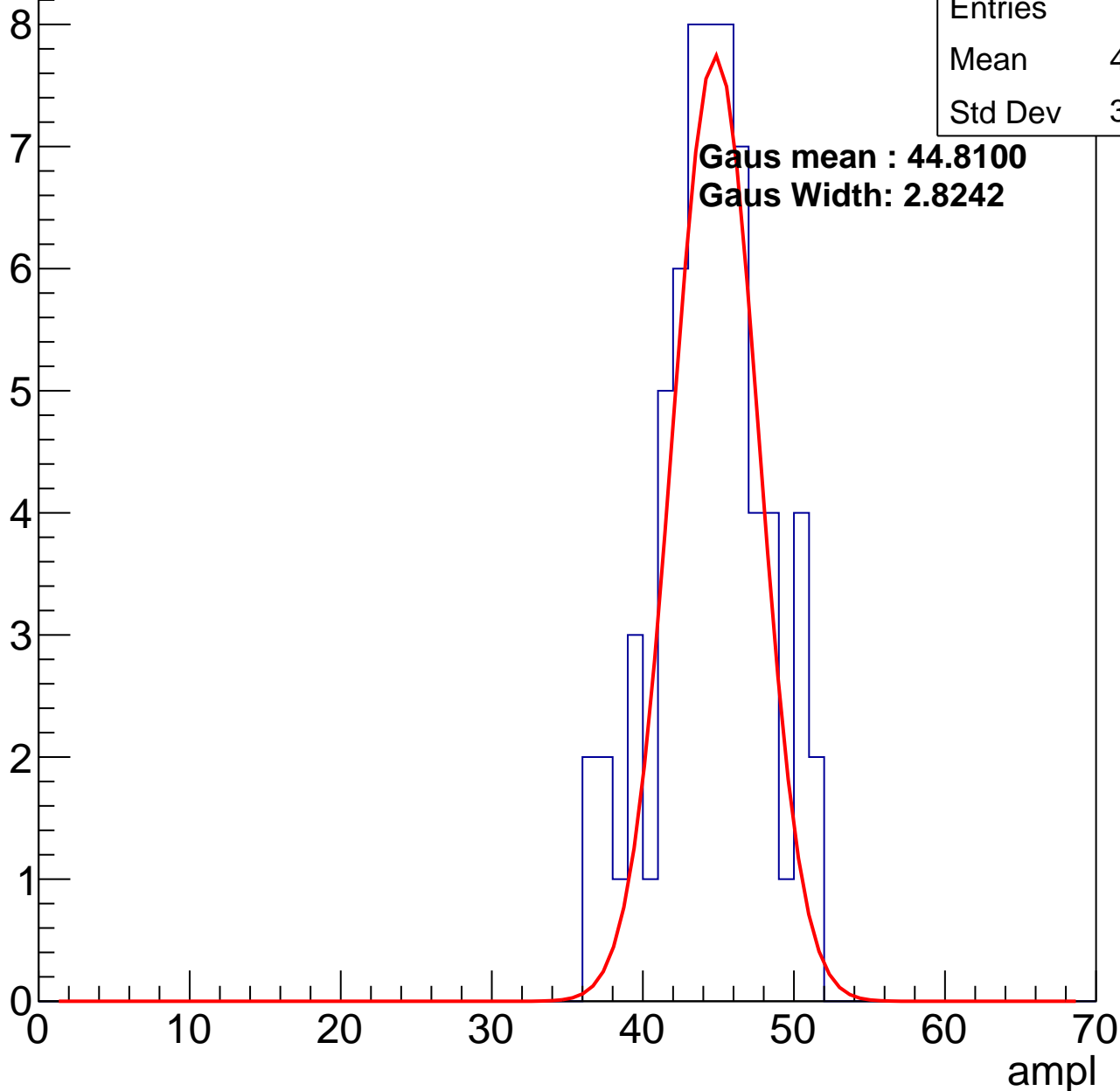
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.05
Std Dev	3.552

Gaus mean : 44.8100

Gaus Width: 2.8242

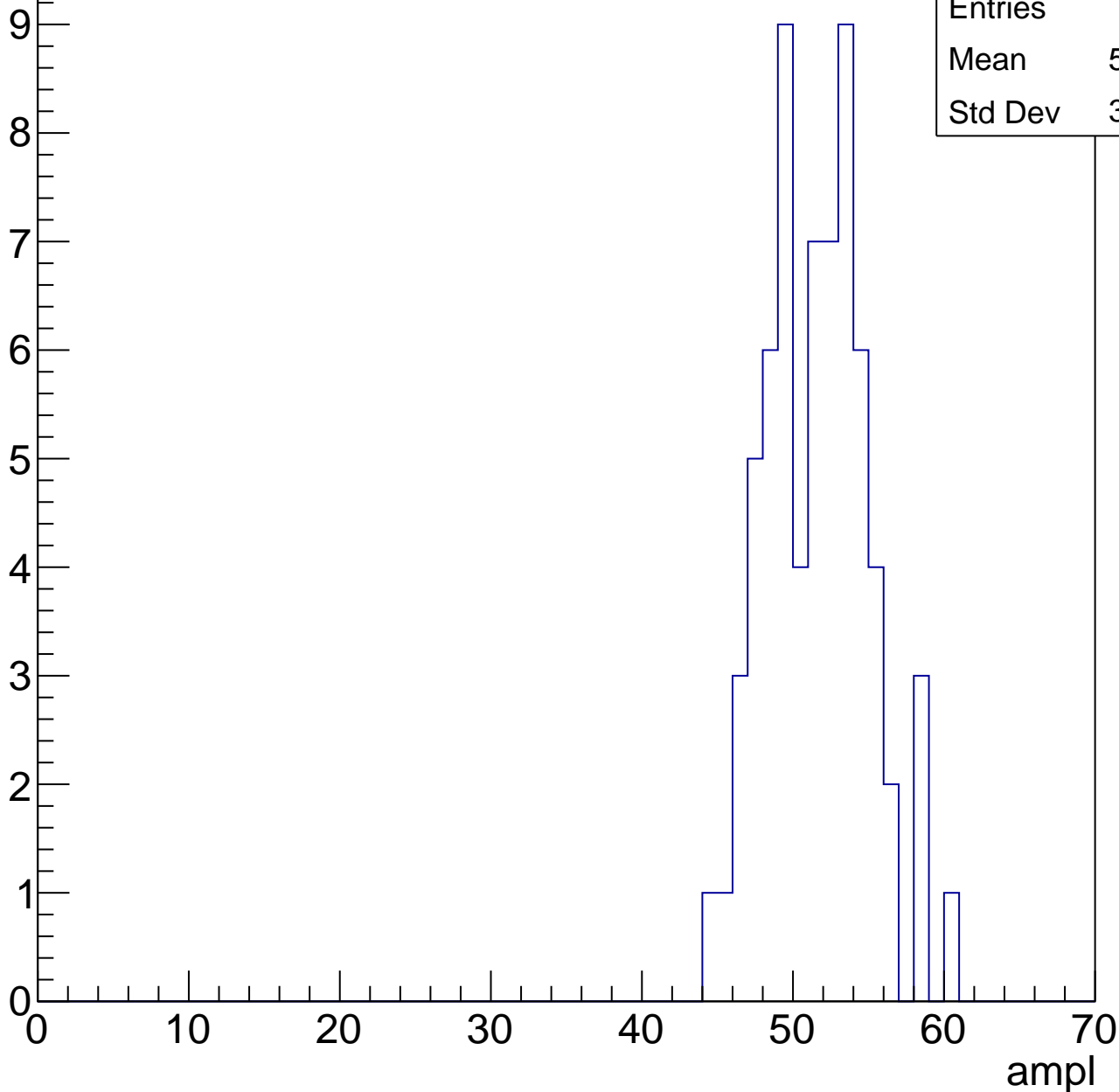


B0L002S, U9-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

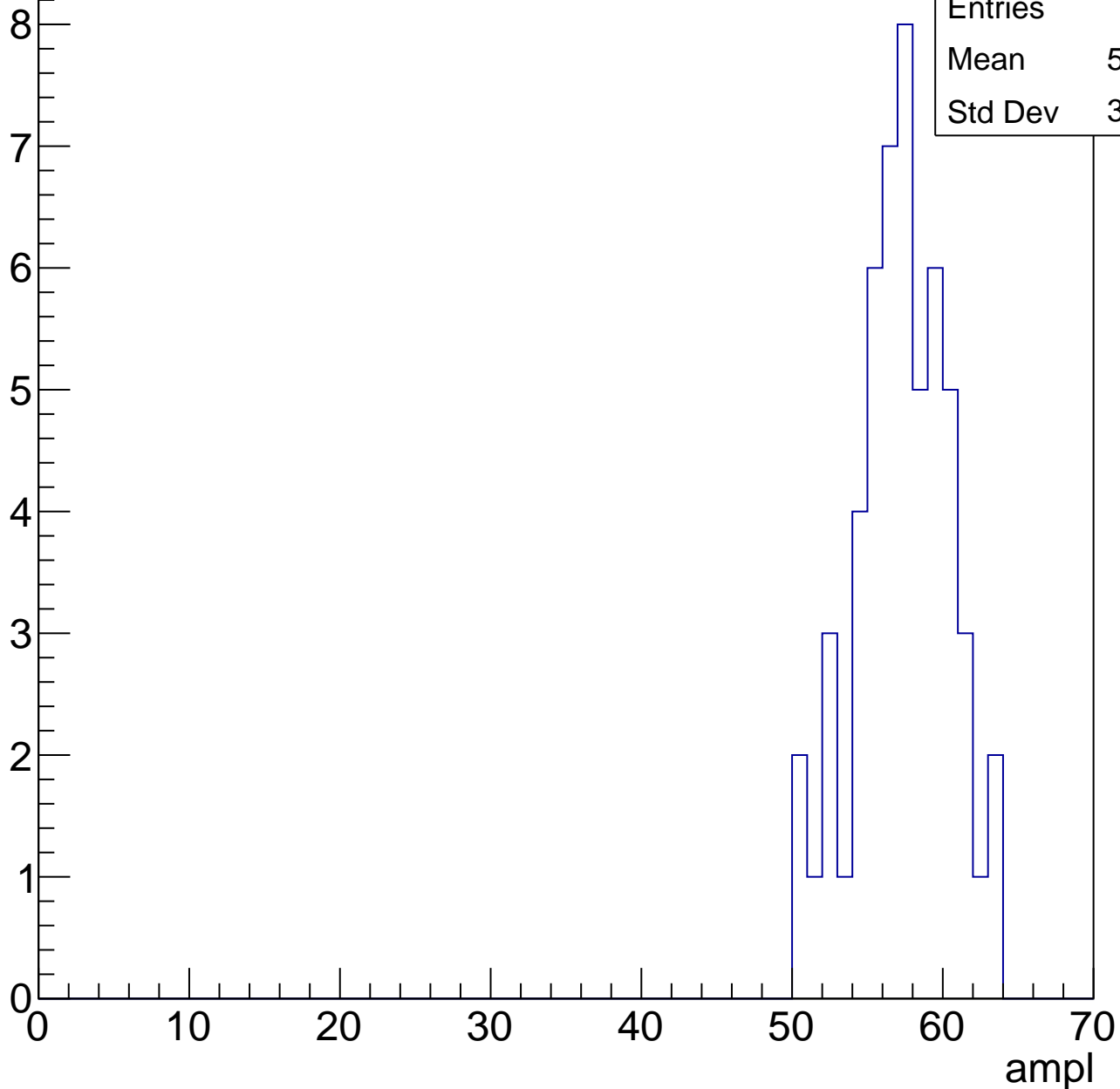
Entries	68
Mean	51.16
Std Dev	3.368



B0L002S, U9-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

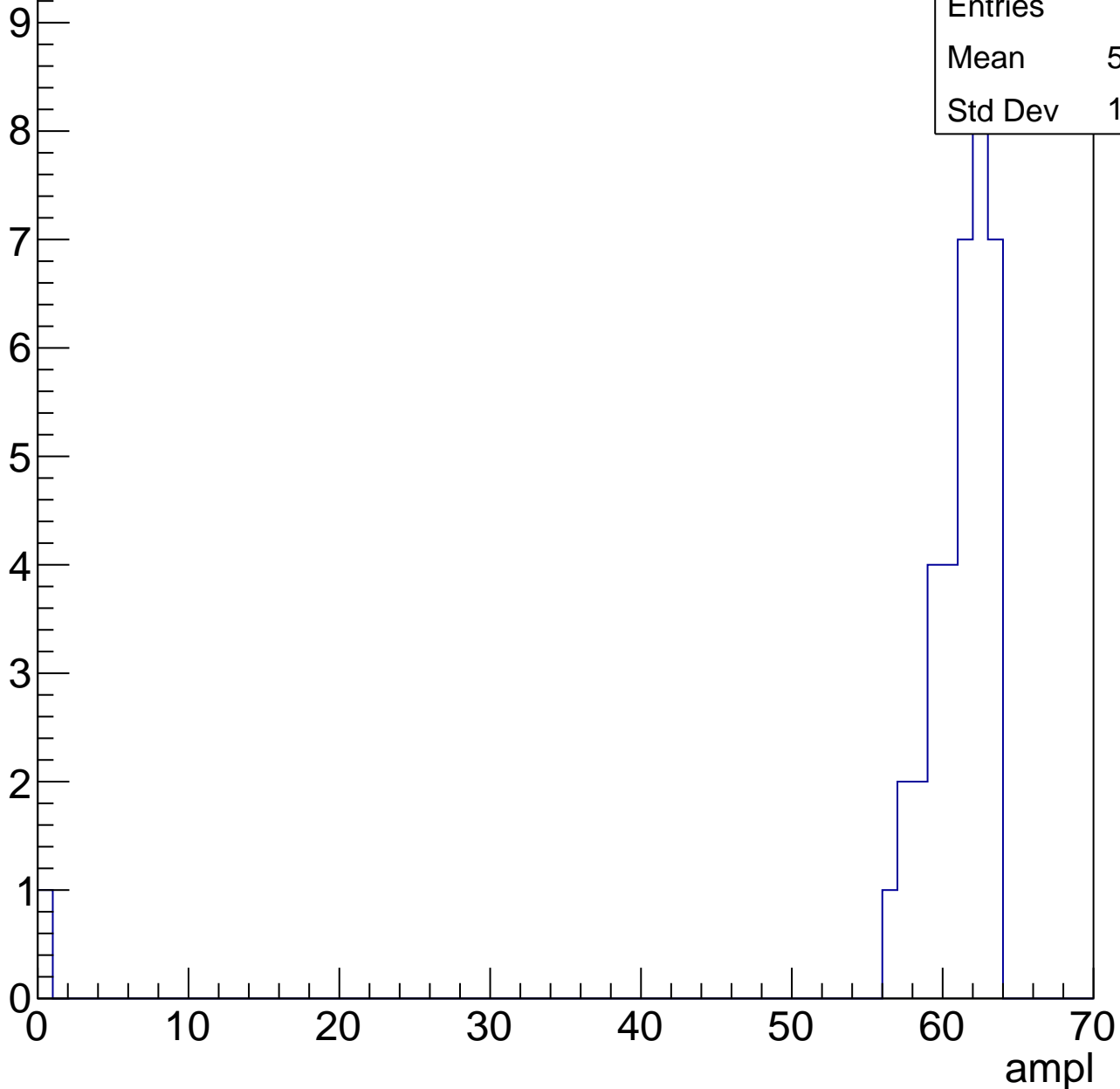


B0L002S, U9-ch96, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.14
Std Dev	10.03



B0L002S, U9-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch97, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	30.2
Std Dev	3.192

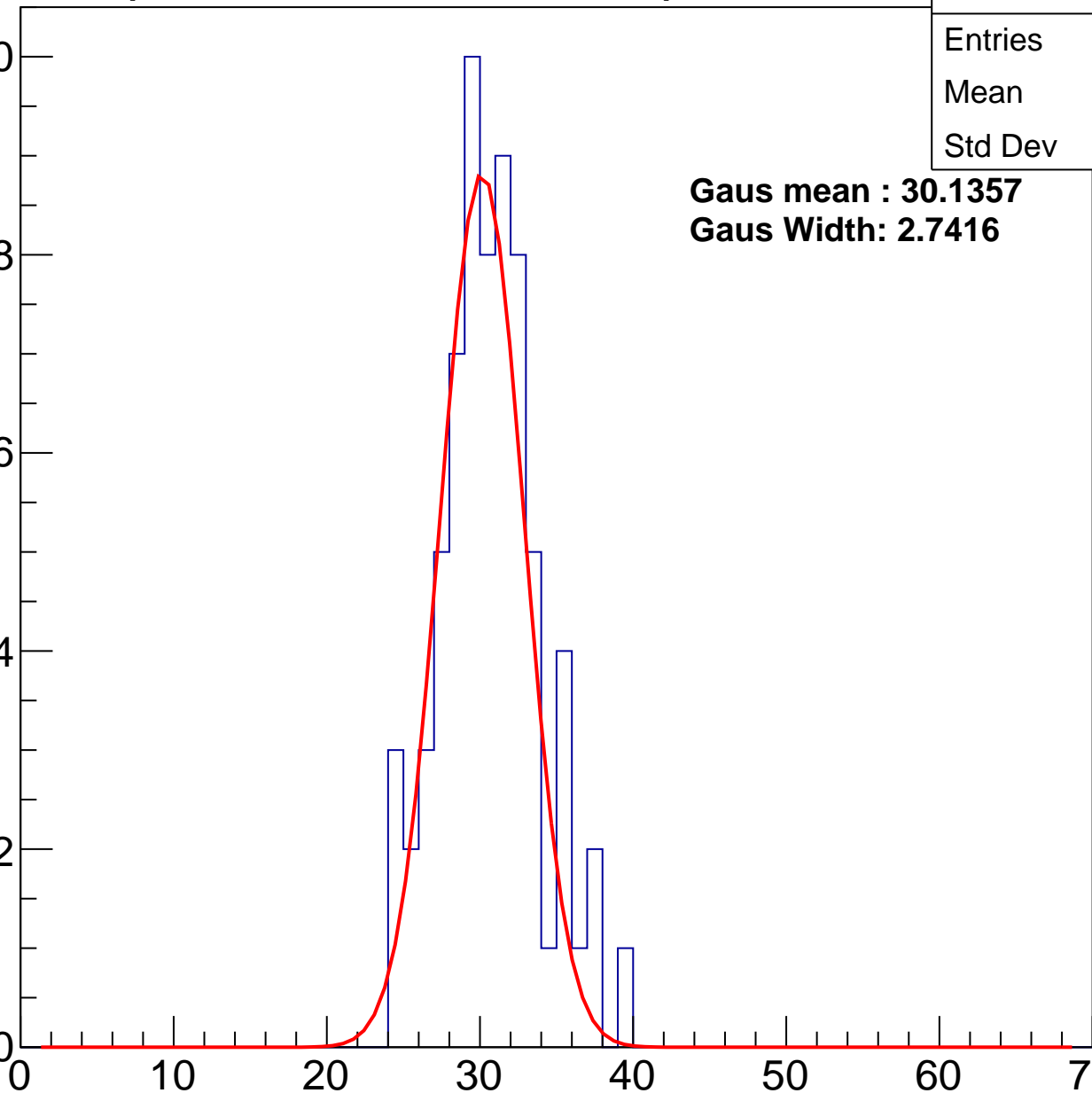
Gaus mean : 30.1357

Gaus Width: 2.7416

Entry

10
8
6
4
2
0

ampl



B0L002S, U9-ch97, adc1

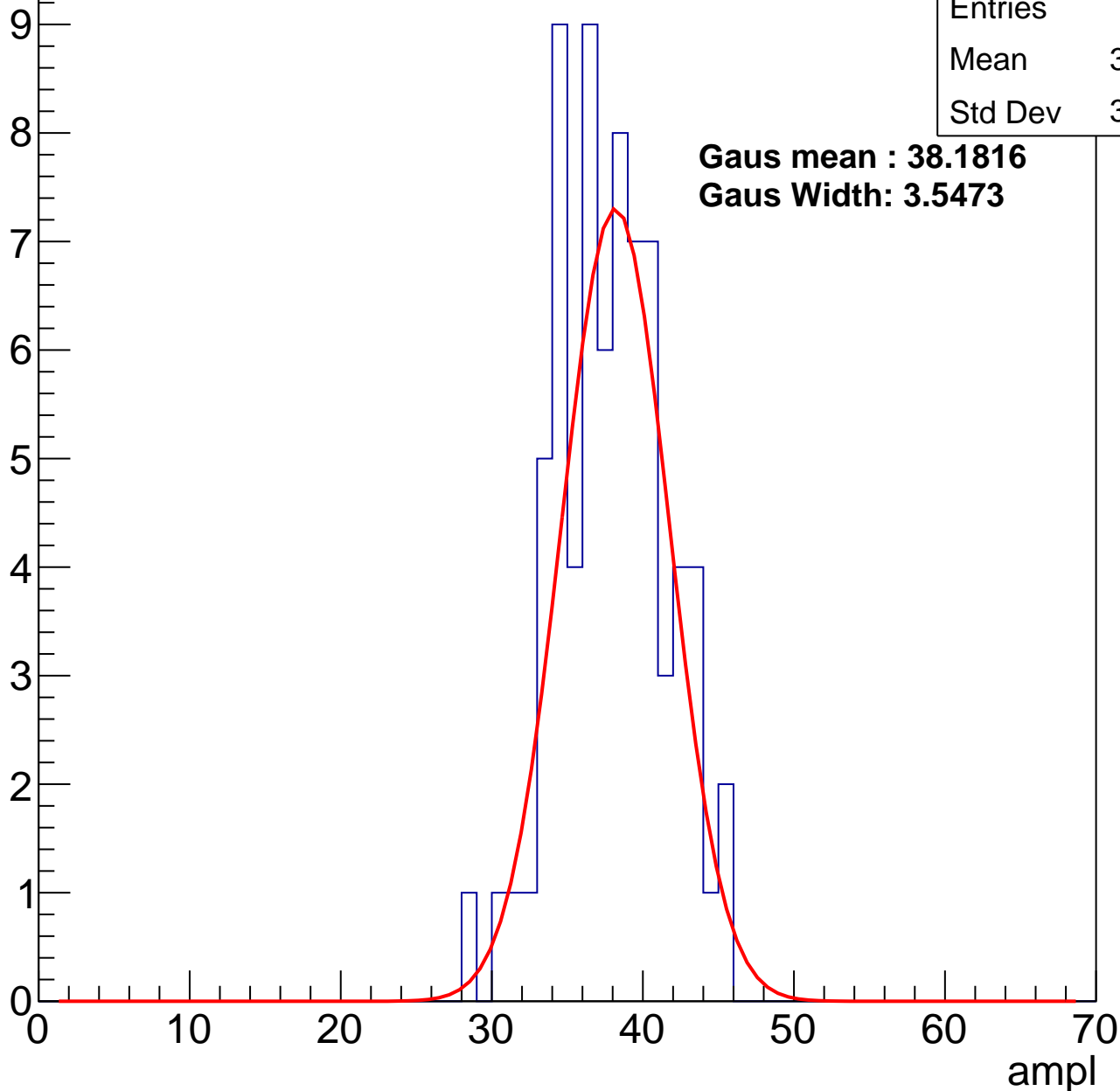
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.42
Std Dev	3.573

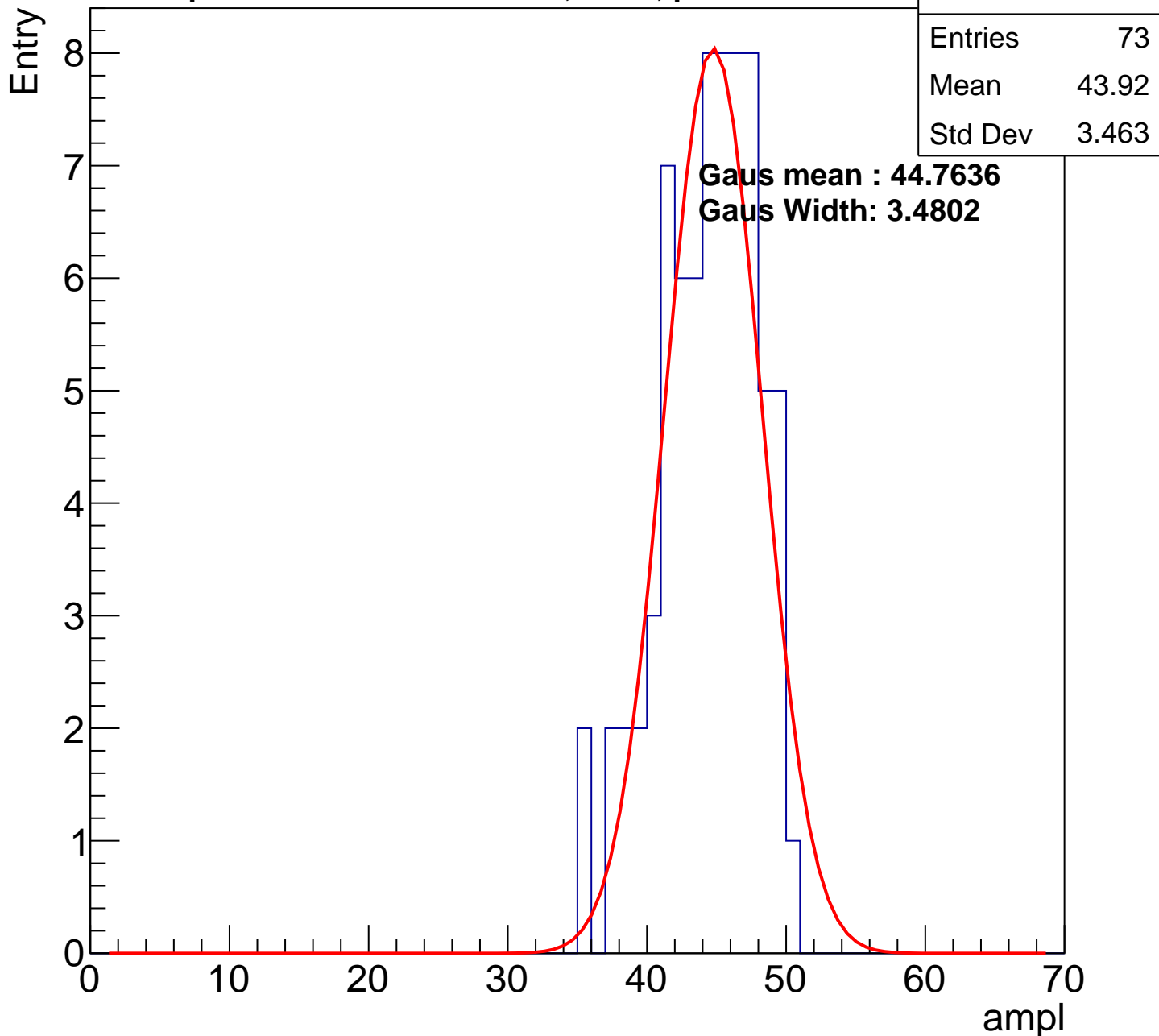
Gaus mean : 38.1816

Gaus Width: 3.5473



B0L002S, U9-ch97, adc2

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

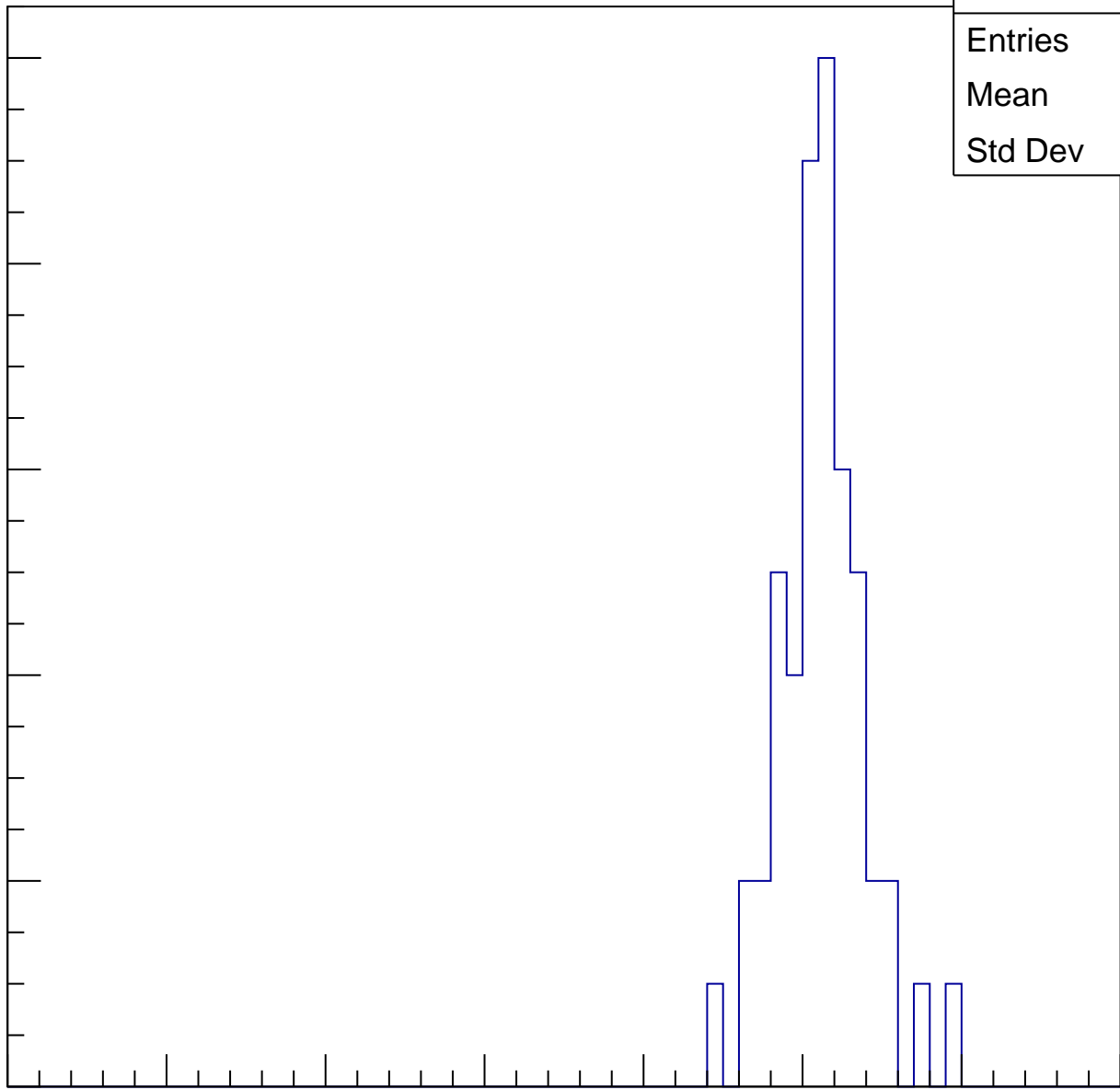
Entries	50
Mean	50.74
Std Dev	2.734

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	59
Mean	56.12
Std Dev	2.598

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

B0L002S, U9-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59
Std Dev	9.497

ampl

0

10

20

30

40

50

60

70

B0L002S, U9-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U9-ch98, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	52
Mean	30.25
Std Dev	5.042

Gaus mean : 30.7867

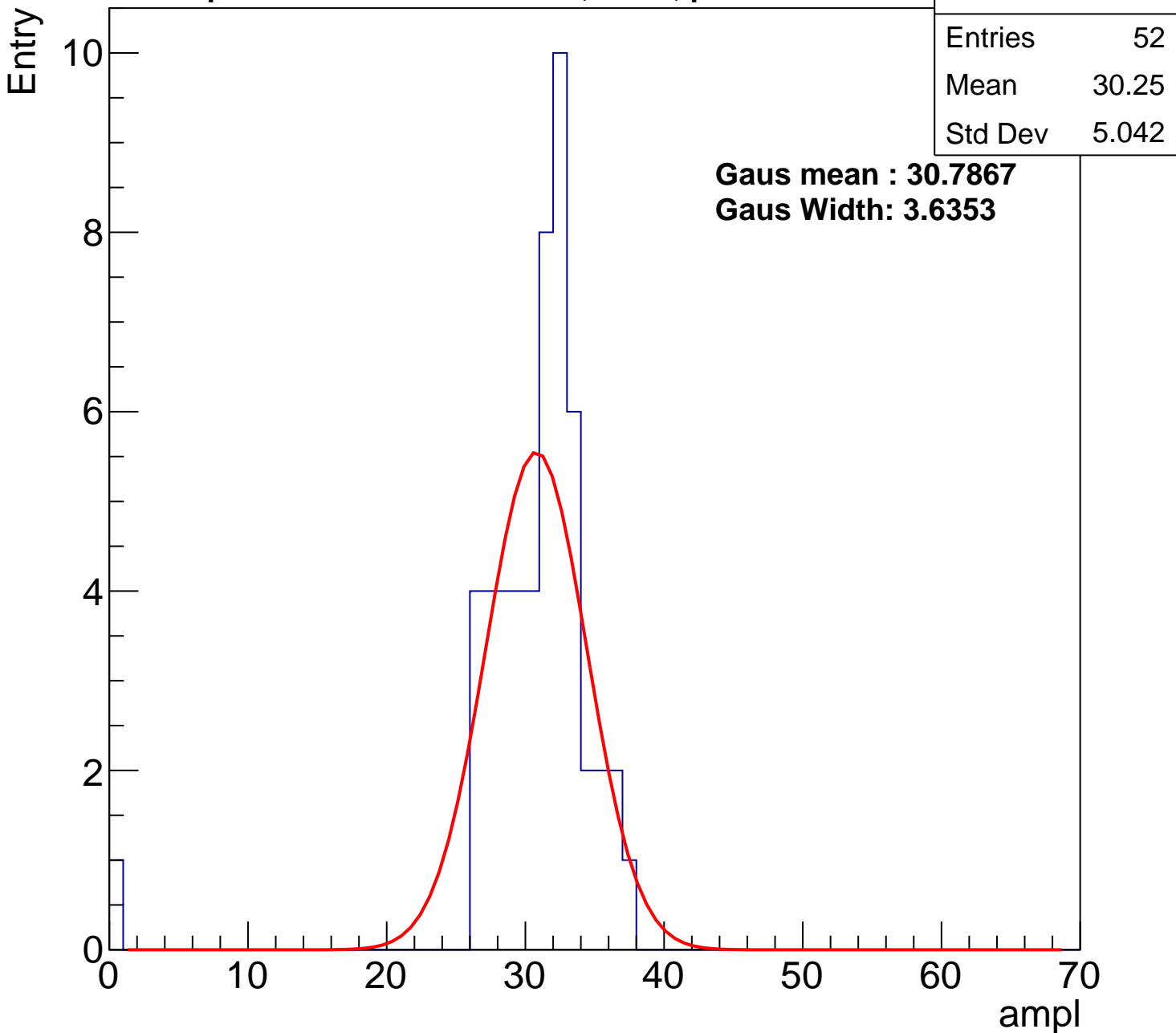
Gaus Width: 3.6353

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch98, adc1

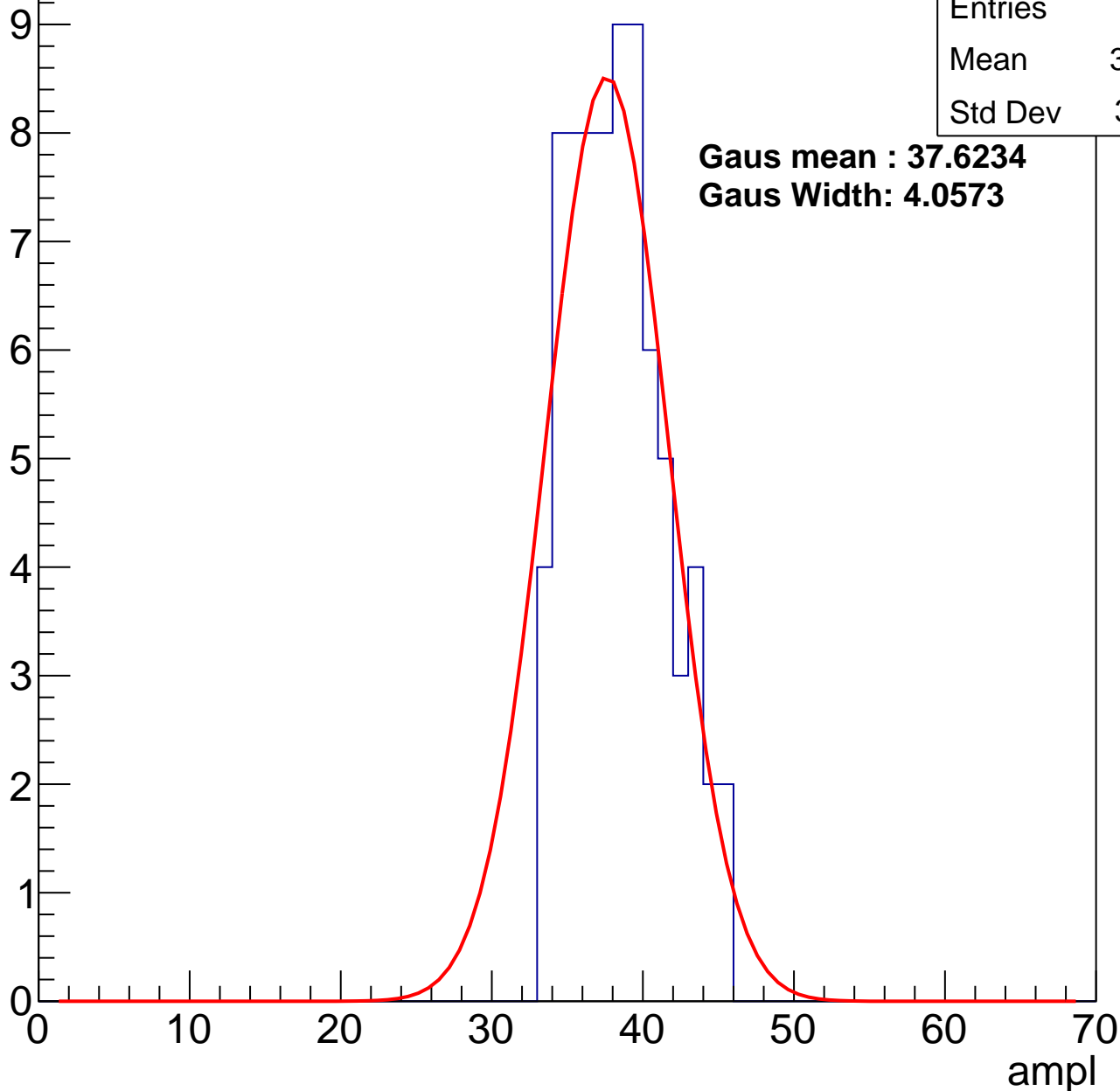
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.92
Std Dev	3.111

Gaus mean : 37.6234

Gaus Width: 4.0573



B0L002S, U9-ch98, adc2

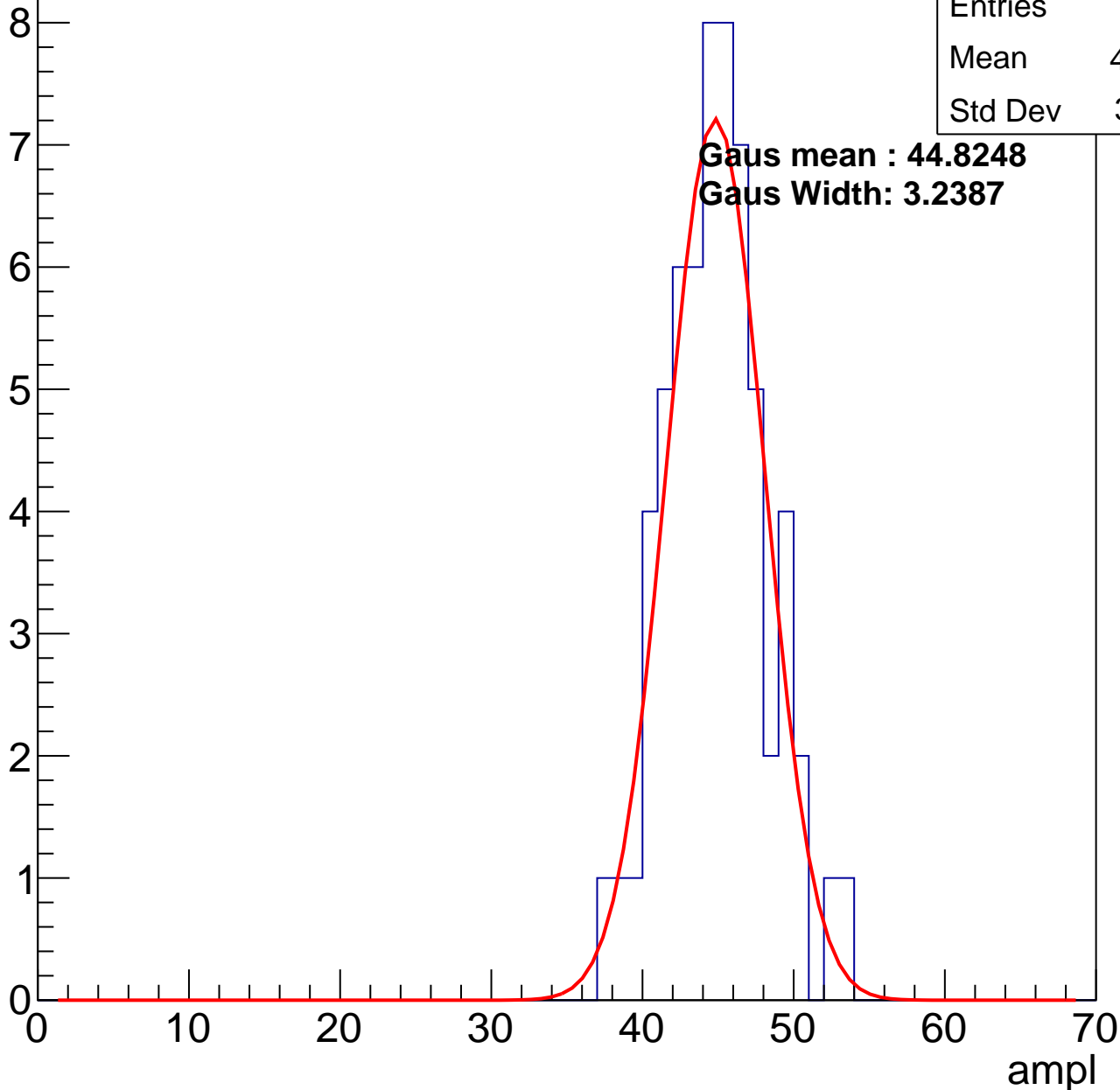
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.44
Std Dev	3.281

Gaus mean : 44.8248

Gaus Width: 3.2387

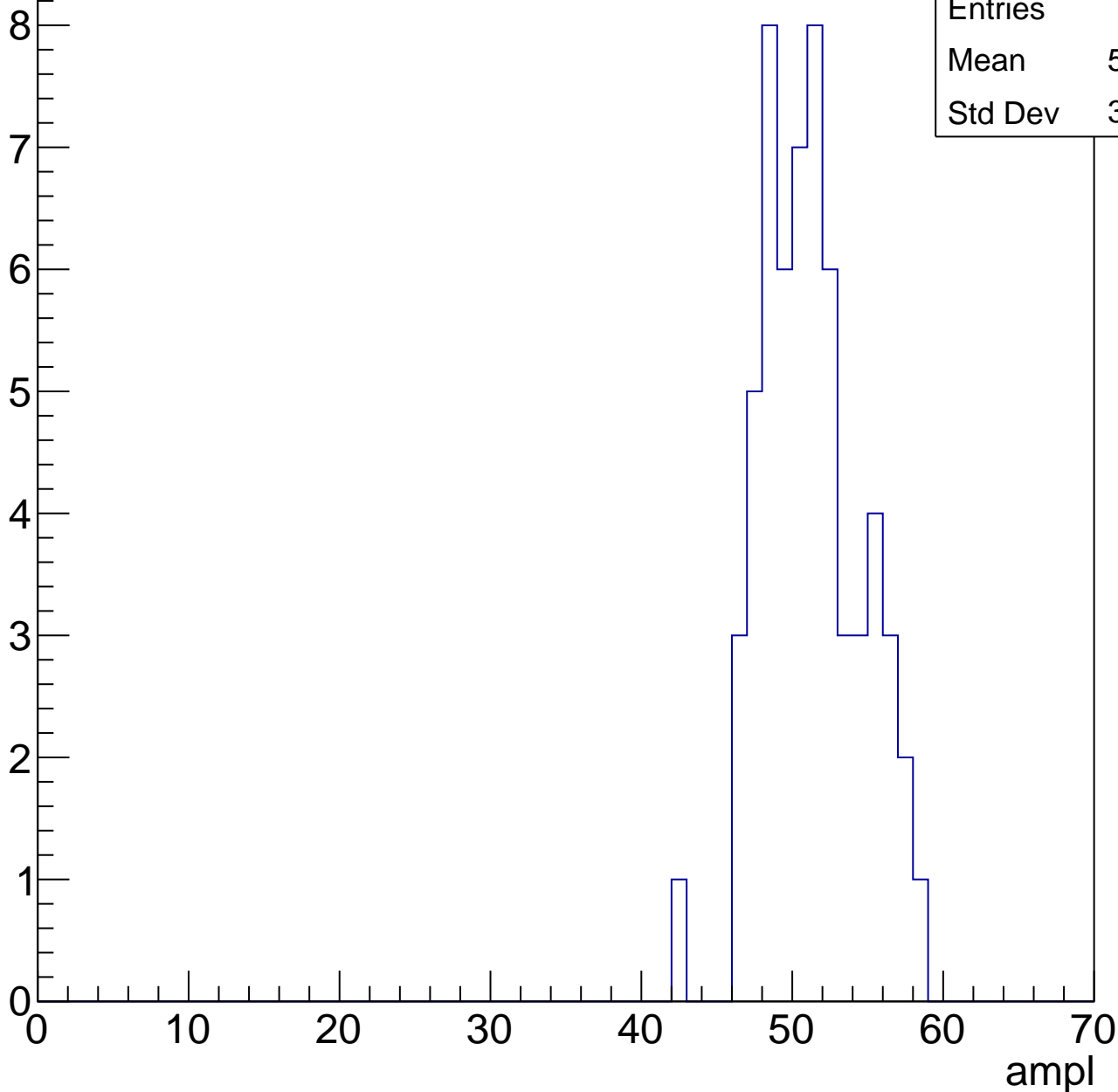


B0L002S, U9-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.73
Std Dev	3.265

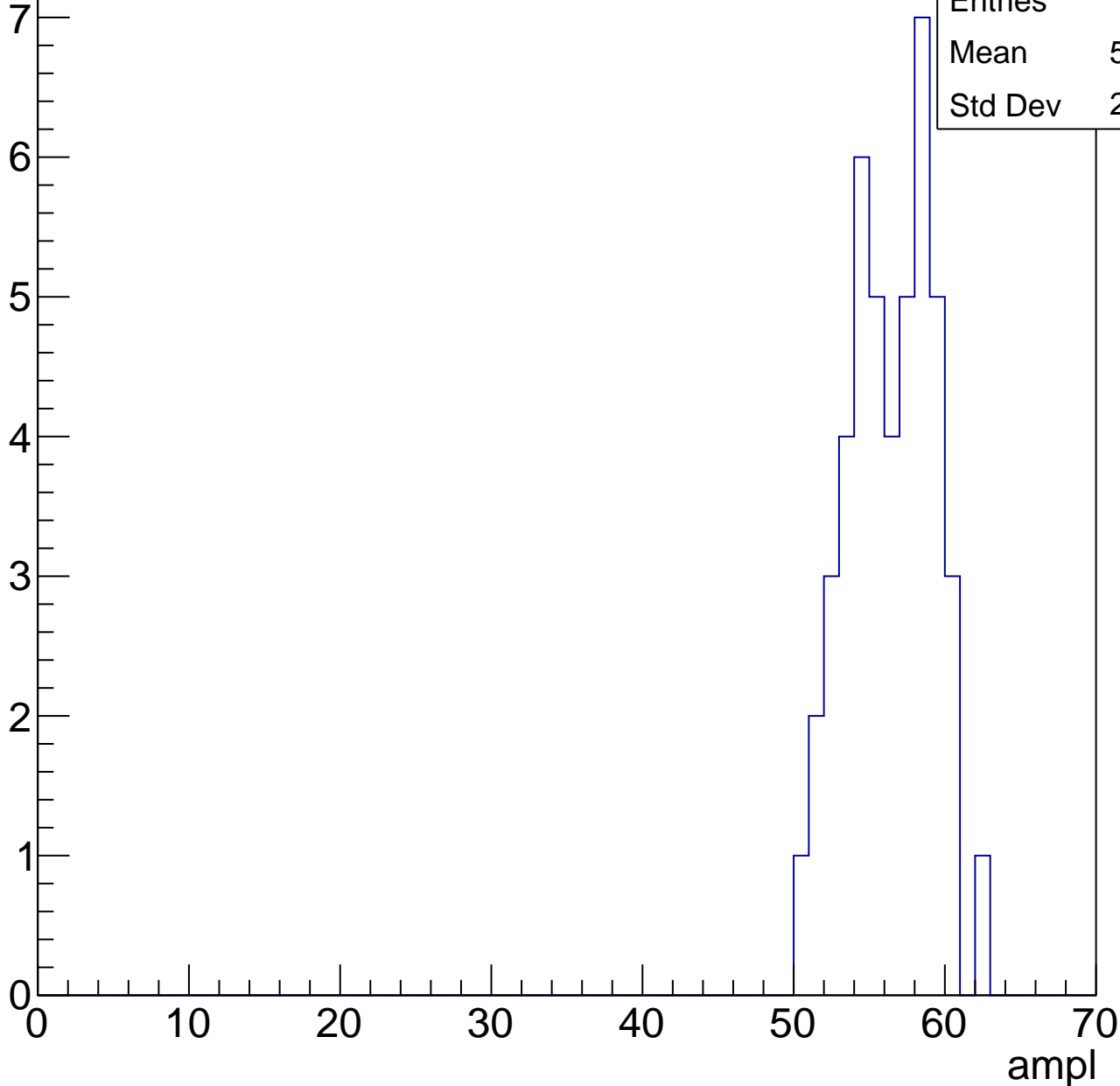


B0L002S, U9-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	55.89
Std Dev	2.799

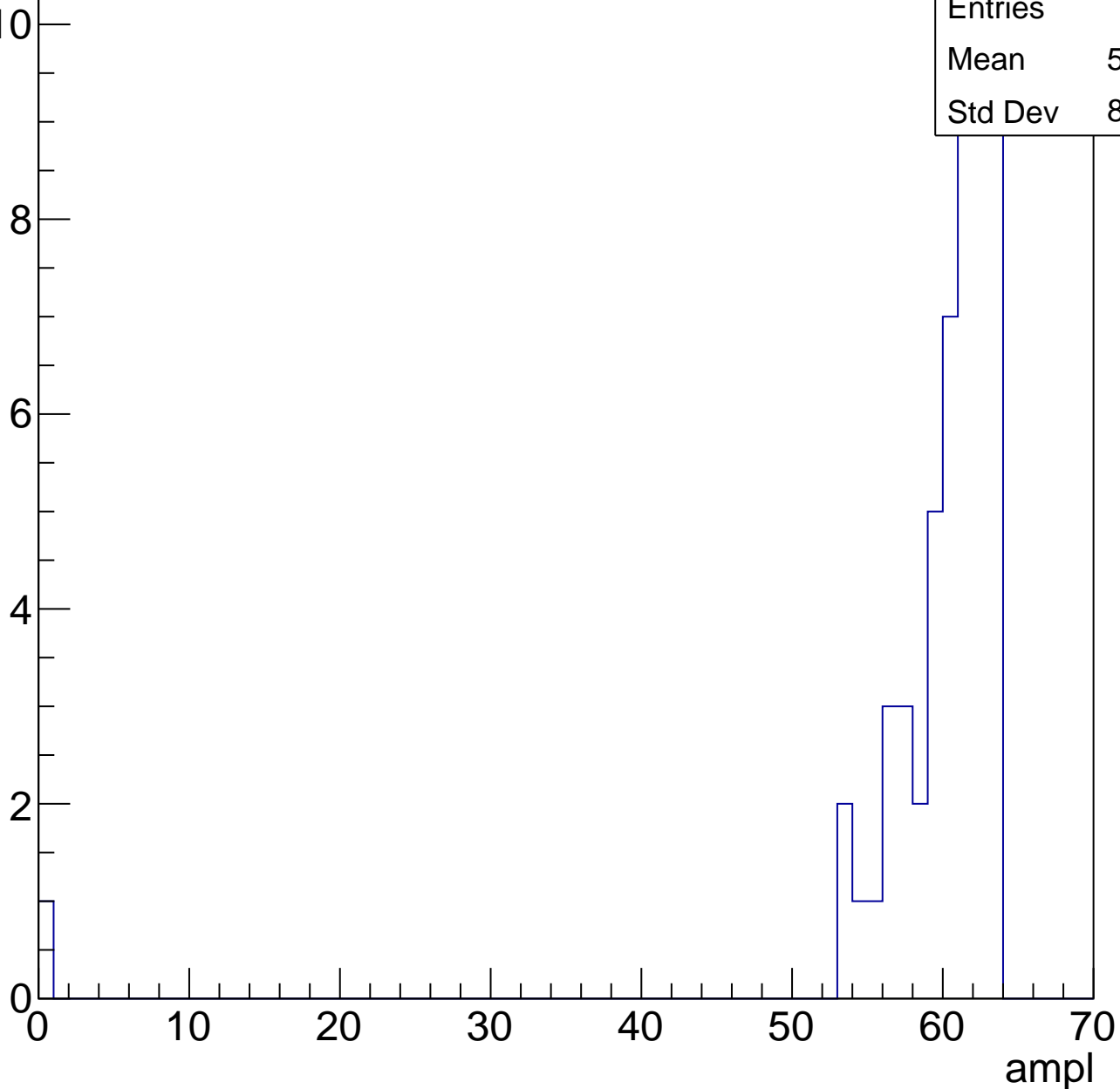


B0L002S, U9-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	58.87
Std Dev	8.585



B0L002S, U9-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U9-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch99, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

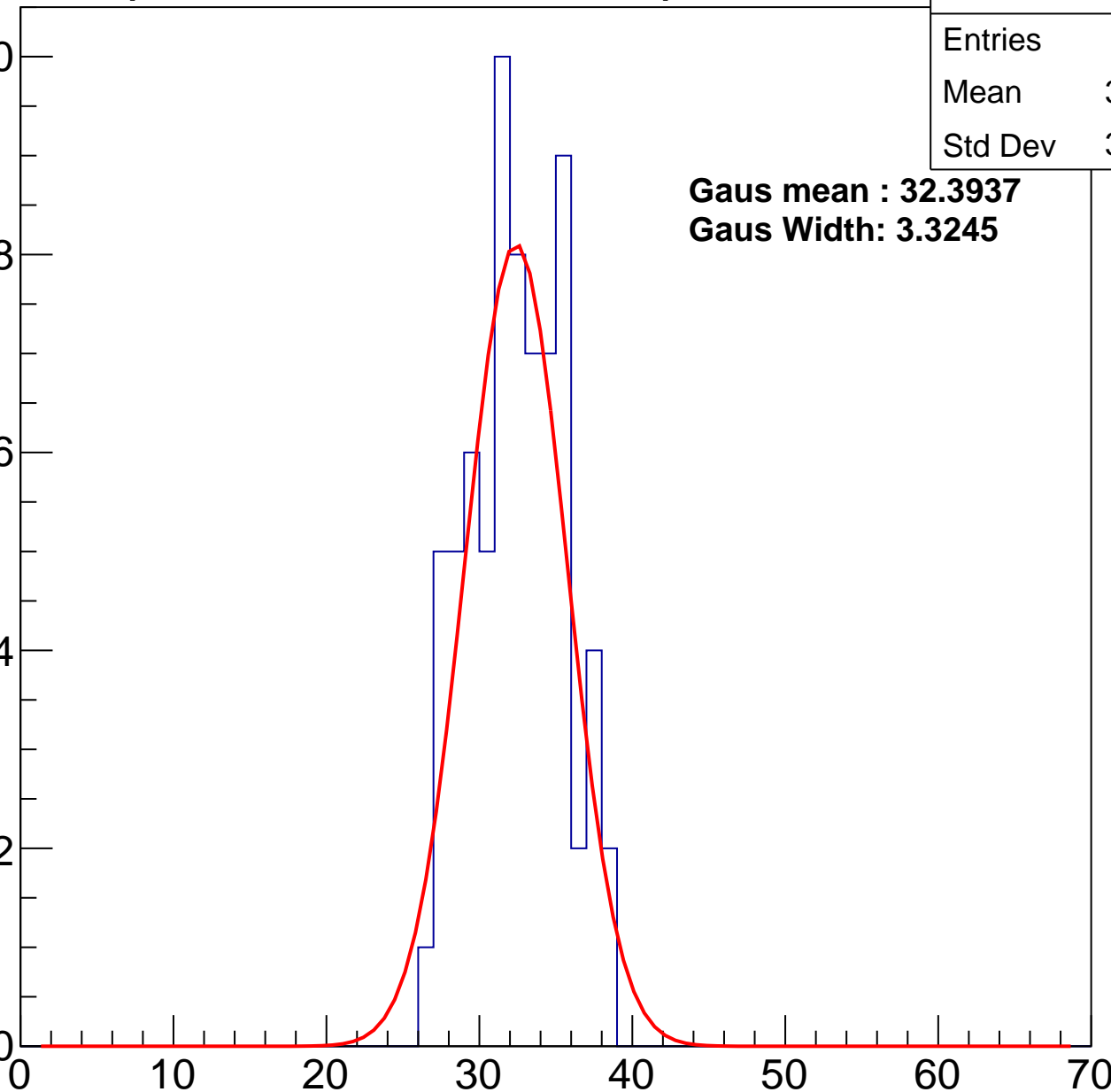
Entries	71
Mean	31.99
Std Dev	3.014

Gaus mean : 32.3937

Gaus Width: 3.3245

10
8
6
4
2
0

ampl



B0L002S, U9-ch99, adc1

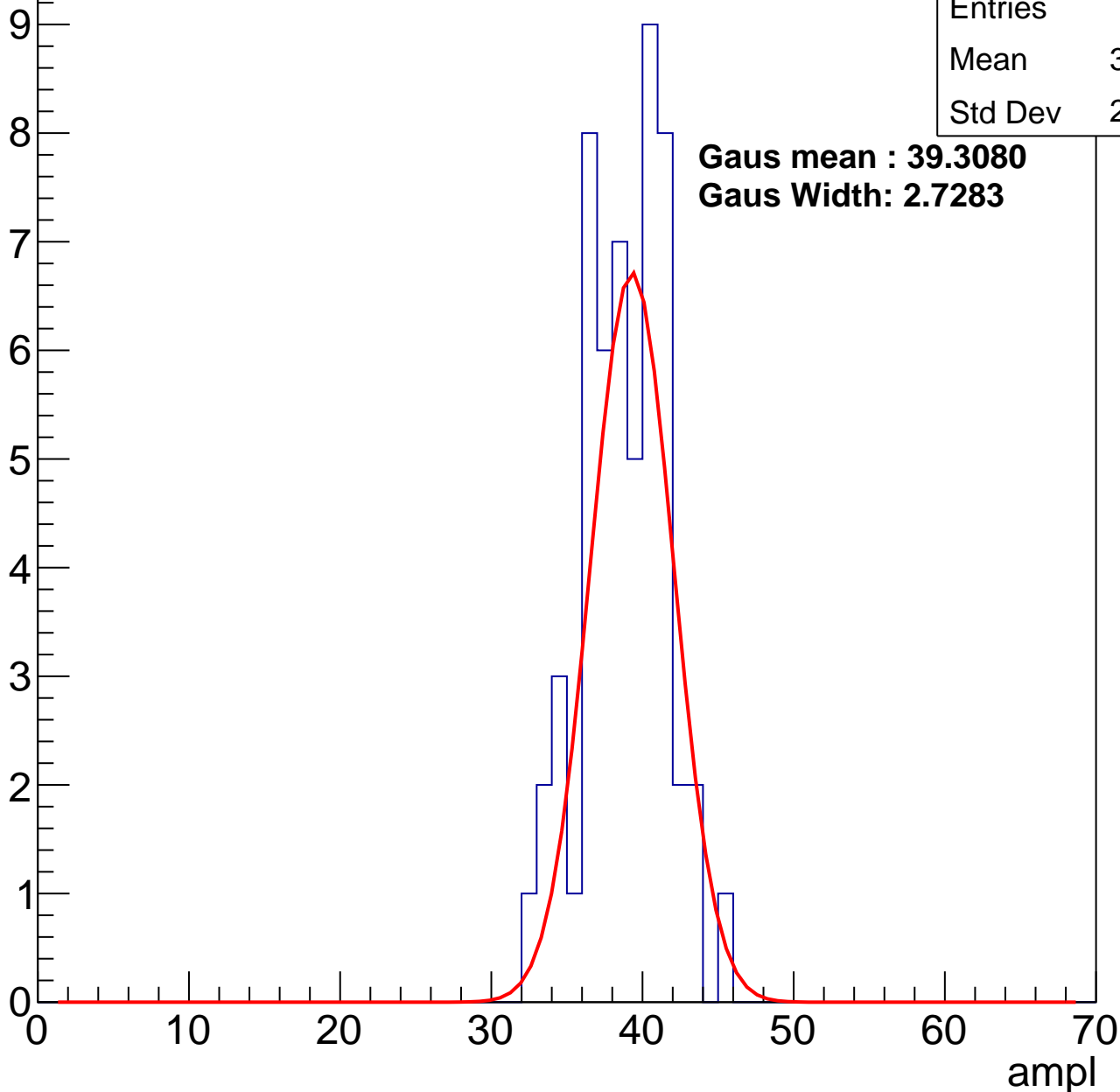
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	38.35
Std Dev	2.758

Gaus mean : 39.3080

Gaus Width: 2.7283



B0L002S, U9-ch99, adc2

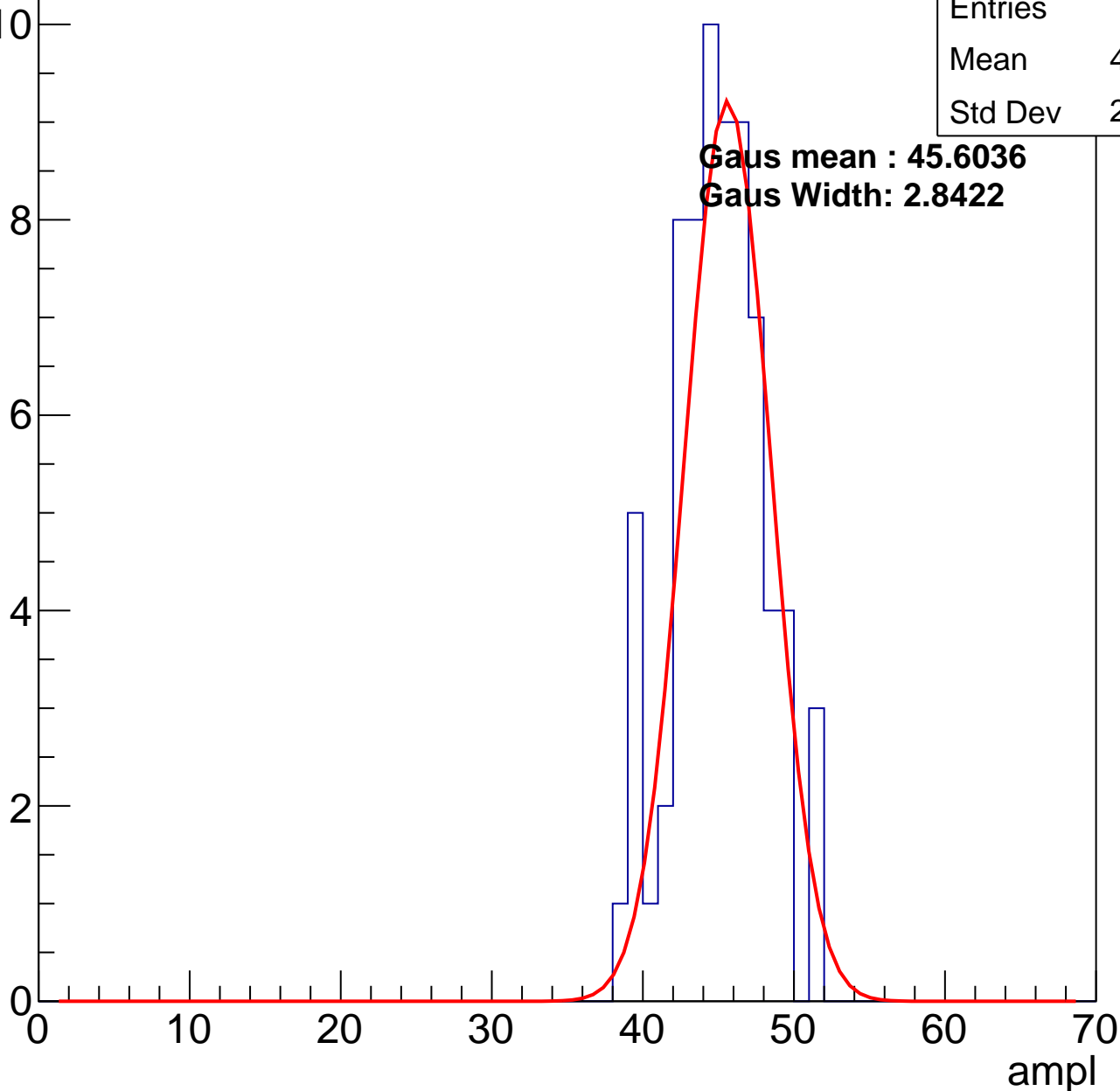
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.56
Std Dev	2.982

Gaus mean : 45.6036

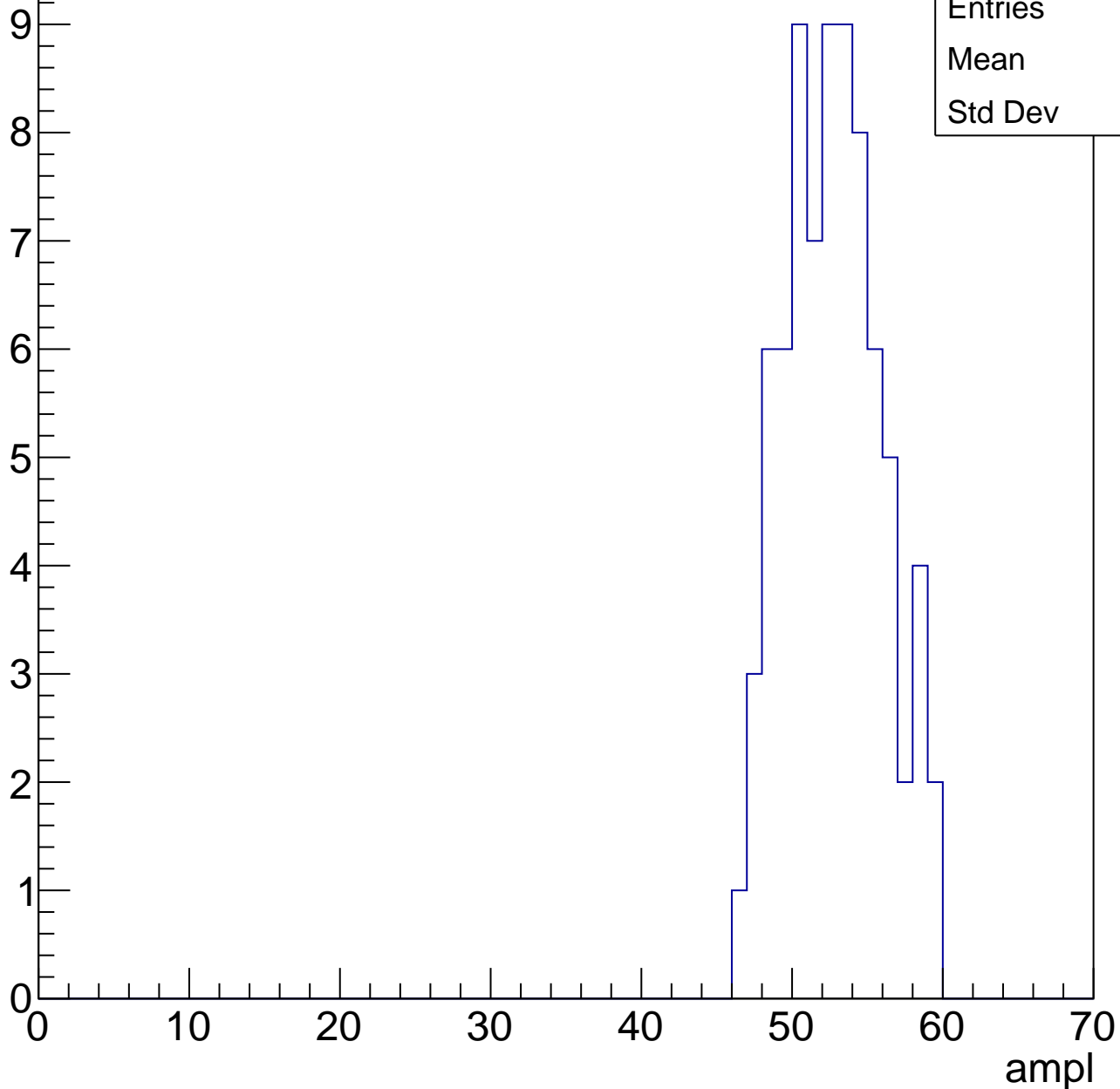
Gaus Width: 2.8422



B0L002S, U9-ch99, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch99, adc4

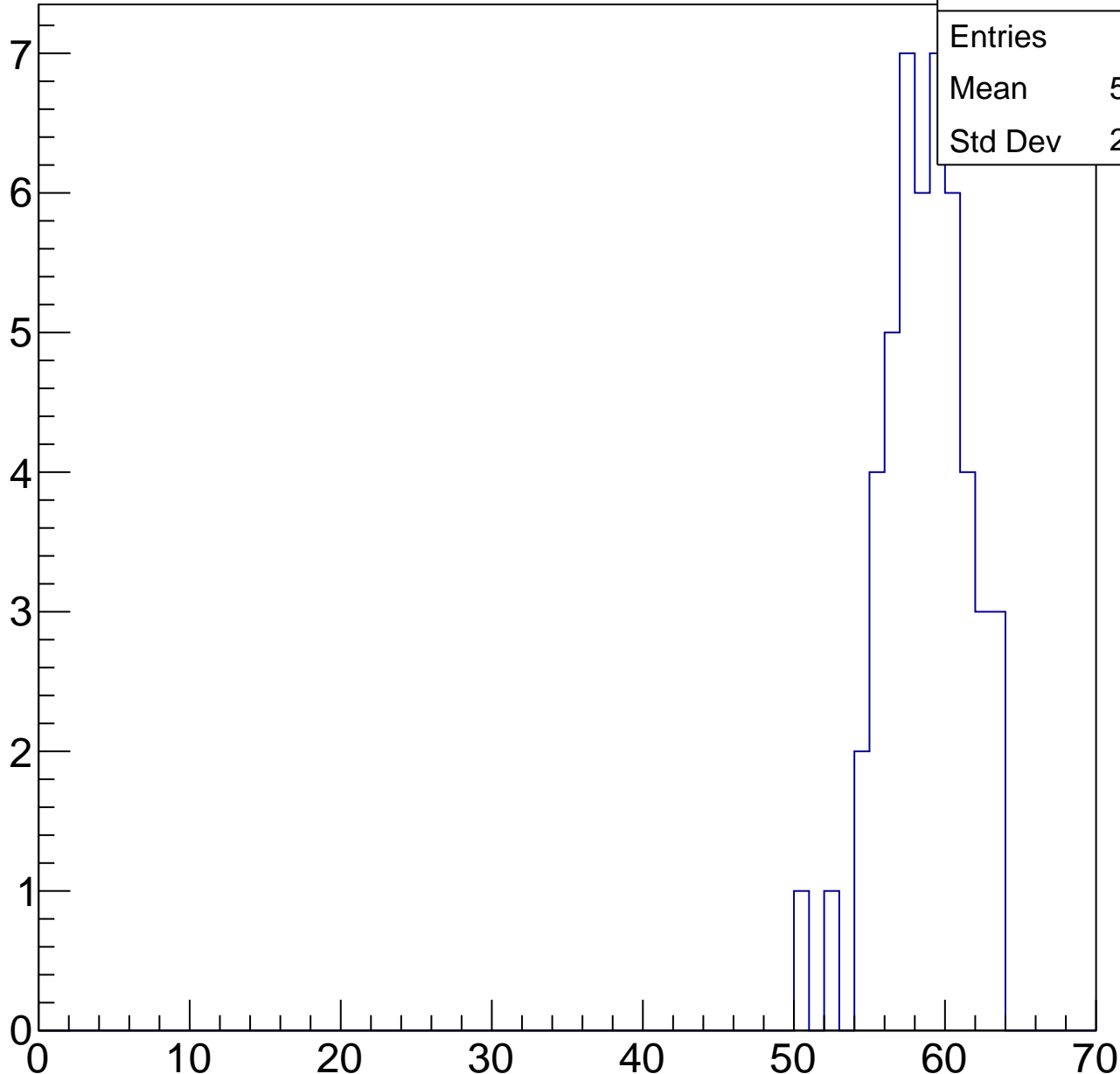
calib_packv5_042523_0143.root, FC#8, port C1

Entry

7
6
5
4
3
2
1
0

Entries	49
Mean	58.14
Std Dev	2.799

ampl

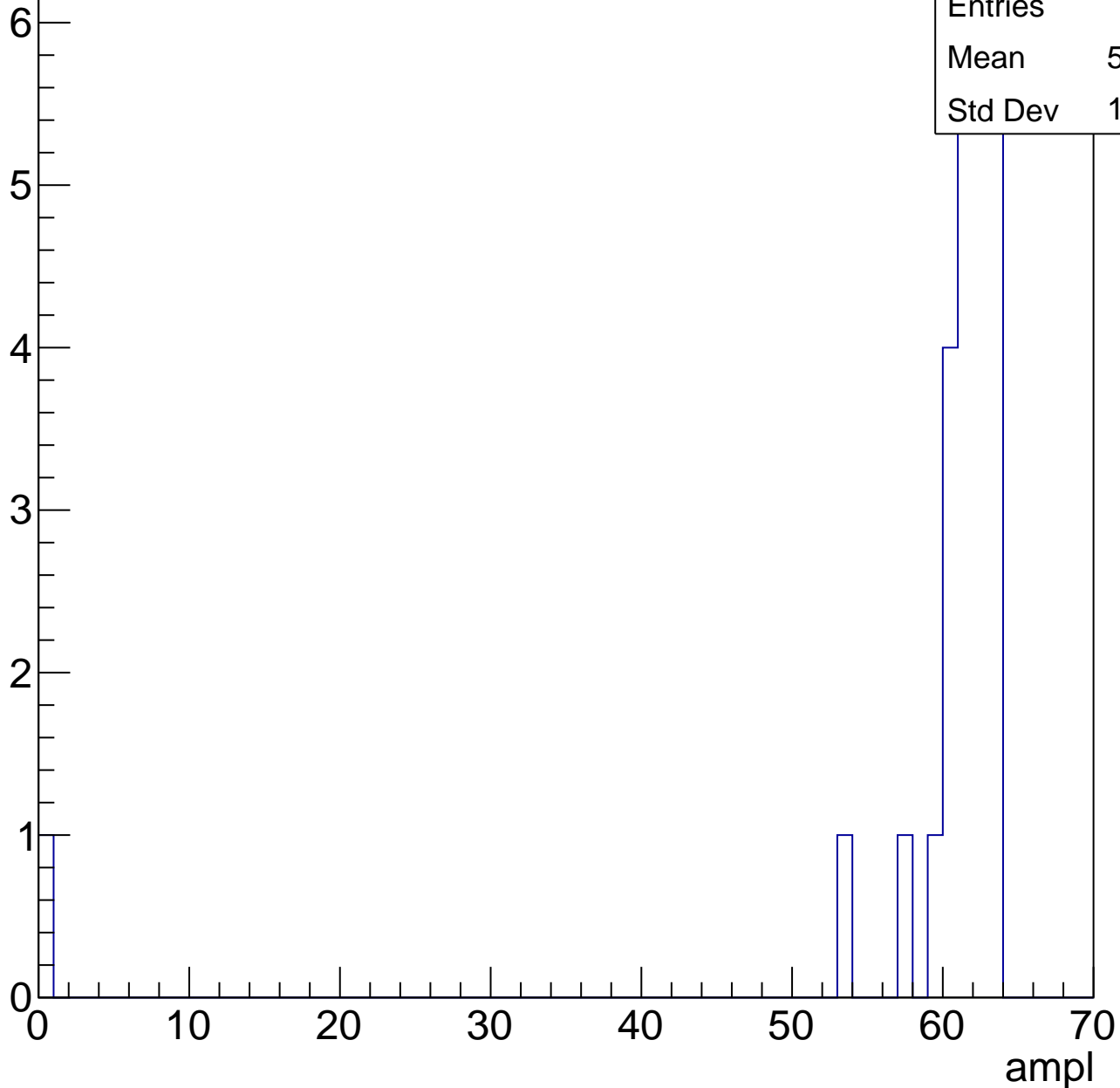


B0L002S, U9-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	58.65
Std Dev	11.92



B0L002S, U9-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B0L002S, U9-ch100, adc0

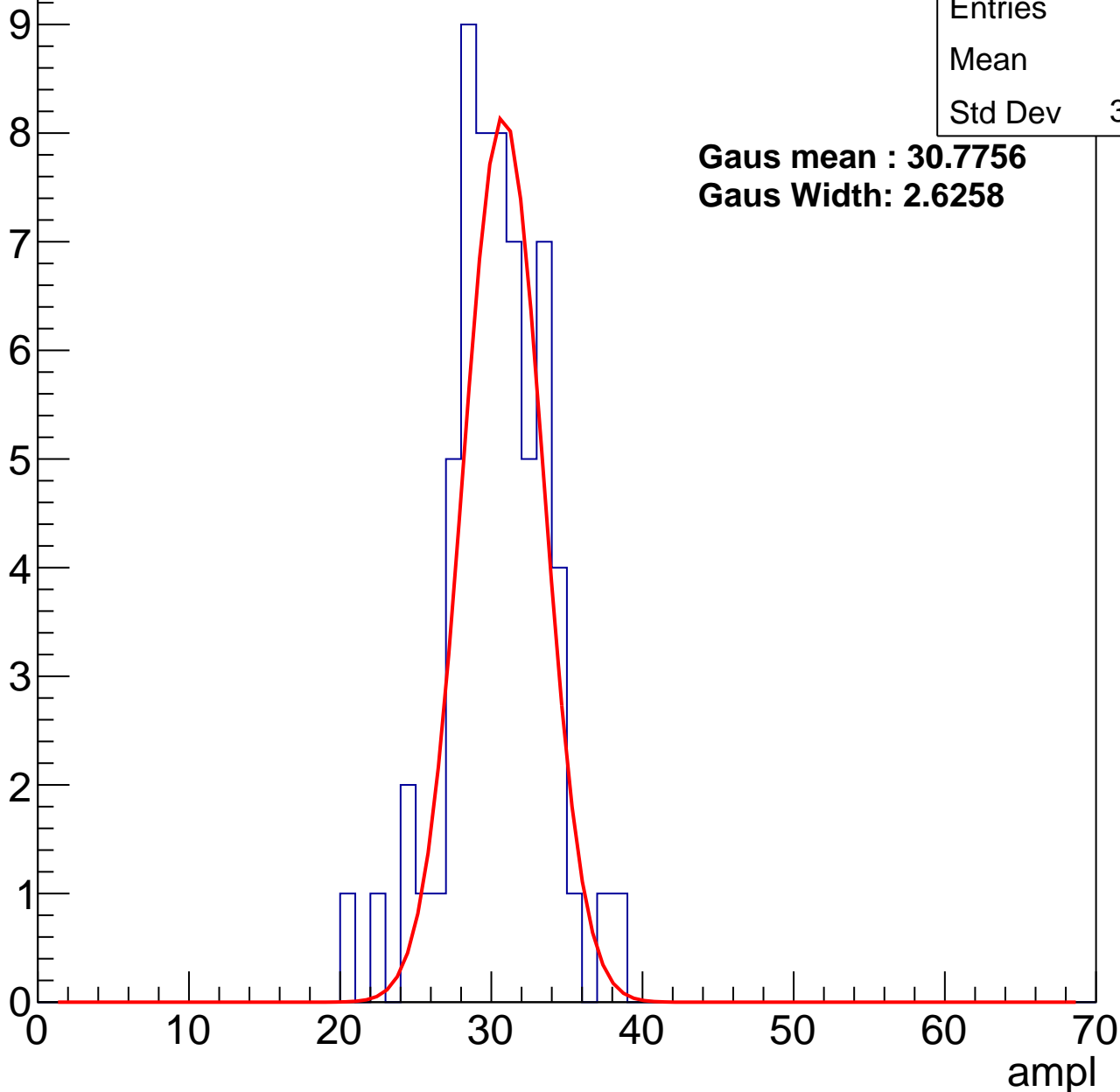
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.9
Std Dev	3.256

Gaus mean : 30.7756

Gaus Width: 2.6258



B0L002S, U9-ch100, adc1

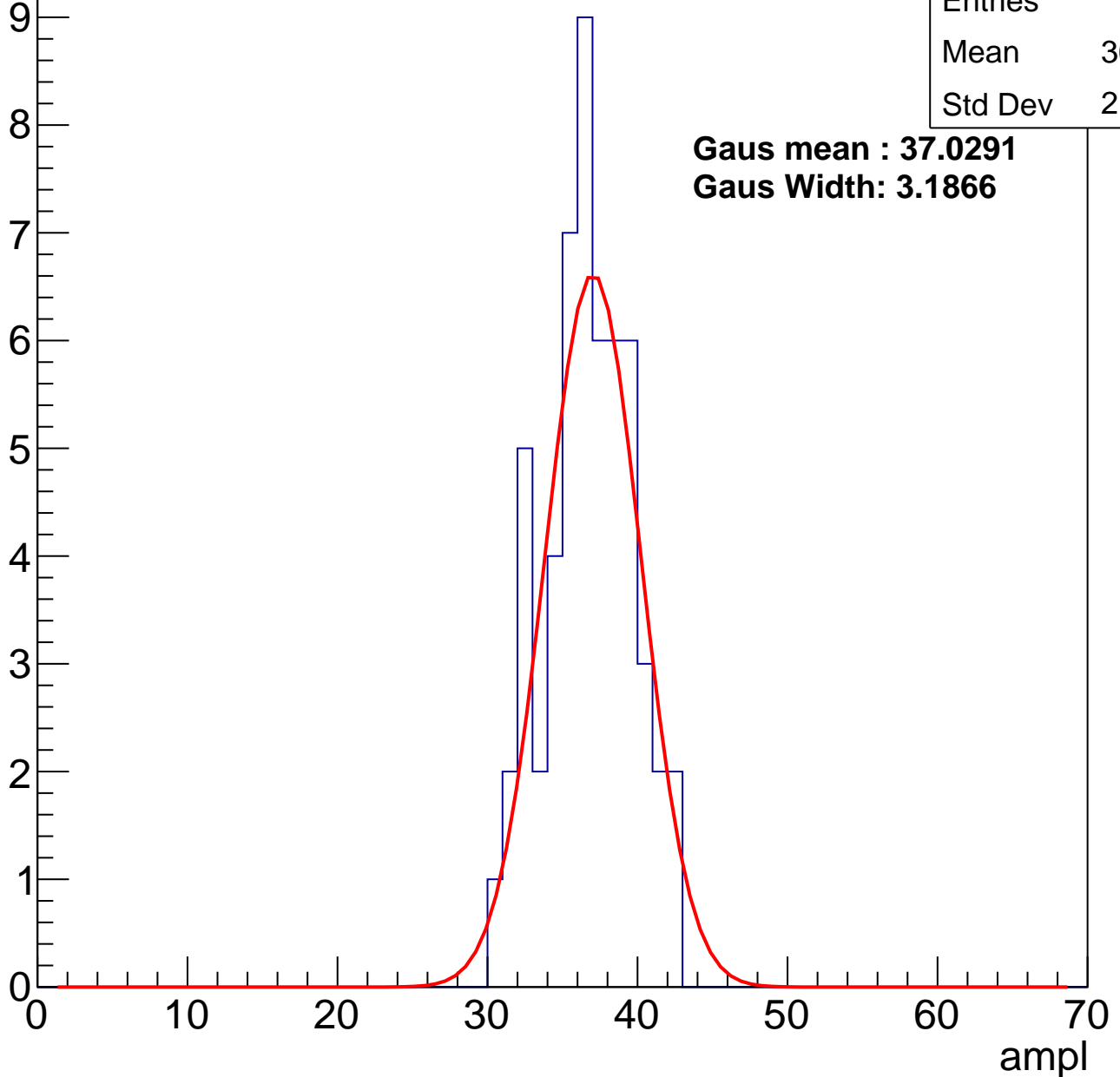
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.24
Std Dev	2.885

Gaus mean : 37.0291

Gaus Width: 3.1866



B0L002S, U9-ch100, adc2

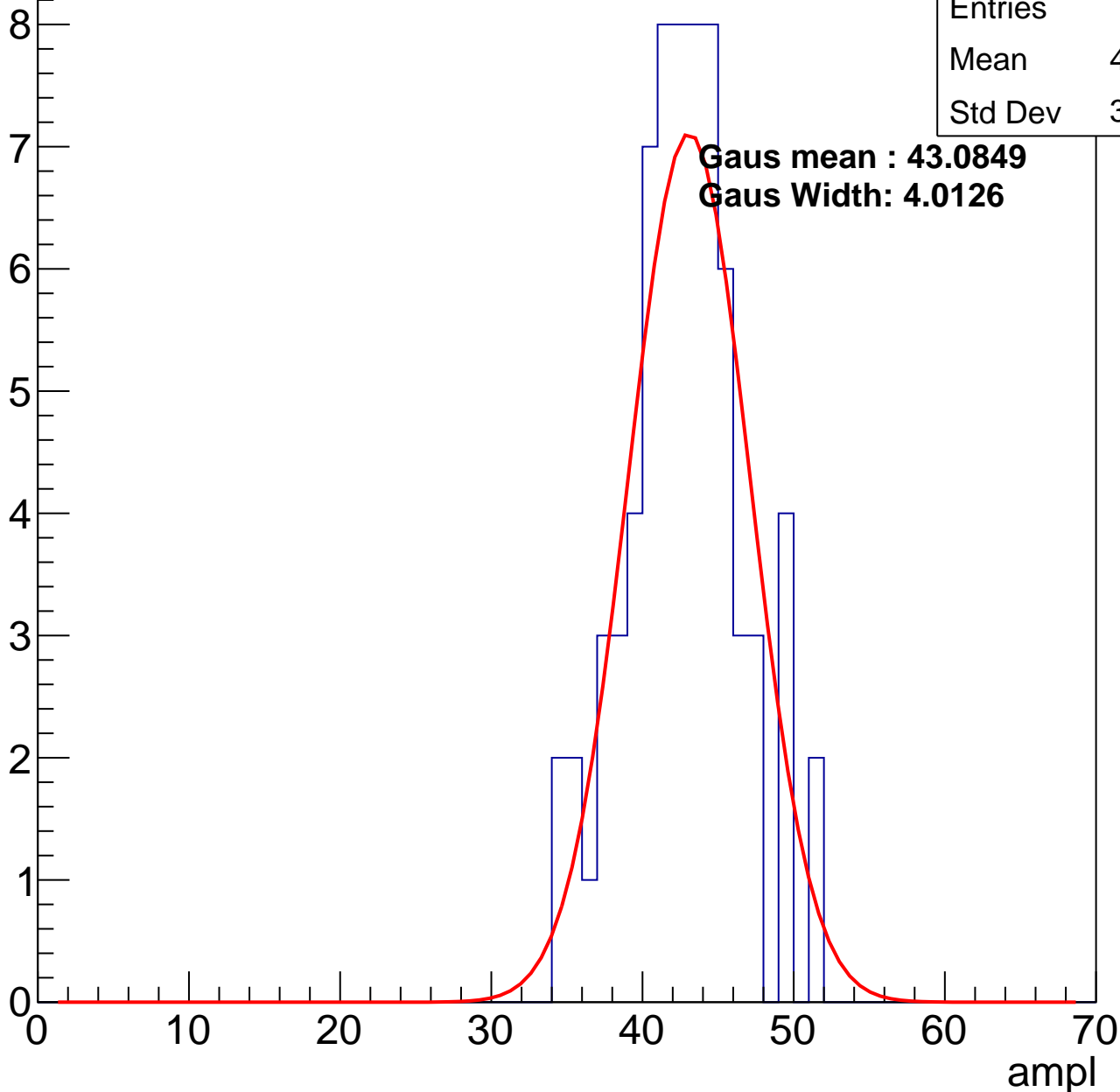
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	42.25
Std Dev	3.778

Gaus mean : 43.0849

Gaus Width: 4.0126



B0L002S, U9-ch100, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	58
Mean	48.66
Std Dev	3.149

Entry

10

8

6

4

2

0

0

10

20

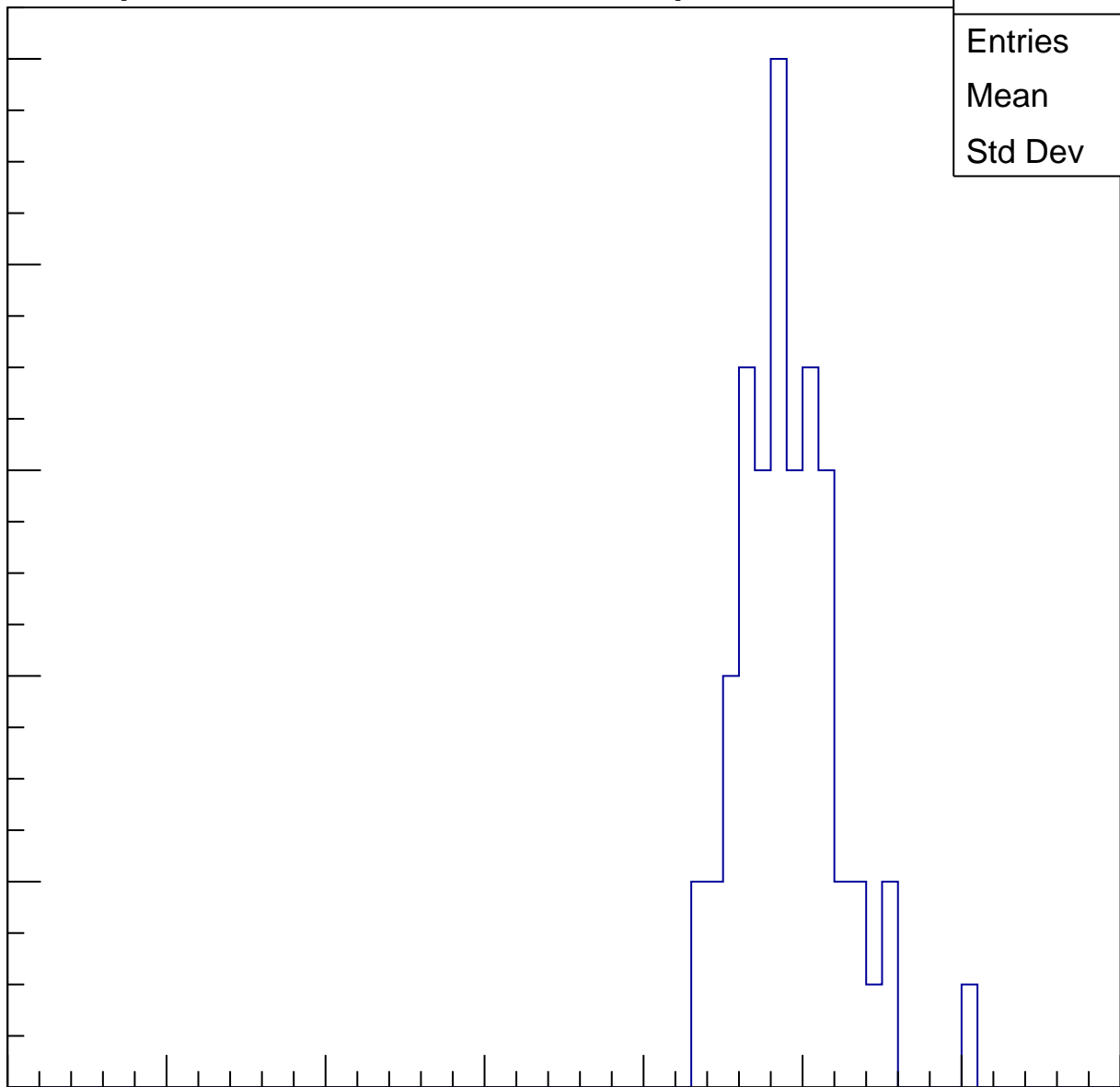
30

40

50

60

ampl



B0L002S, U9-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

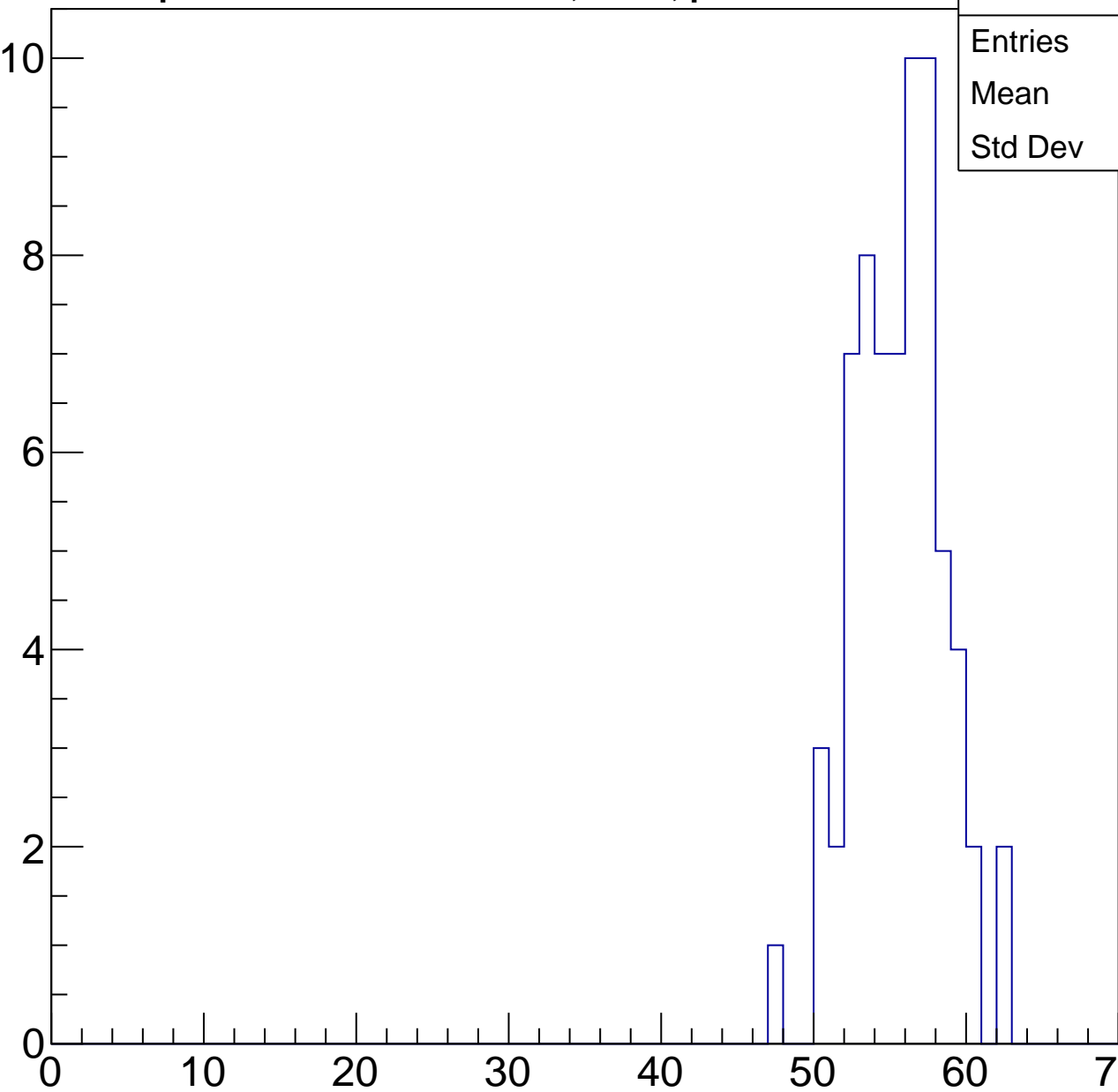
Entries	68
Mean	55.15
Std Dev	2.917

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

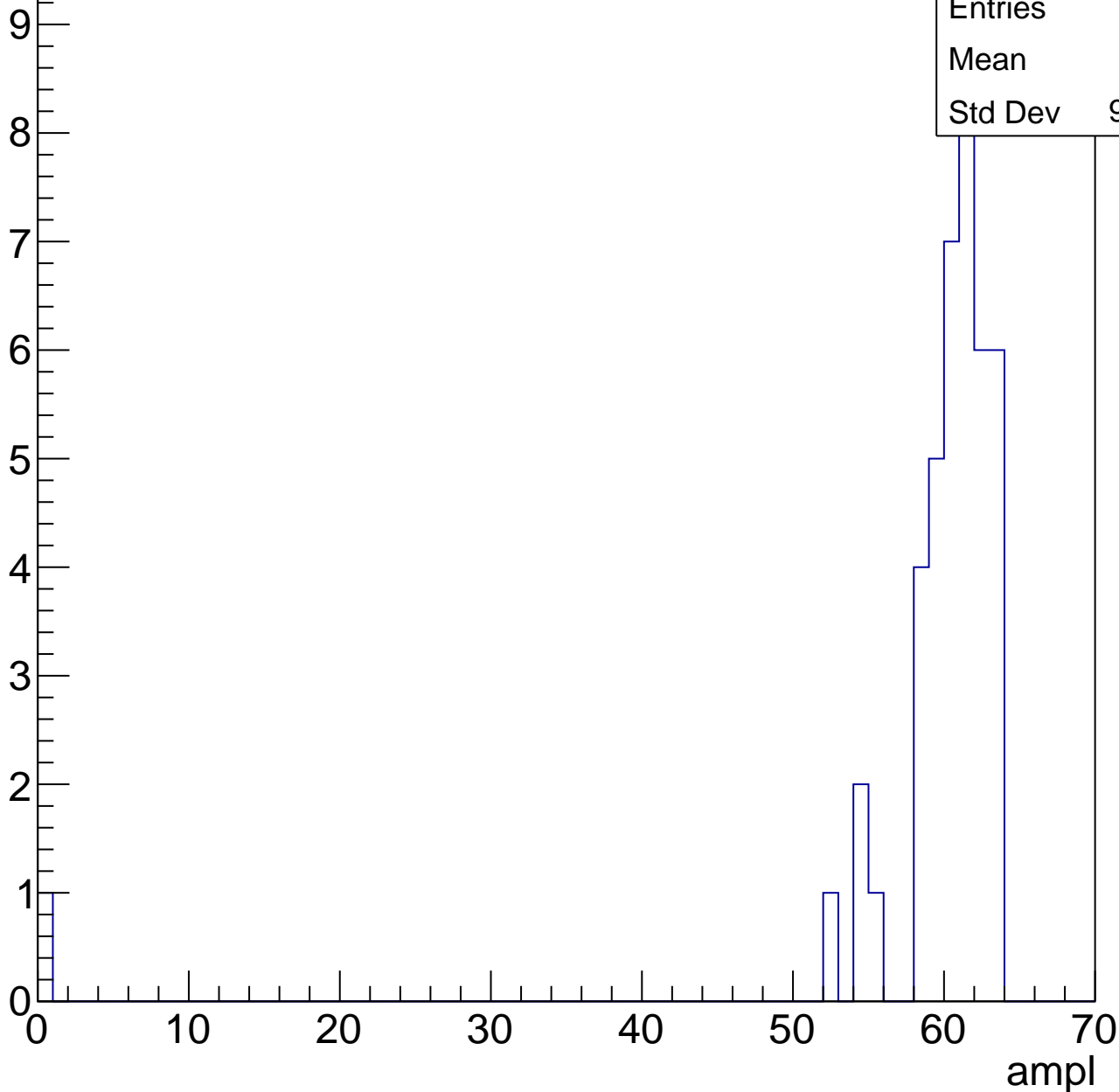


B0L002S, U9-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

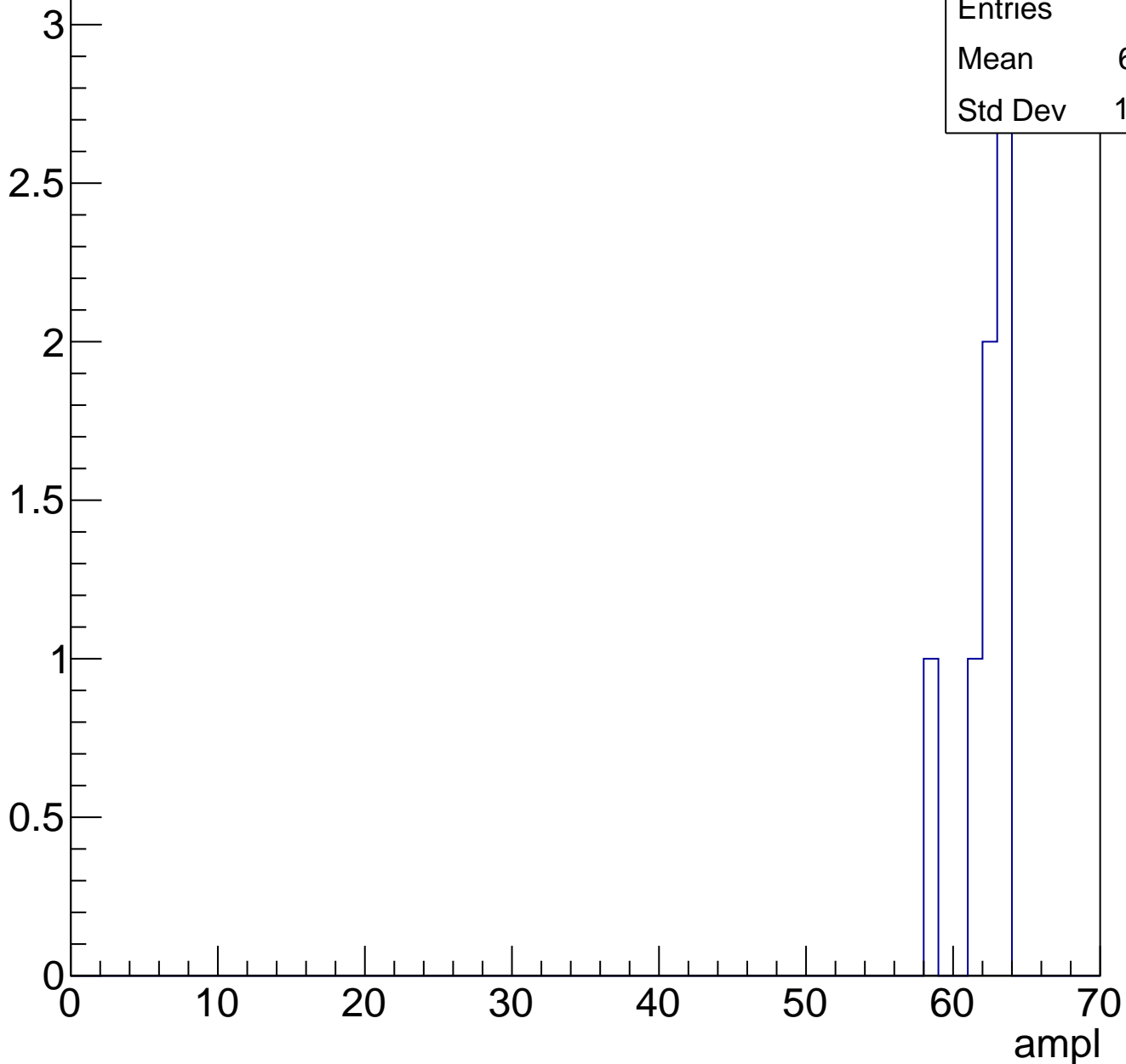
Entries	42
Mean	58.6
Std Dev	9.495



B0L002S, U9-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

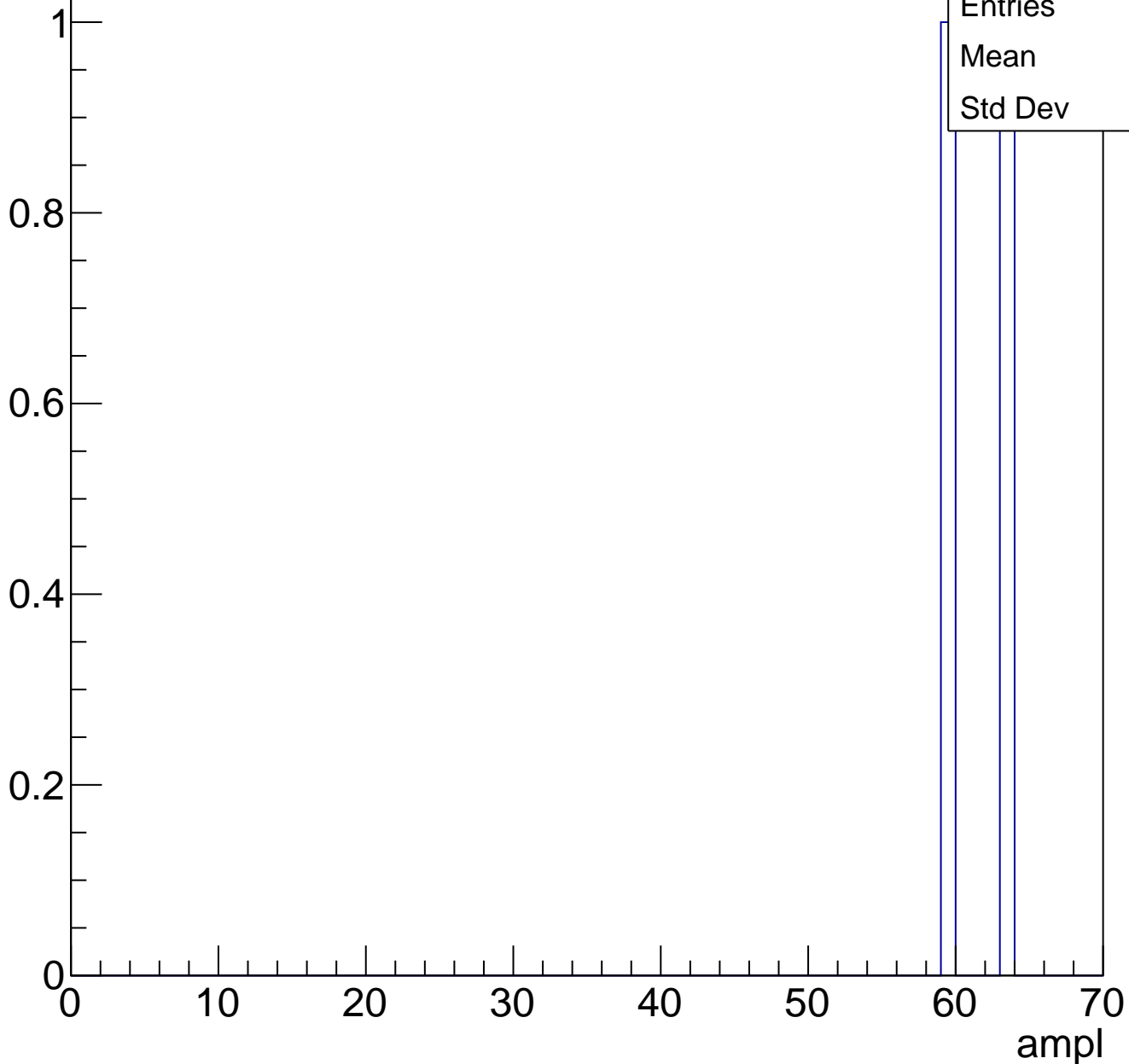


Entries	7
Mean	61.71
Std Dev	1.666

B0L002S, U9-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch101, adc0

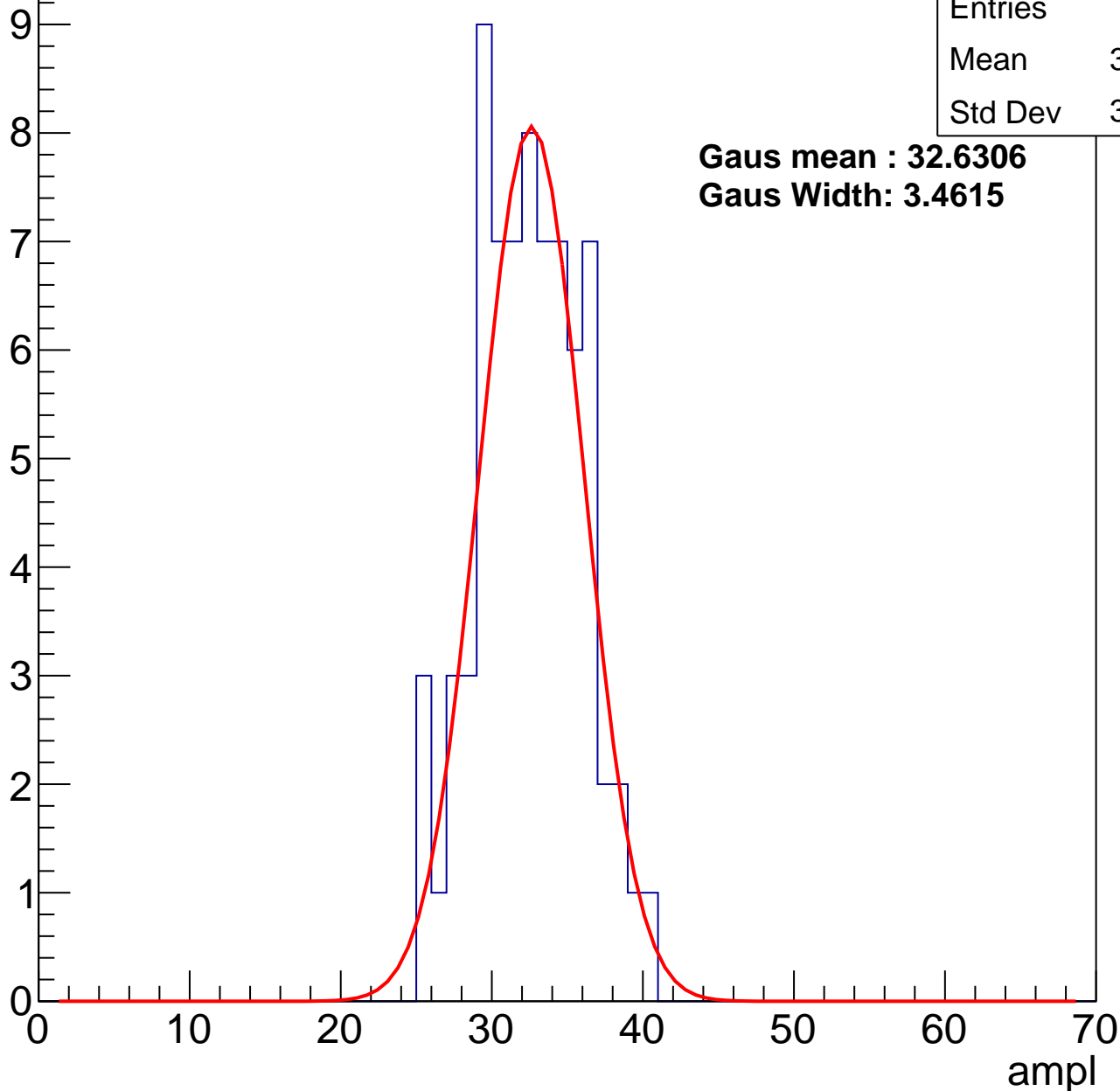
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	32.03
Std Dev	3.405

Gaus mean : 32.6306

Gaus Width: 3.4615



B0L002S, U9-ch101, adc1

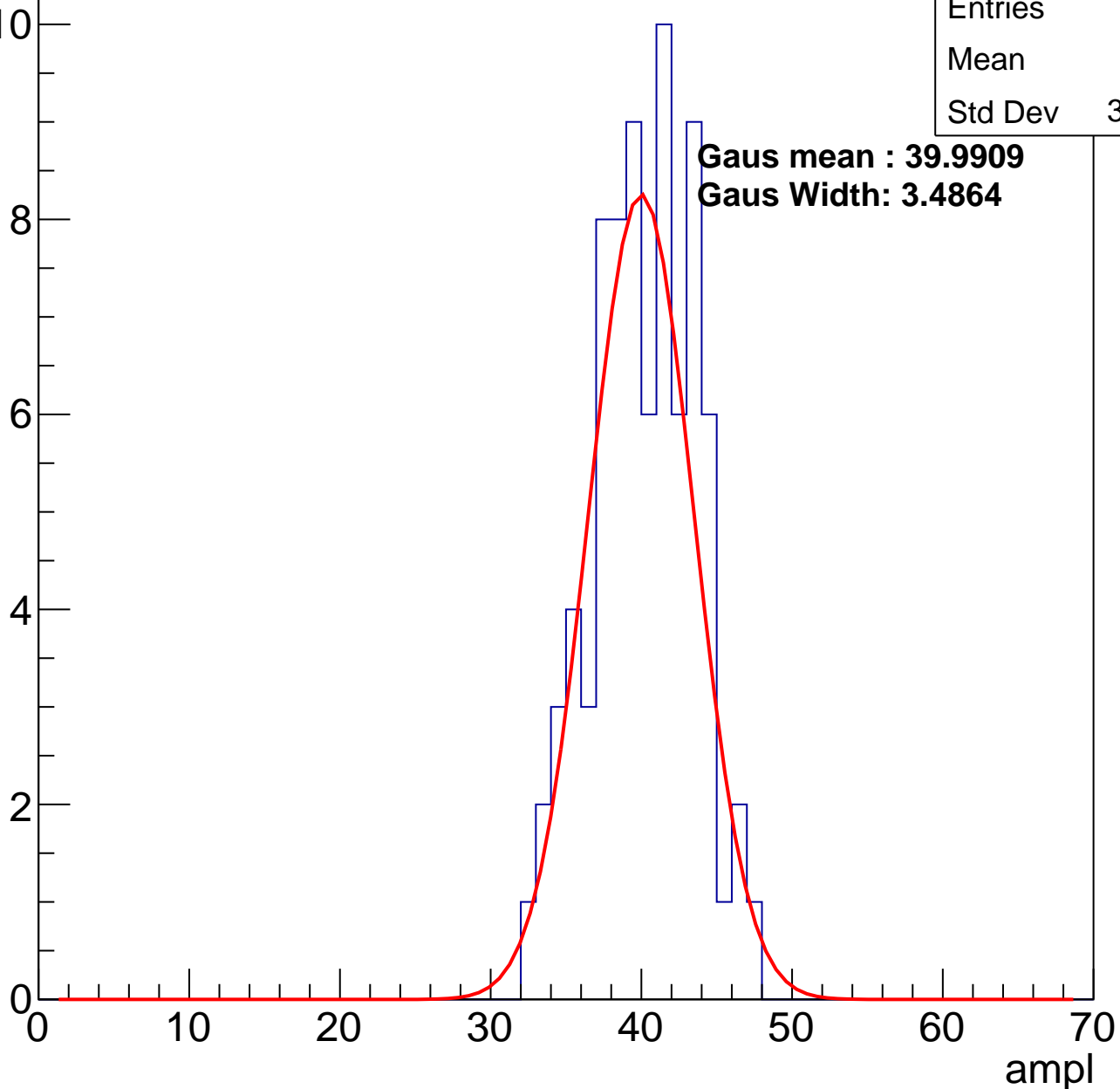
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	39.7
Std Dev	3.339

Gaus mean : 39.9909

Gaus Width: 3.4864



B0L002S, U9-ch101, adc2

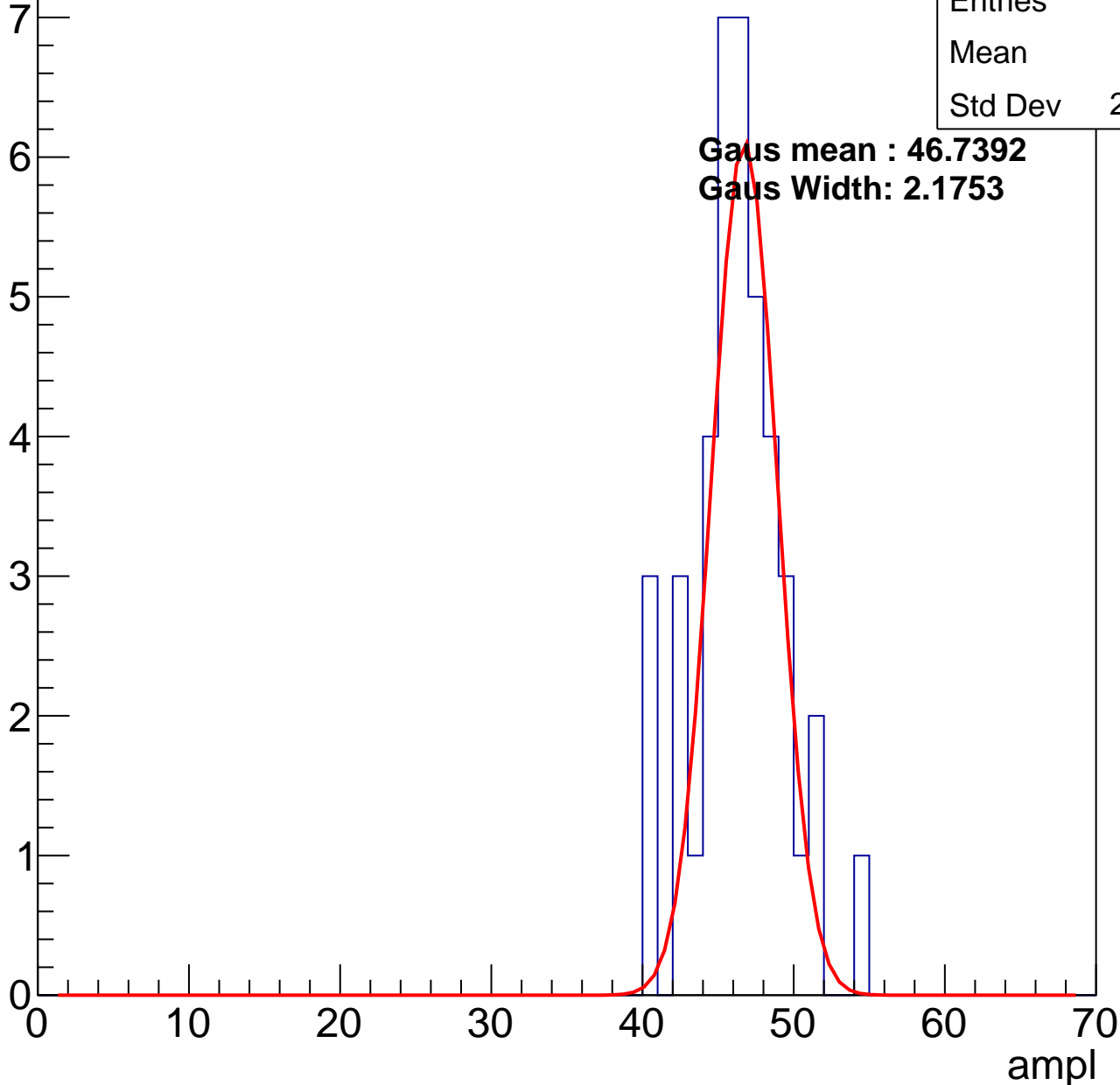
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	45.9
Std Dev	2.986

Gaus mean : 46.7392

Gaus Width: 2.1753

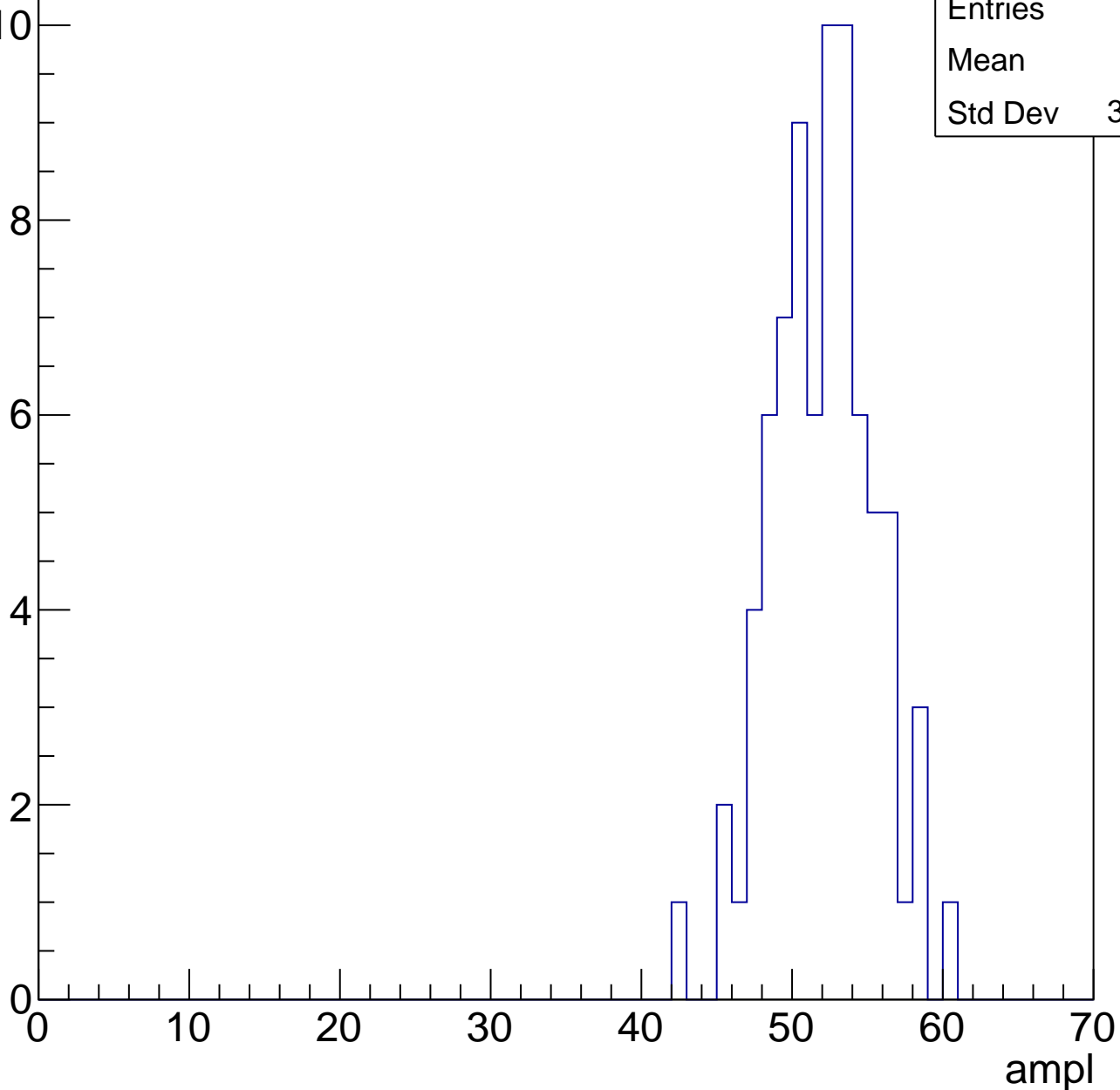


B0L002S, U9-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	51.6
Std Dev	3.378

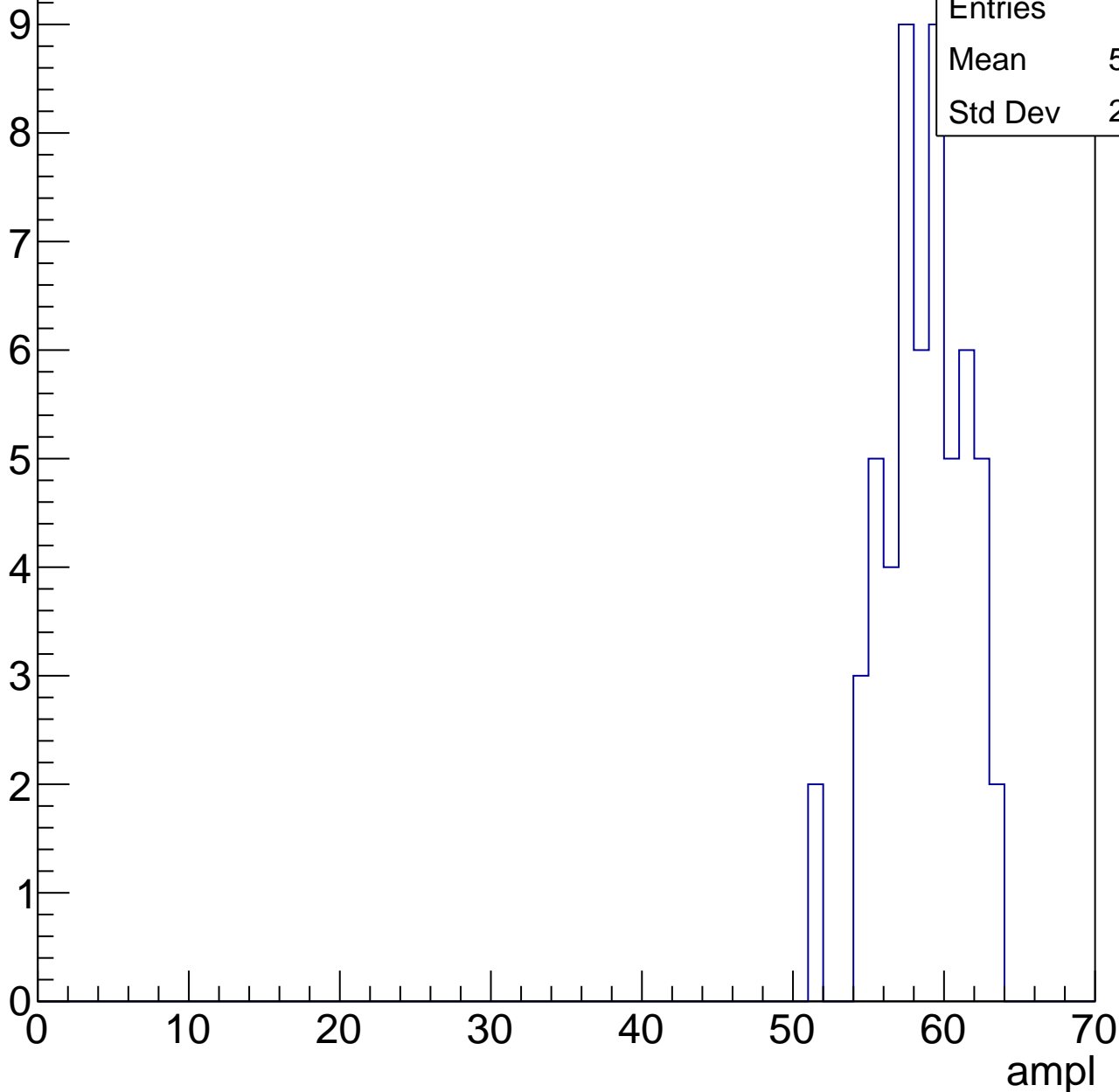


B0L002S, U9-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	58.16
Std Dev	2.763

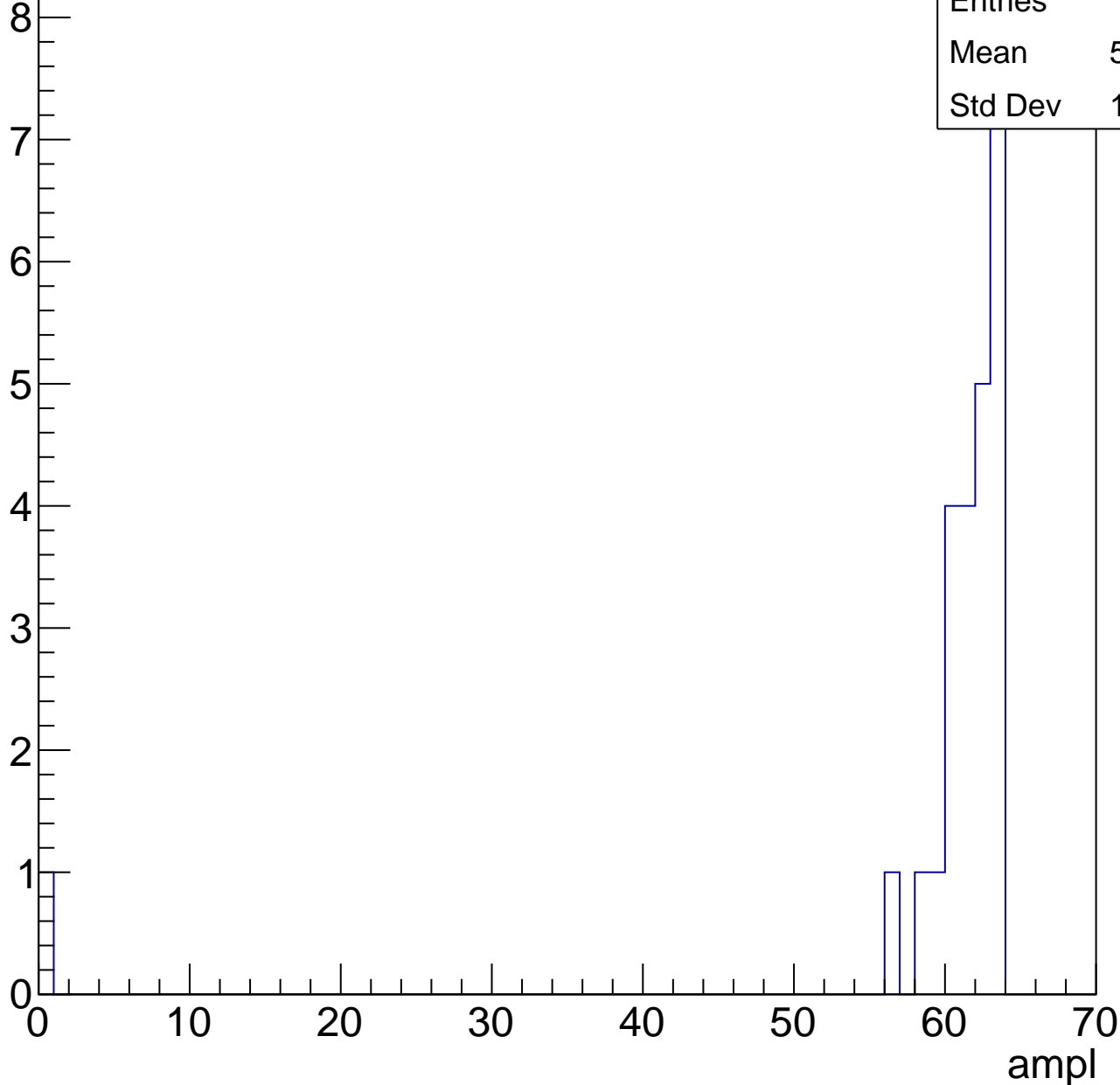


B0L002S, U9-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.84
Std Dev	12.14



B0L002S, U9-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B0L002S, U9-ch102, adc0

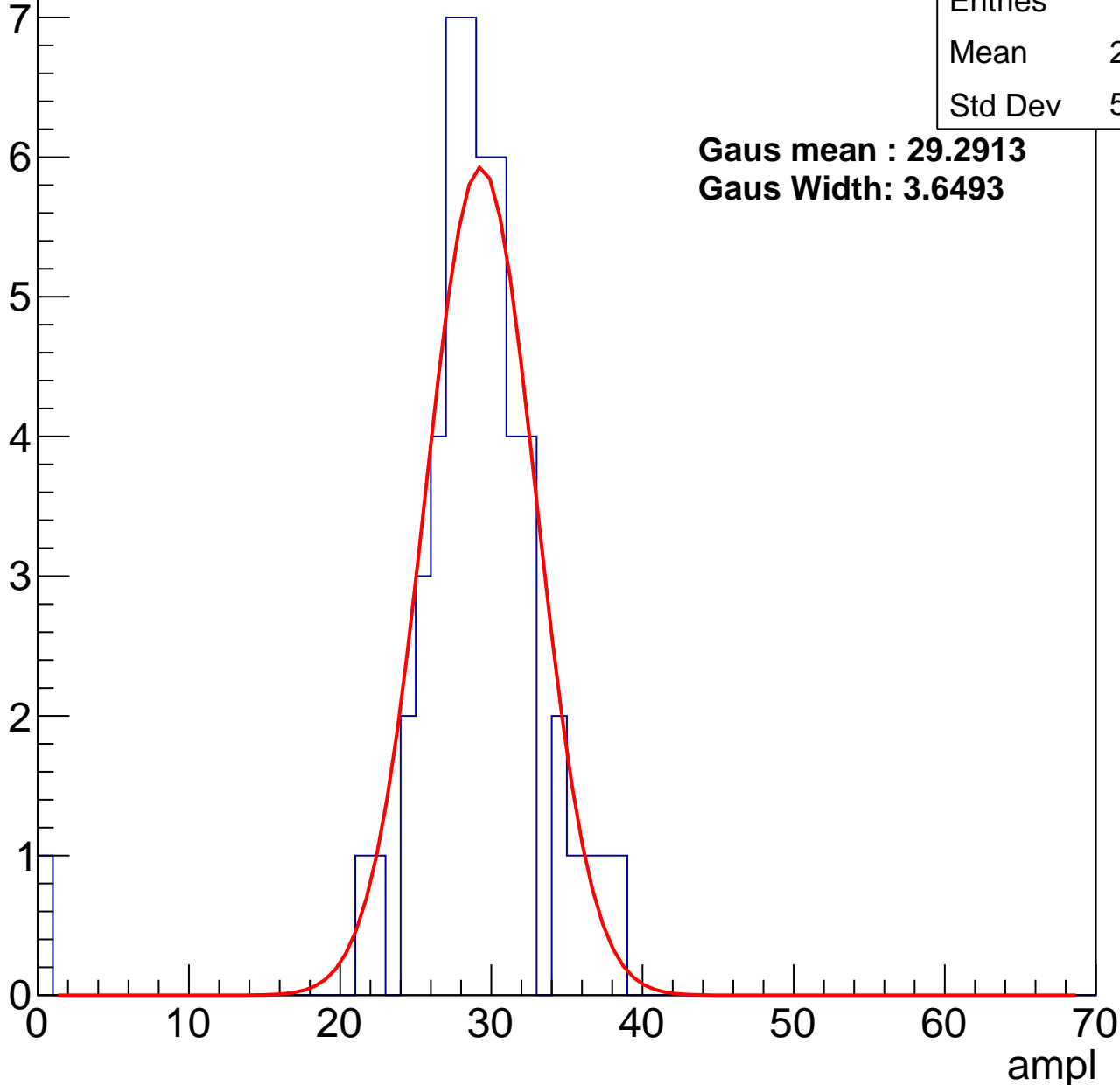
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	28.37
Std Dev	5.262

Gaus mean : 29.2913

Gaus Width: 3.6493



B0L002S, U9-ch102, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	35.2
Std Dev	3.407

Gaus mean : 35.3220

Gaus Width: 3.5887

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L002S, U9-ch102, adc2

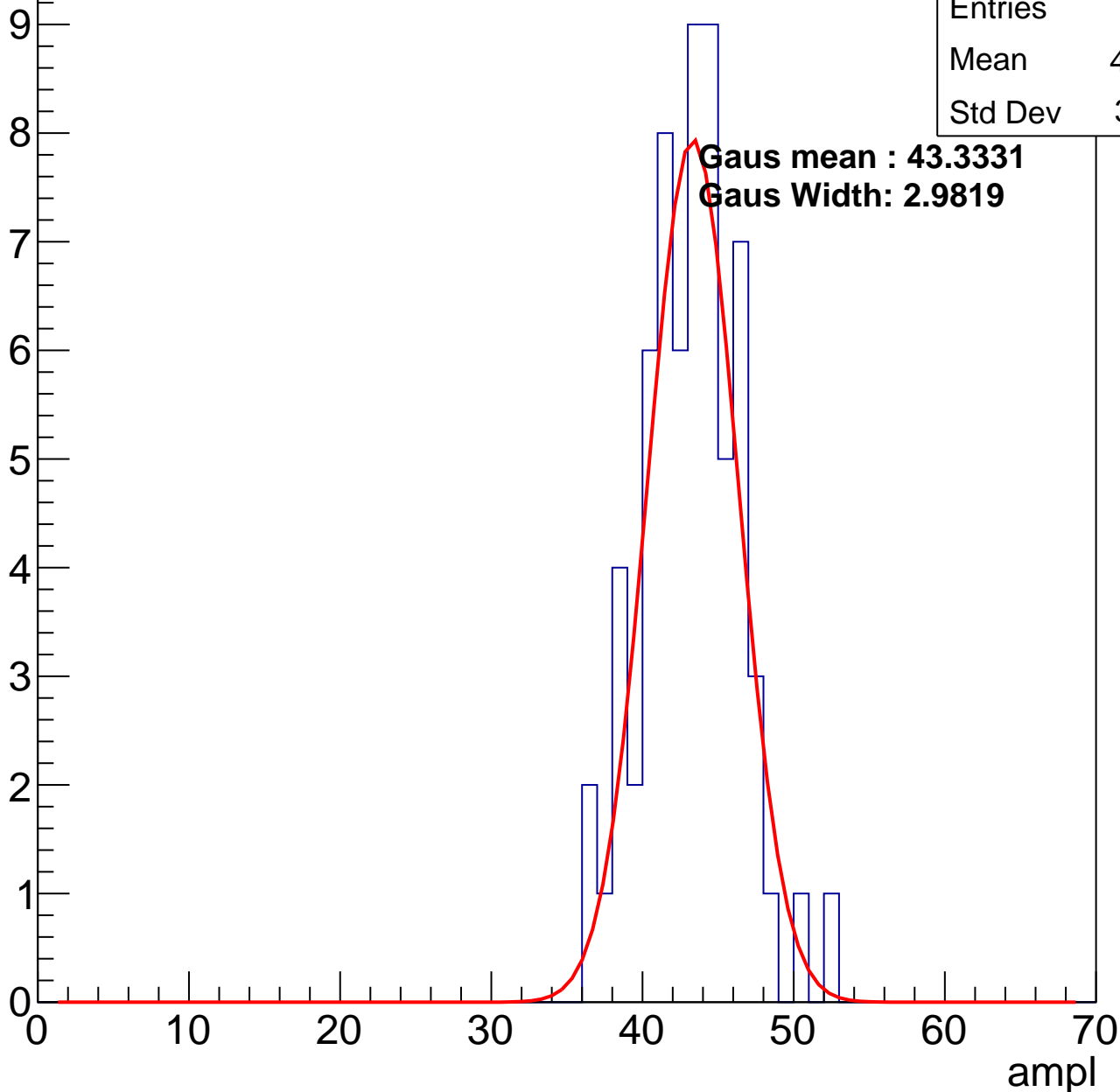
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	42.77
Std Dev	3.161

Gaus mean : 43.3331

Gaus Width: 2.9819

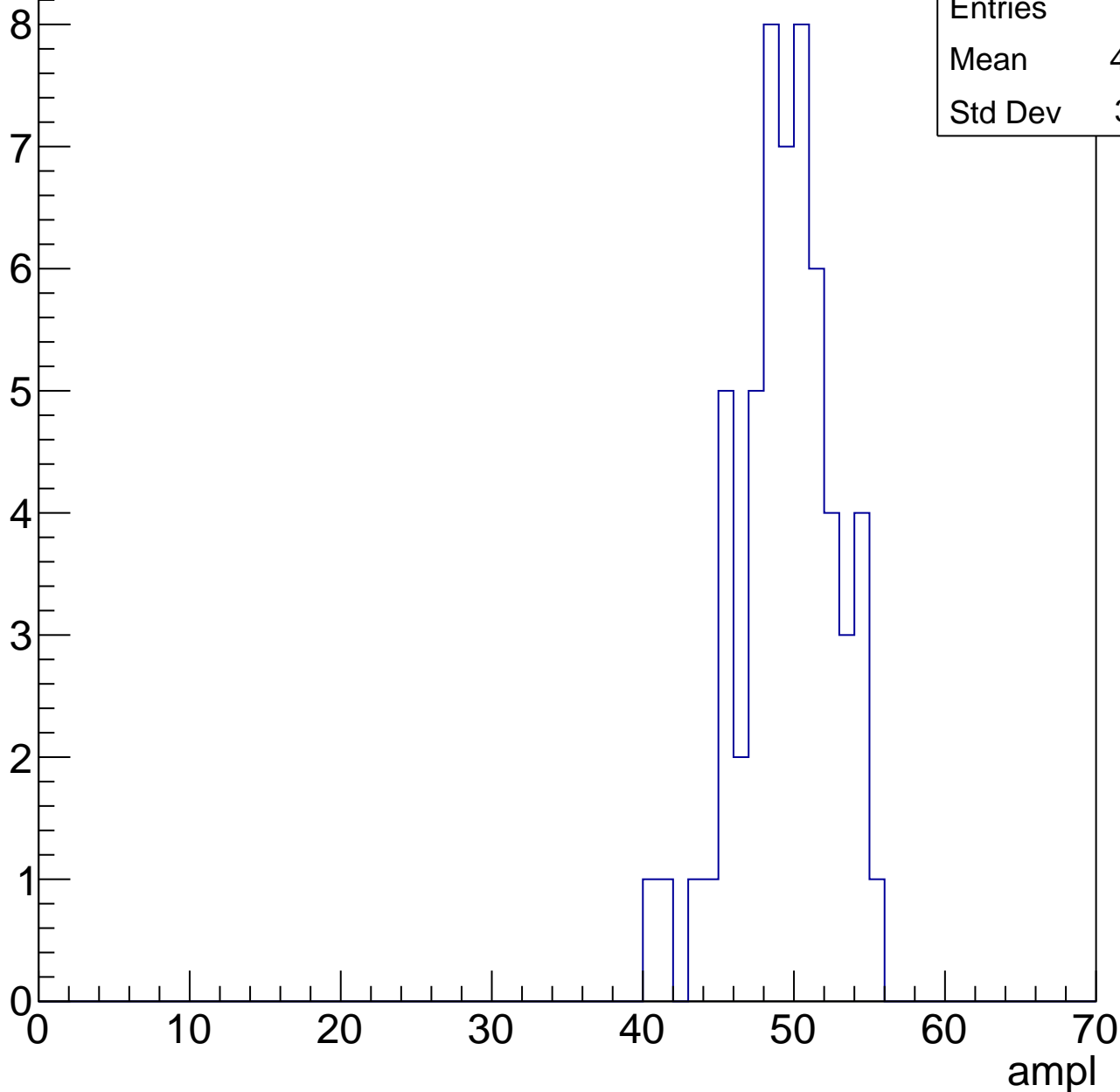


B0L002S, U9-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

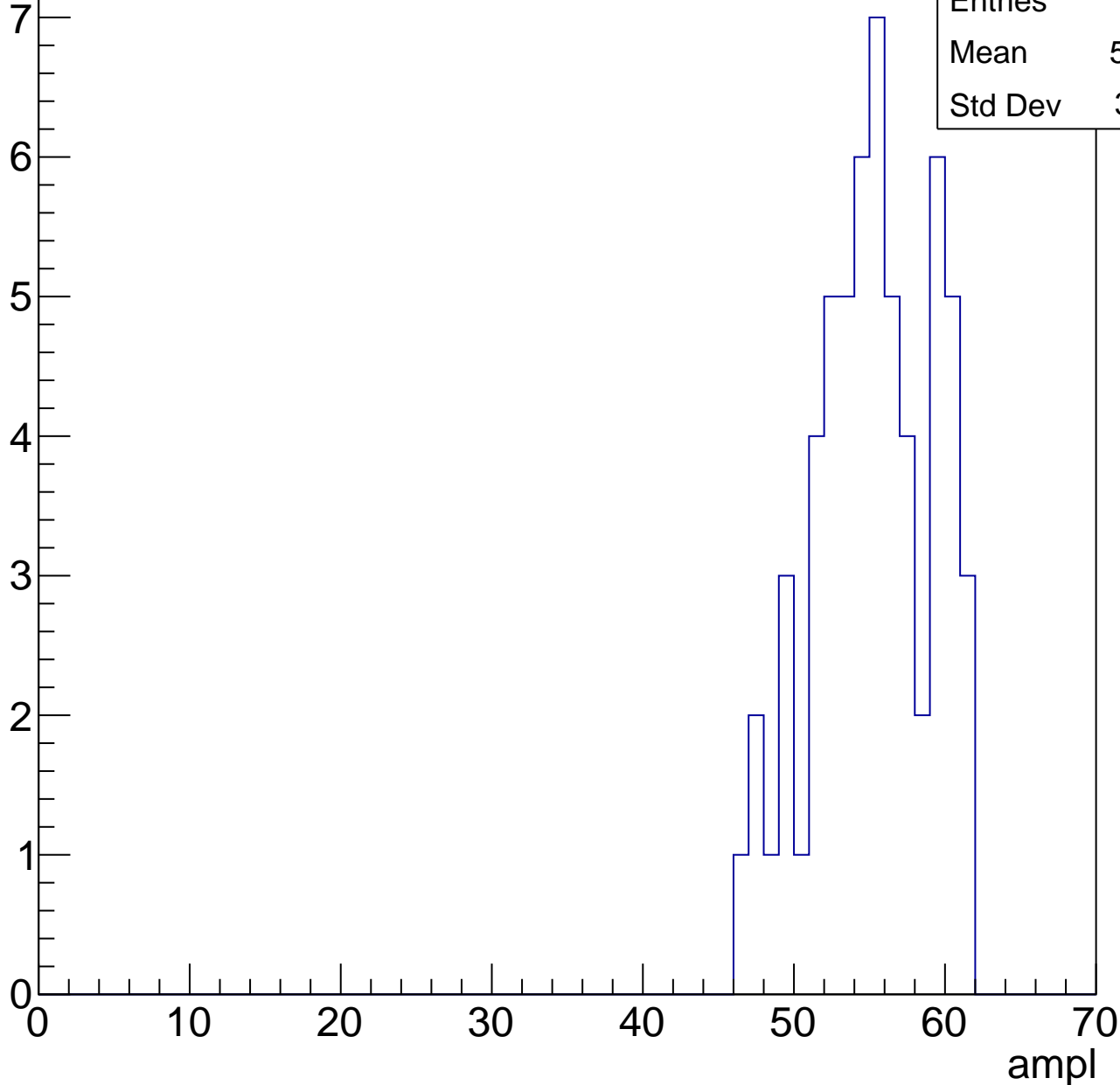
Entries	57
Mean	48.96
Std Dev	3.201



B0L002S, U9-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

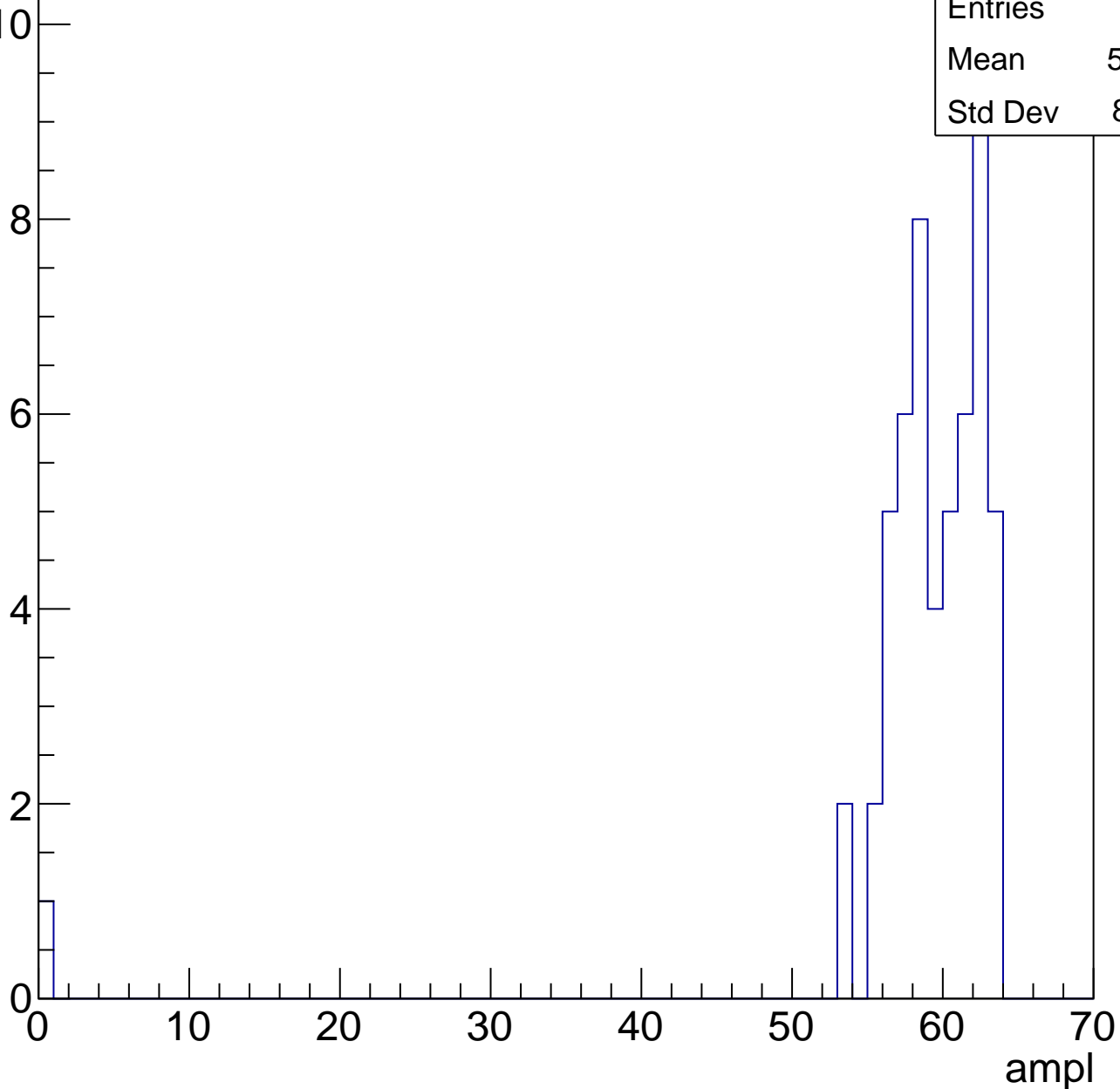


B0L002S, U9-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.13
Std Dev	8.411

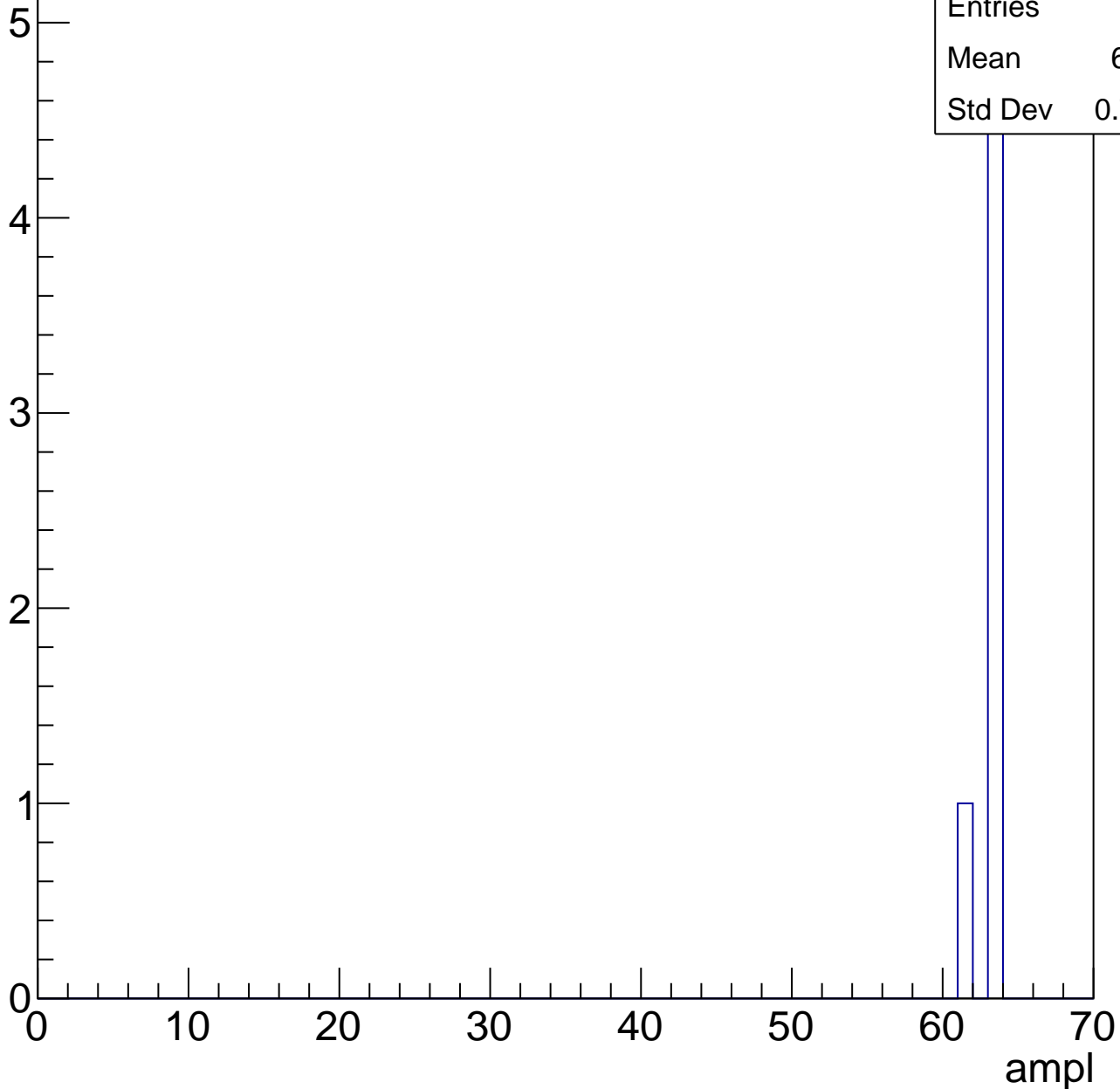


B0L002S, U9-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	6
Mean	62.67
Std Dev	0.7454



B0L002S, U9-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch103, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	27.99
Std Dev	7.319

Gaus mean : 30.6920

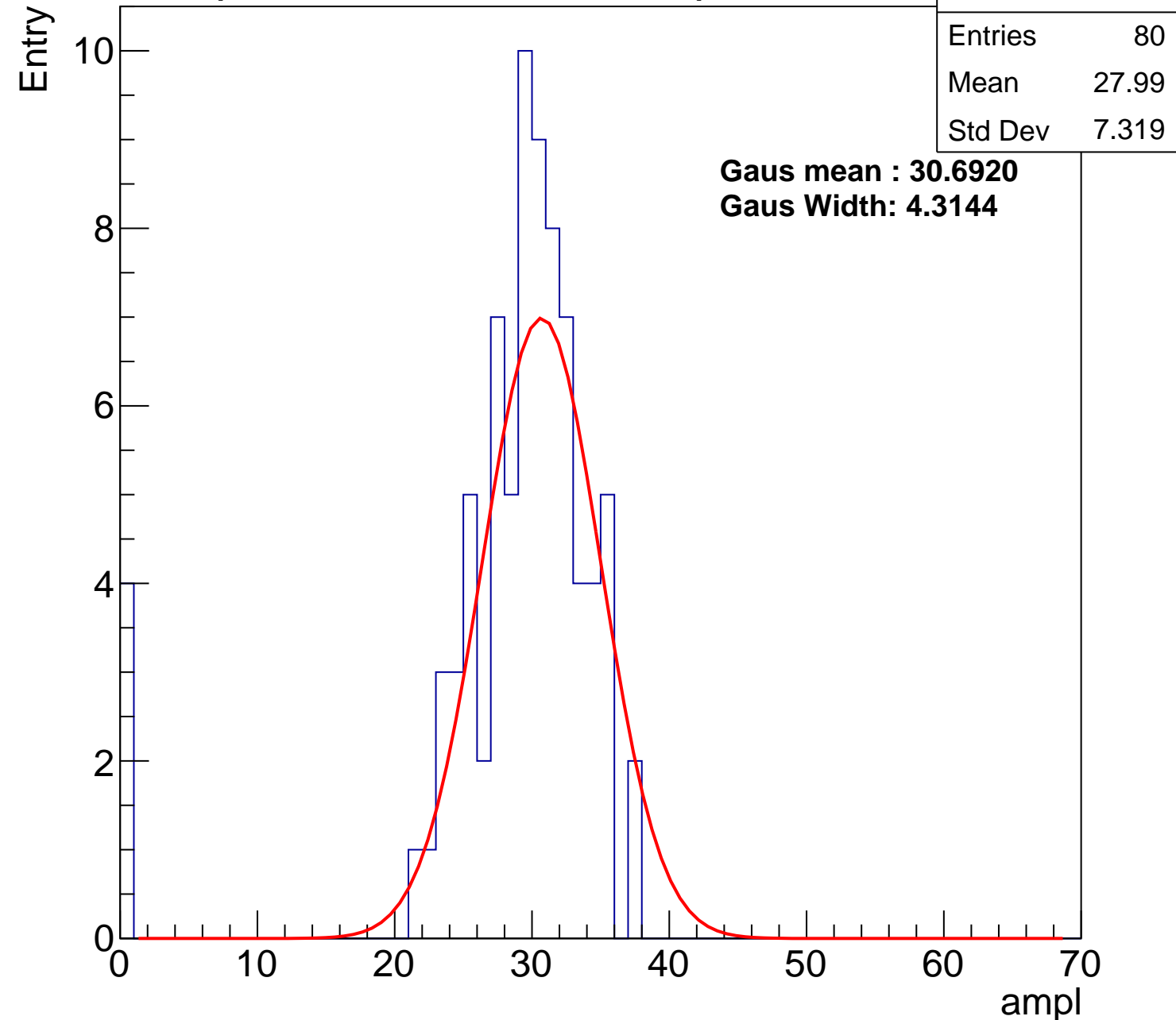
Gaus Width: 4.3144

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch103, adc1

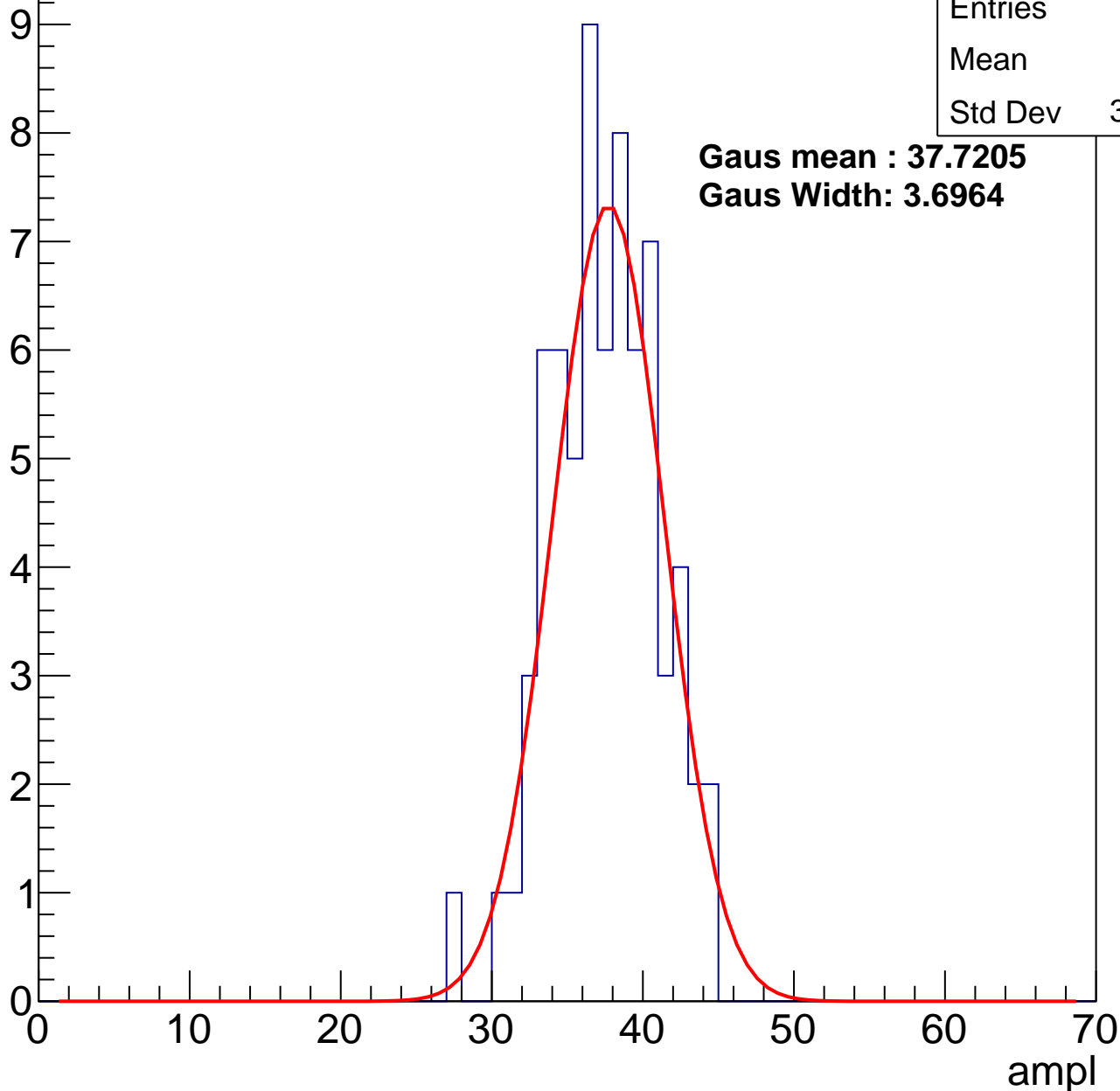
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37
Std Dev	3.489

Gaus mean : 37.7205

Gaus Width: 3.6964



B0L002S, U9-ch103, adc2

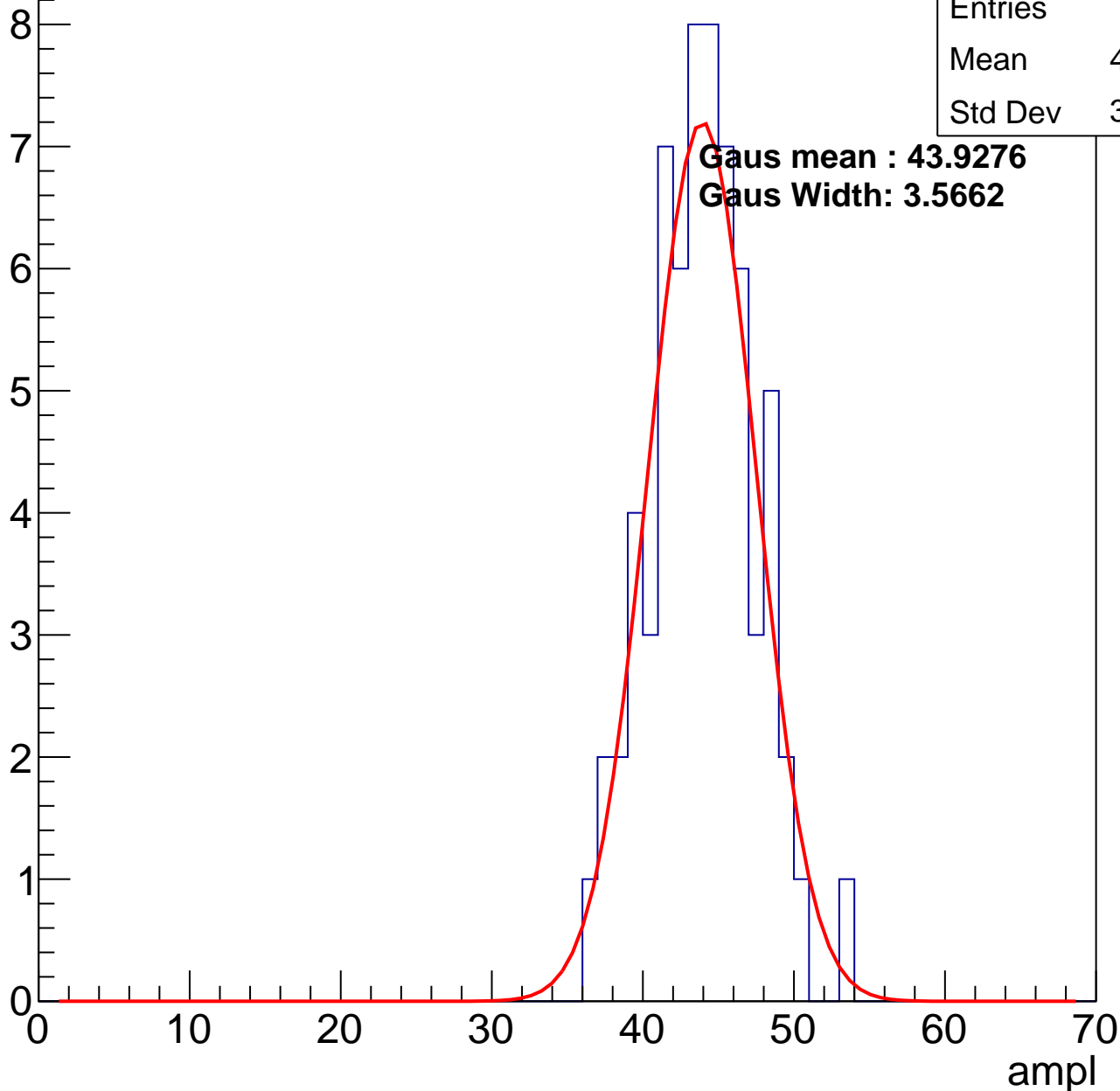
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.48
Std Dev	3.403

Gaus mean : 43.9276

Gaus Width: 3.5662

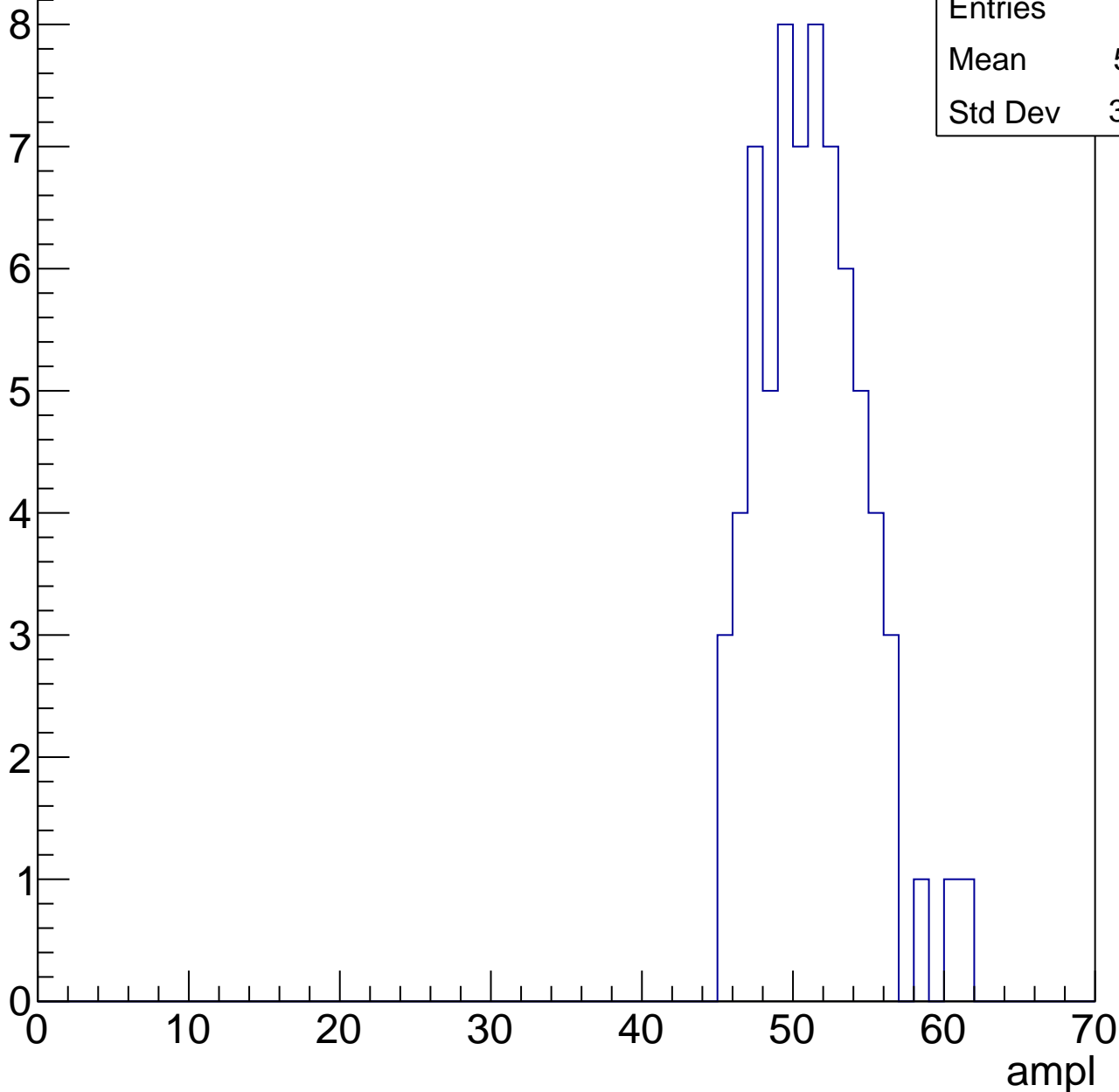


B0L002S, U9-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	50.81
Std Dev	3.478



B0L002S, U9-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	63
Mean	55.84
Std Dev	7.759

Entry

10

8

6

4

2

0

0

10

20

30

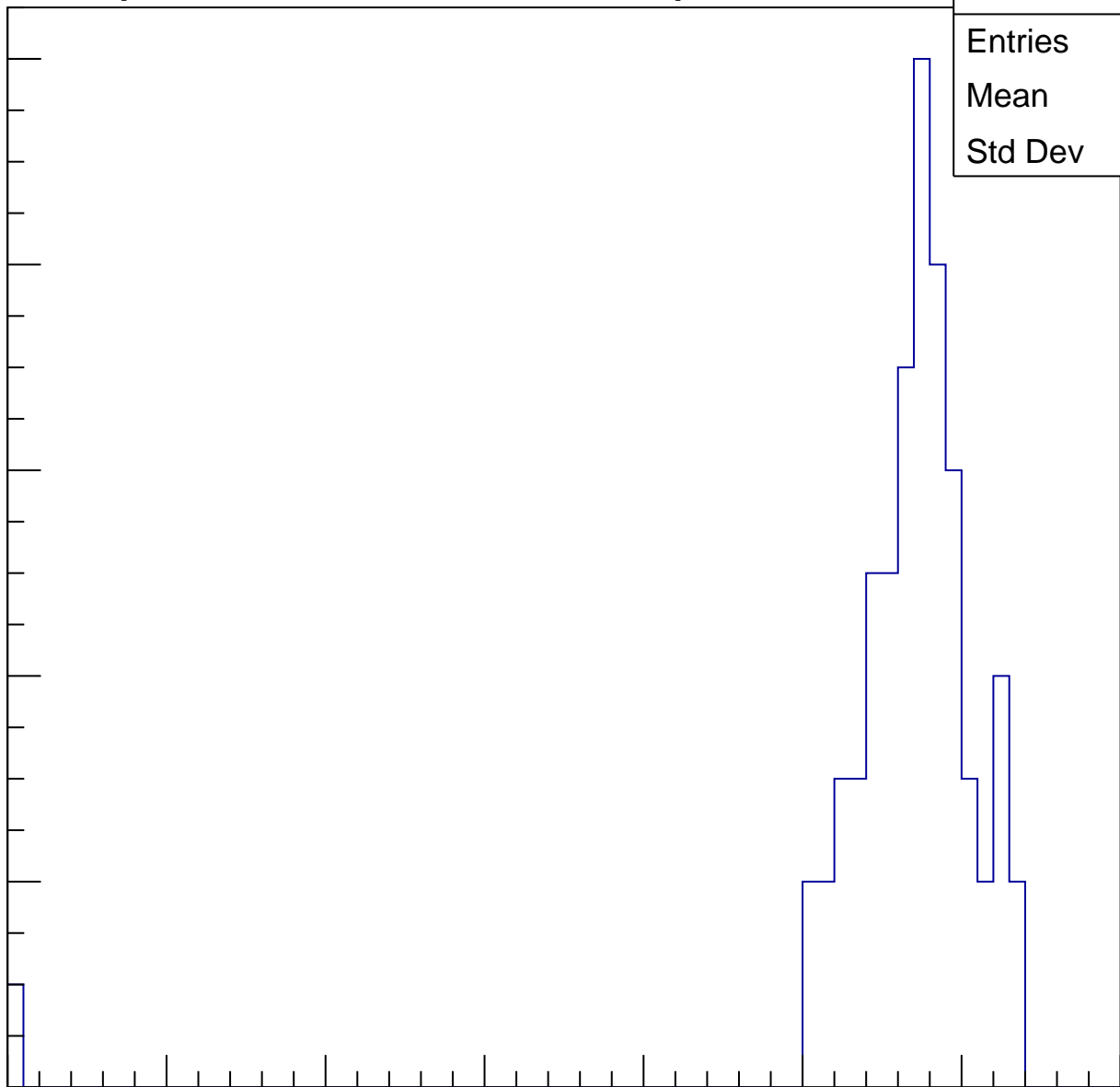
40

50

60

70

ampl

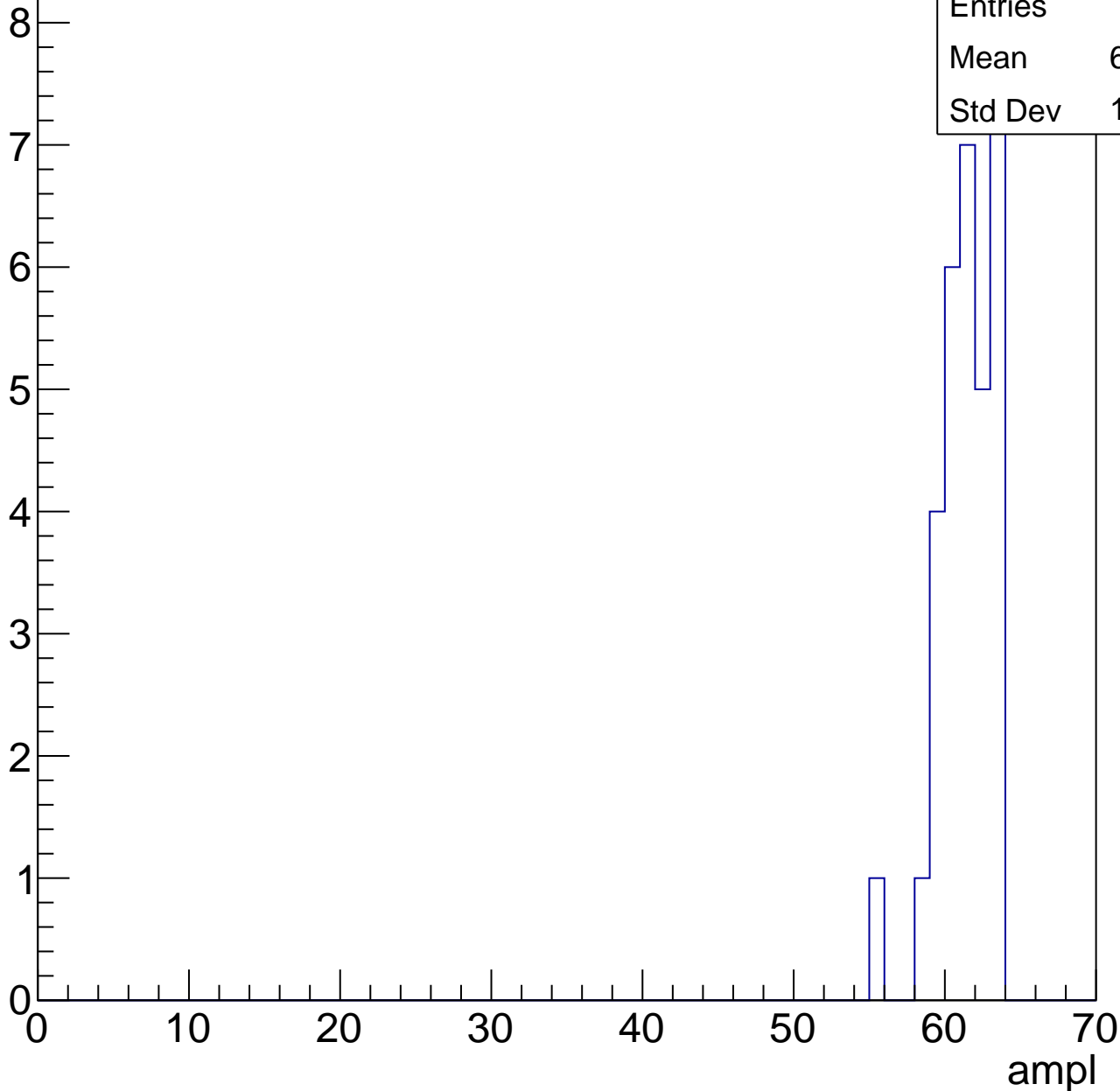


B0L002S, U9-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	60.94
Std Dev	1.802



B0L002S, U9-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch104, adc0

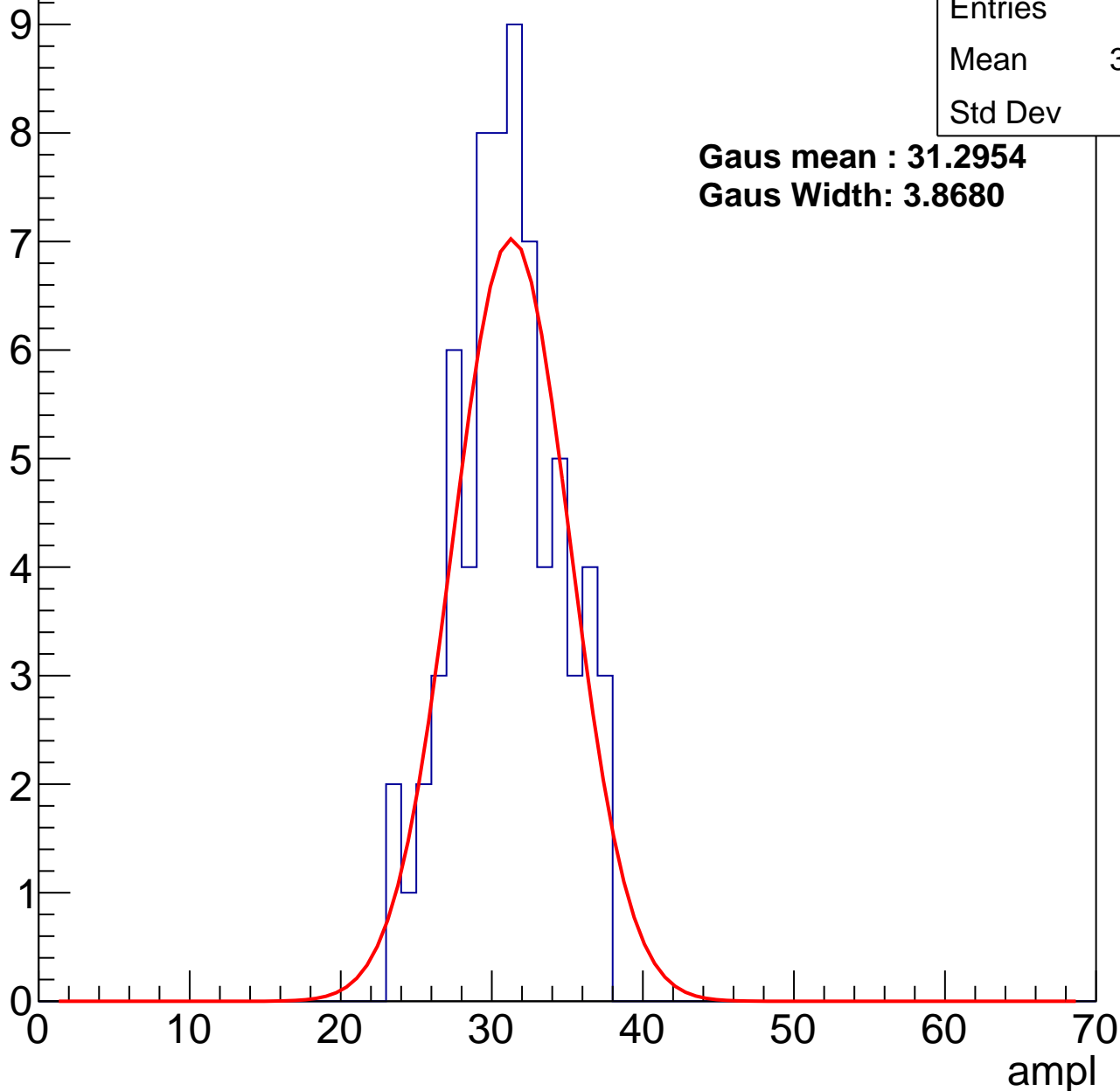
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.57
Std Dev	3.42

Gaus mean : 31.2954

Gaus Width: 3.8680



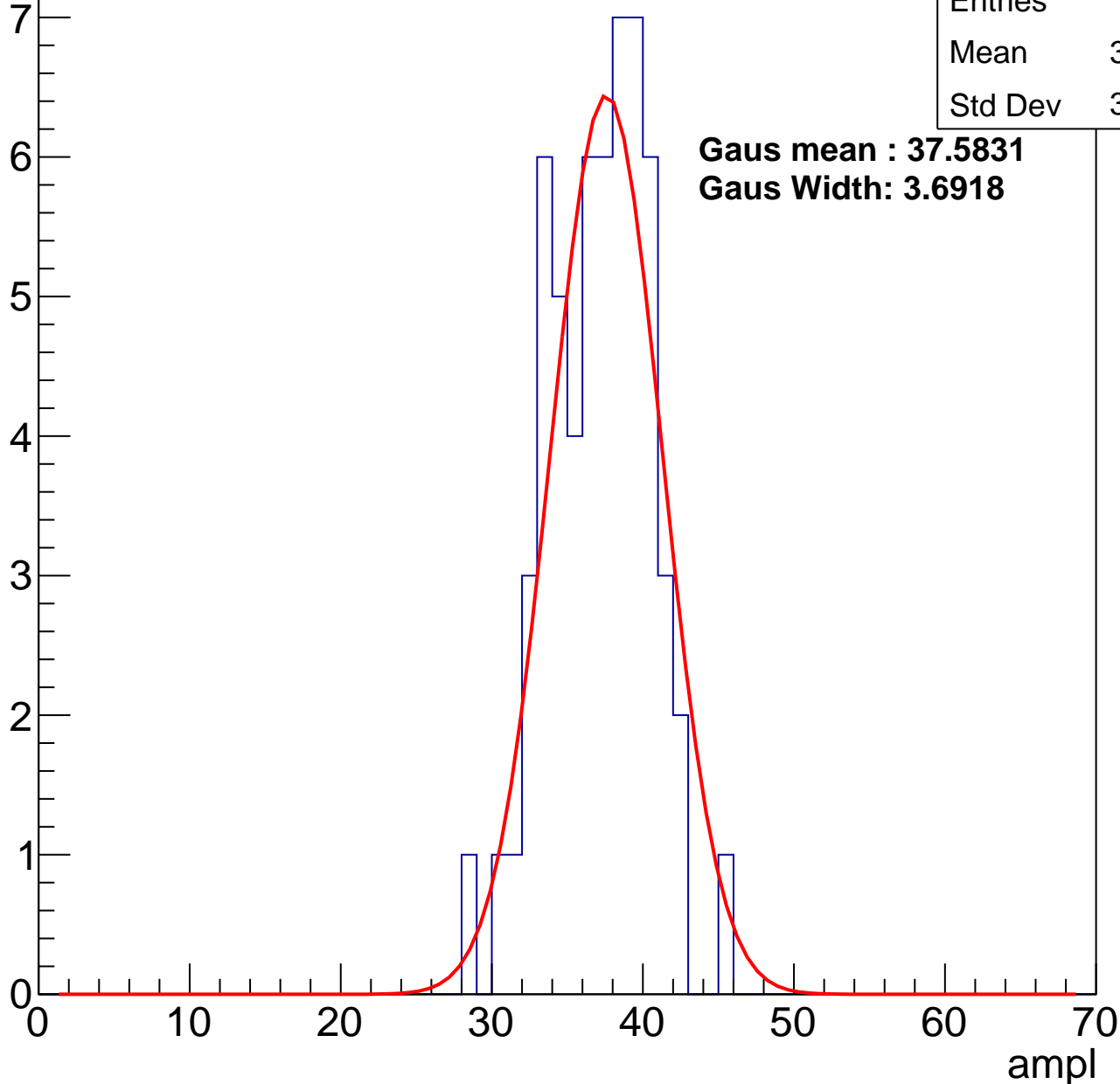
B0L002S, U9-ch104, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.64
Std Dev	3.313

Gaus mean : 37.5831
Gaus Width: 3.6918



B0L002S, U9-ch104, adc2

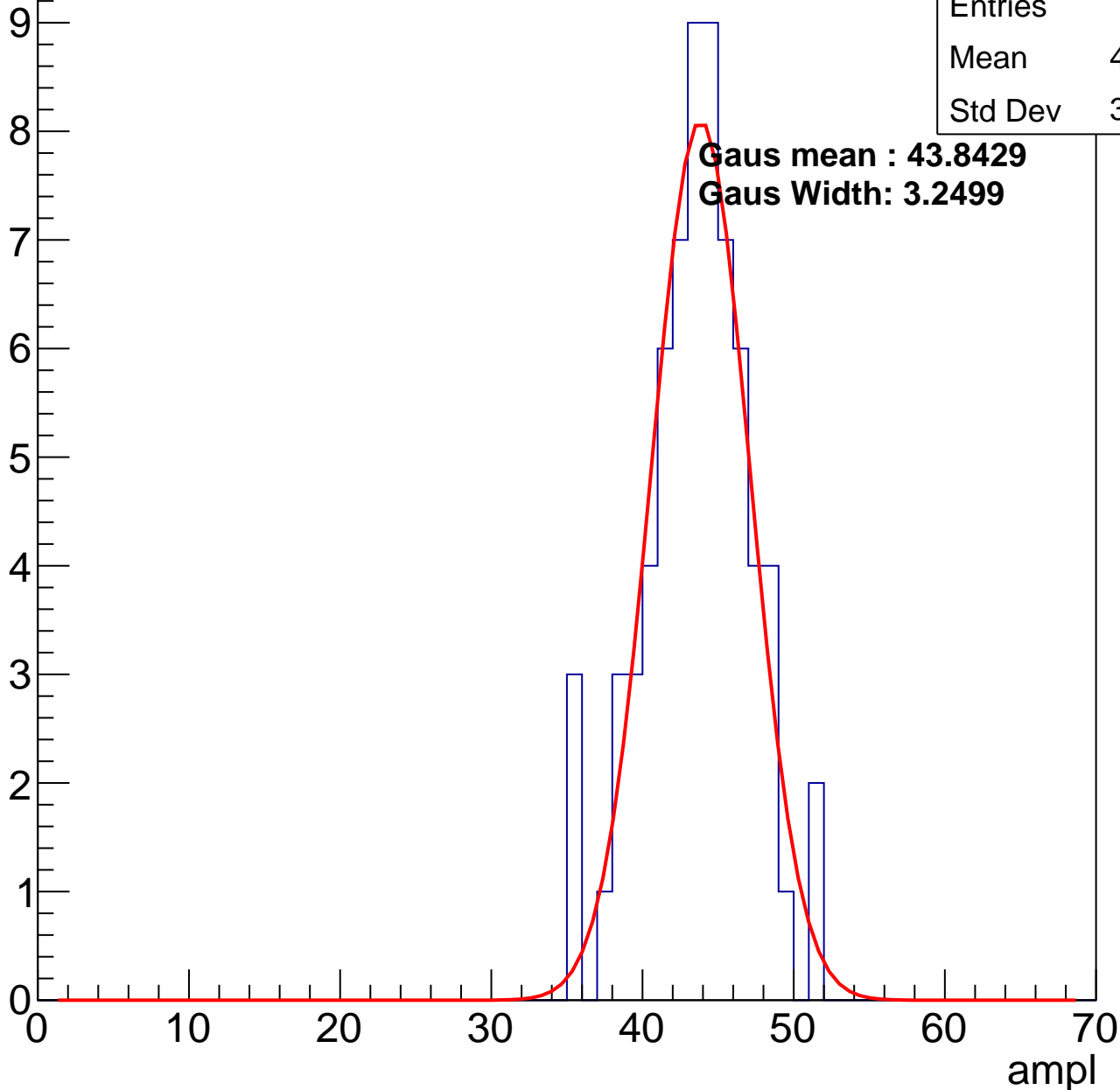
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.16
Std Dev	3.479

Gaus mean : 43.8429

Gaus Width: 3.2499

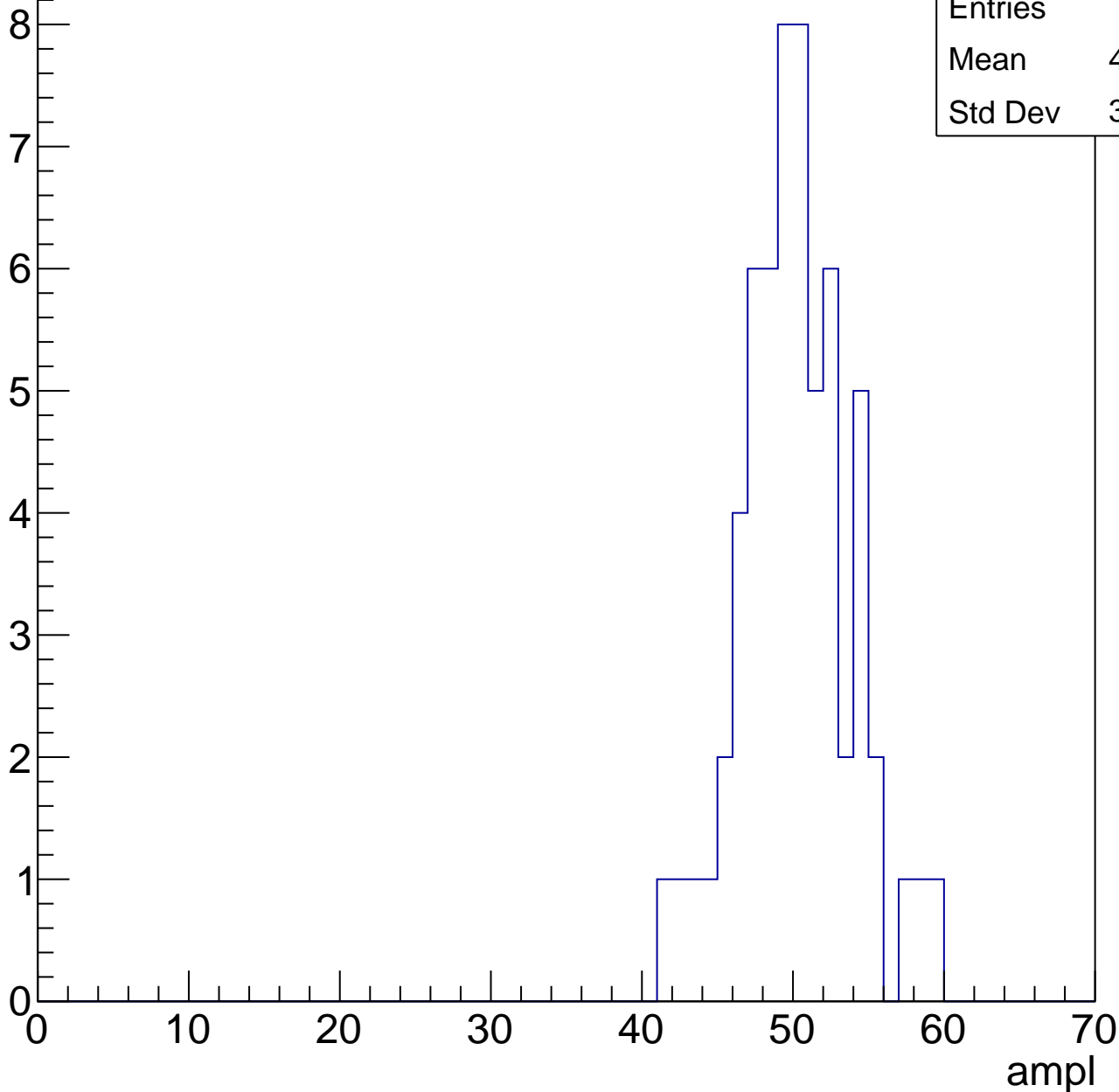


B0L002S, U9-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.72
Std Dev	3.613

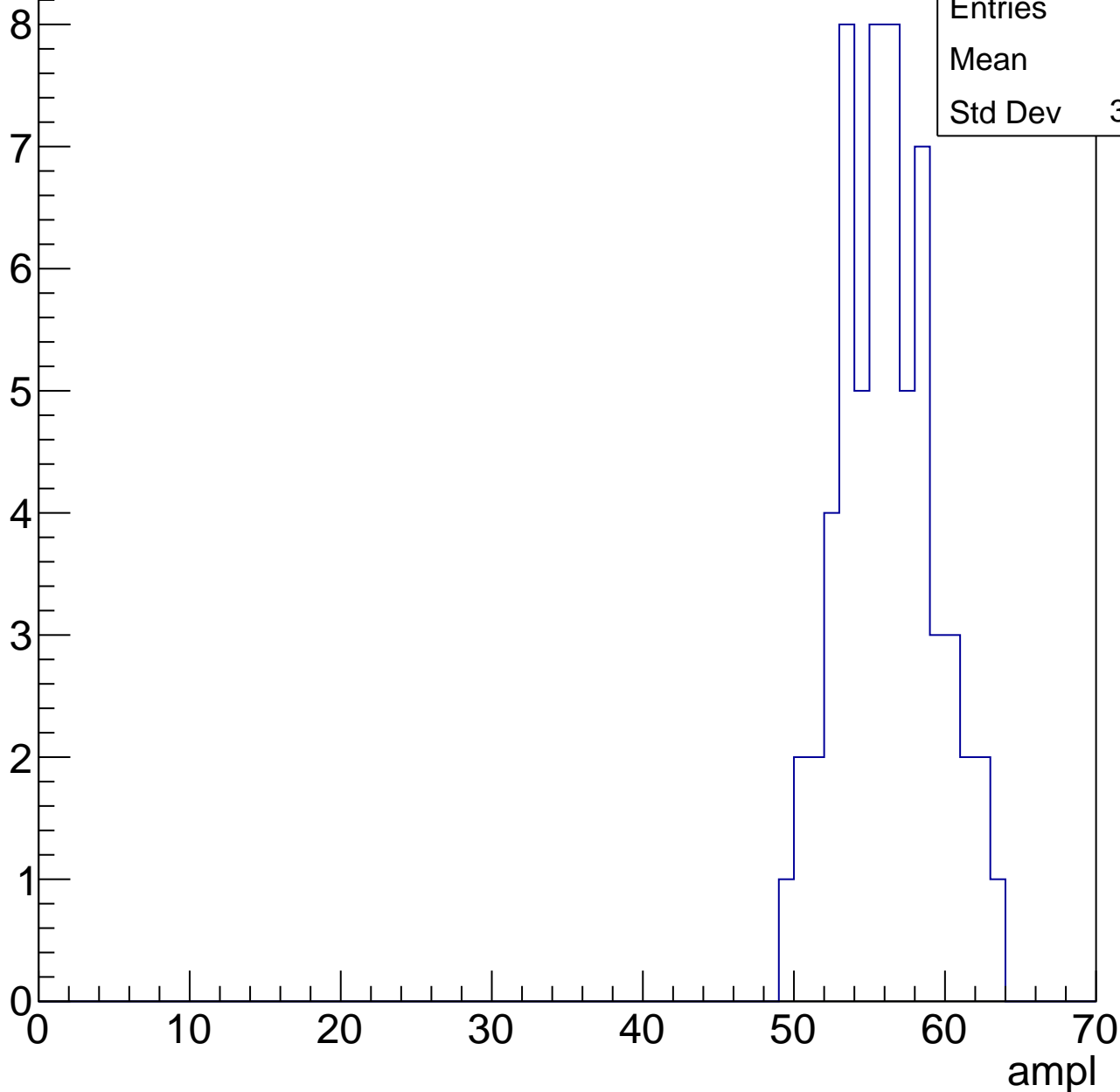


B0L002S, U9-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

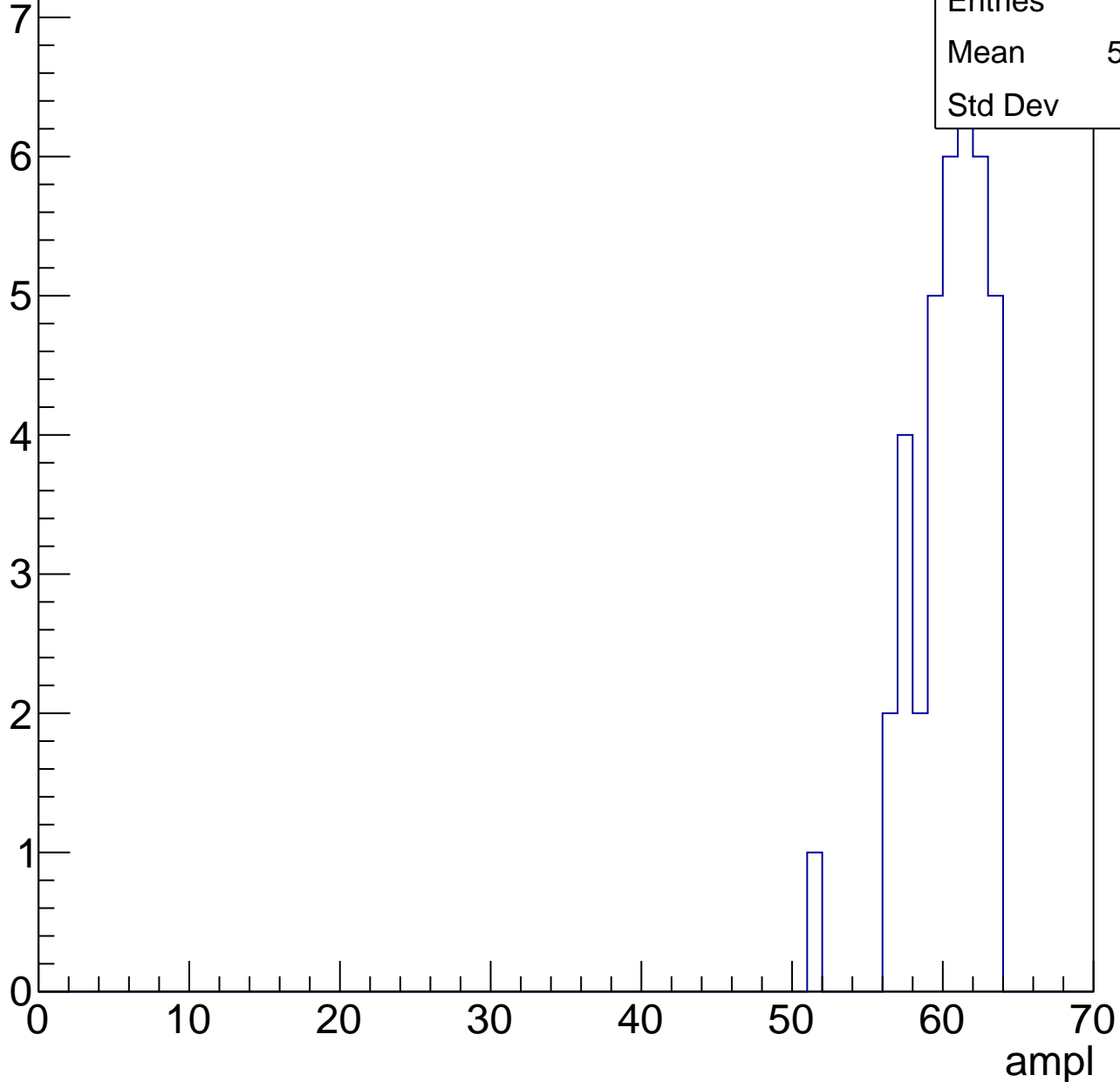
Entries	61
Mean	55.7
Std Dev	3.159



B0L002S, U9-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

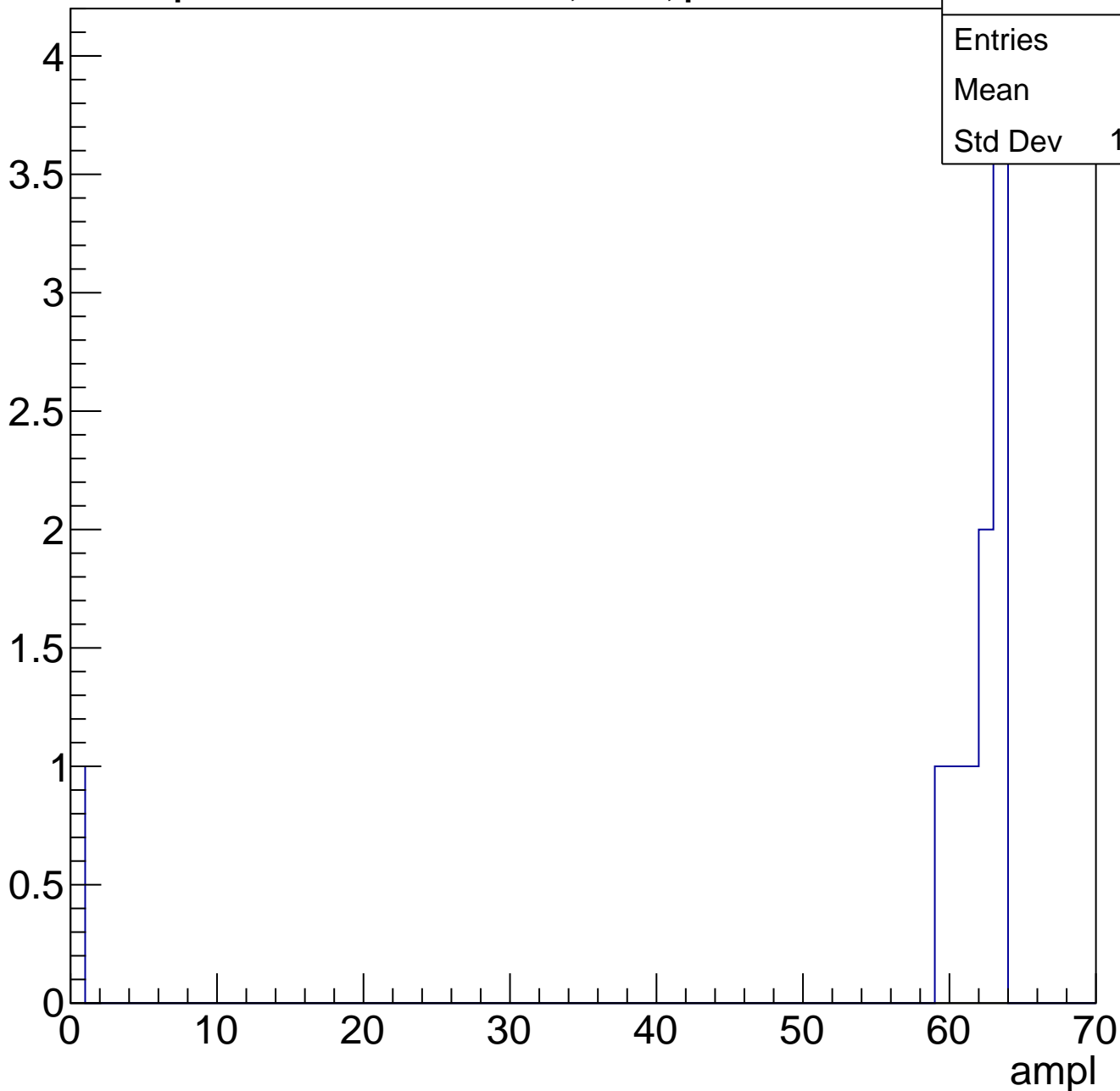
Entry



B0L002S, U9-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch105, adc0

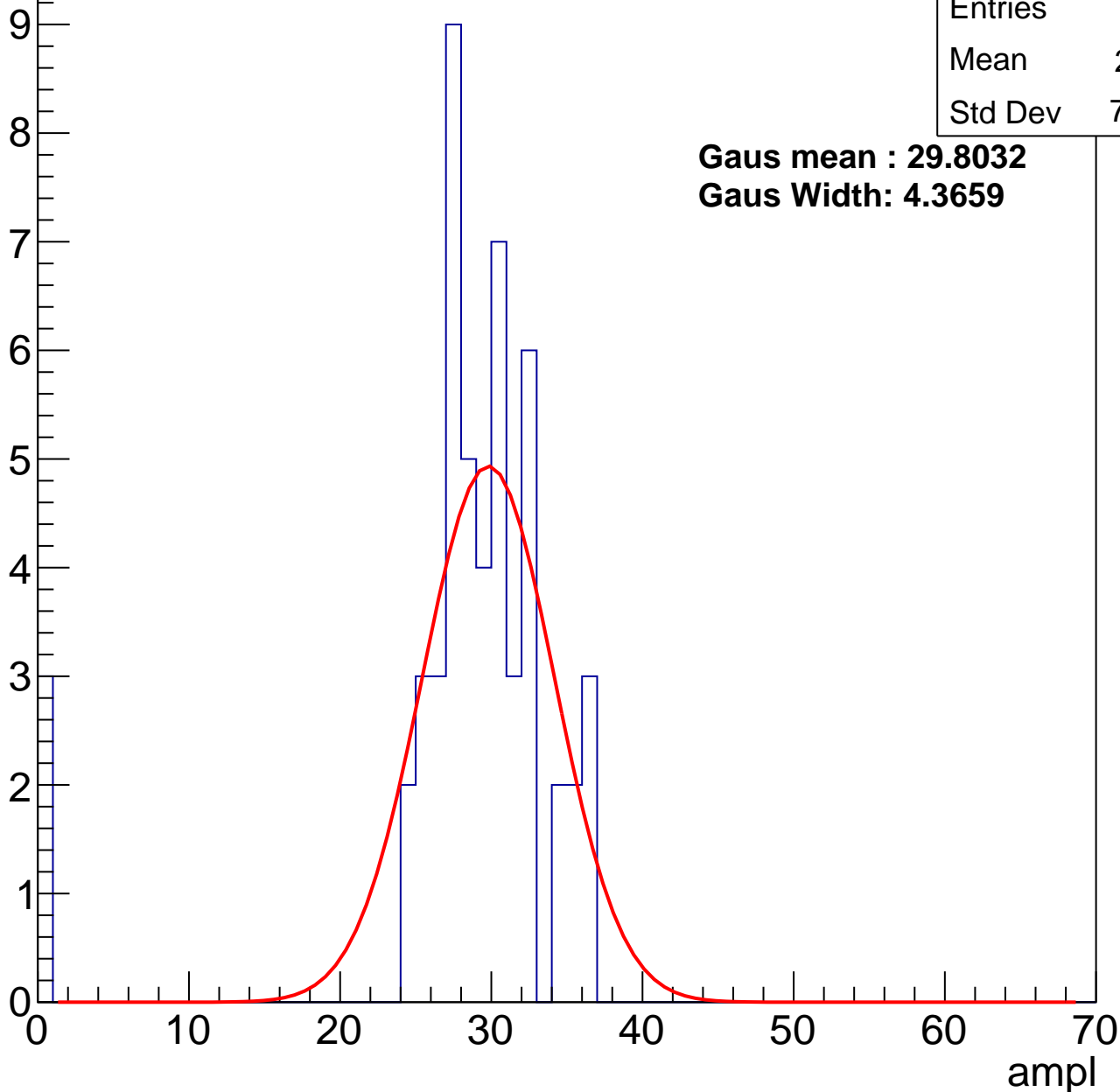
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	27.71
Std Dev	7.525

Gaus mean : 29.8032

Gaus Width: 4.3659



B0L002S, U9-ch105, adc1

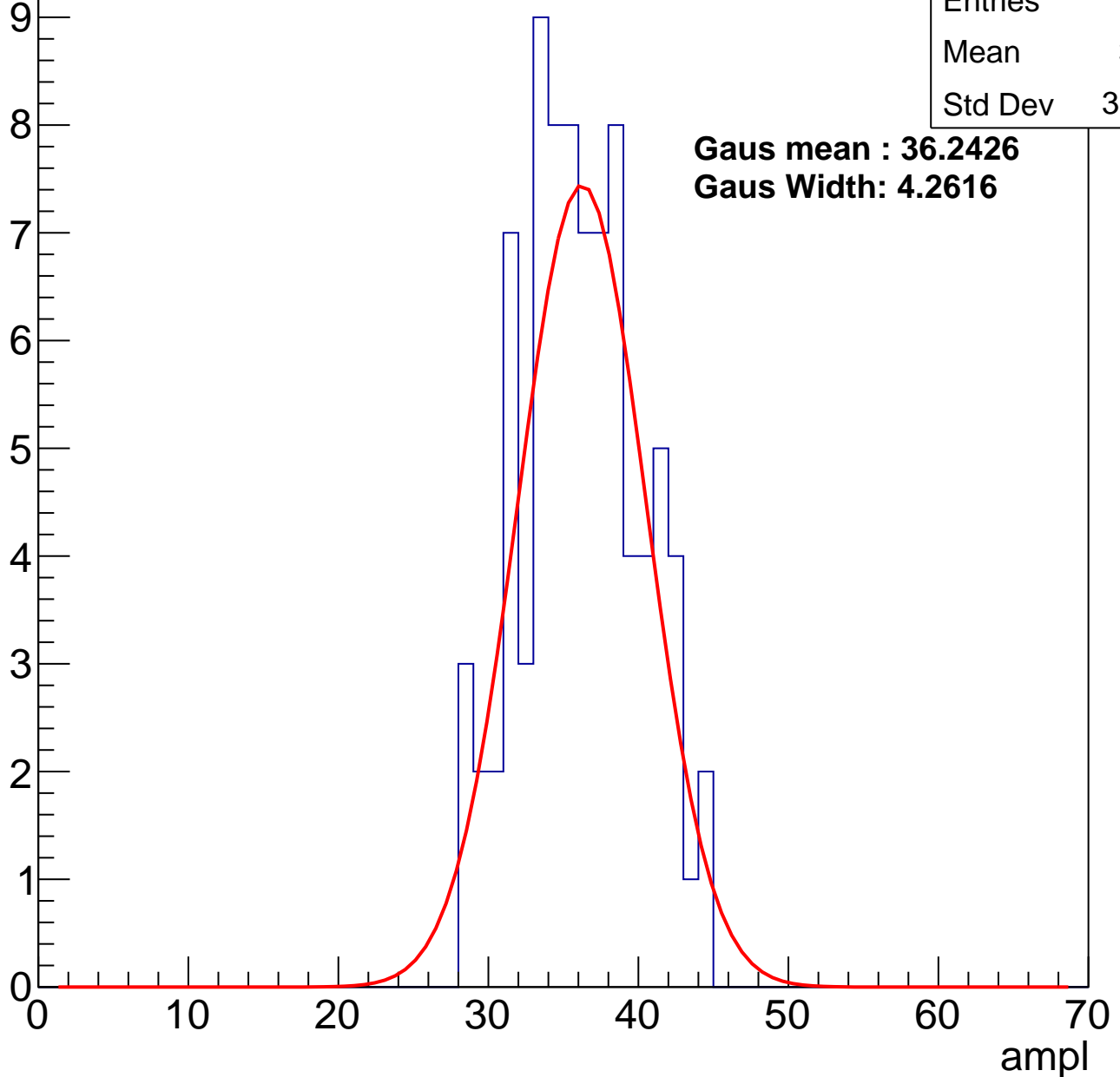
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	35.7
Std Dev	3.909

Gaus mean : 36.2426

Gaus Width: 4.2616



B0L002S, U9-ch105, adc2

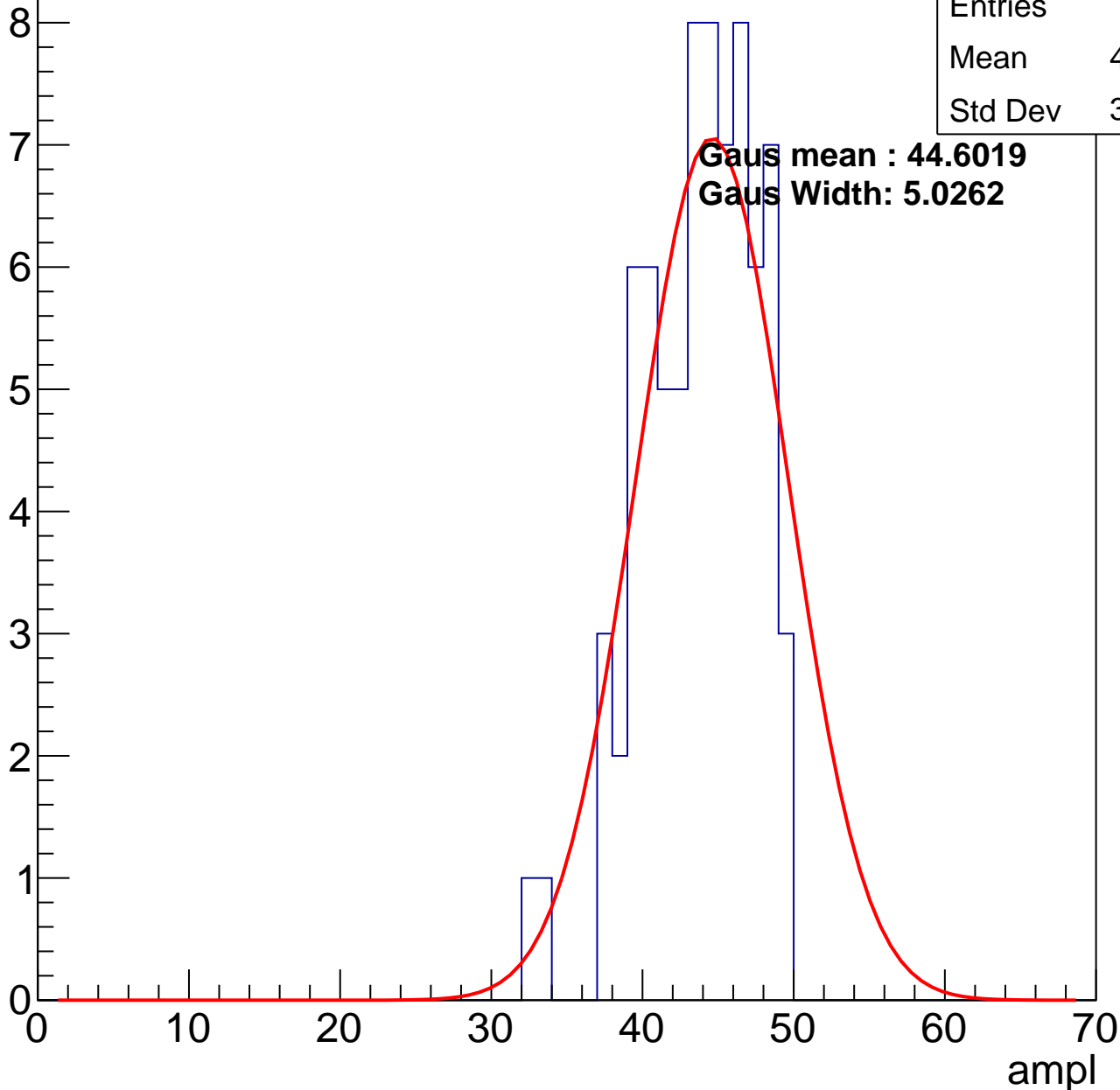
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	43.22
Std Dev	3.694

Gaus mean : 44.6019

Gaus Width: 5.0262



B0L002S, U9-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	50.81
Std Dev	3.329

Entry

10

8

6

4

2

0

0

10

20

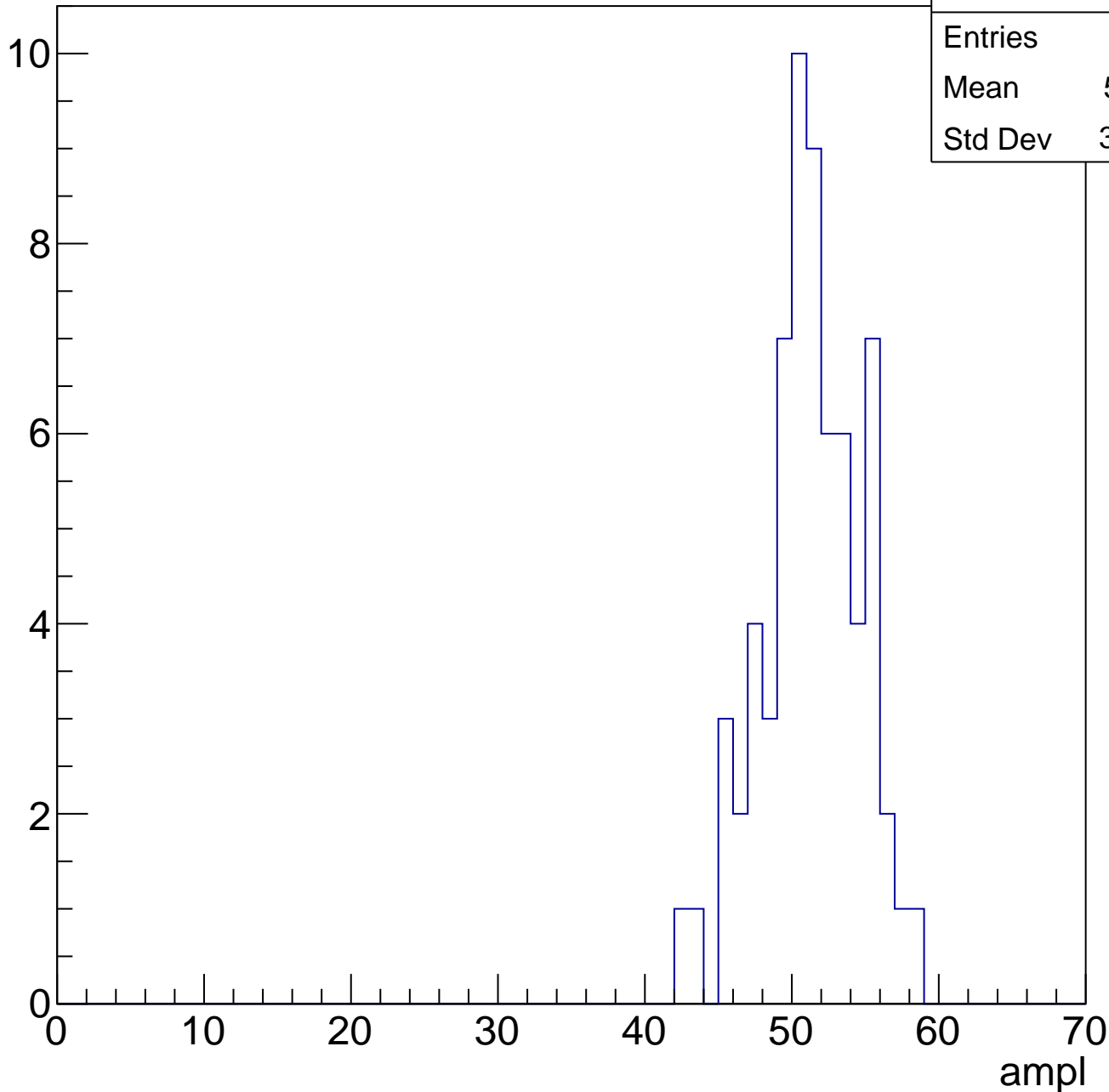
30

40

50

60

ampl



B0L002S, U9-ch105, adc4

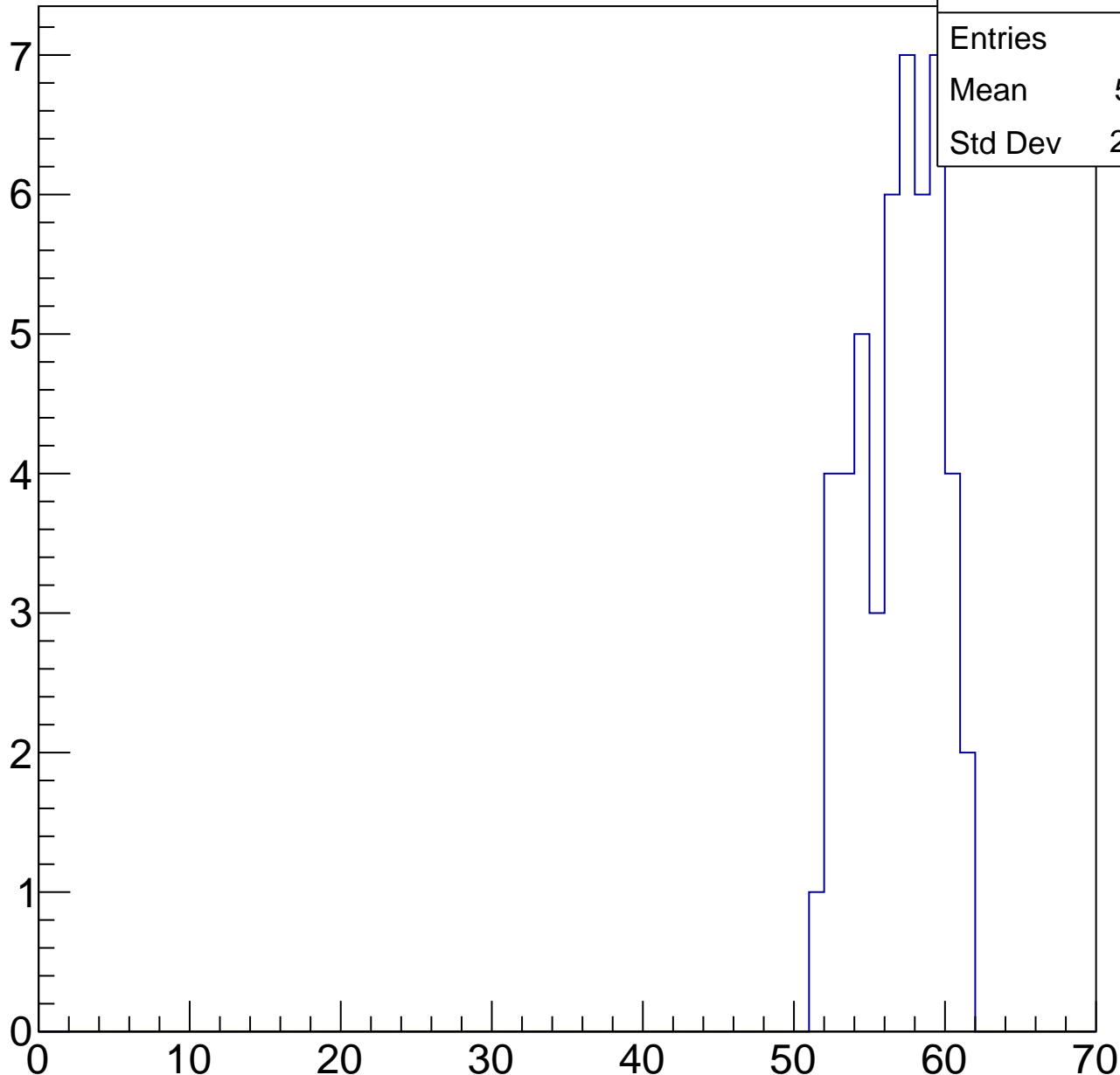
calib_packv5_042523_0143.root, FC#8, port C1

Entry

7
6
5
4
3
2
1
0

Entries	49
Mean	56.41
Std Dev	2.664

ampl

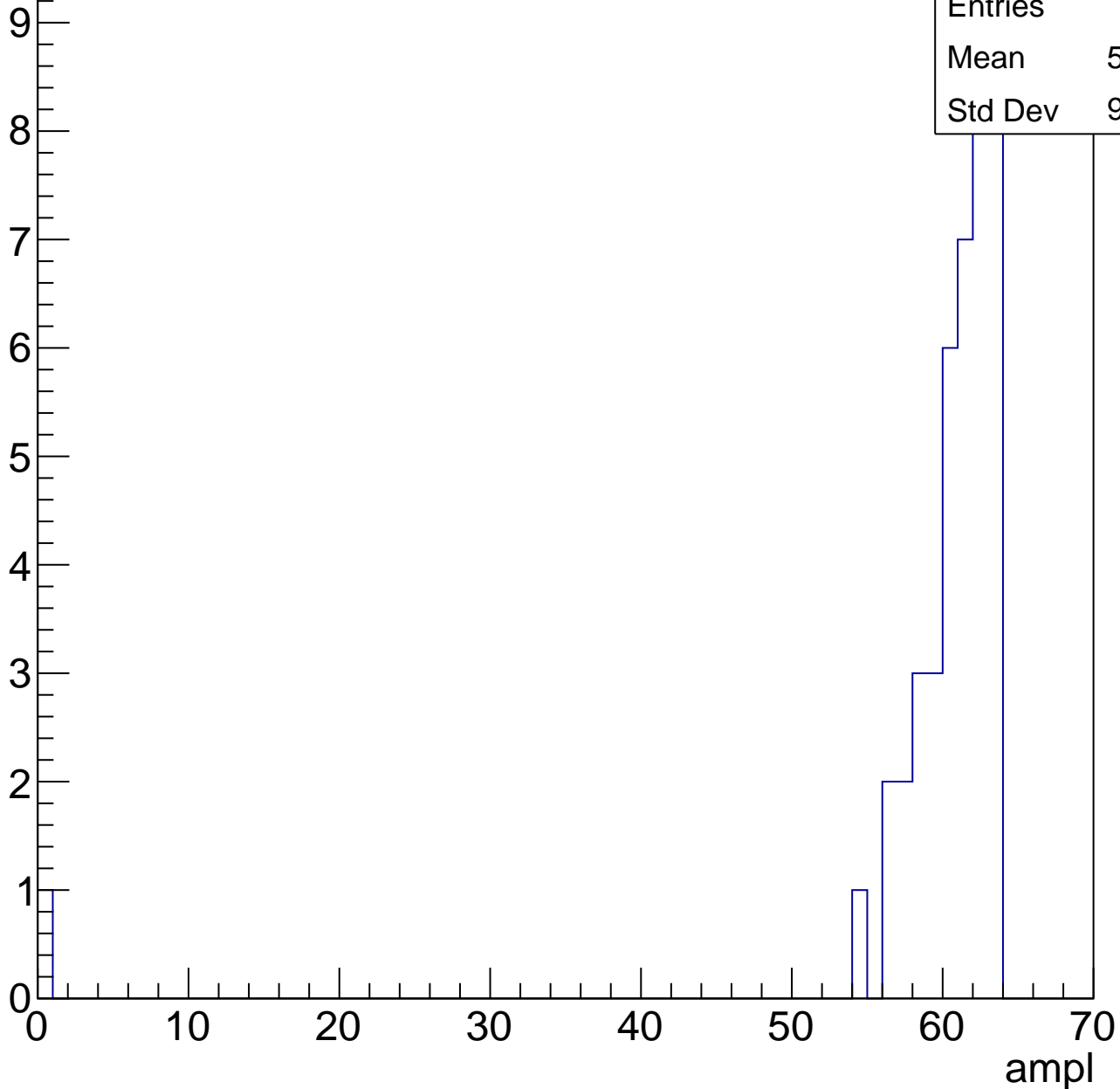


B0L002S, U9-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	59.07
Std Dev	9.493



B0L002S, U9-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B0L002S, U9-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U9-ch106, adc0

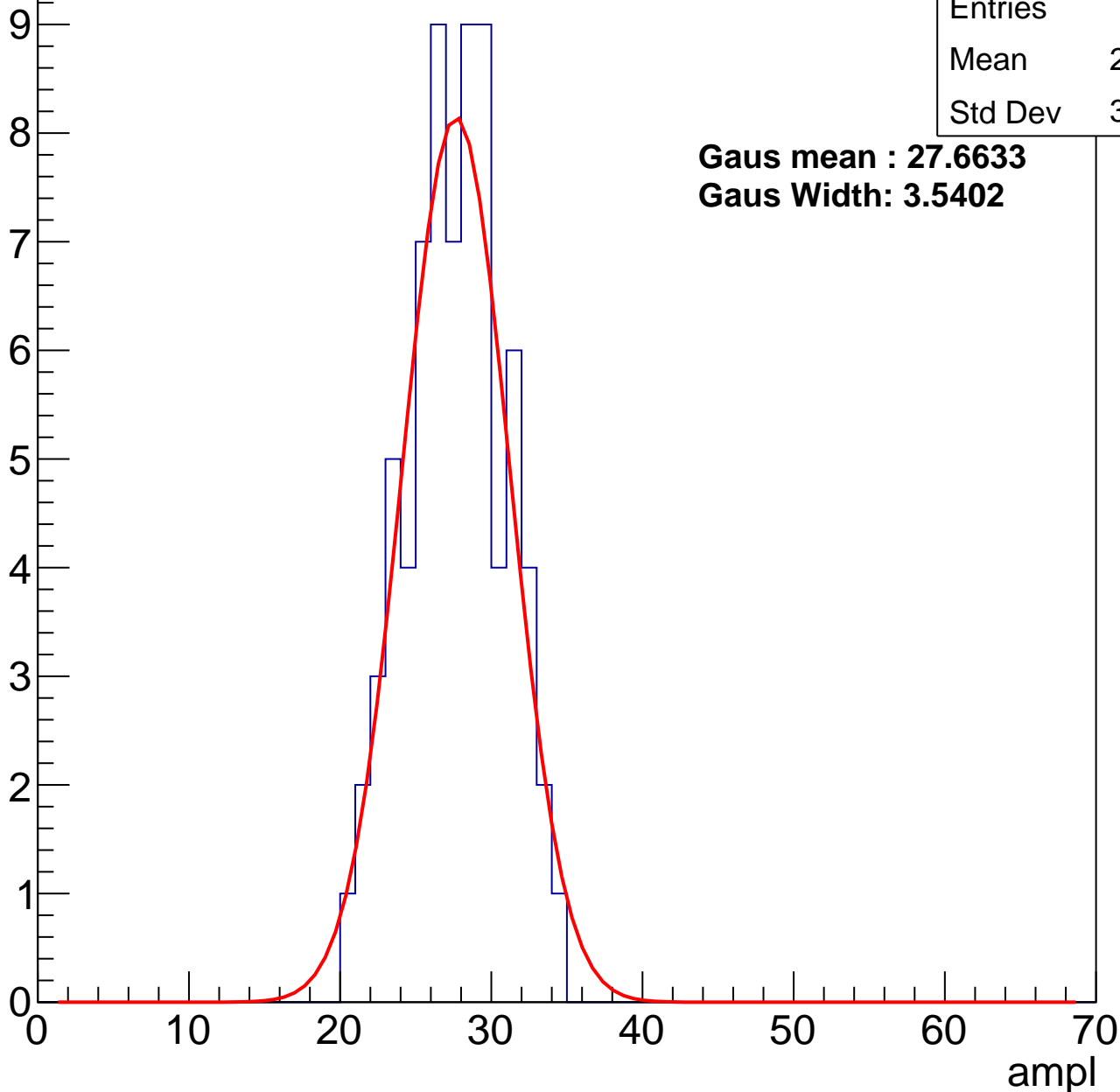
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	27.18
Std Dev	3.194

Gaus mean : 27.6633

Gaus Width: 3.5402



B0L002S, U9-ch106, adc1

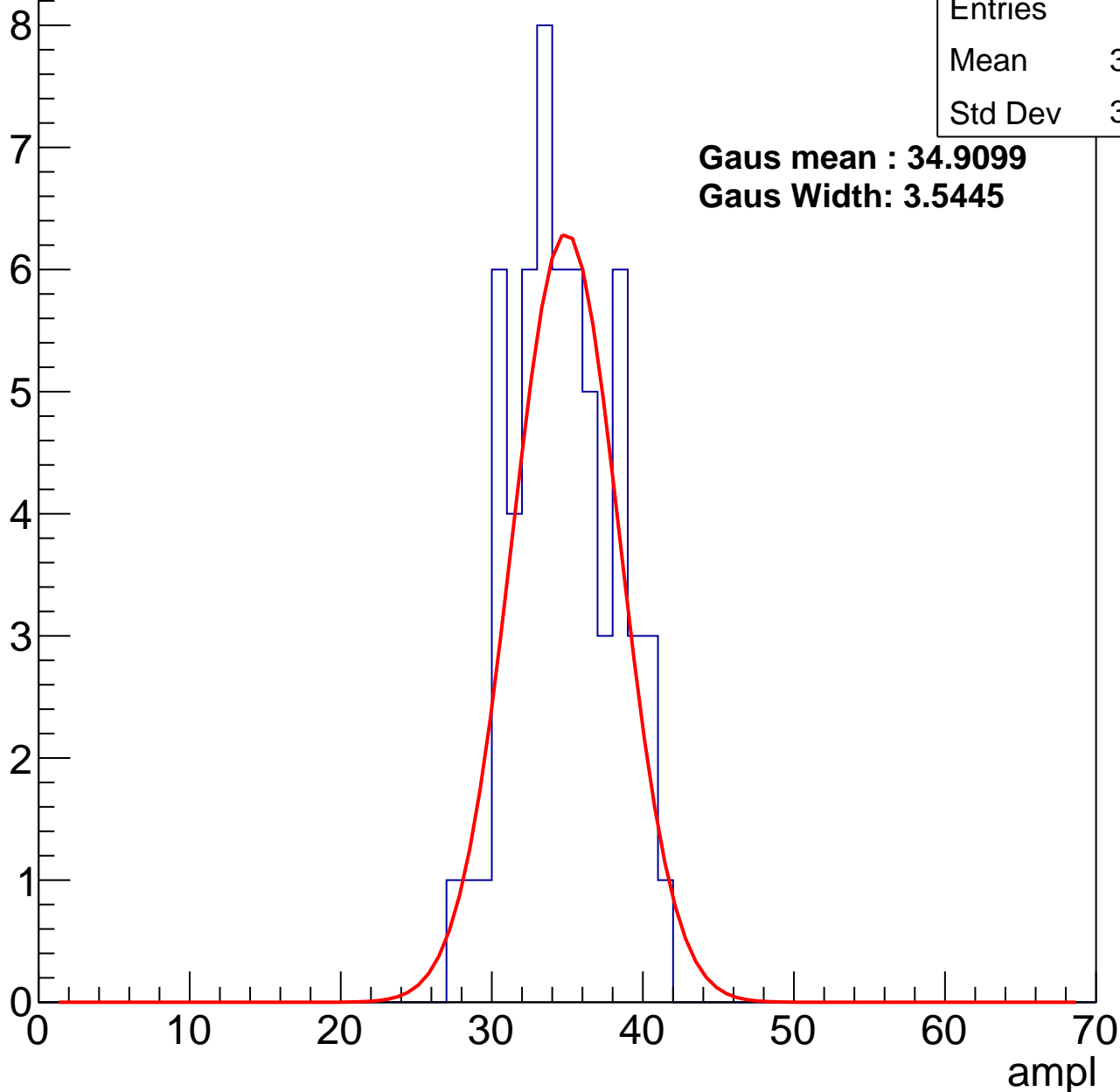
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	34.25
Std Dev	3.295

Gaus mean : 34.9099

Gaus Width: 3.5445



B0L002S, U9-ch106, adc2

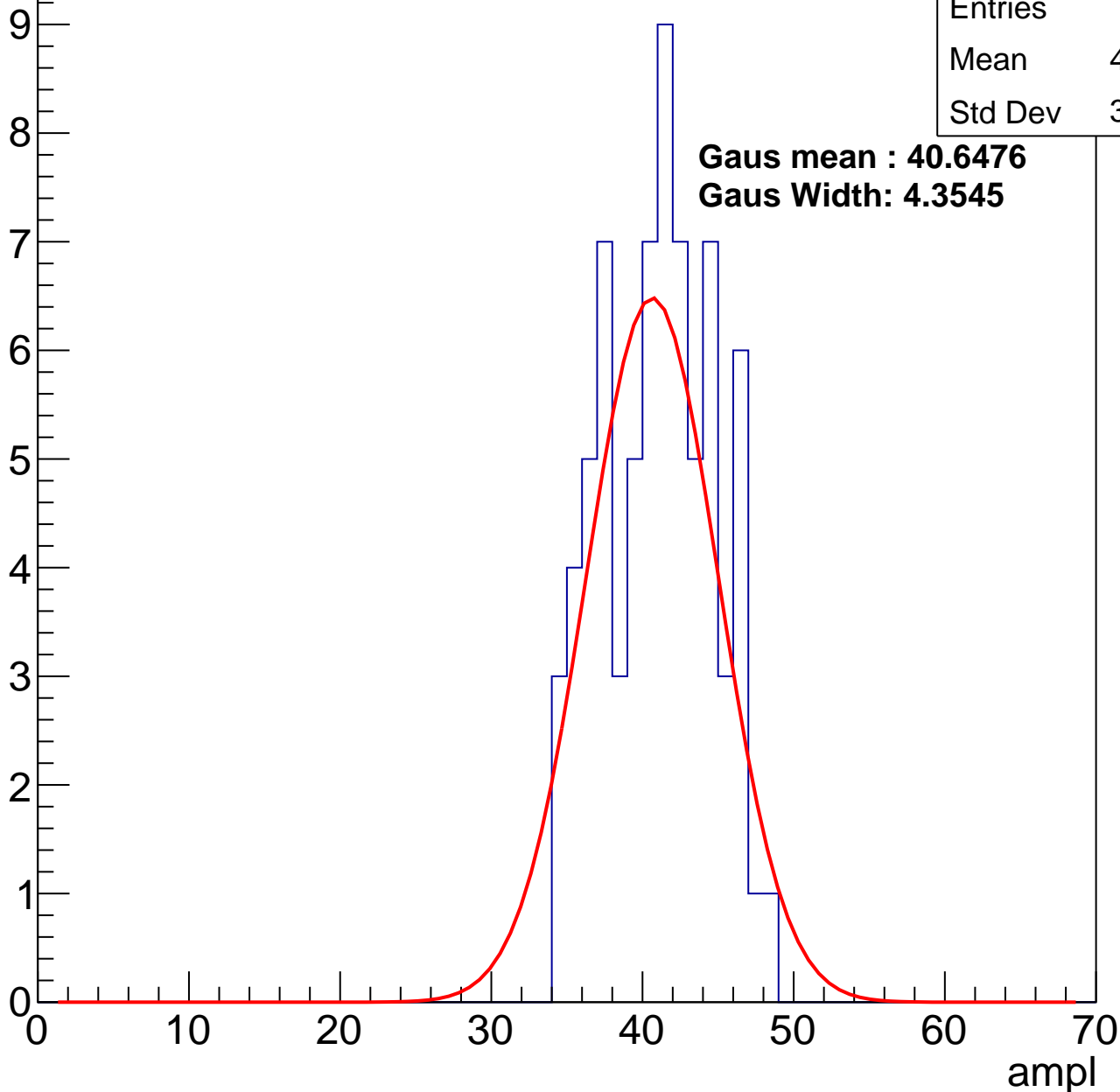
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	40.58
Std Dev	3.592

Gaus mean : 40.6476

Gaus Width: 4.3545

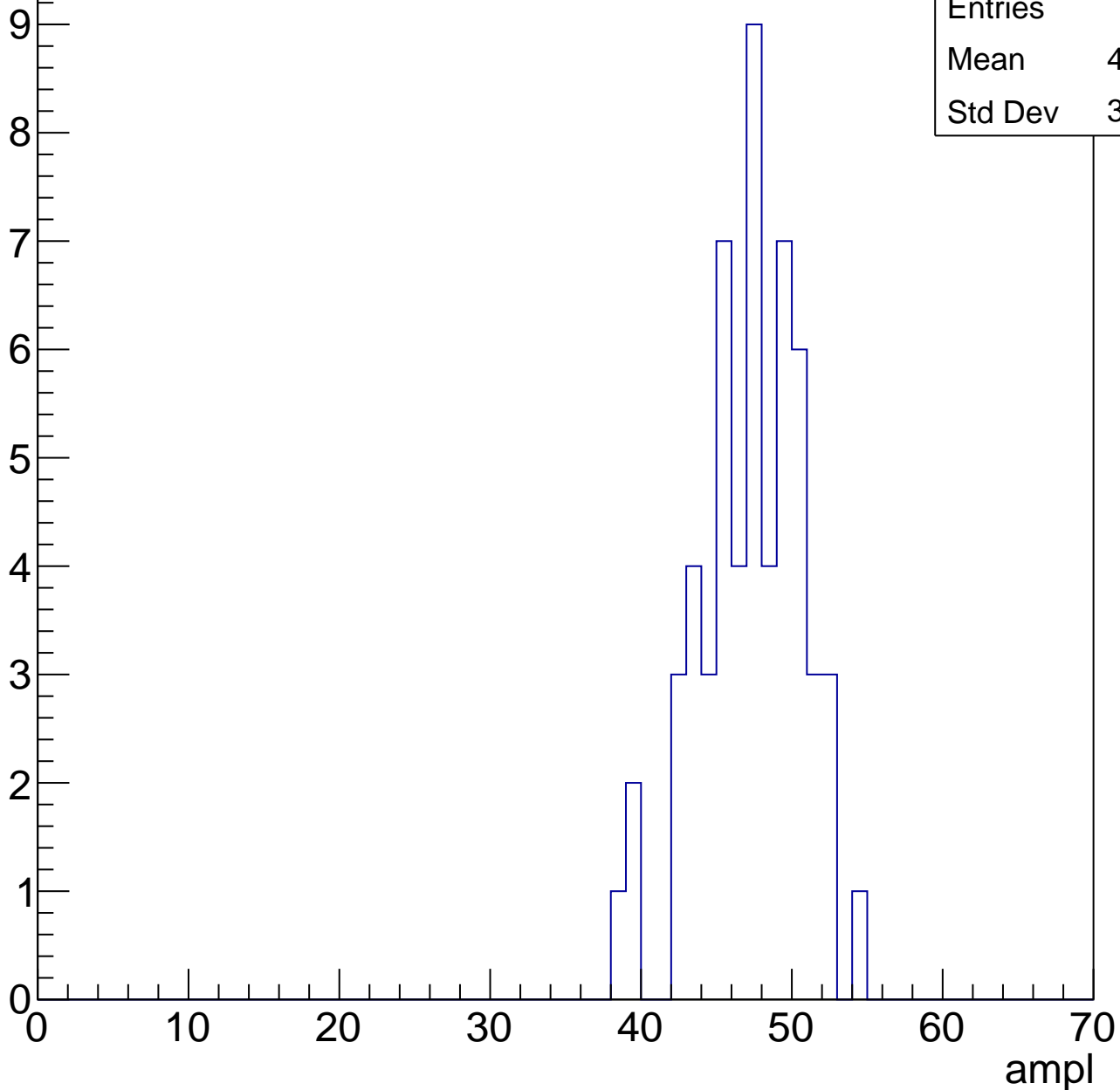


B0L002S, U9-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	46.77
Std Dev	3.408



B0L002S, U9-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	56
Mean	52.55
Std Dev	2.97

Entry

10

8

6

4

2

0

0

10

20

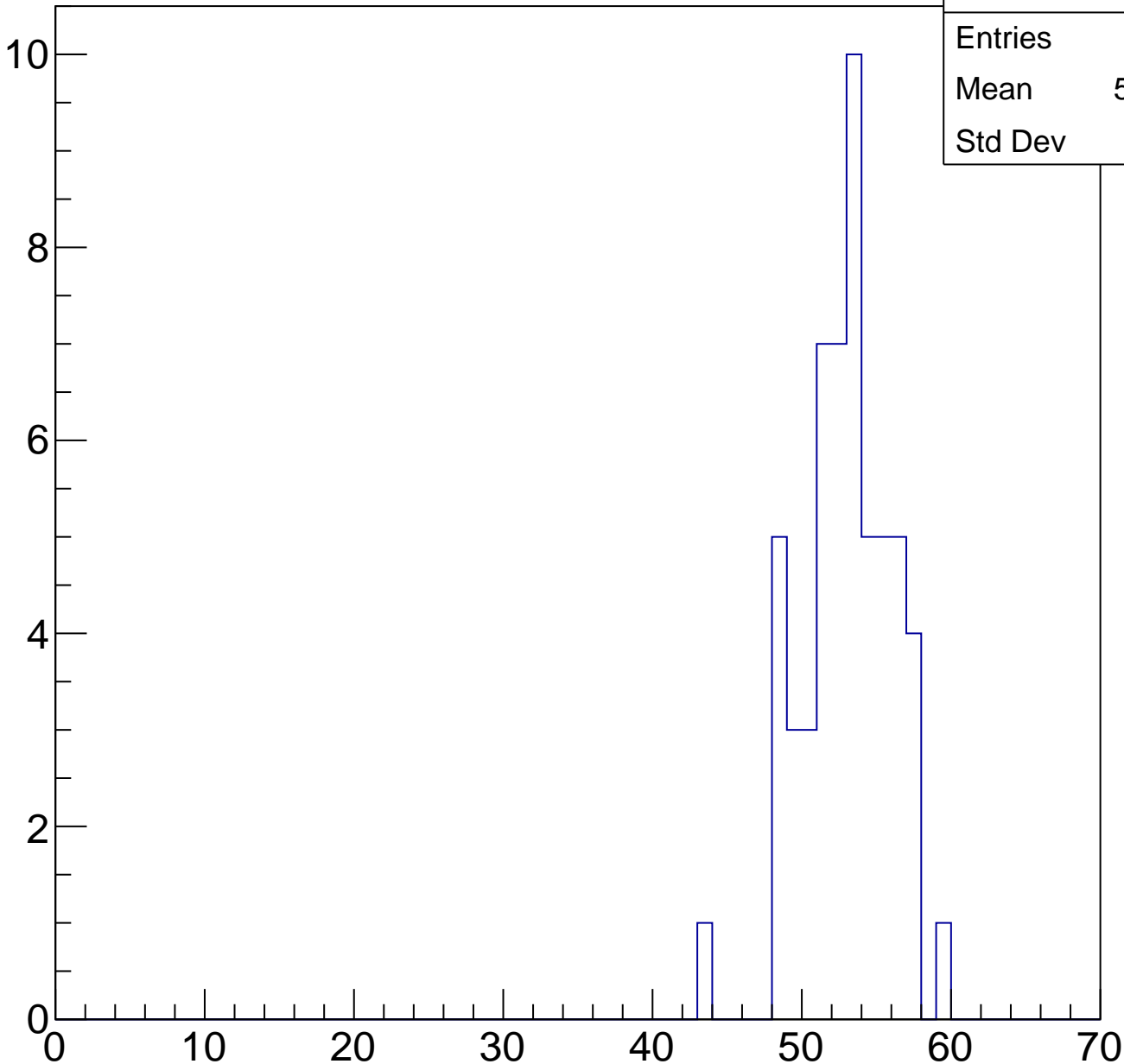
30

40

50

60

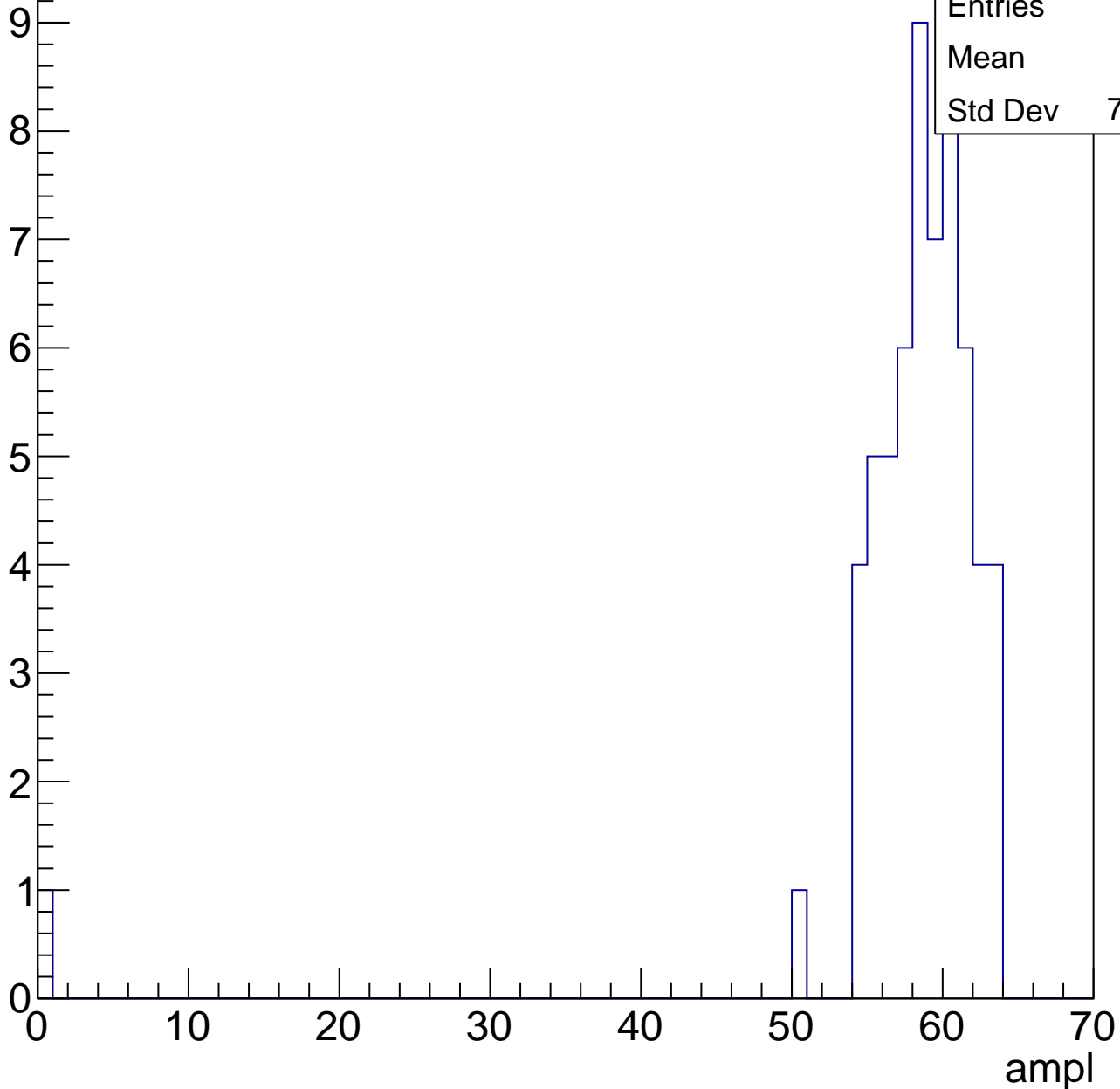
ampl



B0L002S, U9-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

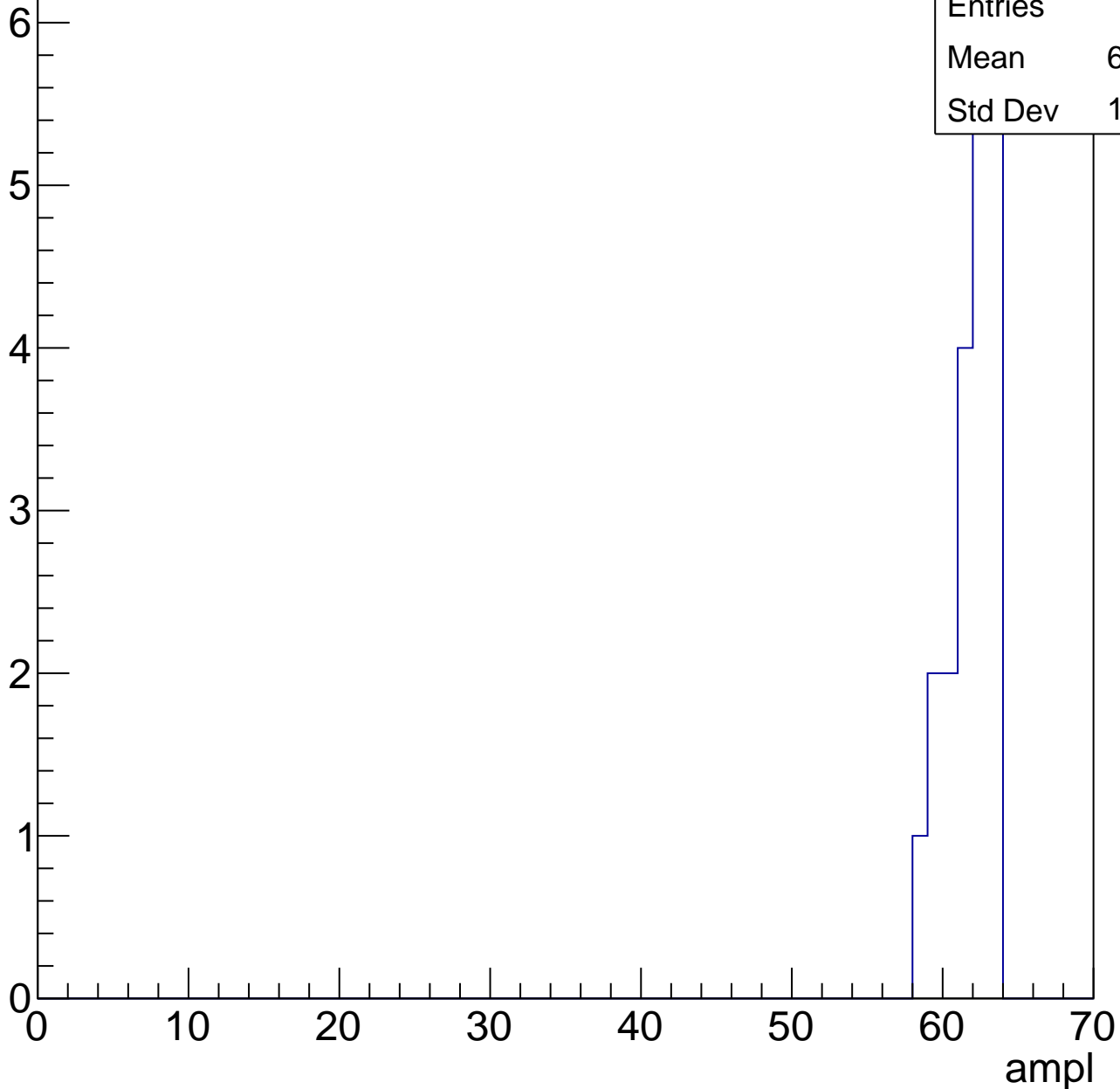


B0L002S, U9-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	61.43
Std Dev	1.466



B0L002S, U9-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch107, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	30.14
Std Dev	3.713

Gaus mean : 30.6205

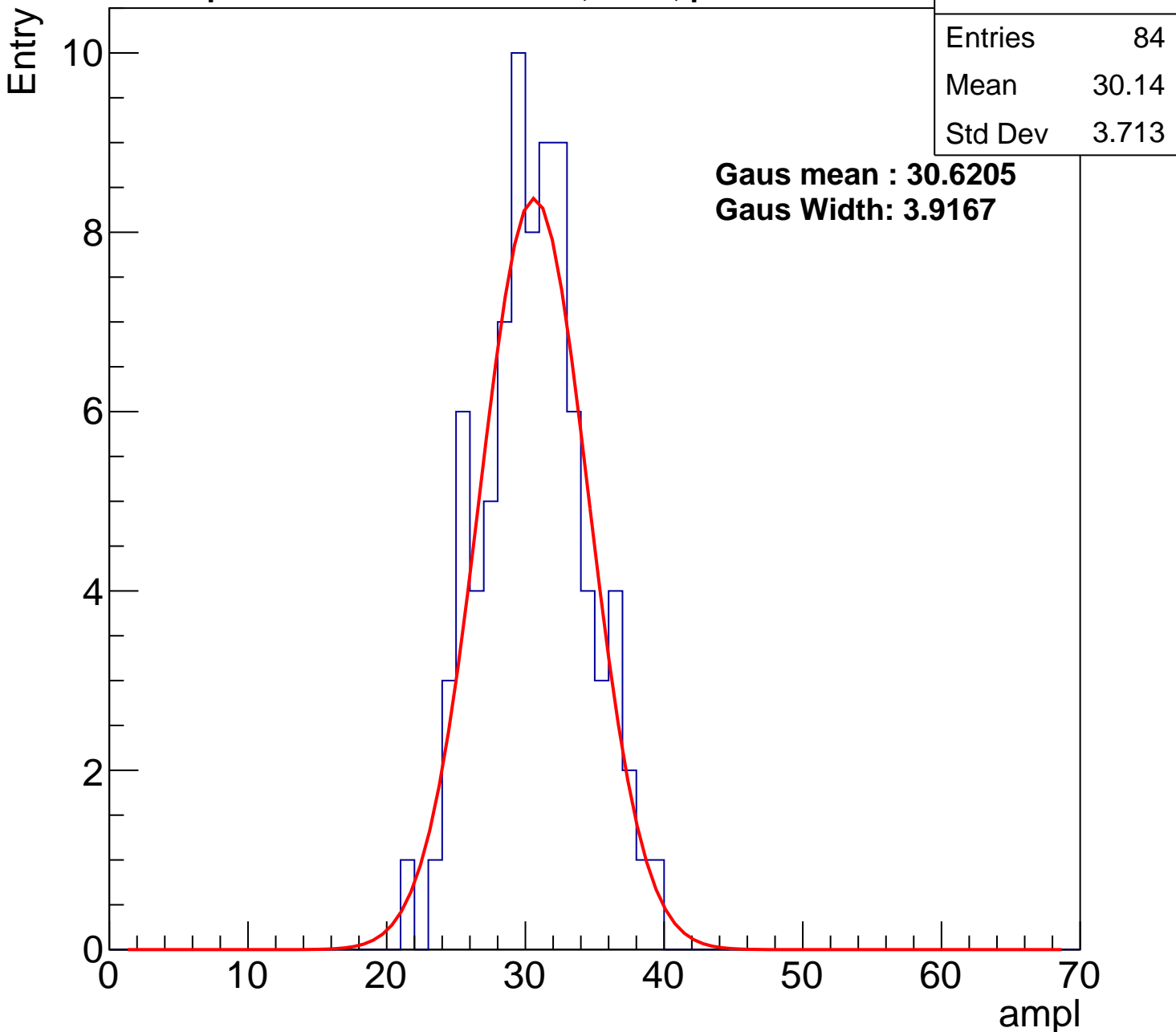
Gaus Width: 3.9167

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



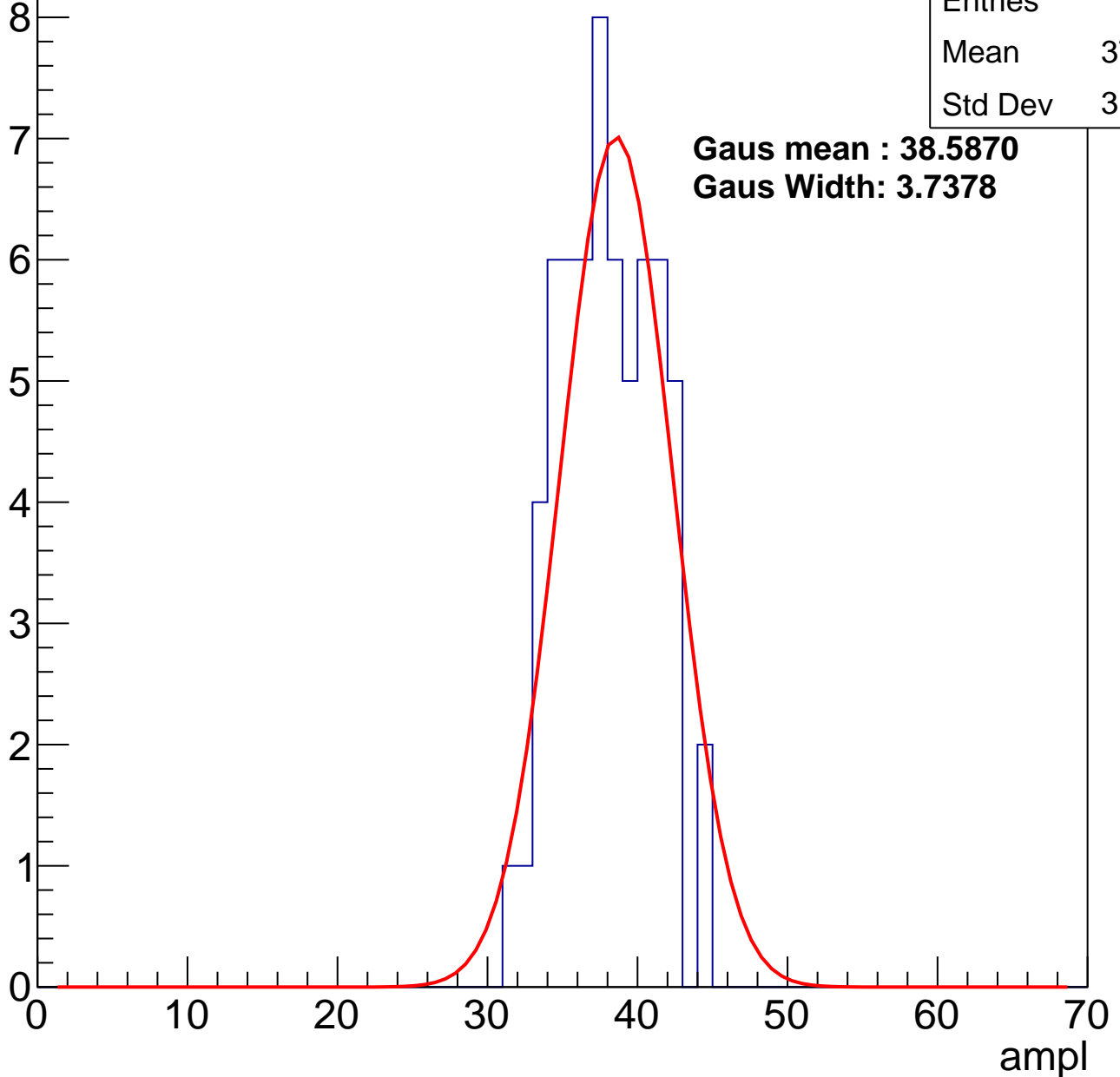
B0L002S, U9-ch107, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.55
Std Dev	3.083

Gaus mean : 38.5870
Gaus Width: 3.7378



B0L002S, U9-ch107, adc2

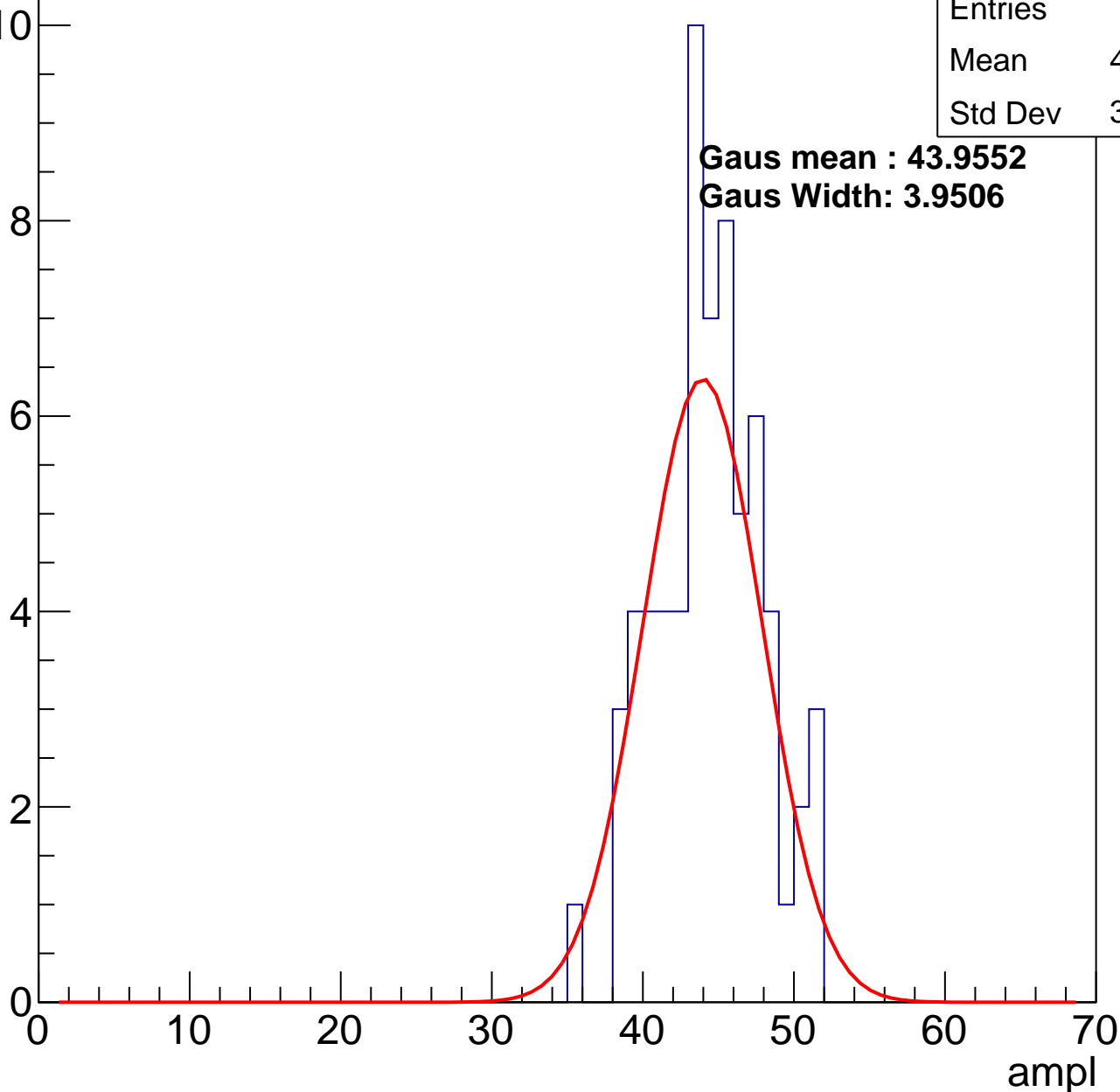
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.95
Std Dev	3.492

Gaus mean : 43.9552

Gaus Width: 3.9506

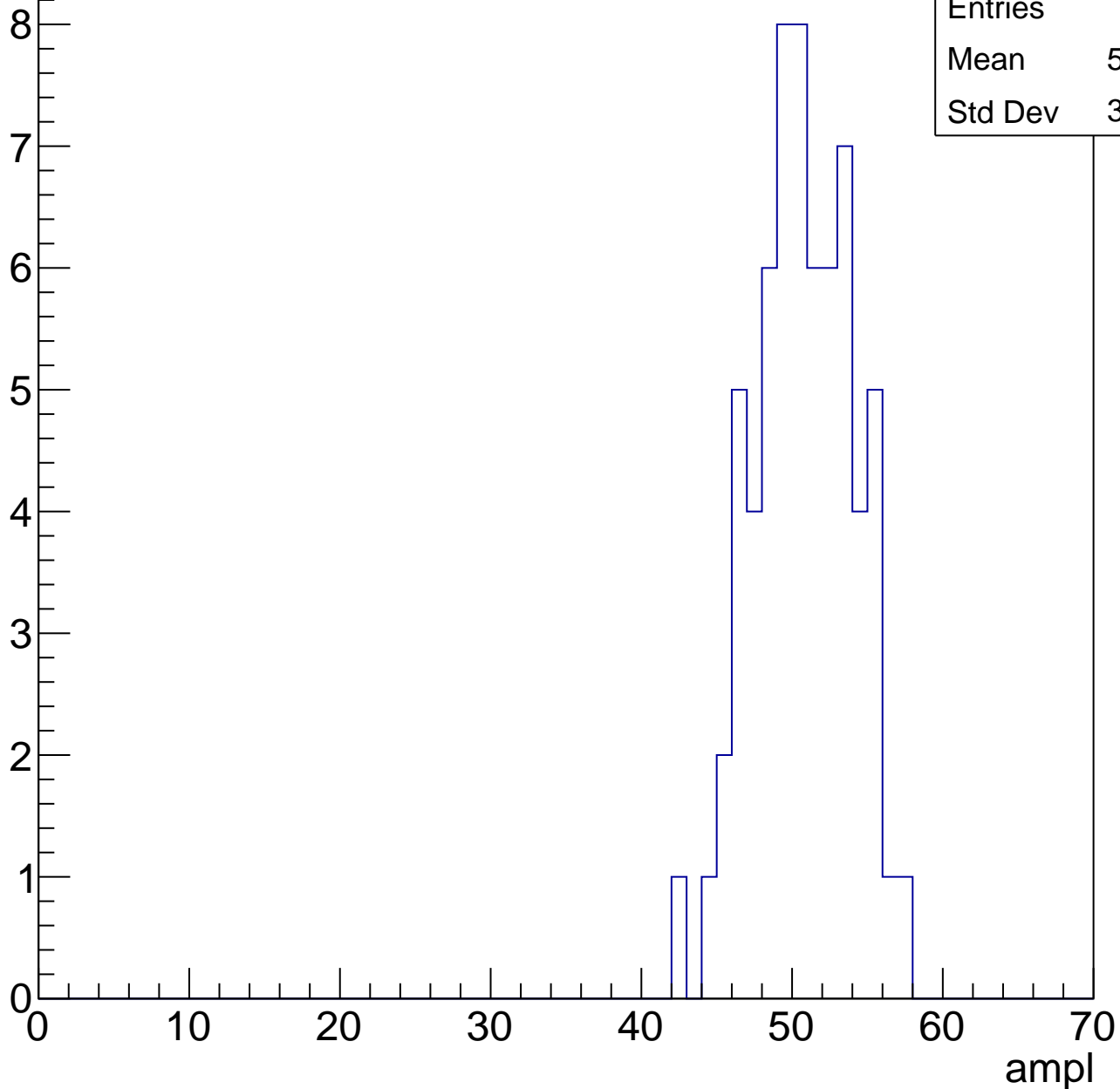


B0L002S, U9-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	50.26
Std Dev	3.188

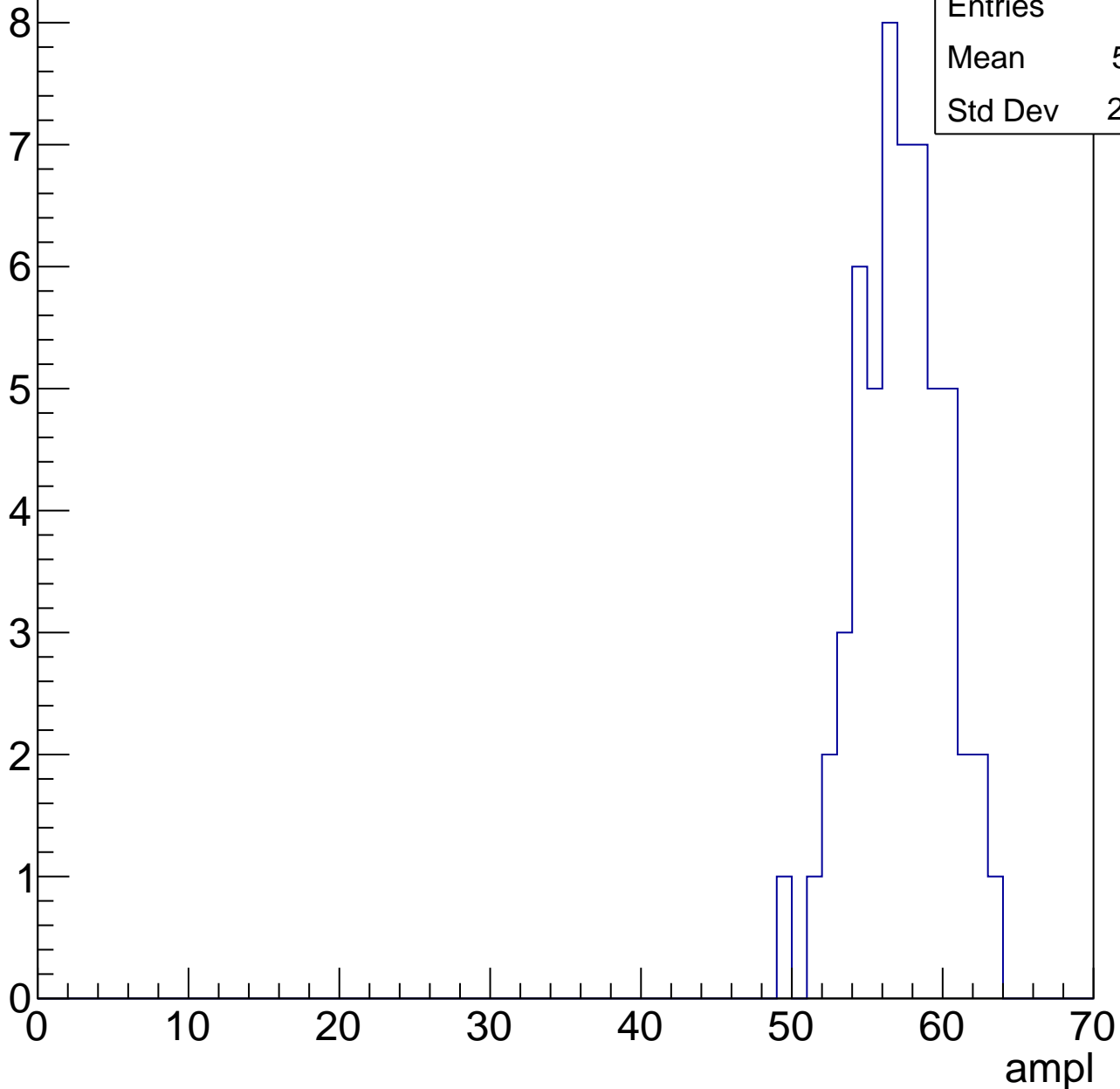


B0L002S, U9-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.71
Std Dev	2.909

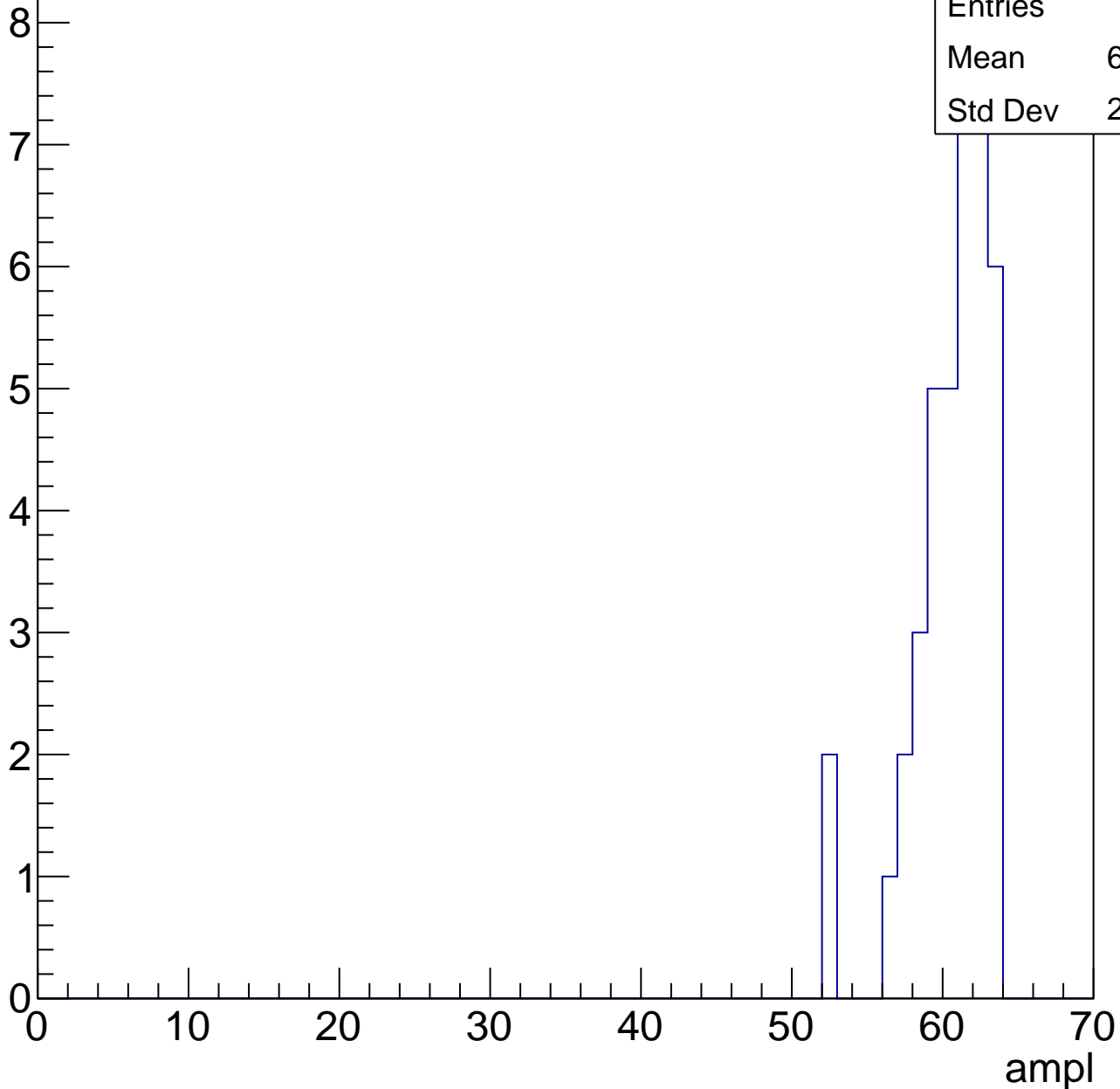


B0L002S, U9-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

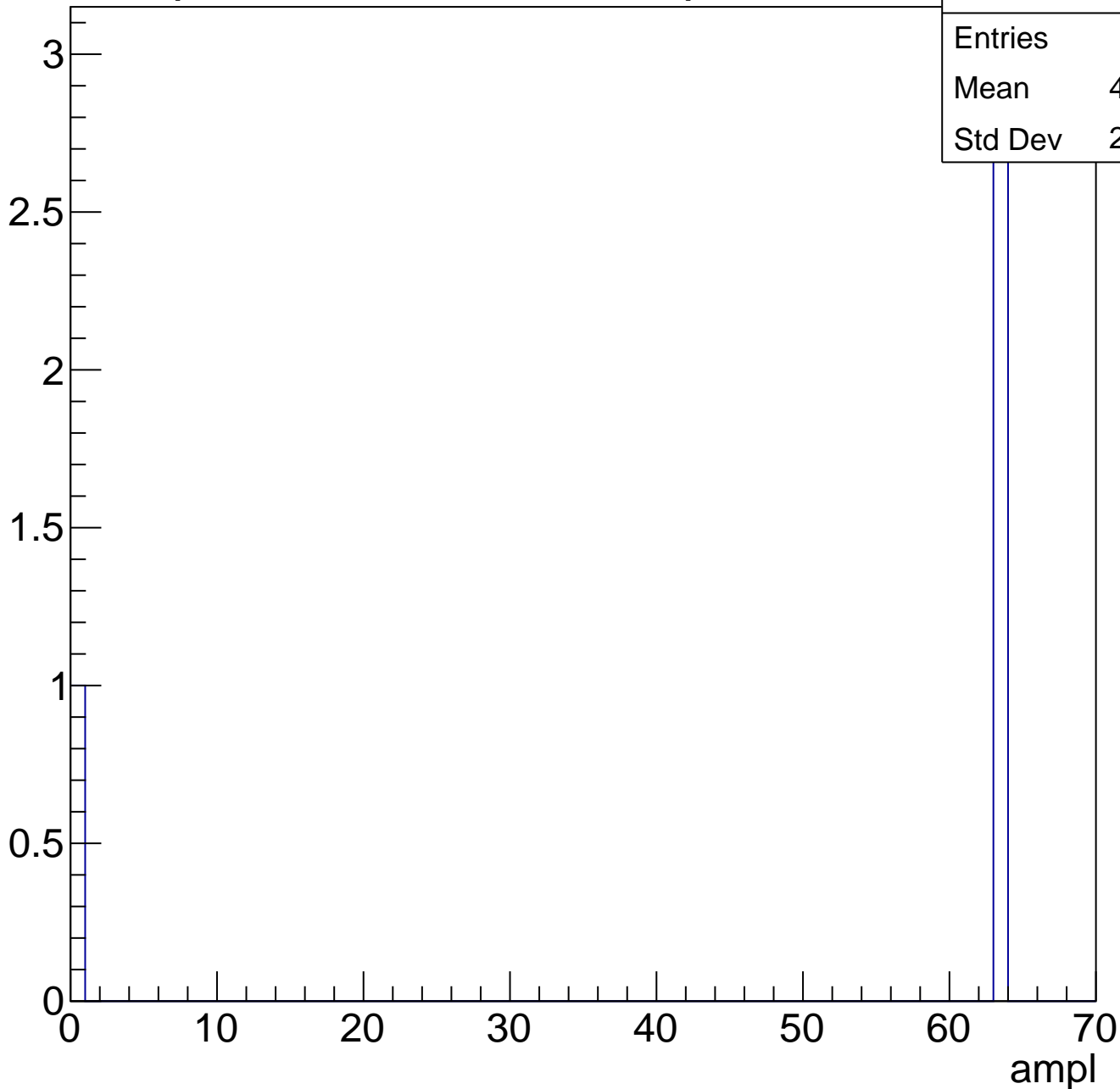
Entries	40
Mean	60.12
Std Dev	2.609



B0L002S, U9-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch108, adc0

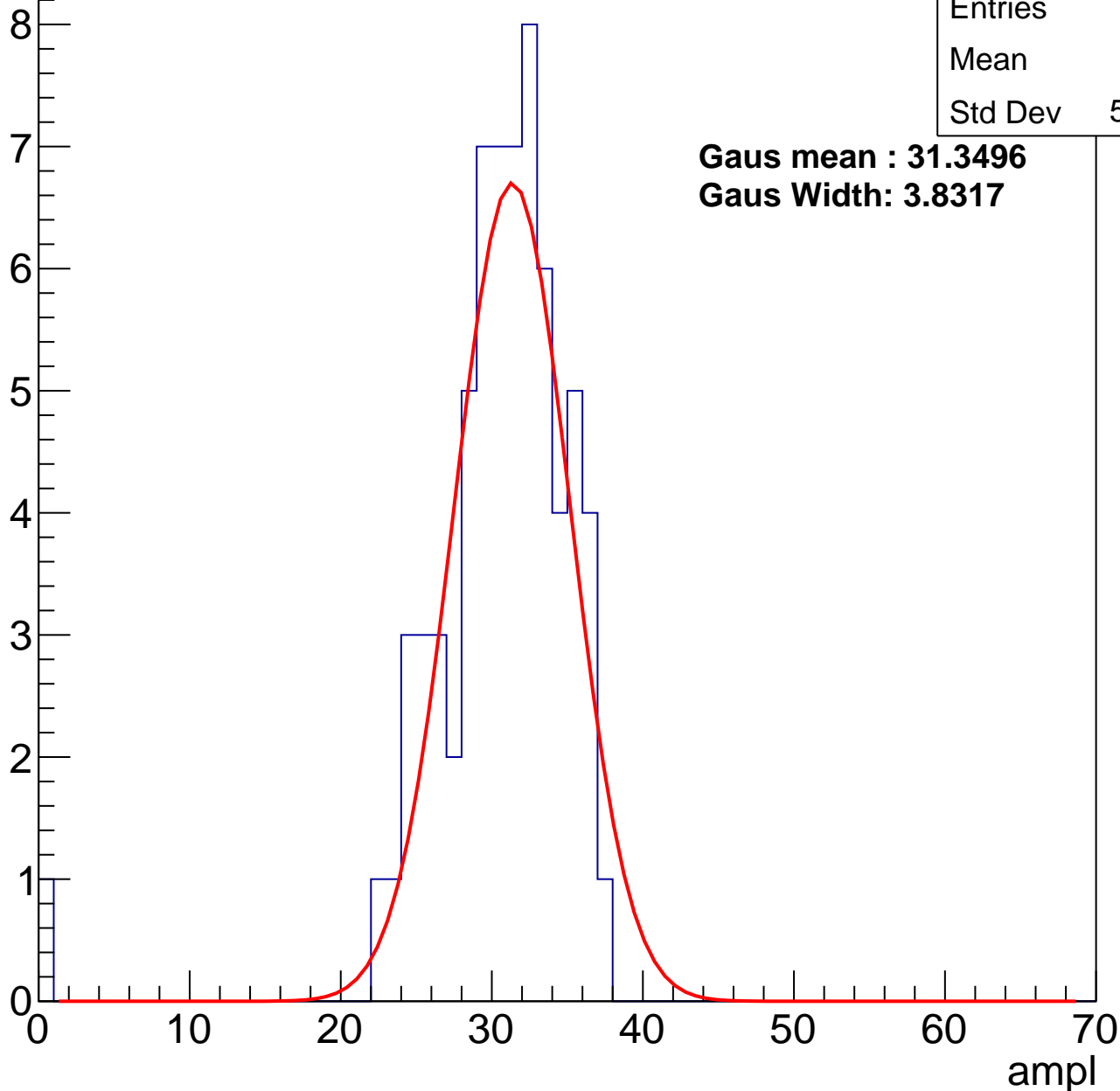
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30
Std Dev	5.093

Gaus mean : 31.3496

Gaus Width: 3.8317



B0L002S, U9-ch108, adc1

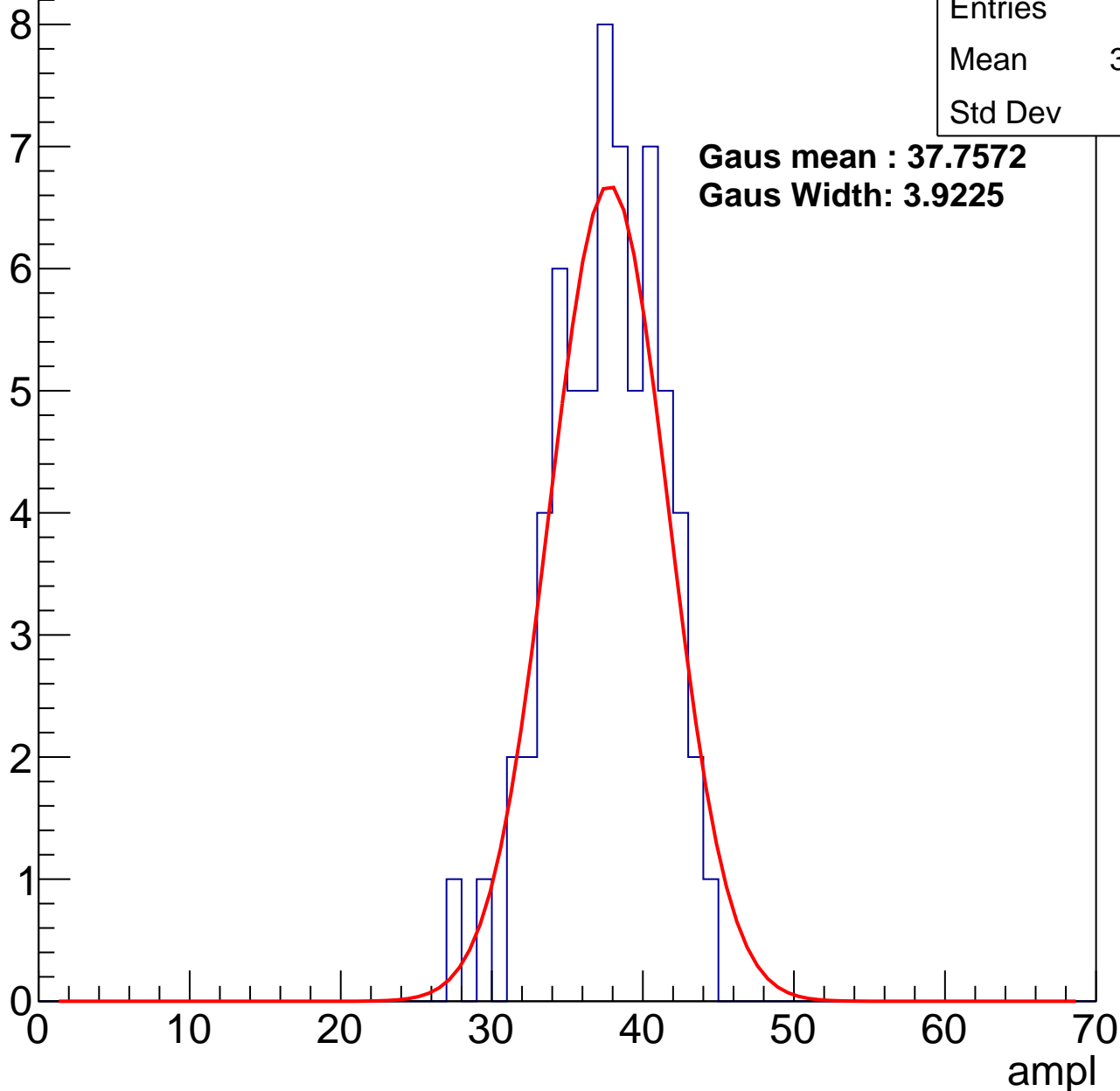
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.12
Std Dev	3.55

Gaus mean : 37.7572

Gaus Width: 3.9225



B0L002S, U9-ch108, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	44.11
Std Dev	3.65

Gaus mean : 44.4533

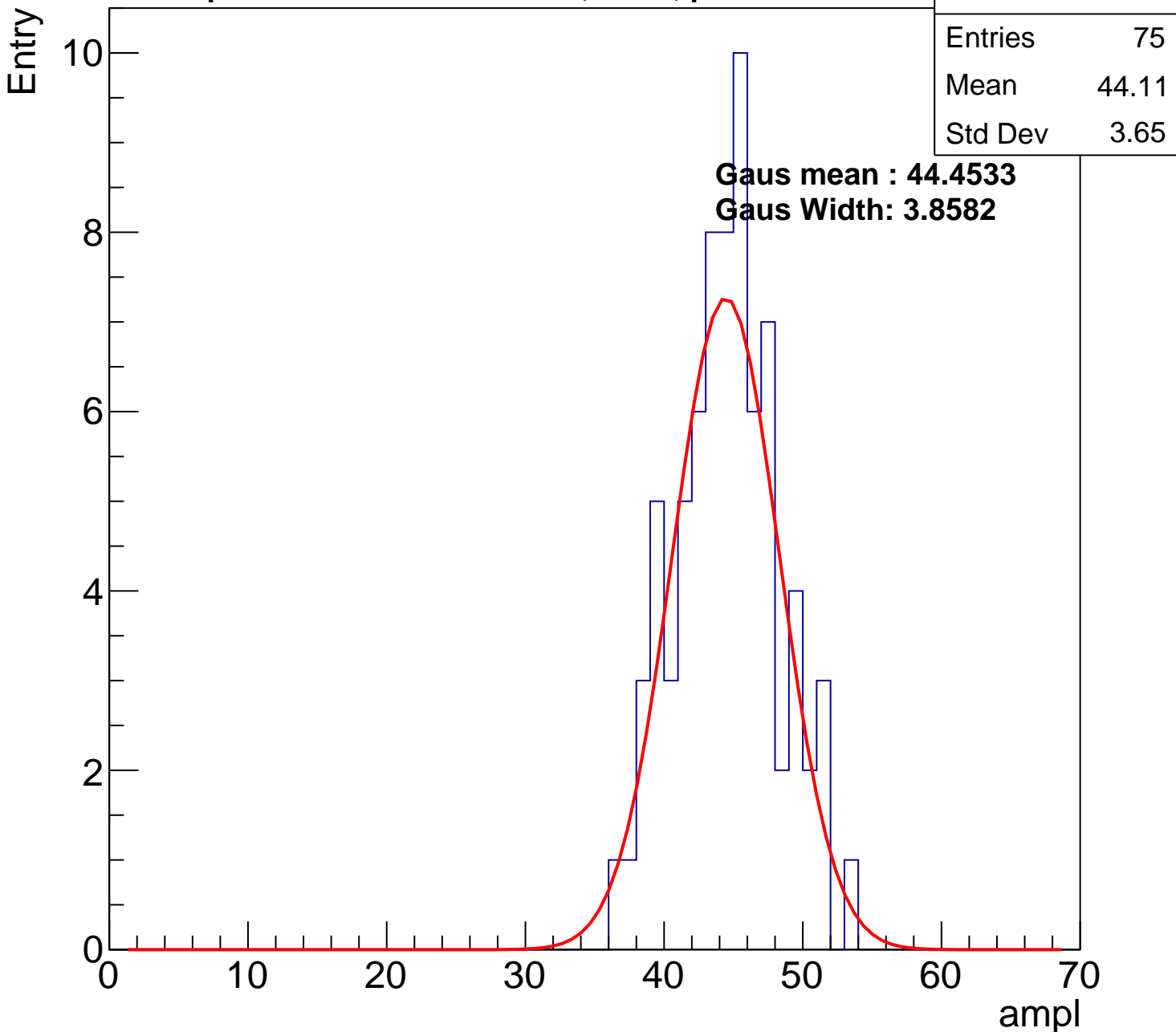
Gaus Width: 3.8582

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

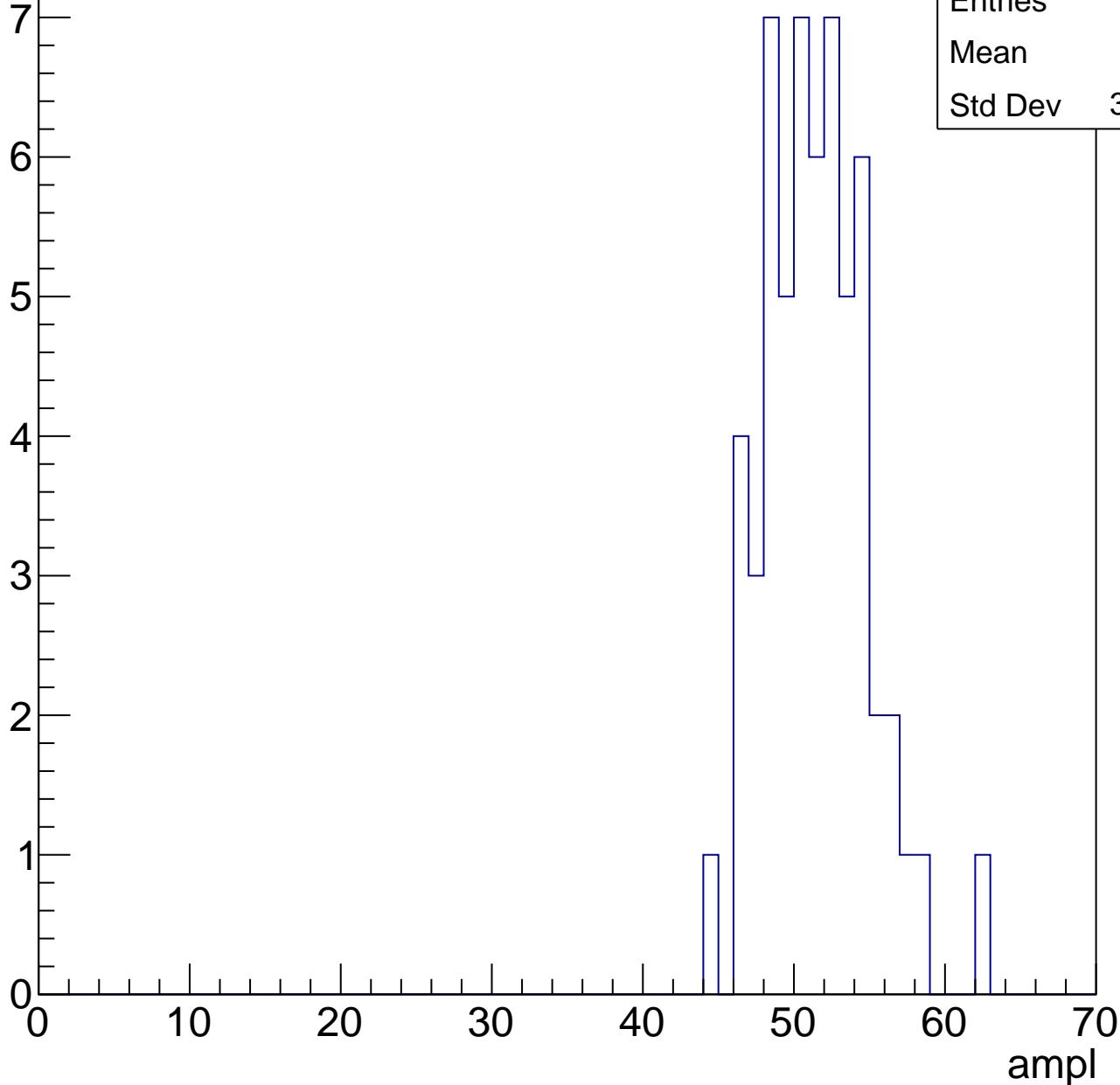


B0L002S, U9-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	51
Std Dev	3.363



B0L002S, U9-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	59
Mean	56.36
Std Dev	3.101

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

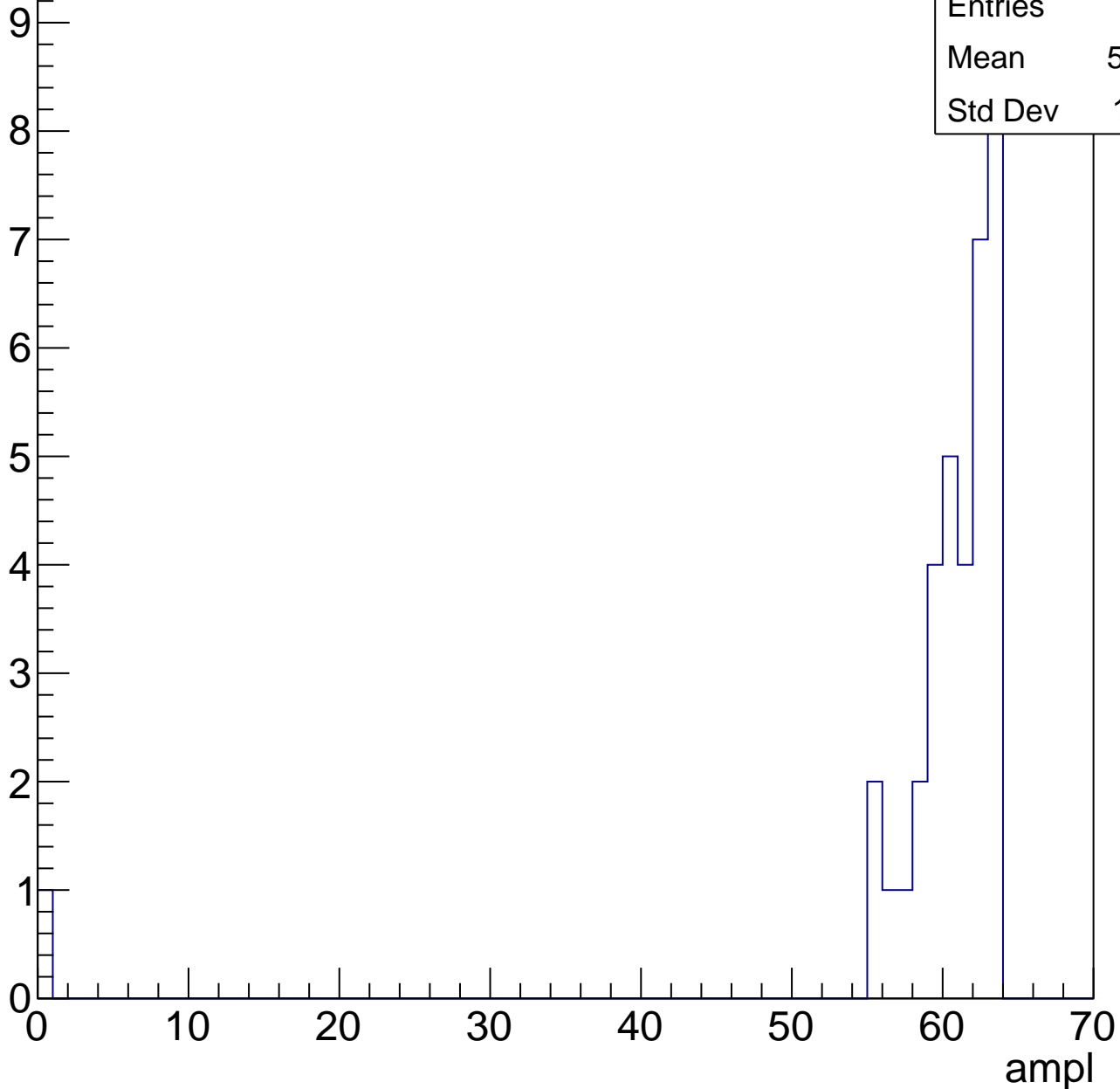
70

B0L002S, U9-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	58.89
Std Dev	10.21



B0L002S, U9-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch109, adc0

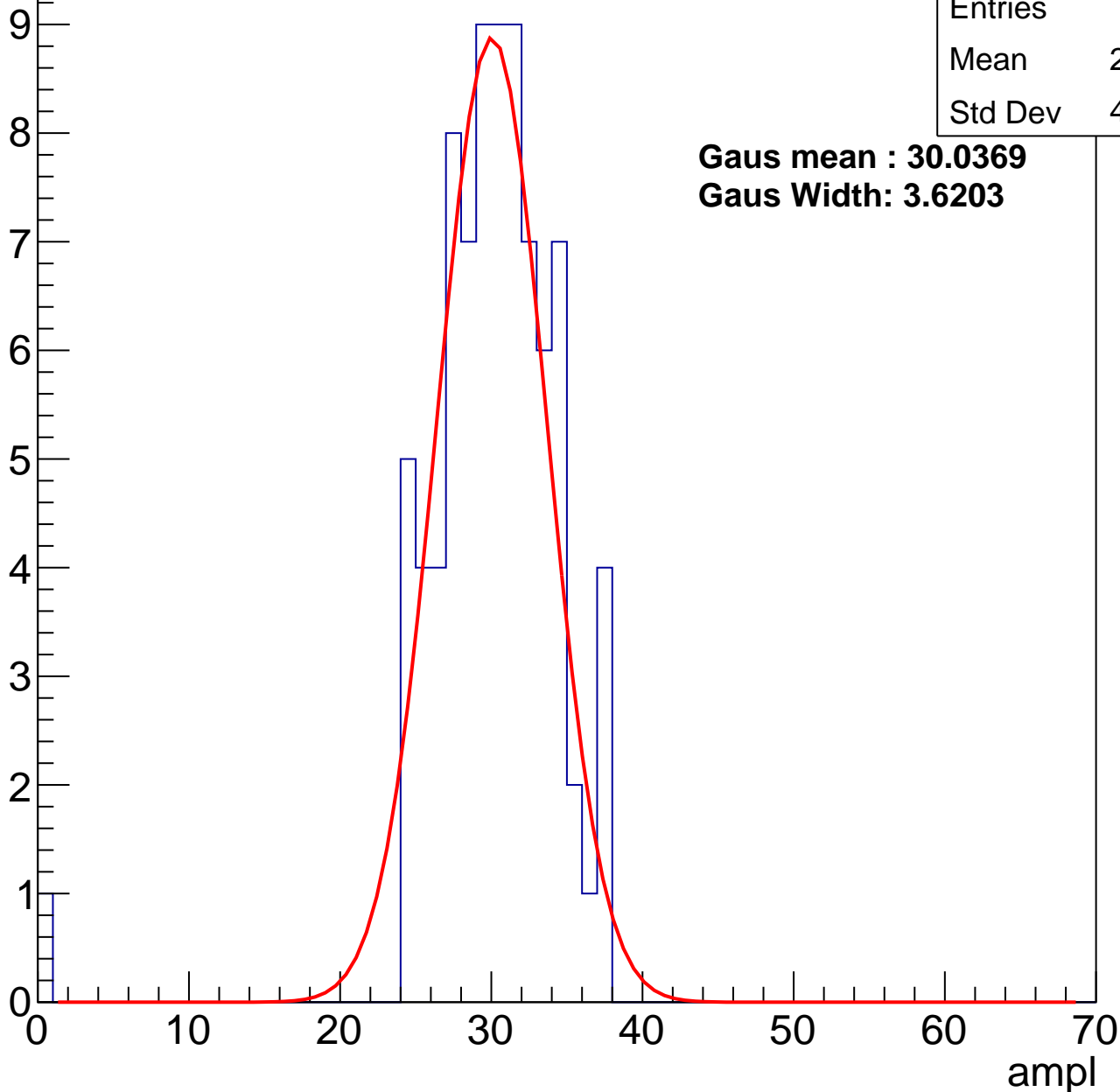
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	29.64
Std Dev	4.692

Gaus mean : 30.0369

Gaus Width: 3.6203



B0L002S, U9-ch109, adc1

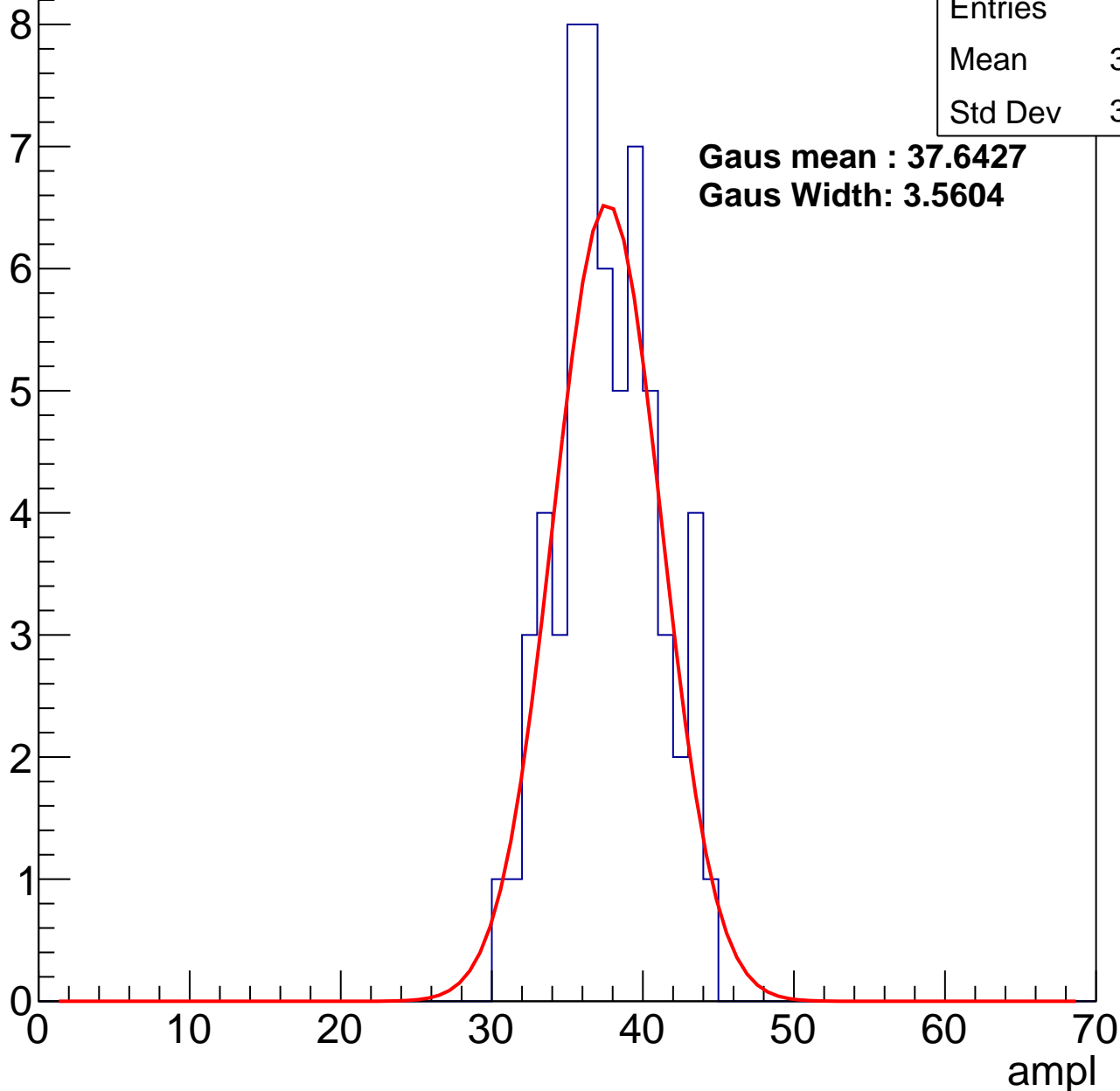
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.16
Std Dev	3.285

Gaus mean : 37.6427

Gaus Width: 3.5604



B0L002S, U9-ch109, adc2

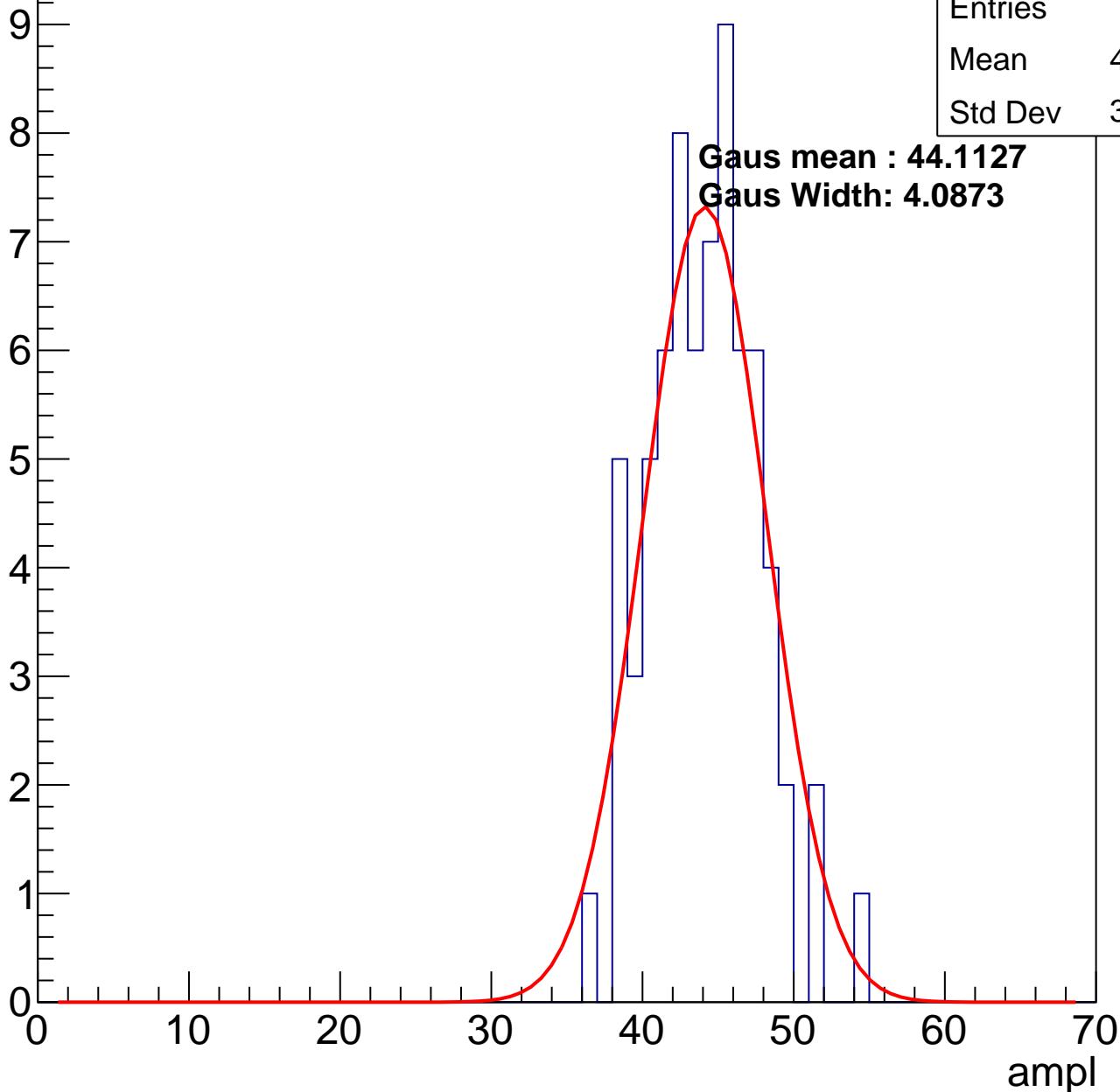
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.66
Std Dev	3.524

Gaus mean : 44.1127

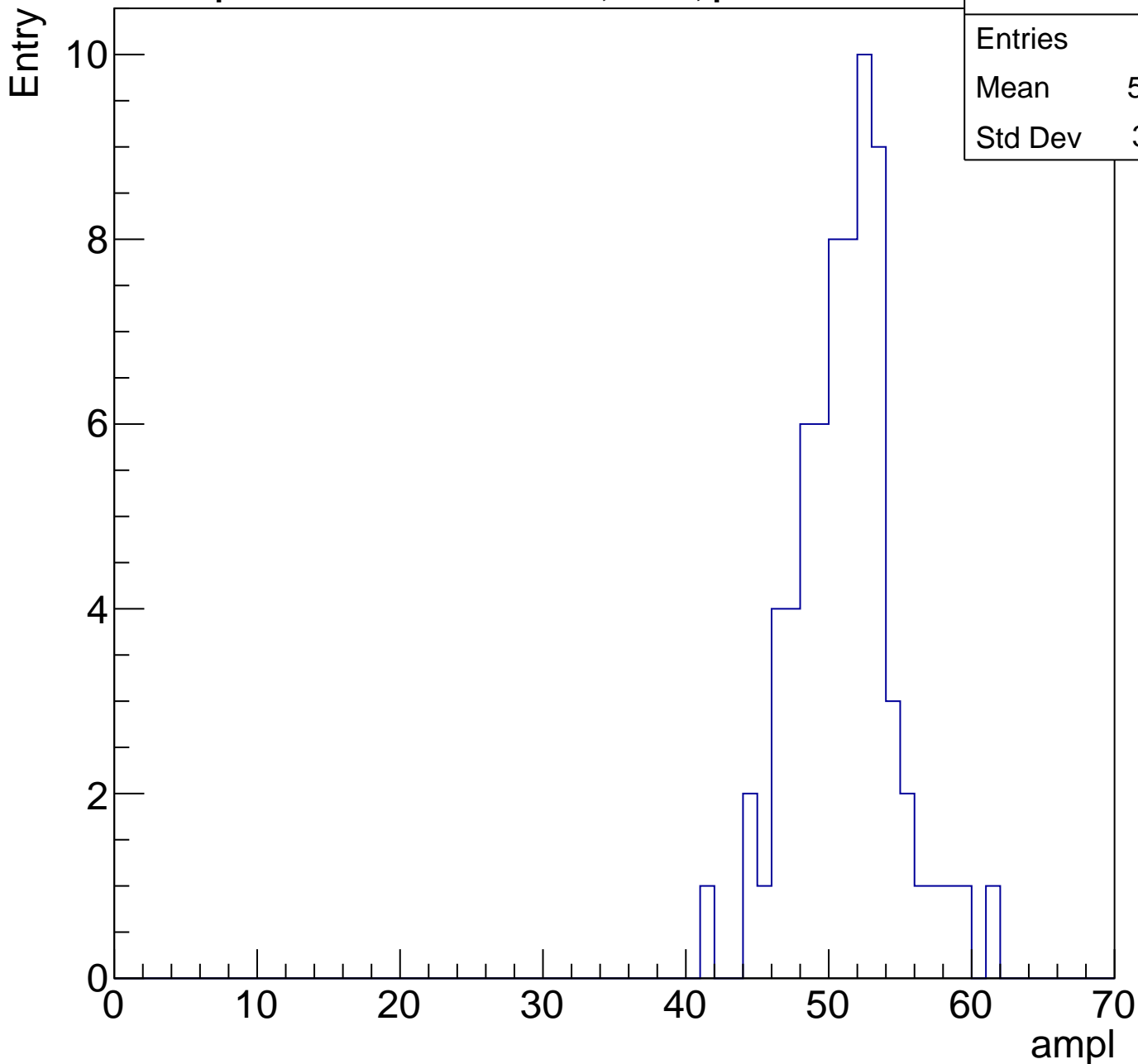
Gaus Width: 4.0873



B0L002S, U9-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

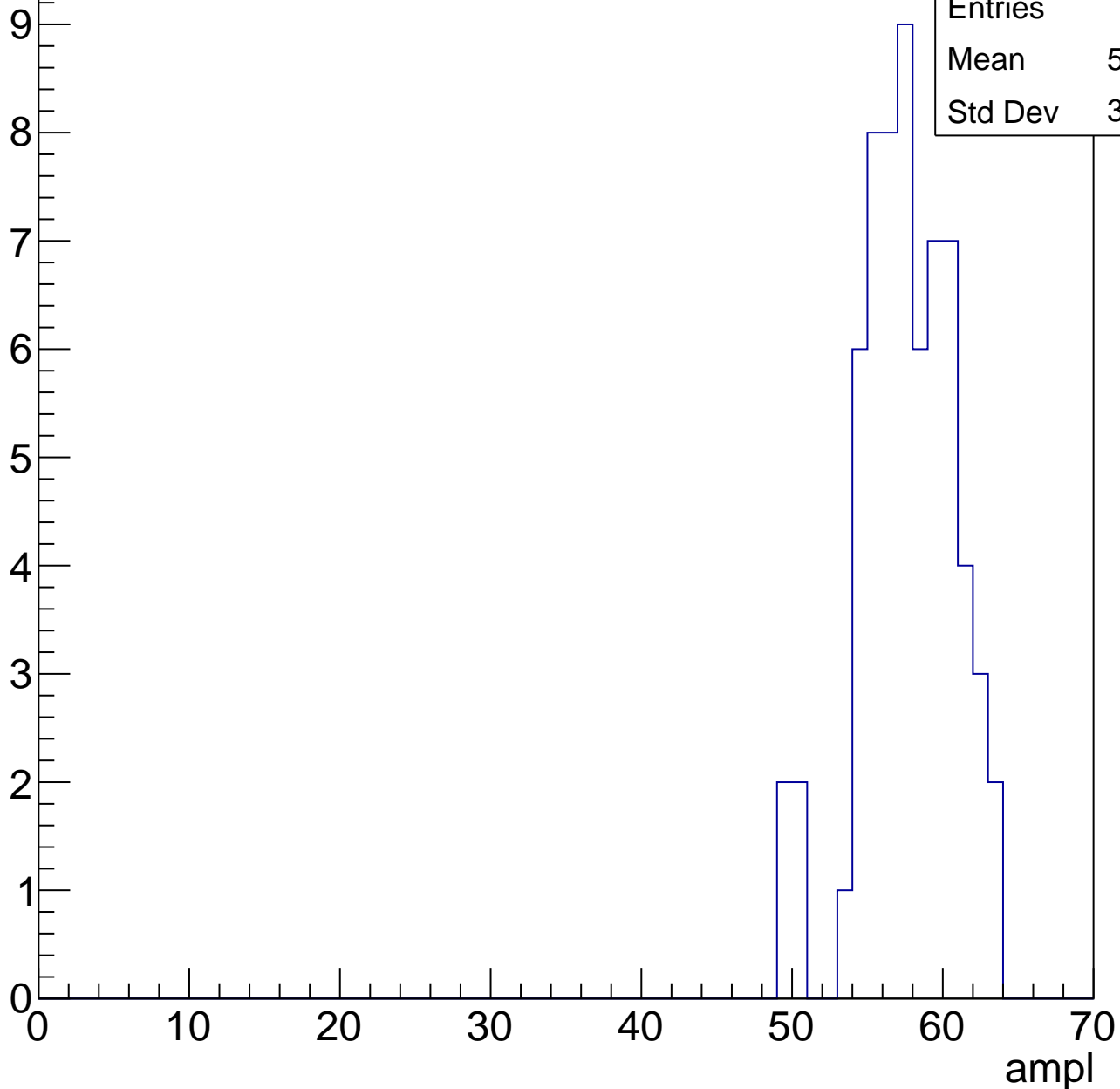
Entries	69
Mean	50.67
Std Dev	3.521



B0L002S, U9-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



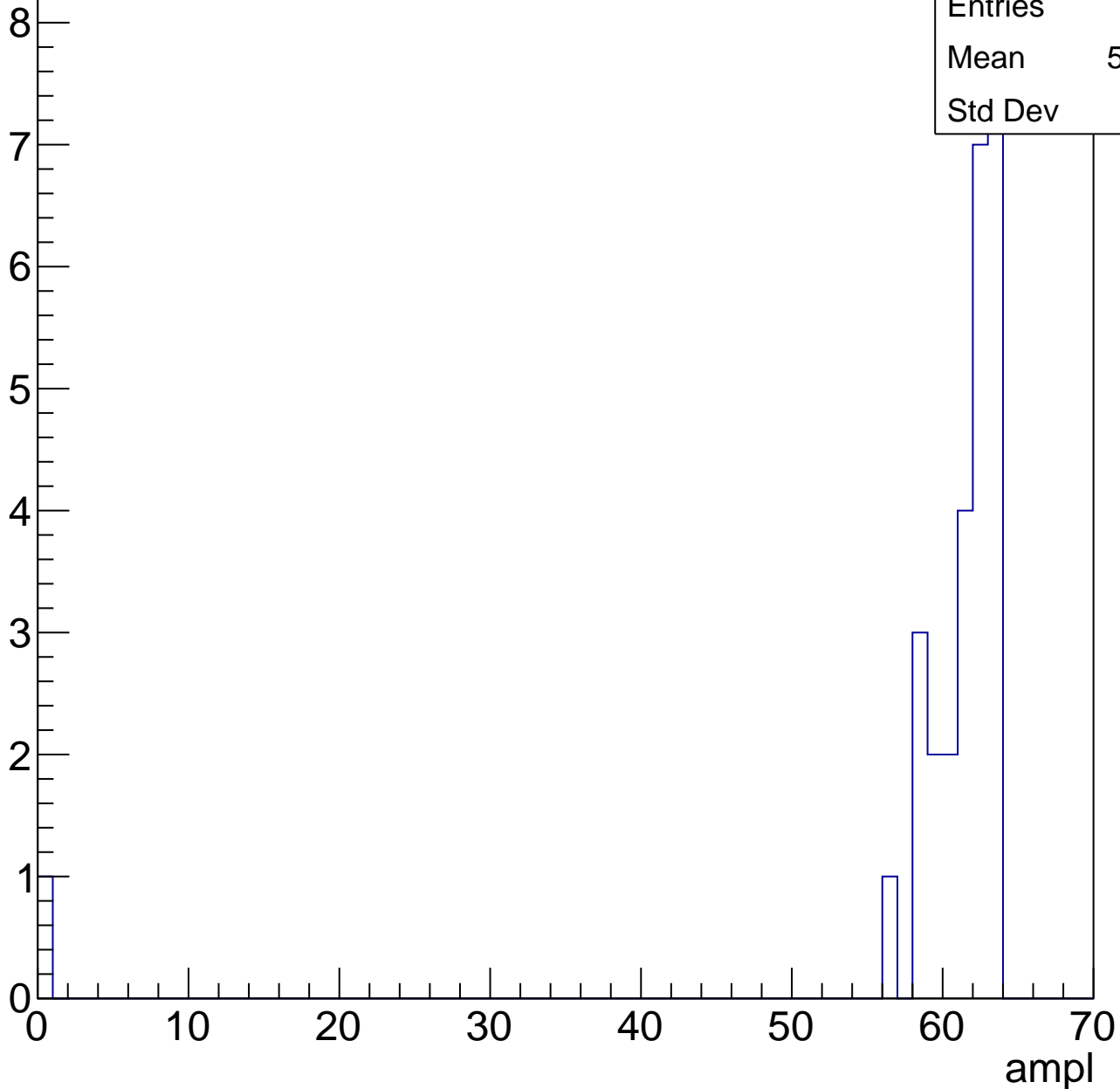
Entries	65
Mean	57.12
Std Dev	3.145

B0L002S, U9-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

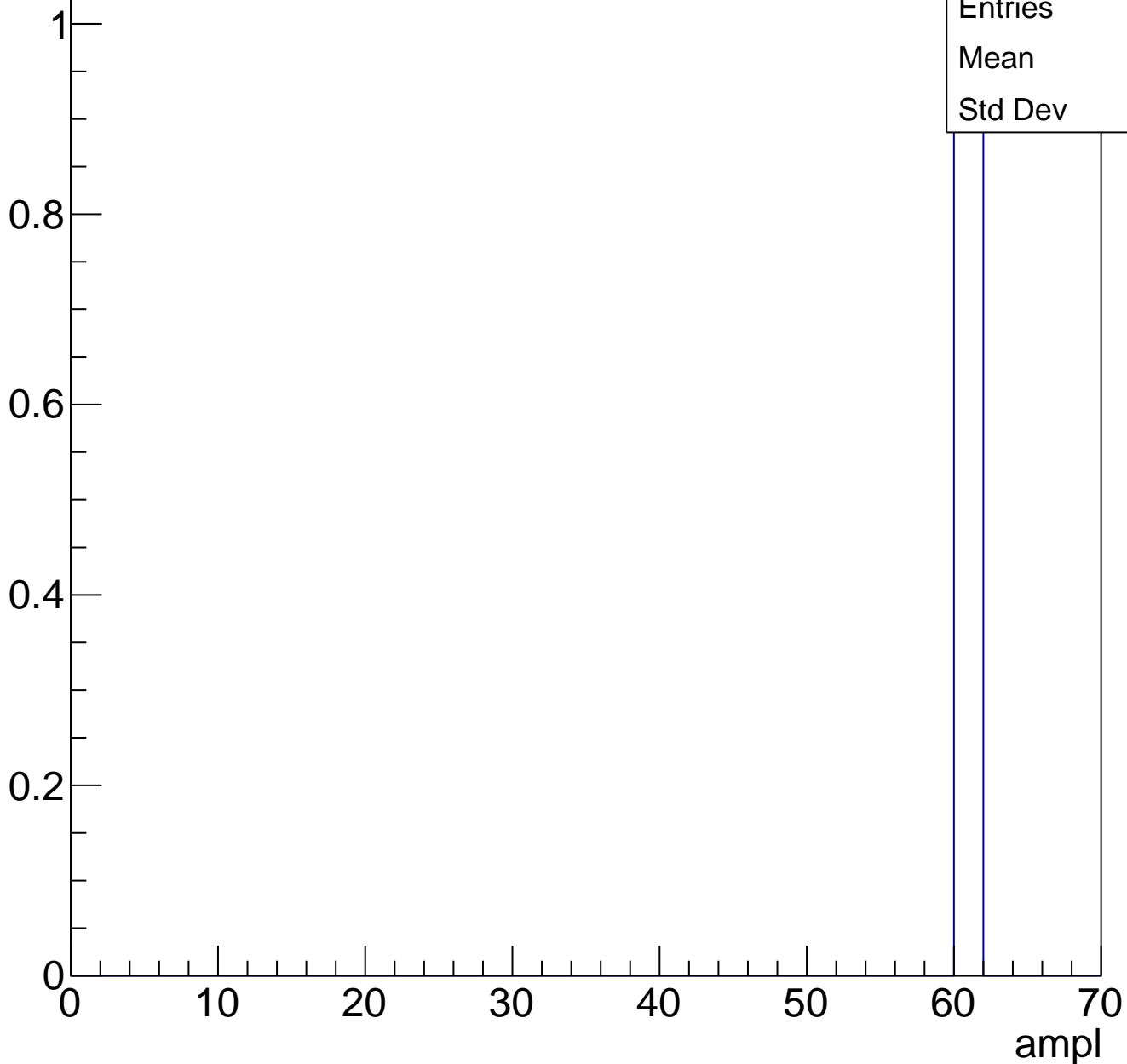
Entries	28
Mean	58.93
Std Dev	11.5



B0L002S, U9-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch109, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U9-ch110, adc0

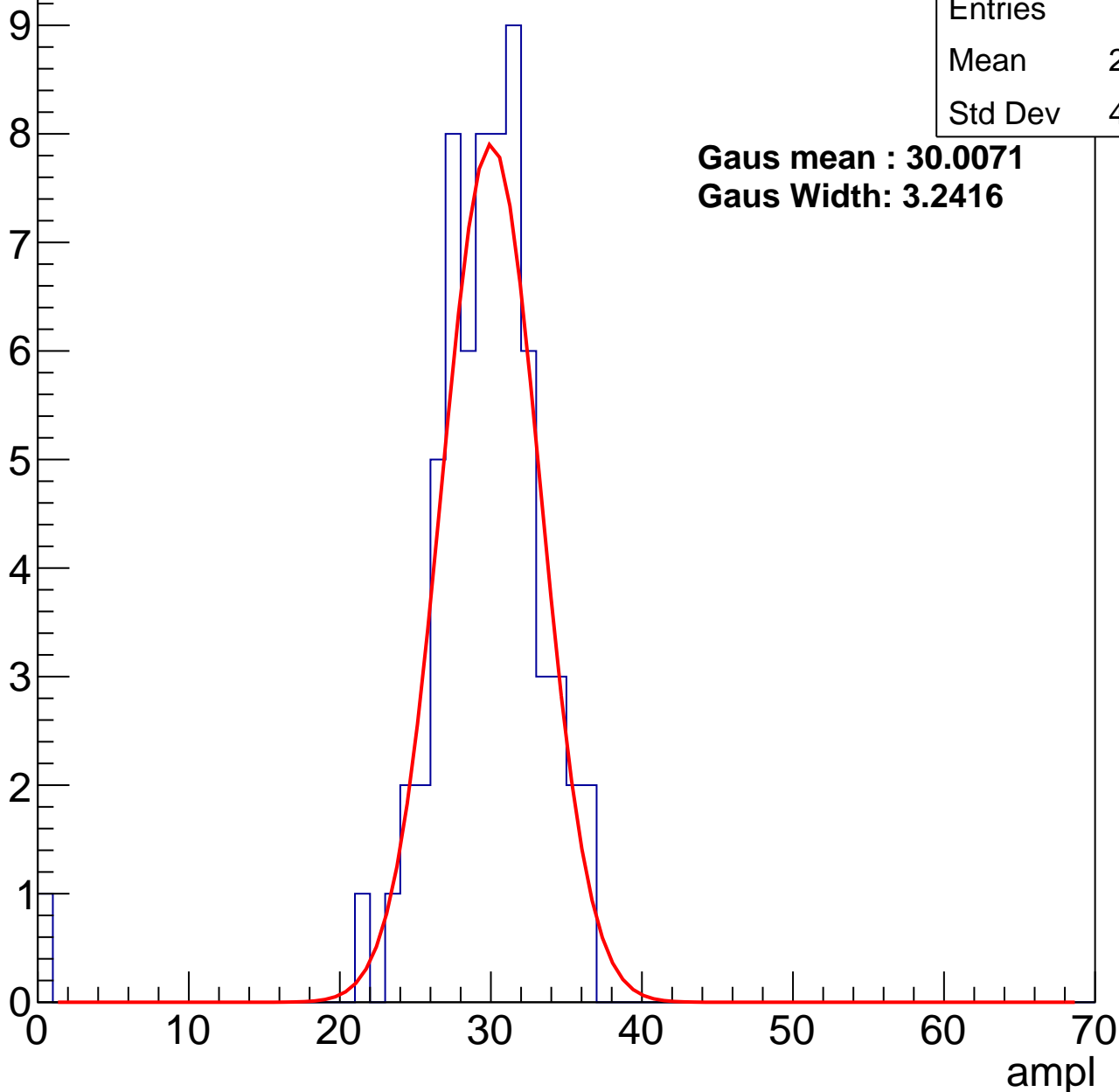
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	28.99
Std Dev	4.727

Gaus mean : 30.0071

Gaus Width: 3.2416



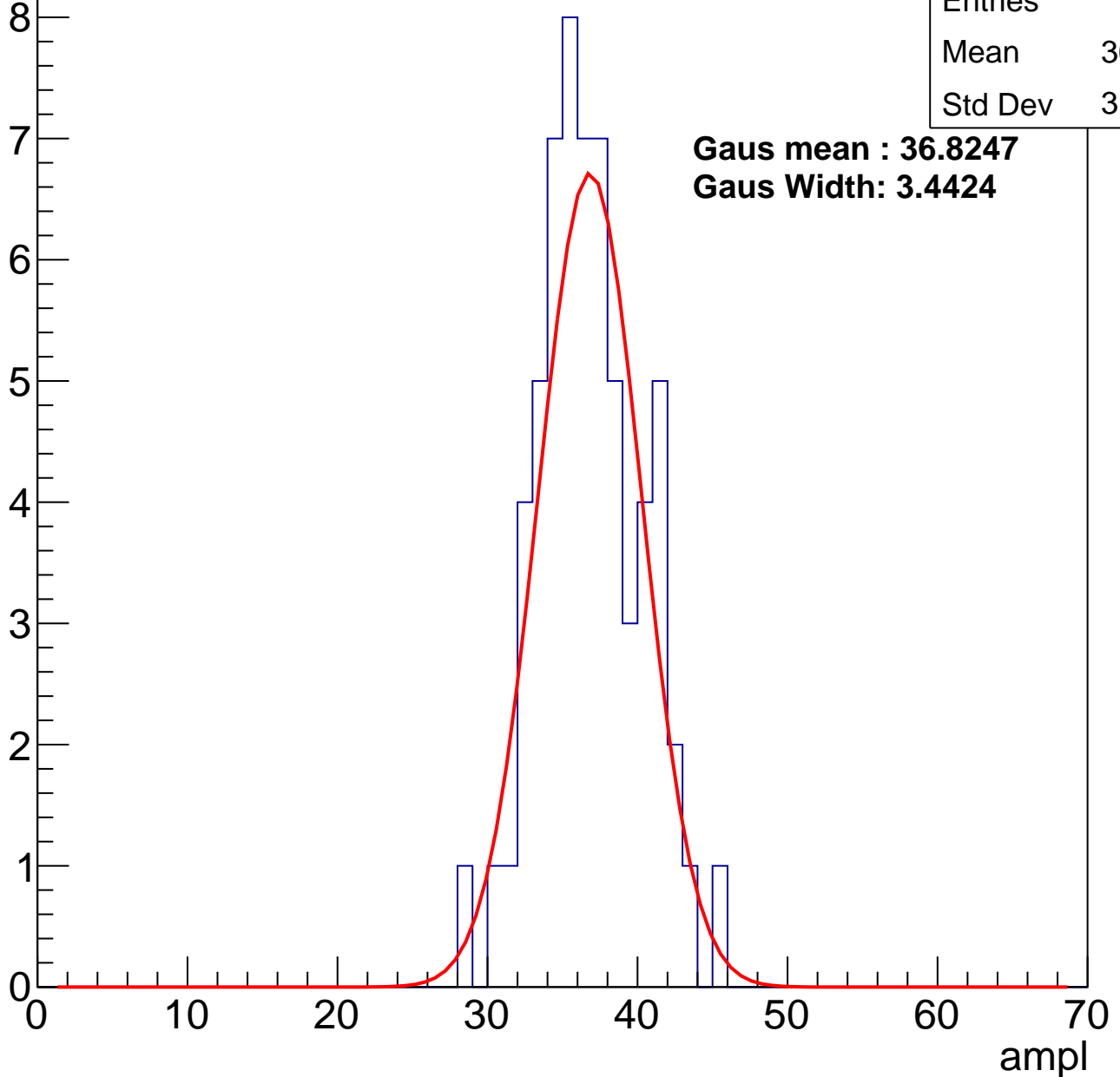
B0L002S, U9-ch110, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.37
Std Dev	3.376

Gaus mean : 36.8247
Gaus Width: 3.4424



B0L002S, U9-ch110, adc2

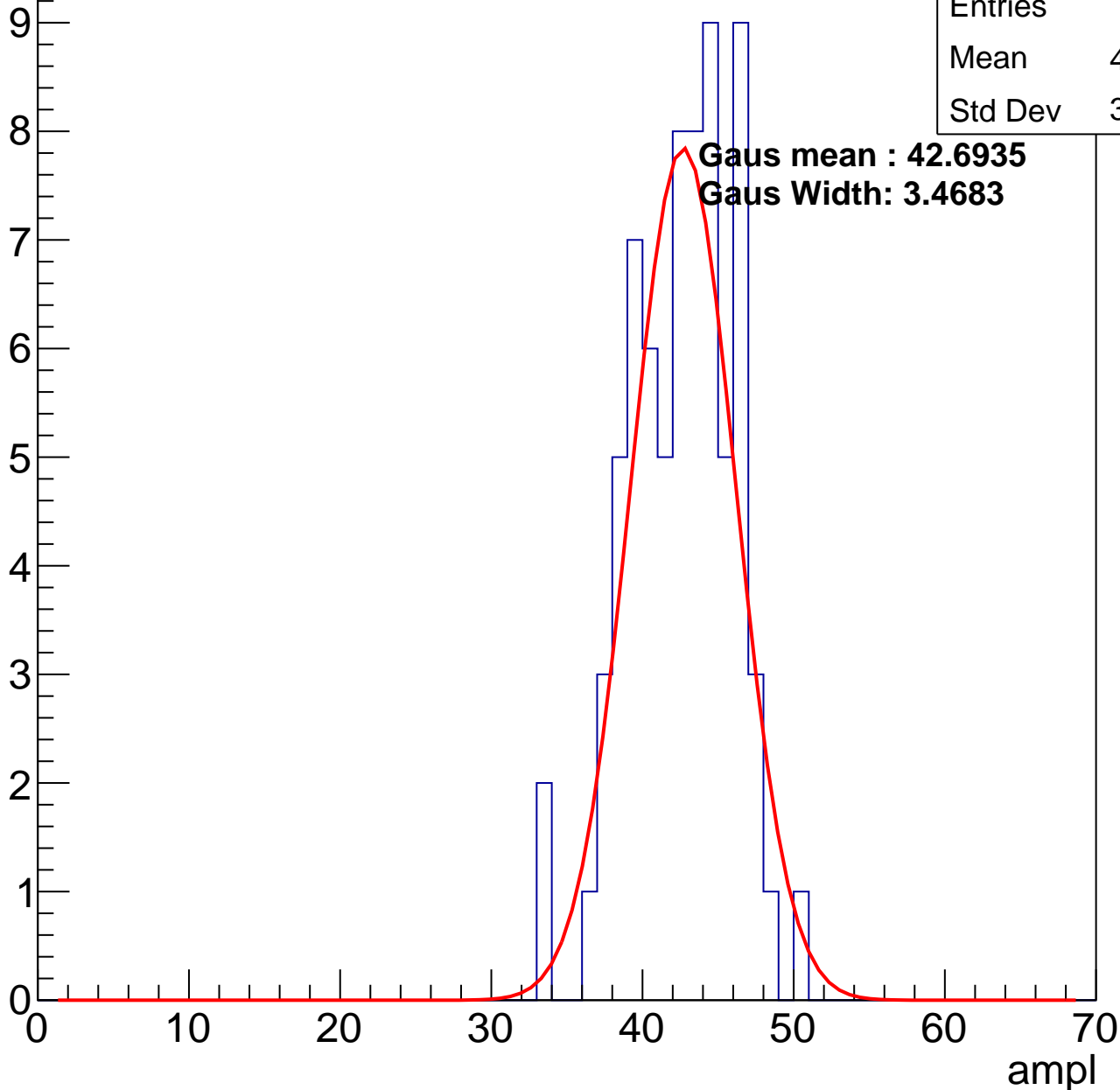
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.12
Std Dev	3.416

Gaus mean : 42.6935

Gaus Width: 3.4683

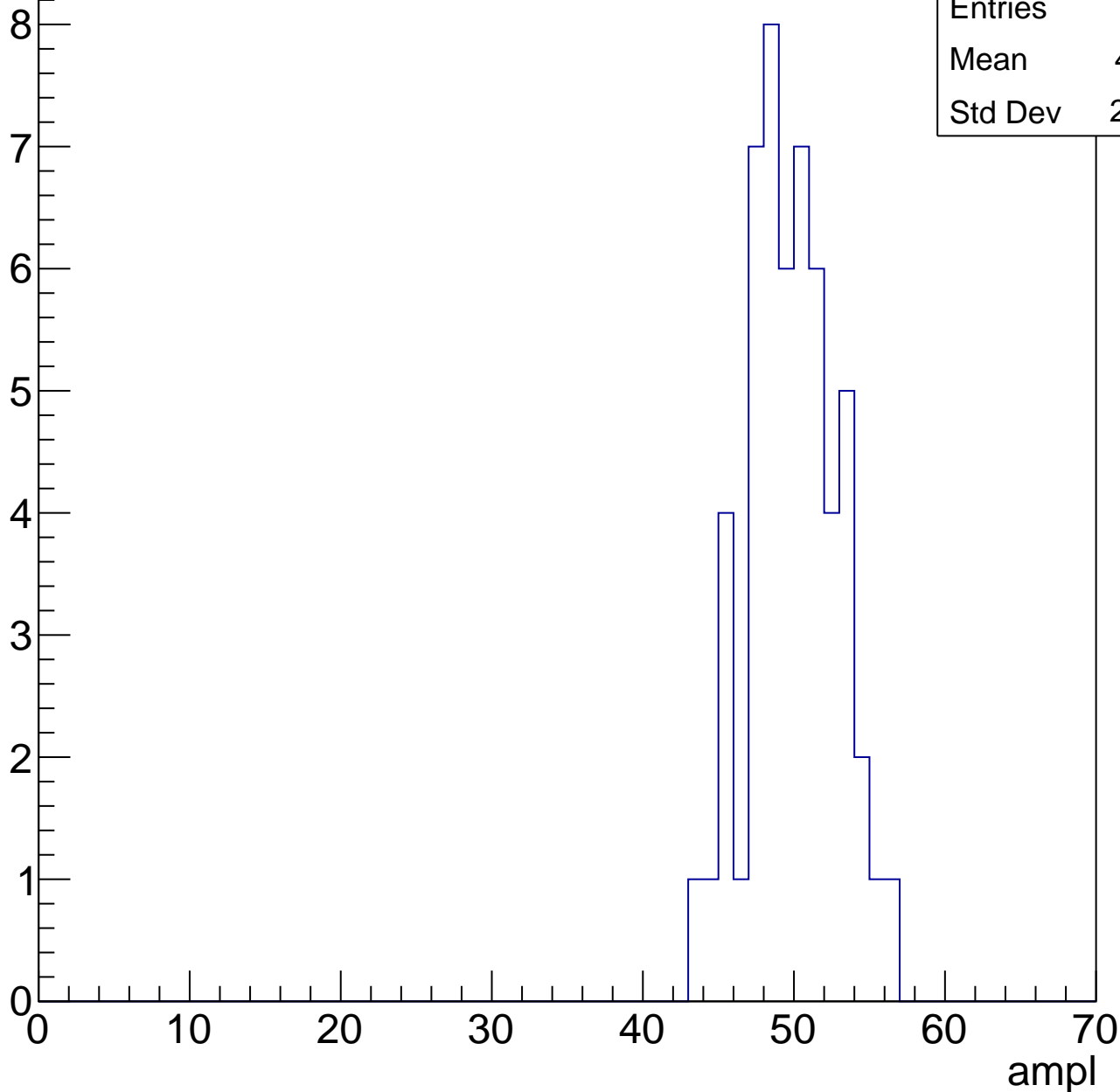


B0L002S, U9-ch110, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	49.41
Std Dev	2.864

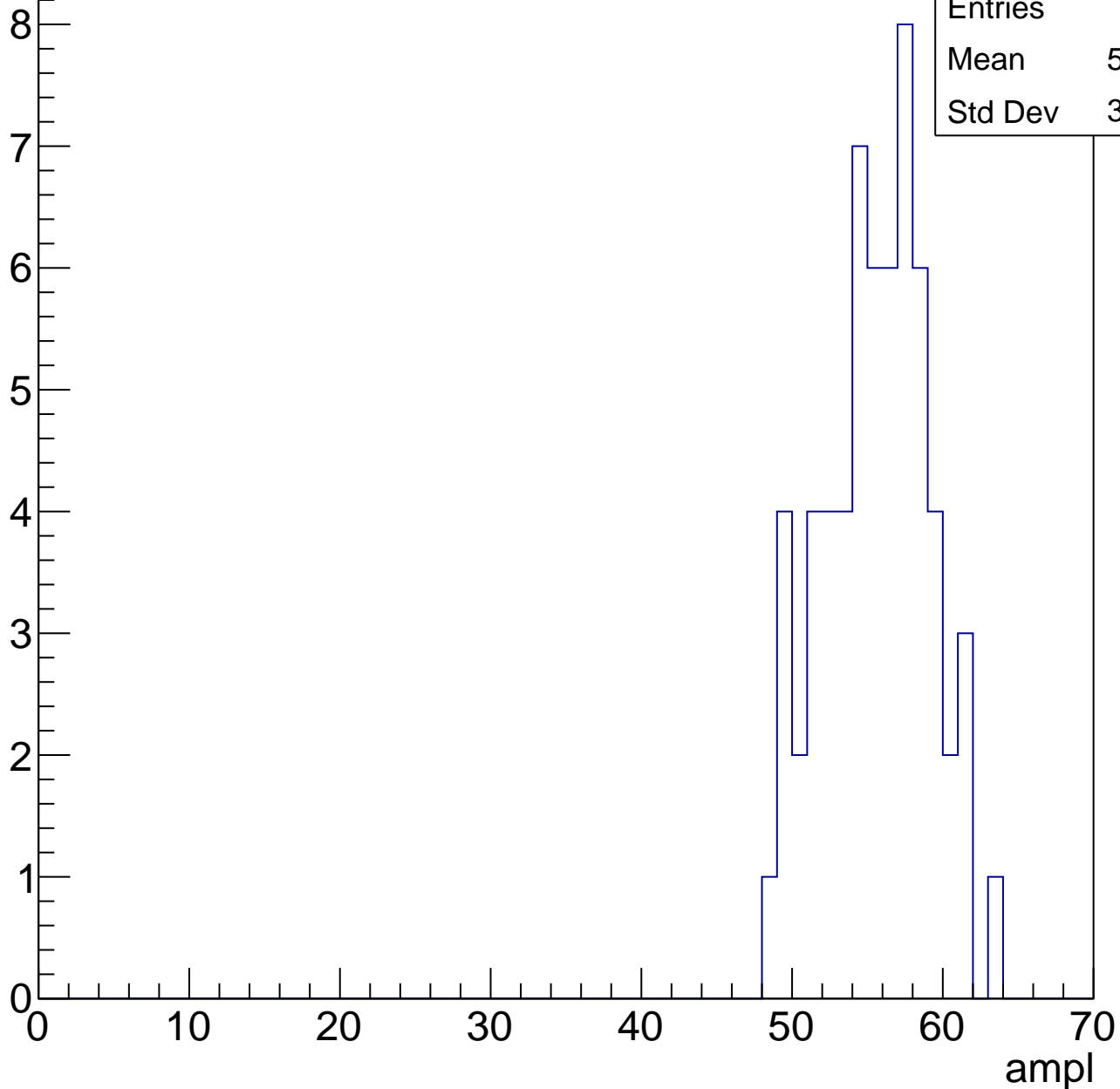


B0L002S, U9-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.13
Std Dev	3.462

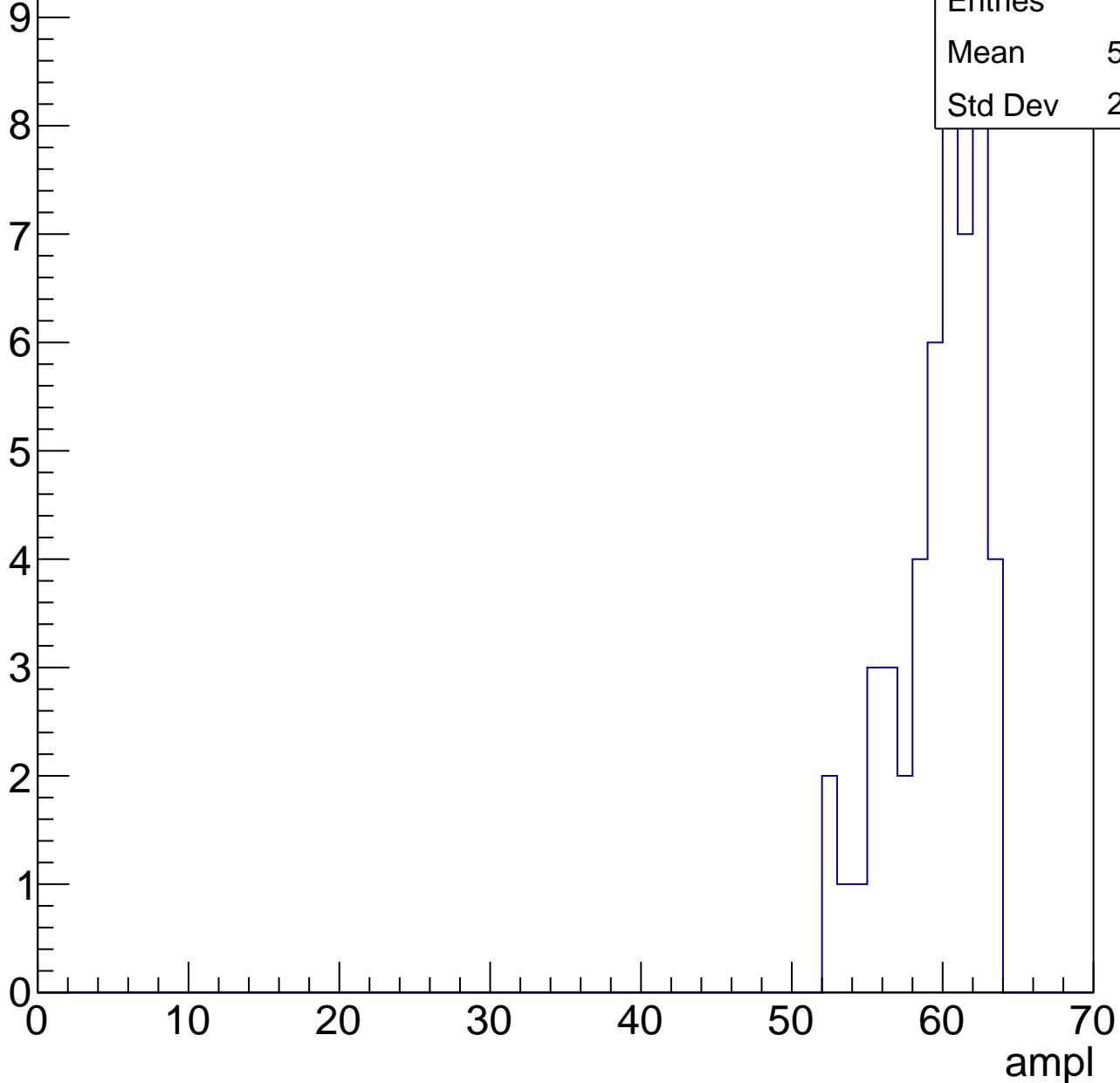


B0L002S, U9-ch110, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	59.22
Std Dev	2.914

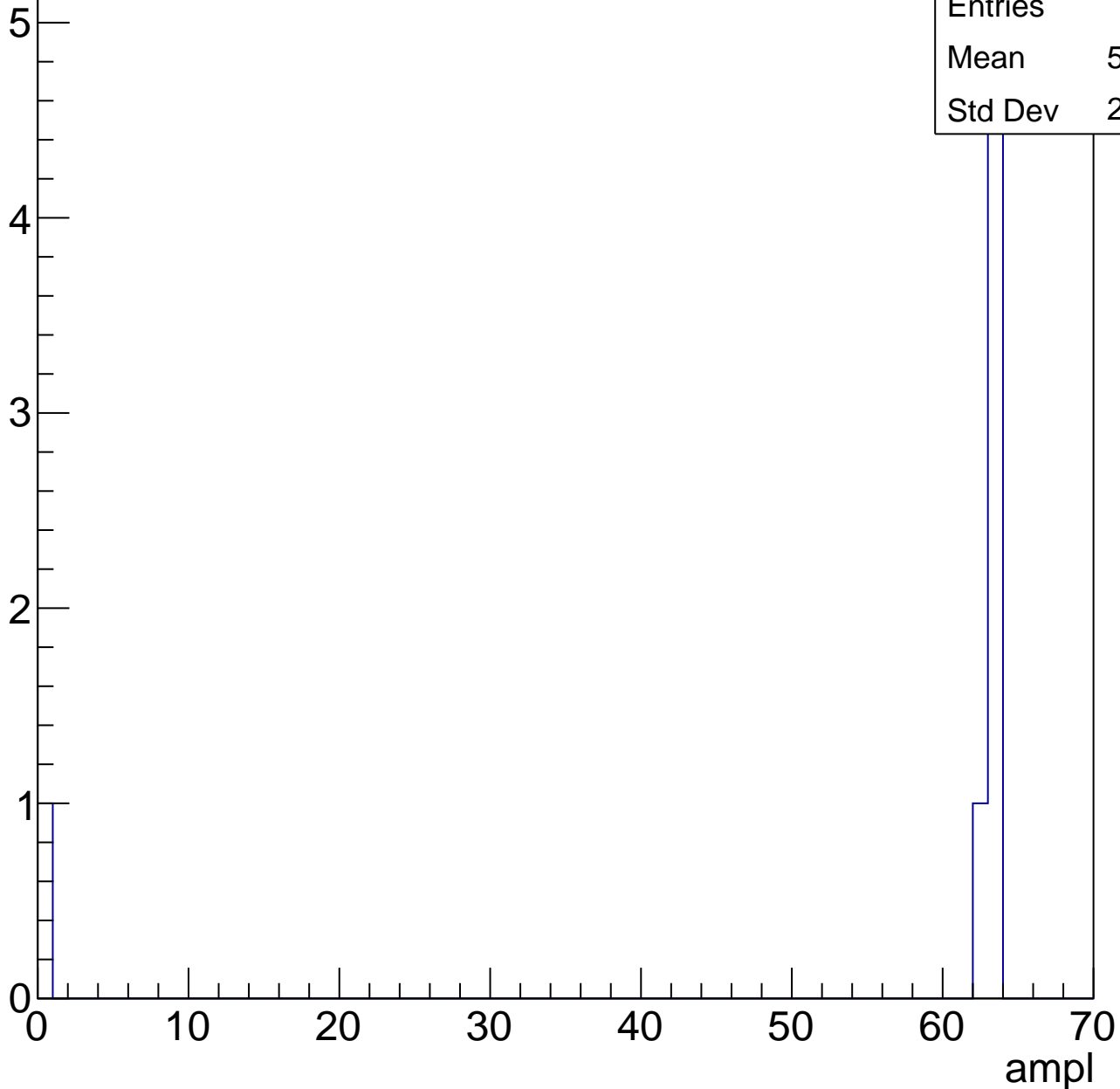


B0L002S, U9-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	7
Mean	53.86
Std Dev	21.99



B0L002S, U9-ch110, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch111, adc0

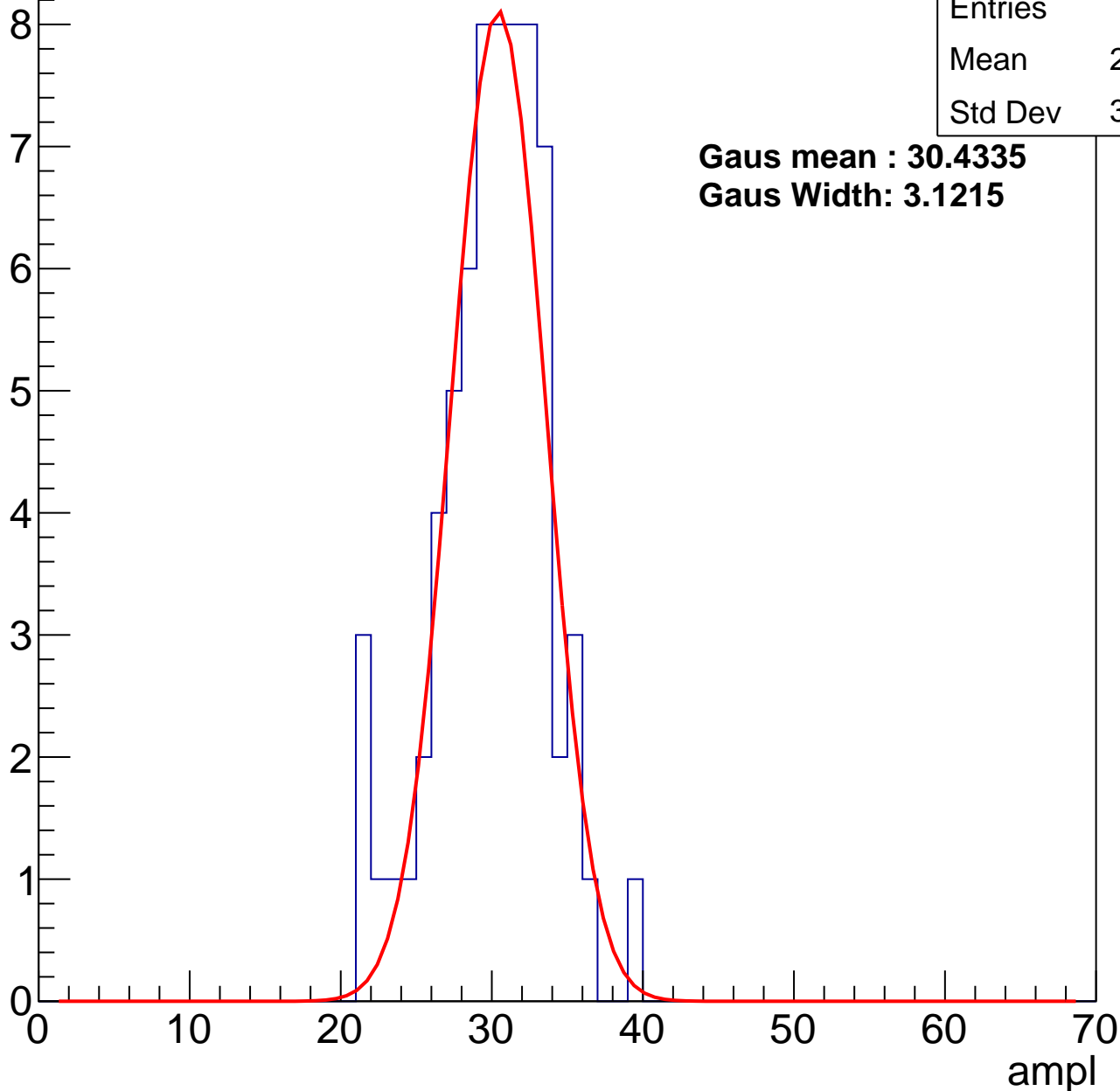
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.62
Std Dev	3.612

Gaus mean : 30.4335

Gaus Width: 3.1215



B0L002S, U9-ch111, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	37.19
Std Dev	2.91

Gaus mean : 37.8183

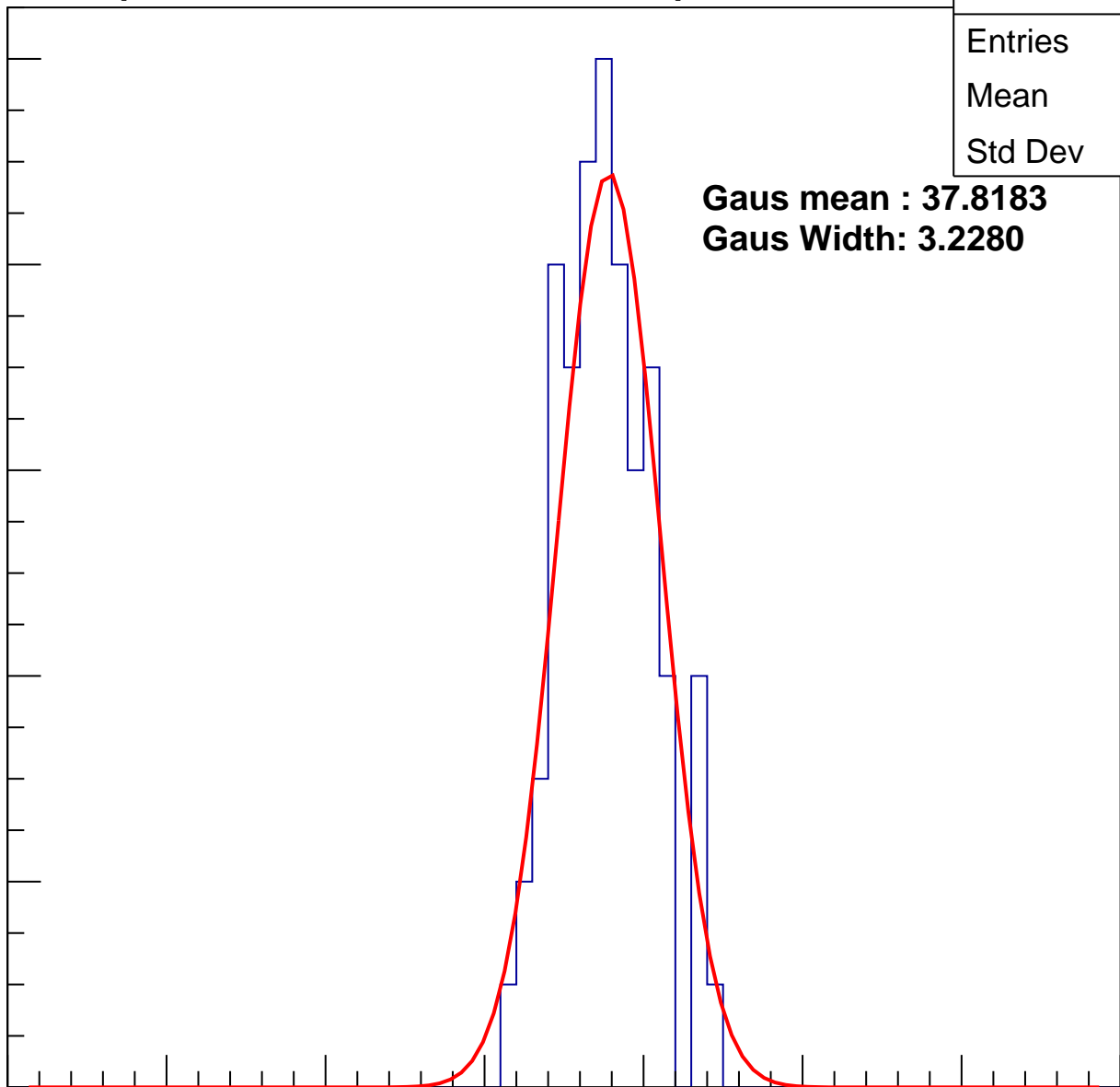
Gaus Width: 3.2280

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch111, adc2

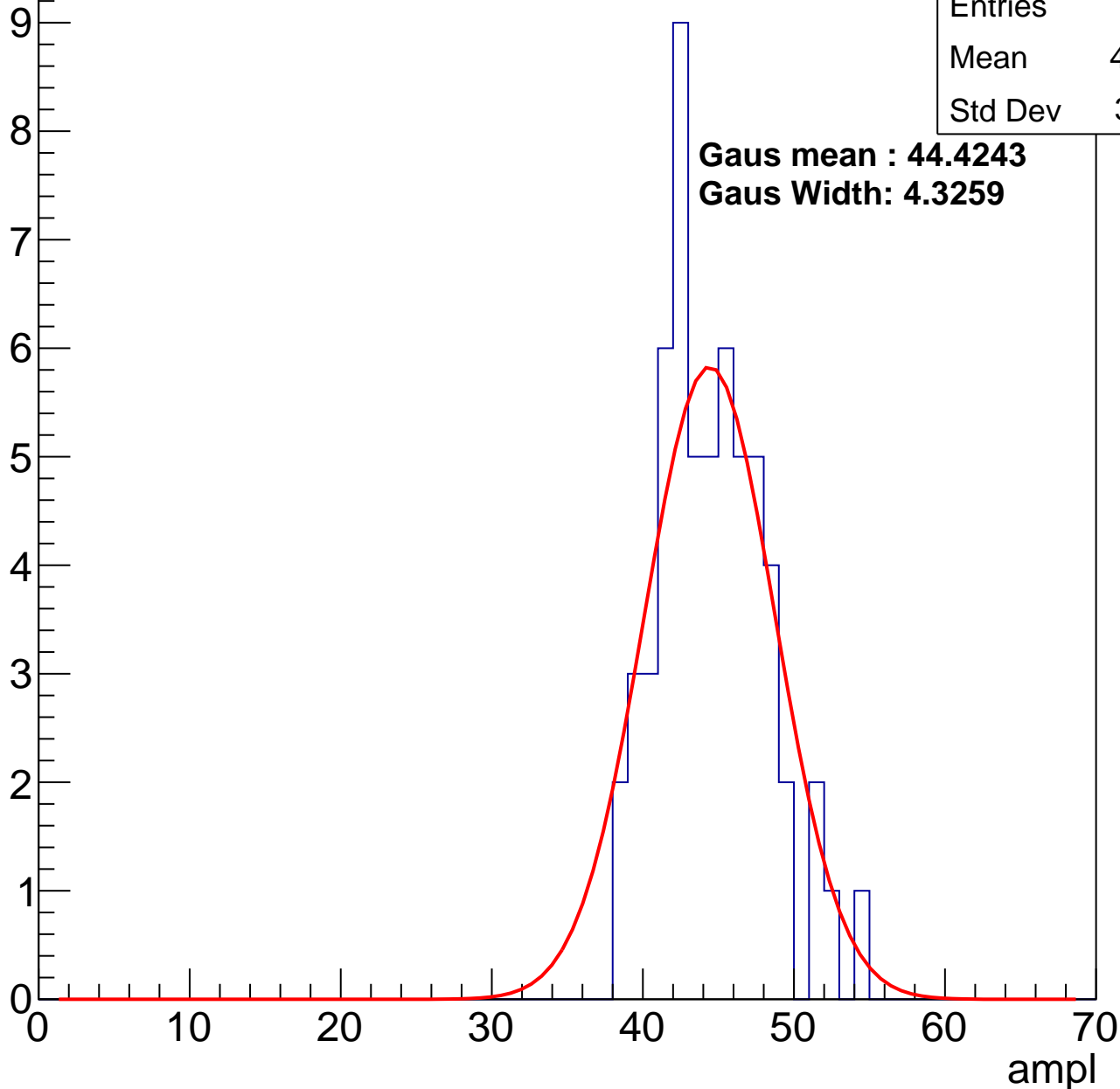
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44.15
Std Dev	3.531

Gaus mean : 44.4243

Gaus Width: 4.3259



B0L002S, U9-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

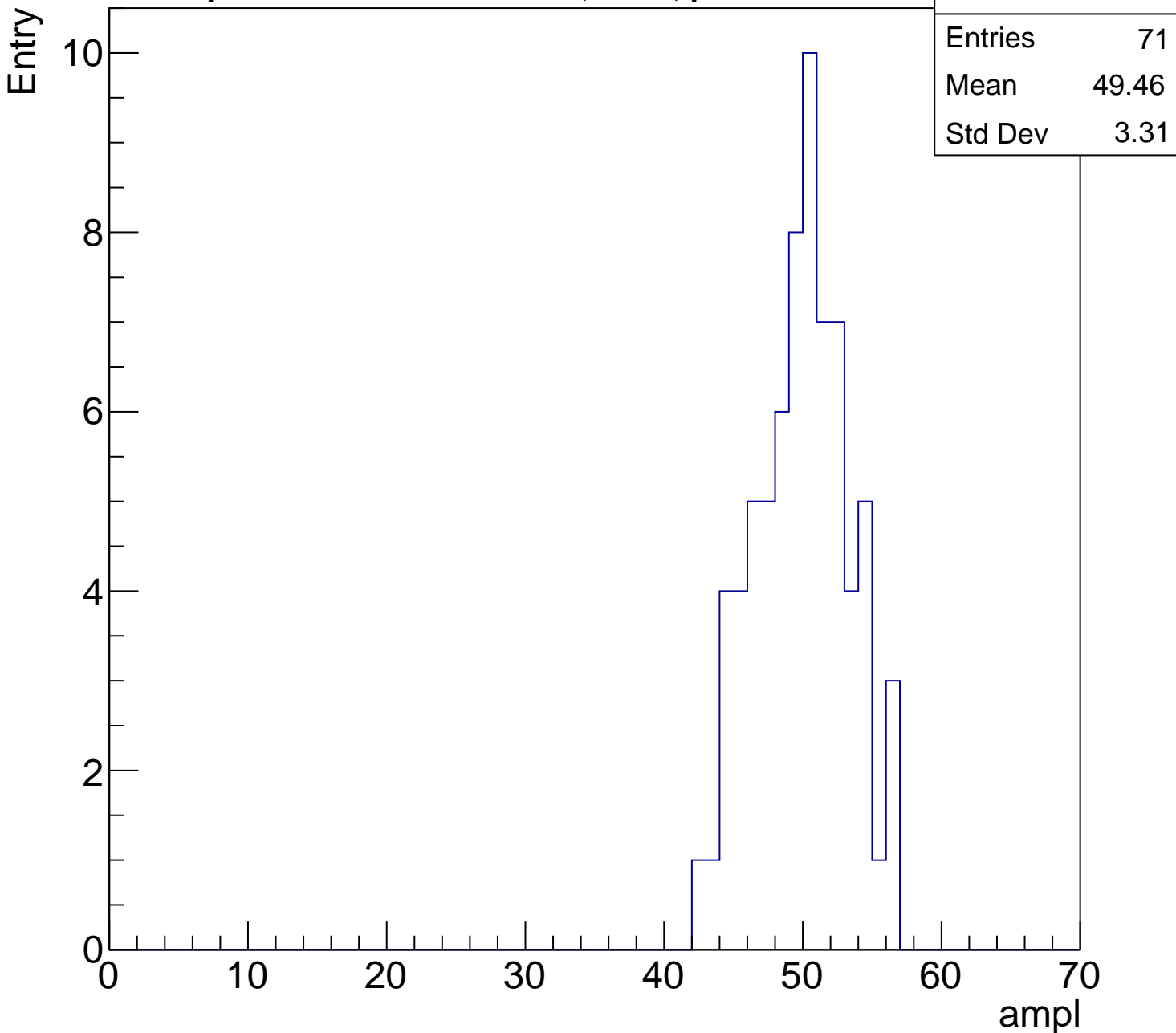
Entries	71
Mean	49.46
Std Dev	3.31

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

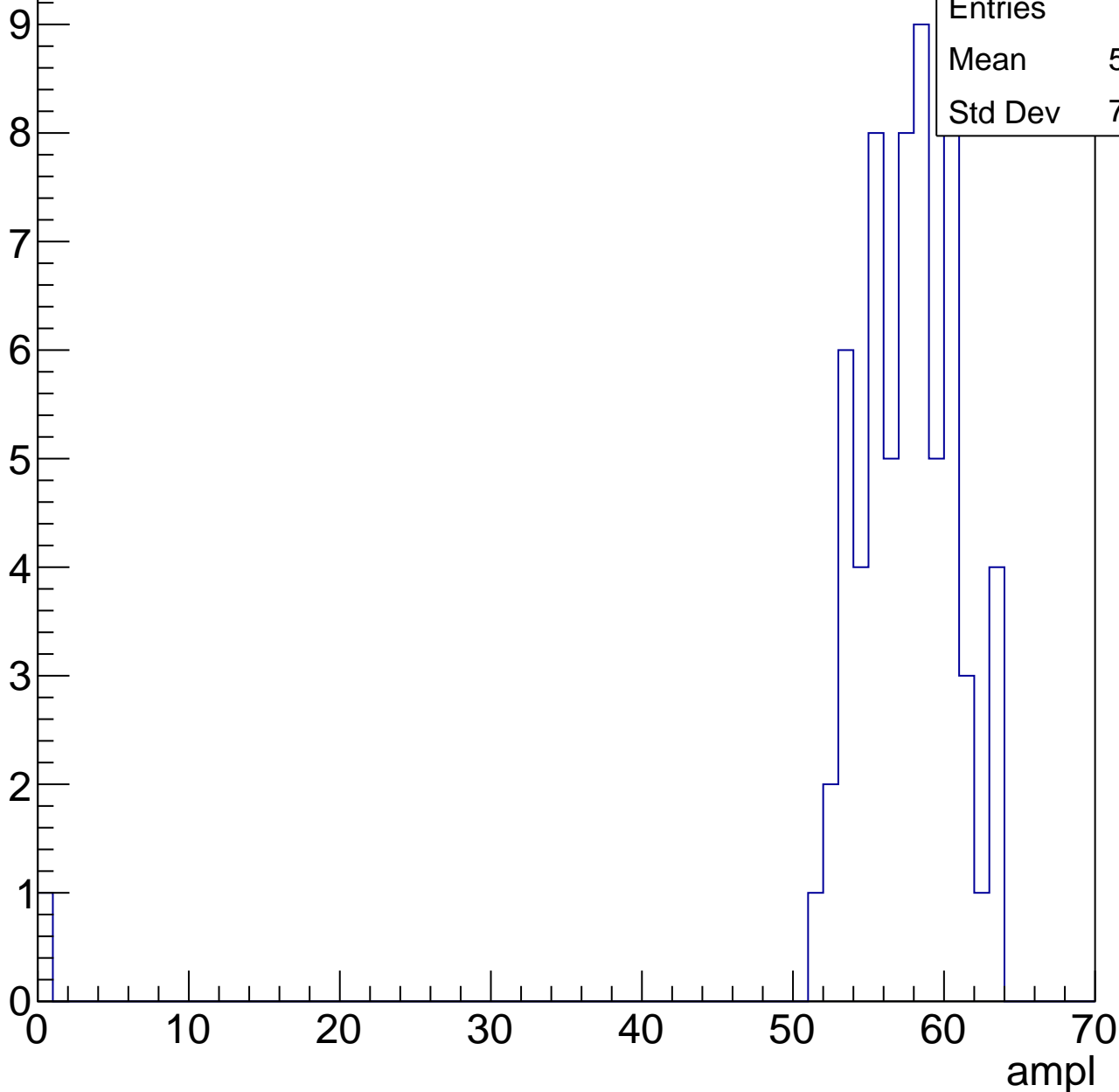


B0L002S, U9-ch111, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	56.29
Std Dev	7.635

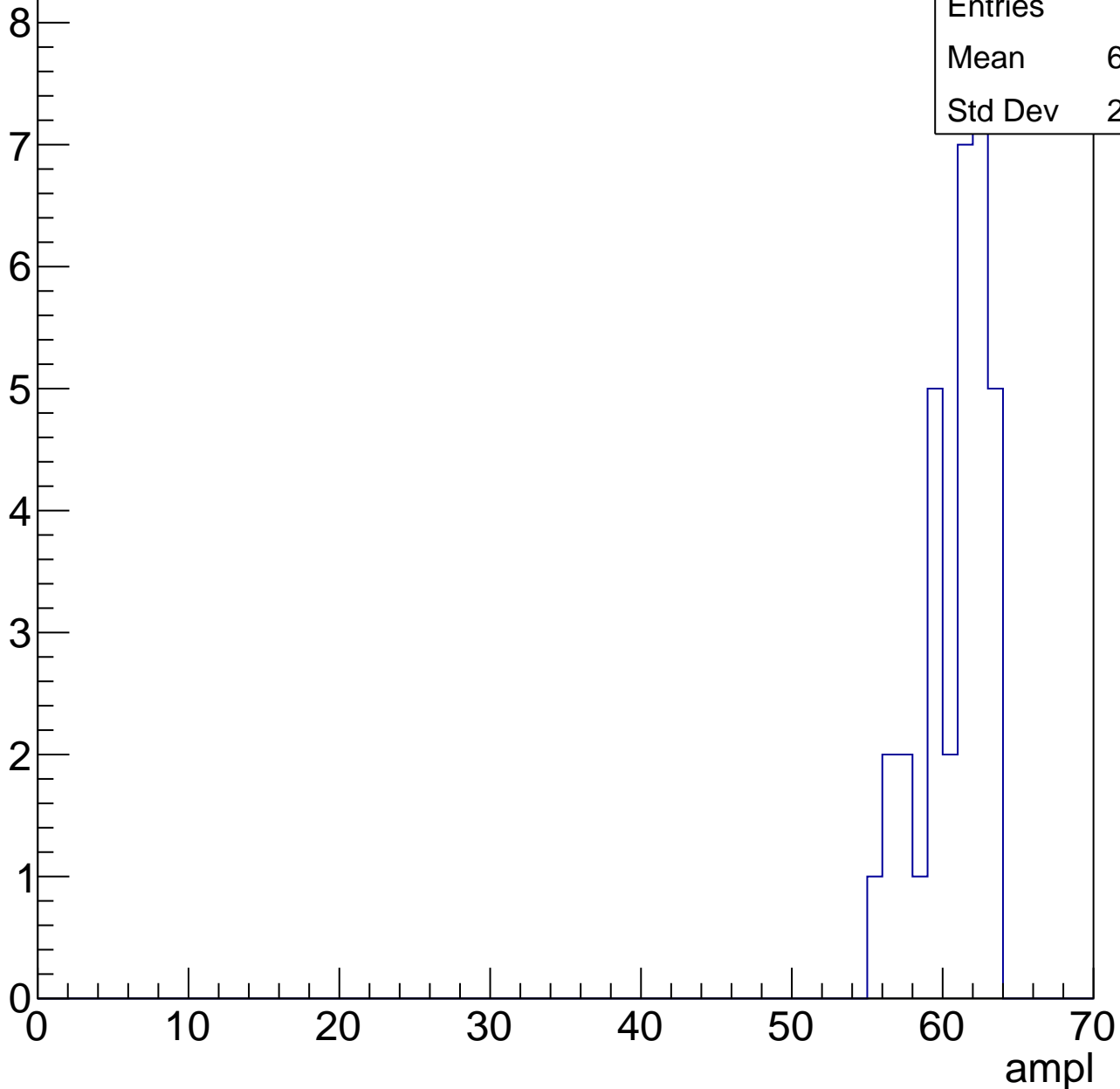


B0L002S, U9-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	60.36
Std Dev	2.227



B0L002S, U9-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch112, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	29.84
Std Dev	3.132

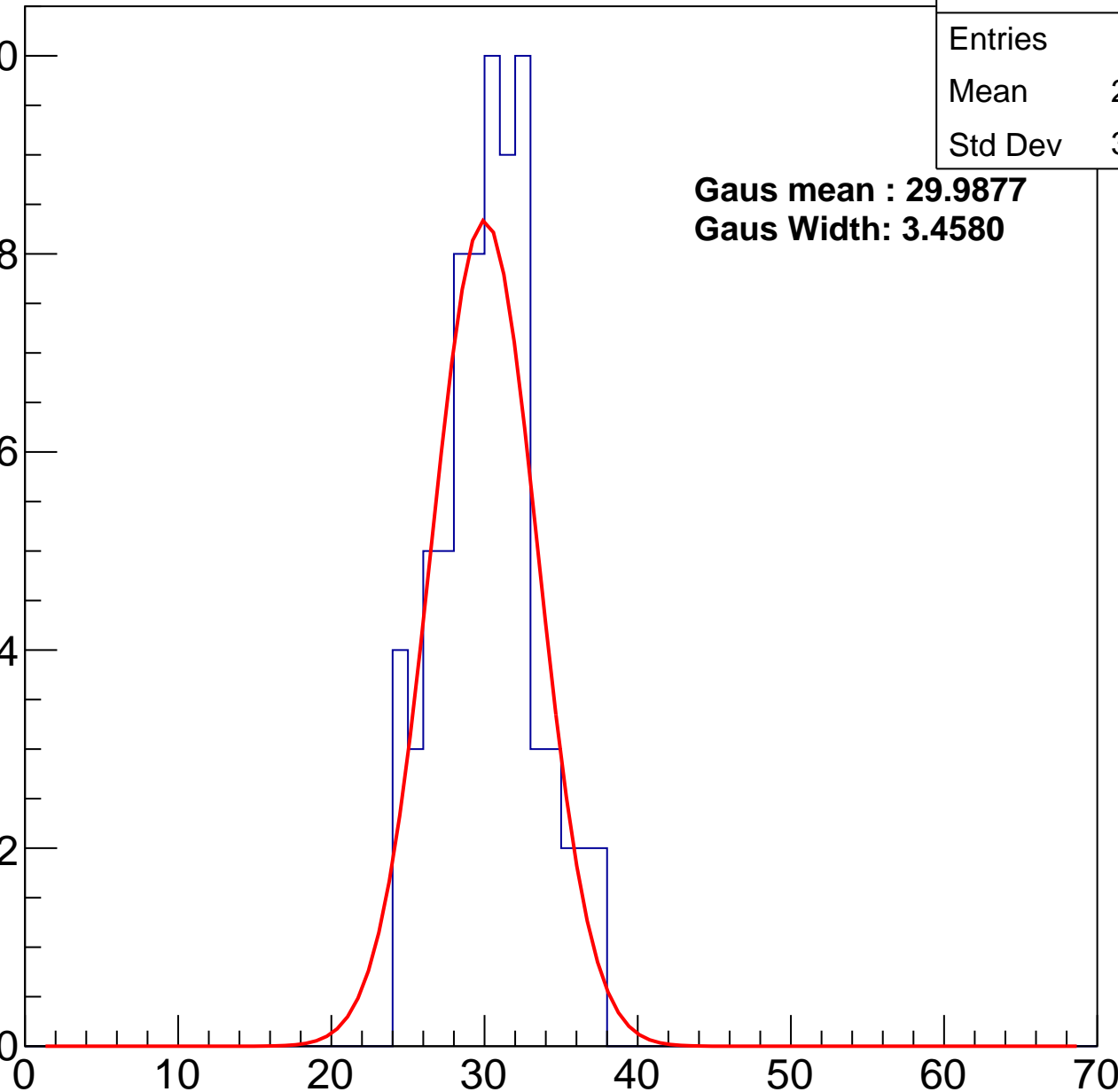
Gaus mean : 29.9877

Gaus Width: 3.4580

Entry

10
8
6
4
2
0

ampl



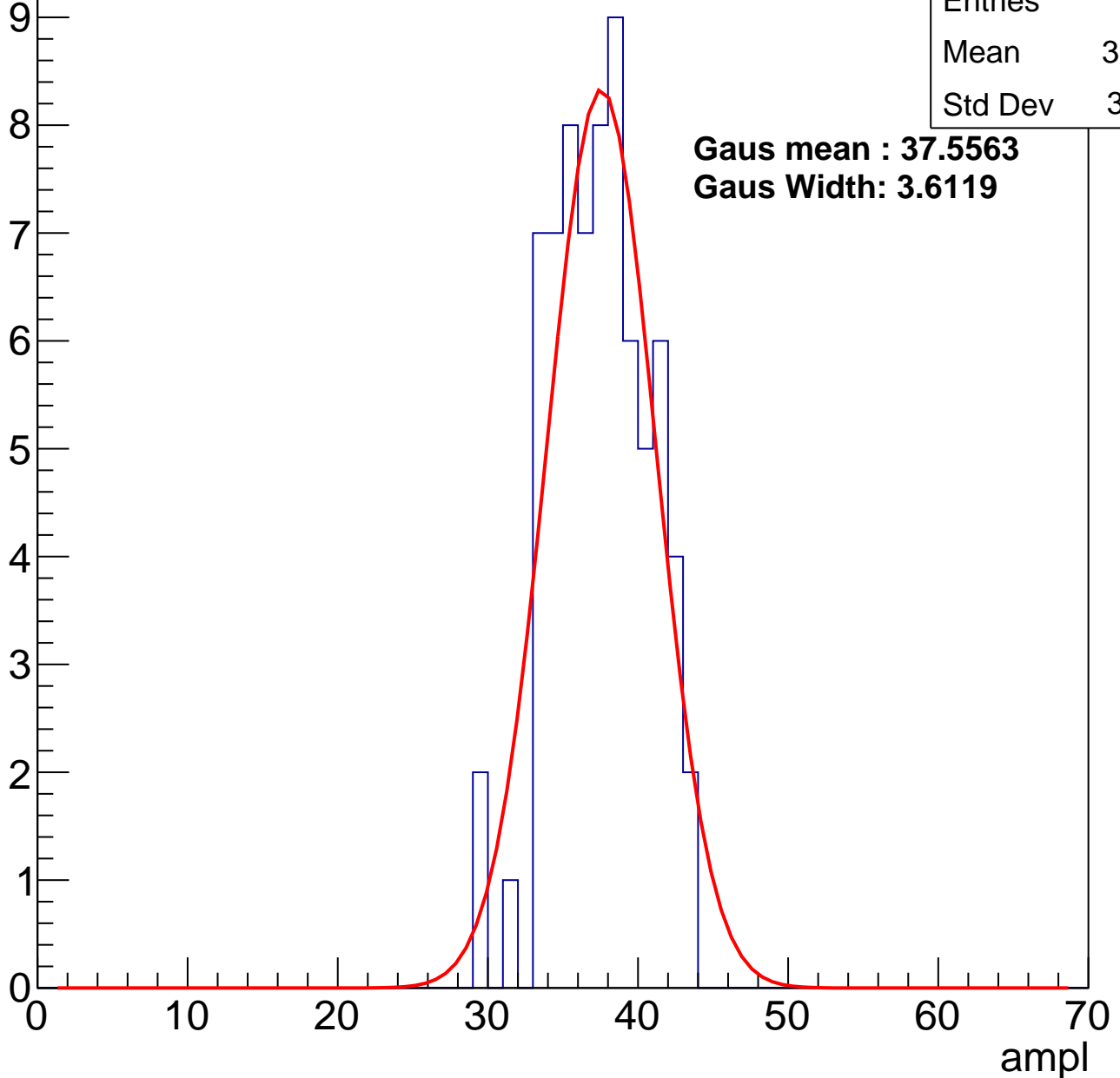
B0L002S, U9-ch112, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.97
Std Dev	3.171

Gaus mean : 37.5563
Gaus Width: 3.6119



B0L002S, U9-ch112, adc2

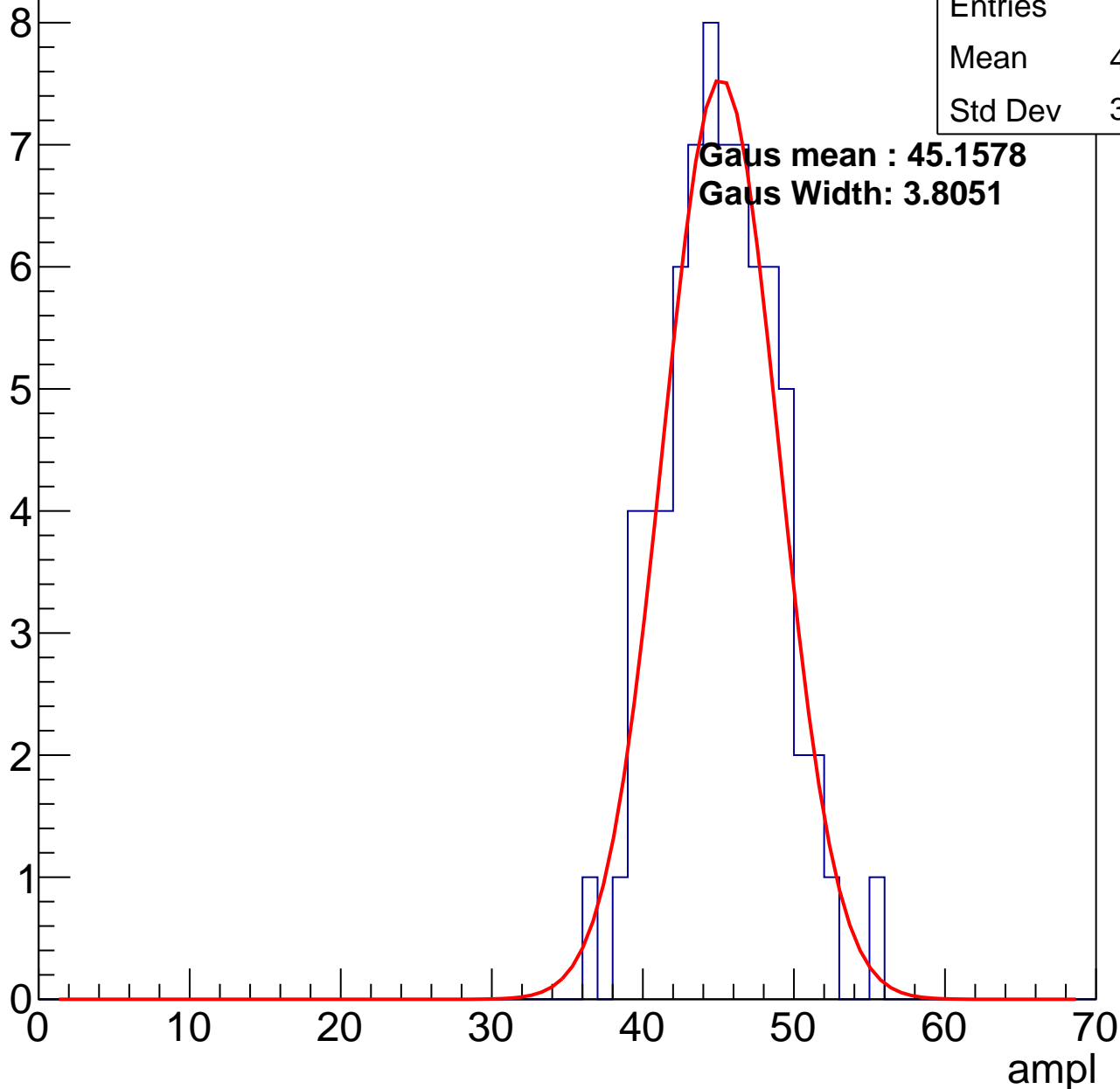
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.72
Std Dev	3.649

Gaus mean : 45.1578

Gaus Width: 3.8051

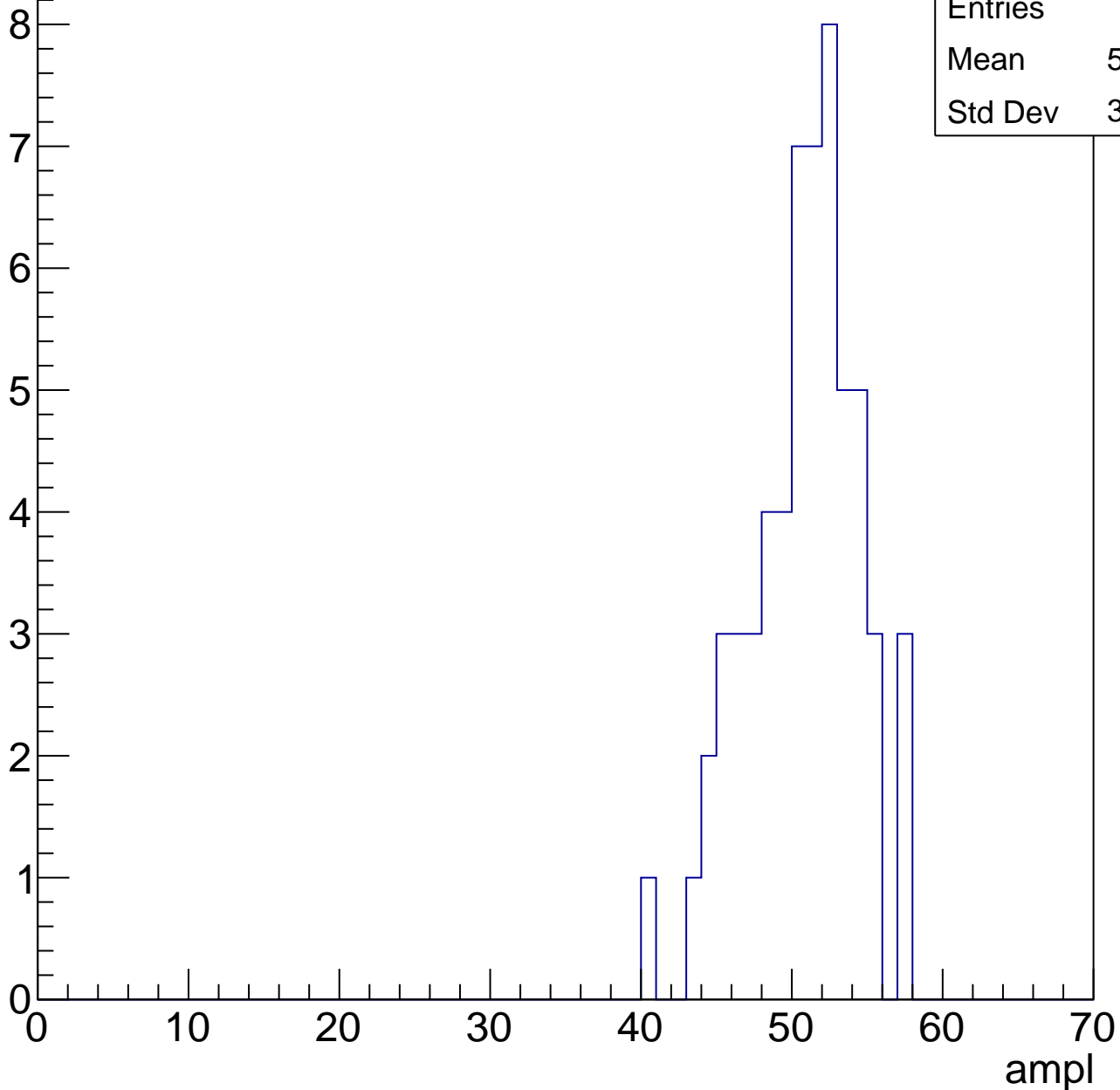


B0L002S, U9-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.29
Std Dev	3.618

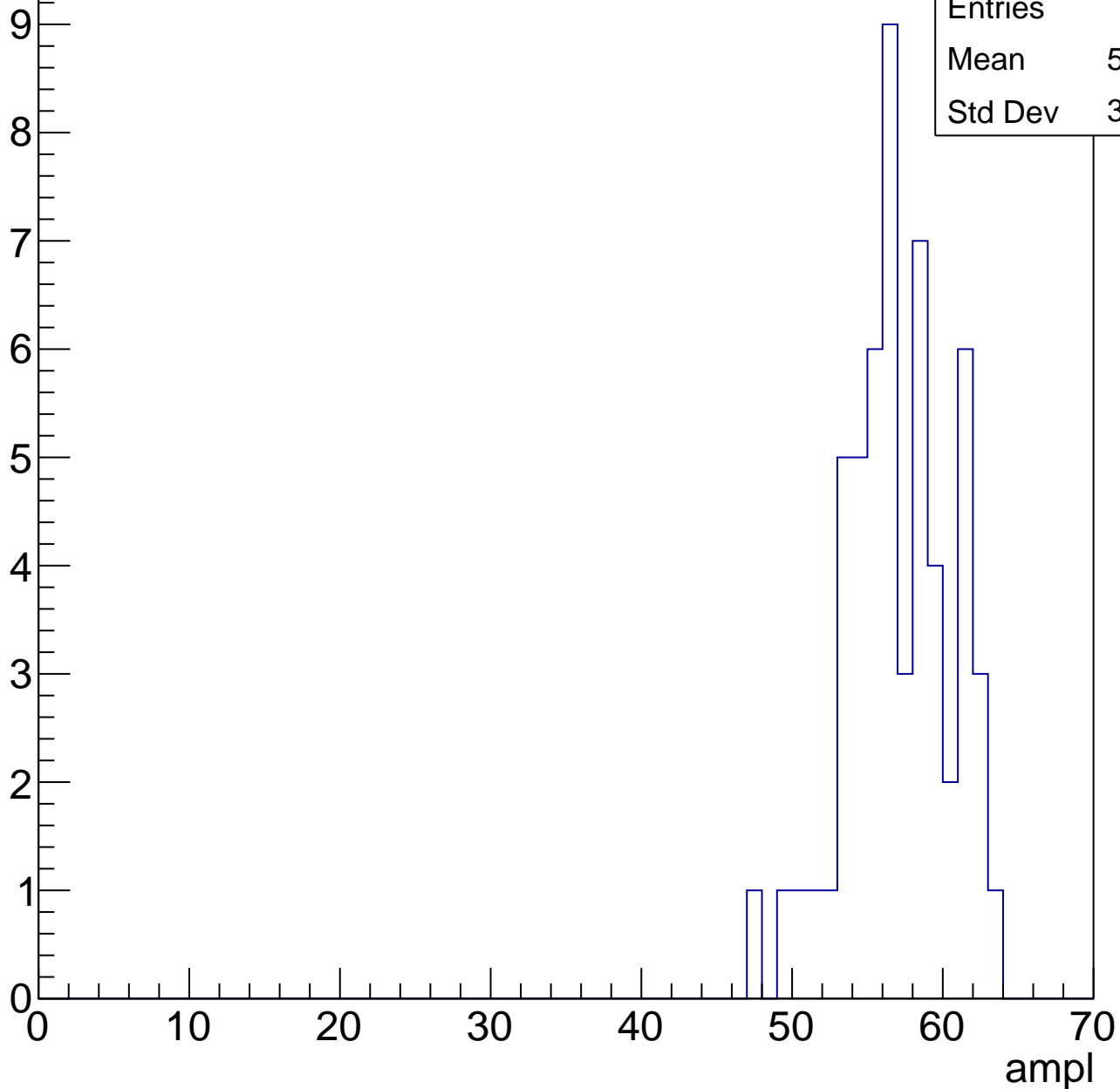


B0L002S, U9-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

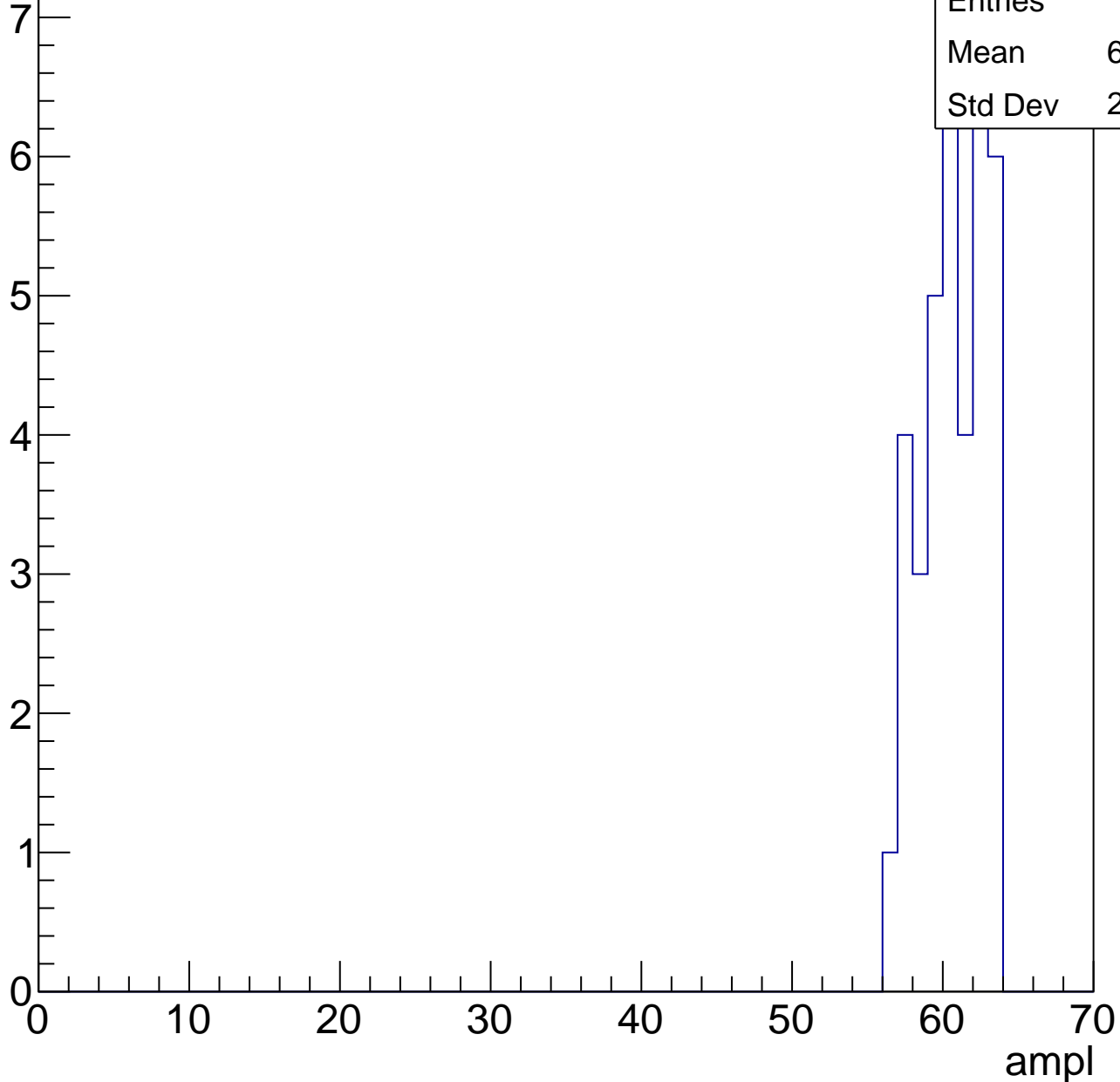
Entries	56
Mean	56.54
Std Dev	3.454



B0L002S, U9-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

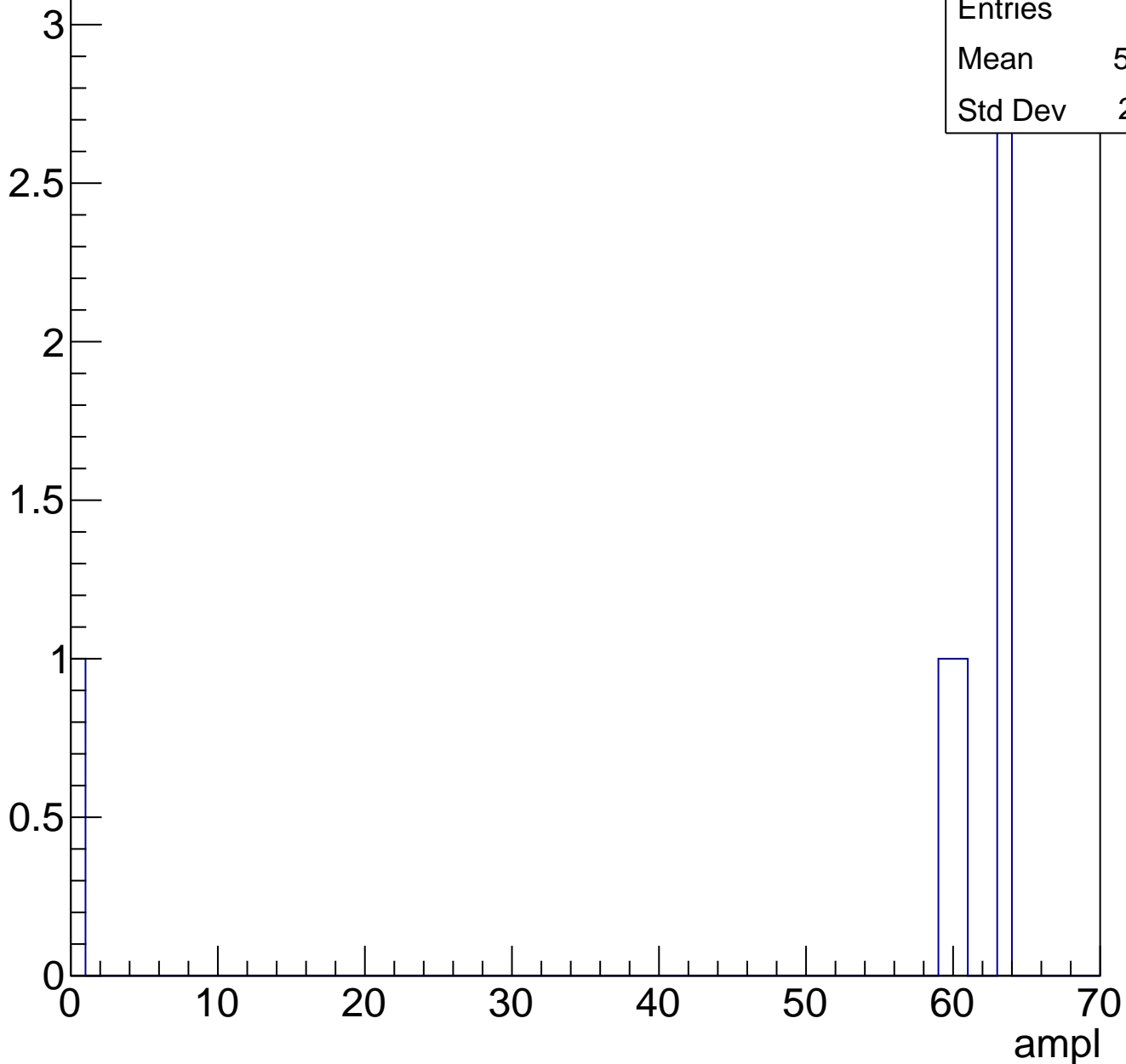


Entries	37
Mean	60.24
Std Dev	2.032

B0L002S, U9-ch112, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L002S, U9-ch113, adc0

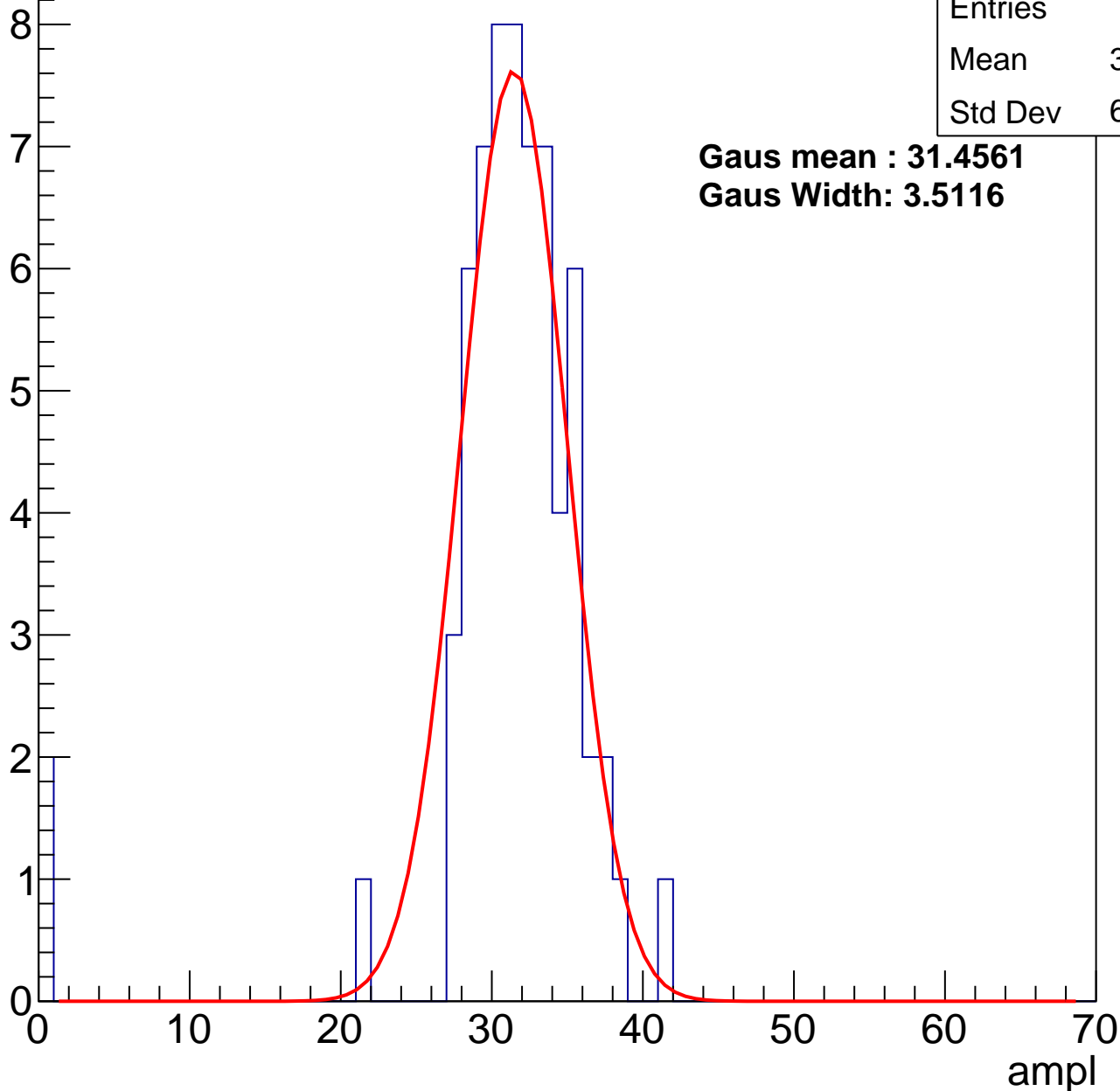
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.57
Std Dev	6.307

Gaus mean : 31.4561

Gaus Width: 3.5116



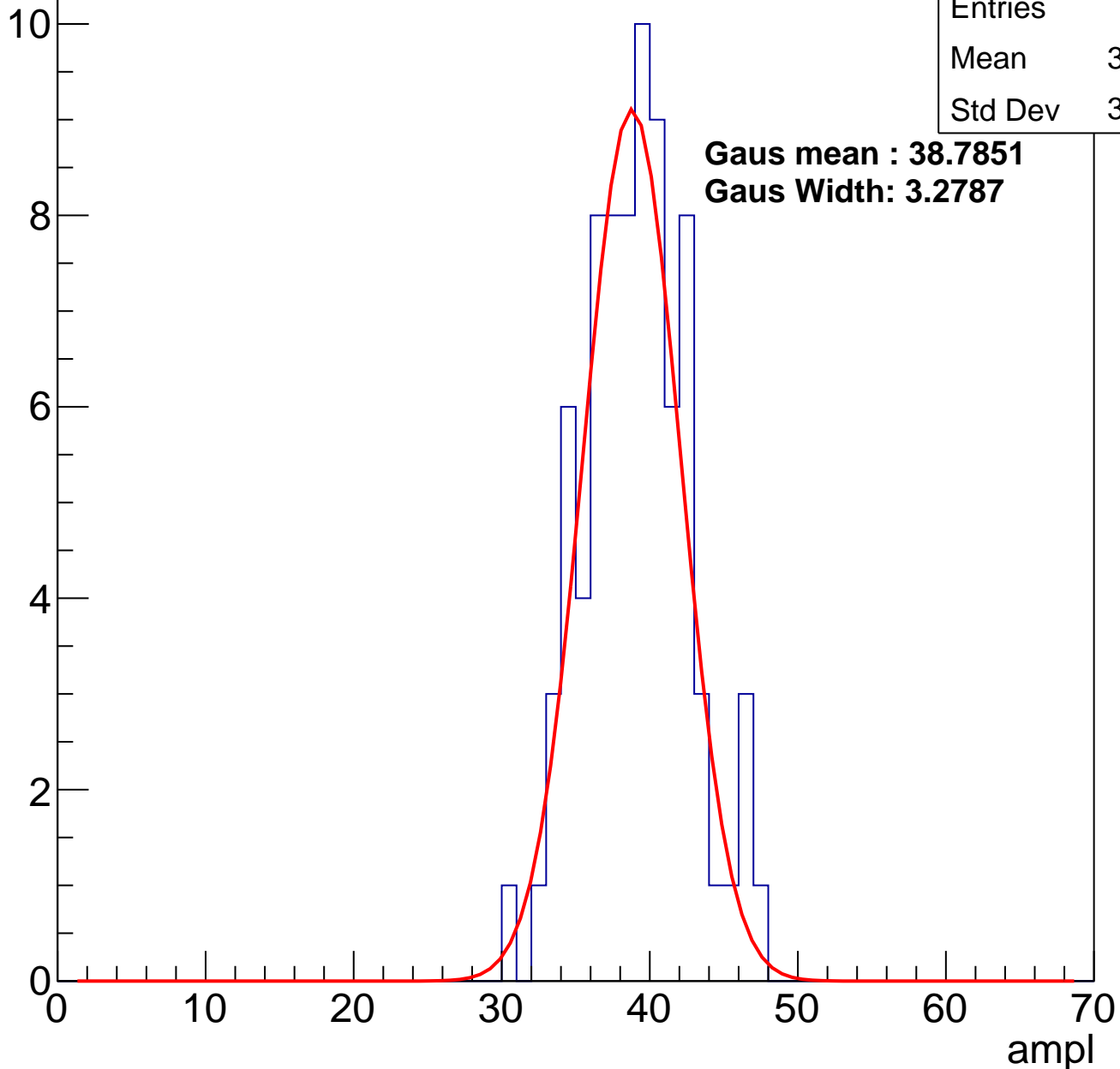
B0L002S, U9-ch113, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	38.62
Std Dev	3.473

Gaus mean : 38.7851
Gaus Width: 3.2787

Entry



B0L002S, U9-ch113, adc2

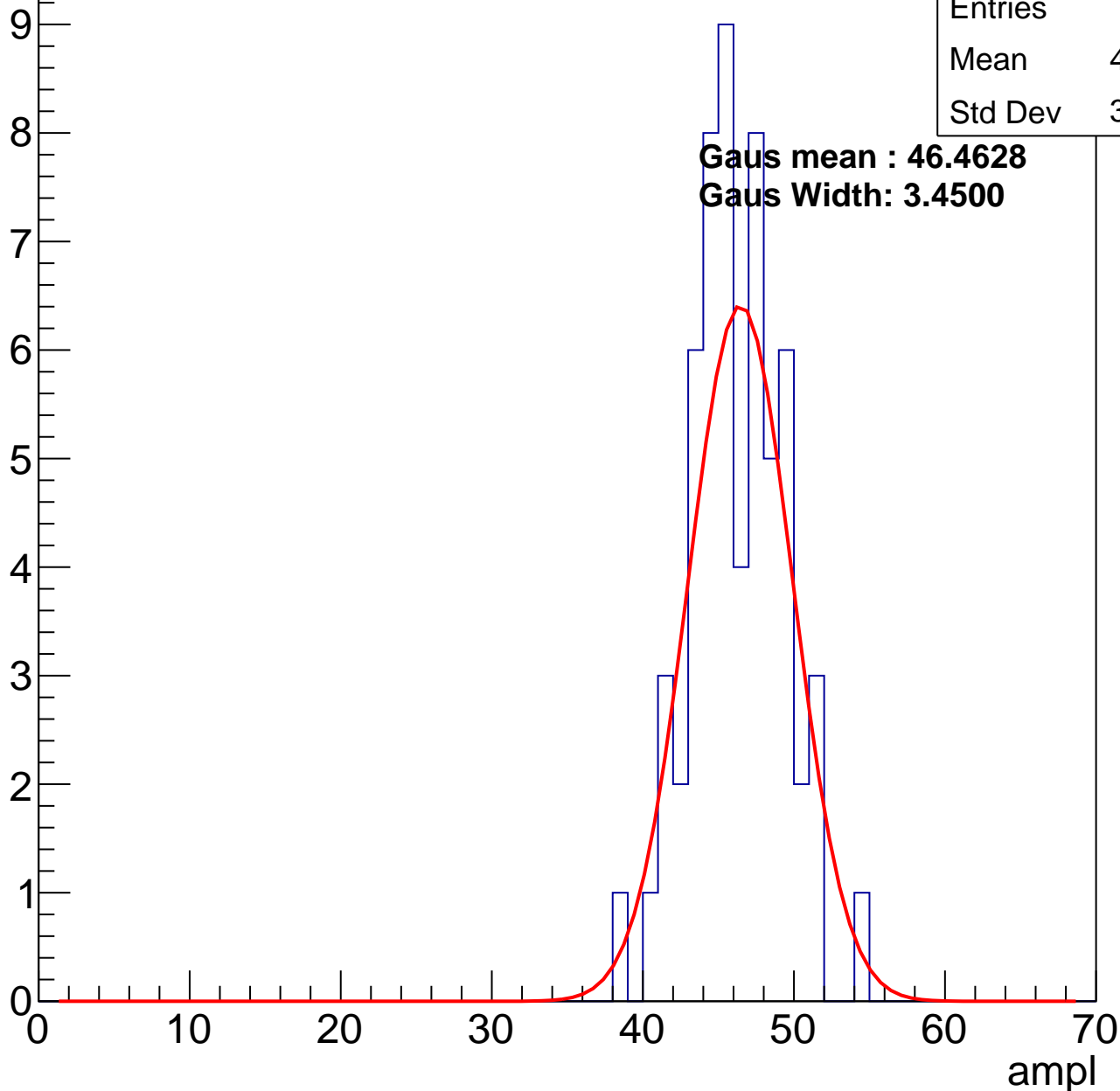
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	45.78
Std Dev	3.059

Gaus mean : 46.4628

Gaus Width: 3.4500

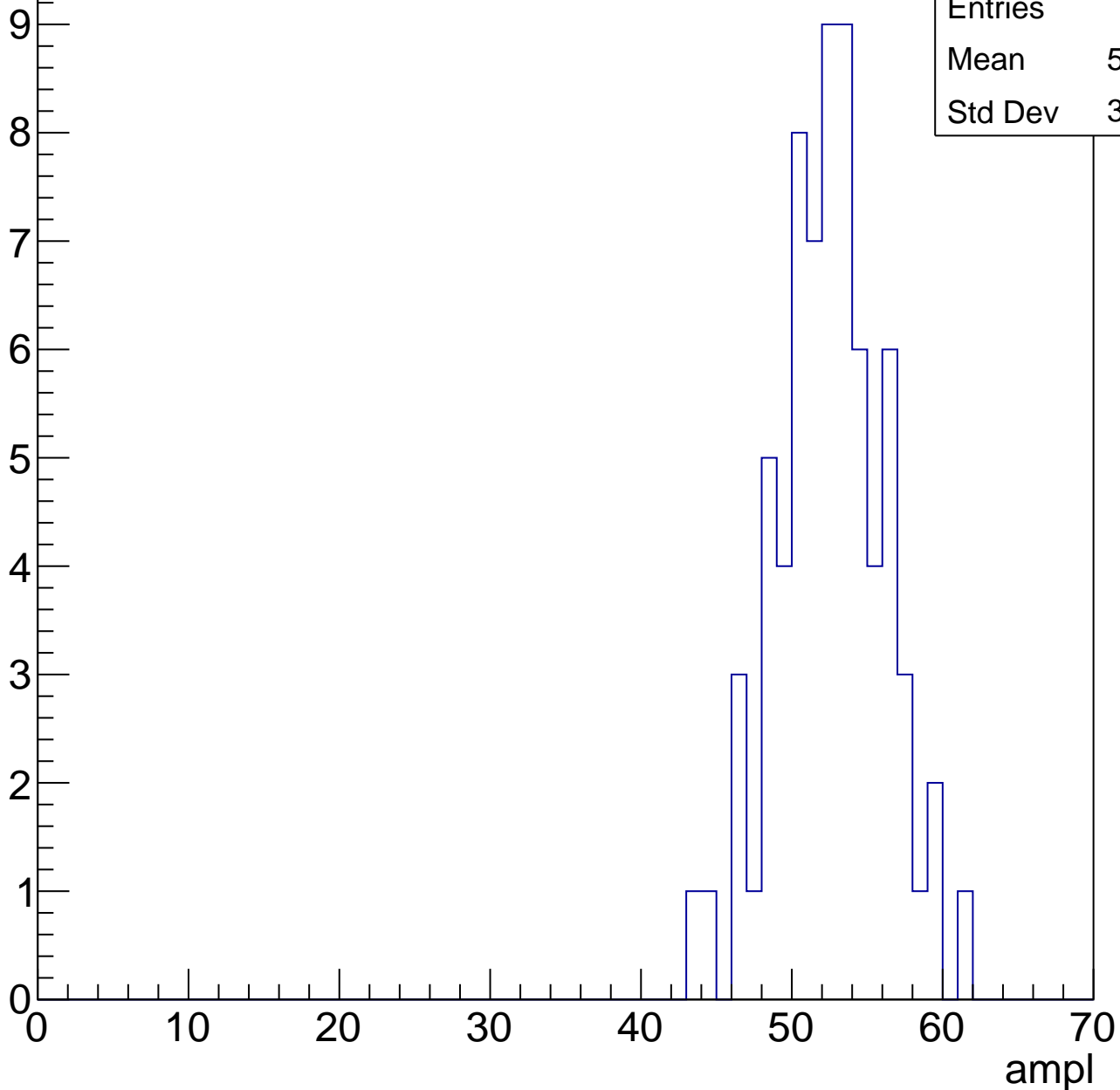


B0L002S, U9-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	52.08
Std Dev	3.536

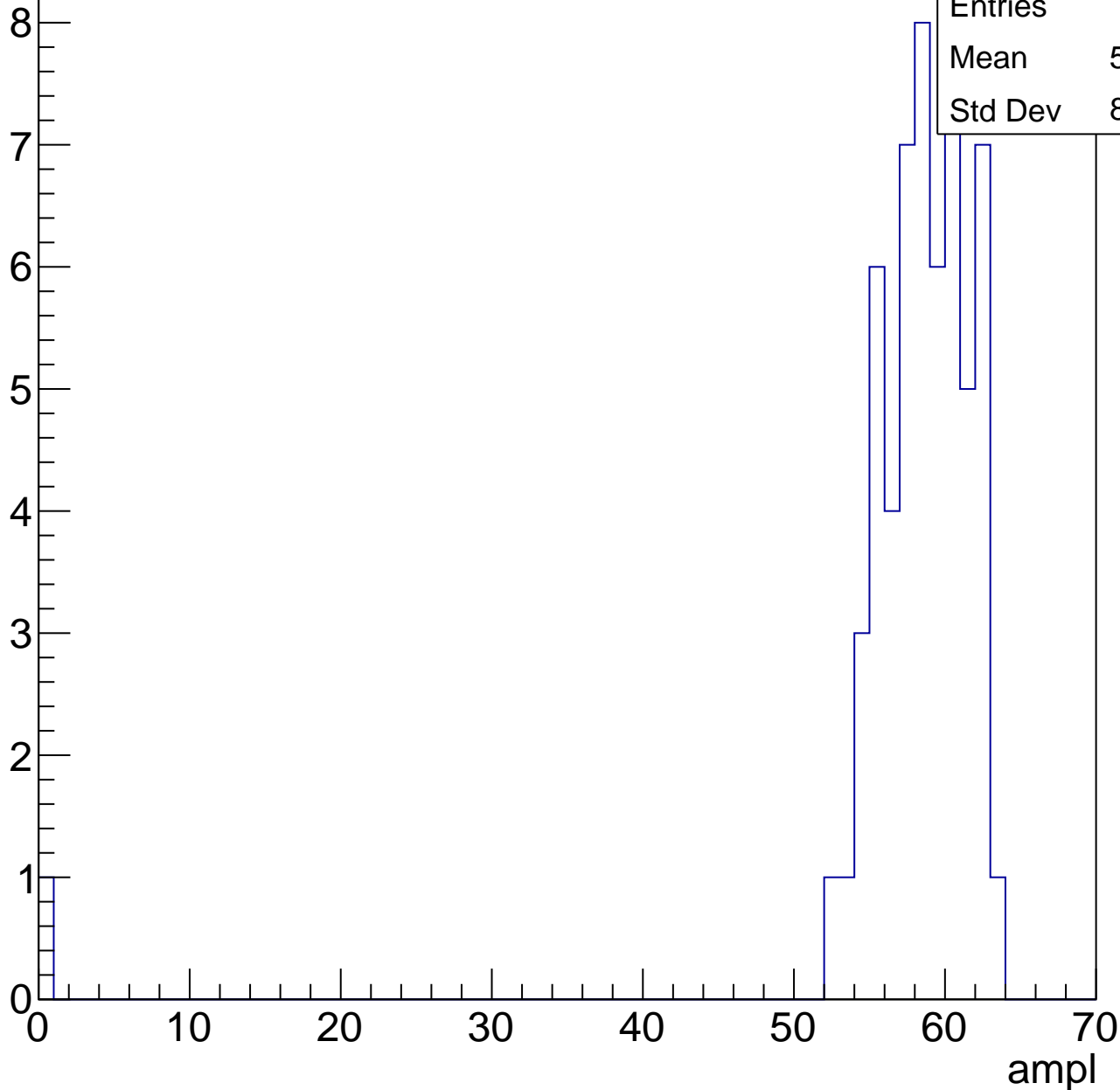


B0L002S, U9-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	57.24
Std Dev	8.026

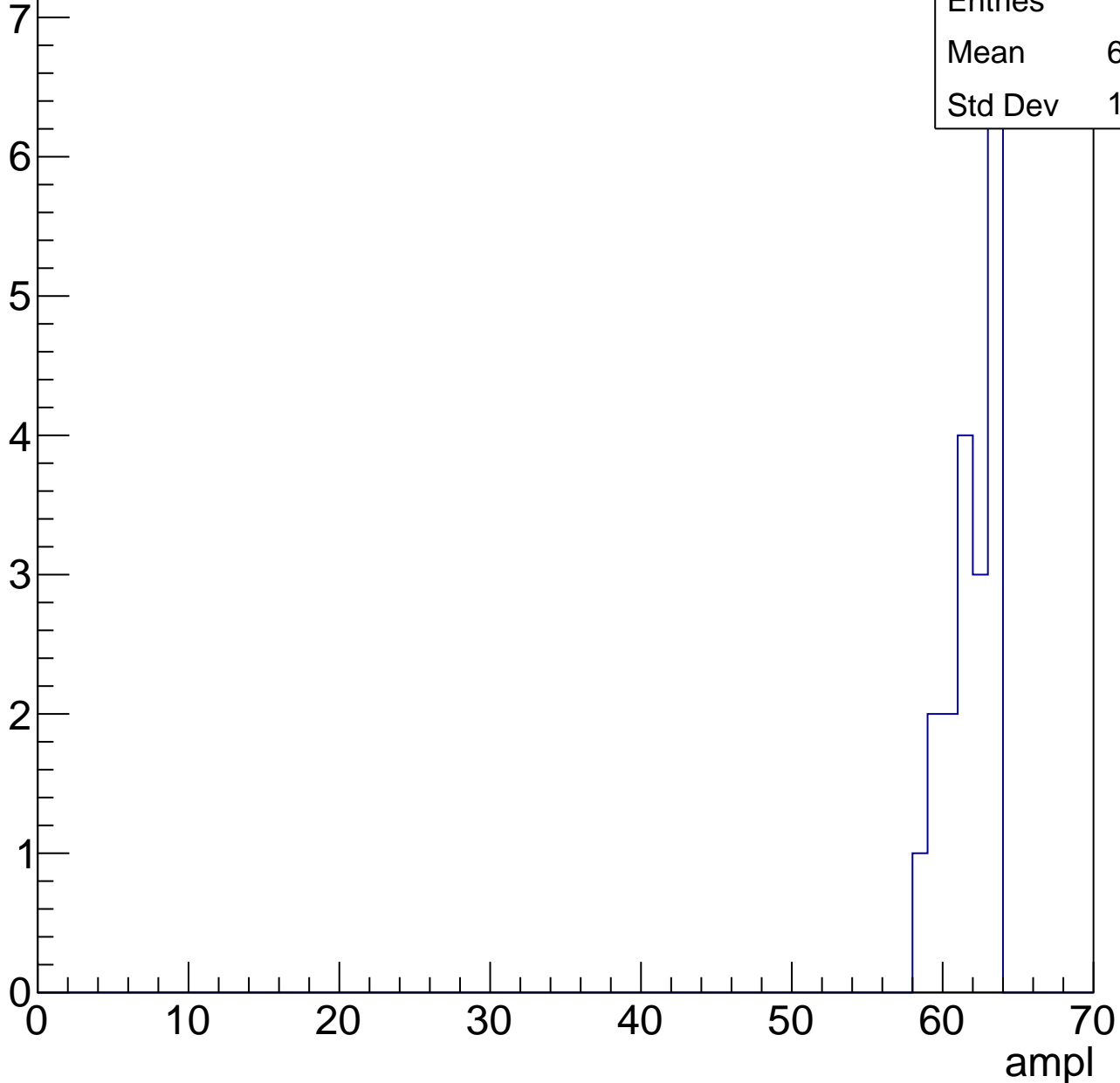


B0L002S, U9-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	61.42
Std Dev	1.567



B0L002S, U9-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch114, adc0

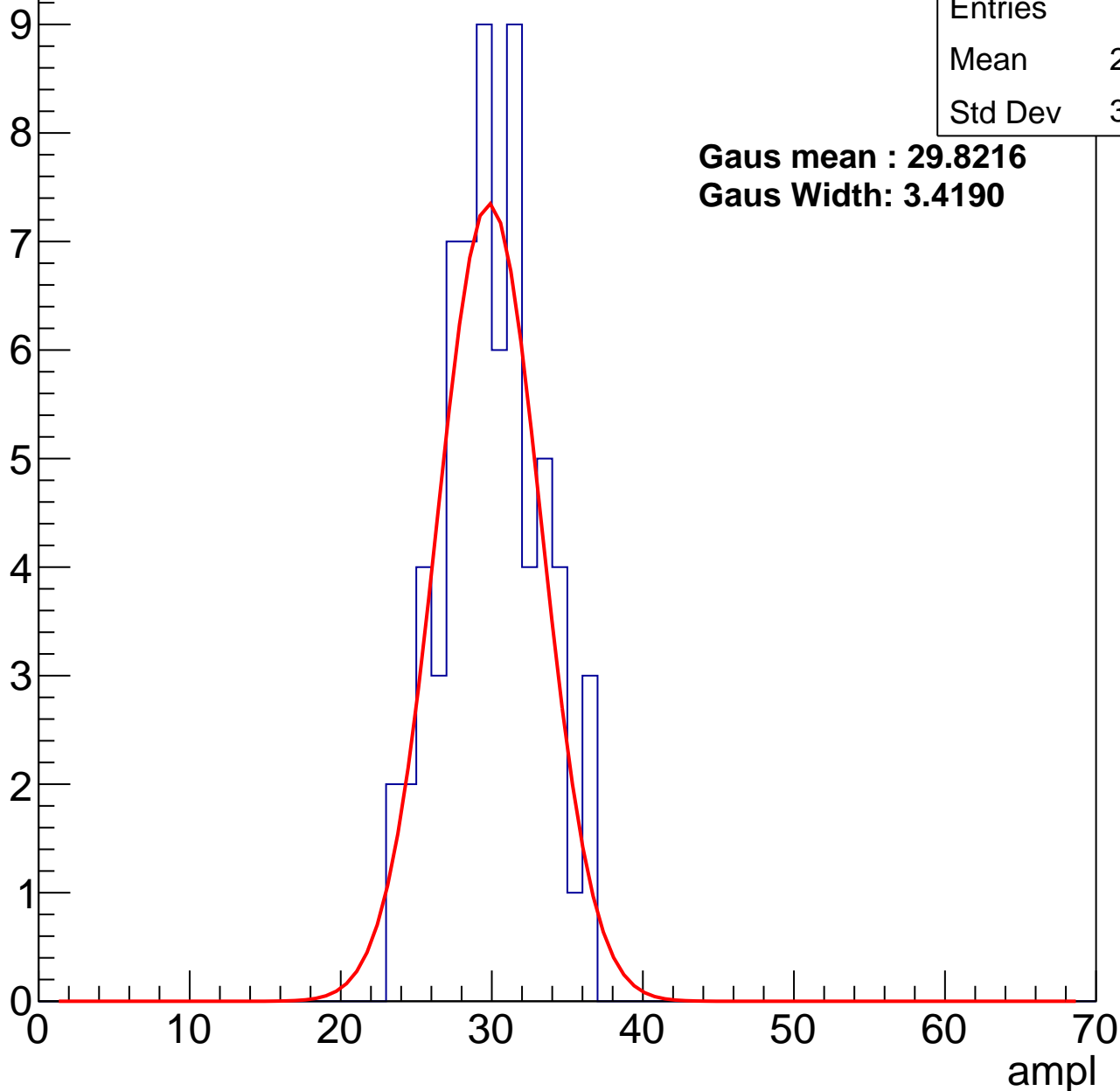
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.53
Std Dev	3.187

Gaus mean : 29.8216

Gaus Width: 3.4190



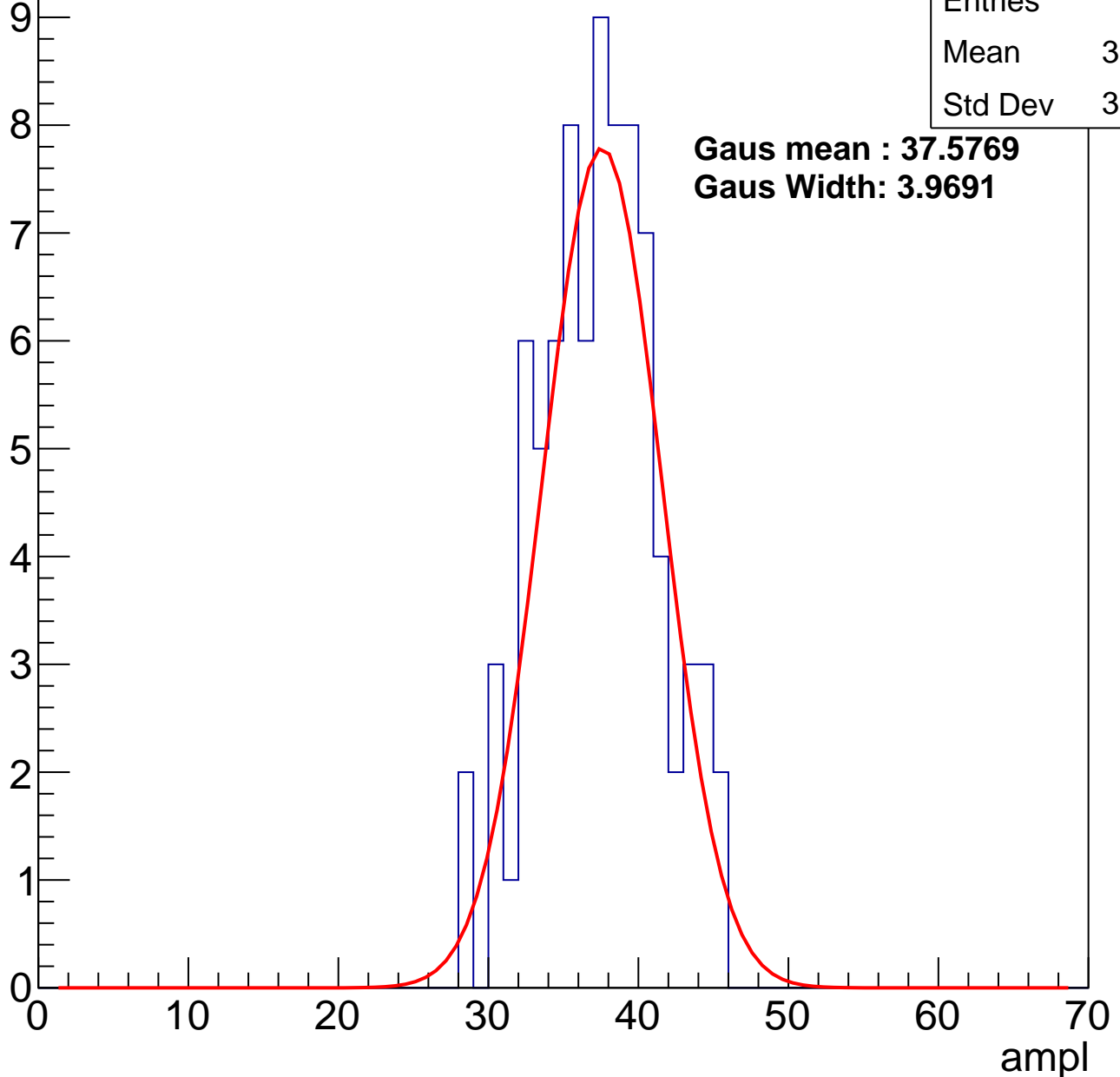
B0L002S, U9-ch114, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	36.89
Std Dev	3.906

Gaus mean : 37.5769
Gaus Width: 3.9691



B0L002S, U9-ch114, adc2

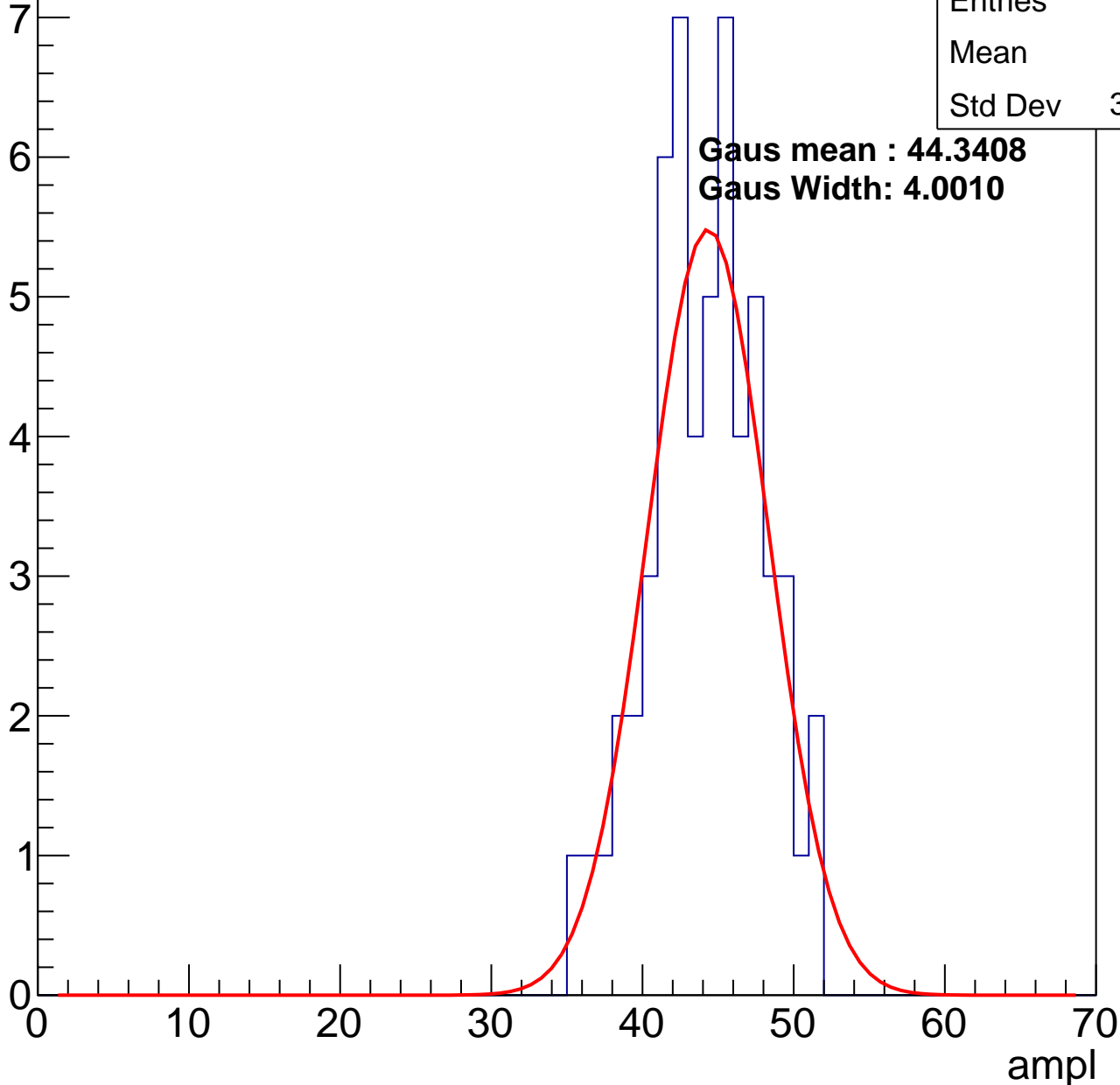
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.7
Std Dev	3.675

Gaus mean : 44.3408

Gaus Width: 4.0010

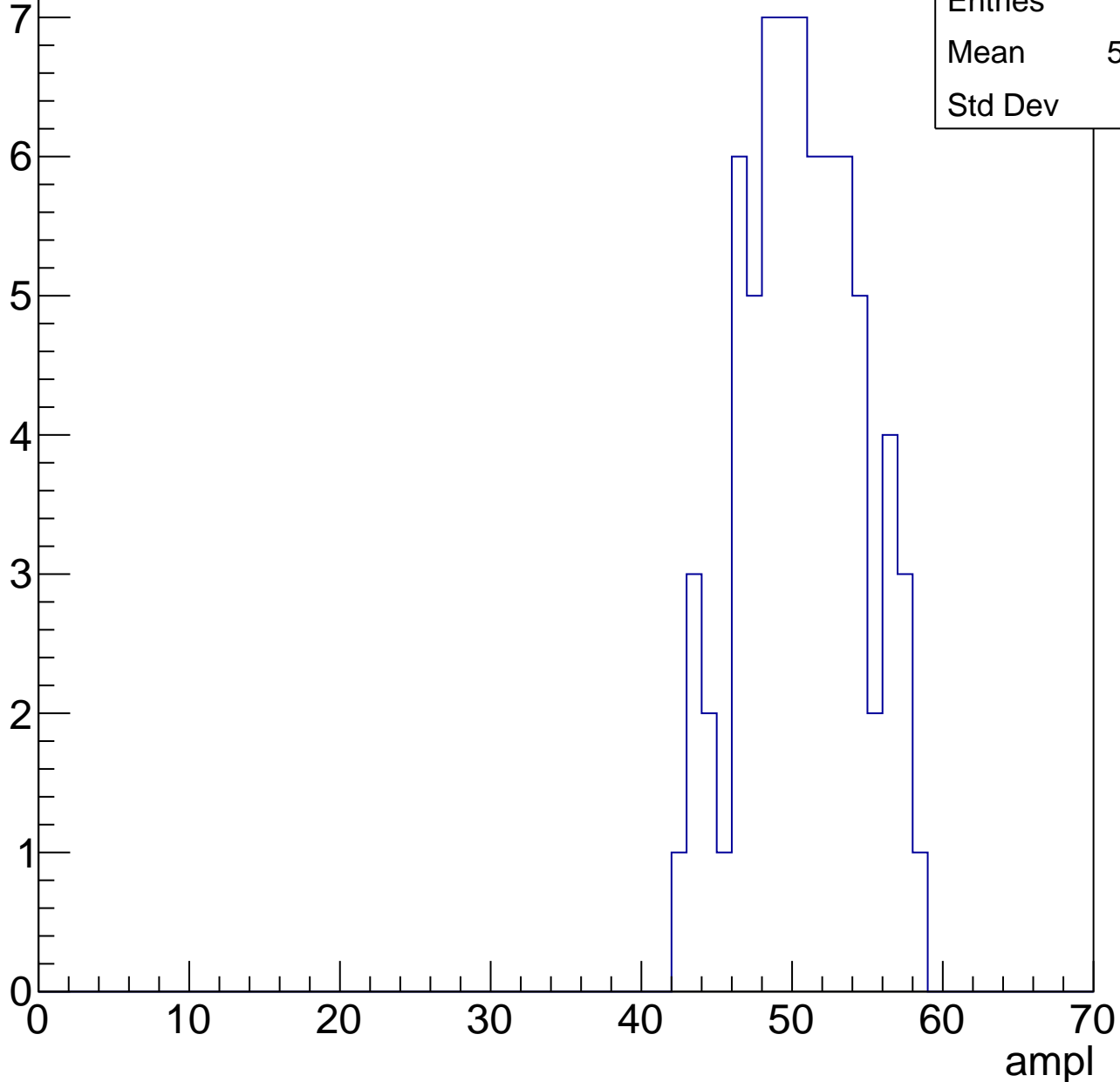


B0L002S, U9-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	50.18
Std Dev	3.82

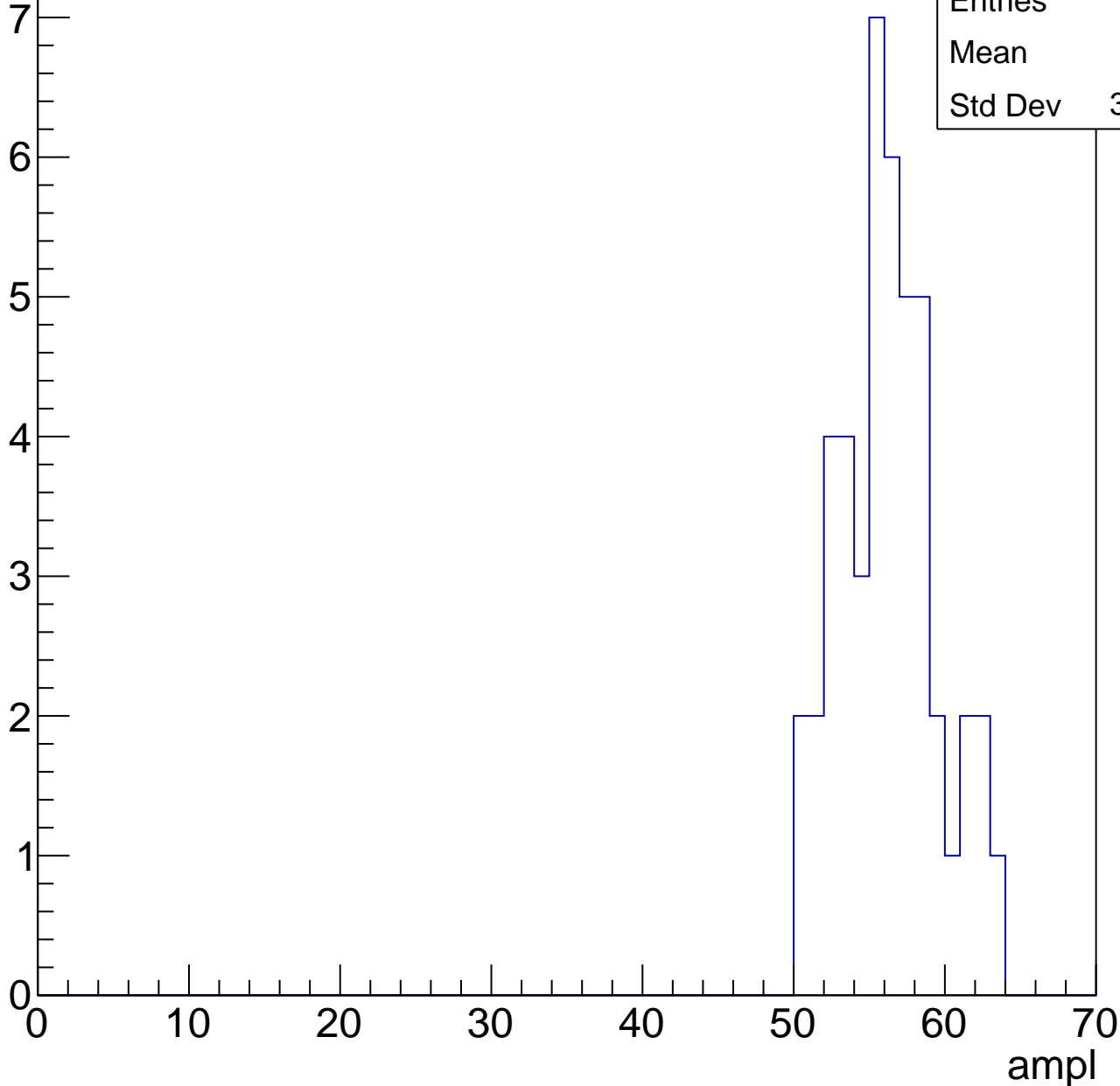


B0L002S, U9-ch114, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	55.8
Std Dev	3.194



B0L002S, U9-ch114, adc5

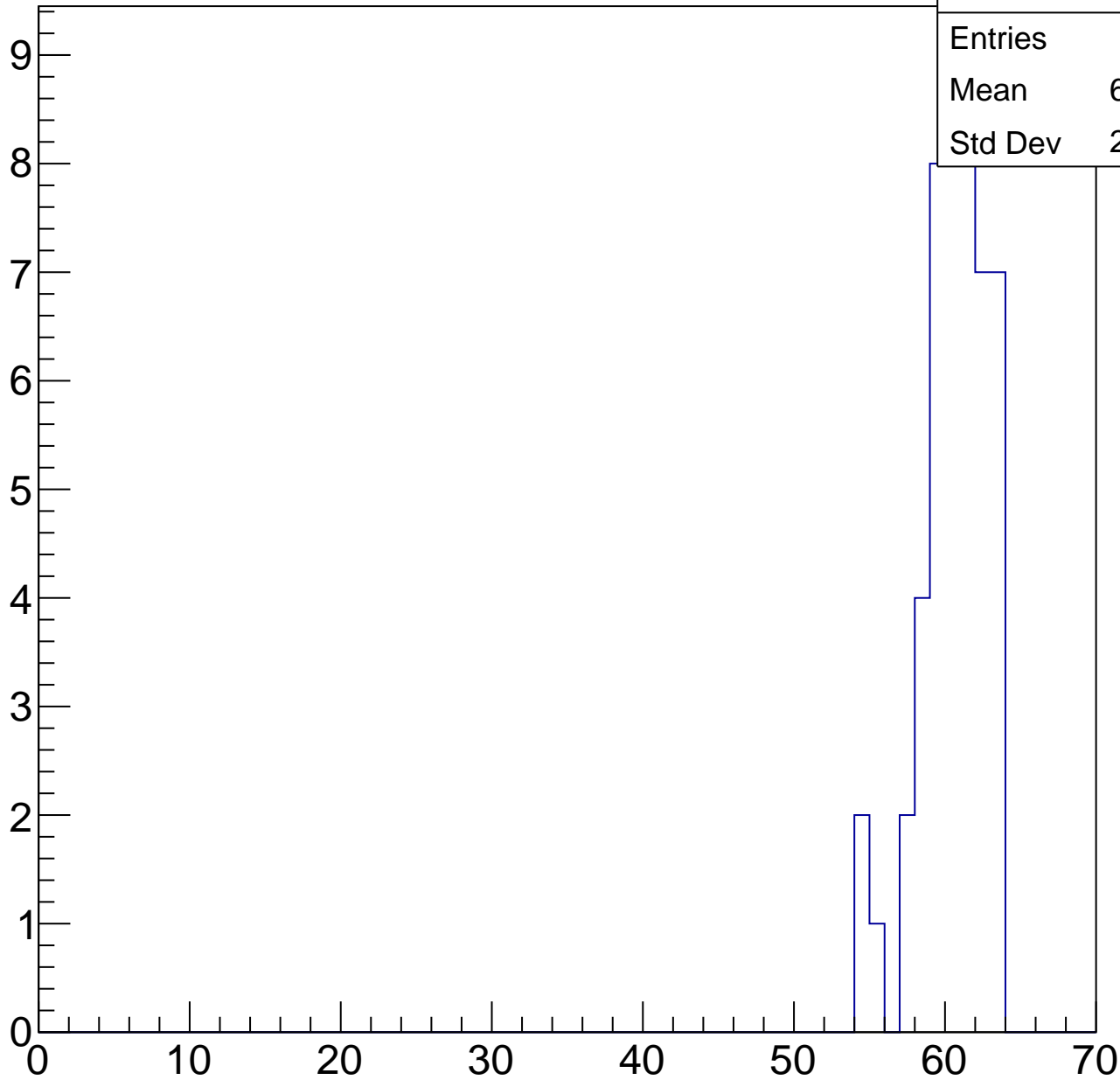
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	60.08
Std Dev	2.225

ampl



B0L002S, U9-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U9-ch115, adc0

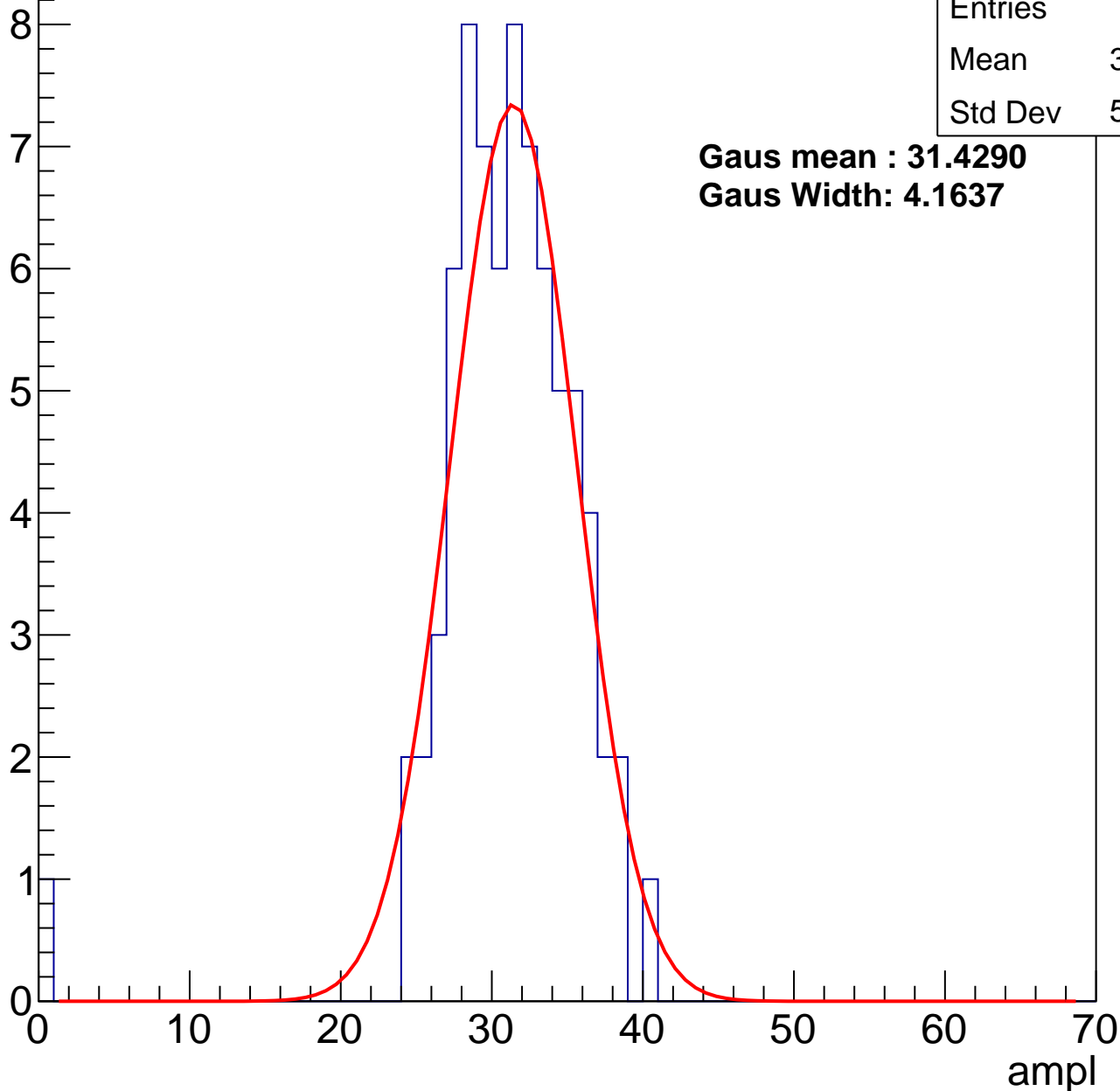
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.59
Std Dev	5.036

Gaus mean : 31.4290

Gaus Width: 4.1637



B0L002S, U9-ch115, adc1

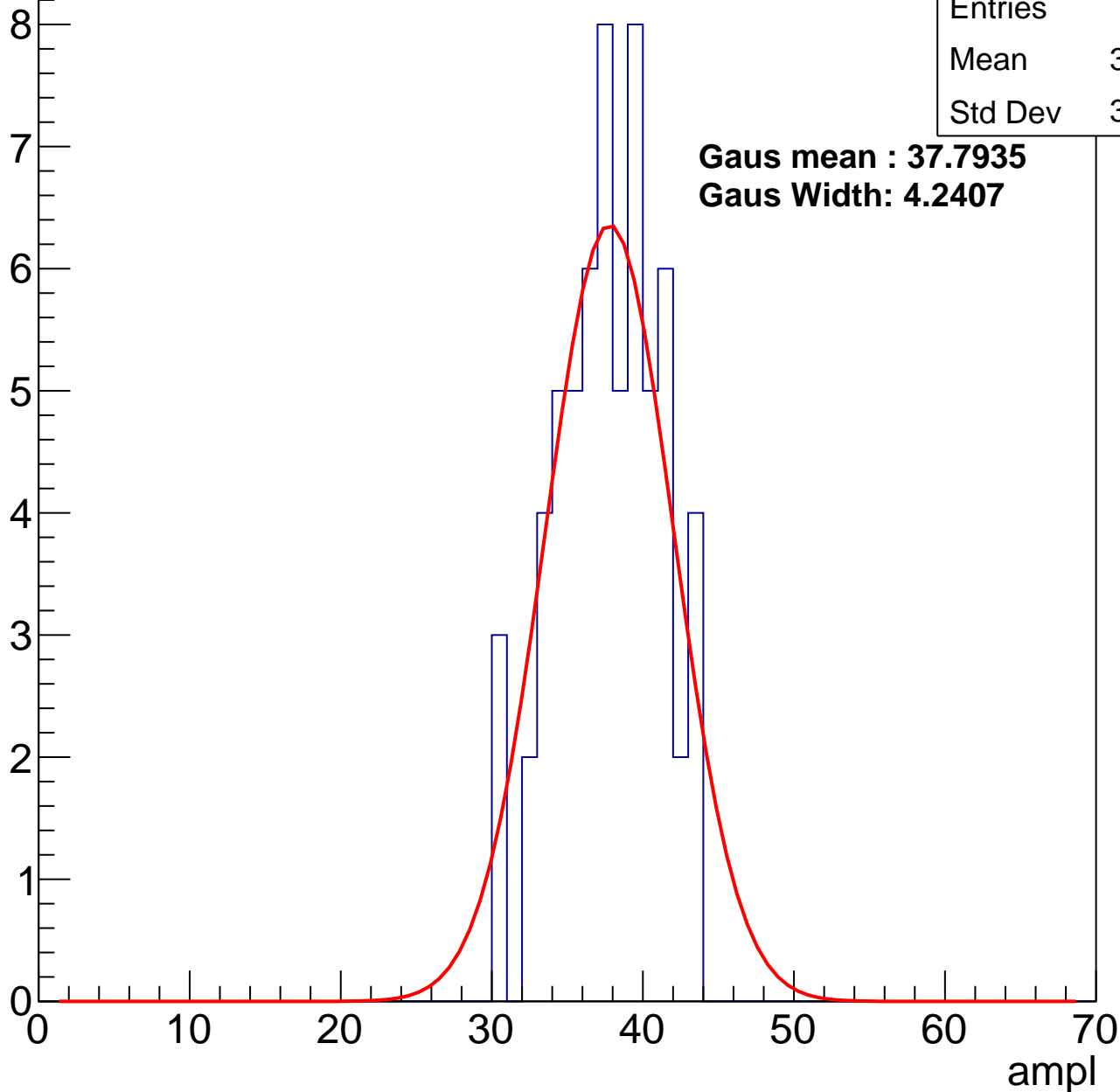
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.25
Std Dev	3.333

Gaus mean : 37.7935

Gaus Width: 4.2407



B0L002S, U9-ch115, adc2

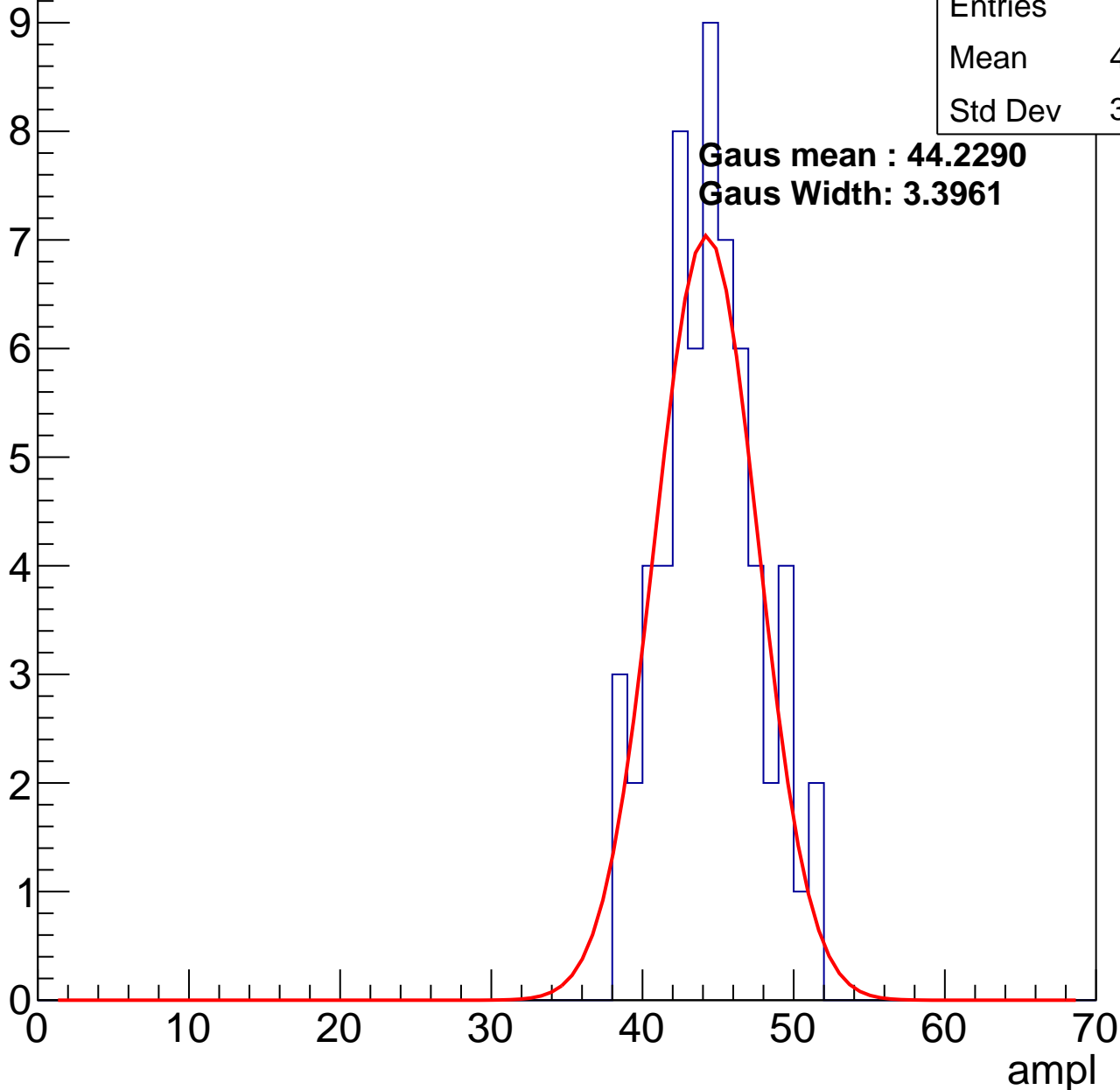
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.02
Std Dev	3.185

Gaus mean : 44.2290

Gaus Width: 3.3961

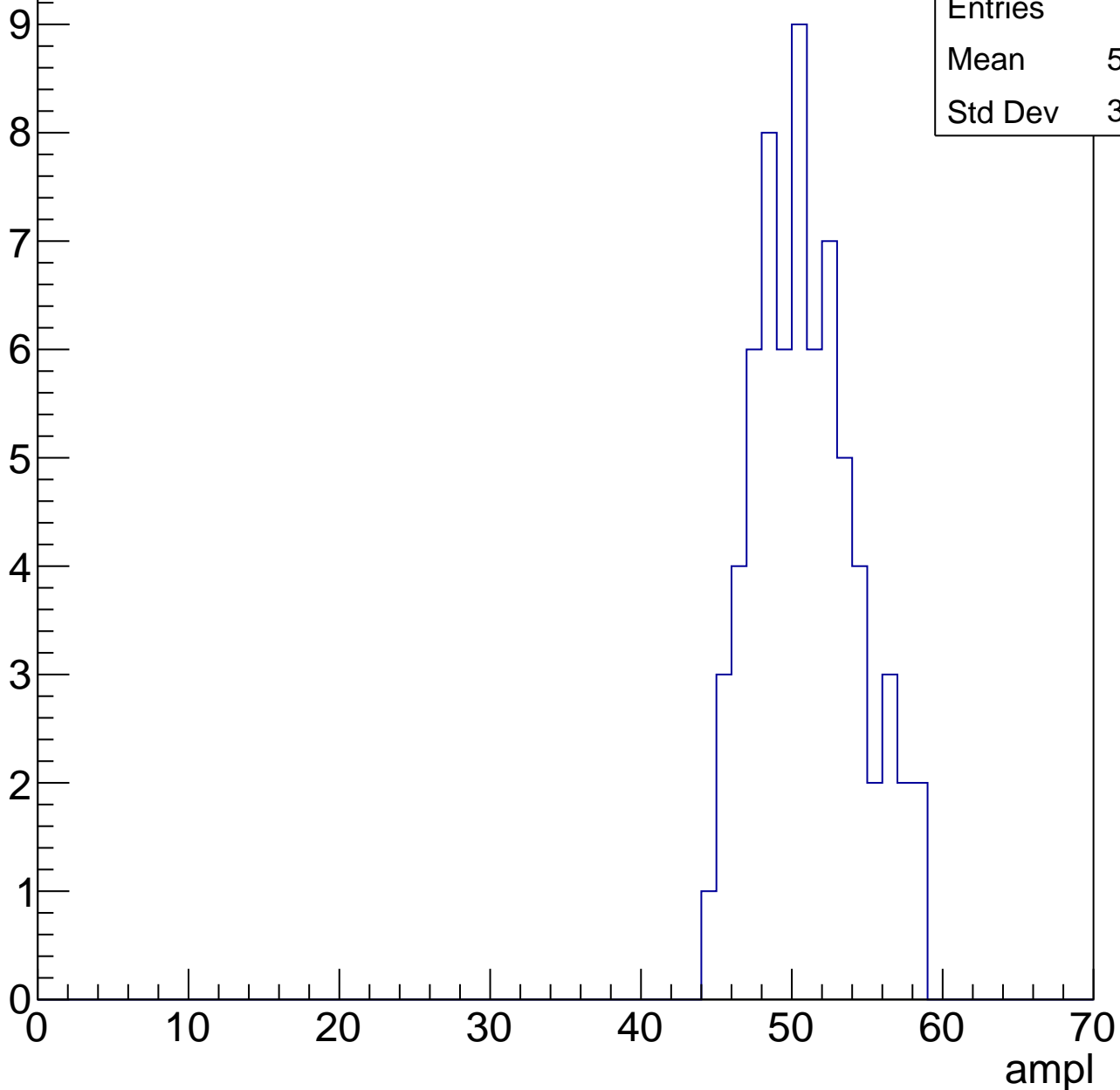


B0L002S, U9-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.47
Std Dev	3.385

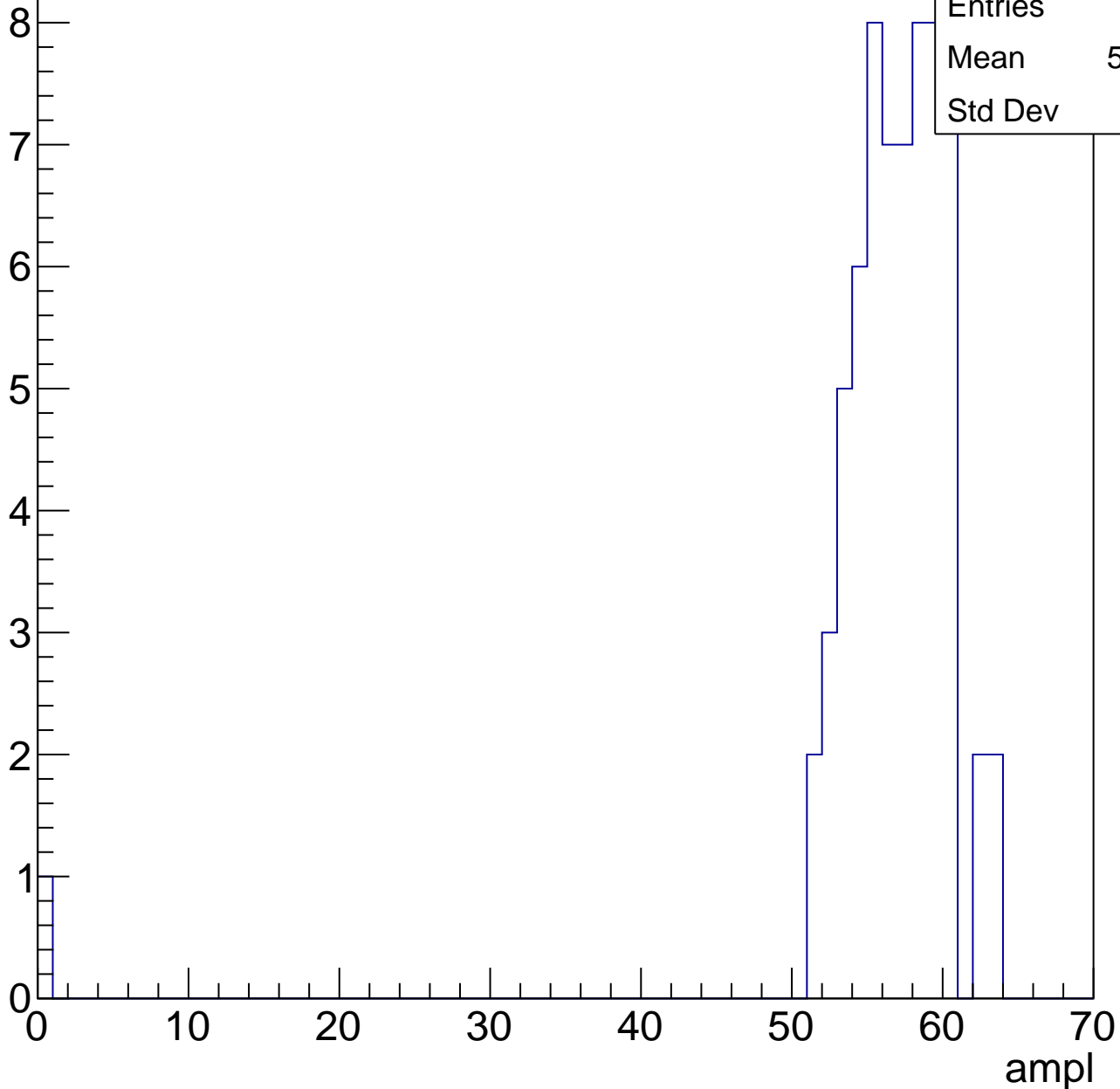


B0L002S, U9-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

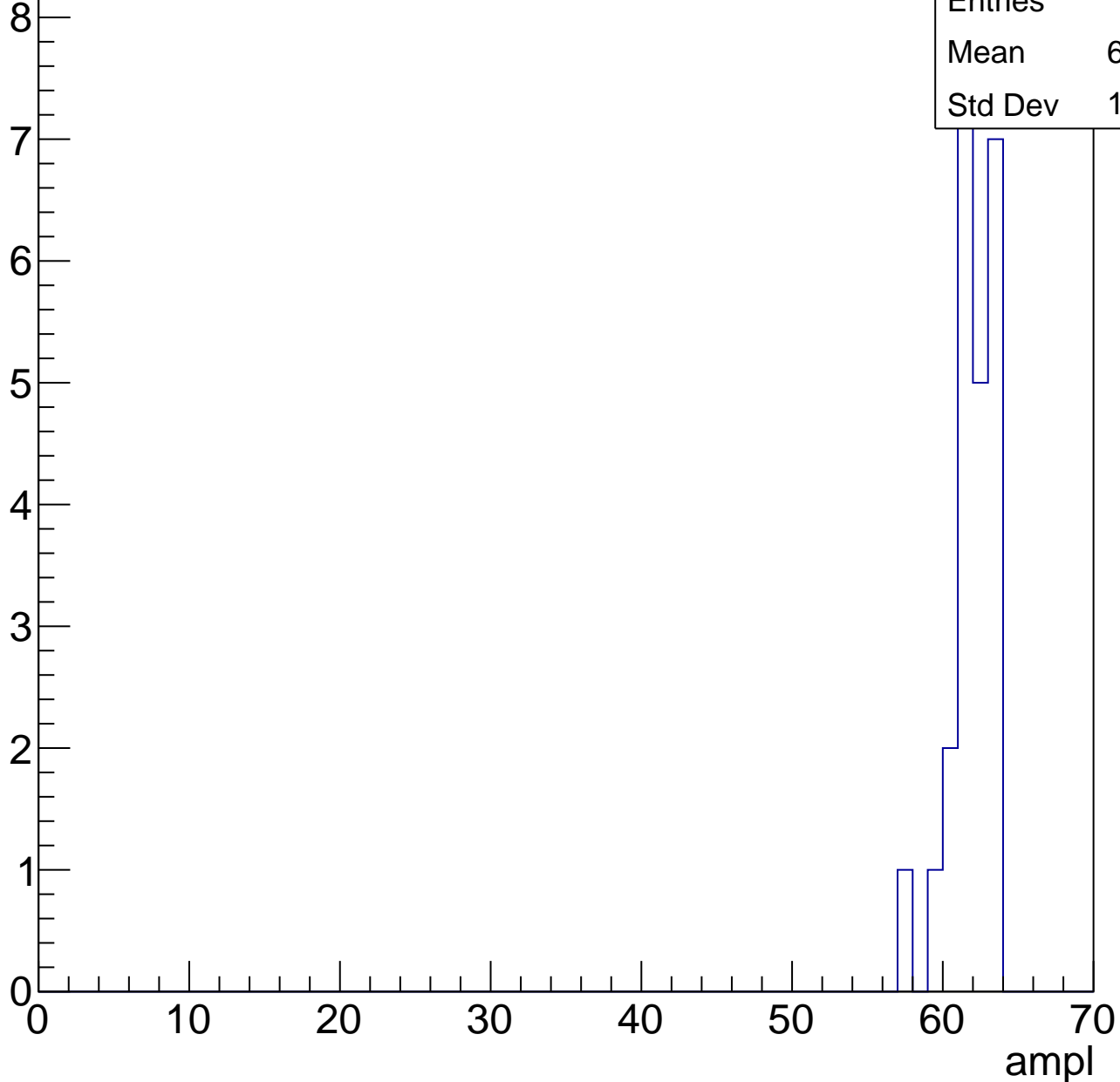
Entries	67
Mean	55.88
Std Dev	7.45



B0L002S, U9-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

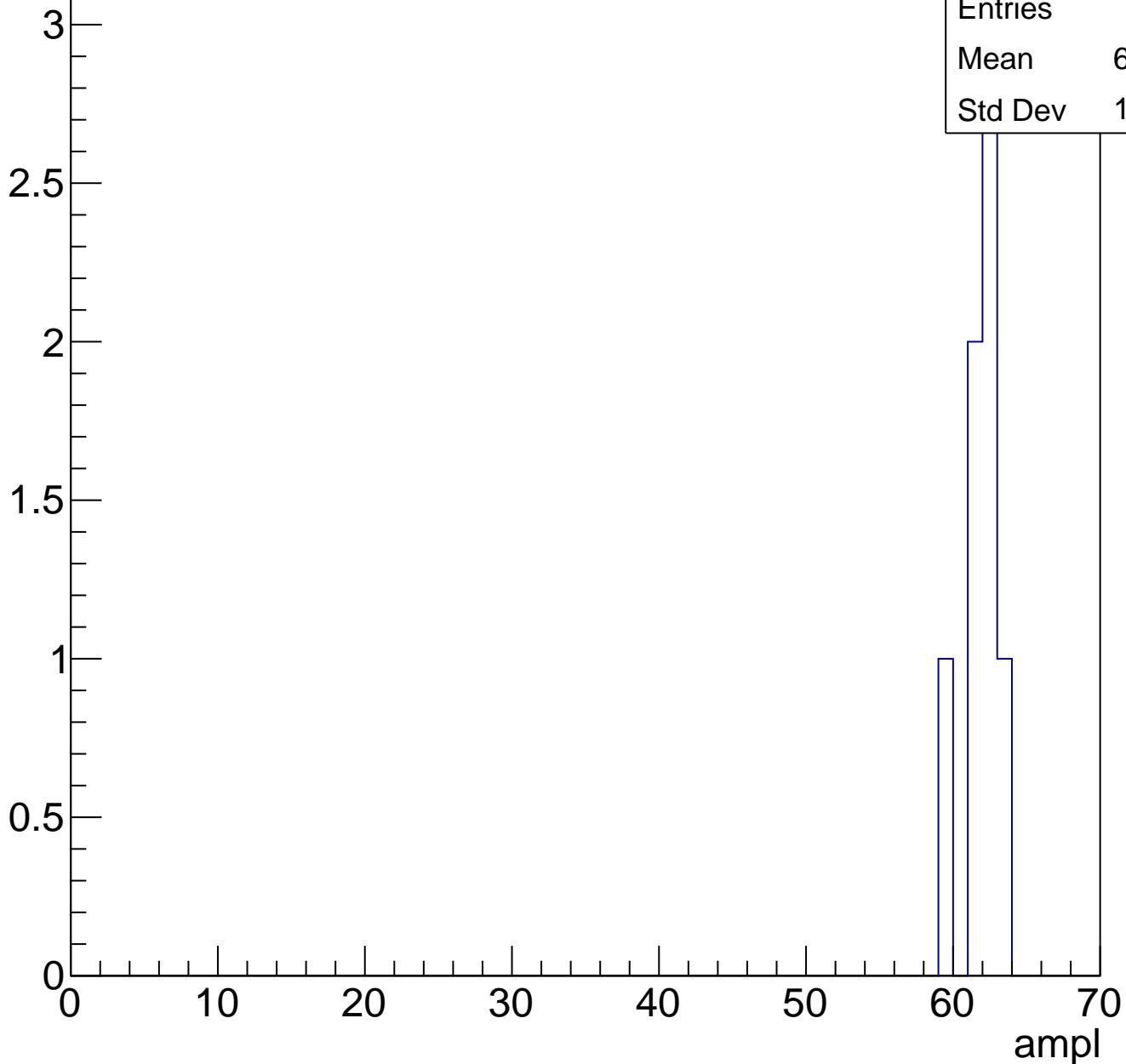
Entry



B0L002S, U9-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch116, adc0

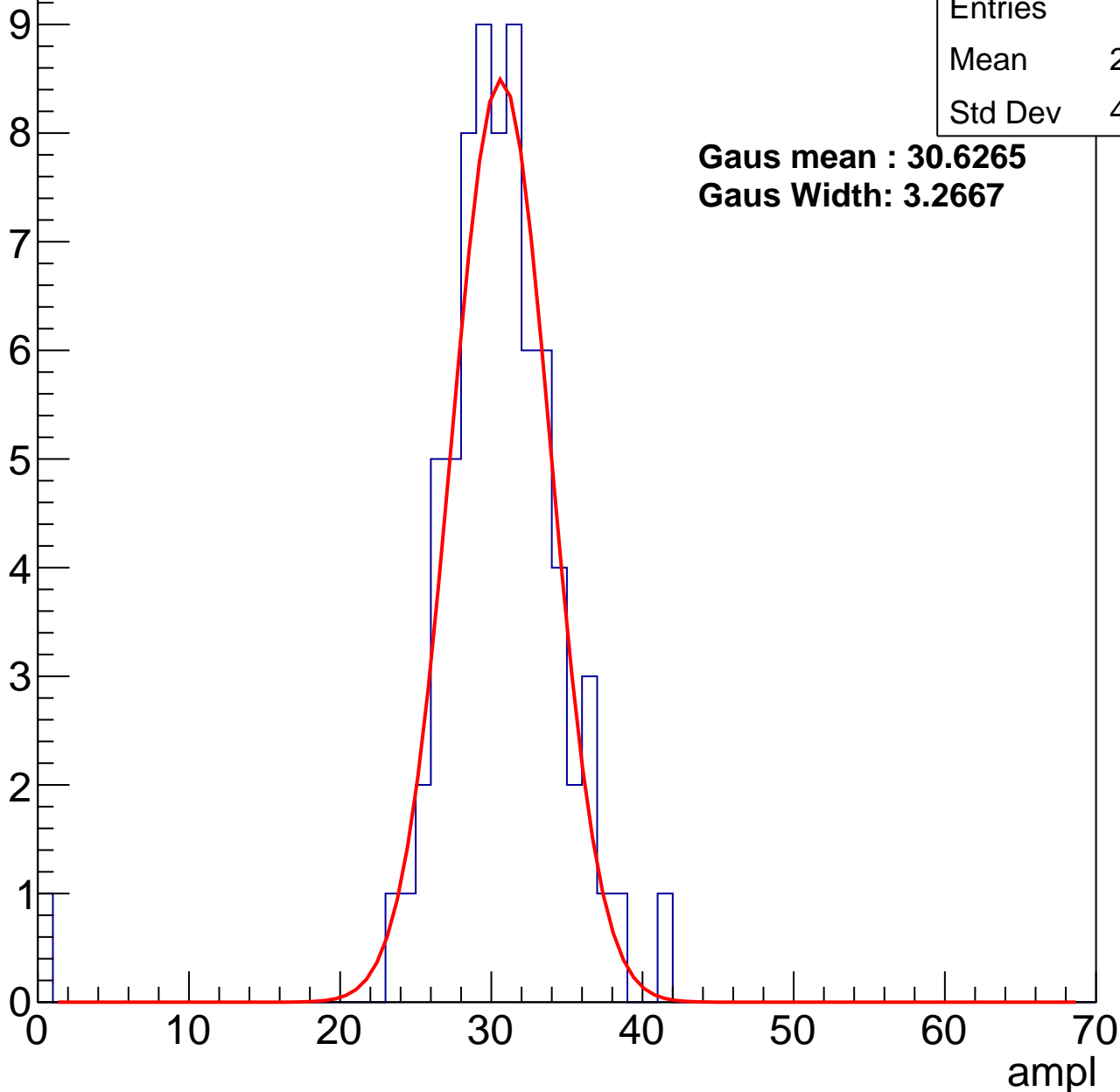
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.95
Std Dev	4.887

Gaus mean : 30.6265

Gaus Width: 3.2667



B0L002S, U9-ch116, adc1

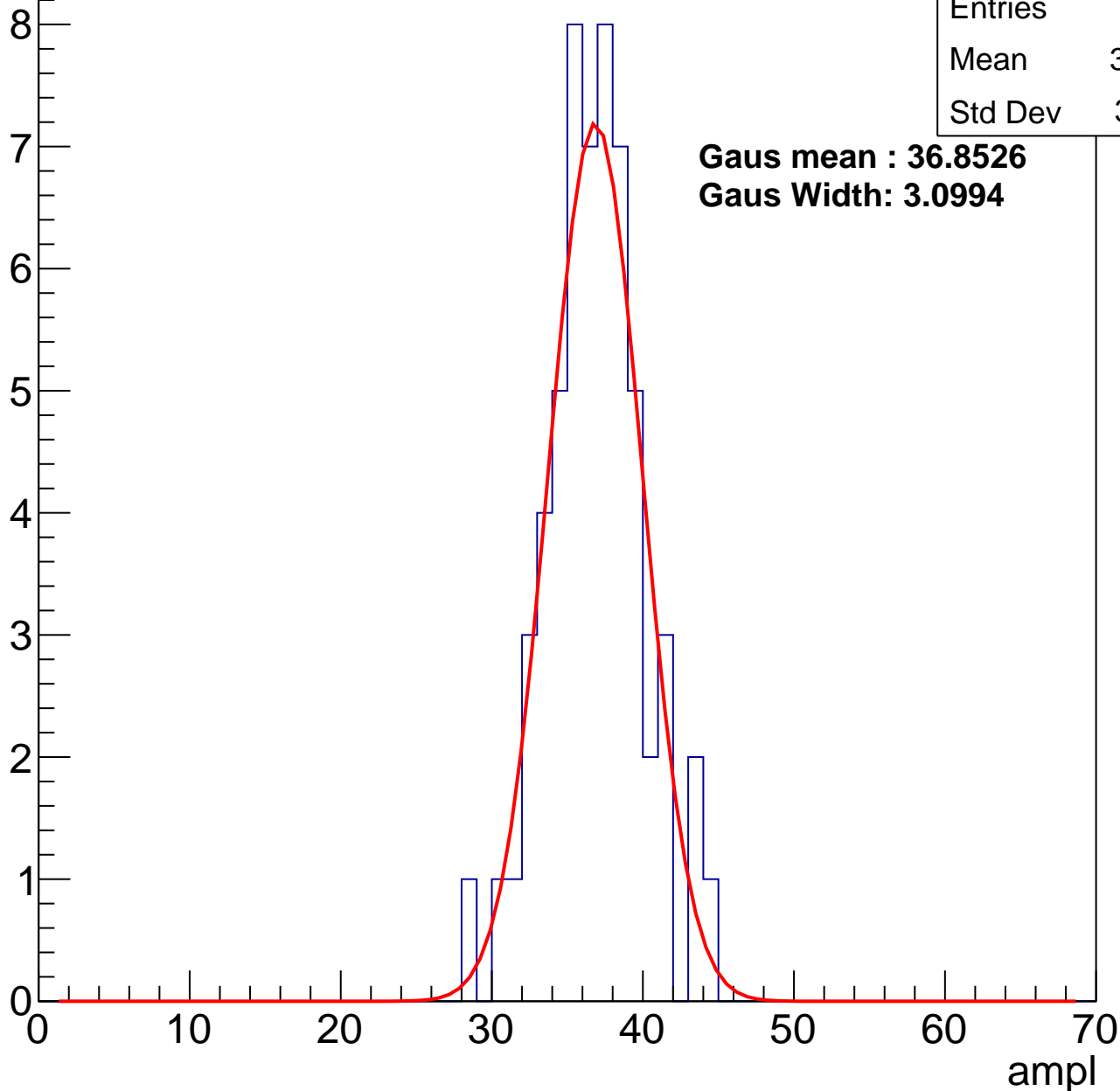
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	36.36
Std Dev	3.161

Gaus mean : 36.8526

Gaus Width: 3.0994



B0L002S, U9-ch116, adc2

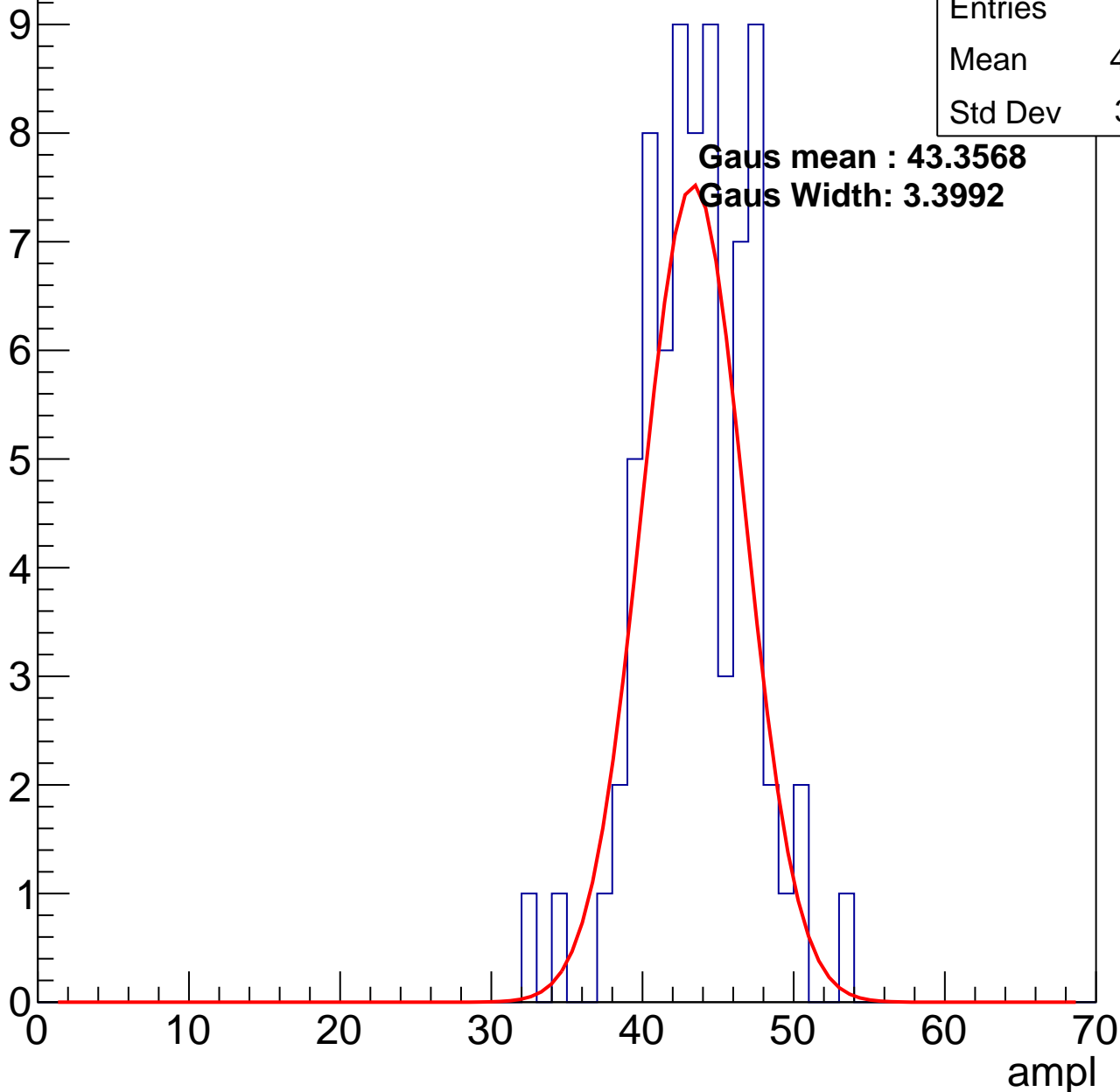
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	43.15
Std Dev	3.621

Gaus mean : 43.3568

Gaus Width: 3.3992

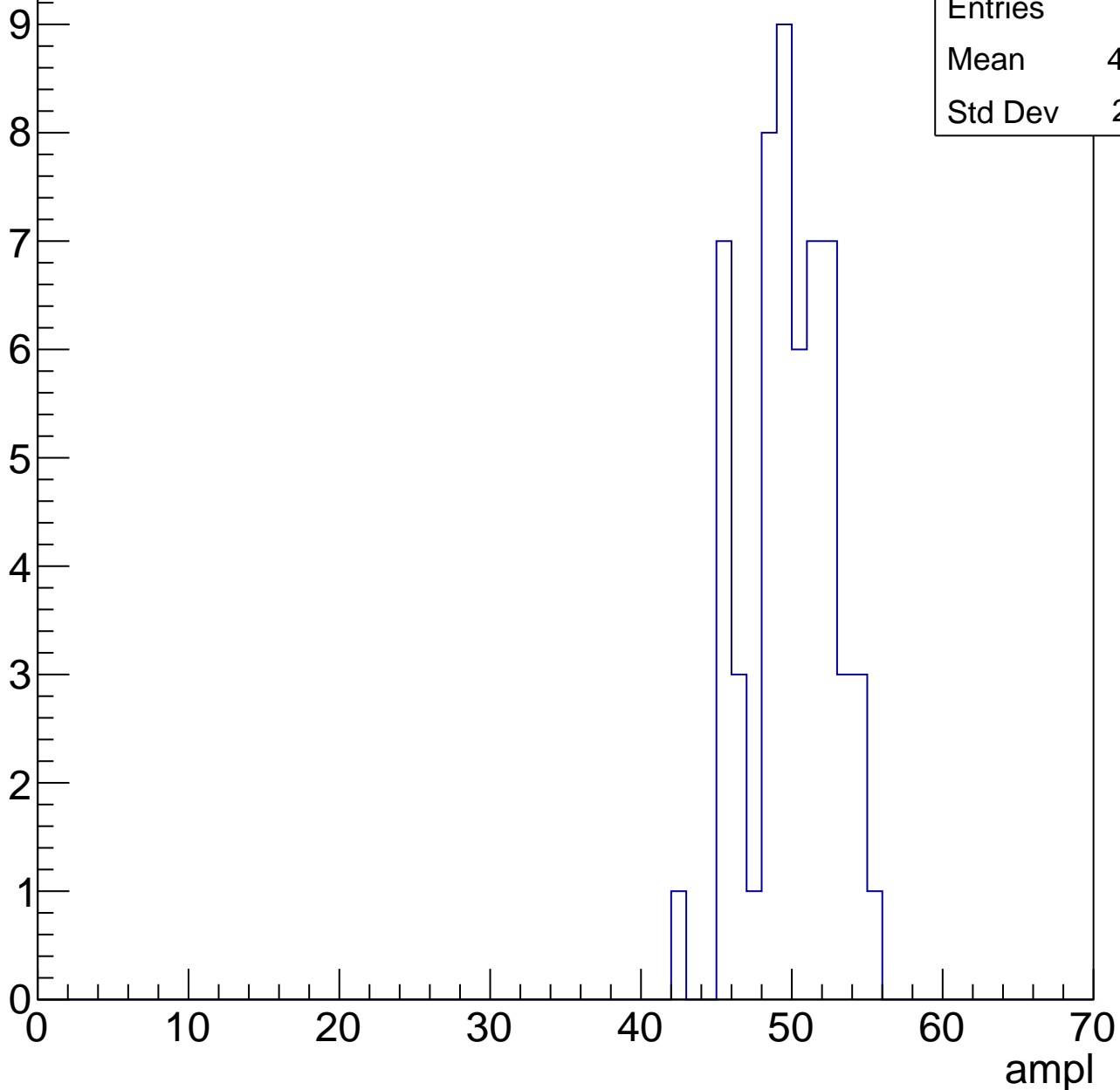


B0L002S, U9-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

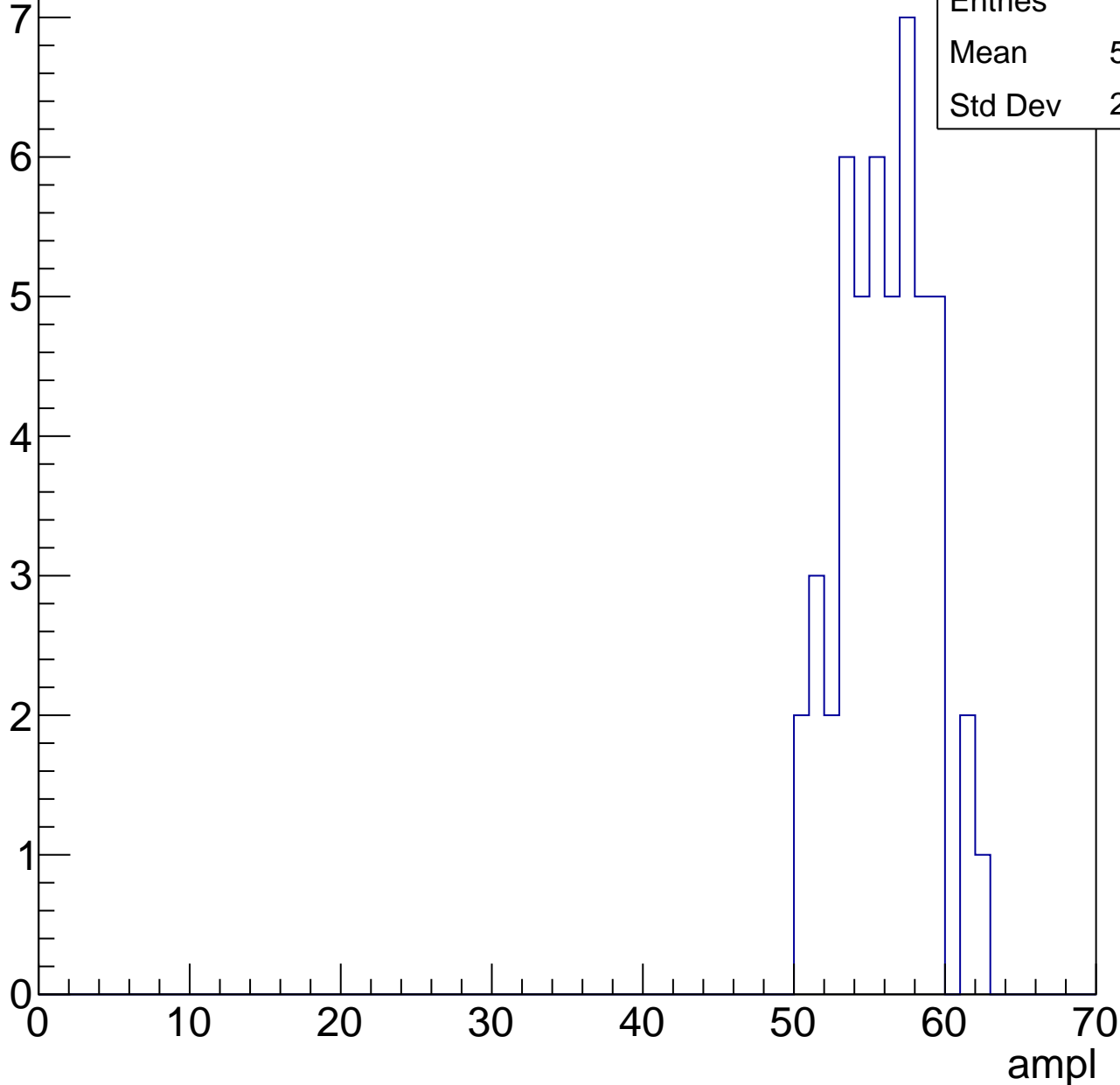
Entries	56
Mean	49.36
Std Dev	2.831



B0L002S, U9-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	49
Mean	55.57
Std Dev	2.886

B0L002S, U9-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	57
Mean	58.37
Std Dev	8.273

Entry

10

8

6

4

2

0

0

10

20

30

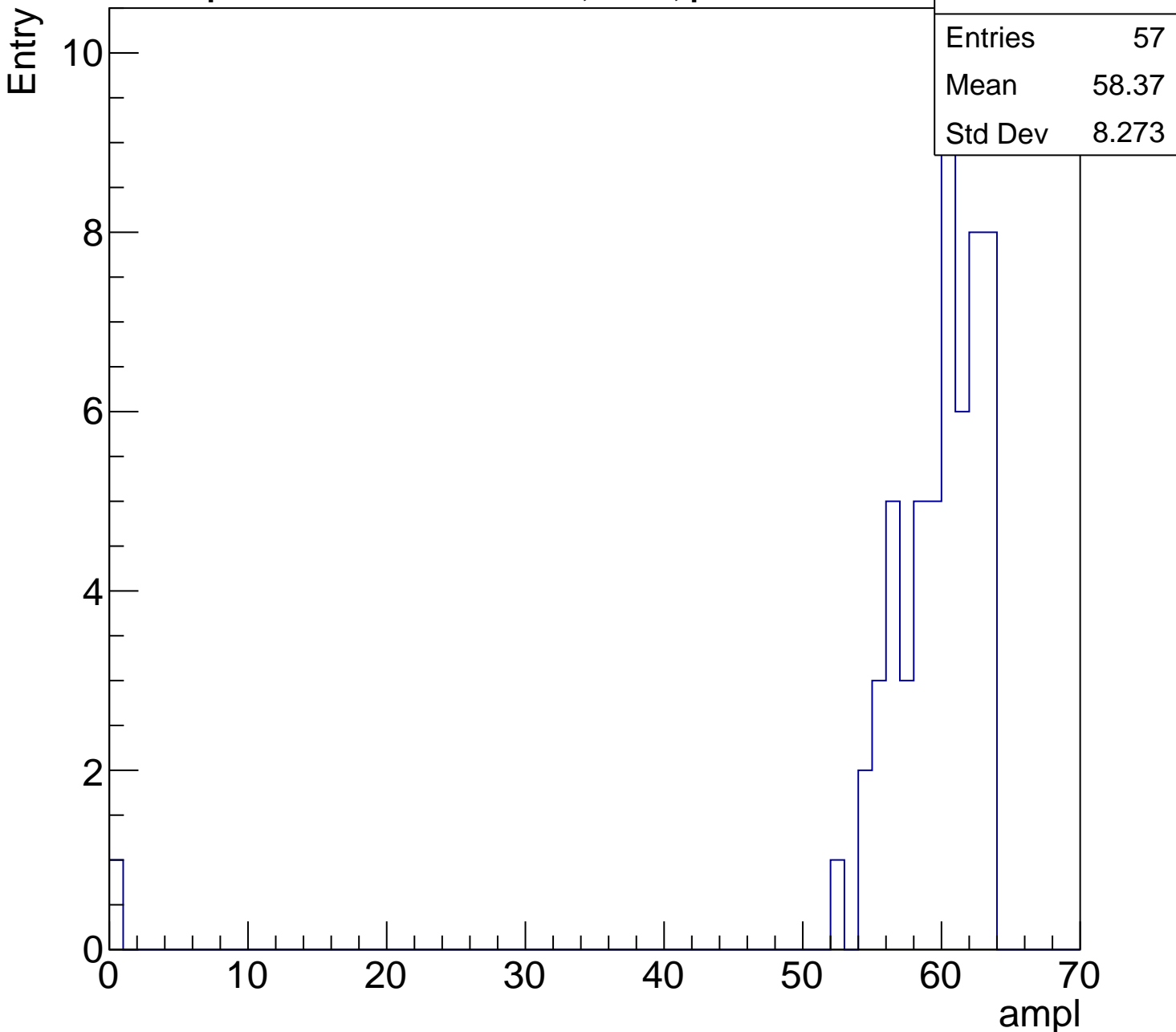
40

50

60

ampl

70



B0L002S, U9-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

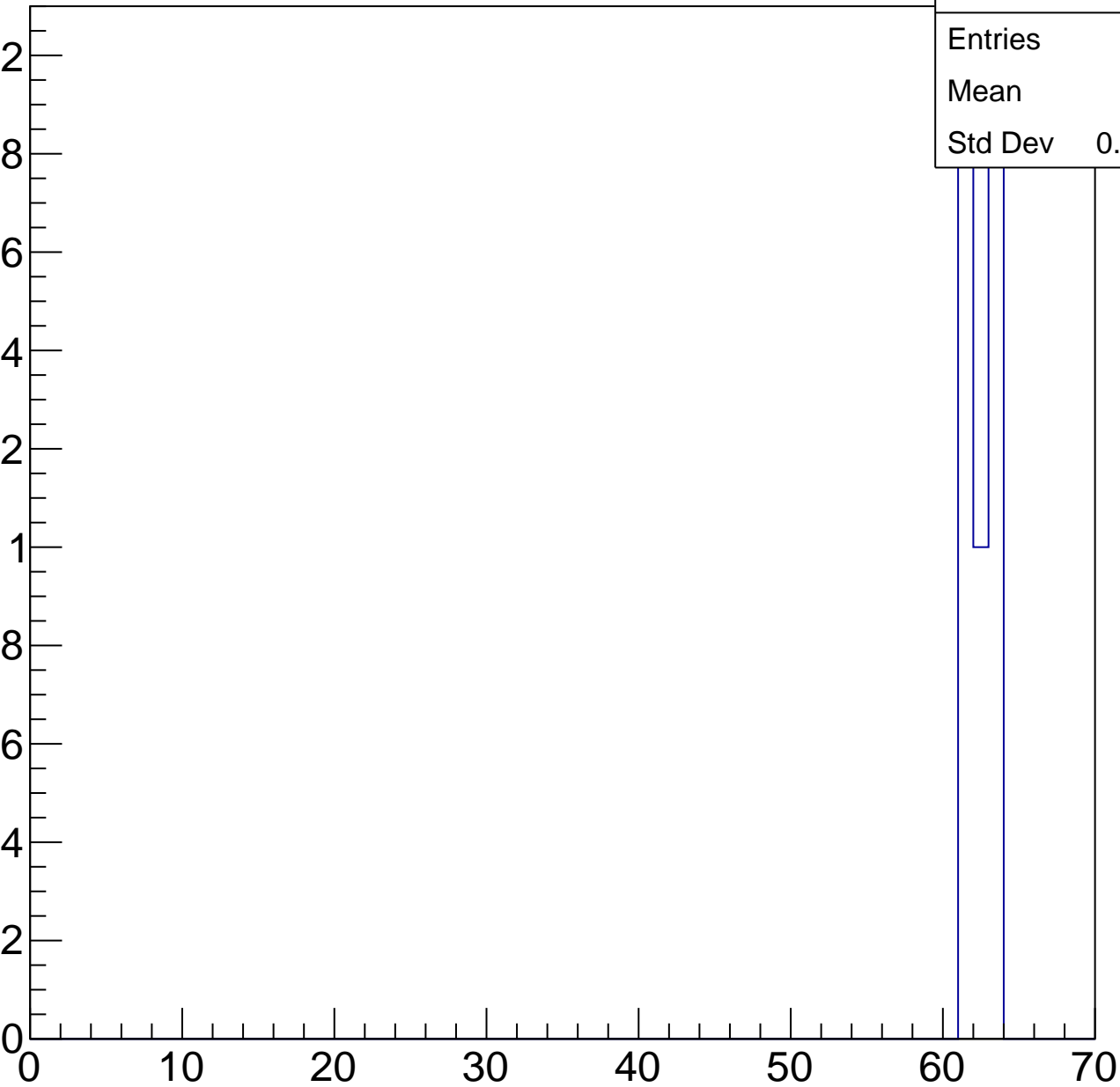
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

0 10 20 30 40 50 60 70

ampl



B0L002S, U9-ch116, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch117, adc0

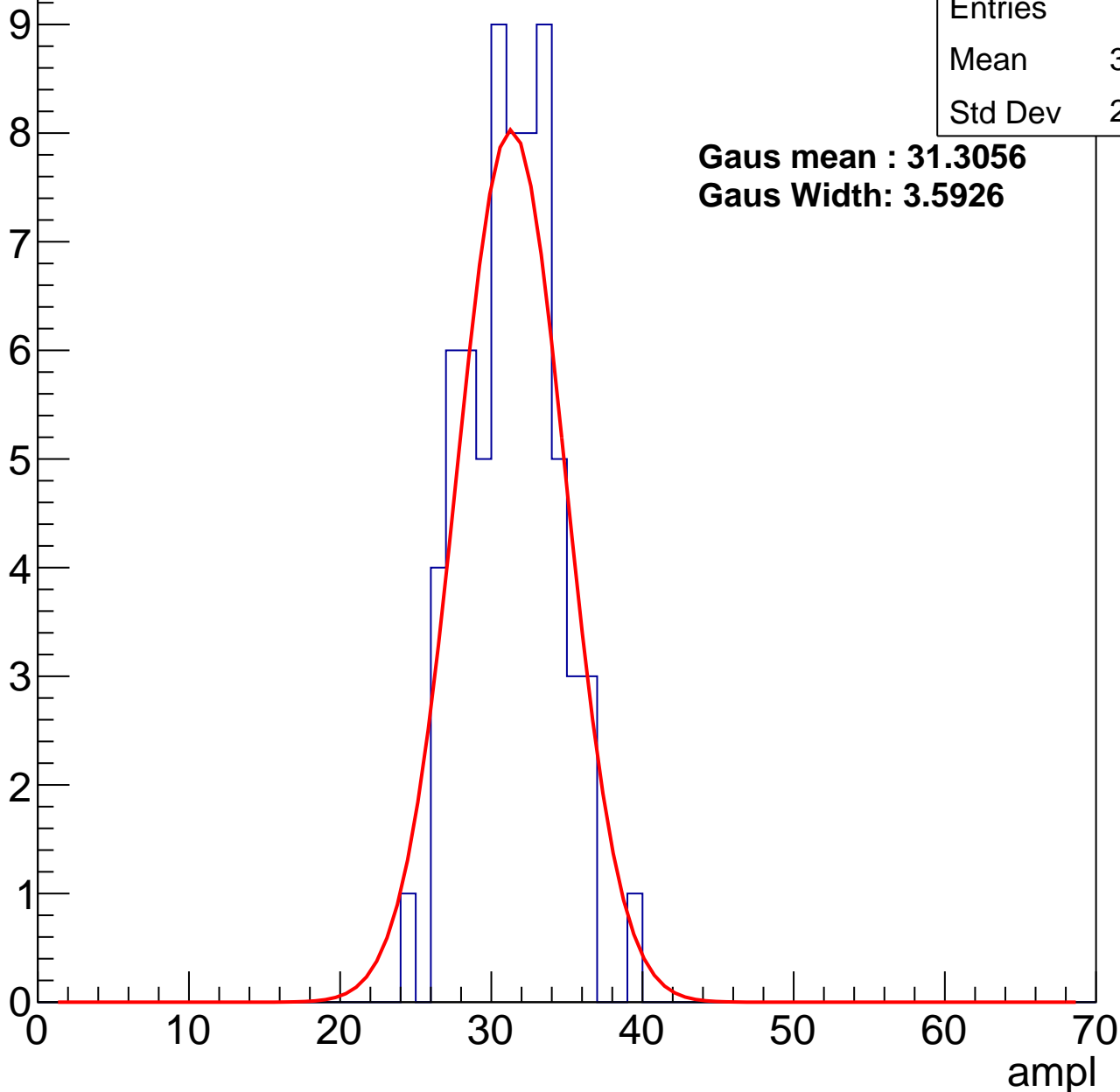
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.82
Std Dev	2.975

Gaus mean : 31.3056

Gaus Width: 3.5926

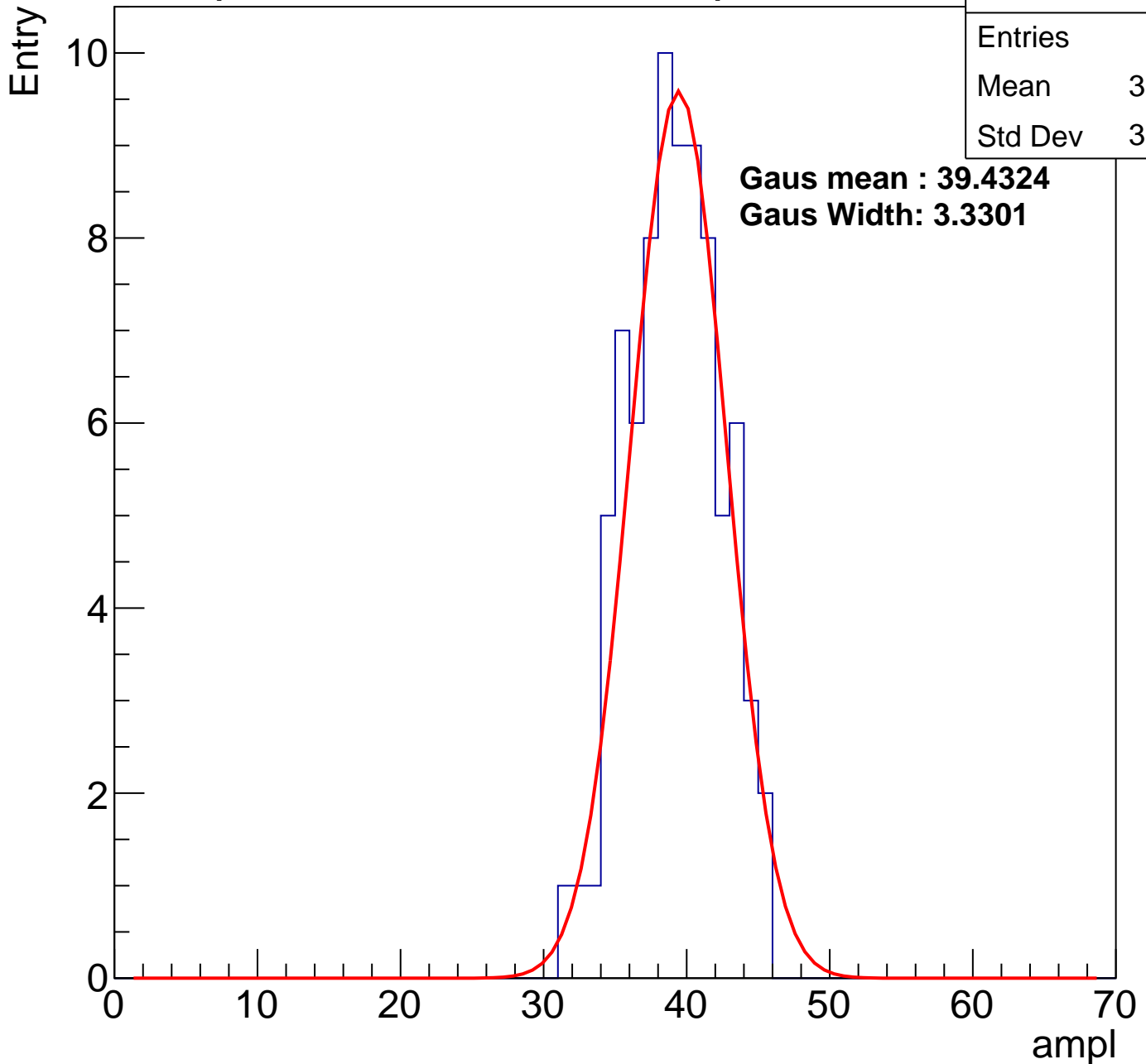


B0L002S, U9-ch117, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	38.67
Std Dev	3.147

Gaus mean : 39.4324
Gaus Width: 3.3301



B0L002S, U9-ch117, adc2

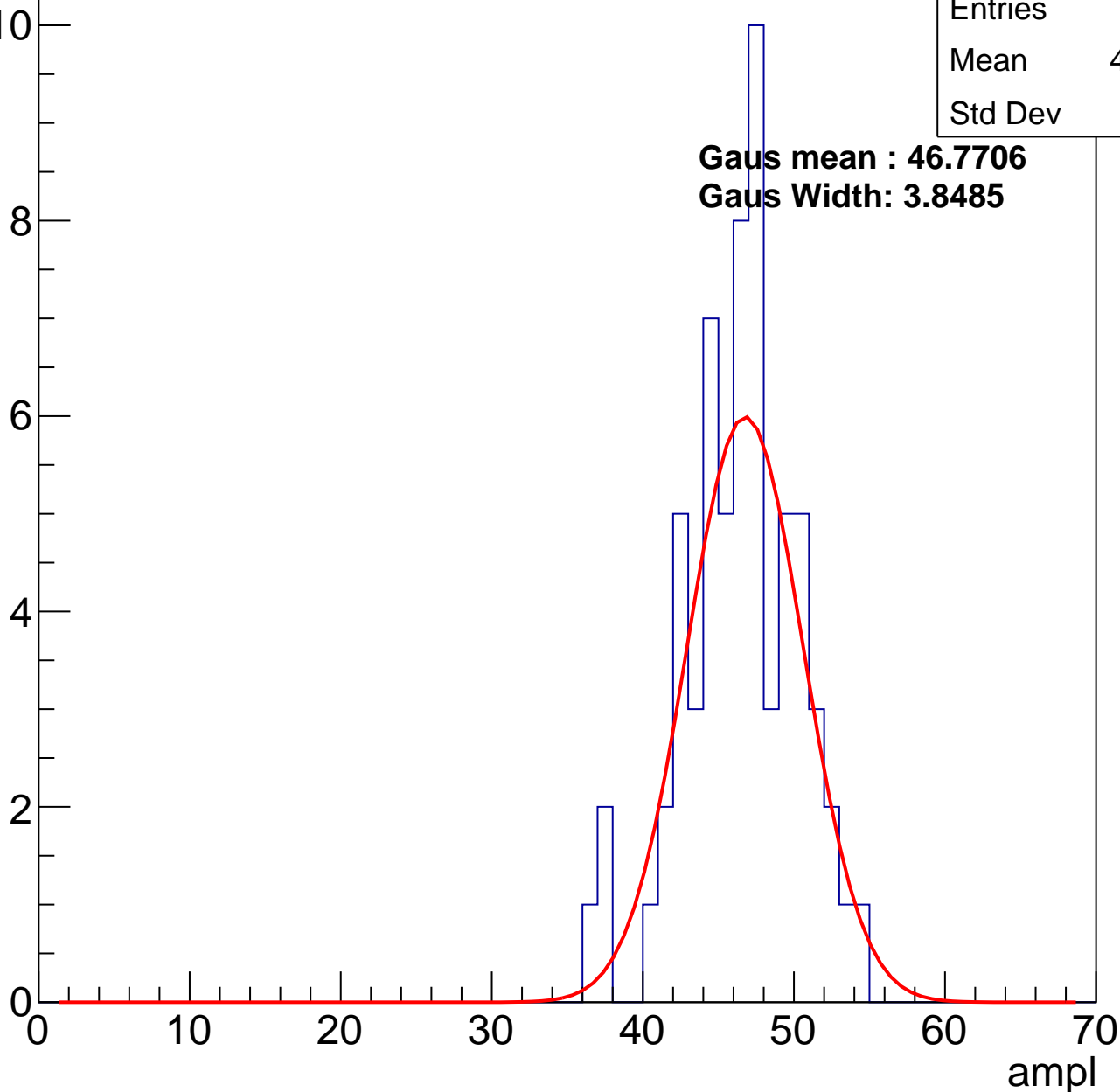
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	46.02
Std Dev	3.74

Gaus mean : 46.7706

Gaus Width: 3.8485

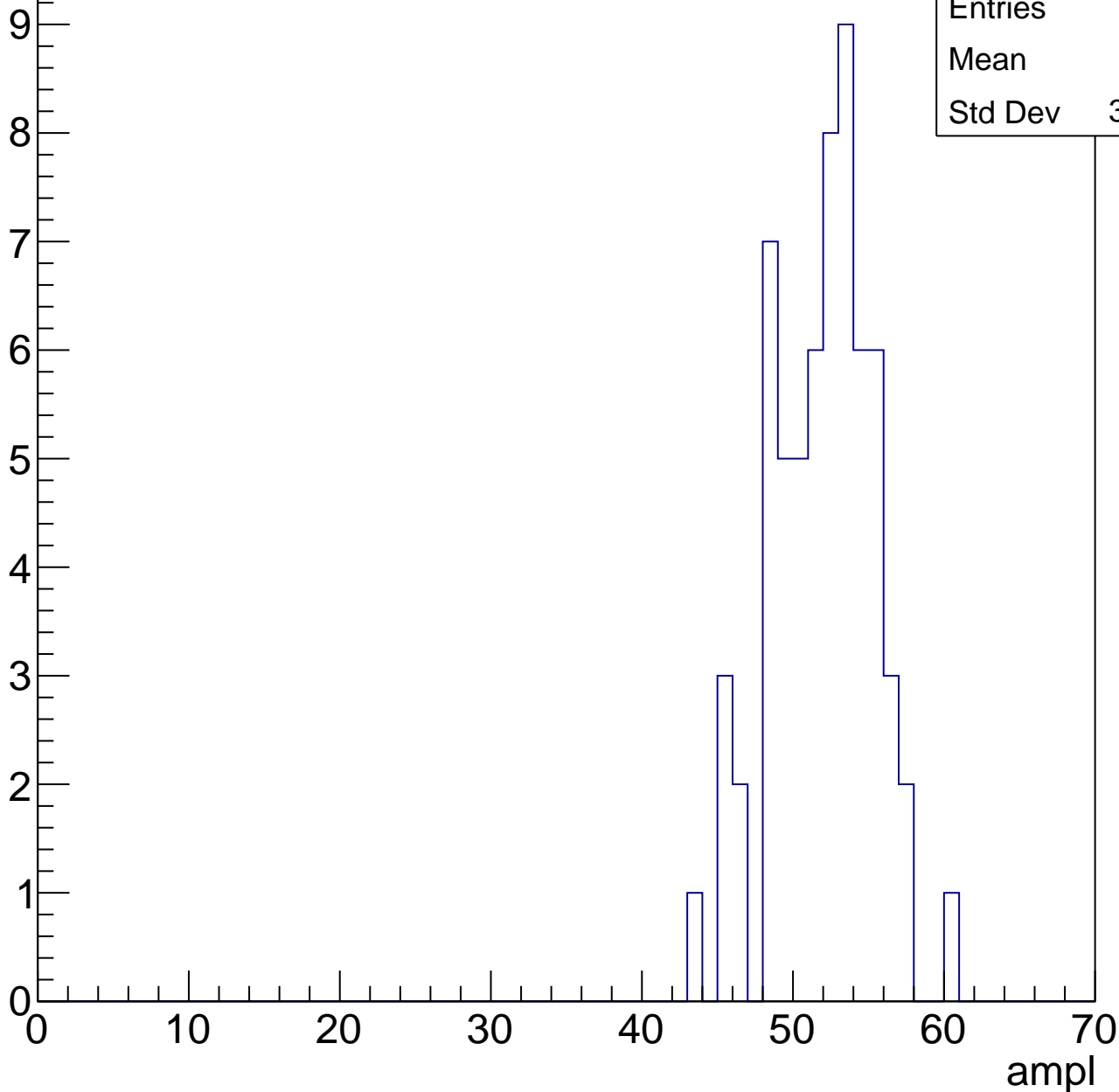


B0L002S, U9-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.5
Std Dev	3.345

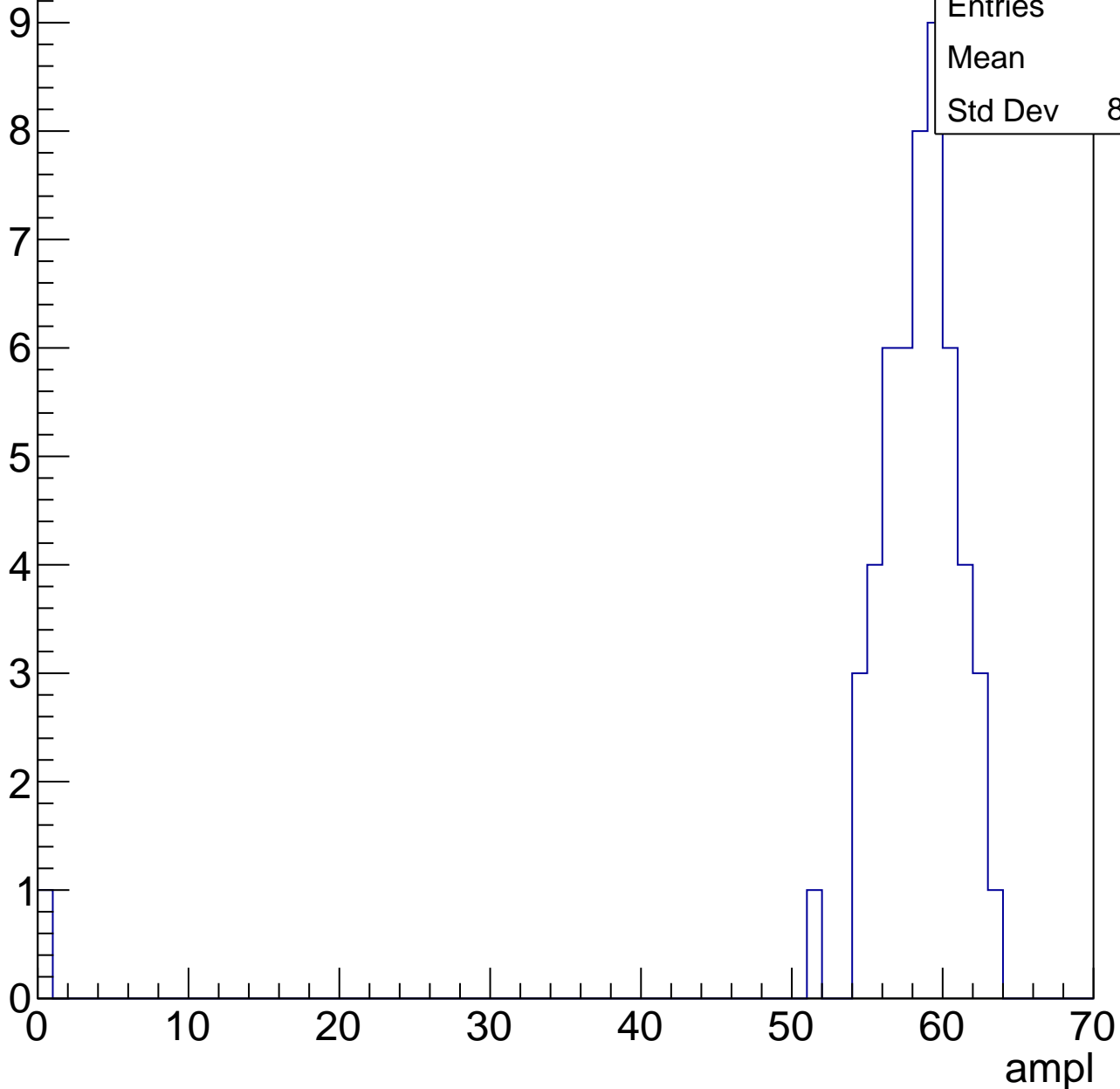


B0L002S, U9-ch117, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.9
Std Dev	8.328

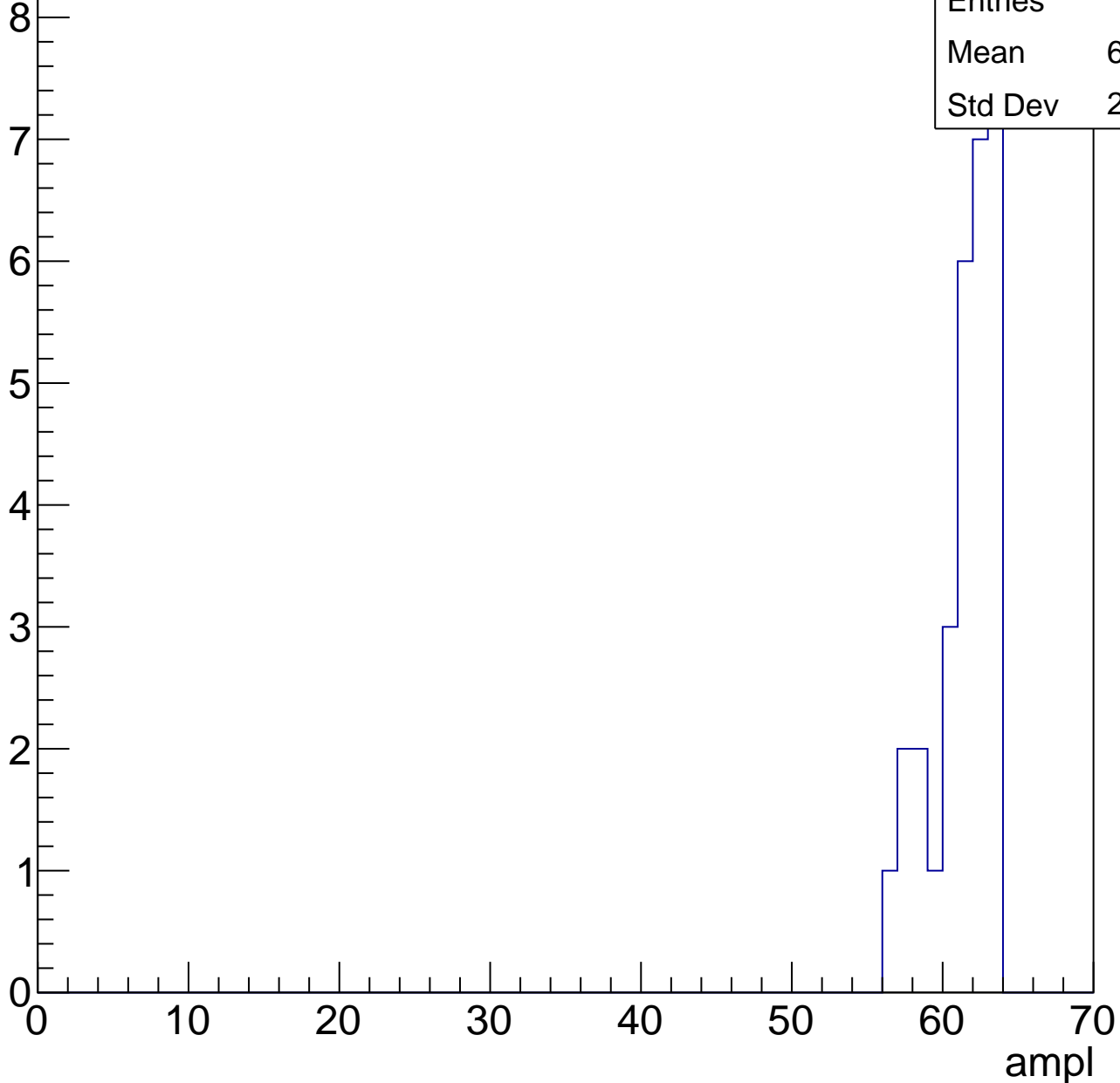


B0L002S, U9-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	60.97
Std Dev	2.008



B0L002S, U9-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U9-ch118, adc0

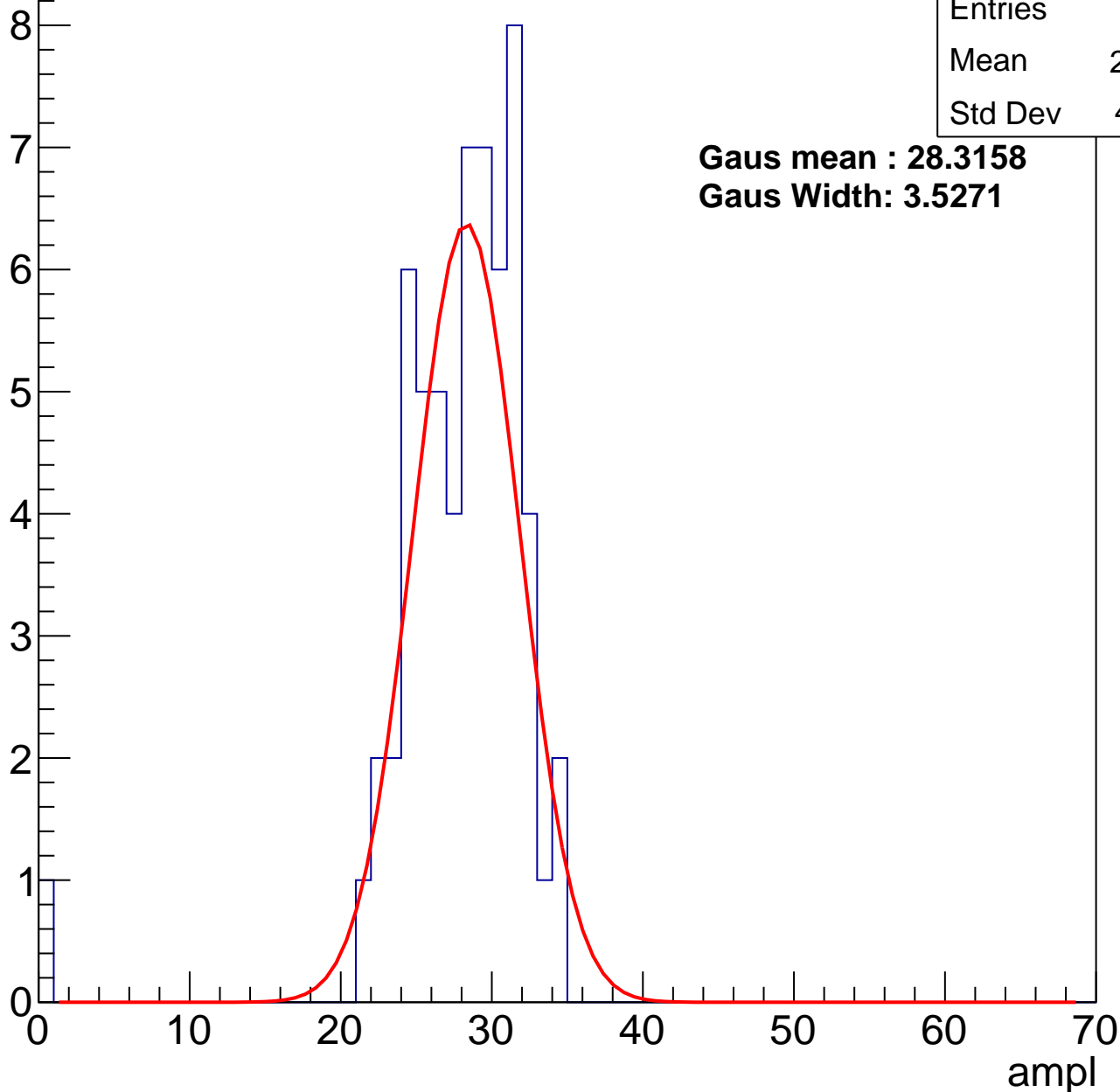
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	27.44
Std Dev	4.731

Gaus mean : 28.3158

Gaus Width: 3.5271



B0L002S, U9-ch118, adc1

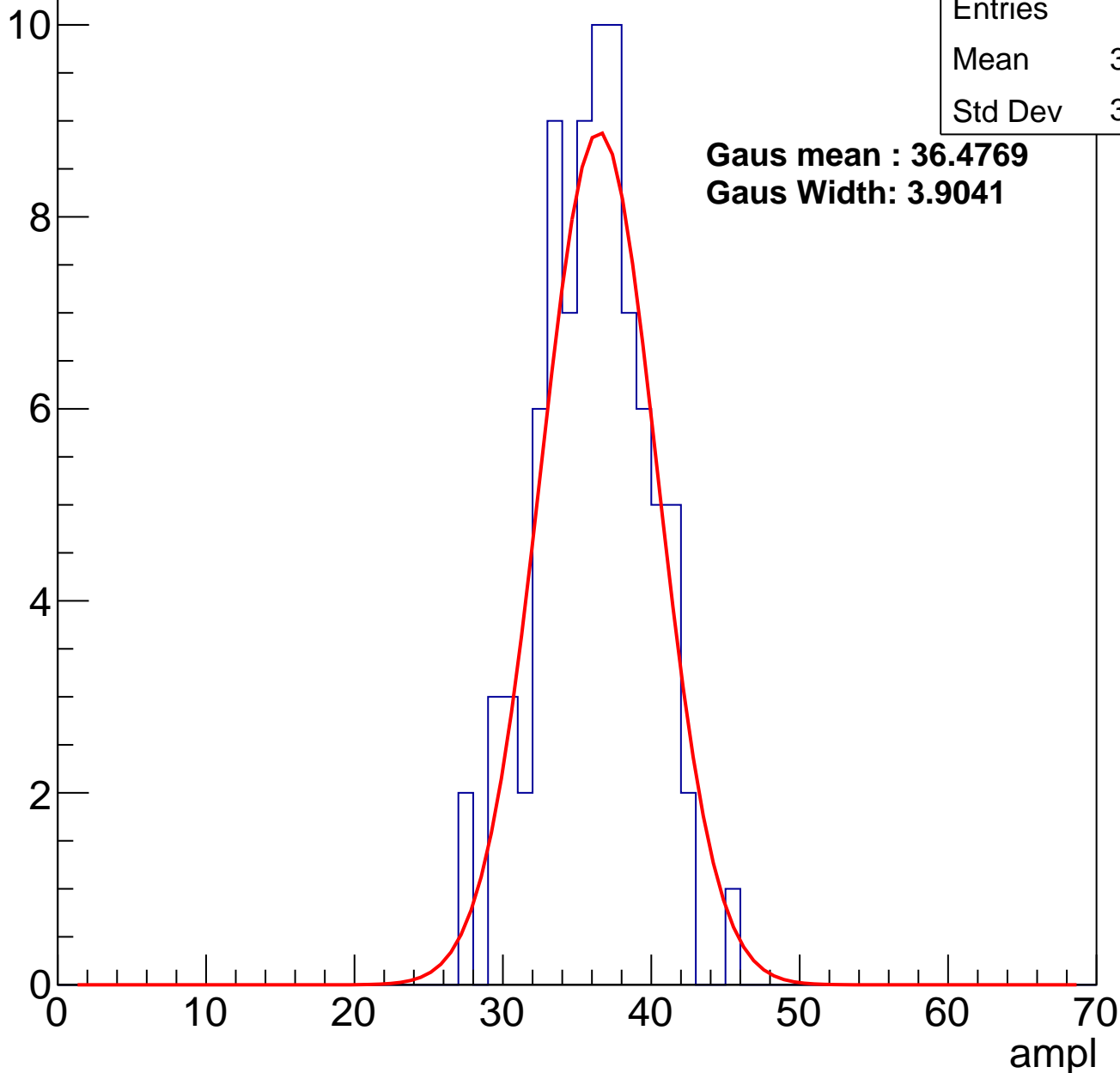
calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	35.62
Std Dev	3.586

Gaus mean : 36.4769

Gaus Width: 3.9041

Entry



B0L002S, U9-ch118, adc2

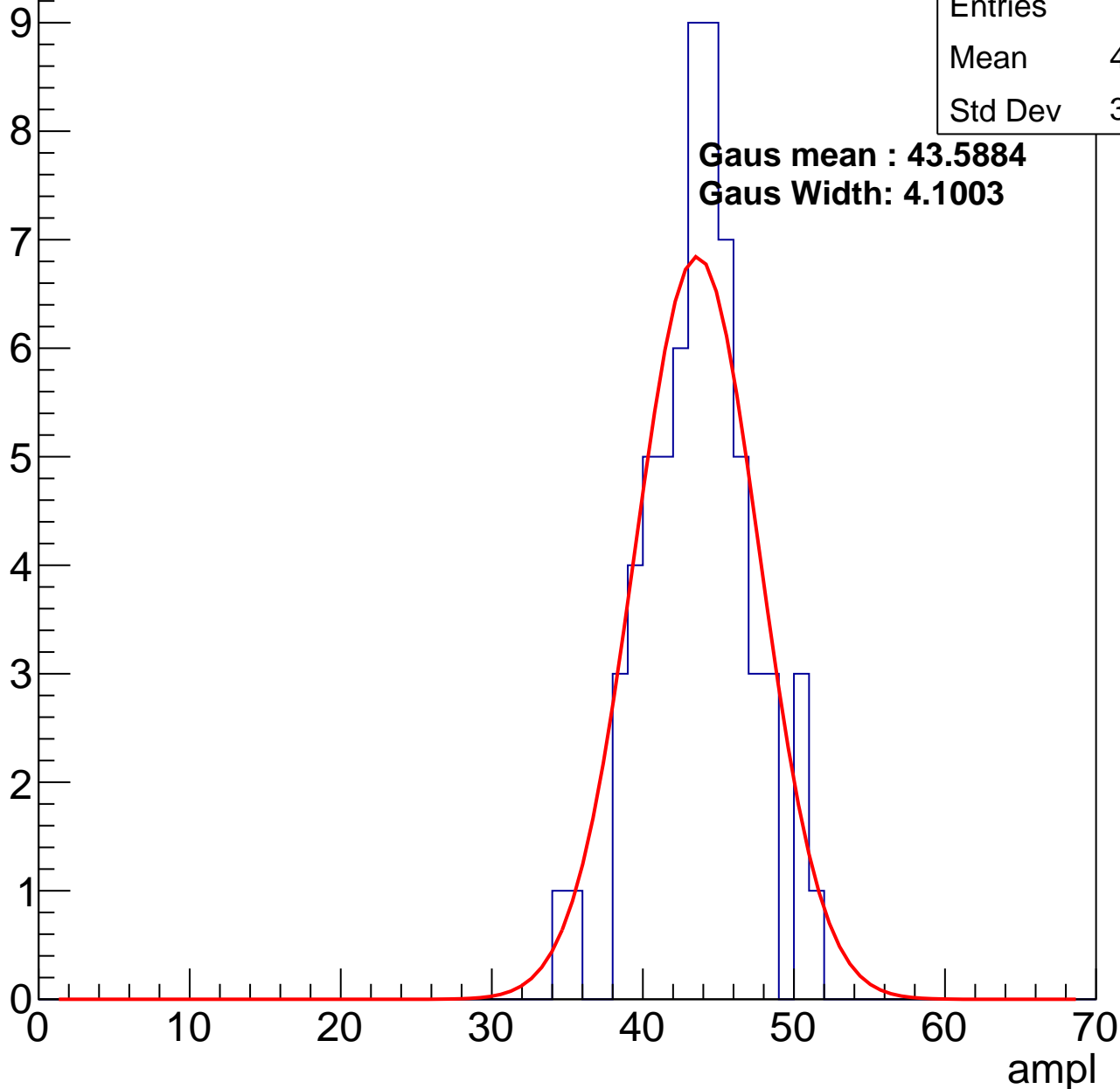
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.23
Std Dev	3.436

Gaus mean : 43.5884

Gaus Width: 4.1003

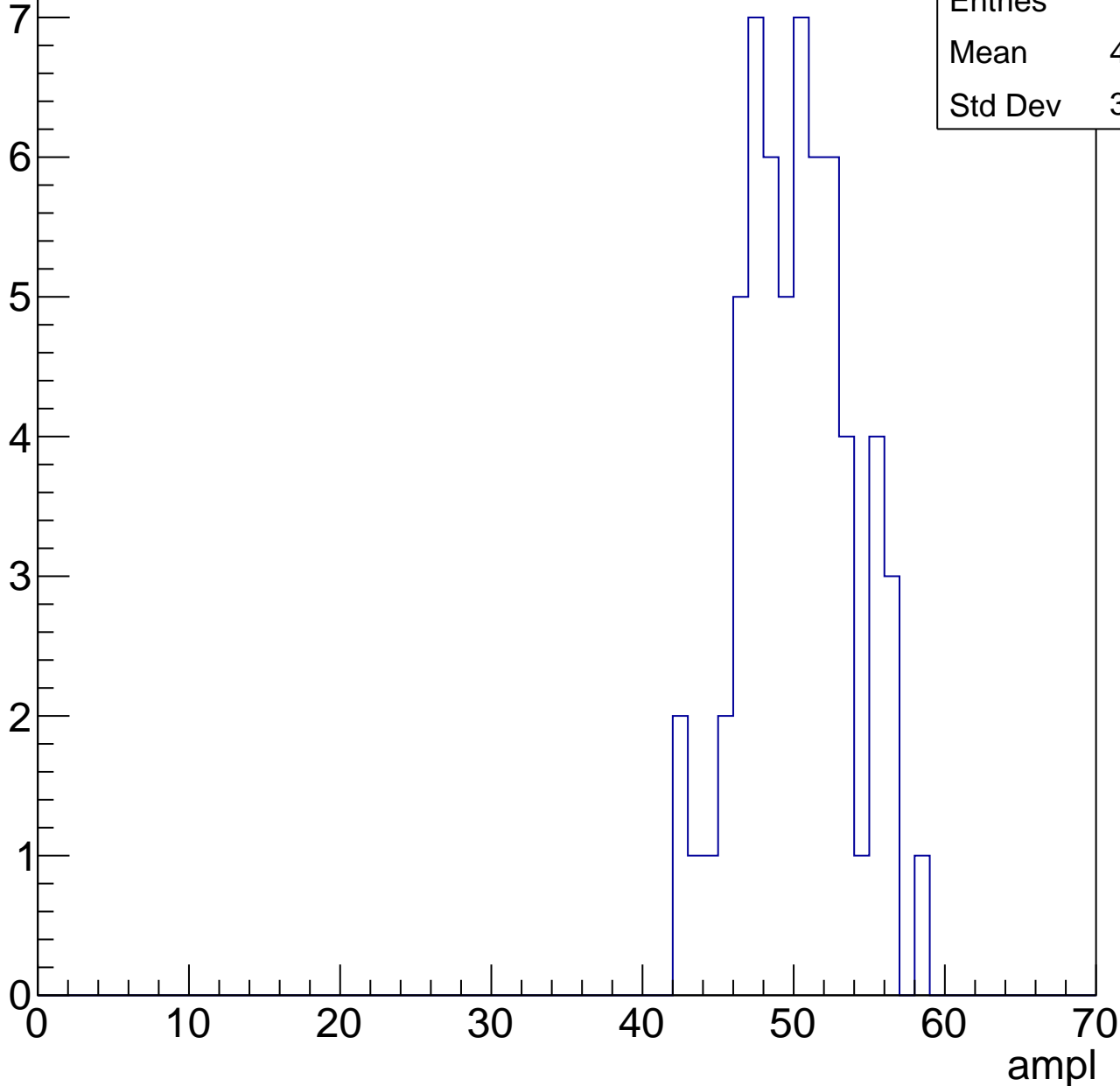


B0L002S, U9-ch118, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.72
Std Dev	3.586

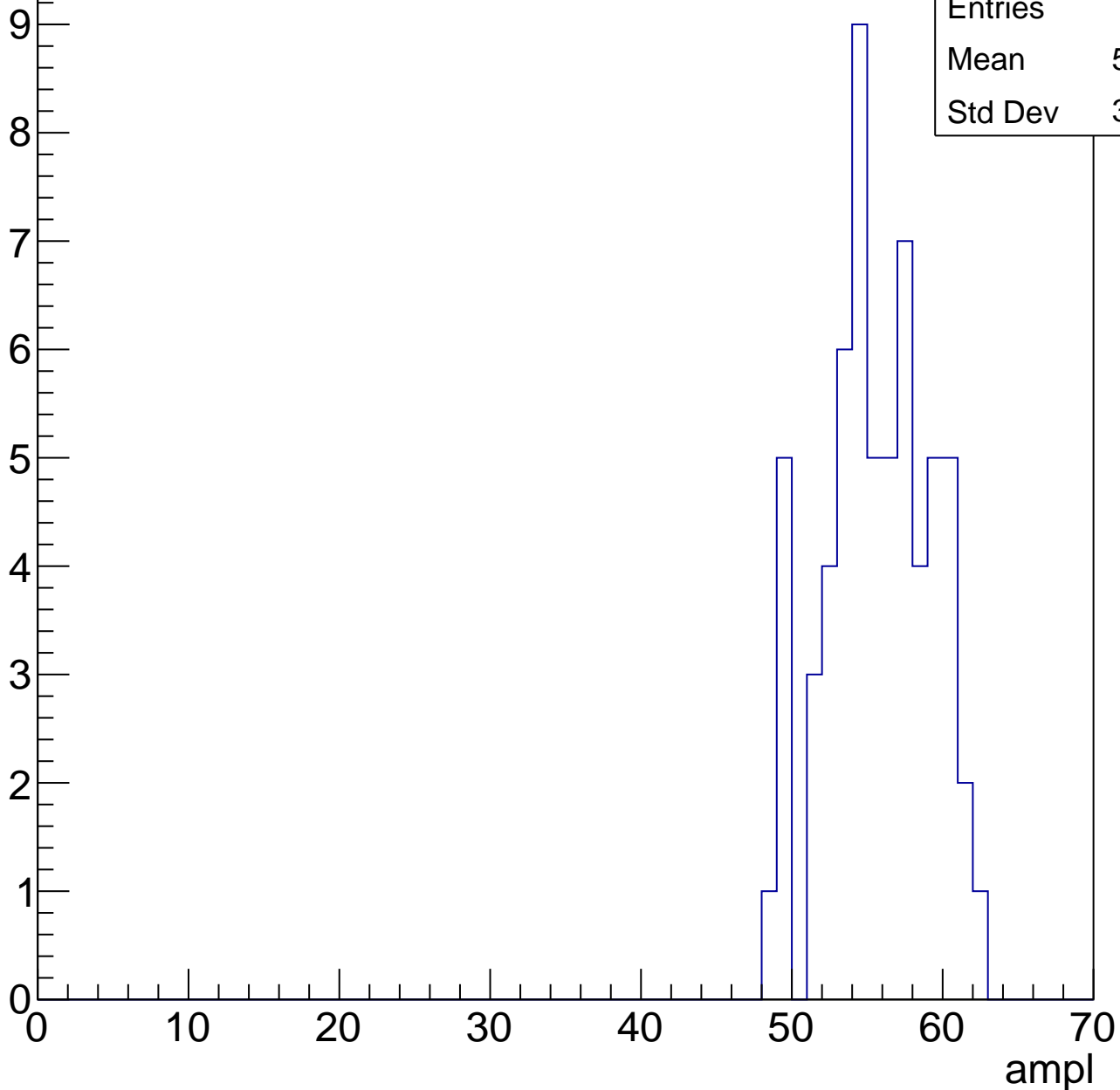


B0L002S, U9-ch118, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.21
Std Dev	3.451

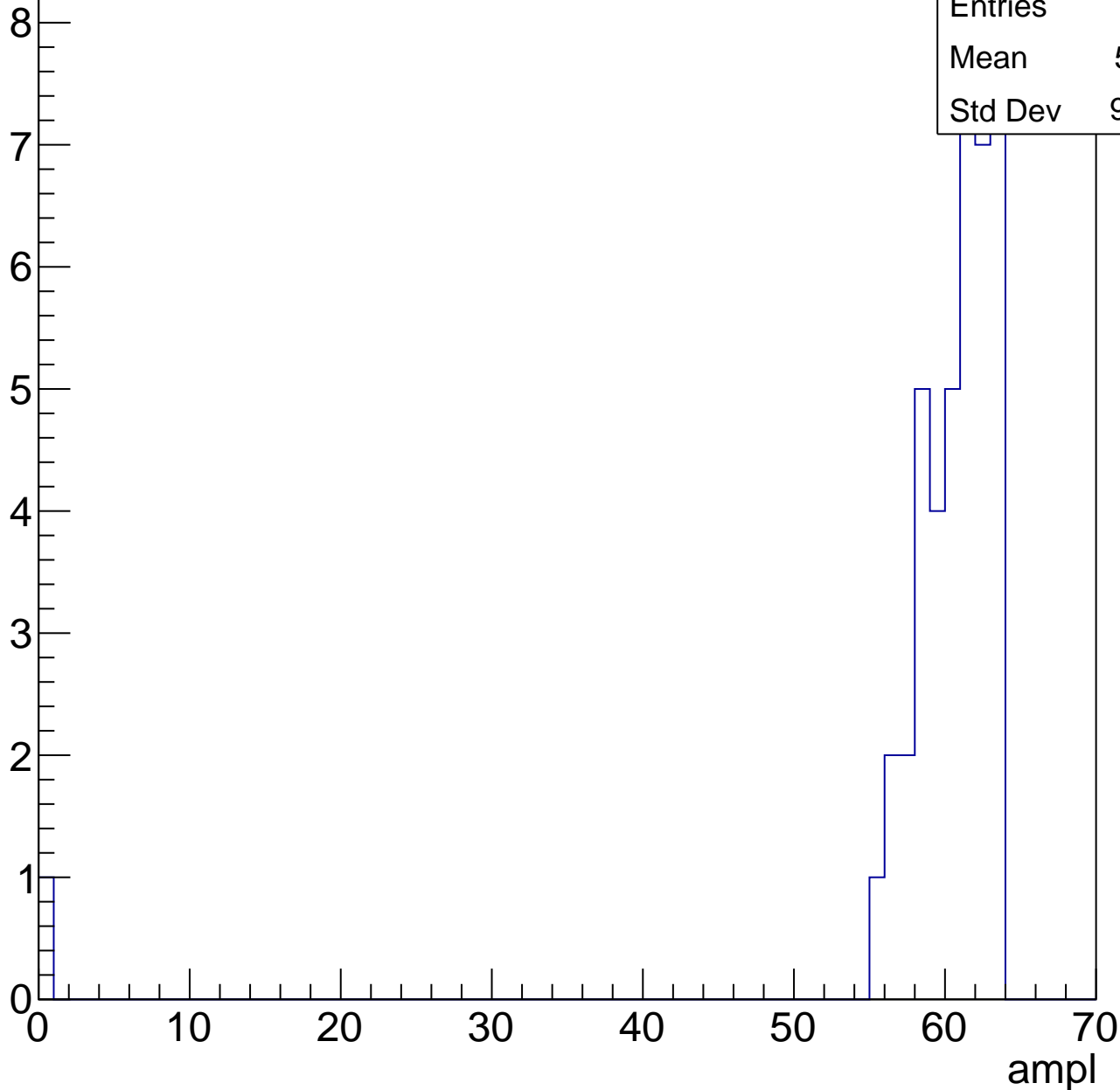


B0L002S, U9-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

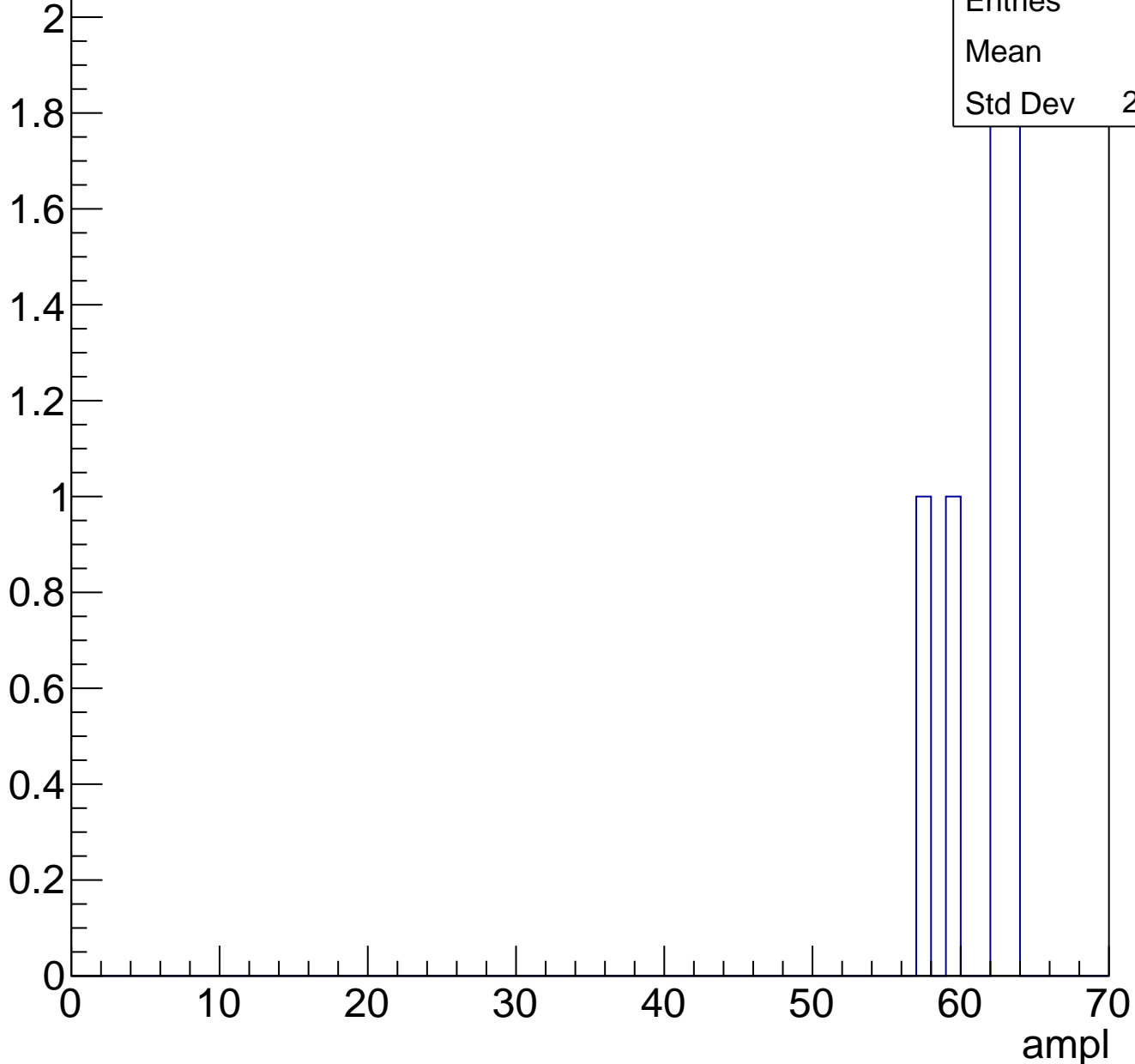
Entries	43
Mean	58.91
Std Dev	9.346



B0L002S, U9-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch119, adc0

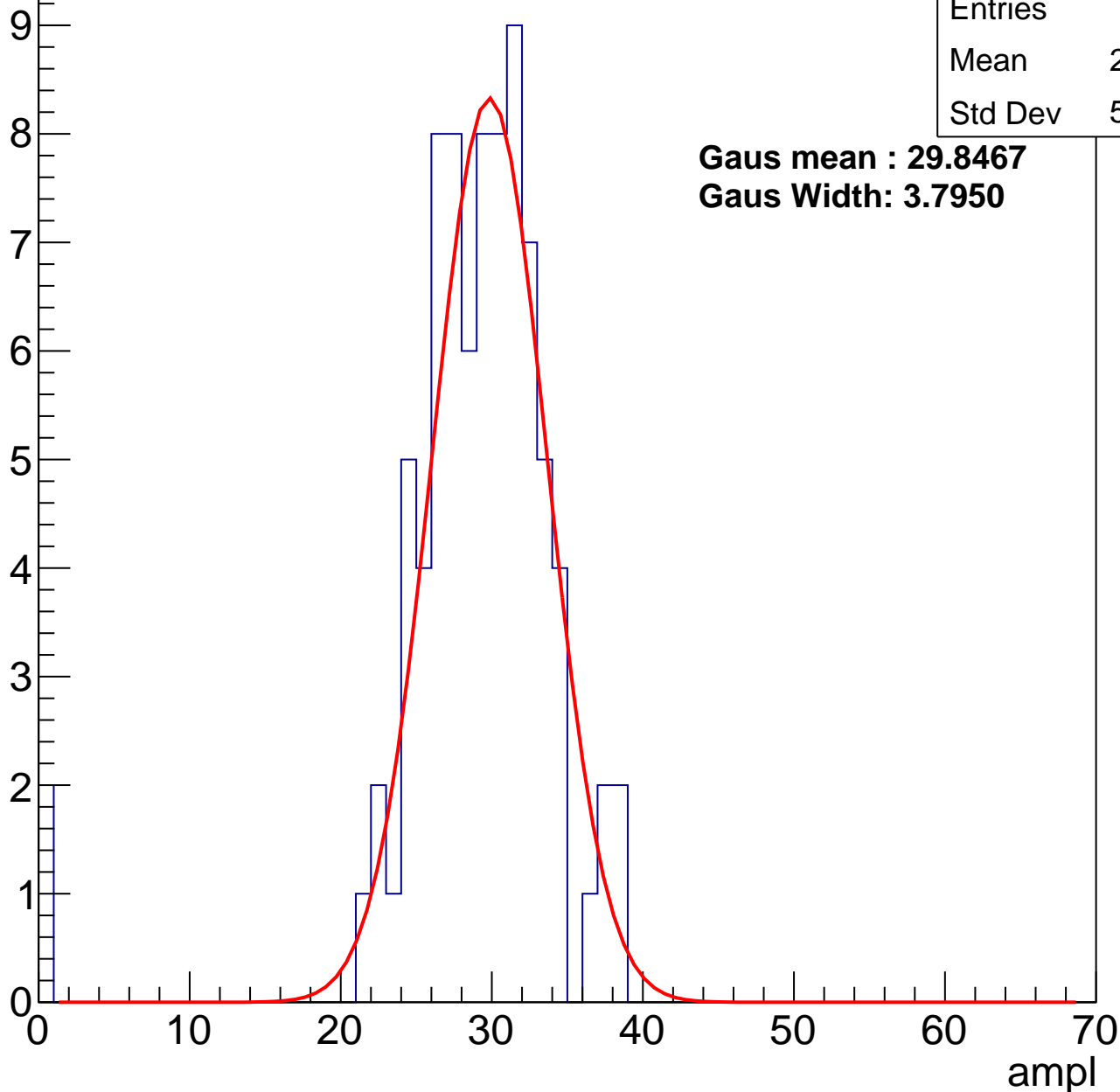
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	28.46
Std Dev	5.788

Gaus mean : 29.8467

Gaus Width: 3.7950



B0L002S, U9-ch119, adc1

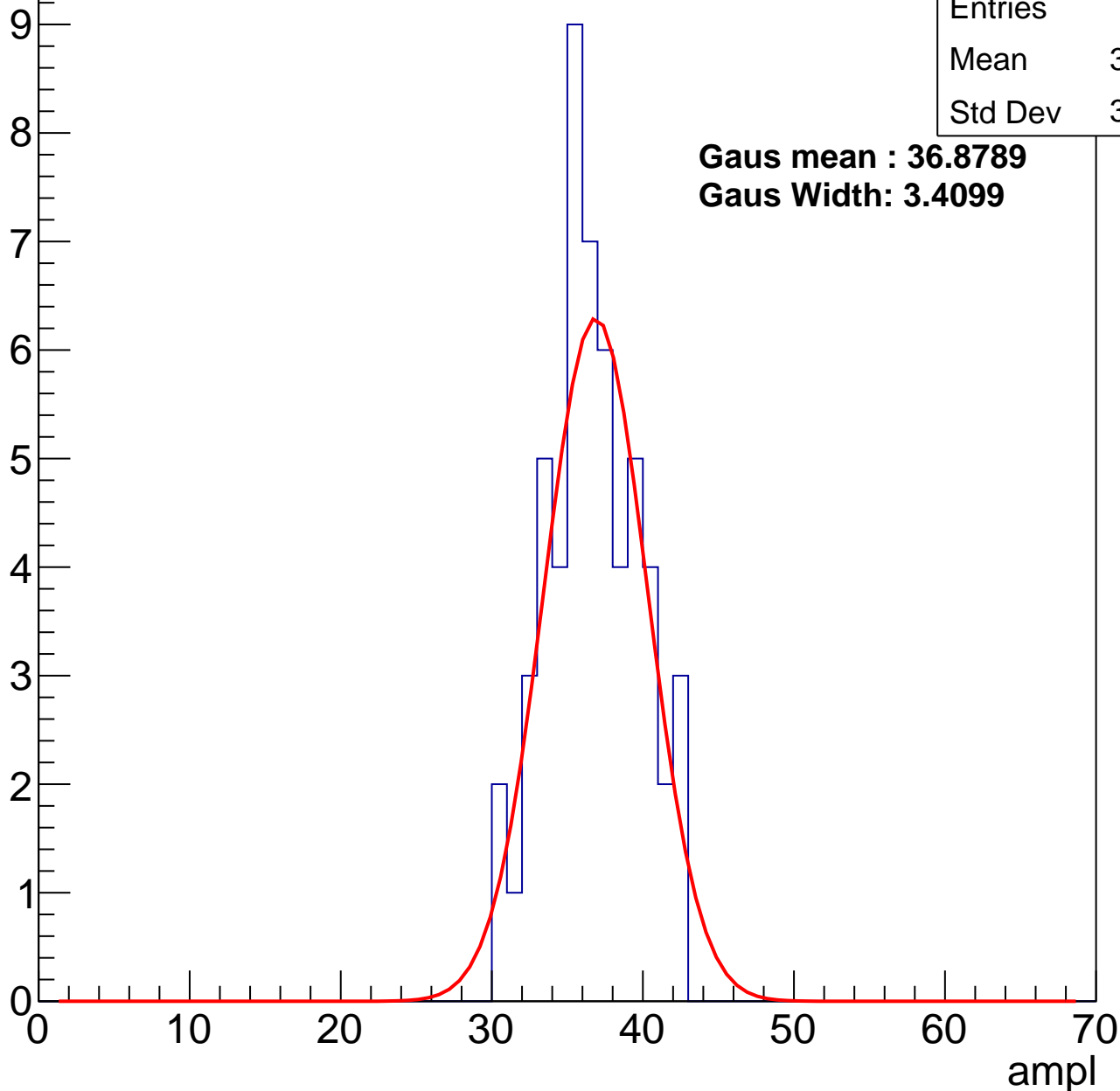
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.22
Std Dev	3.019

Gaus mean : 36.8789

Gaus Width: 3.4099



B0L002S, U9-ch119, adc2

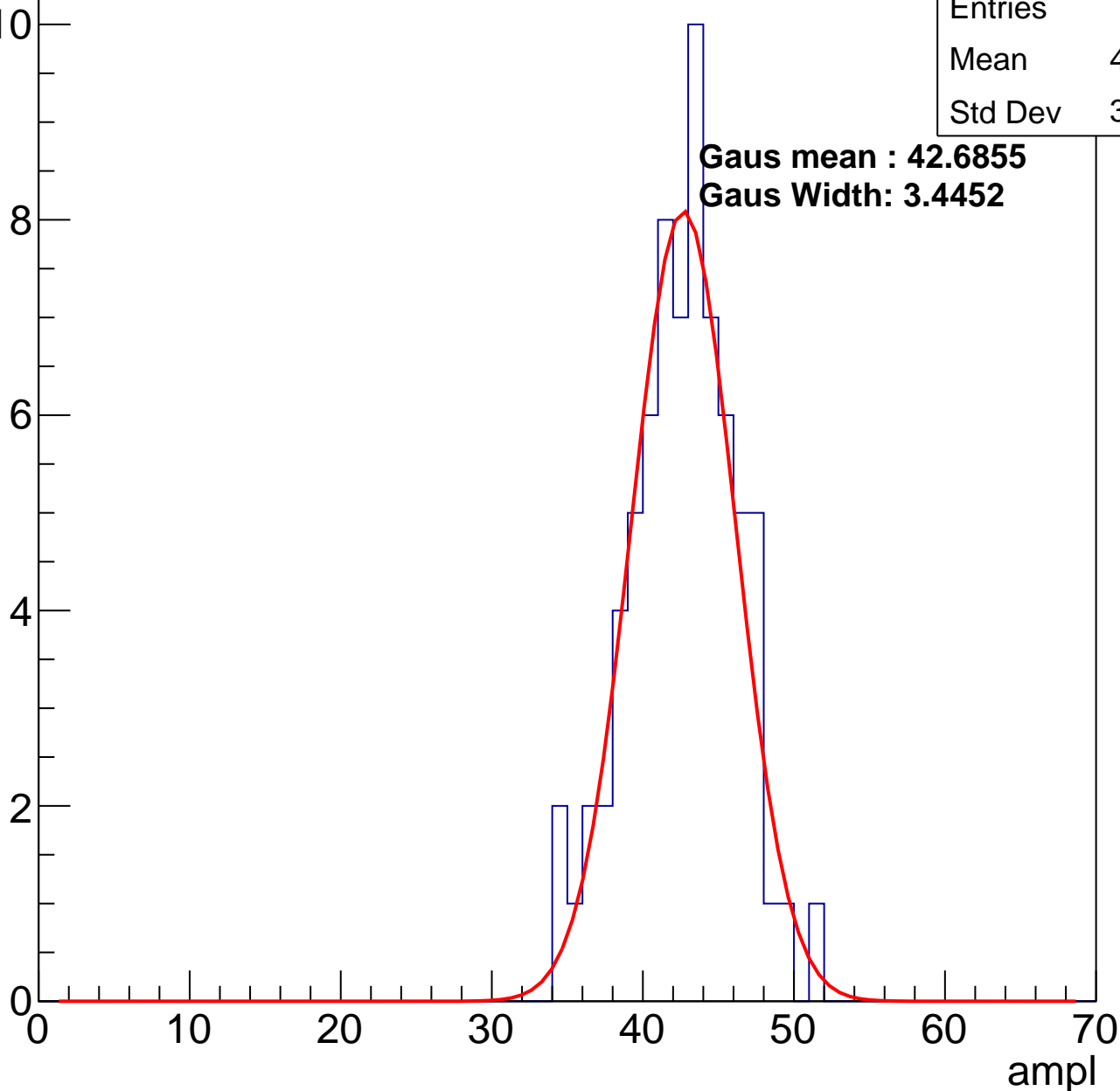
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.18
Std Dev	3.513

Gaus mean : 42.6855

Gaus Width: 3.4452

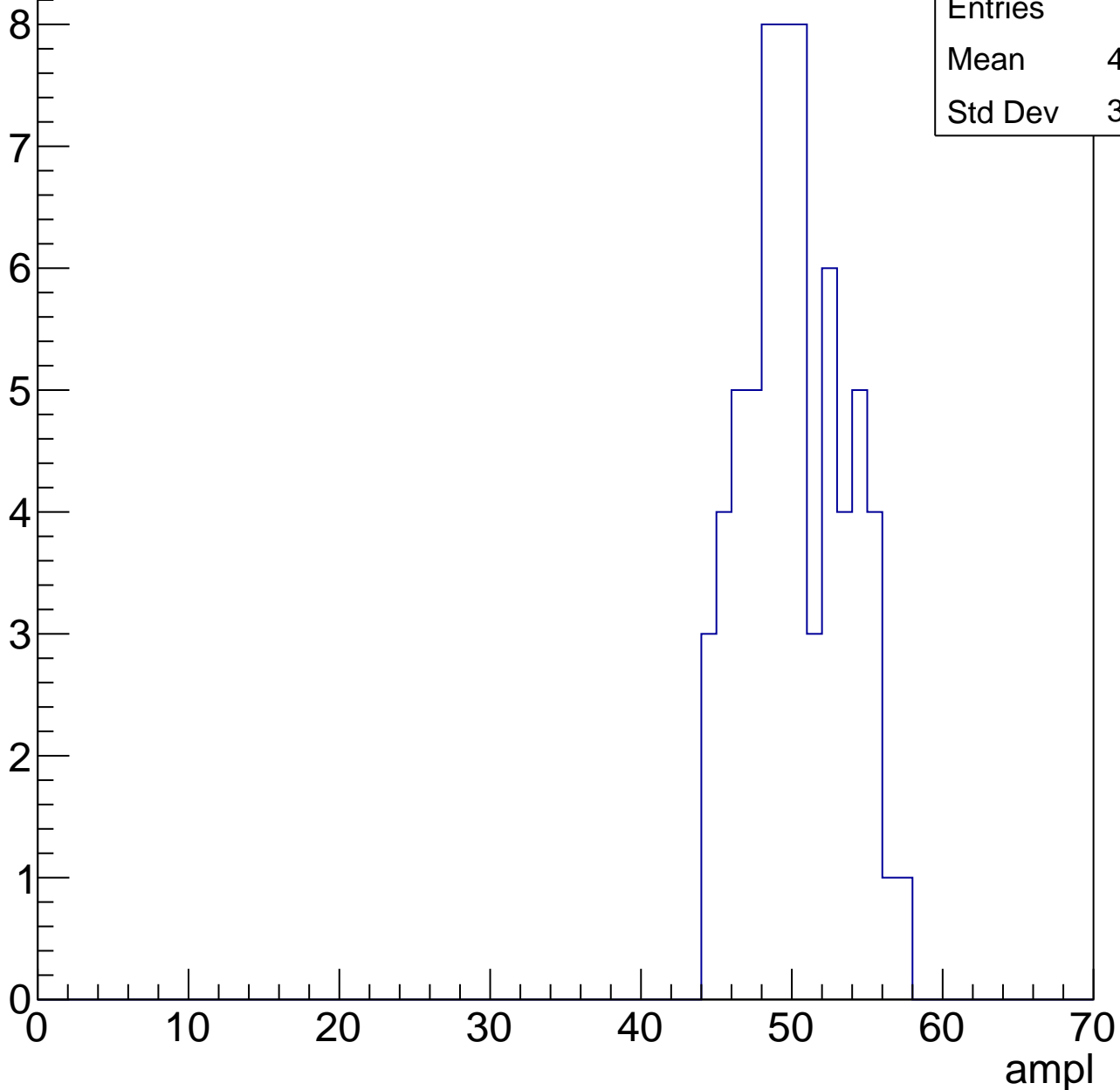


B0L002S, U9-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.74
Std Dev	3.274

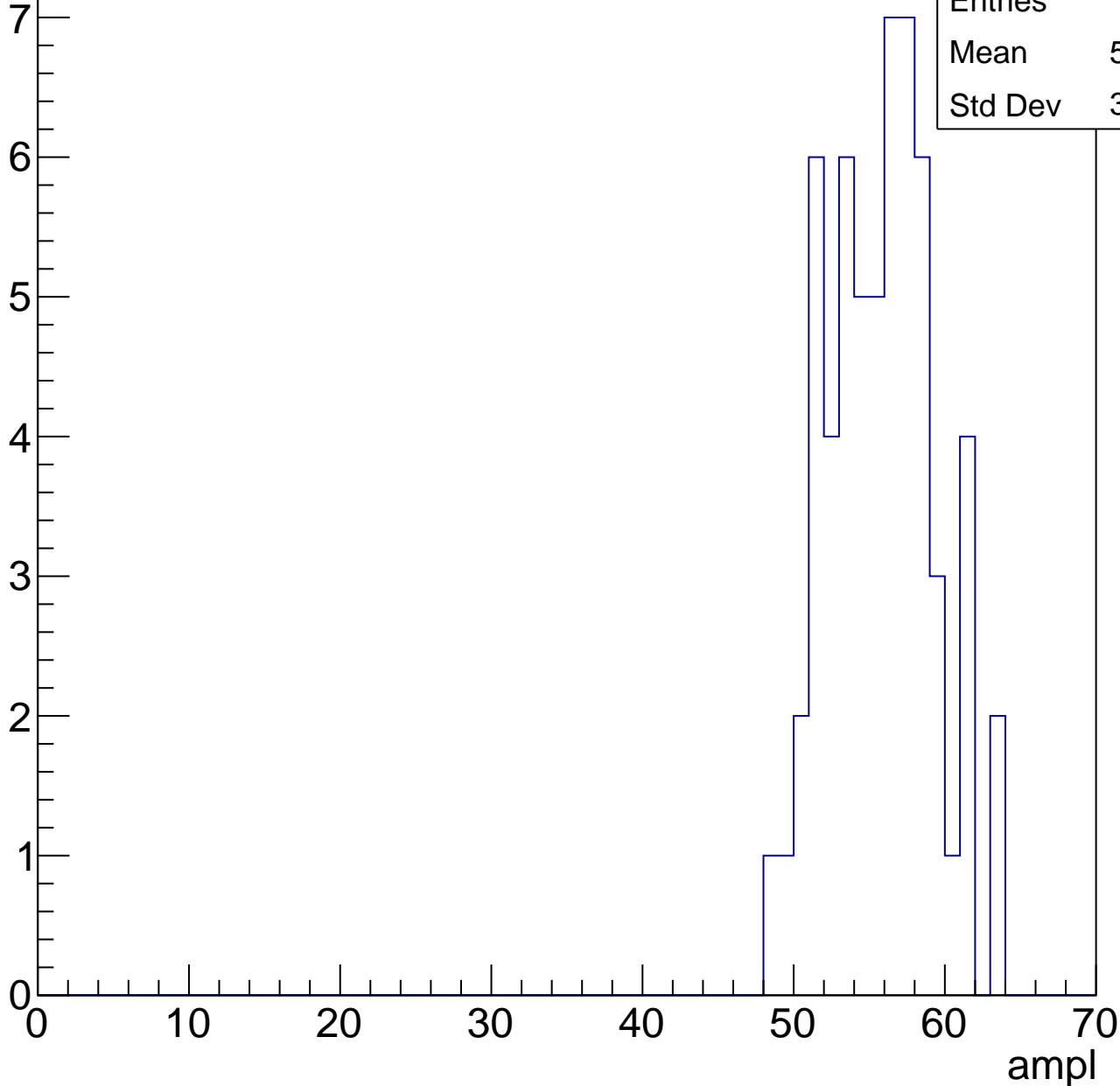


B0L002S, U9-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	55.33
Std Dev	3.472

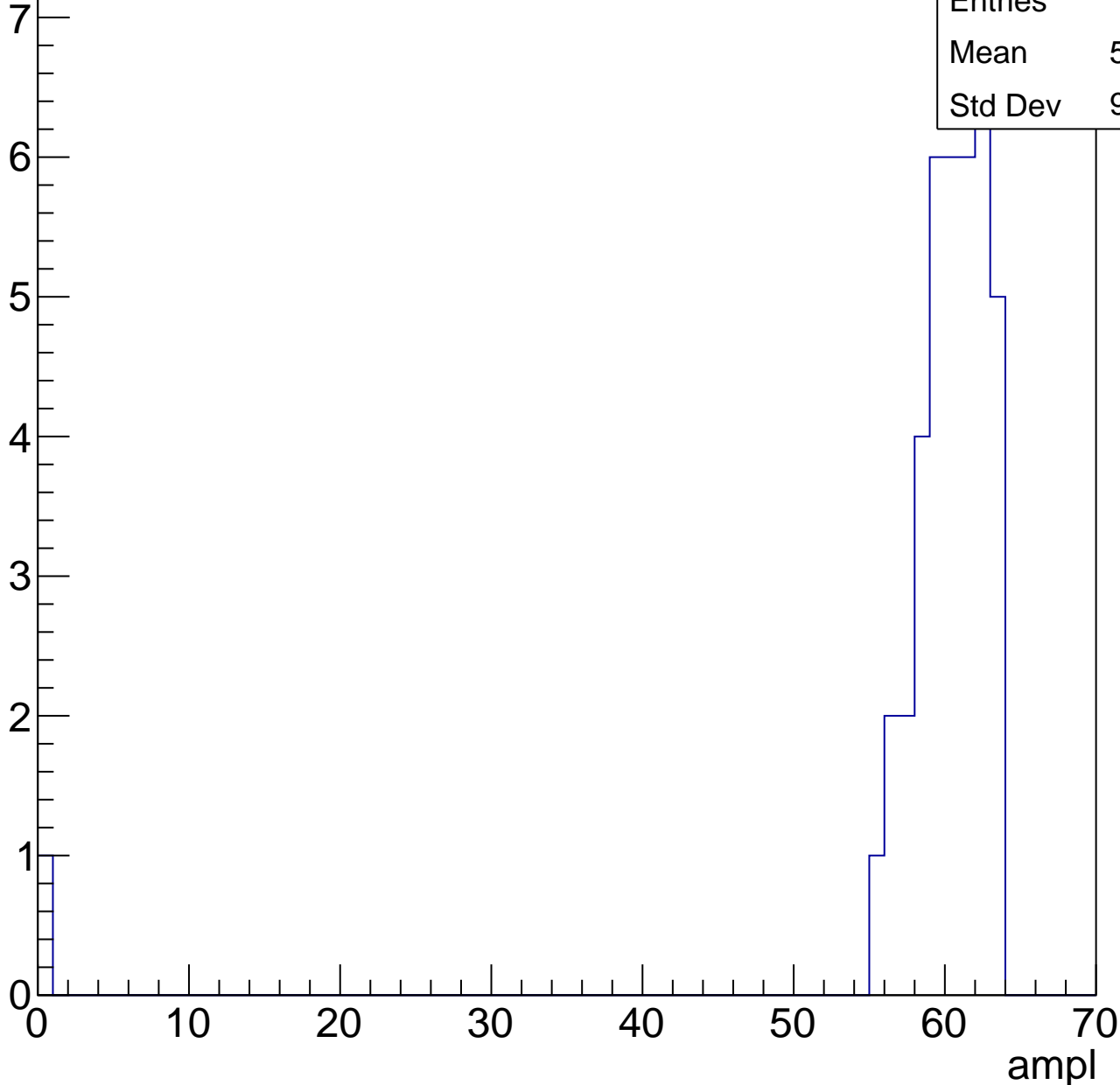


B0L002S, U9-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

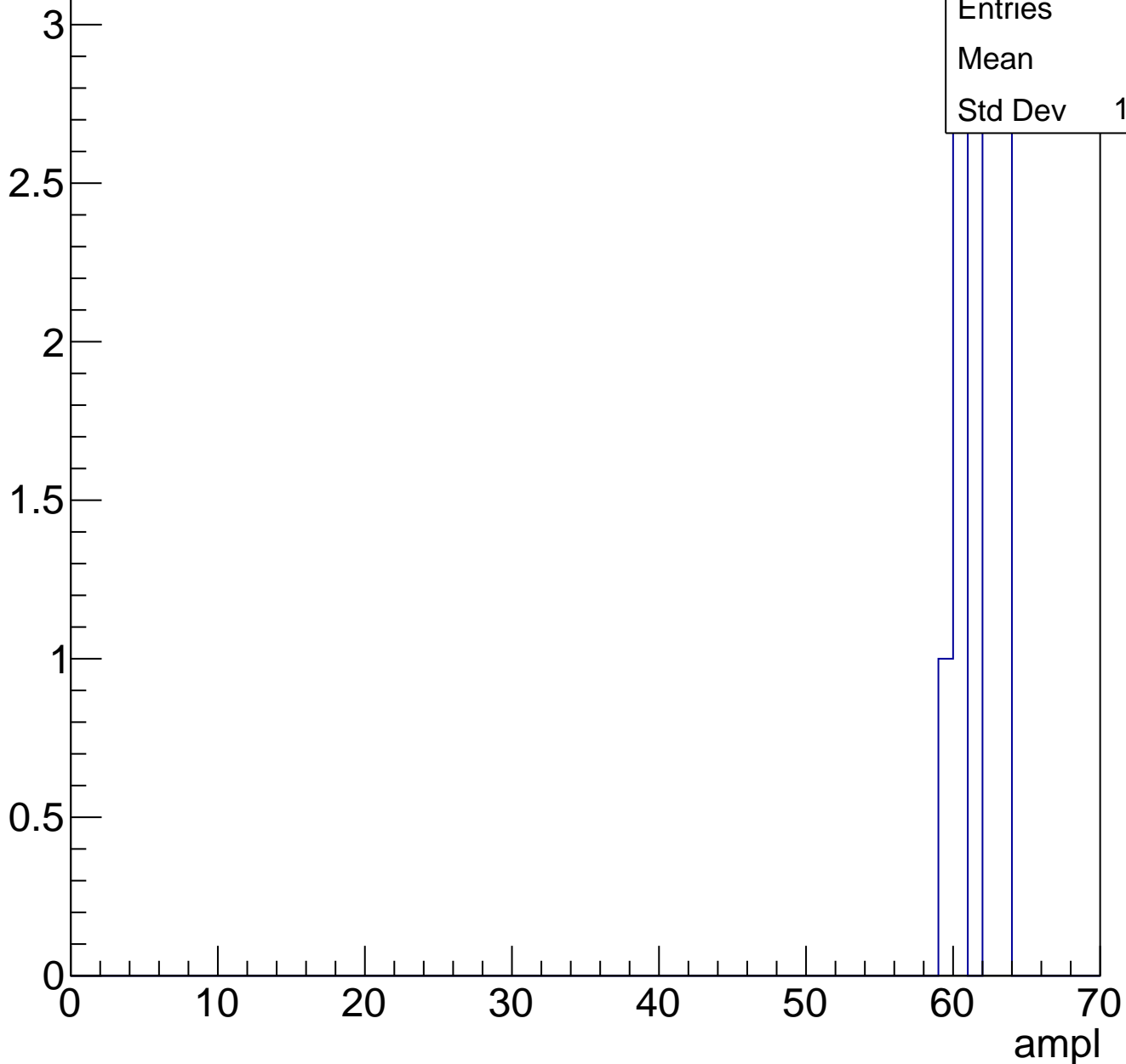
Entries	40
Mean	58.55
Std Dev	9.607



B0L002S, U9-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

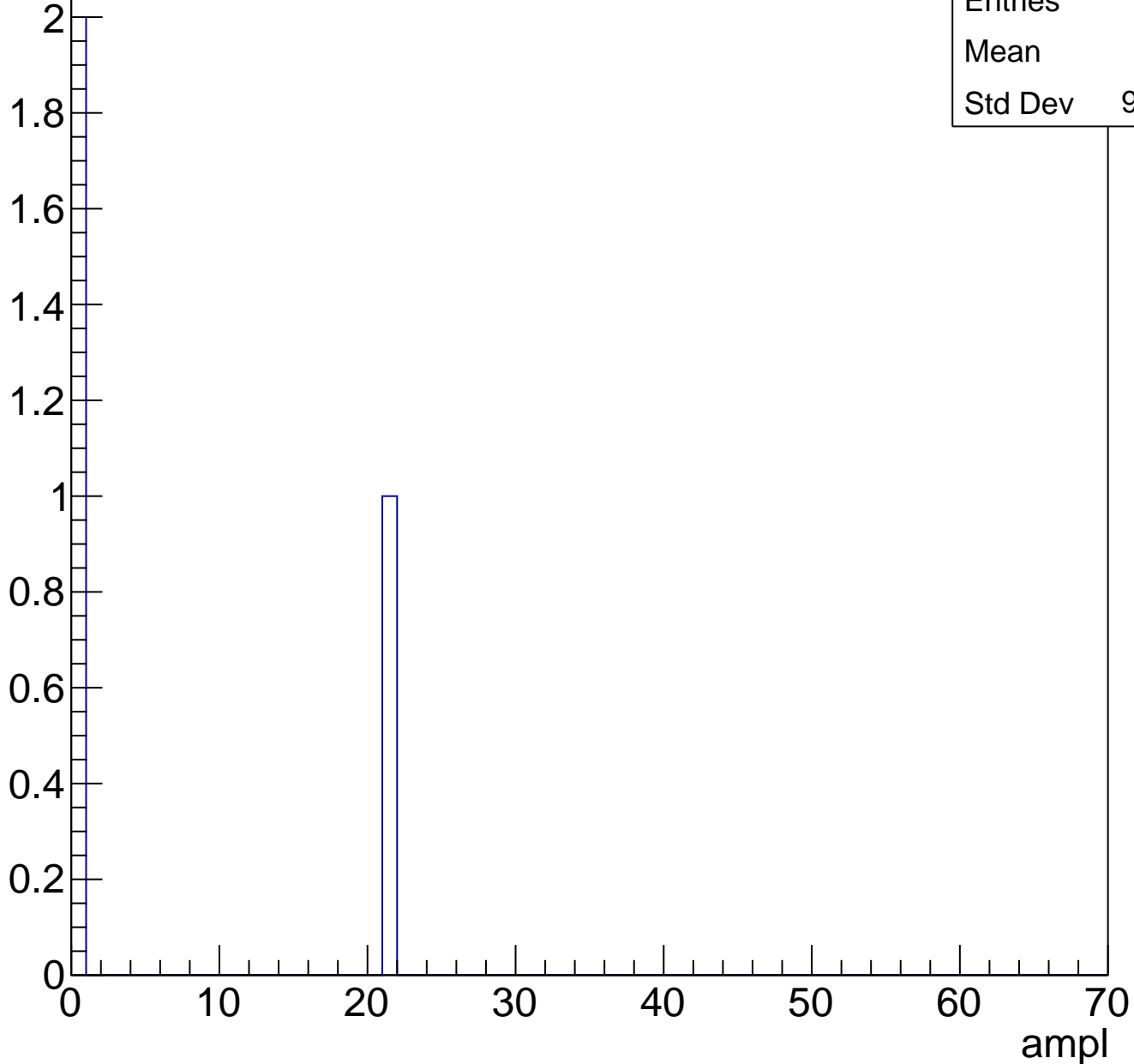
Entry



B0L002S, U9-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L002S, U9-ch120, adc0

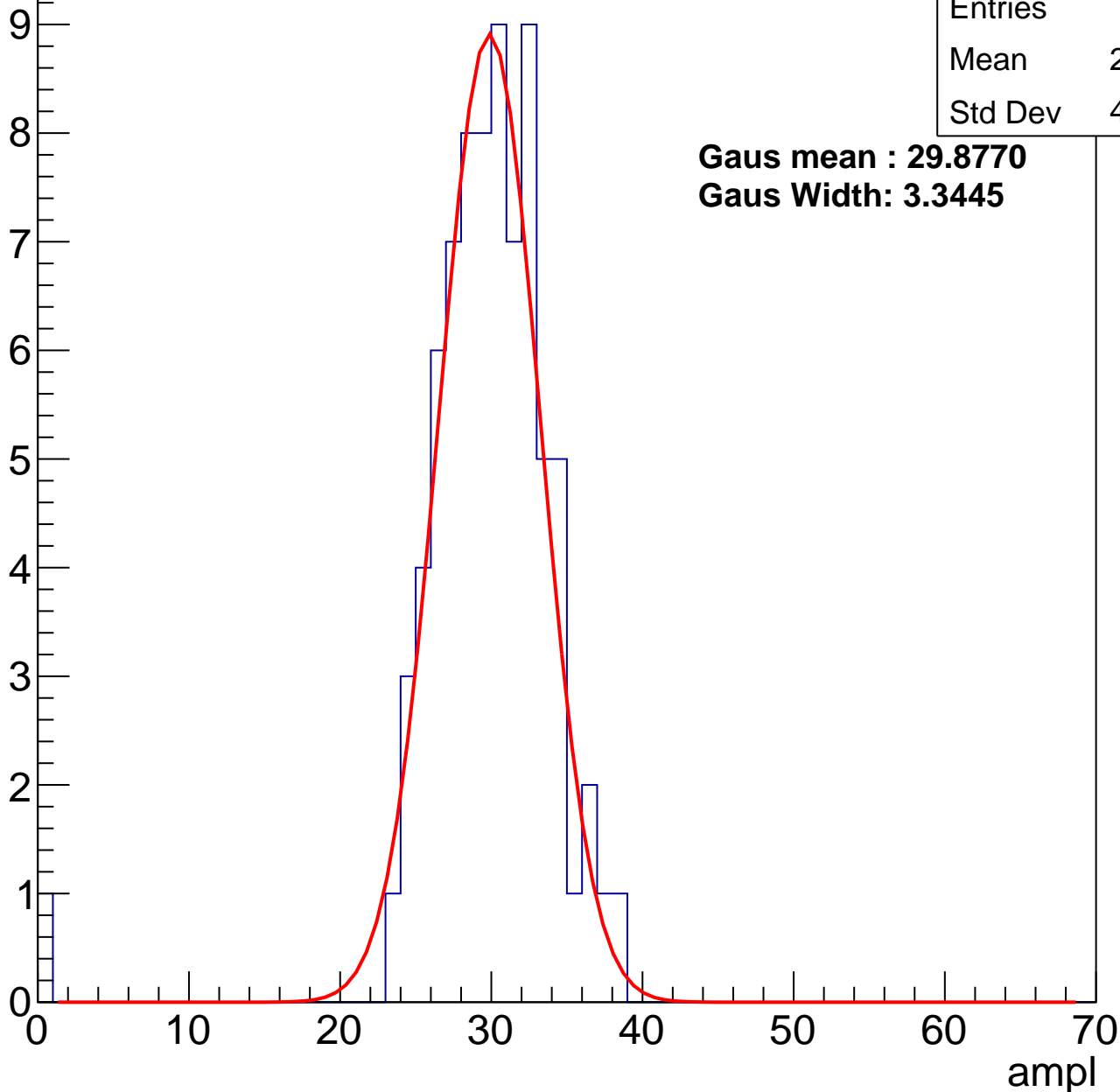
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.33
Std Dev	4.668

Gaus mean : 29.8770

Gaus Width: 3.3445



B0L002S, U9-ch120, adc1

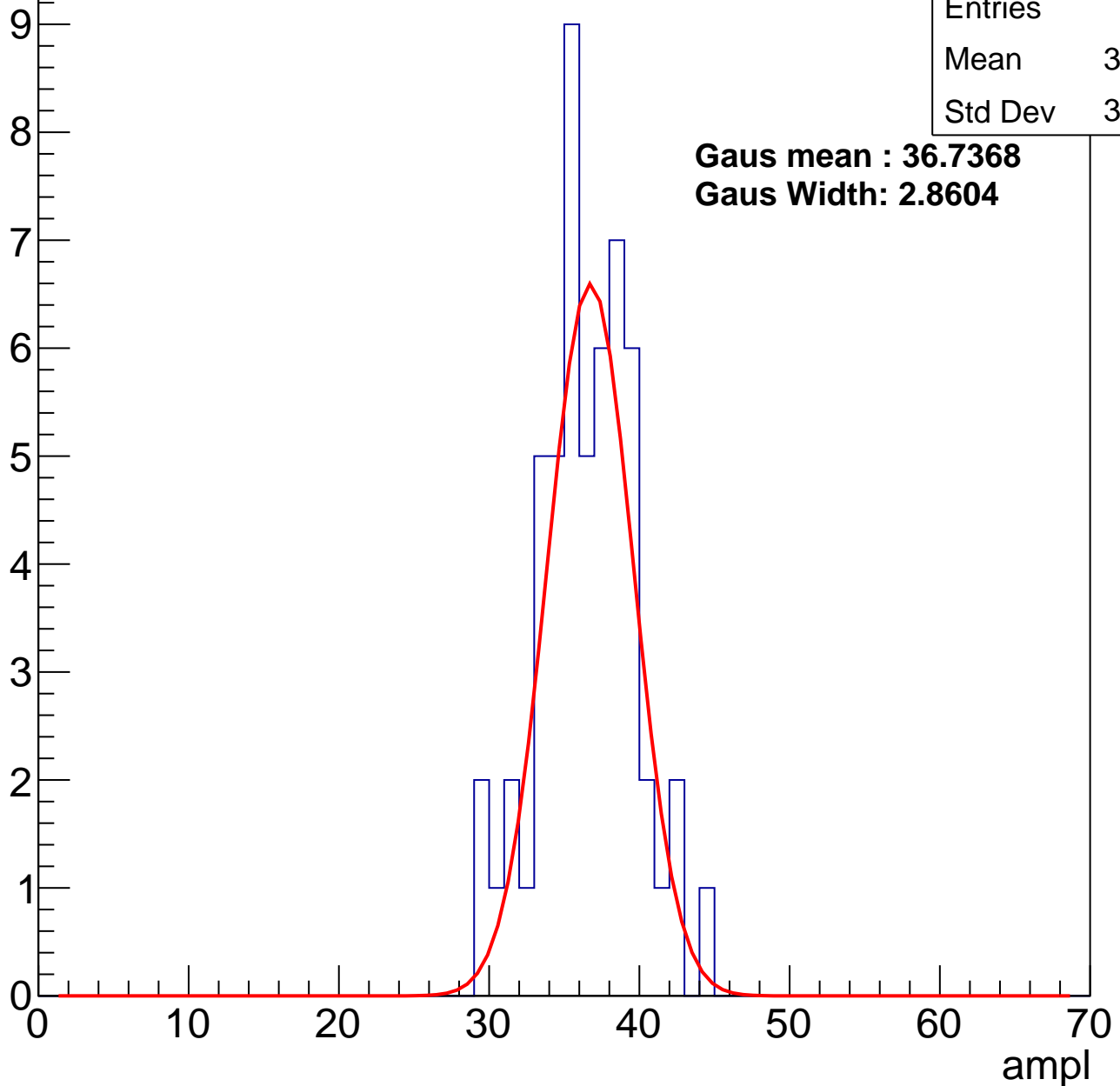
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.05
Std Dev	3.176

Gaus mean : 36.7368

Gaus Width: 2.8604



B0L002S, U9-ch120, adc2

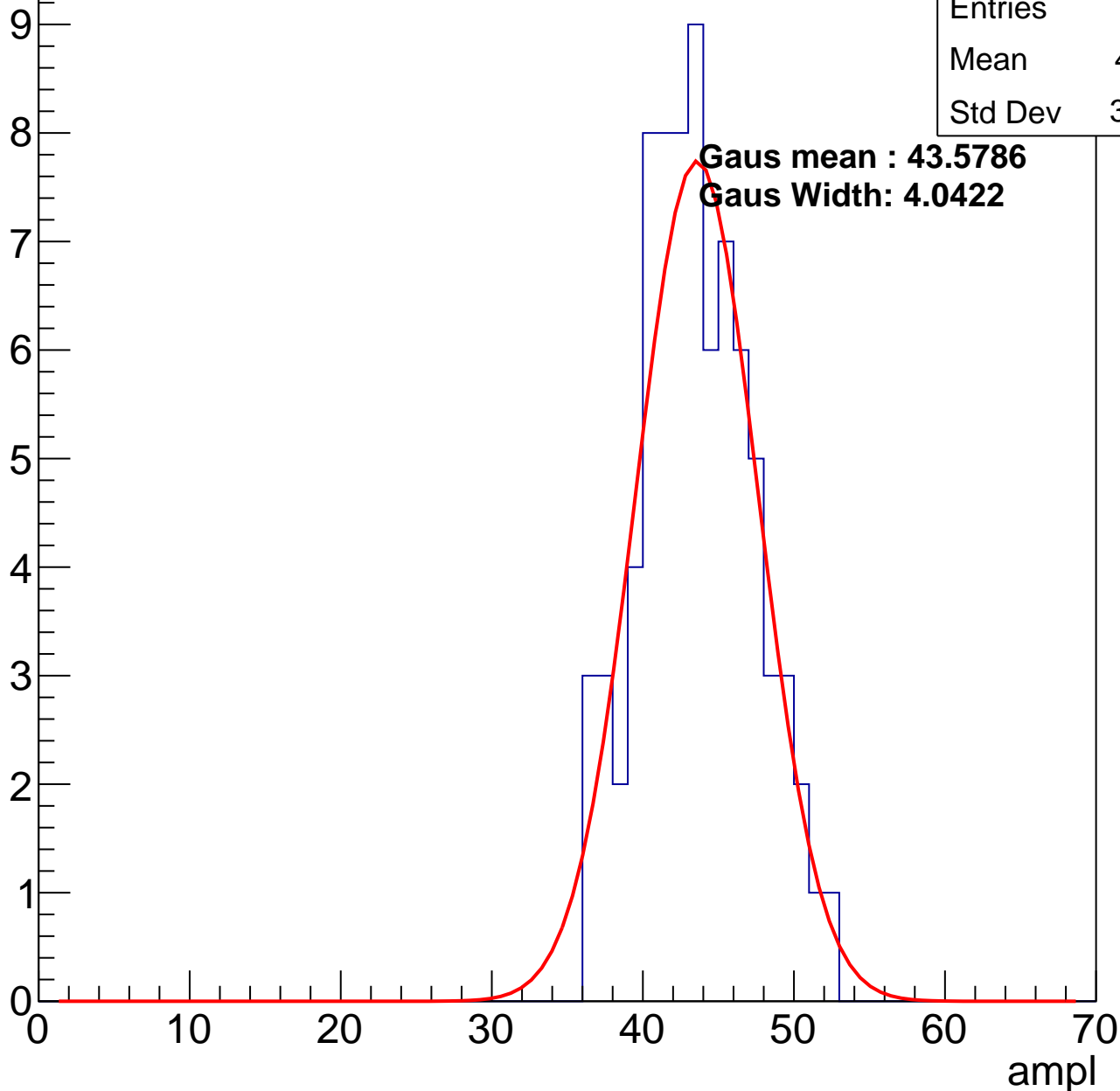
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	43.11
Std Dev	3.687

Gaus mean : 43.5786

Gaus Width: 4.0422

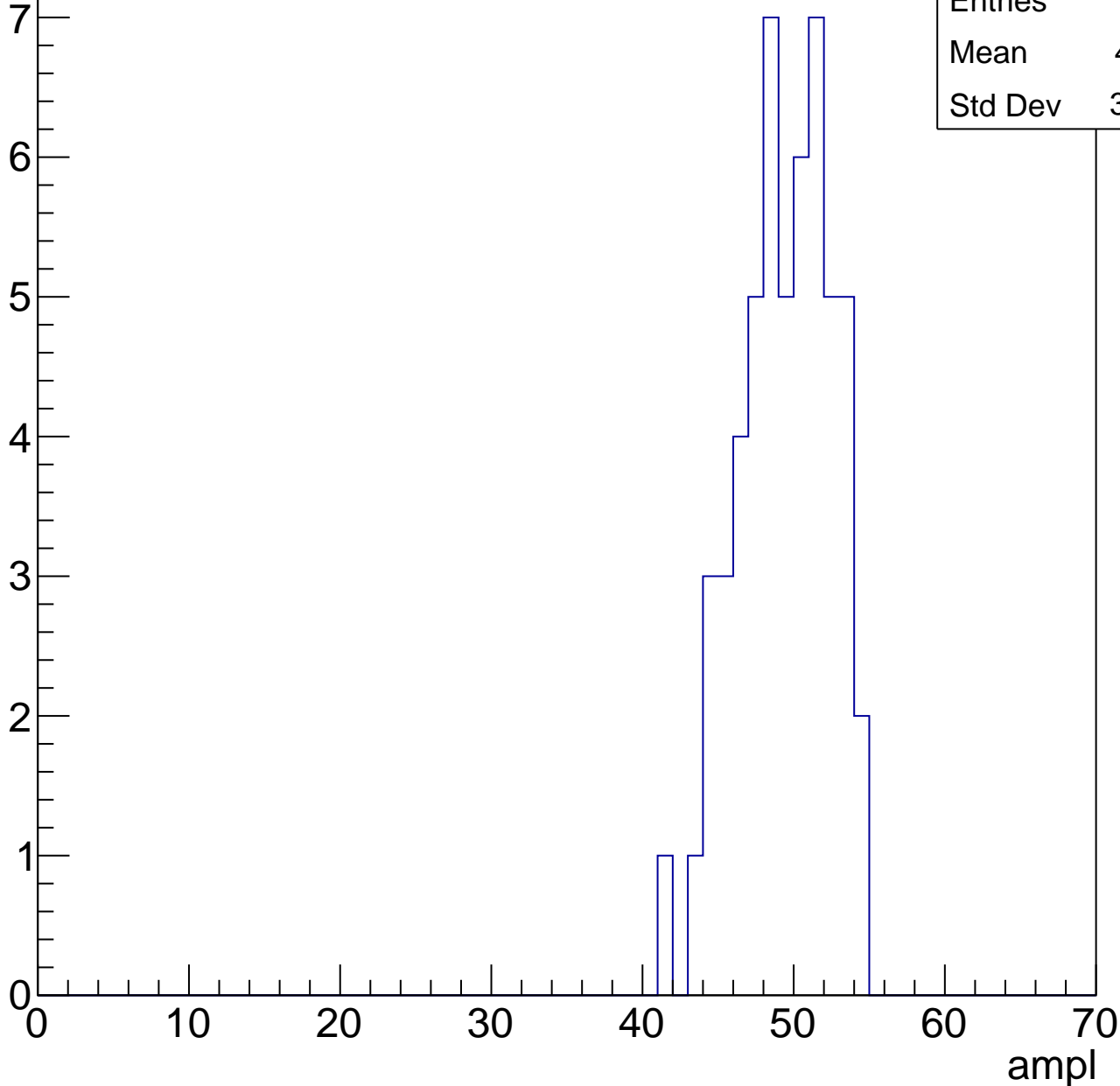


B0L002S, U9-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

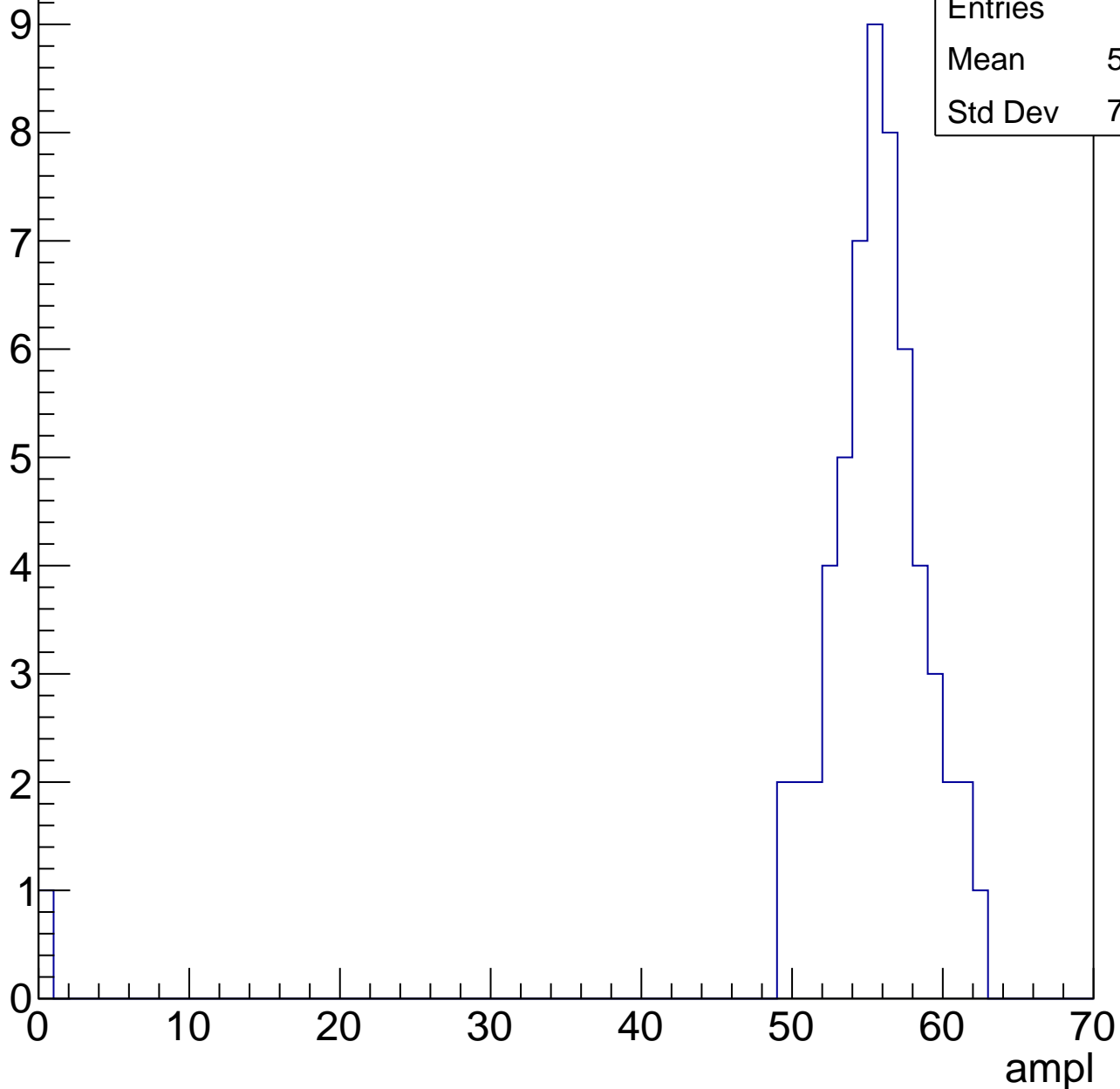
Entries	54
Mean	48.91
Std Dev	3.026



B0L002S, U9-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.9
Std Dev	2.252

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

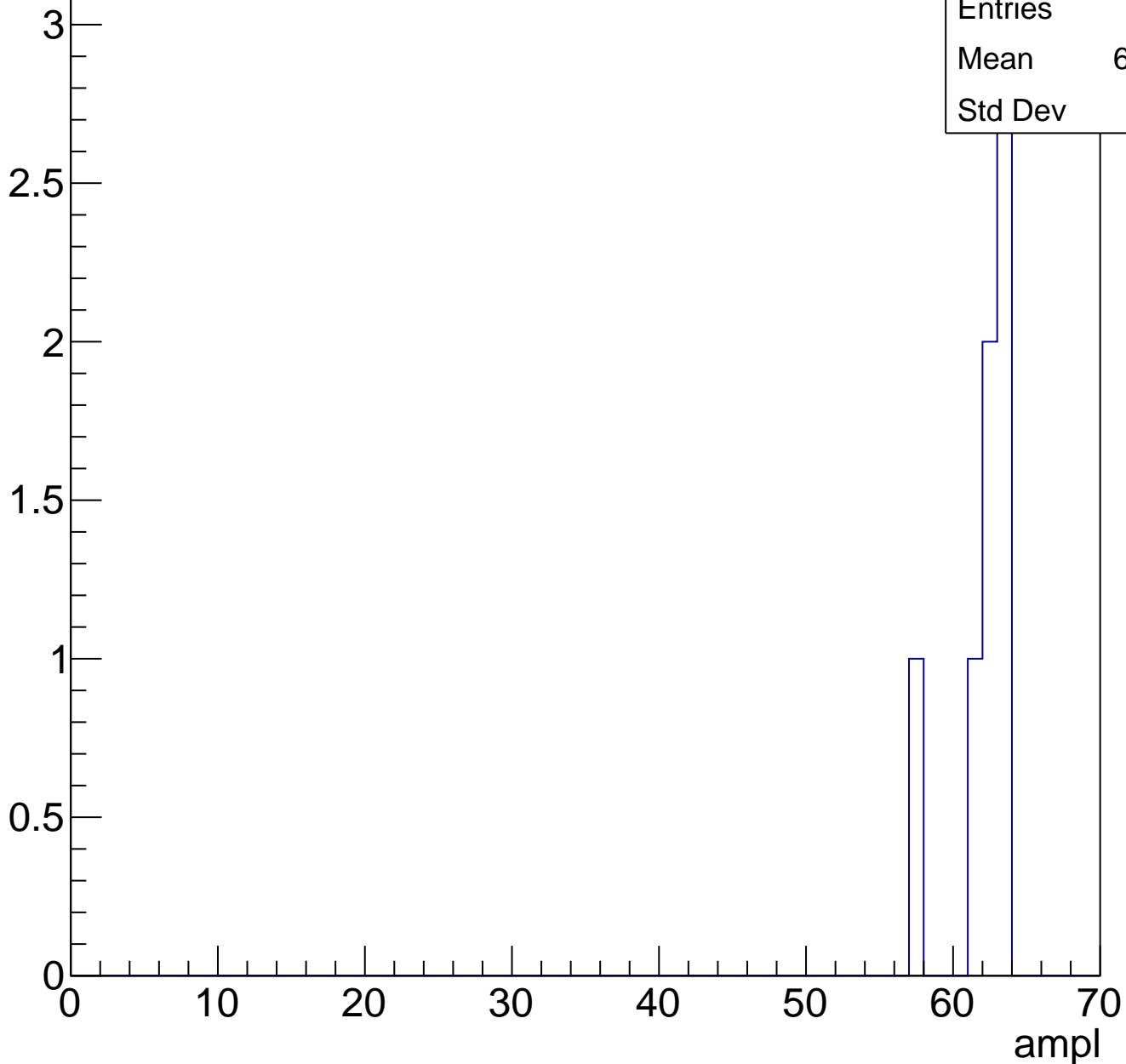
7

8

B0L002S, U9-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch121, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	91
Mean	30.16
Std Dev	4.775

Gaus mean : 31.1463

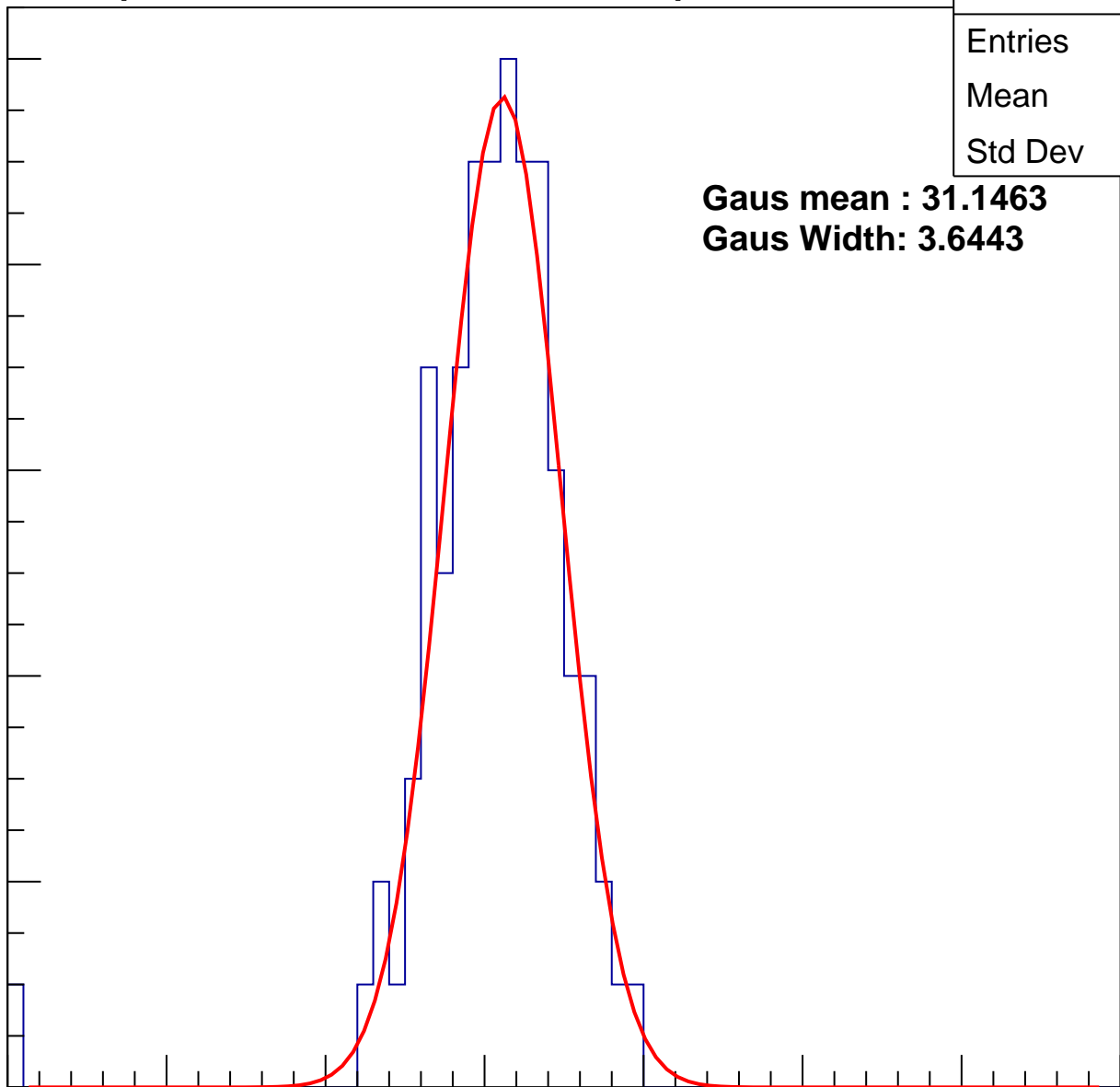
Gaus Width: 3.6443

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U9-ch121, adc1

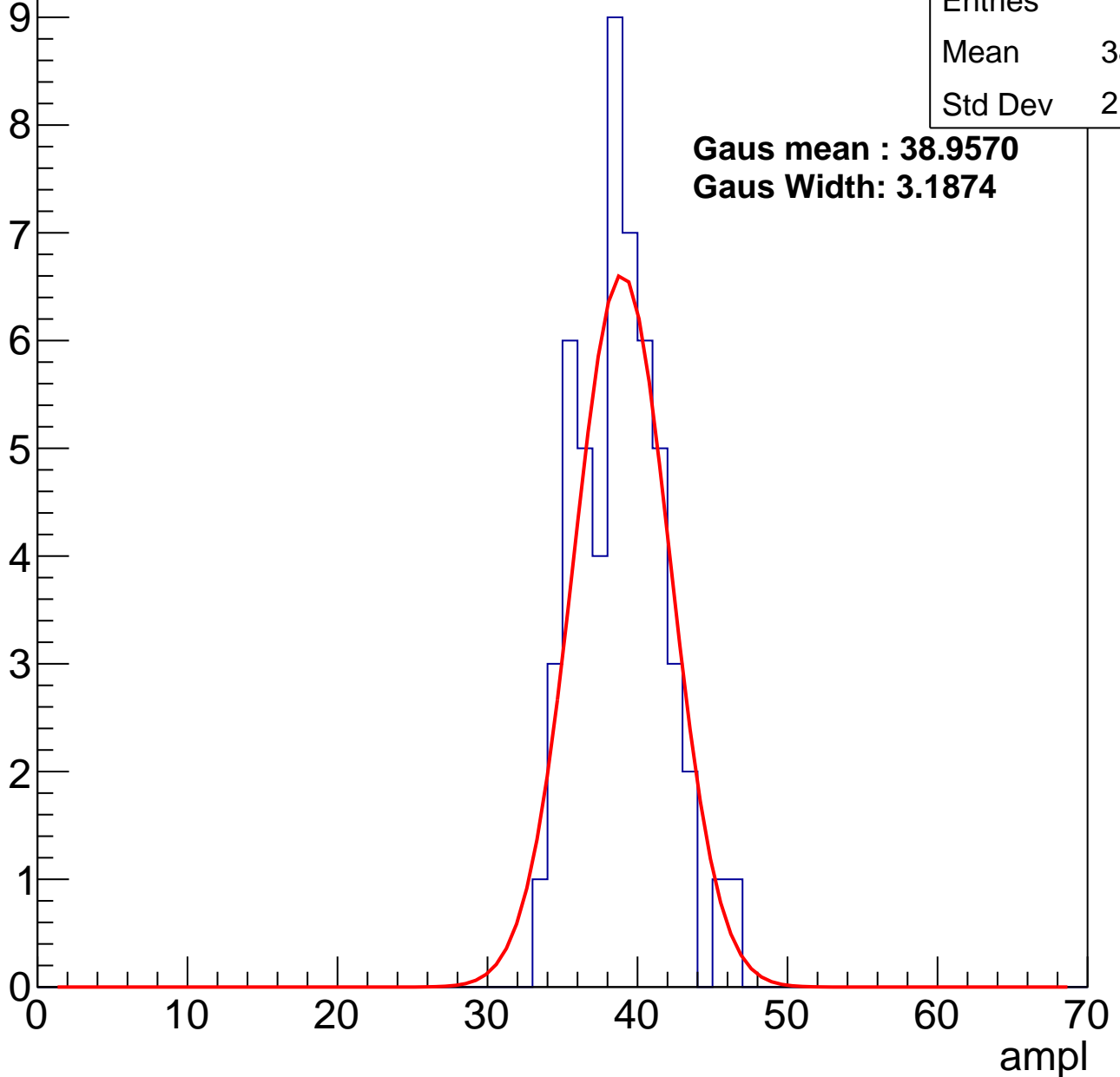
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	38.42
Std Dev	2.845

Gaus mean : 38.9570

Gaus Width: 3.1874



B0L002S, U9-ch121, adc2

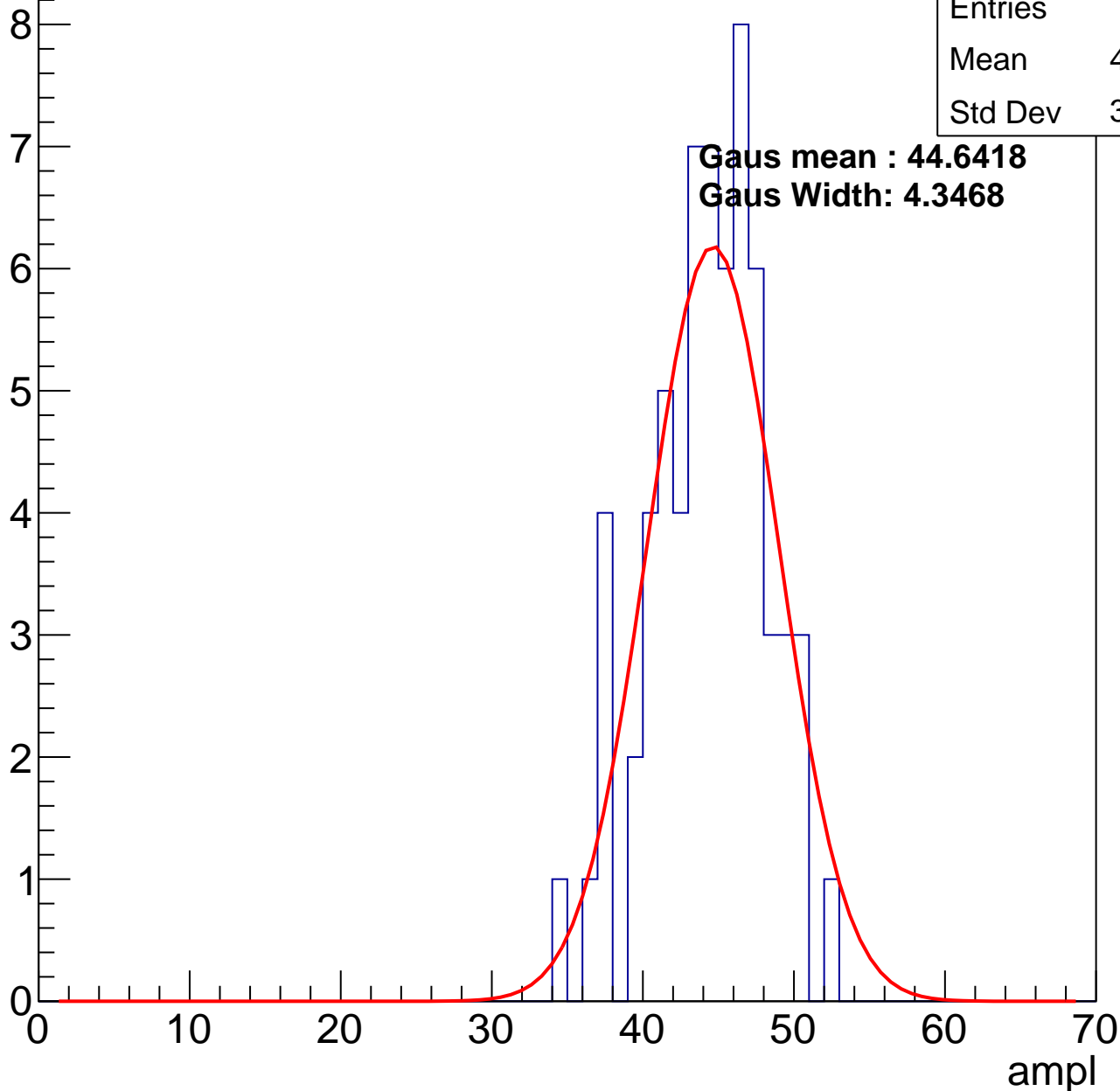
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.86
Std Dev	3.778

Gaus mean : 44.6418

Gaus Width: 4.3468

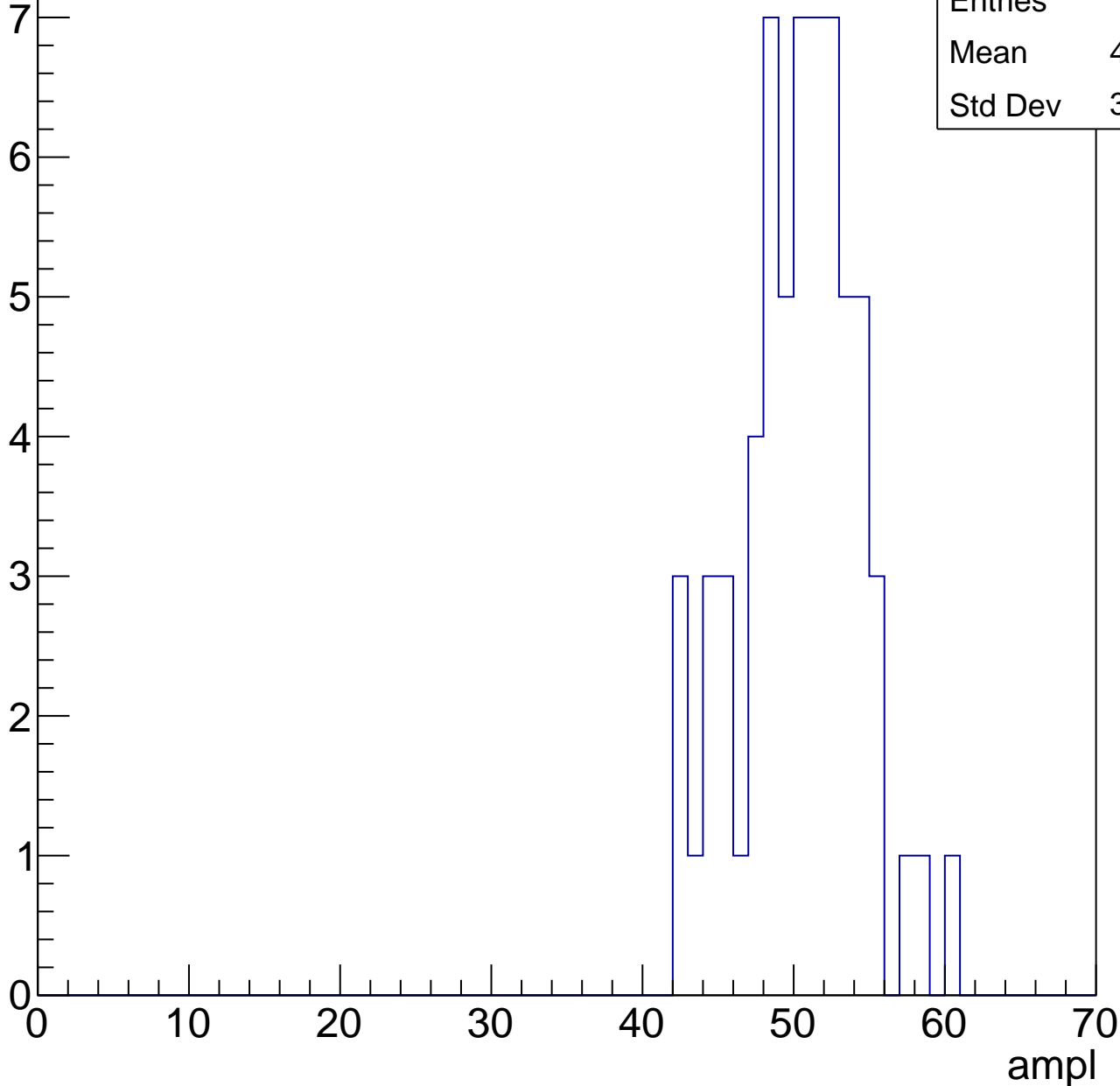


B0L002S, U9-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

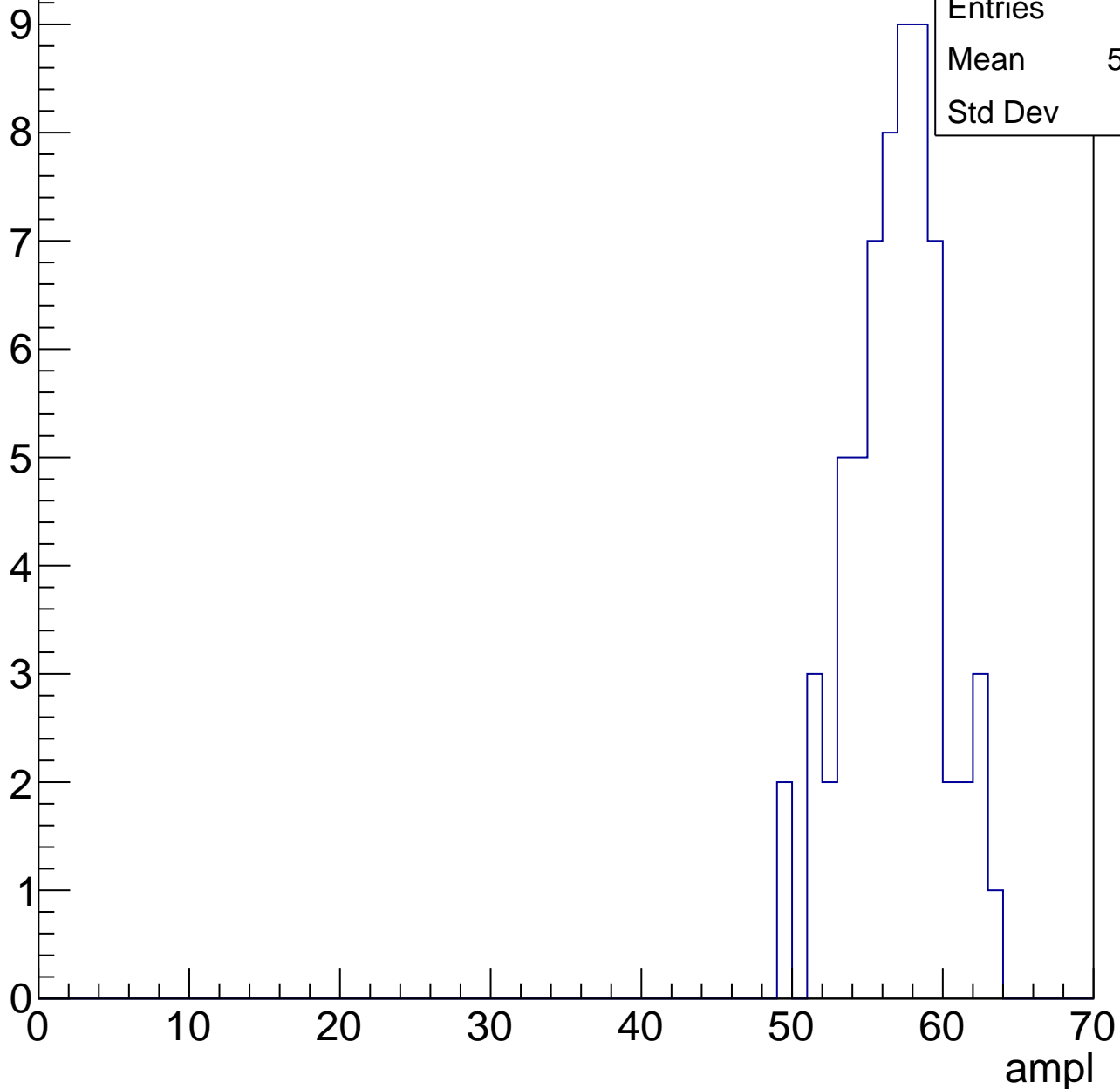
Entries	64
Mean	49.95
Std Dev	3.867



B0L002S, U9-ch121, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

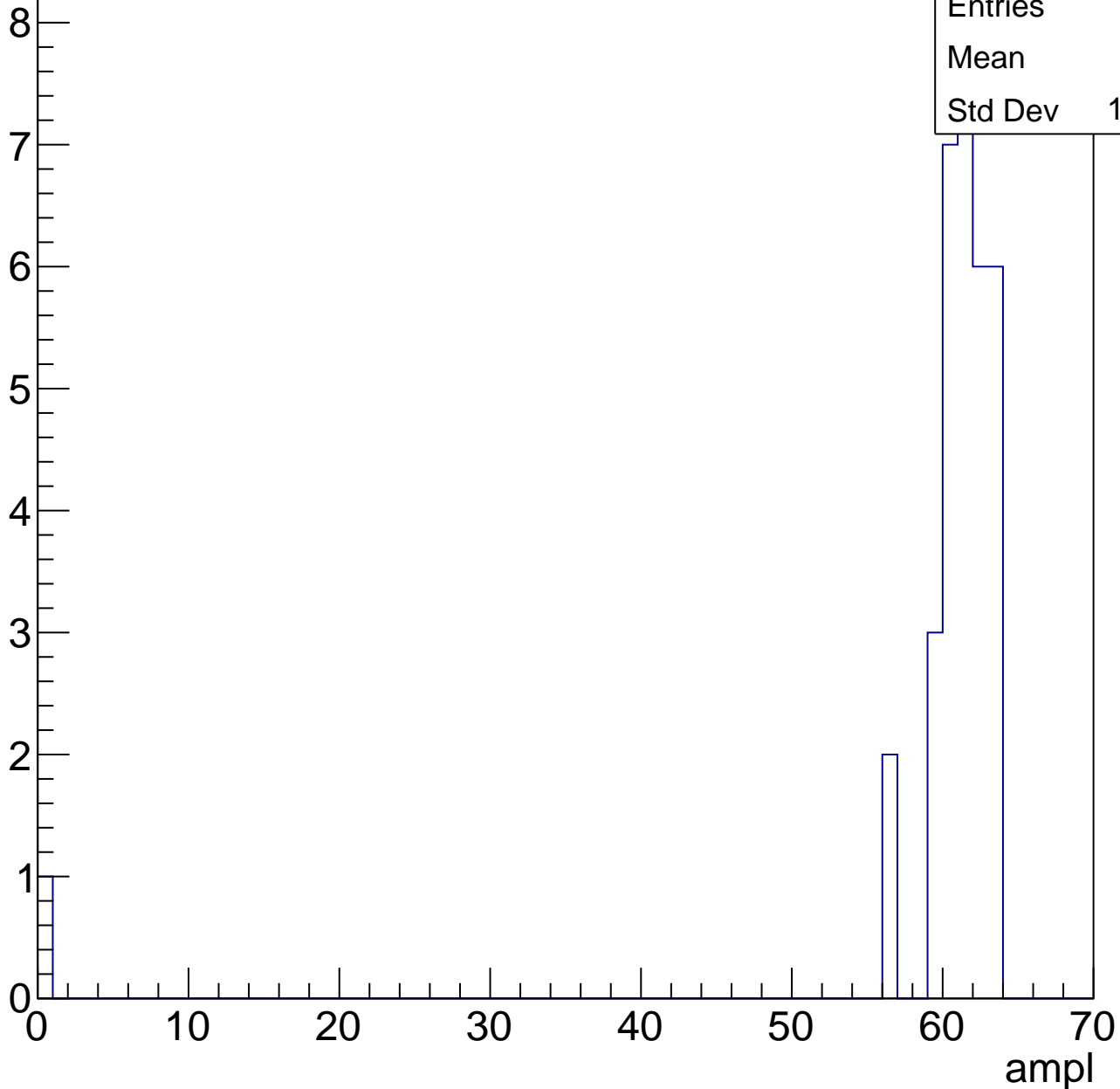


B0L002S, U9-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	59
Std Dev	10.57



B0L002S, U9-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch122, adc0

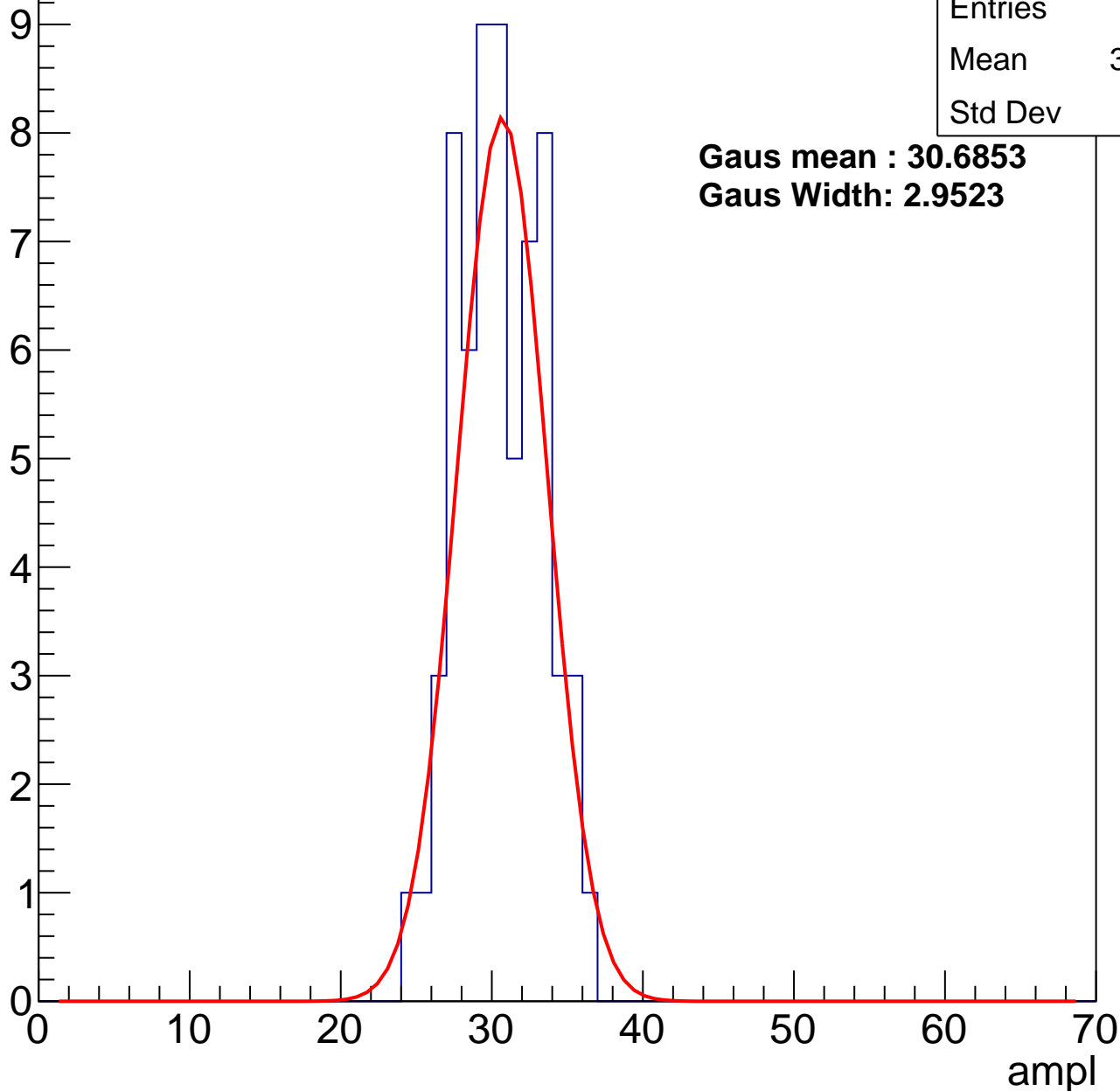
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.12
Std Dev	2.73

Gaus mean : 30.6853

Gaus Width: 2.9523



B0L002S, U9-ch122, adc1

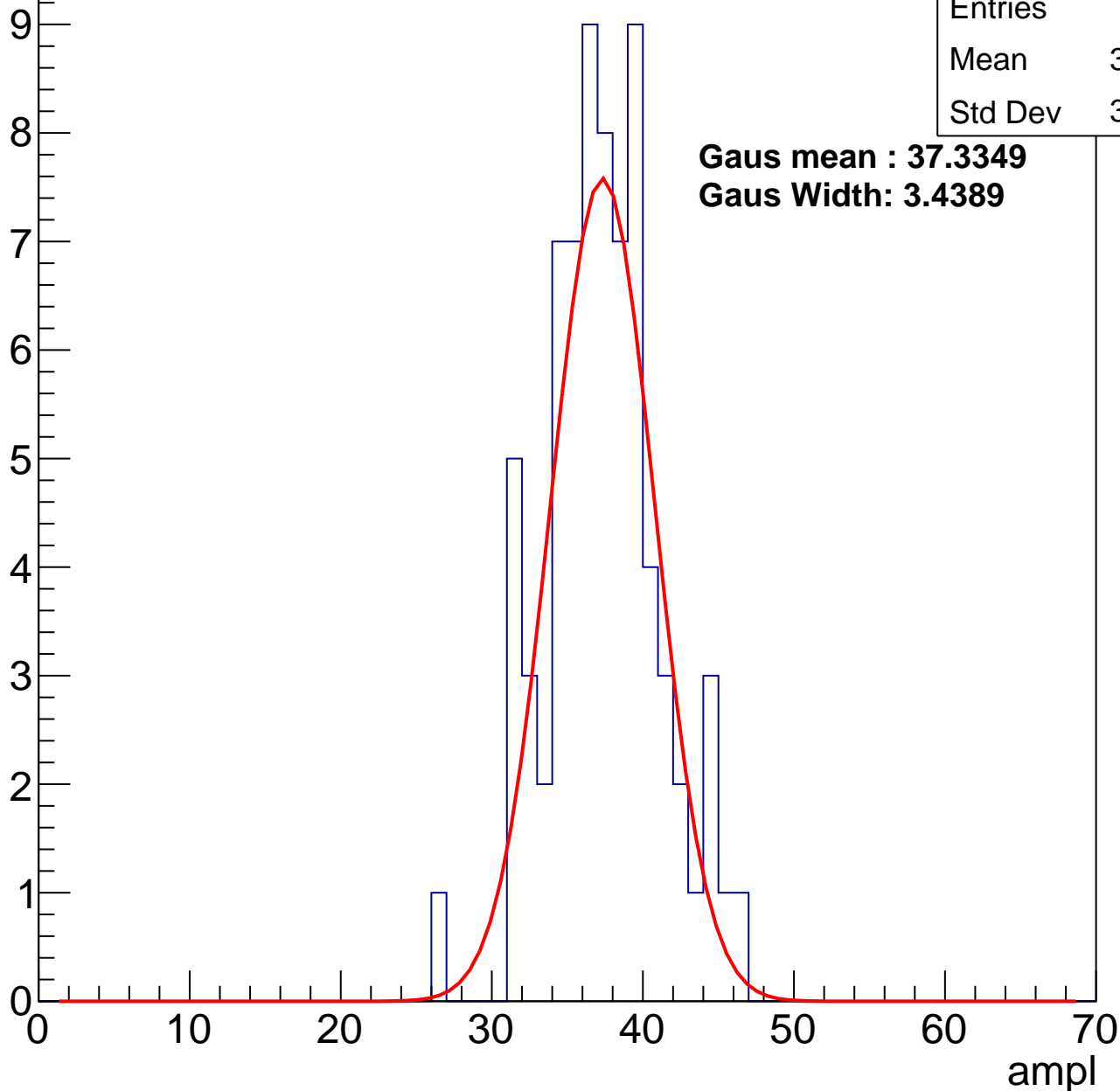
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	36.93
Std Dev	3.725

Gaus mean : 37.3349

Gaus Width: 3.4389

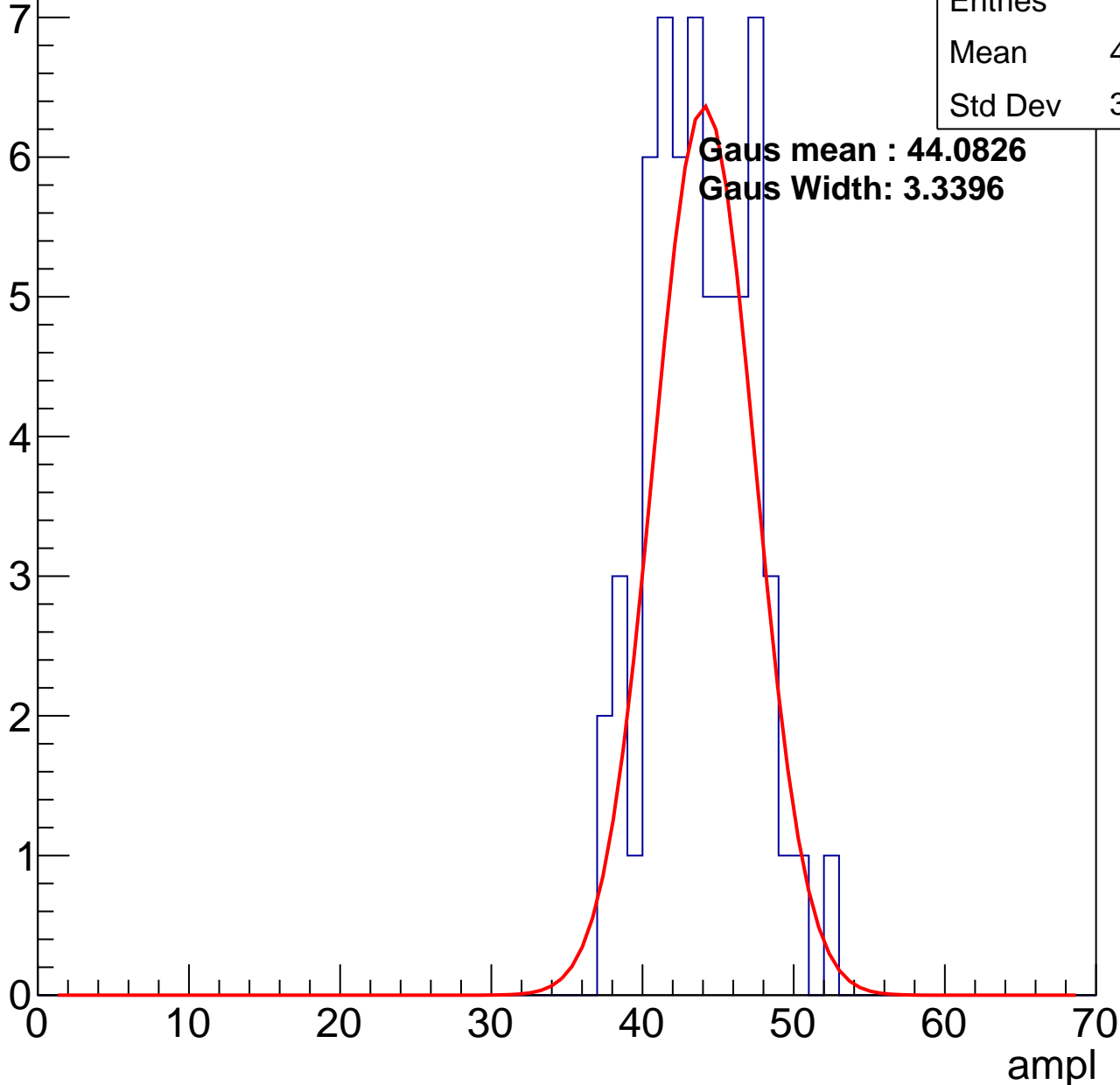


B0L002S, U9-ch122, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

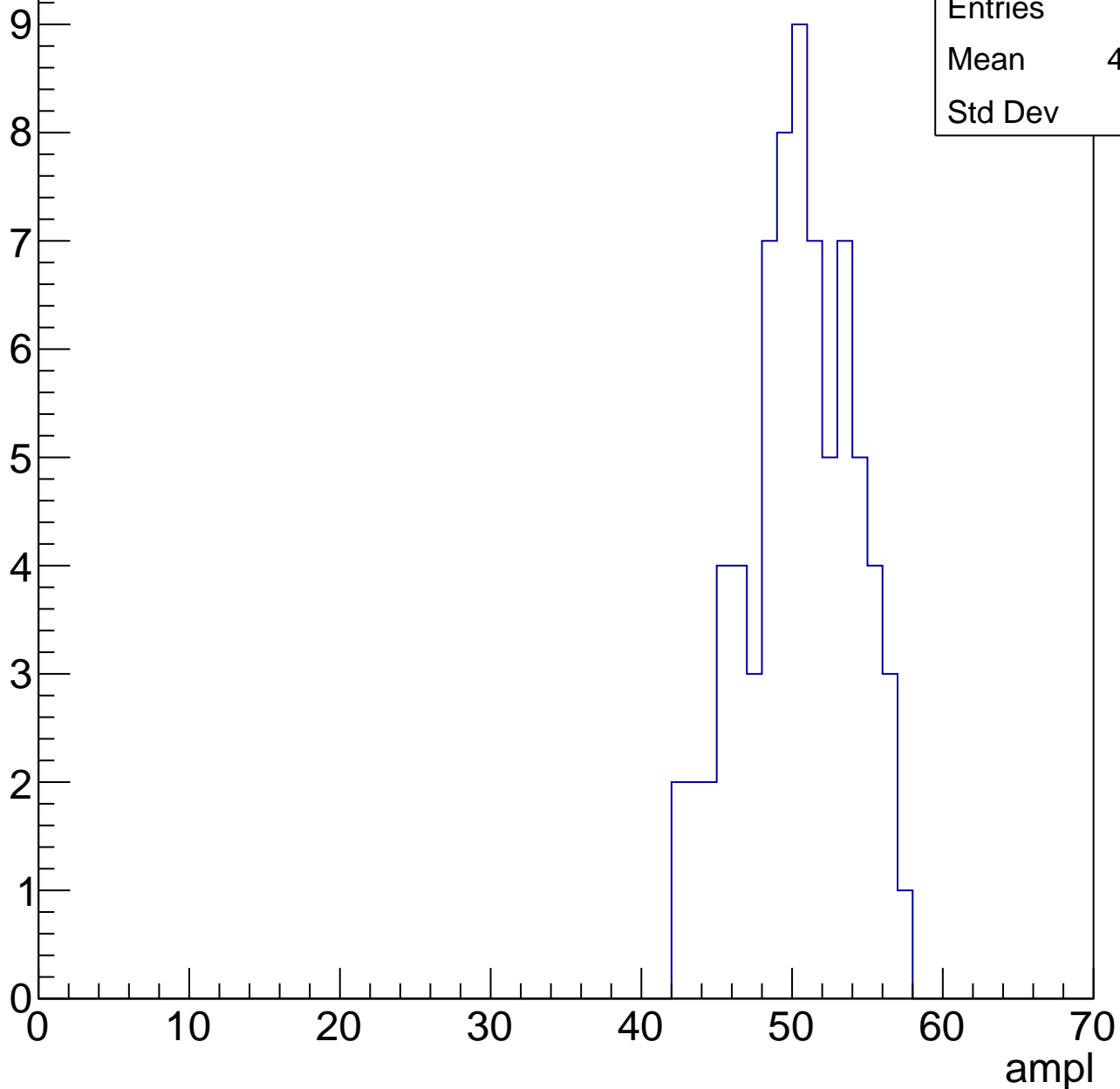
Entries	60
Mean	43.43
Std Dev	3.323



B0L002S, U9-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

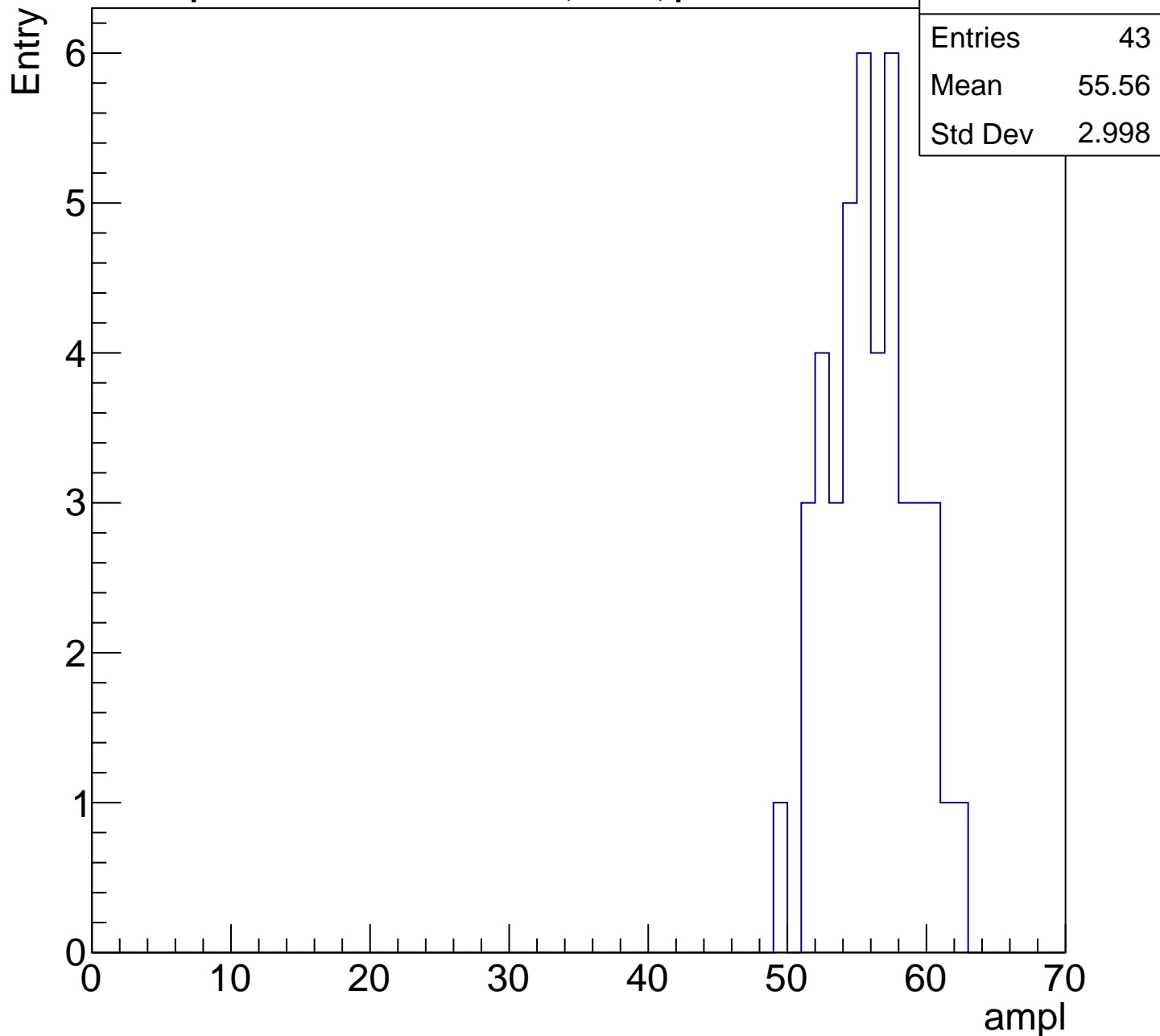
Entry



Entries	73
Mean	49.92
Std Dev	3.61

B0L002S, U9-ch122, adc4

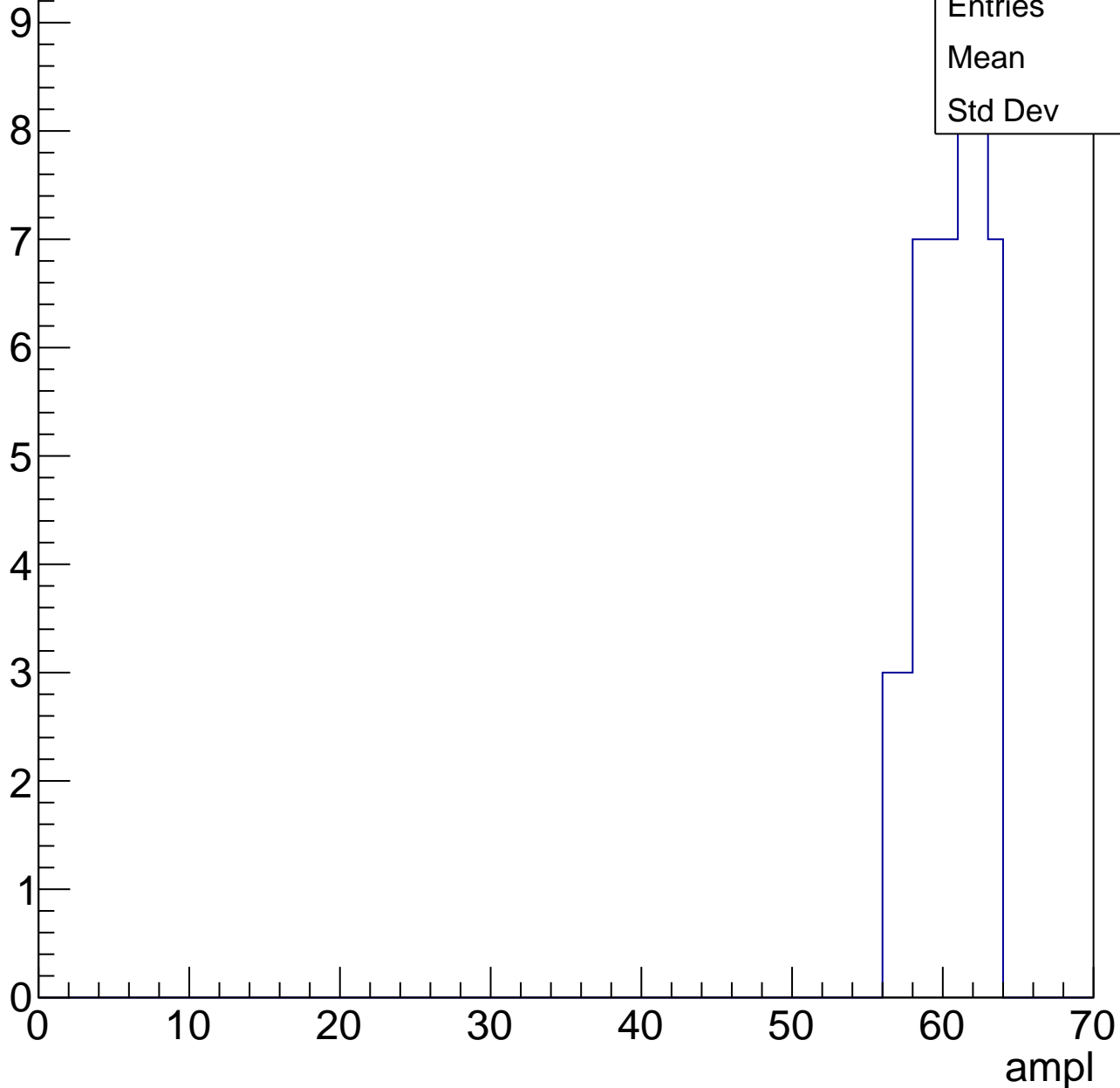
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U9-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

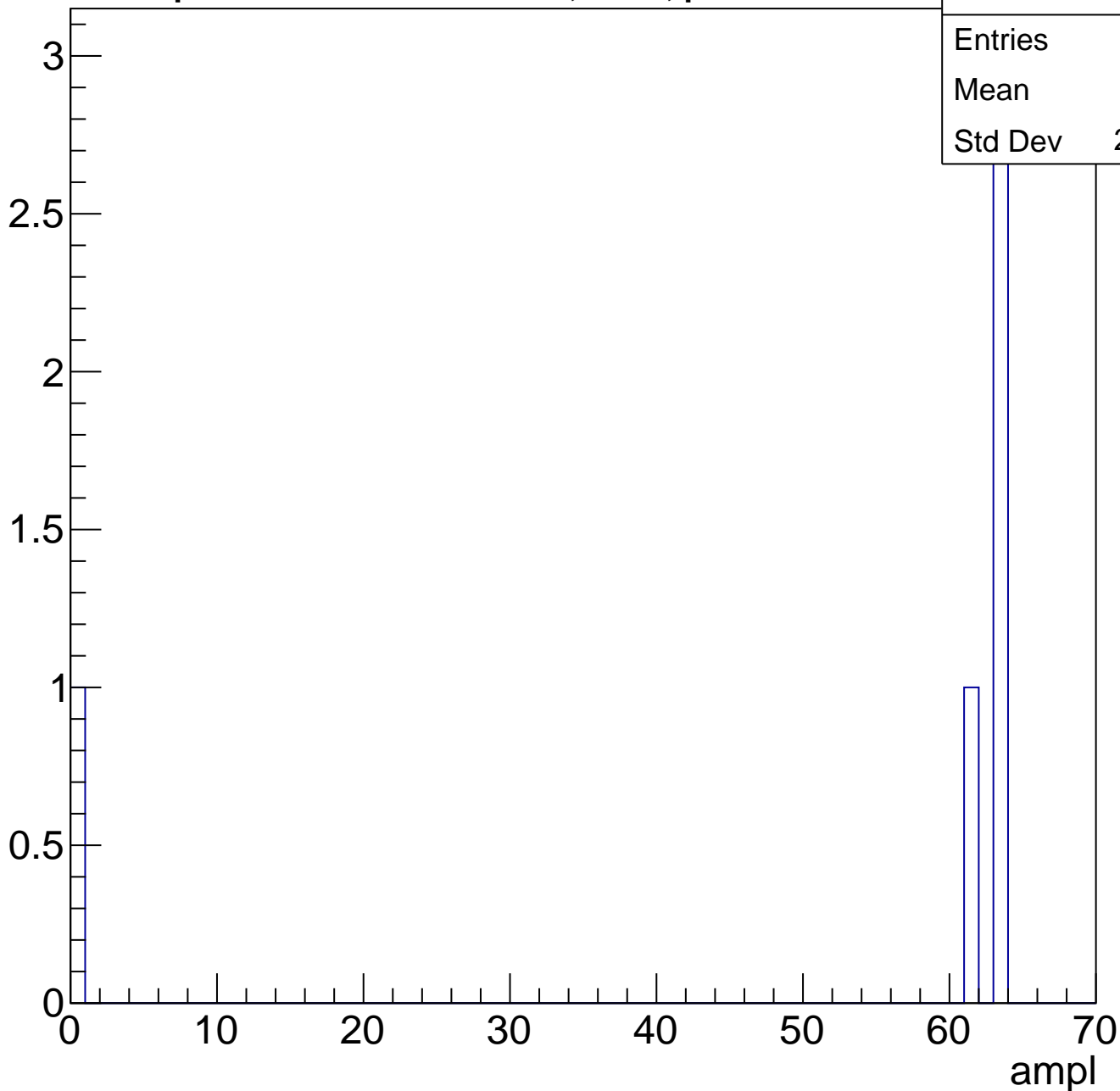


Entries	51
Mean	60.1
Std Dev	2.06

B0L002S, U9-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch123, adc0

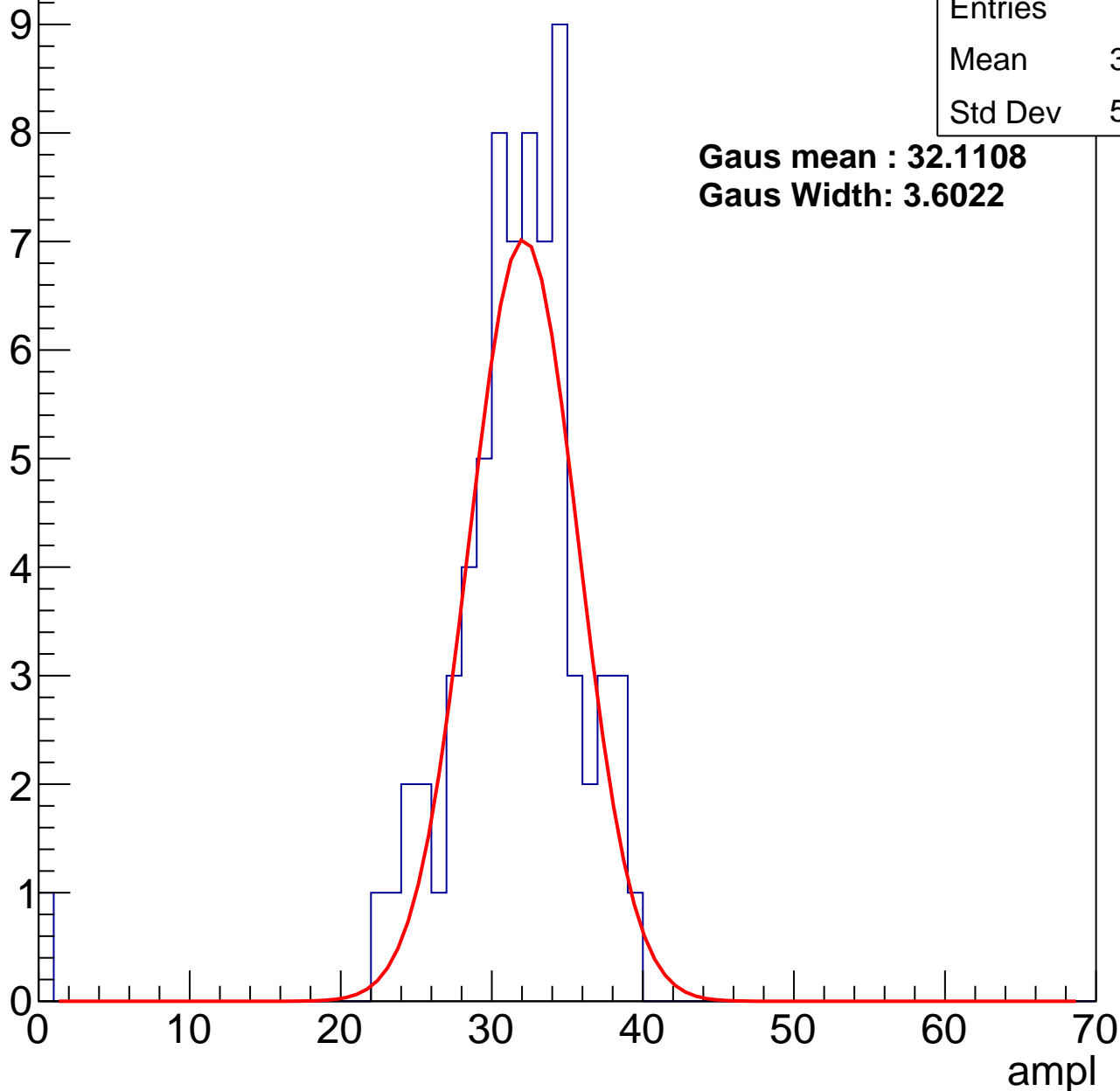
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.96
Std Dev	5.236

Gaus mean : 32.1108

Gaus Width: 3.6022



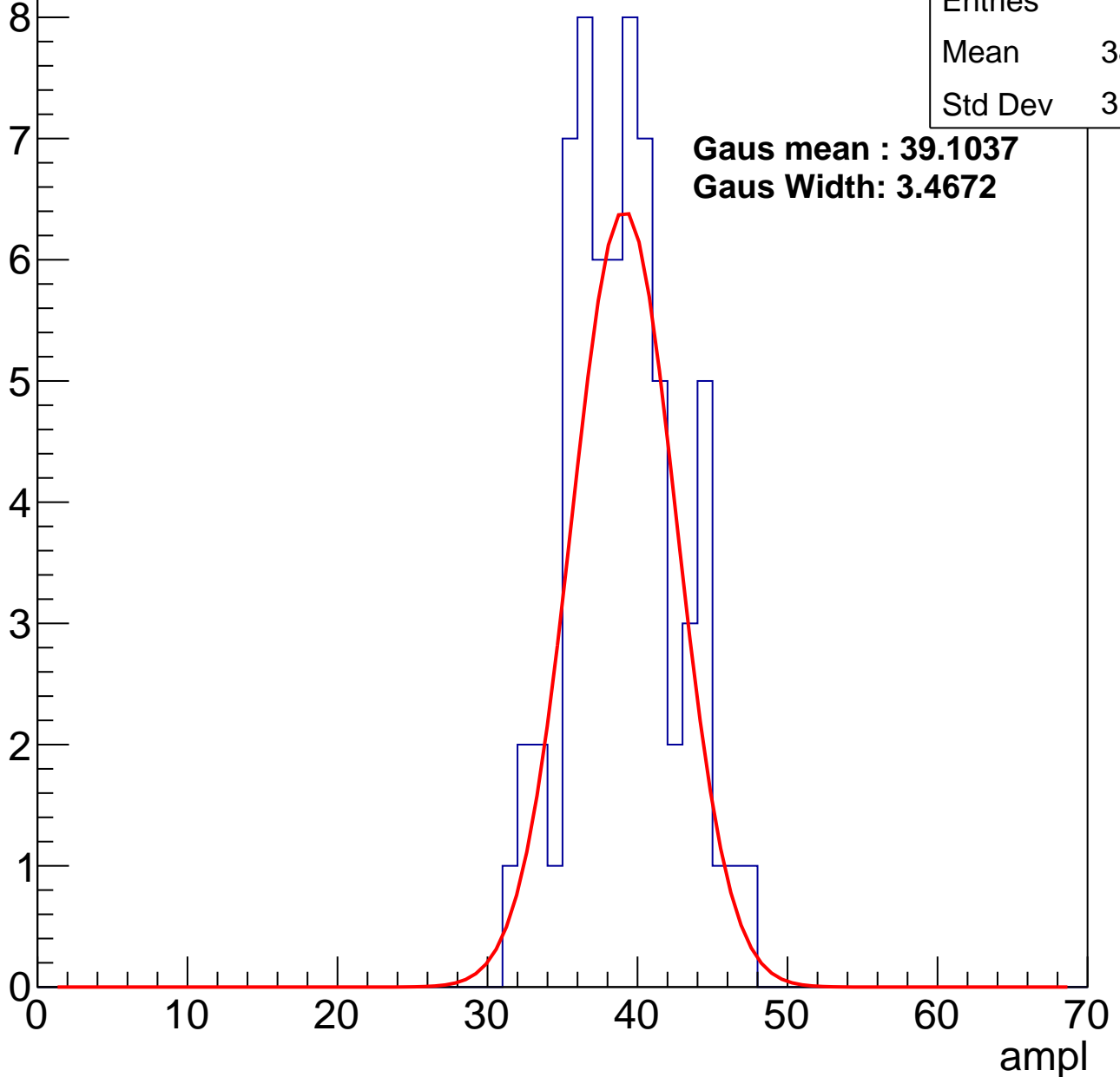
B0L002S, U9-ch123, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.58
Std Dev	3.542

Gaus mean : 39.1037
Gaus Width: 3.4672



B0L002S, U9-ch123, adc2

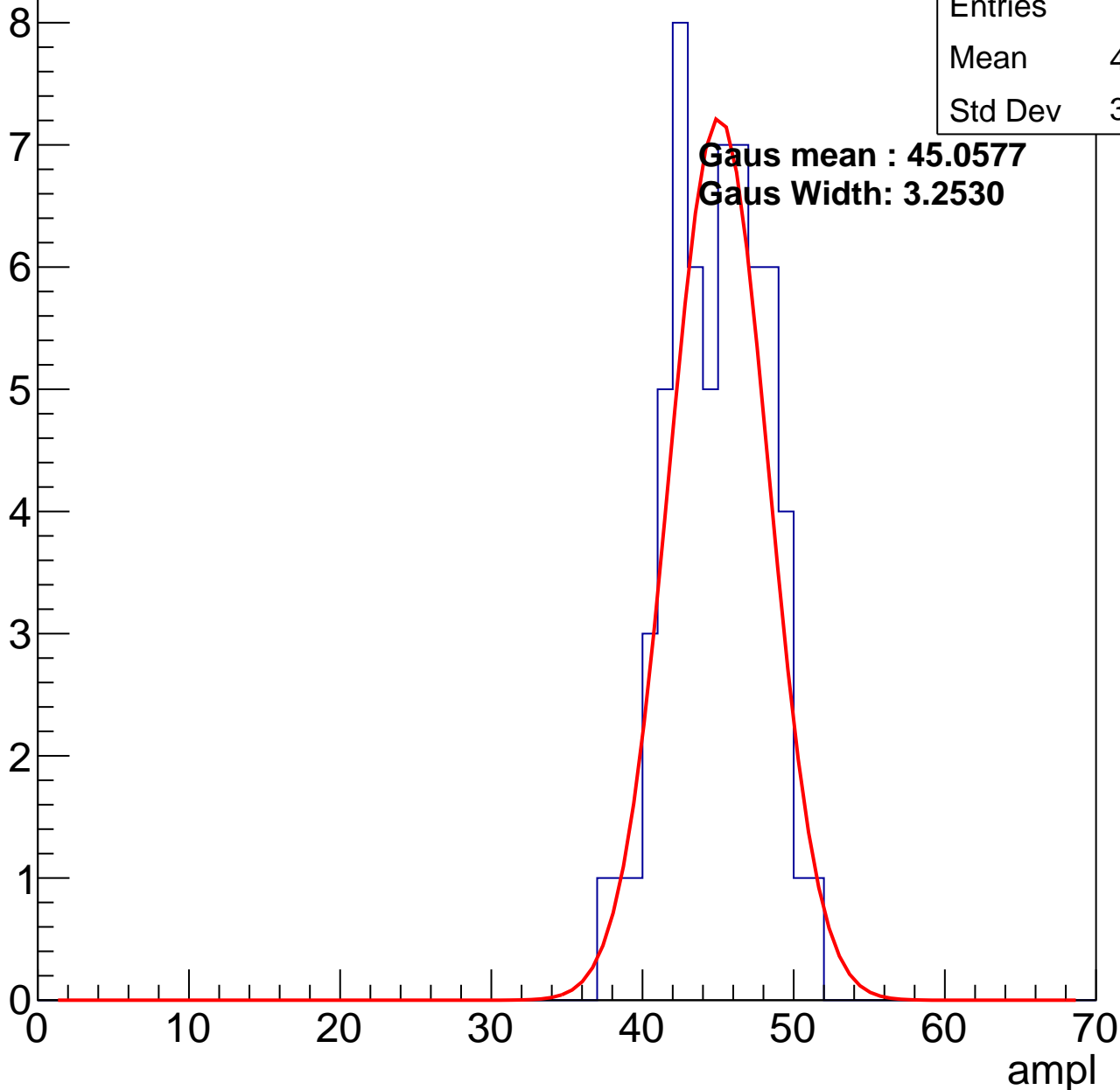
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.47
Std Dev	3.104

Gaus mean : 45.0577

Gaus Width: 3.2530

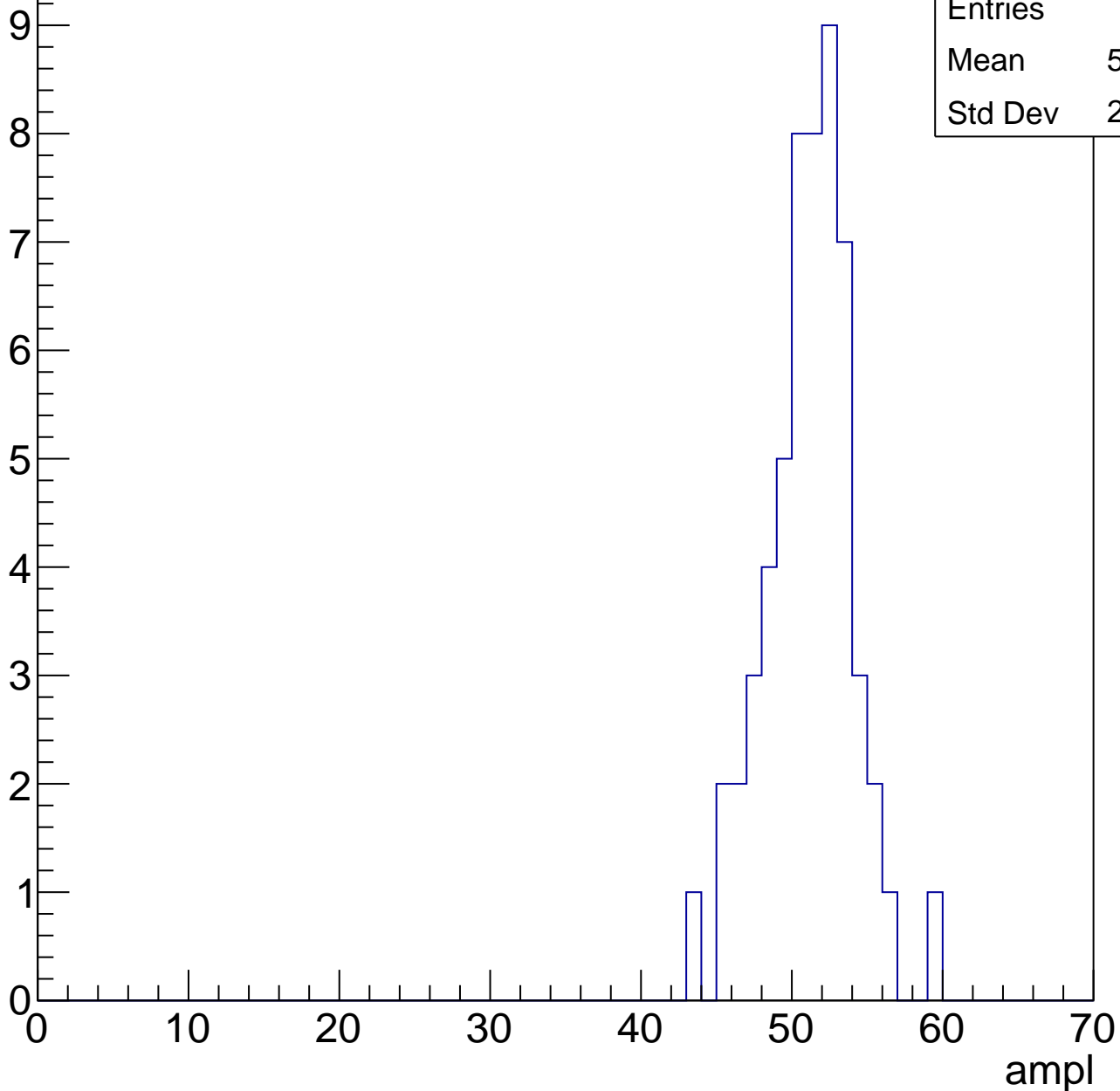


B0L002S, U9-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

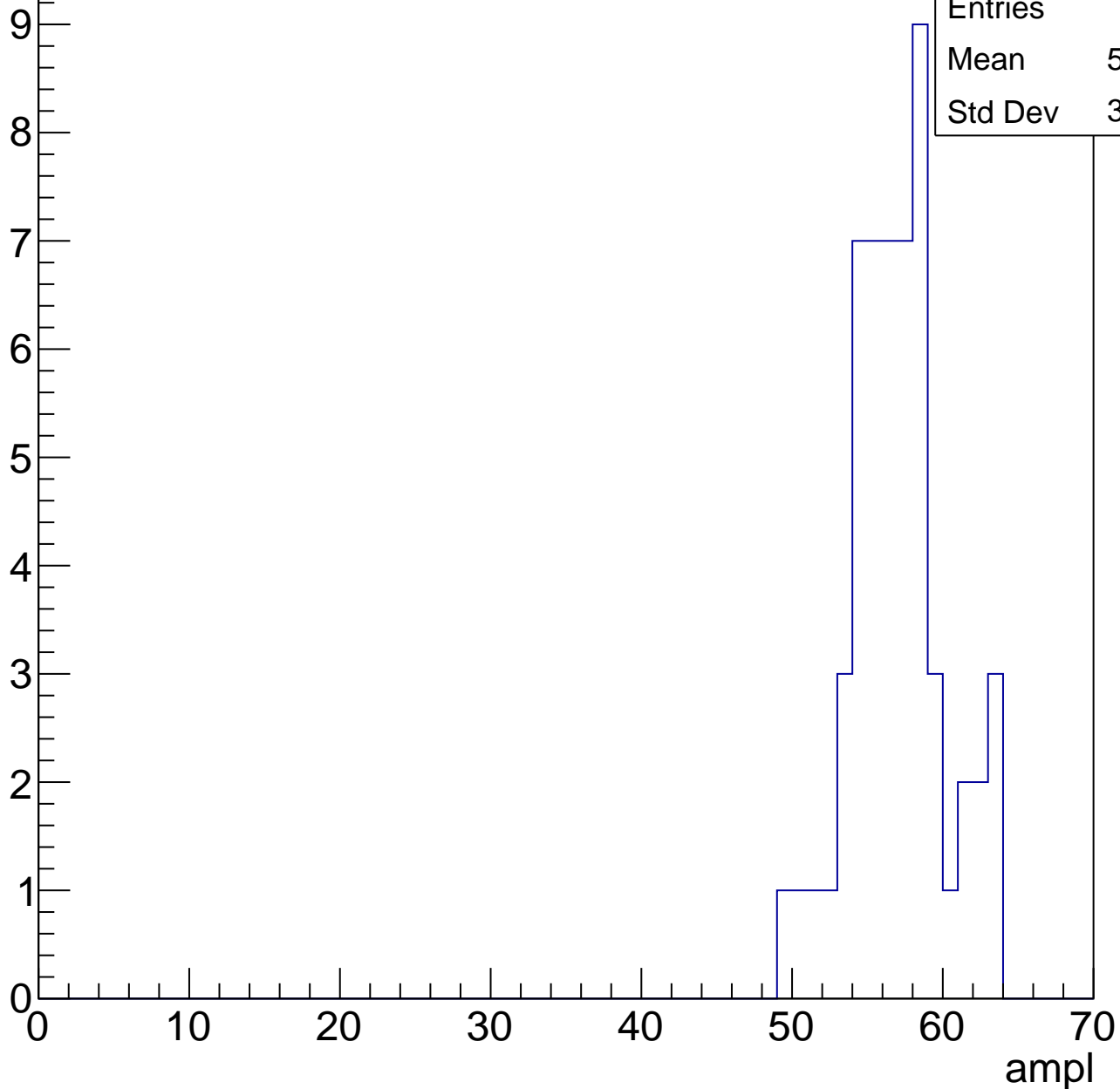
Entries	56
Mean	50.66
Std Dev	2.917



B0L002S, U9-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



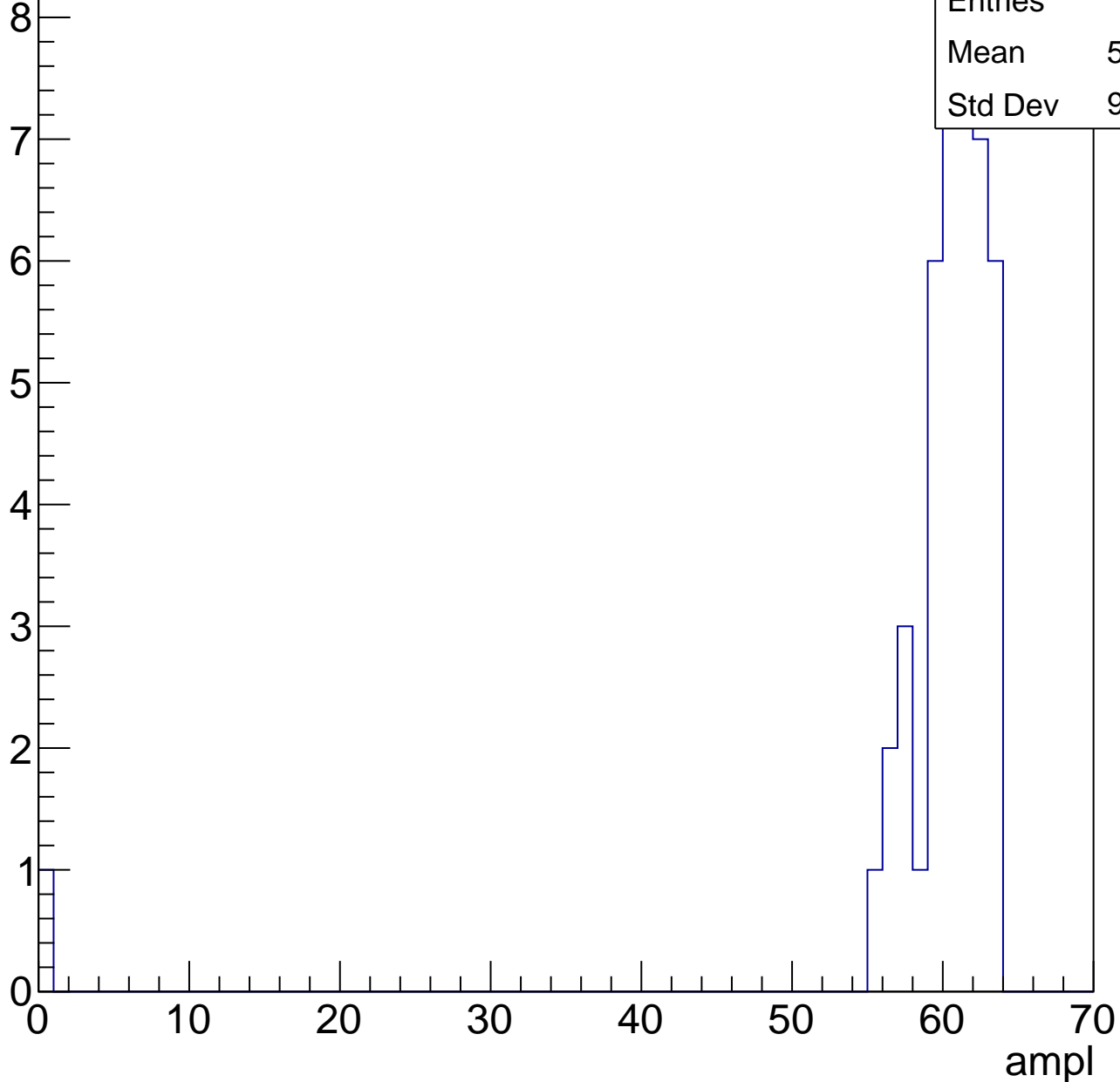
Entries	55
Mean	56.53
Std Dev	3.098

B0L002S, U9-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	58.84
Std Dev	9.308



B0L002S, U9-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch124, adc0

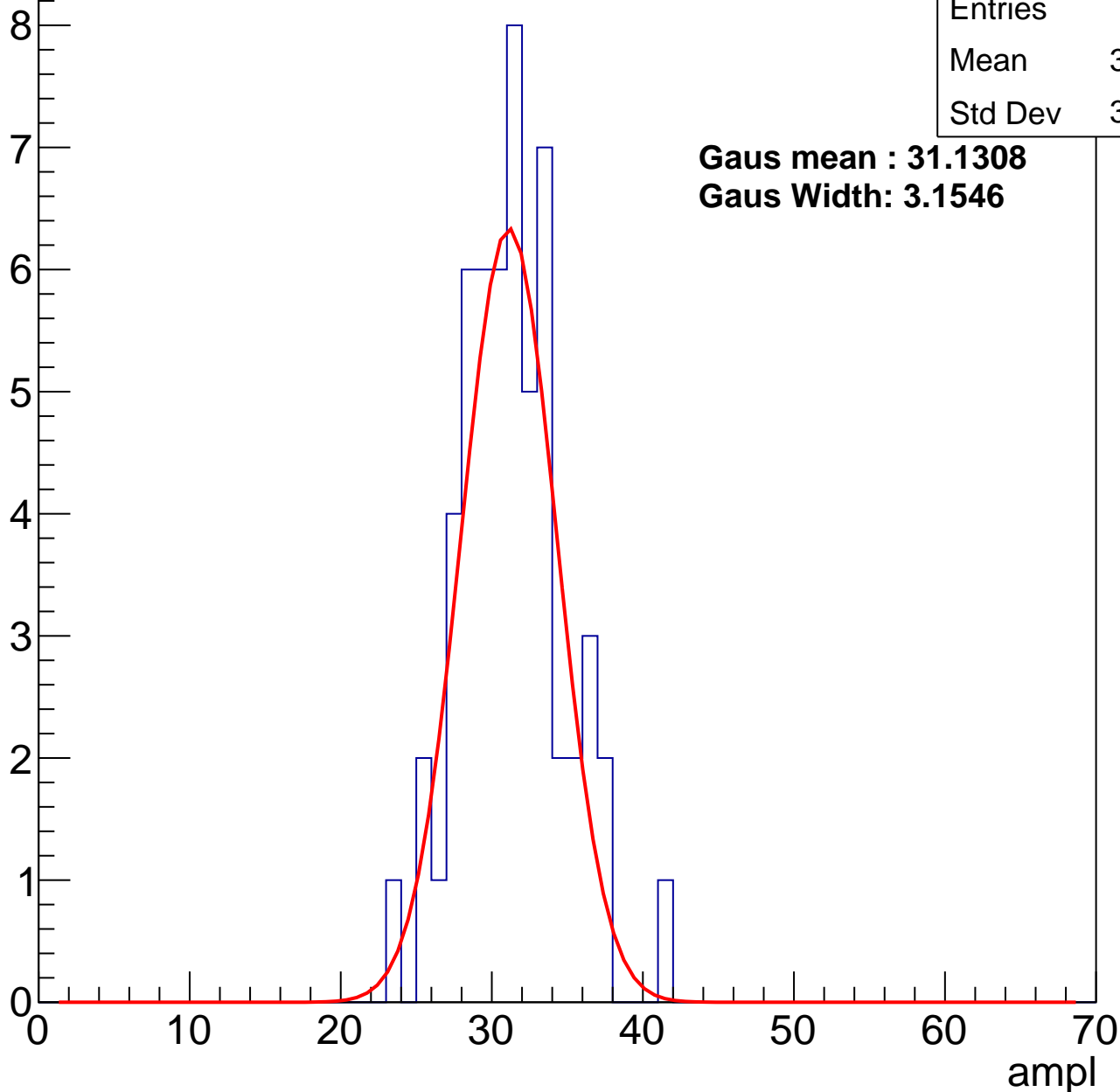
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	30.88
Std Dev	3.376

Gaus mean : 31.1308

Gaus Width: 3.1546



B0L002S, U9-ch124, adc1

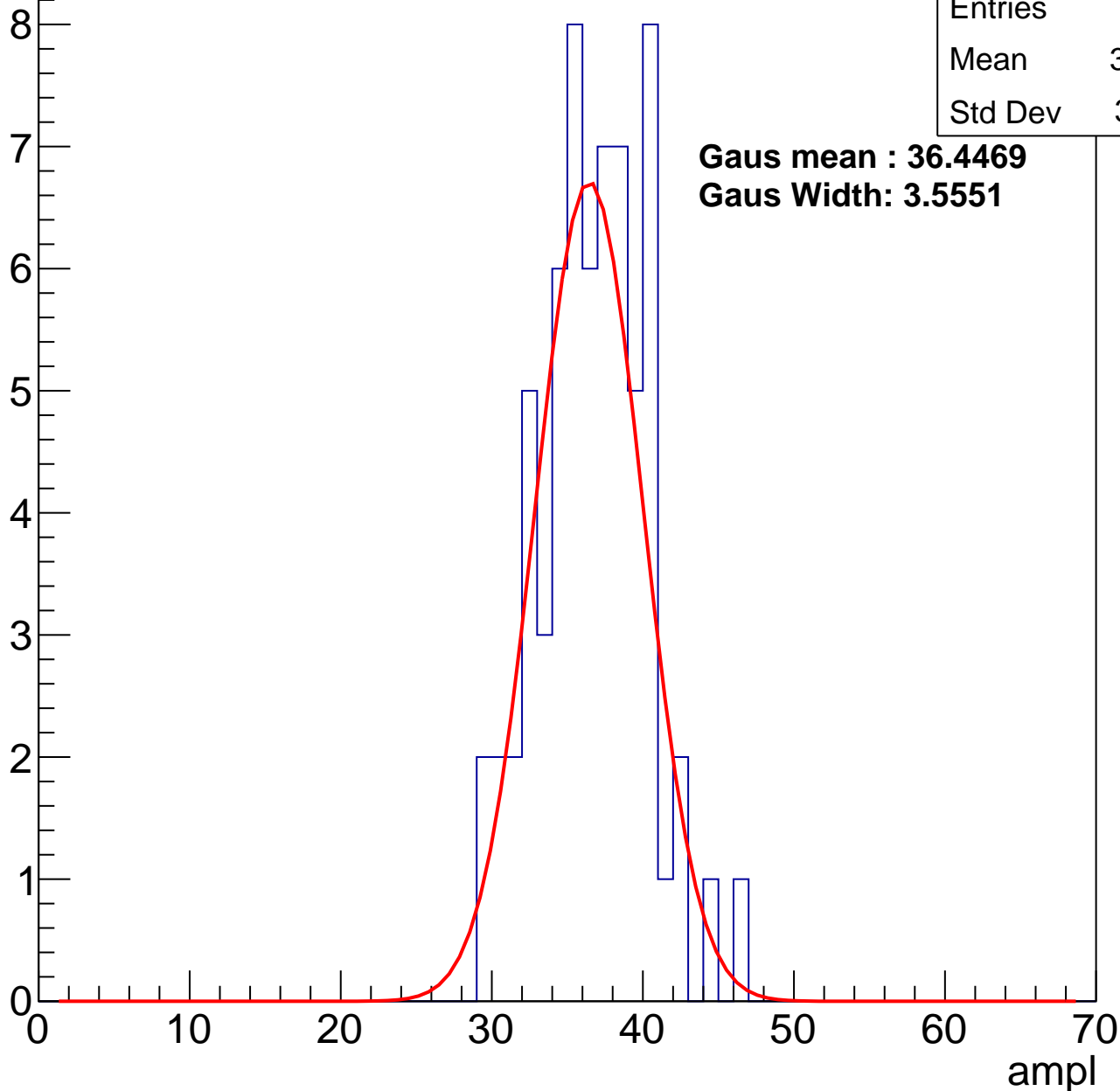
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.27
Std Dev	3.531

Gaus mean : 36.4469

Gaus Width: 3.5551



B0L002S, U9-ch124, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	44.03
Std Dev	3.89

Gaus mean : 45.1295

Gaus Width: 4.0074

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

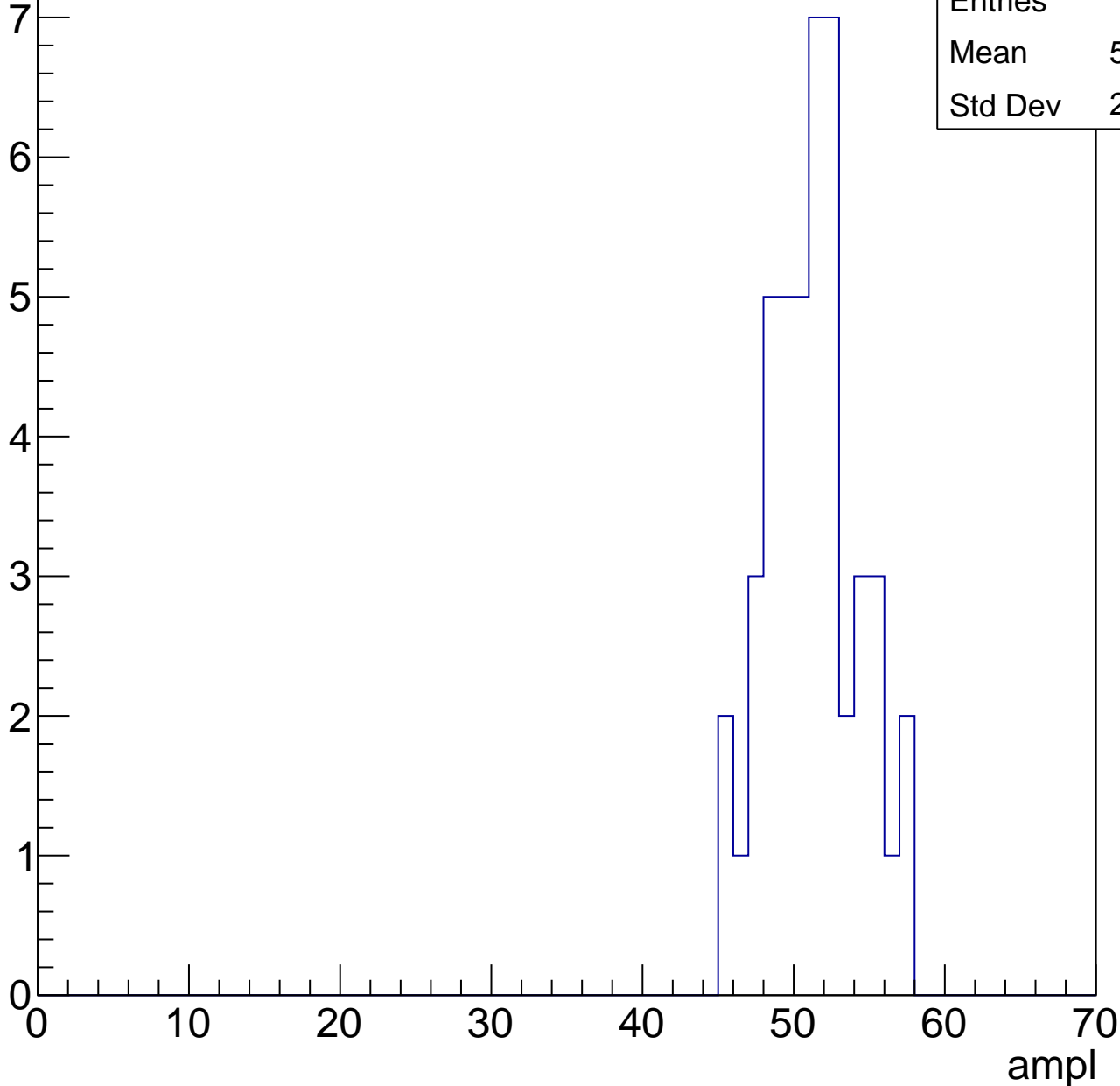
70

B0L002S, U9-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

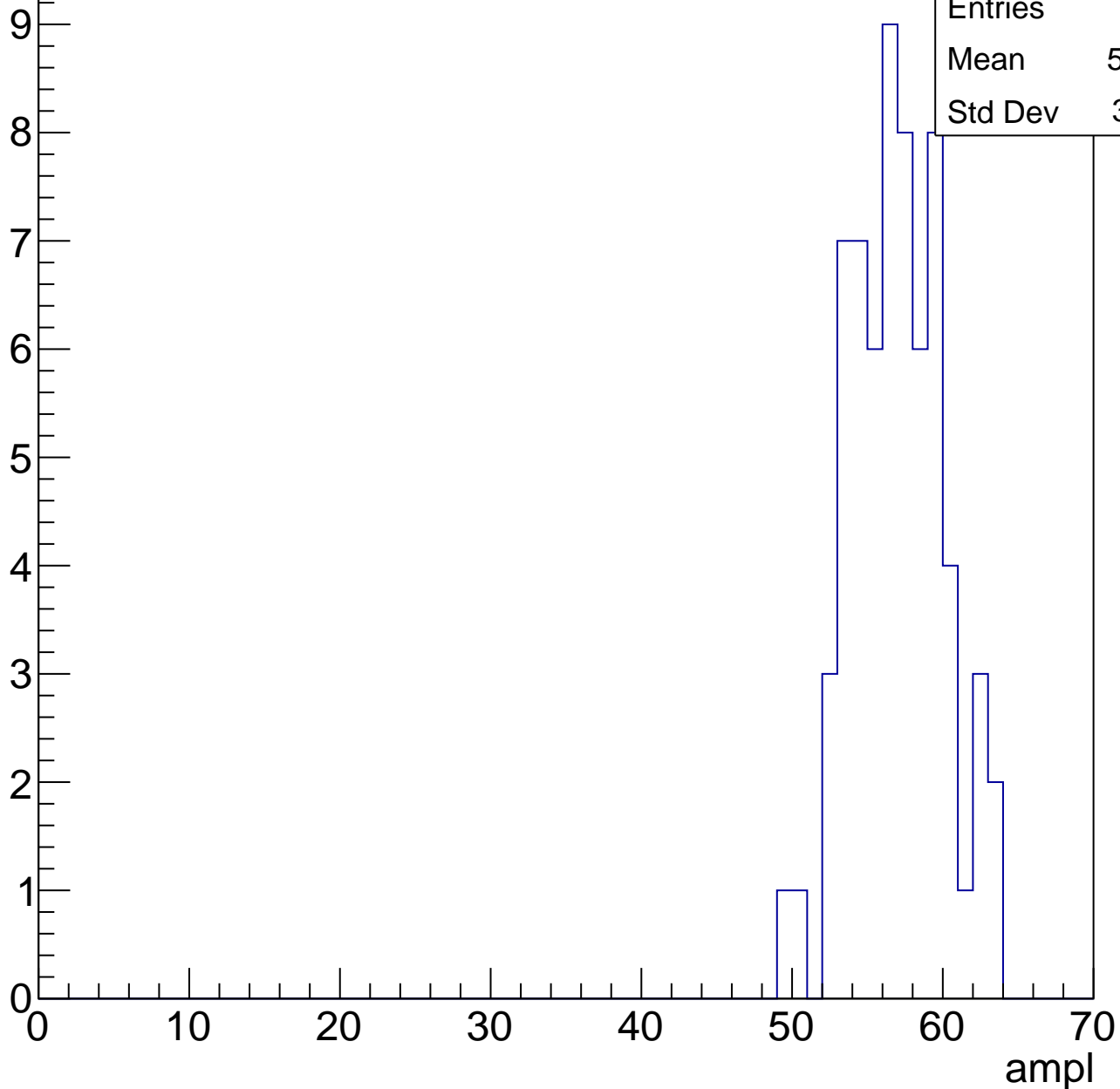
Entries	46
Mean	50.78
Std Dev	2.948



B0L002S, U9-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

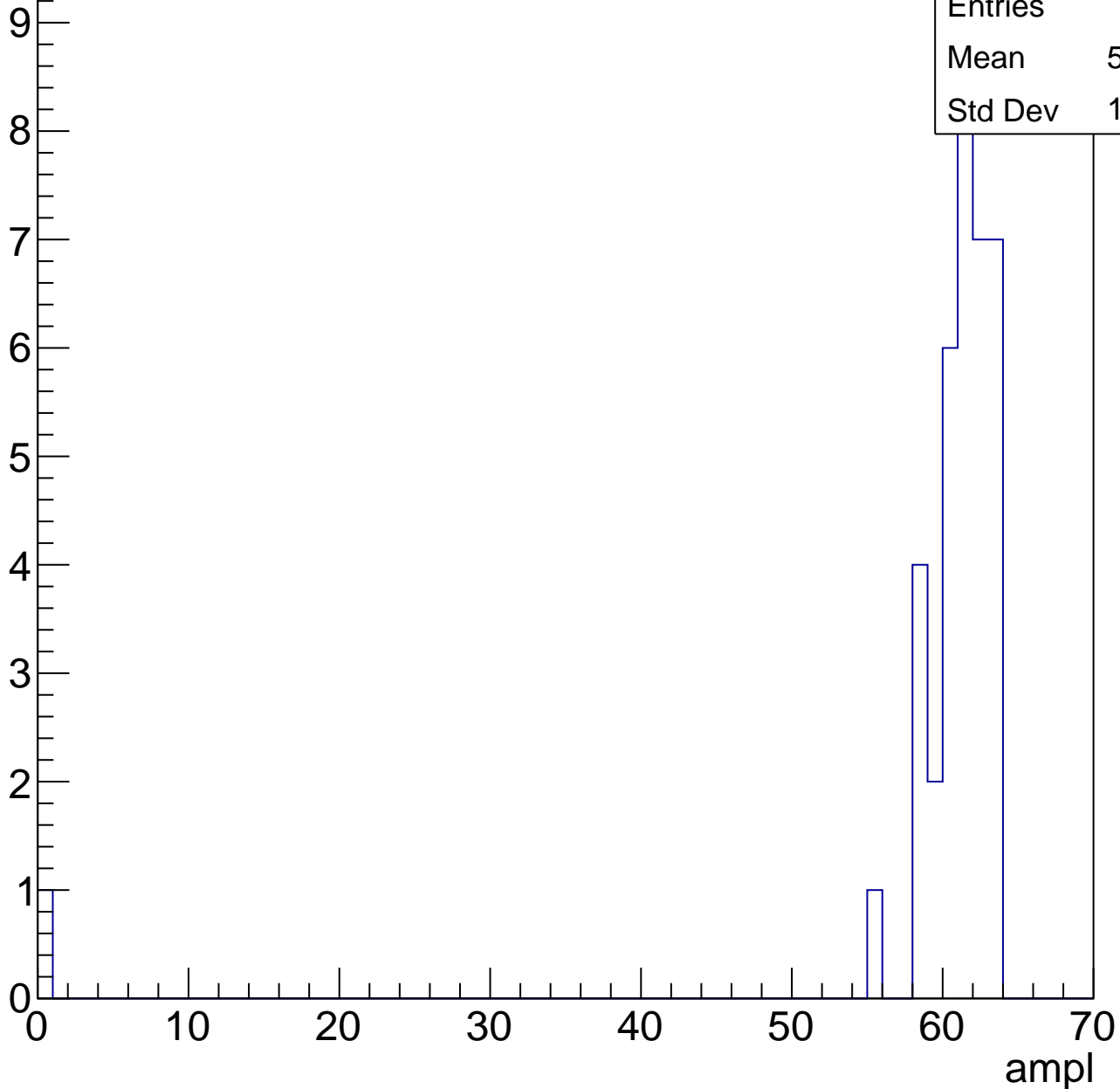


B0L002S, U9-ch124, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.16
Std Dev	10.02



B0L002S, U9-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch125, adc0

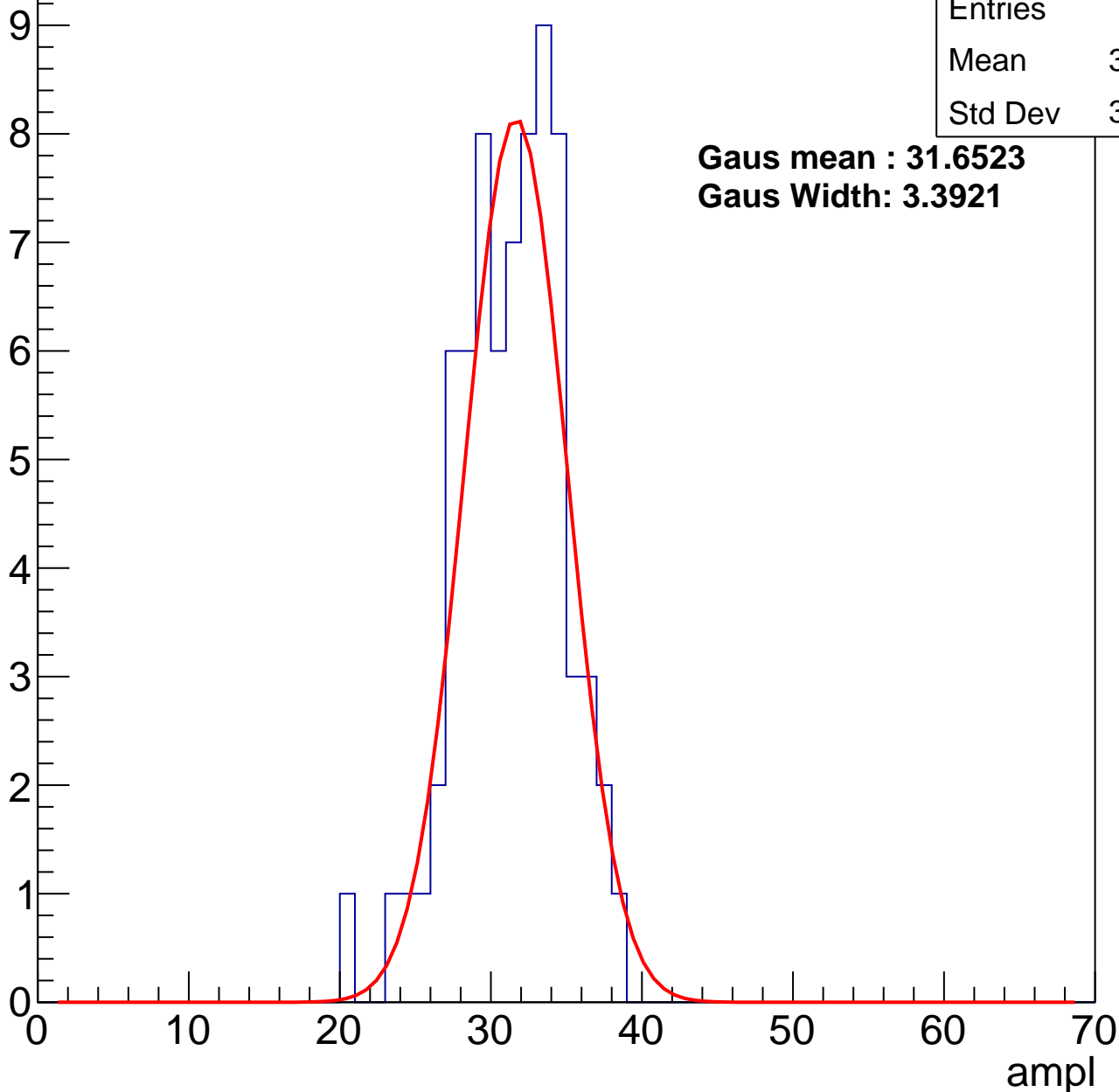
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.86
Std Dev	3.446

Gaus mean : 31.6523

Gaus Width: 3.3921



B0L002S, U9-ch125, adc1

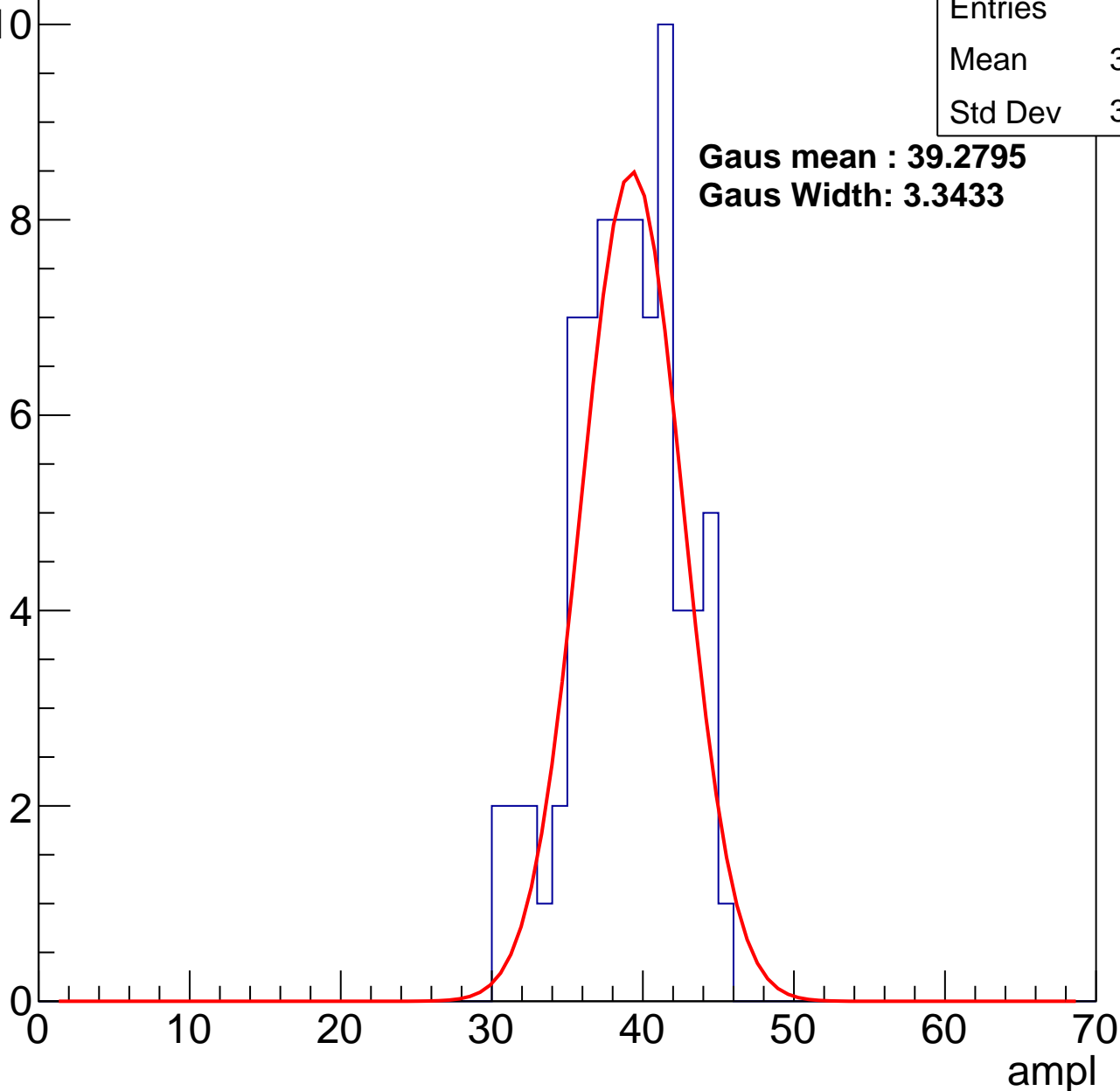
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	38.35
Std Dev	3.515

Gaus mean : 39.2795

Gaus Width: 3.3433



B0L002S, U9-ch125, adc2

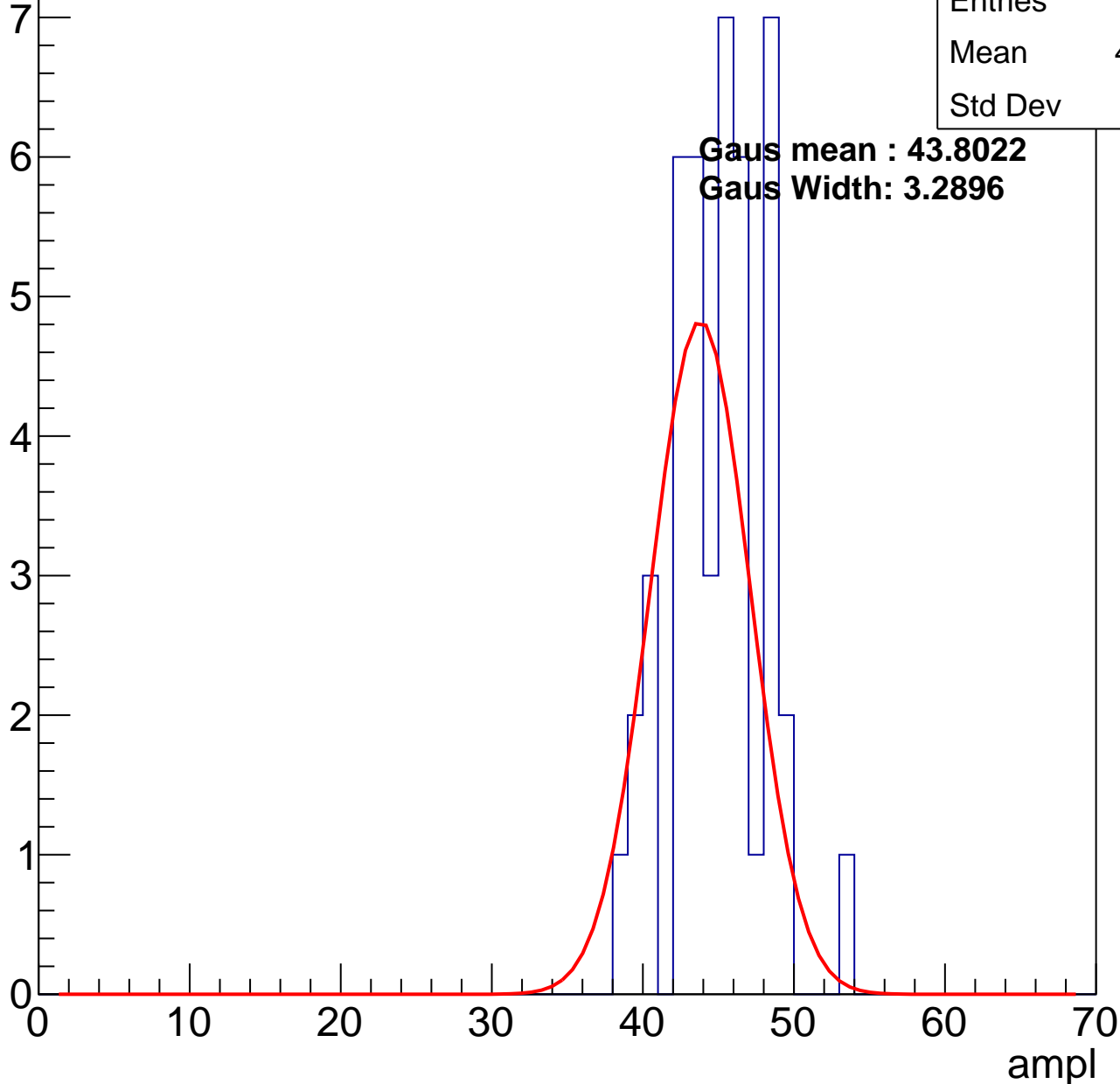
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	44.51
Std Dev	3.11

Gaus mean : 43.8022

Gaus Width: 3.2896



B0L002S, U9-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	50.65
Std Dev	3.226

Entry

10

8

6

4

2

0

0

10

20

30

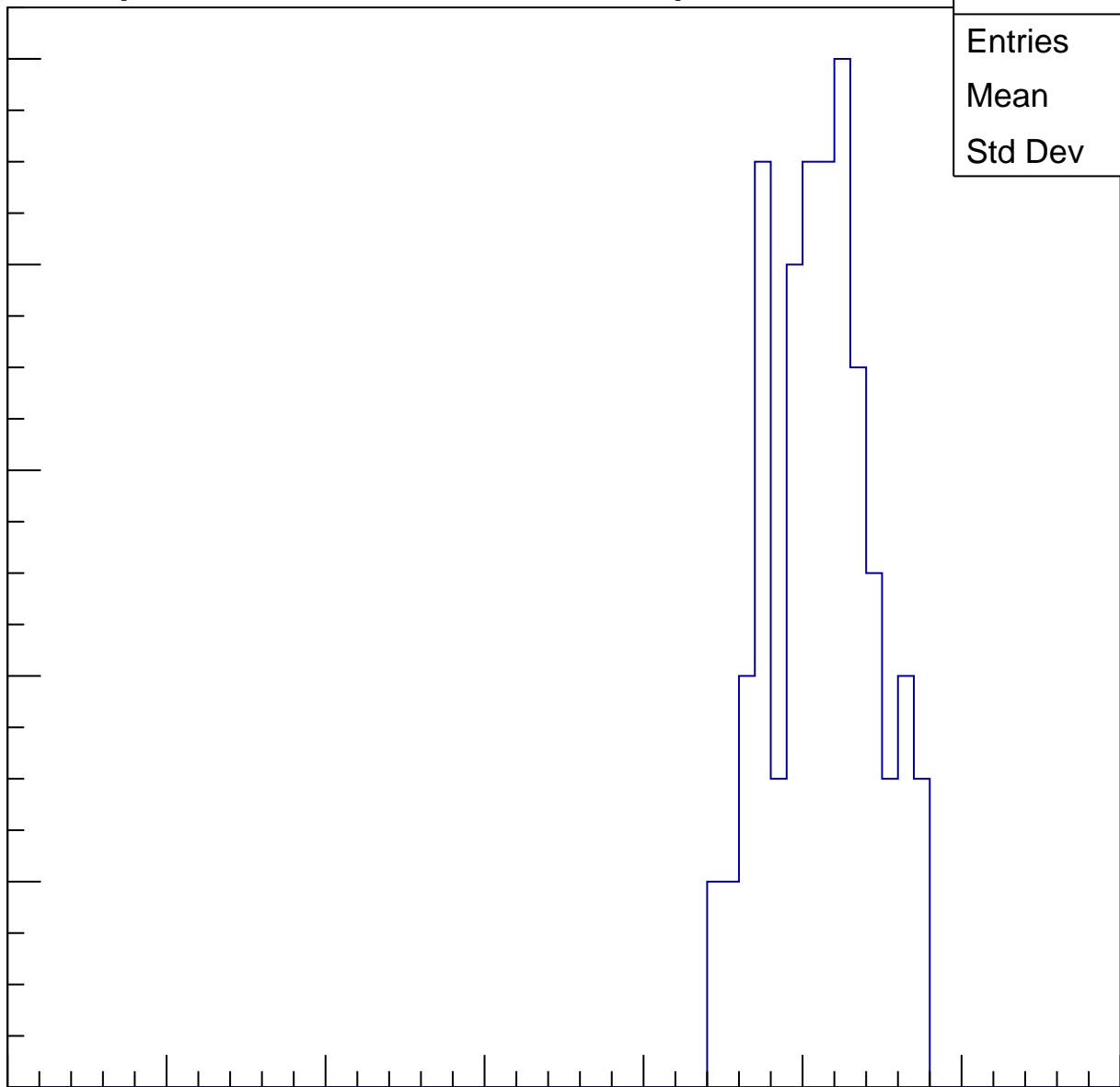
40

50

60

70

ampl

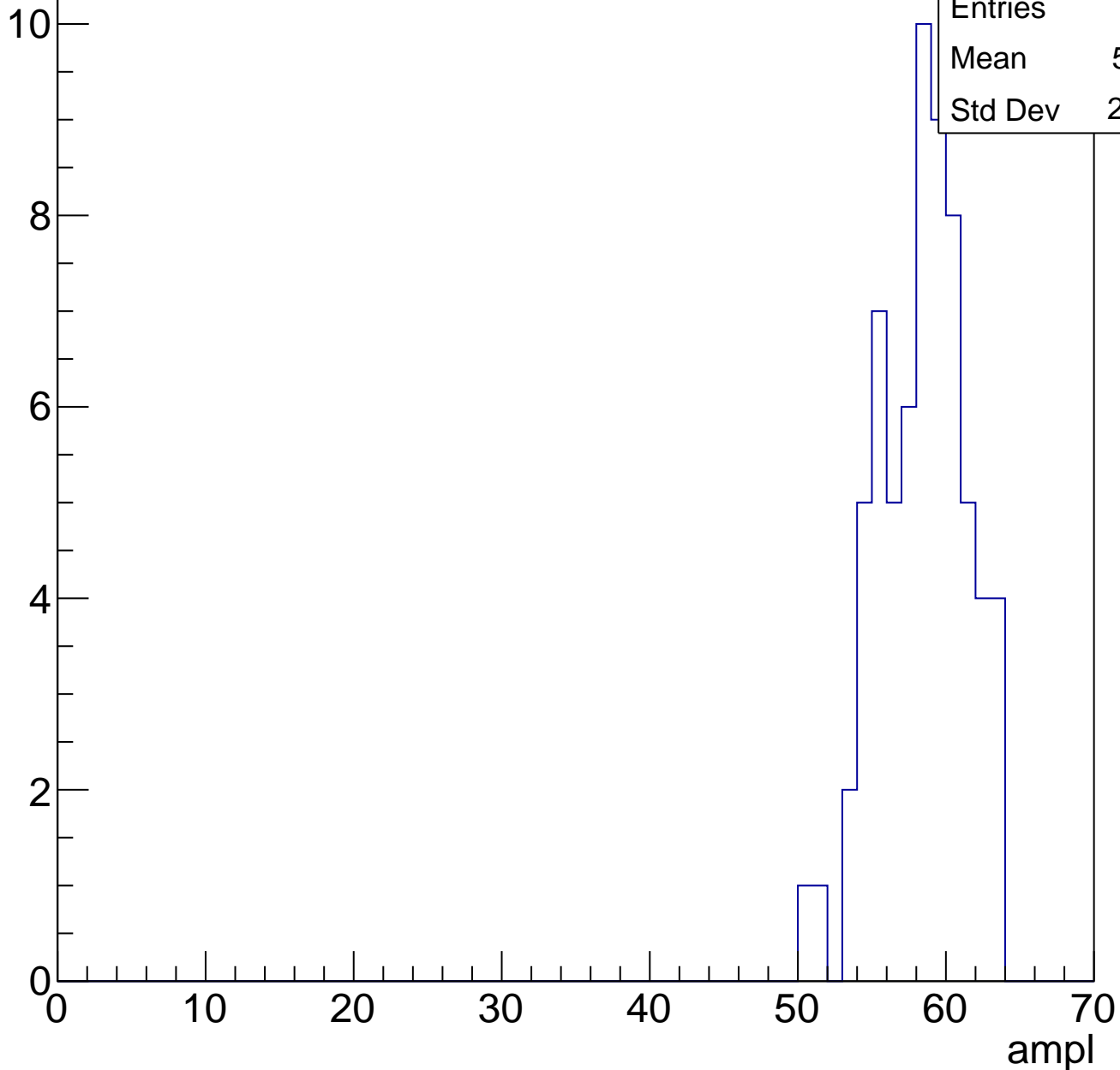


B0L002S, U9-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	57.91
Std Dev	2.946

Entry

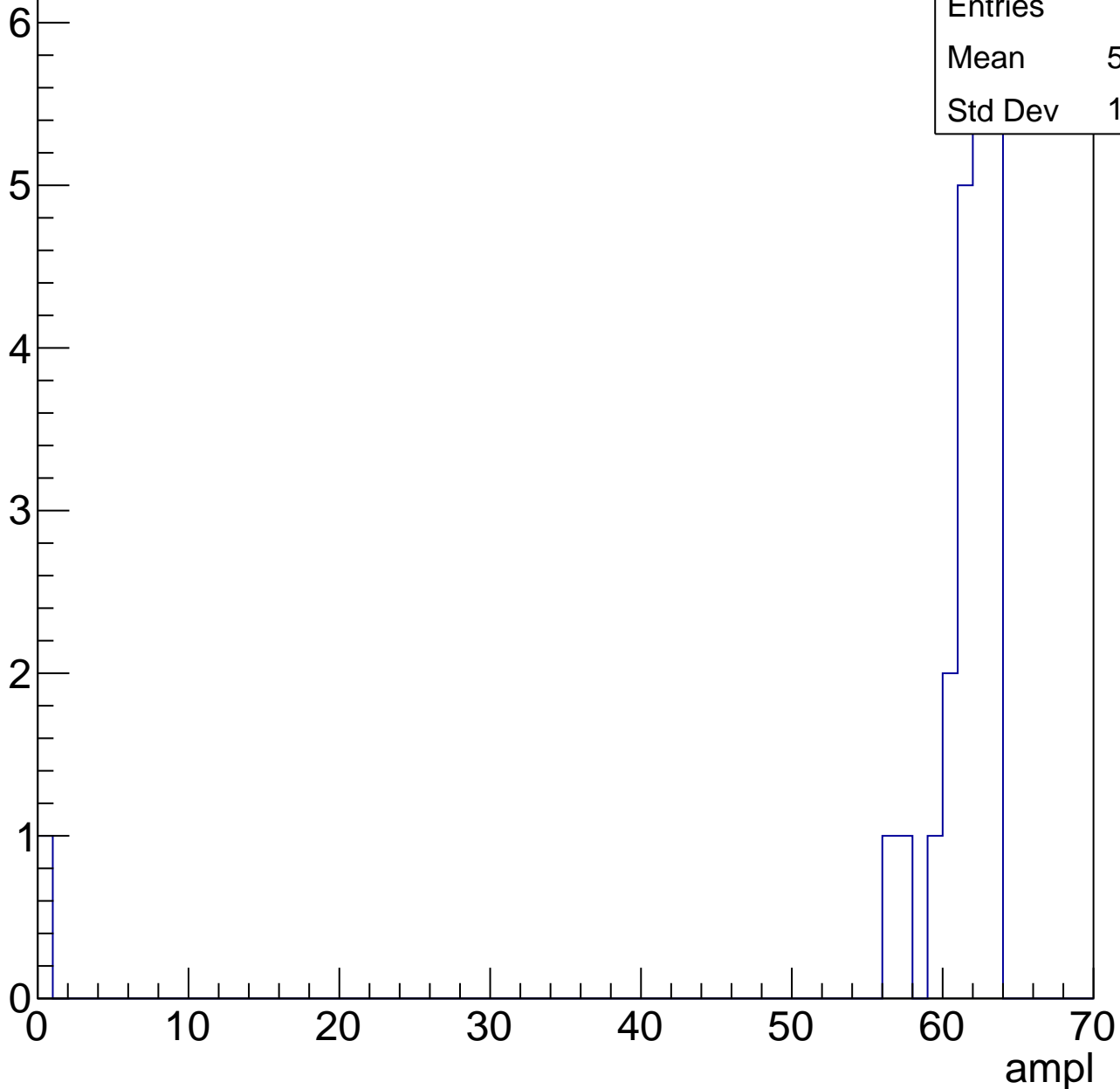


B0L002S, U9-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.57
Std Dev	12.62



B0L002S, U9-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U9-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U9-ch126, adc0

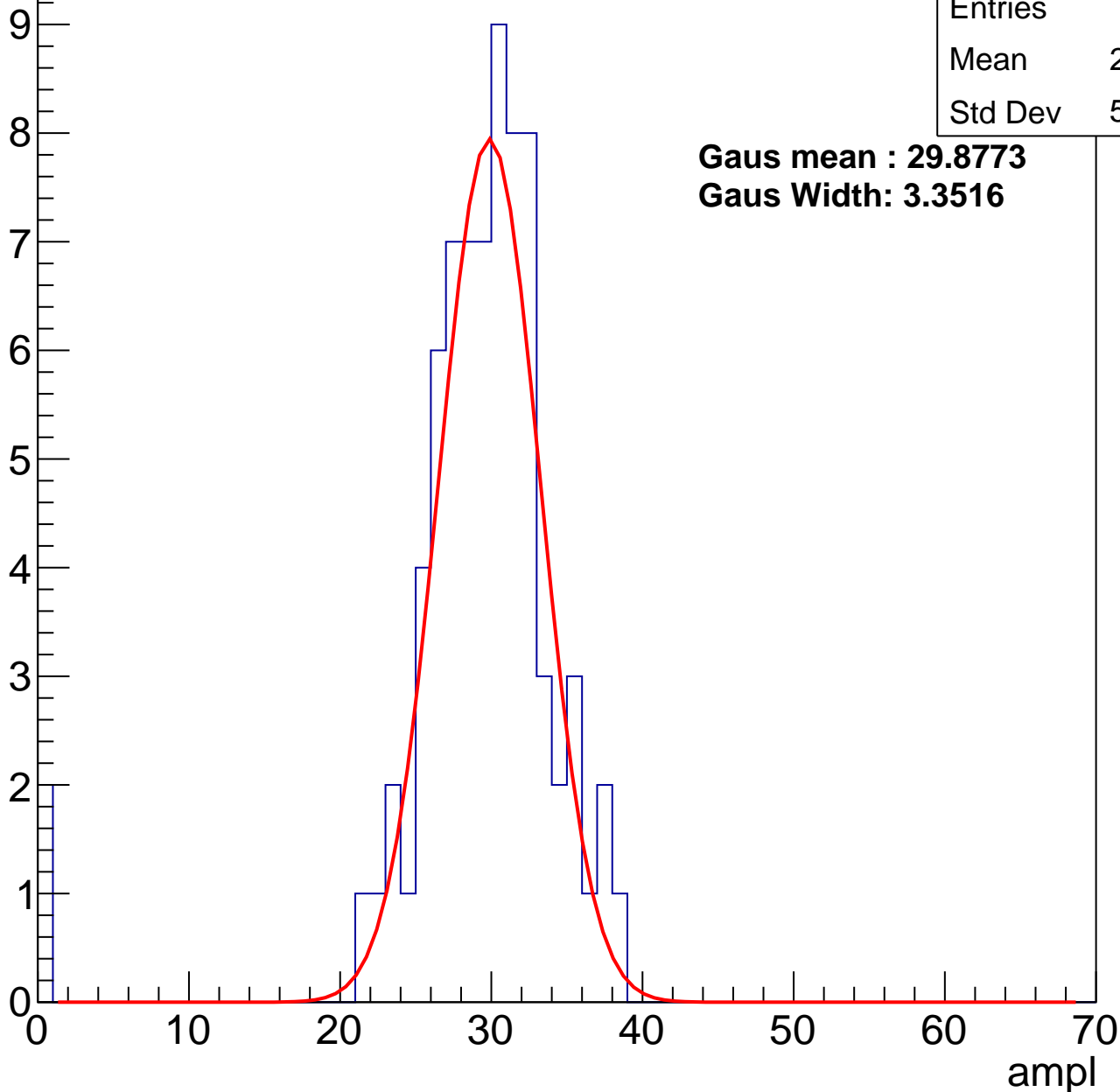
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	28.68
Std Dev	5.904

Gaus mean : 29.8773

Gaus Width: 3.3516



B0L002S, U9-ch126, adc1

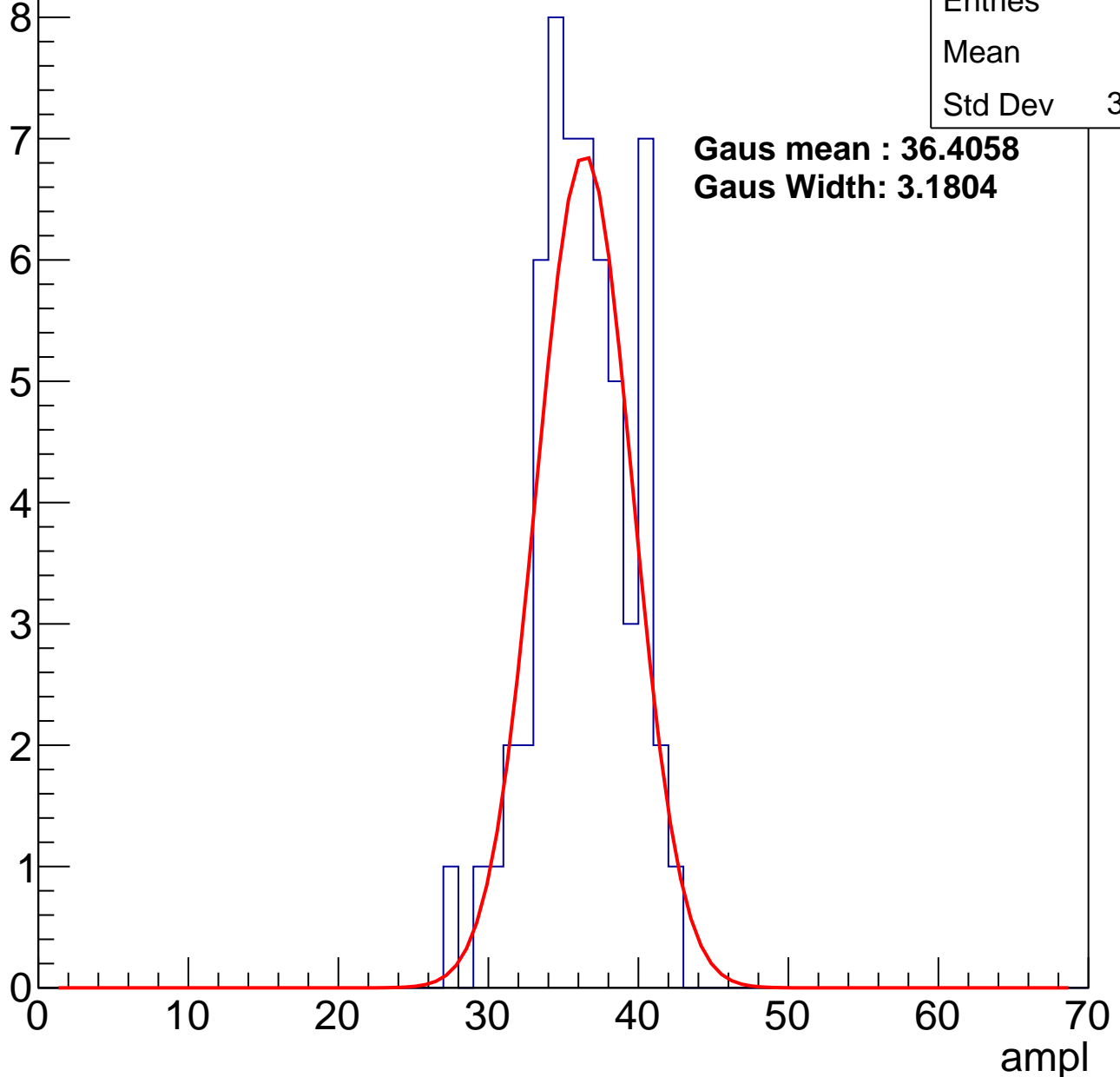
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	35.8
Std Dev	3.161

Gaus mean : 36.4058

Gaus Width: 3.1804



B0L002S, U9-ch126, adc2

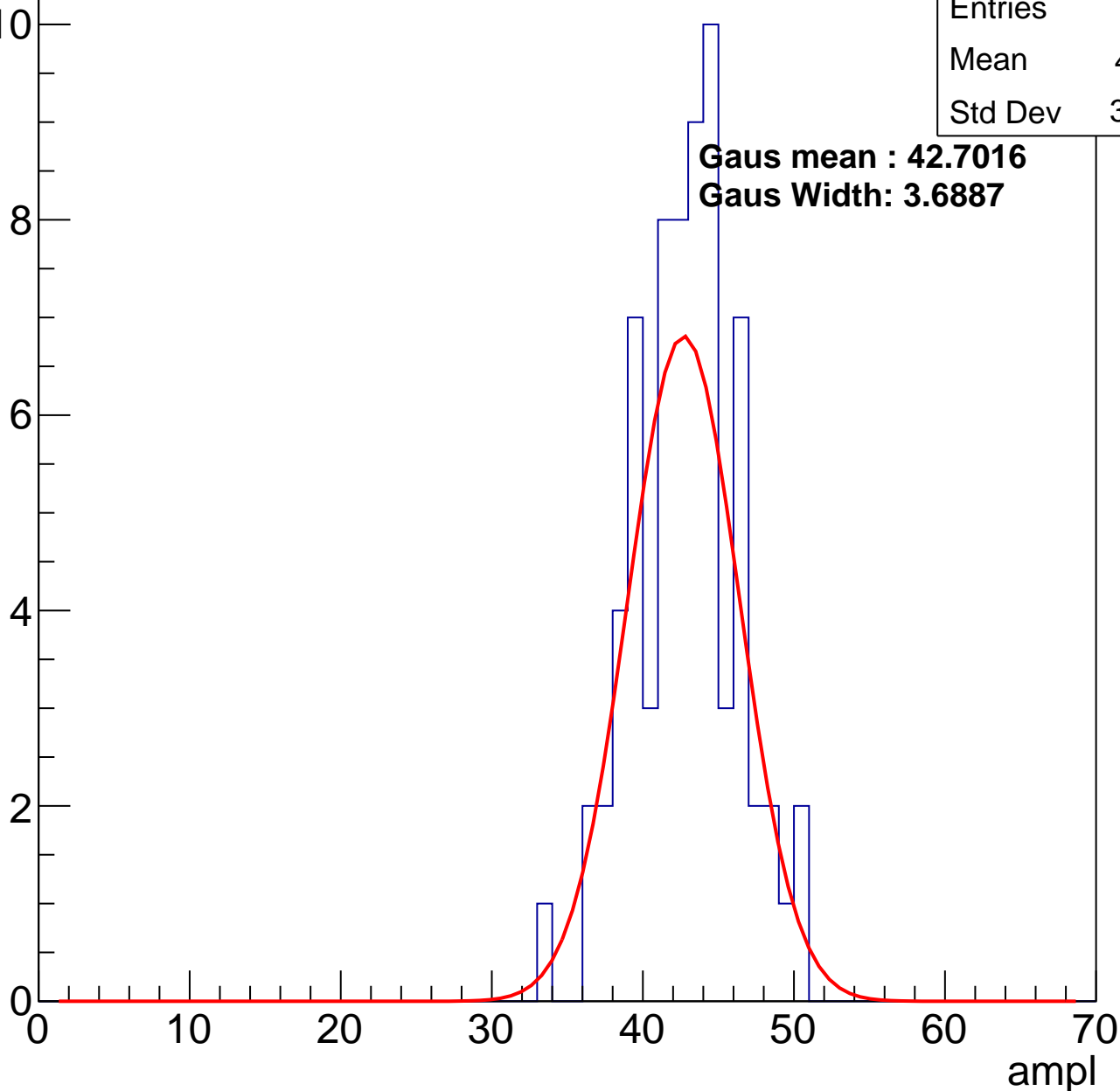
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	42.41
Std Dev	3.409

Gaus mean : 42.7016

Gaus Width: 3.6887

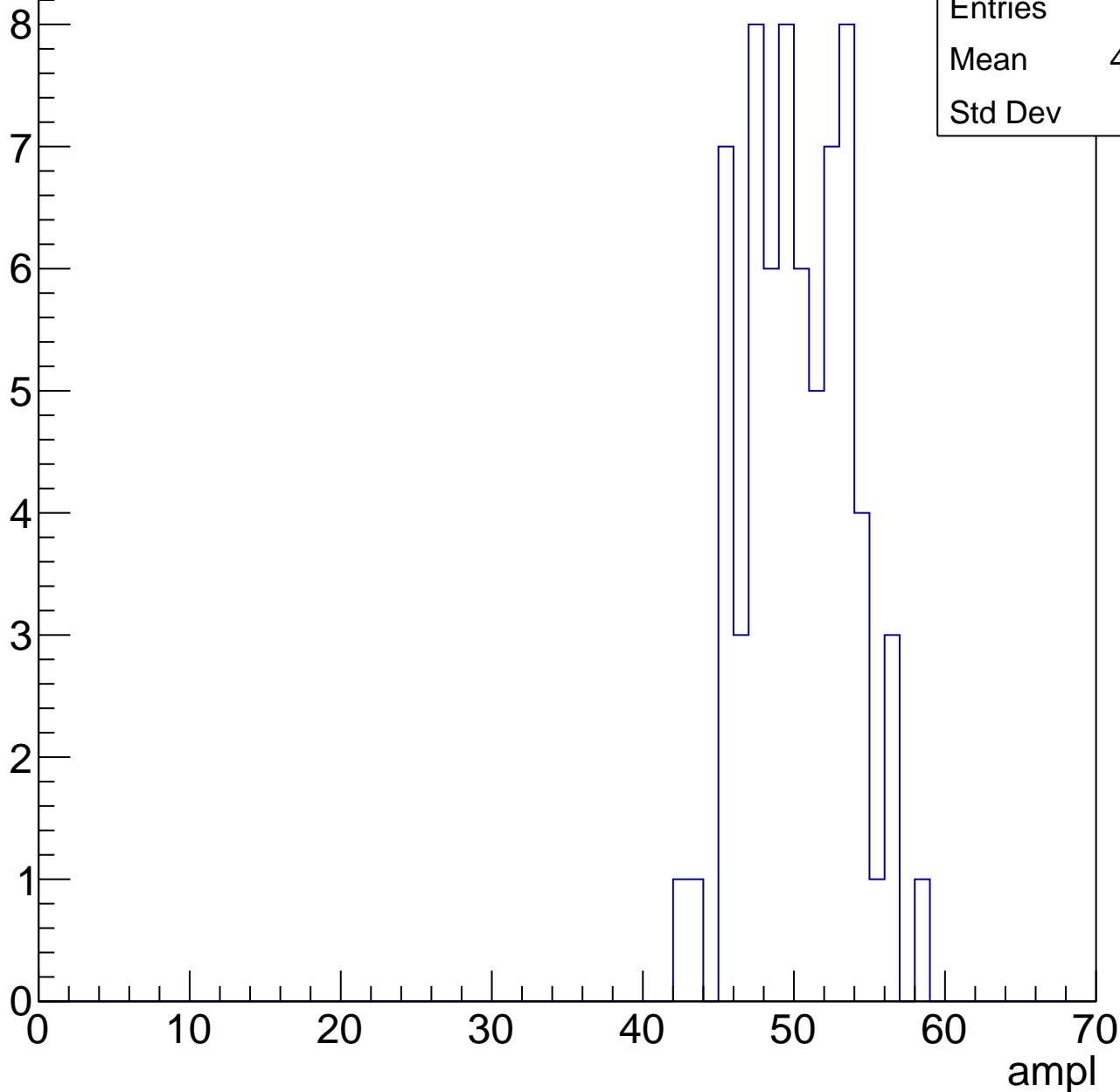


B0L002S, U9-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

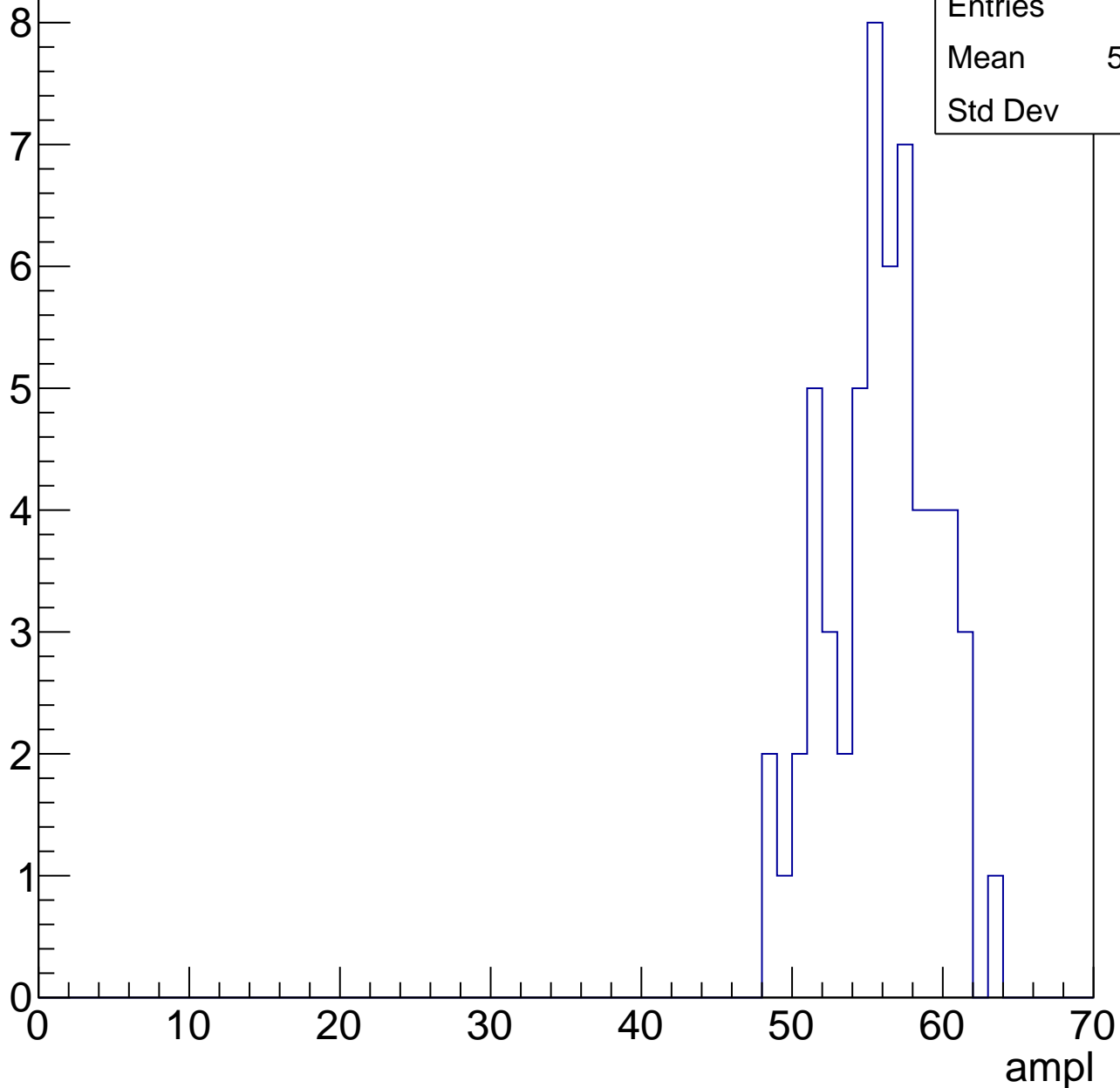
Entries	69
Mean	49.77
Std Dev	3.41



B0L002S, U9-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

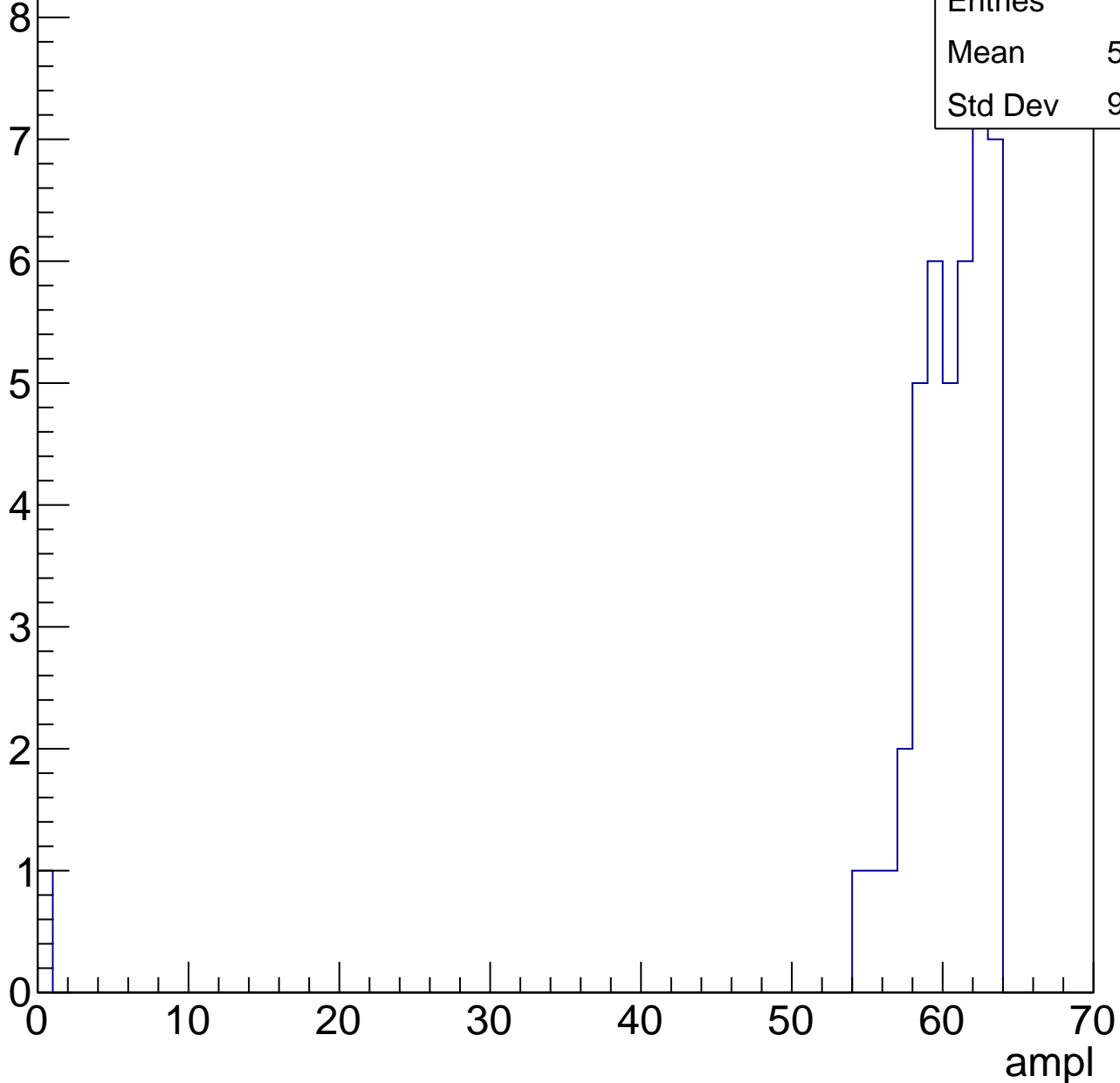


B0L002S, U9-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

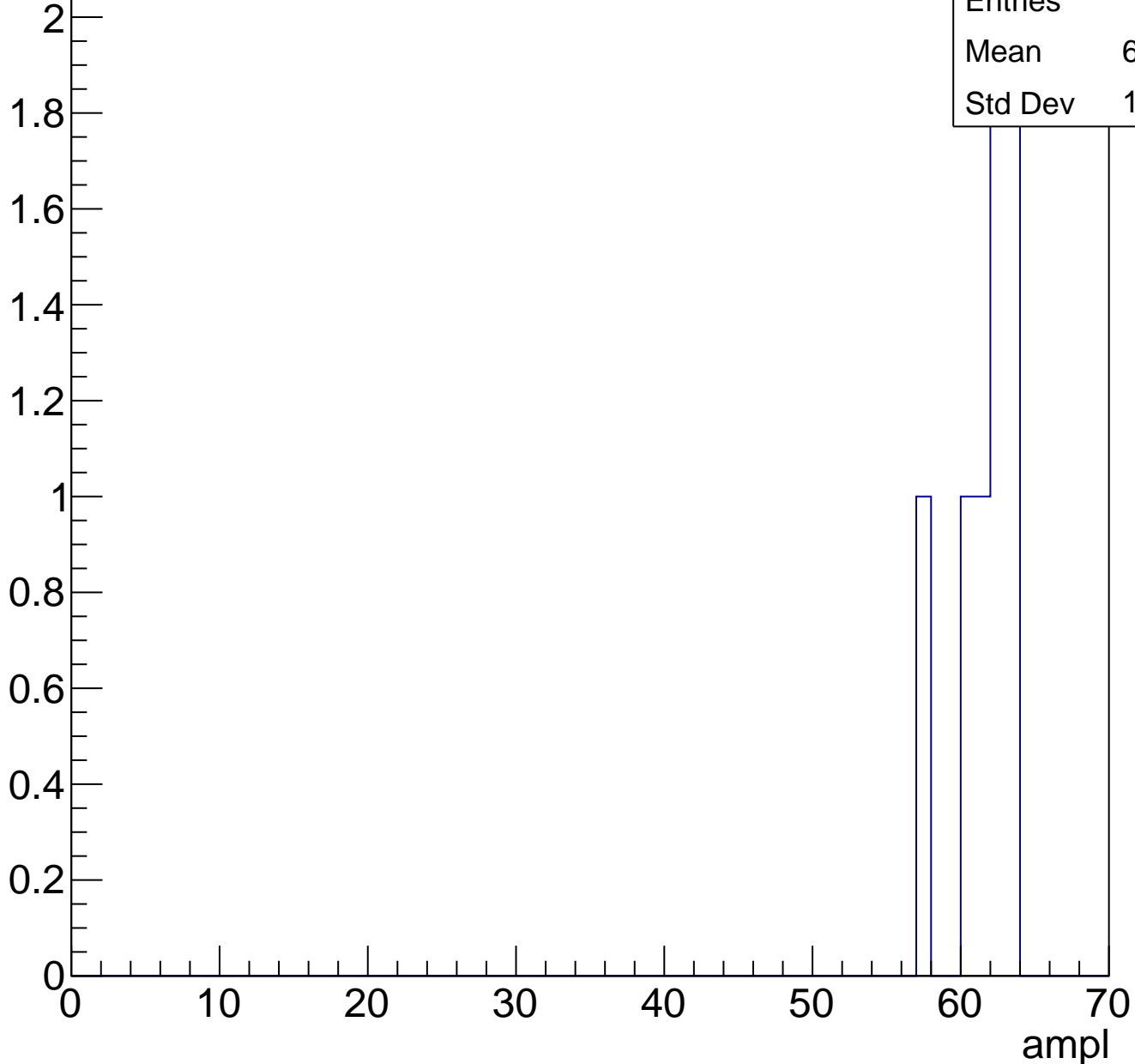
Entries	43
Mean	58.74
Std Dev	9.344



B0L002S, U9-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	7
Mean	61.14
Std Dev	1.959

B0L002S, U9-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch127, adc0

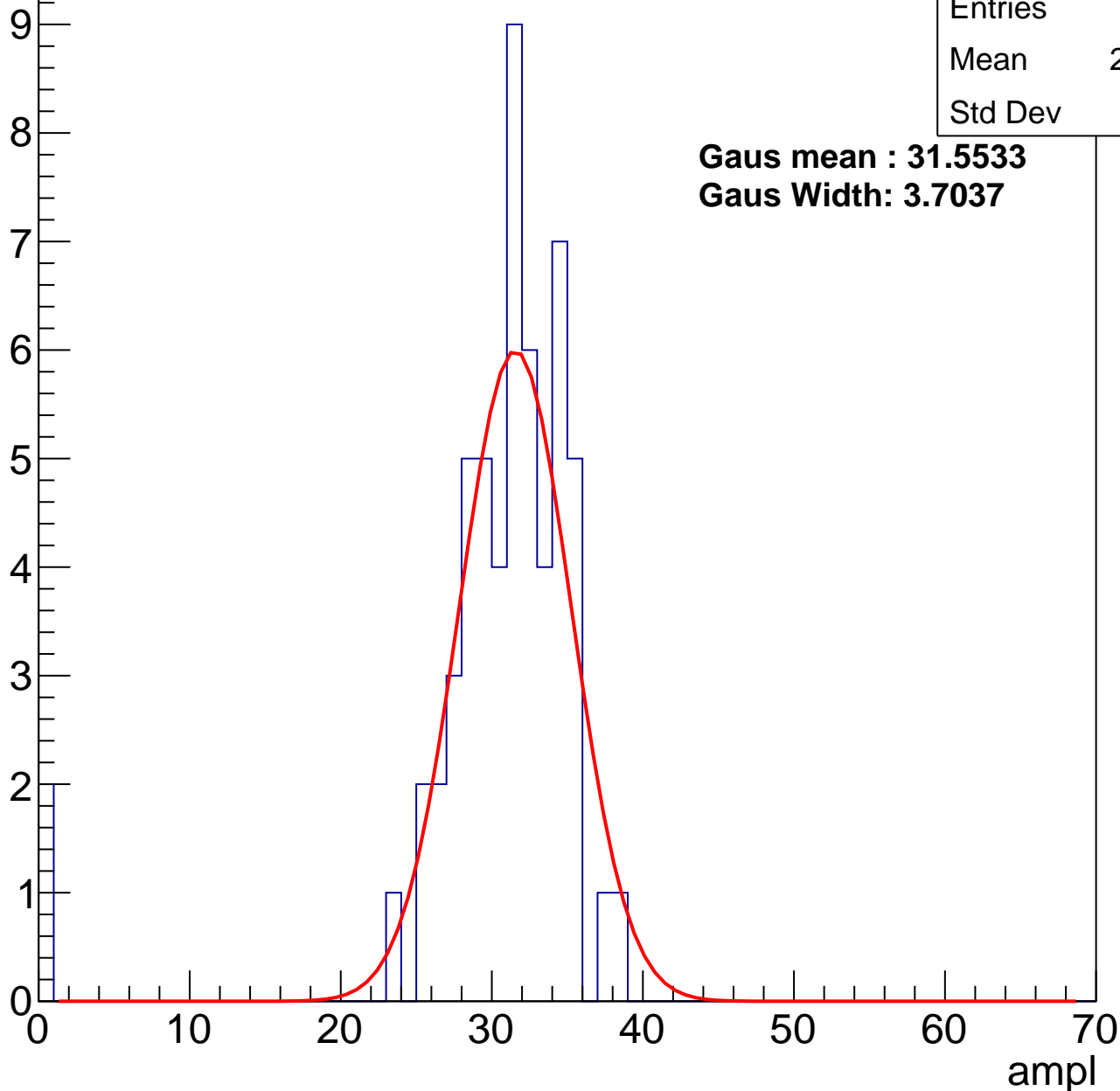
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.86
Std Dev	6.49

Gaus mean : 31.5533

Gaus Width: 3.7037



B0L002S, U9-ch127, adc1

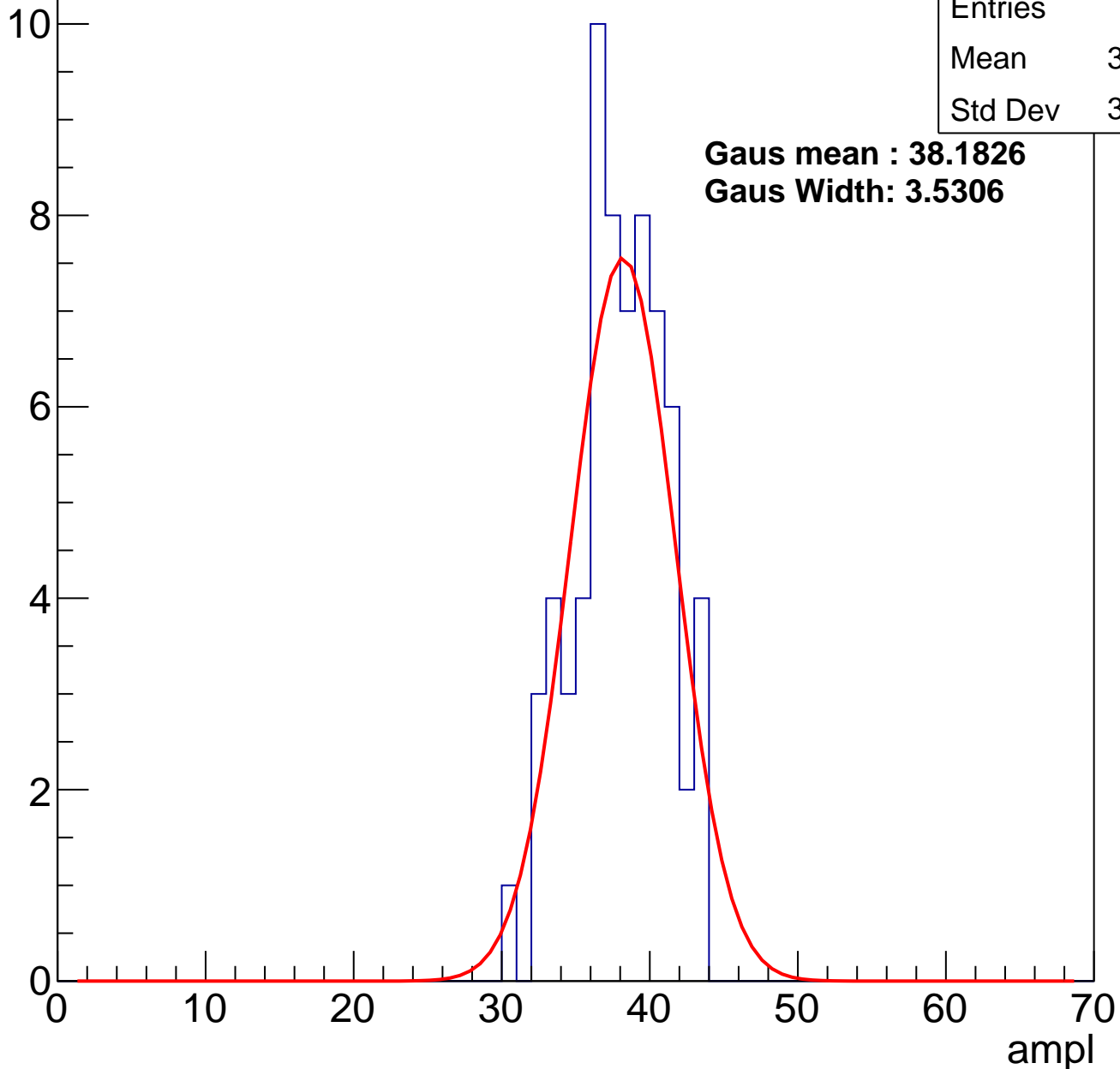
calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	37.55
Std Dev	3.024

Gaus mean : 38.1826

Gaus Width: 3.5306

Entry



B0L002S, U9-ch127, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	44.48
Std Dev	3.654

Gaus mean : 45.0192

Gaus Width: 3.8425

Entry

10

8

6

4

2

0

0

10

20

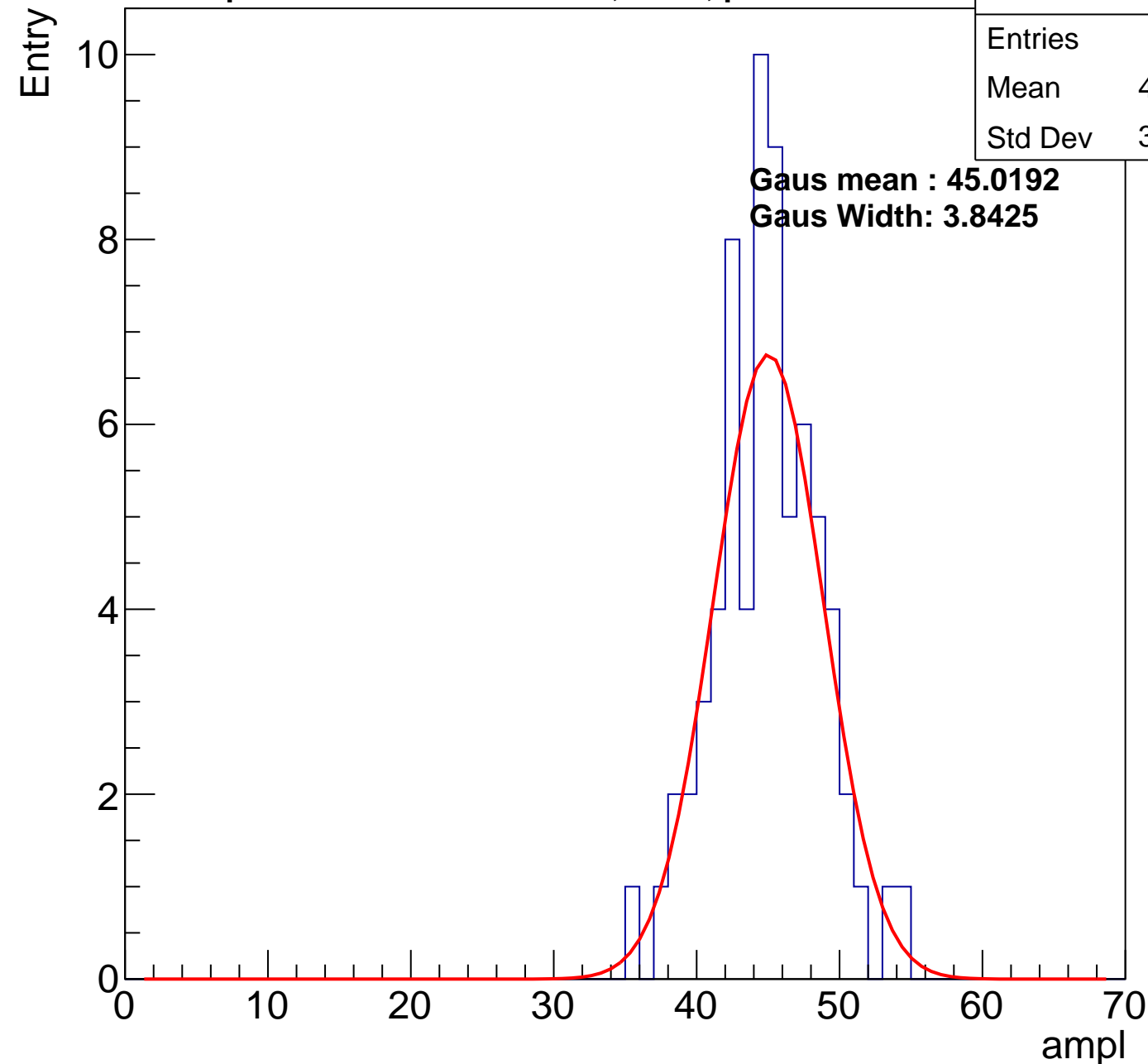
30

40

50

60

ampl

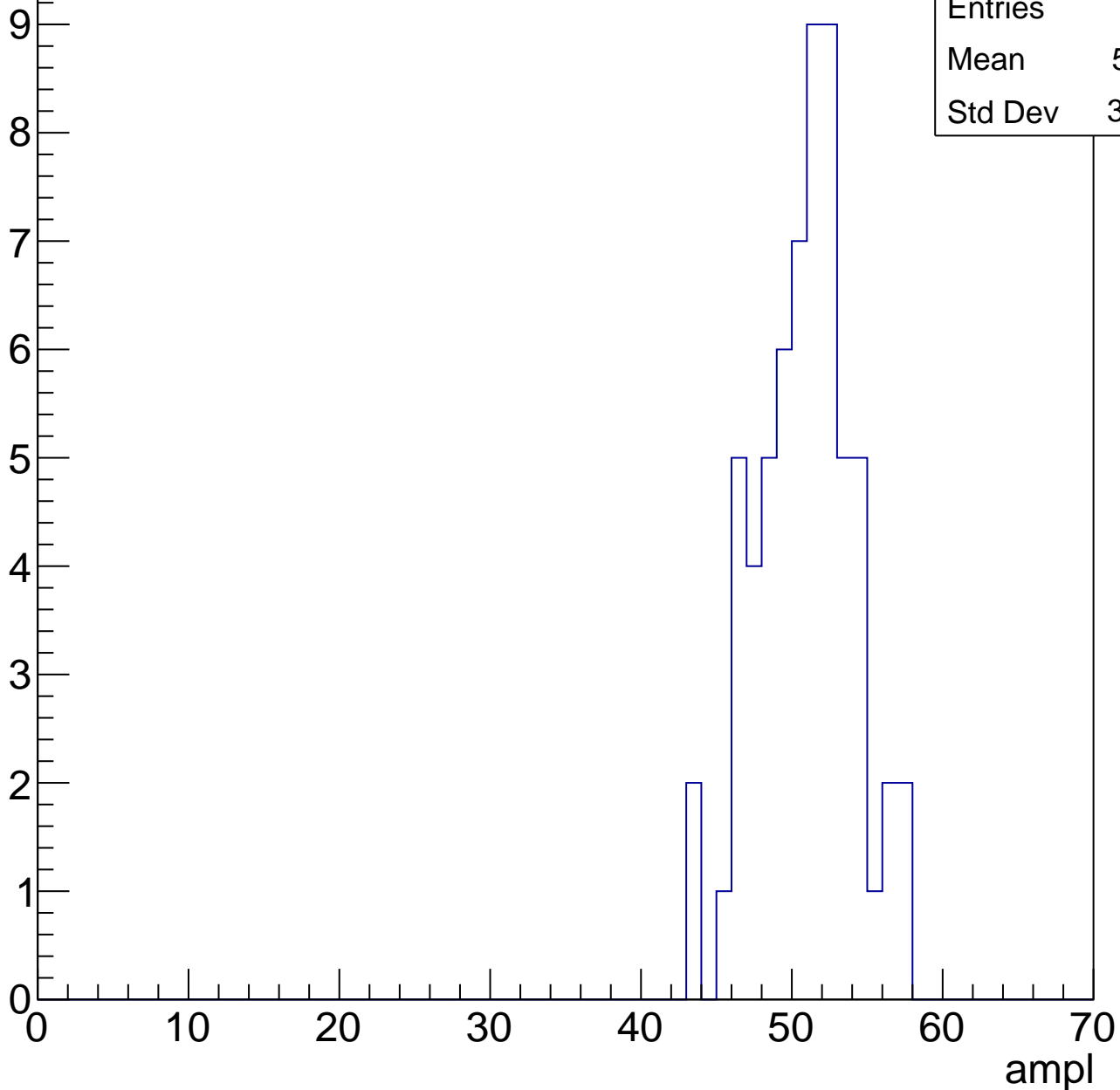


B0L002S, U9-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	50.41
Std Dev	3.135

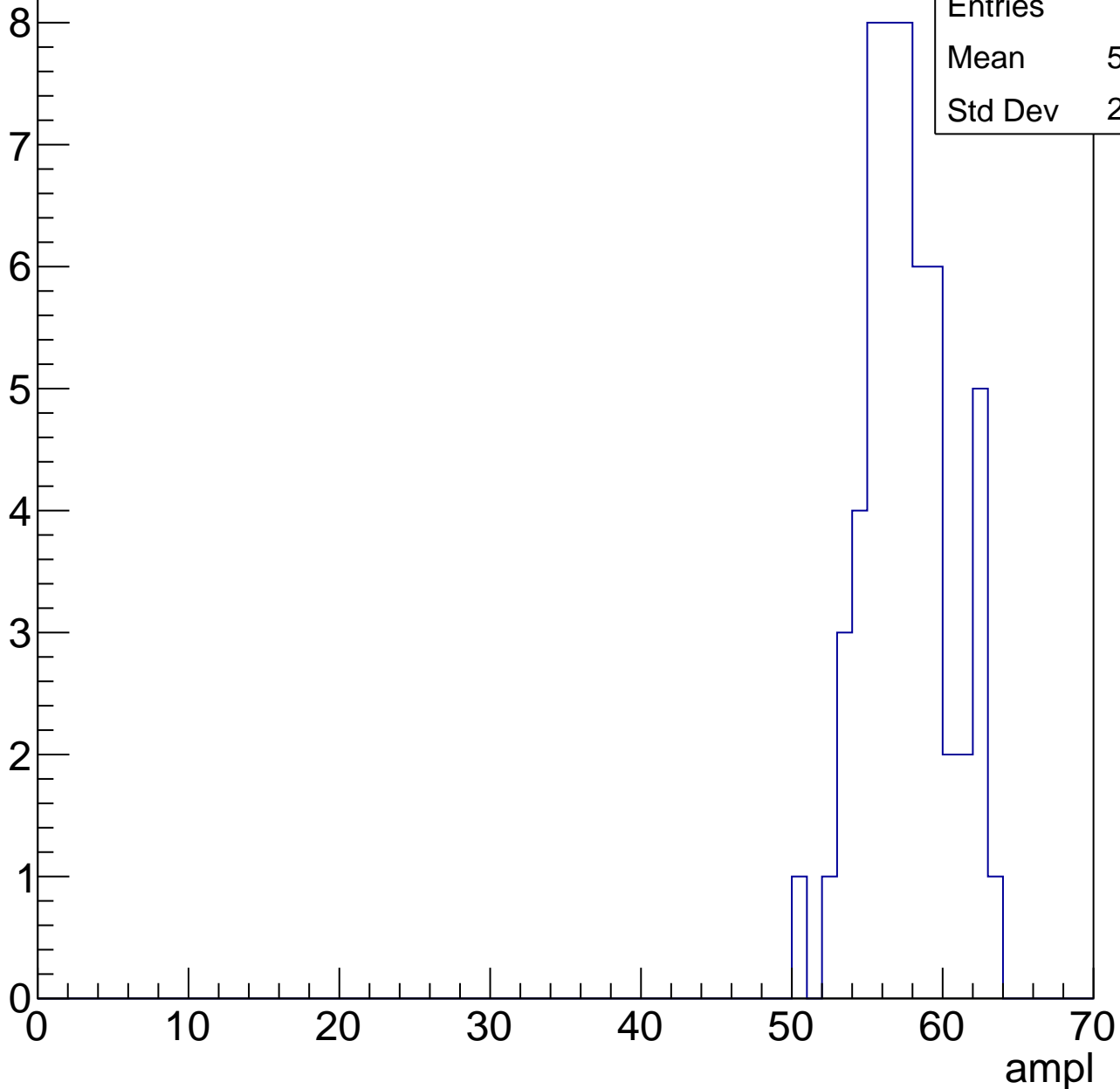


B0L002S, U9-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	57.05
Std Dev	2.825

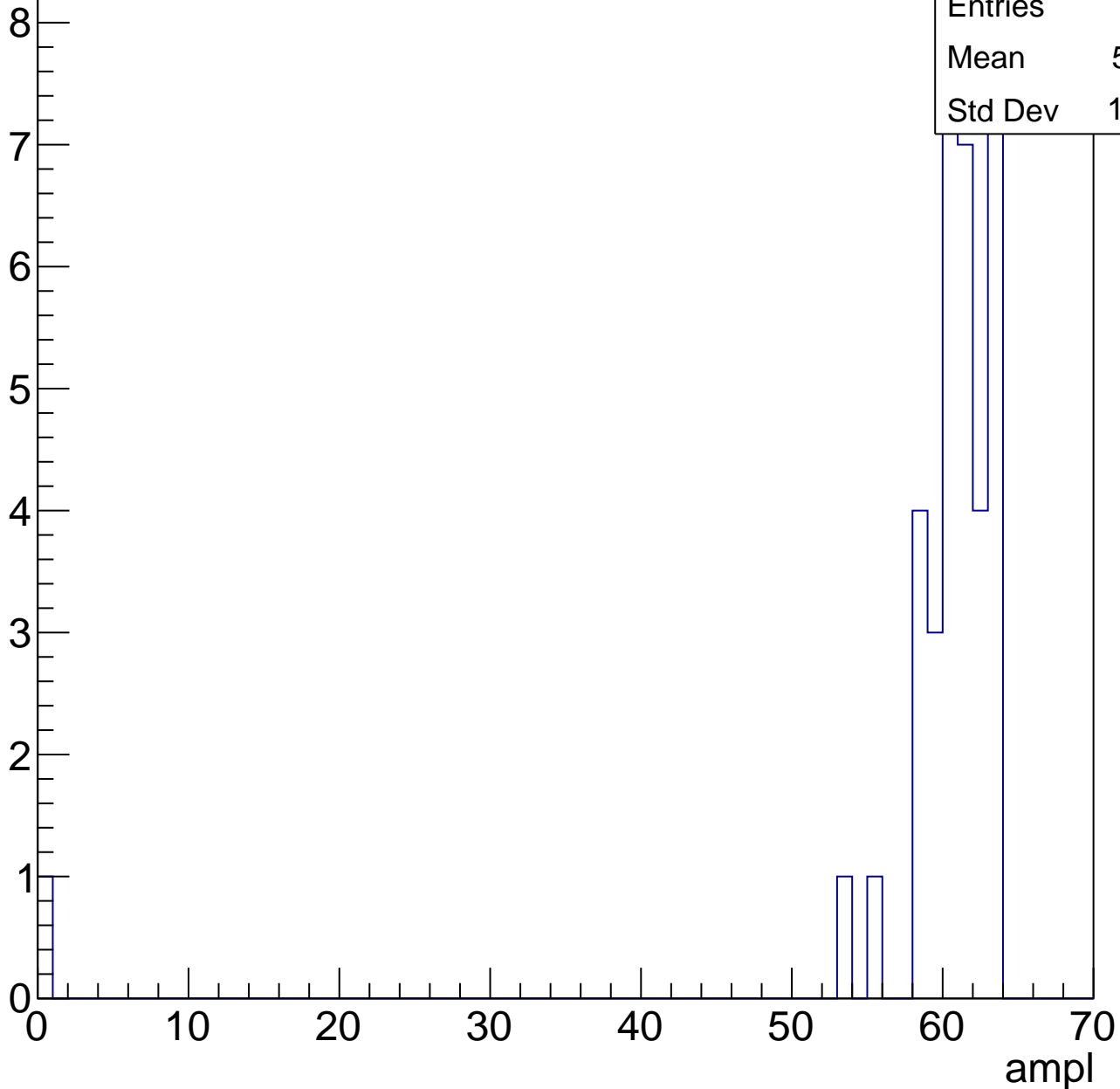


B0L002S, U9-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

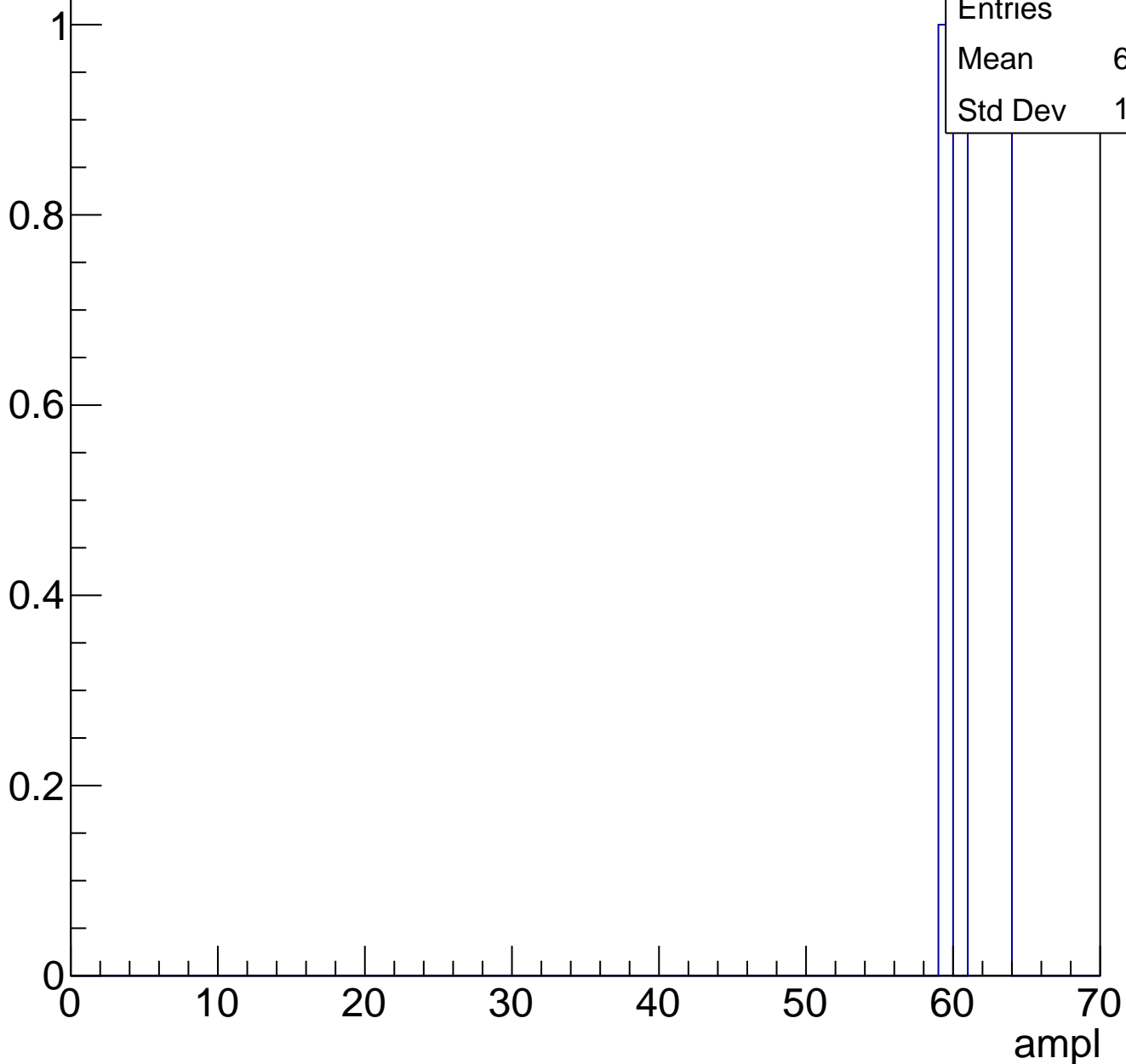
Entries	37
Mean	58.81
Std Dev	10.05



B0L002S, U9-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U9-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

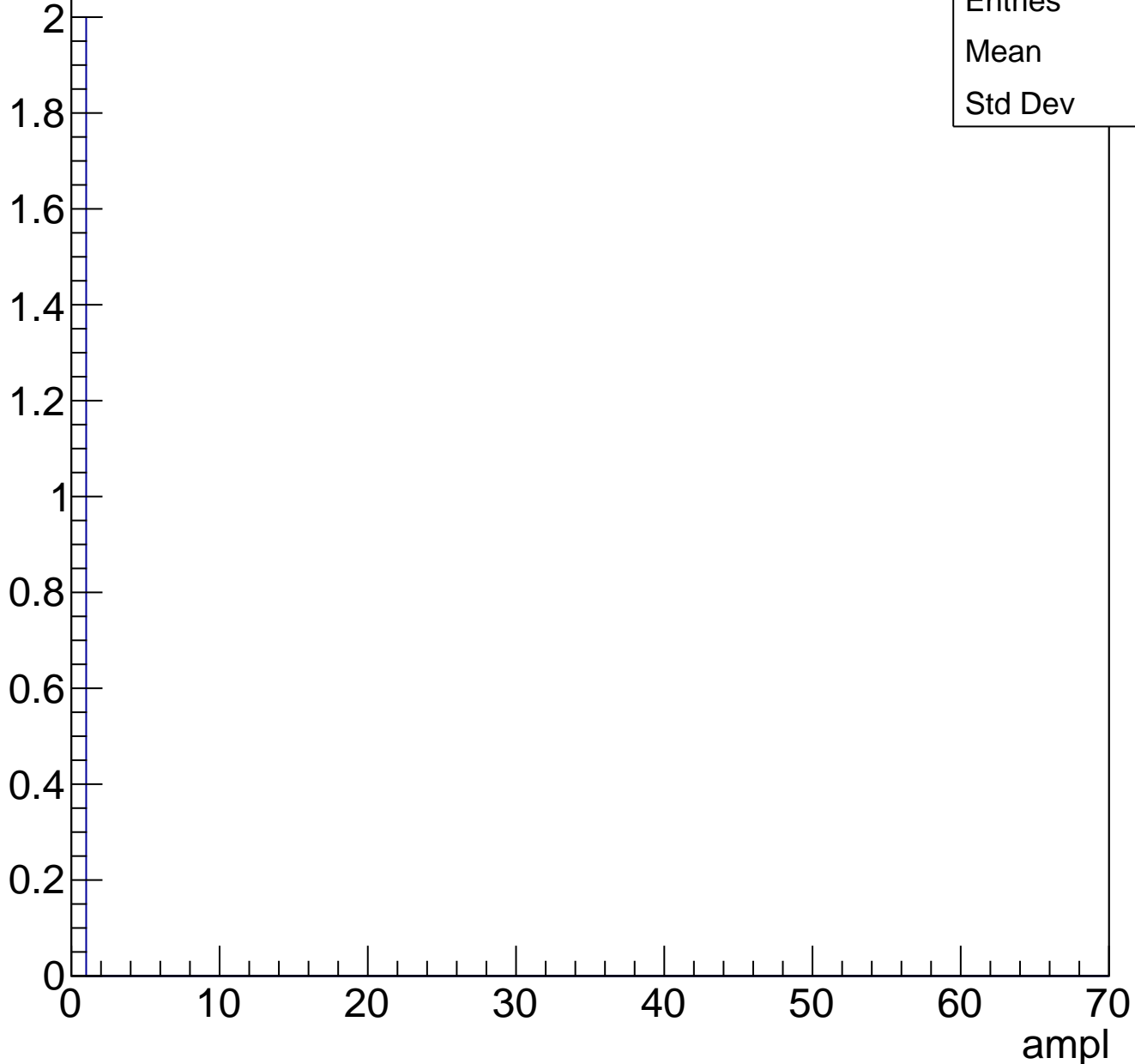


Entries	2
Mean	0
Std Dev	0

B0L002S, U9-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0