

B0L000S, U24-ch0

calib_packv5_042523_0143.root, FC#5, port B1

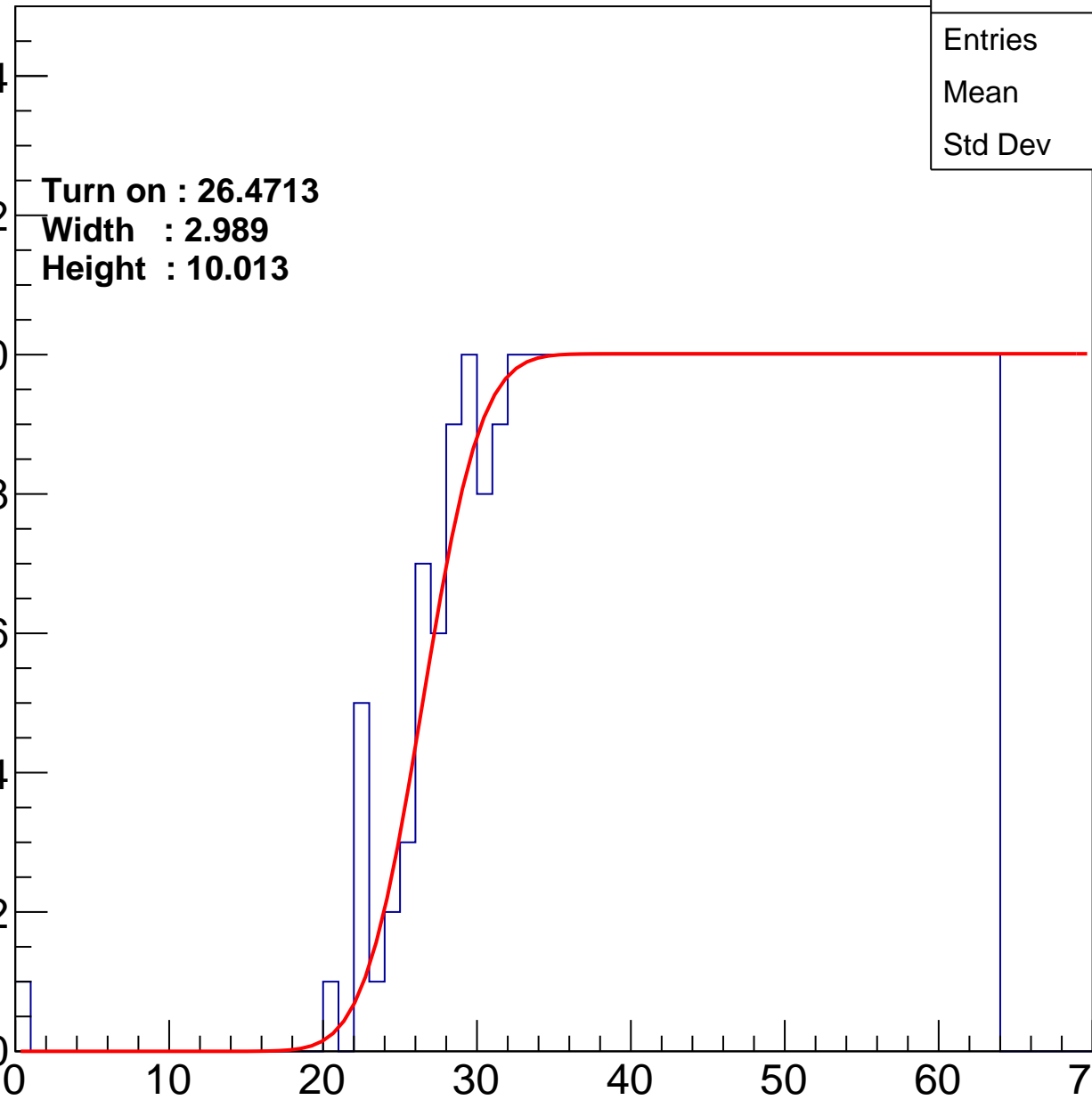
Entry

14
12
10
8
6
4
2
0

Turn on : 26.4713
Width : 2.989
Height : 10.013

Entries	382
Mean	44.19
Std Dev	11.45

ampl



B0L000S, U24-ch1

calib_packv5_042523_0143.root, FC#5, port B1

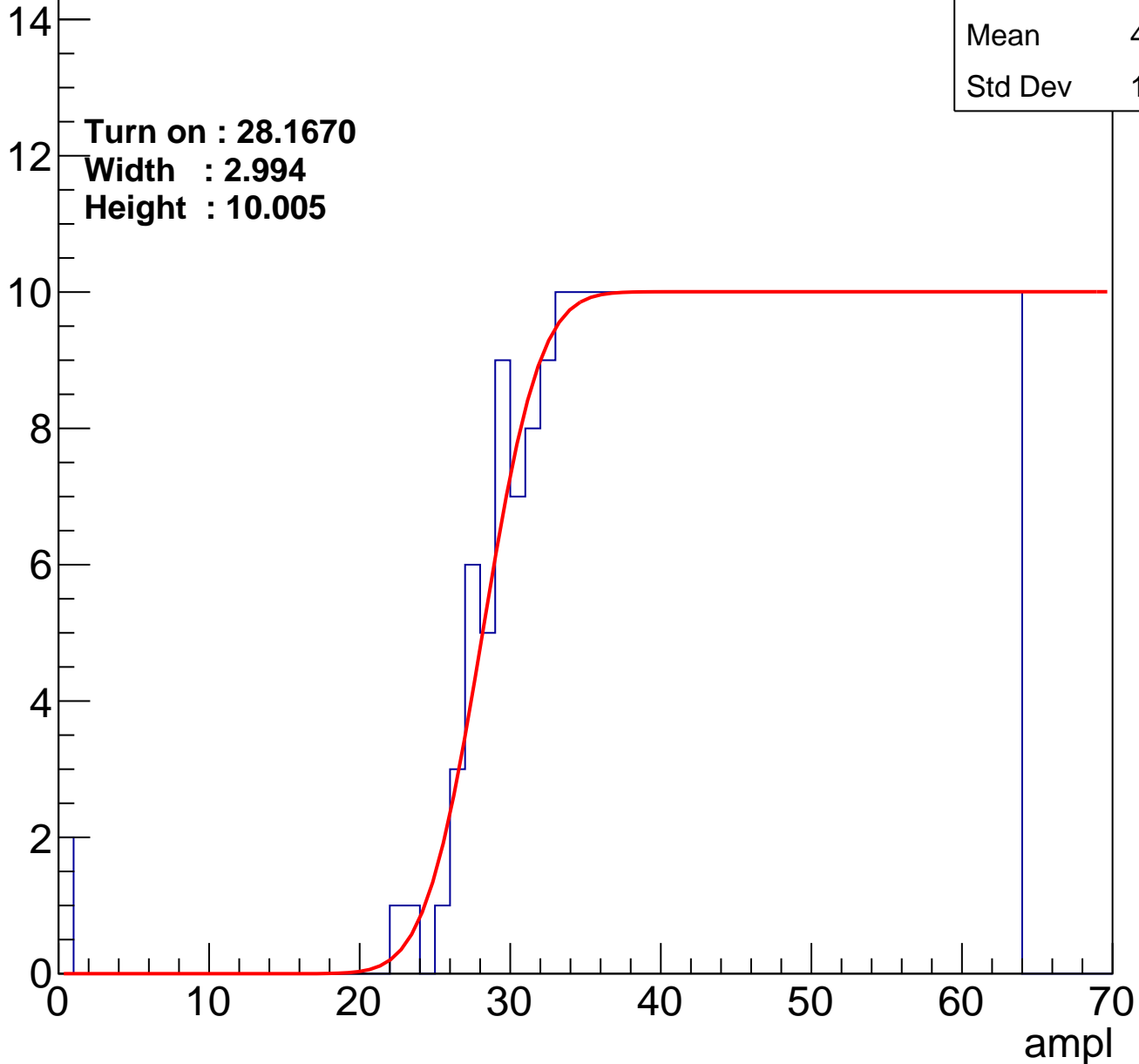
Entries	362
Mean	45.13
Std Dev	11.09

Turn on : 28.1670

Width : 2.994

Height : 10.005

Entry



B0L000S, U24-ch2

calib_packv5_042523_0143.root, FC#5, port B1

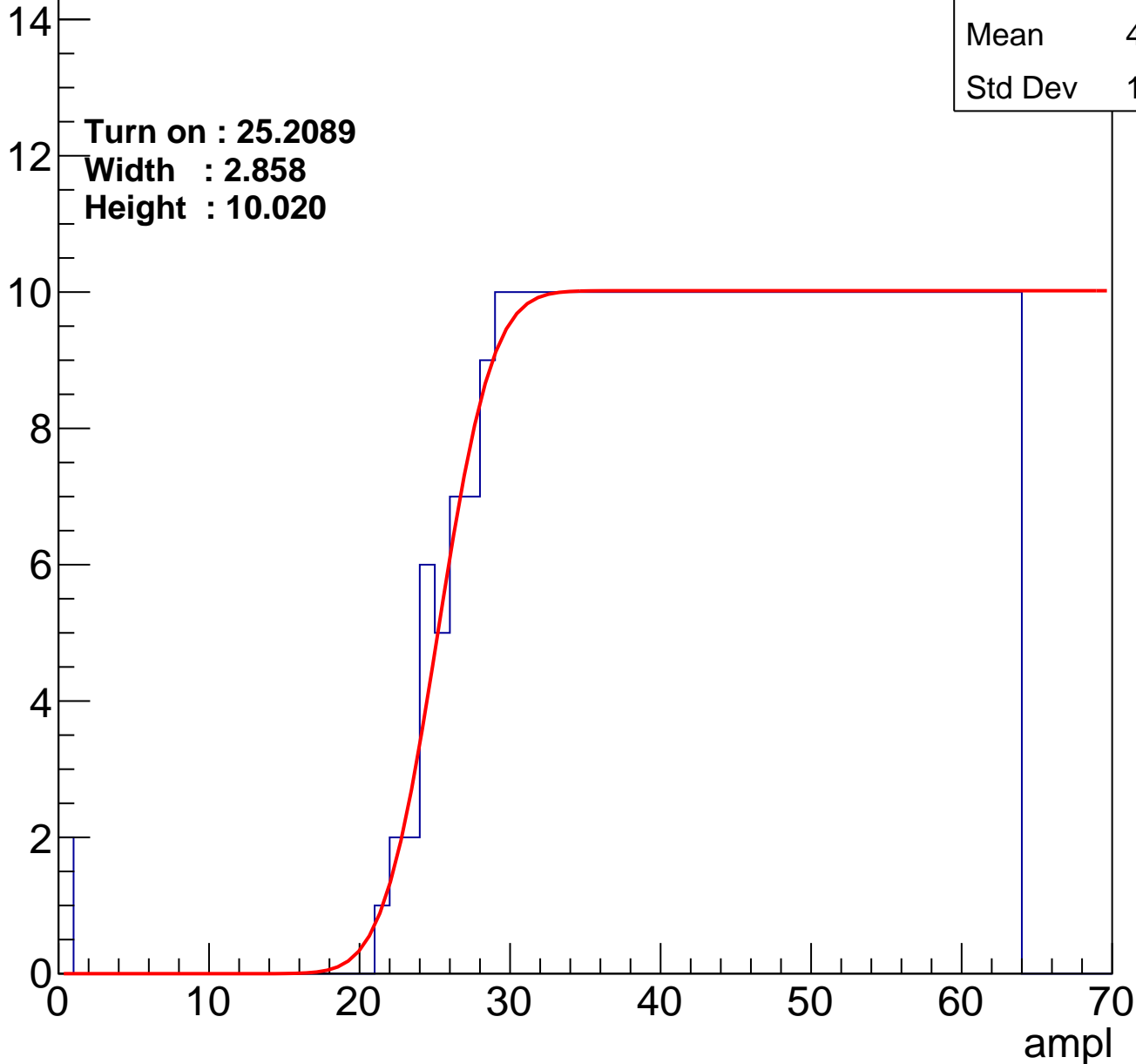
Entry

Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.2089

Width : 2.858

Height : 10.020



B0L000S, U24-ch3

calib_packv5_042523_0143.root, FC#5, port B1

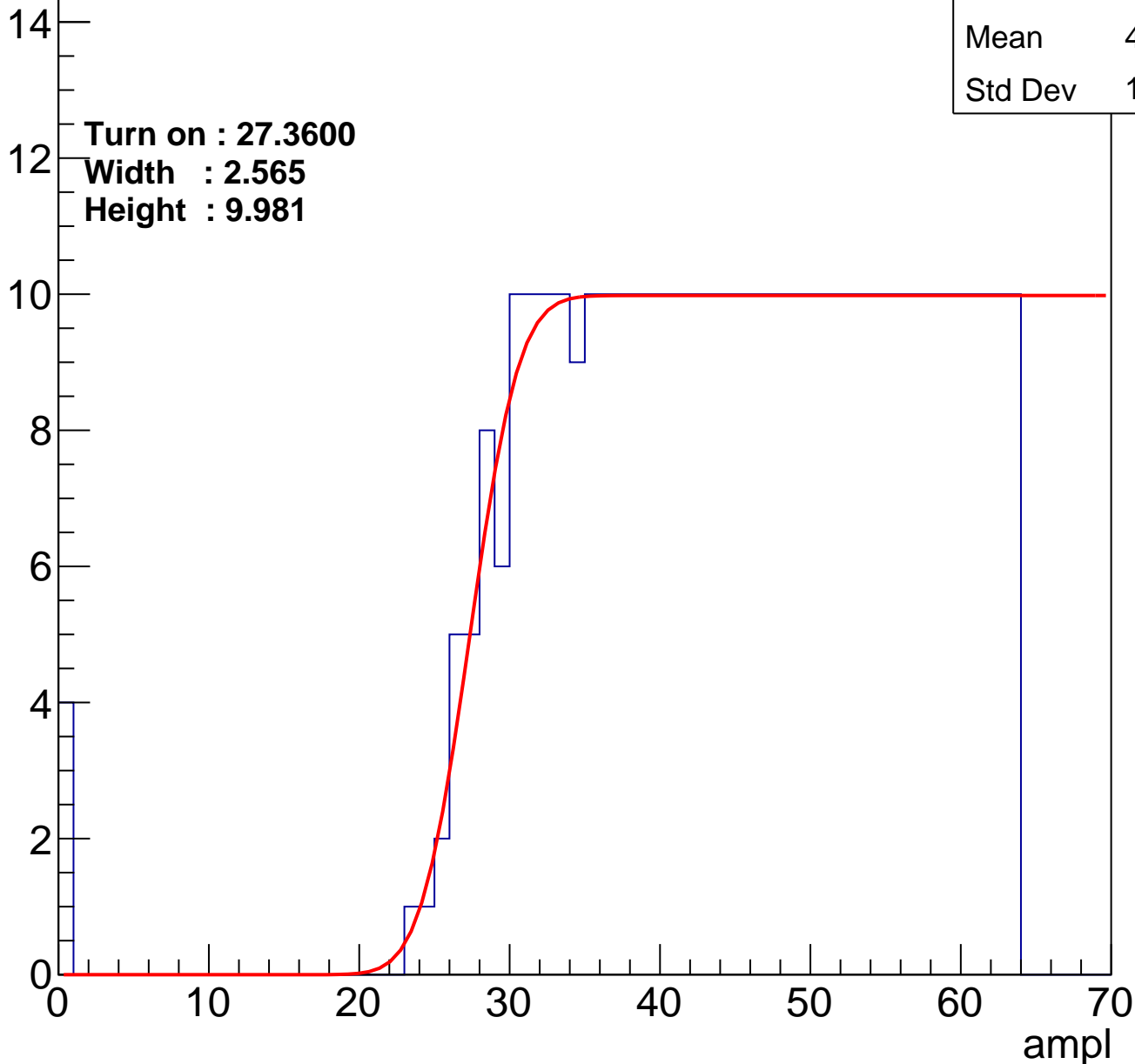
Entry

Entries	371
Mean	44.57
Std Dev	11.66

Turn on : 27.3600

Width : 2.565

Height : 9.981



B0L000S, U24-ch4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.46
Std Dev	11.73

Turn on : 27.4491

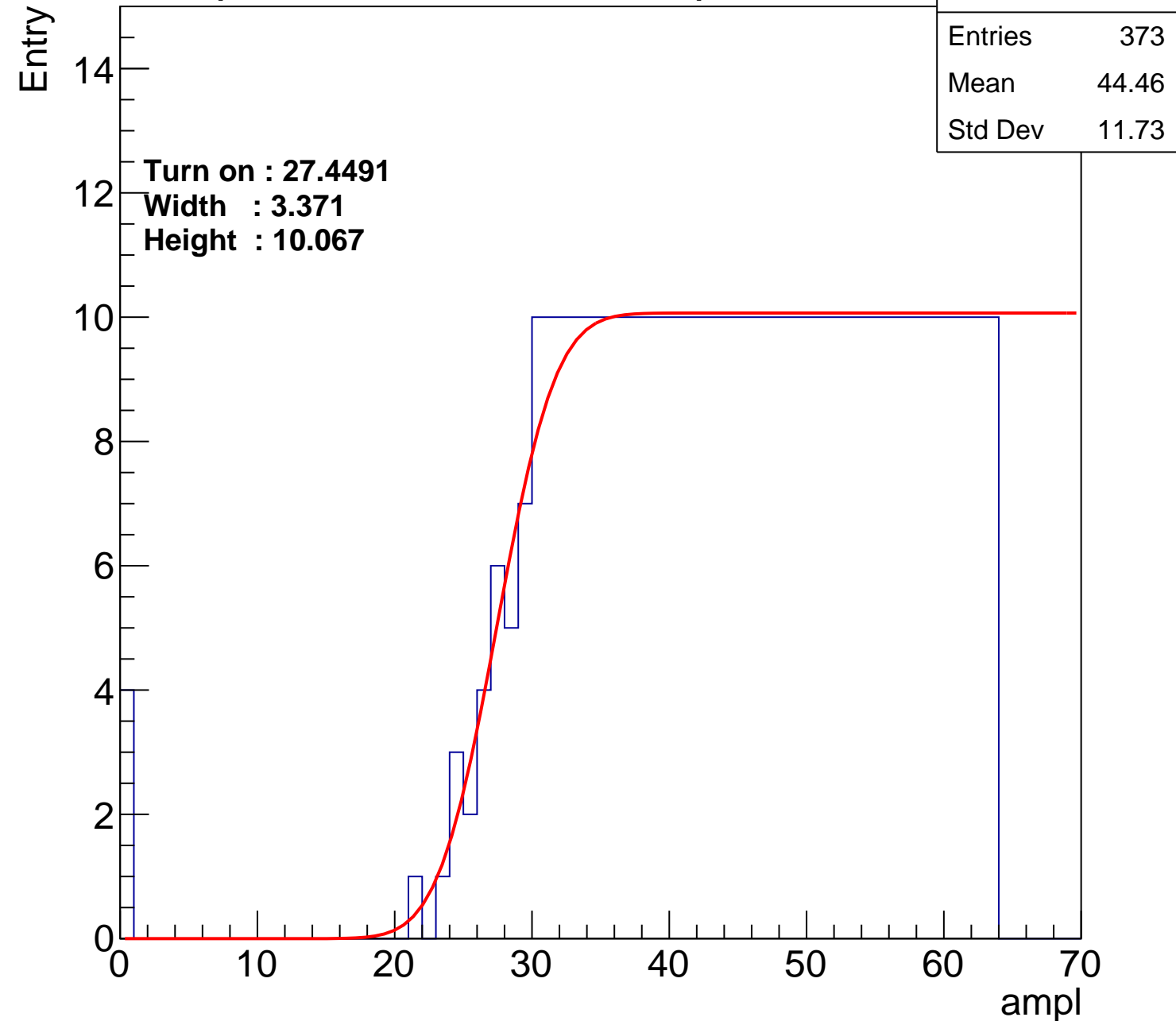
Width : 3.371

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch5

calib_packv5_042523_0143.root, FC#5, port B1

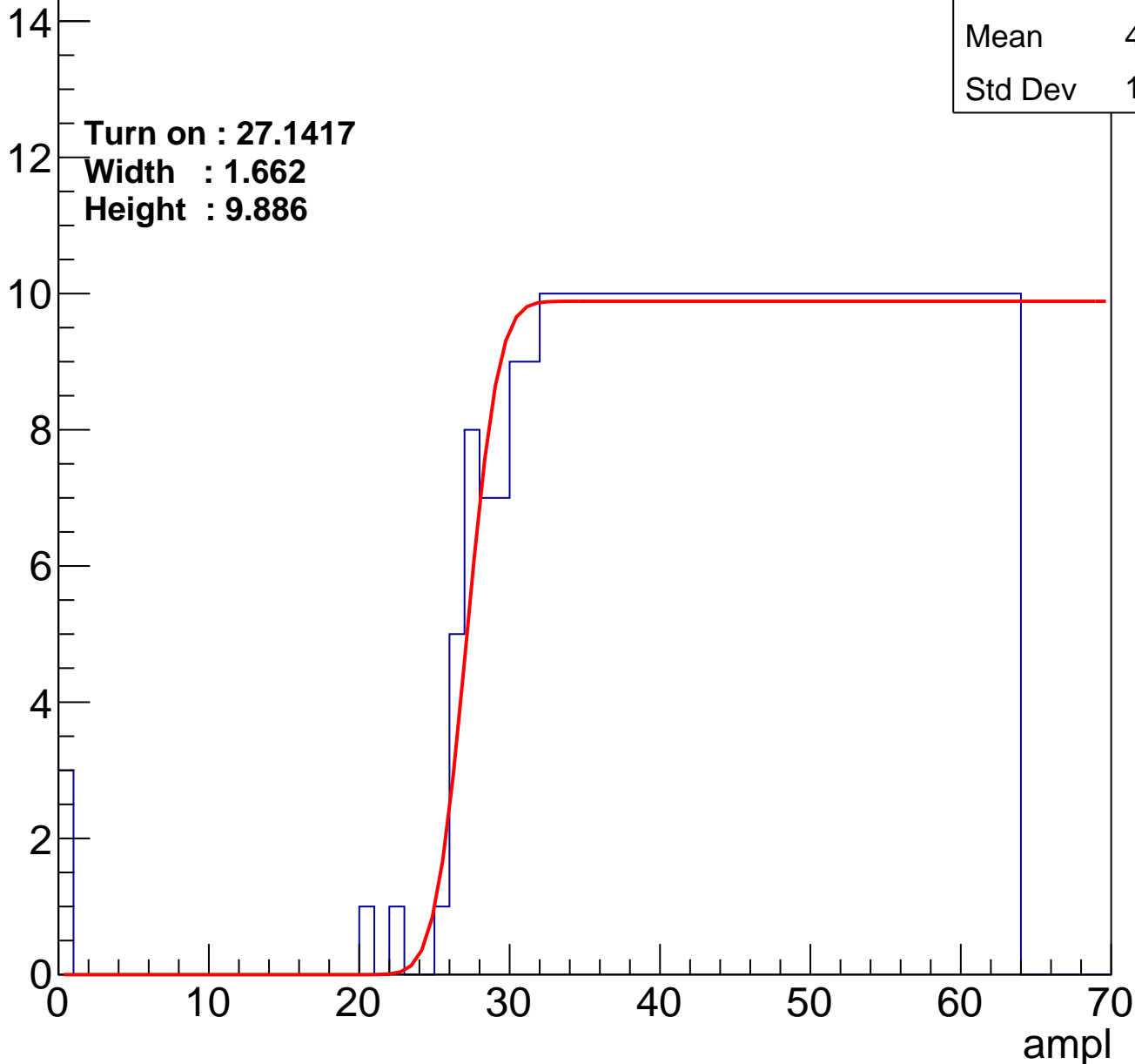
Entry

Entries	371
Mean	44.64
Std Dev	11.48

Turn on : 27.1417

Width : 1.662

Height : 9.886



B0L000S, U24-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.89
Std Dev	11.14

Turn on : 27.2447

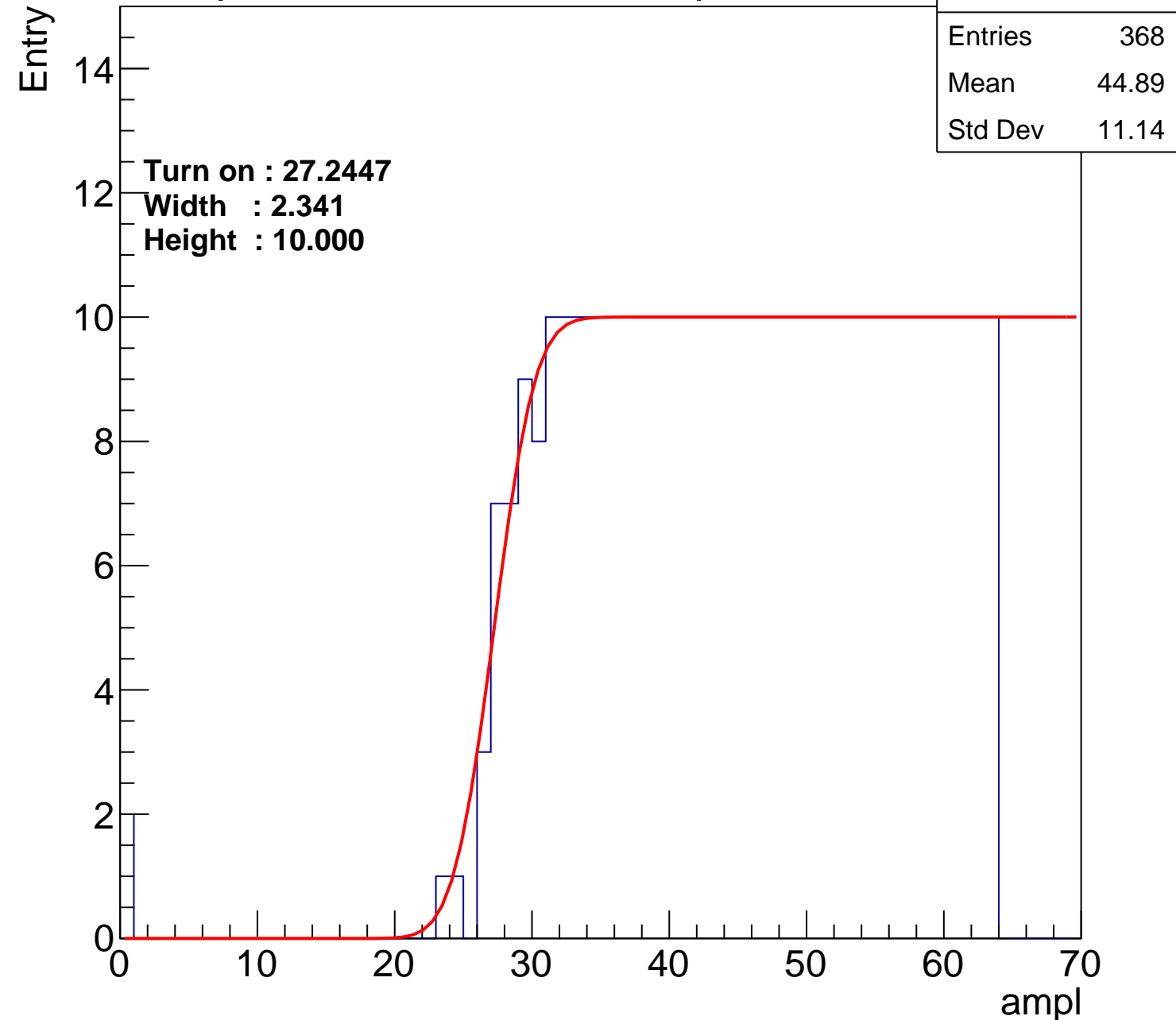
Width : 2.341

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch7

calib_packv5_042523_0143.root, FC#5, port B1

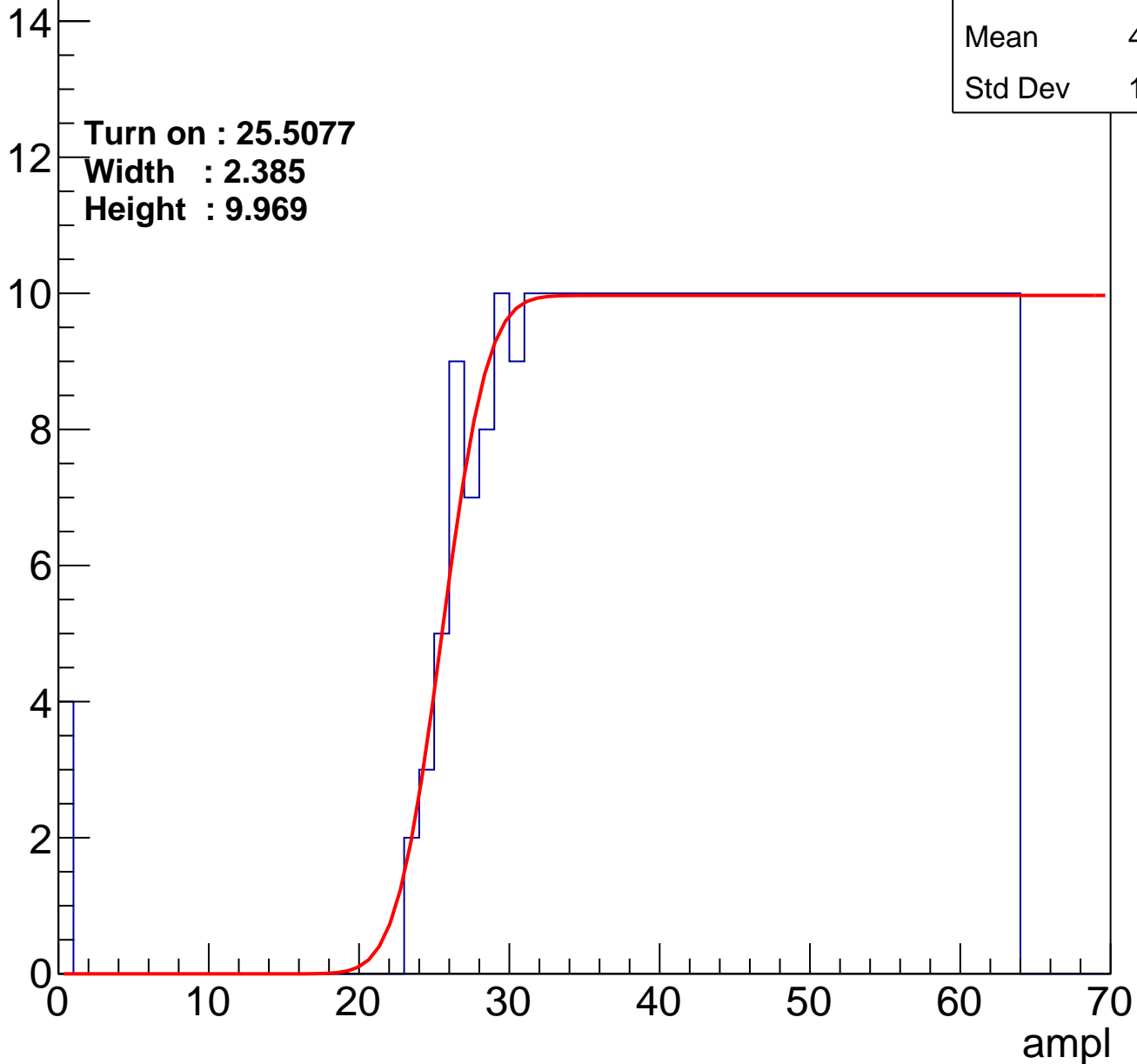
Entry

Entries	387
Mean	43.82
Std Dev	11.98

Turn on : 25.5077

Width : 2.385

Height : 9.969



B0L000S, U24-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.28
Std Dev	11.01

Turn on : 28.2161

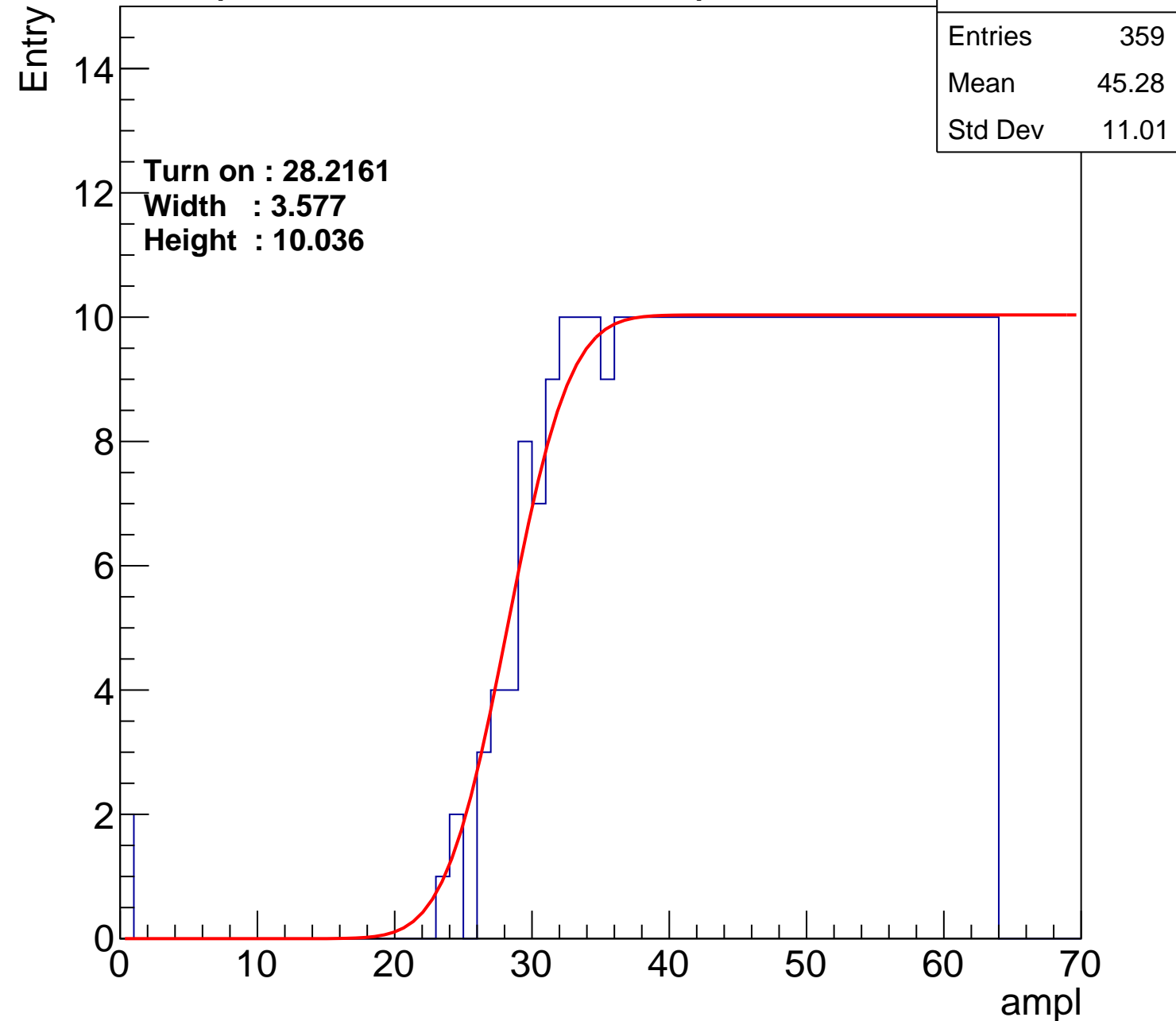
Width : 3.577

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch9

calib_packv5_042523_0143.root, FC#5, port B1

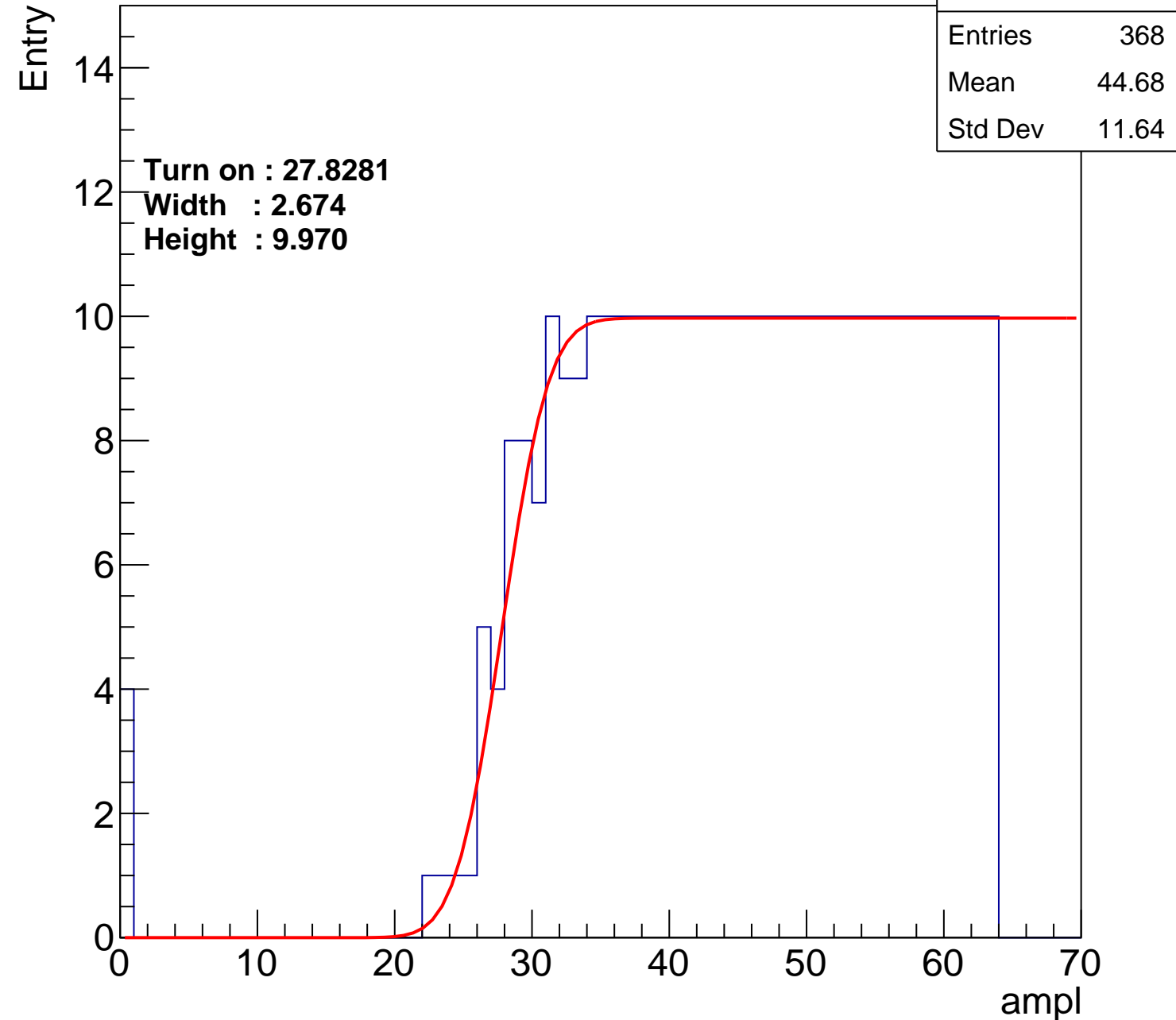
Entry

14
12
10
8
6
4
2
0

Turn on : 27.8281
Width : 2.674
Height : 9.970

Entries	368
Mean	44.68
Std Dev	11.64

ampl



B0L000S, U24-ch10

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.58
Std Dev	10.85

Turn on : 29.5721

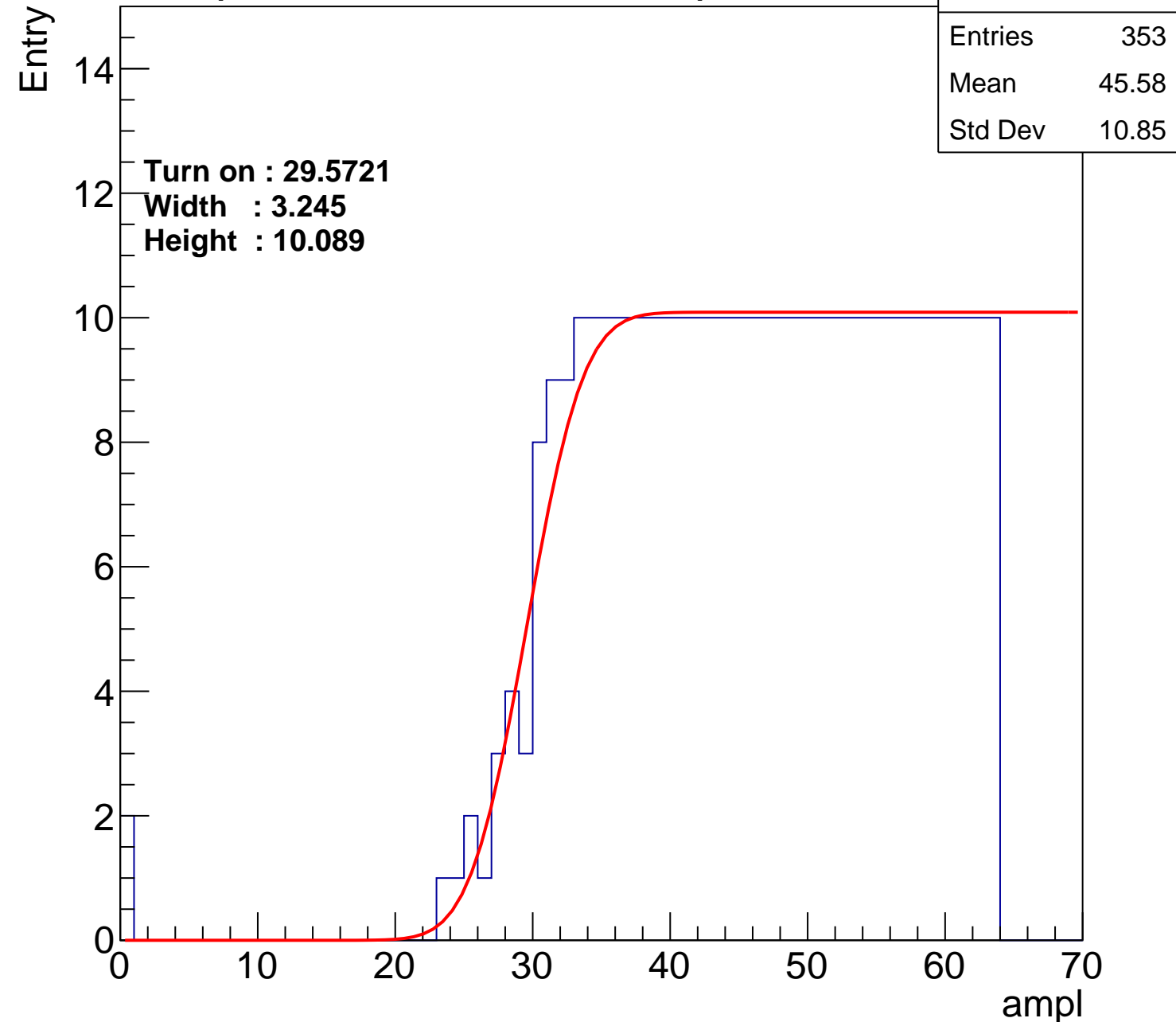
Width : 3.245

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 27.0018

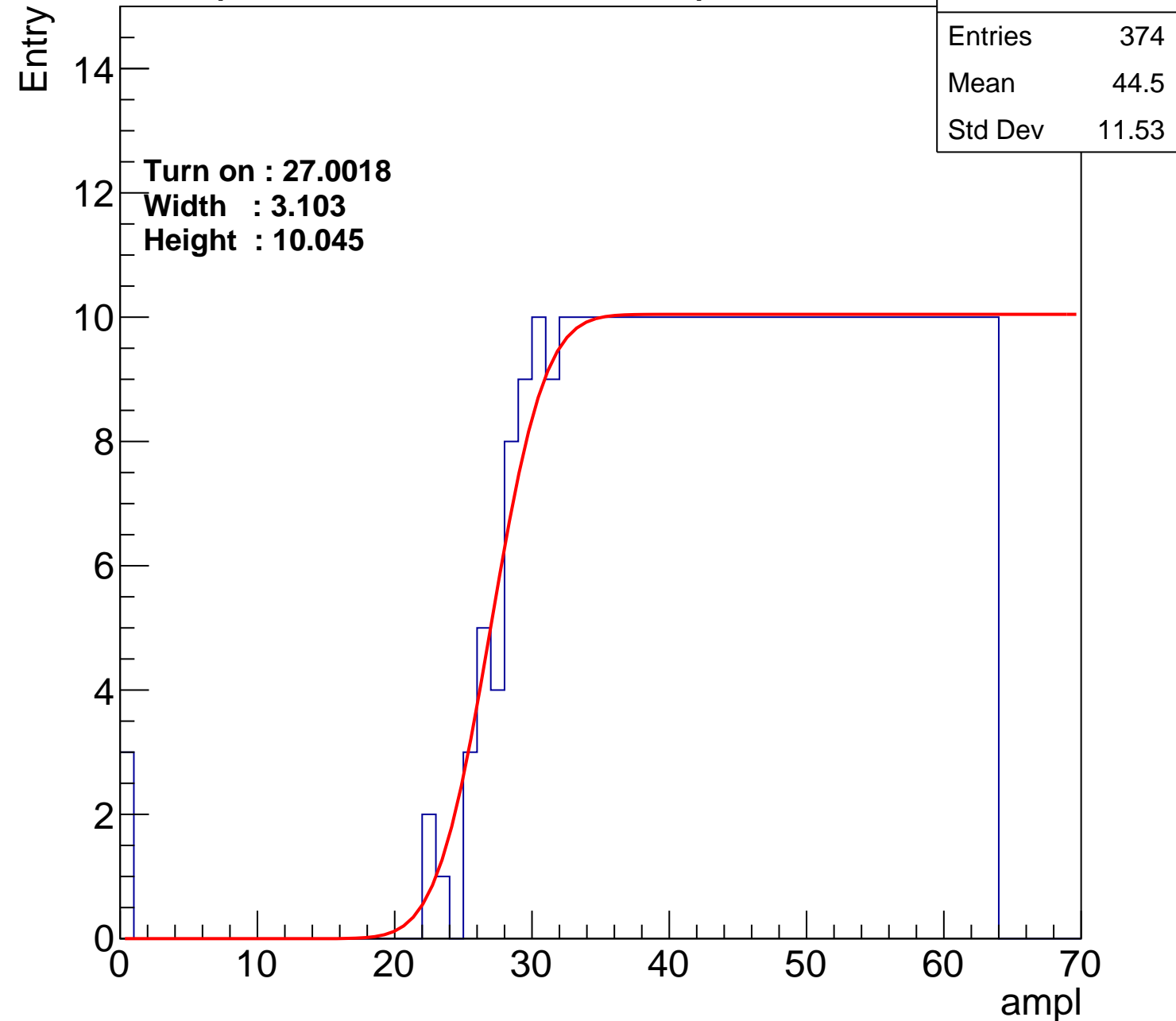
Width : 3.103

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch12

calib_packv5_042523_0143.root, FC#5, port B1

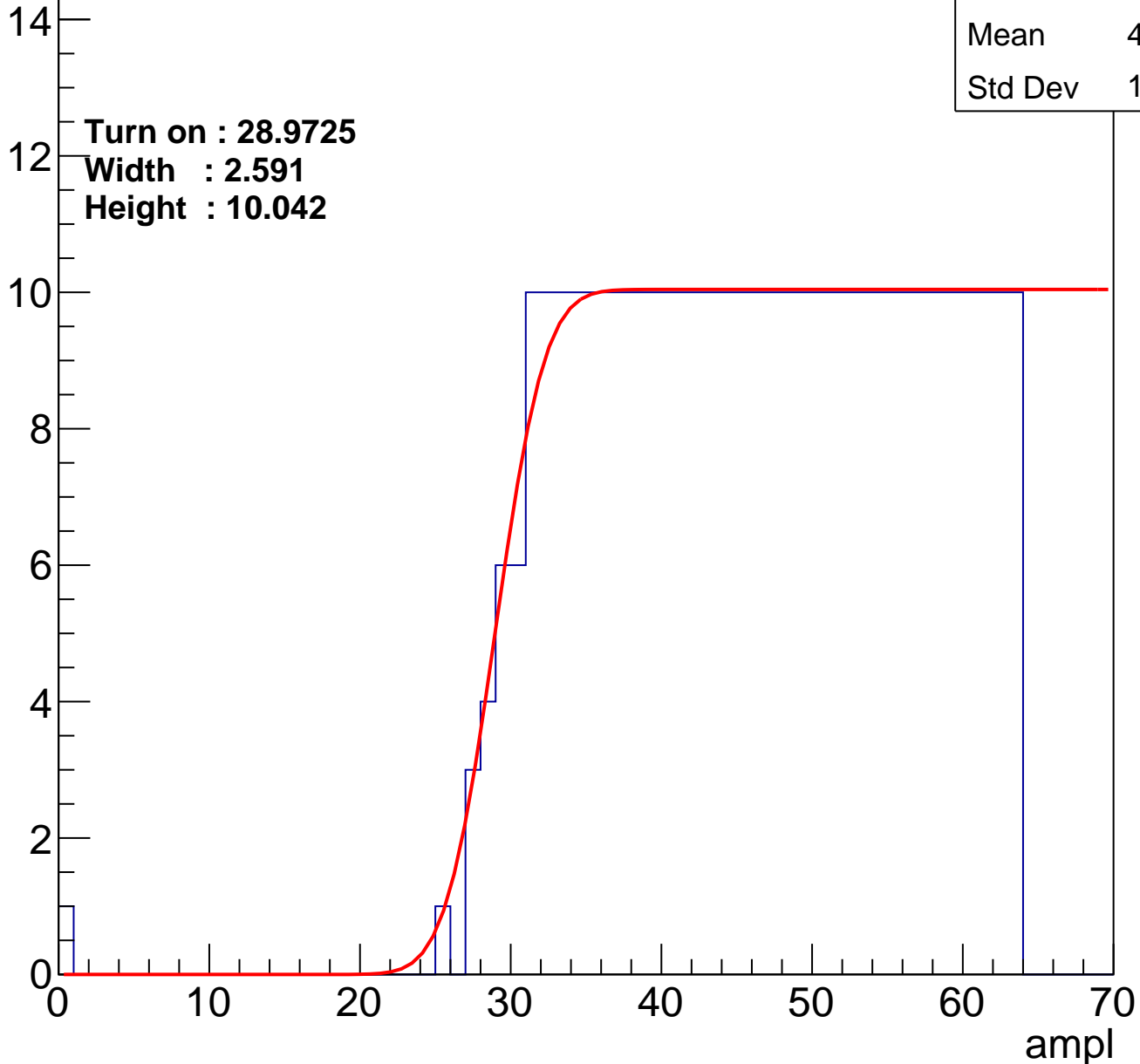
Entries	351
Mean	45.82
Std Dev	10.47

Turn on : 28.9725

Width : 2.591

Height : 10.042

Entry



B0L000S, U24-ch13

calib_packv5_042523_0143.root, FC#5, port B1

Entries	348
Mean	45.88
Std Dev	10.54

Turn on : 29.7429

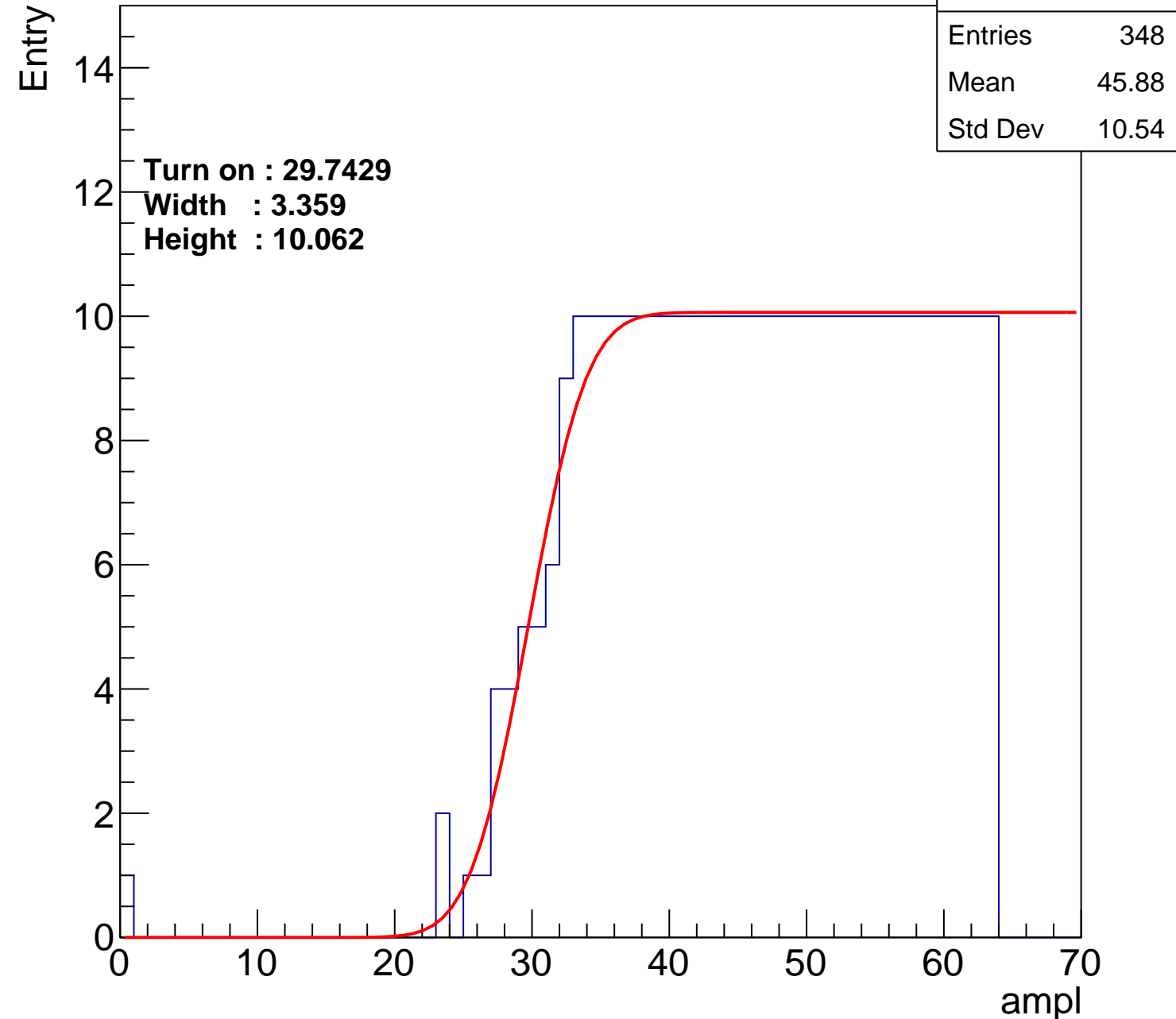
Width : 3.359

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch14

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 28.2148

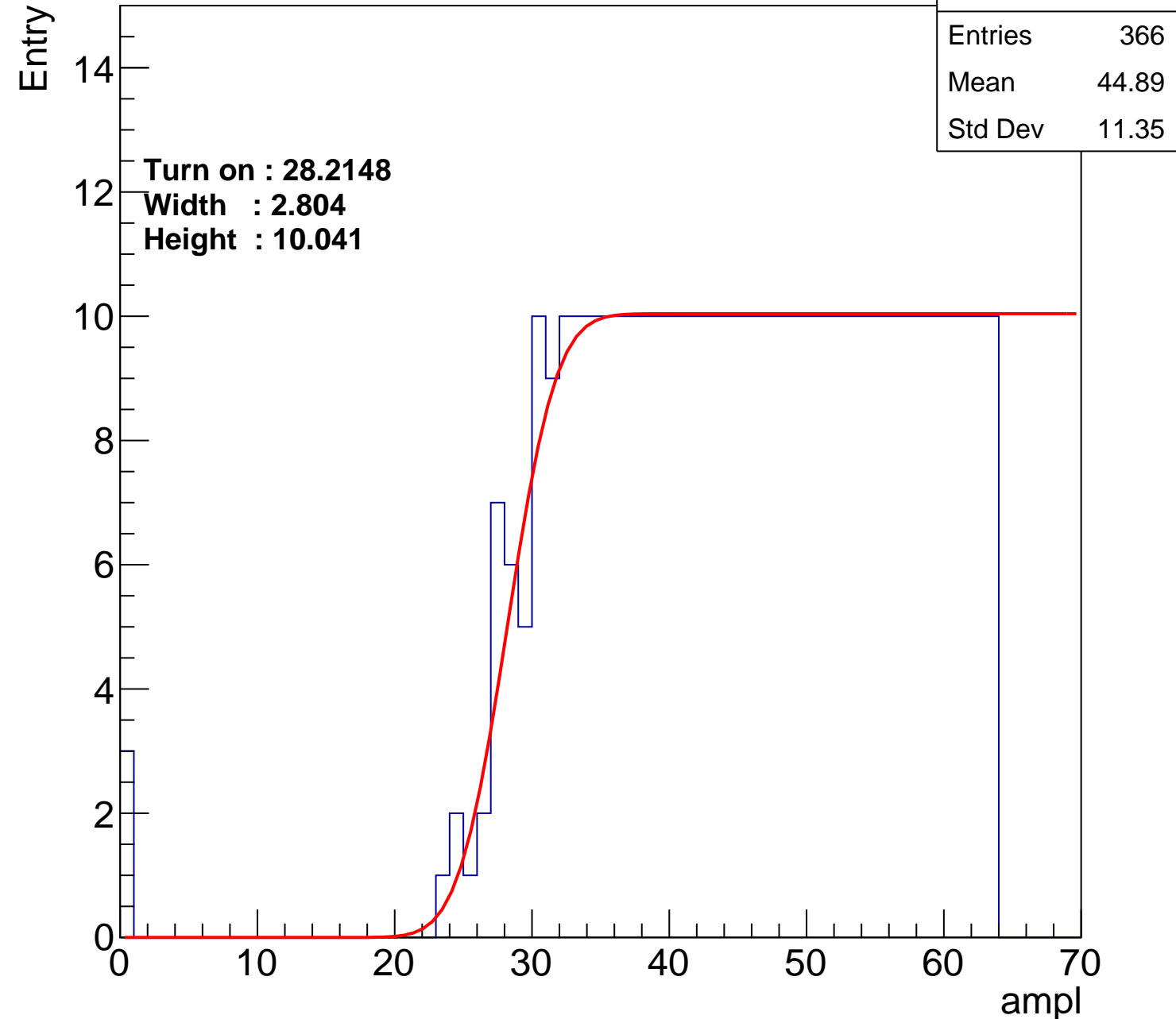
Width : 2.804

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch15

calib_packv5_042523_0143.root, FC#5, port B1

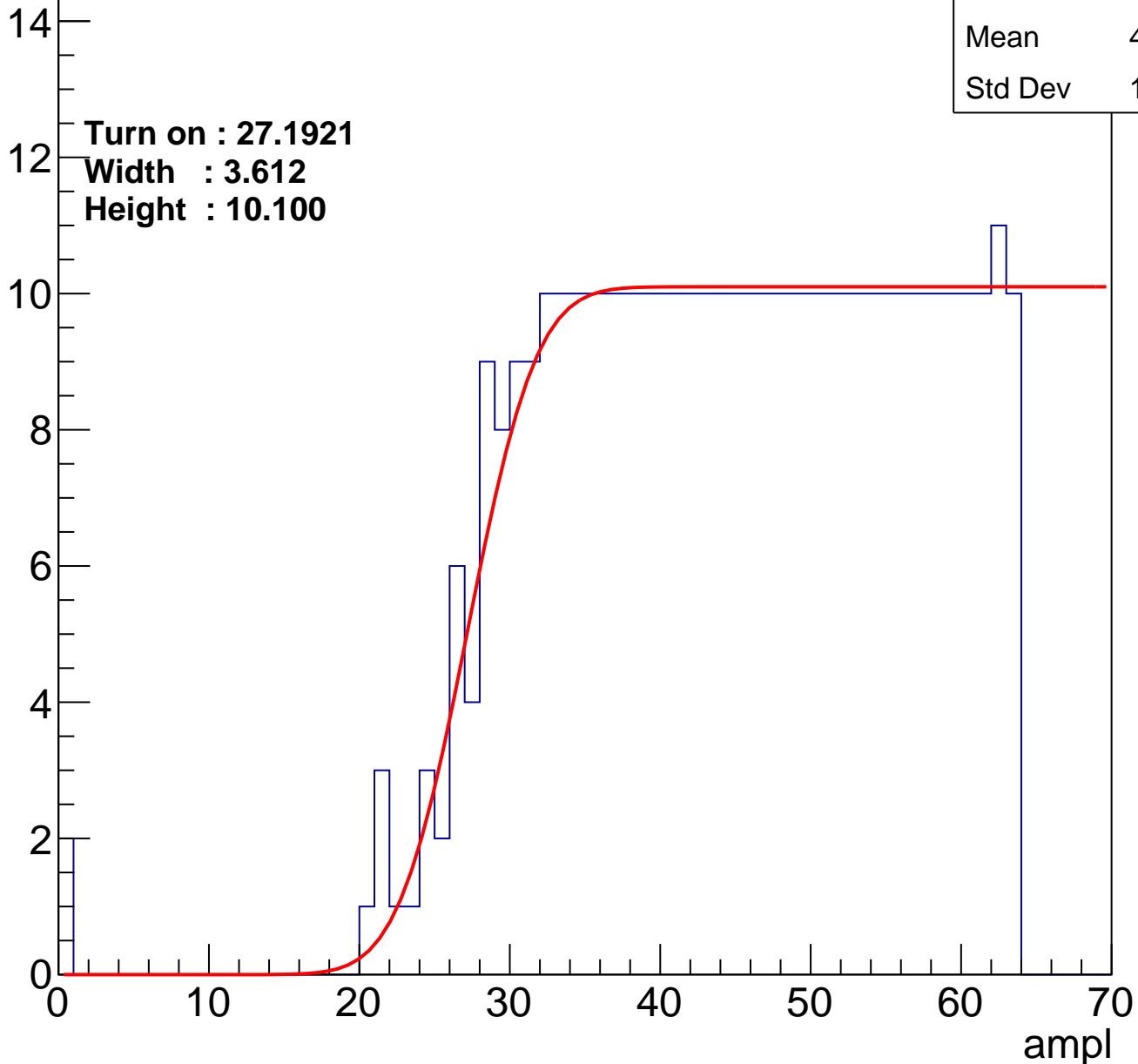
Entries	379
Mean	44.35
Std Dev	11.58

Turn on : 27.1921

Width : 3.612

Height : 10.100

Entry



B0L000S, U24-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.77
Std Dev	11.05

Turn on : 27.1552

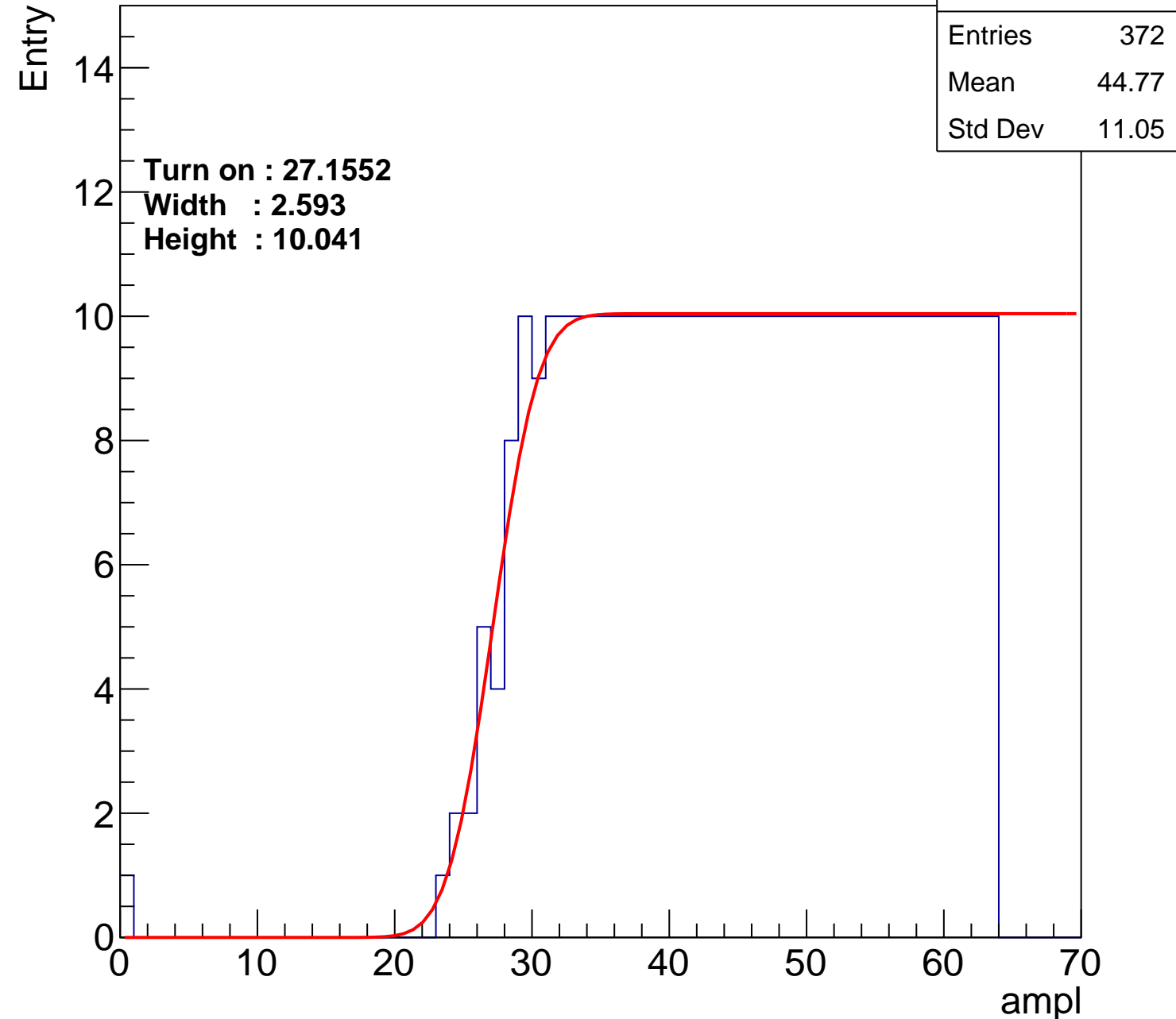
Width : 2.593

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch17

calib_packv5_042523_0143.root, FC#5, port B1

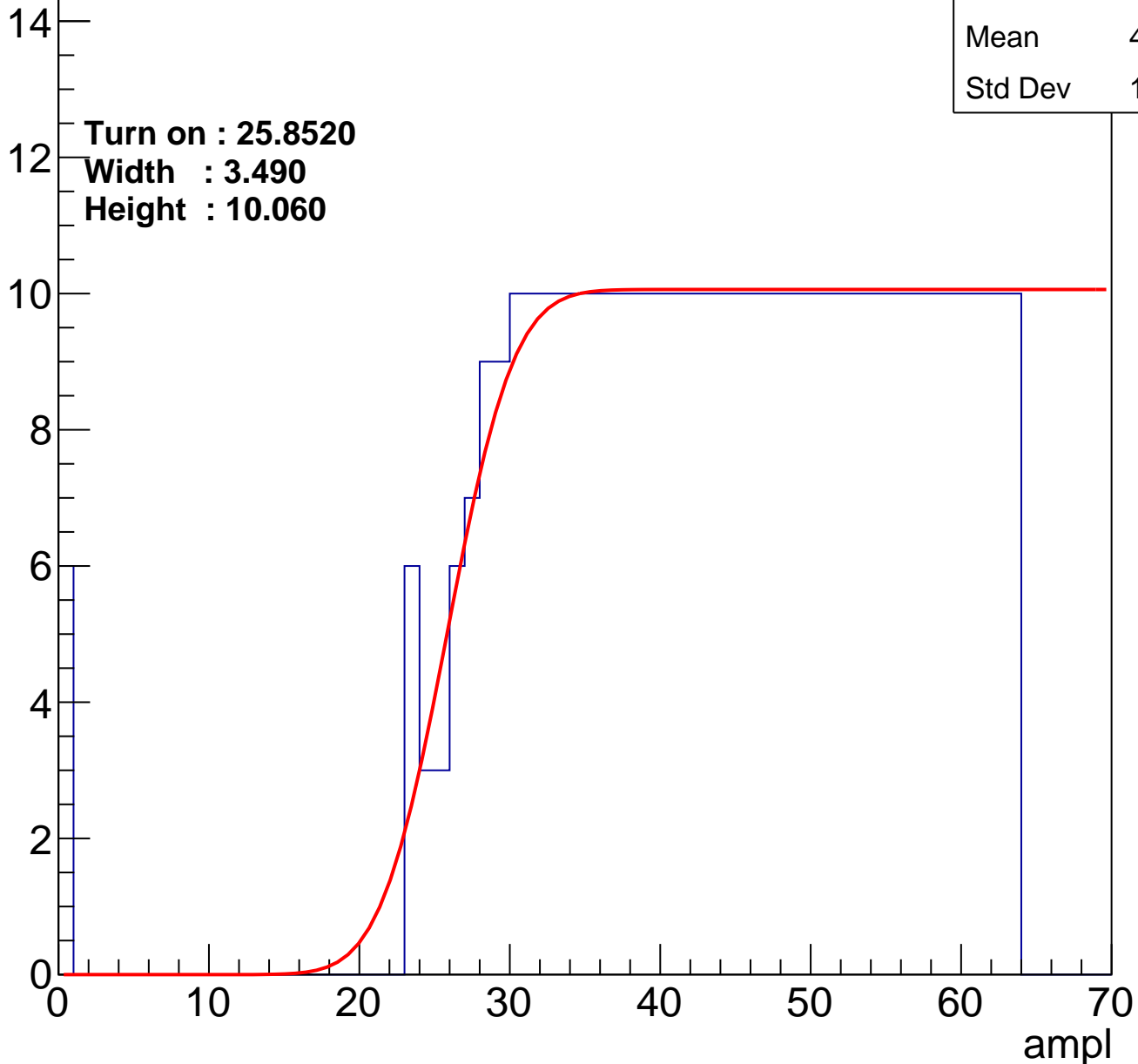
Entries	389
Mean	43.58
Std Dev	12.38

Turn on : 25.8520

Width : 3.490

Height : 10.060

Entry



B0L000S, U24-ch18

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.76
Std Dev	11.25

Turn on : 27.4000

Width : 2.853

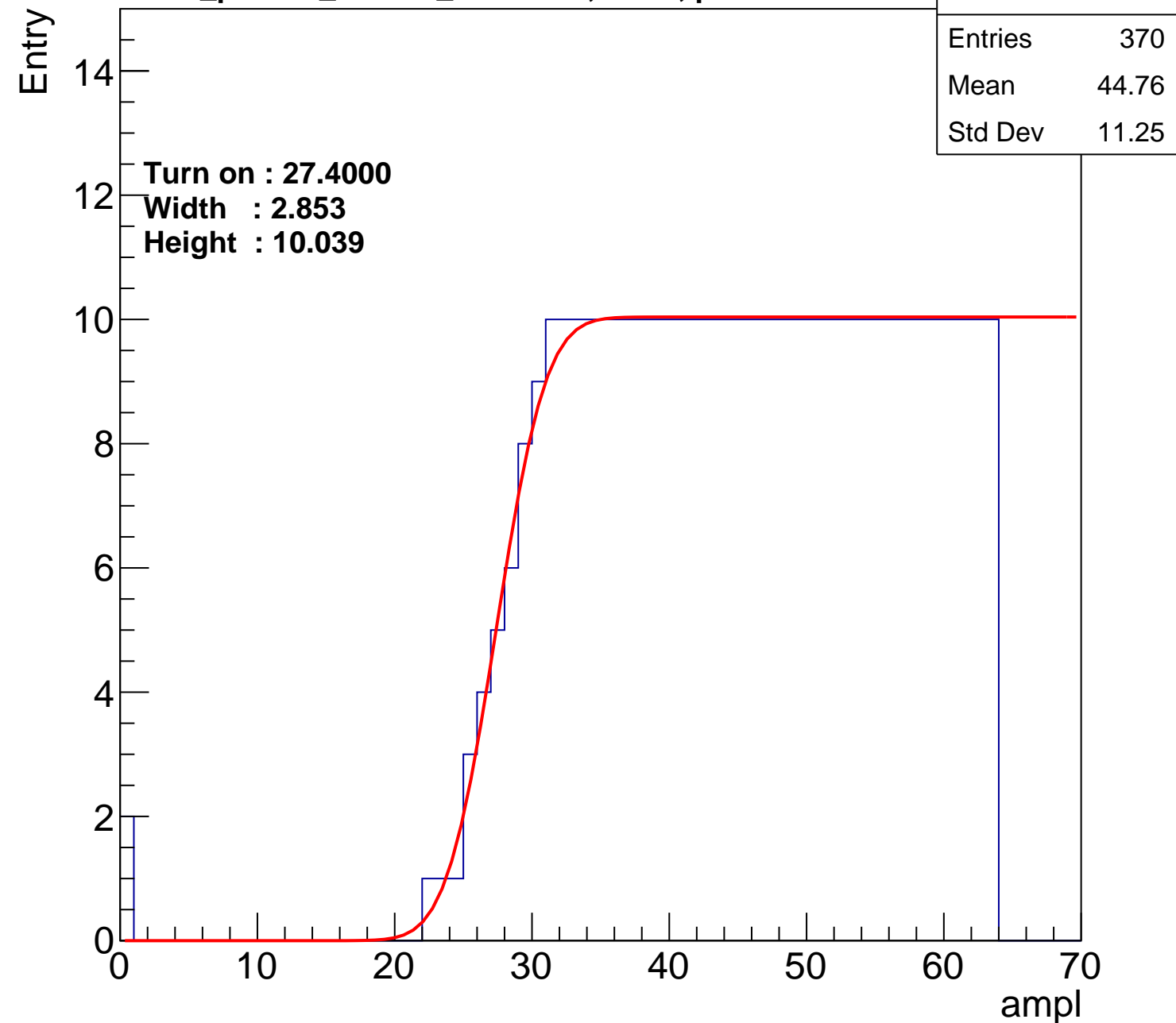
Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U24-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.83
Std Dev	11.58

Turn on : 27.8475

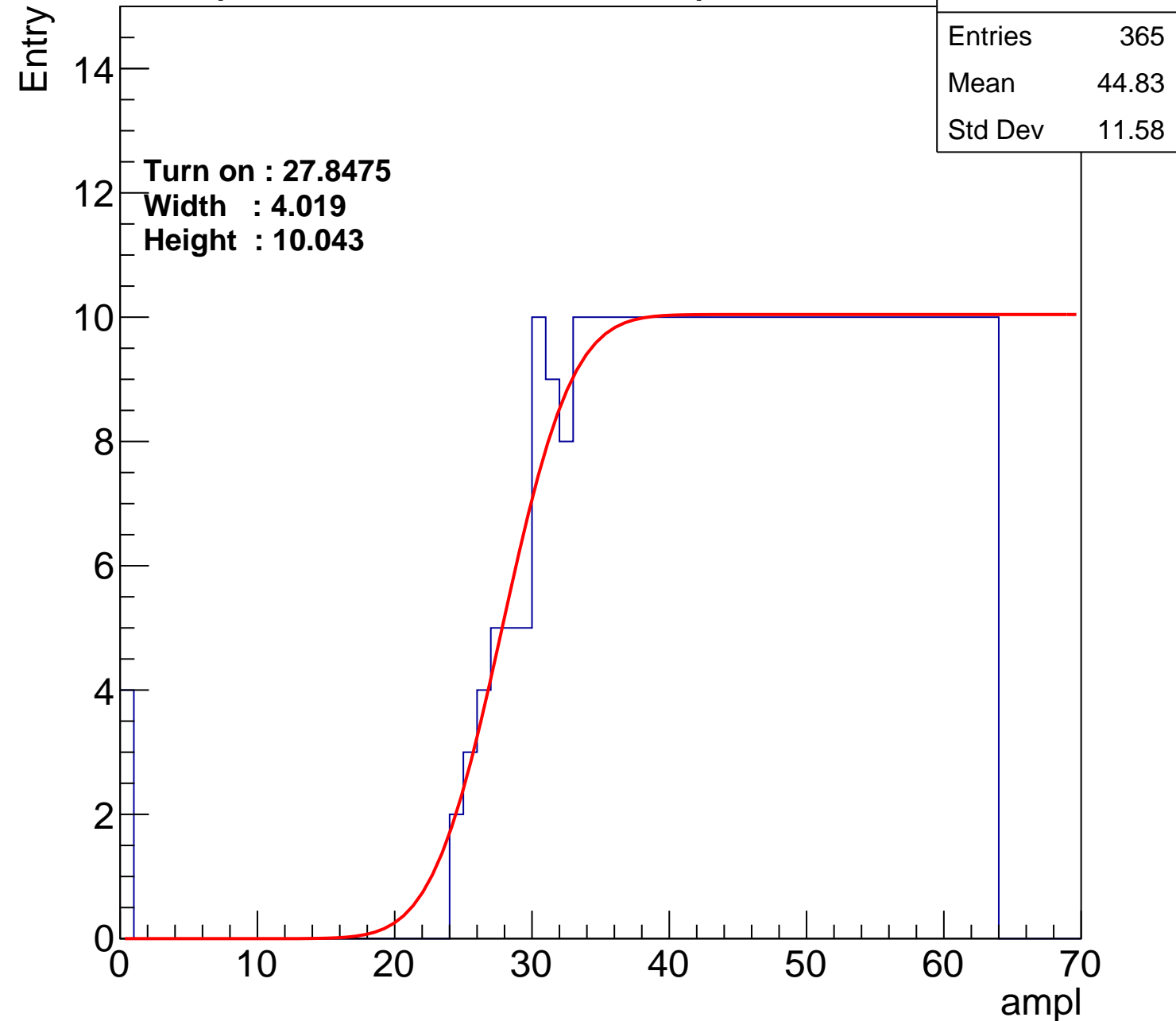
Width : 4.019

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

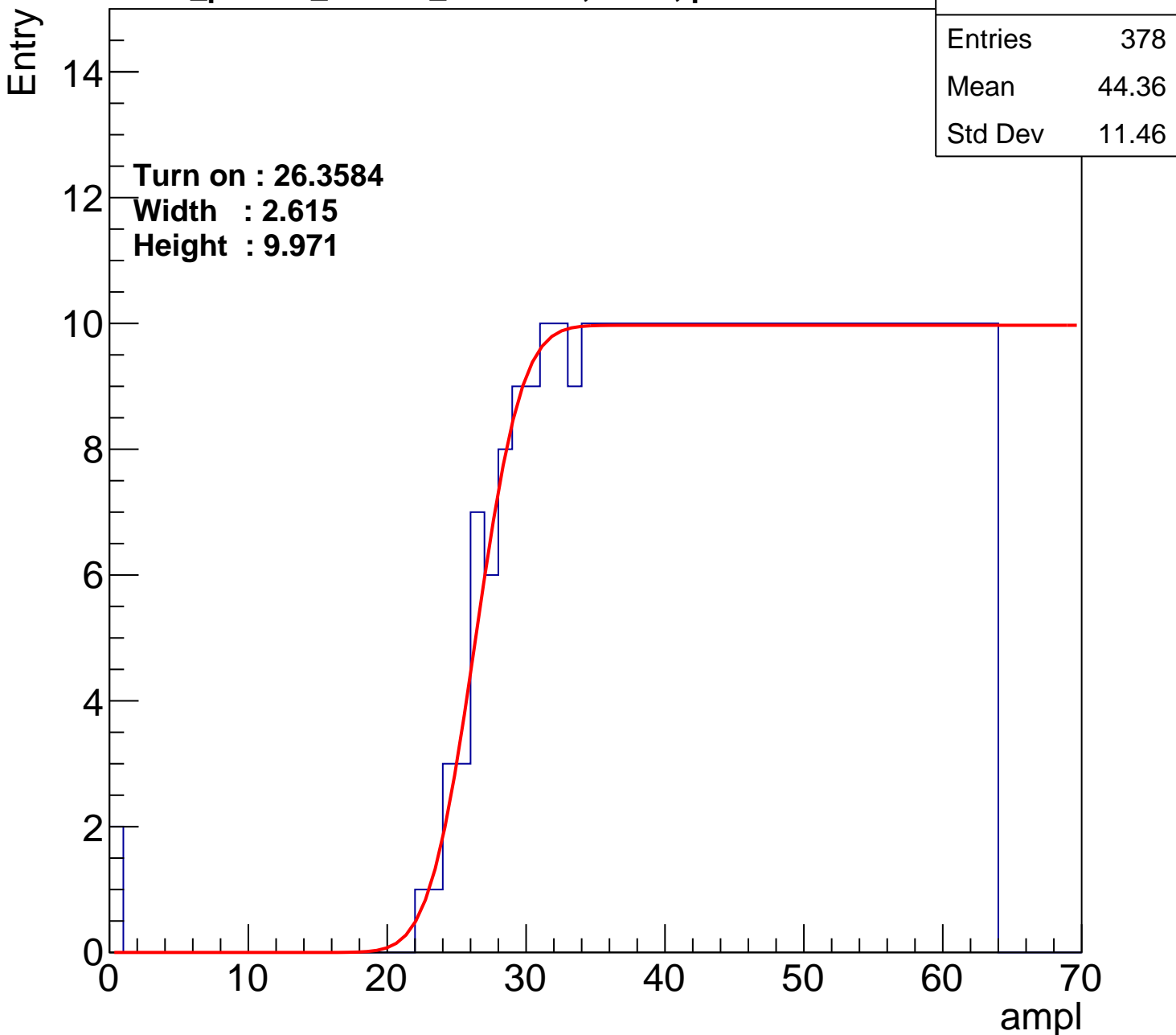
Entries	378
Mean	44.36
Std Dev	11.46

Mean	44.36
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Std Dev	11.46
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Width : 2.615

Height : 9.971



B0L000S, U24-ch21

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.37
Std Dev	10.77

Turn on : 28.5105

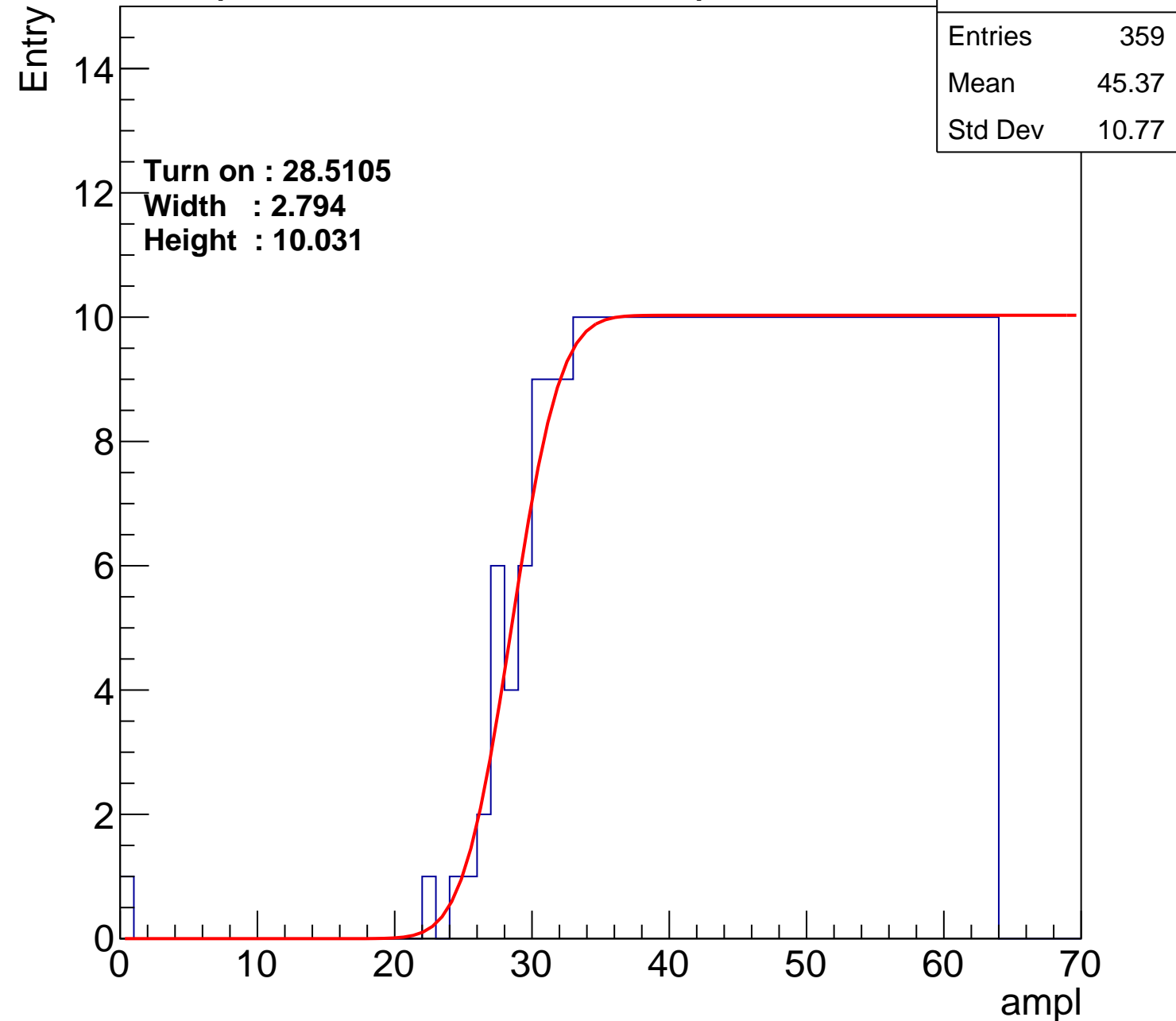
Width : 2.794

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch22

calib_packv5_042523_0143.root, FC#5, port B1

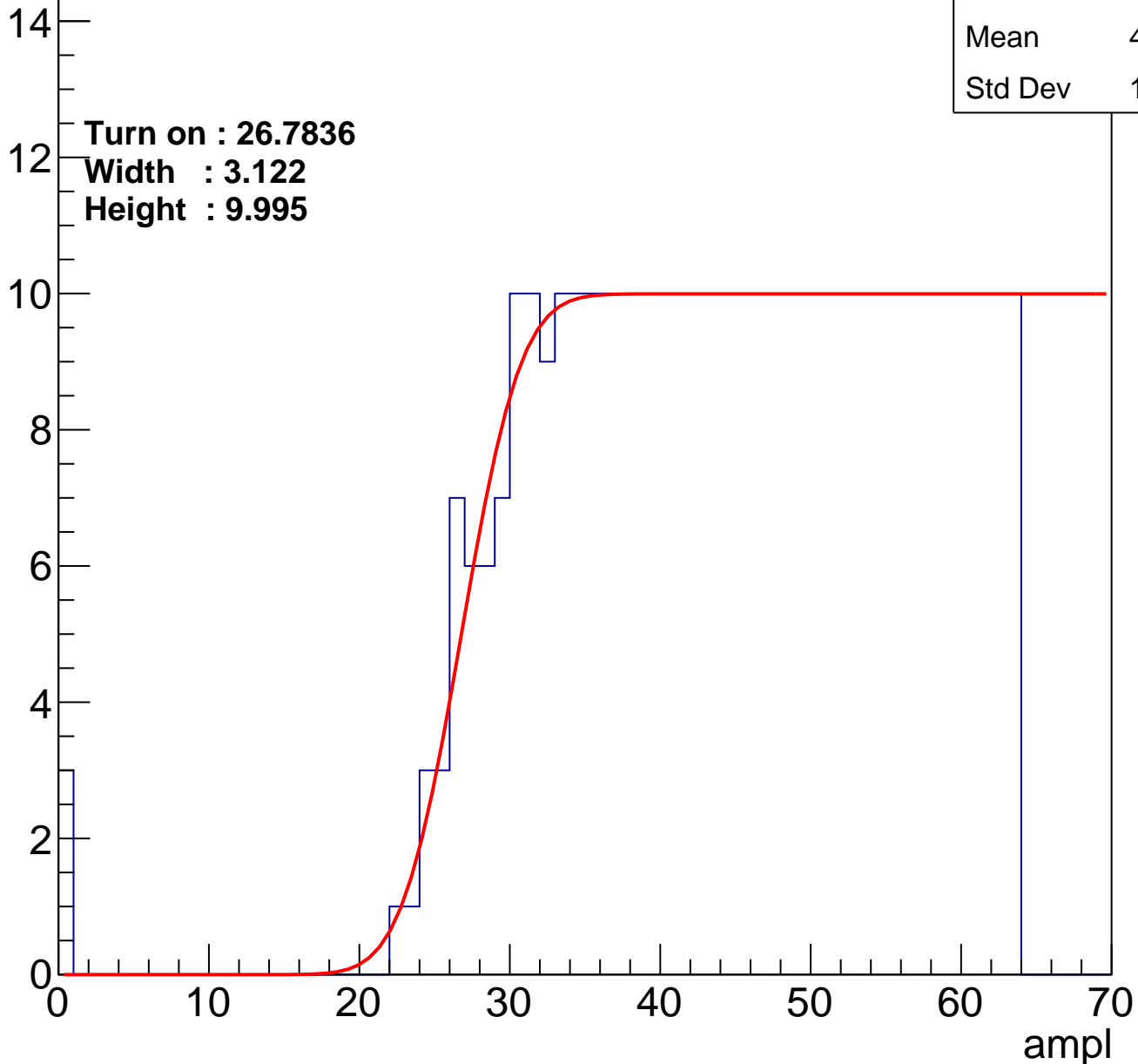
Entries	376
Mean	44.38
Std Dev	11.62

Turn on : 26.7836

Width : 3.122

Height : 9.995

Entry



B0L000S, U24-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.45
Std Dev	11.29

Turn on : 26.5631

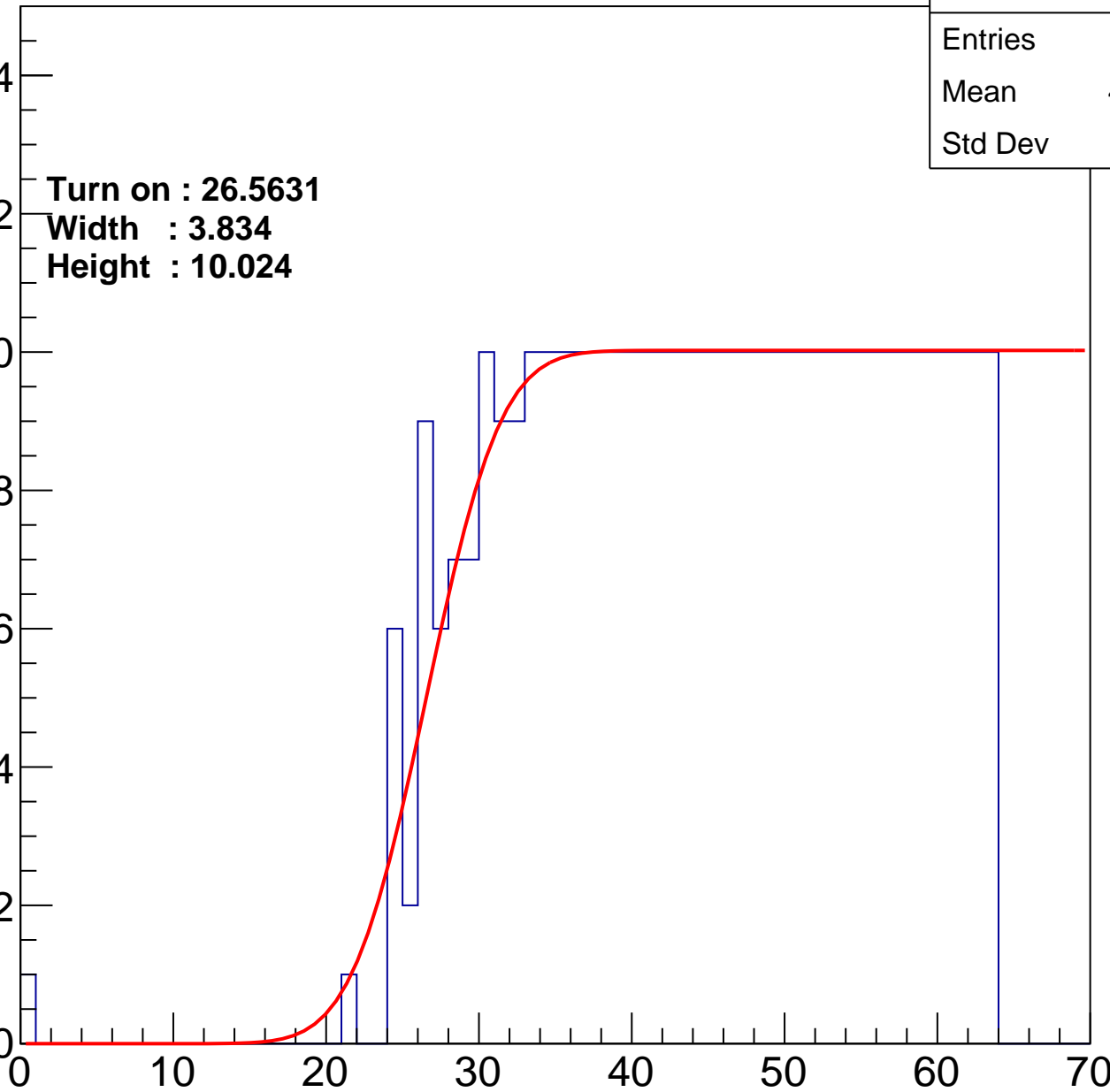
Width : 3.834

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch24

calib_packv5_042523_0143.root, FC#5, port B1

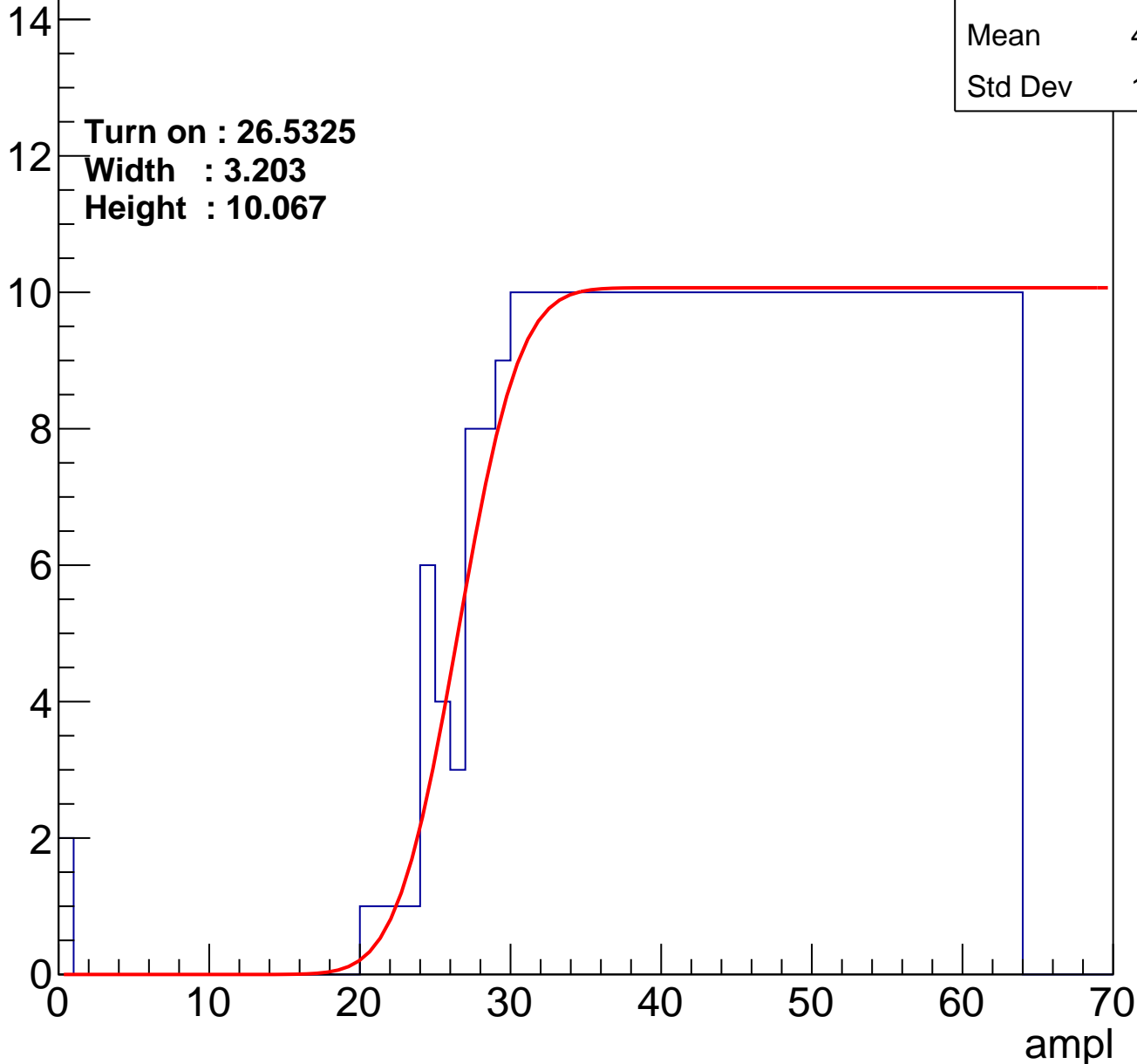
Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 26.5325

Width : 3.203

Height : 10.067

Entry



B0L000S, U24-ch25

calib_packv5_042523_0143.root, FC#5, port B1

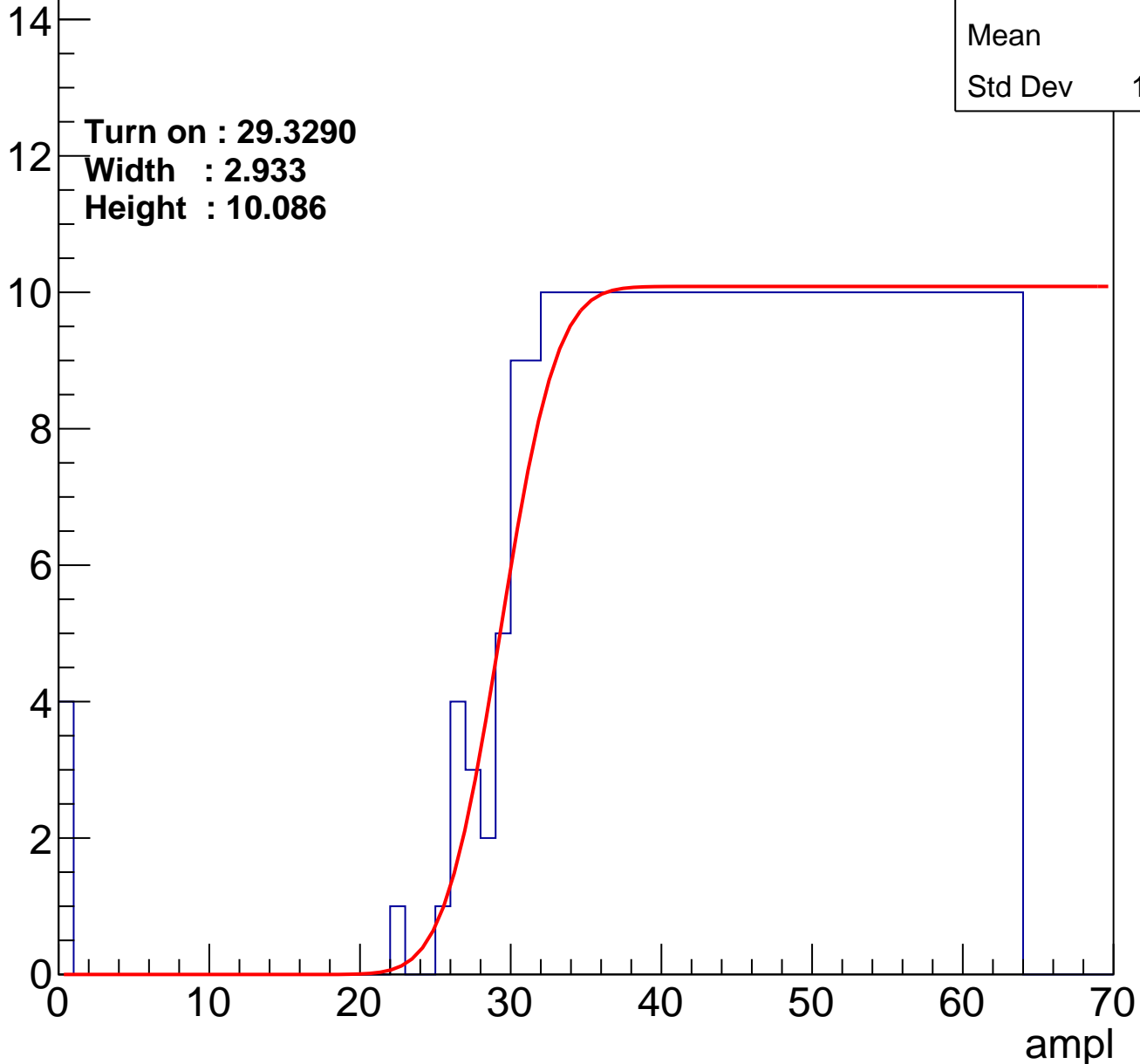
Entries	358
Mean	45.2
Std Dev	11.38

Turn on : 29.3290

Width : 2.933

Height : 10.086

Entry



B0L000S, U24-ch26

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.67
Std Dev	11.61

Turn on : 27.4719

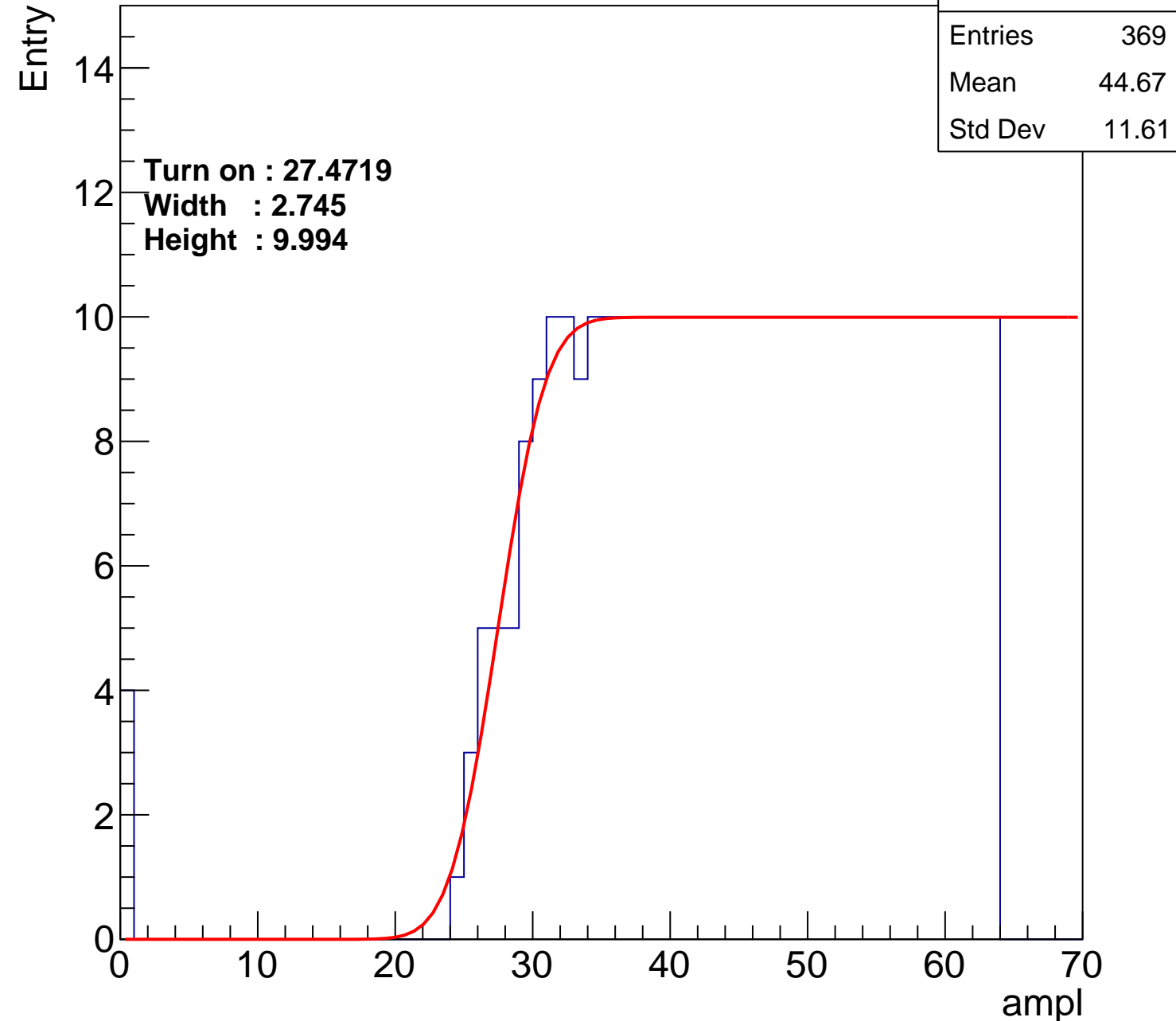
Width : 2.745

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.29
Std Dev	11.66

Turn on : 26.8739

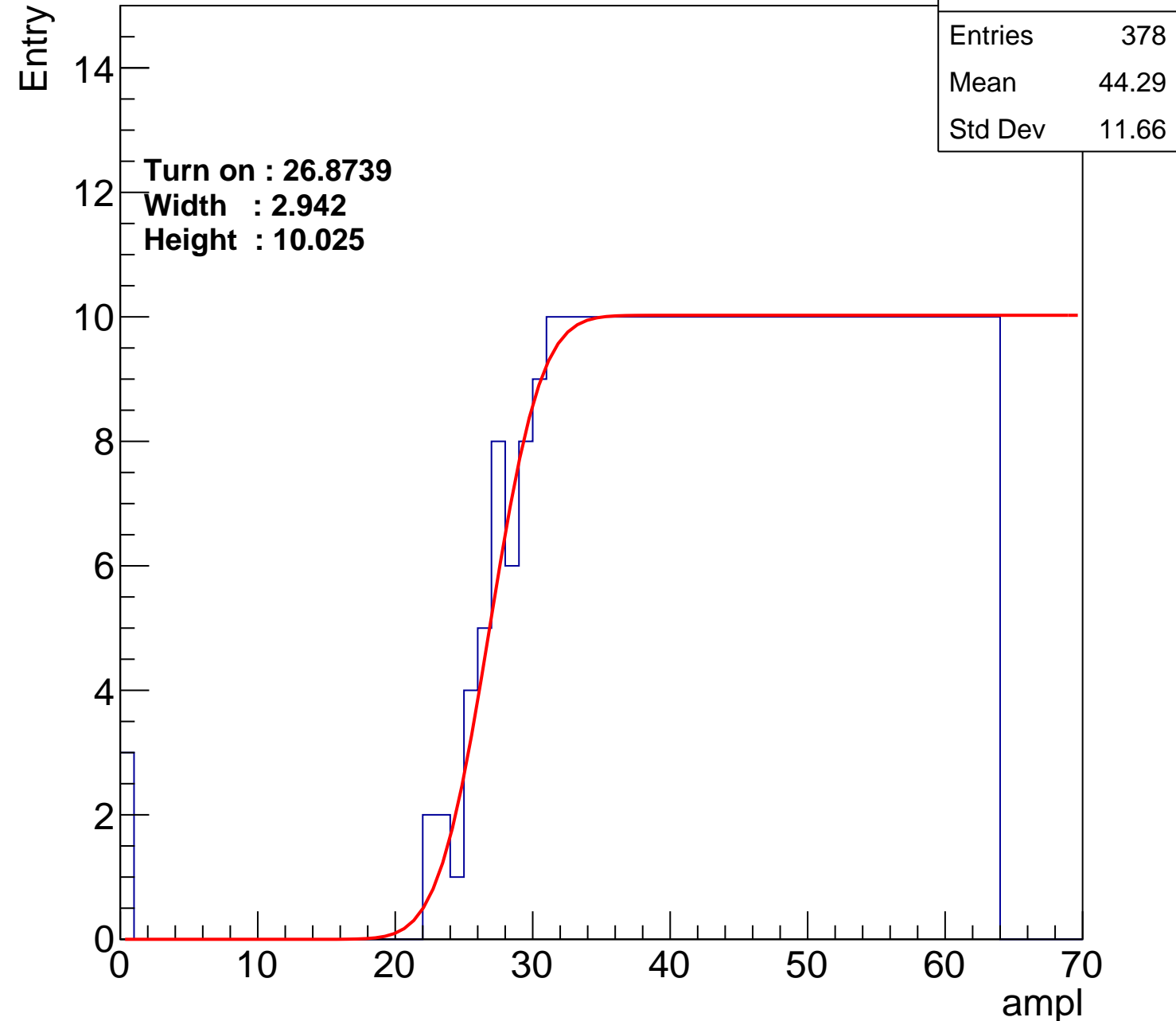
Width : 2.942

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.28
Std Dev	11.1

Turn on : 28.5231

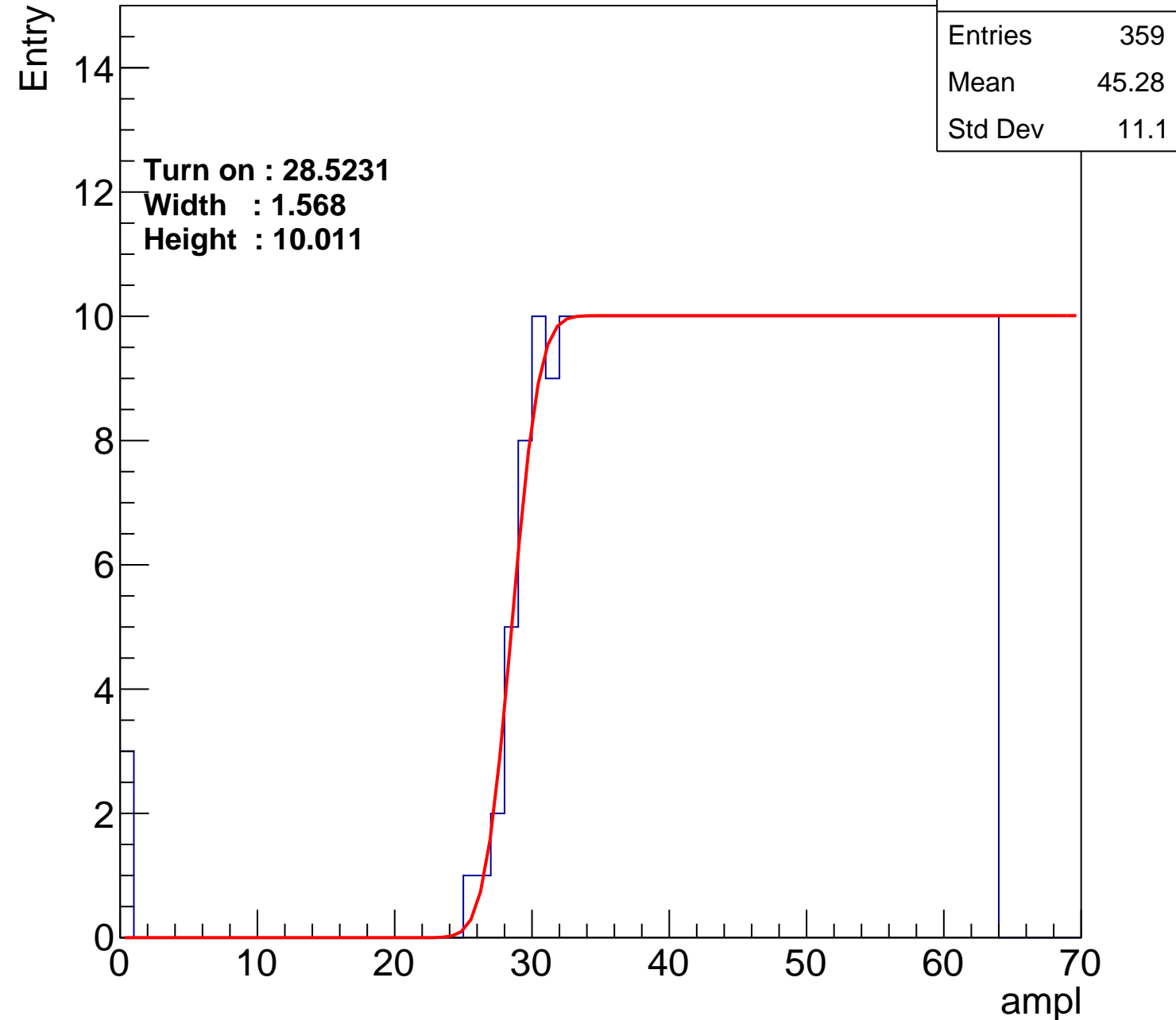
Width : 1.568

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch29

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.83
Std Dev	11.23

Turn on : 28.0176

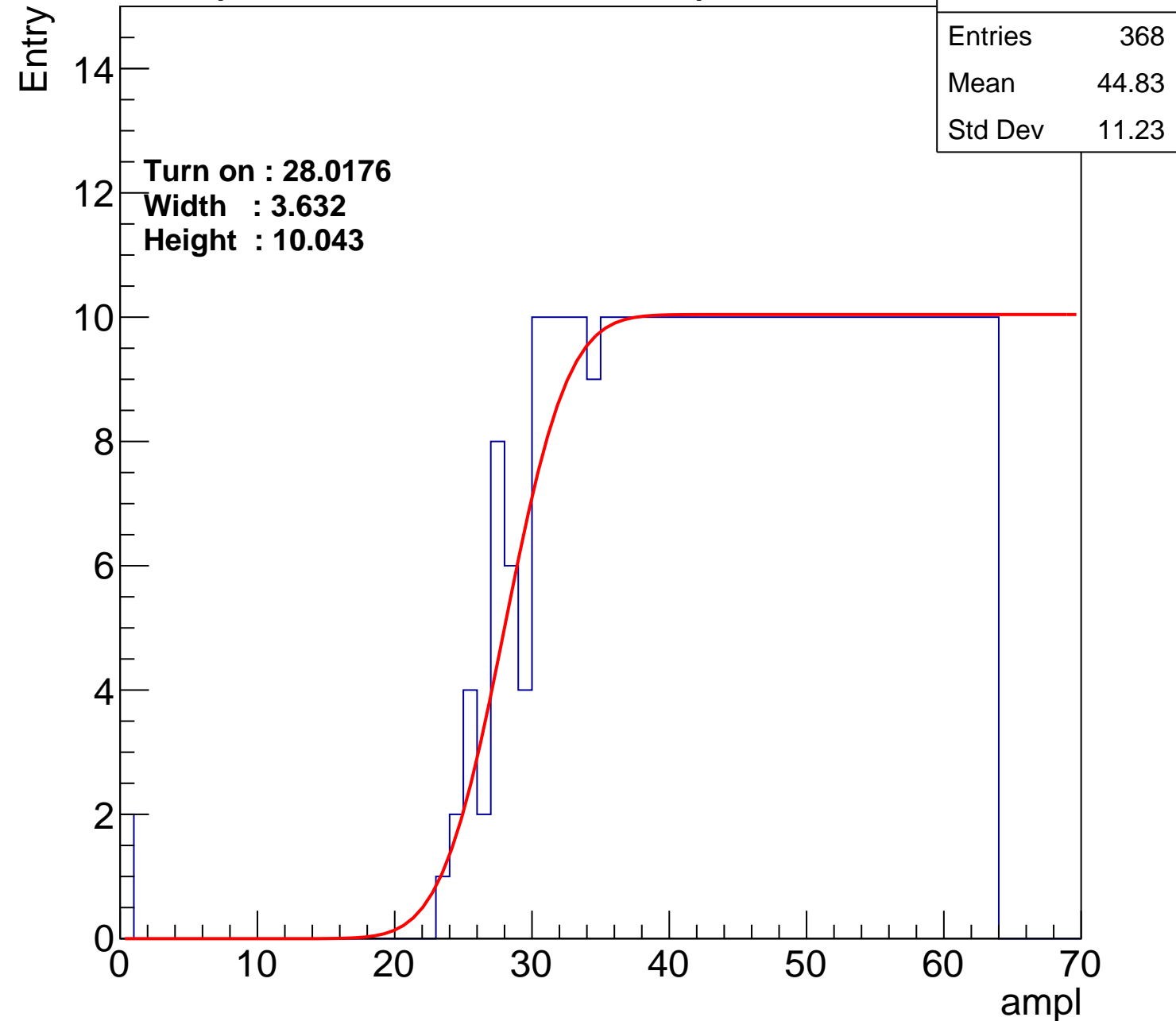
Width : 3.632

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.69
Std Dev	11.36

Turn on : 26.8667

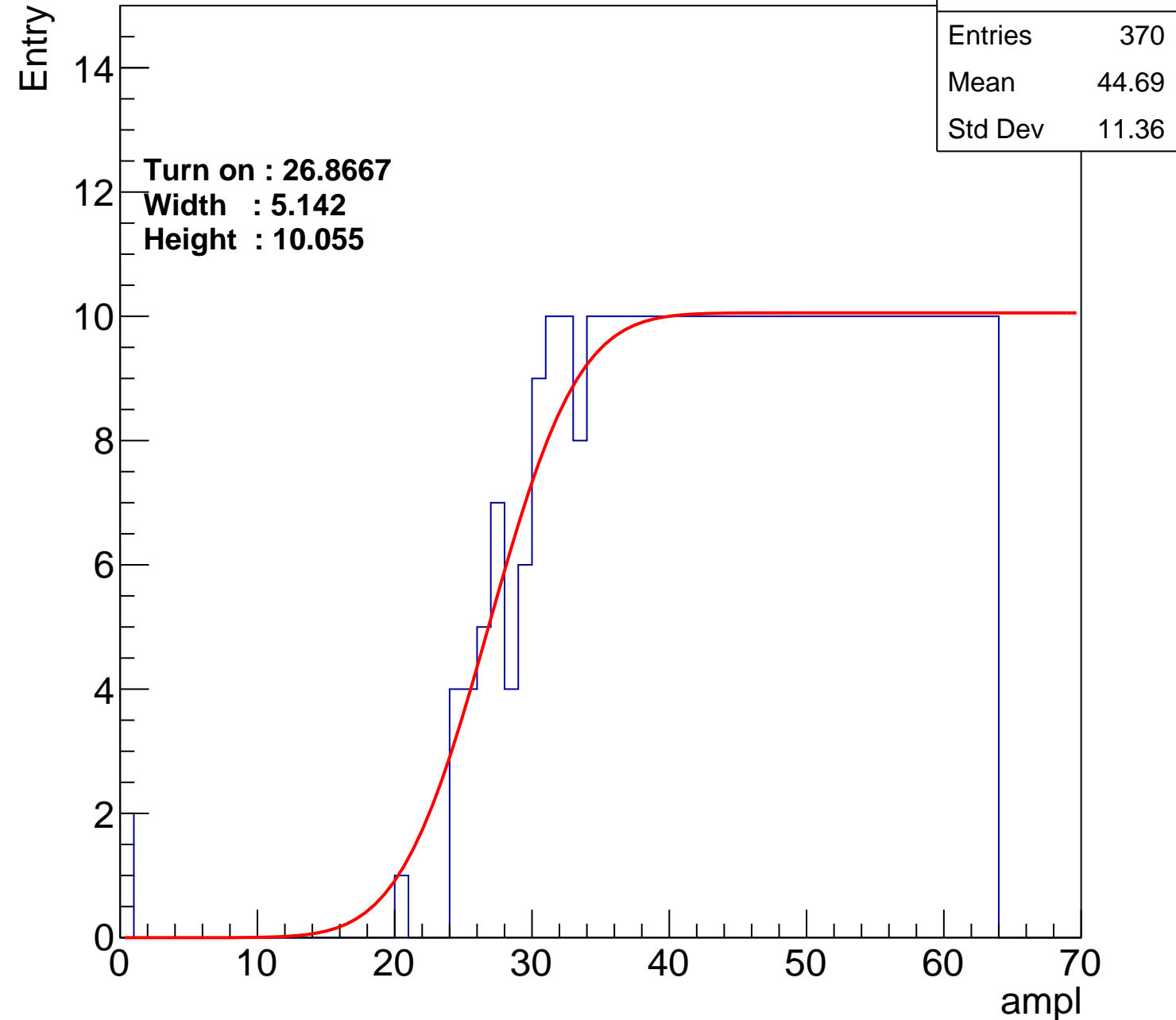
Width : 5.142

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch31

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
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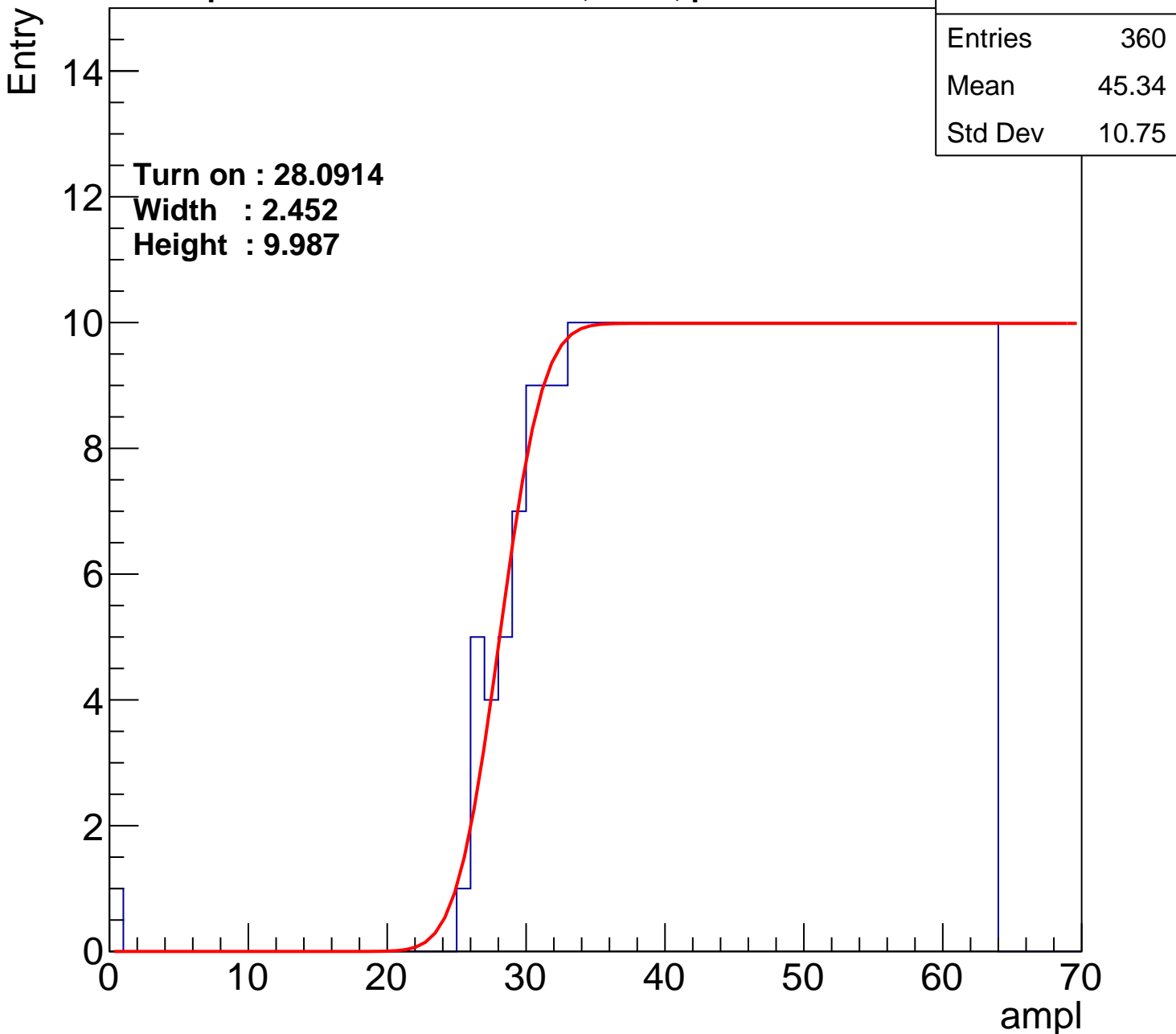
Mean	45.34
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Std Dev	10.75
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Turn on : 28.0914

Width : 2.452

Height : 9.987



B0L000S, U24-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.1027

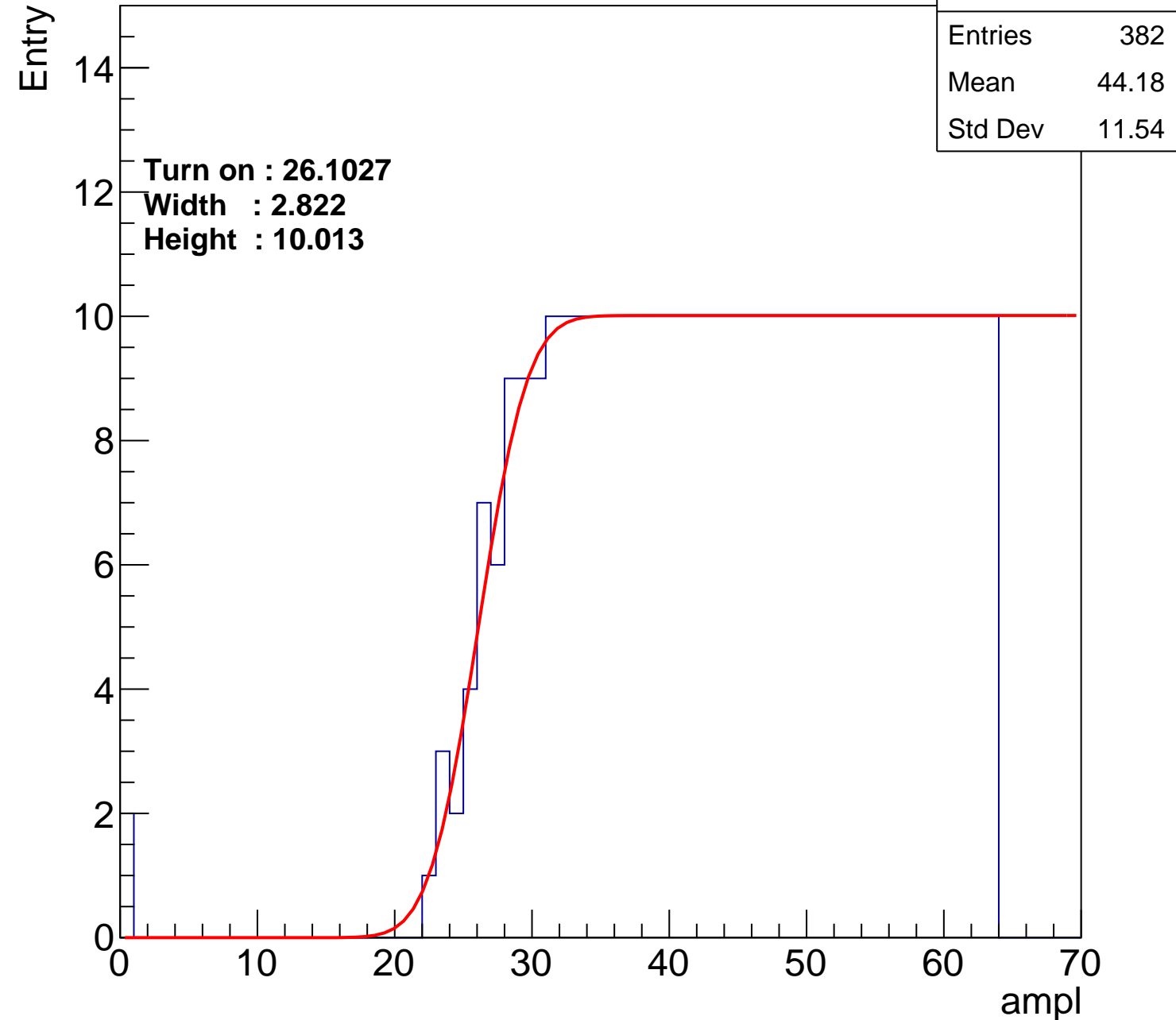
Width : 2.822

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch33

calib_packv5_042523_0143.root, FC#5, port B1

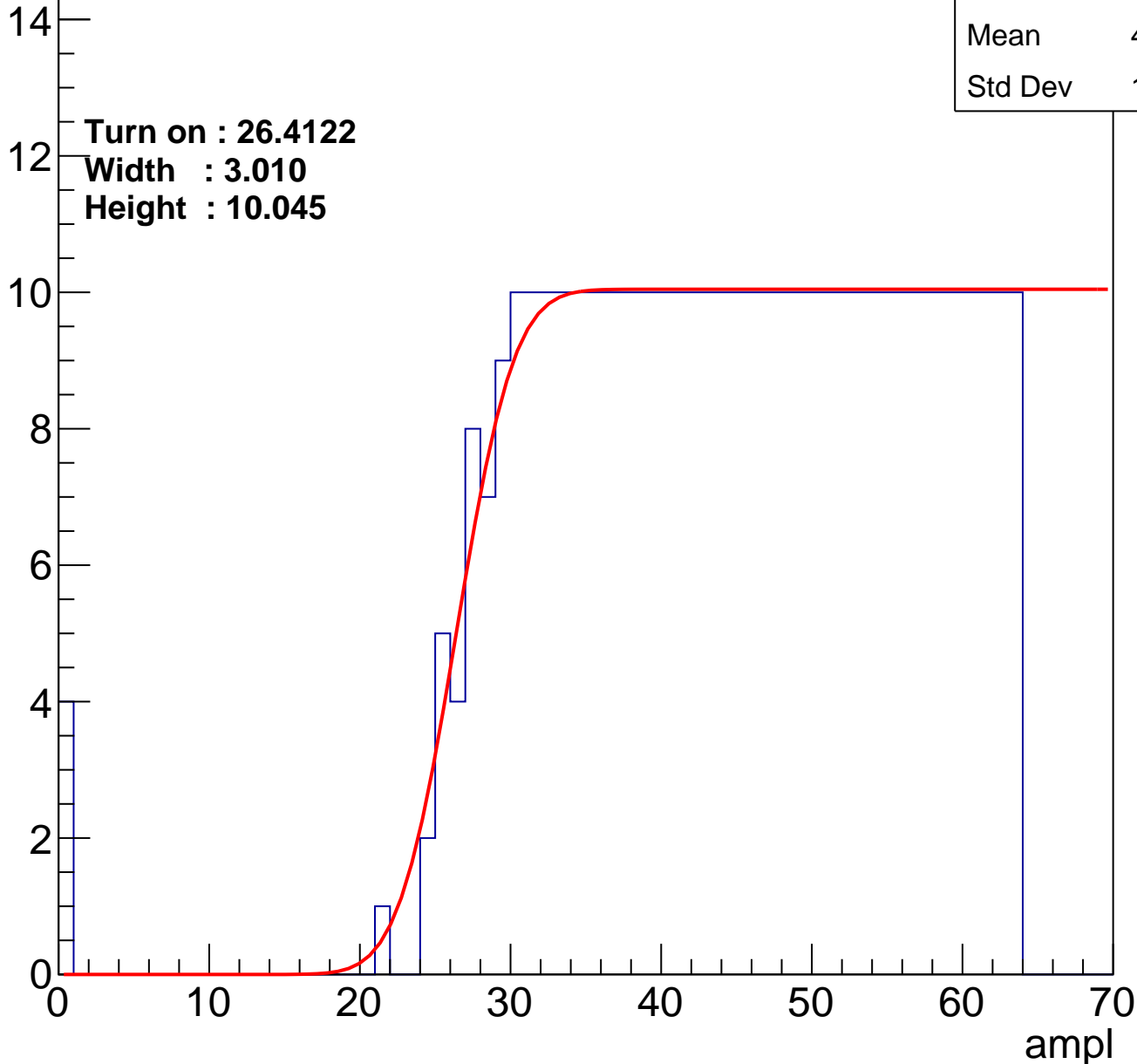
Entries	380
Mean	44.16
Std Dev	11.83

Turn on : 26.4122

Width : 3.010

Height : 10.045

Entry



B0L000S, U24-ch34

calib_packv5_042523_0143.root, FC#5, port B1

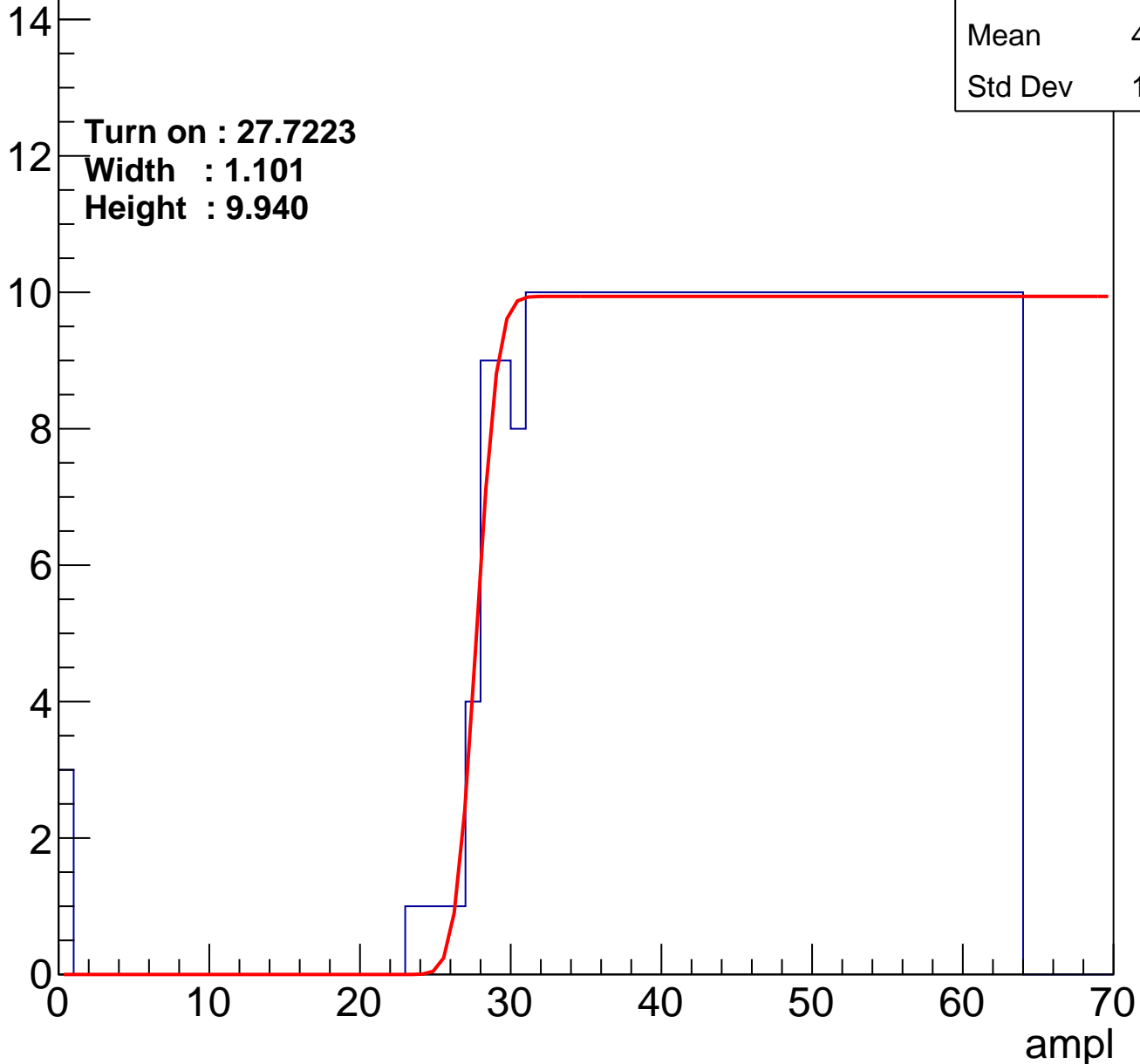
Entries	367
Mean	44.87
Std Dev	11.32

Turn on : 27.7223

Width : 1.101

Height : 9.940

Entry



B0L000S, U24-ch35

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 27.2395

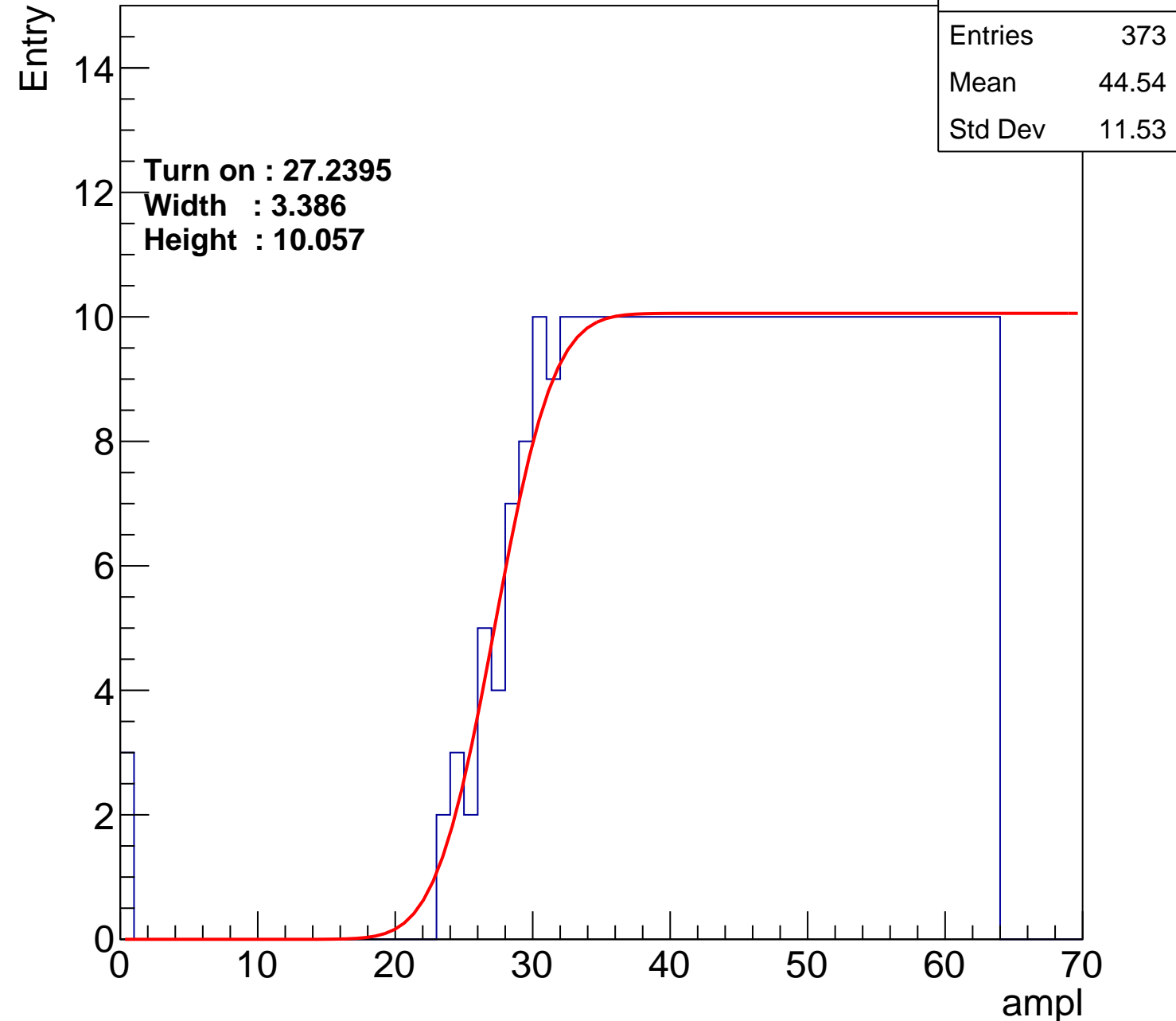
Width : 3.386

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch36

calib_packv5_042523_0143.root, FC#5, port B1

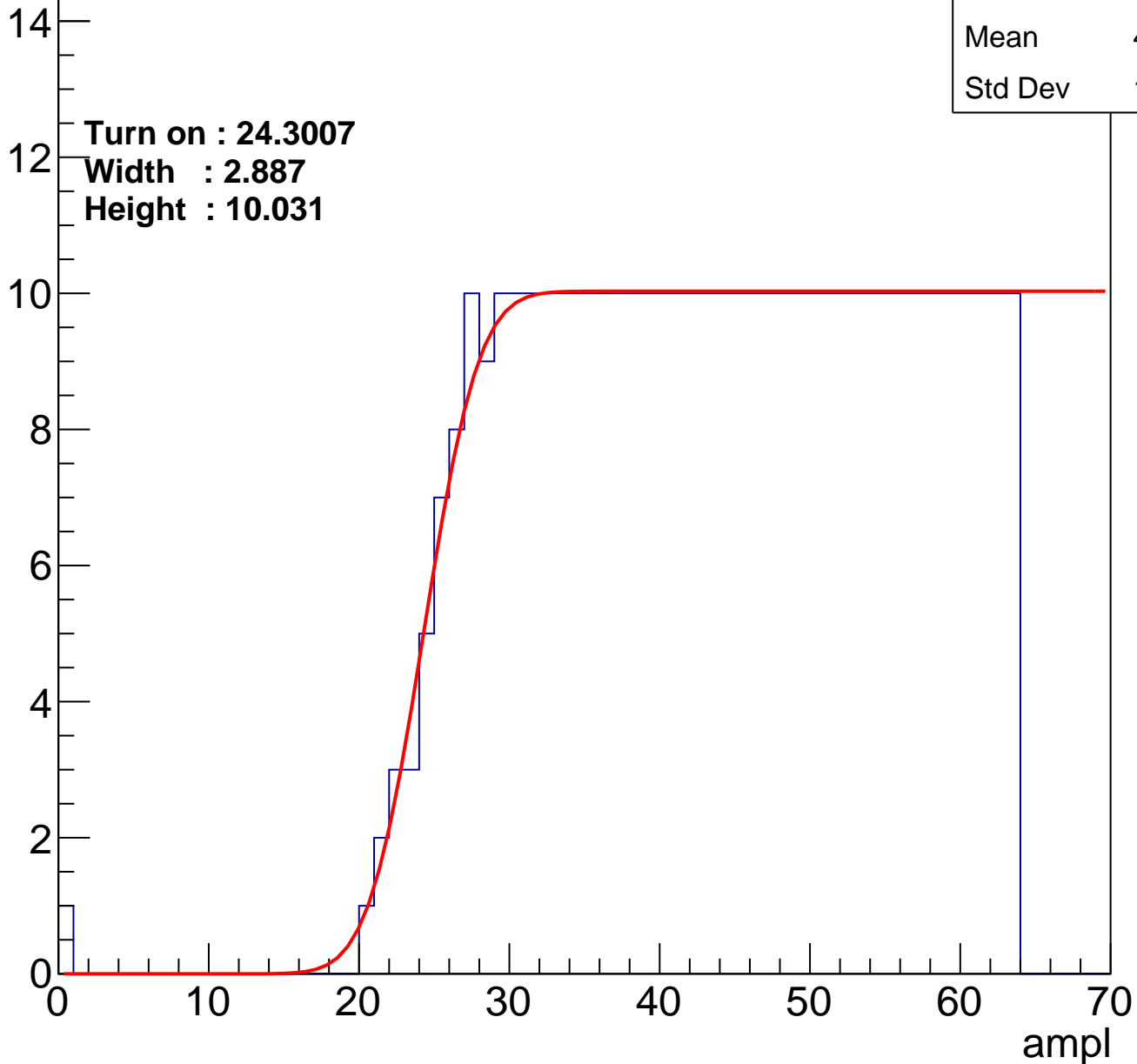
Entries	399
Mean	43.41
Std Dev	11.81

Turn on : 24.3007

Width : 2.887

Height : 10.031

Entry



B0L000S, U24-ch37

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.78
Std Dev	12.17

Turn on : 26.2715

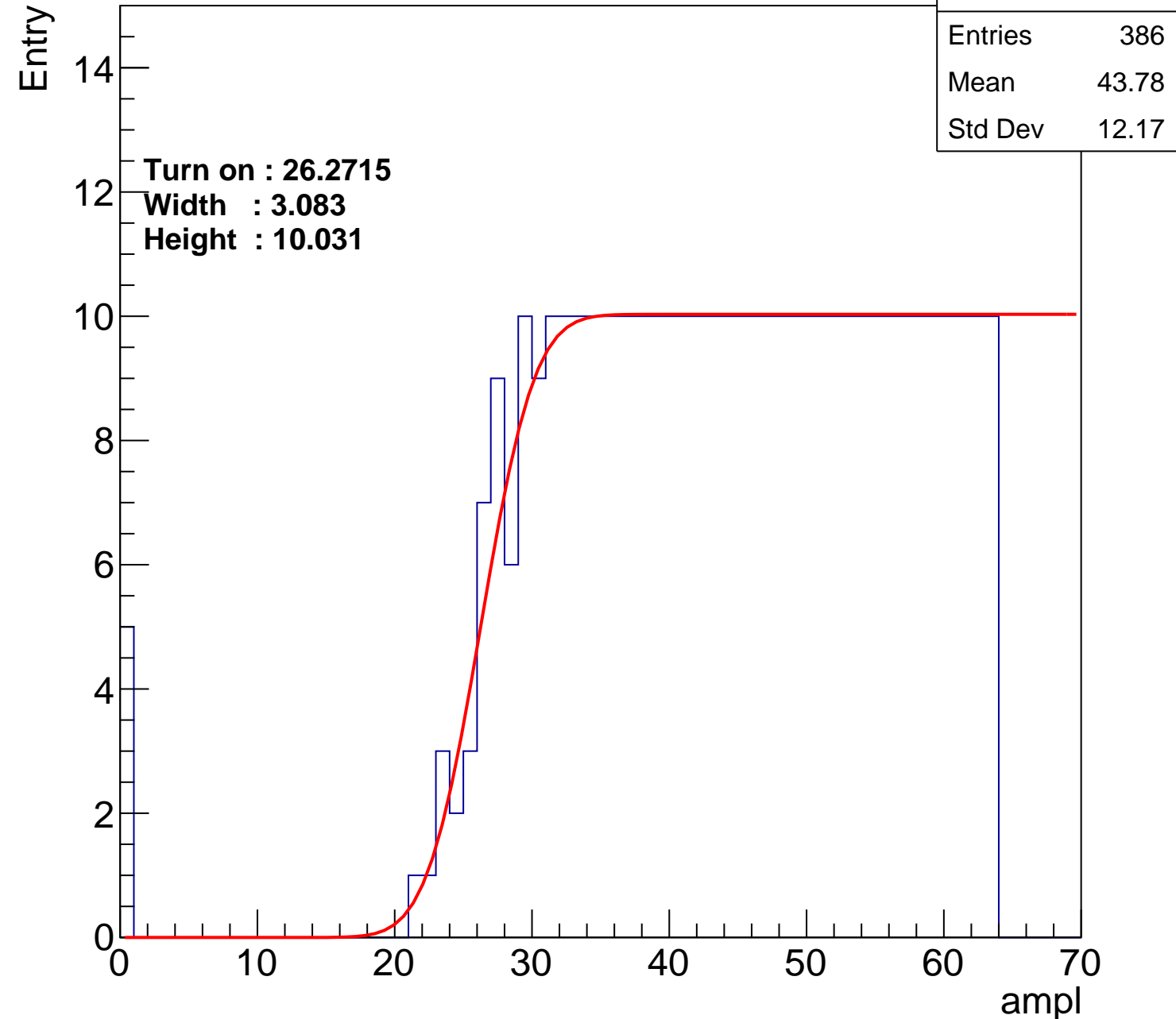
Width : 3.083

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.02
Std Dev	11.63

Turn on : 28.9971

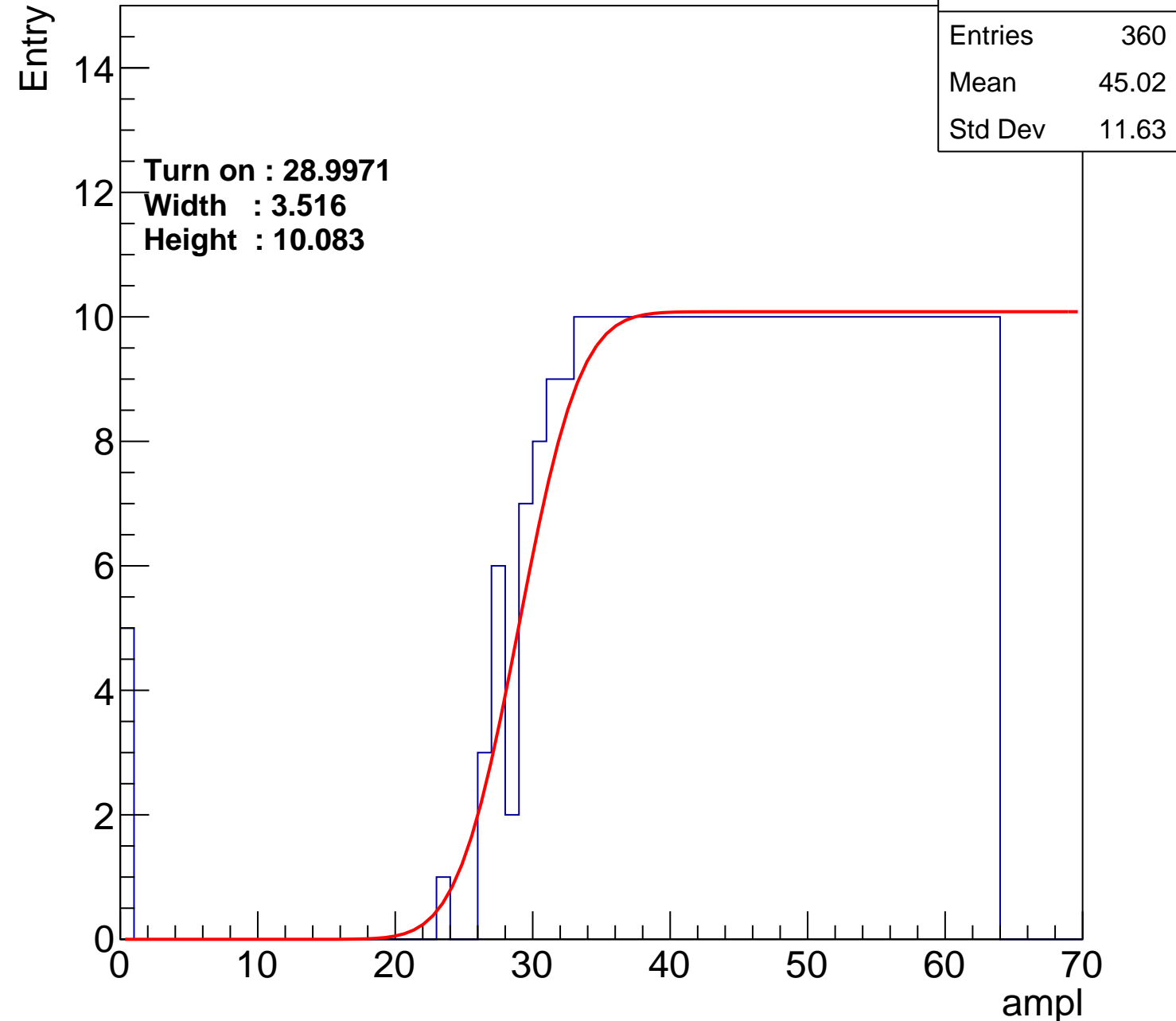
Width : 3.516

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch39

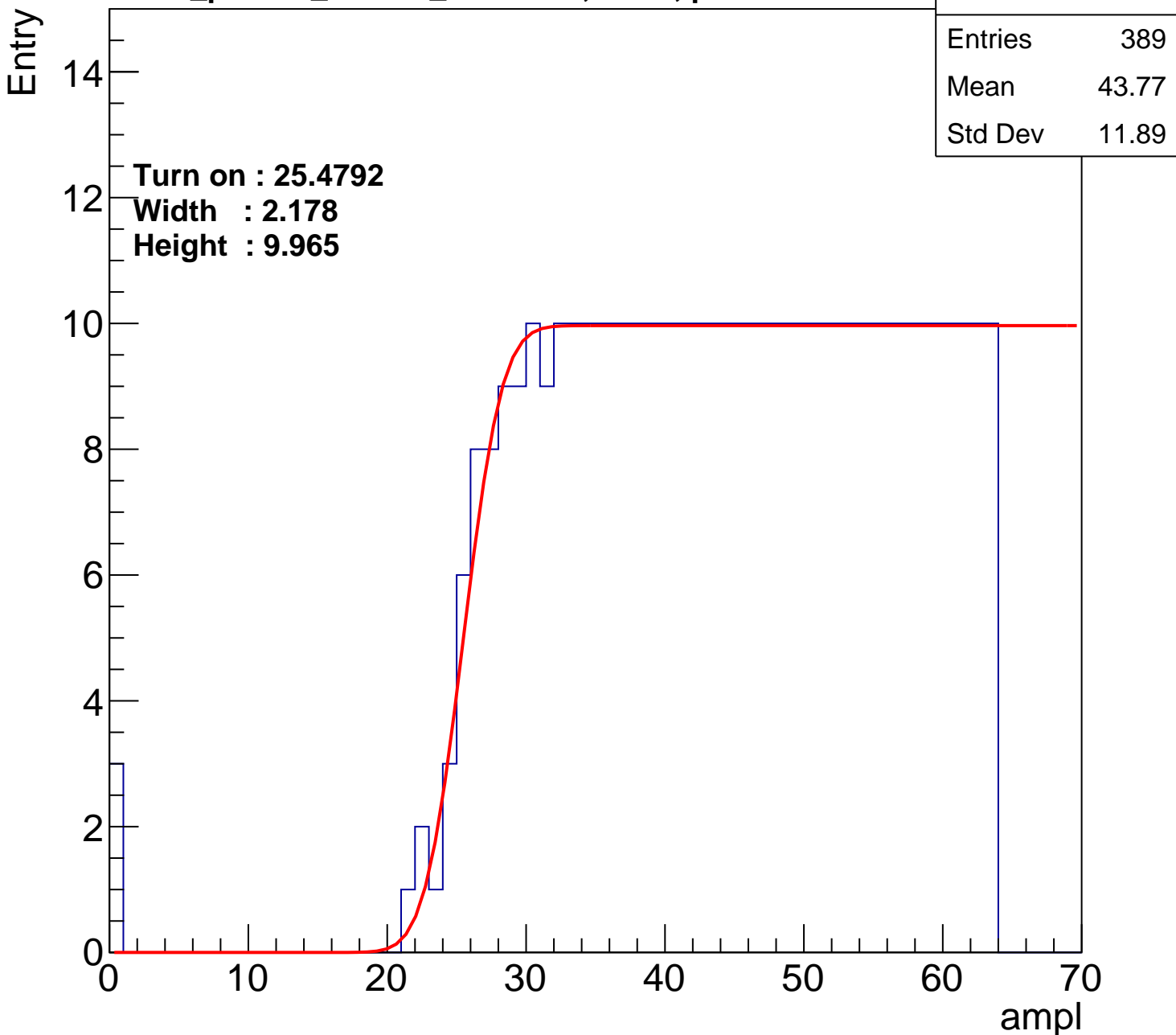
calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 25.4792

Width : 2.178

Height : 9.965

Entries	389
Mean	43.77
Std Dev	11.89



B0L000S, U24-ch40

calib_packv5_042523_0143.root, FC#5, port B1

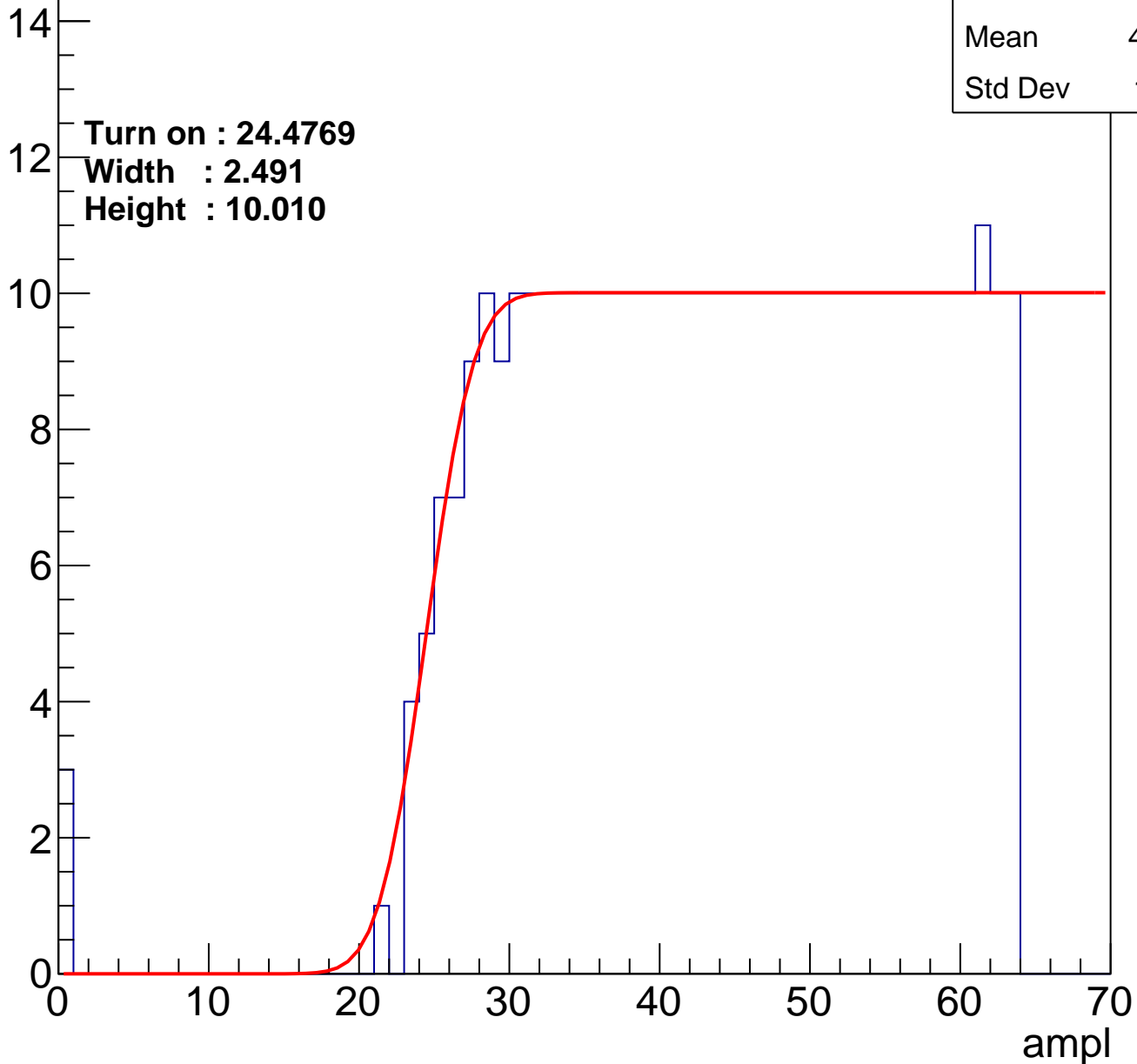
Entries	396
Mean	43.55
Std Dev	12.01

Turn on : 24.4769

Width : 2.491

Height : 10.010

Entry



B0L000S, U24-ch41

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.41
Std Dev	11.36

Turn on : 26.5737

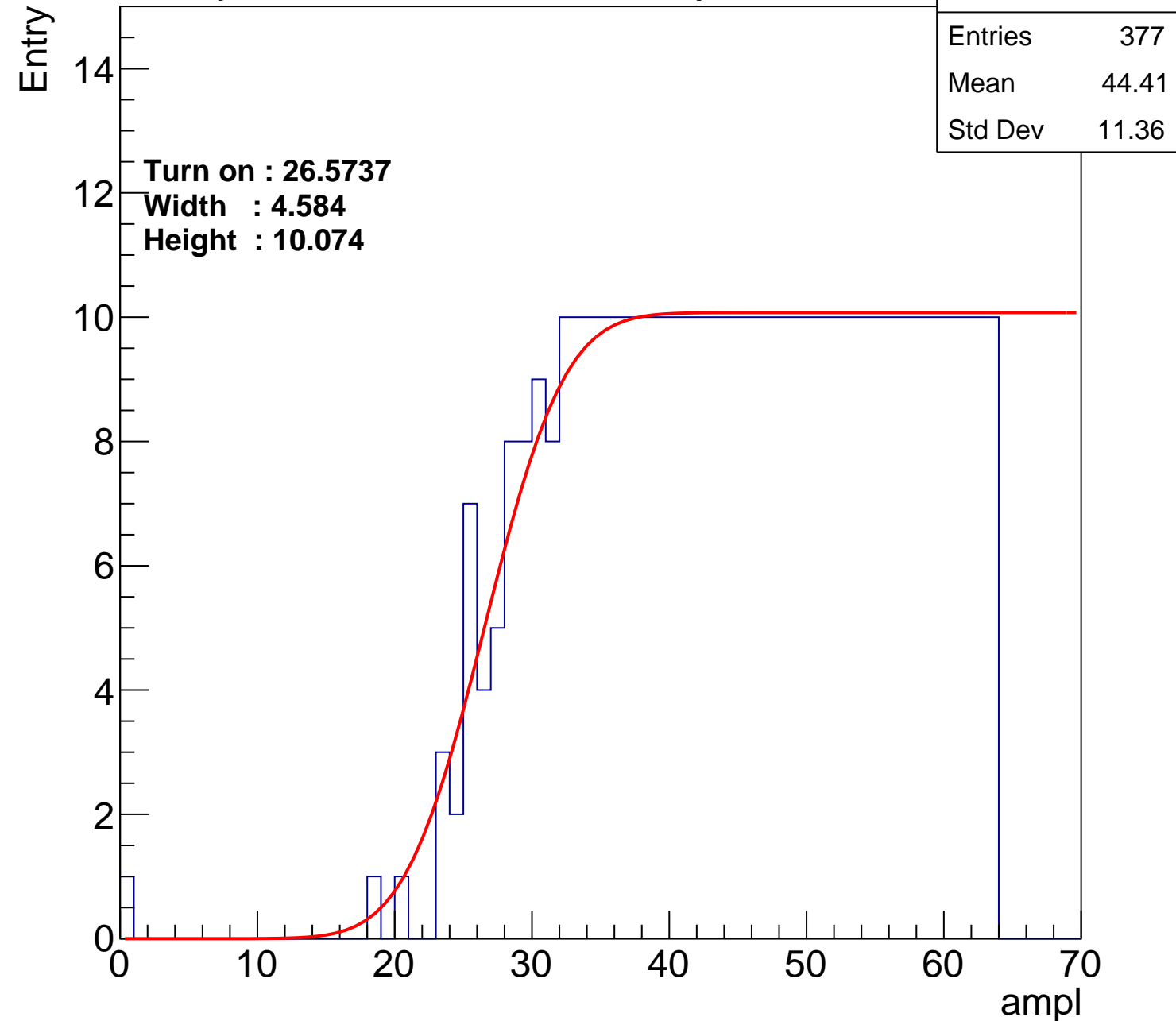
Width : 4.584

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch42

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 26.9961

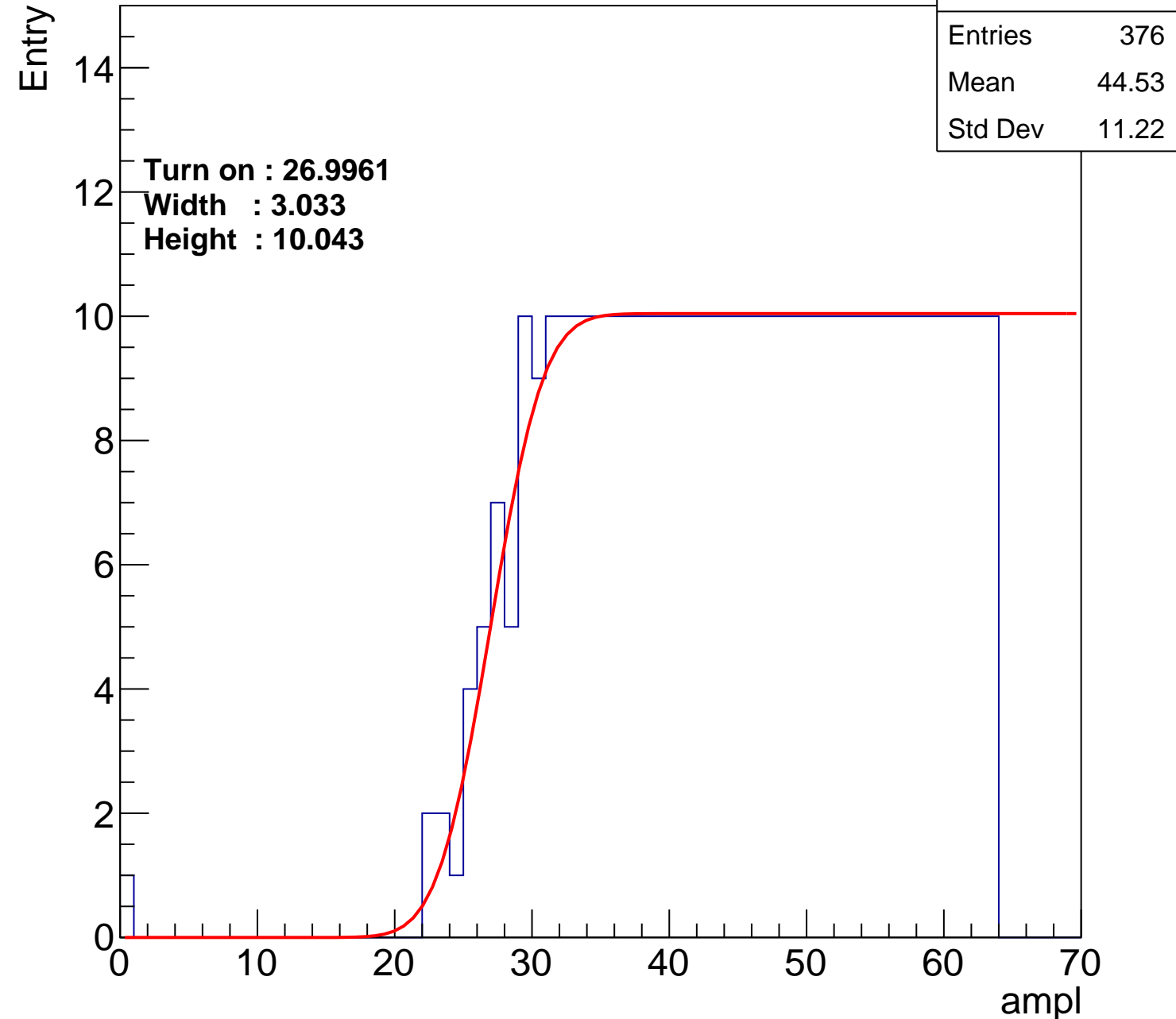
Width : 3.033

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.31
Std Dev	11.8

Turn on : 27.3559

Width : 3.254

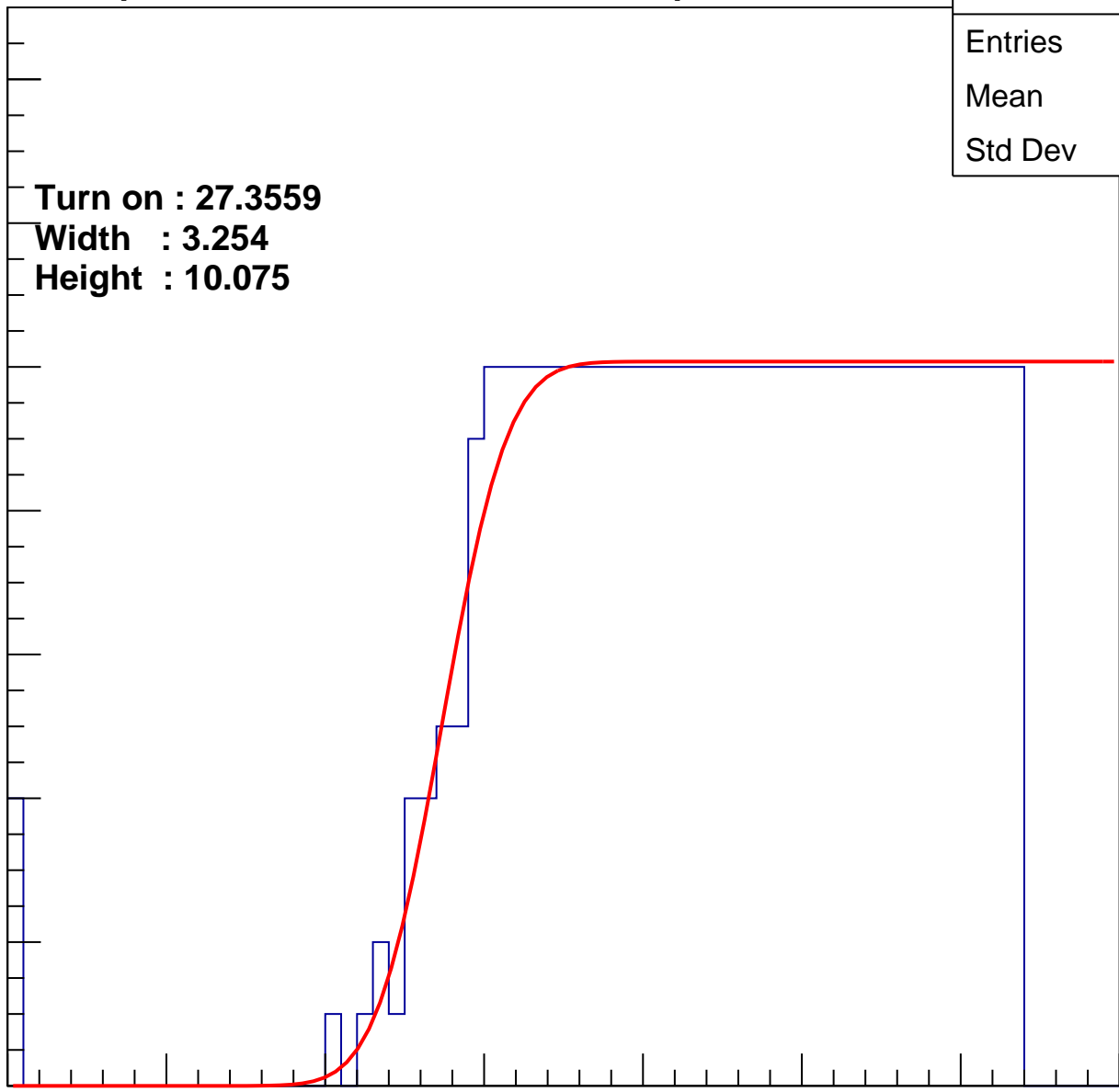
Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U24-ch44

calib_packv5_042523_0143.root, FC#5, port B1

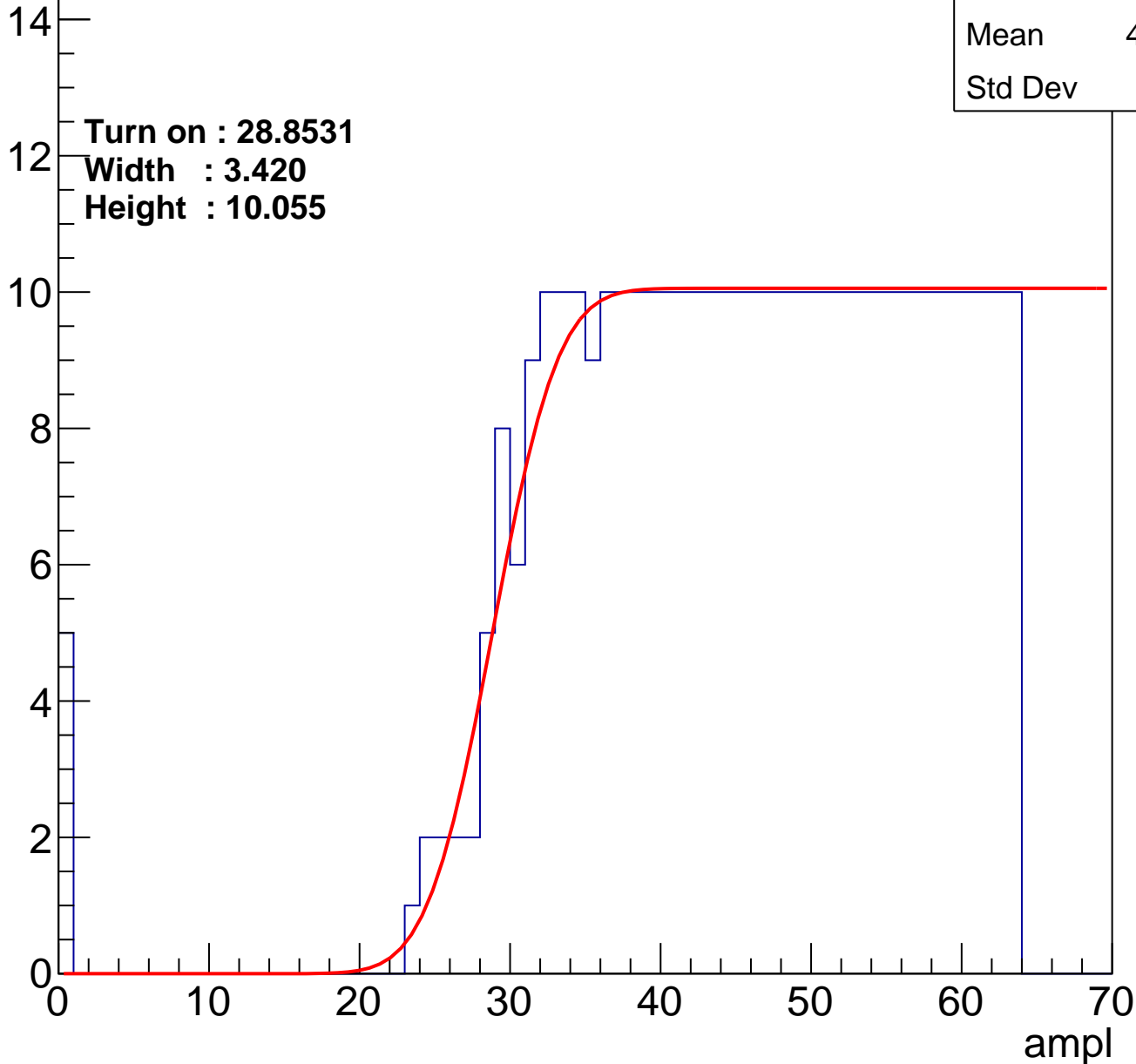
Entries	361
Mean	44.94
Std Dev	11.7

Turn on : 28.8531

Width : 3.420

Height : 10.055

Entry



B0L000S, U24-ch45

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.68
Std Dev	11.46

Turn on : 27.1930

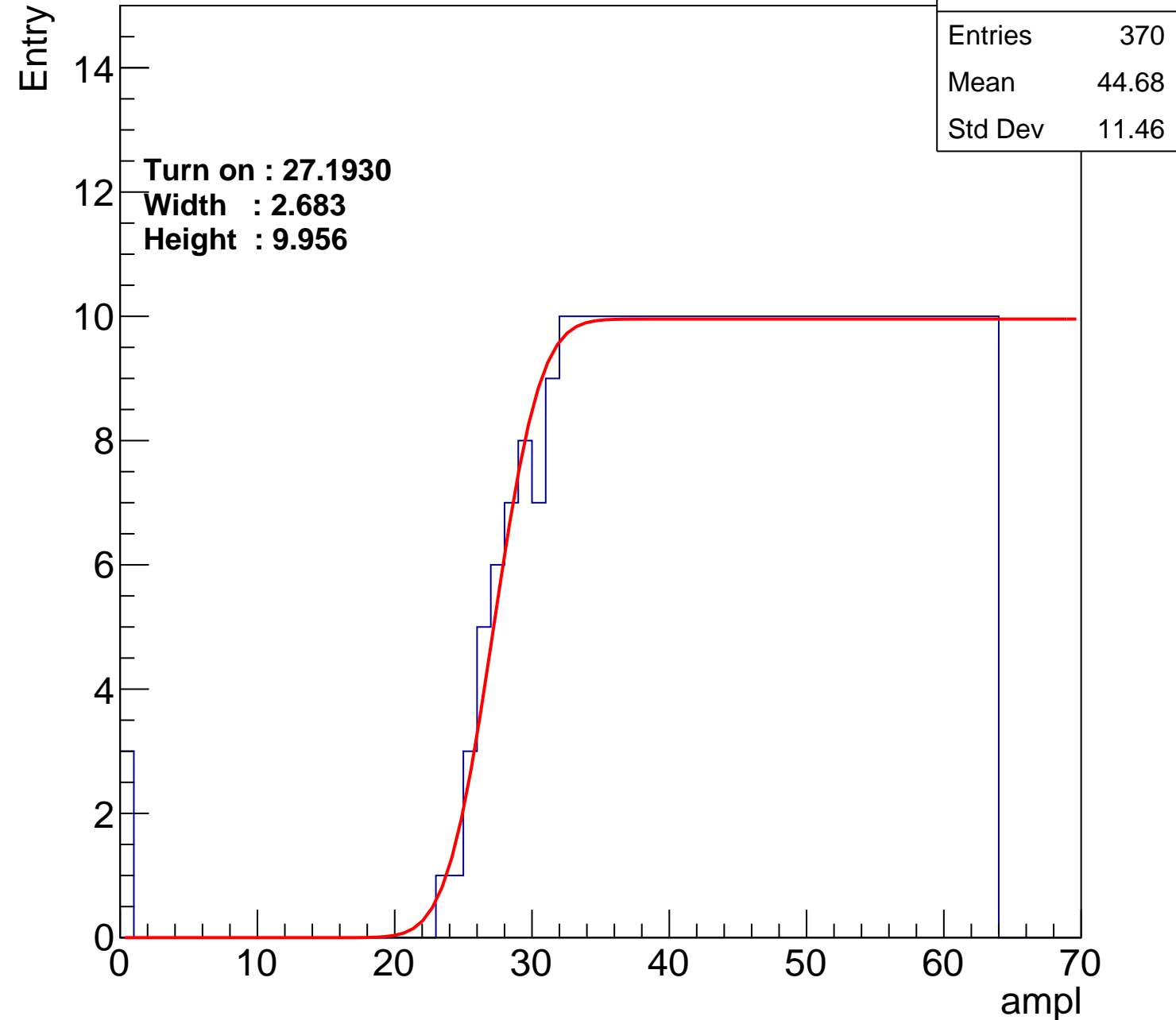
Width : 2.683

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch46

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.12
Std Dev	12.02

Turn on : 26.7924

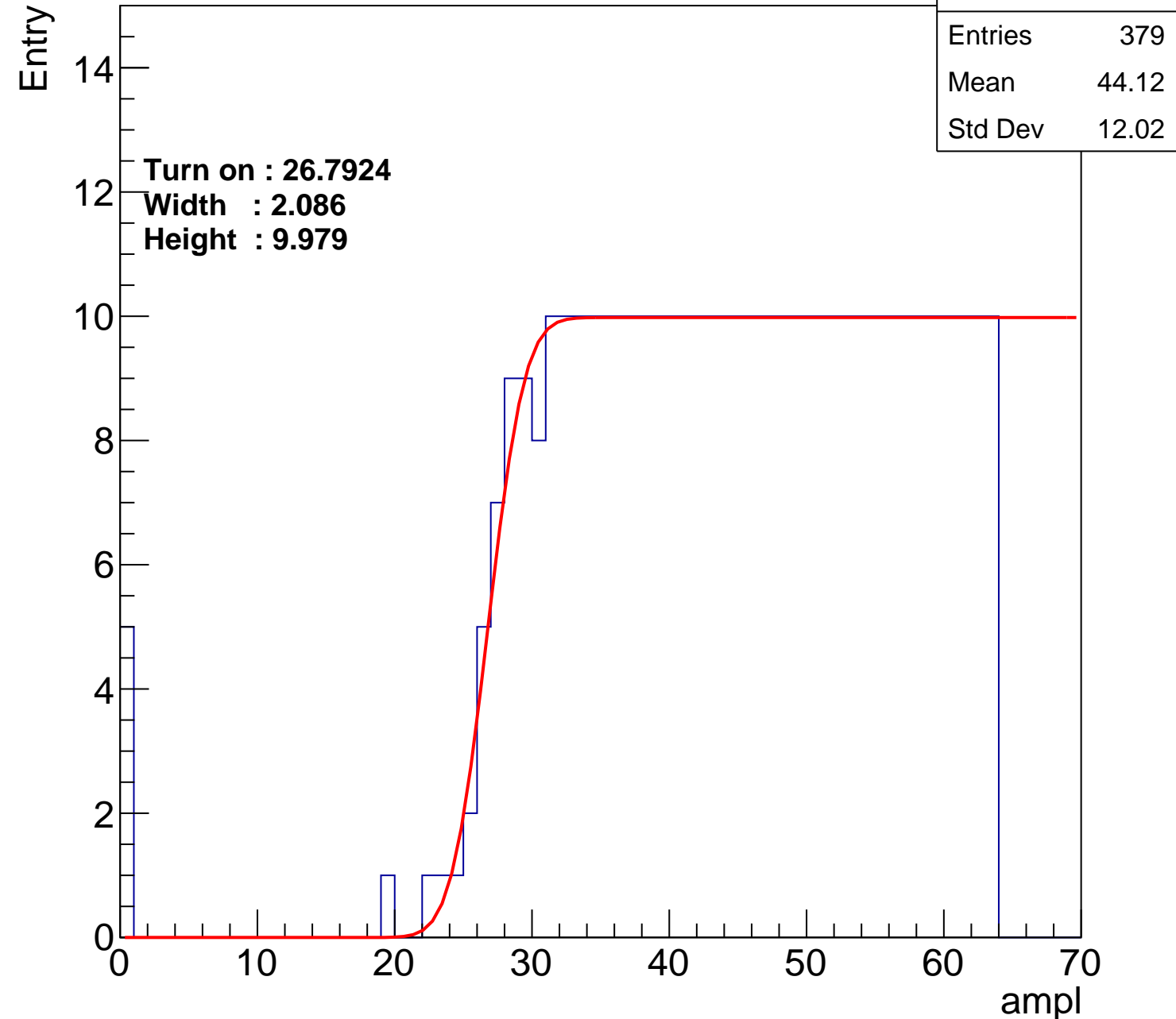
Width : 2.086

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch47

calib_packv5_042523_0143.root, FC#5, port B1

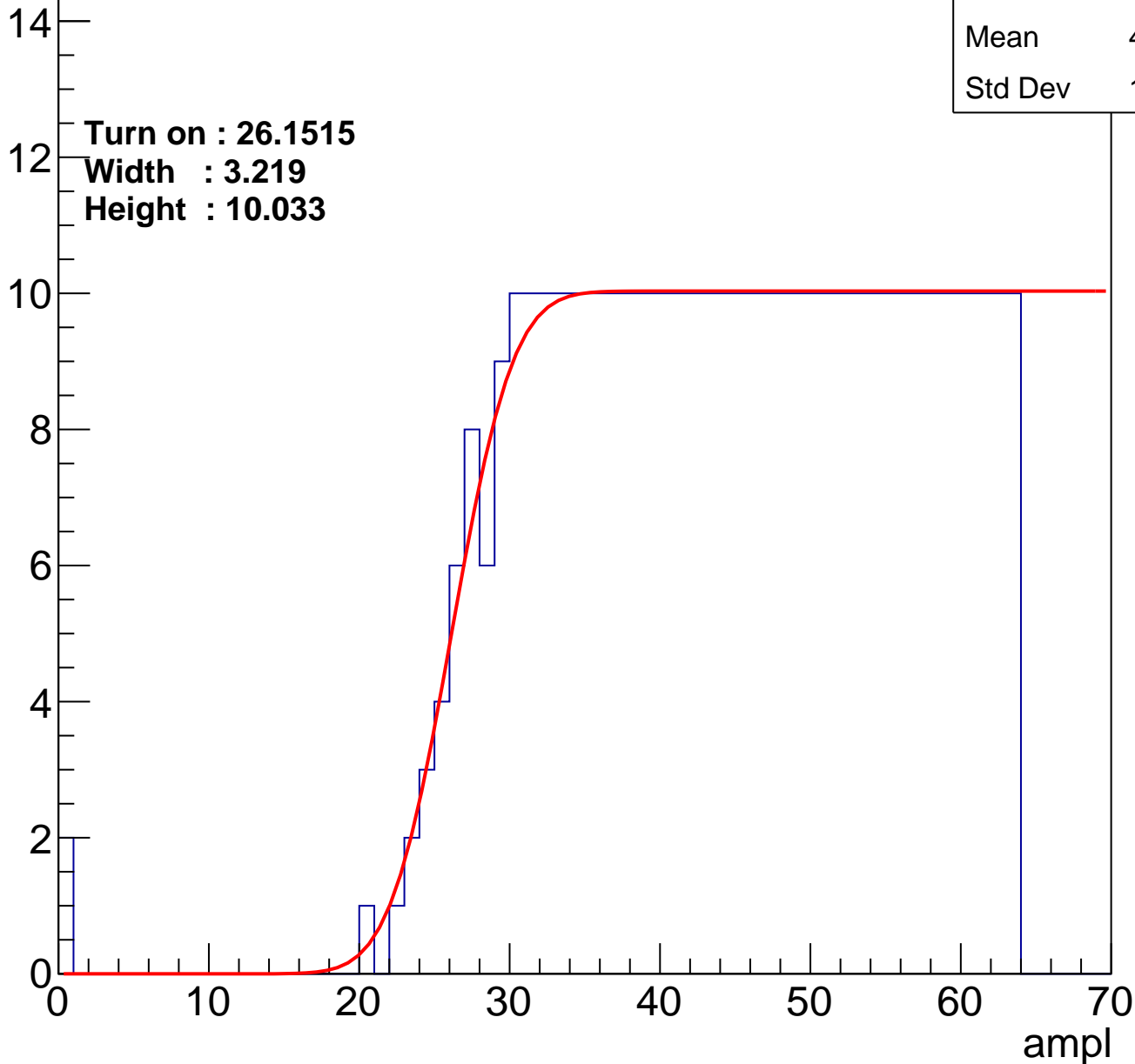
Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.1515

Width : 3.219

Height : 10.033

Entry



B0L000S, U24-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.79
Std Dev	11.91

Turn on : 25.9777

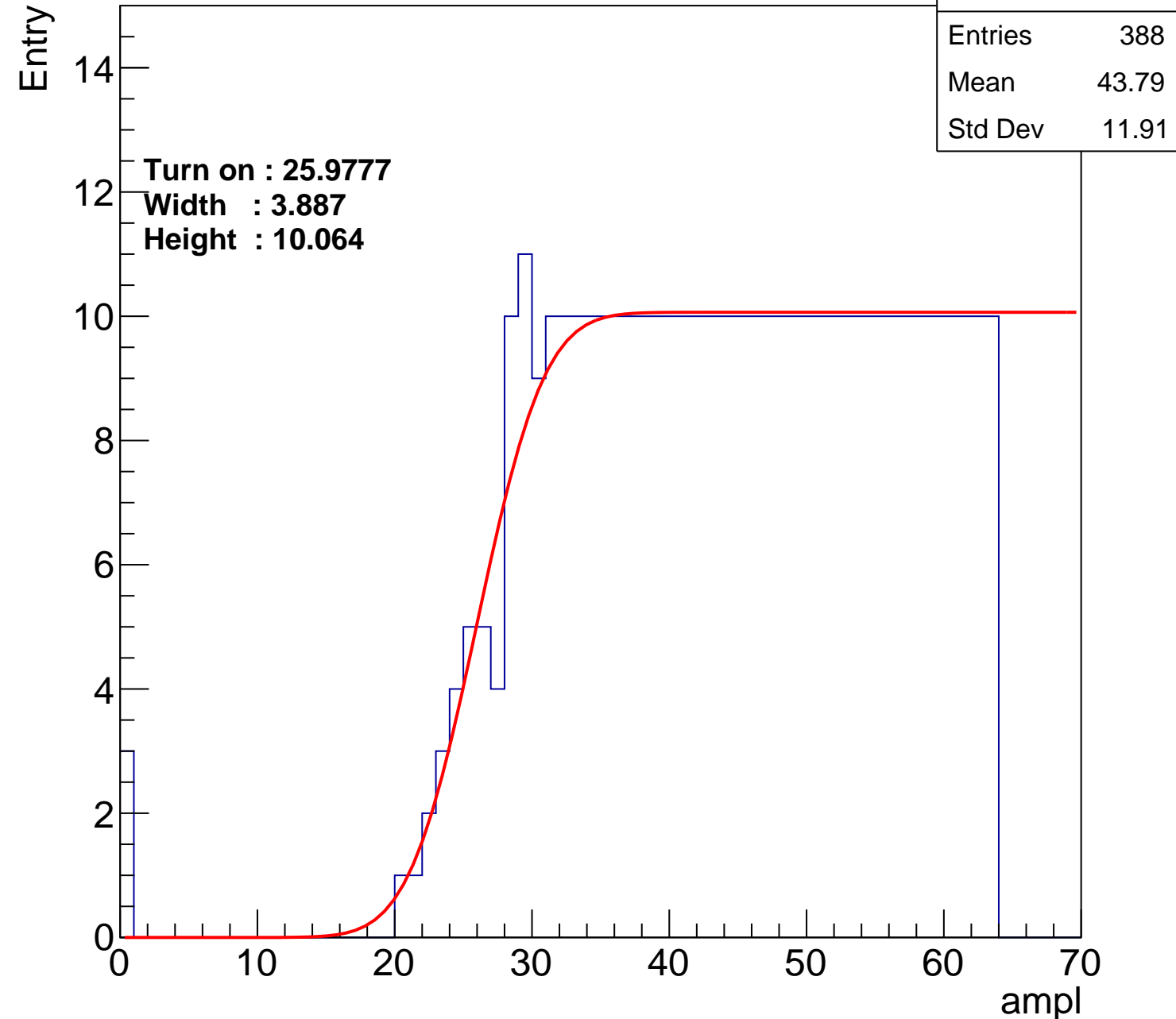
Width : 3.887

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.52
Std Dev	11.87

Turn on : 27.4676

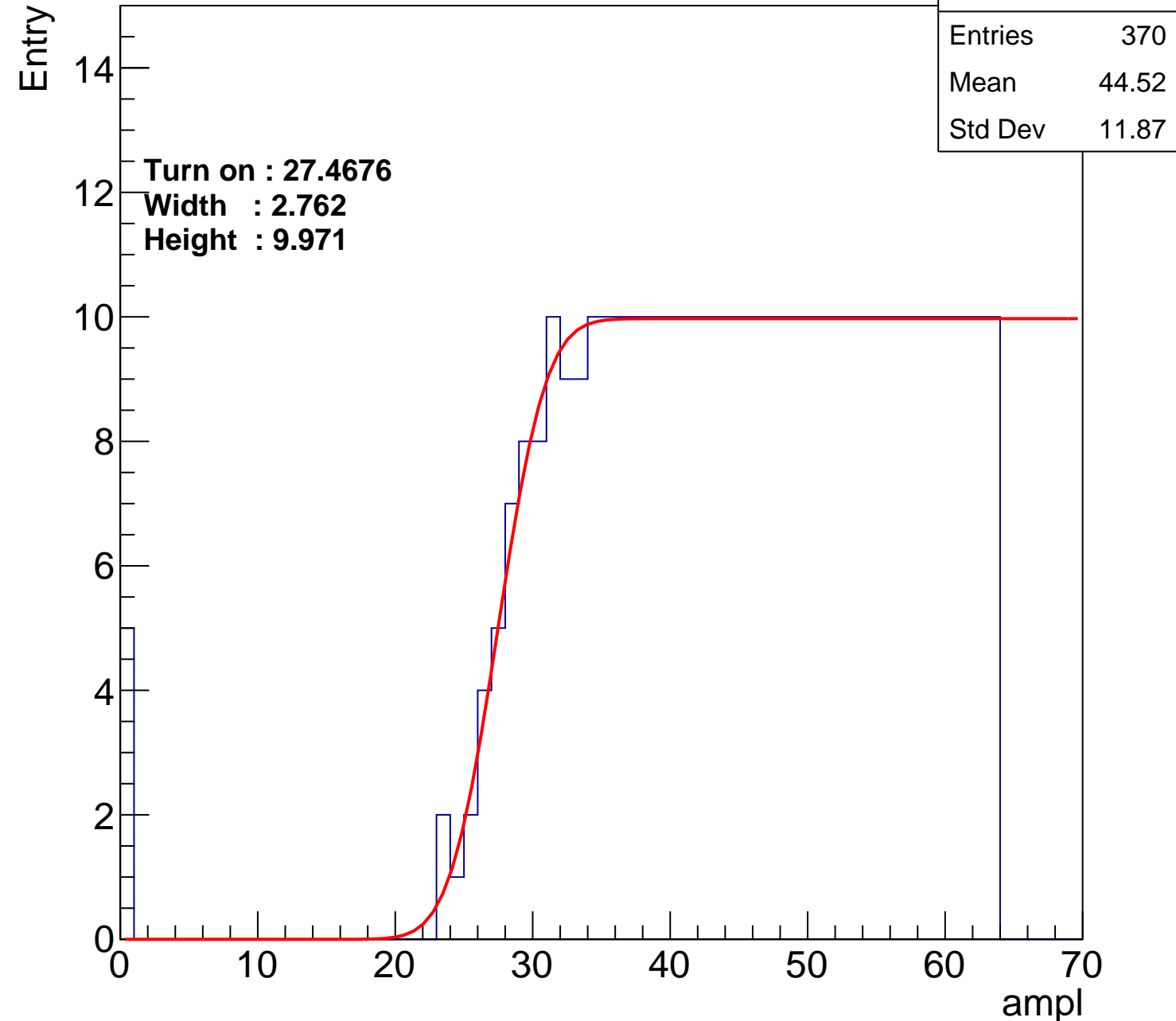
Width : 2.762

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch50

calib_packv5_042523_0143.root, FC#5, port B1

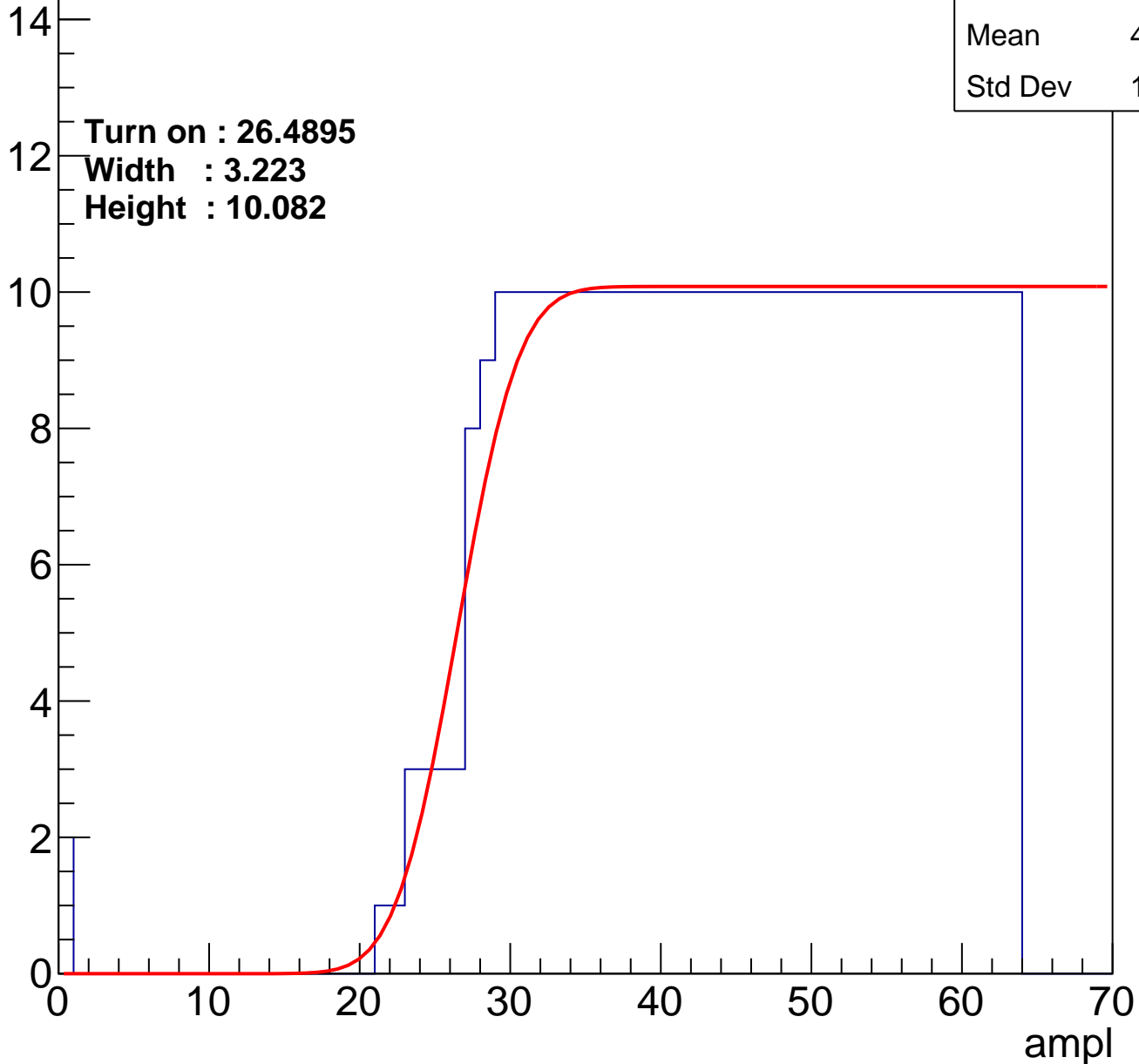
Entries	383
Mean	44.14
Std Dev	11.56

Turn on : 26.4895

Width : 3.223

Height : 10.082

Entry



B0L000S, U24-ch51

calib_packv5_042523_0143.root, FC#5, port B1

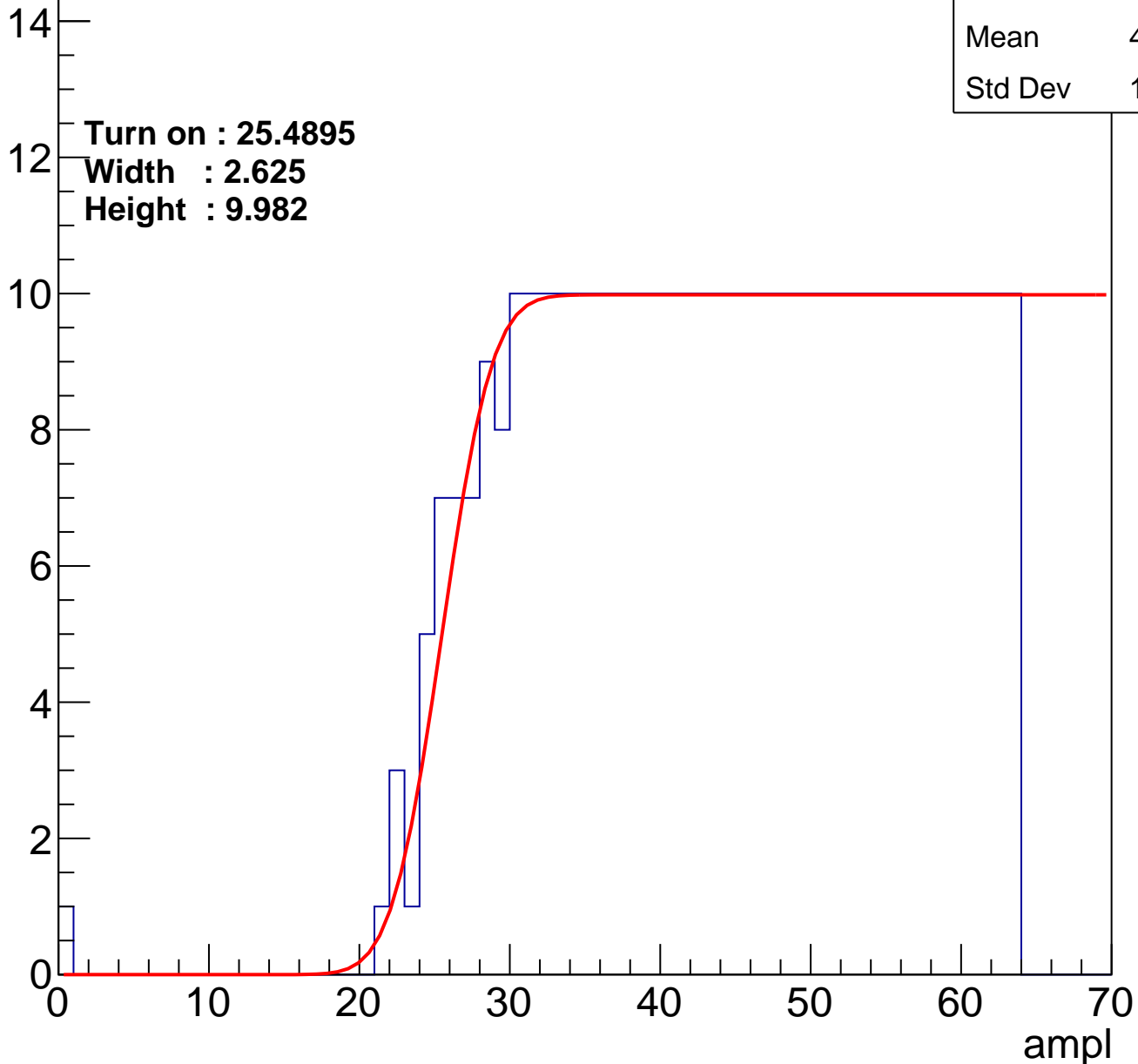
Entries	389
Mean	43.88
Std Dev	11.58

Turn on : 25.4895

Width : 2.625

Height : 9.982

Entry



B0L000S, U24-ch52

calib_packv5_042523_0143.root, FC#5, port B1

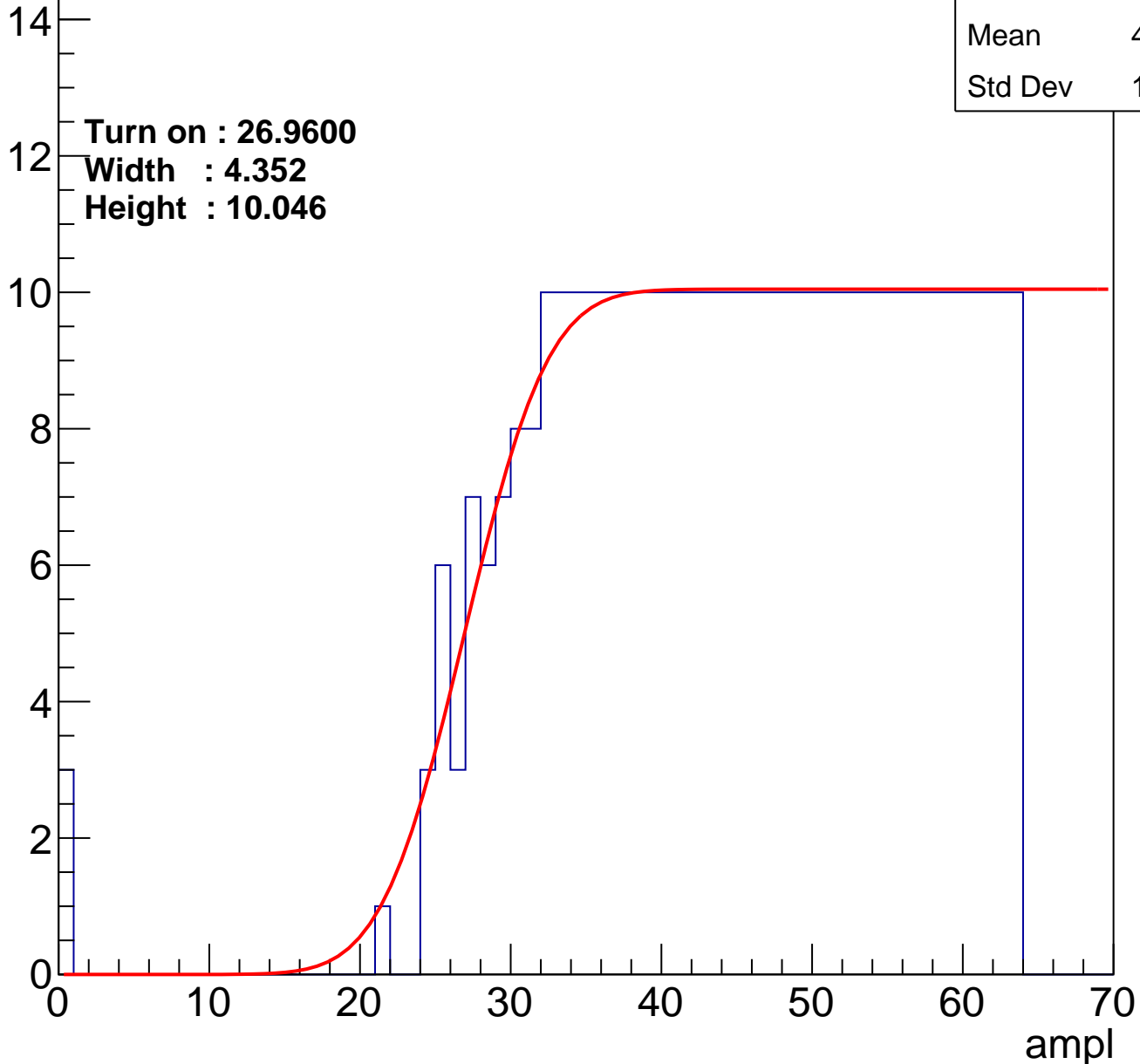
Entries	372
Mean	44.54
Std Dev	11.57

Turn on : 26.9600

Width : 4.352

Height : 10.046

Entry



B0L000S, U24-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.33
Std Dev	10.94

Turn on : 28.4271

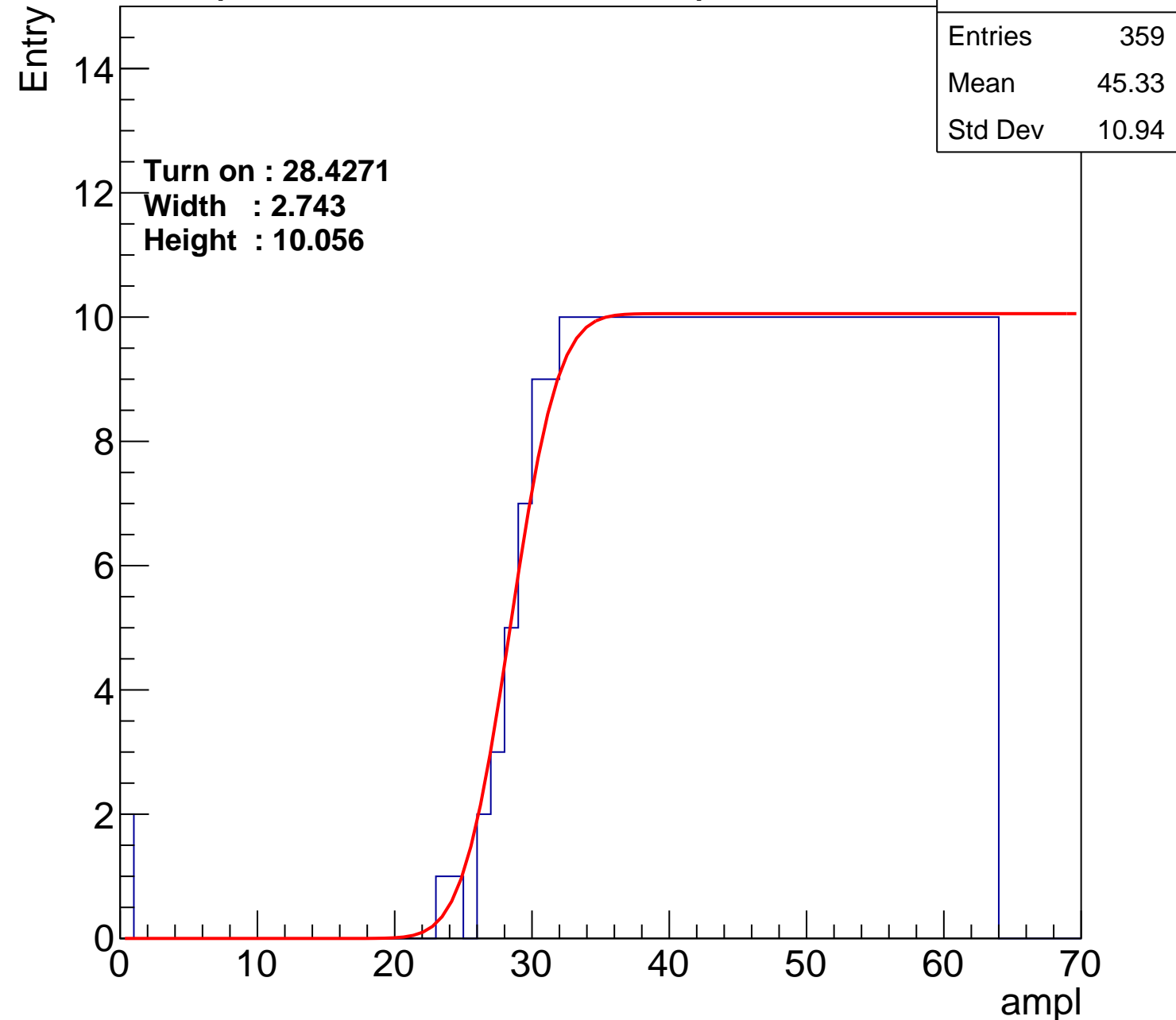
Width : 2.743

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch54

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.03
Std Dev	11.68

Turn on : 26.0279

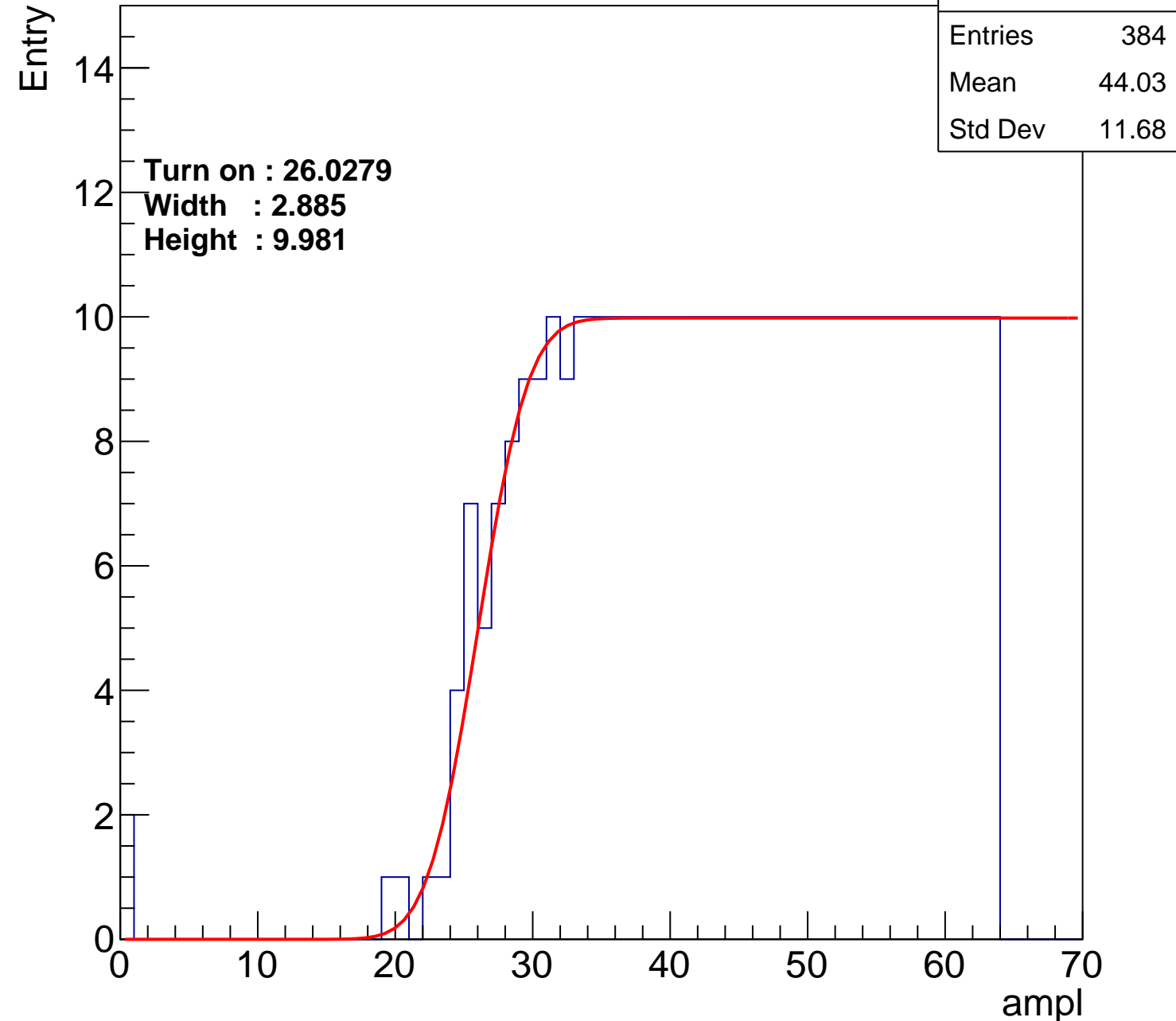
Width : 2.885

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch55

calib_packv5_042523_0143.root, FC#5, port B1

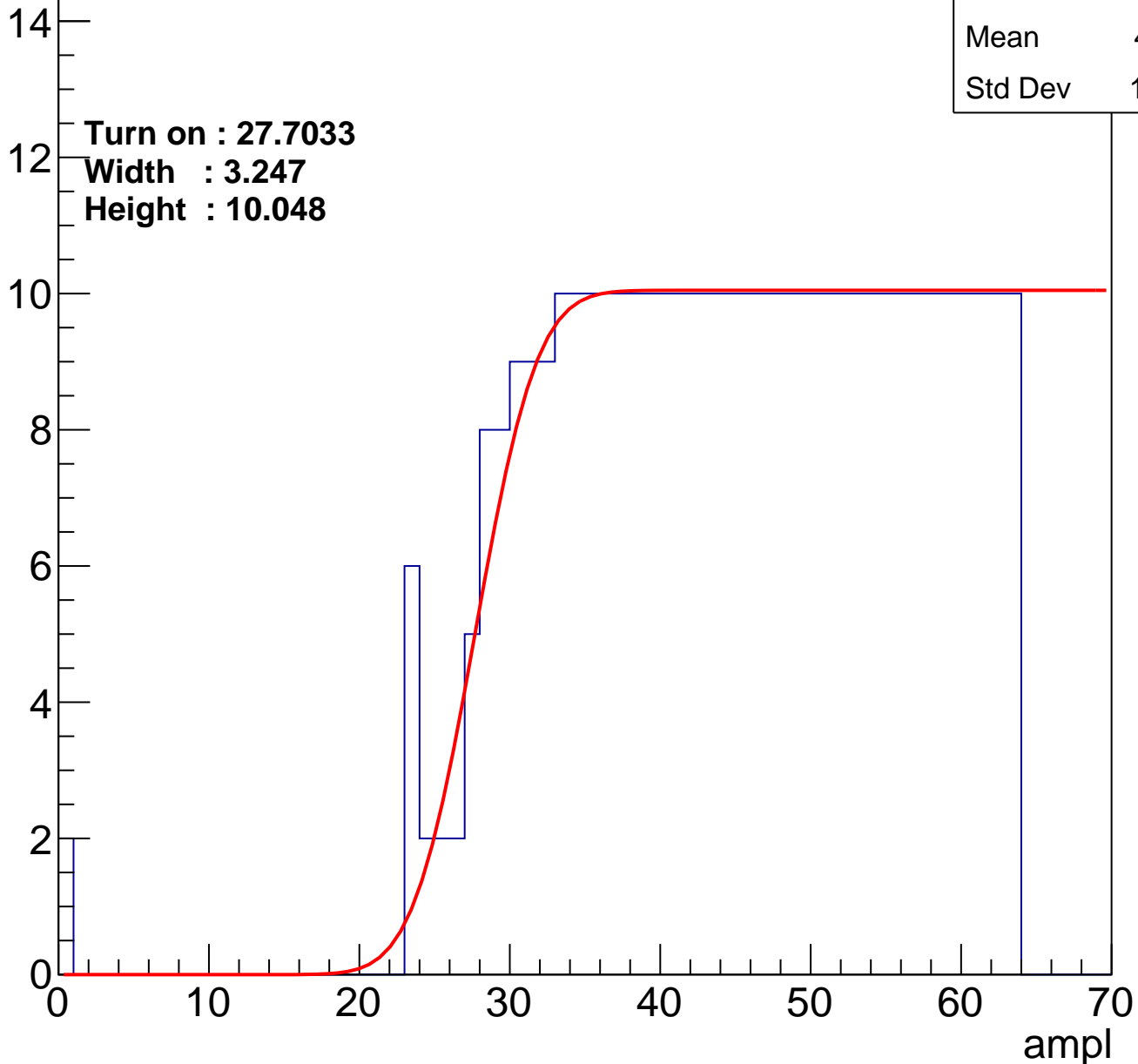
Entries	372
Mean	44.61
Std Dev	11.38

Turn on : 27.7033

Width : 3.247

Height : 10.048

Entry



B0L000S, U24-ch56

calib_packv5_042523_0143.root, FC#5, port B1

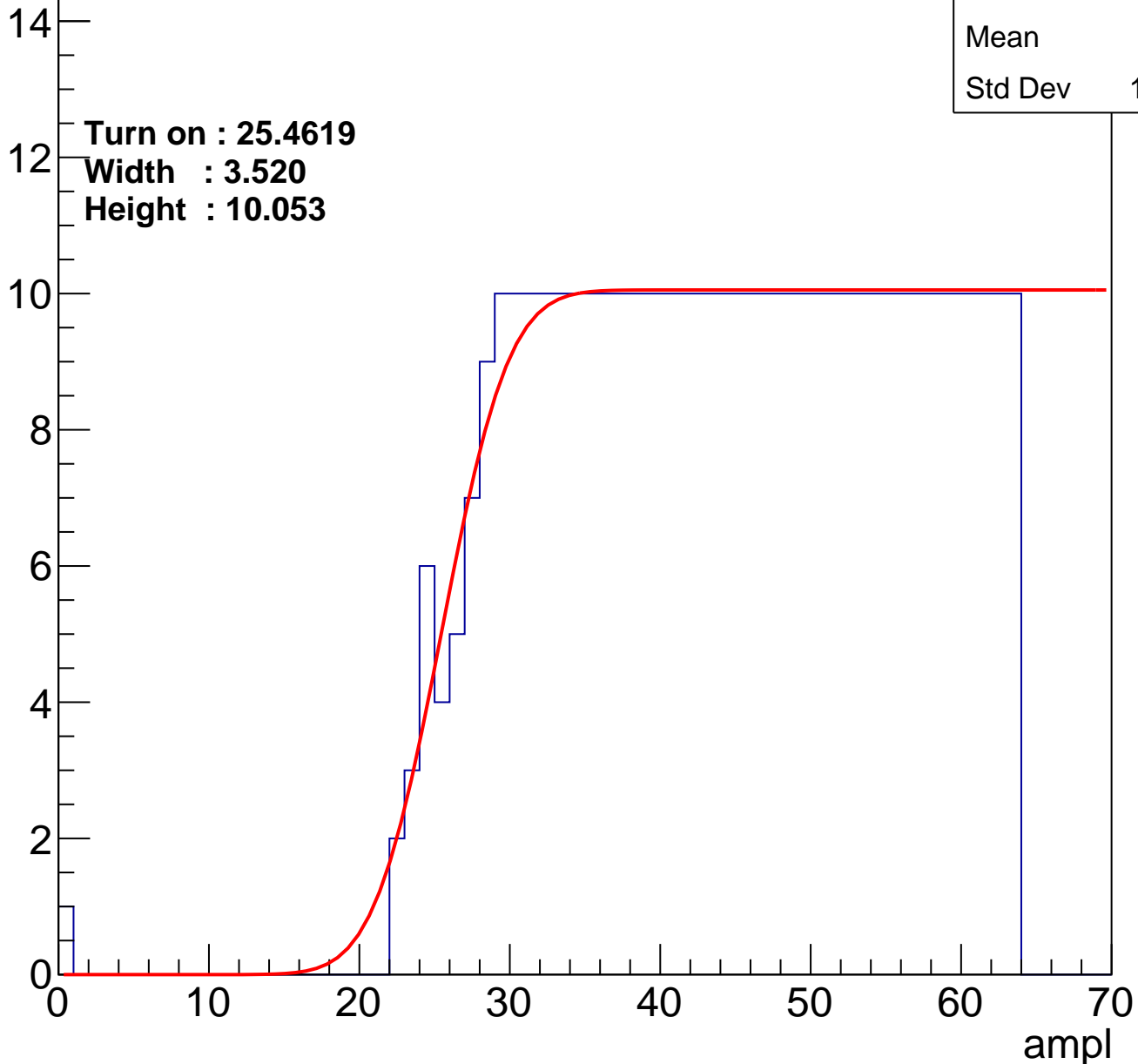
Entries	387
Mean	44
Std Dev	11.49

Turn on : 25.4619

Width : 3.520

Height : 10.053

Entry



B0L000S, U24-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.44
Std Dev	10.76

Turn on : 28.1961

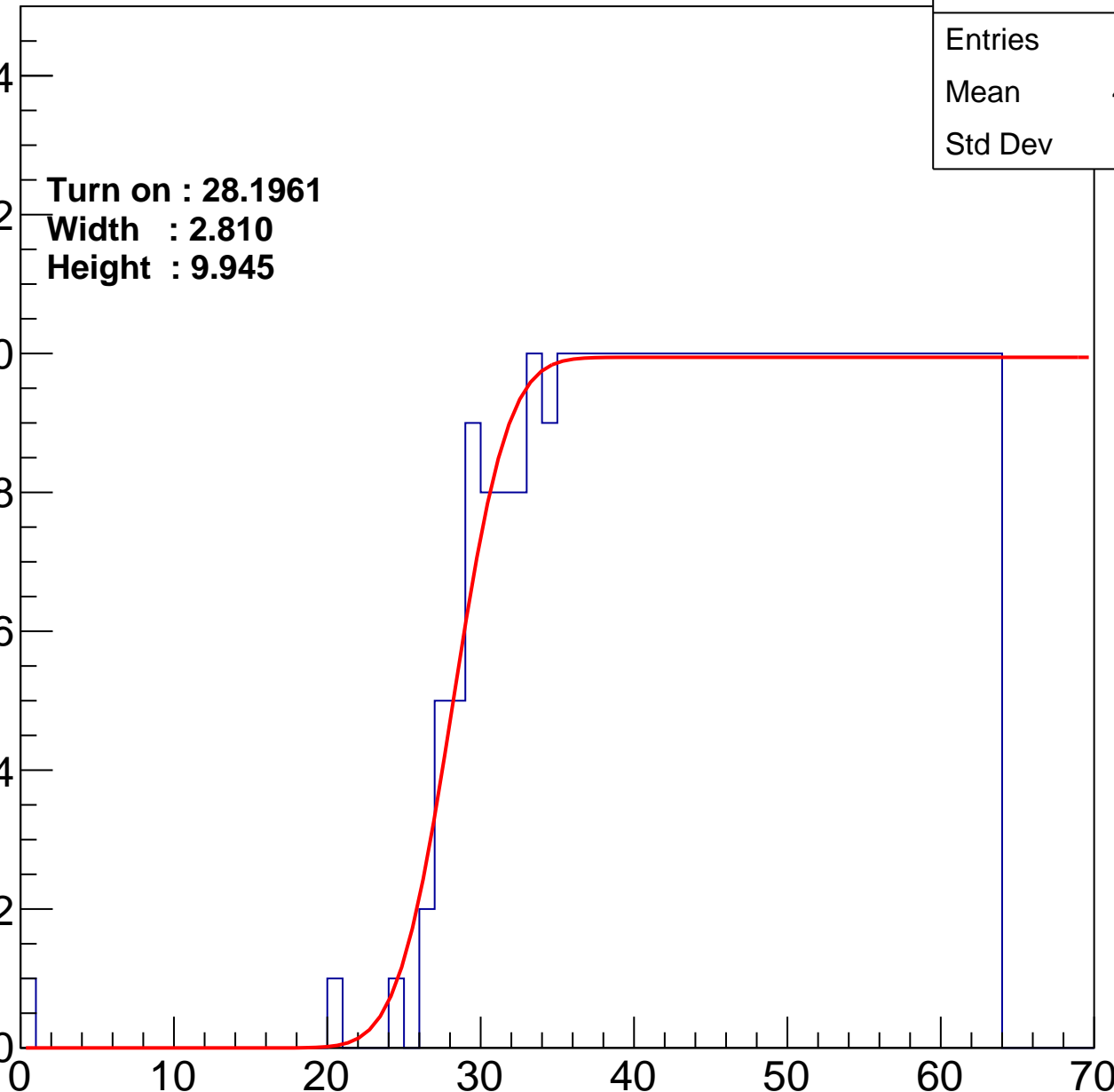
Width : 2.810

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch58

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.5633

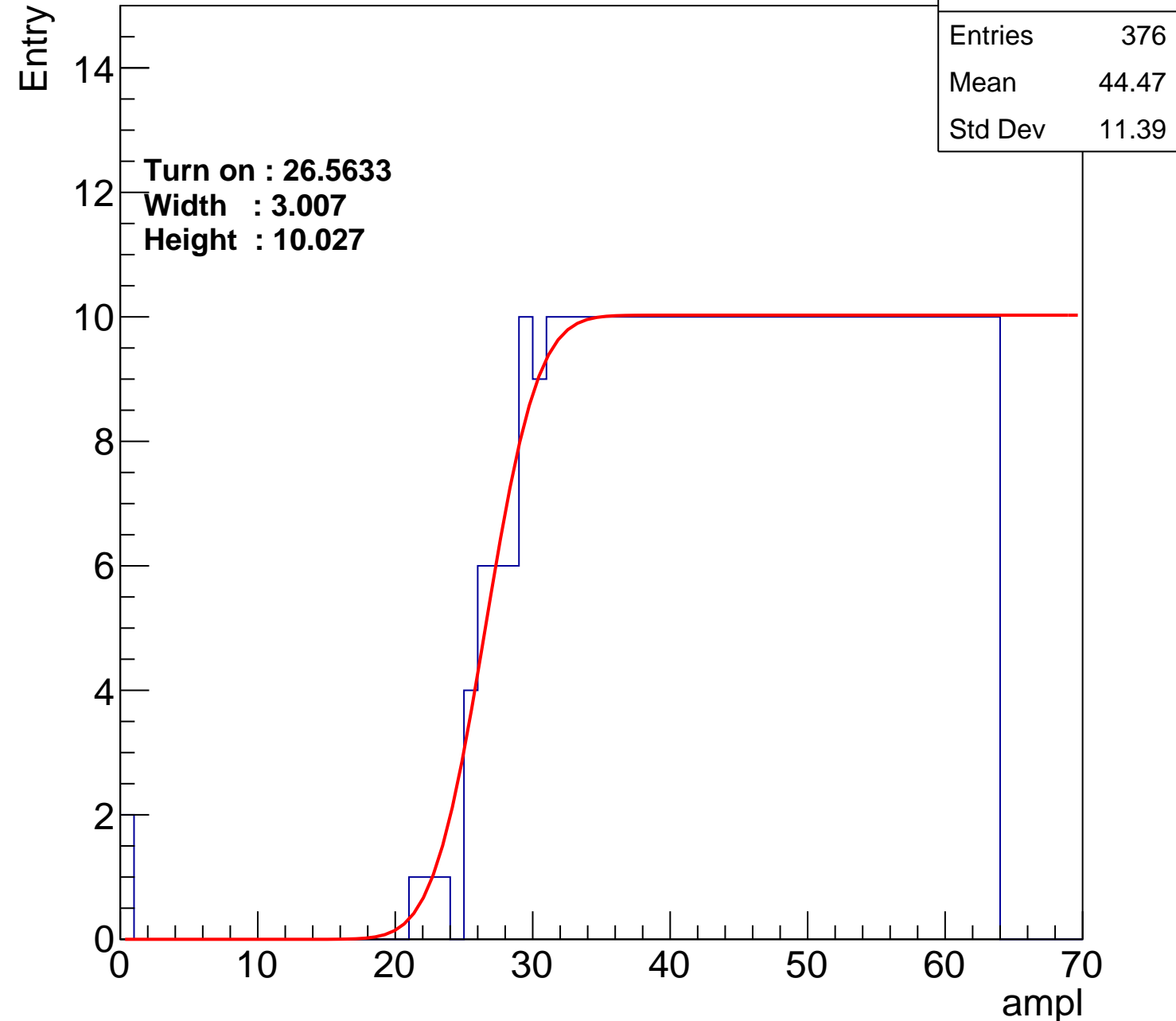
Width : 3.007

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch59

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.02
Std Dev	11.16

Turn on : 28.0472

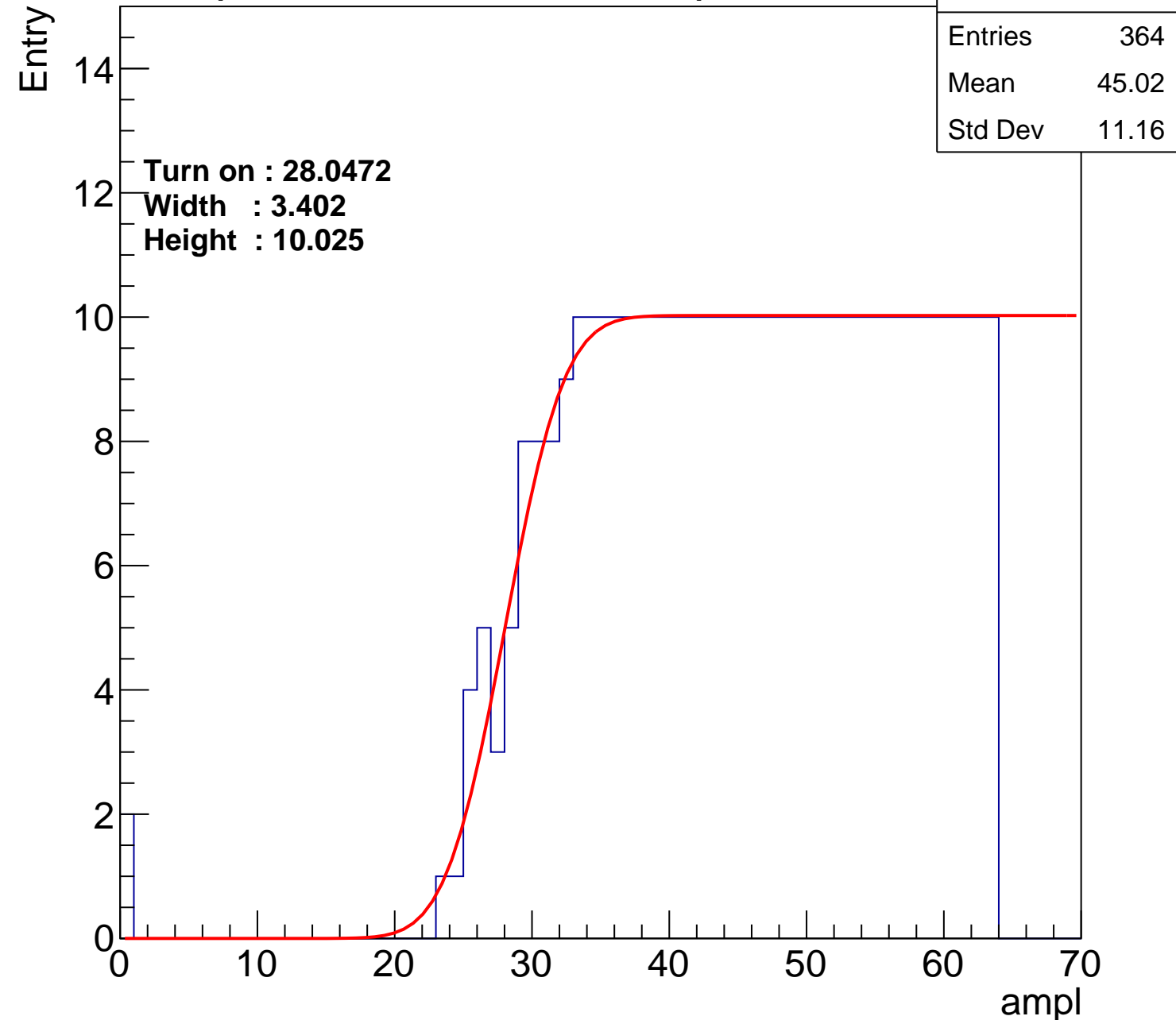
Width : 3.402

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch60

calib_packv5_042523_0143.root, FC#5, port B1

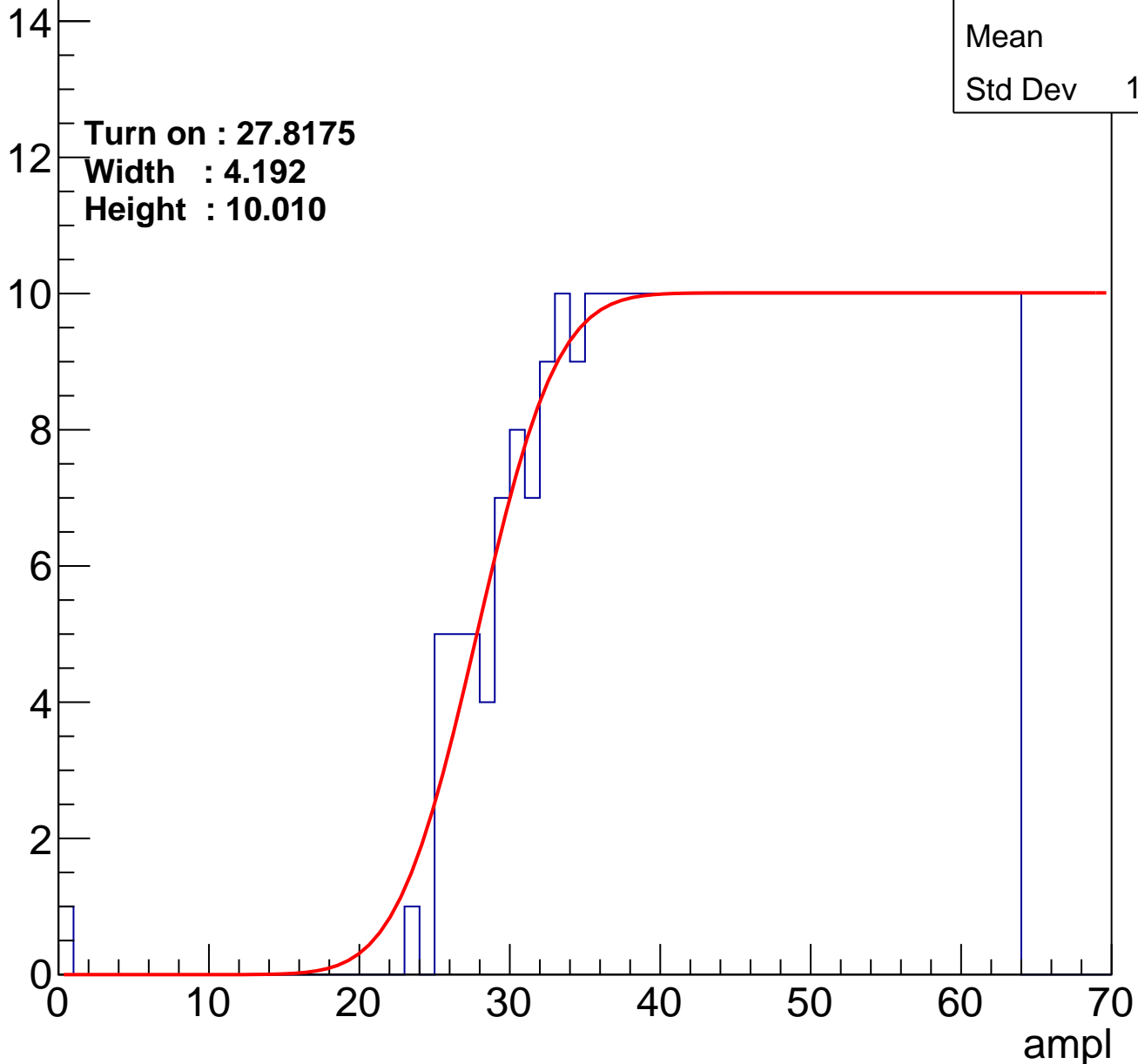
Entries	361
Mean	45.2
Std Dev	10.92

Turn on : 27.8175

Width : 4.192

Height : 10.010

Entry



B0L000S, U24-ch61

calib_packv5_042523_0143.root, FC#5, port B1

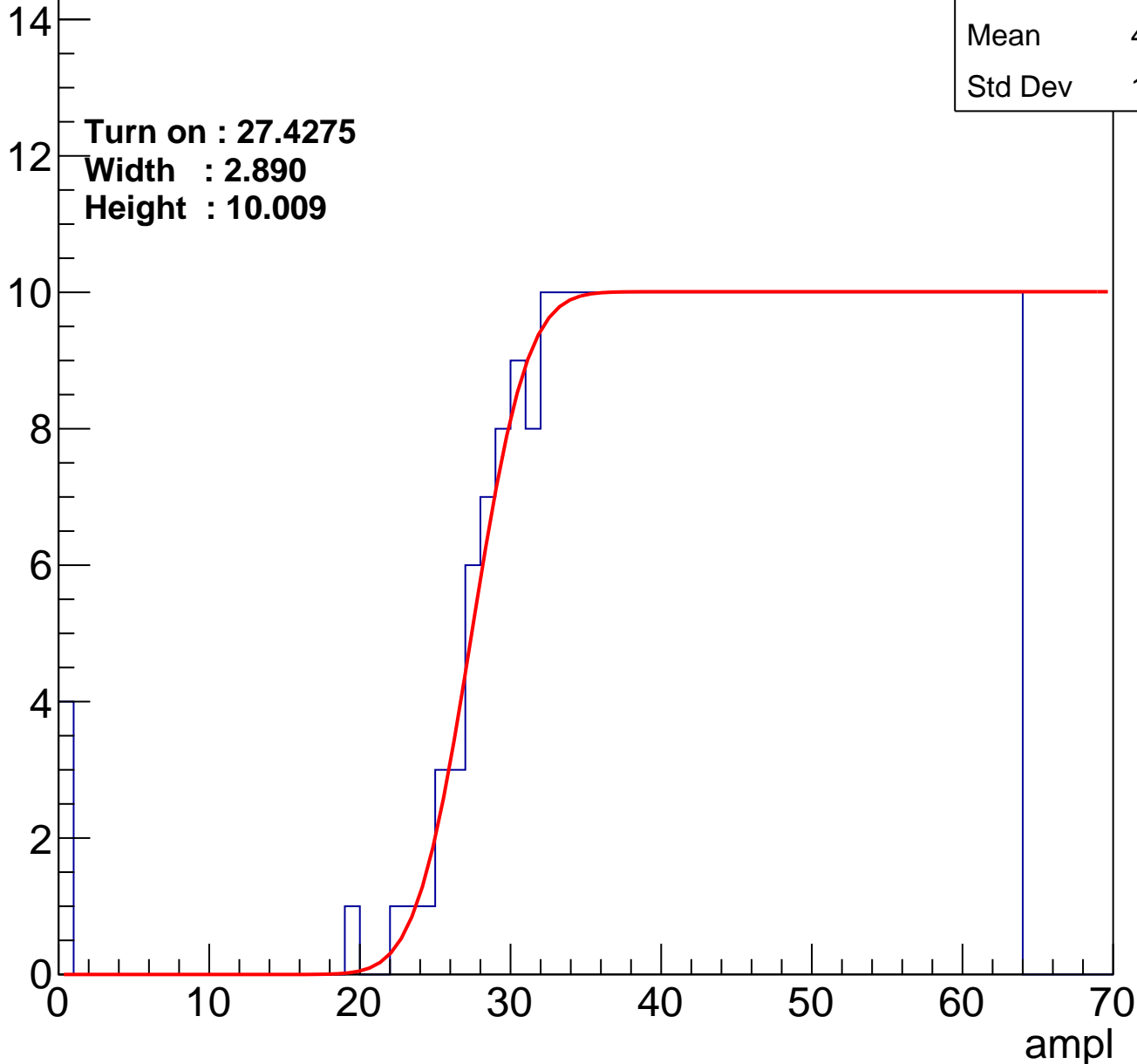
Entries	372
Mean	44.49
Std Dev	11.74

Turn on : 27.4275

Width : 2.890

Height : 10.009

Entry



B0L000S, U24-ch62

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.5480

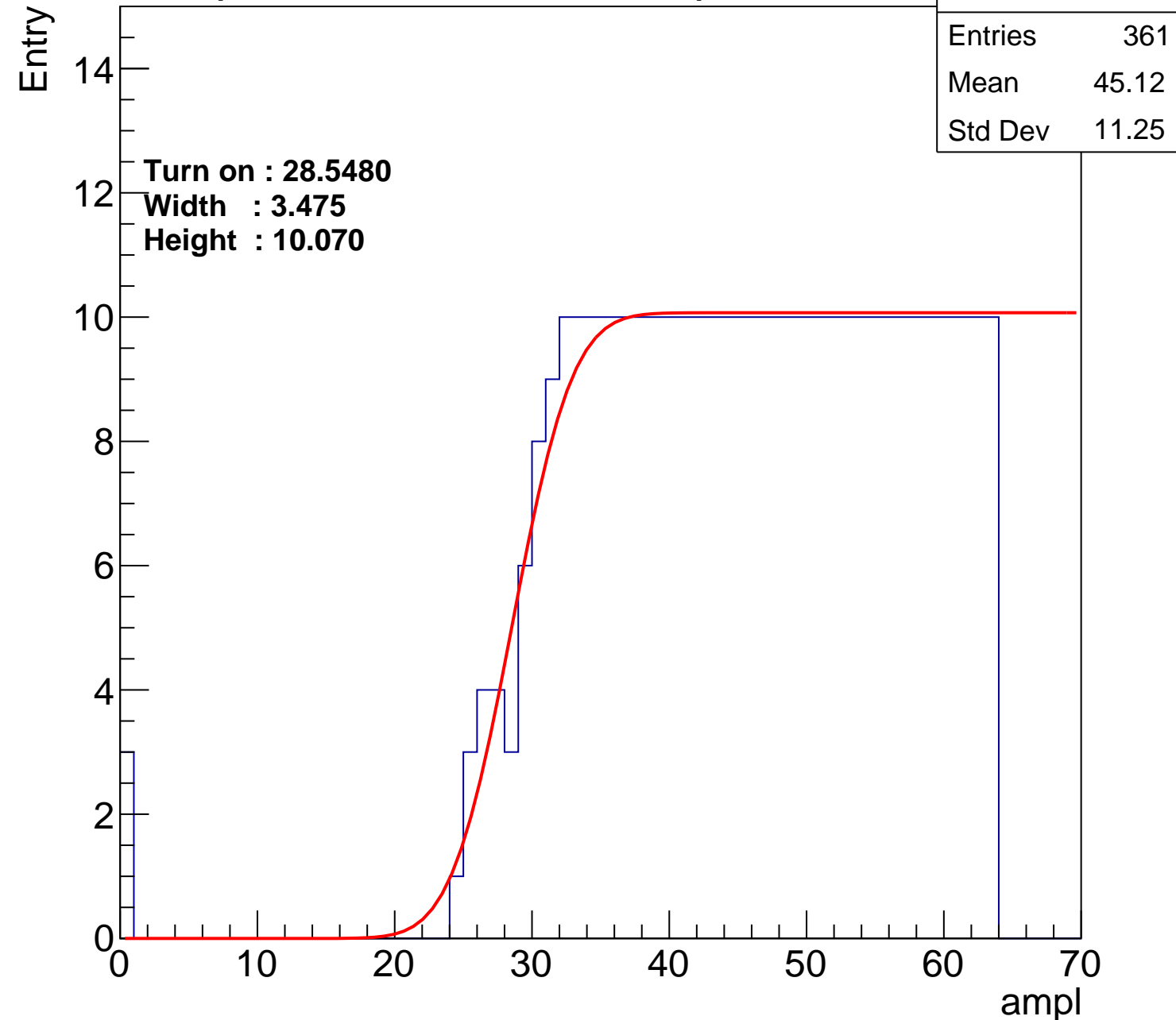
Width : 3.475

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.25
Std Dev	10.79

Turn on : 28.1340

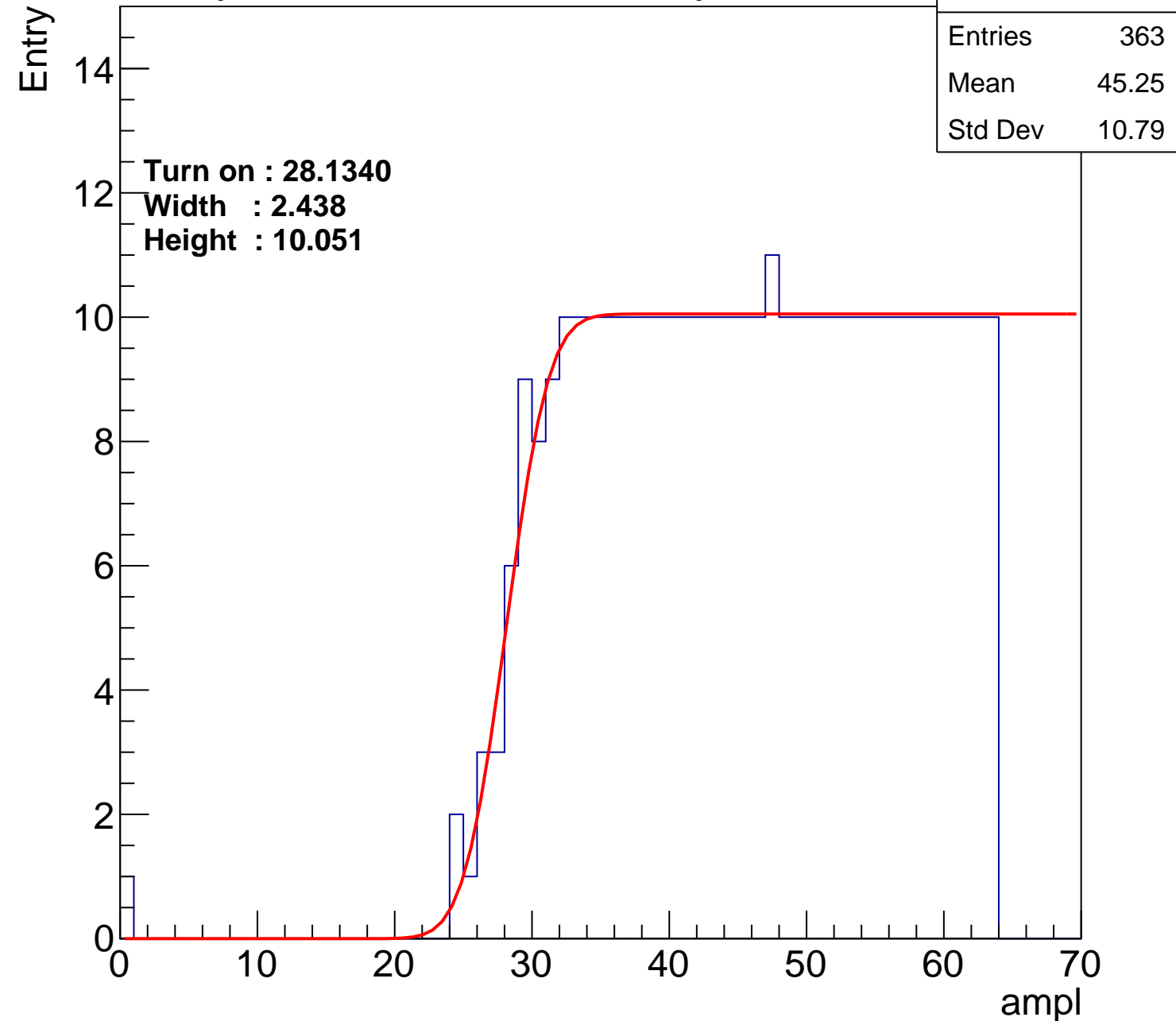
Width : 2.438

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch64

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.96
Std Dev	11.56

Turn on : 25.4468

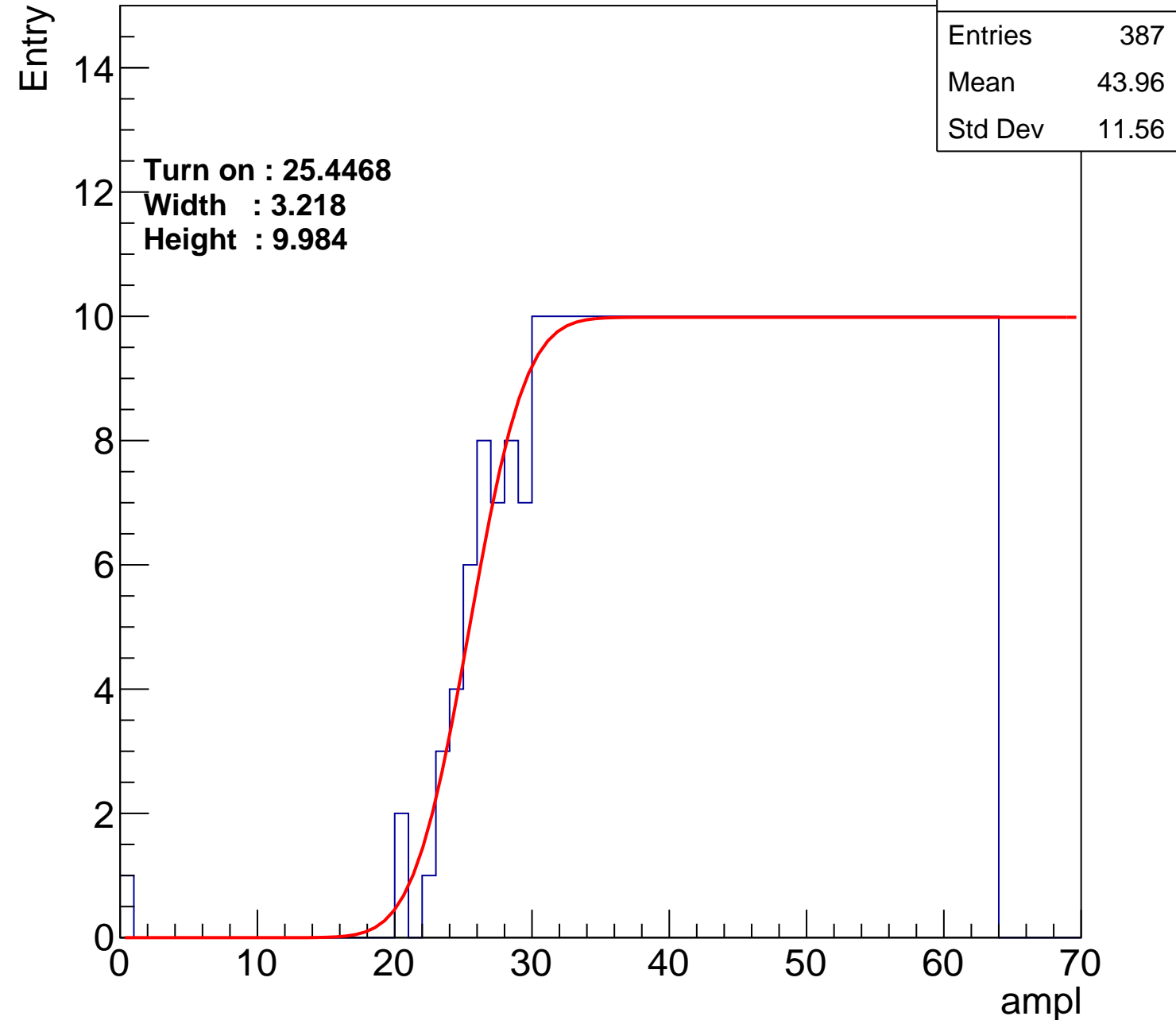
Width : 3.218

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch65

calib_packv5_042523_0143.root, FC#5, port B1

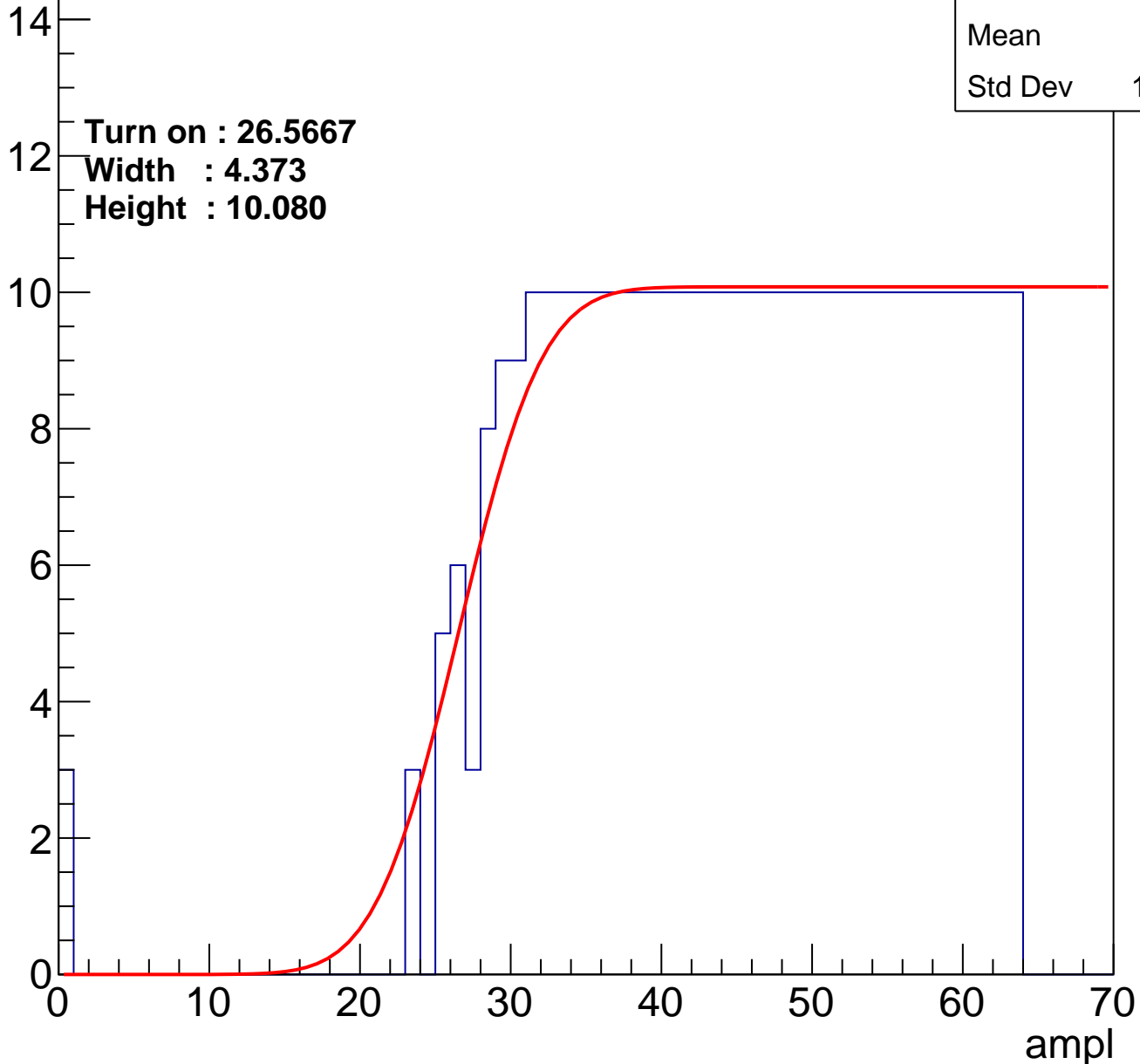
Entries	376
Mean	44.4
Std Dev	11.58

Turn on : 26.5667

Width : 4.373

Height : 10.080

Entry



B0L000S, U24-ch66

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.71
Std Dev	11.48

Turn on : 28.1638

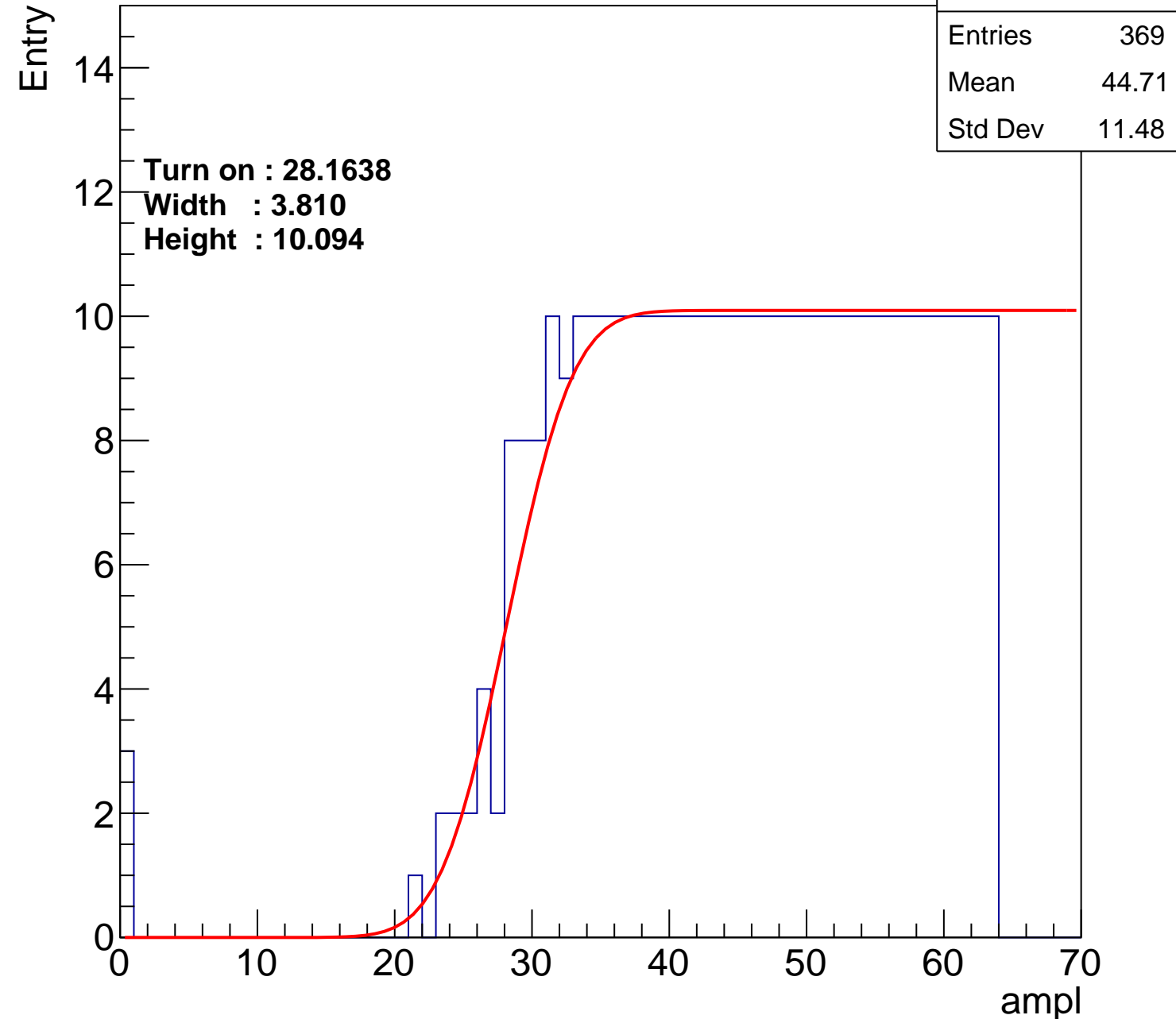
Width : 3.810

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.43
Std Dev	11.77

Turn on : 27.3725

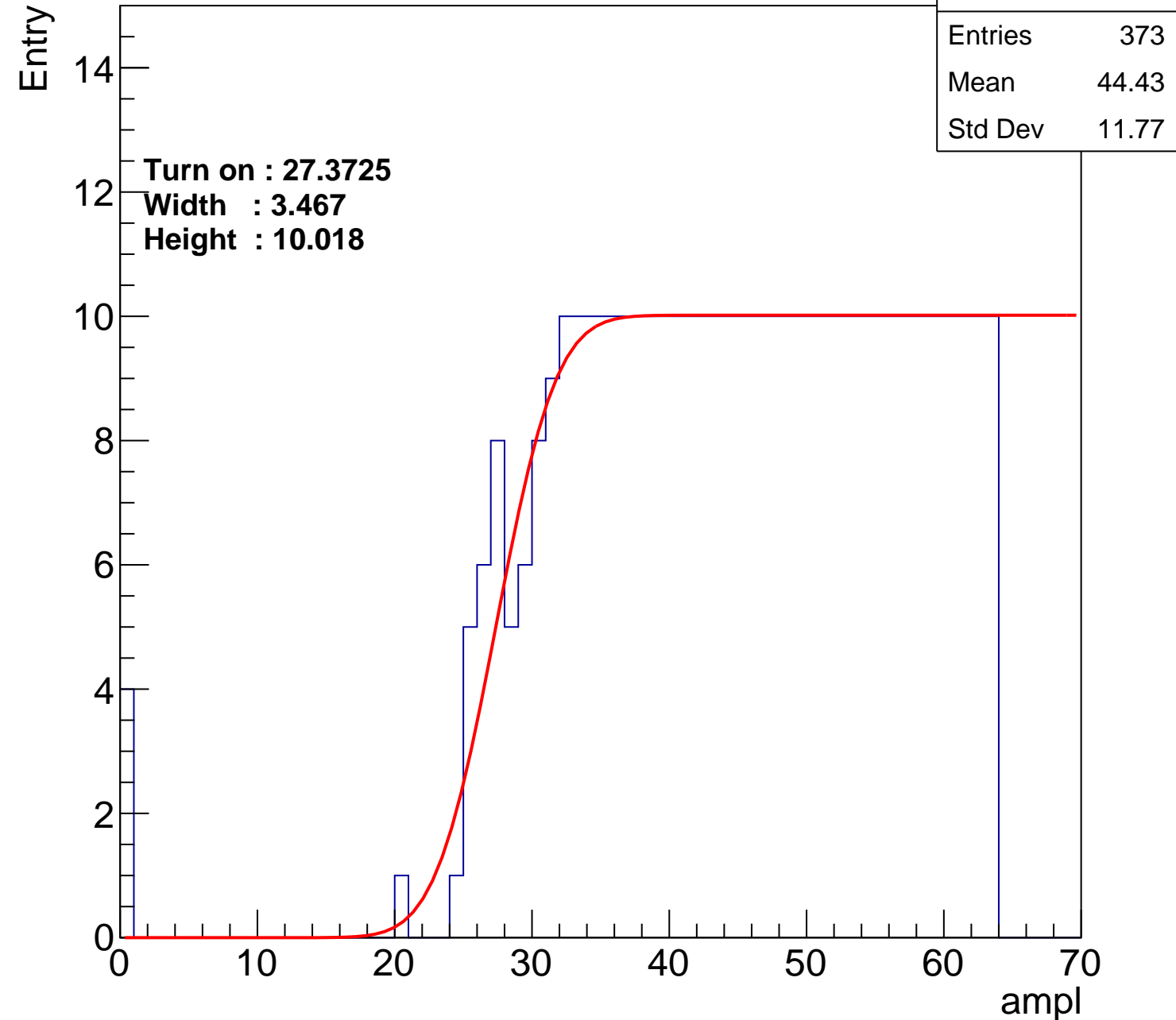
Width : 3.467

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.45
Std Dev	11.56

Turn on : 27.0439

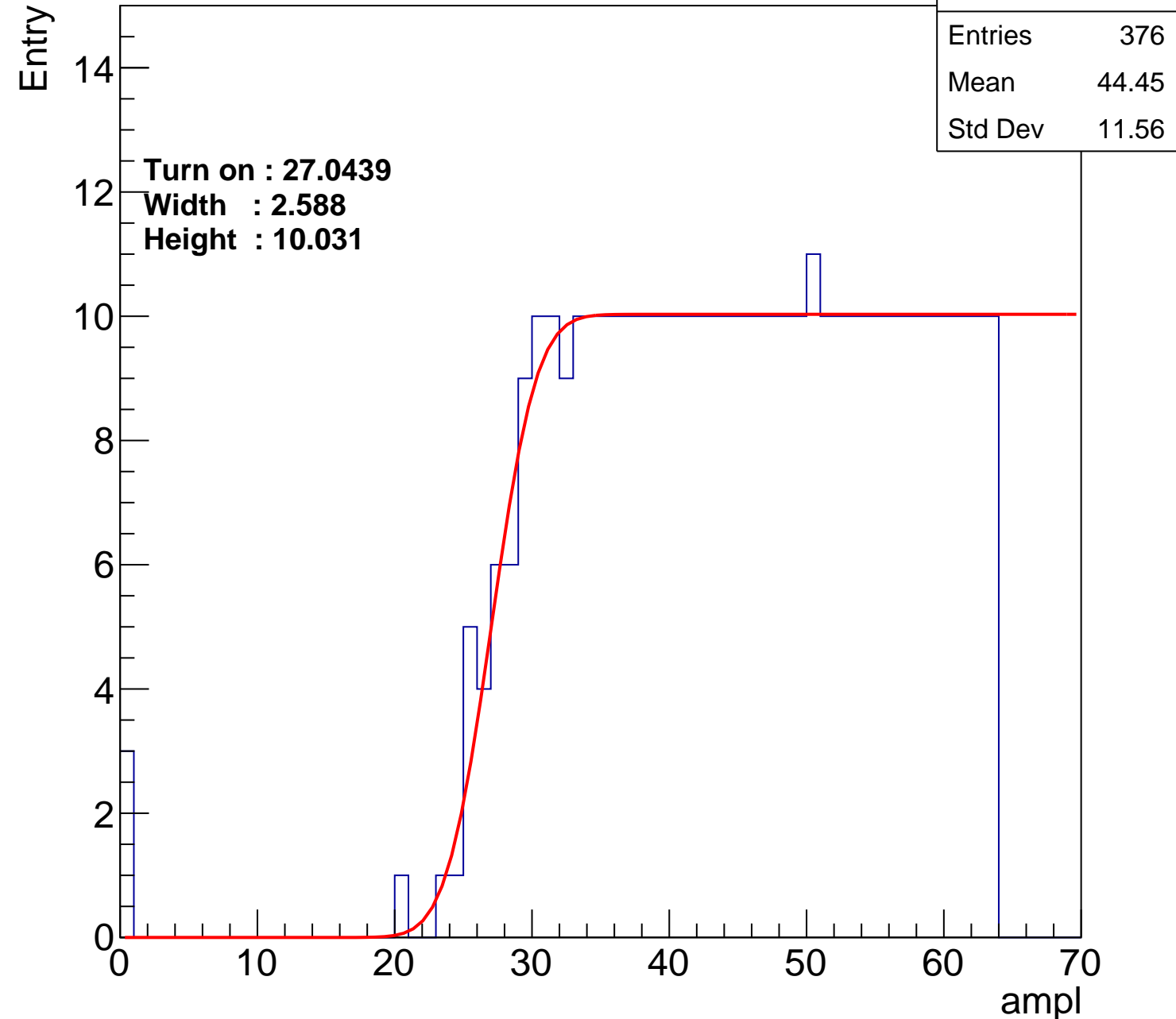
Width : 2.588

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch69

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.37
Std Dev	11.59

Turn on : 26.6667

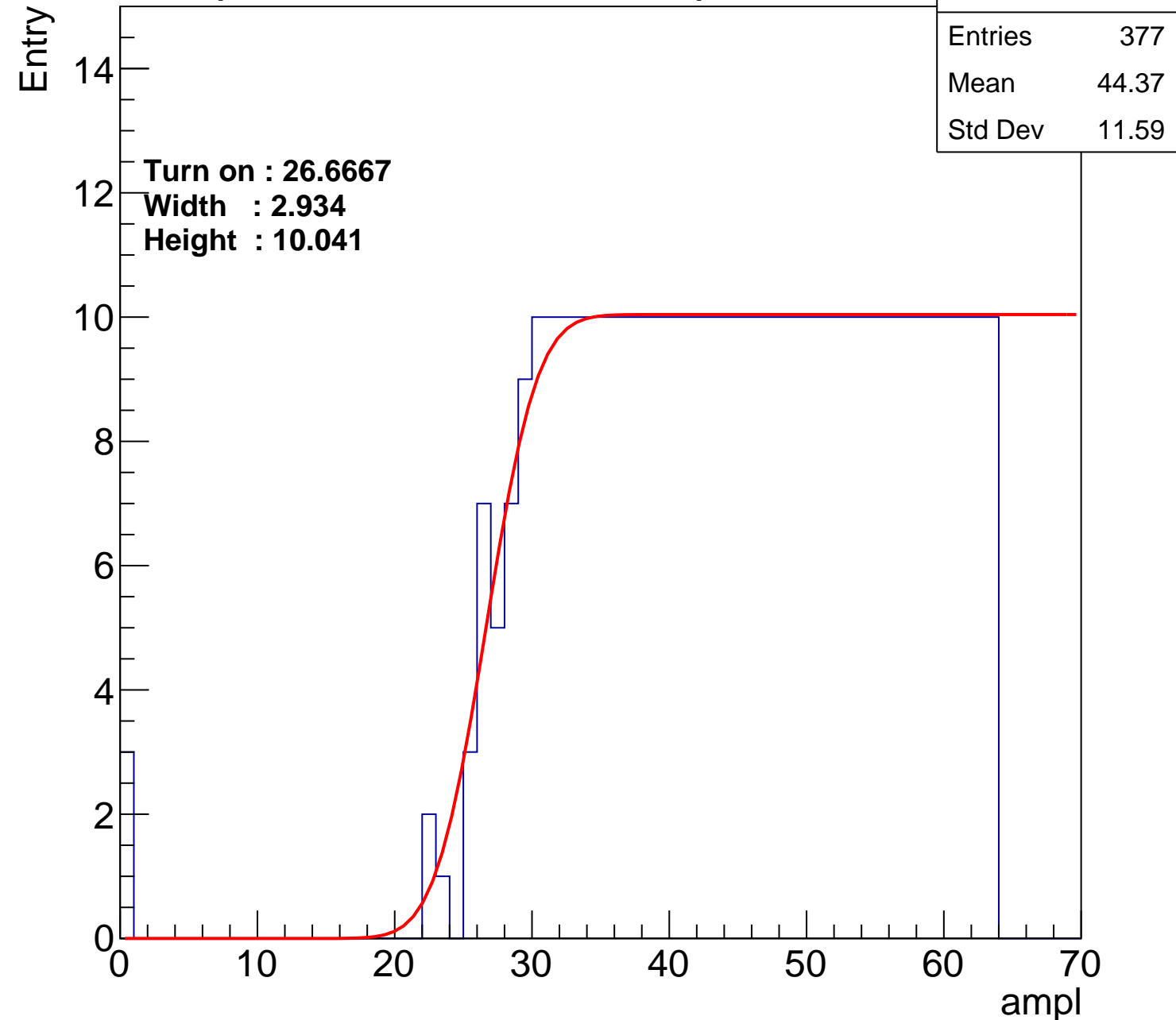
Width : 2.934

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.31
Std Dev	11.92

Turn on : 27.0775

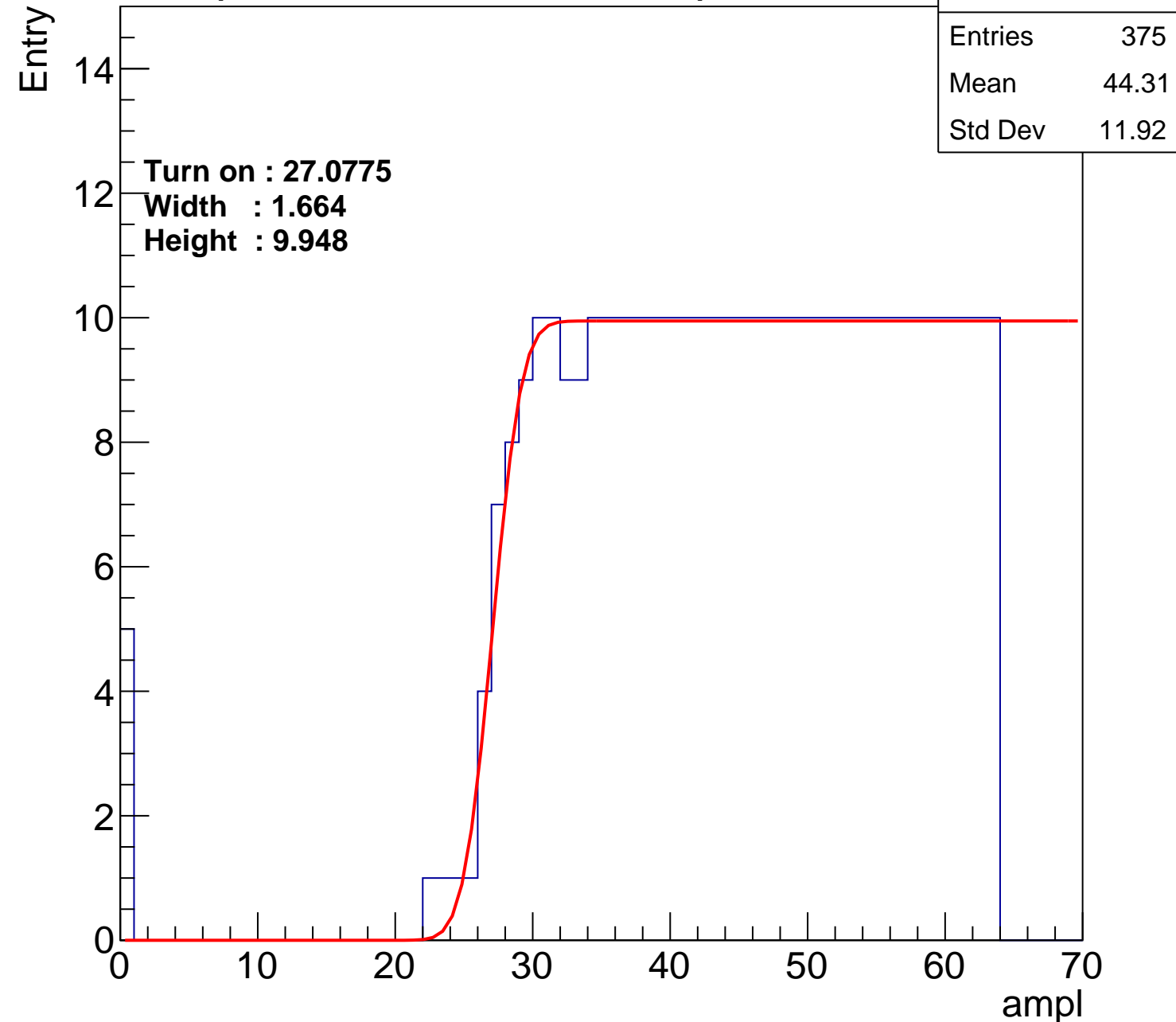
Width : 1.664

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.71
Std Dev	12.05

Turn on : 25.5112

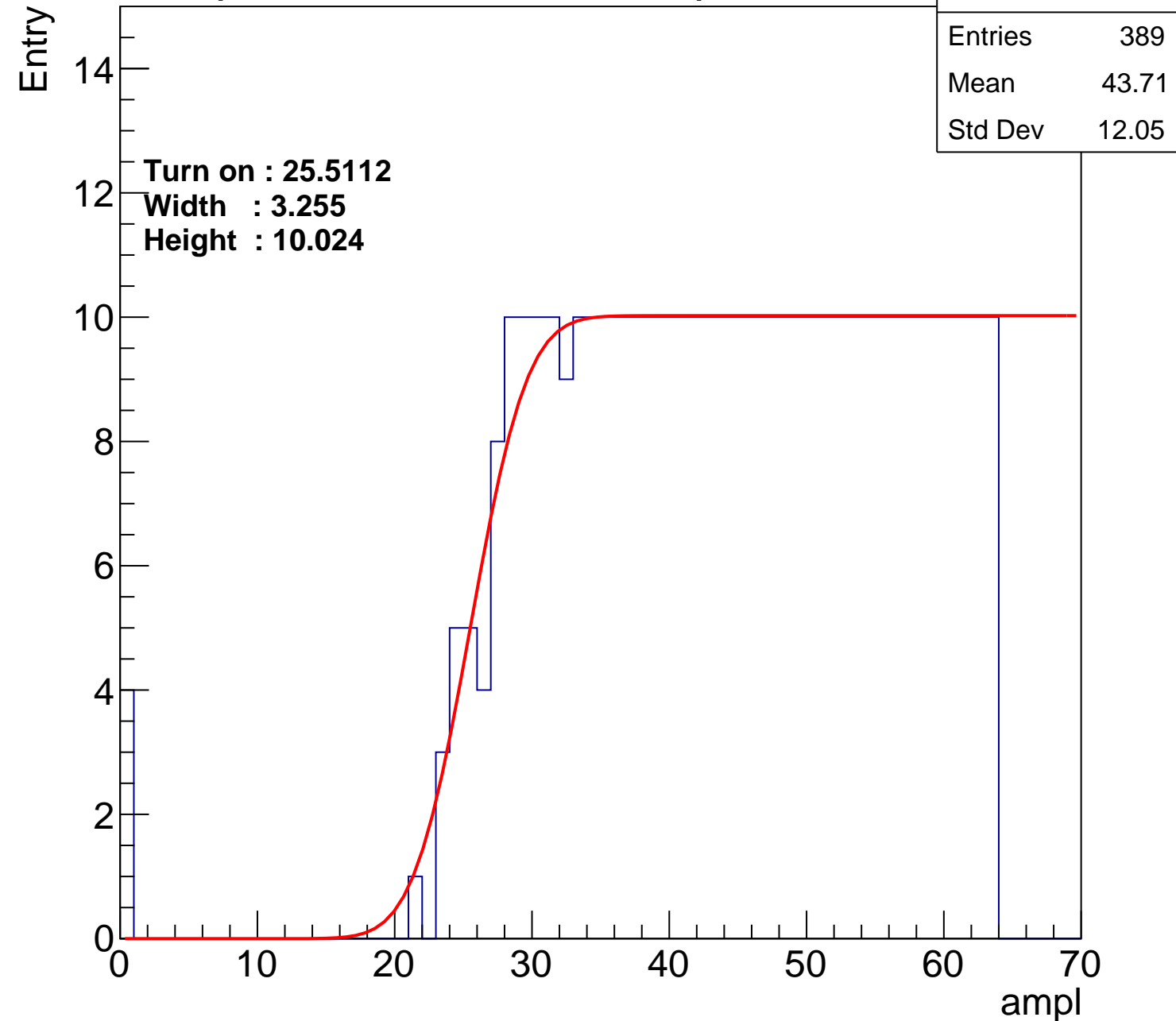
Width : 3.255

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 26.9670

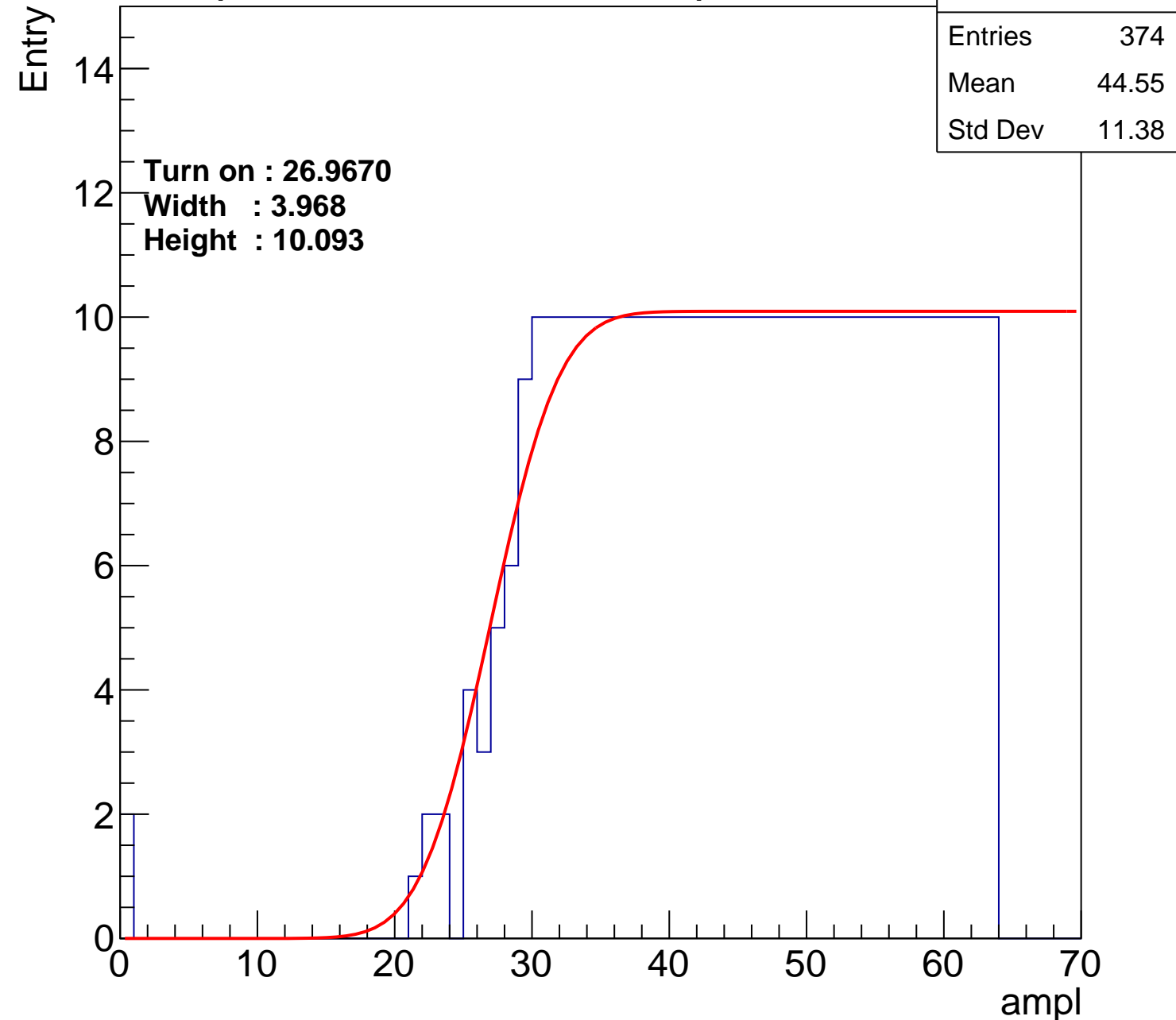
Width : 3.968

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.47
Std Dev	10.75

Turn on : 28.4471

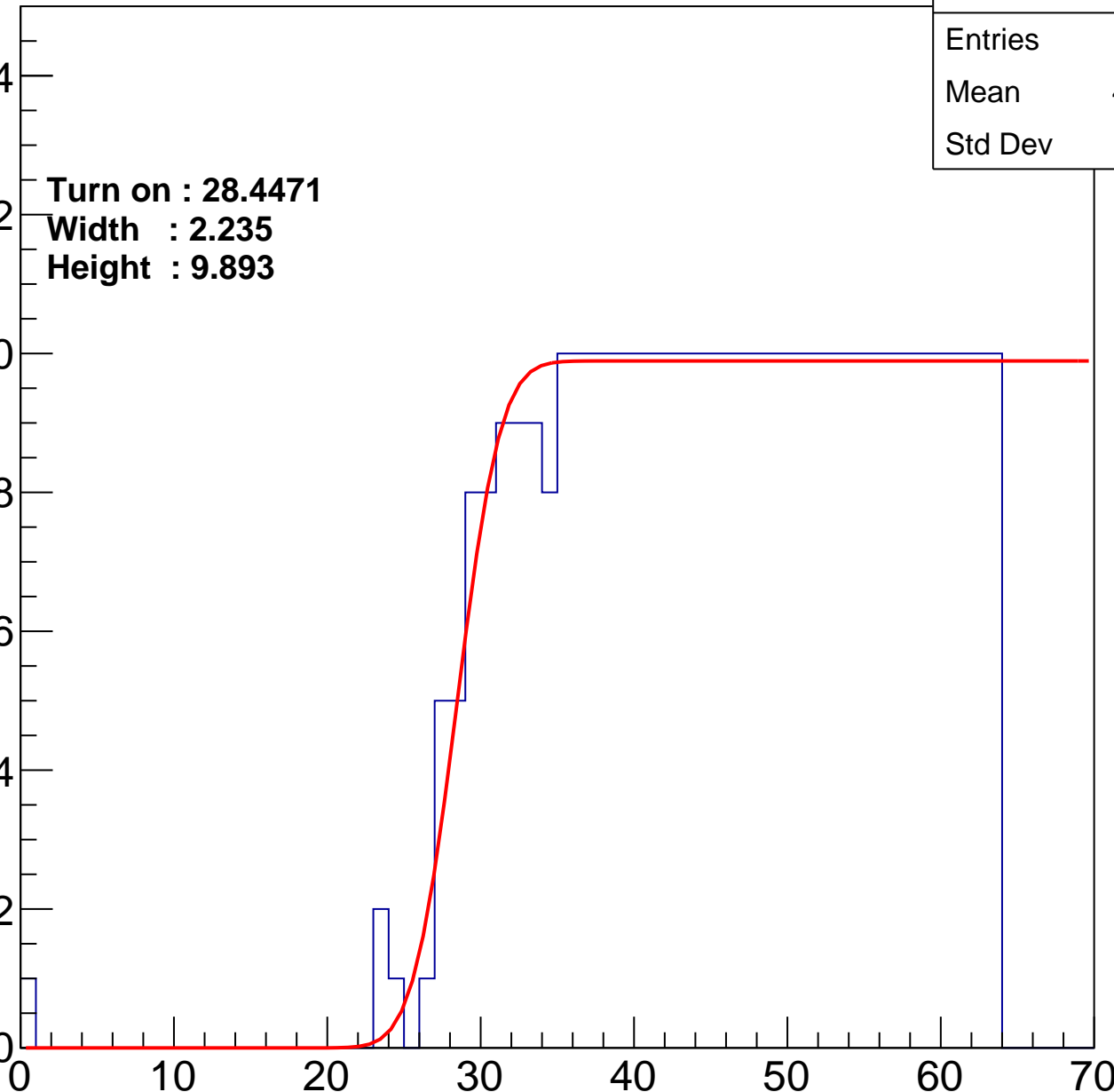
Width : 2.235

Height : 9.893

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.65
Std Dev	11.45

Turn on : 27.1159

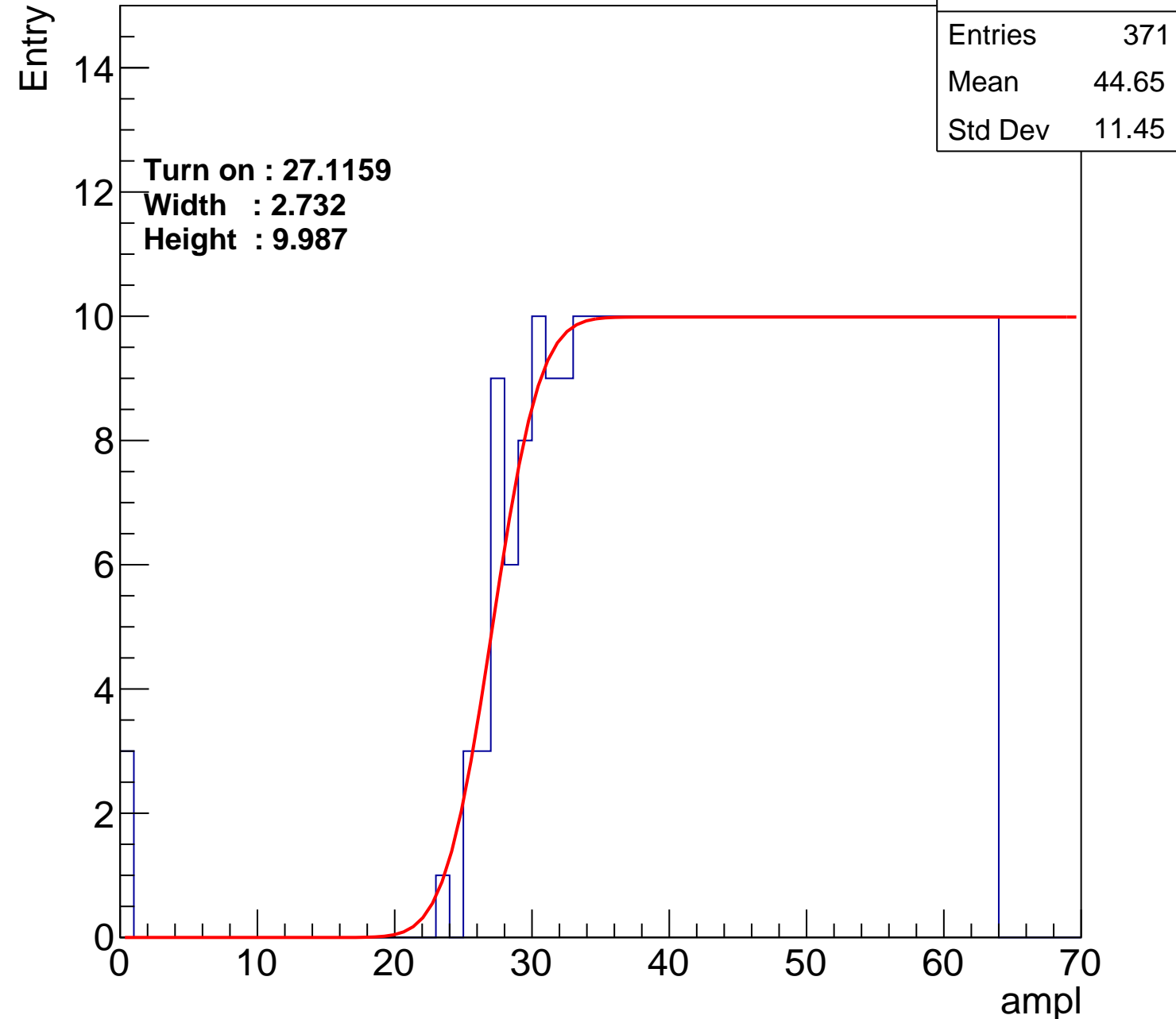
Width : 2.732

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.27
Std Dev	10.98

Turn on : 28.4486

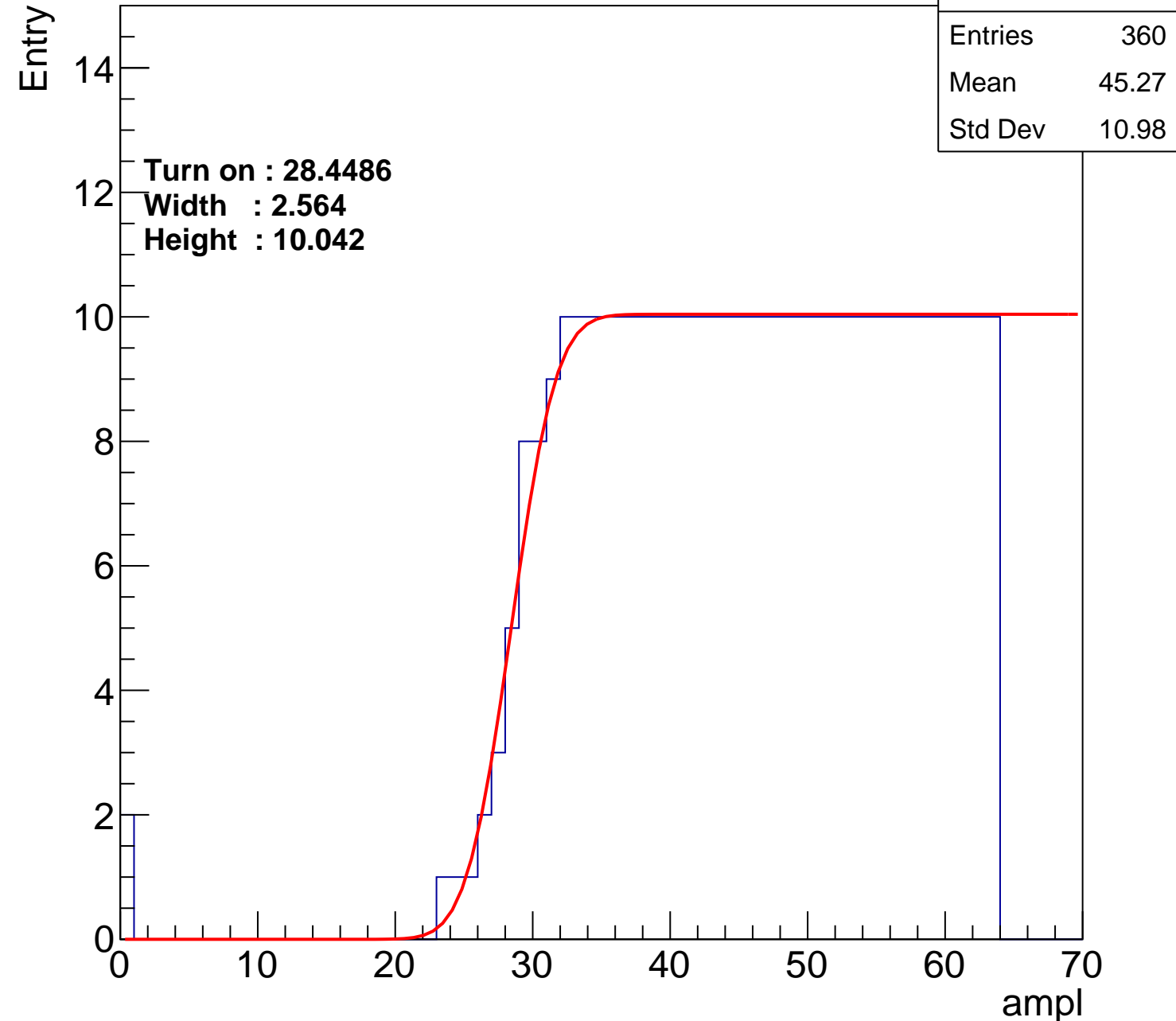
Width : 2.564

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch76

calib_packv5_042523_0143.root, FC#5, port B1

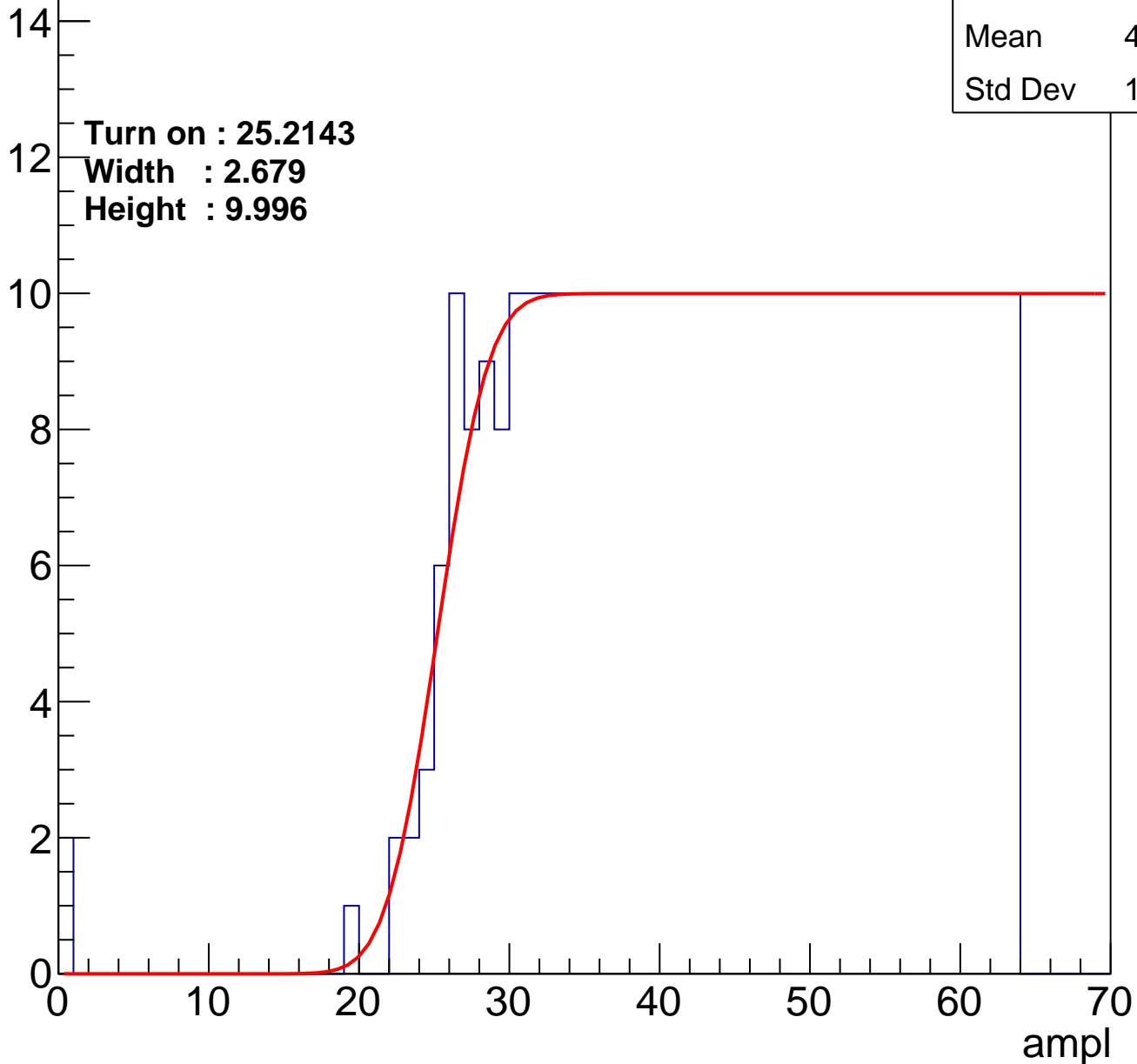
Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.2143

Width : 2.679

Height : 9.996

Entry



B0L000S, U24-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	351
Mean	45.56
Std Dev	11.08

Turn on : 29.5297

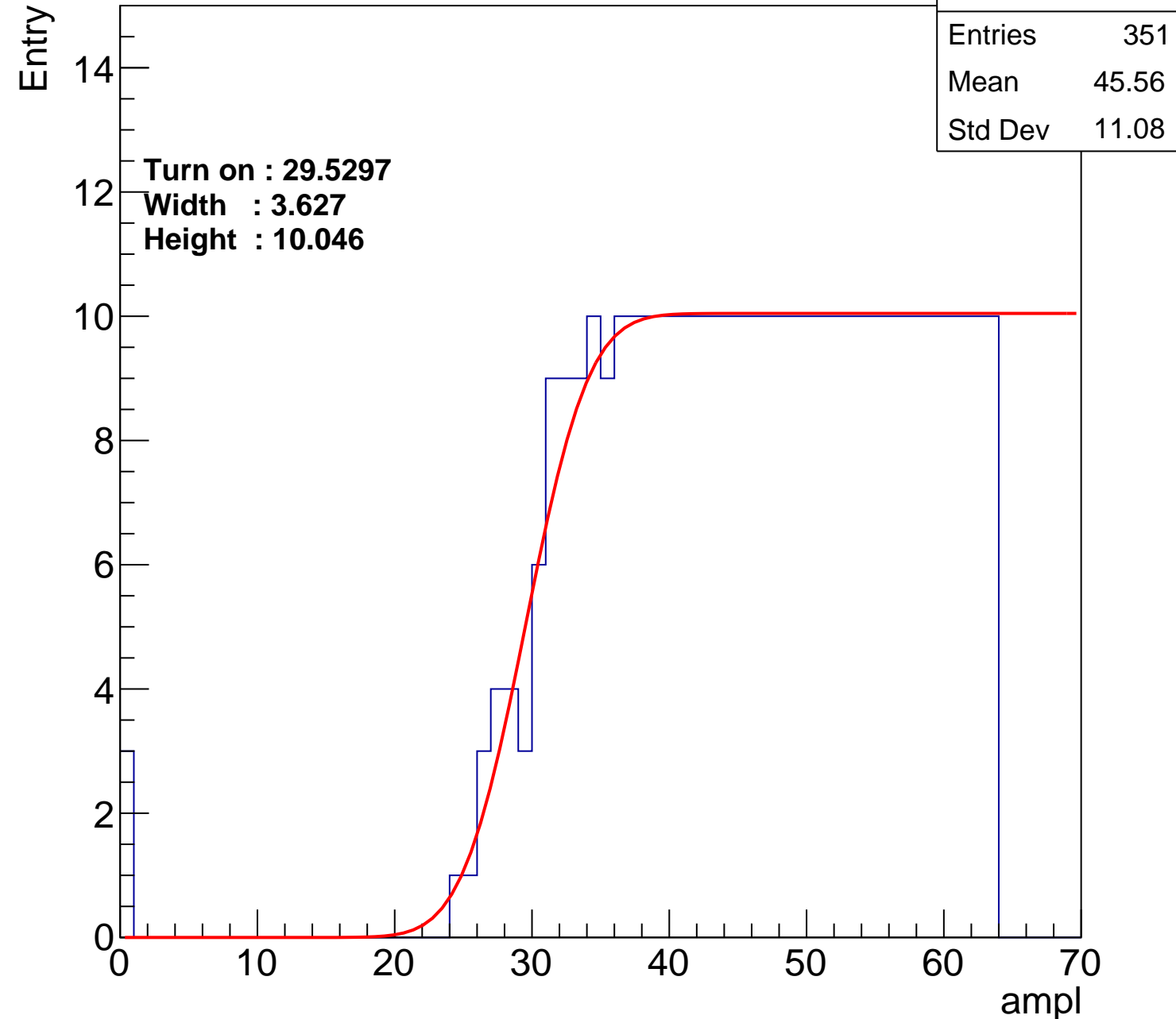
Width : 3.627

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch78

calib_packv5_042523_0143.root, FC#5, port B1

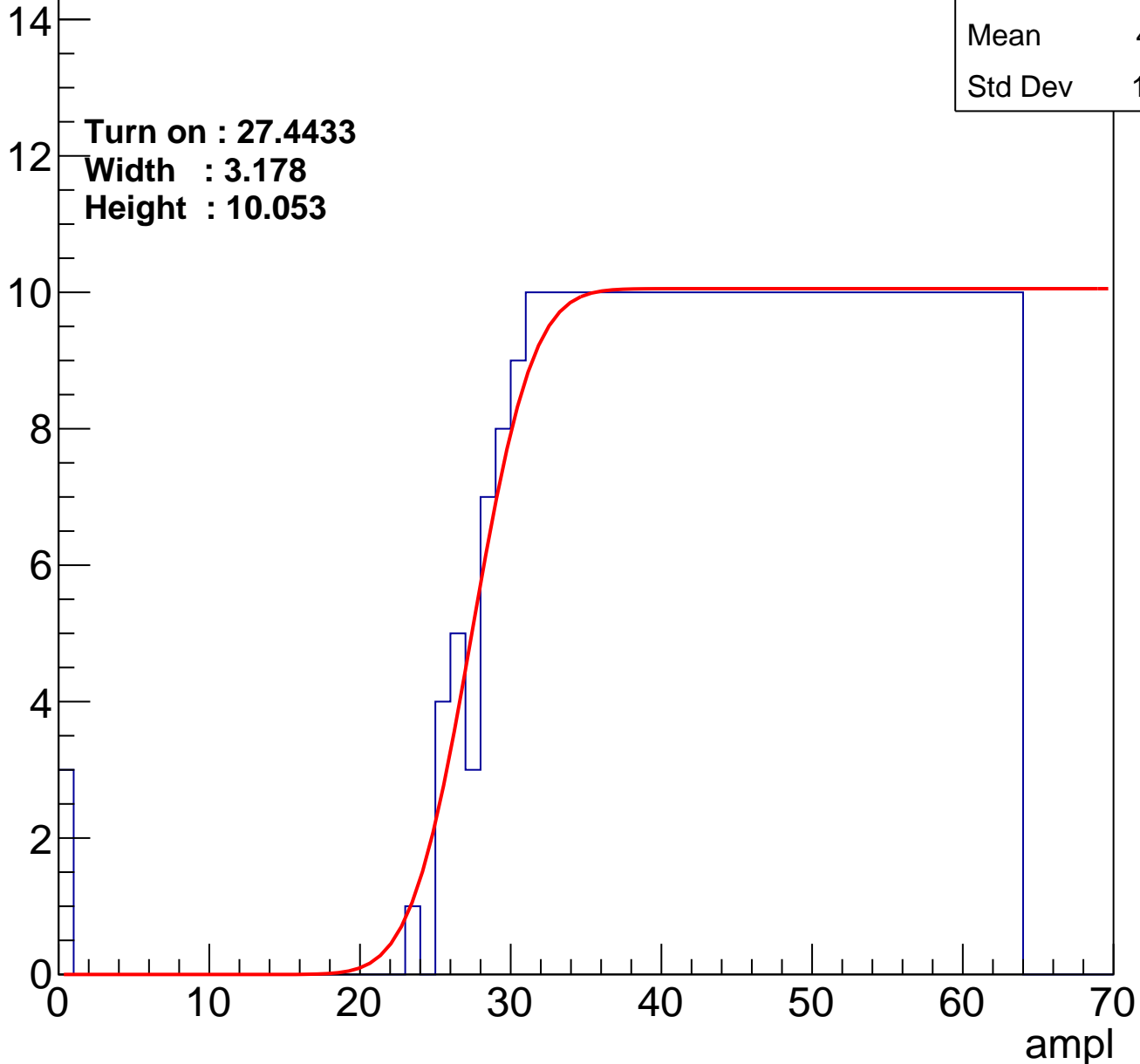
Entries	370
Mean	44.71
Std Dev	11.42

Turn on : 27.4433

Width : 3.178

Height : 10.053

Entry



B0L000S, U24-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.39
Std Dev	10.99

Turn on : 28.8582

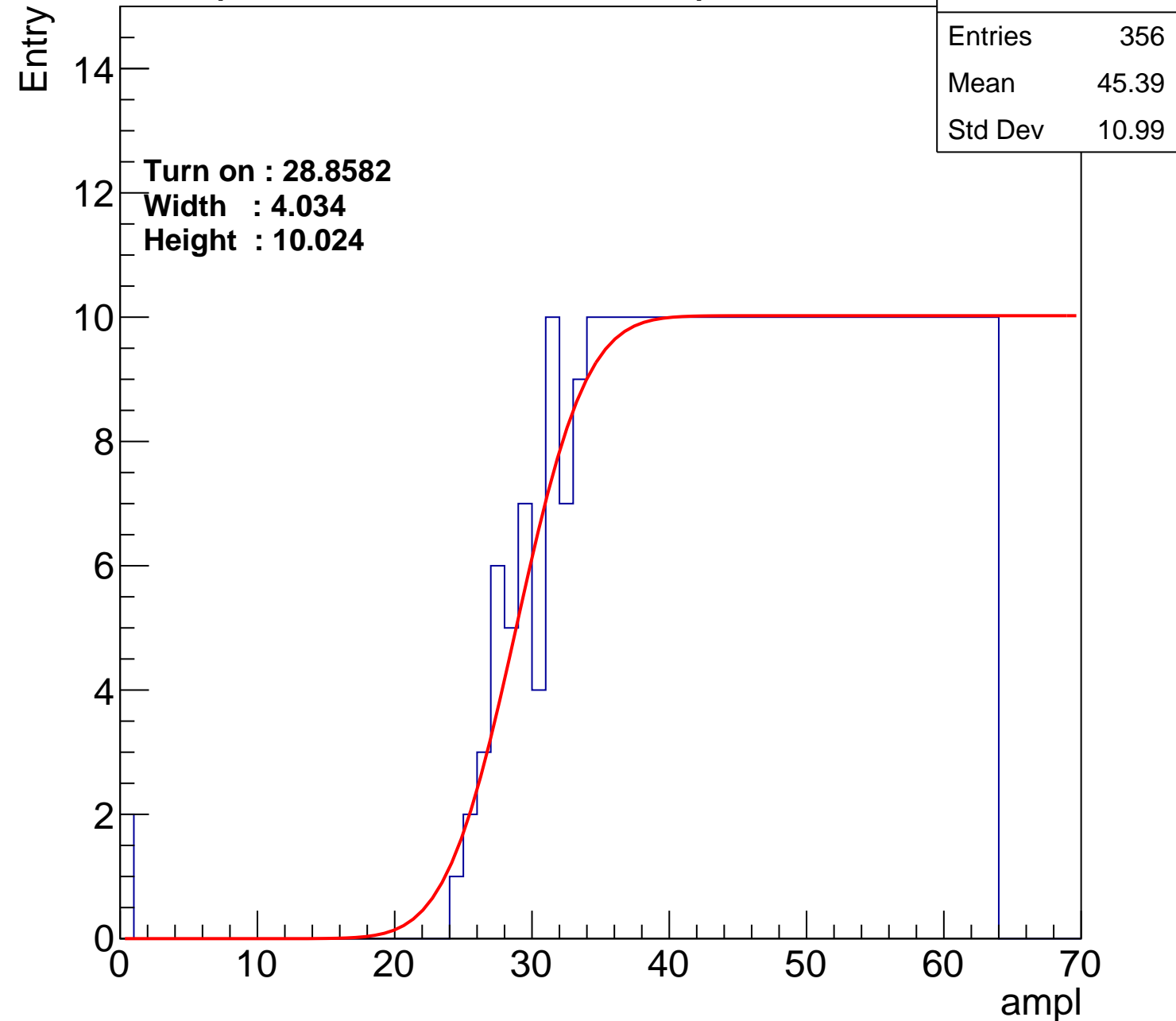
Width : 4.034

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch80

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.96
Std Dev	10.95

Turn on : 27.5726

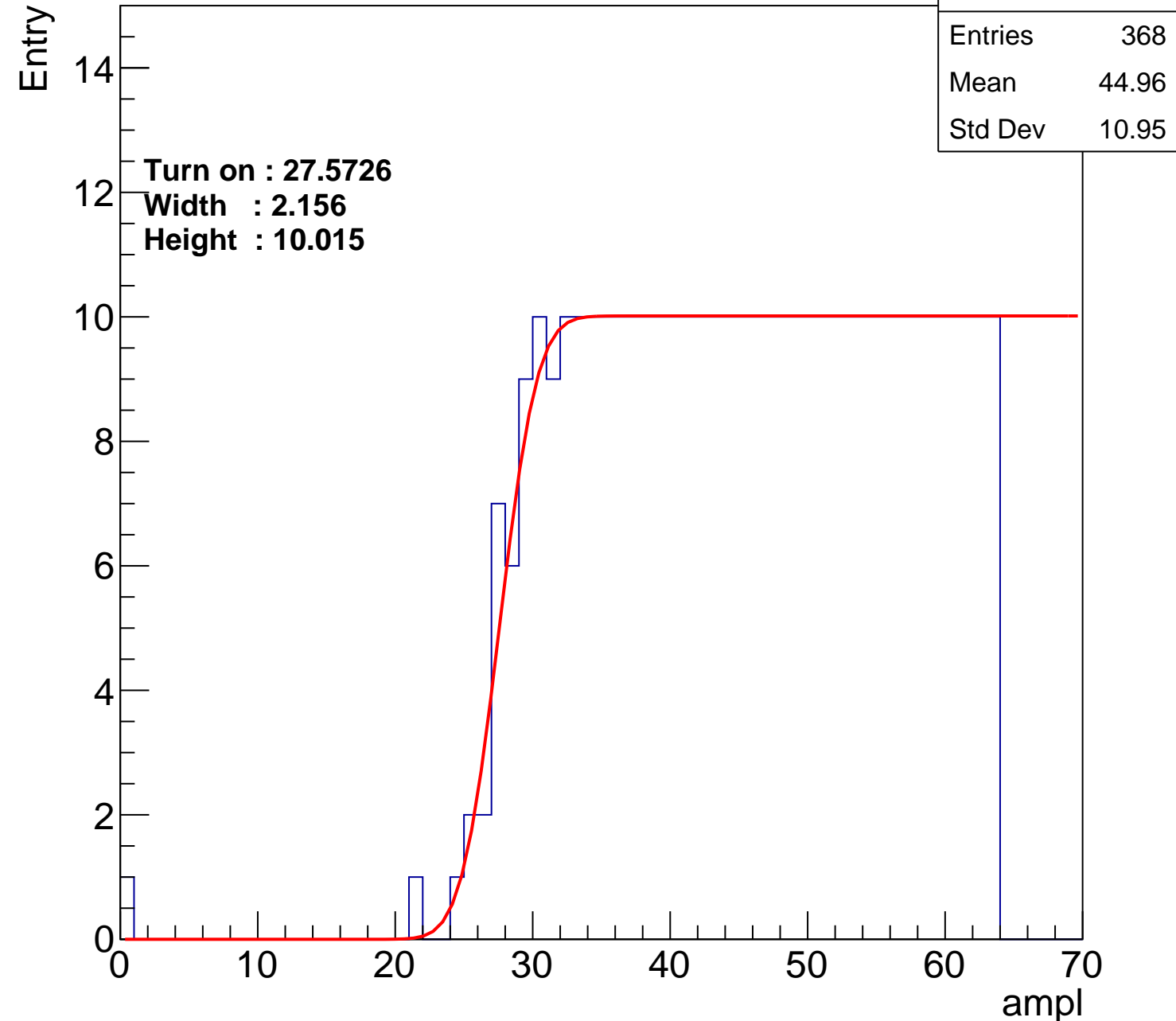
Width : 2.156

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch81

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.83
Std Dev	11.25

Turn on : 27.3241

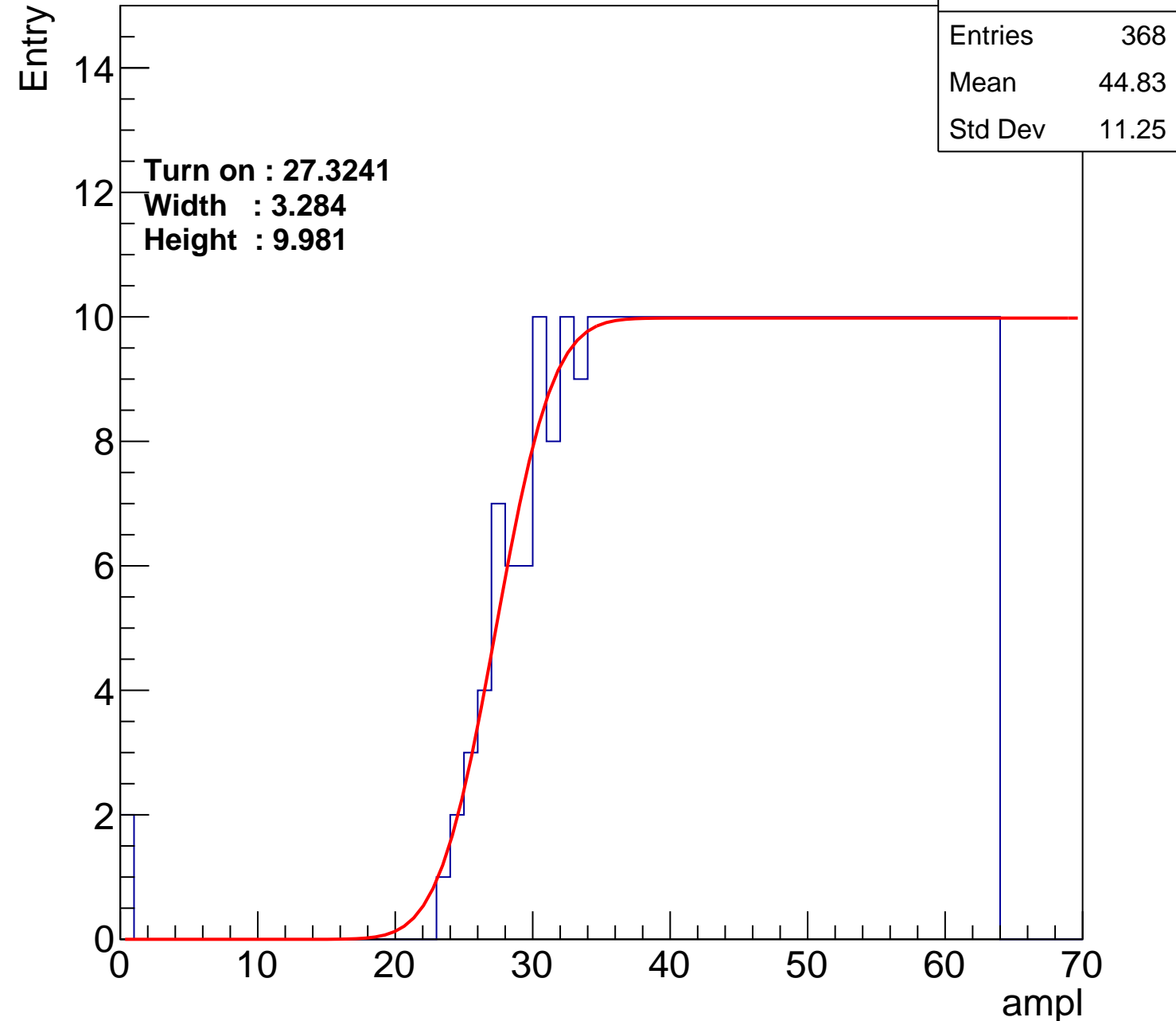
Width : 3.284

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 26.9970
Width : 3.208
Height : 10.037



B0L000S, U24-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.62
Std Dev	10.78

Turn on : 29.0314

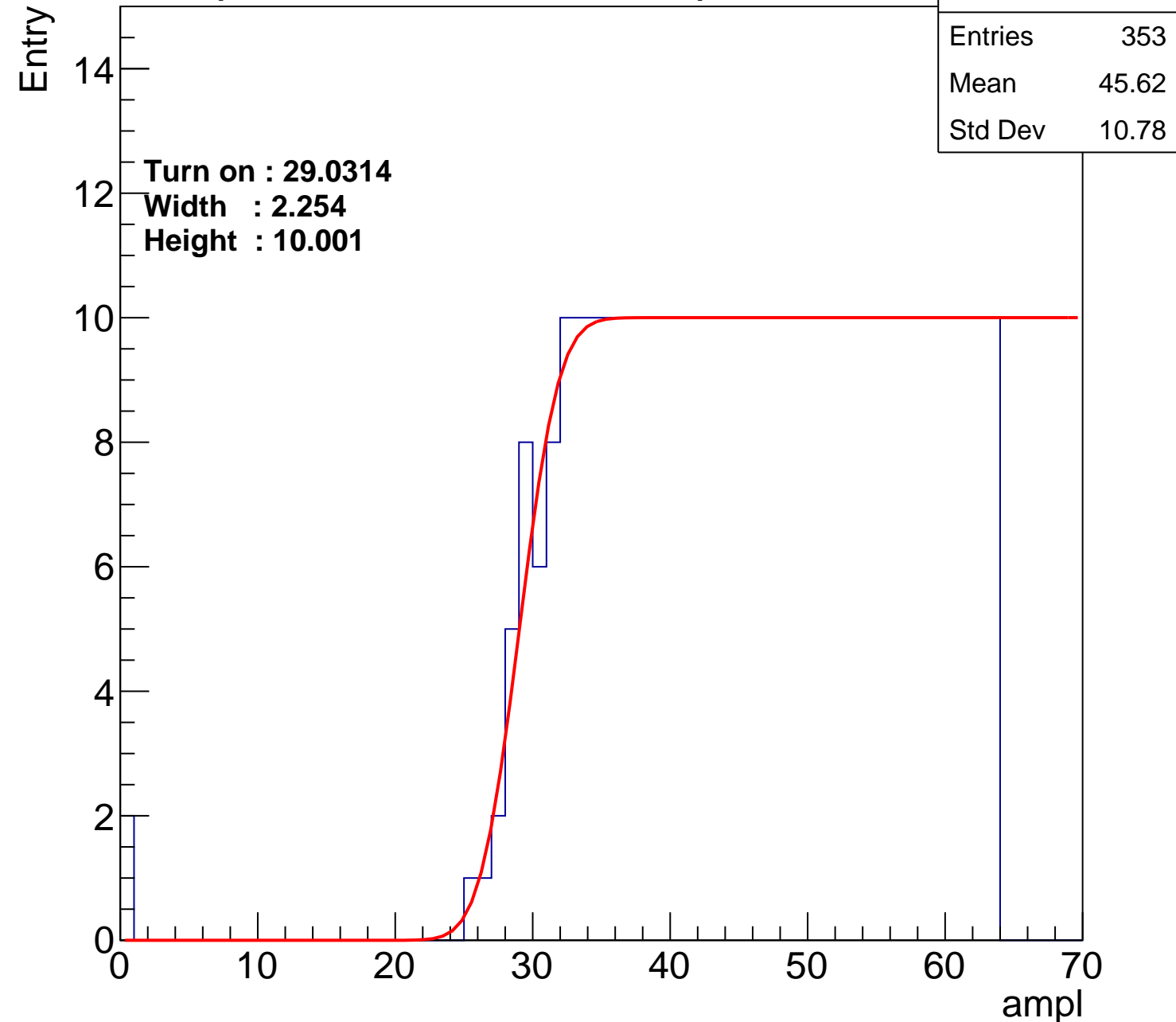
Width : 2.254

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.3200

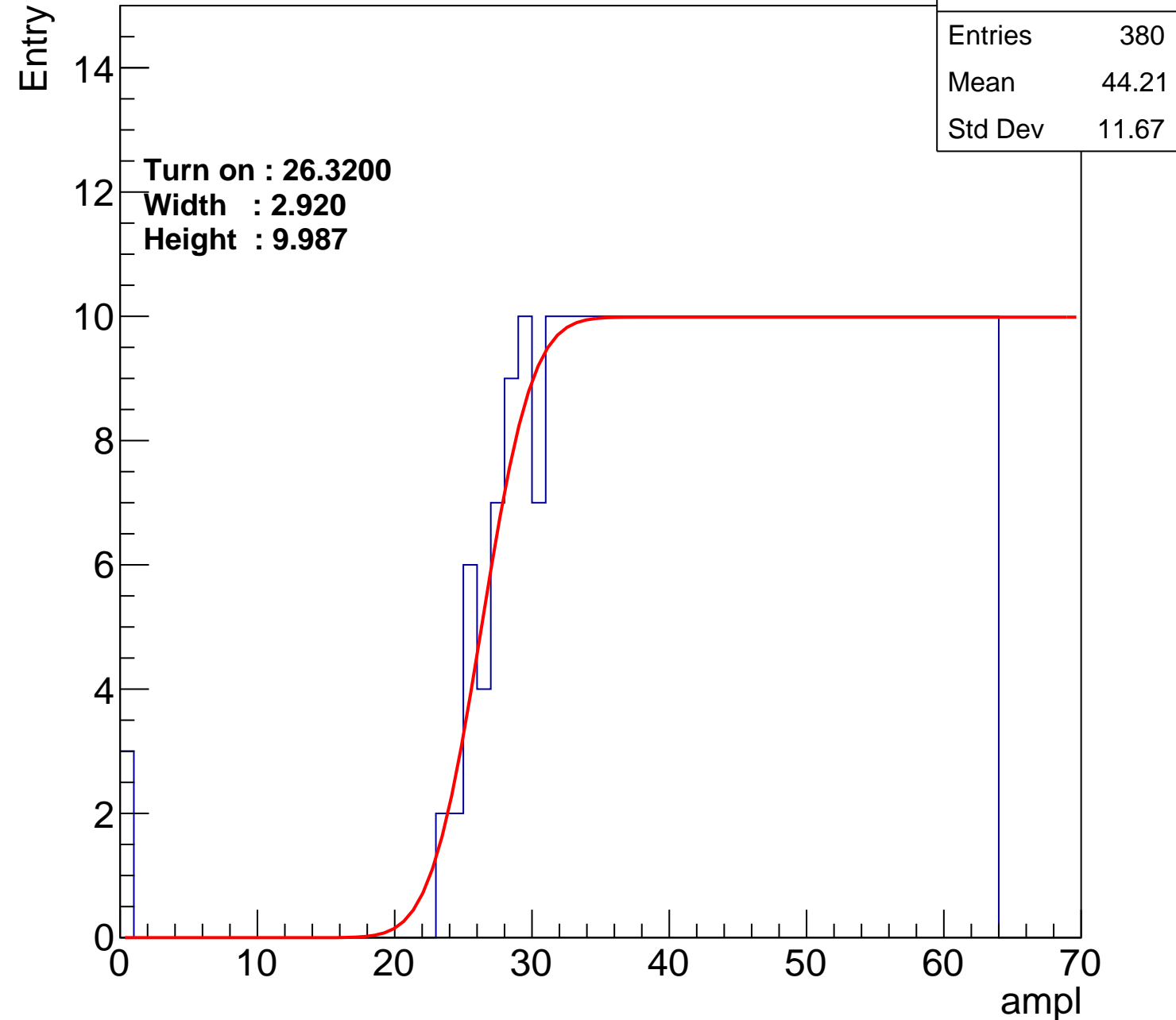
Width : 2.920

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.19
Std Dev	11.45

Turn on : 26.0299

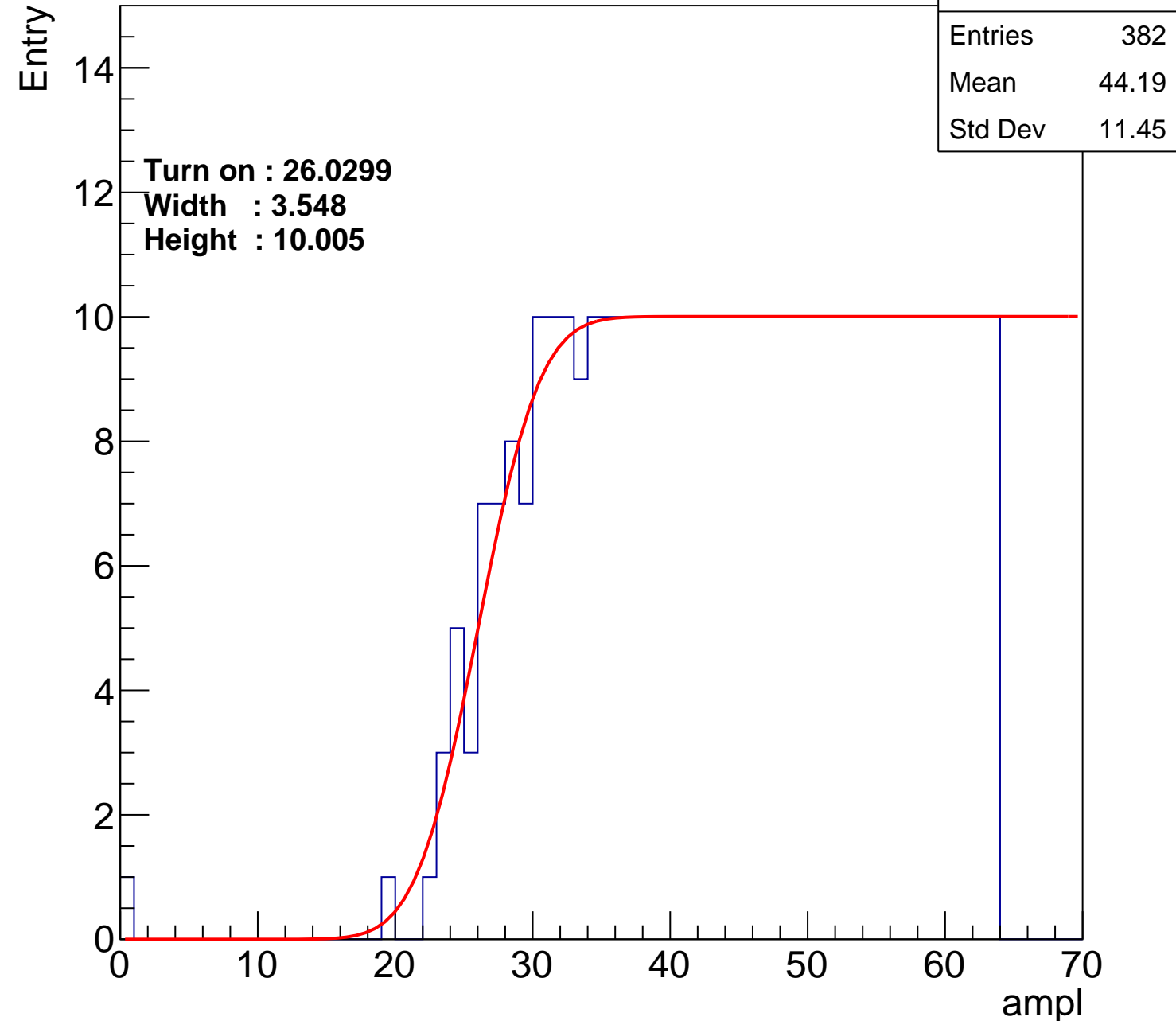
Width : 3.548

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch86

calib_packv5_042523_0143.root, FC#5, port B1

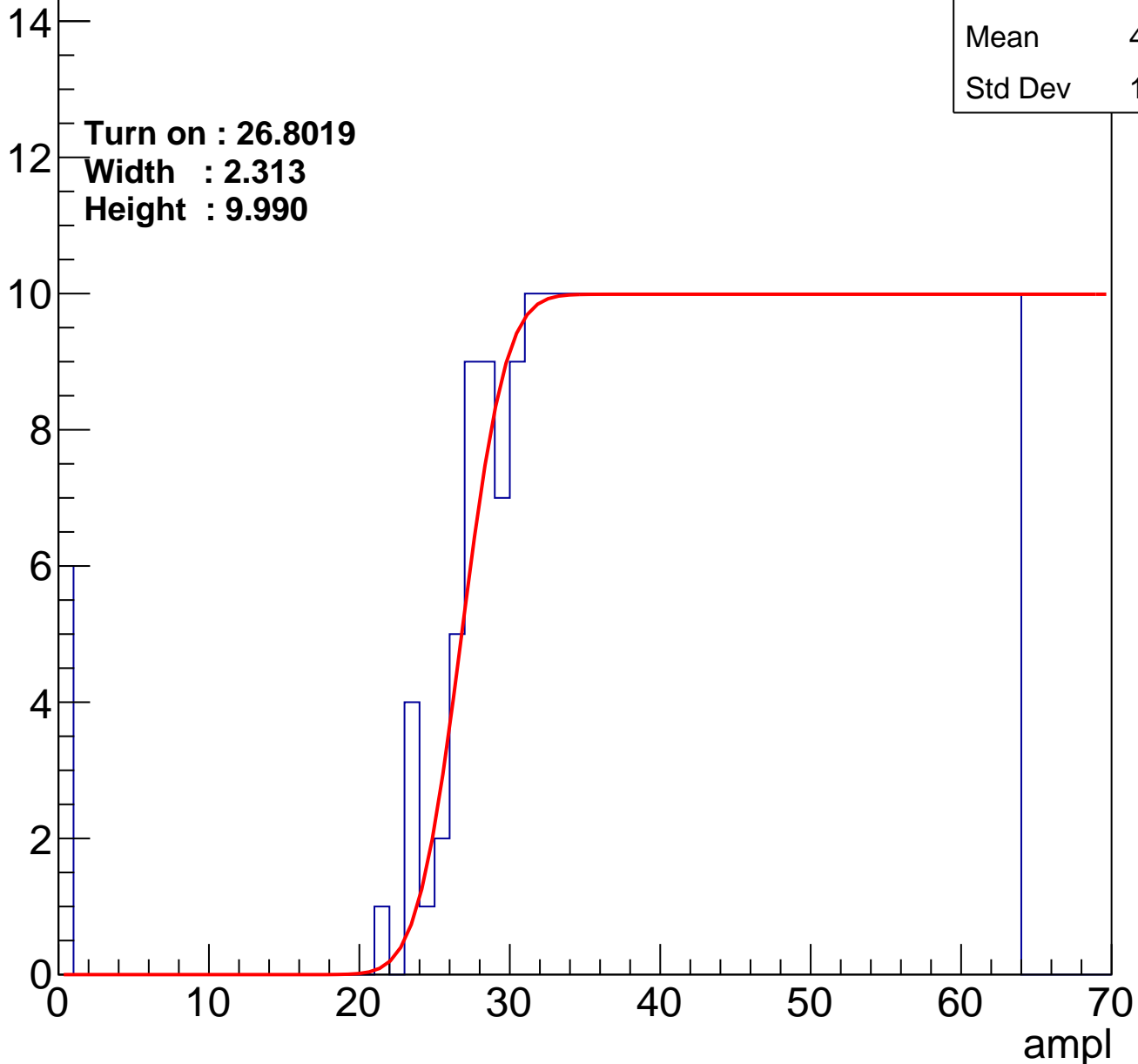
Entries	383
Mean	43.85
Std Dev	12.28

Turn on : 26.8019

Width : 2.313

Height : 9.990

Entry



B0L000S, U24-ch87

calib_packv5_042523_0143.root, FC#5, port B1

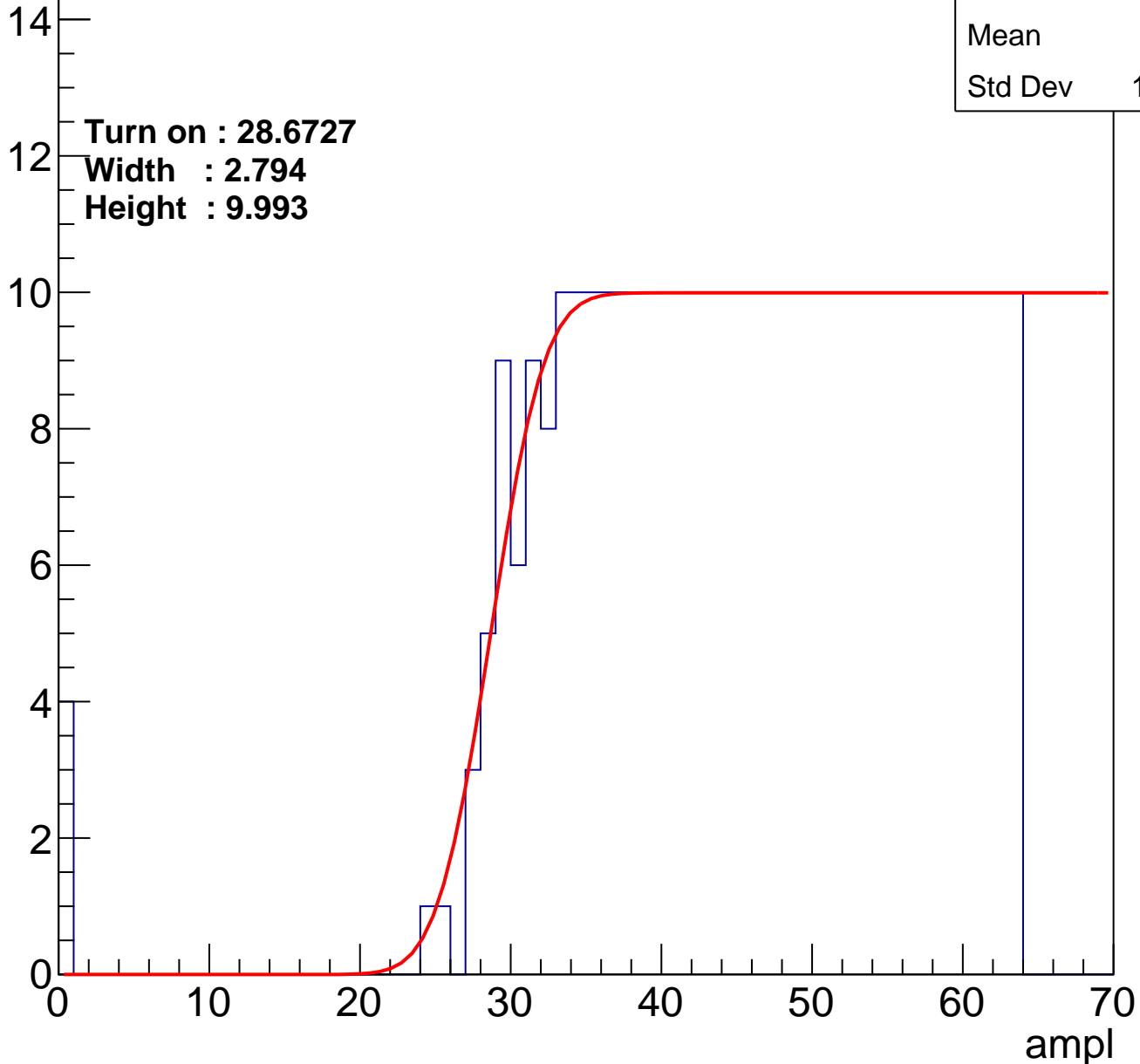
Entries	356
Mean	45.3
Std Dev	11.33

Turn on : 28.6727

Width : 2.794

Height : 9.993

Entry



B0L000S, U24-ch88

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.88
Std Dev	11.09

Turn on : 27.7984

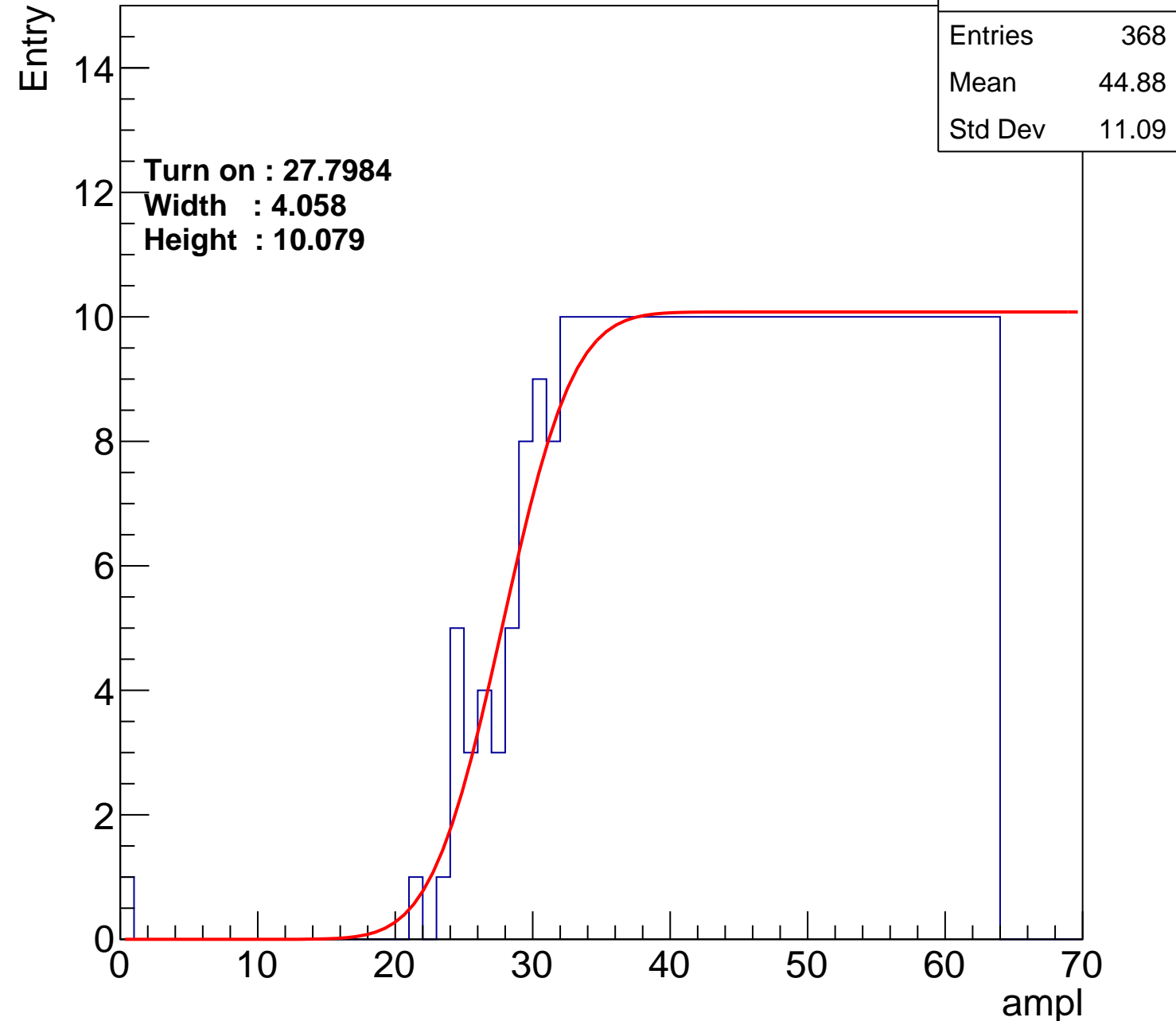
Width : 4.058

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.29
Std Dev	11.7

Turn on : 27.1477

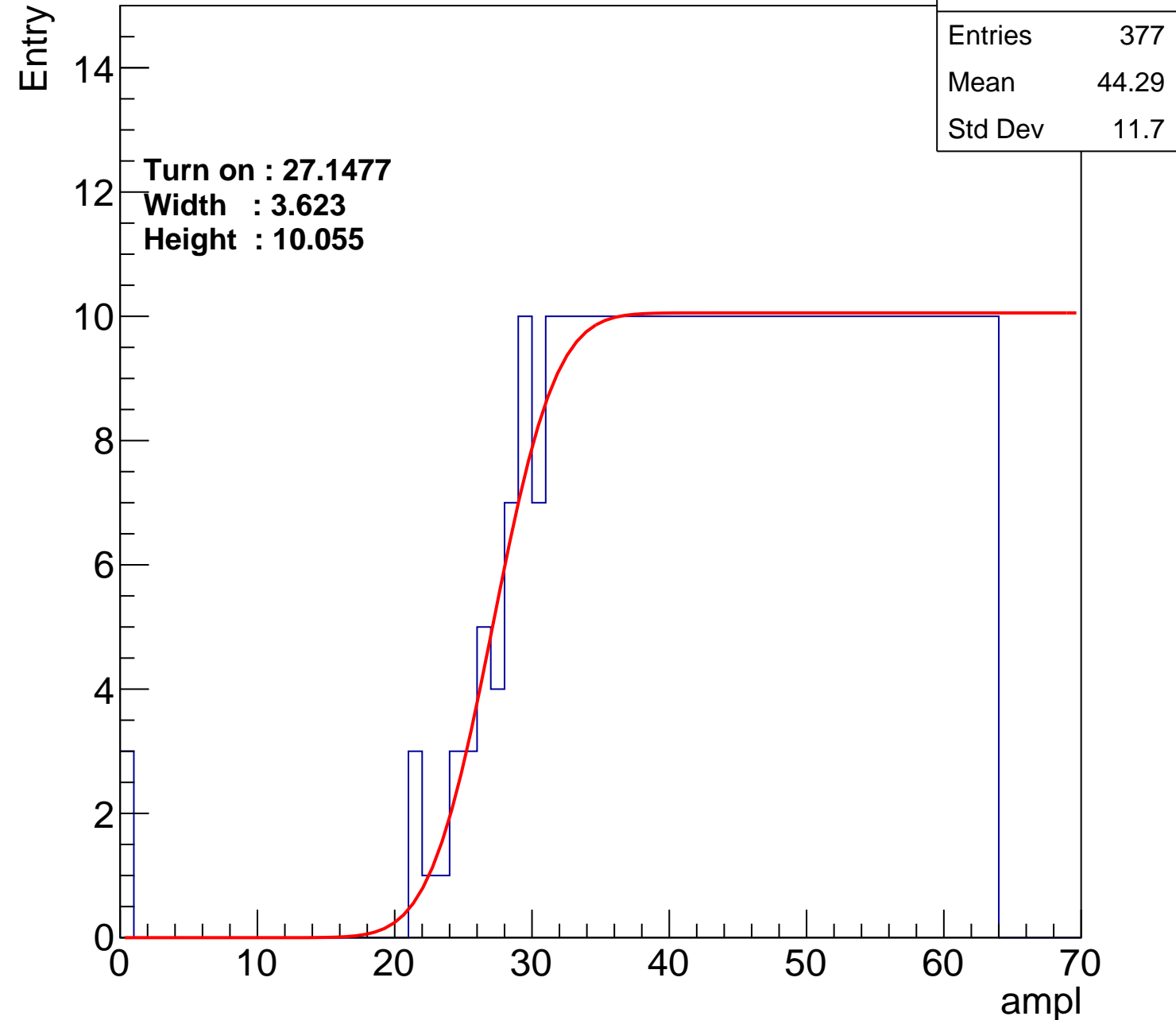
Width : 3.623

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch90

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.24
Std Dev	11.02

Turn on : 28.3252

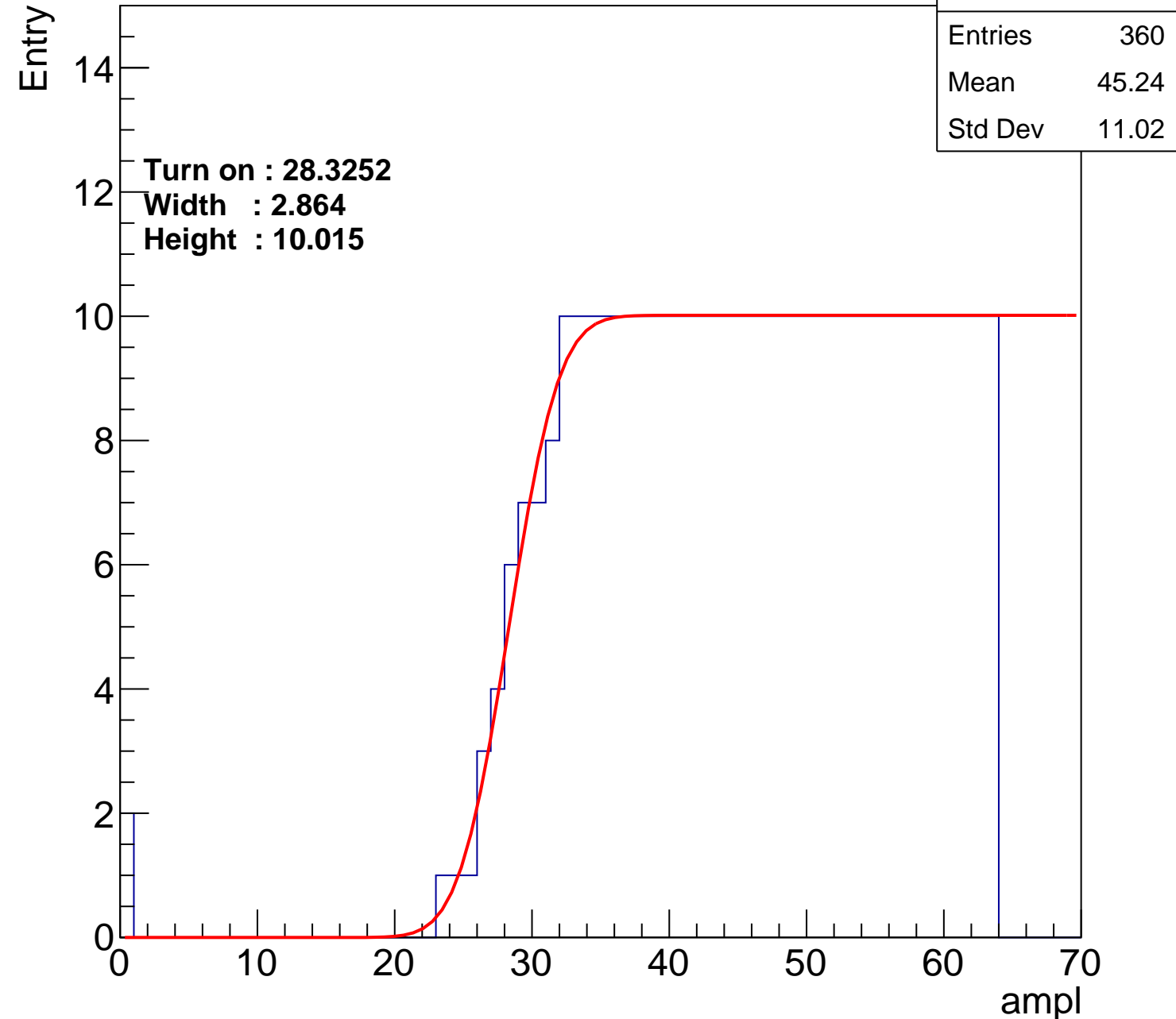
Width : 2.864

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch91

calib_packv5_042523_0143.root, FC#5, port B1

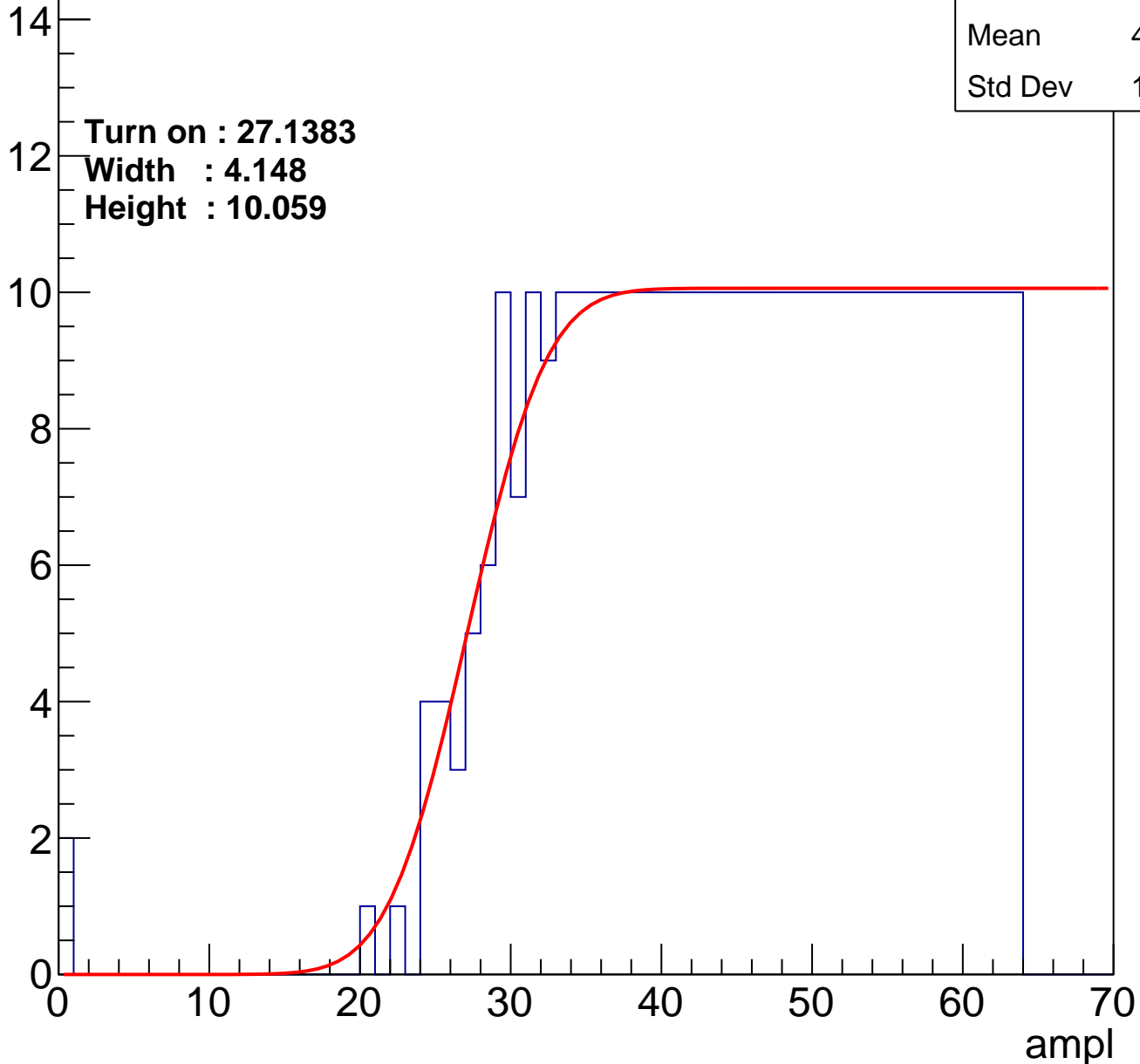
Entries	372
Mean	44.62
Std Dev	11.38

Turn on : 27.1383

Width : 4.148

Height : 10.059

Entry



B0L000S, U24-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.47
Std Dev	12.15

Turn on : 27.8617

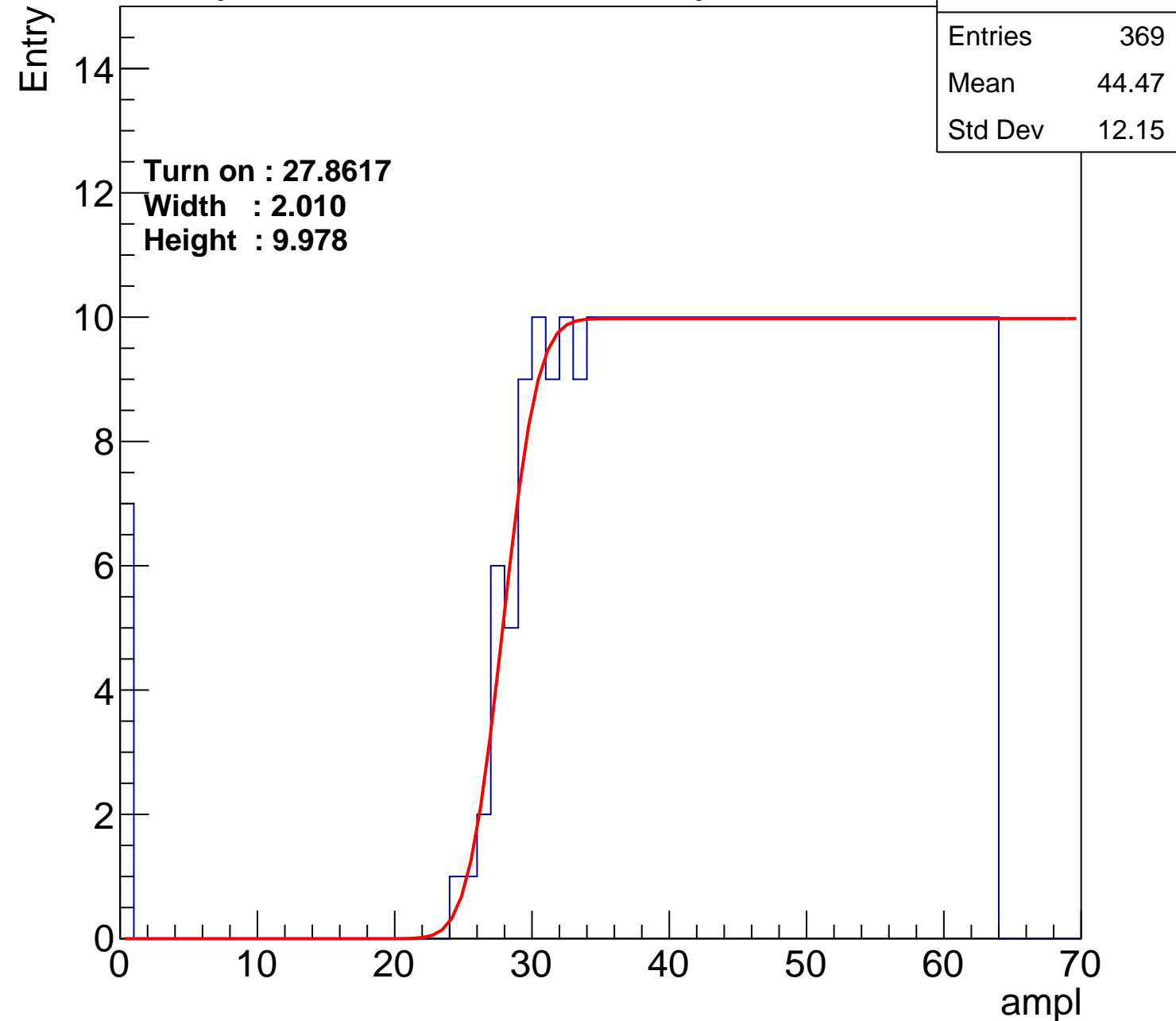
Width : 2.010

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.4573
Width : 3.540
Height : 10.014



B0L000S, U24-ch94

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.73
Std Dev	11.57

Turn on : 27.5502

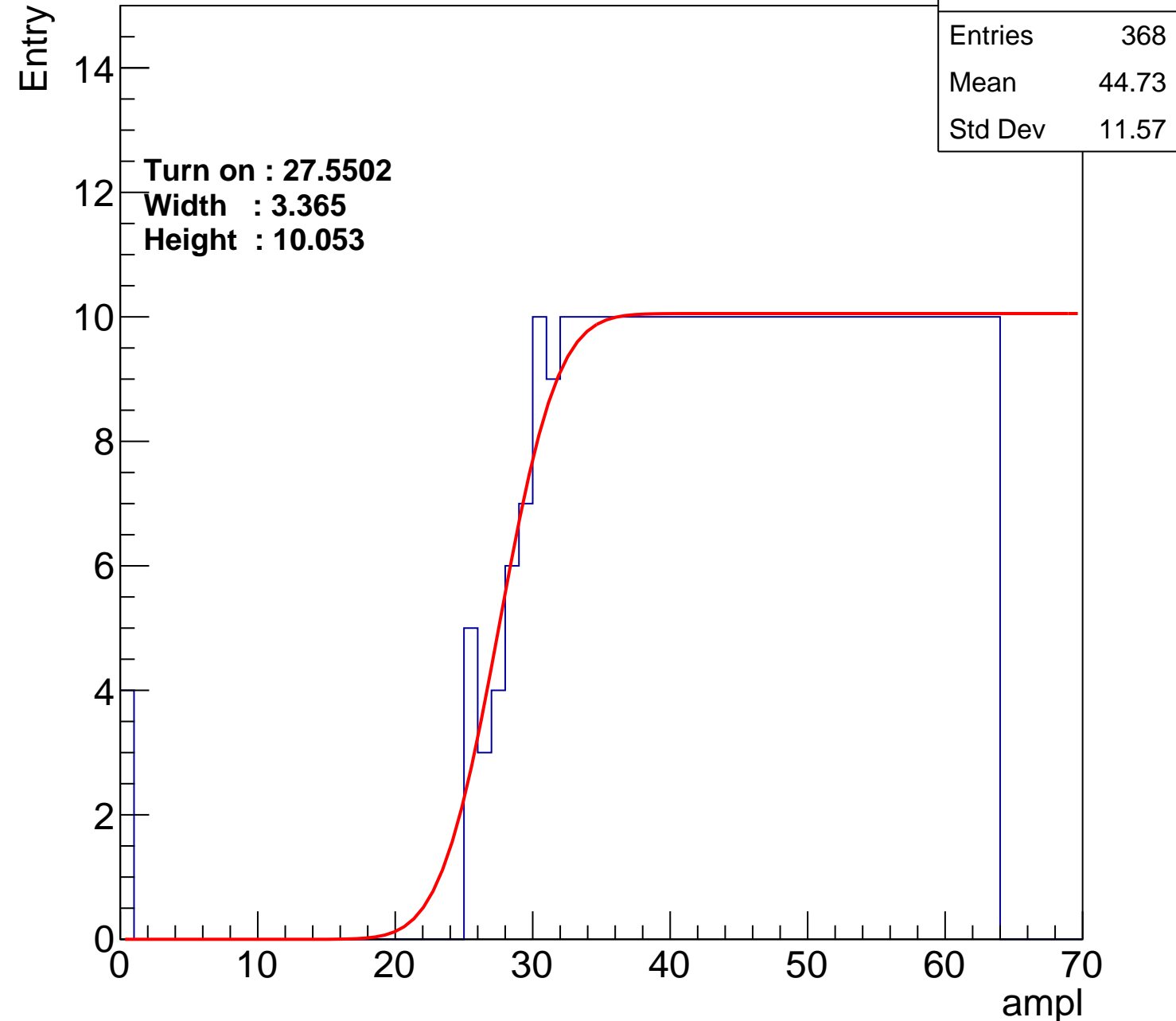
Width : 3.365

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch95

calib_packv5_042523_0143.root, FC#5, port B1

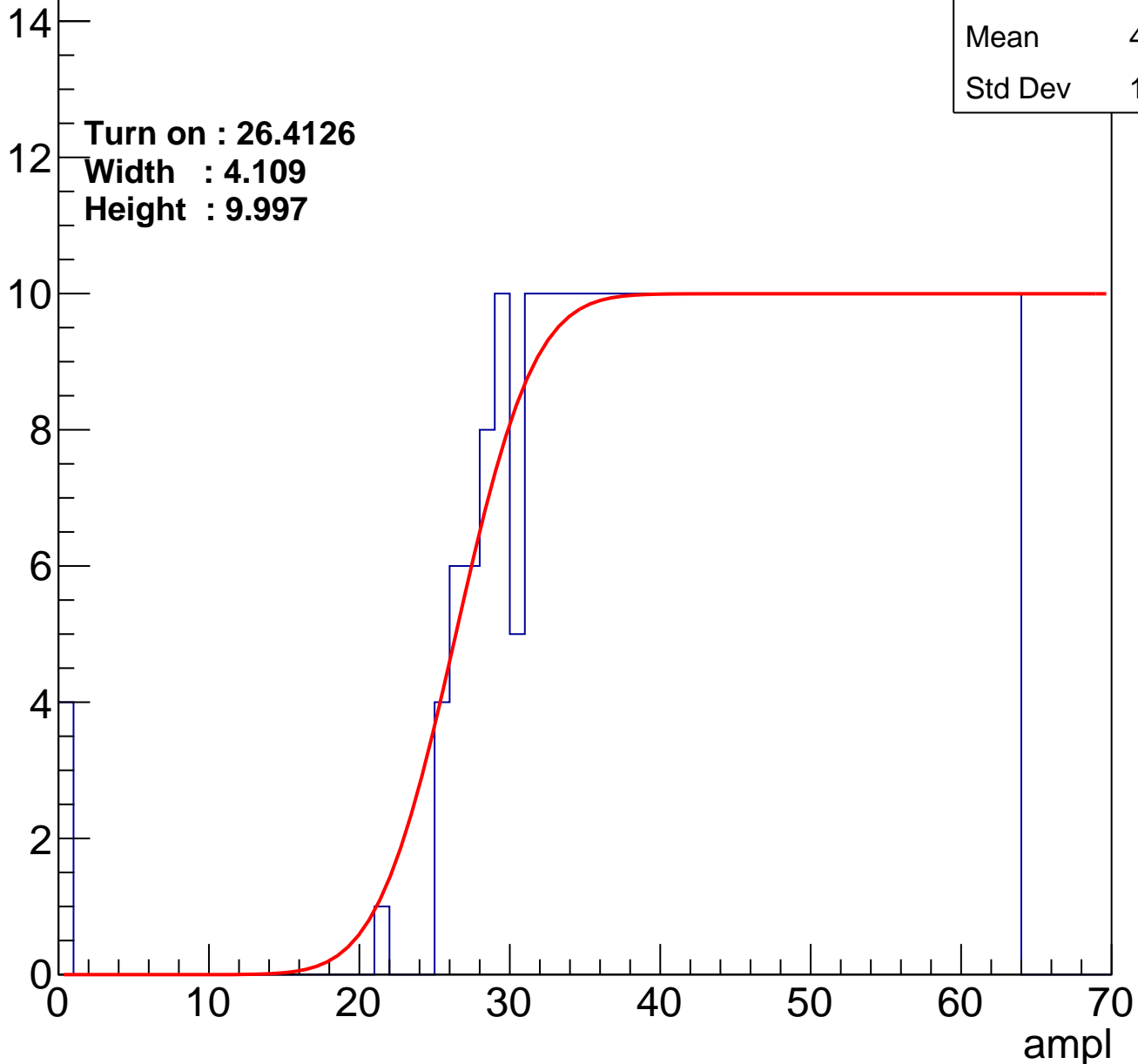
Entries	374
Mean	44.42
Std Dev	11.73

Turn on : 26.4126

Width : 4.109

Height : 9.997

Entry



B0L000S, U24-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.38
Std Dev	11.3

Turn on : 25.8923

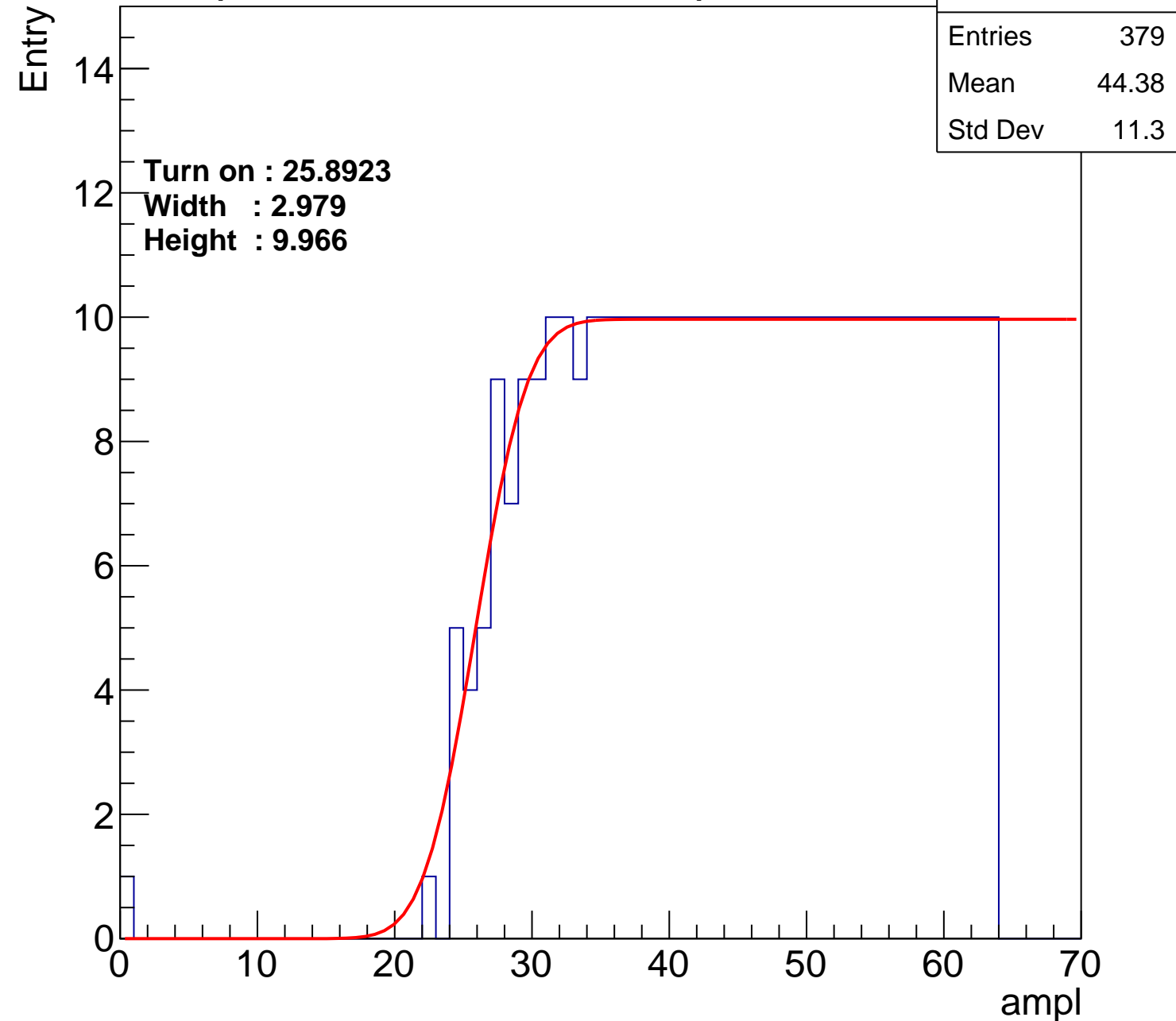
Width : 2.979

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 28.4247

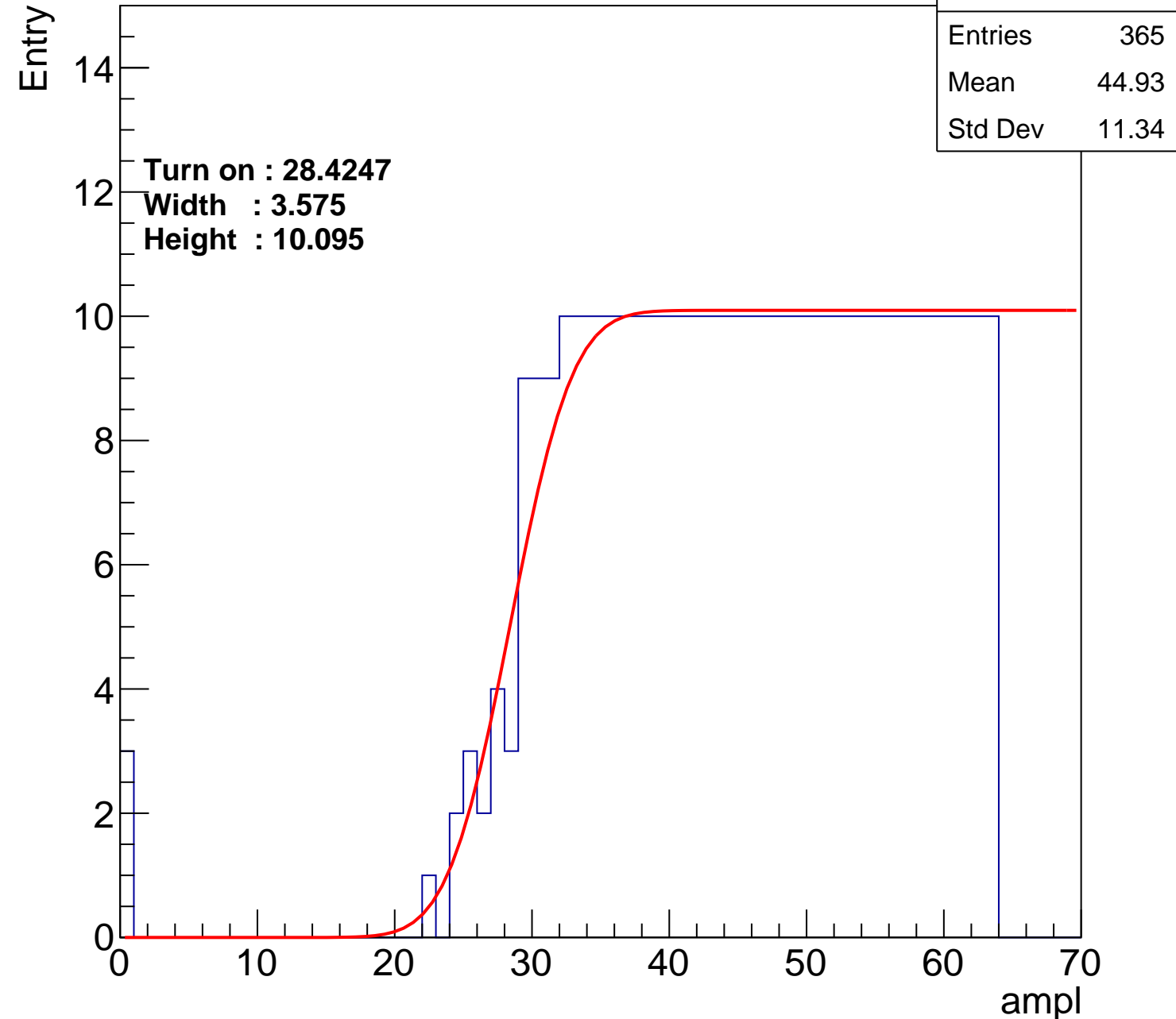
Width : 3.575

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 26.4861
Width : 2.769
Height : 9.988



B0L000S, U24-ch99

calib_packv5_042523_0143.root, FC#5, port B1

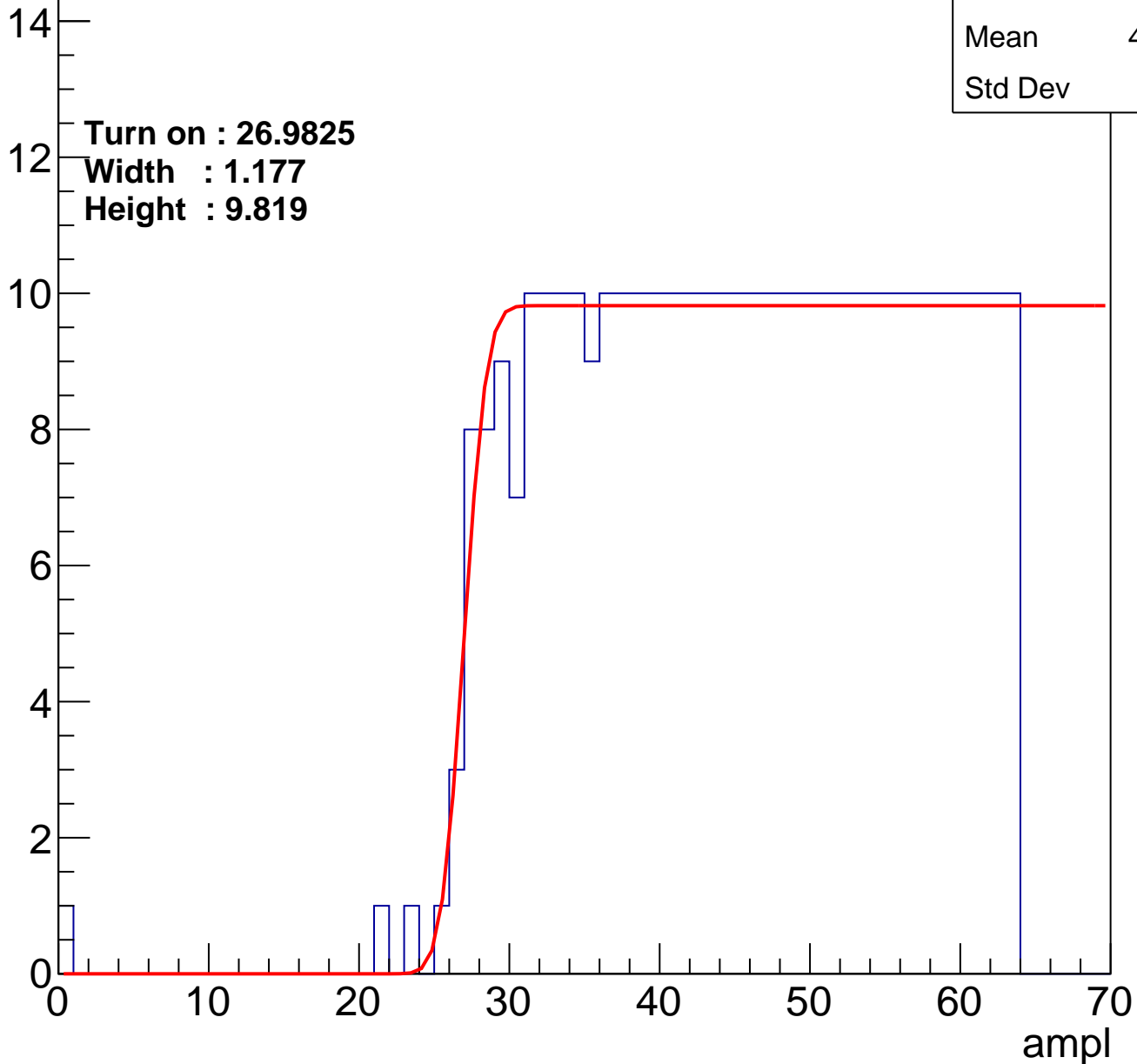
Entries	368
Mean	44.93
Std Dev	11

Turn on : 26.9825

Width : 1.177

Height : 9.819

Entry



B0L000S, U24-ch100

calib_packv5_042523_0143.root, FC#5, port B1

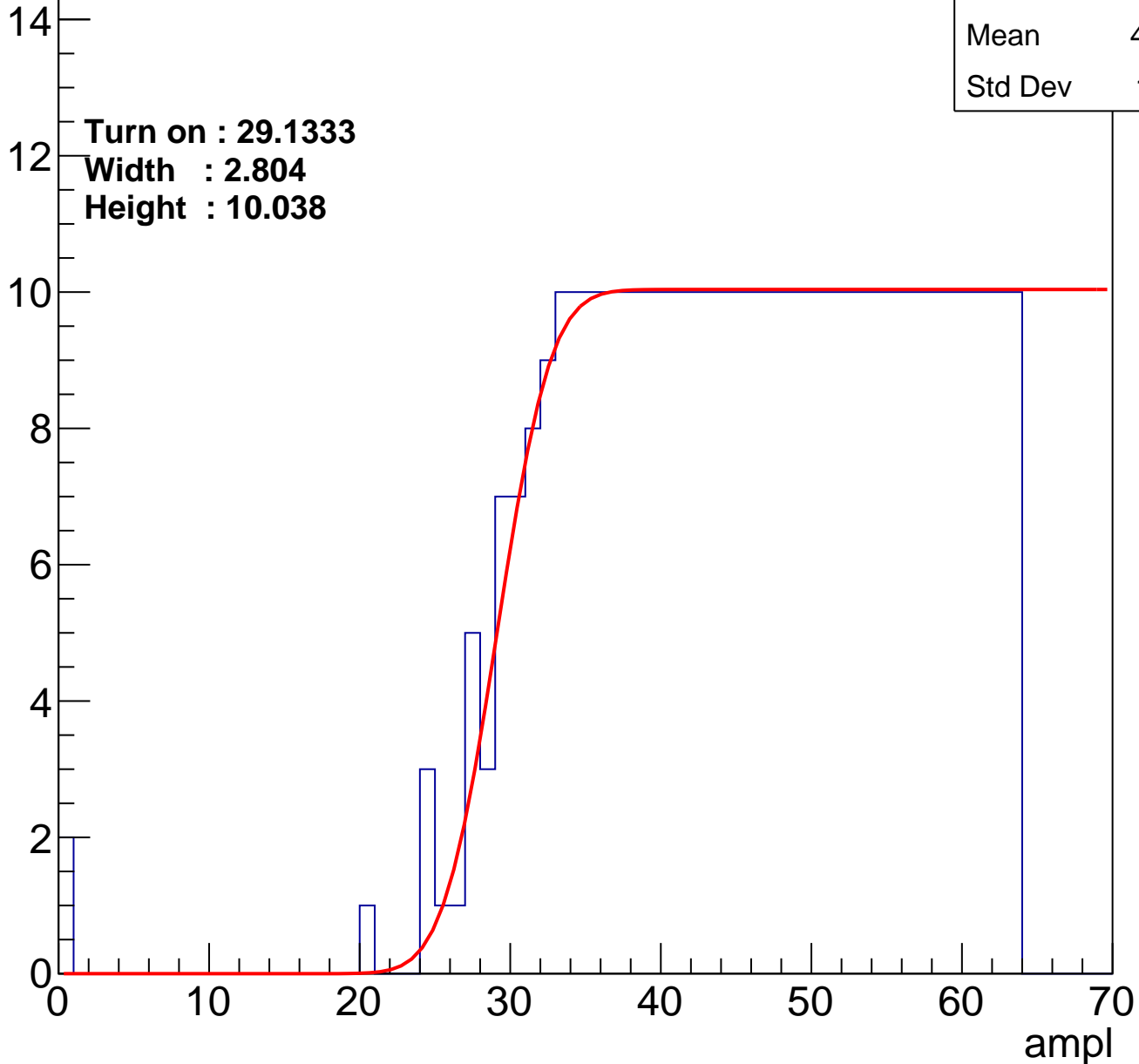
Entries	357
Mean	45.35
Std Dev	11.01

Turn on : 29.1333

Width : 2.804

Height : 10.038

Entry



B0L000S, U24-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.45
Std Dev	11.61

Turn on : 27.1503

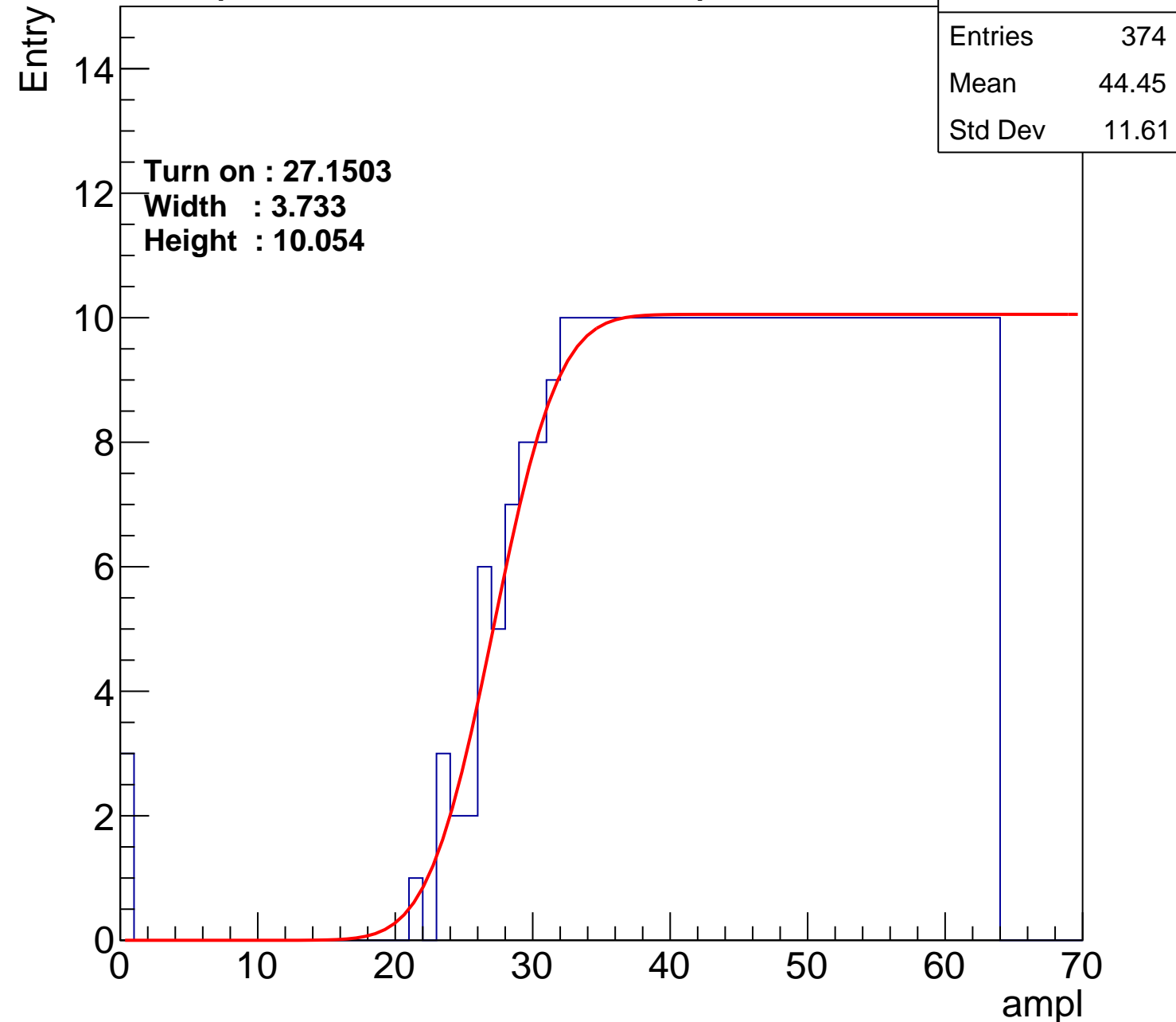
Width : 3.733

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch102

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.96
Std Dev	11.55

Turn on : 25.0242

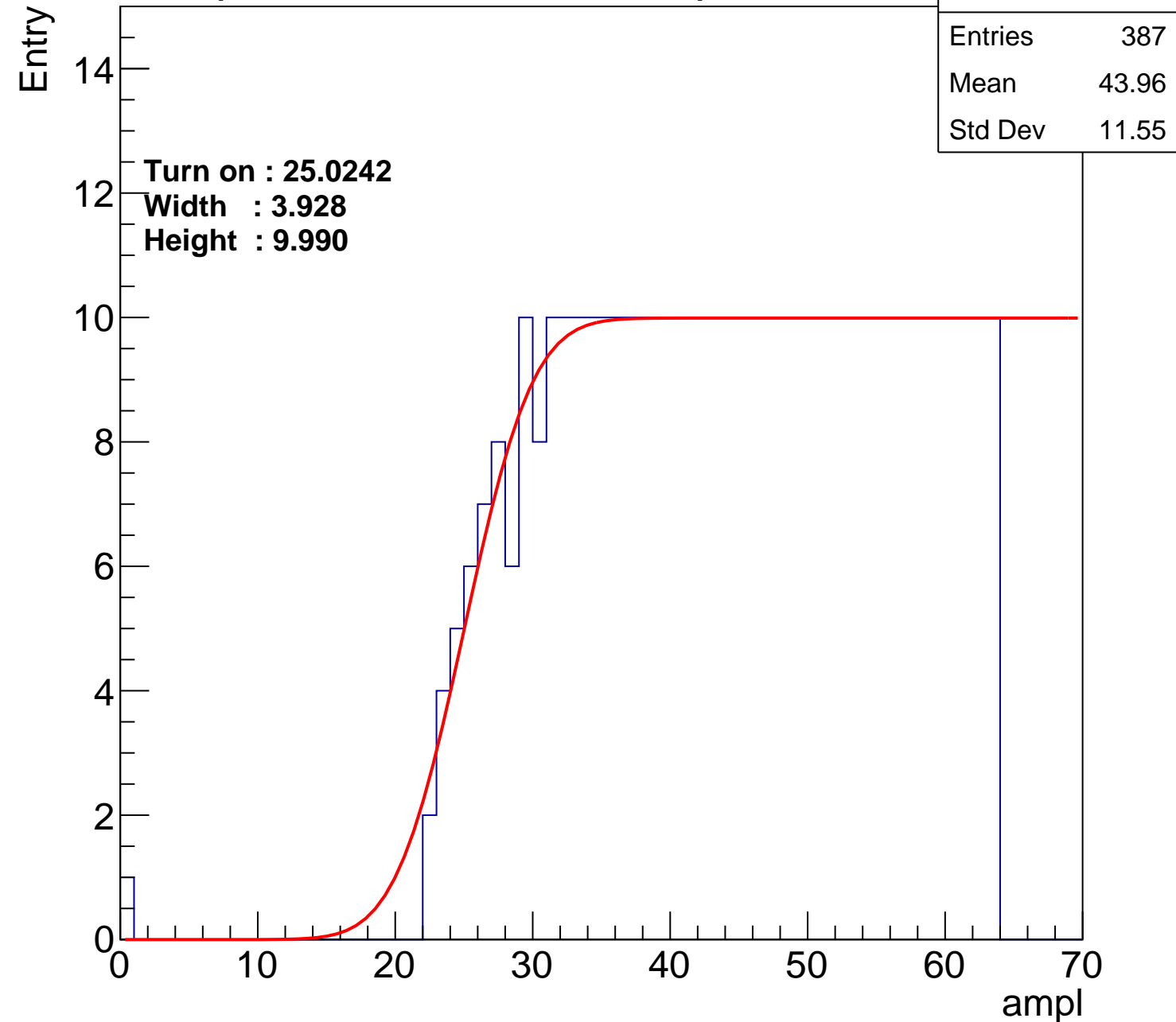
Width : 3.928

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch103

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	364
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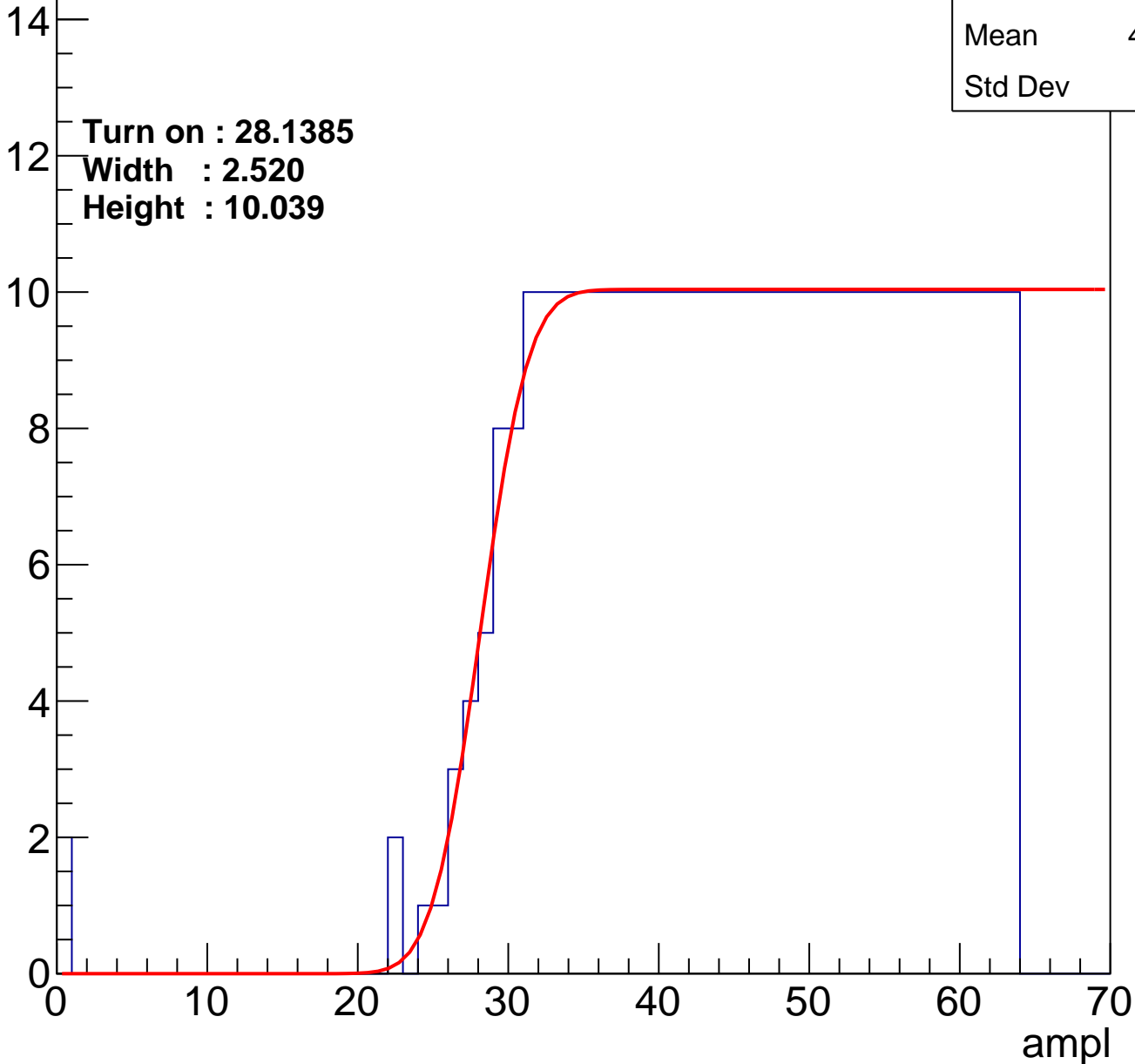
Mean	45.06
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Std Dev	11.1
---------	------

Turn on : 28.1385

Width : 2.520

Height : 10.039



B0L000S, U24-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	44.98
Std Dev	11.3

Turn on : 28.0007

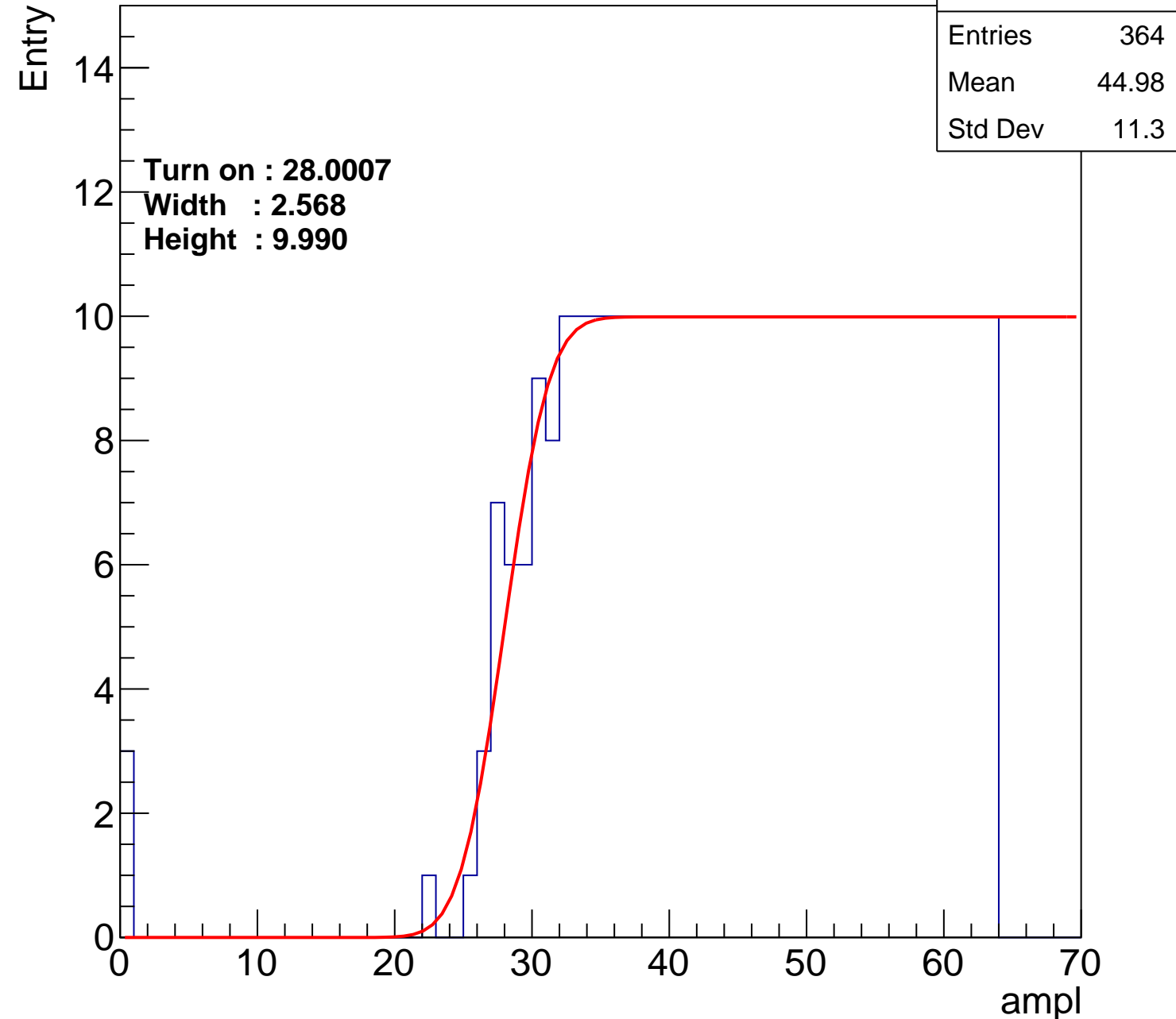
Width : 2.568

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.23
Std Dev	12.12

Turn on : 26.9166

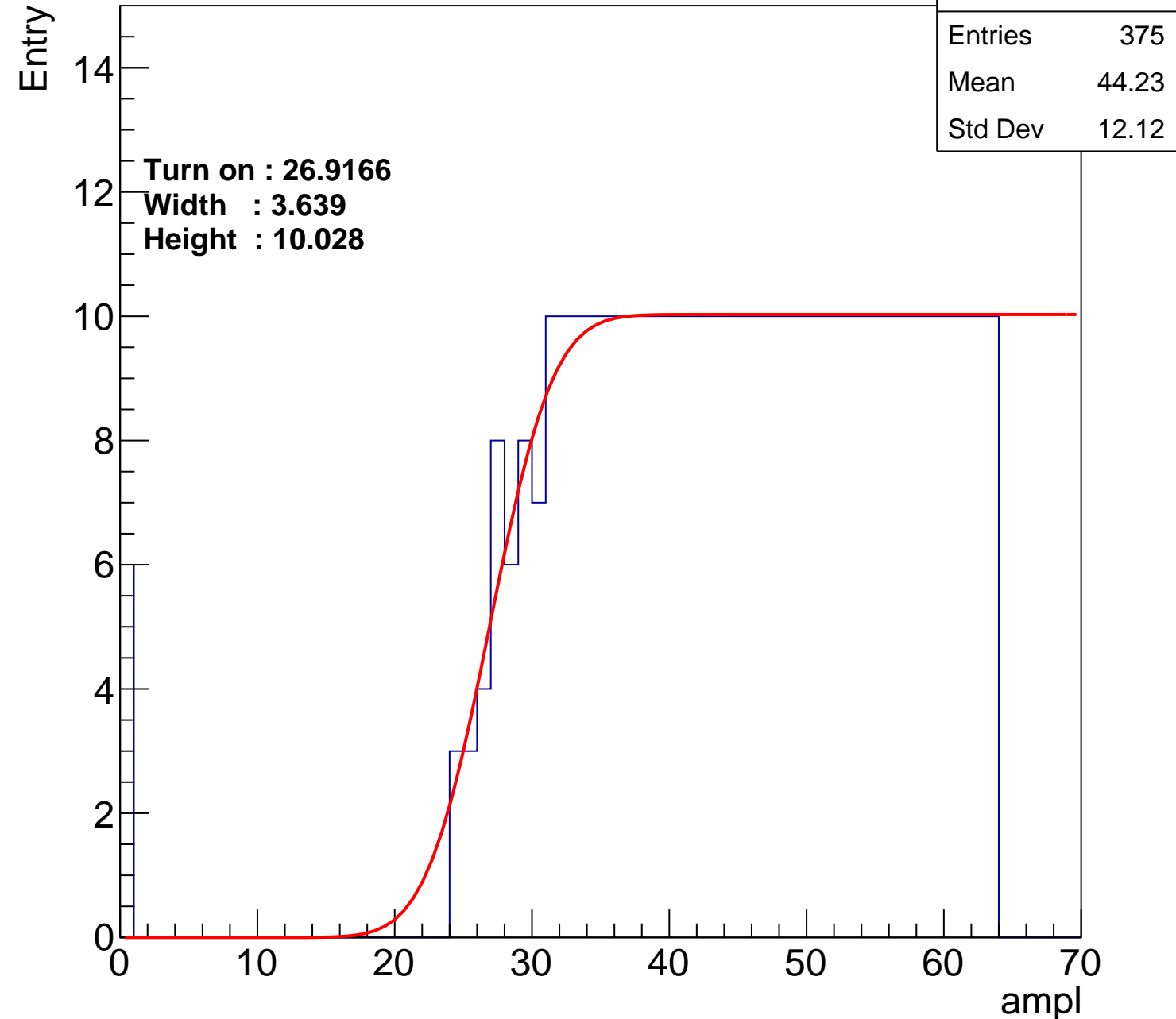
Width : 3.639

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.87
Std Dev	11.83

Turn on : 25.0262

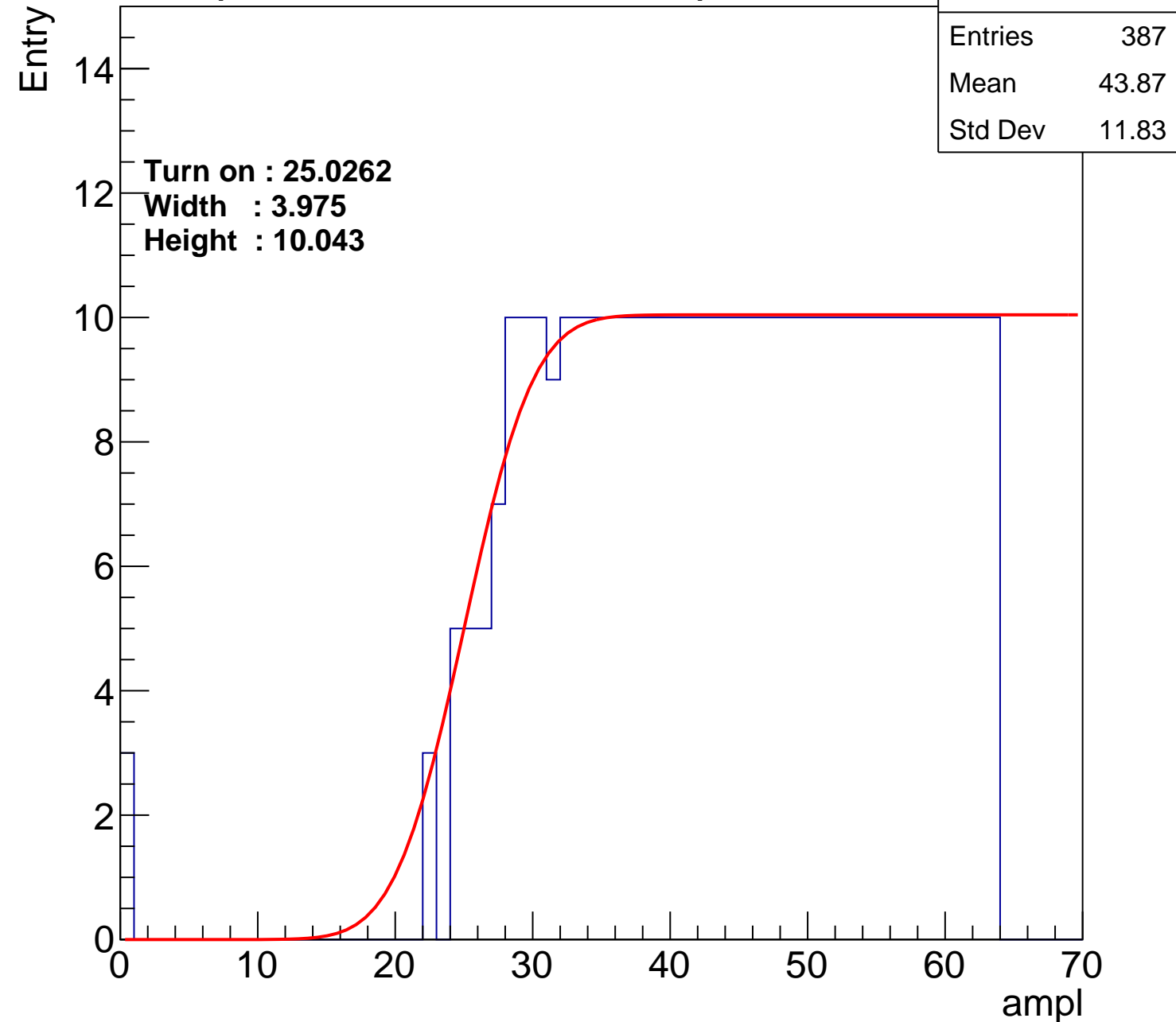
Width : 3.975

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.59
Std Dev	12.26

Turn on : 25.9523

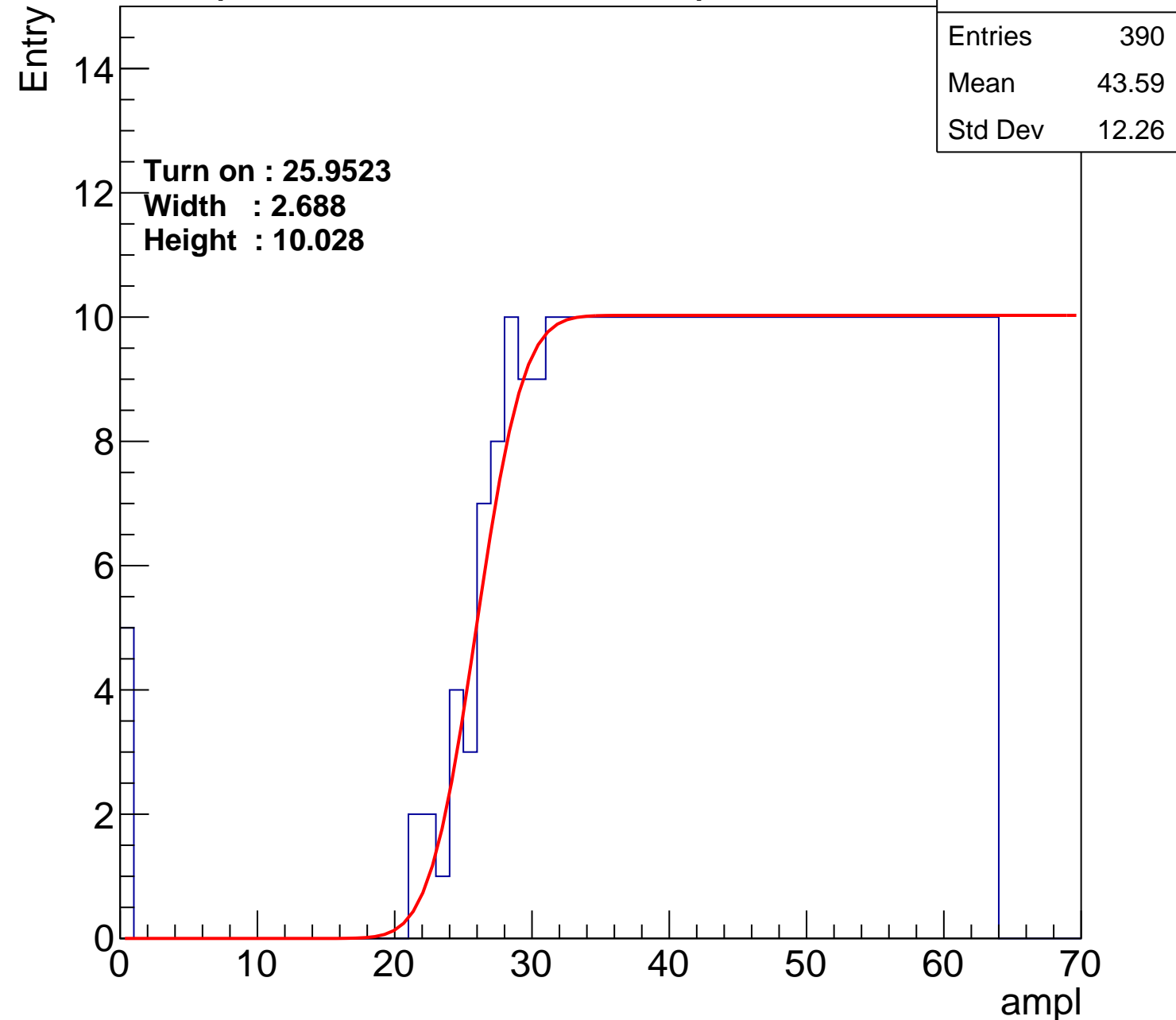
Width : 2.688

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	394
Mean	43.51
Std Dev	12.05

Turn on : 25.0856

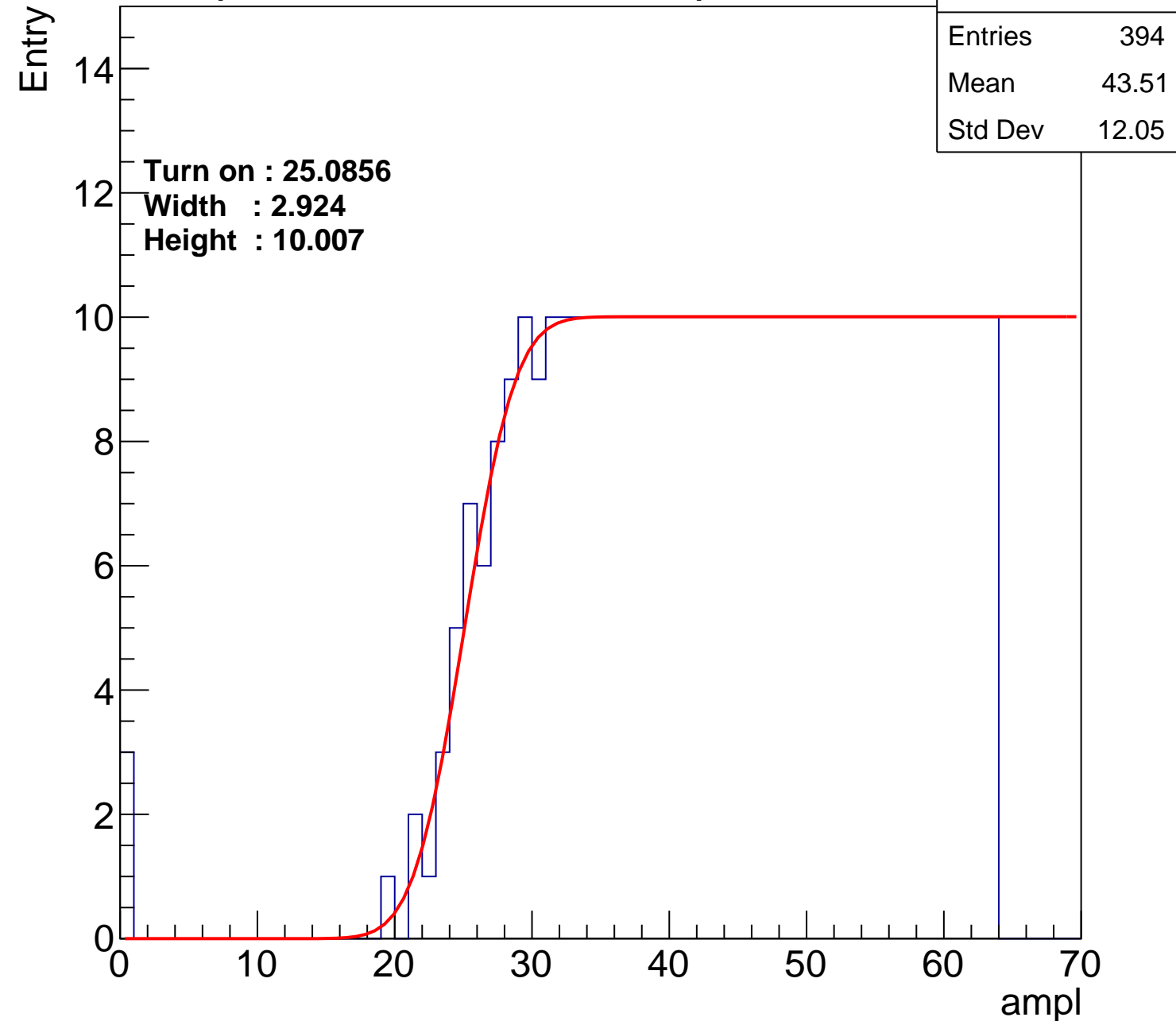
Width : 2.924

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.84
Std Dev	11.03

Turn on : 27.4387

Width : 3.073

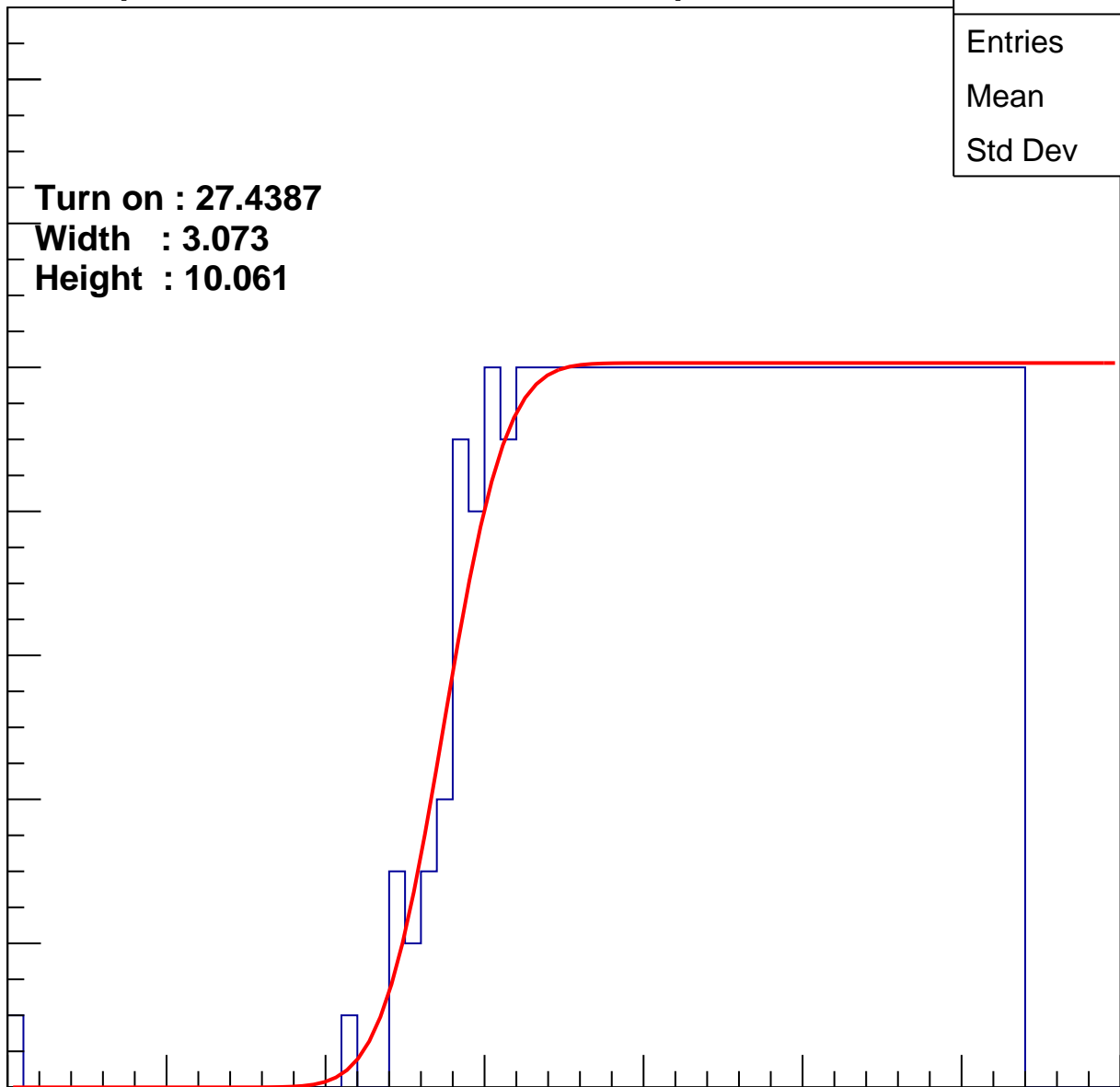
Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U24-ch110

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.3
Std Dev	11.95

Turn on : 27.3626

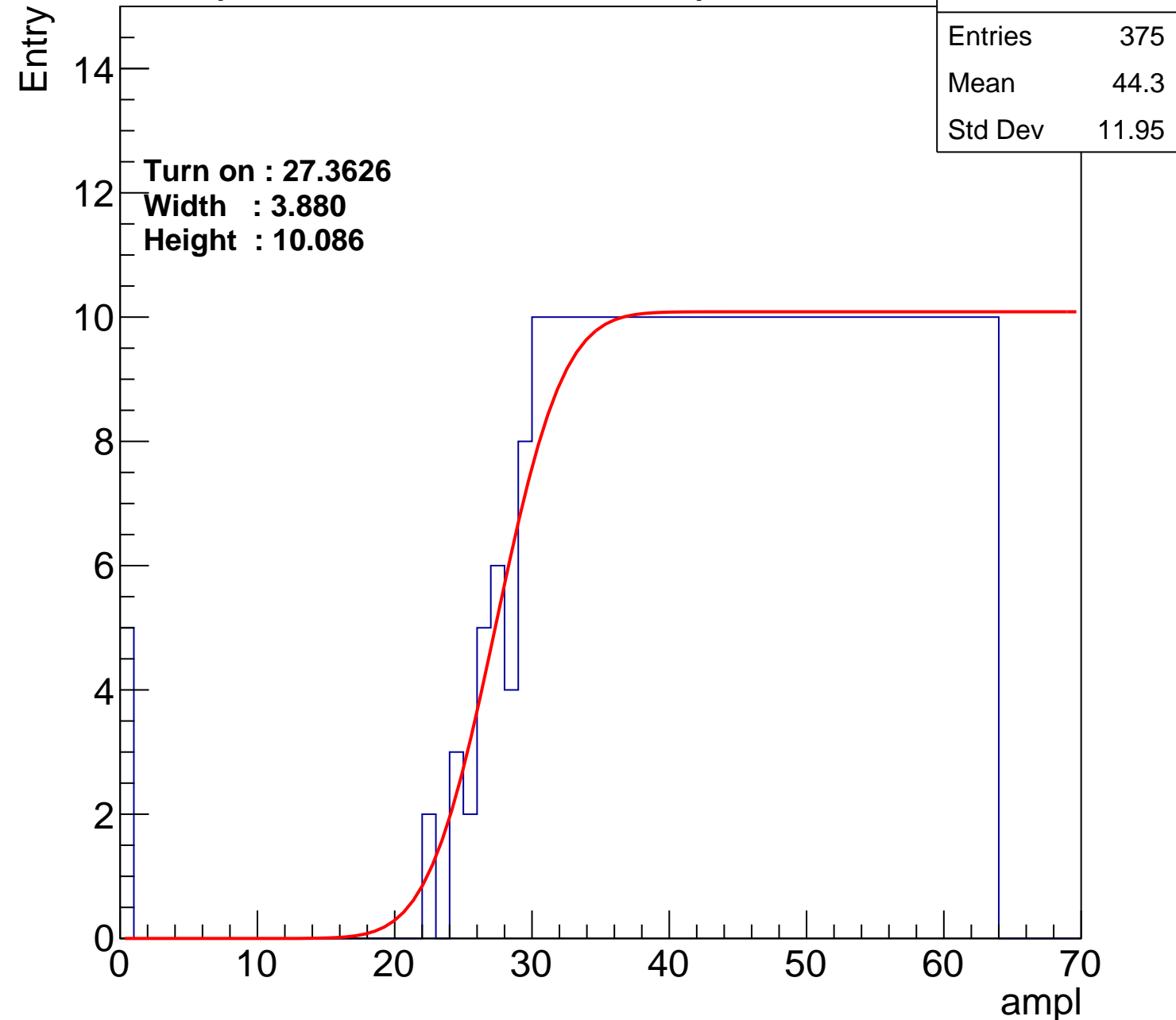
Width : 3.880

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.43
Std Dev	11.72

Turn on : 27.1294

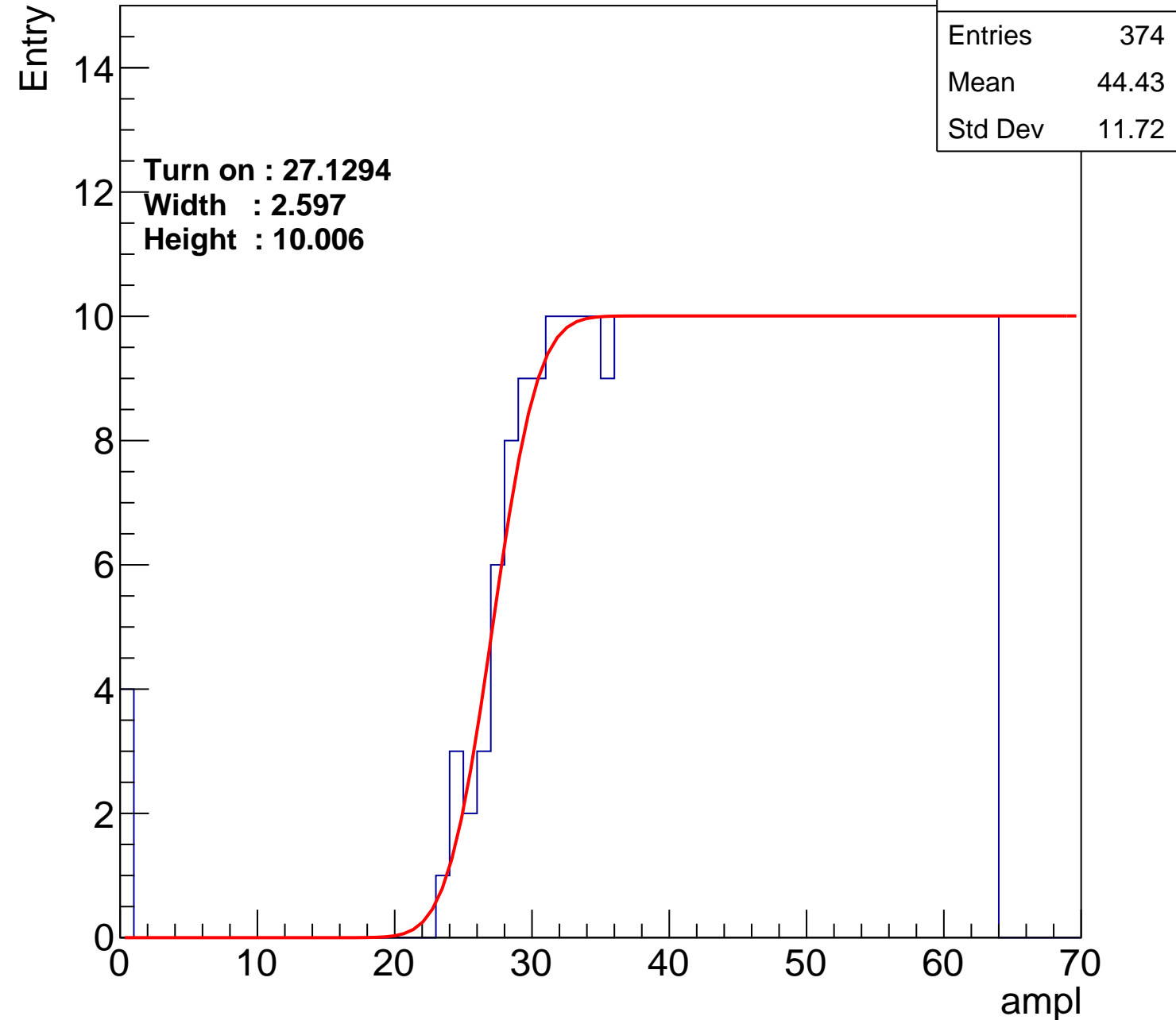
Width : 2.597

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.58
Std Dev	11.52

Turn on : 27.7083

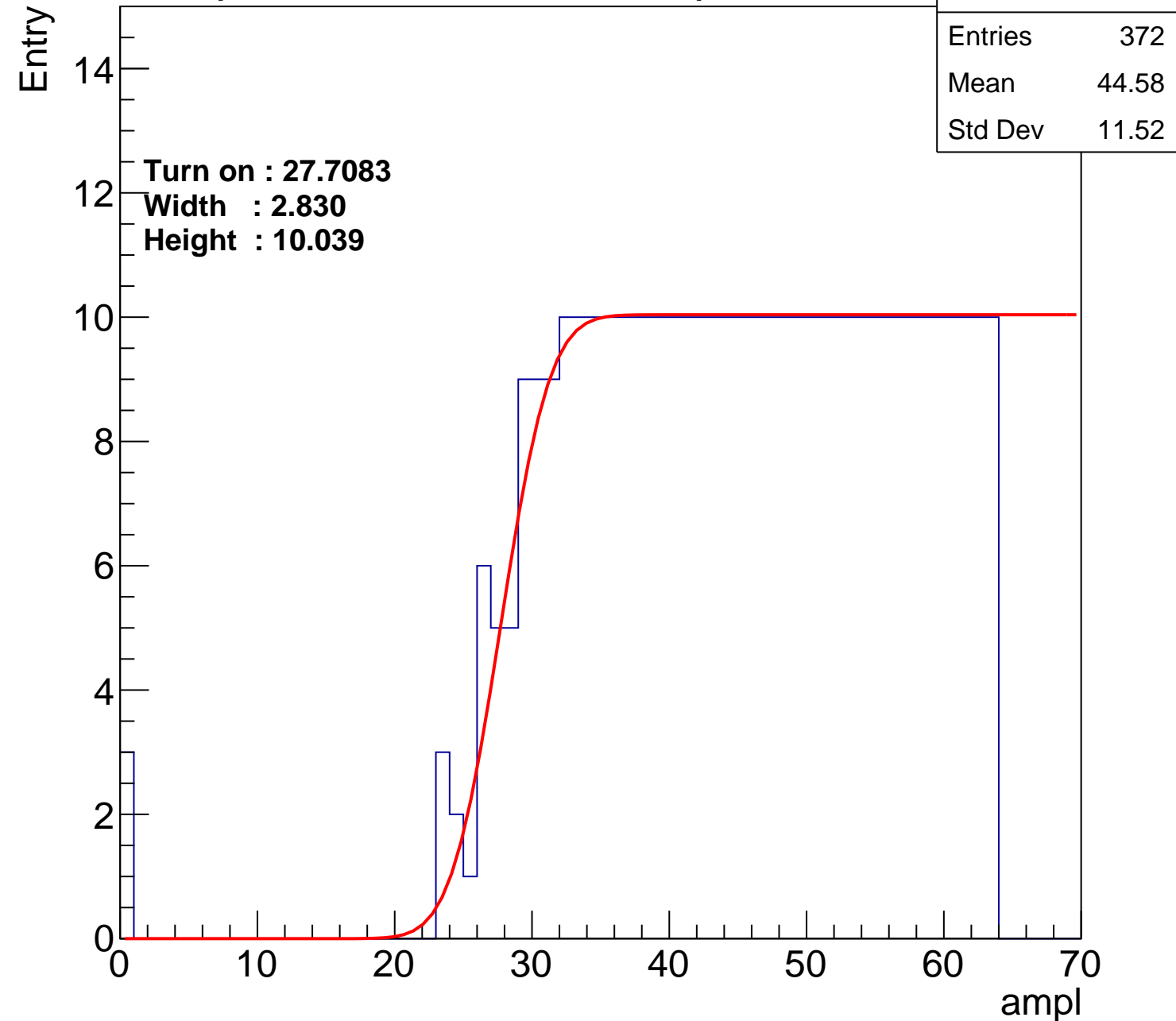
Width : 2.830

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.87
Std Dev	11.95

Turn on : 26.9543

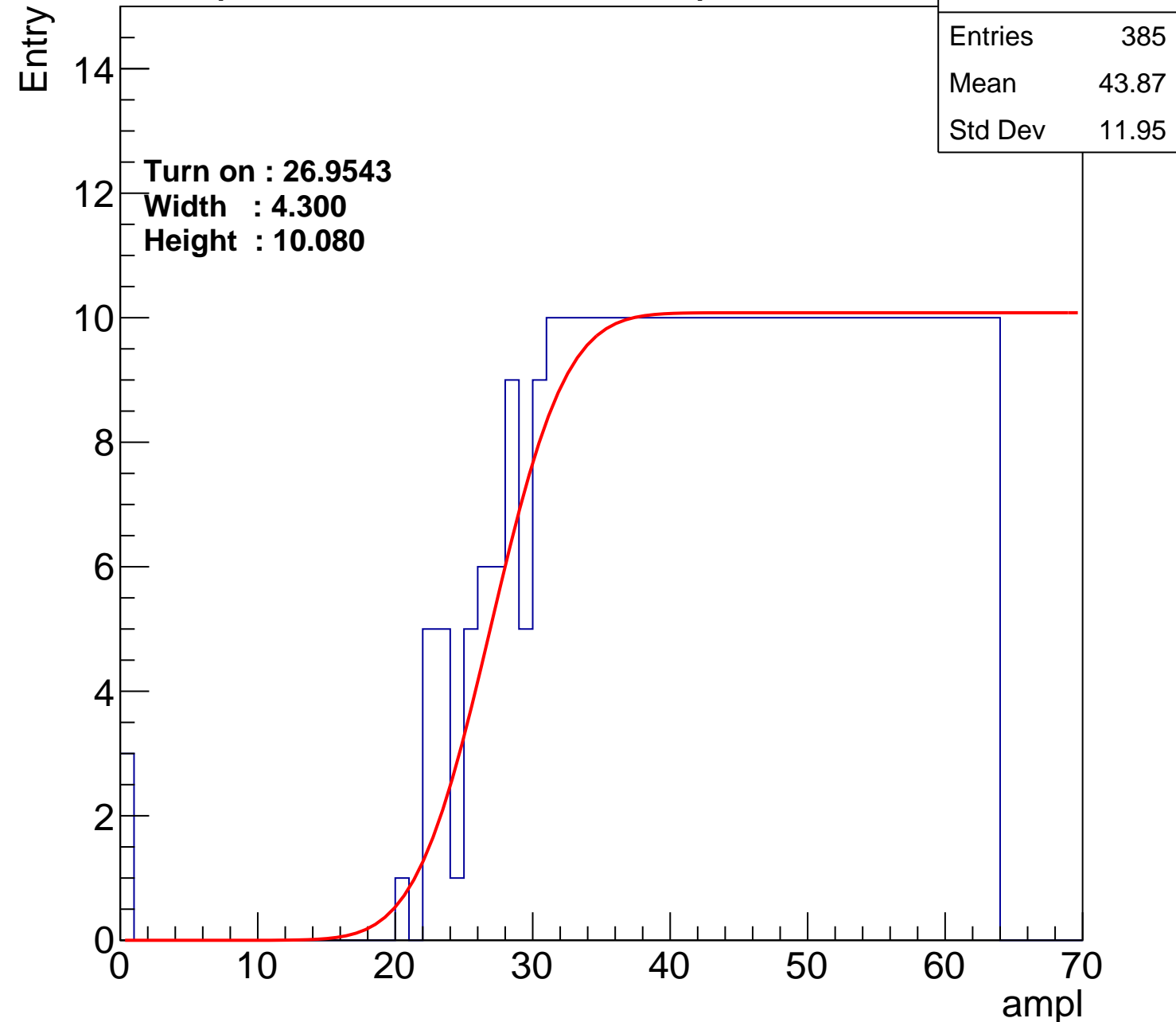
Width : 4.300

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	44.94
Std Dev	11.37

Turn on : 28.4118

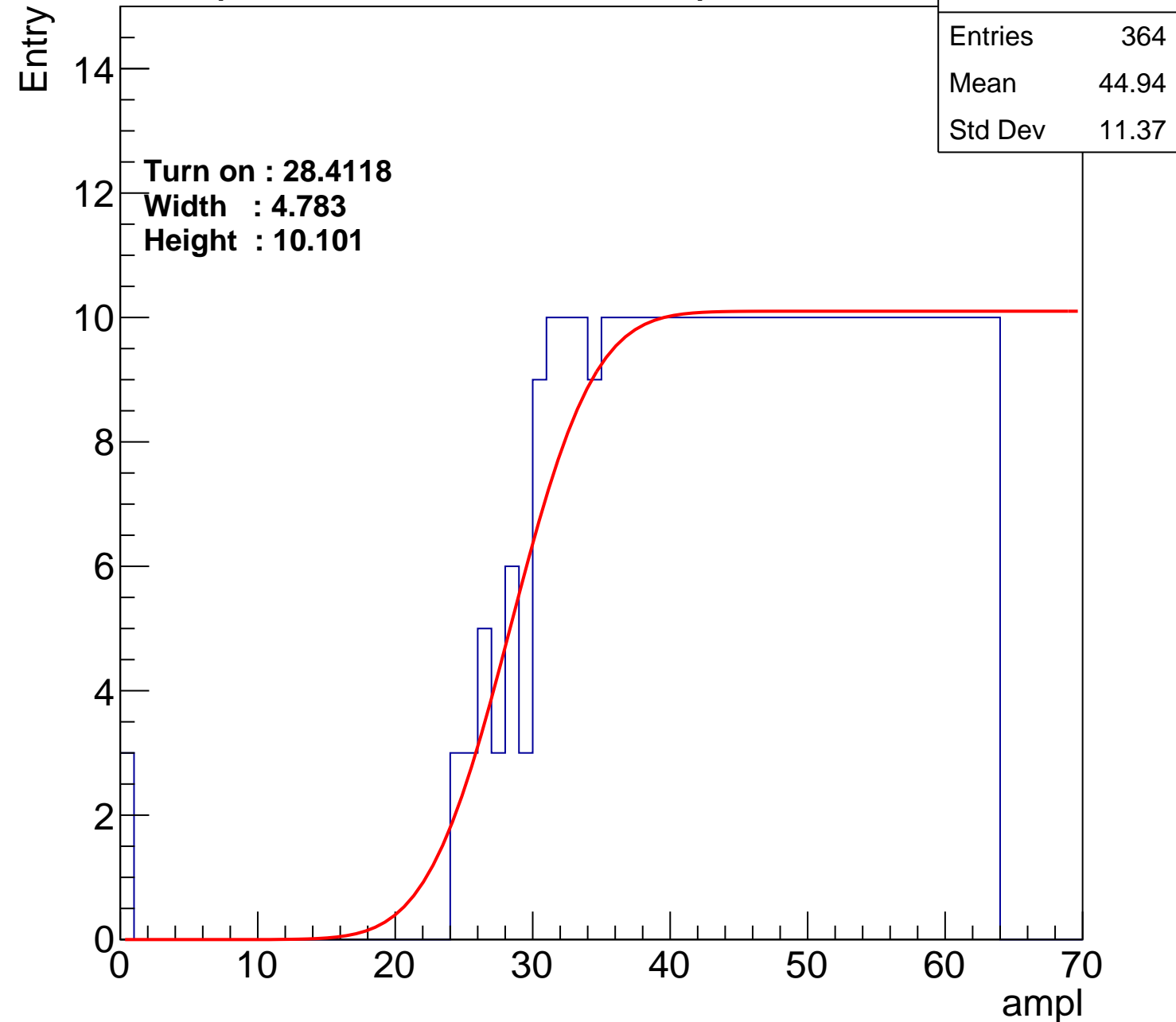
Width : 4.783

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch115

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.17
Std Dev	11.81

Turn on : 26.6321

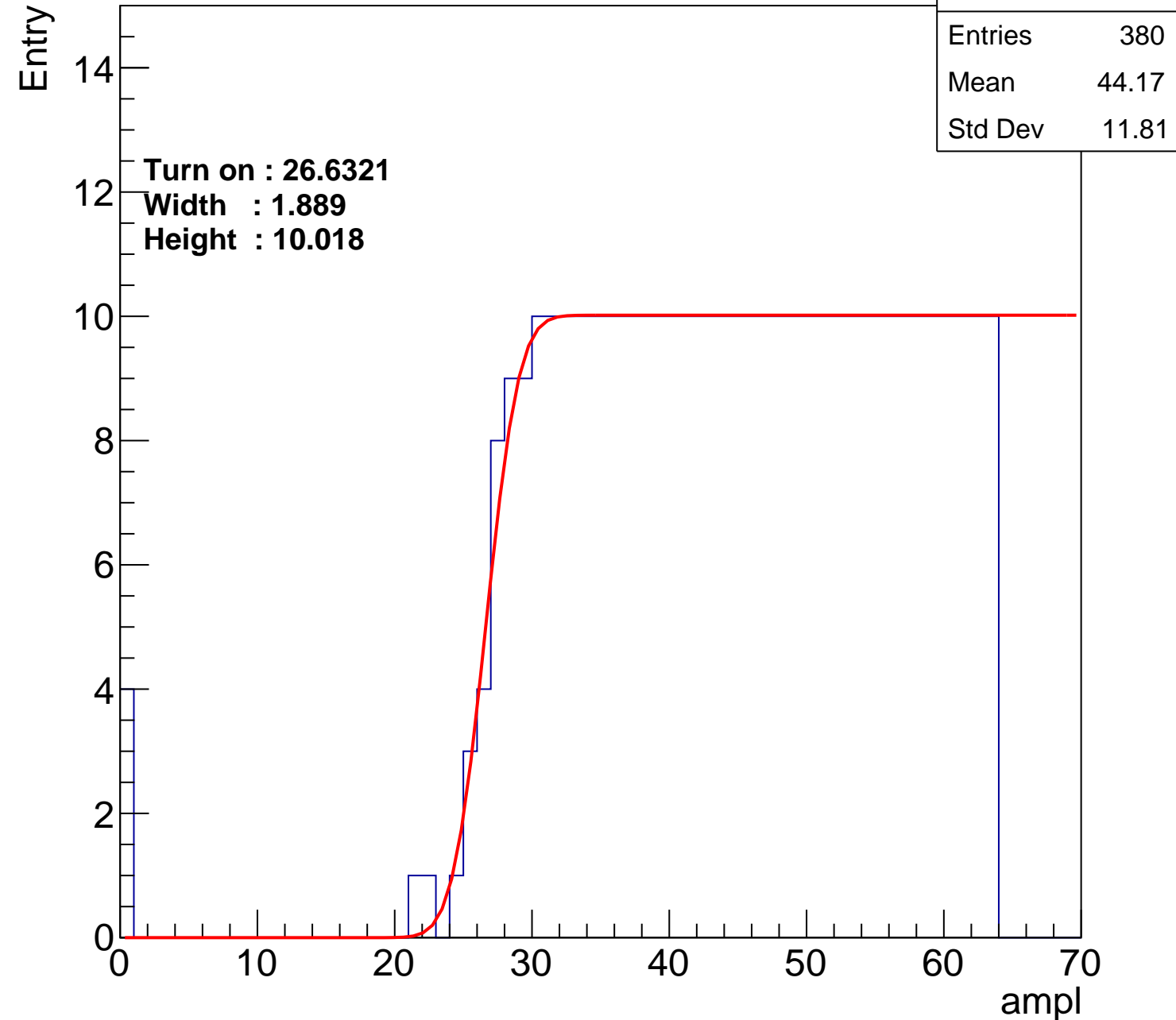
Width : 1.889

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.18
Std Dev	12.46

Turn on : 28.2893

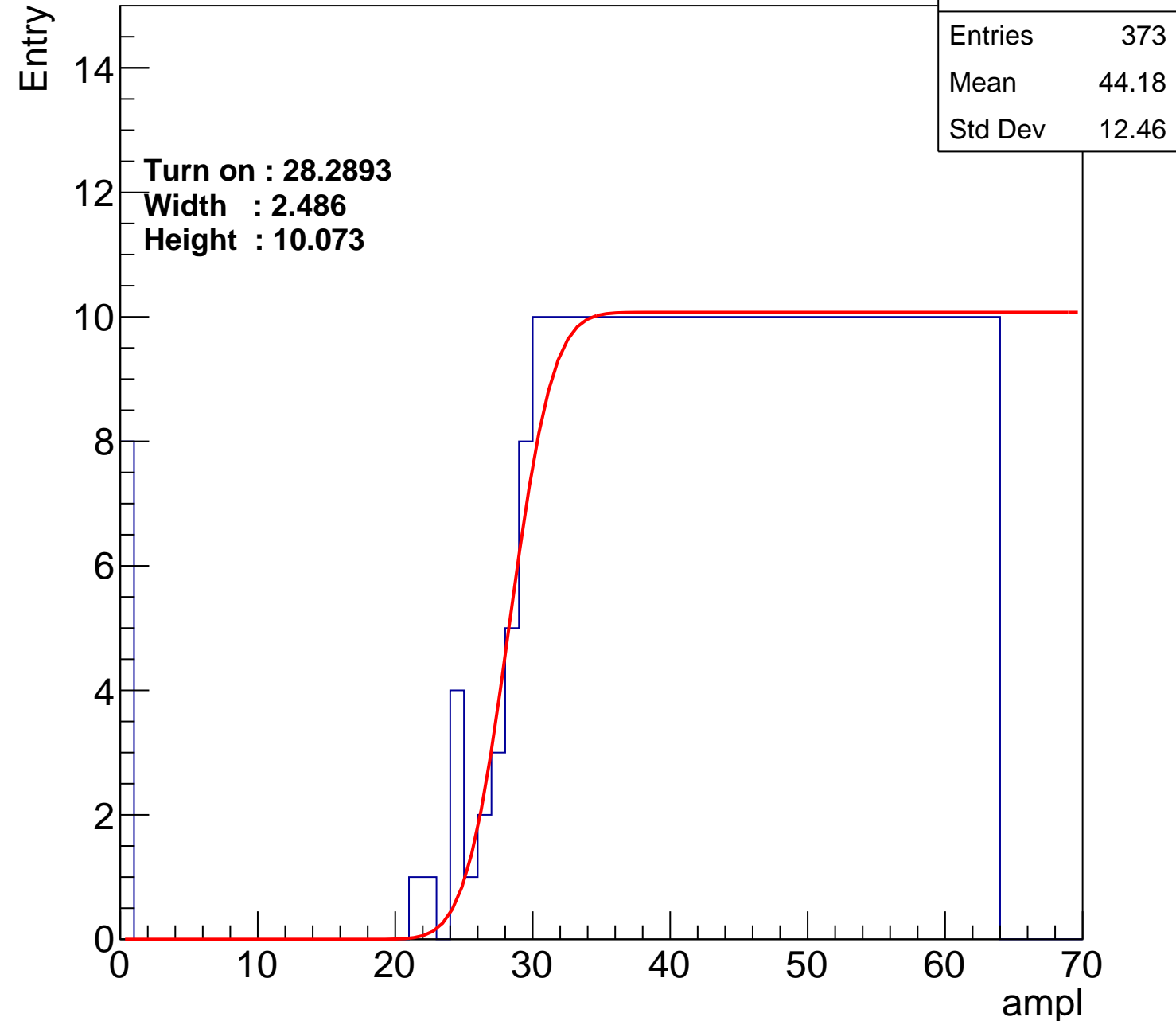
Width : 2.486

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.59
Std Dev	11.21

Turn on : 26.5547

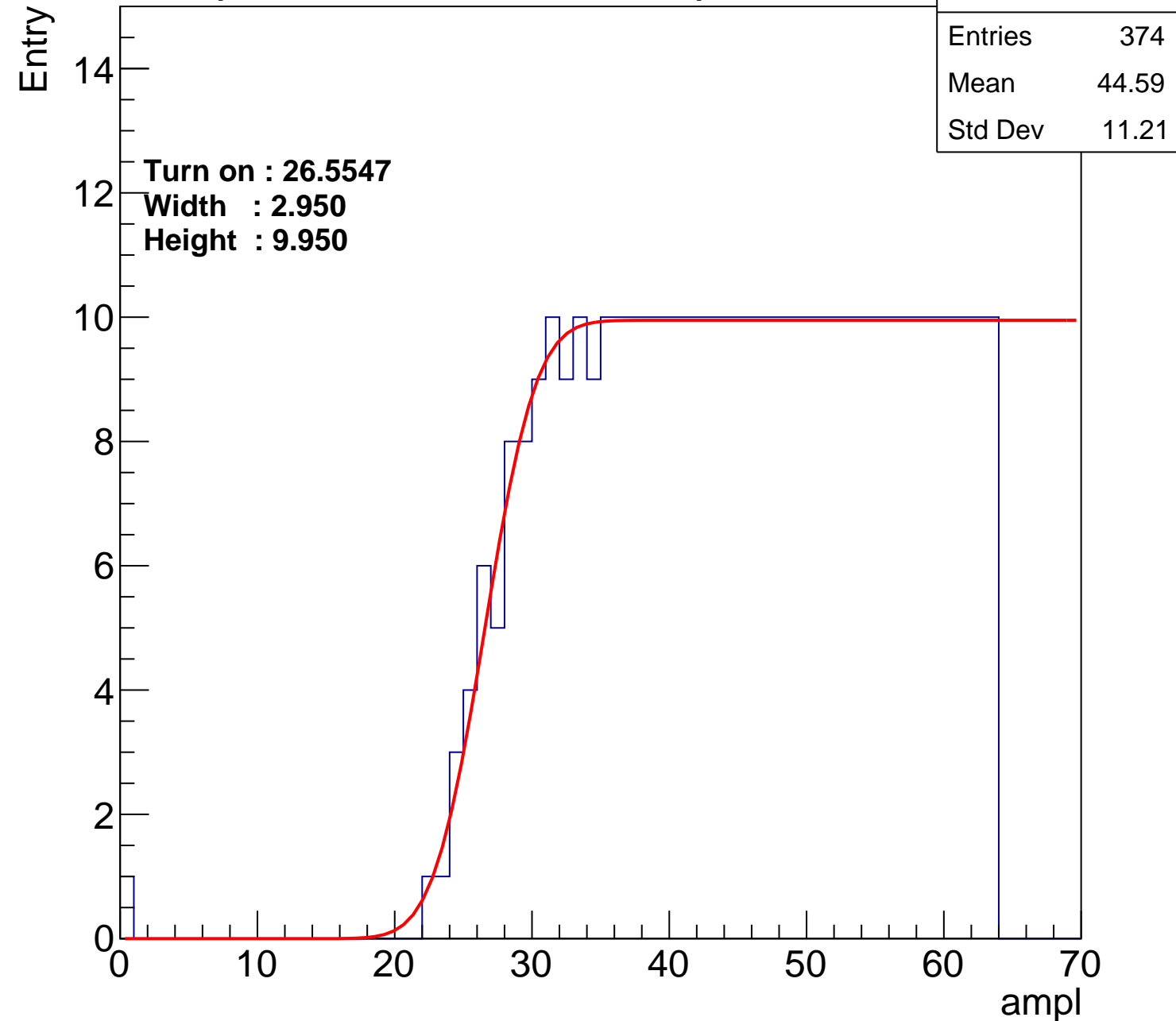
Width : 2.950

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.29
Std Dev	11.48

Turn on : 28.8762

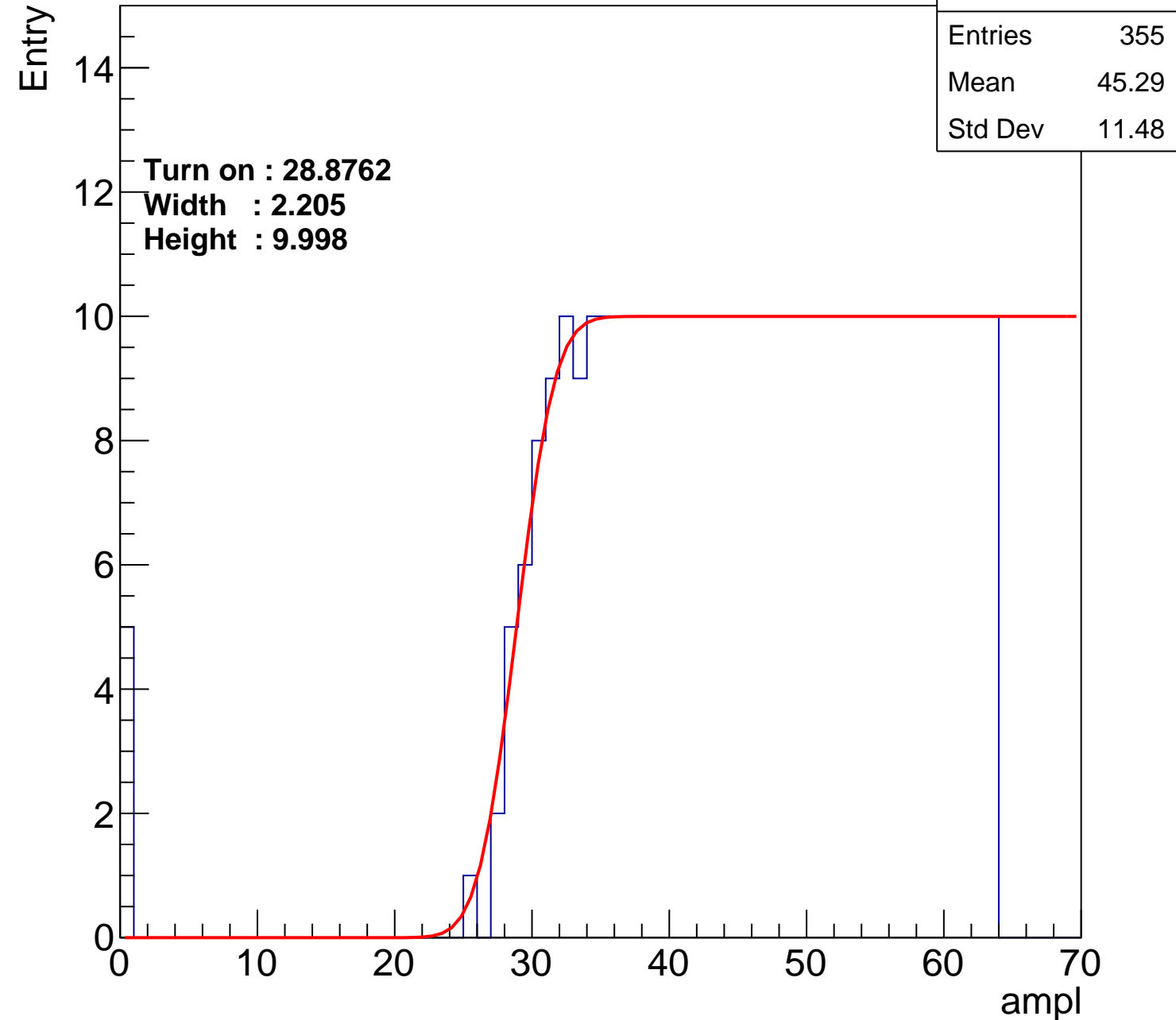
Width : 2.205

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch119

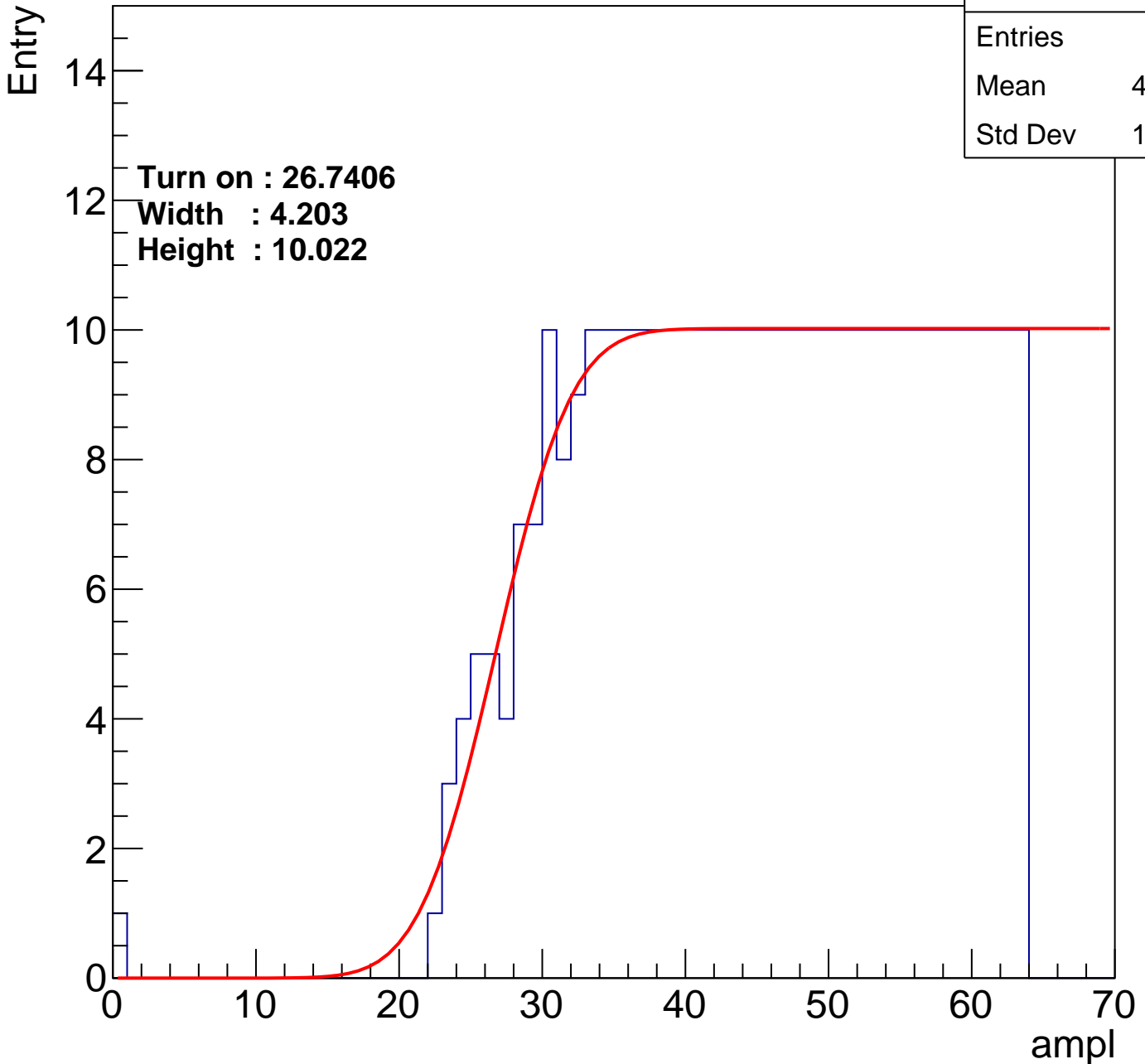
calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.56
Std Dev	11.27

Turn on : 26.7406

Width : 4.203

Height : 10.022



B0L000S, U24-ch120

calib_packv5_042523_0143.root, FC#5, port B1

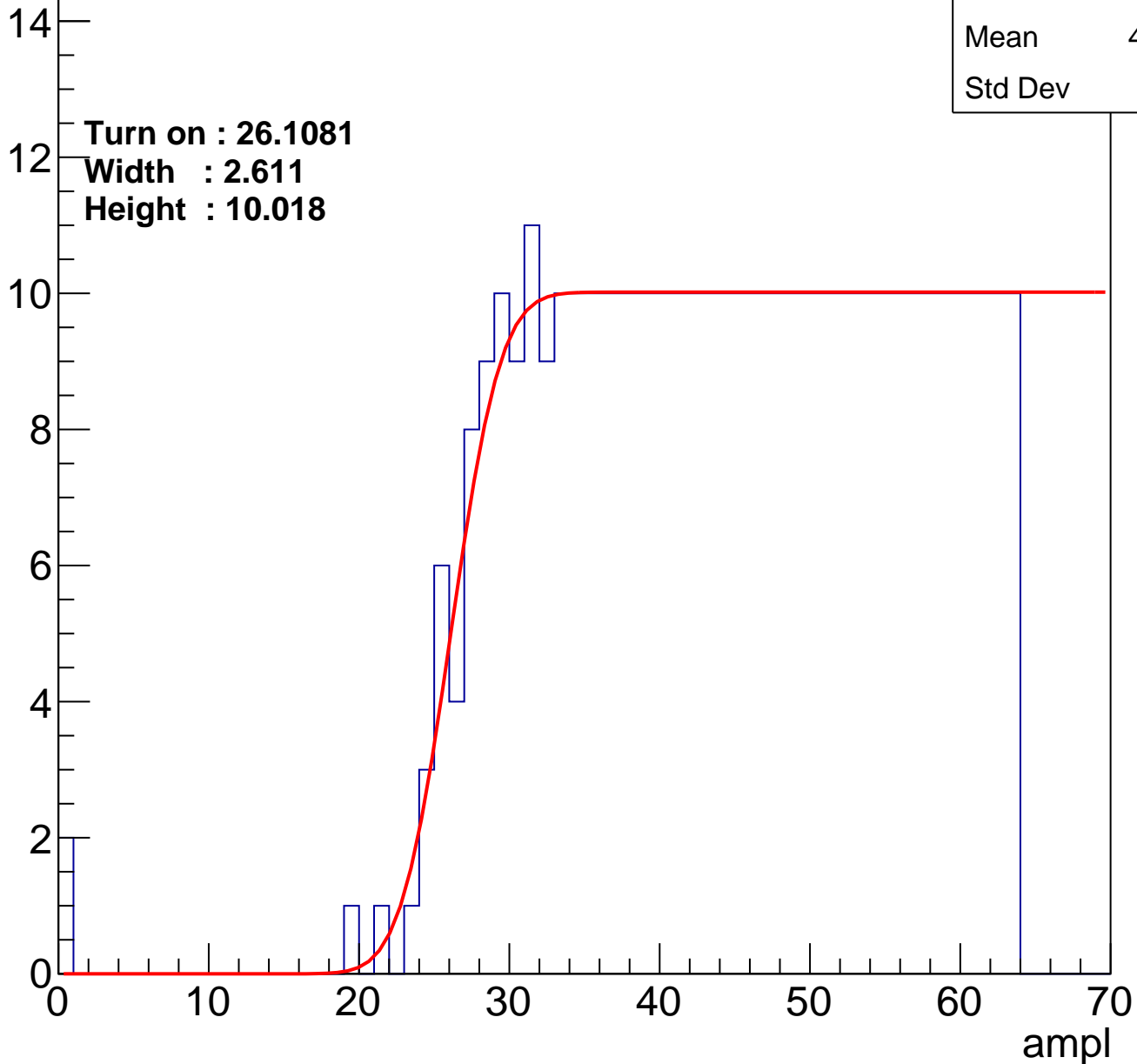
Entries	384
Mean	44.08
Std Dev	11.6

Turn on : 26.1081

Width : 2.611

Height : 10.018

Entry



B0L000S, U24-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.9
Std Dev	11.04

Turn on : 27.7661

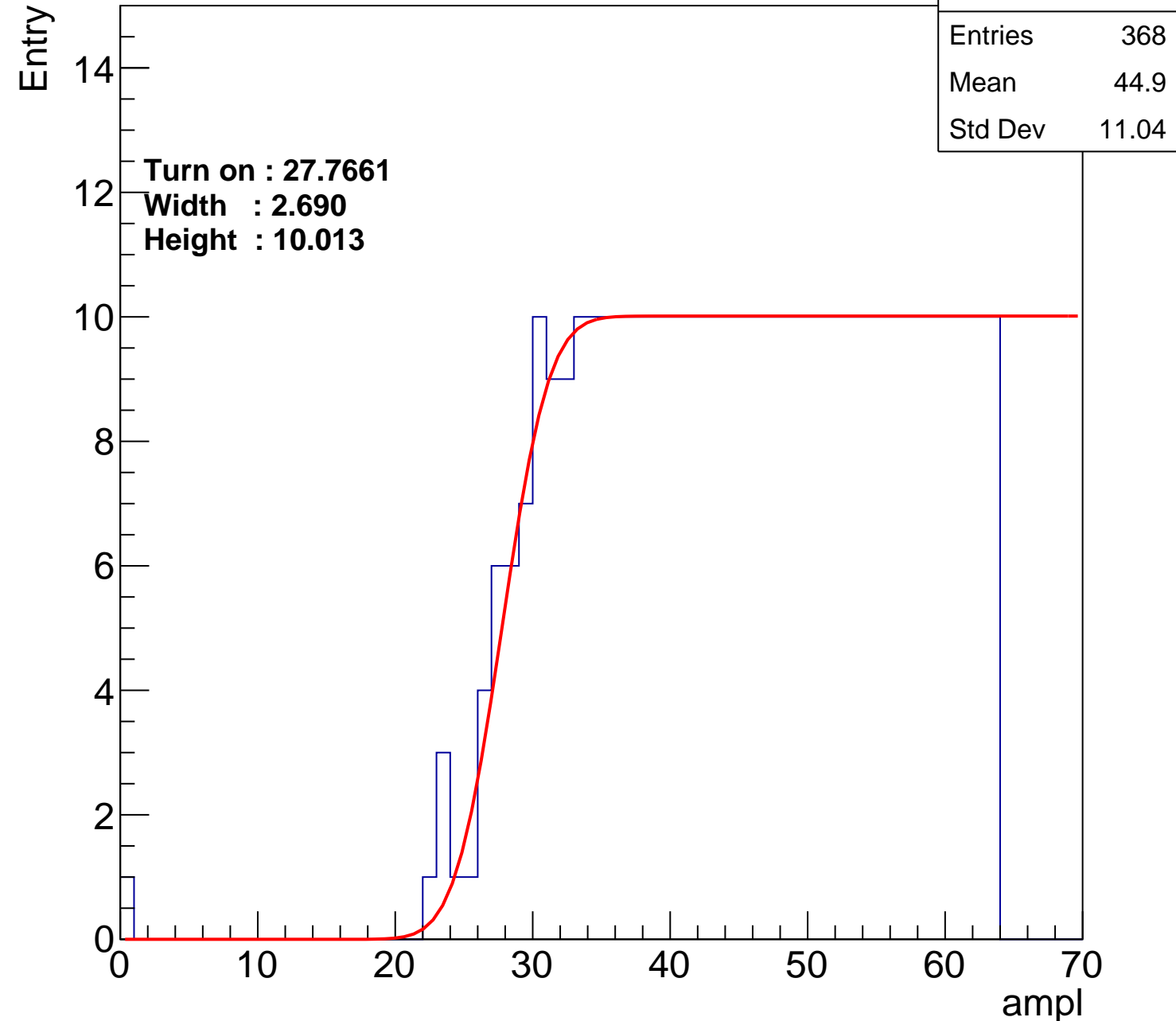
Width : 2.690

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch122

calib_packv5_042523_0143.root, FC#5, port B1

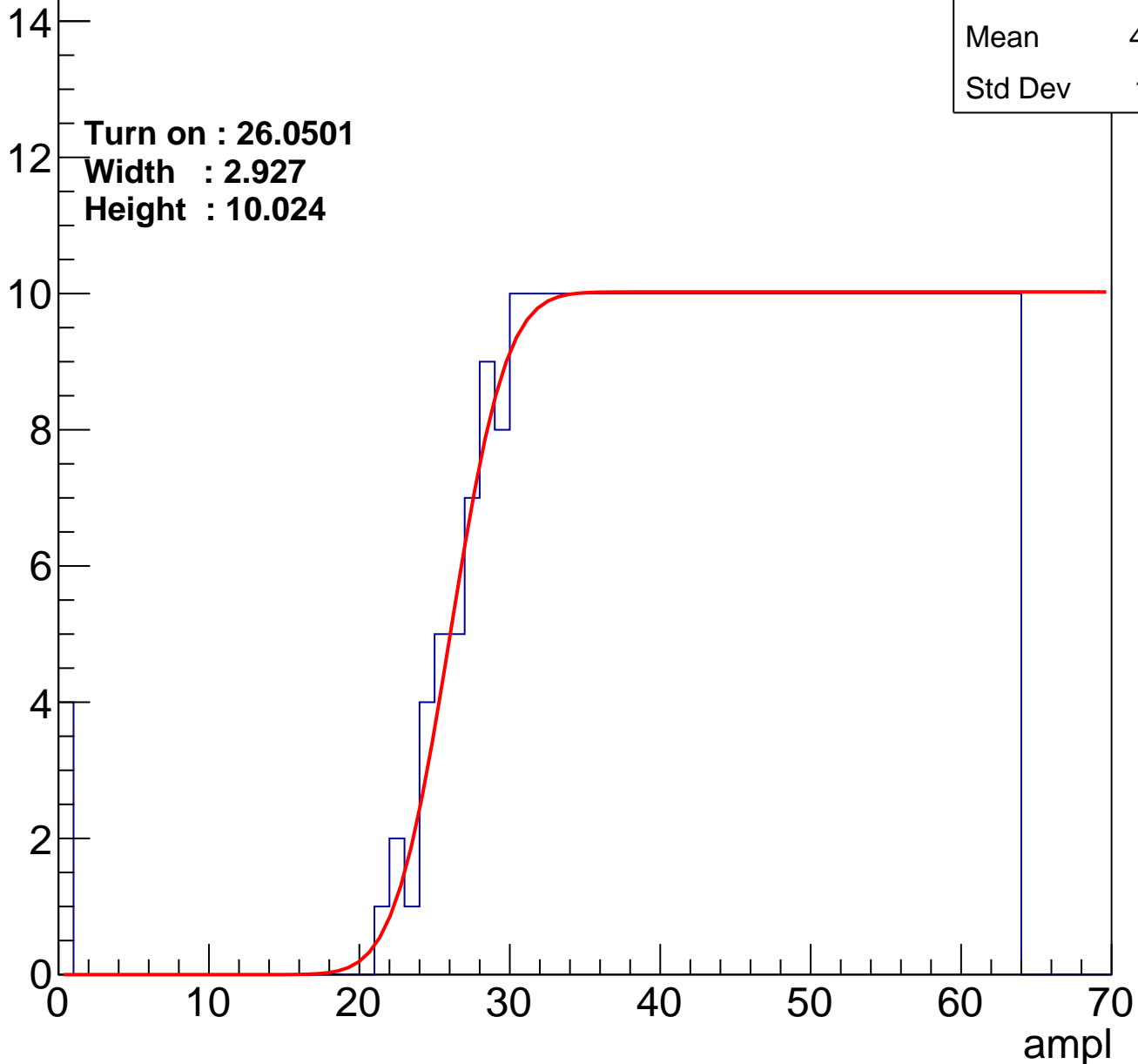
Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 26.0501

Width : 2.927

Height : 10.024

Entry



B0L000S, U24-ch123

calib_packv5_042523_0143.root, FC#5, port B1

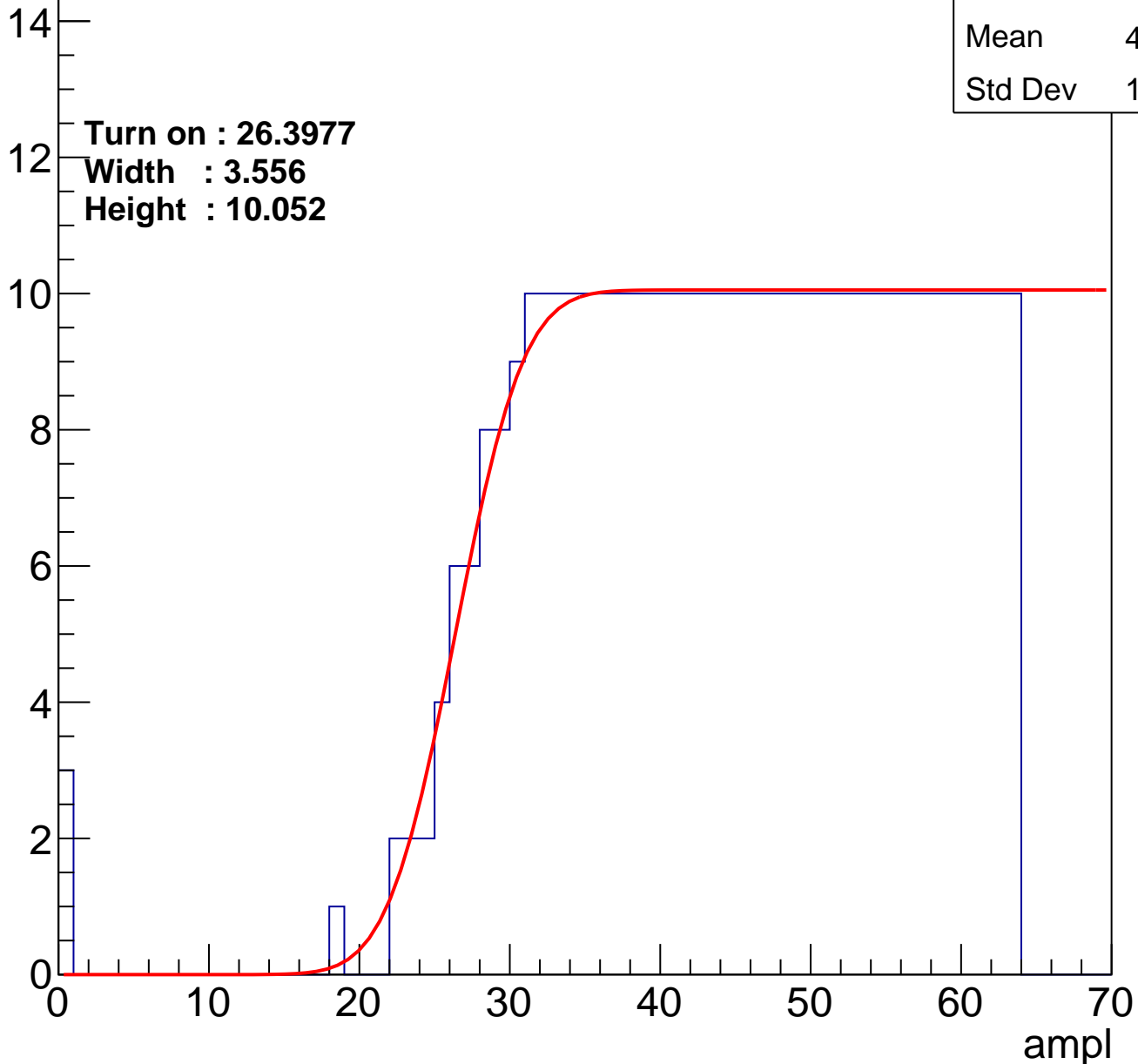
Entries	381
Mean	44.12
Std Dev	11.77

Turn on : 26.3977

Width : 3.556

Height : 10.052

Entry



B0L000S, U24-ch124

calib_packv5_042523_0143.root, FC#5, port B1

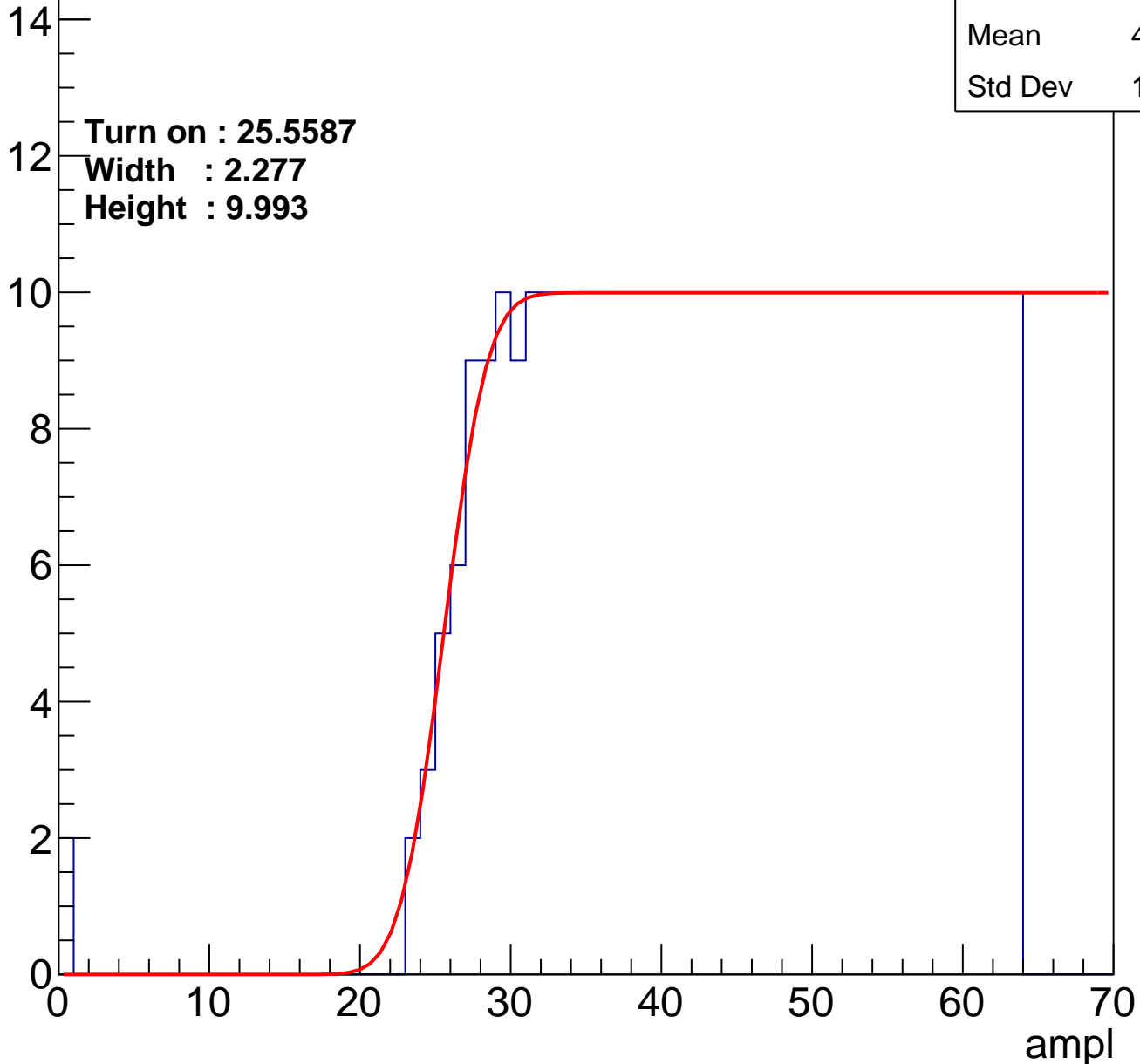
Entries	385
Mean	44.06
Std Dev	11.57

Turn on : 25.5587

Width : 2.277

Height : 9.993

Entry



B0L000S, U24-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.66
Std Dev	11.31

Turn on : 27.1283

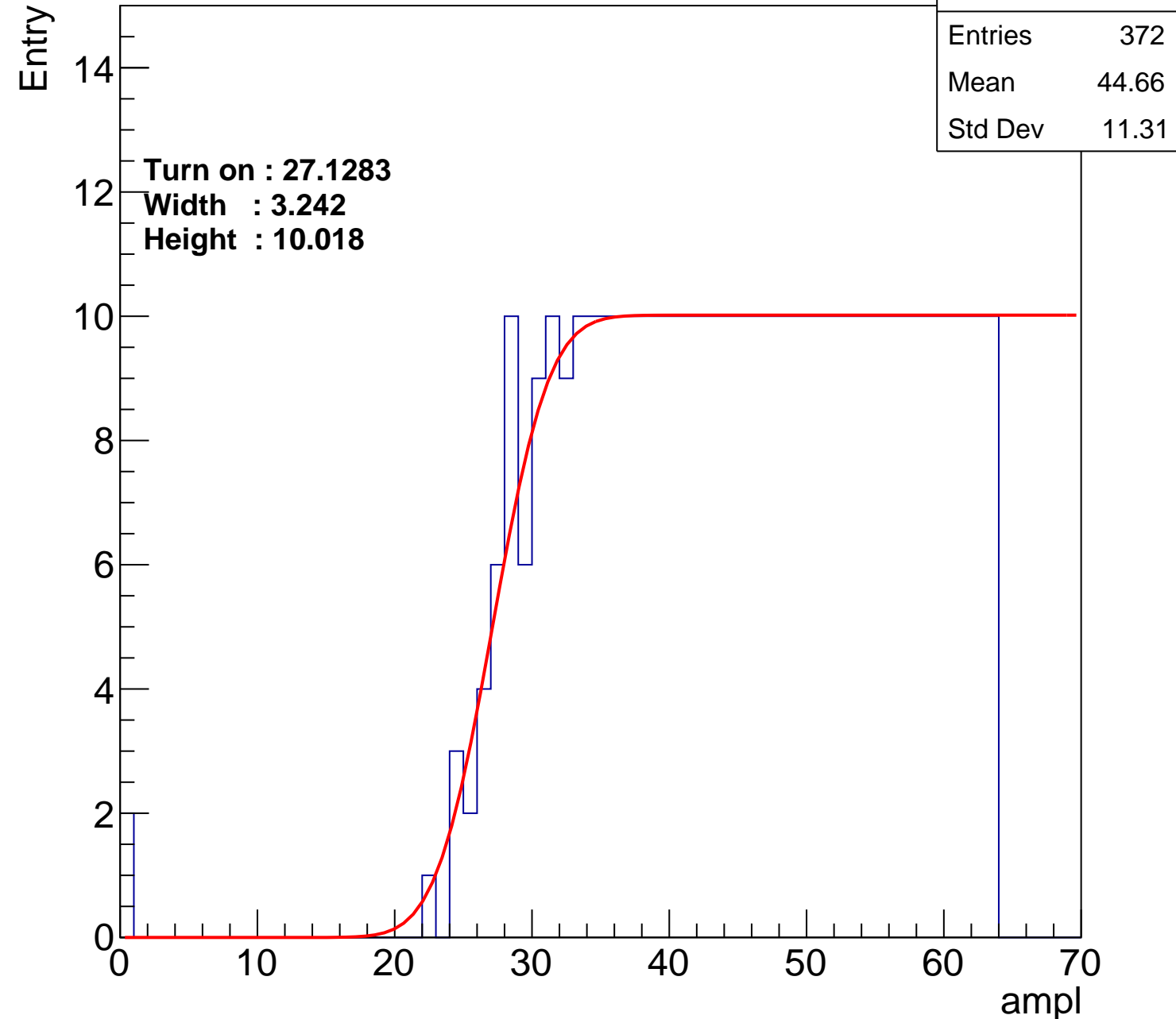
Width : 3.242

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 28.1095

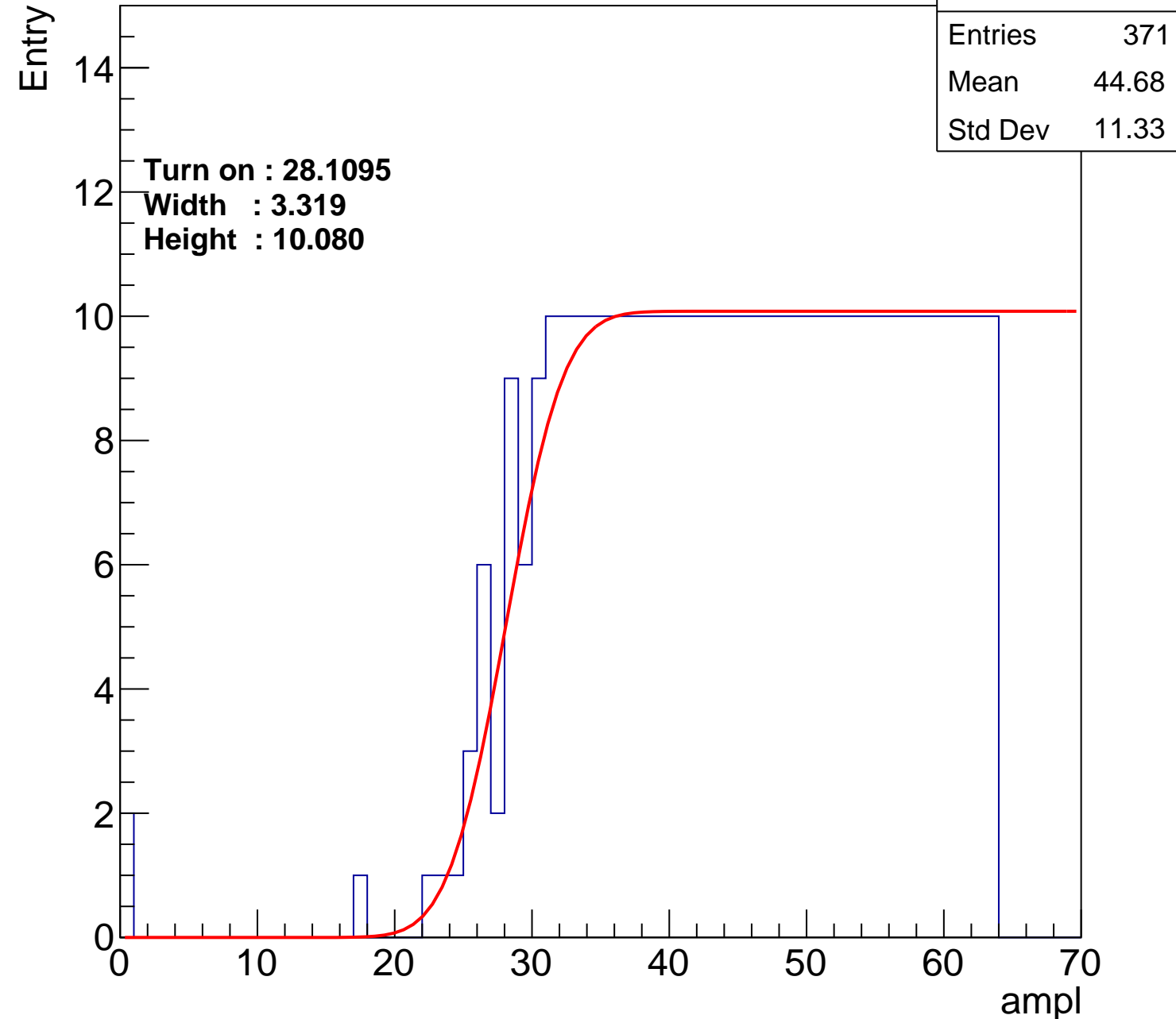
Width : 3.319

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.09
Std Dev	11.6

Turn on : 26.3492

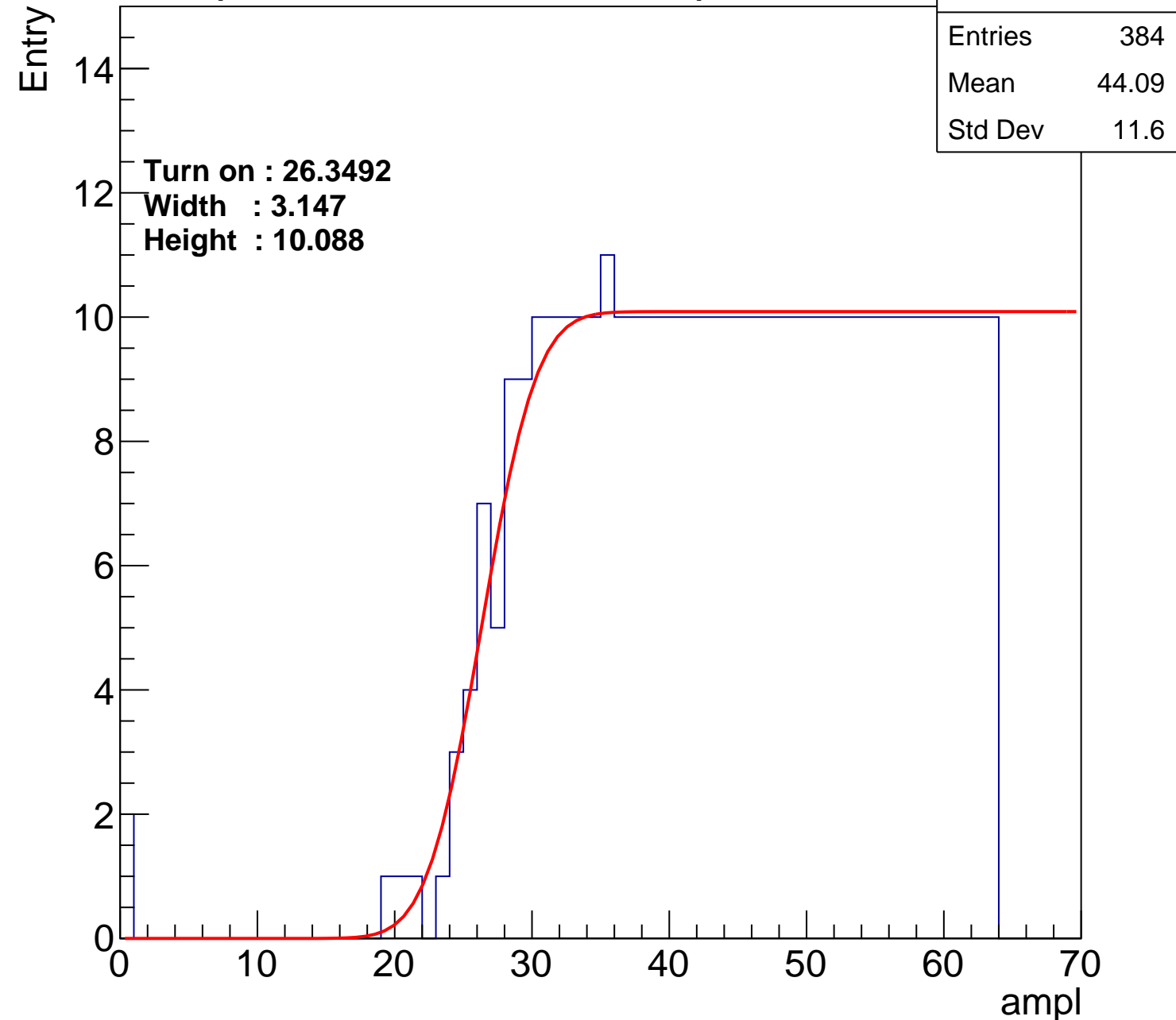
Width : 3.147

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U24-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.09
Std Dev	11.6

Turn on : 26.3492

Width : 3.147

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl

