

B1L103S, U23-ch0, adc0

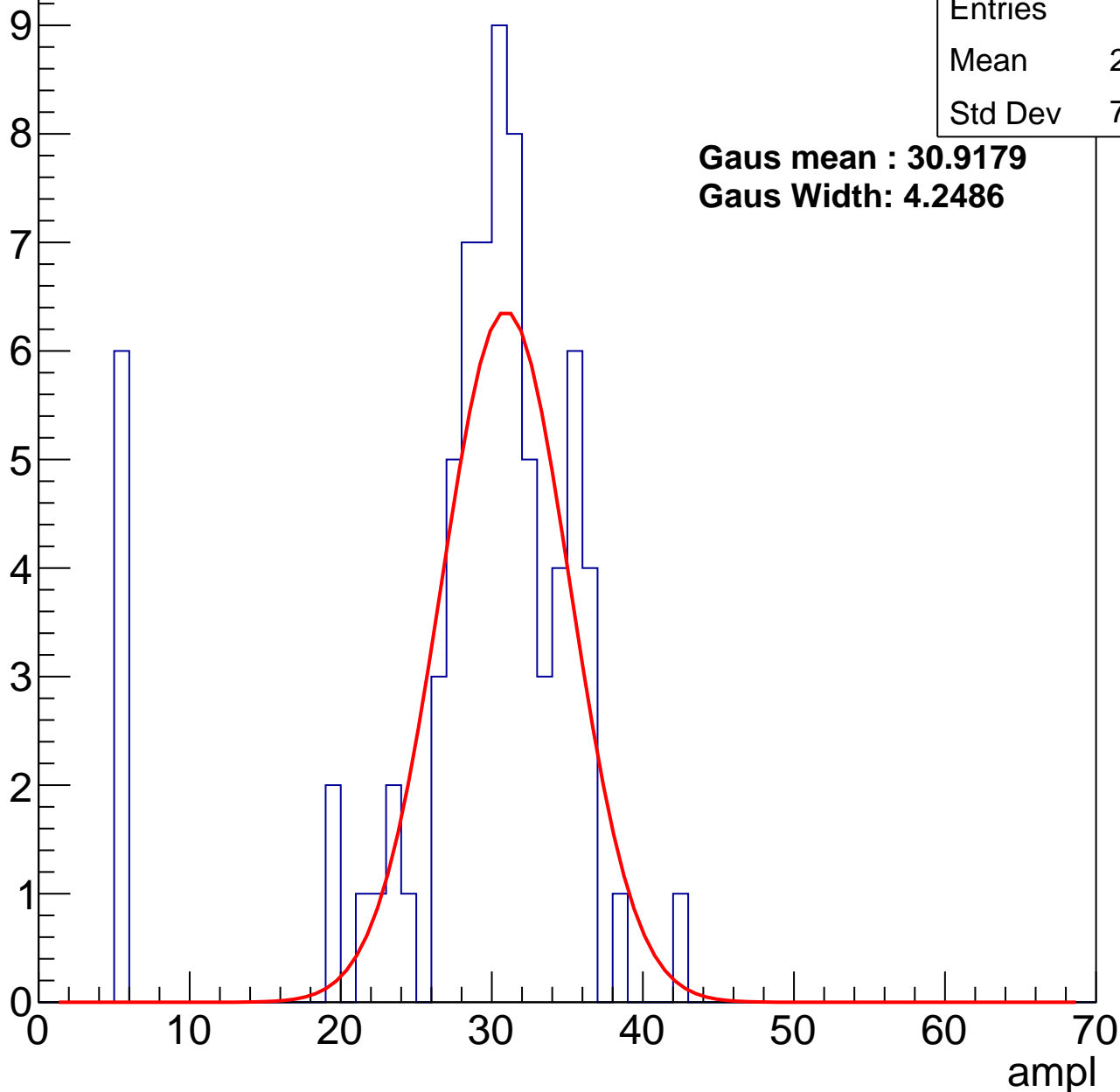
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.16
Std Dev	7.916

Gaus mean : 30.9179

Gaus Width: 4.2486



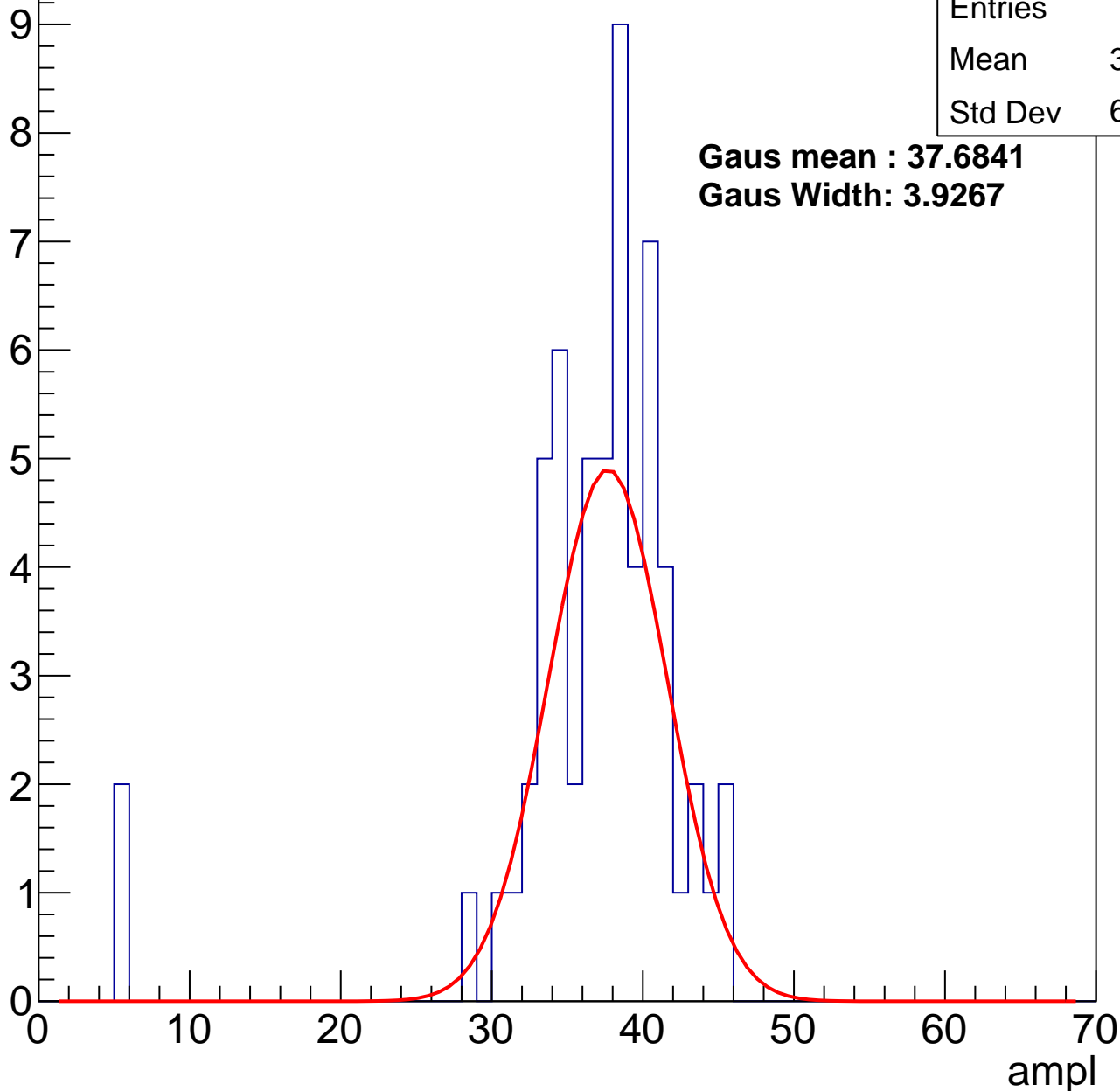
B1L103S, U23-ch0, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.18
Std Dev	6.828

Gaus mean : 37.6841
Gaus Width: 3.9267



B1L103S, U23-ch0, adc2

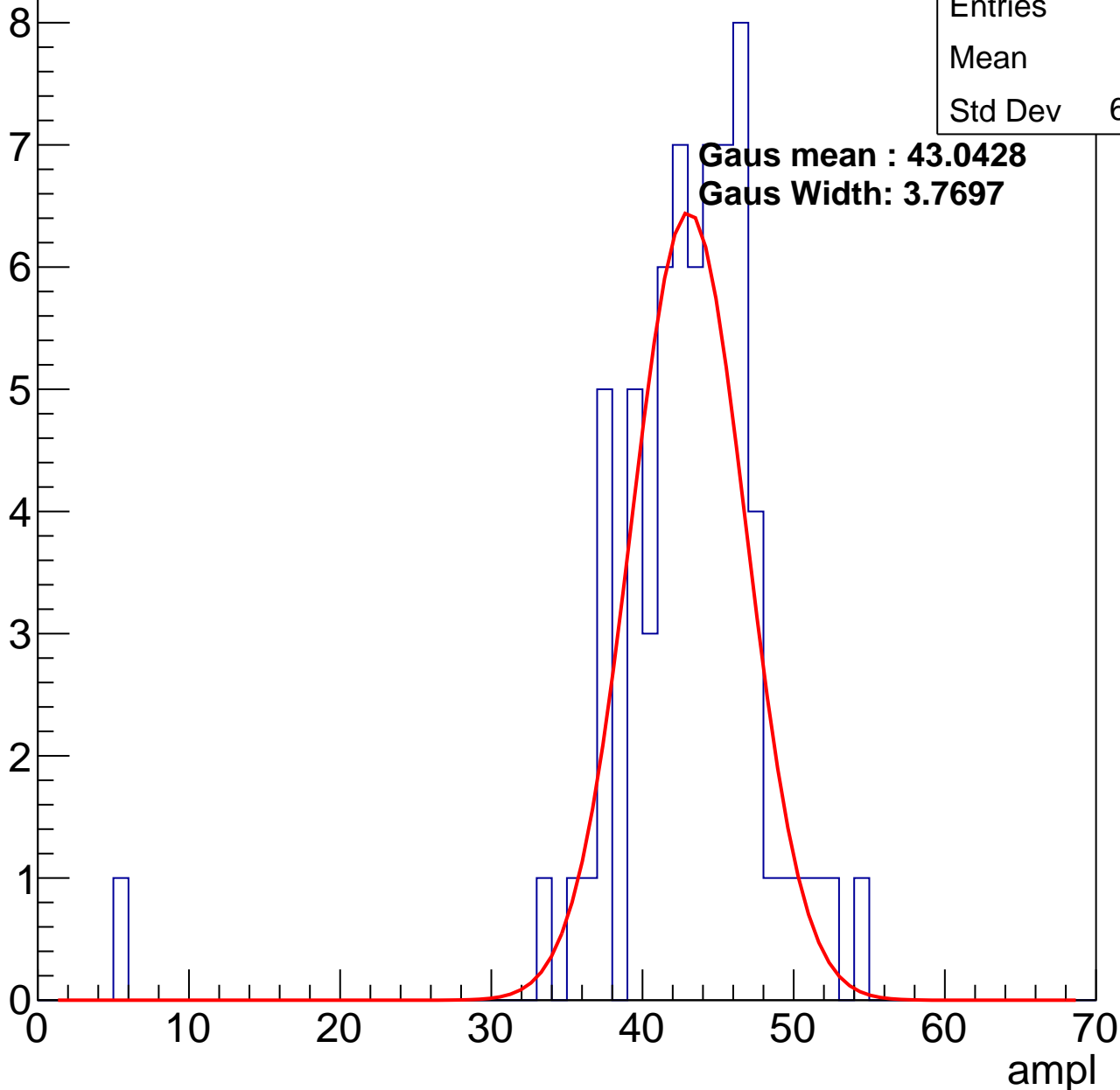
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.5
Std Dev	6.069

Gaus mean : 43.0428

Gaus Width: 3.7697

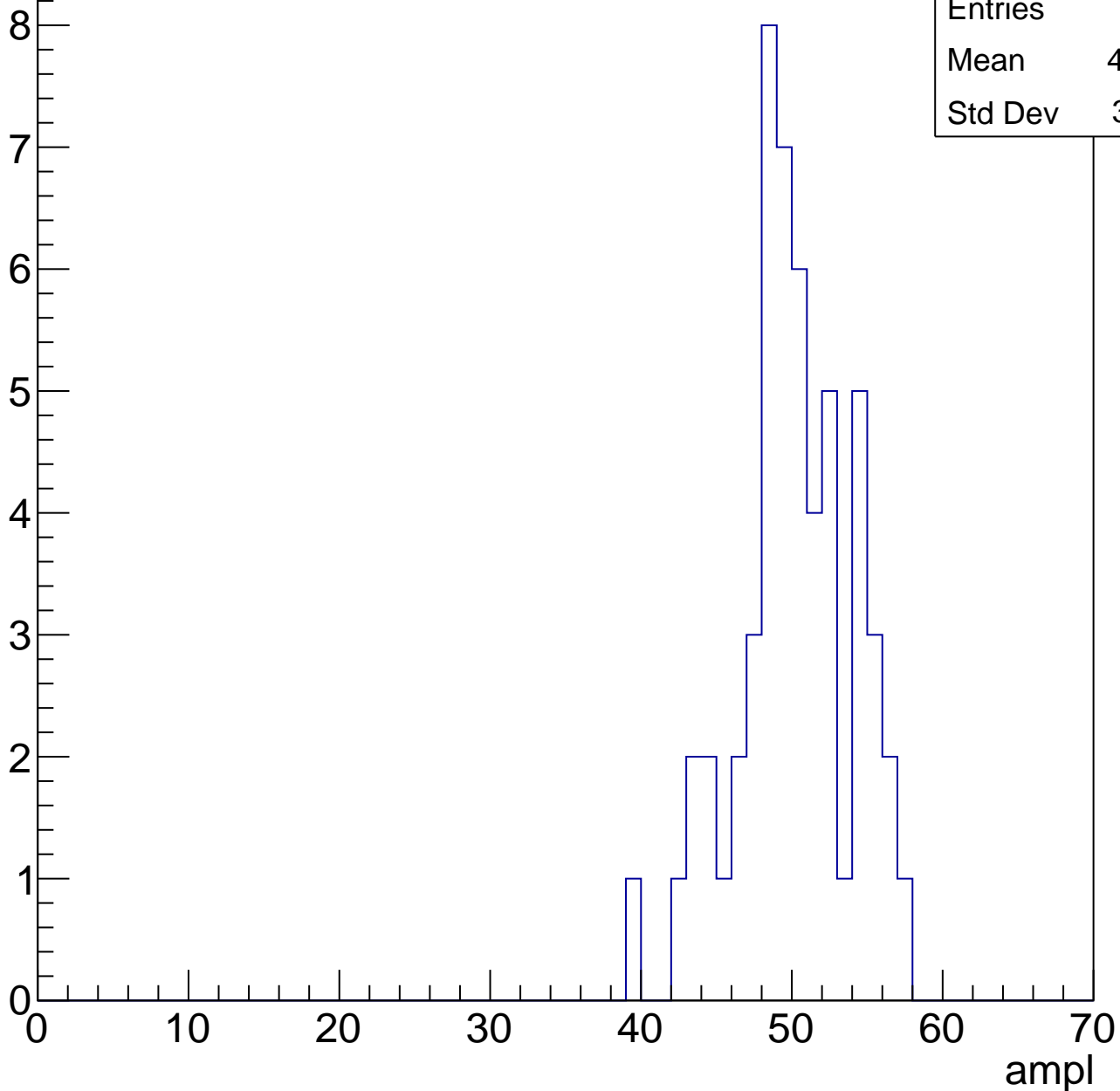


B1L103S, U23-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	49.65
Std Dev	3.801

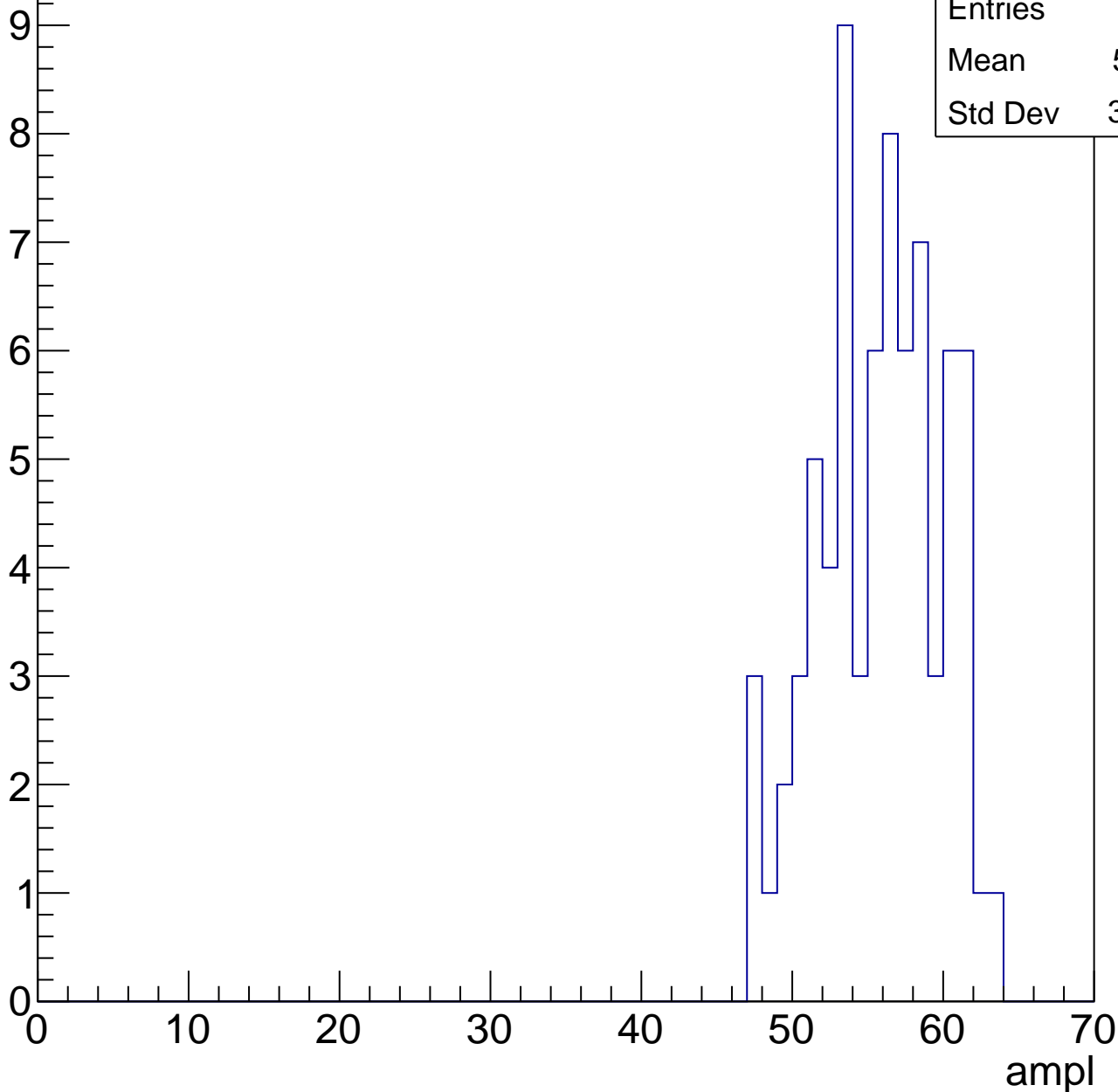


B1L103S, U23-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	55.31
Std Dev	3.956

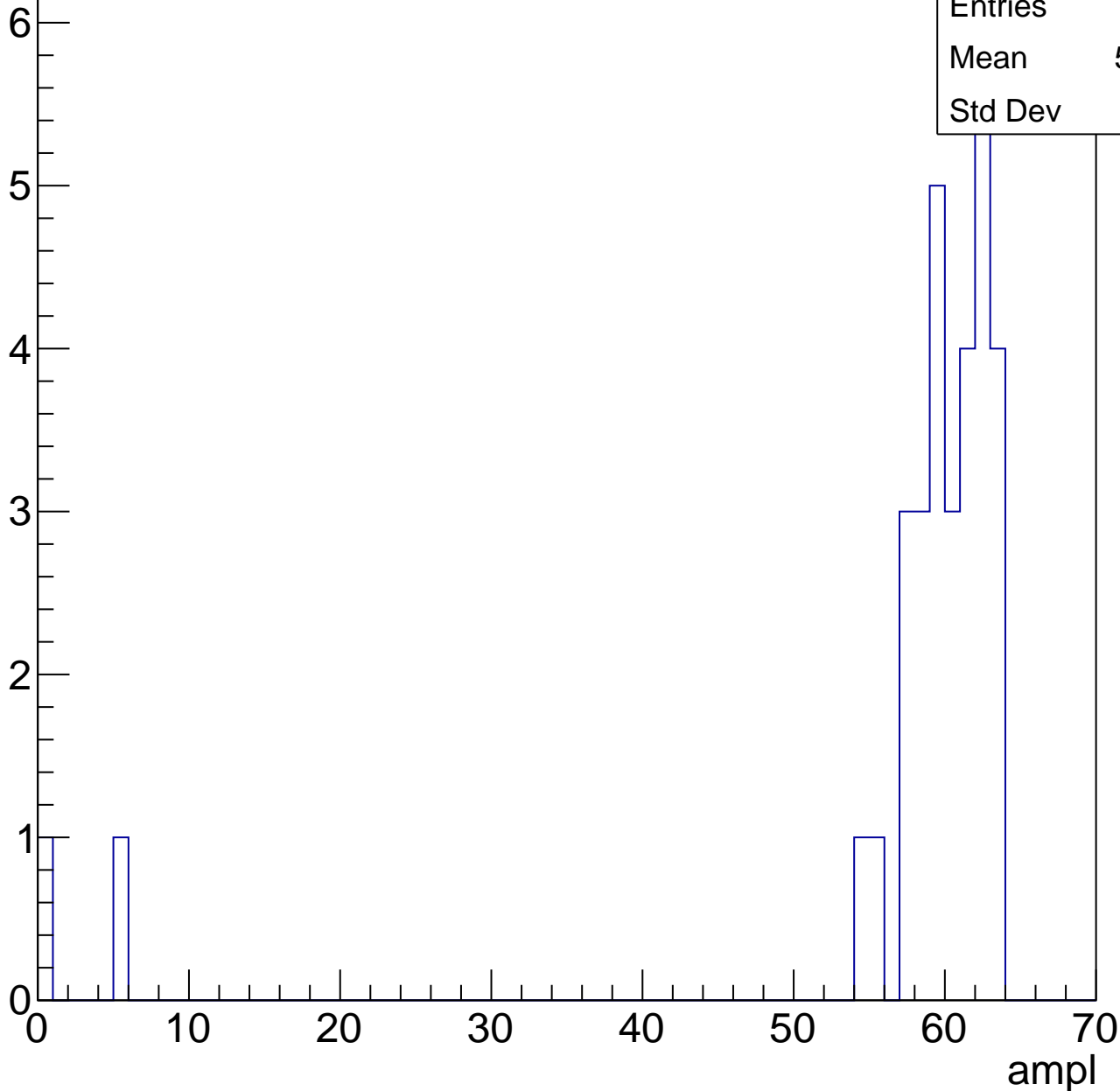


B1L103S, U23-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	56.31
Std Dev	14.1

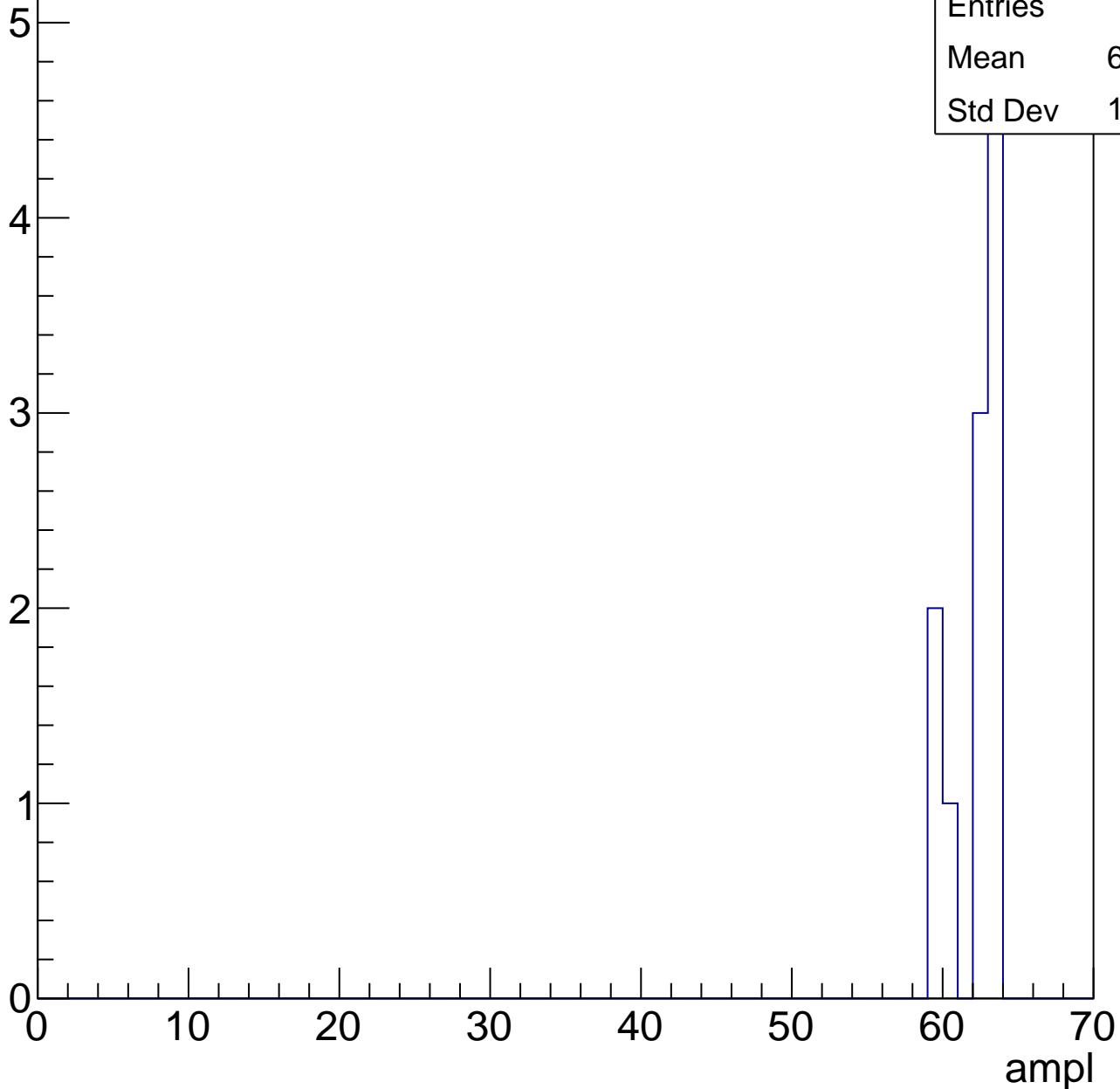


B1L103S, U23-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	61.73
Std Dev	1.543



B1L103S, U23-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch1, adc0

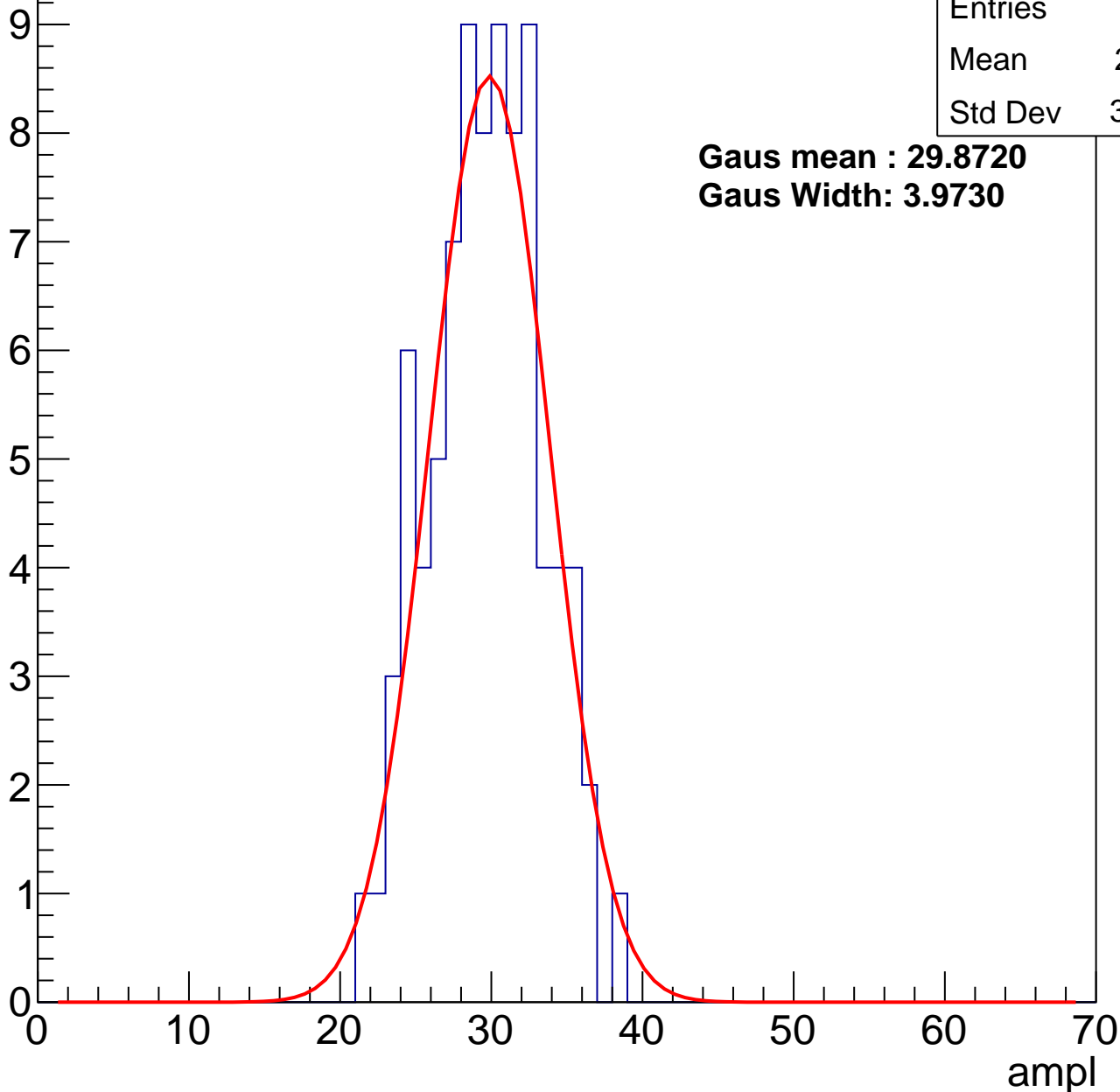
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	29.21
Std Dev	3.633

Gaus mean : 29.8720

Gaus Width: 3.9730



B1L103S, U23-ch1, adc1

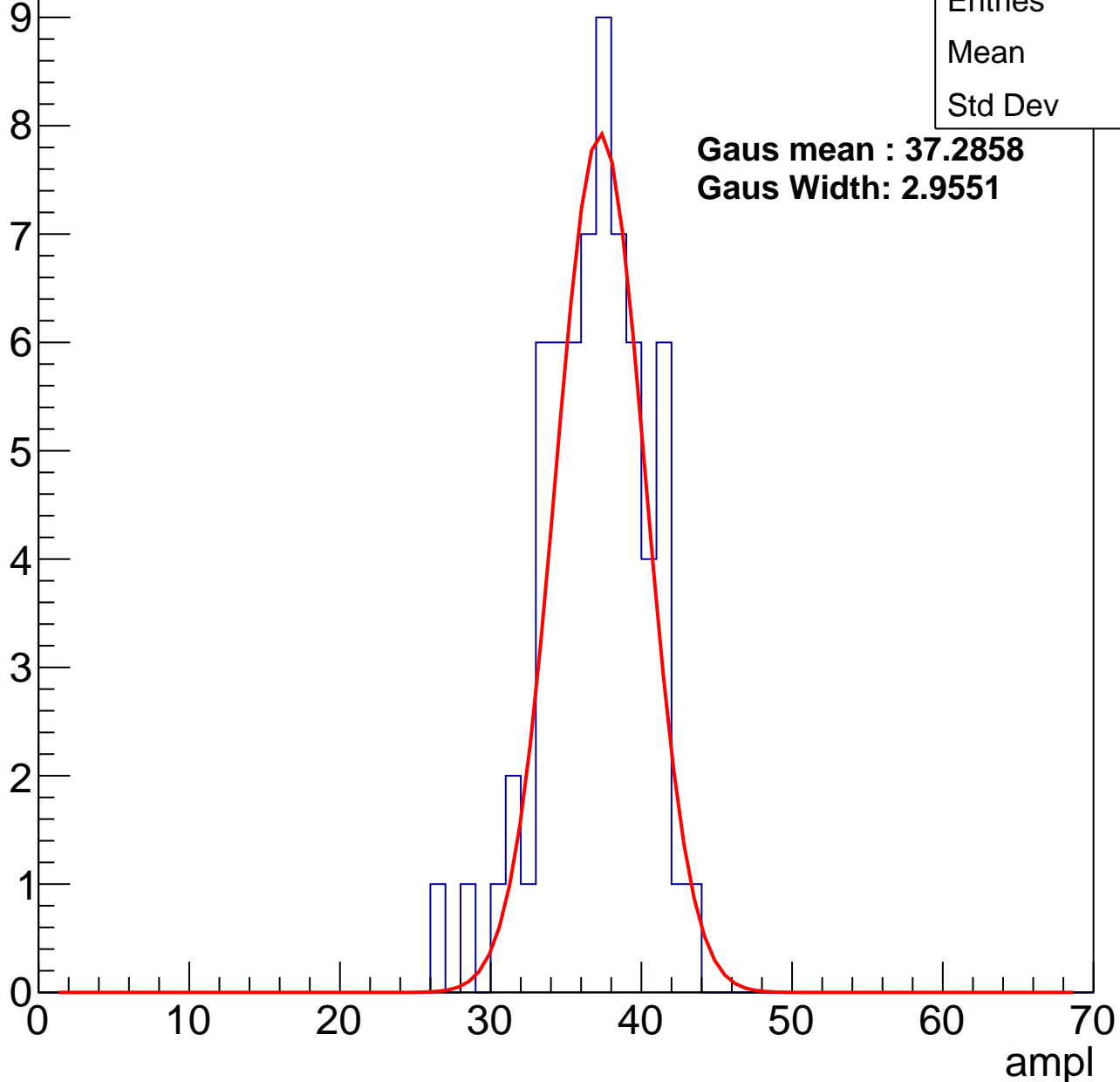
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.4
Std Dev	3.35

Gaus mean : 37.2858

Gaus Width: 2.9551



B1L103S, U23-ch1, adc2

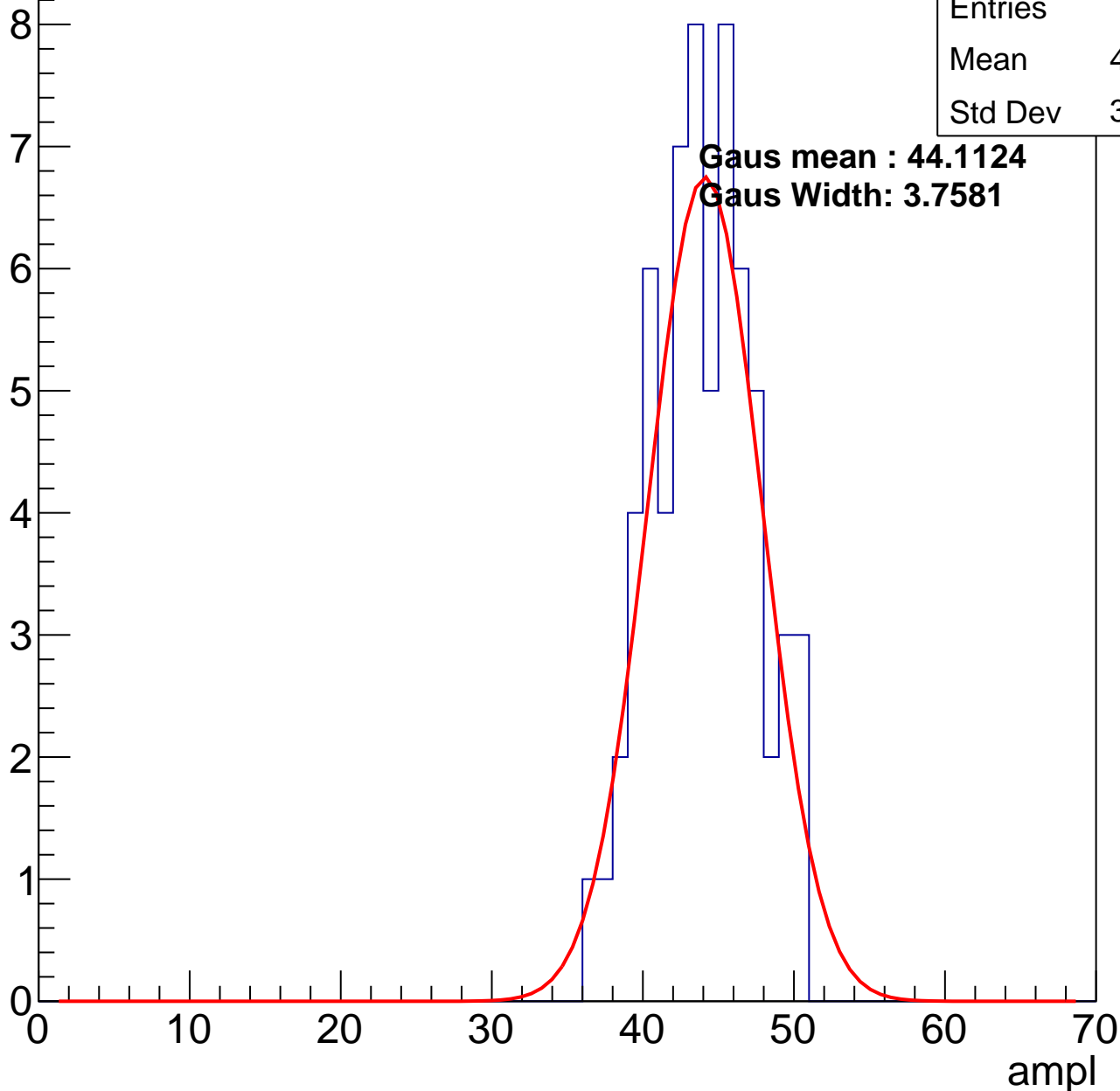
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	43.55
Std Dev	3.356

Gaus mean : 44.1124

Gaus Width: 3.7581

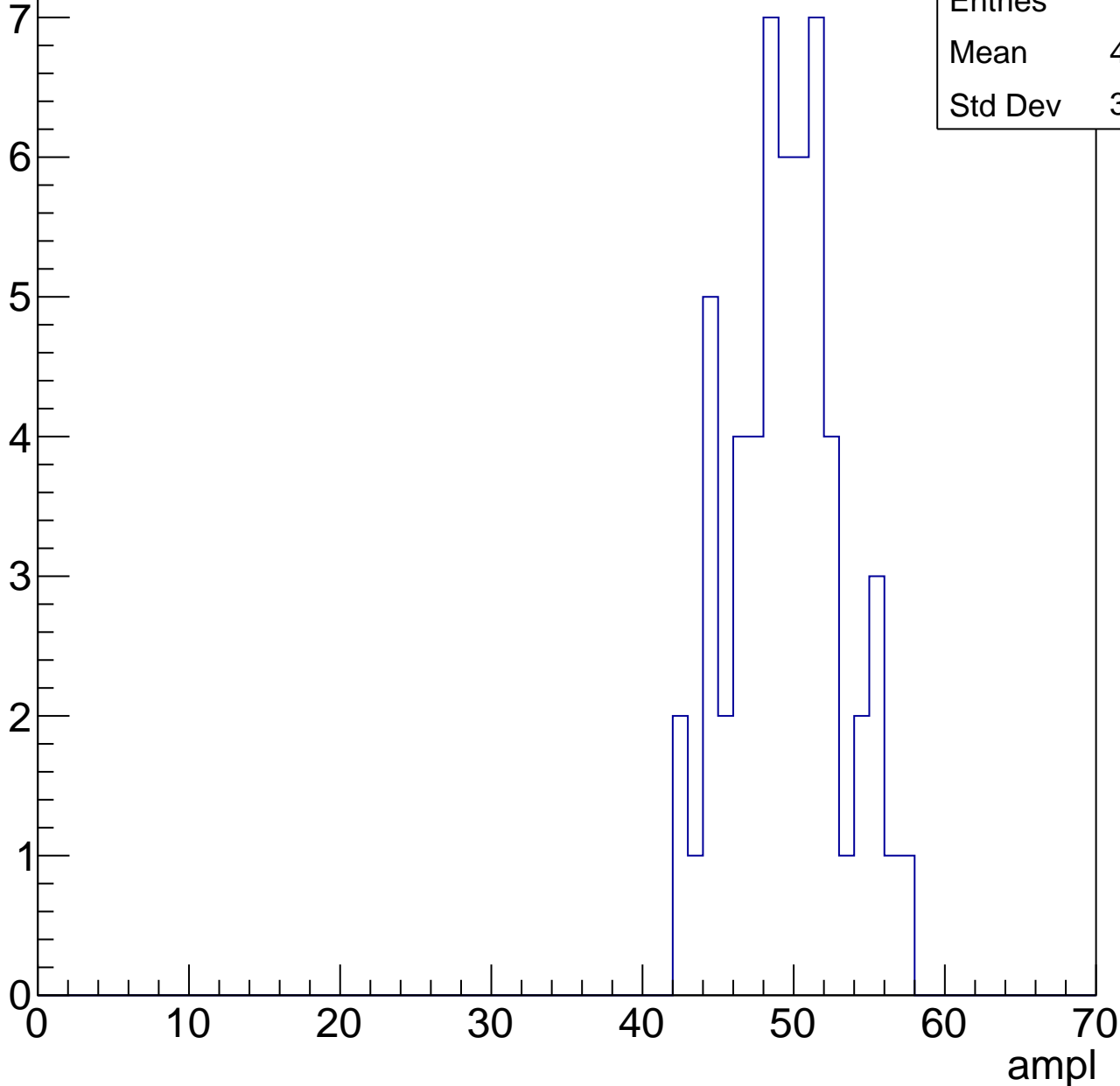


B1L103S, U23-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	48.98
Std Dev	3.553

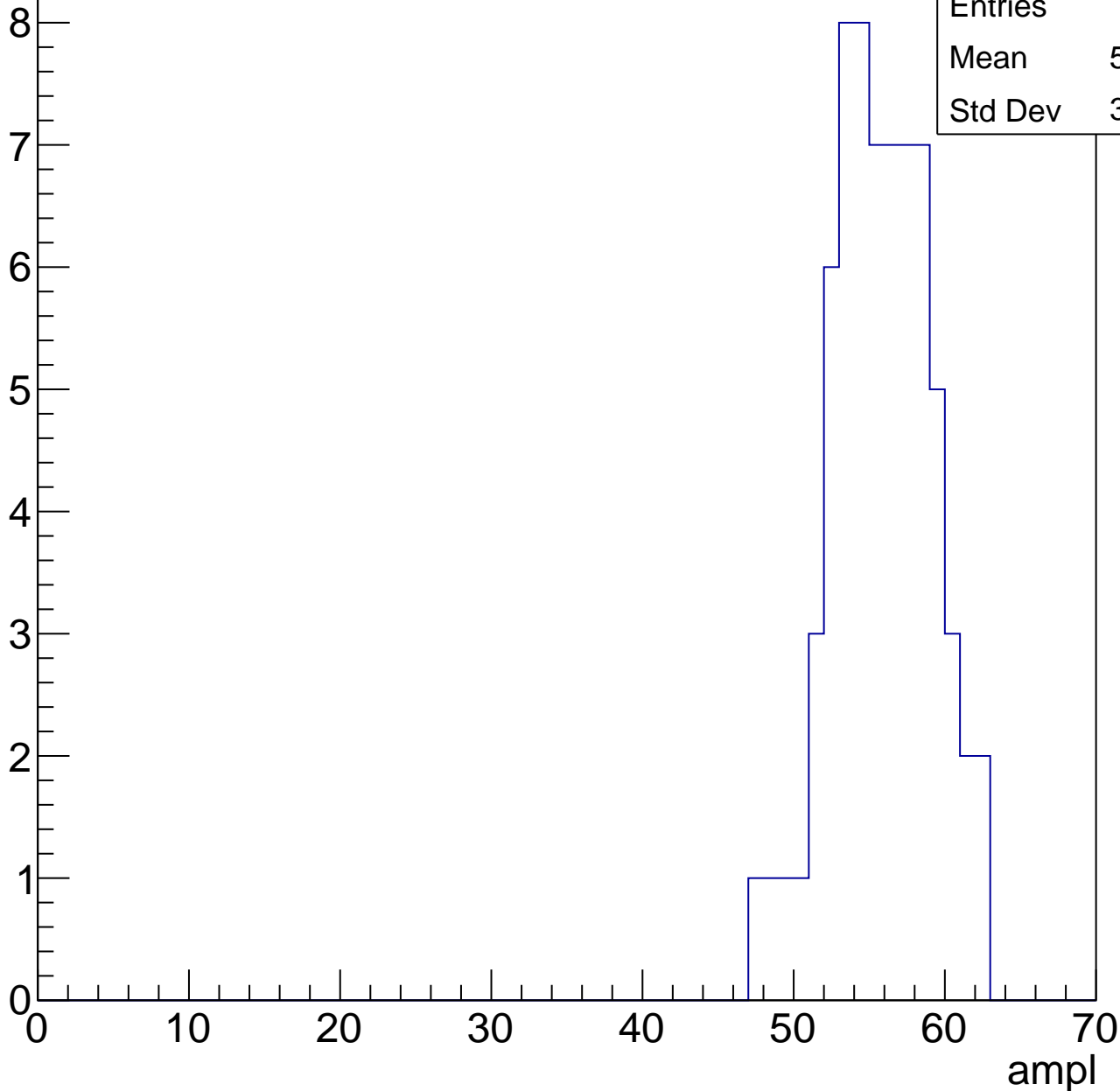


B1L103S, U23-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	55.33
Std Dev	3.256

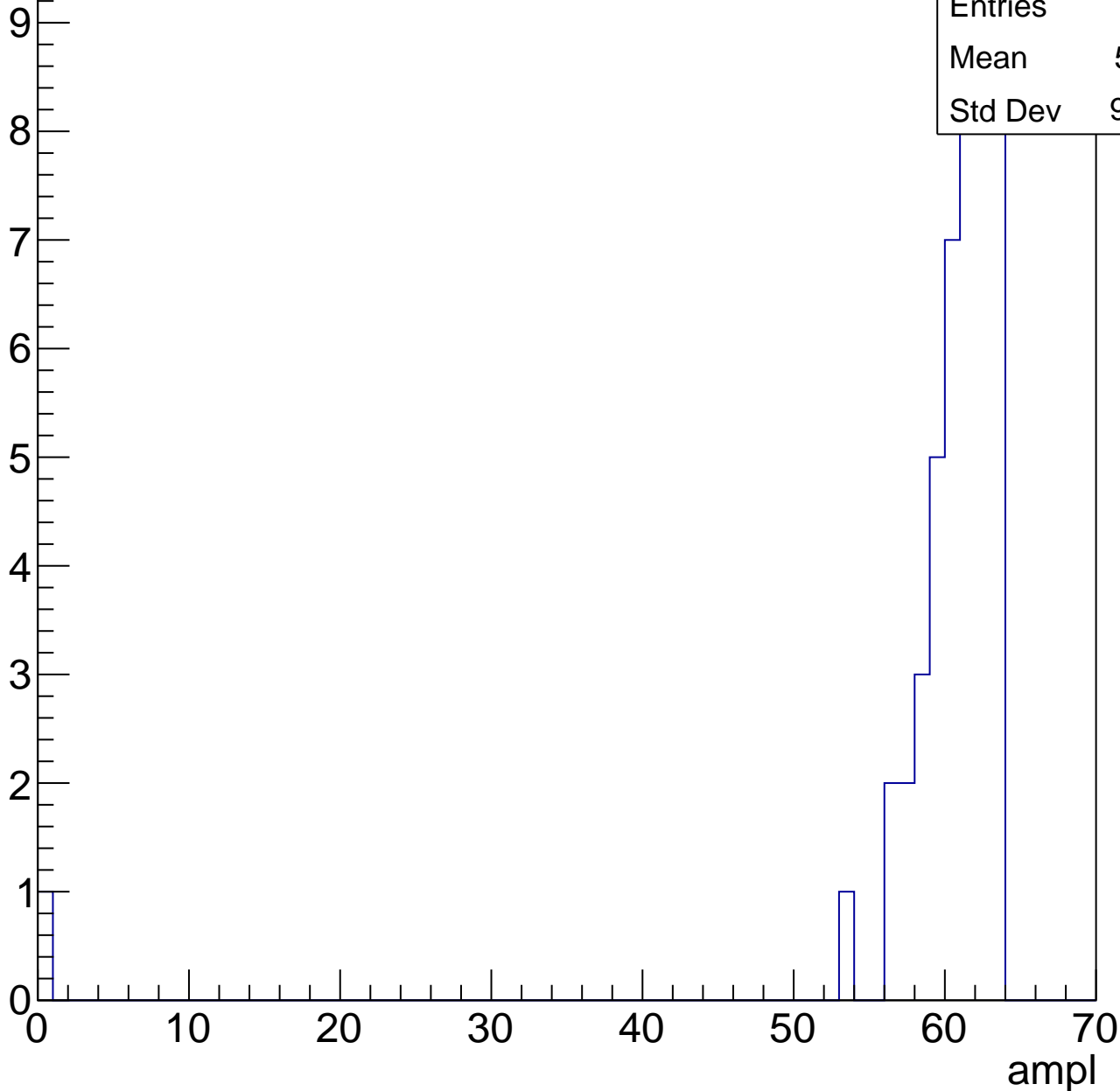


B1L103S, U23-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	59.11
Std Dev	9.089



B1L103S, U23-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U23-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B1L103S, U23-ch2, adc0

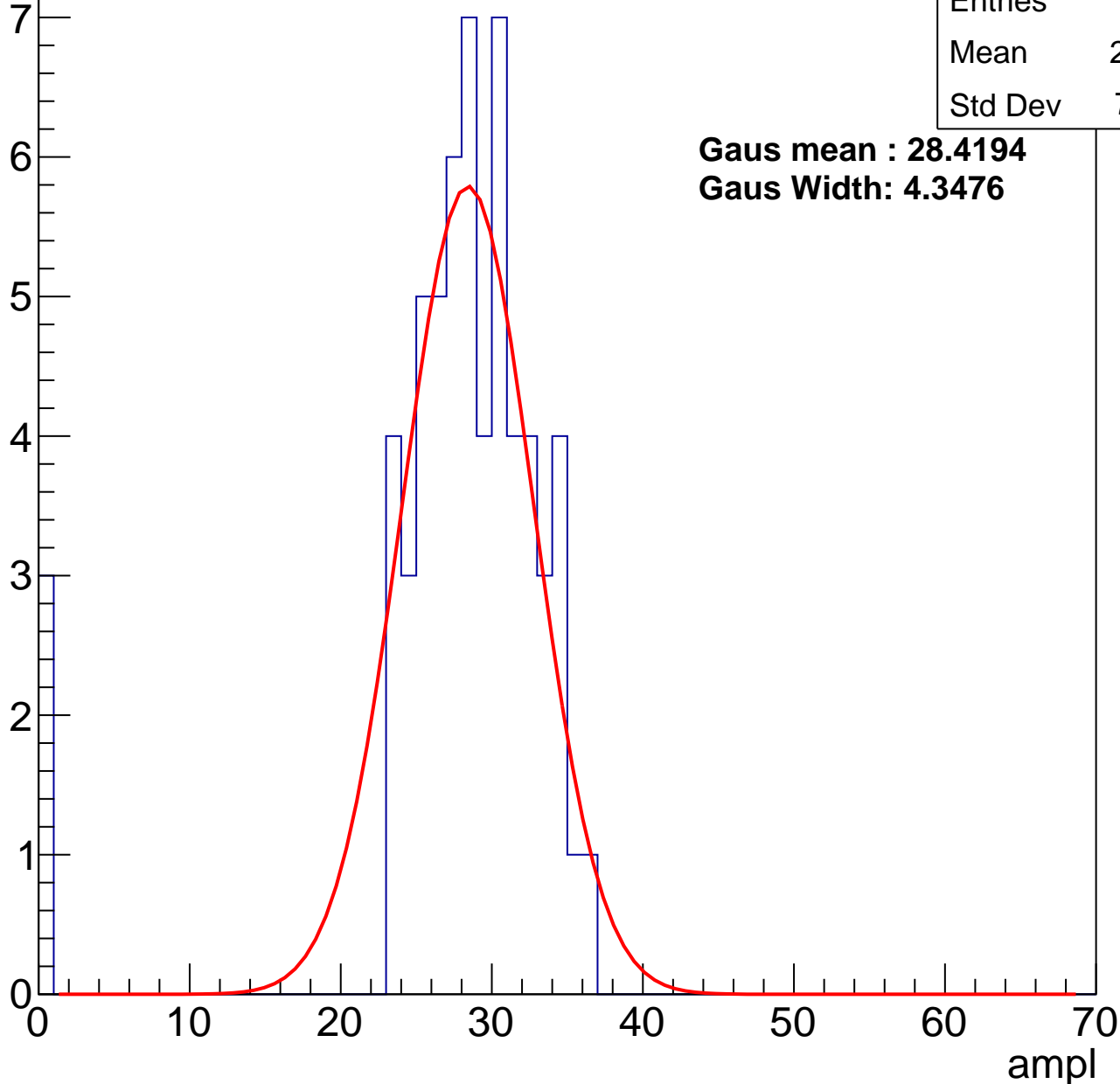
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	27.23
Std Dev	7.011

Gaus mean : 28.4194

Gaus Width: 4.3476



B1L103S, U23-ch2, adc1

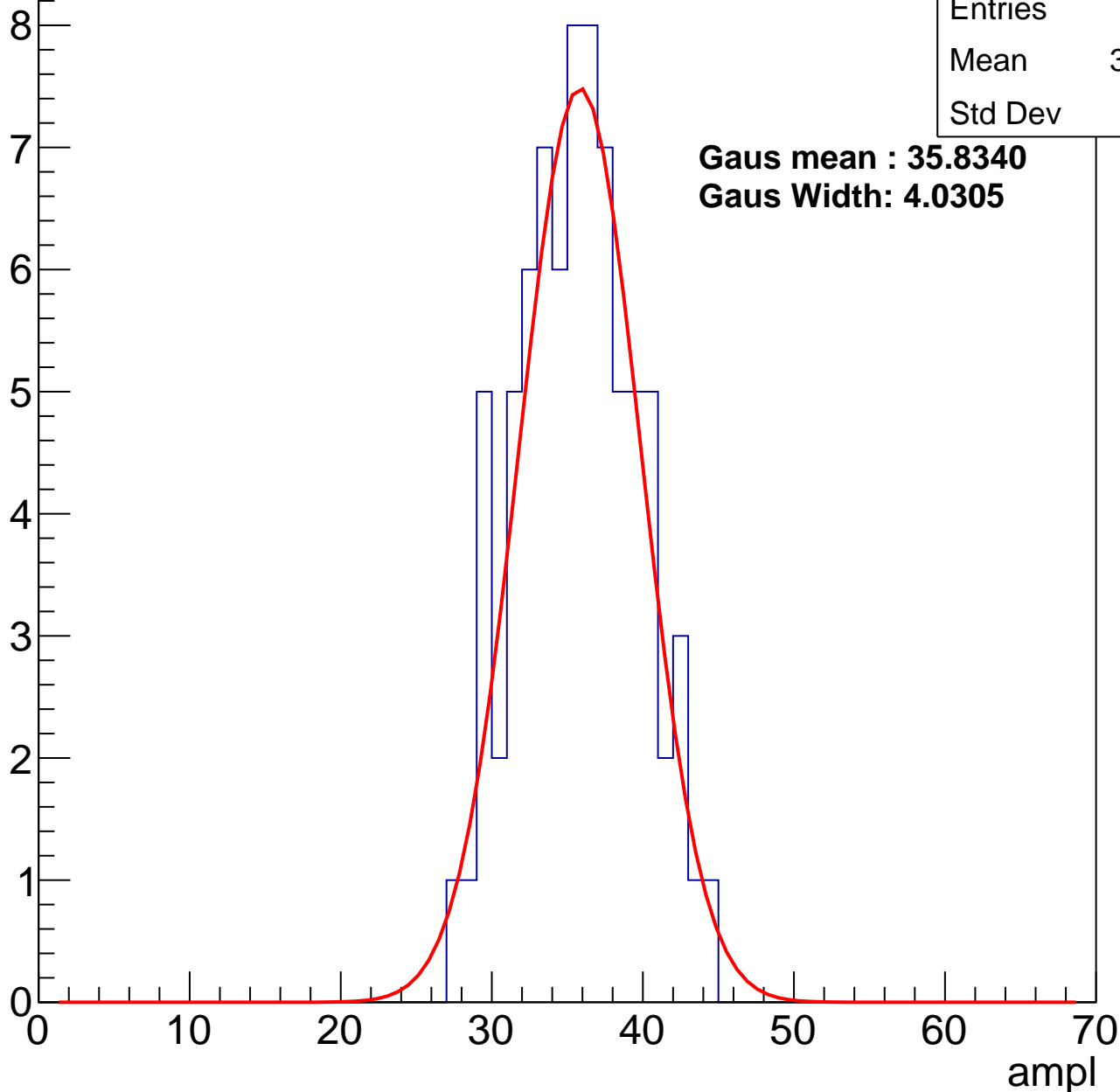
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	35.24
Std Dev	3.85

Gaus mean : 35.8340

Gaus Width: 4.0305



B1L103S, U23-ch2, adc2

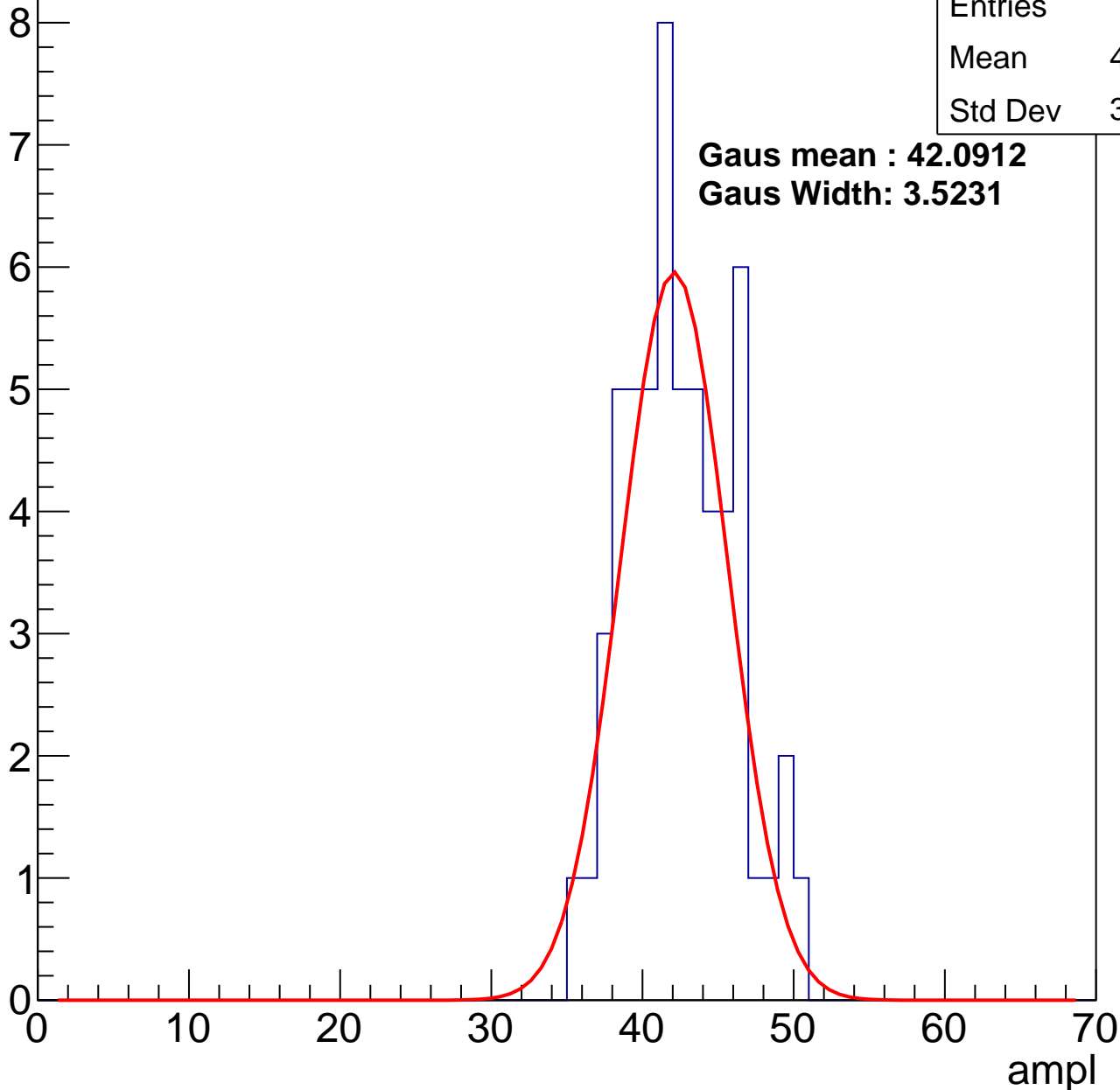
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.02
Std Dev	3.477

Gaus mean : 42.0912

Gaus Width: 3.5231

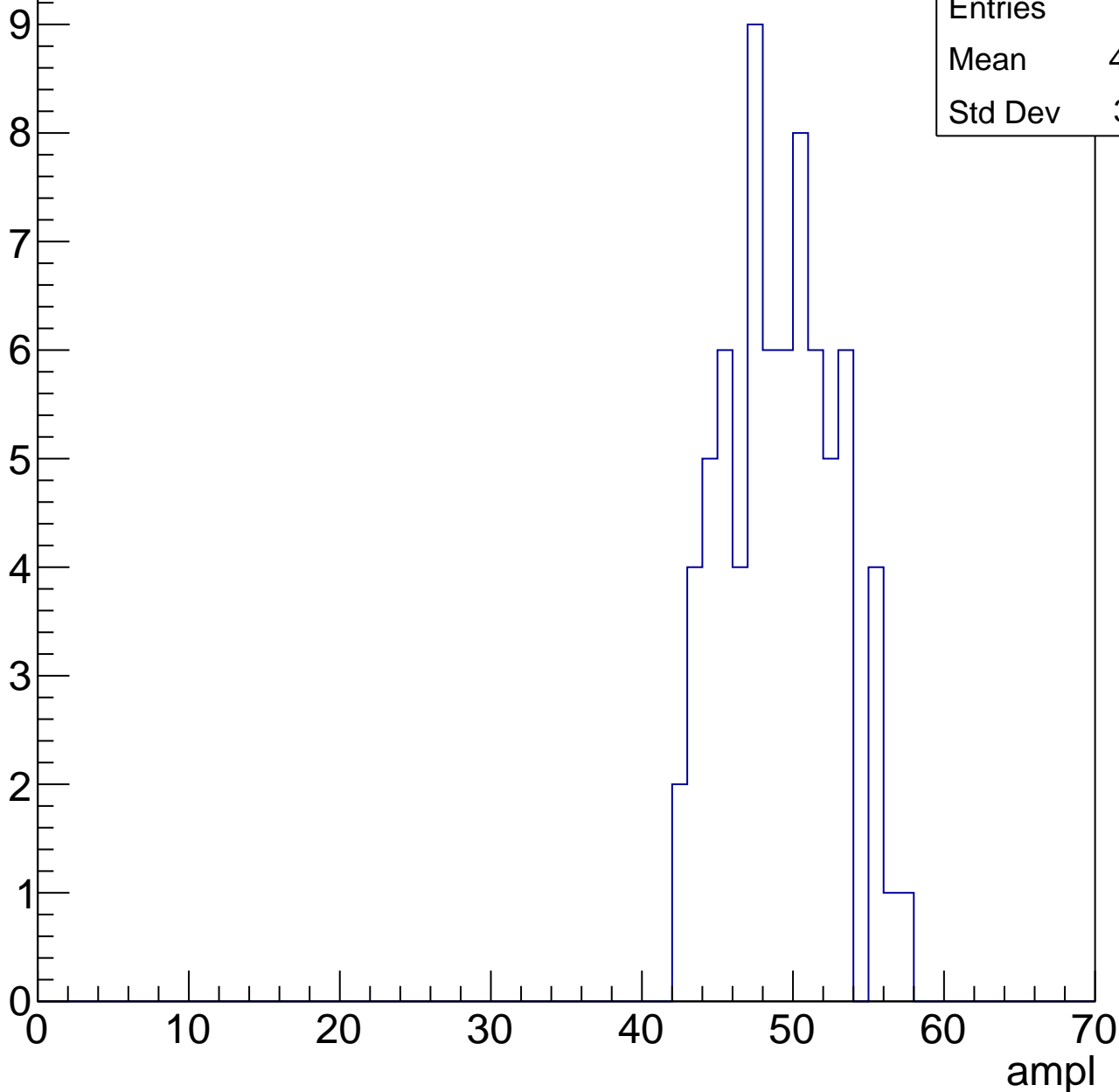


B1L103S, U23-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

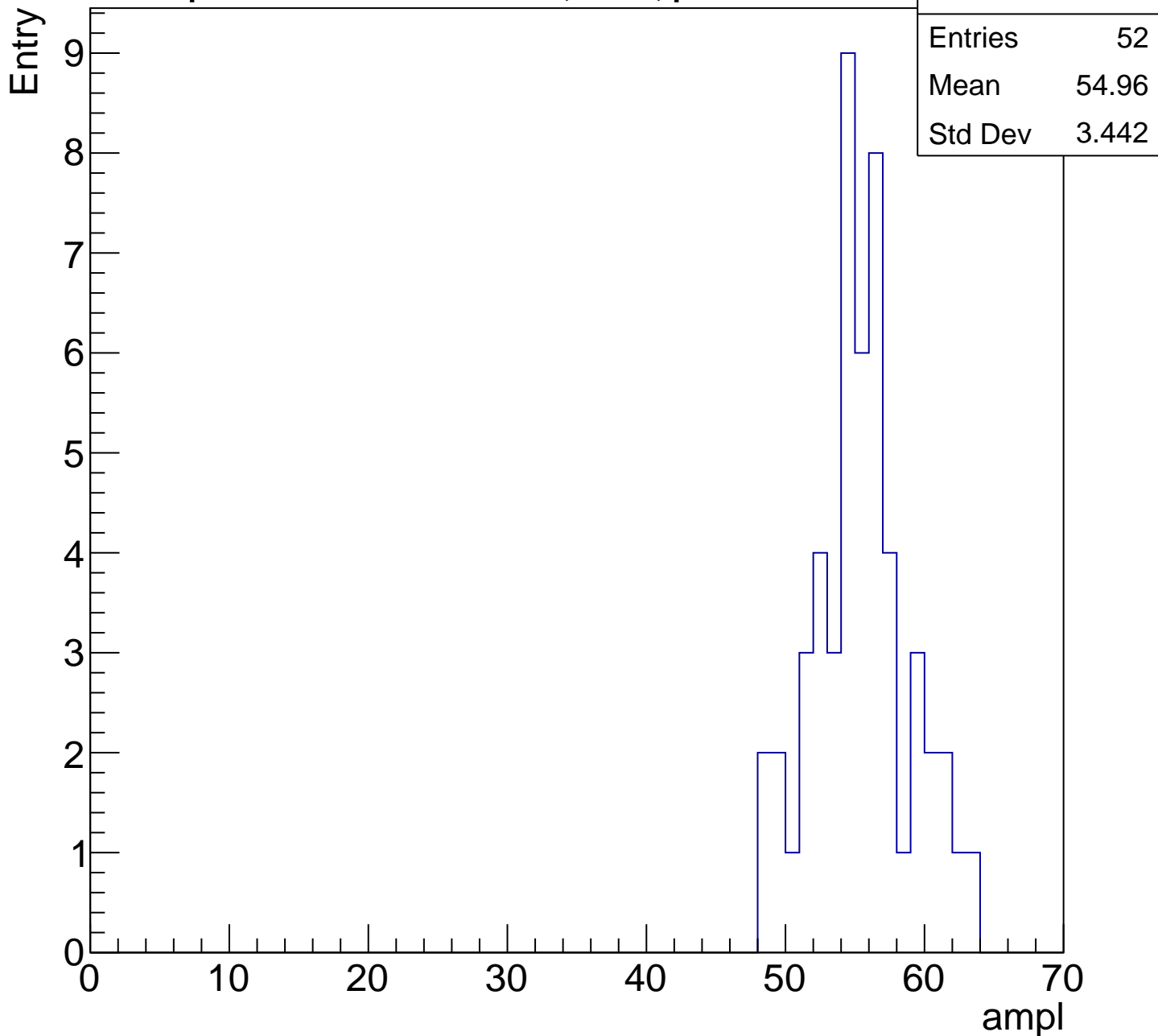
Entry

Entries	73
Mean	48.66
Std Dev	3.631



B1L103S, U23-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

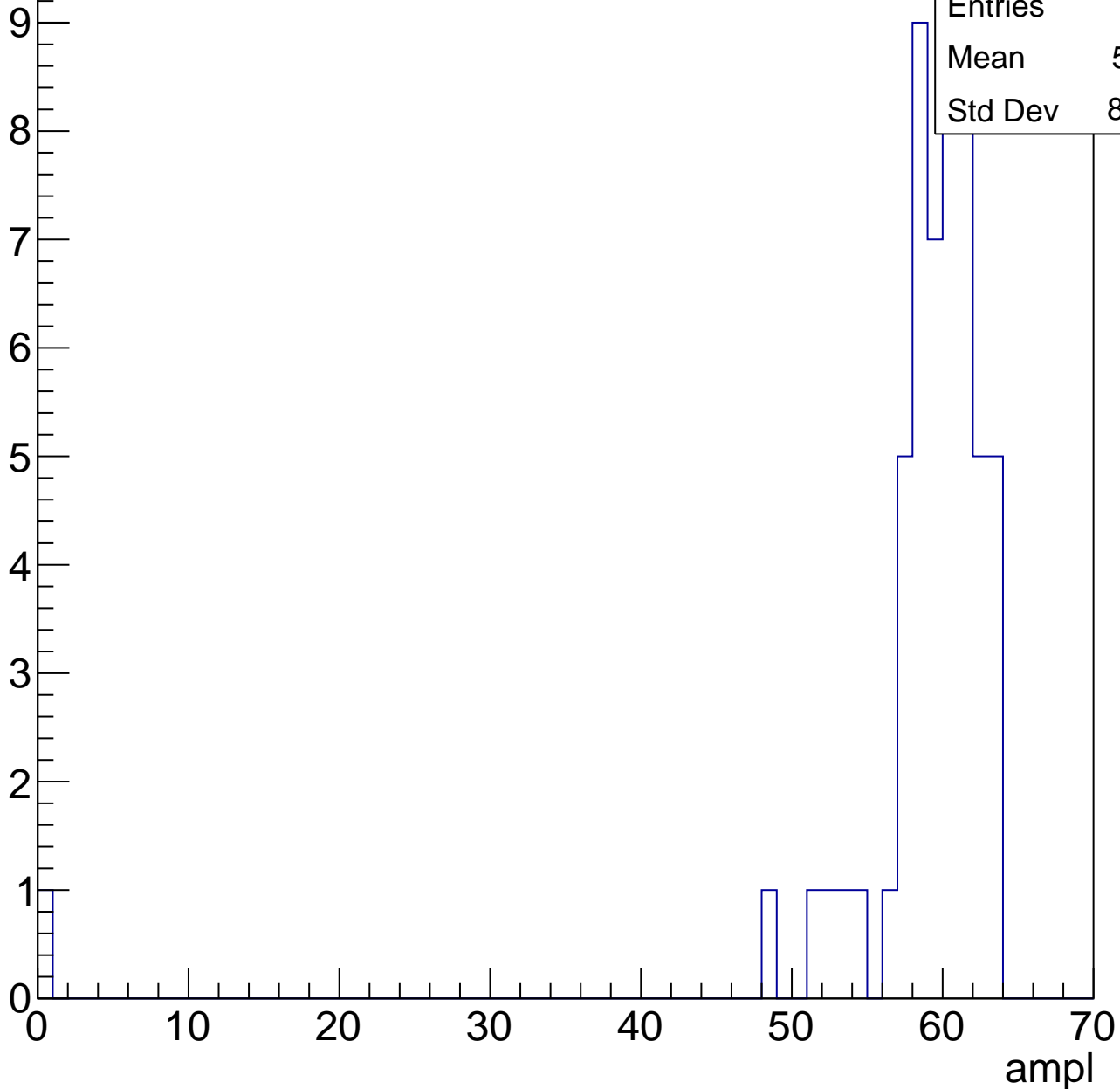


B1L103S, U23-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

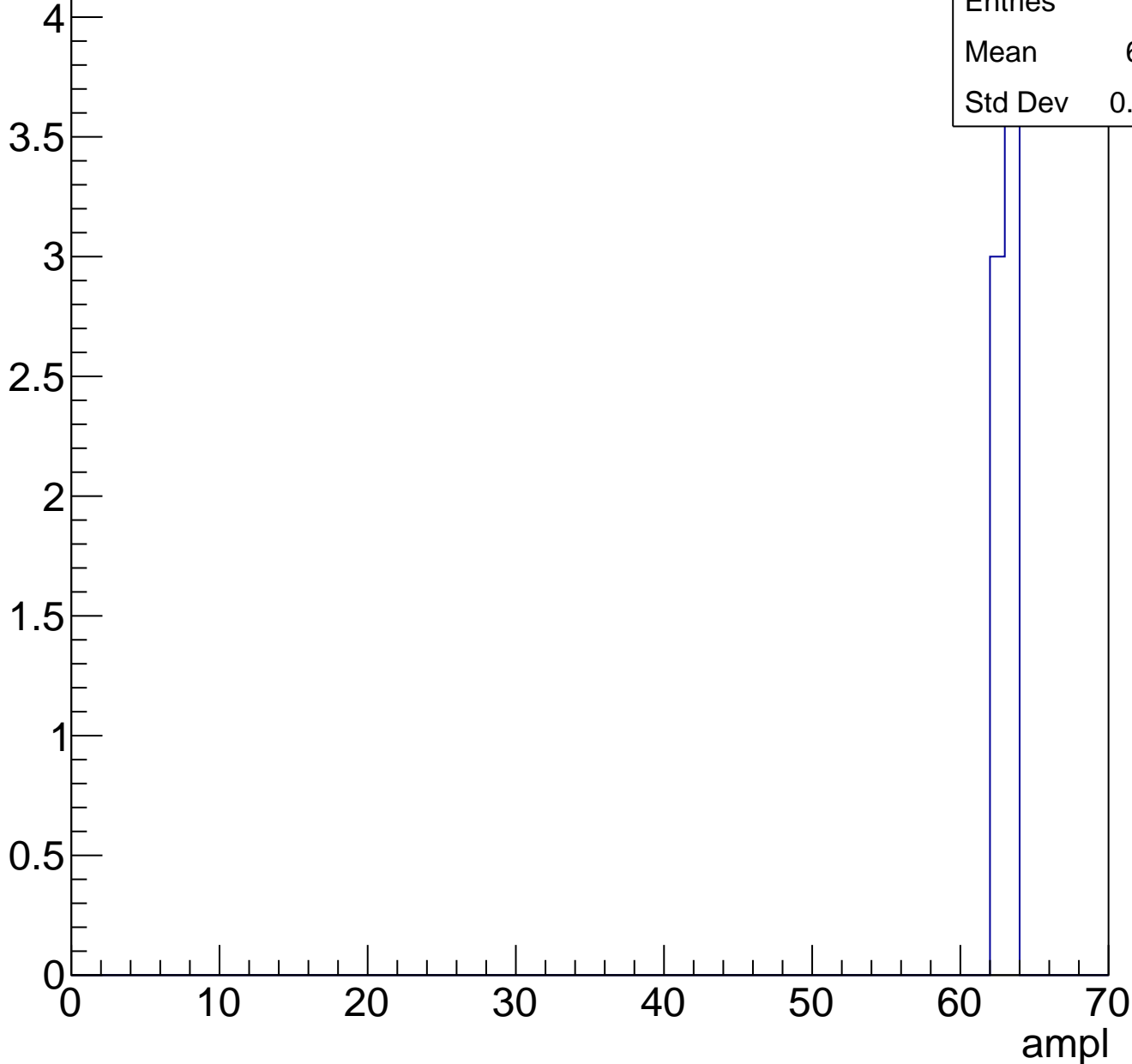
Entries	54
Mean	57.91
Std Dev	8.514



B1L103S, U23-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	62
Std Dev	0

ampl

B1L103S, U23-ch3, adc0

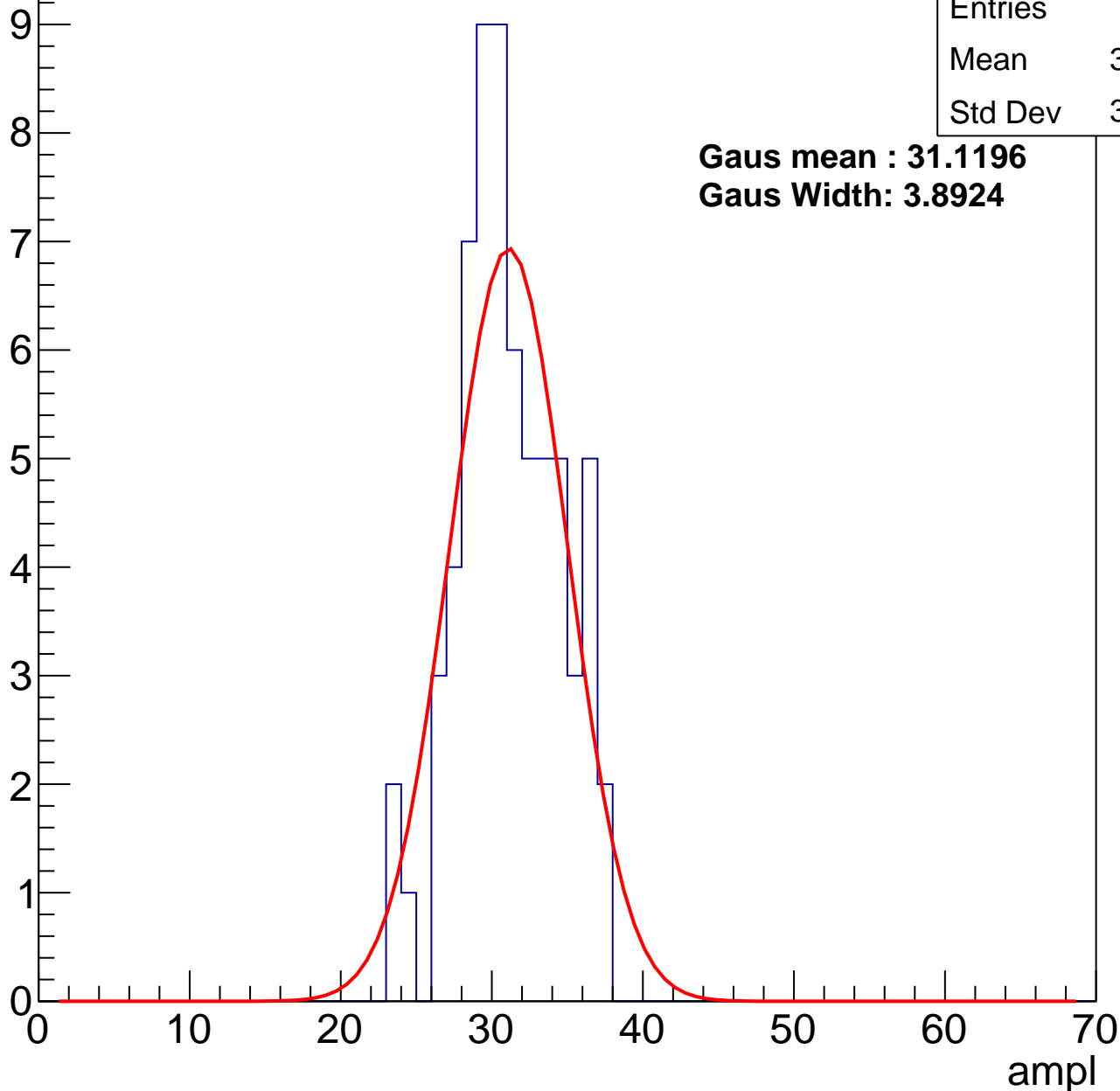
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	30.65
Std Dev	3.328

Gaus mean : 31.1196

Gaus Width: 3.8924



B1L103S, U23-ch3, adc1

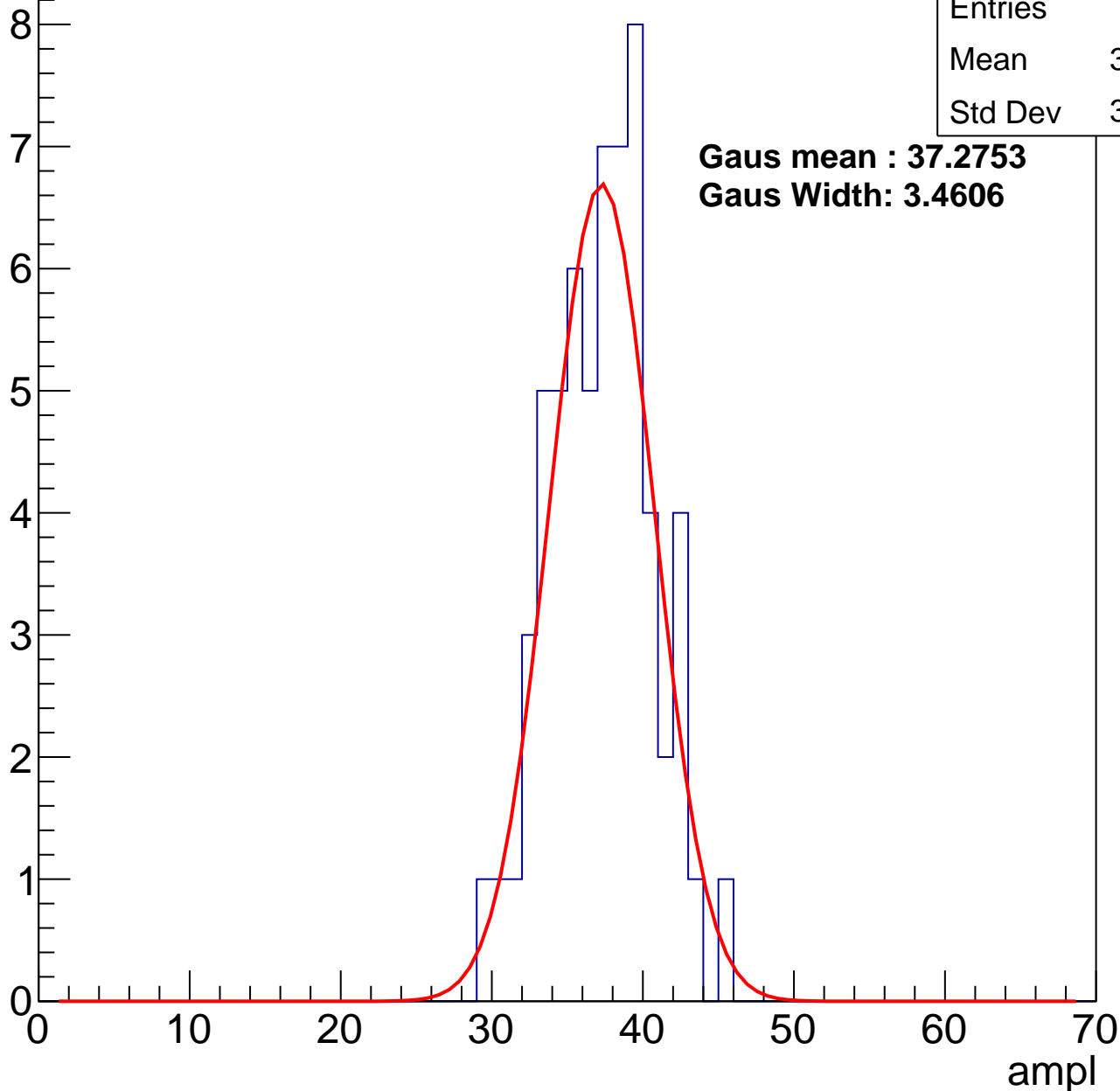
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	36.82
Std Dev	3.346

Gaus mean : 37.2753

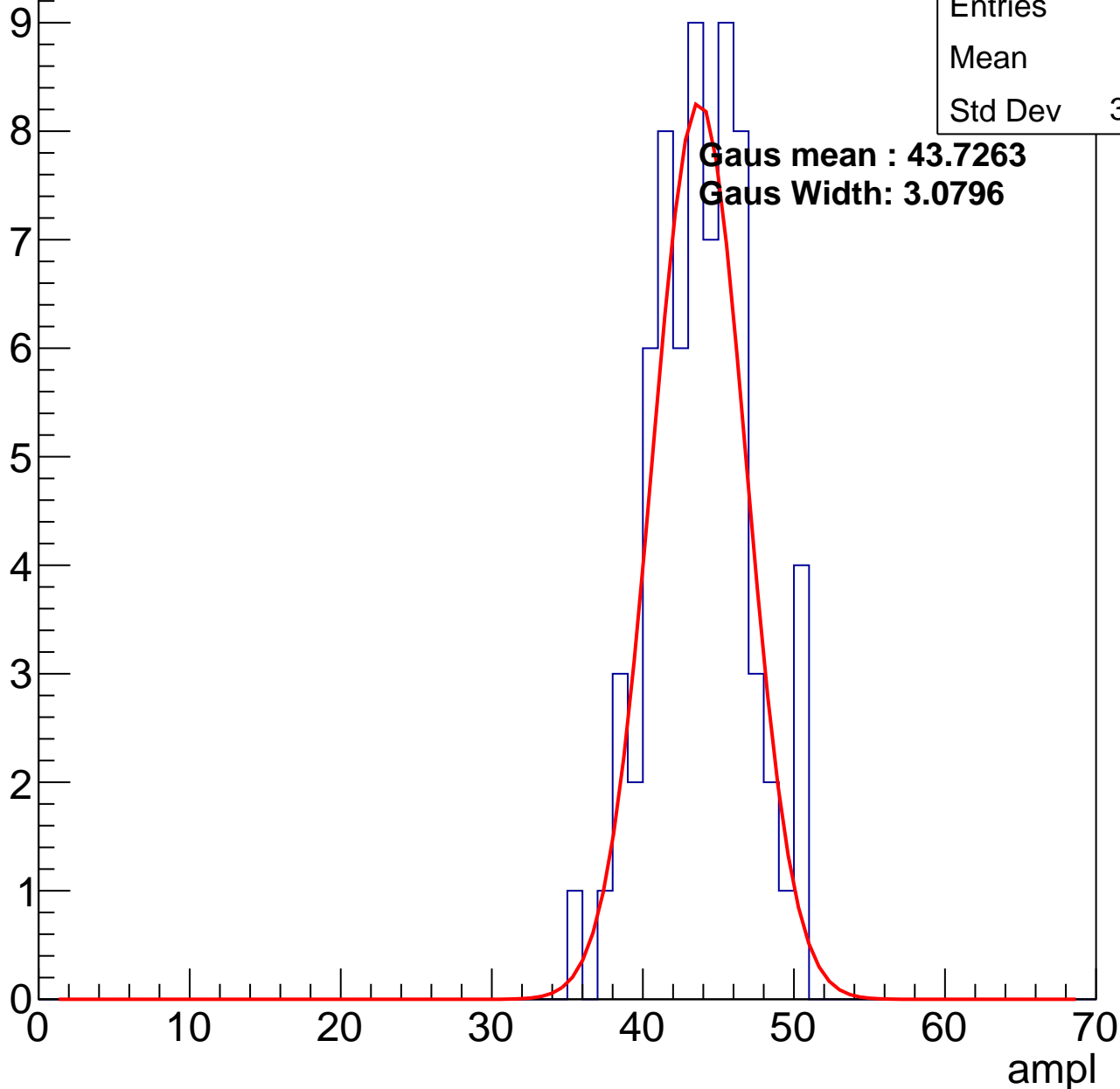
Gaus Width: 3.4606



B1L103S, U23-ch3, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

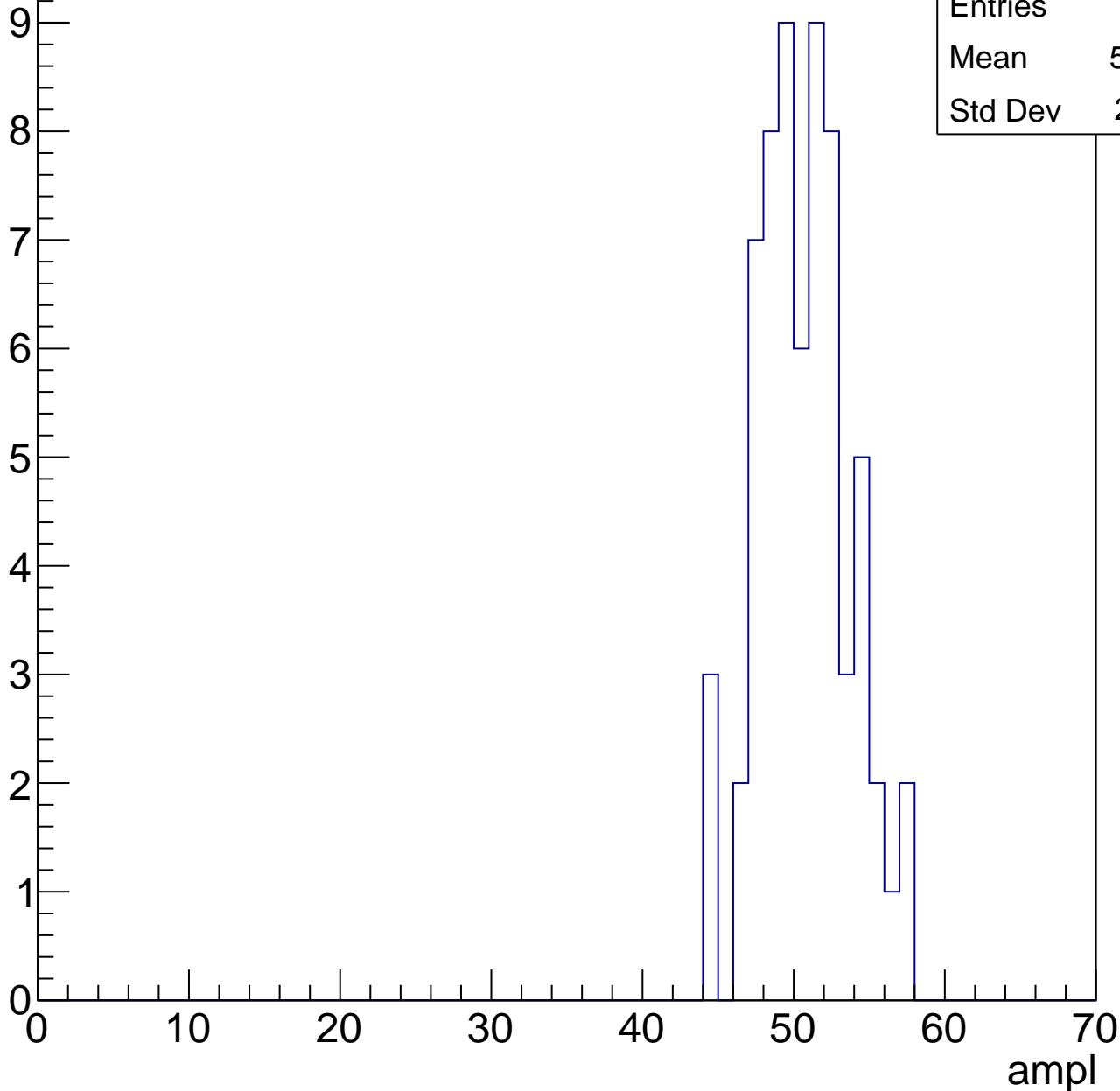


B1L103S, U23-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

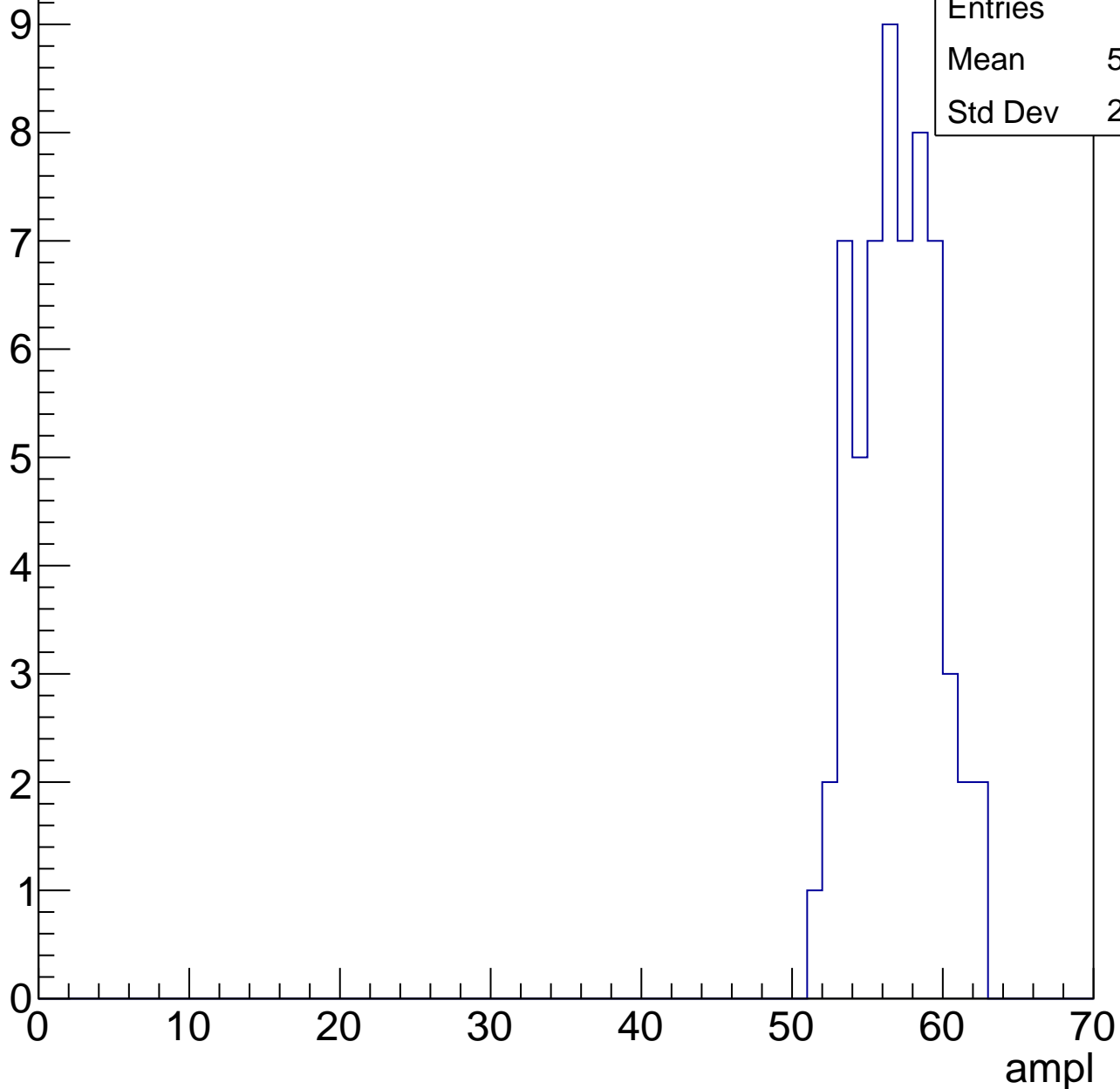
Entries	65
Mean	50.18
Std Dev	2.971



B1L103S, U23-ch3, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



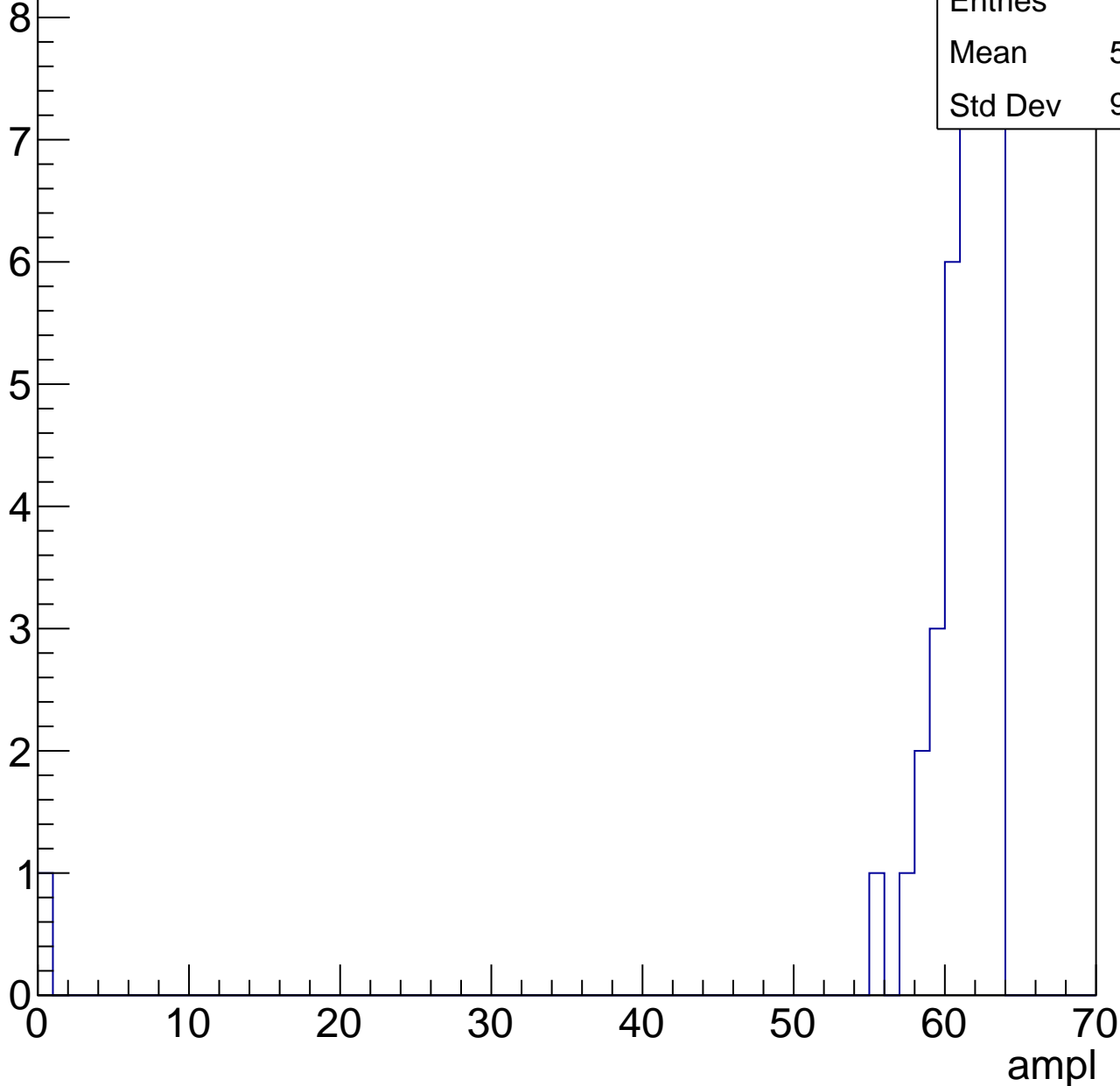
Entries	60
Mean	56.45
Std Dev	2.604

B1L103S, U23-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	59.29
Std Dev	9.918



B1L103S, U23-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L103S, U23-ch4, adc0

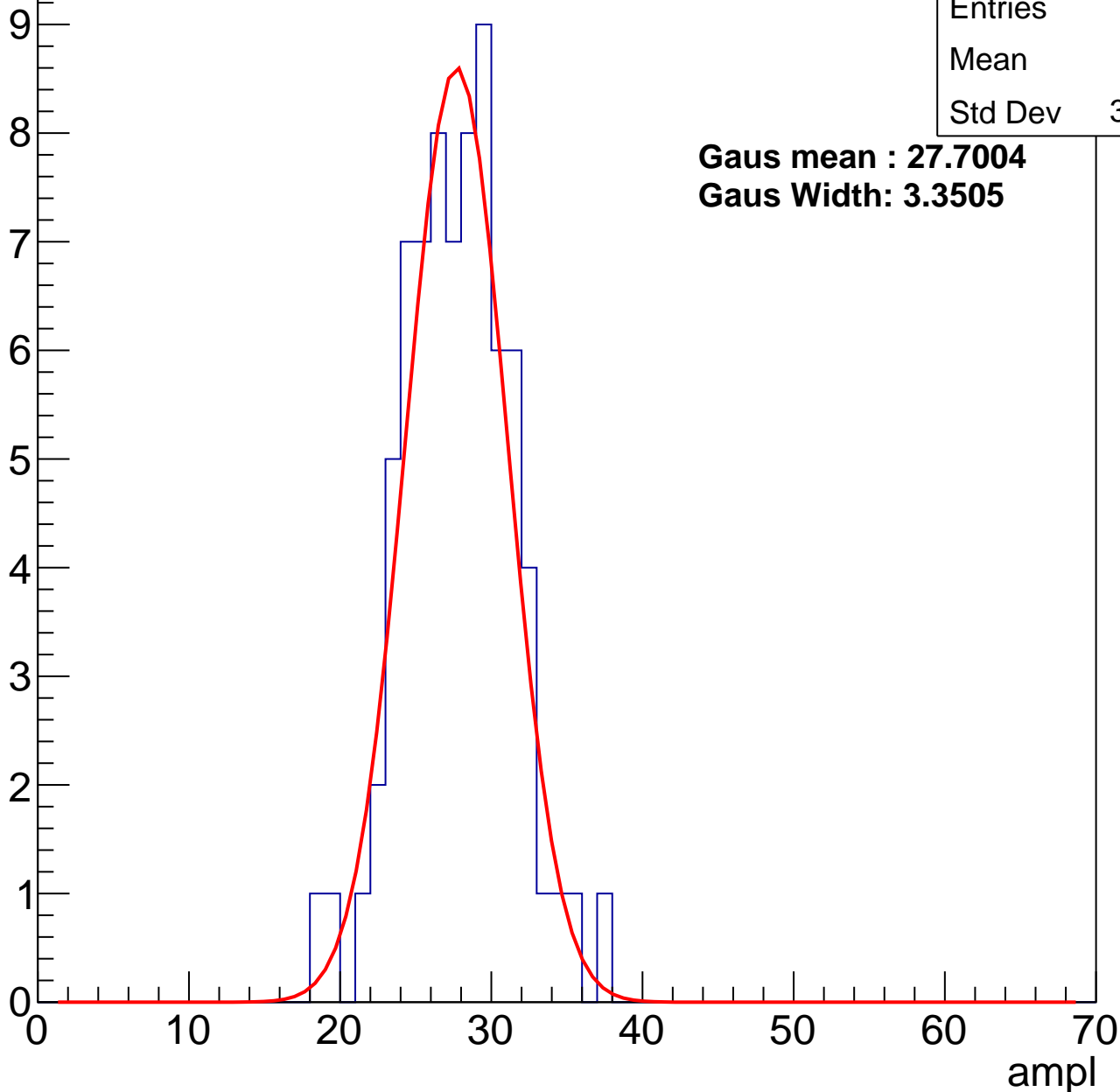
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	27.3
Std Dev	3.528

Gaus mean : 27.7004

Gaus Width: 3.3505



B1L103S, U23-ch4, adc1

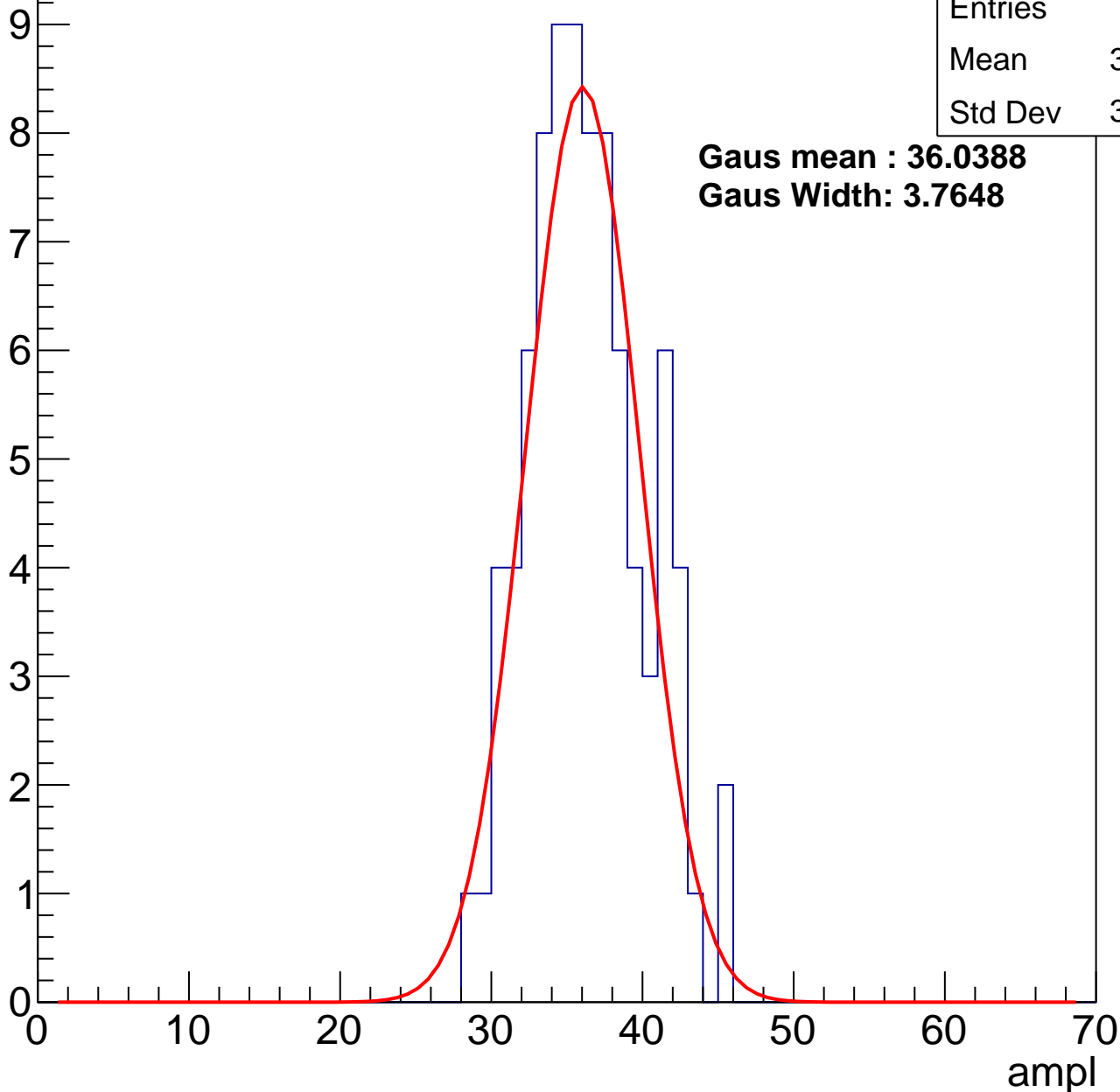
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	35.87
Std Dev	3.763

Gaus mean : 36.0388

Gaus Width: 3.7648



B1L103S, U23-ch4, adc2

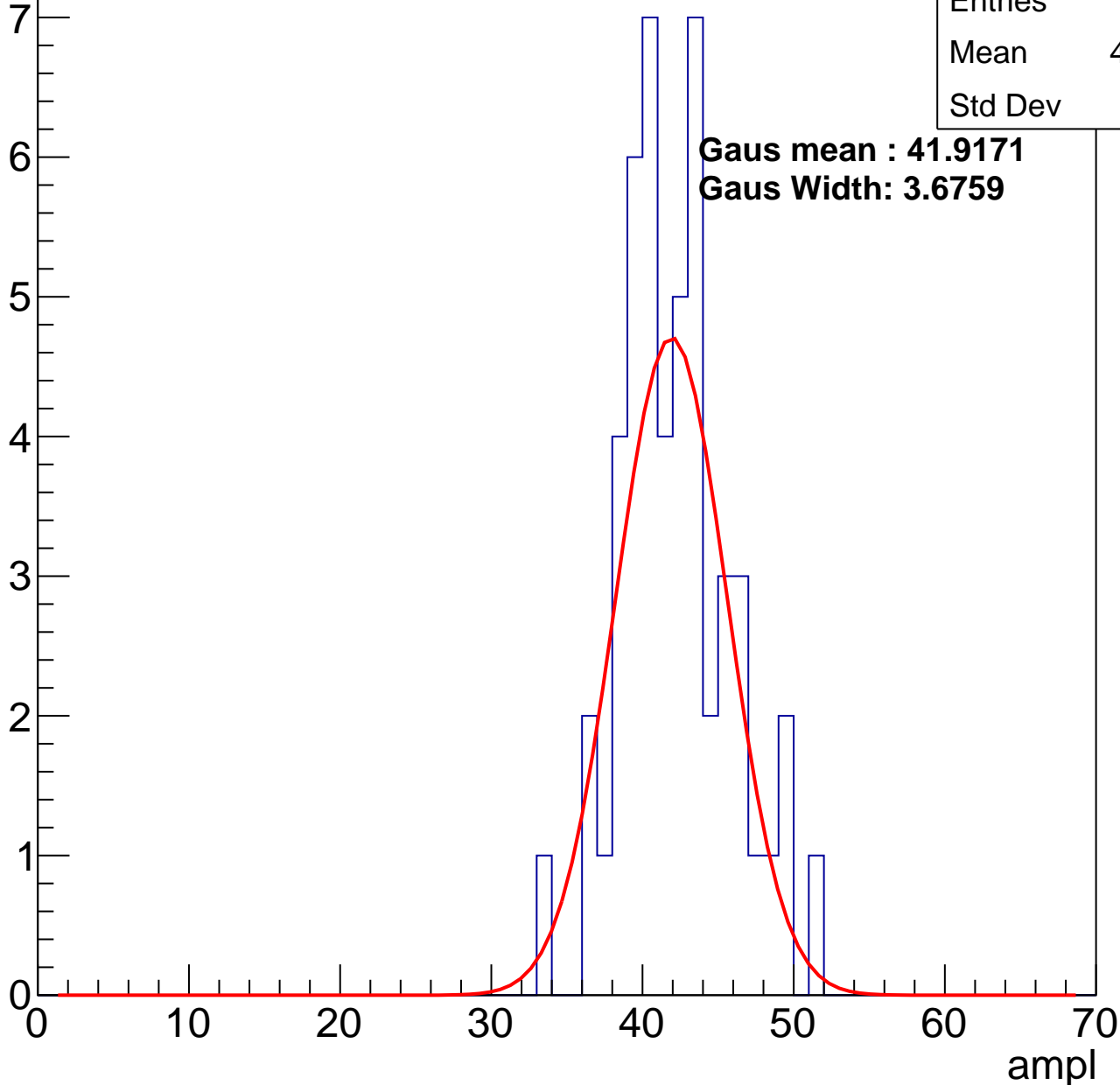
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	41.76
Std Dev	3.62

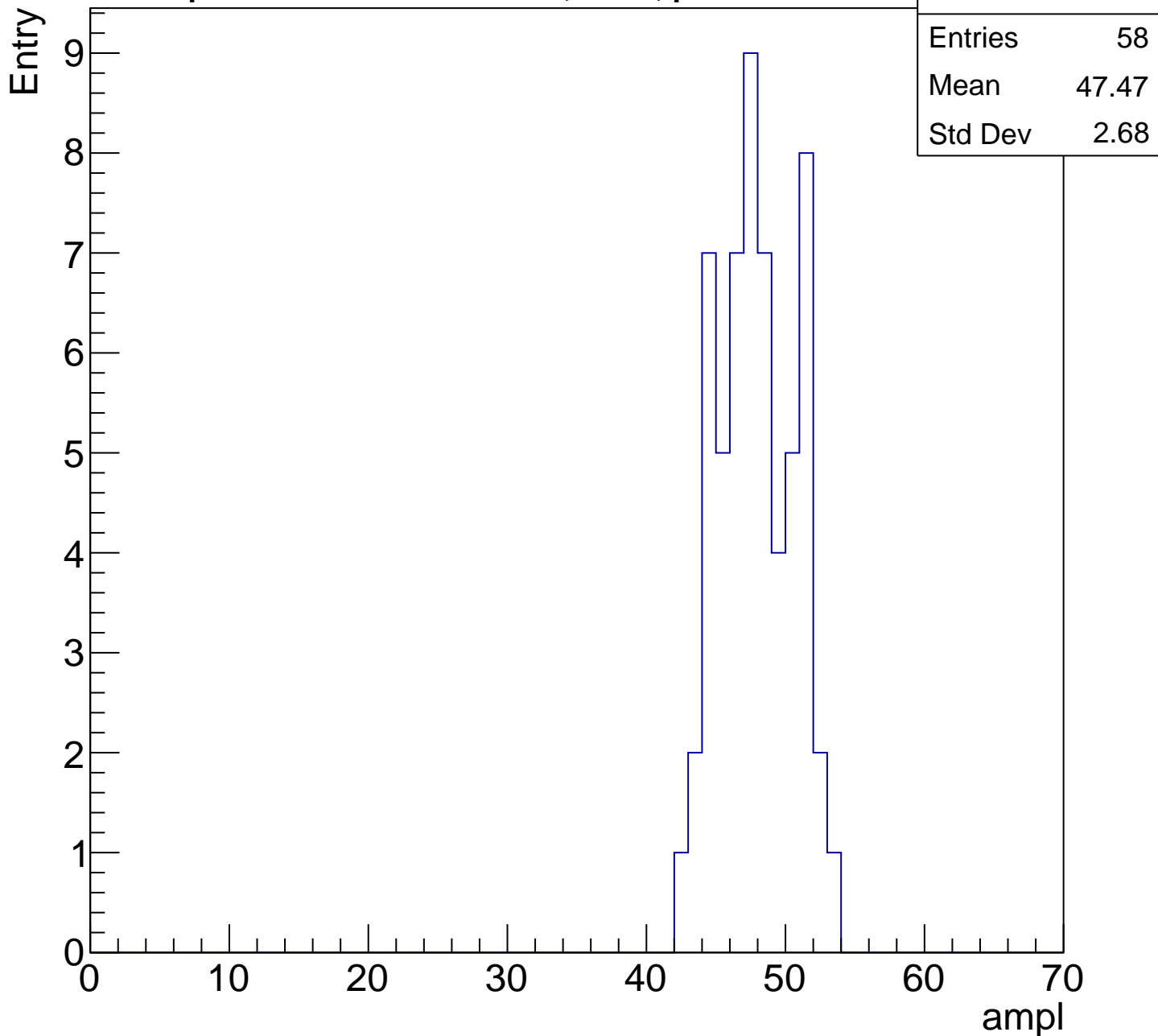
Gaus mean : 41.9171

Gaus Width: 3.6759



B1L103S, U23-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

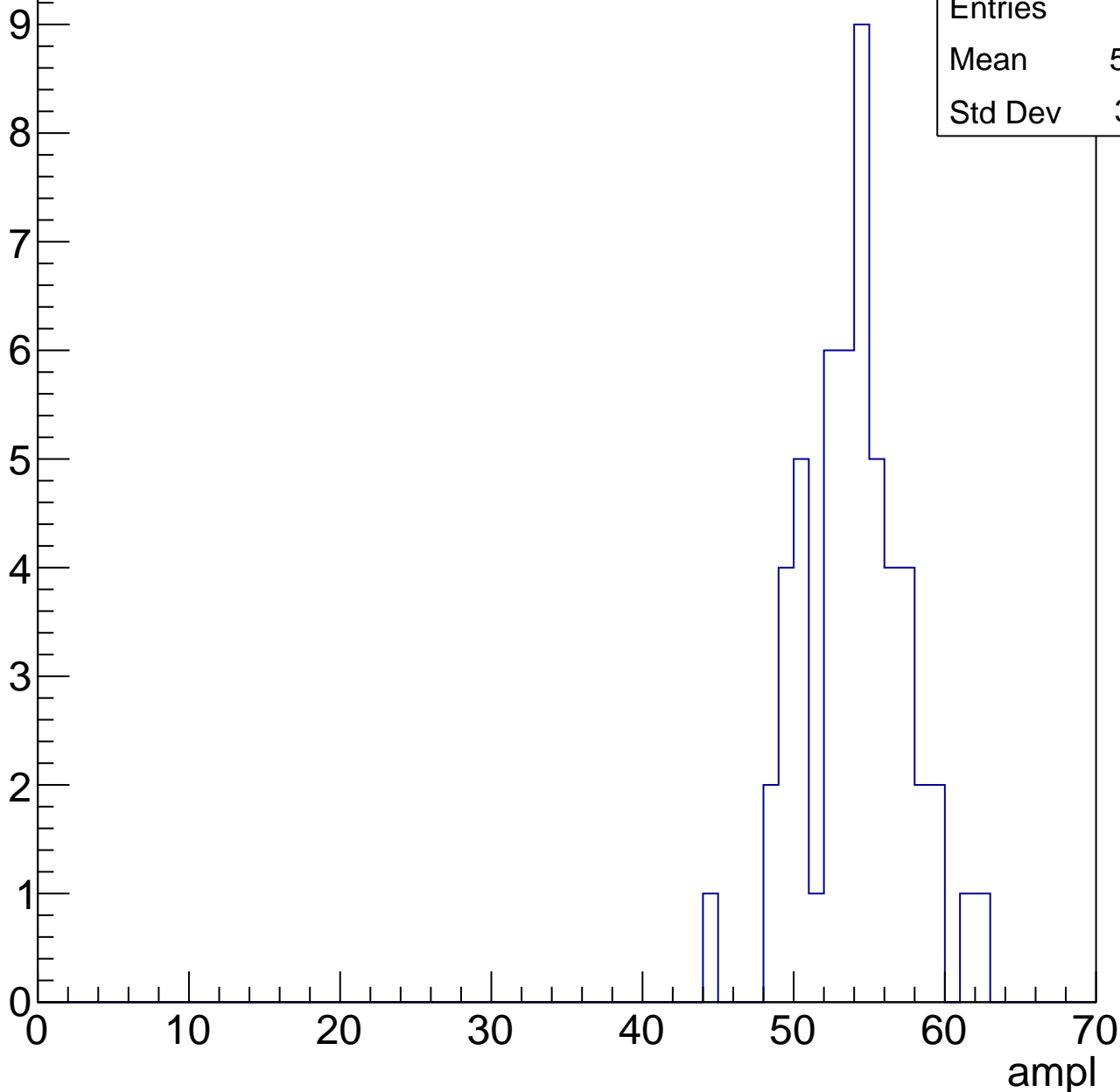


B1L103S, U23-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	53.53
Std Dev	3.451

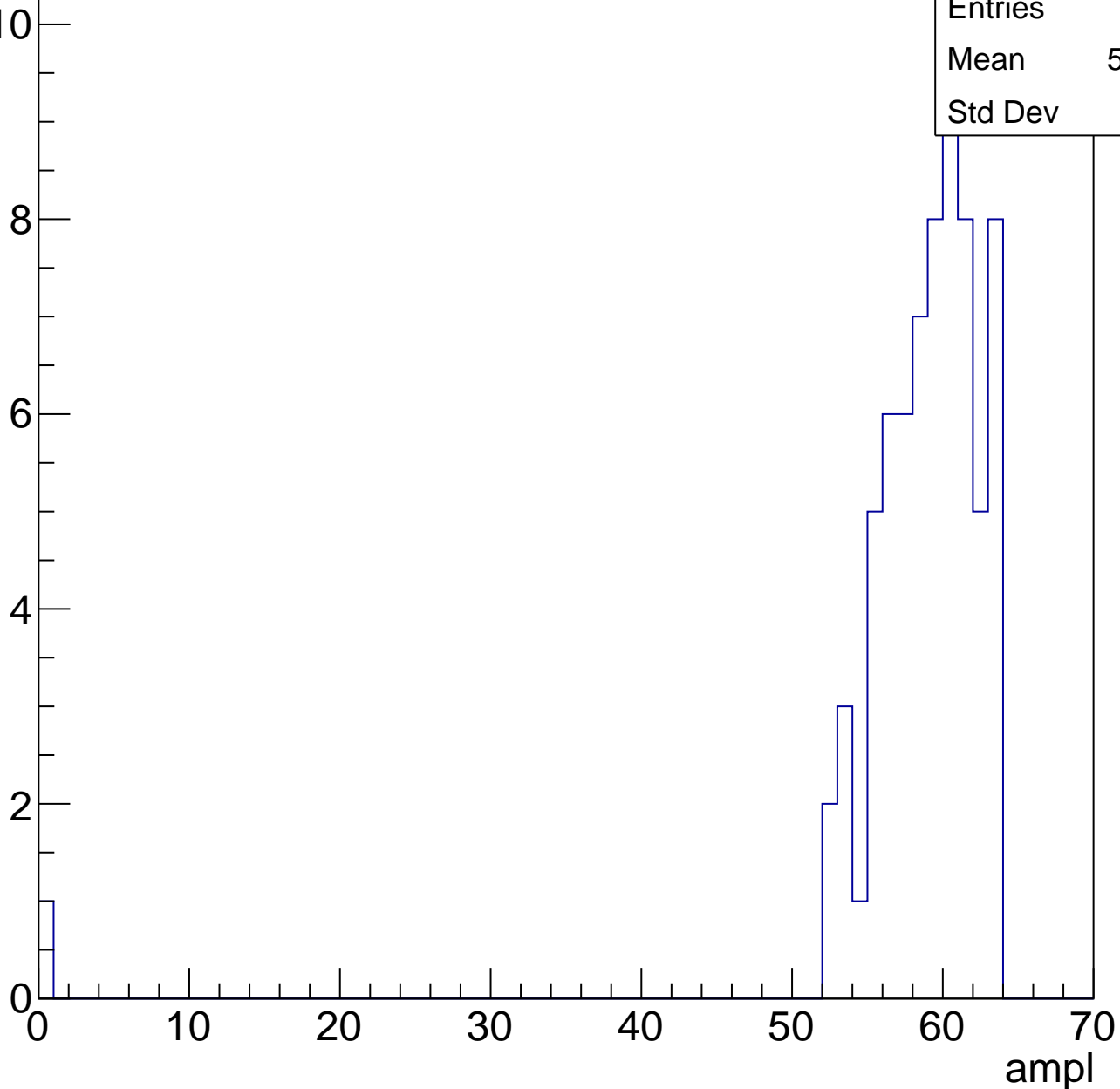


B1L103S, U23-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

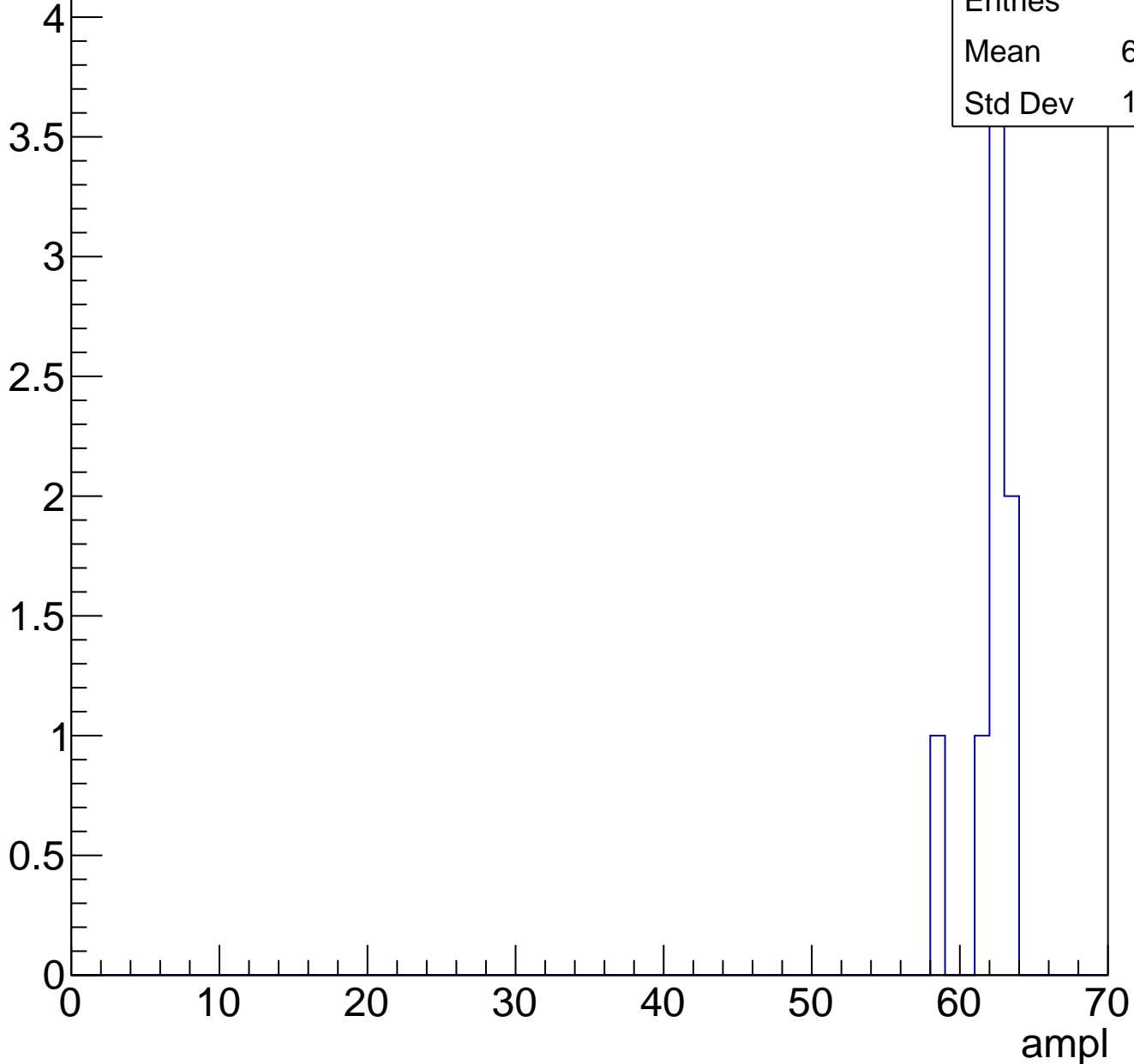
Entries	70
Mean	57.86
Std Dev	7.56



B1L103S, U23-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch5, adc0

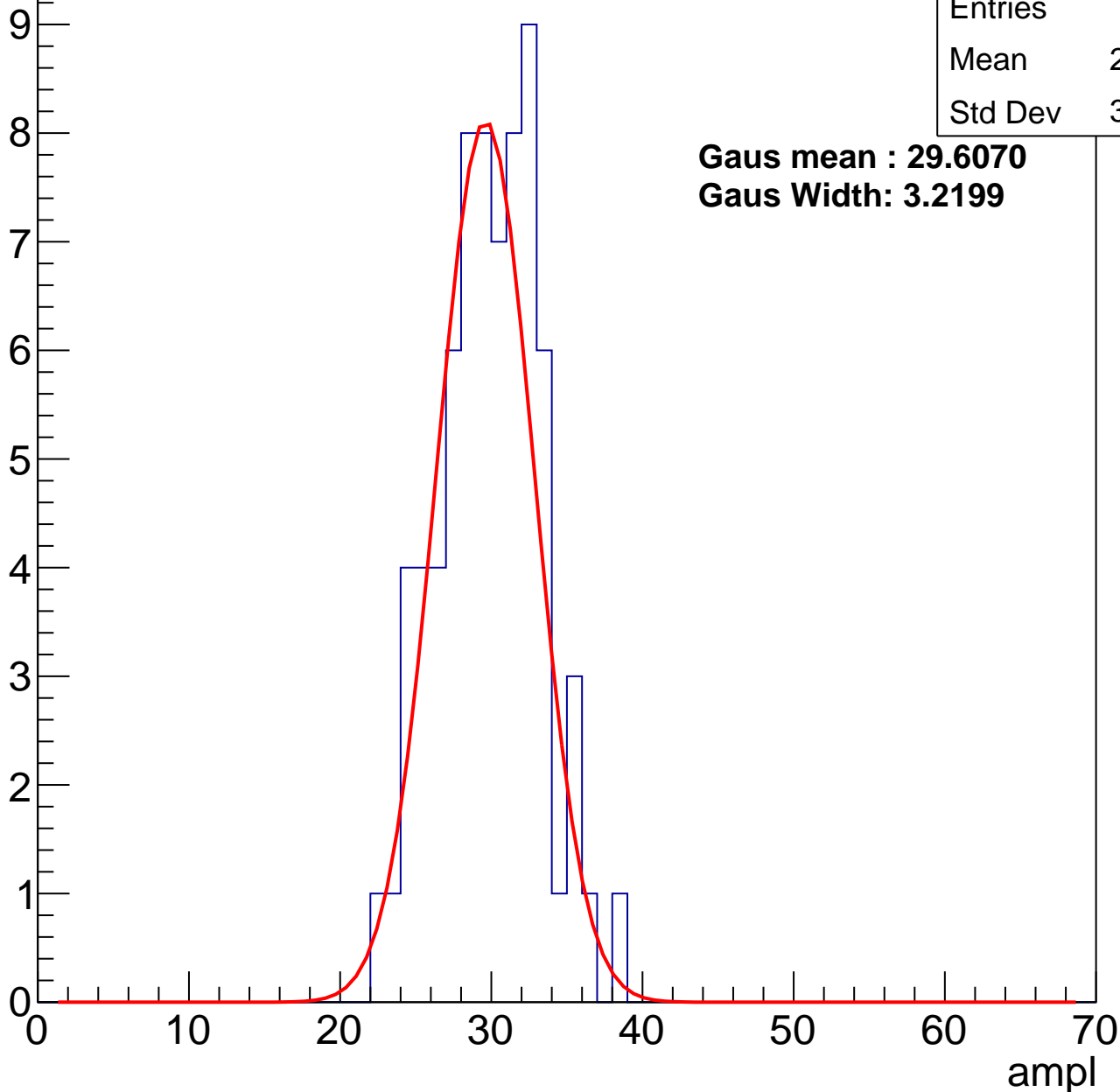
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.44
Std Dev	3.295

Gaus mean : 29.6070

Gaus Width: 3.2199



B1L103S, U23-ch5, adc1

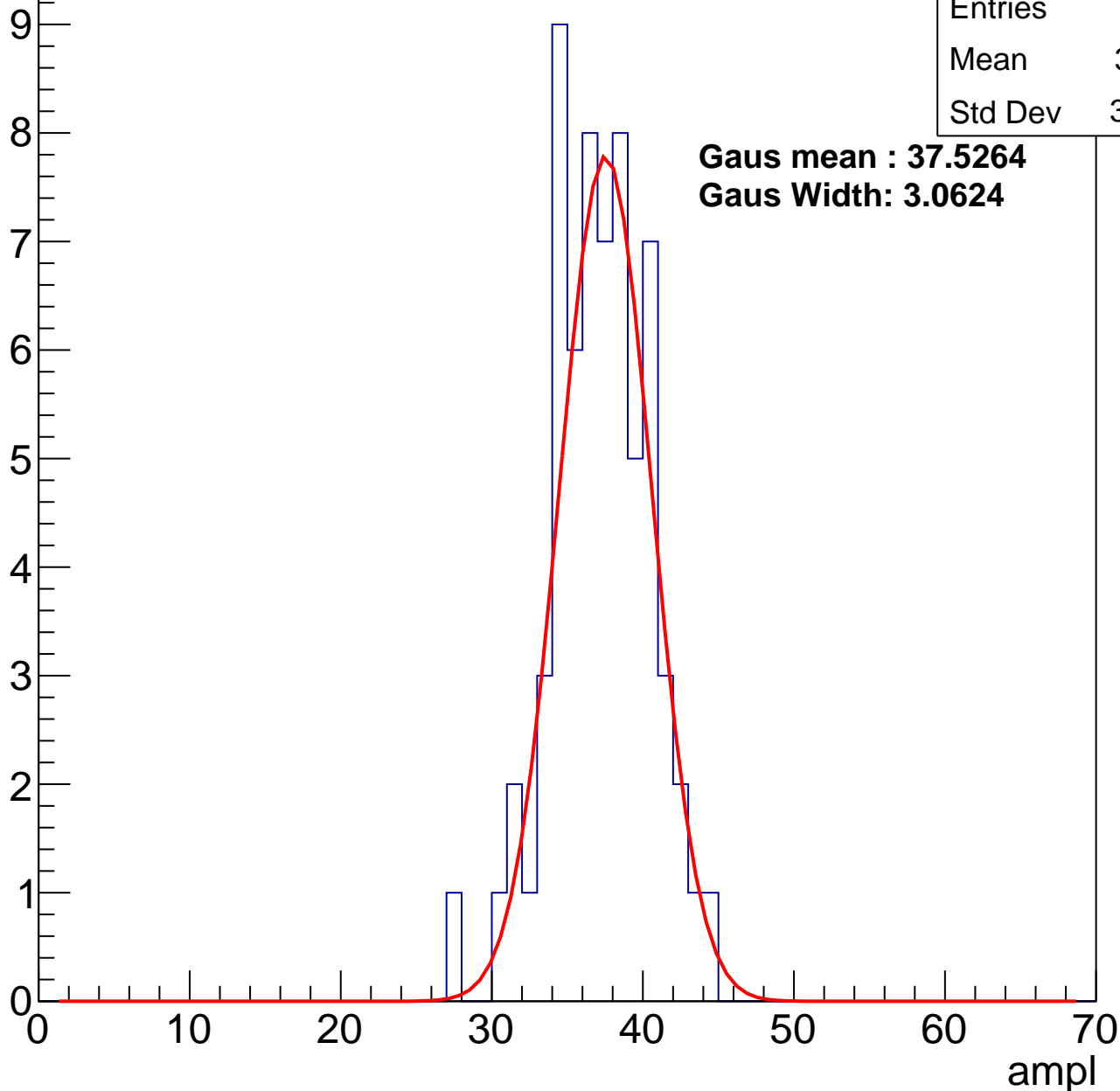
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.71
Std Dev	3.224

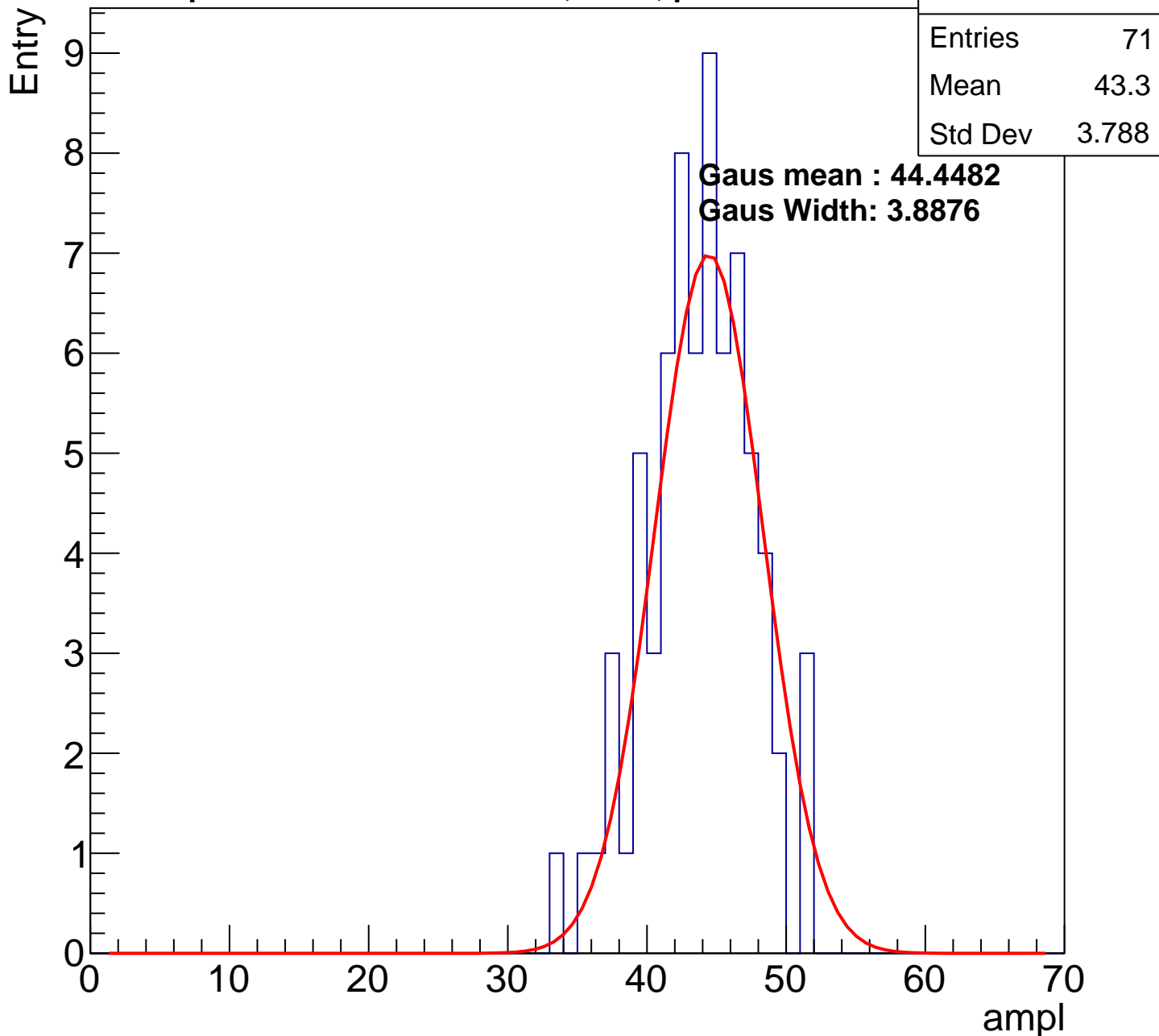
Gaus mean : 37.5264

Gaus Width: 3.0624



B1L103S, U23-ch5, adc2

calib_packv5_042523_0143.root, FC#7, port C2

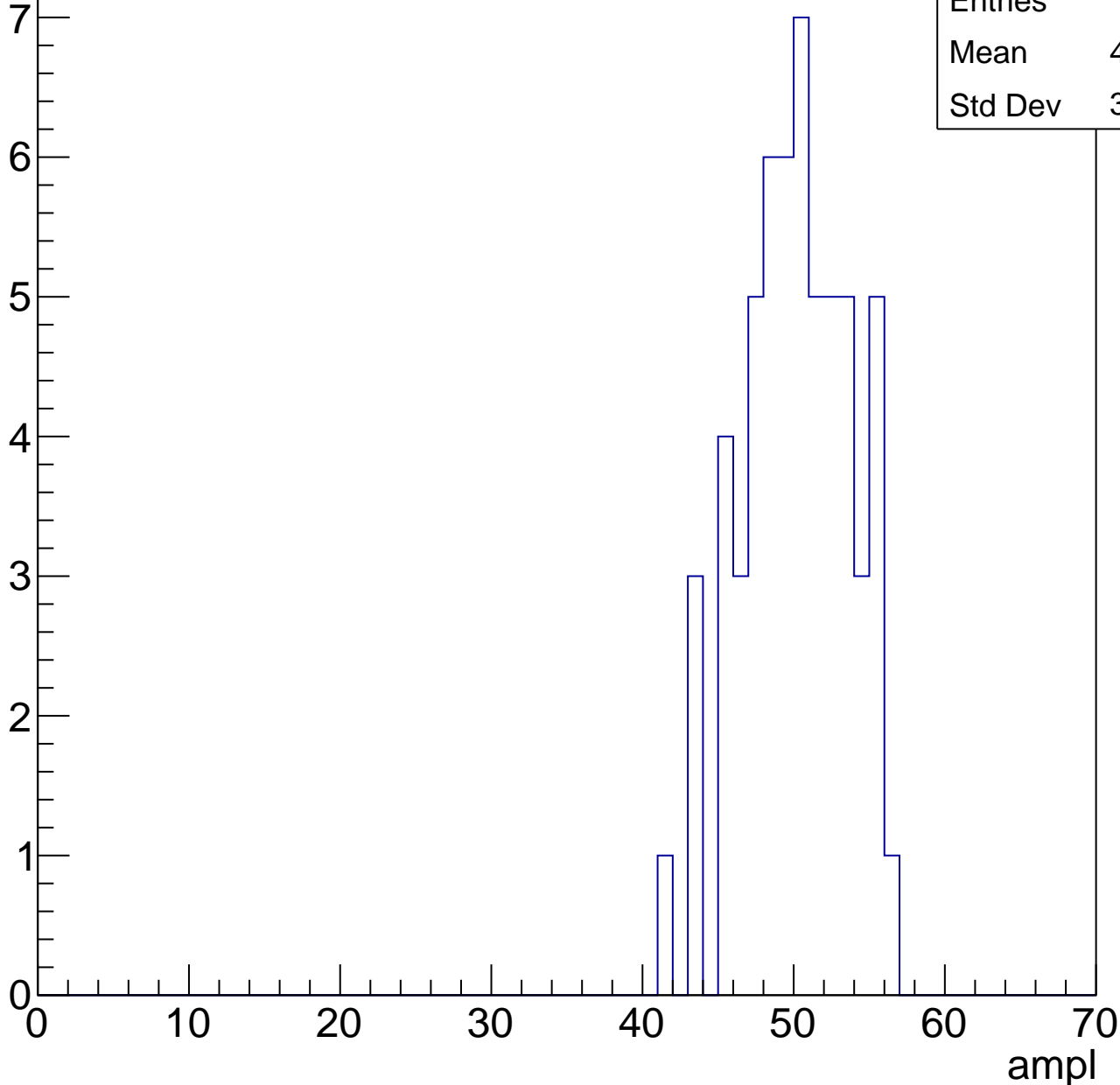


B1L103S, U23-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	49.63
Std Dev	3.498



B1L103S, U23-ch5, adc4

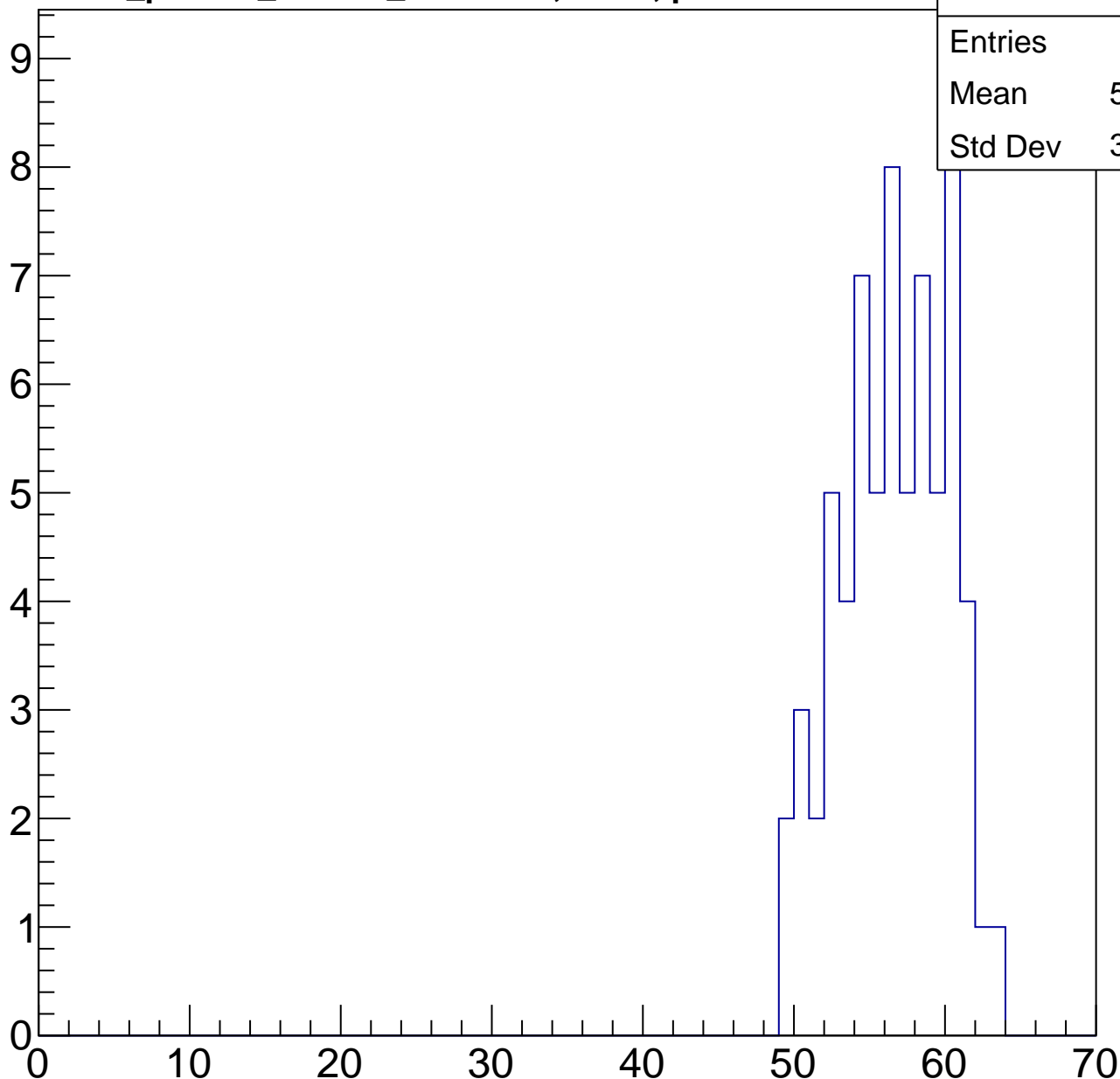
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	68
Mean	56.15
Std Dev	3.452

ampl

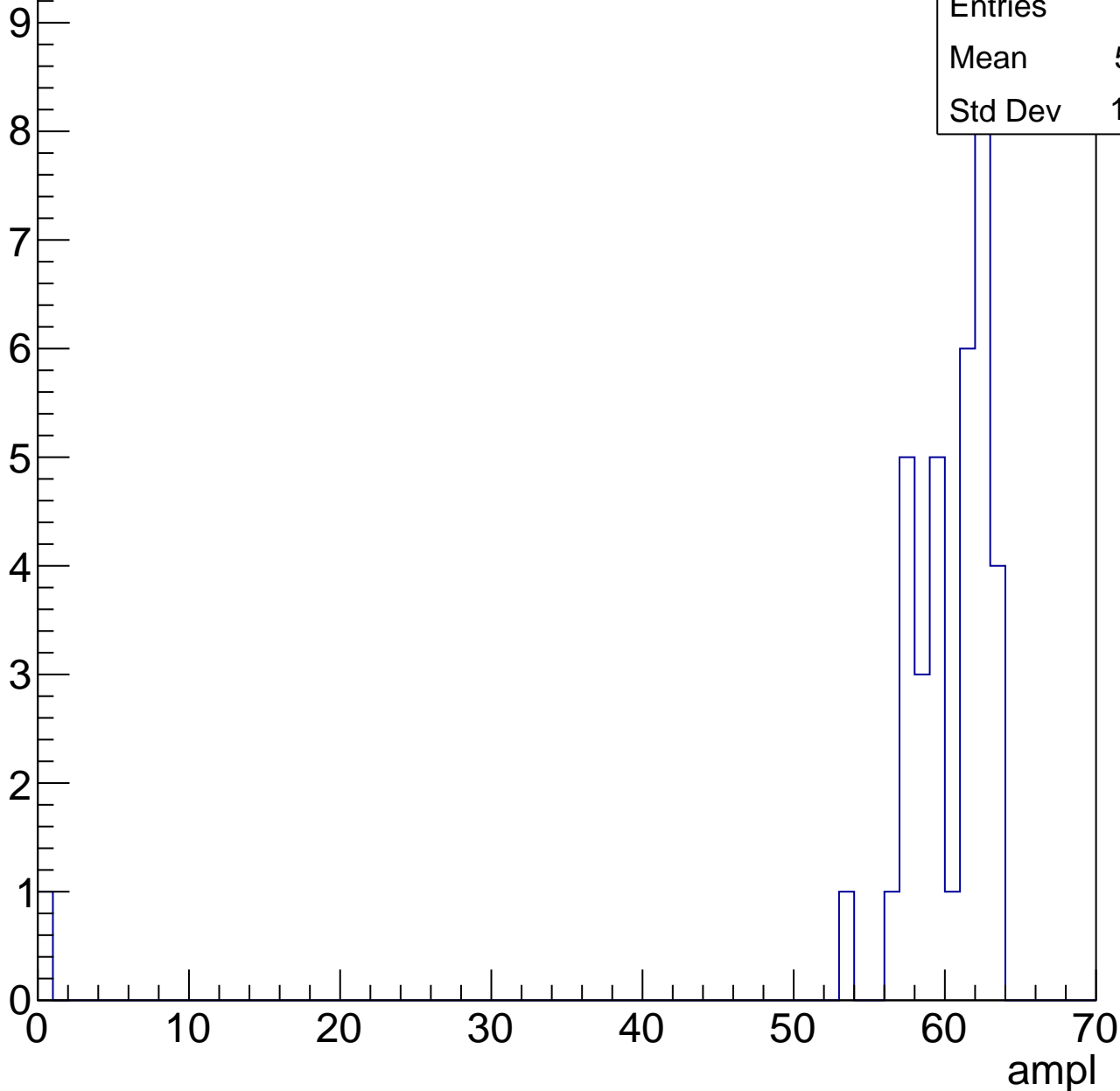


B1L103S, U23-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.31
Std Dev	10.14



B1L103S, U23-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

63

Std Dev

0

B1L103S, U23-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch6, adc0

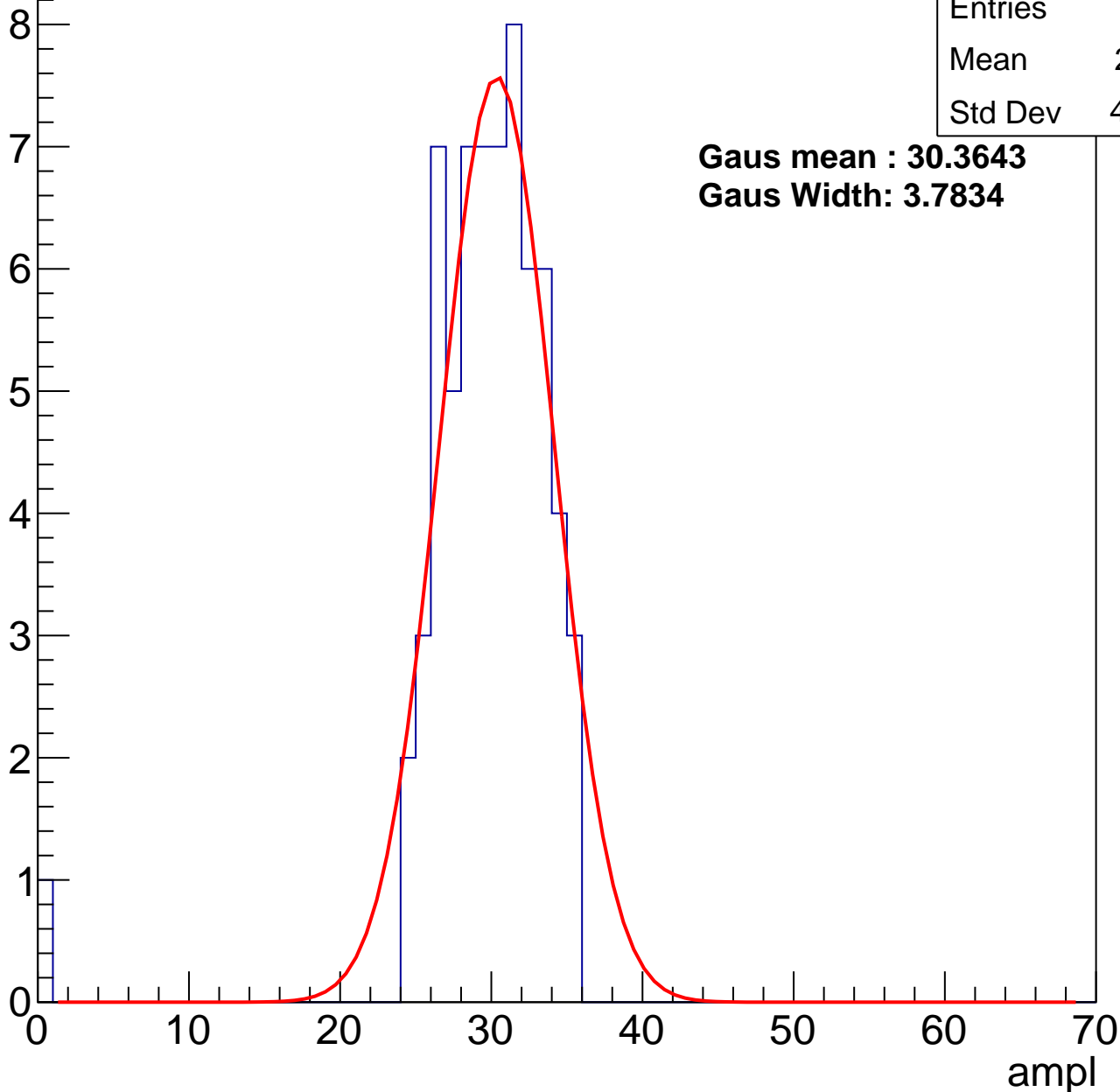
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	29.21
Std Dev	4.643

Gaus mean : 30.3643

Gaus Width: 3.7834



B1L103S, U23-ch6, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	37.29
Std Dev	3.959

Gaus mean : 38.0545

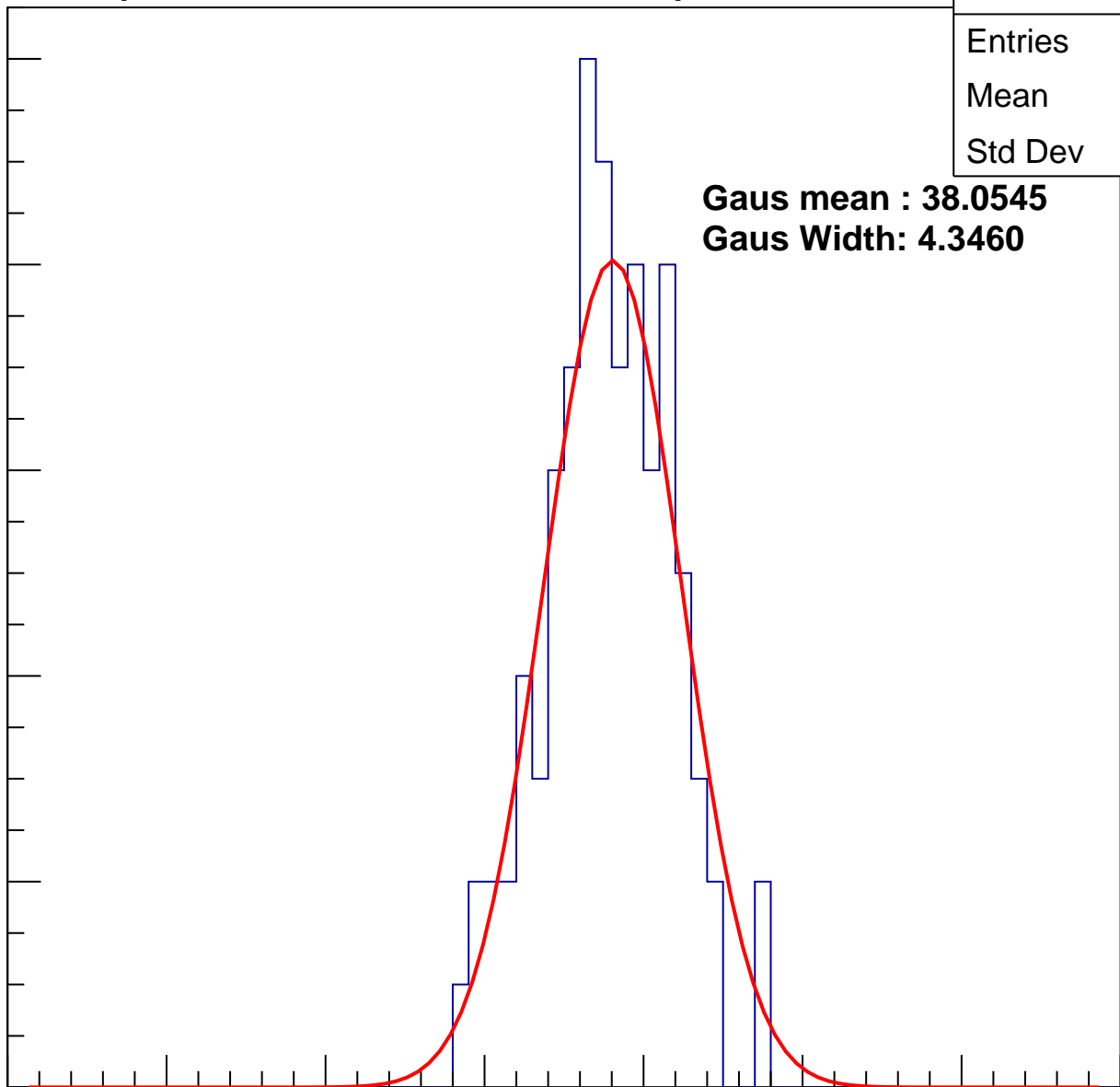
Gaus Width: 4.3460

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch6, adc2

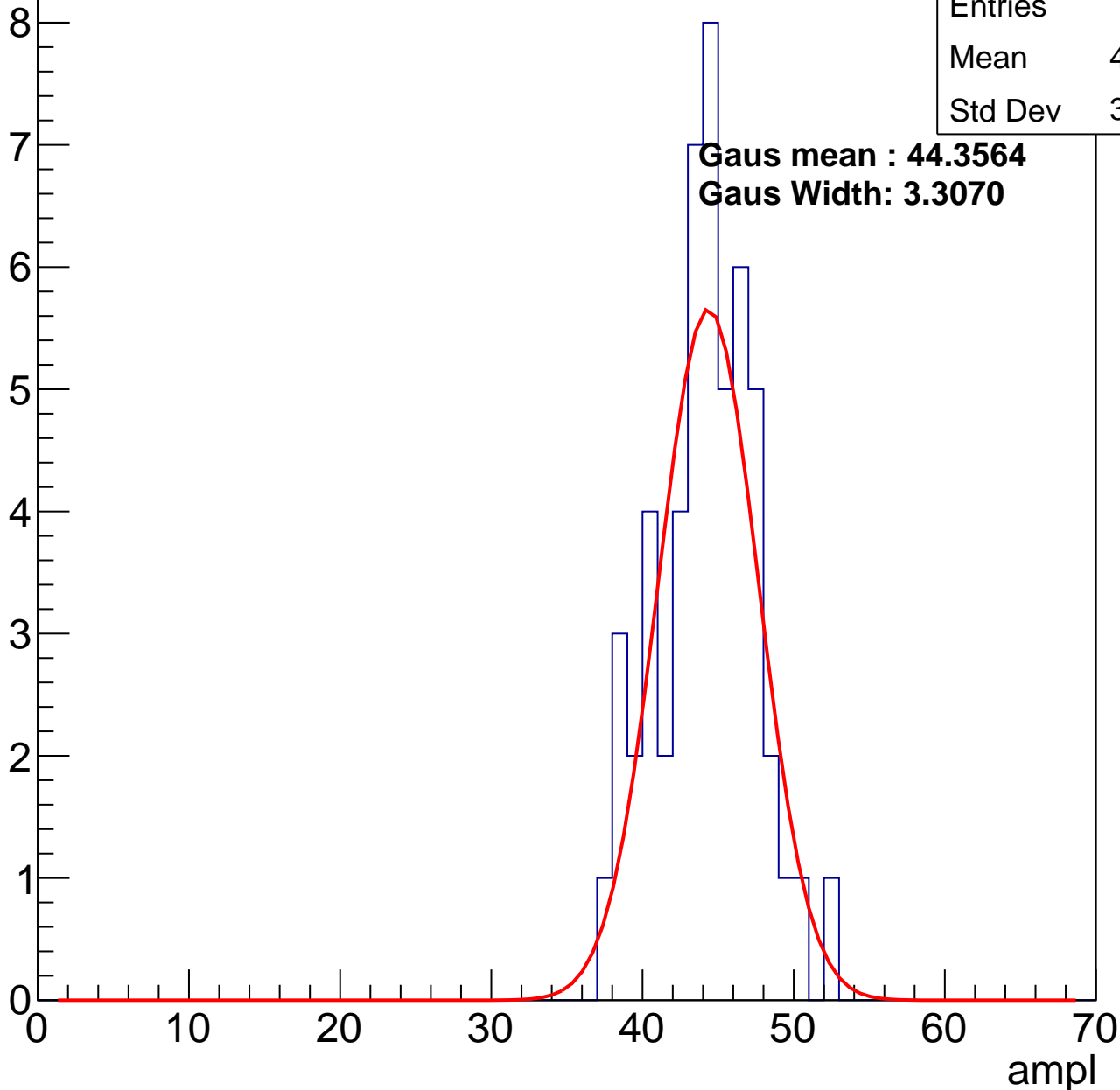
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	43.75
Std Dev	3.222

Gaus mean : 44.3564

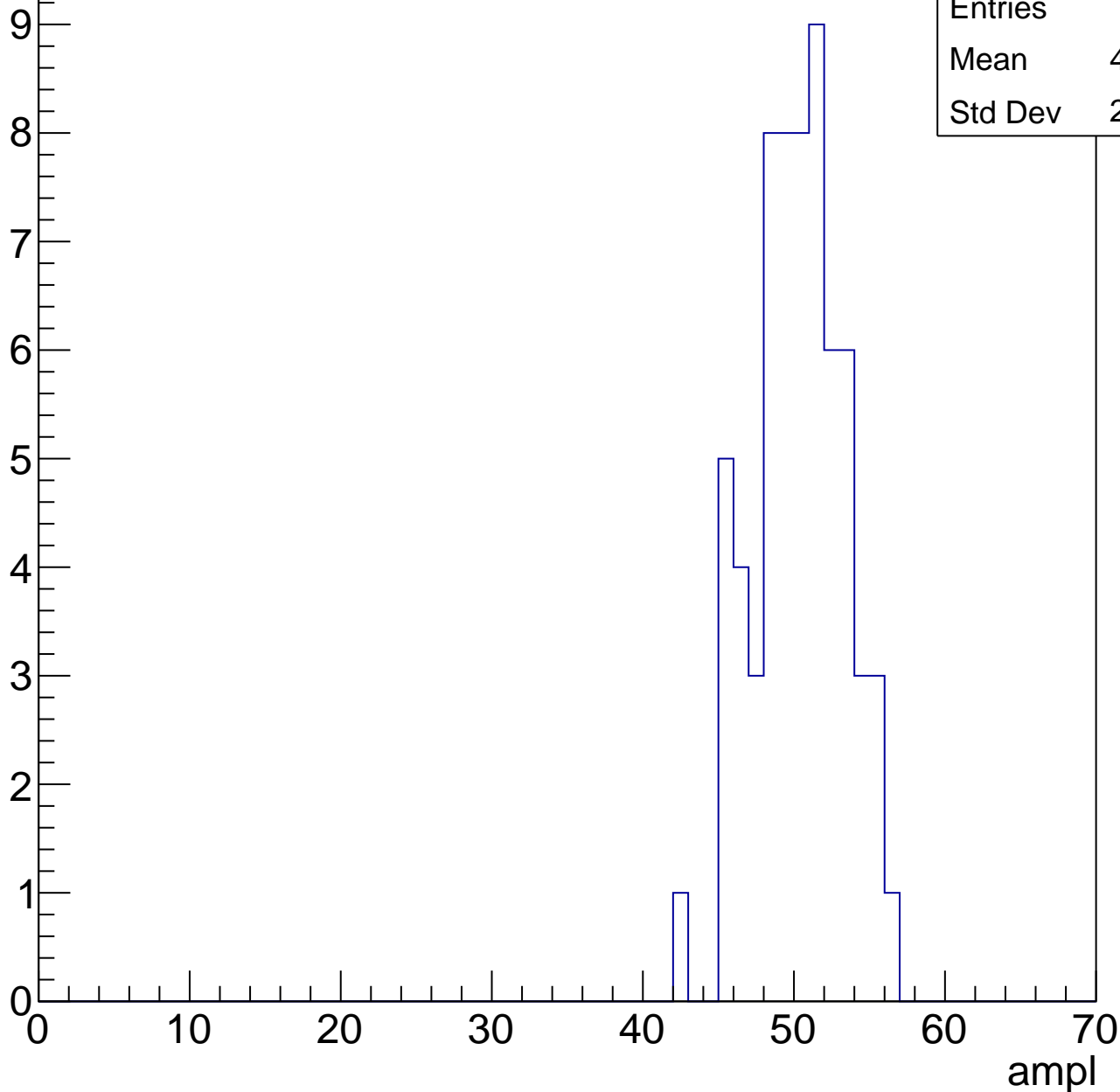
Gaus Width: 3.3070



B1L103S, U23-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

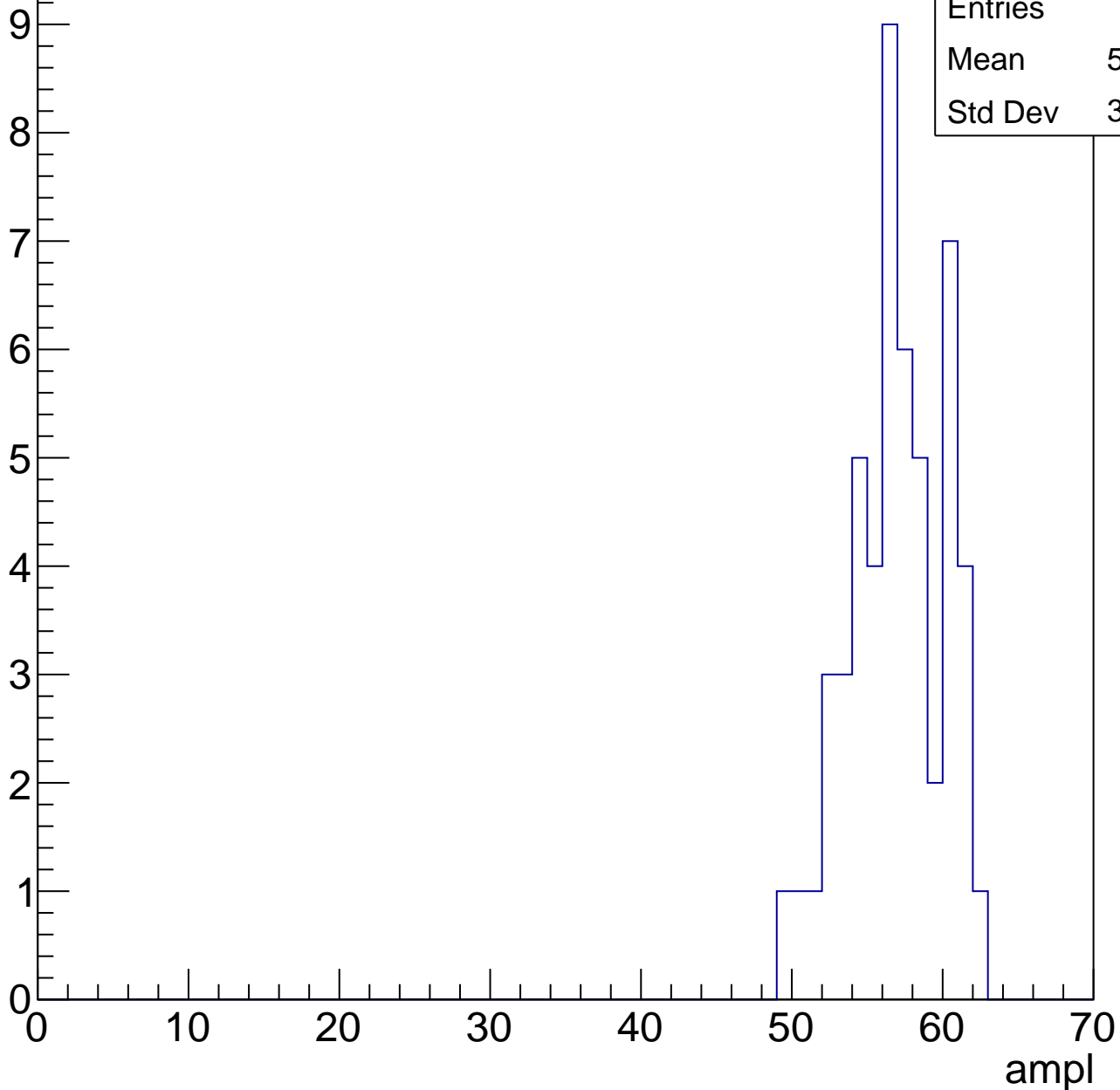
Entry



B1L103S, U23-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

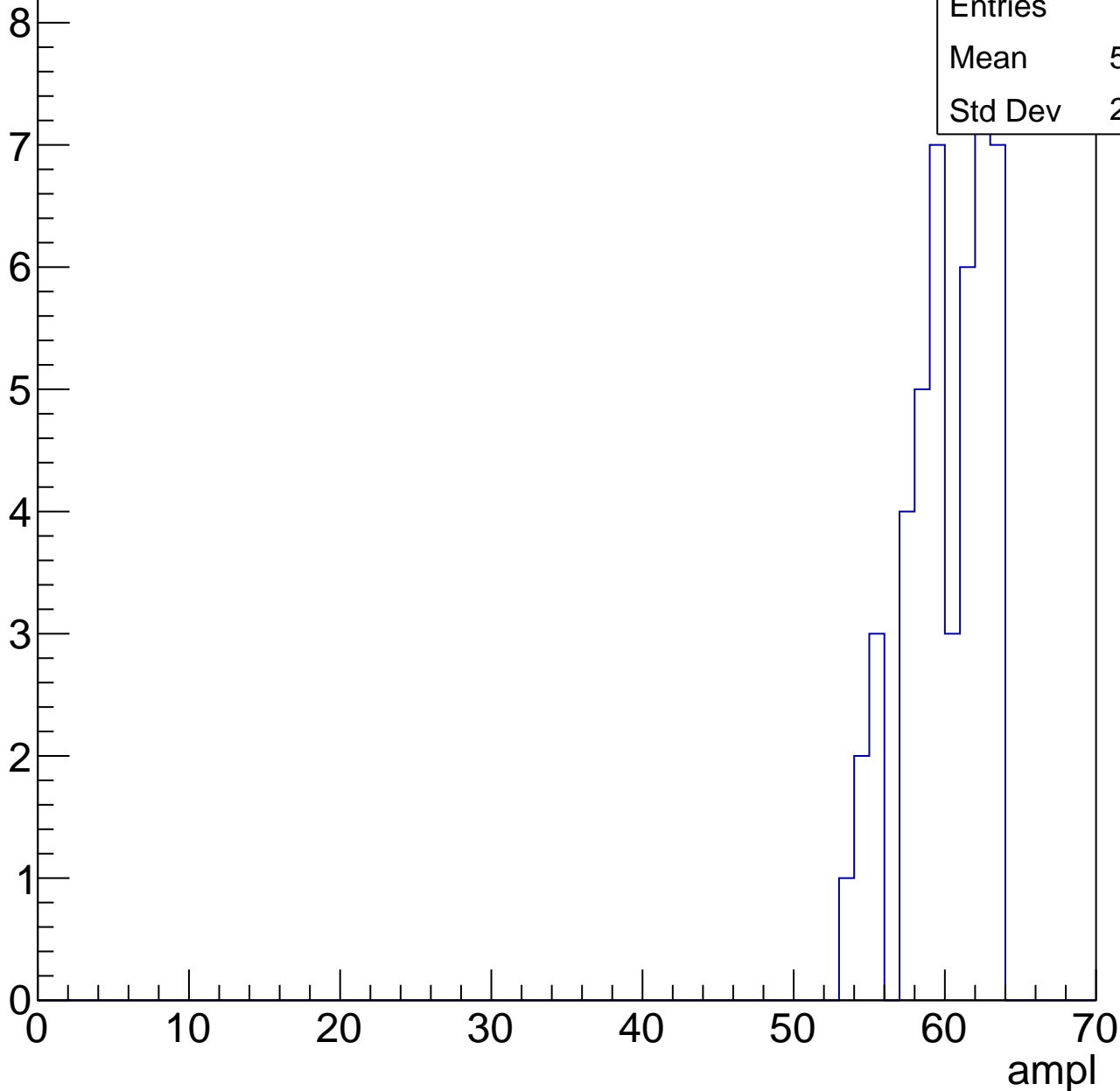


B1L103S, U23-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

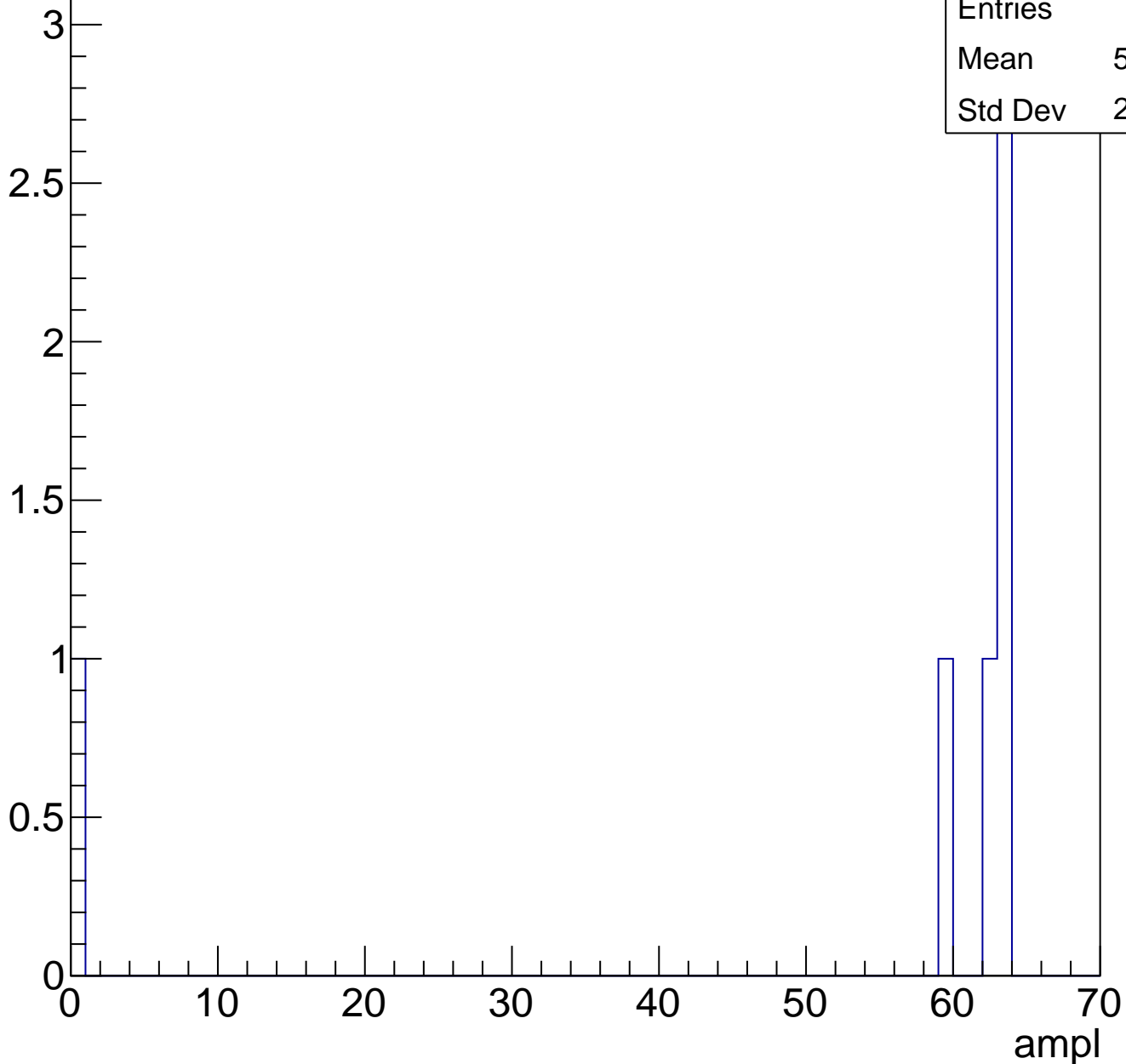
Entries	46
Mean	59.57
Std Dev	2.764



B1L103S, U23-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L103S, U23-ch7, adc0

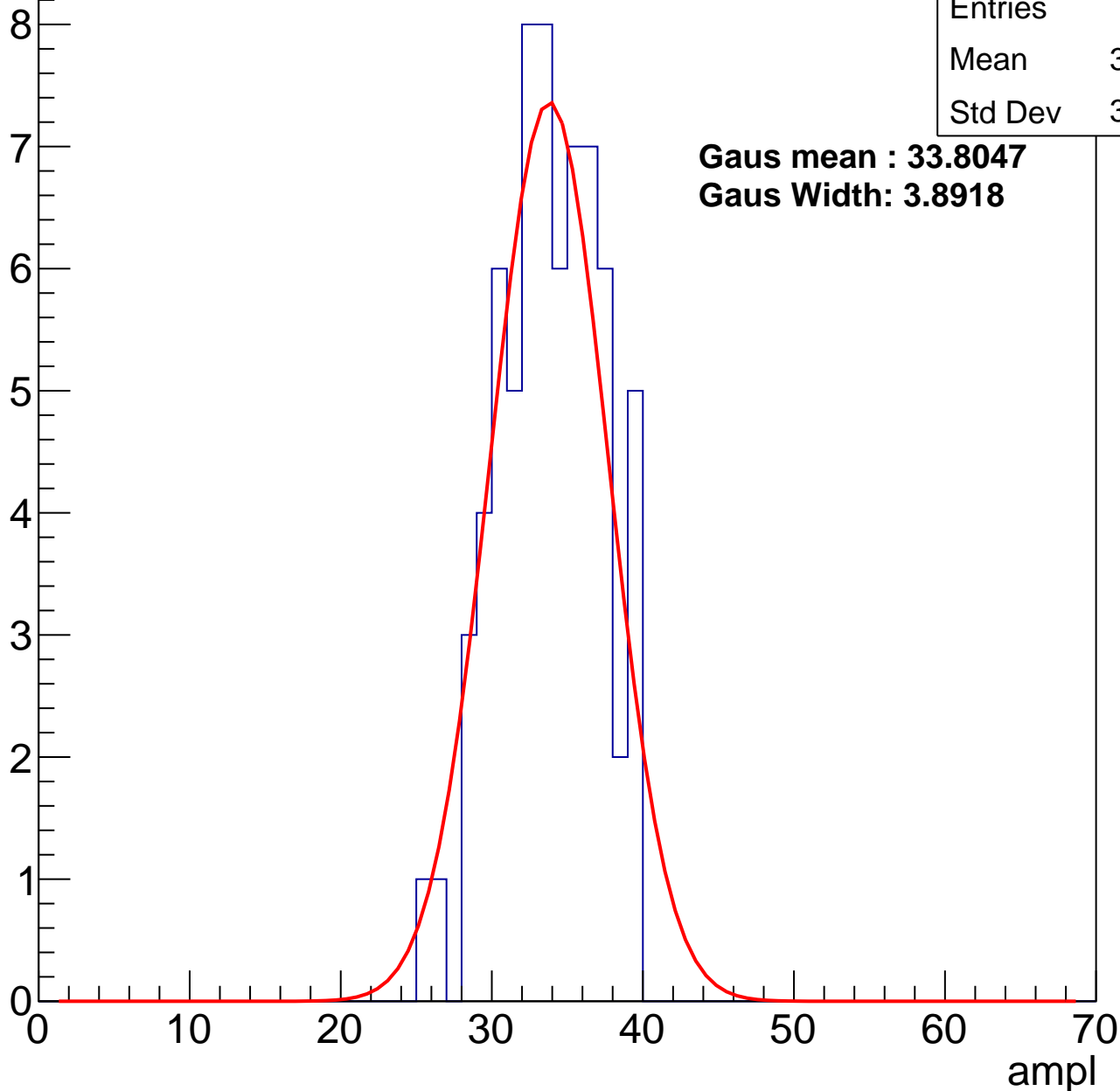
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	33.33
Std Dev	3.295

Gaus mean : 33.8047

Gaus Width: 3.8918



B1L103S, U23-ch7, adc1

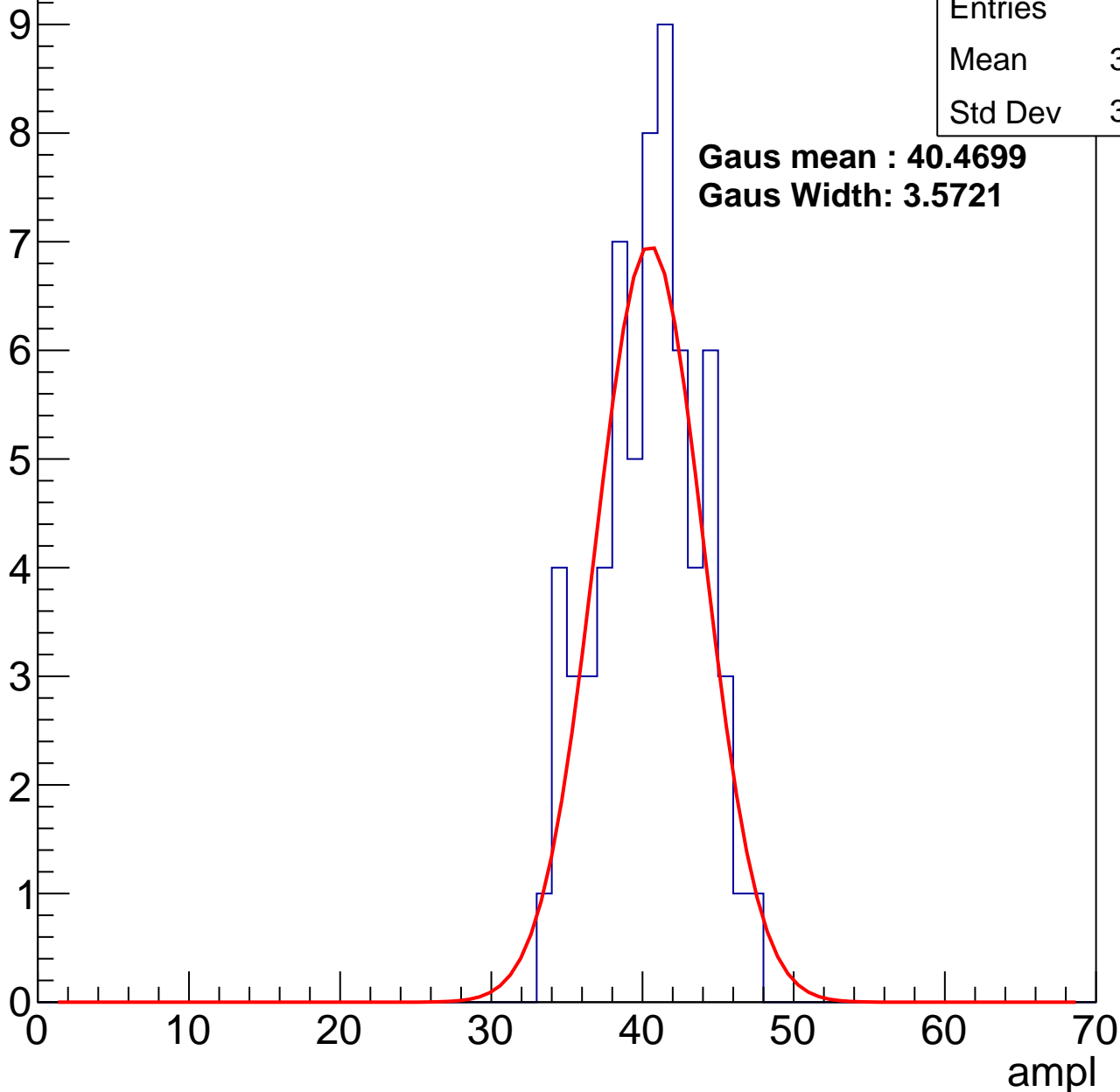
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	39.94
Std Dev	3.304

Gaus mean : 40.4699

Gaus Width: 3.5721



B1L103S, U23-ch7, adc2

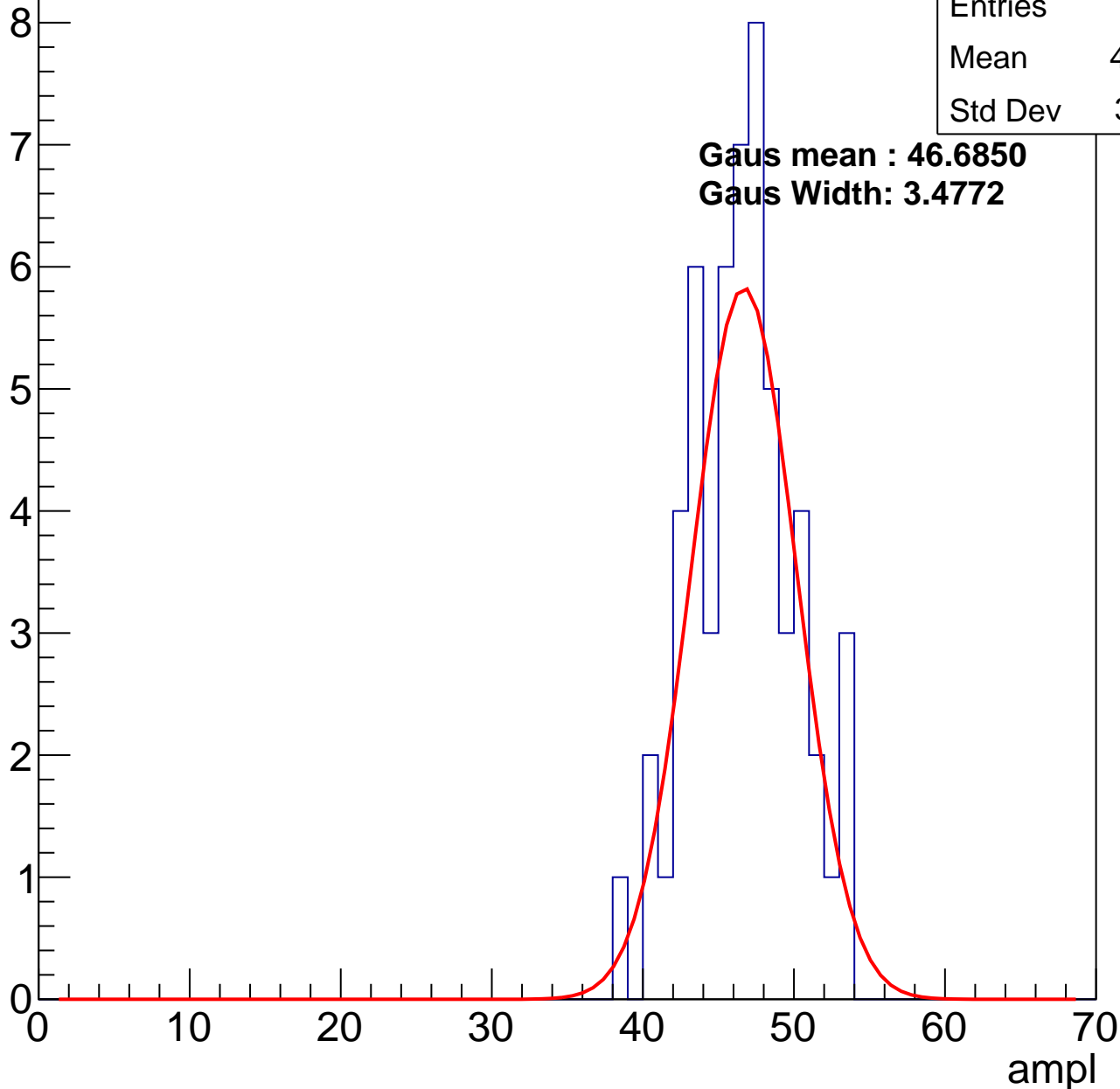
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	46.16
Std Dev	3.401

Gaus mean : 46.6850

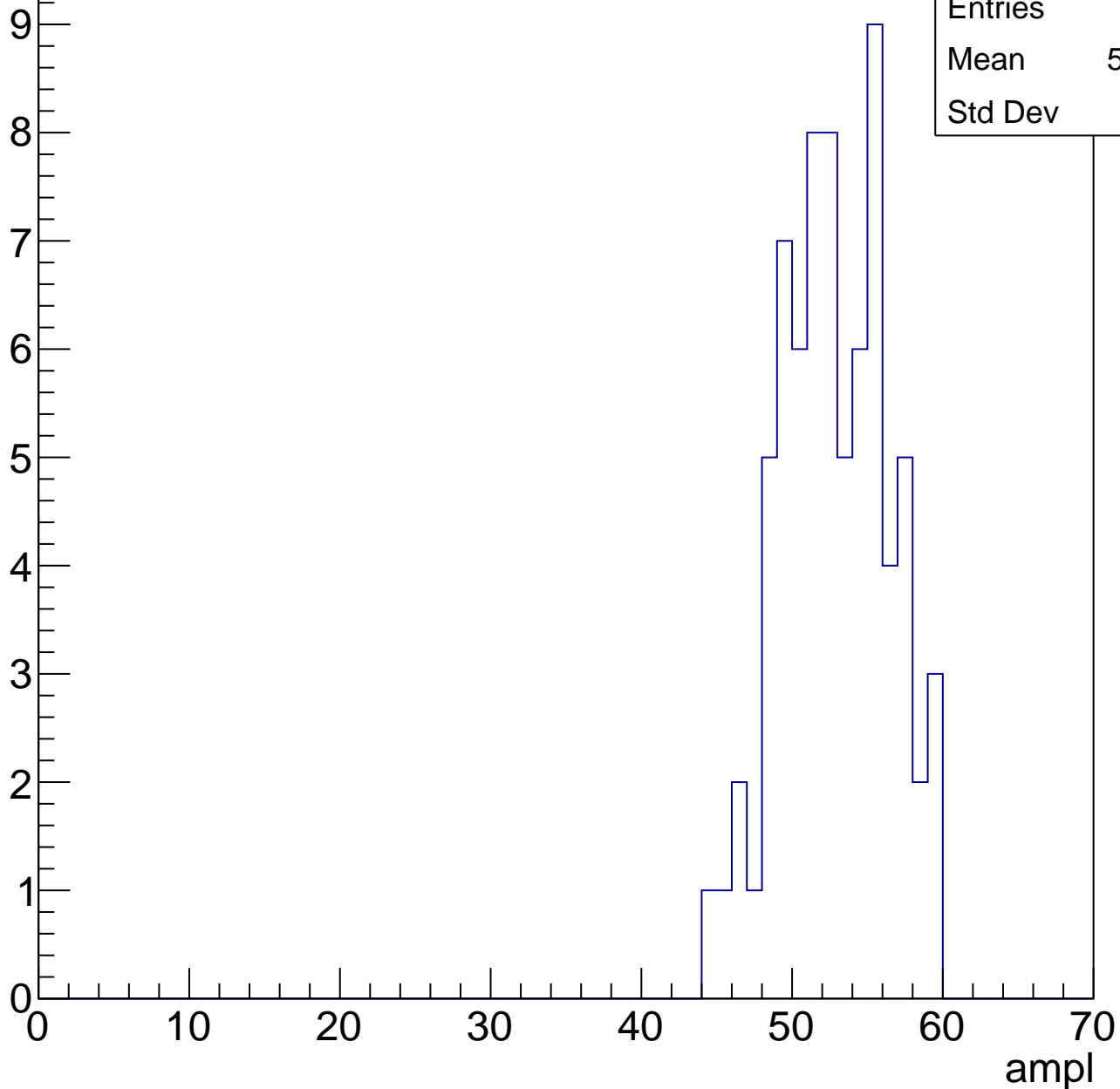
Gaus Width: 3.4772



B1L103S, U23-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

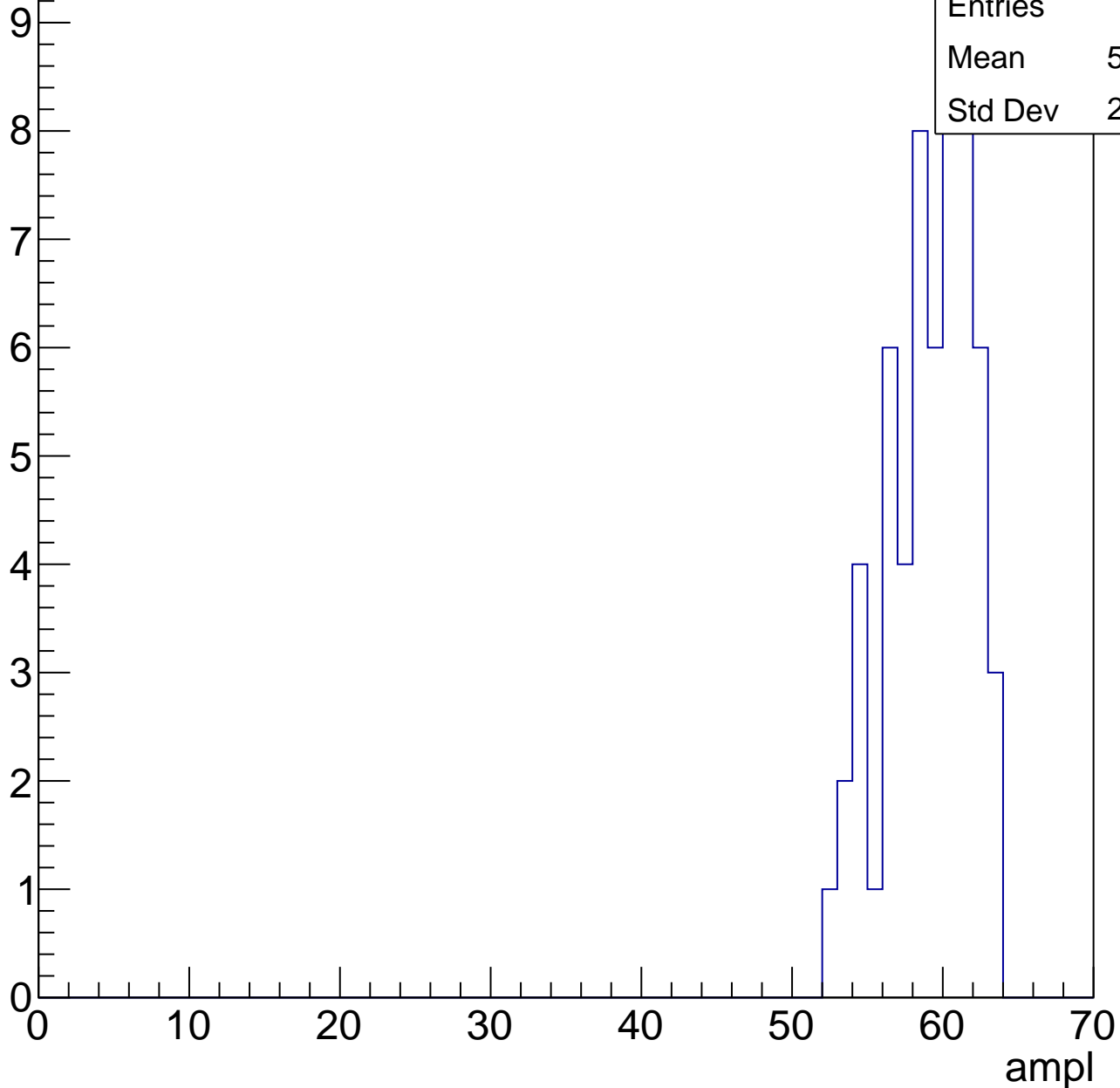
Entry



B1L103S, U23-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



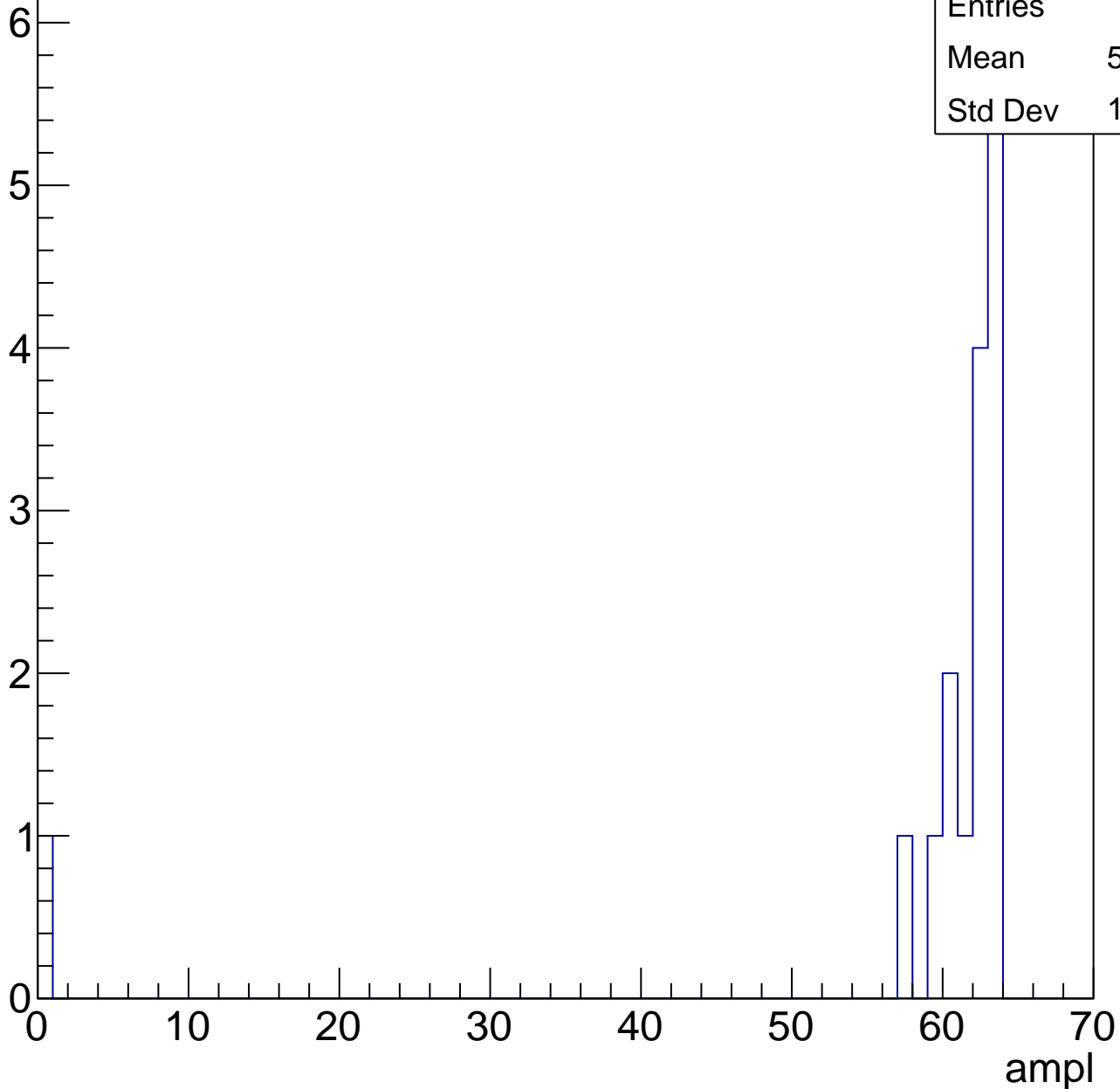
Entries	58
Mean	58.64
Std Dev	2.796

B1L103S, U23-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	57.69
Std Dev	14.99



B1L103S, U23-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch8, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	29.85
Std Dev	4.995

Gaus mean : 30.7105

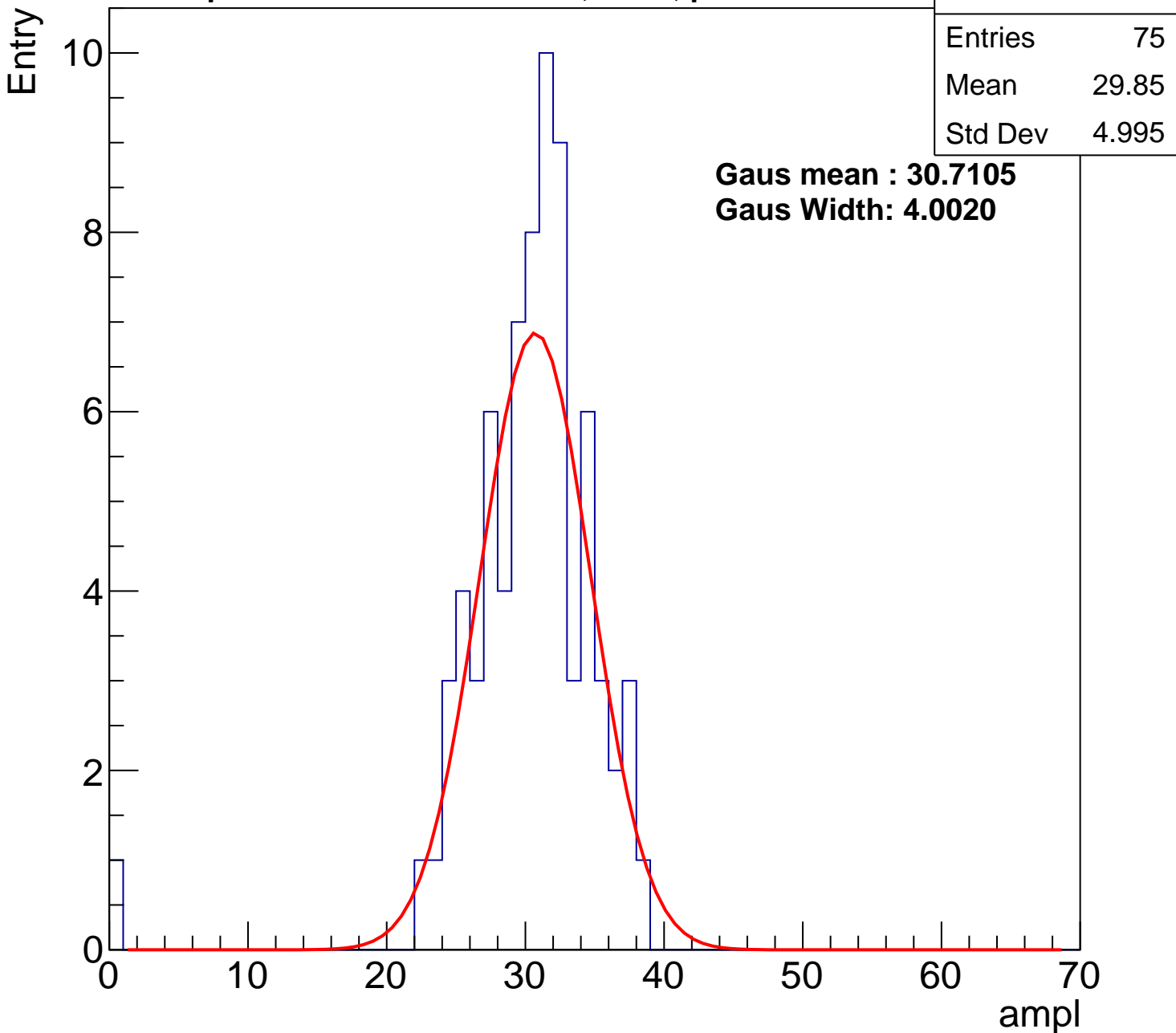
Gaus Width: 4.0020

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch8, adc1

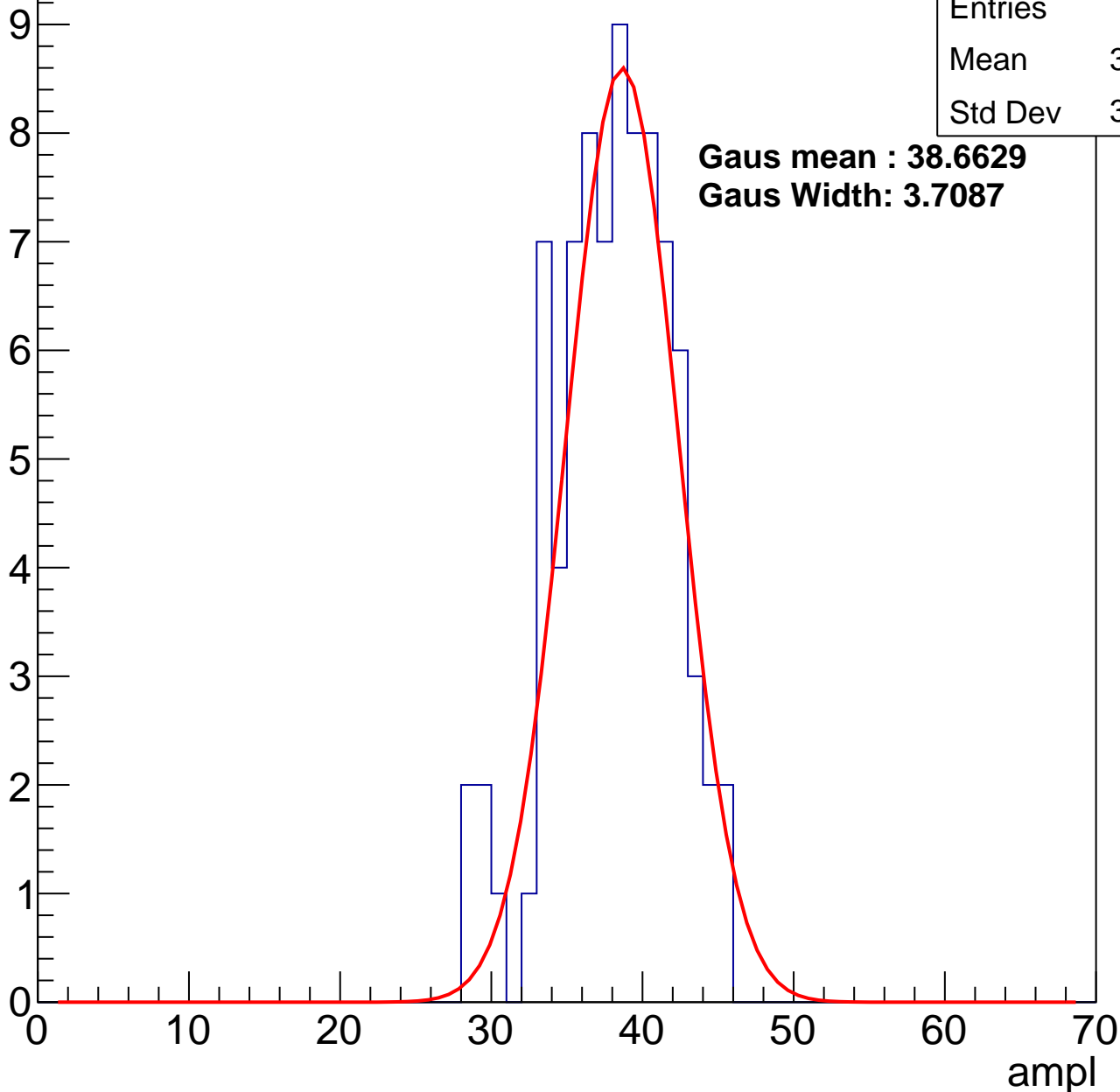
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	37.56
Std Dev	3.828

Gaus mean : 38.6629

Gaus Width: 3.7087



B1L103S, U23-ch8, adc2

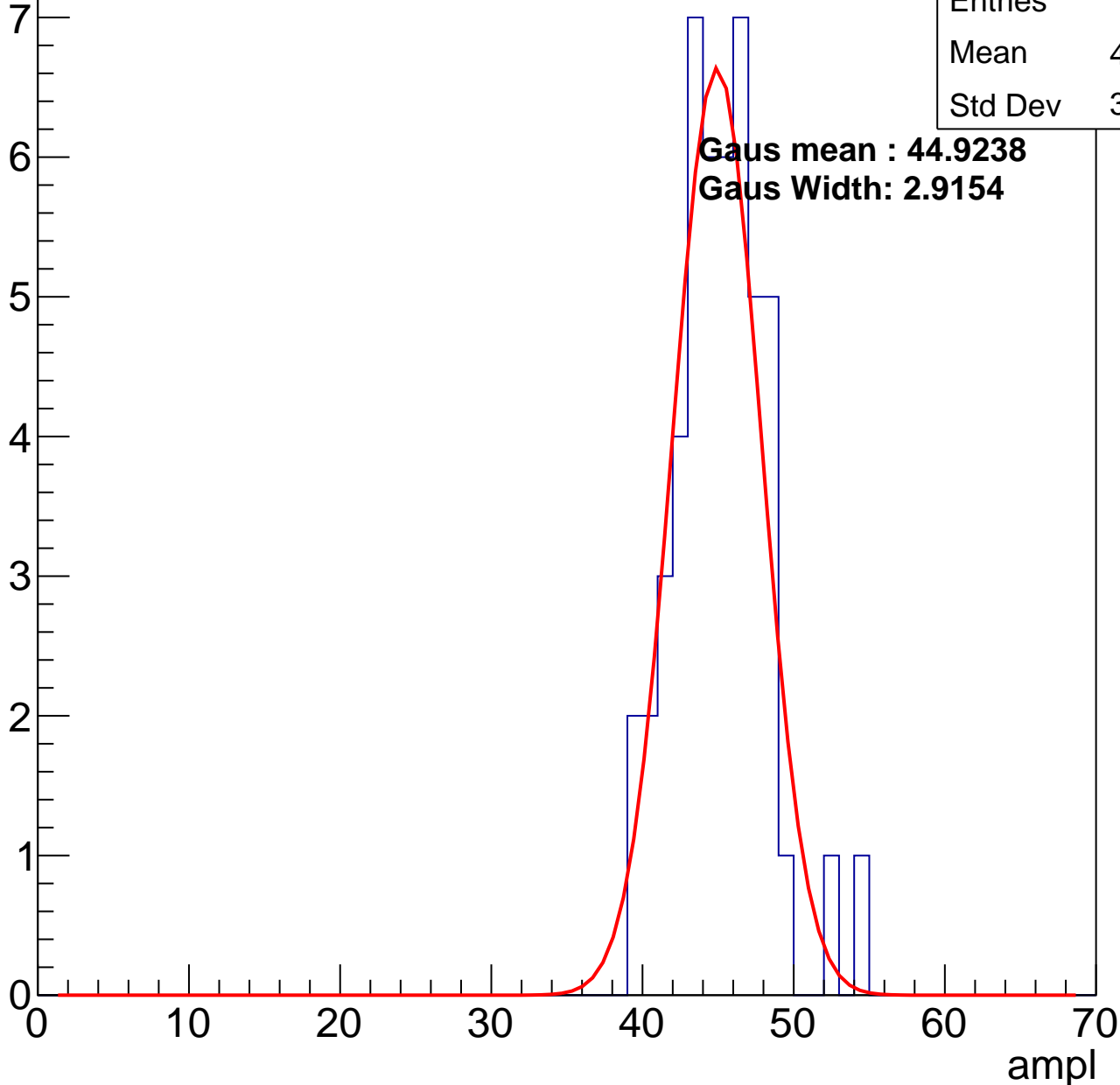
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	44.72
Std Dev	3.014

Gaus mean : 44.9238

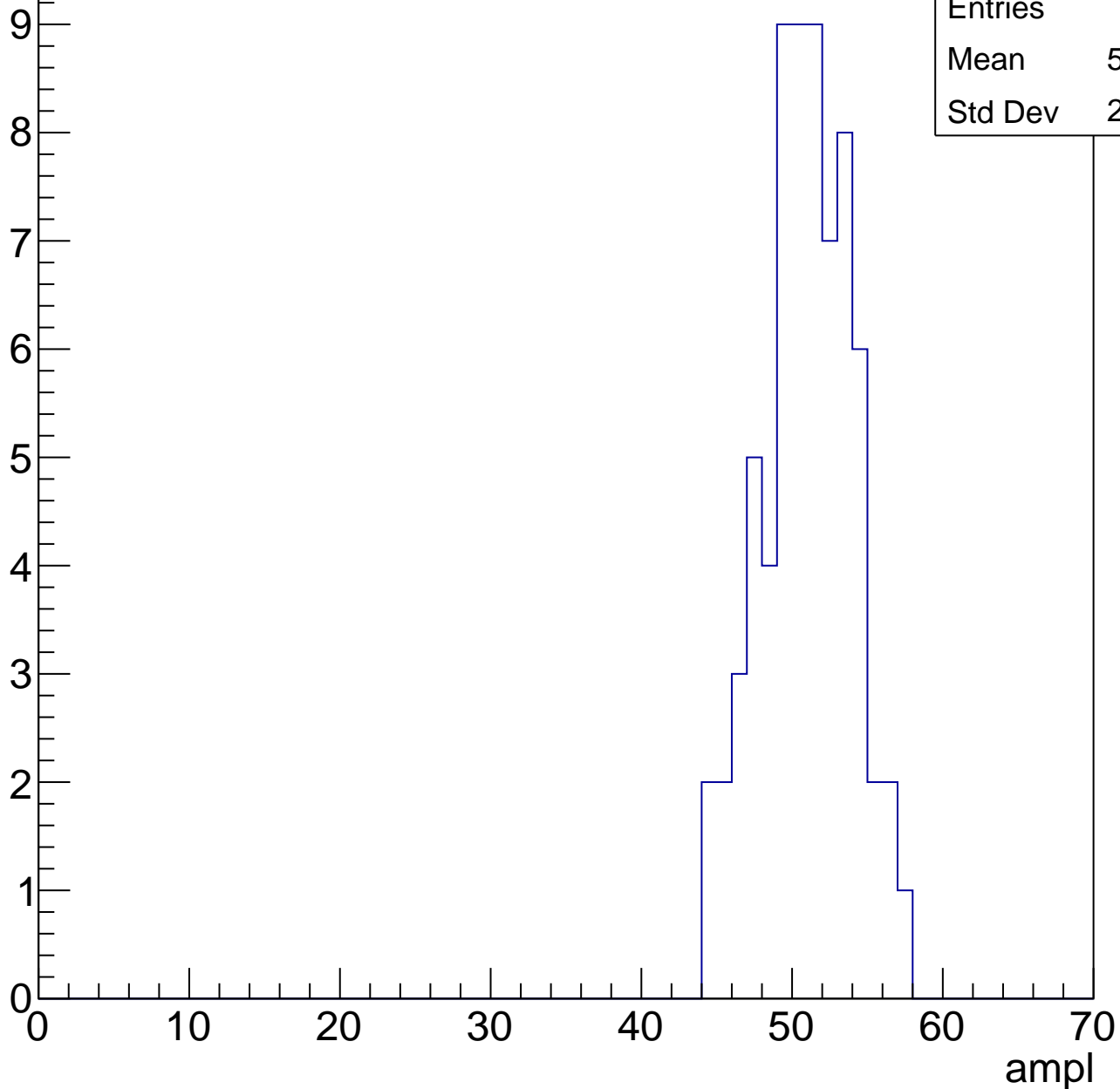
Gaus Width: 2.9154



B1L103S, U23-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

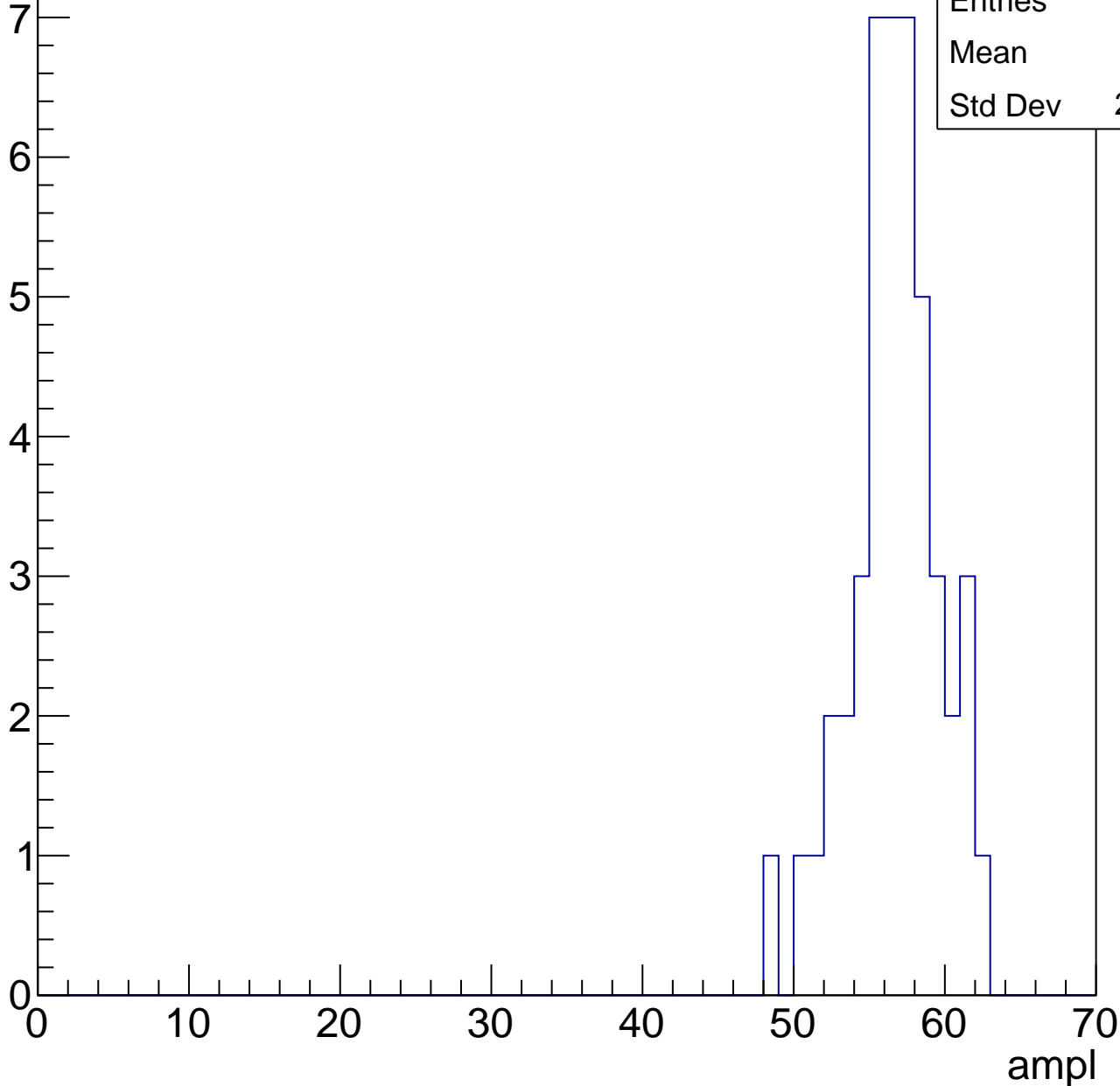


B1L103S, U23-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	56.2
Std Dev	2.941



B1L103S, U23-ch8, adc5

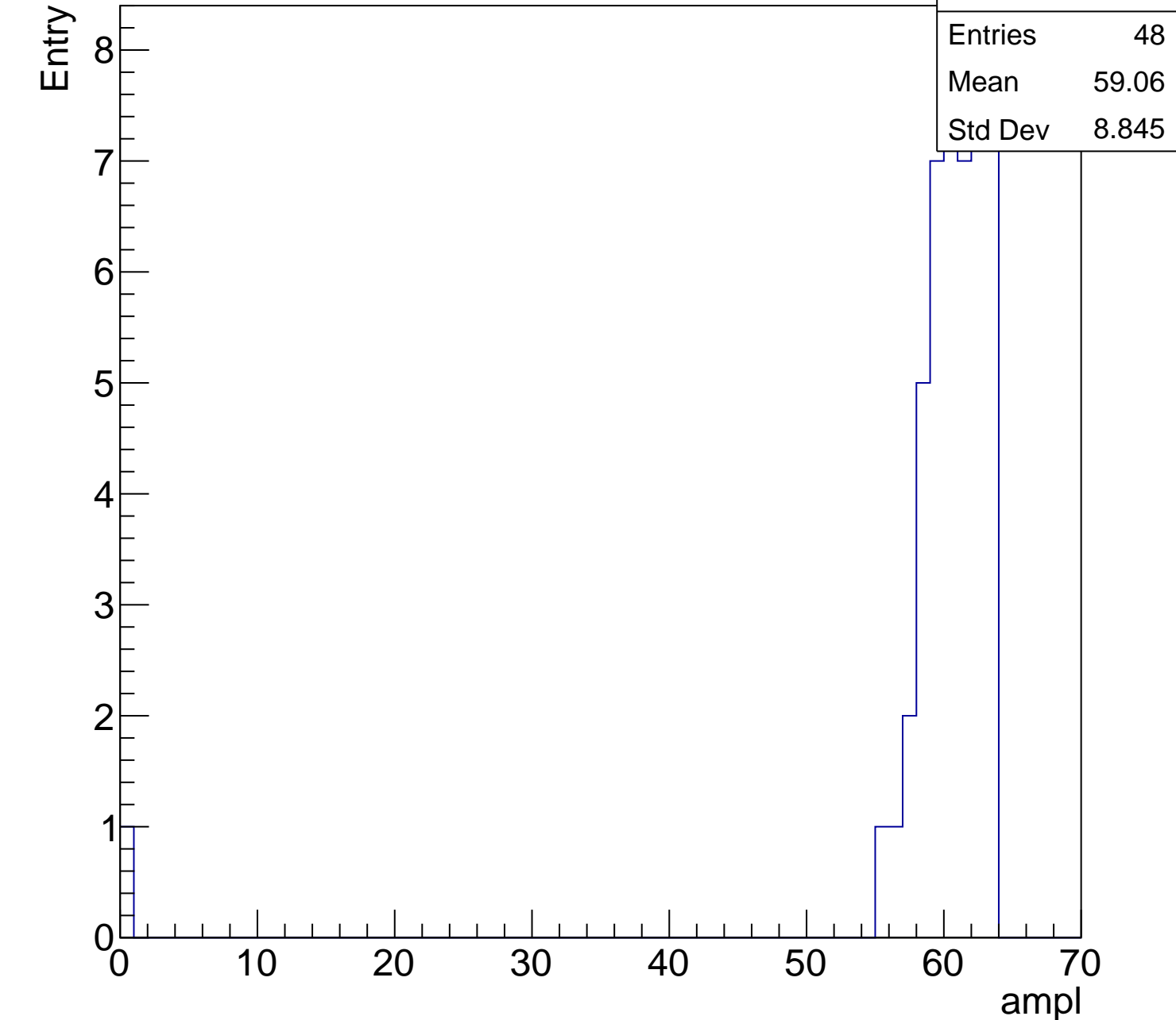
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.06
Std Dev	8.845

ampl



B1L103S, U23-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch9, adc0

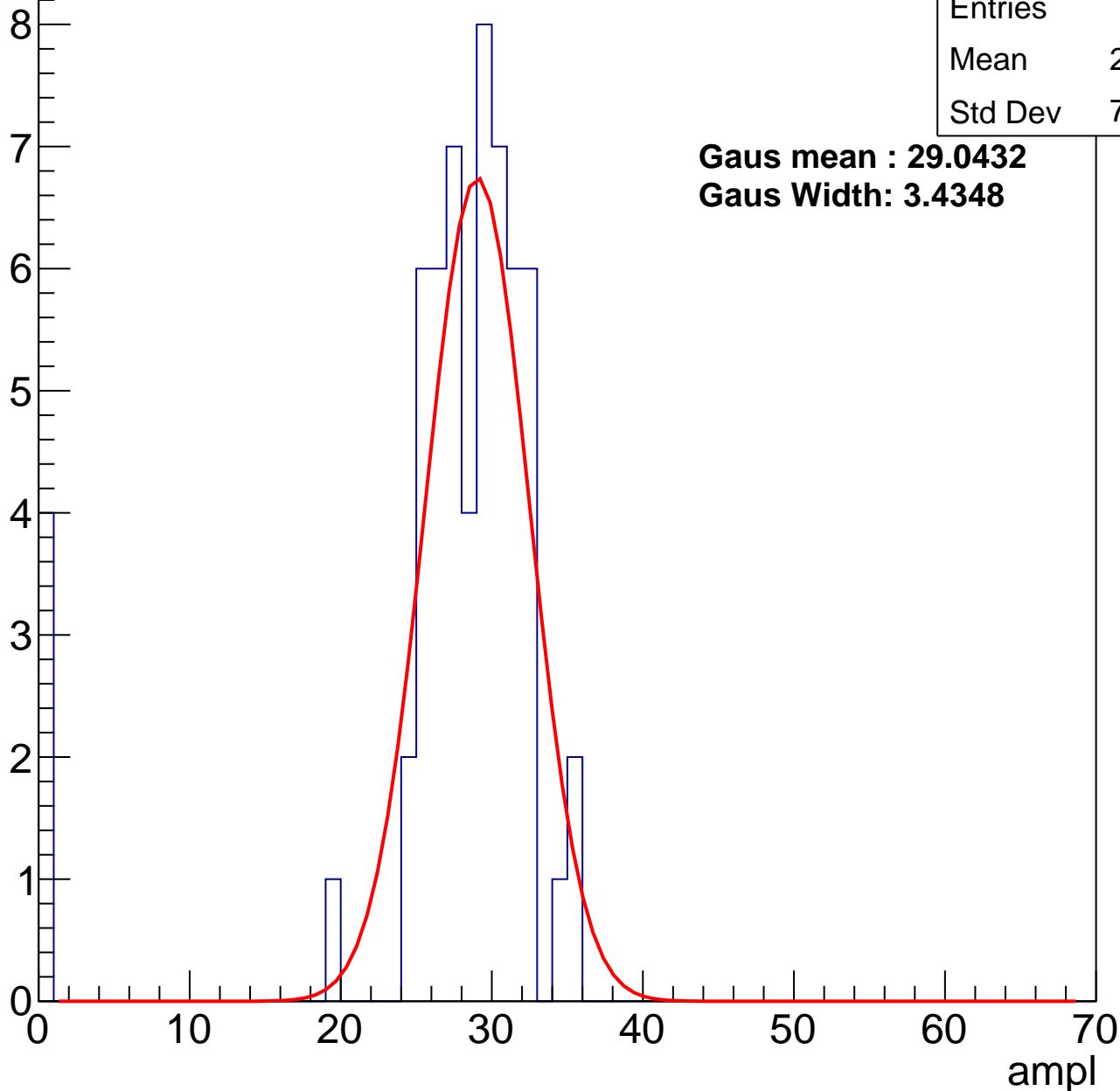
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	26.63
Std Dev	7.683

Gaus mean : 29.0432

Gaus Width: 3.4348



B1L103S, U23-ch9, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	35.26
Std Dev	3.069

Gaus mean : 35.1275

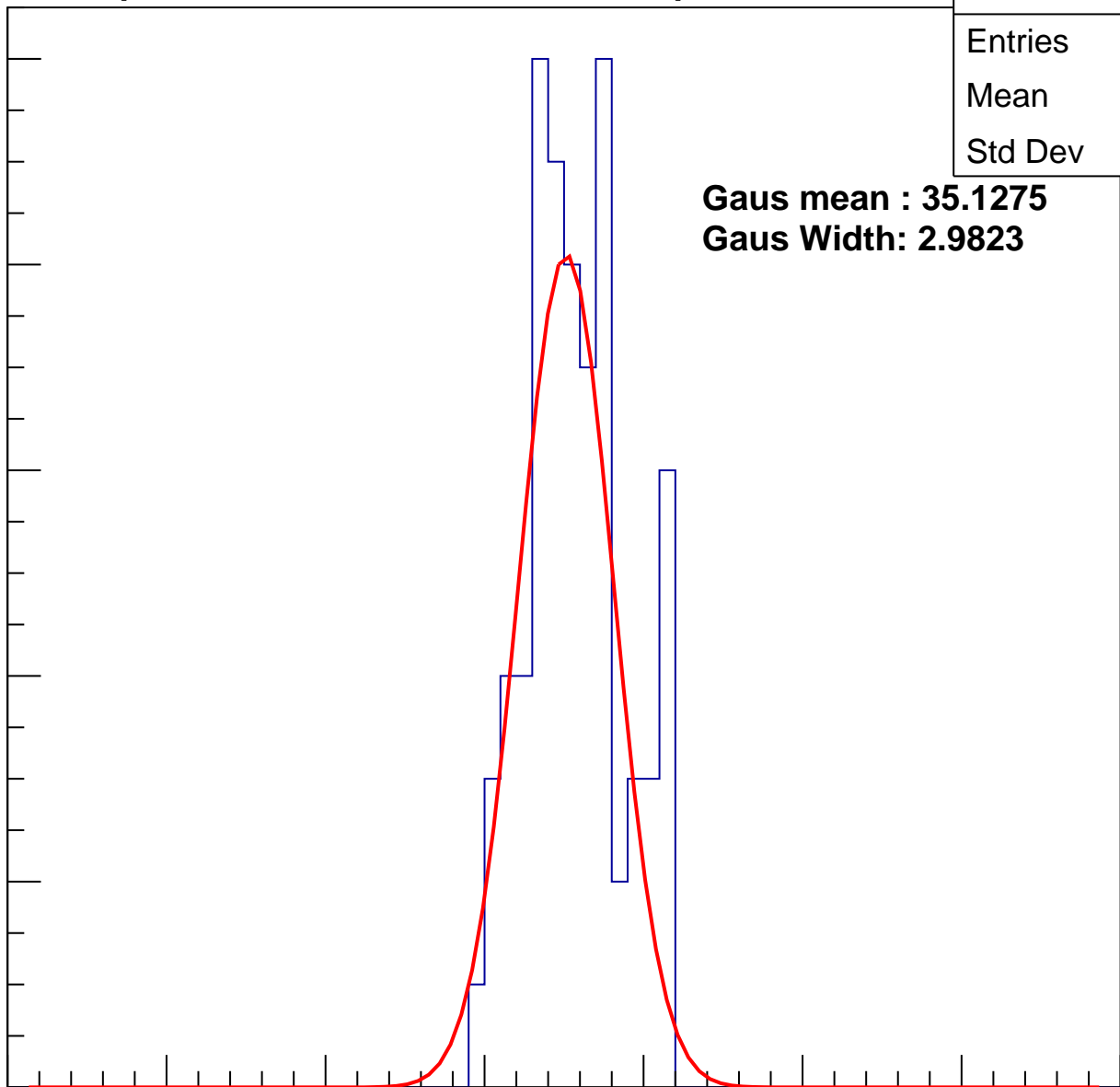
Gaus Width: 2.9823

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch9, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	42.11
Std Dev	3.174

Gaus mean : 41.8211

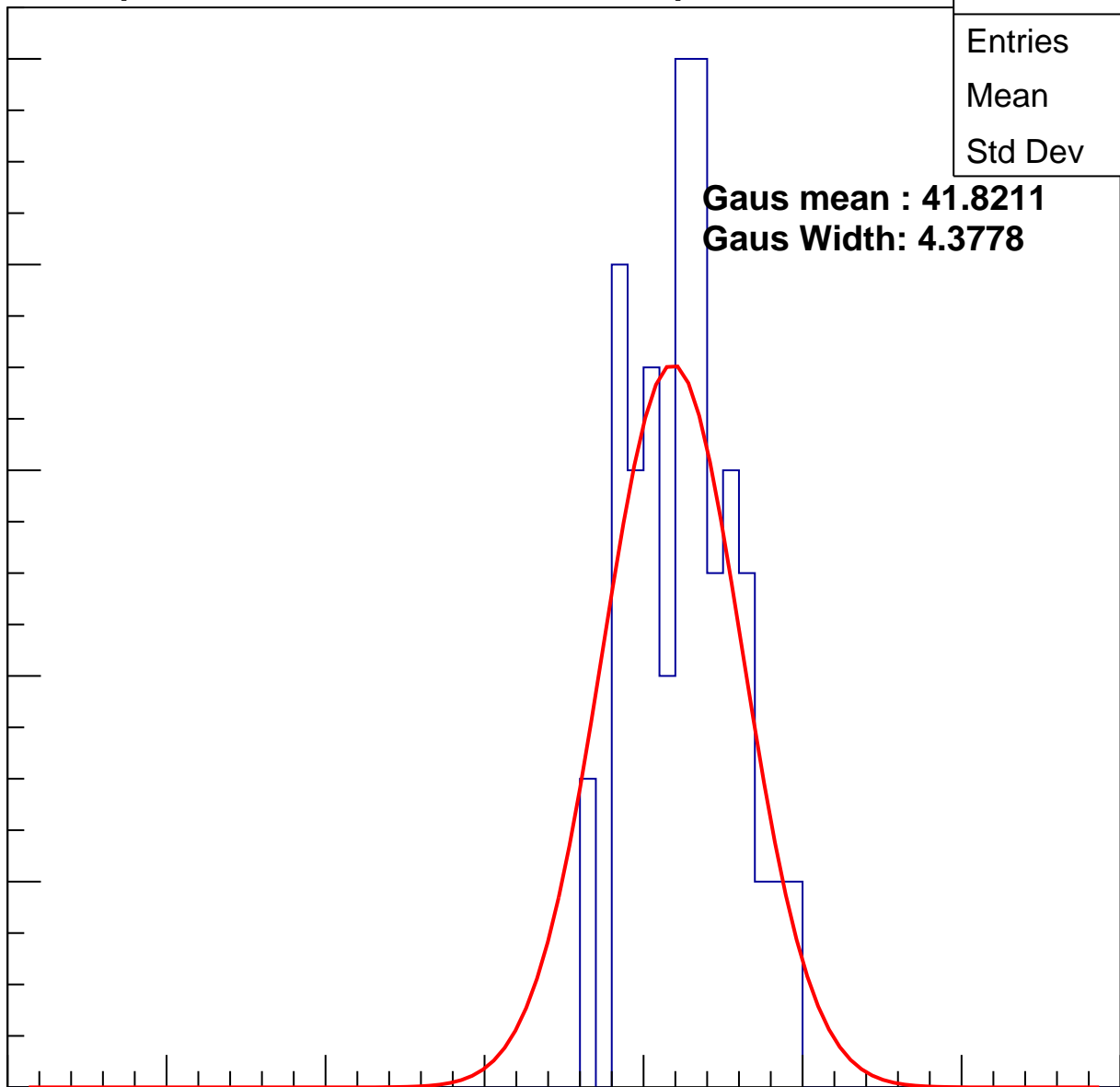
Gaus Width: 4.3778

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

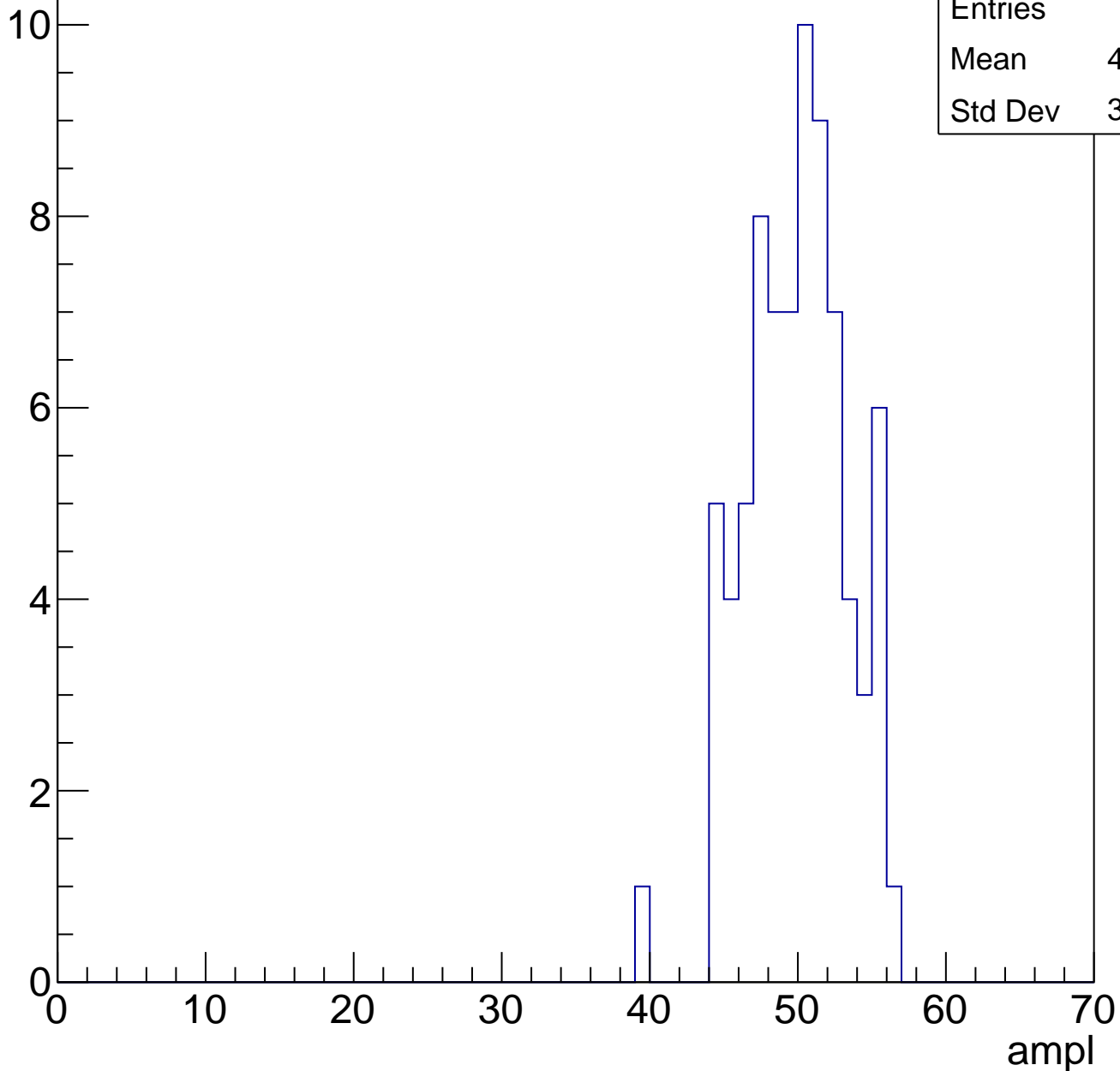


B1L103S, U23-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	49.44
Std Dev	3.363

Entry

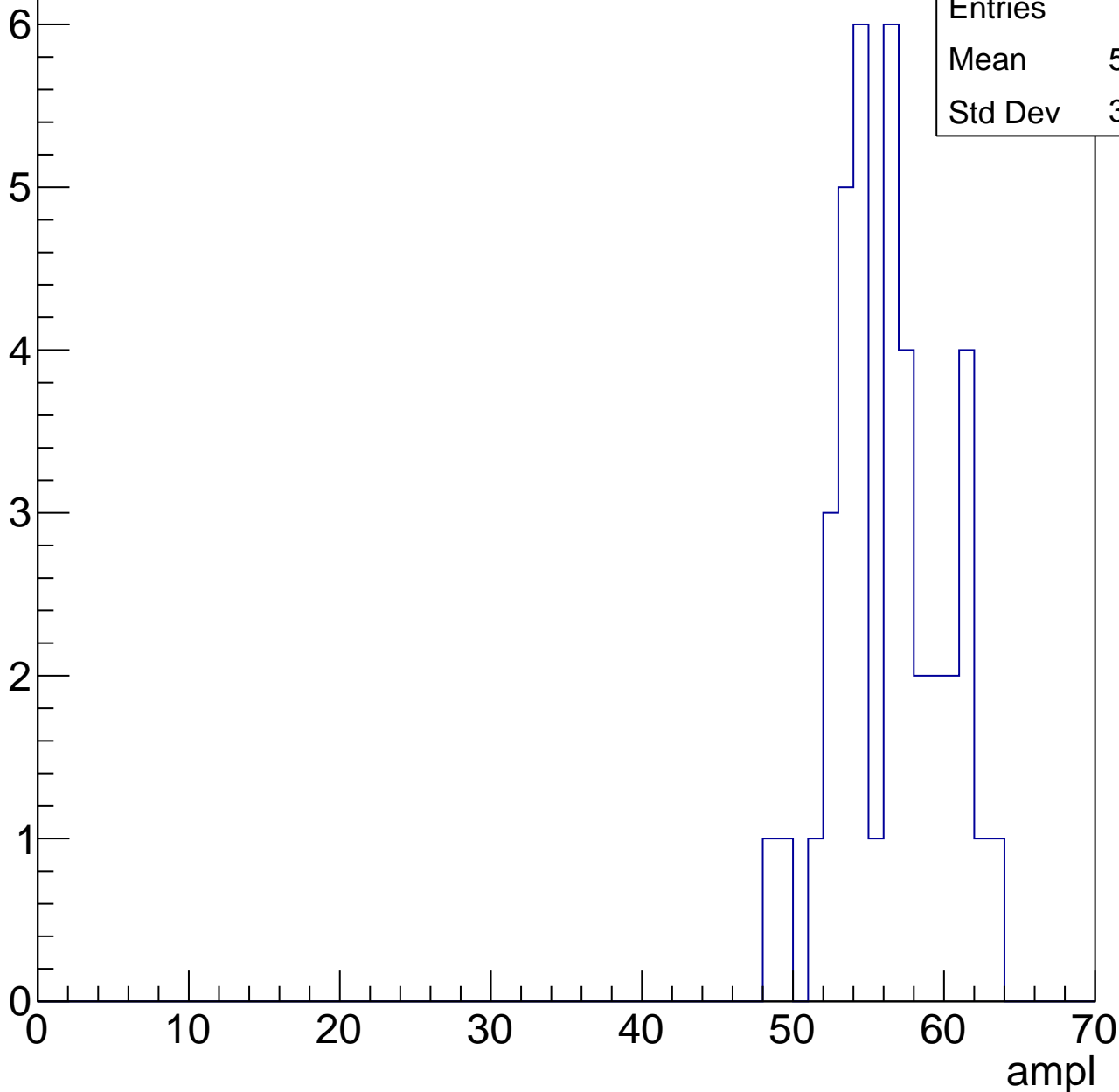


B1L103S, U23-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

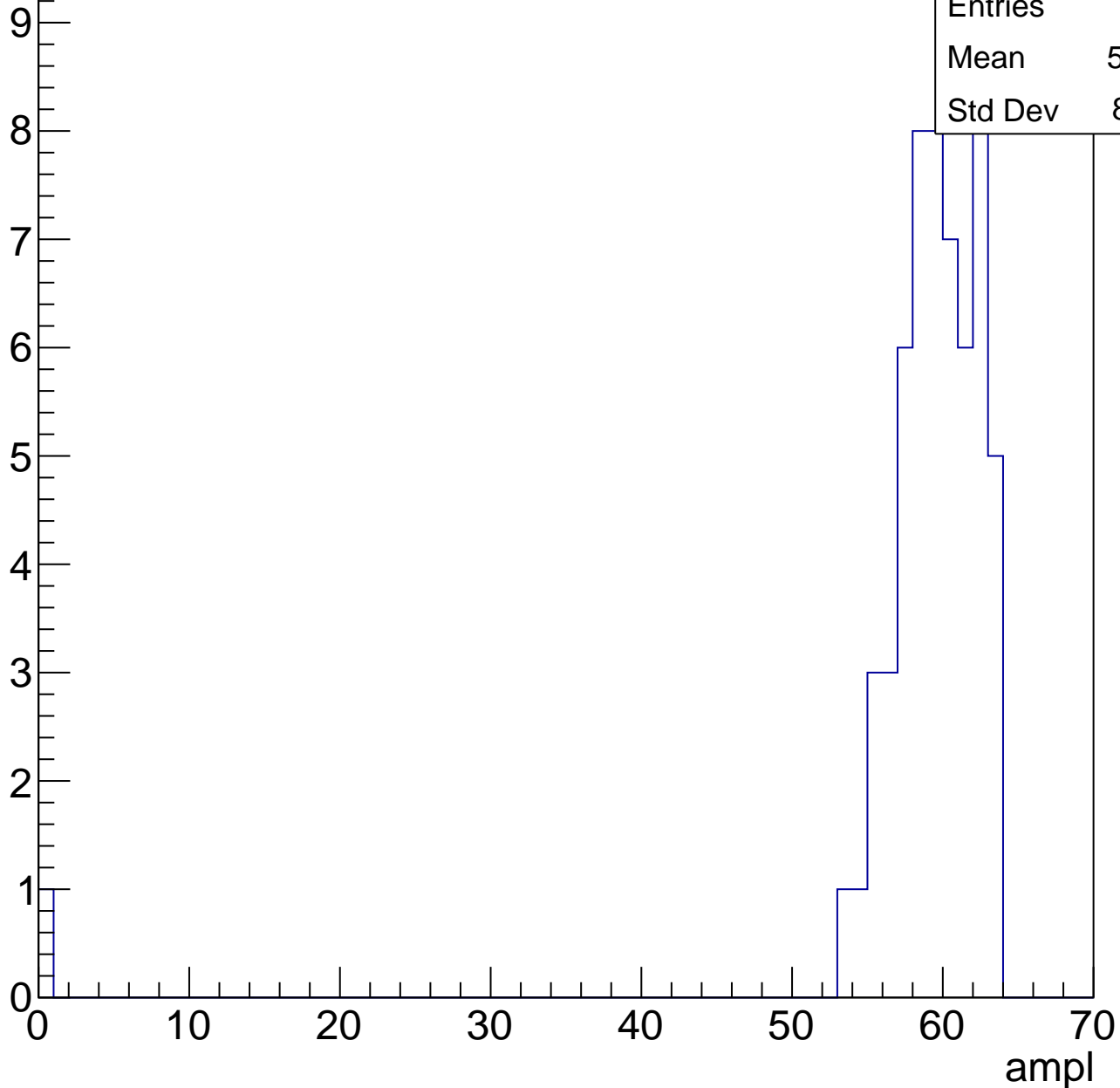
Entries	40
Mean	55.88
Std Dev	3.544



B1L103S, U23-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

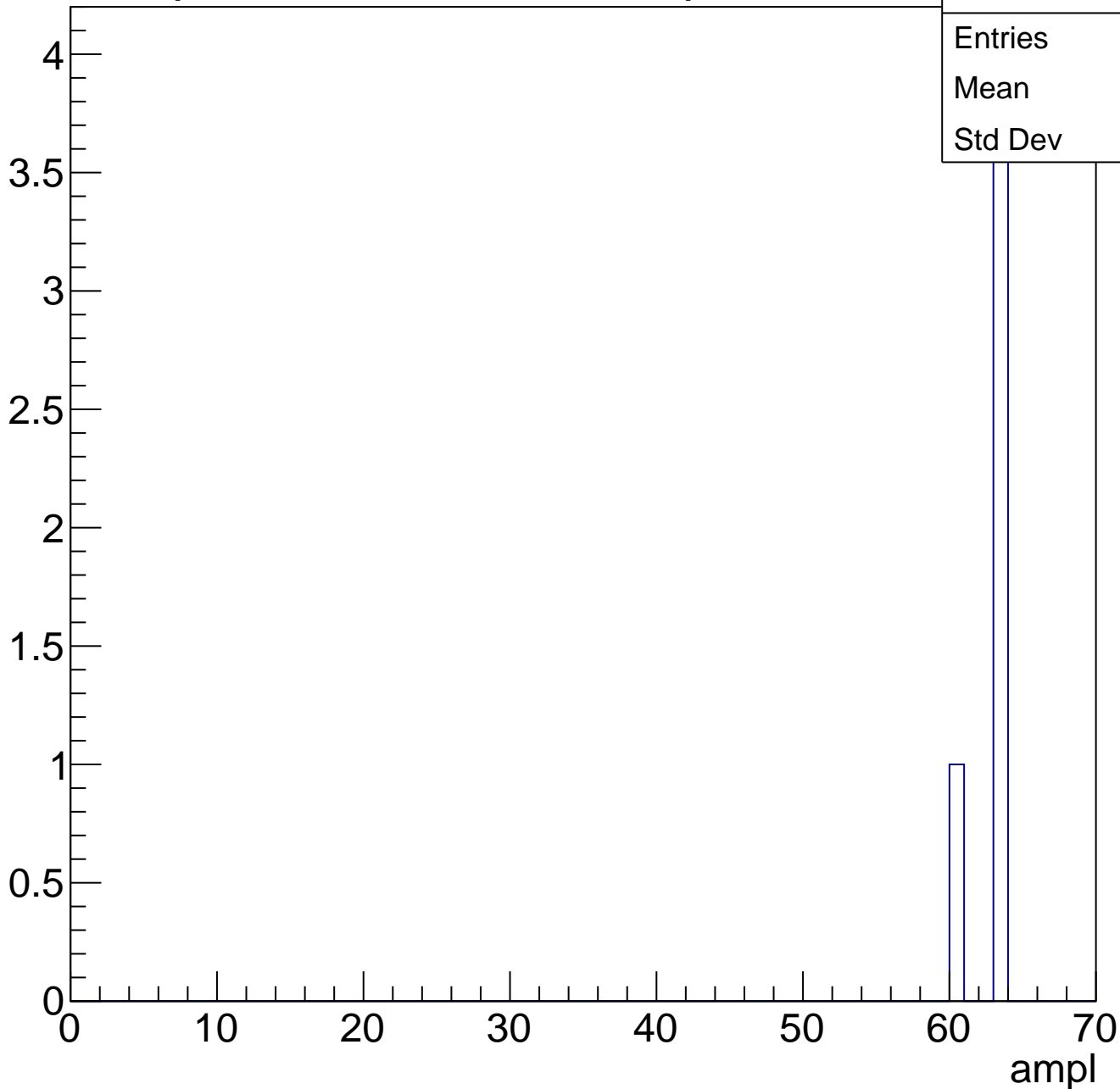
Entry



B1L103S, U23-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch10, adc0

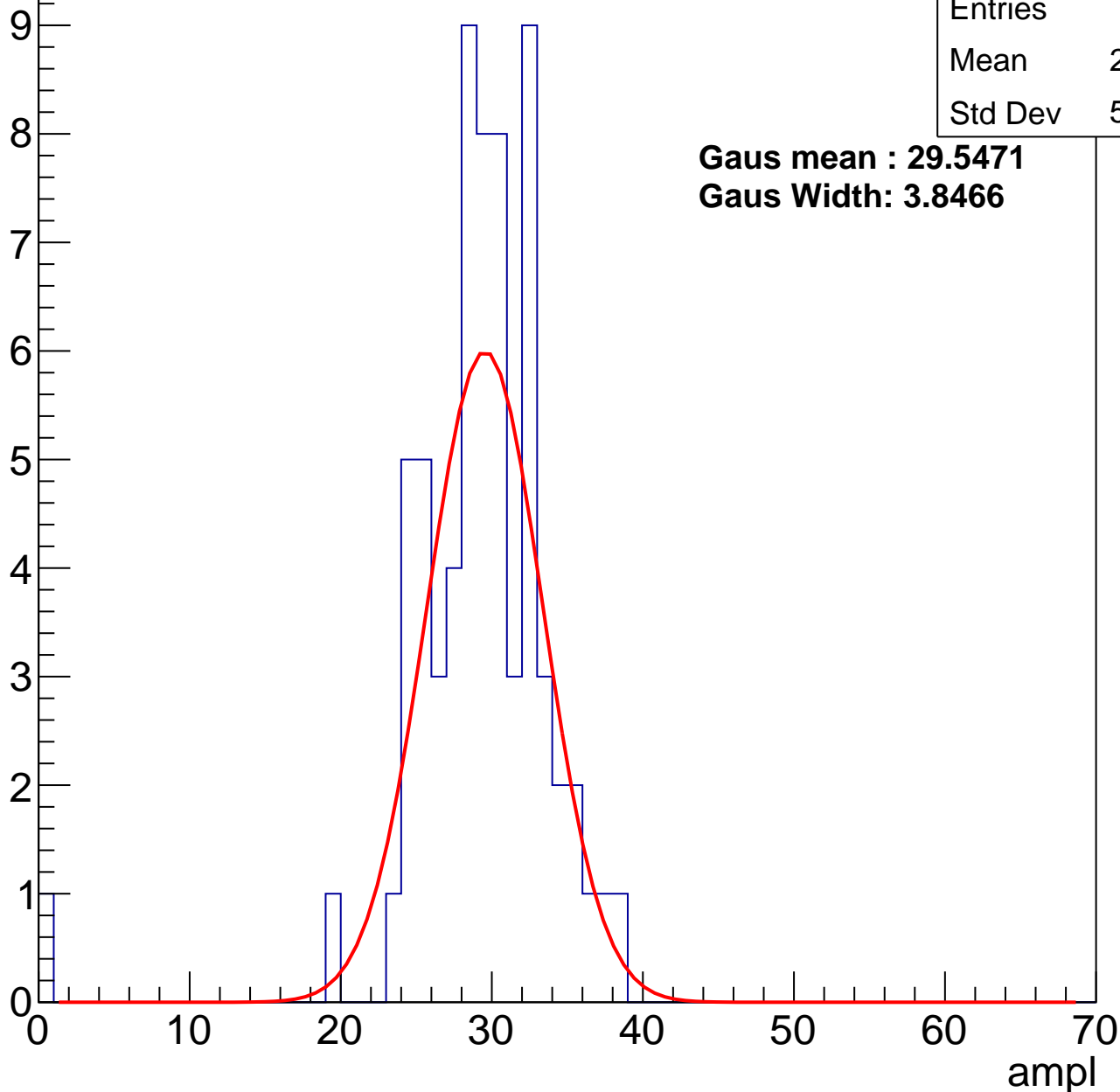
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.75
Std Dev	5.029

Gaus mean : 29.5471

Gaus Width: 3.8466



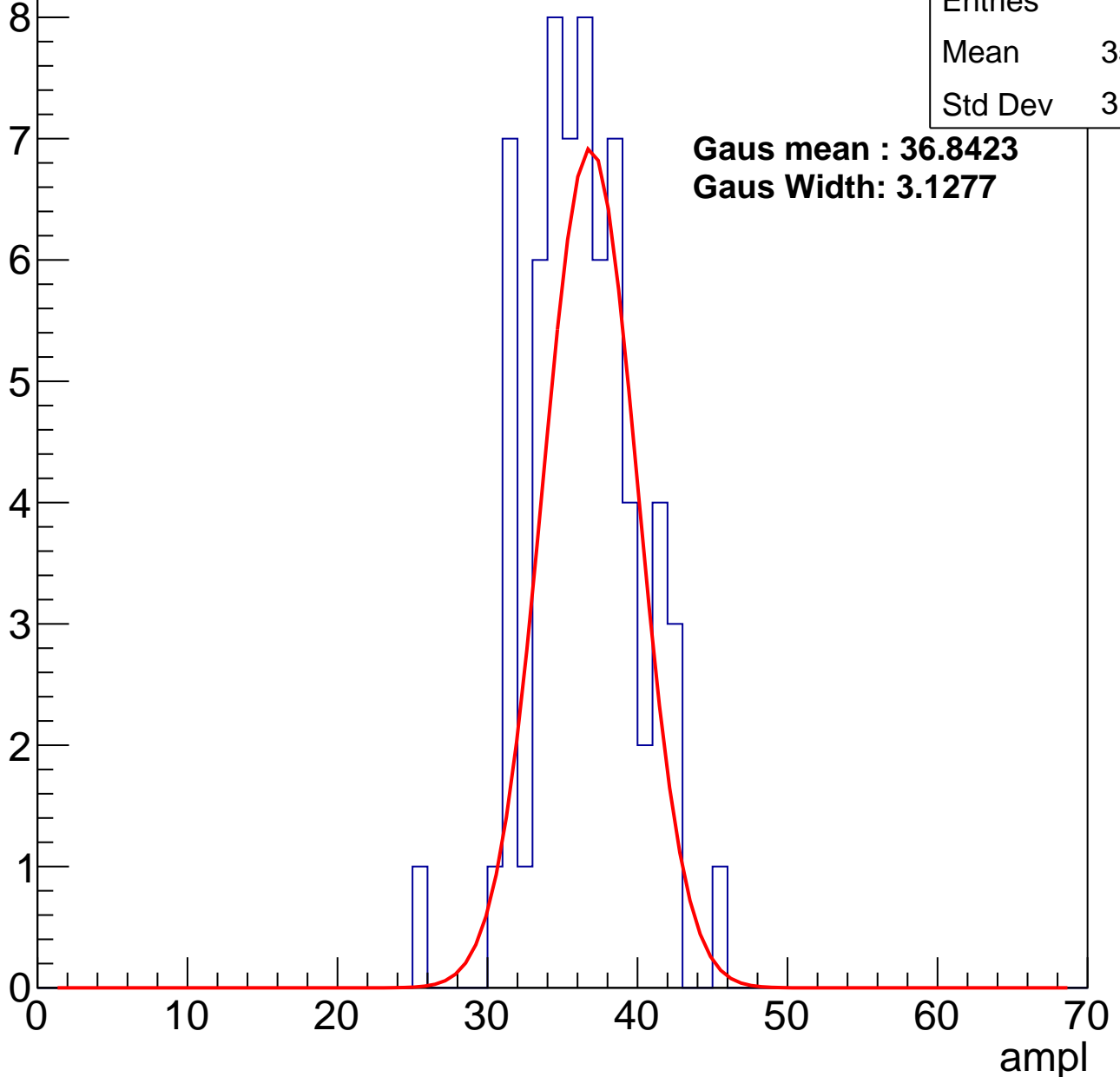
B1L103S, U23-ch10, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.85
Std Dev	3.547

Gaus mean : 36.8423
Gaus Width: 3.1277



B1L103S, U23-ch10, adc2

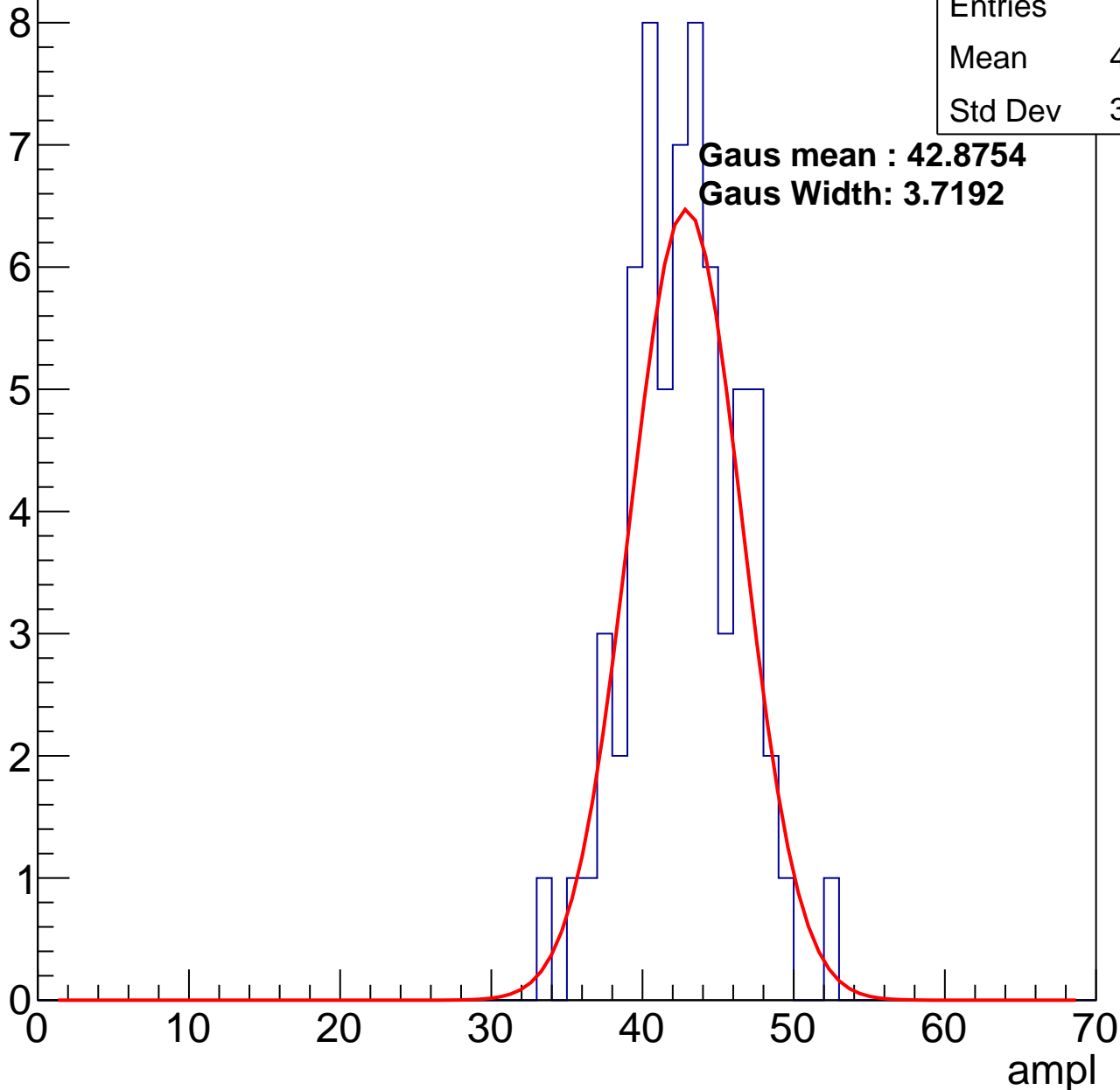
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.29
Std Dev	3.602

Gaus mean : 42.8754

Gaus Width: 3.7192

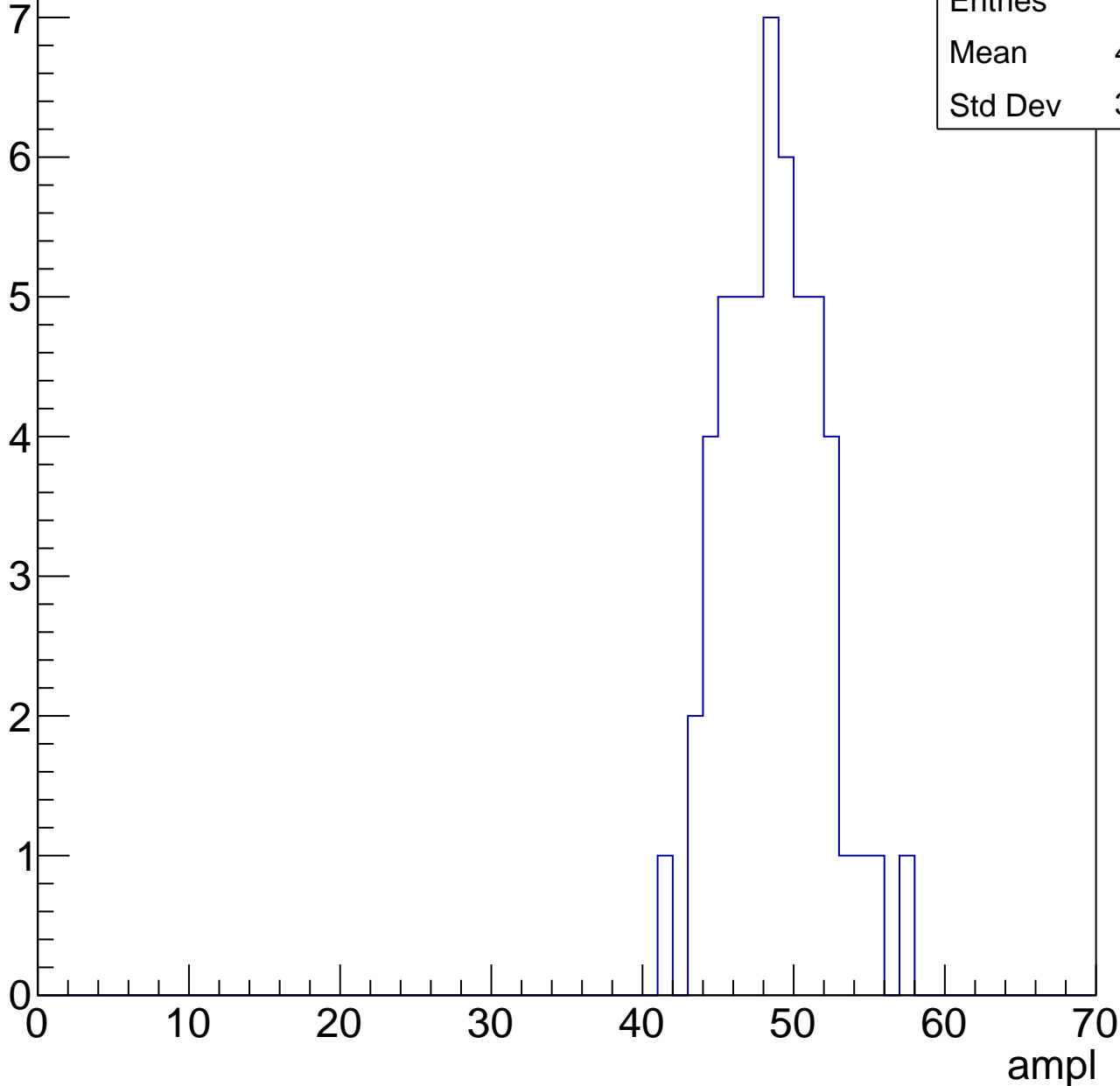


B1L103S, U23-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.21
Std Dev	3.241



B1L103S, U23-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	54.76
Std Dev	3.579

Entry

10

8

6

4

2

0

0

10

20

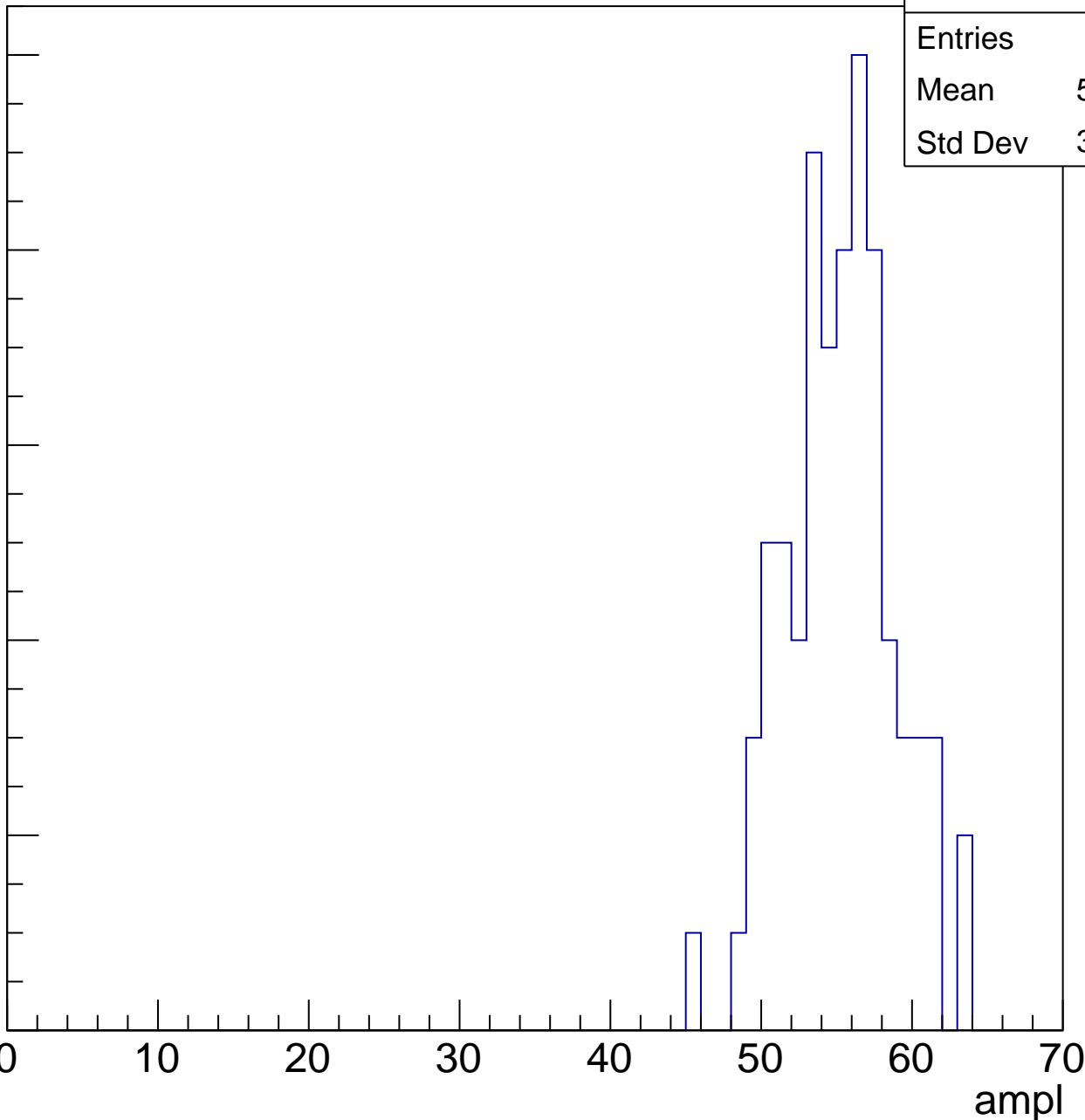
30

40

50

60

ampl

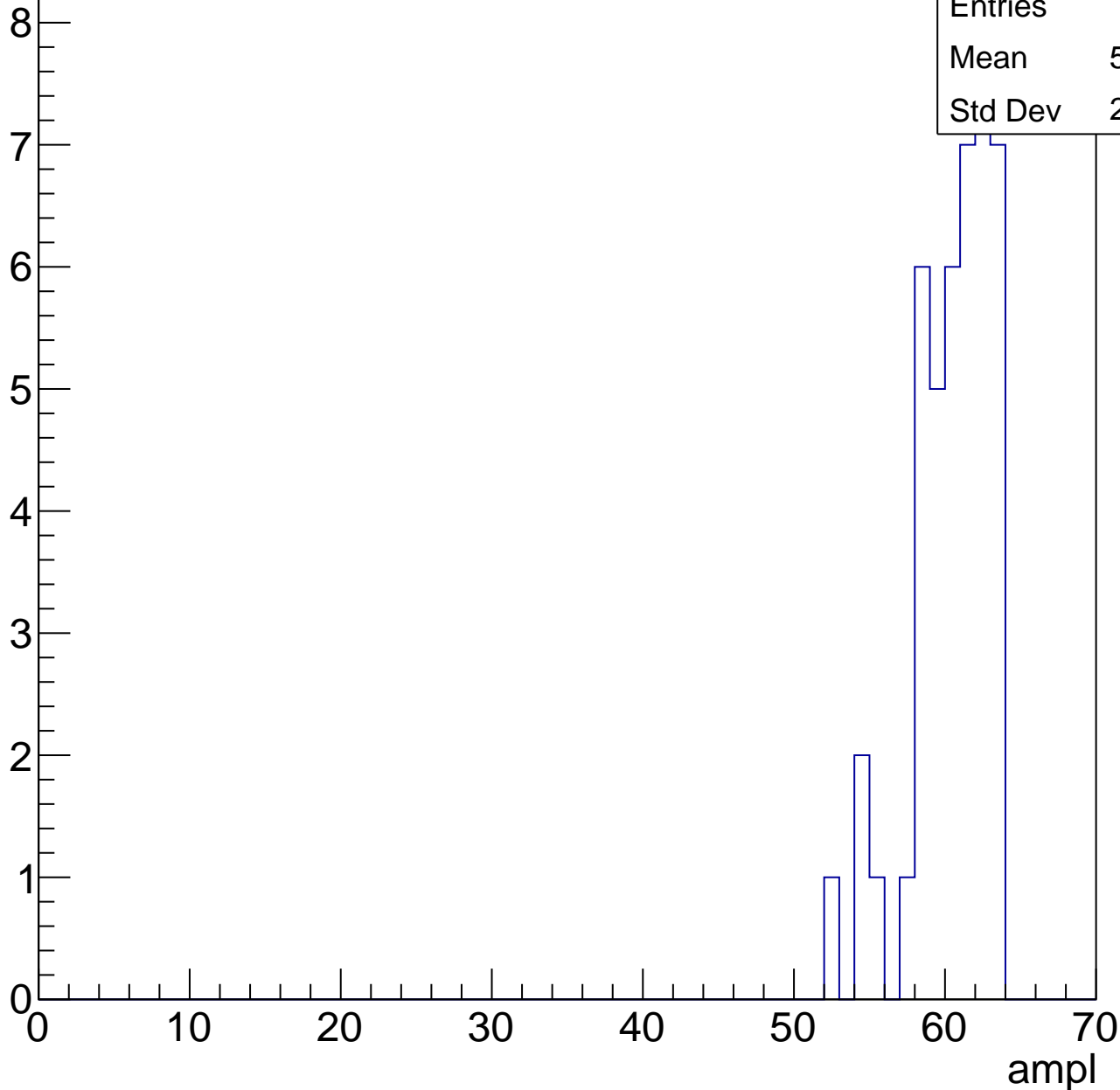


B1L103S, U23-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

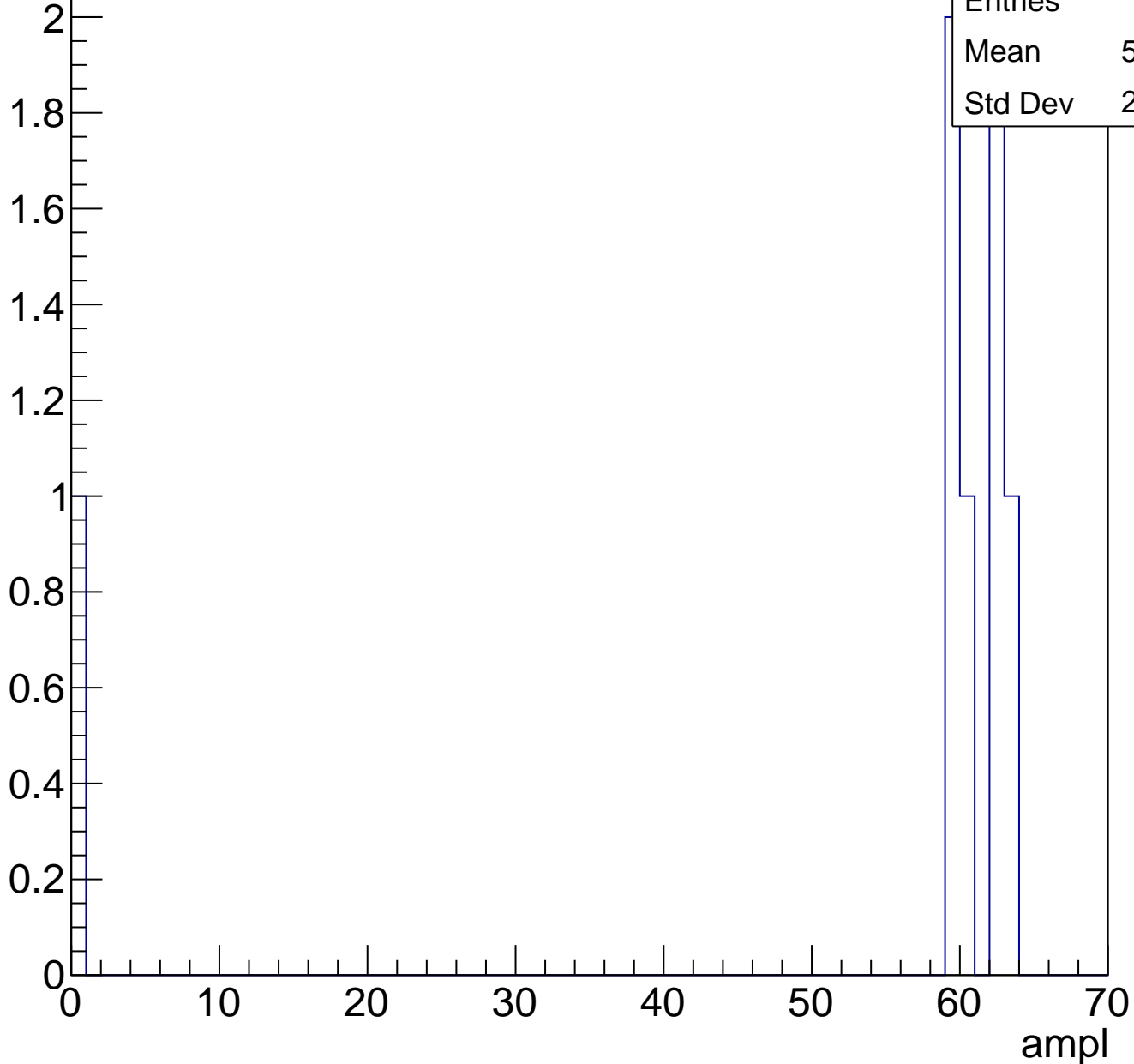
Entries	44
Mean	59.98
Std Dev	2.615



B1L103S, U23-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch11, adc0

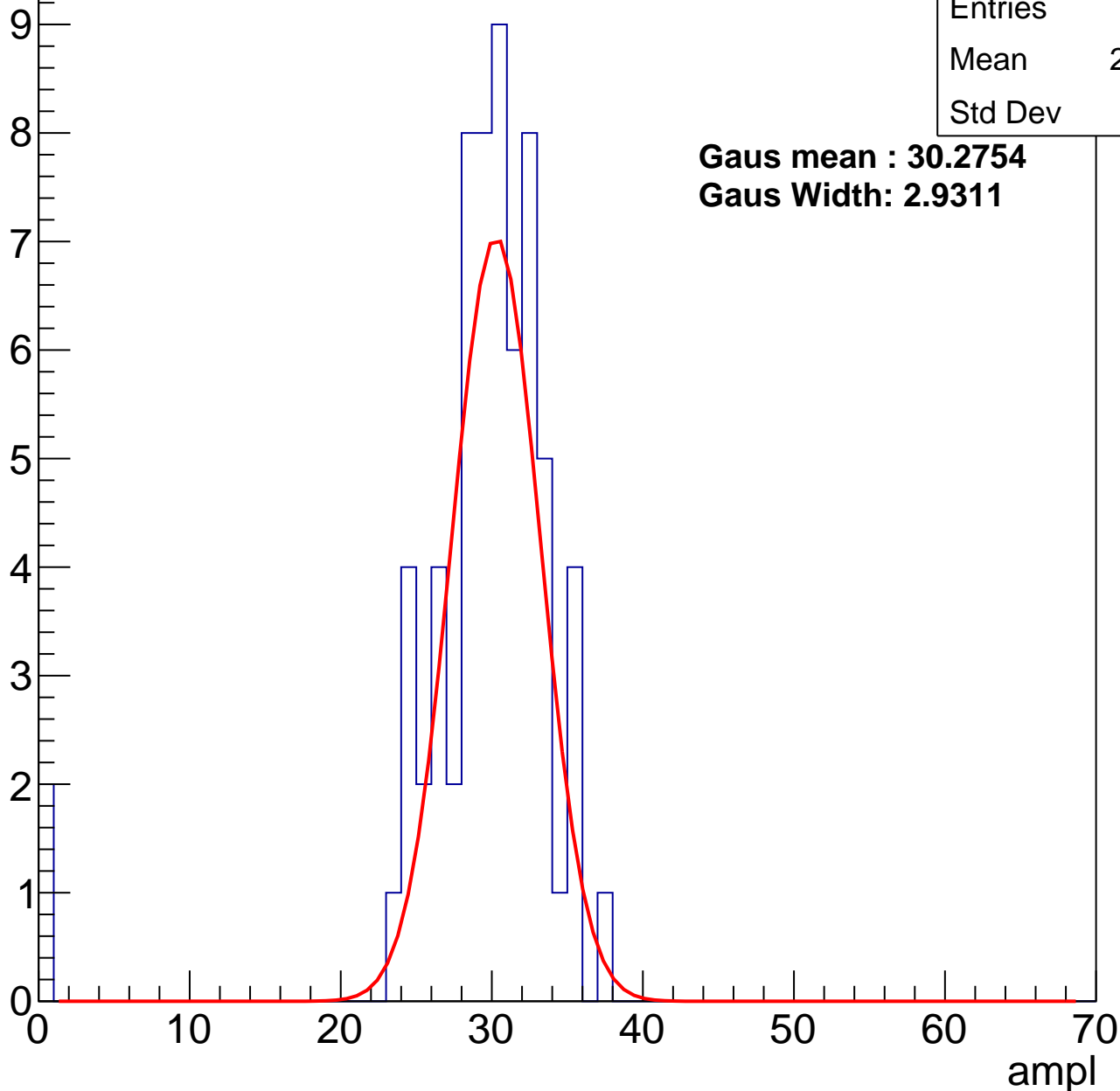
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28.78
Std Dev	5.97

Gaus mean : 30.2754

Gaus Width: 2.9311



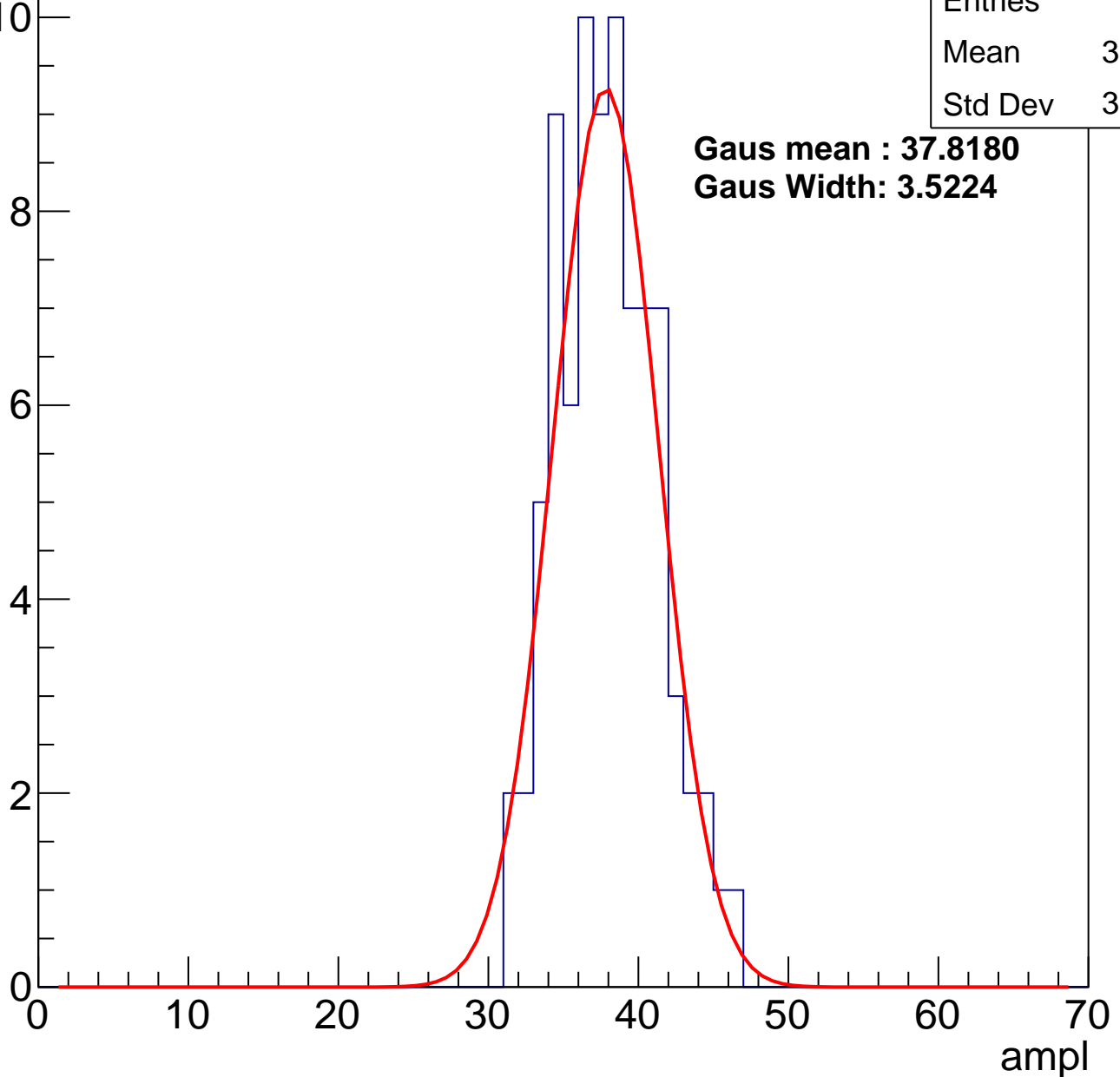
B1L103S, U23-ch11, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	37.48
Std Dev	3.294

Gaus mean : 37.8180
Gaus Width: 3.5224



B1L103S, U23-ch11, adc2

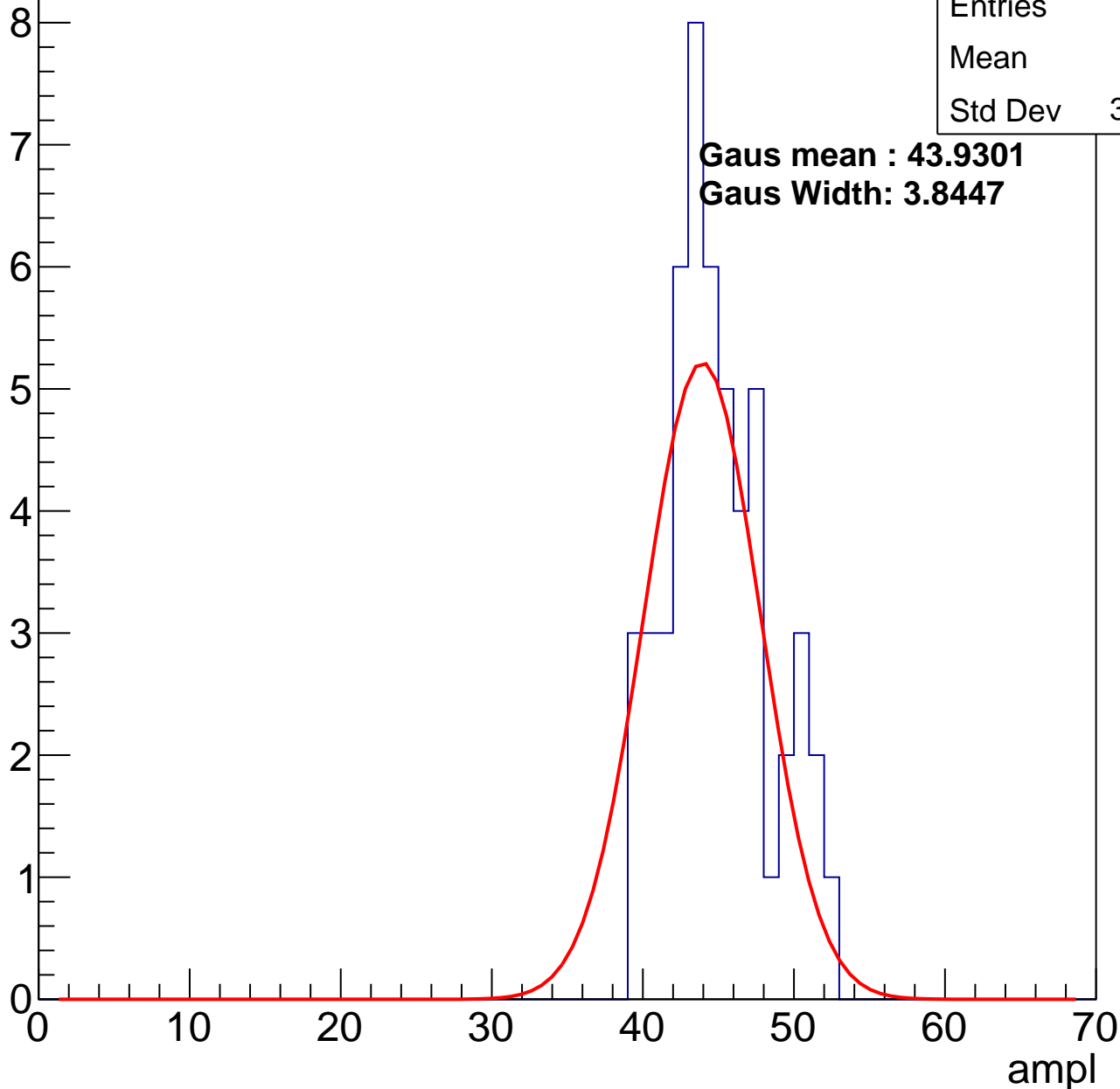
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	44.5
Std Dev	3.314

Gaus mean : 43.9301

Gaus Width: 3.8447

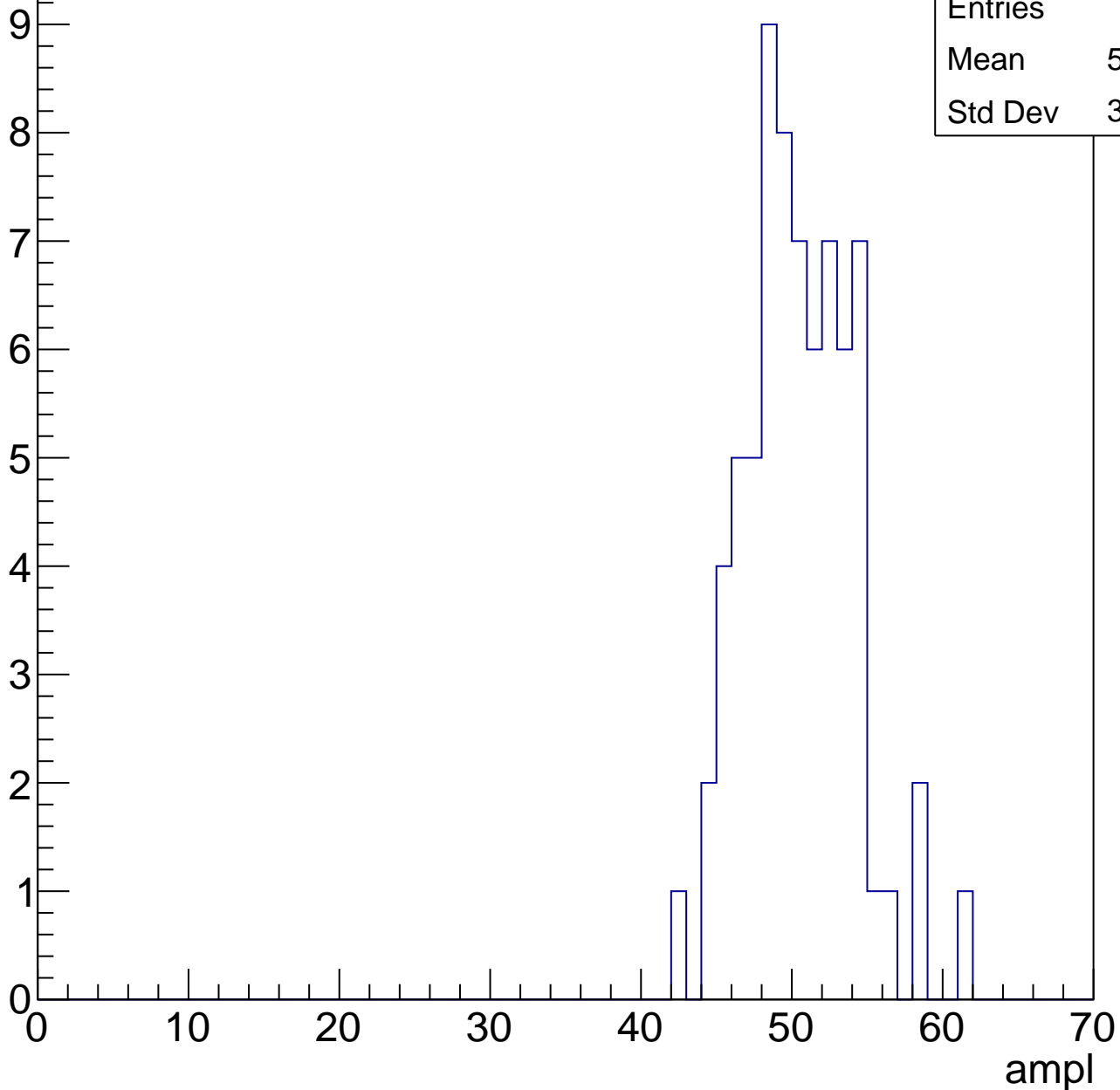


B1L103S, U23-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	50.04
Std Dev	3.565

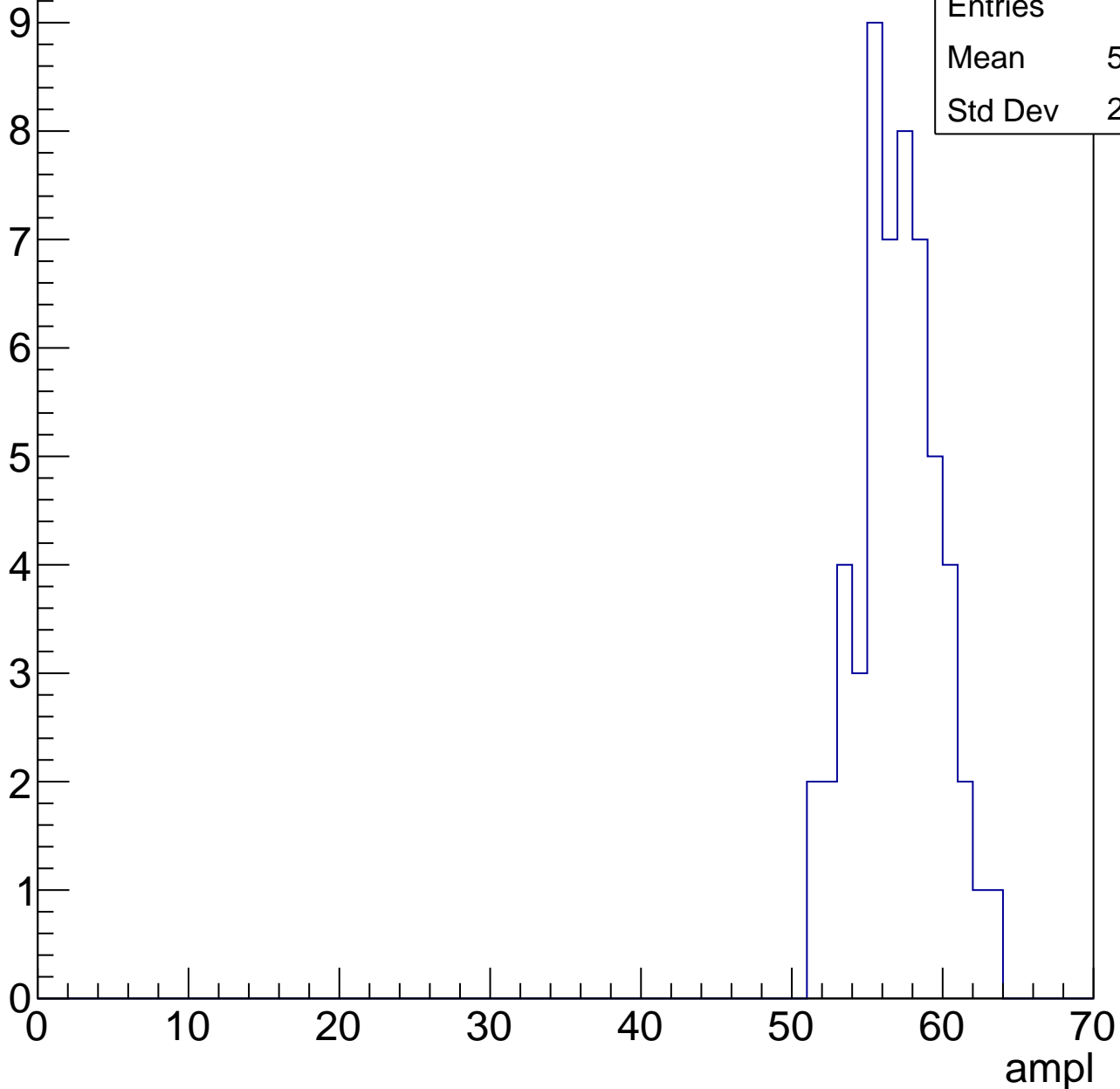


B1L103S, U23-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.56
Std Dev	2.702

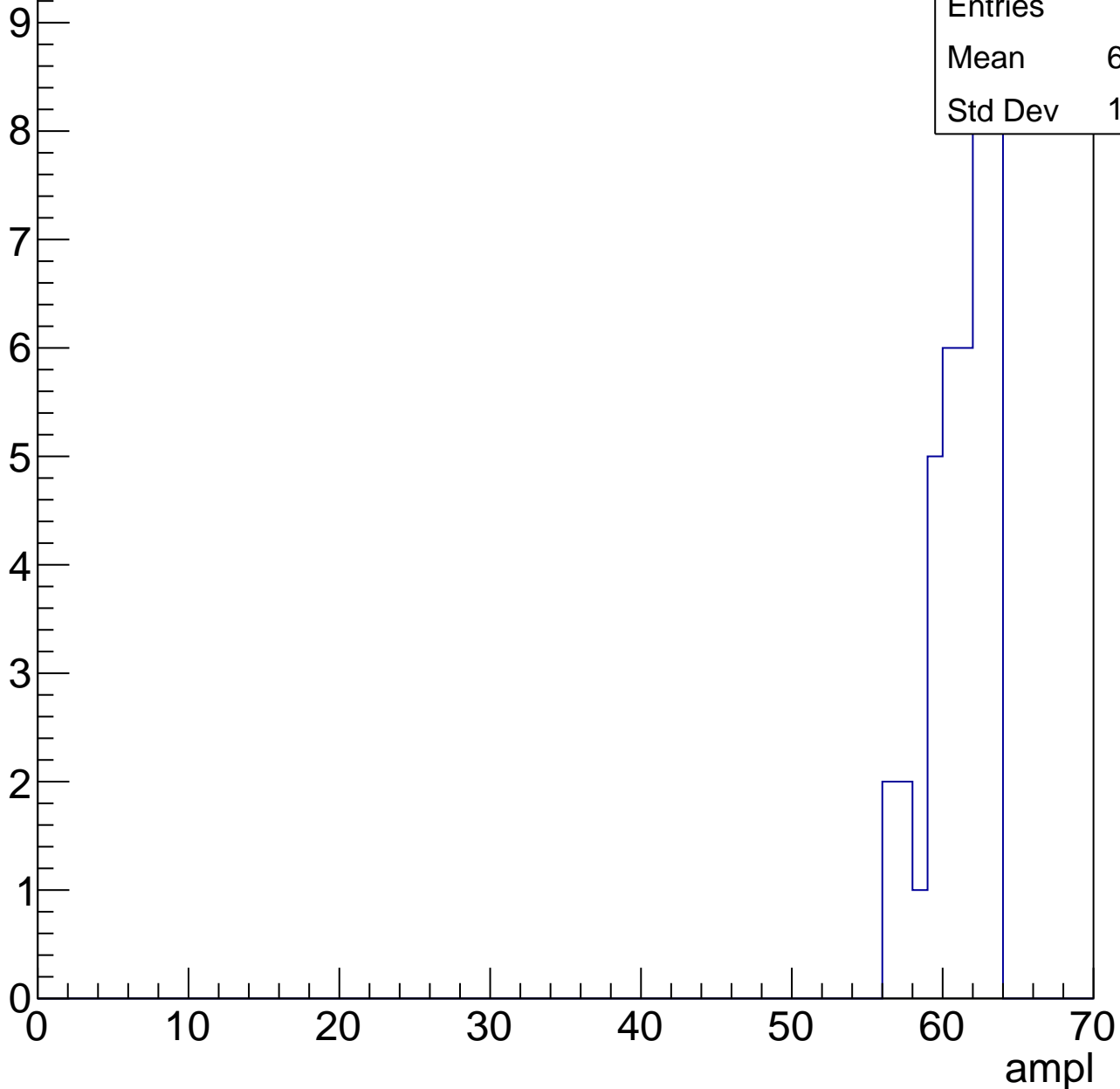


B1L103S, U23-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

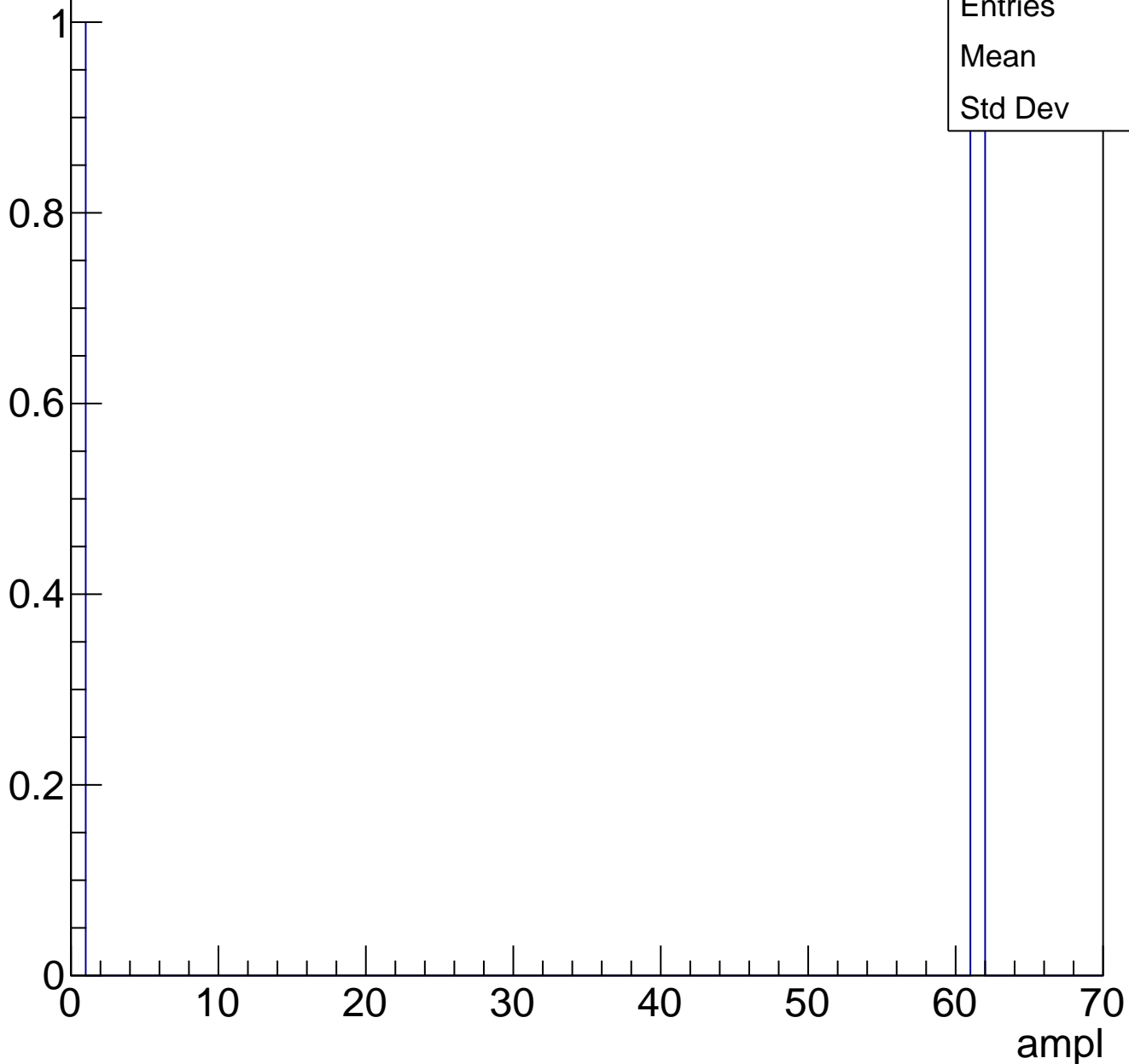
Entries	40
Mean	60.75
Std Dev	1.997



B1L103S, U23-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch12, adc0

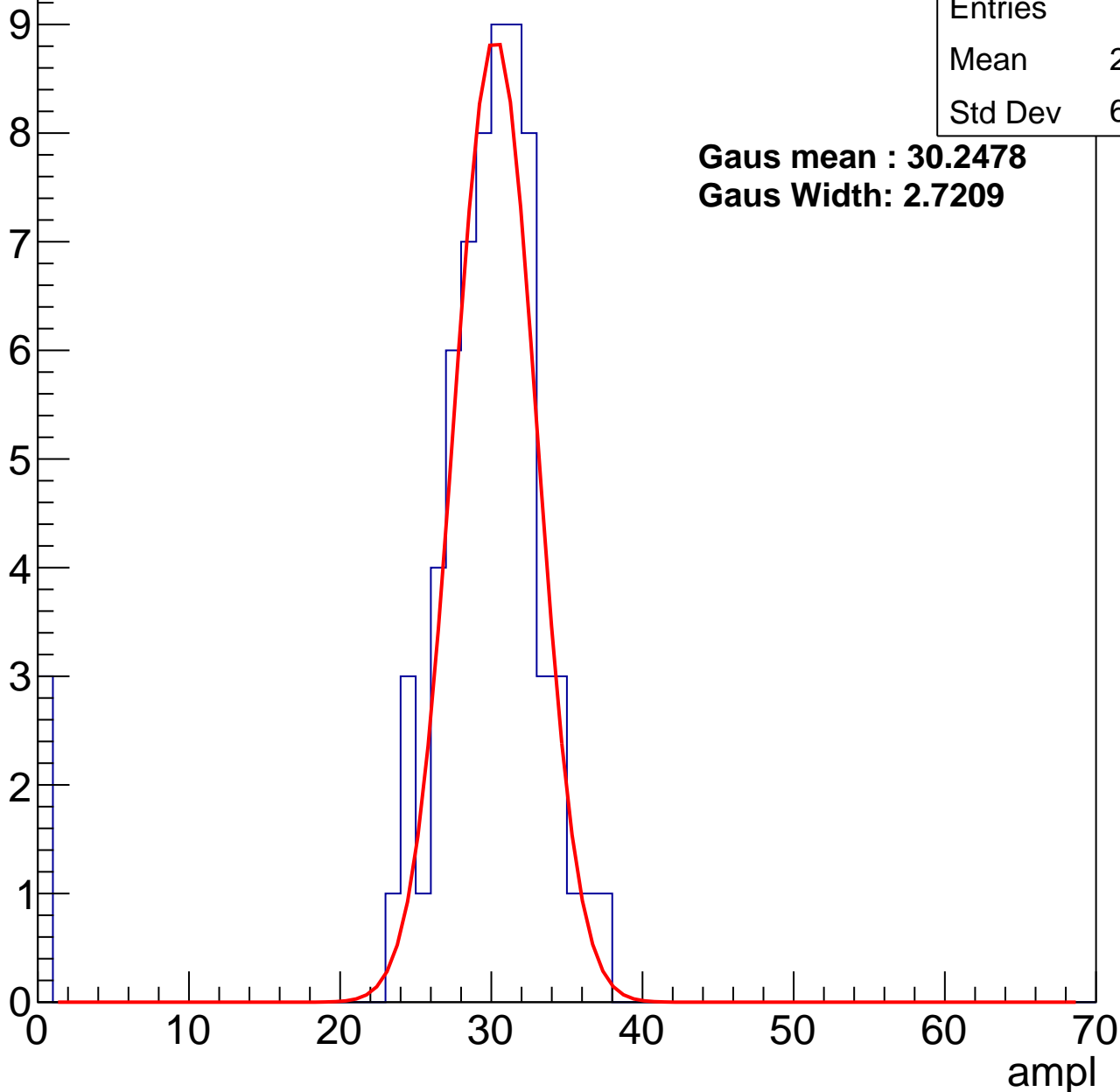
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.35
Std Dev	6.727

Gaus mean : 30.2478

Gaus Width: 2.7209



B1L103S, U23-ch12, adc1

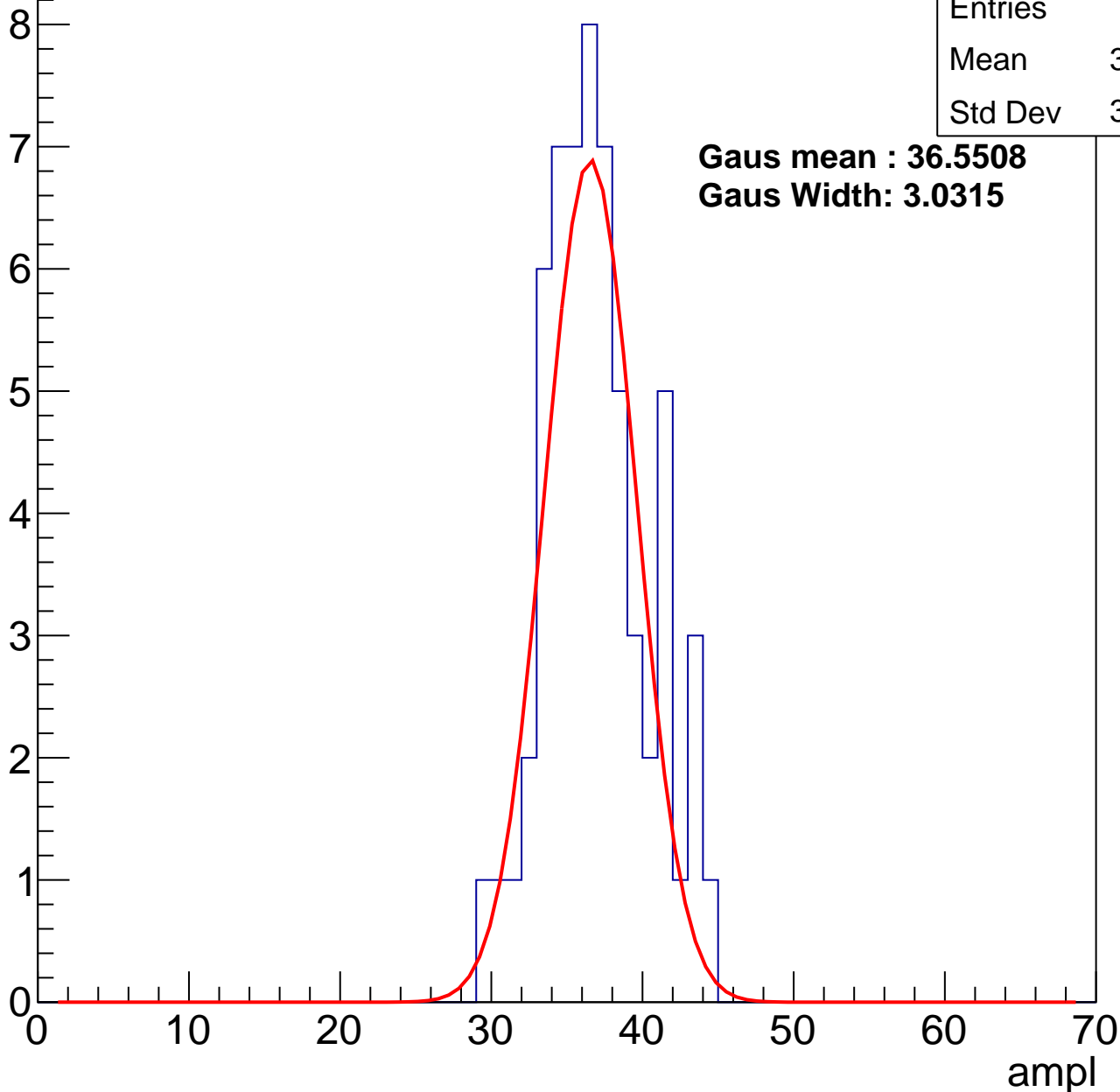
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.48
Std Dev	3.354

Gaus mean : 36.5508

Gaus Width: 3.0315



B1L103S, U23-ch12, adc2

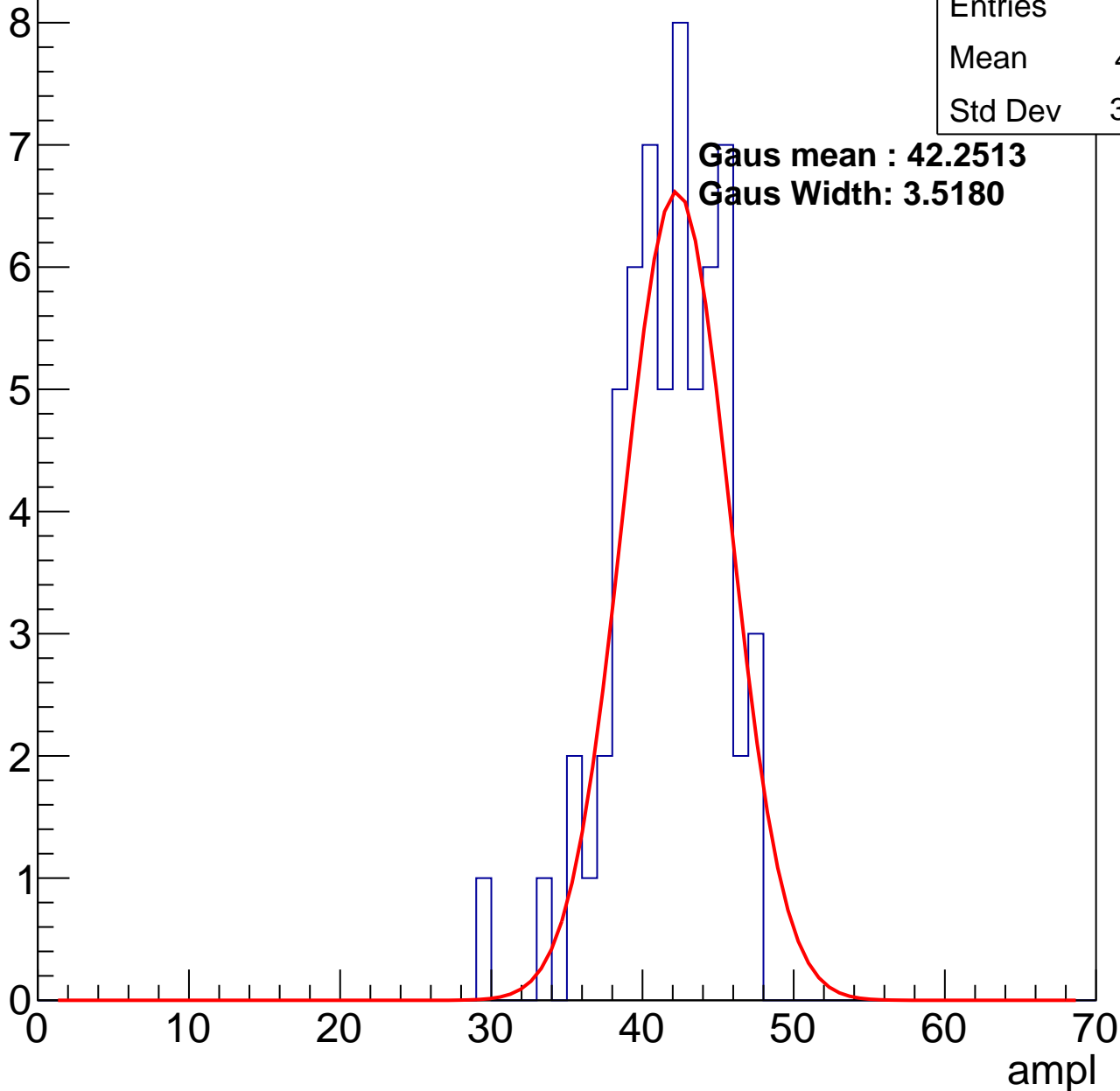
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.21
Std Dev	3.535

Gaus mean : 42.2513

Gaus Width: 3.5180

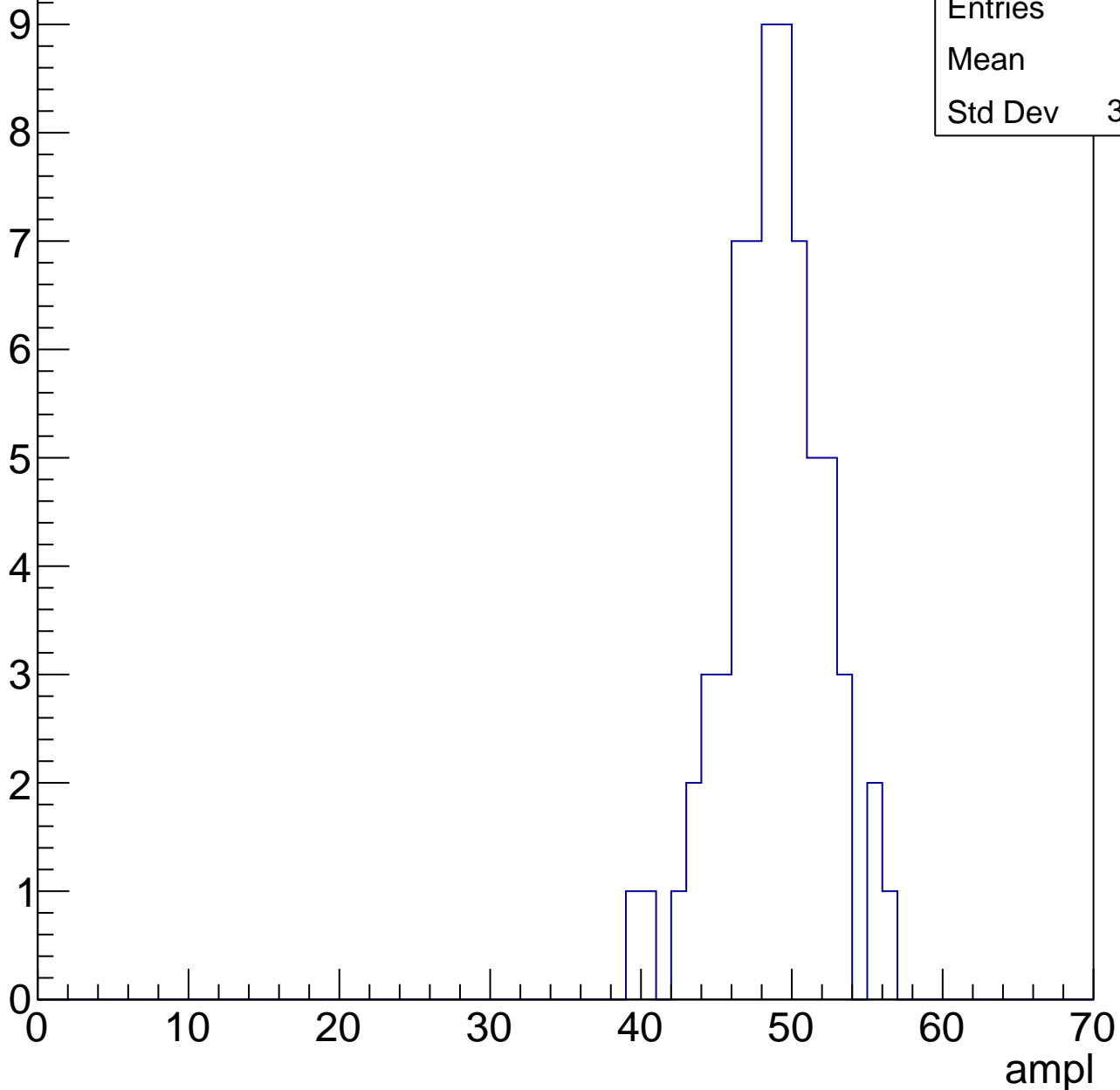


B1L103S, U23-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

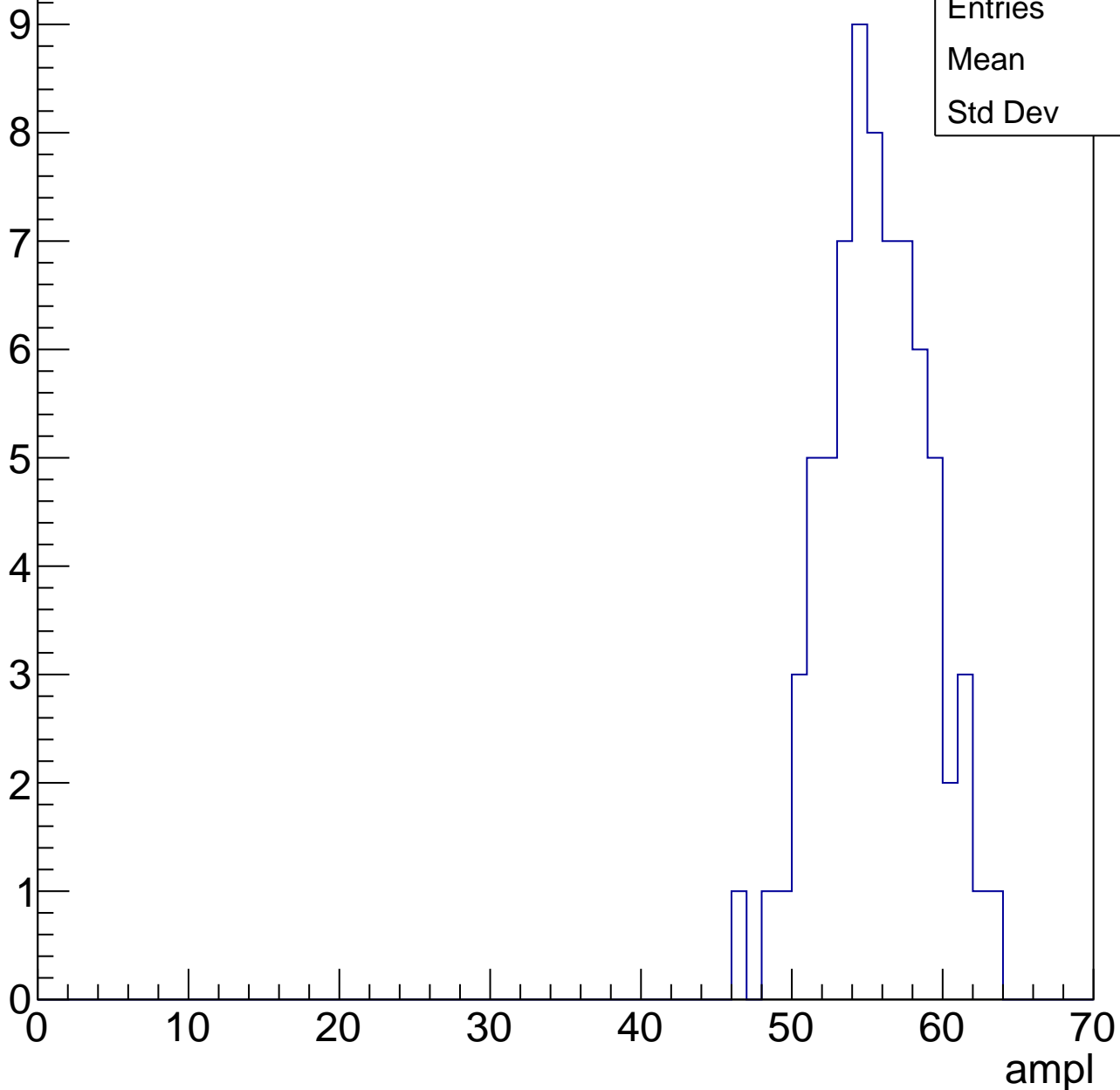
Entries	66
Mean	48.3
Std Dev	3.335



B1L103S, U23-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

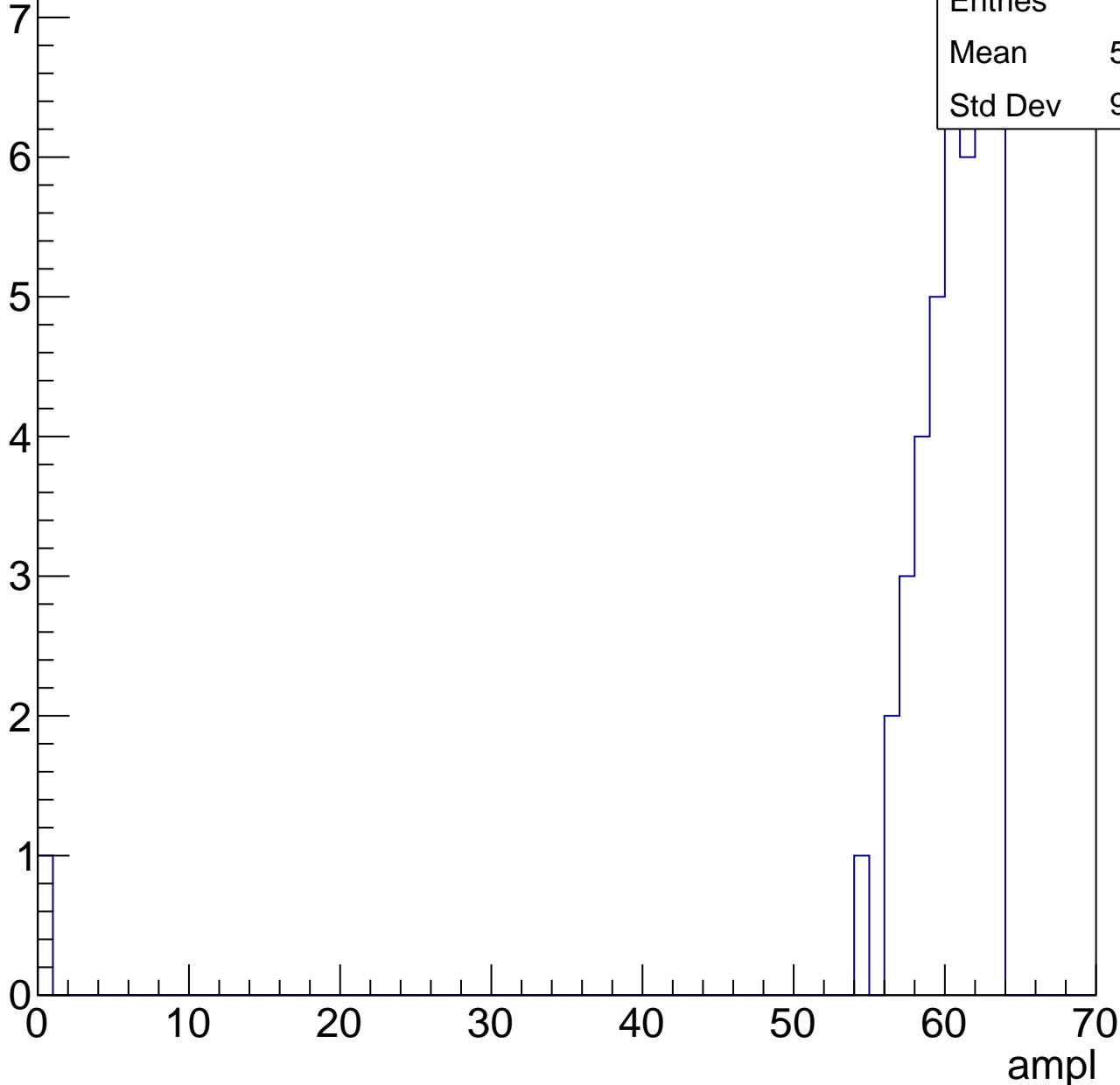


B1L103S, U23-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.72
Std Dev	9.329



B1L103S, U23-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch13, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	30.96
Std Dev	3.387

Gaus mean : 31.8250

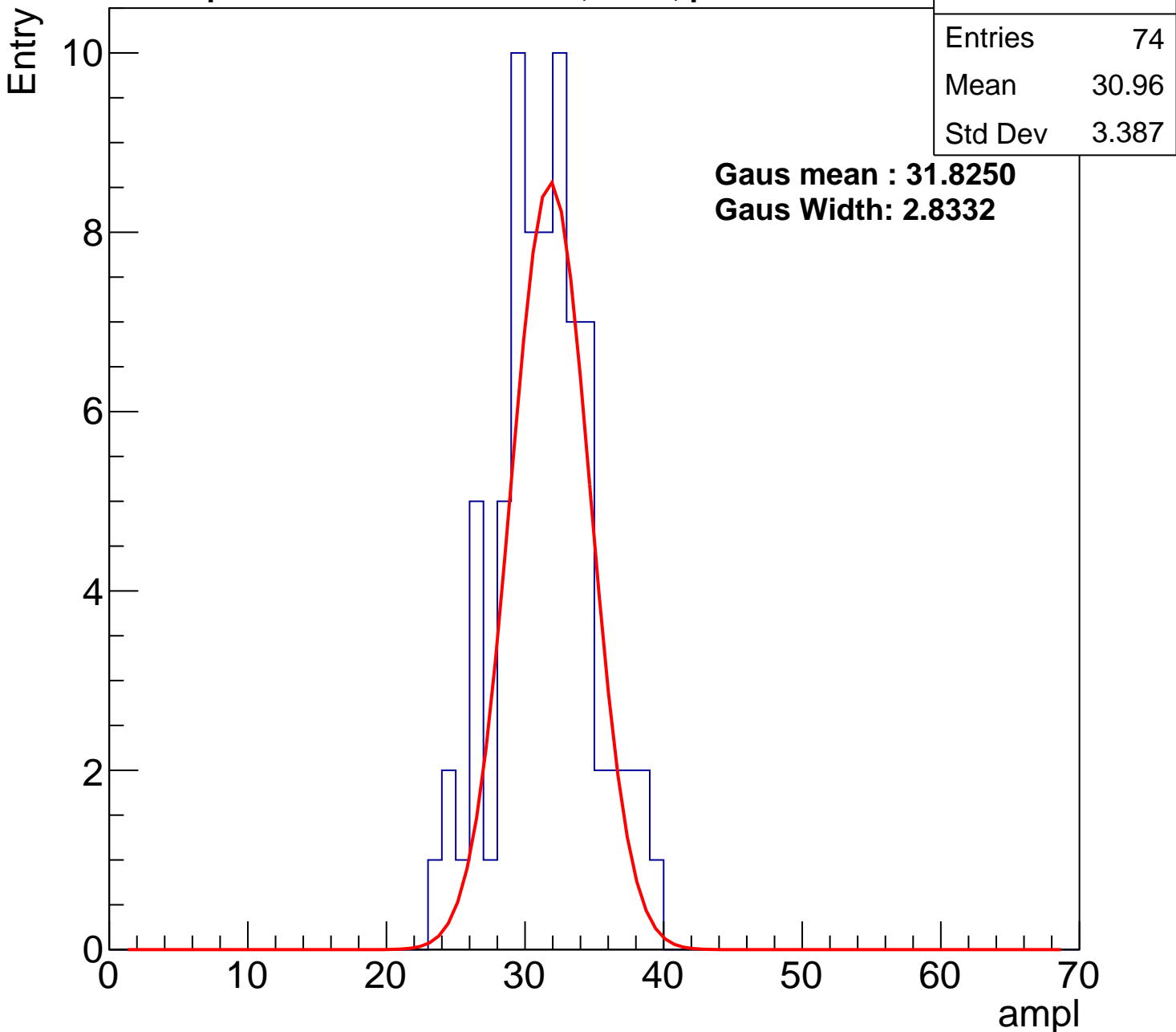
Gaus Width: 2.8332

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch13, adc1

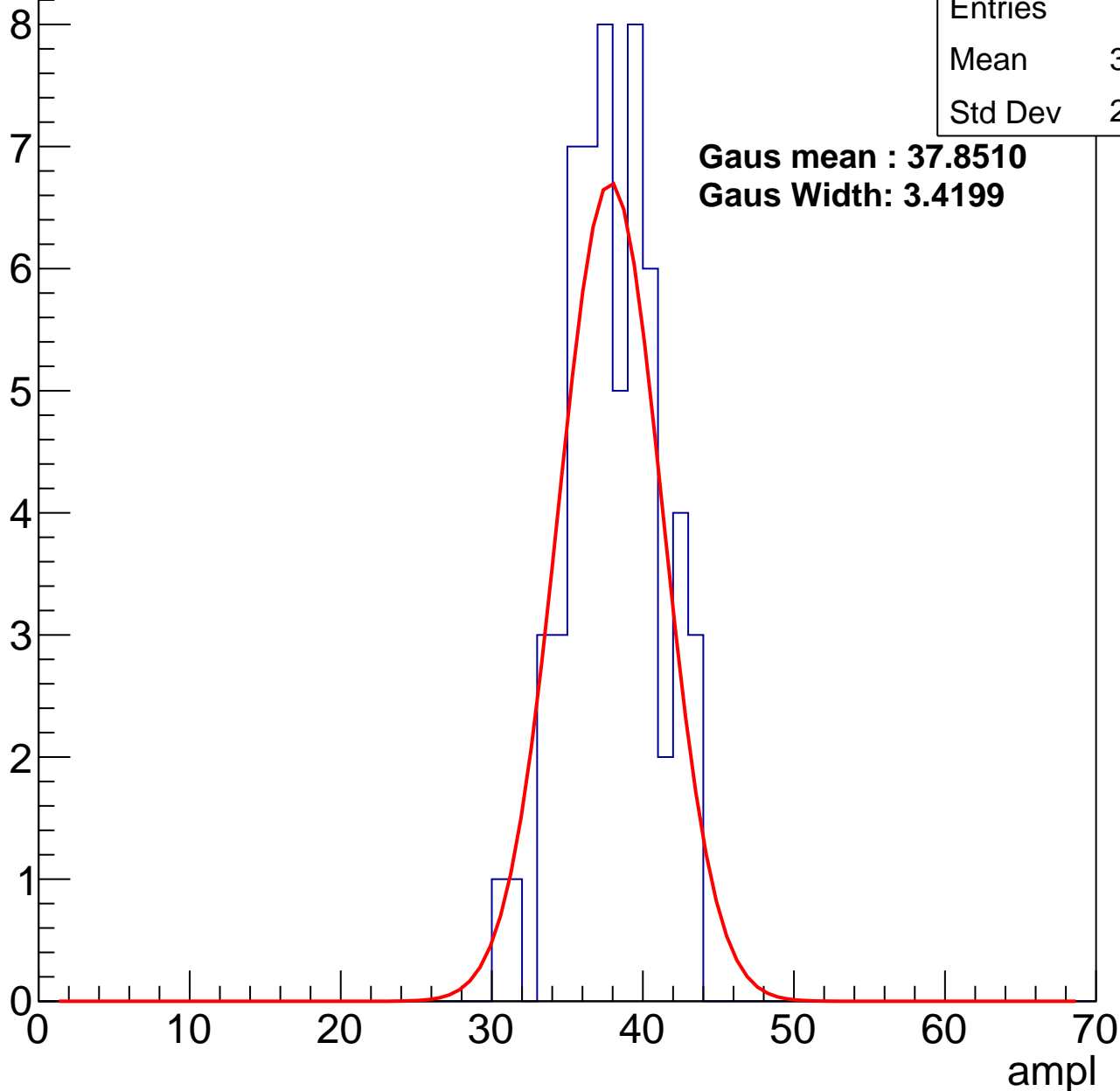
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	37.52
Std Dev	2.967

Gaus mean : 37.8510

Gaus Width: 3.4199



B1L103S, U23-ch13, adc2

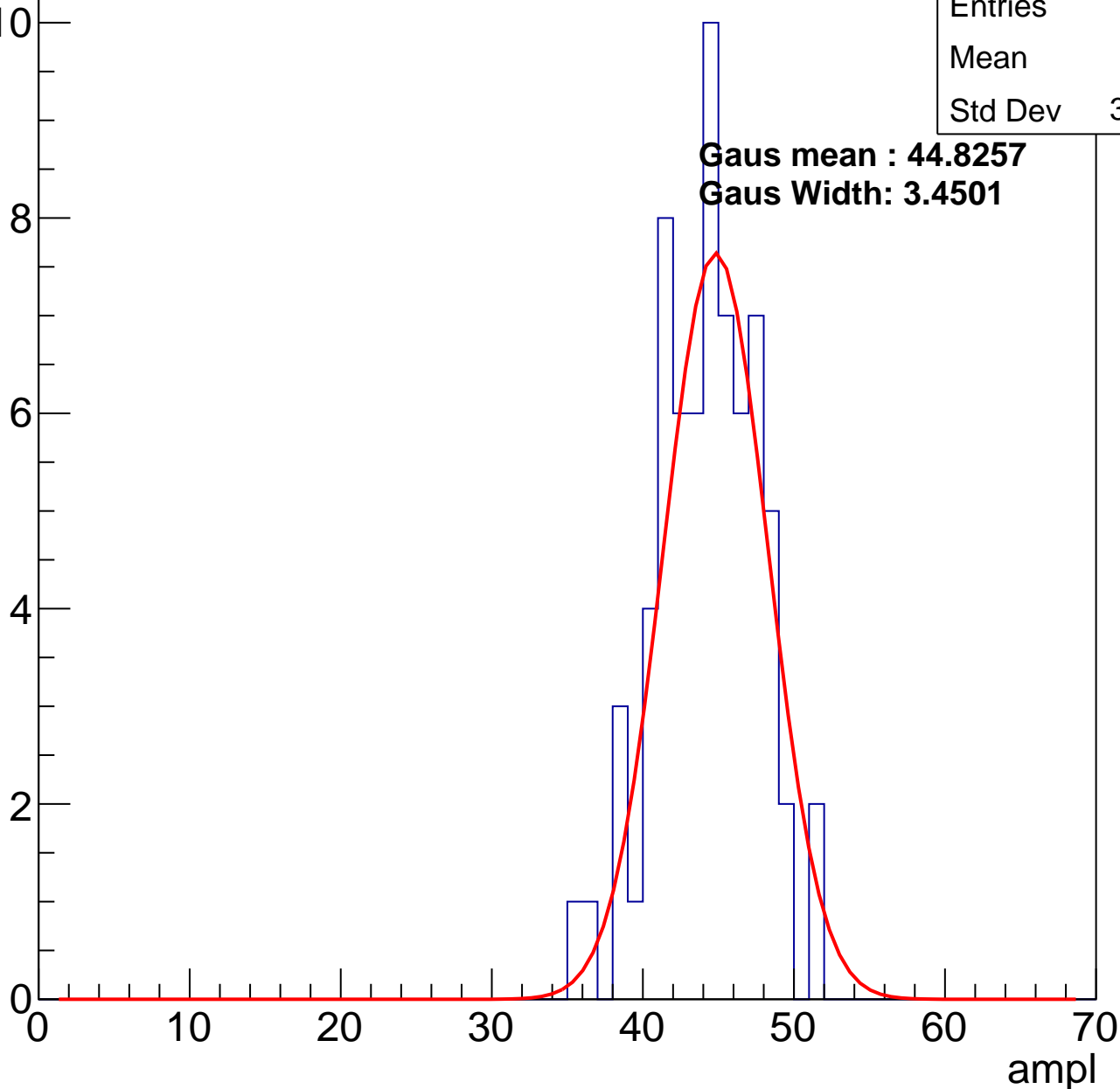
calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	43.8
Std Dev	3.326

Gaus mean : 44.8257

Gaus Width: 3.4501

Entry

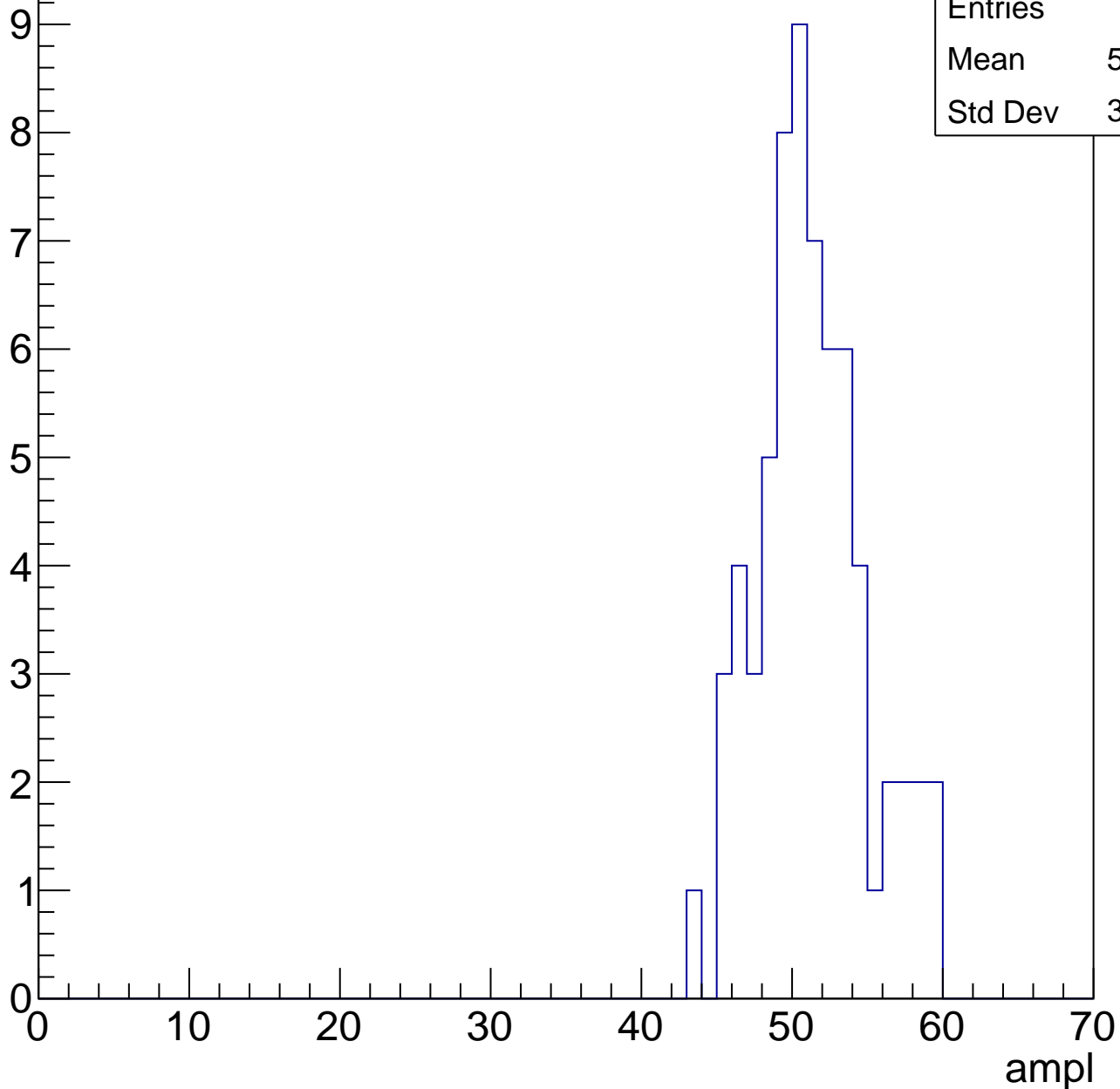


B1L103S, U23-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

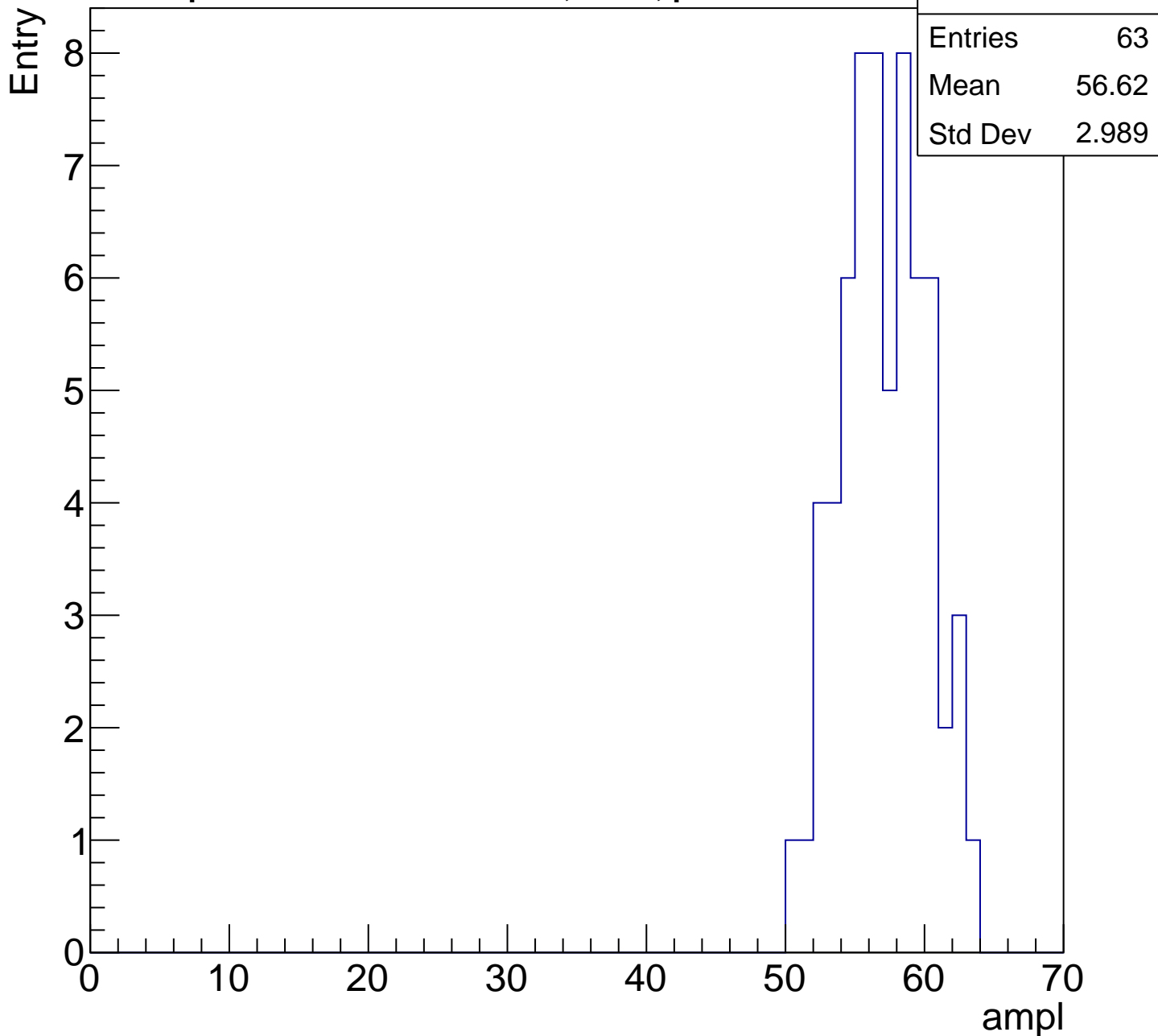
Entry

Entries	65
Mean	50.82
Std Dev	3.582



B1L103S, U23-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

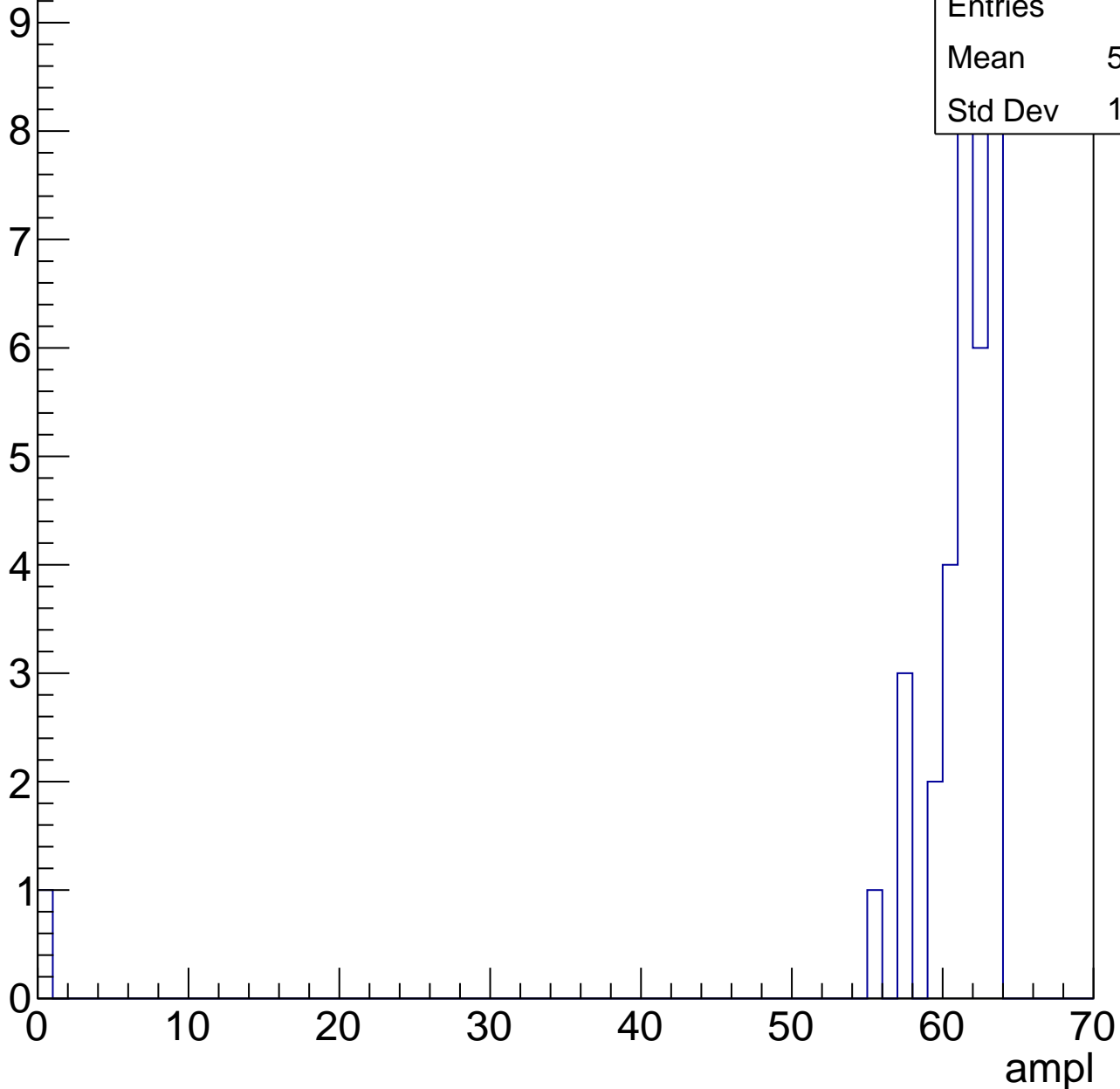


B1L103S, U23-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	59.15
Std Dev	10.49



B1L103S, U23-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	62
Std Dev	0

B1L103S, U23-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch14, adc0

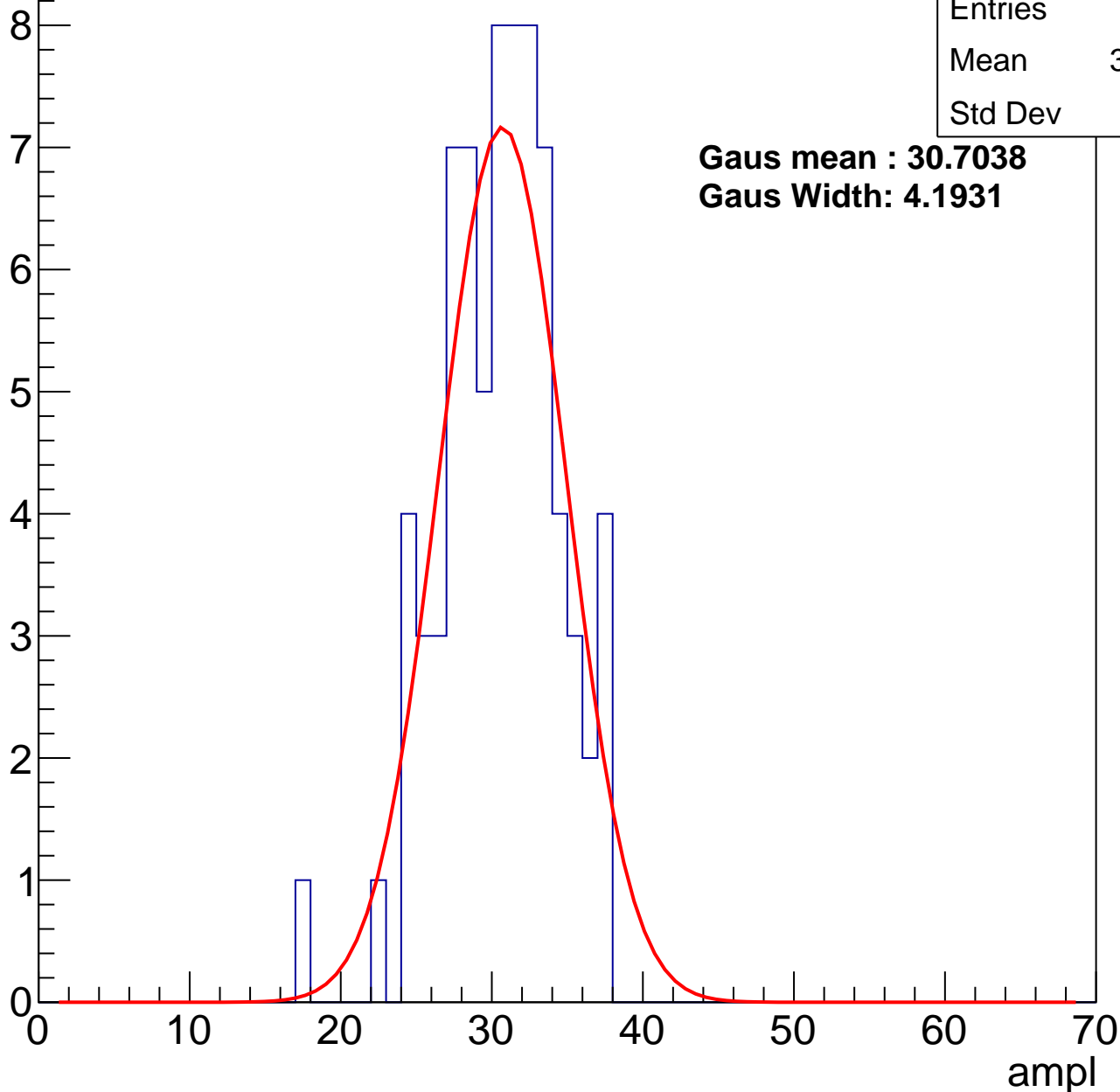
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	30.05
Std Dev	3.84

Gaus mean : 30.7038

Gaus Width: 4.1931



B1L103S, U23-ch14, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	37.14
Std Dev	5.56

Gaus mean : 37.9765

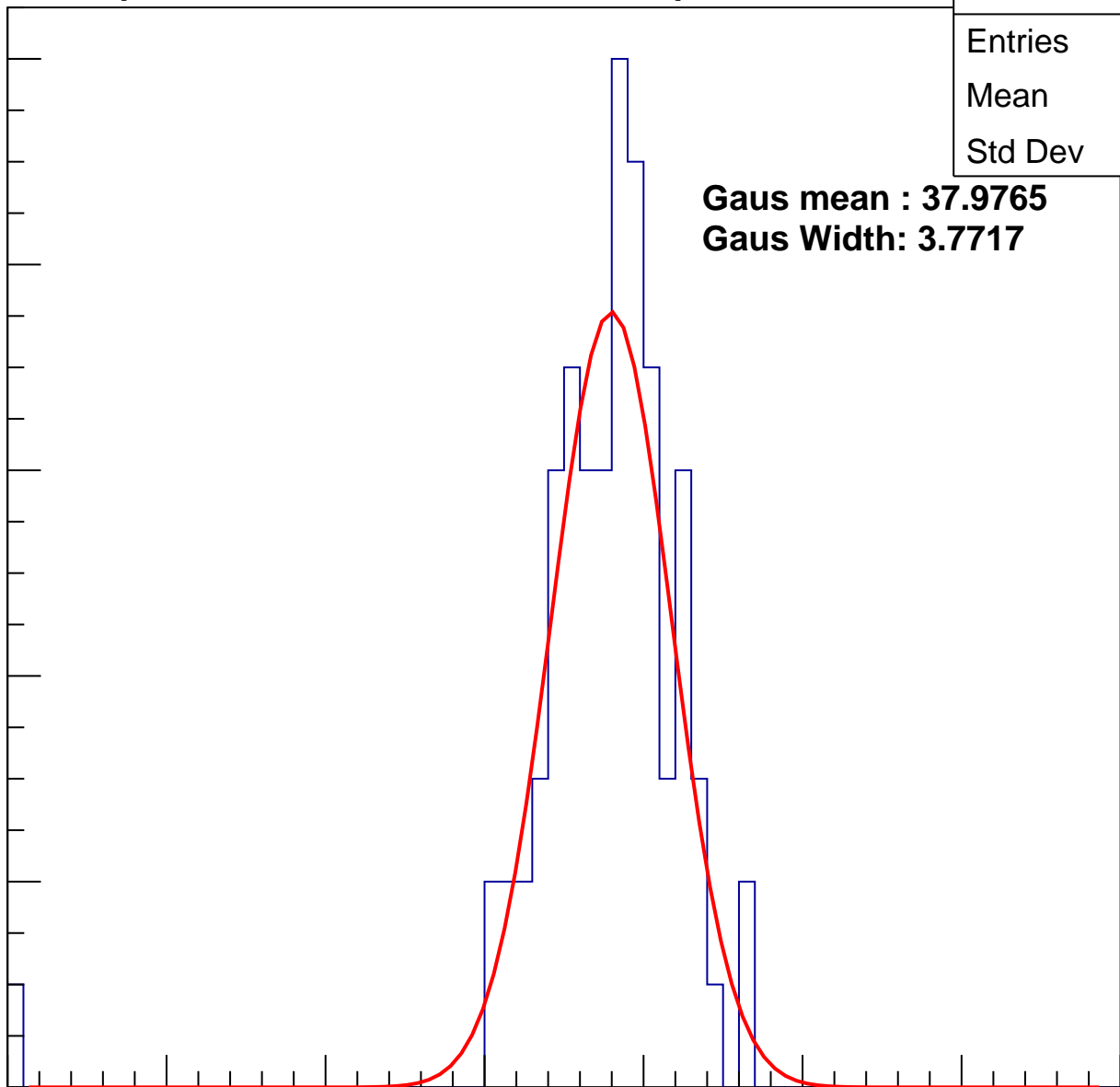
Gaus Width: 3.7717

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



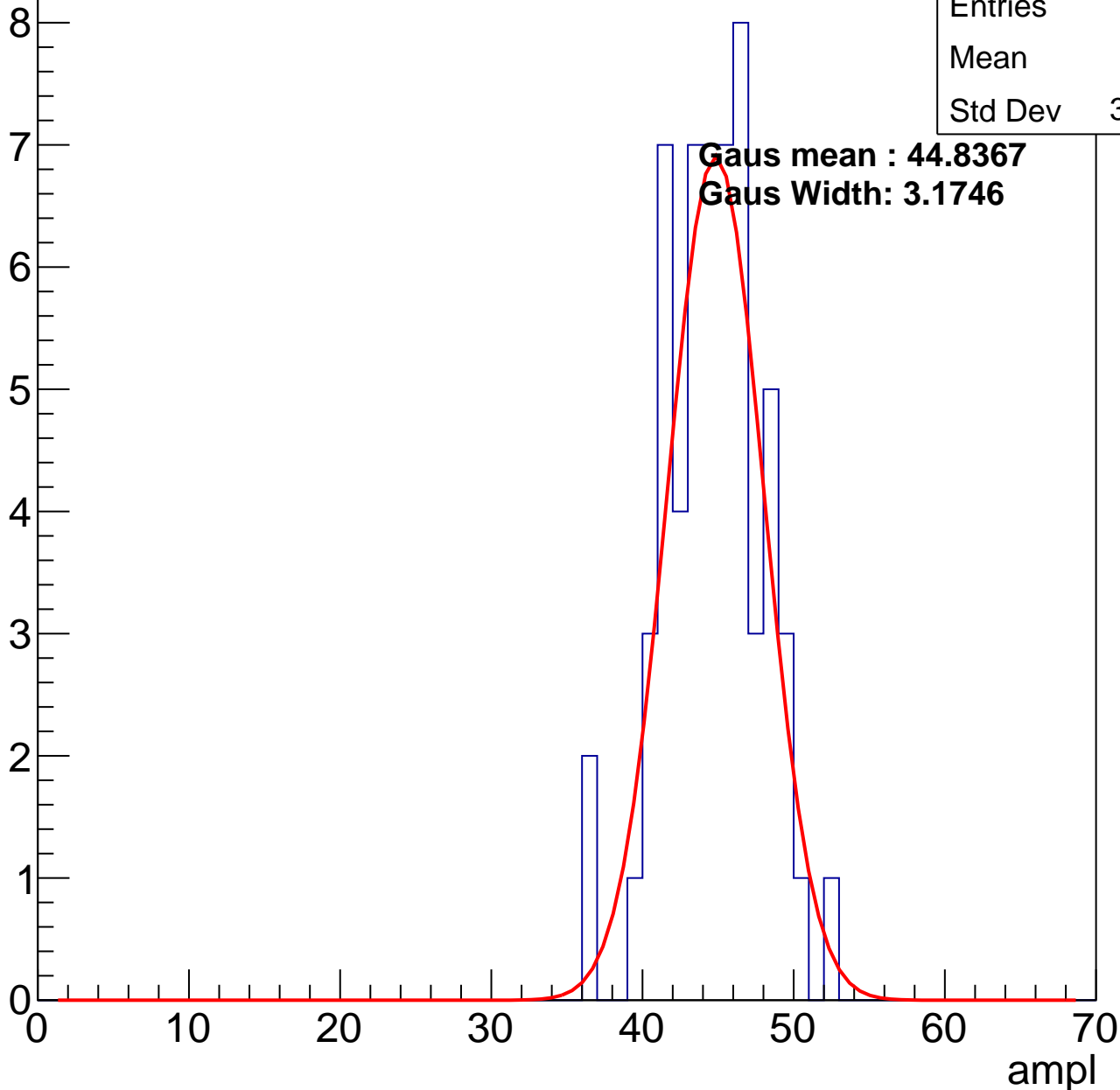
B1L103S, U23-ch14, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	44.2
Std Dev	3.204

Gaus mean : 44.8367
Gaus Width: 3.1746

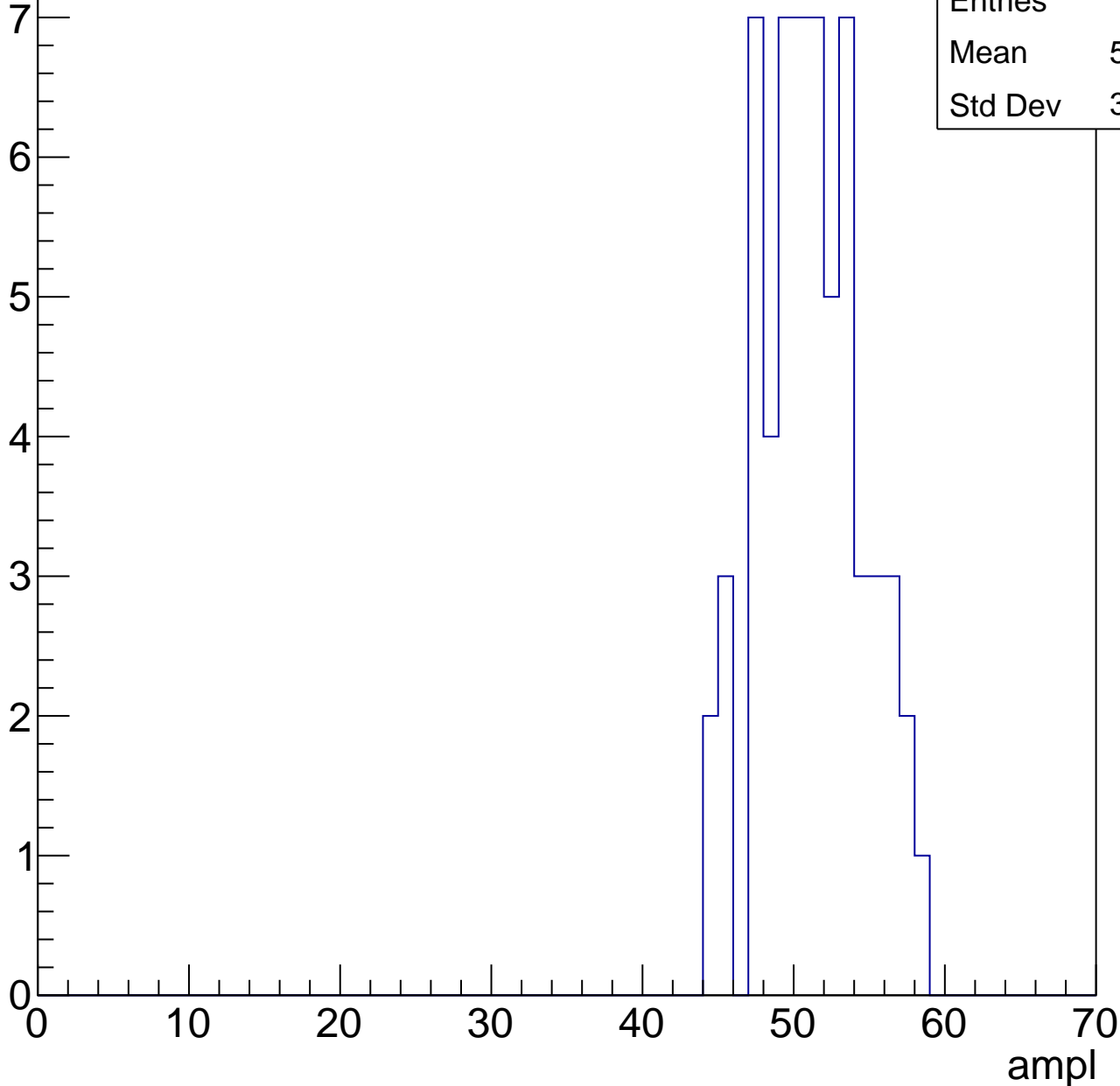


B1L103S, U23-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.69
Std Dev	3.356

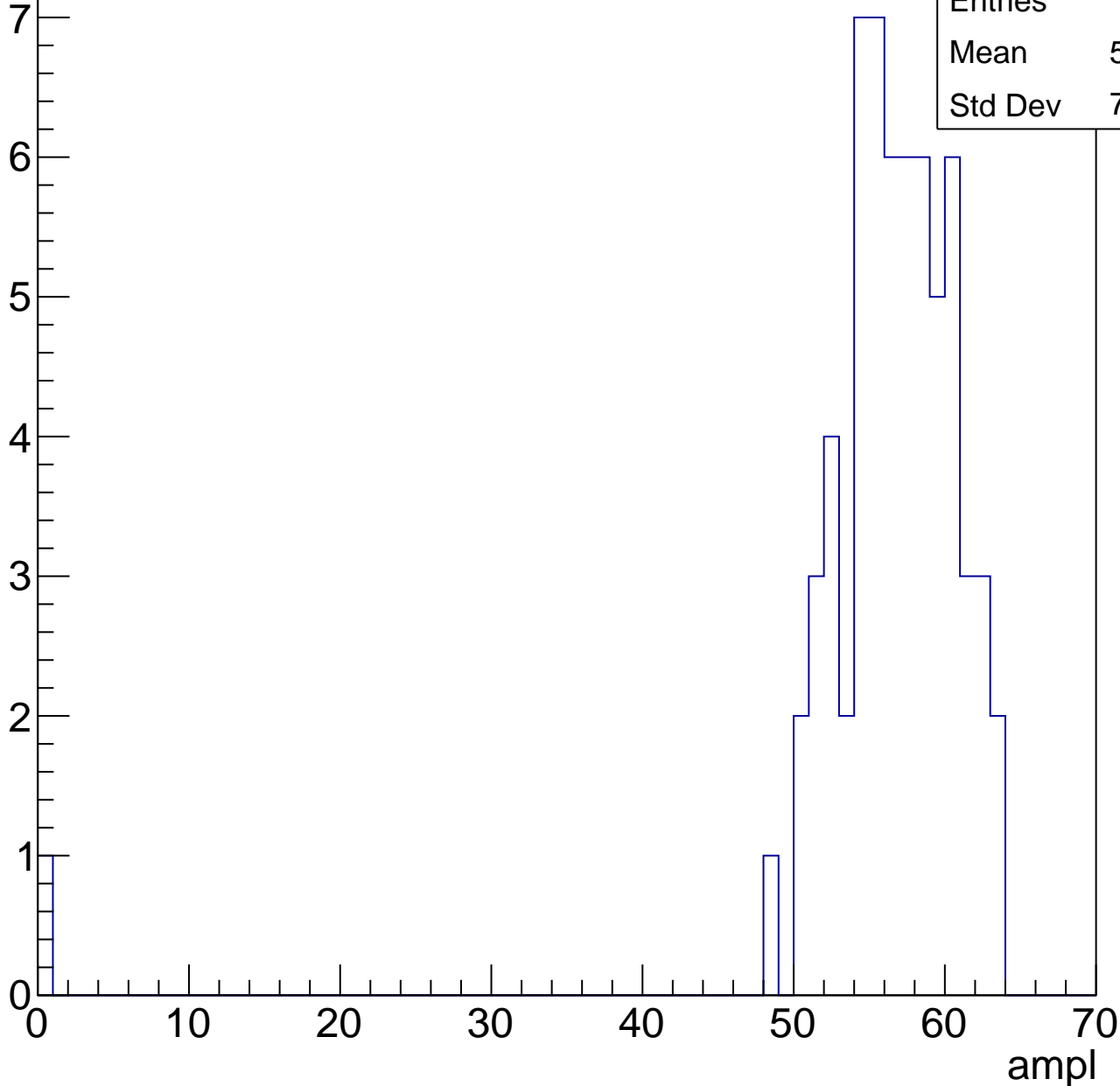


B1L103S, U23-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.53
Std Dev	7.808

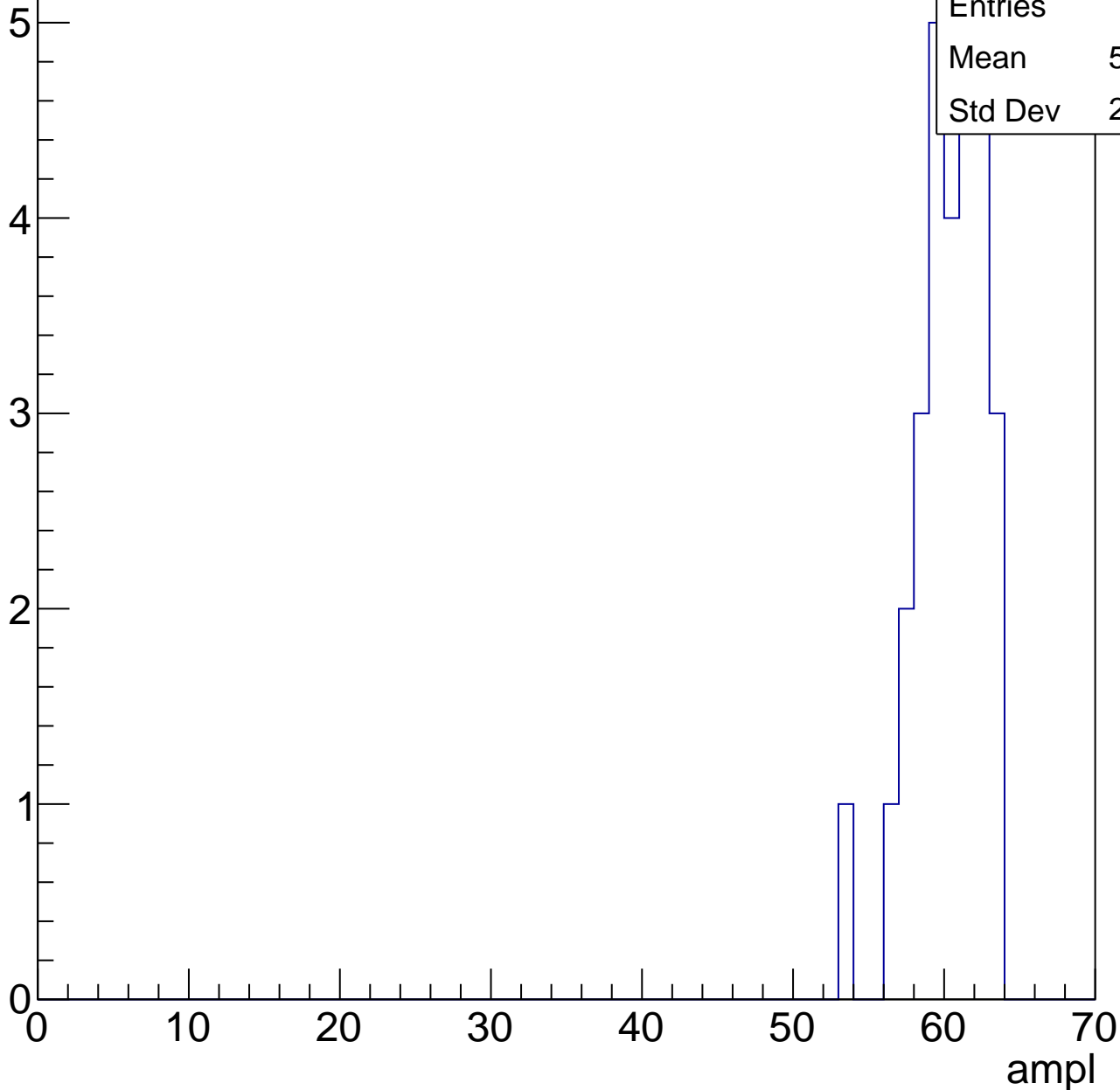


B1L103S, U23-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	59.86
Std Dev	2.285

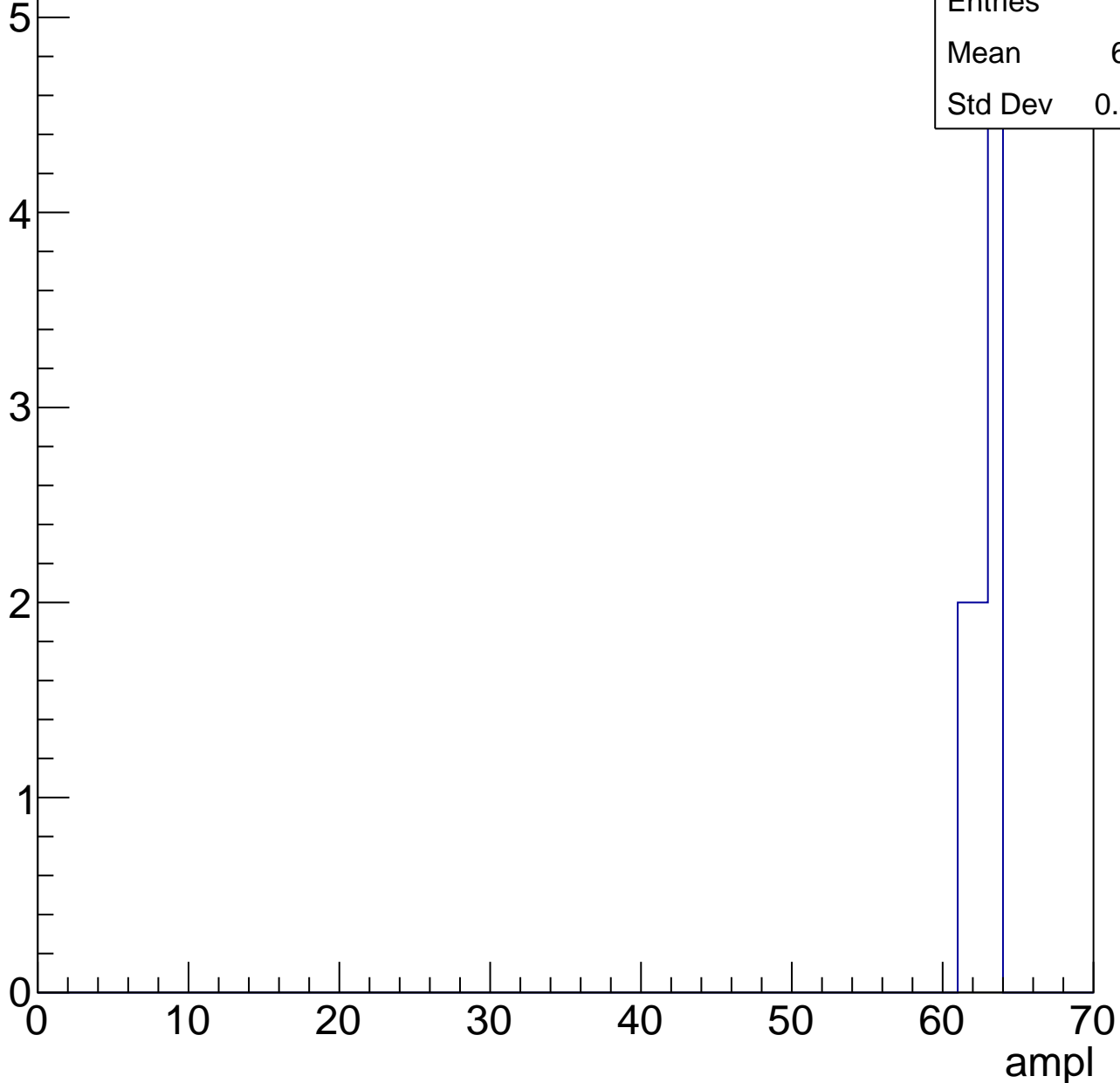


B1L103S, U23-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	62.33
Std Dev	0.8165



B1L103S, U23-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch15, adc0

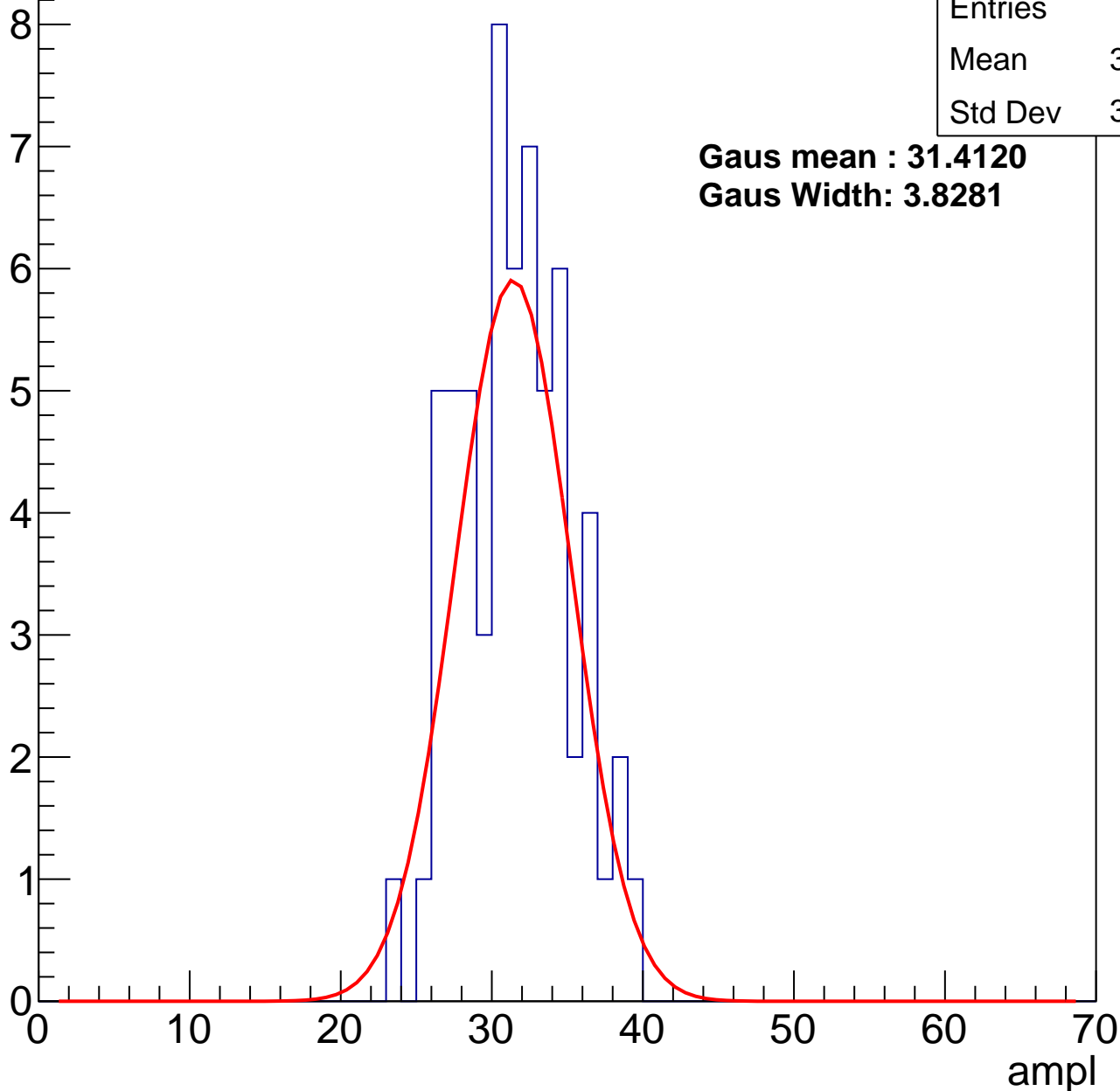
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	31.05
Std Dev	3.549

Gaus mean : 31.4120

Gaus Width: 3.8281



B1L103S, U23-ch15, adc1

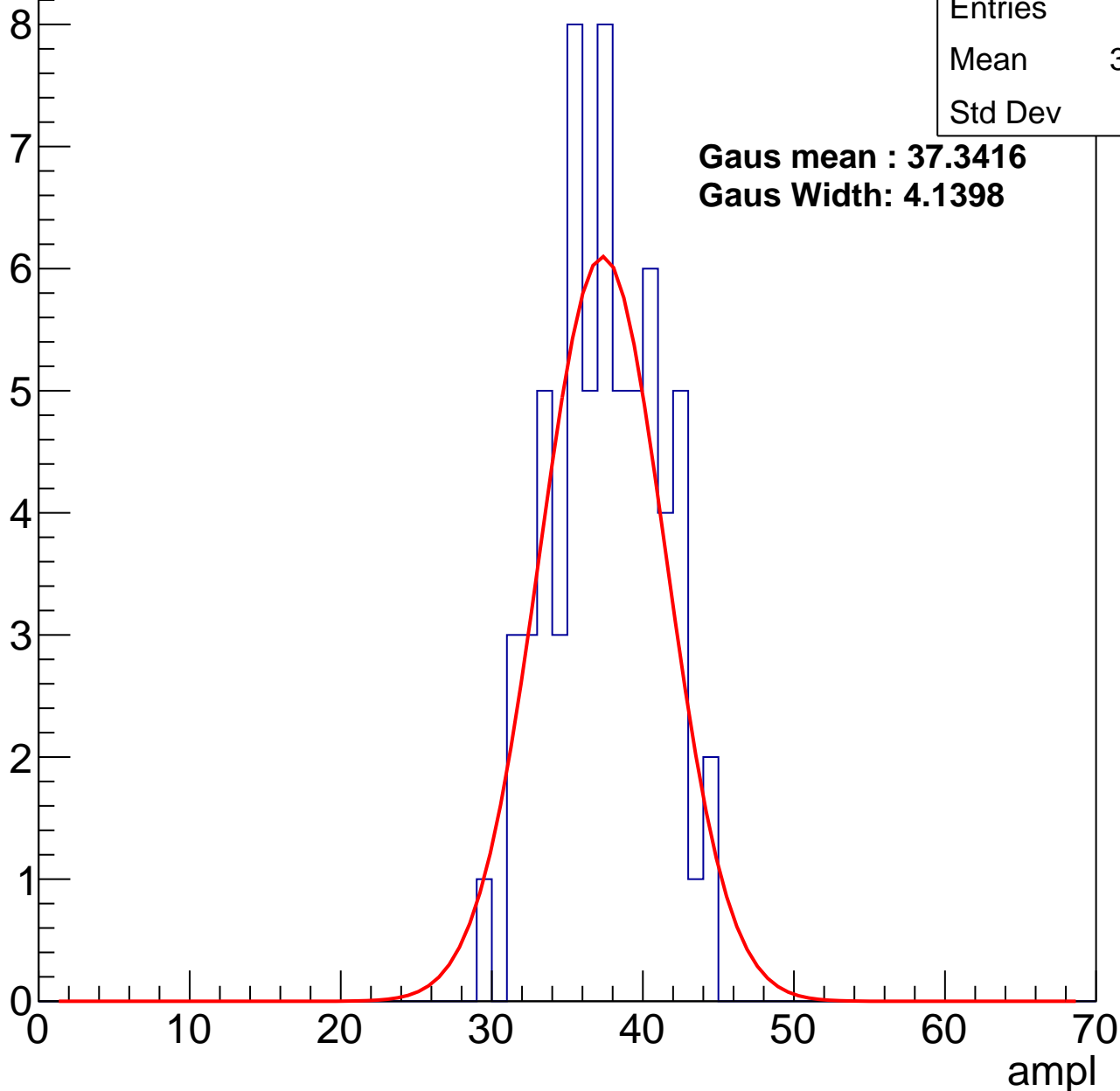
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	37.05
Std Dev	3.52

Gaus mean : 37.3416

Gaus Width: 4.1398



B1L103S, U23-ch15, adc2

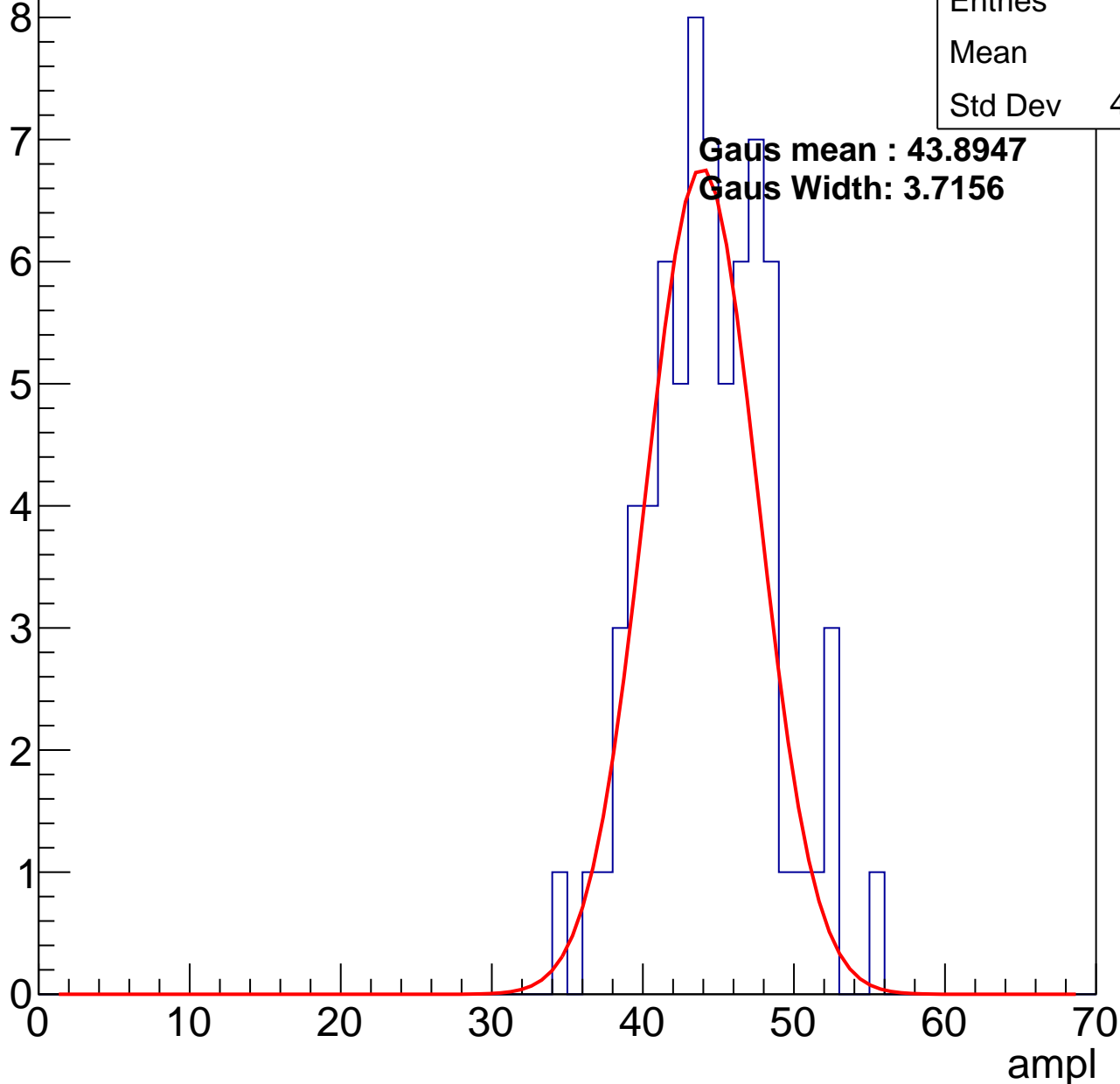
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	44
Std Dev	4.073

Gaus mean : 43.8947

Gaus Width: 3.7156

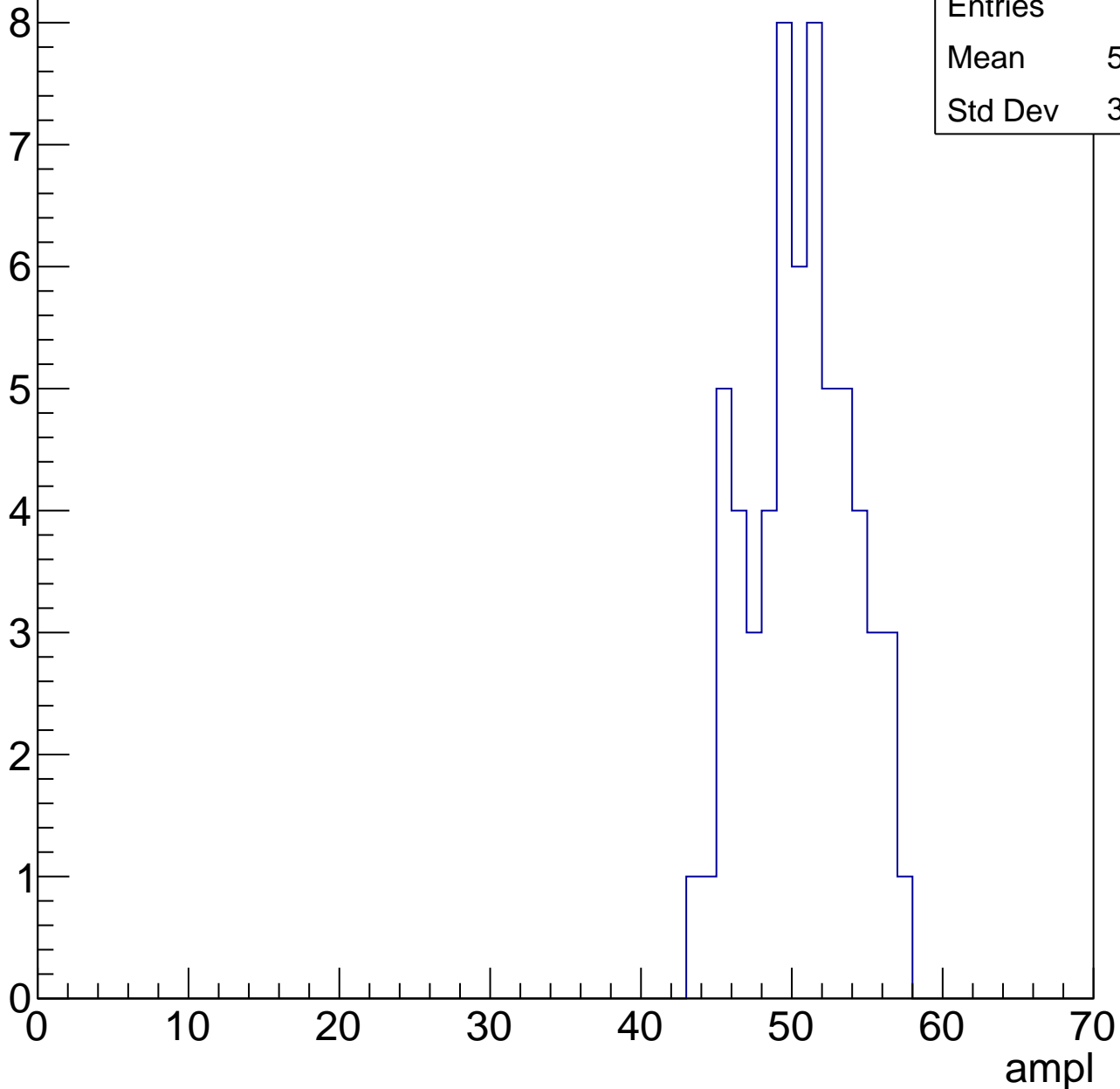


B1L103S, U23-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.16
Std Dev	3.369

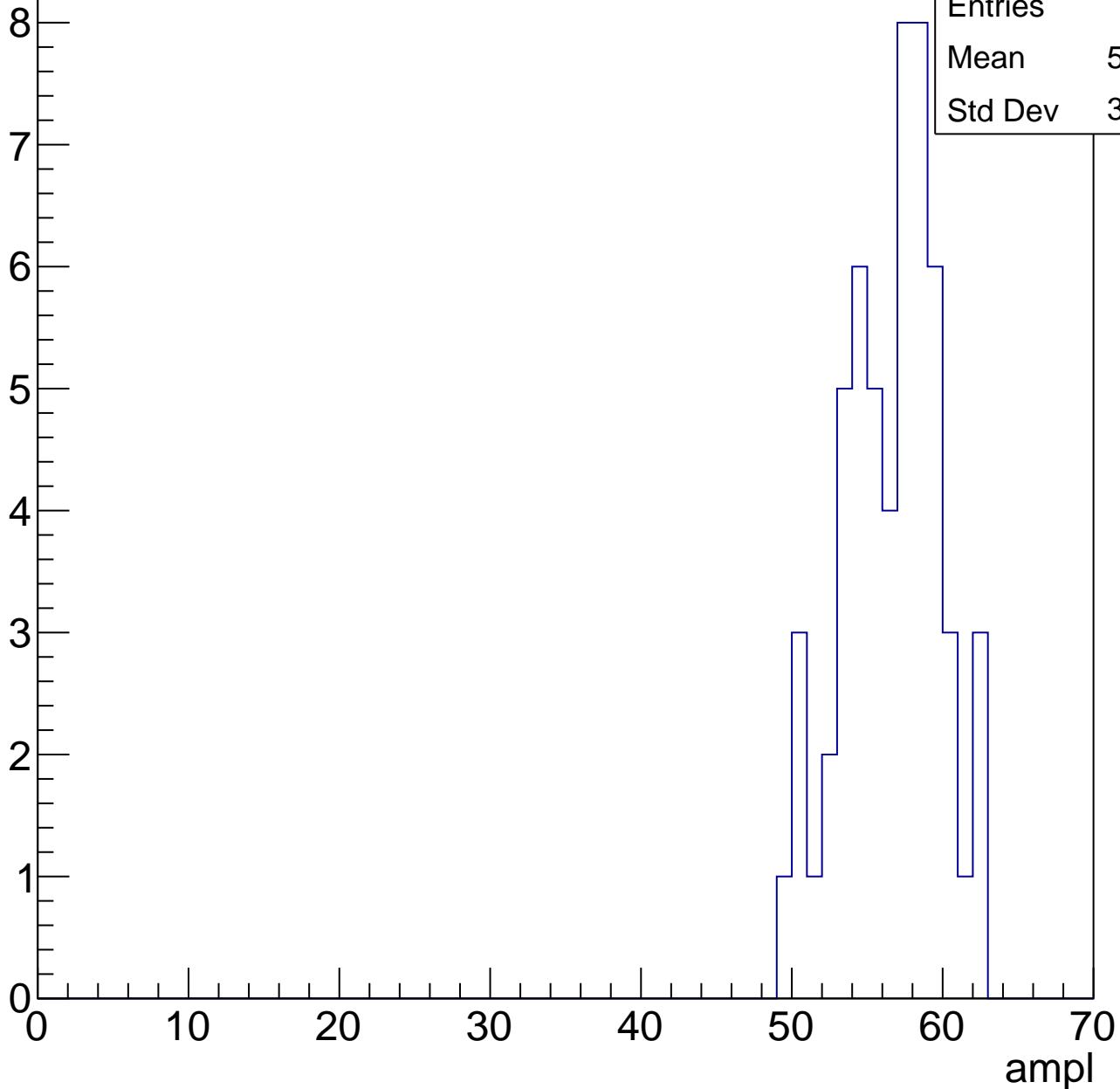


B1L103S, U23-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	56.12
Std Dev	3.168

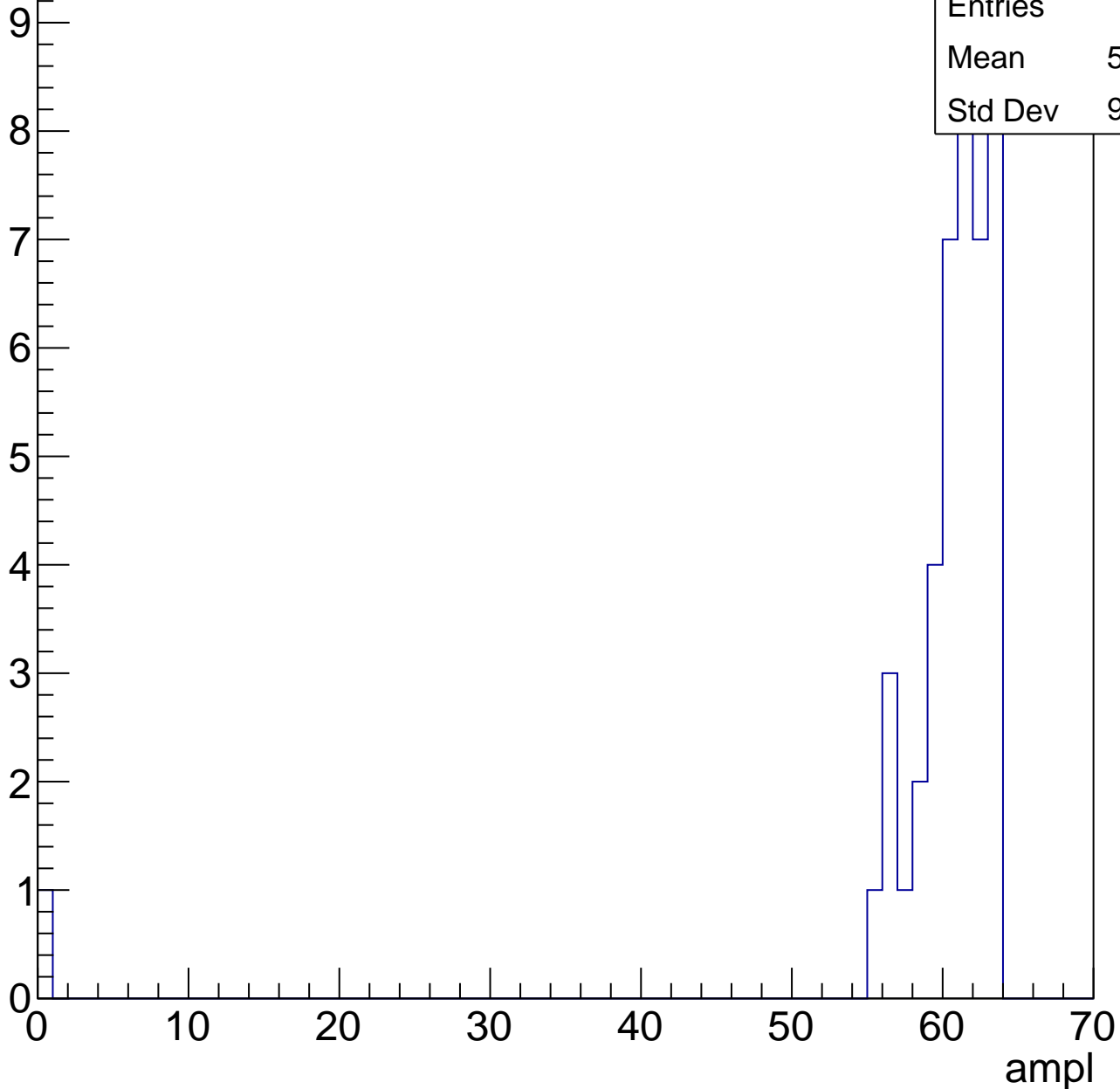


B1L103S, U23-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	59.14
Std Dev	9.268



B1L103S, U23-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch16, adc0

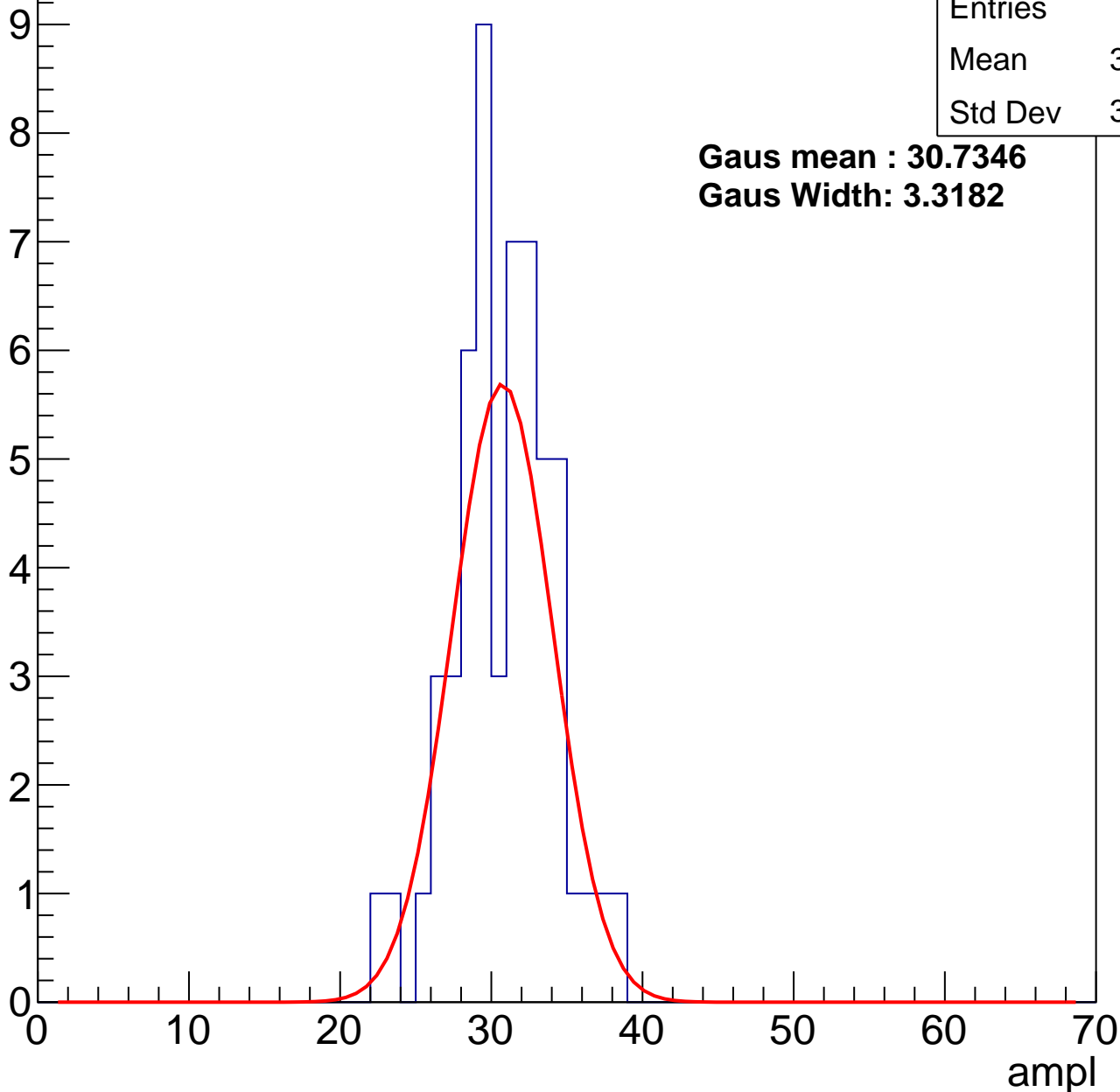
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	30.36
Std Dev	3.227

Gaus mean : 30.7346

Gaus Width: 3.3182



B1L103S, U23-ch16, adc1

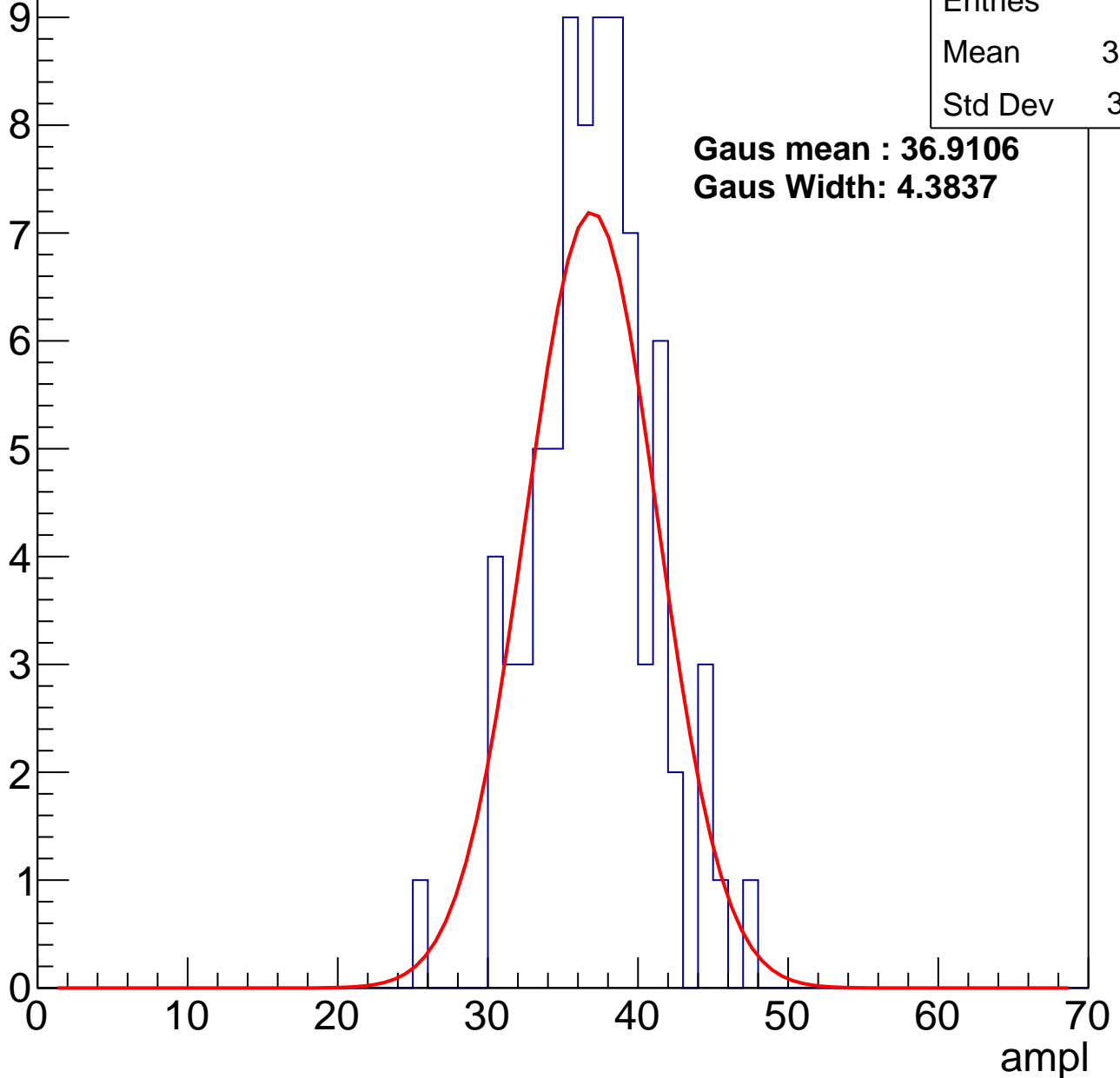
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	36.63
Std Dev	3.901

Gaus mean : 36.9106

Gaus Width: 4.3837



B1L103S, U23-ch16, adc2

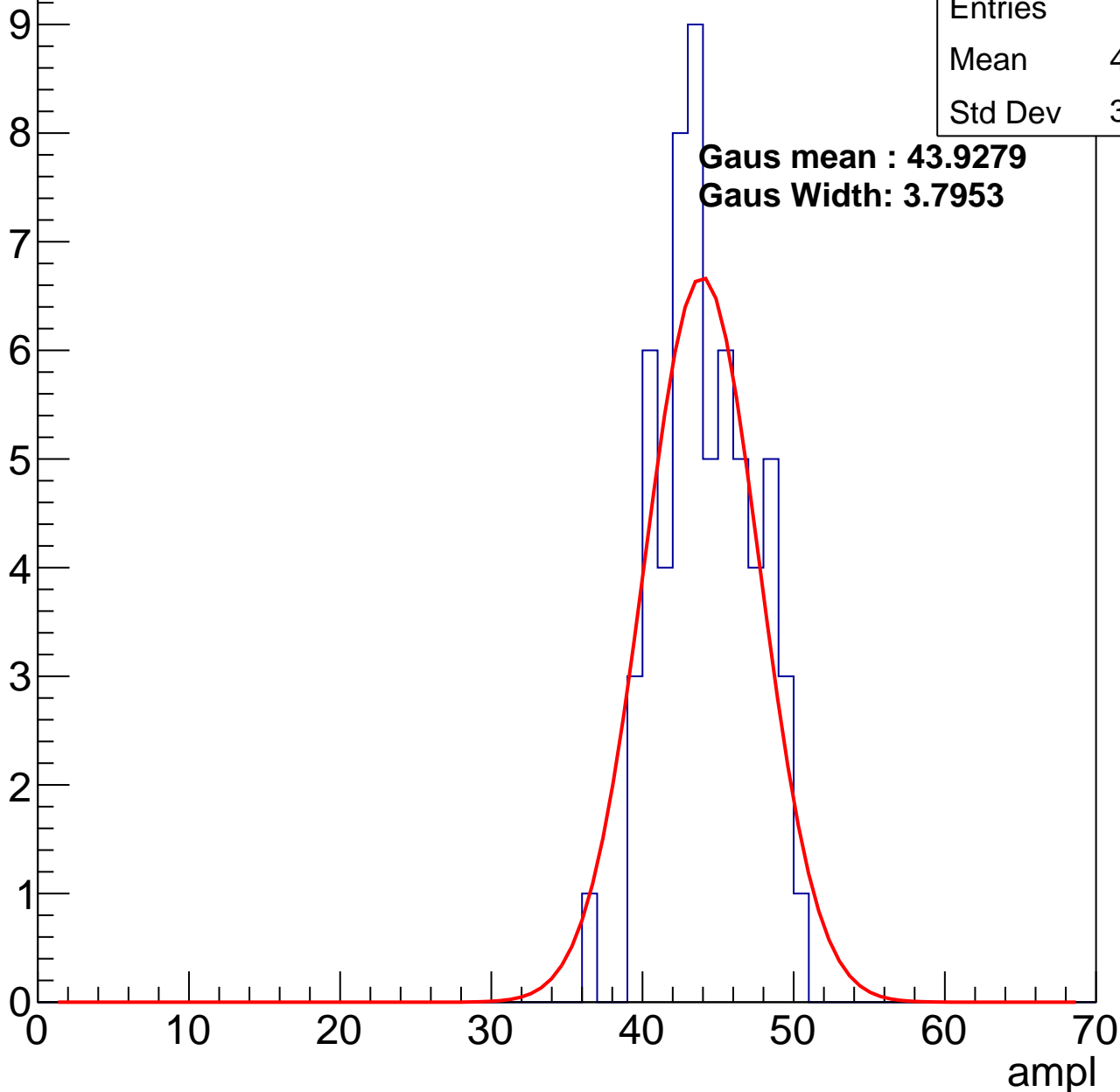
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.75
Std Dev	3.058

Gaus mean : 43.9279

Gaus Width: 3.7953

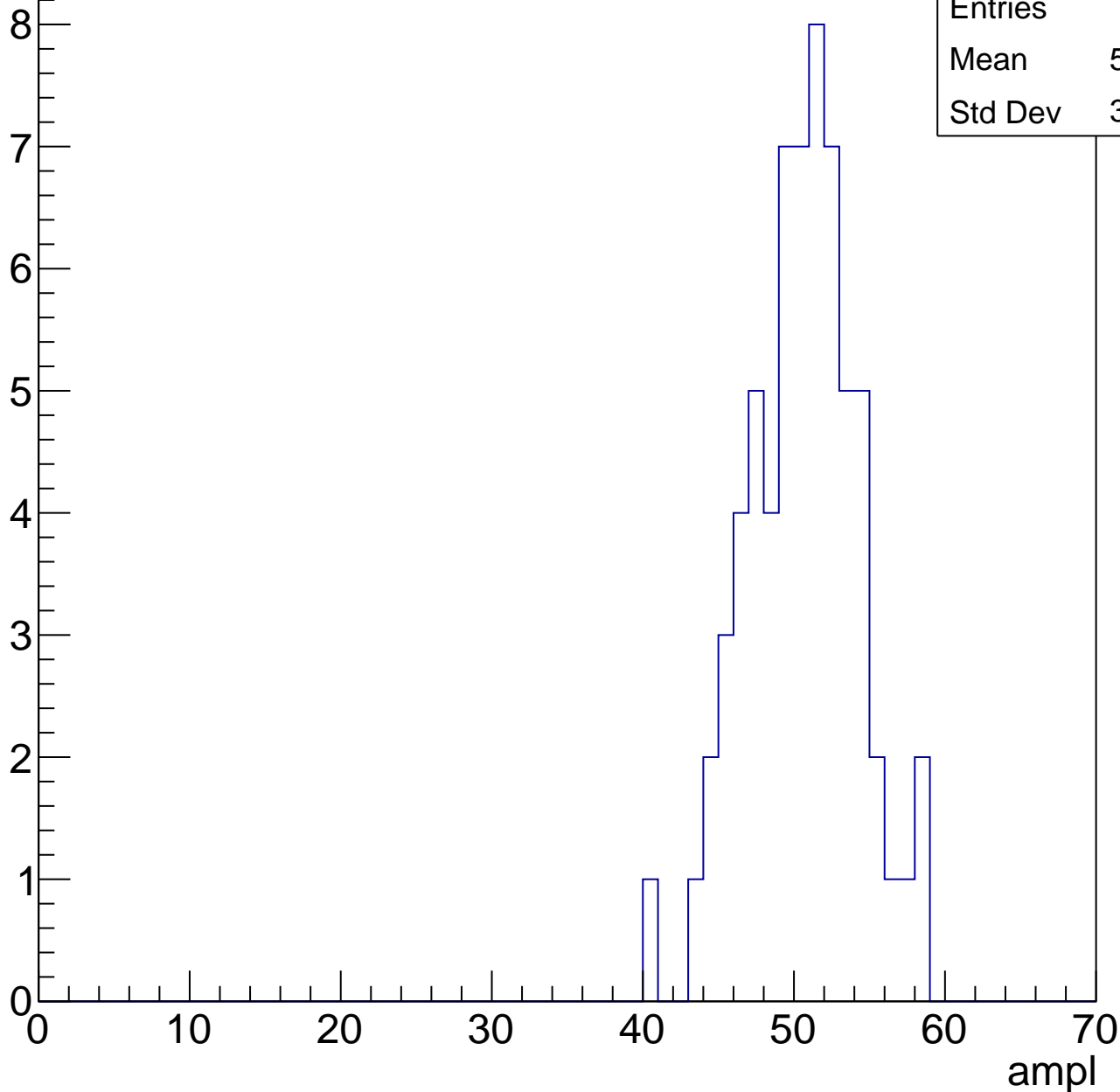


B1L103S, U23-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

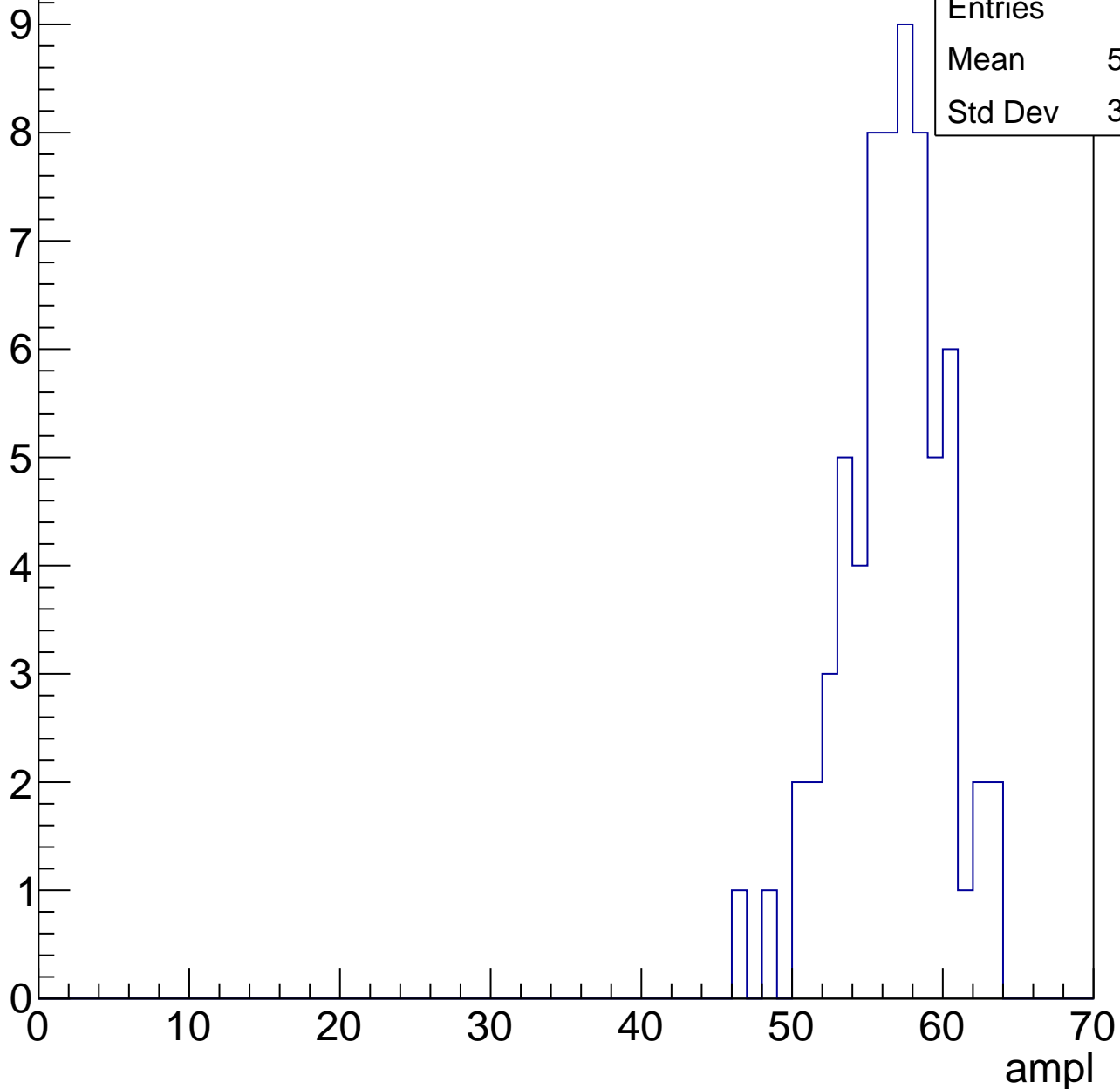
Entries	65
Mean	50.09
Std Dev	3.619



B1L103S, U23-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



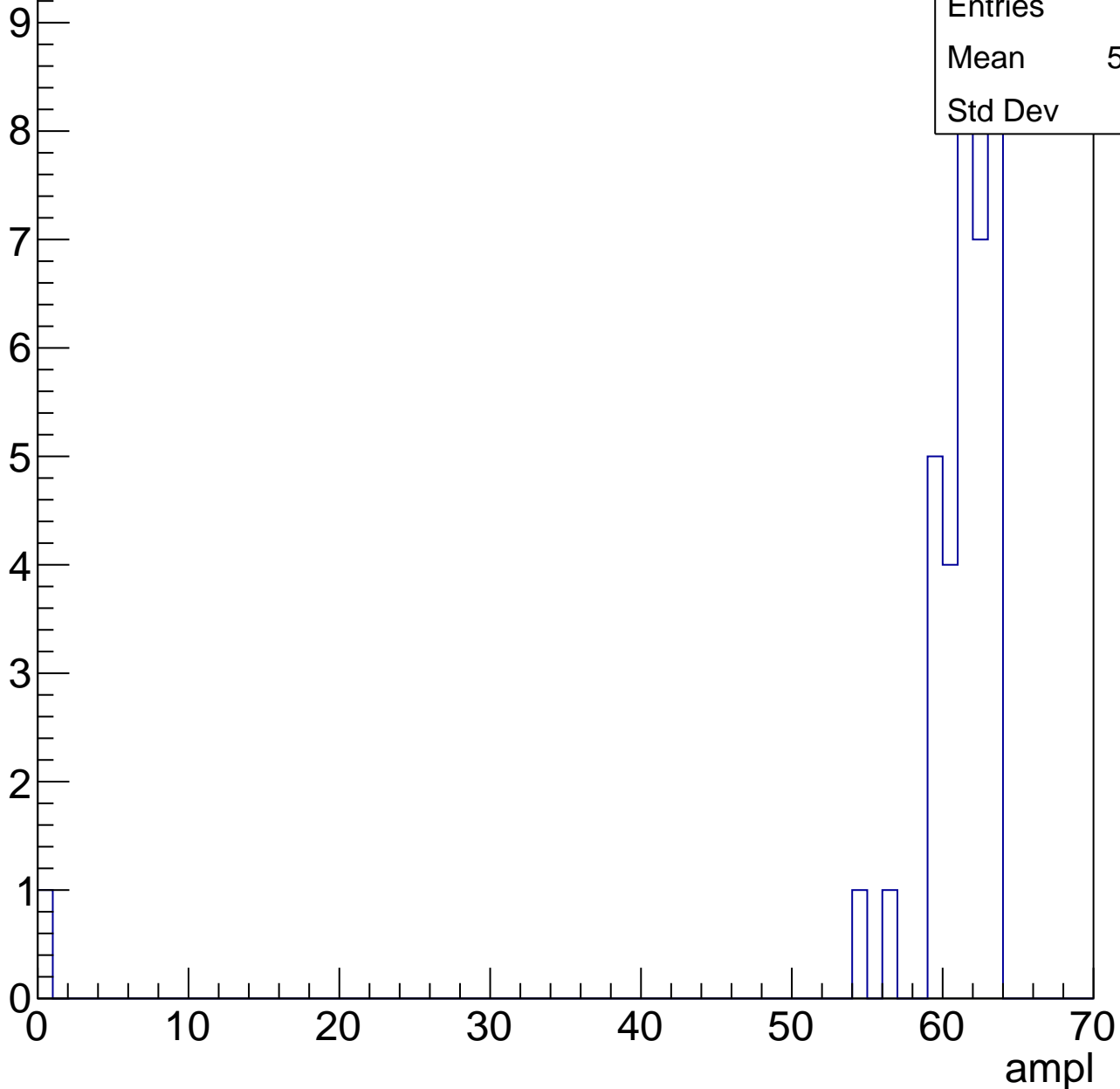
Entries	67
Mean	56.18
Std Dev	3.407

B1L103S, U23-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	59.22
Std Dev	10.2



B1L103S, U23-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch17, adc0

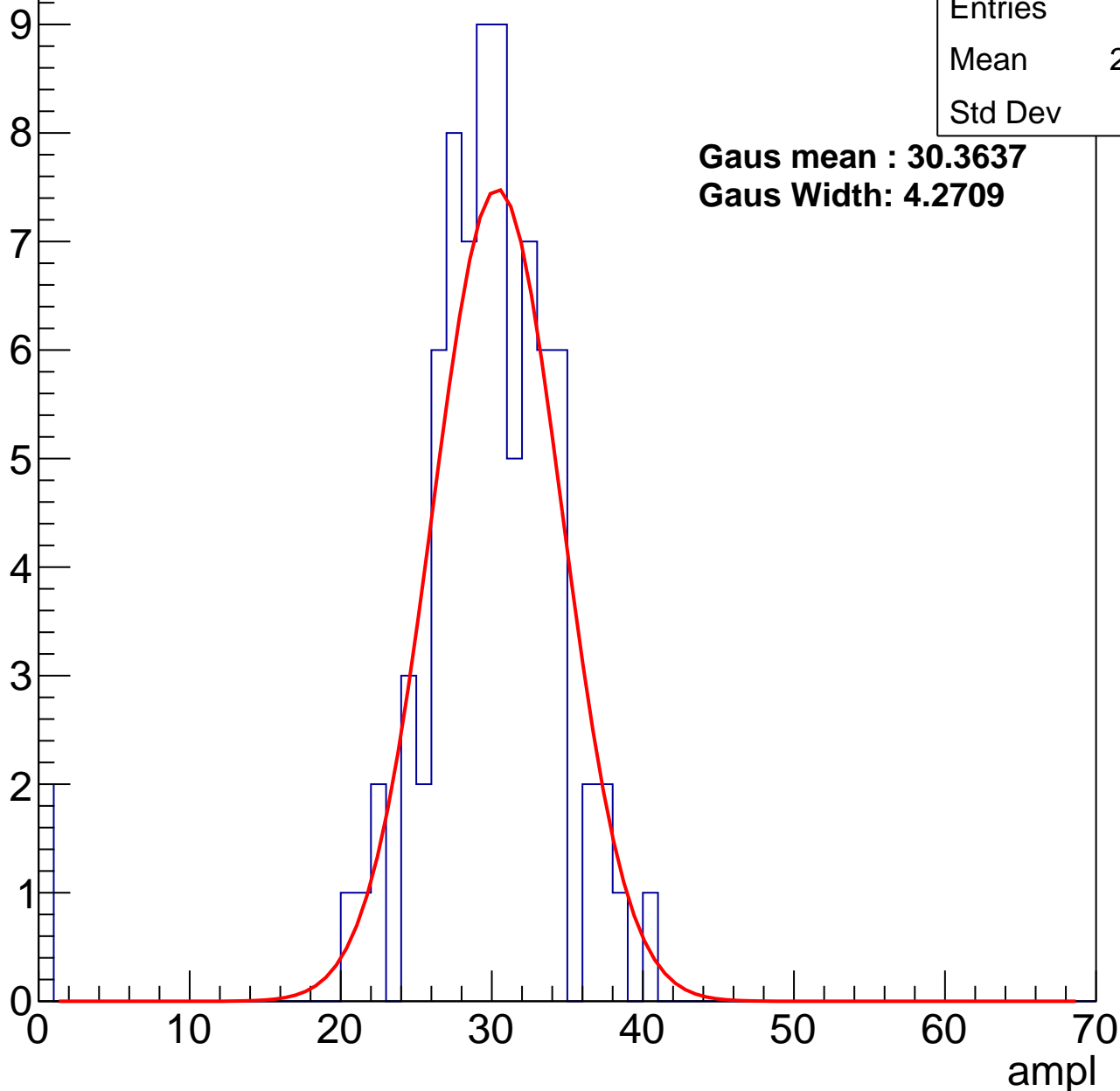
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	28.89
Std Dev	6.01

Gaus mean : 30.3637

Gaus Width: 4.2709



B1L103S, U23-ch17, adc1

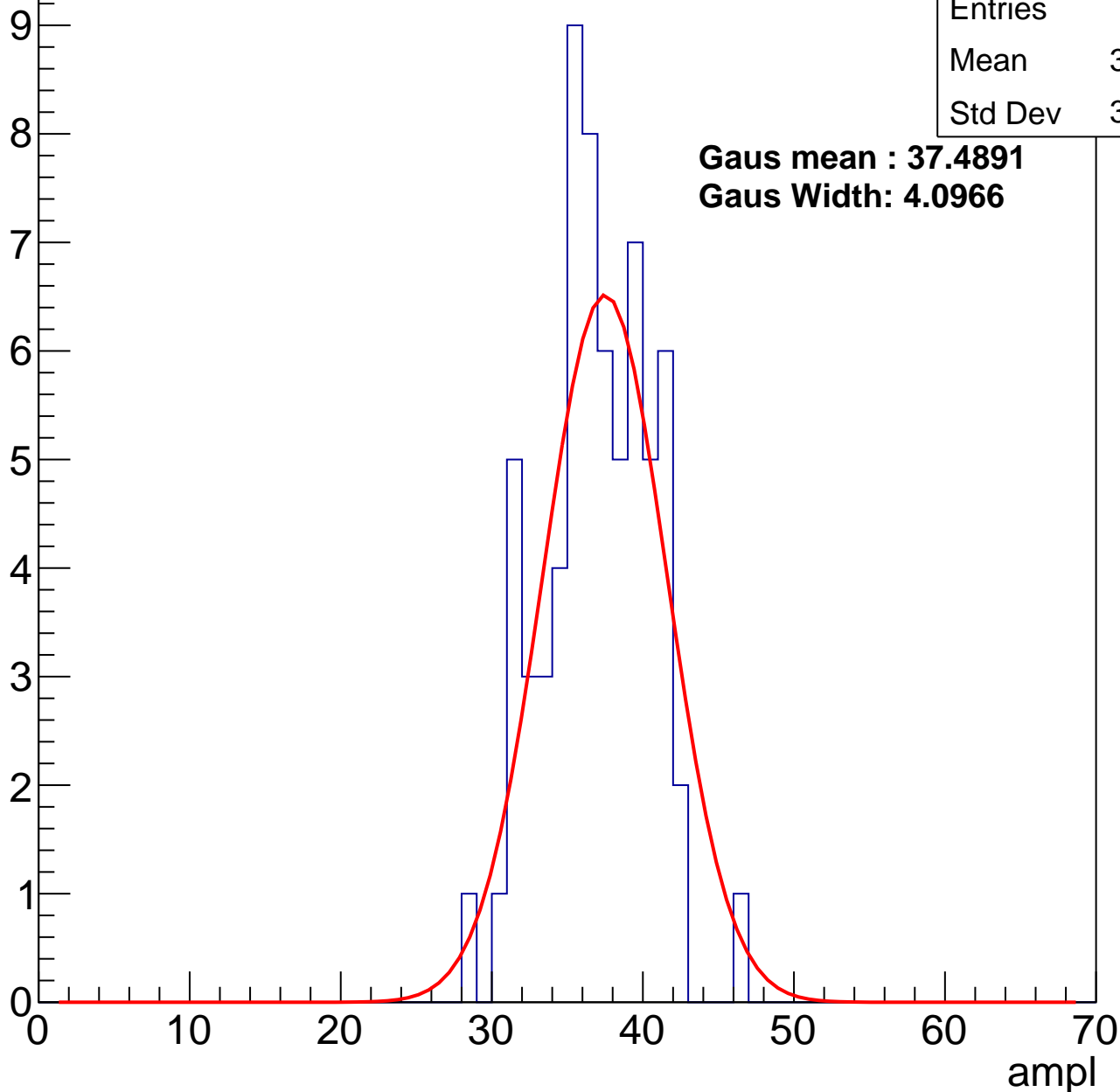
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.48
Std Dev	3.483

Gaus mean : 37.4891

Gaus Width: 4.0966



B1L103S, U23-ch17, adc2

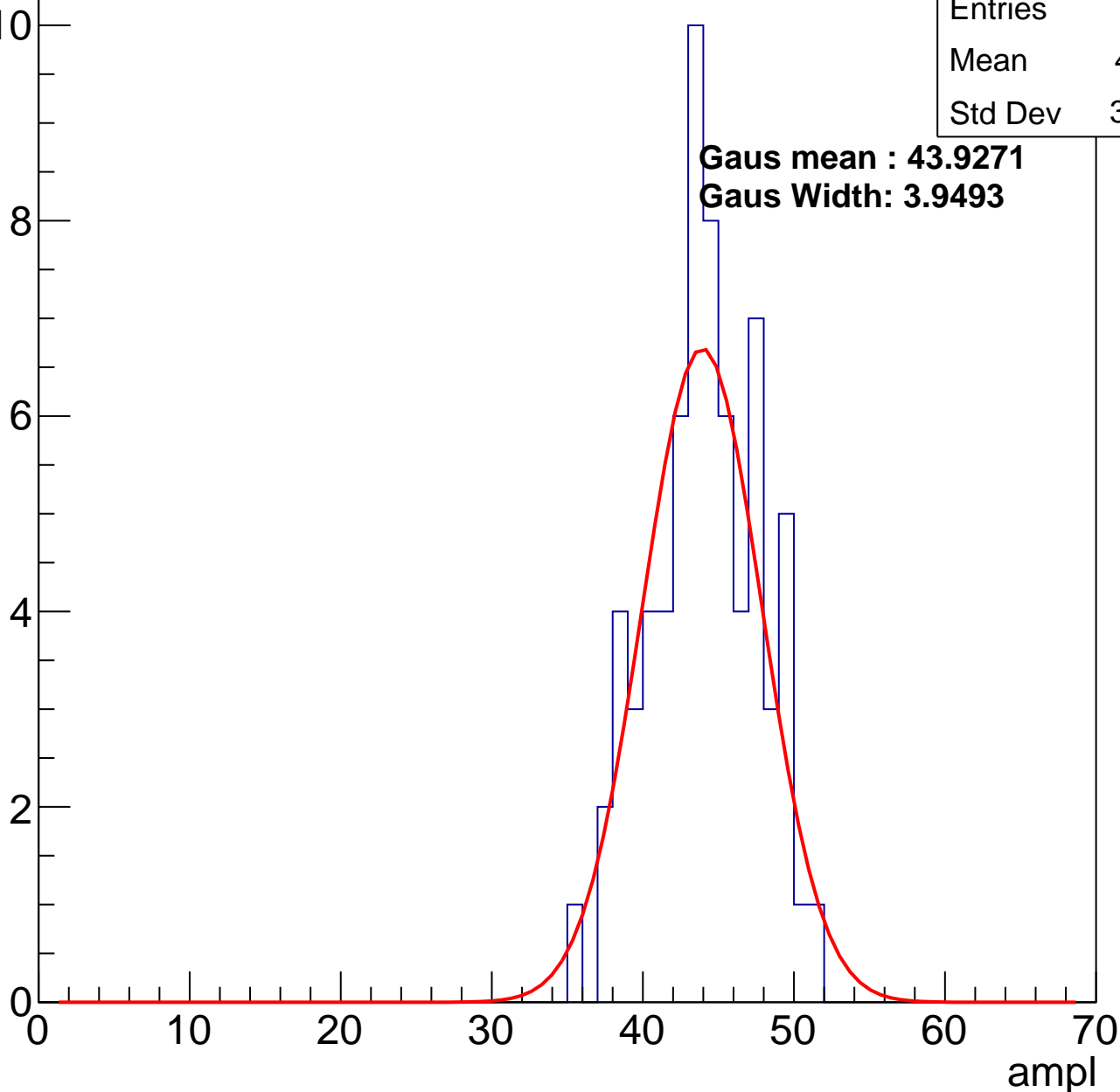
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	43.61
Std Dev	3.539

Gaus mean : 43.9271

Gaus Width: 3.9493

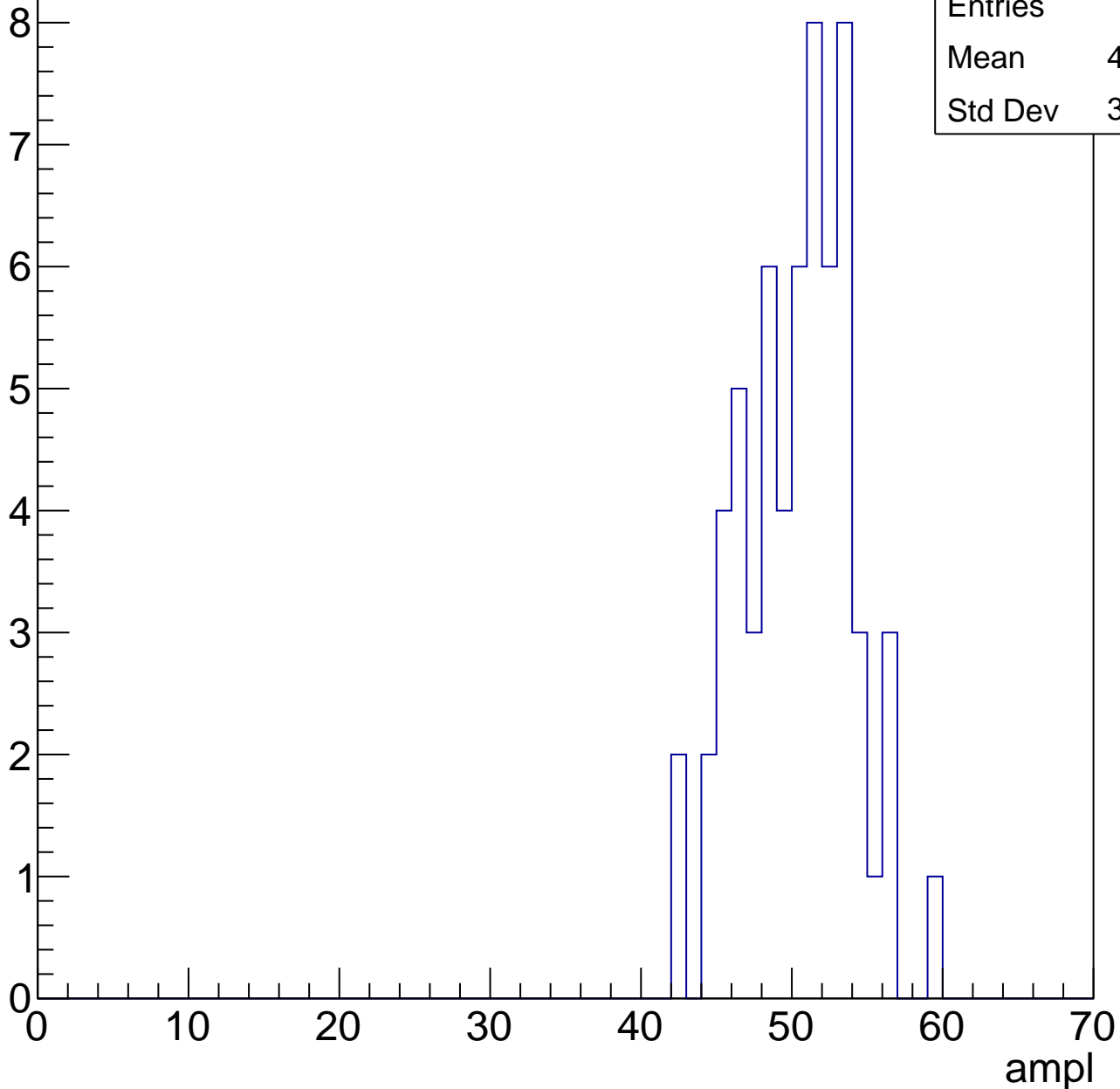


B1L103S, U23-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	49.92
Std Dev	3.593

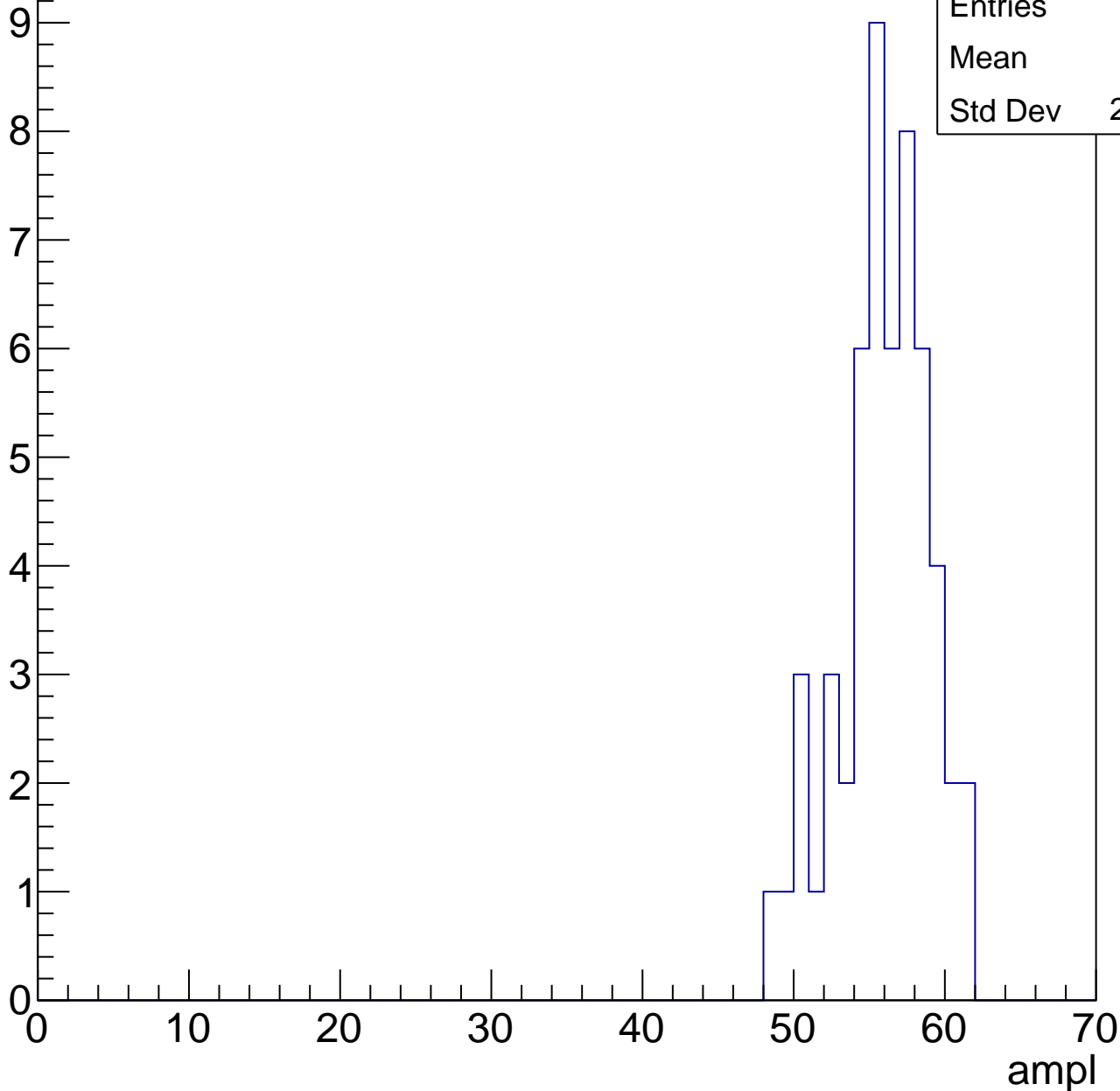


B1L103S, U23-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.5
Std Dev	2.986

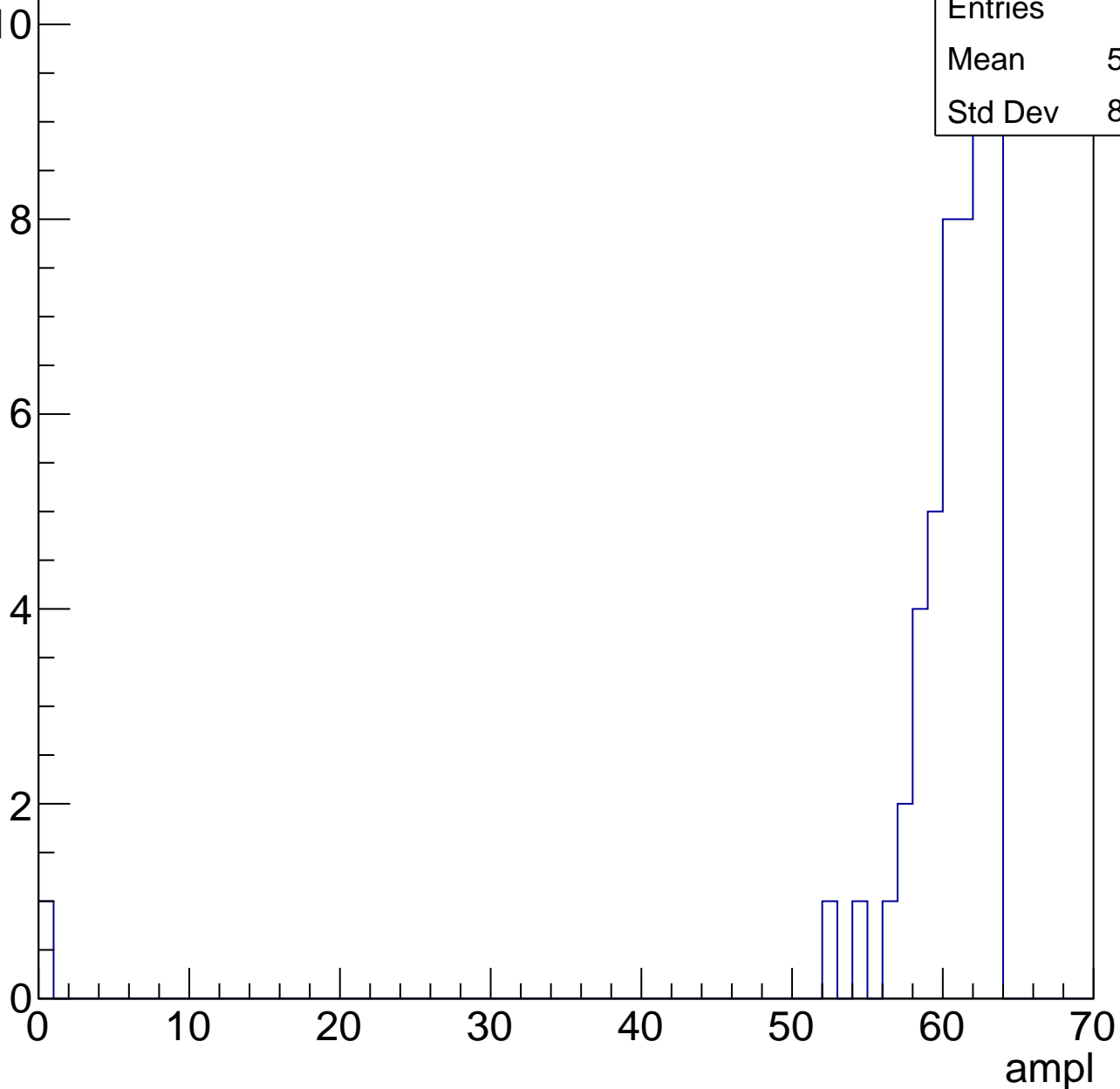


B1L103S, U23-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	59.24
Std Dev	8.703



B1L103S, U23-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch18, adc0

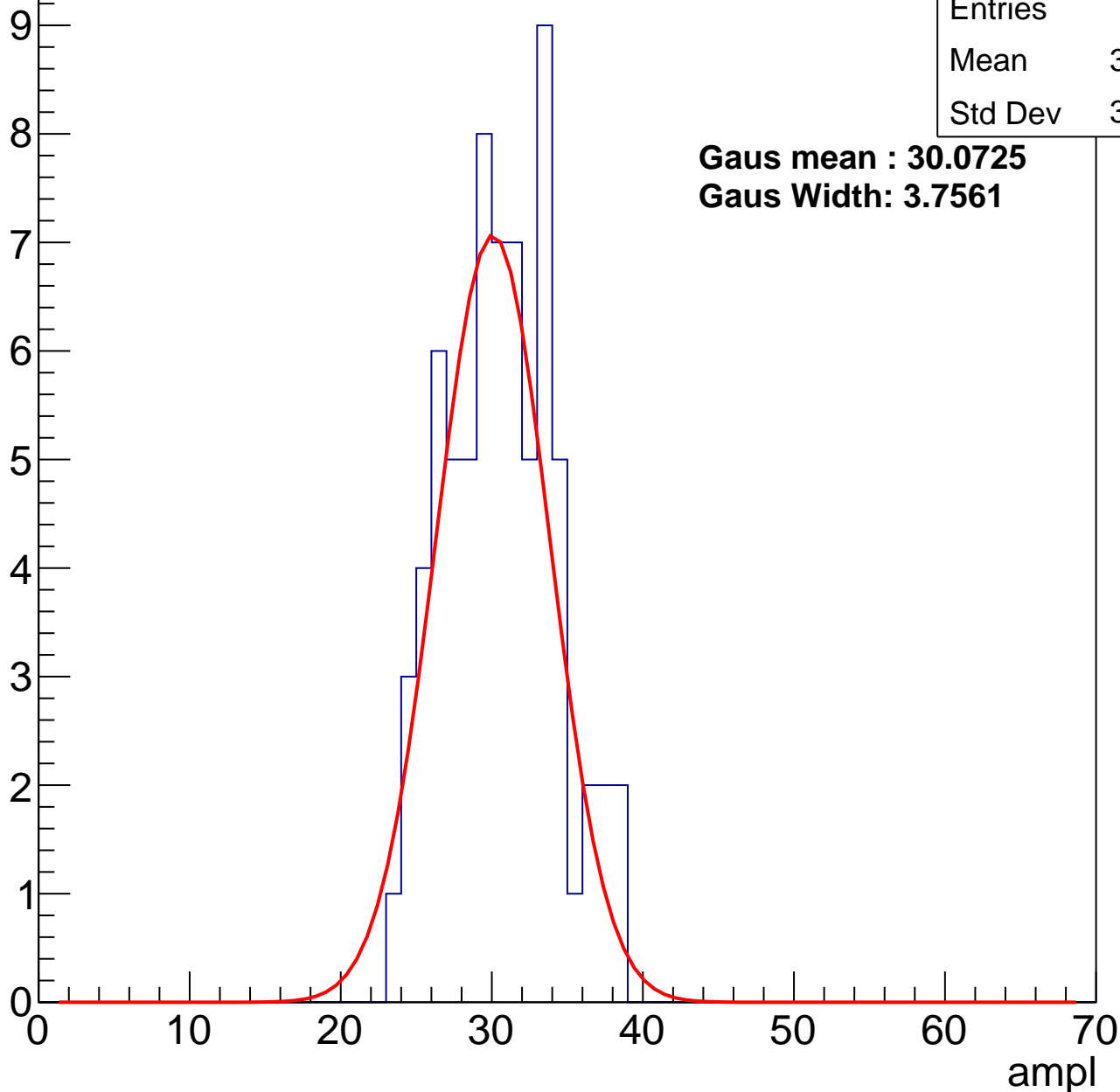
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.12
Std Dev	3.605

Gaus mean : 30.0725

Gaus Width: 3.7561



B1L103S, U23-ch18, adc1

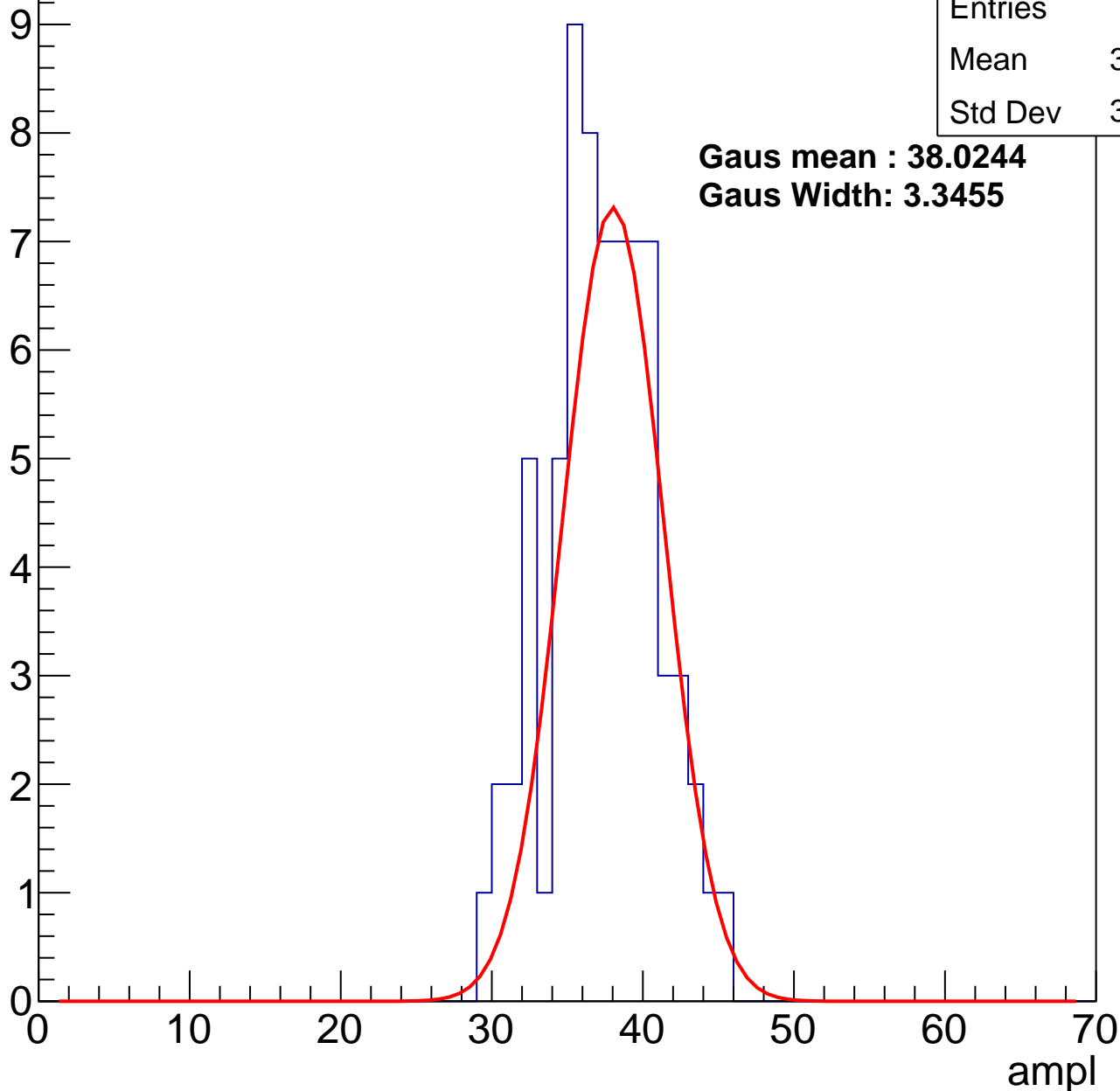
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.89
Std Dev	3.499

Gaus mean : 38.0244

Gaus Width: 3.3455



B1L103S, U23-ch18, adc2

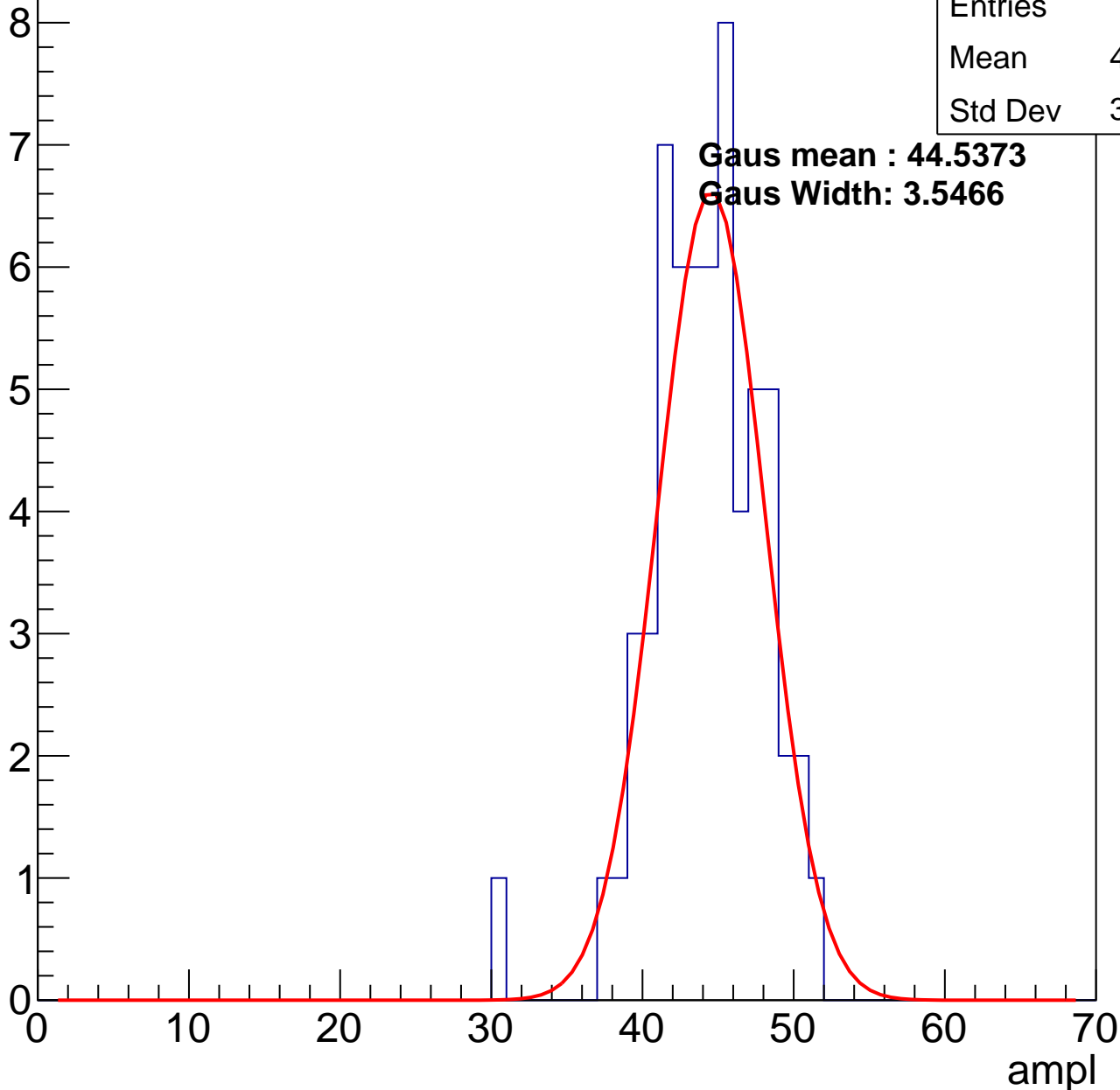
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	43.79
Std Dev	3.653

Gaus mean : 44.5373

Gaus Width: 3.5466

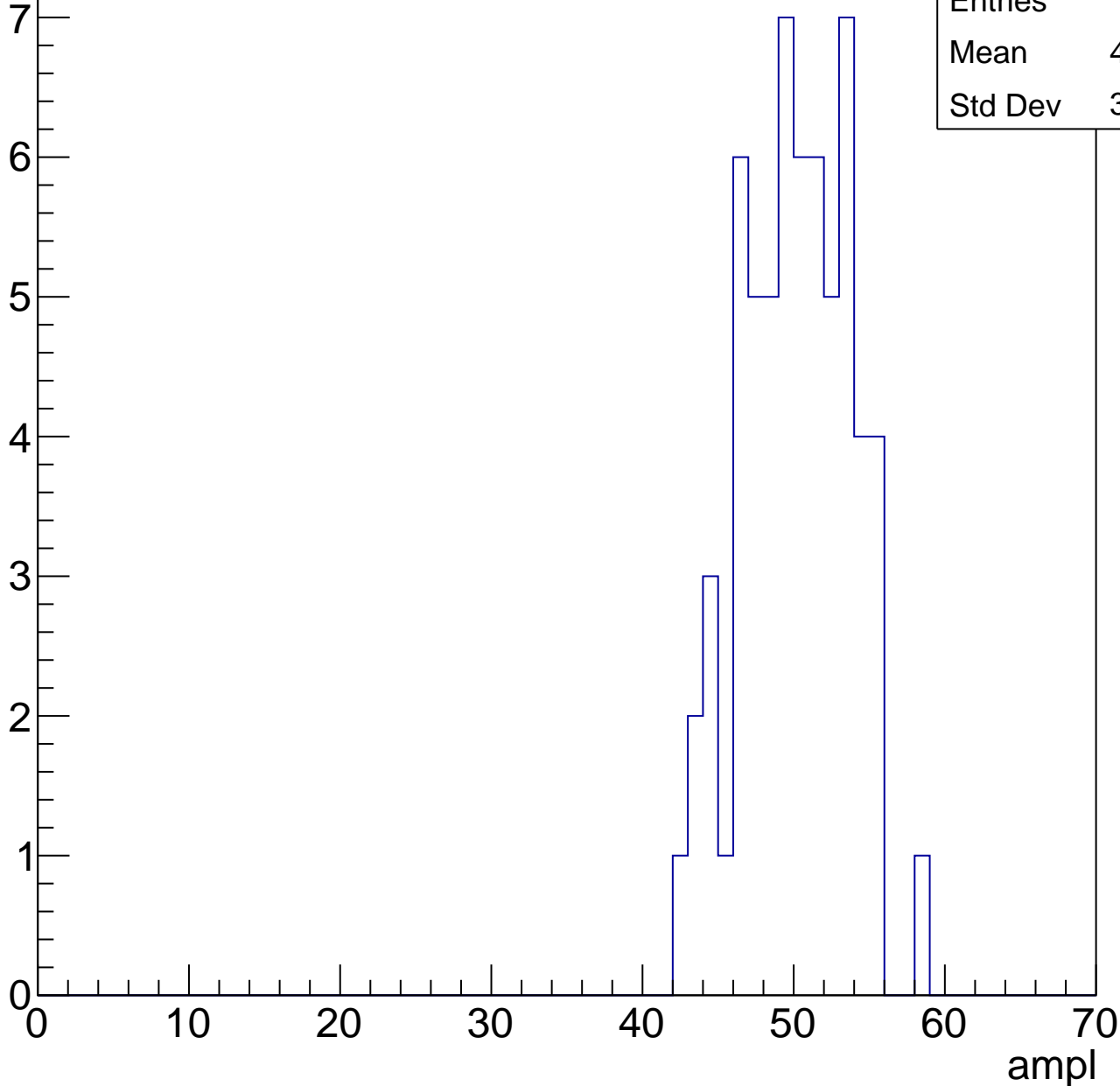


B1L103S, U23-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.68
Std Dev	3.504

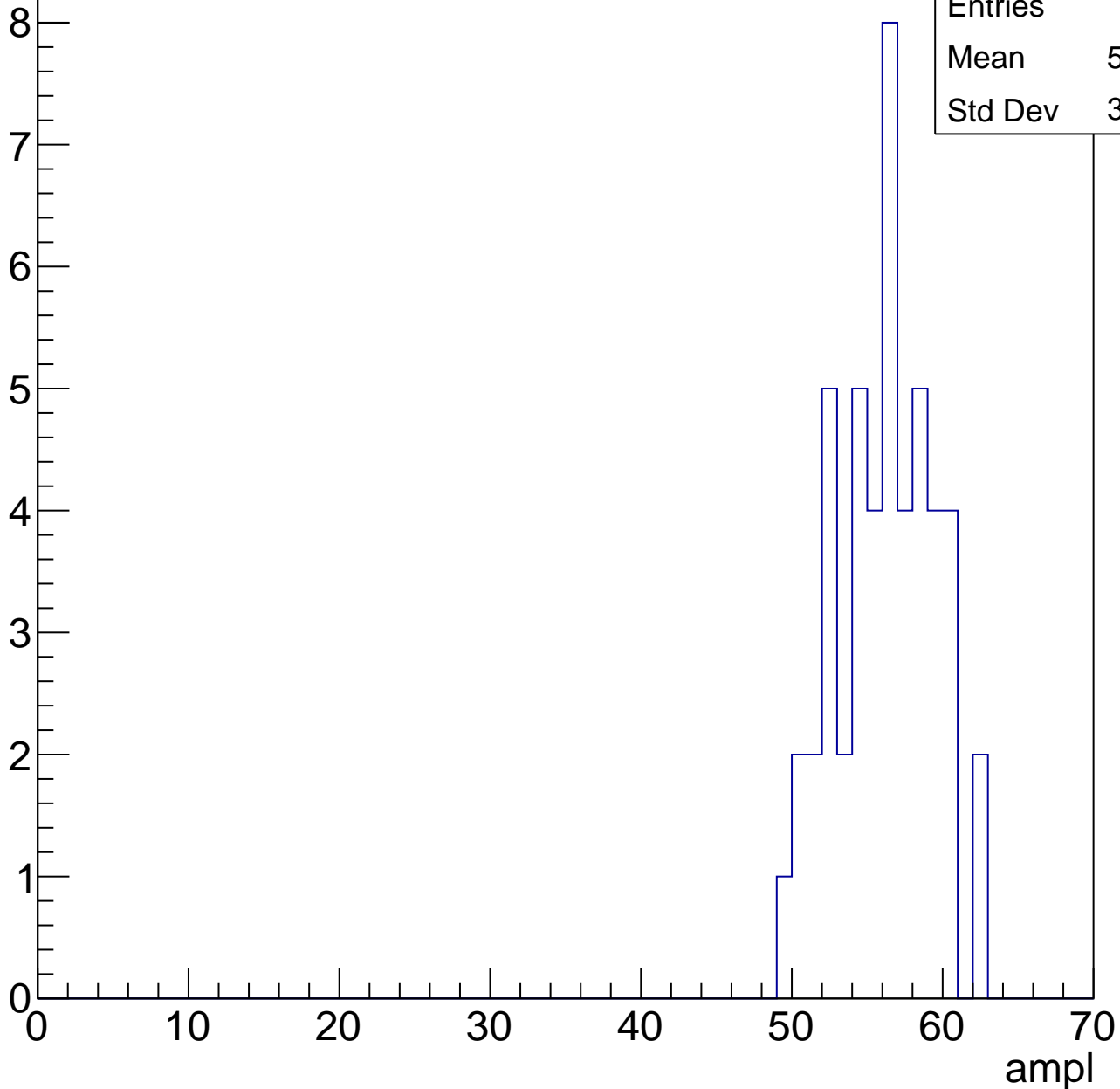


B1L103S, U23-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	55.69
Std Dev	3.176

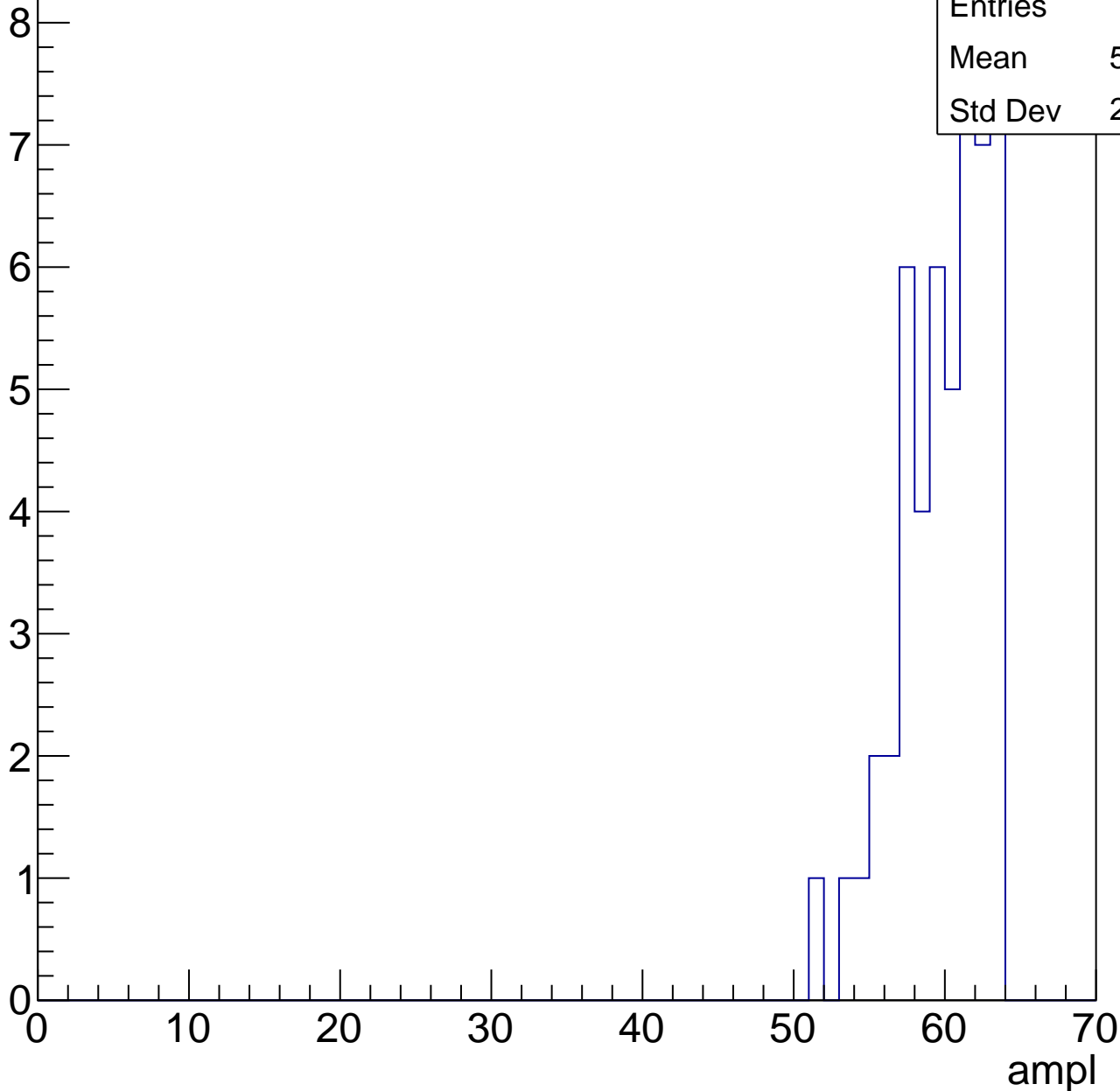


B1L103S, U23-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	59.49
Std Dev	2.865



B1L103S, U23-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

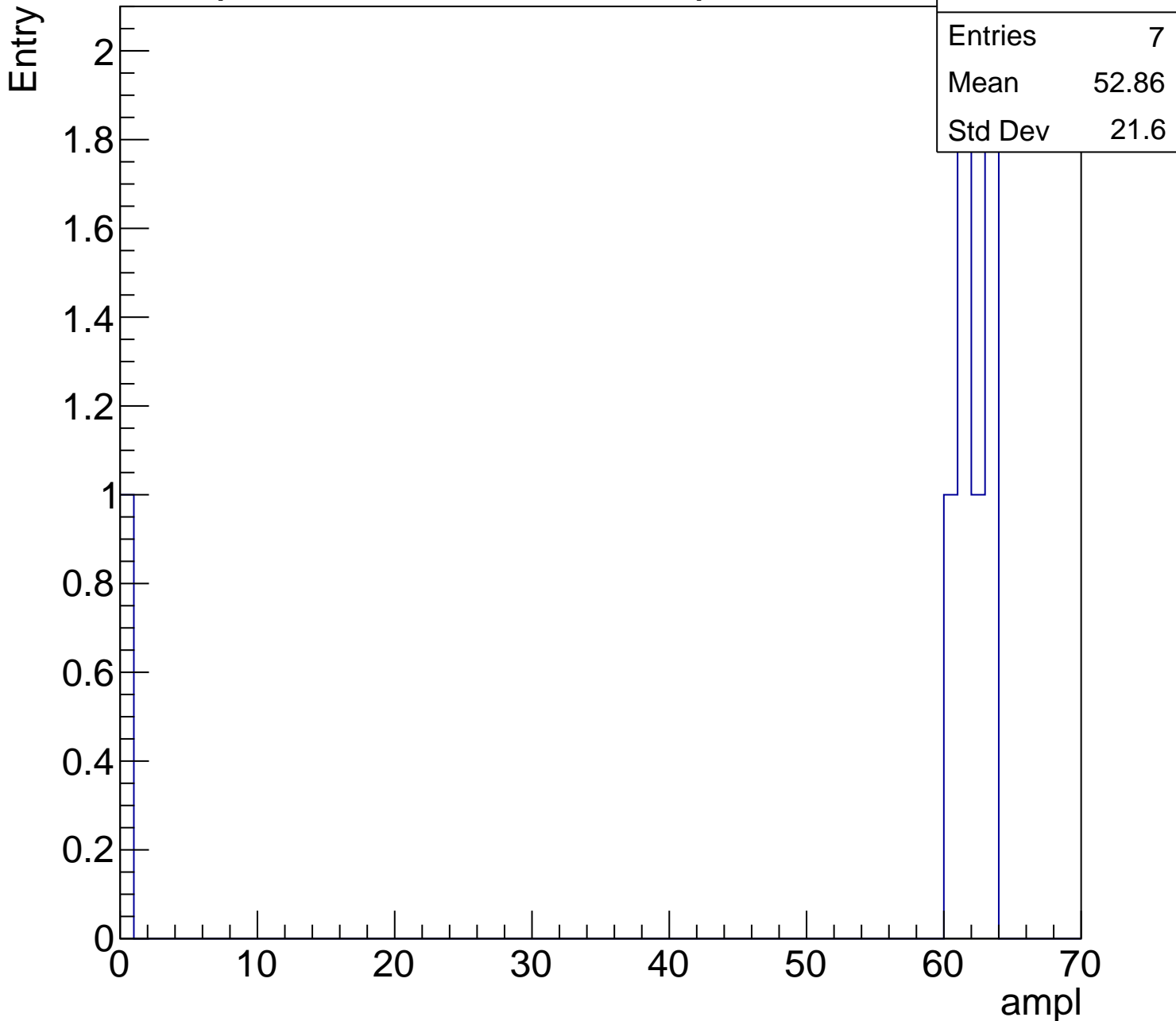
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.6

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch19, adc0

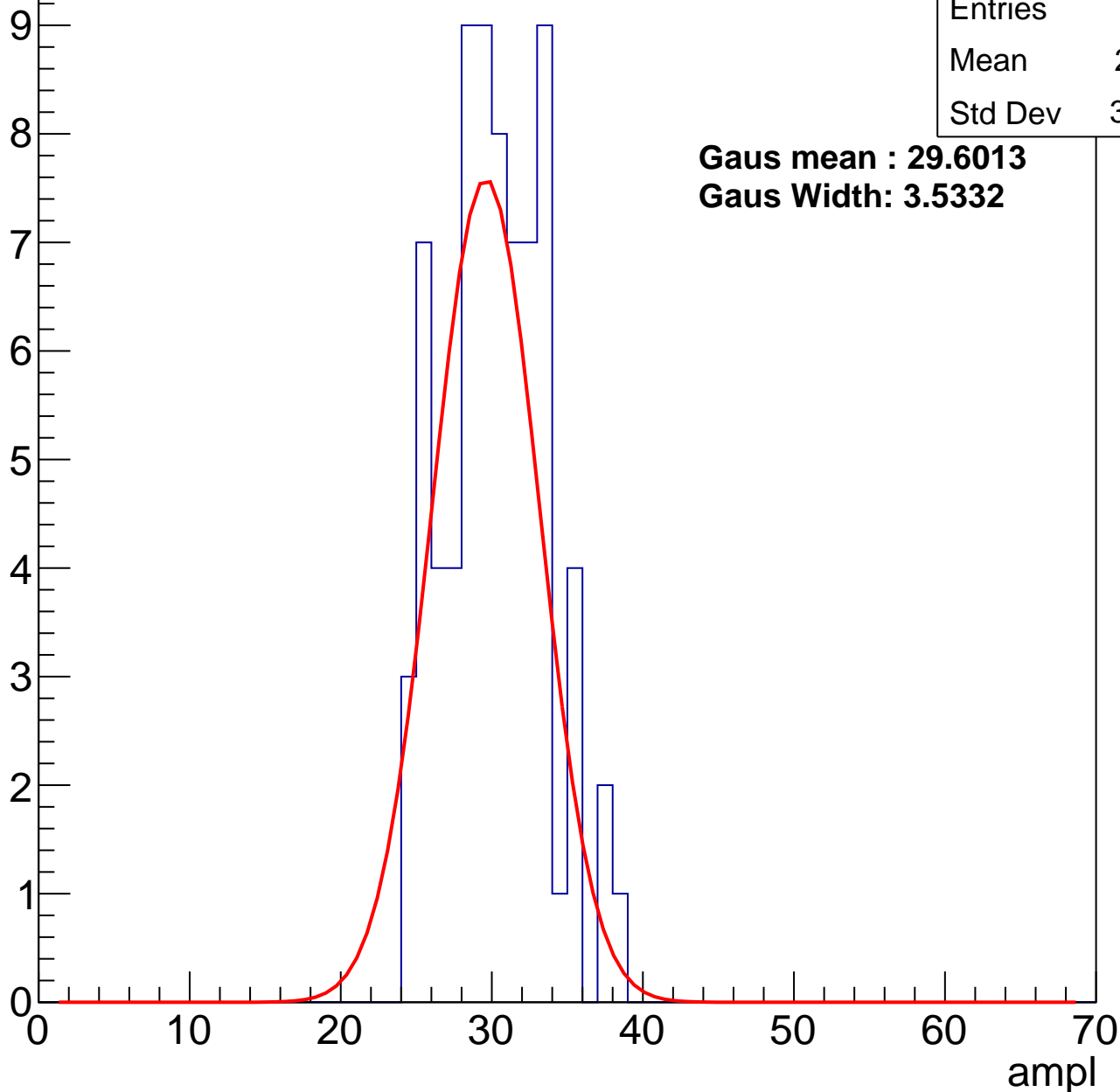
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.81
Std Dev	3.305

Gaus mean : 29.6013

Gaus Width: 3.5332



B1L103S, U23-ch19, adc1

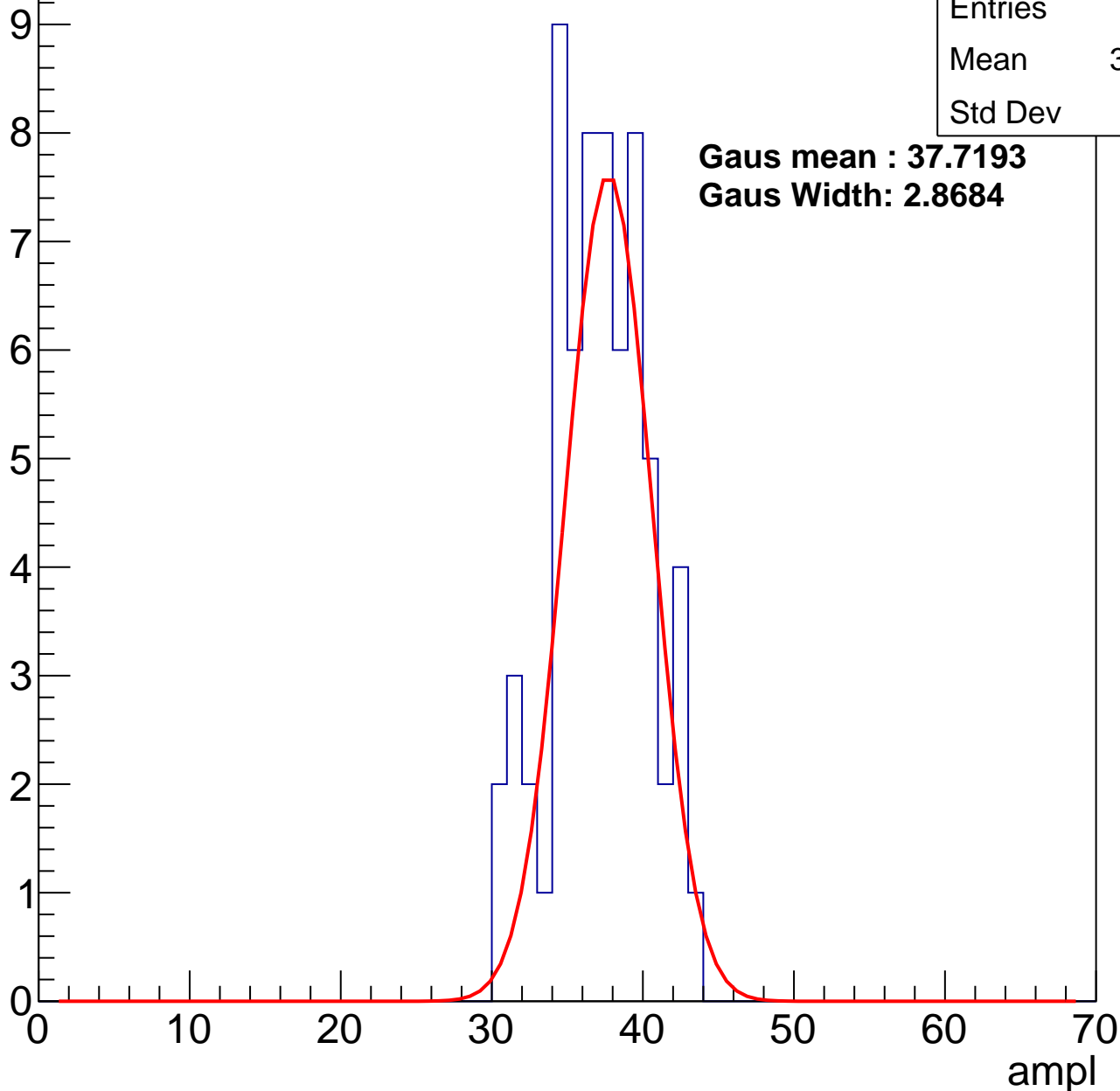
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.66
Std Dev	3.11

Gaus mean : 37.7193

Gaus Width: 2.8684



B1L103S, U23-ch19, adc2

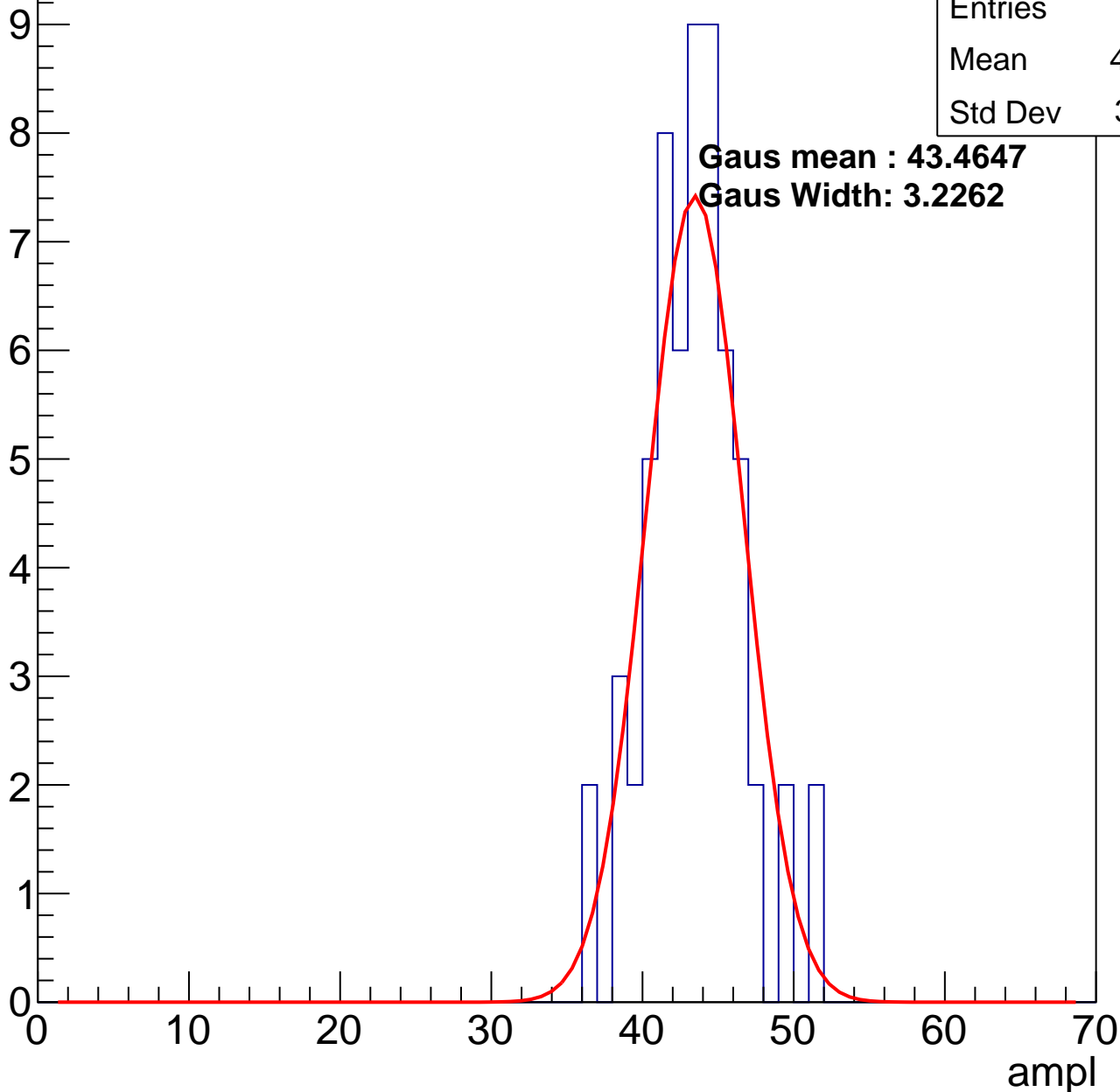
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.97
Std Dev	3.131

Gaus mean : 43.4647

Gaus Width: 3.2262

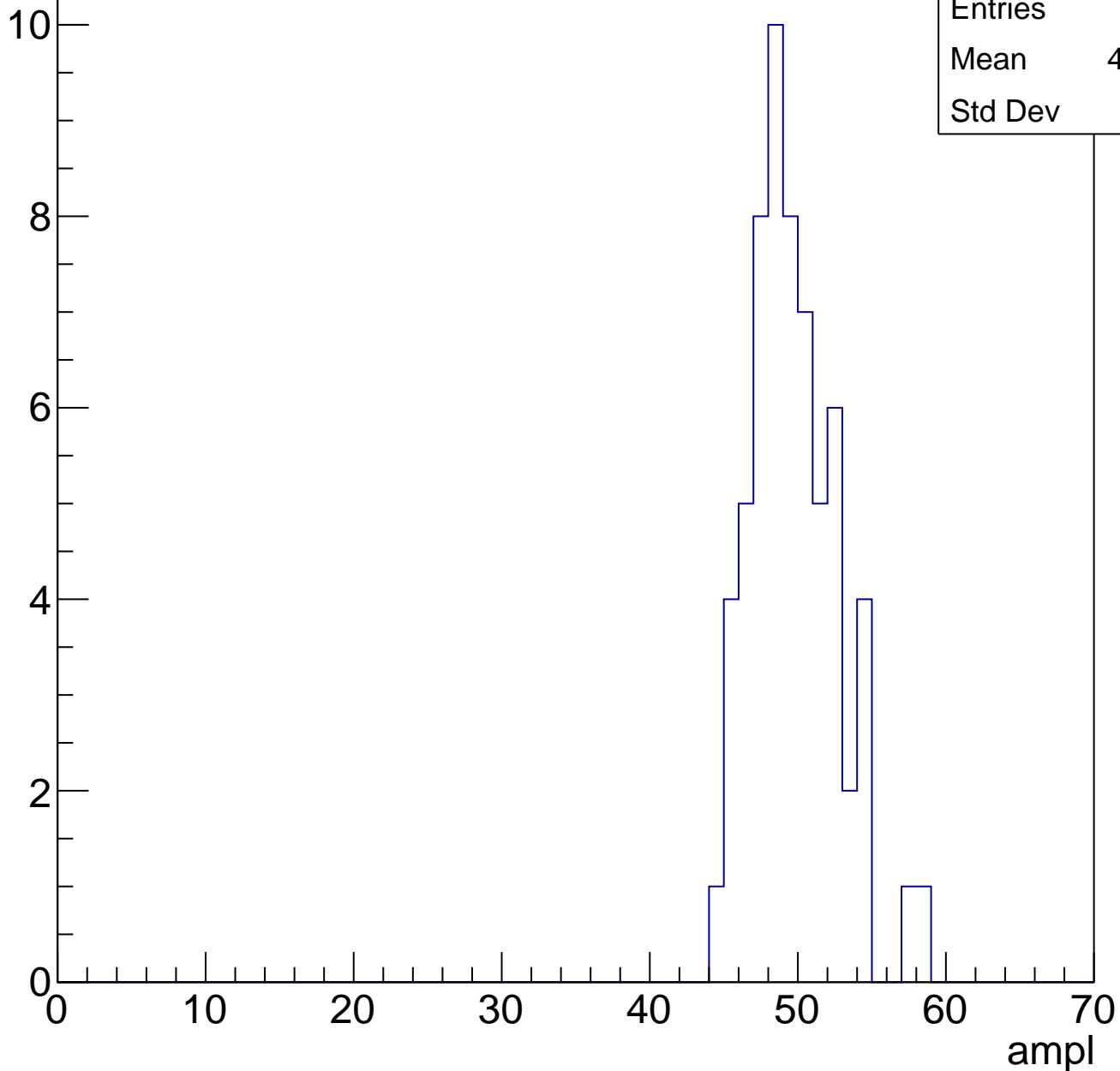


B1L103S, U23-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	49.29
Std Dev	2.92

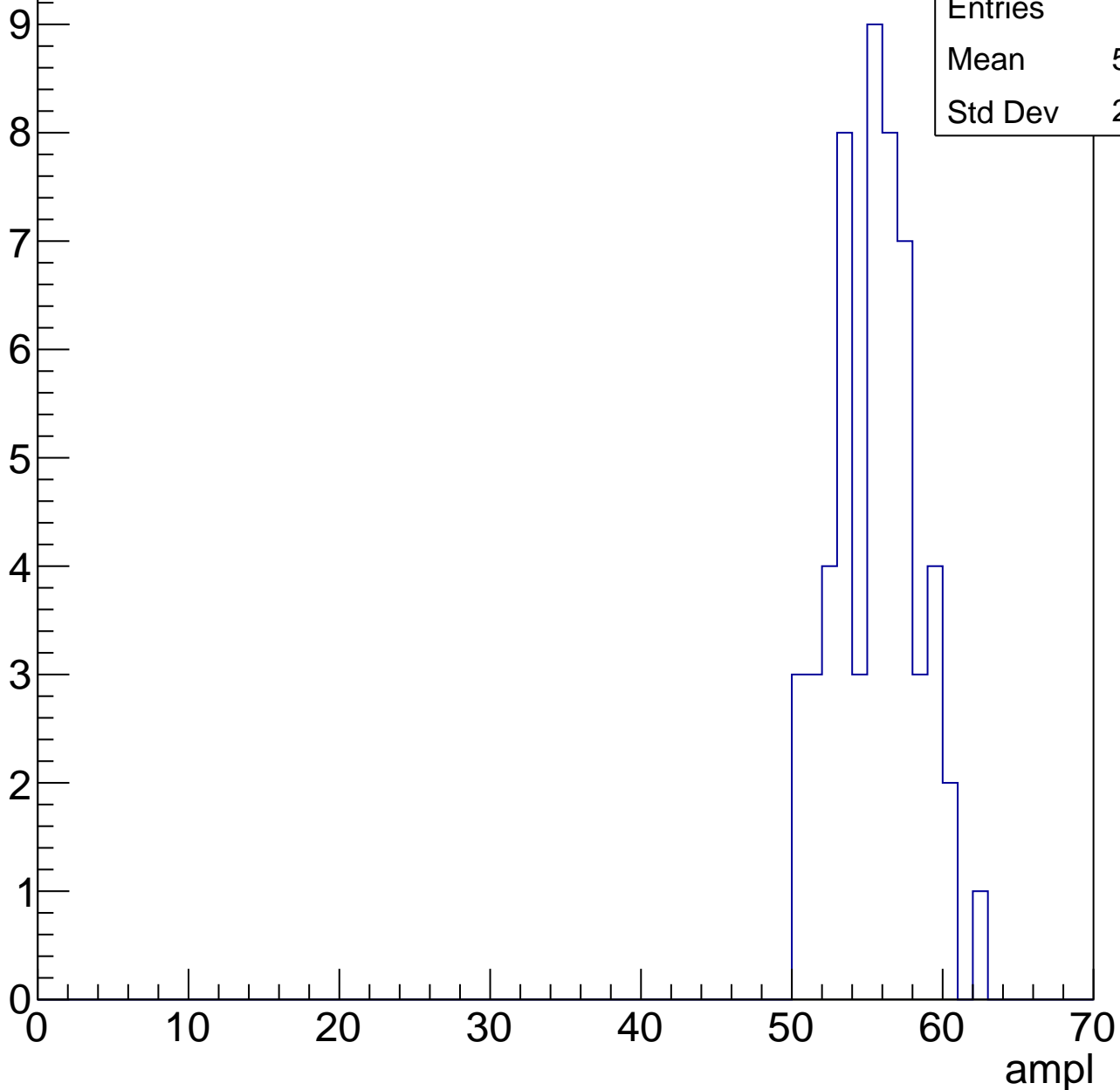


B1L103S, U23-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	55.11
Std Dev	2.761

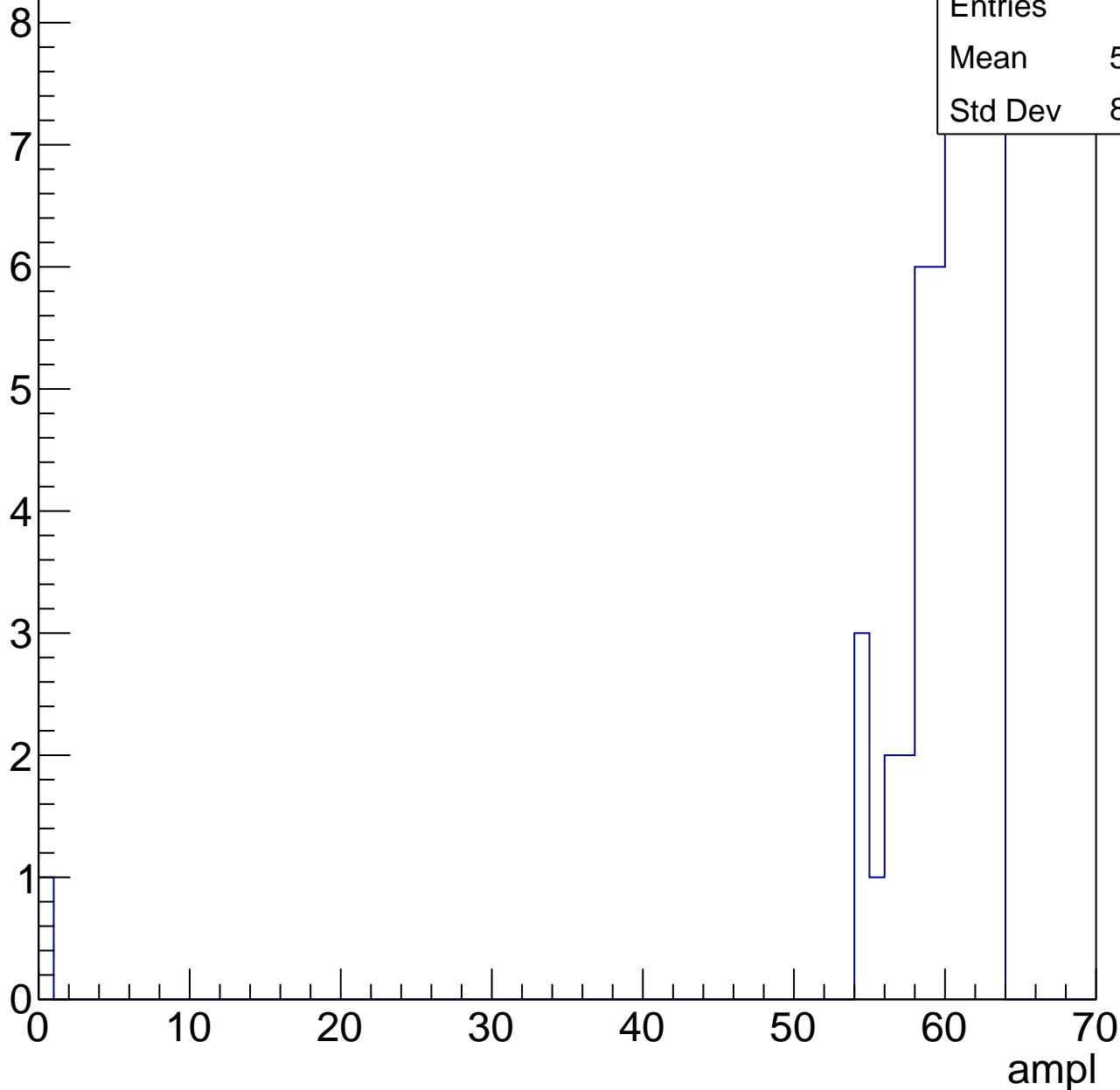


B1L103S, U23-ch19, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

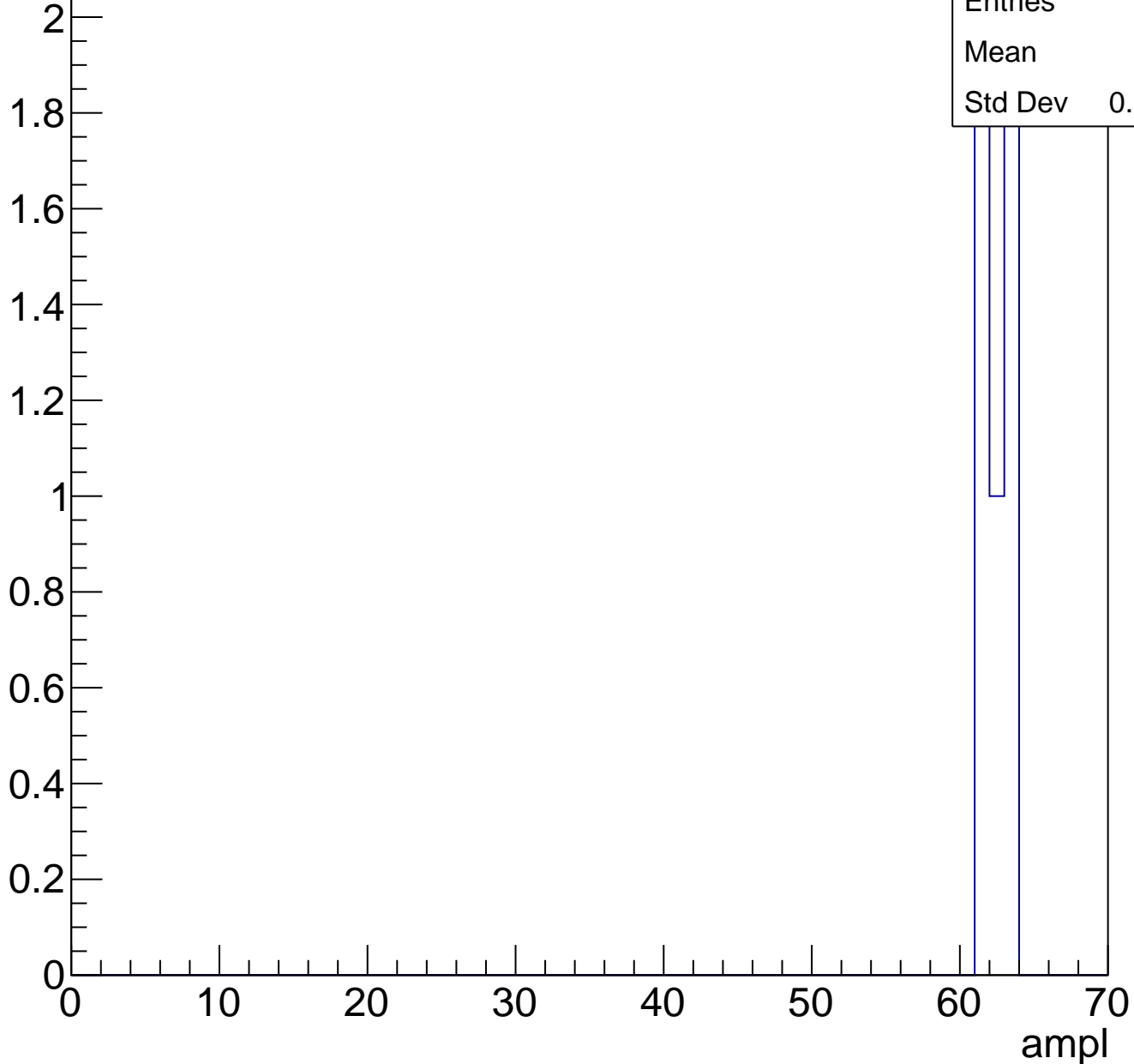
Entries	53
Mean	58.74
Std Dev	8.512



B1L103S, U23-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch20, adc0

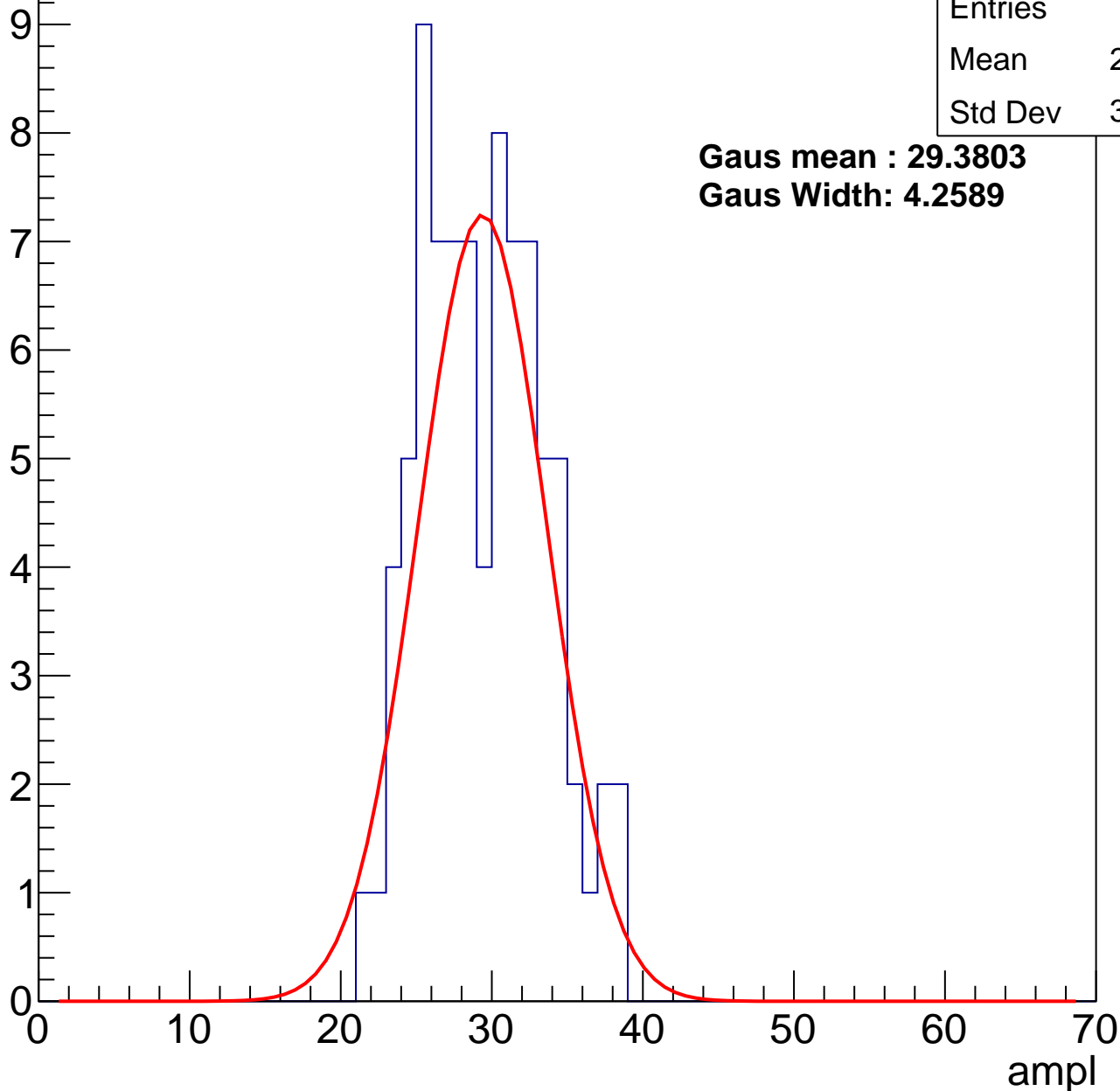
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	28.99
Std Dev	3.993

Gaus mean : 29.3803

Gaus Width: 4.2589



B1L103S, U23-ch20, adc1

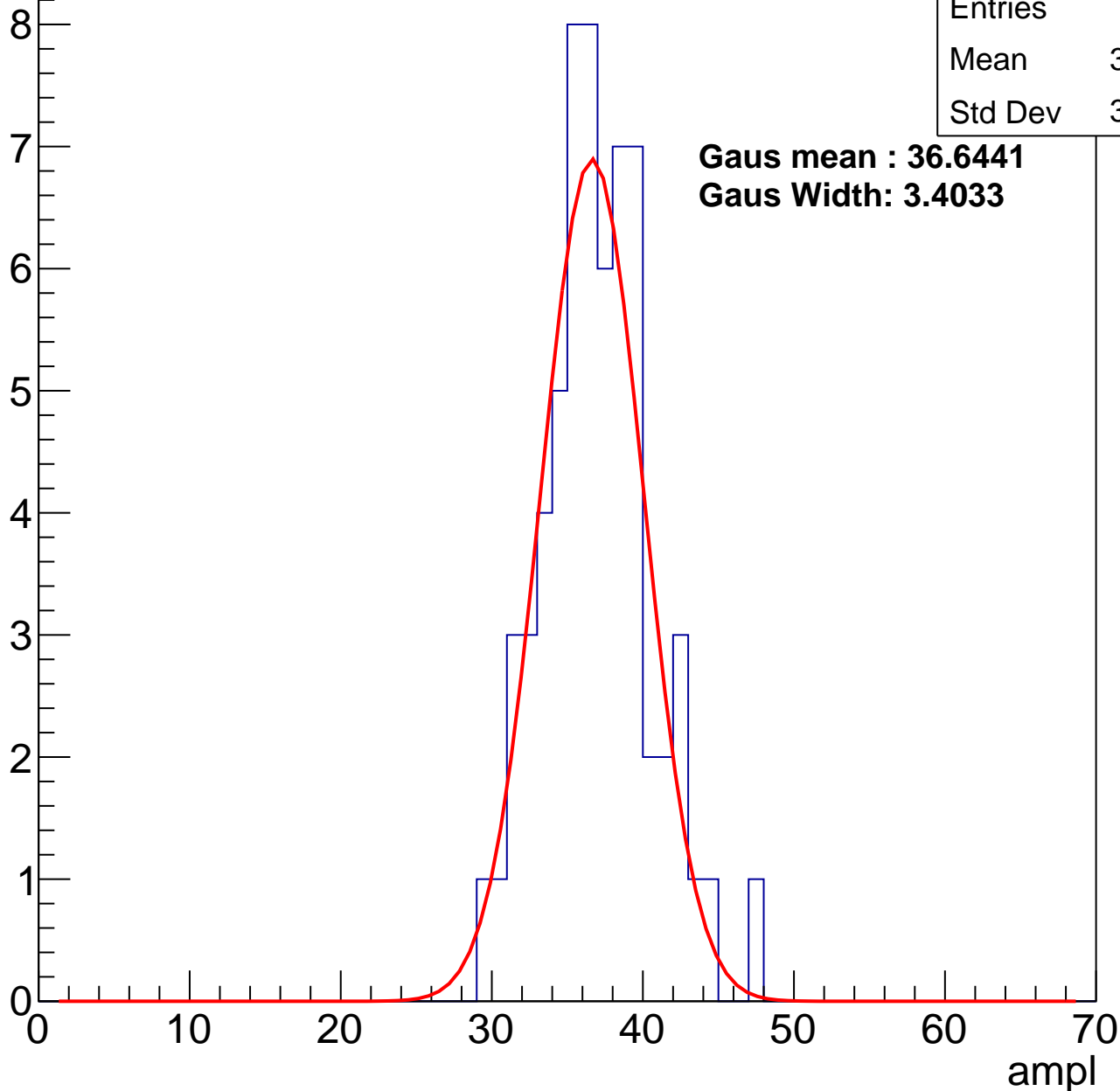
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	36.52
Std Dev	3.509

Gaus mean : 36.6441

Gaus Width: 3.4033



B1L103S, U23-ch20, adc2

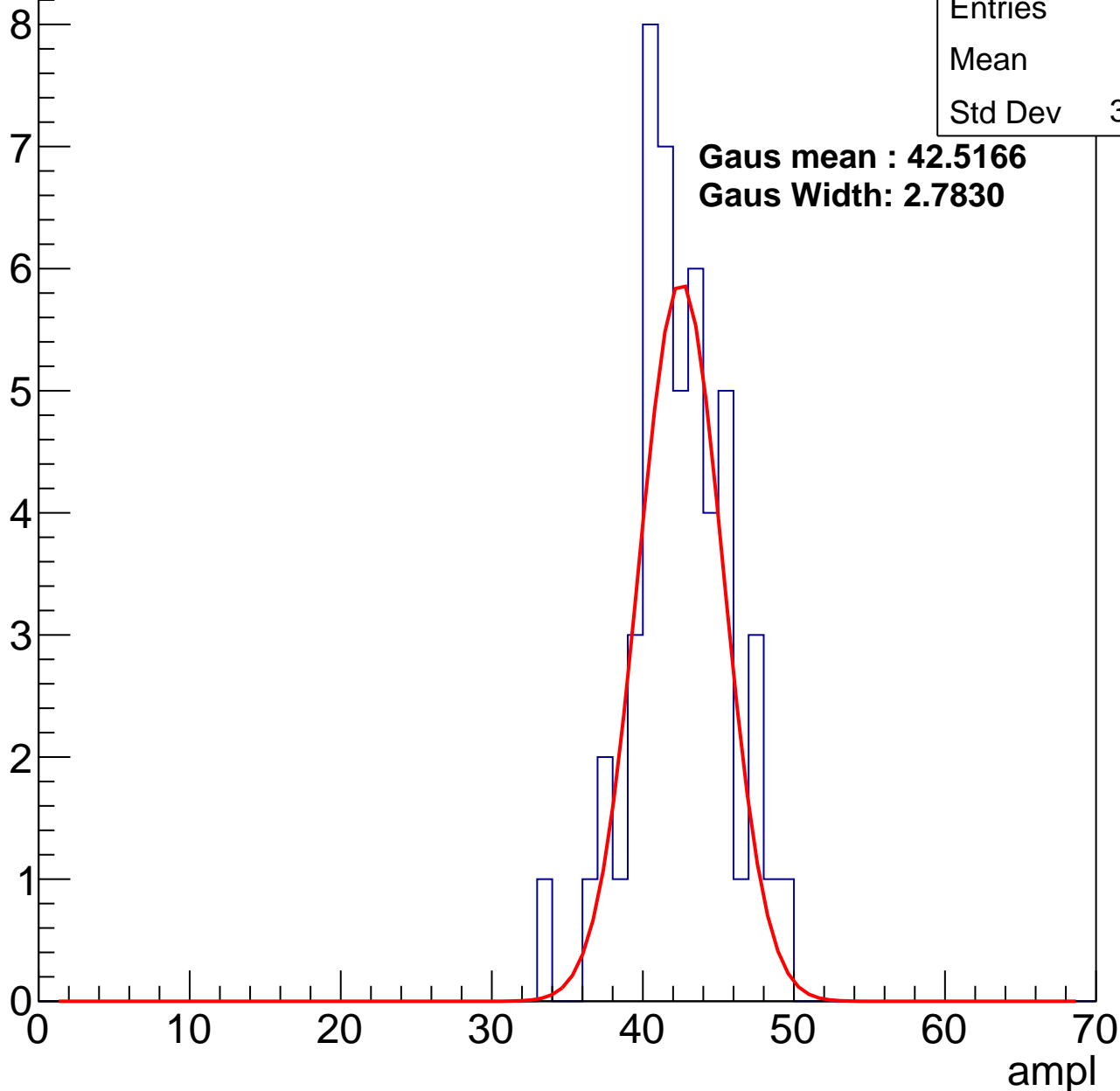
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	42
Std Dev	3.169

Gaus mean : 42.5166

Gaus Width: 2.7830

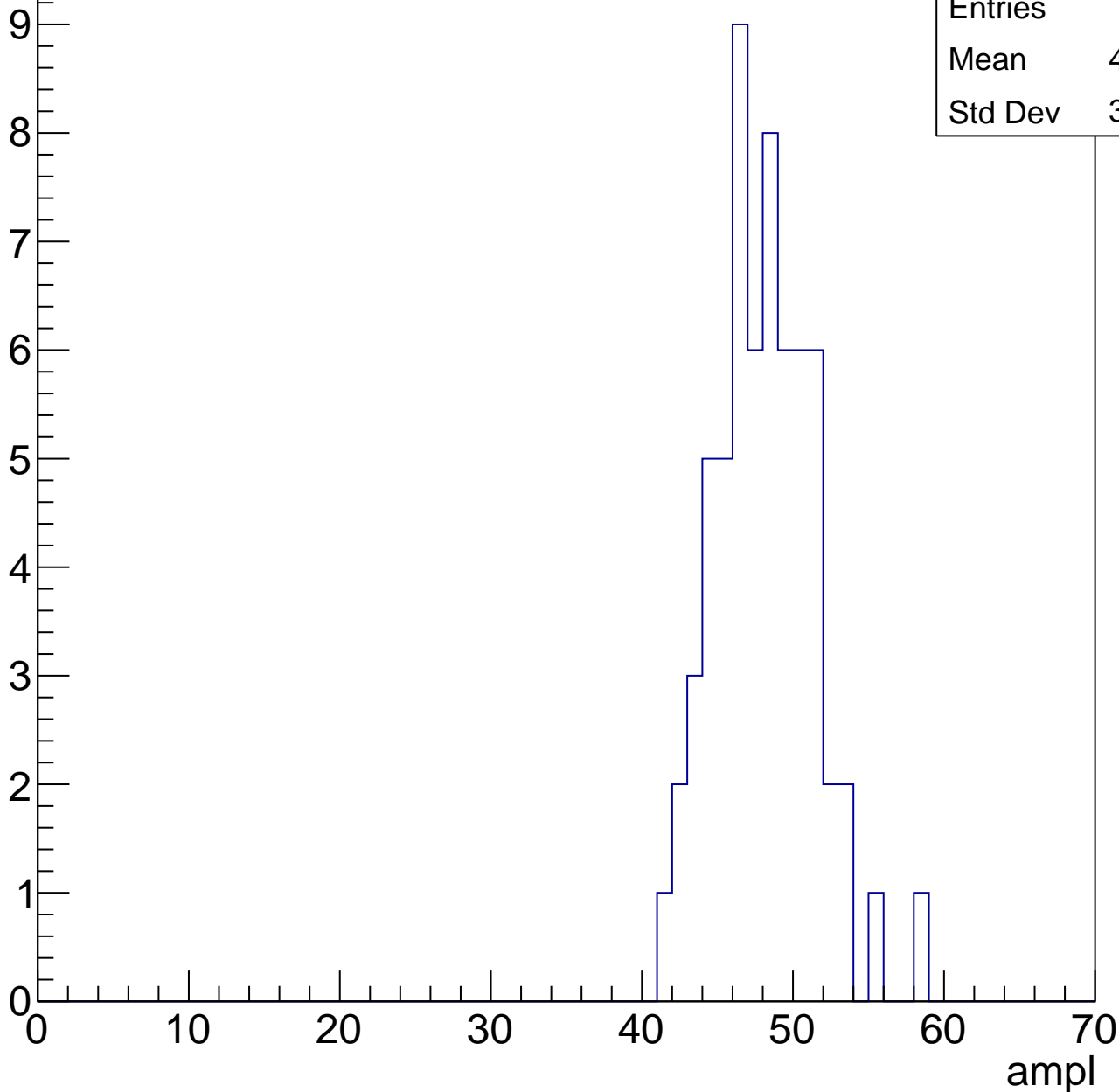


B1L103S, U23-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

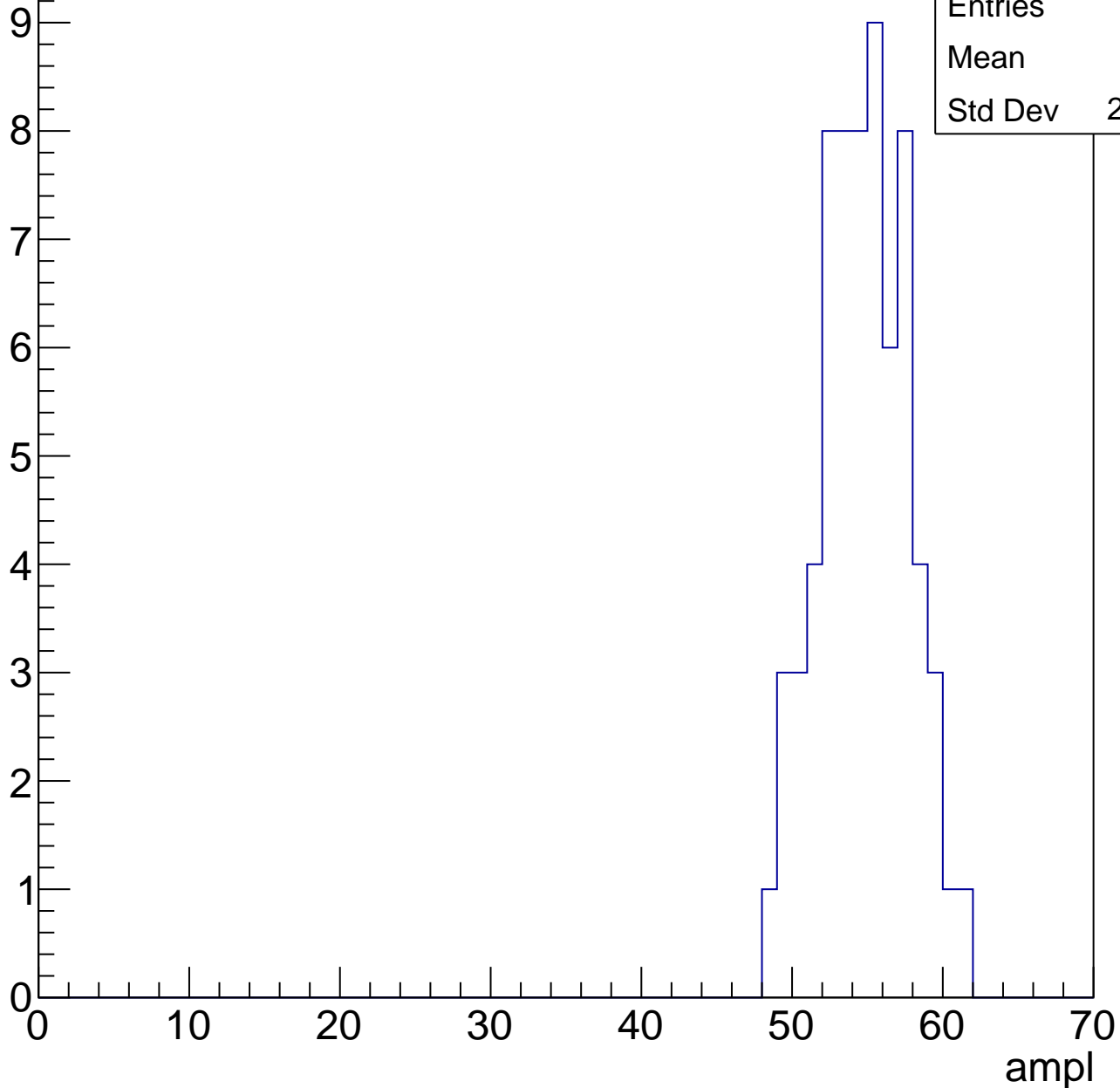
Entries	63
Mean	47.65
Std Dev	3.262



B1L103S, U23-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

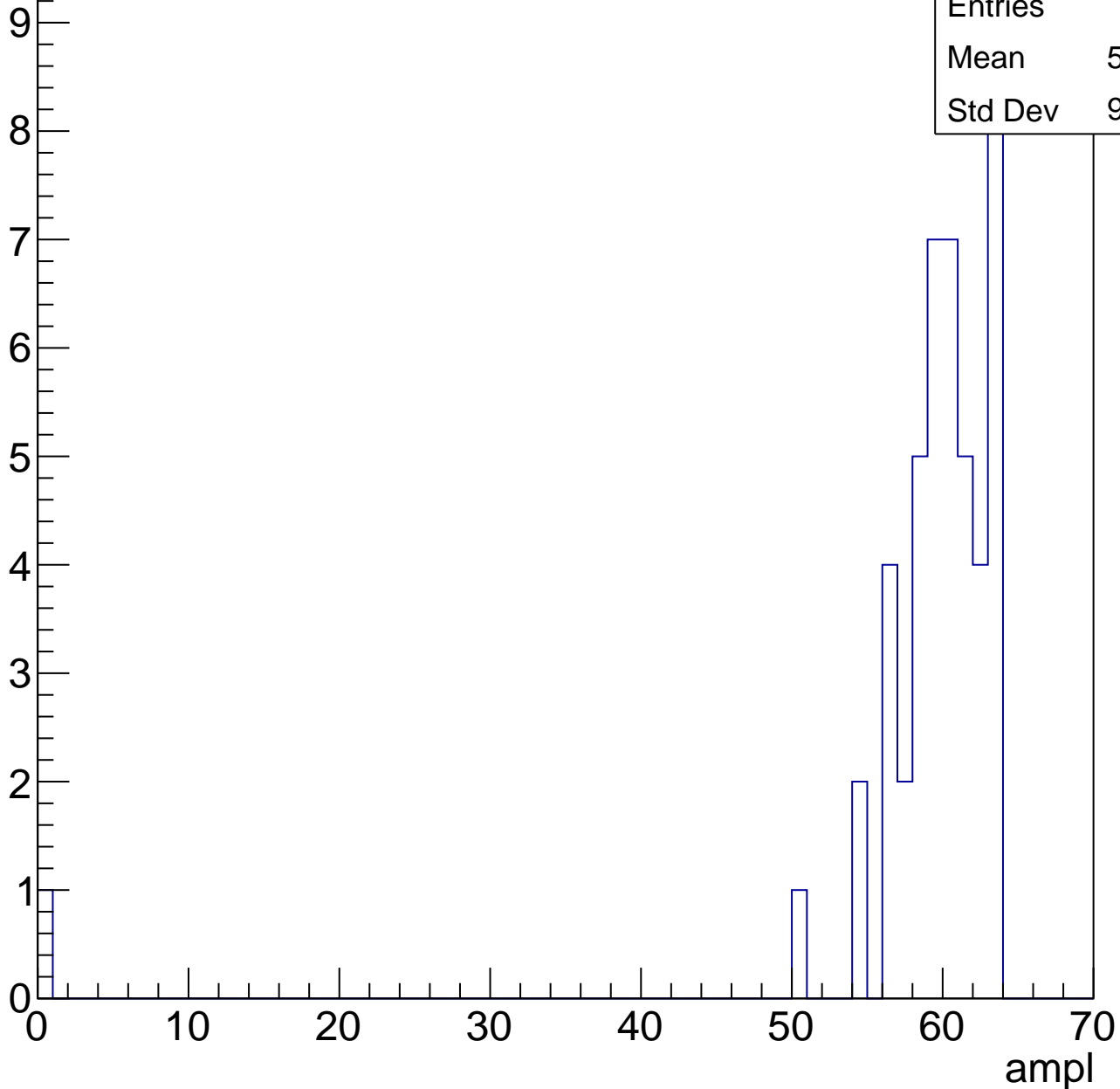


B1L103S, U23-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	58.28
Std Dev	9.043

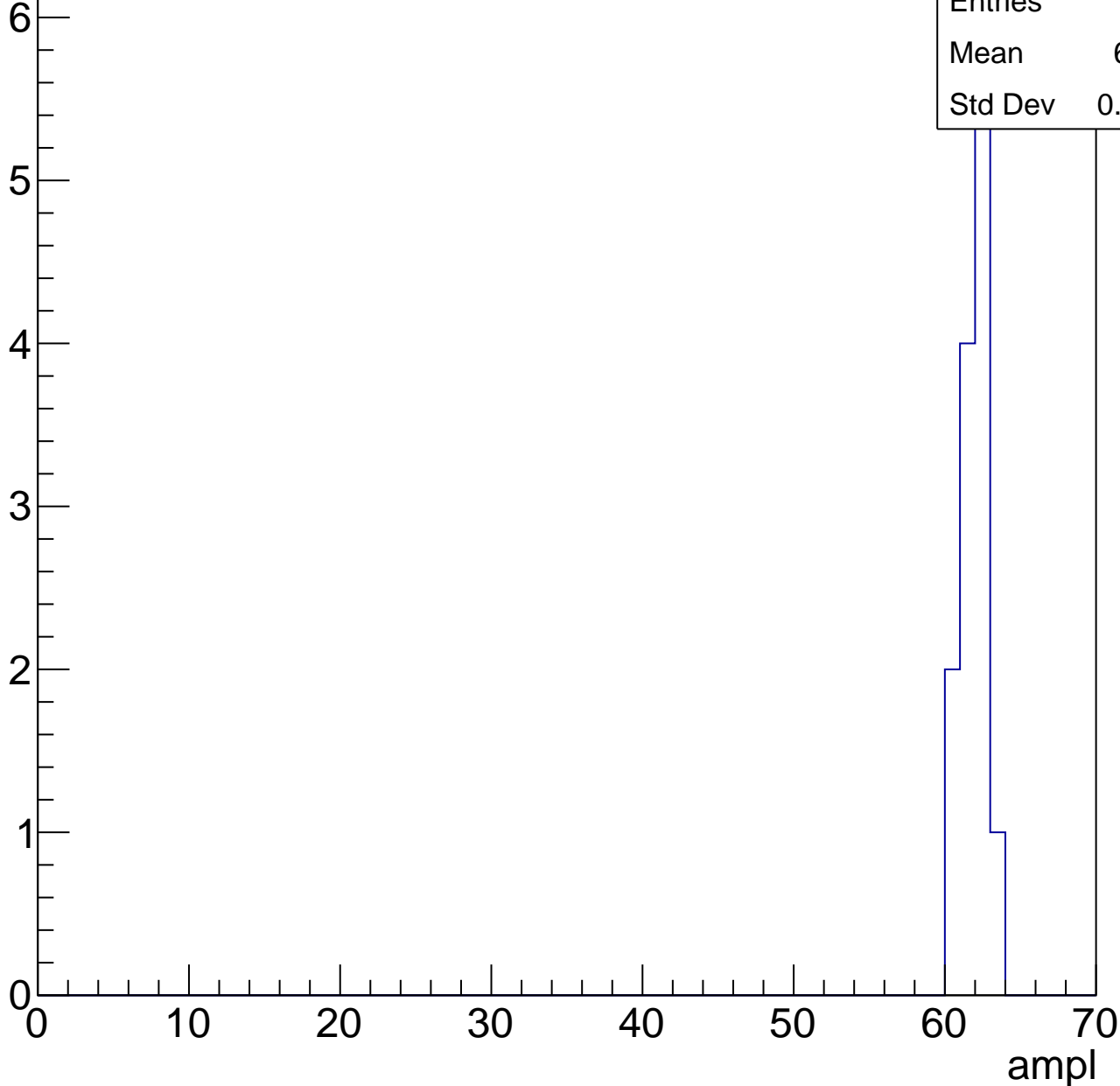


B1L103S, U23-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	61.46
Std Dev	0.8427



B1L103S, U23-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch21, adc0

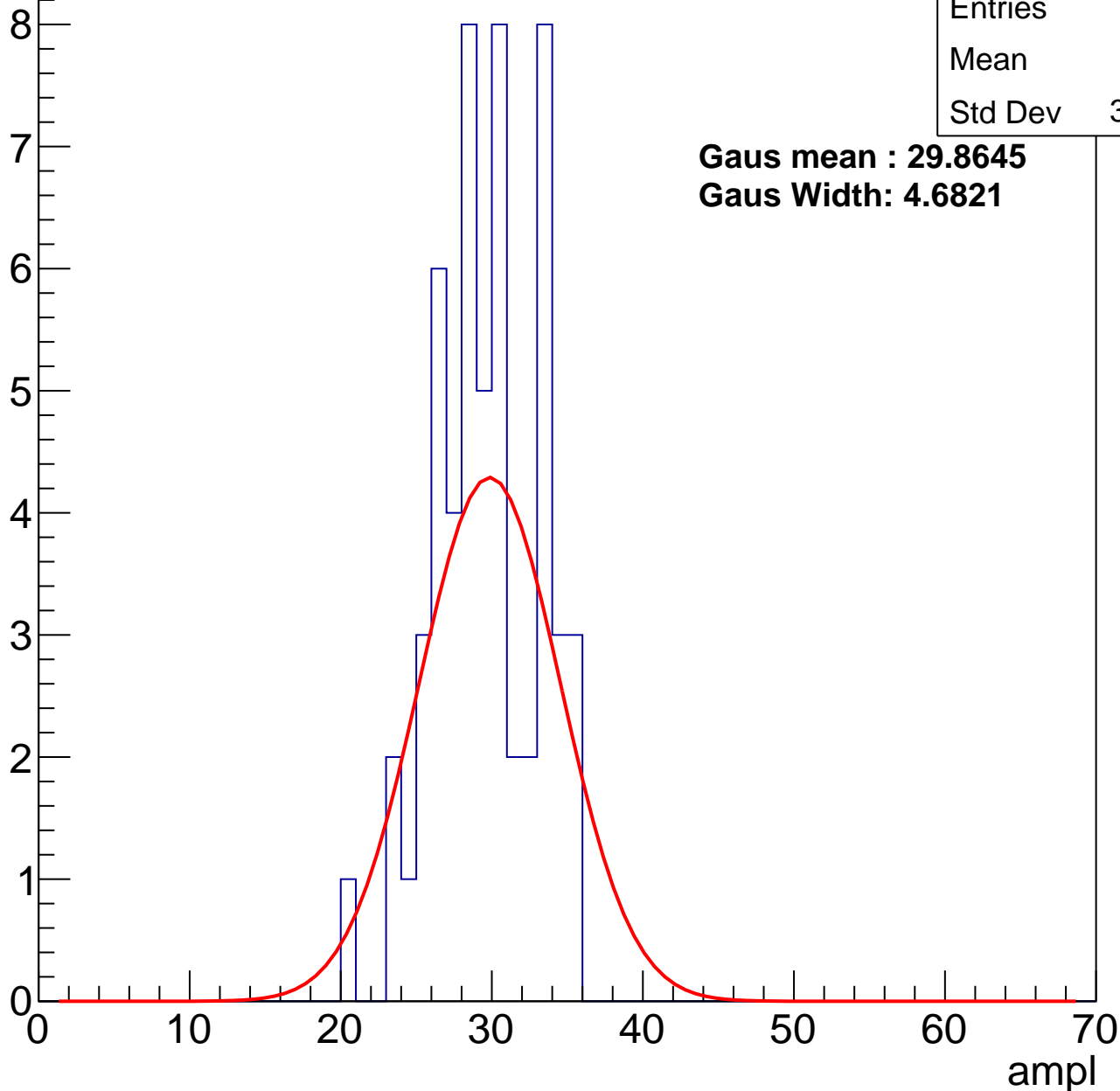
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	29.2
Std Dev	3.399

Gaus mean : 29.8645

Gaus Width: 4.6821



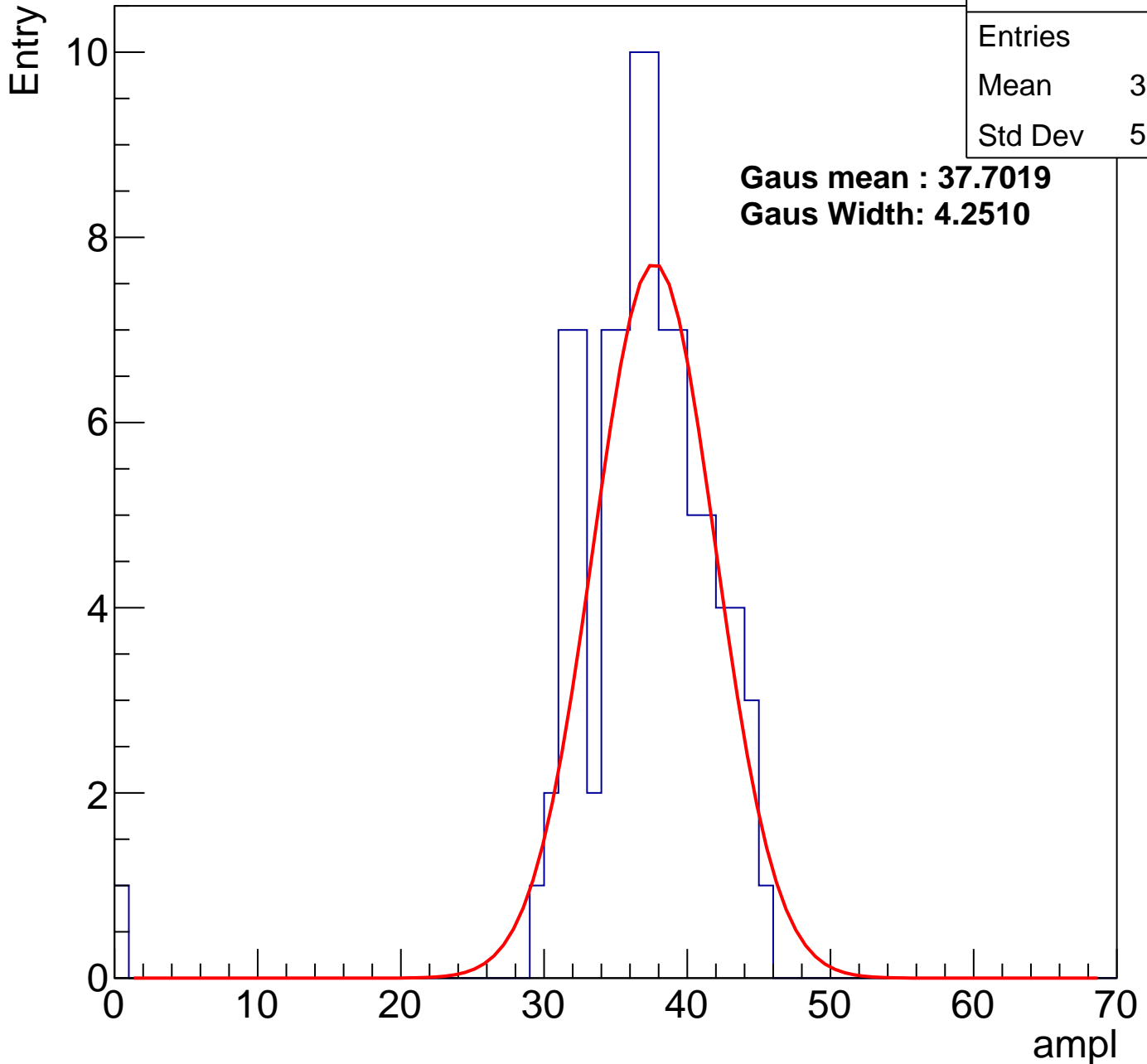
B1L103S, U23-ch21, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	90
Mean	36.33
Std Dev	5.443

Gaus mean : 37.7019

Gaus Width: 4.2510



B1L103S, U23-ch21, adc2

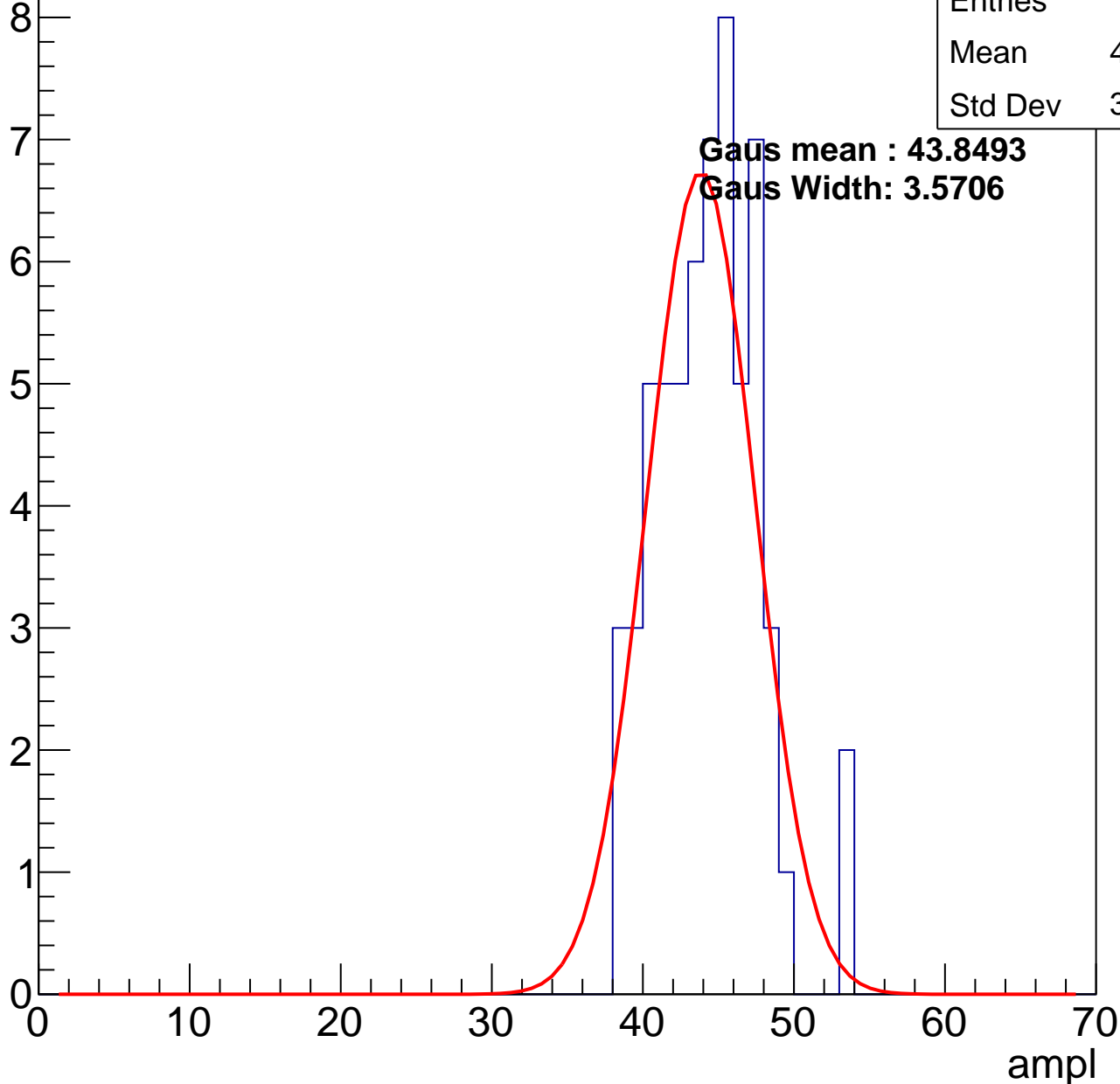
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.83
Std Dev	3.312

Gaus mean : 43.8493

Gaus Width: 3.5706

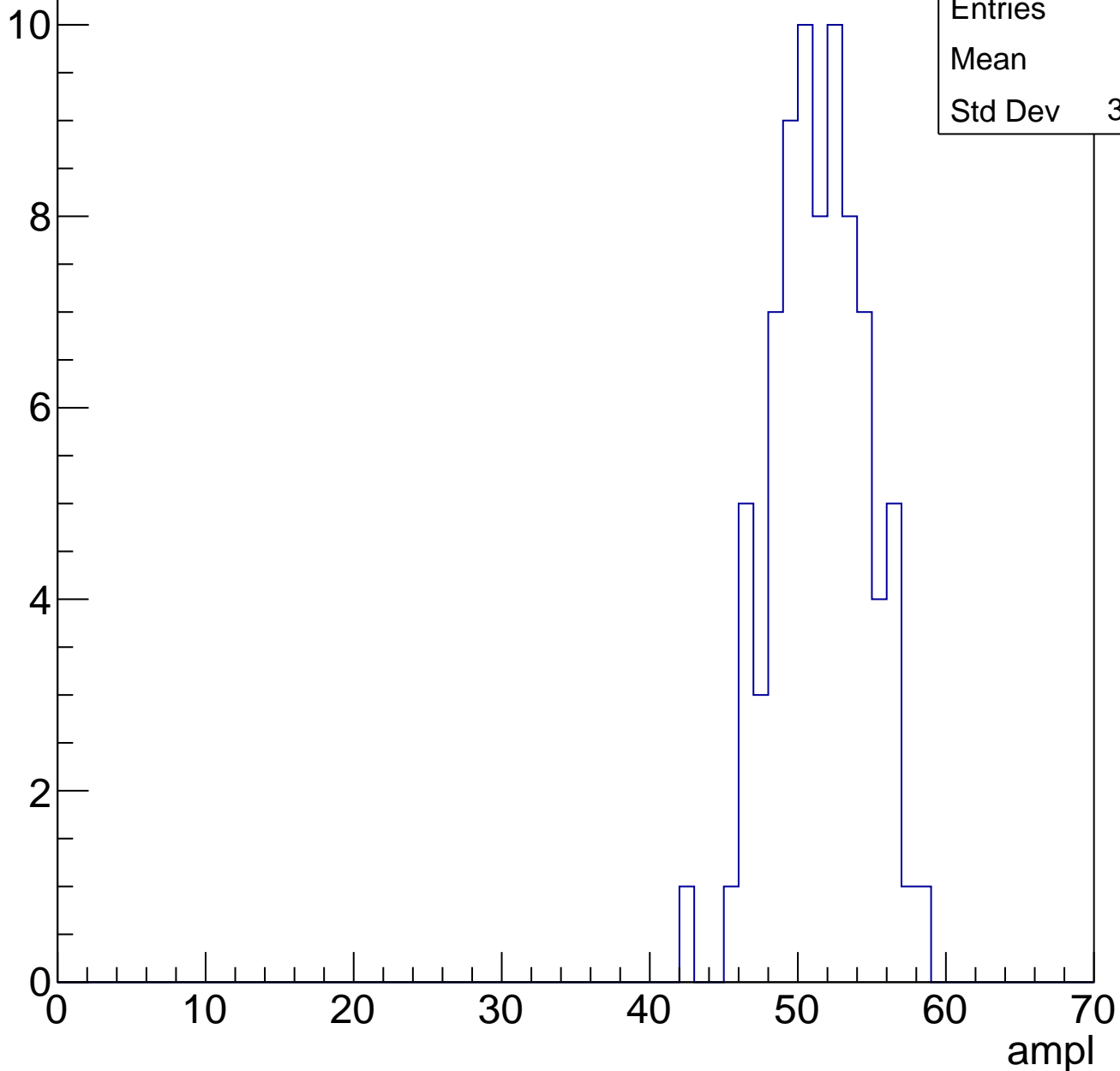


B1L103S, U23-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	51
Std Dev	3.118

Entry

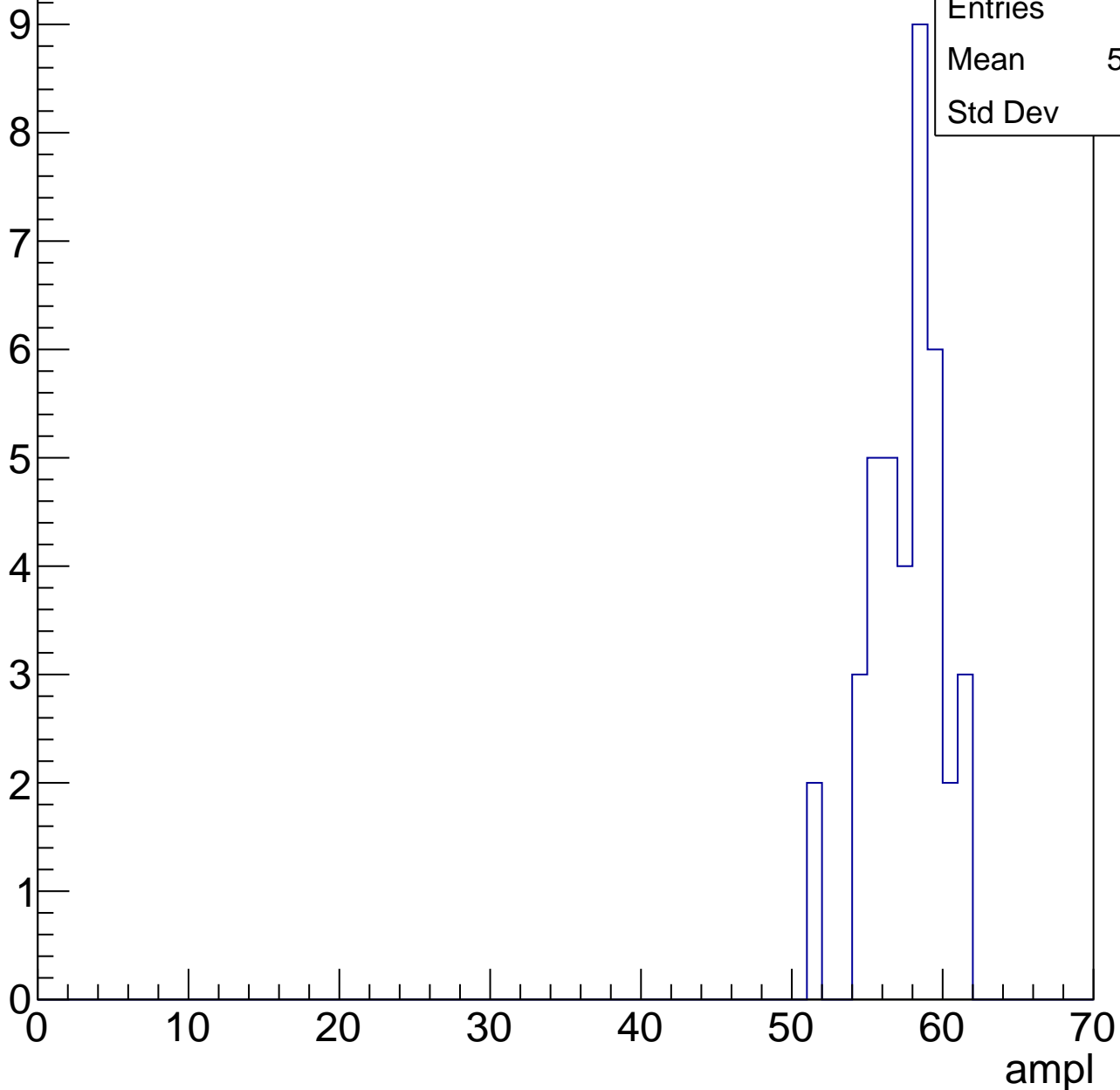


B1L103S, U23-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	57.08
Std Dev	2.39

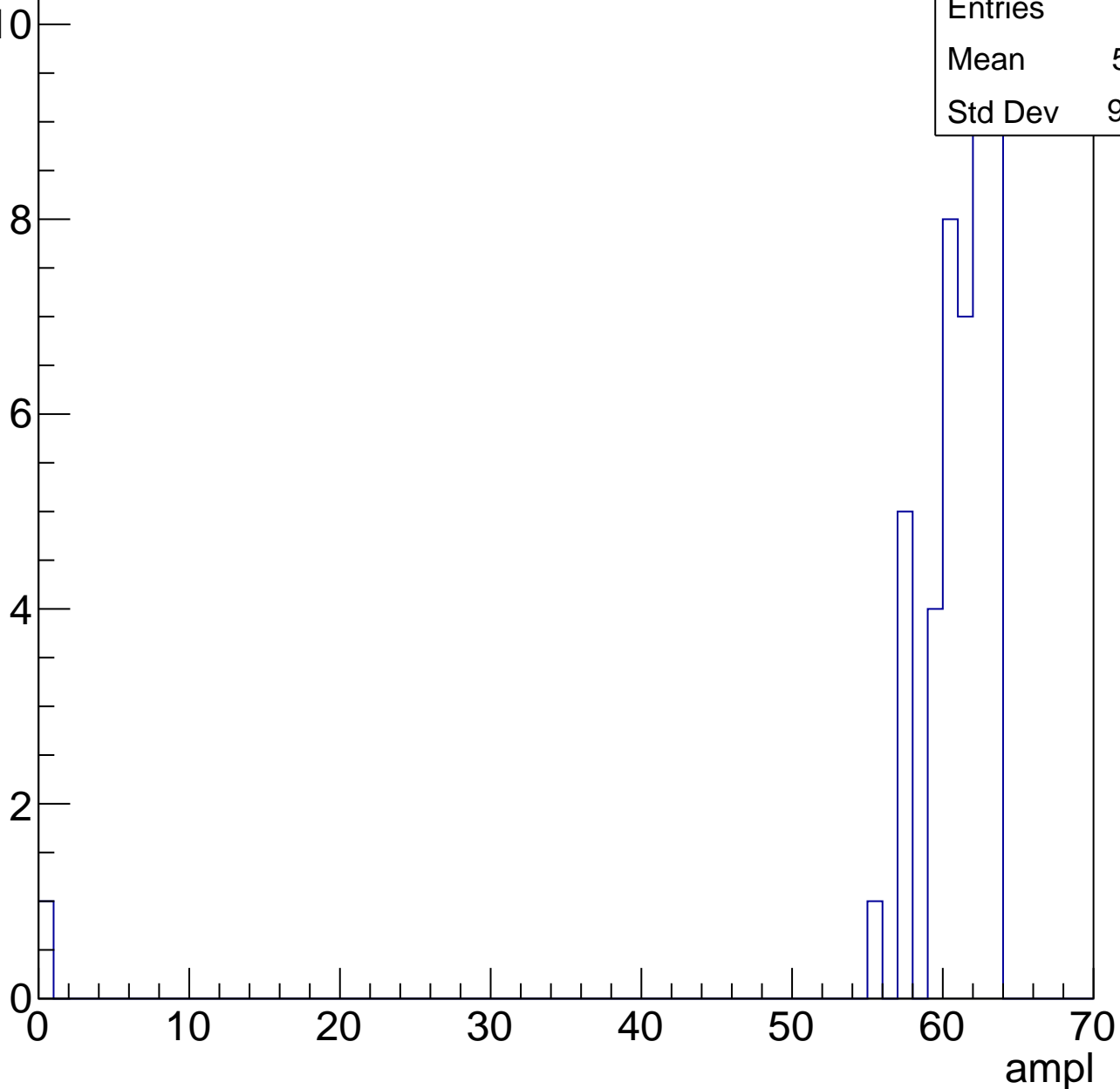


B1L103S, U23-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	59.41
Std Dev	9.083



B1L103S, U23-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch22, adc0

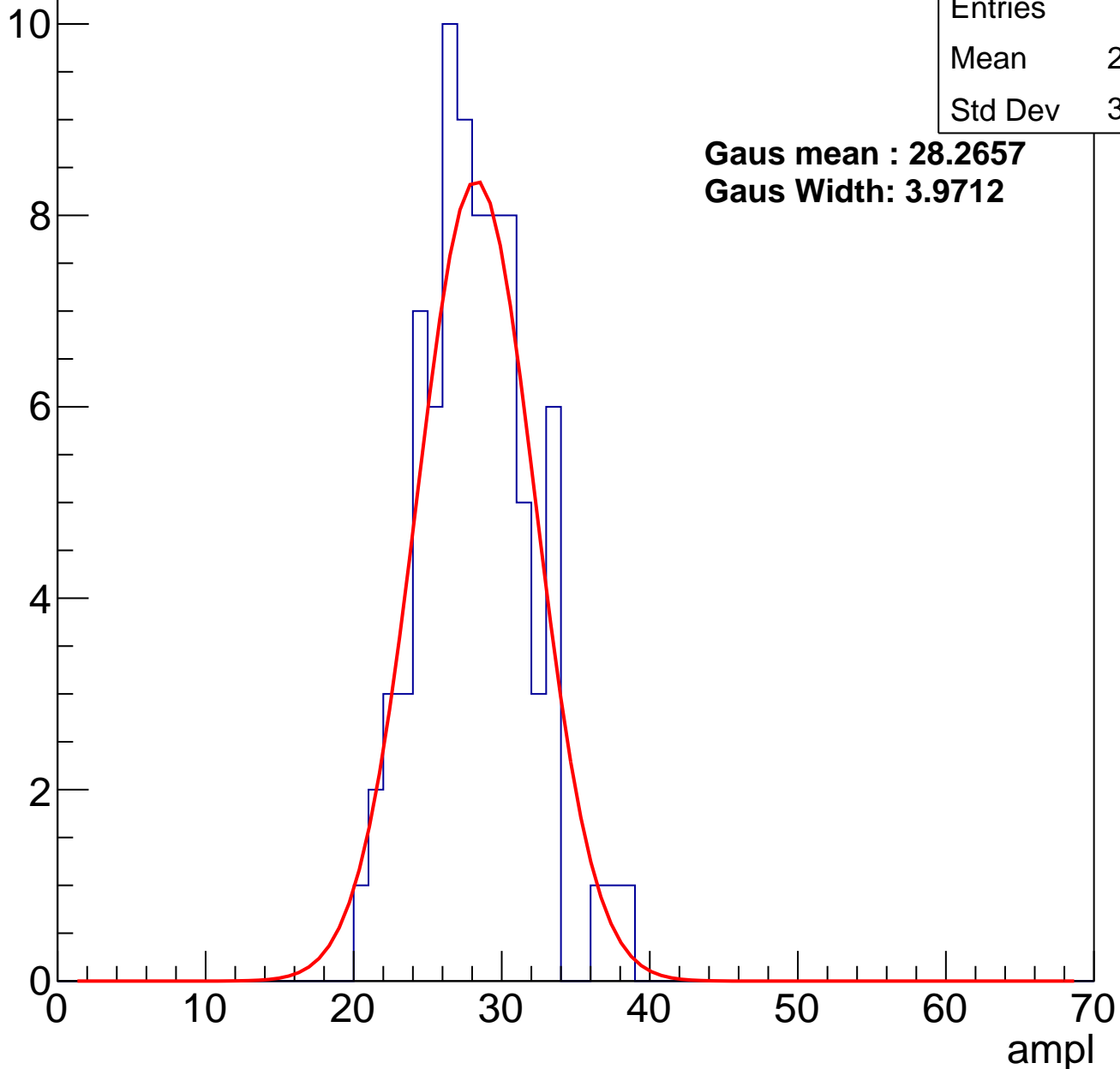
calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	27.73
Std Dev	3.633

Gaus mean : 28.2657

Gaus Width: 3.9712

Entry



B1L103S, U23-ch22, adc1

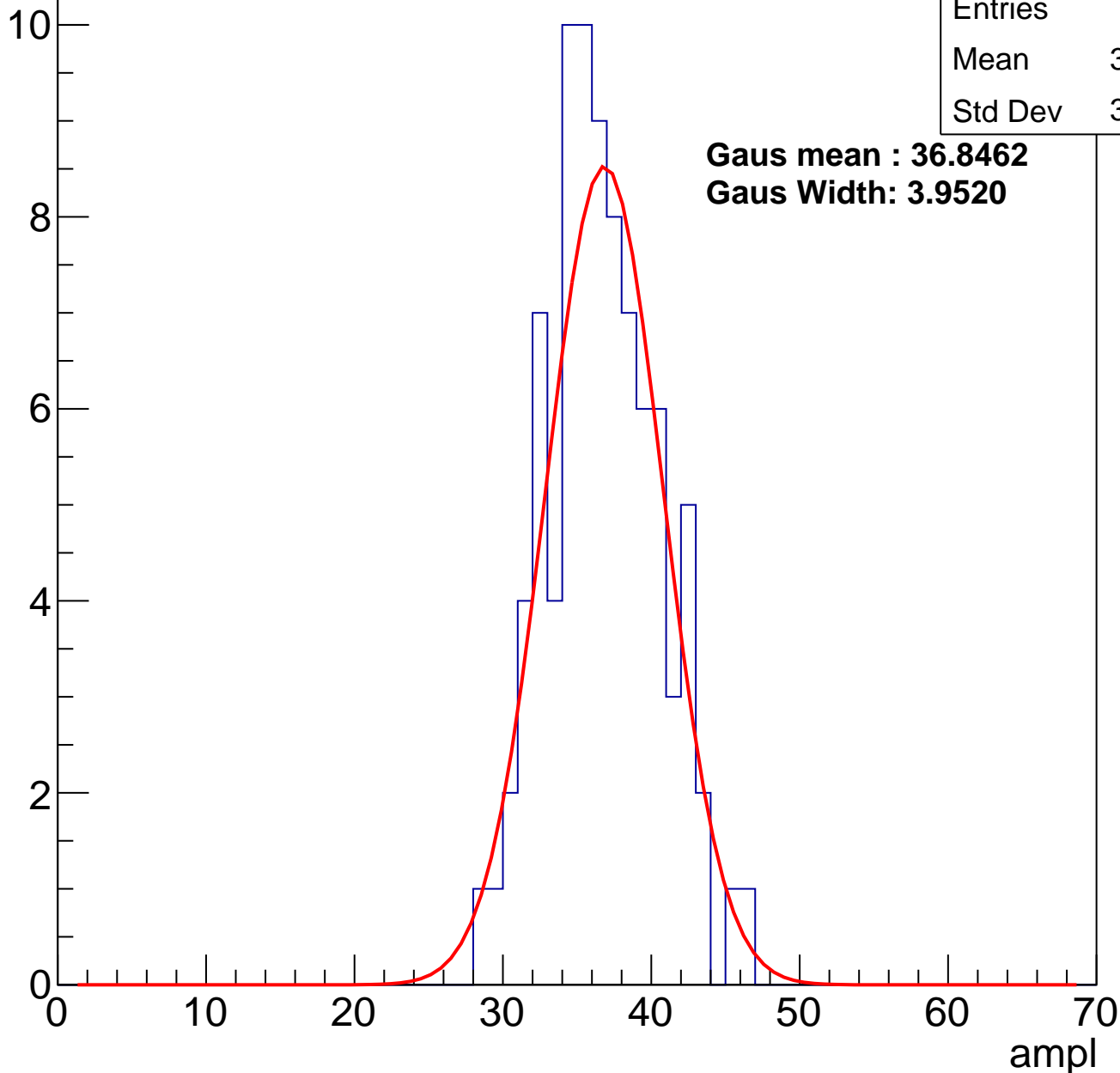
calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	36.29
Std Dev	3.707

Gaus mean : 36.8462

Gaus Width: 3.9520

Entry



B1L103S, U23-ch22, adc2

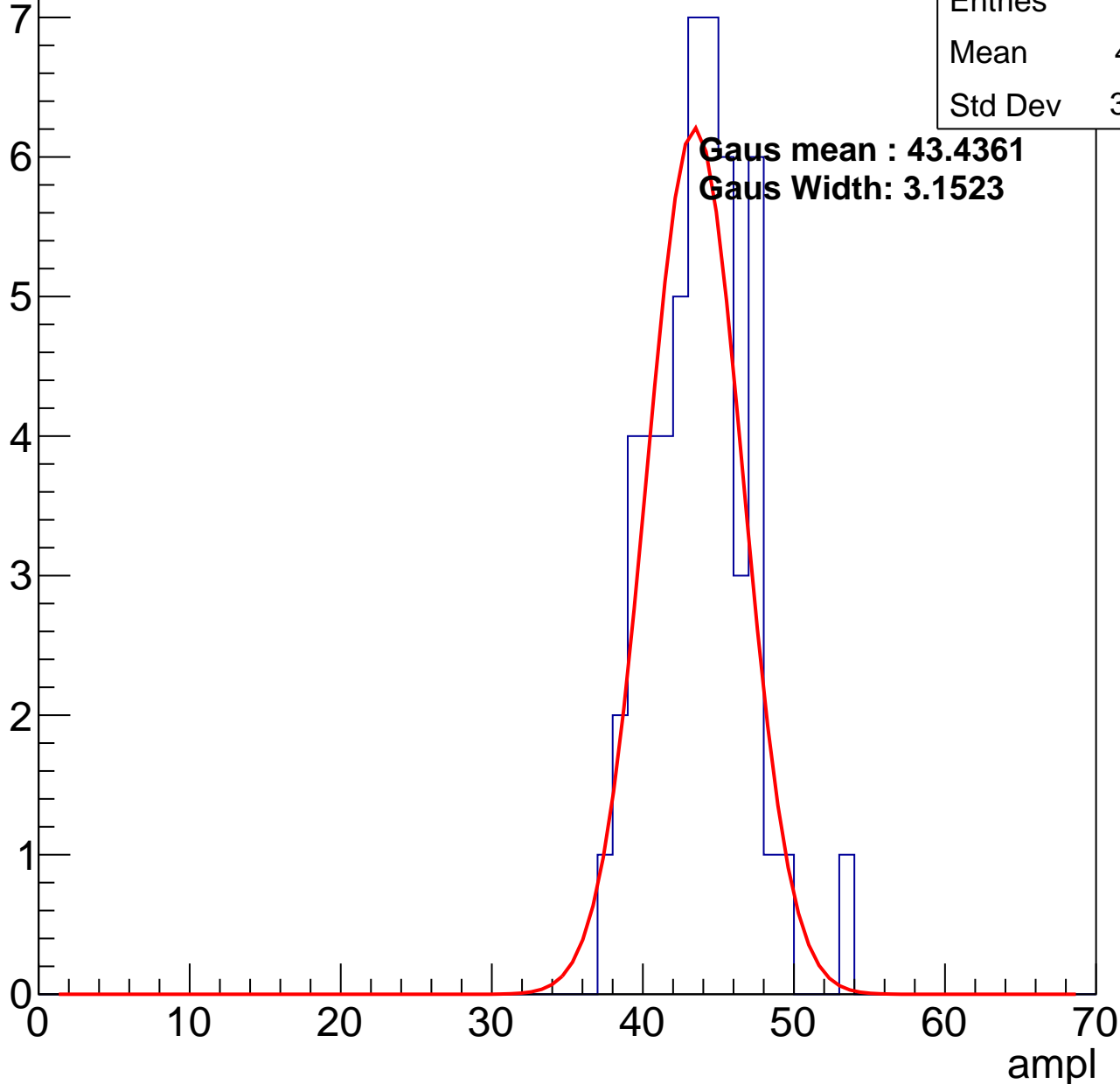
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	43.31
Std Dev	3.153

Gaus mean : 43.4361

Gaus Width: 3.1523

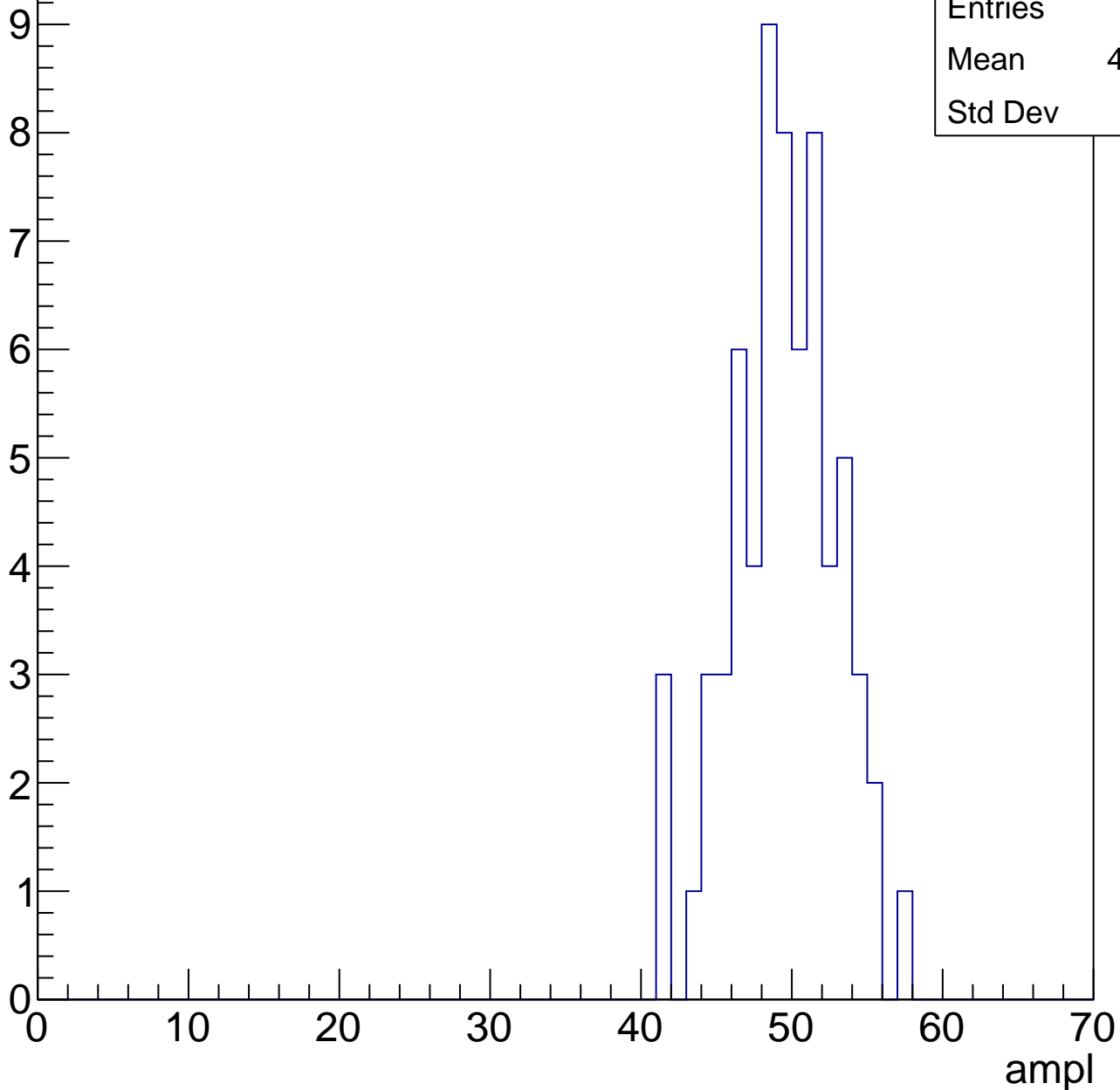


B1L103S, U23-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	48.95
Std Dev	3.47

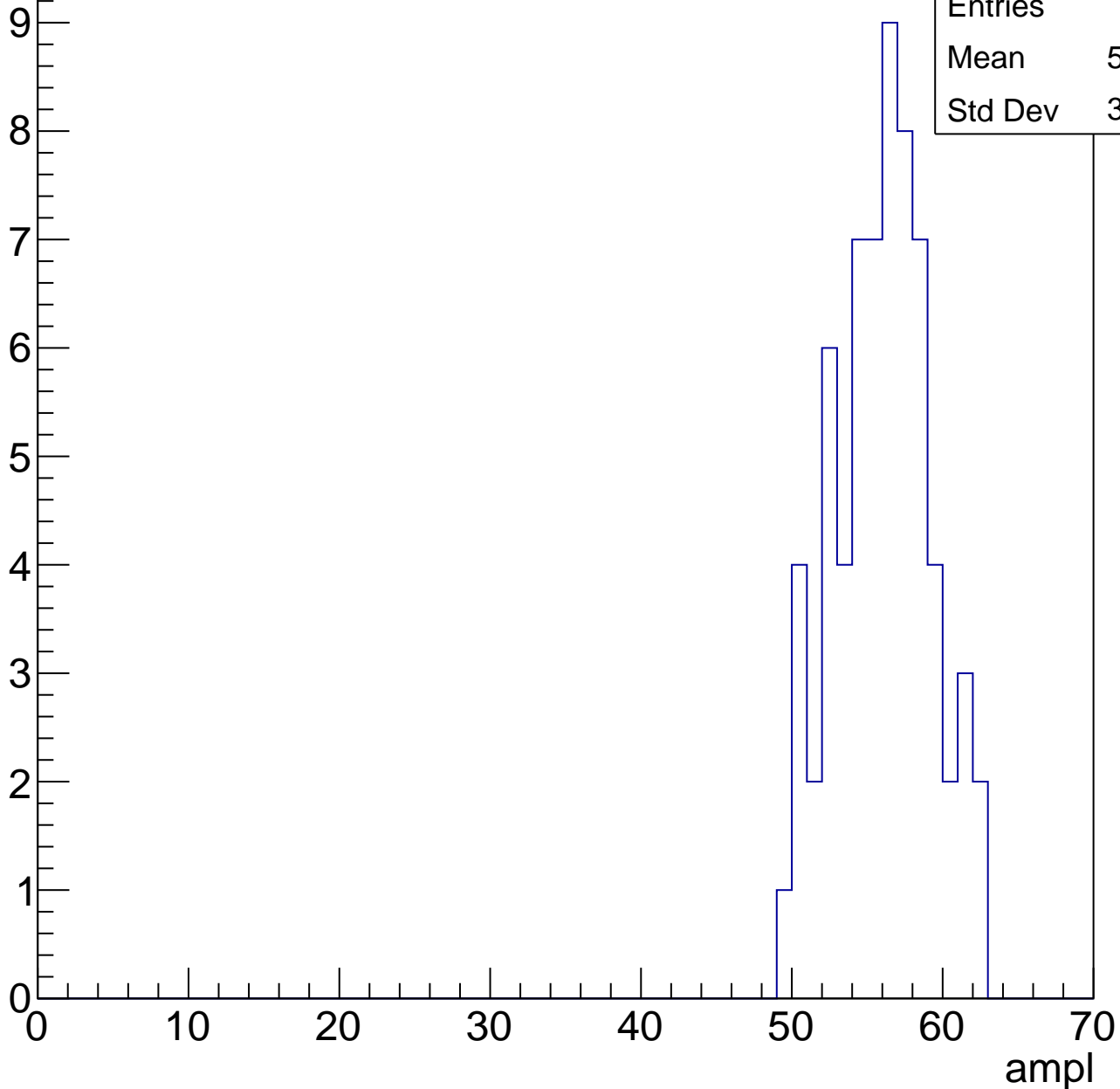


B1L103S, U23-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	55.56
Std Dev	3.134

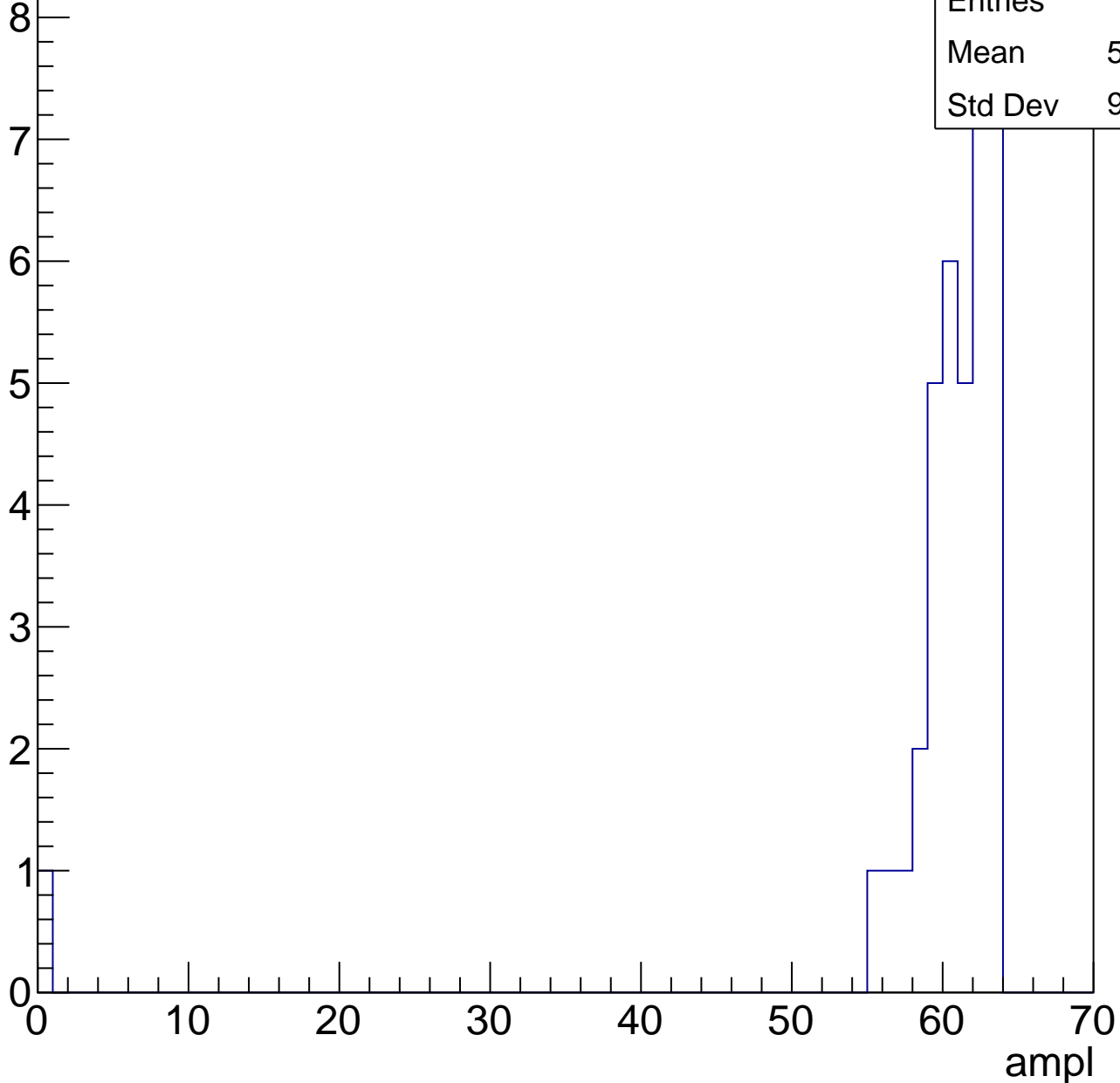


B1L103S, U23-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	59.05
Std Dev	9.918



B1L103S, U23-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

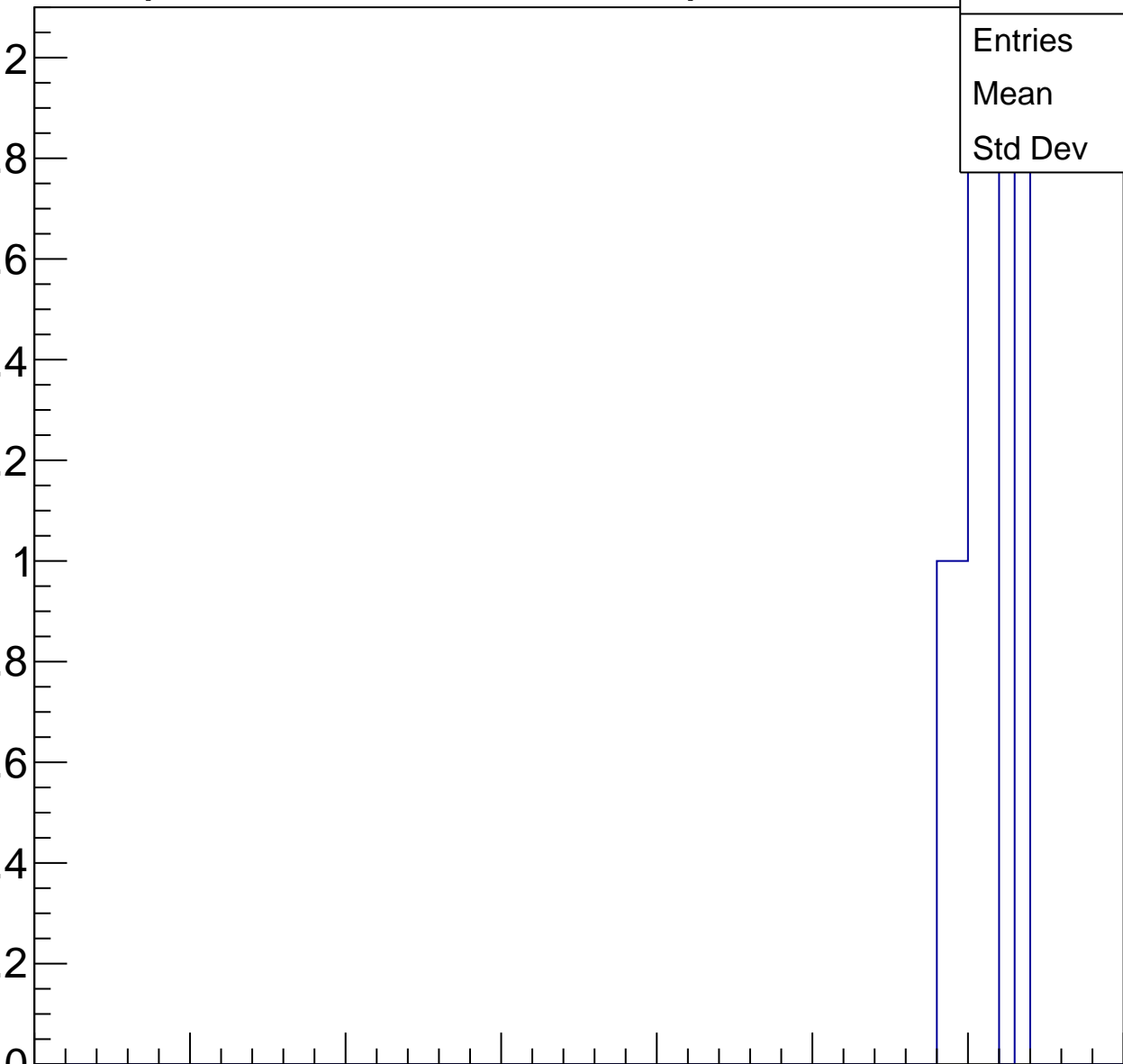
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	60.62
Std Dev	1.654

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L103S, U23-ch23, adc0

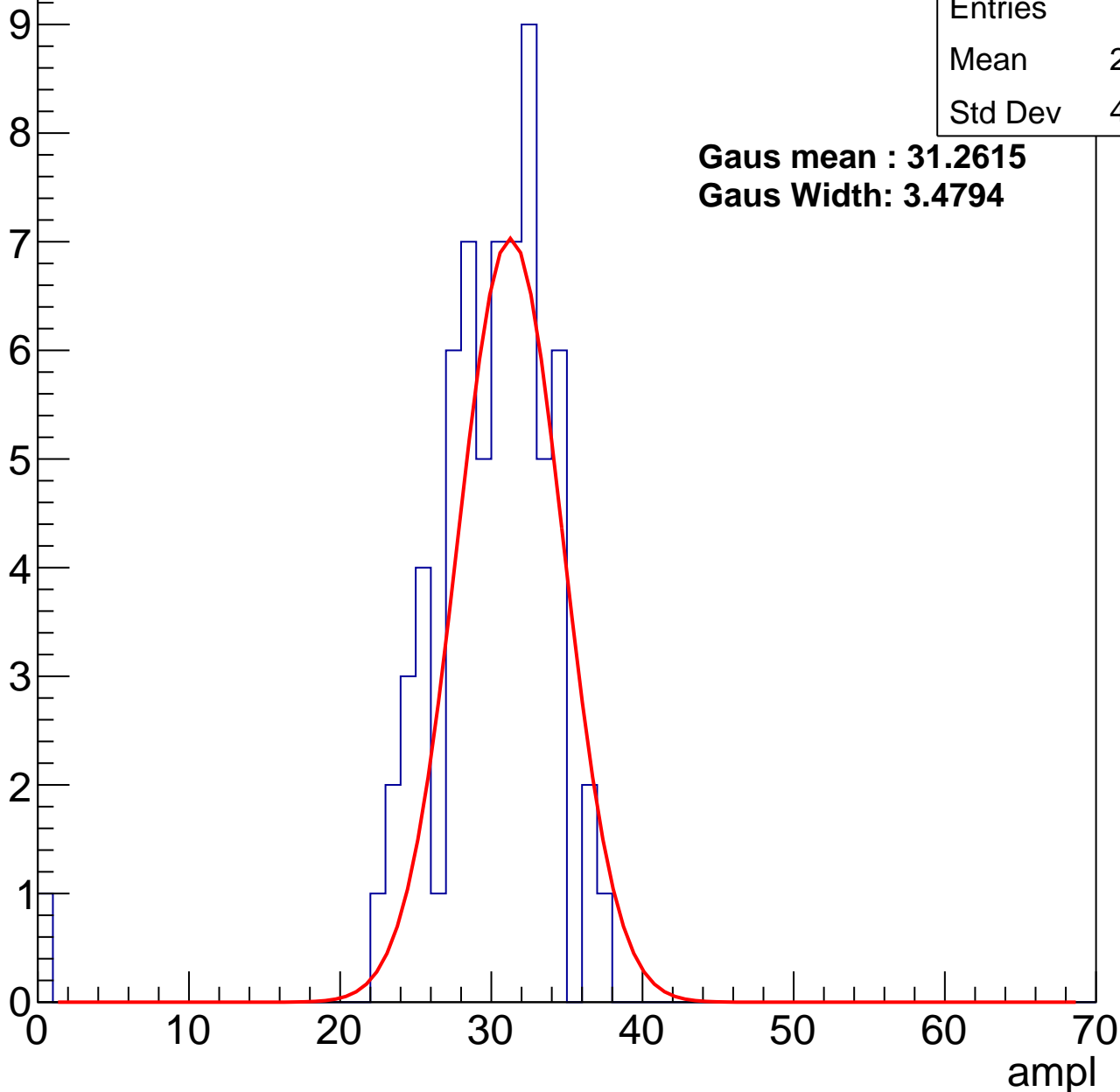
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.28
Std Dev	4.953

Gaus mean : 31.2615

Gaus Width: 3.4794



B1L103S, U23-ch23, adc1

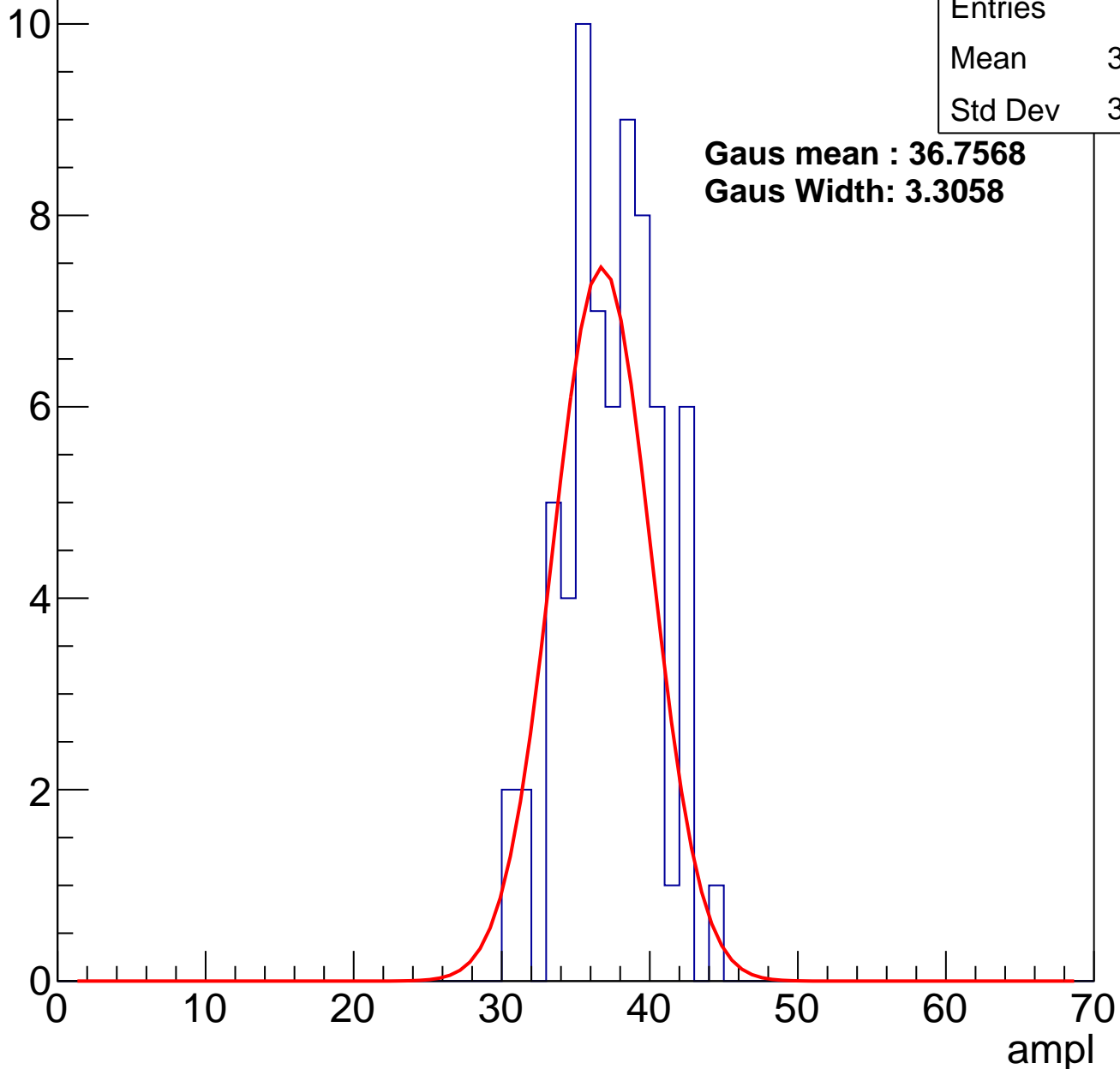
calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	36.99
Std Dev	3.098

Gaus mean : 36.7568

Gaus Width: 3.3058

Entry



B1L103S, U23-ch23, adc2

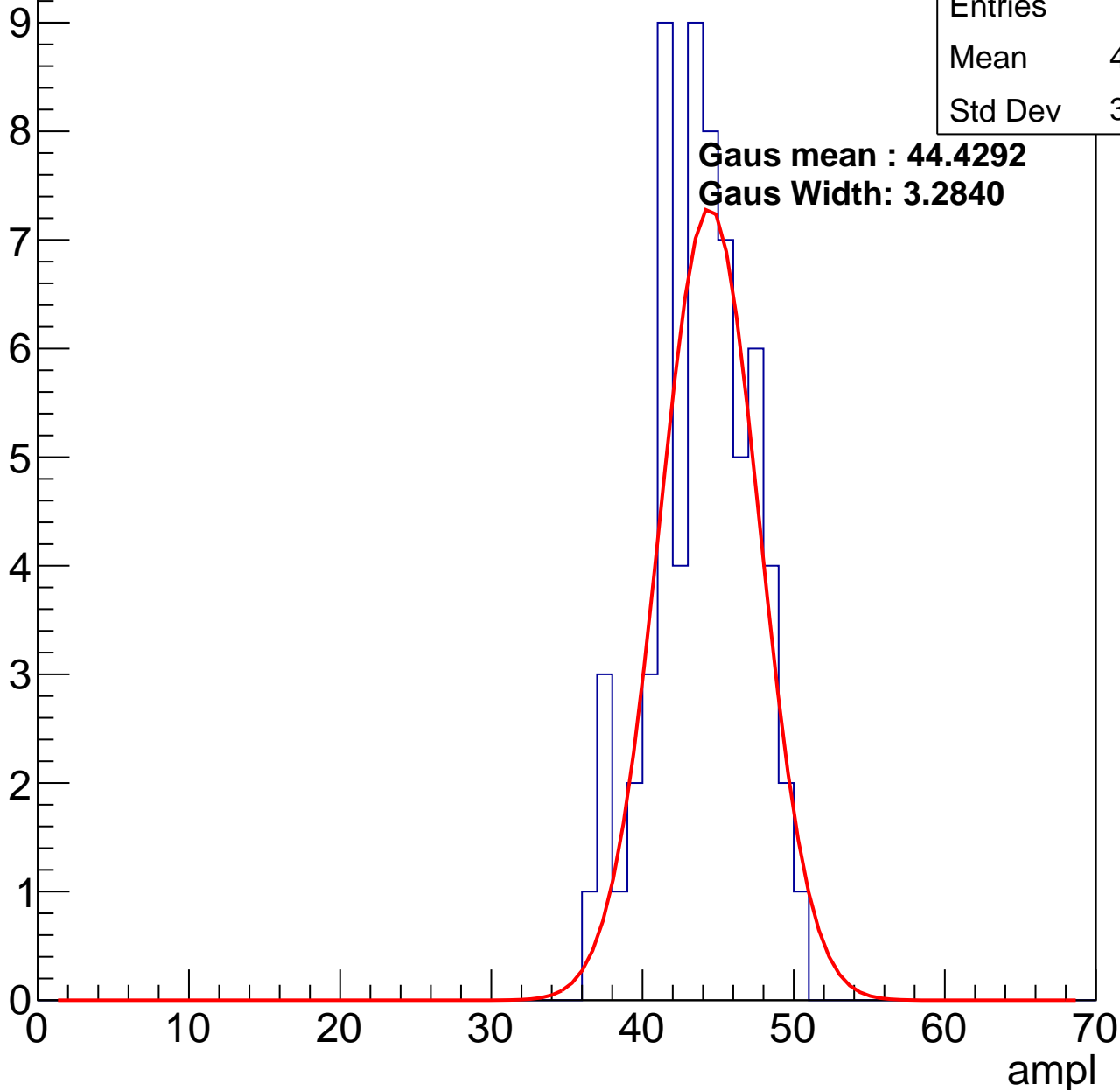
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	43.48
Std Dev	3.197

Gaus mean : 44.4292

Gaus Width: 3.2840

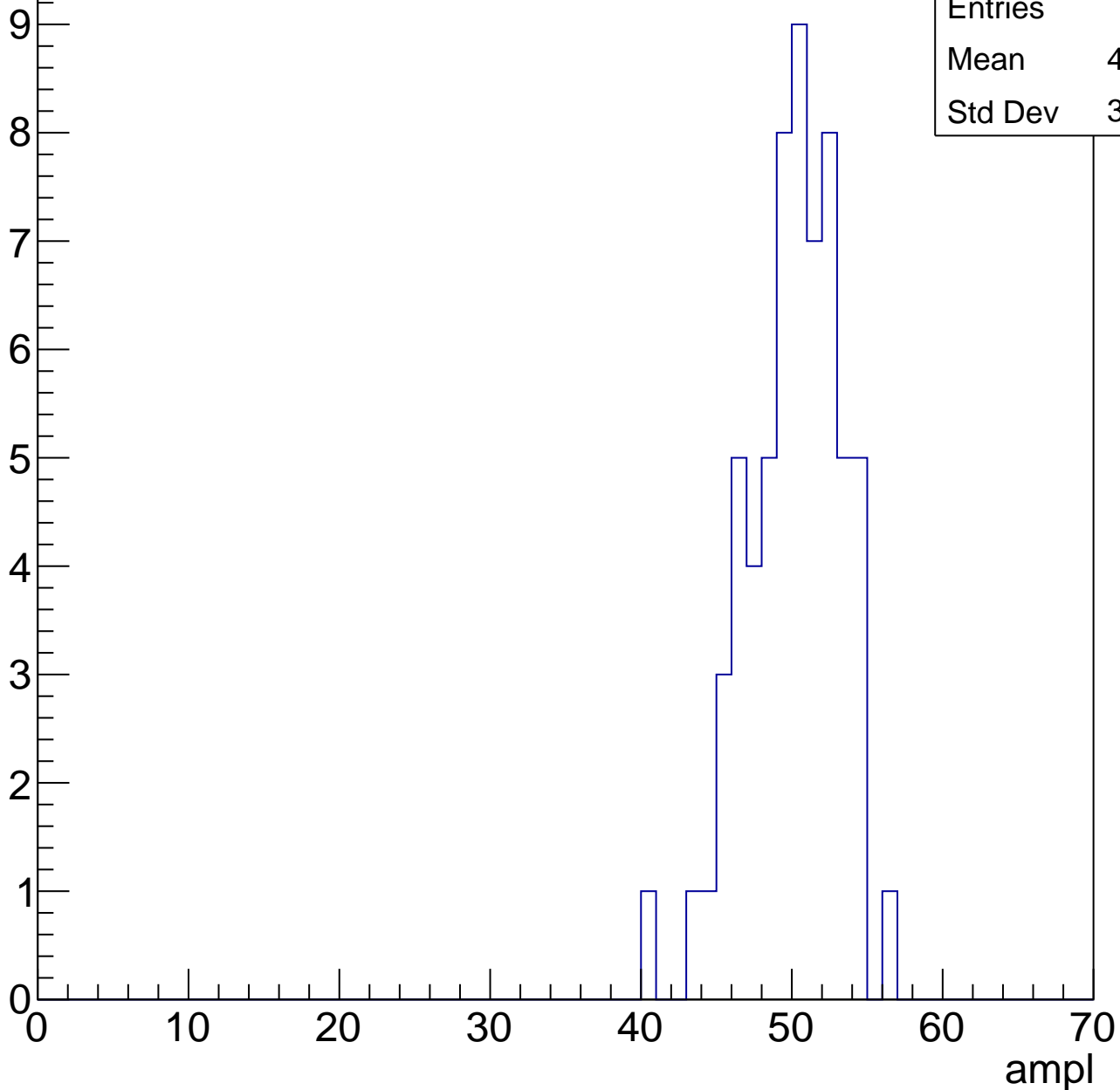


B1L103S, U23-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.62
Std Dev	3.073

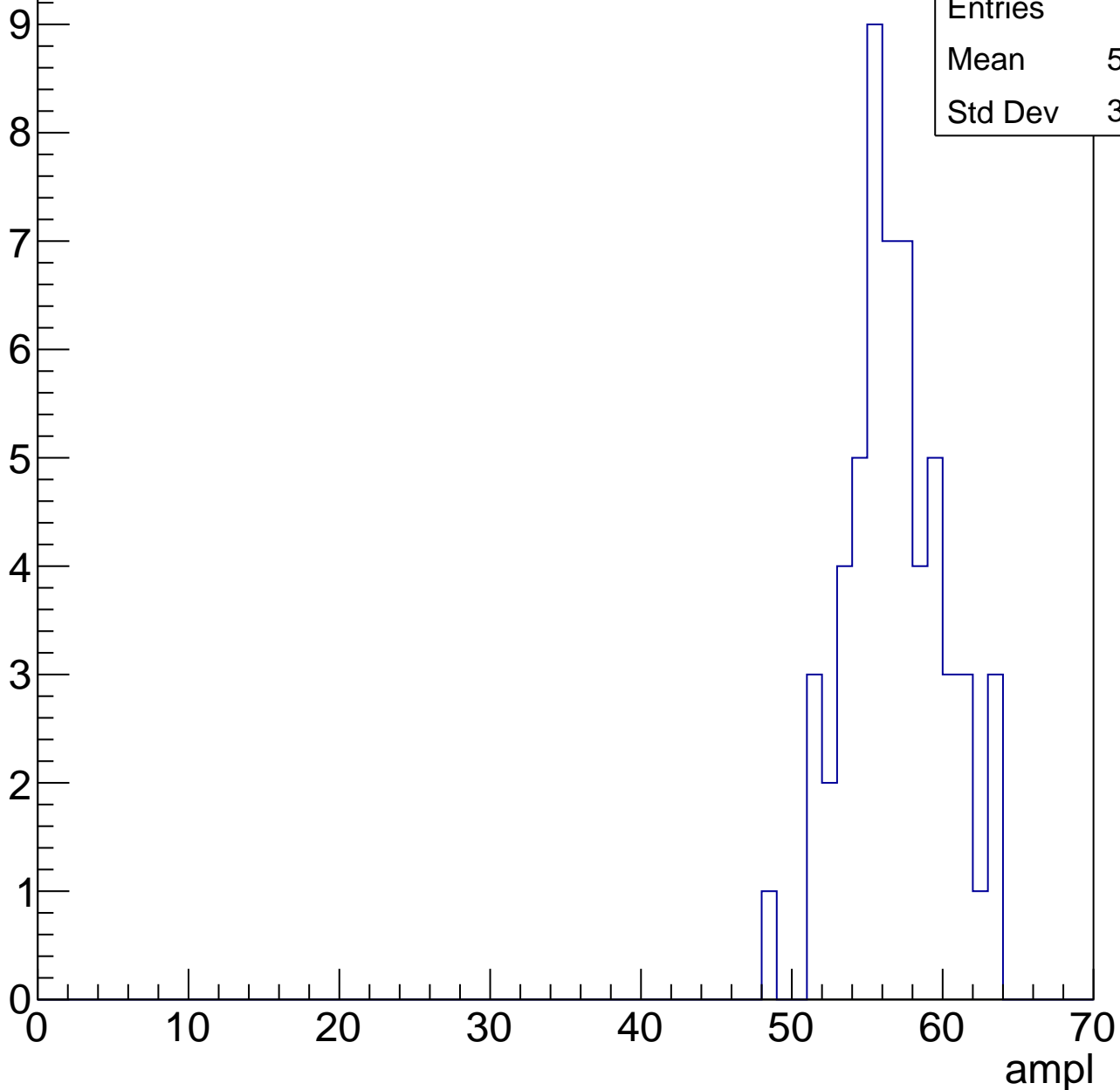


B1L103S, U23-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	56.39
Std Dev	3.249

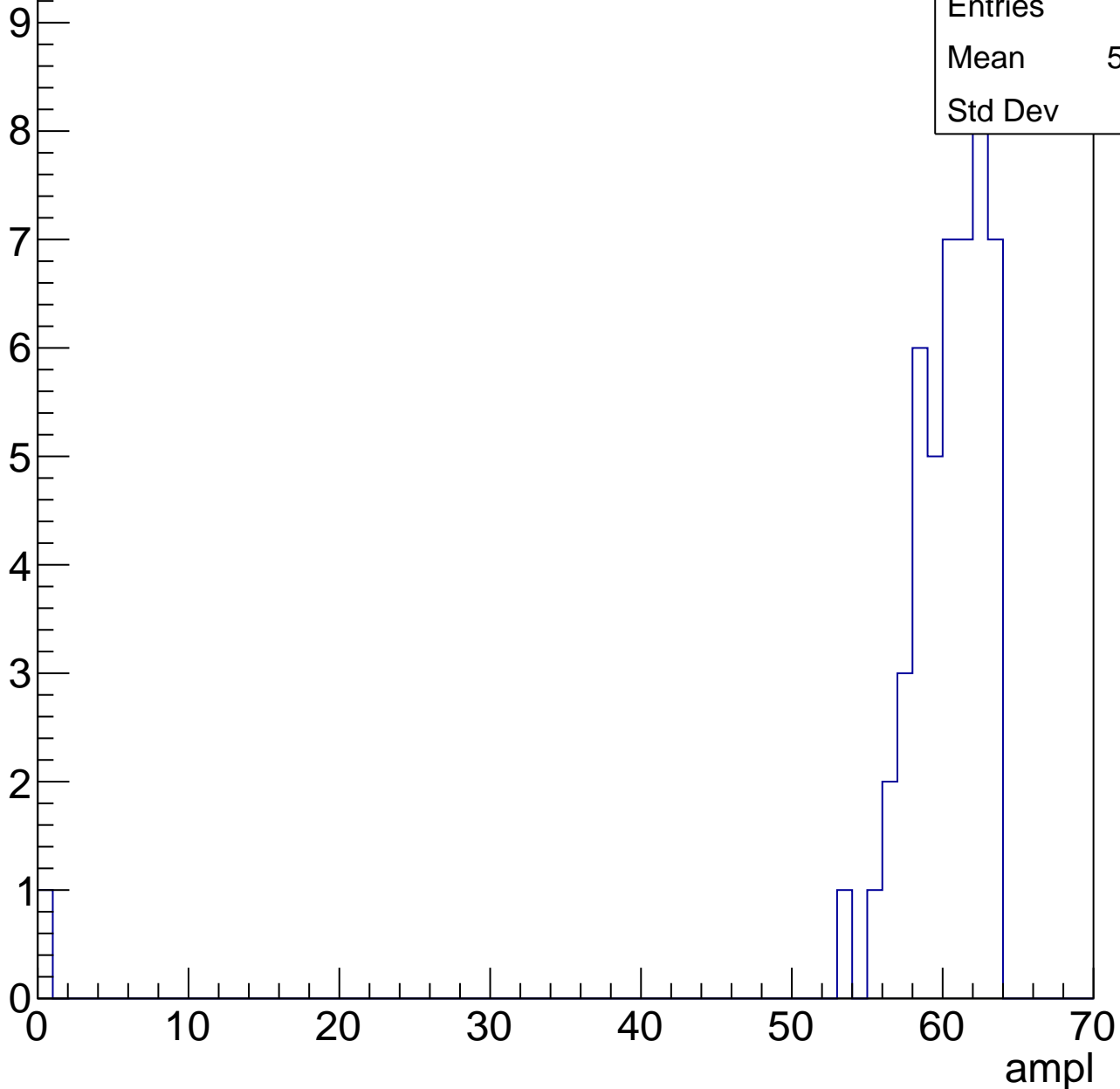


B1L103S, U23-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.78
Std Dev	8.8



B1L103S, U23-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U23-ch24, adc0

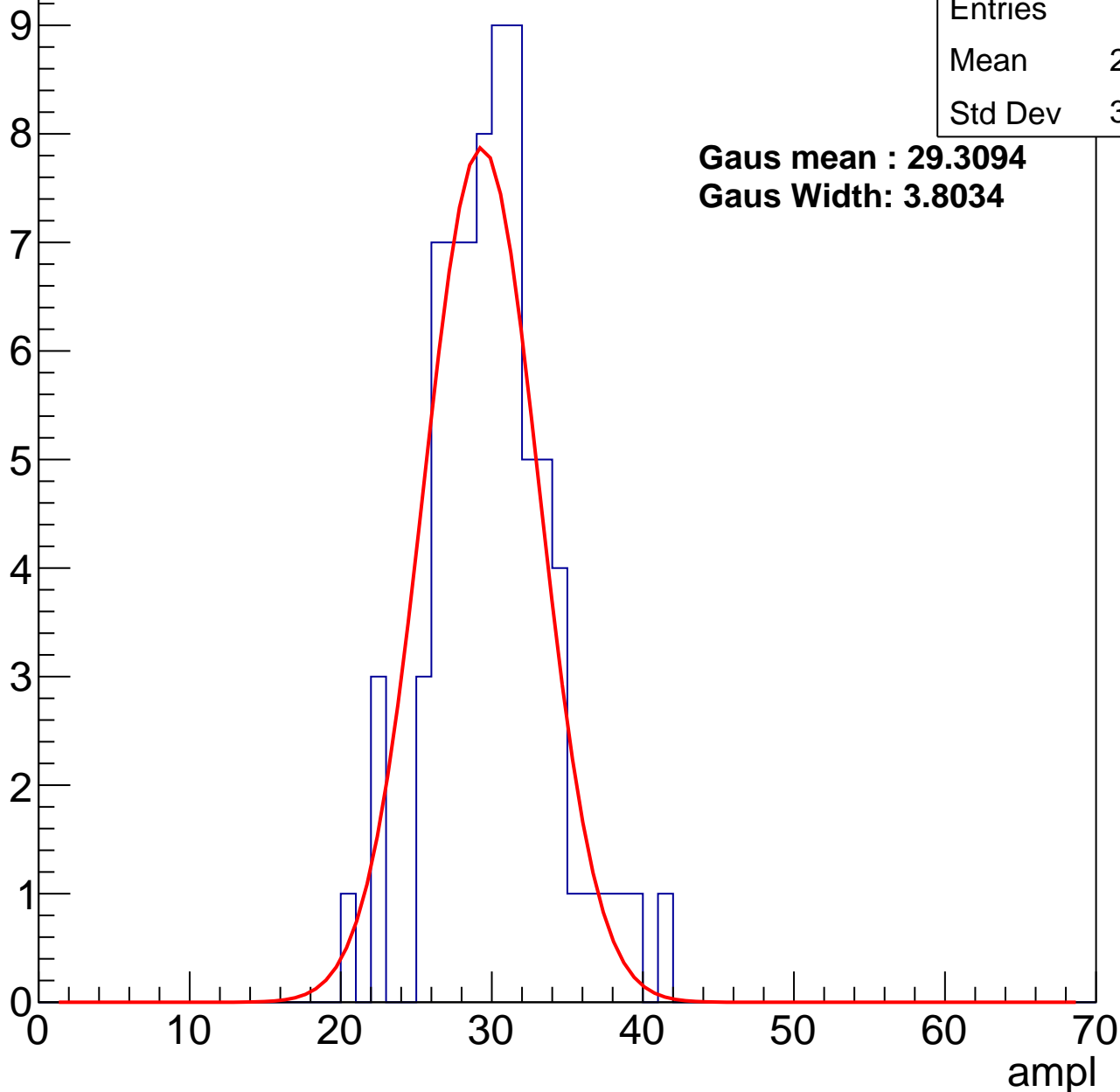
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	29.68
Std Dev	3.828

Gaus mean : 29.3094

Gaus Width: 3.8034



B1L103S, U23-ch24, adc1

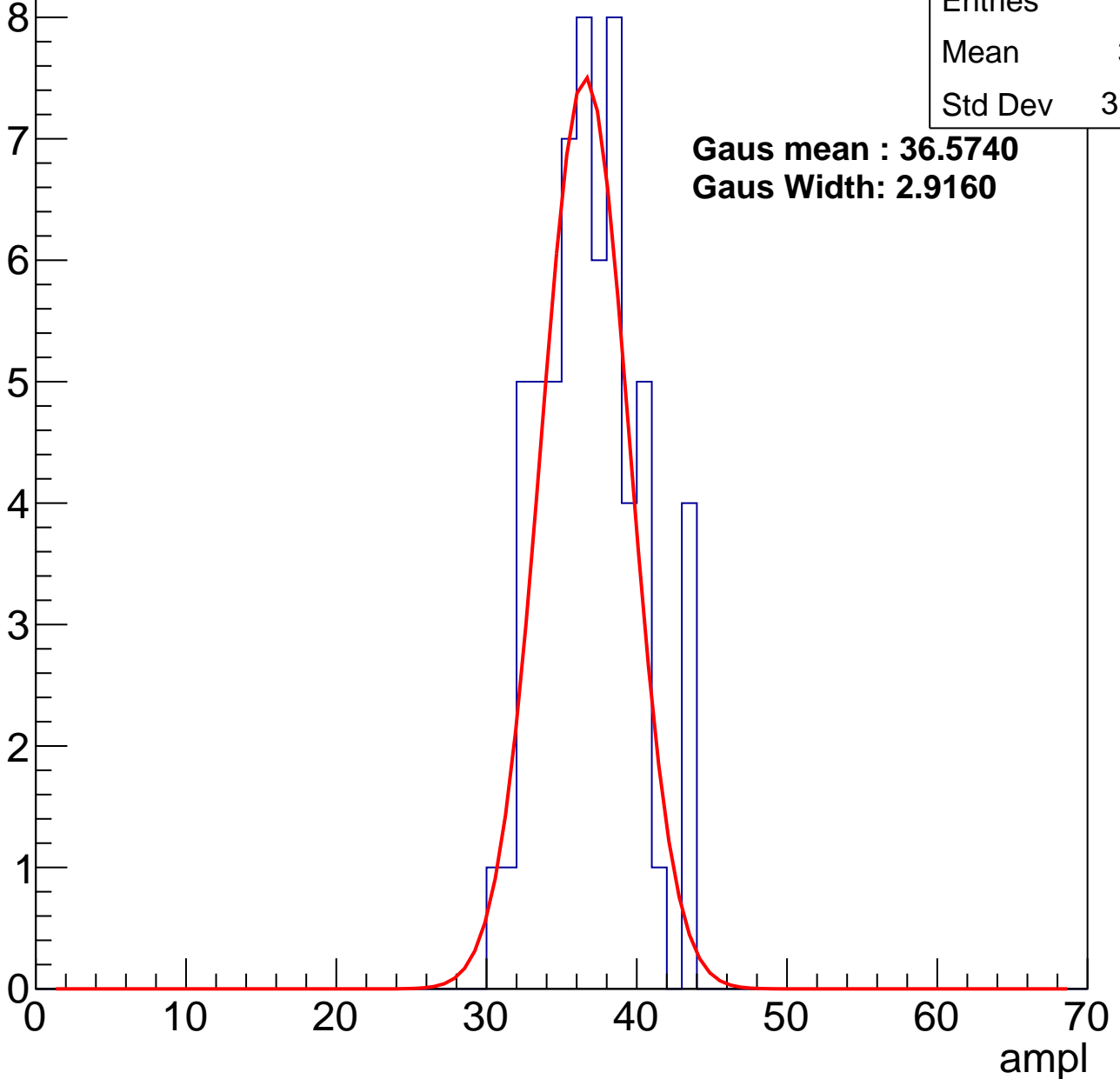
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.4
Std Dev	3.105

Gaus mean : 36.5740

Gaus Width: 2.9160



B1L103S, U23-ch24, adc2

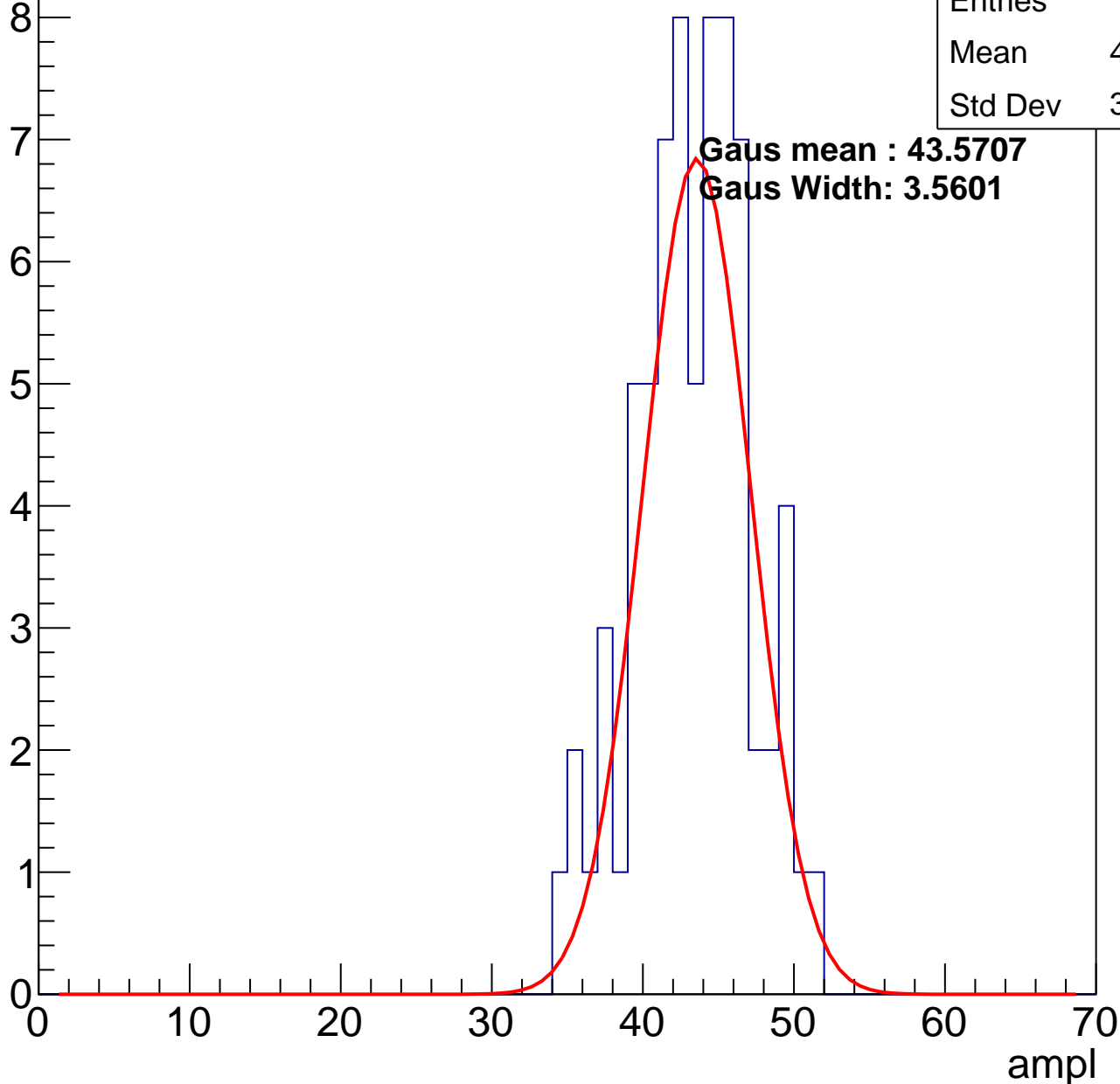
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.86
Std Dev	3.739

Gaus mean : 43.5707

Gaus Width: 3.5601

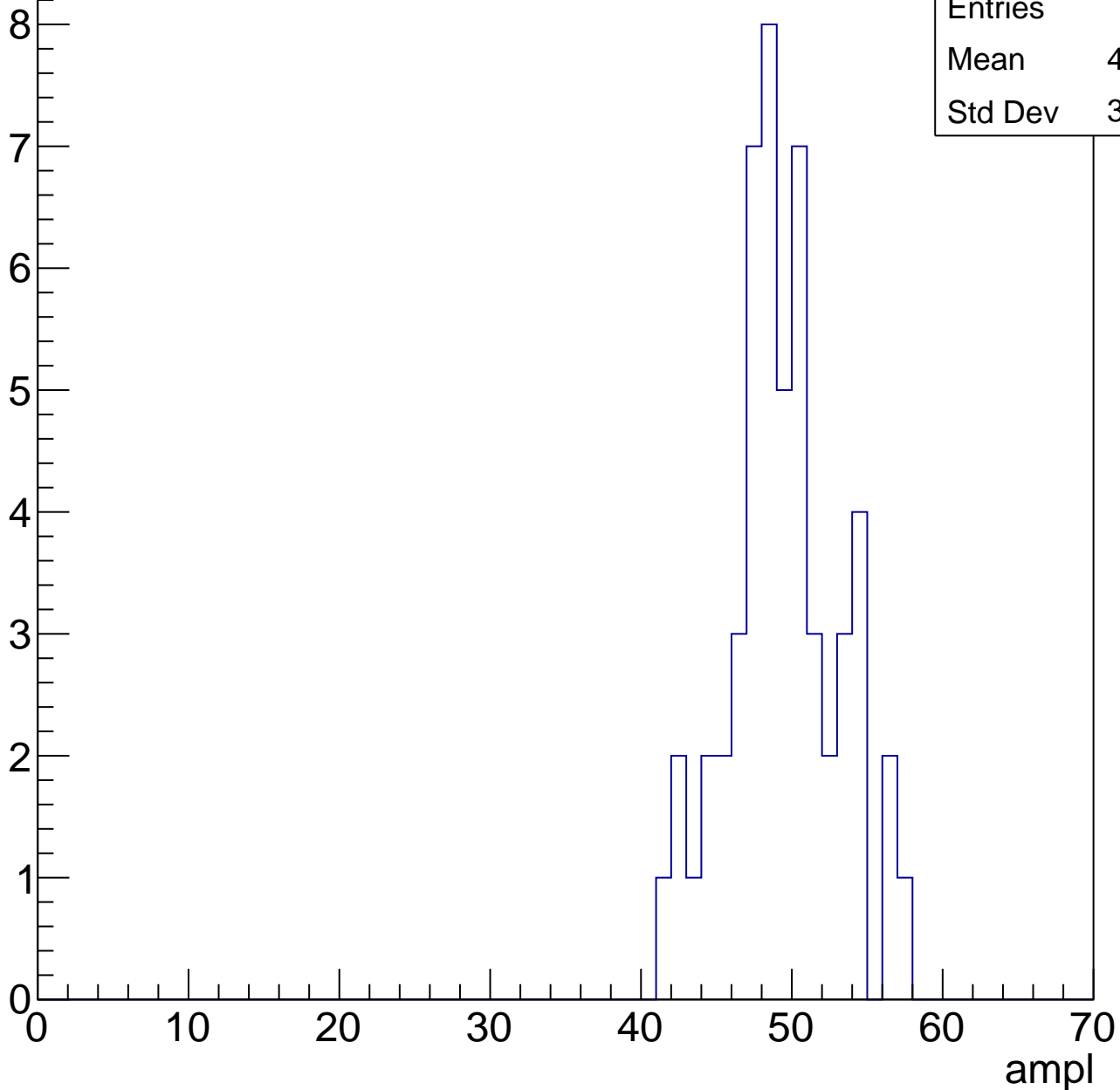


B1L103S, U23-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.92
Std Dev	3.607

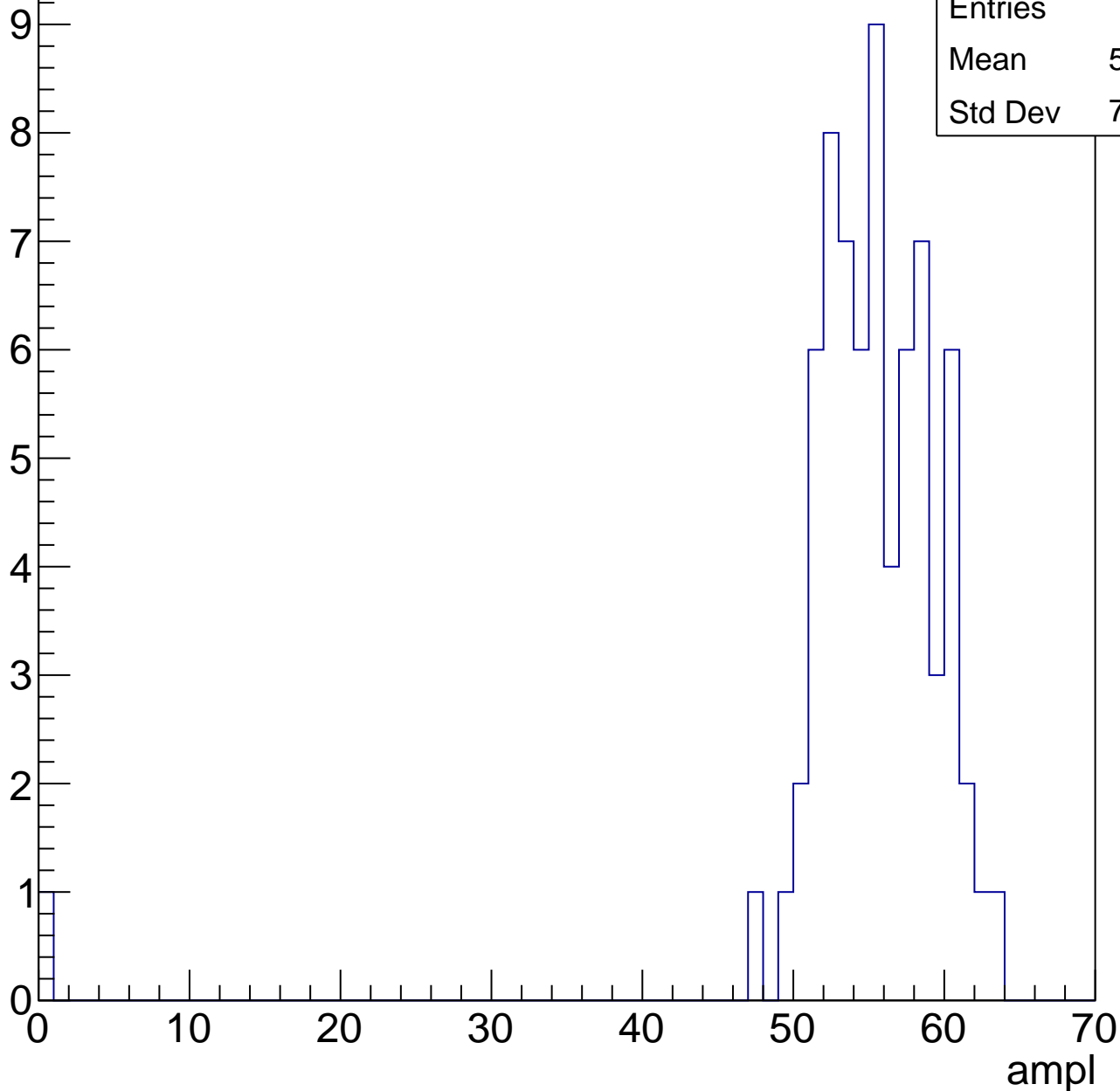


B1L103S, U23-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	54.42
Std Dev	7.342

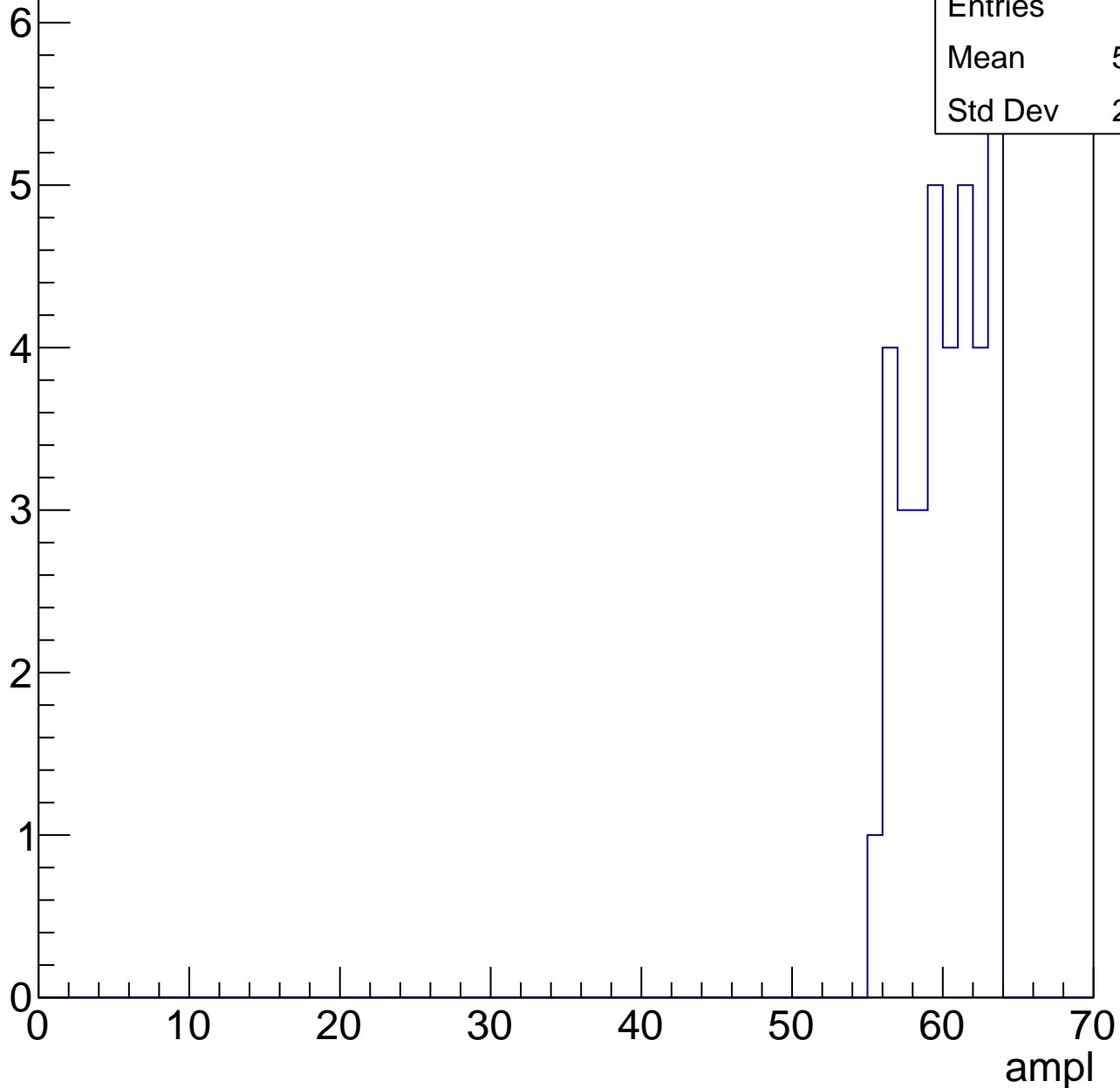


B1L103S, U23-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	59.71
Std Dev	2.421

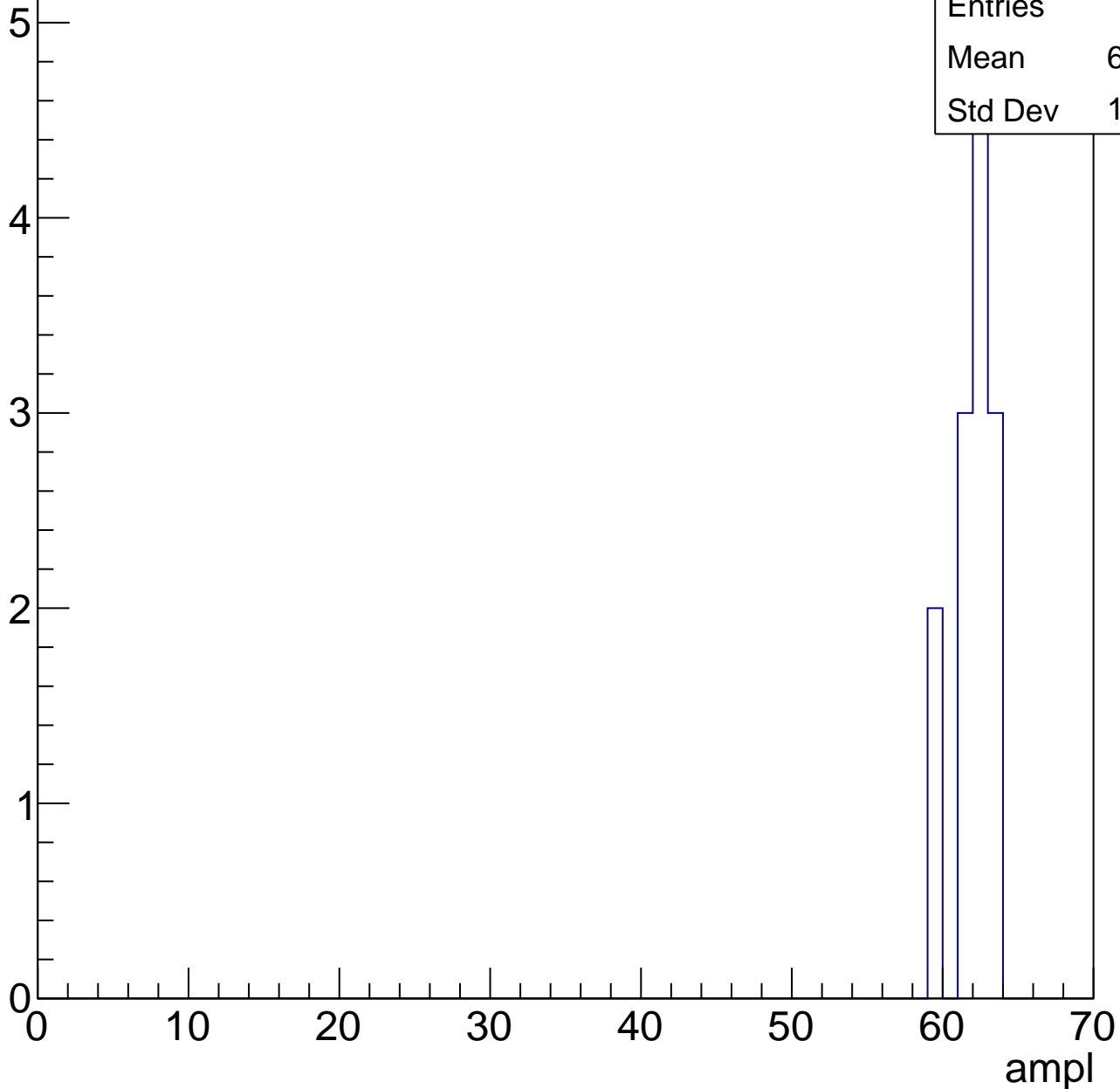


B1L103S, U23-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	61.54
Std Dev	1.278



B1L103S, U23-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch25, adc0

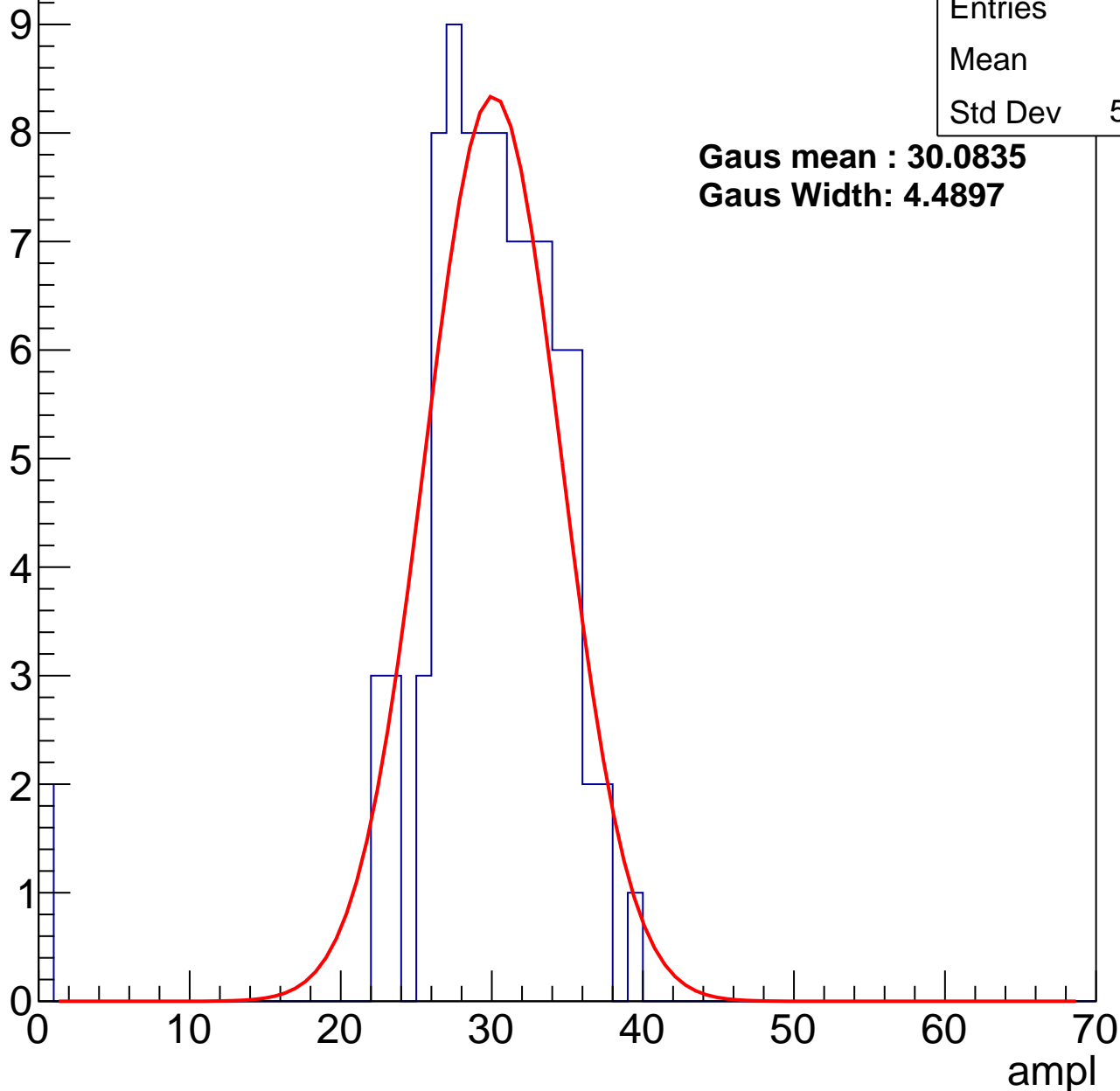
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	29.2
Std Dev	5.774

Gaus mean : 30.0835

Gaus Width: 4.4897



B1L103S, U23-ch25, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	63
Mean	36.6
Std Dev	3.089

Gaus mean : 37.2477

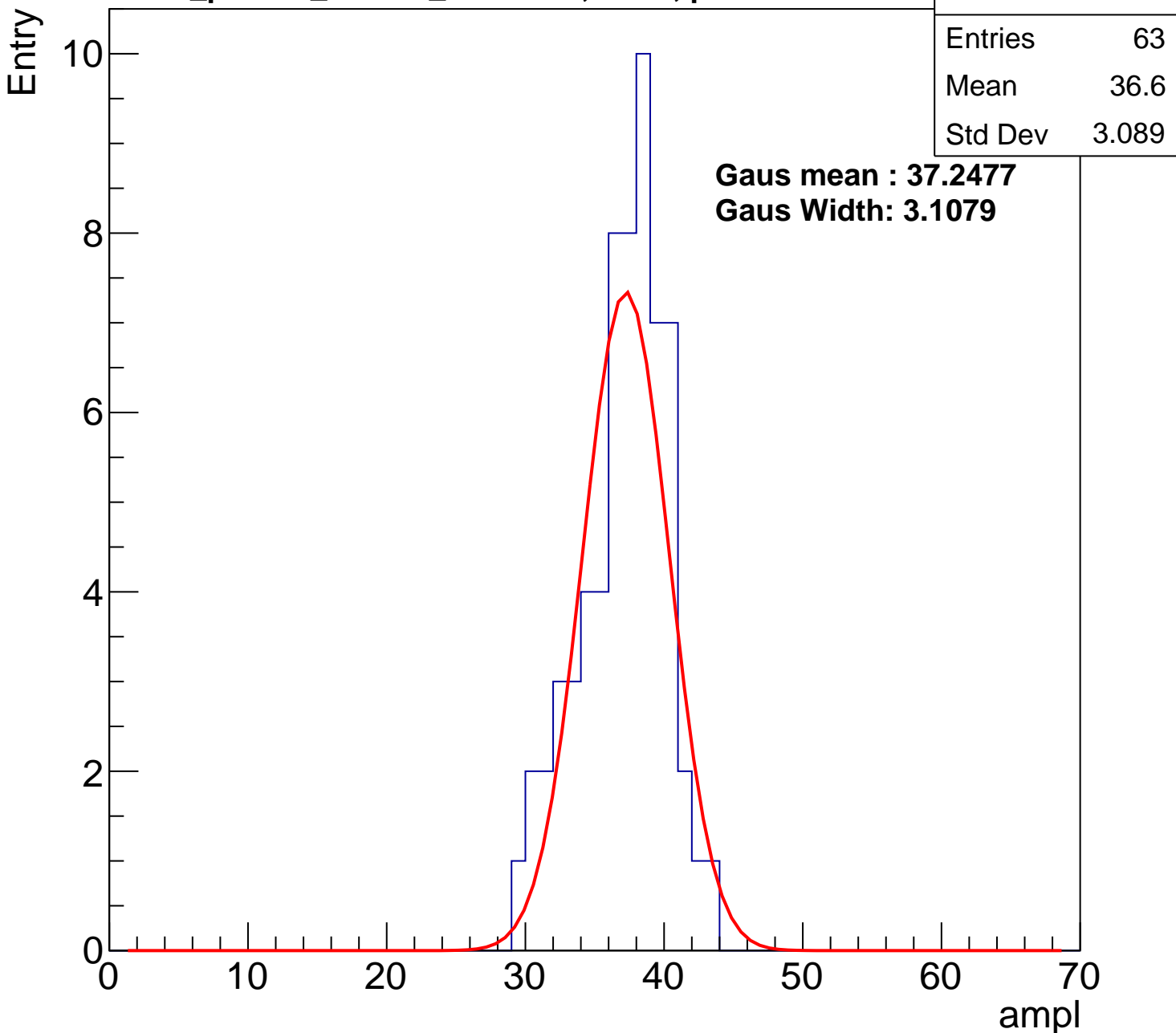
Gaus Width: 3.1079

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch25, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	44.33
Std Dev	3.304

Gaus mean : 44.7100

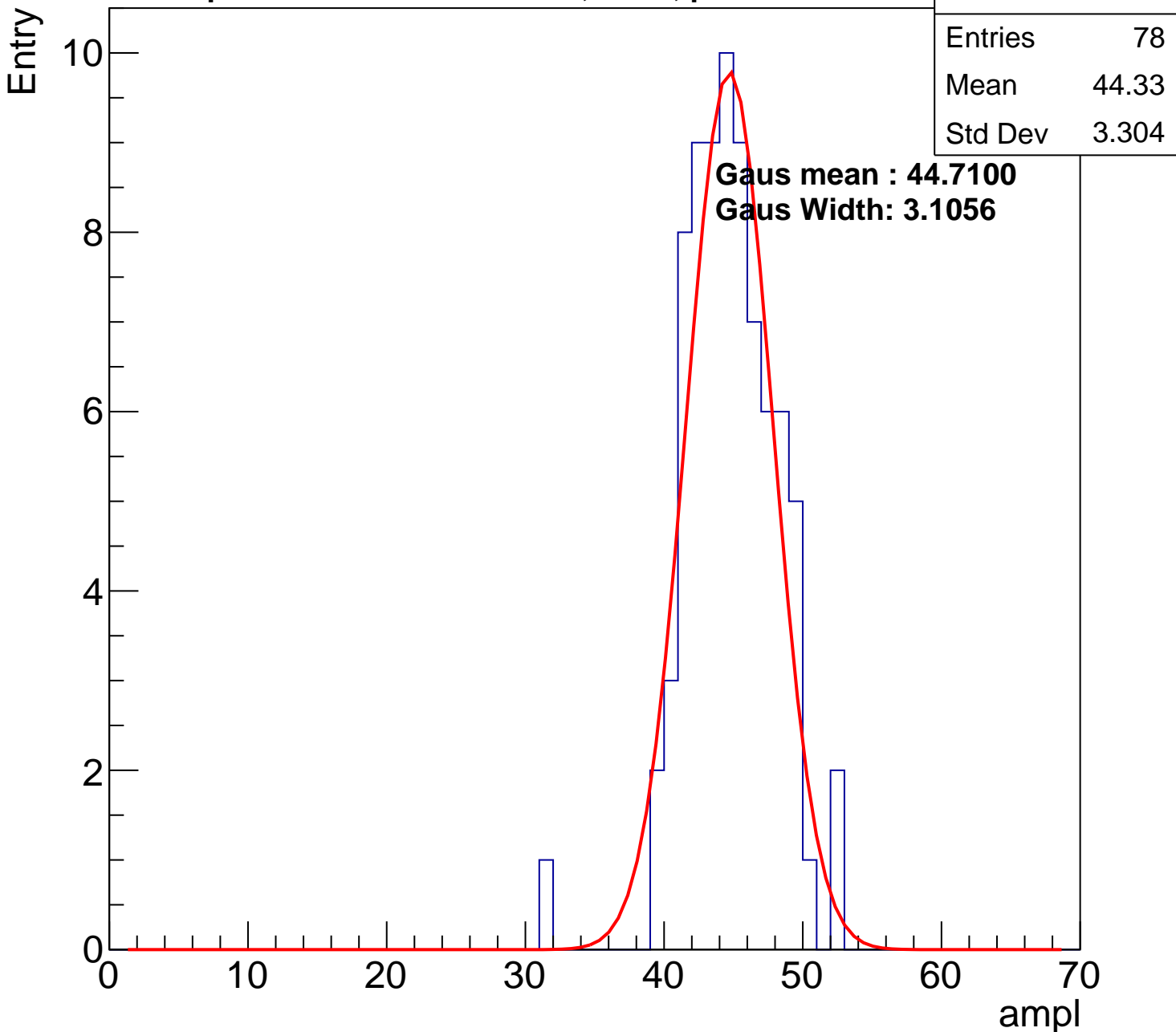
Gaus Width: 3.1056

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

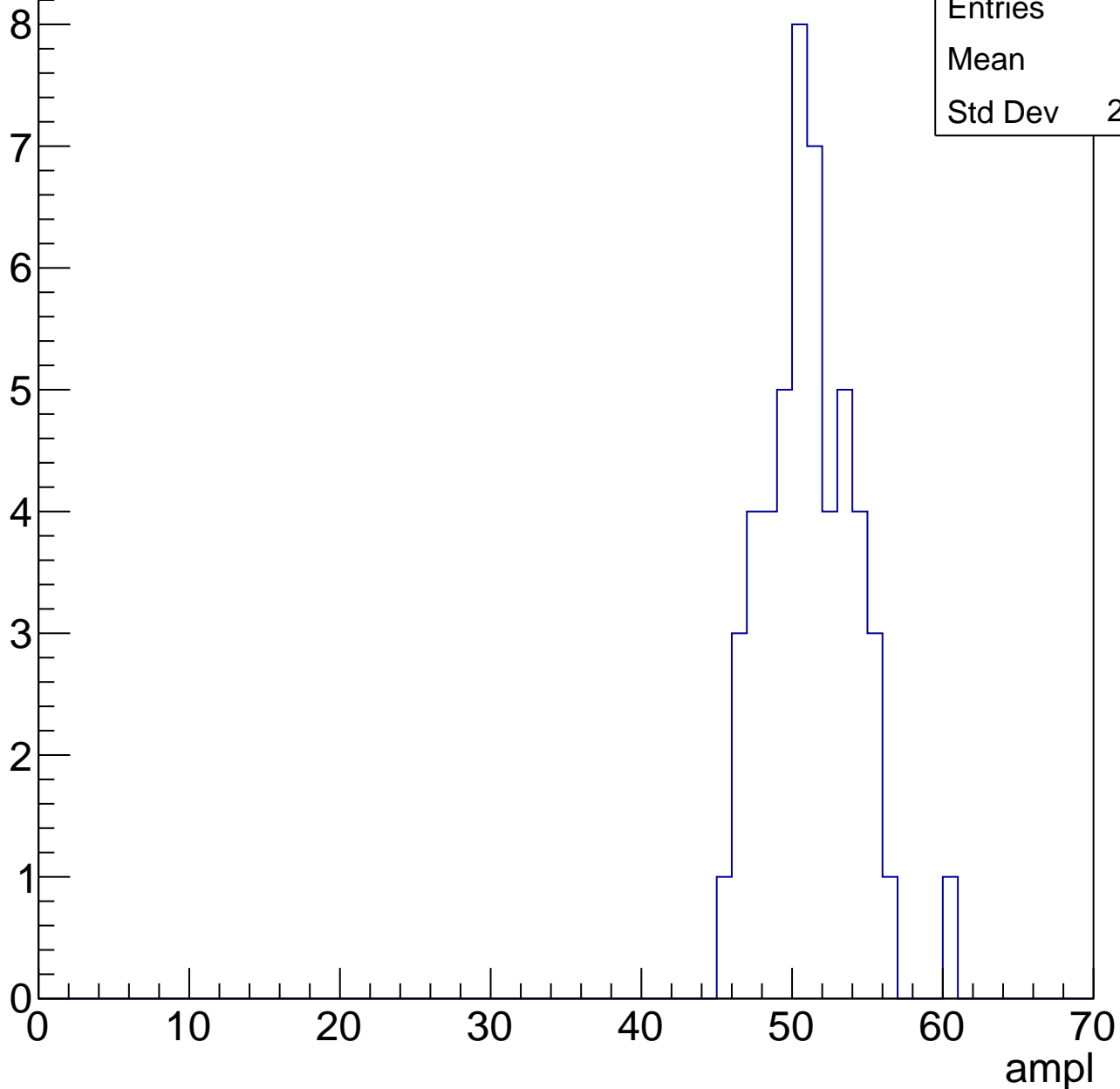


B1L103S, U23-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	50.7
Std Dev	2.995



B1L103S, U23-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

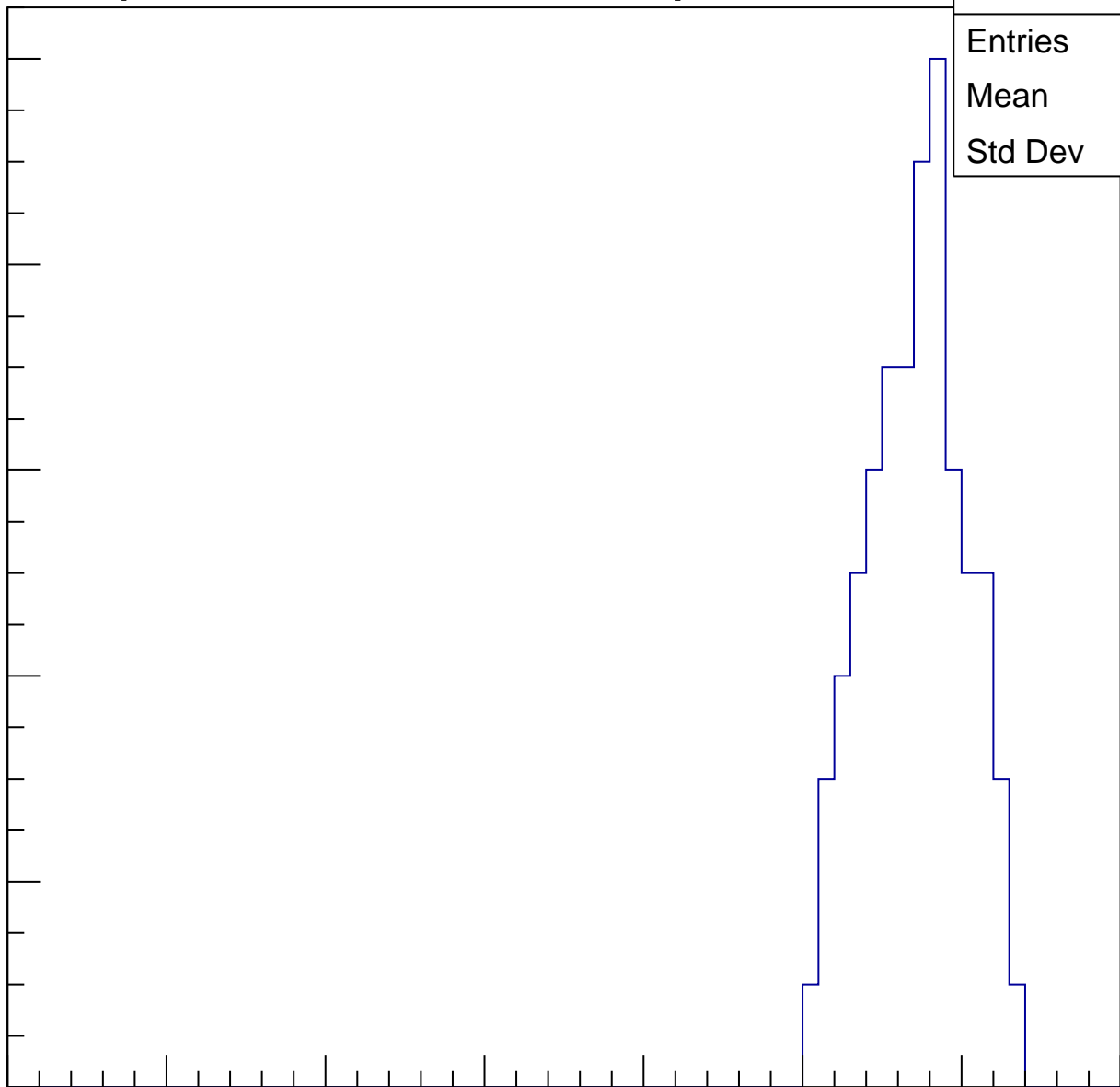
50

60

70

ampl

Entries	72
Mean	56.64
Std Dev	3.088

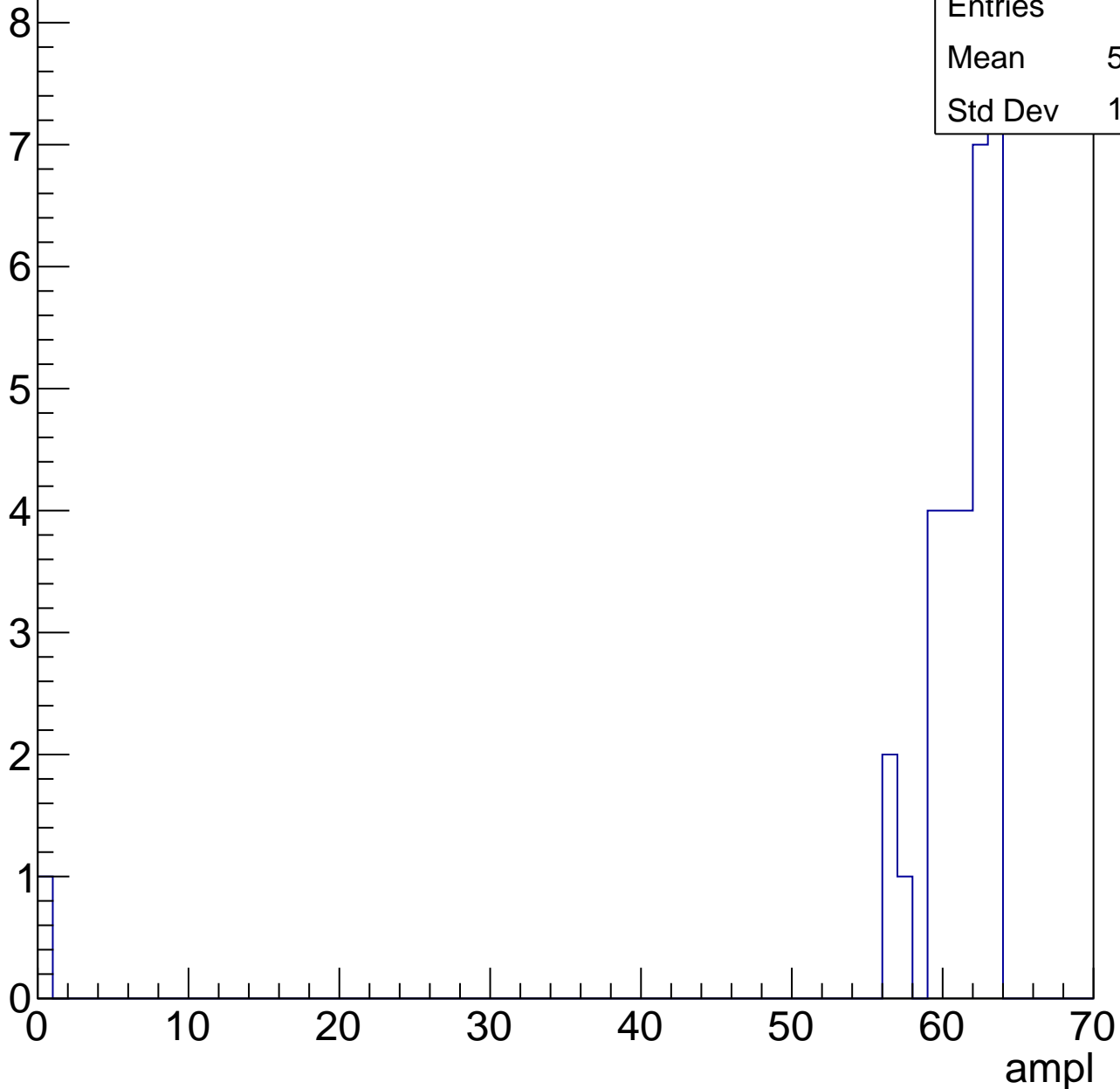


B1L103S, U23-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	58.94
Std Dev	10.95



B1L103S, U23-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch26, adc0

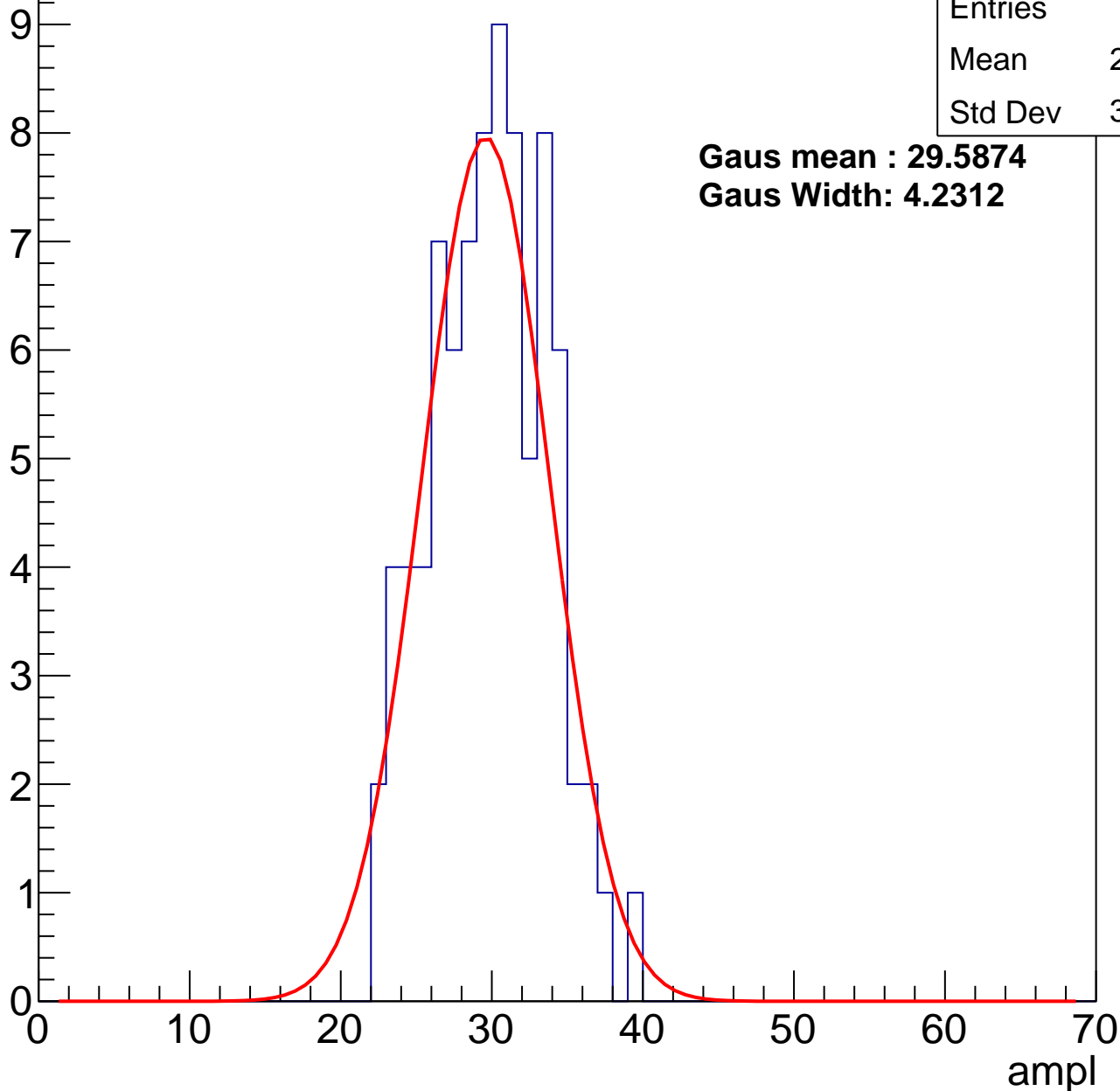
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	29.38
Std Dev	3.757

Gaus mean : 29.5874

Gaus Width: 4.2312



B1L103S, U23-ch26, adc1

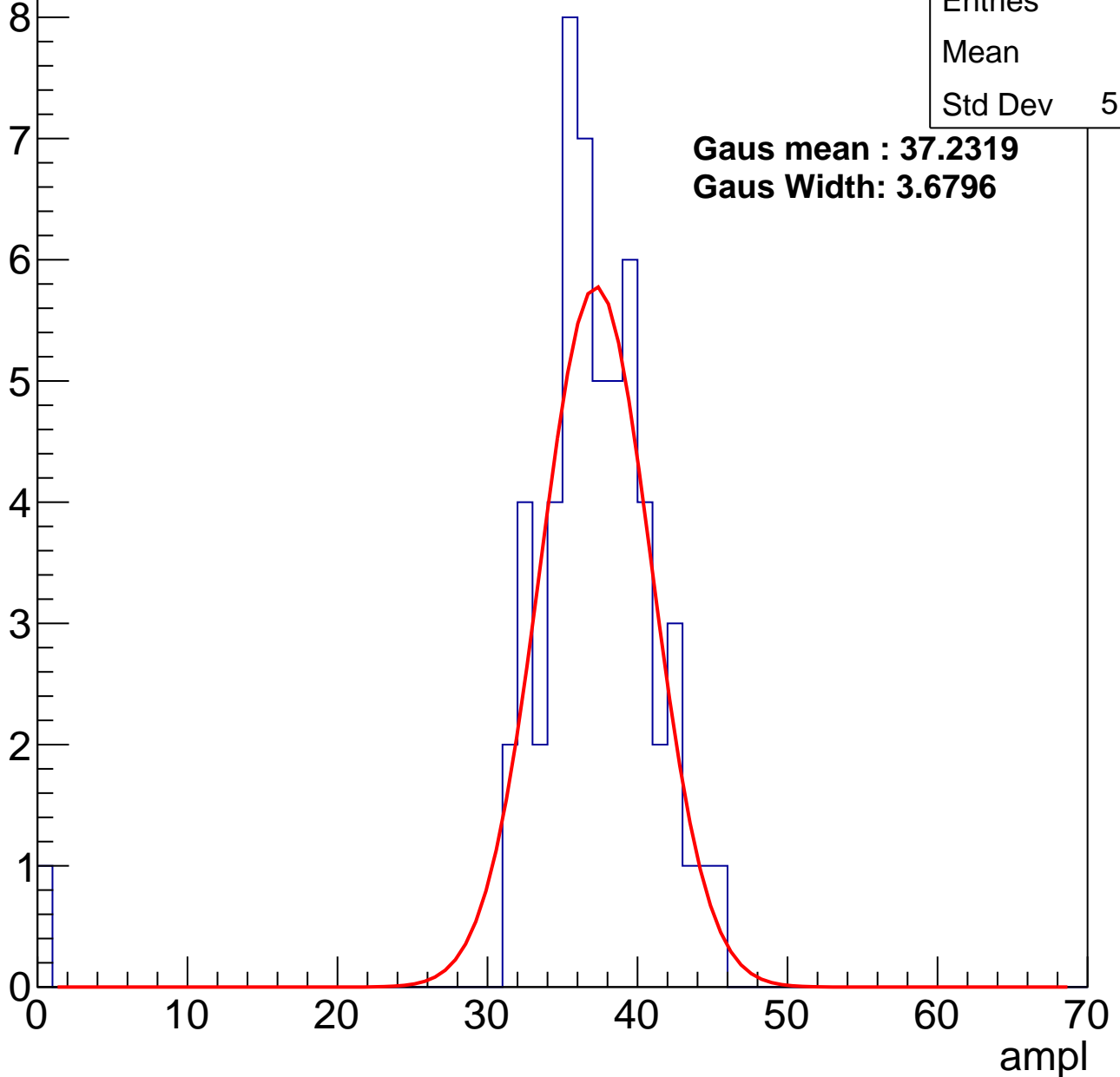
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	36.3
Std Dev	5.889

Gaus mean : 37.2319

Gaus Width: 3.6796



B1L103S, U23-ch26, adc2

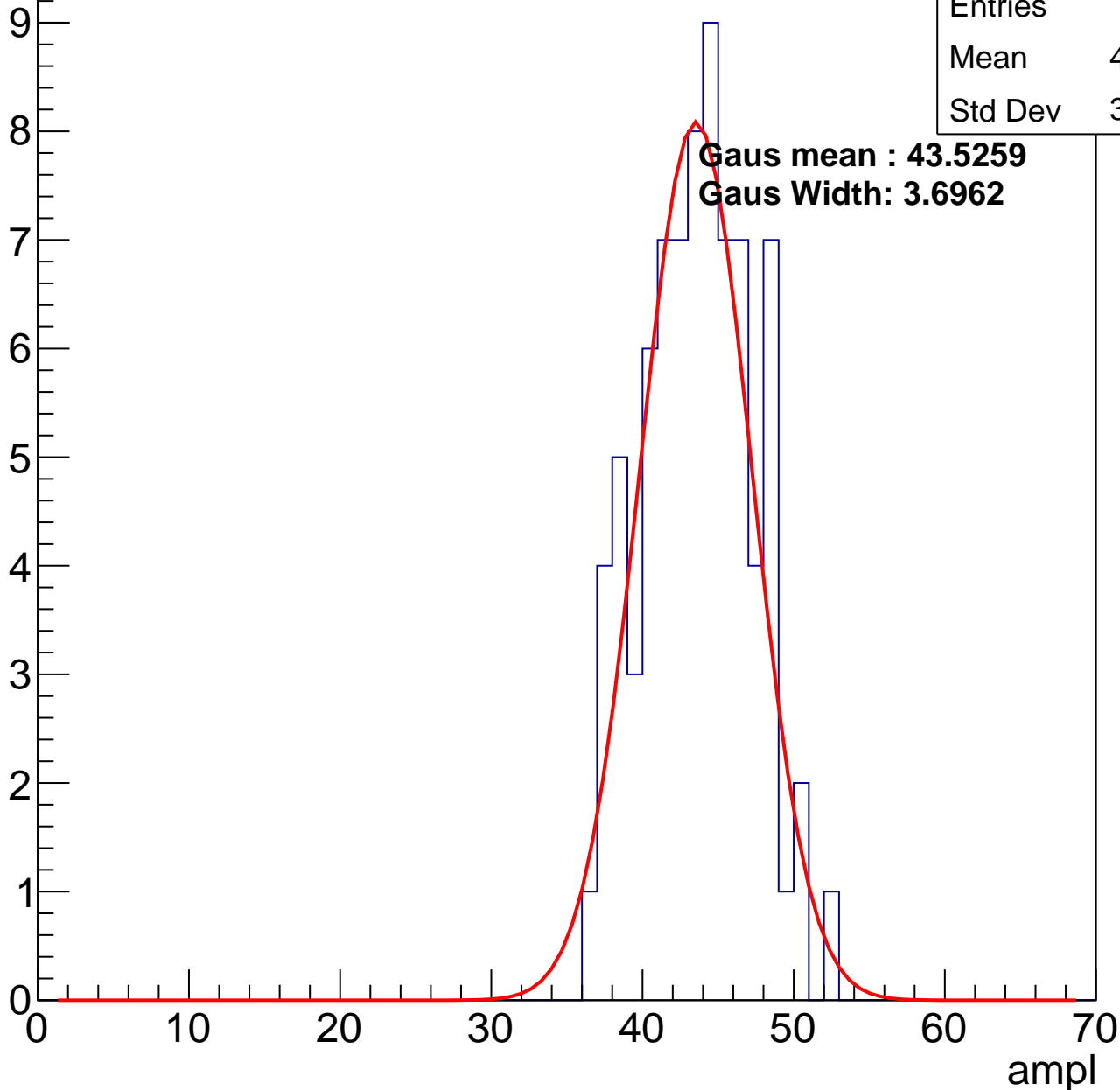
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	43.22
Std Dev	3.567

Gaus mean : 43.5259

Gaus Width: 3.6962

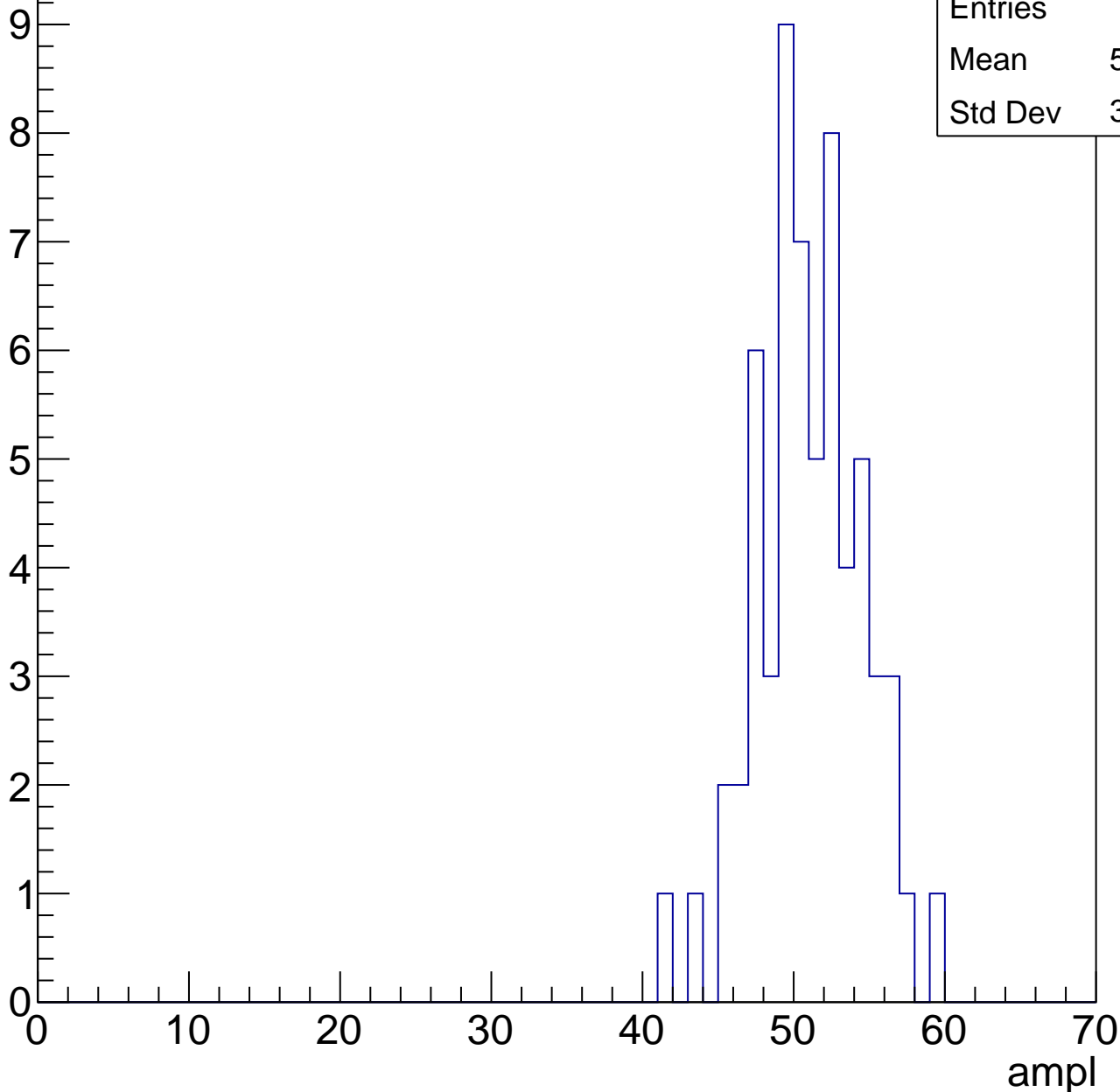


B1L103S, U23-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.57
Std Dev	3.466

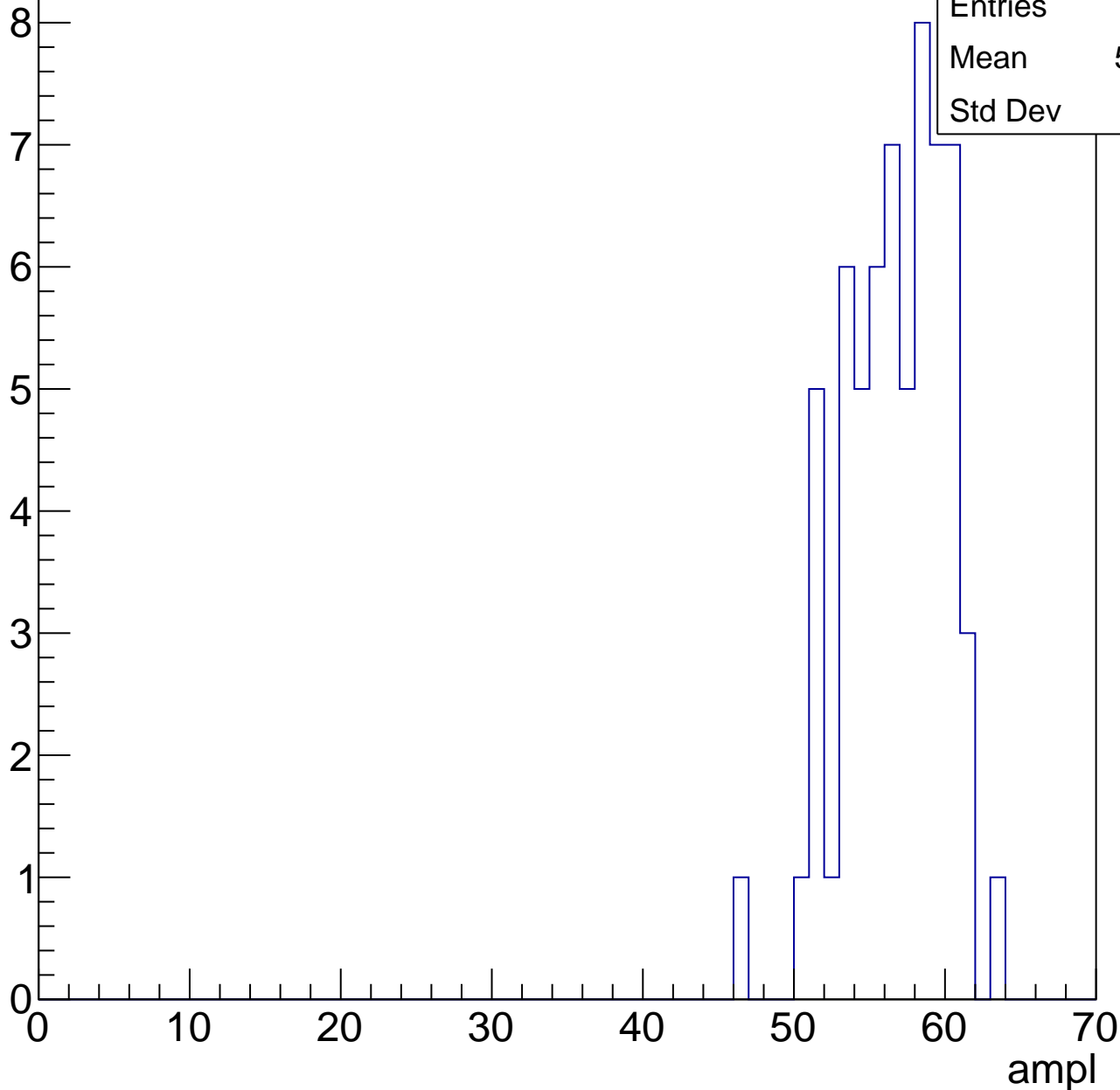


B1L103S, U23-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	56.21
Std Dev	3.31

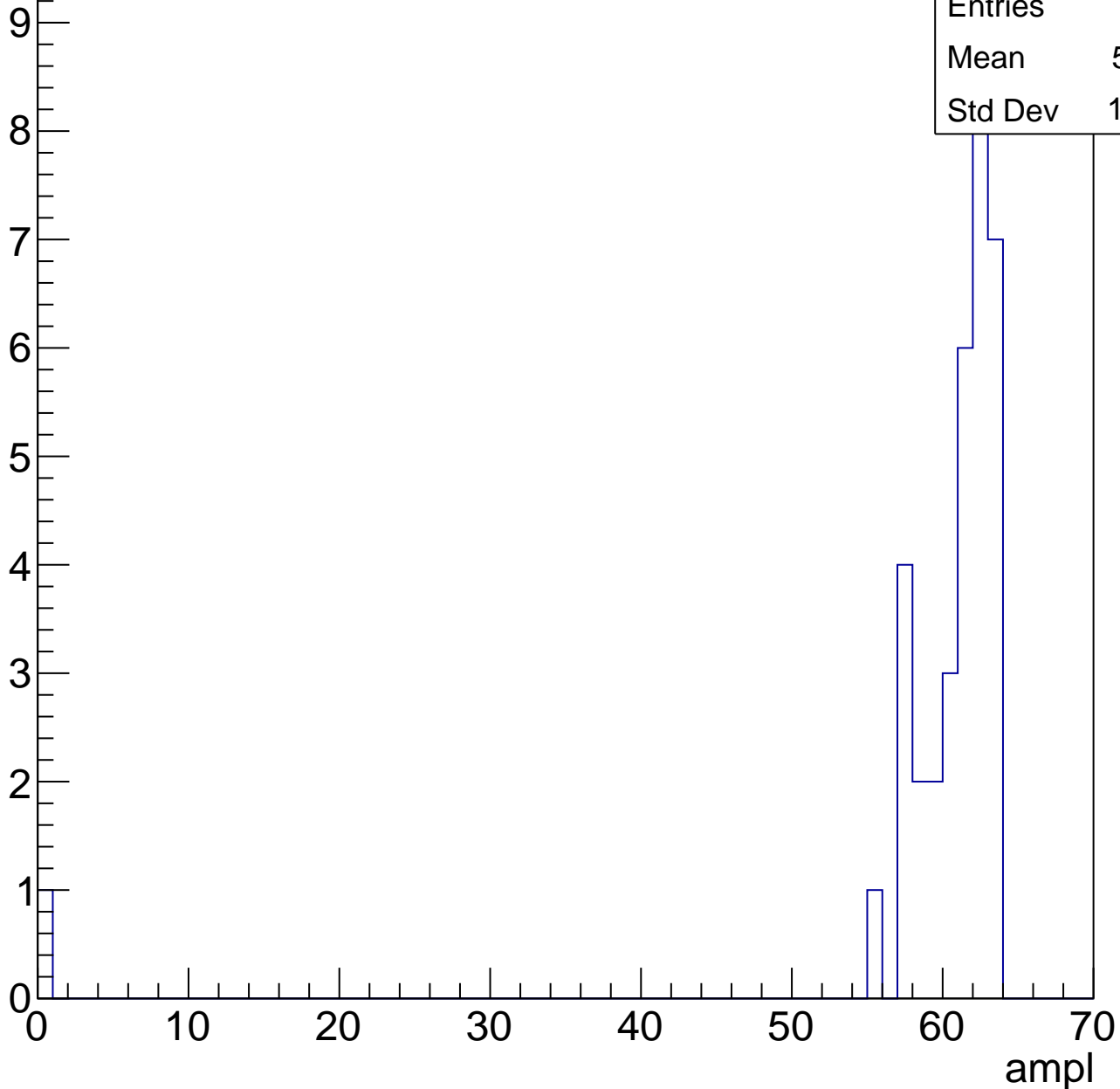


B1L103S, U23-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.91
Std Dev	10.33



B1L103S, U23-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

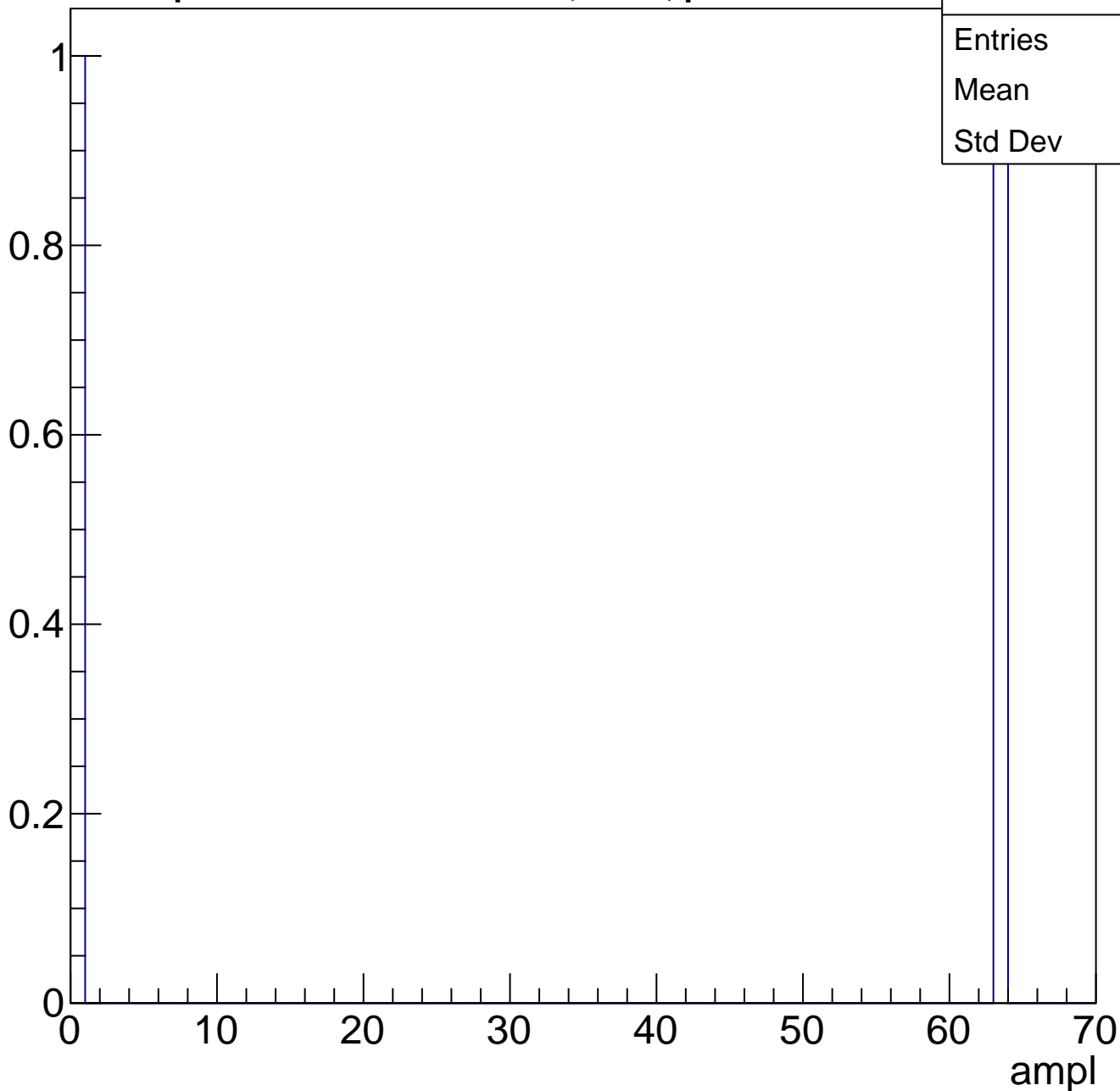
Entry



B1L103S, U23-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch27, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	29.51
Std Dev	5.236

Gaus mean : 30.6160

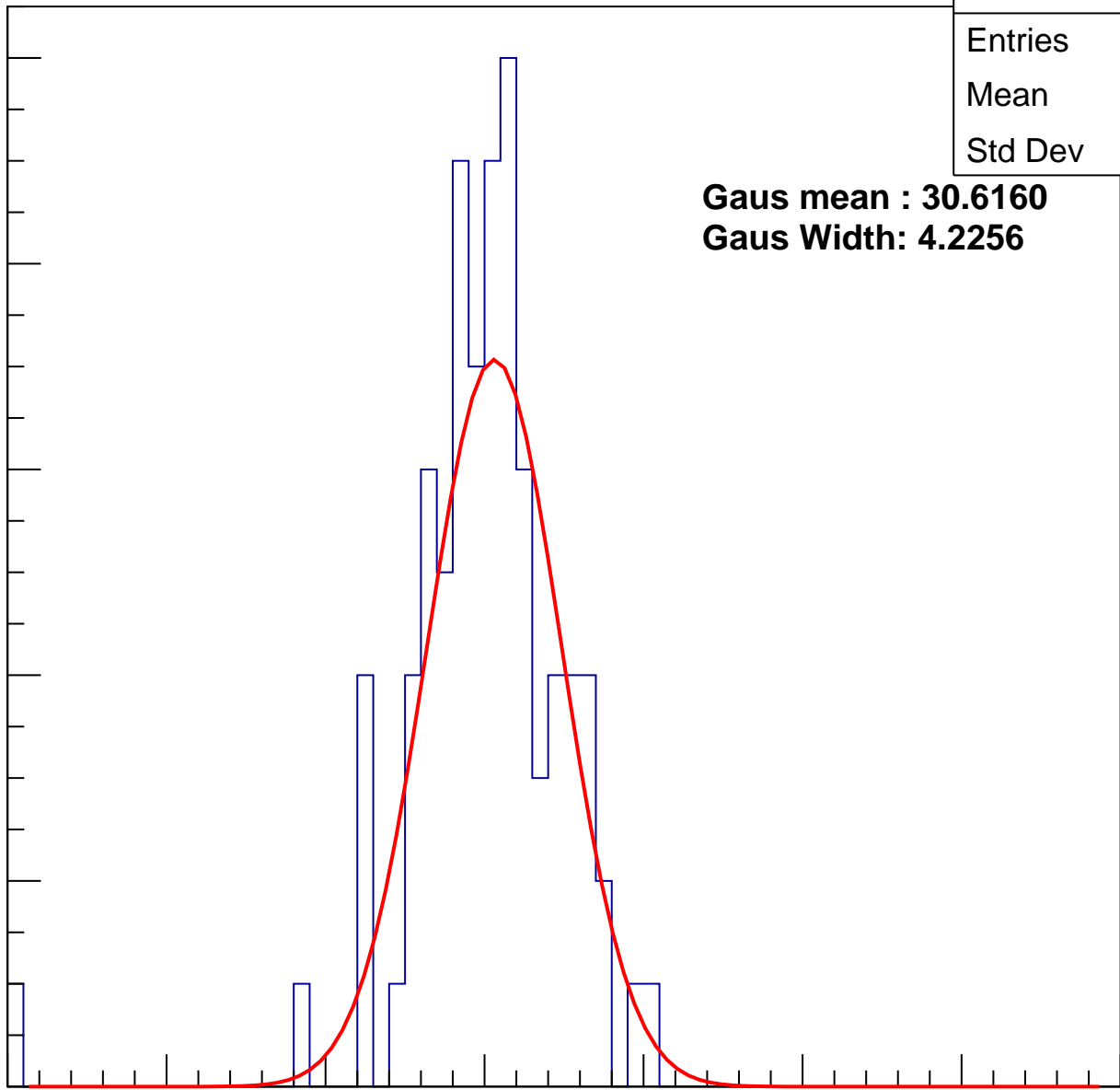
Gaus Width: 4.2256

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch27, adc1

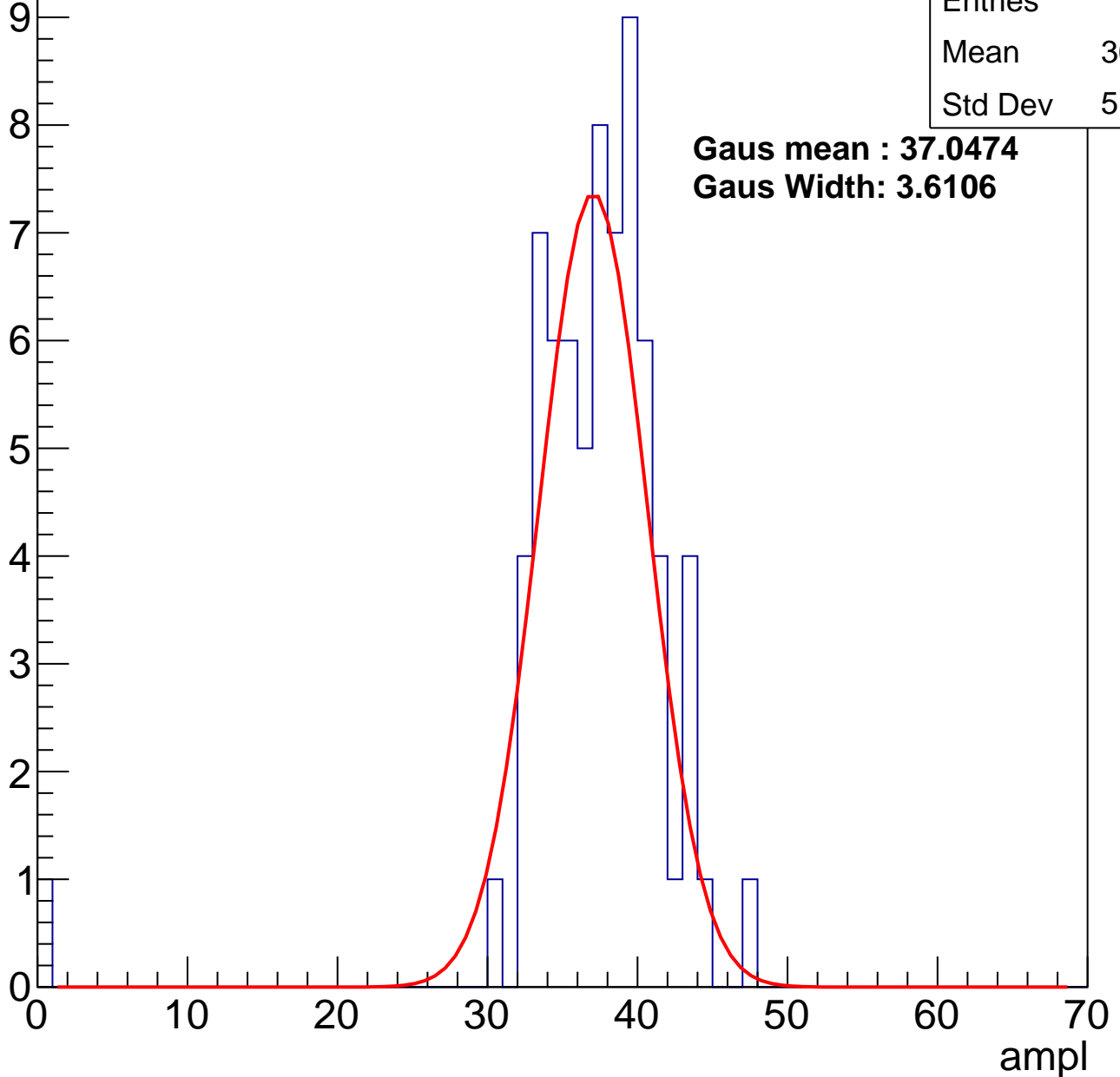
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.69
Std Dev	5.548

Gaus mean : 37.0474

Gaus Width: 3.6106



B1L103S, U23-ch27, adc2

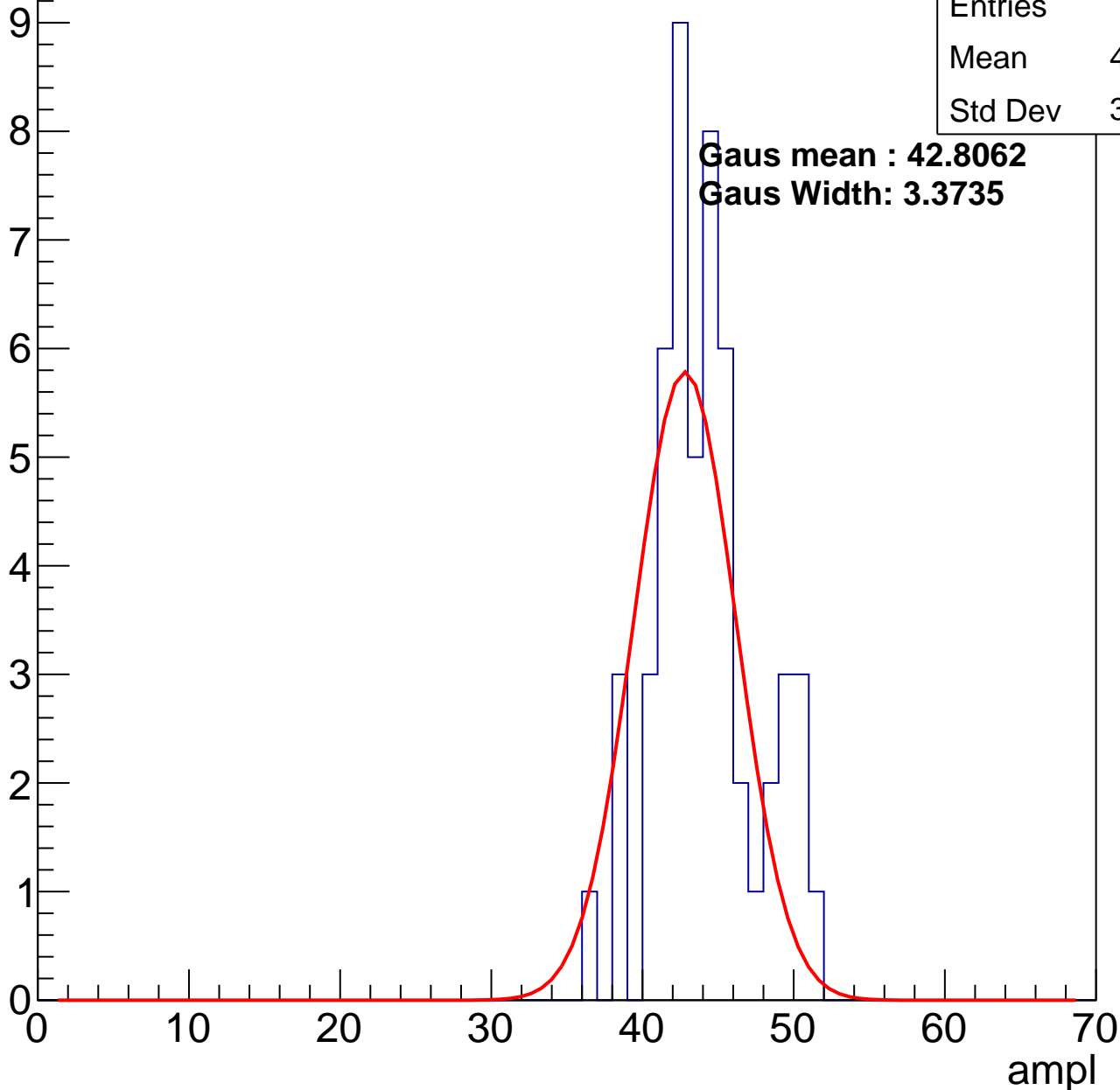
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	43.66
Std Dev	3.353

Gaus mean : 42.8062

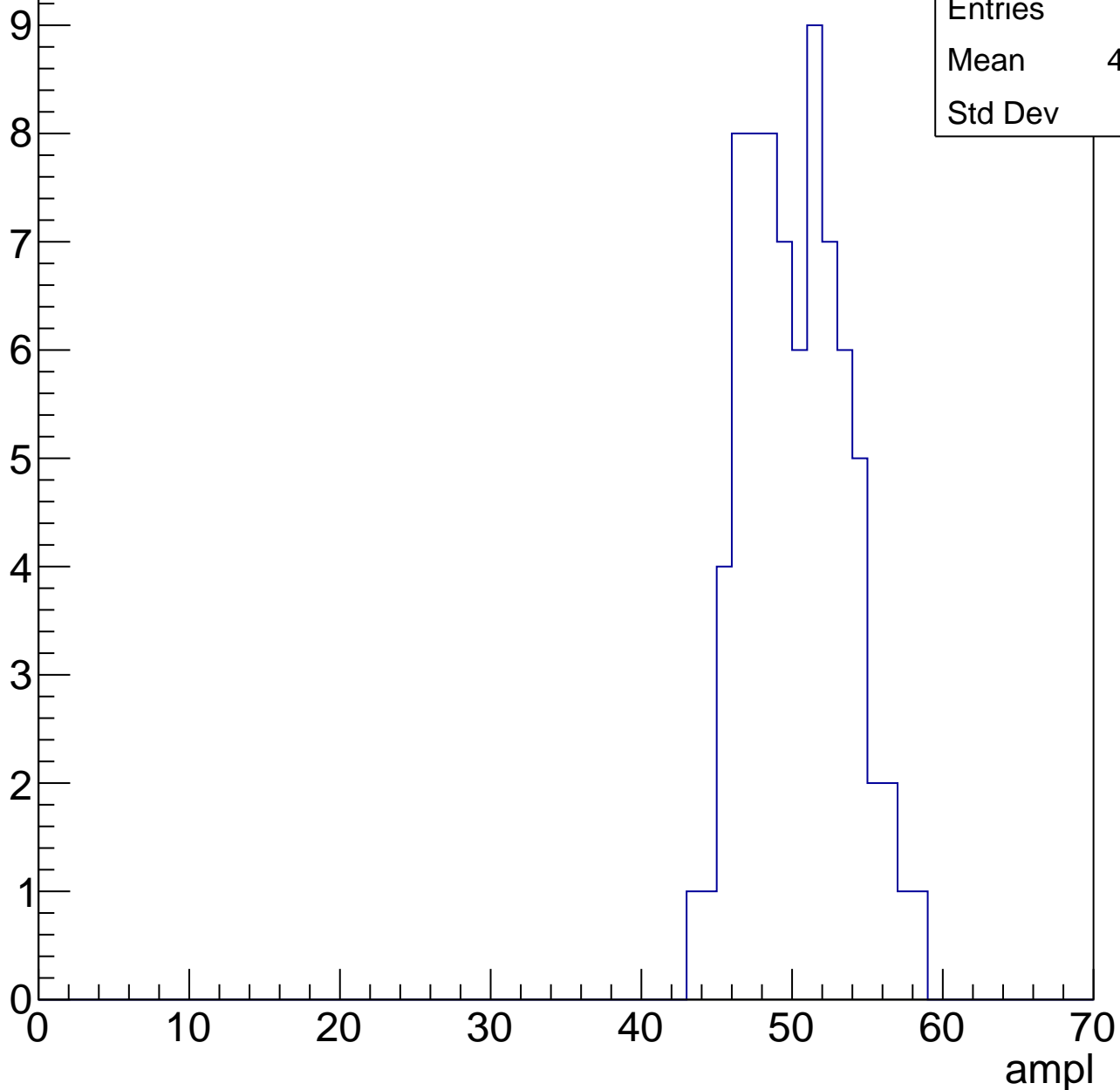
Gaus Width: 3.3735



B1L103S, U23-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

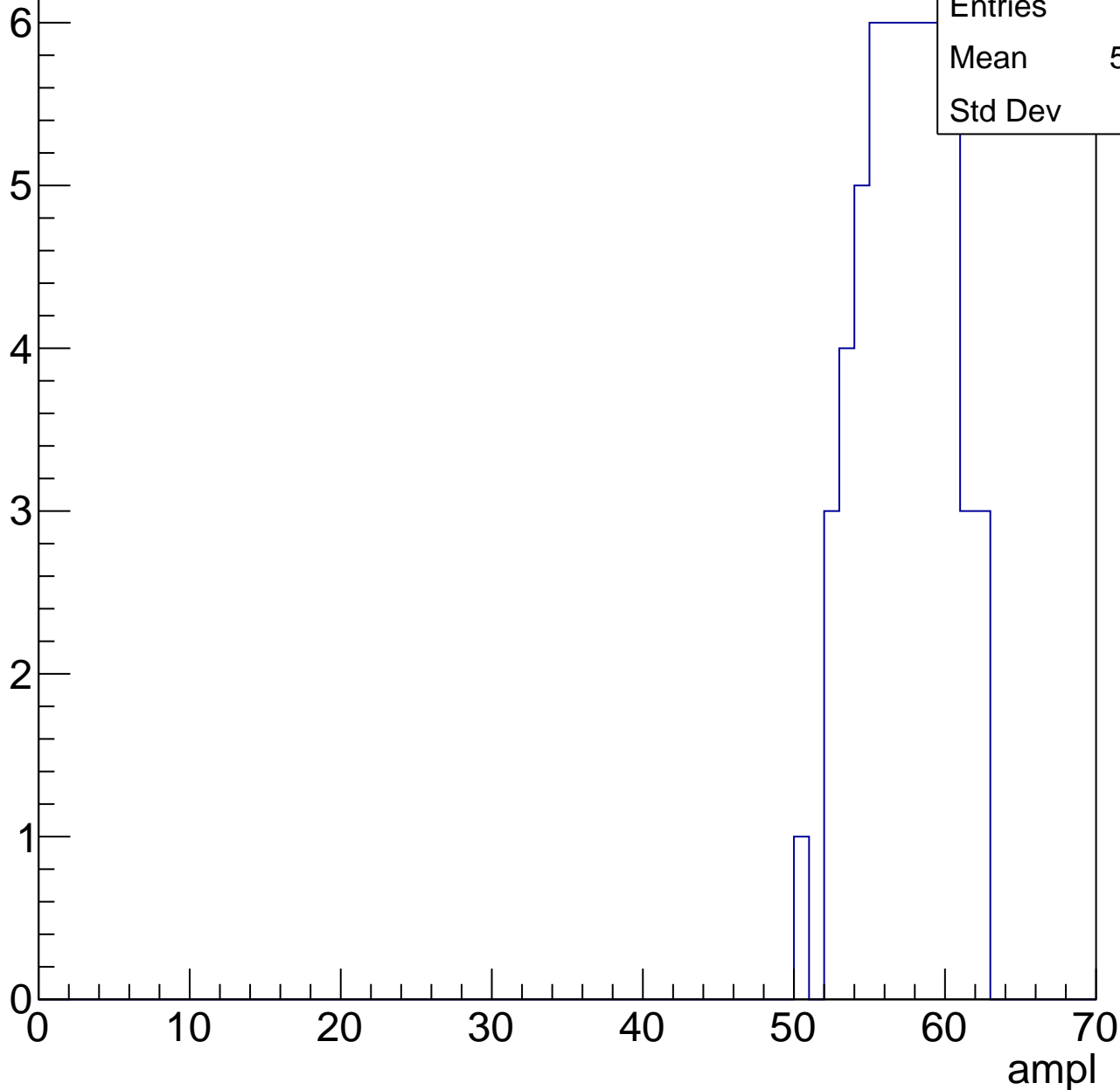


B1L103S, U23-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.85
Std Dev	2.92



B1L103S, U23-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

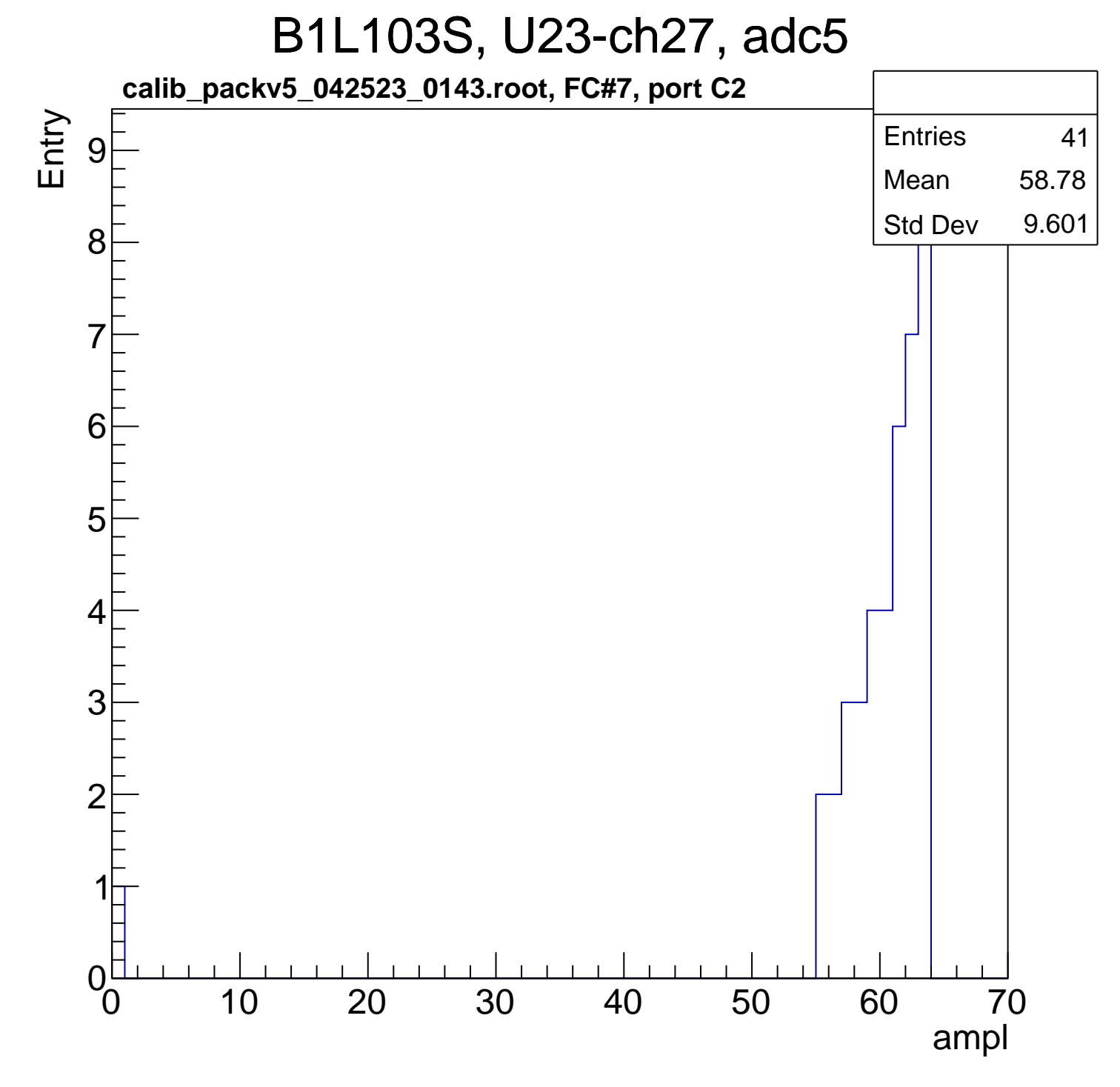
Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.78
Std Dev	9.601

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch28, adc0

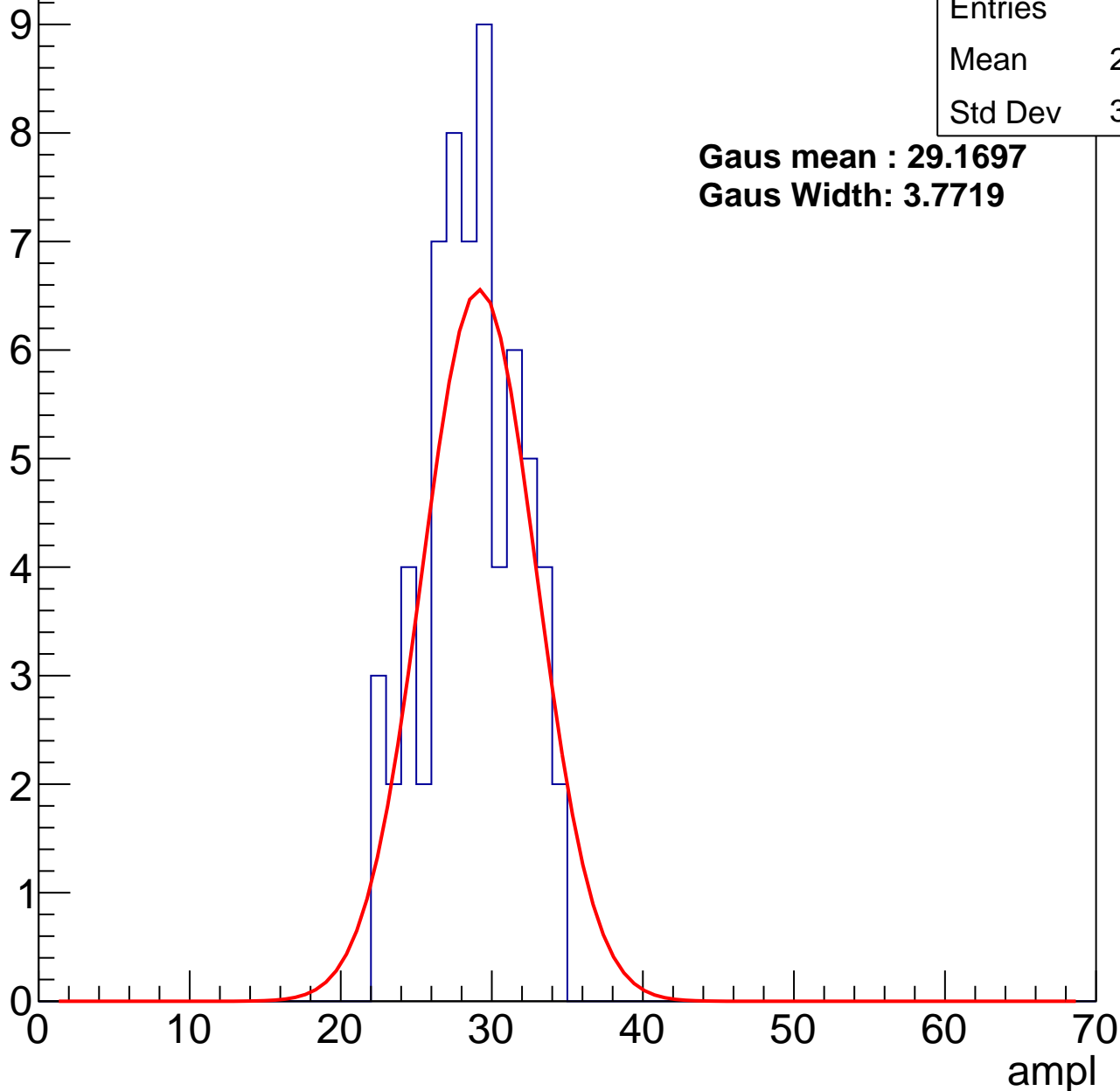
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.24
Std Dev	3.095

Gaus mean : 29.1697

Gaus Width: 3.7719



B1L103S, U23-ch28, adc1

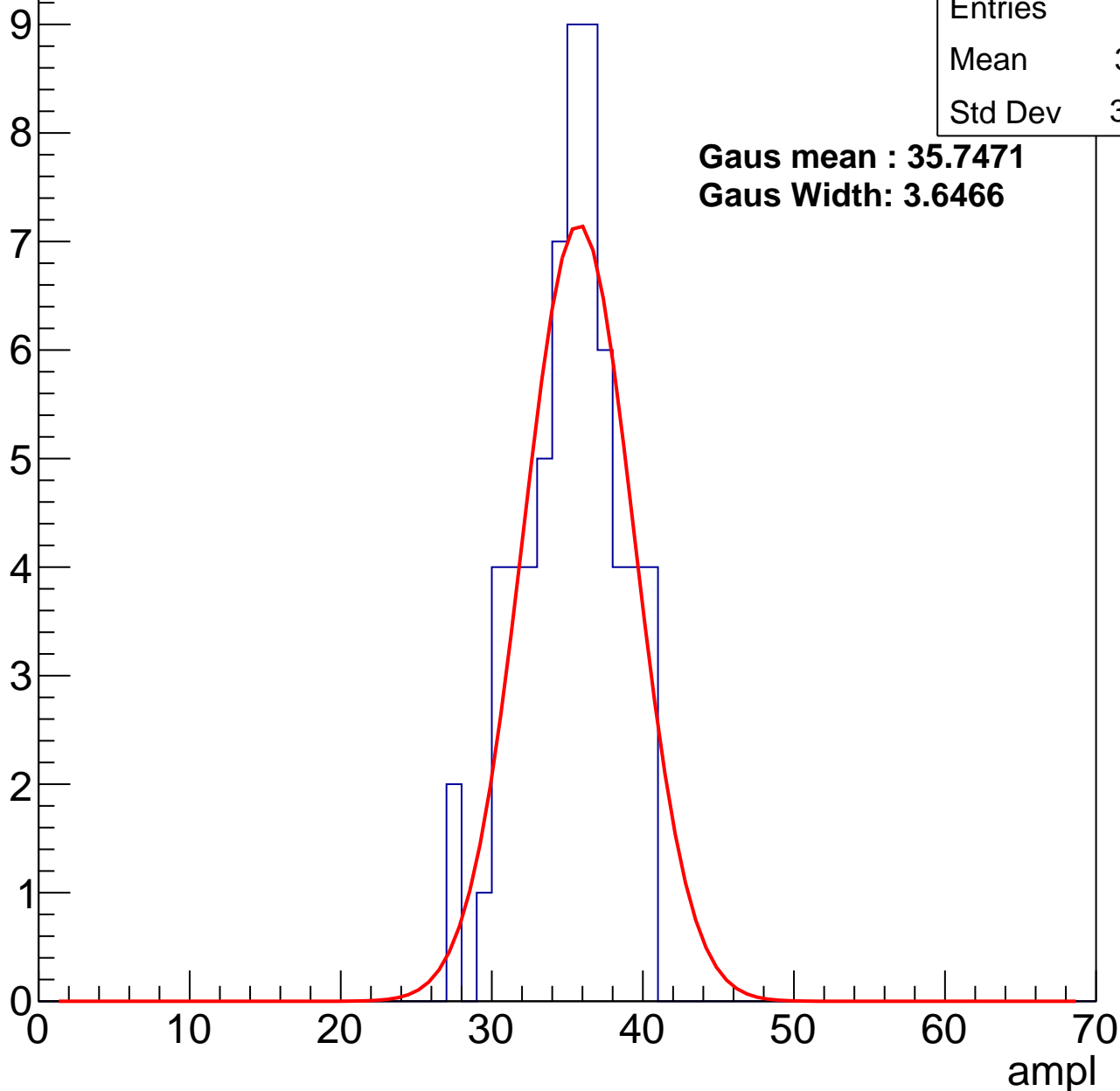
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	34.71
Std Dev	3.134

Gaus mean : 35.7471

Gaus Width: 3.6466



B1L103S, U23-ch28, adc2

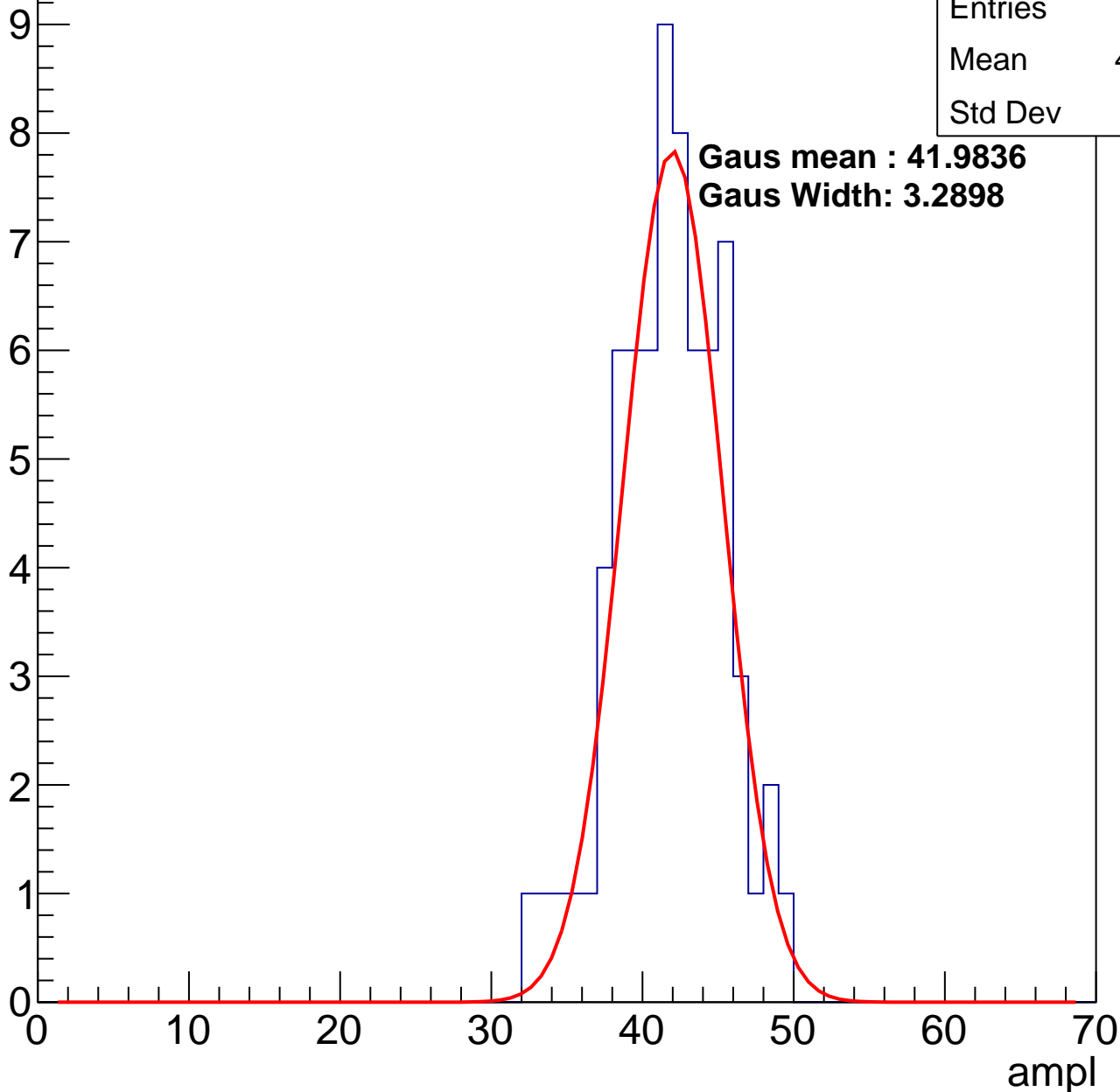
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	41.31
Std Dev	3.52

Gaus mean : 41.9836

Gaus Width: 3.2898

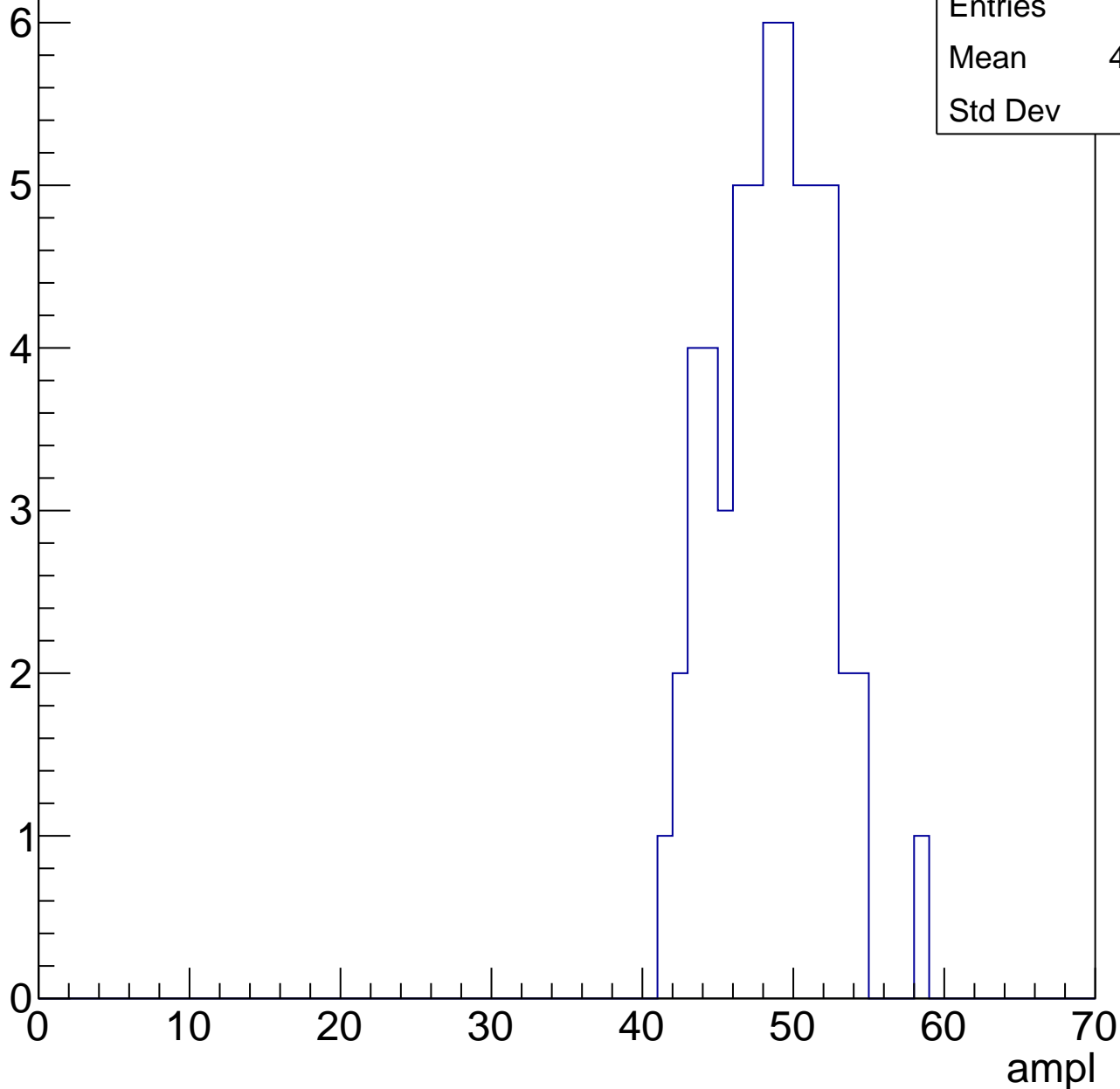


B1L103S, U23-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	48.07
Std Dev	3.56

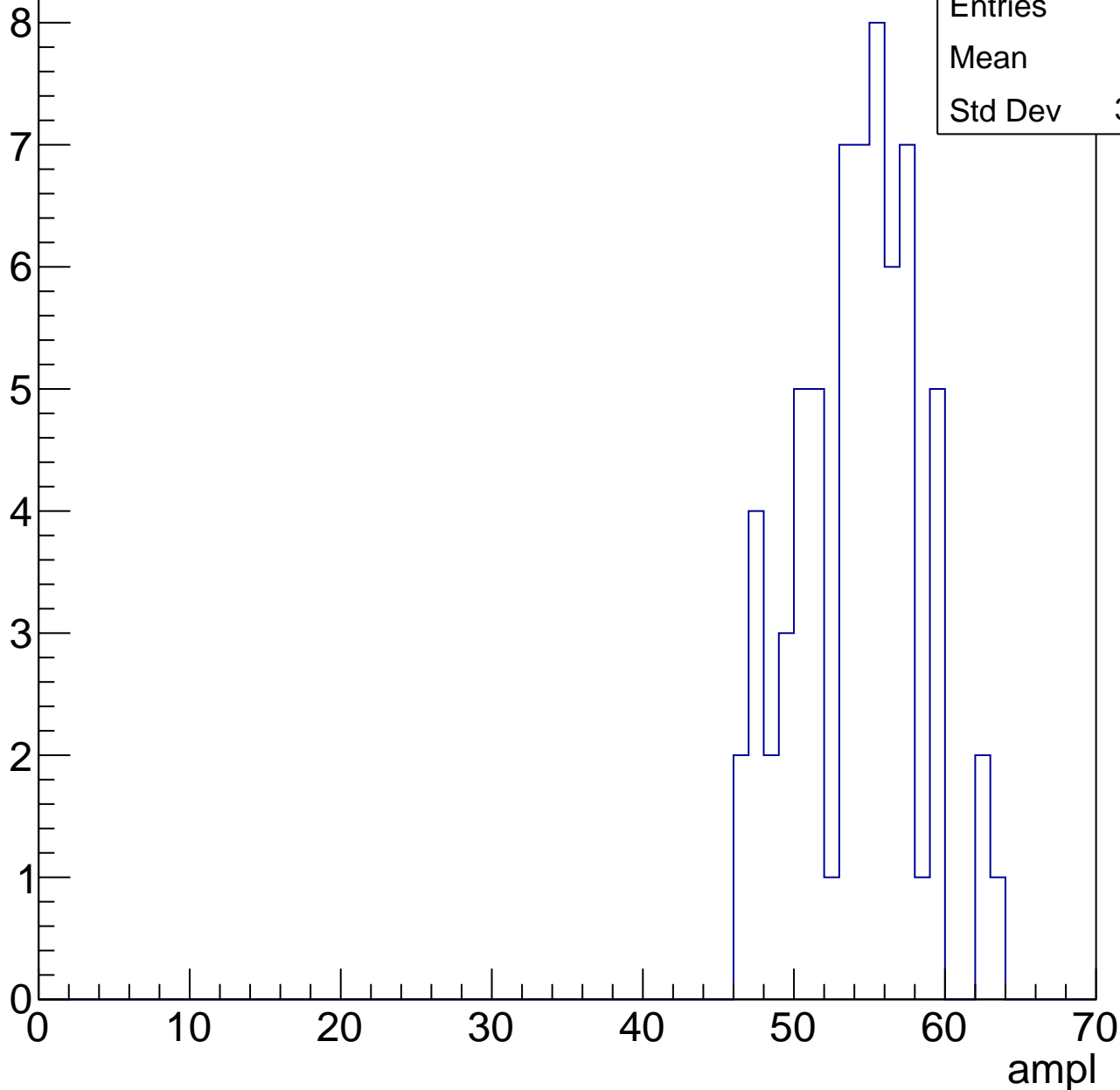


B1L103S, U23-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	53.7
Std Dev	3.981

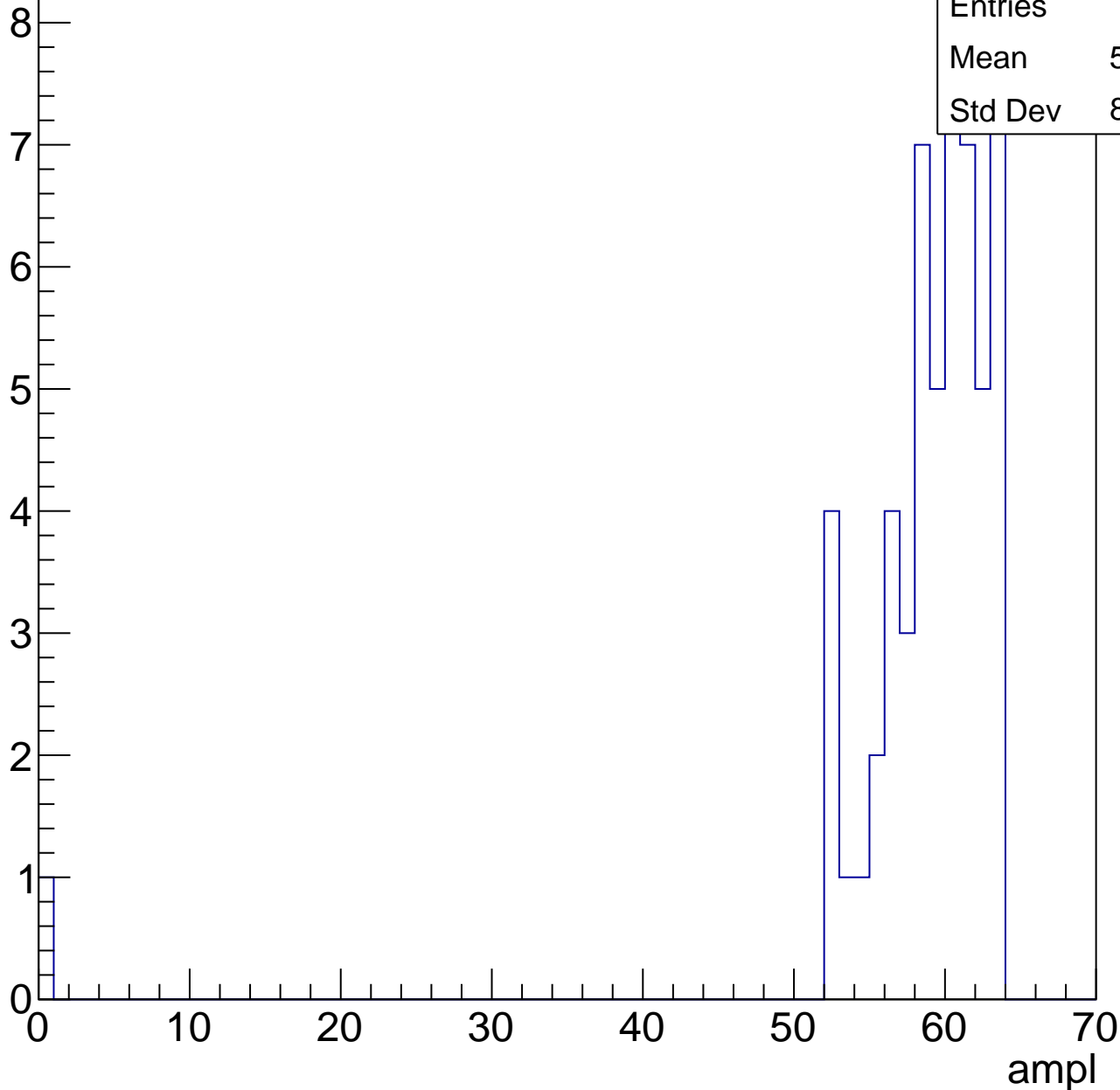


B1L103S, U23-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

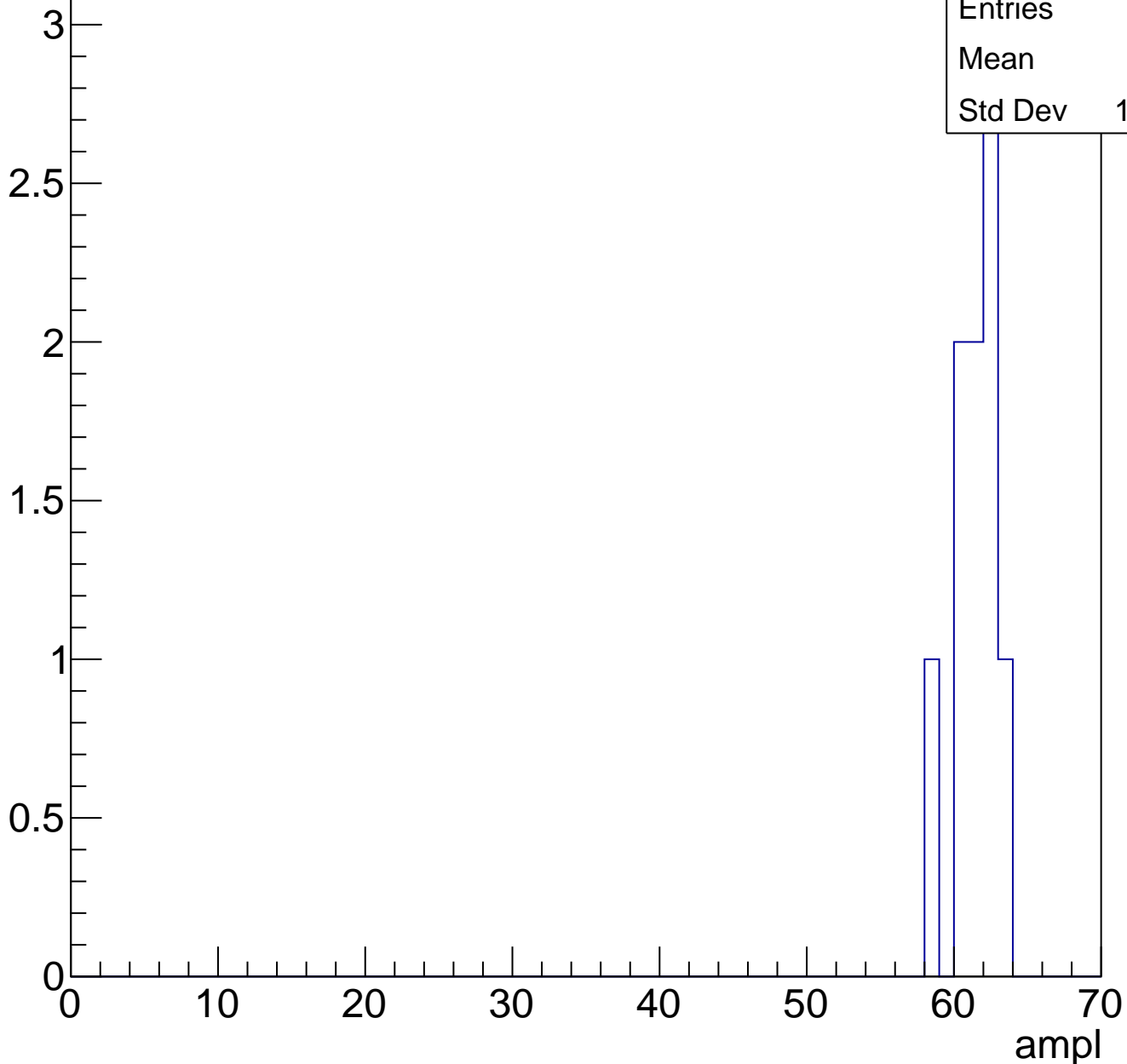
Entries	56
Mean	57.89
Std Dev	8.415



B1L103S, U23-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch29, adc0

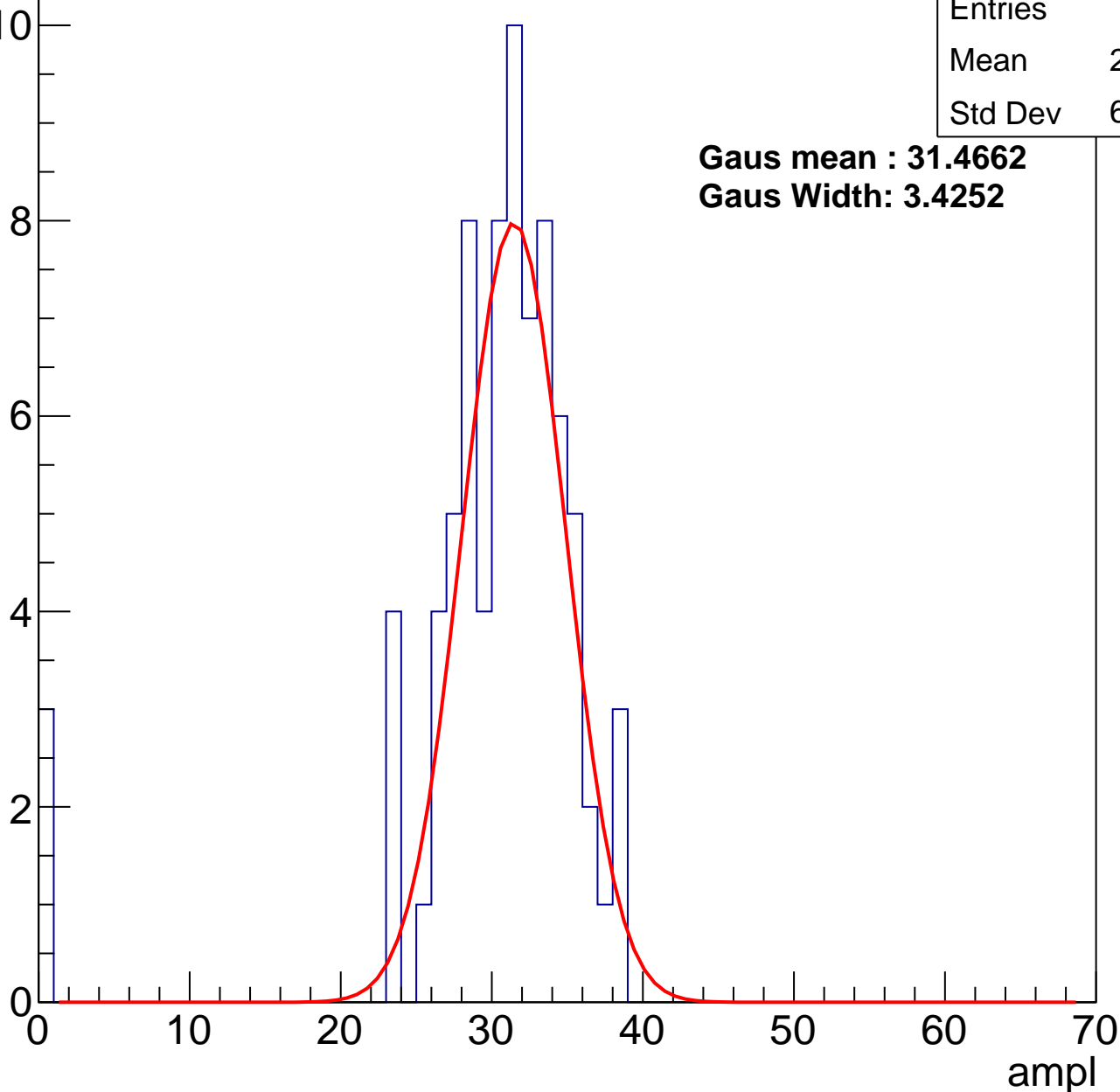
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	29.57
Std Dev	6.839

Gaus mean : 31.4662

Gaus Width: 3.4252



B1L103S, U23-ch29, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	68
Mean	38.06
Std Dev	3.258

Gaus mean : 38.4145

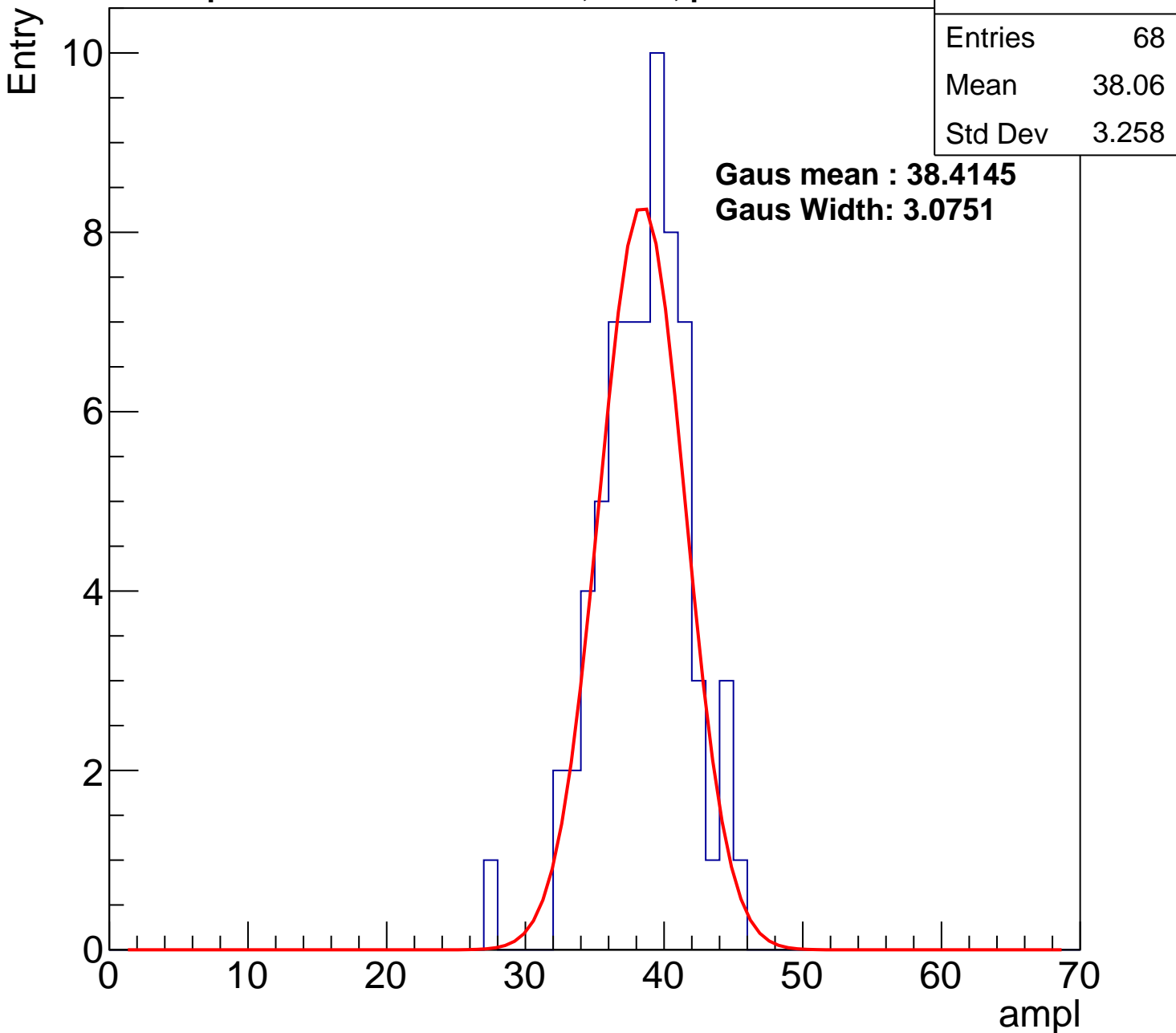
Gaus Width: 3.0751

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch29, adc2

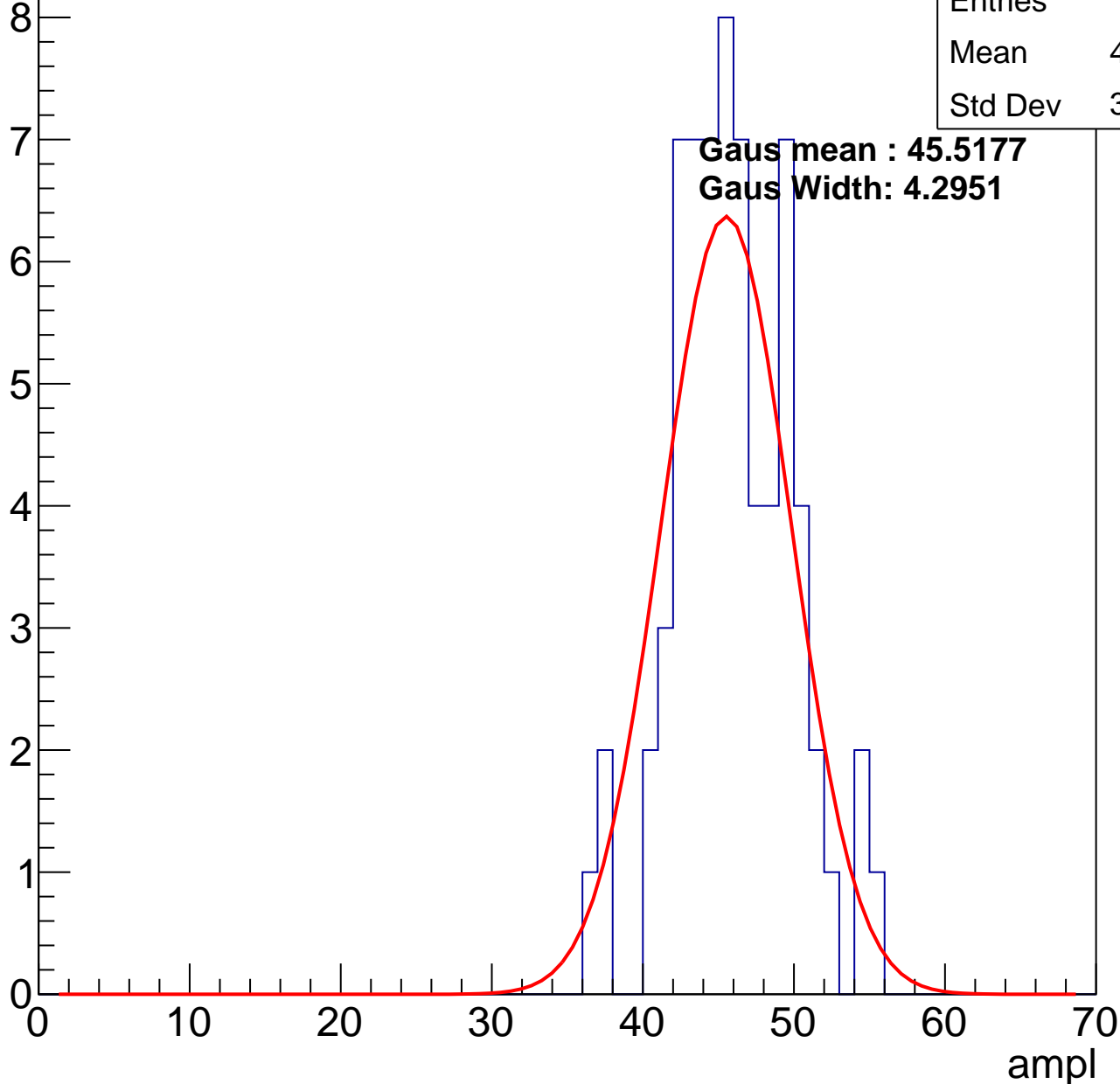
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	45.48
Std Dev	3.888

Gaus mean : 45.5177

Gaus Width: 4.2951

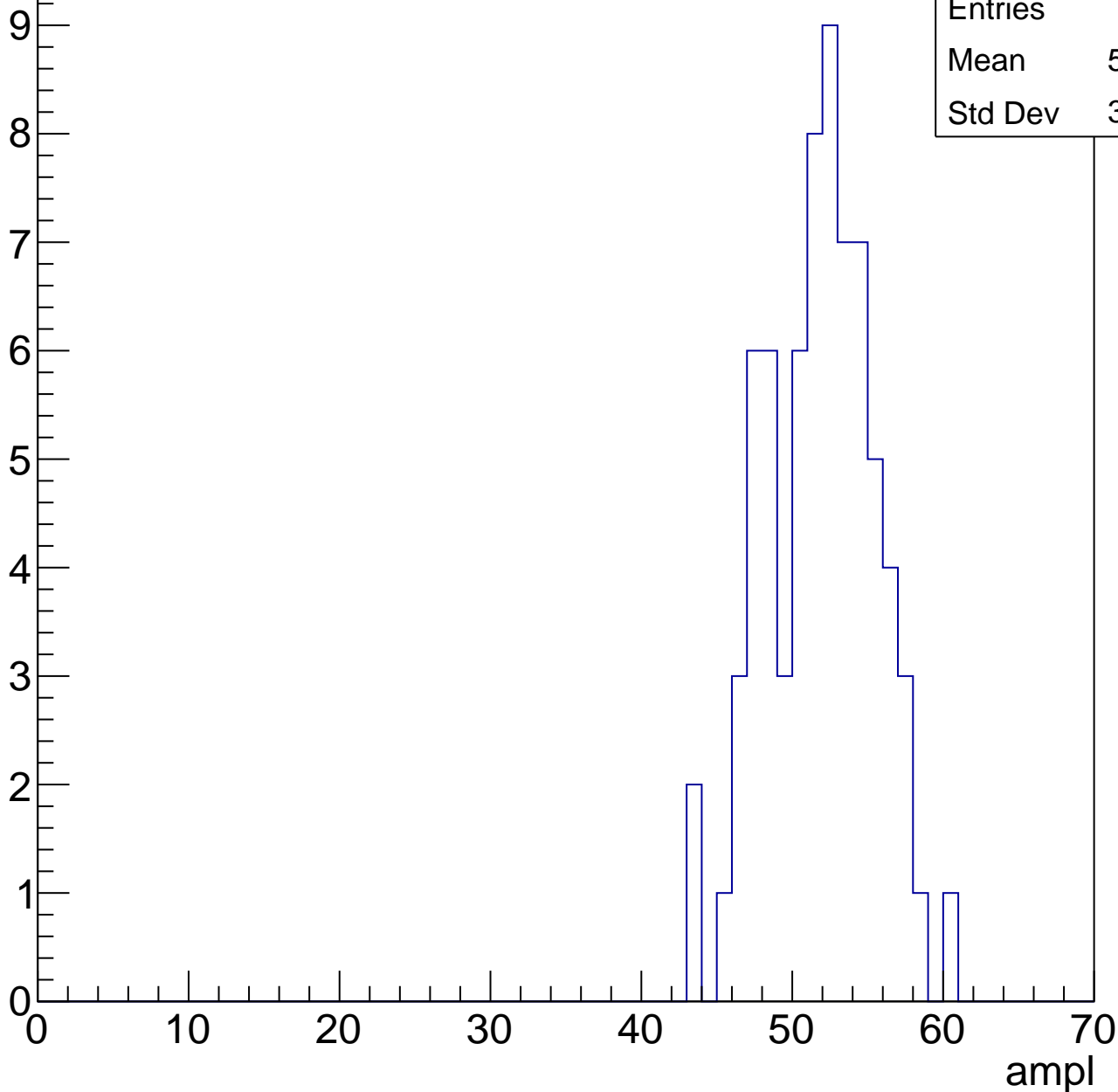


B1L103S, U23-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

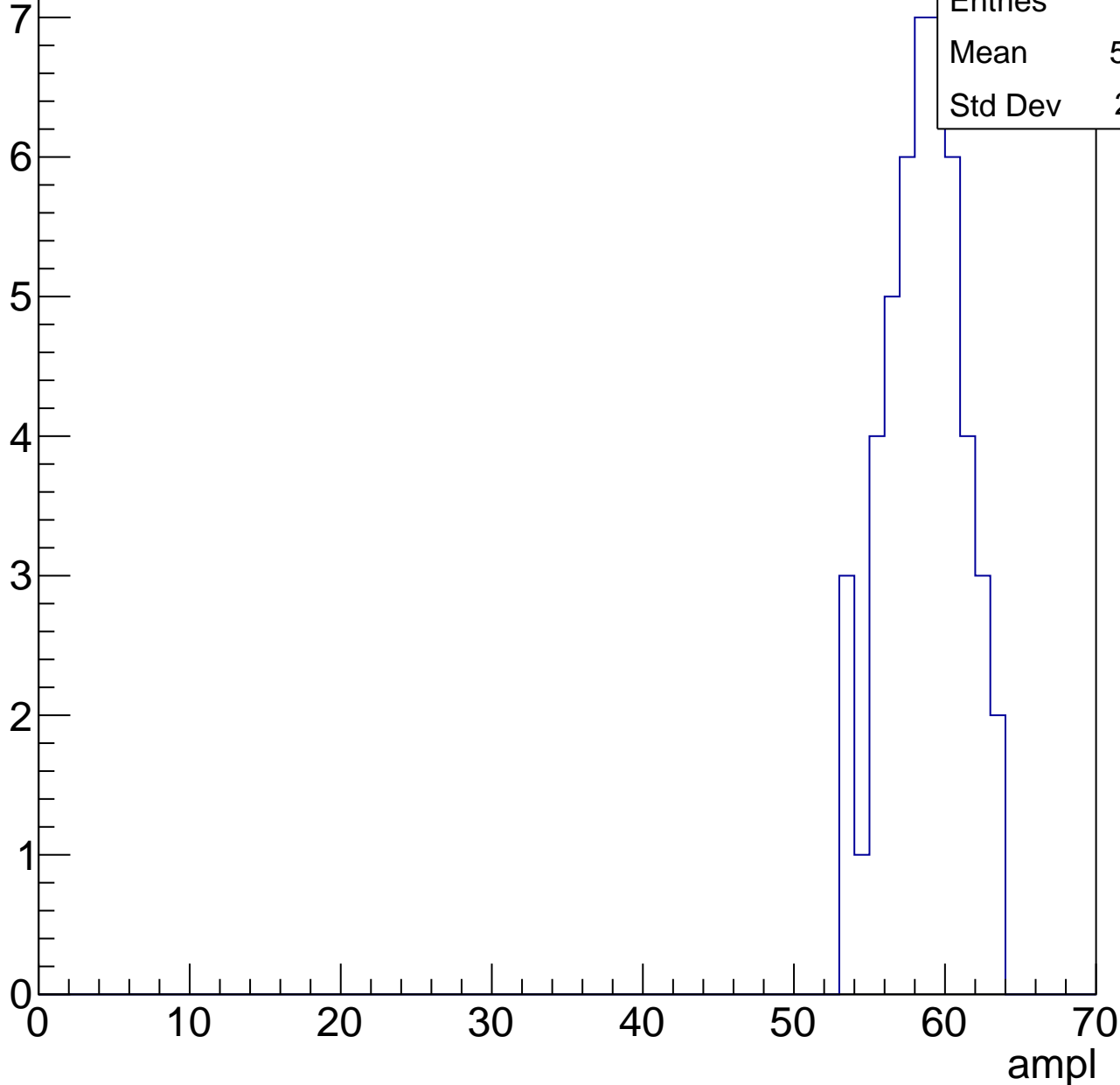
Entries	72
Mean	51.38
Std Dev	3.565



B1L103S, U23-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



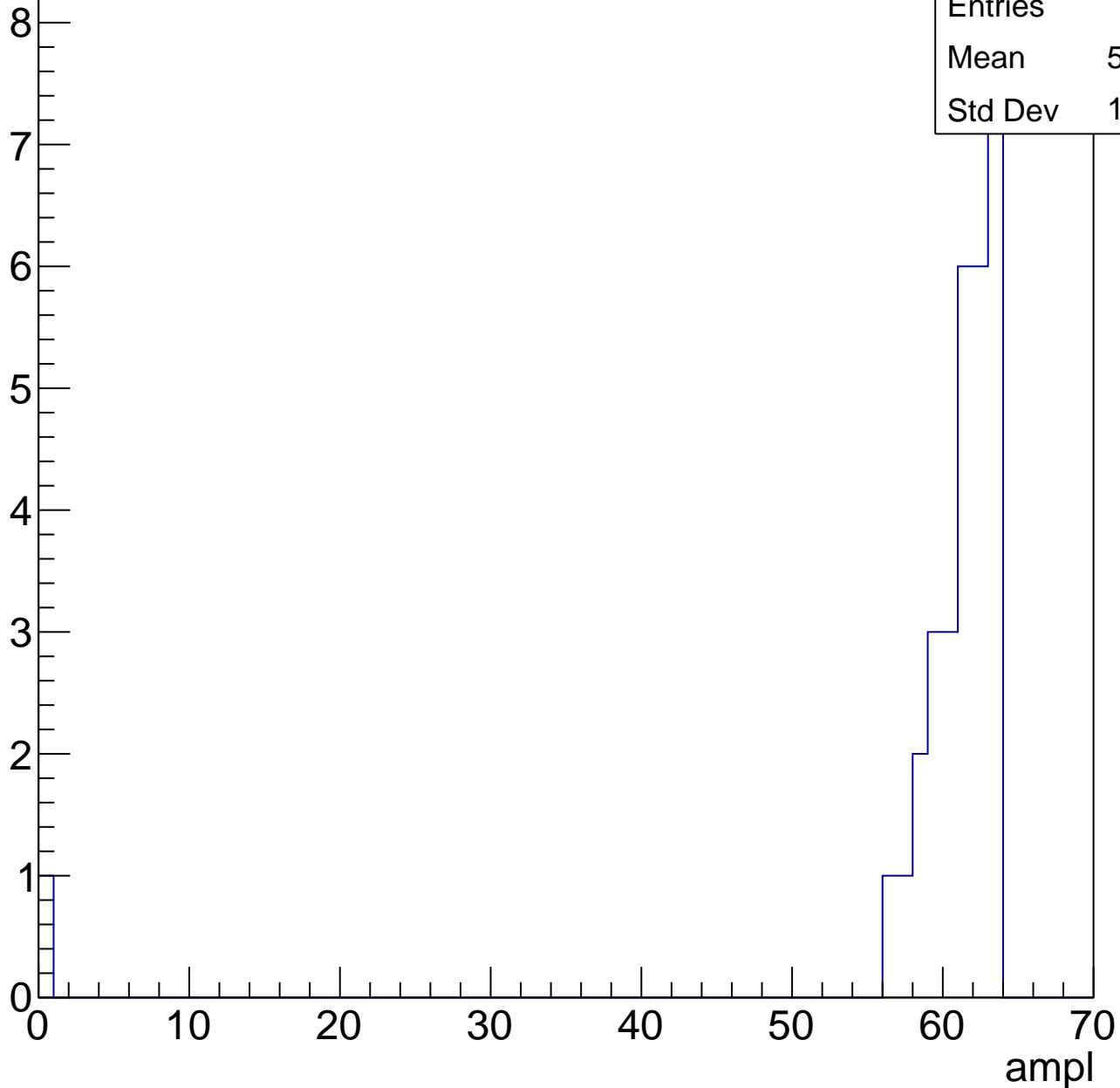
Entries	48
Mean	58.12
Std Dev	2.571

B1L103S, U23-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	58.97
Std Dev	10.93



B1L103S, U23-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U23-ch30, adc0

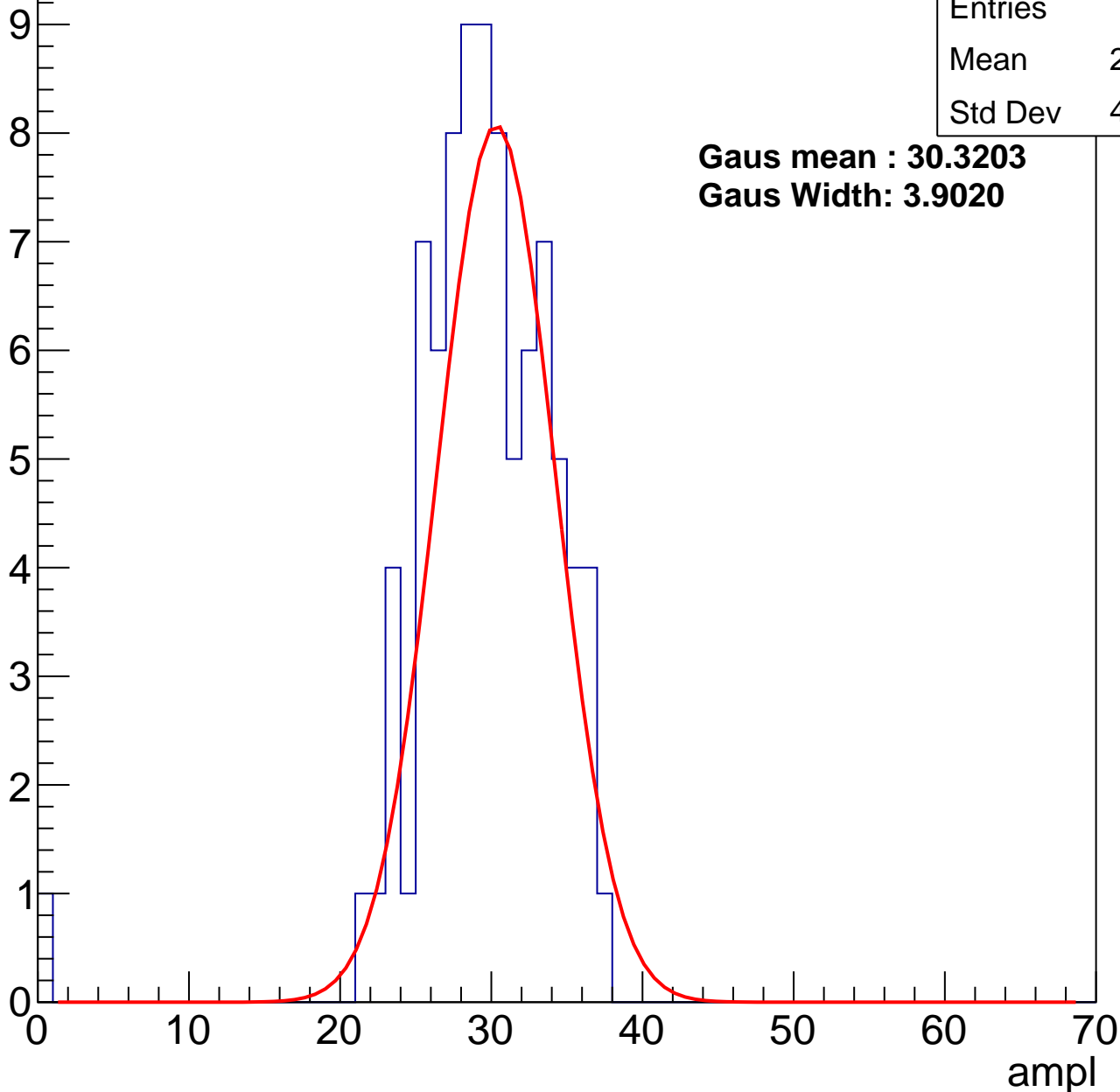
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	29.06
Std Dev	4.857

Gaus mean : 30.3203

Gaus Width: 3.9020



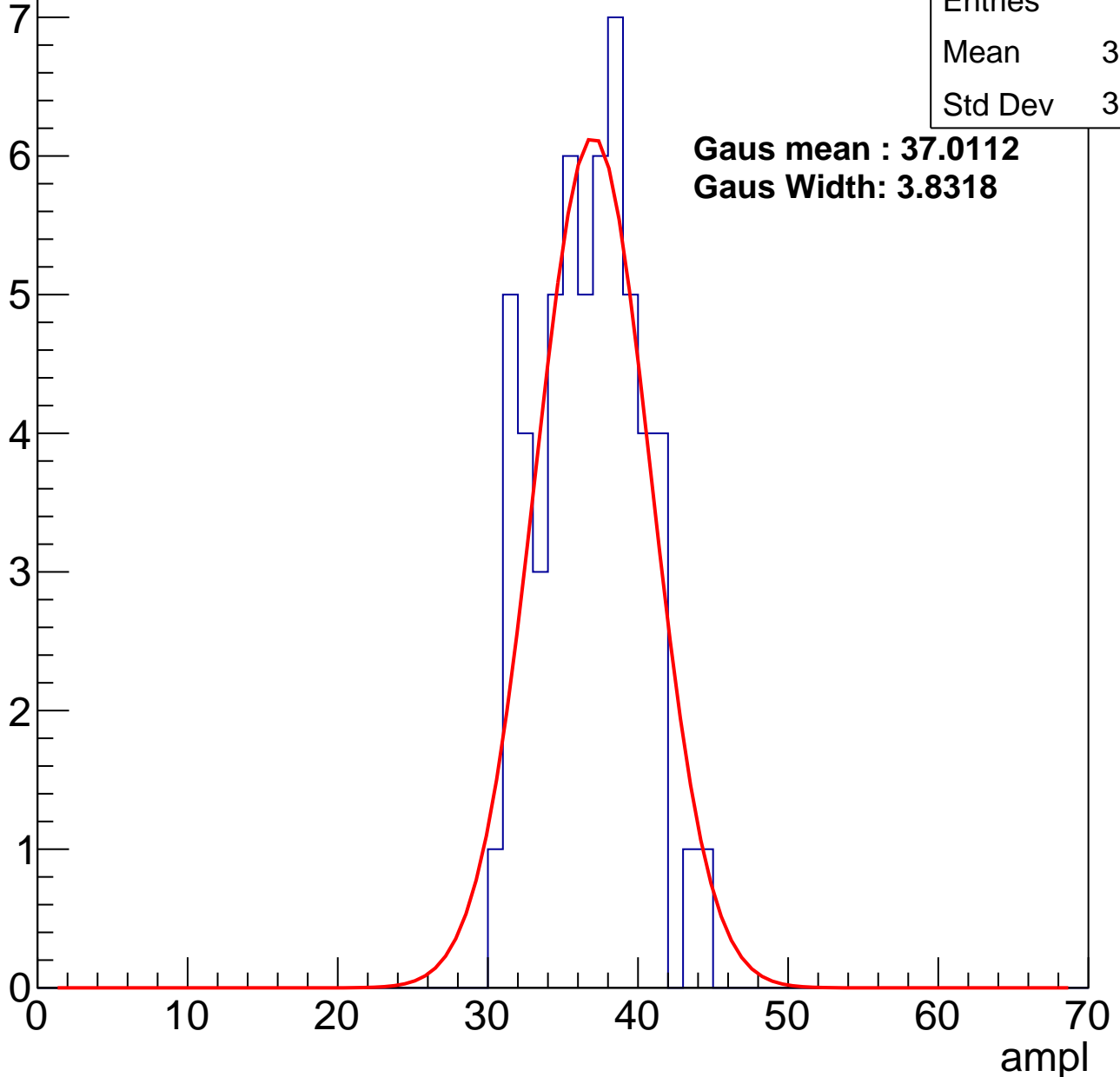
B1L103S, U23-ch30, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.25
Std Dev	3.326

Gaus mean : 37.0112
Gaus Width: 3.8318



B1L103S, U23-ch30, adc2

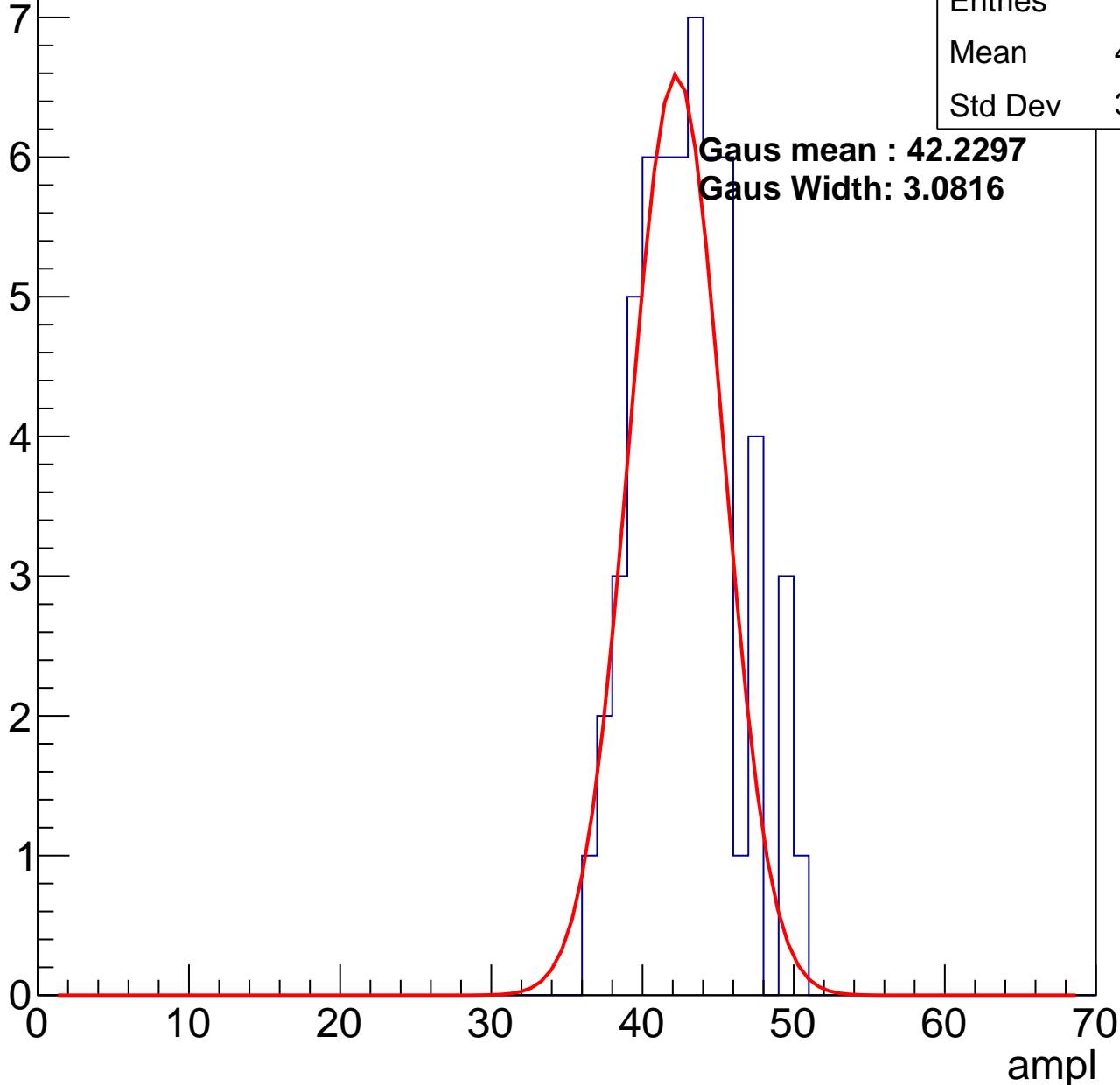
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.51
Std Dev	3.261

Gaus mean : 42.2297

Gaus Width: 3.0816

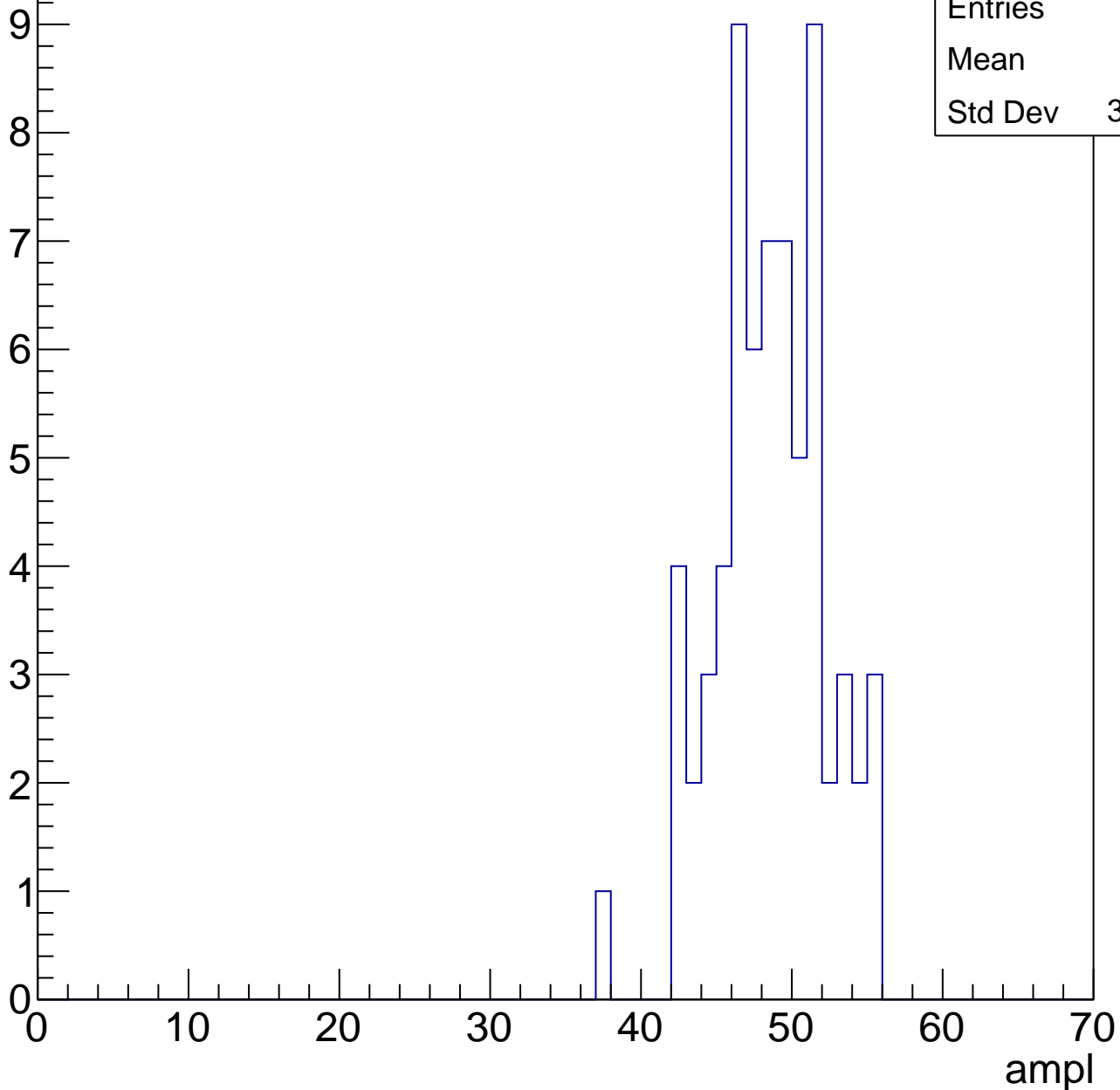


B1L103S, U23-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.1
Std Dev	3.612

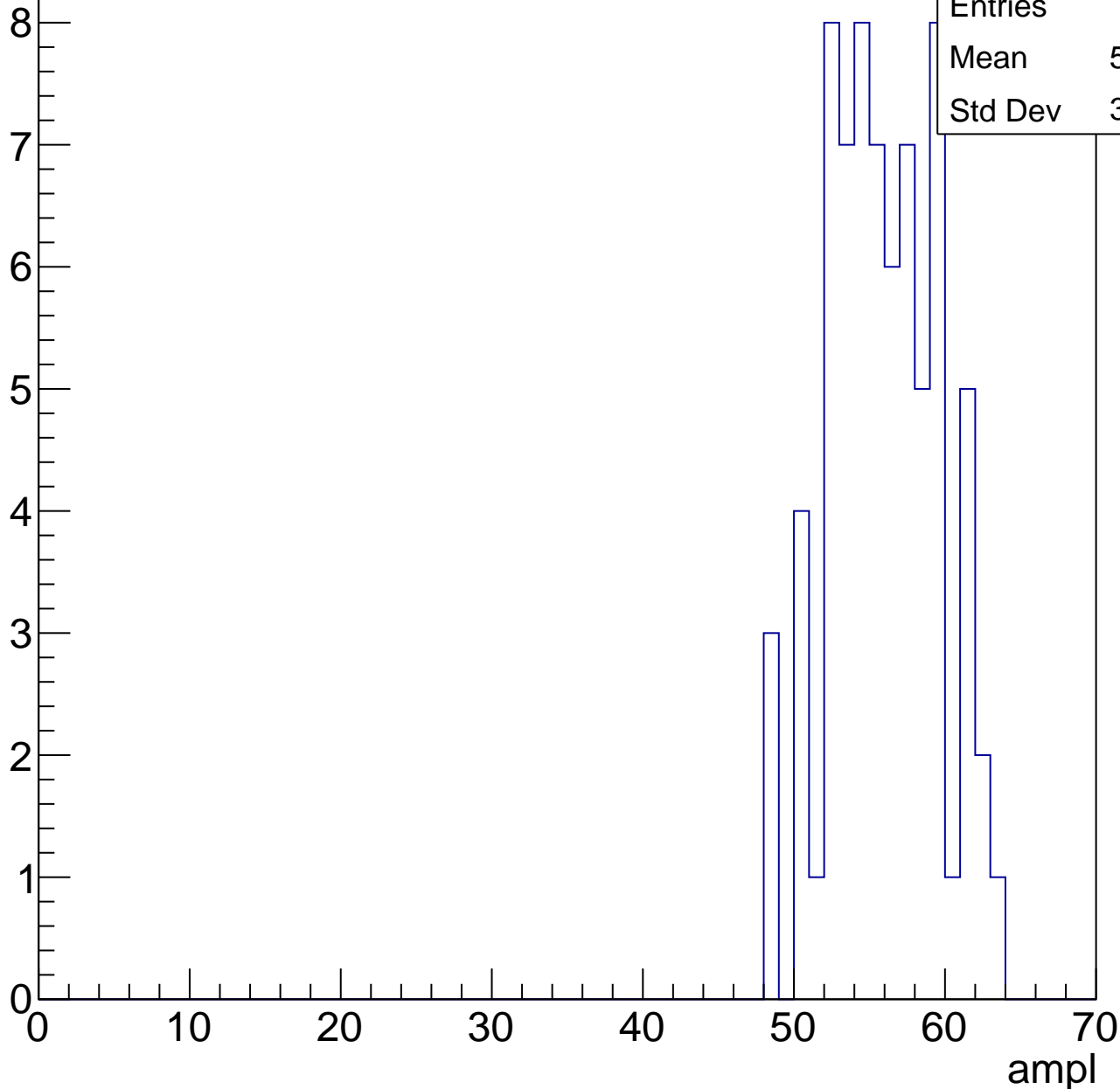


B1L103S, U23-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	55.45
Std Dev	3.569

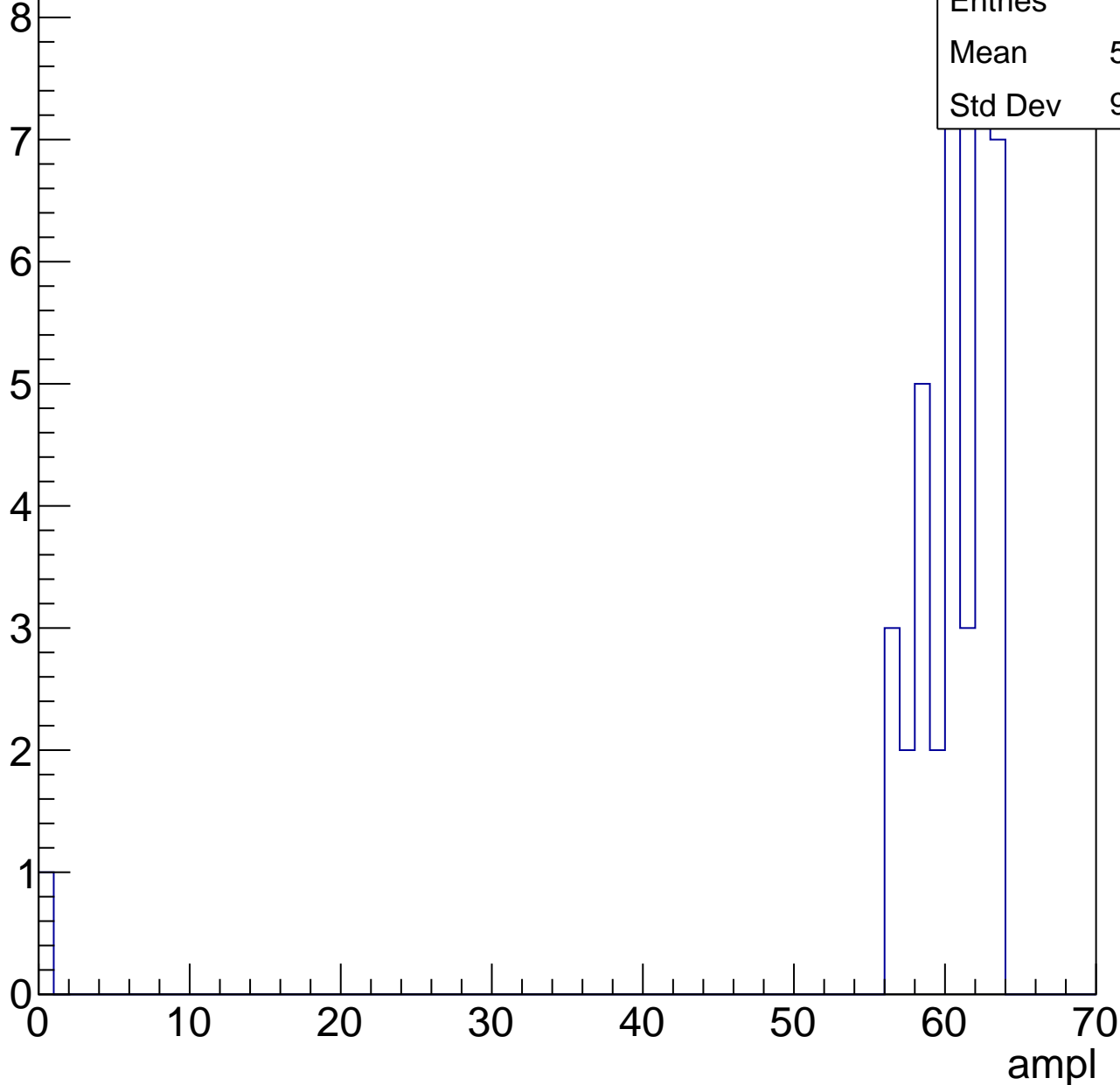


B1L103S, U23-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

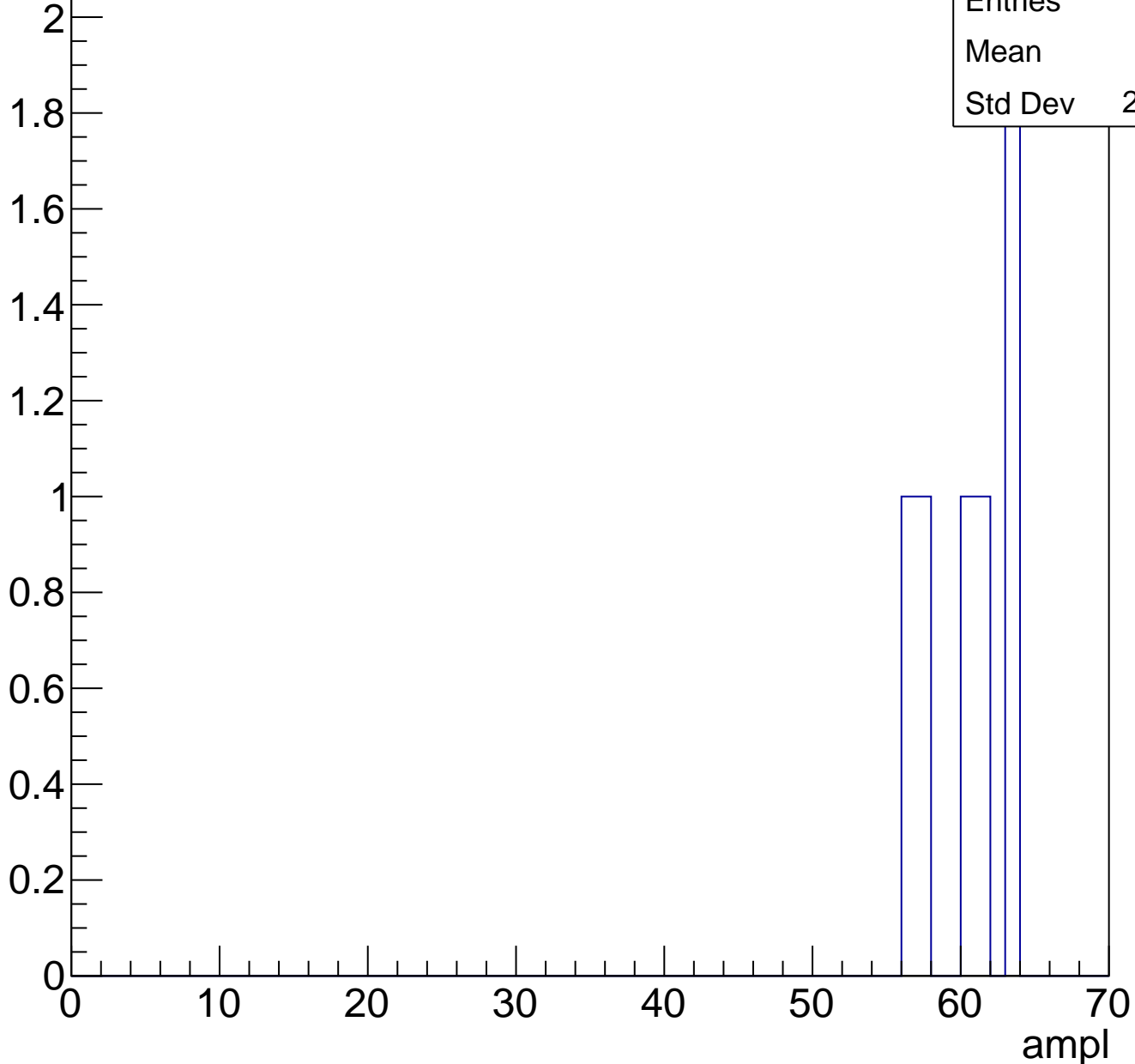
Entries	39
Mean	58.72
Std Dev	9.769



B1L103S, U23-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

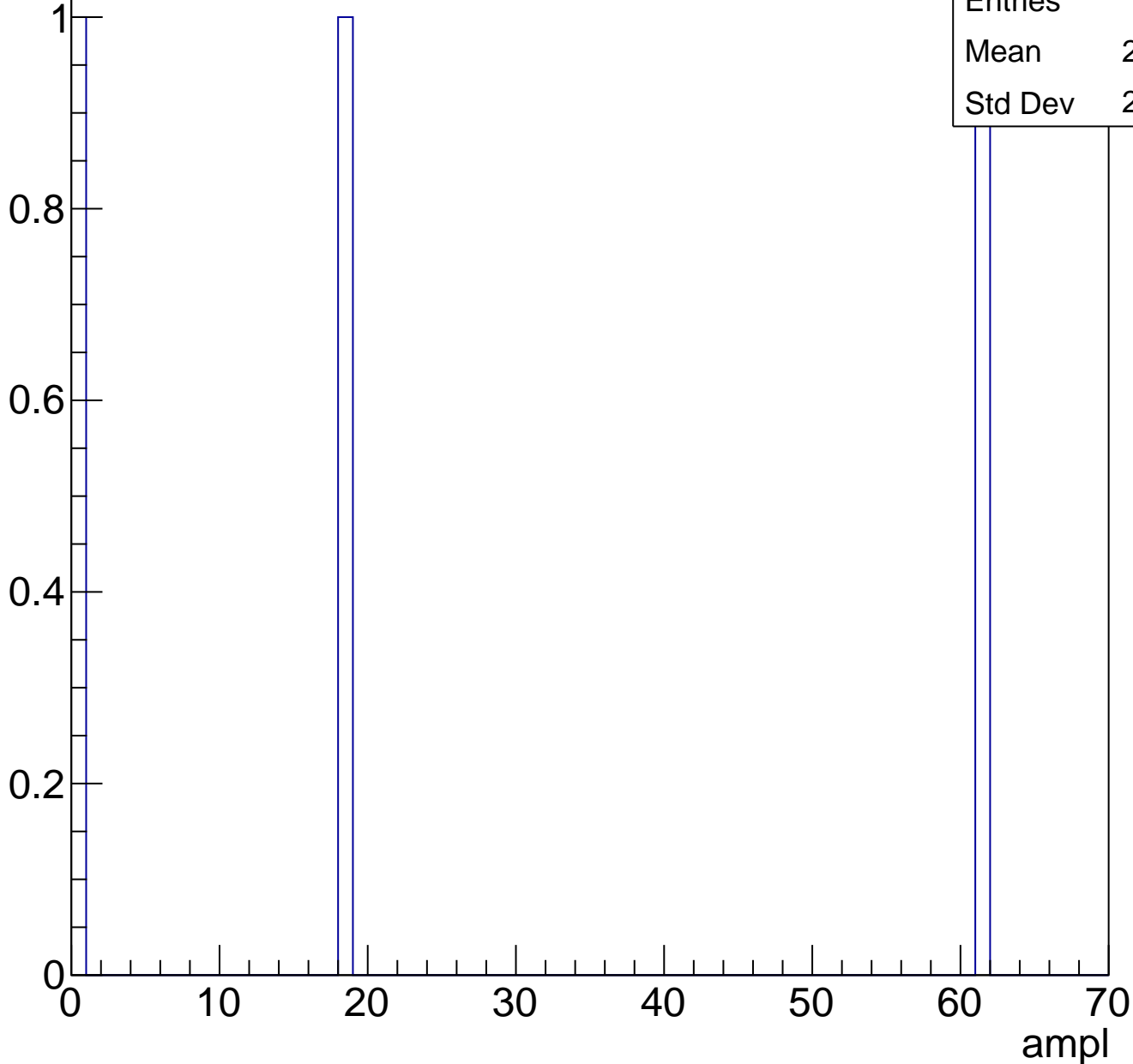


Entries	6
Mean	60
Std Dev	2.708

B1L103S, U23-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	26.33
Std Dev	25.59

B1L103S, U23-ch31, adc0

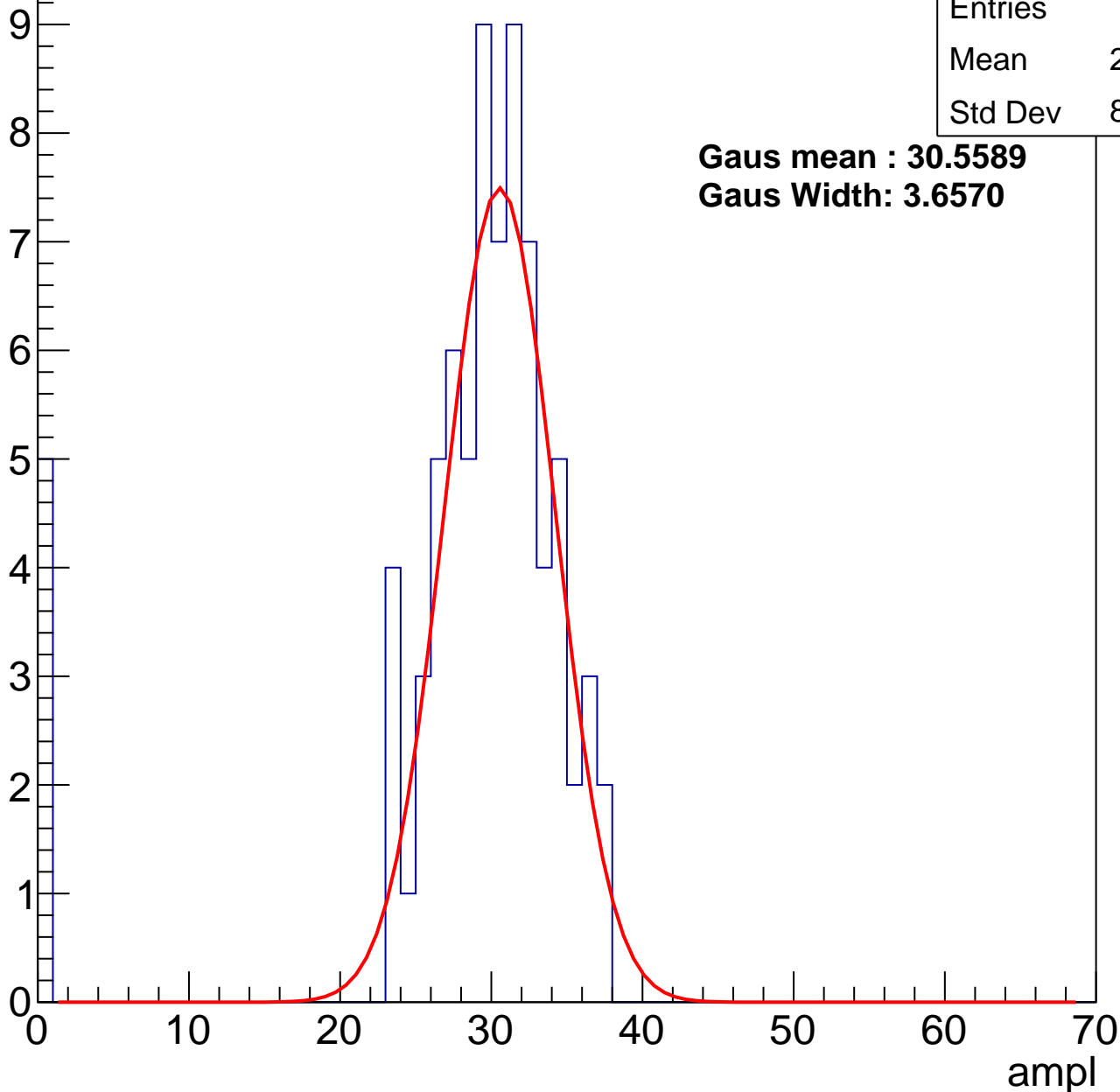
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	27.94
Std Dev	8.099

Gaus mean : 30.5589

Gaus Width: 3.6570



B1L103S, U23-ch31, adc1

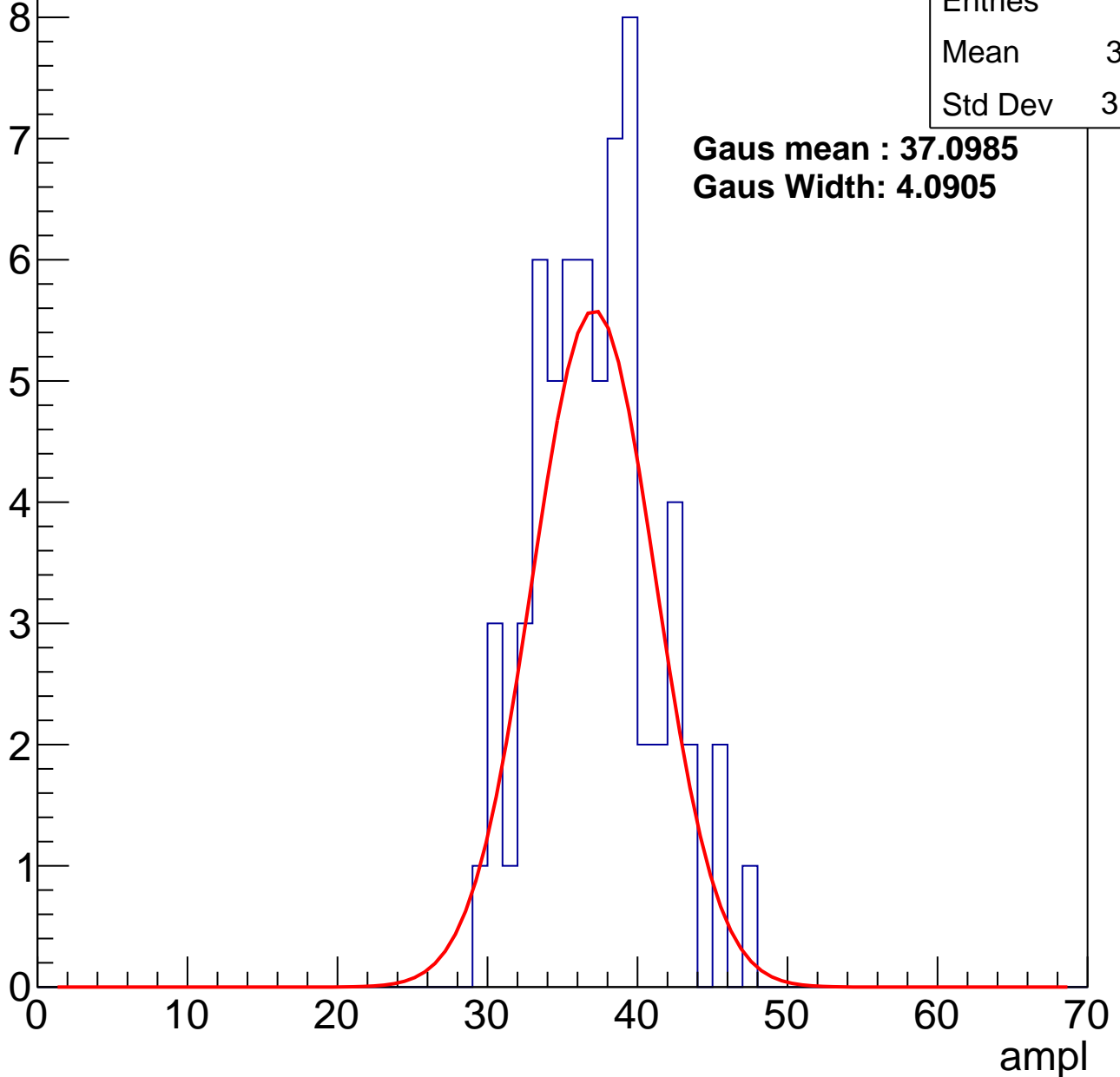
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.81
Std Dev	3.913

Gaus mean : 37.0985

Gaus Width: 4.0905



B1L103S, U23-ch31, adc2

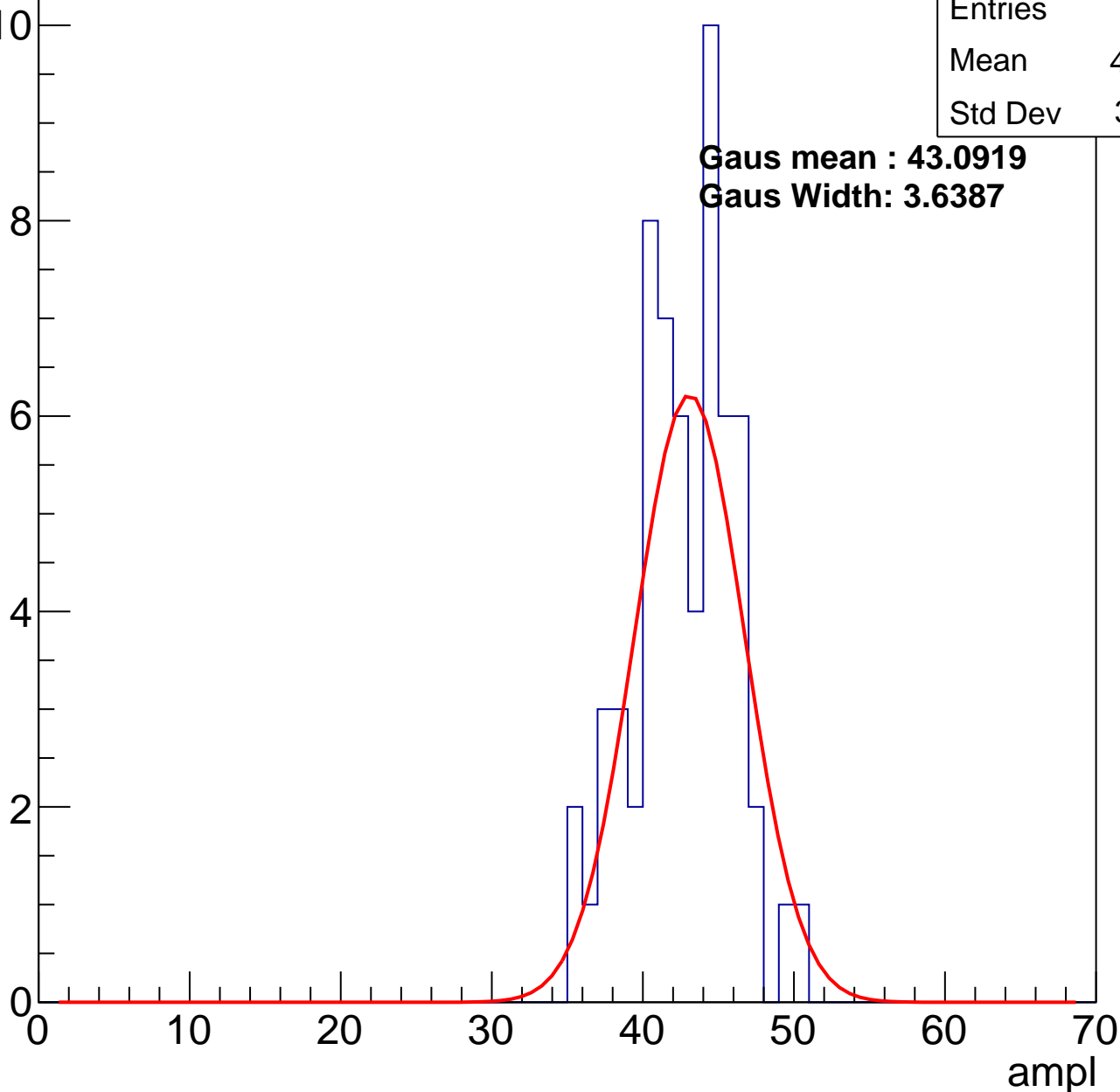
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.24
Std Dev	3.281

Gaus mean : 43.0919

Gaus Width: 3.6387

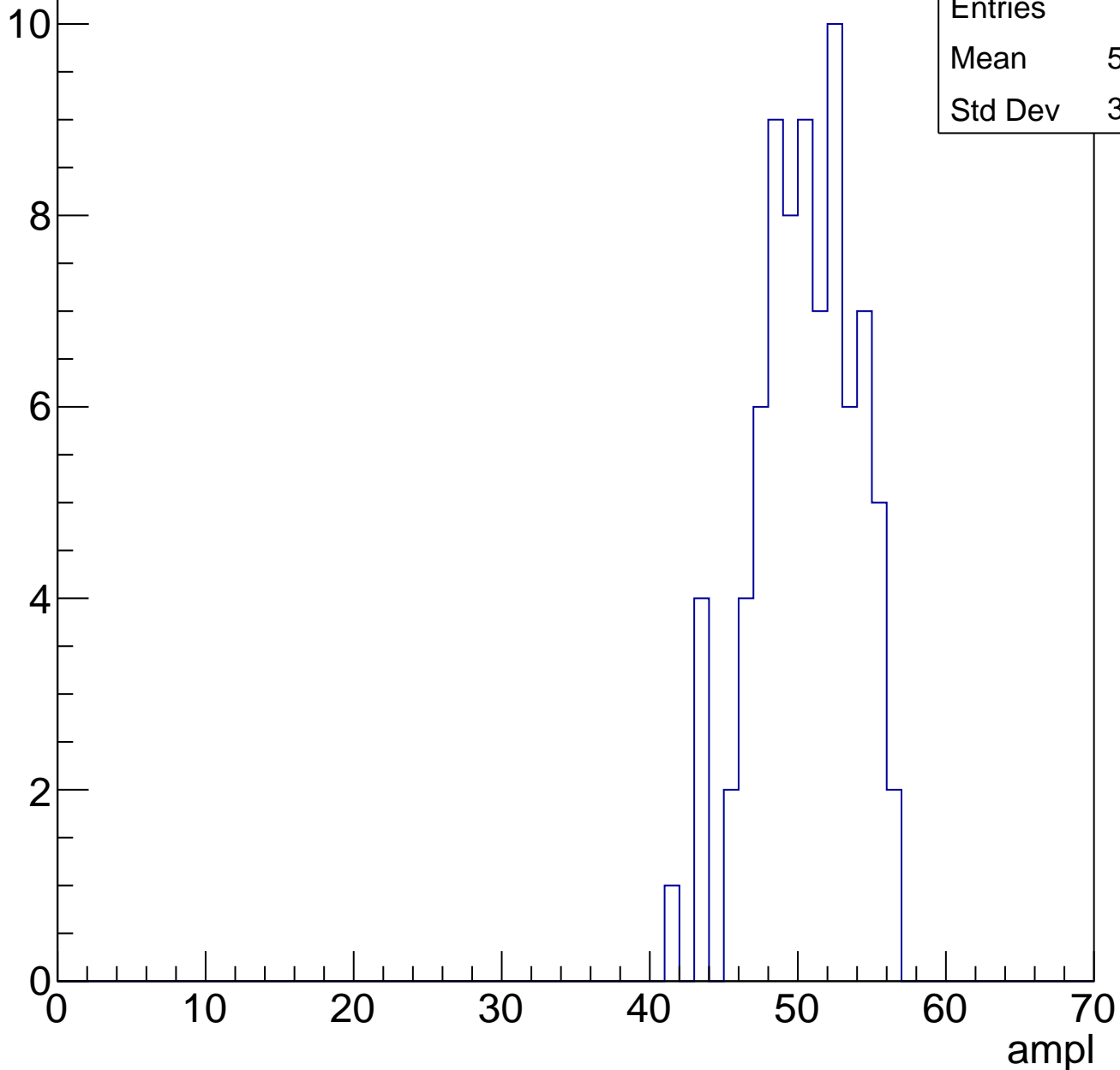


B1L103S, U23-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	50.04
Std Dev	3.352

Entry

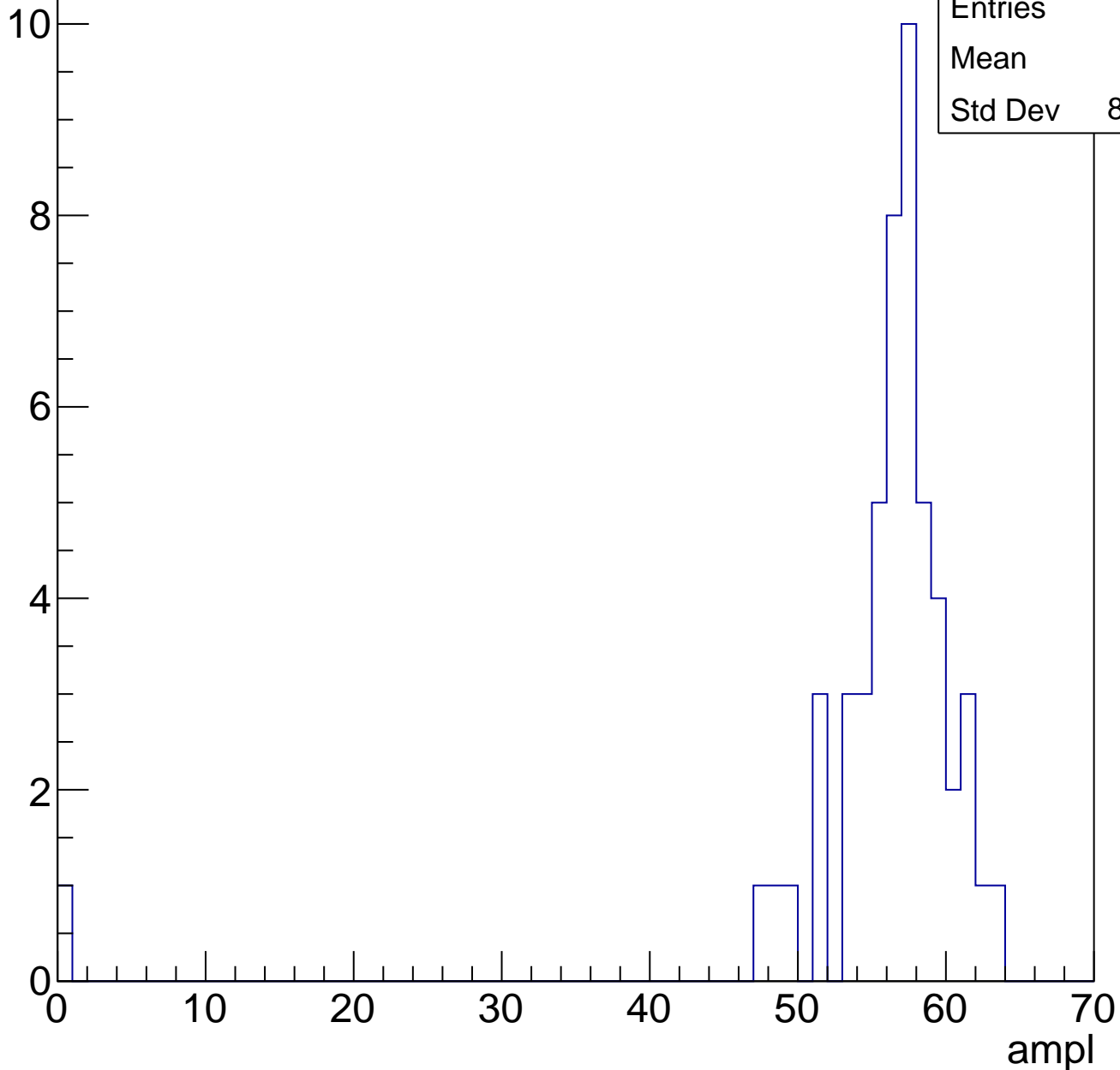


B1L103S, U23-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	52
Mean	55.1
Std Dev	8.395

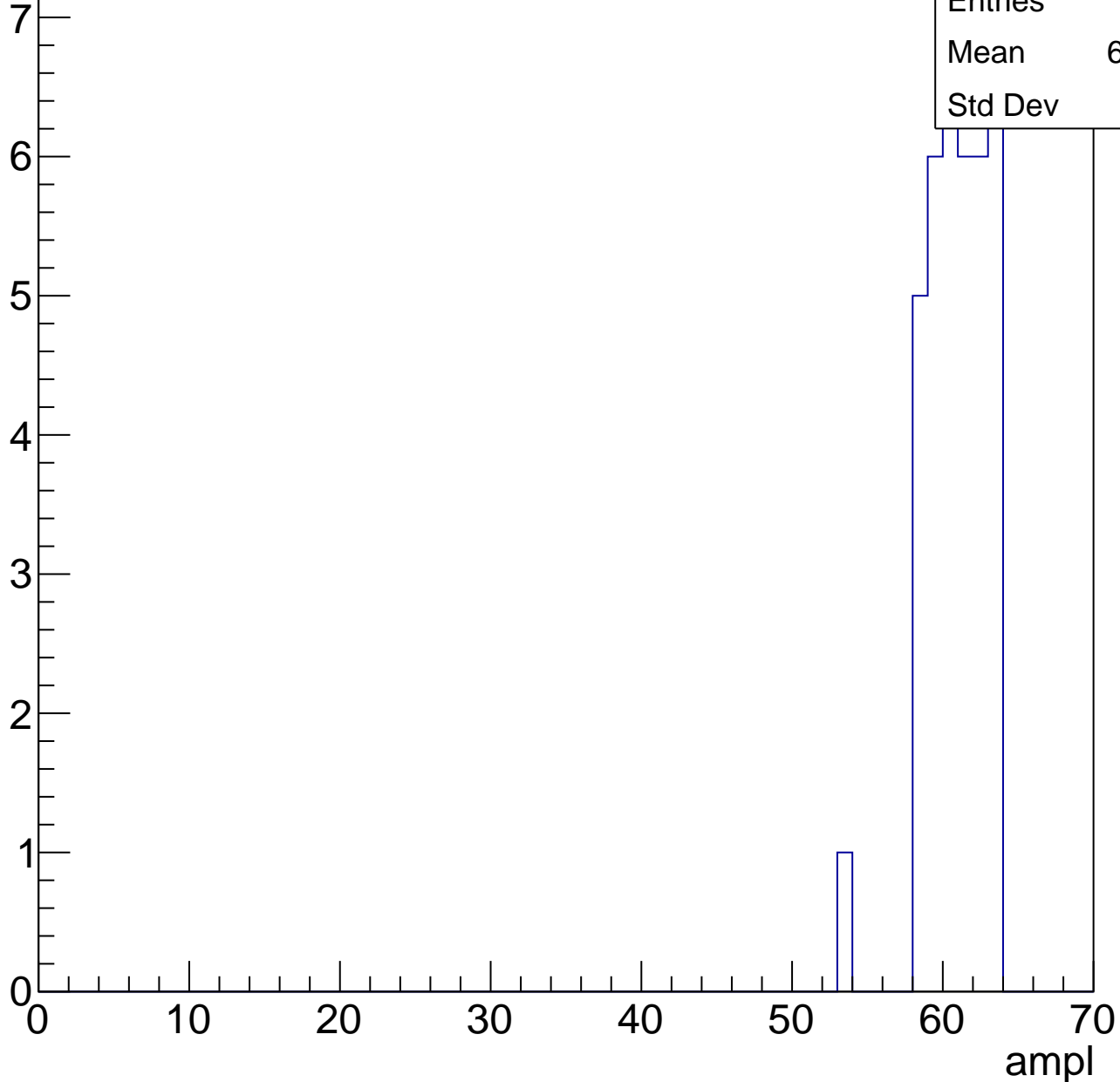
Entry



B1L103S, U23-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch32, adc0

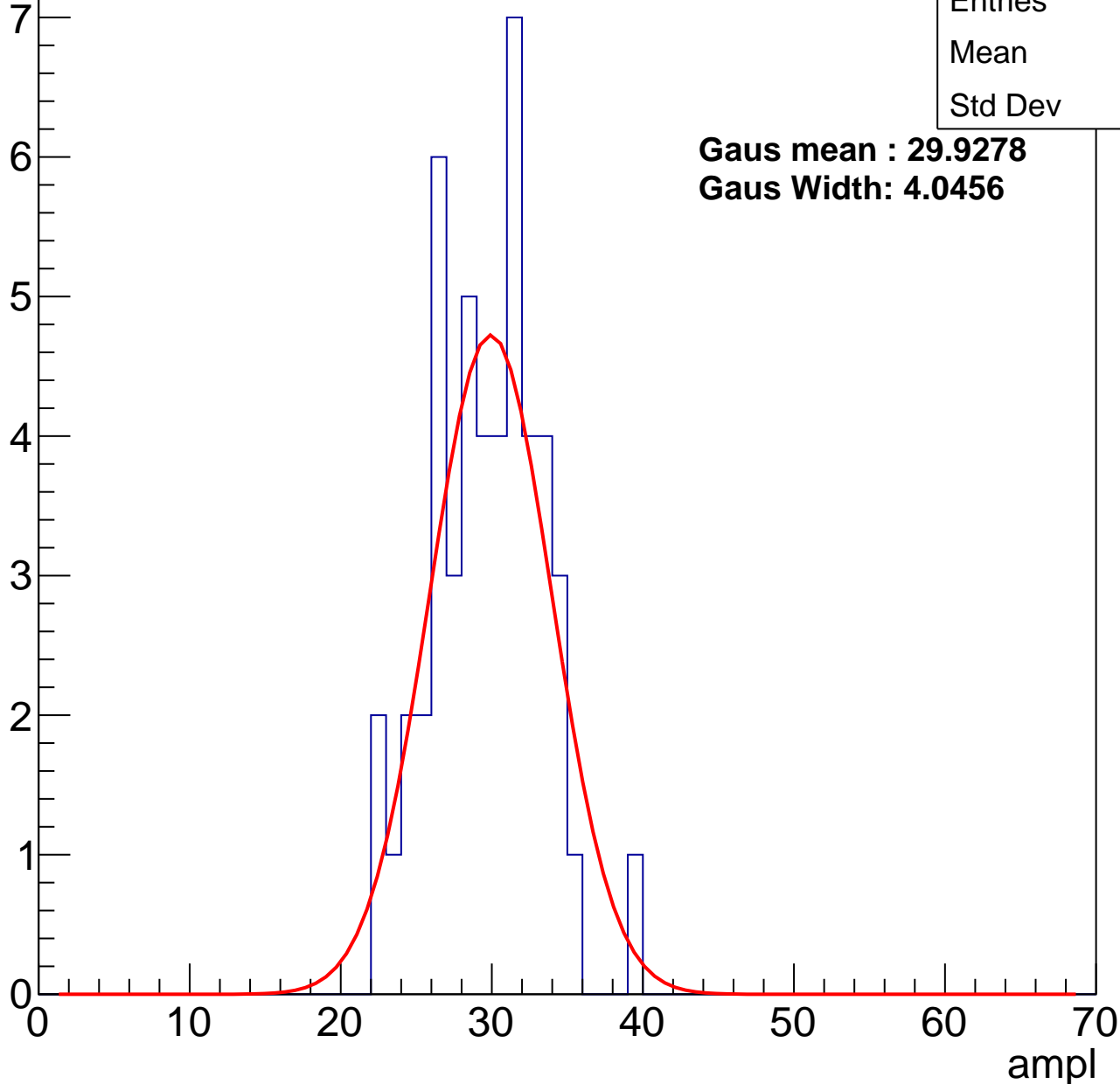
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	29.2
Std Dev	3.58

Gaus mean : 29.9278

Gaus Width: 4.0456



B1L103S, U23-ch32, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	35.18
Std Dev	3.812

Gaus mean : 35.3509

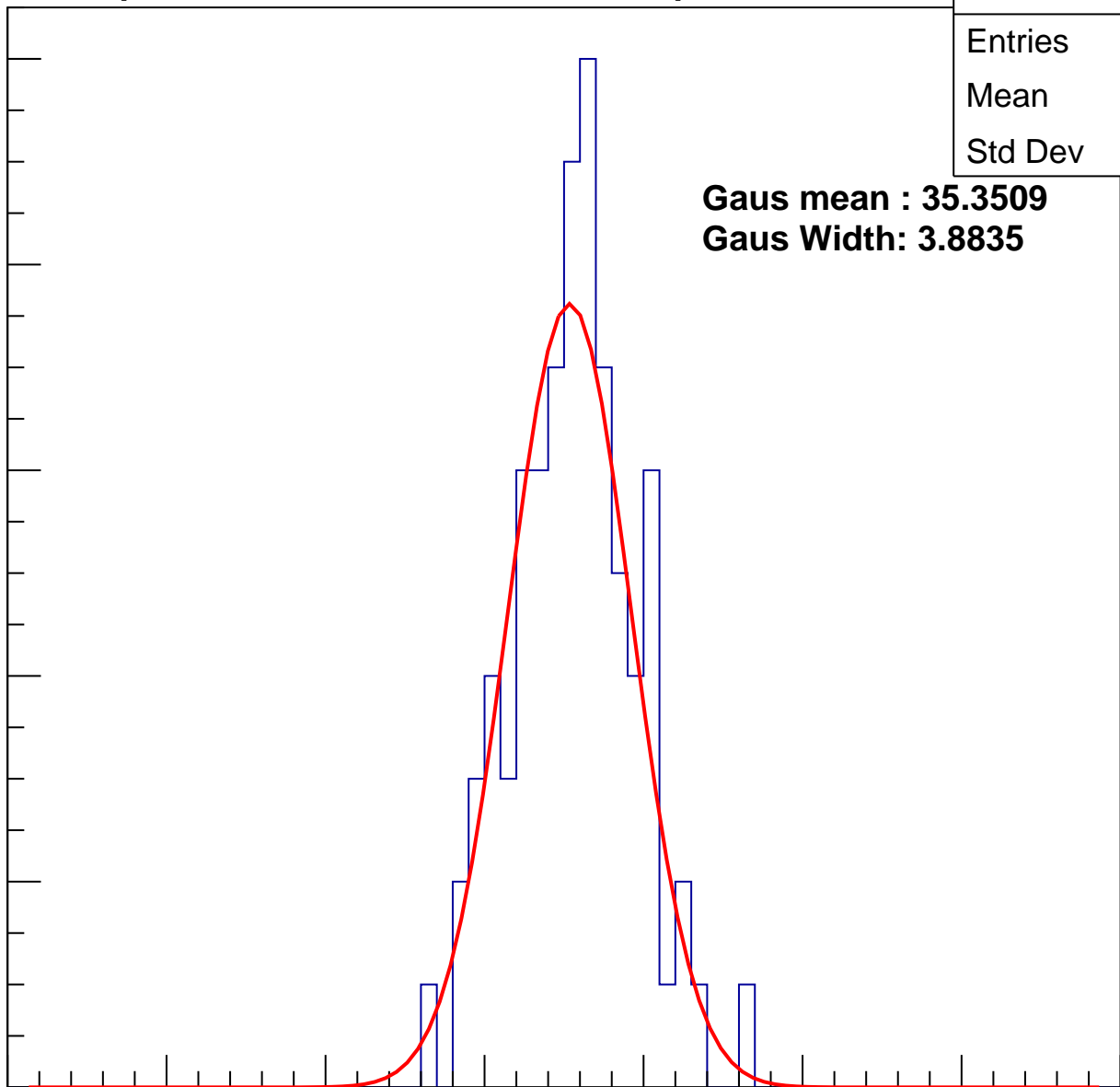
Gaus Width: 3.8835

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch32, adc2

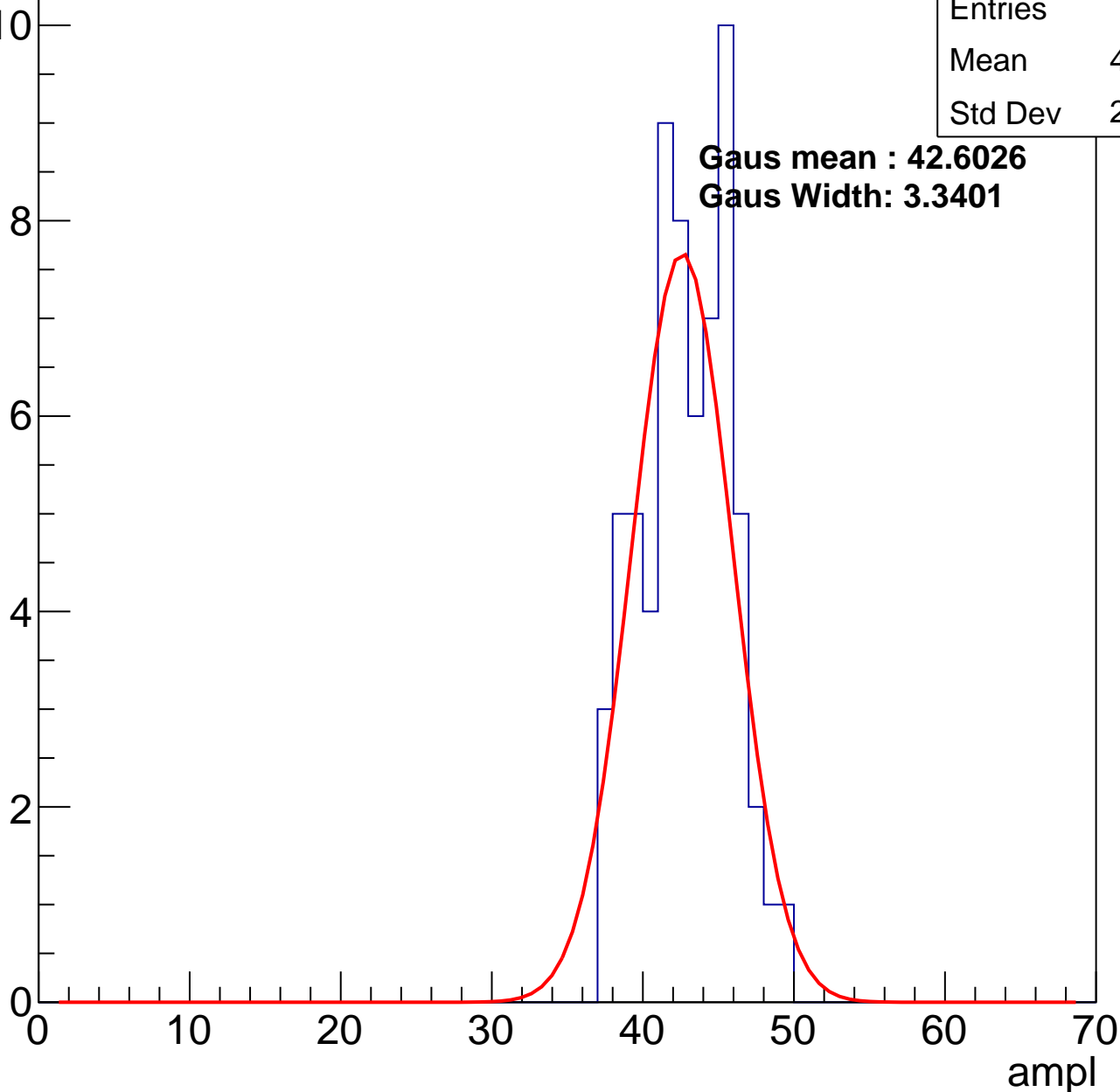
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.39
Std Dev	2.897

Gaus mean : 42.6026

Gaus Width: 3.3401

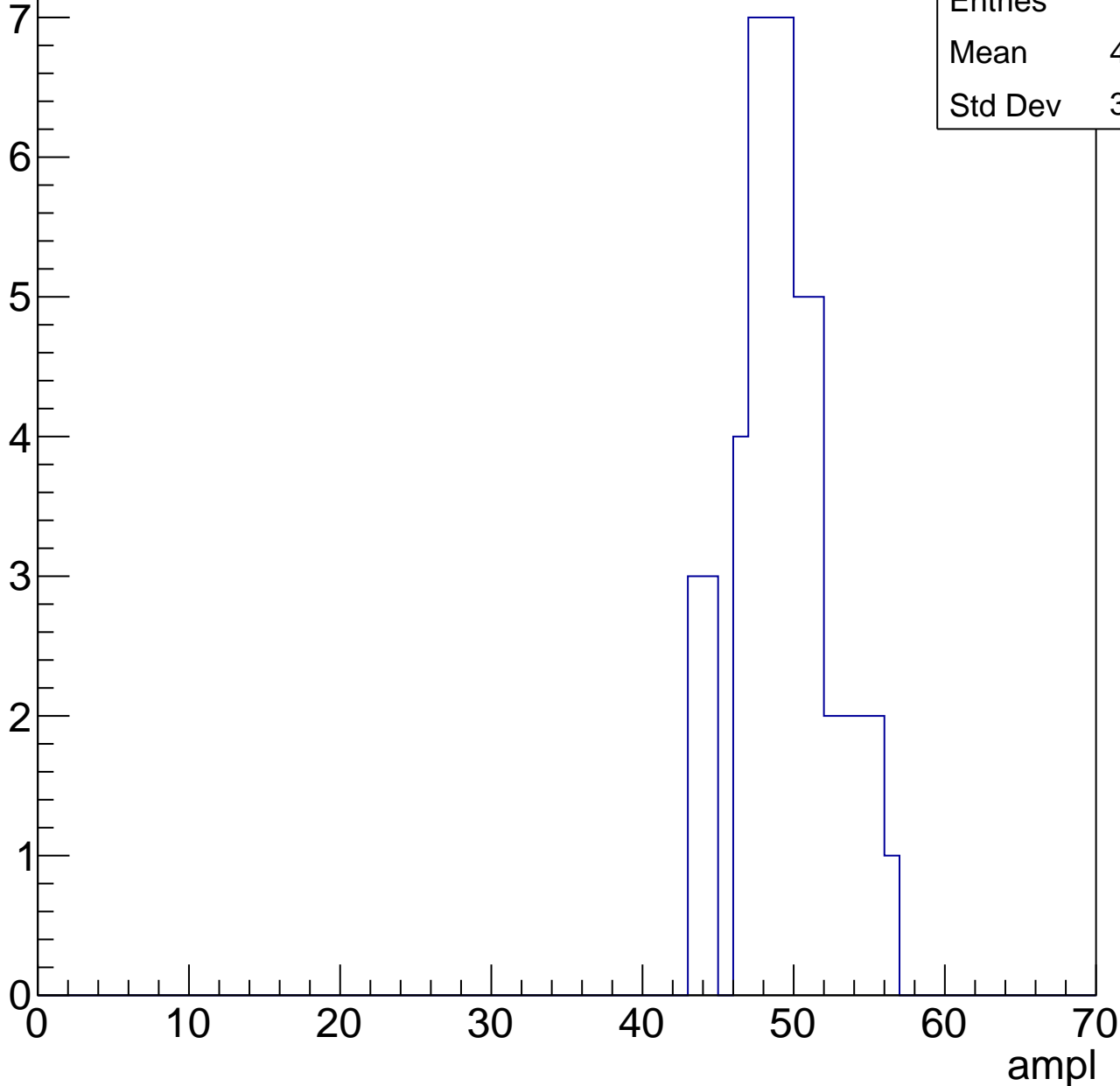


B1L103S, U23-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

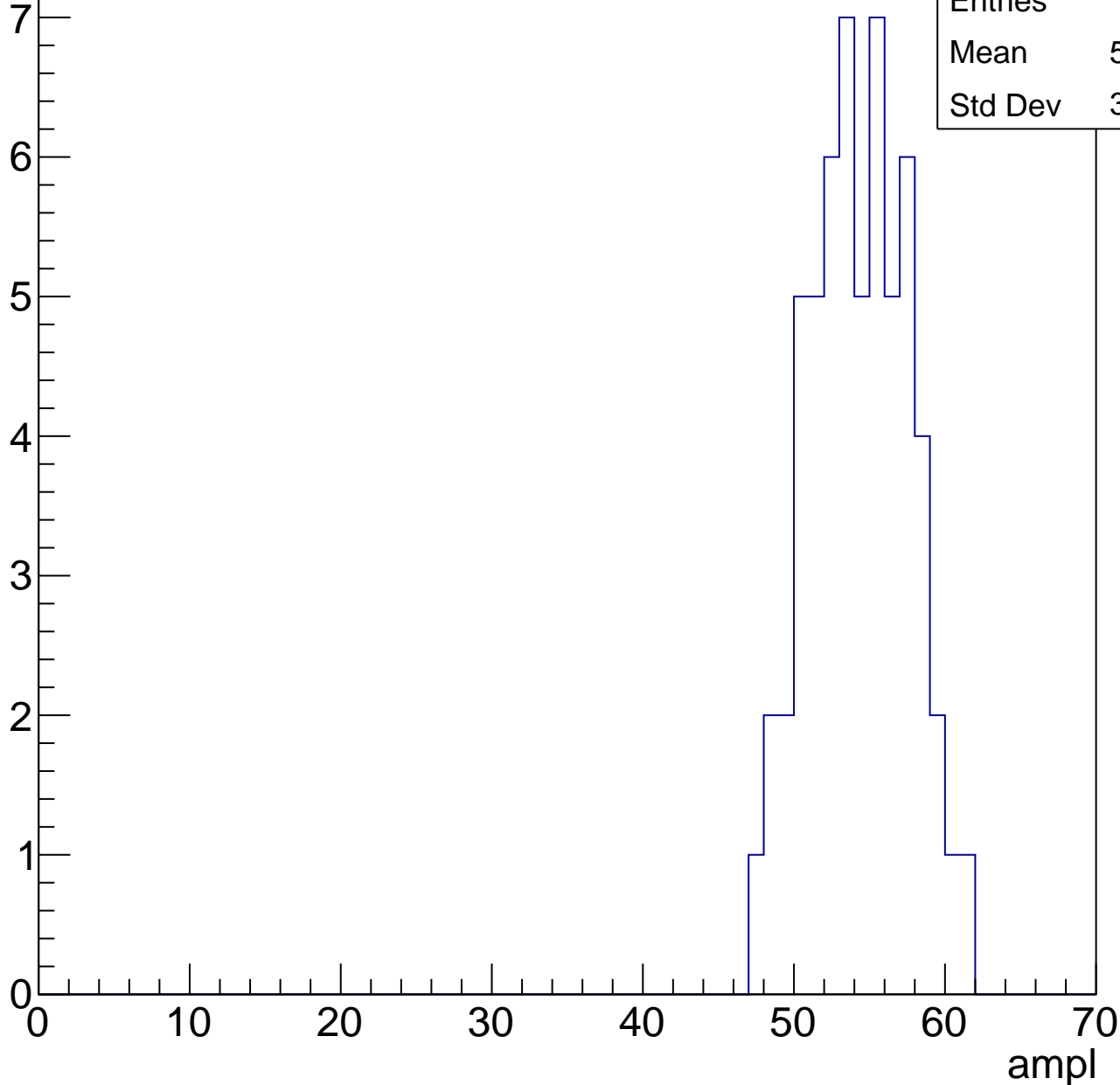
Entries	50
Mean	48.84
Std Dev	3.158



B1L103S, U23-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	59
Mean	53.85
Std Dev	3.204

B1L103S, U23-ch32, adc5

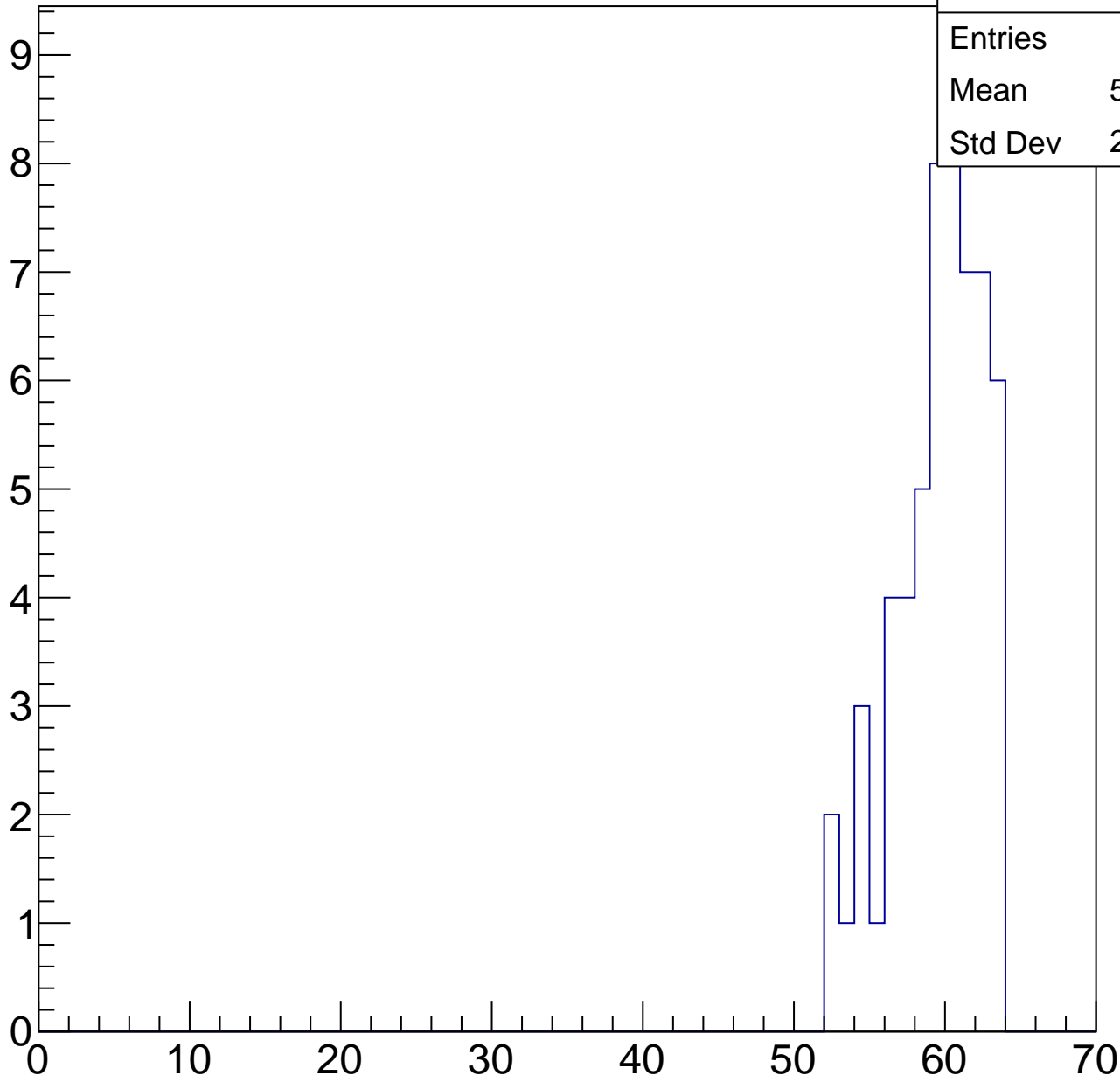
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	59.07
Std Dev	2.895

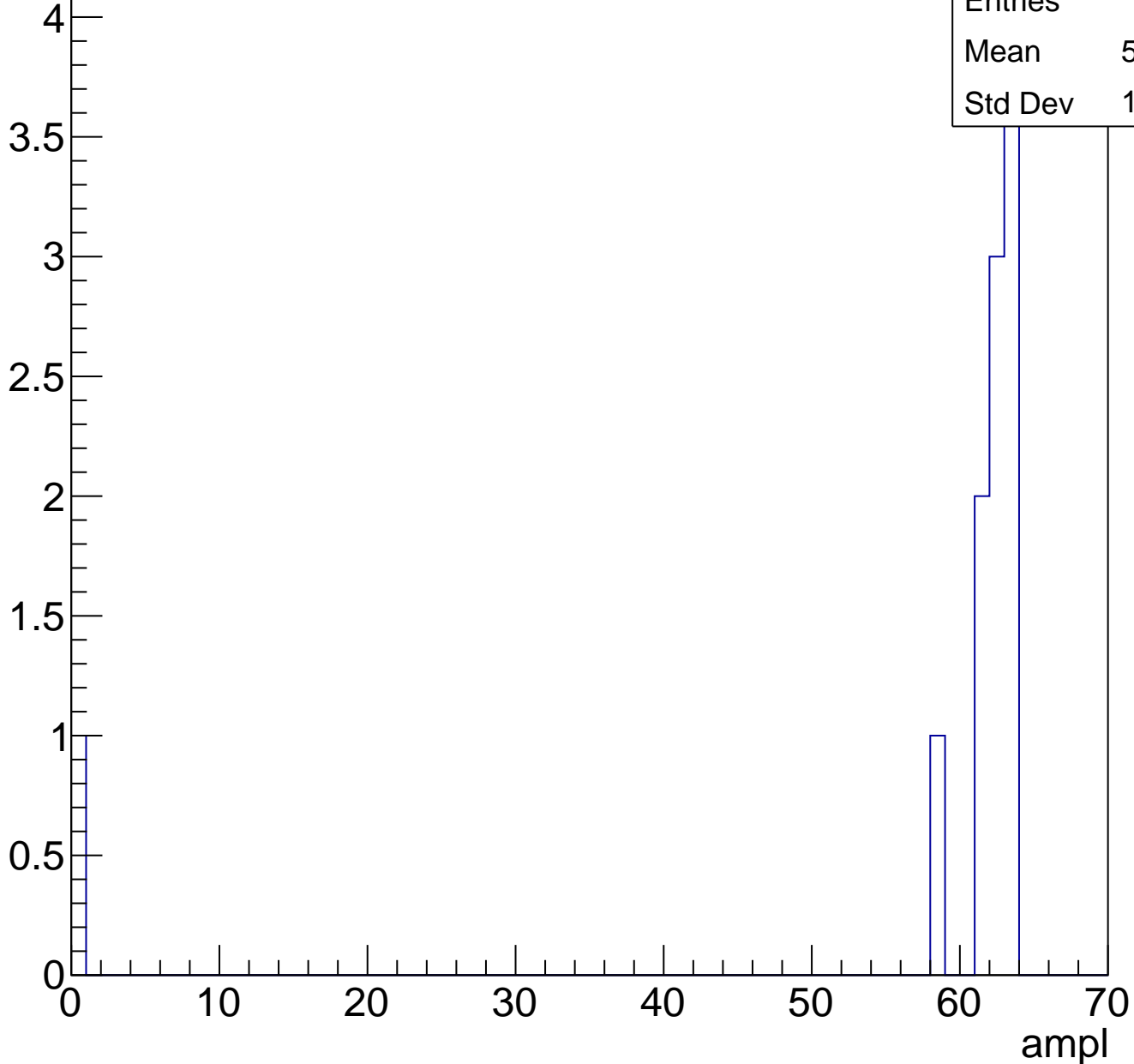
ampl



B1L103S, U23-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch33, adc0

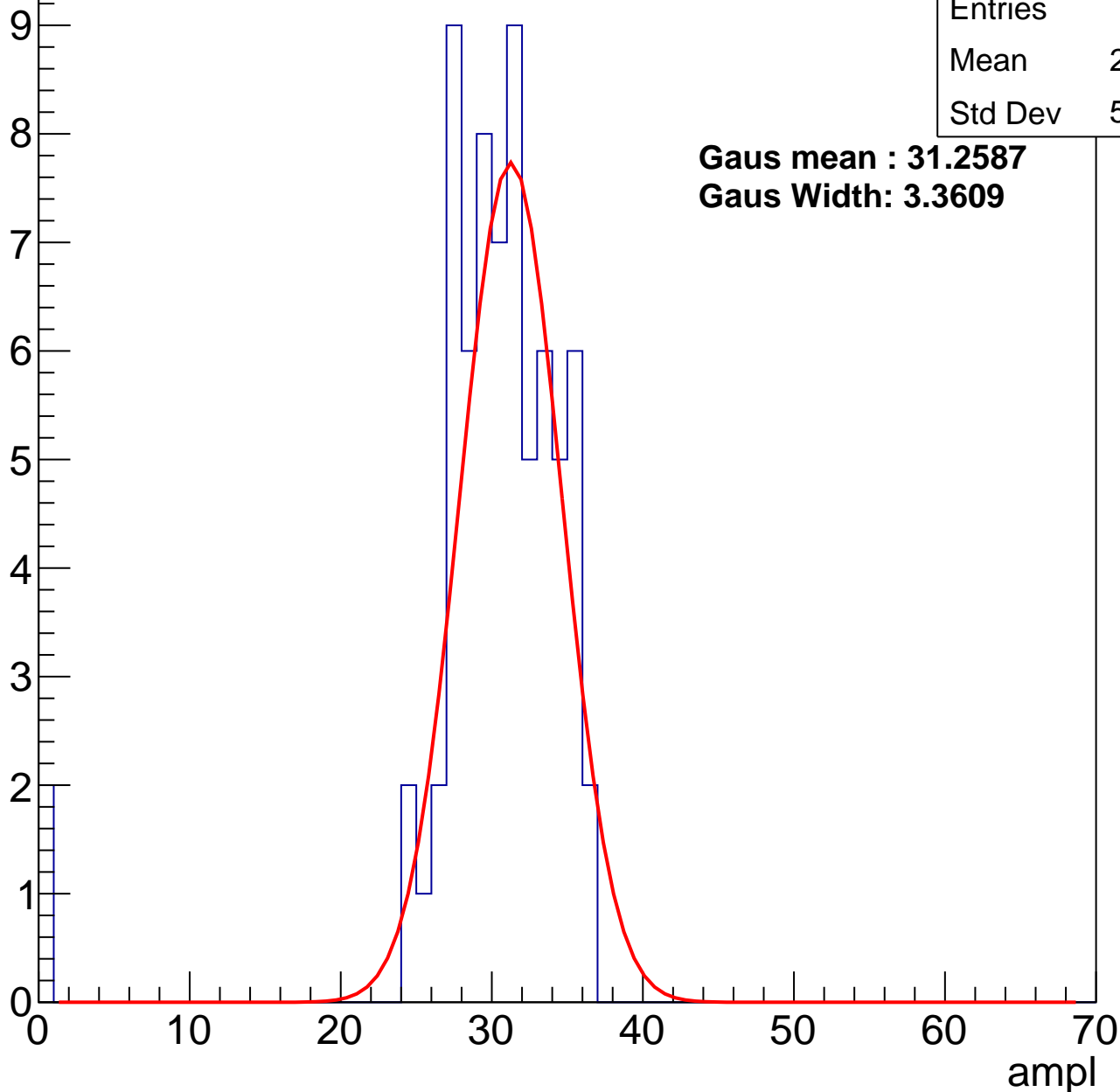
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.53
Std Dev	5.869

Gaus mean : 31.2587

Gaus Width: 3.3609



B1L103S, U23-ch33, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	37.71
Std Dev	3.589

Gaus mean : 37.5740

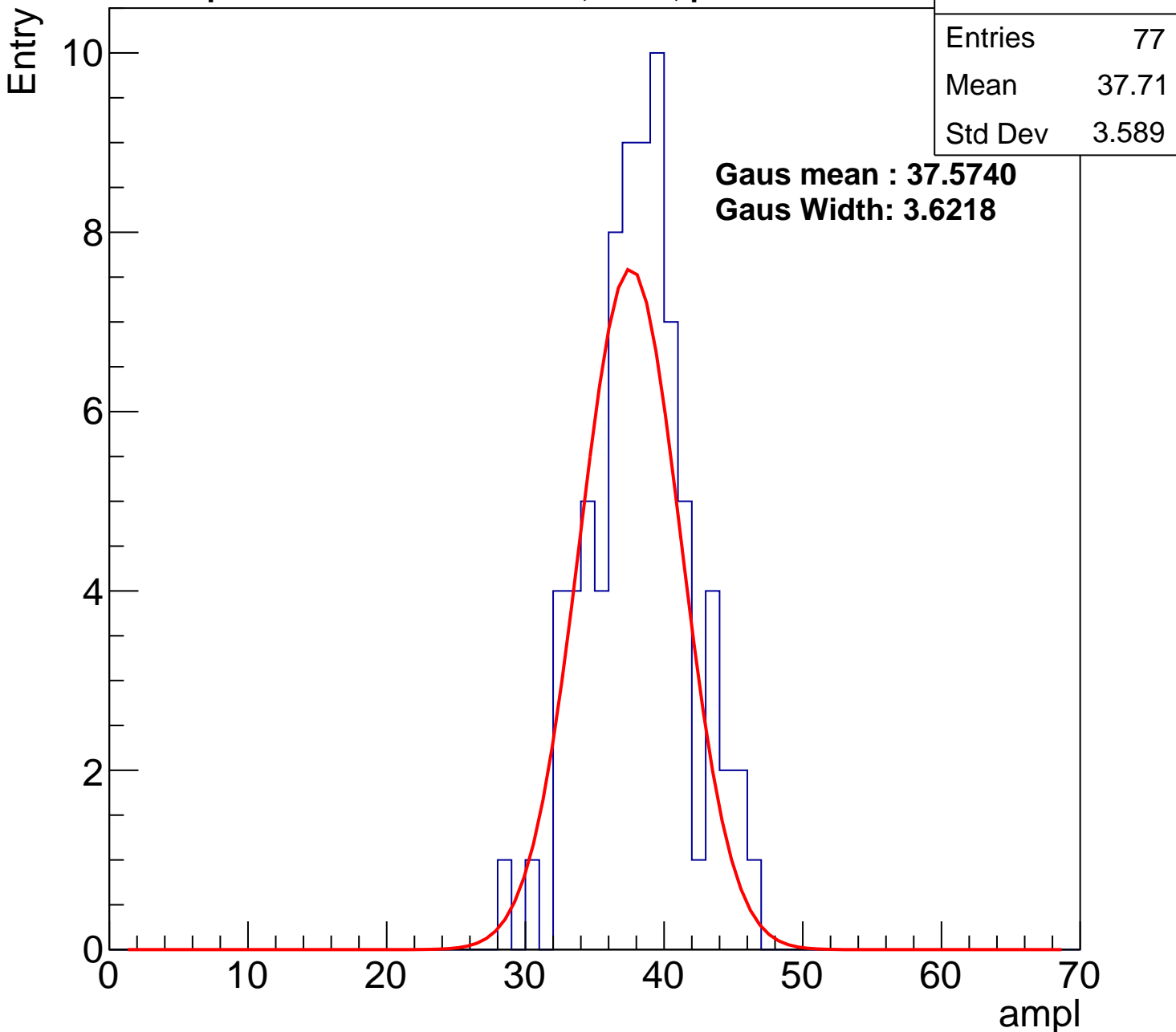
Gaus Width: 3.6218

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch33, adc2

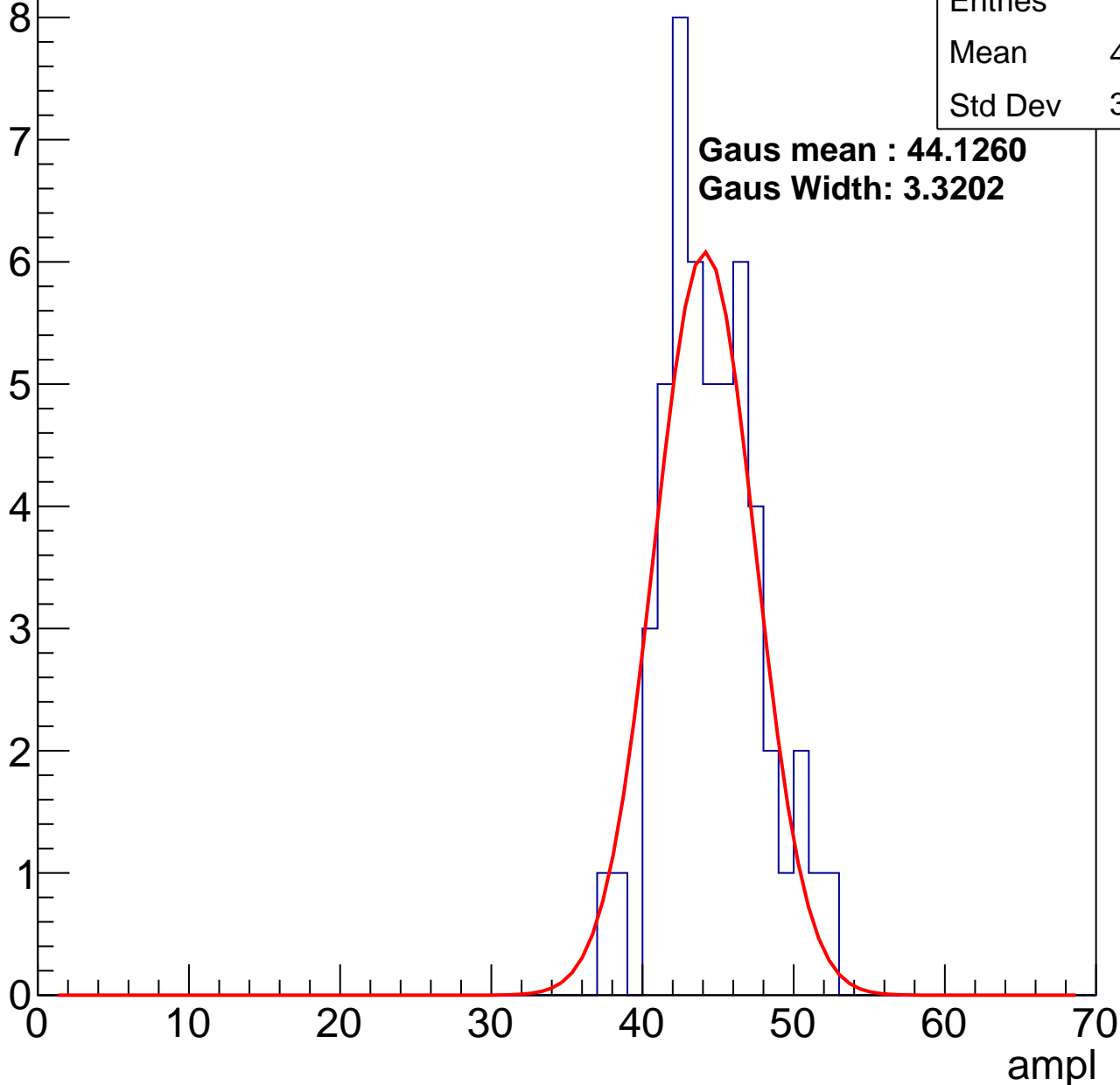
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	44.14
Std Dev	3.199

Gaus mean : 44.1260

Gaus Width: 3.3202

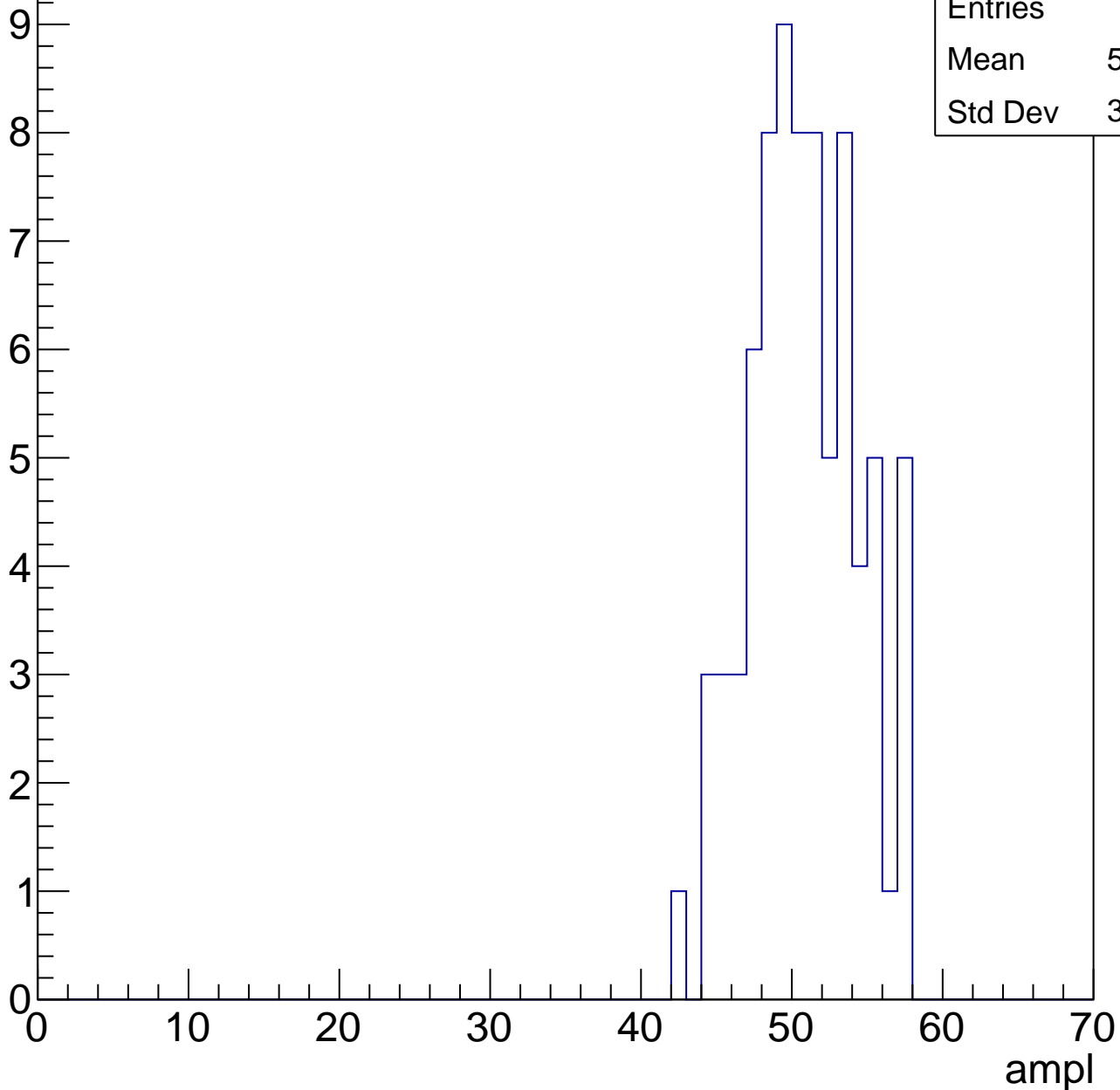


B1L103S, U23-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

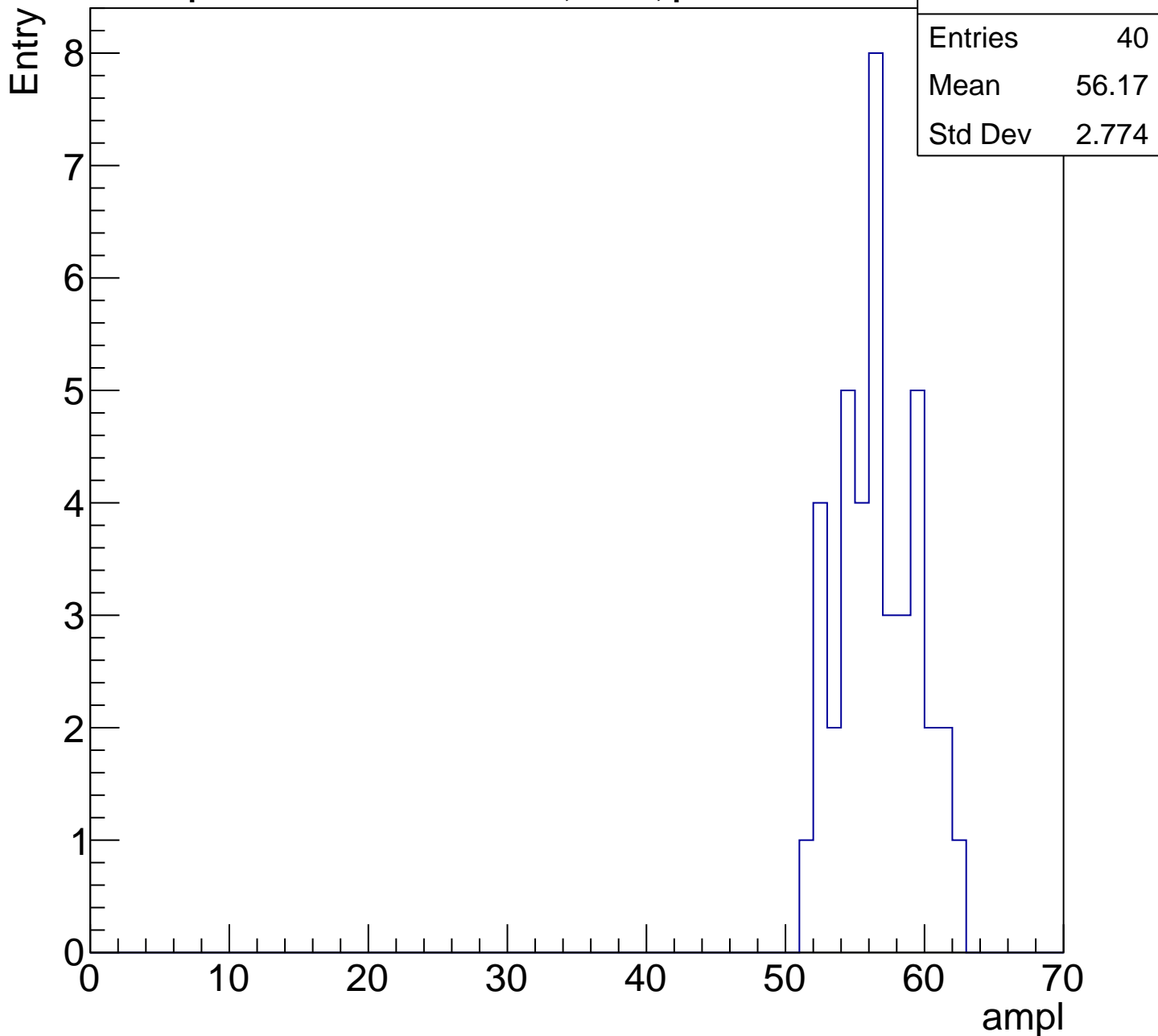
Entry

Entries	77
Mean	50.36
Std Dev	3.512



B1L103S, U23-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U23-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	60.07
Std Dev	2.23

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

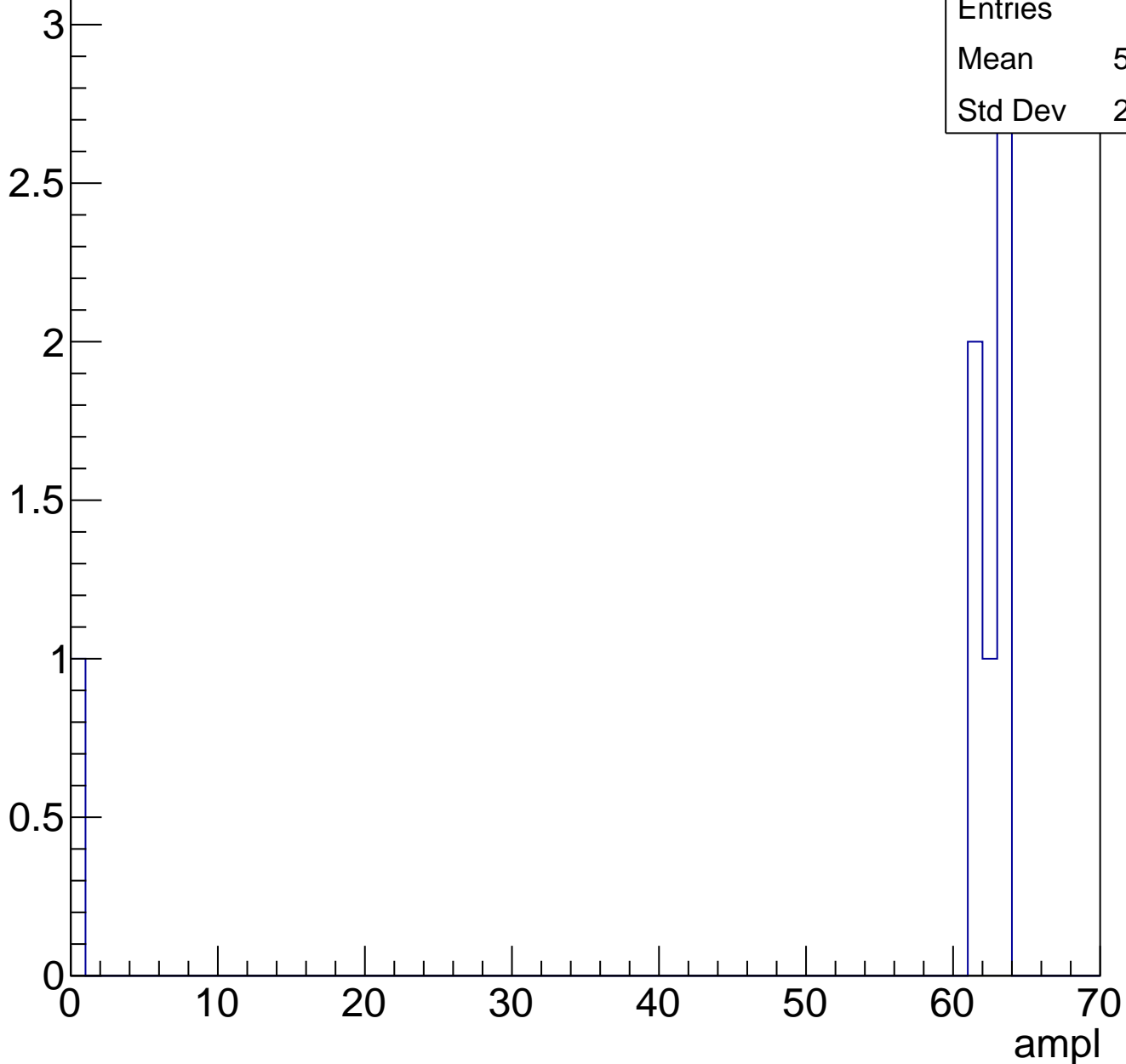
7

8

B1L103S, U23-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch34, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	28.24
Std Dev	3.916

Gaus mean : 28.3956

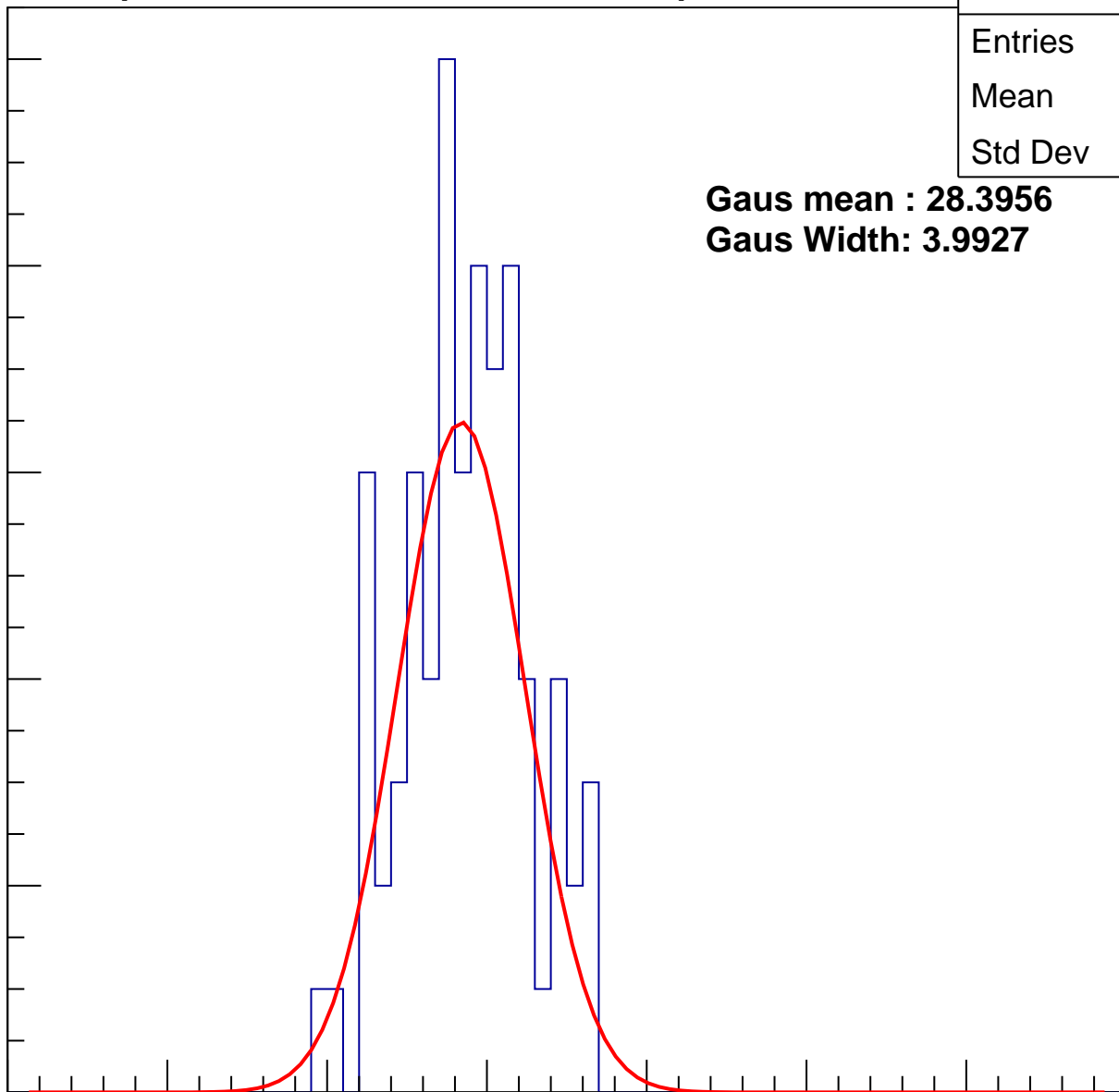
Gaus Width: 3.9927

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch34, adc1

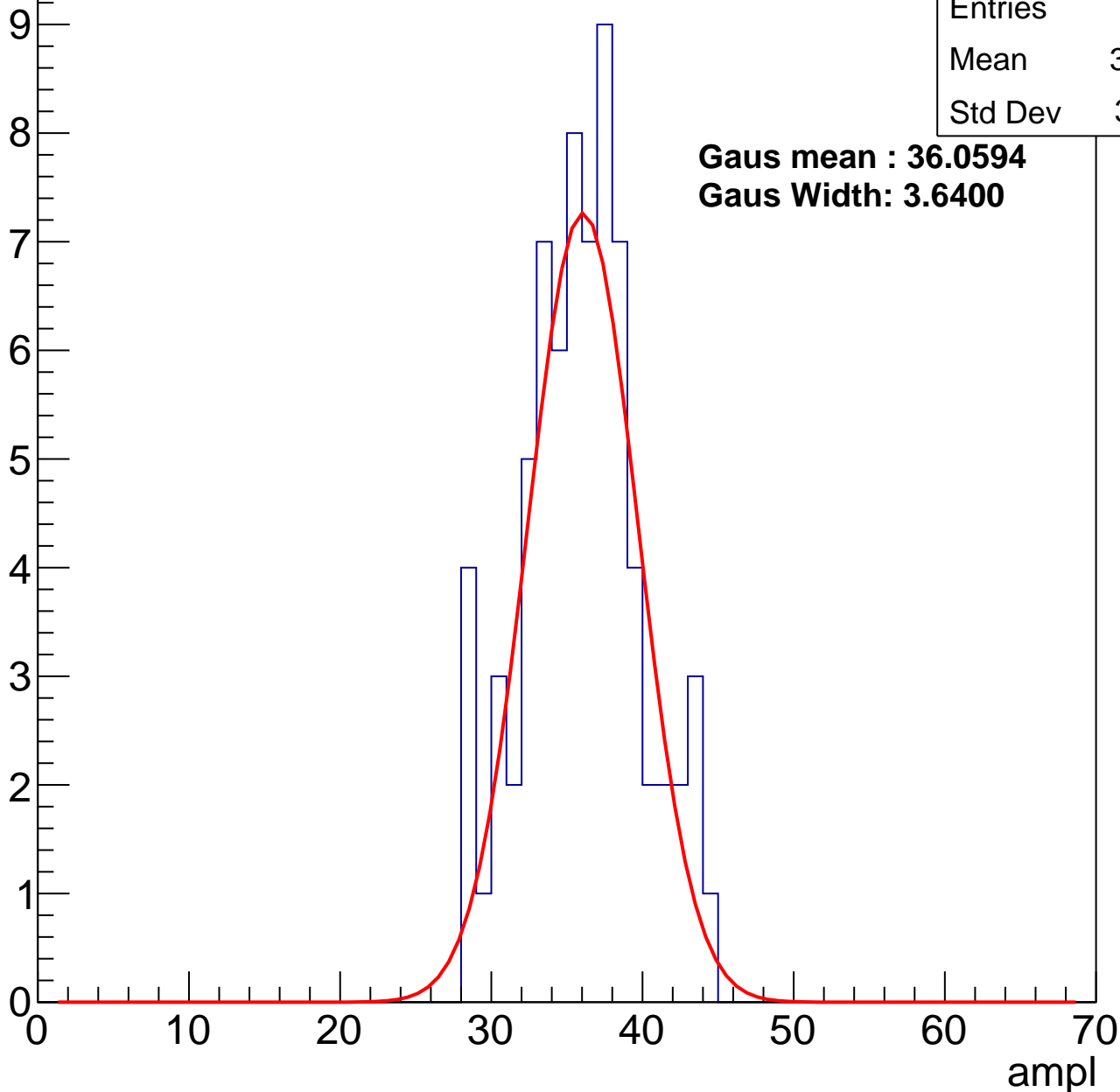
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.53
Std Dev	3.811

Gaus mean : 36.0594

Gaus Width: 3.6400



B1L103S, U23-ch34, adc2

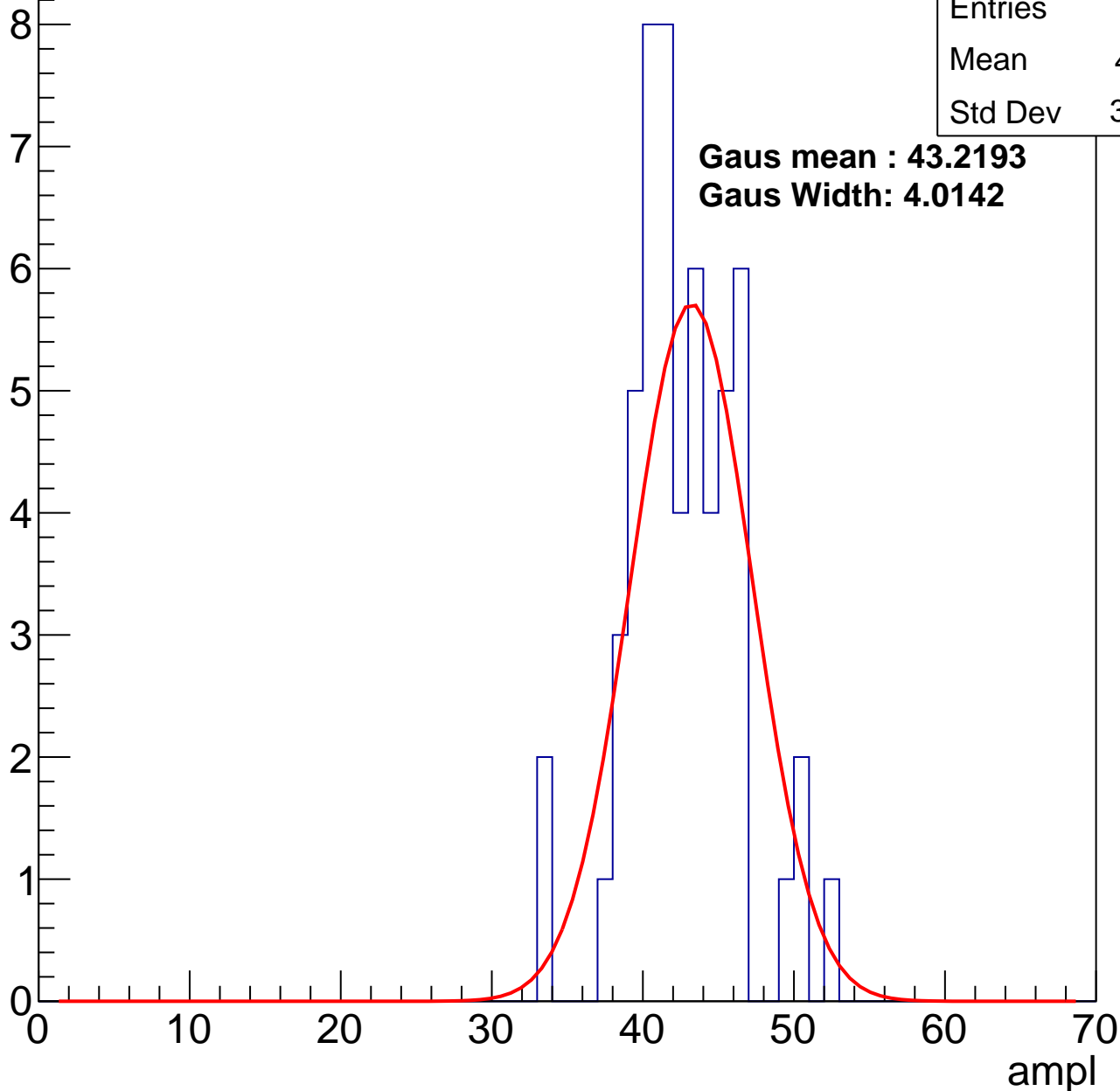
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42.21
Std Dev	3.678

Gaus mean : 43.2193

Gaus Width: 4.0142

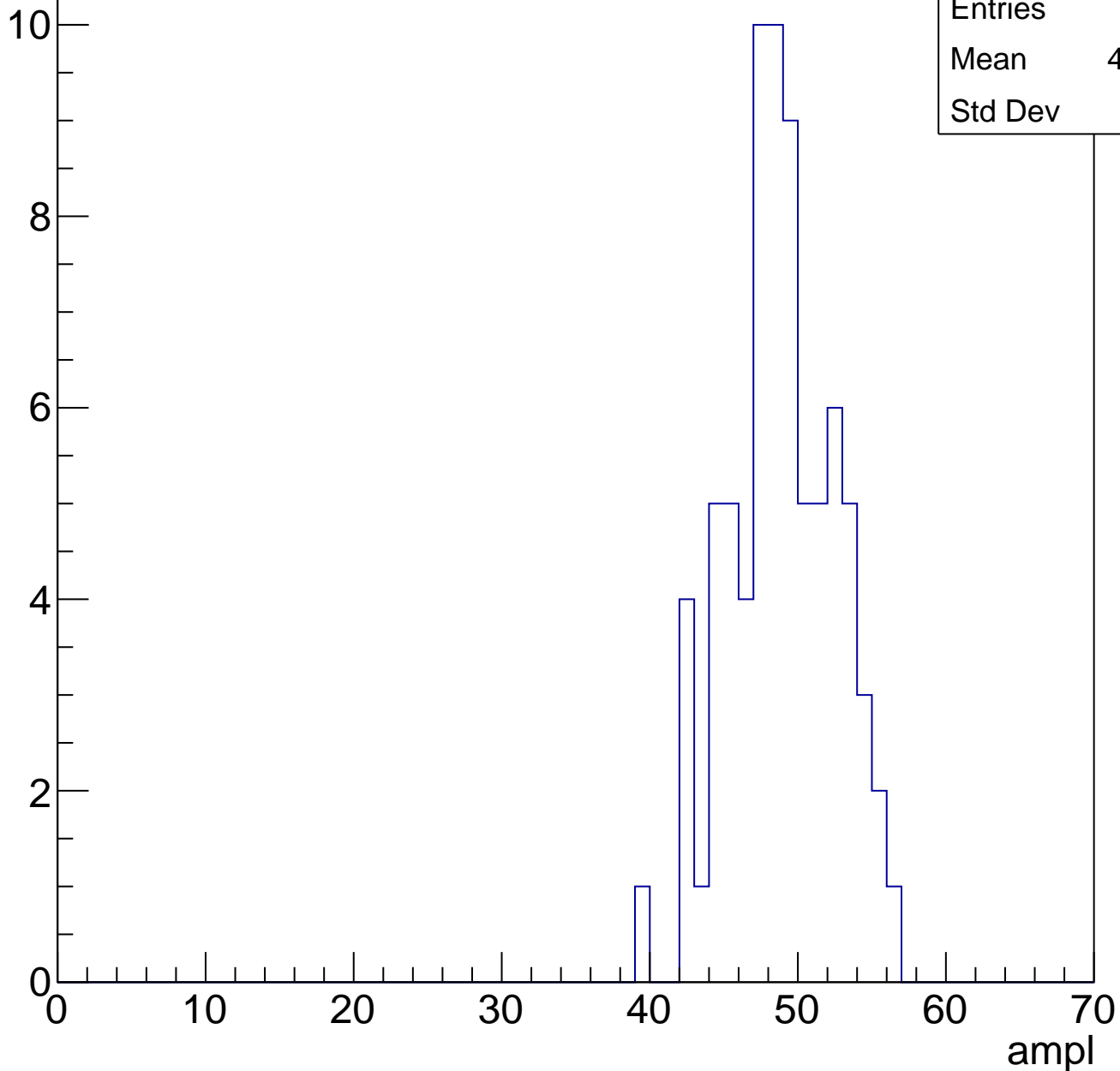


B1L103S, U23-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	48.42
Std Dev	3.54

Entry

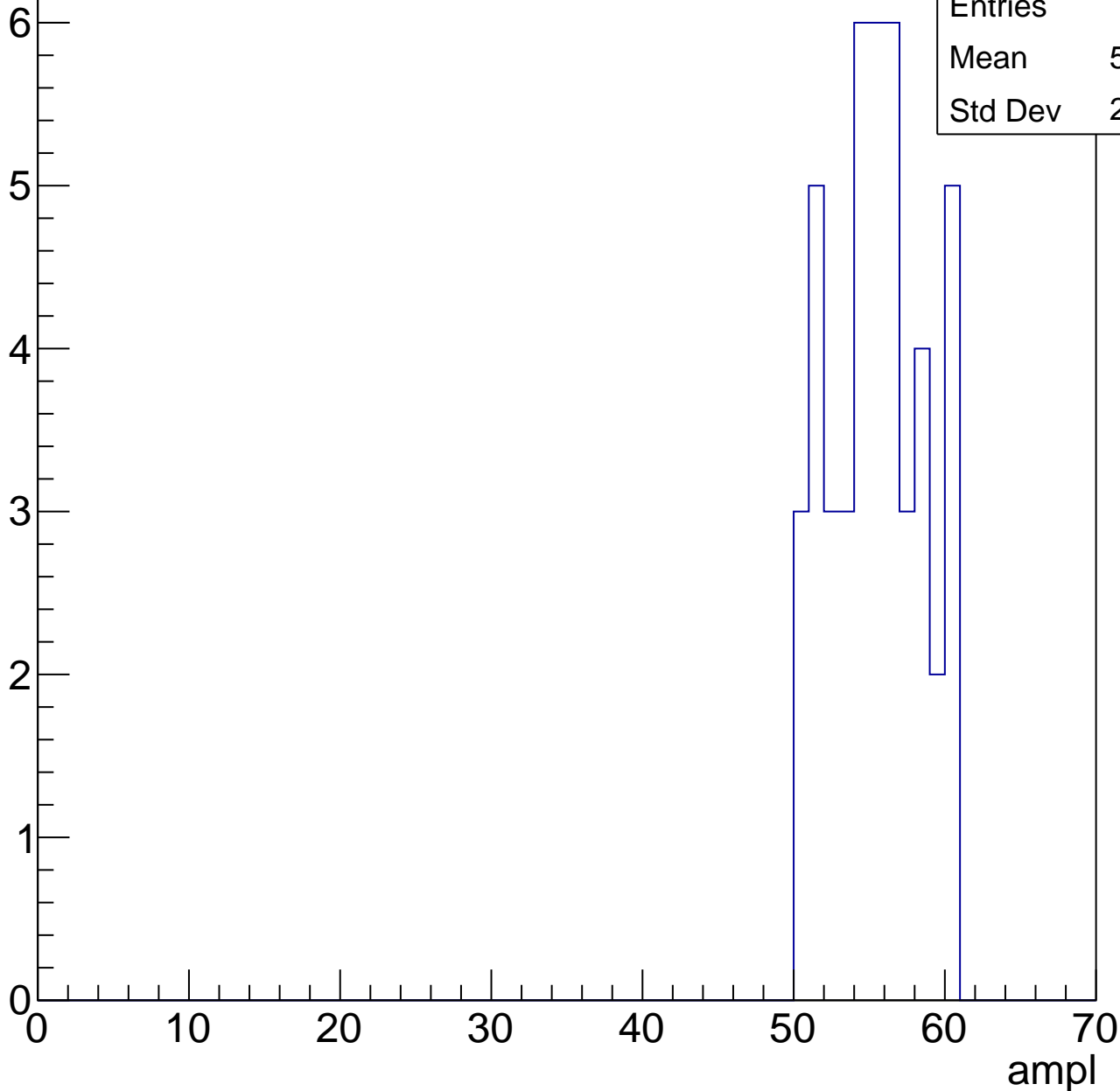


B1L103S, U23-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	55.02
Std Dev	2.989

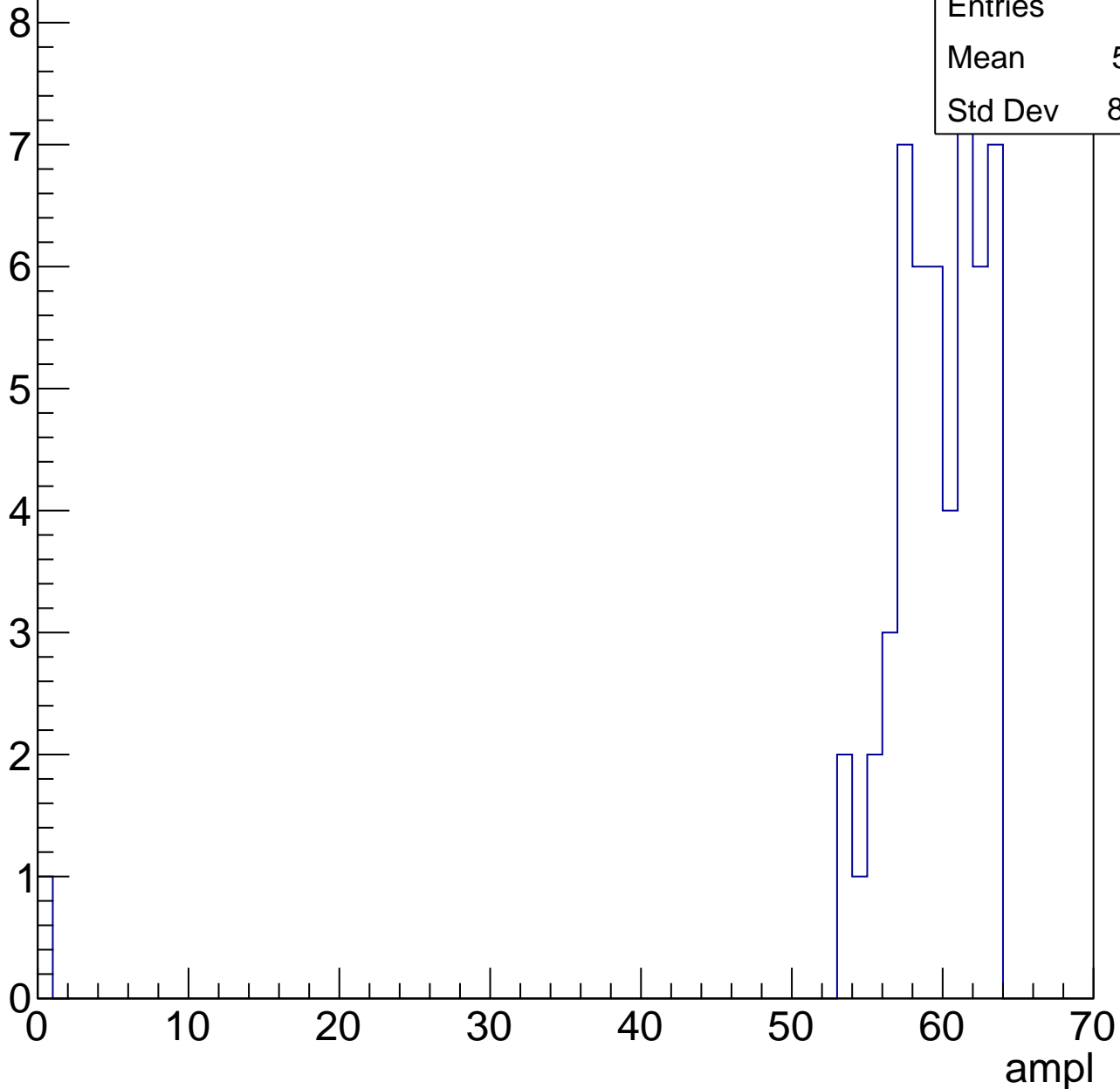


B1L103S, U23-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

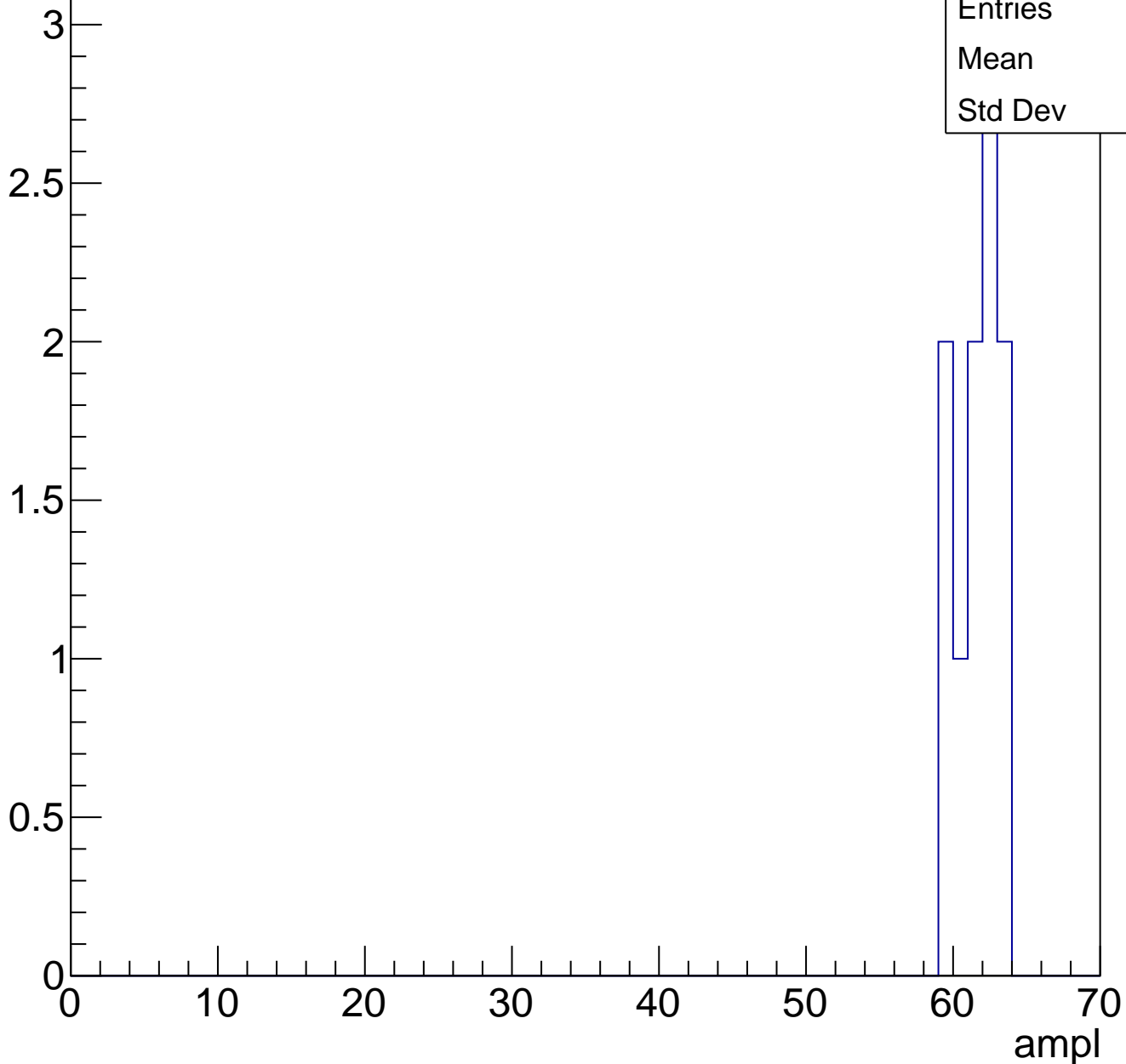
Entries	53
Mean	58.11
Std Dev	8.502



B1L103S, U23-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

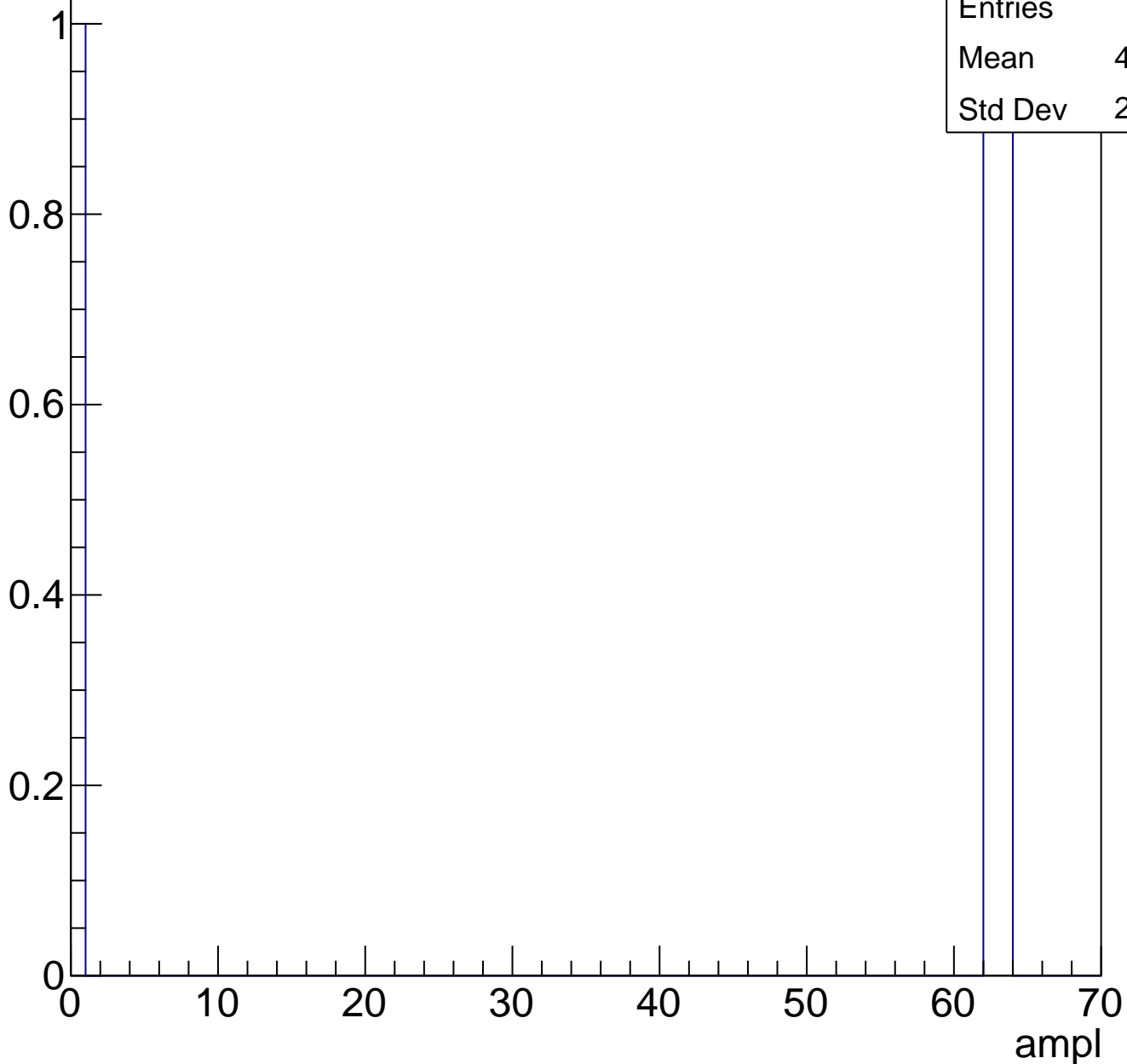
Entry



B1L103S, U23-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch35, adc0

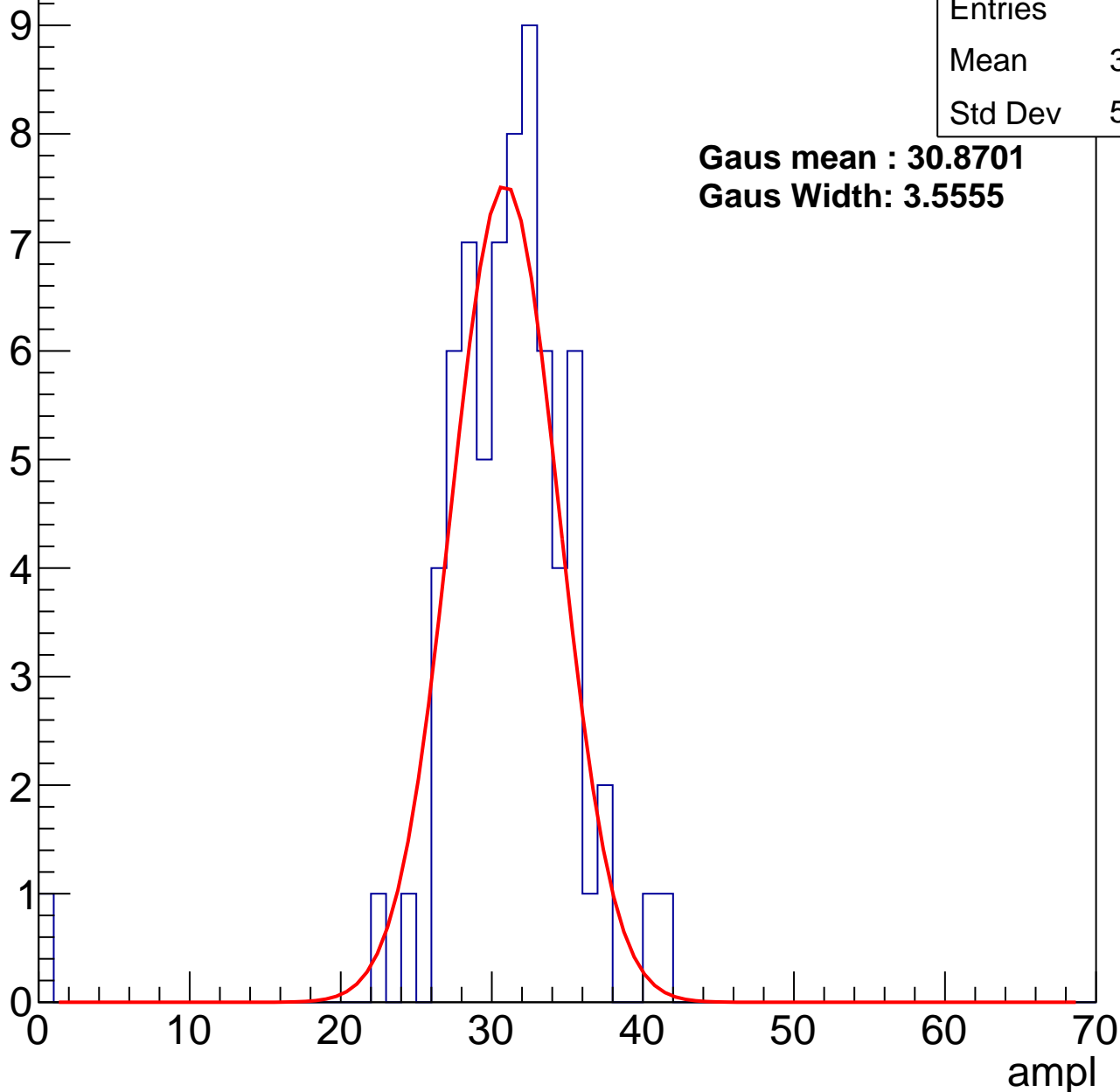
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	30.49
Std Dev	5.073

Gaus mean : 30.8701

Gaus Width: 3.5555



B1L103S, U23-ch35, adc1

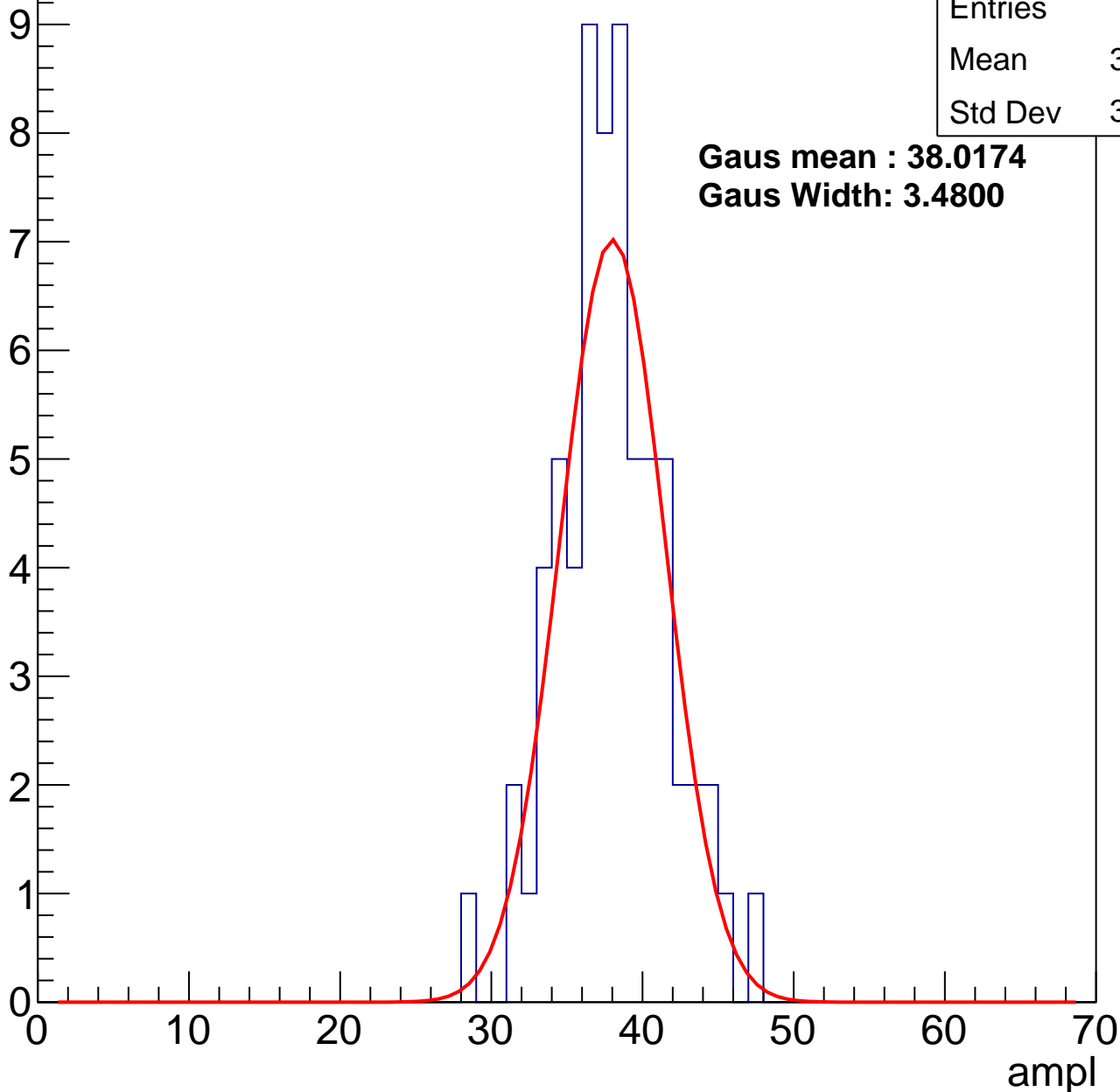
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	37.52
Std Dev	3.552

Gaus mean : 38.0174

Gaus Width: 3.4800



B1L103S, U23-ch35, adc2

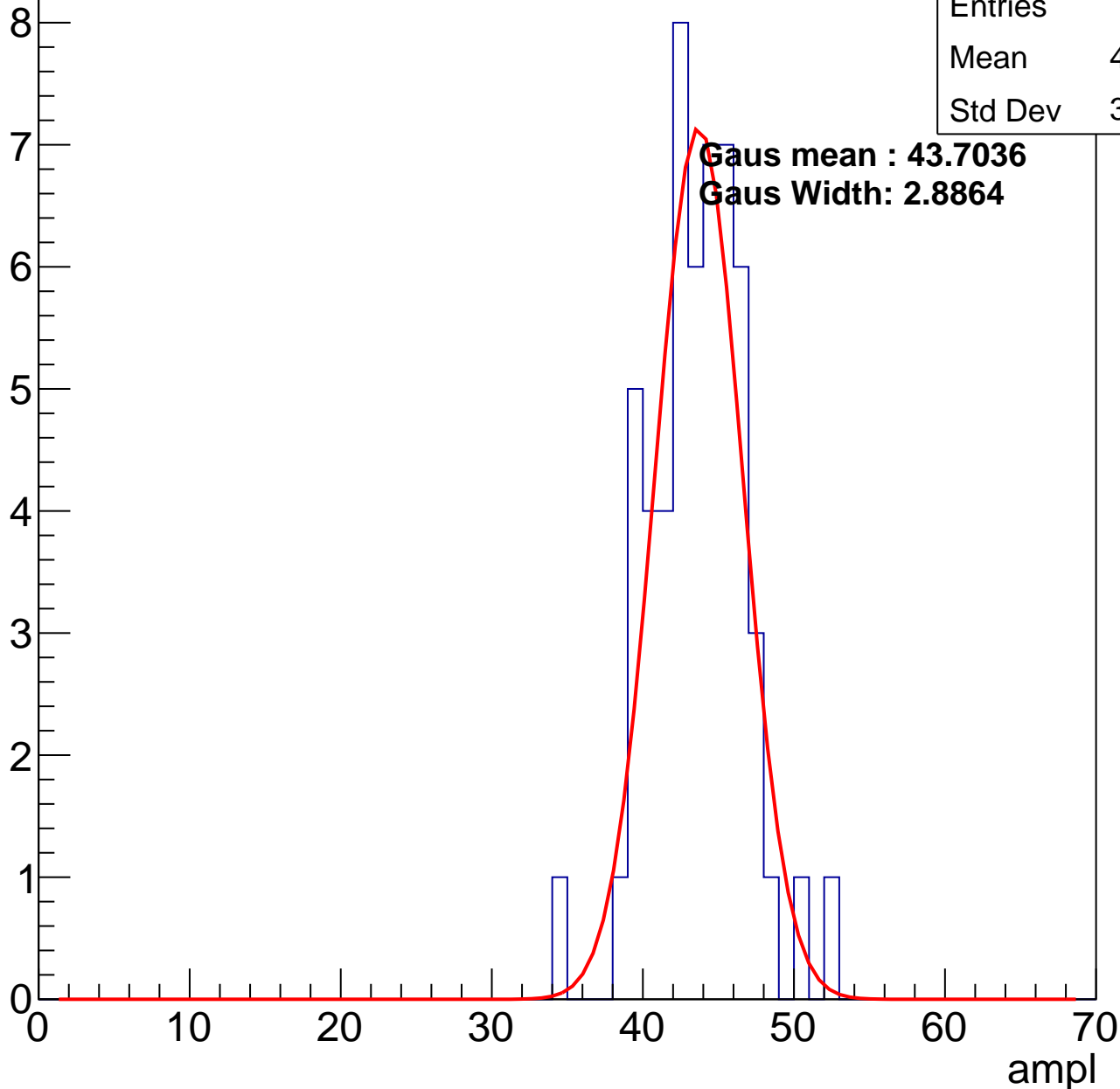
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.18
Std Dev	3.122

Gaus mean : 43.7036

Gaus Width: 2.8864



B1L103S, U23-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	50.09
Std Dev	3.29

Entry

10

8

6

4

2

0

0

10

20

30

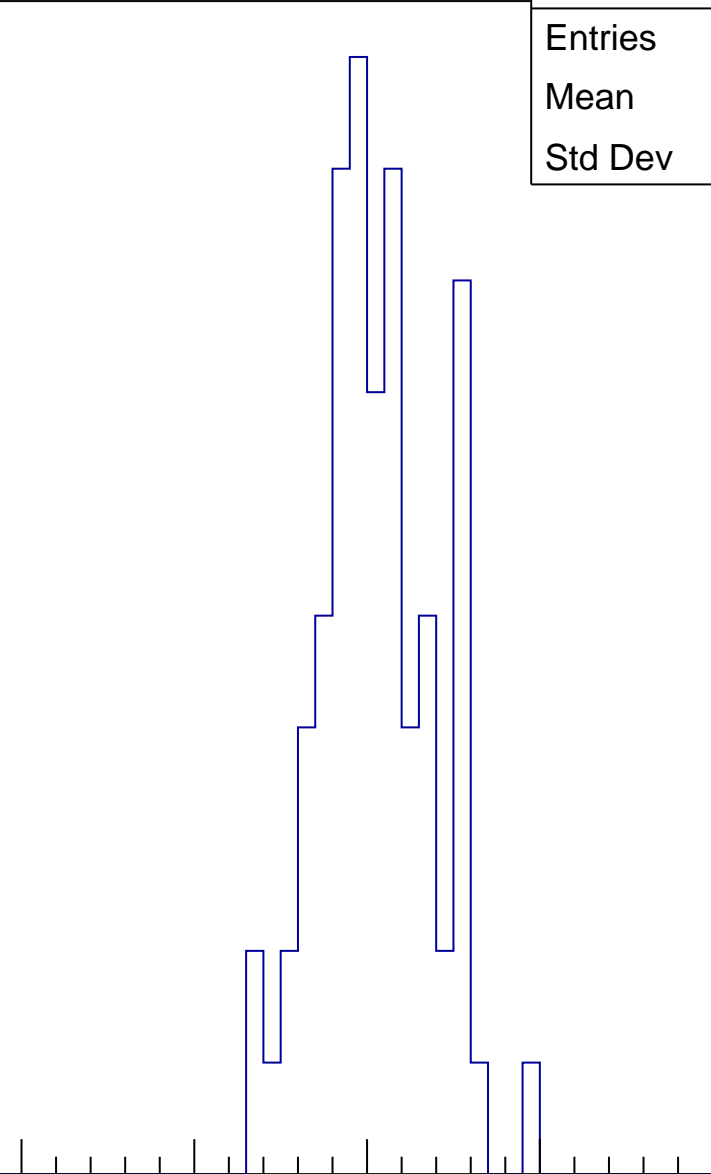
40

50

60

ampl

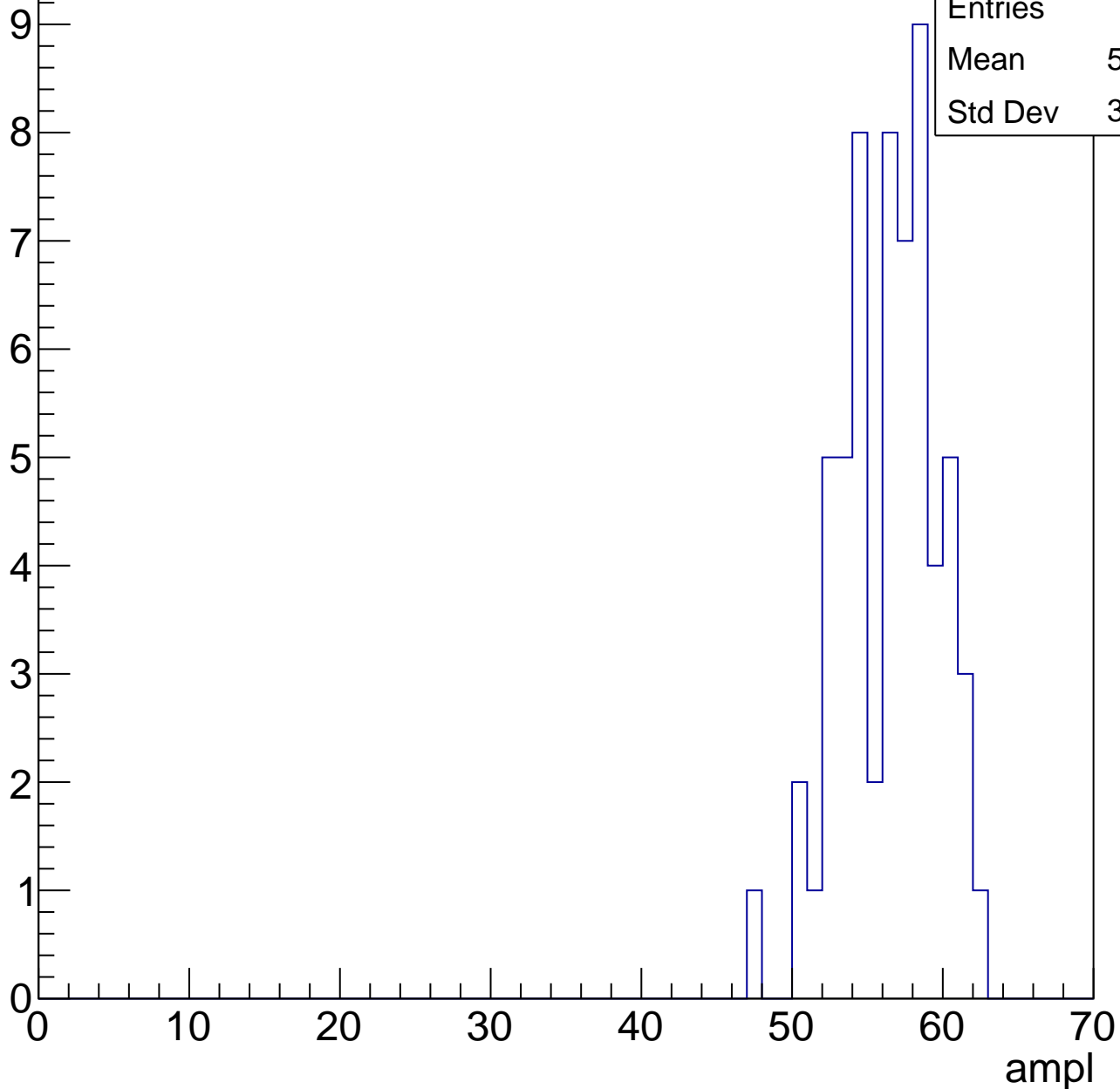
70



B1L103S, U23-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

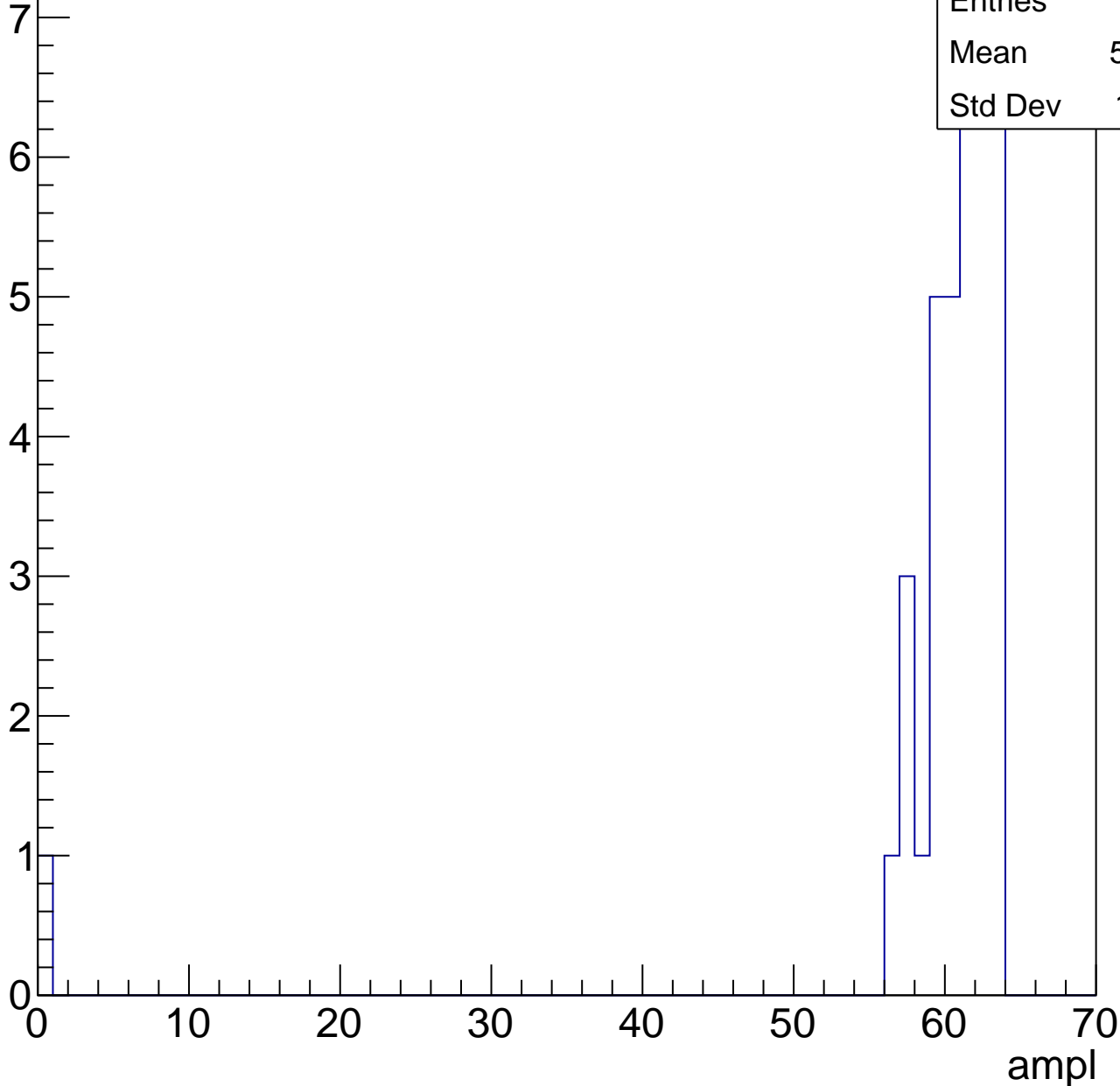


B1L103S, U23-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.97
Std Dev	10.01



B1L103S, U23-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U23-ch36, adc0

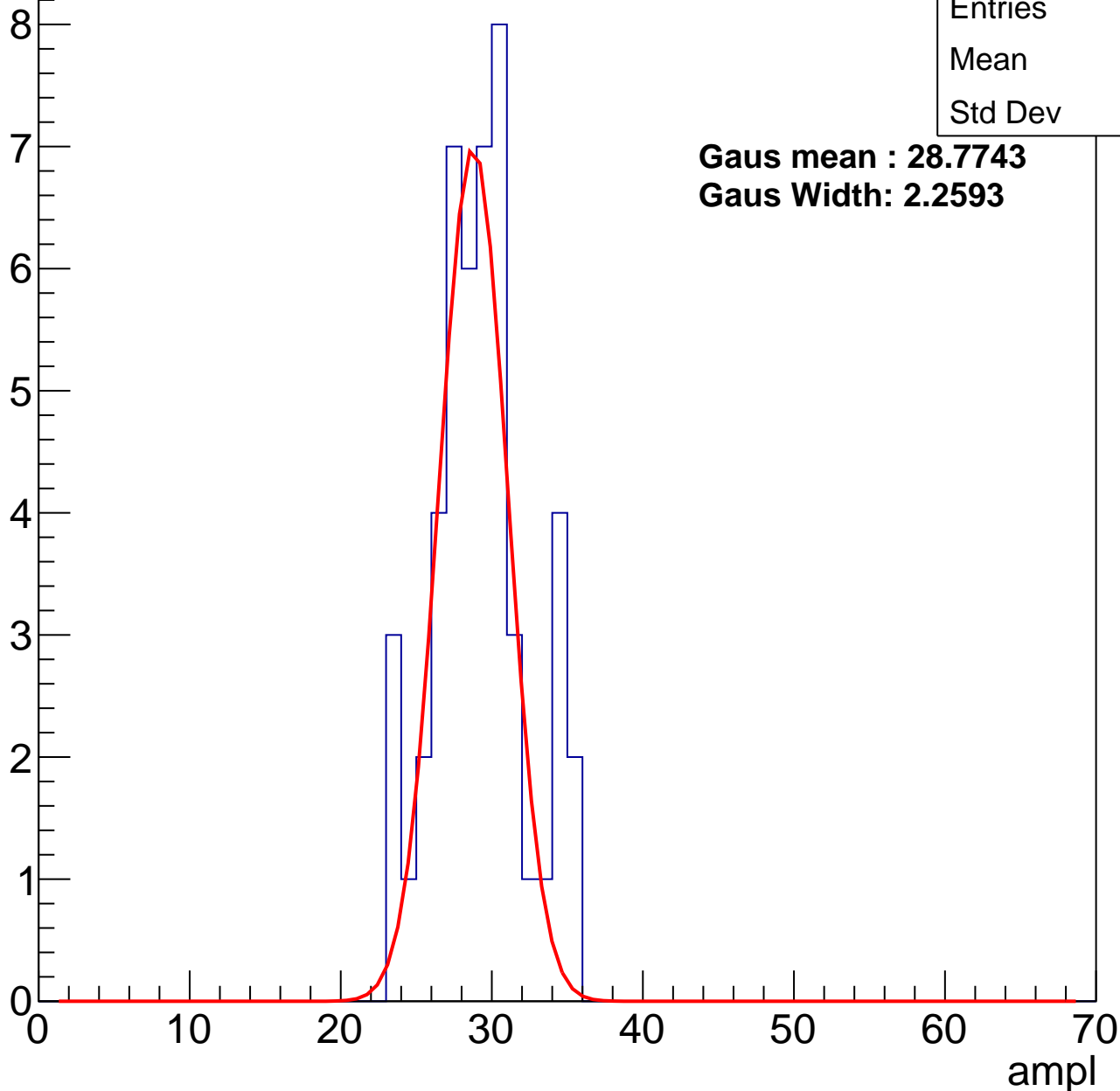
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	28.8
Std Dev	3.03

Gaus mean : 28.7743

Gaus Width: 2.2593

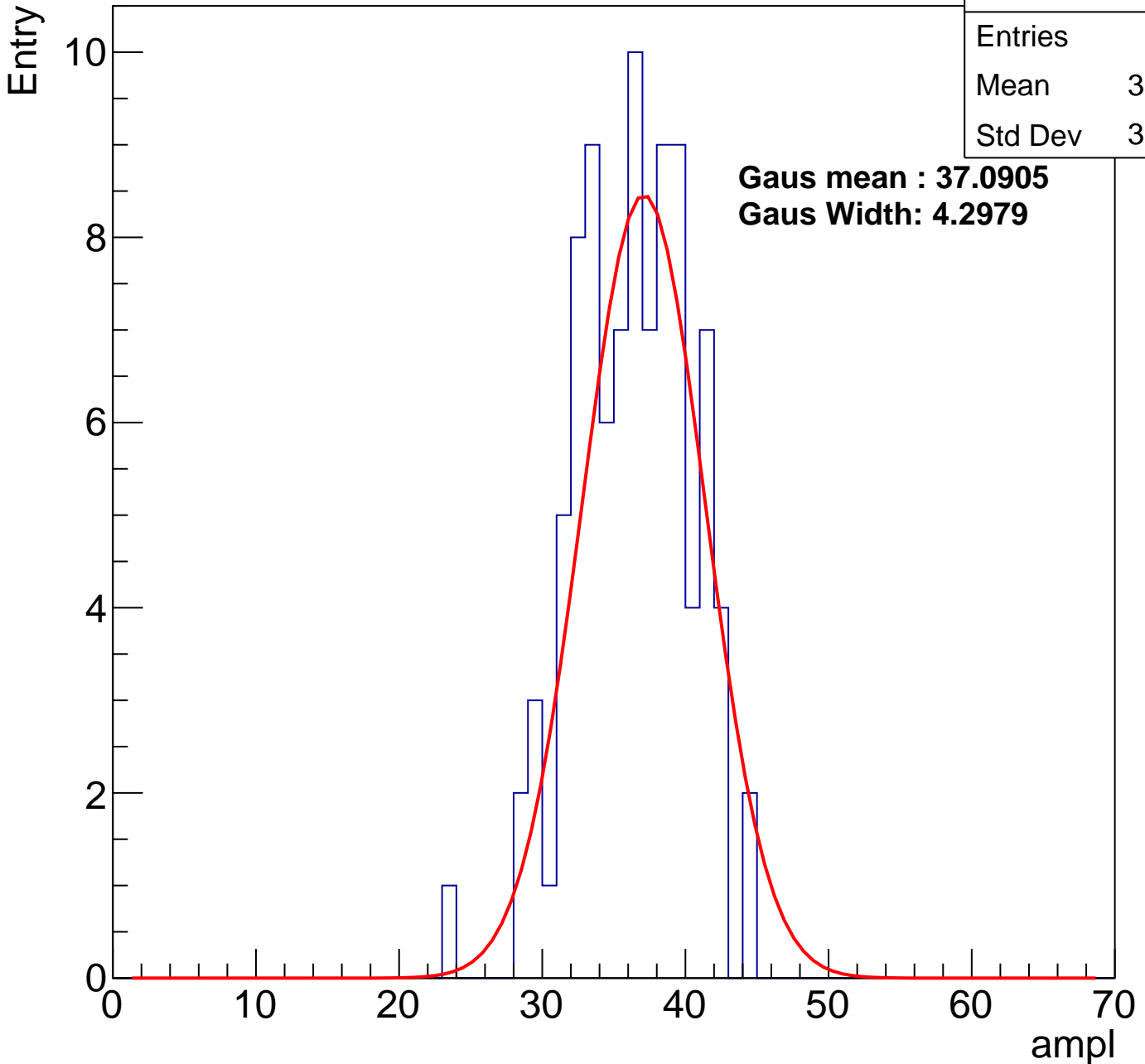


B1L103S, U23-ch36, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	94
Mean	35.83
Std Dev	3.964

Gaus mean : 37.0905
Gaus Width: 4.2979



B1L103S, U23-ch36, adc2

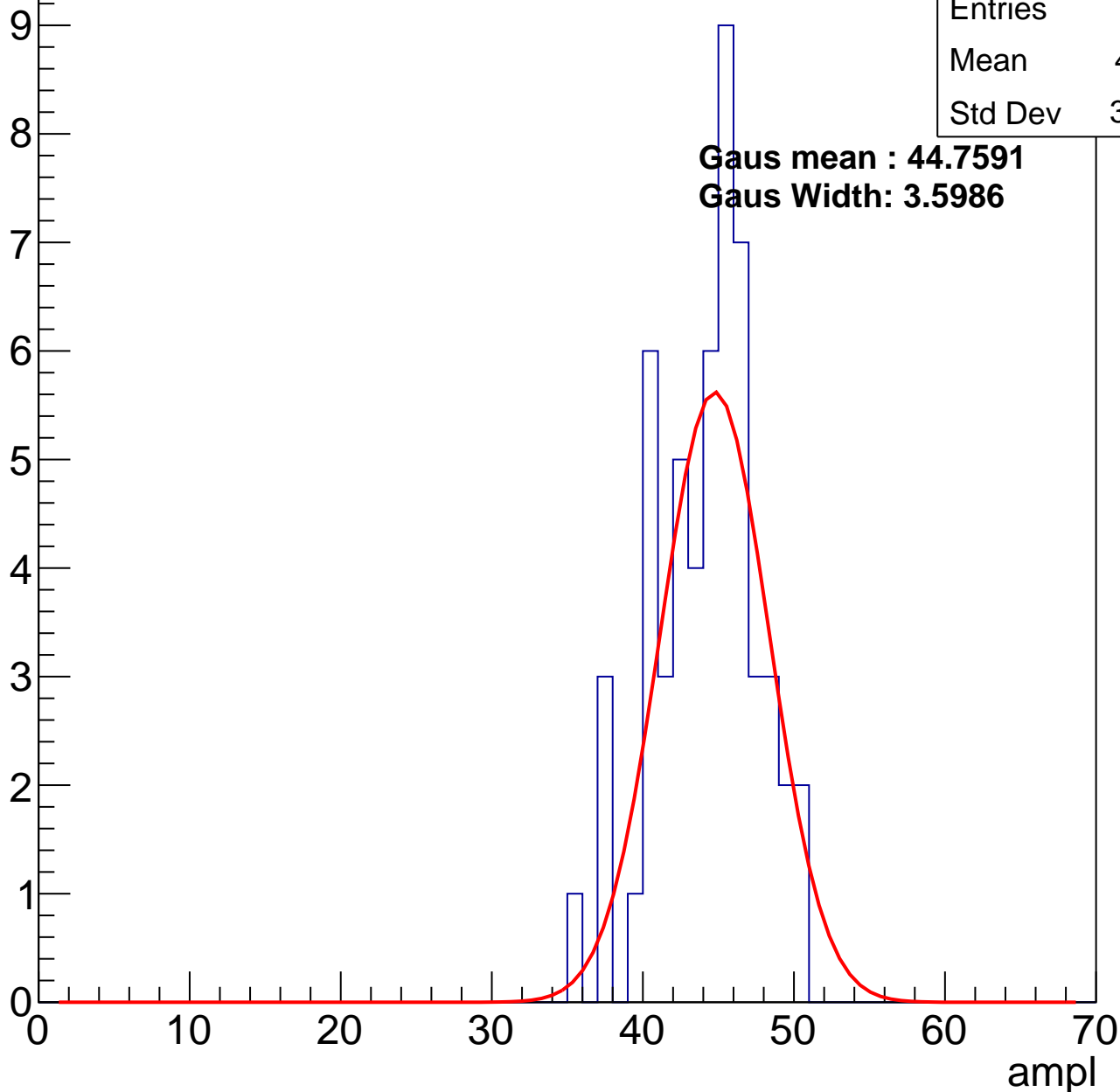
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.71
Std Dev	3.383

Gaus mean : 44.7591

Gaus Width: 3.5986

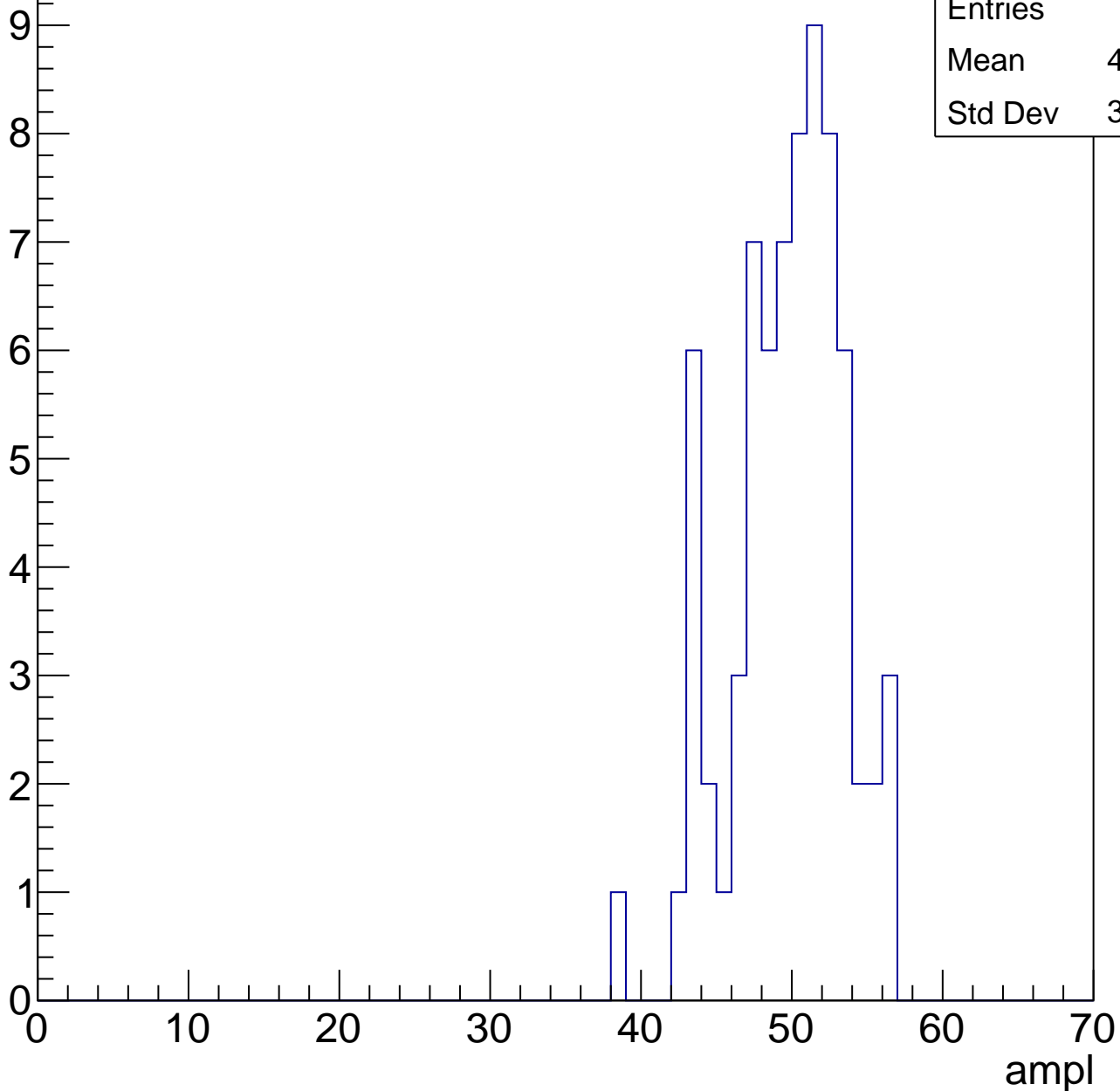


B1L103S, U23-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

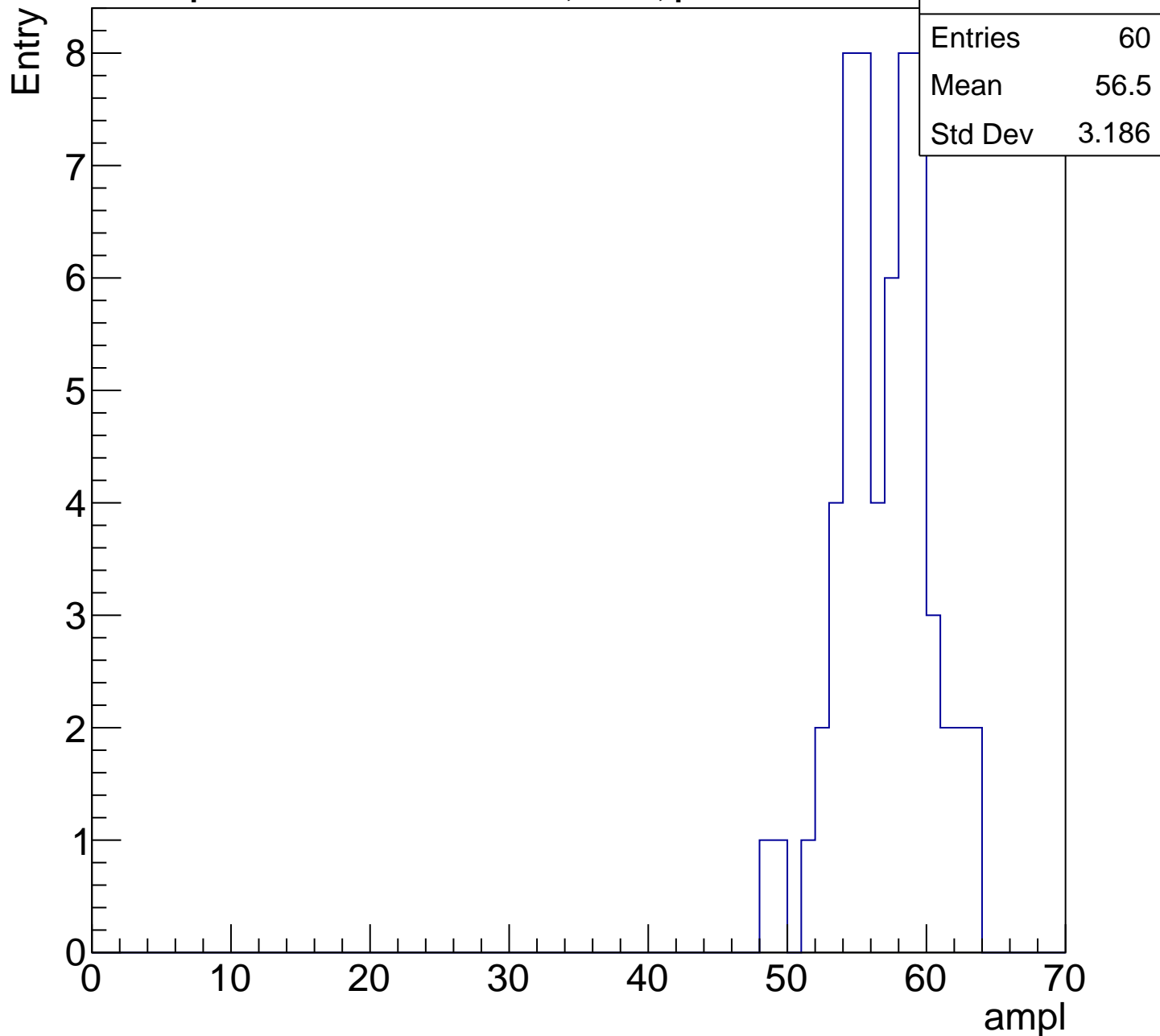
Entry

Entries	72
Mean	49.28
Std Dev	3.705



B1L103S, U23-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

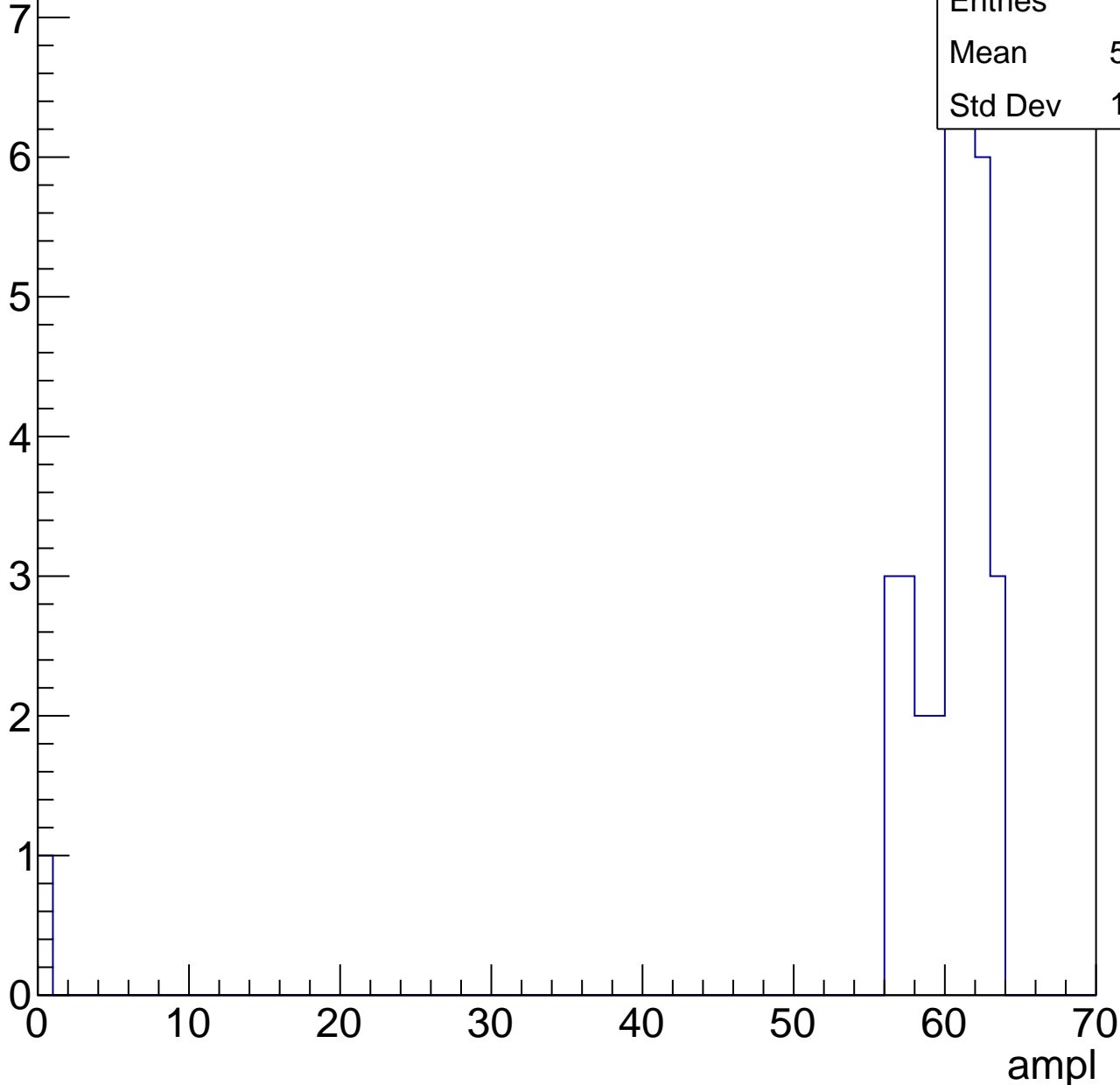


B1L103S, U23-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	58.26
Std Dev	10.35

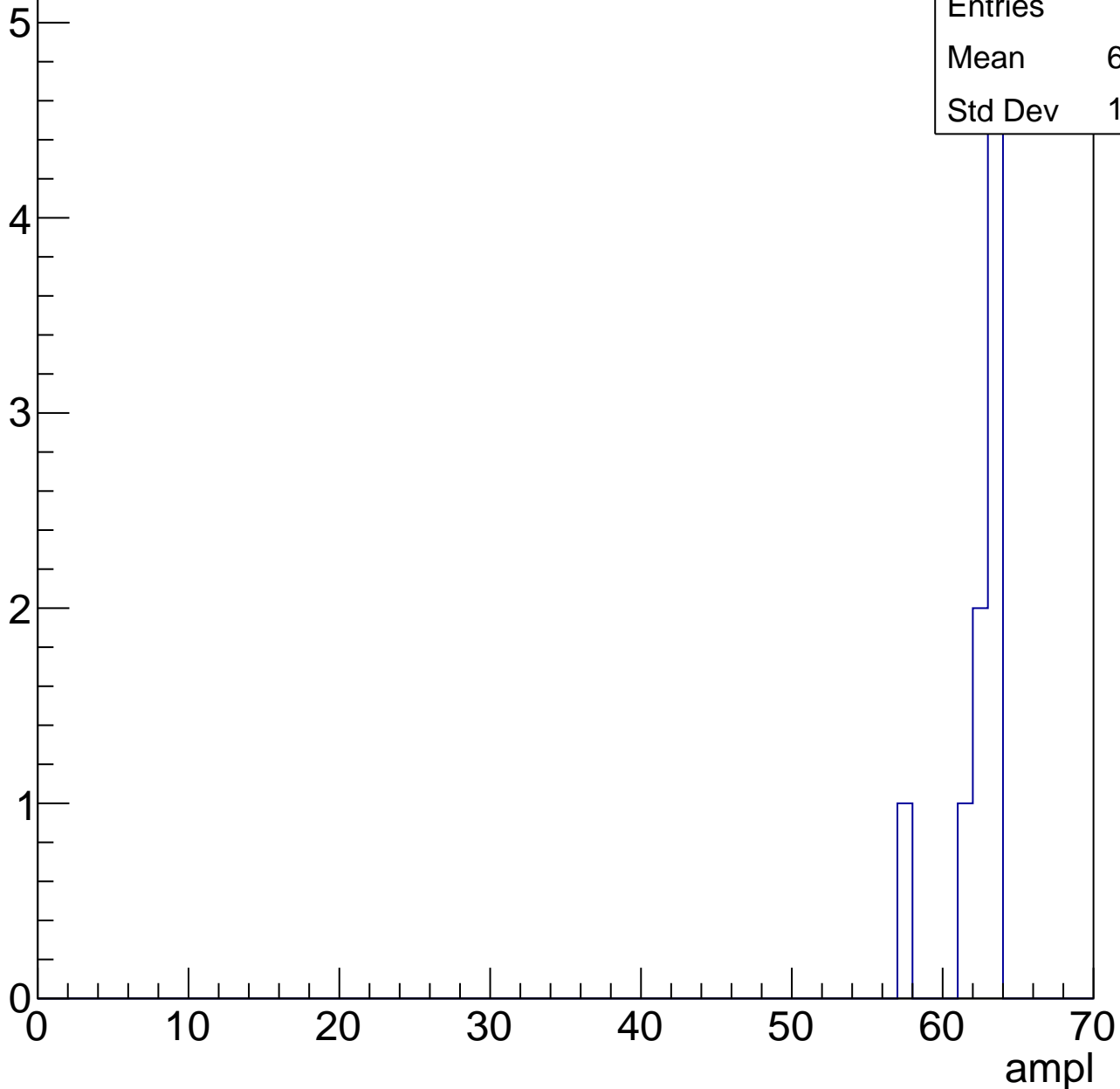


B1L103S, U23-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	61.89
Std Dev	1.853



B1L103S, U23-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch37, adc0

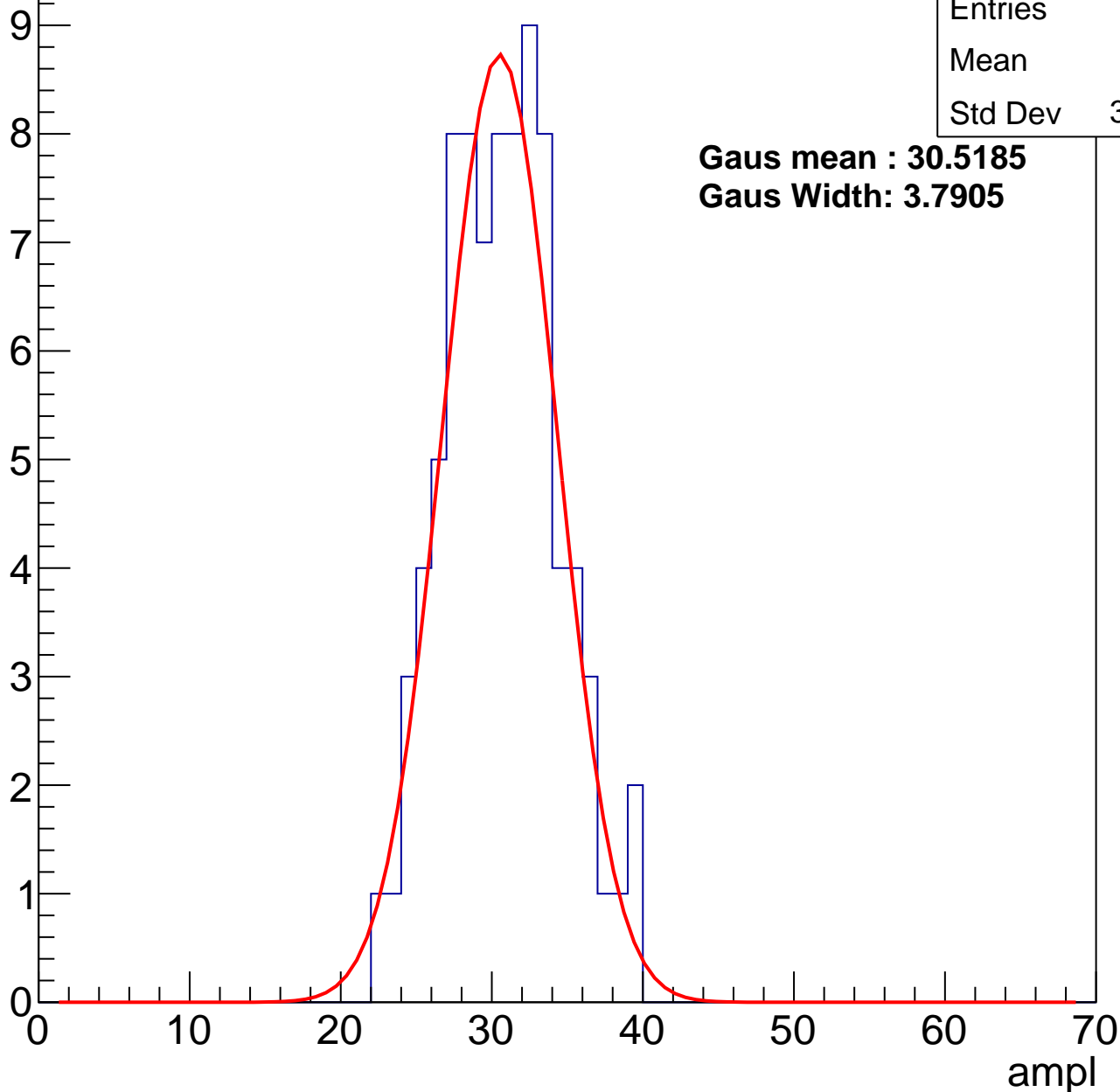
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	30.2
Std Dev	3.713

Gaus mean : 30.5185

Gaus Width: 3.7905



B1L103S, U23-ch37, adc1

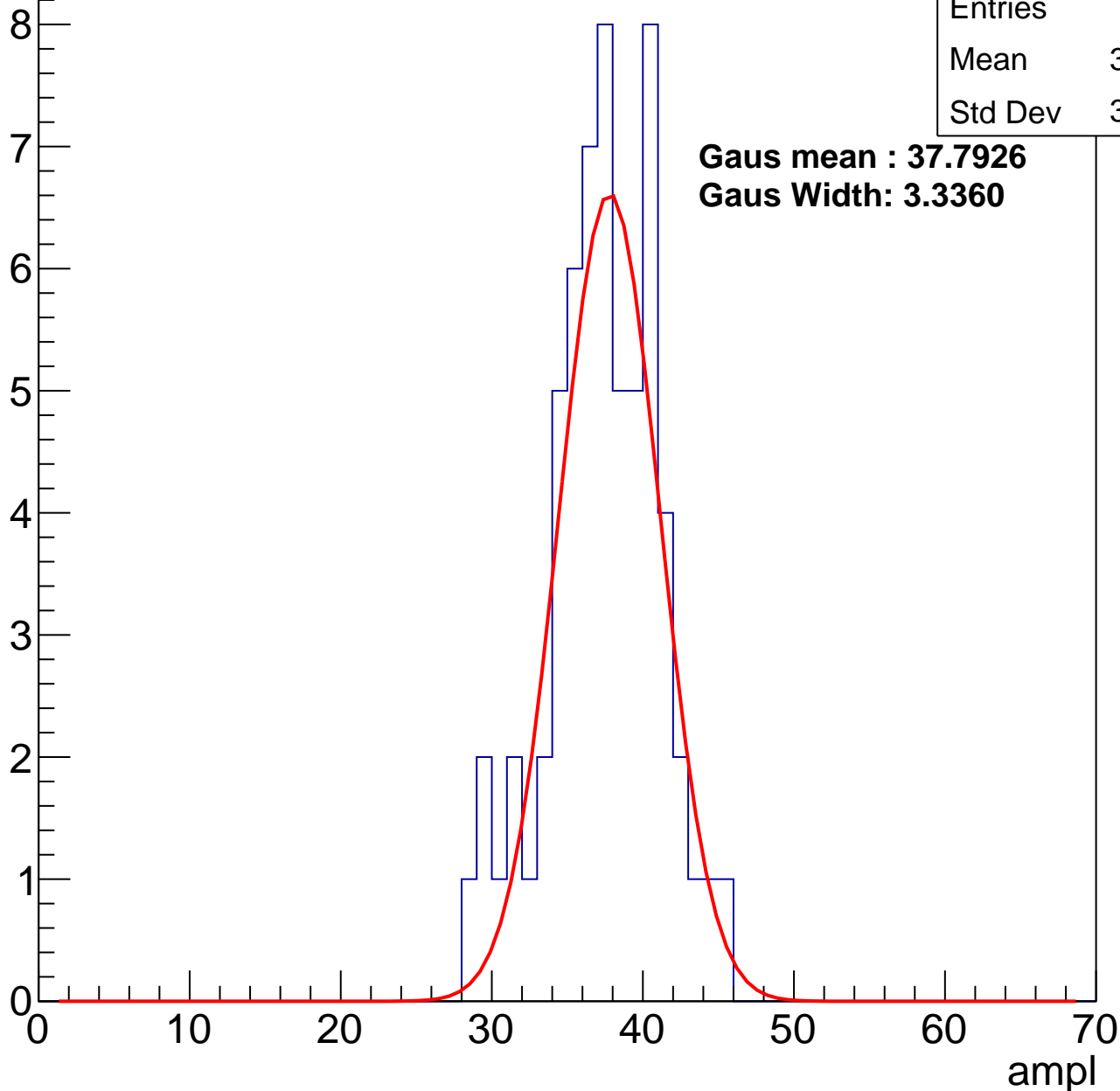
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	36.92
Std Dev	3.642

Gaus mean : 37.7926

Gaus Width: 3.3360



B1L103S, U23-ch37, adc2

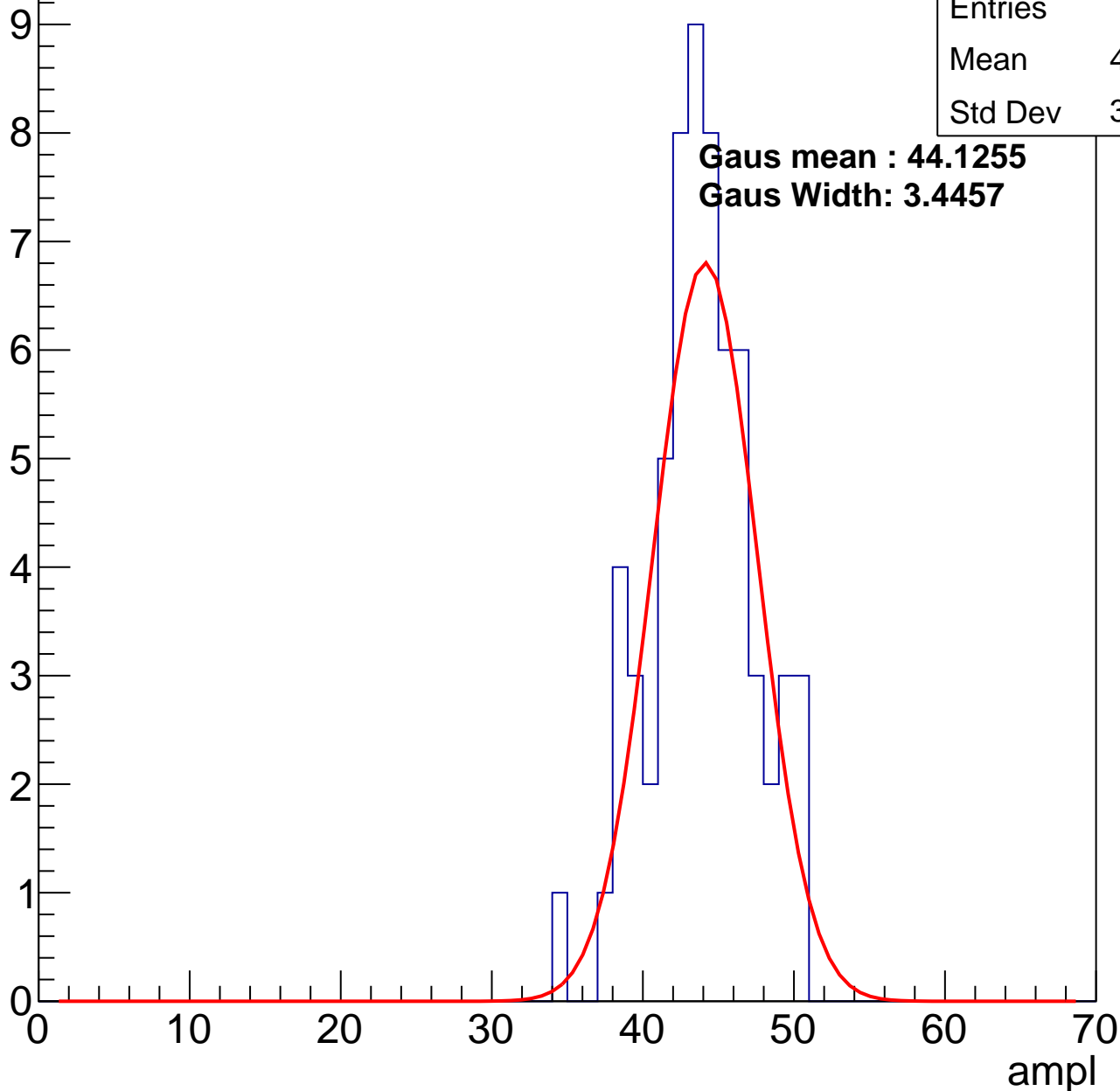
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.44
Std Dev	3.395

Gaus mean : 44.1255

Gaus Width: 3.4457

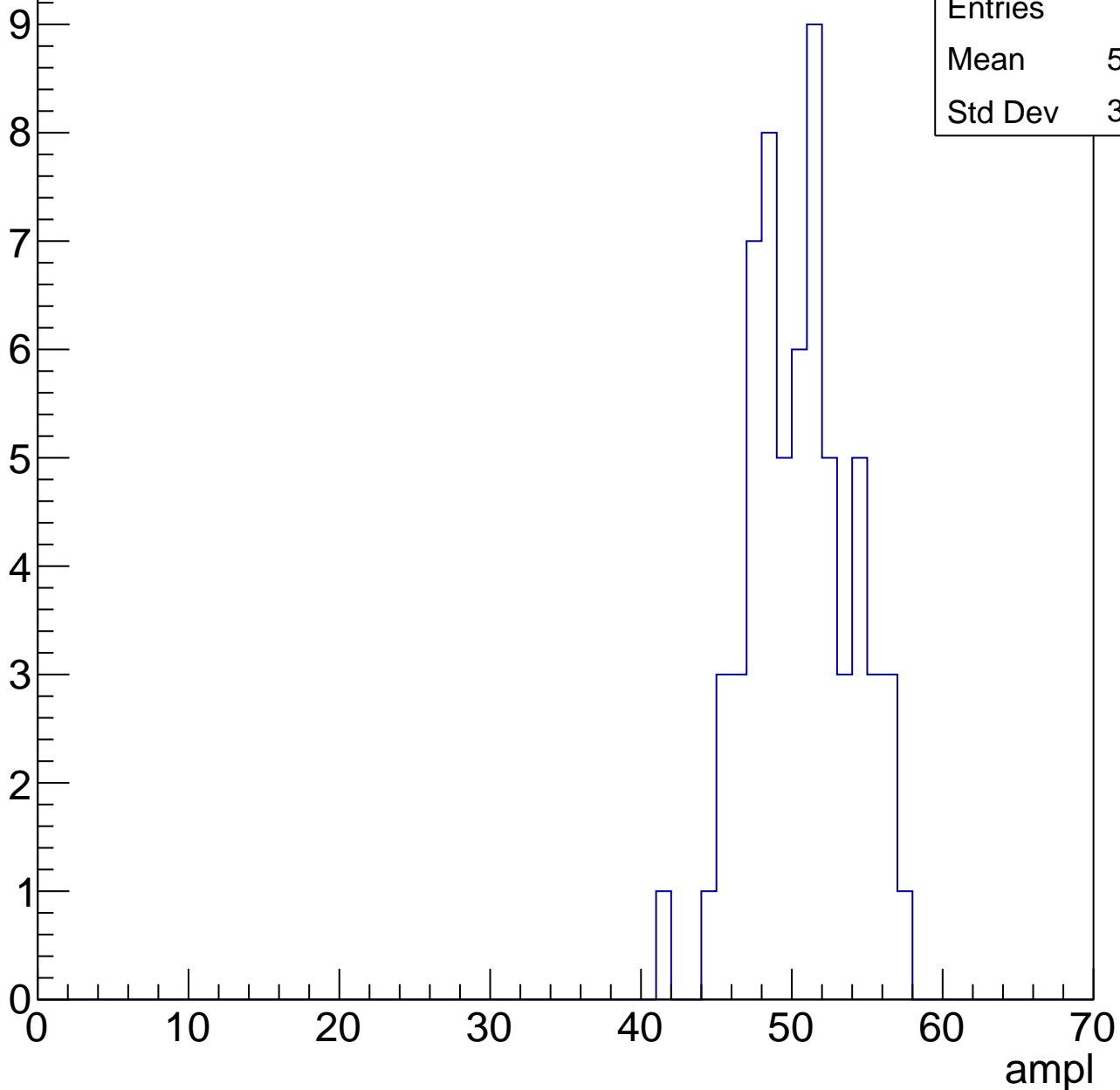


B1L103S, U23-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	50.06
Std Dev	3.352

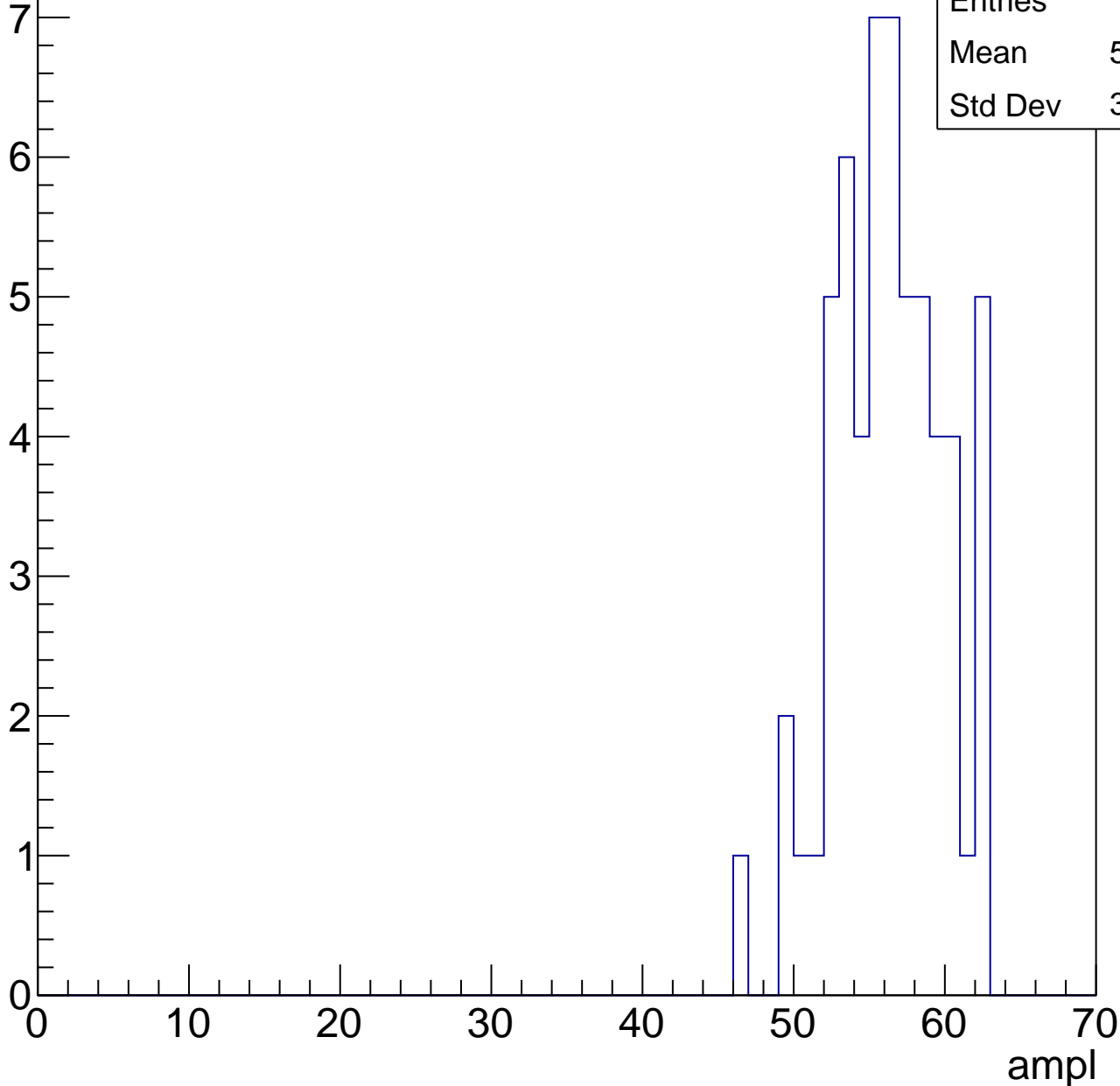


B1L103S, U23-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	55.83
Std Dev	3.582

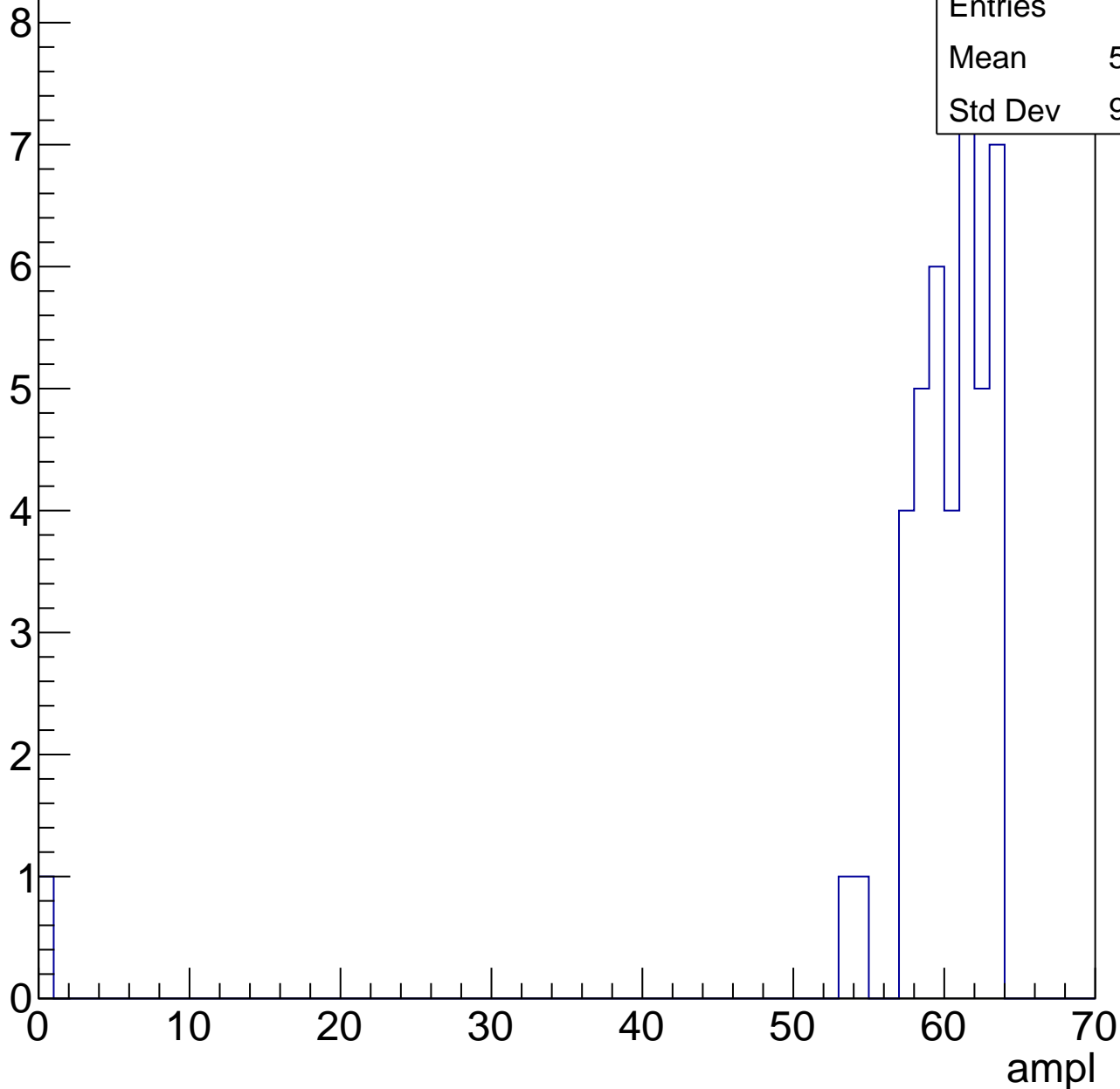


B1L103S, U23-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

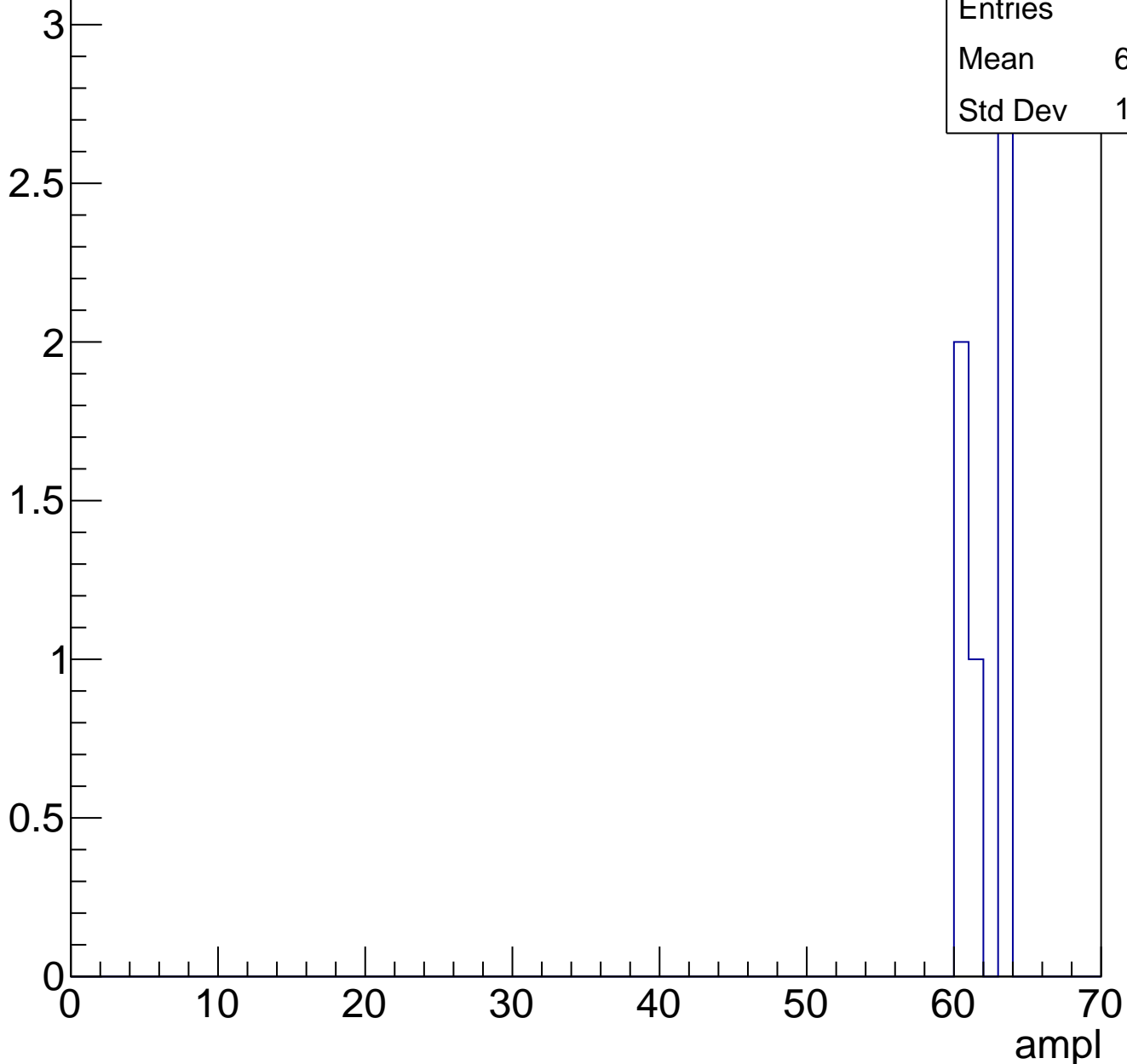
Entries	42
Mean	58.52
Std Dev	9.445



B1L103S, U23-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch38, adc0

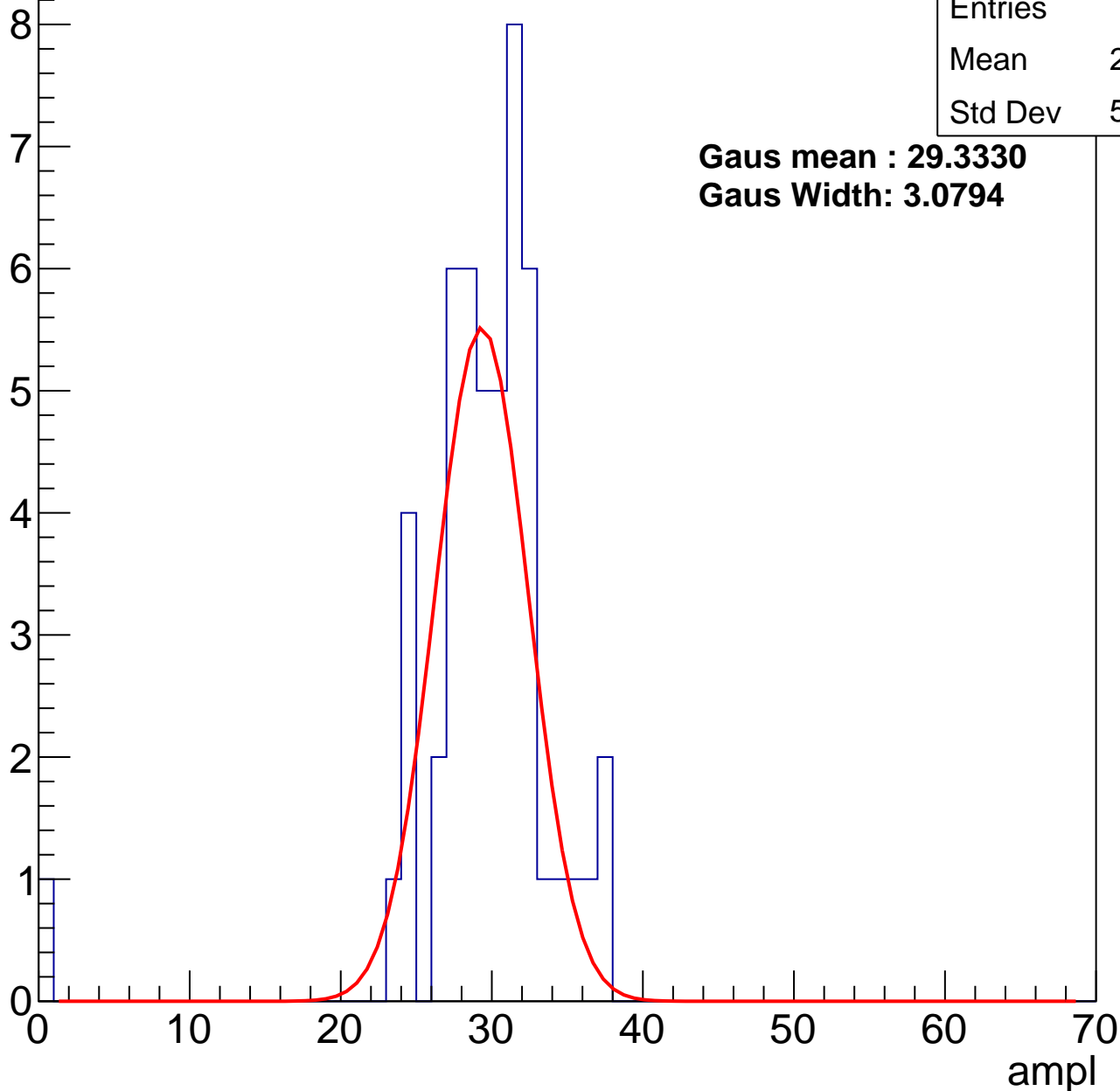
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	28.96
Std Dev	5.234

Gaus mean : 29.3330

Gaus Width: 3.0794



B1L103S, U23-ch38, adc1

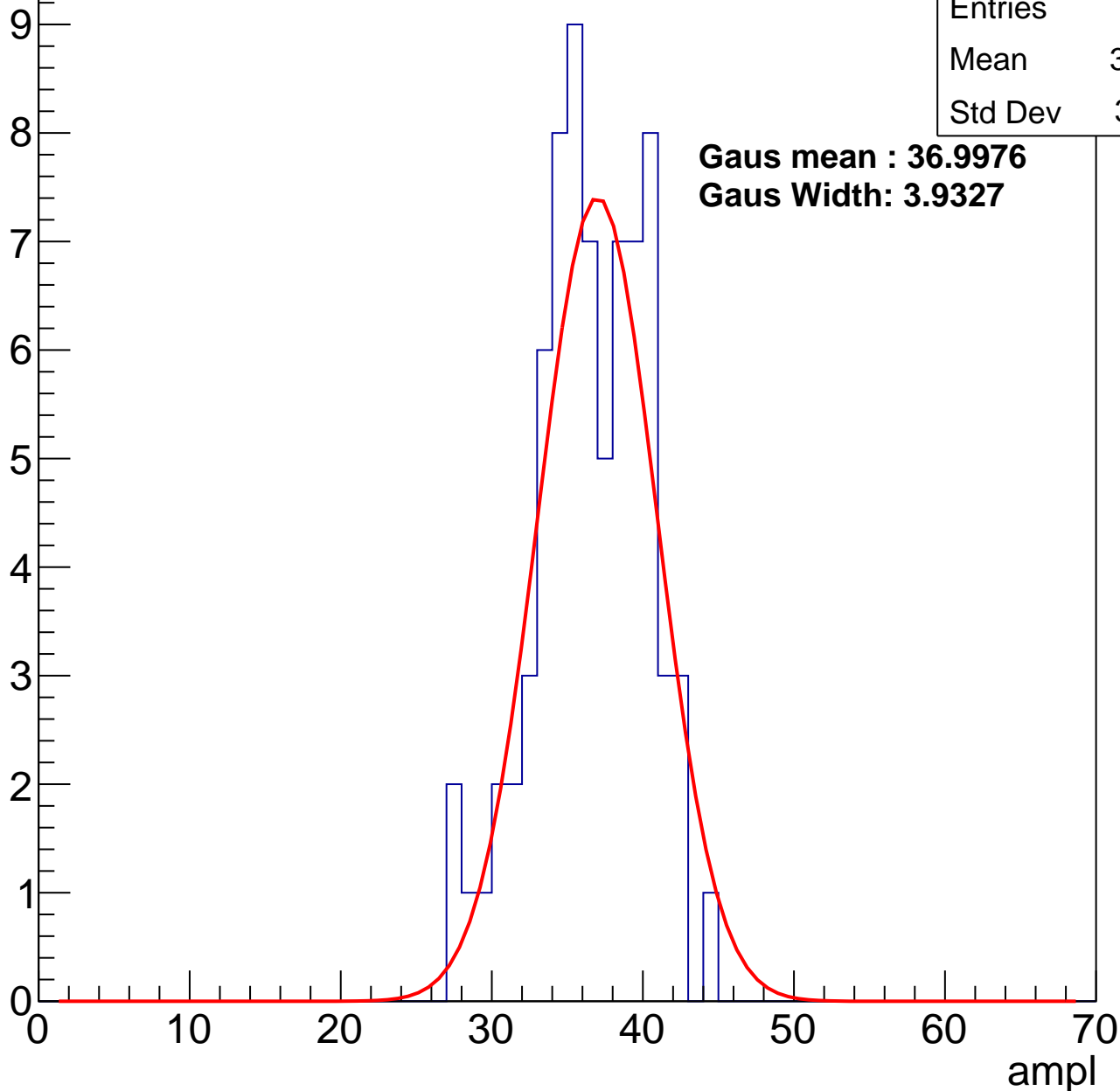
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.04
Std Dev	3.671

Gaus mean : 36.9976

Gaus Width: 3.9327



B1L103S, U23-ch38, adc2

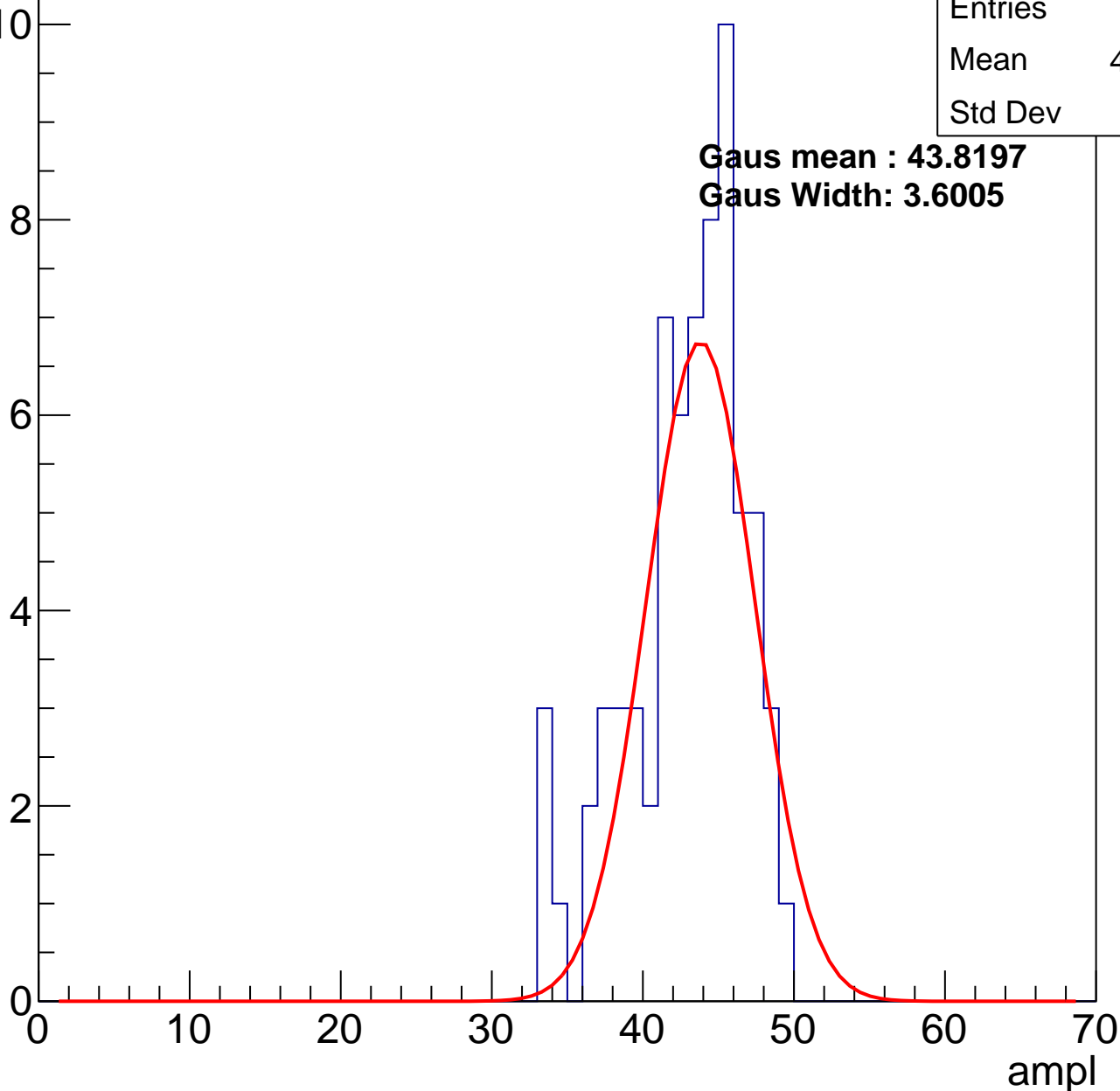
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	42.42
Std Dev	3.85

Gaus mean : 43.8197

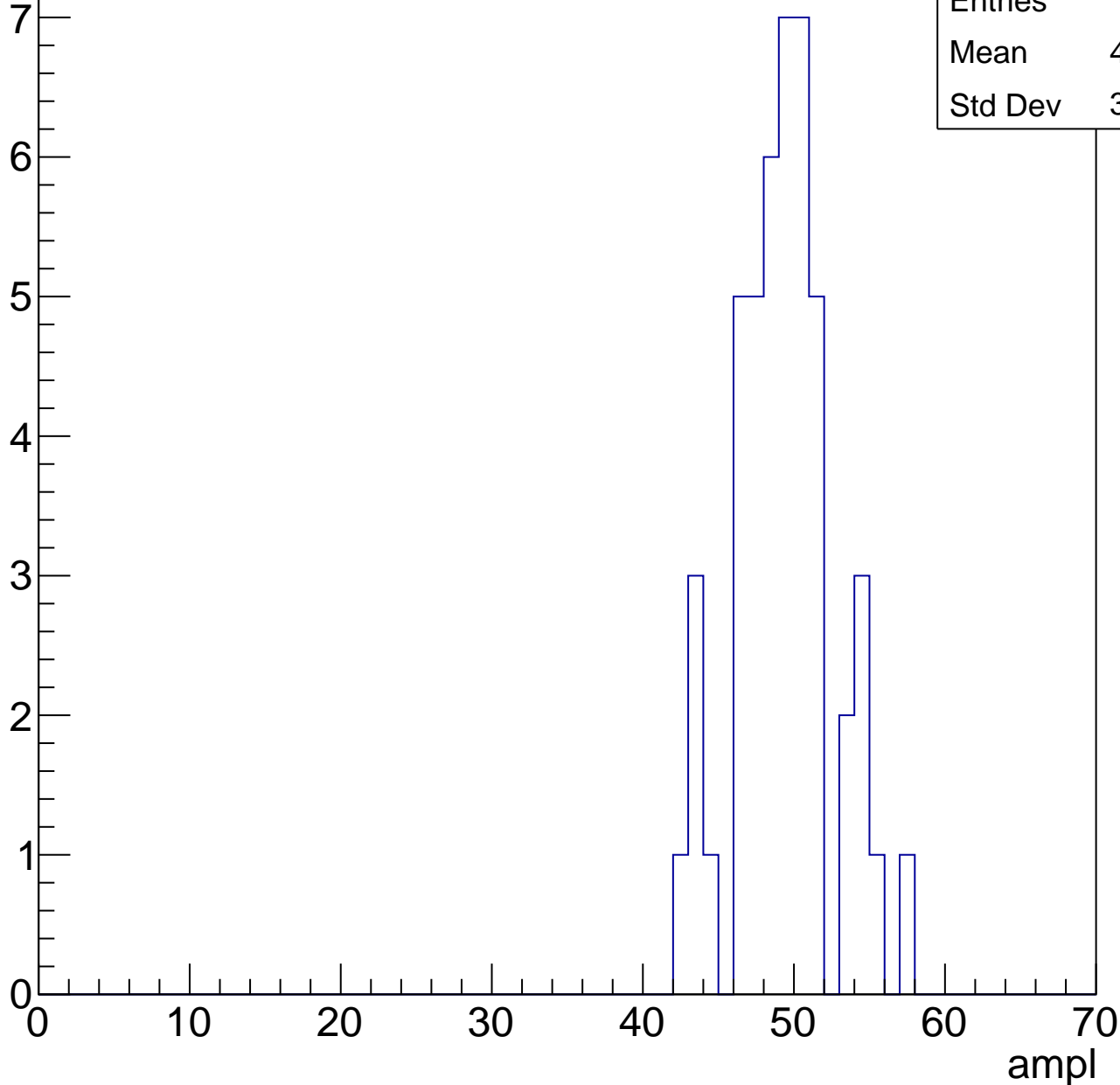
Gaus Width: 3.6005



B1L103S, U23-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	54.7
Std Dev	3.344

Entry

10

8

6

4

2

0

0

10

20

30

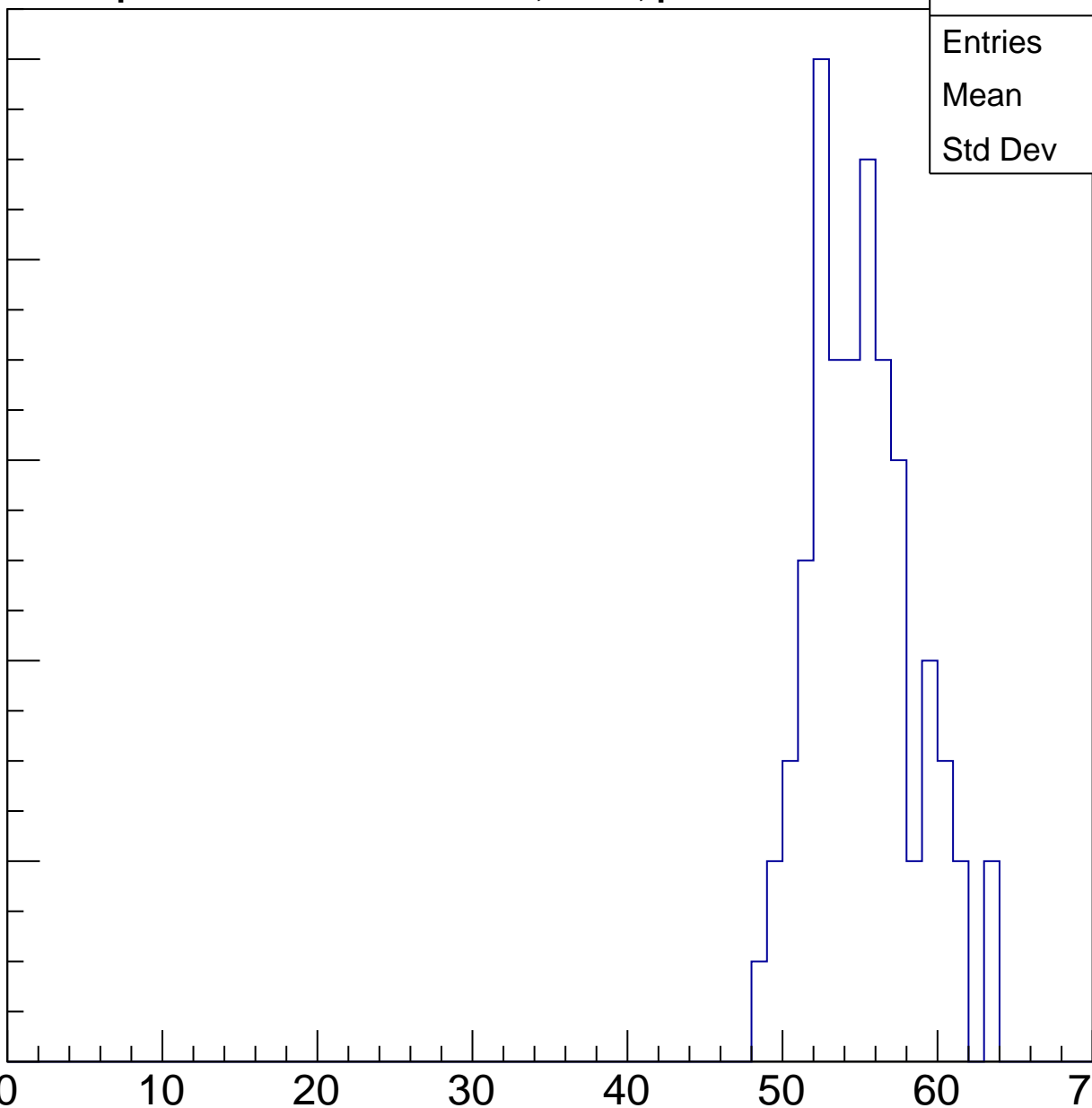
40

50

60

70

ampl

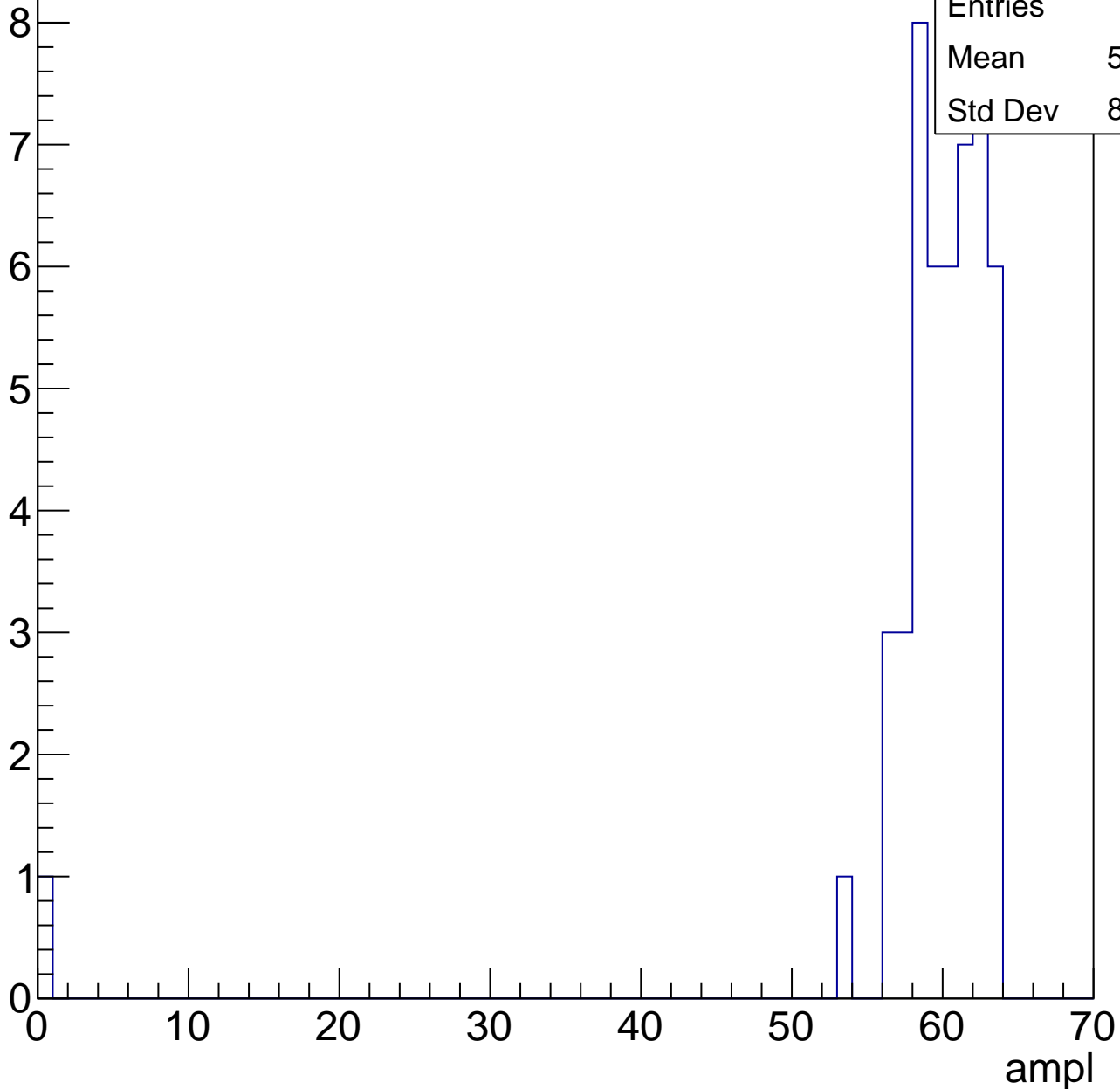


B1L103S, U23-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

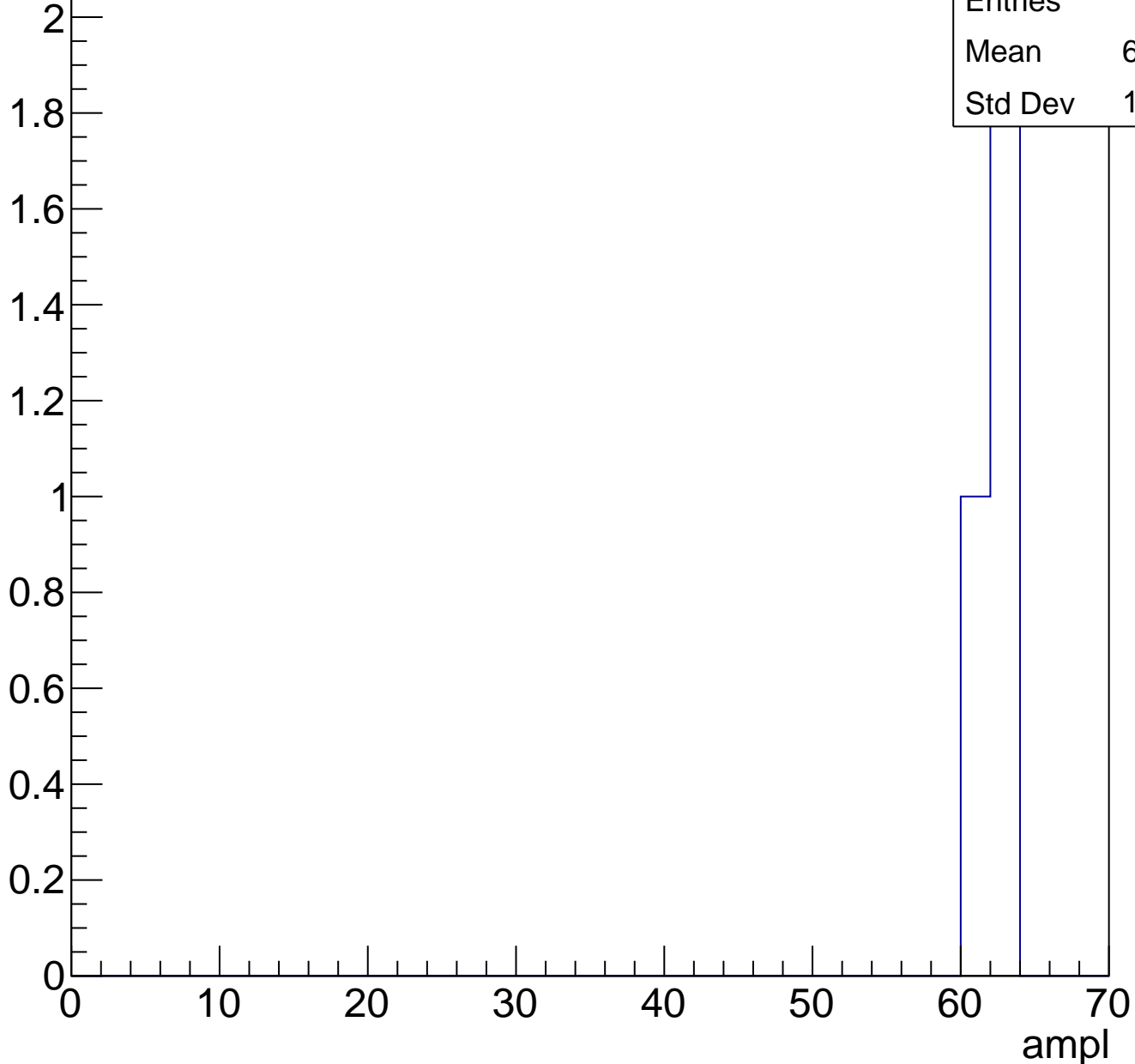
Entries	49
Mean	58.59
Std Dev	8.757



B1L103S, U23-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch39, adc0

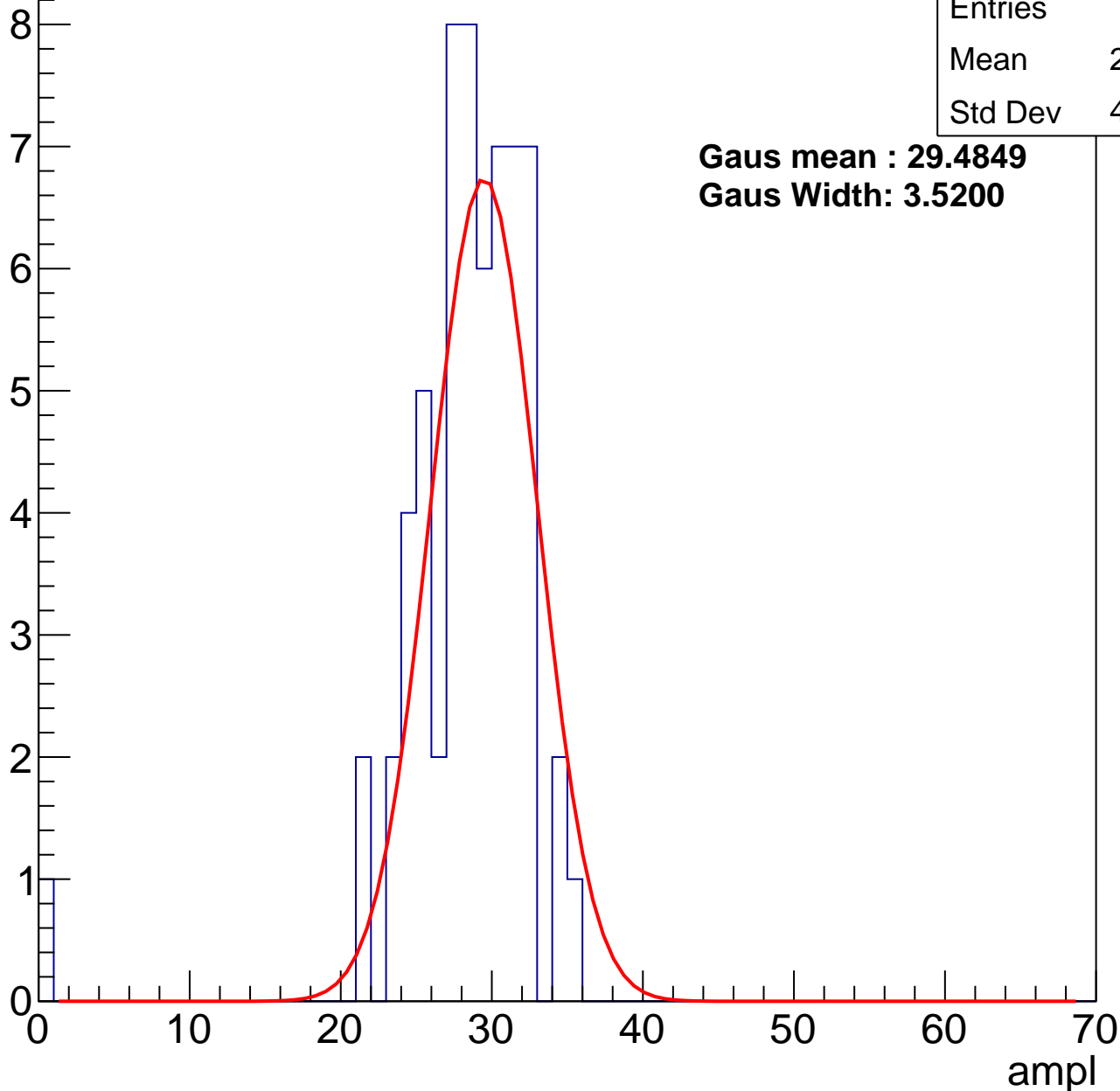
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	27.89
Std Dev	4.728

Gaus mean : 29.4849

Gaus Width: 3.5200



B1L103S, U23-ch39, adc1

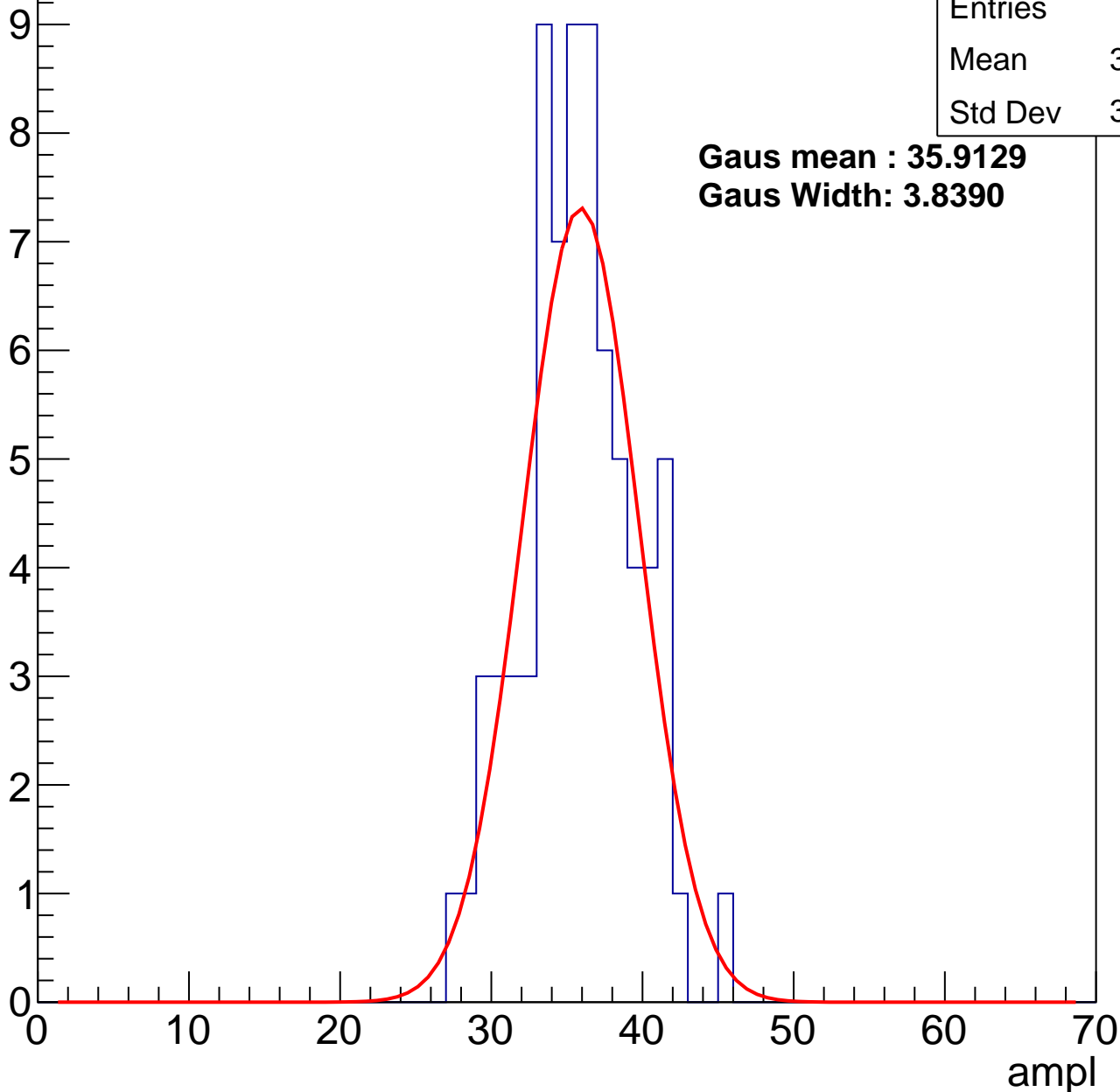
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.34
Std Dev	3.633

Gaus mean : 35.9129

Gaus Width: 3.8390



B1L103S, U23-ch39, adc2

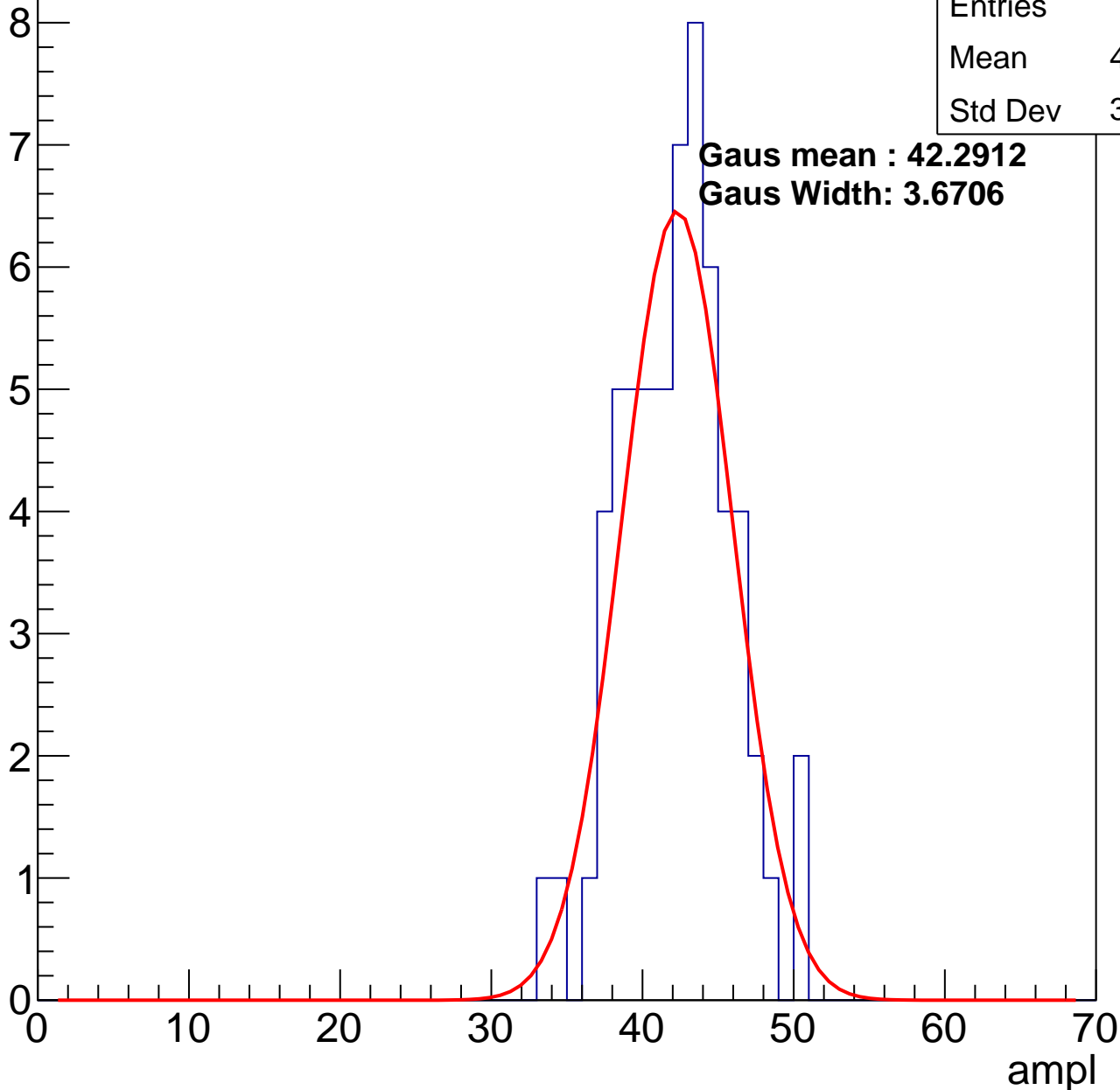
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.79
Std Dev	3.563

Gaus mean : 42.2912

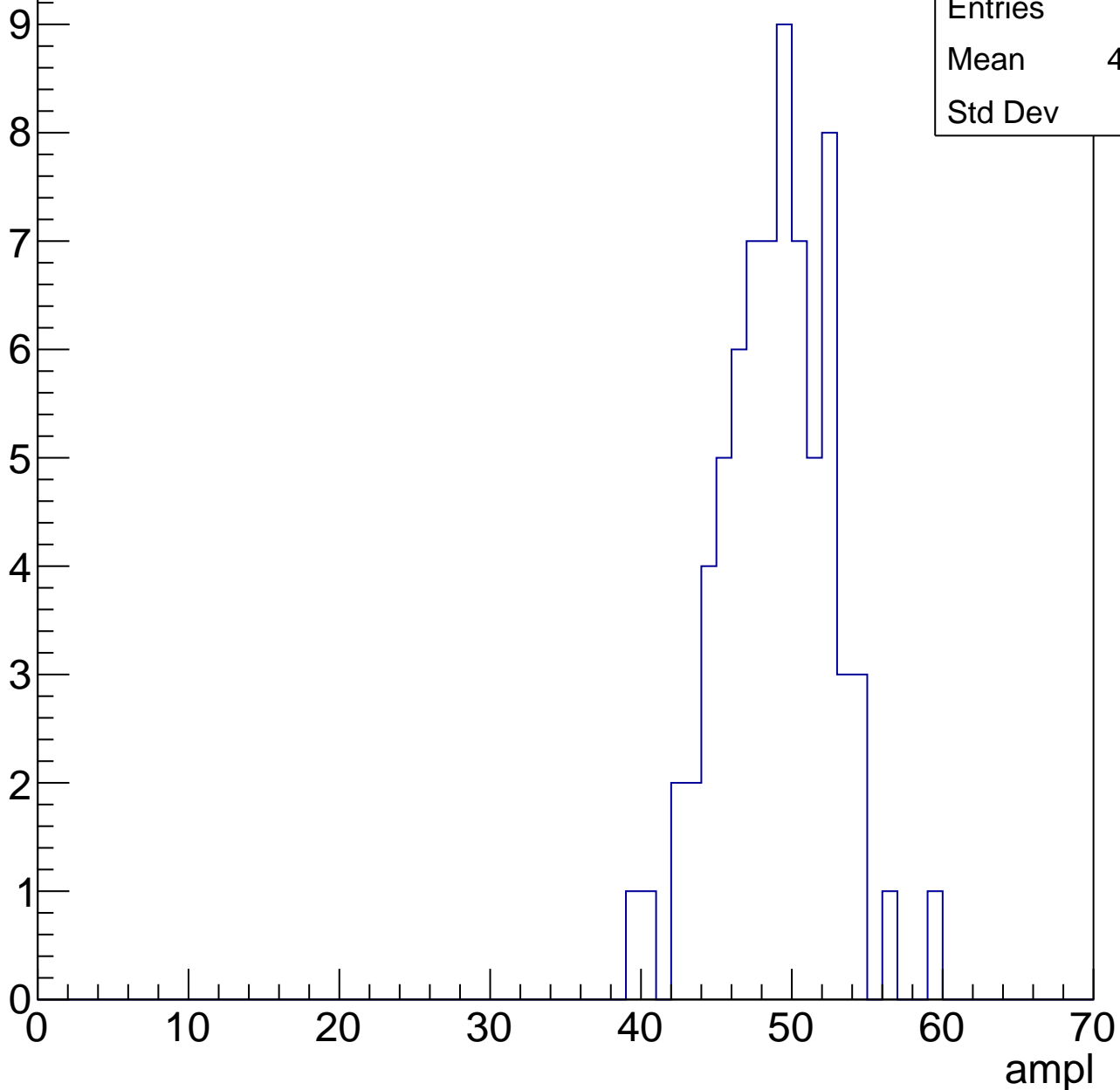
Gaus Width: 3.6706



B1L103S, U23-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

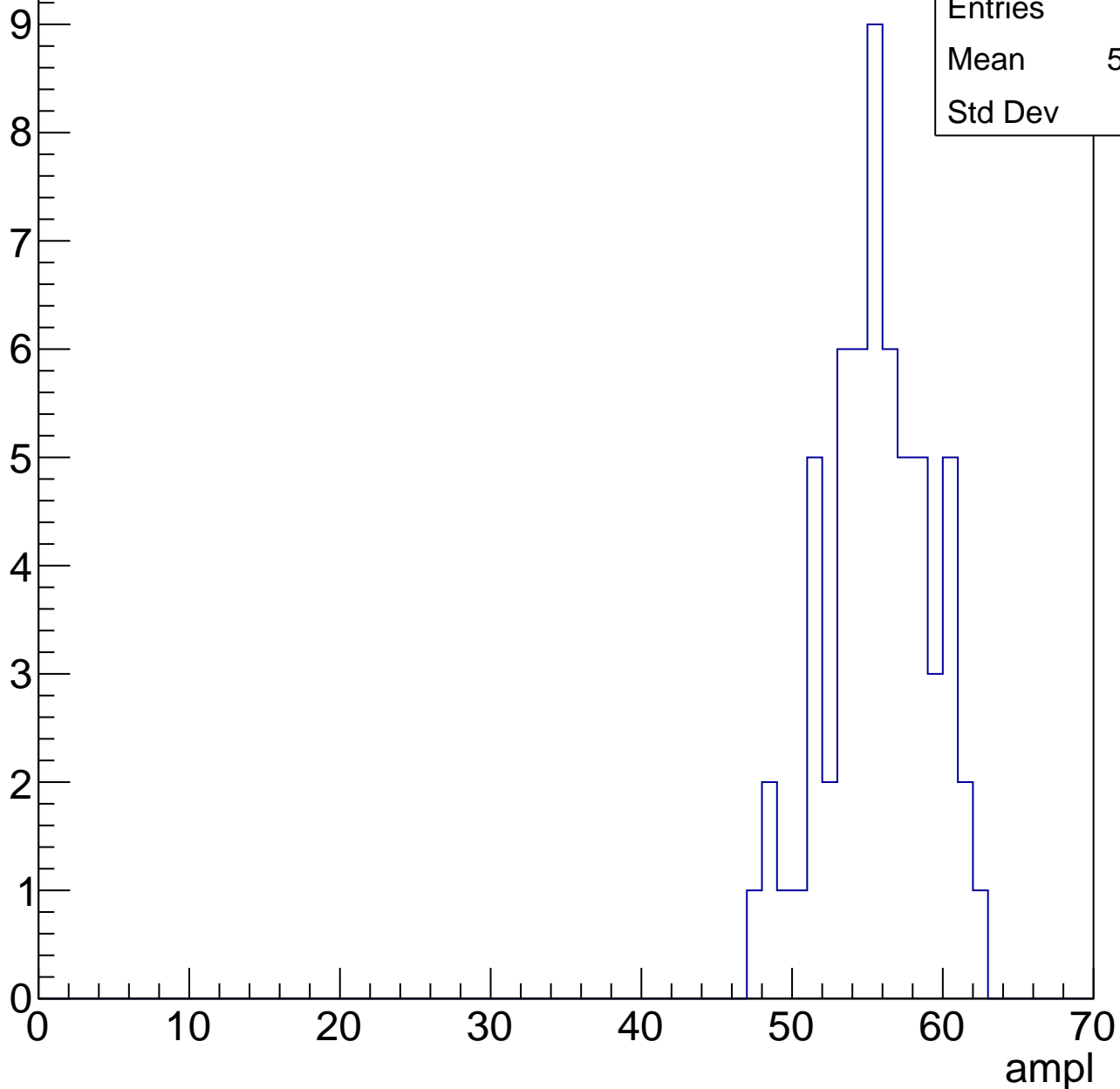
Entry



B1L103S, U23-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

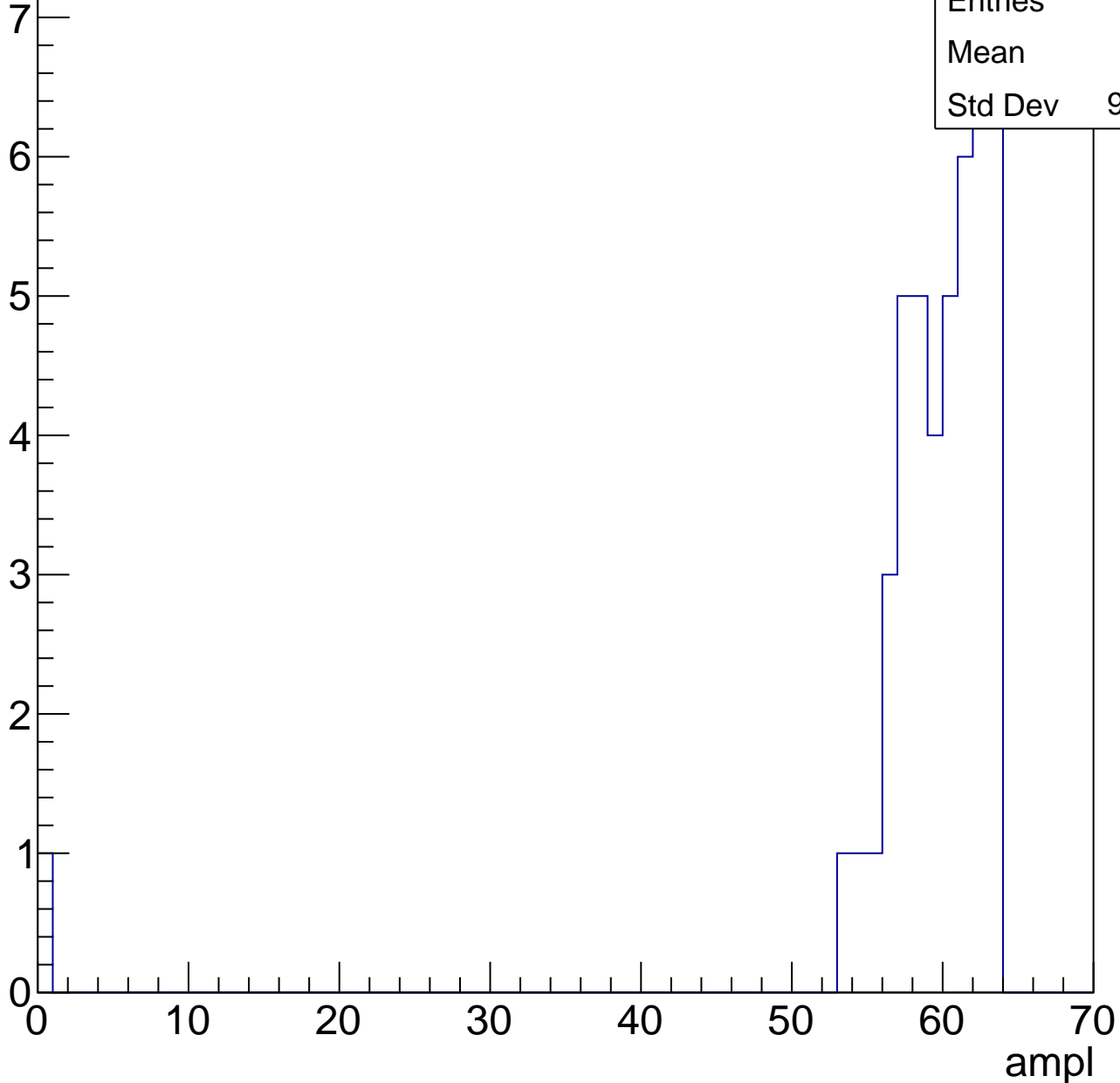


B1L103S, U23-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

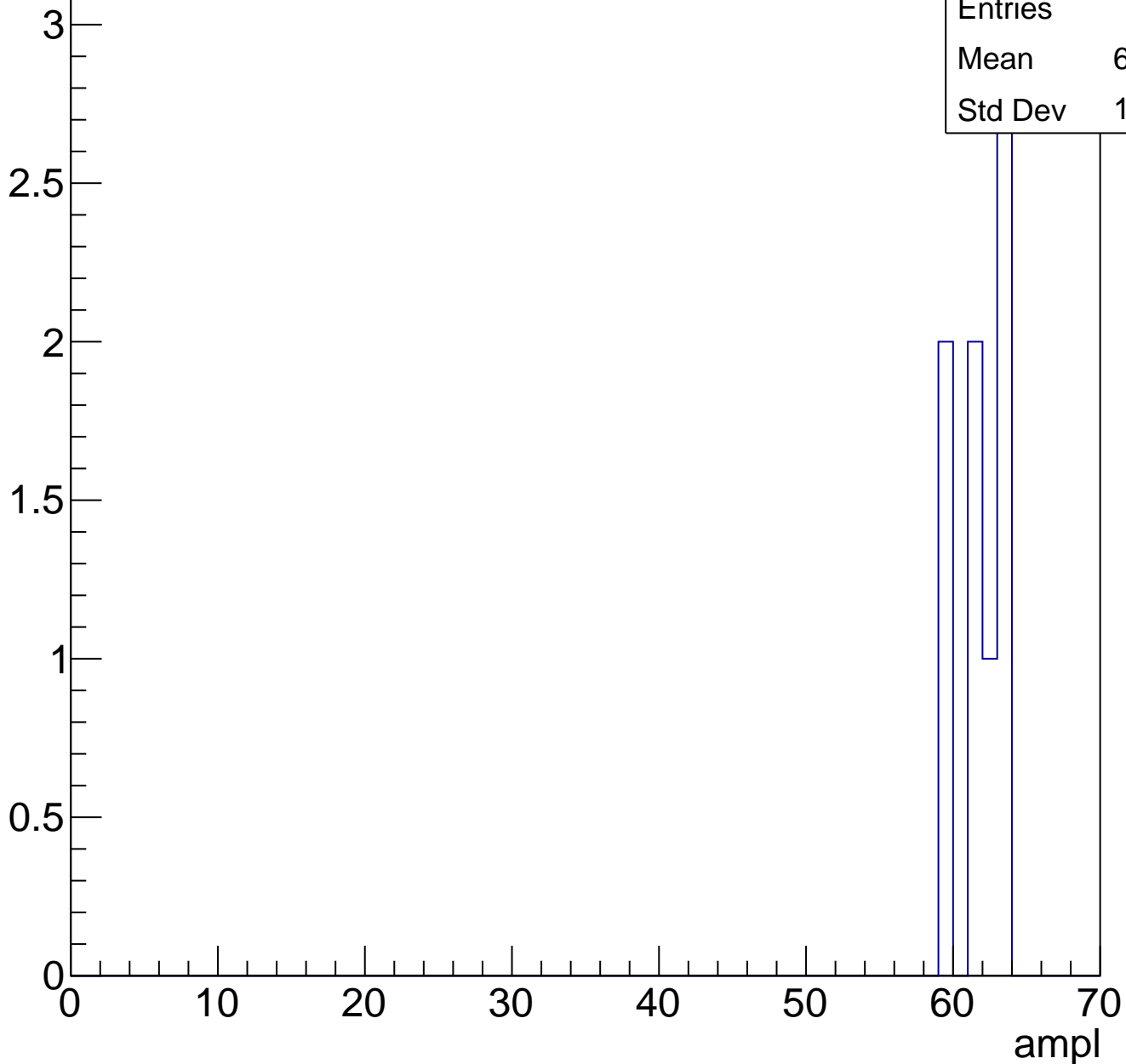
Entries	46
Mean	58.3
Std Dev	9.079



B1L103S, U23-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch40, adc0

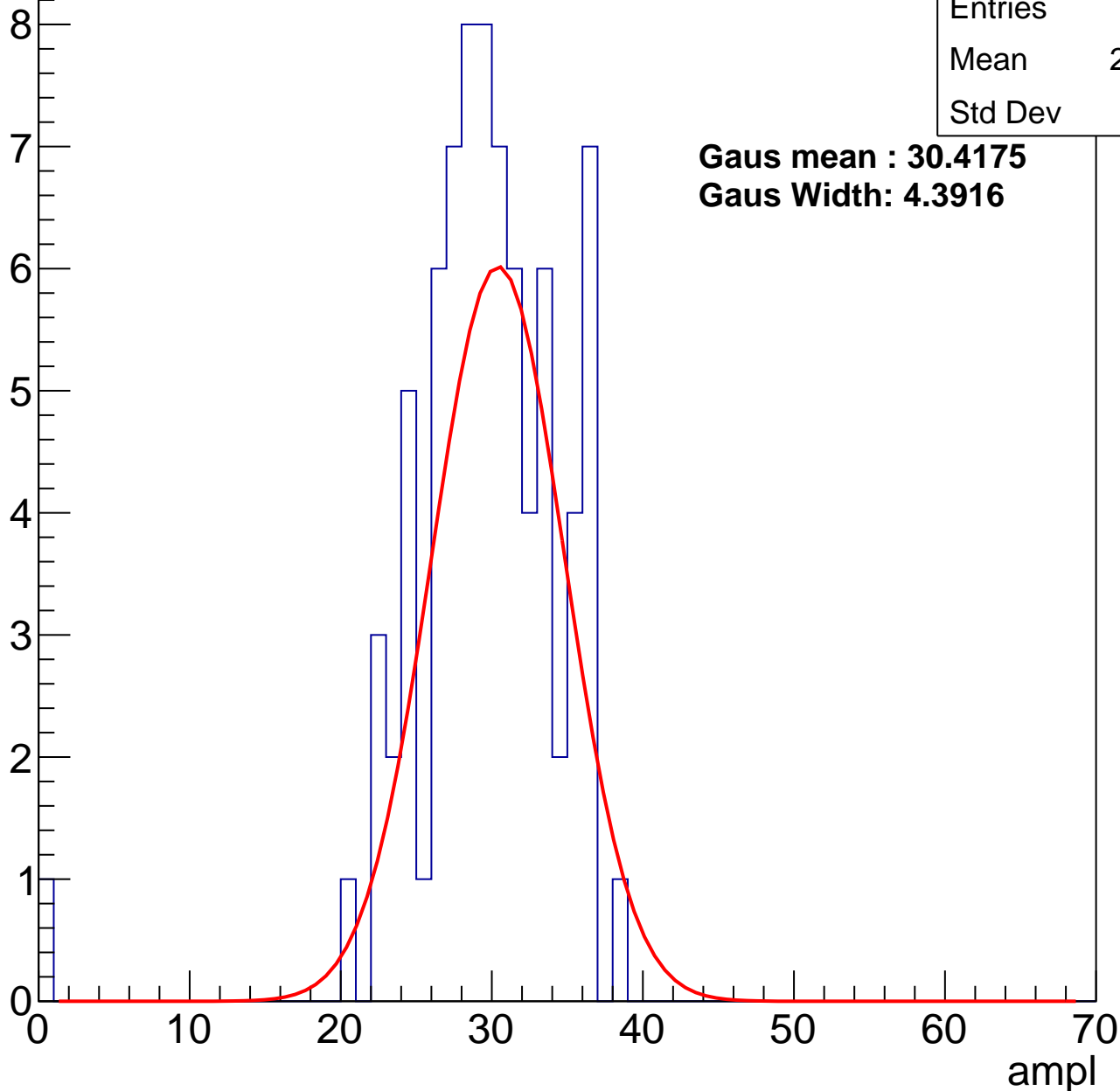
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	29.09
Std Dev	5.22

Gaus mean : 30.4175

Gaus Width: 4.3916



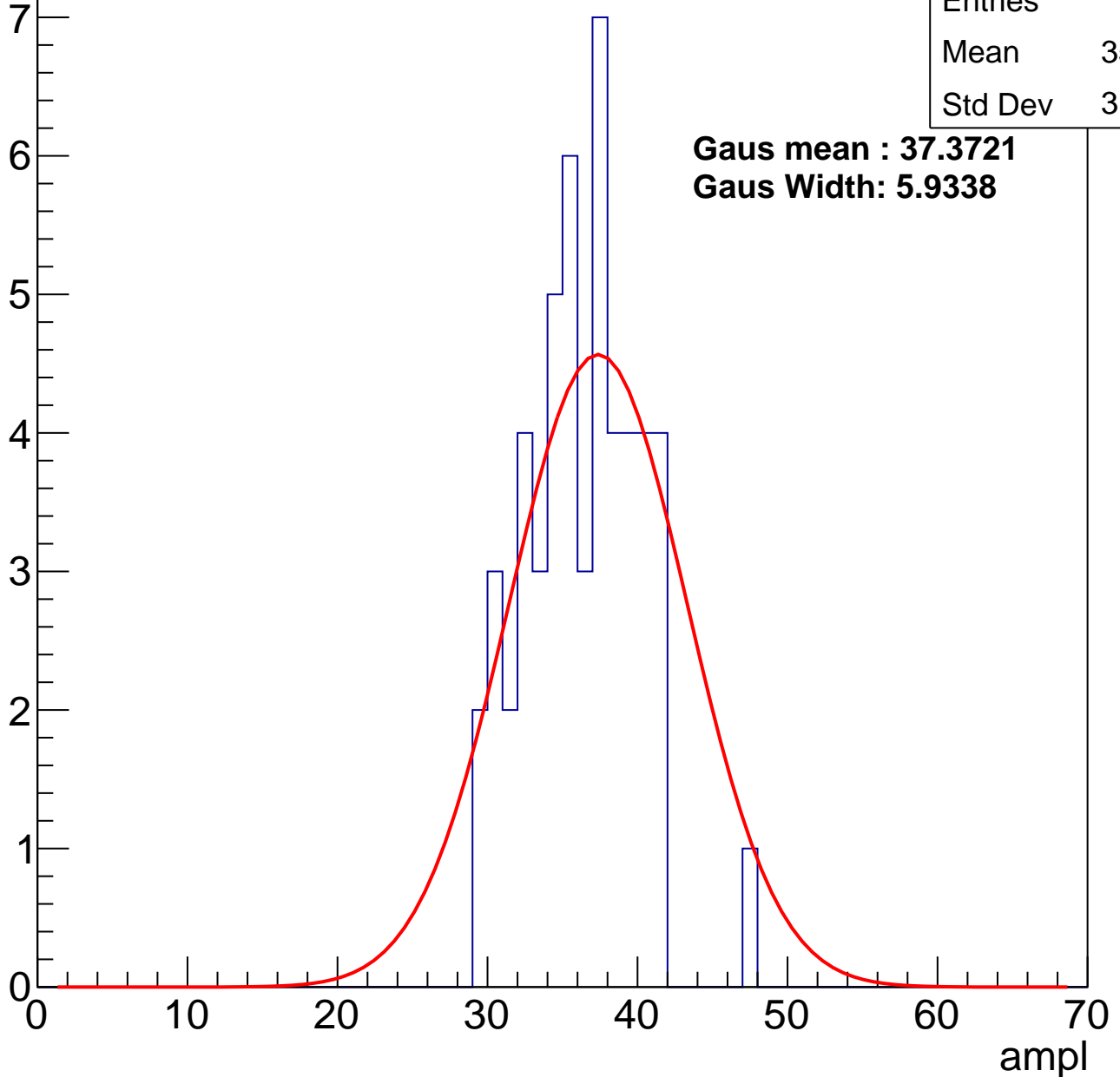
B1L103S, U23-ch40, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	35.83
Std Dev	3.709

Gaus mean : 37.3721
Gaus Width: 5.9338



B1L103S, U23-ch40, adc2

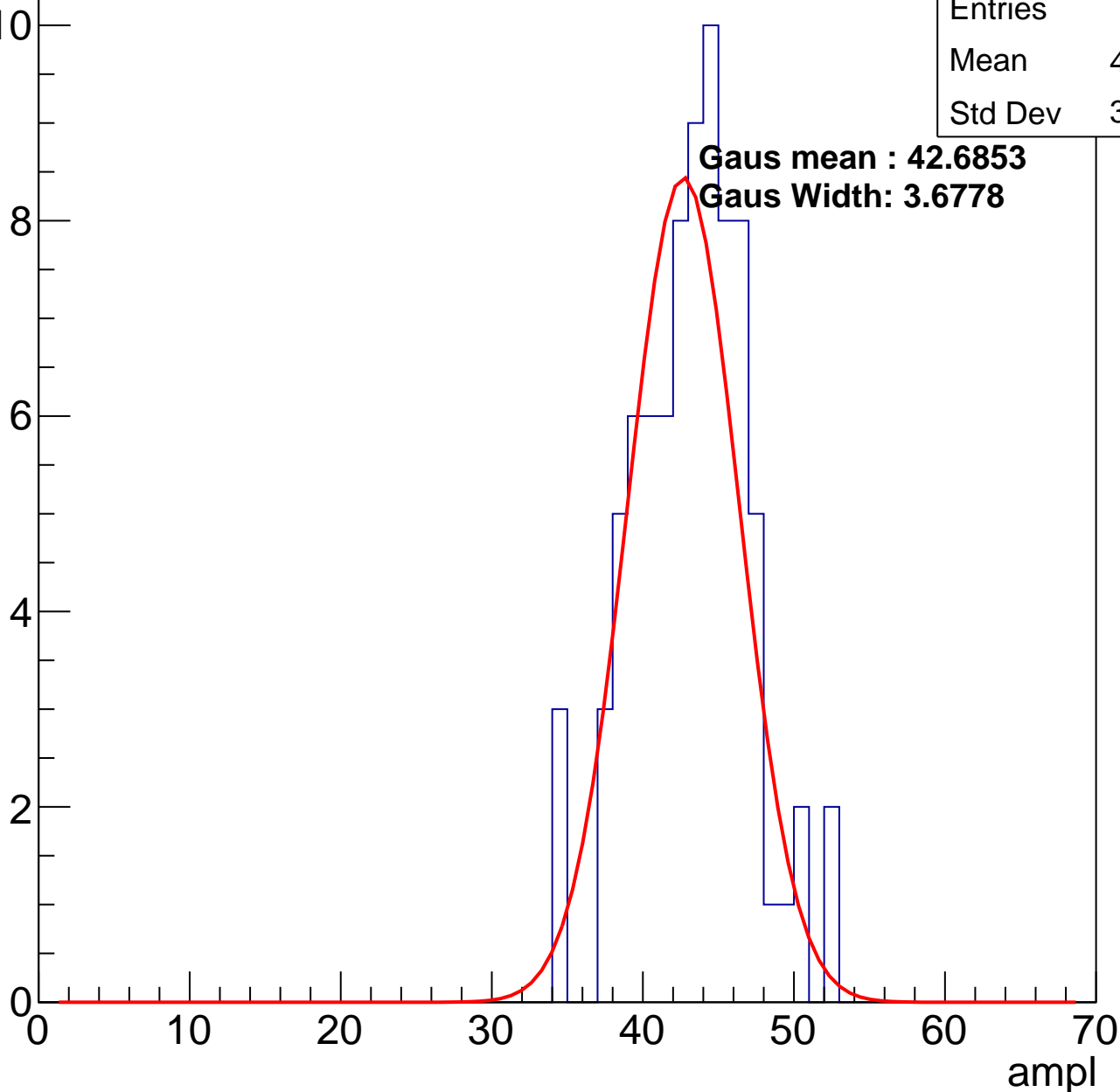
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	42.77
Std Dev	3.752

Gaus mean : 42.6853

Gaus Width: 3.6778

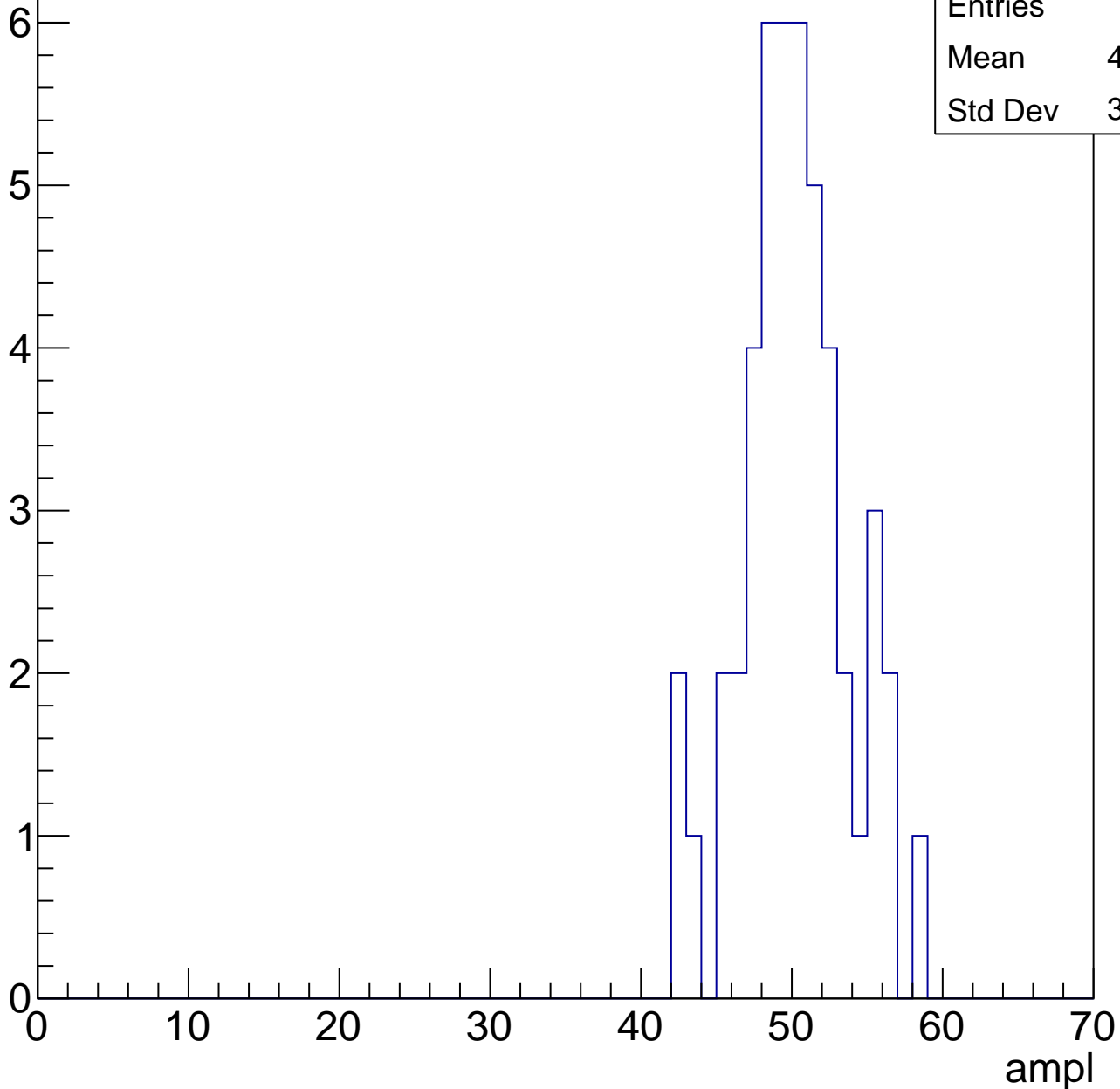


B1L103S, U23-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	49.72
Std Dev	3.535

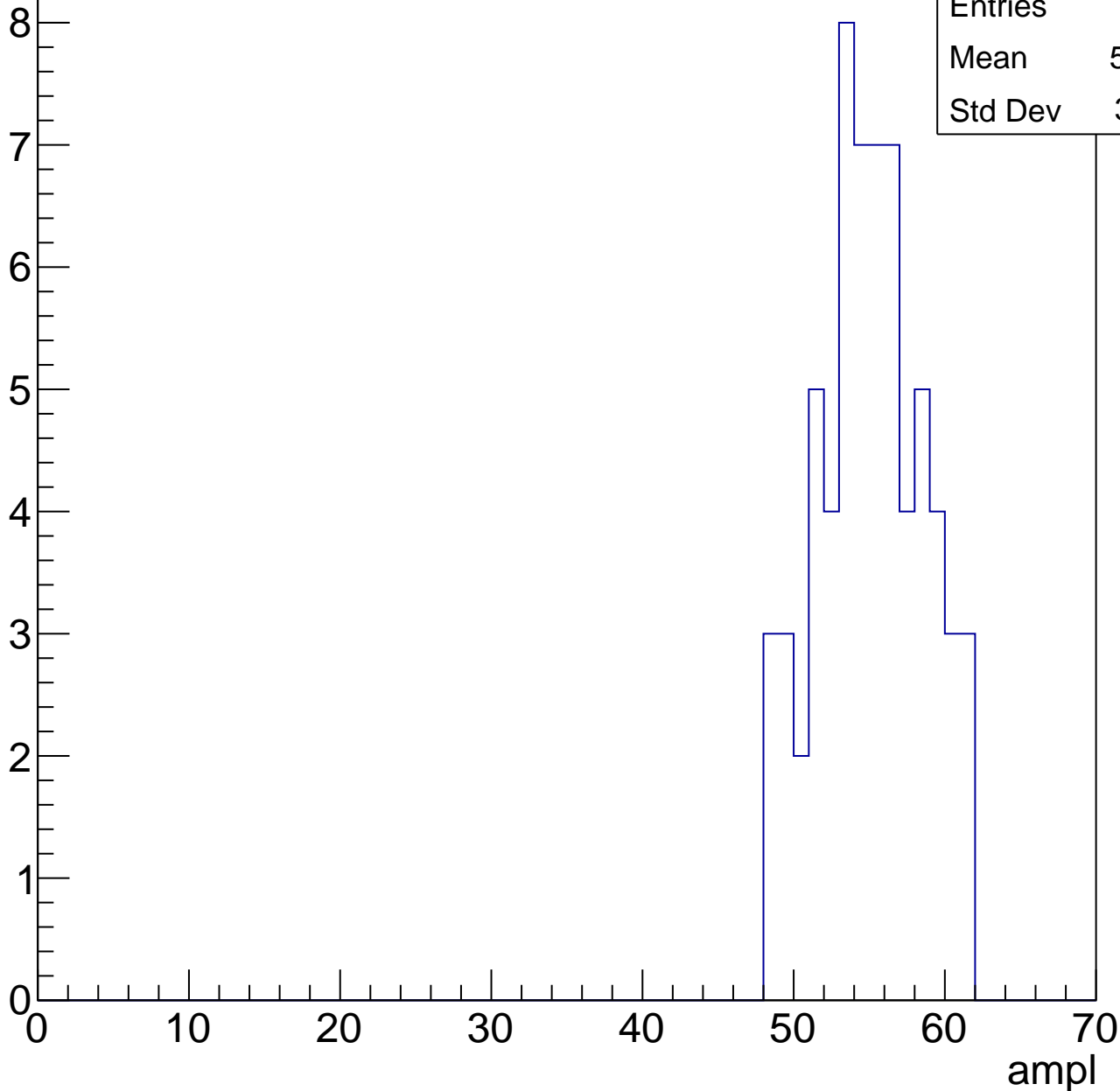


B1L103S, U23-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	54.62
Std Dev	3.431

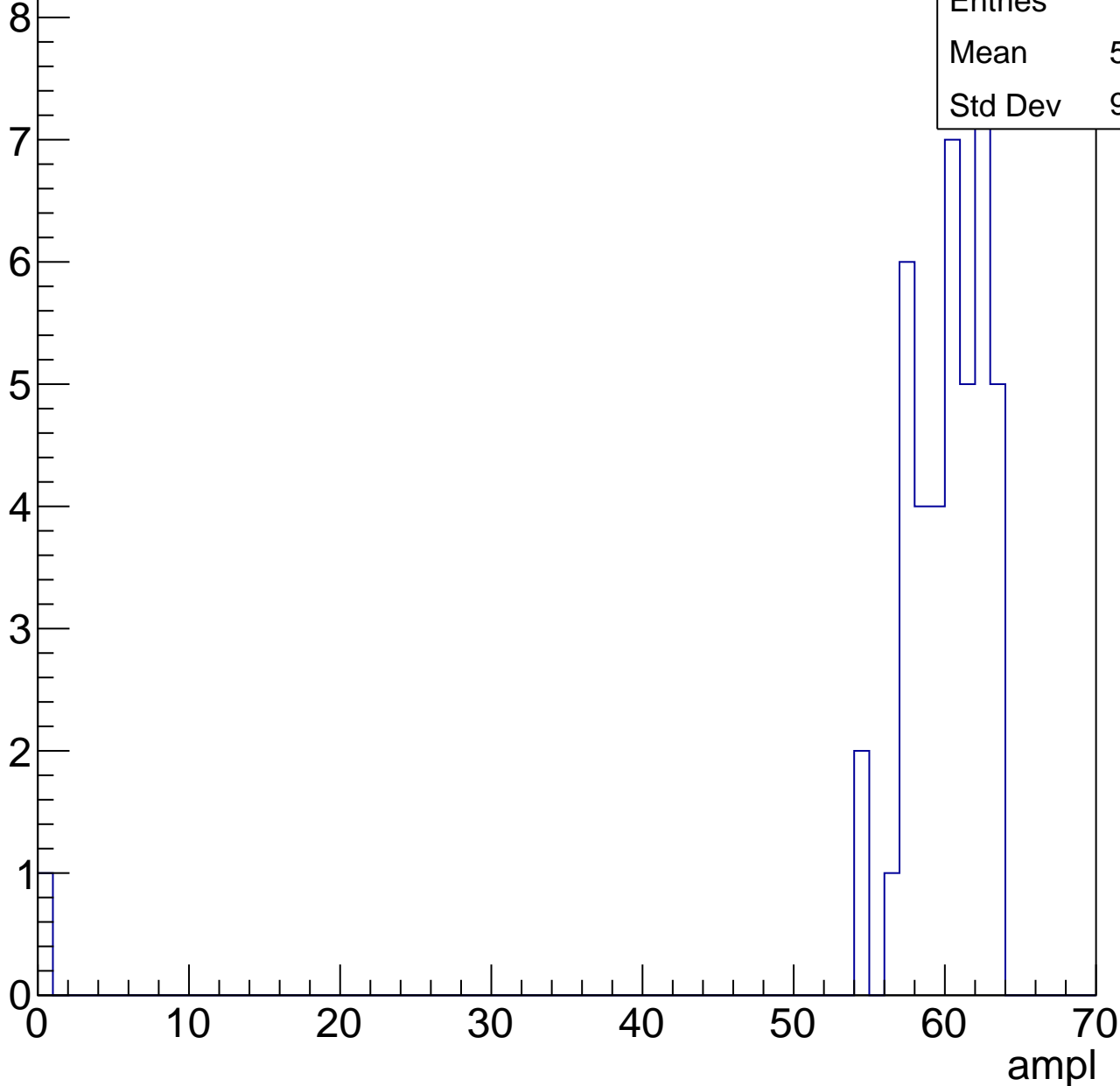


B1L103S, U23-ch40, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.37
Std Dev	9.314

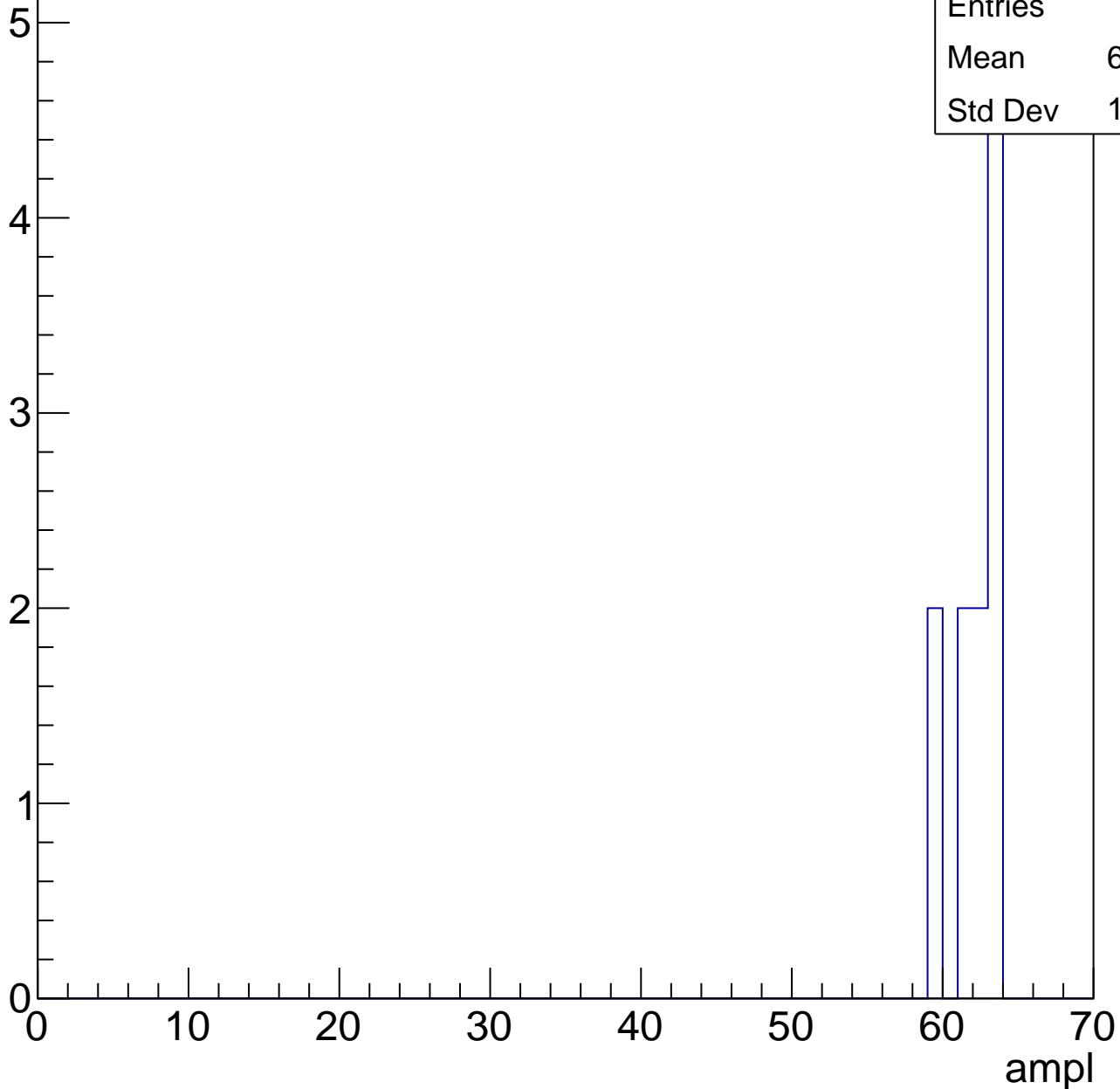


B1L103S, U23-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	61.73
Std Dev	1.483



B1L103S, U23-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch41, adc0

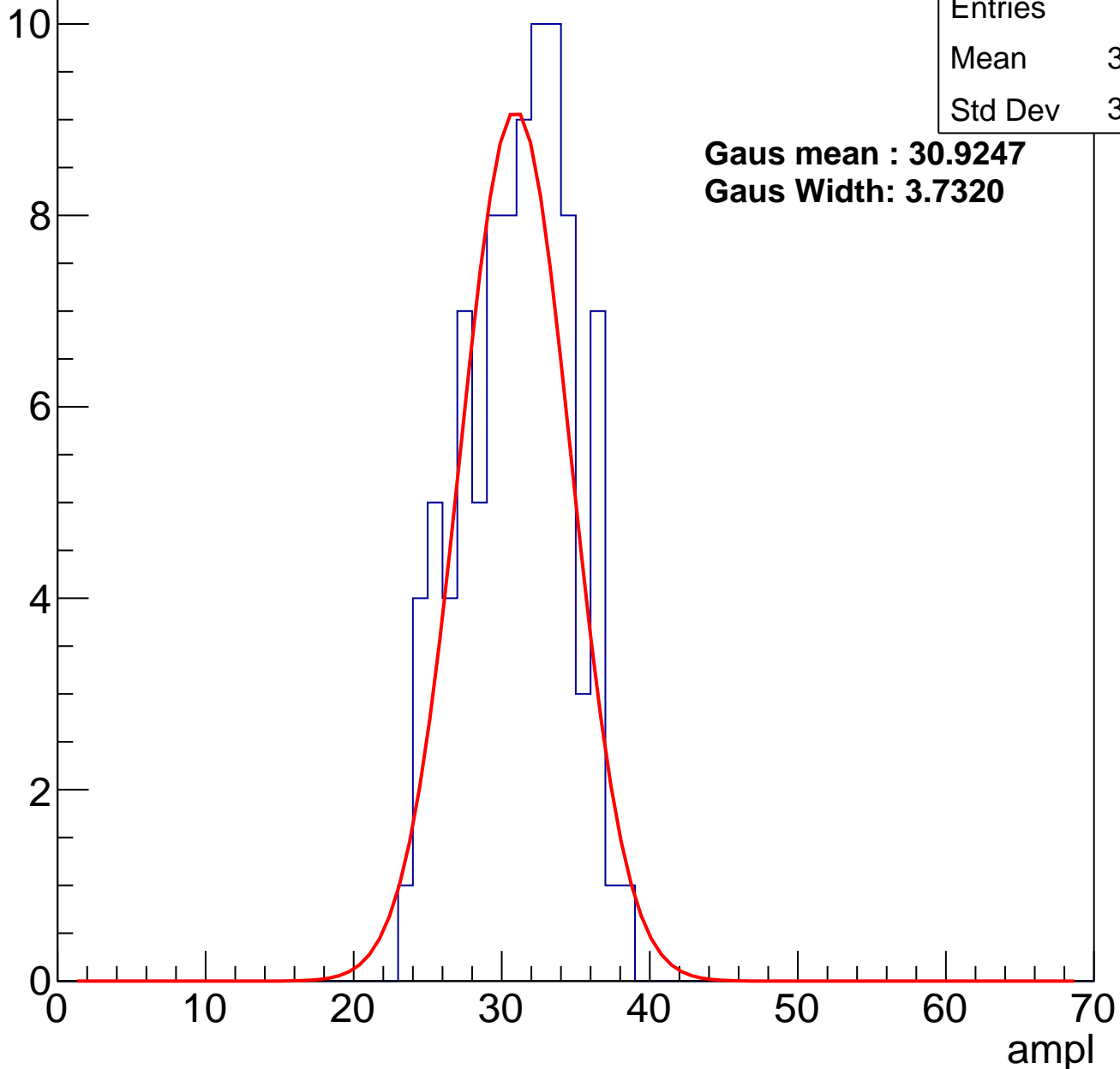
calib_packv5_042523_0143.root, FC#7, port C2

Entries	91
Mean	30.57
Std Dev	3.555

Gaus mean : 30.9247

Gaus Width: 3.7320

Entry



B1L103S, U23-ch41, adc1

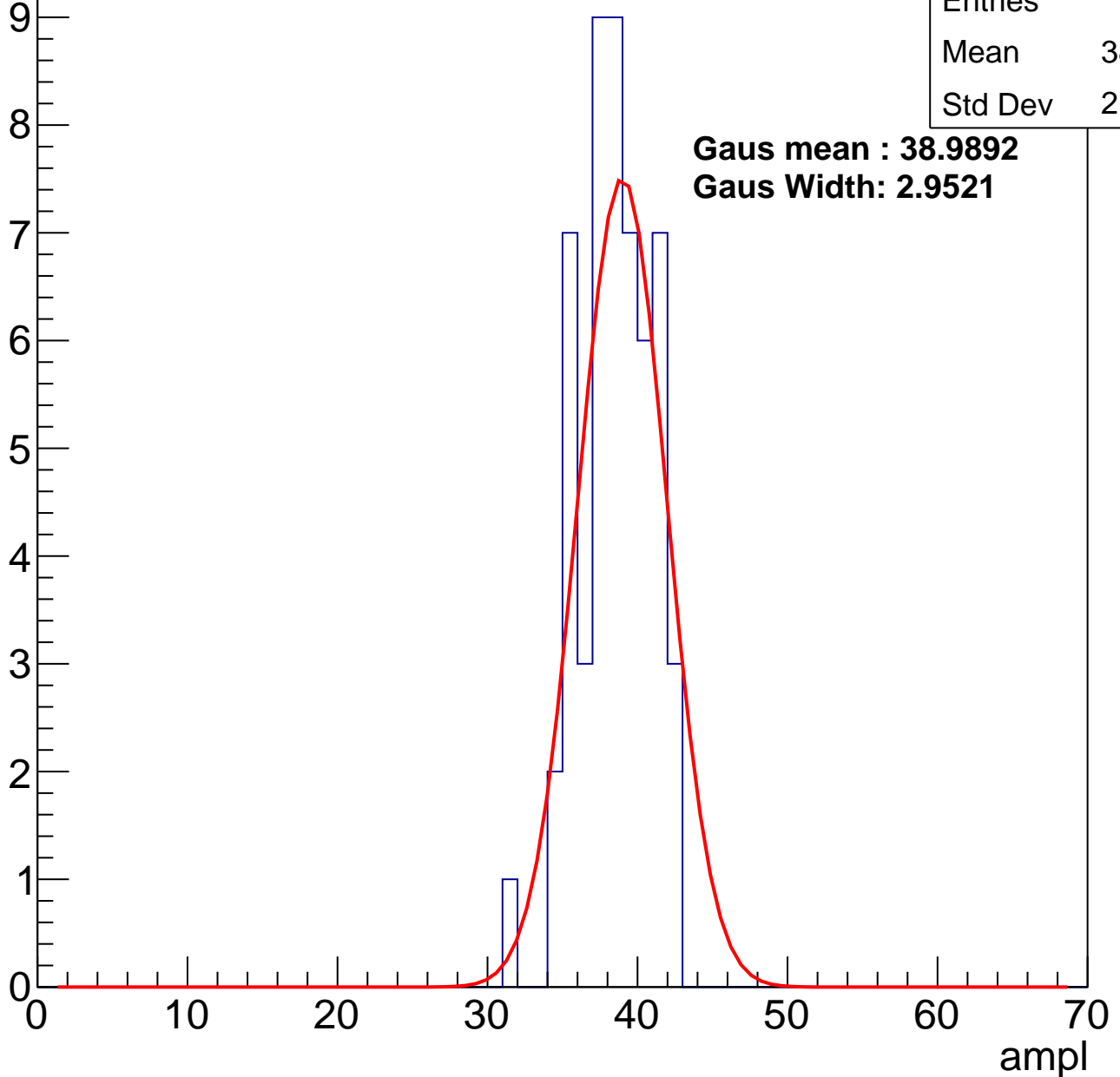
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	38.02
Std Dev	2.384

Gaus mean : 38.9892

Gaus Width: 2.9521



B1L103S, U23-ch41, adc2

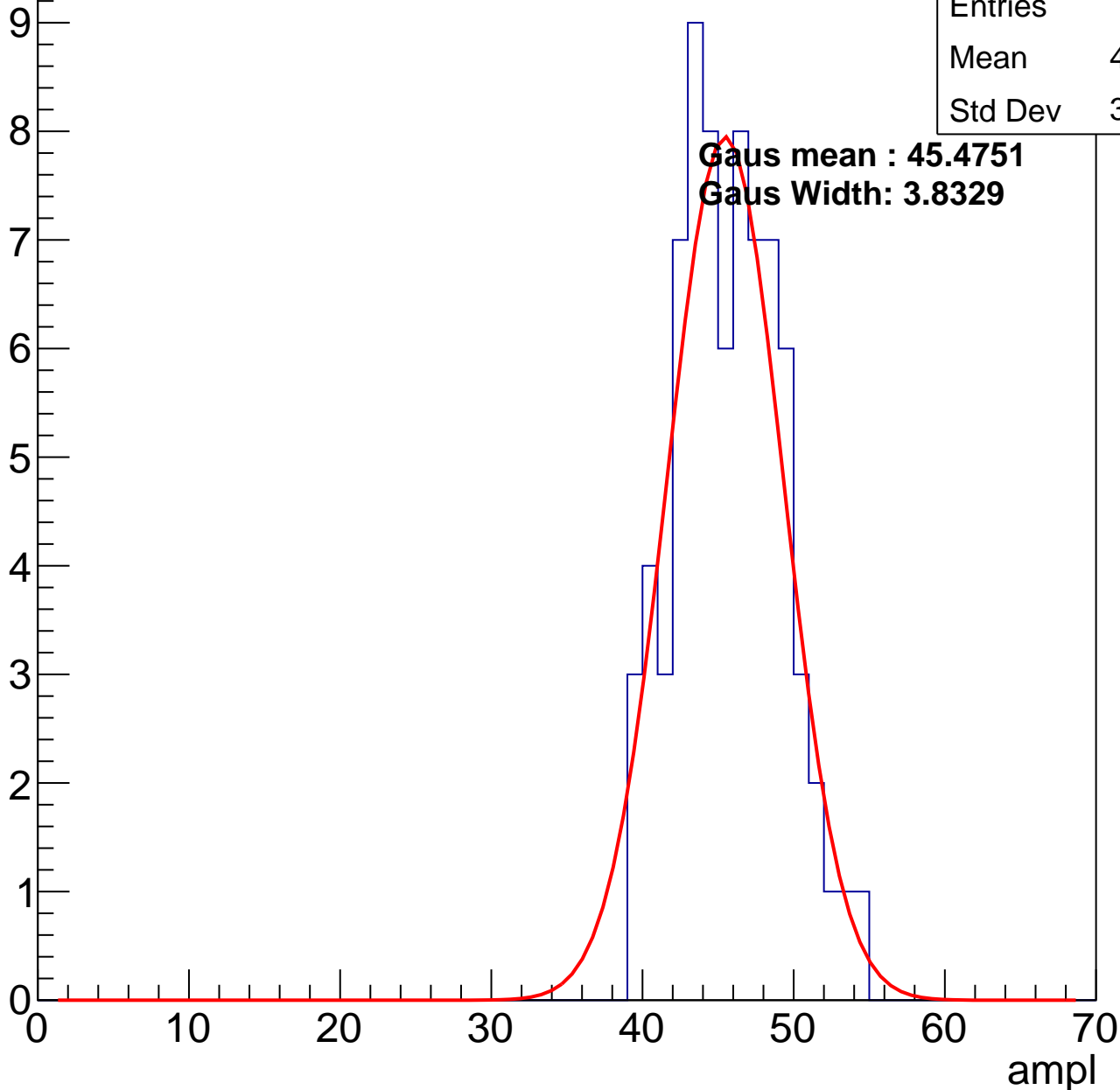
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	45.28
Std Dev	3.428

Gaus mean : 45.4751

Gaus Width: 3.8329

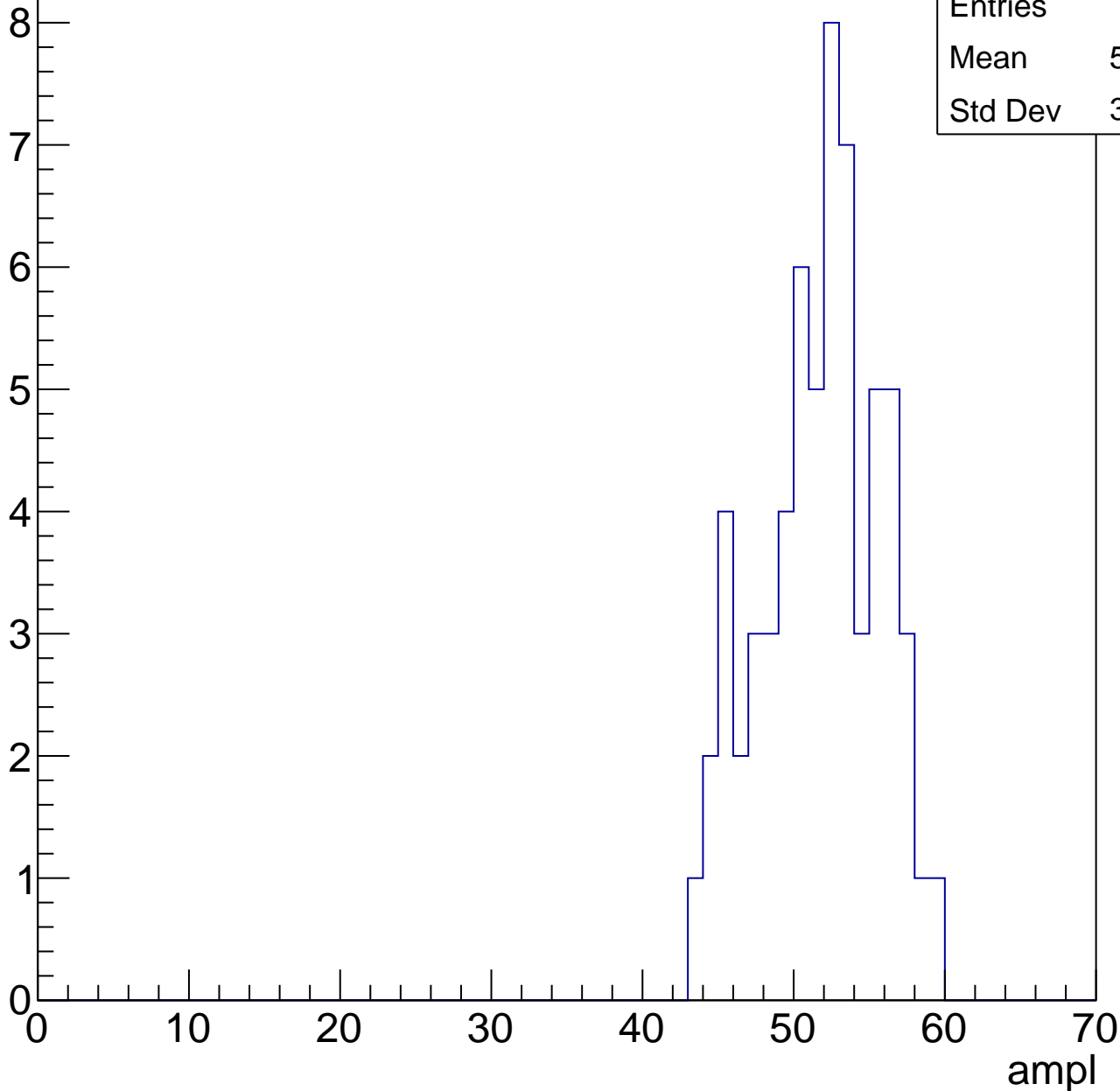


B1L103S, U23-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	51.29
Std Dev	3.856

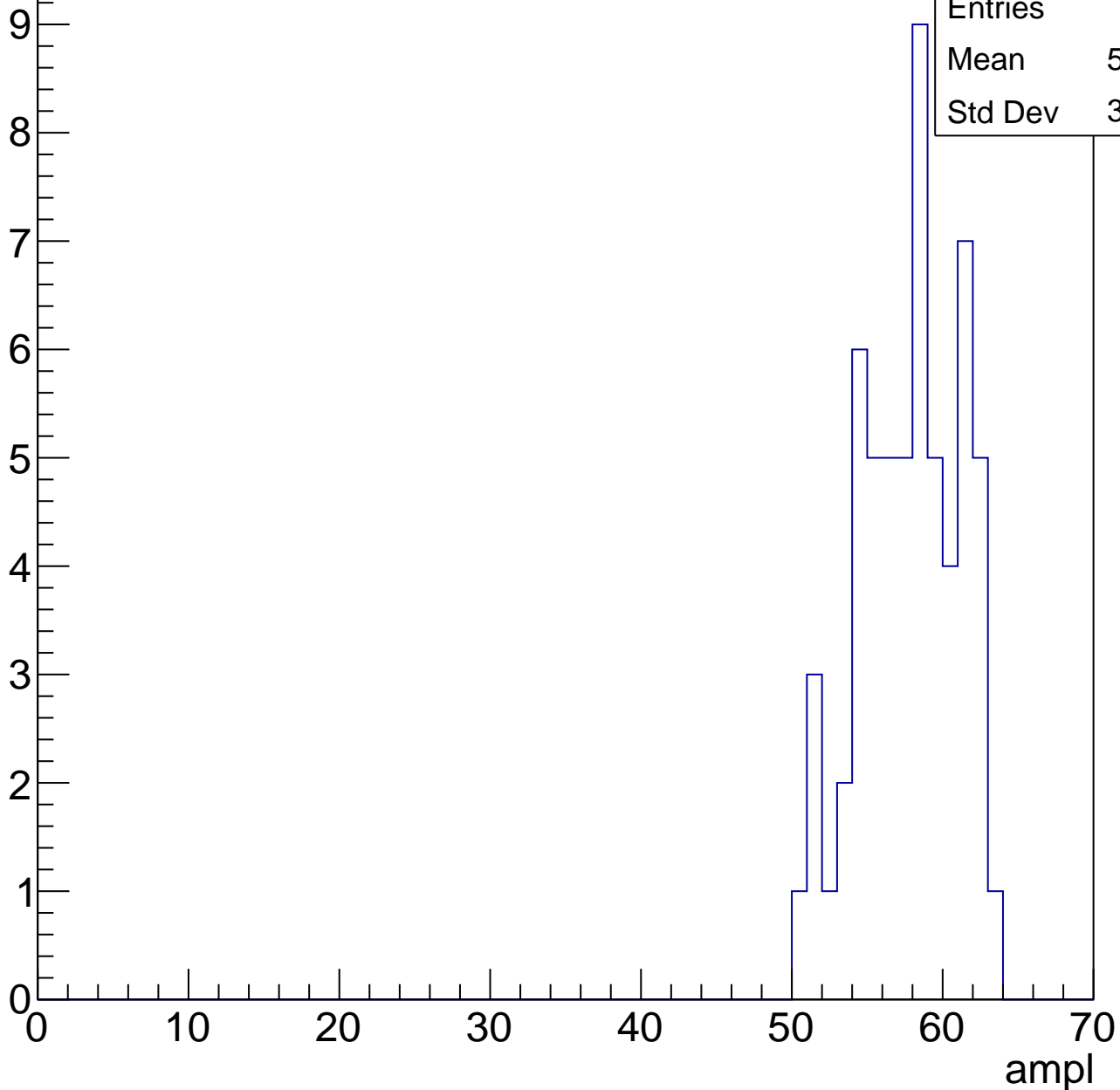


B1L103S, U23-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

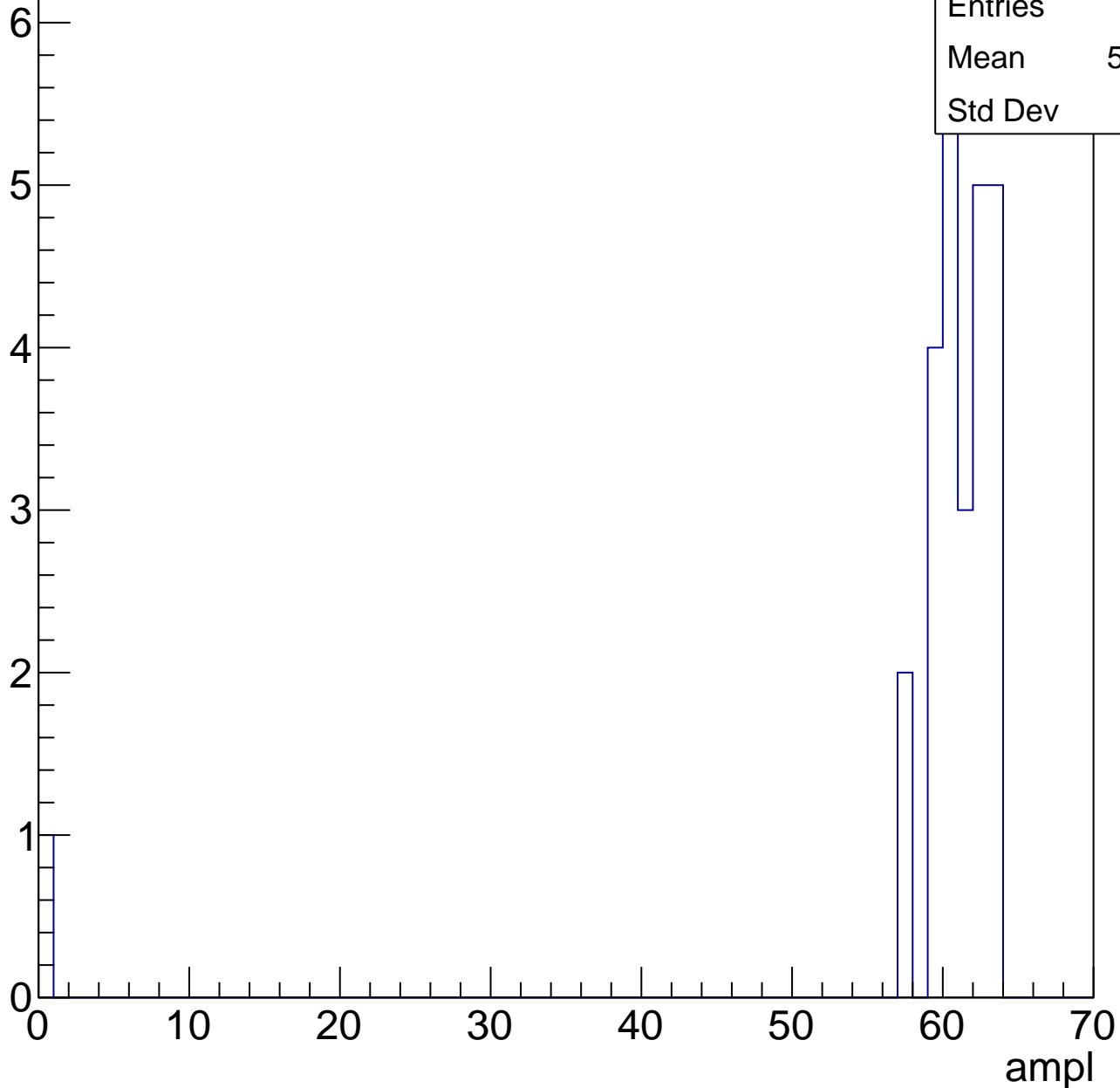
Entries	59
Mean	57.32
Std Dev	3.254



B1L103S, U23-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch42, adc0

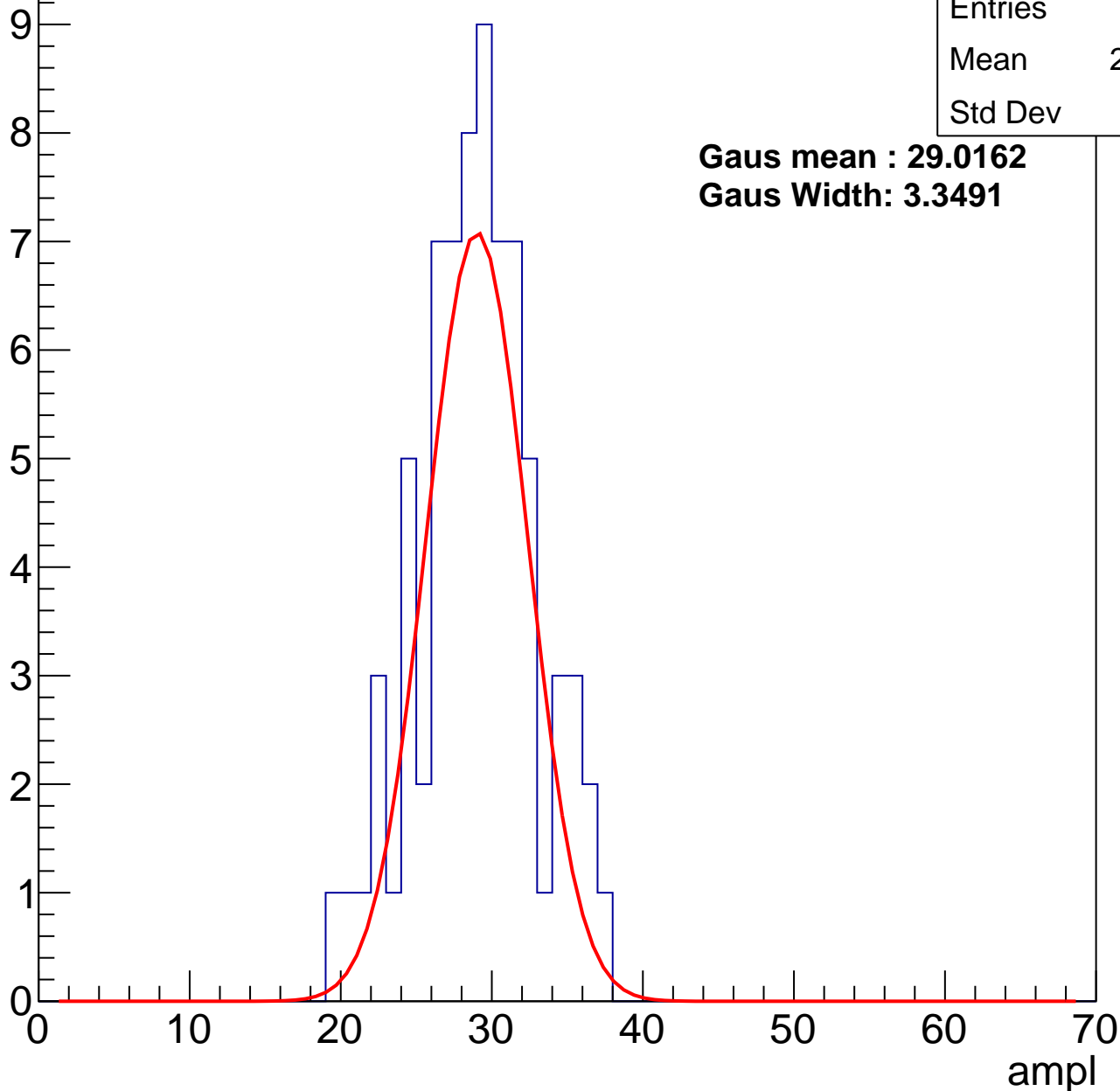
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.53
Std Dev	3.87

Gaus mean : 29.0162

Gaus Width: 3.3491



B1L103S, U23-ch42, adc1

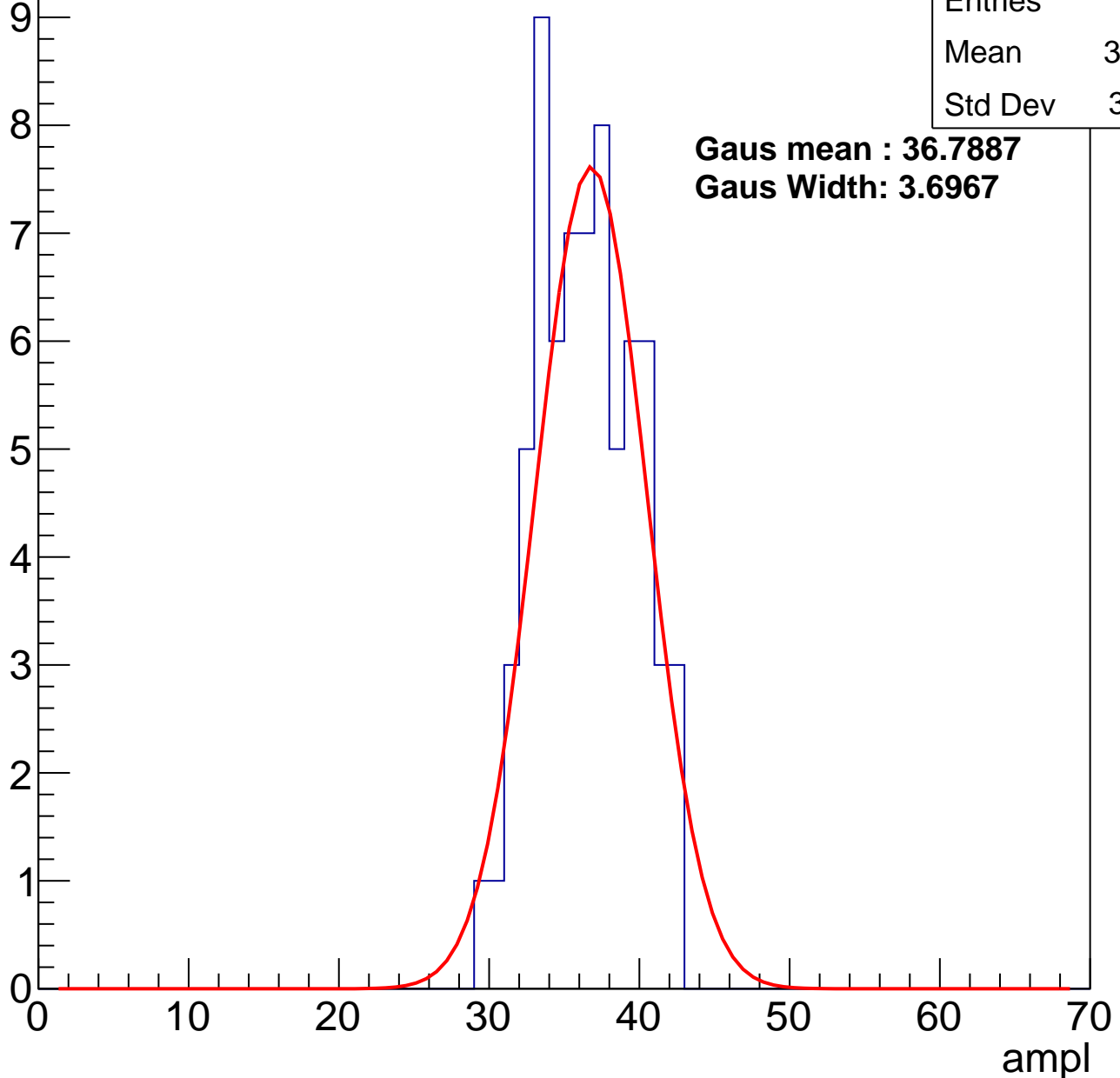
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.99
Std Dev	3.191

Gaus mean : 36.7887

Gaus Width: 3.6967

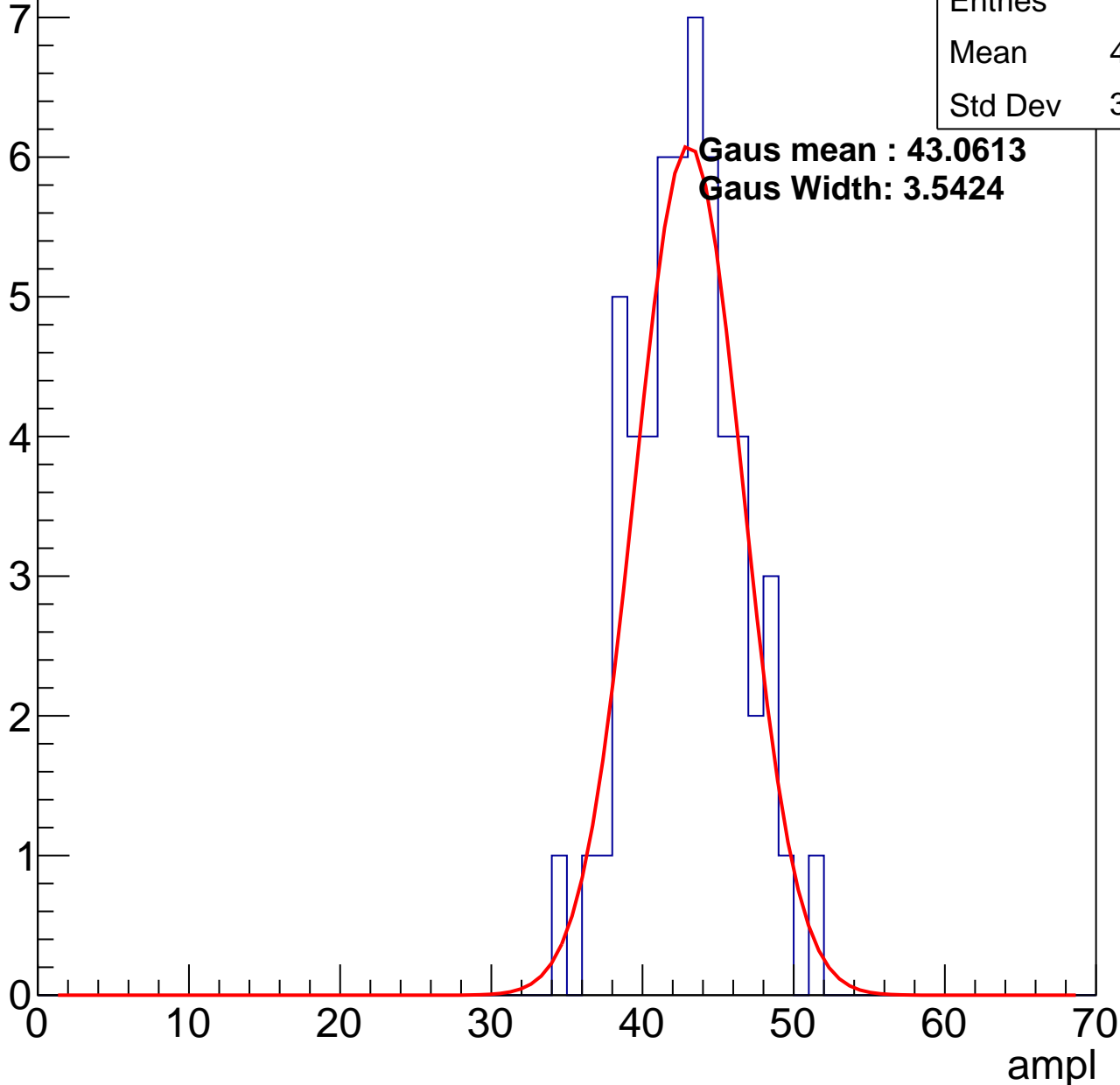


B1L103S, U23-ch42, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42.46
Std Dev	3.459

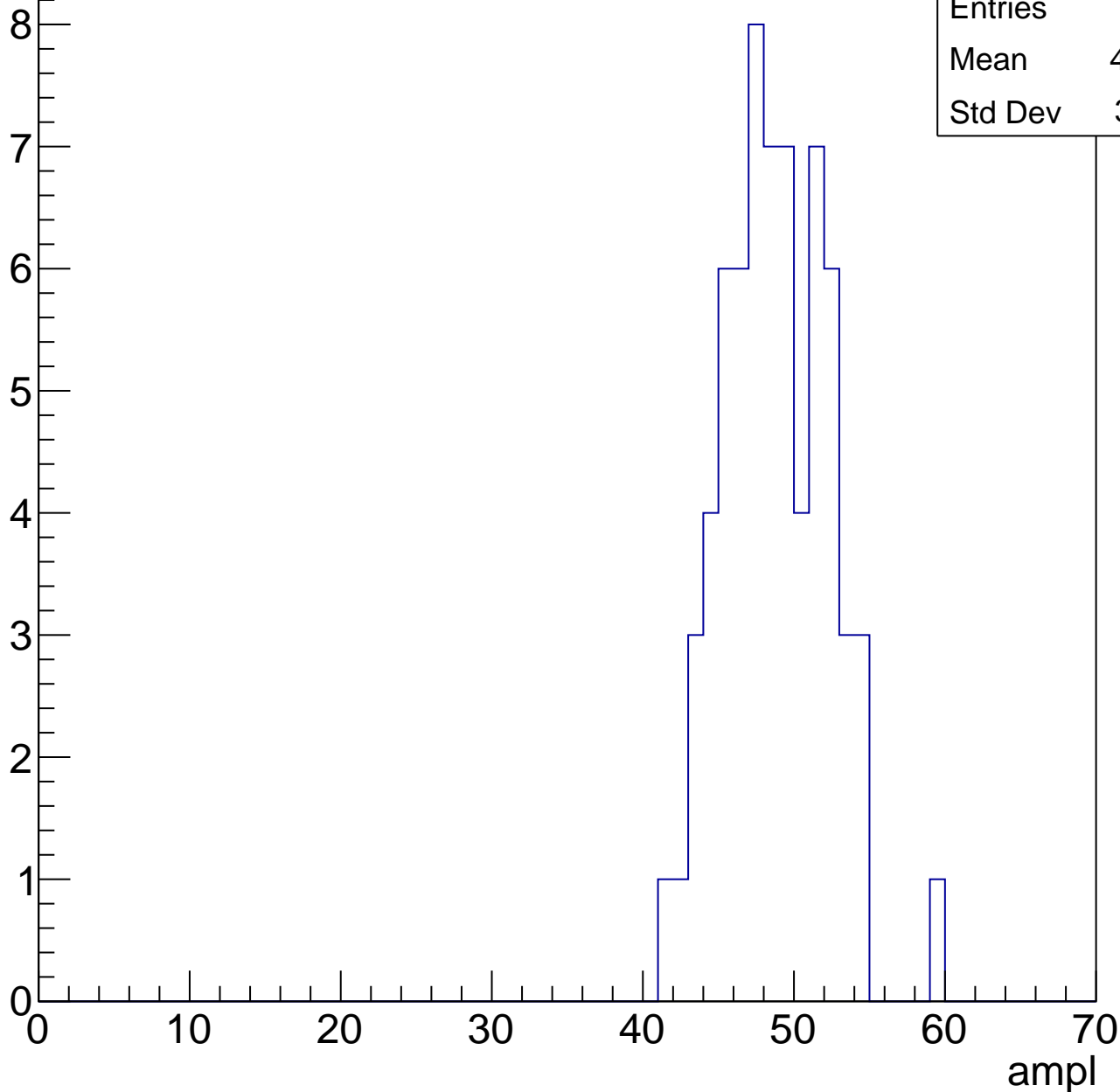


B1L103S, U23-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

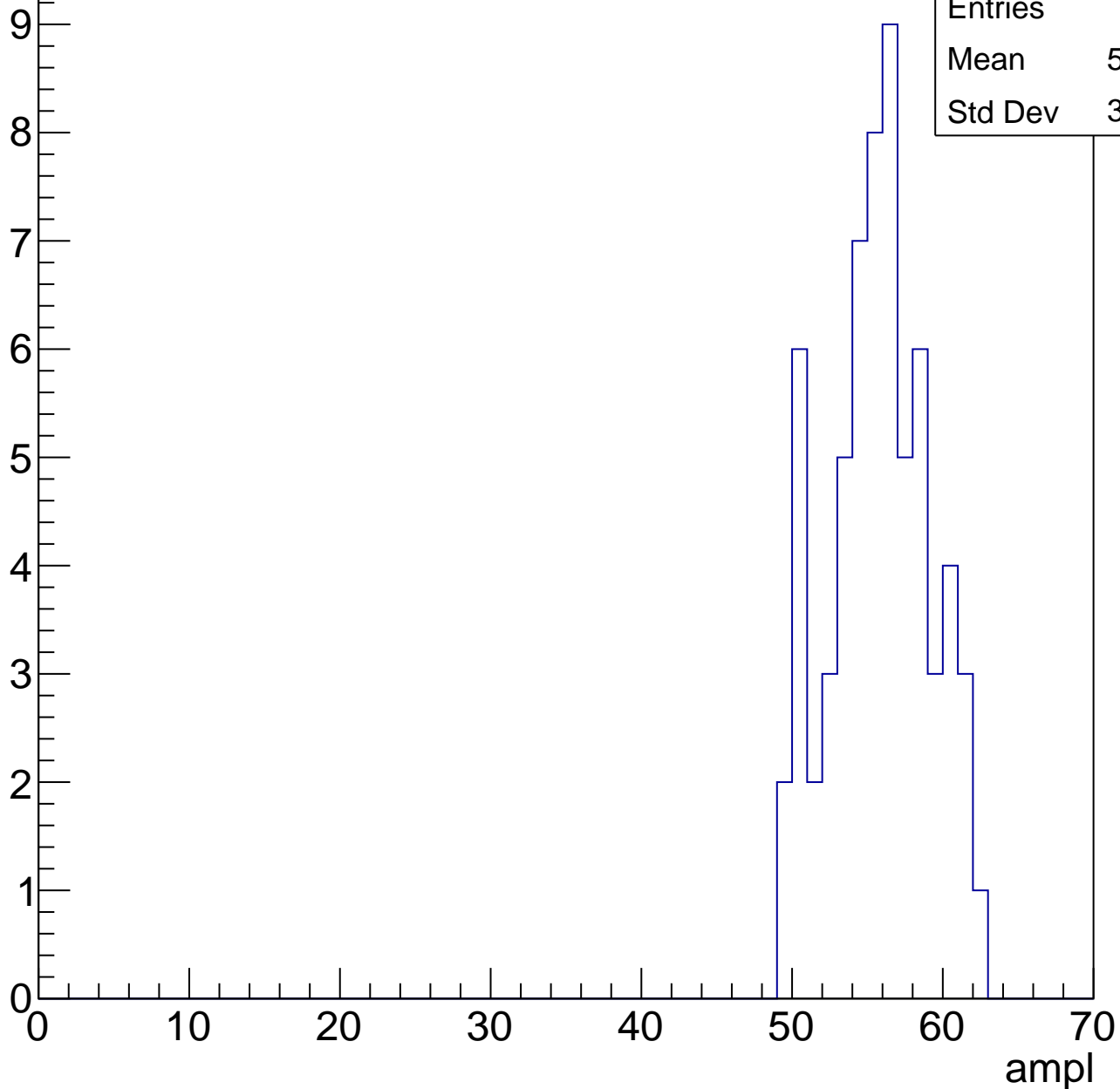
Entries	67
Mean	48.33
Std Dev	3.431



B1L103S, U23-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



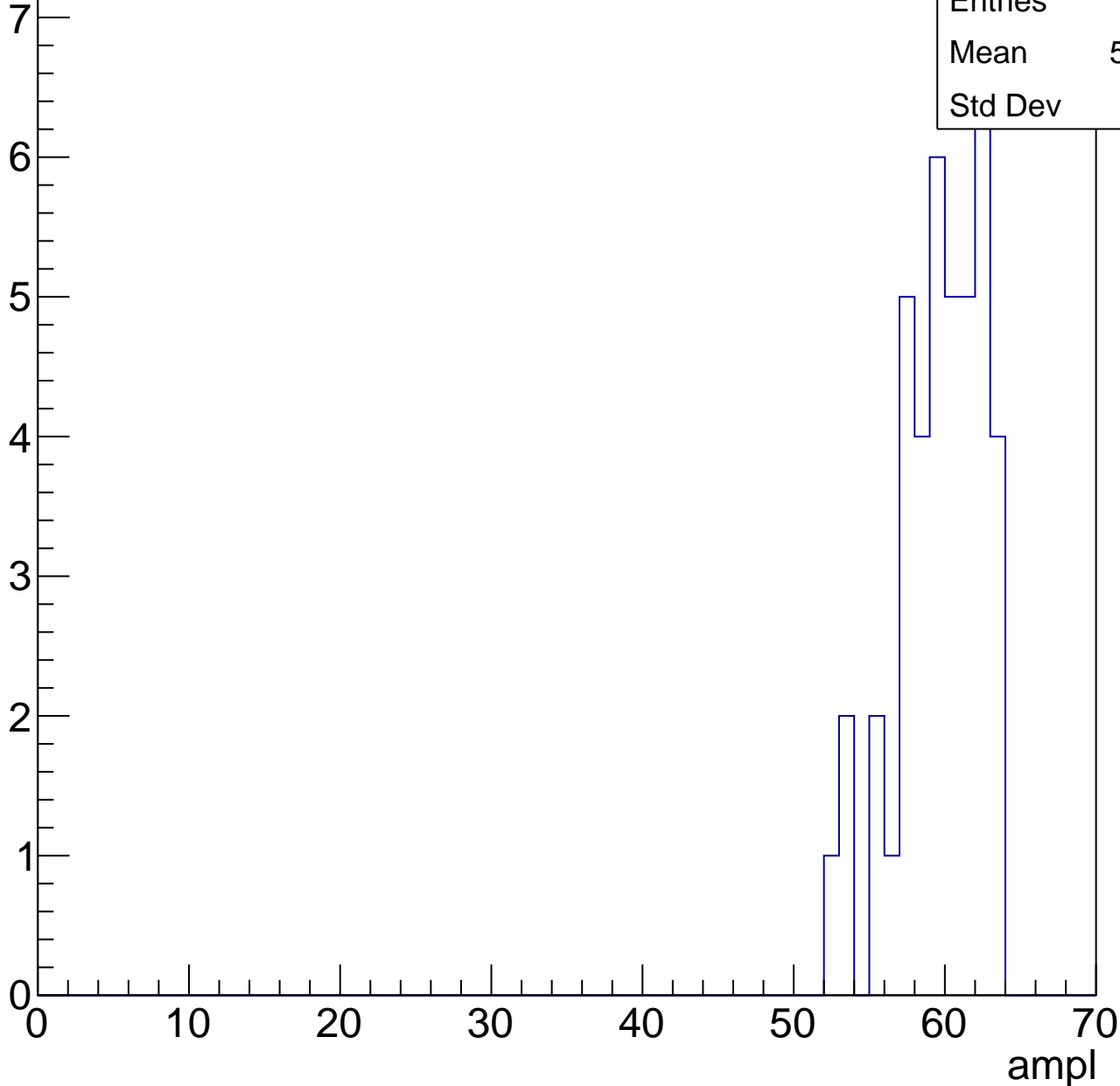
Entries	64
Mean	55.28
Std Dev	3.286

B1L103S, U23-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59.19
Std Dev	2.83

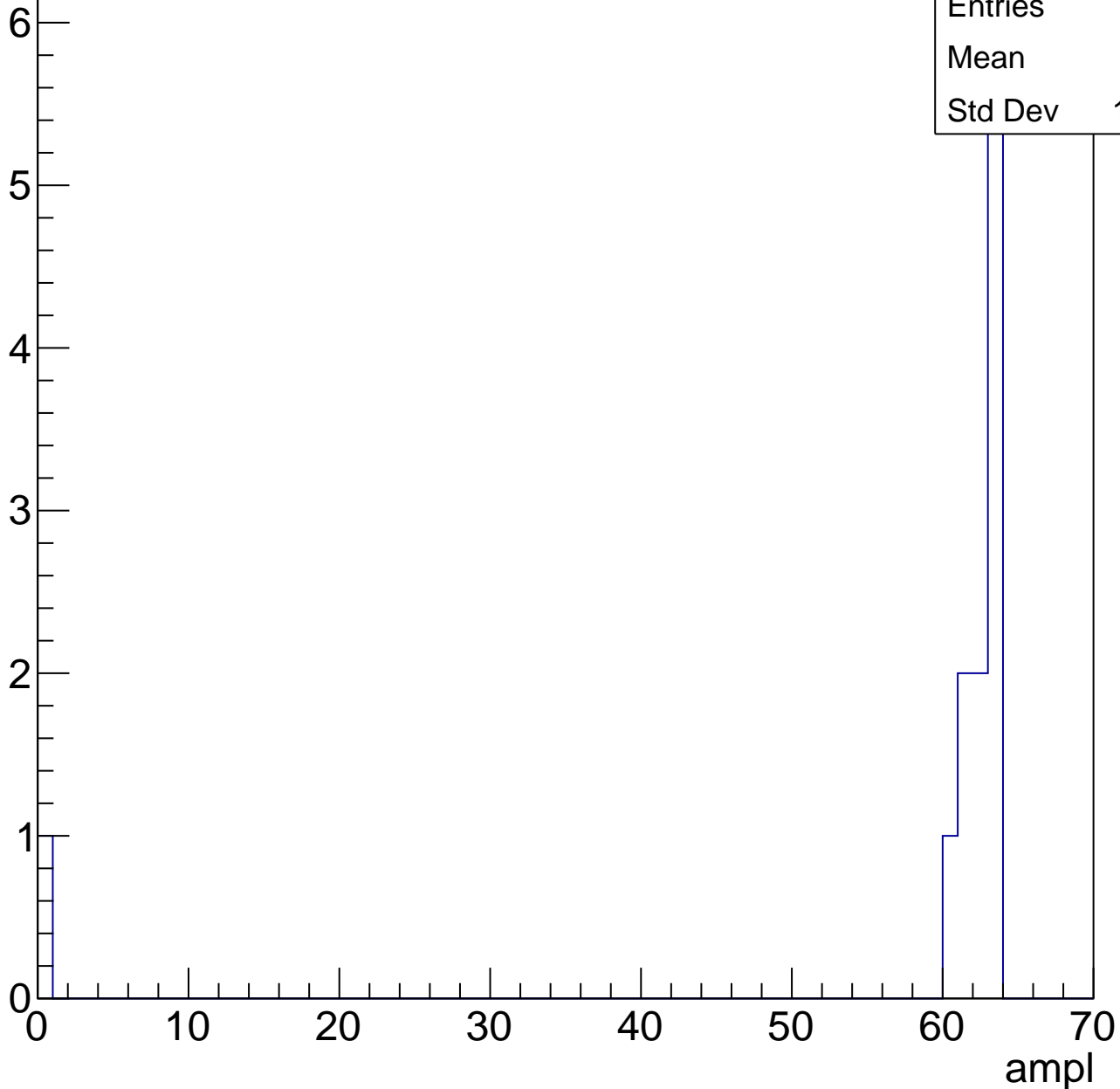


B1L103S, U23-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

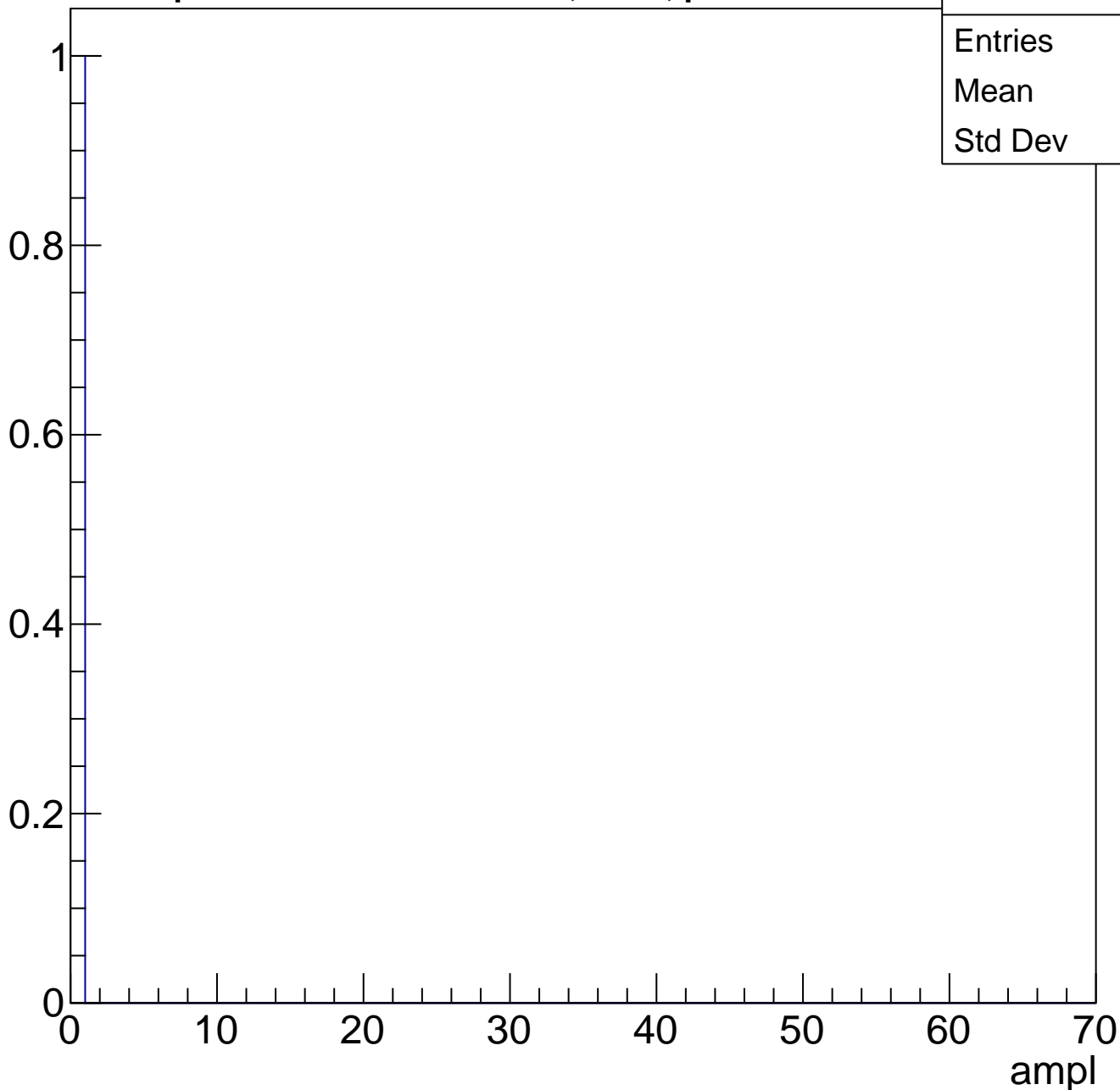
Entries	12
Mean	57
Std Dev	17.21



B1L103S, U23-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch43, adc0

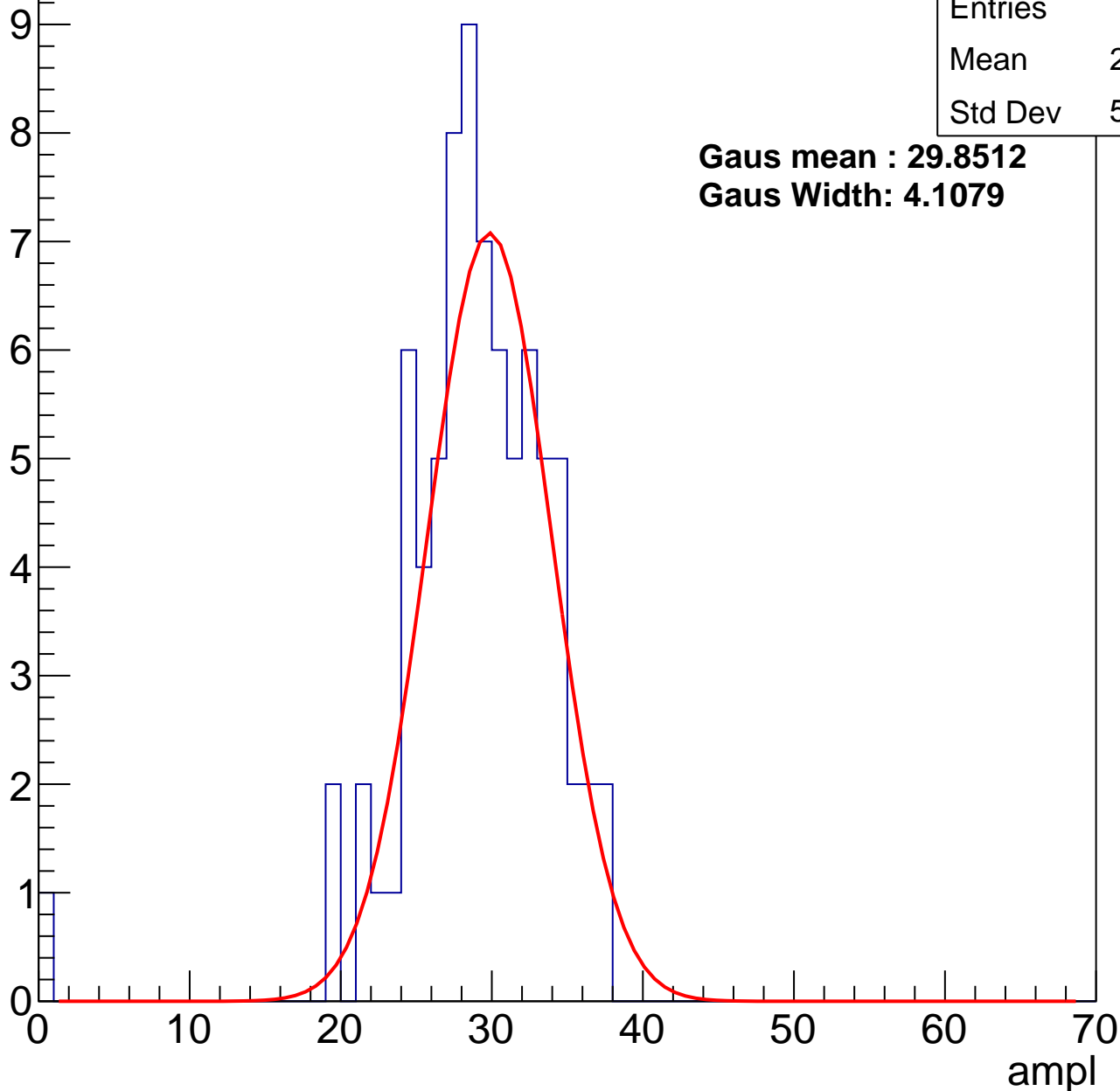
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.46
Std Dev	5.172

Gaus mean : 29.8512

Gaus Width: 4.1079



B1L103S, U23-ch43, adc1

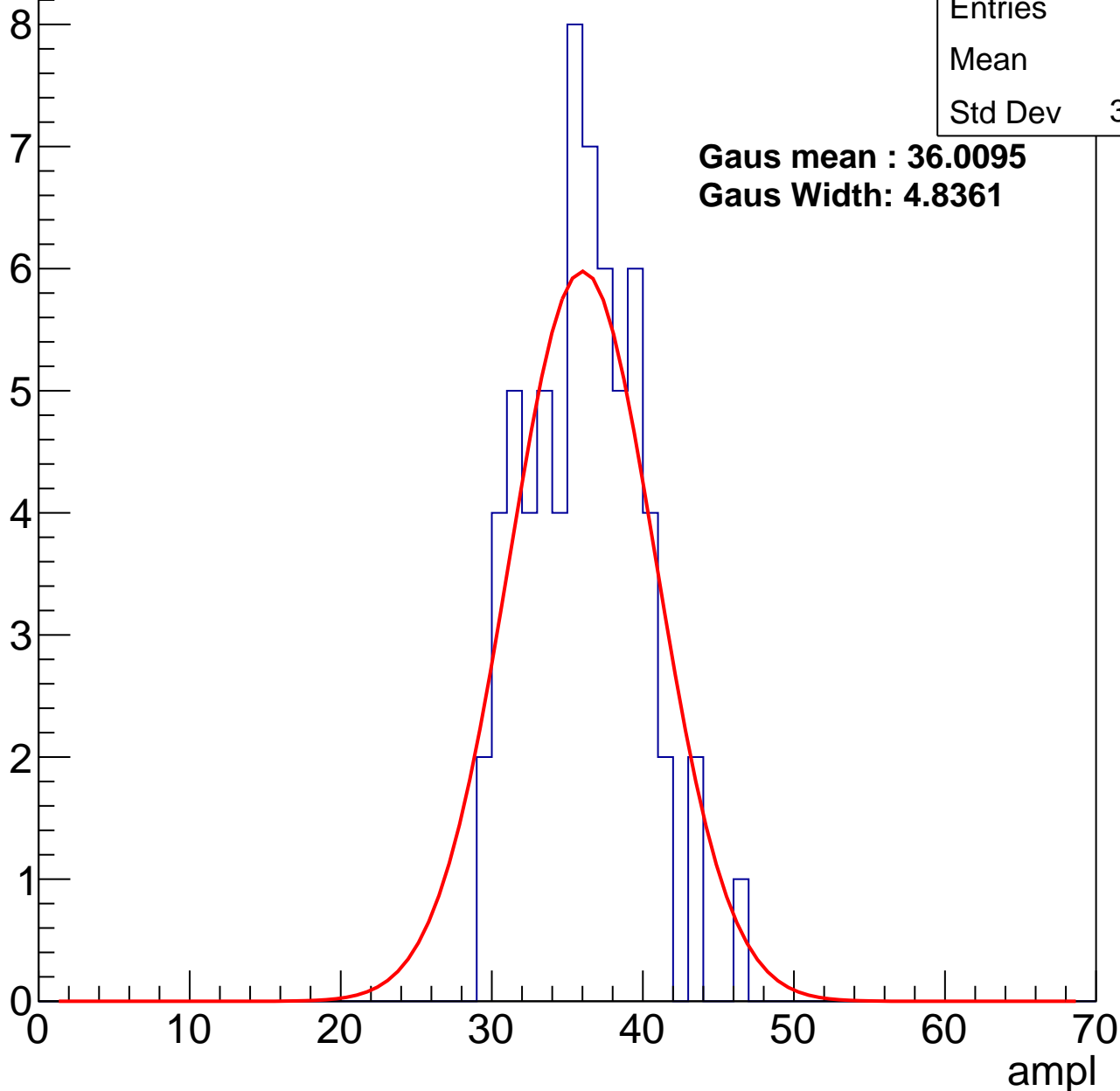
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.6
Std Dev	3.683

Gaus mean : 36.0095

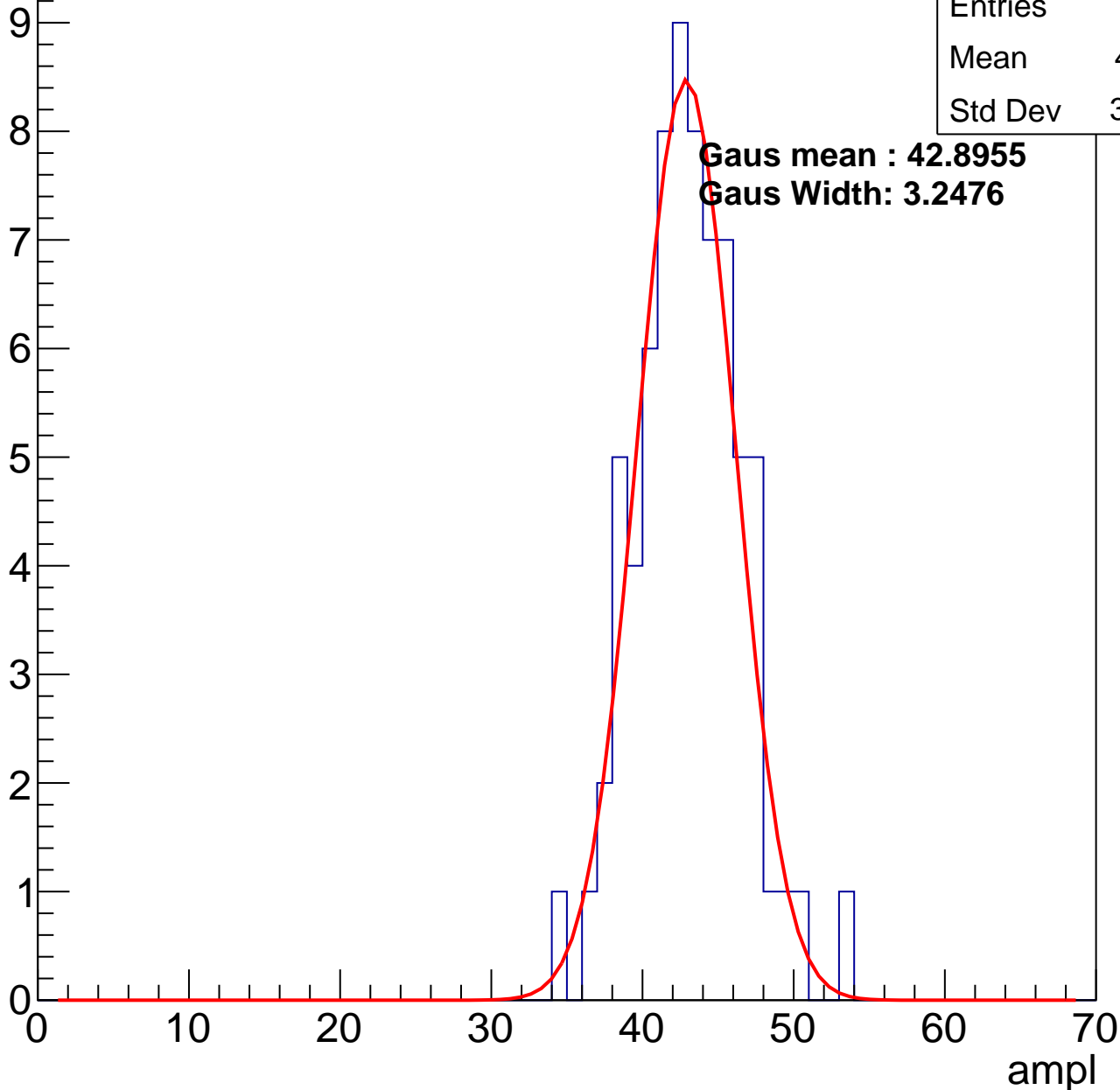
Gaus Width: 4.8361



B1L103S, U23-ch43, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

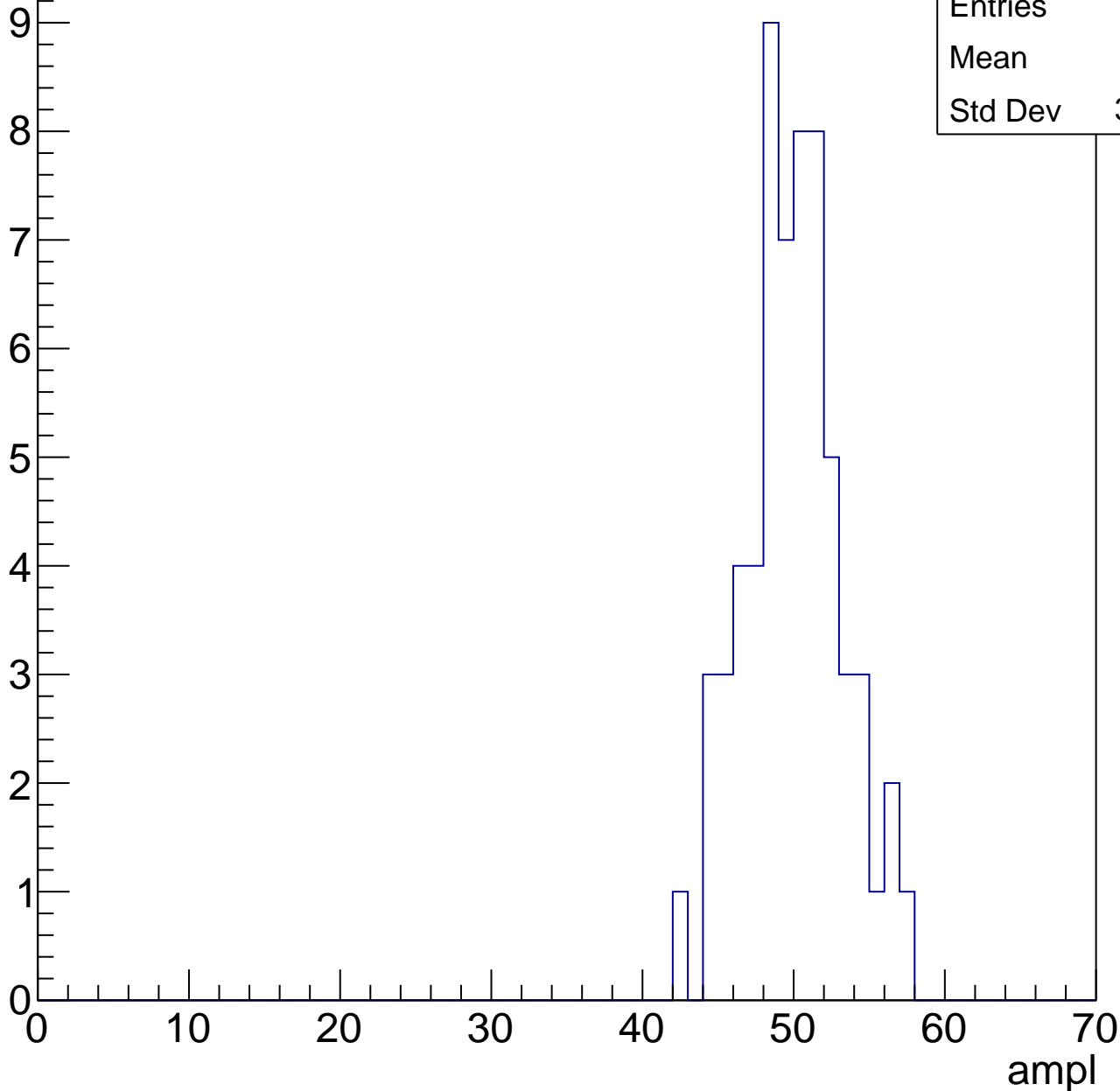


B1L103S, U23-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	49.5
Std Dev	3.171

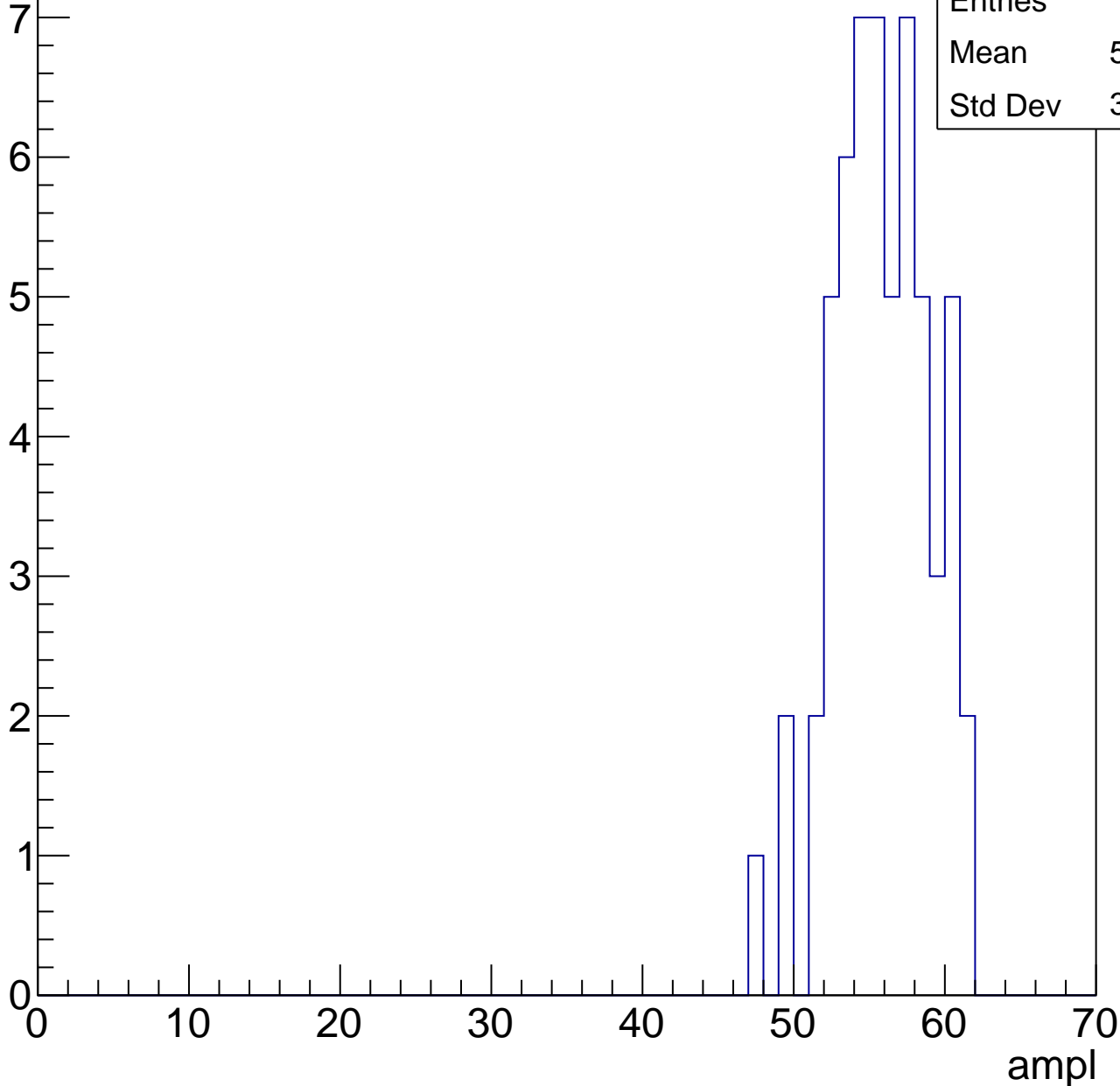


B1L103S, U23-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.37
Std Dev	3.132



B1L103S, U23-ch43, adc5

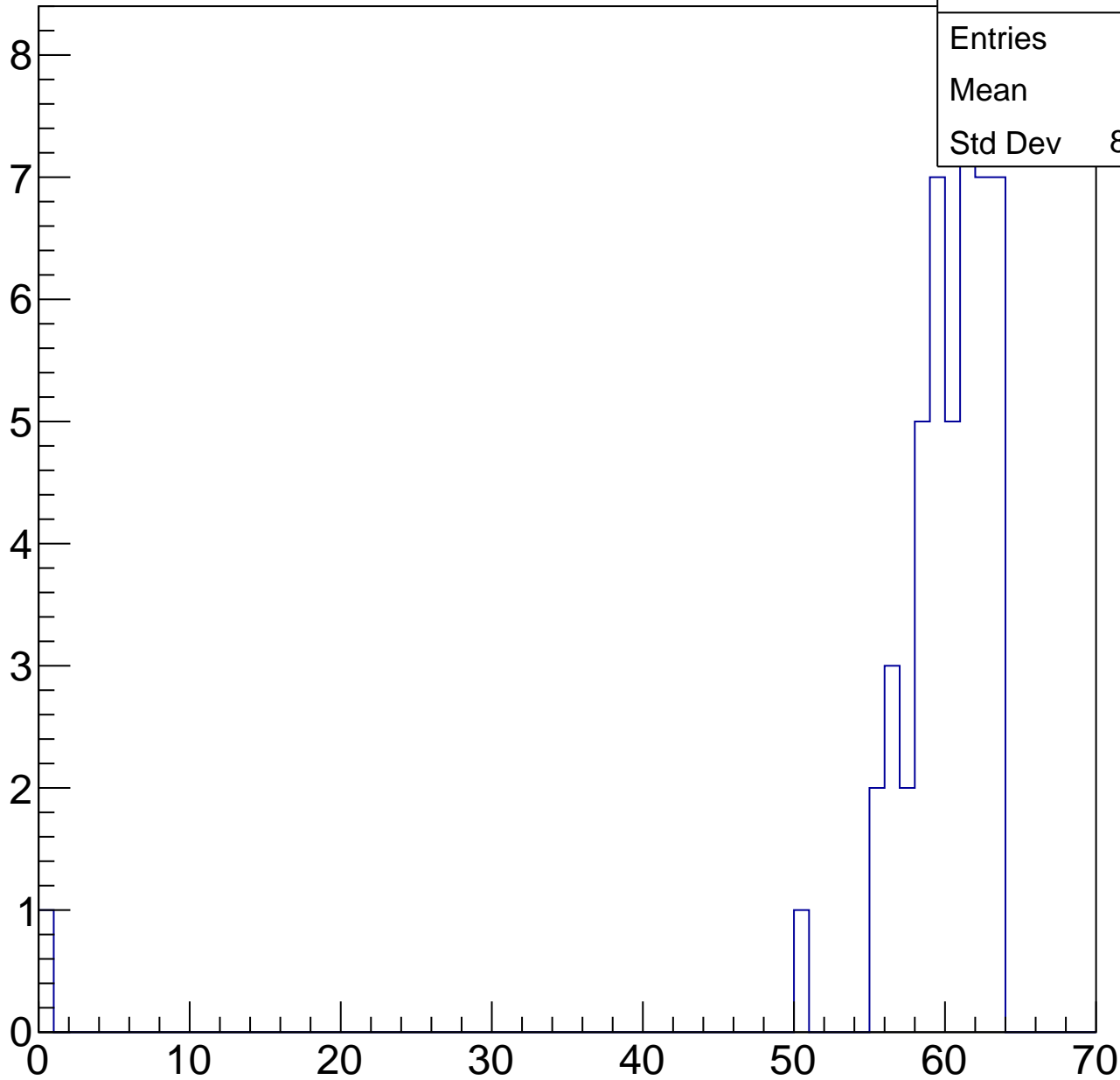
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.5
Std Dev	8.937

ampl



B1L103S, U23-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch44, adc0

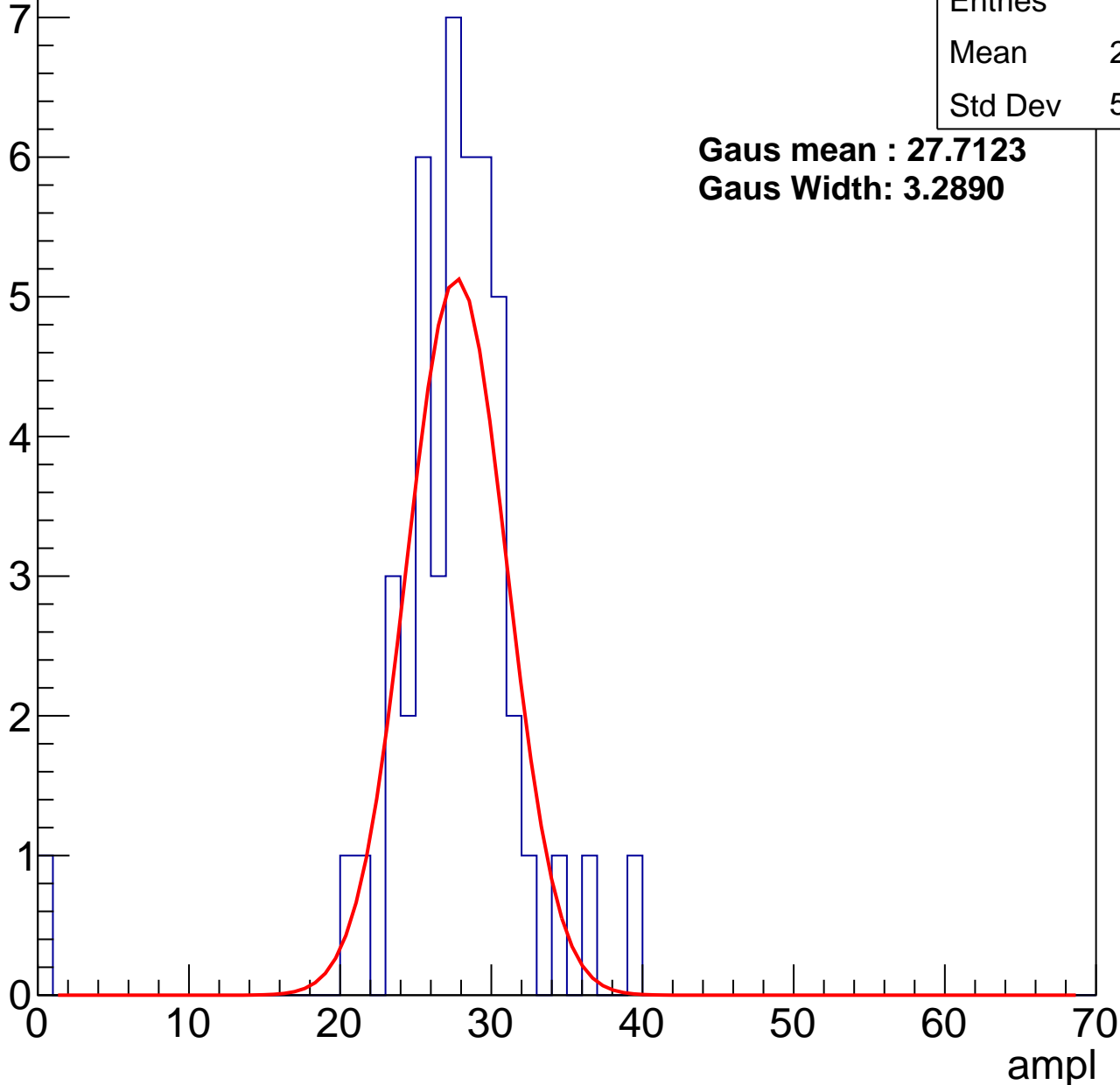
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	27.02
Std Dev	5.285

Gaus mean : 27.7123

Gaus Width: 3.2890



B1L103S, U23-ch44, adc1

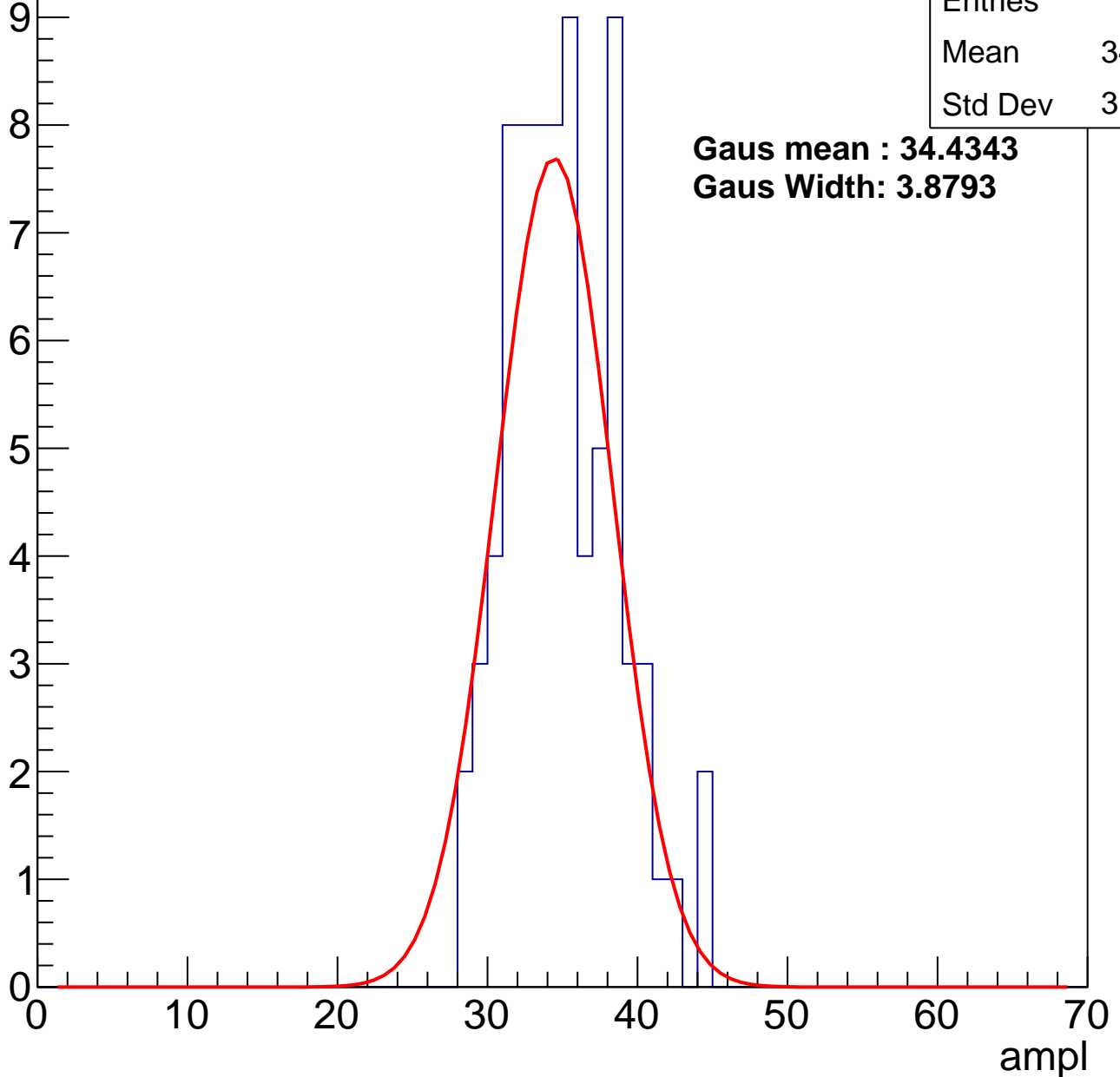
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	34.58
Std Dev	3.593

Gaus mean : 34.4343

Gaus Width: 3.8793



B1L103S, U23-ch44, adc2

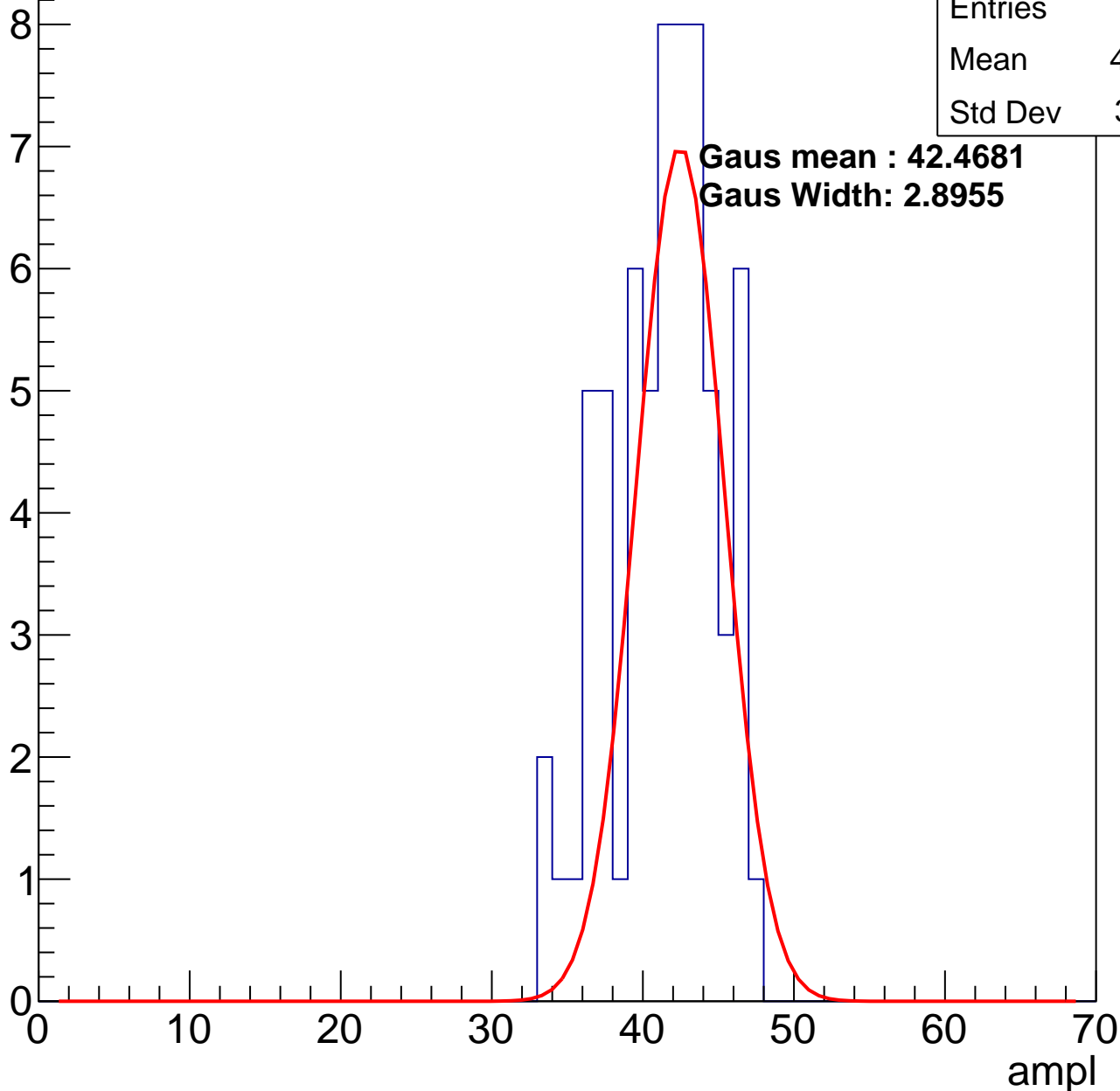
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	40.89
Std Dev	3.451

Gaus mean : 42.4681

Gaus Width: 2.8955



B1L103S, U23-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

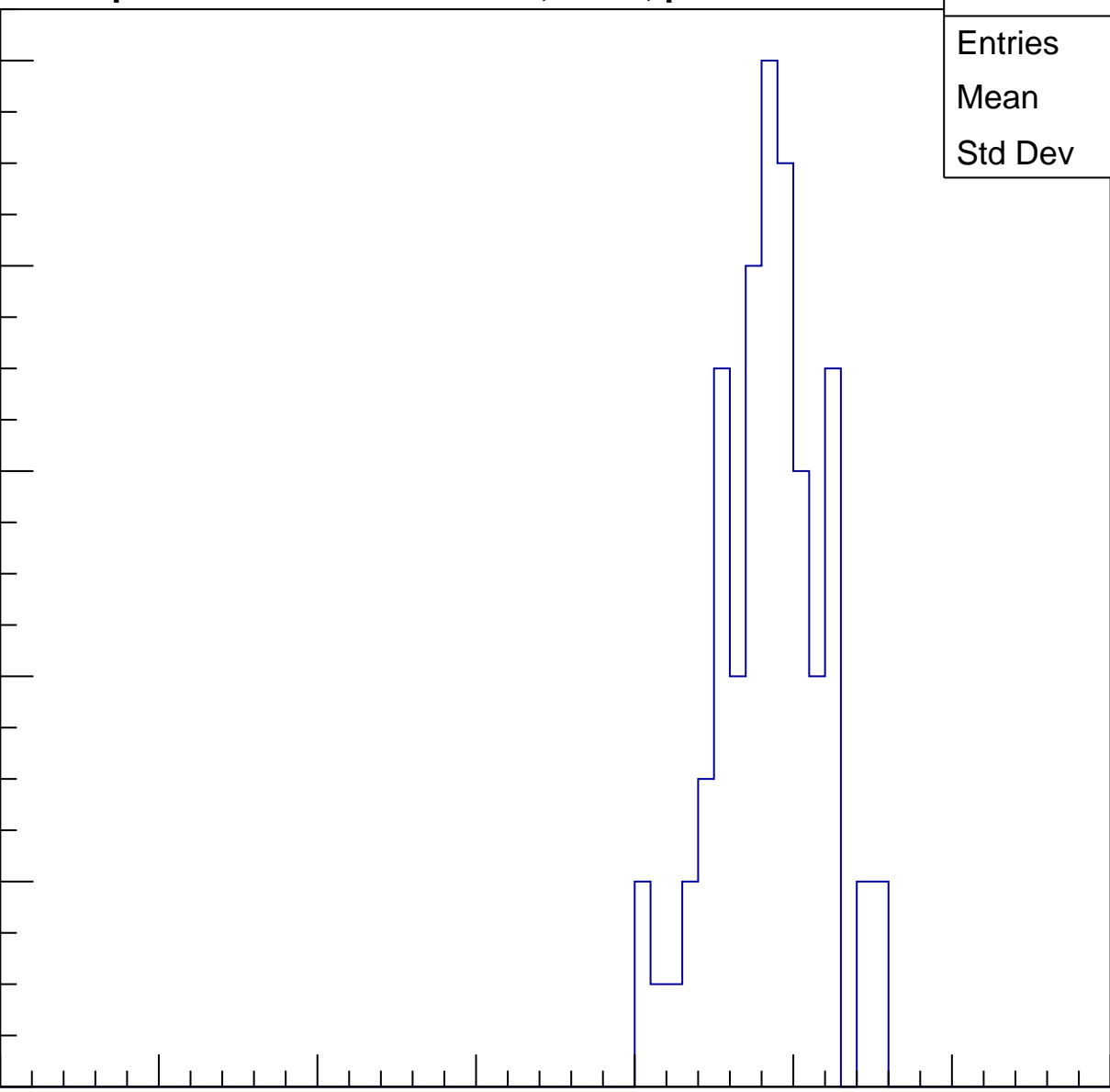
Entries	68
Mean	47.99
Std Dev	3.319

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

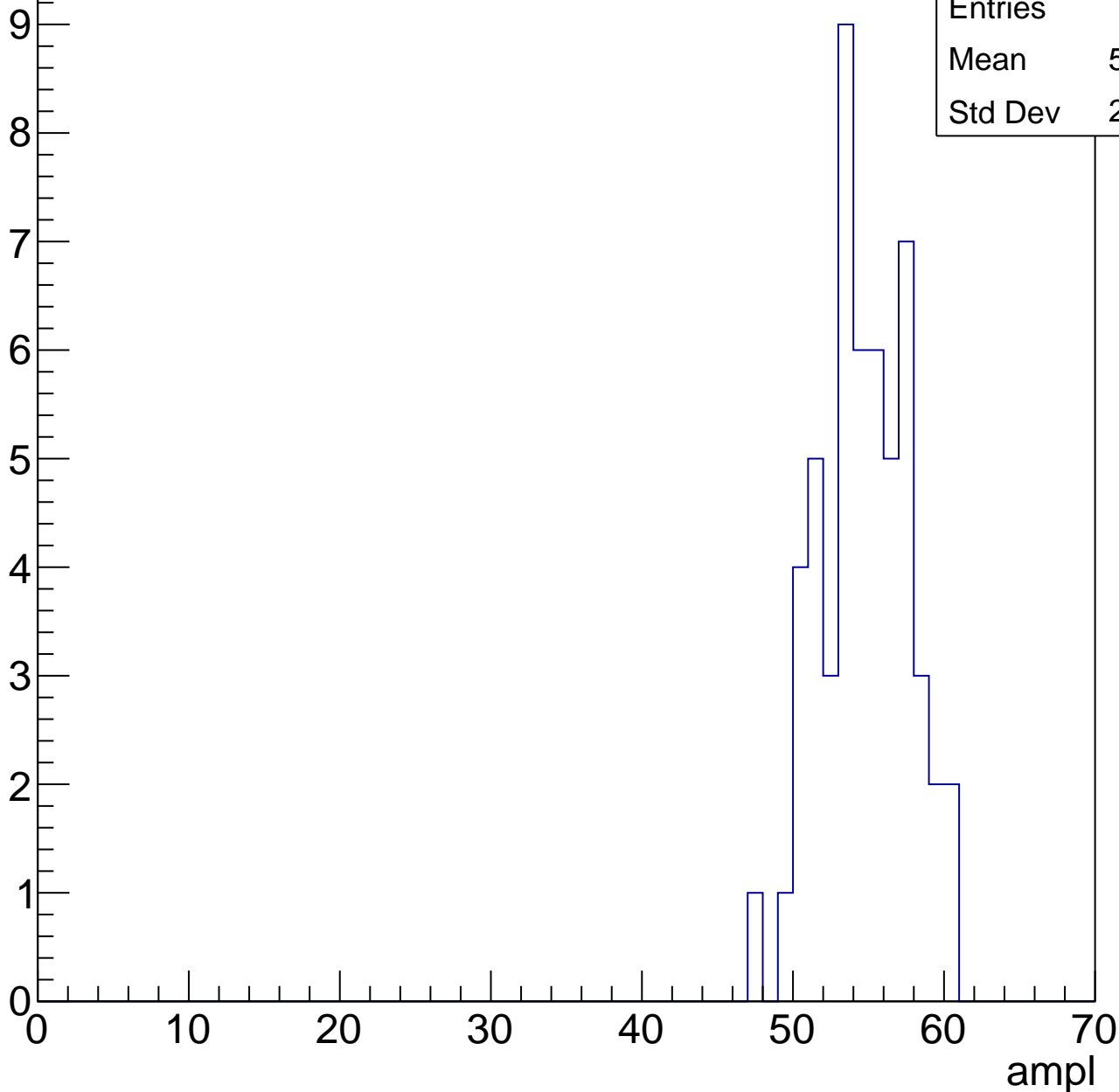


B1L103S, U23-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	54.24
Std Dev	2.918



B1L103S, U23-ch44, adc5

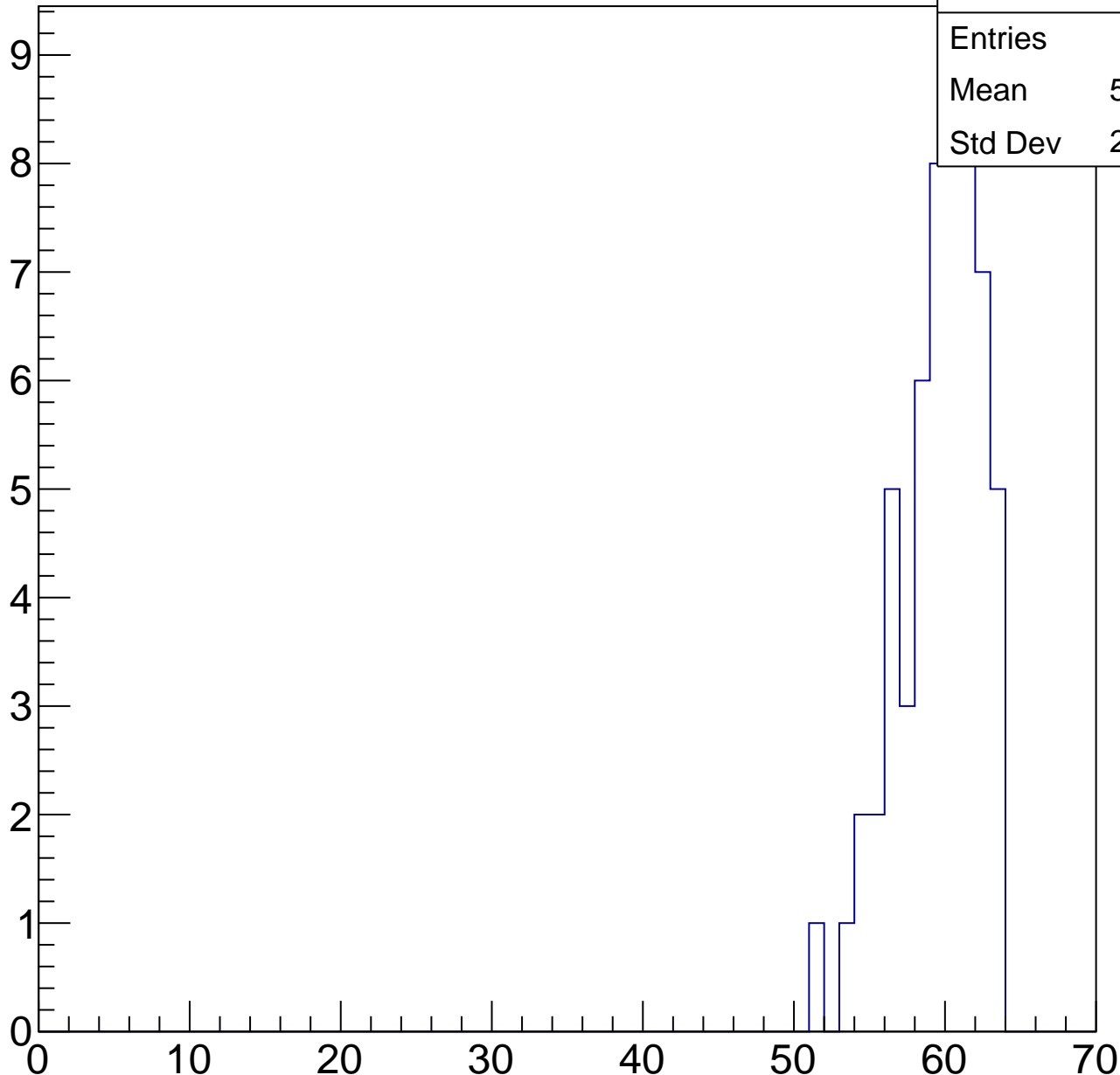
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	59.14
Std Dev	2.749

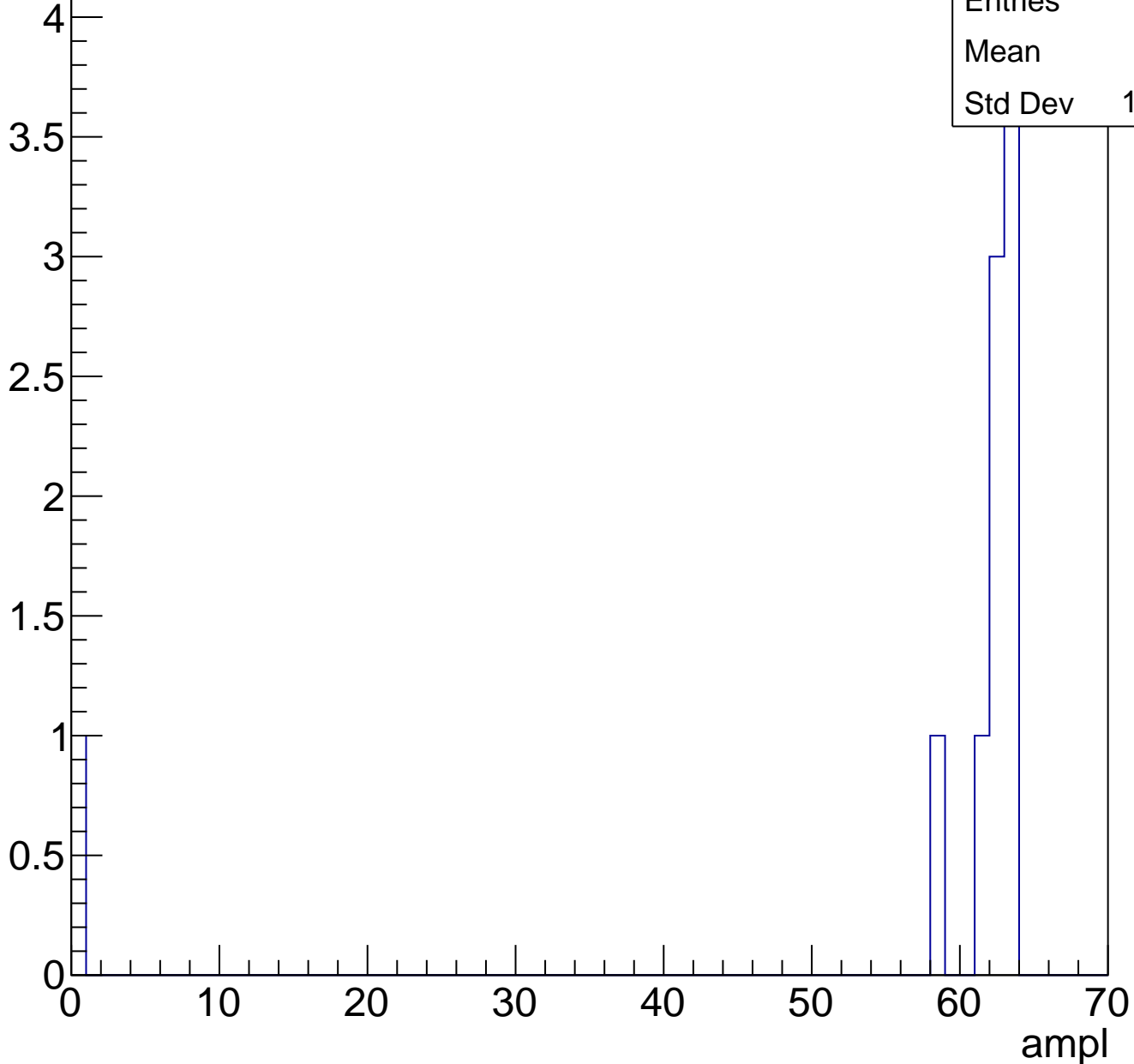
ampl



B1L103S, U23-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch45, adc0

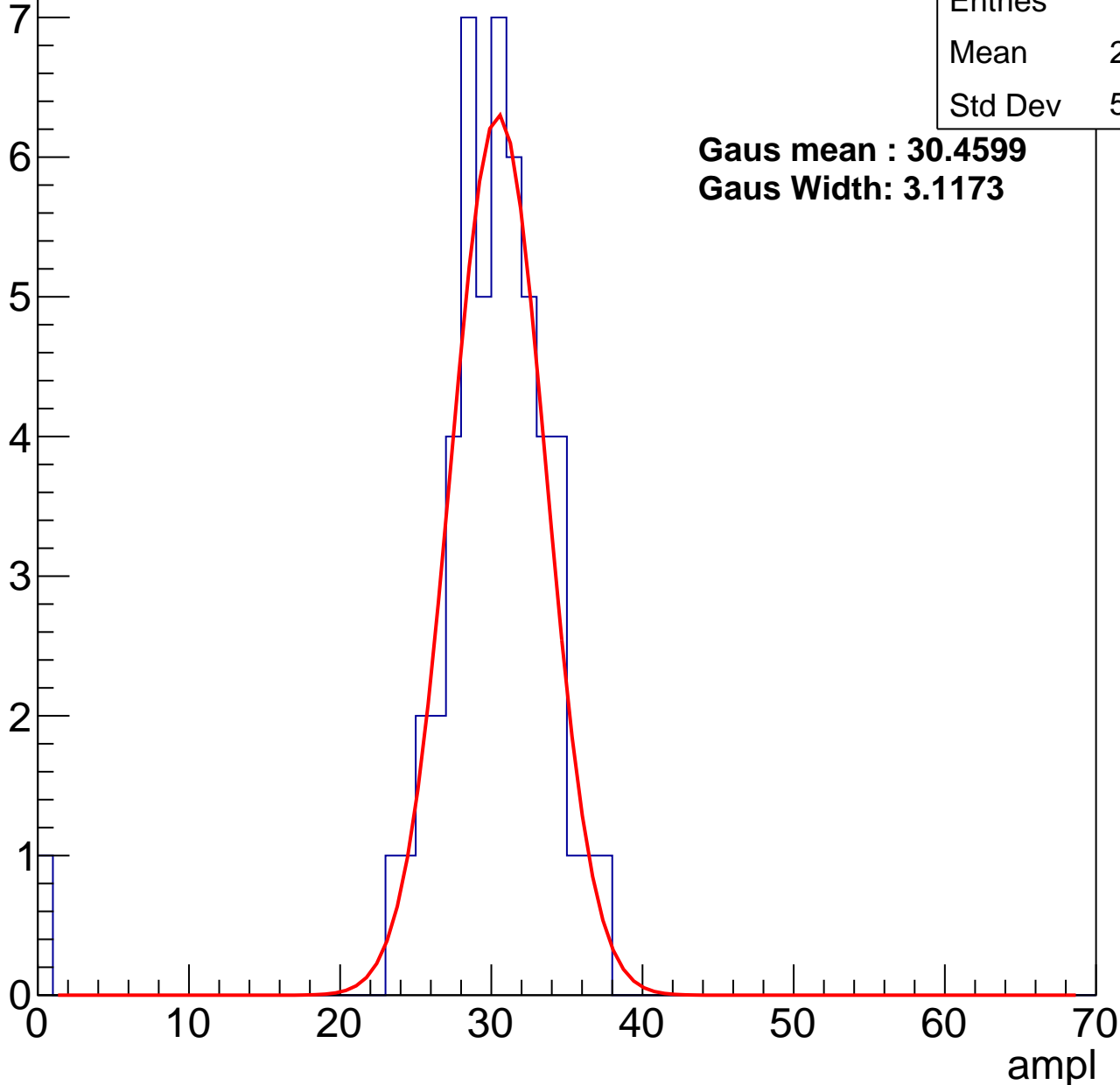
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	29.42
Std Dev	5.104

Gaus mean : 30.4599

Gaus Width: 3.1173



B1L103S, U23-ch45, adc1

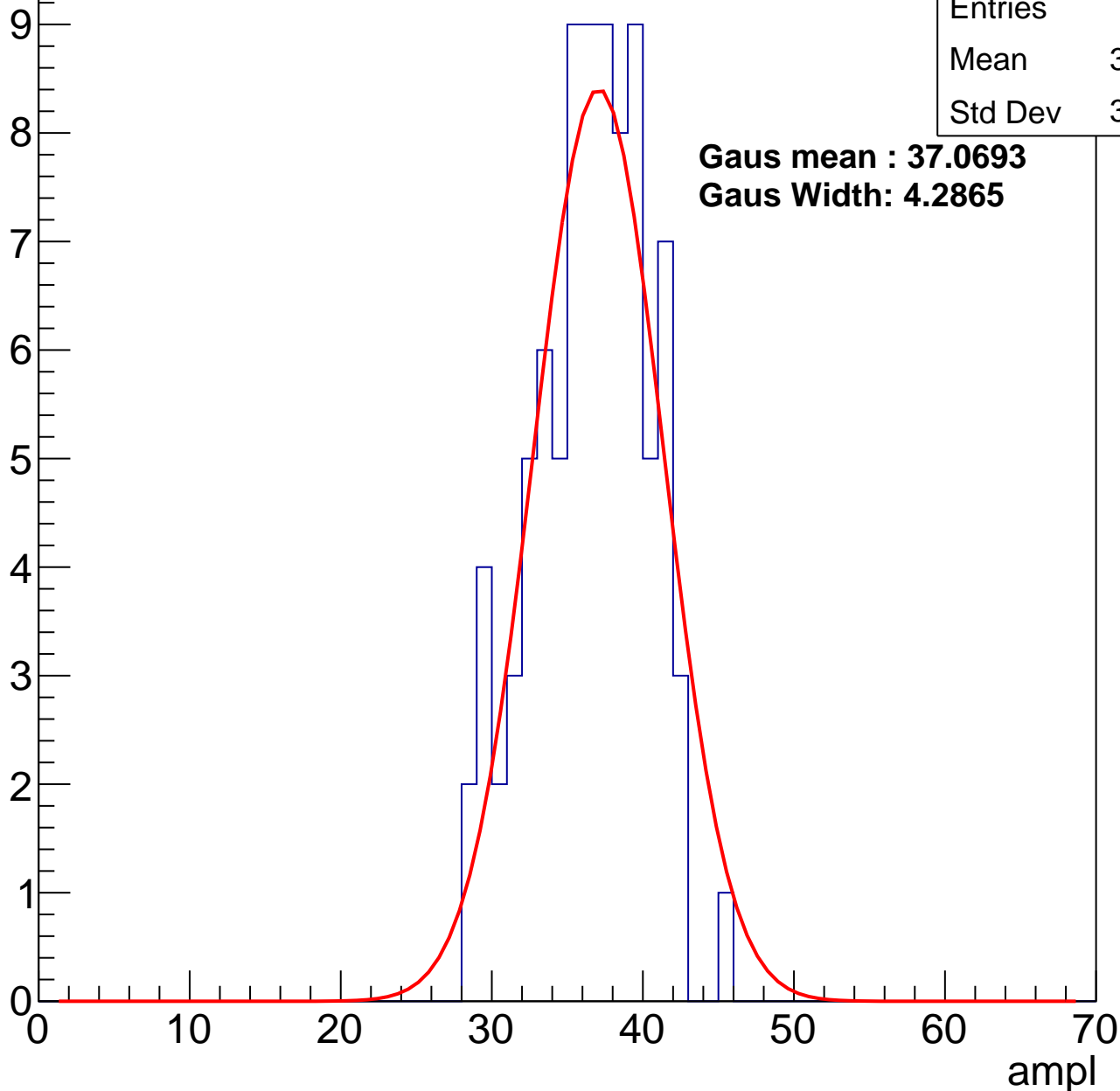
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	36.07
Std Dev	3.713

Gaus mean : 37.0693

Gaus Width: 4.2865



B1L103S, U23-ch45, adc2

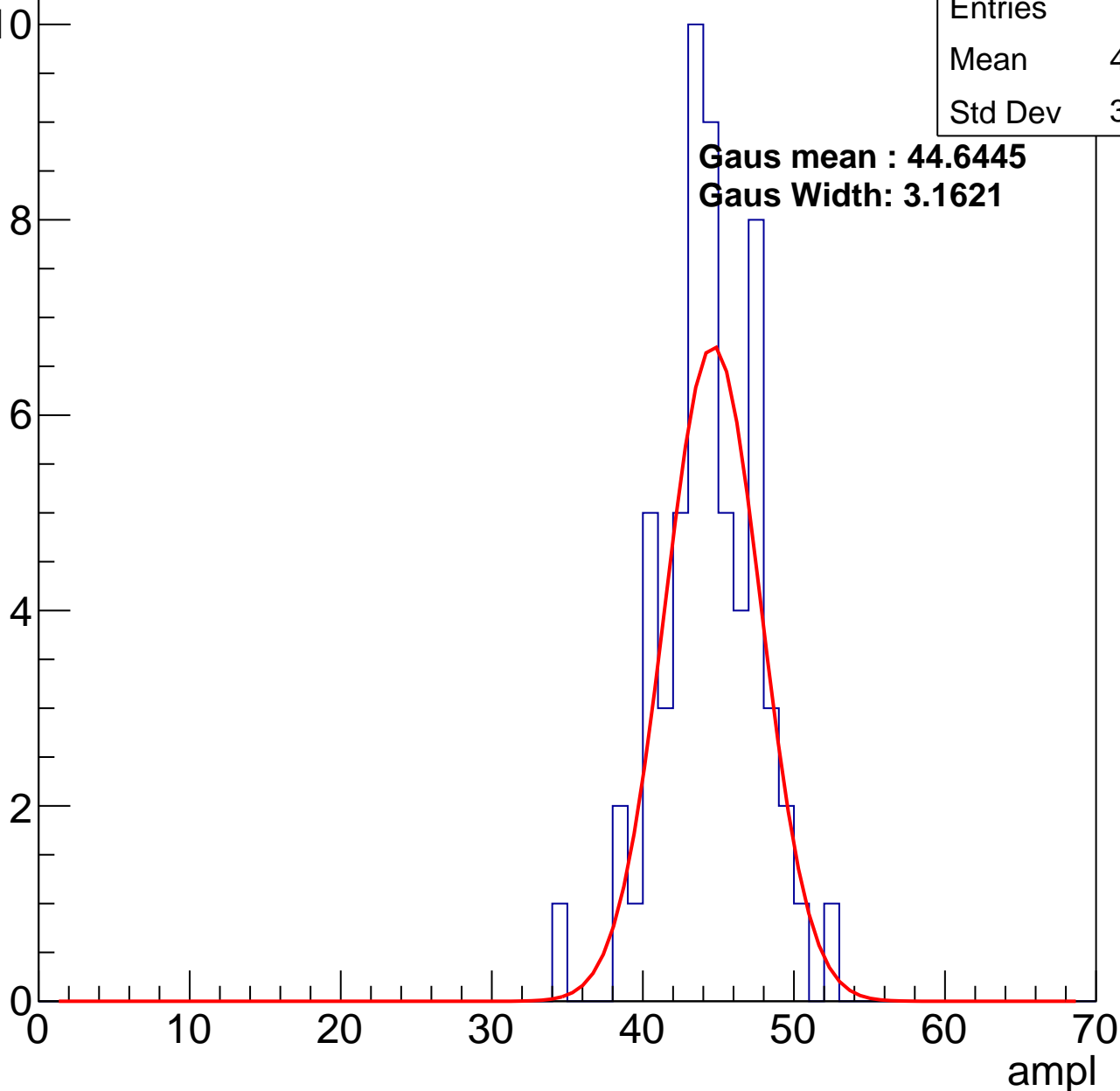
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.95
Std Dev	3.227

Gaus mean : 44.6445

Gaus Width: 3.1621

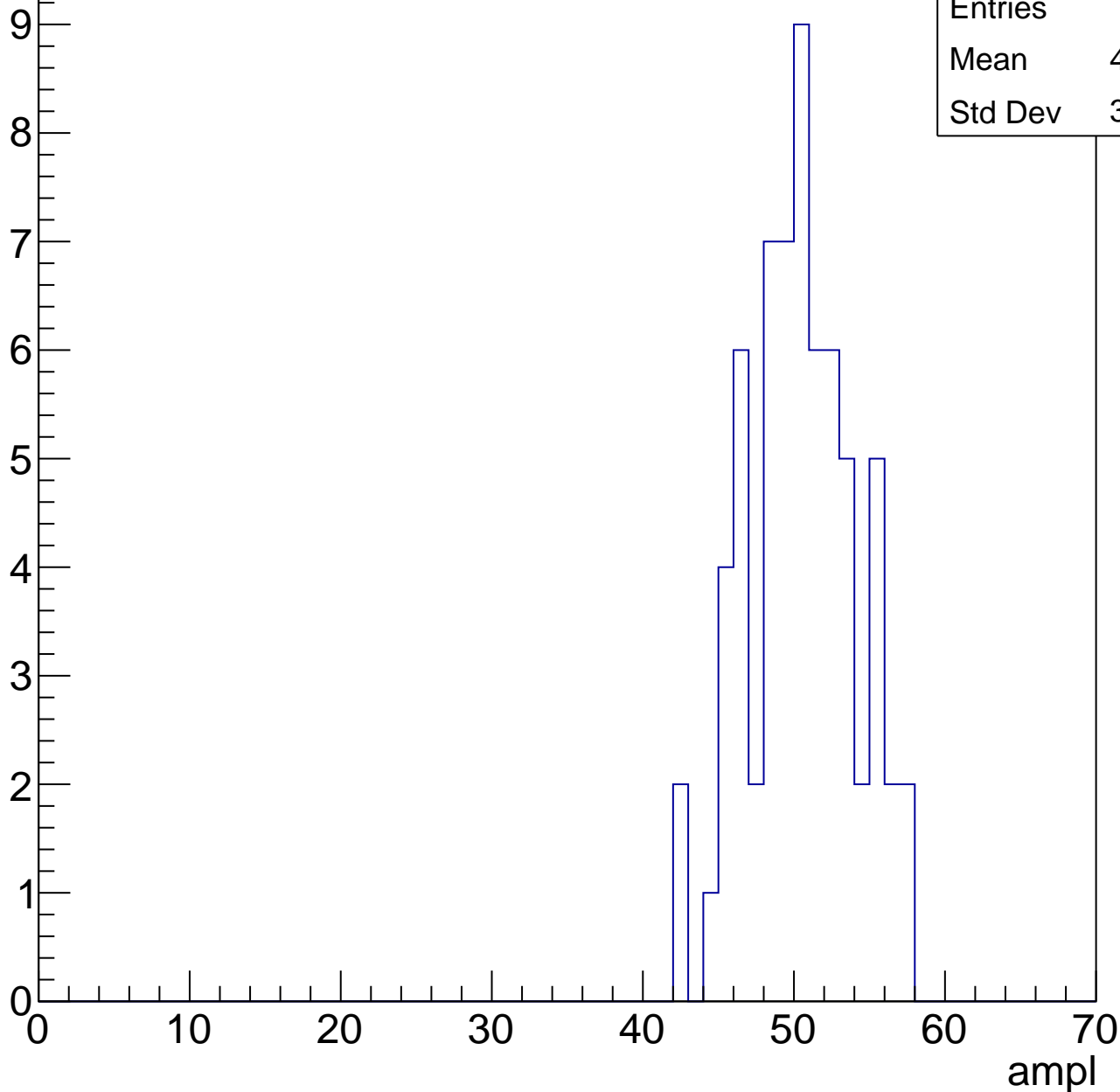


B1L103S, U23-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

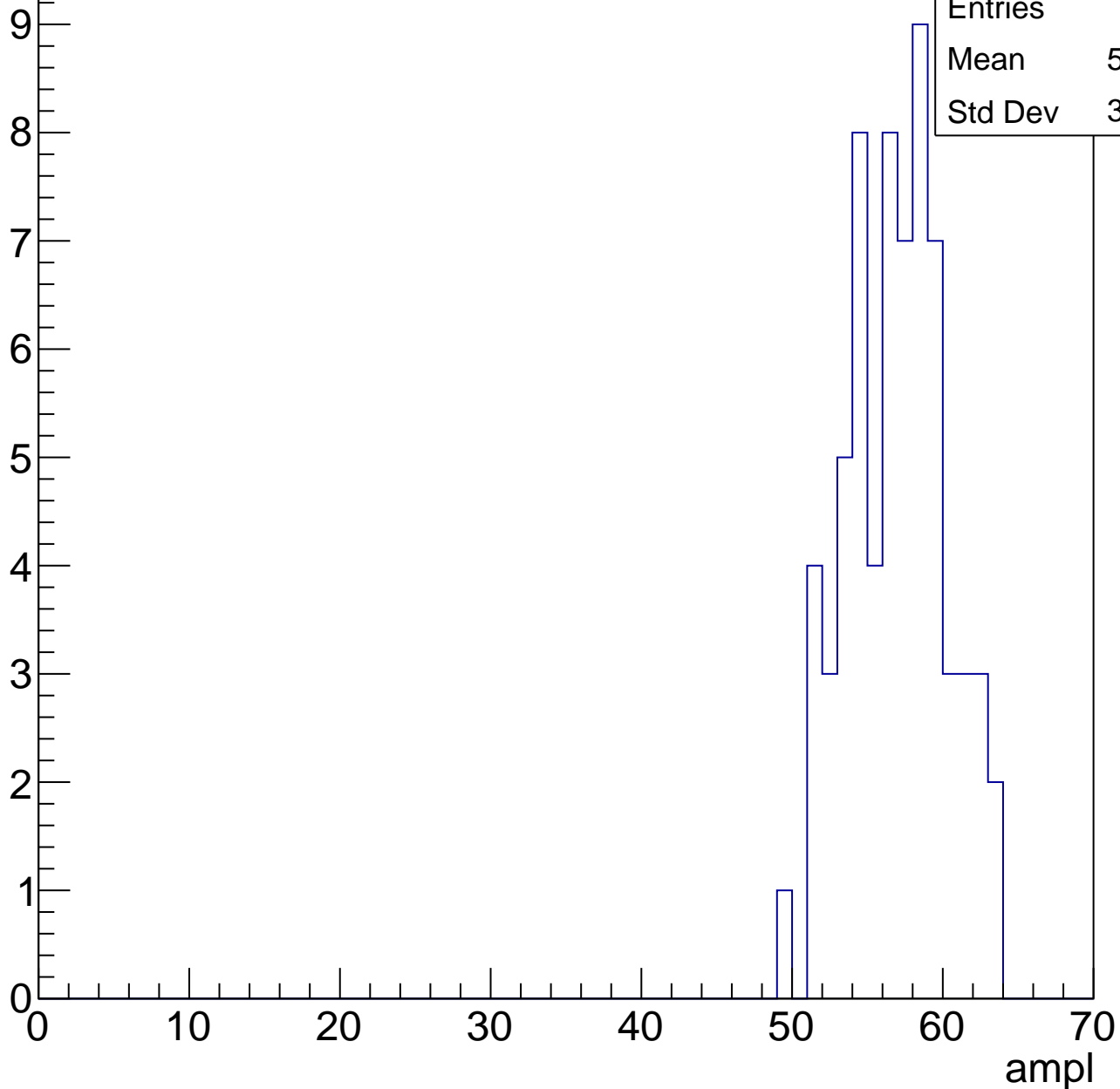
Entries	66
Mean	49.98
Std Dev	3.514



B1L103S, U23-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

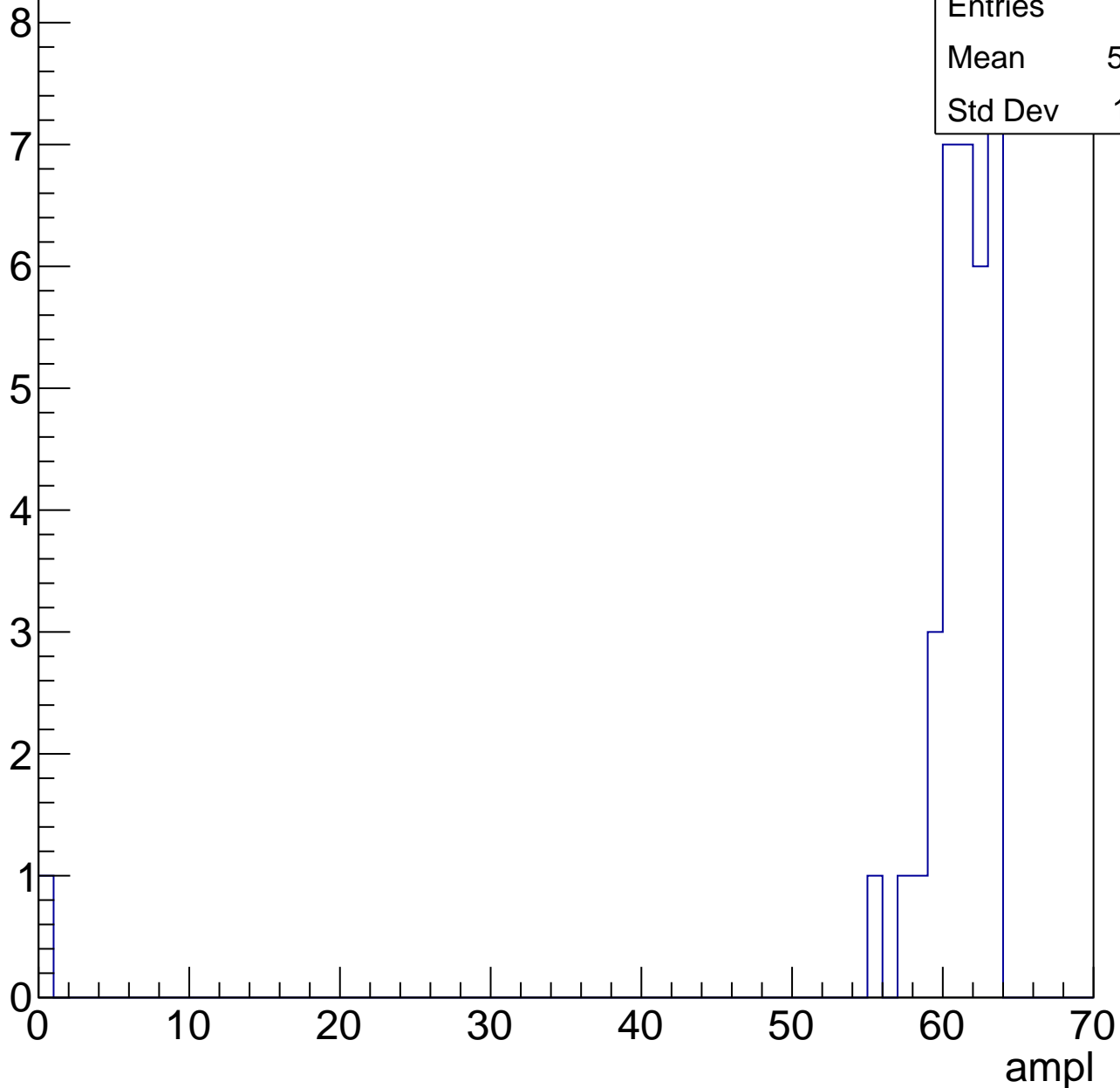


B1L103S, U23-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	59.14
Std Dev	10.31



B1L103S, U23-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch46, adc0

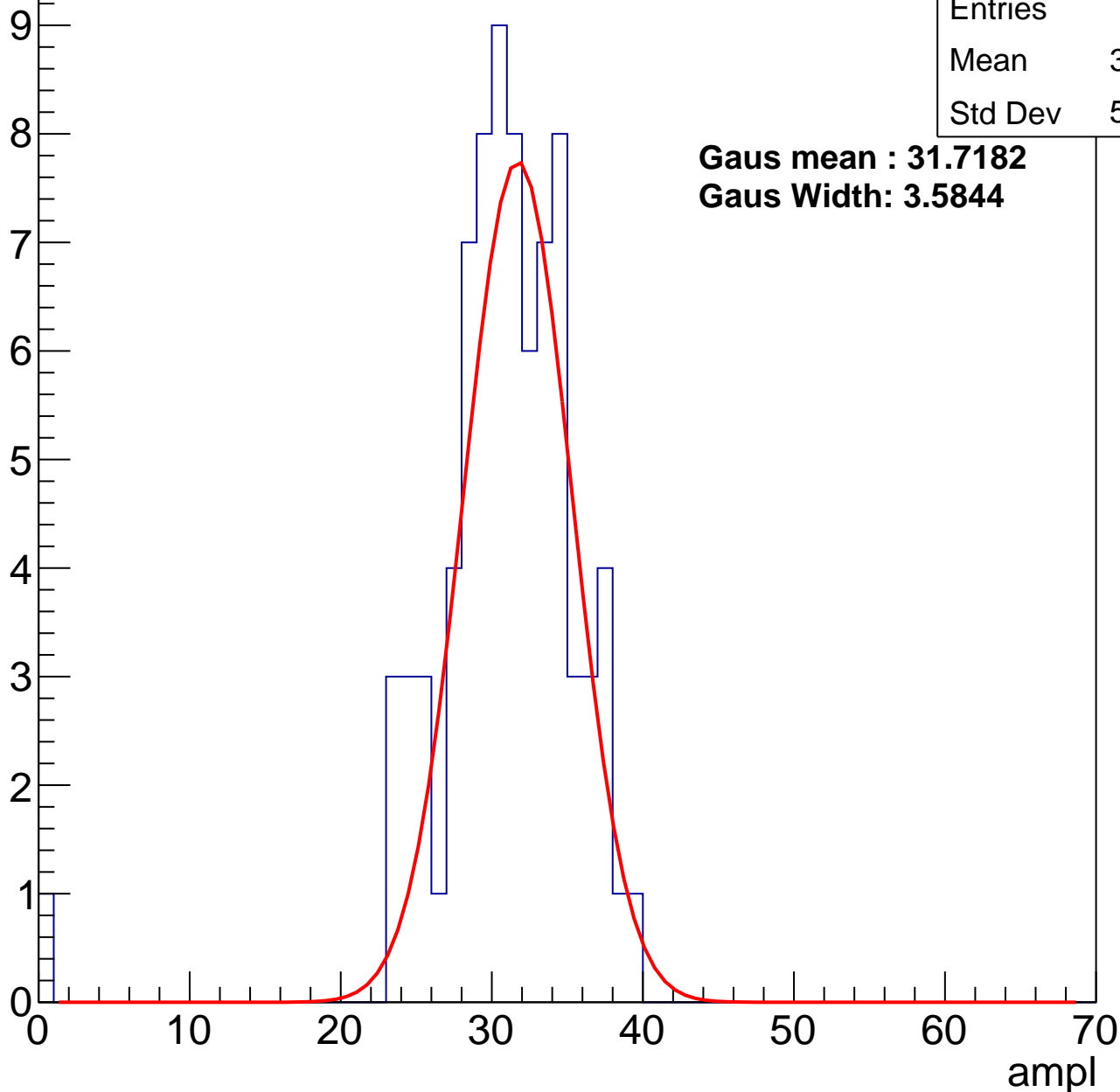
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	30.36
Std Dev	5.068

Gaus mean : 31.7182

Gaus Width: 3.5844



B1L103S, U23-ch46, adc1

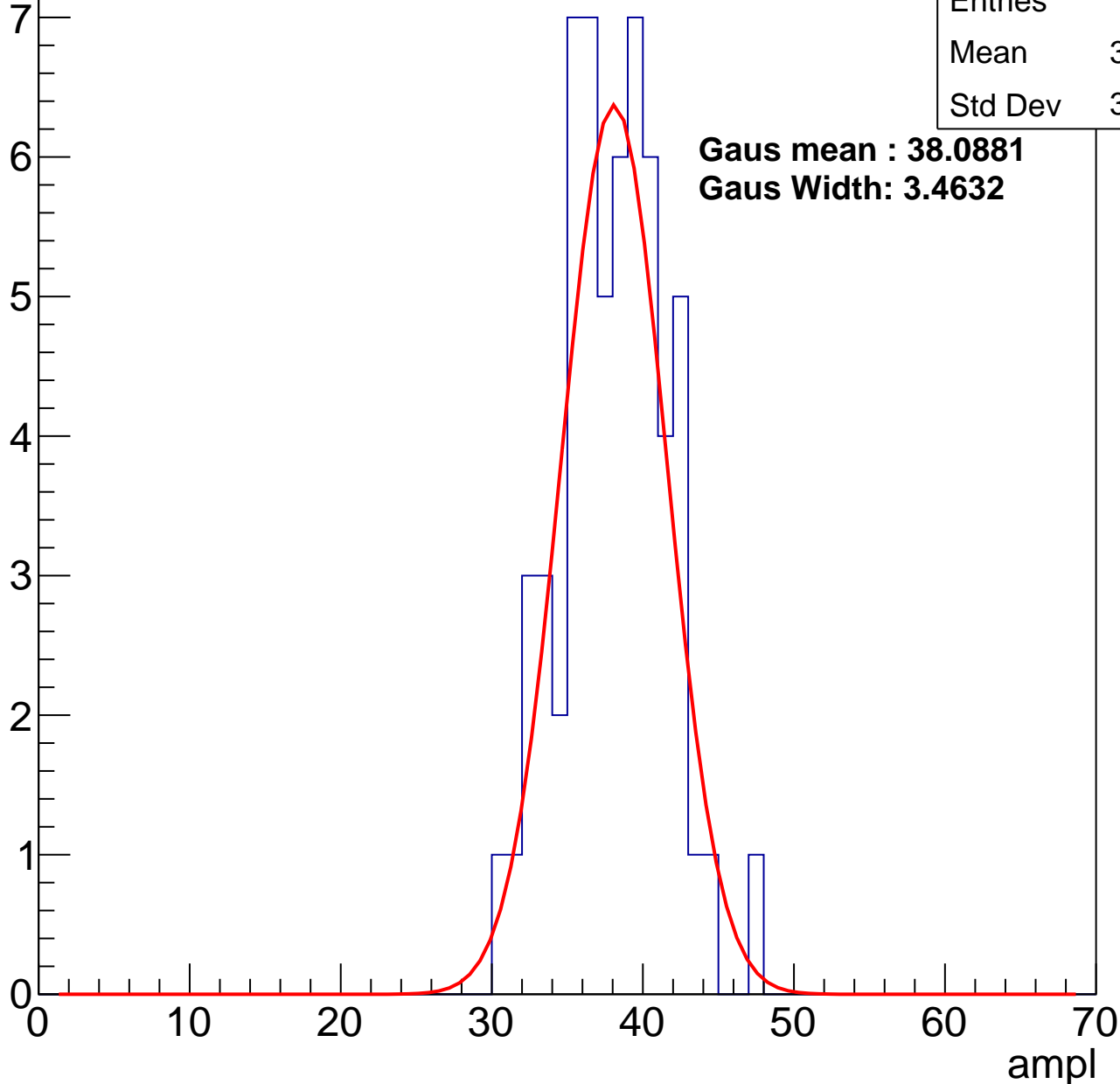
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	37.58
Std Dev	3.422

Gaus mean : 38.0881

Gaus Width: 3.4632



B1L103S, U23-ch46, adc2

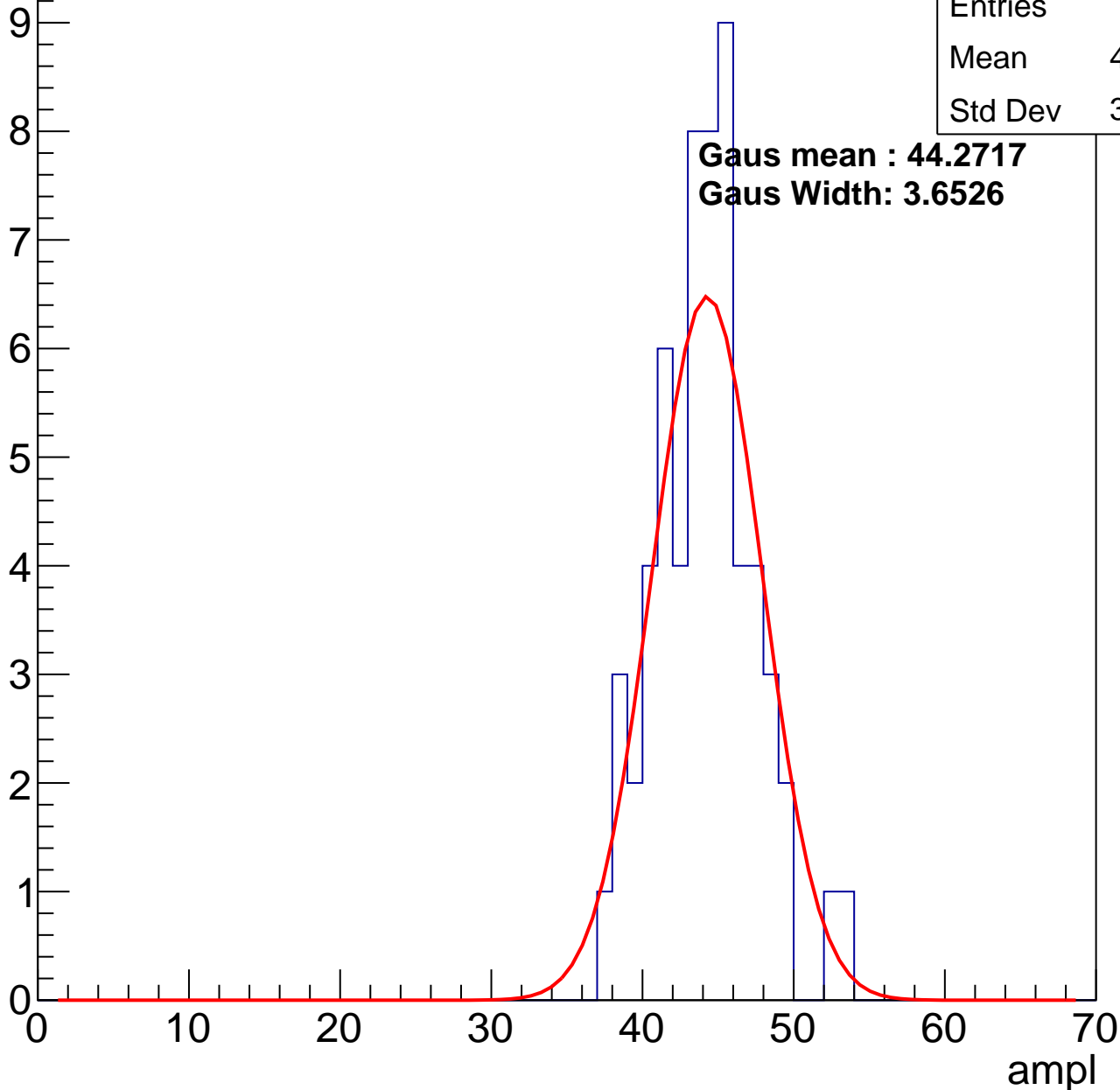
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.72
Std Dev	3.287

Gaus mean : 44.2717

Gaus Width: 3.6526

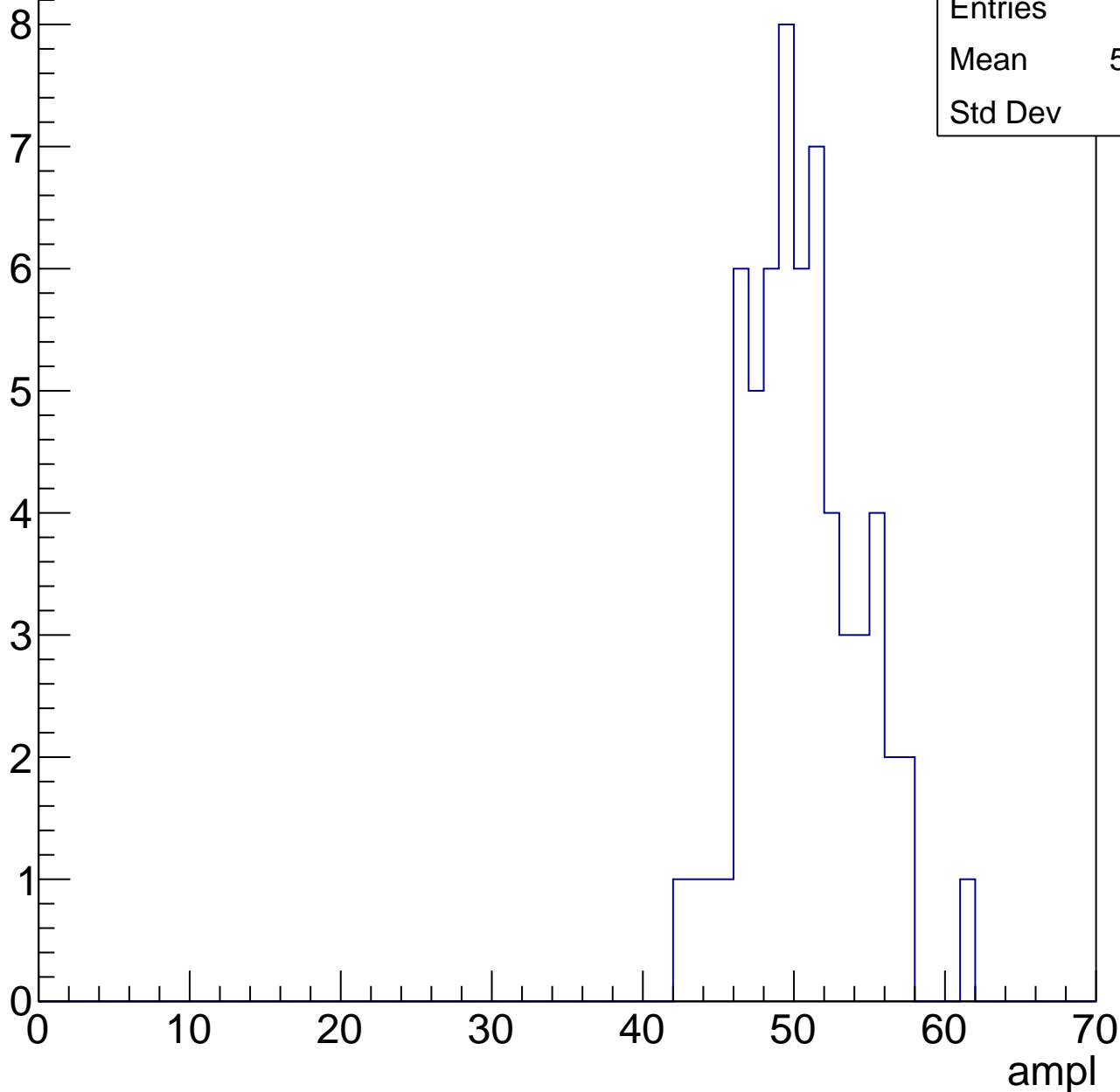


B1L103S, U23-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.13
Std Dev	3.7

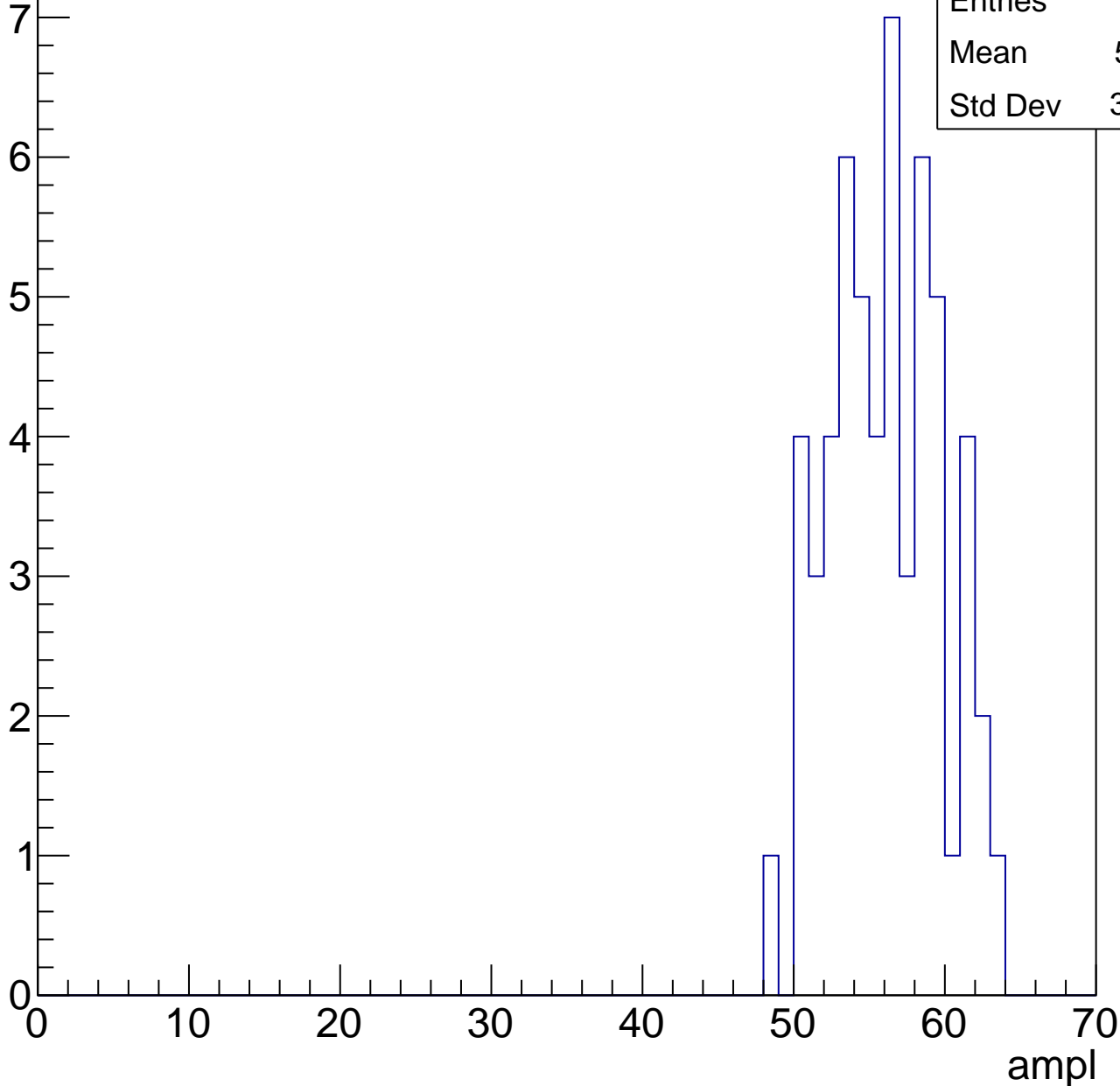


B1L103S, U23-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	55.61
Std Dev	3.599

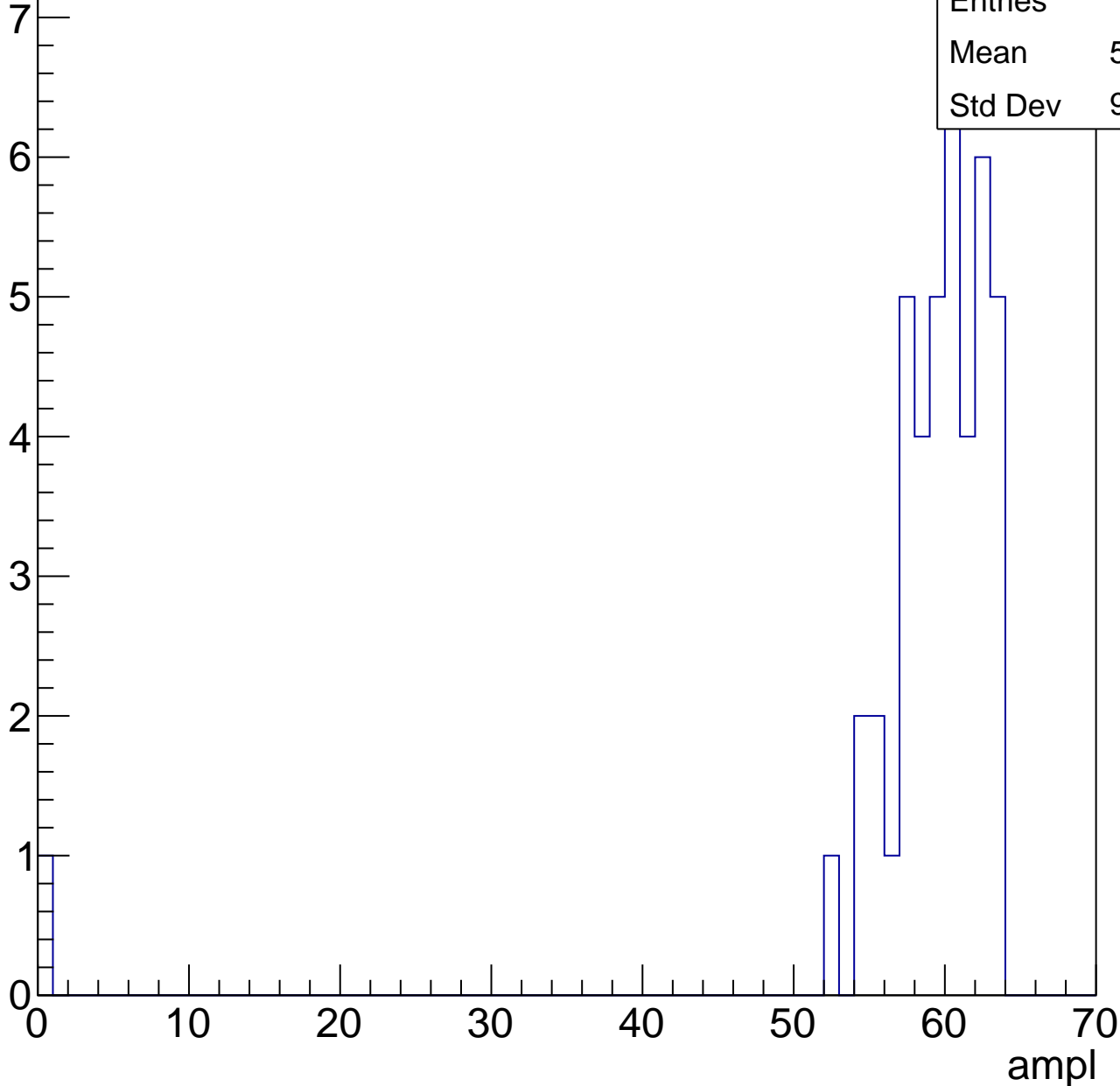


B1L103S, U23-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

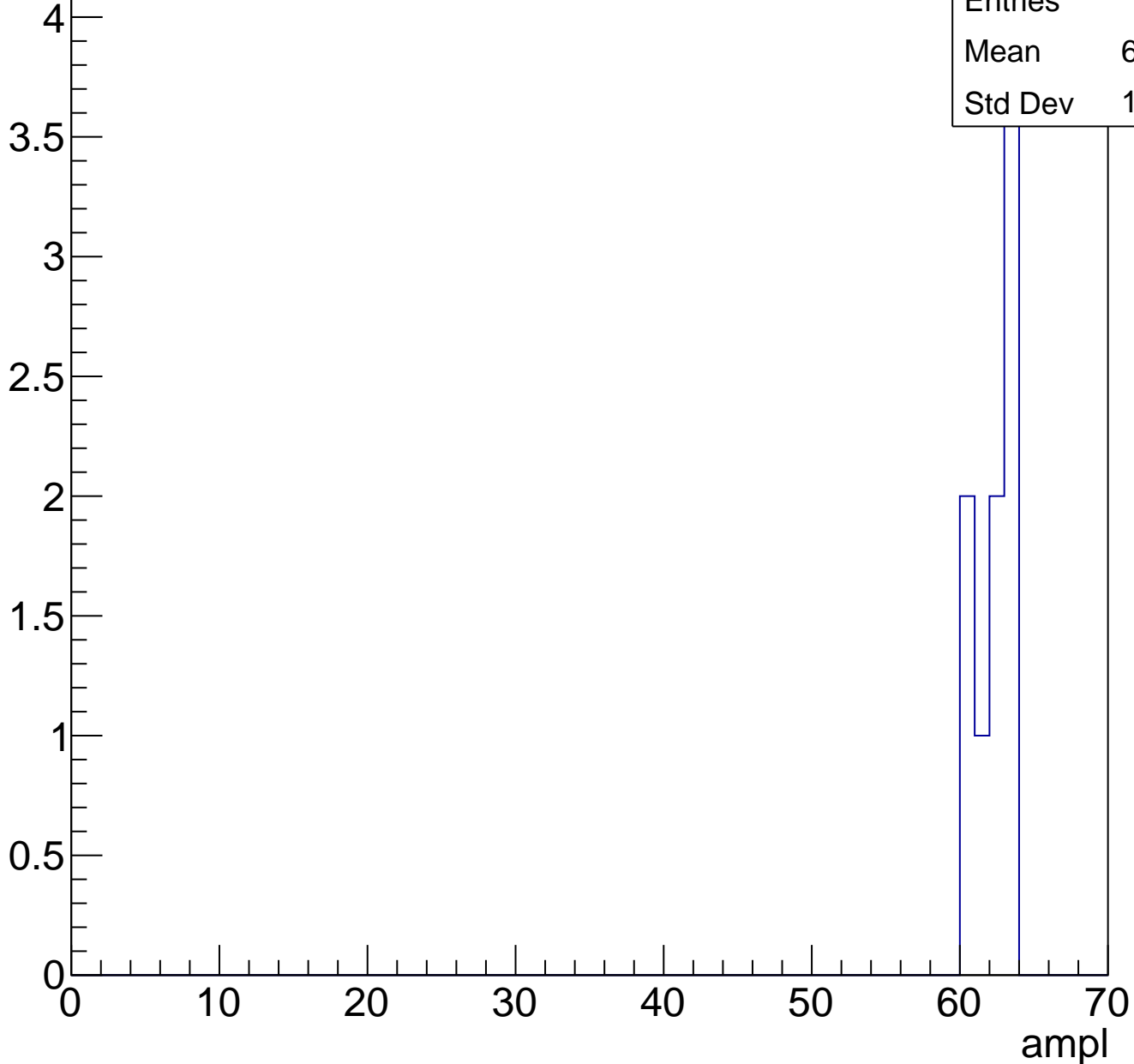
Entries	43
Mean	57.88
Std Dev	9.337



B1L103S, U23-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch47, adc0

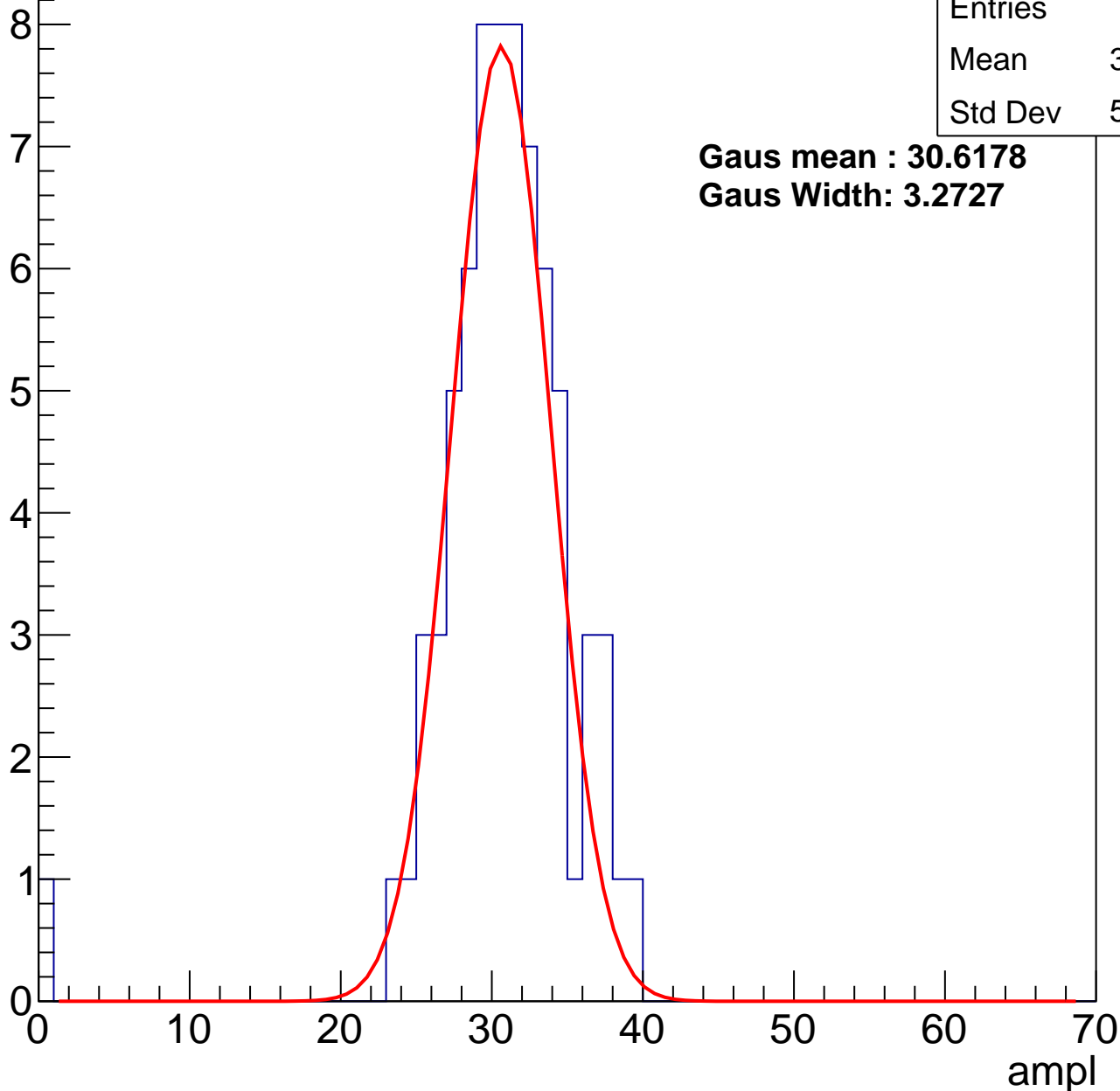
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.23
Std Dev	5.002

Gaus mean : 30.6178

Gaus Width: 3.2727



B1L103S, U23-ch47, adc1

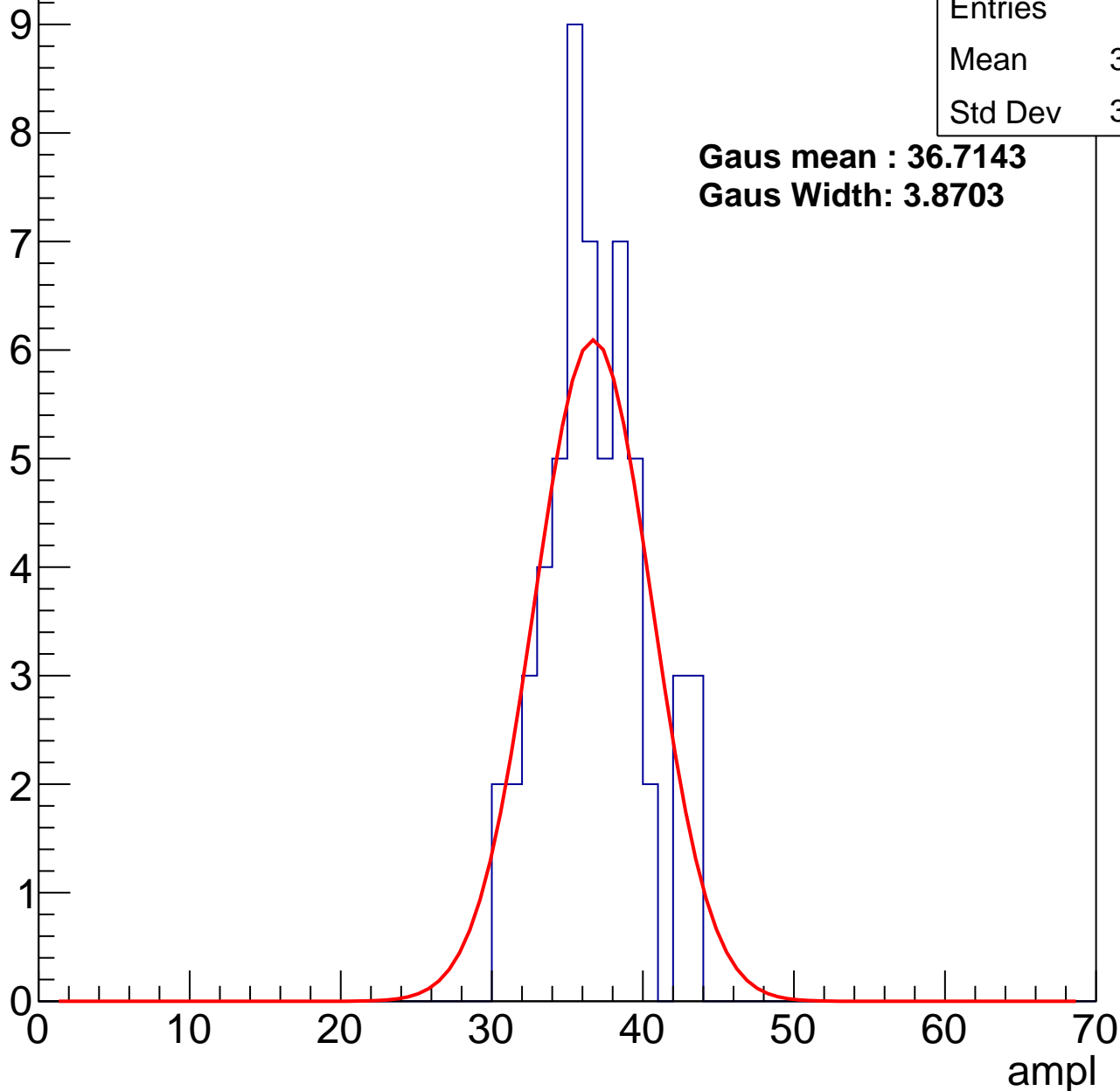
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.28
Std Dev	3.232

Gaus mean : 36.7143

Gaus Width: 3.8703



B1L103S, U23-ch47, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.32
Std Dev	3.117

Gaus mean : 43.4885

Gaus Width: 3.3931

10

8

6

4

2

0

0

10

20

30

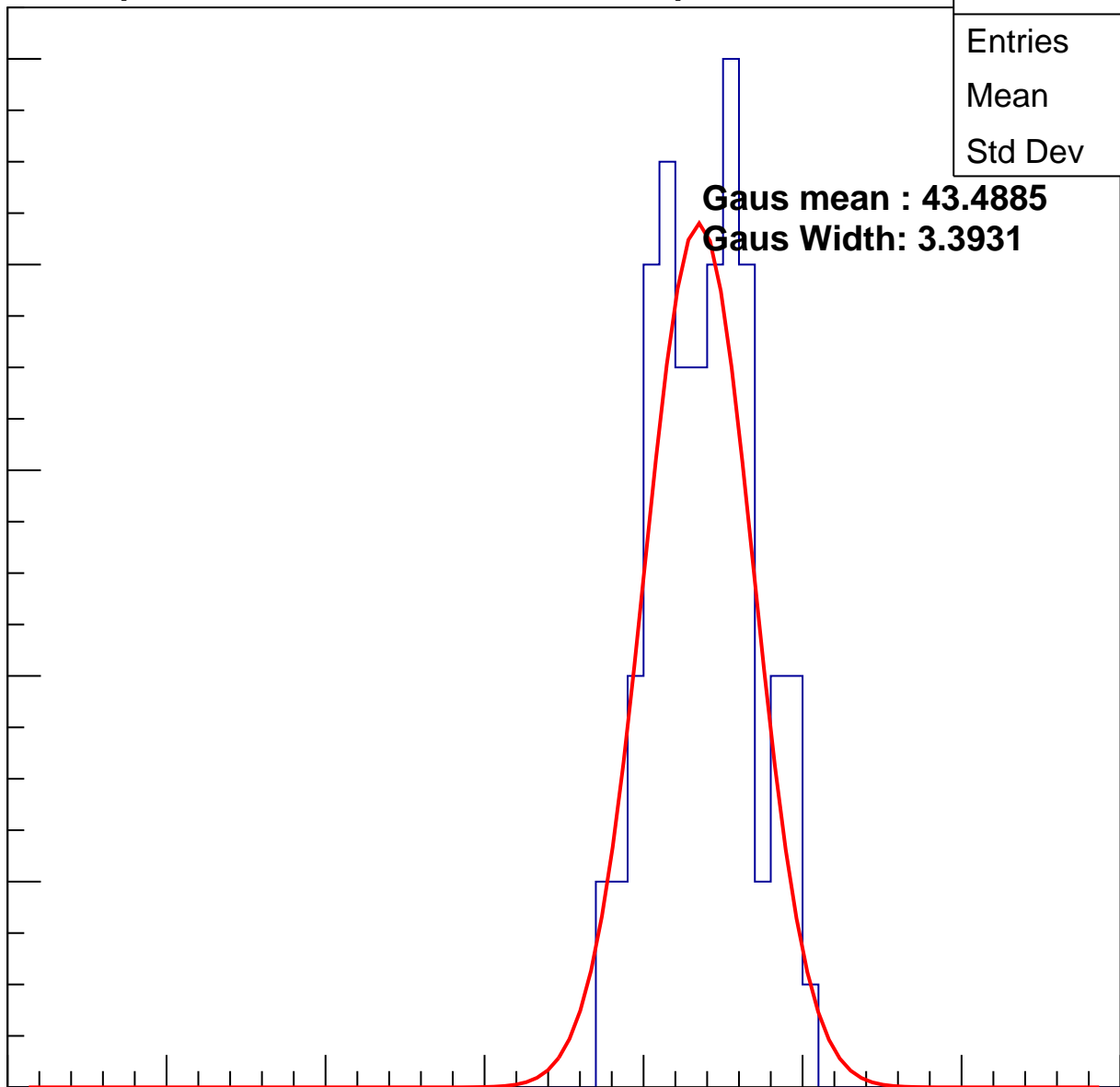
40

50

60

70

ampl

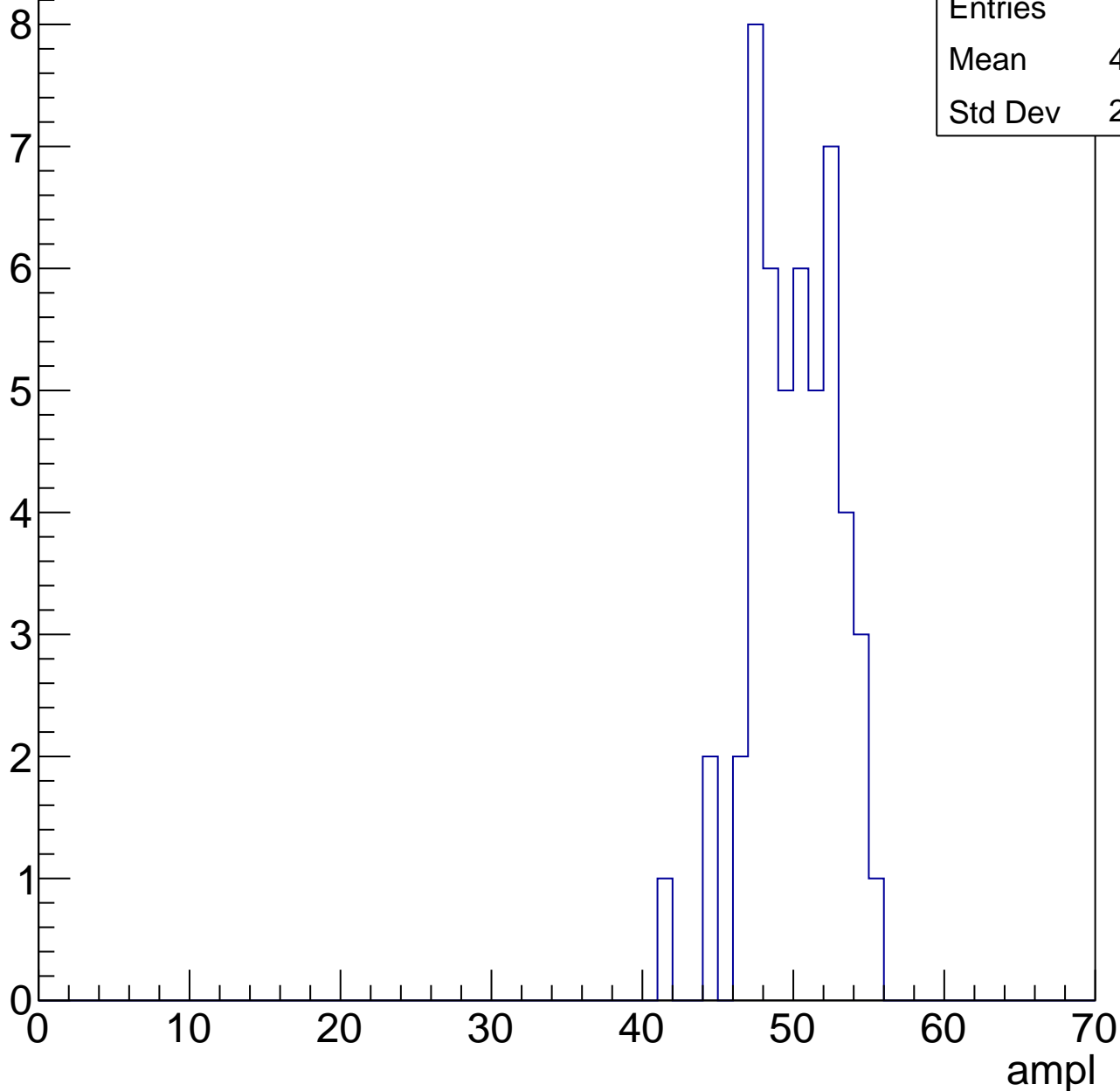


B1L103S, U23-ch47, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	49.56
Std Dev	2.892

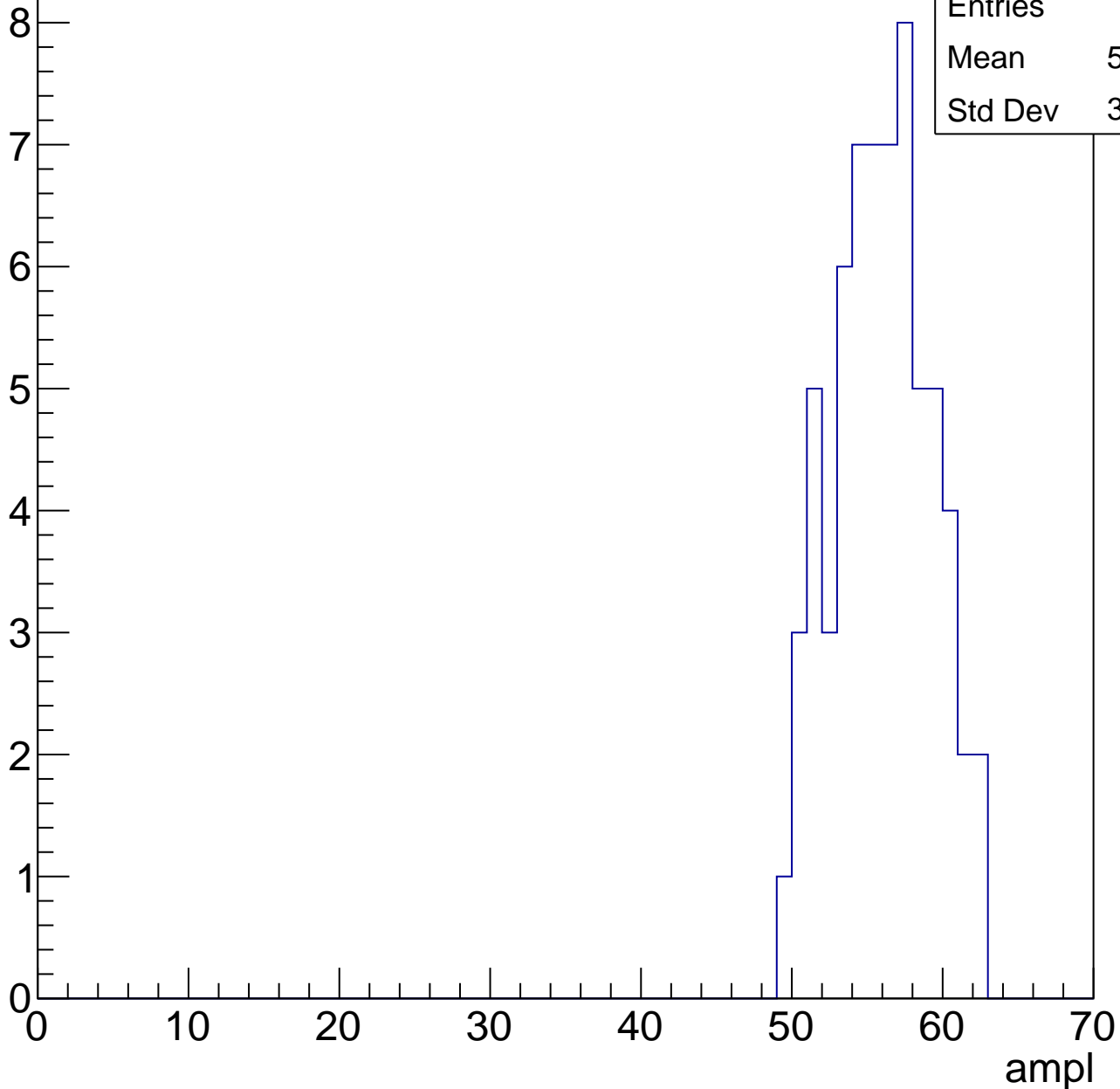


B1L103S, U23-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.54
Std Dev	3.197

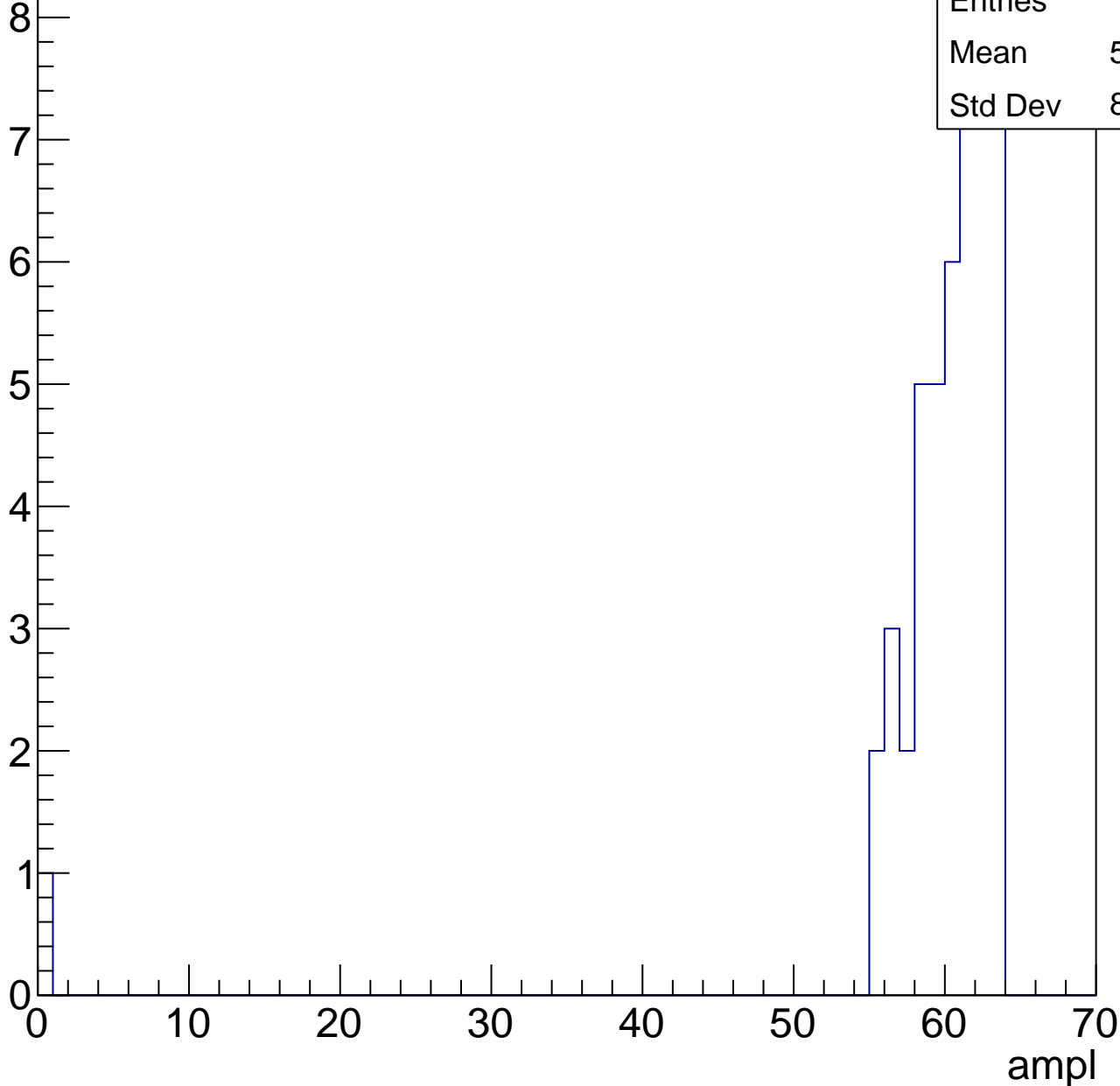


B1L103S, U23-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.85
Std Dev	8.886



B1L103S, U23-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L103S, U23-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch48, adc0

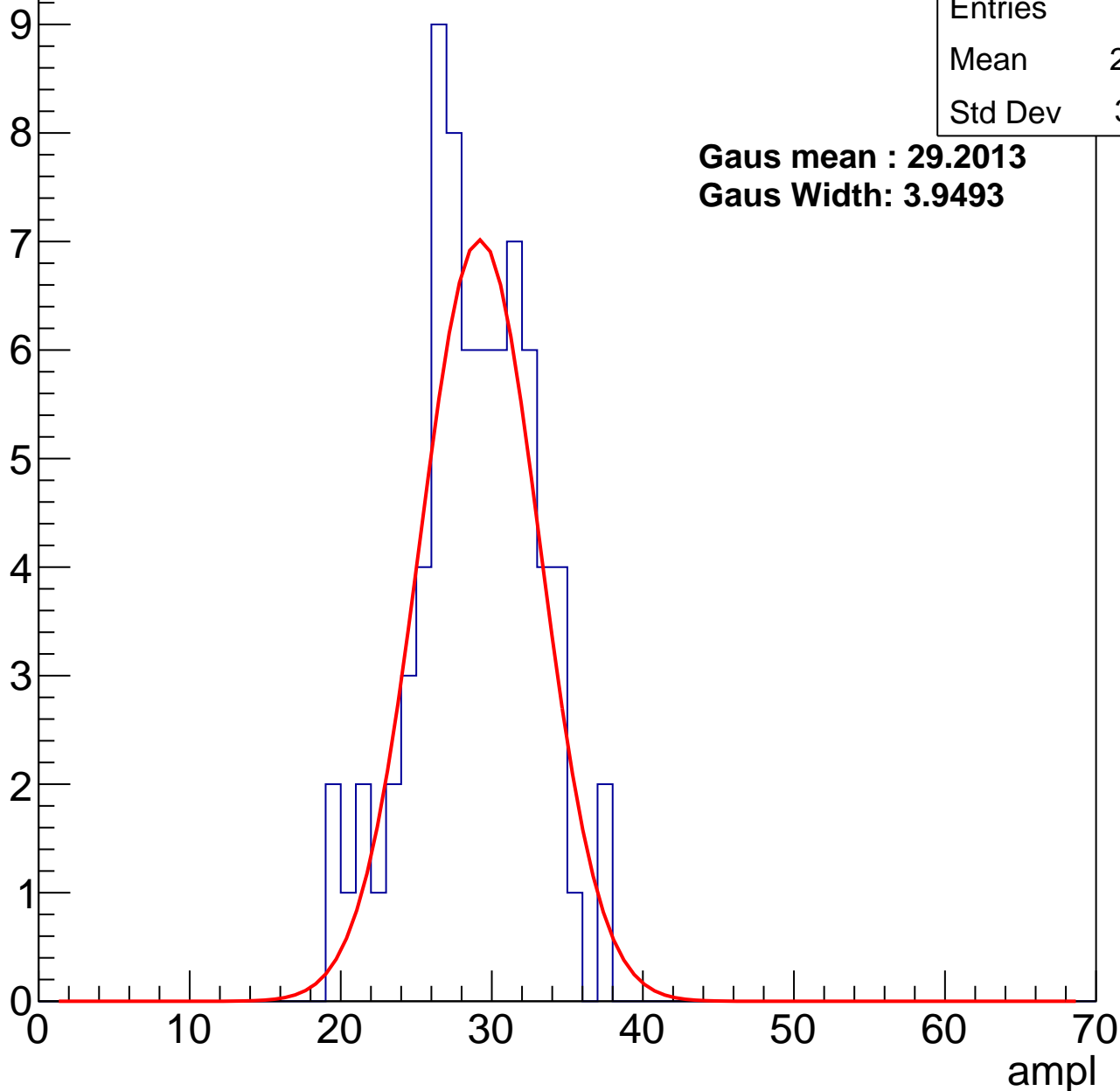
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.35
Std Dev	3.981

Gaus mean : 29.2013

Gaus Width: 3.9493



B1L103S, U23-ch48, adc1

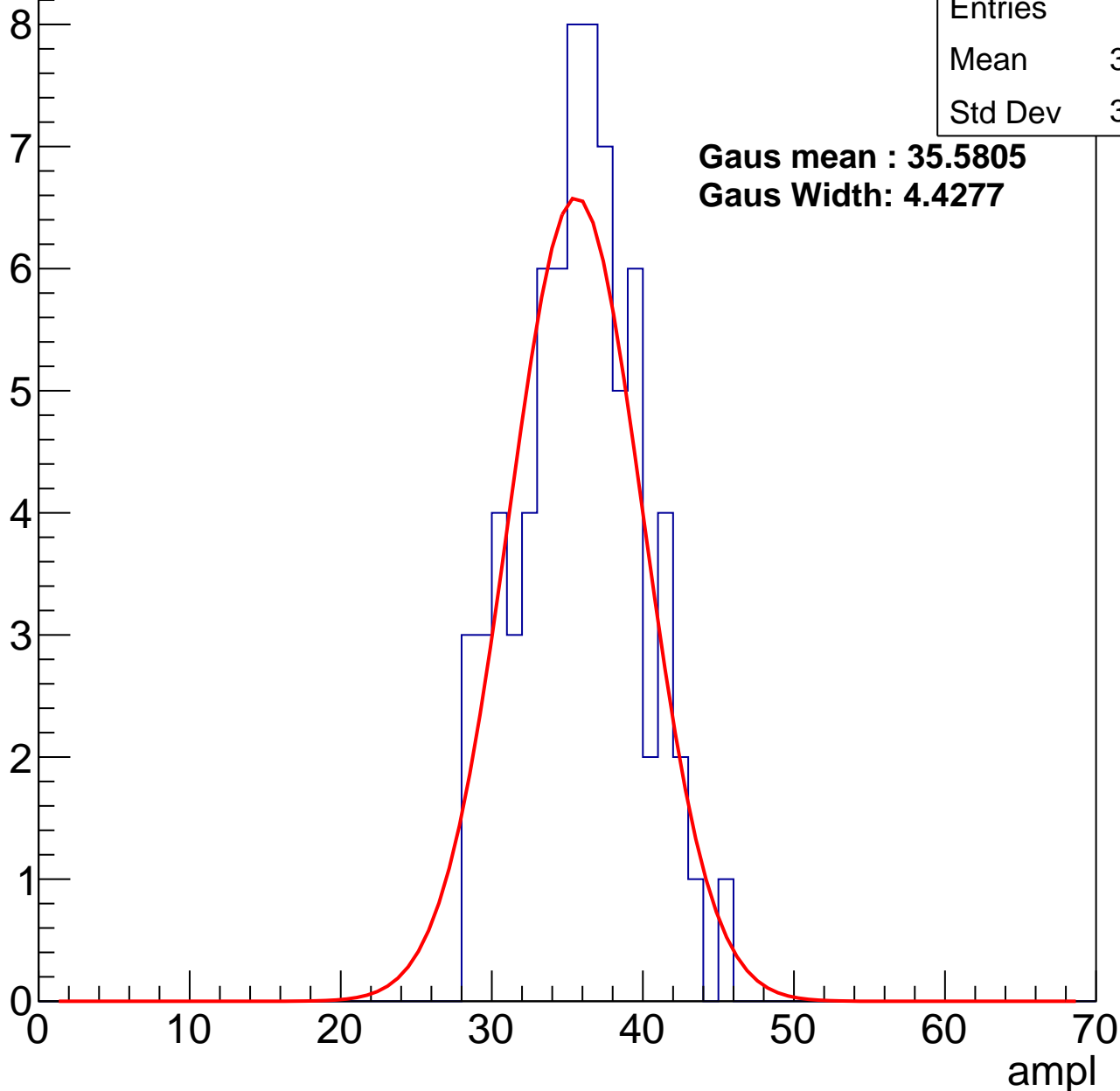
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.36
Std Dev	3.869

Gaus mean : 35.5805

Gaus Width: 4.4277



B1L103S, U23-ch48, adc2

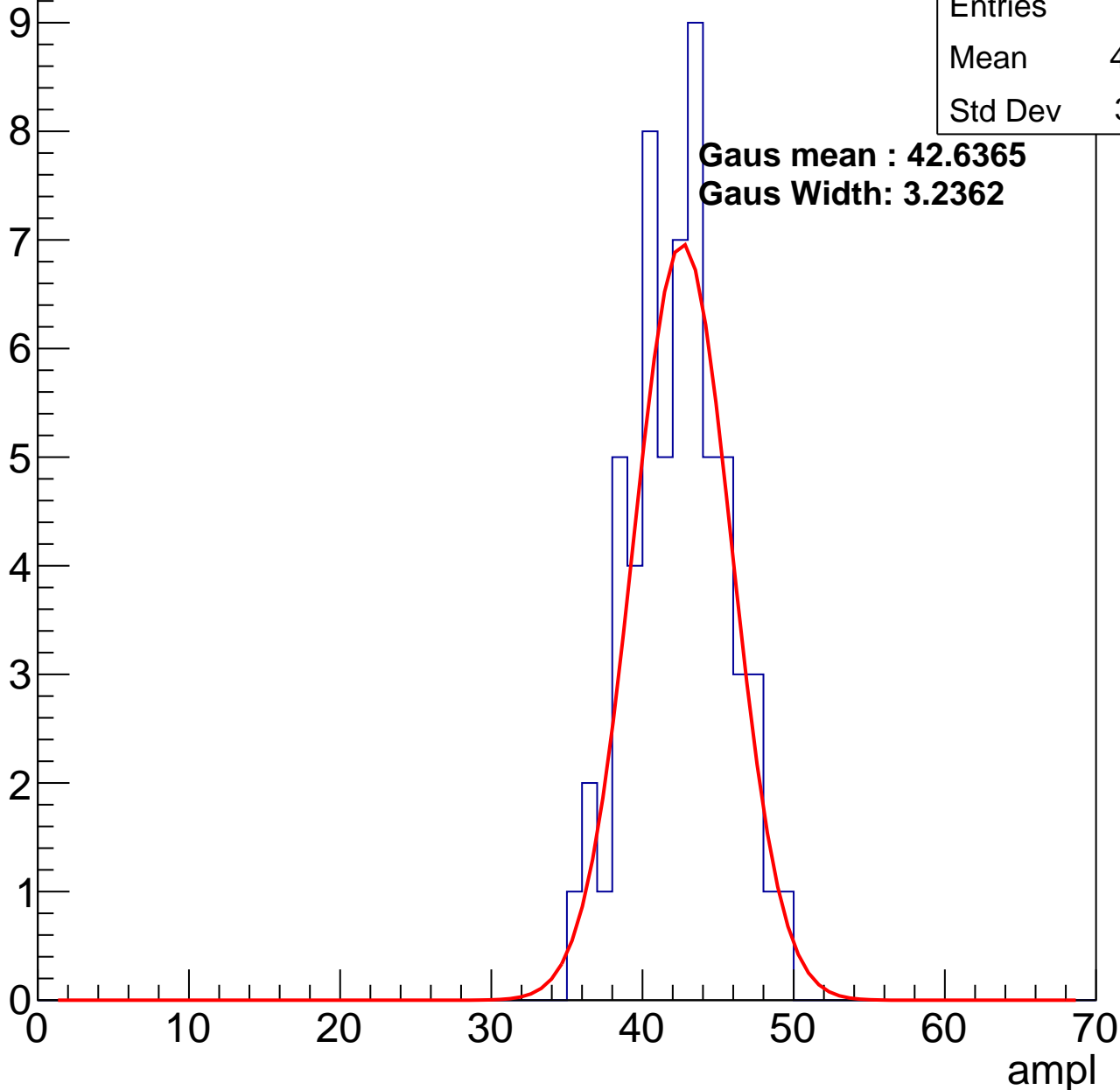
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	41.95
Std Dev	3.111

Gaus mean : 42.6365

Gaus Width: 3.2362

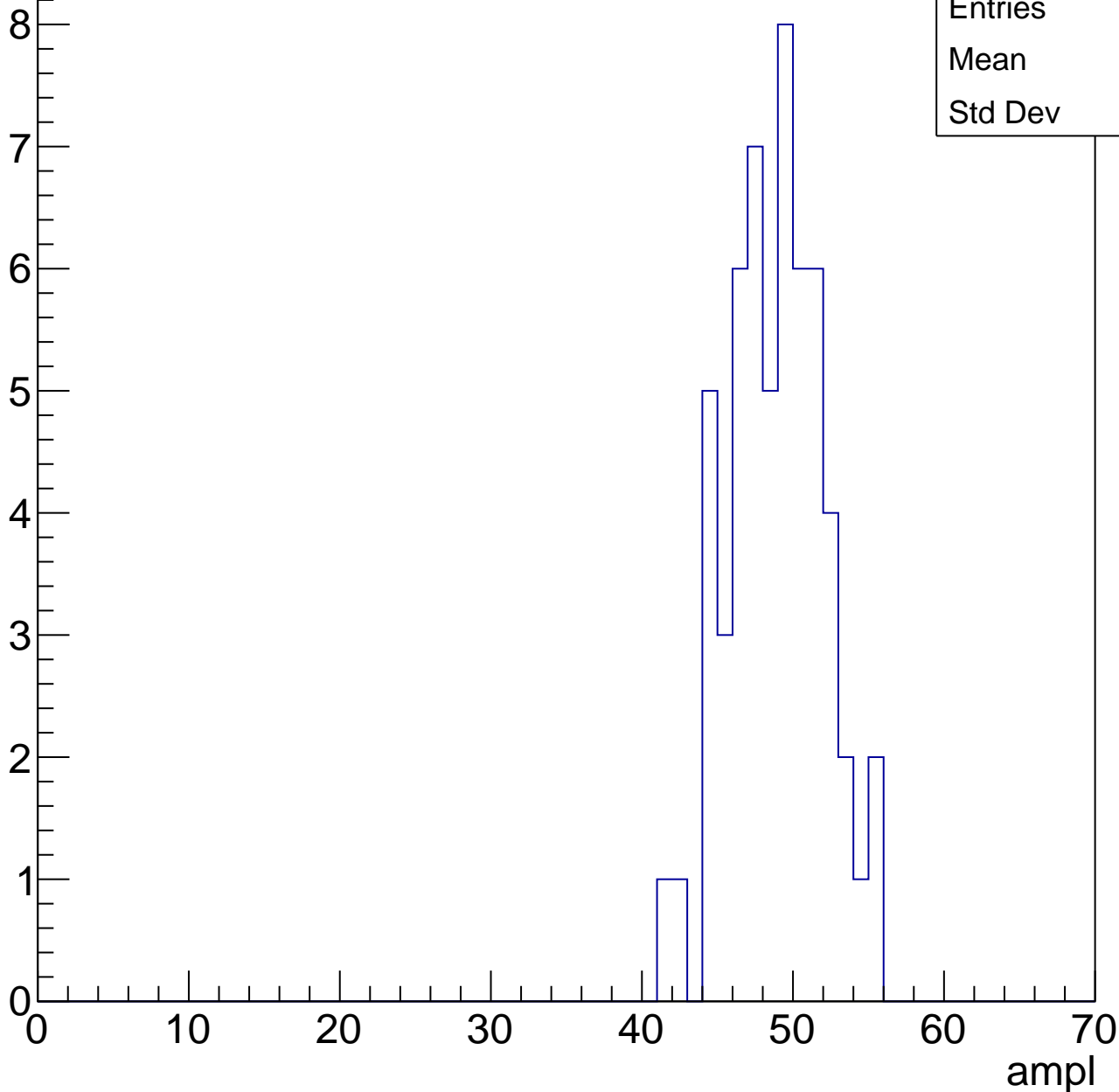


B1L103S, U23-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

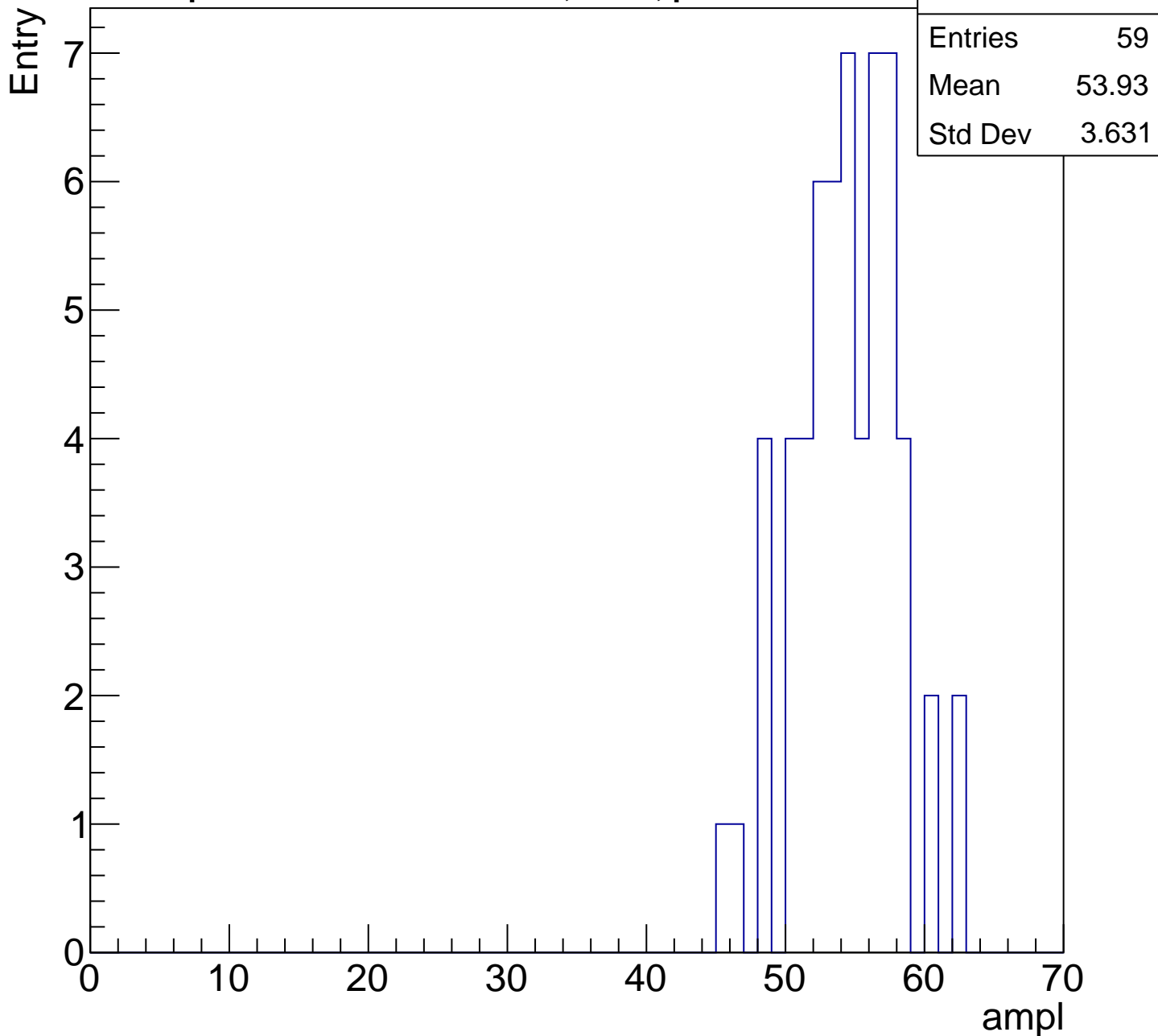
Entry

Entries	57
Mean	48.4
Std Dev	3.1



B1L103S, U23-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

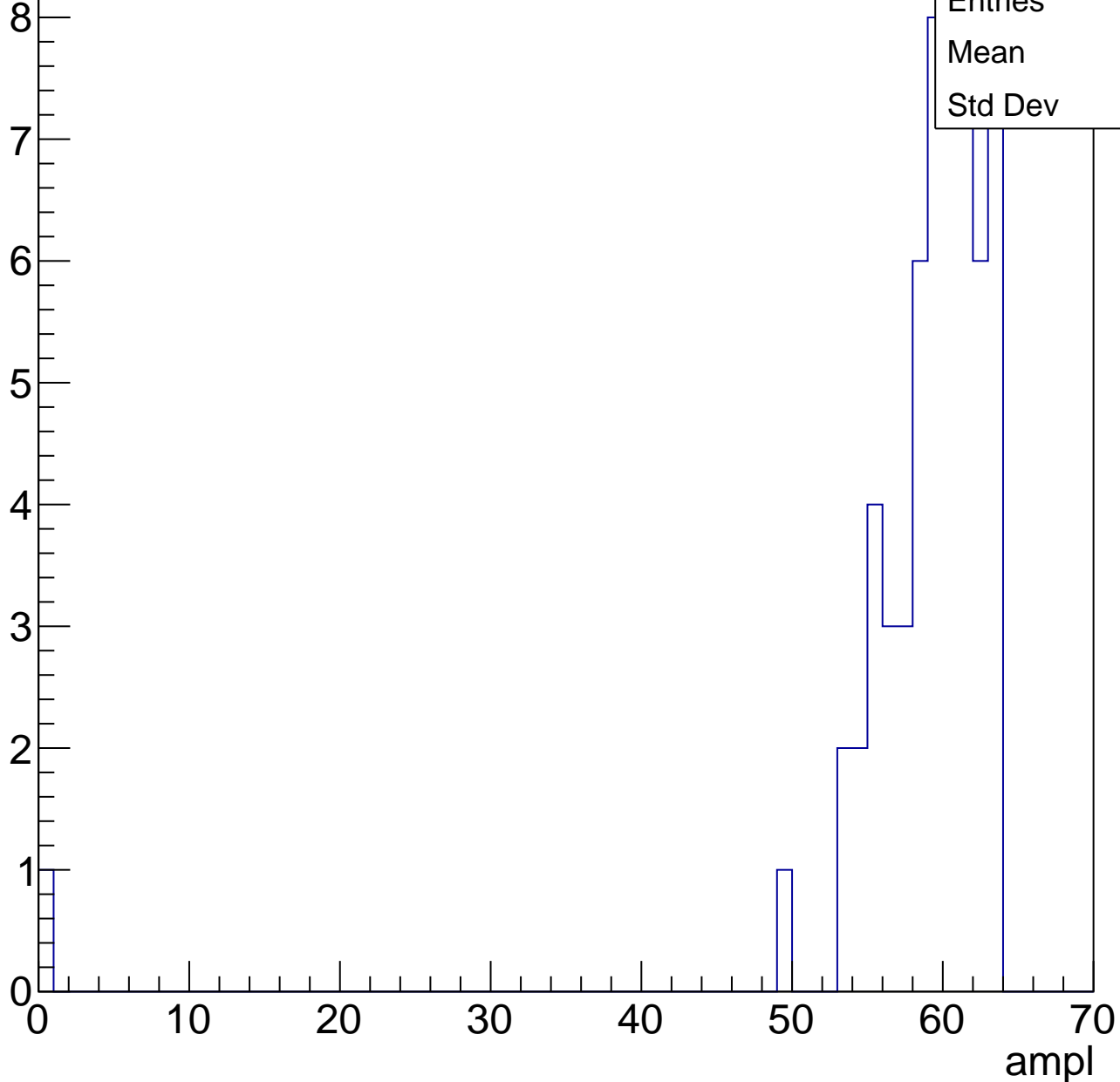


B1L103S, U23-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	58.1
Std Dev	8.15



B1L103S, U23-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.25
Std Dev	1.479

0 10 20 30 40 50 60 70

ampl

B1L103S, U23-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch49, adc0

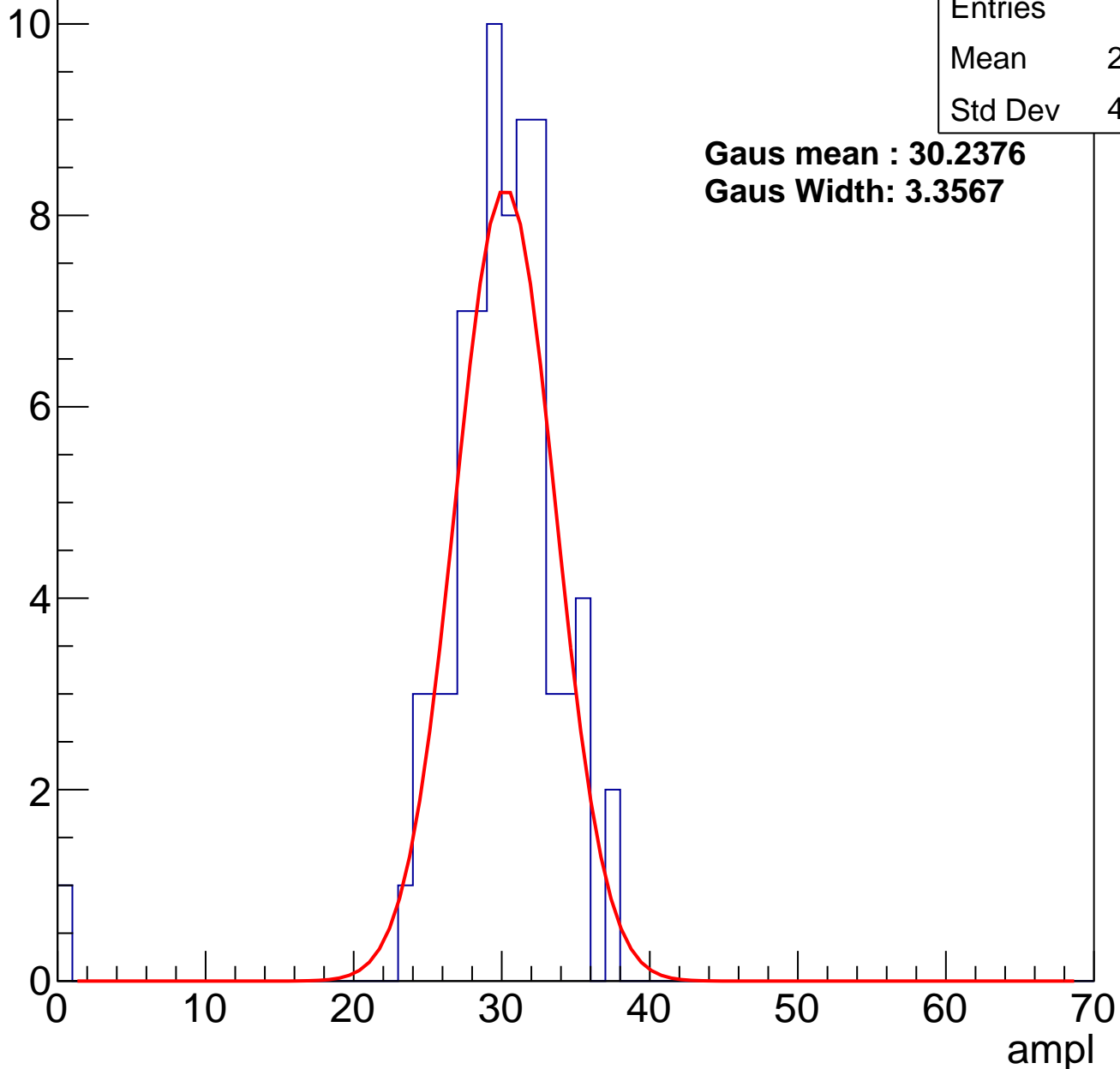
calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	29.38
Std Dev	4.639

Gaus mean : 30.2376

Gaus Width: 3.3567

Entry



B1L103S, U23-ch49, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	65
Mean	36.71
Std Dev	2.975

Gaus mean : 37.4544

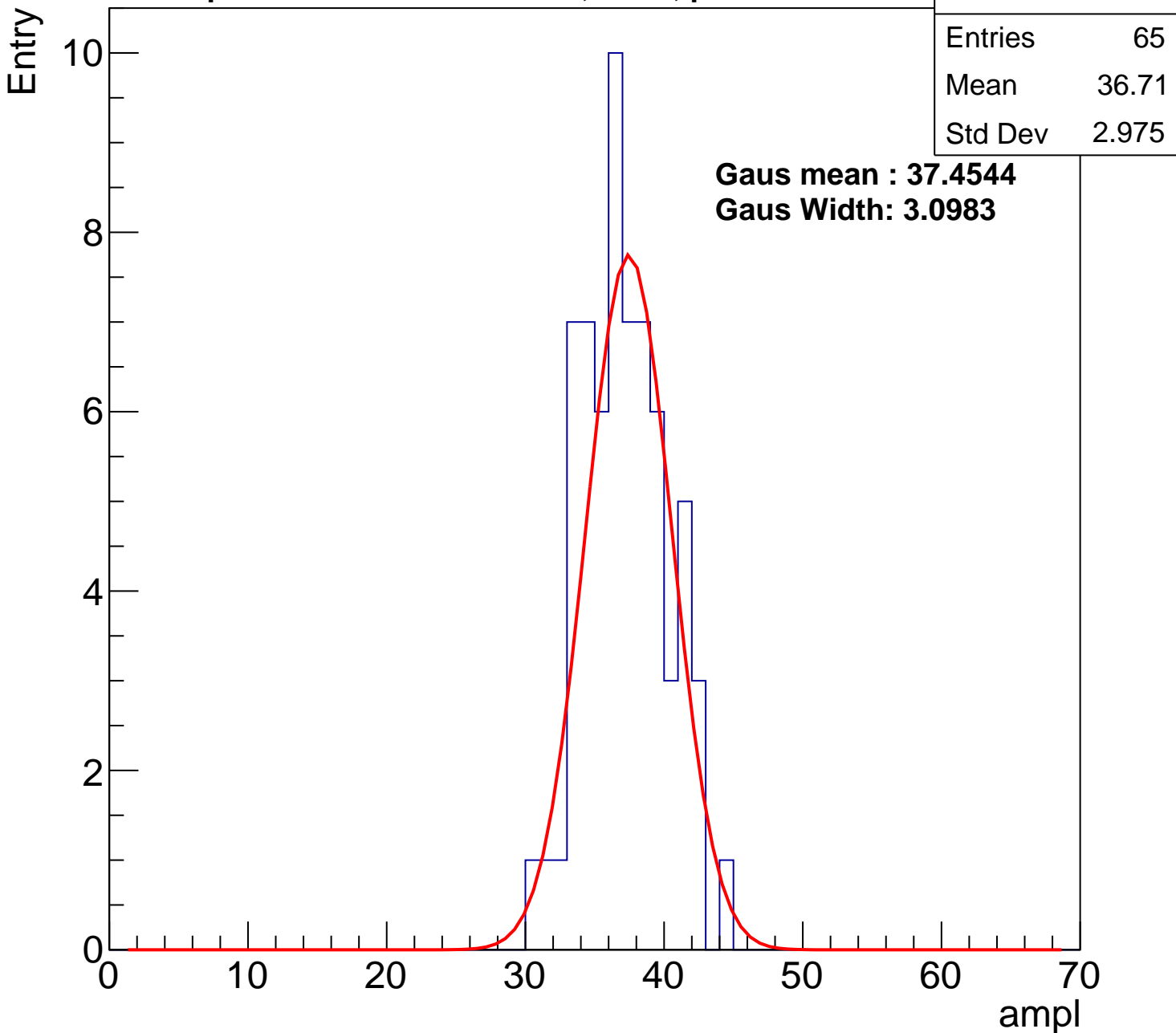
Gaus Width: 3.0983

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch49, adc2

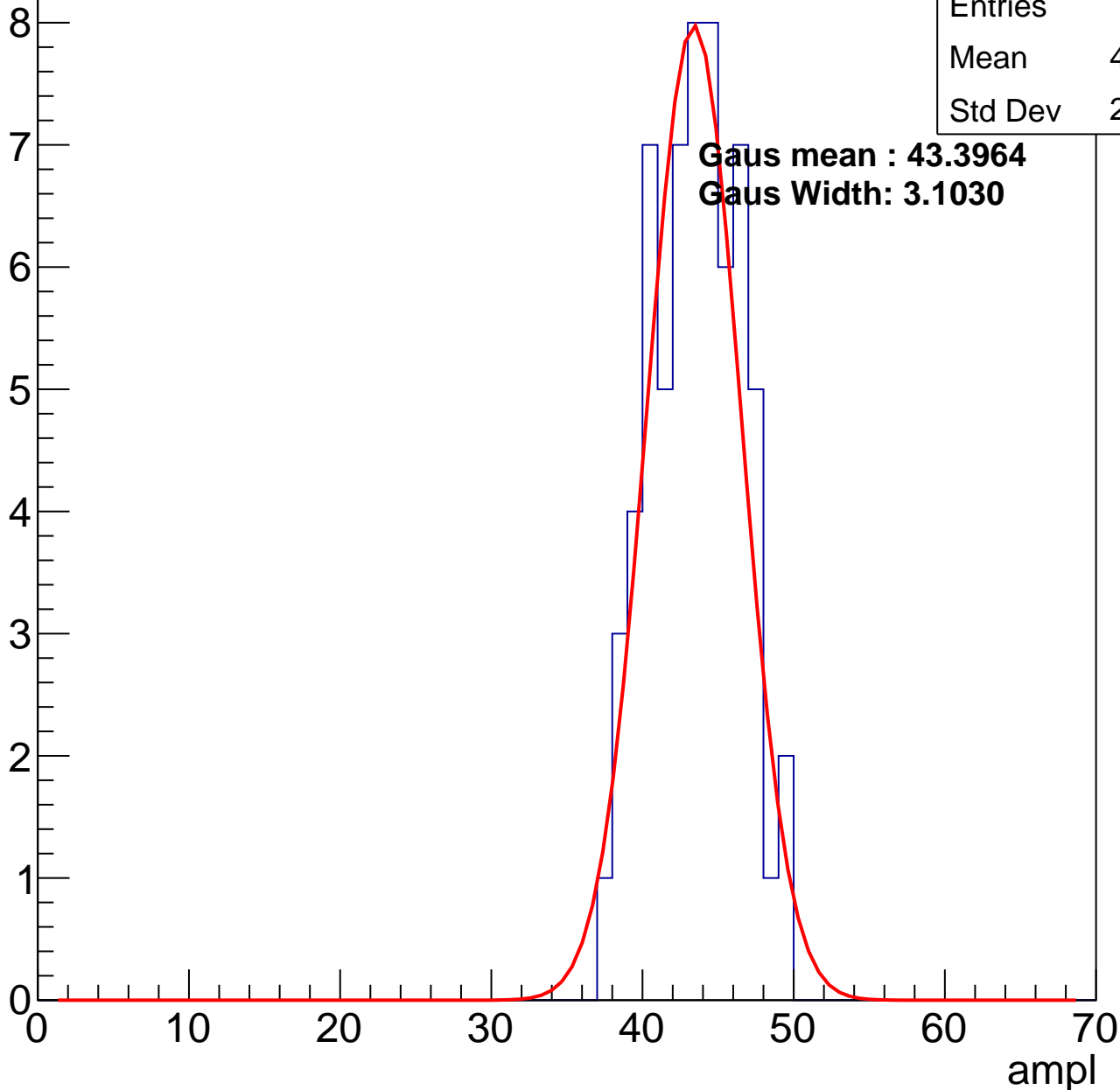
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.05
Std Dev	2.896

Gaus mean : 43.3964

Gaus Width: 3.1030



B1L103S, U23-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	50.15
Std Dev	3.354

Entry

10

8

6

4

2

0

0

10

20

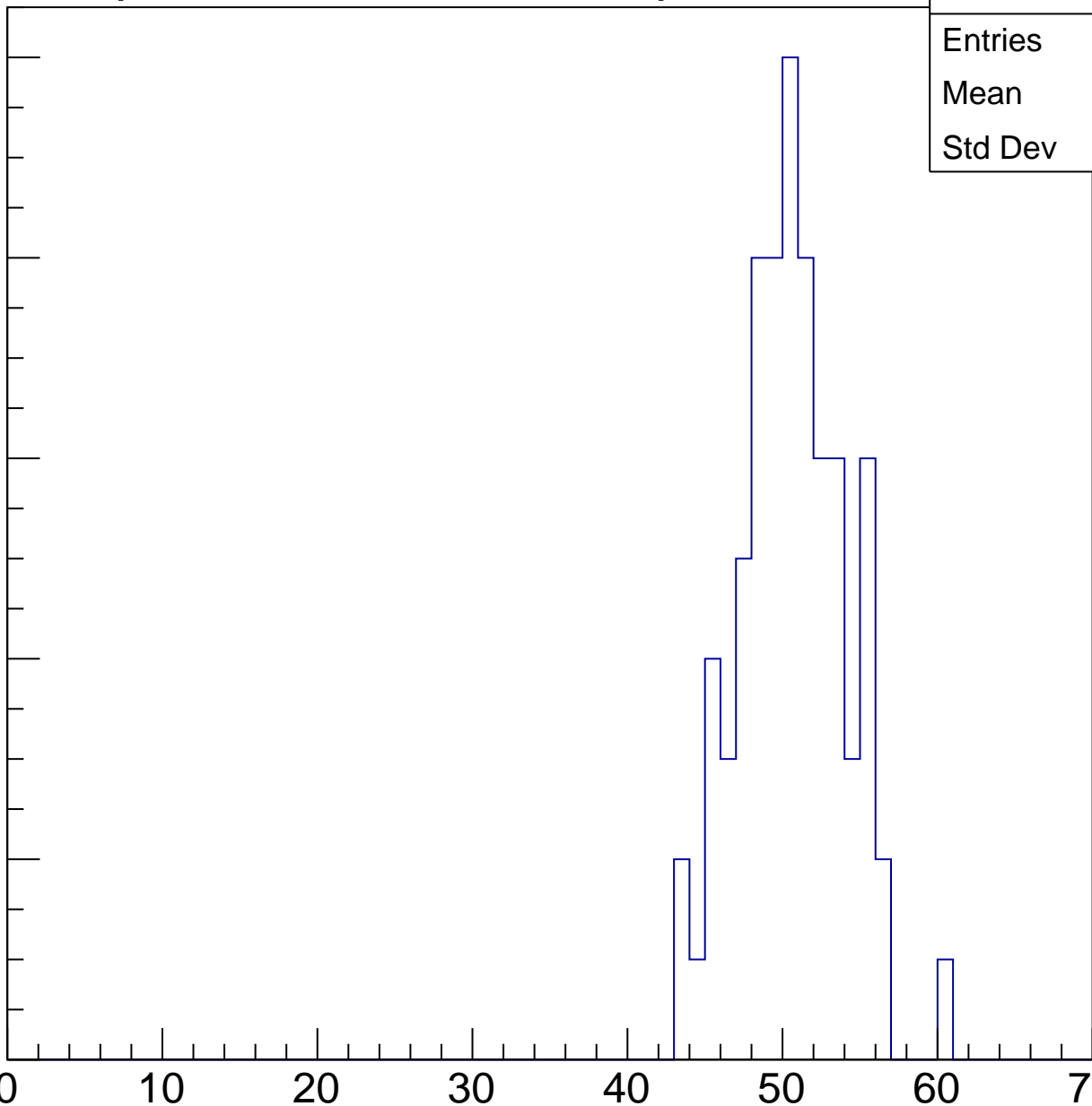
30

40

50

60

ampl

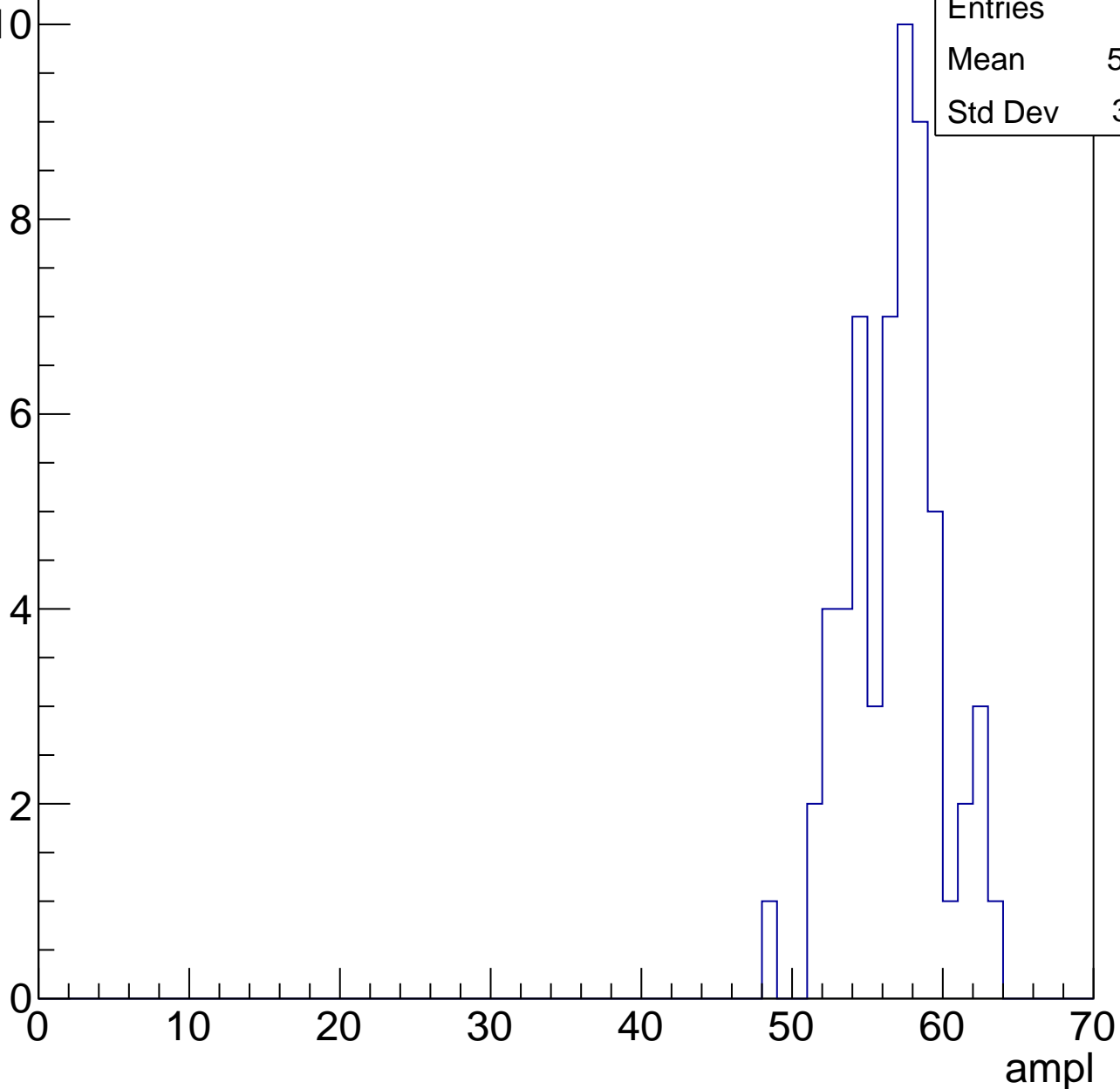


B1L103S, U23-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	56.32
Std Dev	3.061

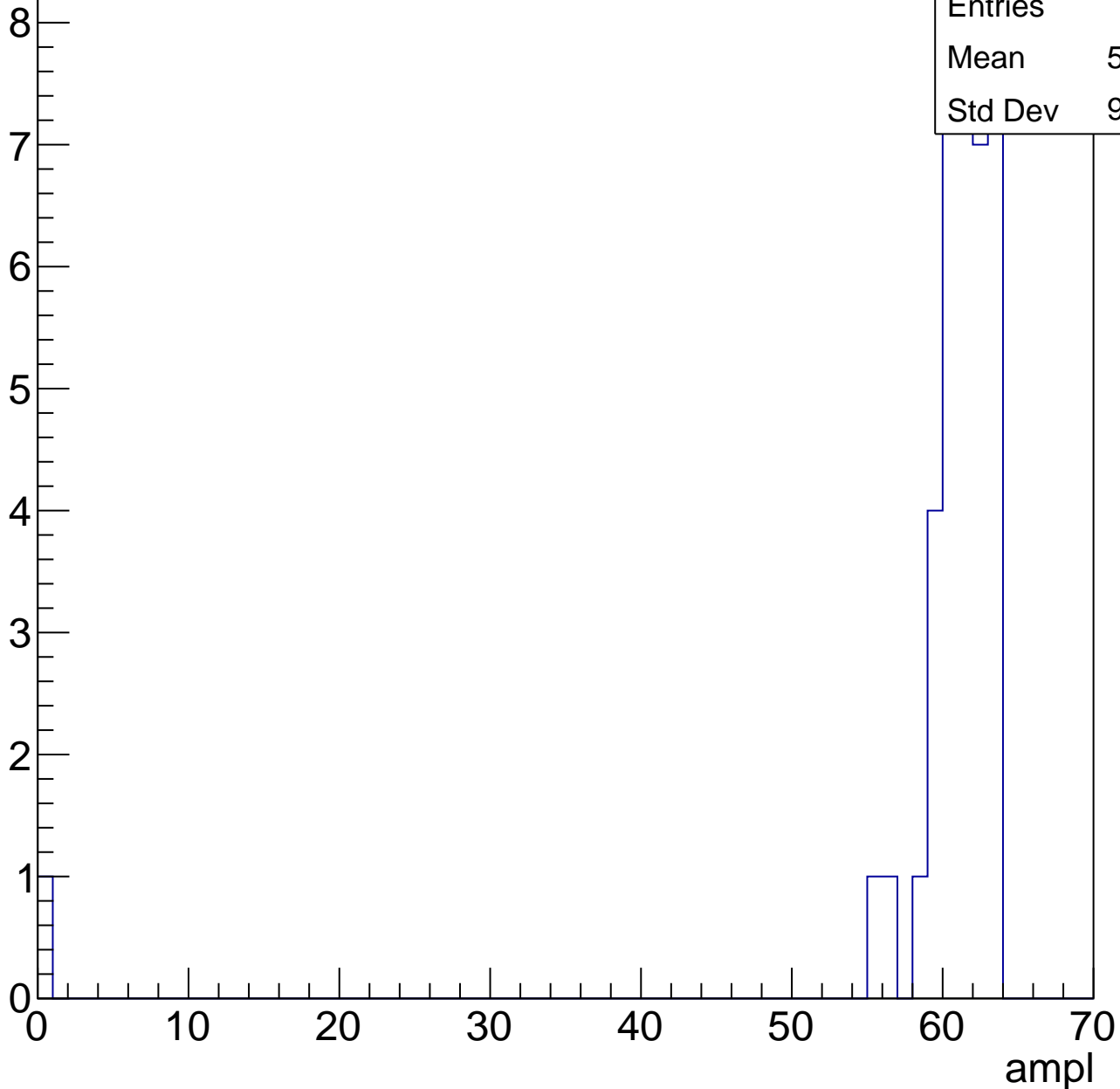


B1L103S, U23-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

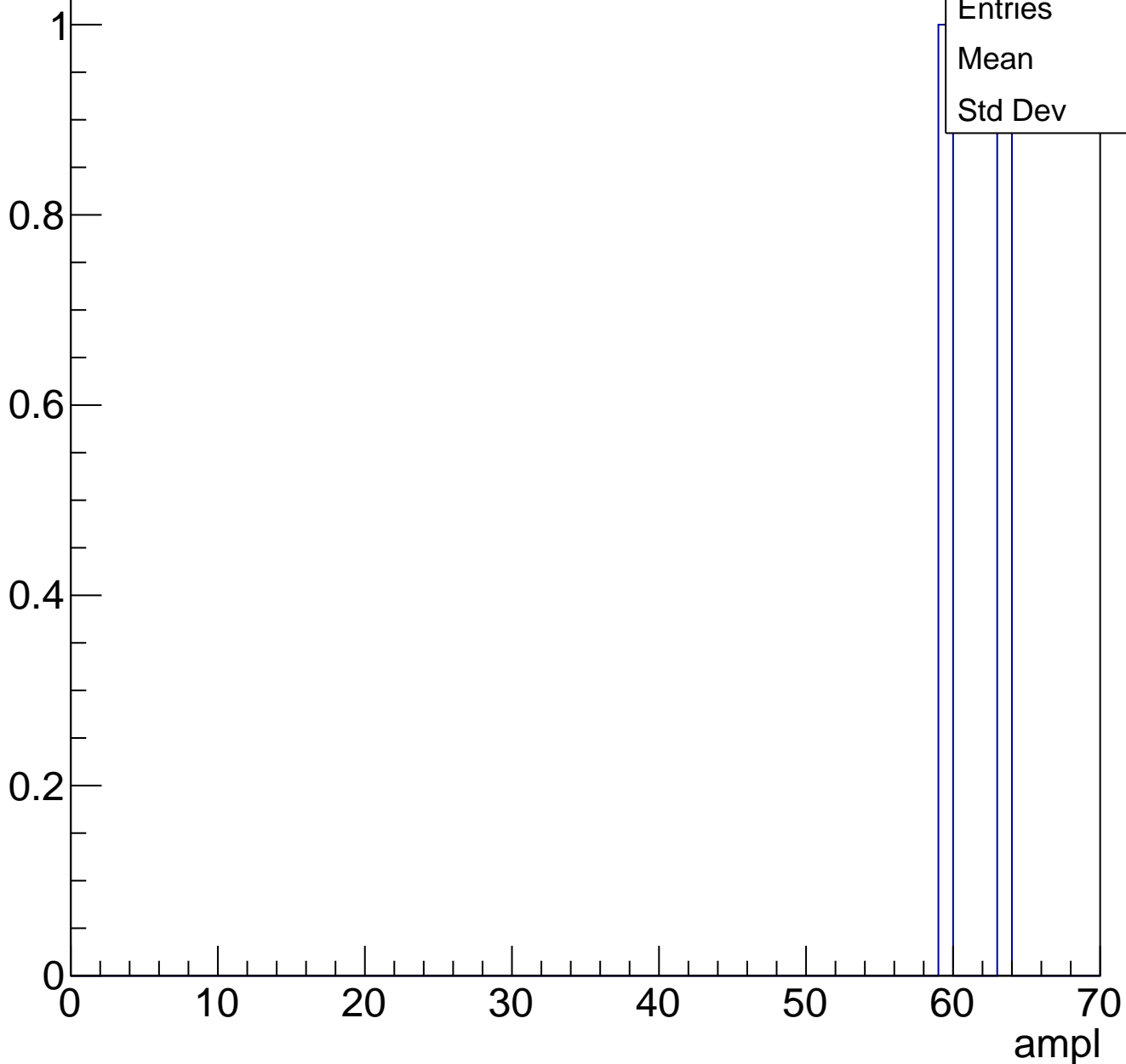
Entries	39
Mean	59.26
Std Dev	9.787



B1L103S, U23-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch50, adc0

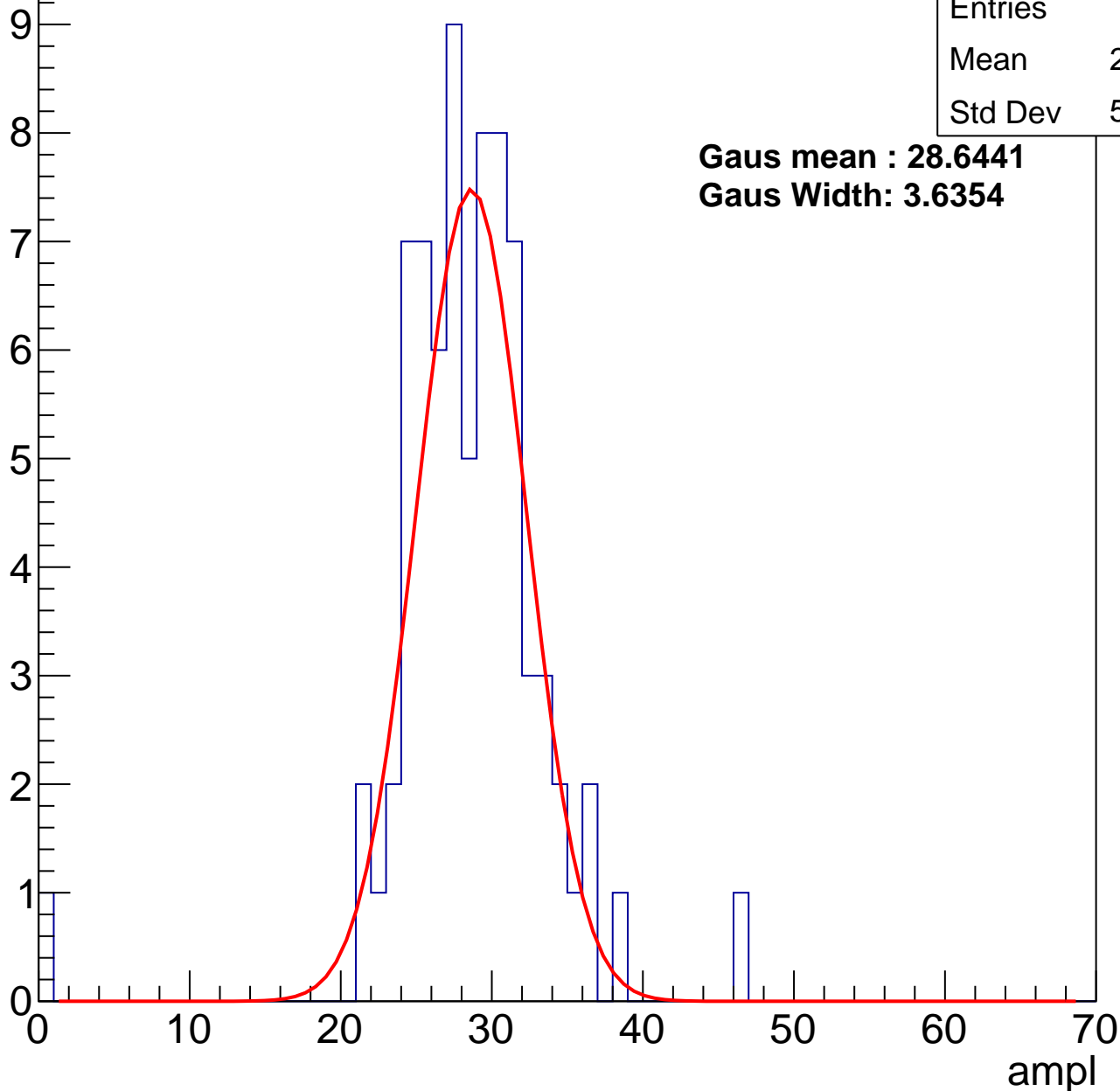
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.09
Std Dev	5.224

Gaus mean : 28.6441

Gaus Width: 3.6354



B1L103S, U23-ch50, adc1

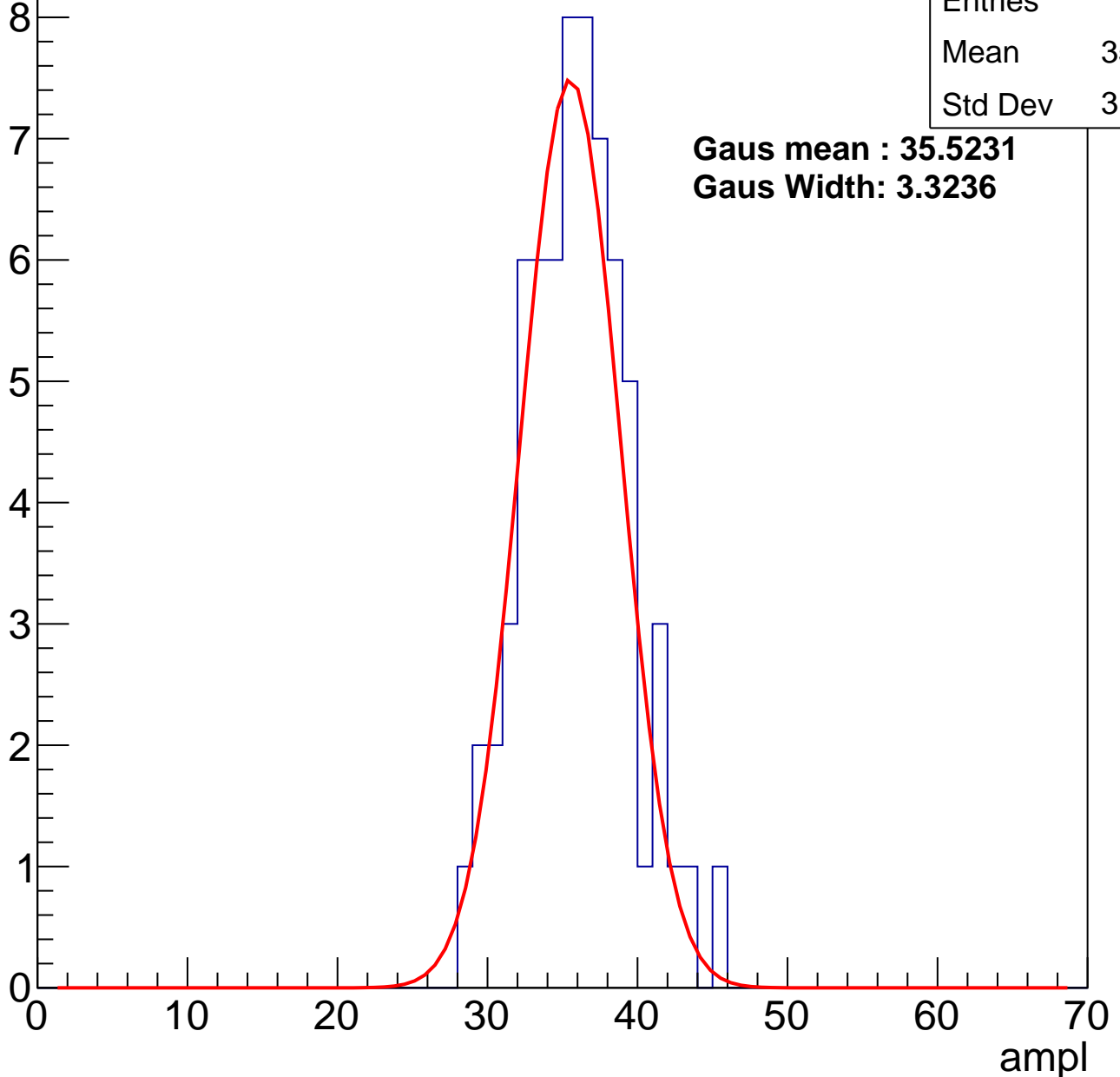
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.46
Std Dev	3.466

Gaus mean : 35.5231

Gaus Width: 3.3236



B1L103S, U23-ch50, adc2

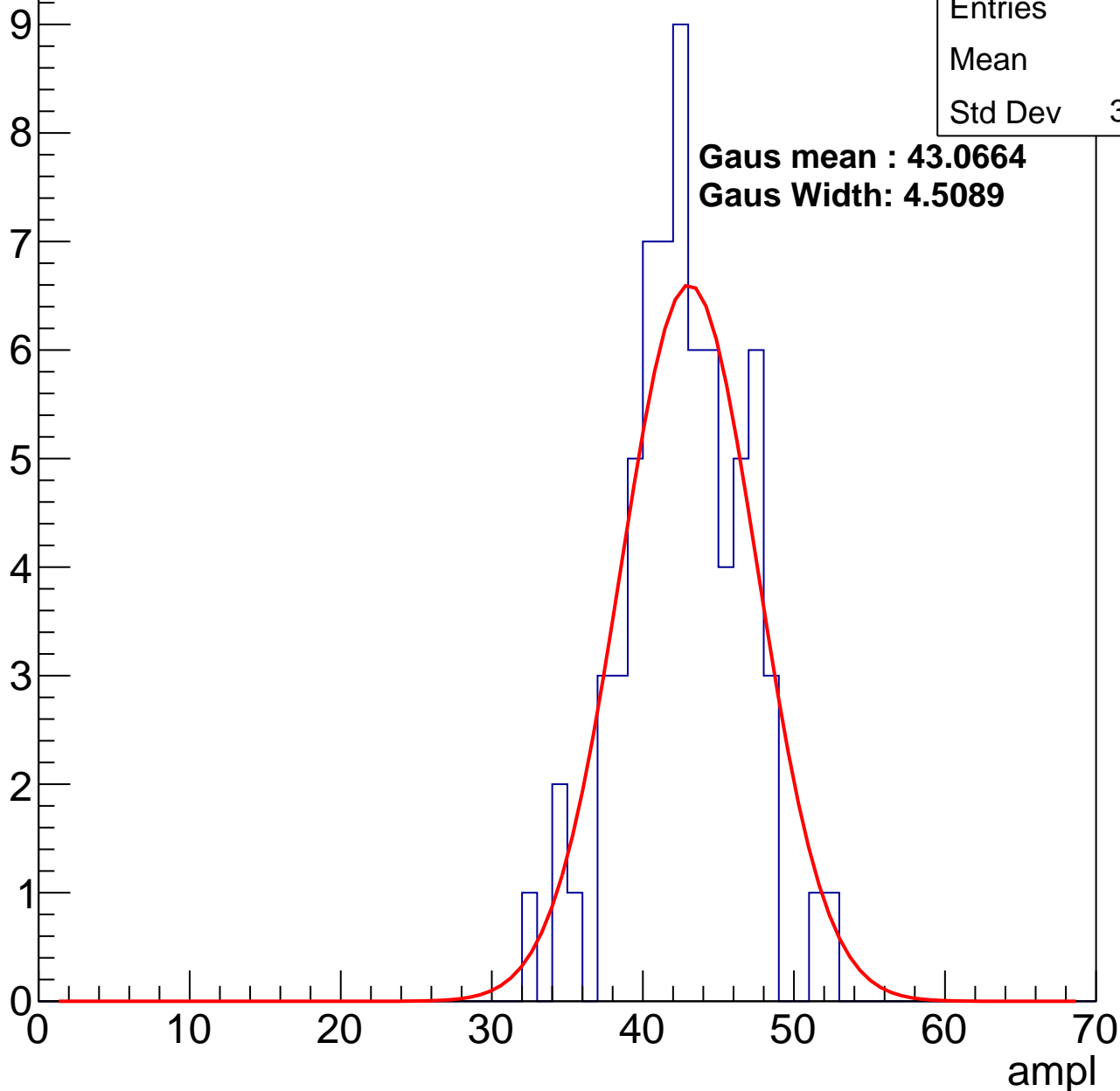
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.3
Std Dev	3.889

Gaus mean : 43.0664

Gaus Width: 4.5089

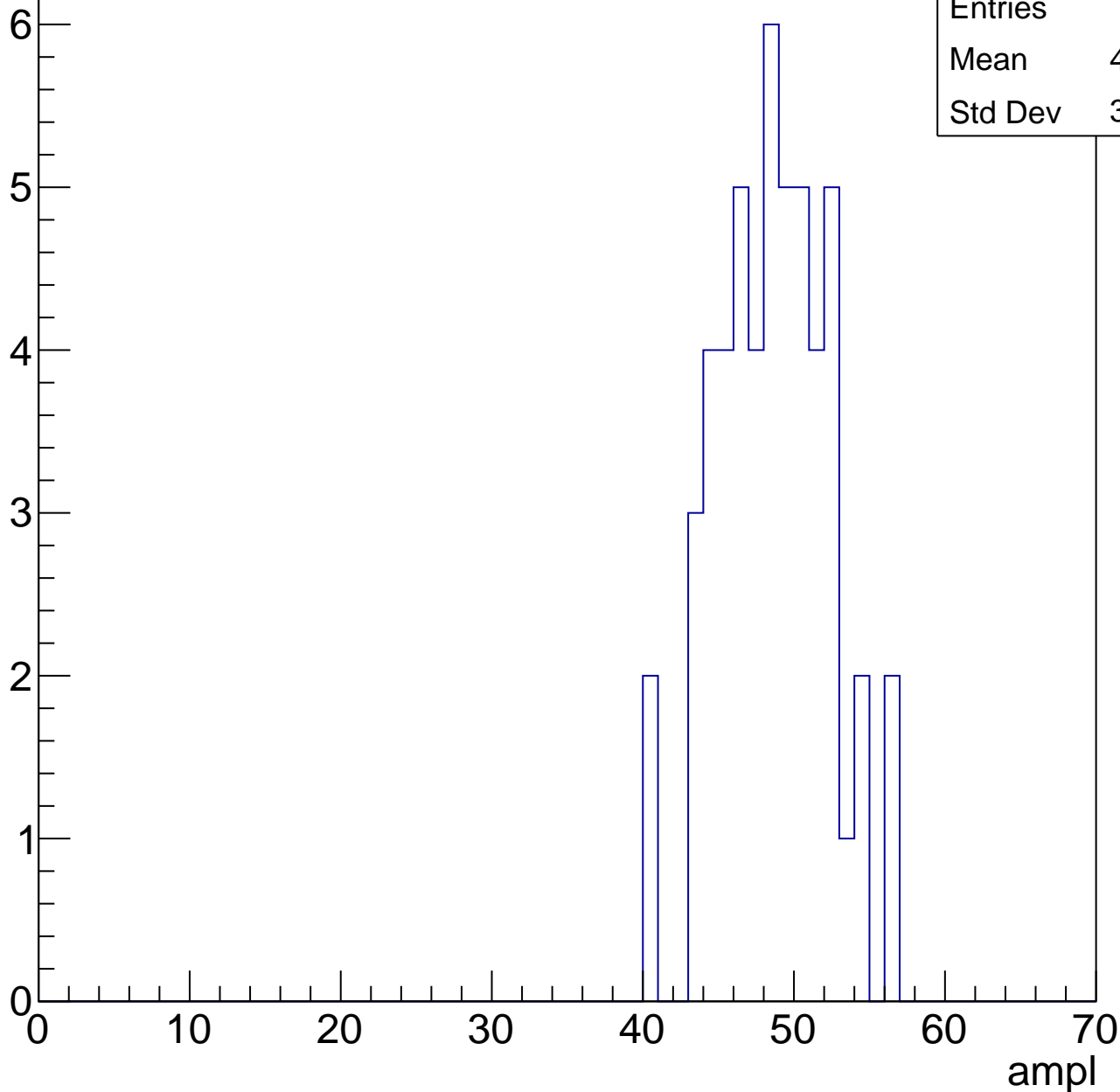


B1L103S, U23-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

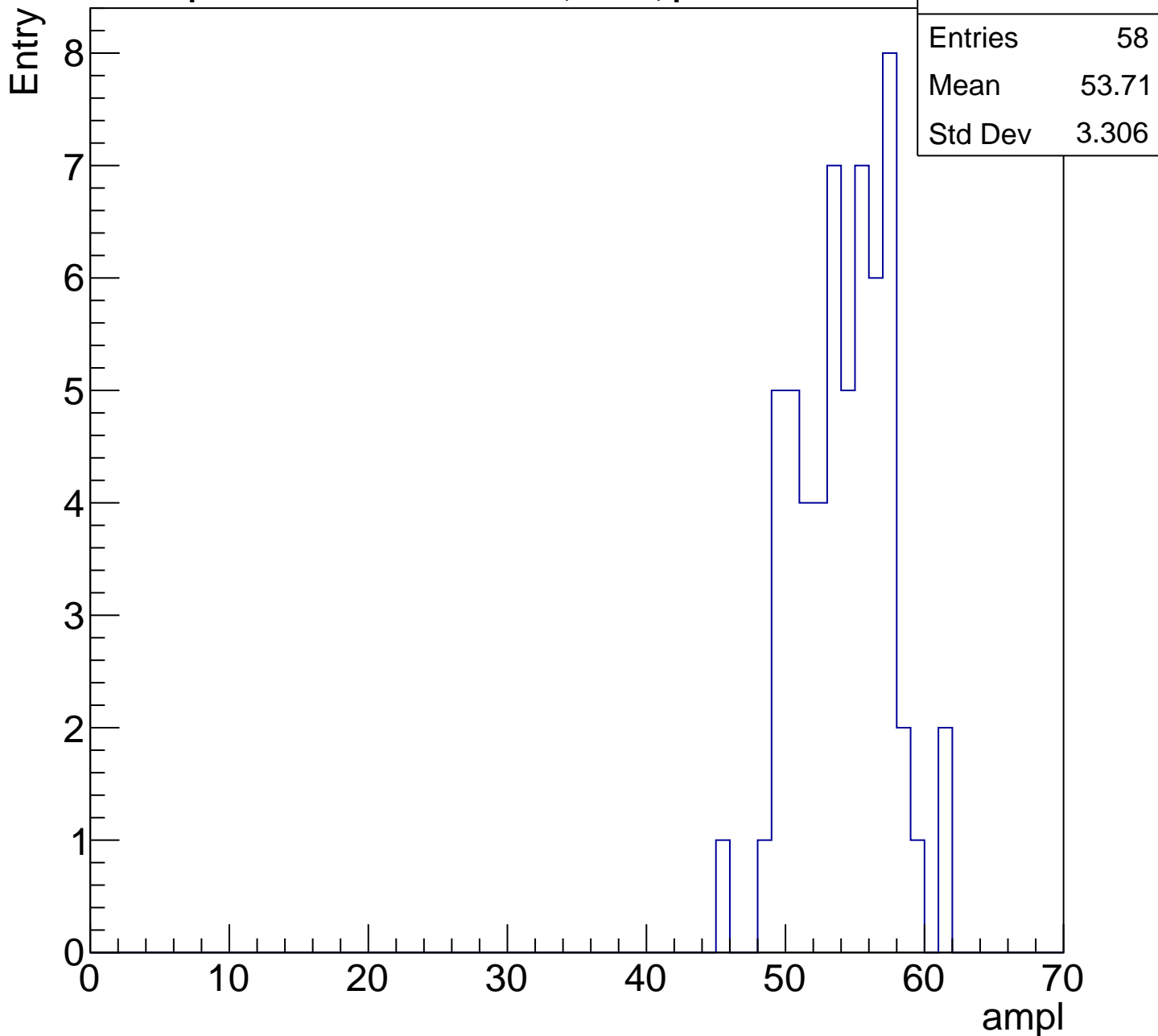
Entry

Entries	52
Mean	48.13
Std Dev	3.648



B1L103S, U23-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

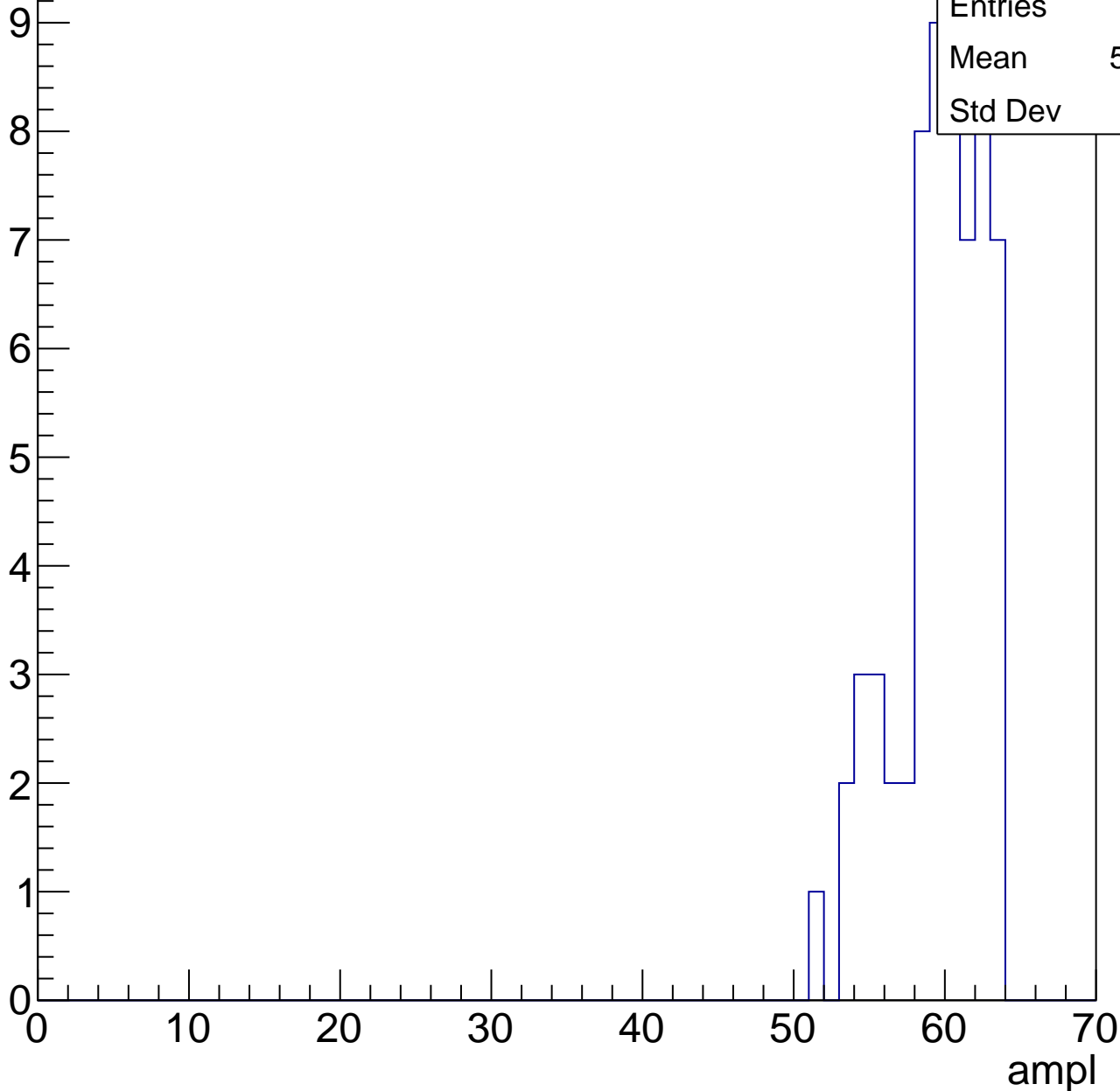


B1L103S, U23-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	59.15
Std Dev	2.92



B1L103S, U23-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

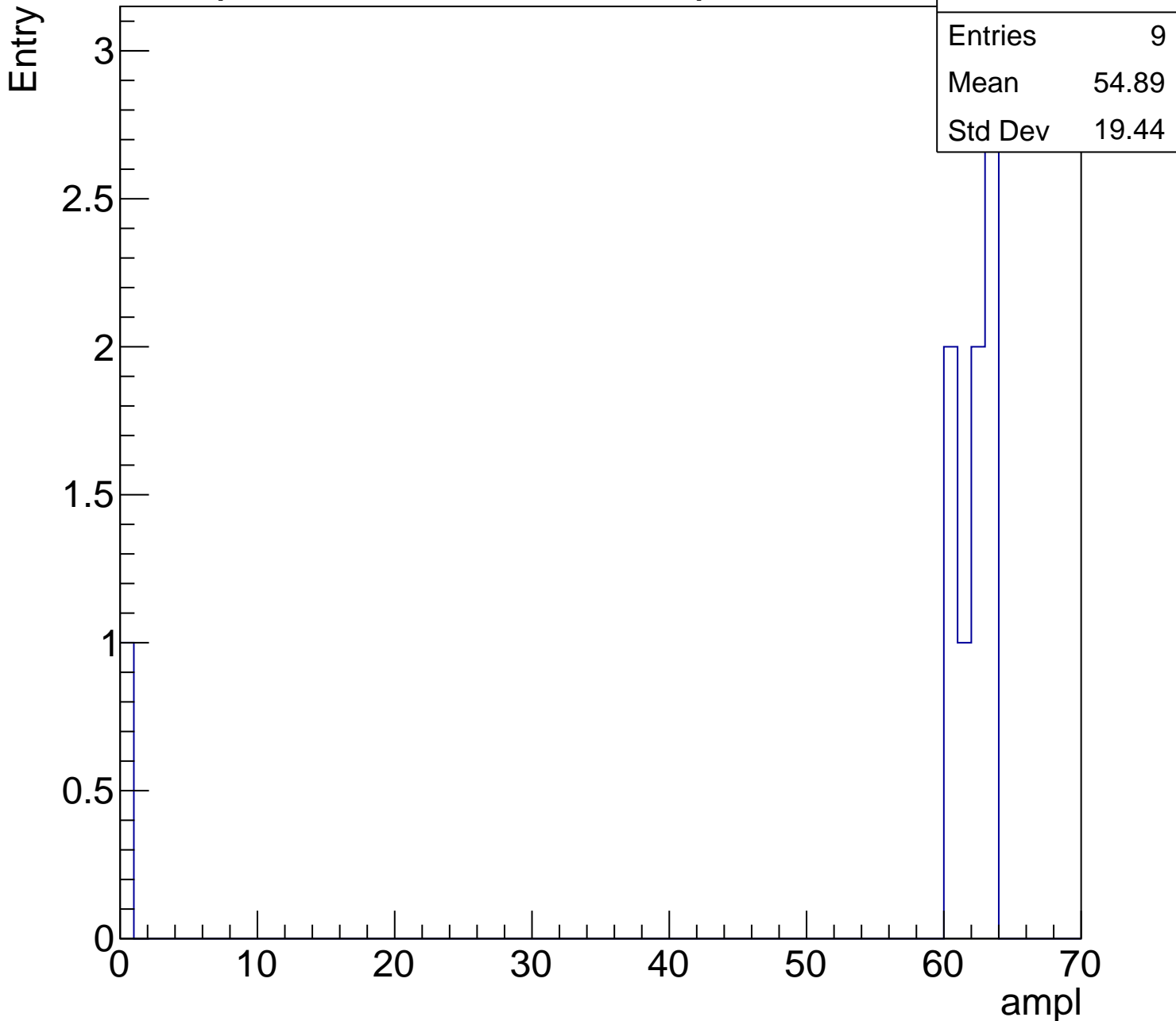
Mean

54.89

Std Dev

19.44

ampl



B1L103S, U23-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch51, adc0

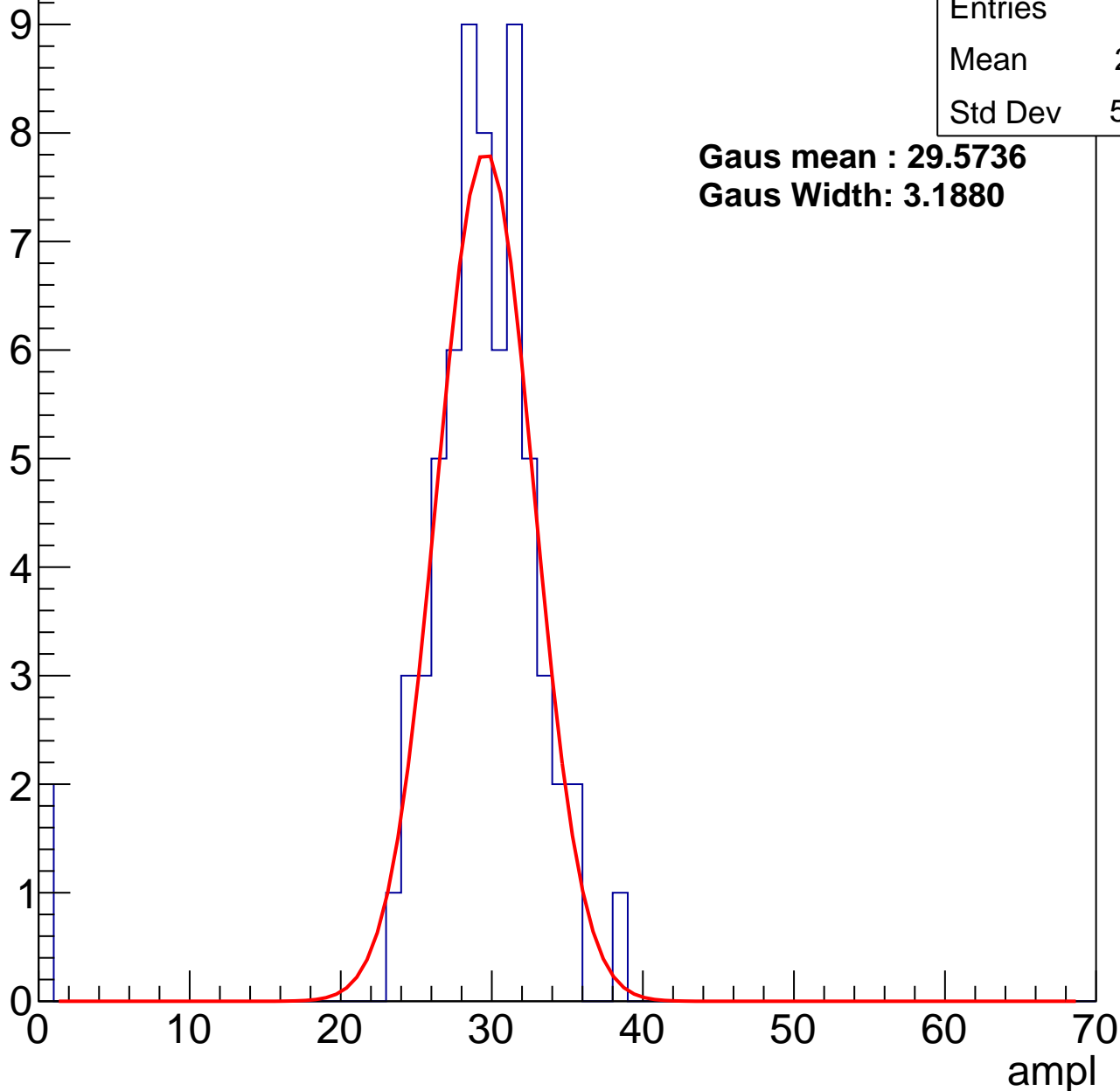
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28.31
Std Dev	5.852

Gaus mean : 29.5736

Gaus Width: 3.1880



B1L103S, U23-ch51, adc1

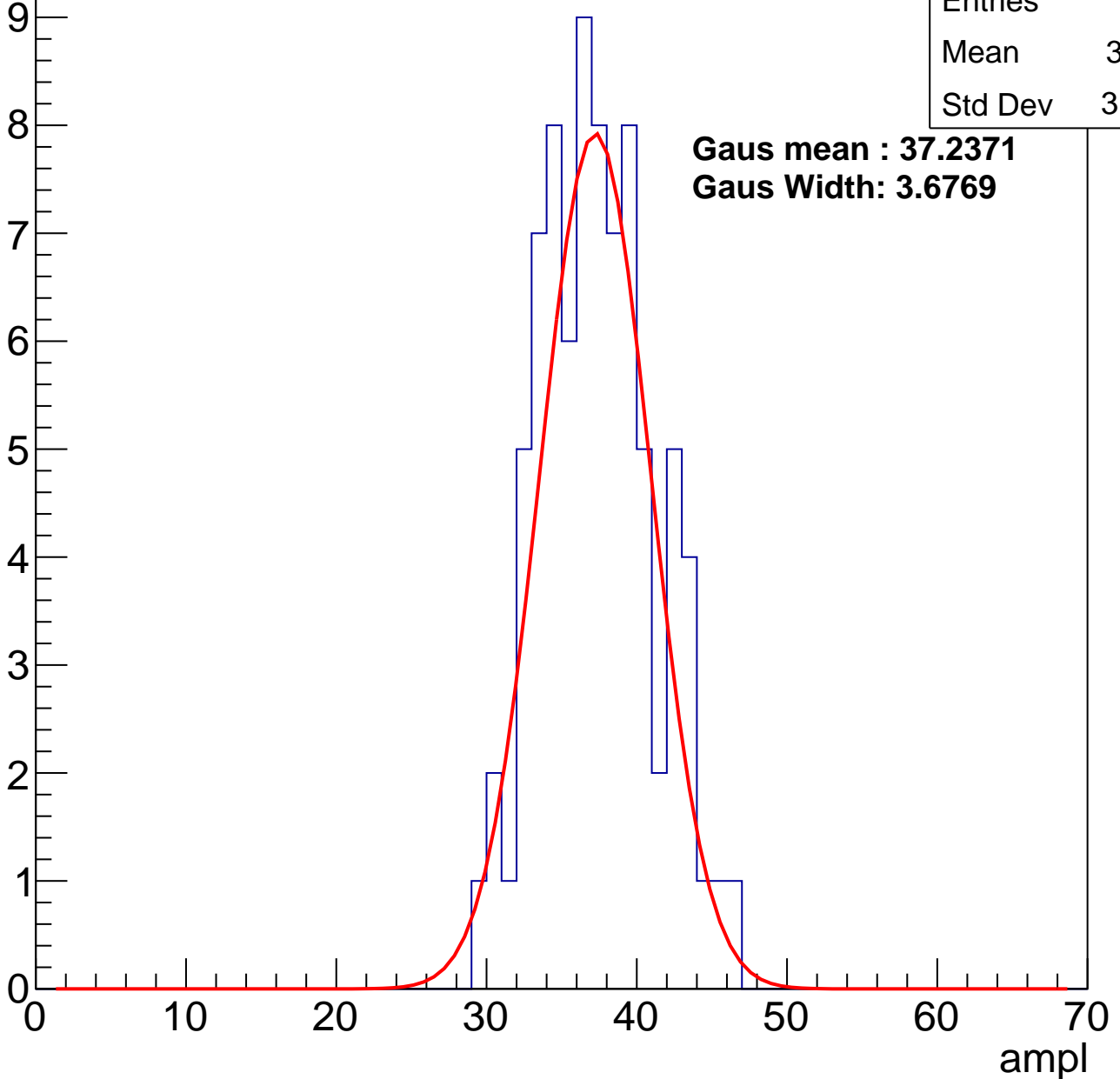
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	36.91
Std Dev	3.716

Gaus mean : 37.2371

Gaus Width: 3.6769

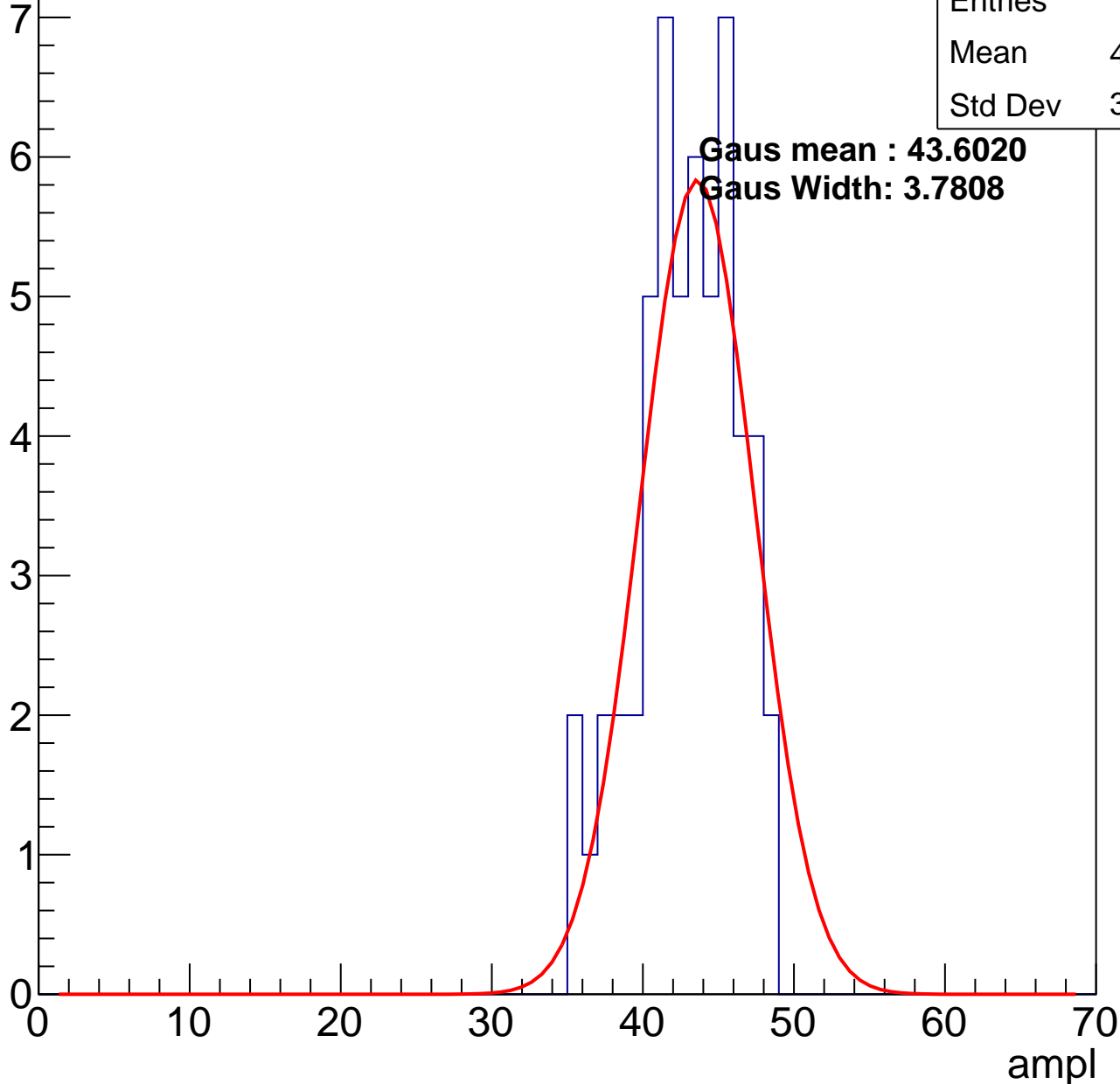


B1L103S, U23-ch51, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	42.44
Std Dev	3.264

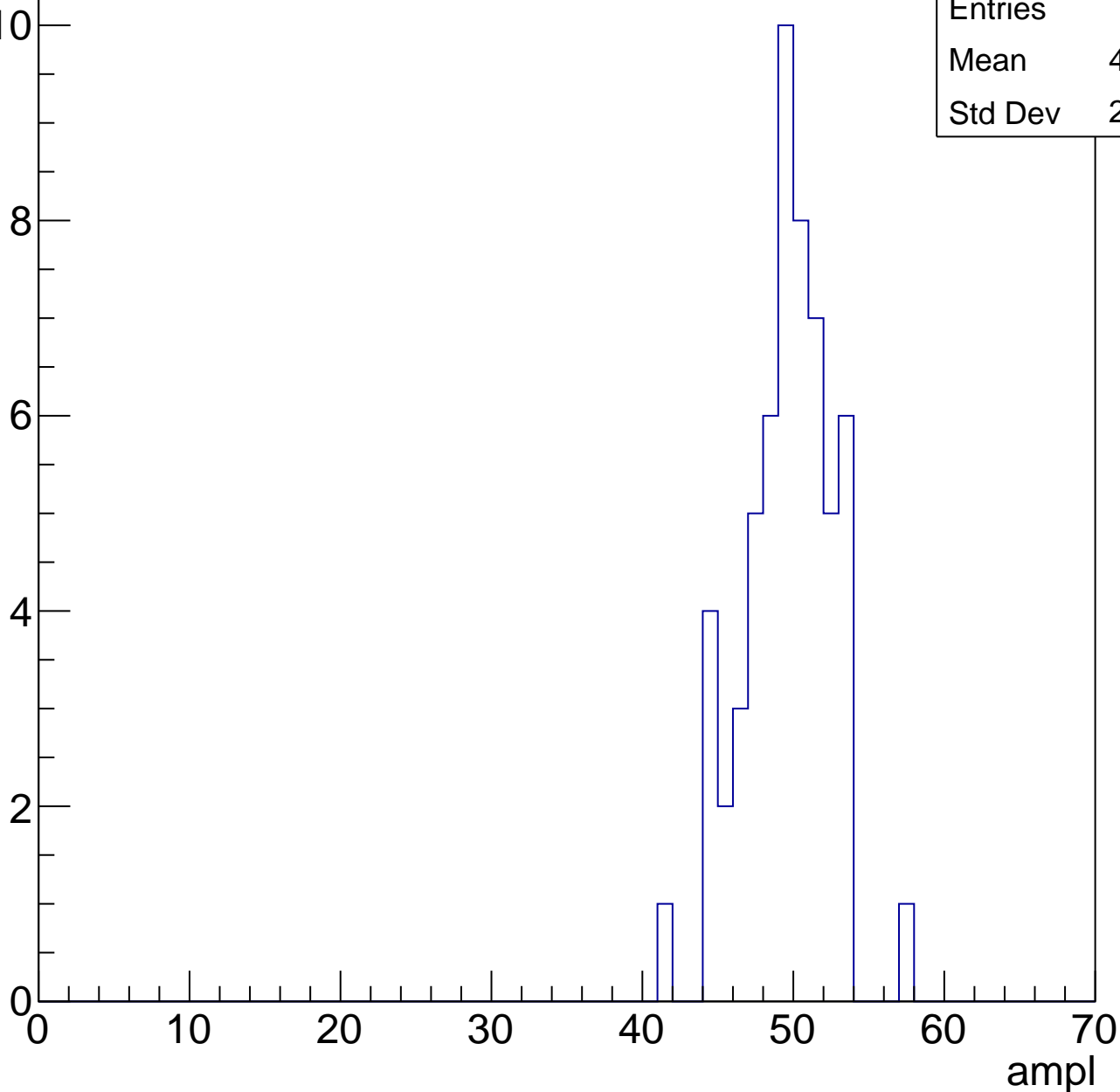


B1L103S, U23-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.14
Std Dev	2.903

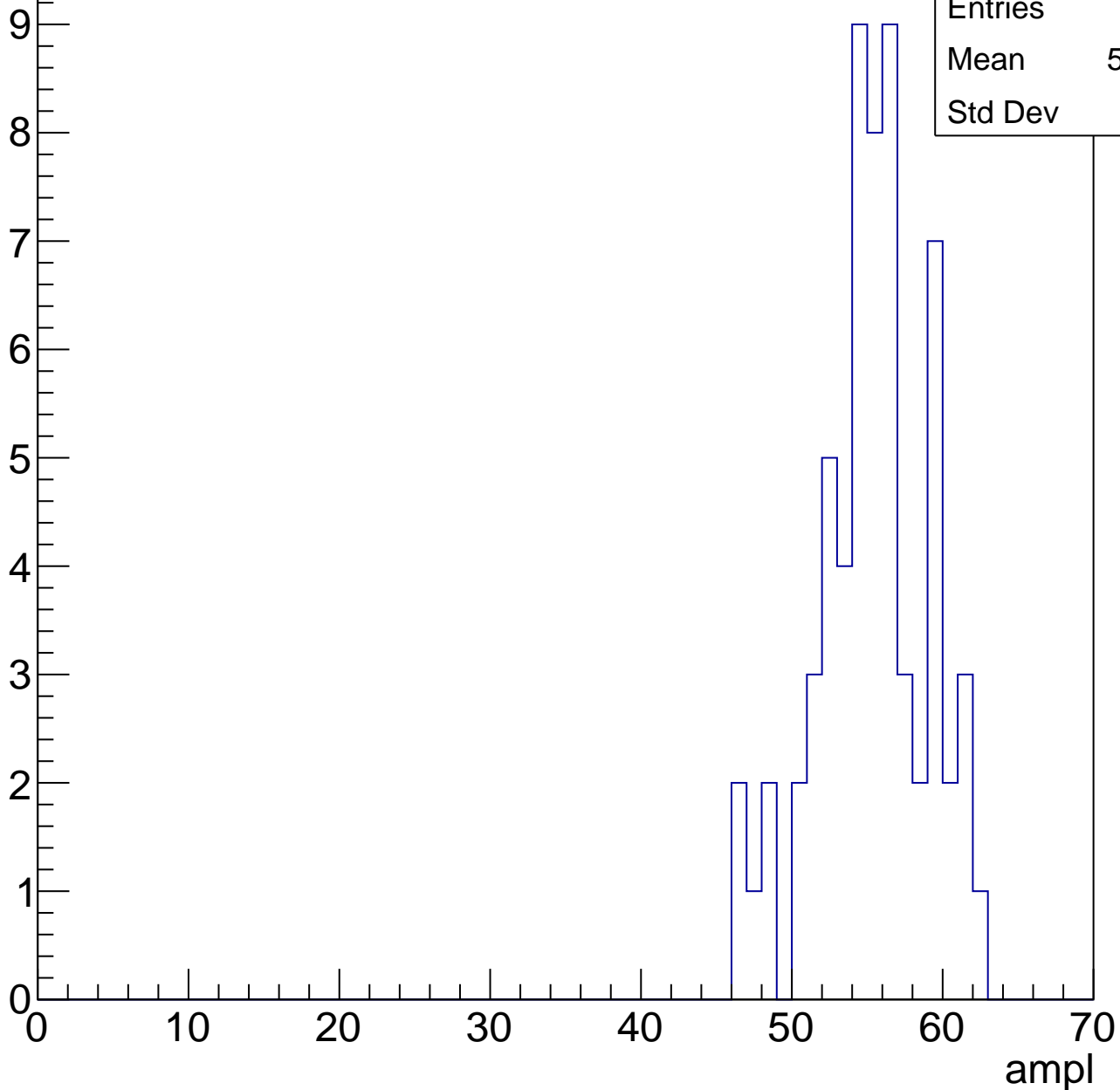


B1L103S, U23-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	54.84
Std Dev	3.67

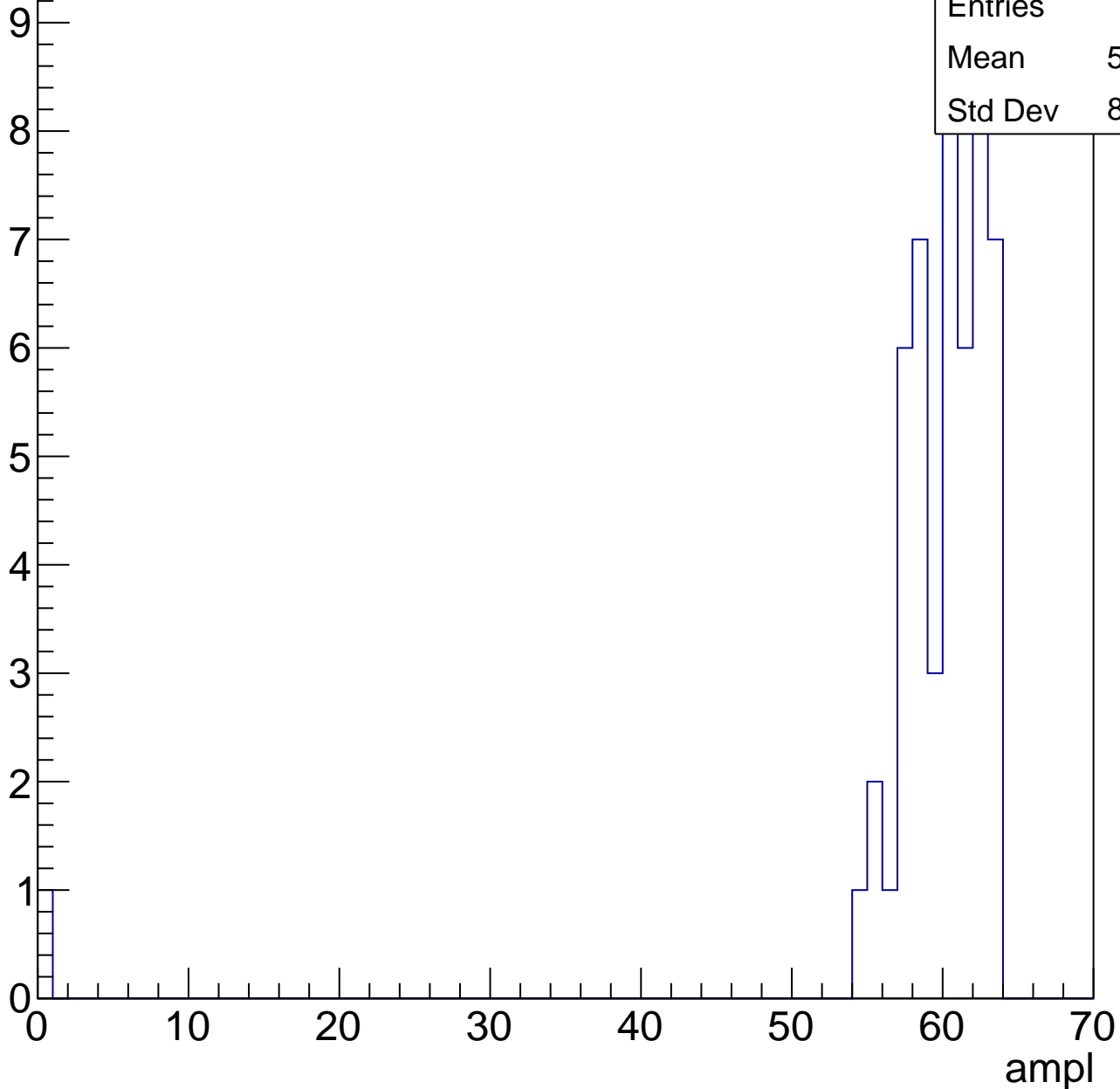


B1L103S, U23-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

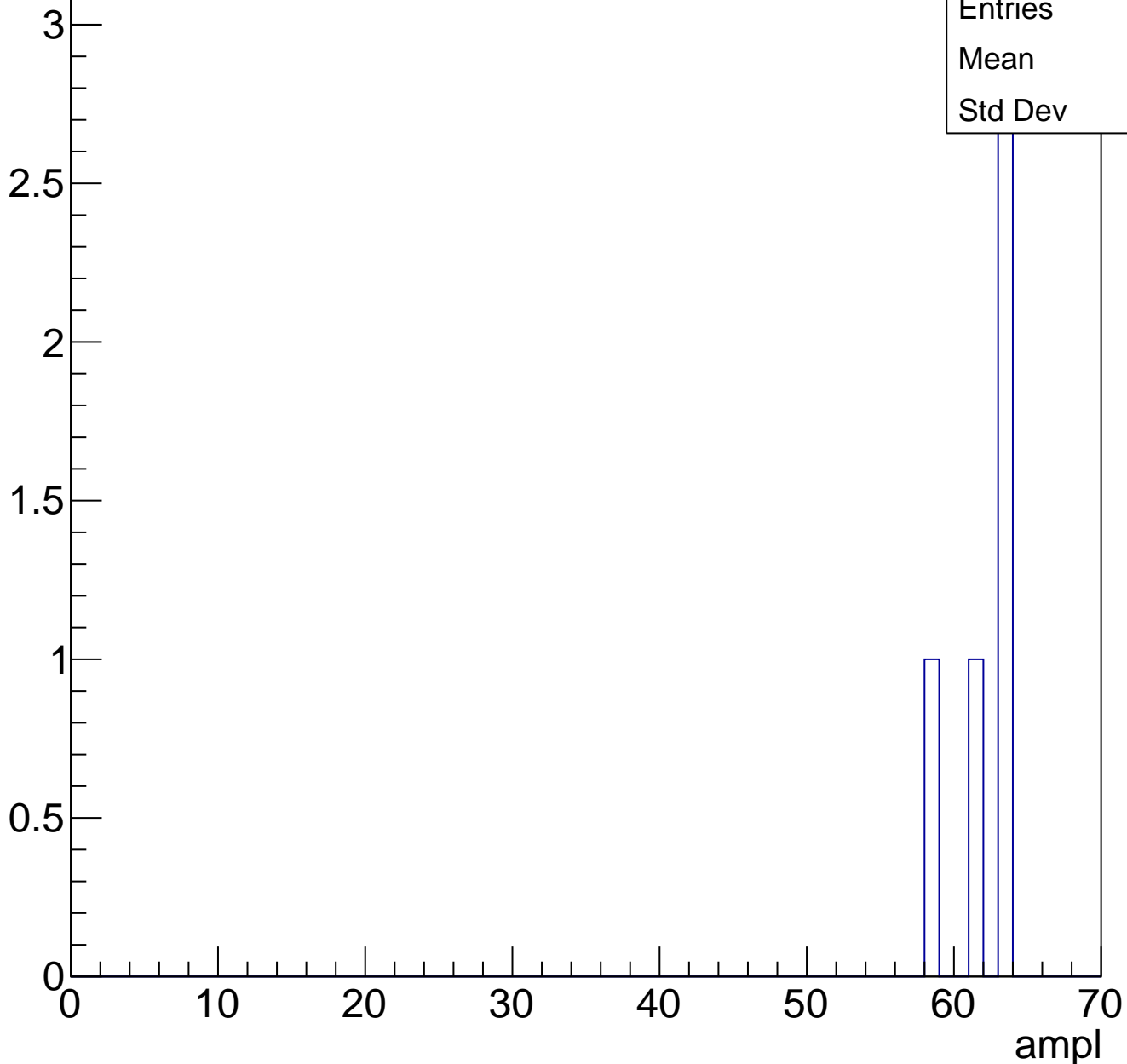
Entries	51
Mean	58.63
Std Dev	8.627



B1L103S, U23-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch52, adc0

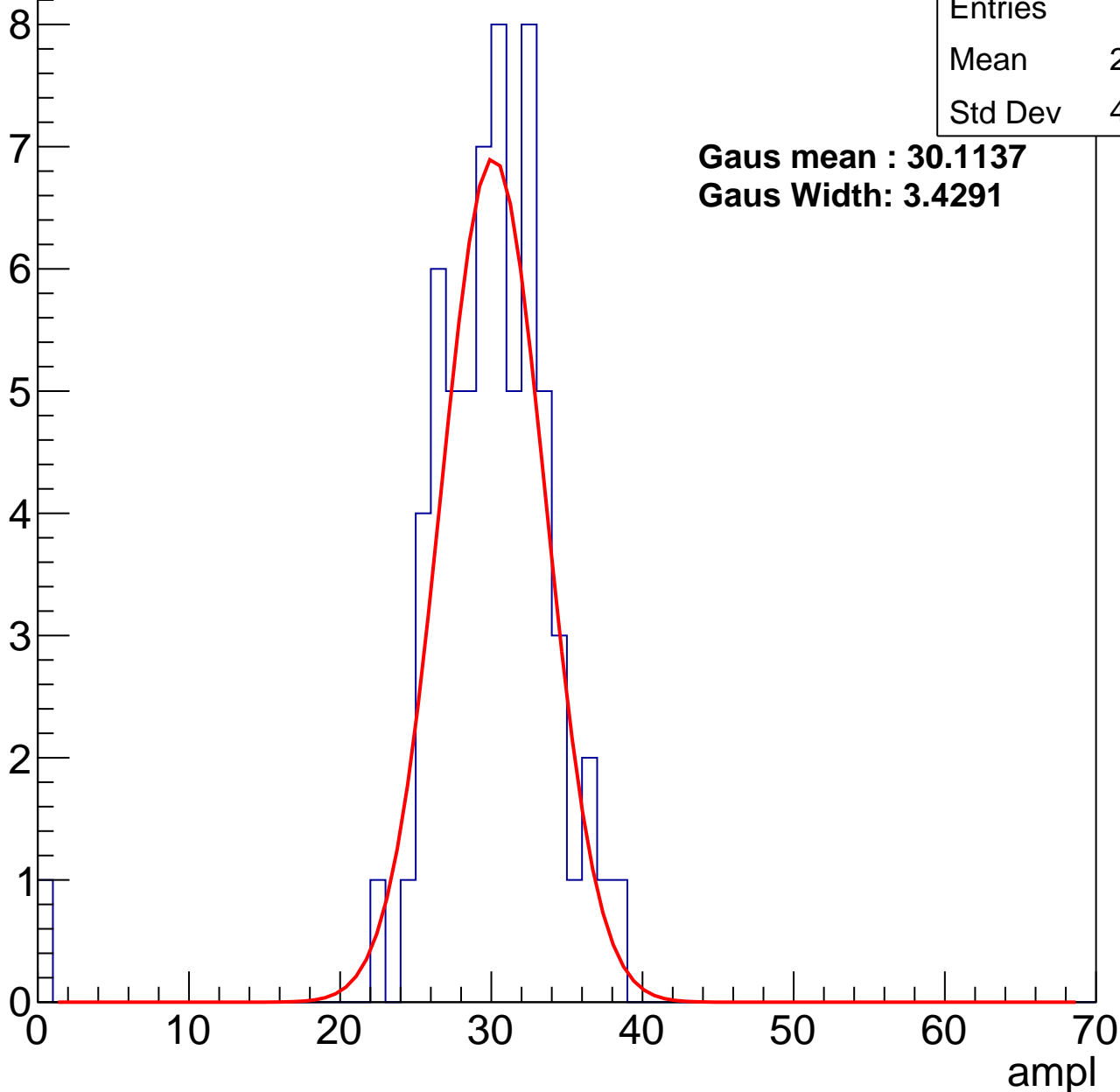
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	29.38
Std Dev	4.973

Gaus mean : 30.1137

Gaus Width: 3.4291



B1L103S, U23-ch52, adc1

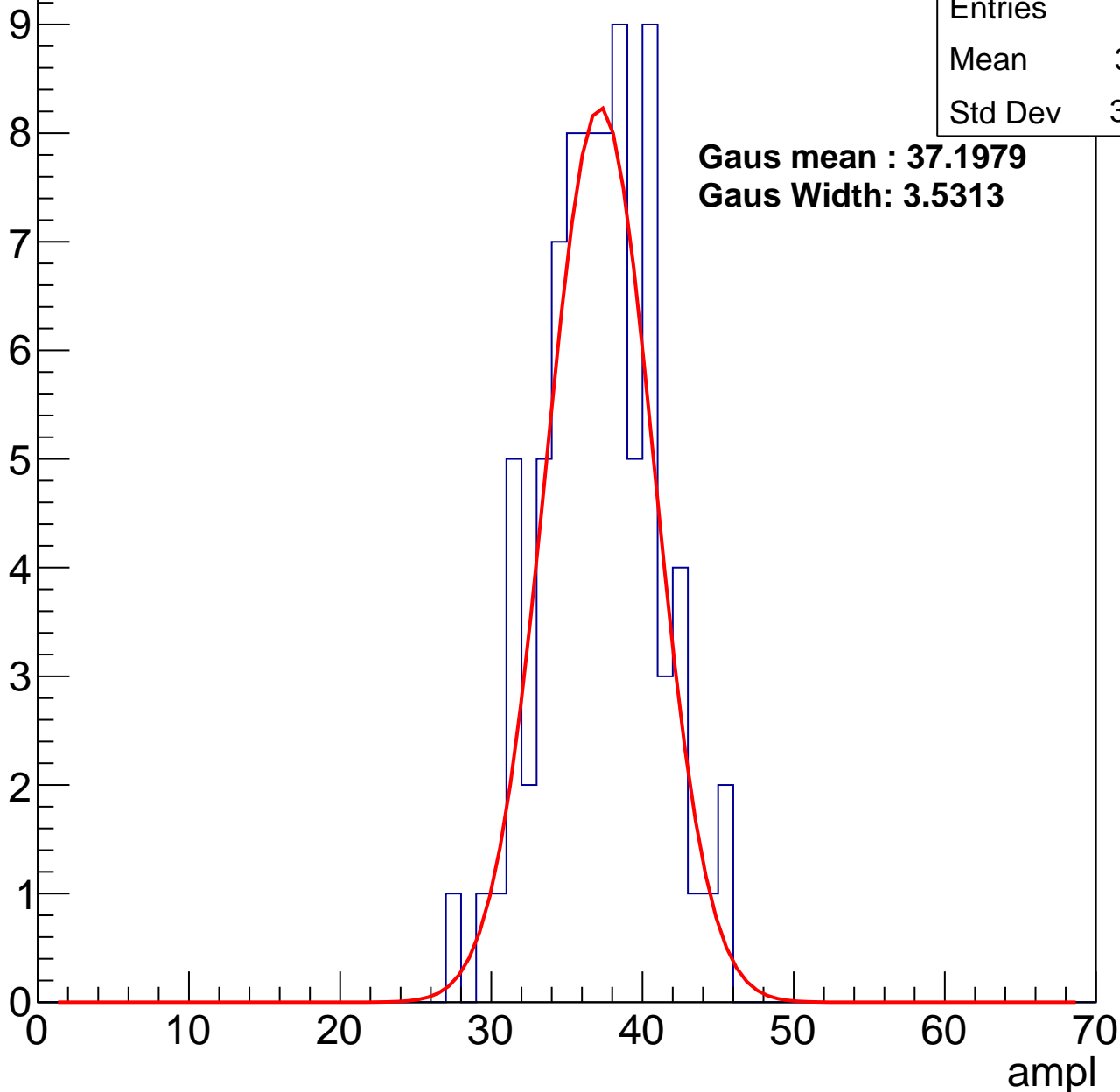
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	36.71
Std Dev	3.692

Gaus mean : 37.1979

Gaus Width: 3.5313



B1L103S, U23-ch52, adc2

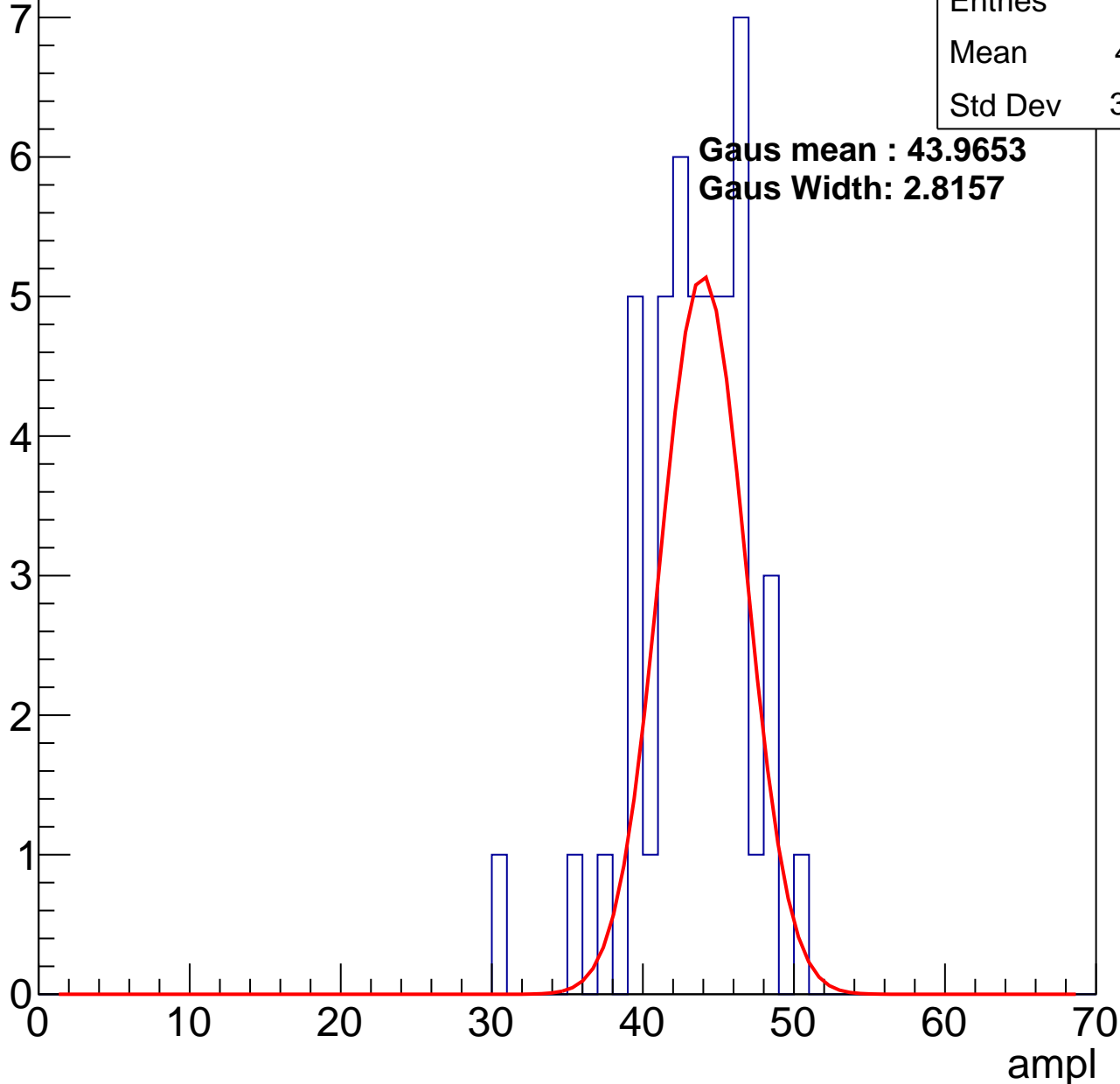
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	42.91
Std Dev	3.607

Gaus mean : 43.9653

Gaus Width: 2.8157

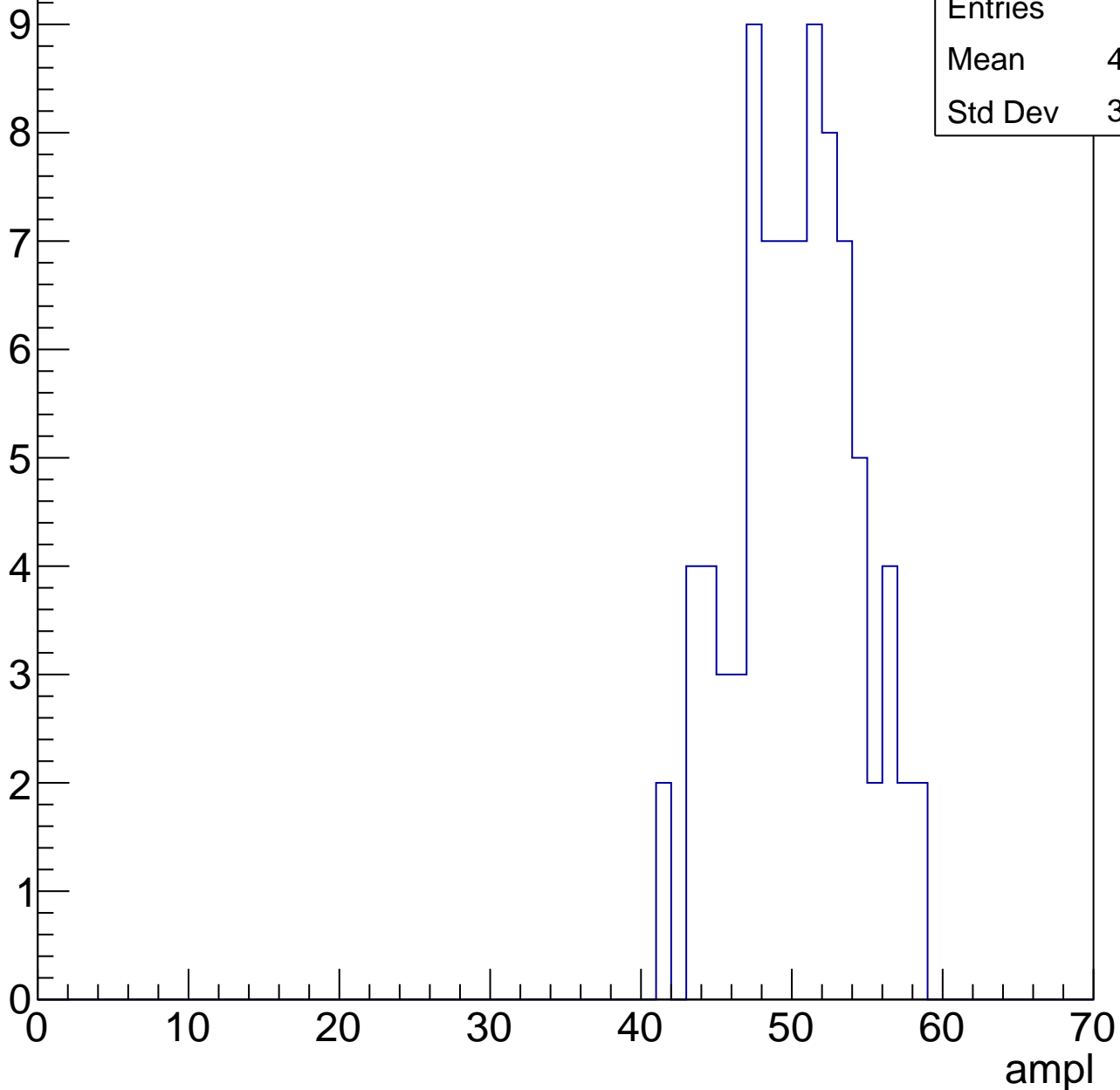


B1L103S, U23-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	49.82
Std Dev	3.974



B1L103S, U23-ch52, adc4

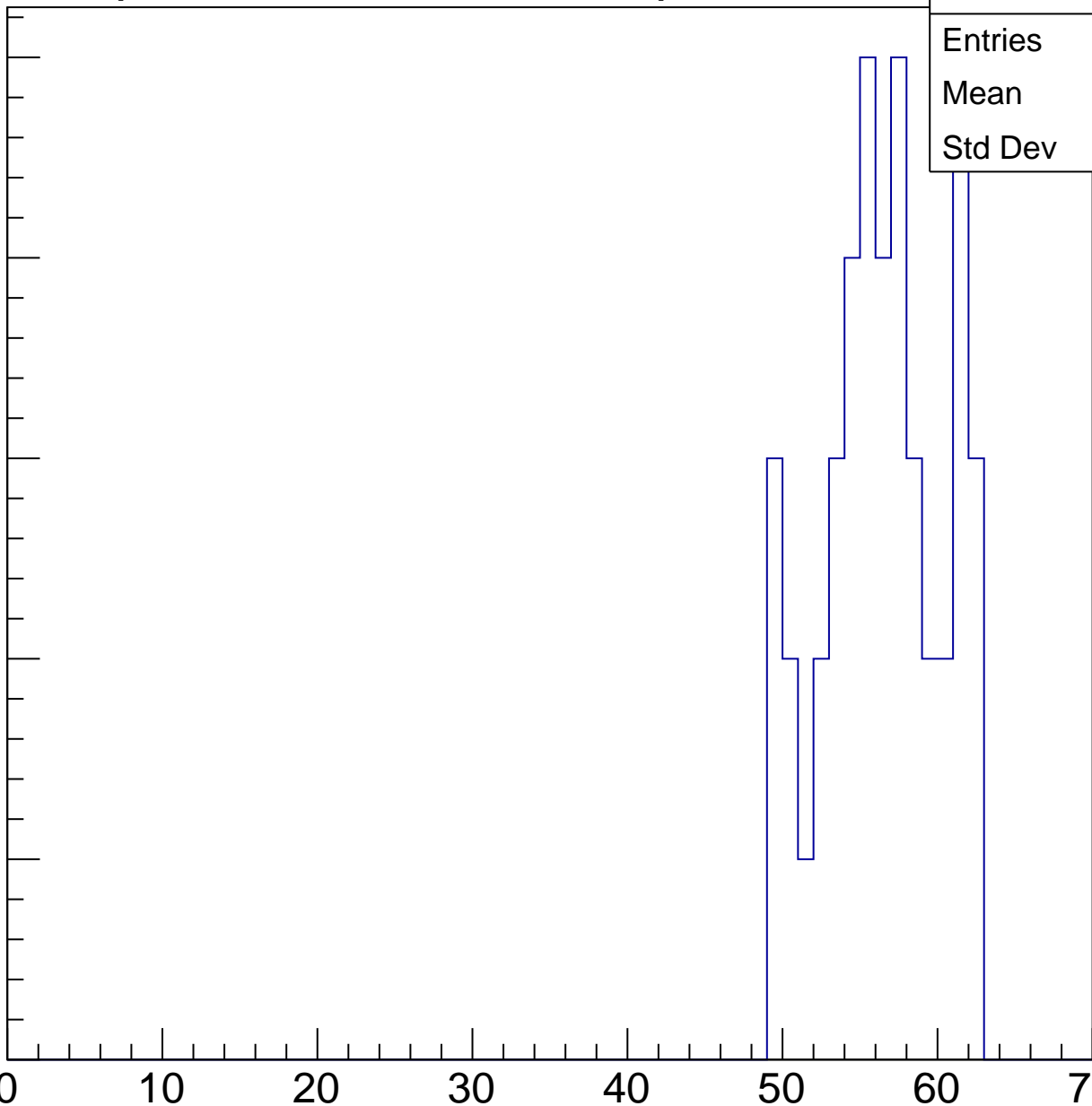
calib_packv5_042523_0143.root, FC#7, port C2

Entry

5
4
3
2
1
0

Entries	44
Mean	56
Std Dev	3.766

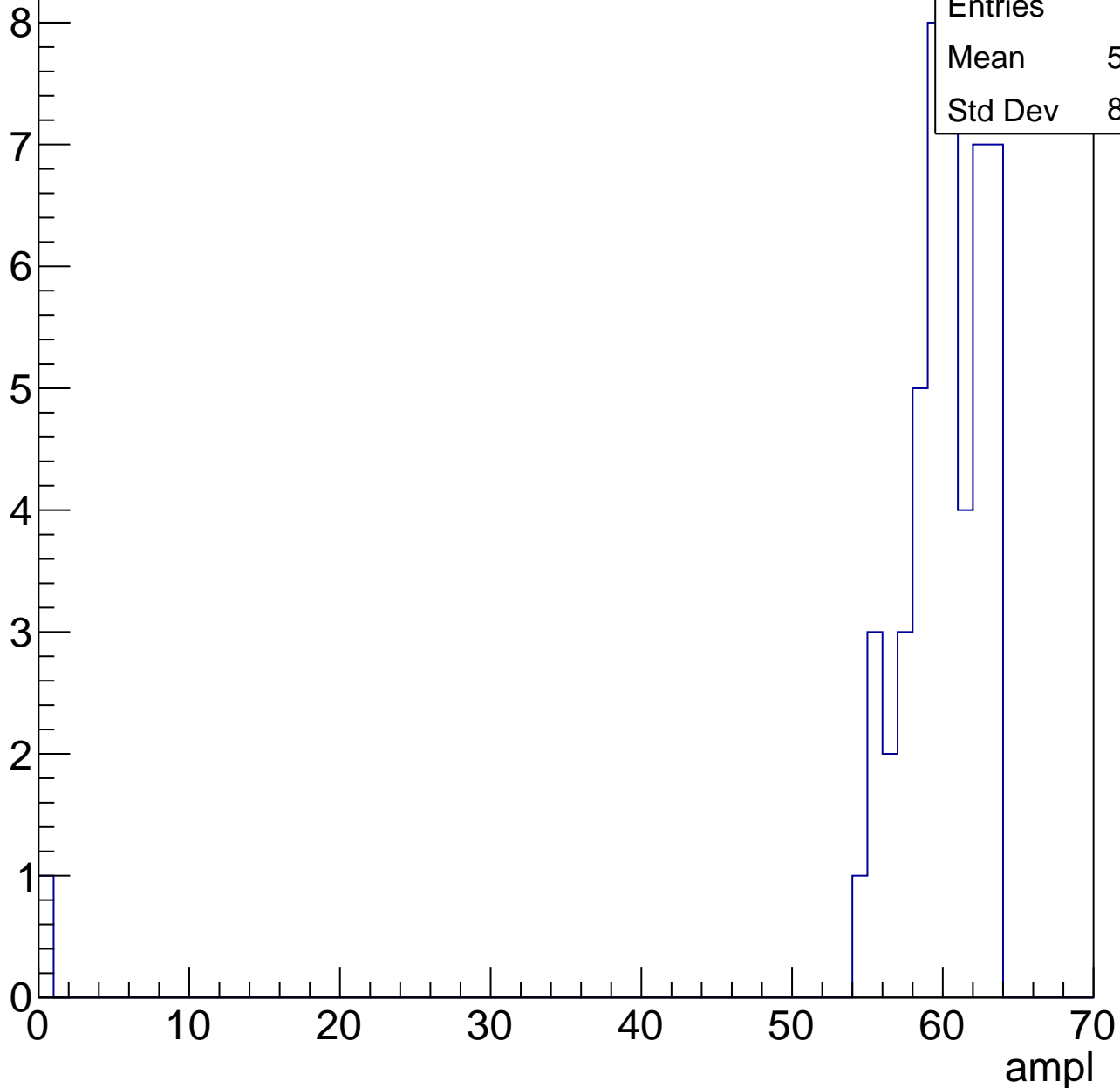
ampl



B1L103S, U23-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch53, adc0

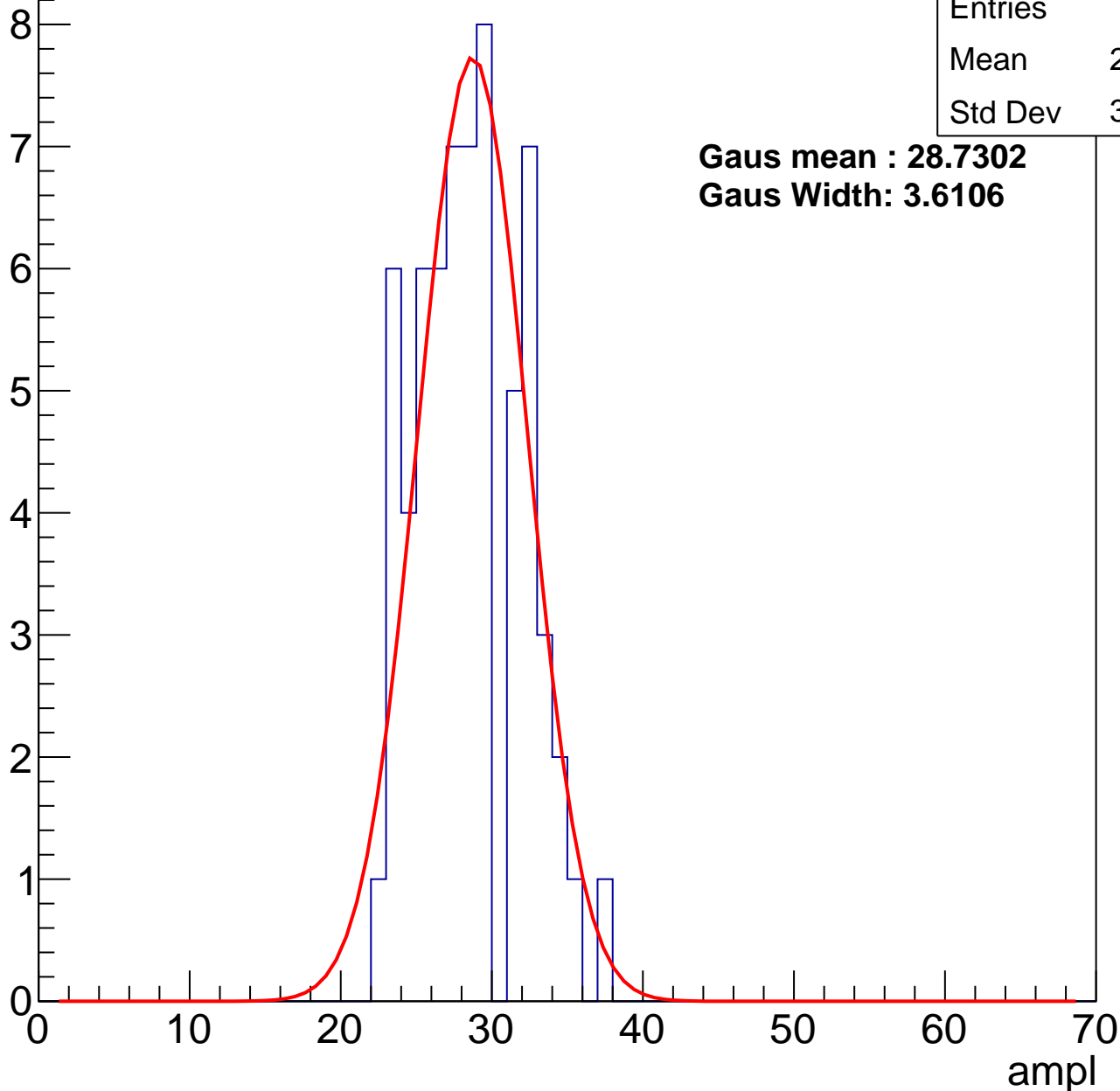
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.08
Std Dev	3.483

Gaus mean : 28.7302

Gaus Width: 3.6106



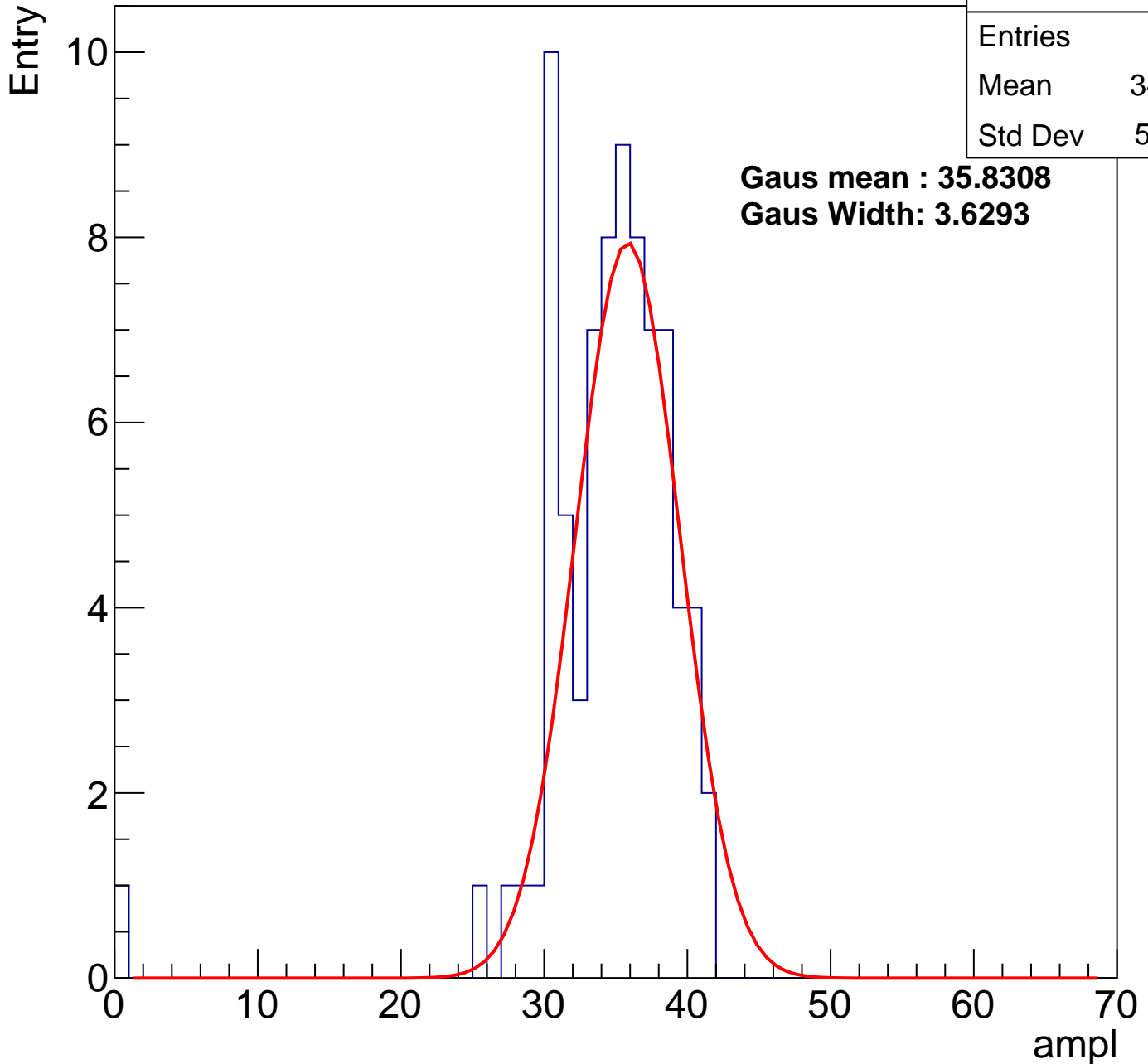
B1L103S, U23-ch53, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	34.04
Std Dev	5.191

Gaus mean : 35.8308

Gaus Width: 3.6293



B1L103S, U23-ch53, adc2

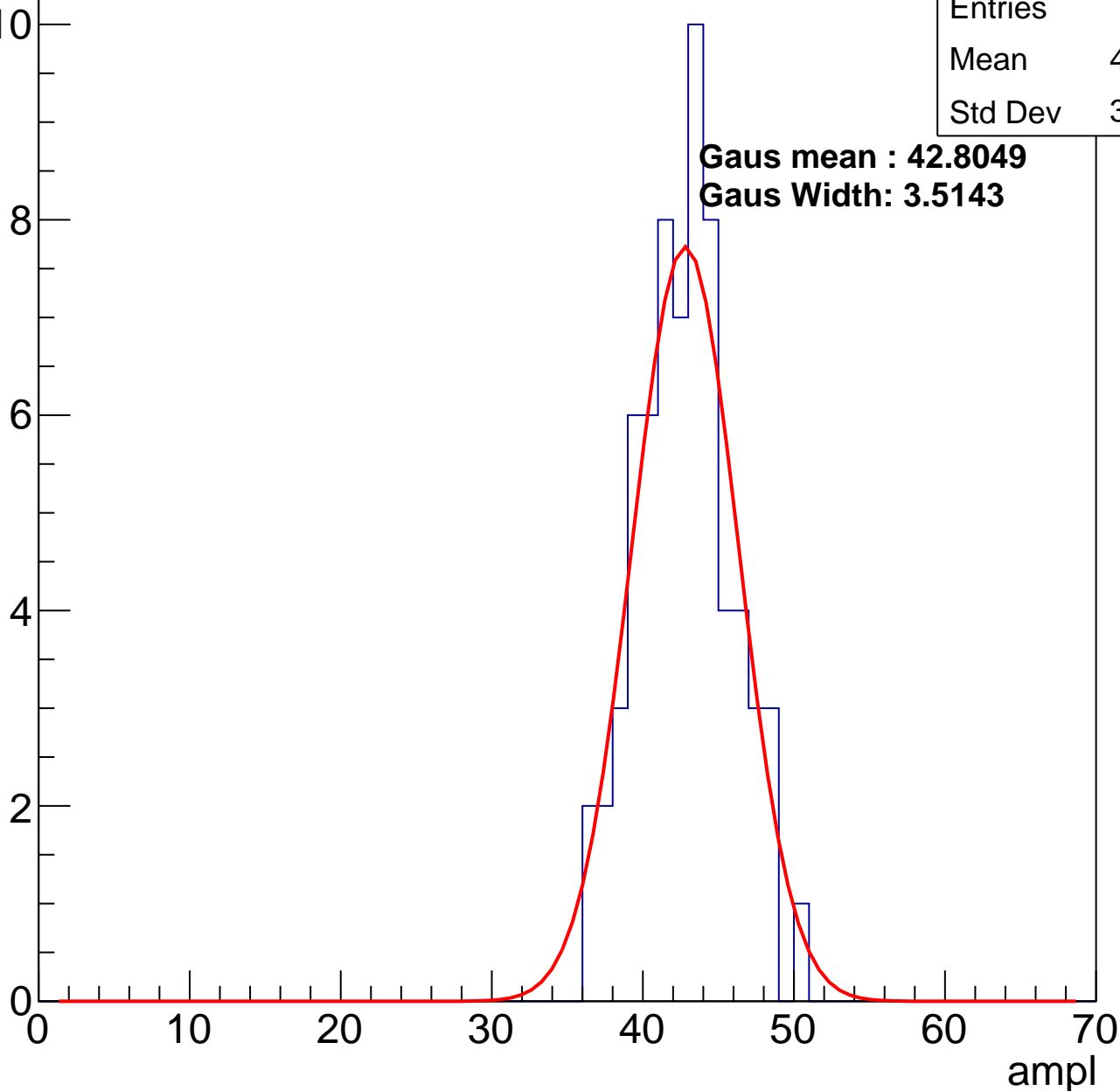
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.34
Std Dev	3.084

Gaus mean : 42.8049

Gaus Width: 3.5143



B1L103S, U23-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	57
Mean	48.33
Std Dev	2.981

Entry

10

8

6

4

2

0

0

10

20

30

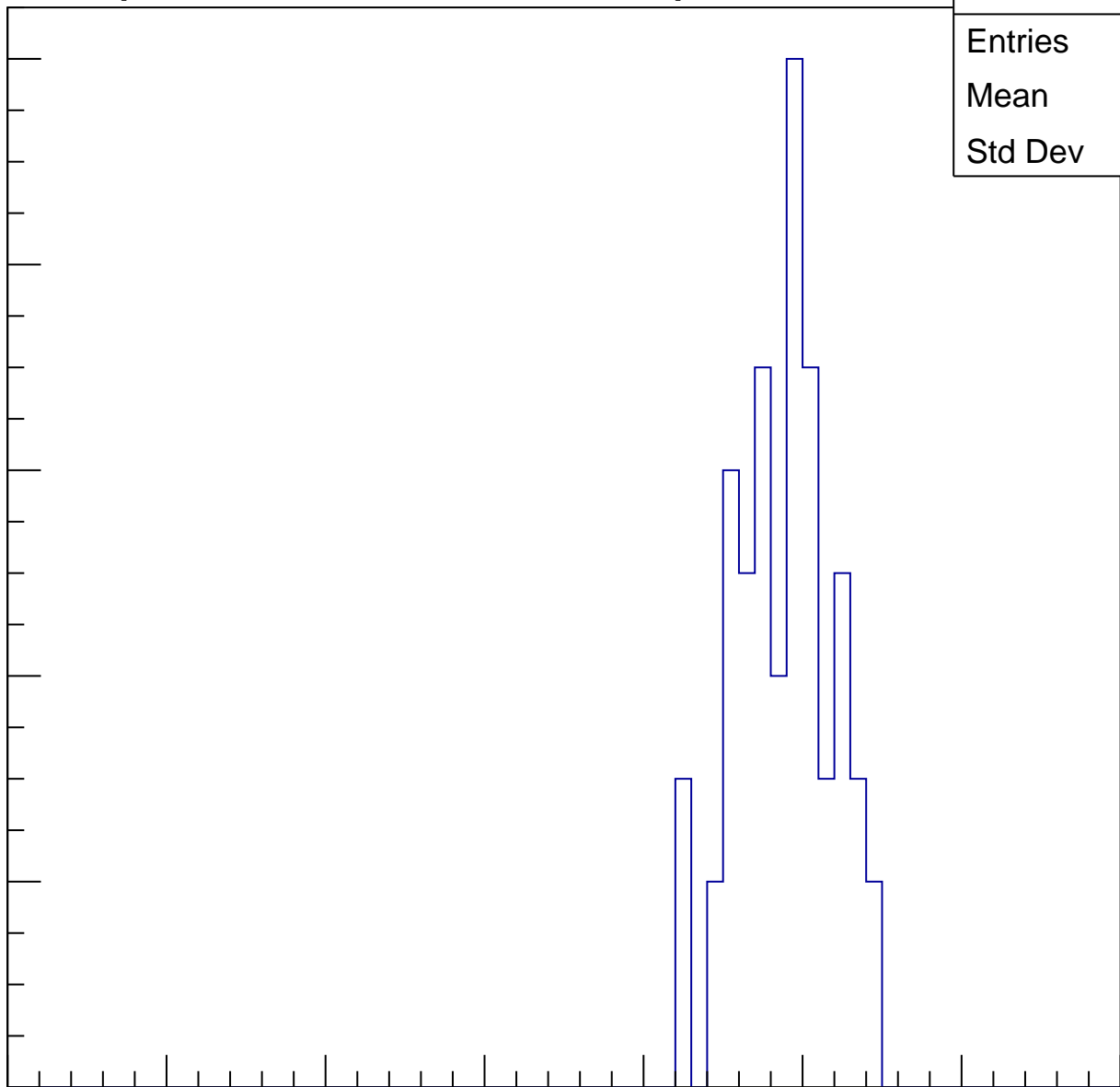
40

50

60

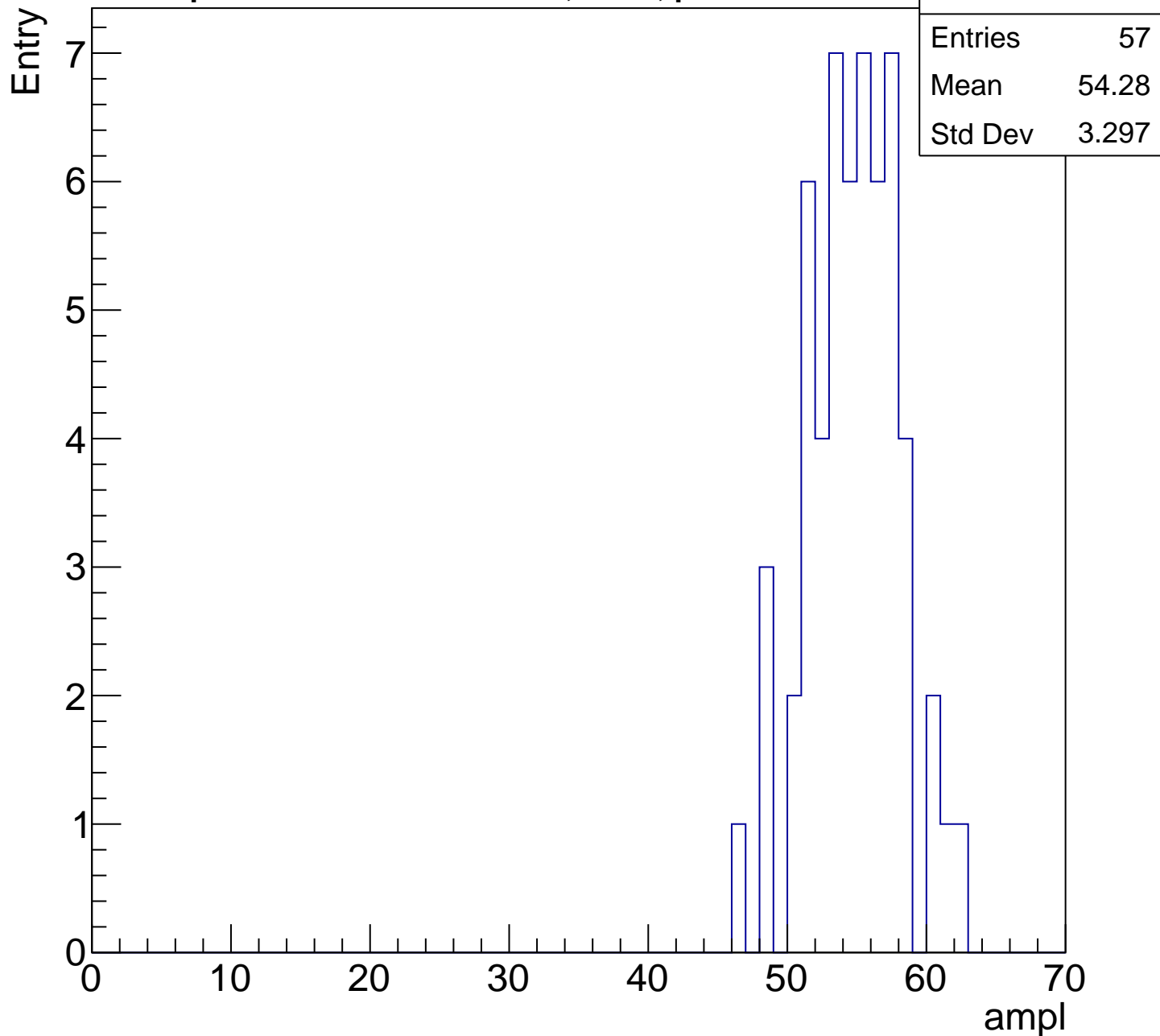
70

ampl



B1L103S, U23-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U23-ch53, adc5

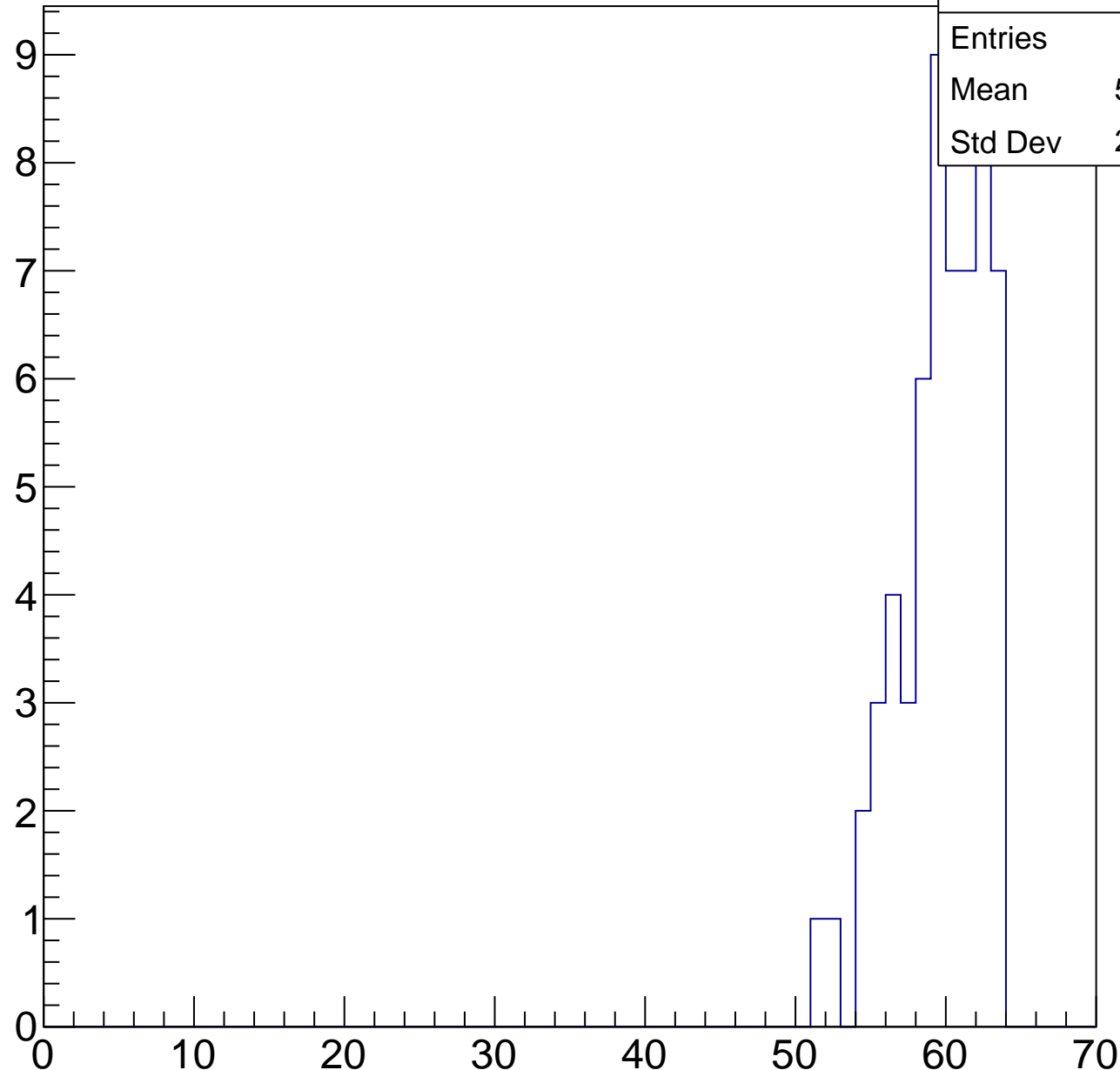
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	59.21
Std Dev	2.881

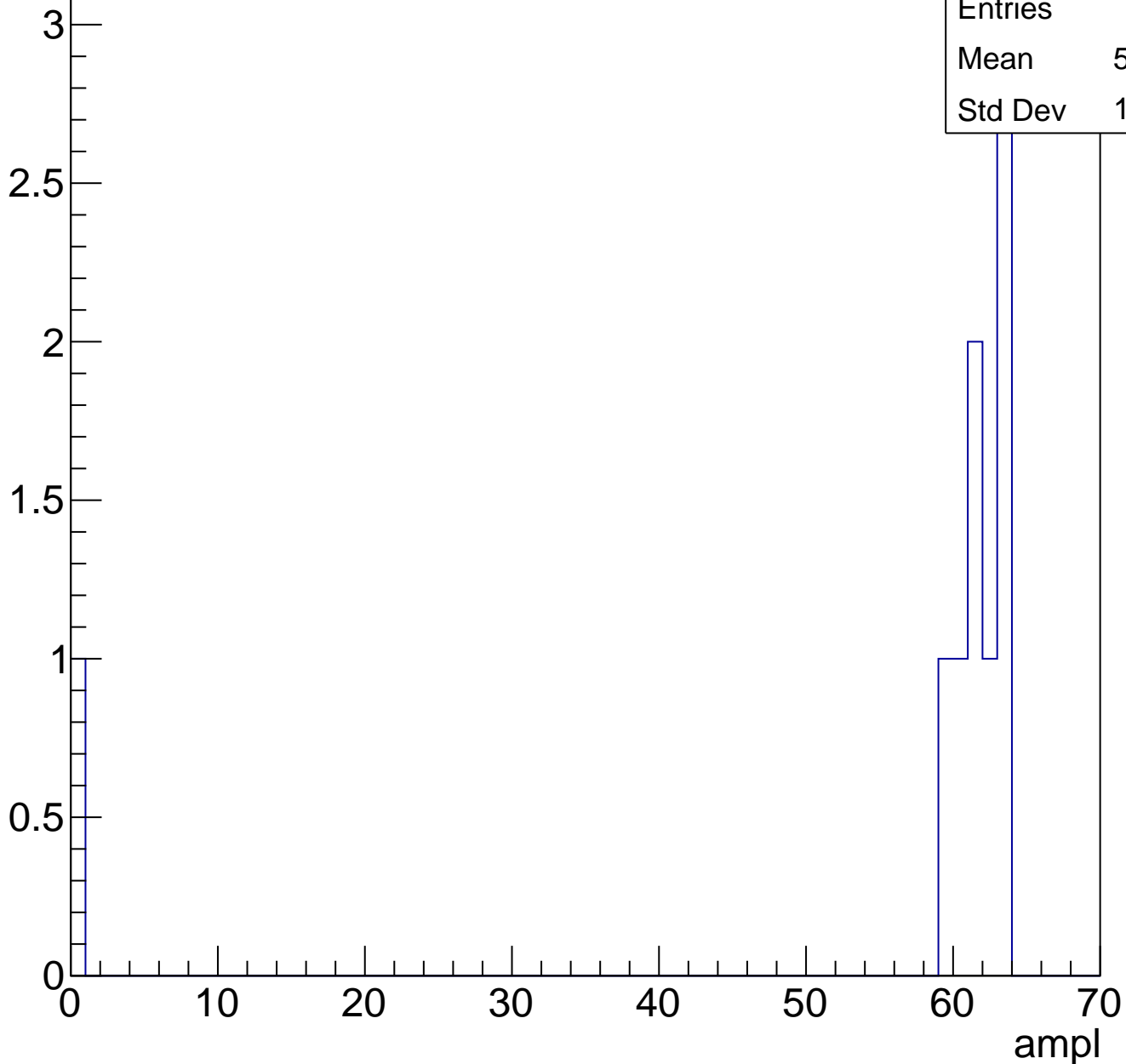
ampl



B1L103S, U23-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch54, adc0

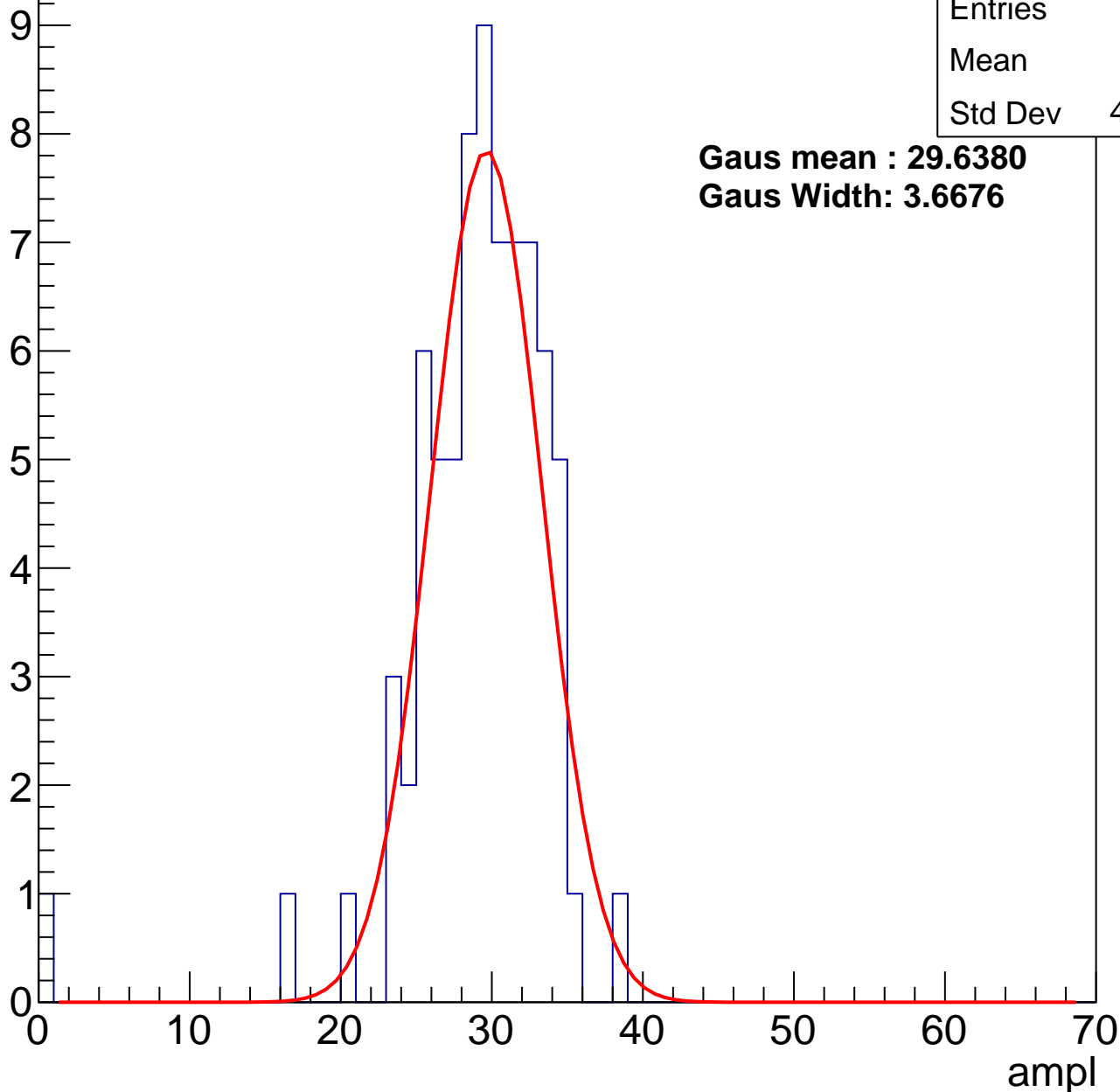
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.6
Std Dev	4.956

Gaus mean : 29.6380

Gaus Width: 3.6676



B1L103S, U23-ch54, adc1

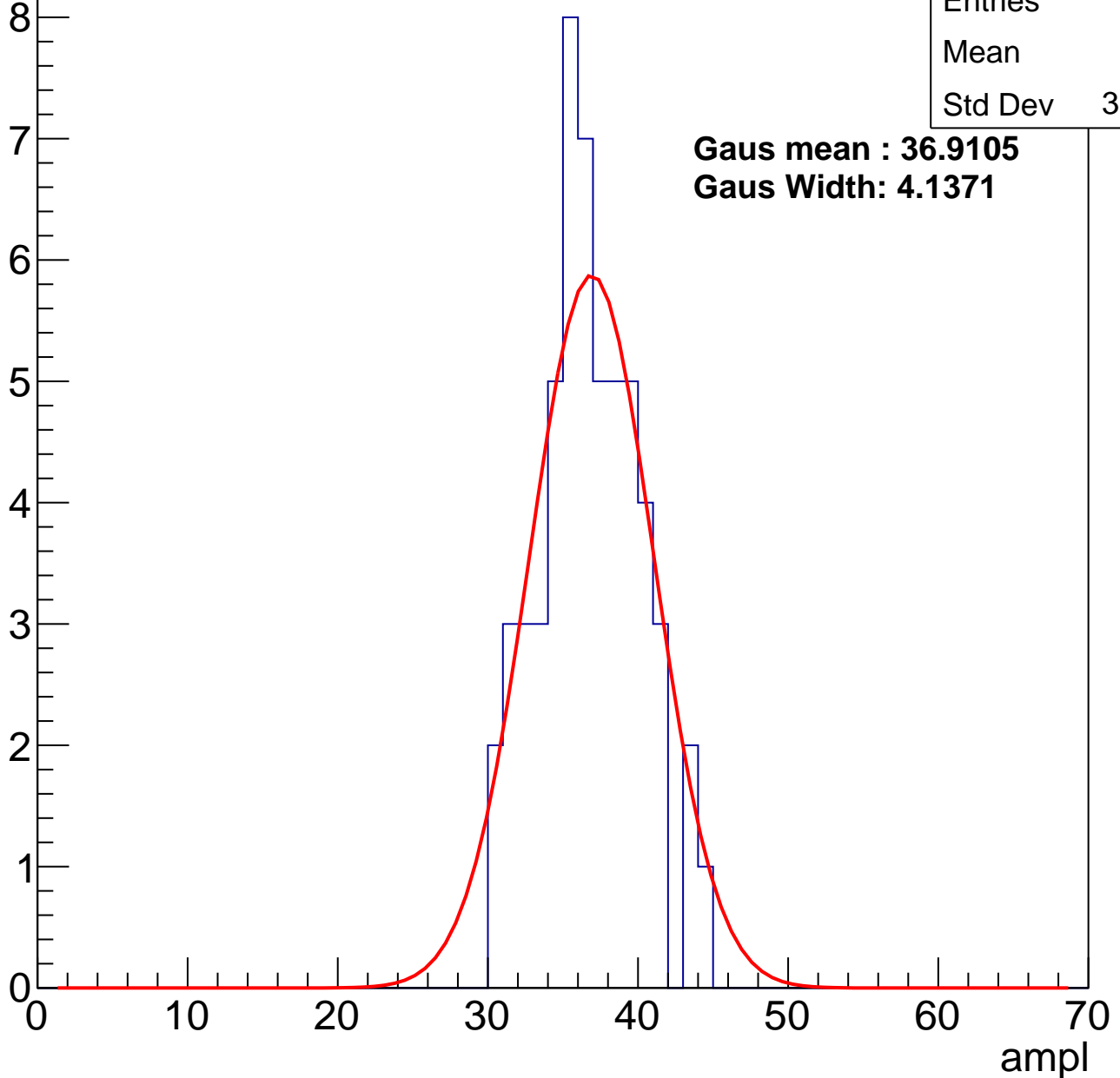
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	36.3
Std Dev	3.316

Gaus mean : 36.9105

Gaus Width: 4.1371



B1L103S, U23-ch54, adc2

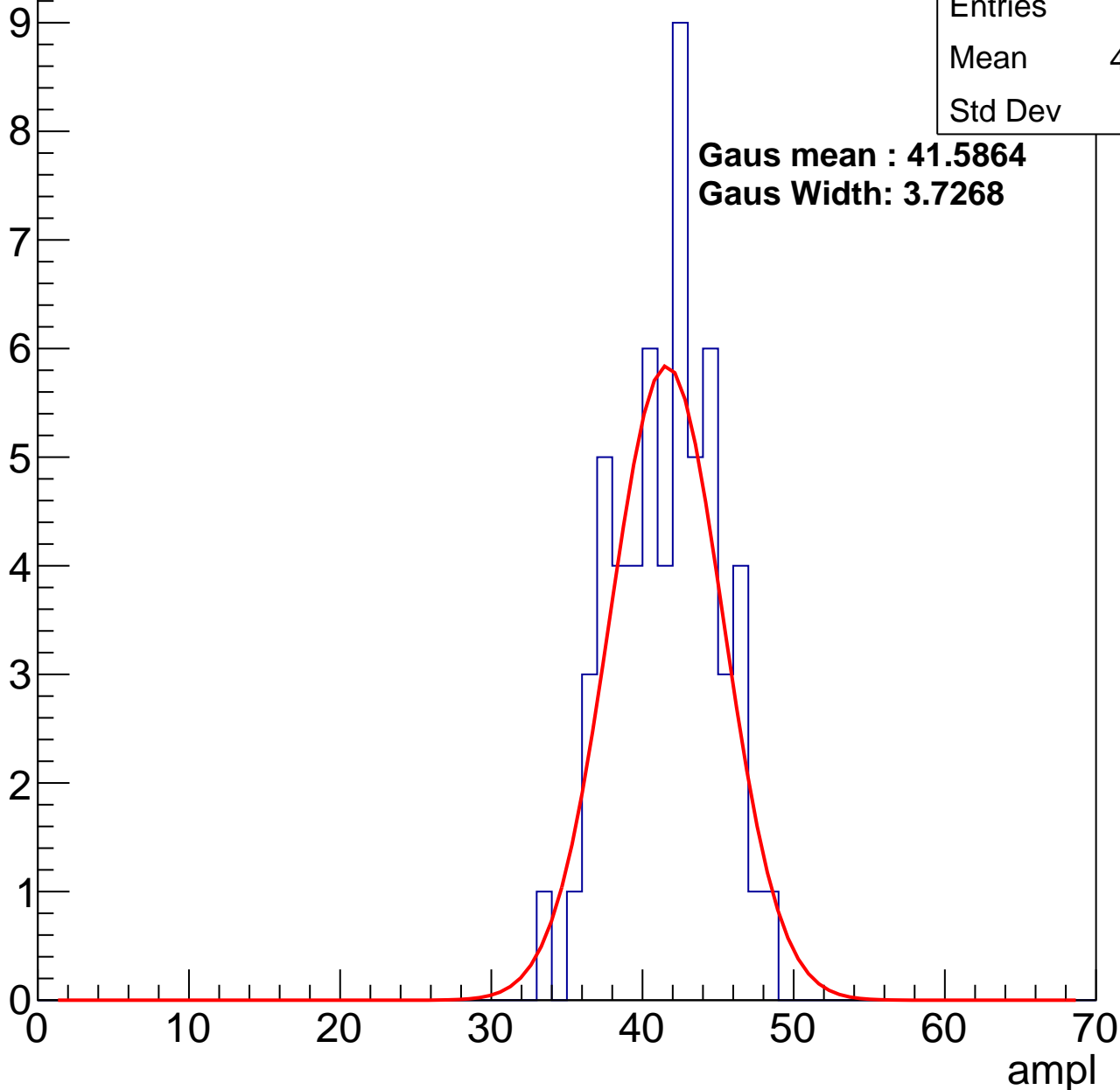
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.12
Std Dev	3.32

Gaus mean : 41.5864

Gaus Width: 3.7268

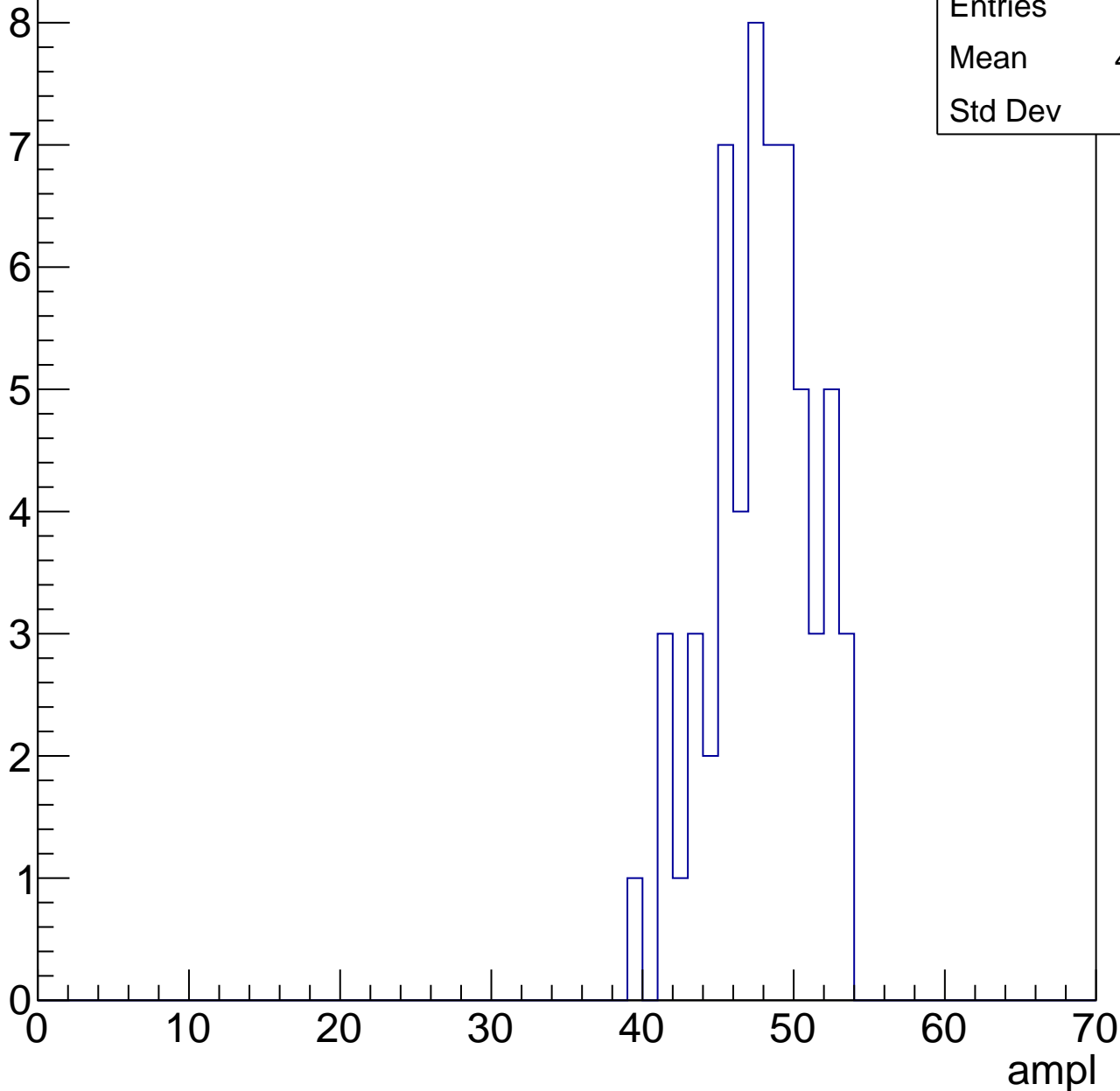


B1L103S, U23-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	47.41
Std Dev	3.31

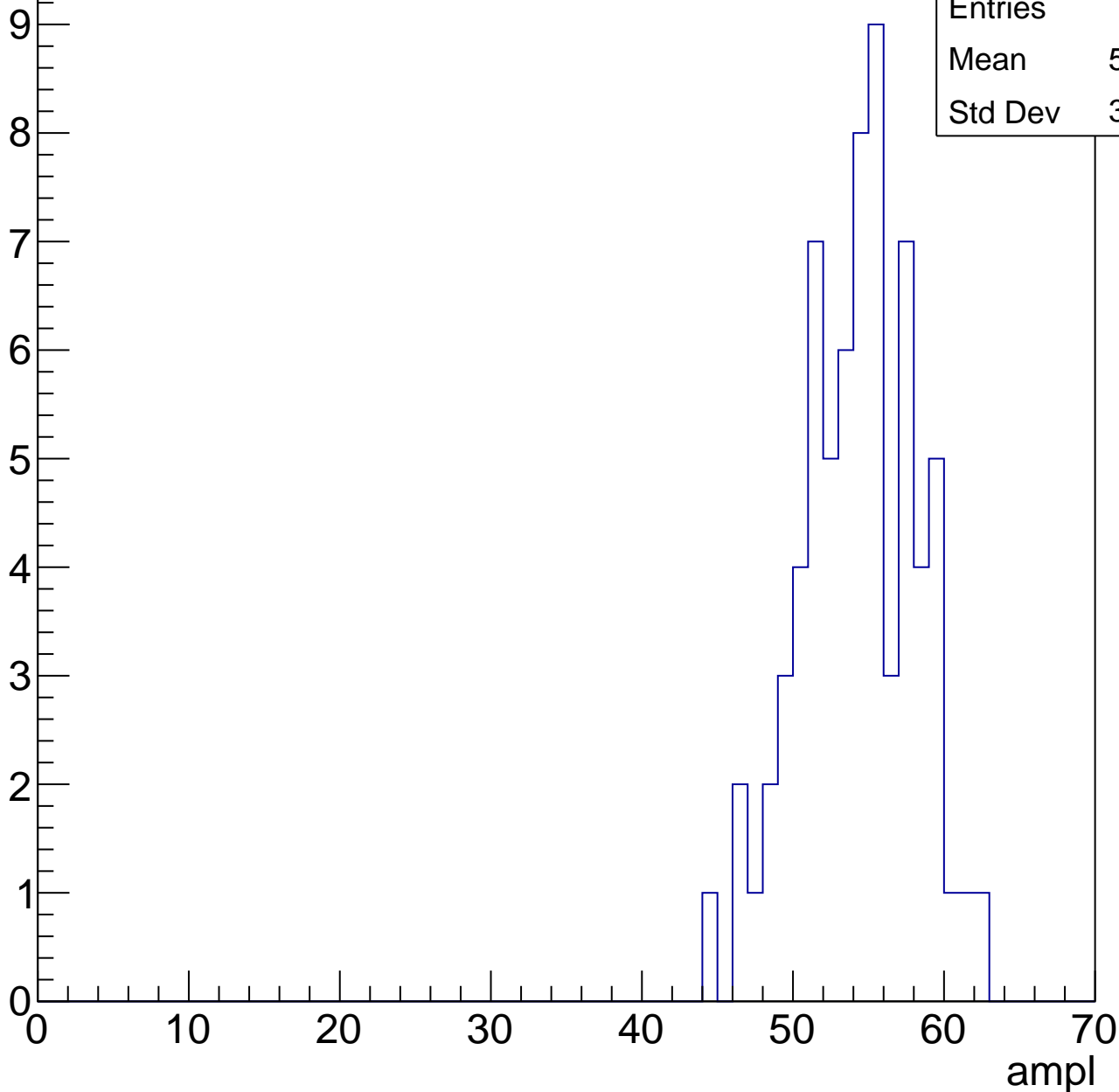


B1L103S, U23-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	53.79
Std Dev	3.772



B1L103S, U23-ch54, adc5

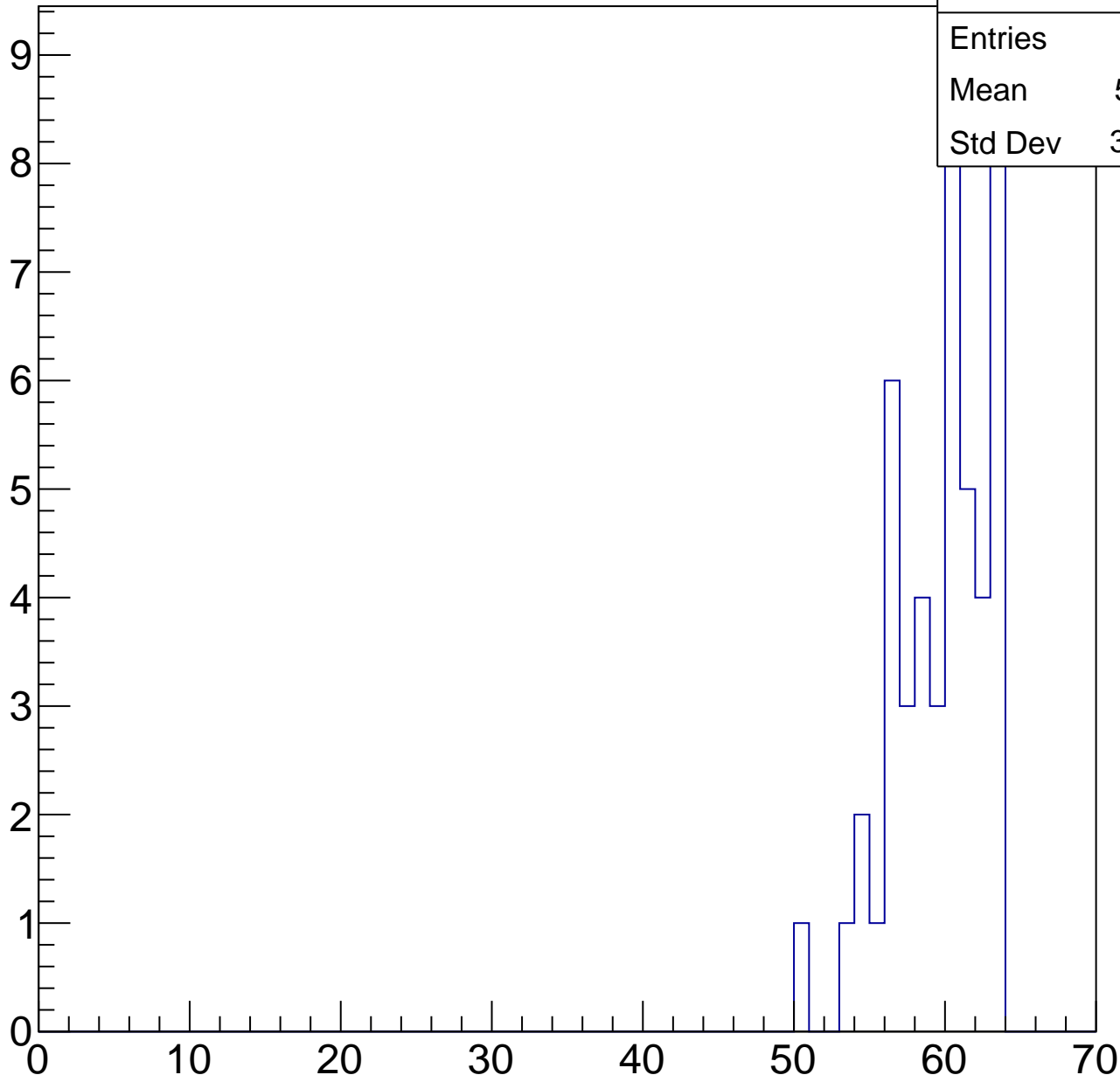
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.21
Std Dev	3.095

ampl

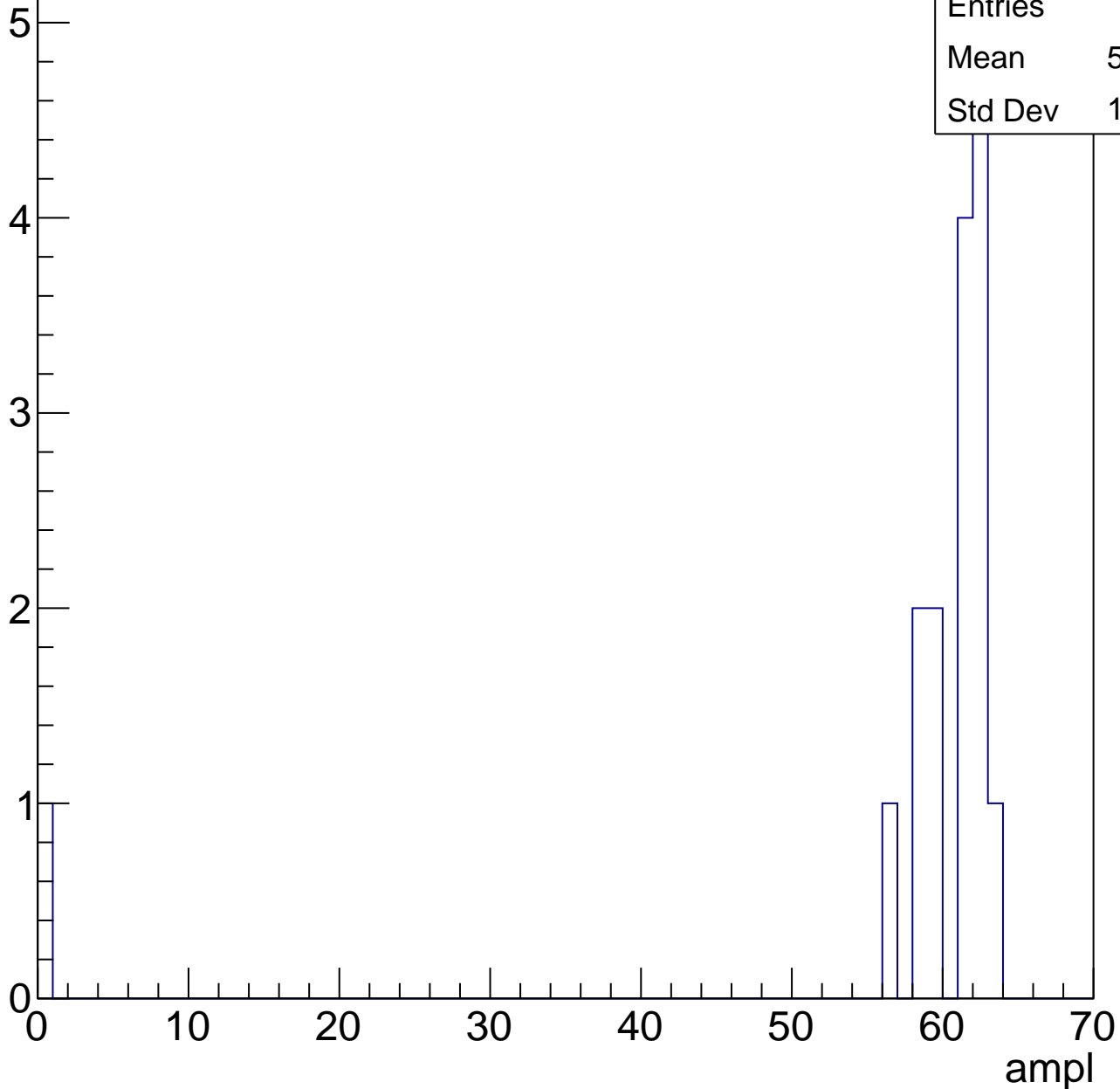


B1L103S, U23-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	56.69
Std Dev	14.76



B1L103S, U23-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch55, adc0

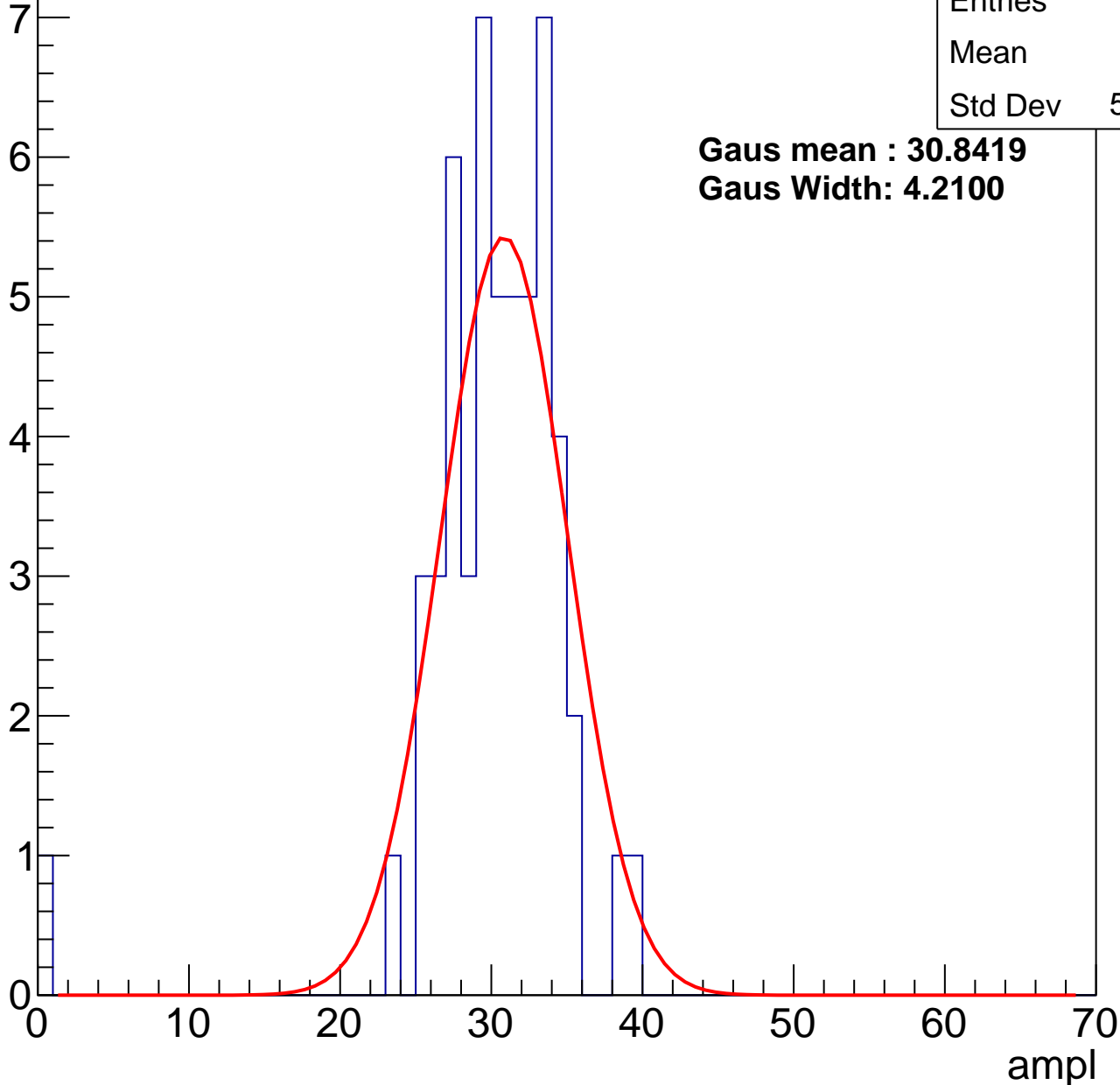
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	29.7
Std Dev	5.248

Gaus mean : 30.8419

Gaus Width: 4.2100



B1L103S, U23-ch55, adc1

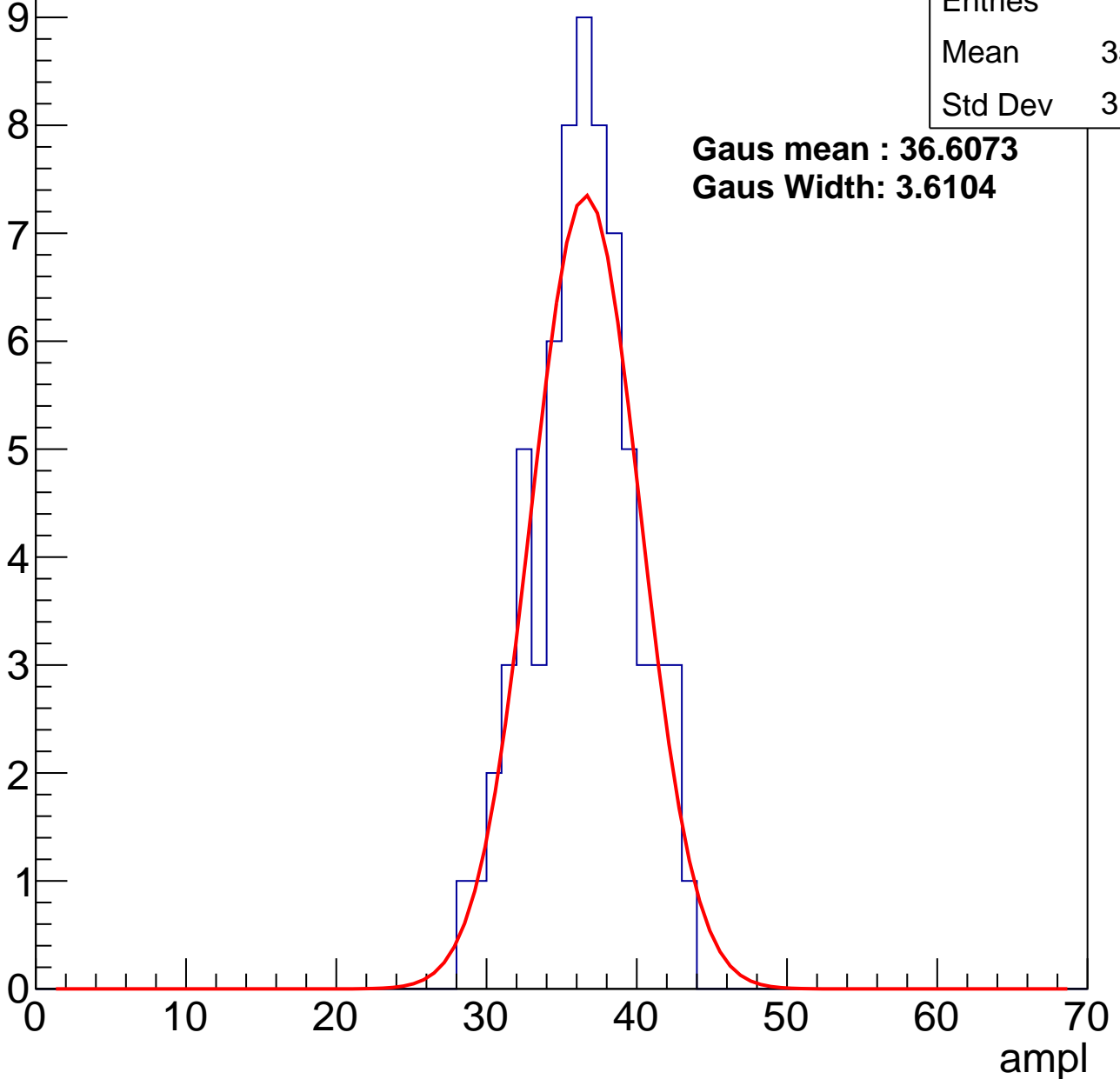
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.97
Std Dev	3.343

Gaus mean : 36.6073

Gaus Width: 3.6104



B1L103S, U23-ch55, adc2

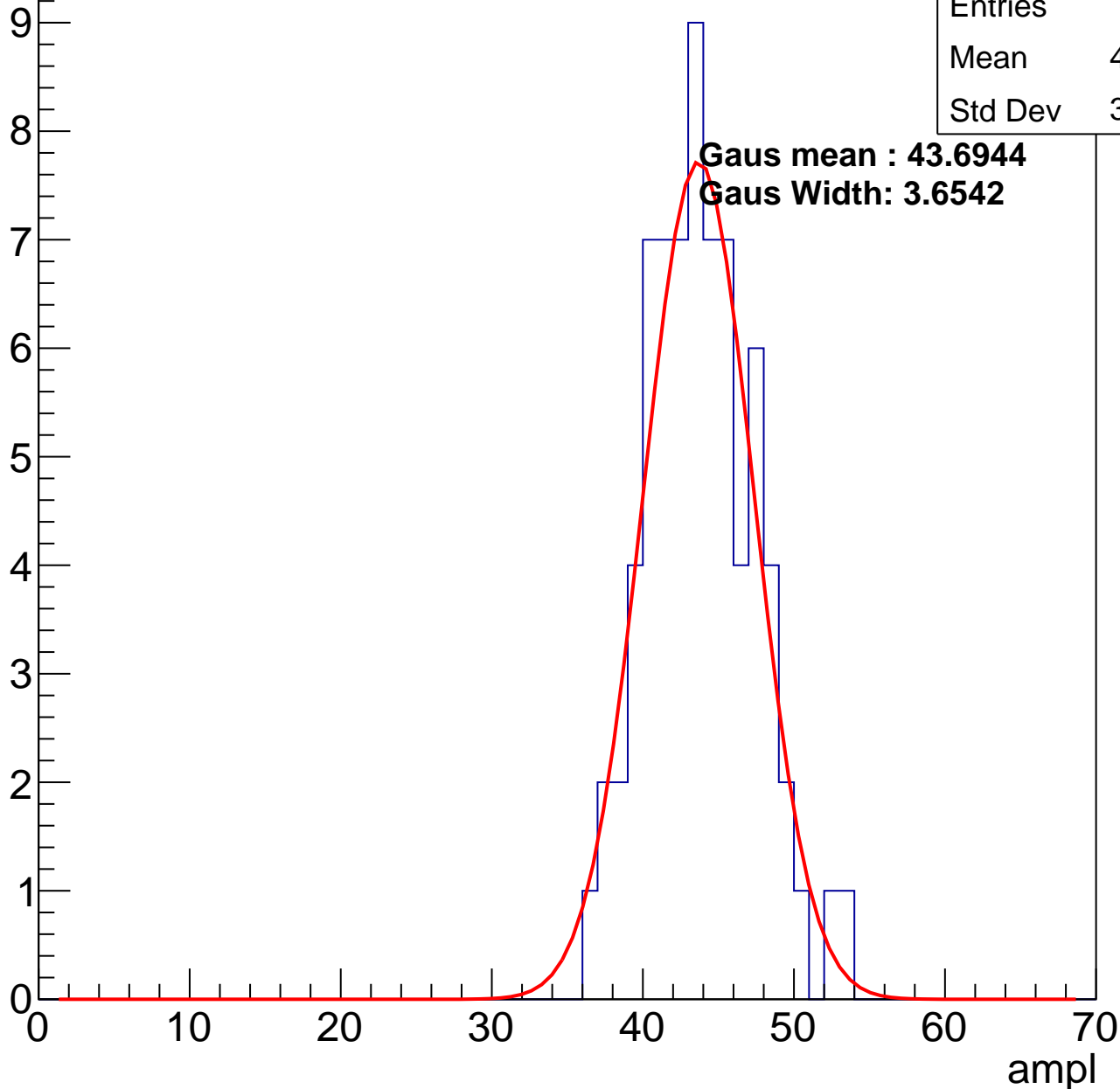
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.39
Std Dev	3.518

Gaus mean : 43.6944

Gaus Width: 3.6542

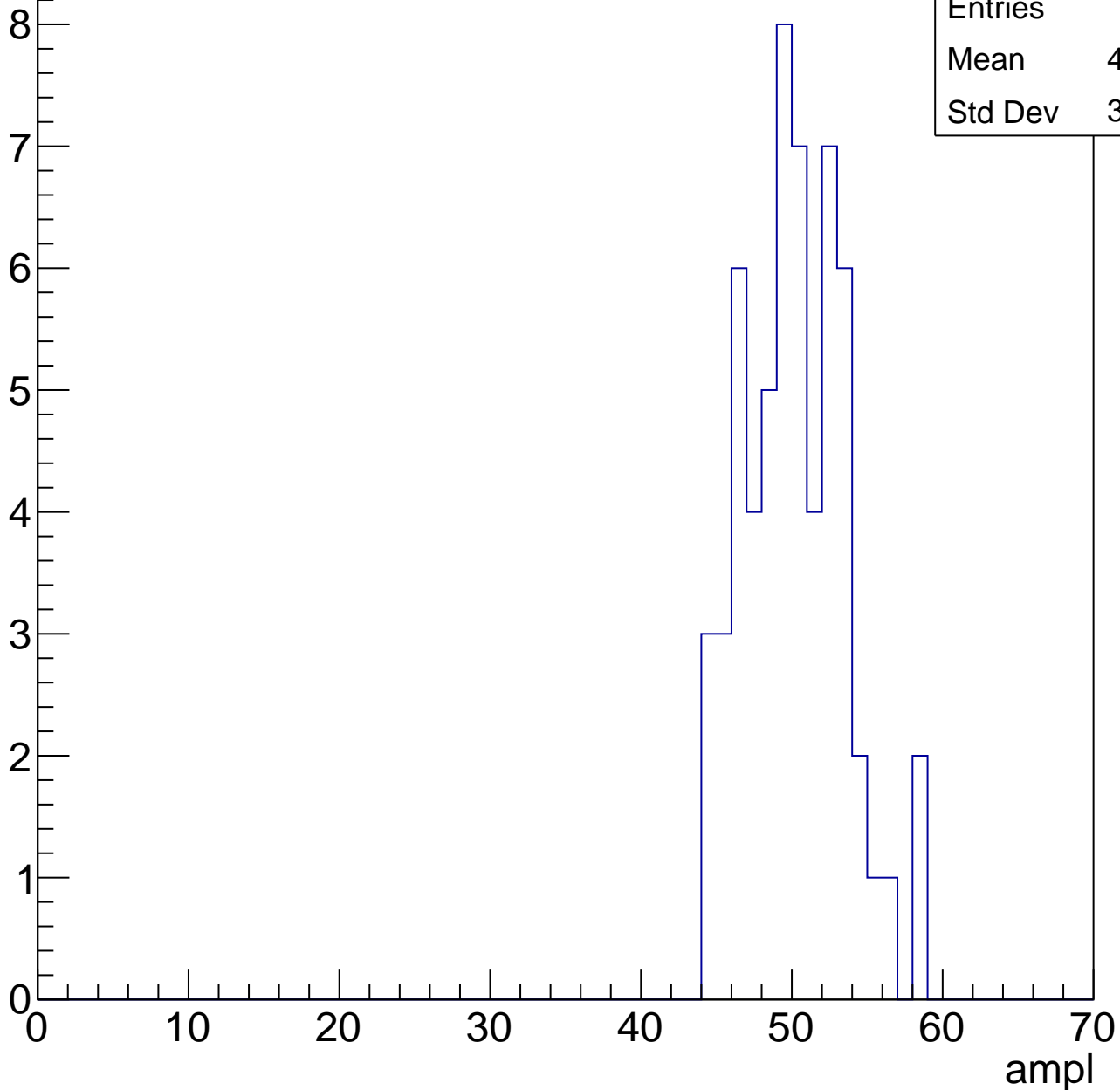


B1L103S, U23-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	49.73
Std Dev	3.308

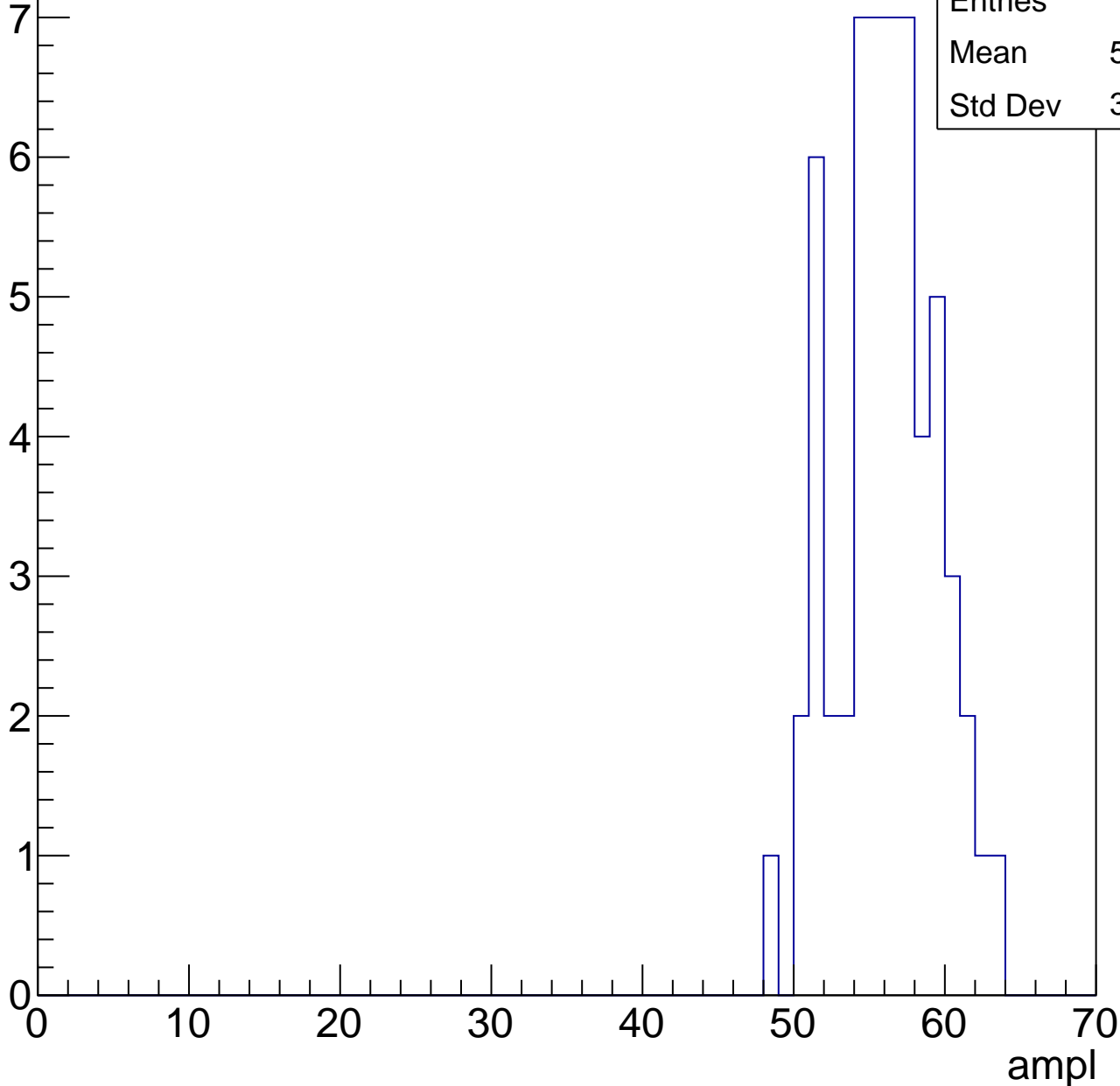


B1L103S, U23-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.65
Std Dev	3.285

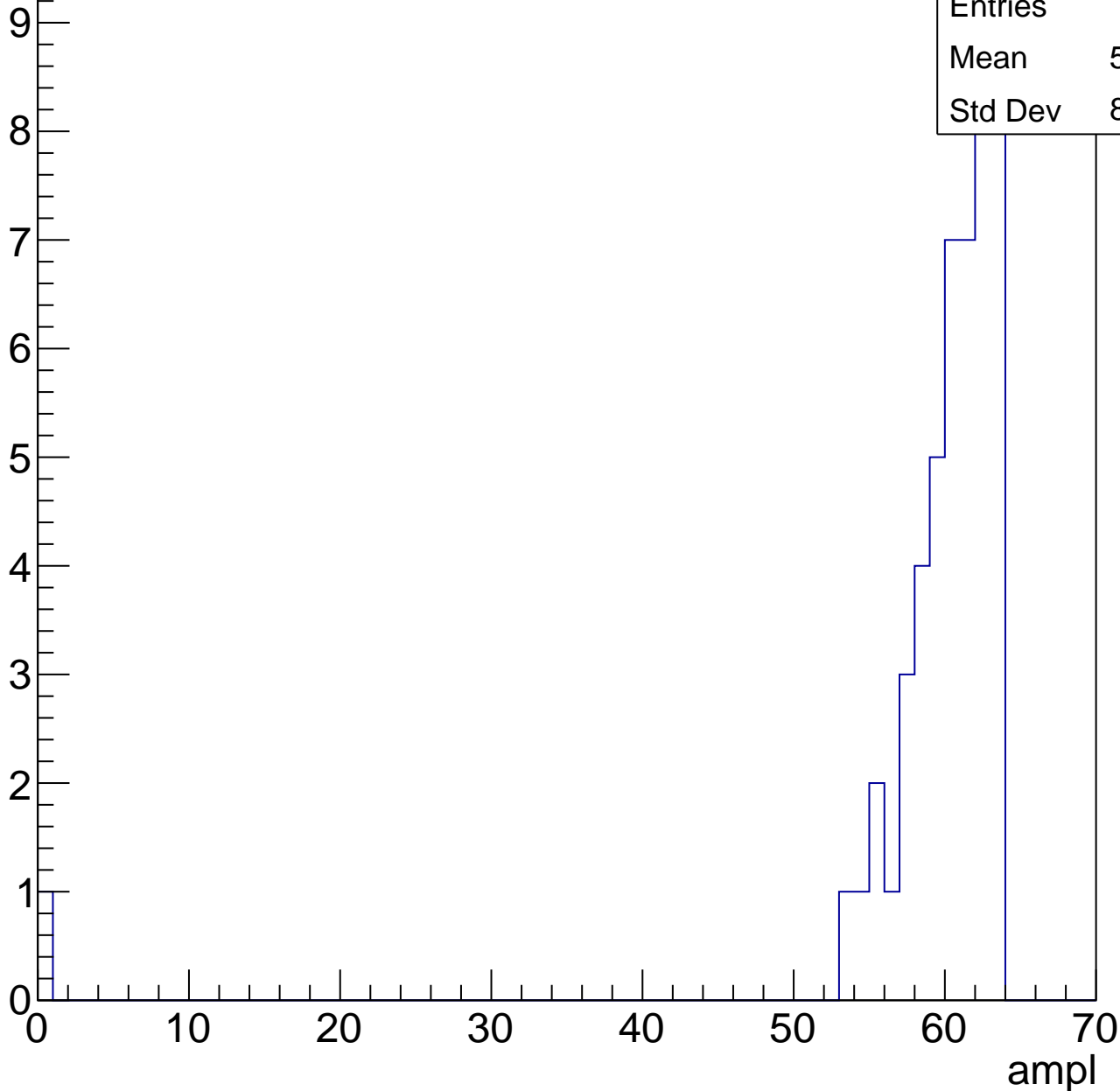


B1L103S, U23-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

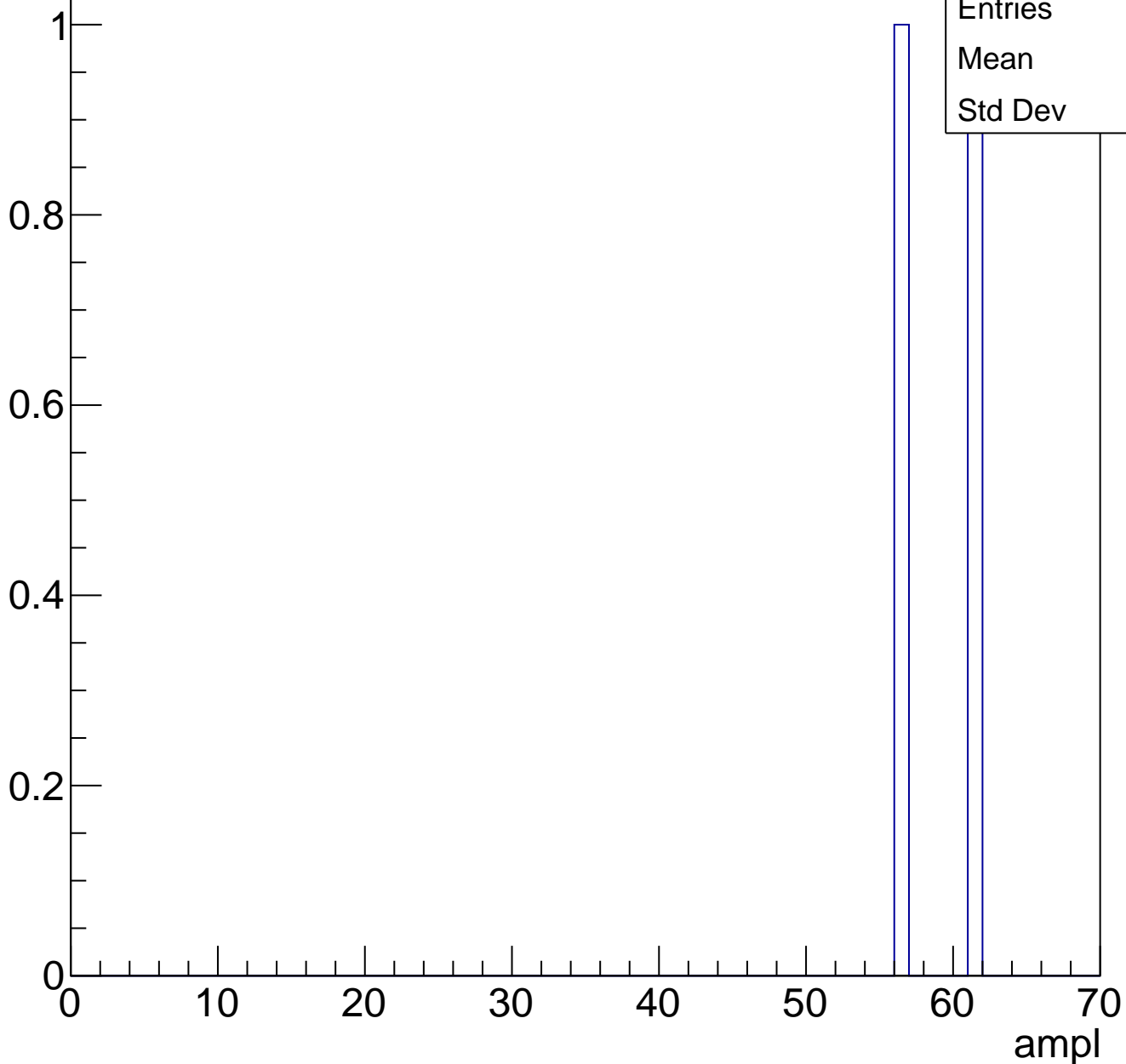
Entries	50
Mean	58.86
Std Dev	8.784



B1L103S, U23-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch56, adc0

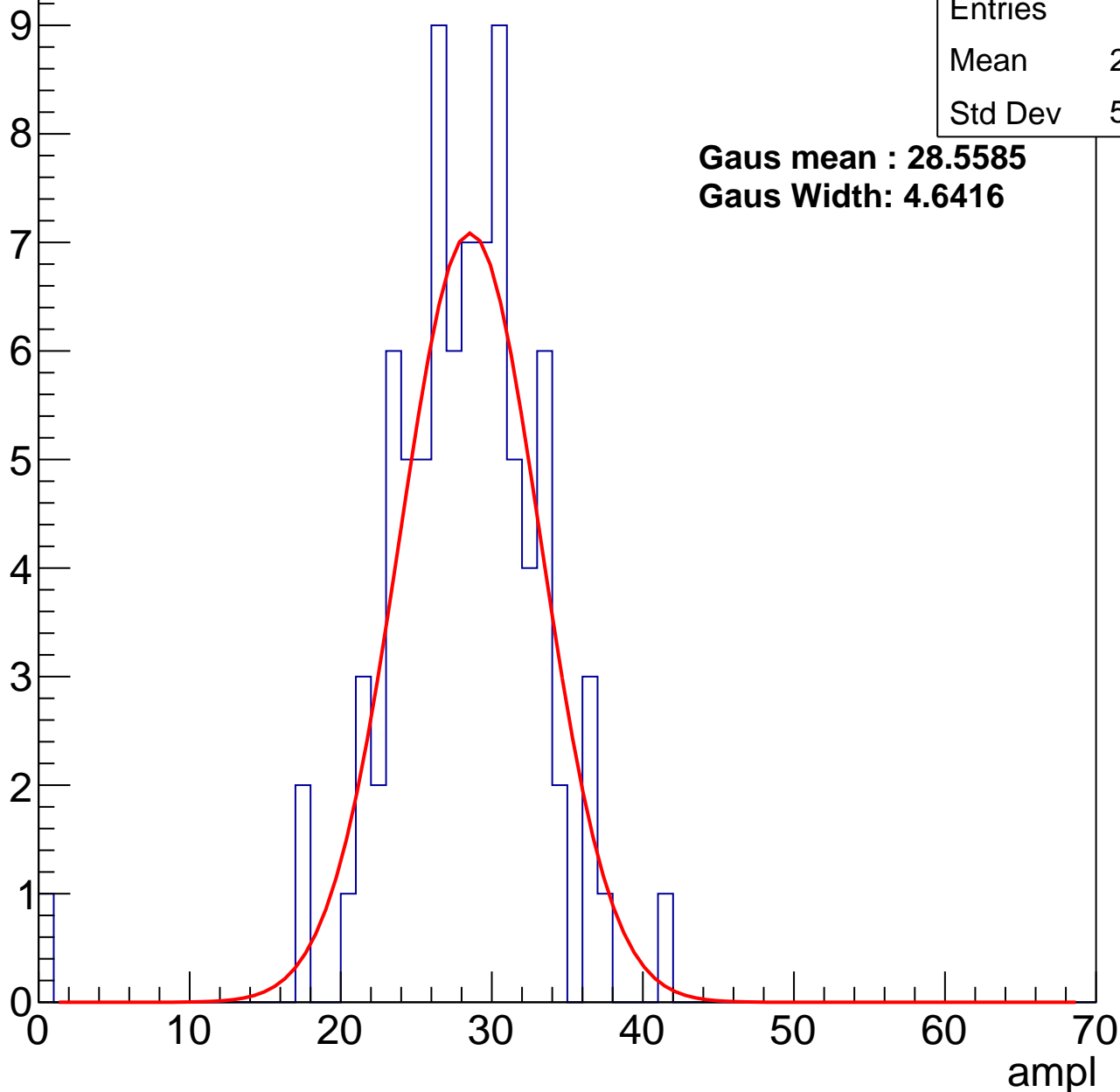
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	27.58
Std Dev	5.343

Gaus mean : 28.5585

Gaus Width: 4.6416



B1L103S, U23-ch56, adc1

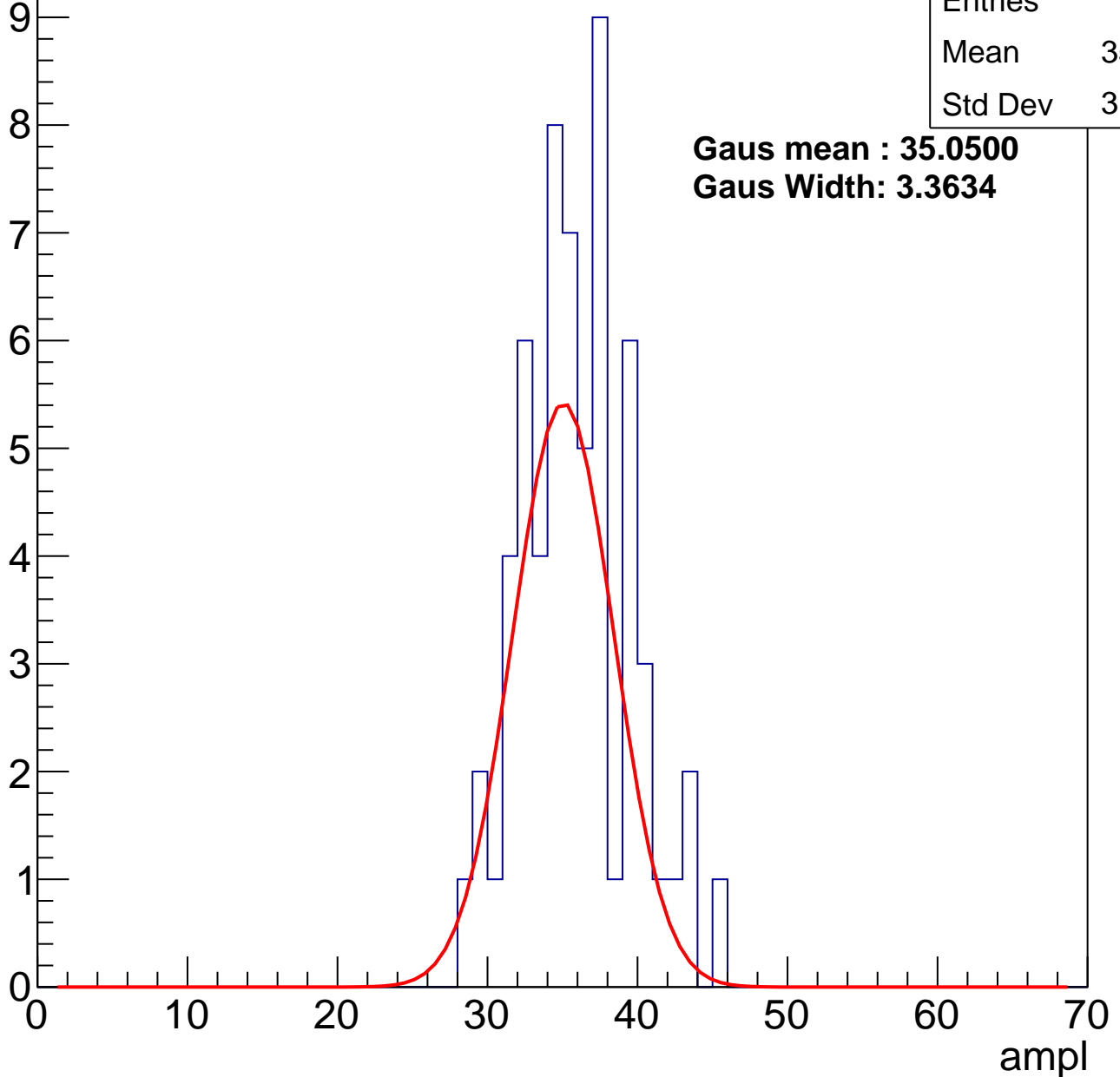
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.48
Std Dev	3.609

Gaus mean : 35.0500

Gaus Width: 3.3634



B1L103S, U23-ch56, adc2

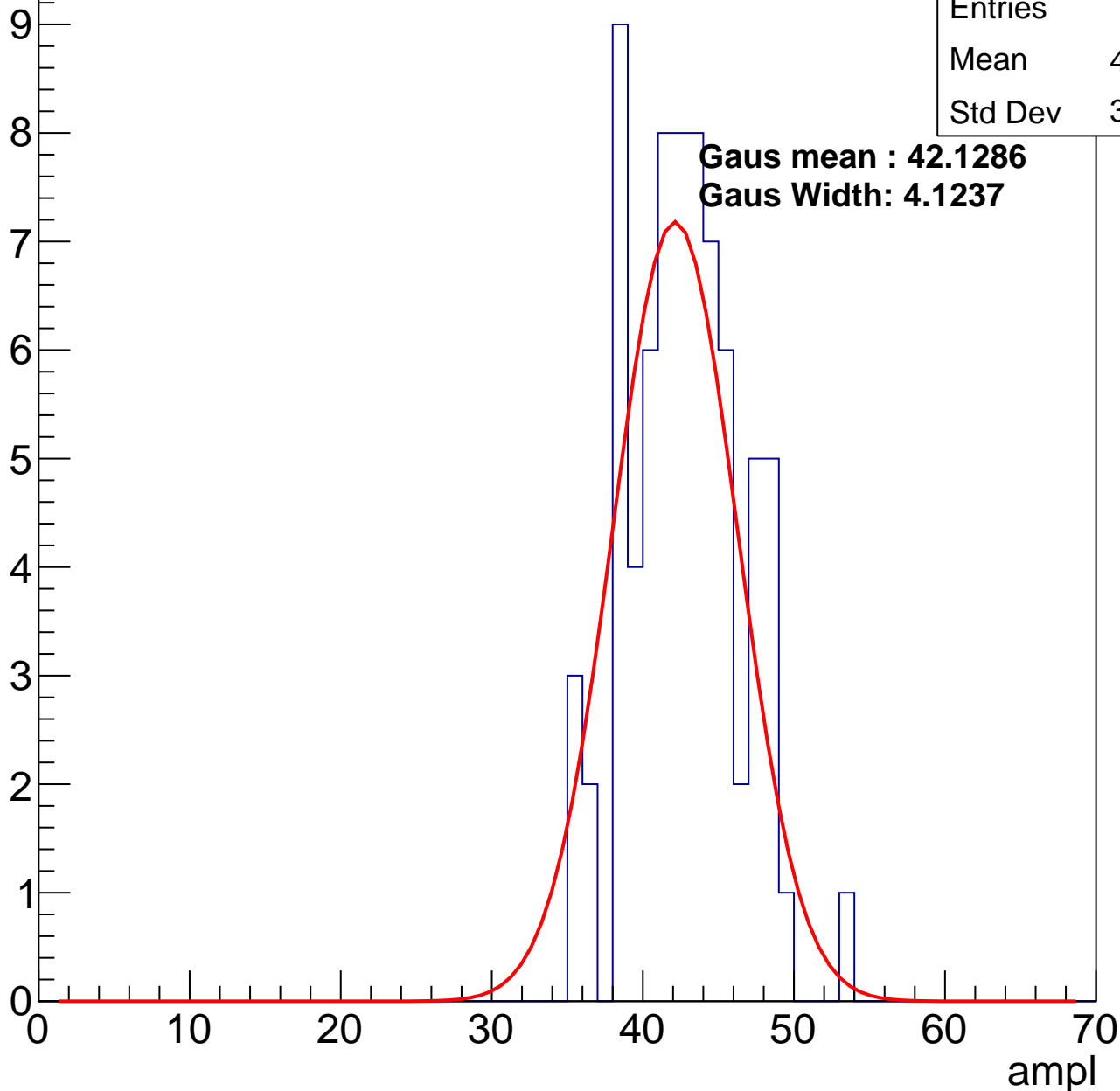
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.27
Std Dev	3.693

Gaus mean : 42.1286

Gaus Width: 4.1237

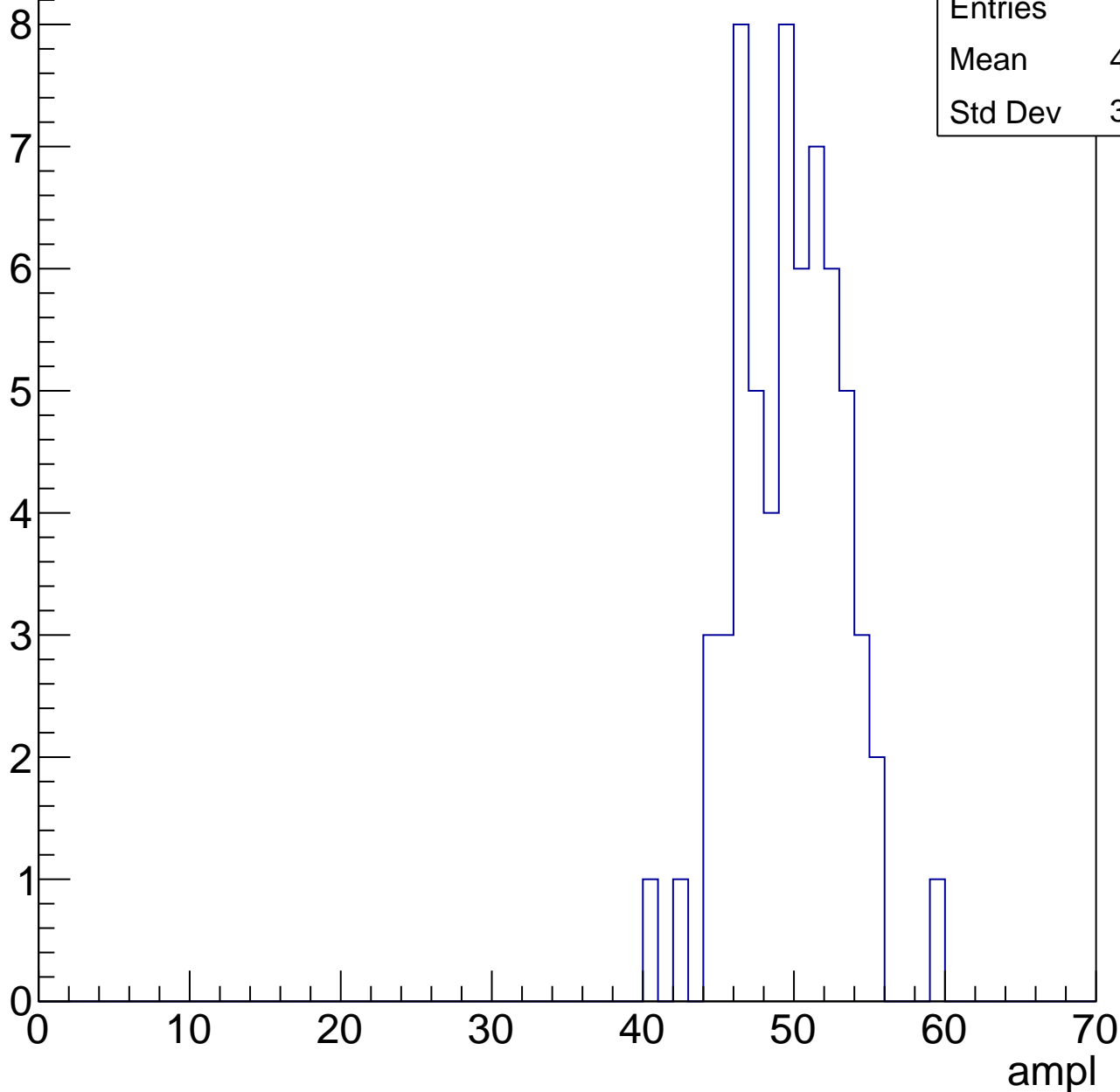


B1L103S, U23-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.22
Std Dev	3.475

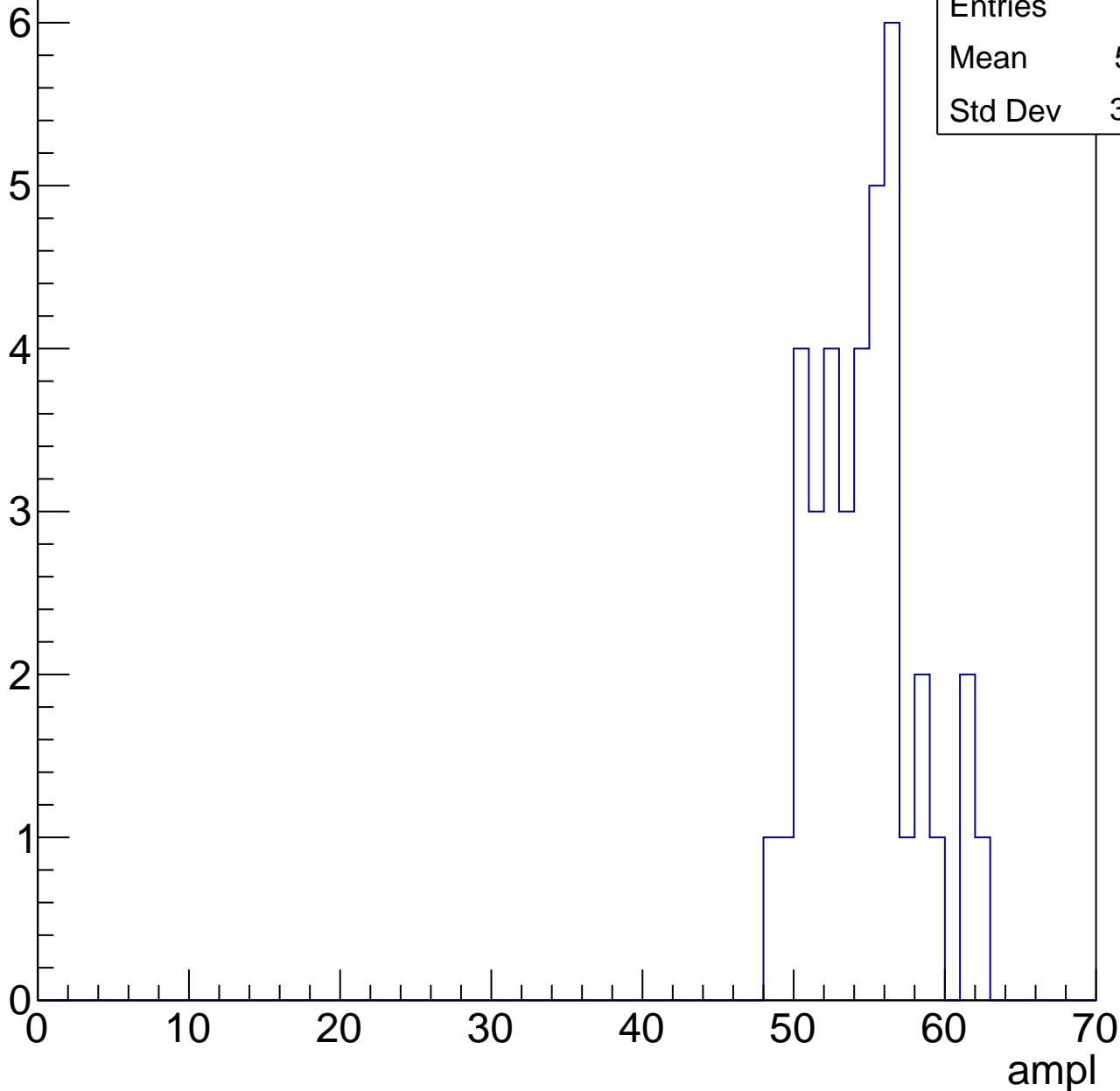


B1L103S, U23-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

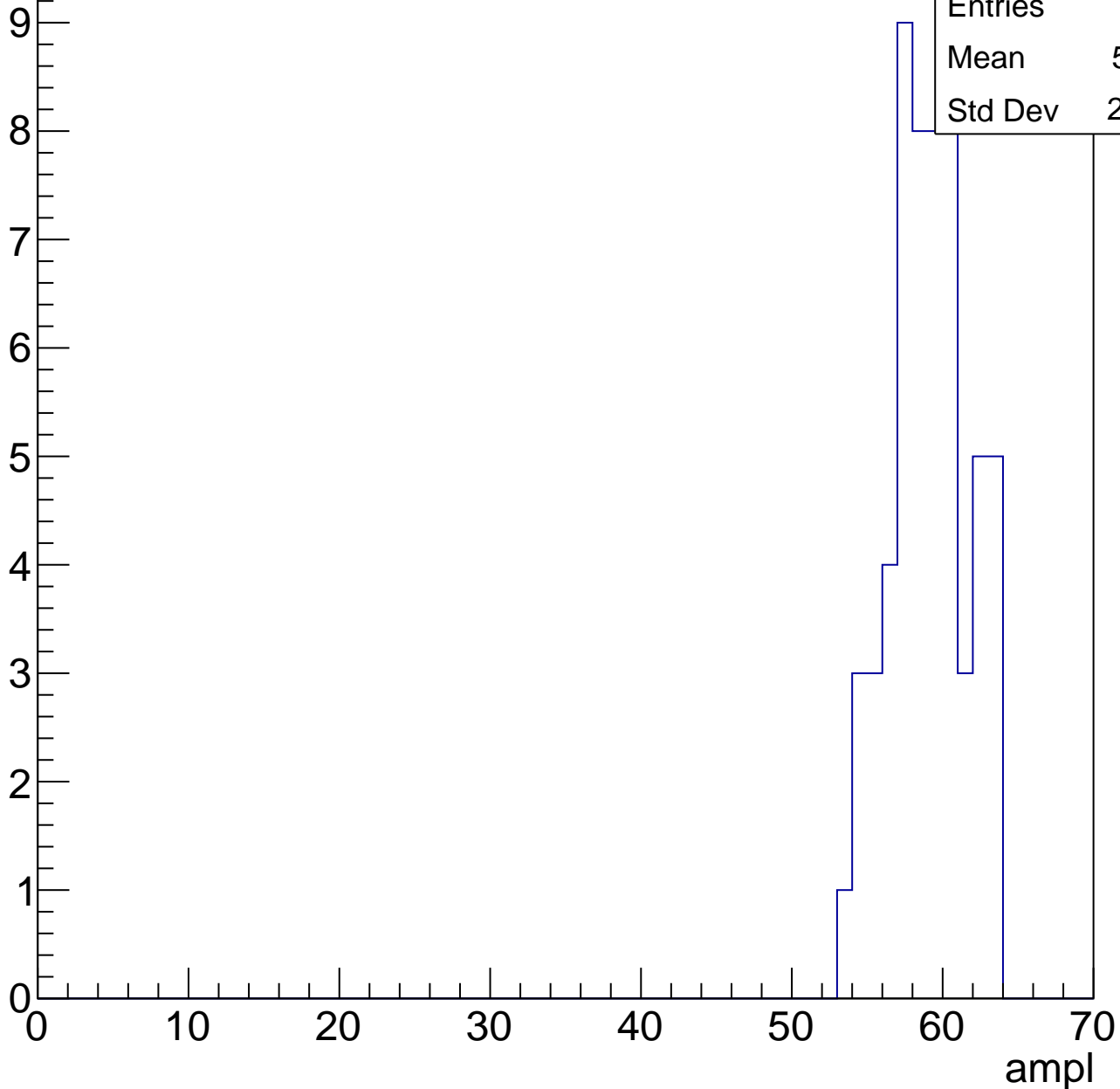
Entries	38
Mean	54.21
Std Dev	3.349



B1L103S, U23-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

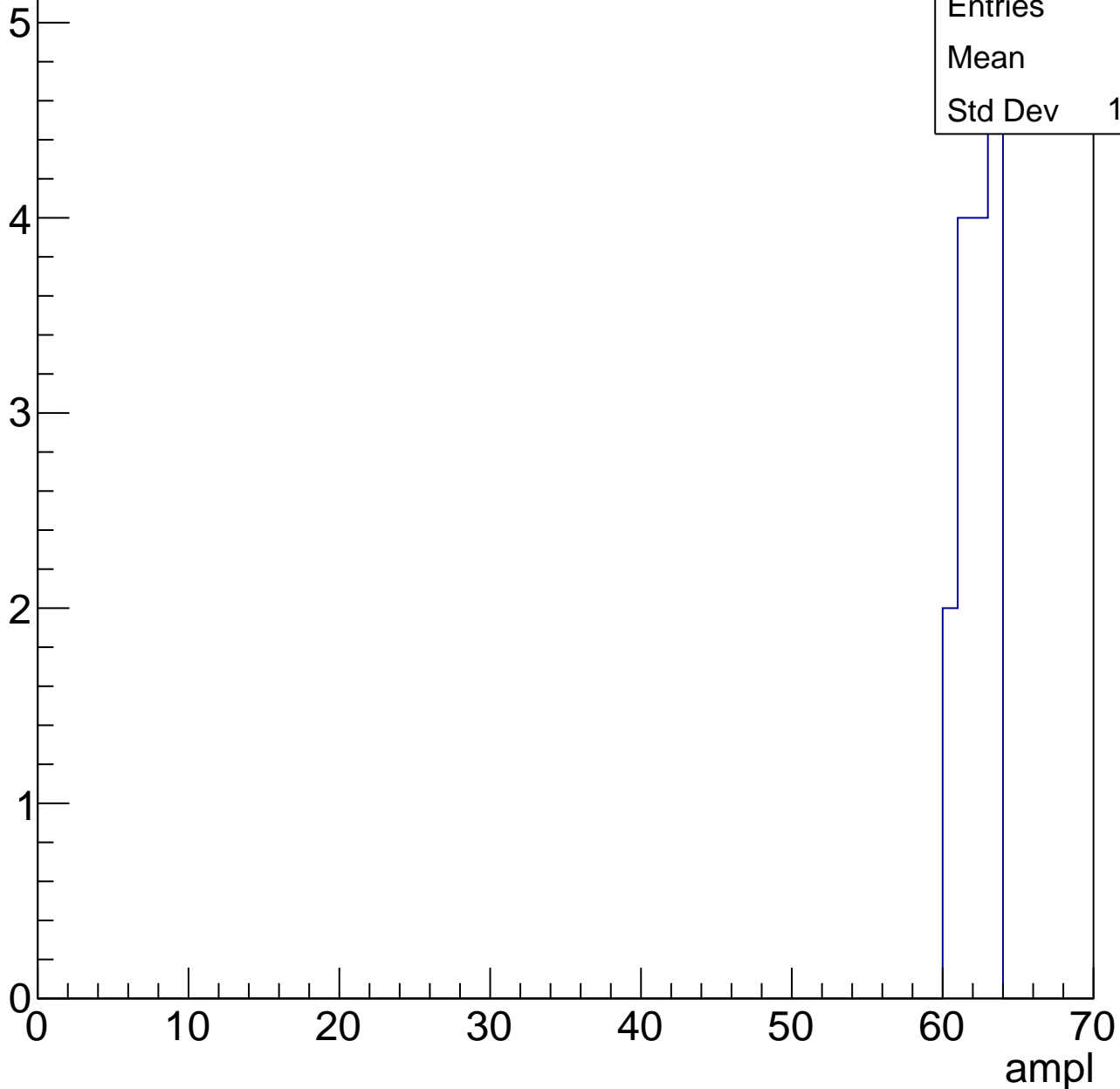


B1L103S, U23-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

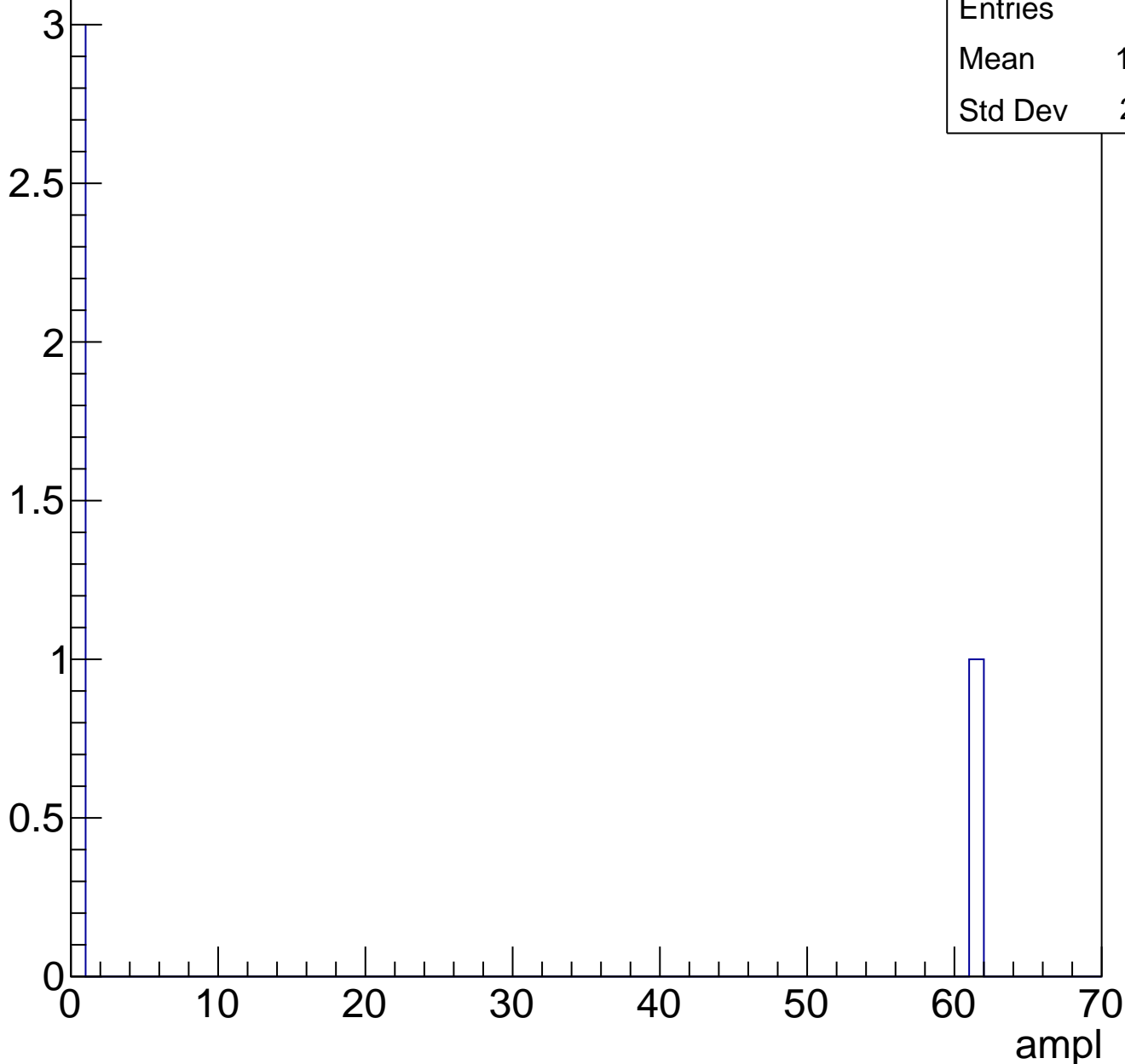
Entries	15
Mean	61.8
Std Dev	1.046



B1L103S, U23-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch57, adc0

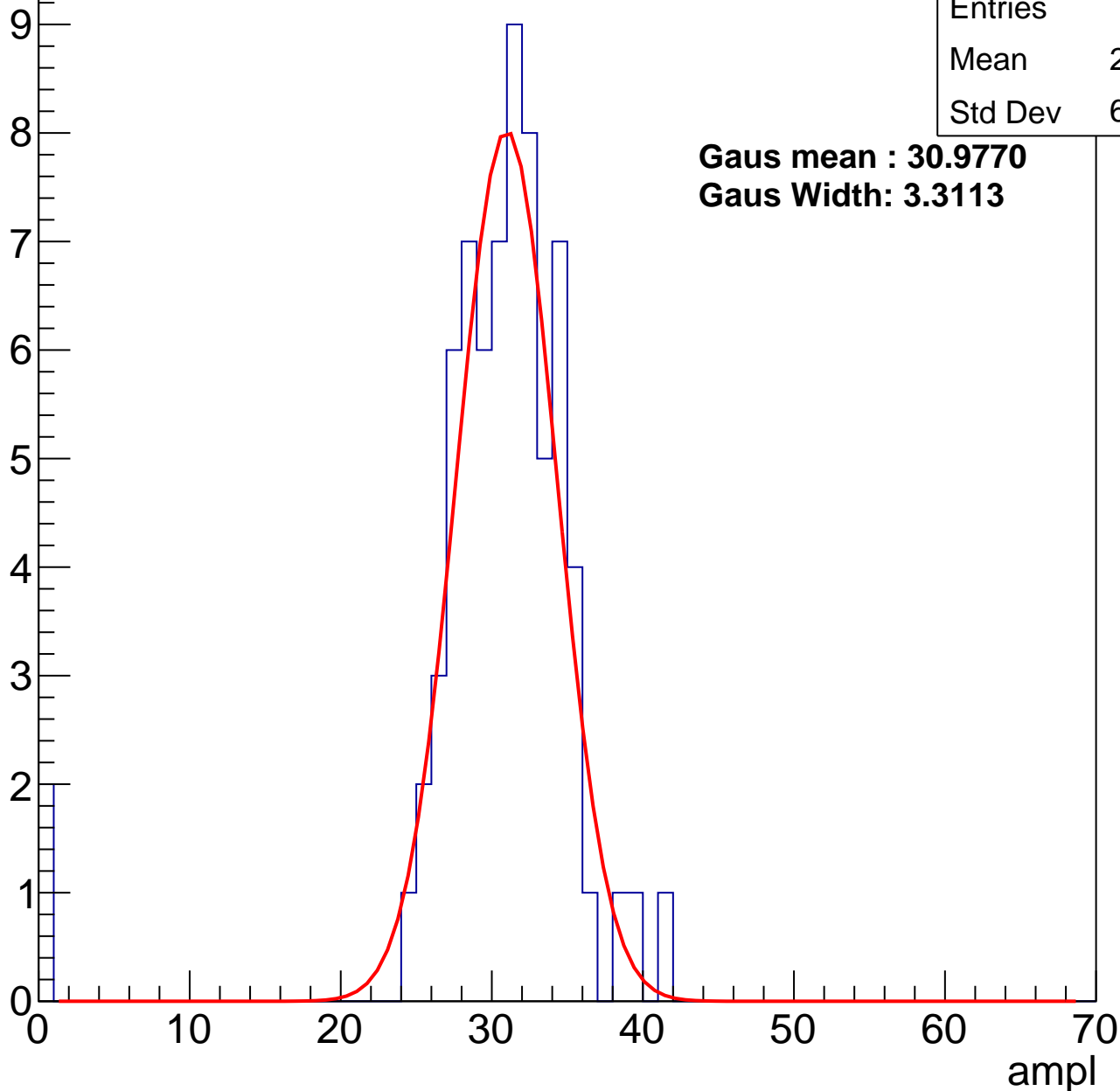
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.94
Std Dev	6.077

Gaus mean : 30.9770

Gaus Width: 3.3113



B1L103S, U23-ch57, adc1

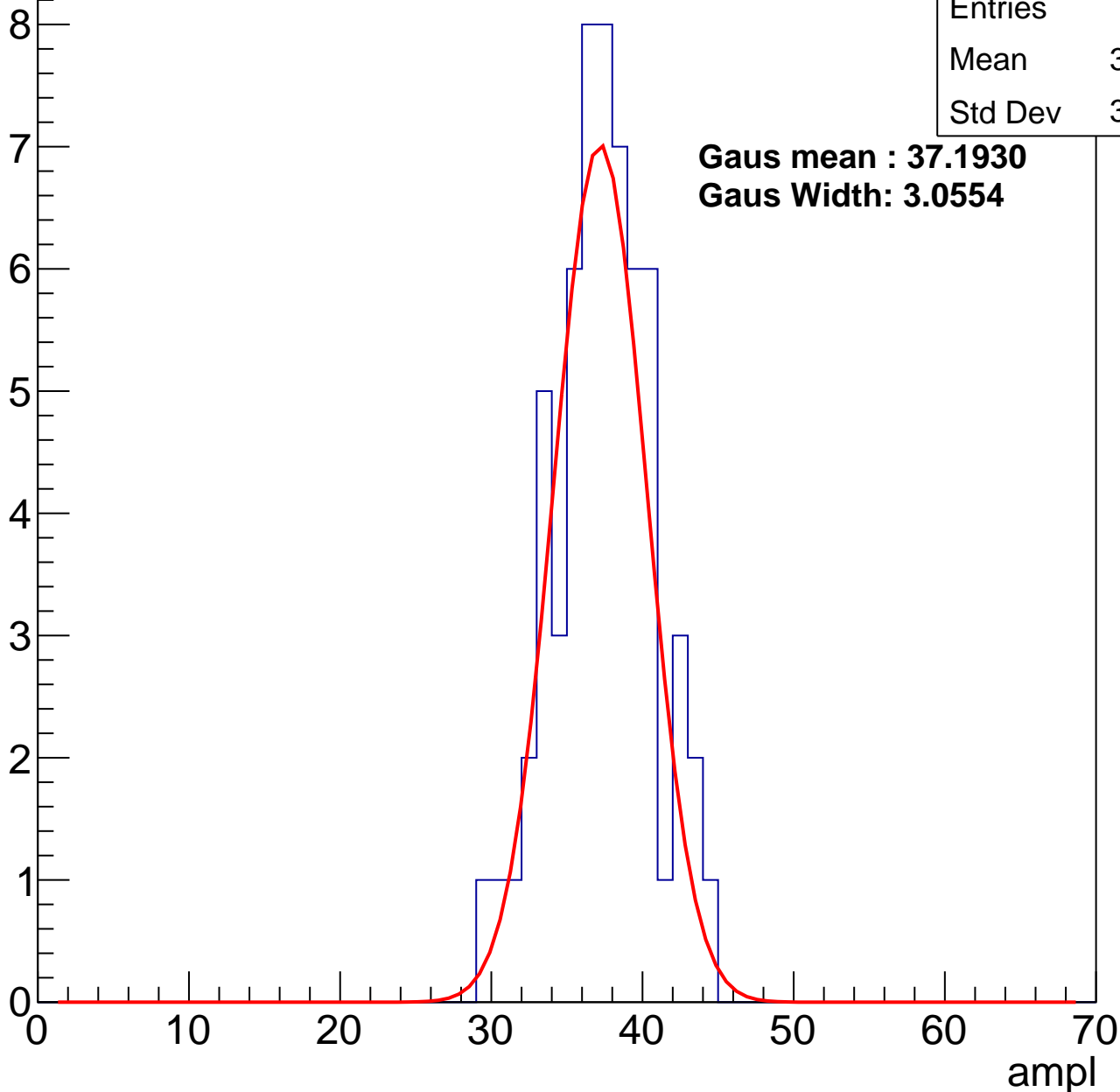
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	36.92
Std Dev	3.225

Gaus mean : 37.1930

Gaus Width: 3.0554



B1L103S, U23-ch57, adc2

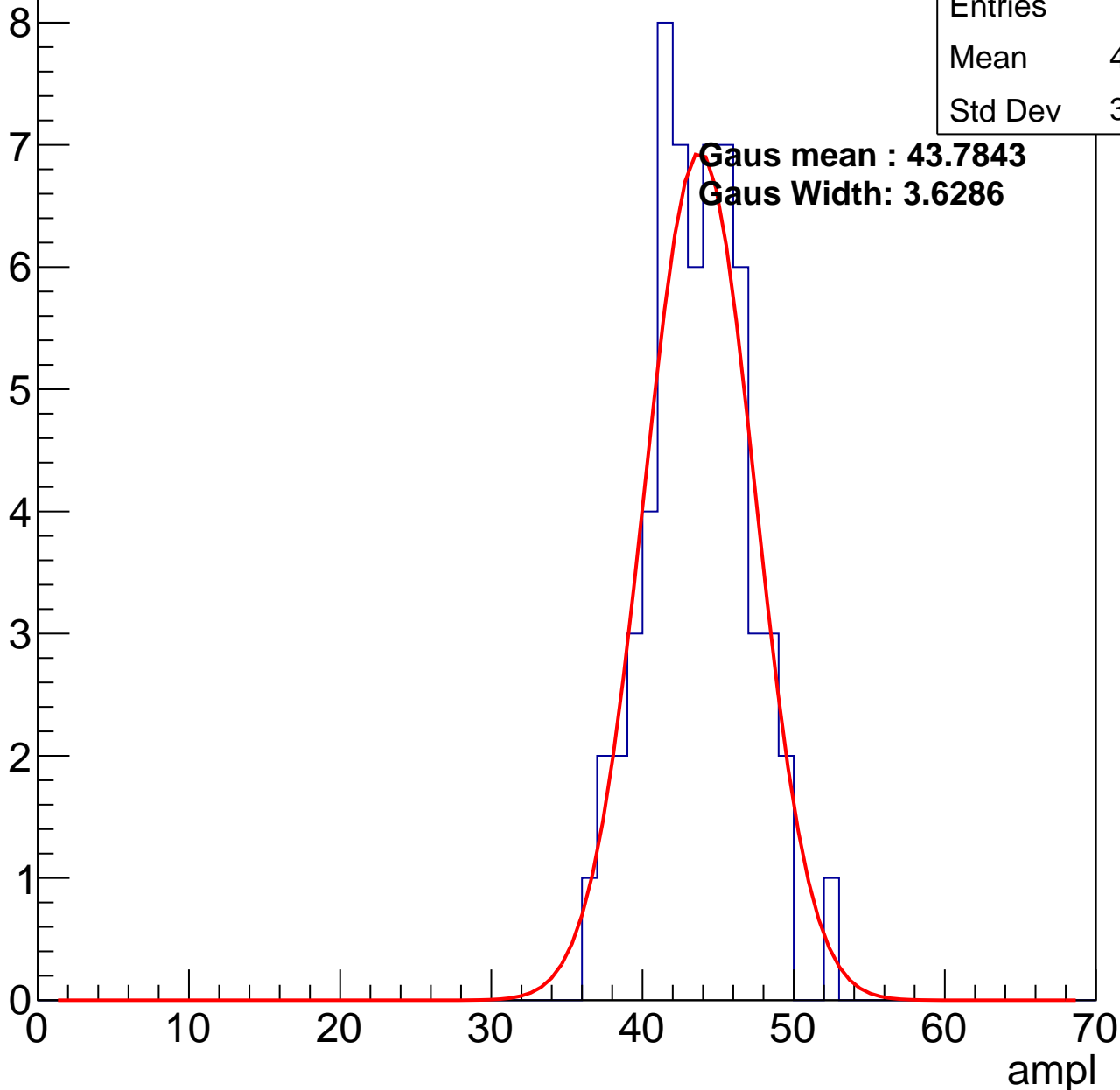
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.18
Std Dev	3.255

Gaus mean : 43.7843

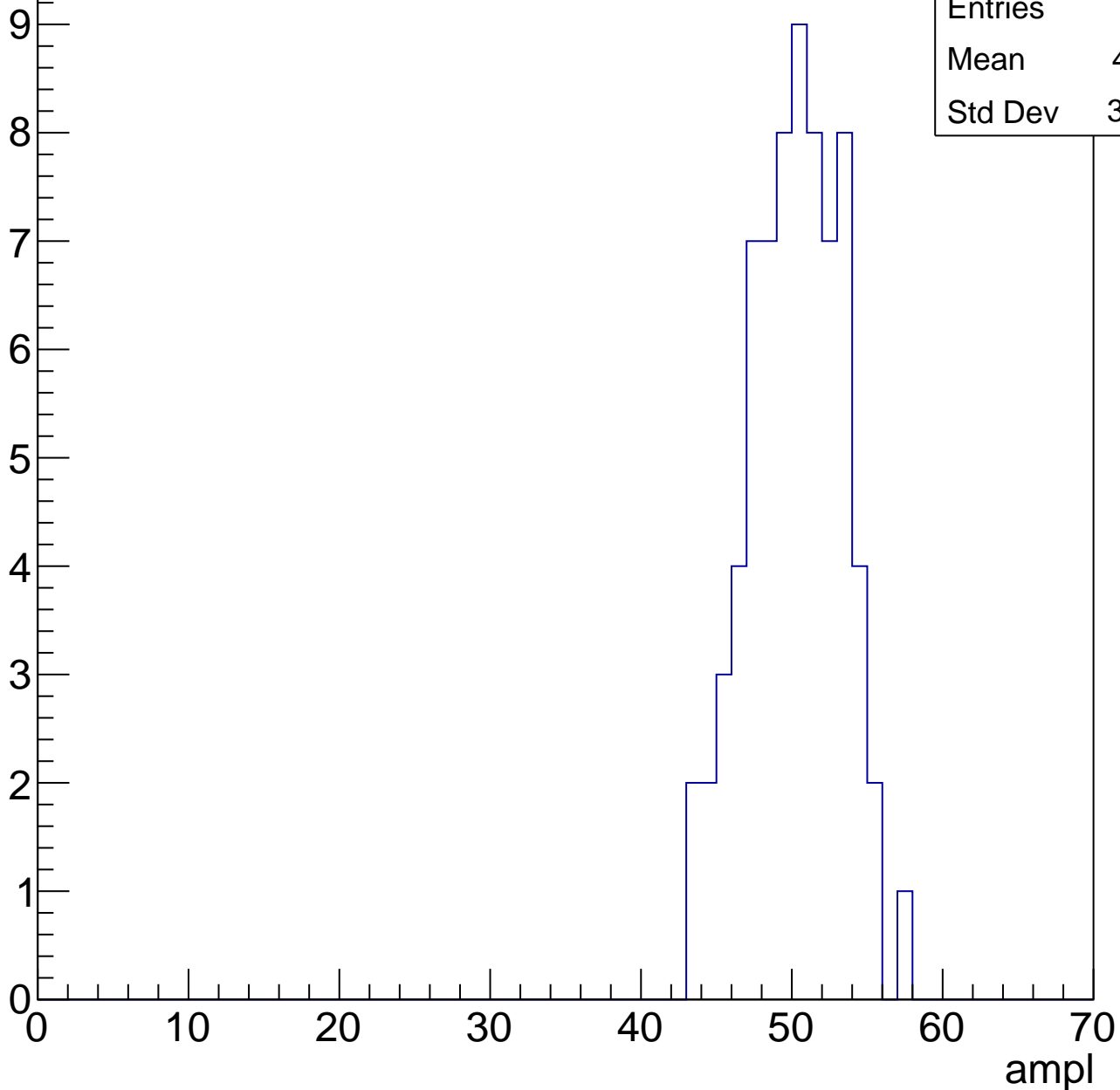
Gaus Width: 3.6286



B1L103S, U23-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

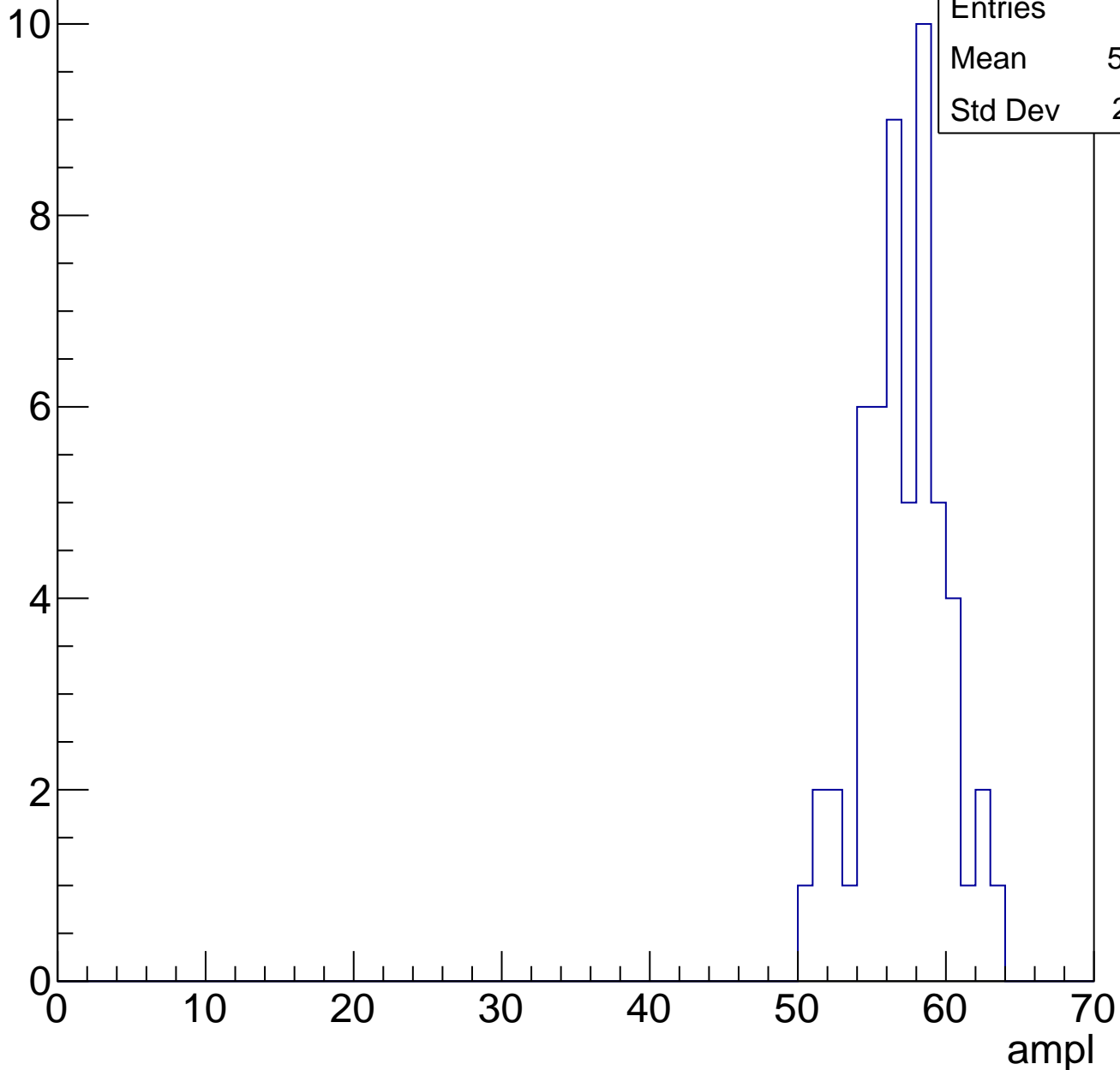


B1L103S, U23-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	55
Mean	56.64
Std Dev	2.811

Entry

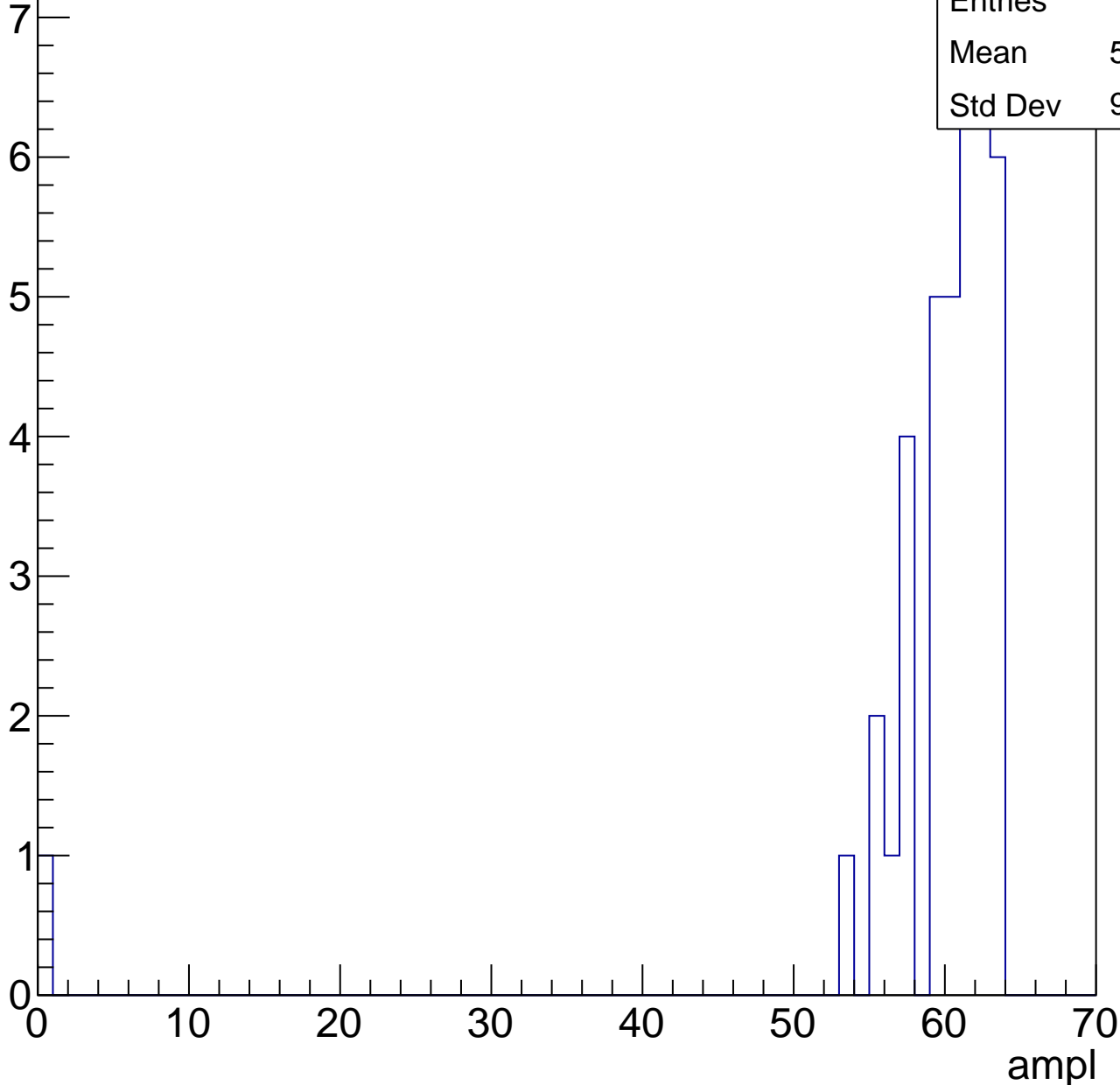


B1L103S, U23-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.49
Std Dev	9.813



B1L103S, U23-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch58, adc0

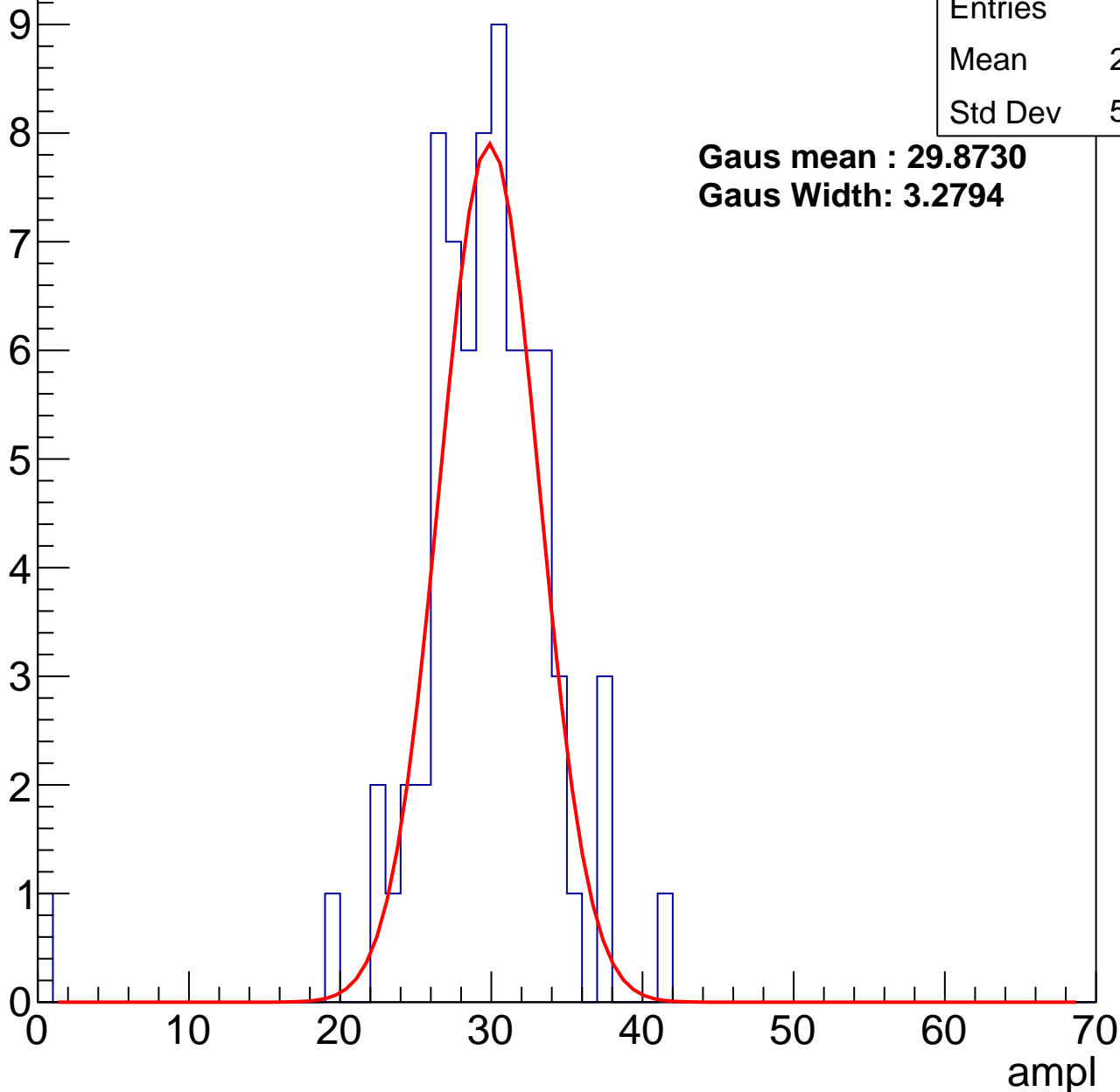
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.99
Std Dev	5.092

Gaus mean : 29.8730

Gaus Width: 3.2794



B1L103S, U23-ch58, adc1

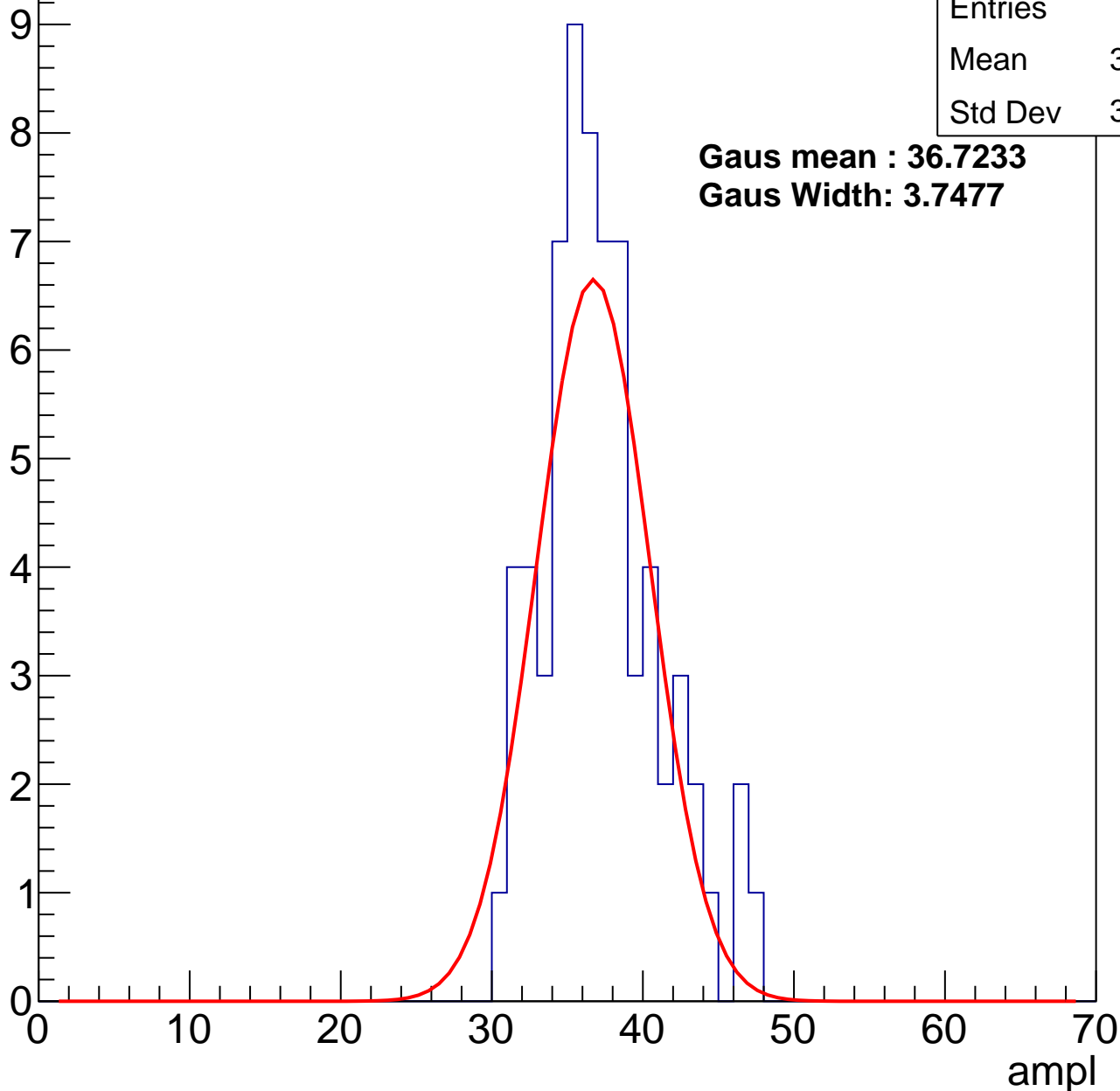
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.78
Std Dev	3.815

Gaus mean : 36.7233

Gaus Width: 3.7477



B1L103S, U23-ch58, adc2

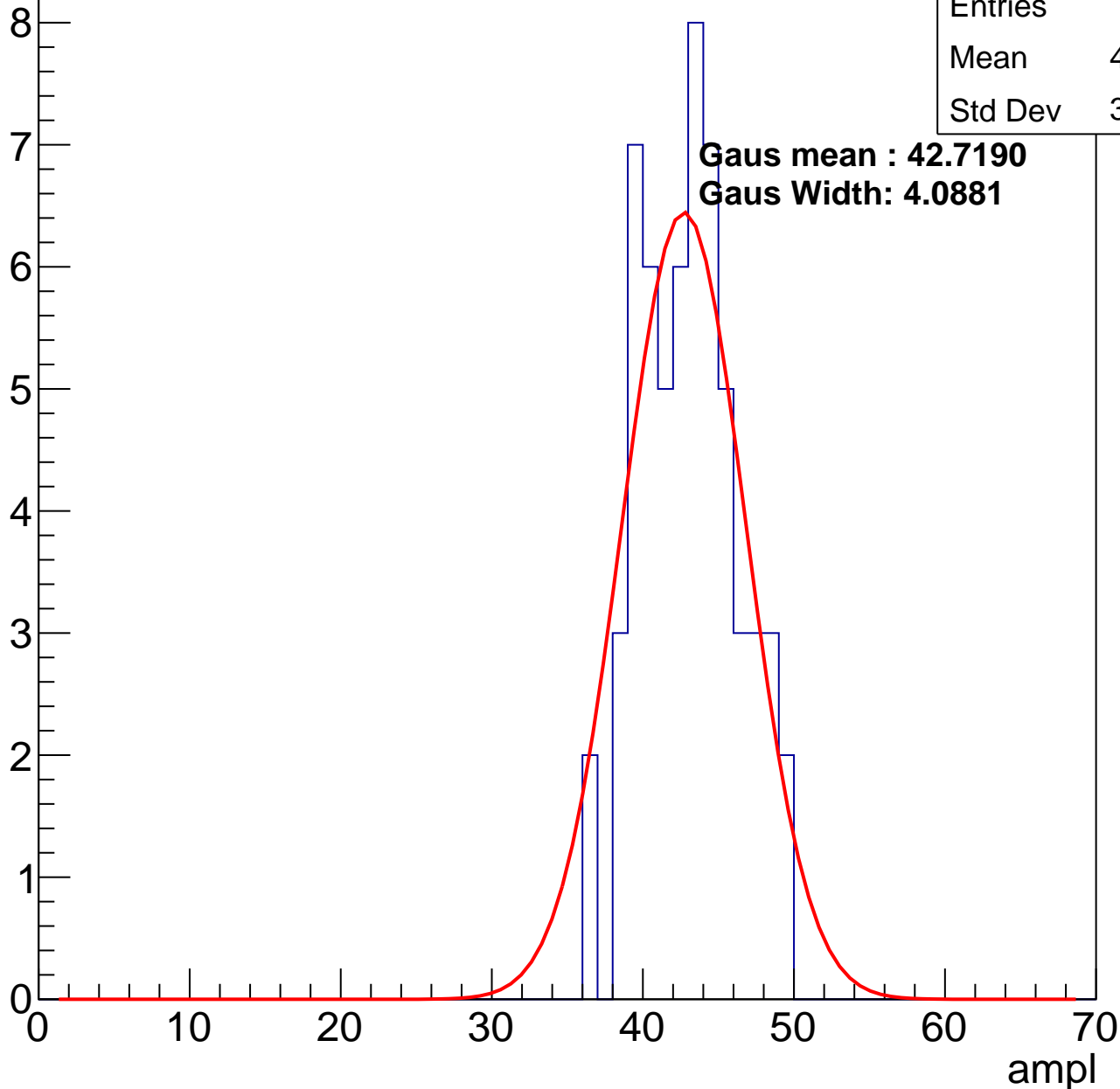
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.57
Std Dev	3.169

Gaus mean : 42.7190

Gaus Width: 4.0881

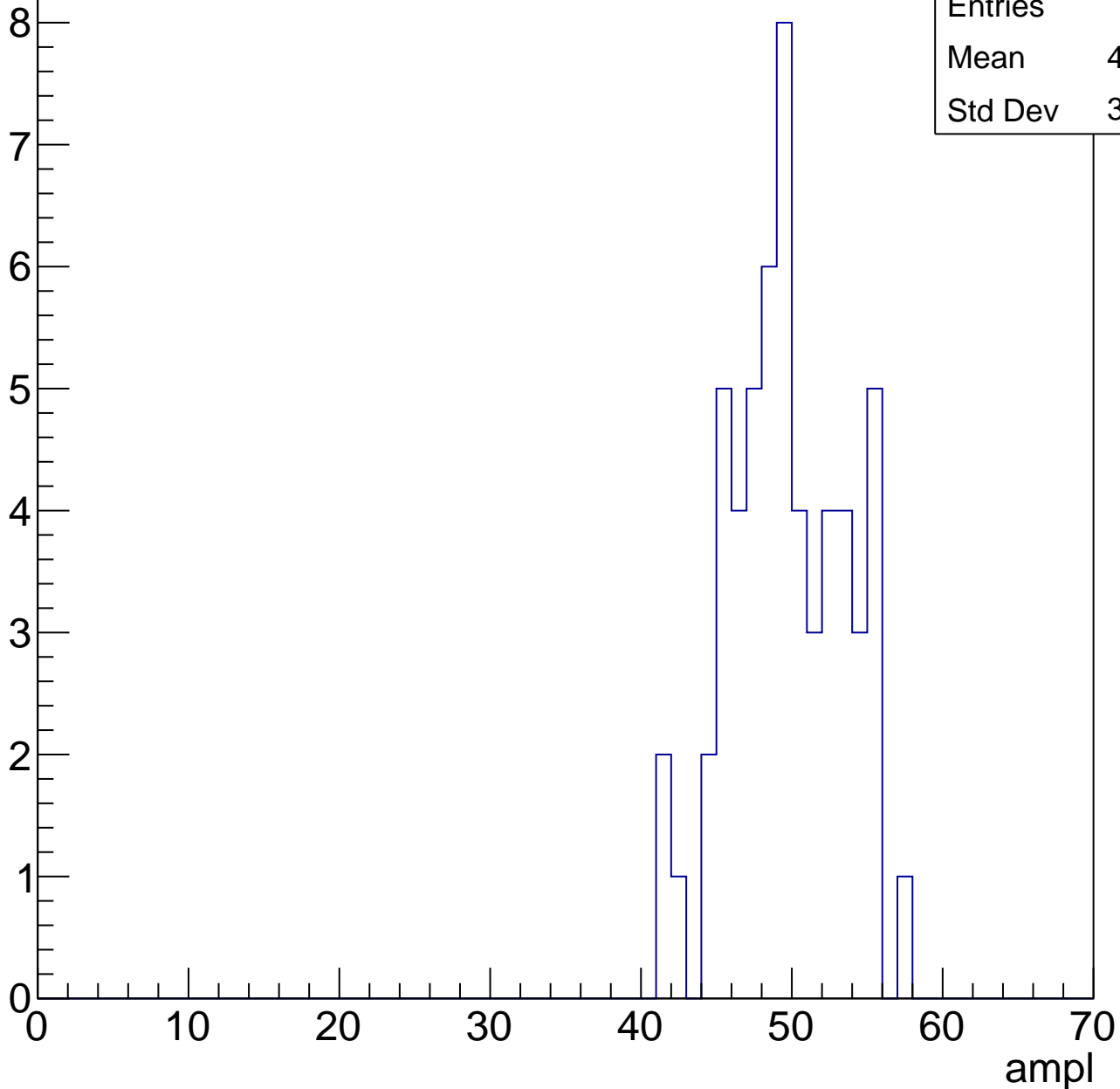


B1L103S, U23-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	49.18
Std Dev	3.752

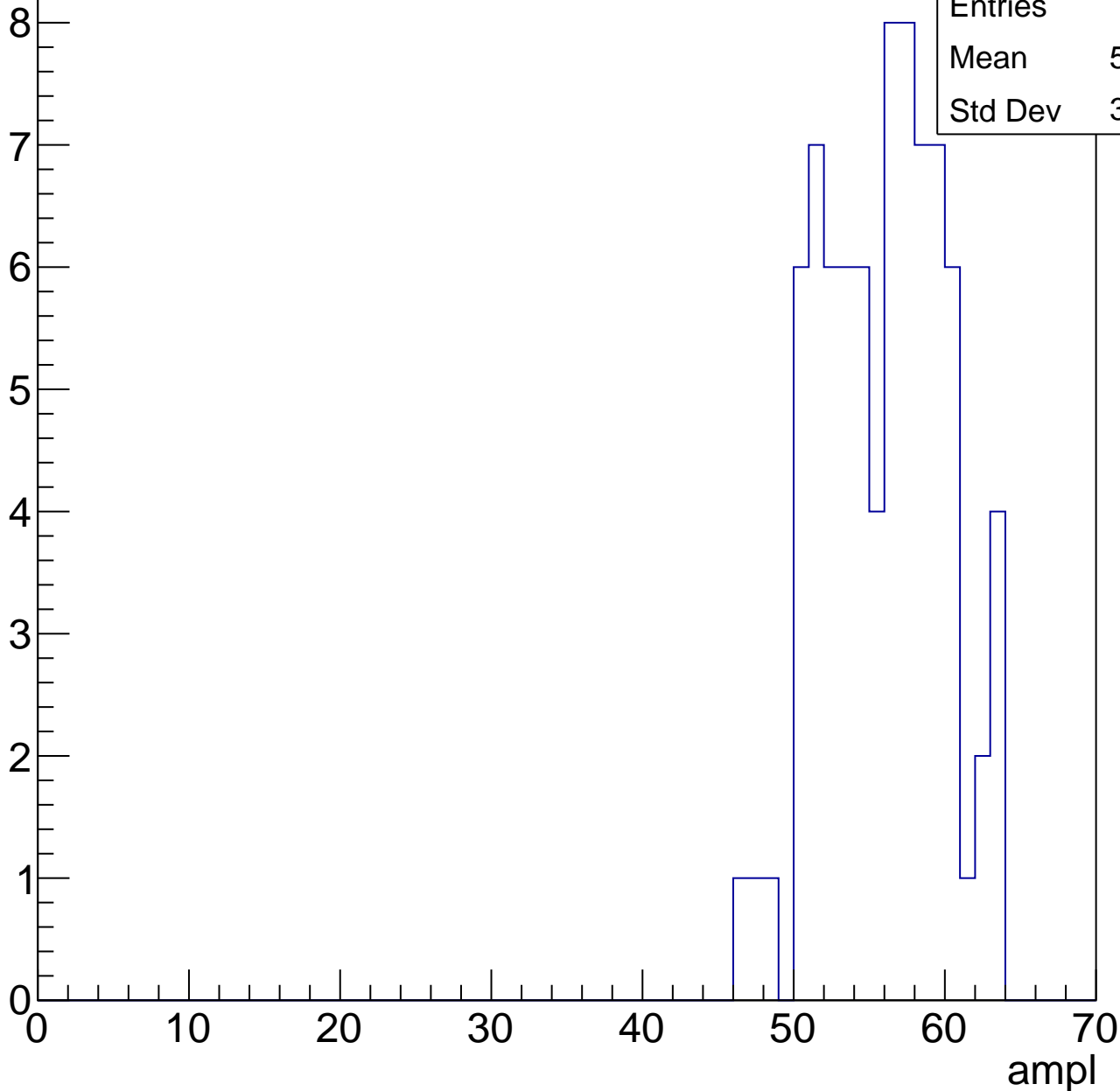


B1L103S, U23-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	55.46
Std Dev	3.972

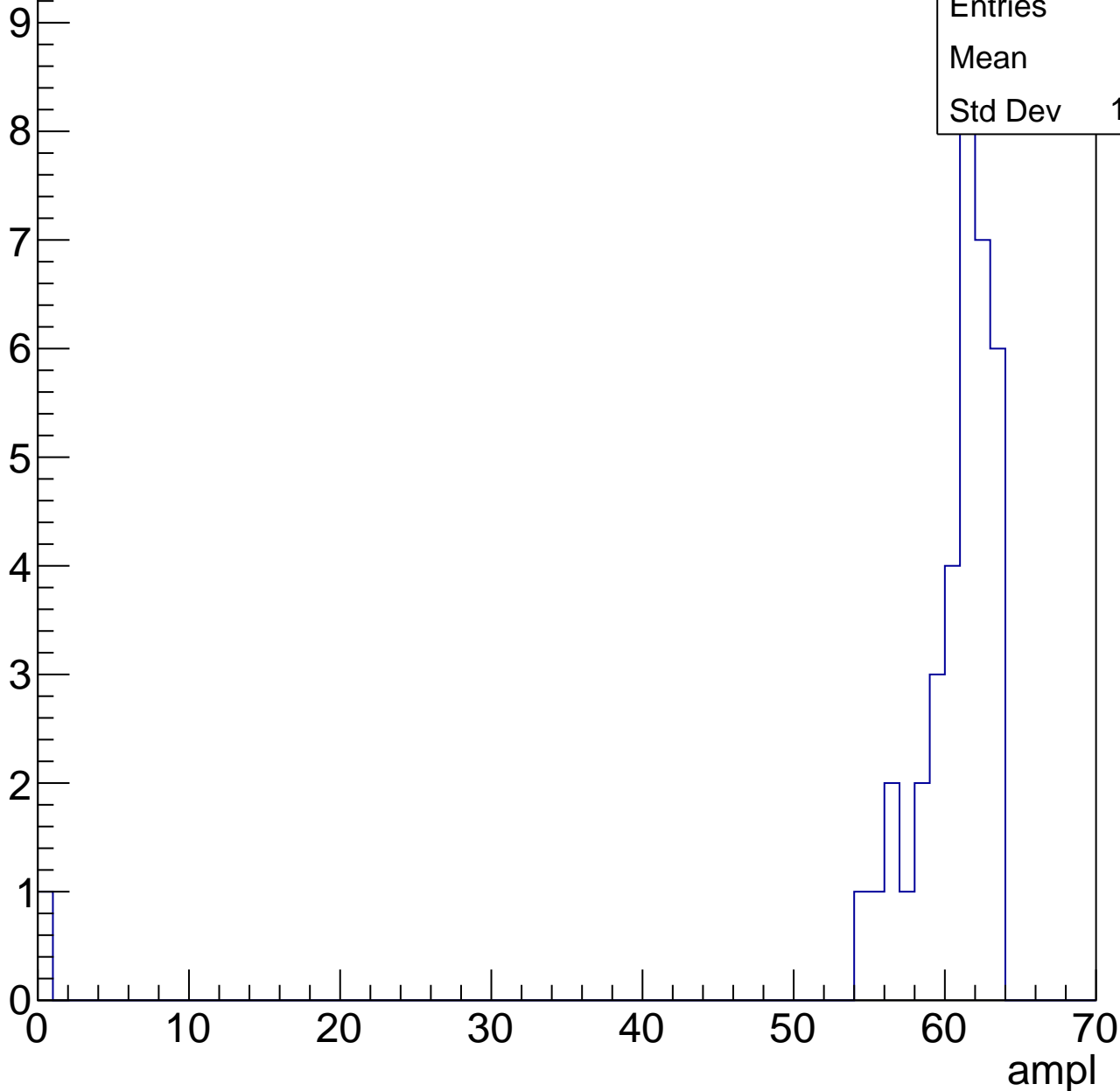


B1L103S, U23-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

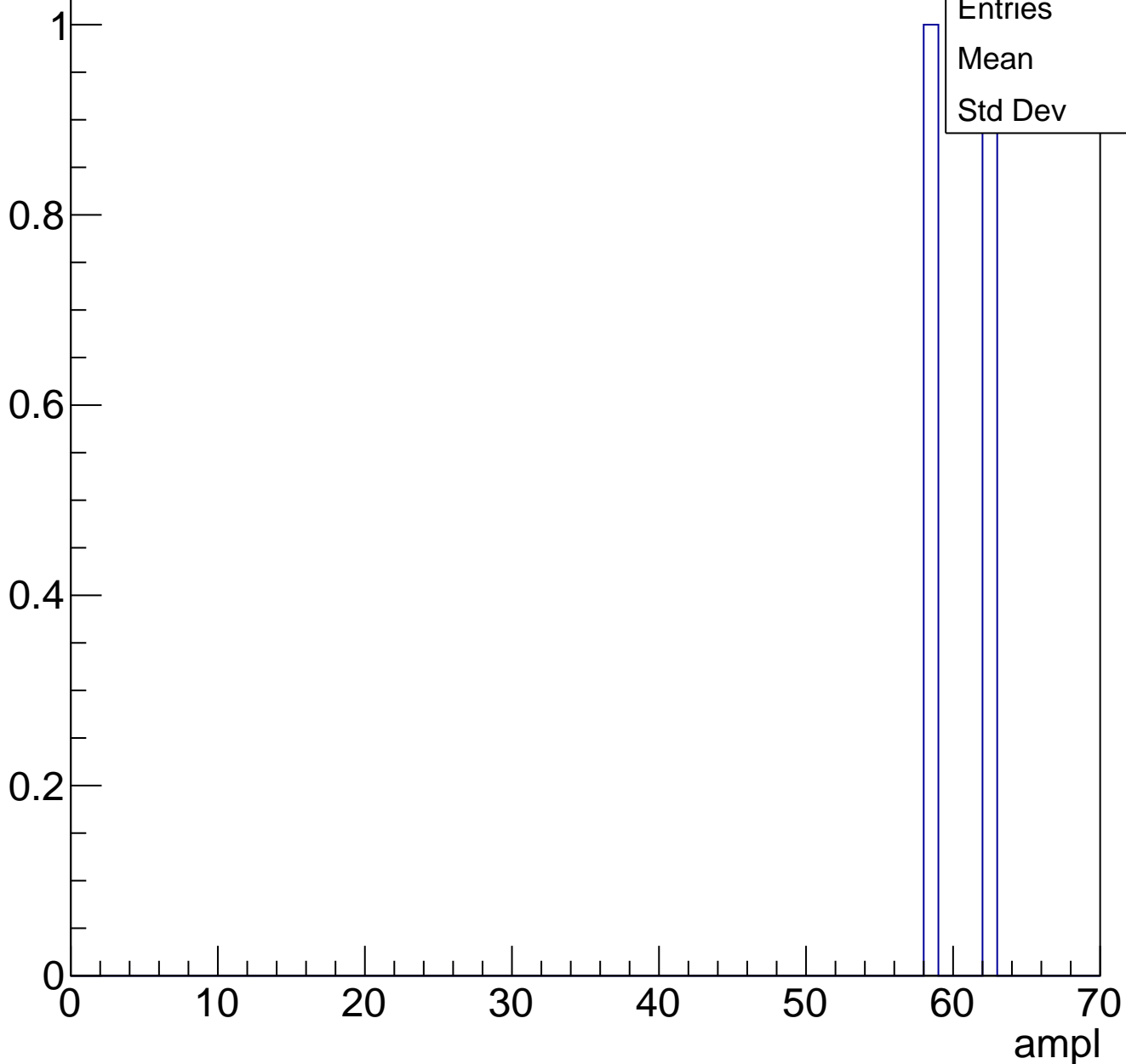
Entries	37
Mean	58.7
Std Dev	10.06



B1L103S, U23-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch59, adc0

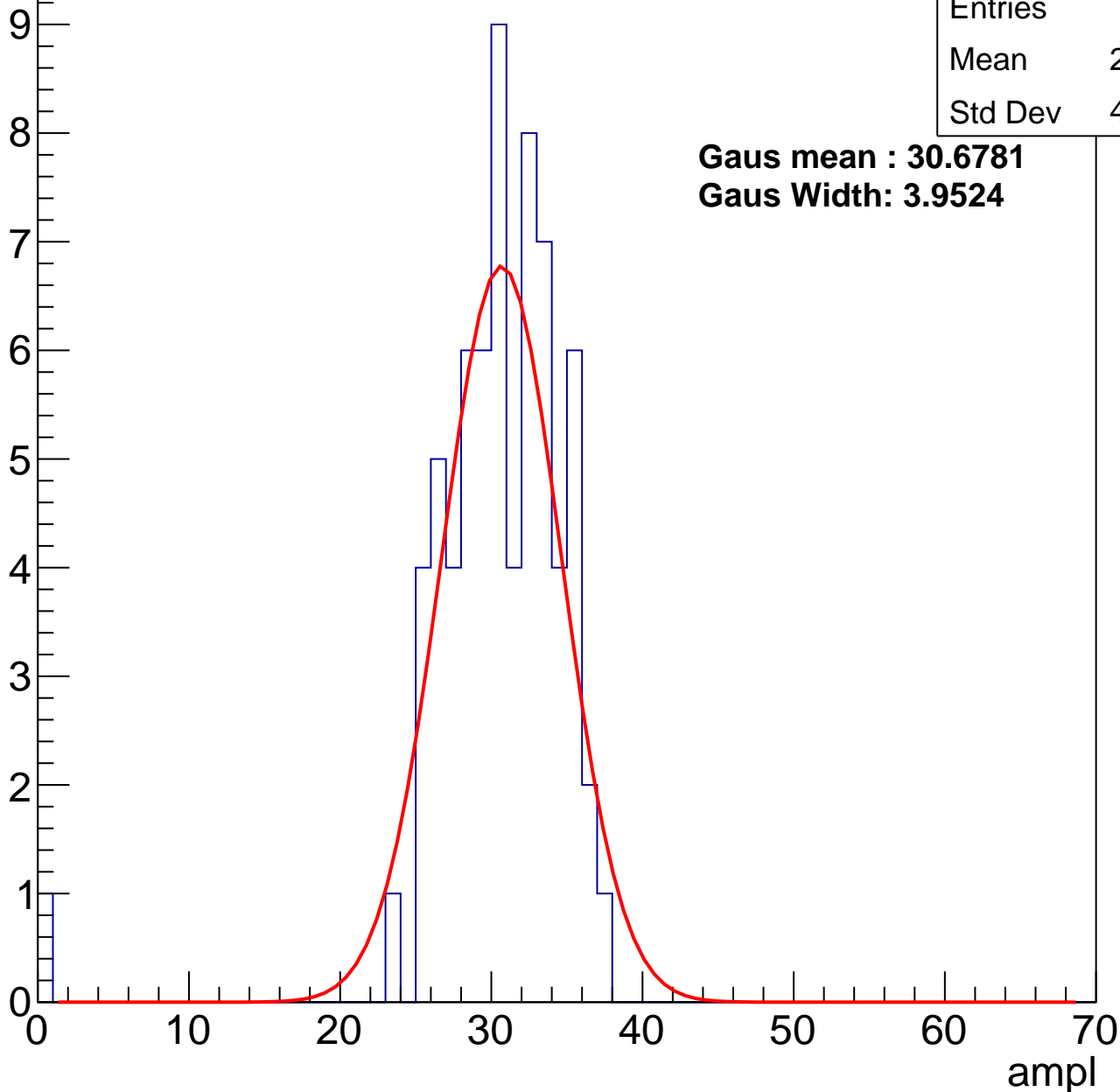
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	29.99
Std Dev	4.894

Gaus mean : 30.6781

Gaus Width: 3.9524



B1L103S, U23-ch59, adc1

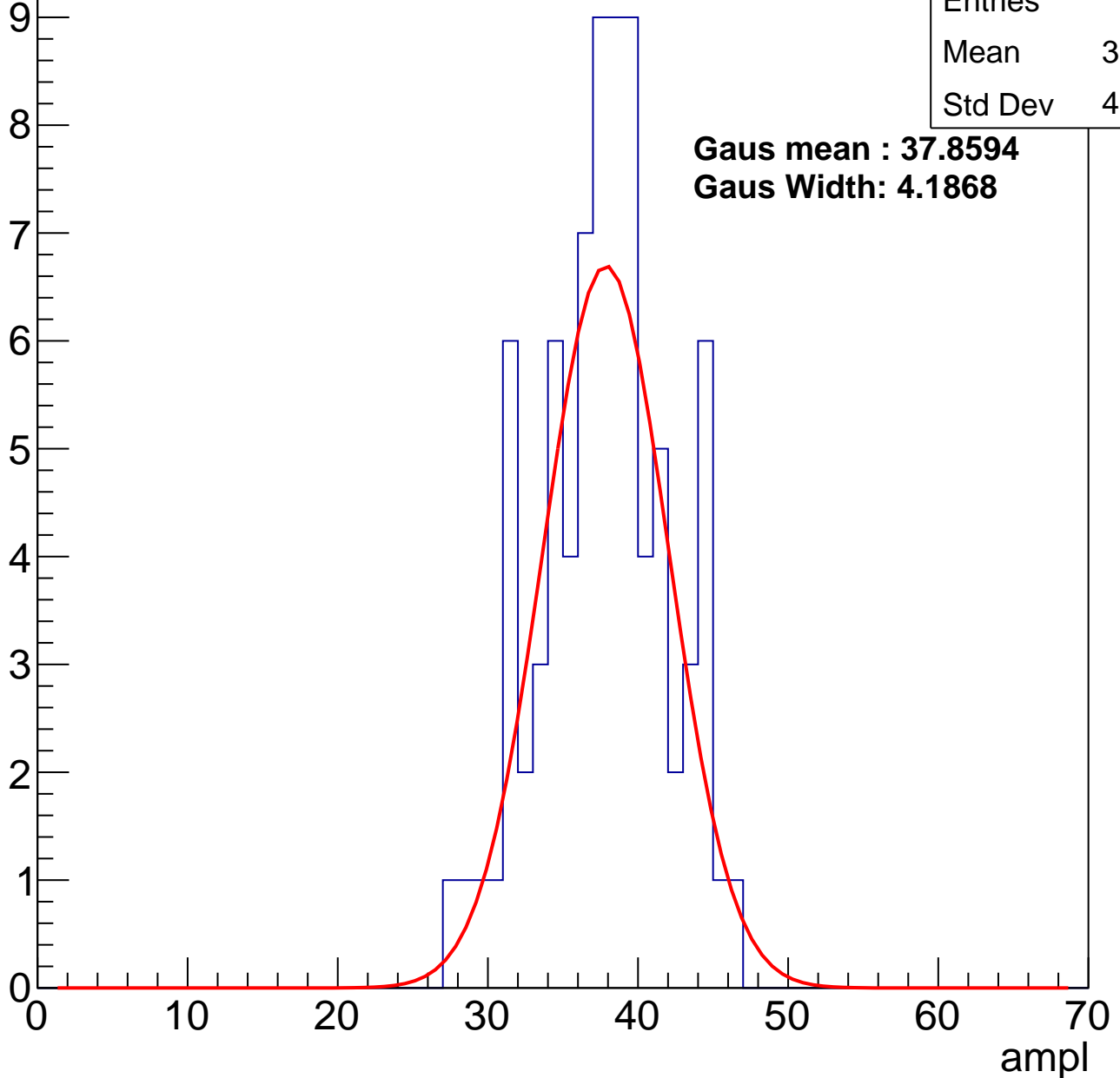
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	37.26
Std Dev	4.213

Gaus mean : 37.8594

Gaus Width: 4.1868



B1L103S, U23-ch59, adc2

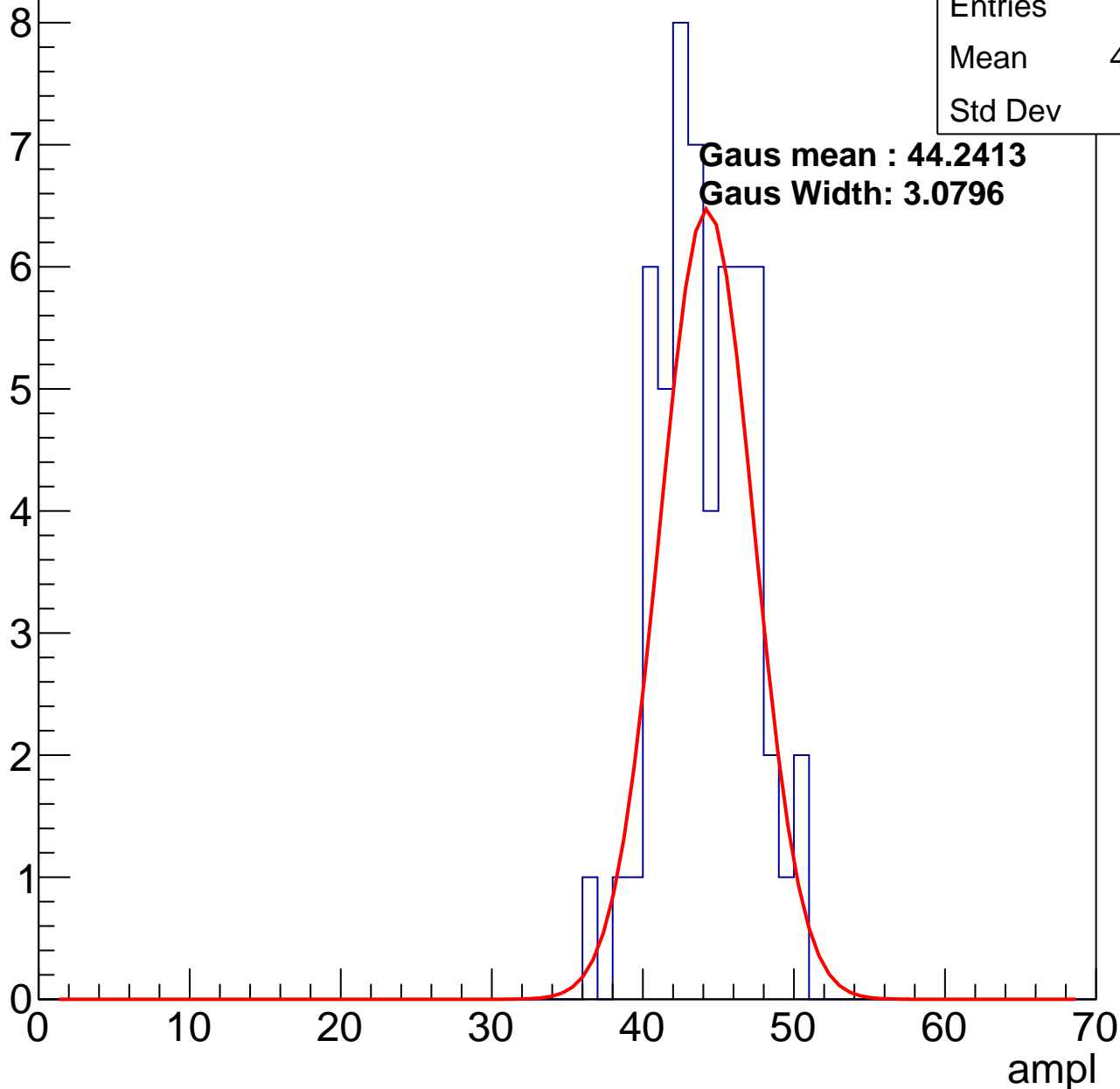
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.64
Std Dev	3.02

Gaus mean : 44.2413

Gaus Width: 3.0796

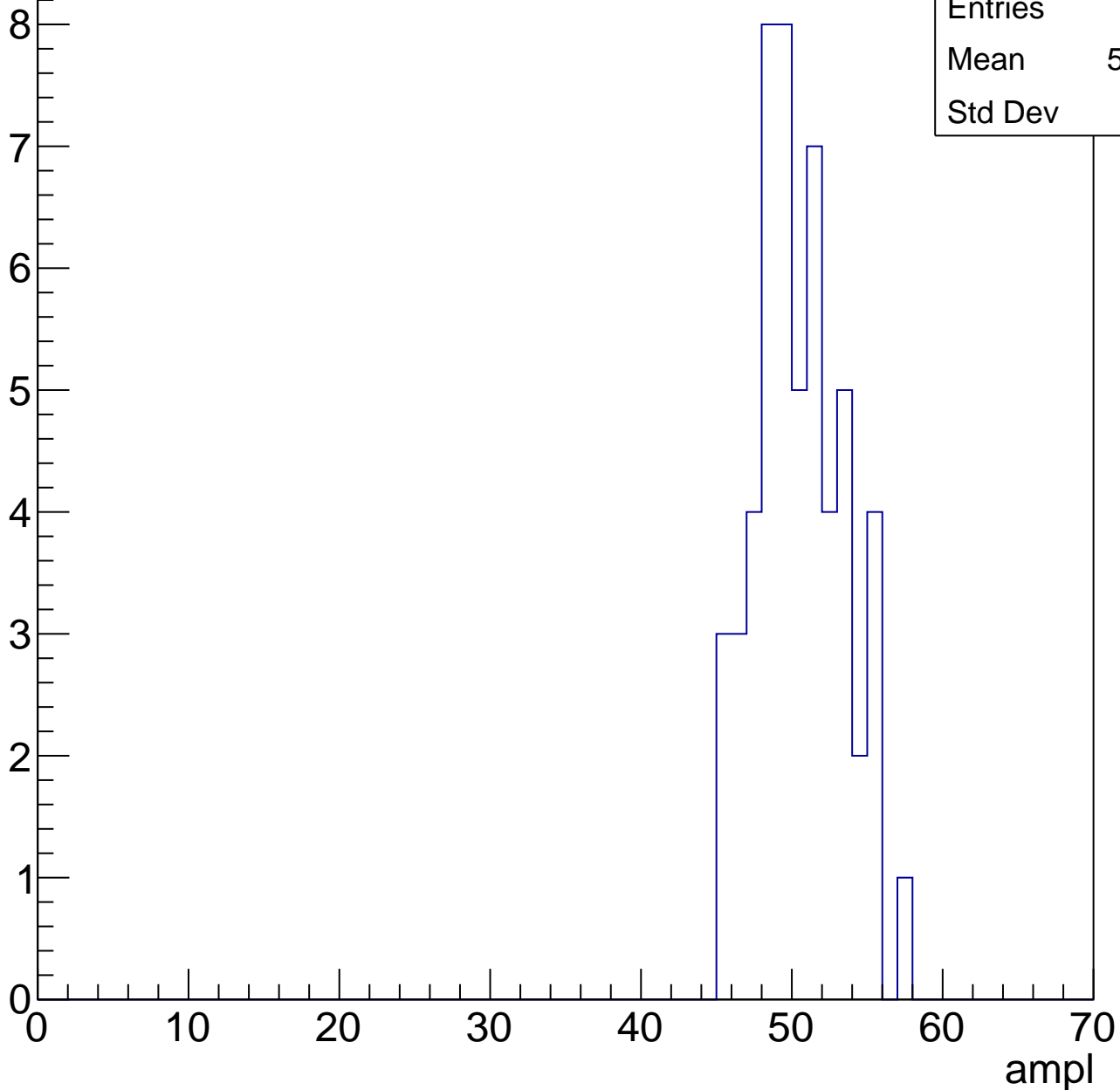


B1L103S, U23-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

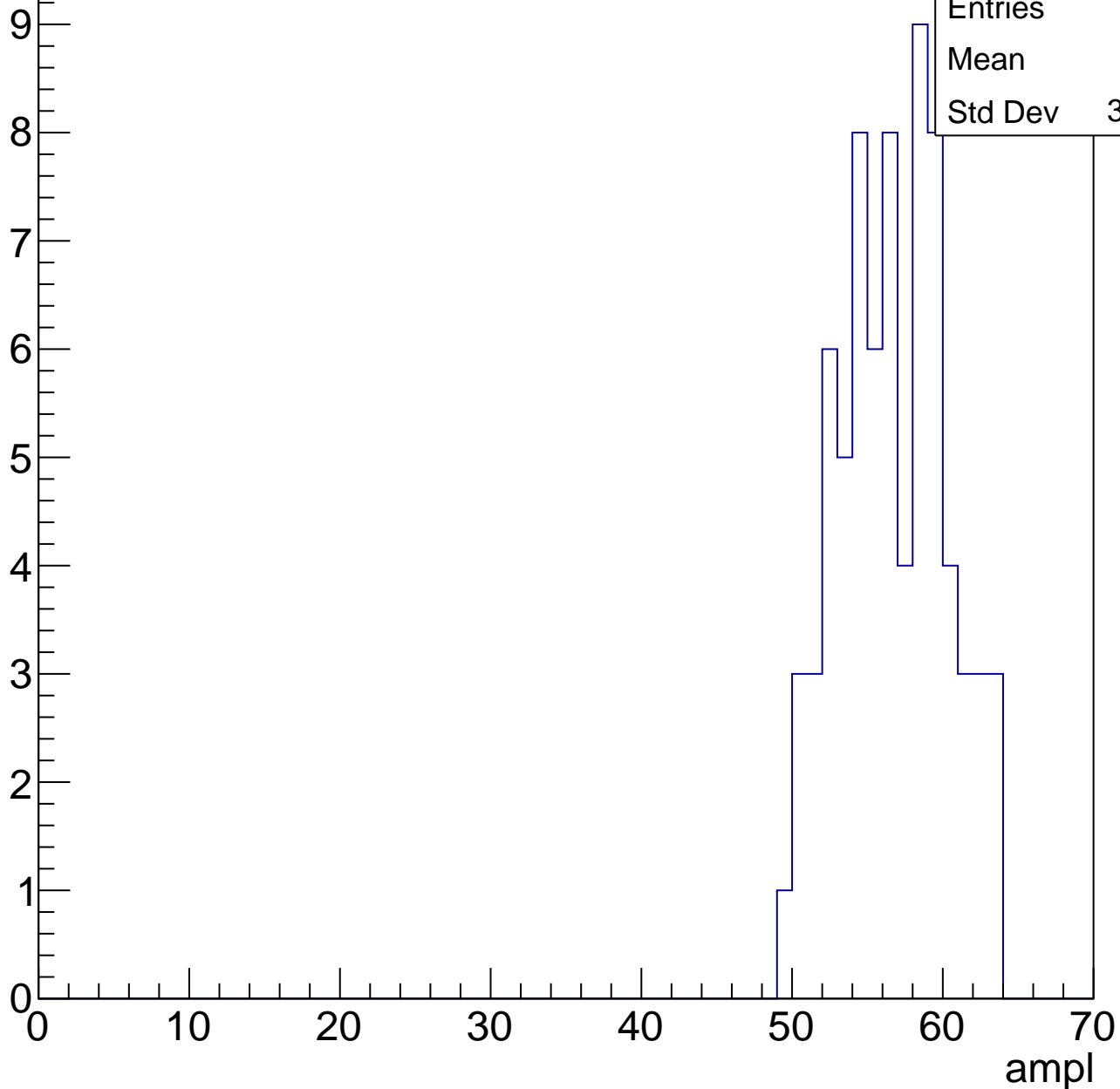
Entries	54
Mean	50.04
Std Dev	2.88



B1L103S, U23-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



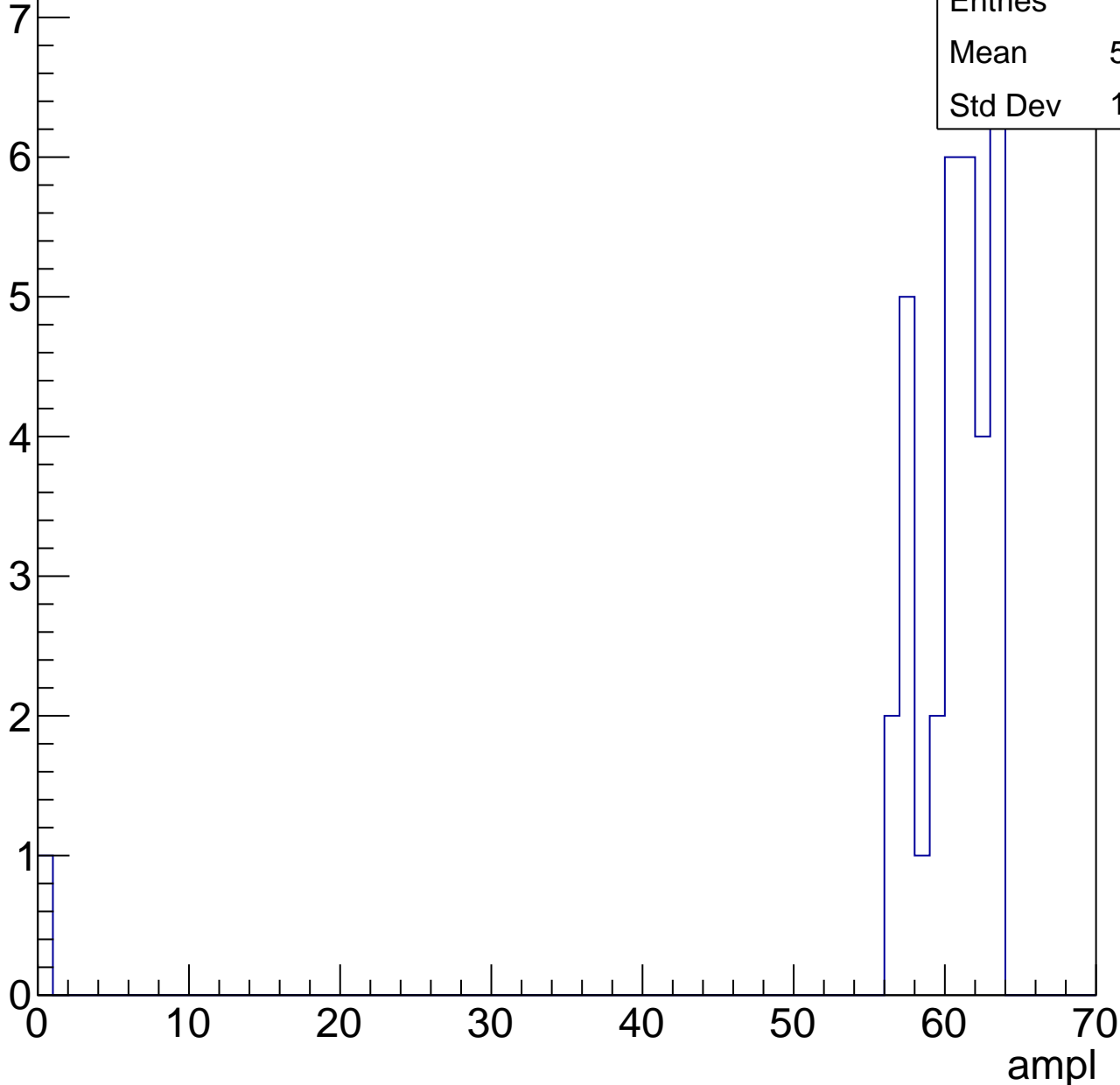
Entries	74
Mean	56.2
Std Dev	3.514

B1L103S, U23-ch59, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	58.47
Std Dev	10.42



B1L103S, U23-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L103S, U23-ch60, adc0

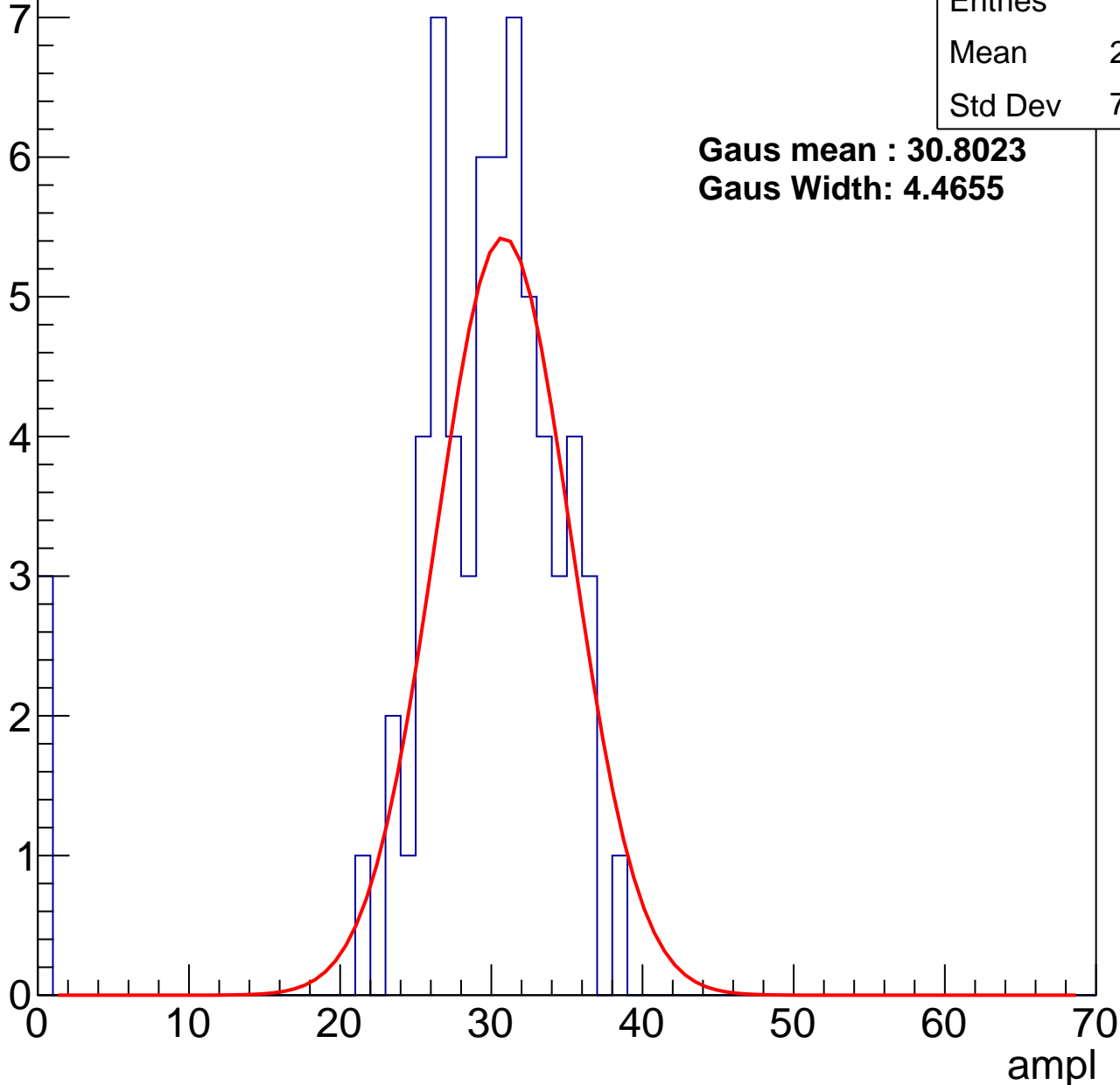
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.38
Std Dev	7.283

Gaus mean : 30.8023

Gaus Width: 4.4655



B1L103S, U23-ch60, adc1

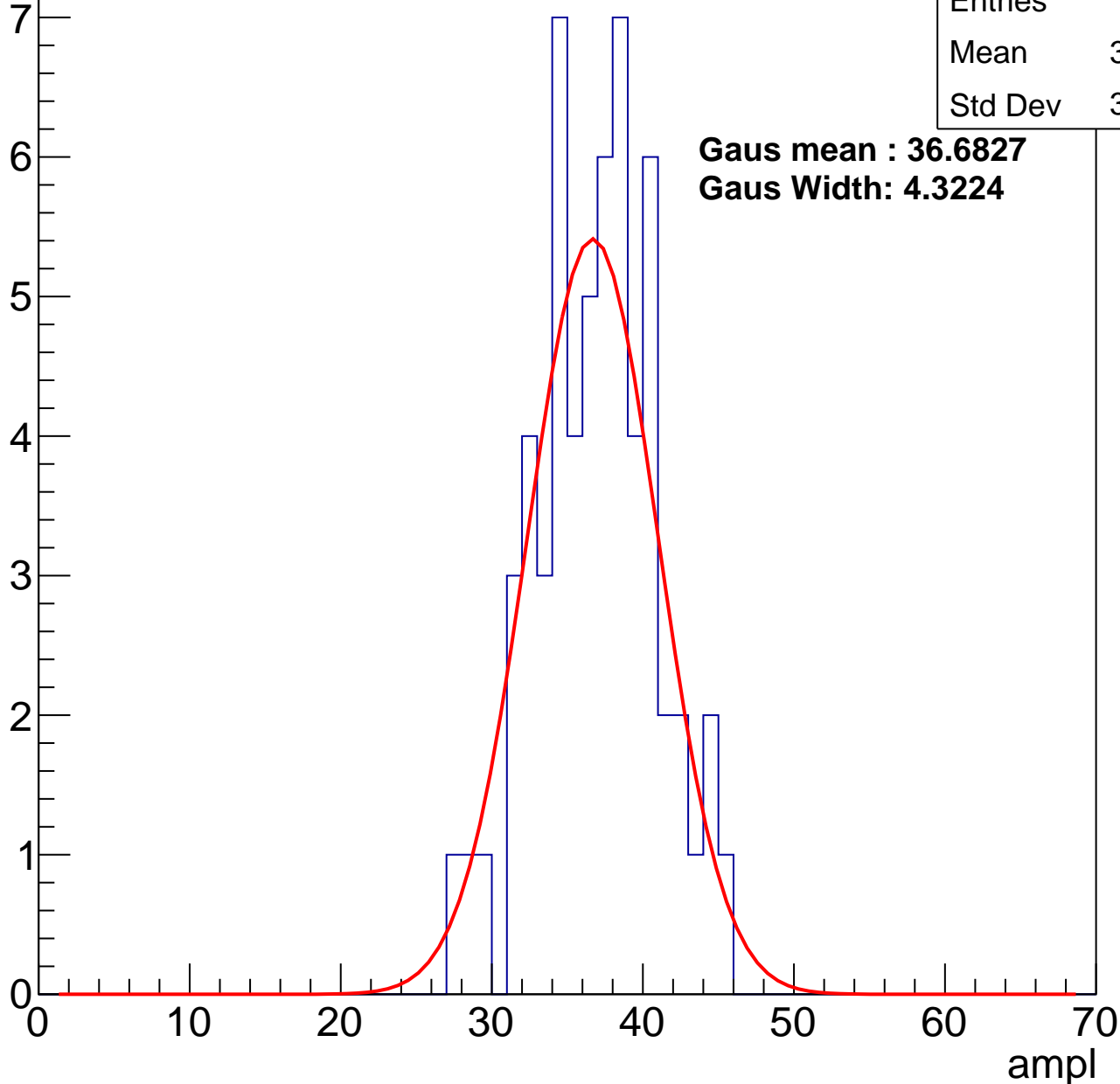
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.47
Std Dev	3.926

Gaus mean : 36.6827

Gaus Width: 4.3224



B1L103S, U23-ch60, adc2

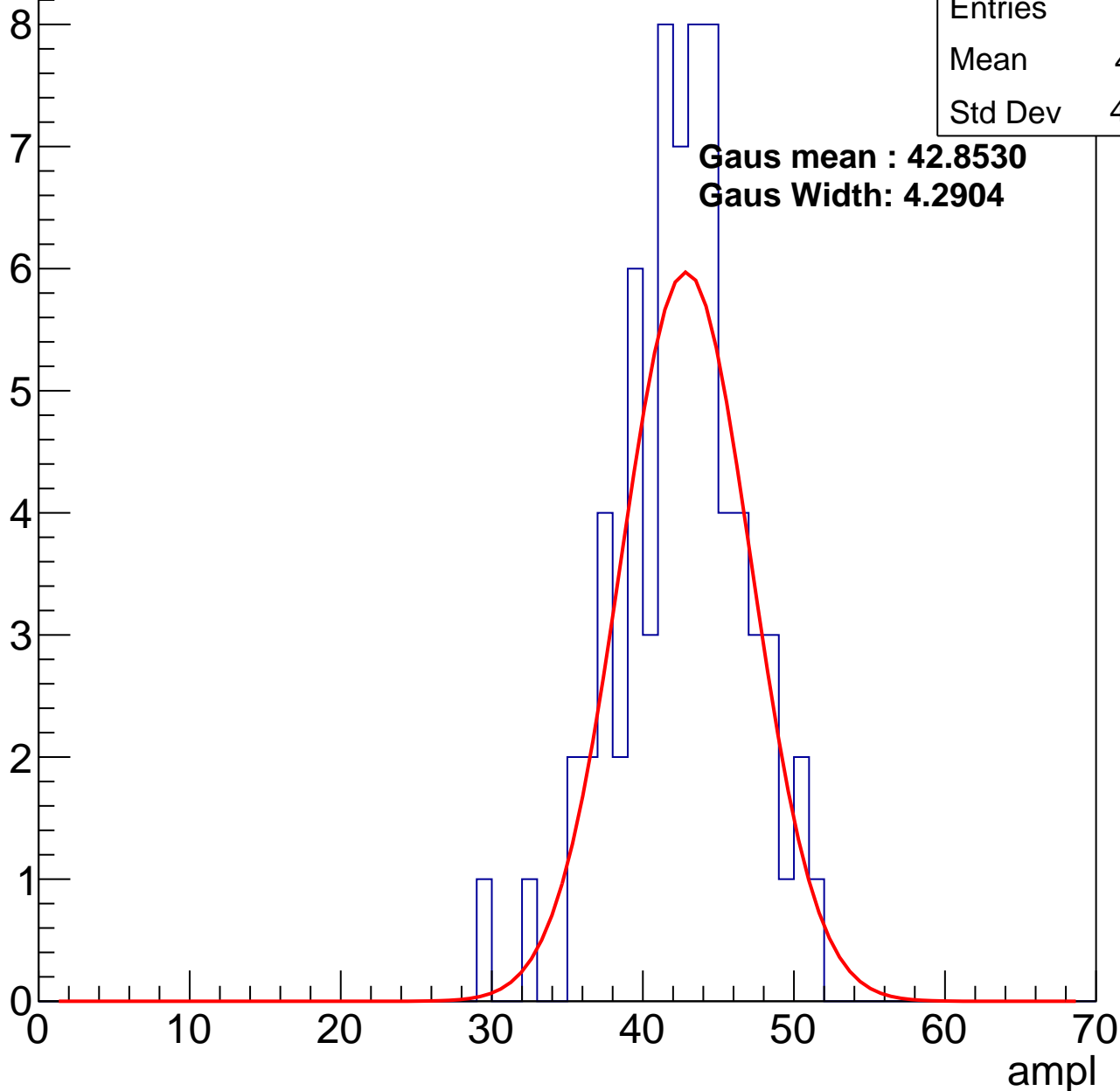
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.11
Std Dev	4.183

Gaus mean : 42.8530

Gaus Width: 4.2904

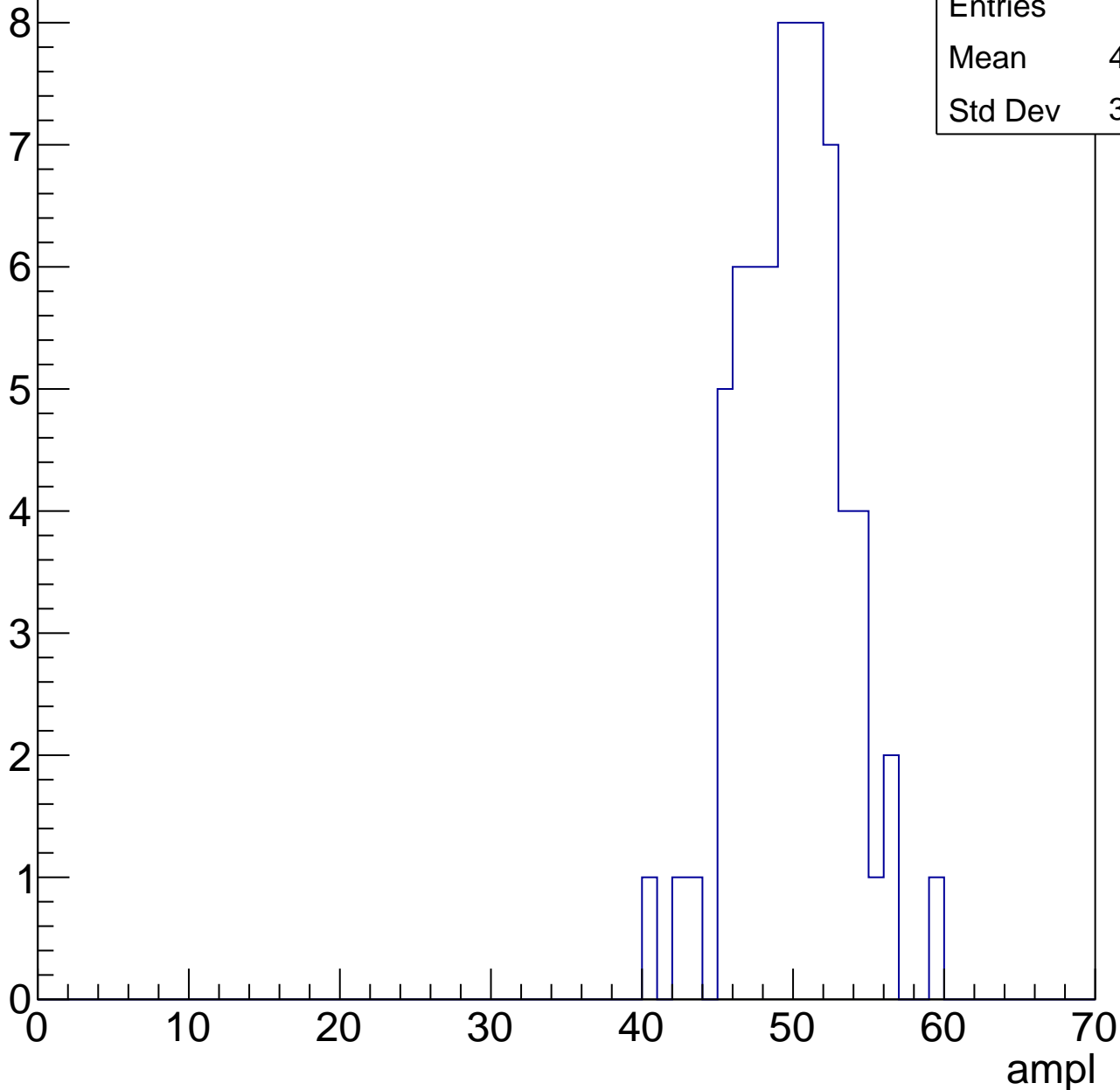


B1L103S, U23-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.48
Std Dev	3.437

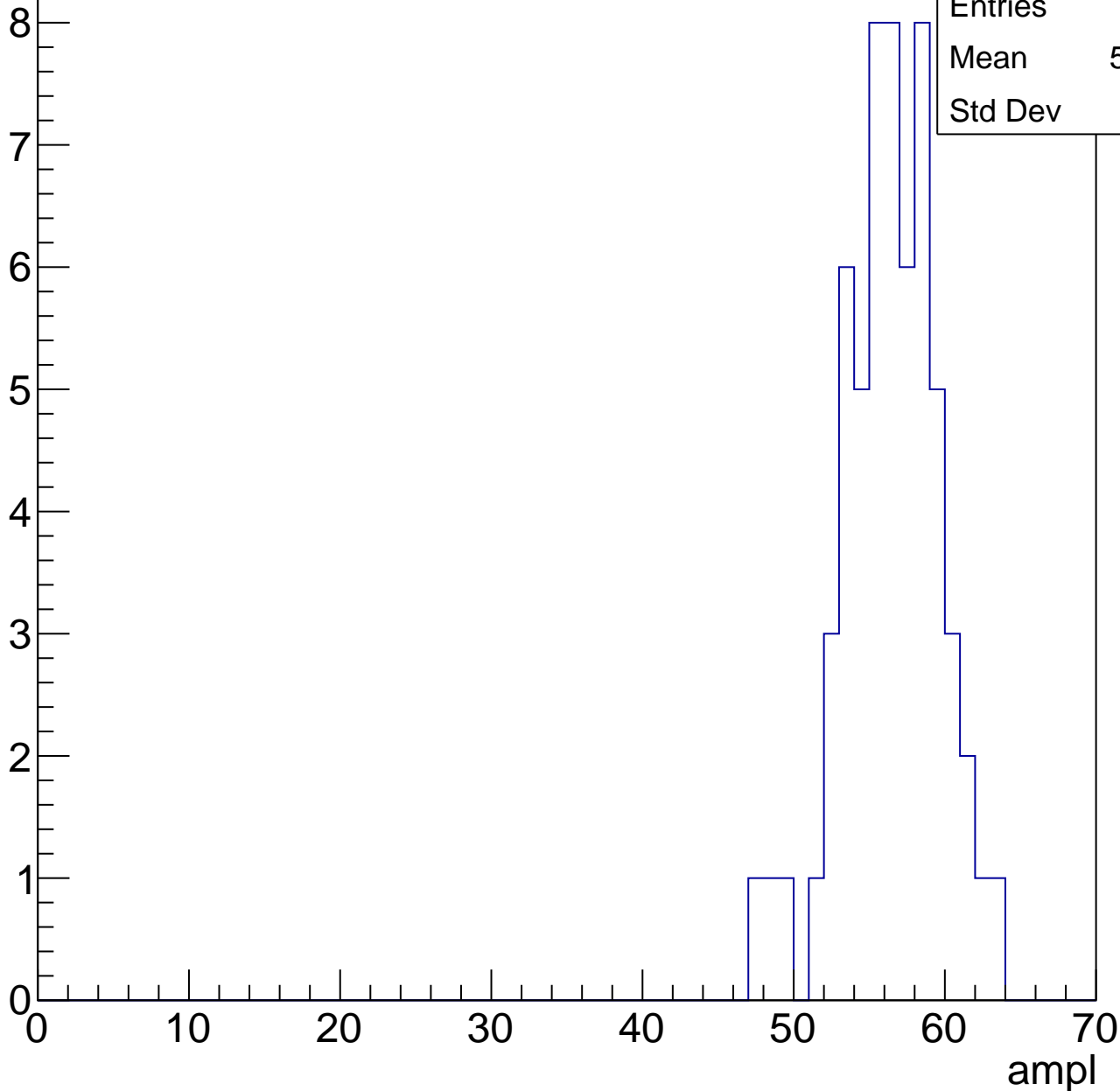


B1L103S, U23-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.92
Std Dev	3.2

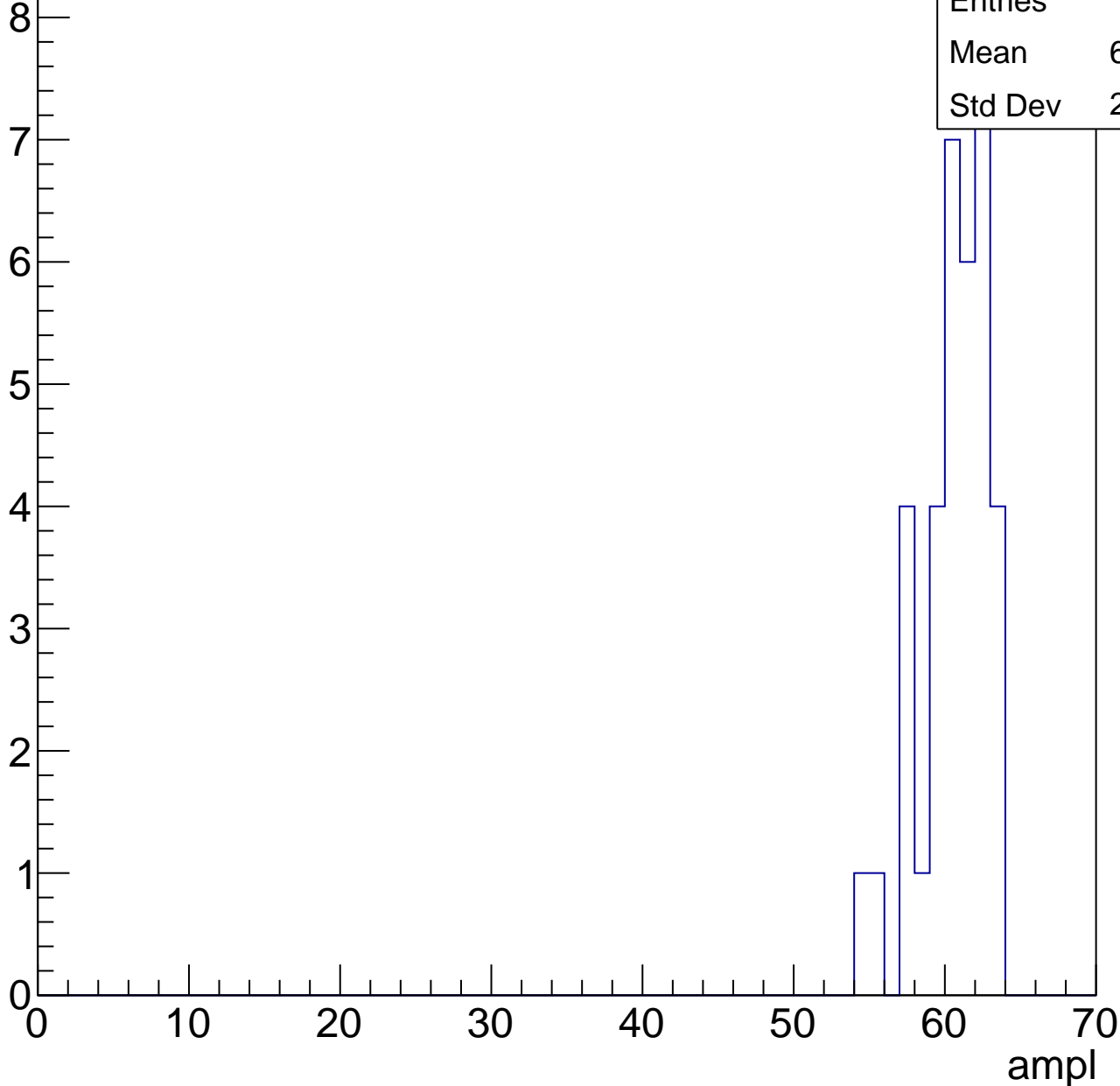


B1L103S, U23-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

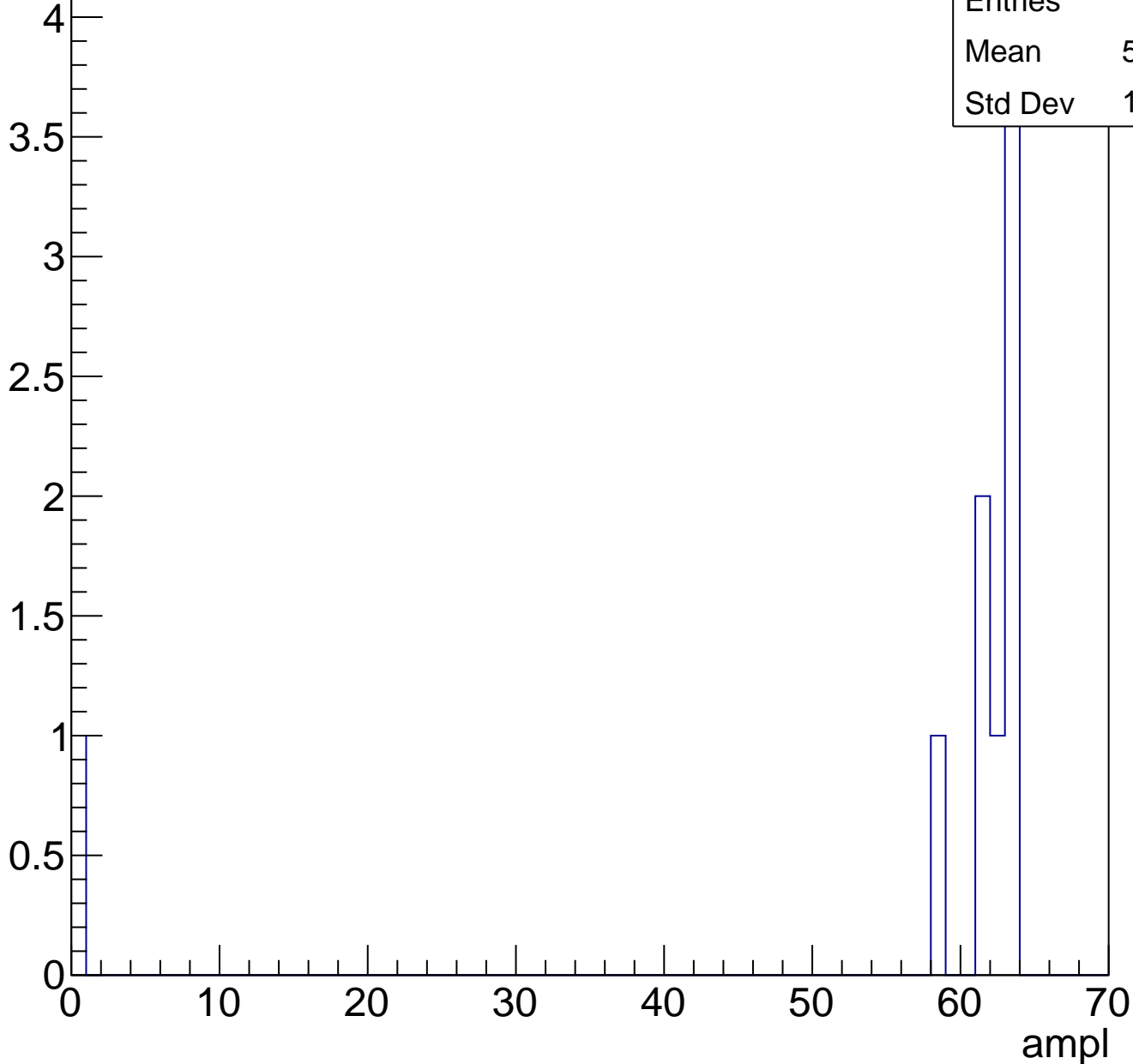
Entries	36
Mean	60.14
Std Dev	2.226



B1L103S, U23-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U23-ch61, adc0

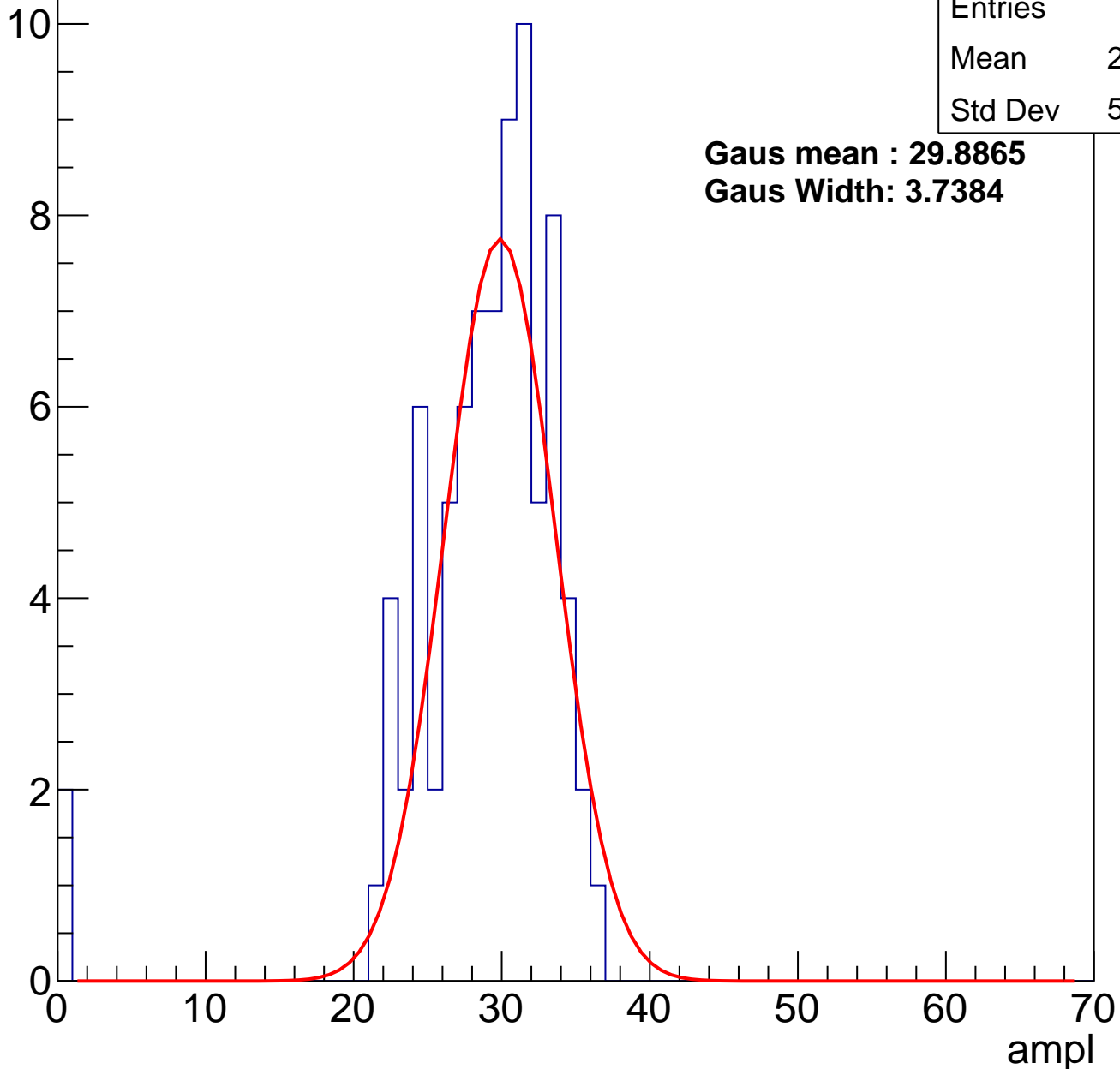
calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	28.22
Std Dev	5.737

Gaus mean : 29.8865

Gaus Width: 3.7384

Entry



B1L103S, U23-ch61, adc1

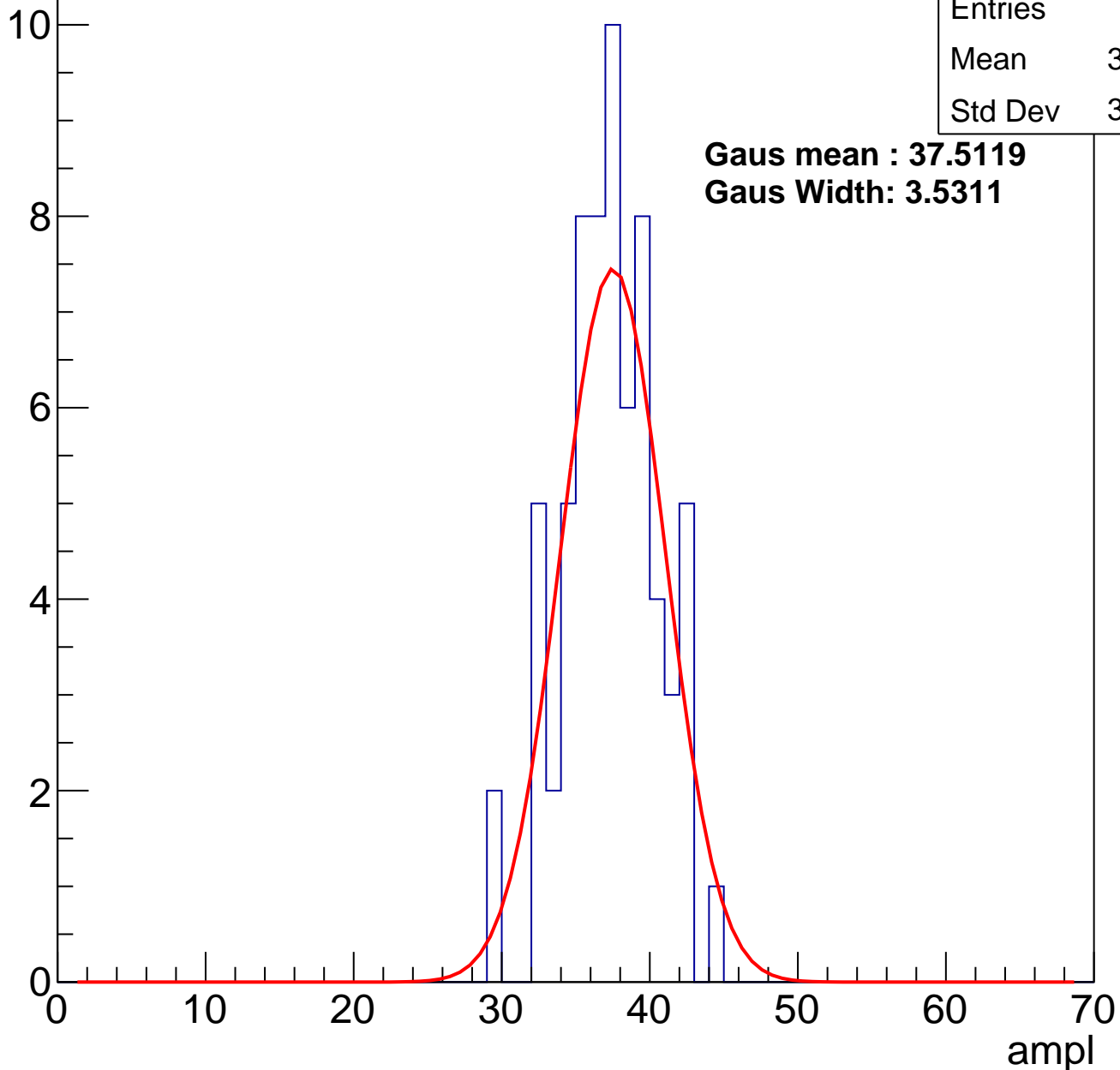
calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	36.85
Std Dev	3.149

Gaus mean : 37.5119

Gaus Width: 3.5311

Entry



B1L103S, U23-ch61, adc2

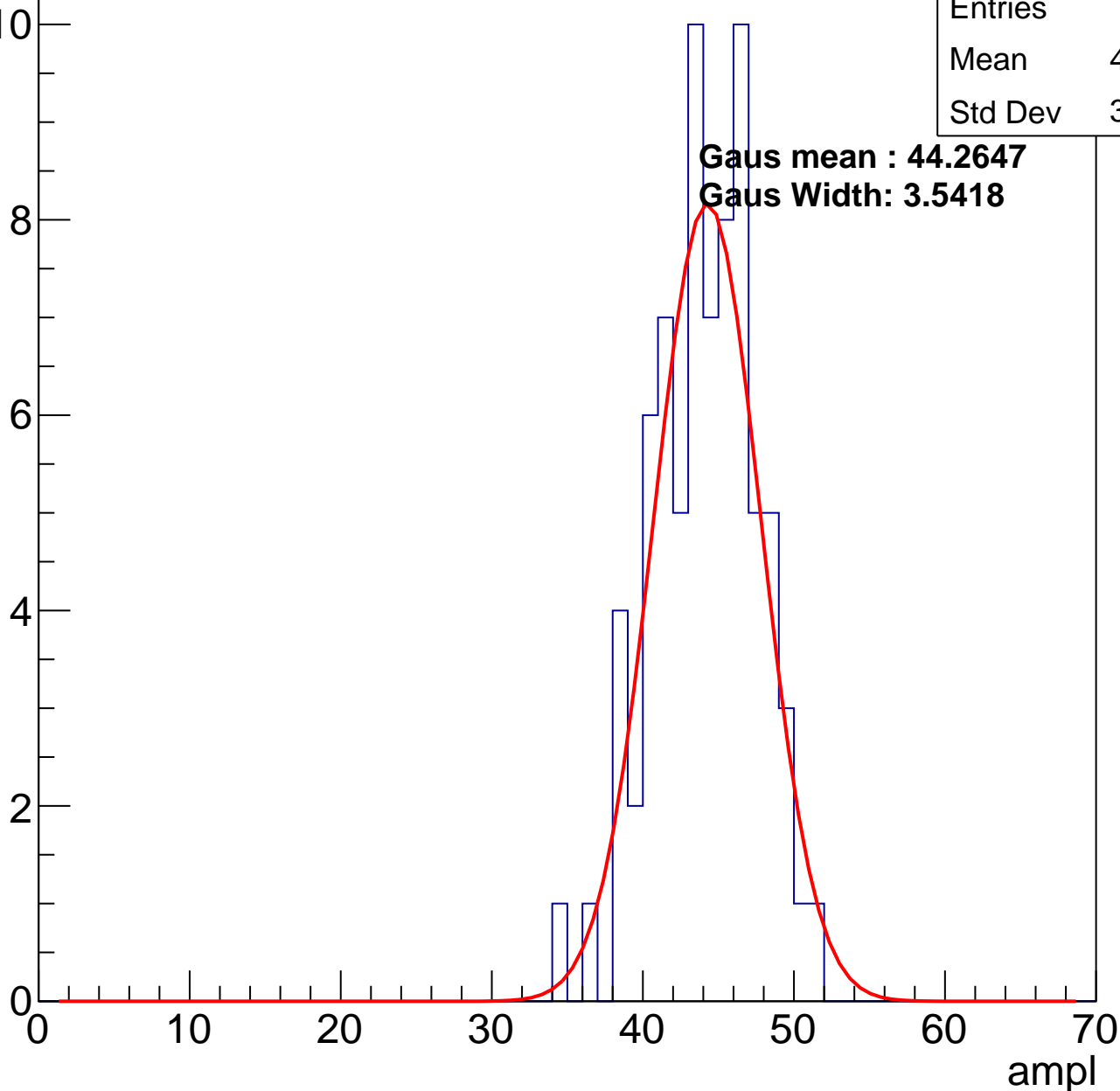
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.66
Std Dev	3.386

Gaus mean : 44.2647

Gaus Width: 3.5418

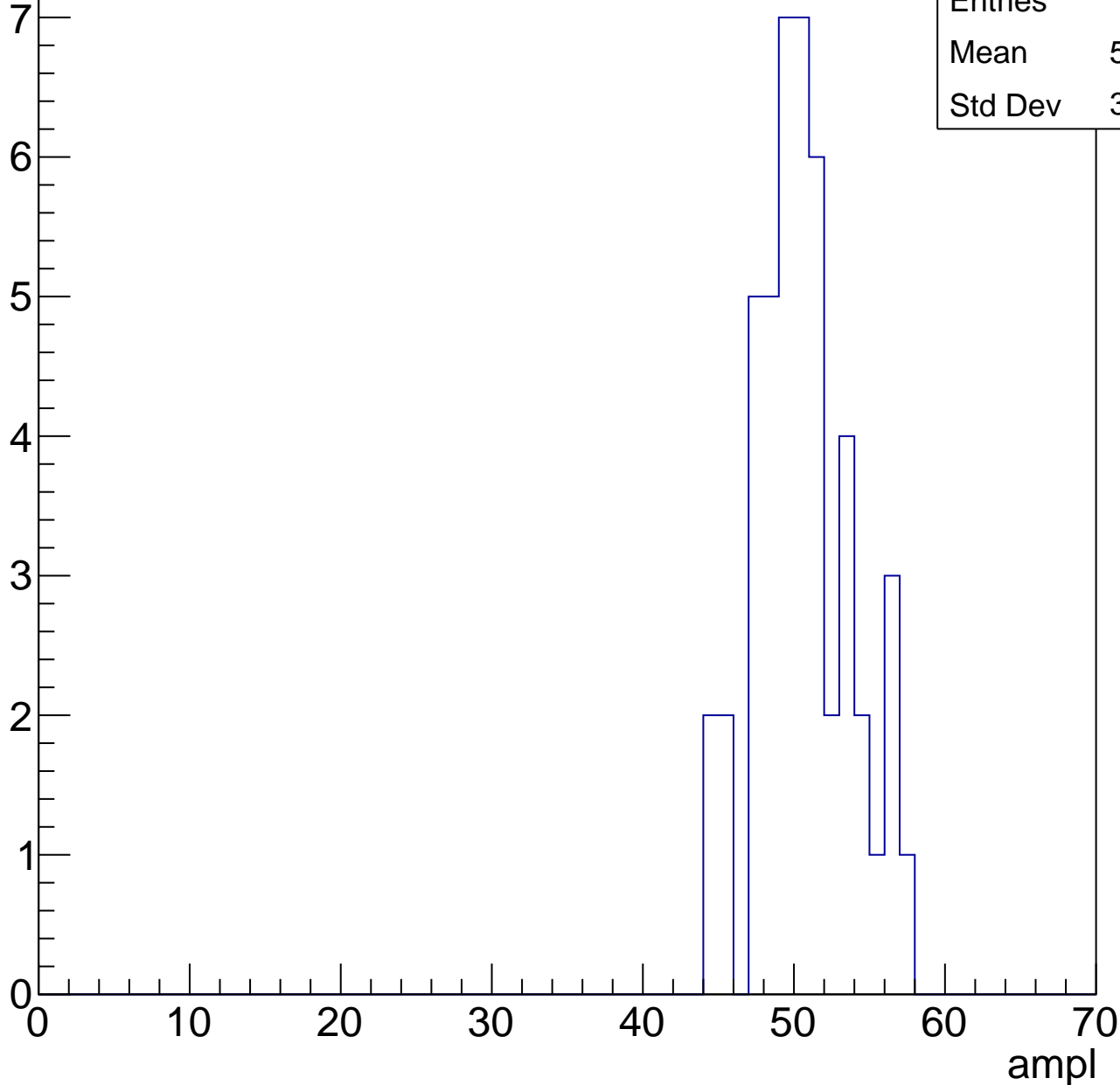


B1L103S, U23-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	50.13
Std Dev	3.119

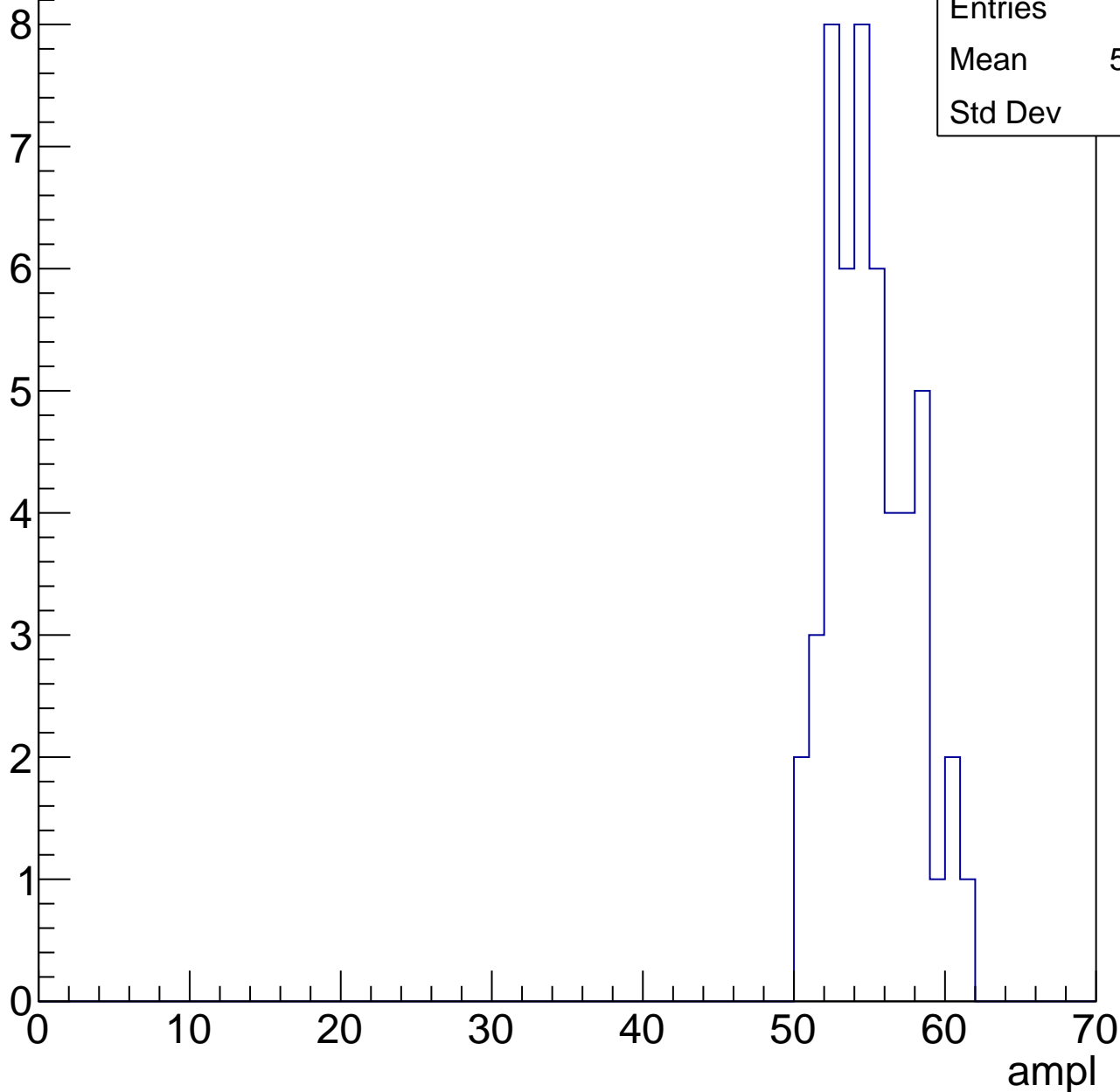


B1L103S, U23-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

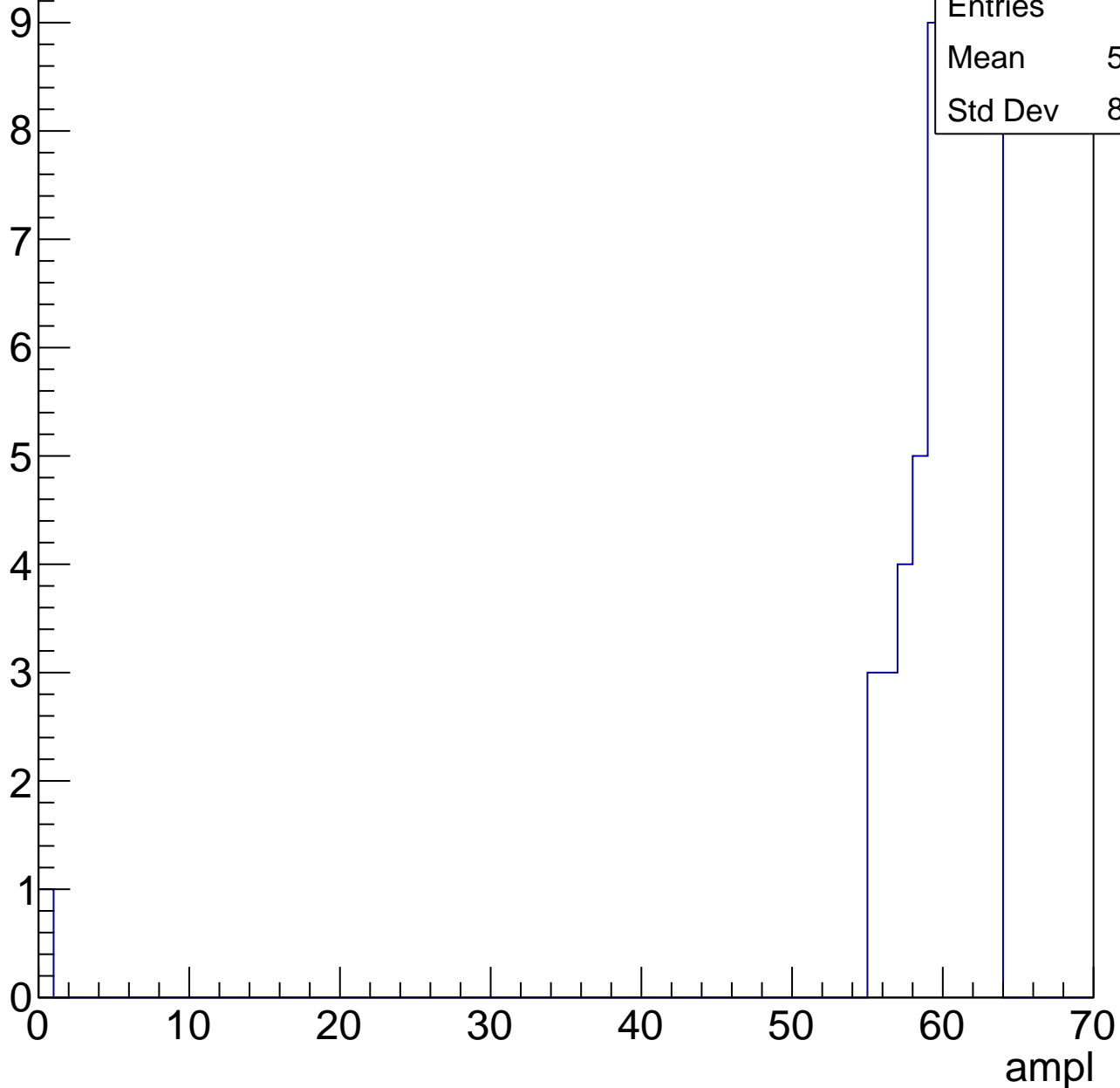
Entries	50
Mean	54.62
Std Dev	2.69



B1L103S, U23-ch61, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

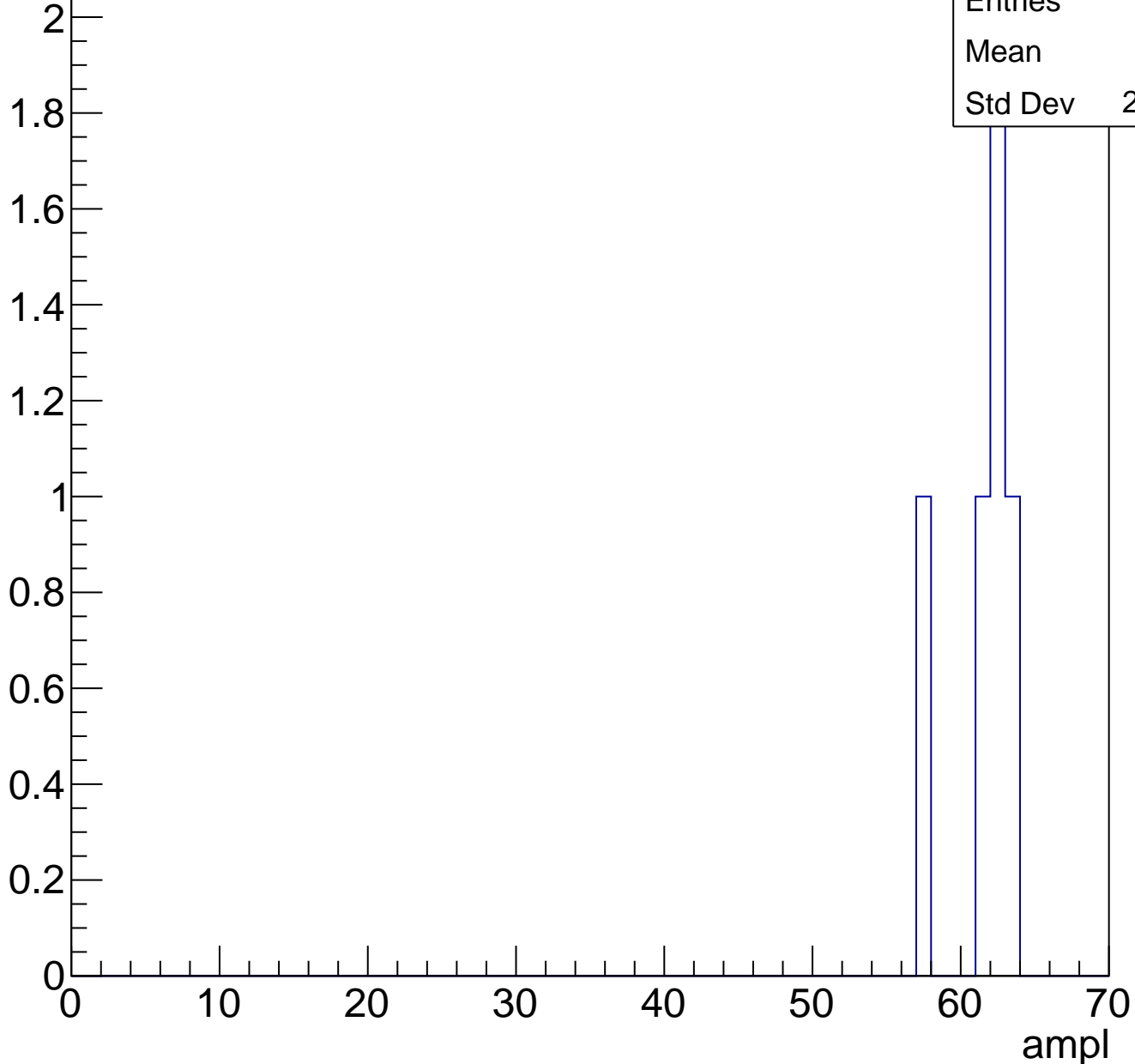


Entries	58
Mean	58.84
Std Dev	8.128

B1L103S, U23-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch62, adc0

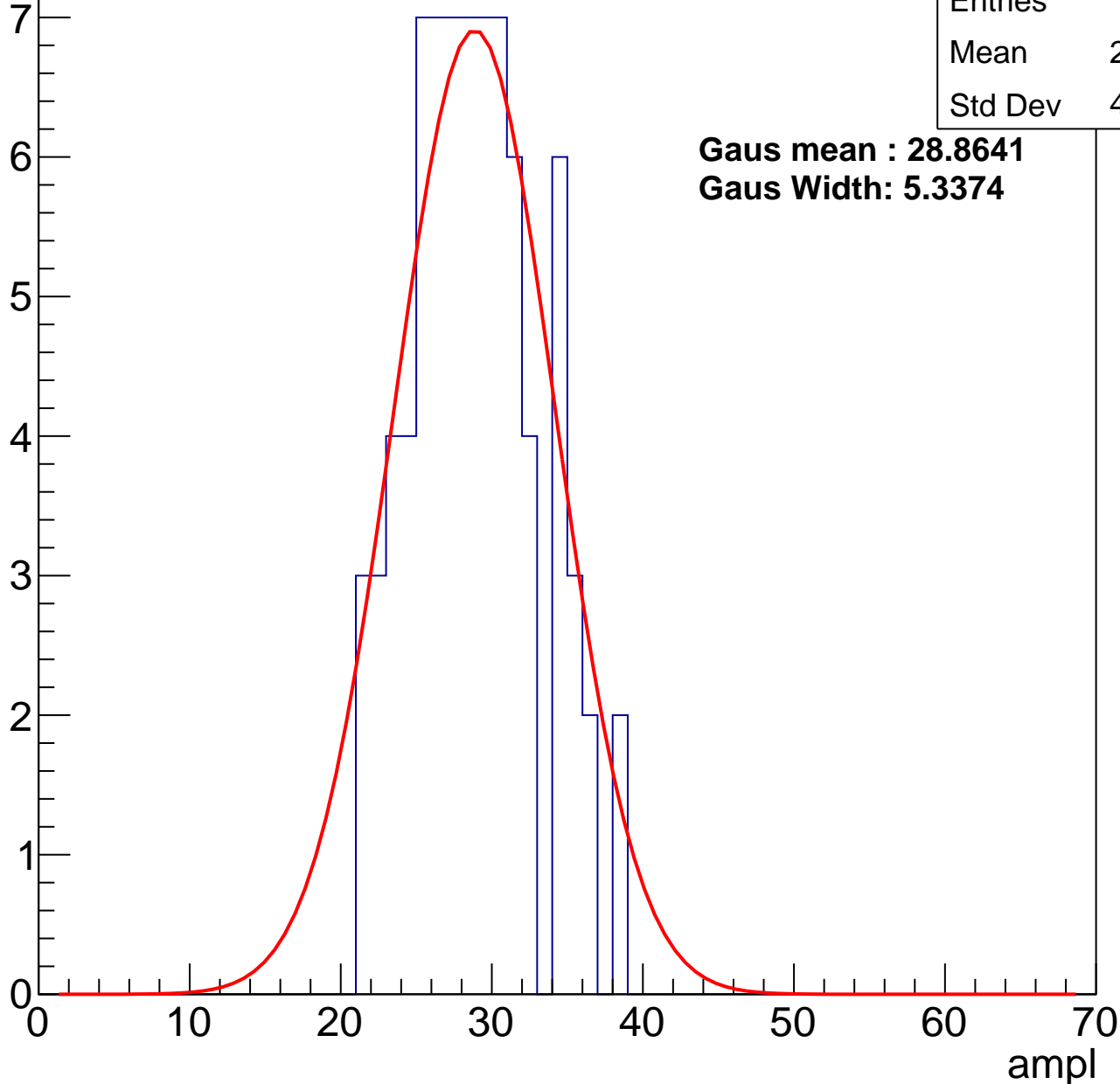
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.39
Std Dev	4.132

Gaus mean : 28.8641

Gaus Width: 5.3374



B1L103S, U23-ch62, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	68
Mean	35.16
Std Dev	3.701

Gaus mean : 35.0222

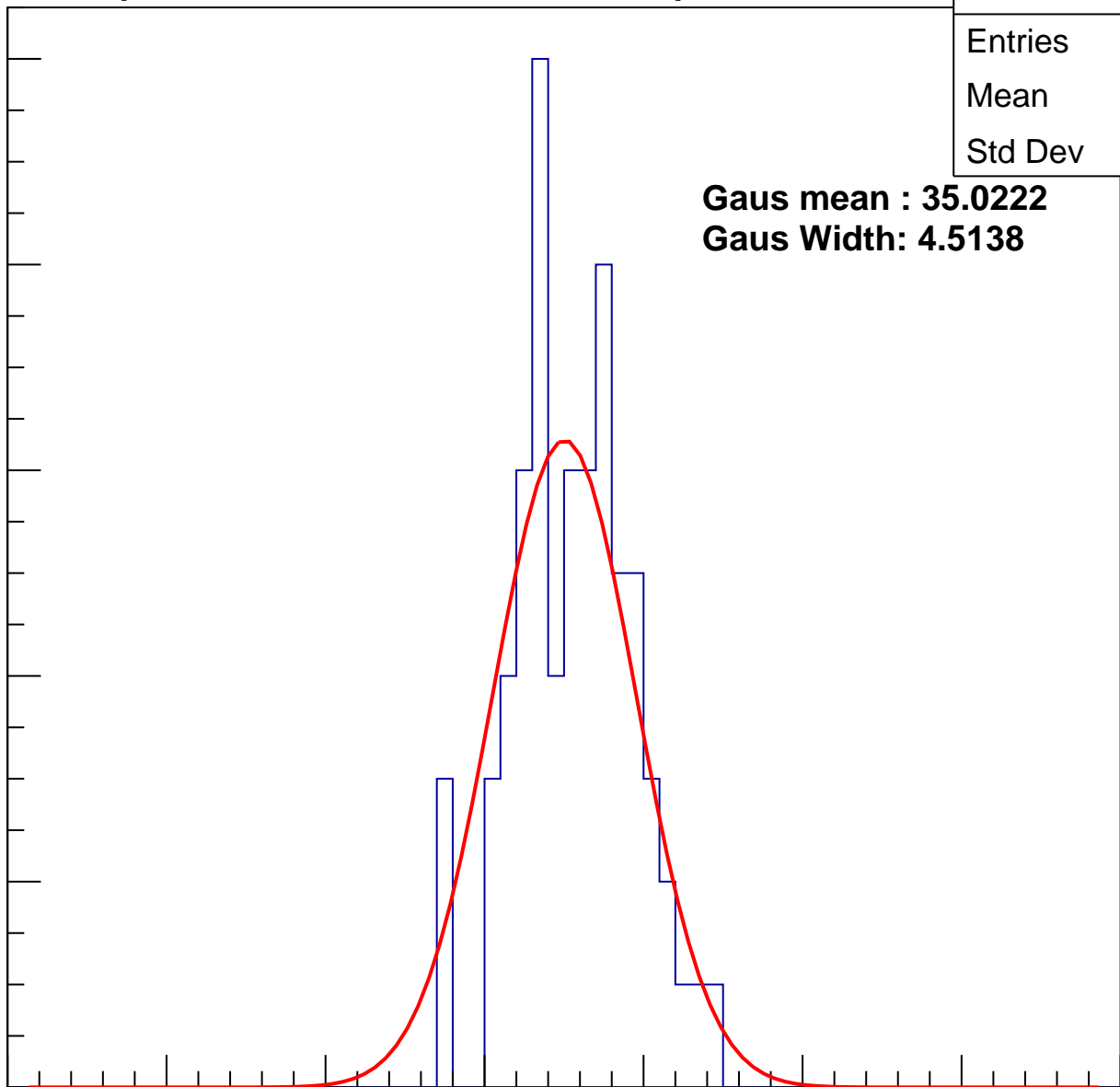
Gaus Width: 4.5138

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch62, adc2

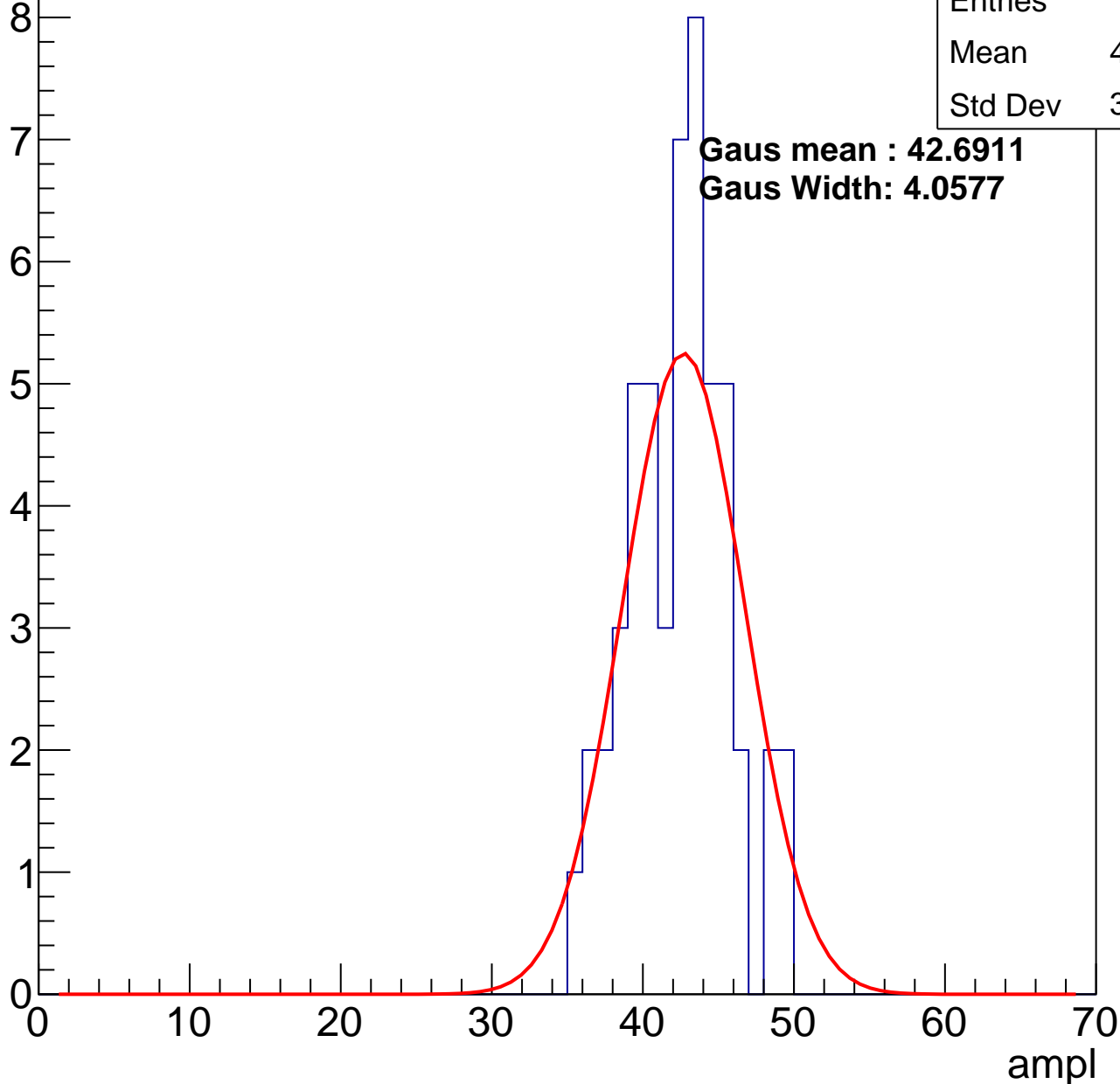
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	41.96
Std Dev	3.287

Gaus mean : 42.6911

Gaus Width: 4.0577



B1L103S, U23-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	48.32
Std Dev	4.445

Entry

10

8

6

4

2

0

0

10

20

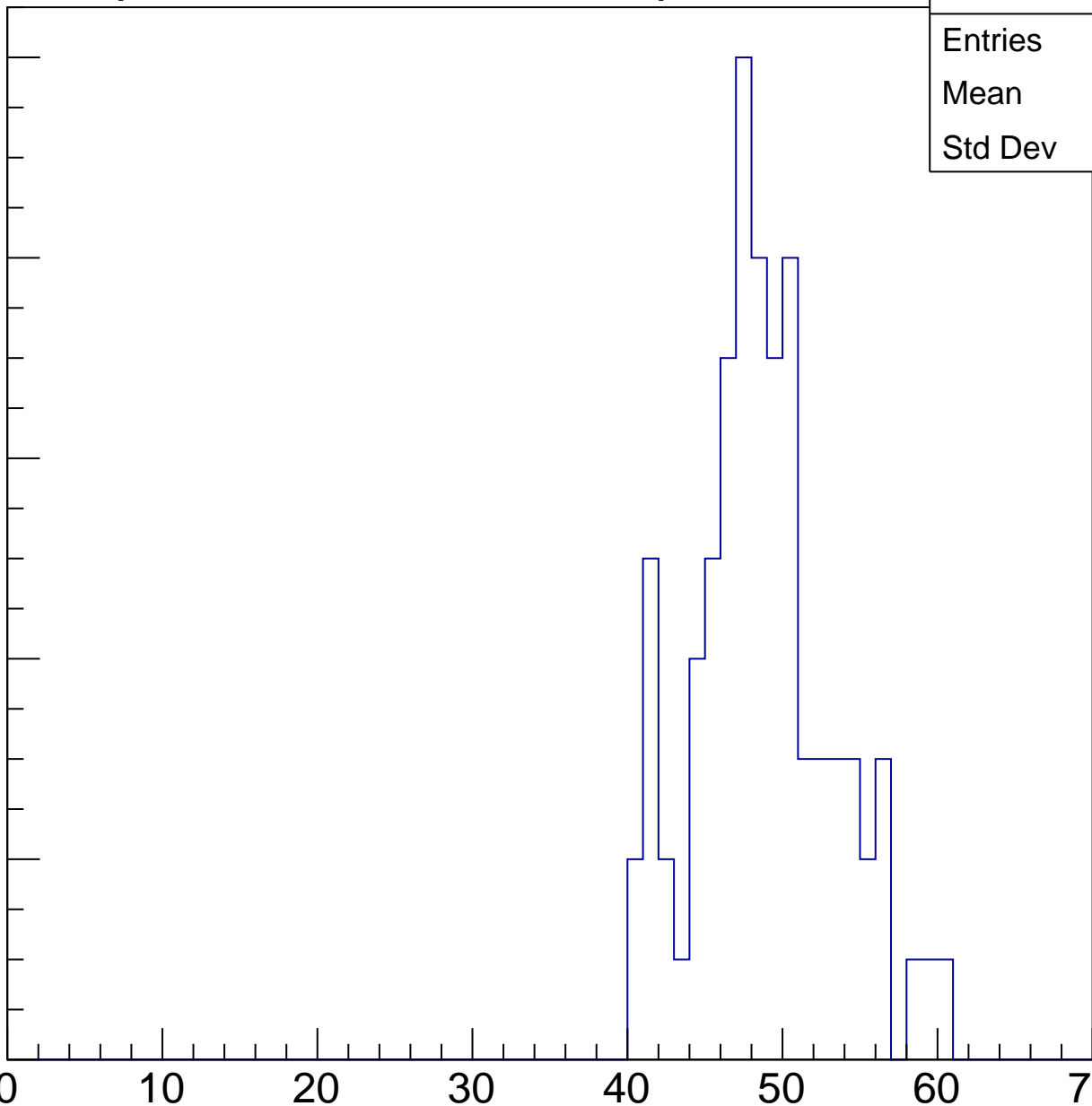
30

40

50

60

ampl

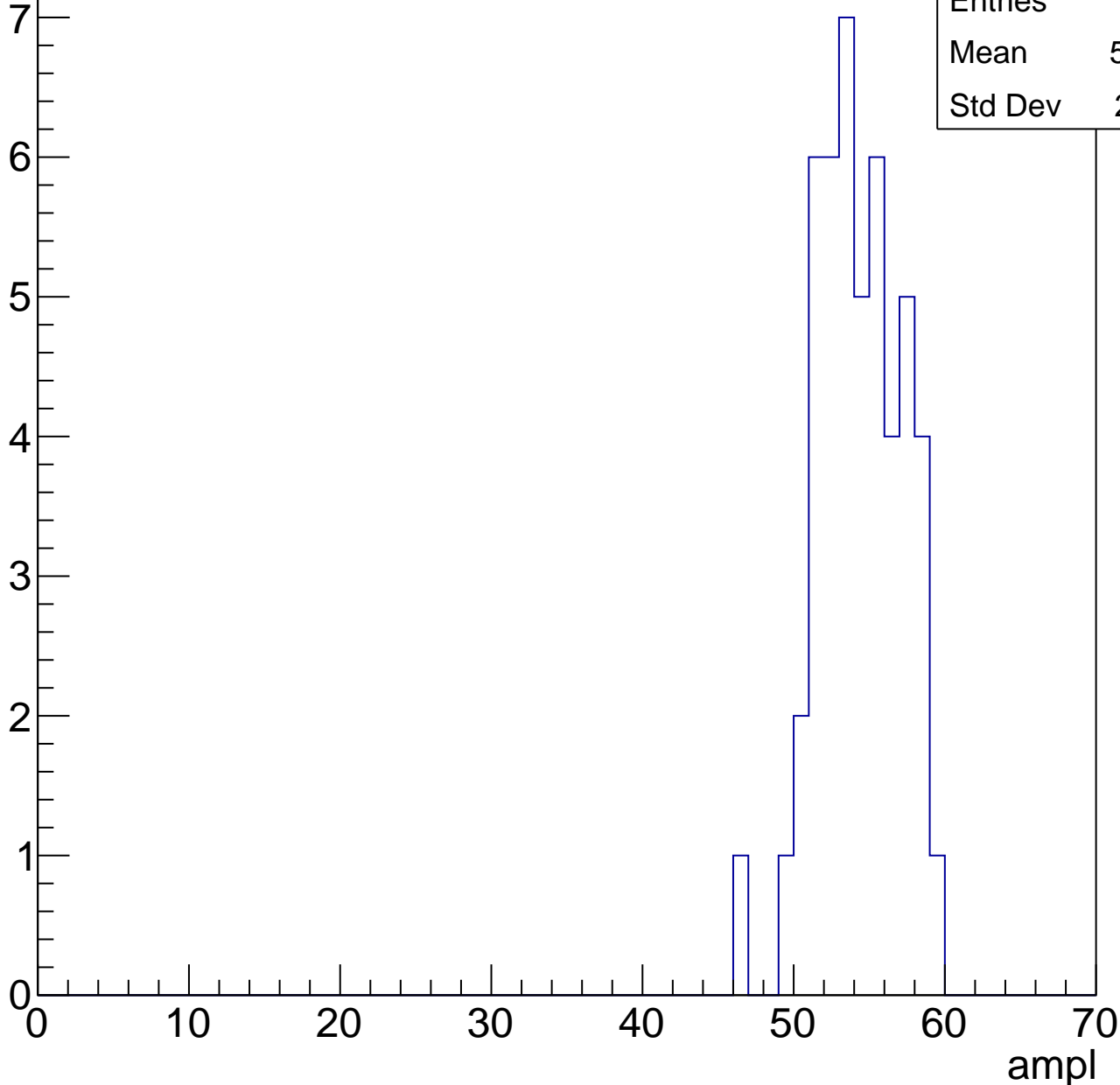


B1L103S, U23-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	53.83
Std Dev	2.741

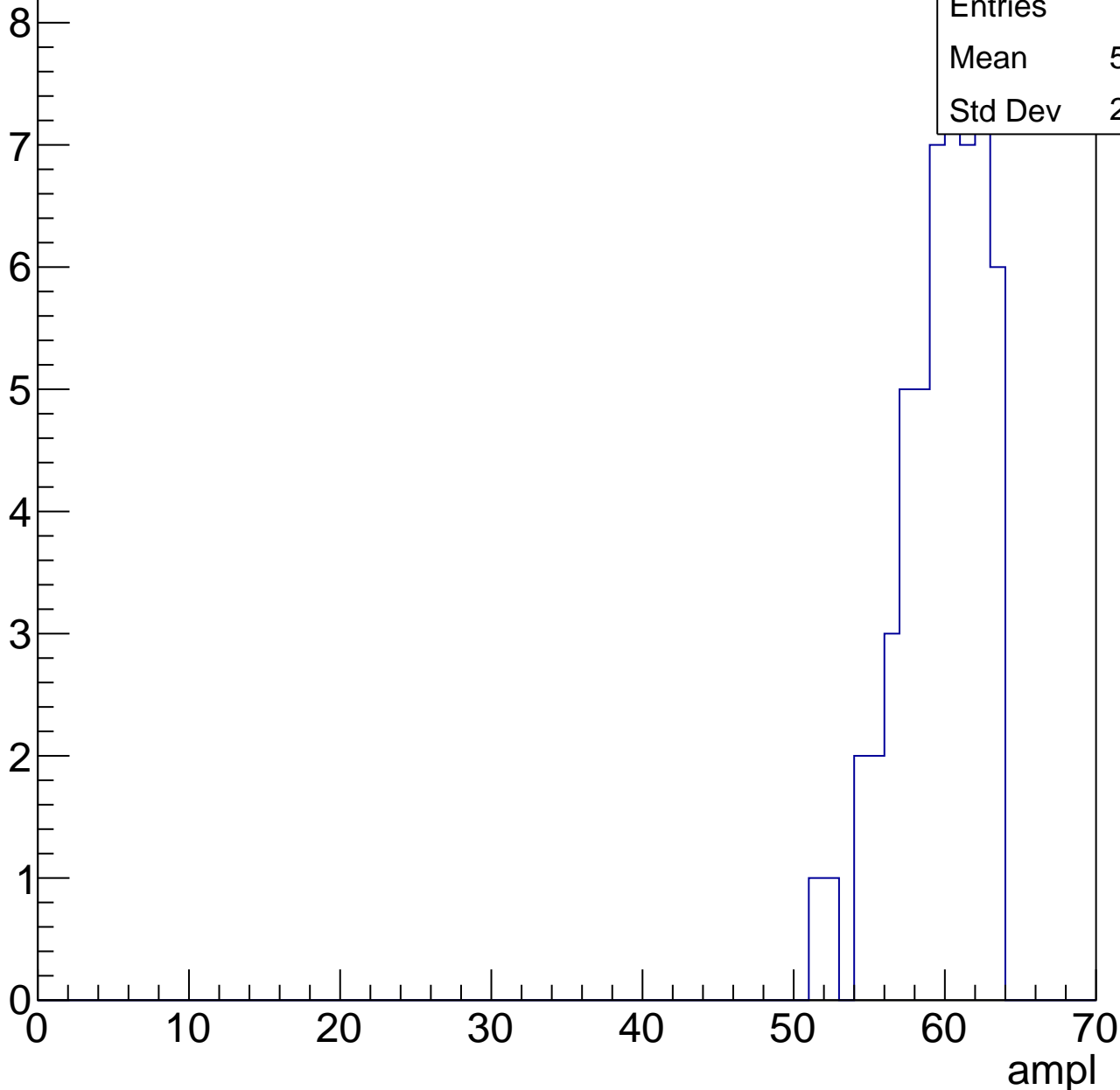


B1L103S, U23-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

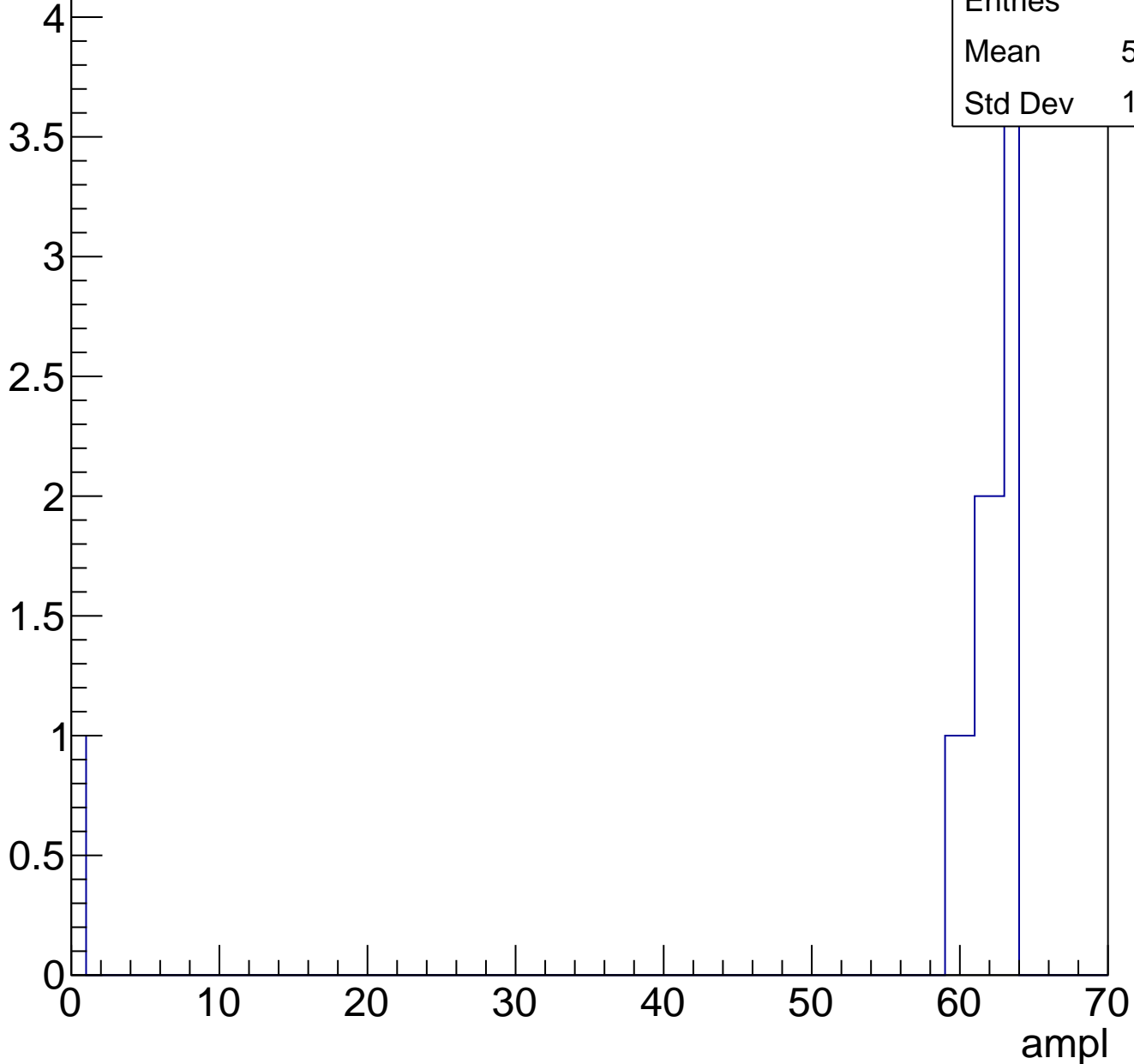
Entries	55
Mean	59.24
Std Dev	2.854



B1L103S, U23-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

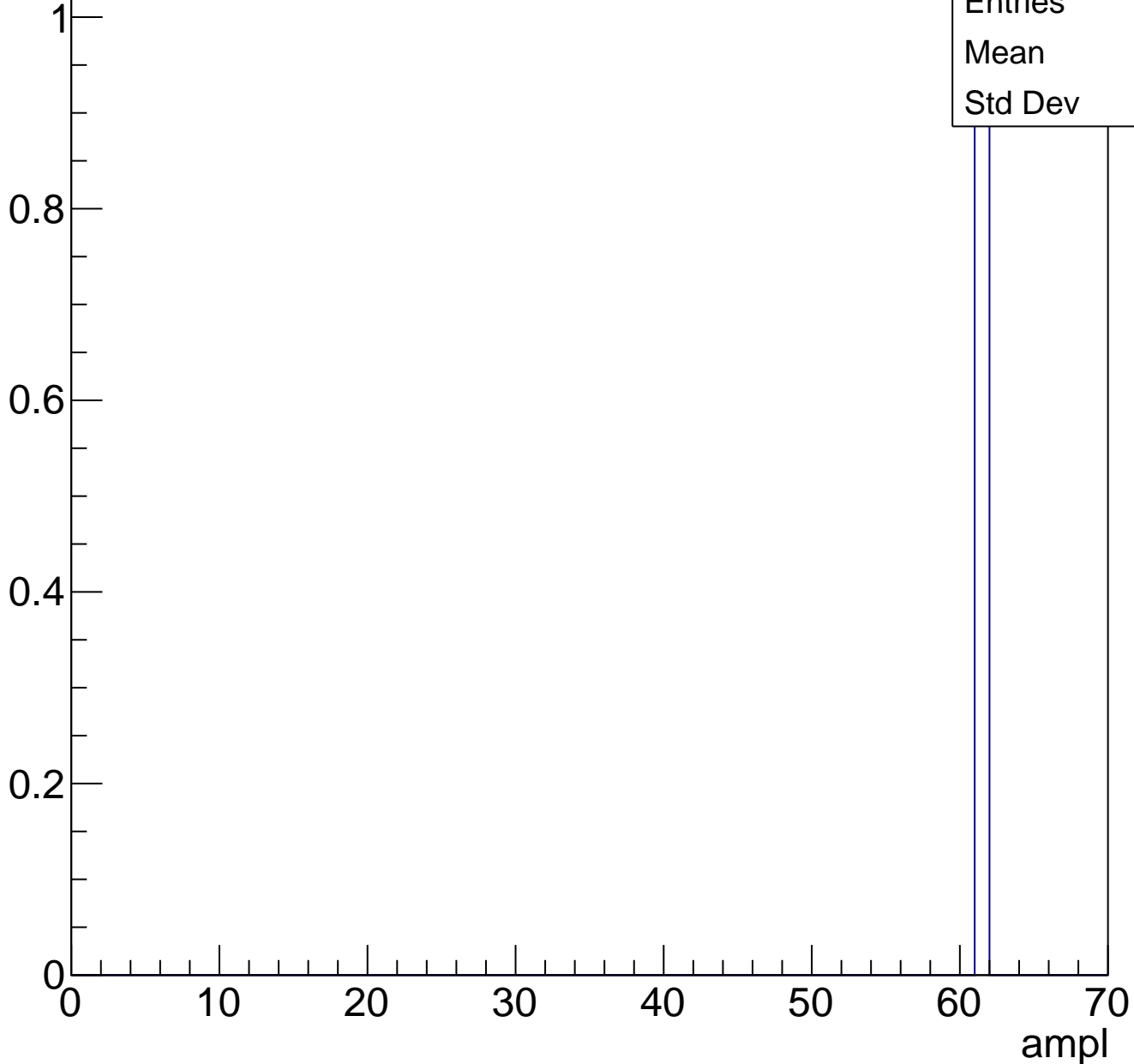
Entry



B1L103S, U23-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch63, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	96
Mean	28.21
Std Dev	4.013

Gaus mean : 28.2935

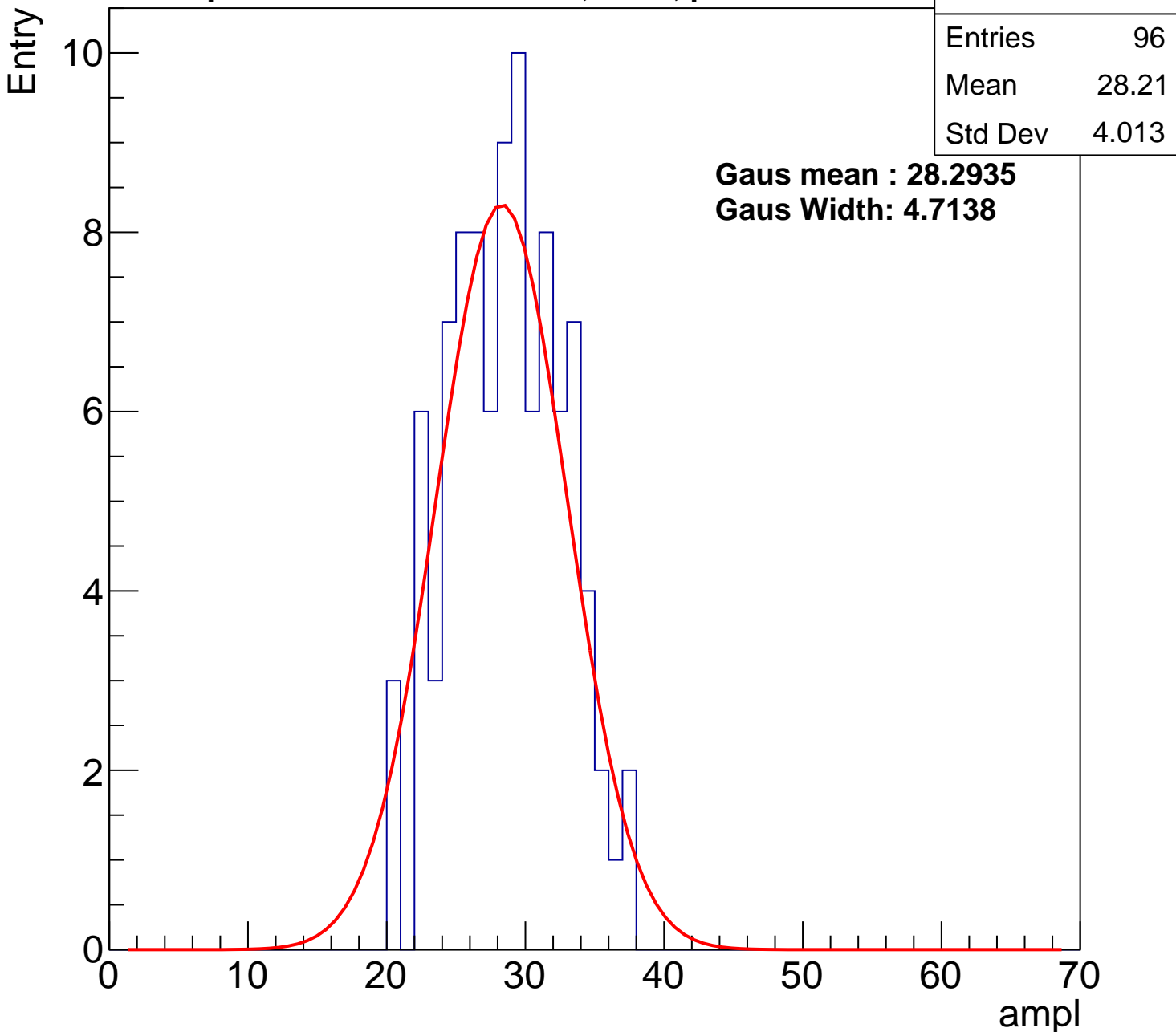
Gaus Width: 4.7138

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch63, adc1

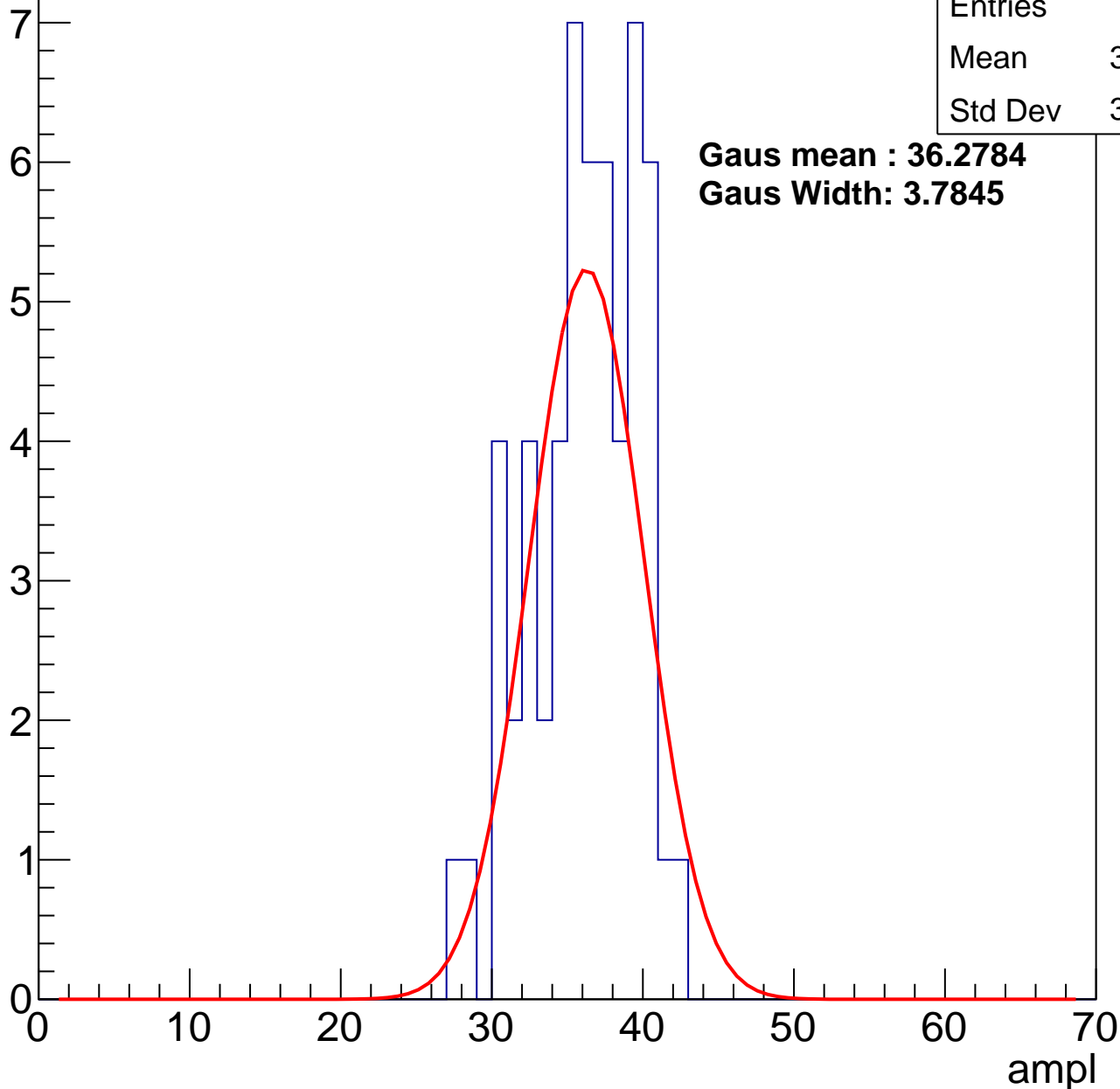
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	35.68
Std Dev	3.485

Gaus mean : 36.2784

Gaus Width: 3.7845



B1L103S, U23-ch63, adc2

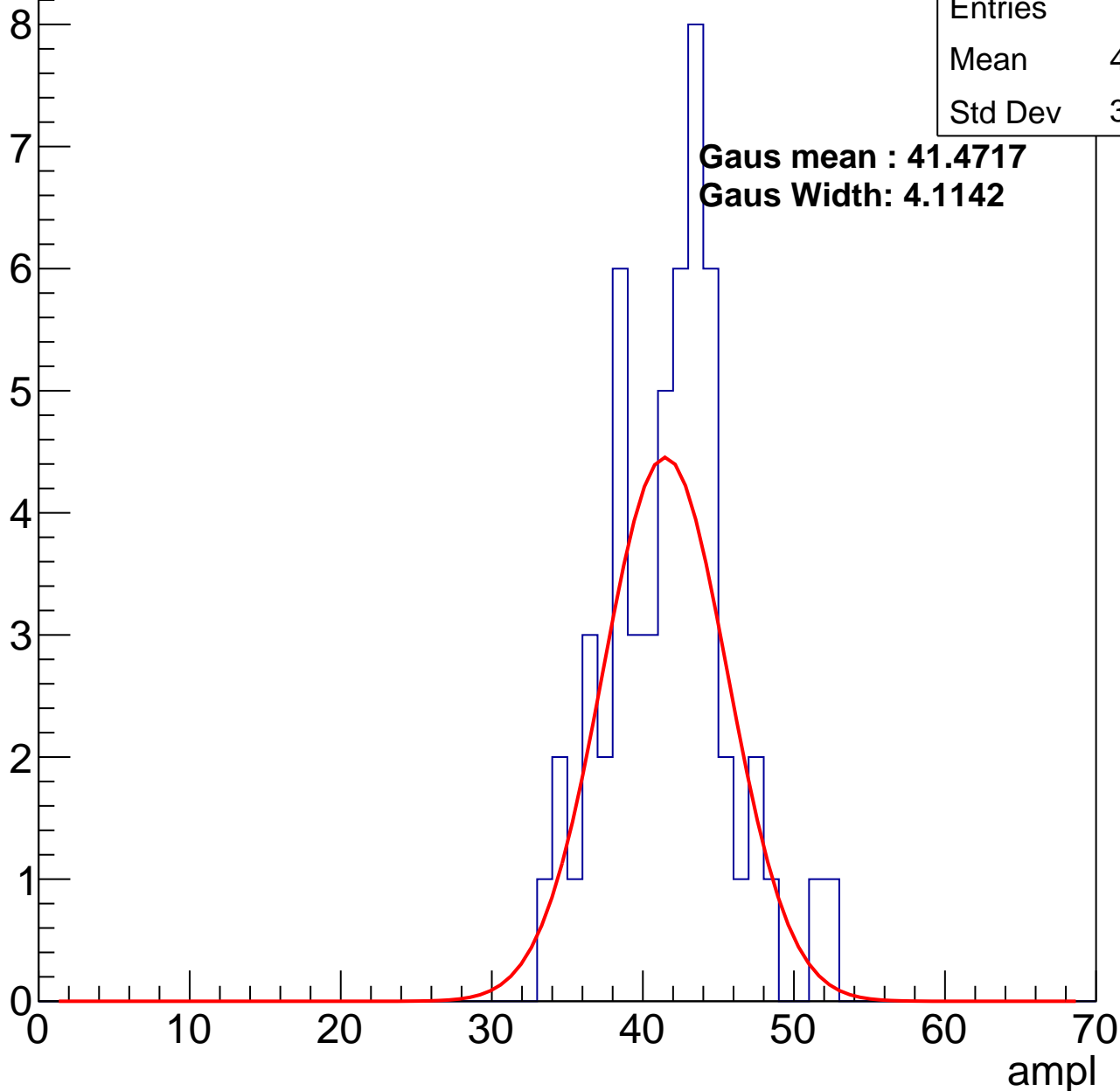
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	41.28
Std Dev	3.993

Gaus mean : 41.4717

Gaus Width: 4.1142

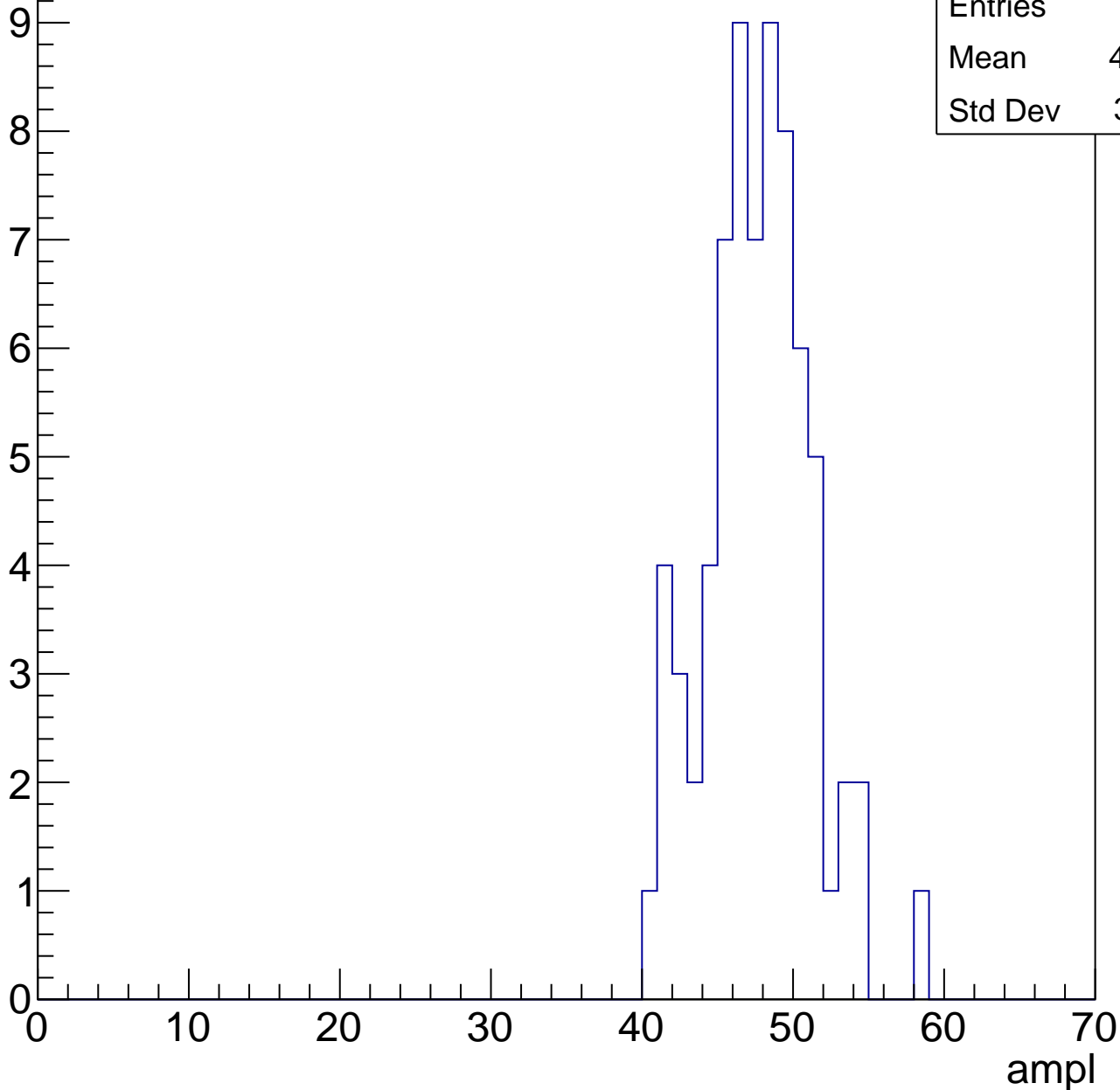


B1L103S, U23-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	47.23
Std Dev	3.481

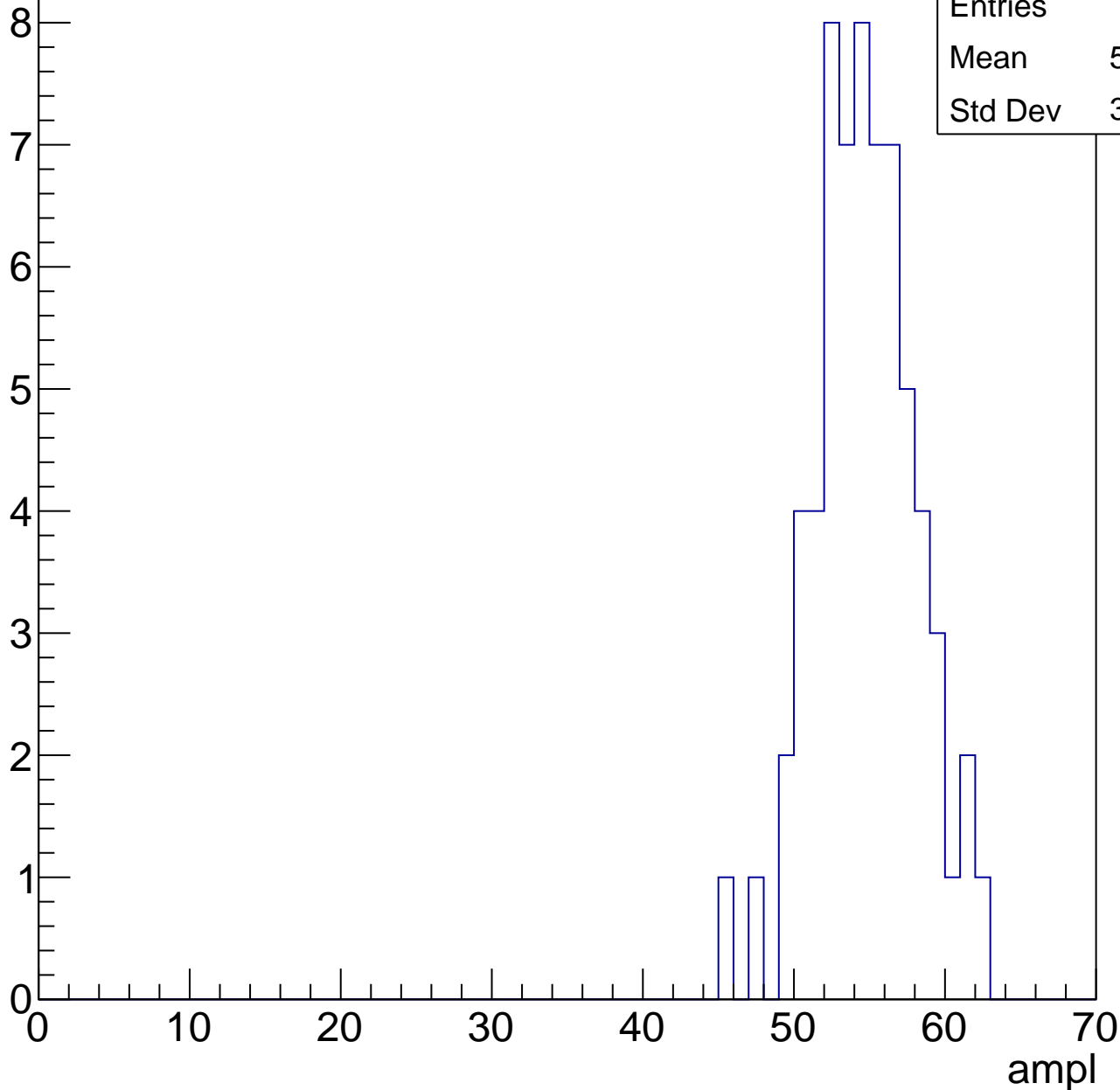


B1L103S, U23-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	54.28
Std Dev	3.354

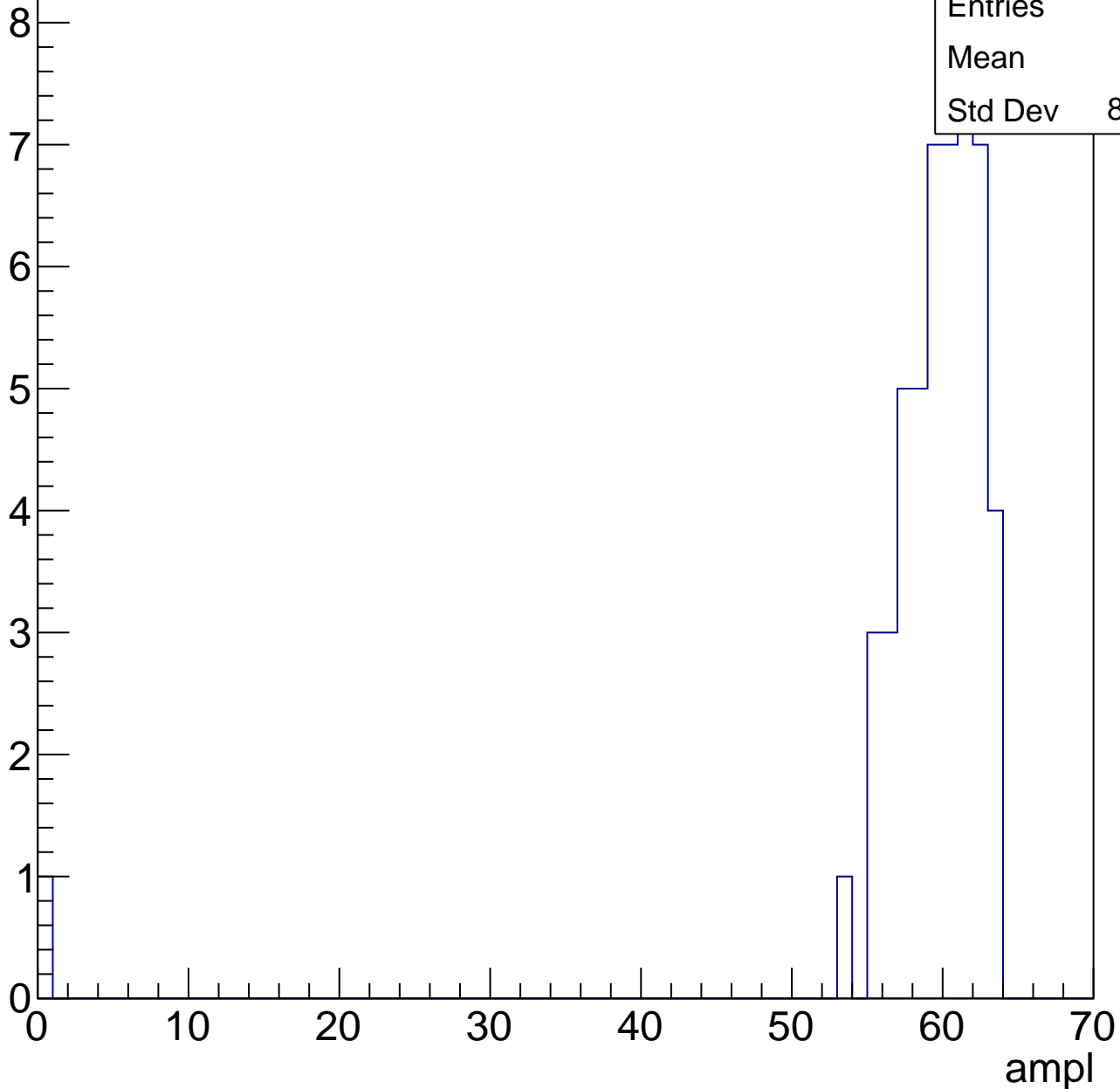


B1L103S, U23-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.2
Std Dev	8.575

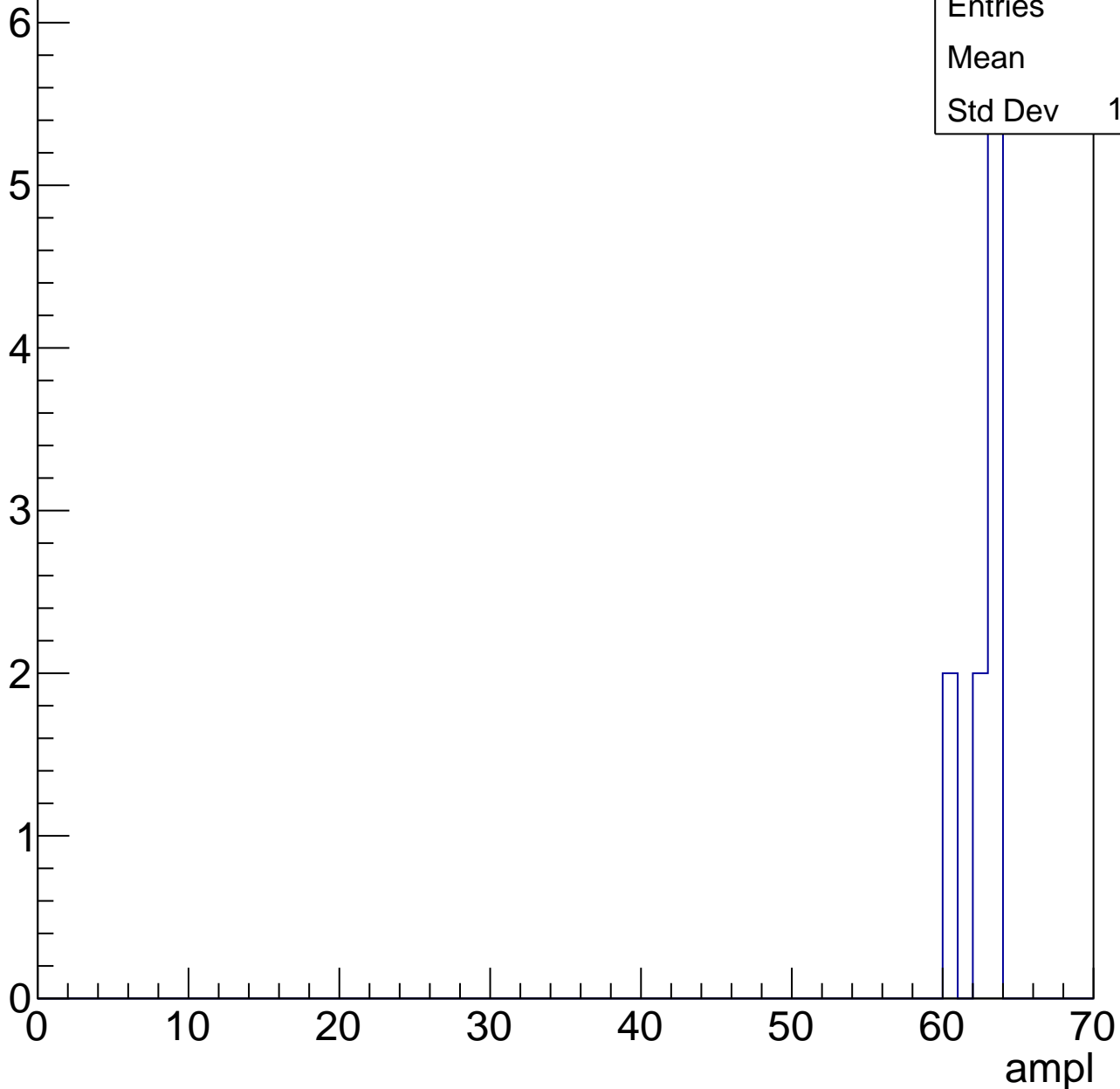


B1L103S, U23-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	10
Mean	62.2
Std Dev	1.166



B1L103S, U23-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch64, adc0

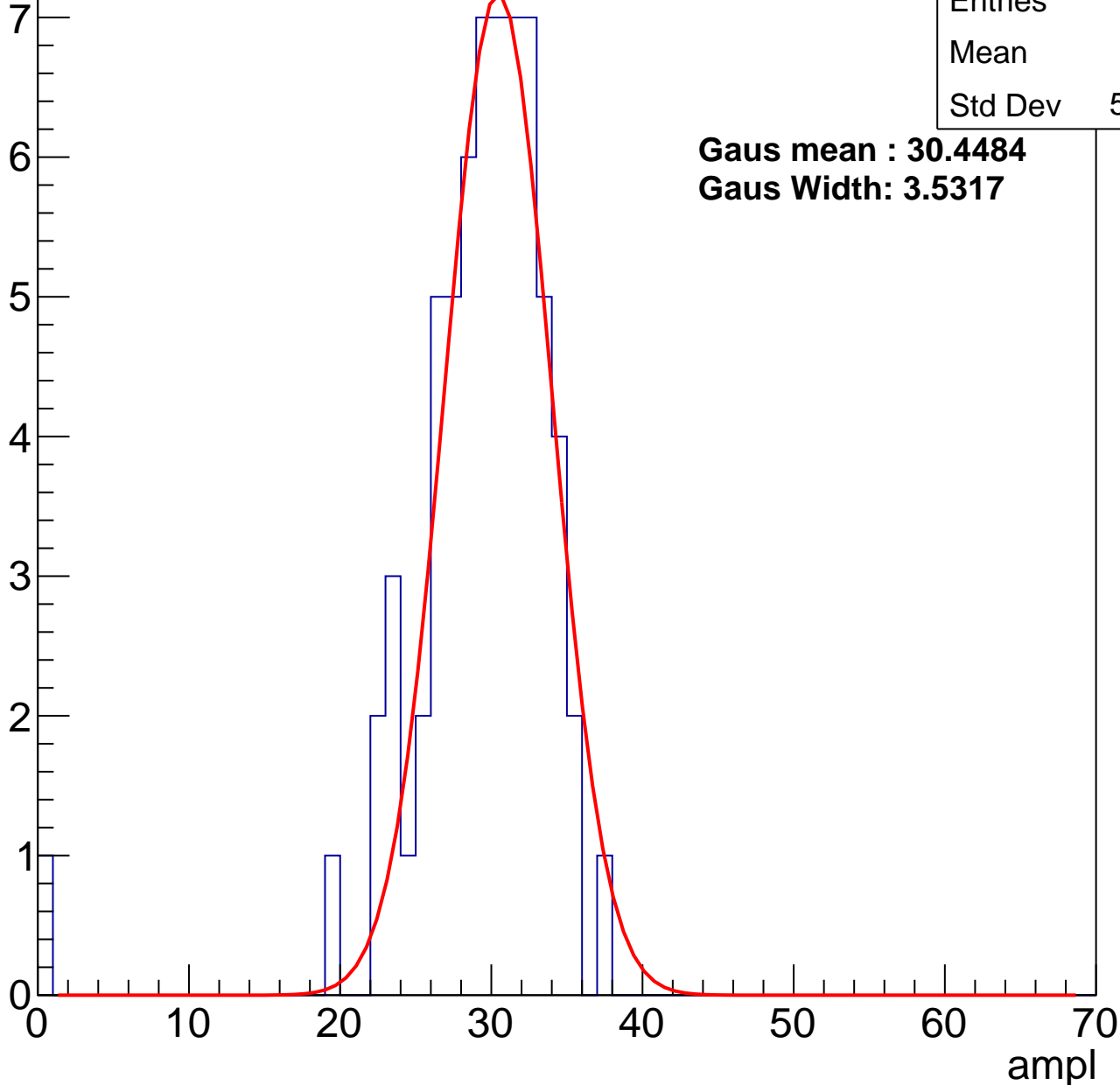
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.8
Std Dev	5.055

Gaus mean : 30.4484

Gaus Width: 3.5317



B1L103S, U23-ch64, adc1

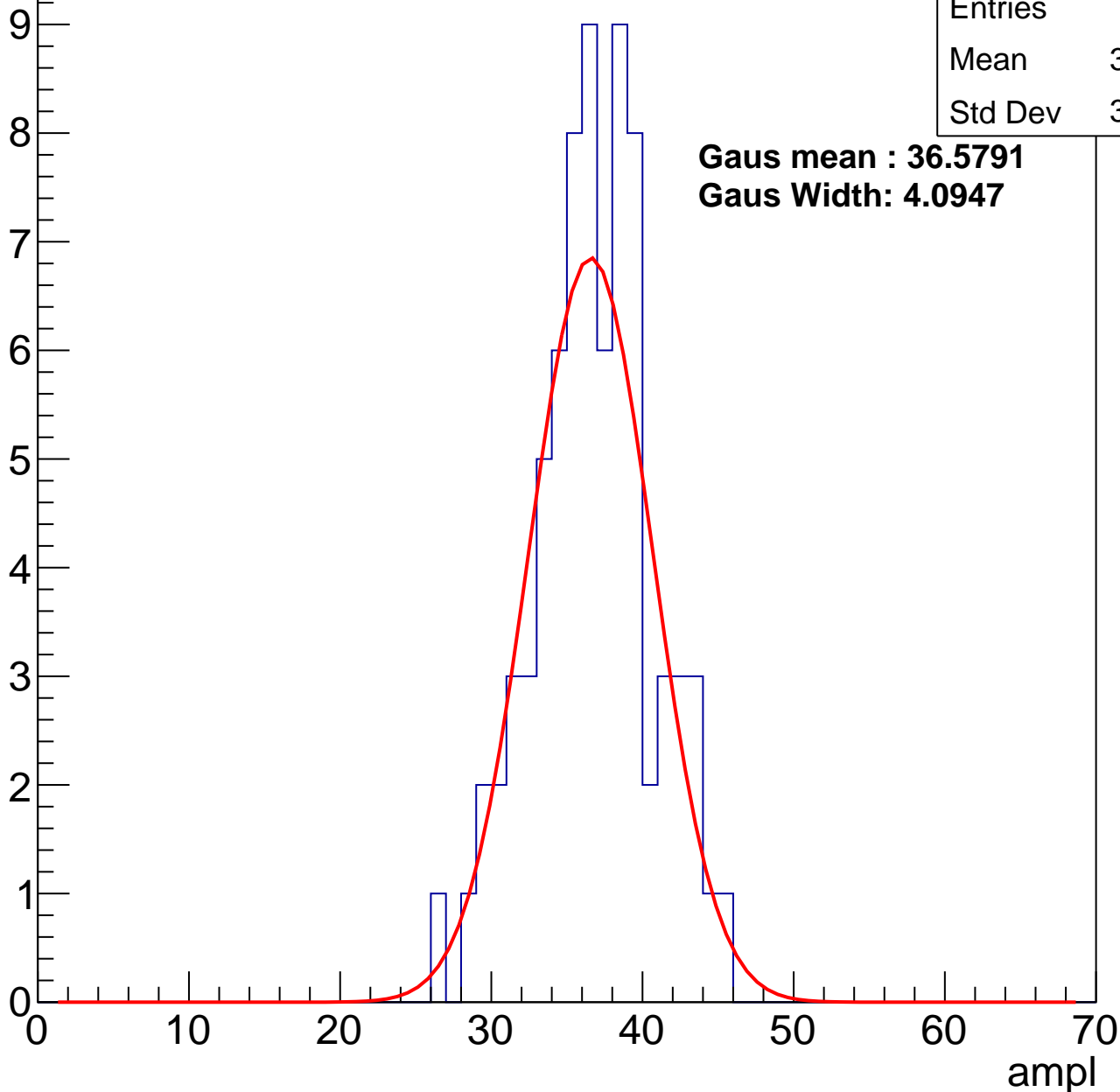
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.28
Std Dev	3.889

Gaus mean : 36.5791

Gaus Width: 4.0947



B1L103S, U23-ch64, adc2

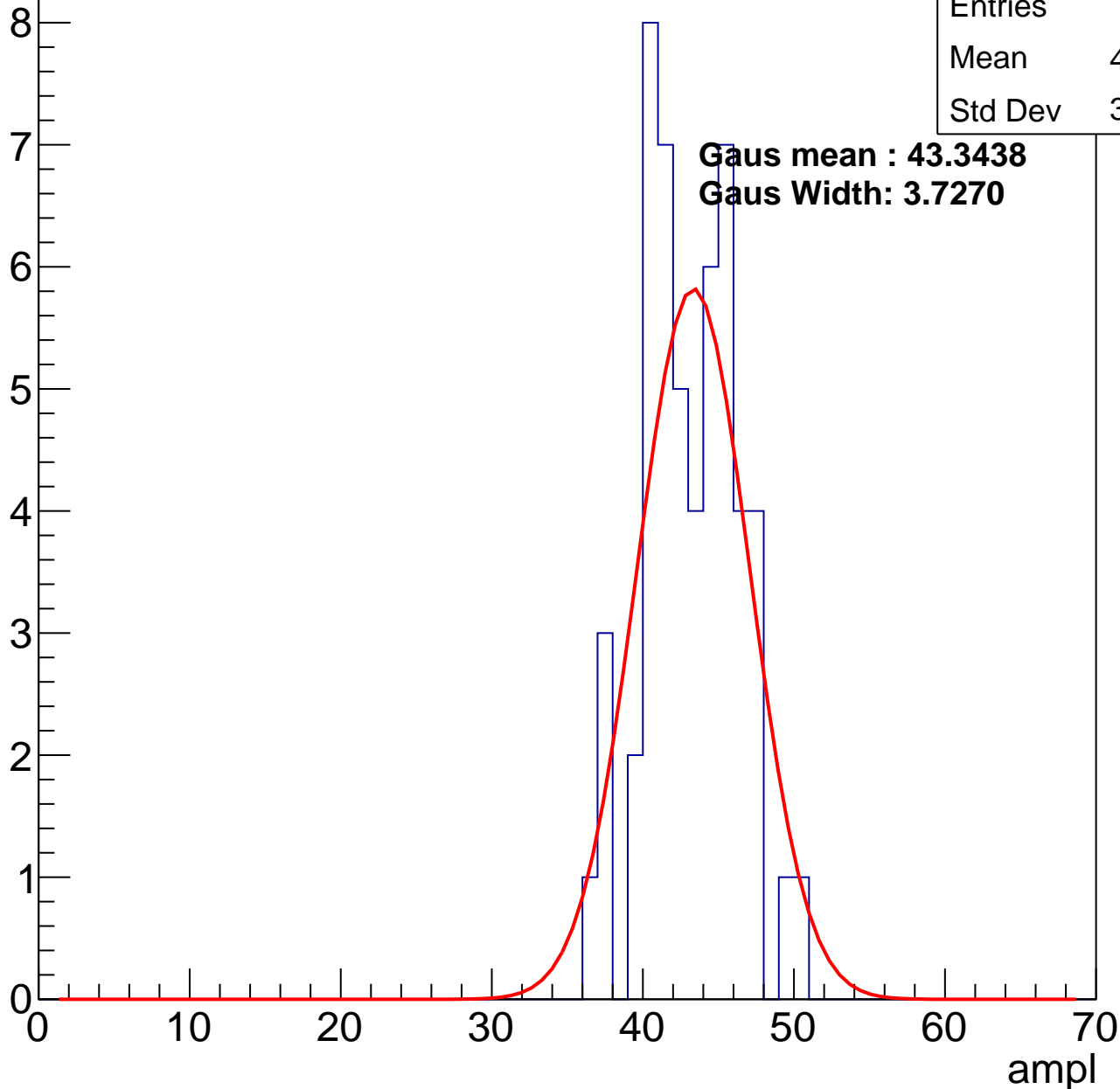
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.72
Std Dev	3.098

Gaus mean : 43.3438

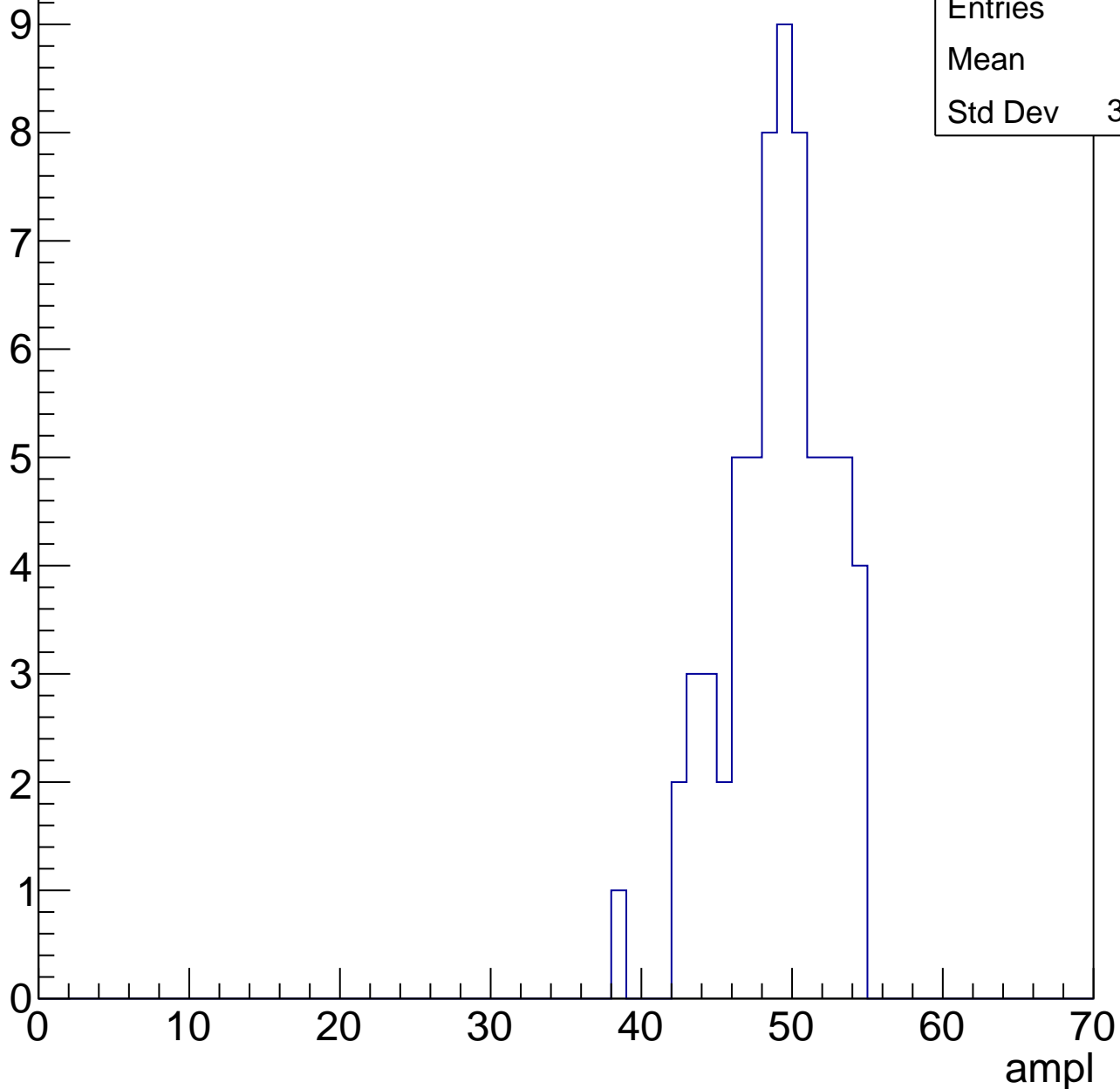
Gaus Width: 3.7270



B1L103S, U23-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

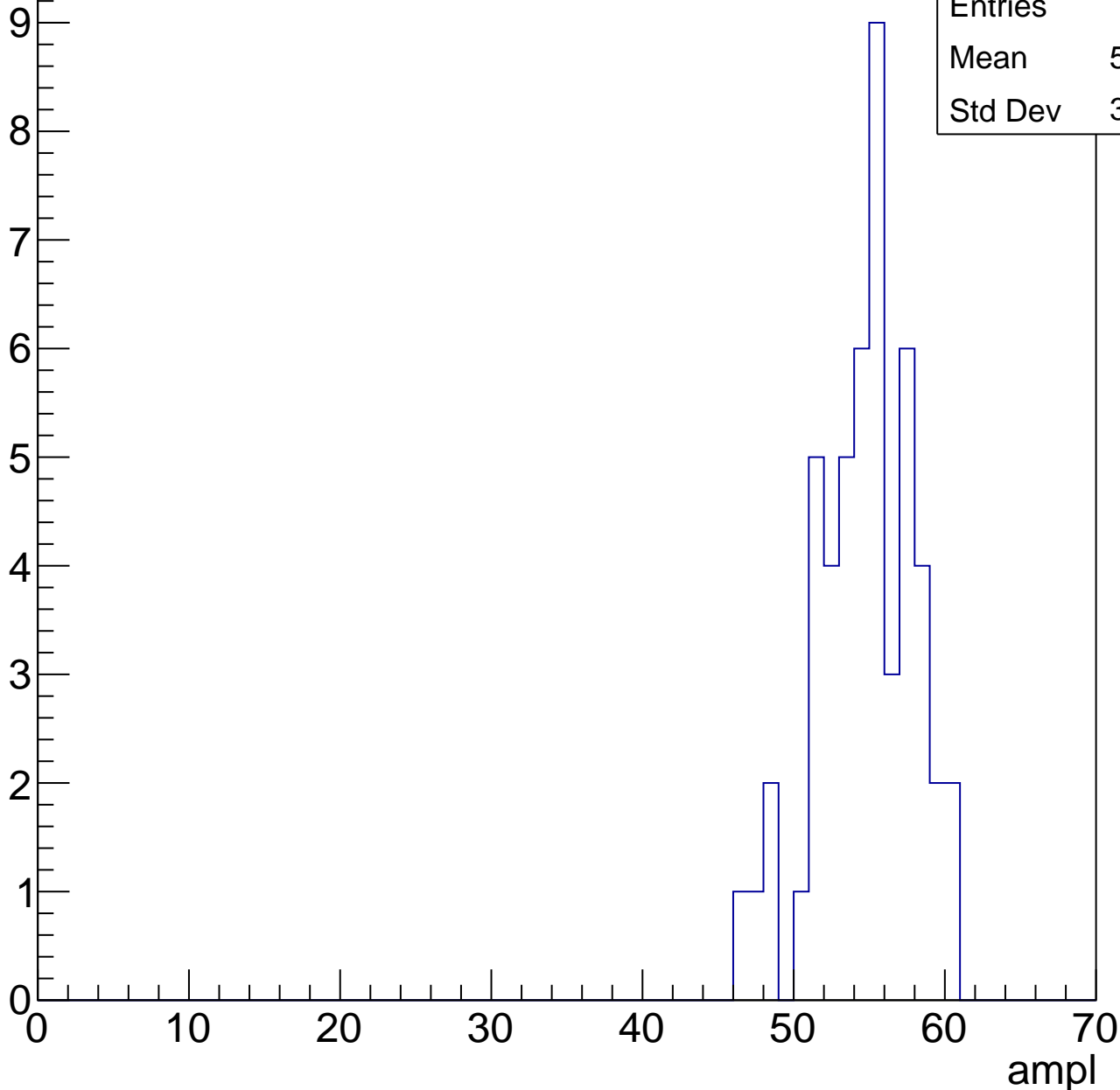


B1L103S, U23-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	54.24
Std Dev	3.209

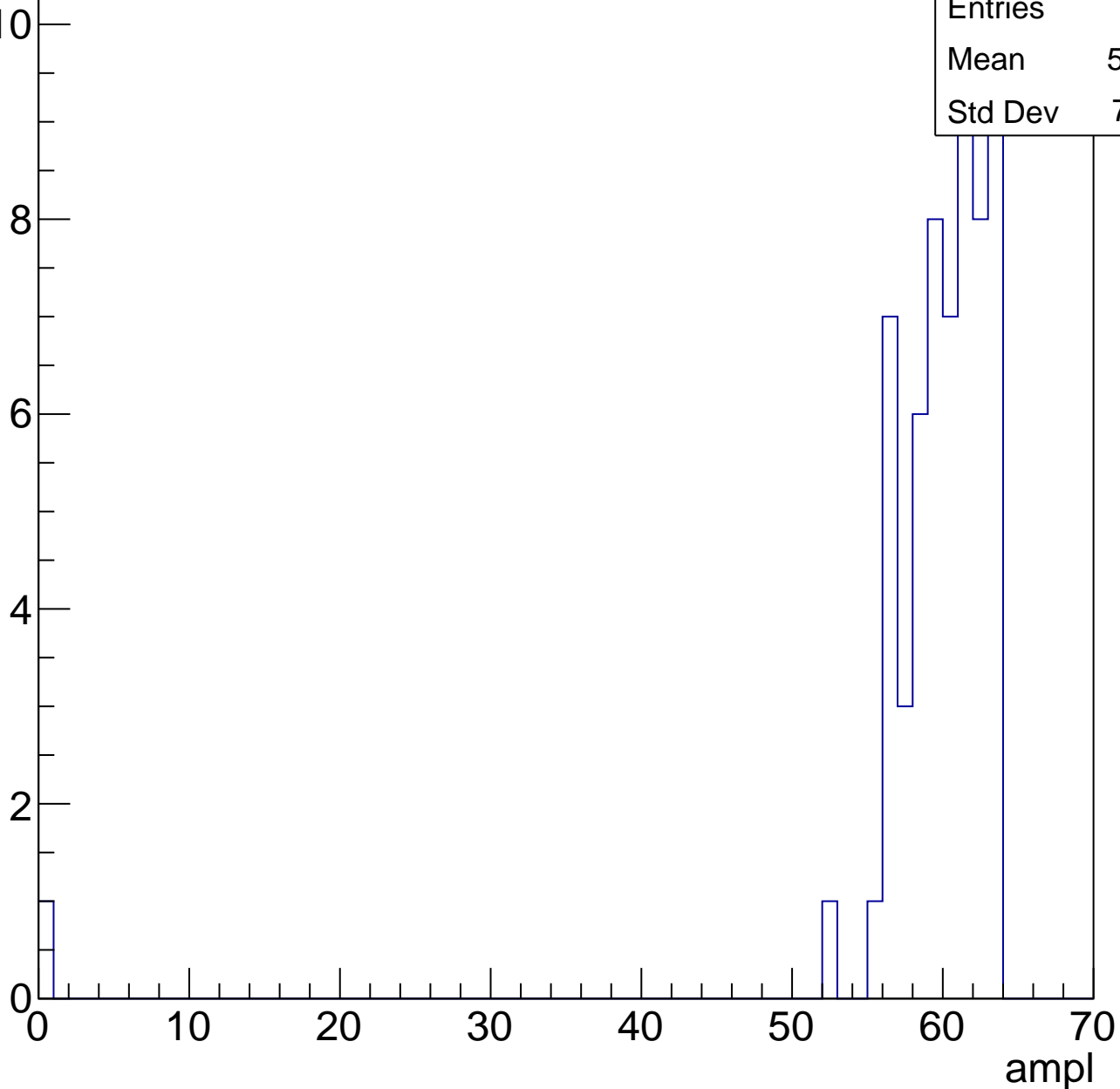


B1L103S, U23-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	58.74
Std Dev	7.981



B1L103S, U23-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

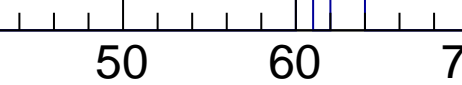
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch65, adc0

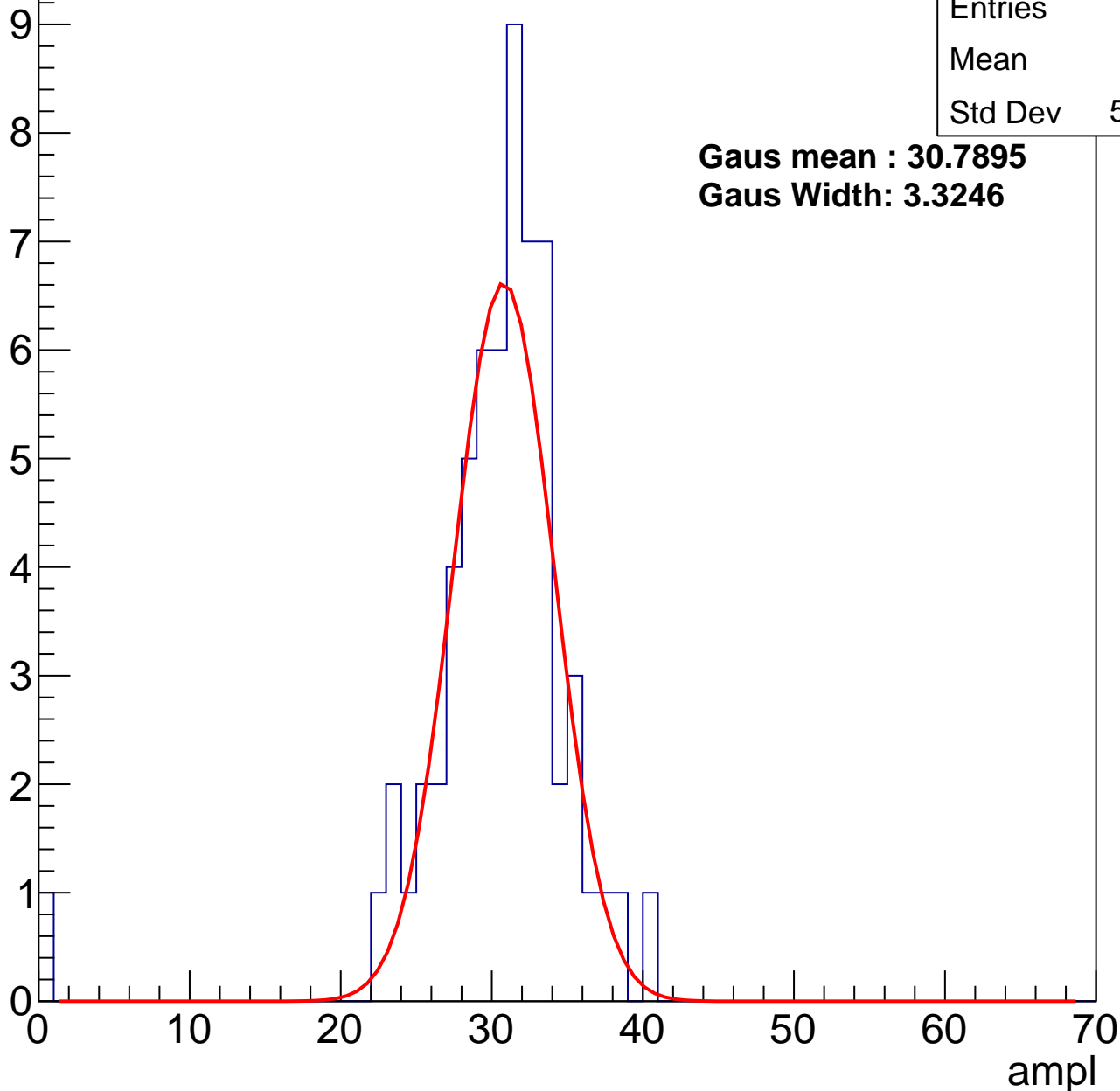
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.9
Std Dev	5.232

Gaus mean : 30.7895

Gaus Width: 3.3246



B1L103S, U23-ch65, adc1

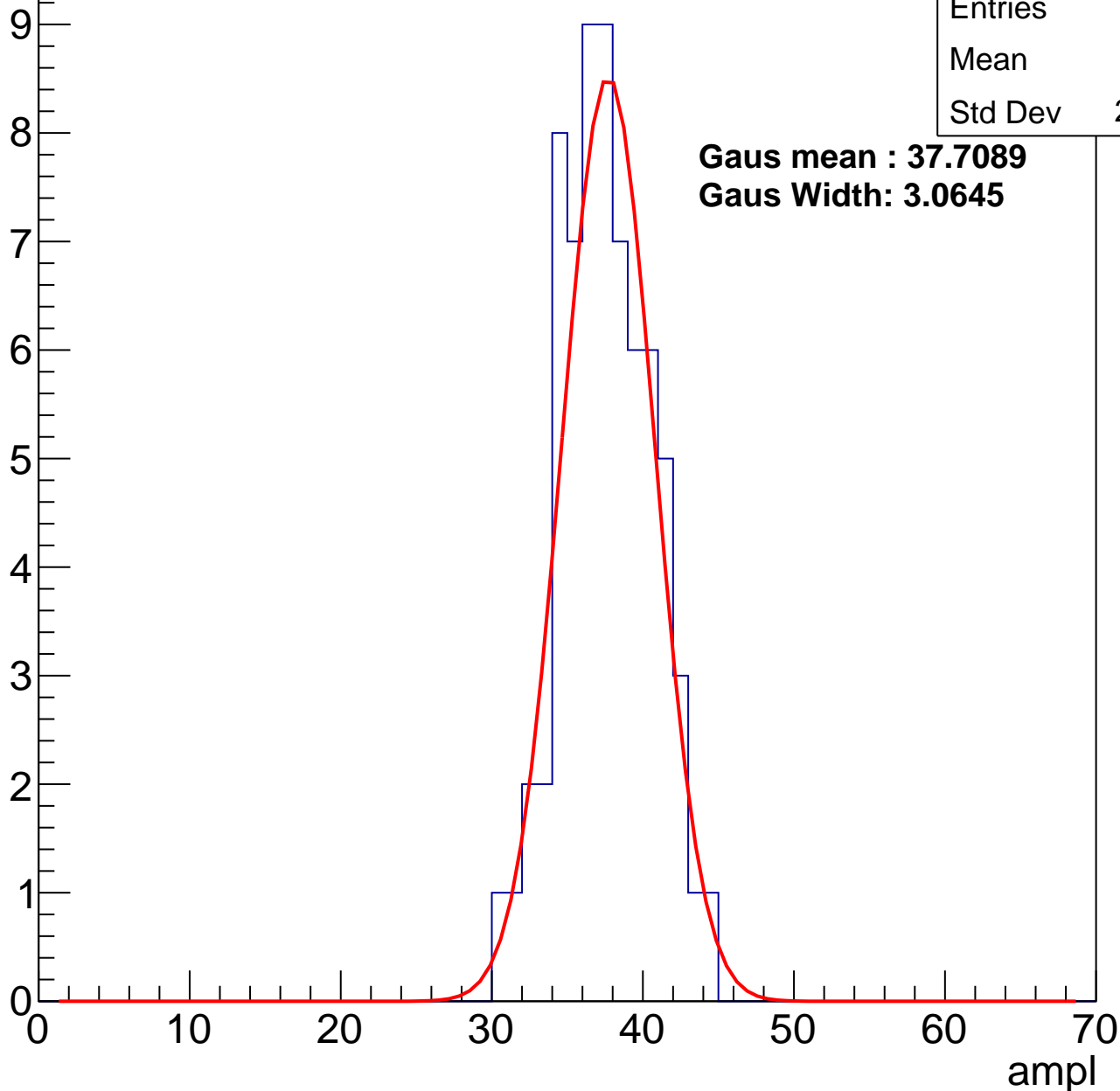
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.1
Std Dev	2.971

Gaus mean : 37.7089

Gaus Width: 3.0645



B1L103S, U23-ch65, adc2

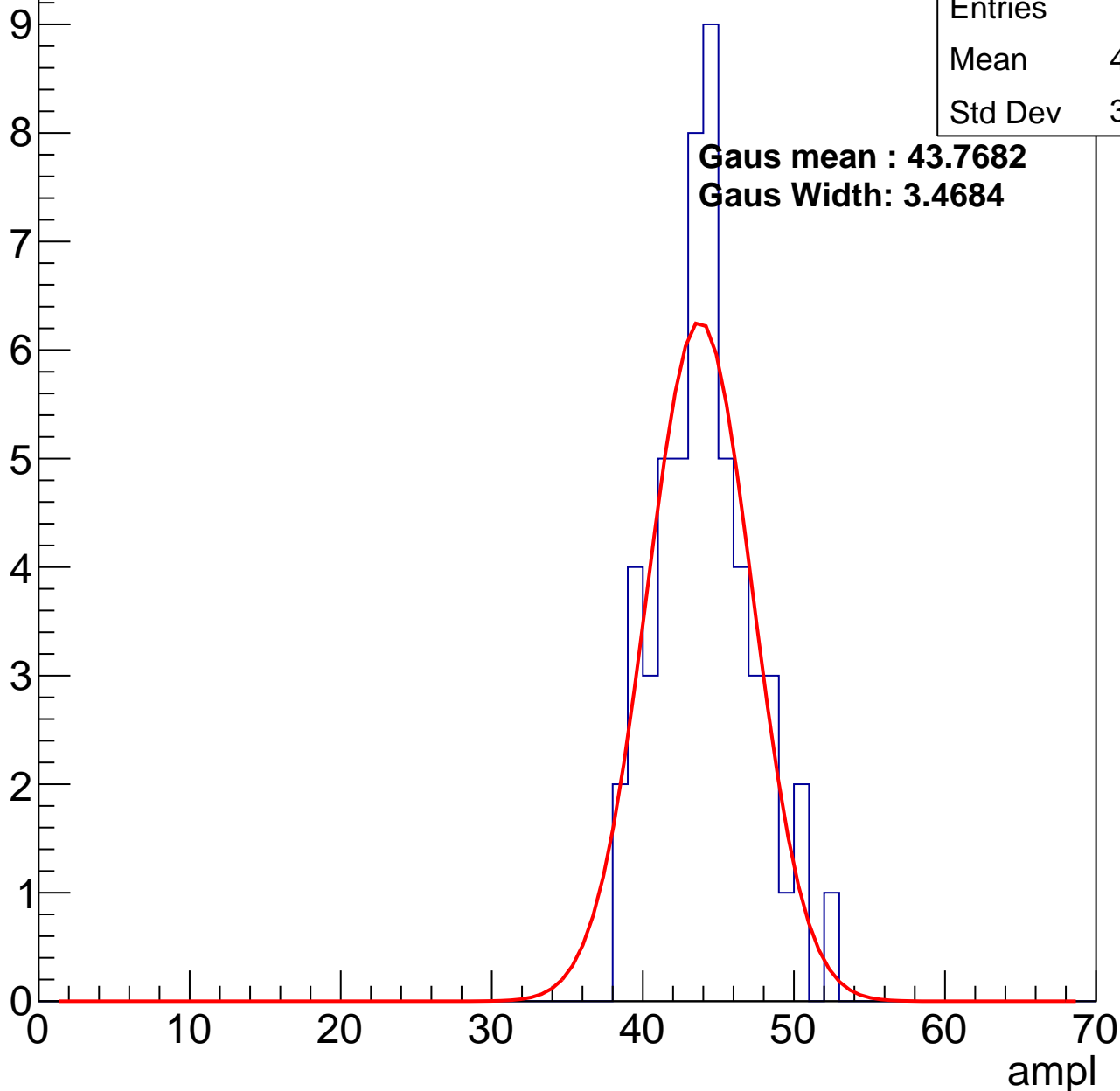
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.67
Std Dev	3.145

Gaus mean : 43.7682

Gaus Width: 3.4684

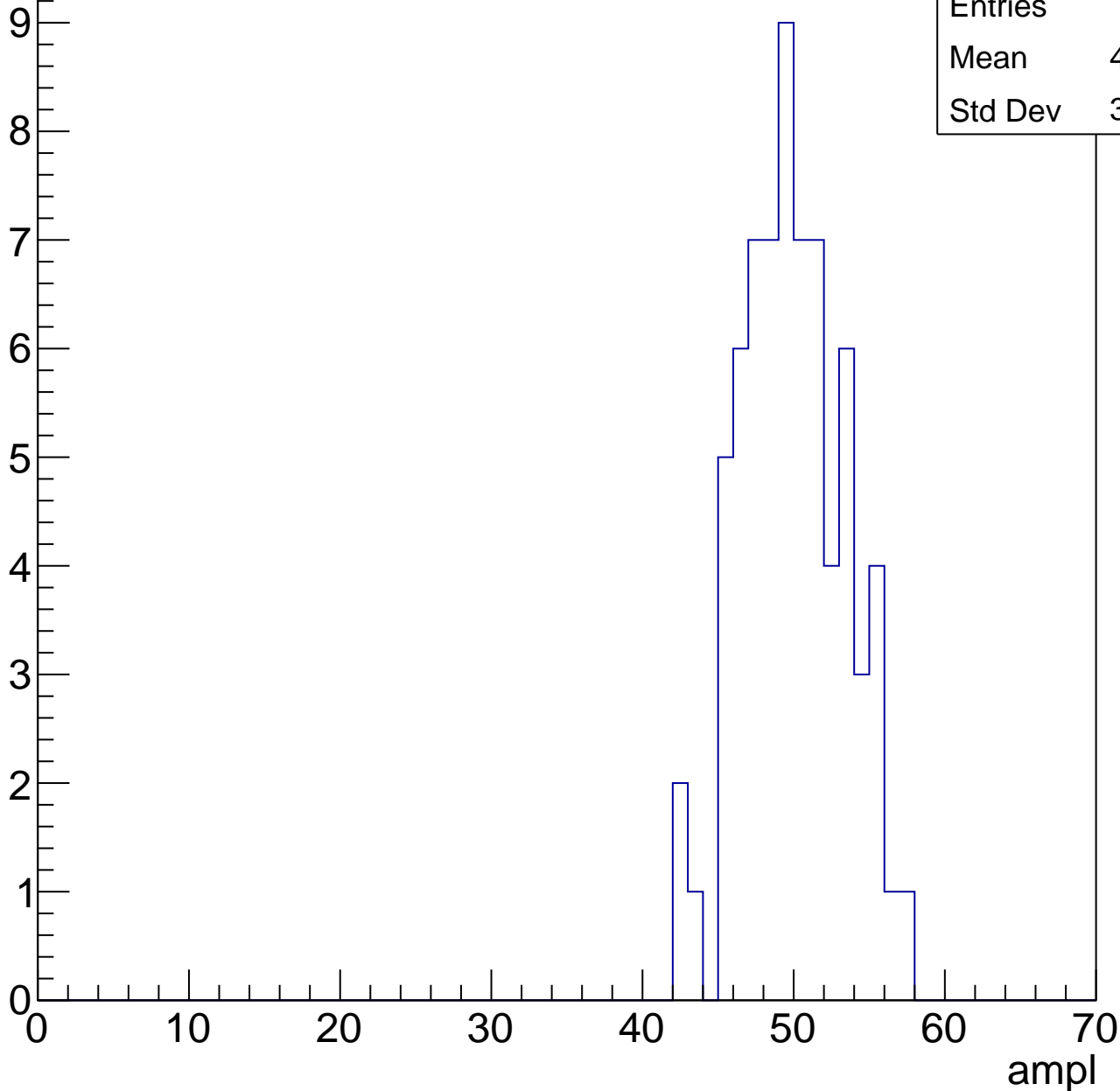


B1L103S, U23-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.46
Std Dev	3.354

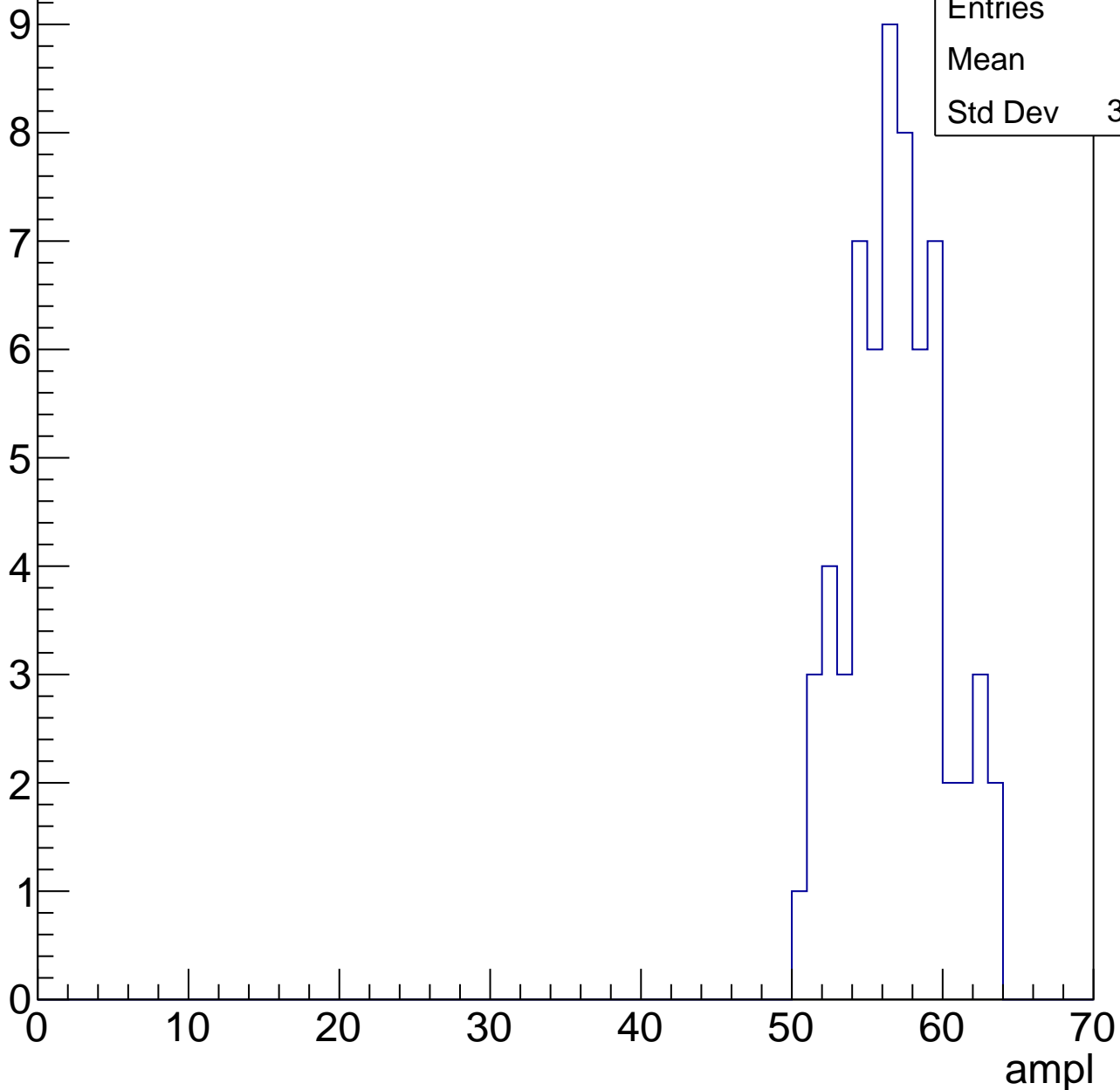


B1L103S, U23-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	56.4
Std Dev	3.109

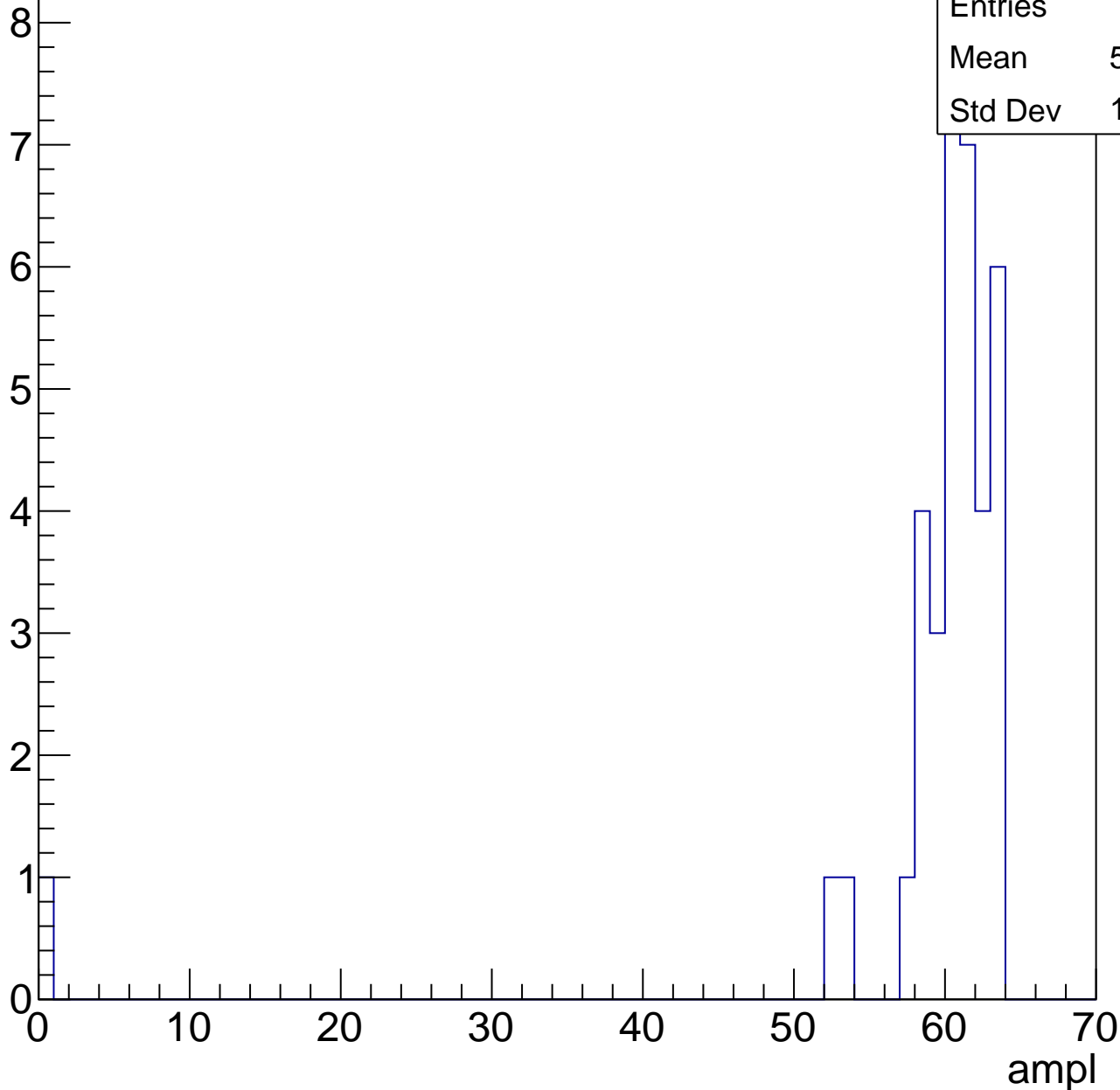


B1L103S, U23-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.44
Std Dev	10.18



B1L103S, U23-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch66, adc0

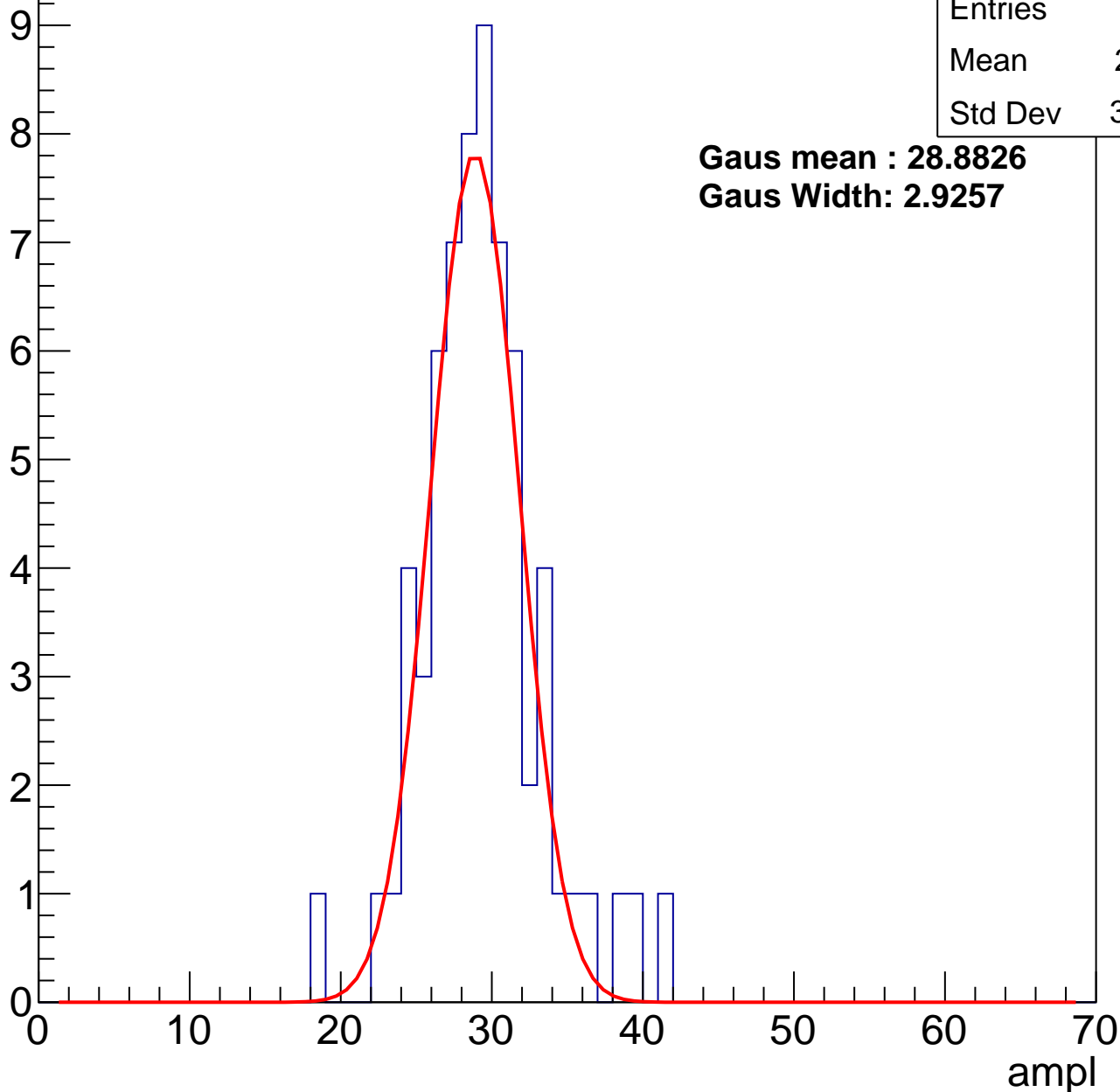
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28.91
Std Dev	3.906

Gaus mean : 28.8826

Gaus Width: 2.9257



B1L103S, U23-ch66, adc1

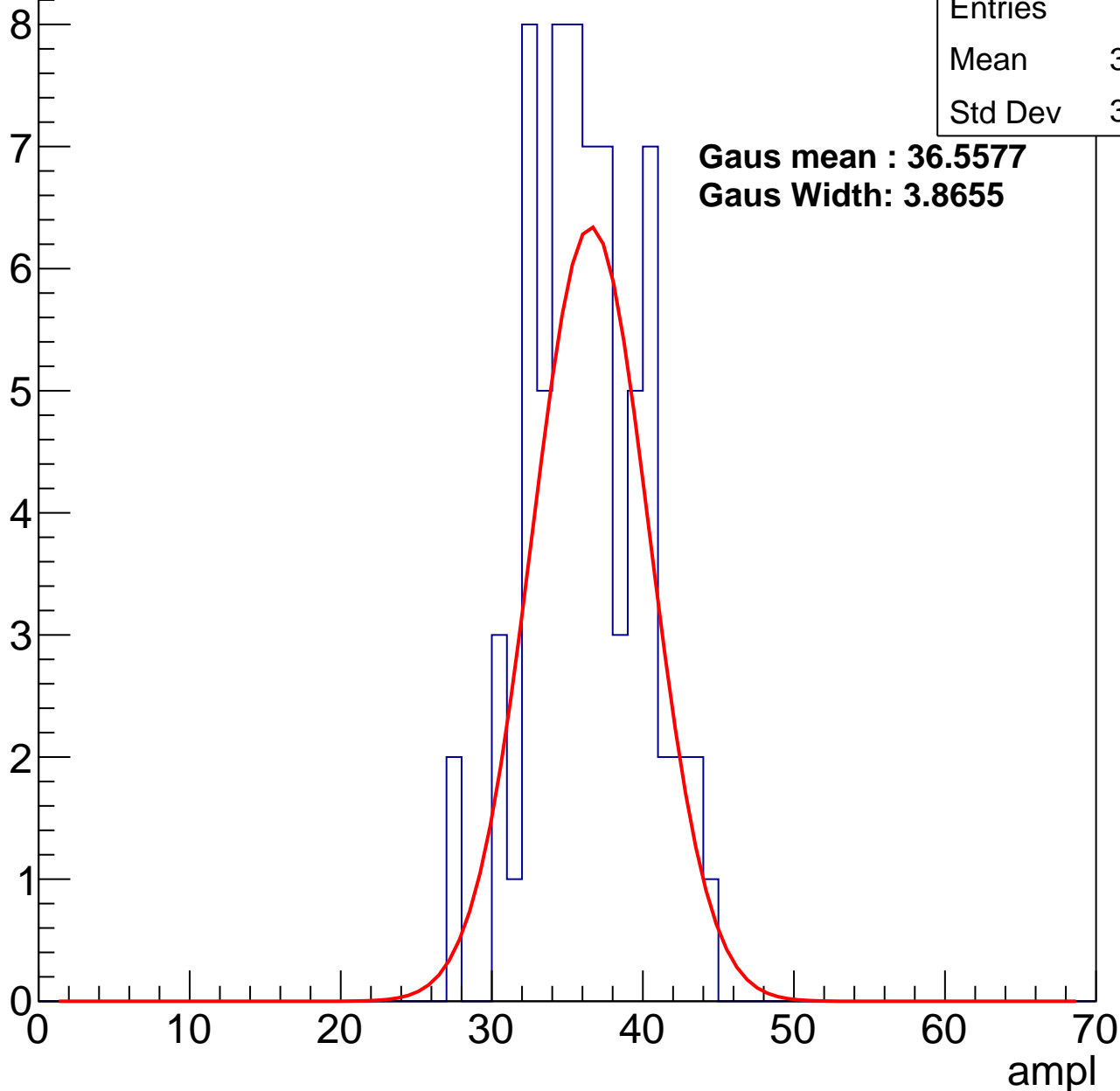
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.83
Std Dev	3.689

Gaus mean : 36.5577

Gaus Width: 3.8655



B1L103S, U23-ch66, adc2

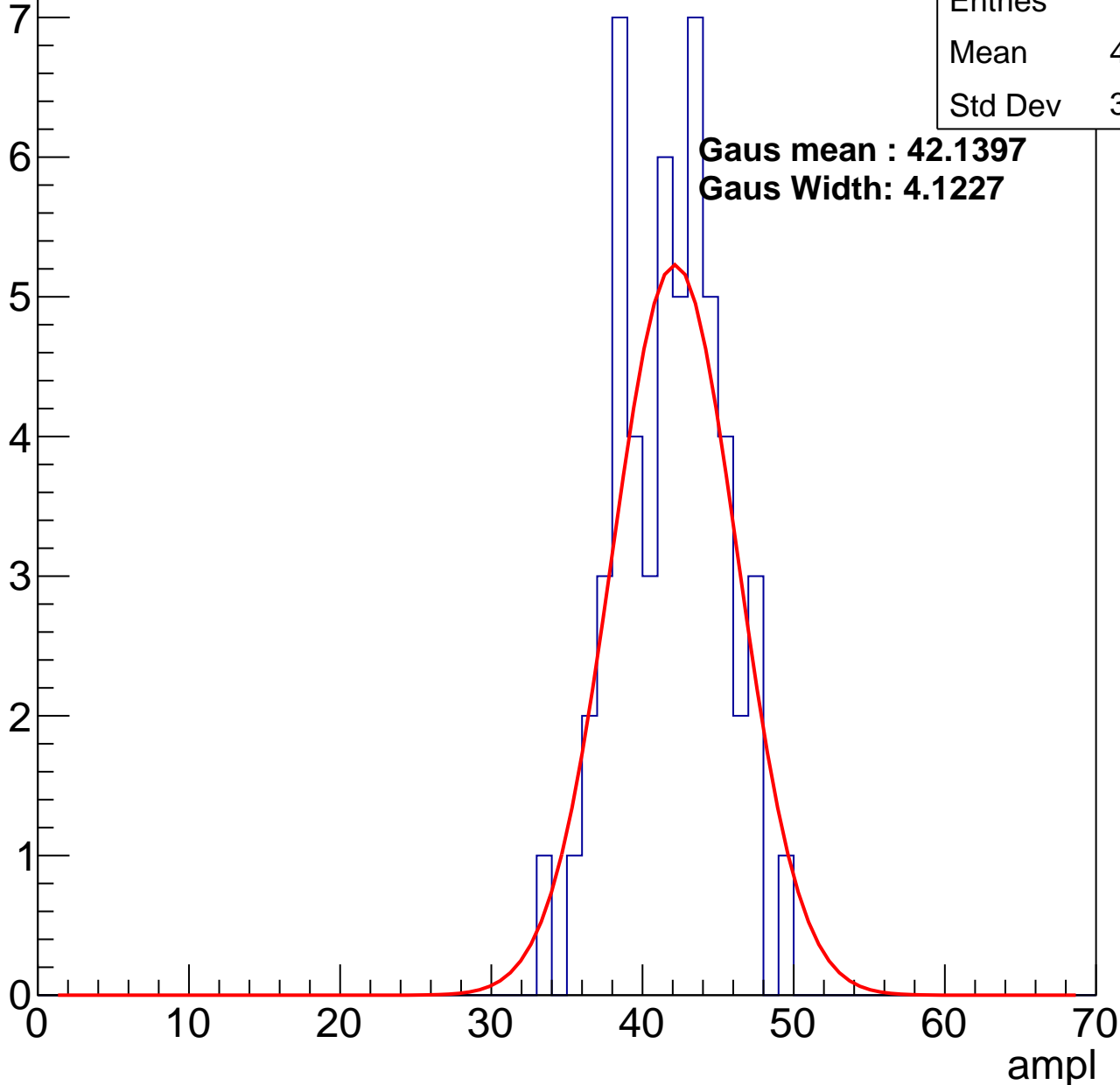
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	41.33
Std Dev	3.437

Gaus mean : 42.1397

Gaus Width: 4.1227

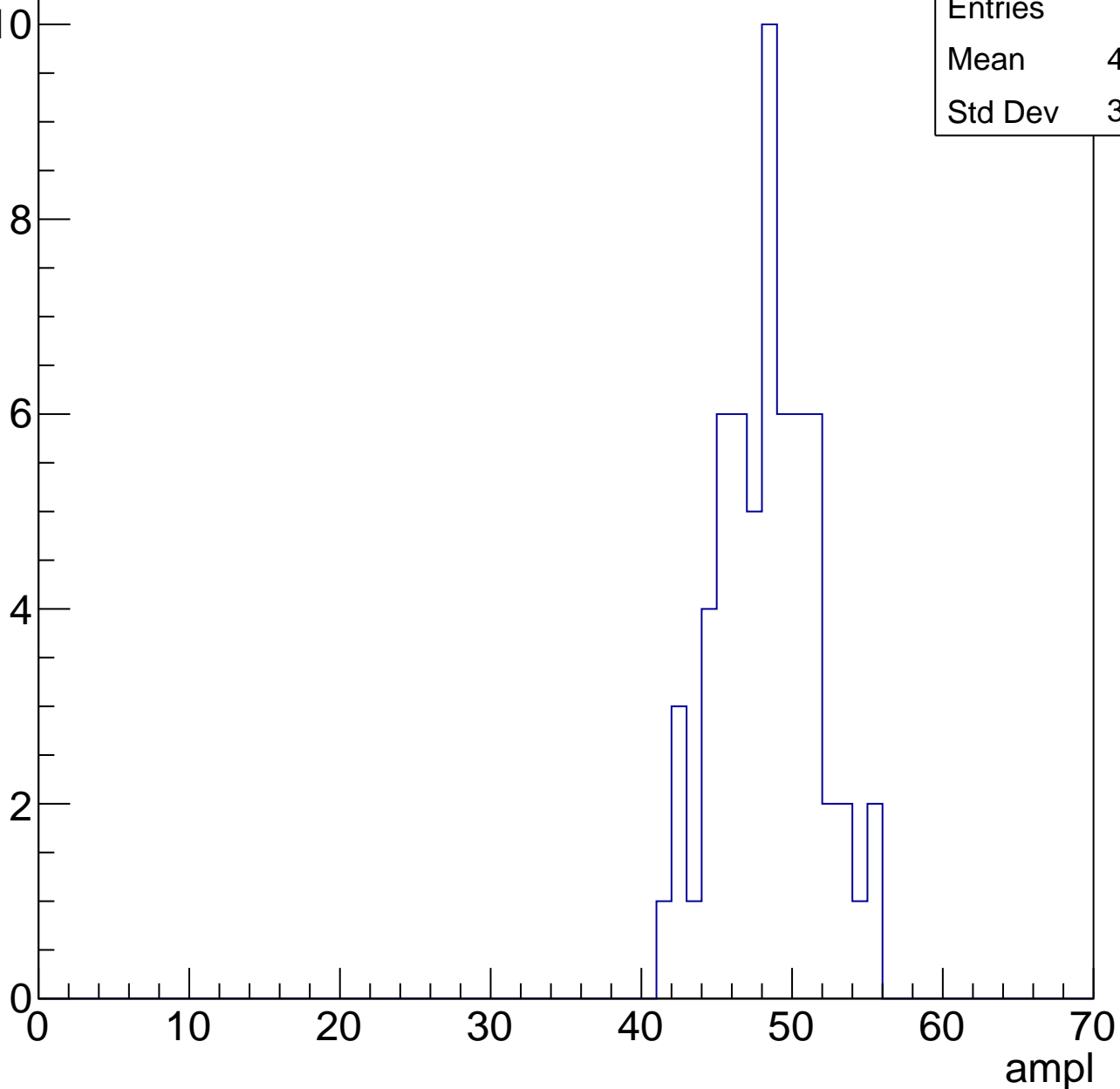


B1L103S, U23-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	47.89
Std Dev	3.209

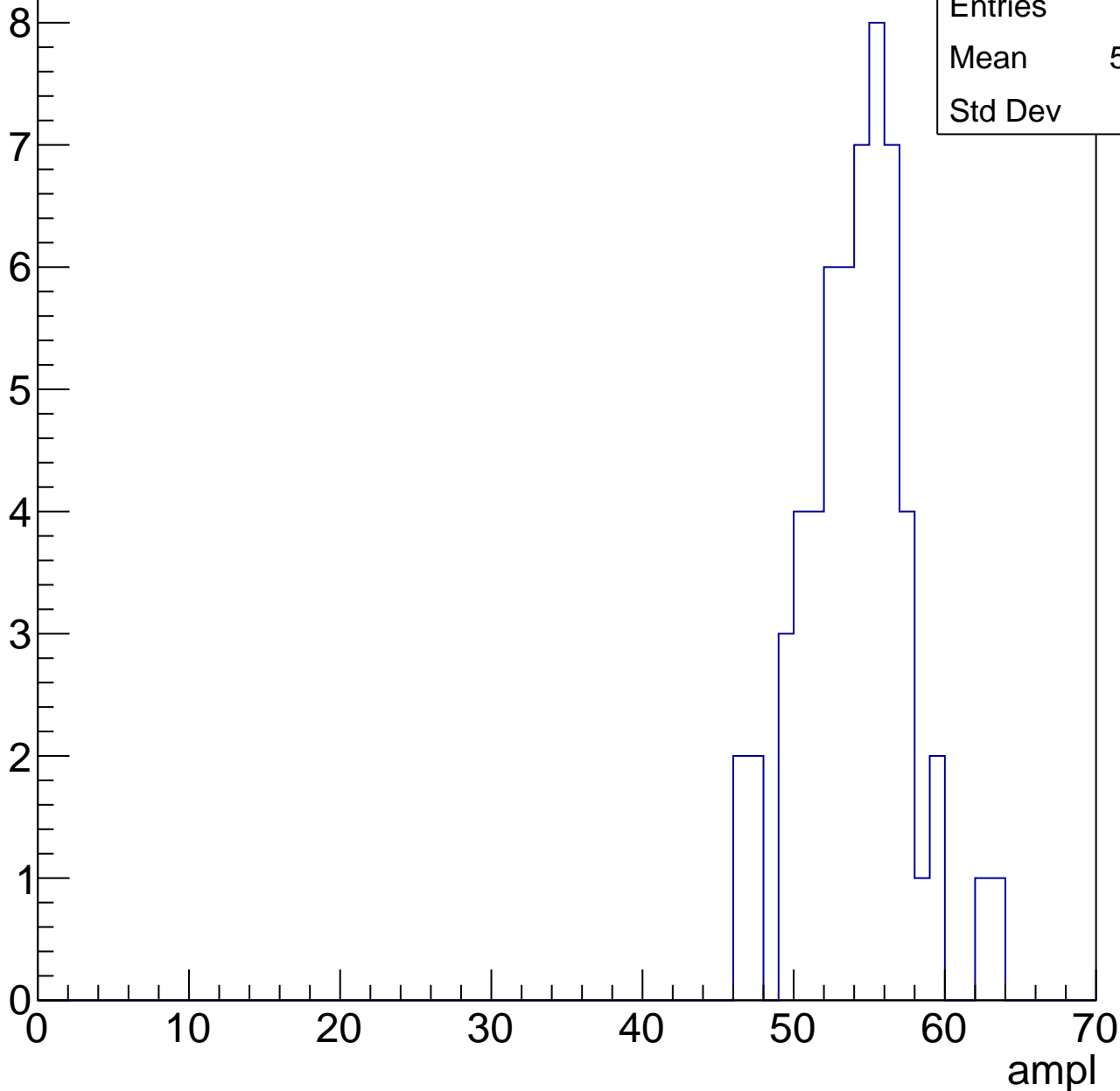


B1L103S, U23-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	53.55
Std Dev	3.48

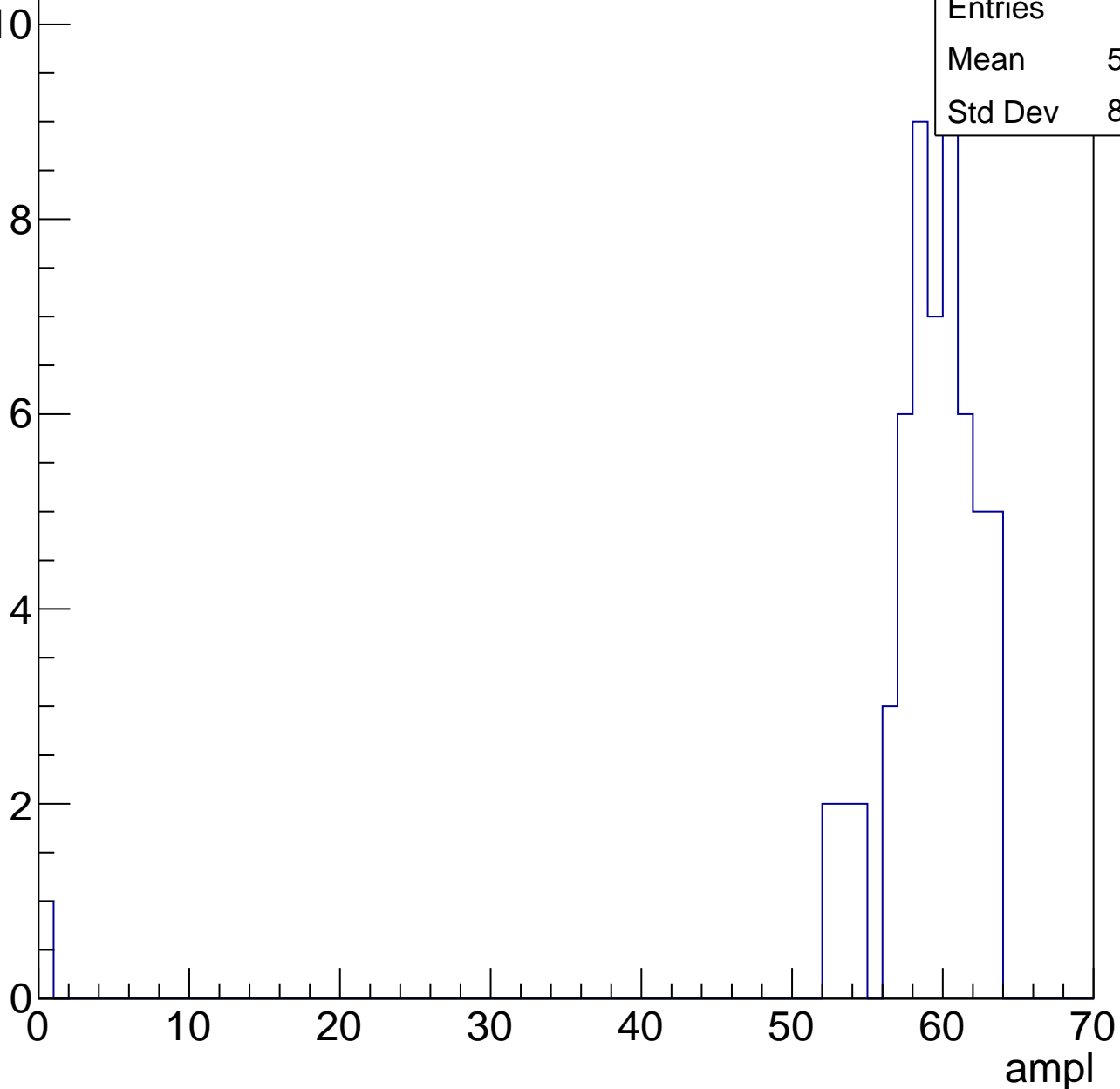


B1L103S, U23-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

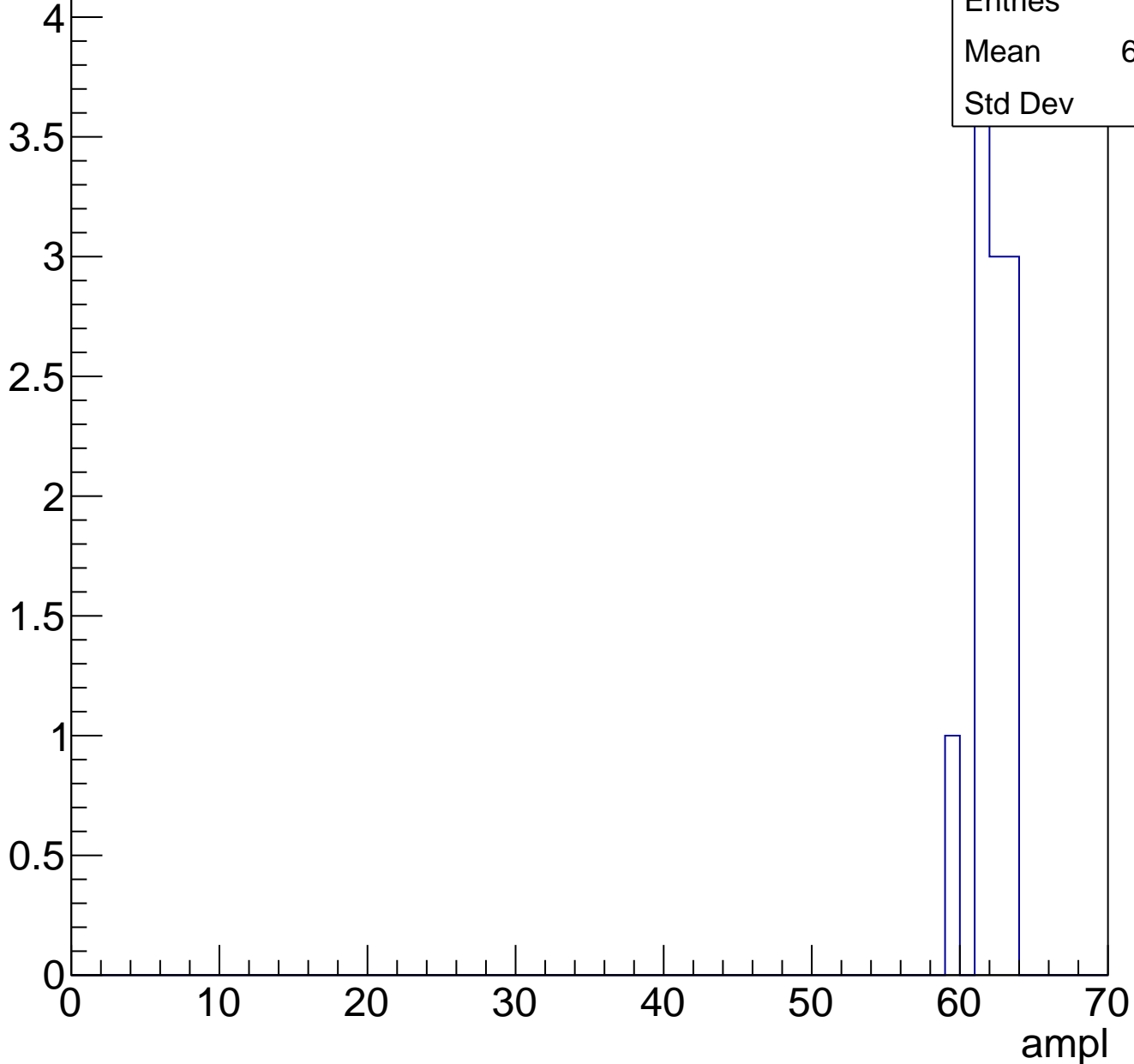
Entries	58
Mean	57.83
Std Dev	8.137



B1L103S, U23-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch67, adc0

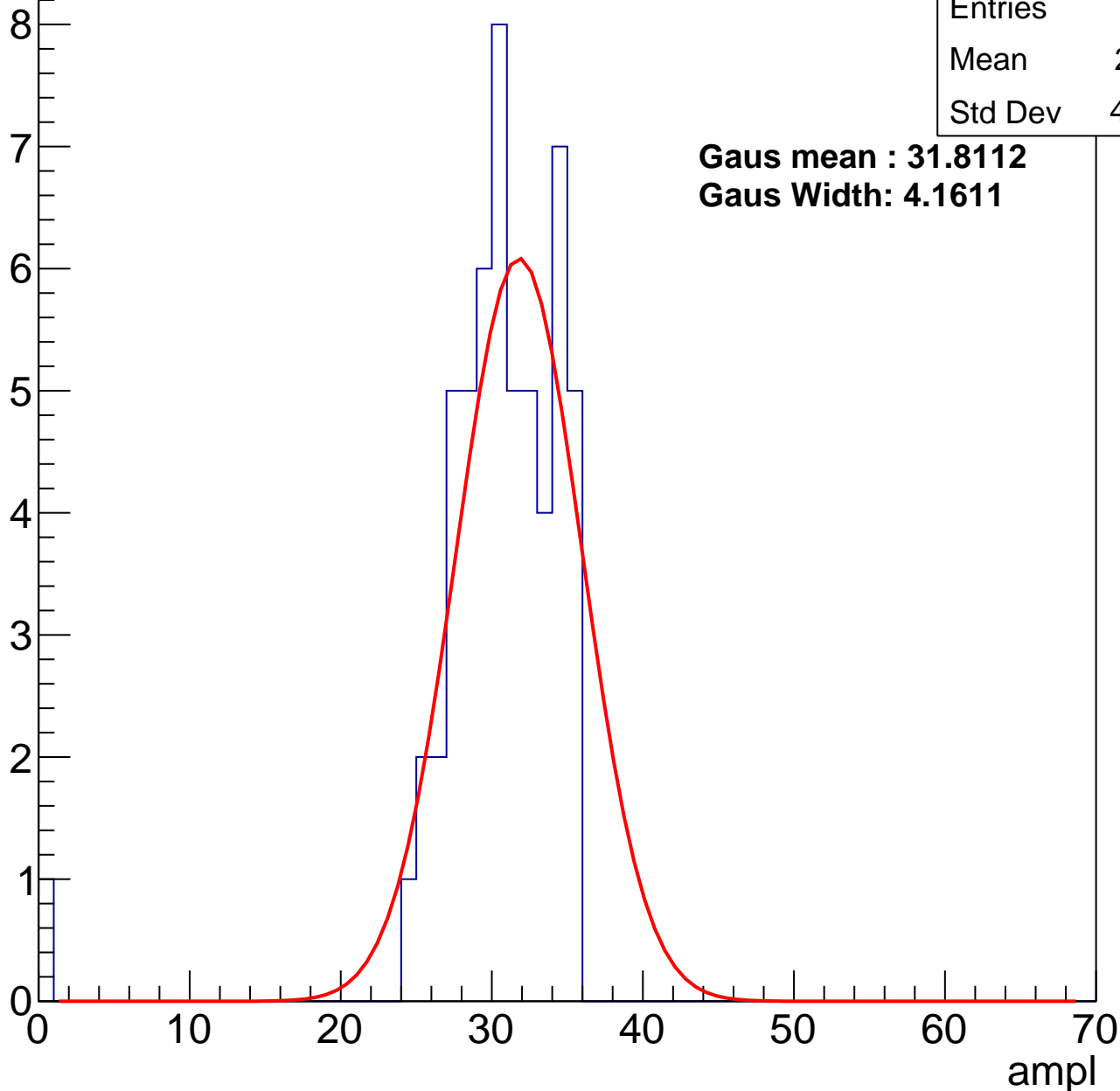
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	29.91
Std Dev	4.976

Gaus mean : 31.8112

Gaus Width: 4.1611



B1L103S, U23-ch67, adc1

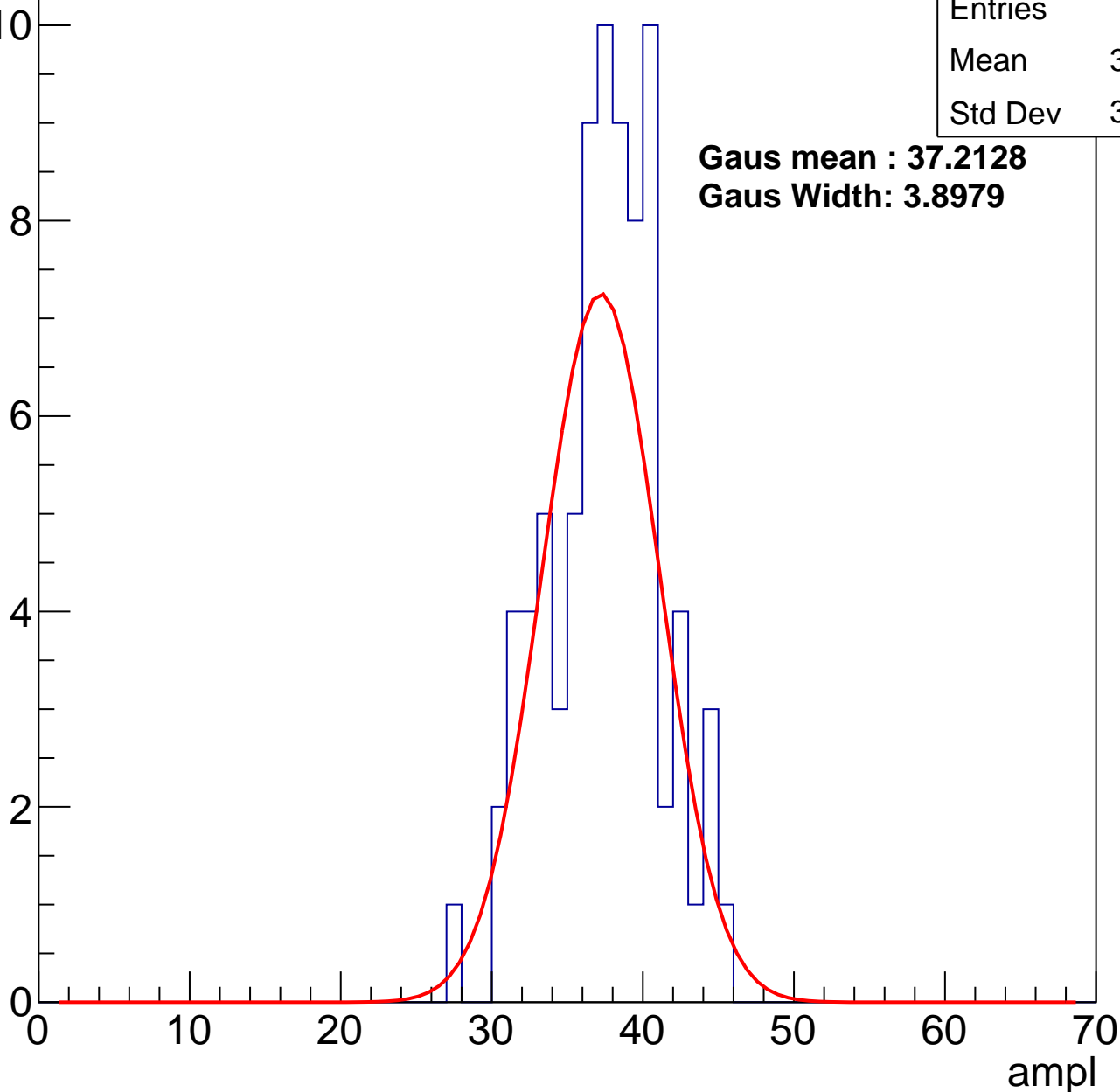
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	37.02
Std Dev	3.665

Gaus mean : 37.2128

Gaus Width: 3.8979



B1L103S, U23-ch67, adc2

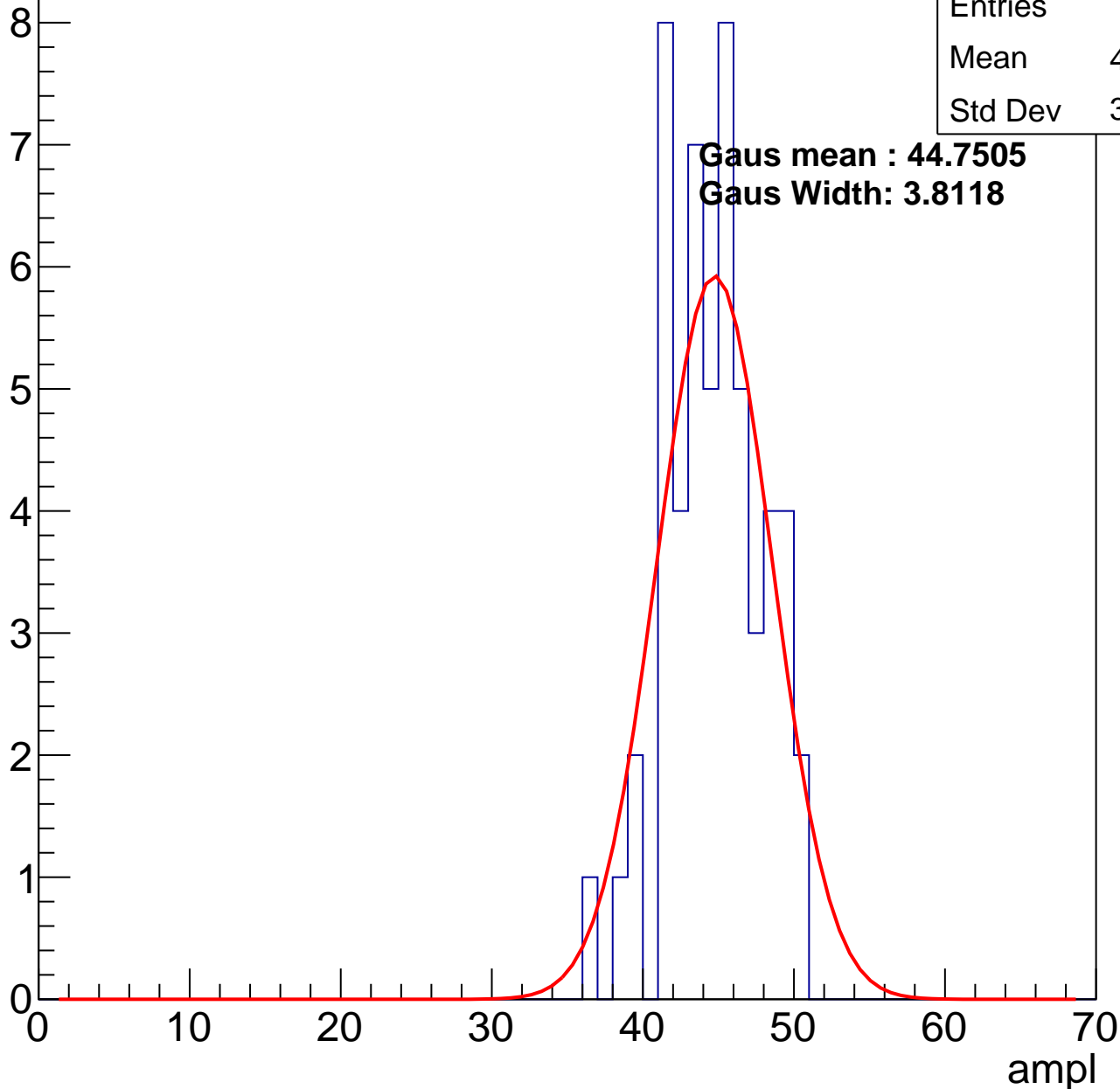
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	44.22
Std Dev	3.143

Gaus mean : 44.7505

Gaus Width: 3.8118

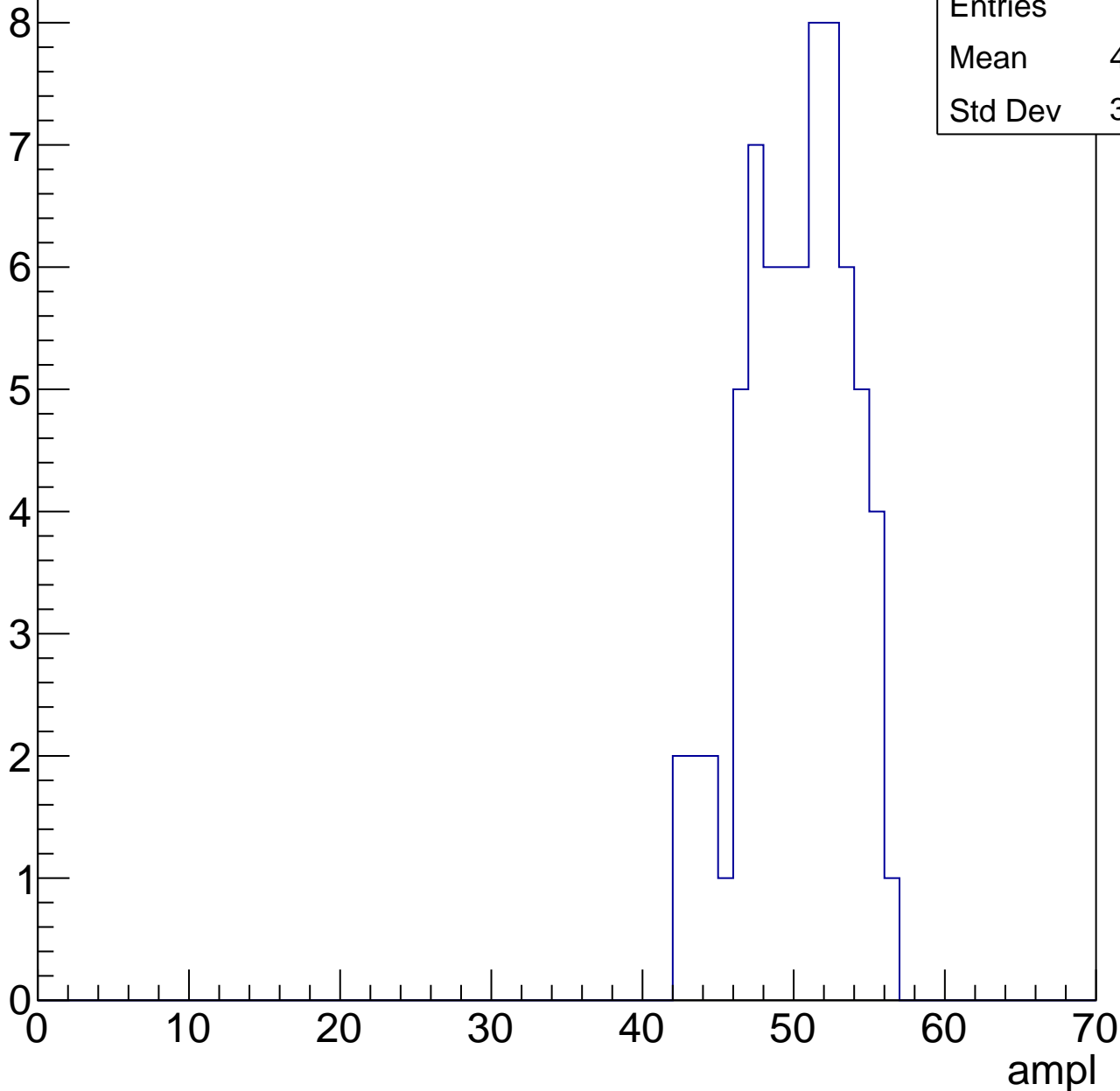


B1L103S, U23-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.74
Std Dev	3.408

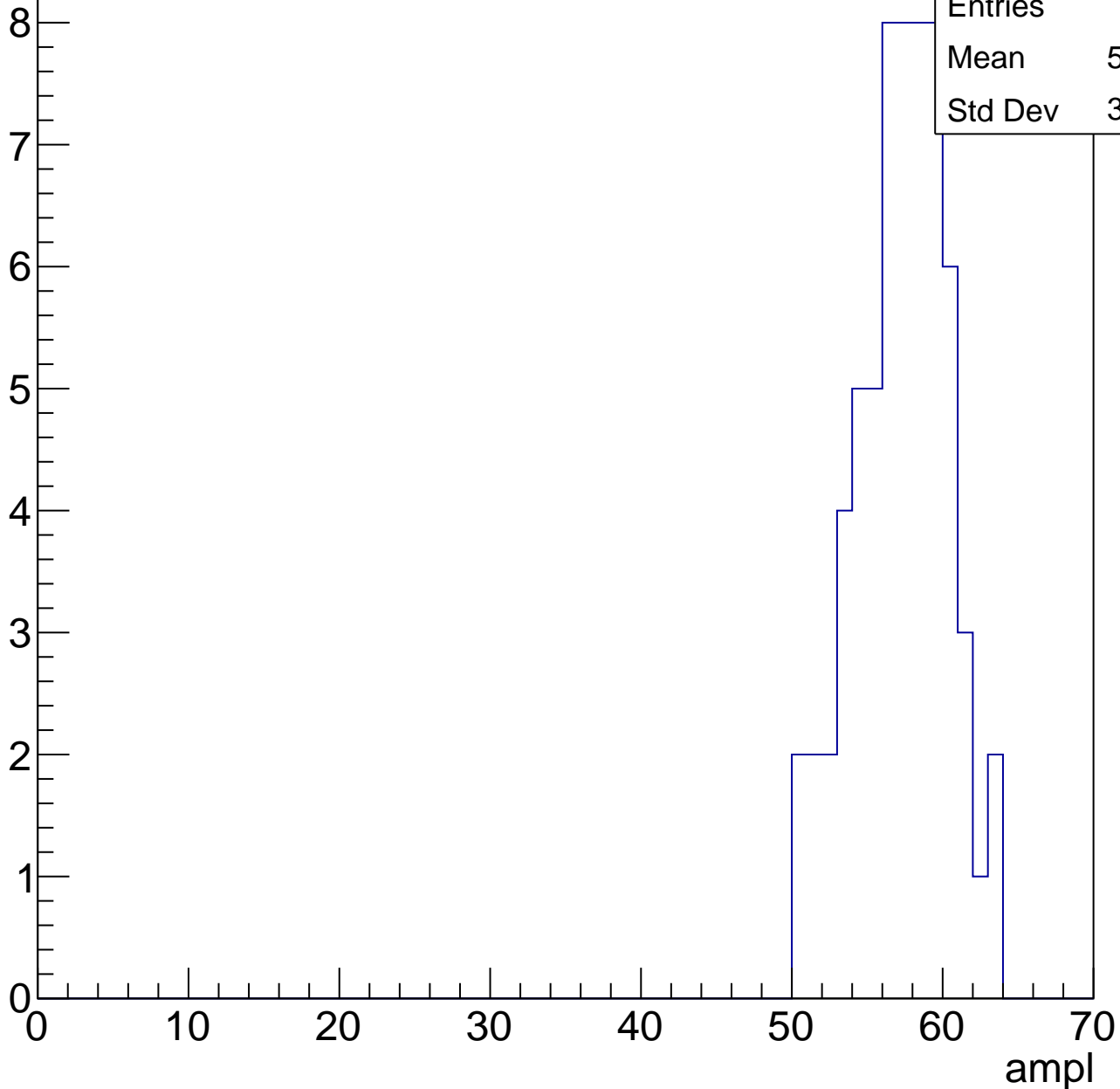


B1L103S, U23-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	56.78
Std Dev	3.044

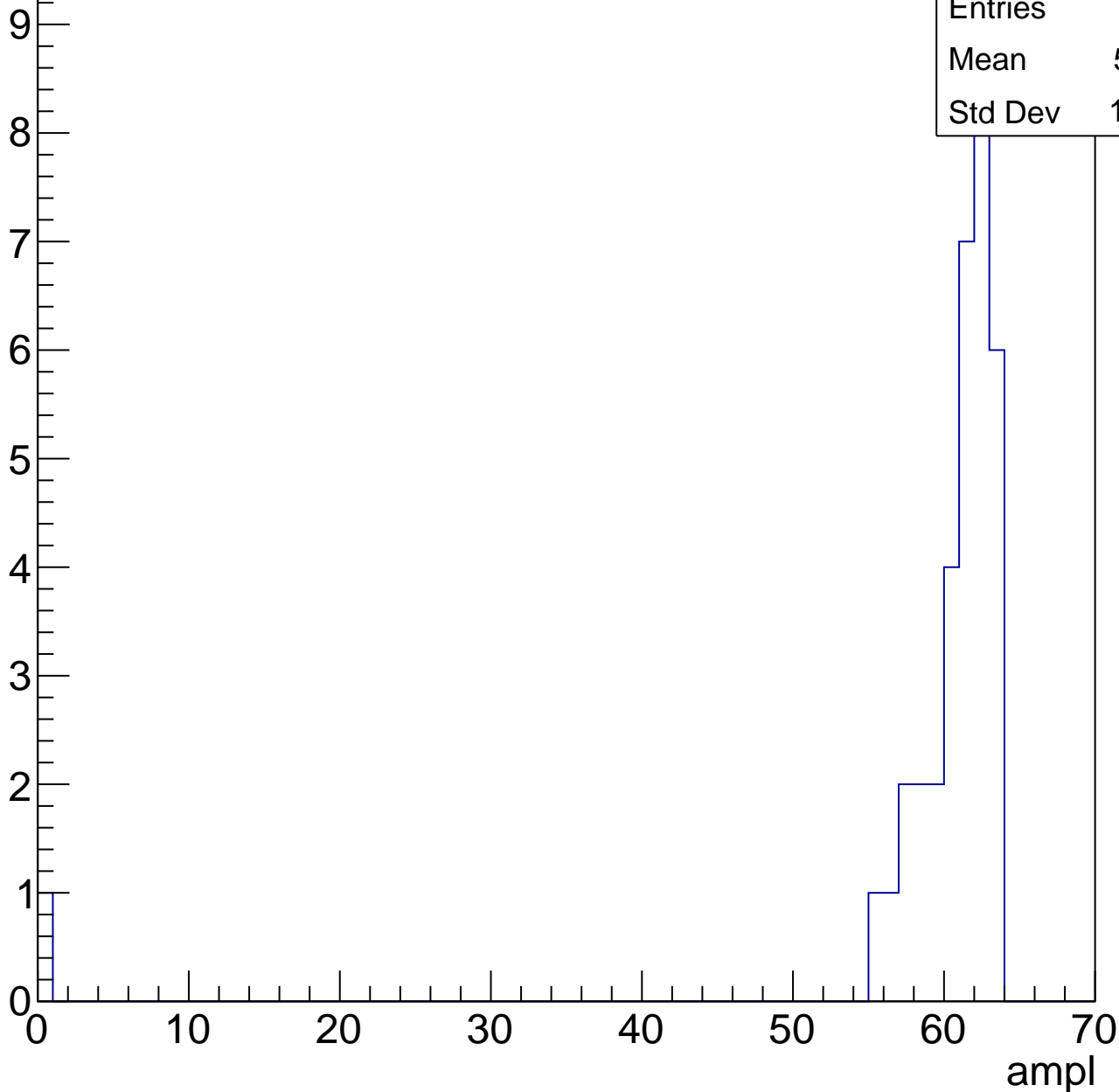


B1L103S, U23-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.91
Std Dev	10.32



B1L103S, U23-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L103S, U23-ch67, adc7

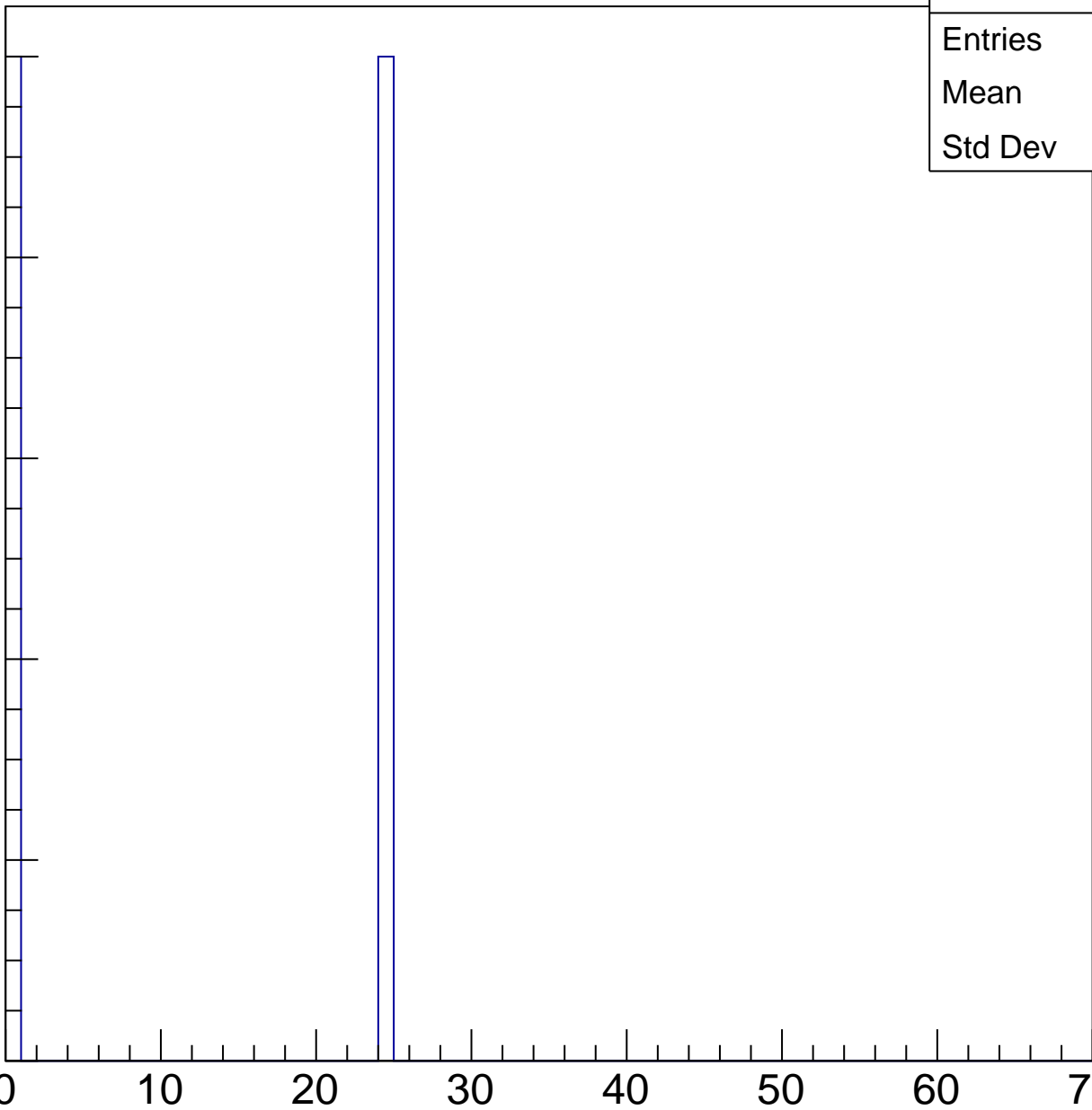
calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl



B1L103S, U23-ch68, adc0

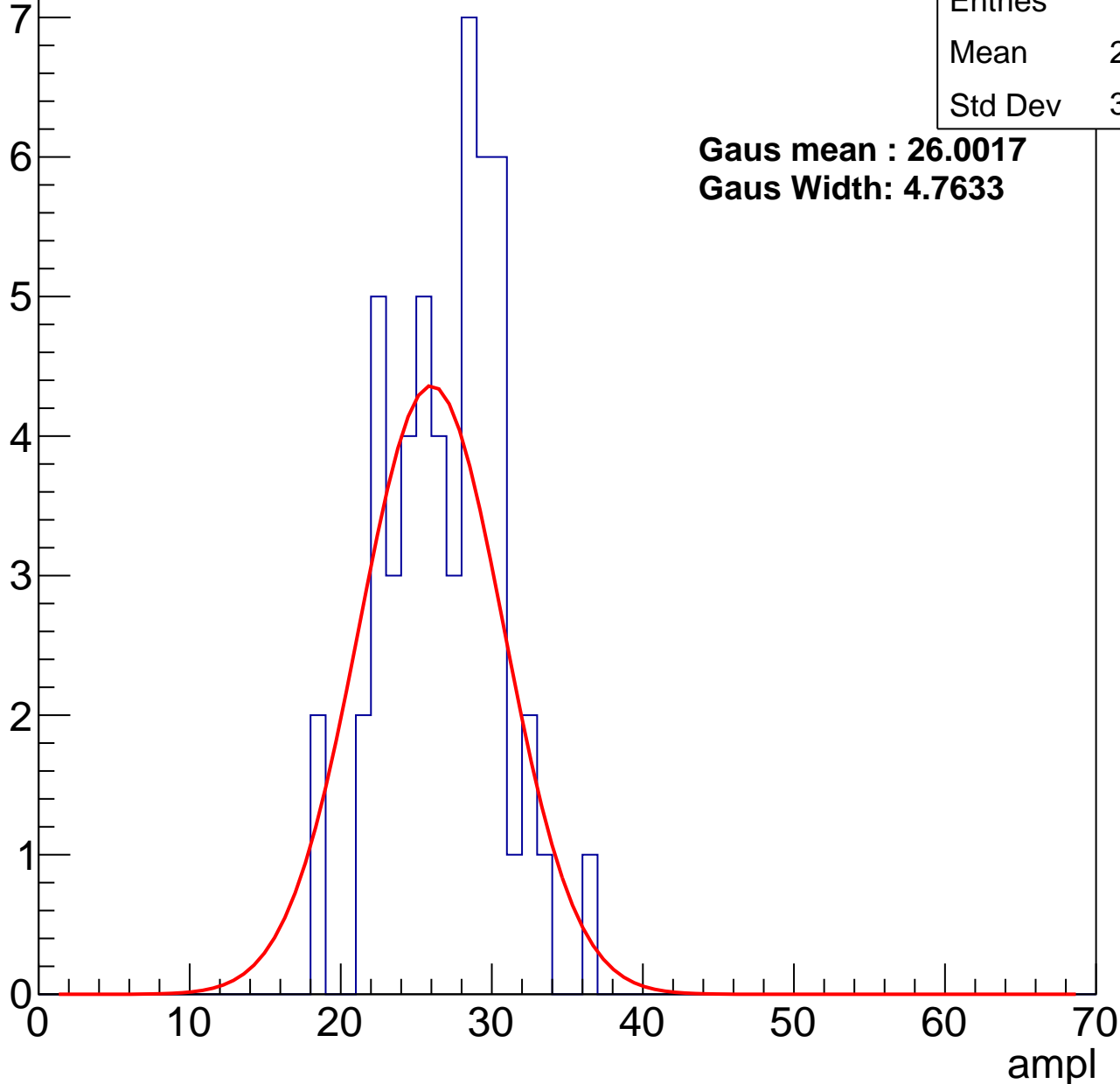
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	26.48
Std Dev	3.724

Gaus mean : 26.0017

Gaus Width: 4.7633



B1L103S, U23-ch68, adc1

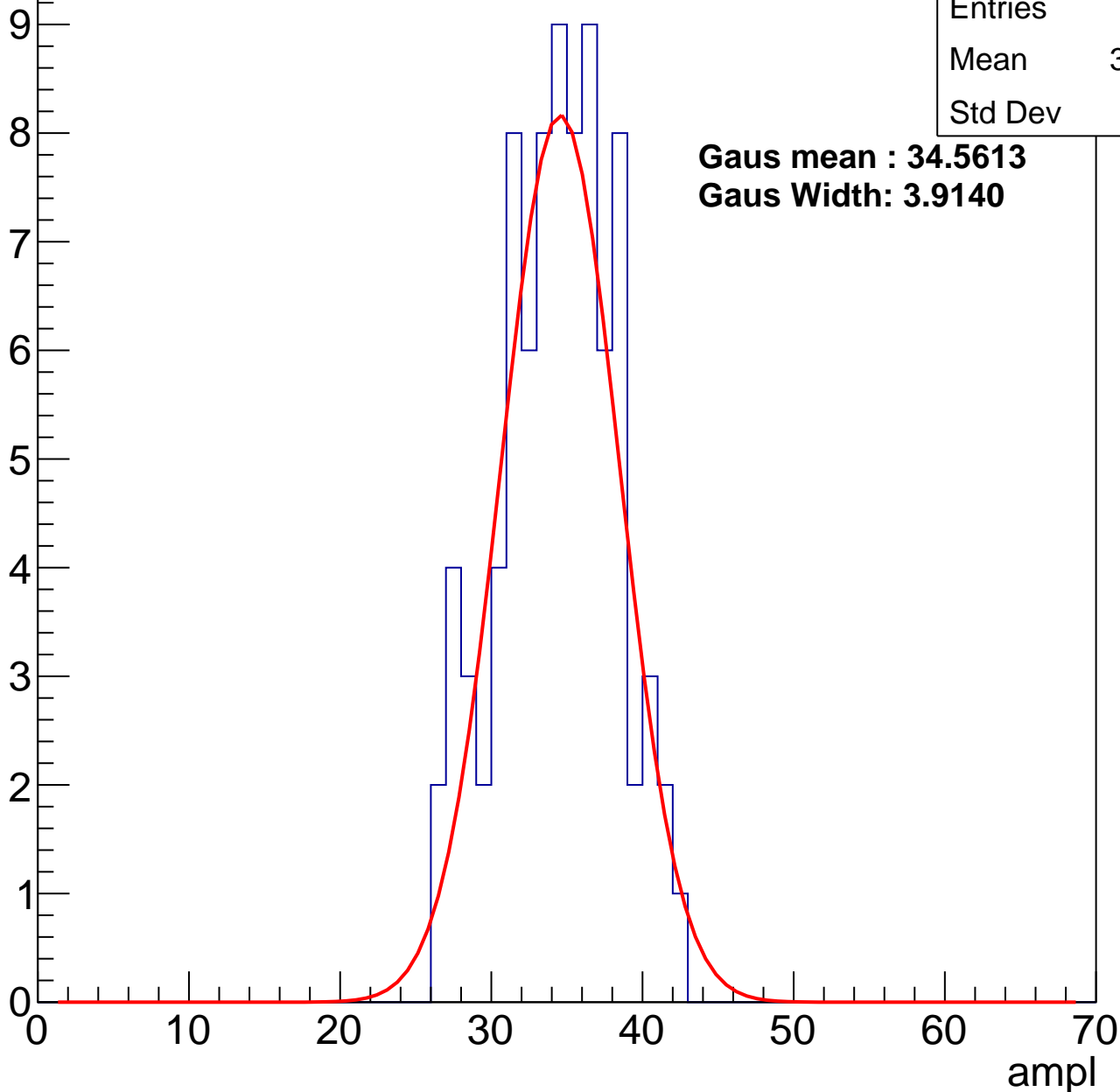
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	33.93
Std Dev	3.75

Gaus mean : 34.5613

Gaus Width: 3.9140



B1L103S, U23-ch68, adc2

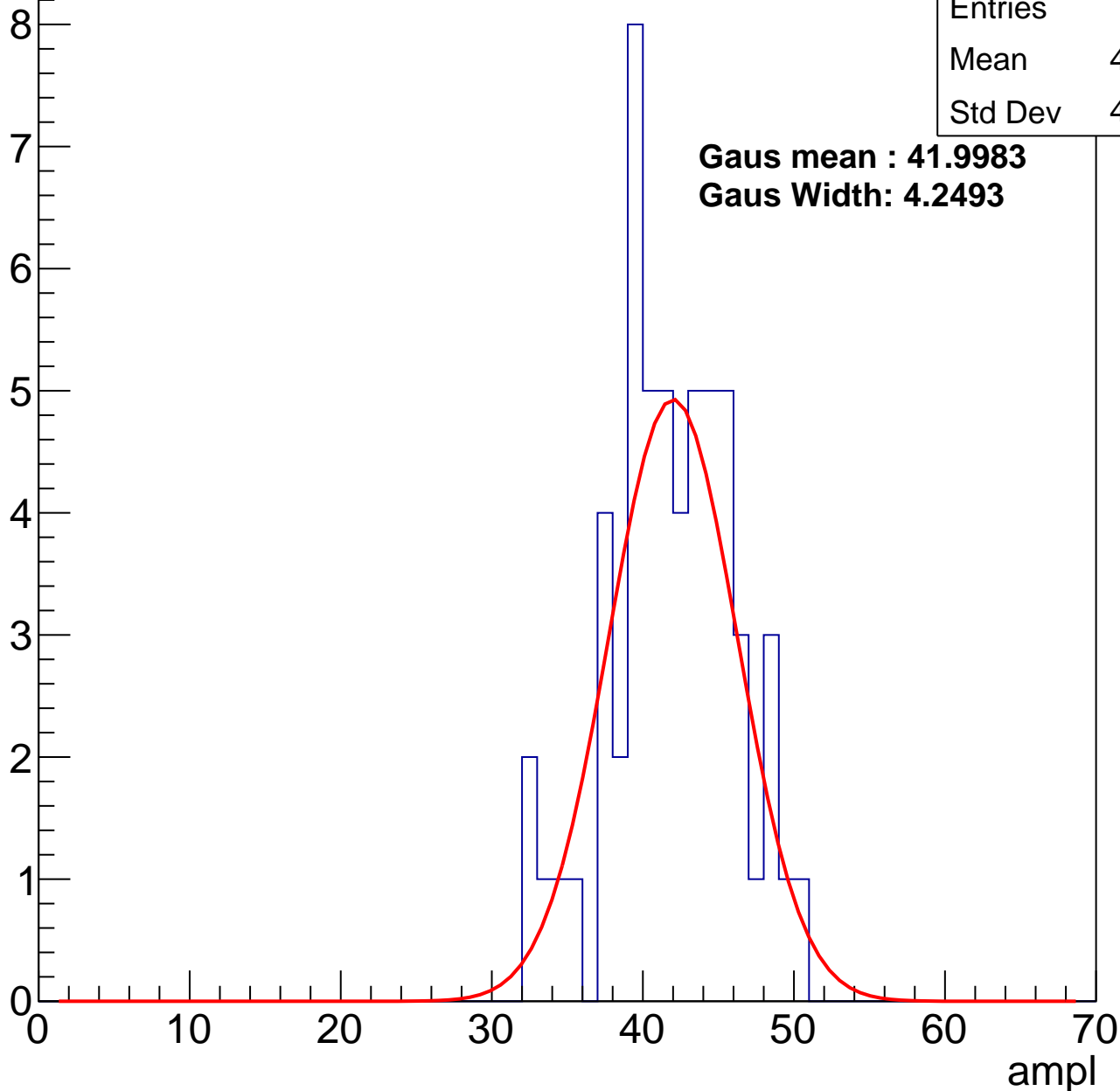
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.46
Std Dev	4.126

Gaus mean : 41.9983

Gaus Width: 4.2493

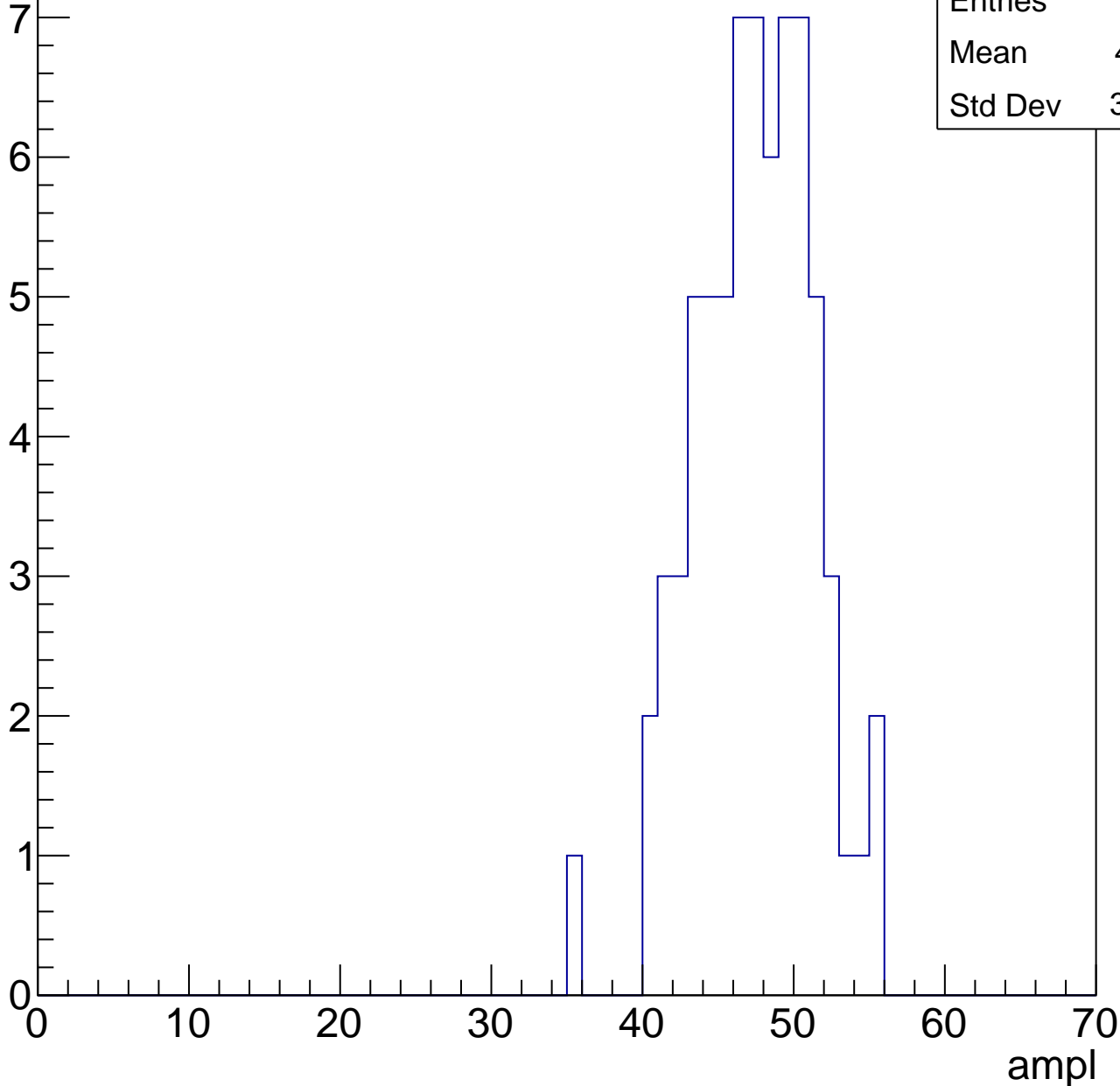


B1L103S, U23-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	46.91
Std Dev	3.857

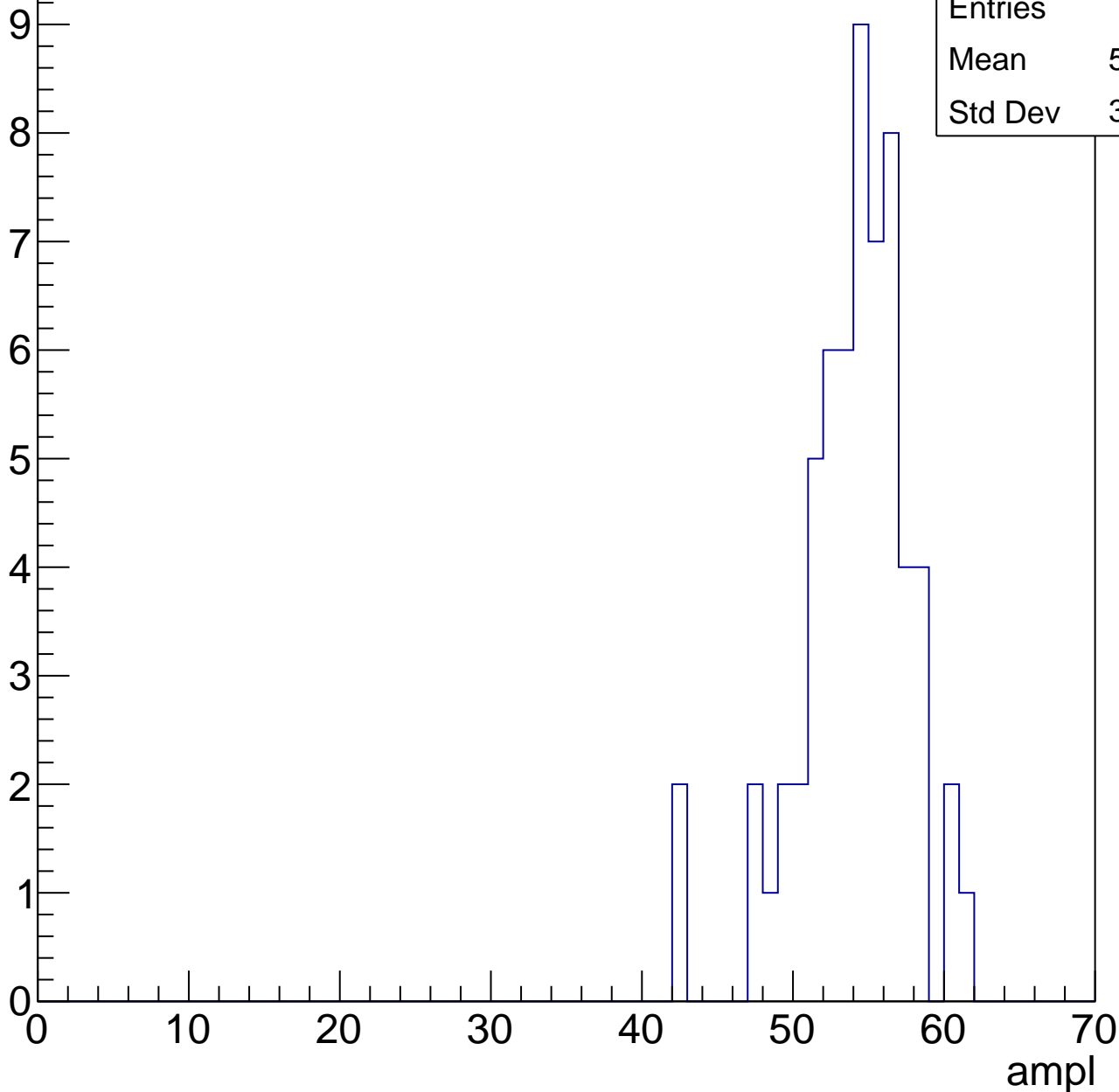


B1L103S, U23-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	53.59
Std Dev	3.686



B1L103S, U23-ch68, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries

43

Mean

59.21

Std Dev

2.93

ampl

0

10

20

30

40

50

60

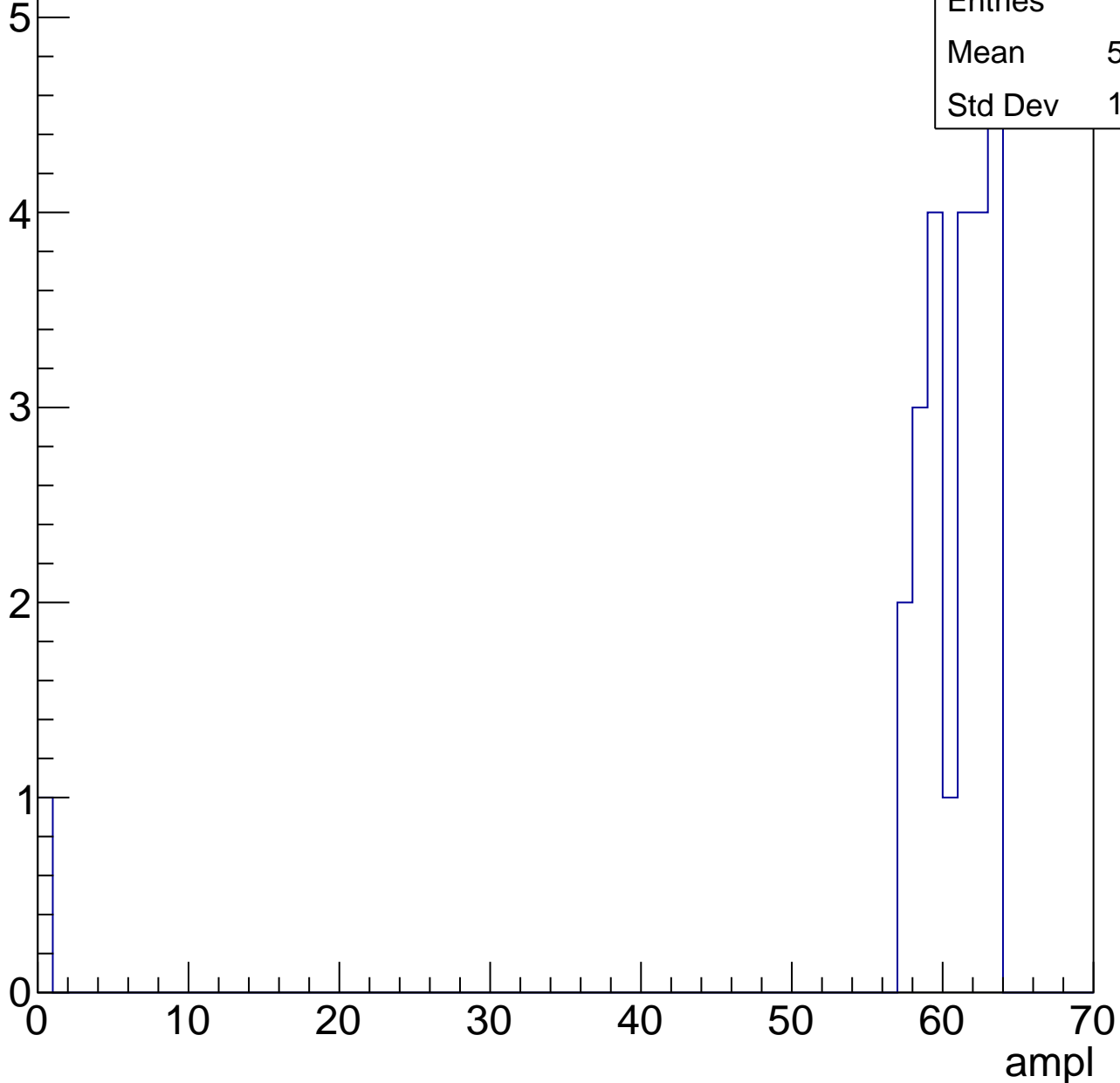
70

B1L103S, U23-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	24
Mean	57.96
Std Dev	12.25



B1L103S, U23-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch69, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	89
Mean	28.44
Std Dev	3.681

Gaus mean : 28.8857

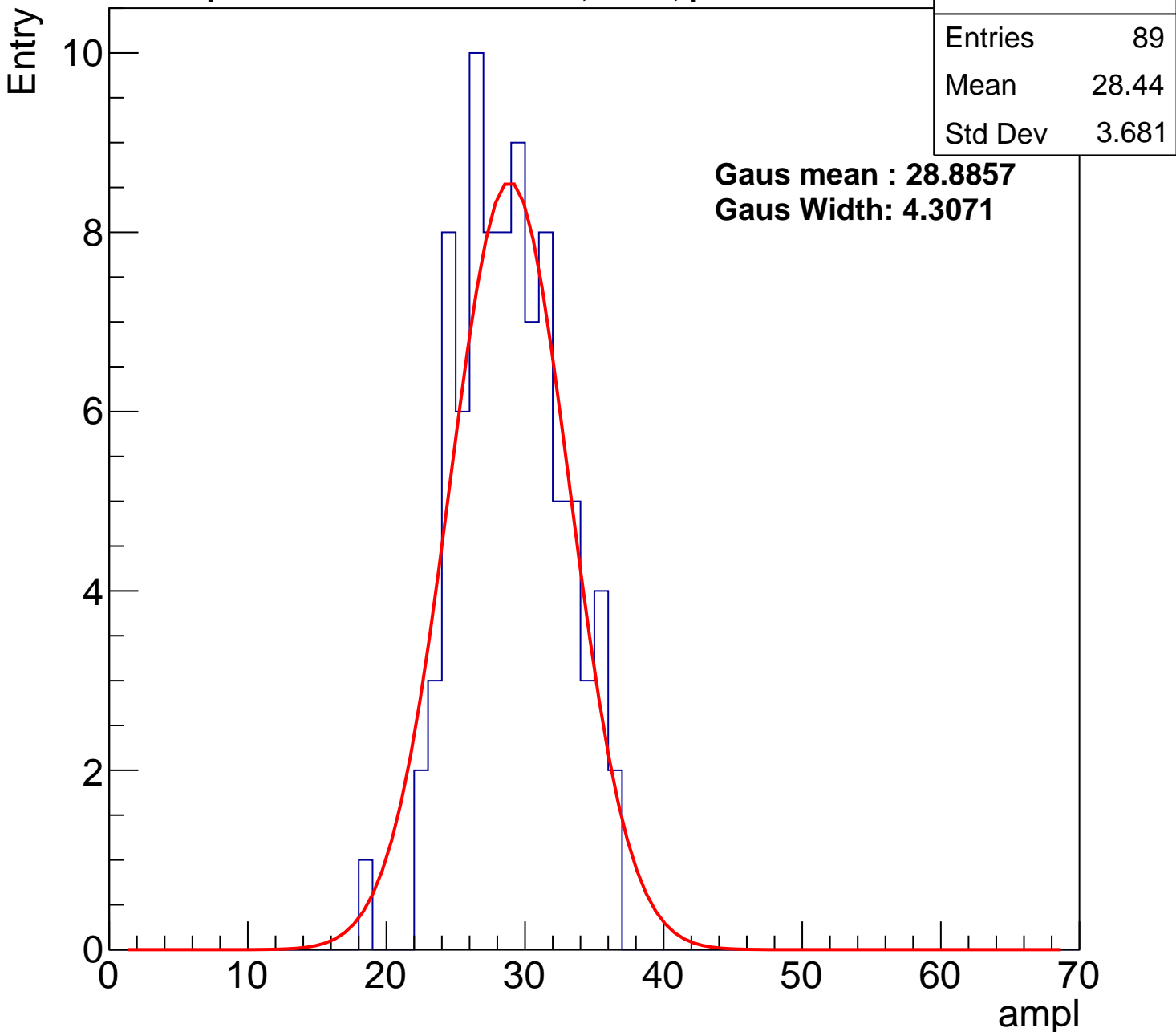
Gaus Width: 4.3071

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch69, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	60
Mean	35.65
Std Dev	3.119

Gaus mean : 36.2085

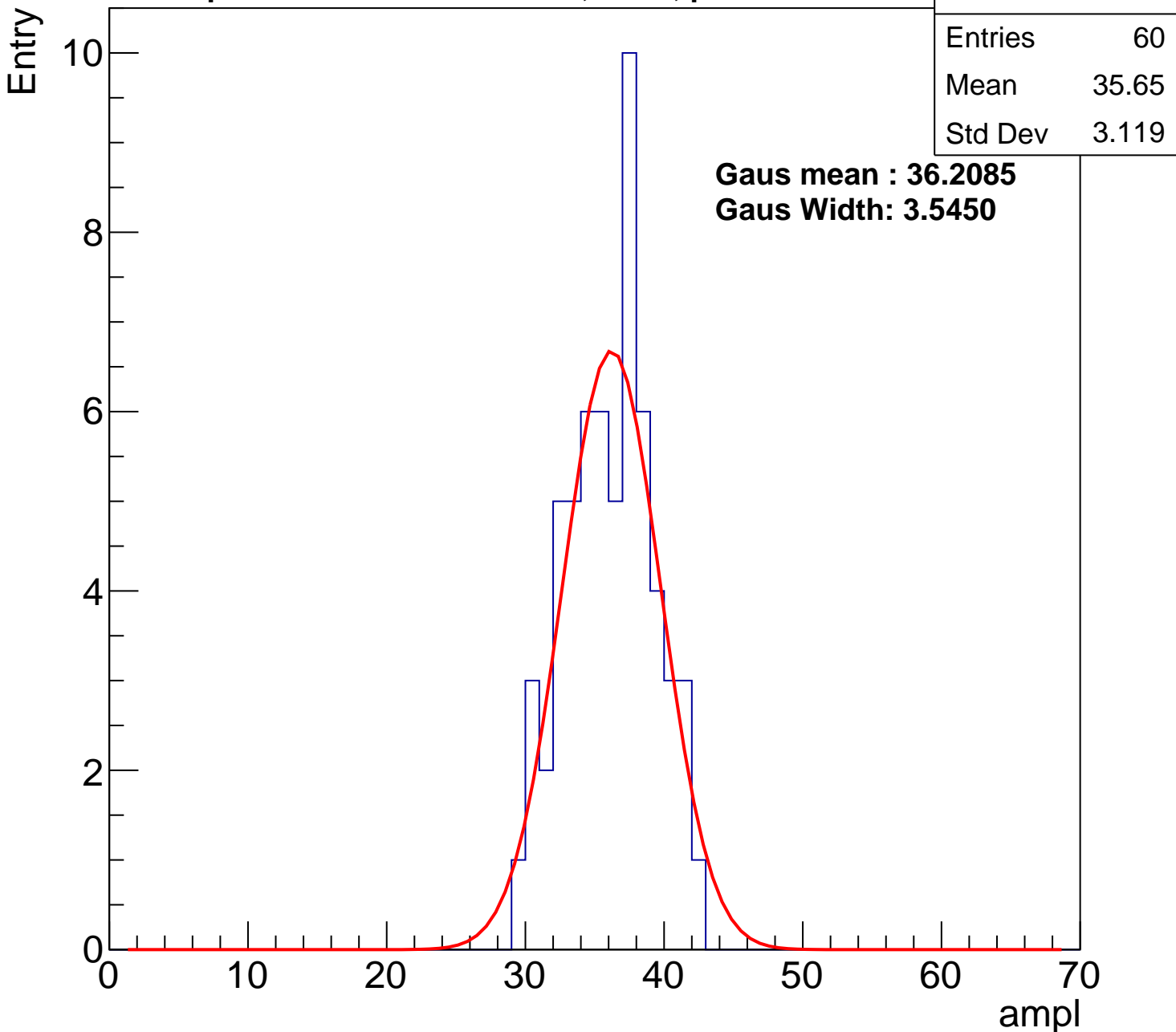
Gaus Width: 3.5450

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch69, adc2

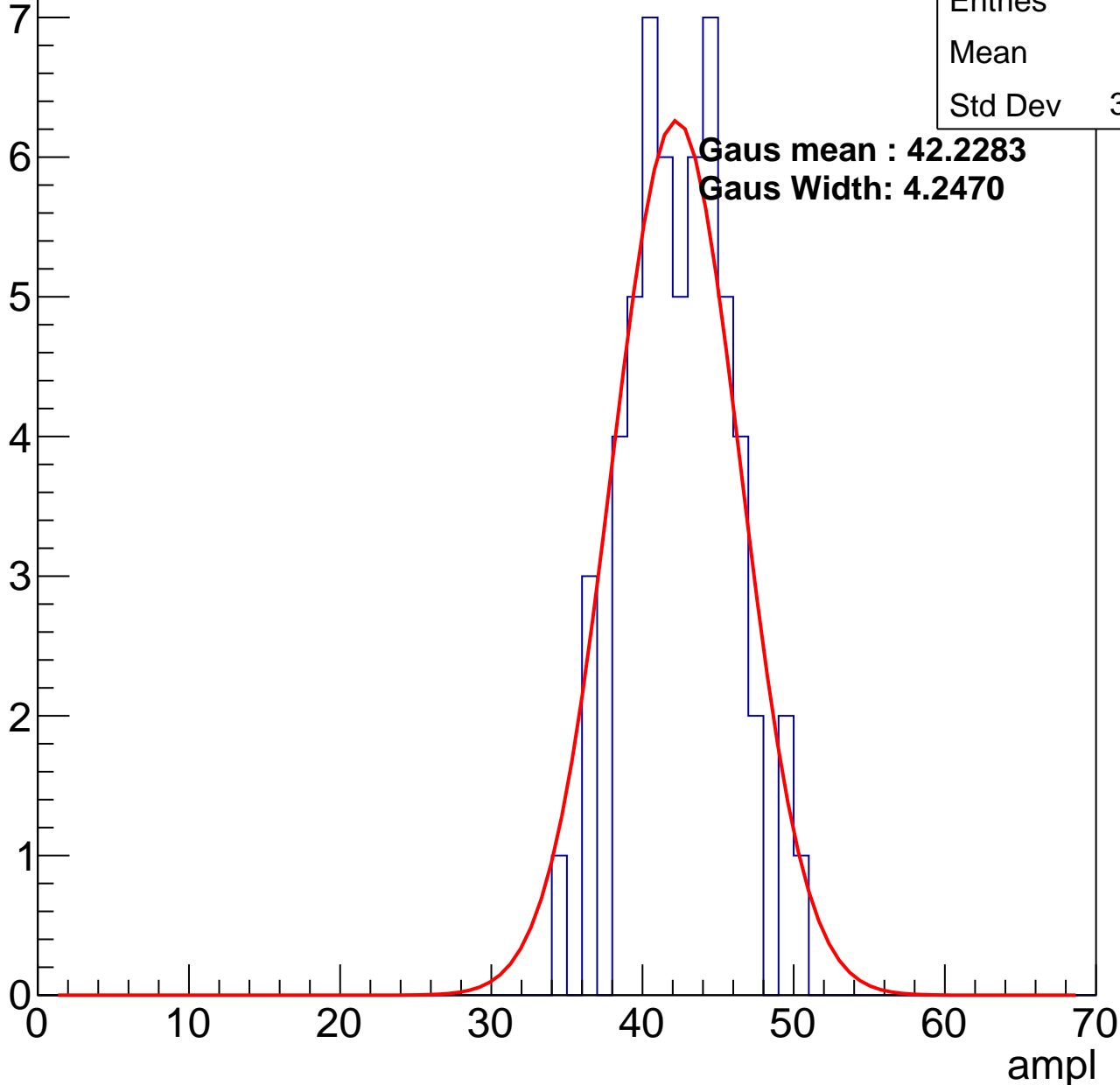
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.1
Std Dev	3.397

Gaus mean : 42.2283

Gaus Width: 4.2470

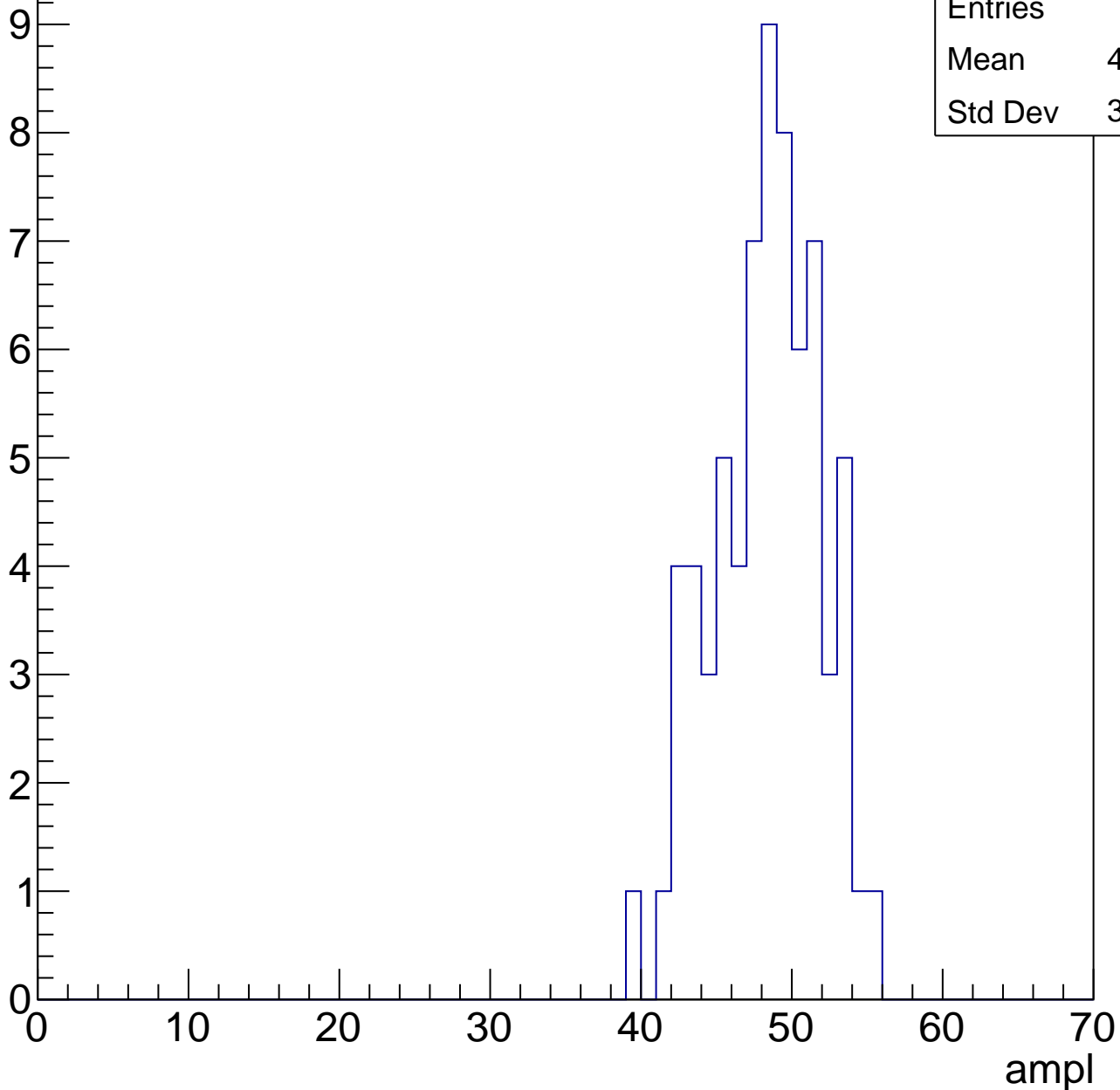


B1L103S, U23-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

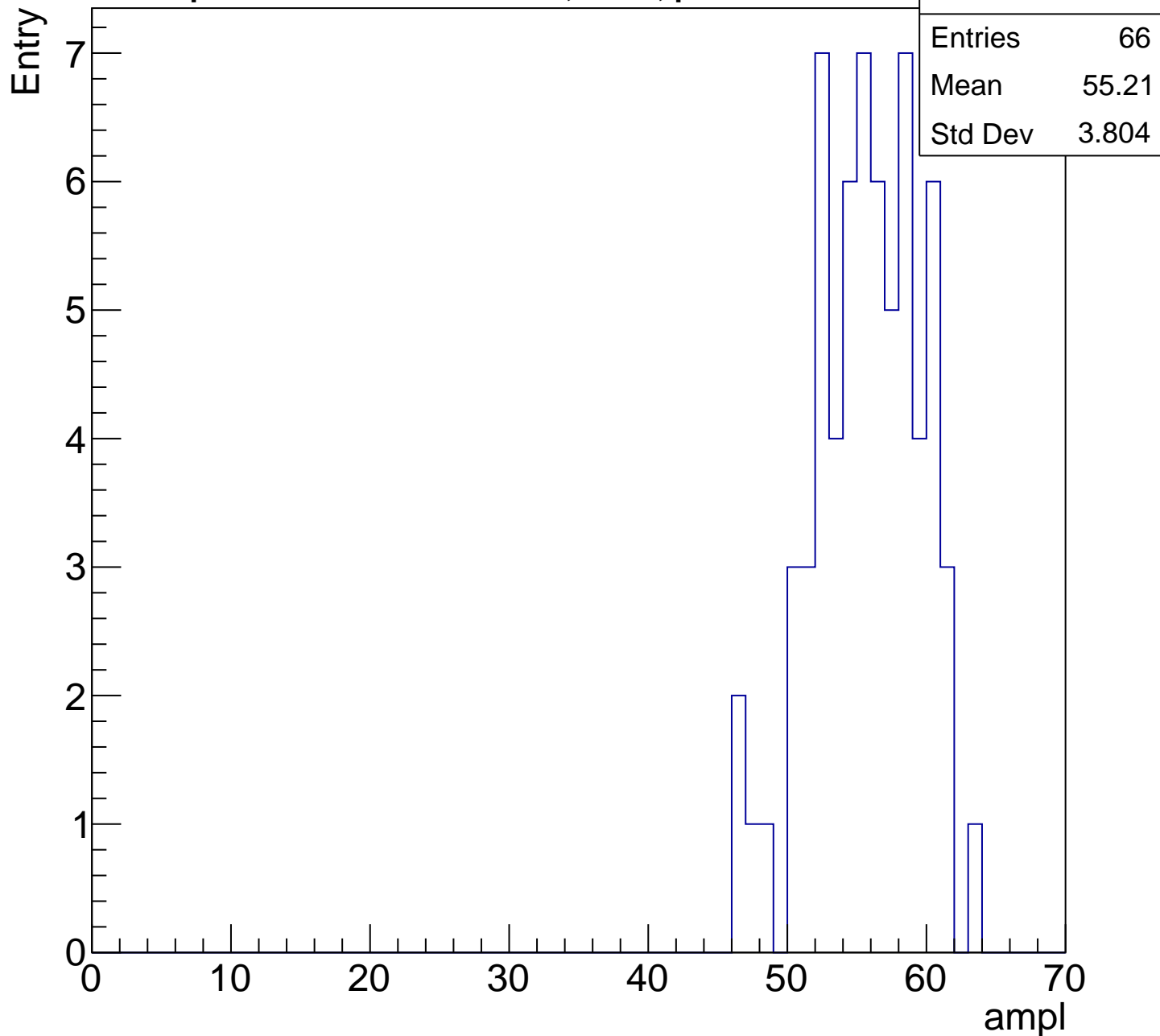
Entry

Entries	69
Mean	47.84
Std Dev	3.492



B1L103S, U23-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

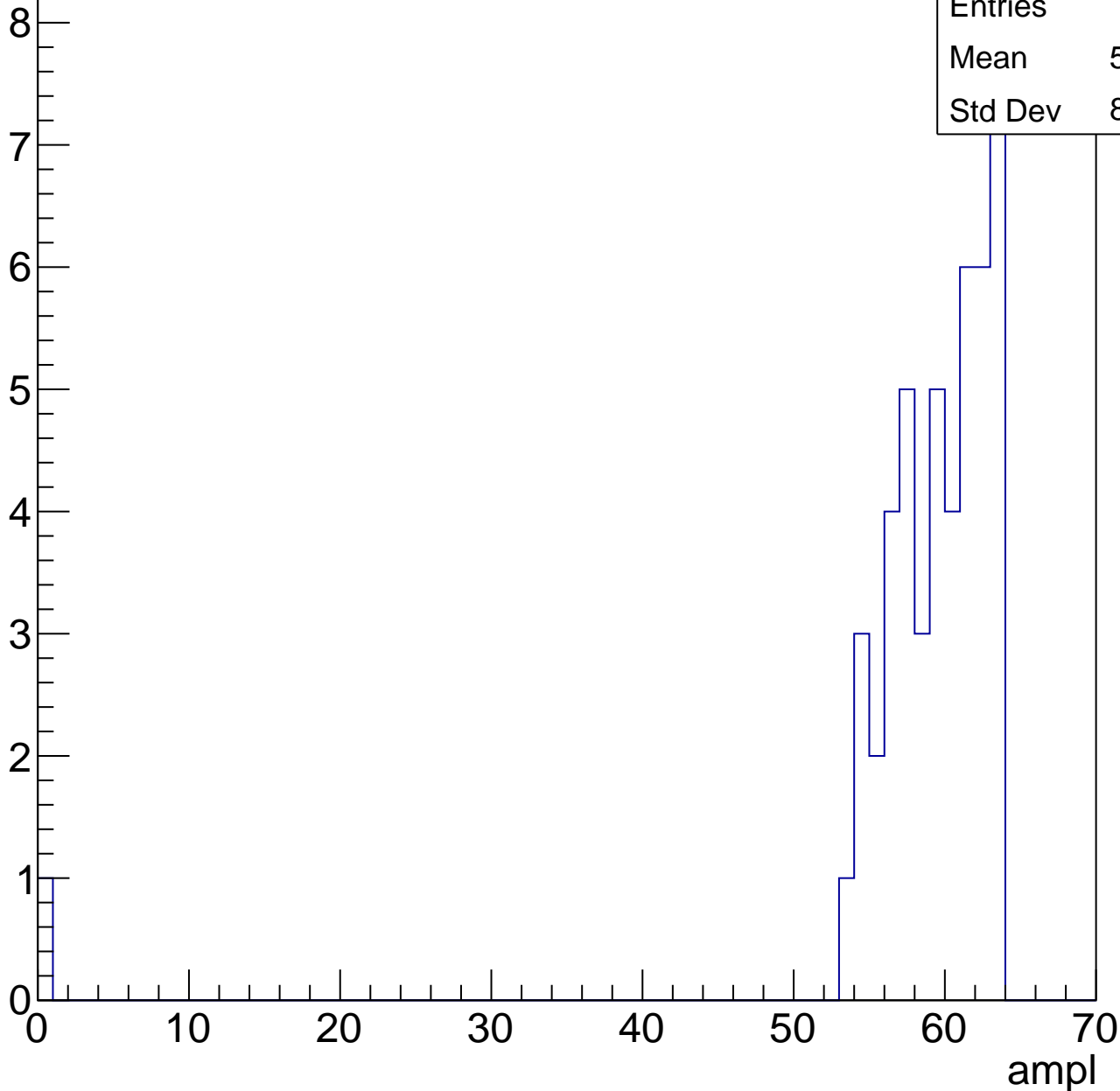


B1L103S, U23-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

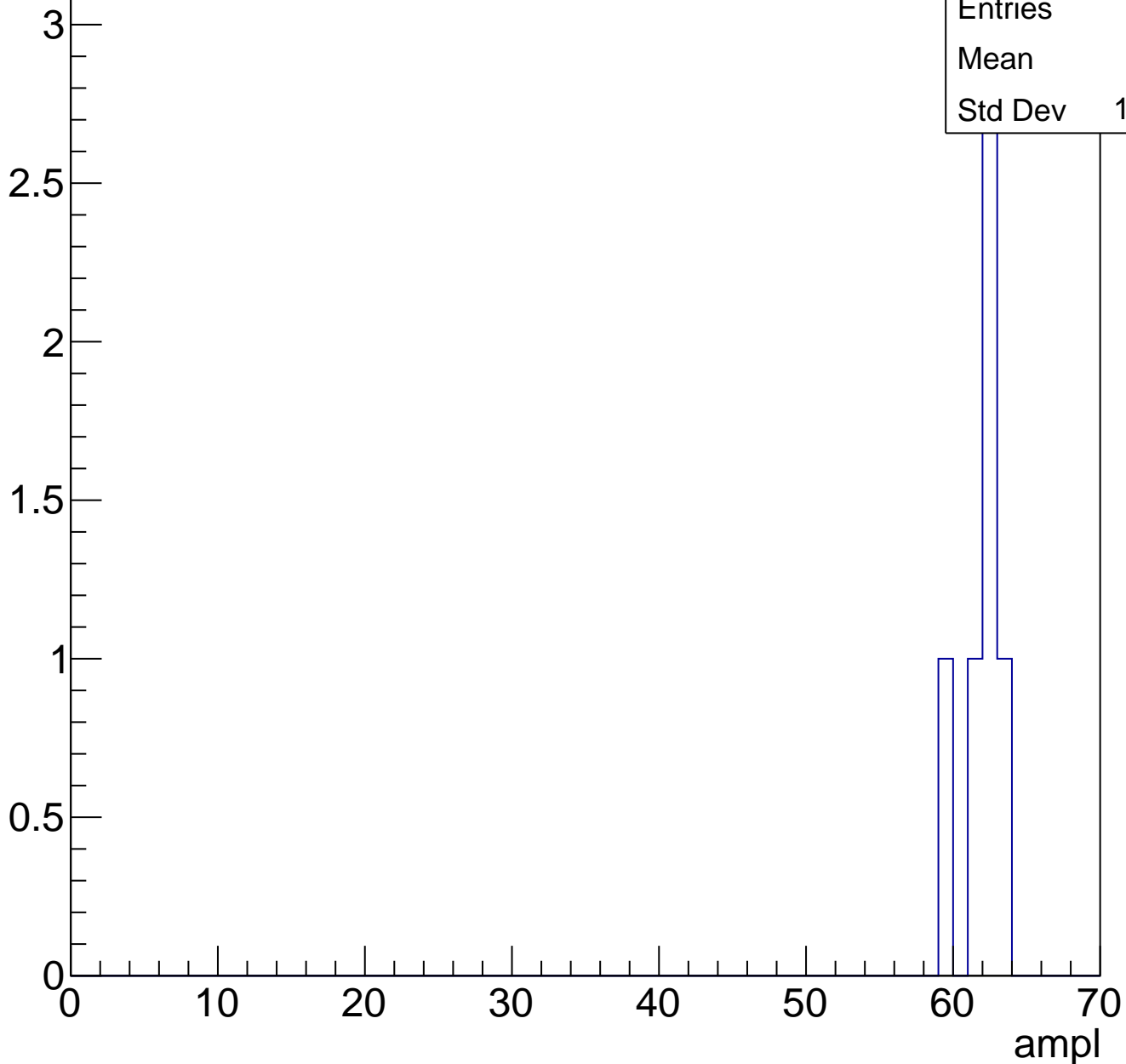
Entries	48
Mean	58.02
Std Dev	8.952



B1L103S, U23-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U23-ch70, adc0

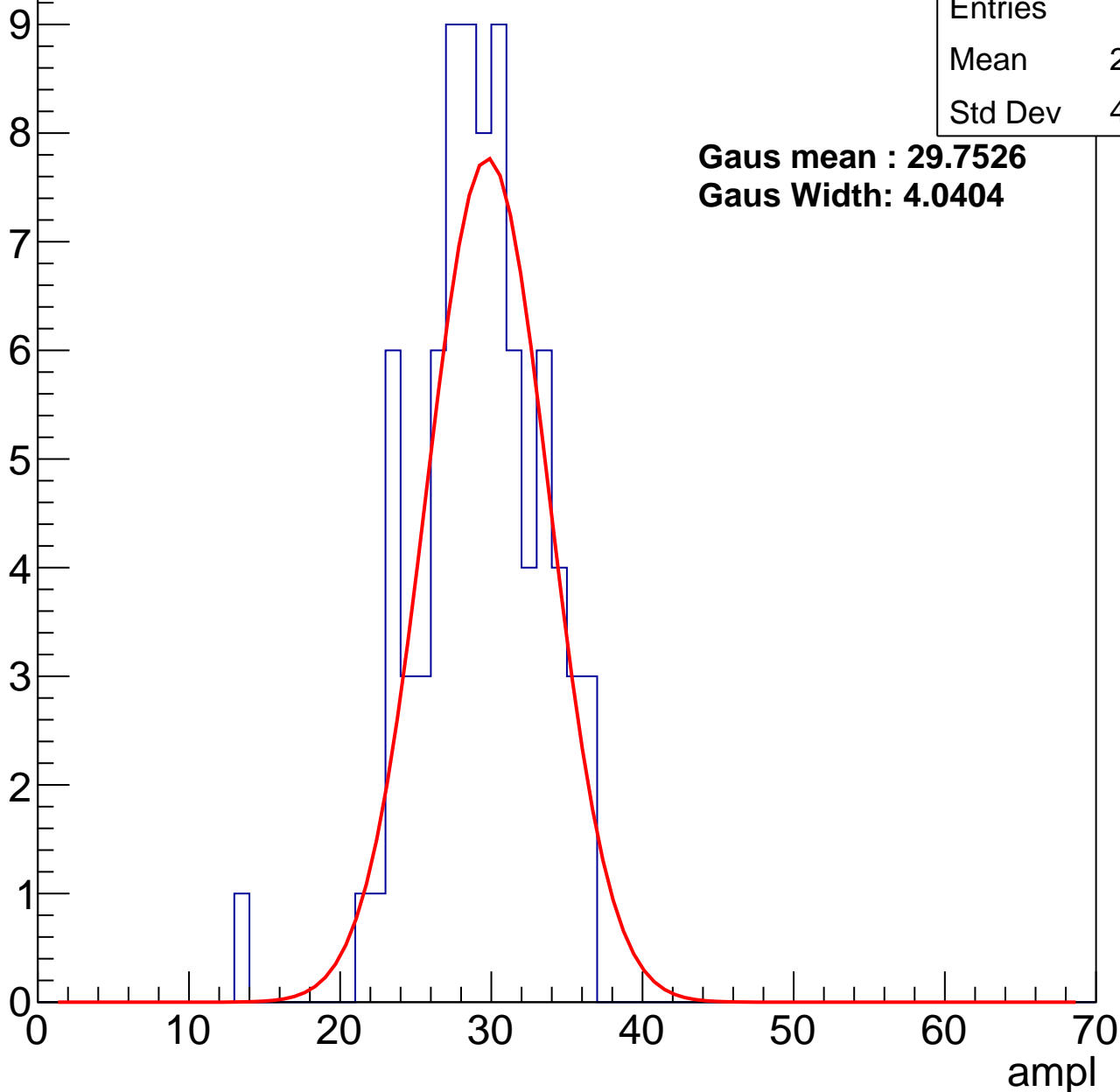
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	28.72
Std Dev	4.013

Gaus mean : 29.7526

Gaus Width: 4.0404



B1L103S, U23-ch70, adc1

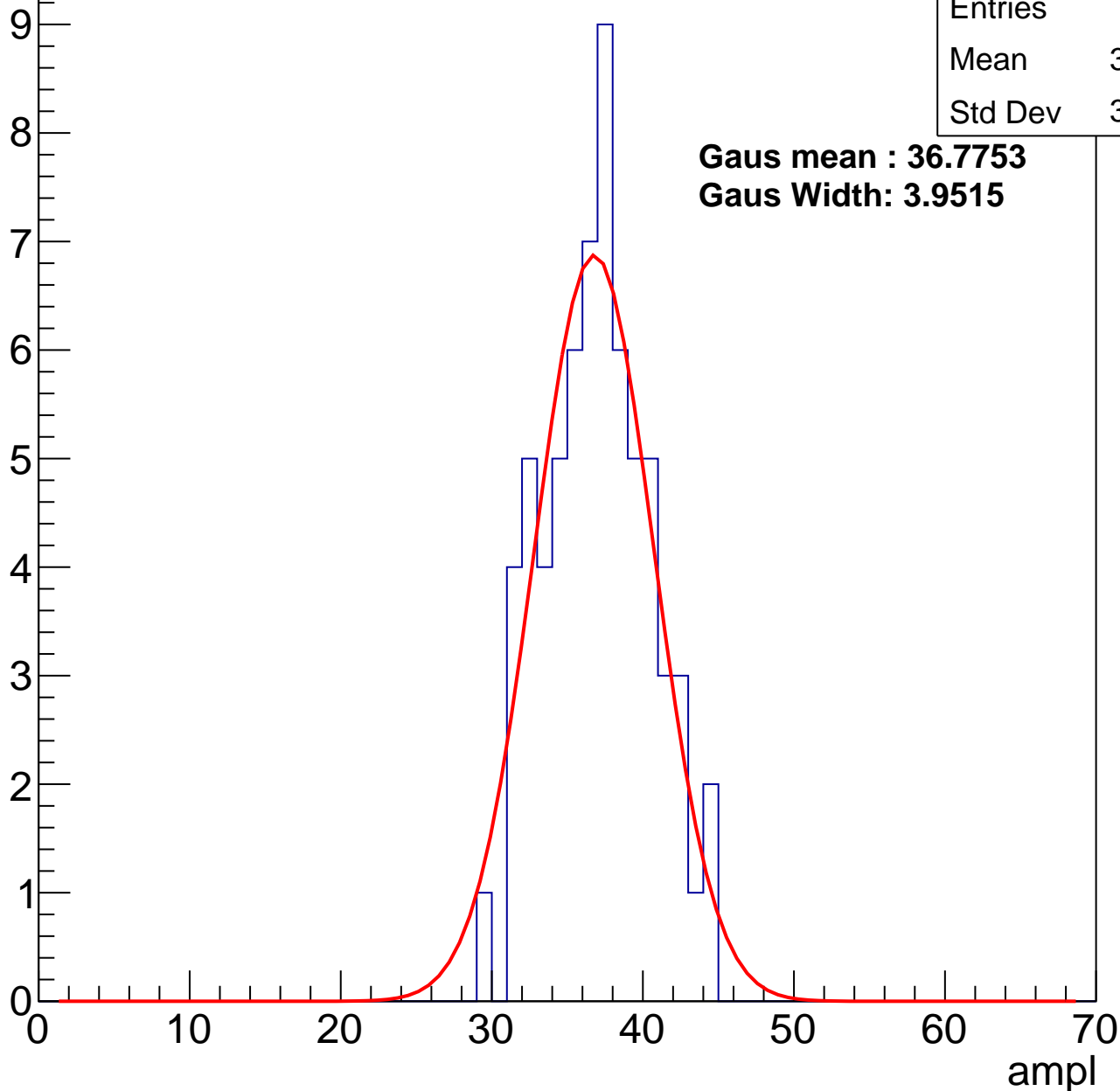
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.56
Std Dev	3.456

Gaus mean : 36.7753

Gaus Width: 3.9515



B1L103S, U23-ch70, adc2

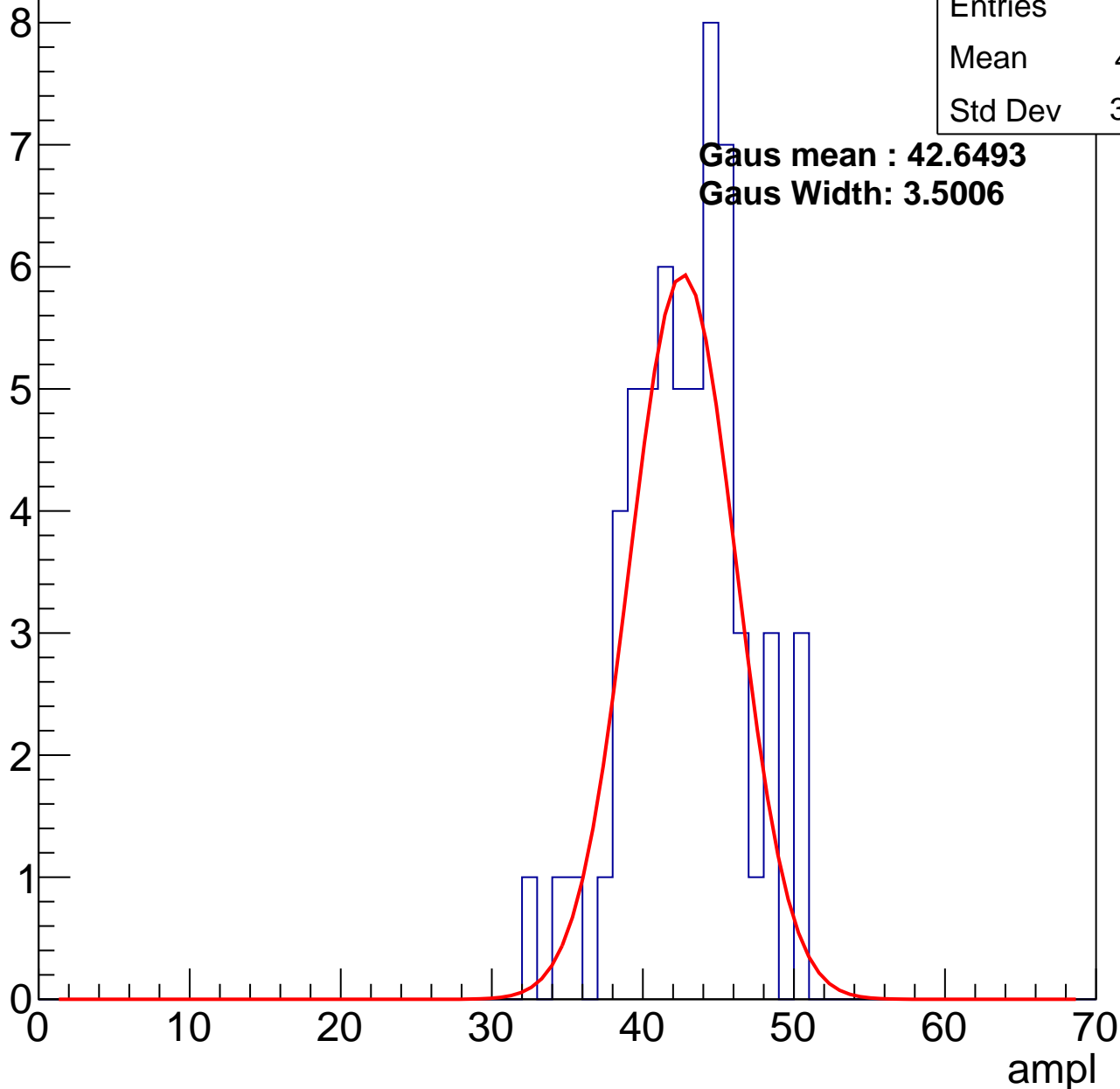
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.41
Std Dev	3.765

Gaus mean : 42.6493

Gaus Width: 3.5006

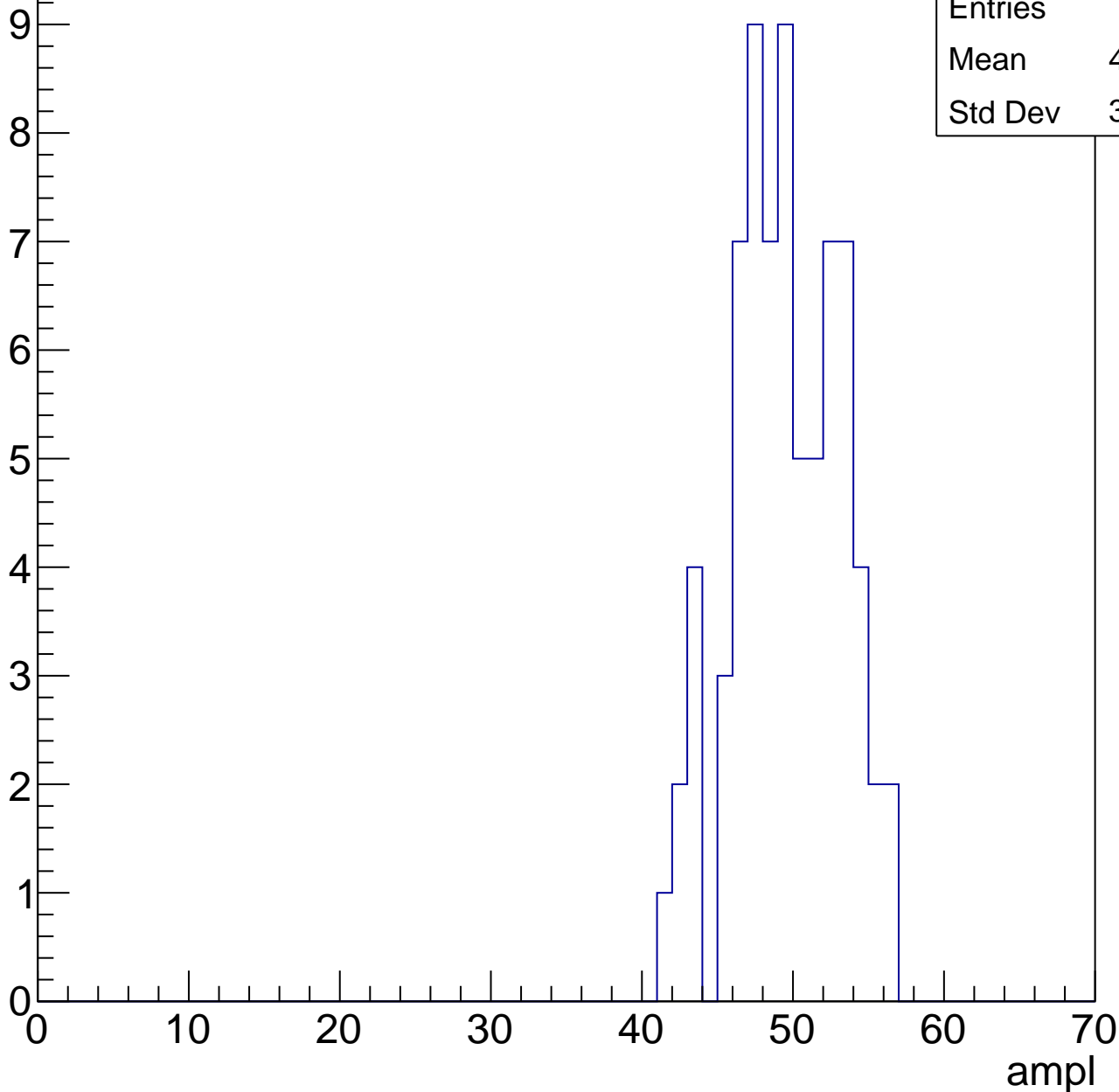


B1L103S, U23-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

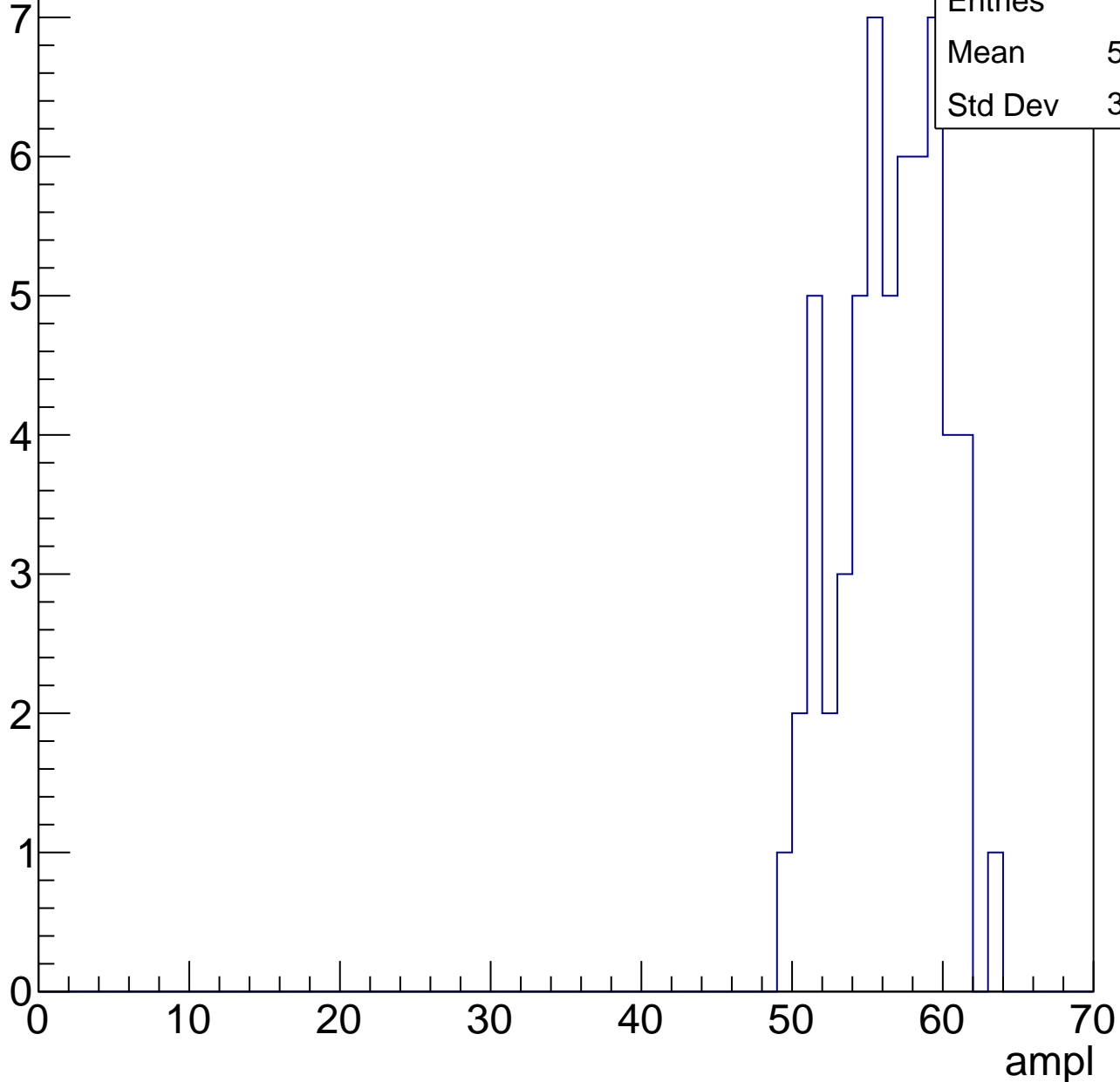
Entries	74
Mean	49.08
Std Dev	3.544



B1L103S, U23-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



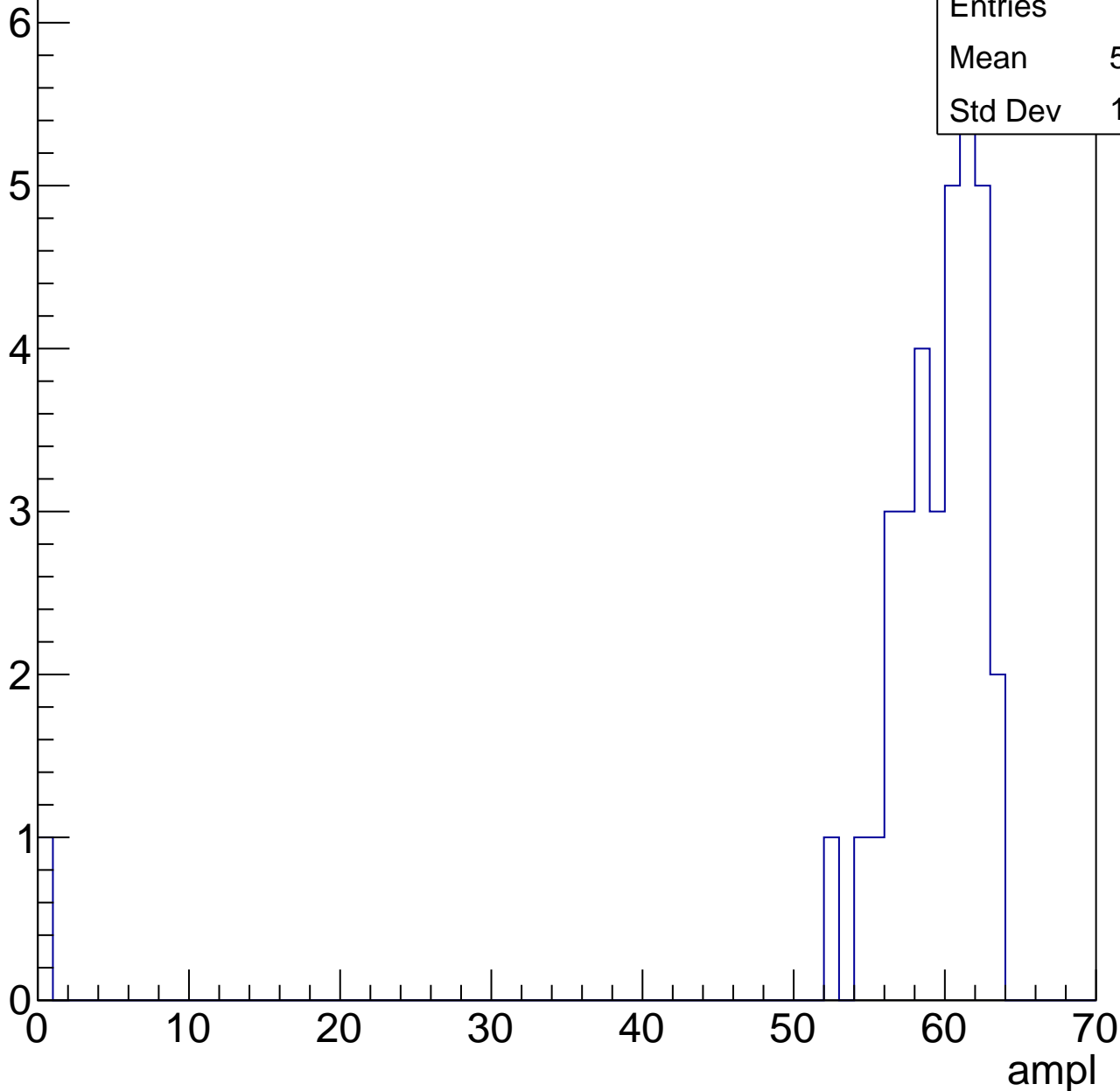
Entries	58
Mean	56.07
Std Dev	3.316

B1L103S, U23-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	57.46
Std Dev	10.19

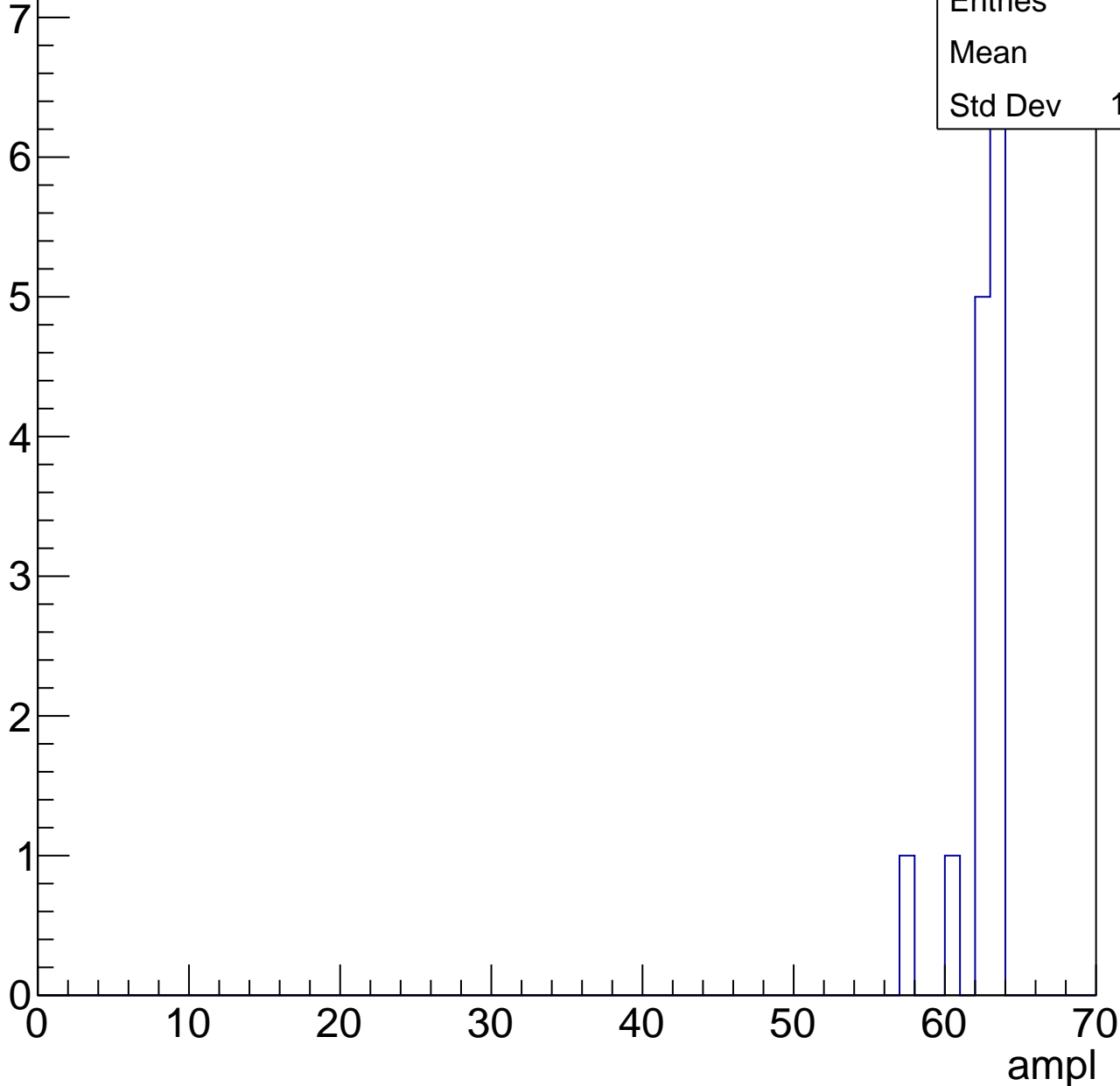


B1L103S, U23-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	62
Std Dev	1.604



B1L103S, U23-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch71, adc0

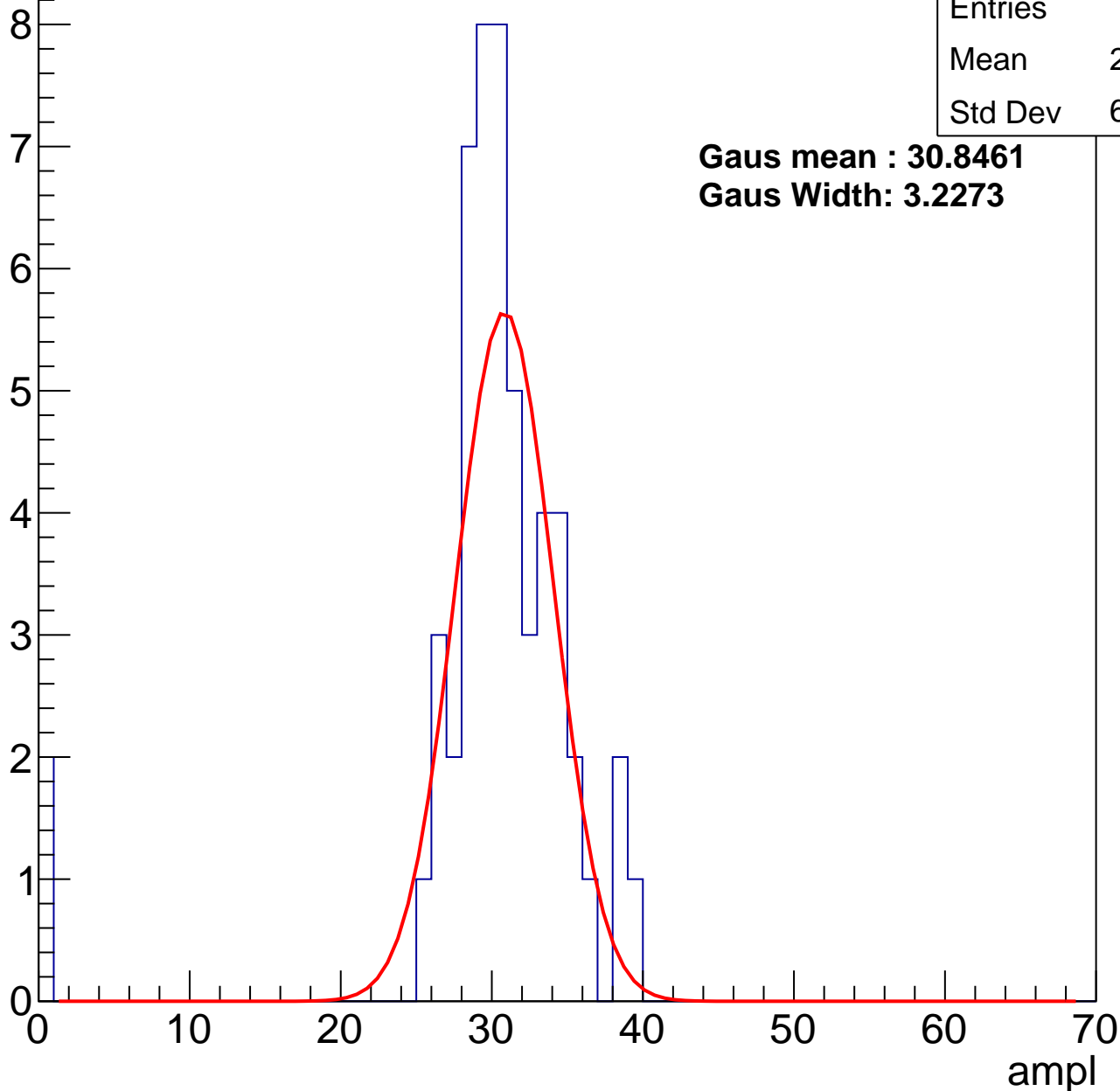
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	29.53
Std Dev	6.626

Gaus mean : 30.8461

Gaus Width: 3.2273



B1L103S, U23-ch71, adc1

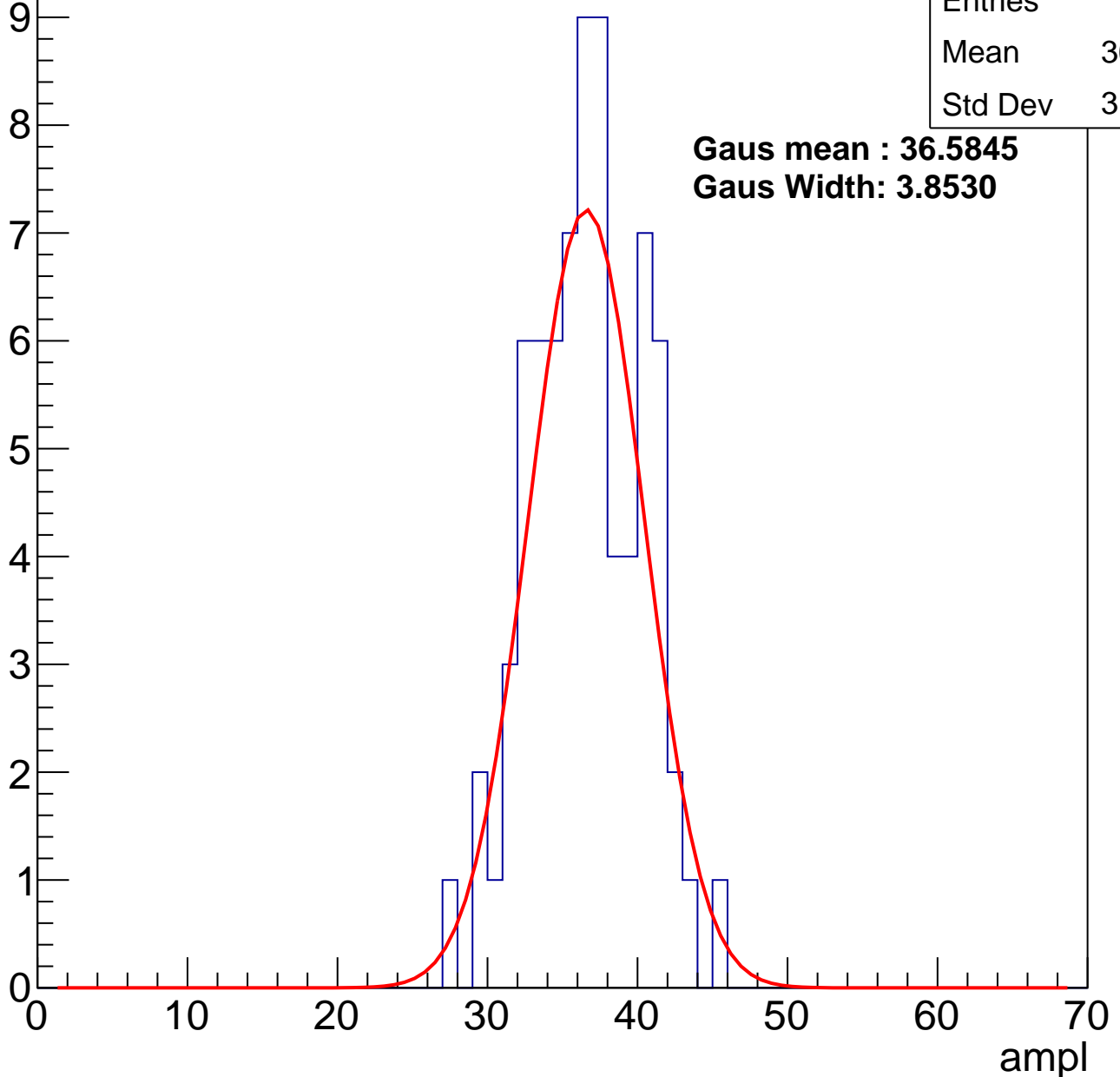
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.13
Std Dev	3.642

Gaus mean : 36.5845

Gaus Width: 3.8530



B1L103S, U23-ch71, adc2

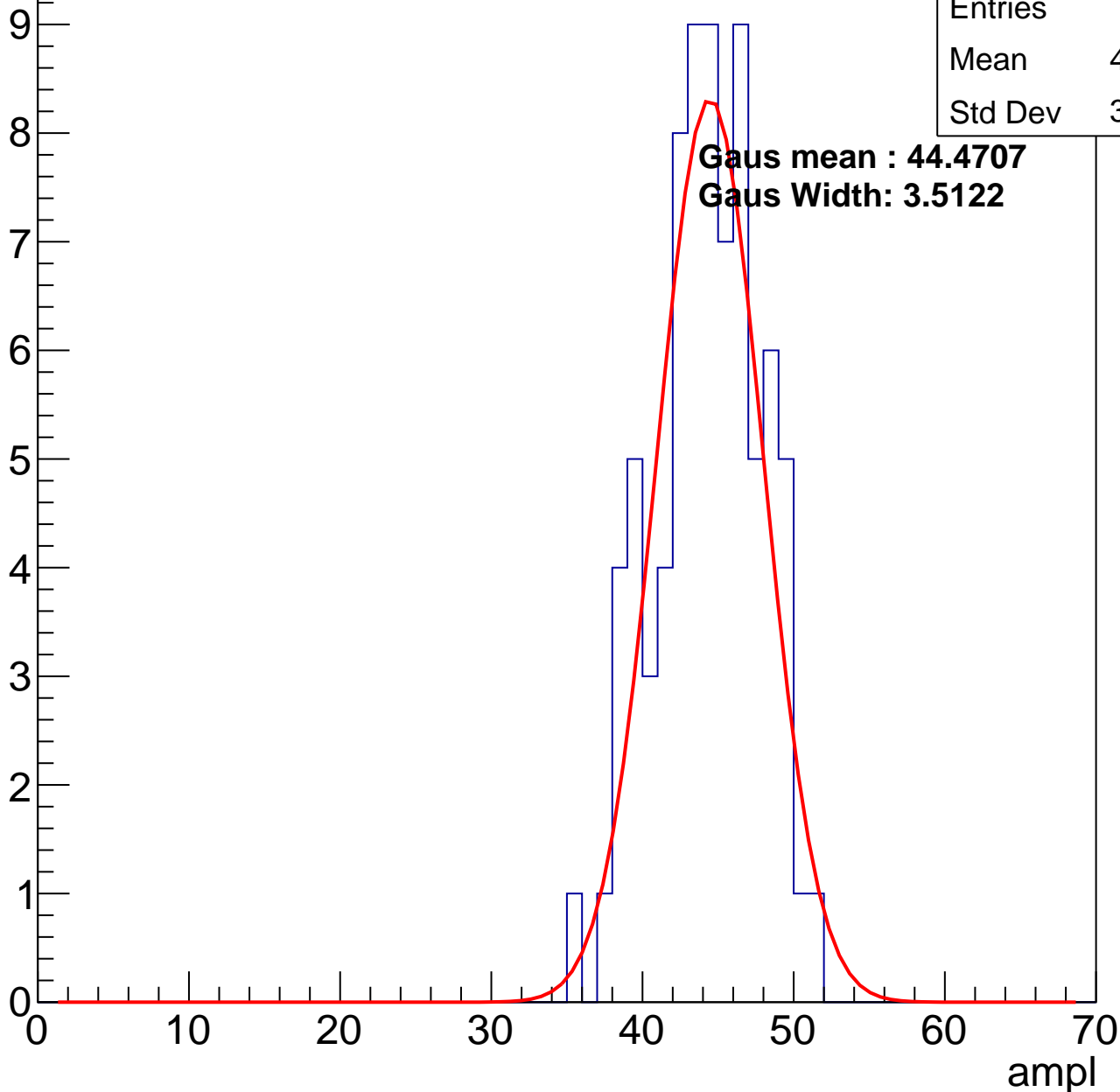
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	43.85
Std Dev	3.427

Gaus mean : 44.4707

Gaus Width: 3.5122

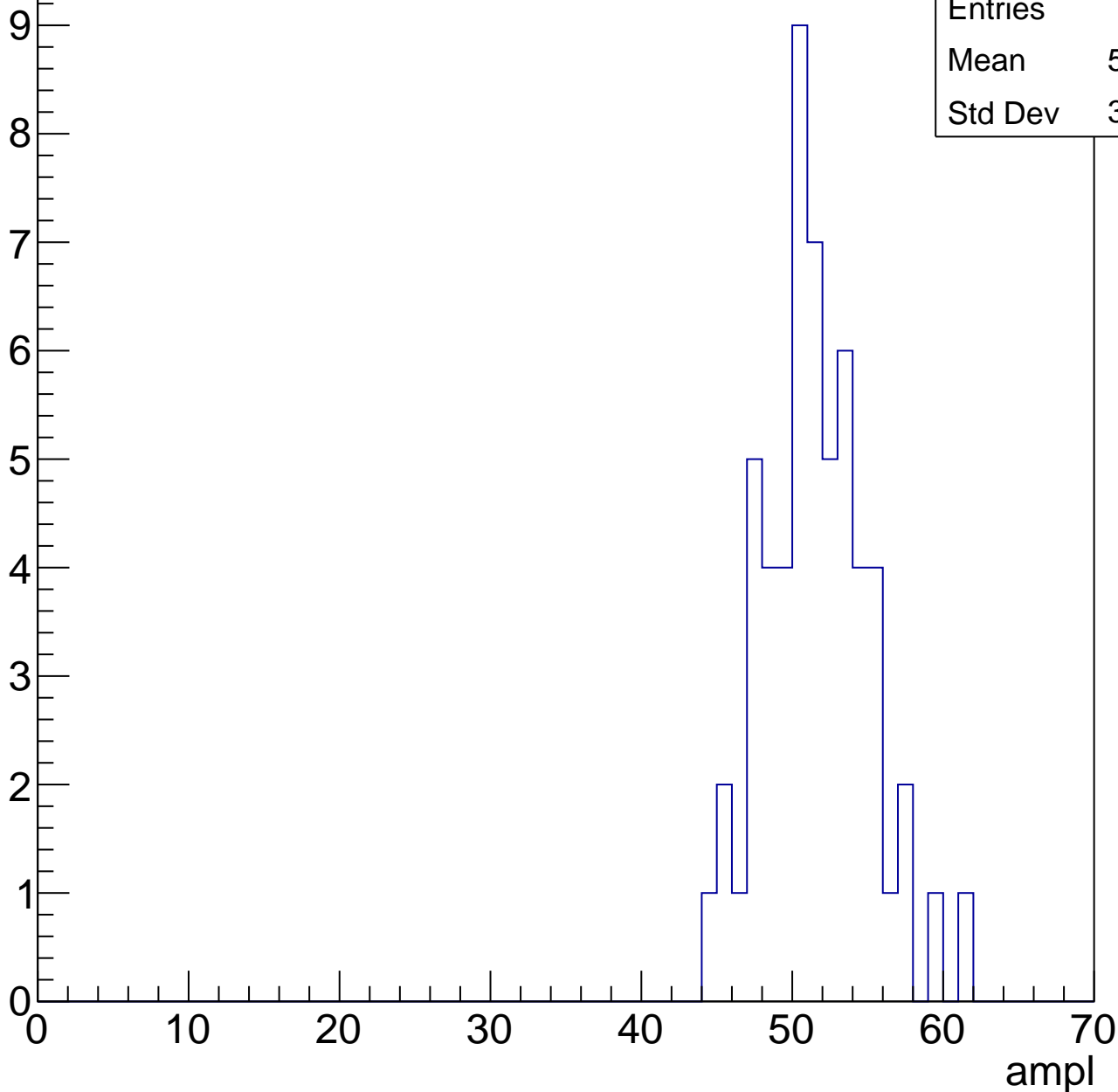


B1L103S, U23-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	51.12
Std Dev	3.444

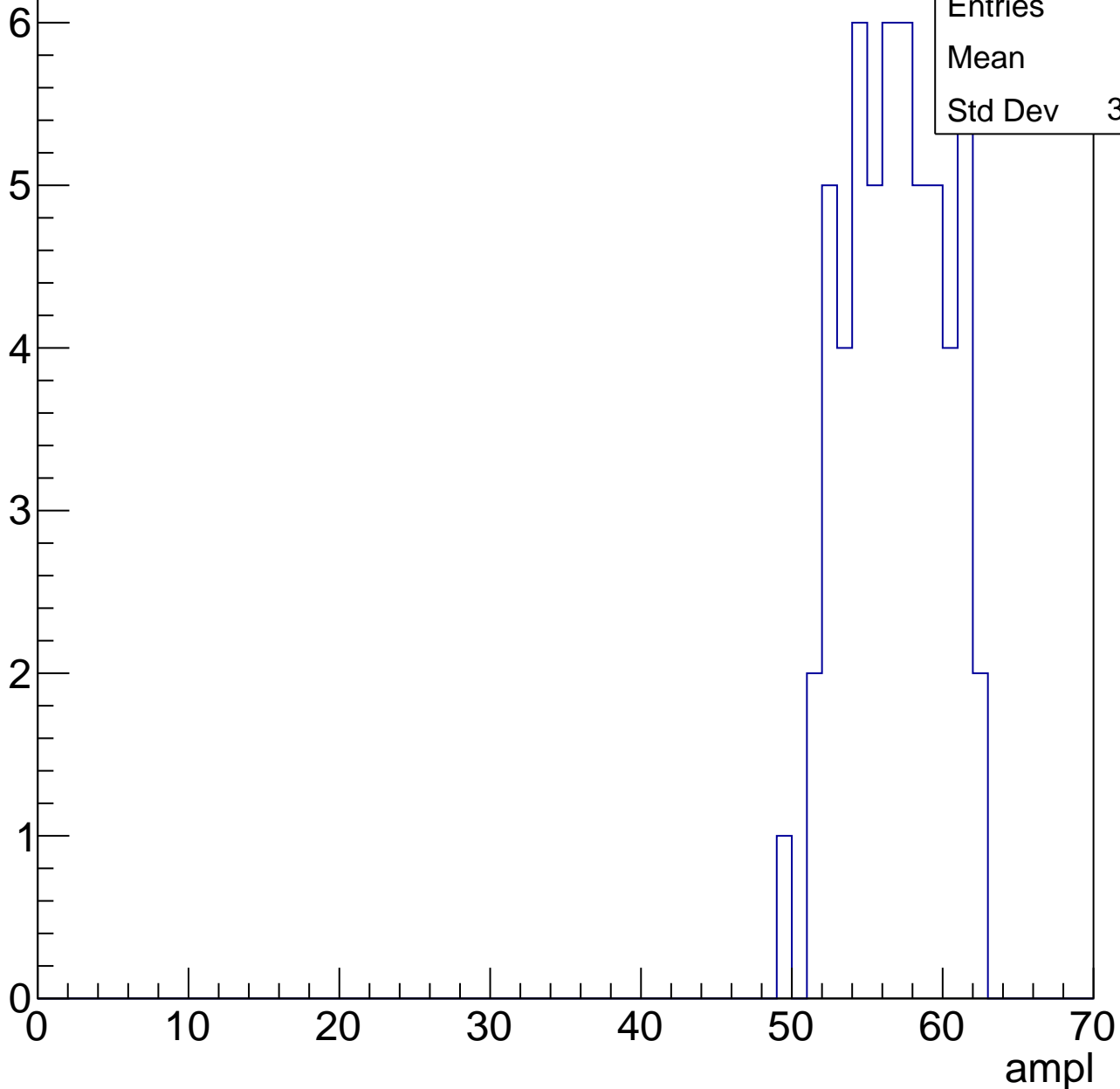


B1L103S, U23-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	56.4
Std Dev	3.222

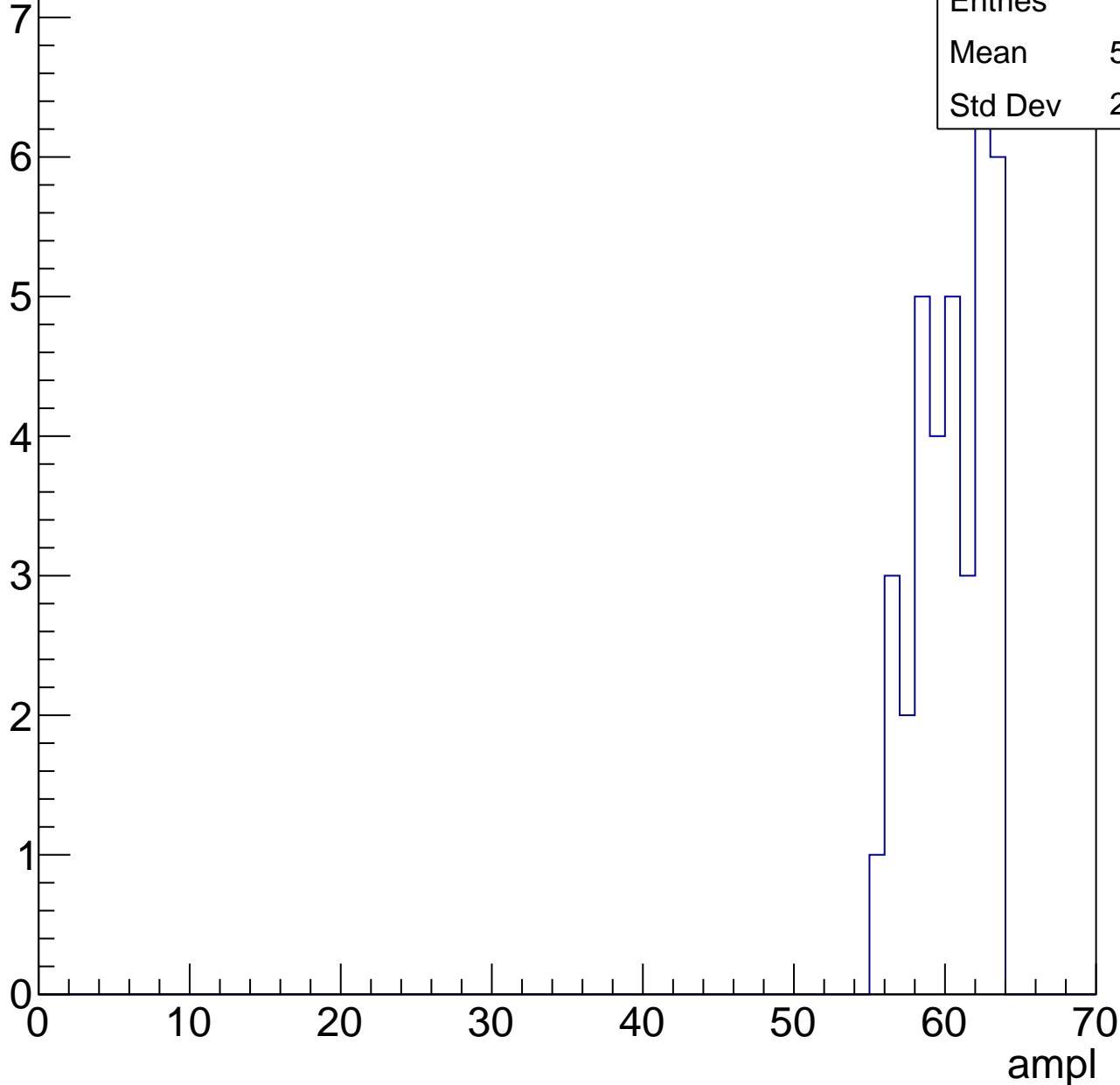


B1L103S, U23-ch71, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

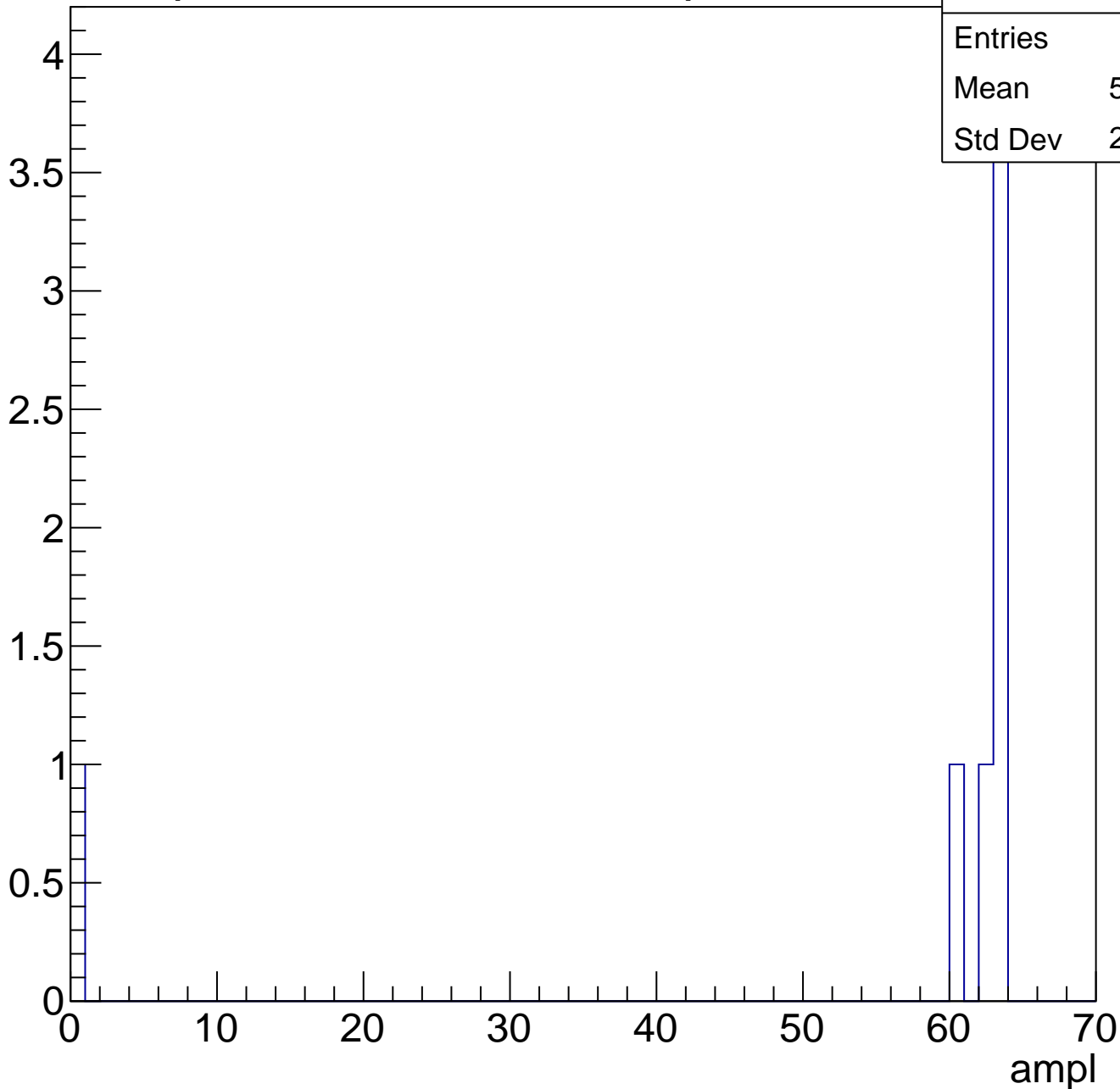
Entries	36
Mean	59.94
Std Dev	2.356



B1L103S, U23-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch72, adc0

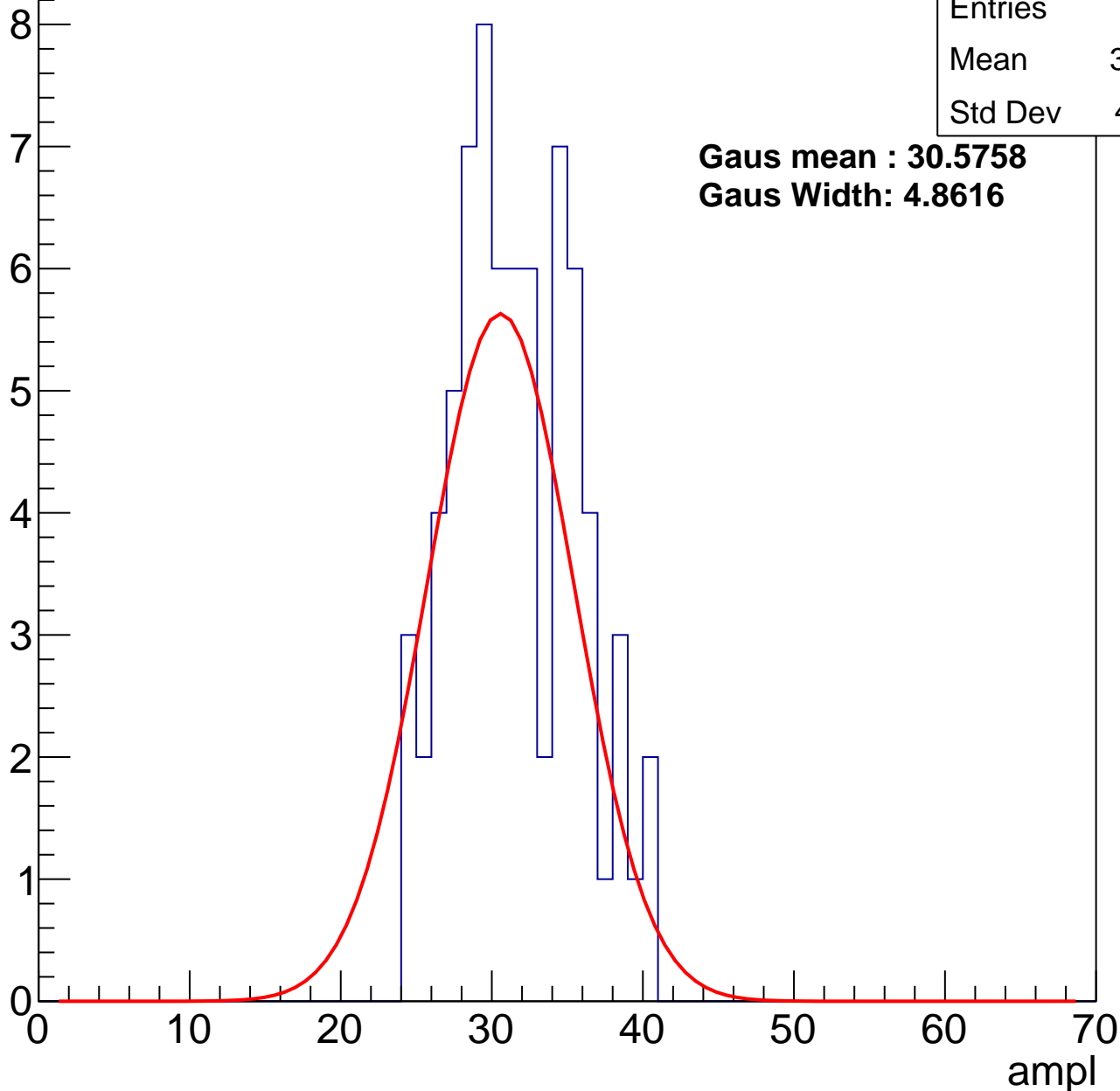
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	31.16
Std Dev	4.021

Gaus mean : 30.5758

Gaus Width: 4.8616



B1L103S, U23-ch72, adc1

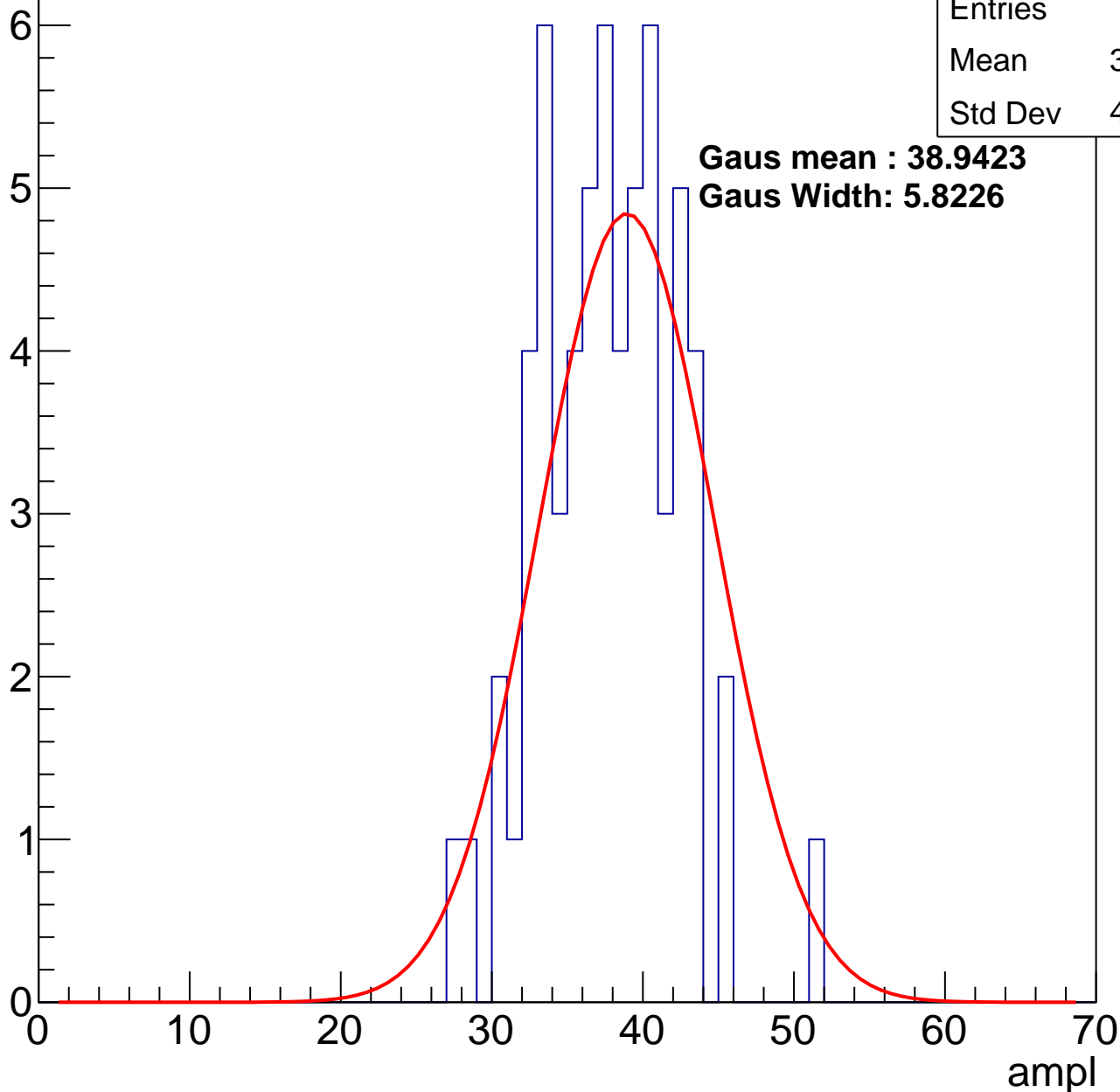
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.29
Std Dev	4.495

Gaus mean : 38.9423

Gaus Width: 5.8226



B1L103S, U23-ch72, adc2

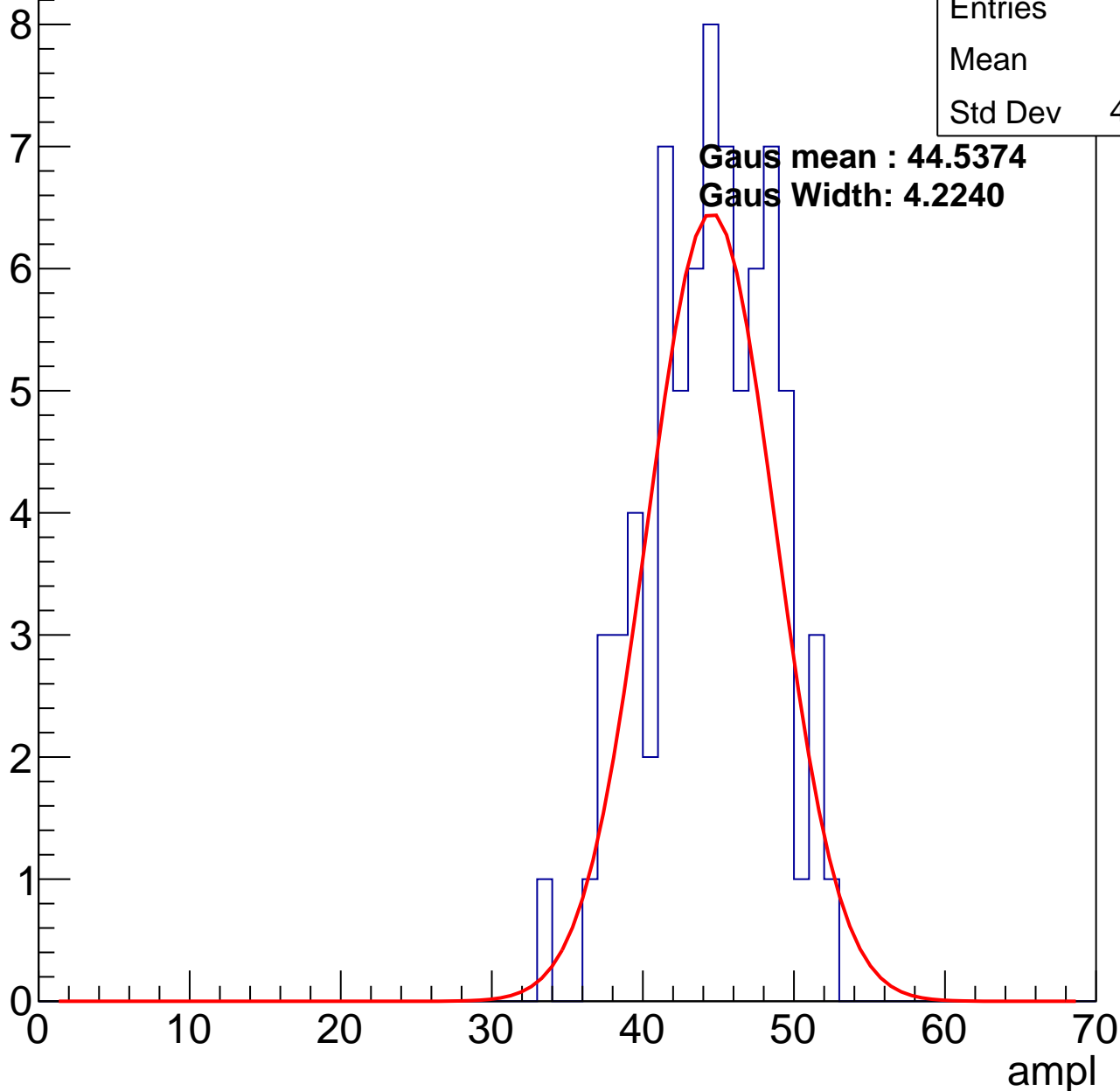
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	44
Std Dev	4.043

Gaus mean : 44.5374

Gaus Width: 4.2240

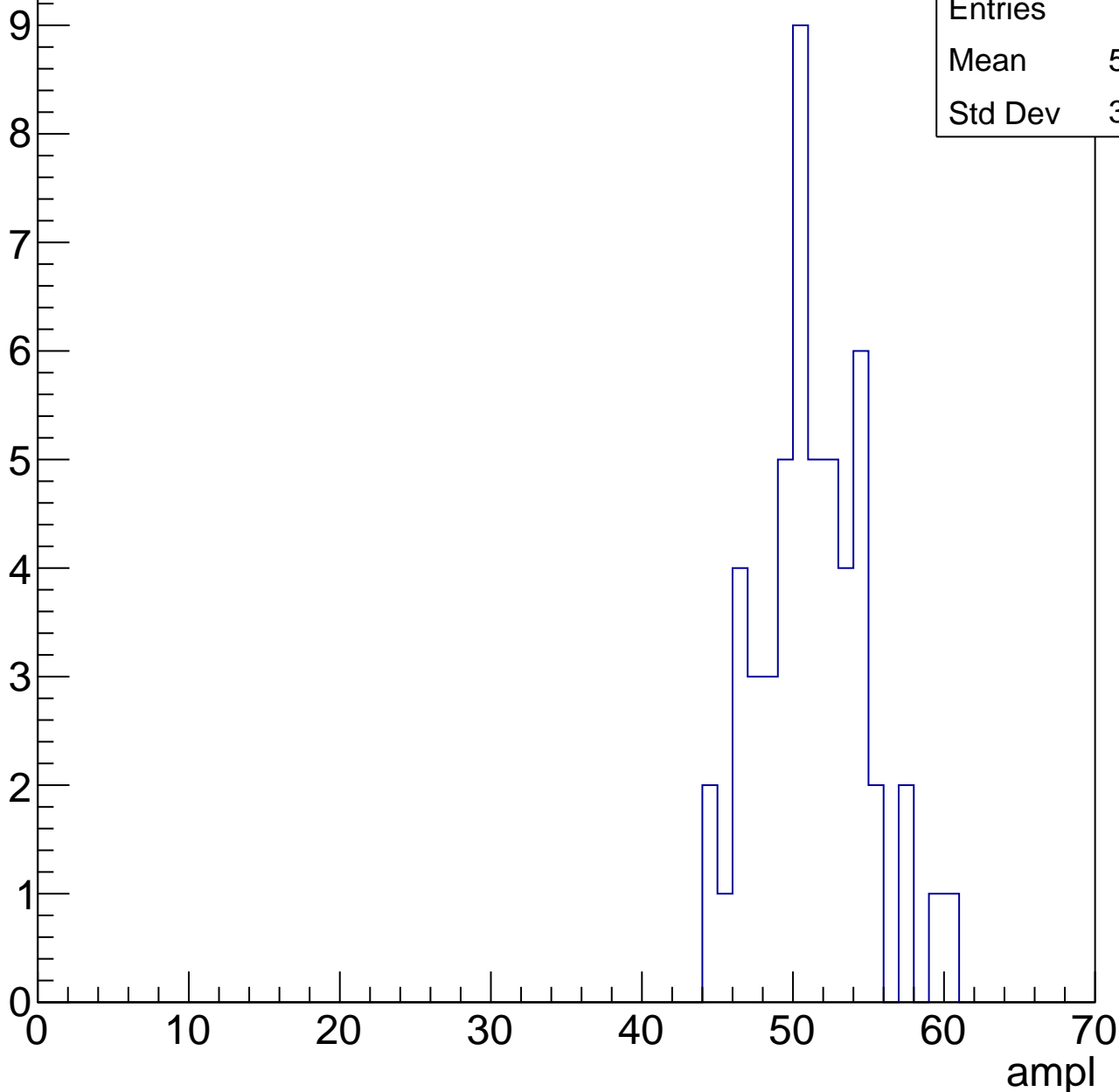


B1L103S, U23-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	50.77
Std Dev	3.527

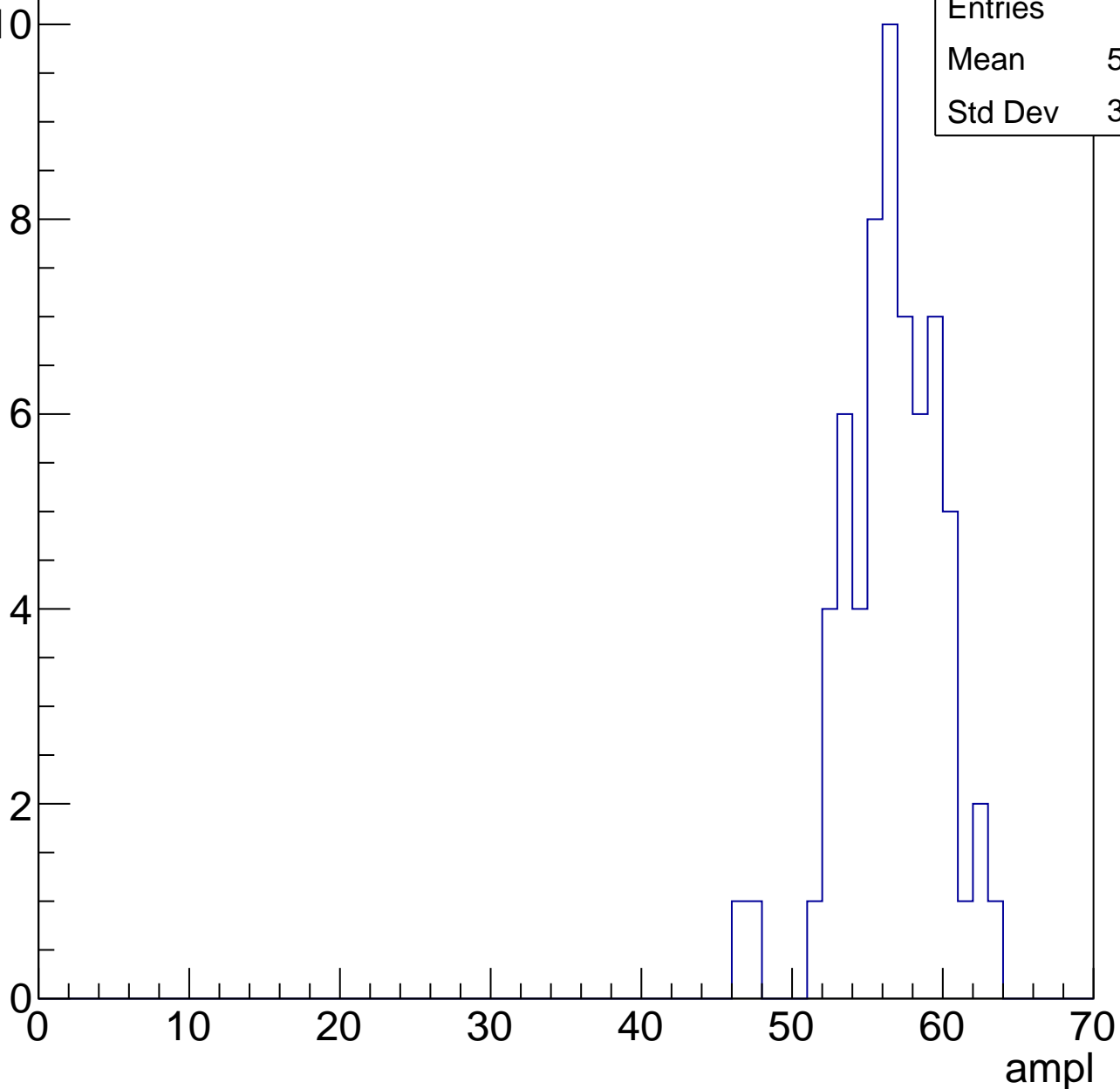


B1L103S, U23-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	56.16
Std Dev	3.227

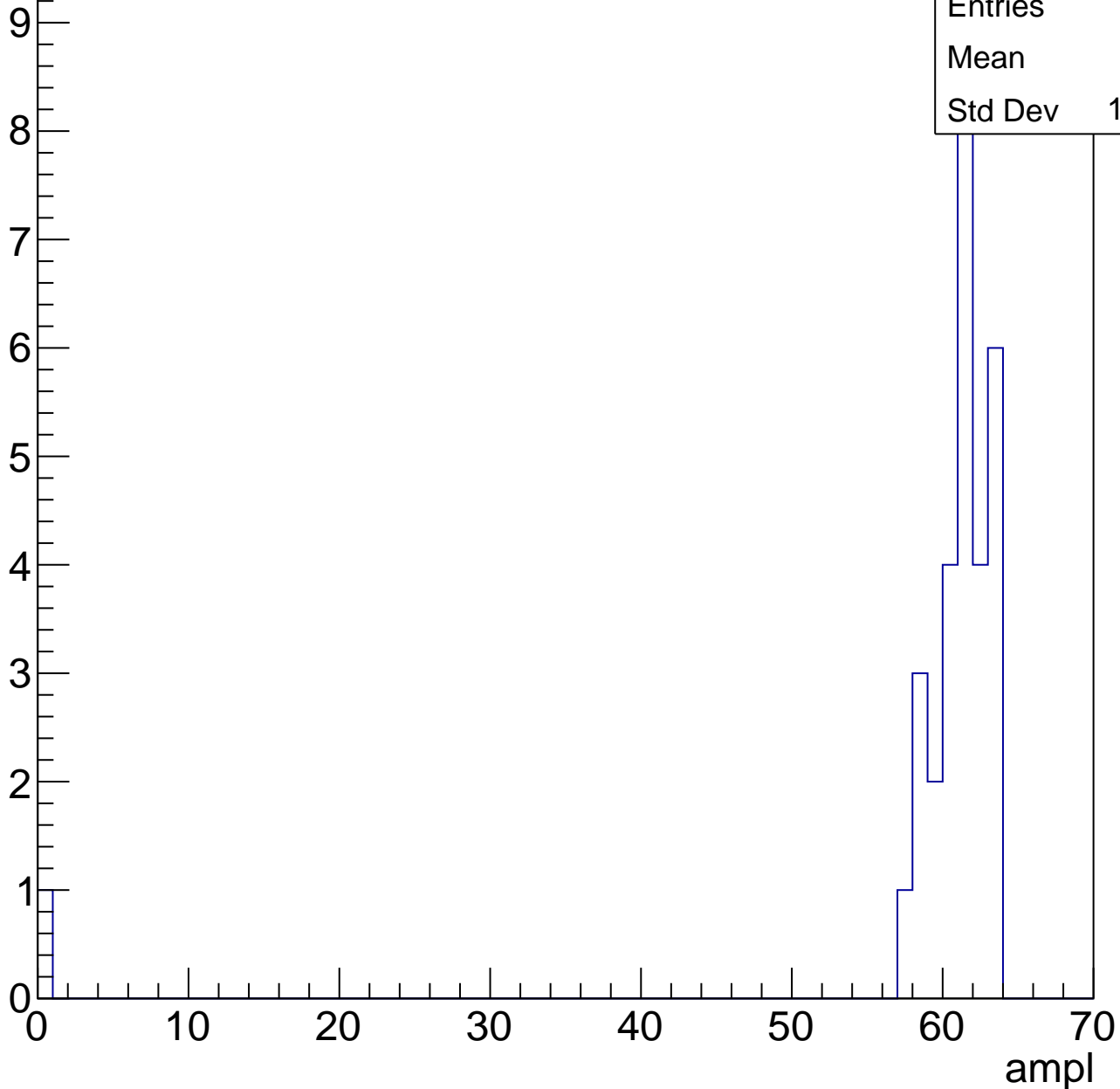


B1L103S, U23-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

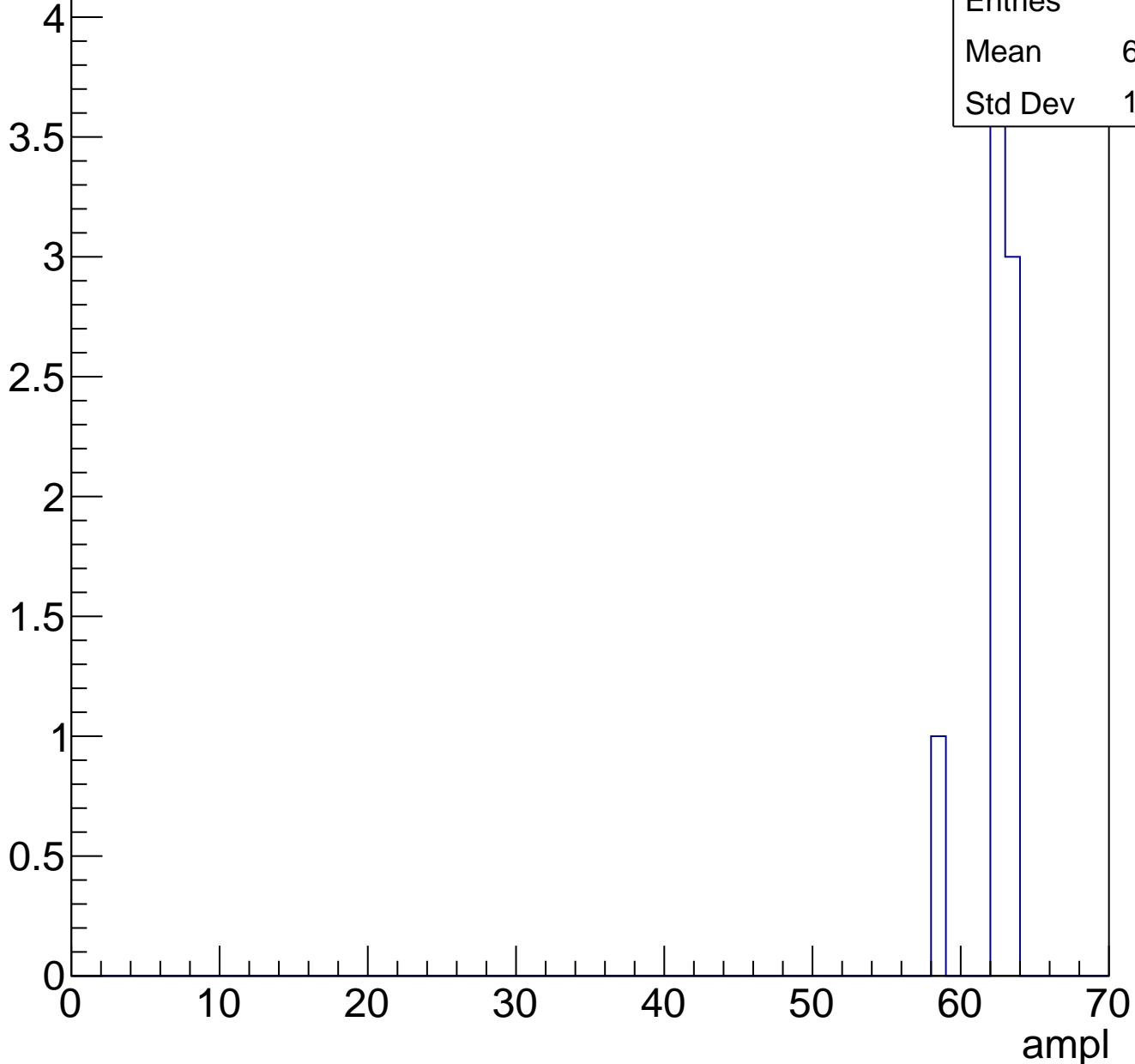
Entries	30
Mean	58.8
Std Dev	11.04



B1L103S, U23-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch73, adc0

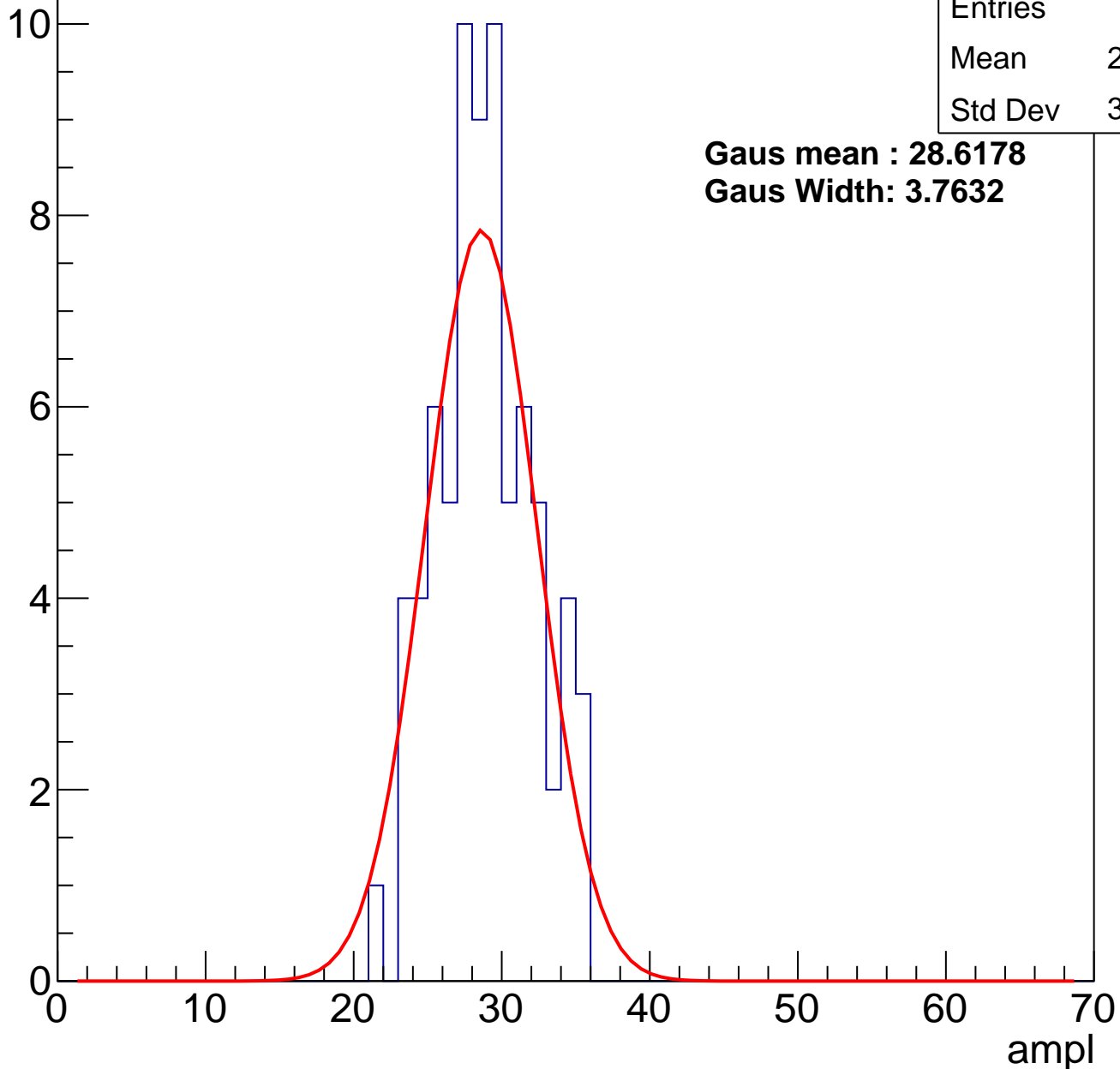
calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	28.43
Std Dev	3.264

Gaus mean : 28.6178

Gaus Width: 3.7632

Entry



B1L103S, U23-ch73, adc1

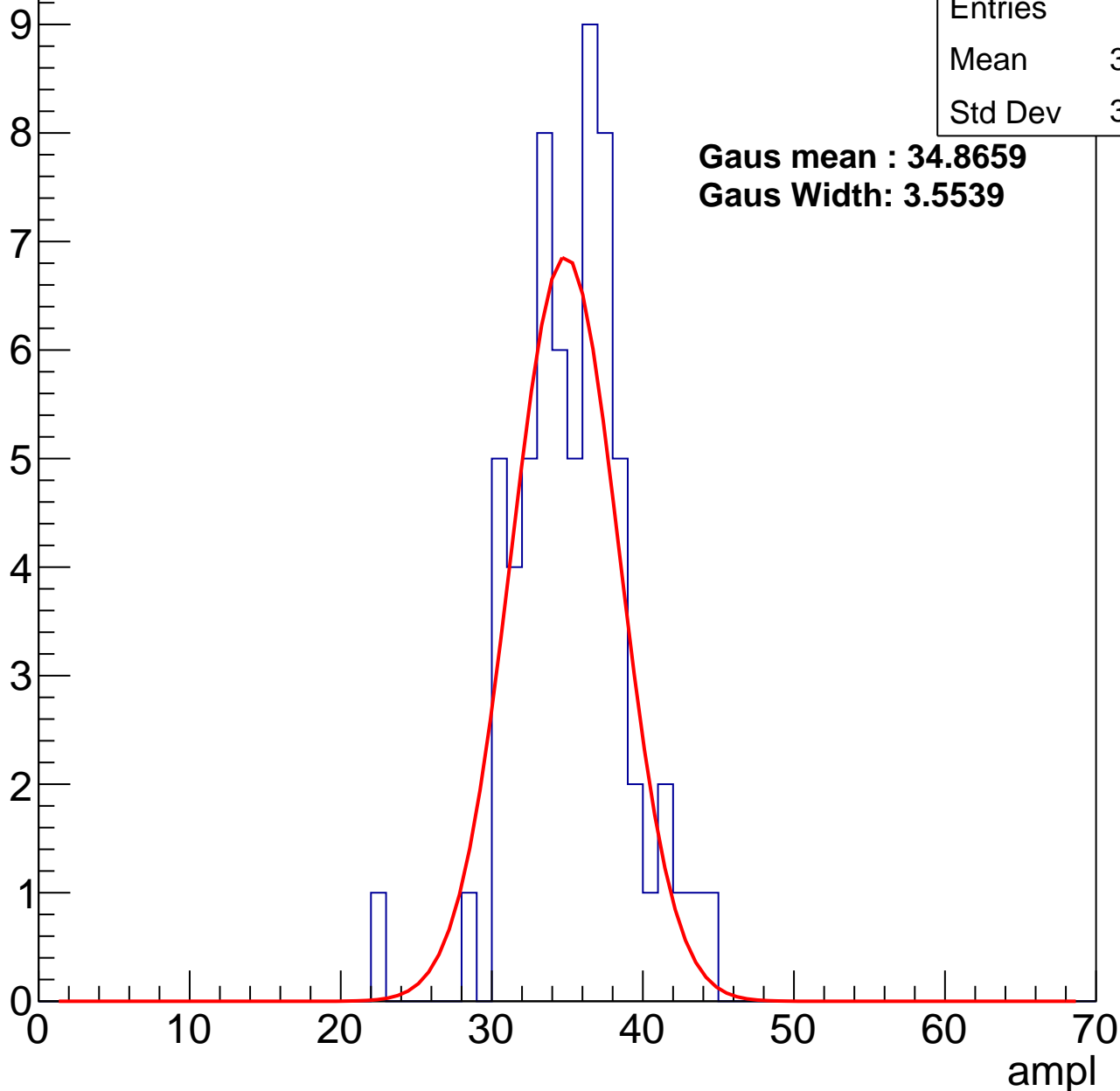
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	34.86
Std Dev	3.716

Gaus mean : 34.8659

Gaus Width: 3.5539



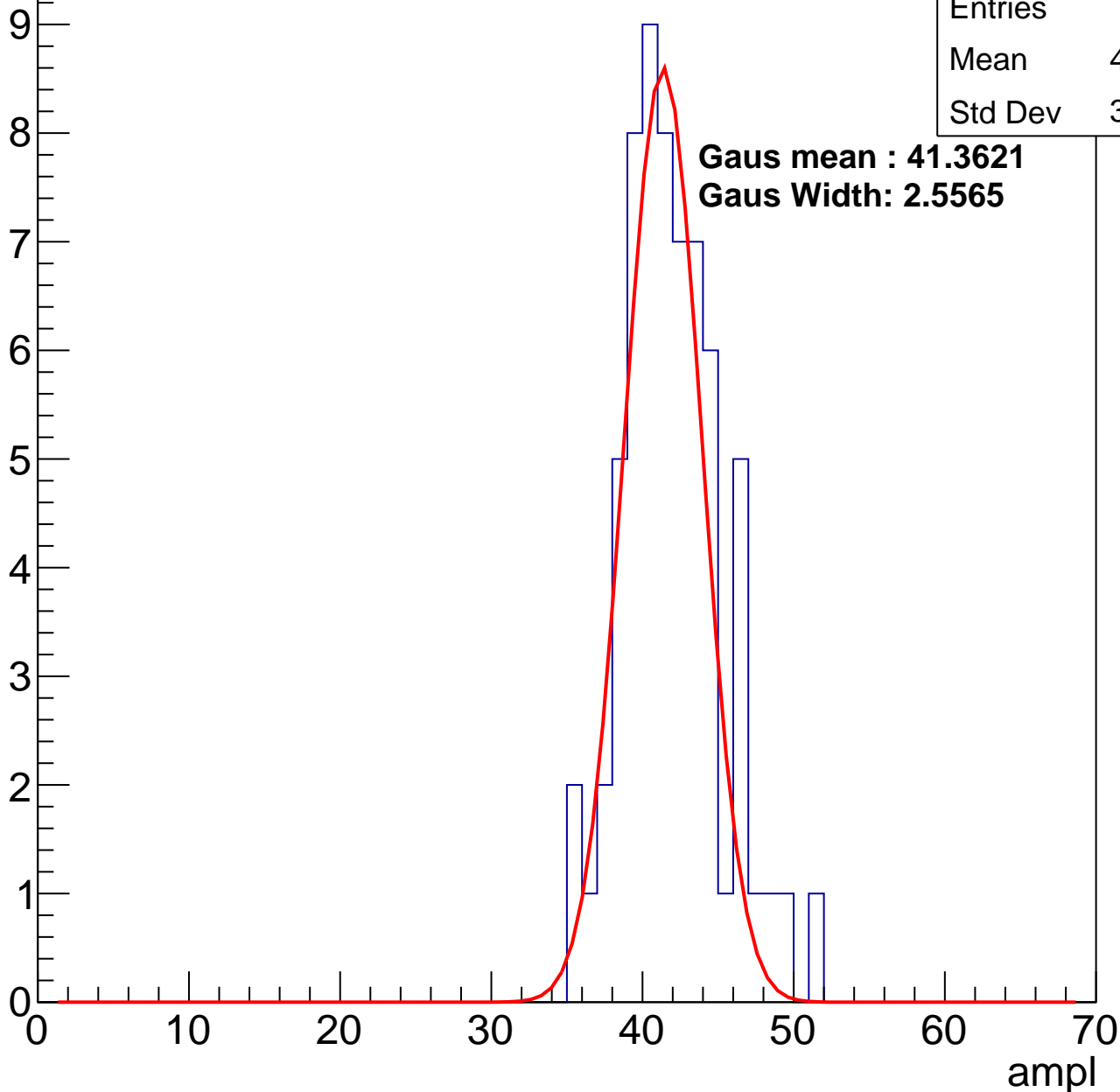
B1L103S, U23-ch73, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.52
Std Dev	3.225

Gaus mean : 41.3621
Gaus Width: 2.5565

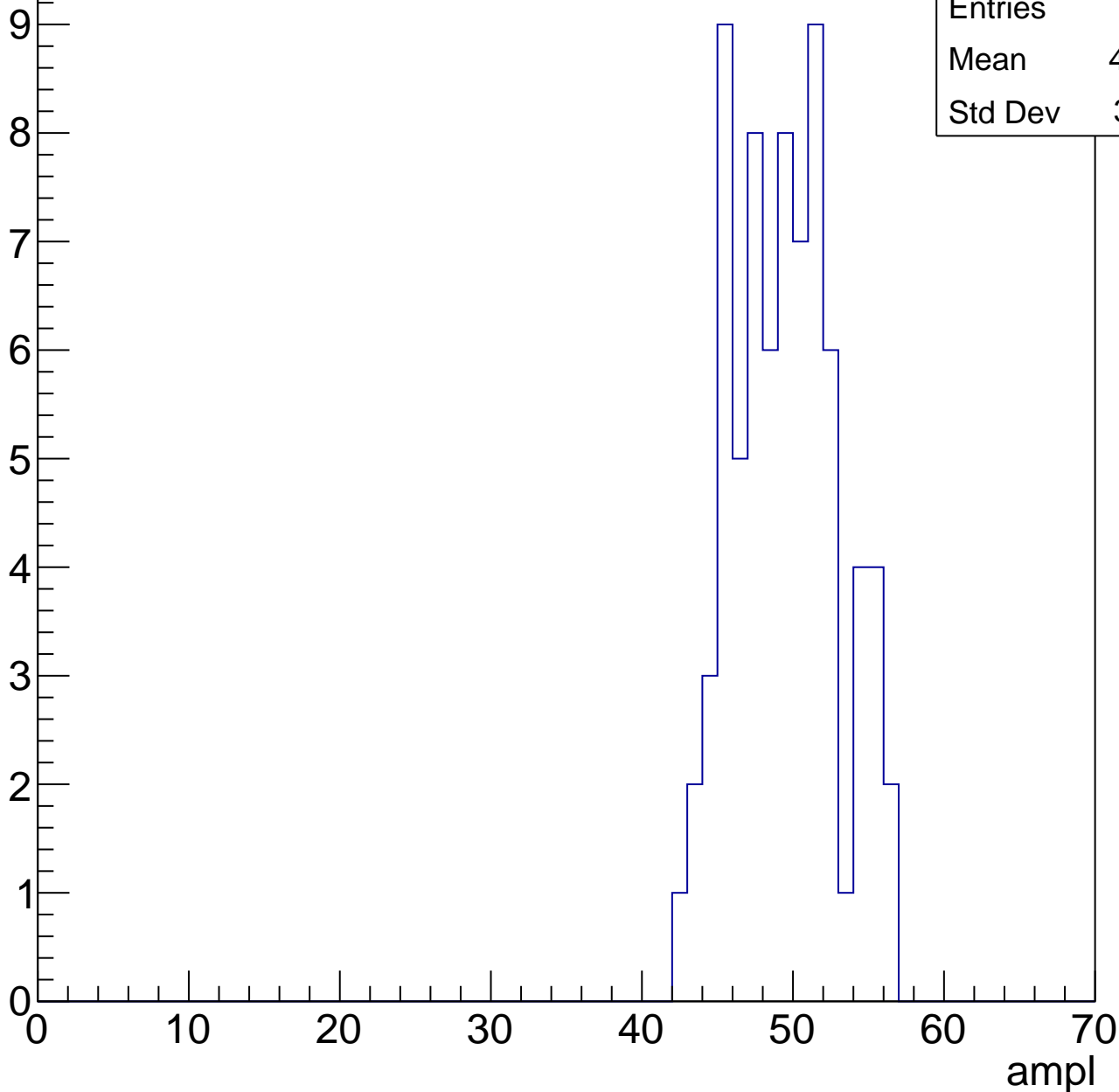


B1L103S, U23-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	48.97
Std Dev	3.421

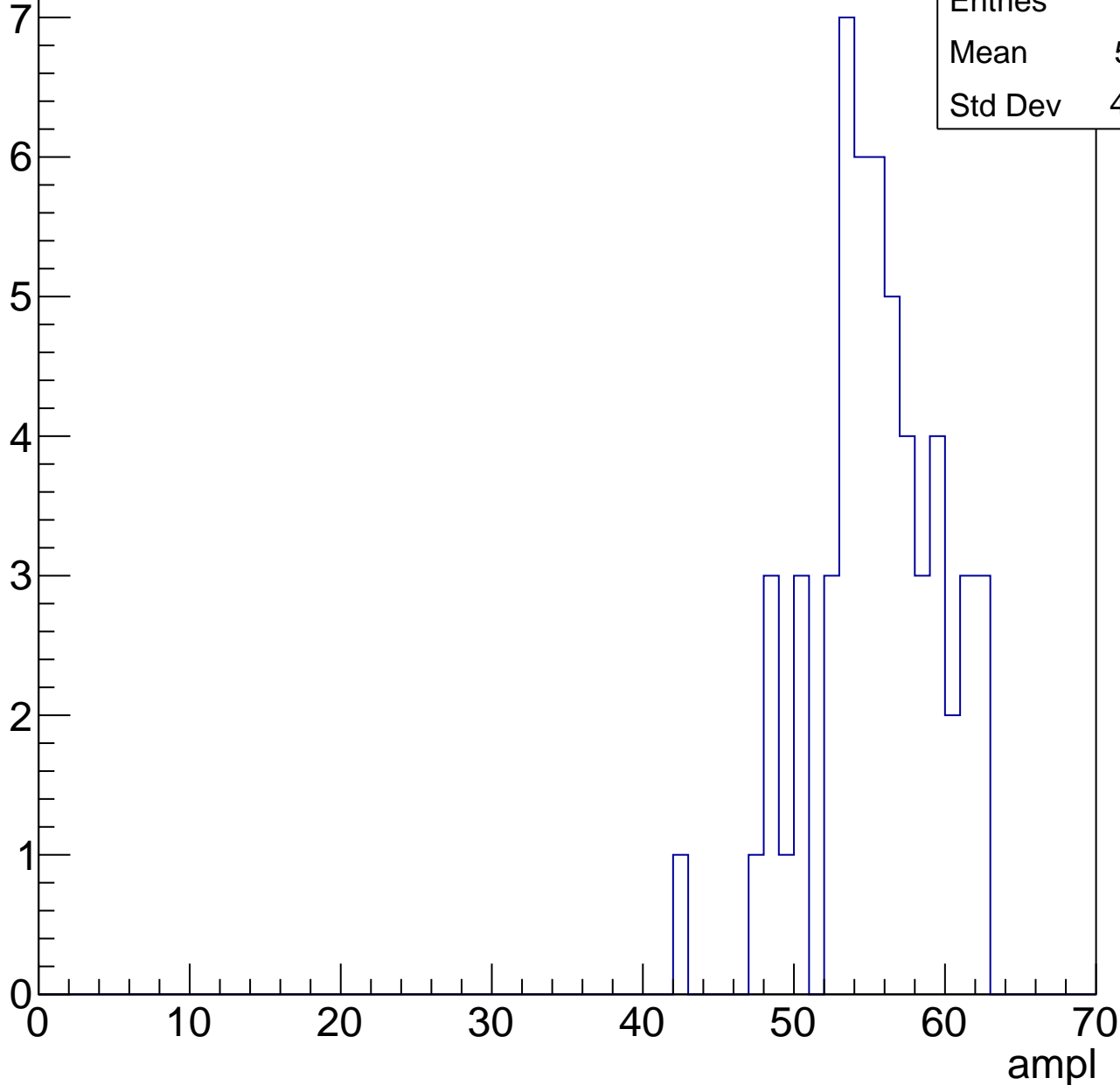


B1L103S, U23-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

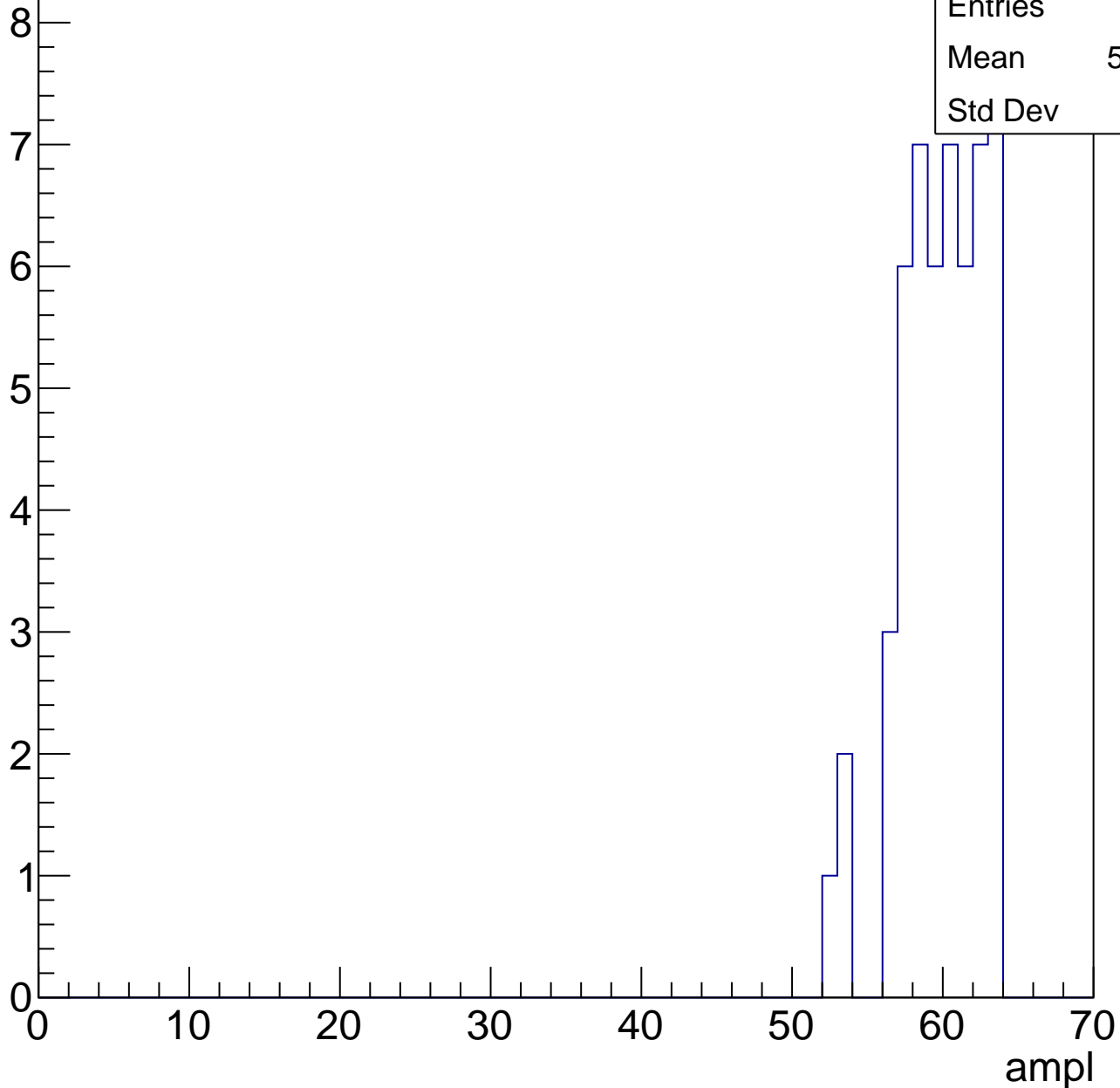
Entries	55
Mean	54.91
Std Dev	4.196



B1L103S, U23-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

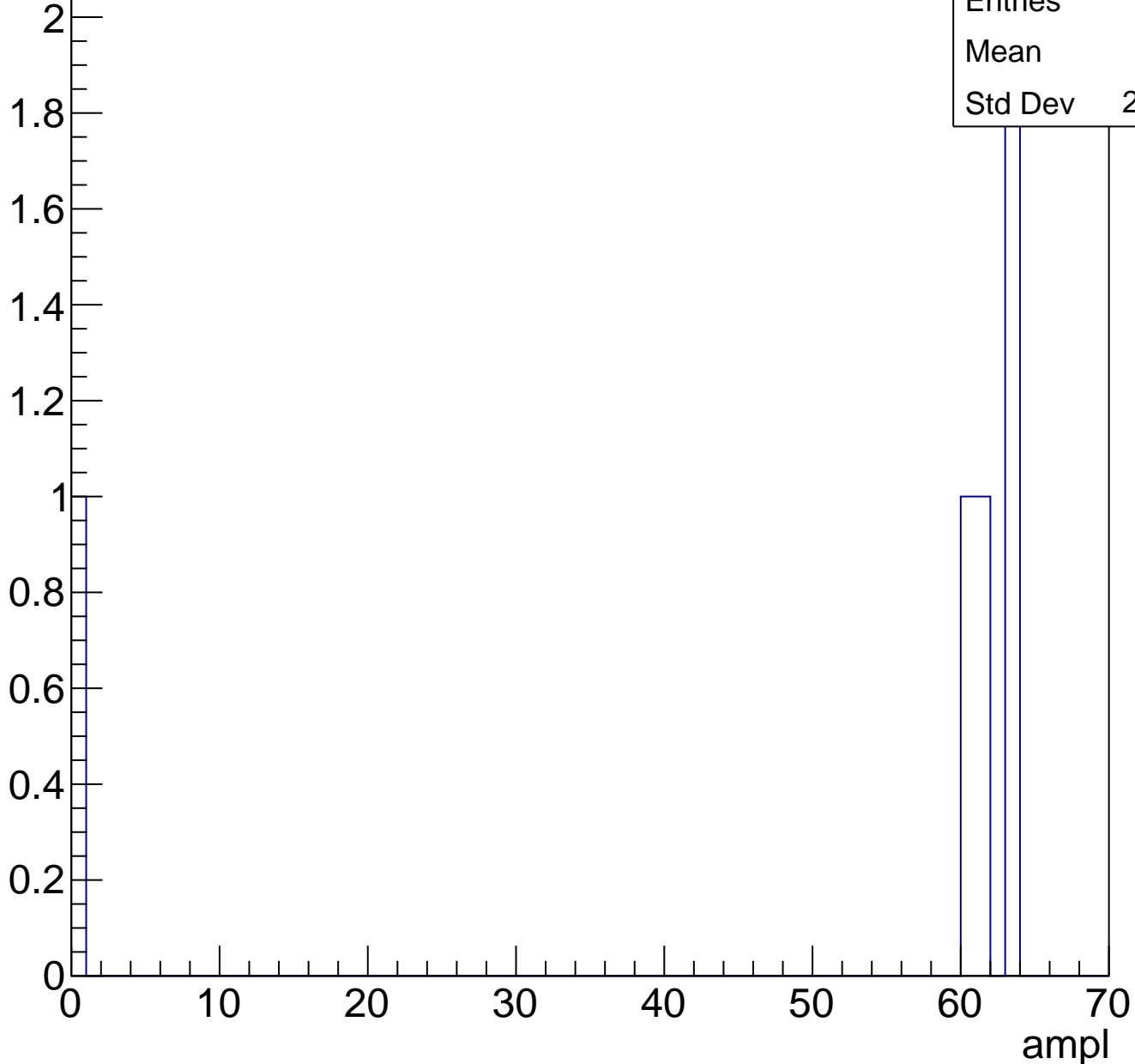
Entry



B1L103S, U23-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch74, adc0

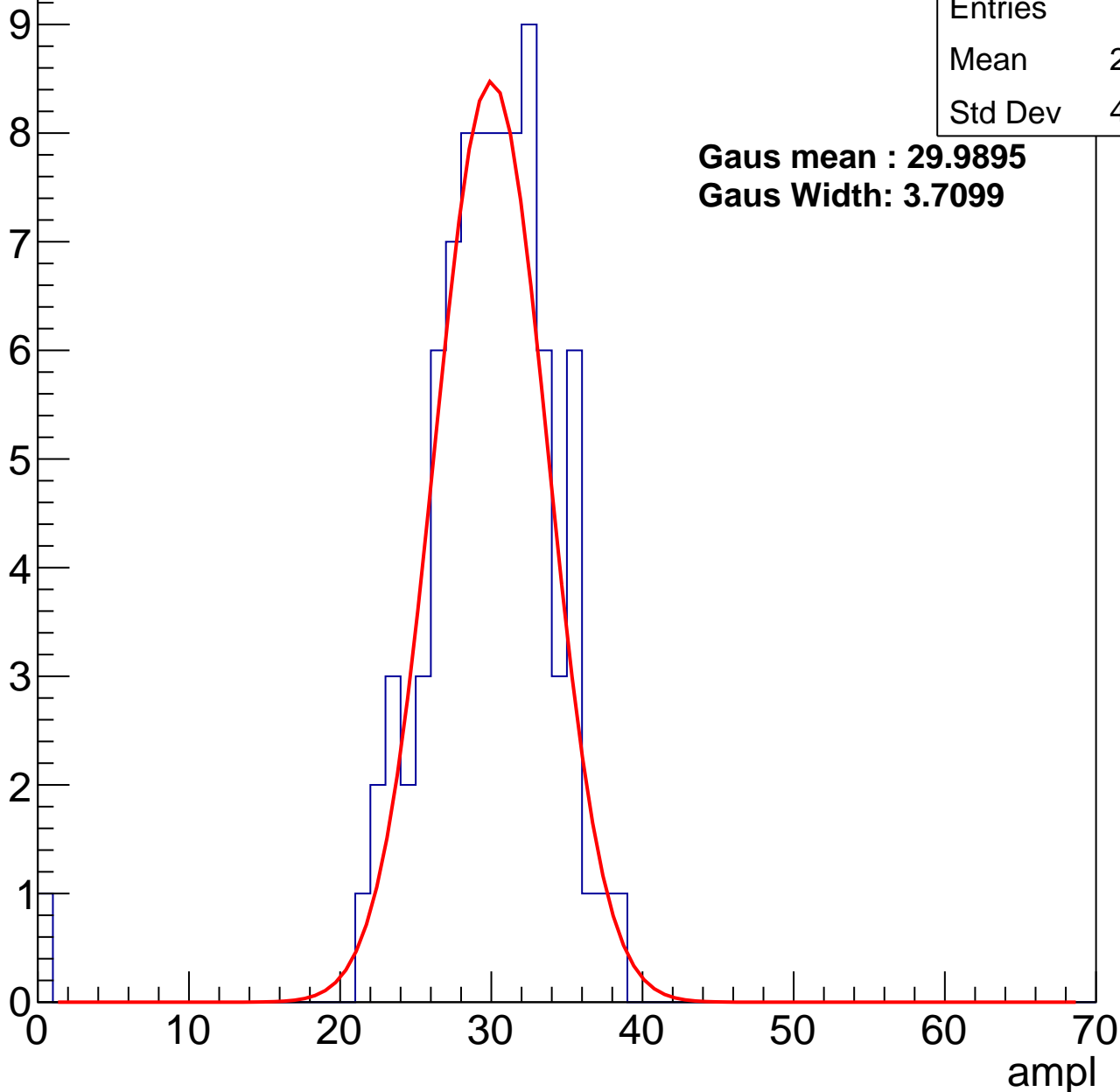
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	29.23
Std Dev	4.875

Gaus mean : 29.9895

Gaus Width: 3.7099



B1L103S, U23-ch74, adc1

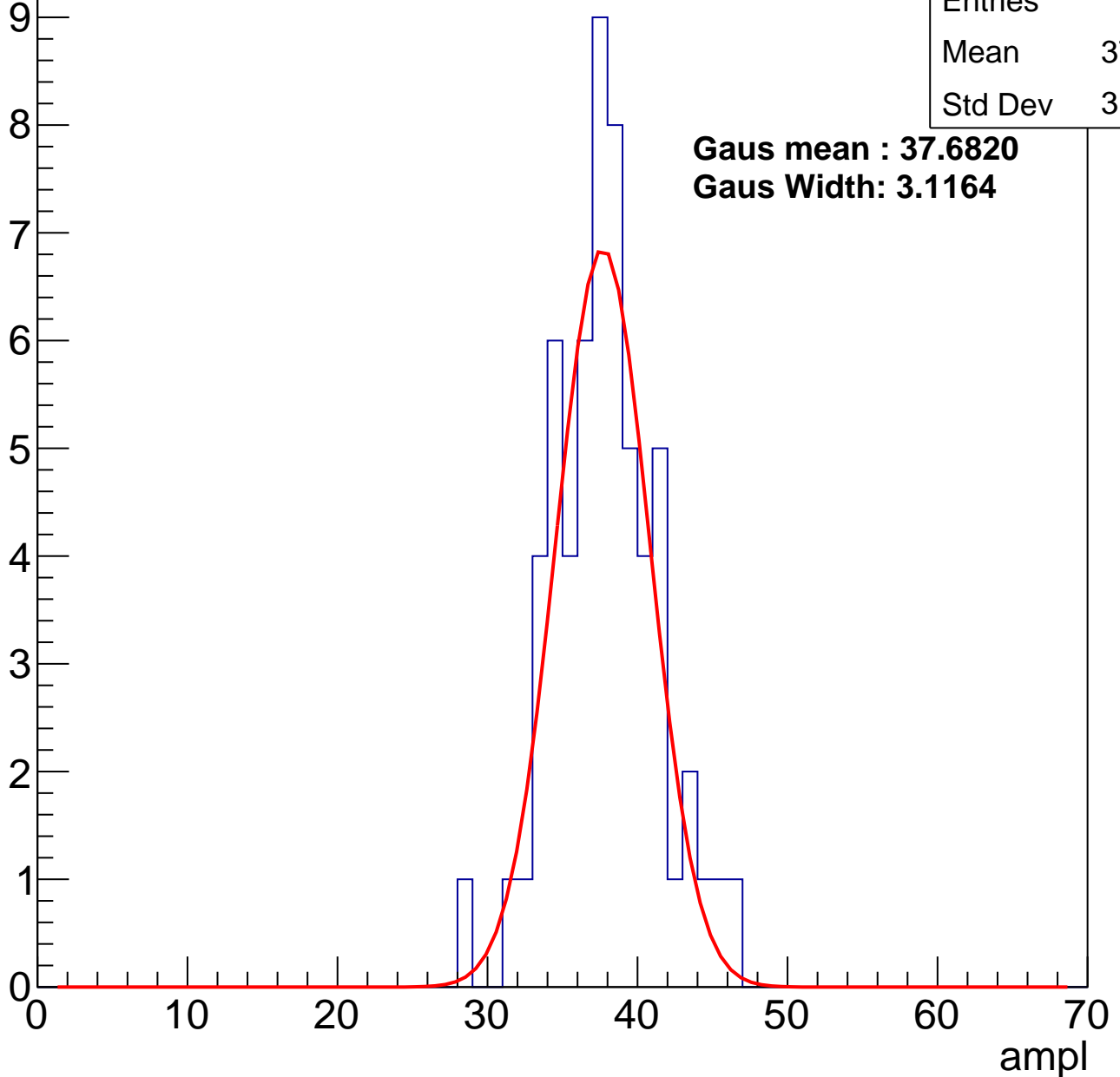
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	37.38
Std Dev	3.436

Gaus mean : 37.6820

Gaus Width: 3.1164



B1L103S, U23-ch74, adc2

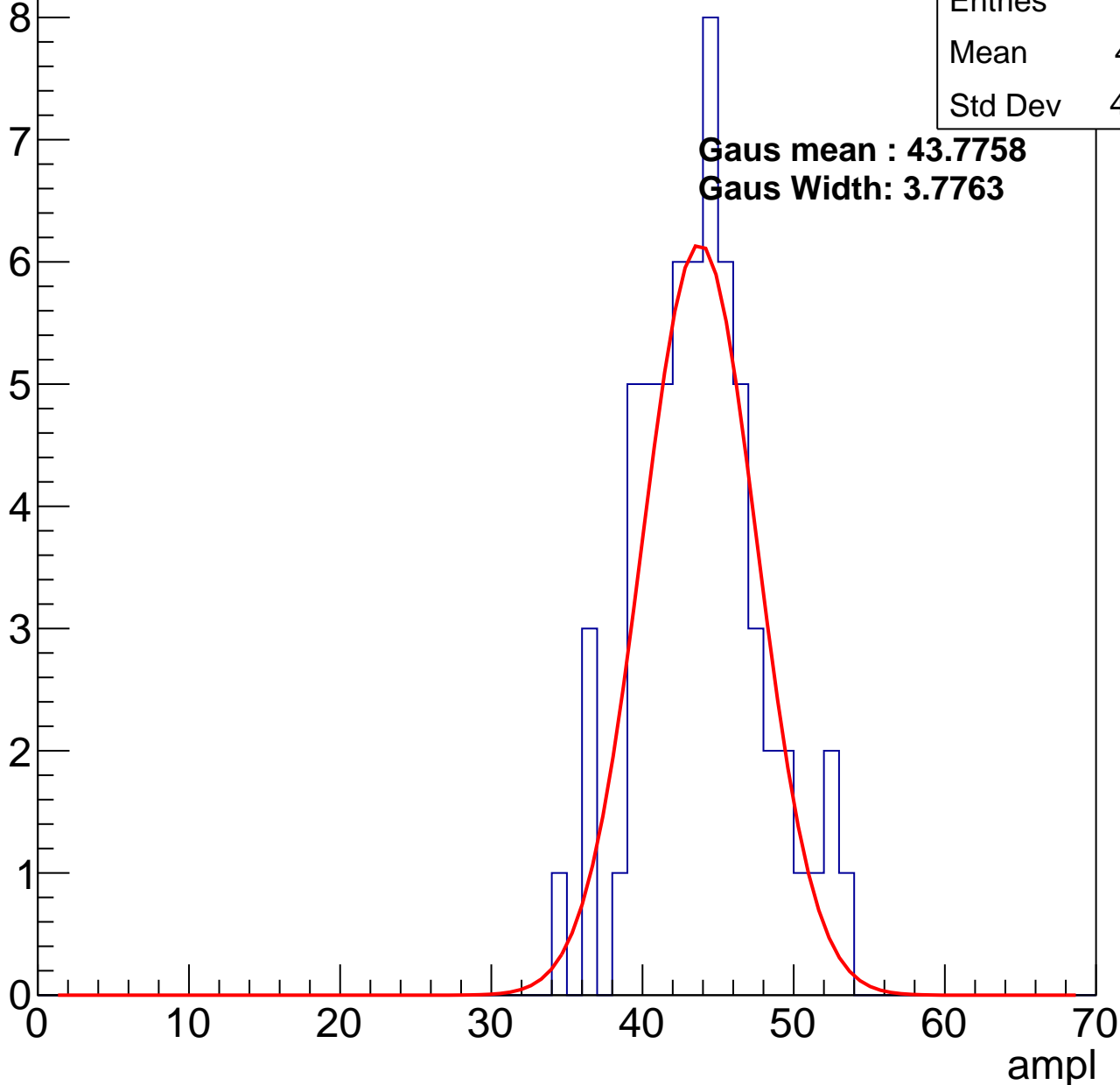
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	43.41
Std Dev	4.014

Gaus mean : 43.7758

Gaus Width: 3.7763

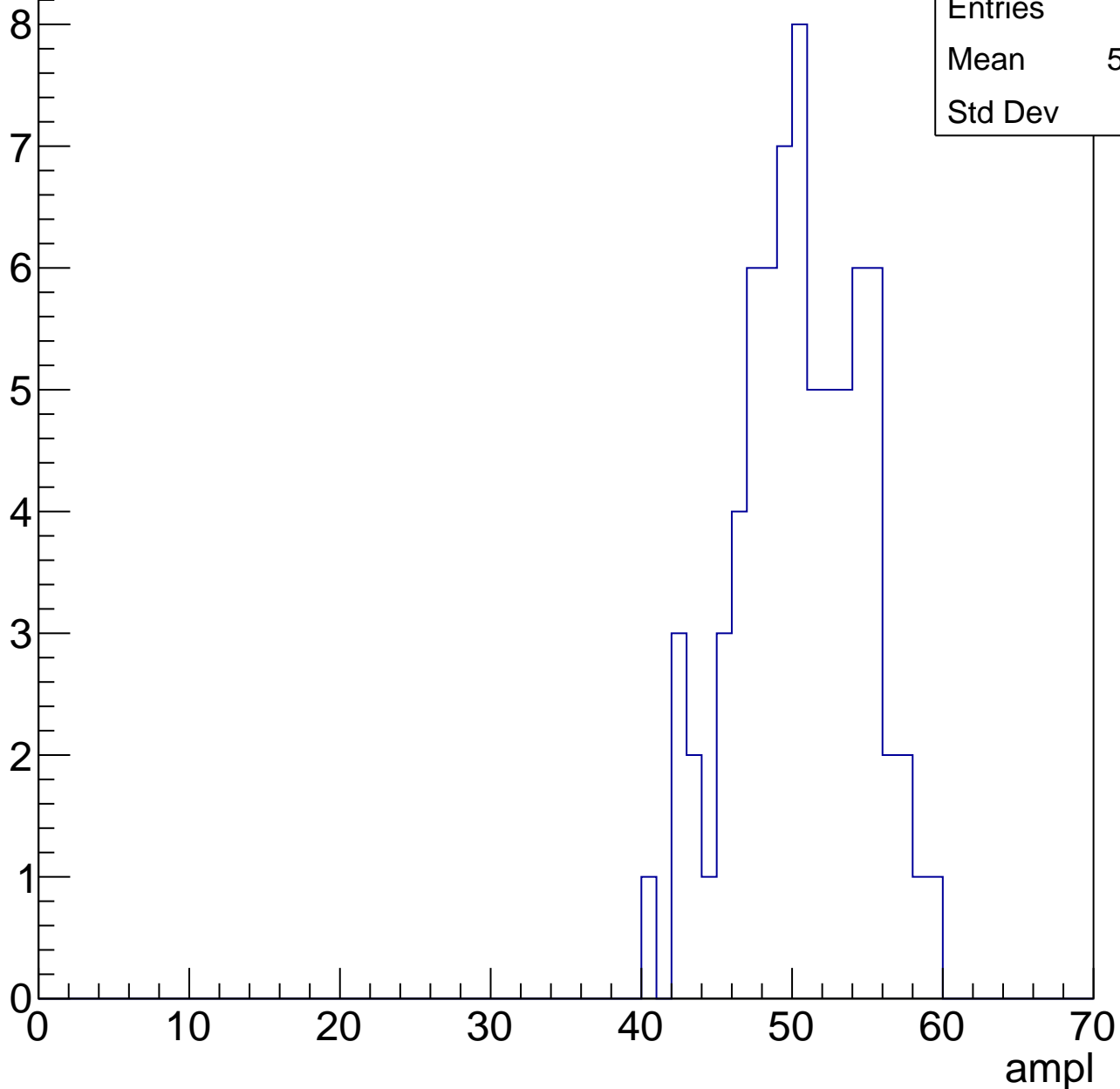


B1L103S, U23-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	50.07
Std Dev	4.16

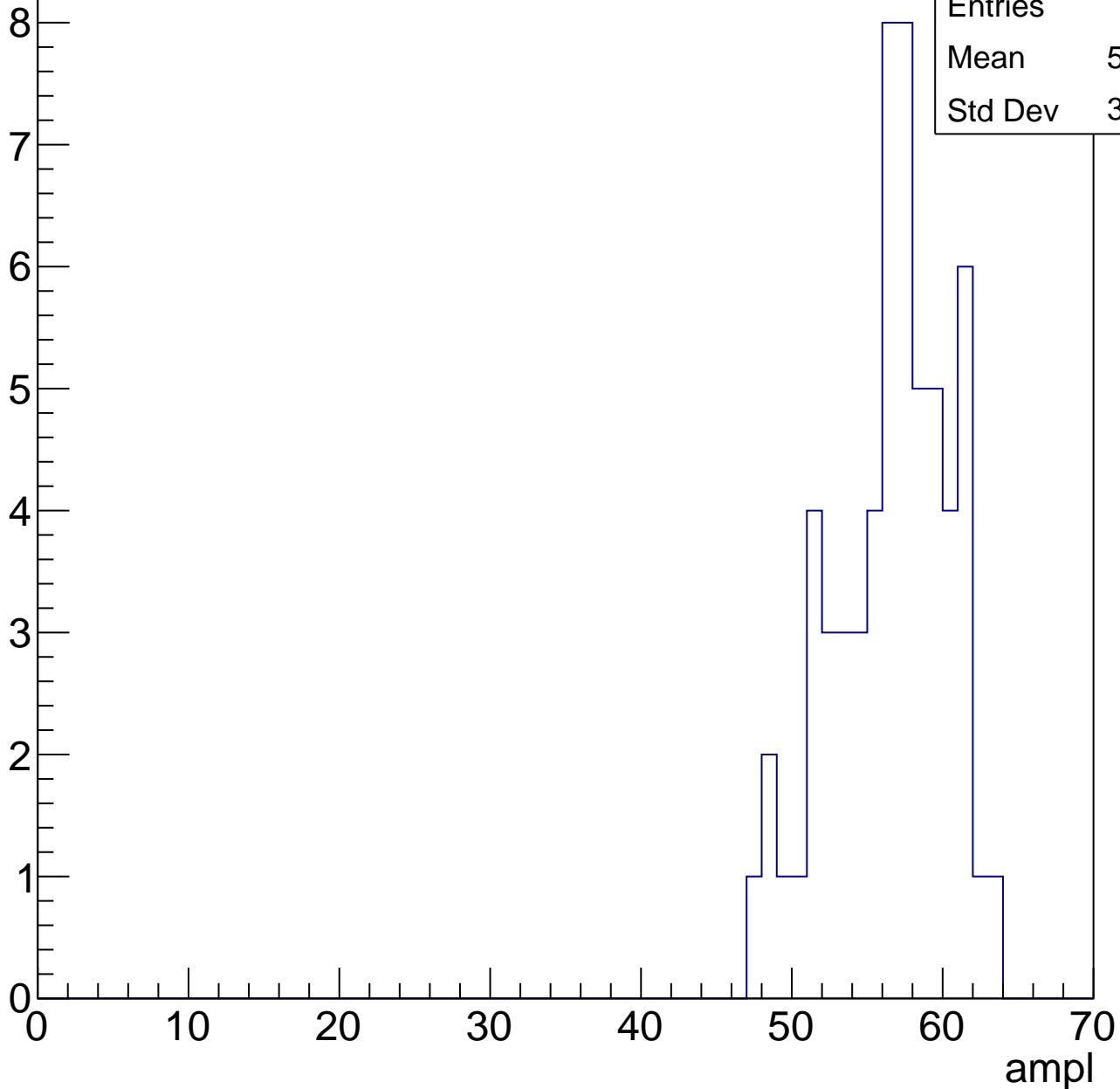


B1L103S, U23-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	56.05
Std Dev	3.779

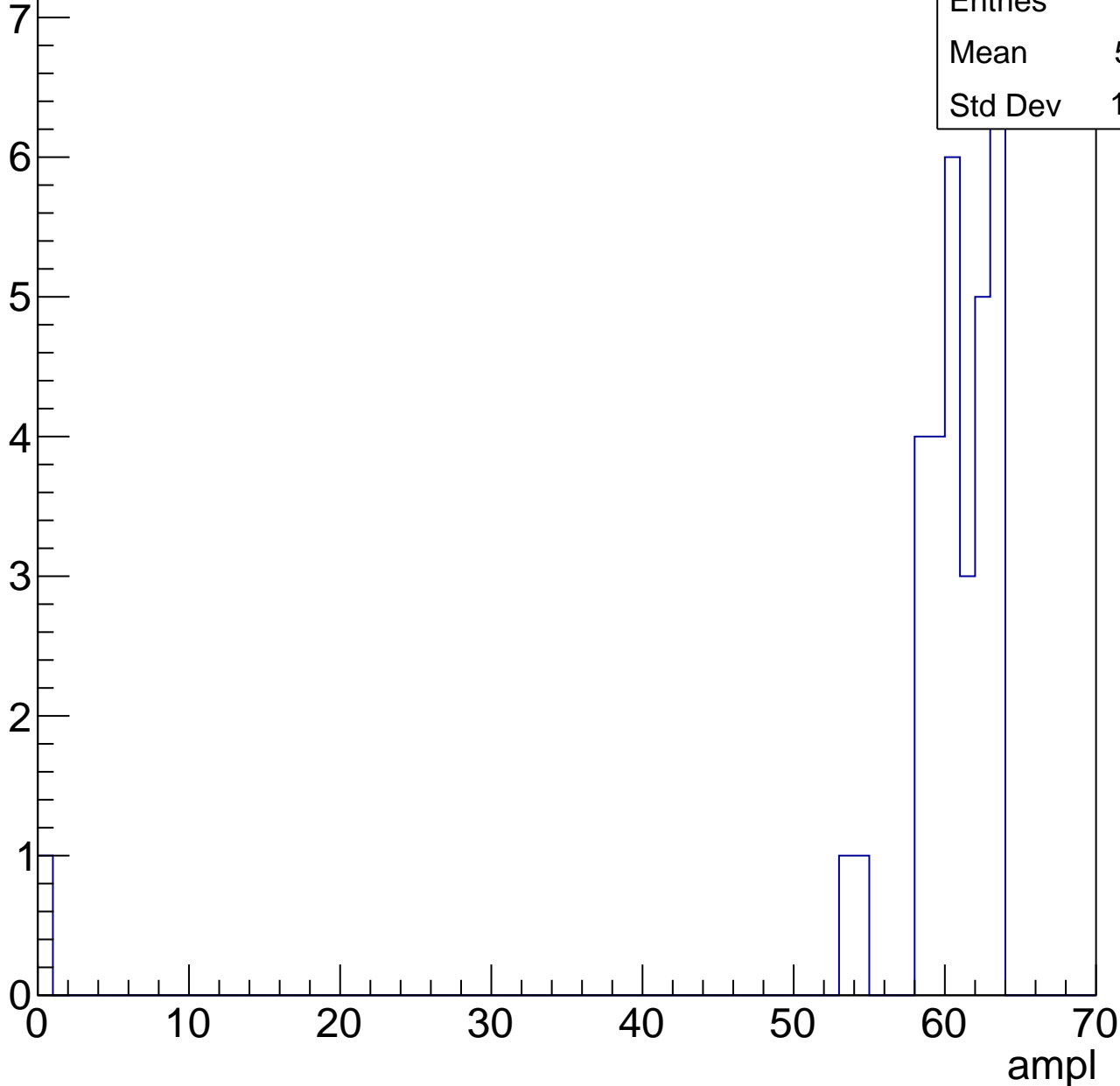


B1L103S, U23-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	58.41
Std Dev	10.77



B1L103S, U23-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch75, adc0

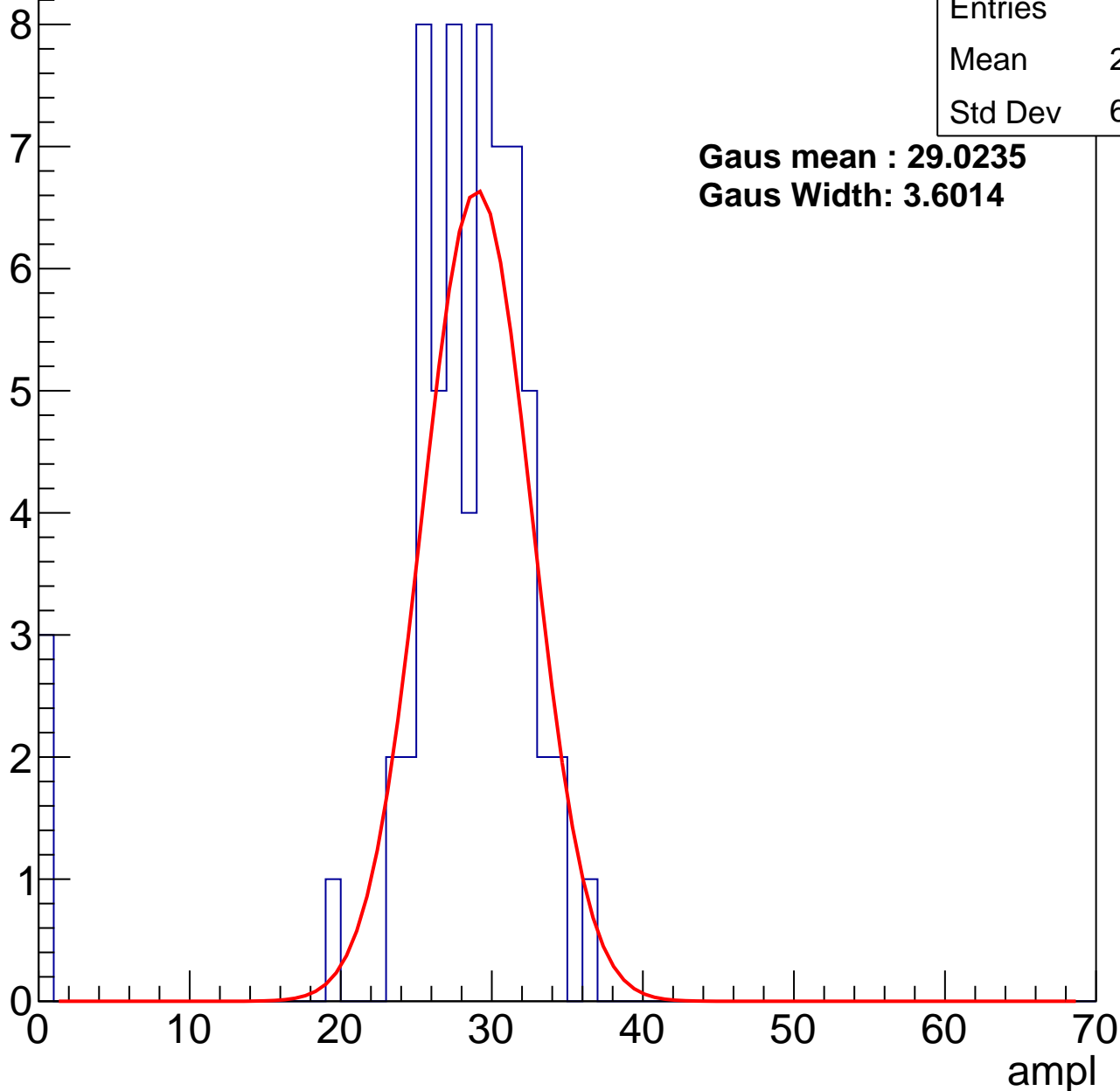
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	27.08
Std Dev	6.708

Gaus mean : 29.0235

Gaus Width: 3.6014

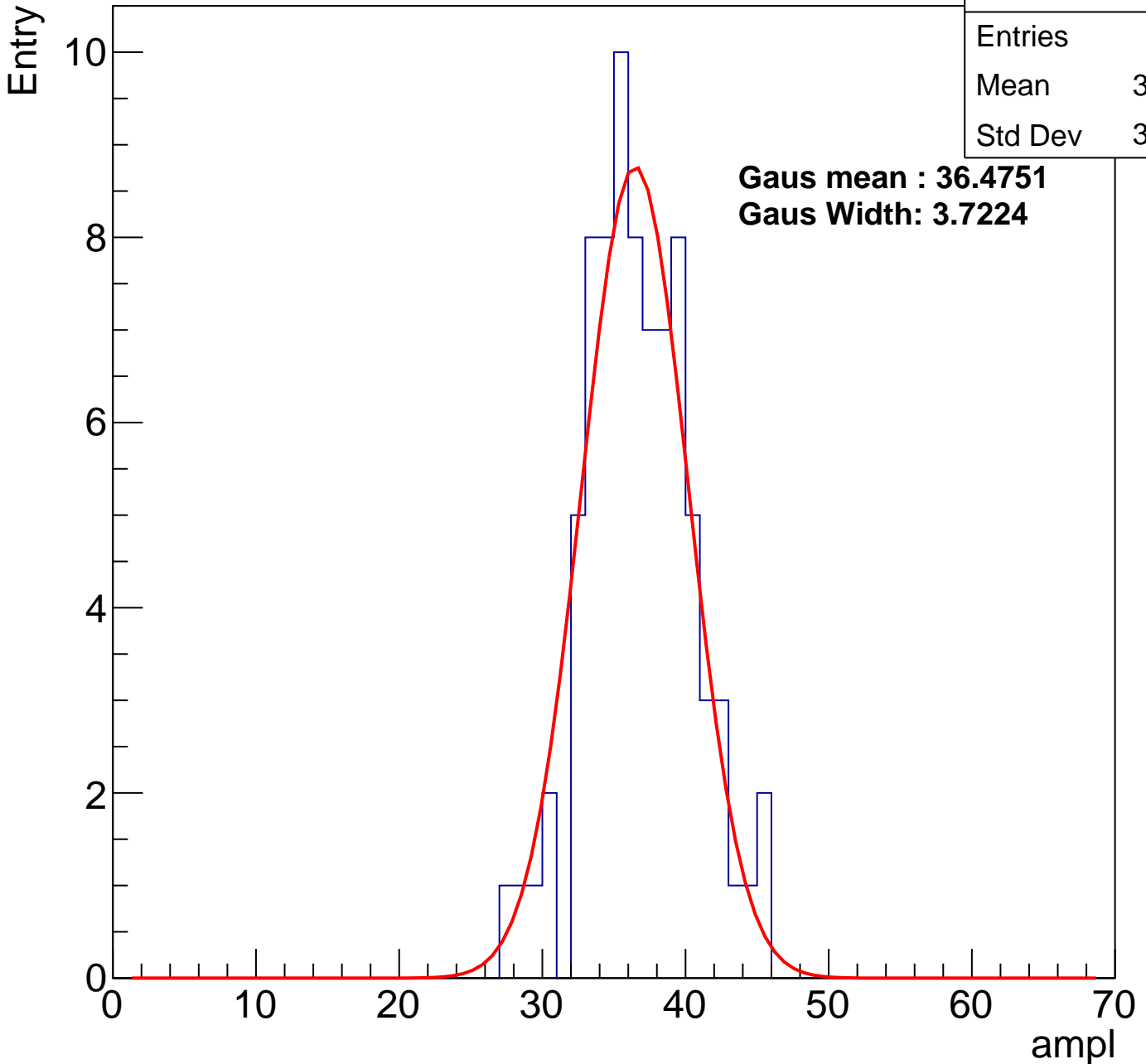


B1L103S, U23-ch75, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	36.31
Std Dev	3.681

Gaus mean : 36.4751
Gaus Width: 3.7224



B1L103S, U23-ch75, adc2

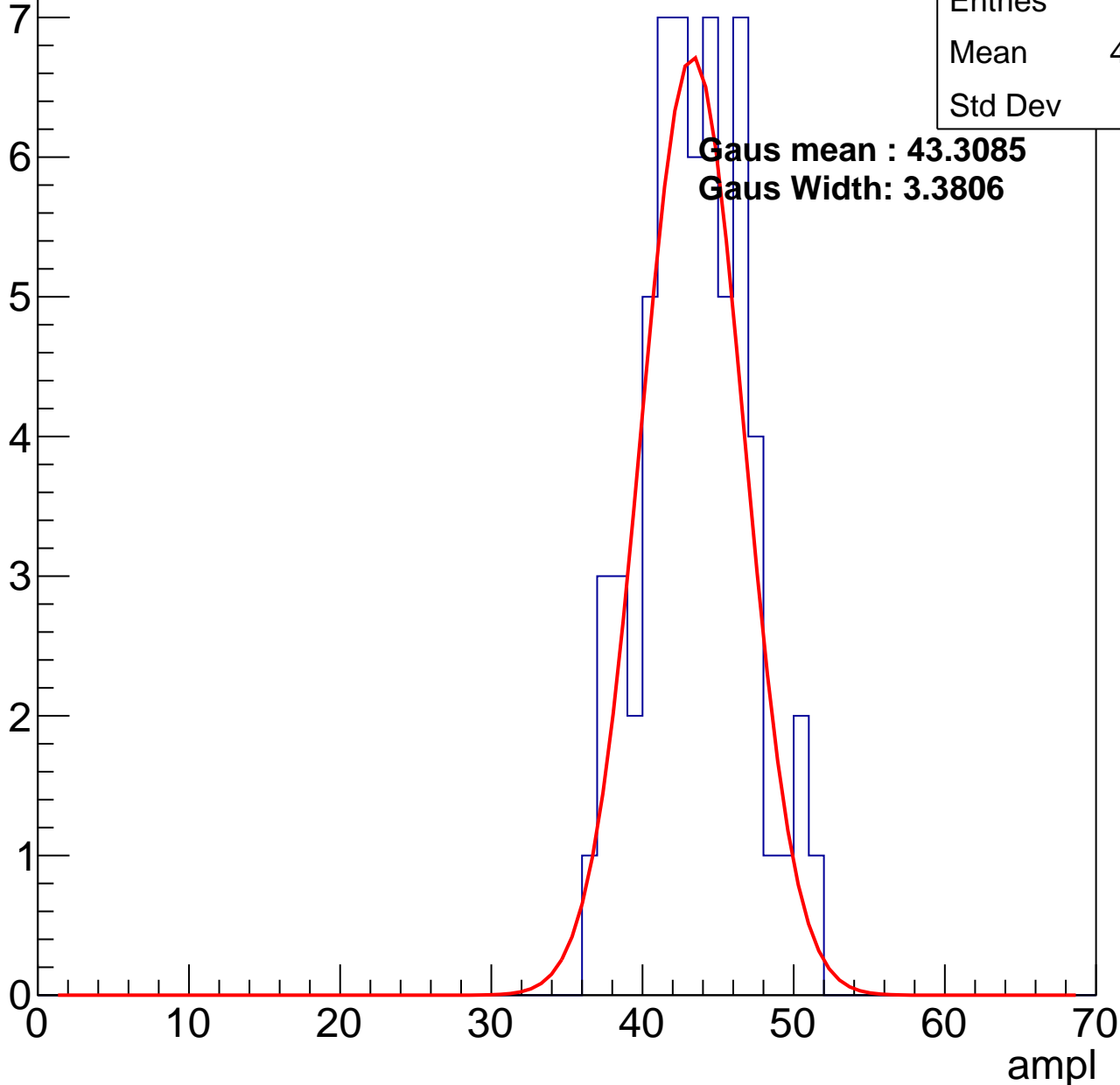
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.05
Std Dev	3.41

Gaus mean : 43.3085

Gaus Width: 3.3806

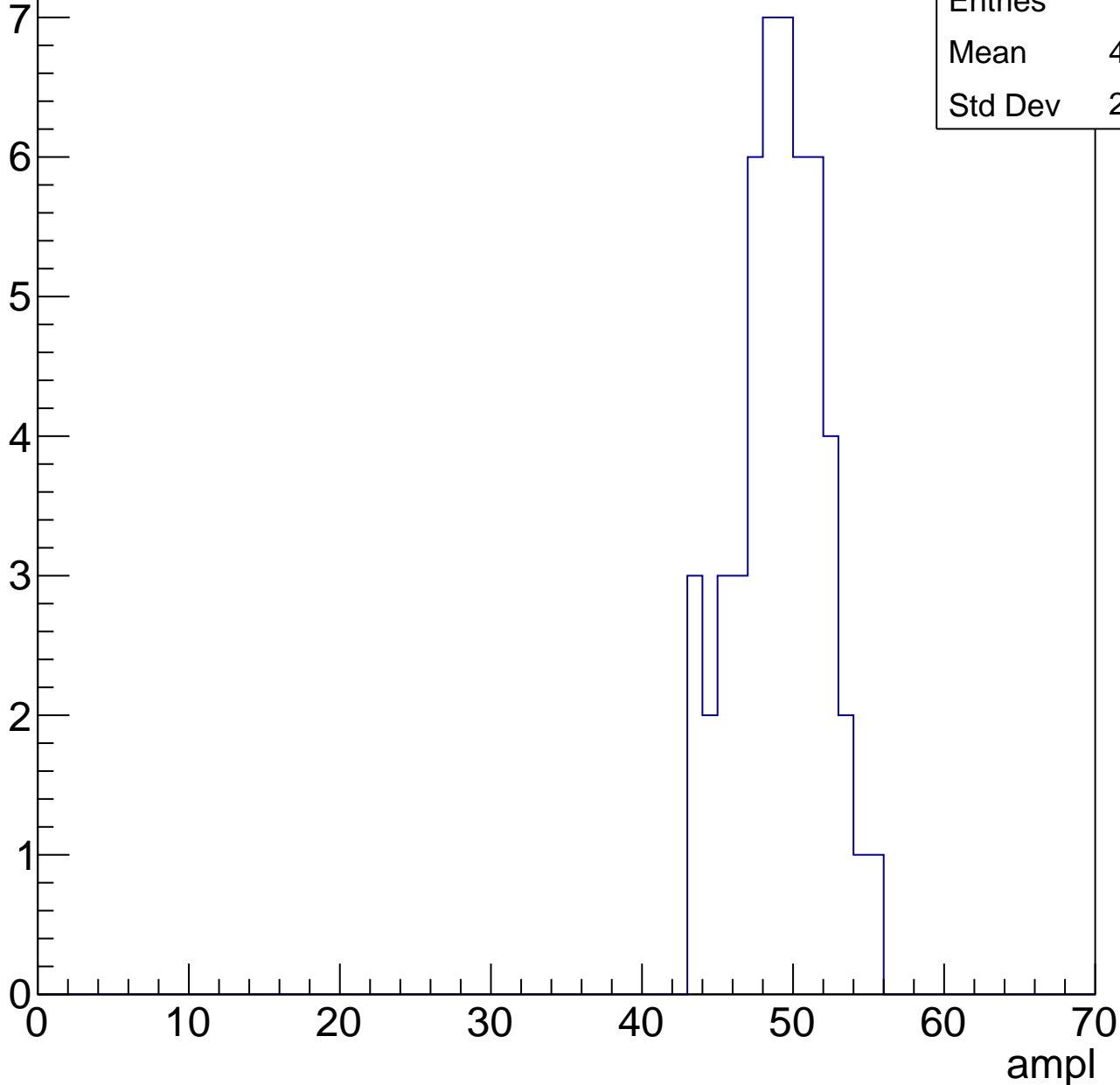


B1L103S, U23-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

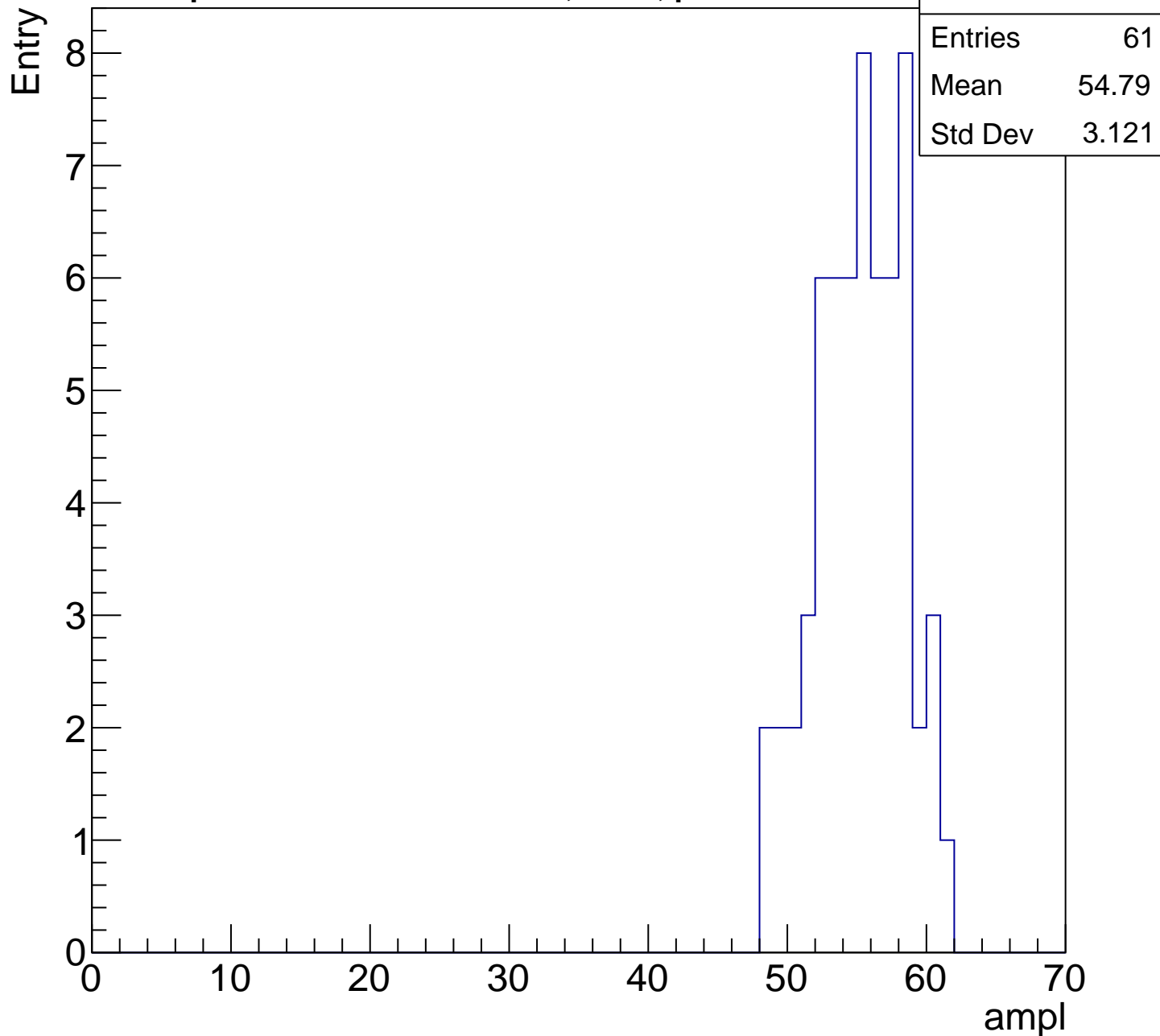
Entry

Entries	51
Mean	48.63
Std Dev	2.856



B1L103S, U23-ch75, adc4

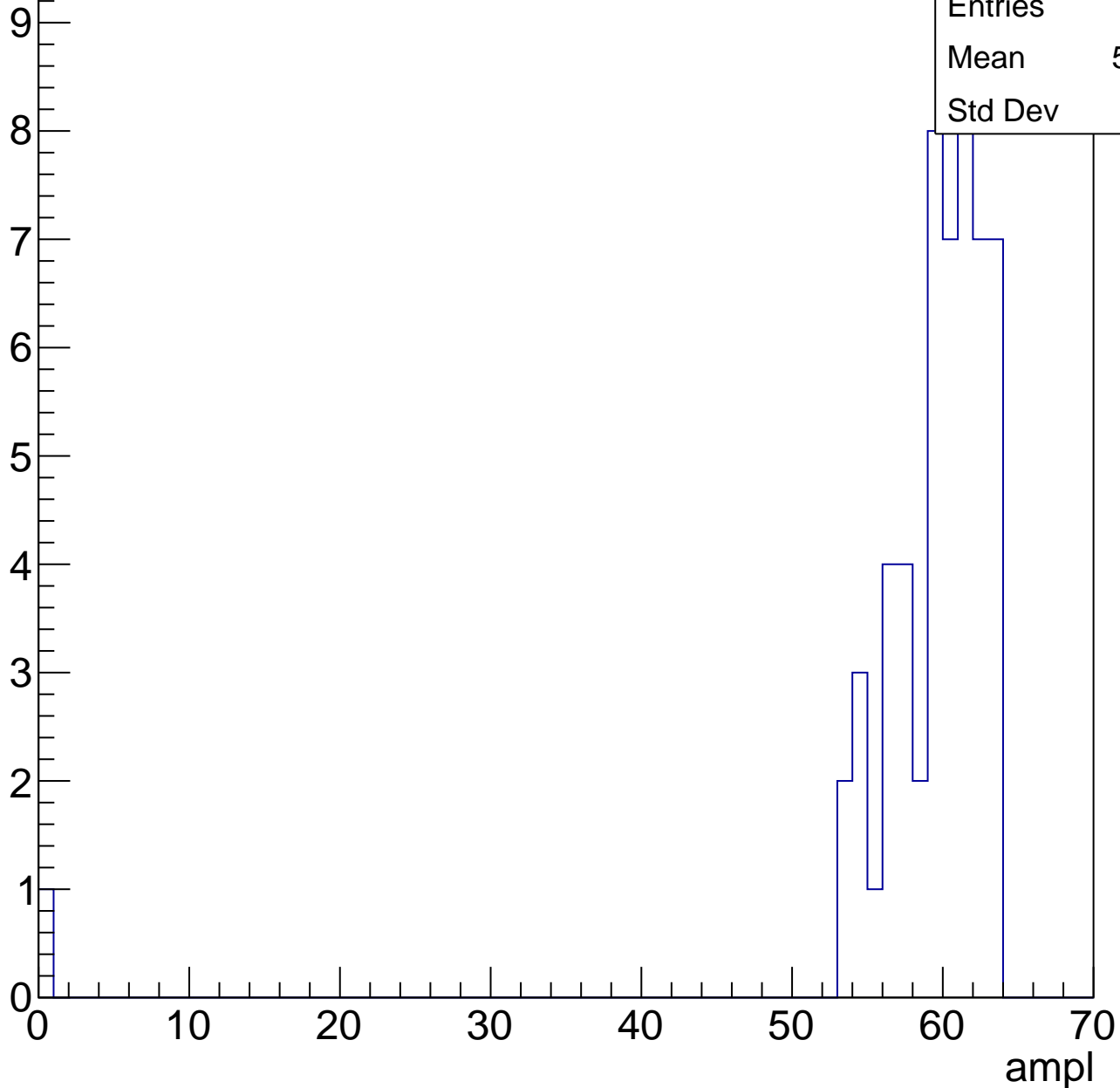
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U23-ch75, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch76, adc0

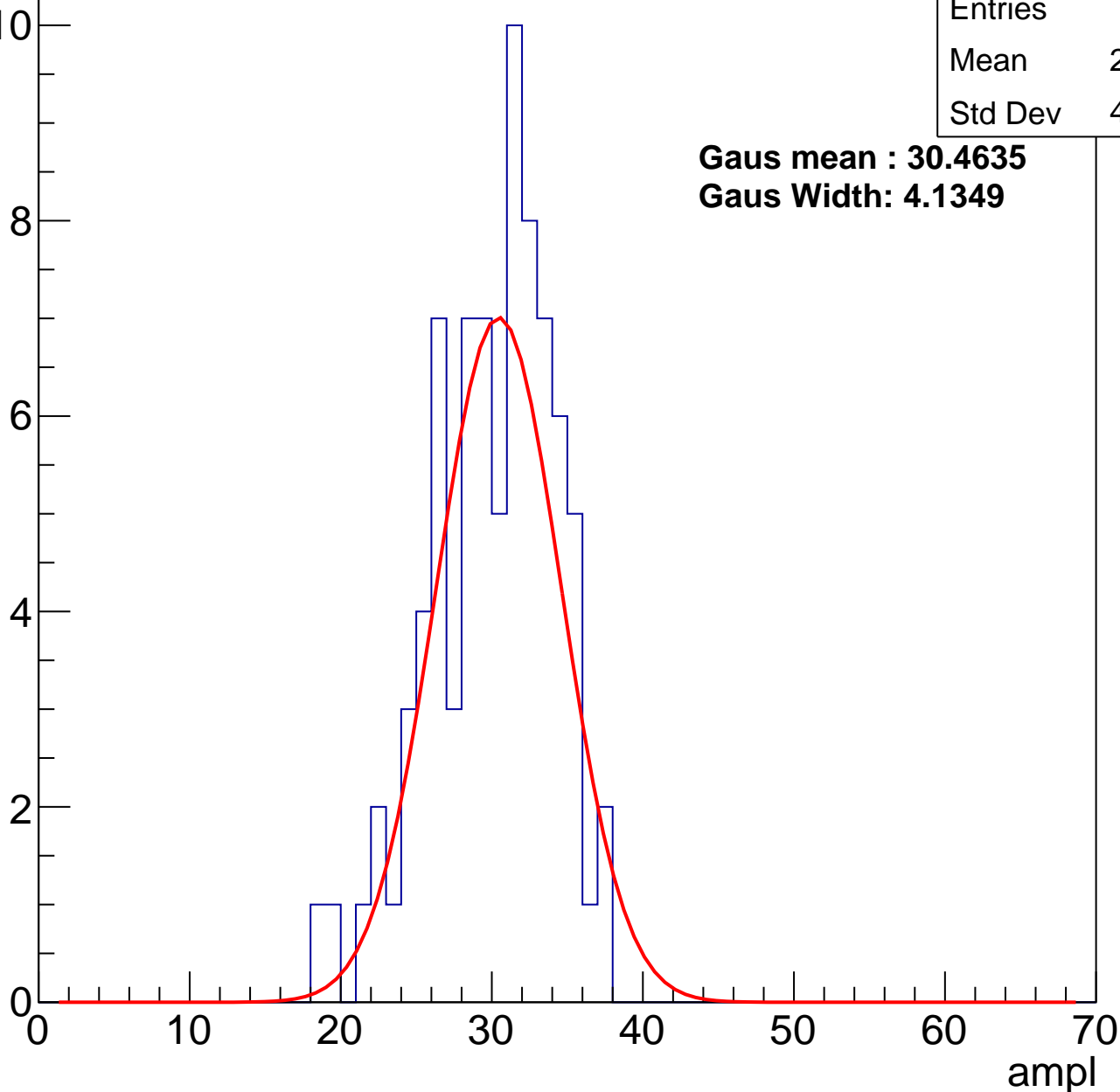
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	29.57
Std Dev	4.082

Gaus mean : 30.4635

Gaus Width: 4.1349



B1L103S, U23-ch76, adc1

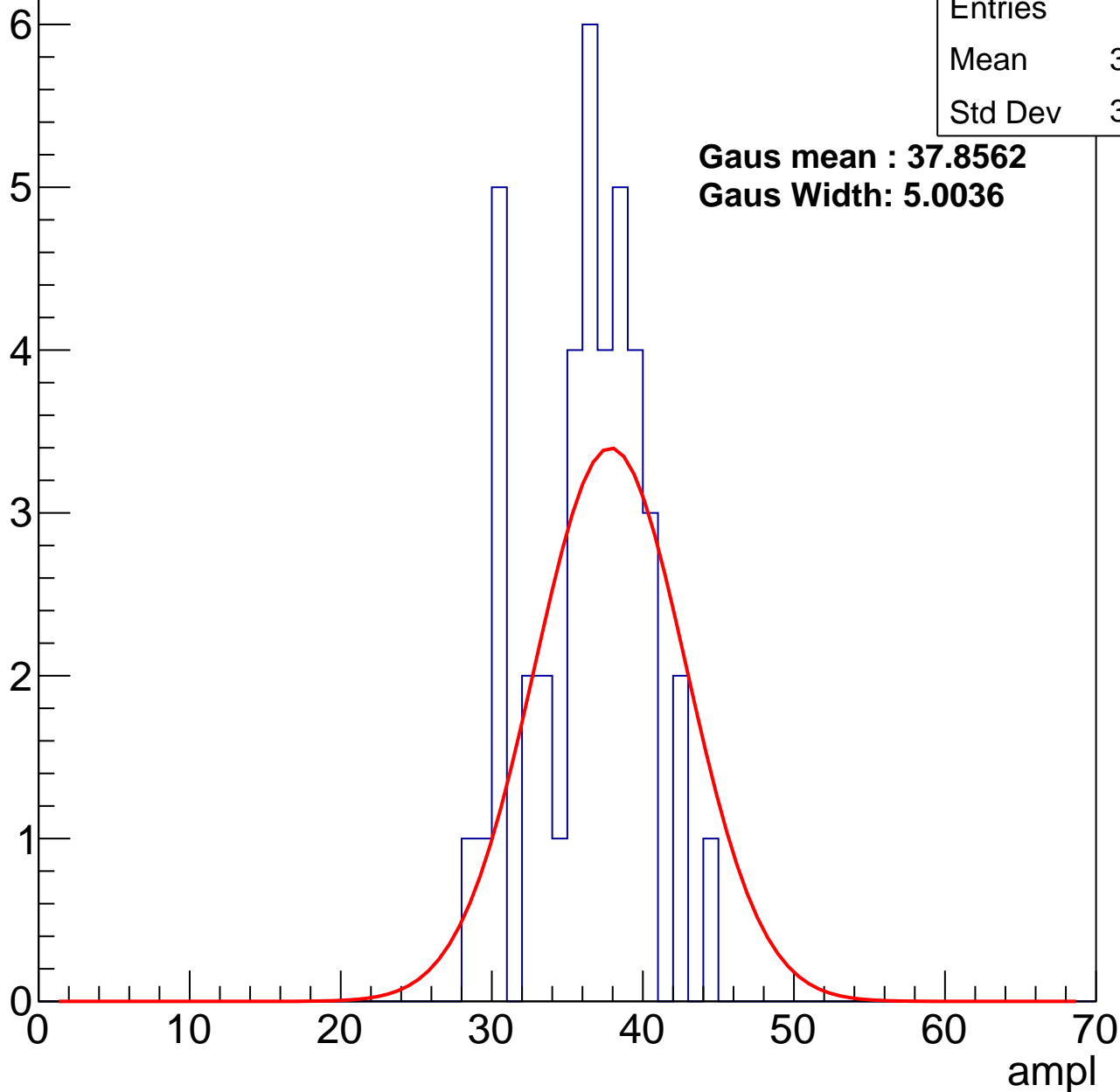
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	35.83
Std Dev	3.806

Gaus mean : 37.8562

Gaus Width: 5.0036



B1L103S, U23-ch76, adc2

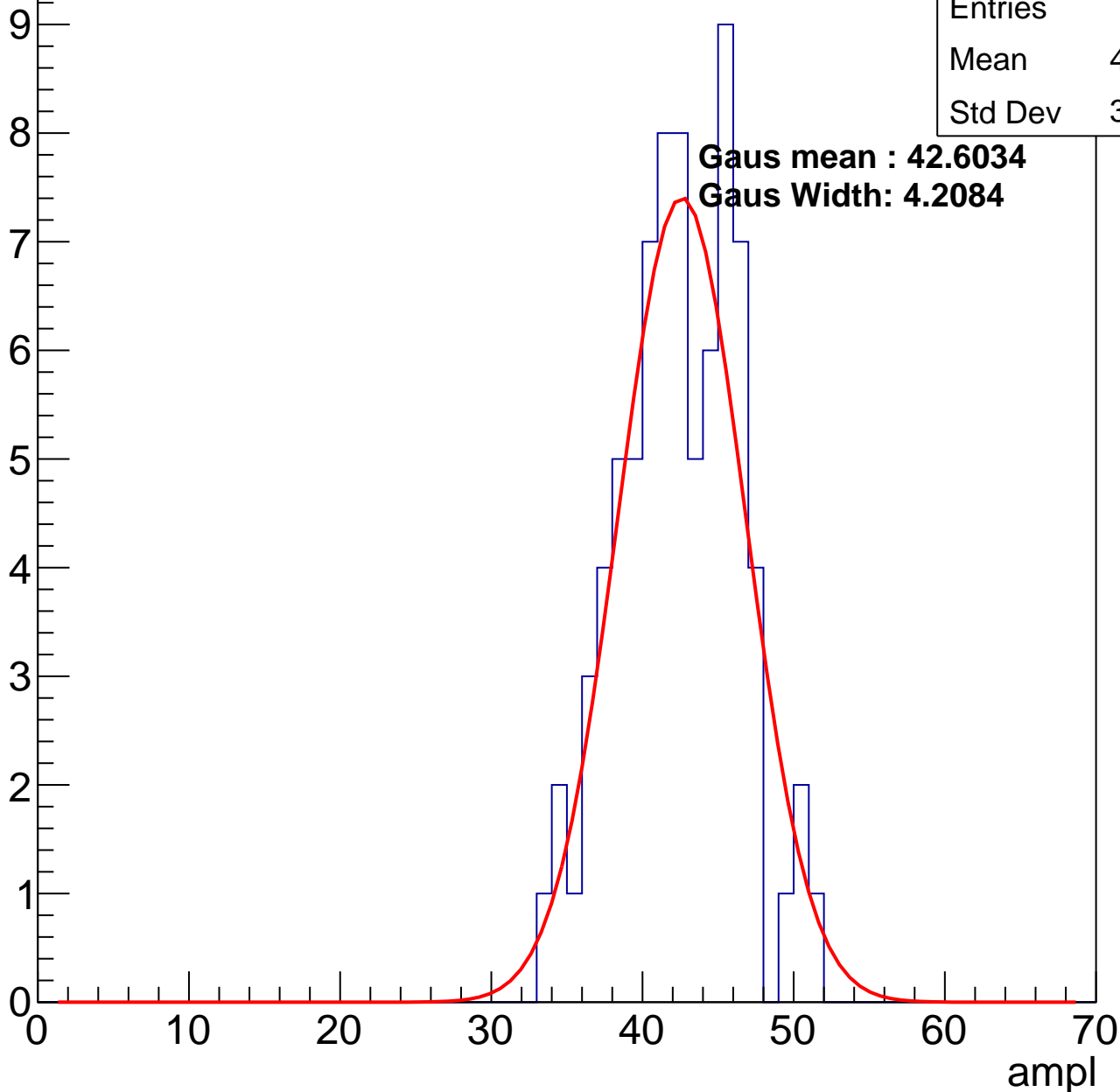
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	41.96
Std Dev	3.909

Gaus mean : 42.6034

Gaus Width: 4.2084

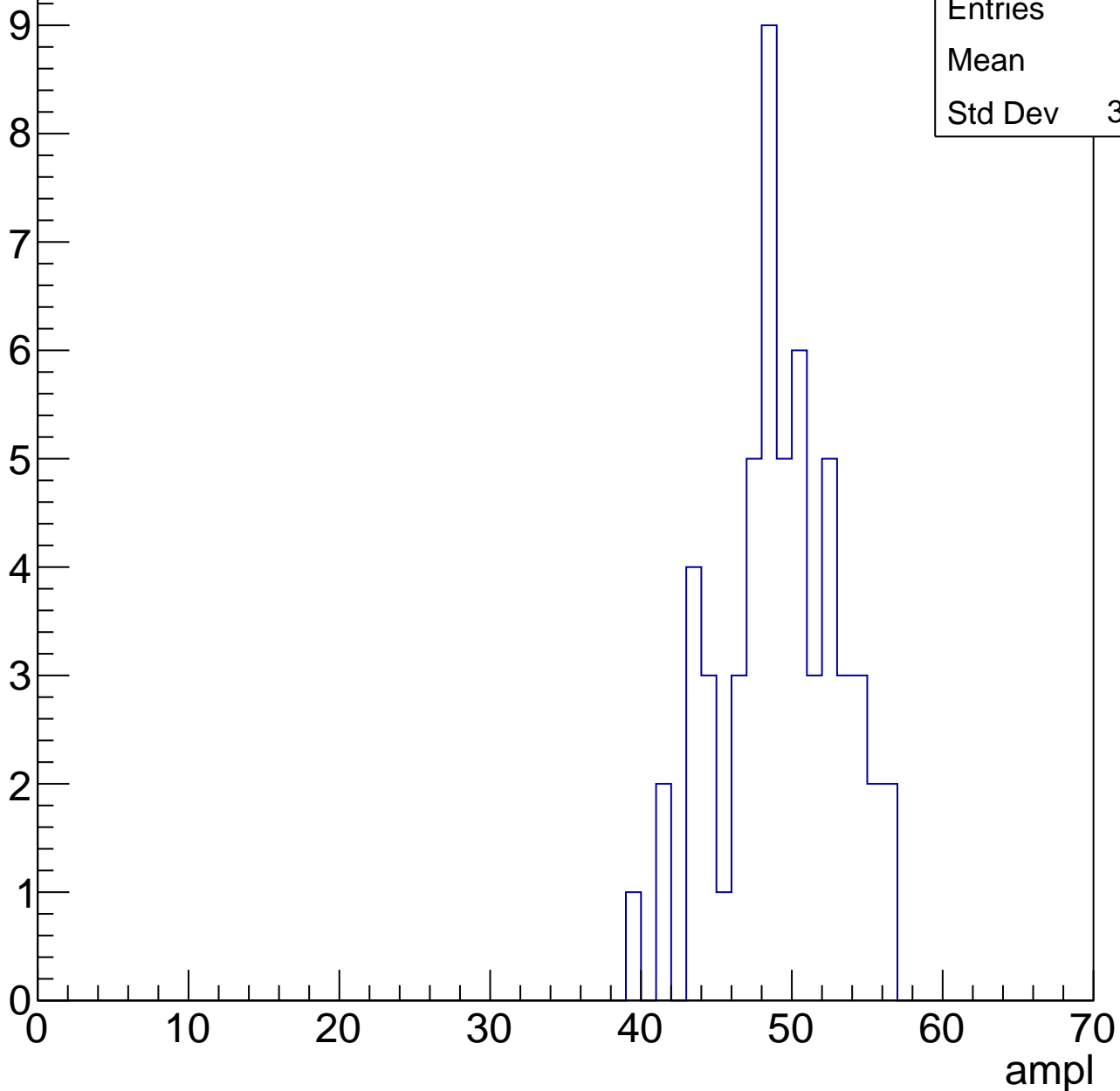


B1L103S, U23-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	48.7
Std Dev	3.907

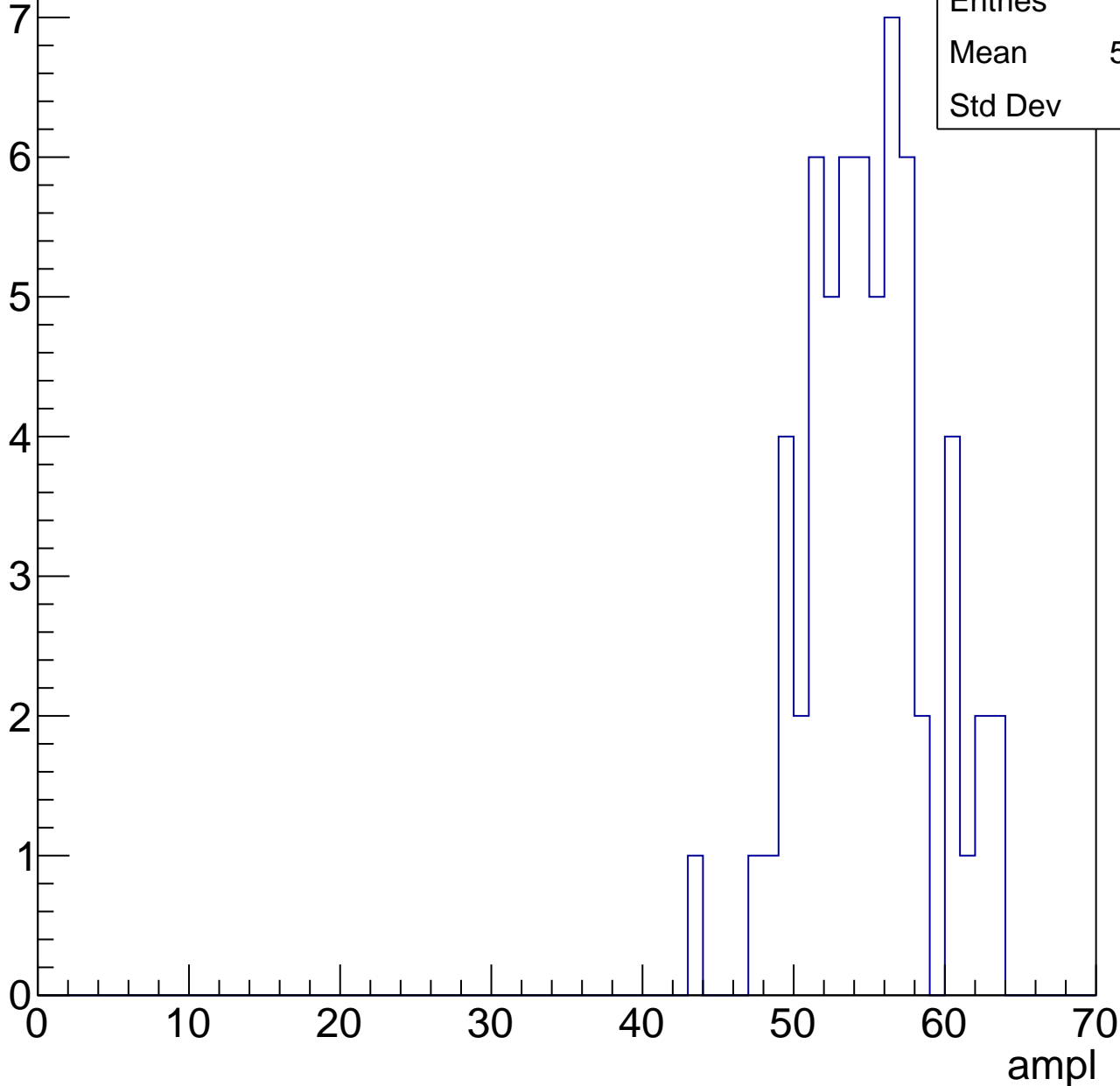


B1L103S, U23-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	54.39
Std Dev	4.07



B1L103S, U23-ch76, adc5

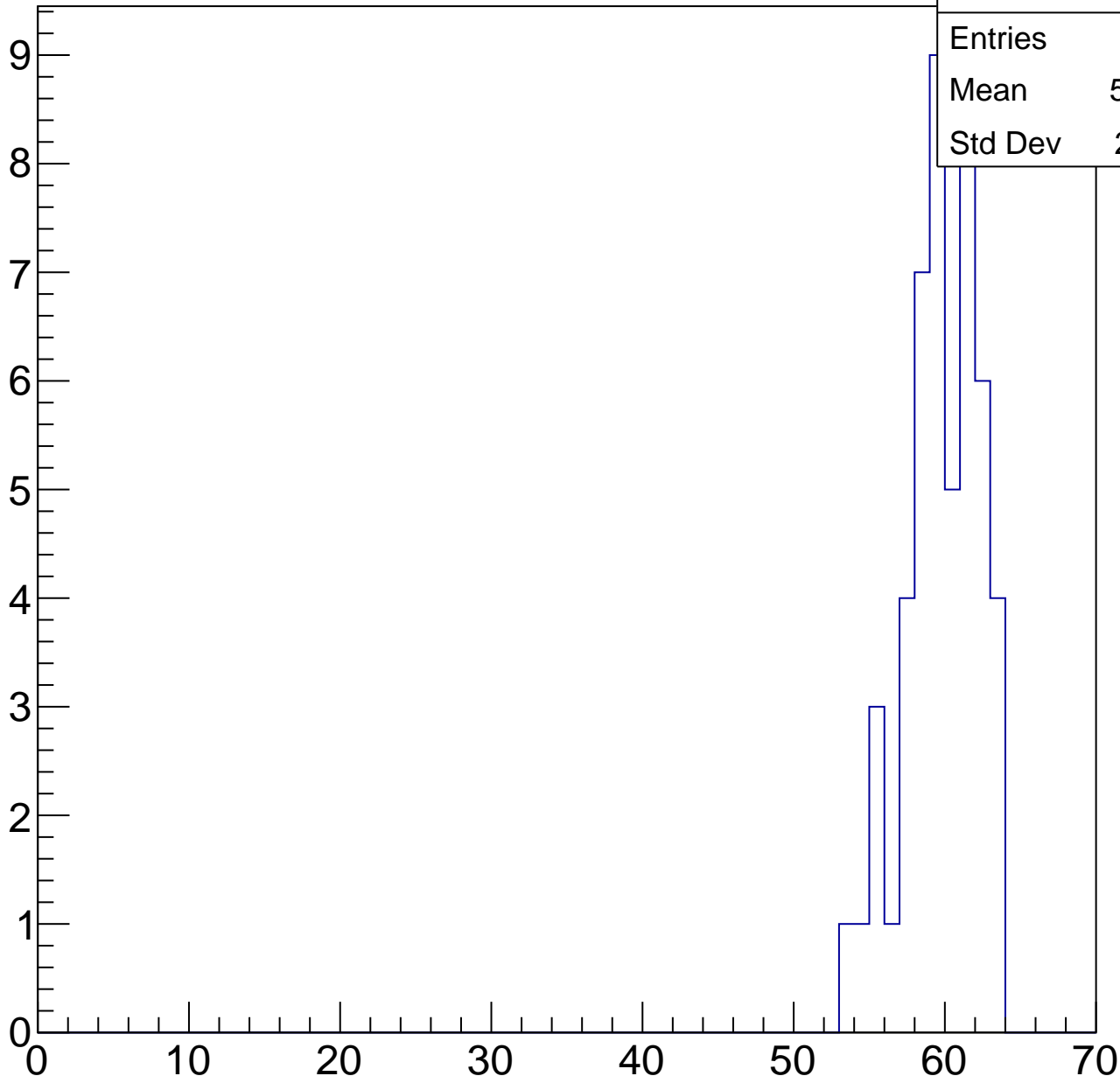
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.29
Std Dev	2.441

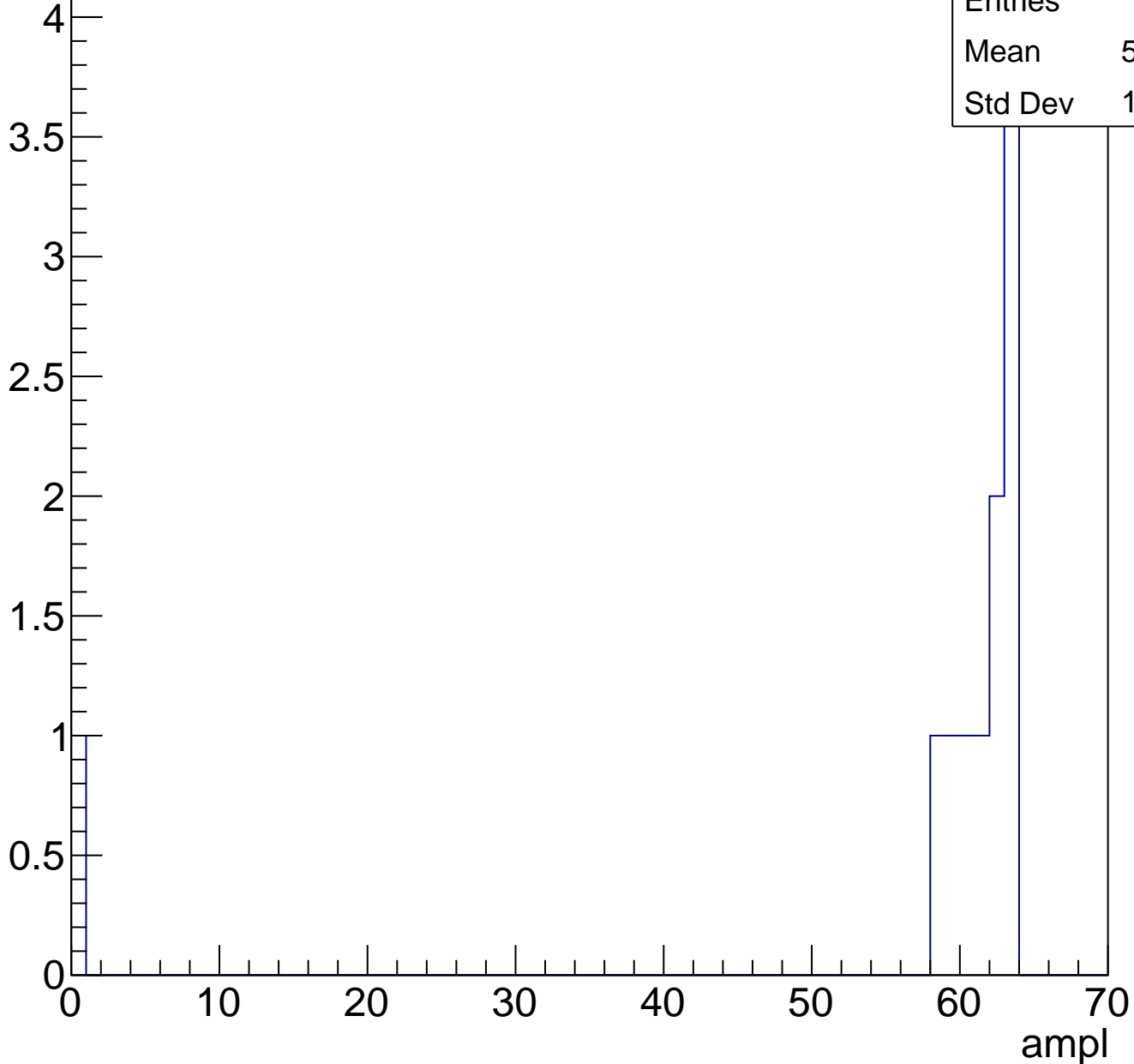
ampl



B1L103S, U23-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch77, adc0

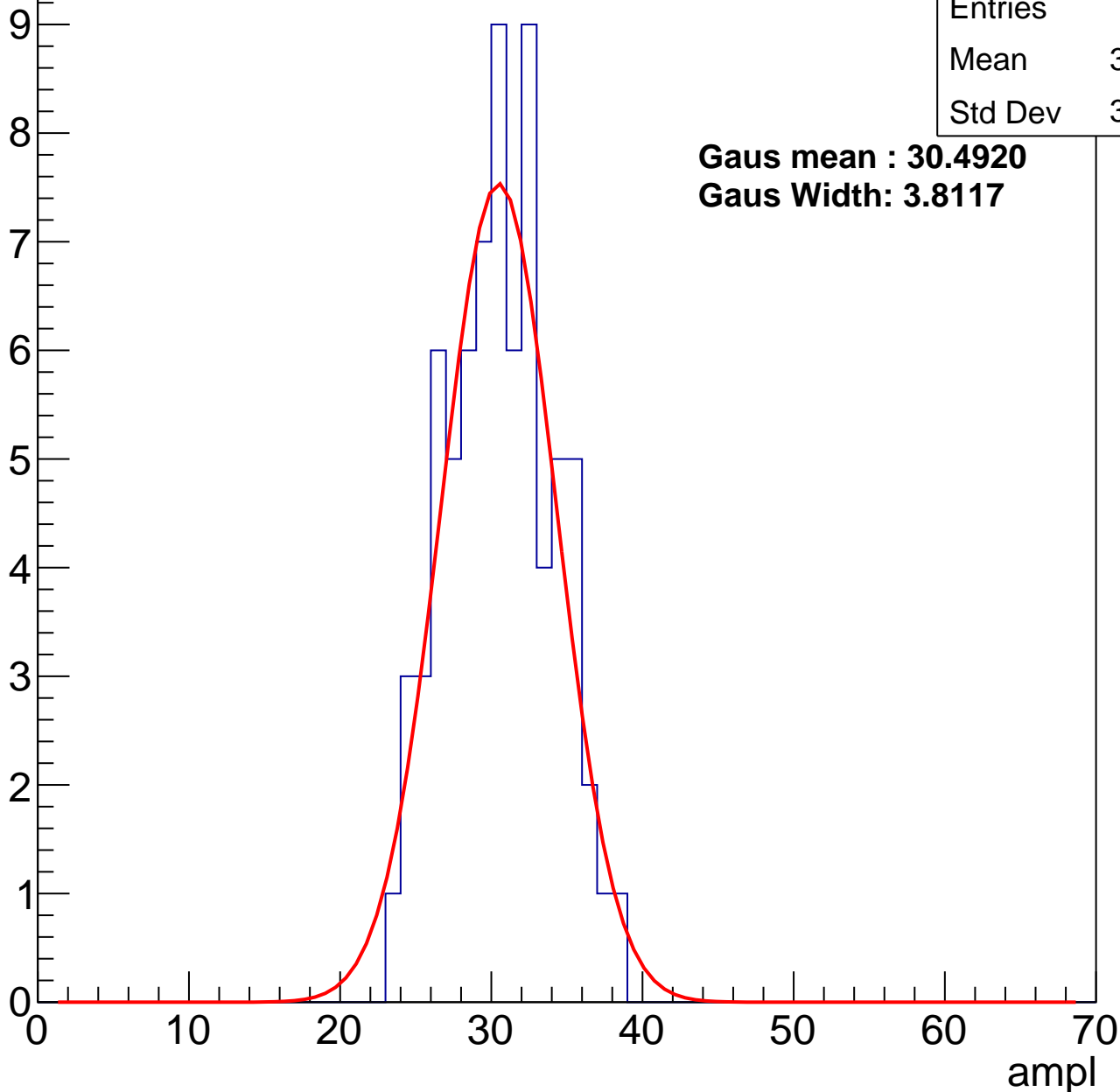
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	30.14
Std Dev	3.453

Gaus mean : 30.4920

Gaus Width: 3.8117



B1L103S, U23-ch77, adc1

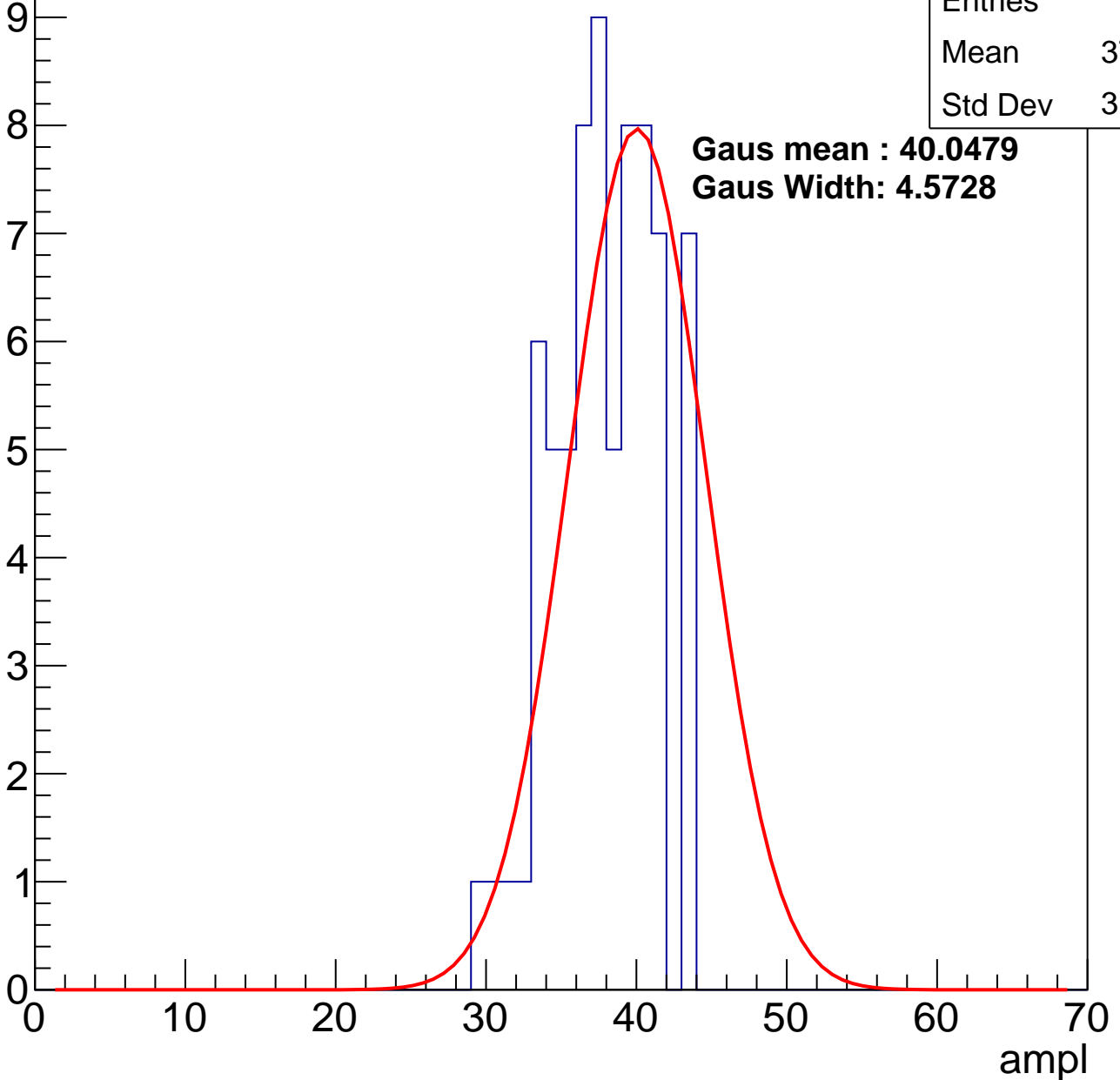
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	37.44
Std Dev	3.337

Gaus mean : 40.0479

Gaus Width: 4.5728



B1L103S, U23-ch77, adc2

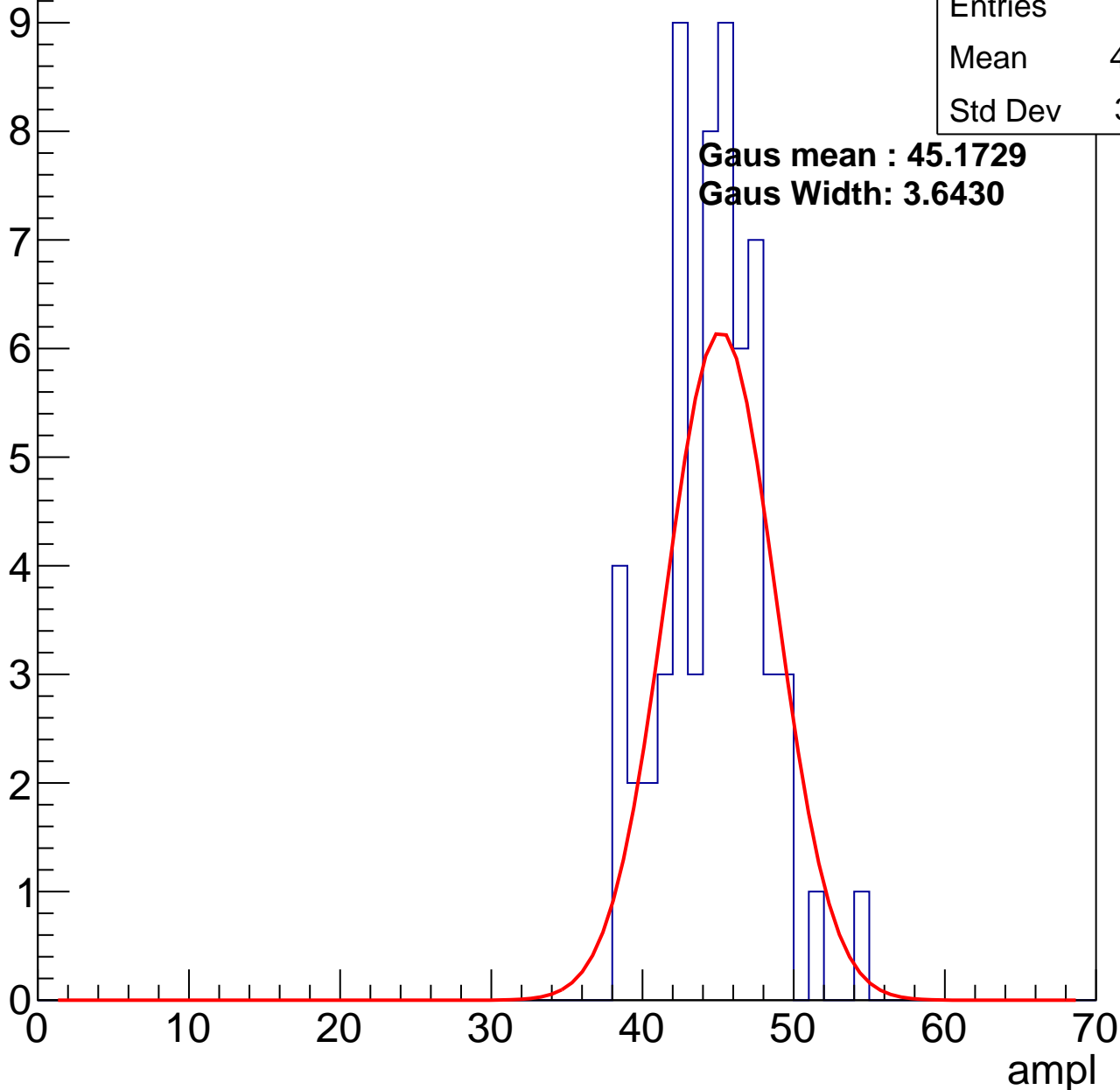
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	44.23
Std Dev	3.281

Gaus mean : 45.1729

Gaus Width: 3.6430

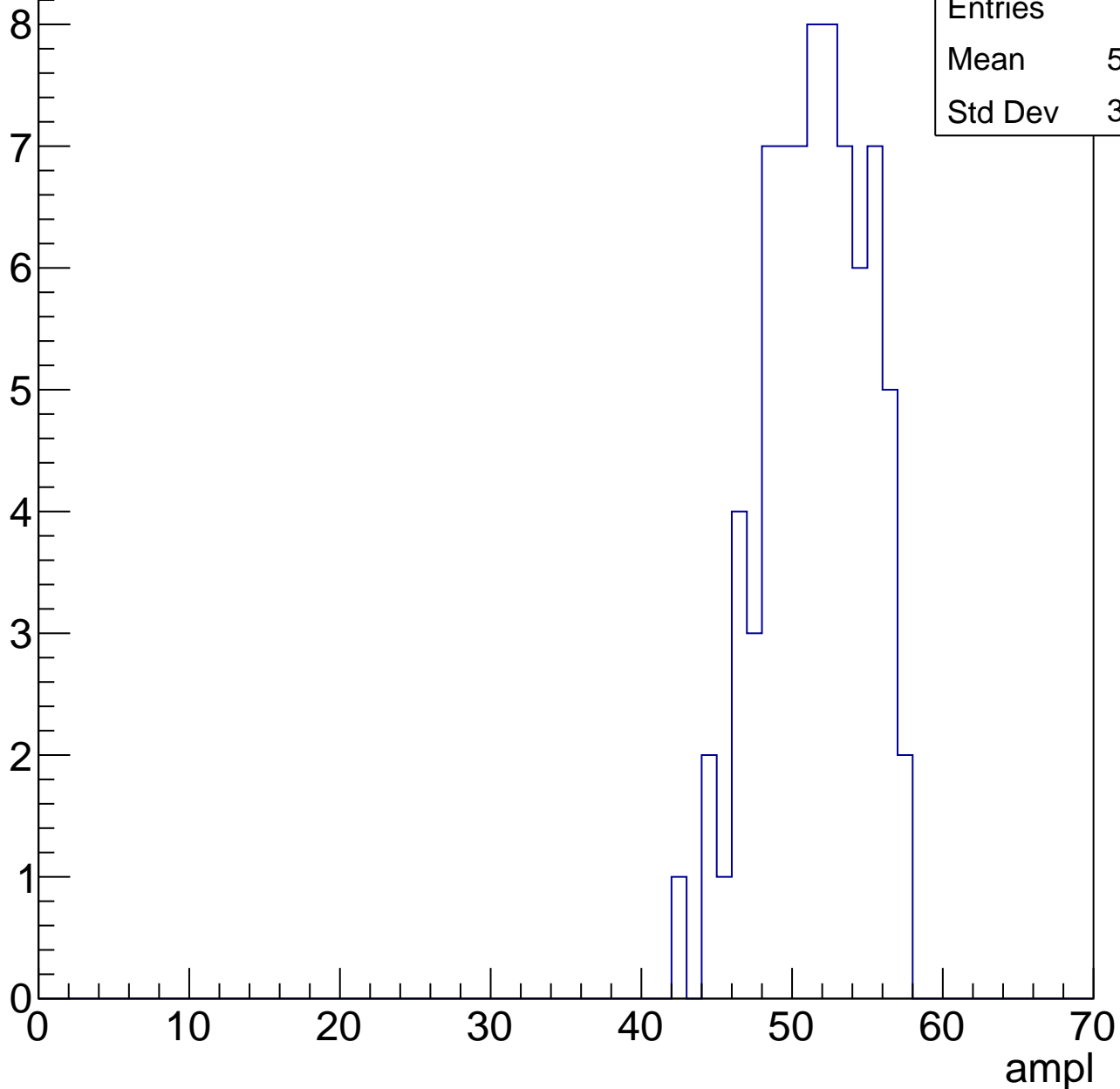


B1L103S, U23-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	51.03
Std Dev	3.386



B1L103S, U23-ch77, adc4

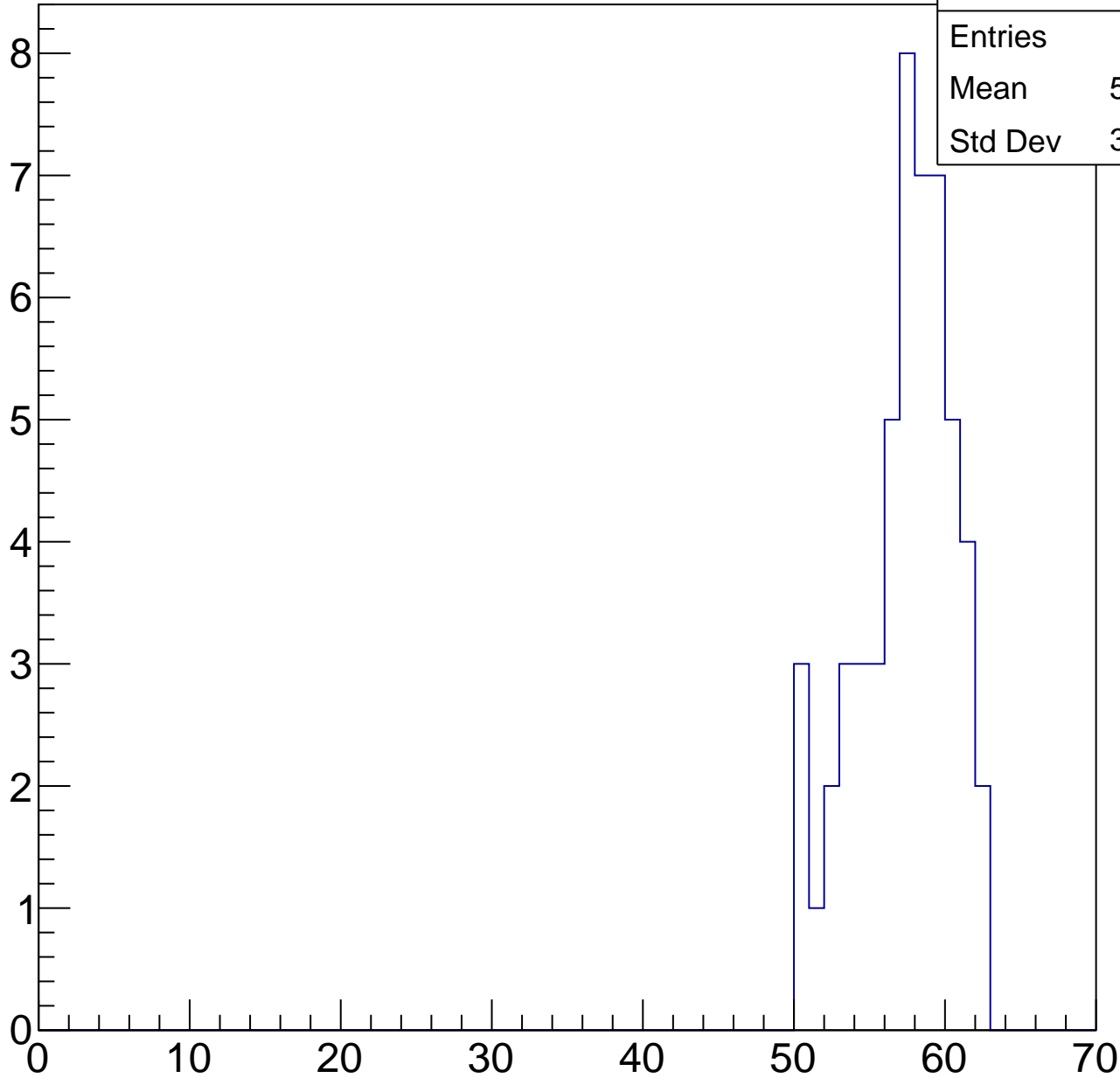
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	56.87
Std Dev	3.127

ampl

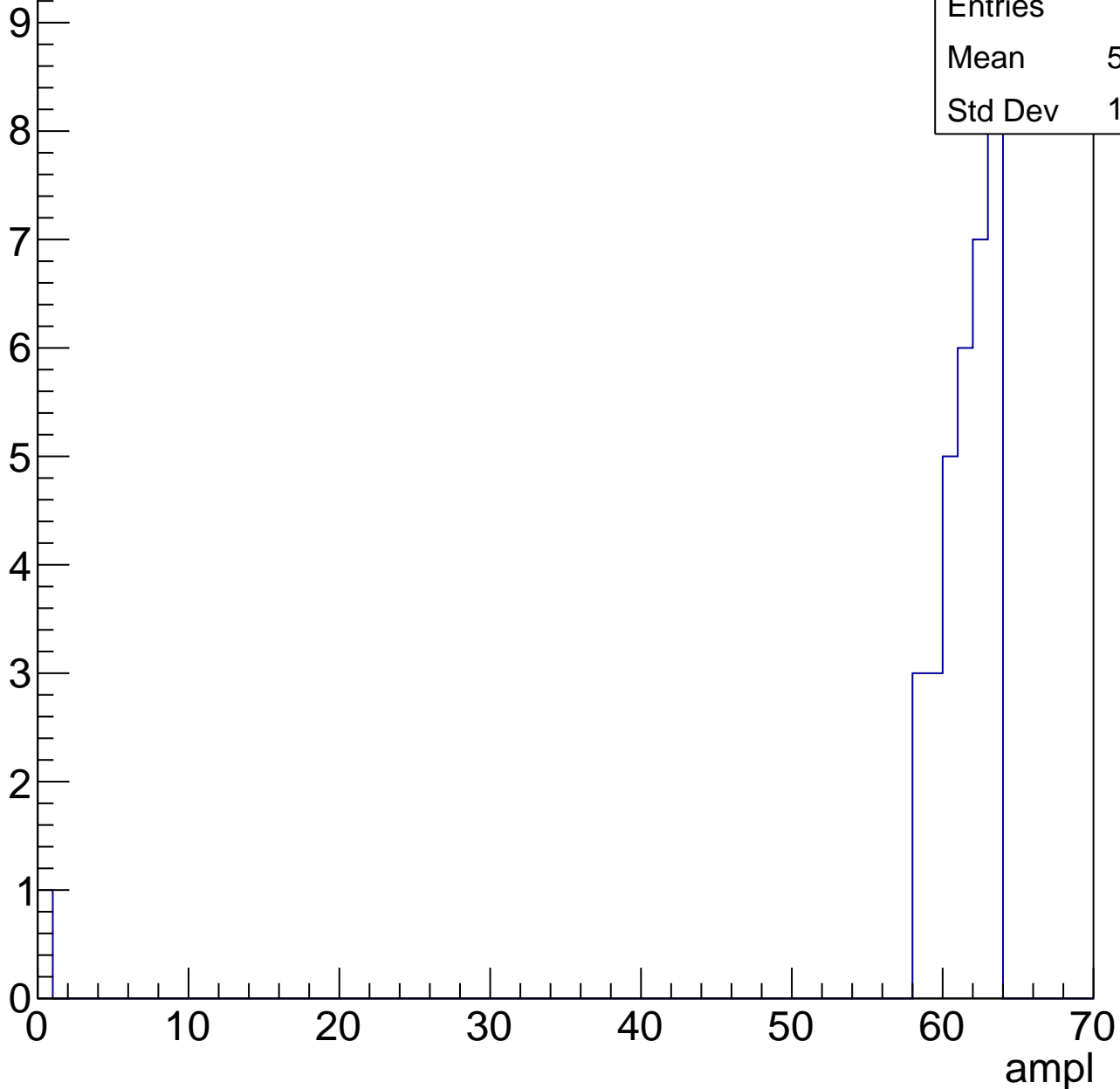


B1L103S, U23-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	59.35
Std Dev	10.45



B1L103S, U23-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch78, adc0

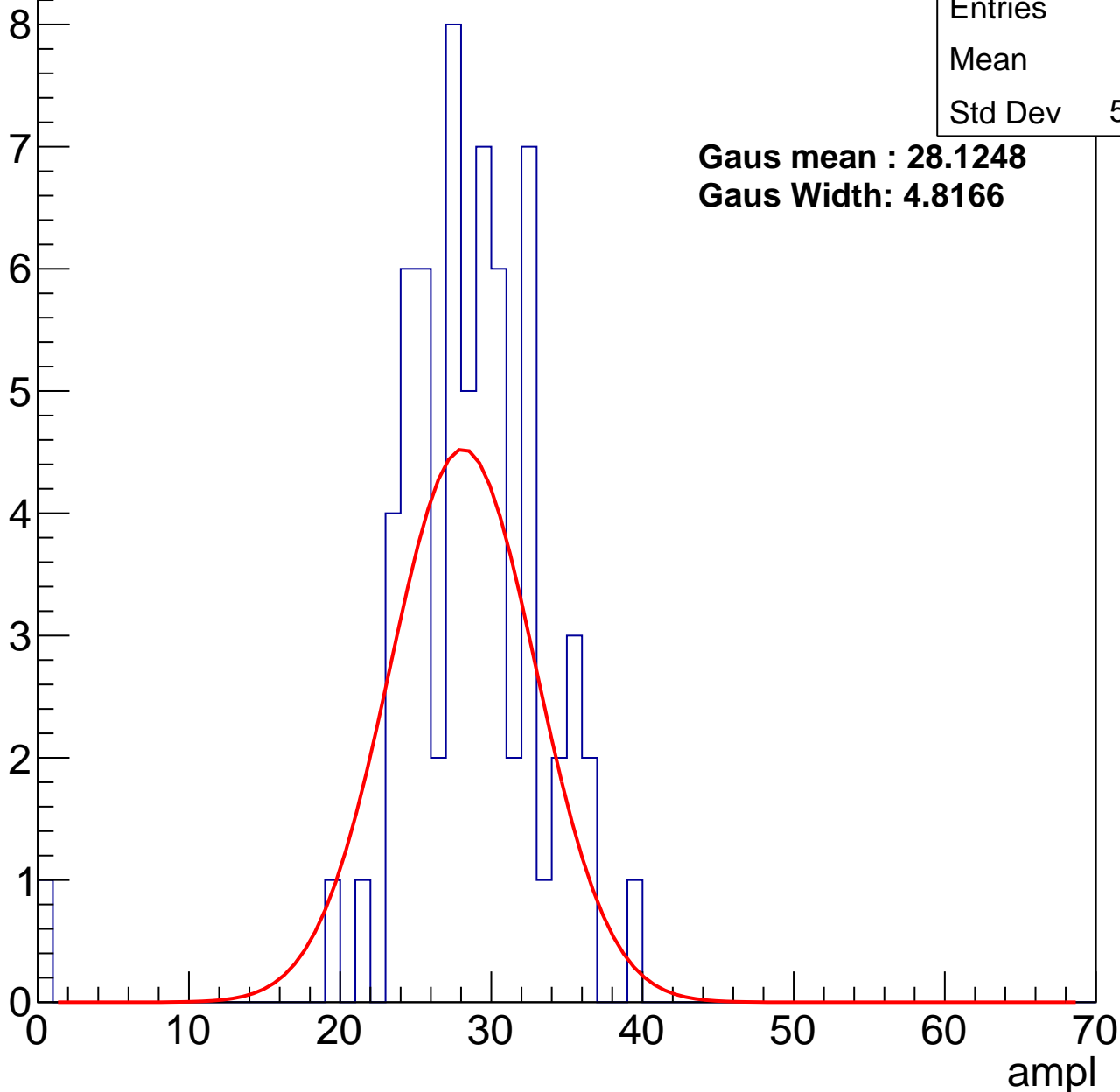
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28
Std Dev	5.312

Gaus mean : 28.1248

Gaus Width: 4.8166



B1L103S, U23-ch78, adc1

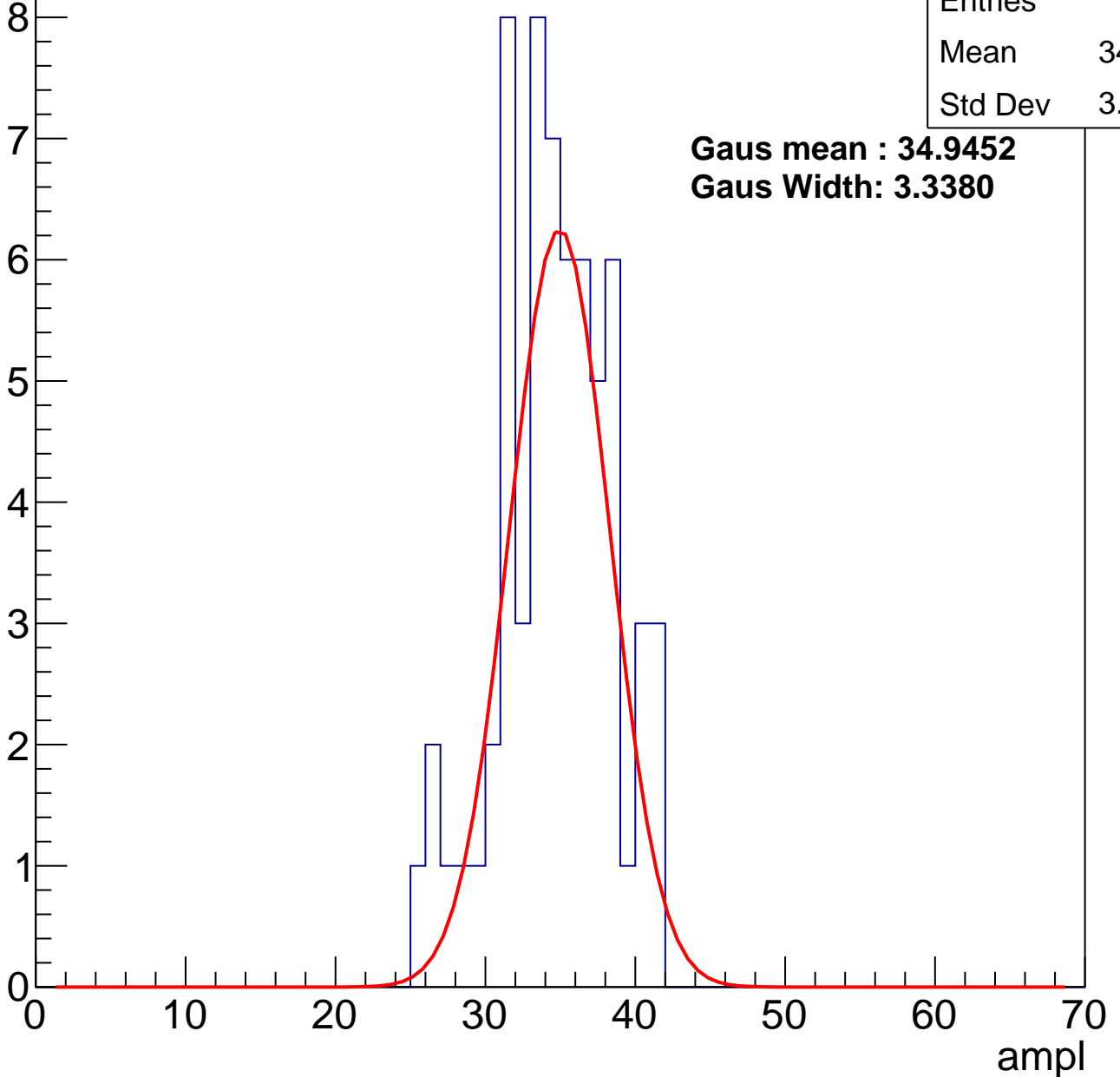
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	34.19
Std Dev	3.733

Gaus mean : 34.9452

Gaus Width: 3.3380



B1L103S, U23-ch78, adc2

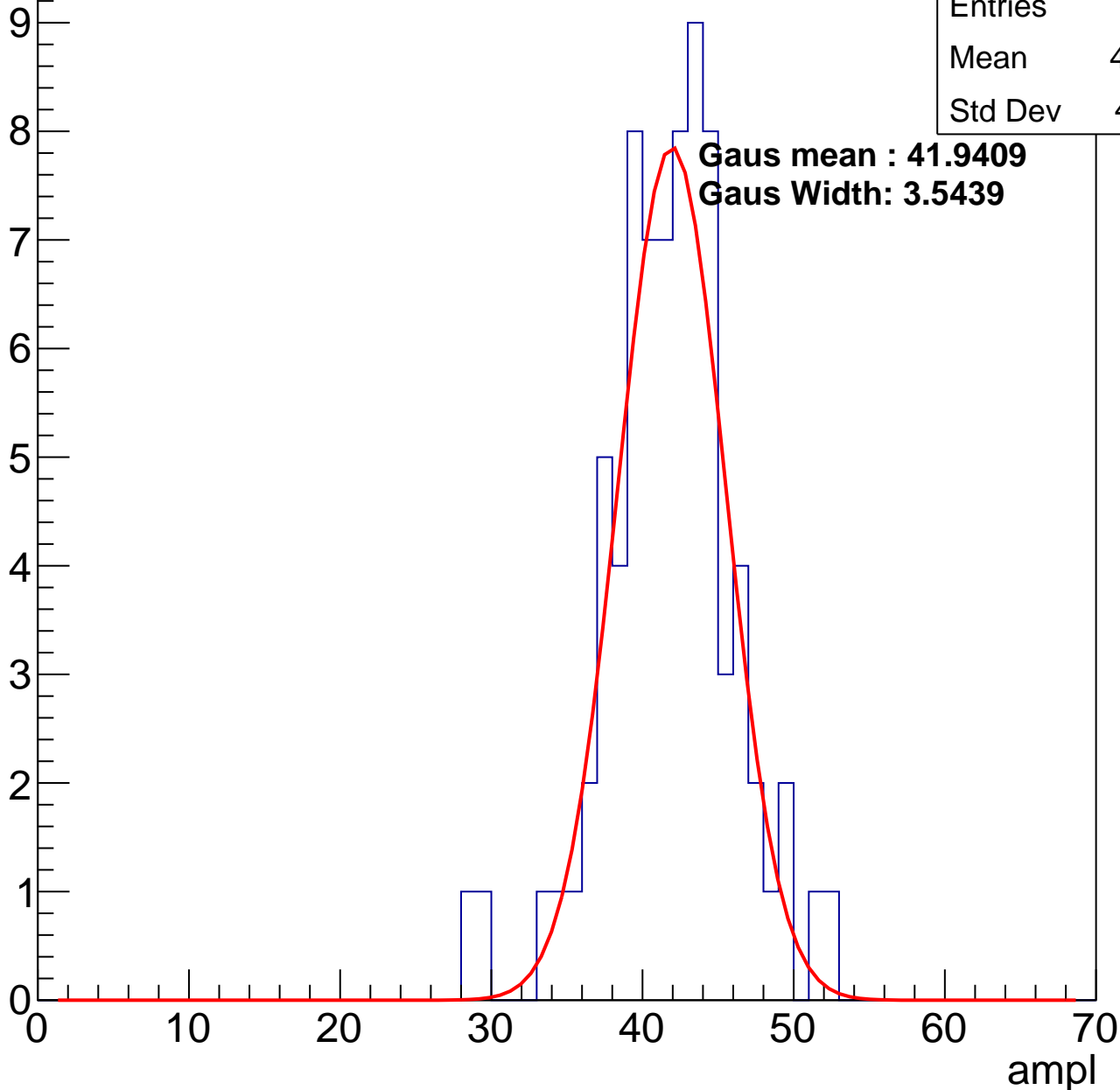
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	41.35
Std Dev	4.251

Gaus mean : 41.9409

Gaus Width: 3.5439

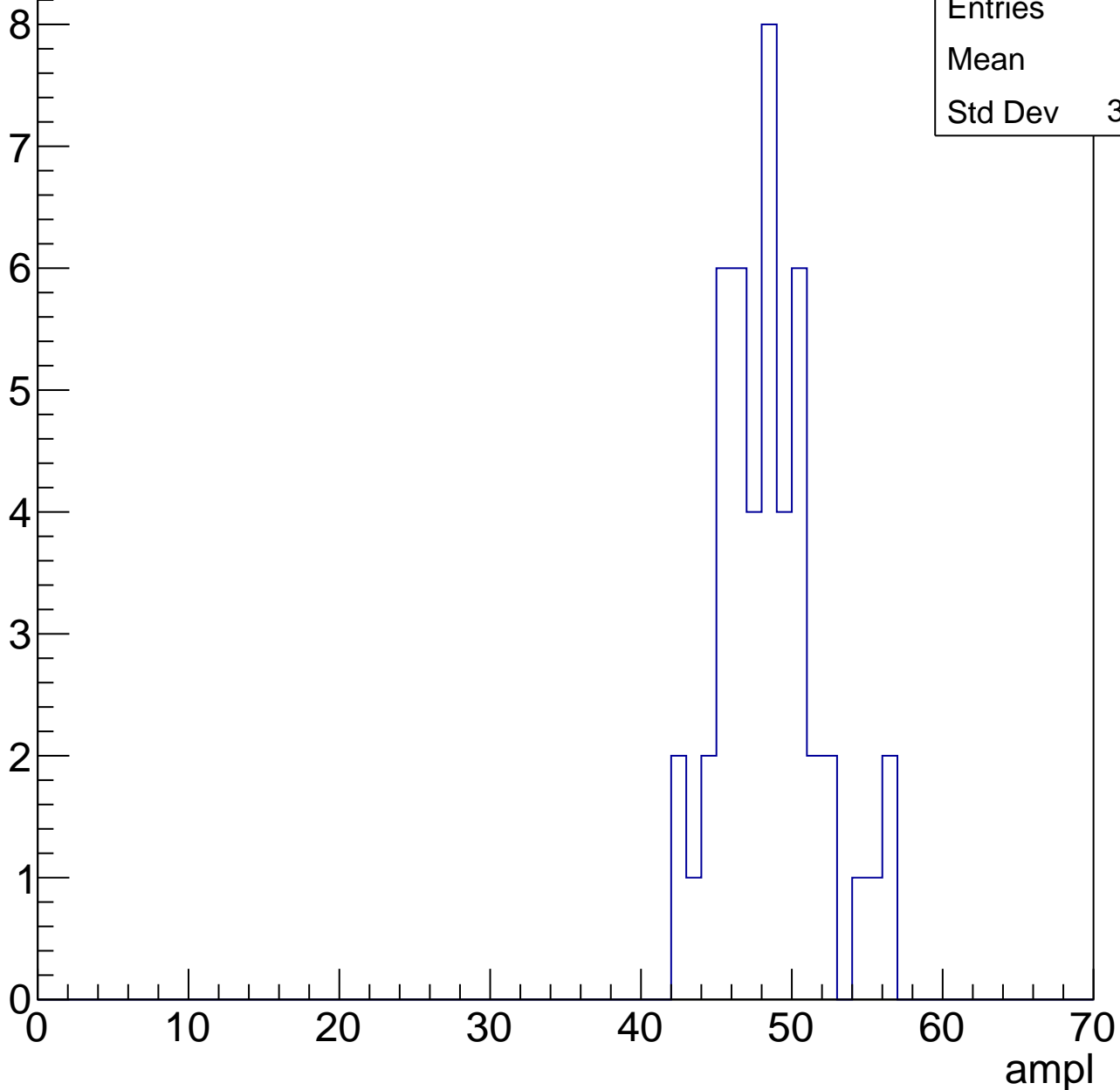


B1L103S, U23-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	48
Std Dev	3.268

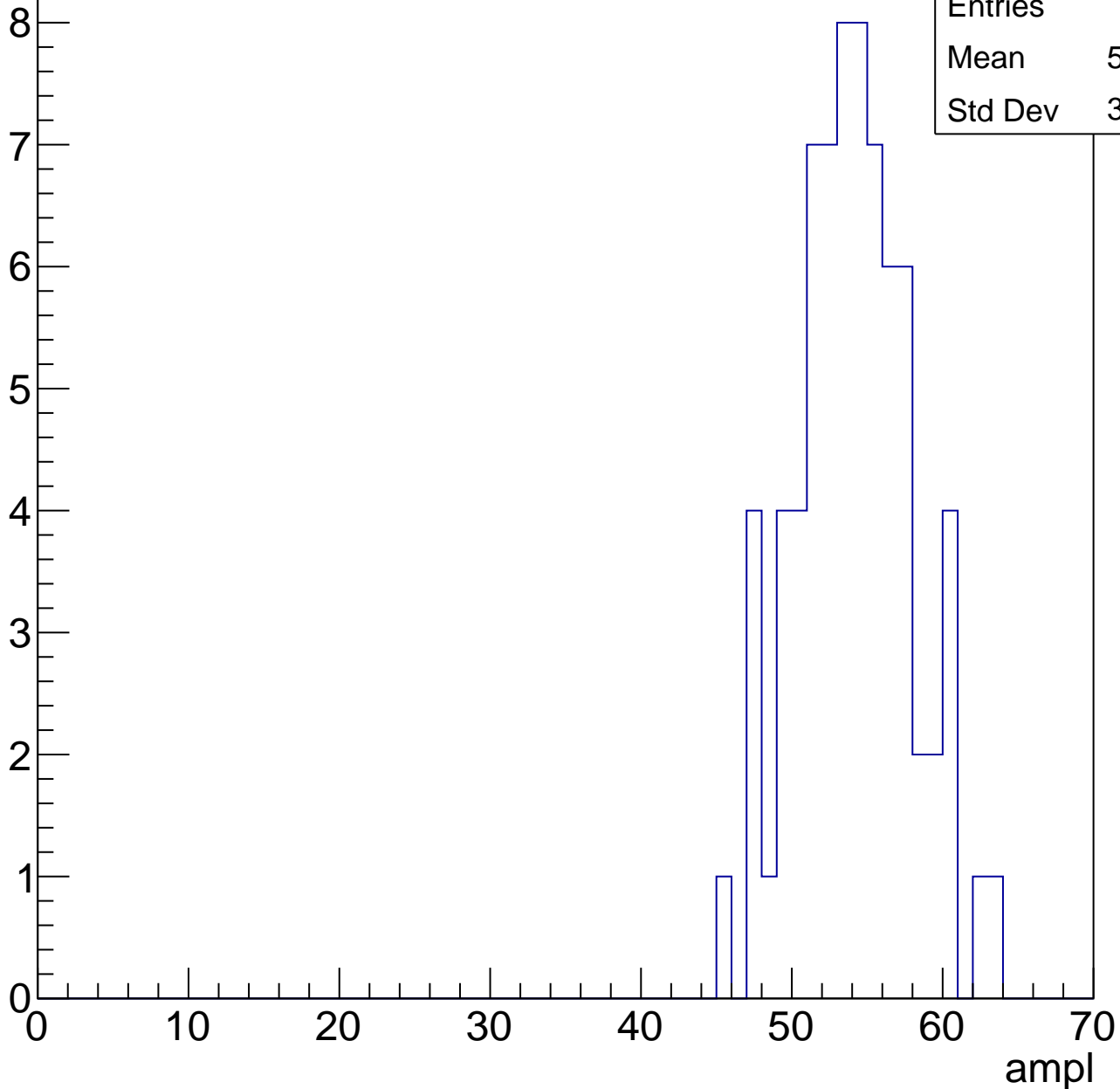


B1L103S, U23-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

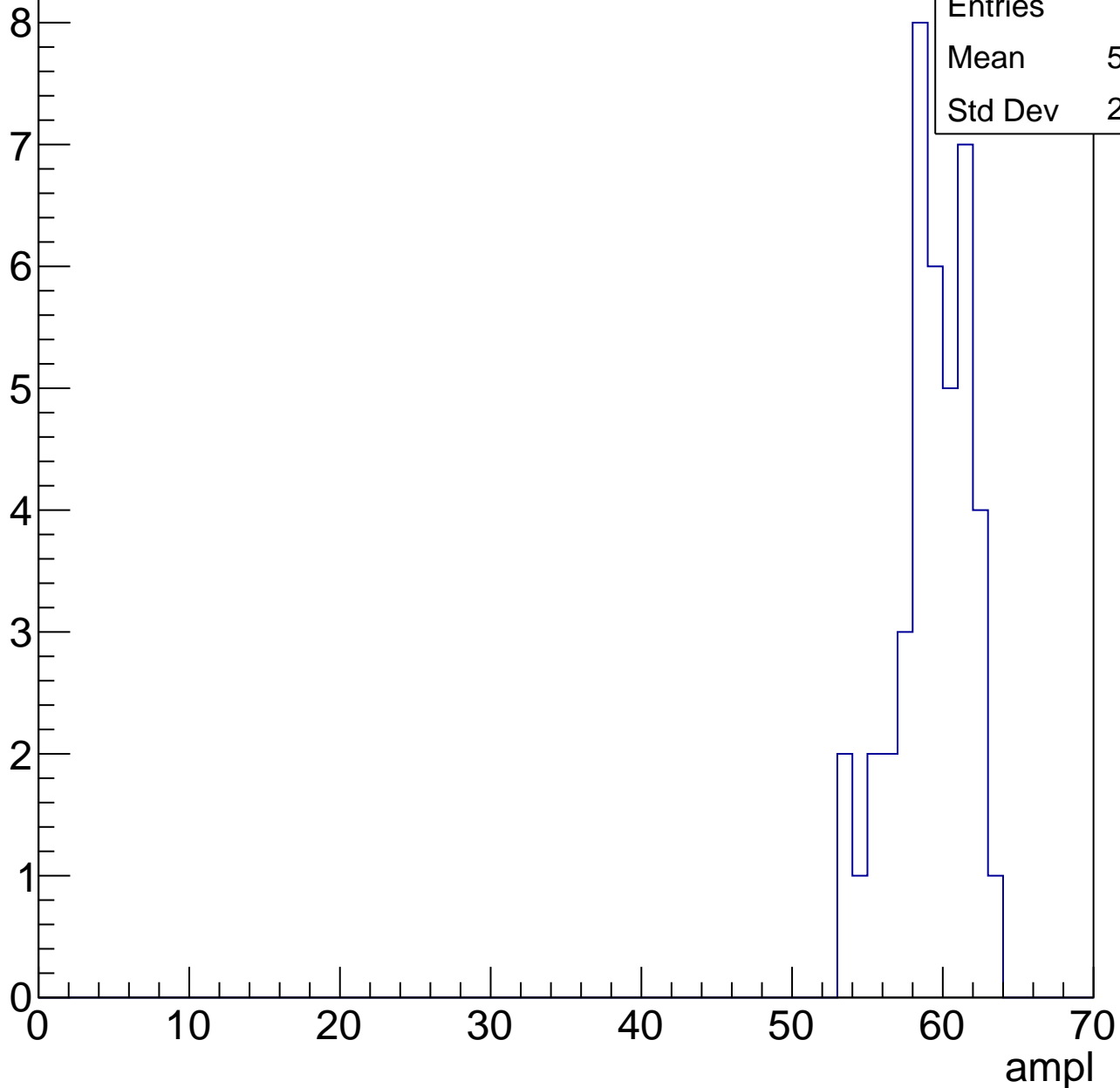
Entries	73
Mean	53.64
Std Dev	3.743



B1L103S, U23-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



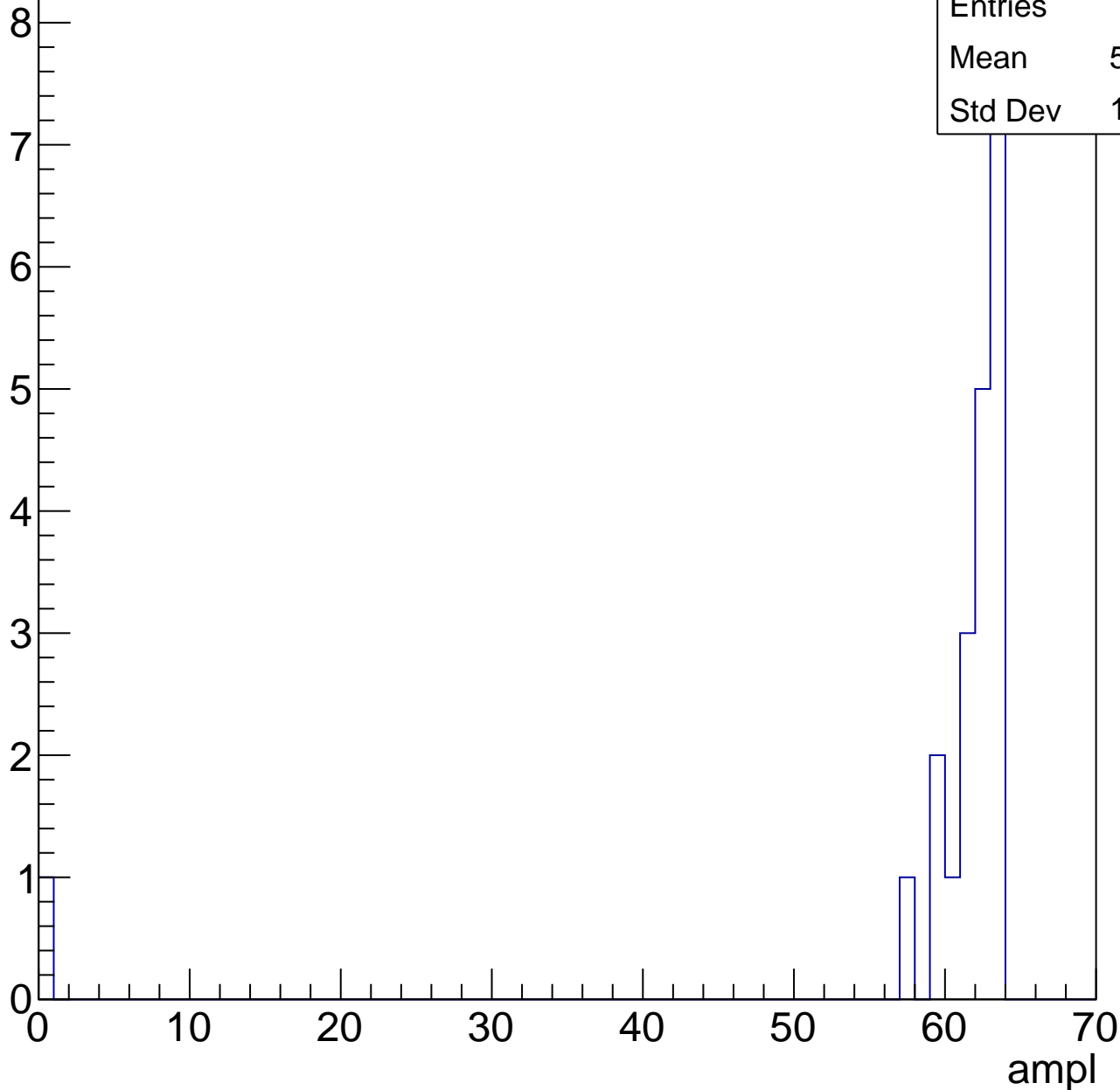
Entries	41
Mean	58.76
Std Dev	2.467

B1L103S, U23-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

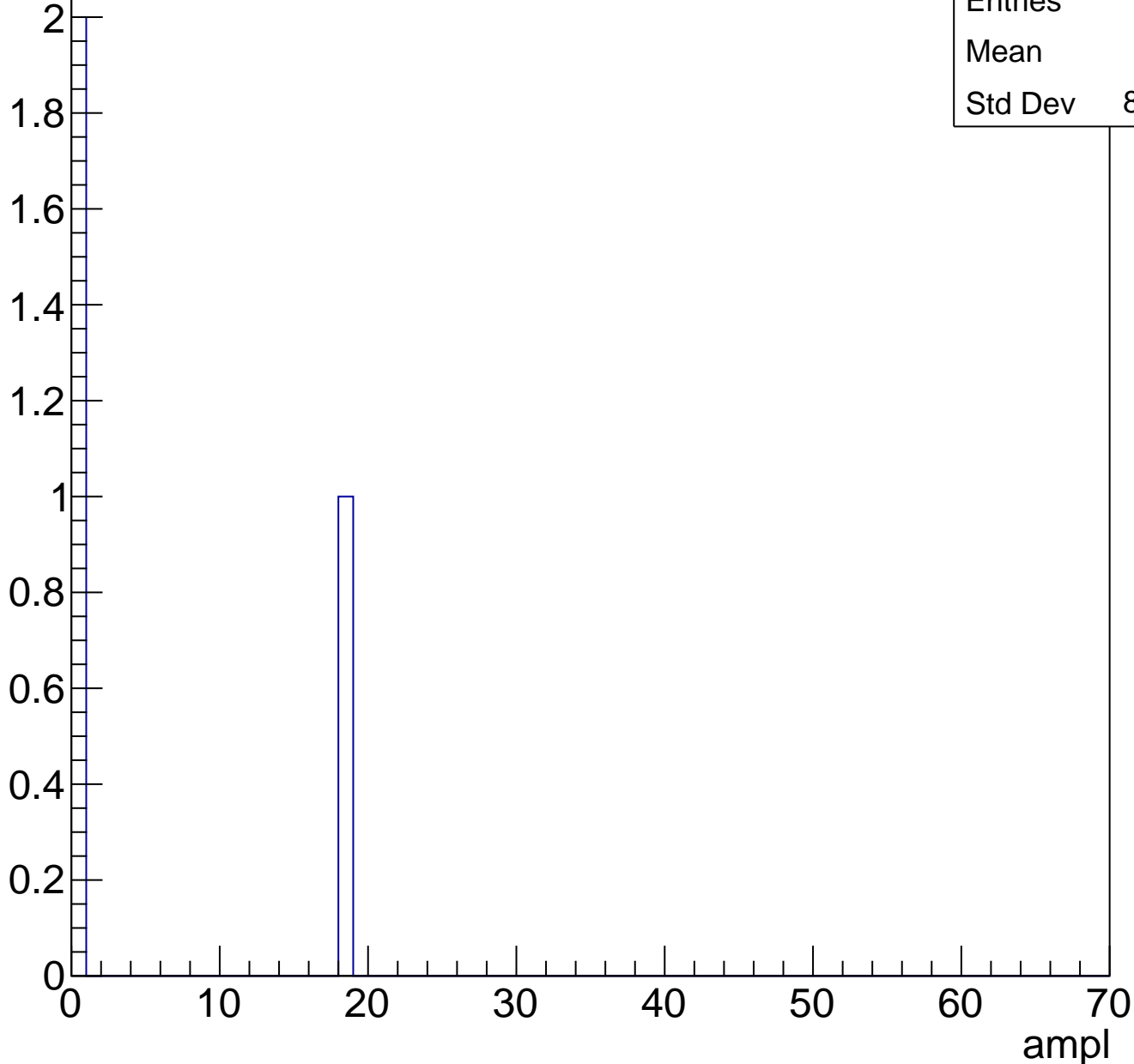
Entries	21
Mean	58.67
Std Dev	13.22



B1L103S, U23-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	6
Std Dev	8.485

B1L103S, U23-ch79, adc0

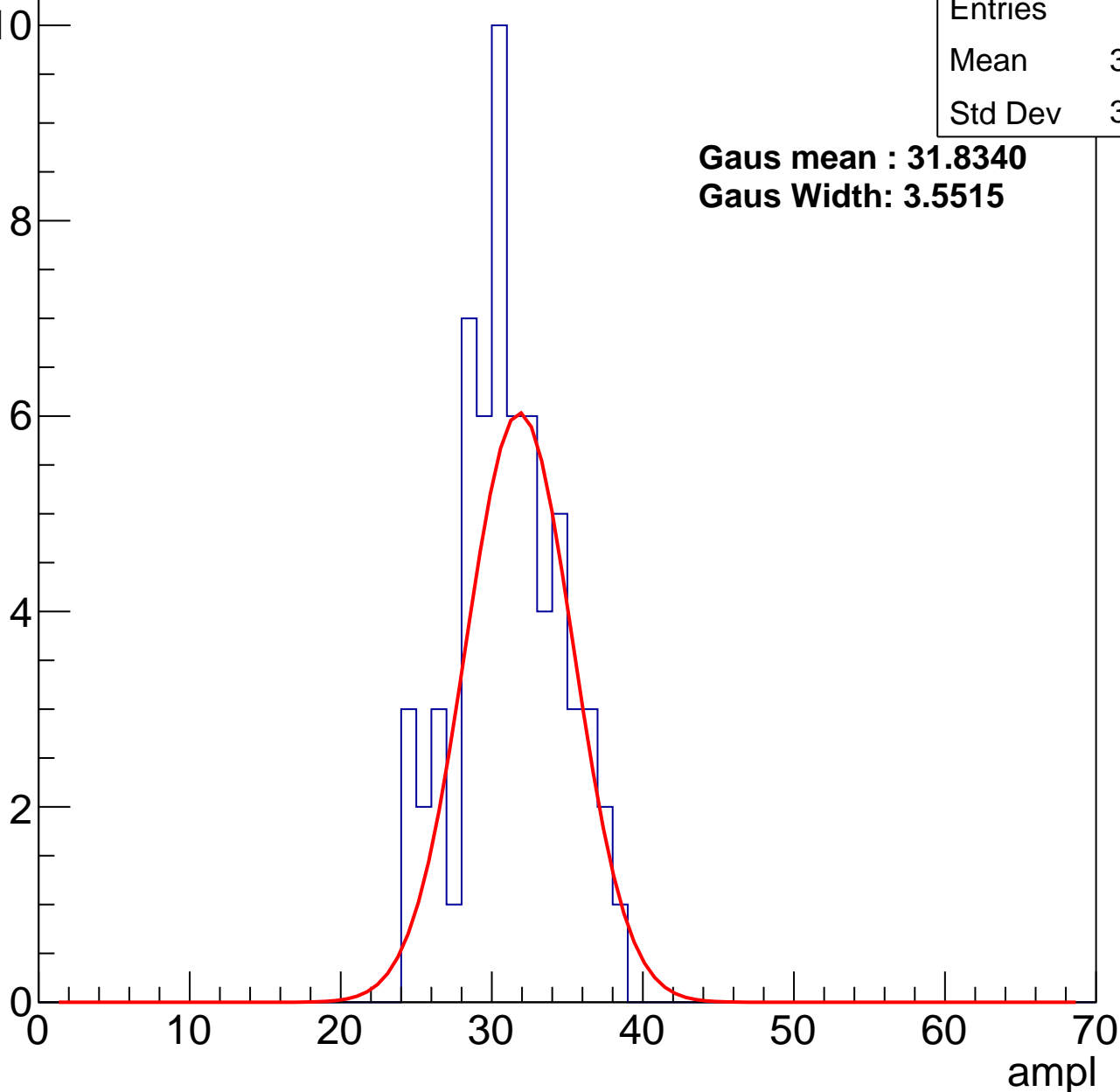
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	30.68
Std Dev	3.388

Gaus mean : 31.8340

Gaus Width: 3.5515



B1L103S, U23-ch79, adc1

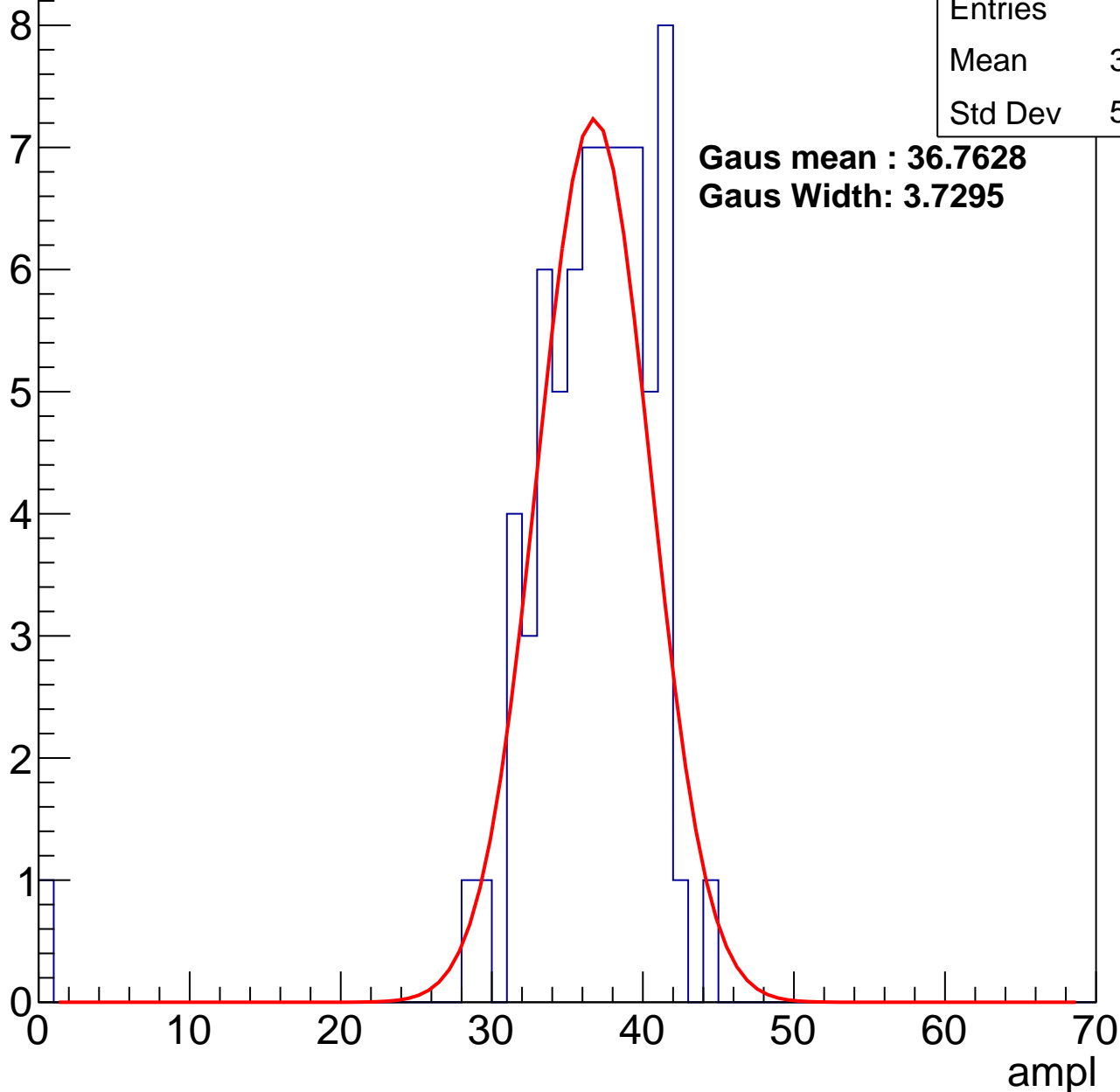
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.99
Std Dev	5.497

Gaus mean : 36.7628

Gaus Width: 3.7295



B1L103S, U23-ch79, adc2

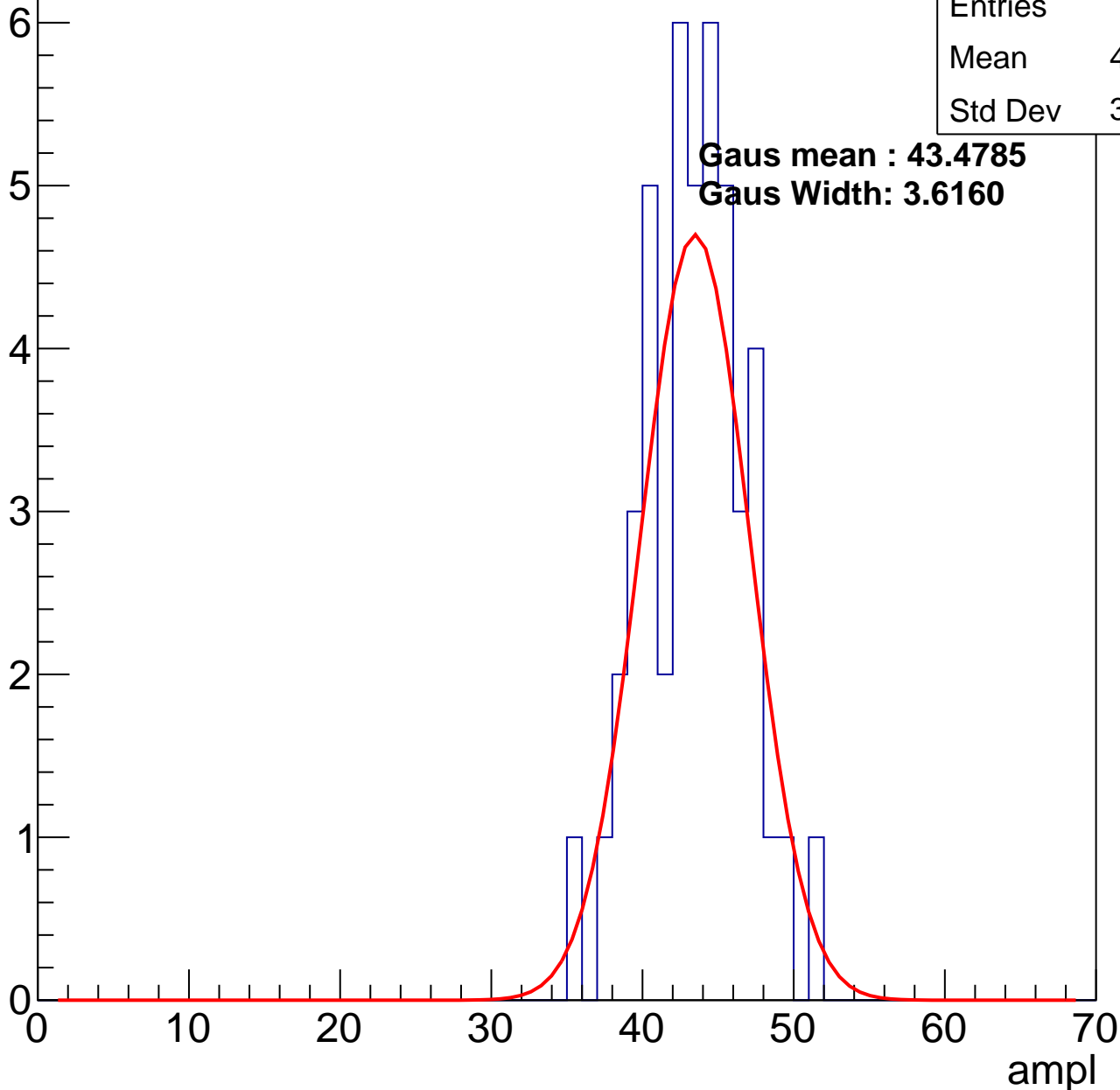
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	42.98
Std Dev	3.294

Gaus mean : 43.4785

Gaus Width: 3.6160

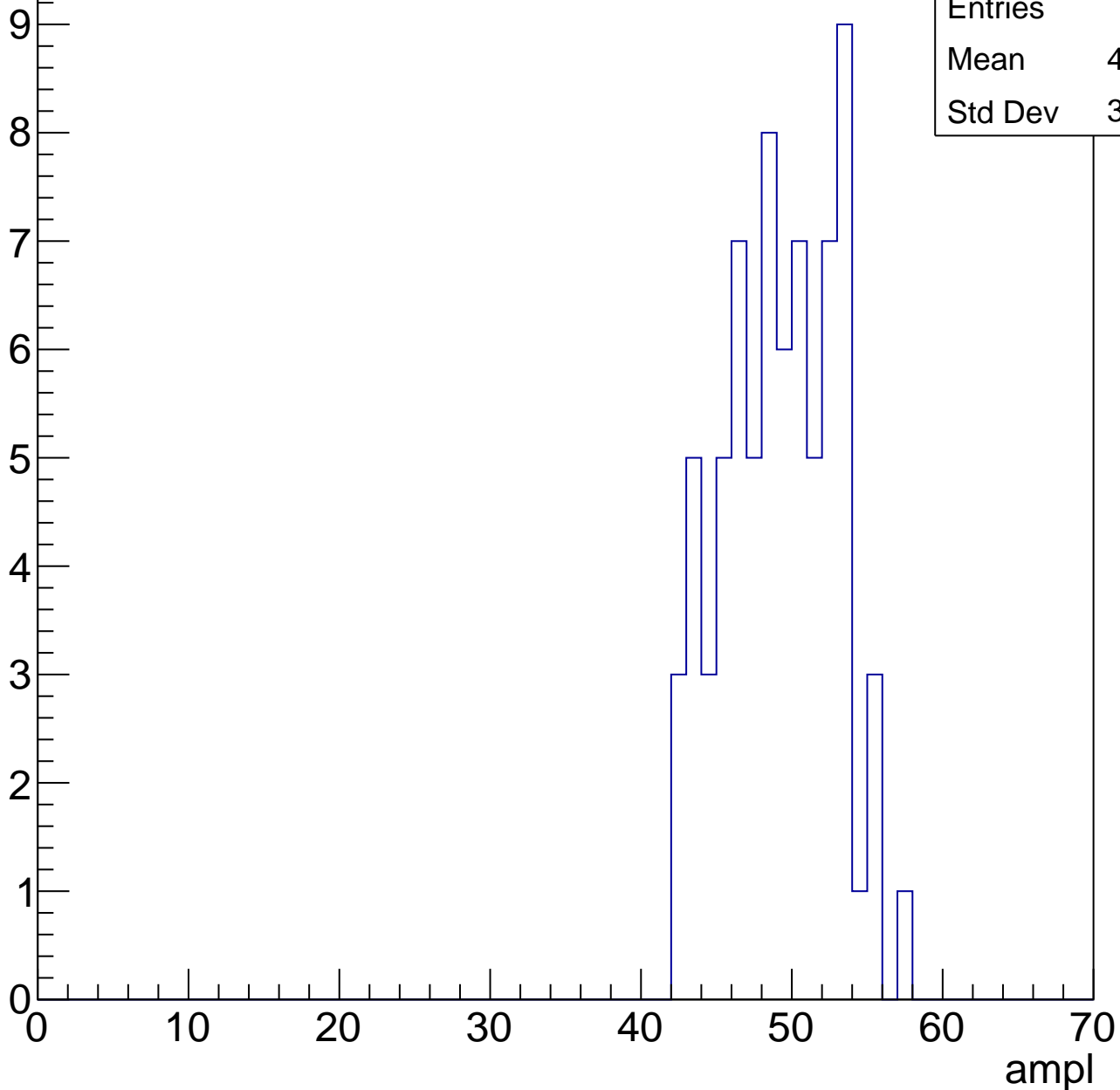


B1L103S, U23-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	48.73
Std Dev	3.649

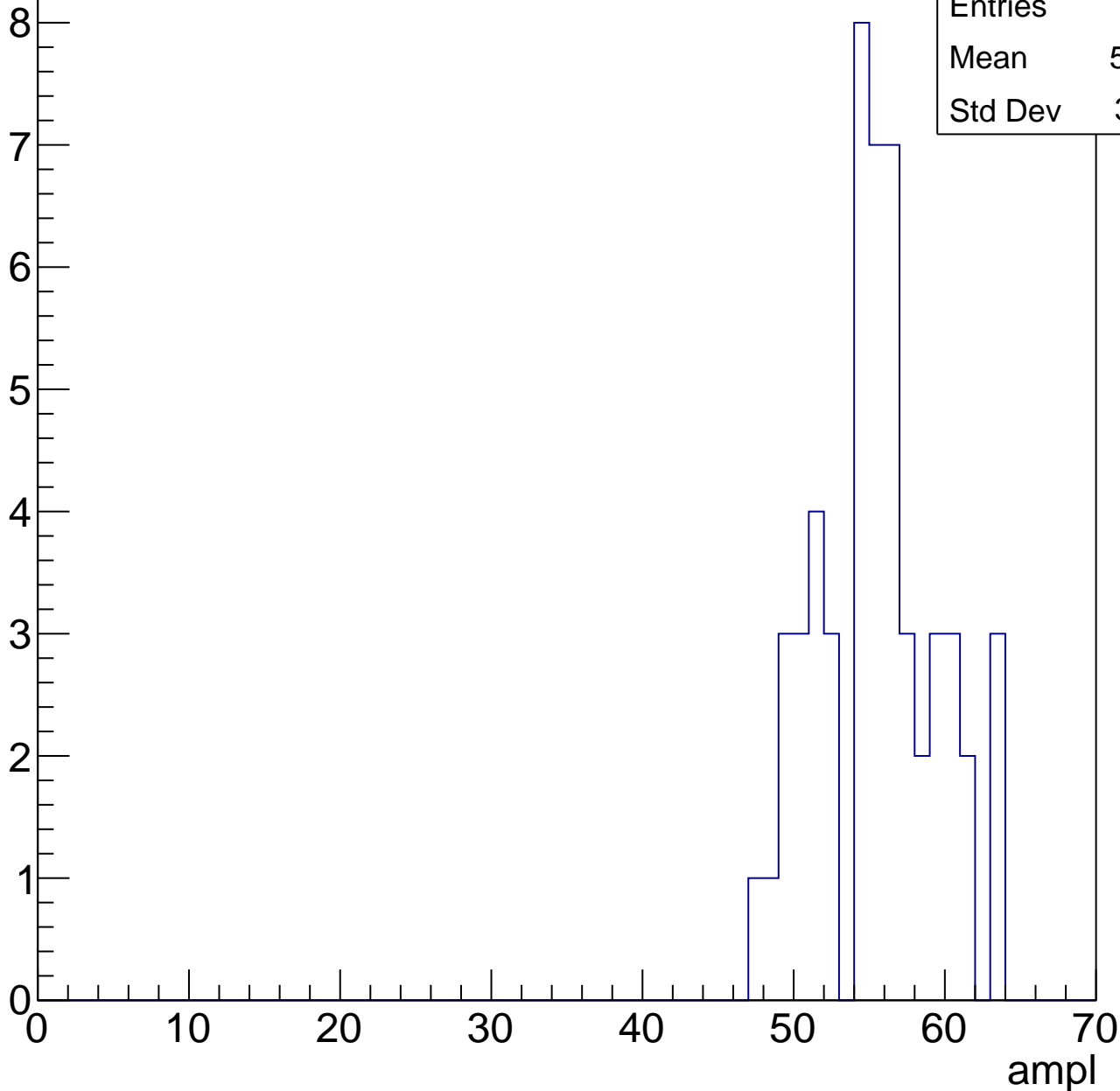


B1L103S, U23-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.02
Std Dev	3.931



B1L103S, U23-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	58
Mean	58.66
Std Dev	8.127

Entry

10

8

6

4

2

0

0

10

20

30

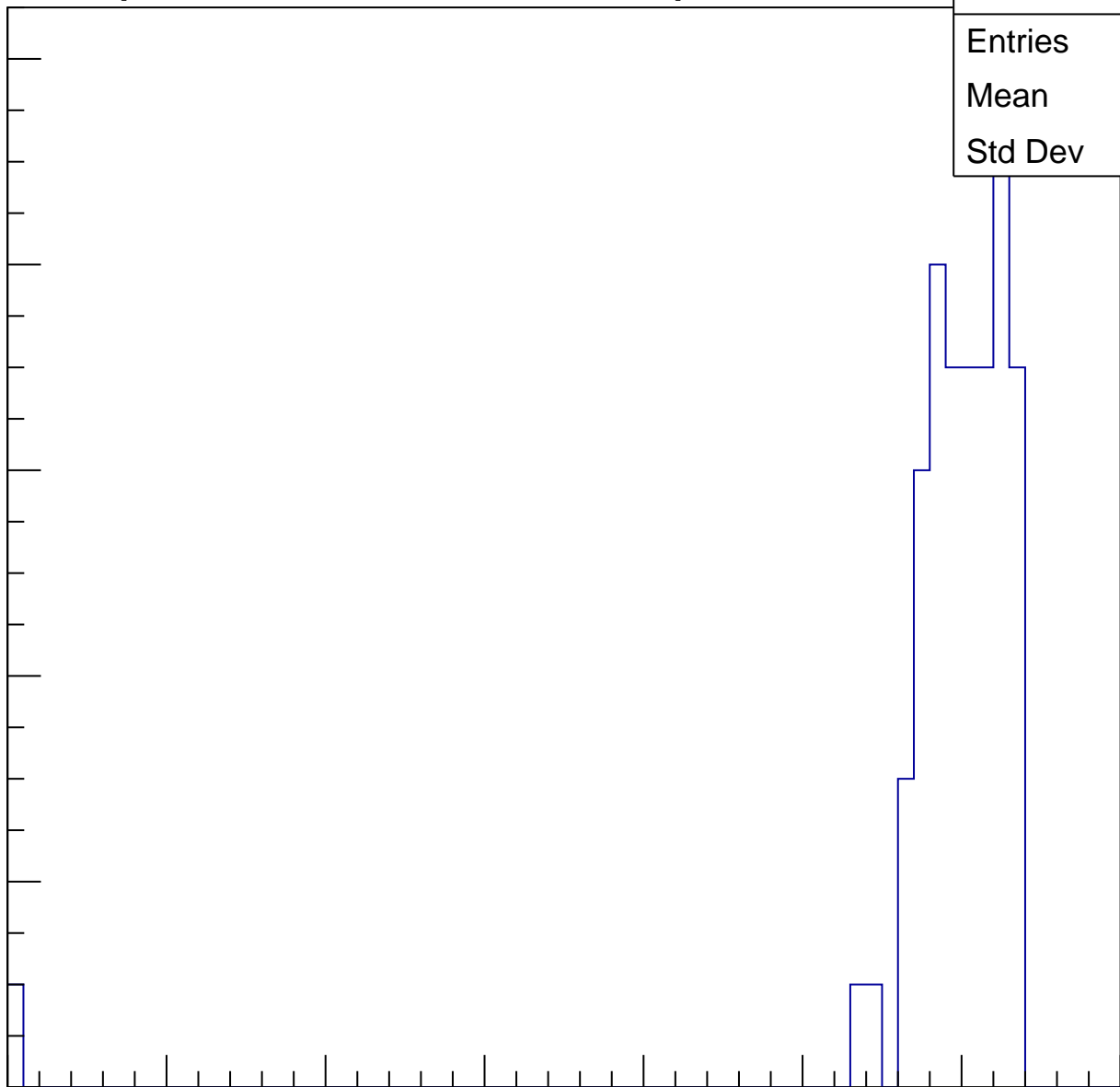
40

50

60

70

ampl



B1L103S, U23-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch80, adc0

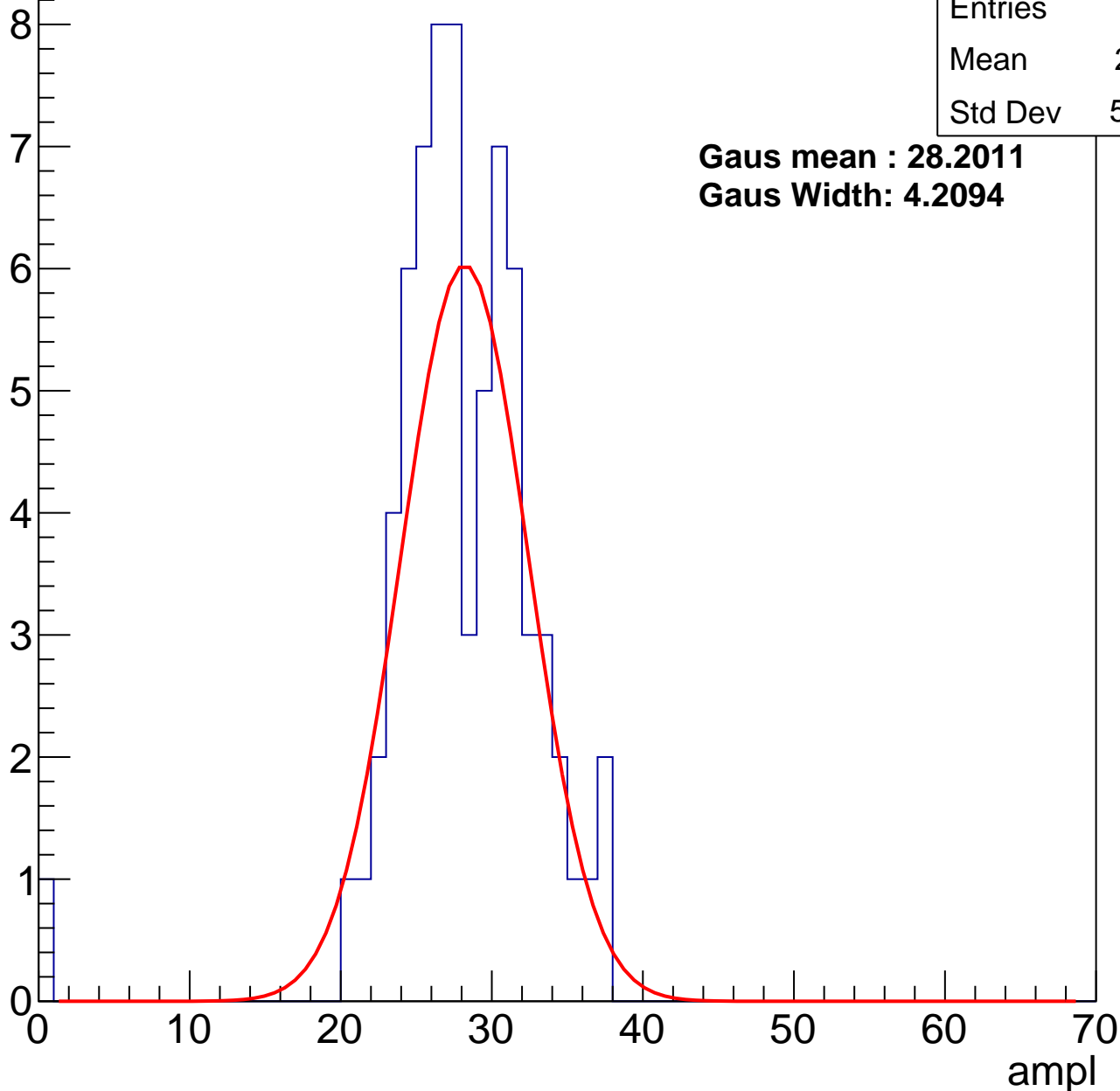
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	27.51
Std Dev	5.046

Gaus mean : 28.2011

Gaus Width: 4.2094



B1L103S, U23-ch80, adc1

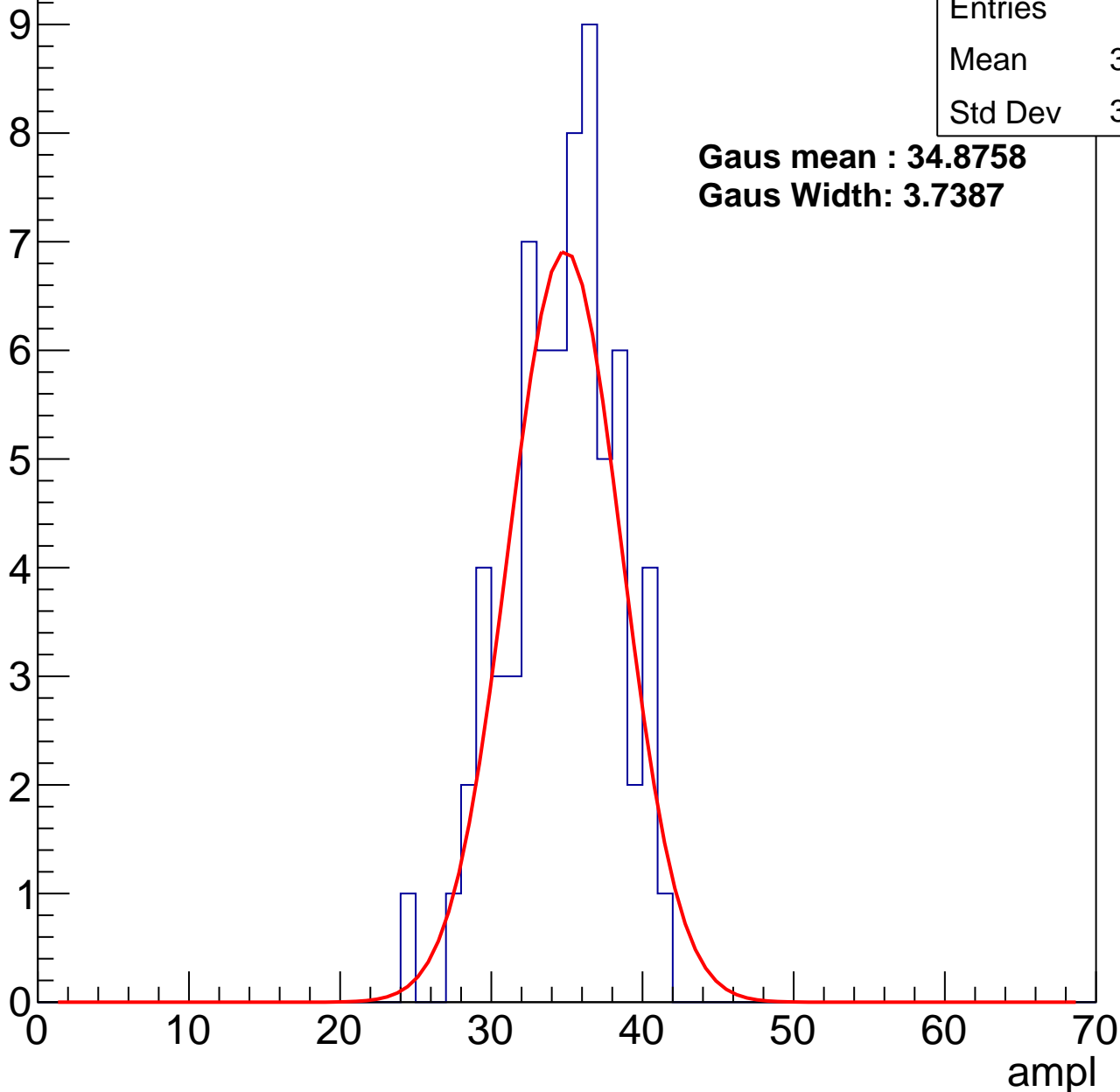
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	34.24
Std Dev	3.544

Gaus mean : 34.8758

Gaus Width: 3.7387



B1L103S, U23-ch80, adc2

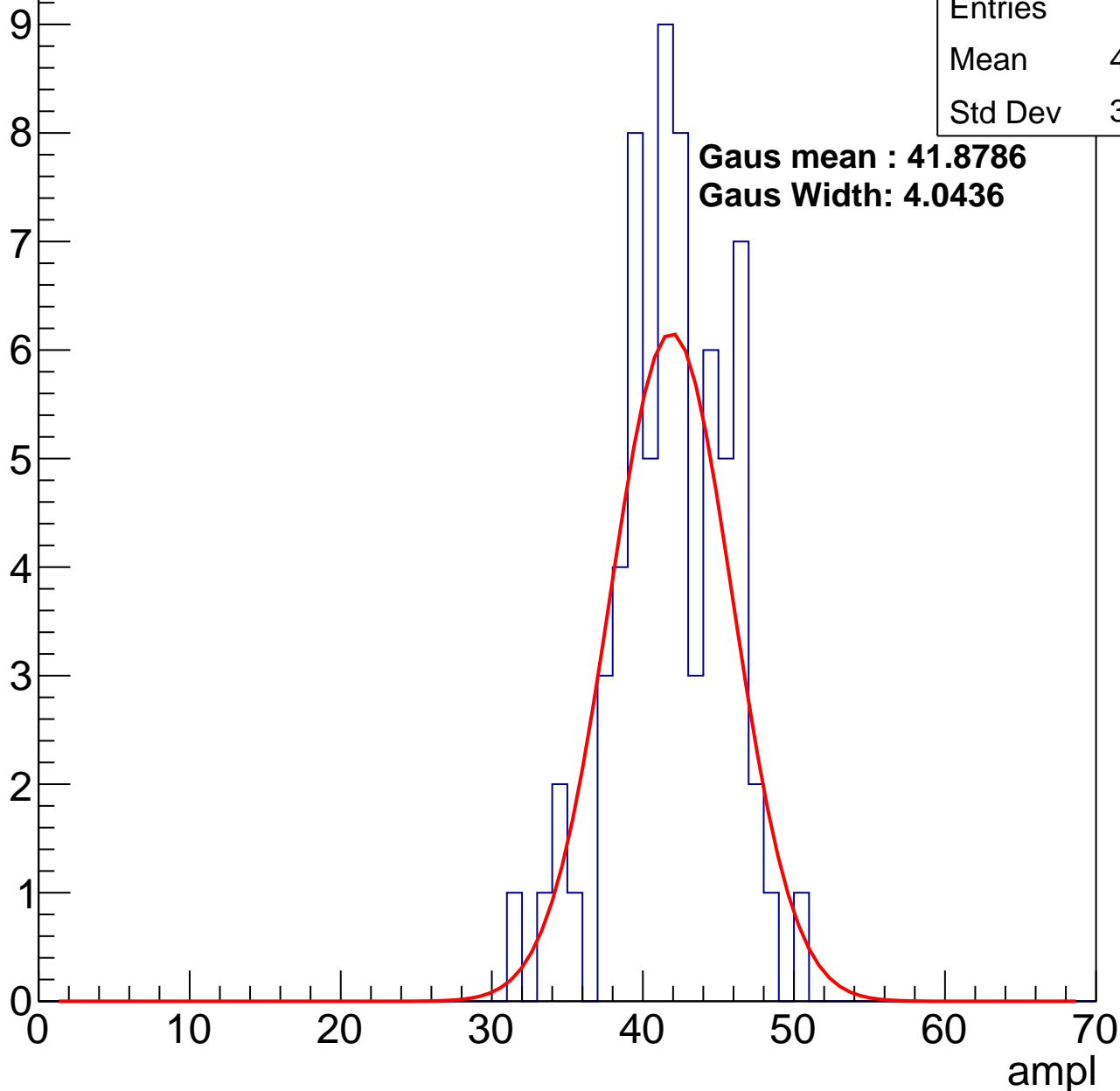
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	41.48
Std Dev	3.735

Gaus mean : 41.8786

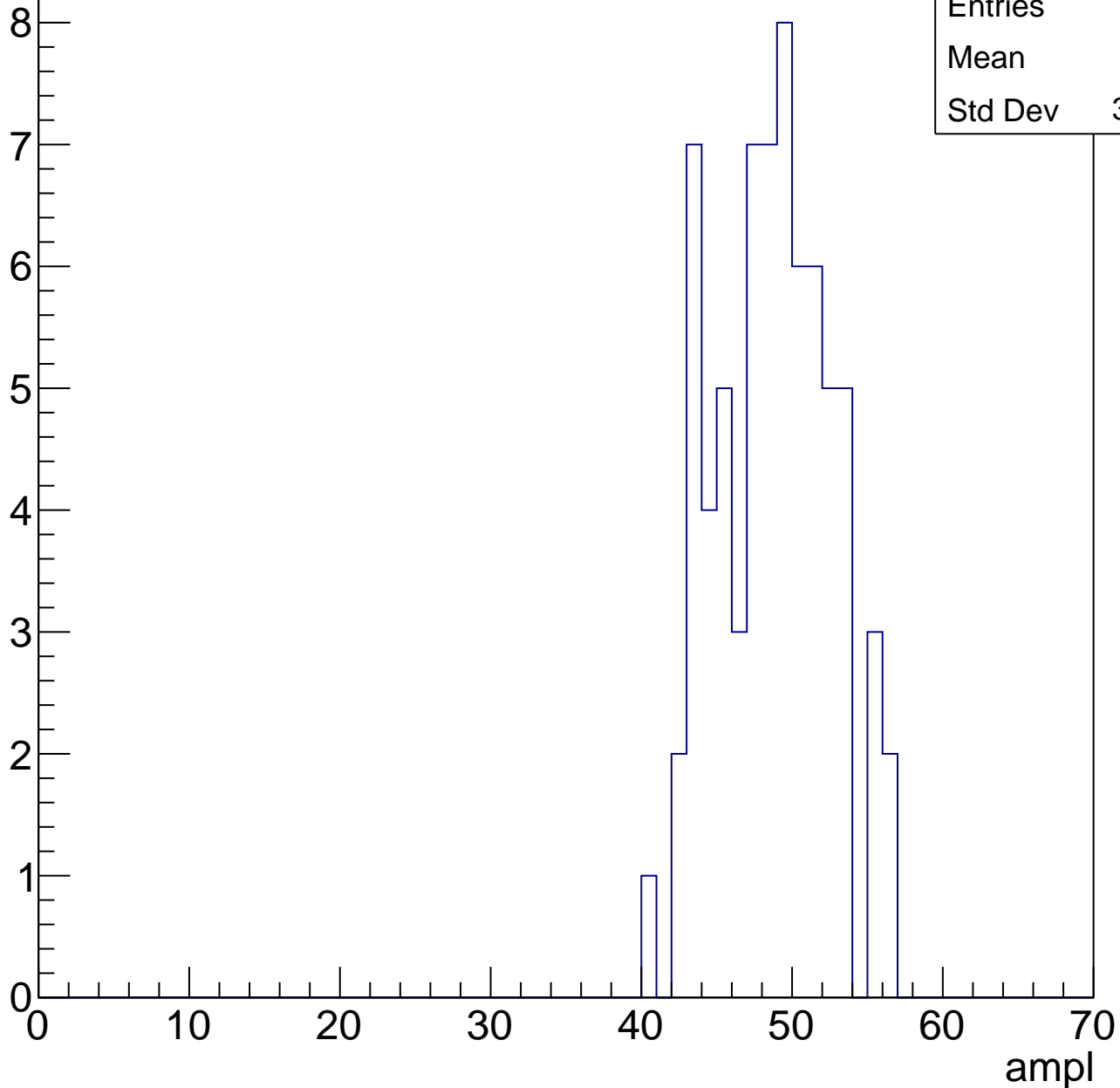
Gaus Width: 4.0436



B1L103S, U23-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



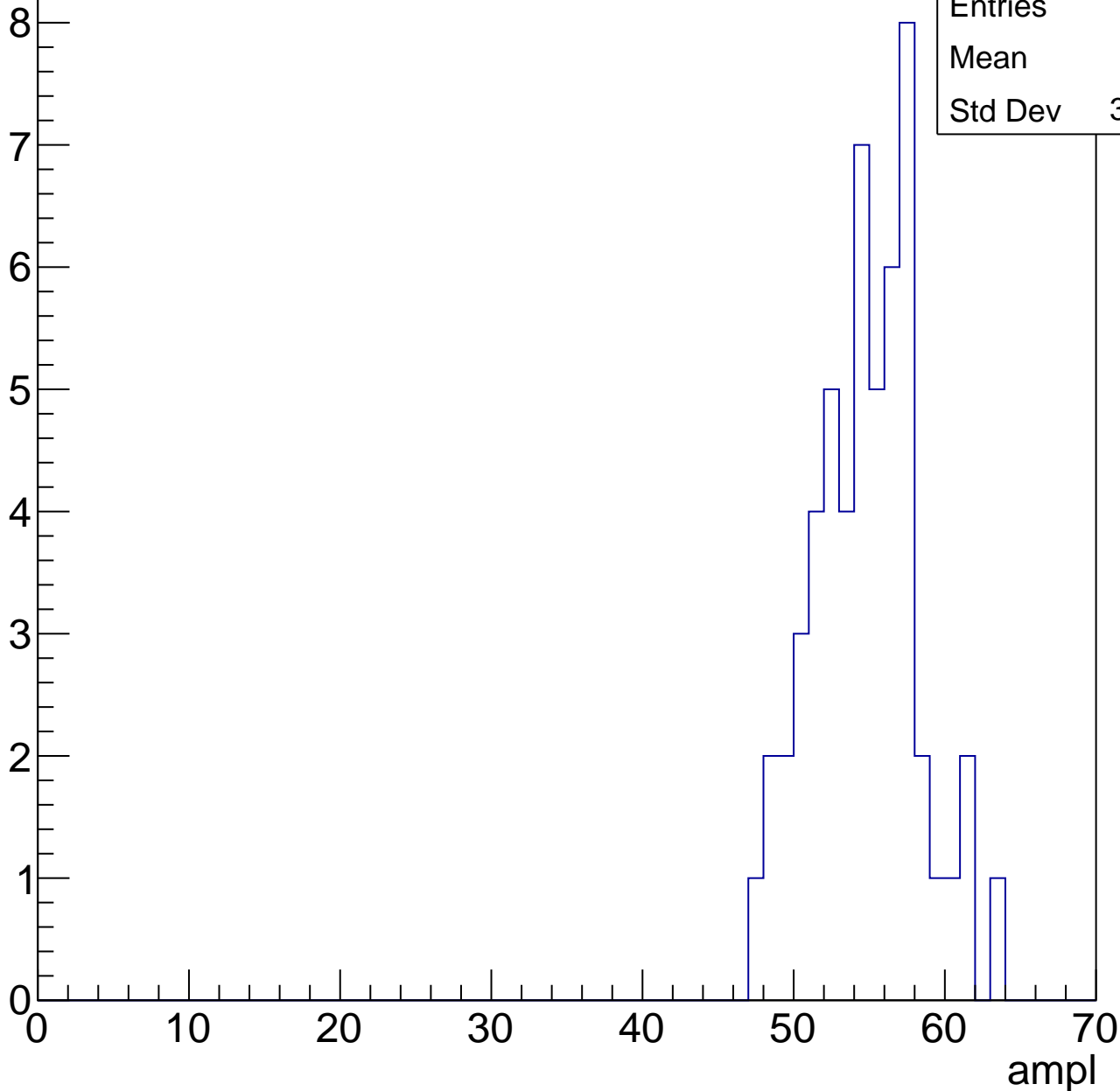
Entries	71
Mean	48.3
Std Dev	3.751

B1L103S, U23-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

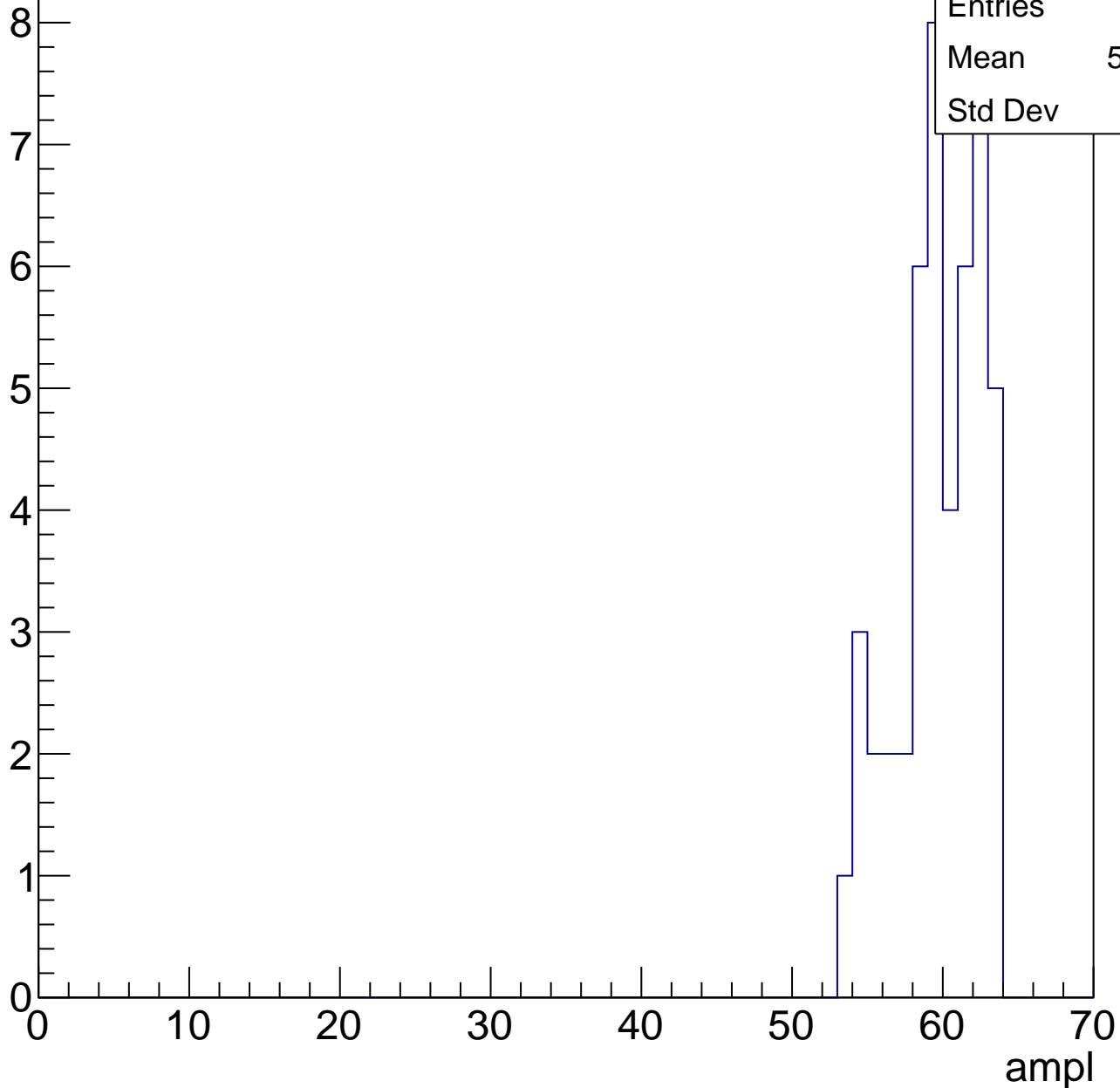
Entries	54
Mean	54.3
Std Dev	3.462



B1L103S, U23-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

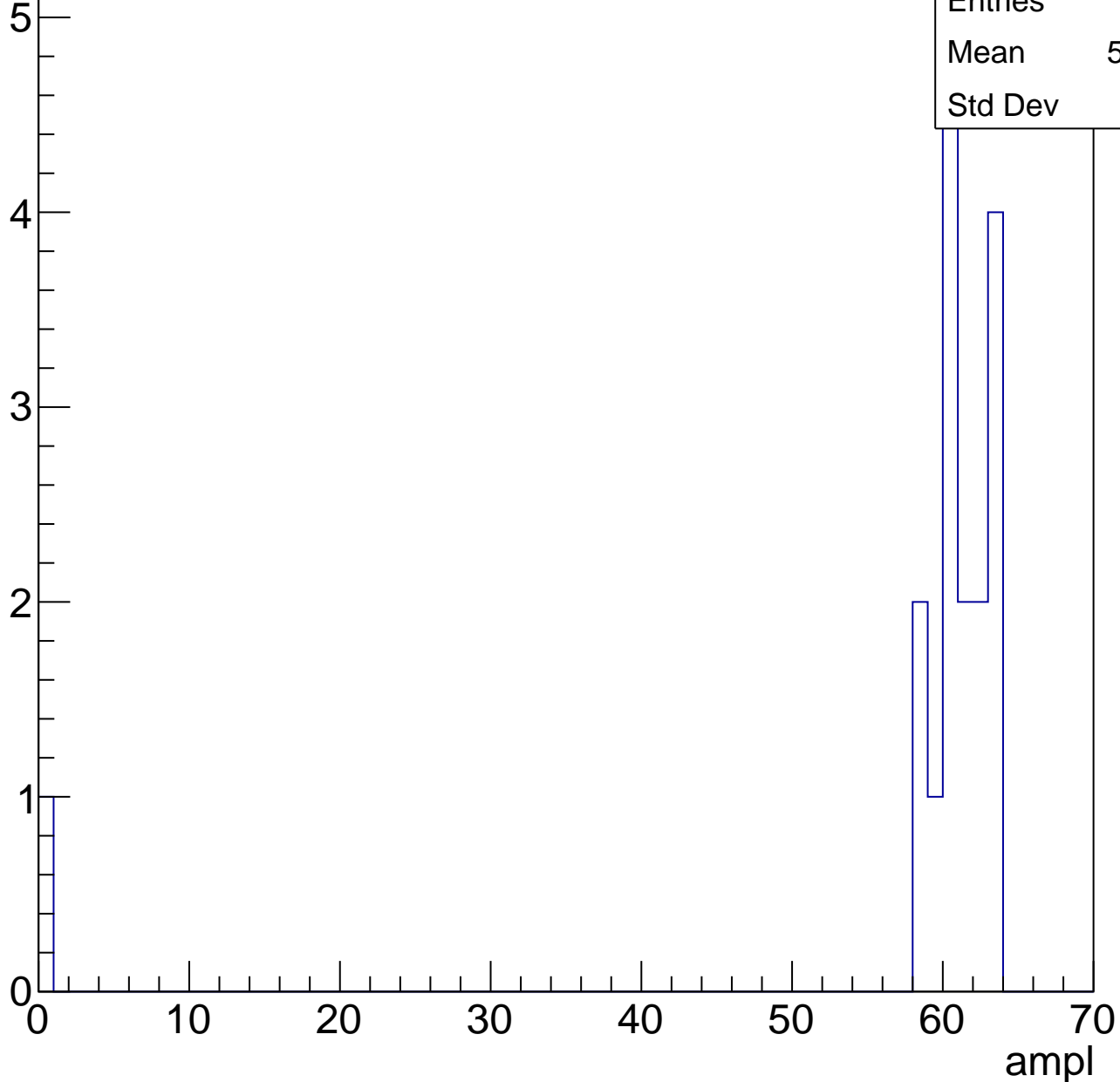
Entry



B1L103S, U23-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

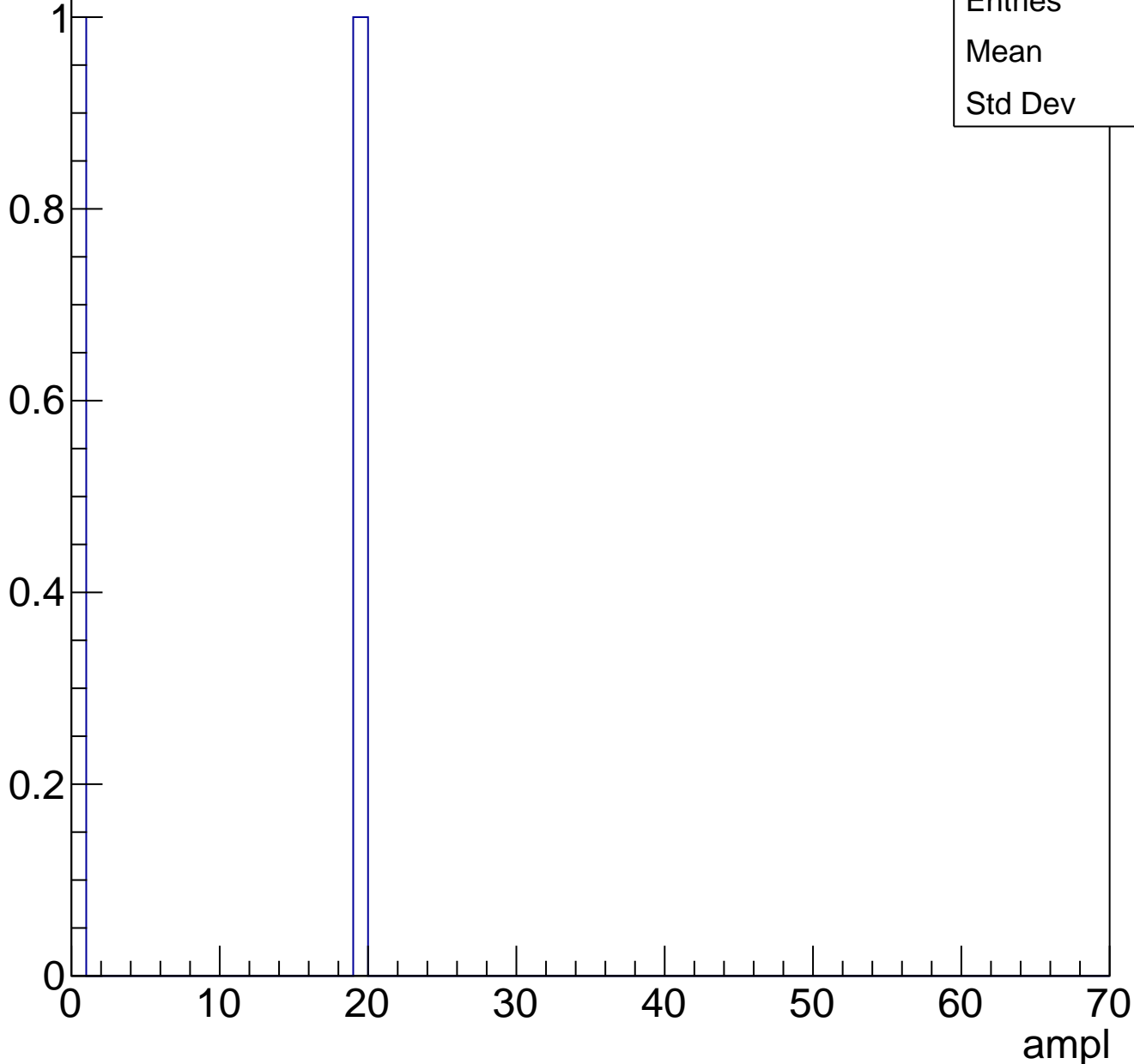
Entry



B1L103S, U23-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L103S, U23-ch81, adc0

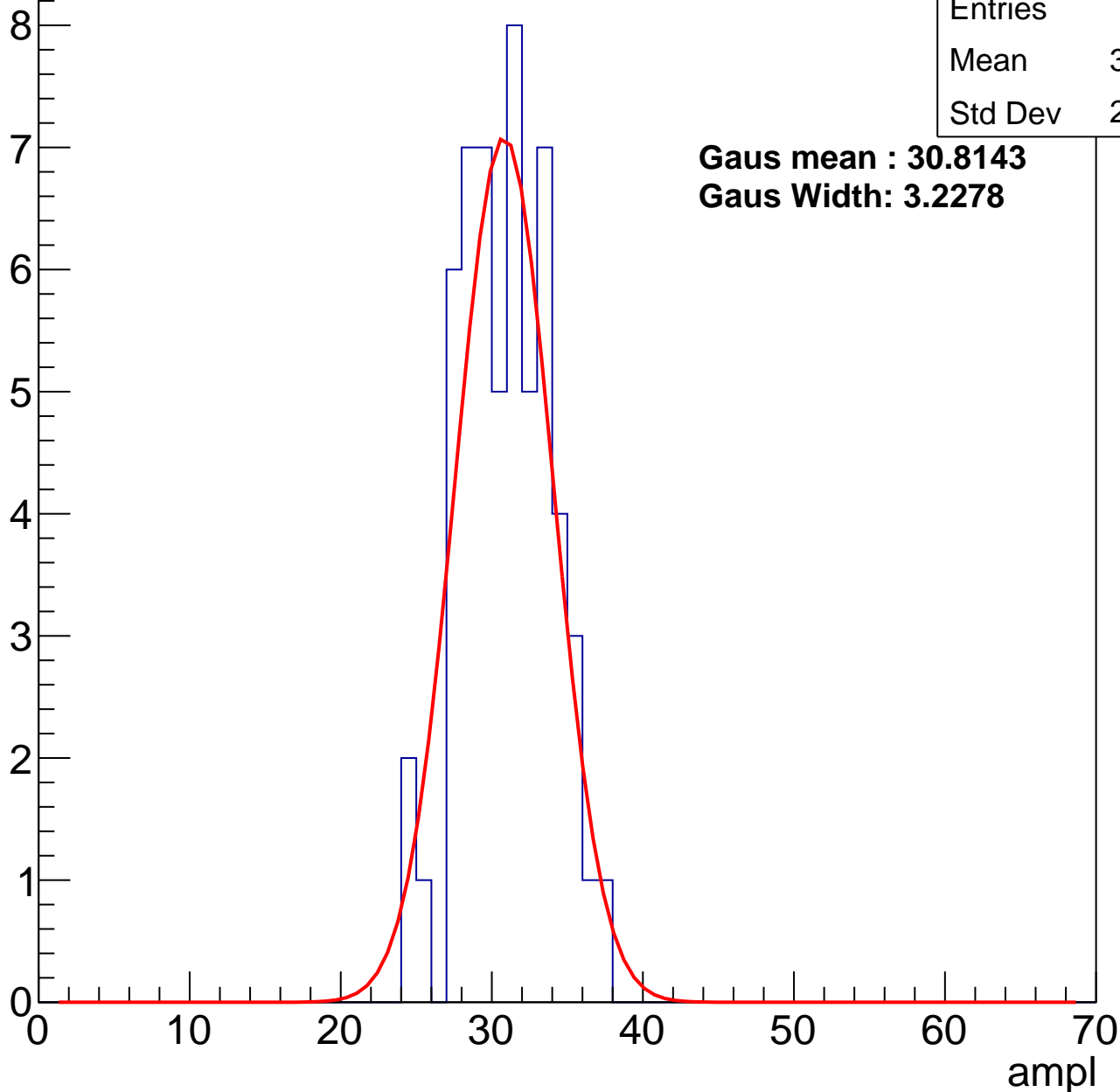
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	30.47
Std Dev	2.927

Gaus mean : 30.8143

Gaus Width: 3.2278



B1L103S, U23-ch81, adc1

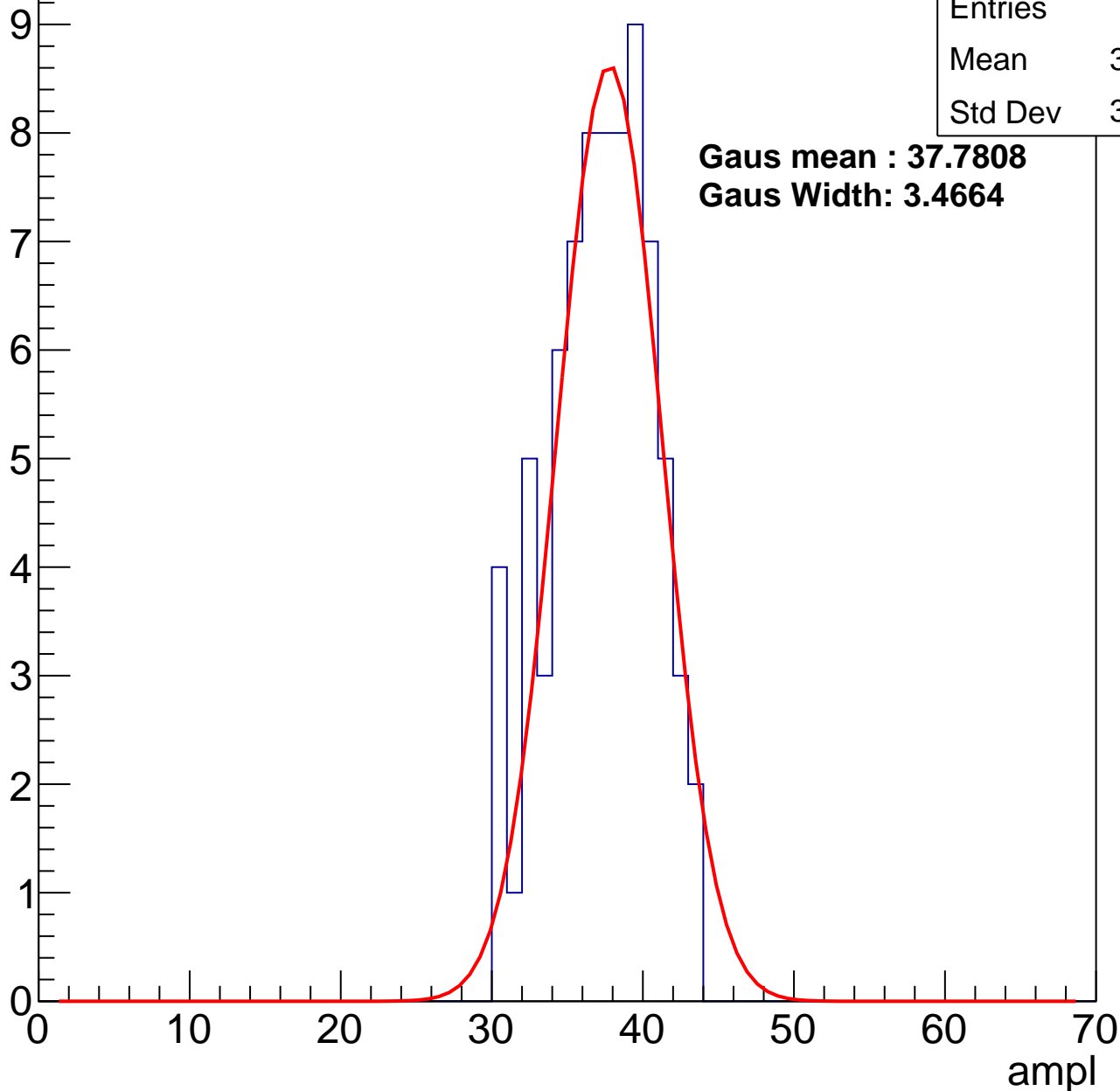
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.78
Std Dev	3.295

Gaus mean : 37.7808

Gaus Width: 3.4664



B1L103S, U23-ch81, adc2

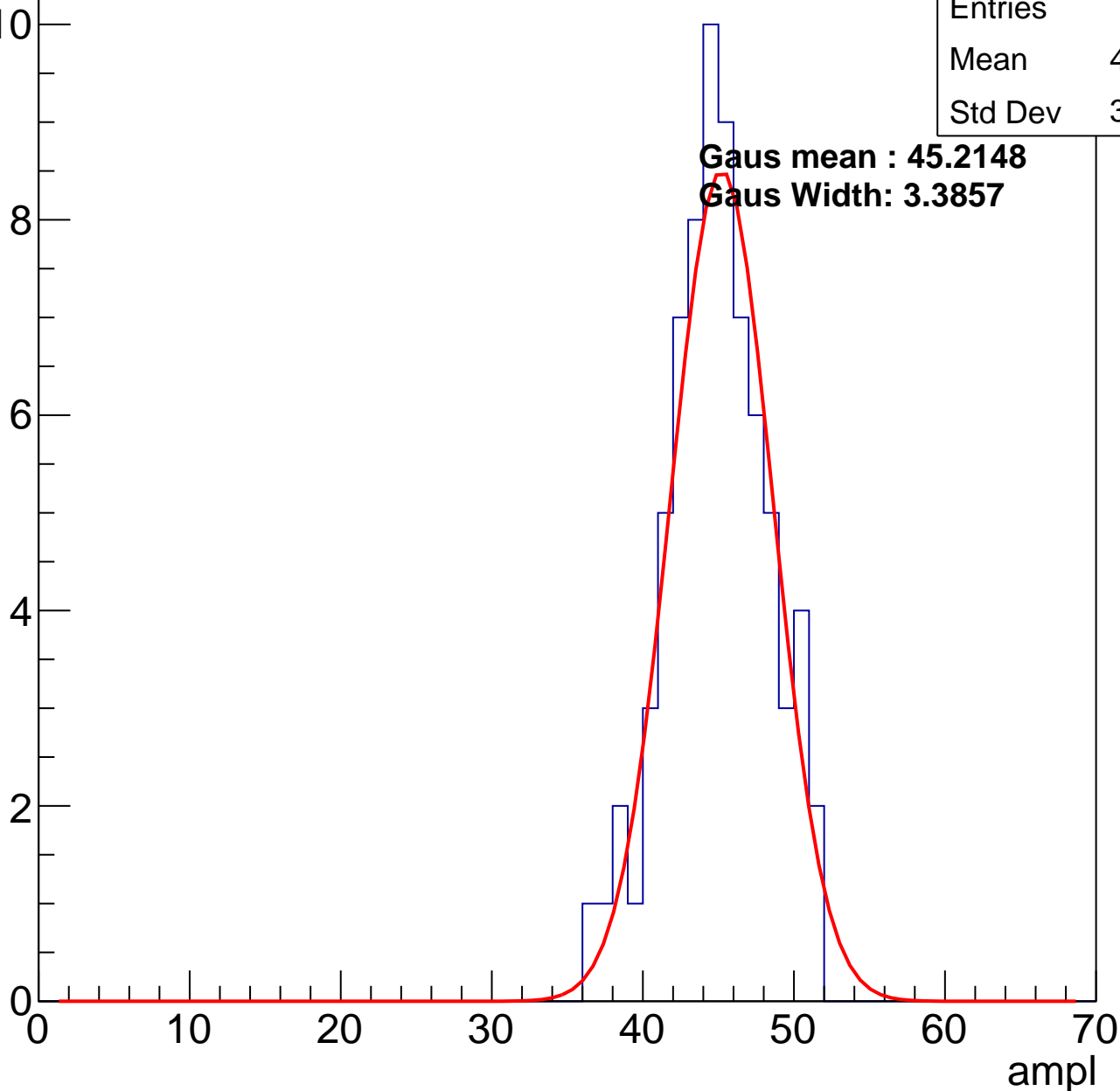
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	44.45
Std Dev	3.313

Gaus mean : 45.2148

Gaus Width: 3.3857

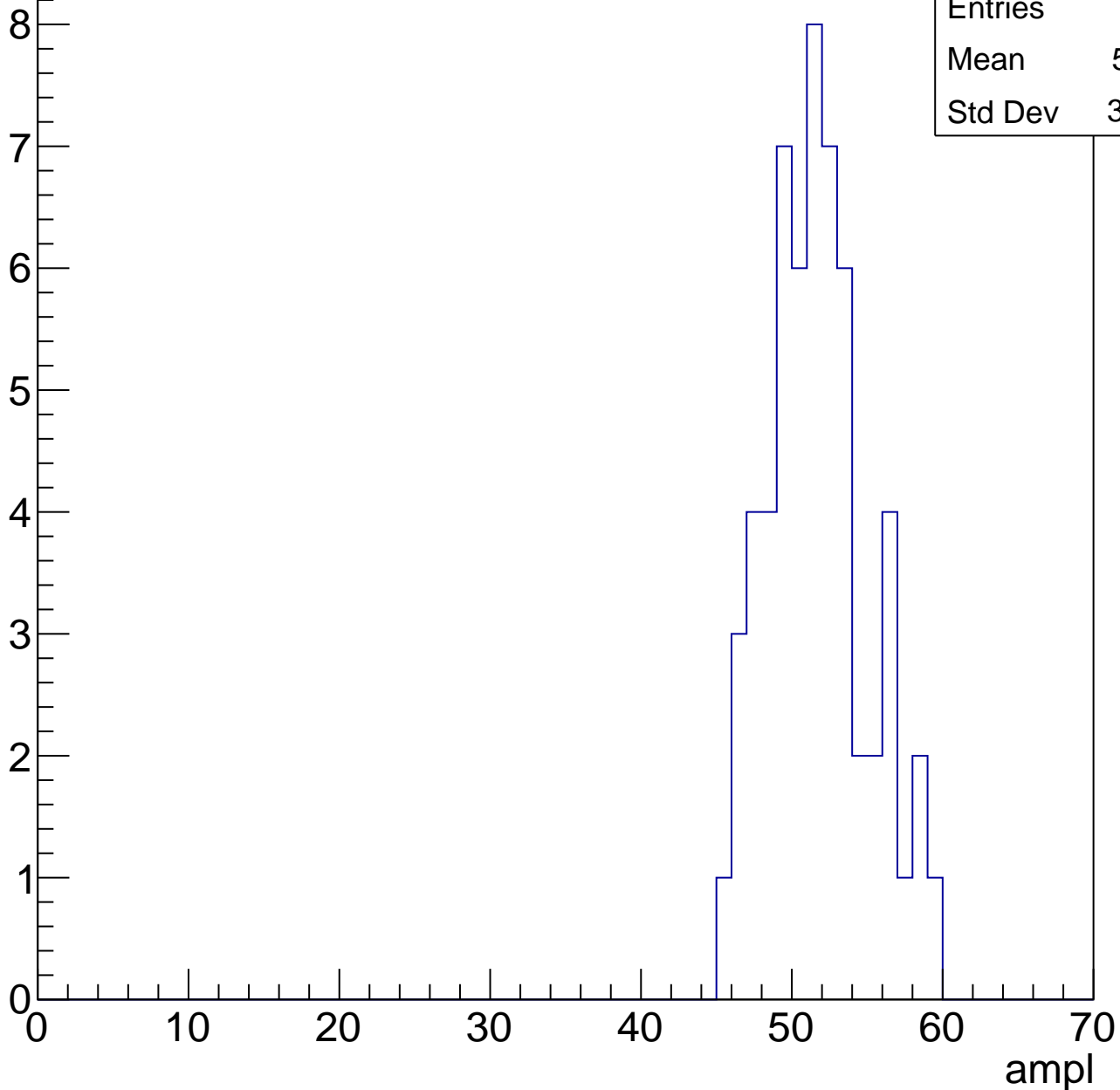


B1L103S, U23-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	51.21
Std Dev	3.274

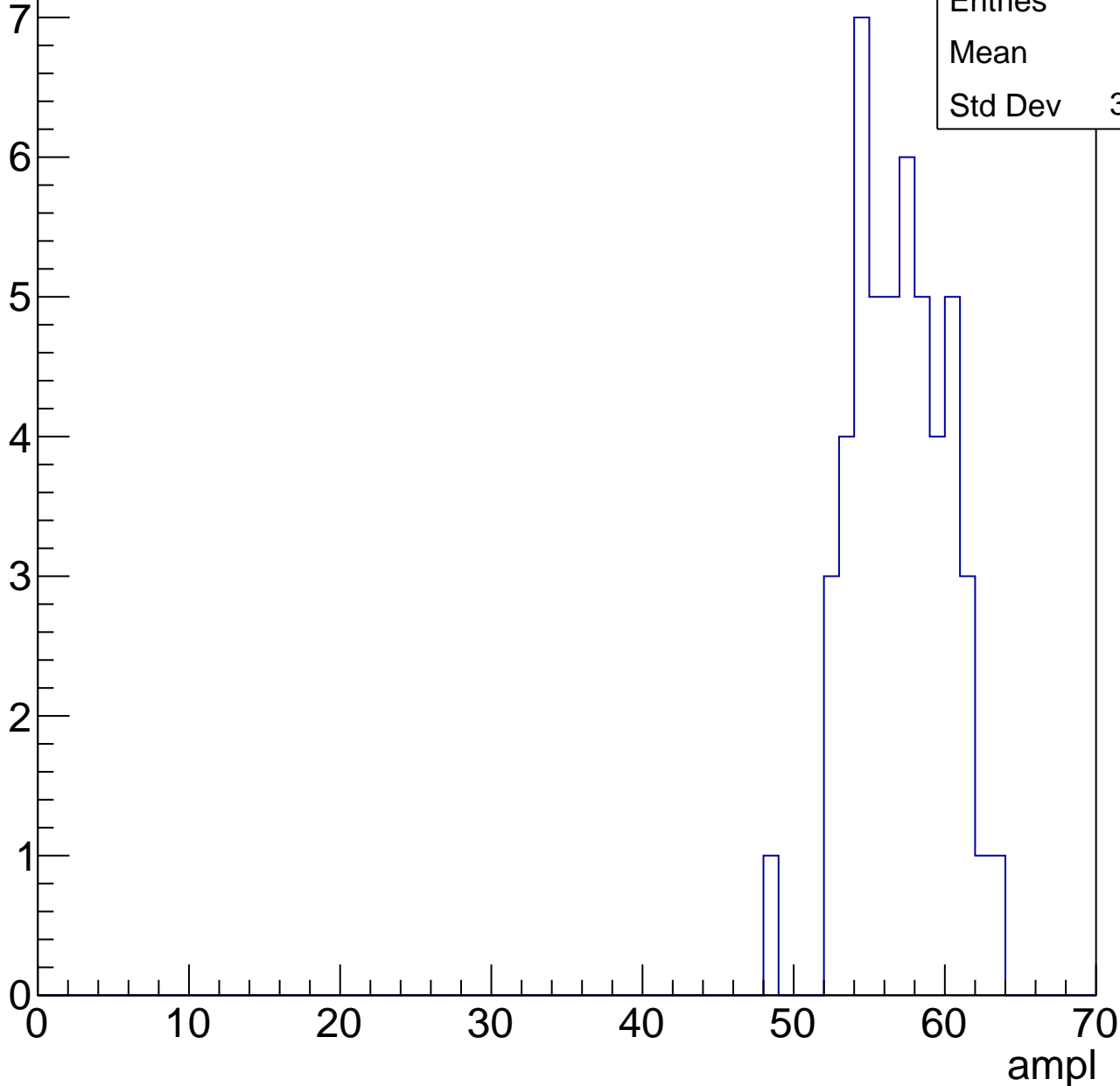


B1L103S, U23-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	56.5
Std Dev	3.068

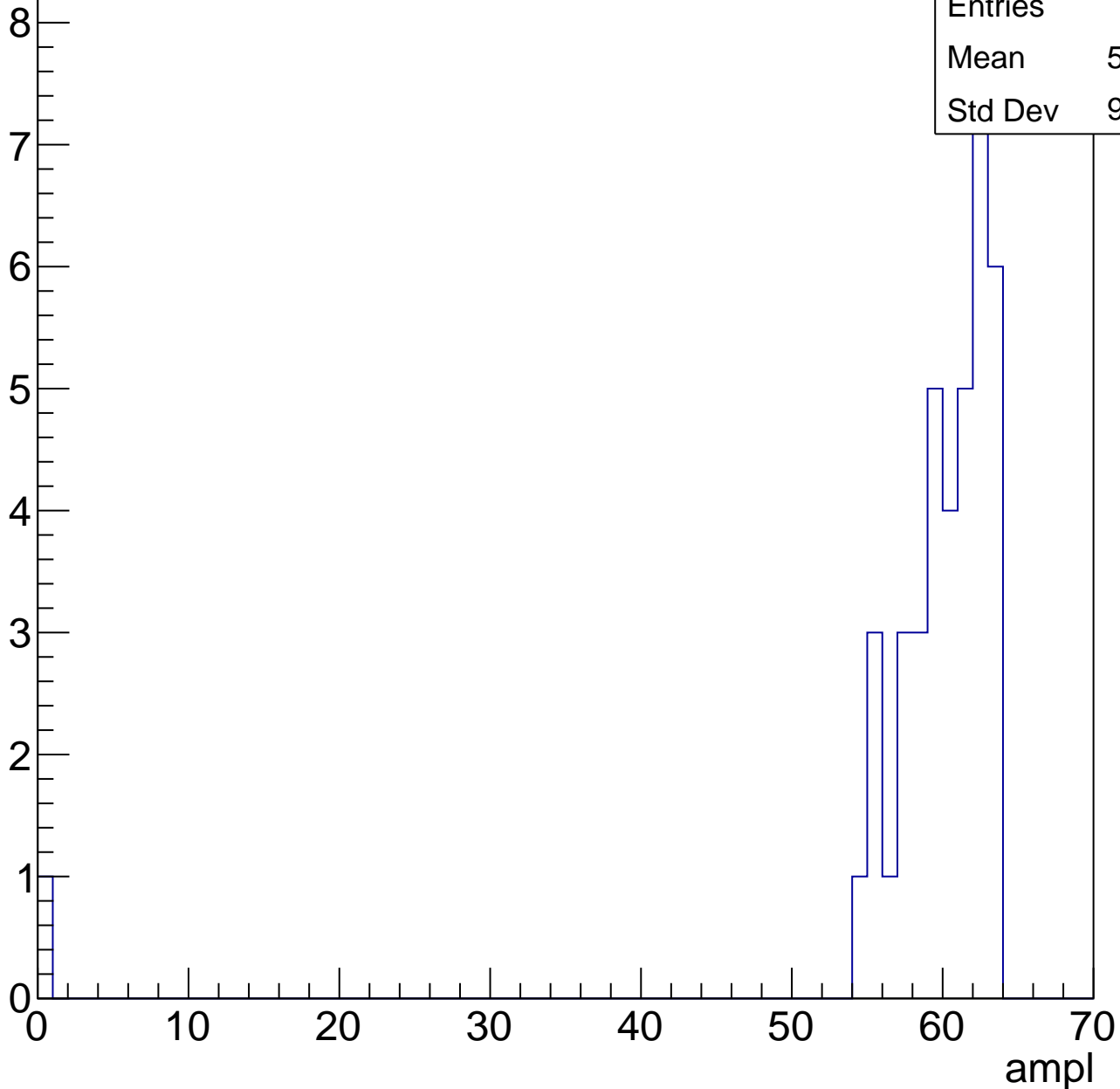


B1L103S, U23-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

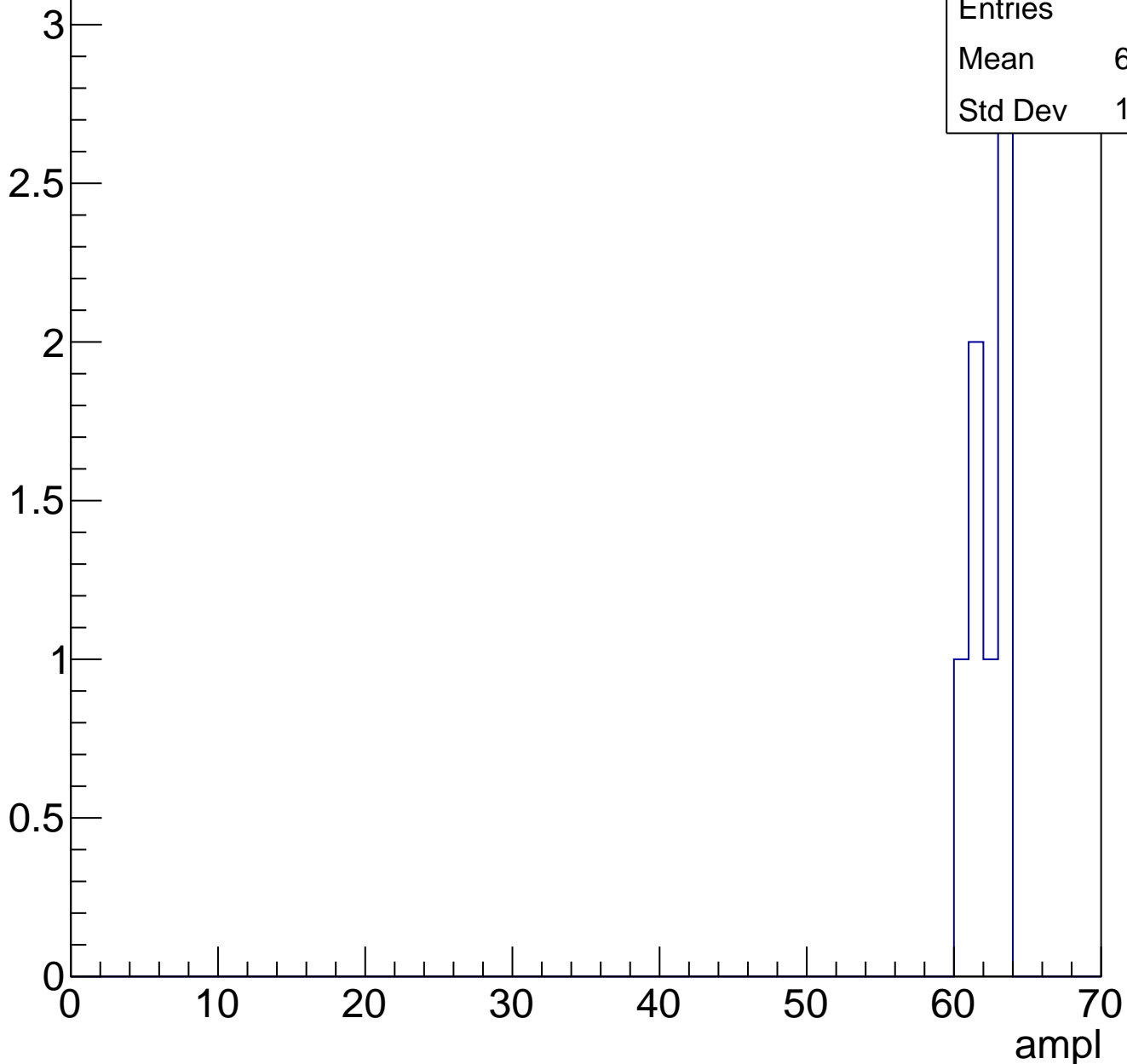
Entries	40
Mean	58.35
Std Dev	9.686



B1L103S, U23-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch82, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	25.35
Std Dev	3.649

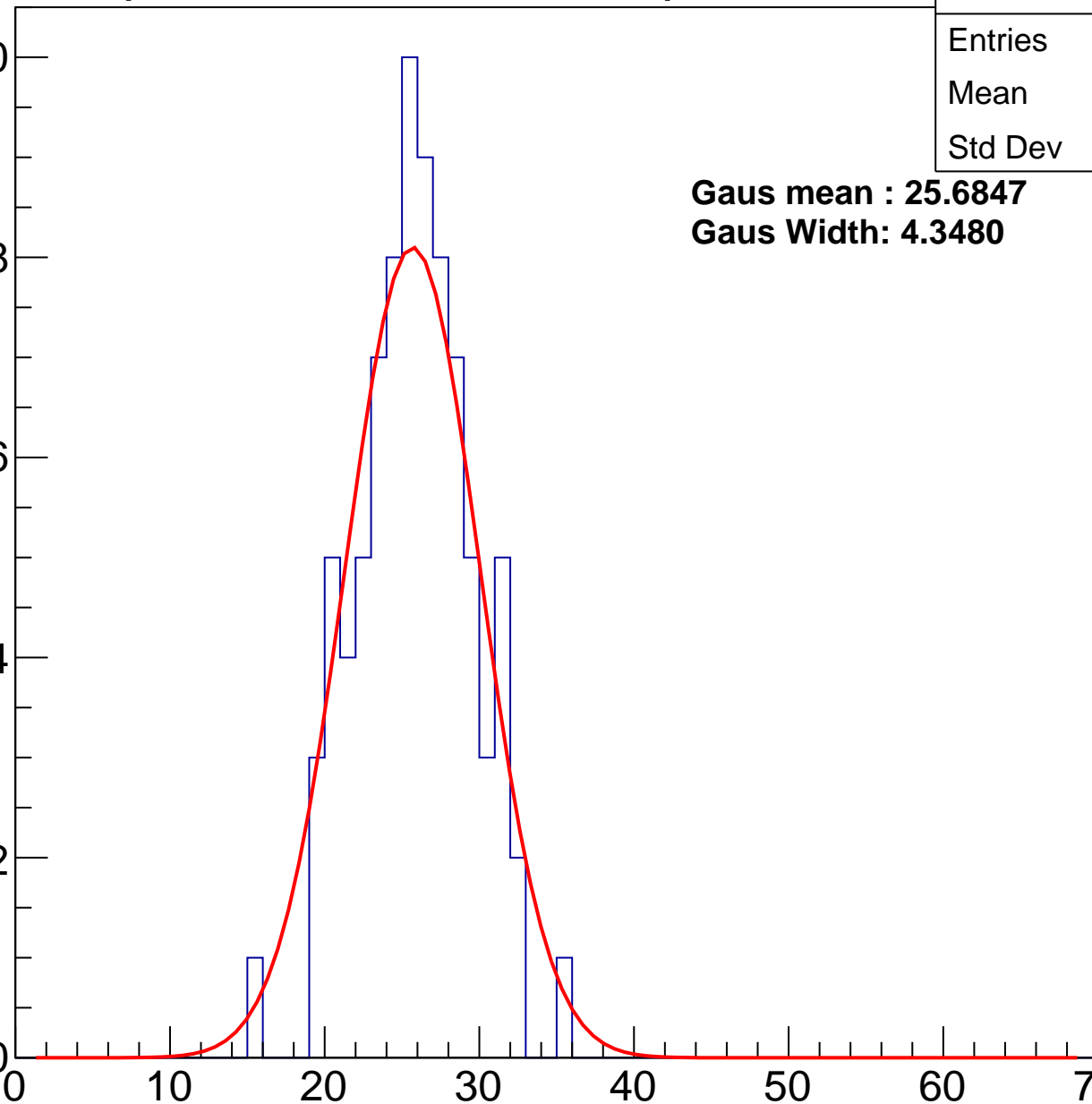
Gaus mean : 25.6847

Gaus Width: 4.3480

Entry

10
8
6
4
2
0

ampl



B1L103S, U23-ch82, adc1

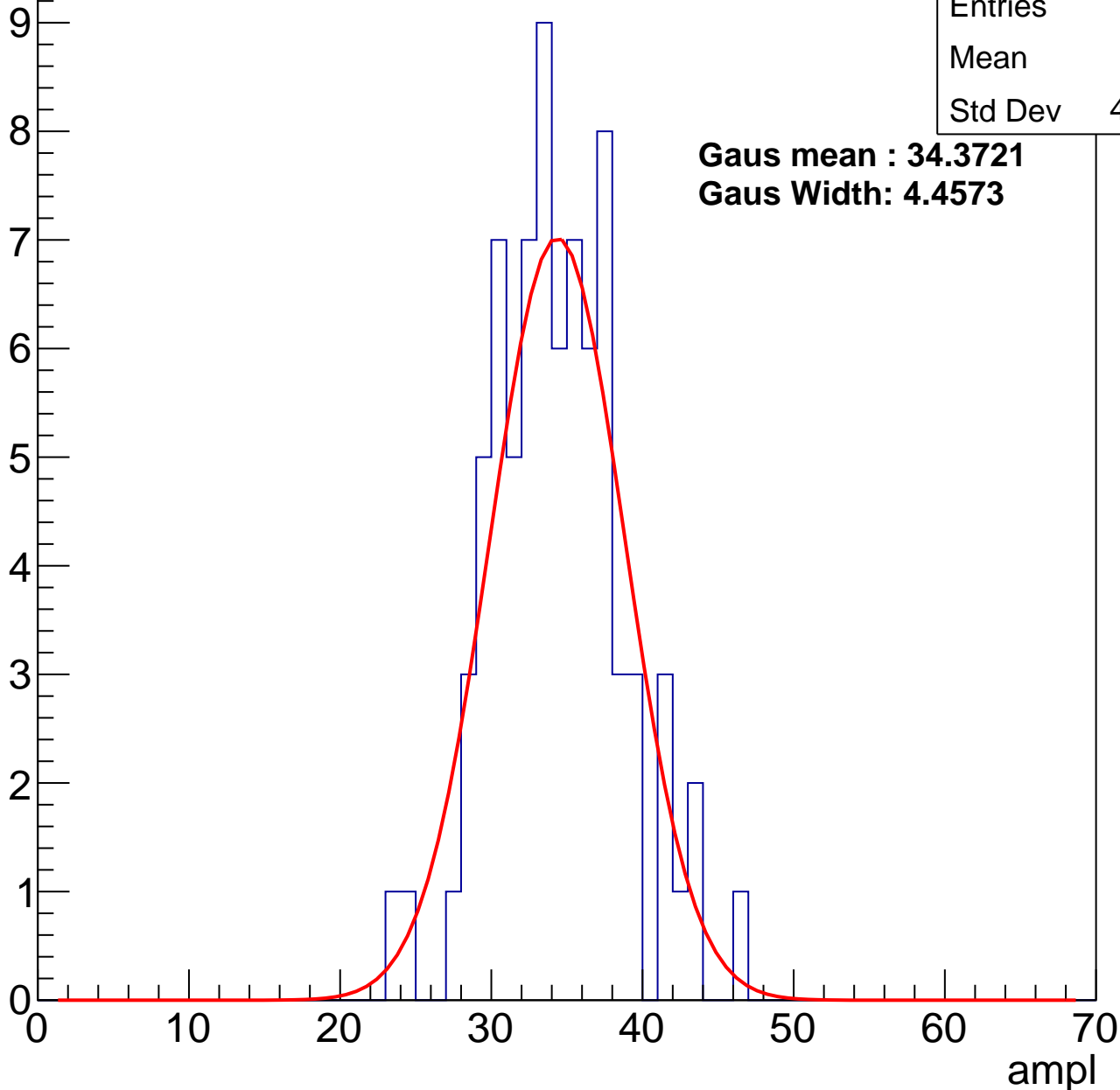
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	33.9
Std Dev	4.268

Gaus mean : 34.3721

Gaus Width: 4.4573



B1L103S, U23-ch82, adc2

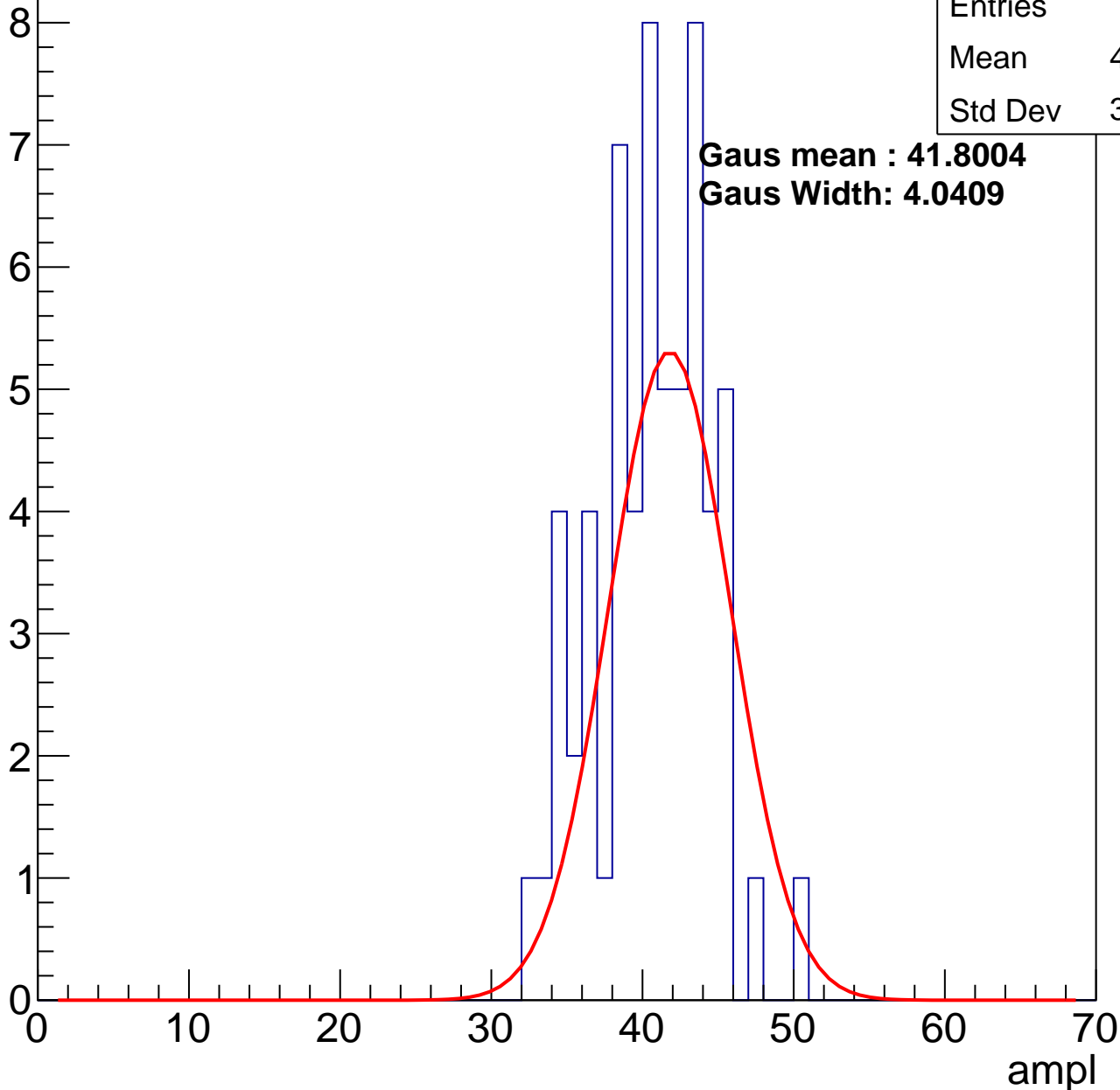
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	40.18
Std Dev	3.722

Gaus mean : 41.8004

Gaus Width: 4.0409

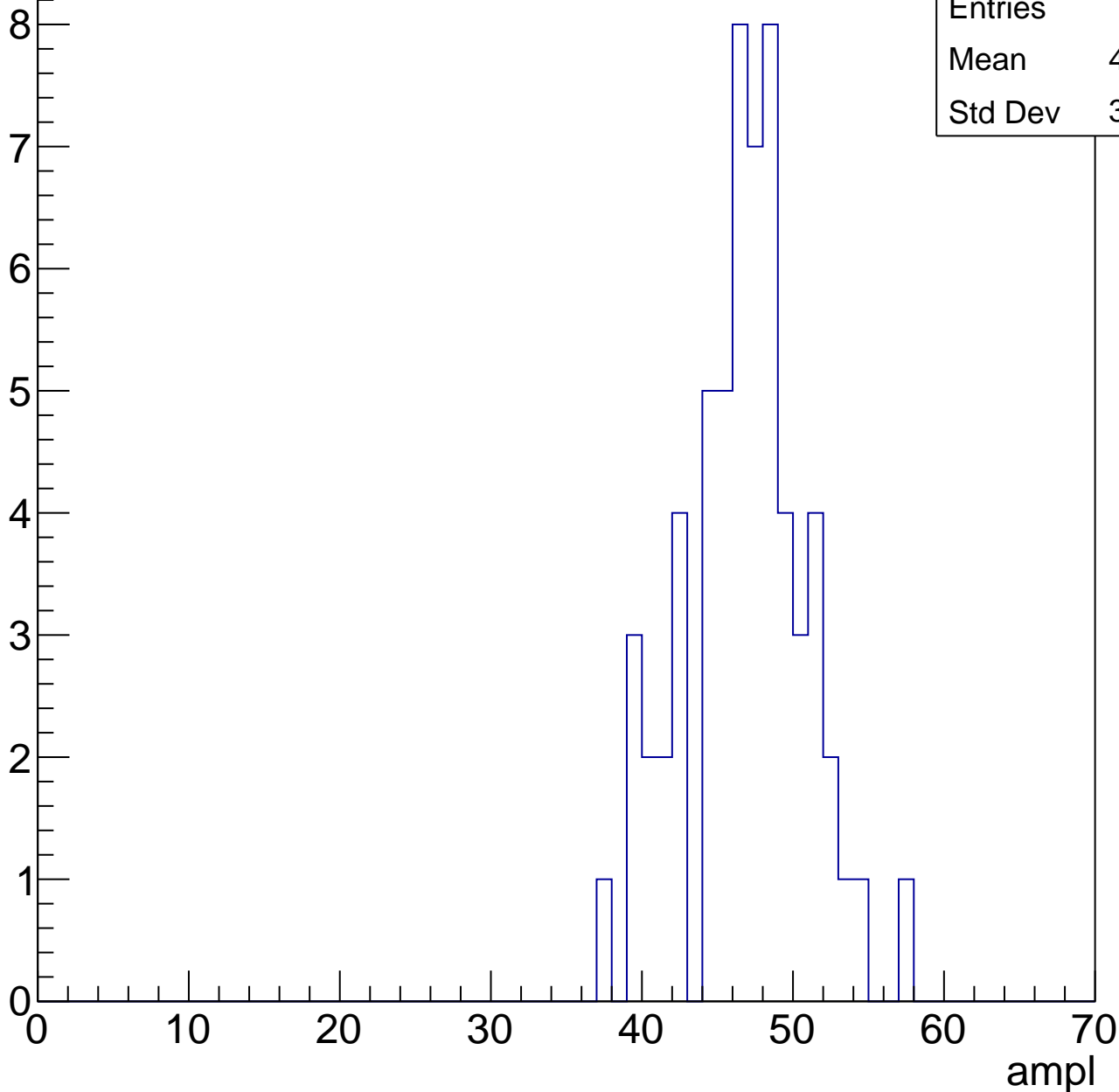


B1L103S, U23-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	46.36
Std Dev	3.967

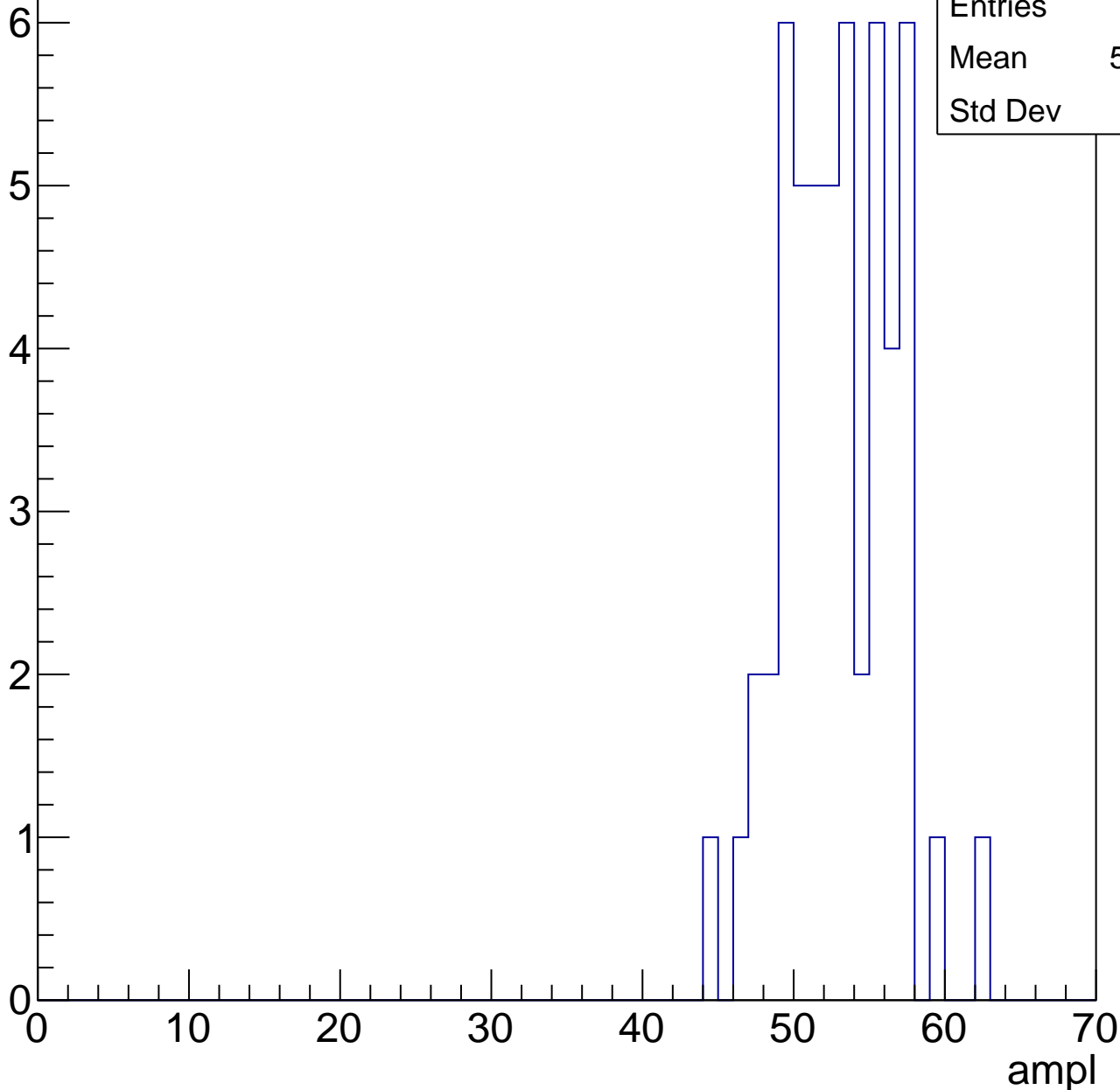


B1L103S, U23-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

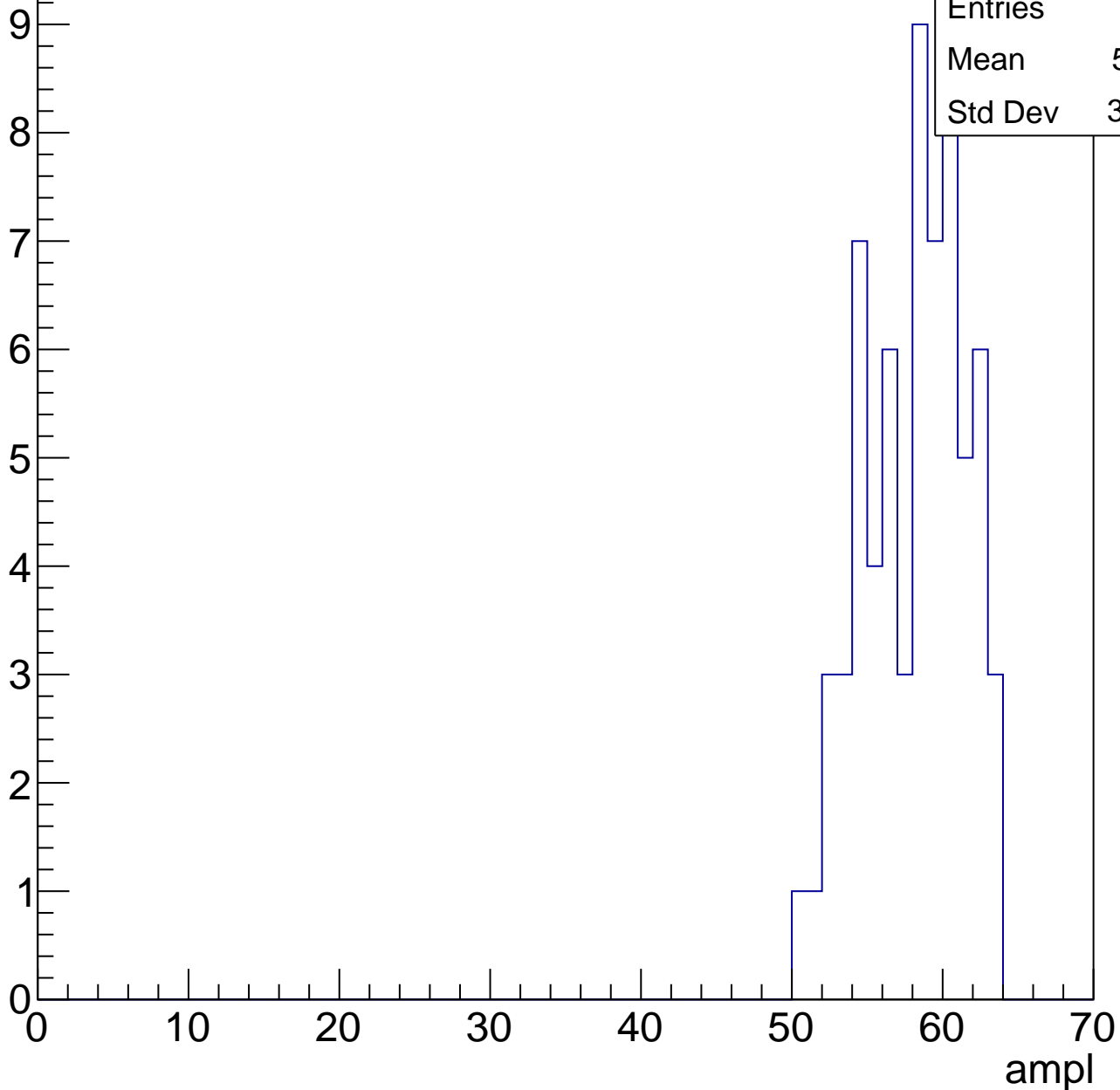
Entries	53
Mean	52.49
Std Dev	3.58



B1L103S, U23-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



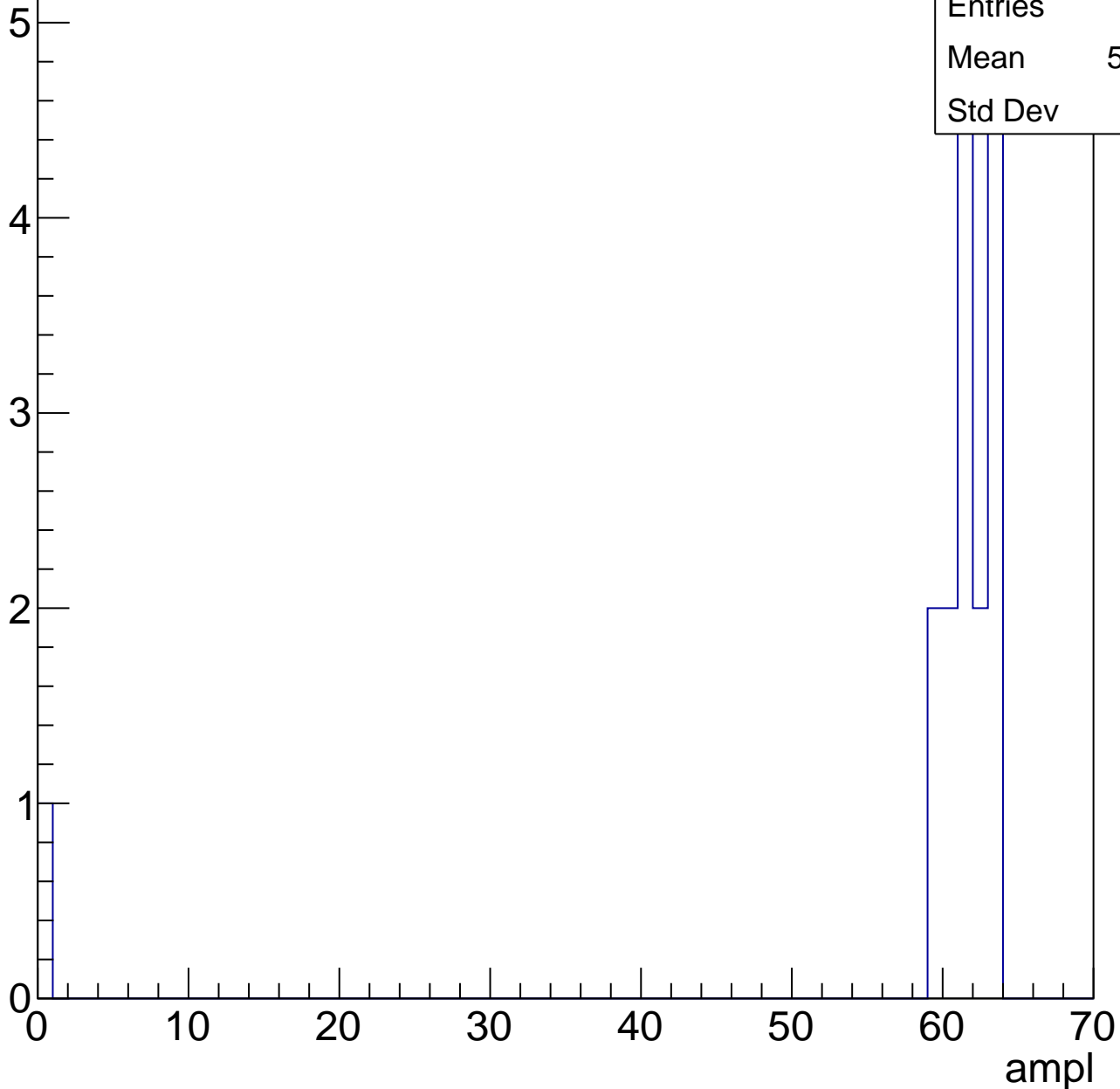
Entries	66
Mean	57.61
Std Dev	3.293

B1L103S, U23-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

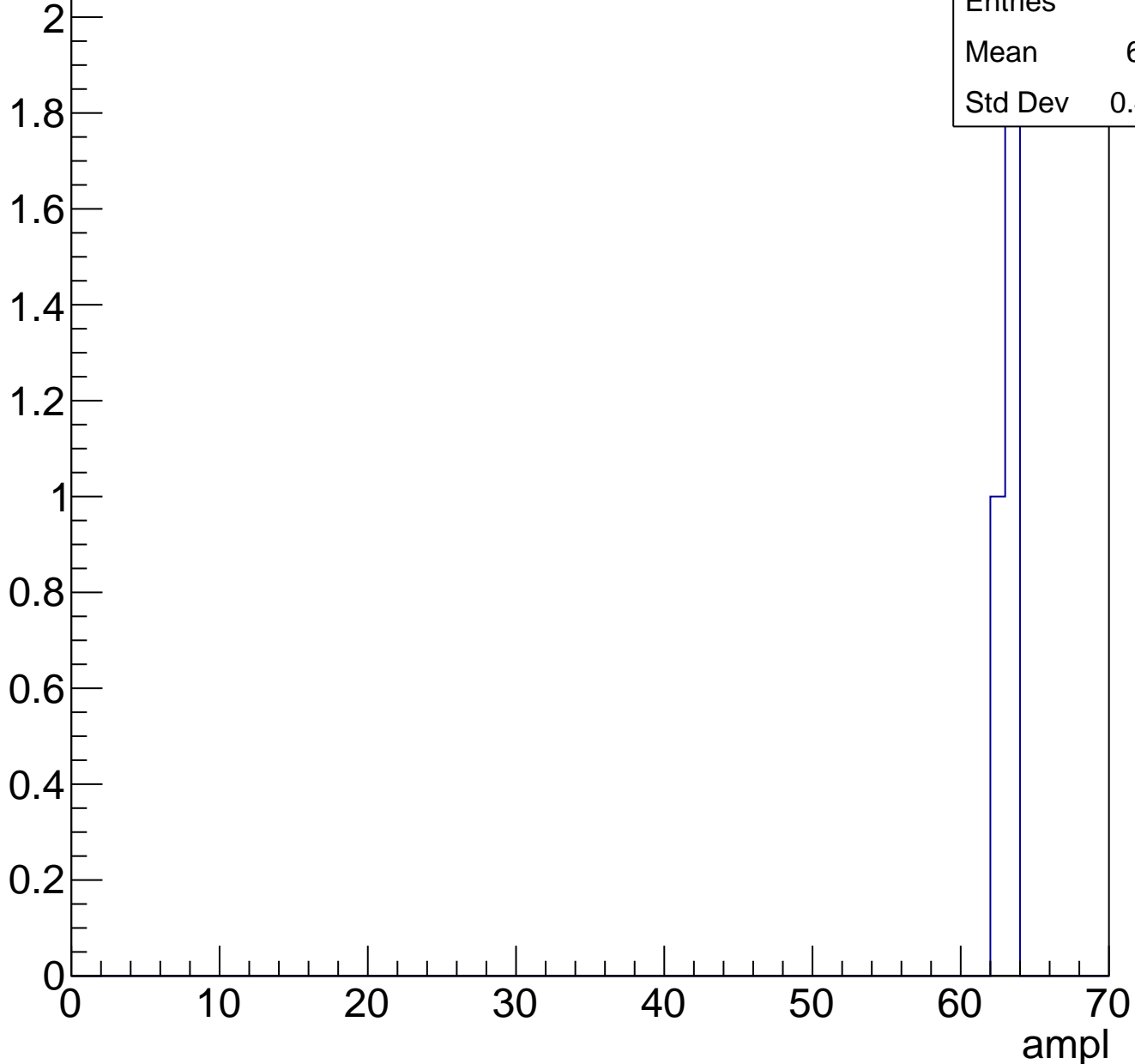
Entries	17
Mean	57.76
Std Dev	14.5



B1L103S, U23-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch83, adc0

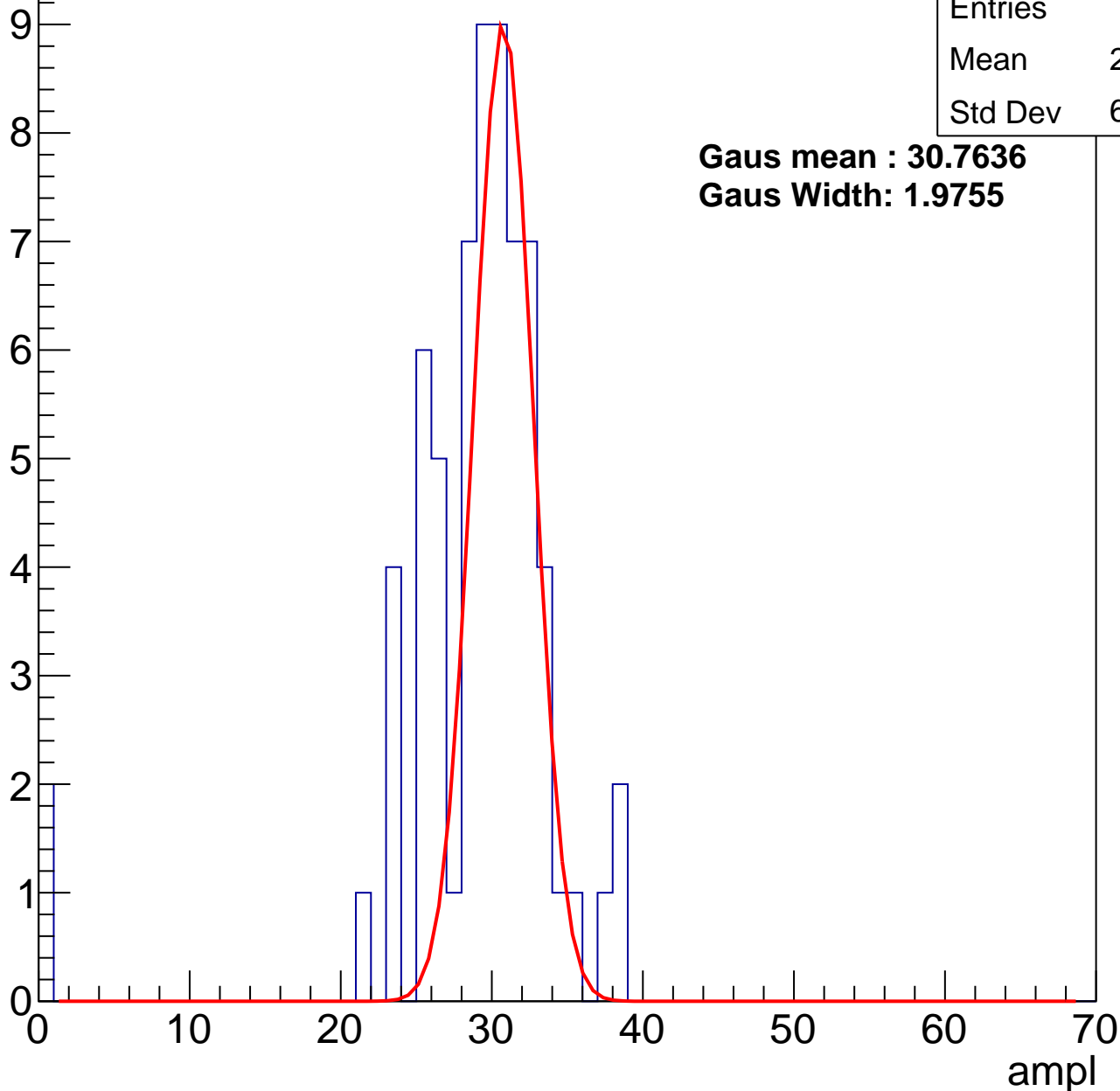
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.39
Std Dev	6.069

Gaus mean : 30.7636

Gaus Width: 1.9755



B1L103S, U23-ch83, adc1

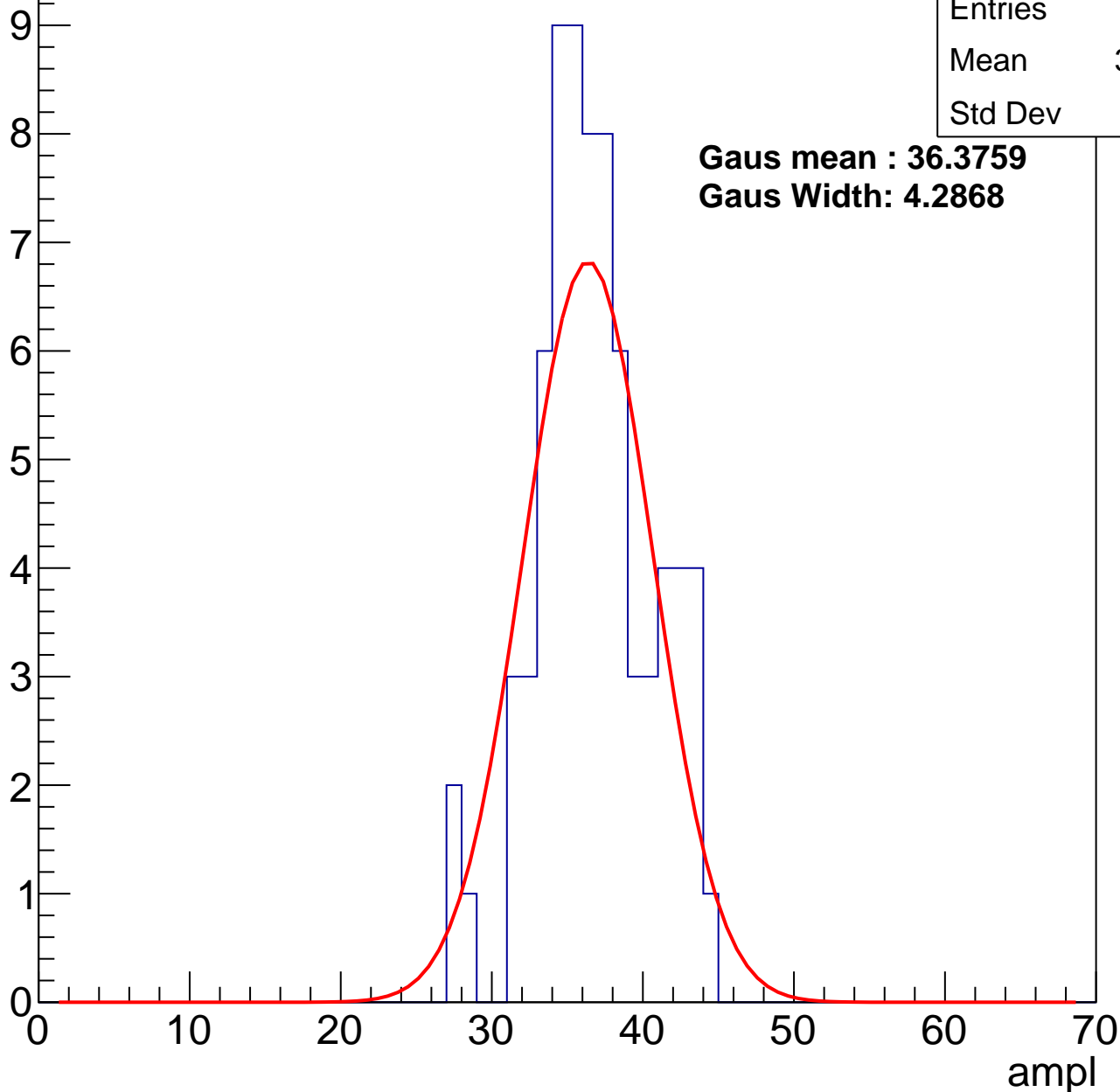
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.31
Std Dev	3.77

Gaus mean : 36.3759

Gaus Width: 4.2868



B1L103S, U23-ch83, adc2

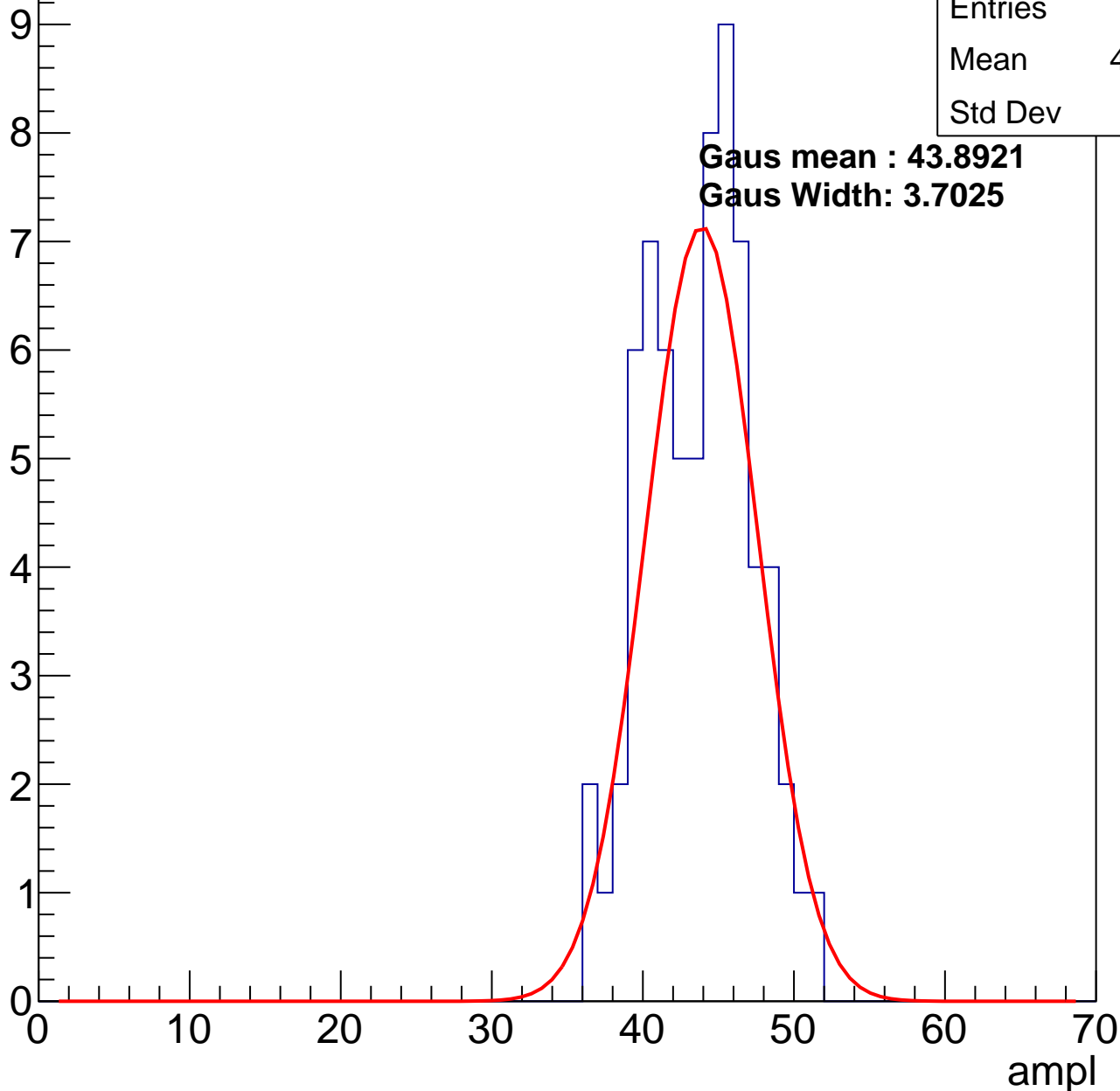
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.26
Std Dev	3.43

Gaus mean : 43.8921

Gaus Width: 3.7025

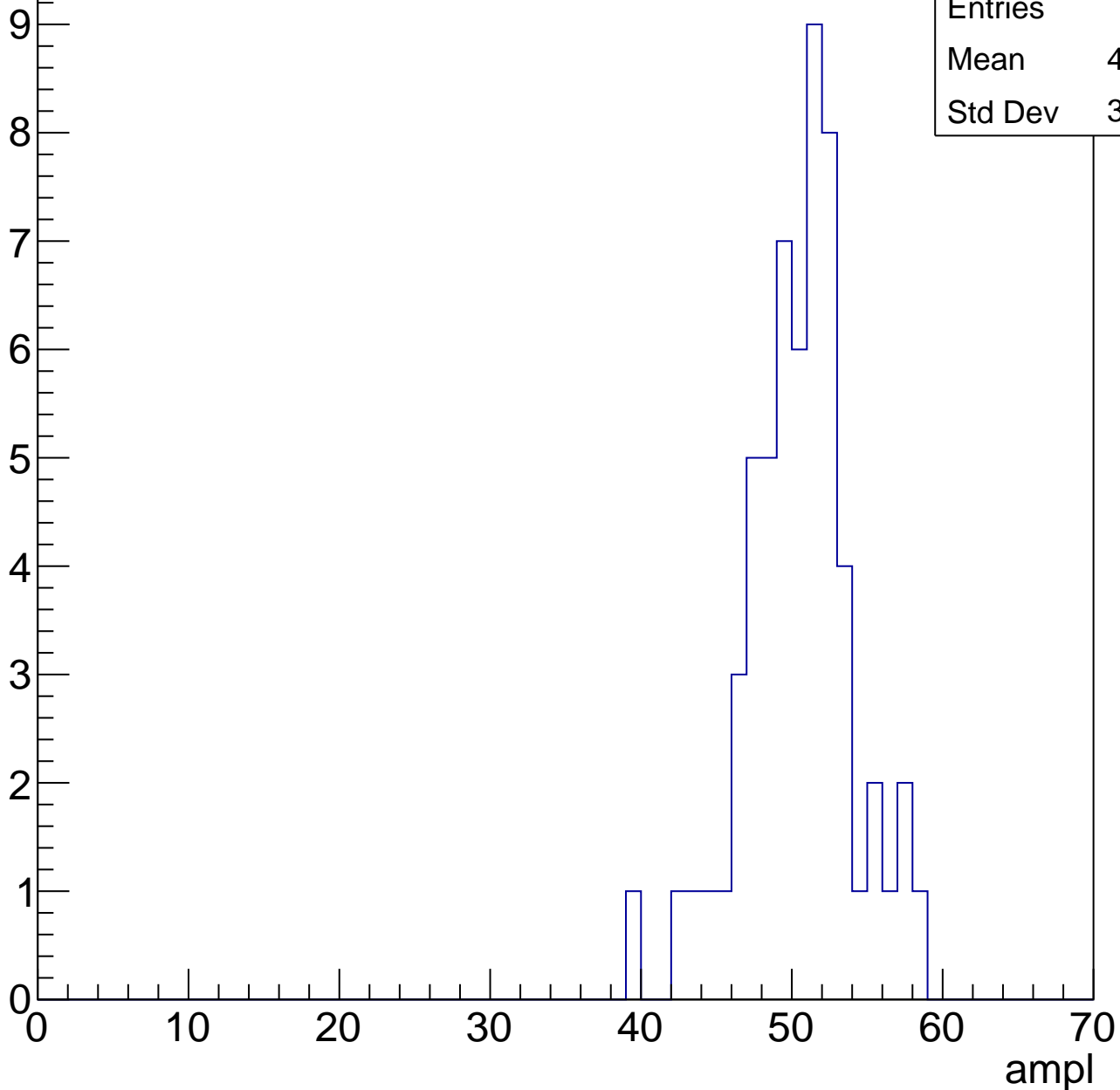


B1L103S, U23-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

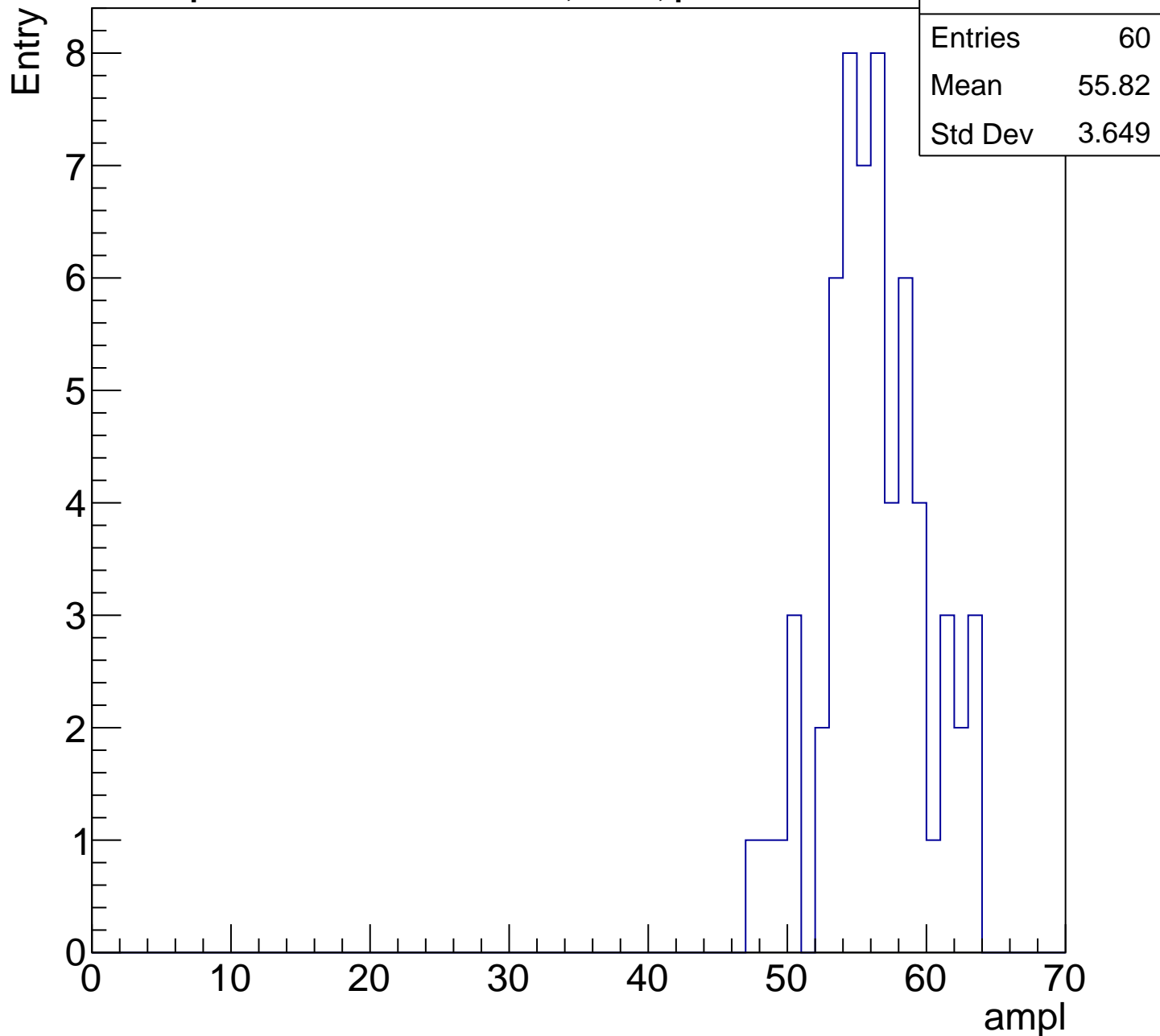
Entry

Entries	59
Mean	49.97
Std Dev	3.579



B1L103S, U23-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U23-ch83, adc5

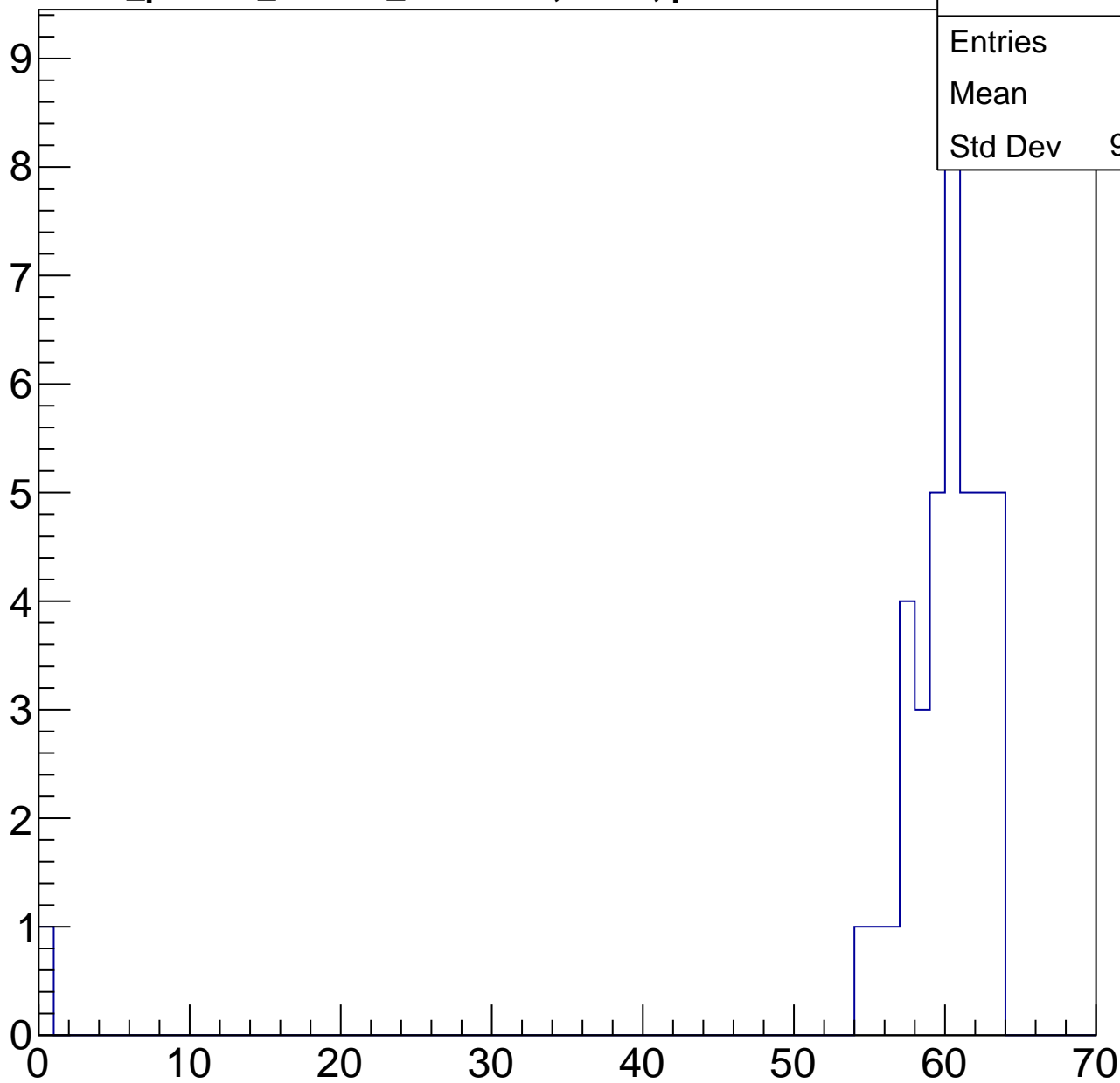
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.3
Std Dev	9.597

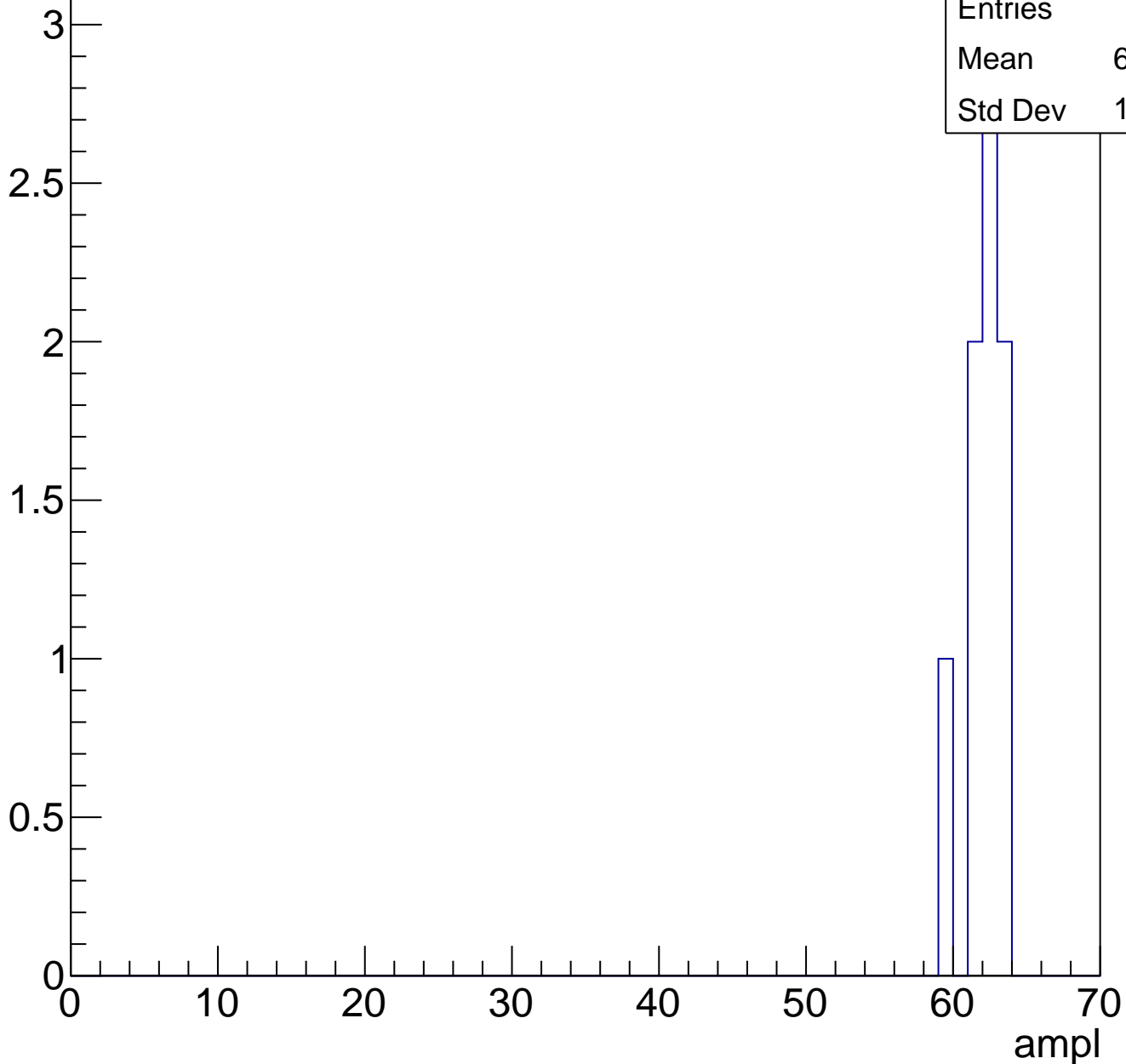
ampl



B1L103S, U23-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch84, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	26.66
Std Dev	4.741

Gaus mean : 26.5649

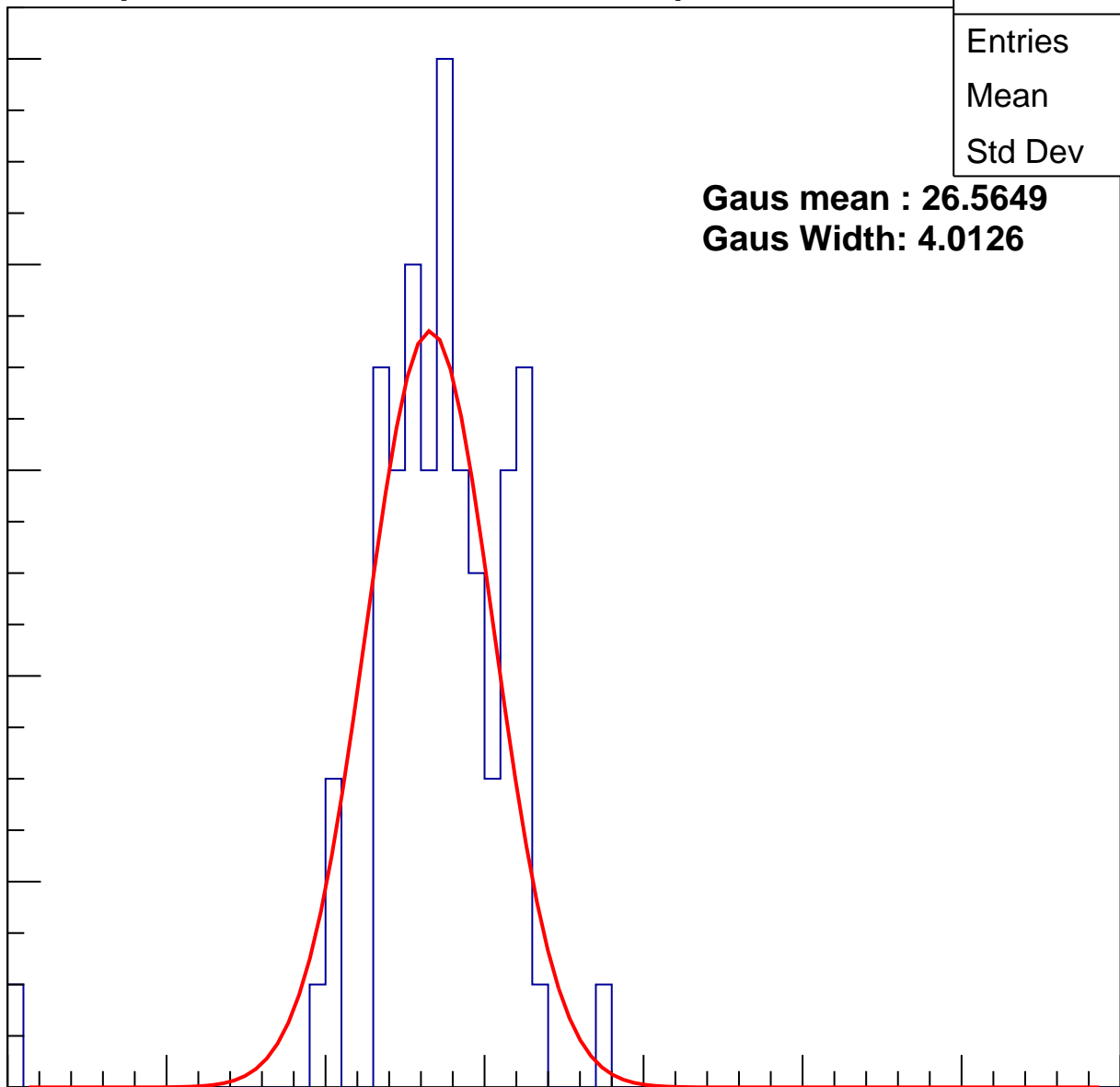
Gaus Width: 4.0126

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



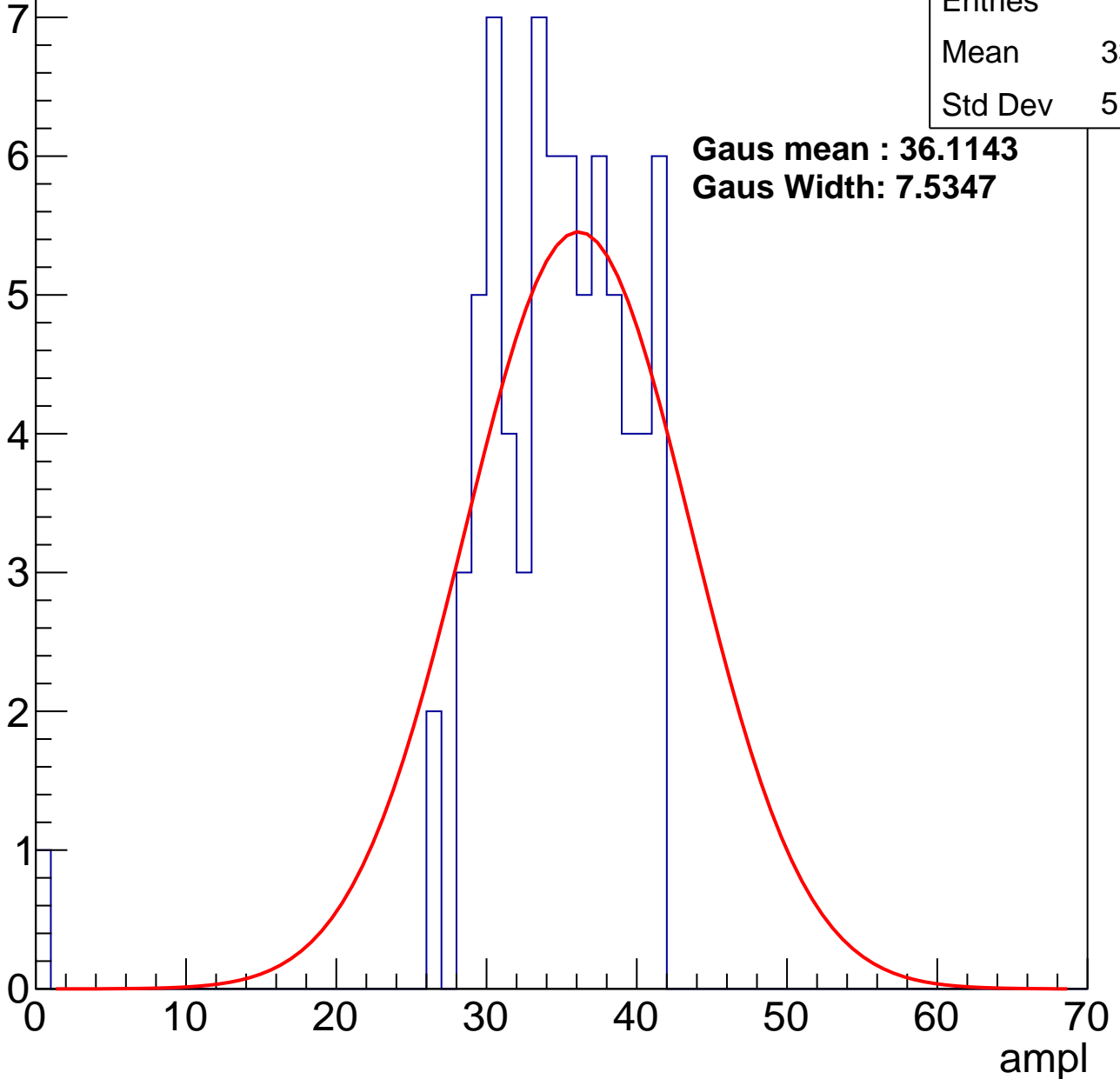
B1L103S, U23-ch84, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	33.92
Std Dev	5.673

Gaus mean : 36.1143
Gaus Width: 7.5347



B1L103S, U23-ch84, adc2

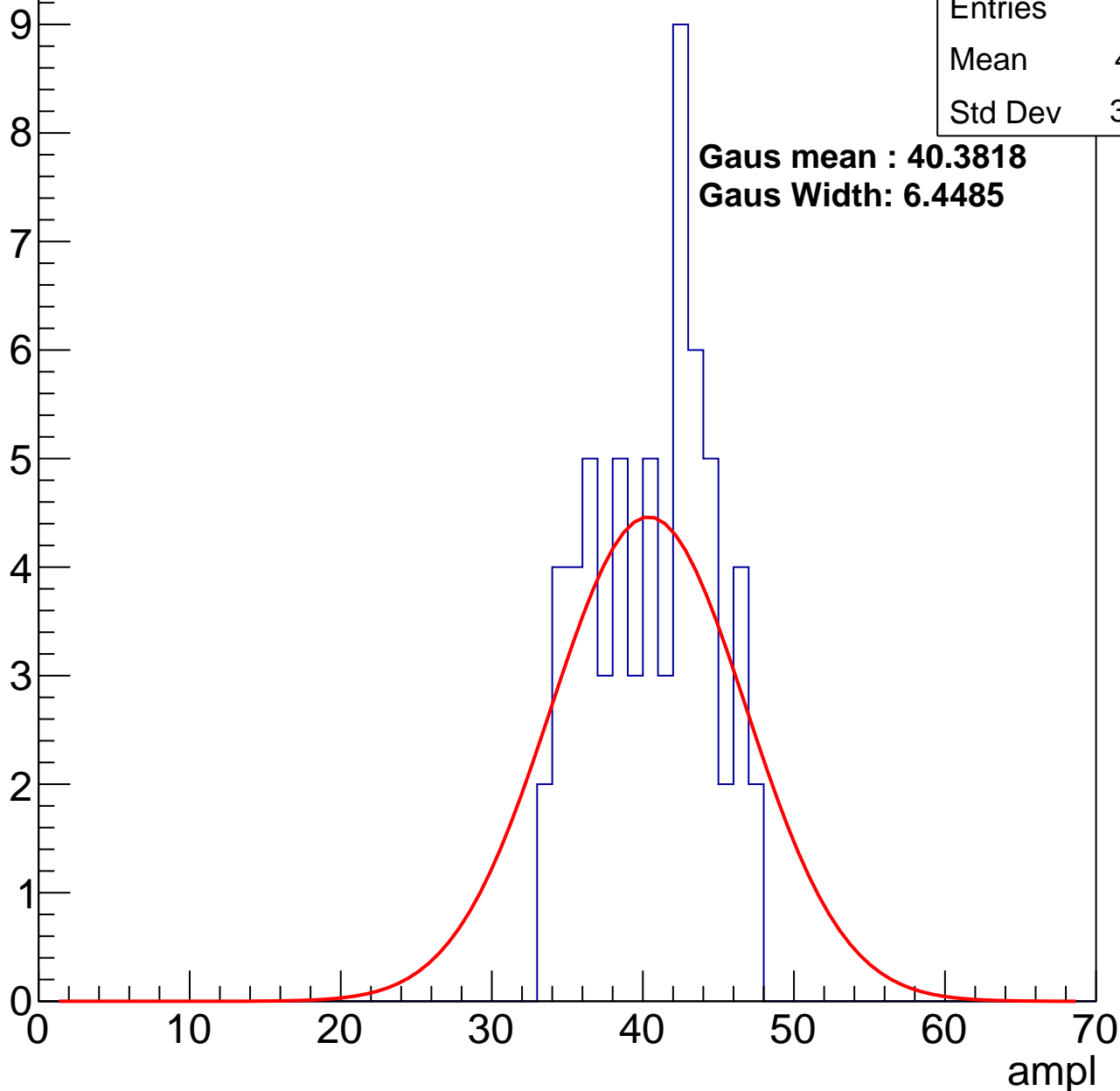
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	40.11
Std Dev	3.886

Gaus mean : 40.3818

Gaus Width: 6.4485



B1L103S, U23-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

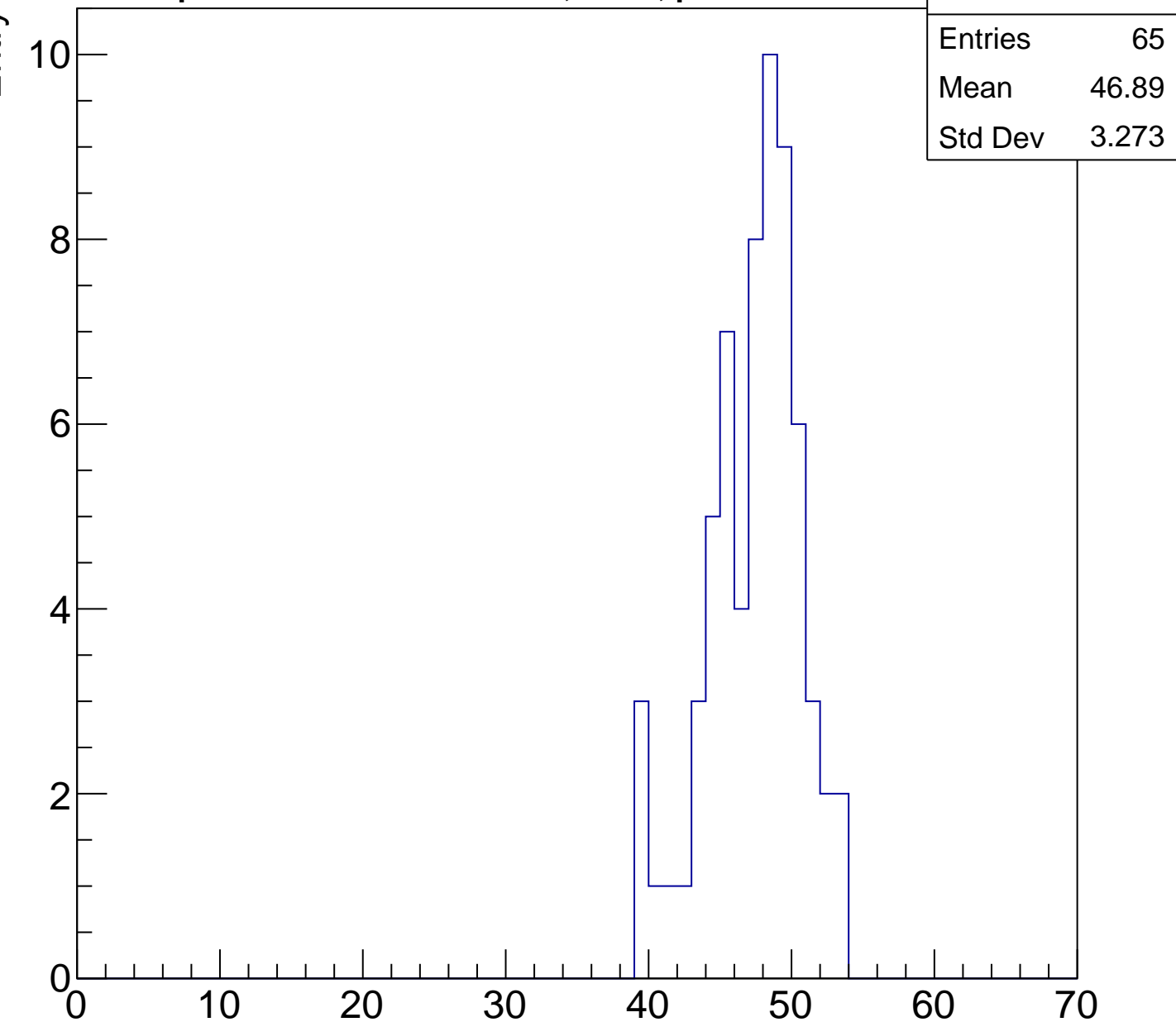
Entries	65
Mean	46.89
Std Dev	3.273

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

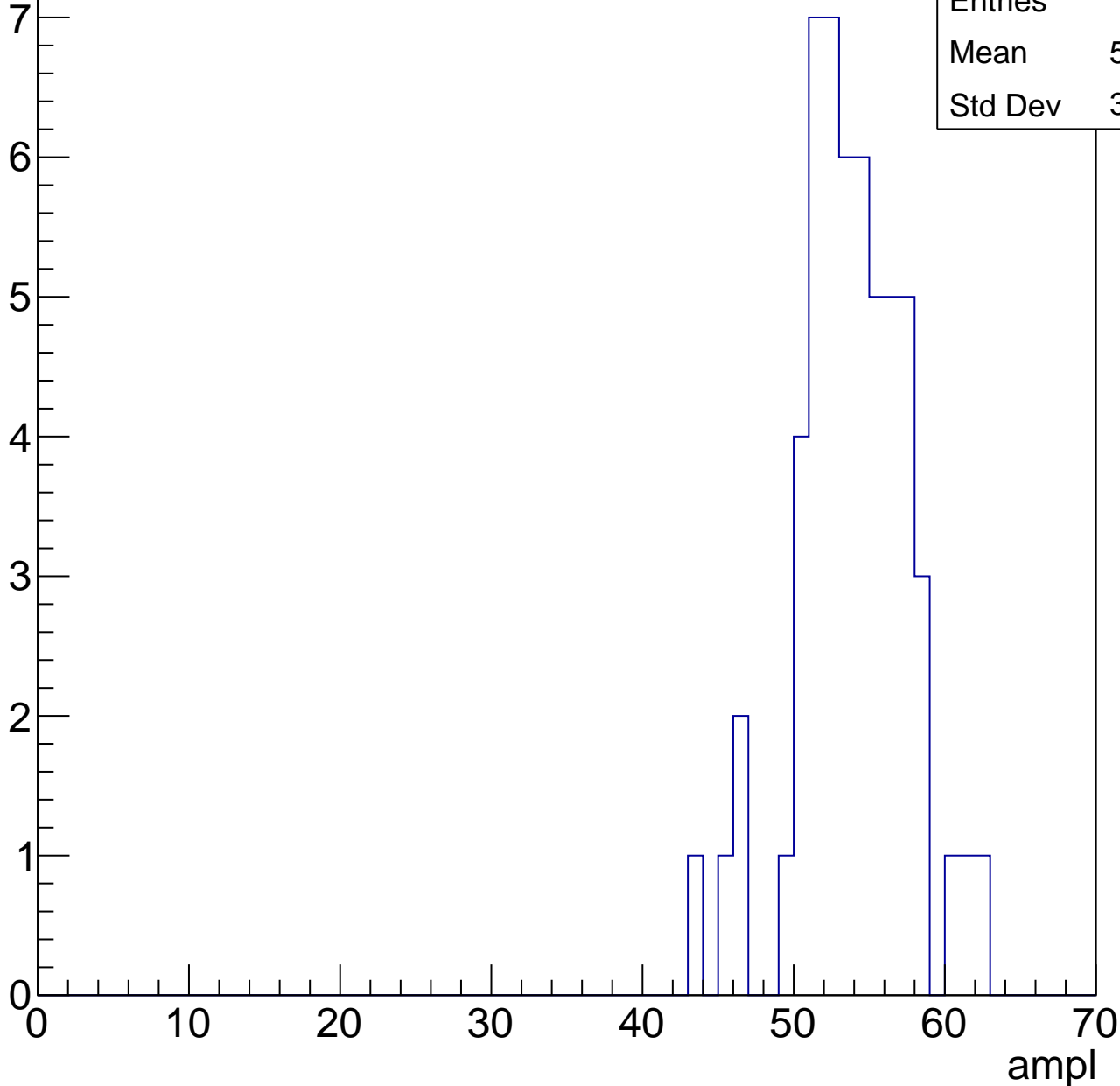


B1L103S, U23-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	53.38
Std Dev	3.687

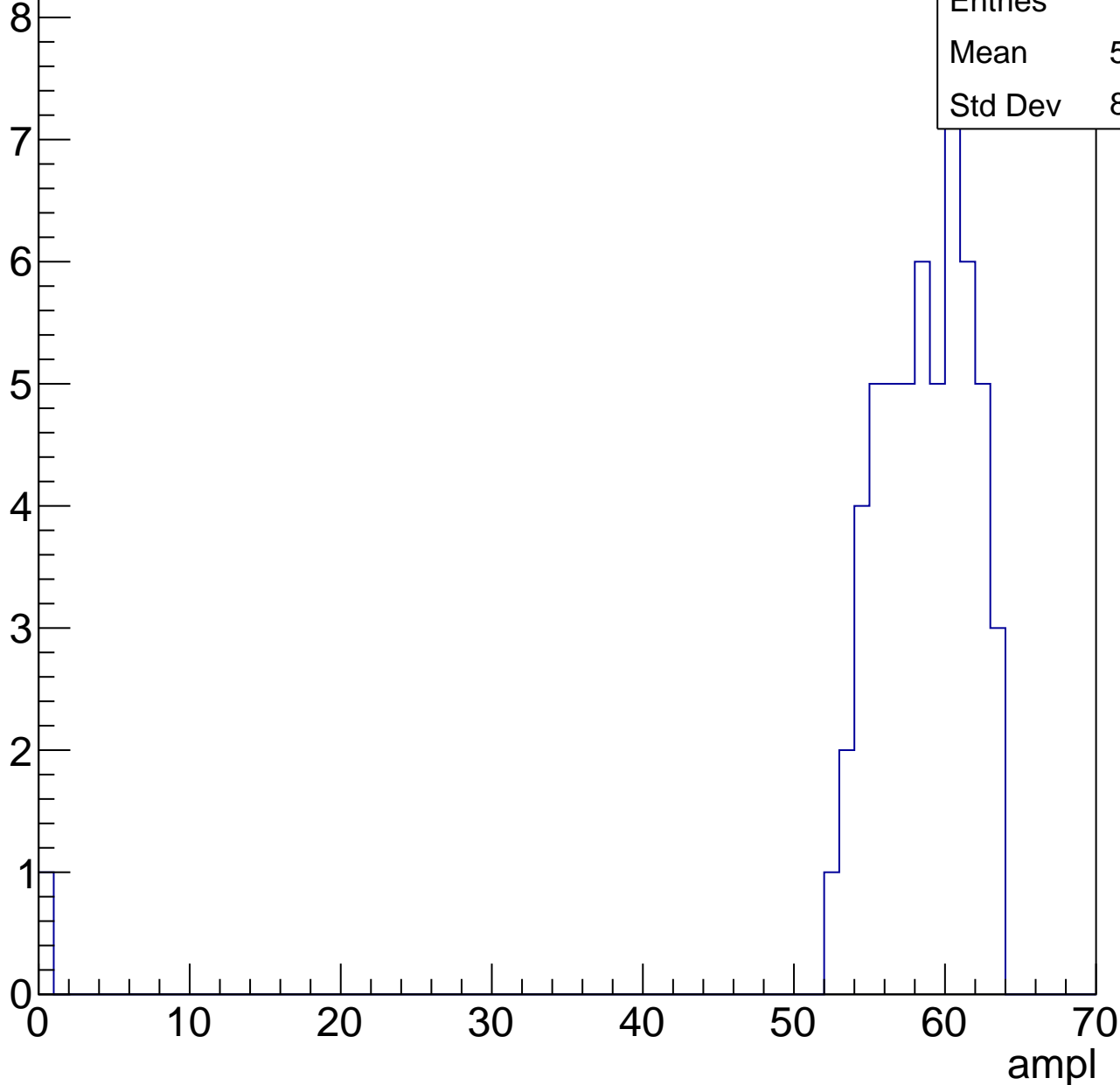


B1L103S, U23-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

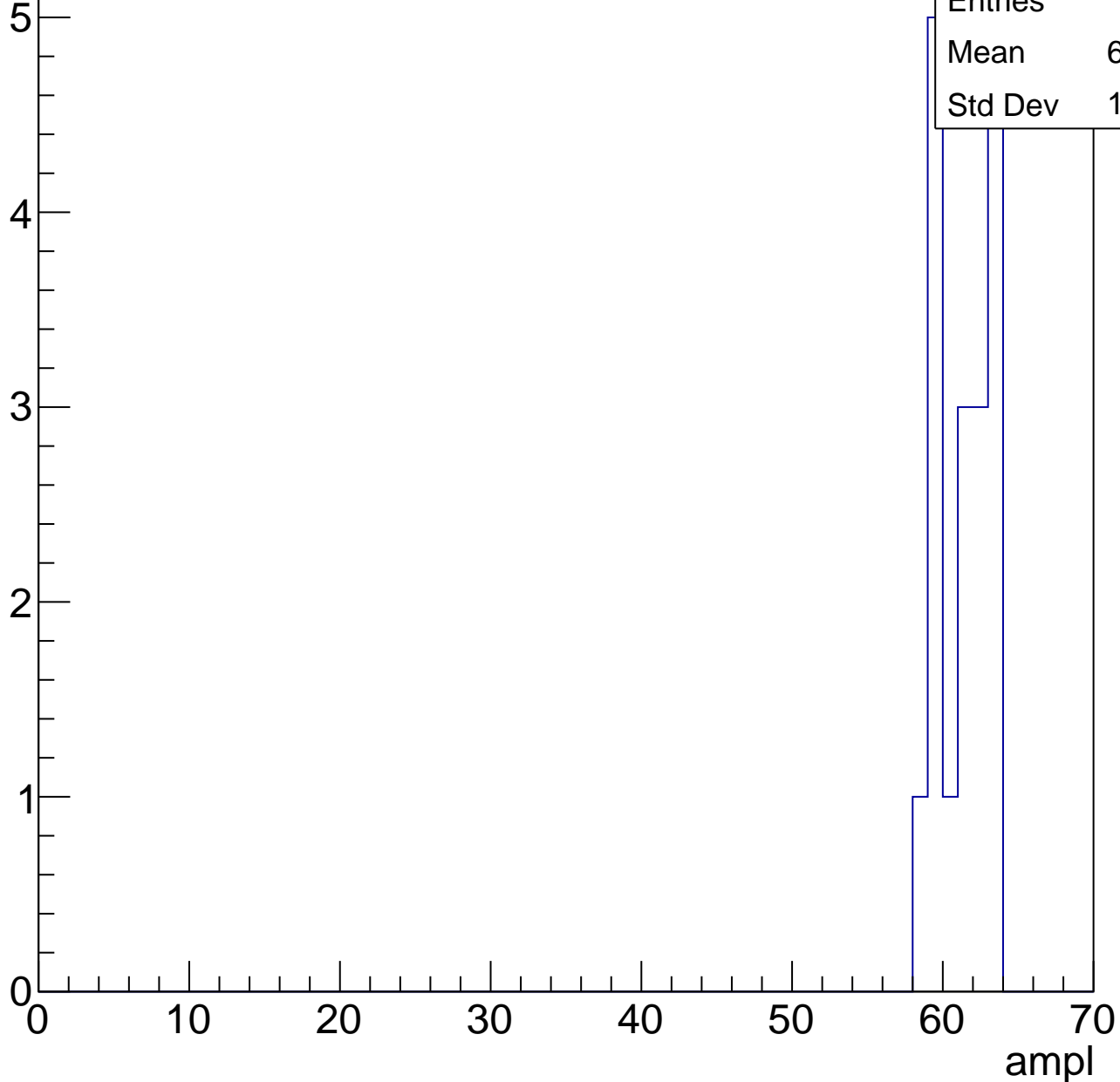
Entries	56
Mean	57.18
Std Dev	8.229



B1L103S, U23-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U23-ch85, adc0

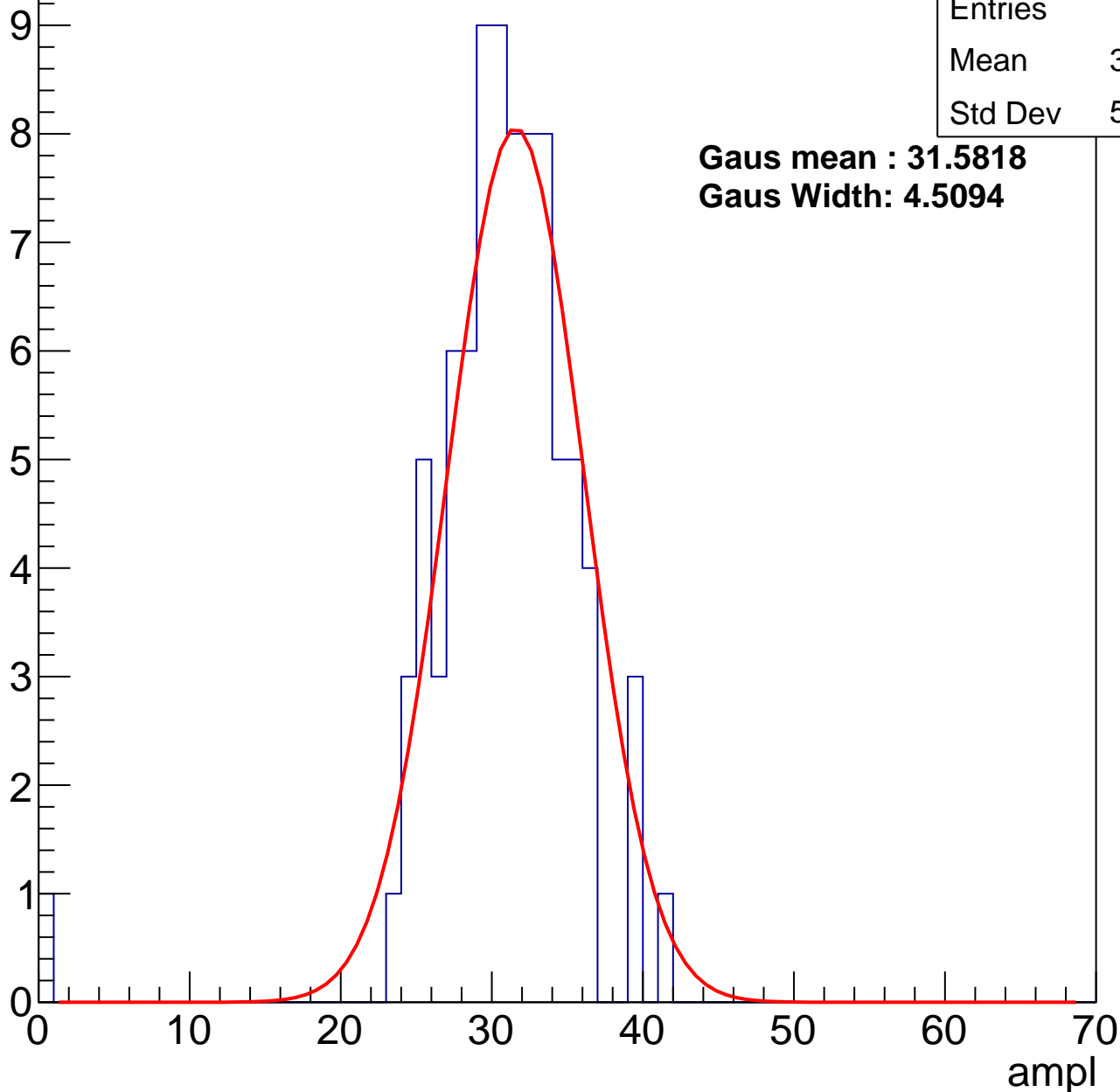
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	30.28
Std Dev	5.012

Gaus mean : 31.5818

Gaus Width: 4.5094



B1L103S, U23-ch85, adc1

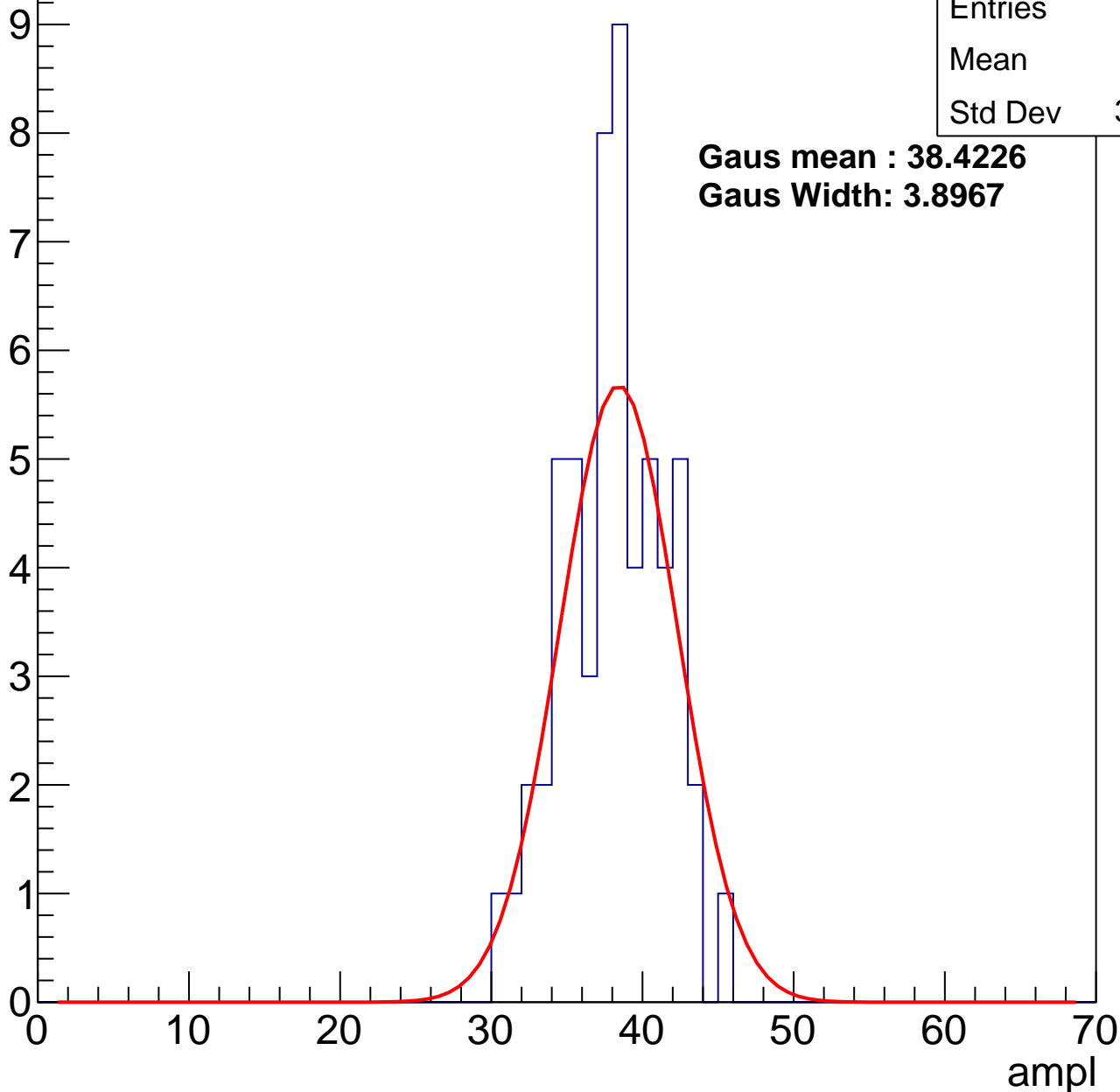
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	37.6
Std Dev	3.271

Gaus mean : 38.4226

Gaus Width: 3.8967



B1L103S, U23-ch85, adc2

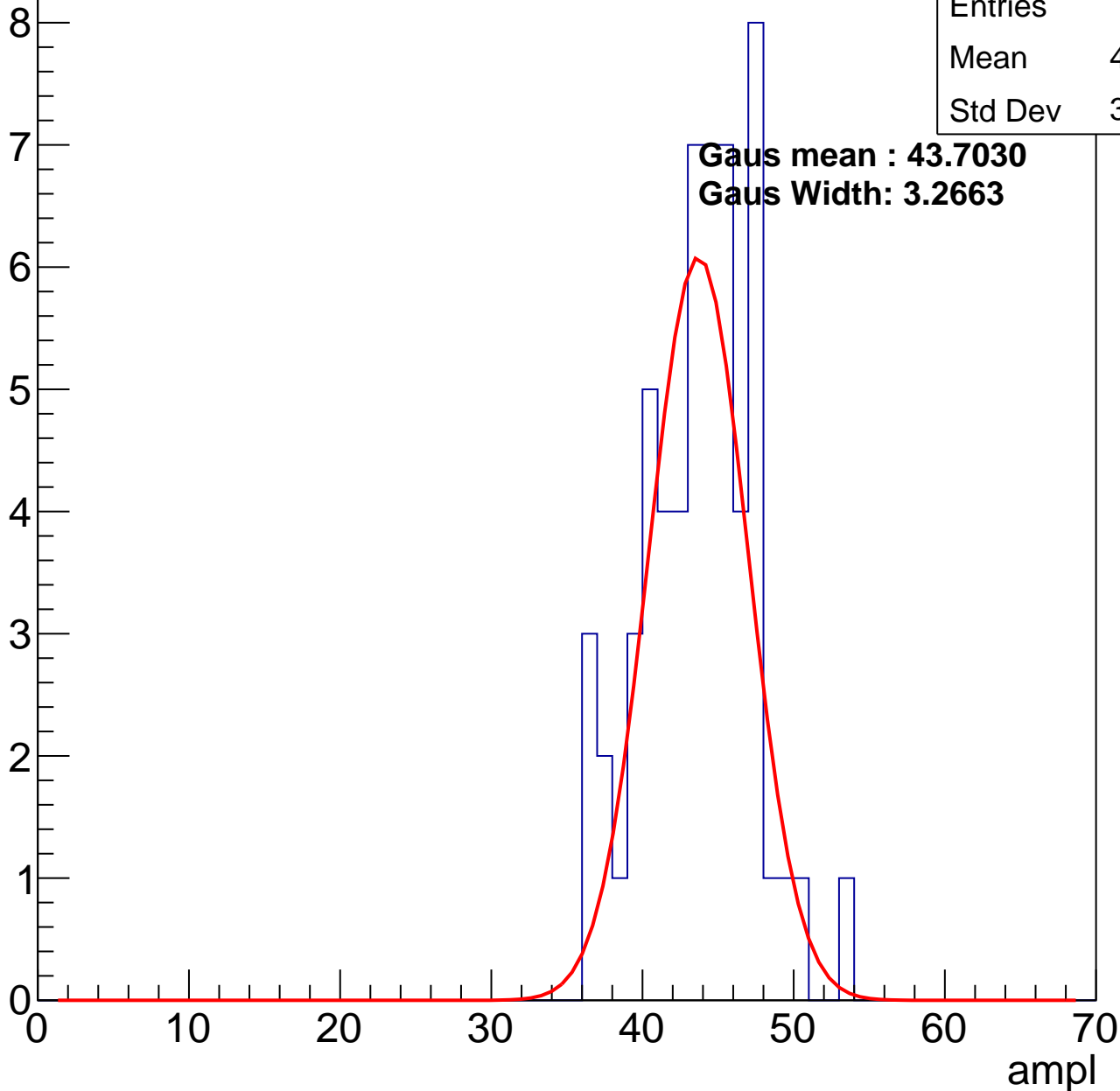
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.27
Std Dev	3.593

Gaus mean : 43.7030

Gaus Width: 3.2663

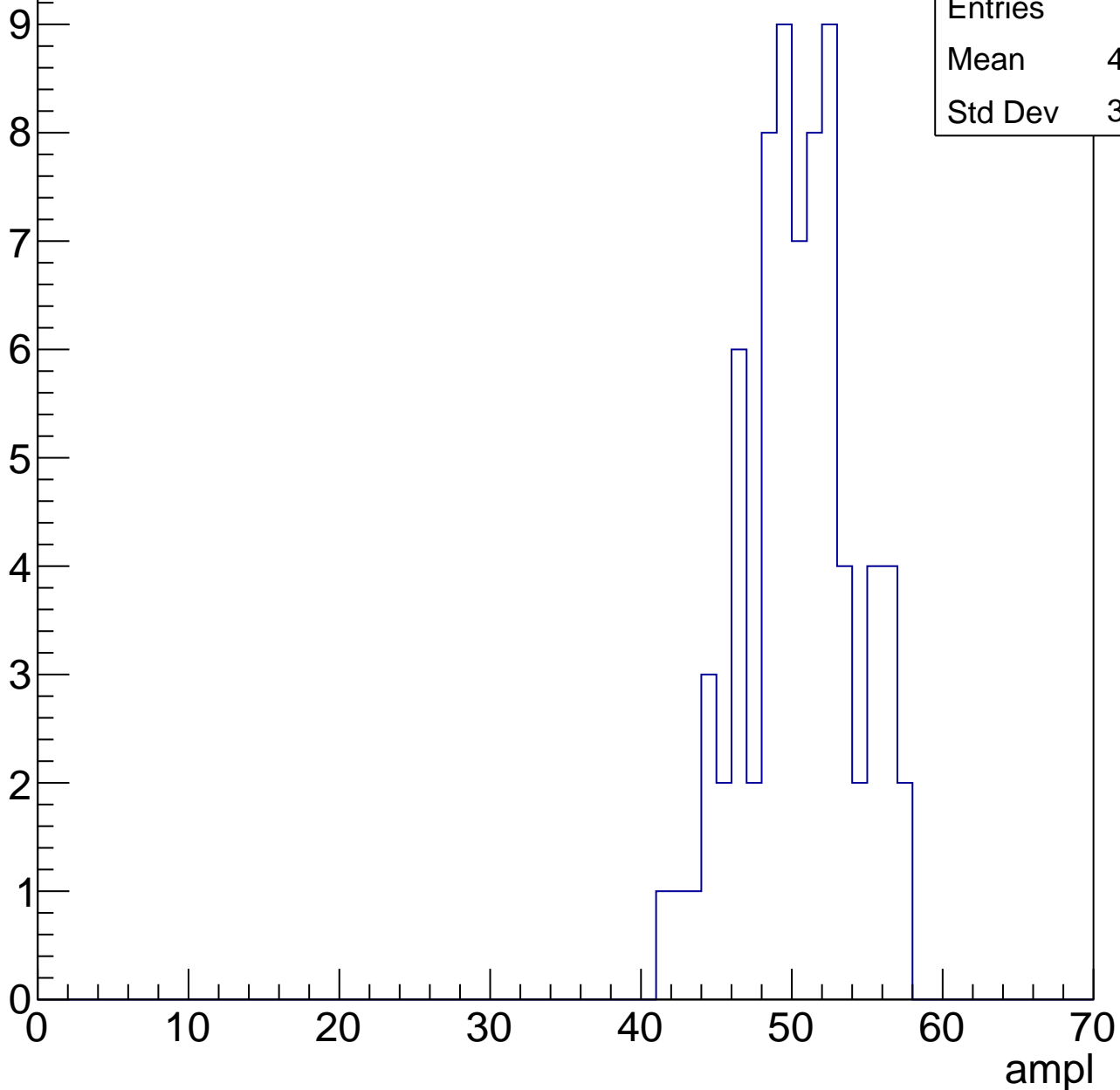


B1L103S, U23-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	49.96
Std Dev	3.628

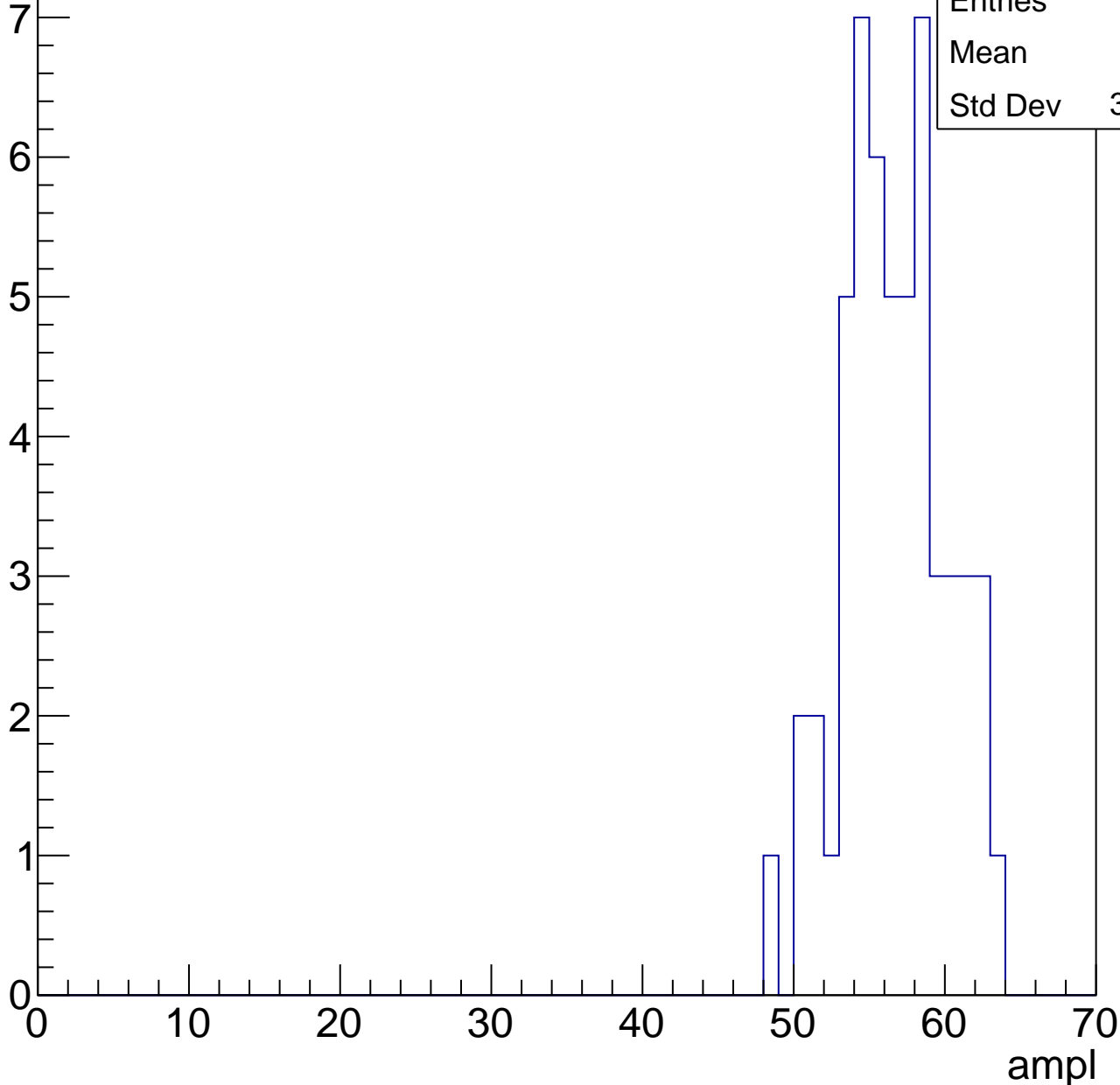


B1L103S, U23-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	56.2
Std Dev	3.385

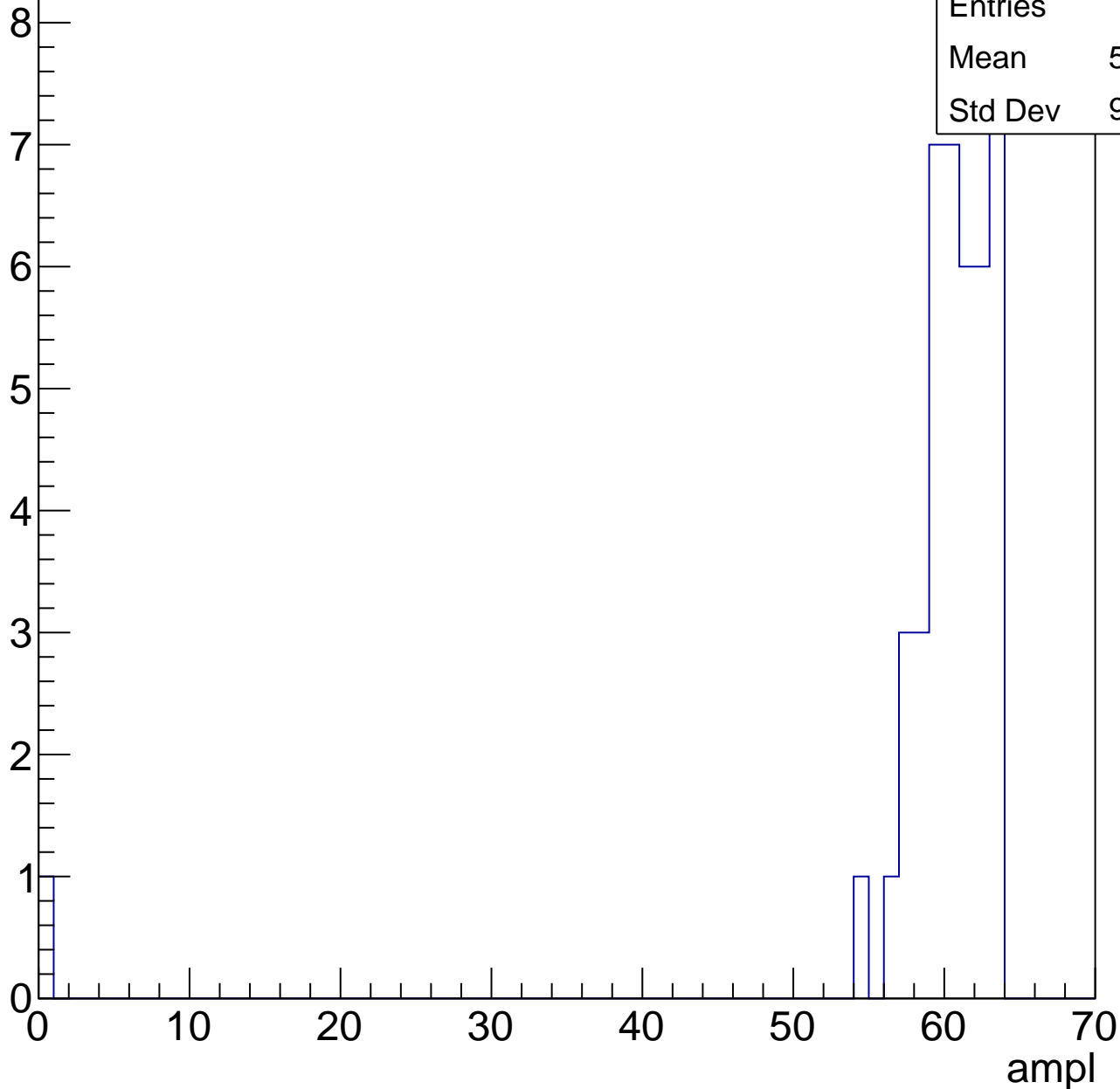


B1L103S, U23-ch85, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.84
Std Dev	9.328



B1L103S, U23-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch86, adc0

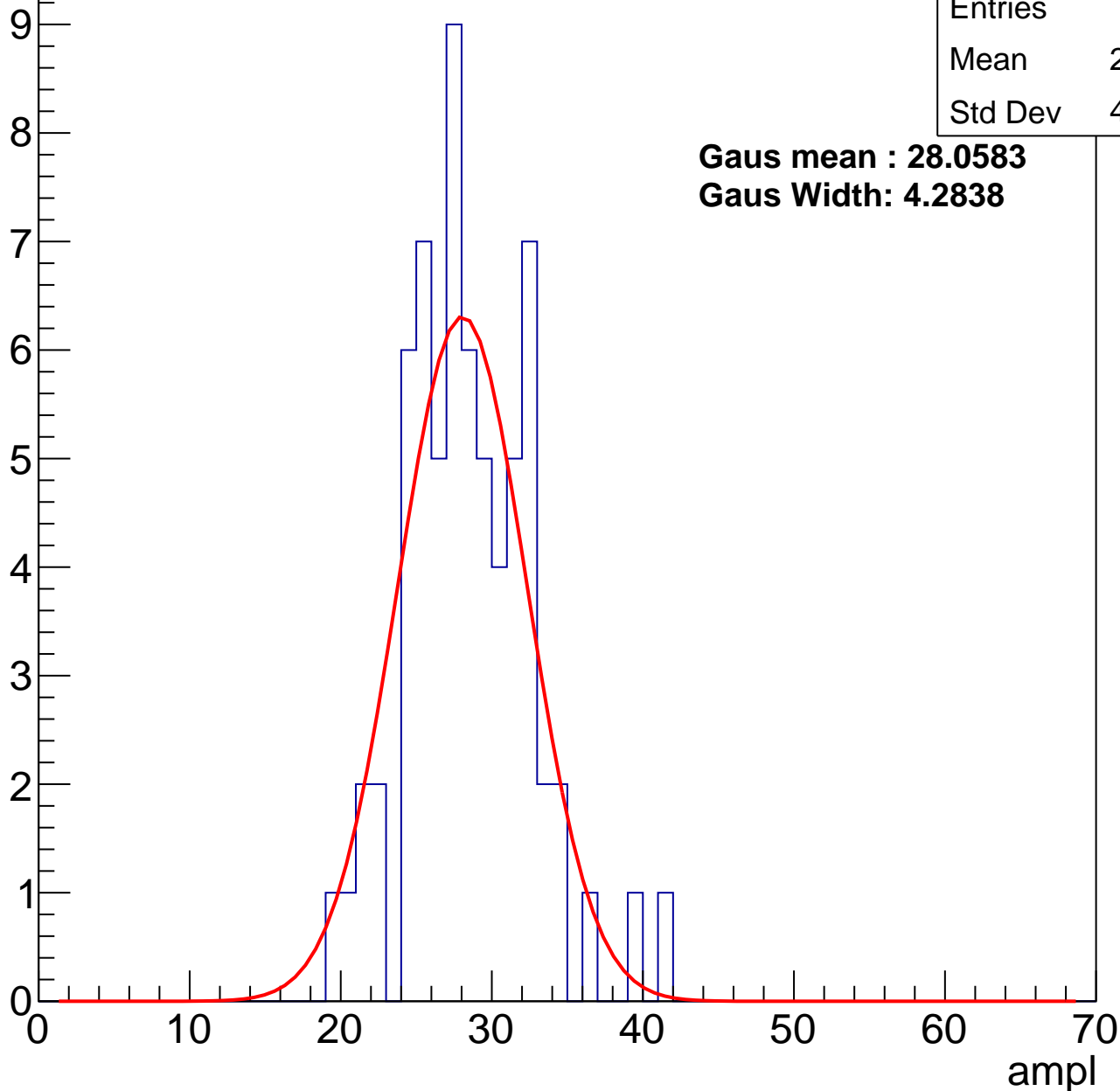
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.04
Std Dev	4.155

Gaus mean : 28.0583

Gaus Width: 4.2838



B1L103S, U23-ch86, adc1

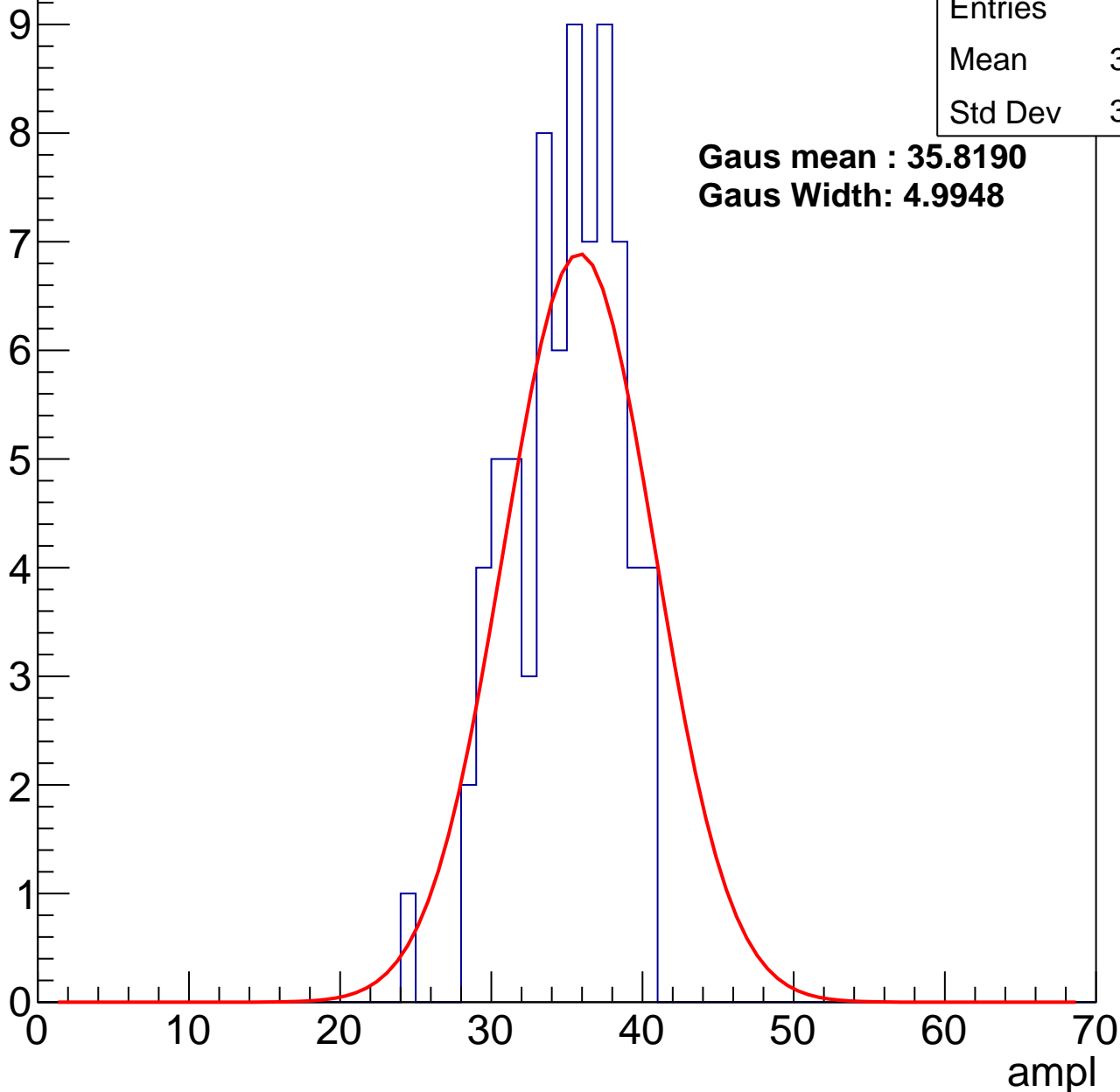
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	34.42
Std Dev	3.445

Gaus mean : 35.8190

Gaus Width: 4.9948



B1L103S, U23-ch86, adc2

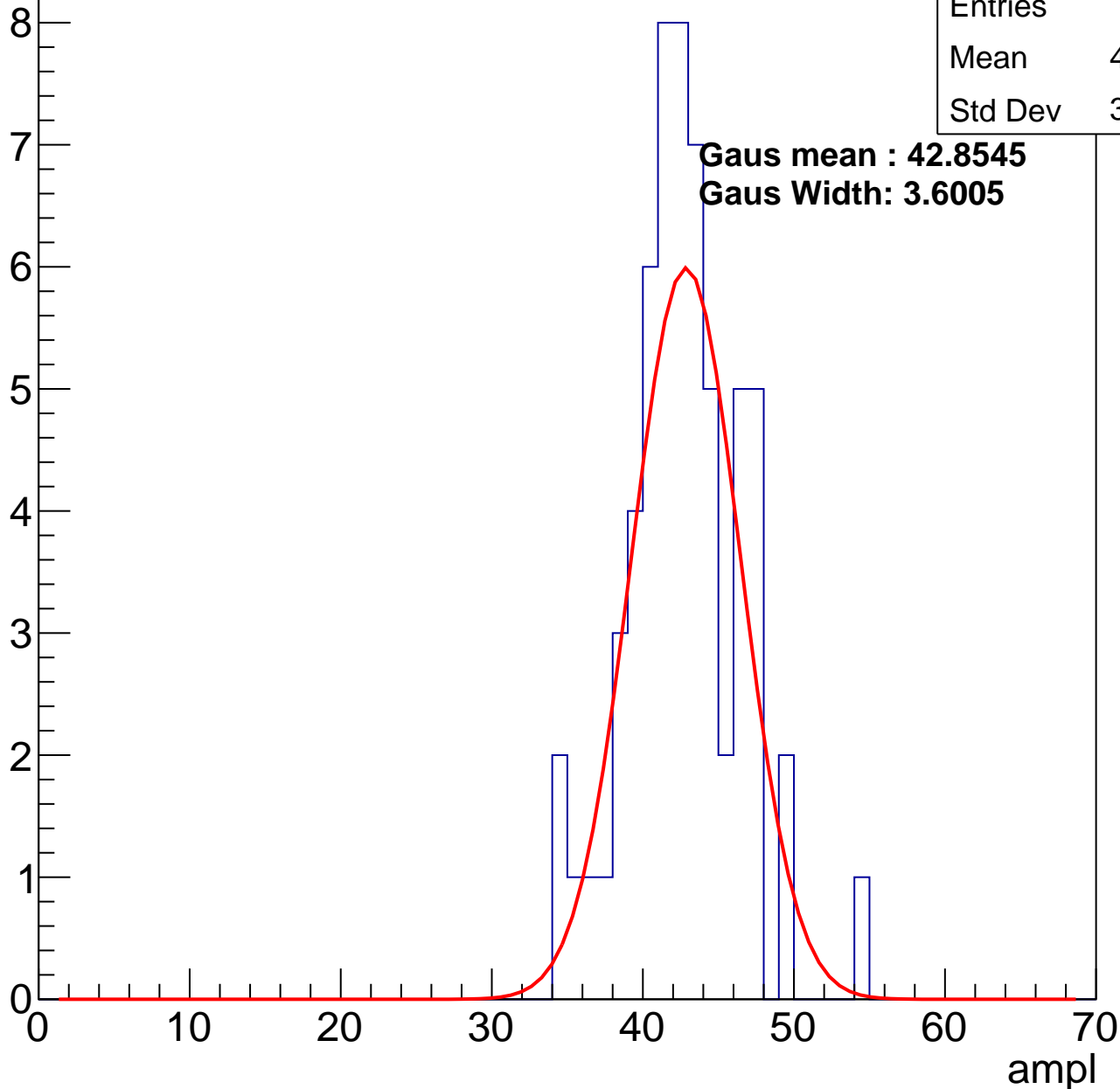
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.26
Std Dev	3.715

Gaus mean : 42.8545

Gaus Width: 3.6005

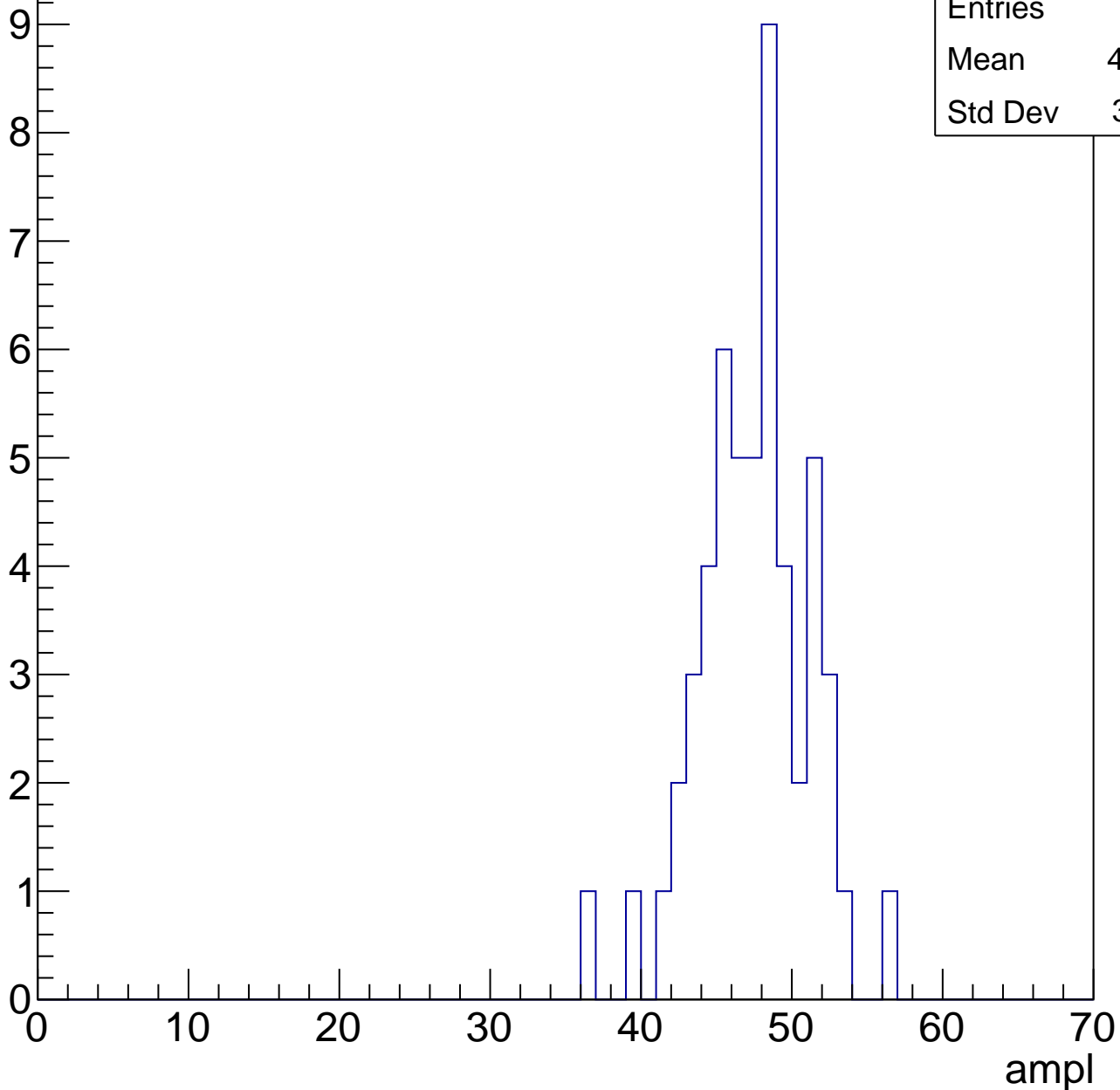


B1L103S, U23-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	46.94
Std Dev	3.631

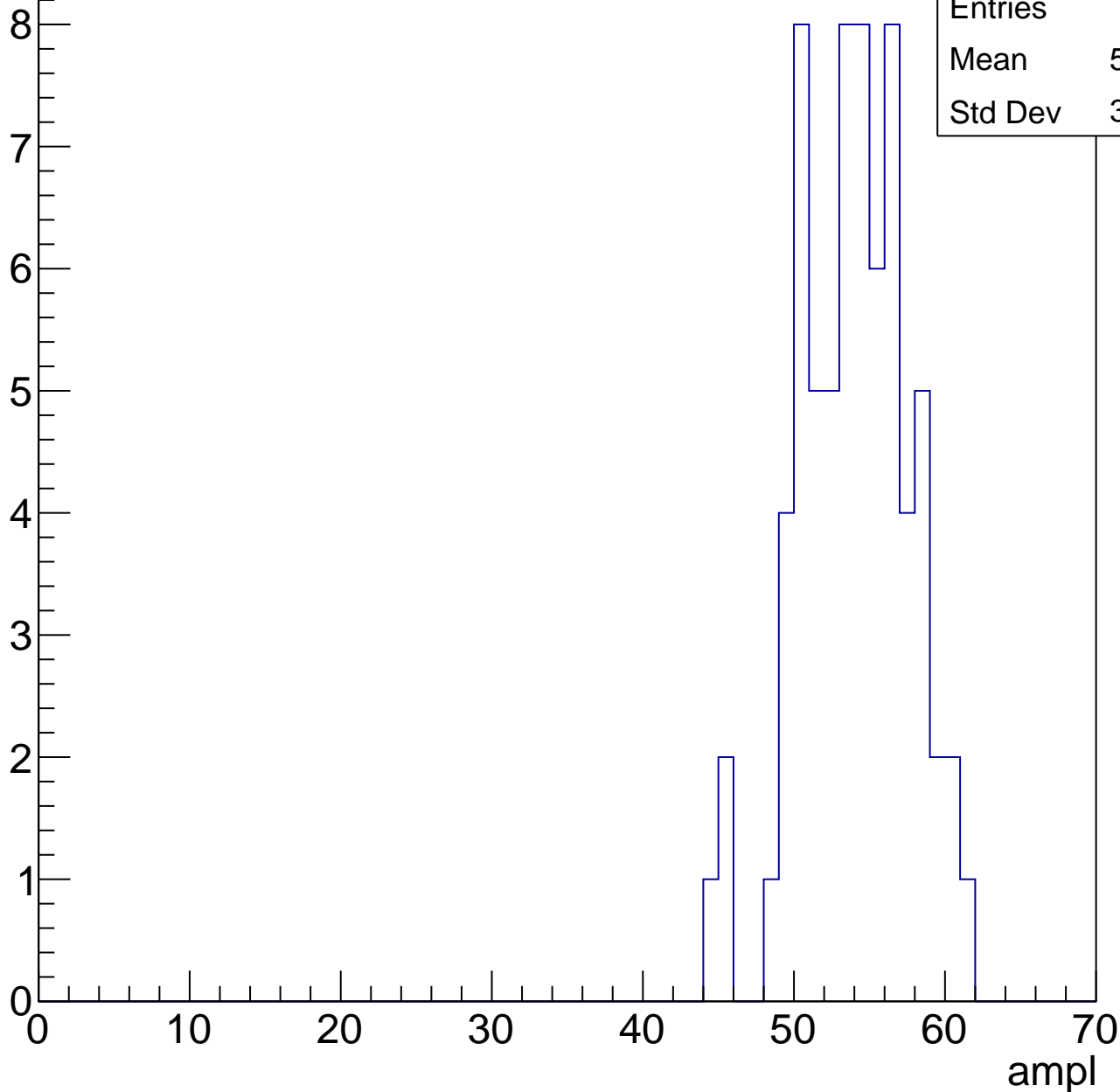


B1L103S, U23-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	53.49
Std Dev	3.597

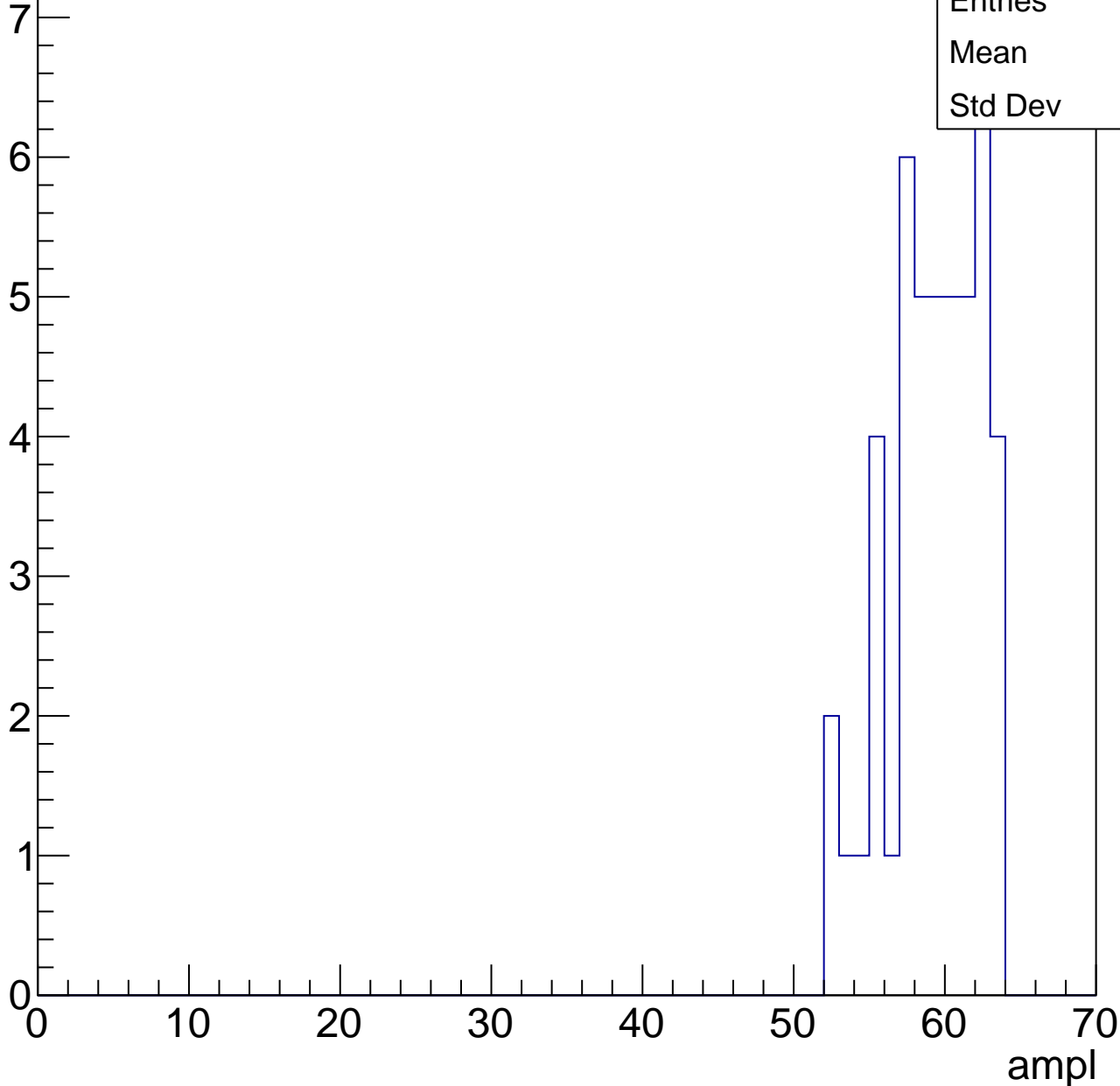


B1L103S, U23-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.8
Std Dev	2.99

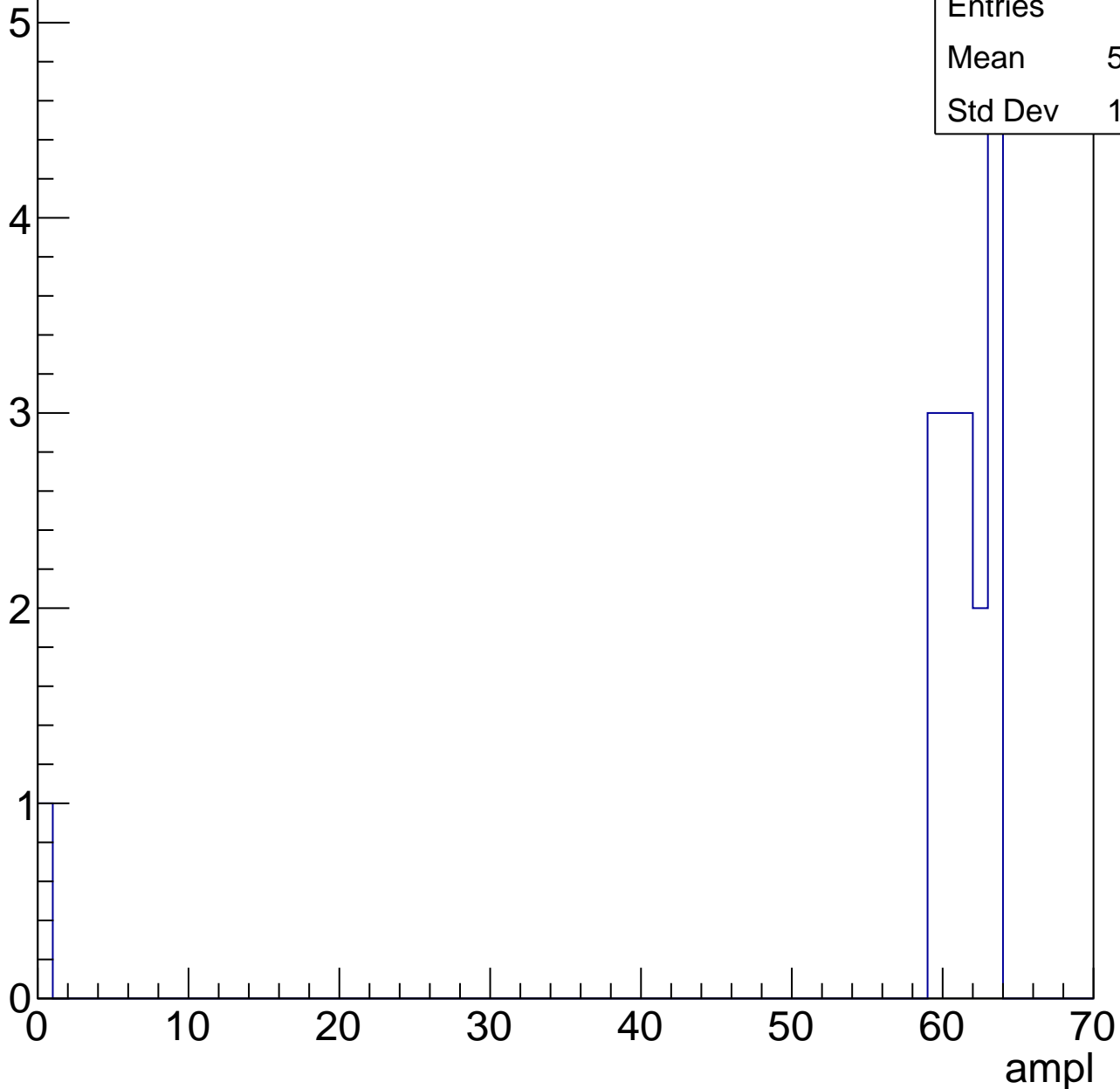


B1L103S, U23-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	57.59
Std Dev	14.47



B1L103S, U23-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	51.25
Std Dev	18.63

ampl

0 10 20 30 40 50 60 70

B1L103S, U23-ch87, adc0

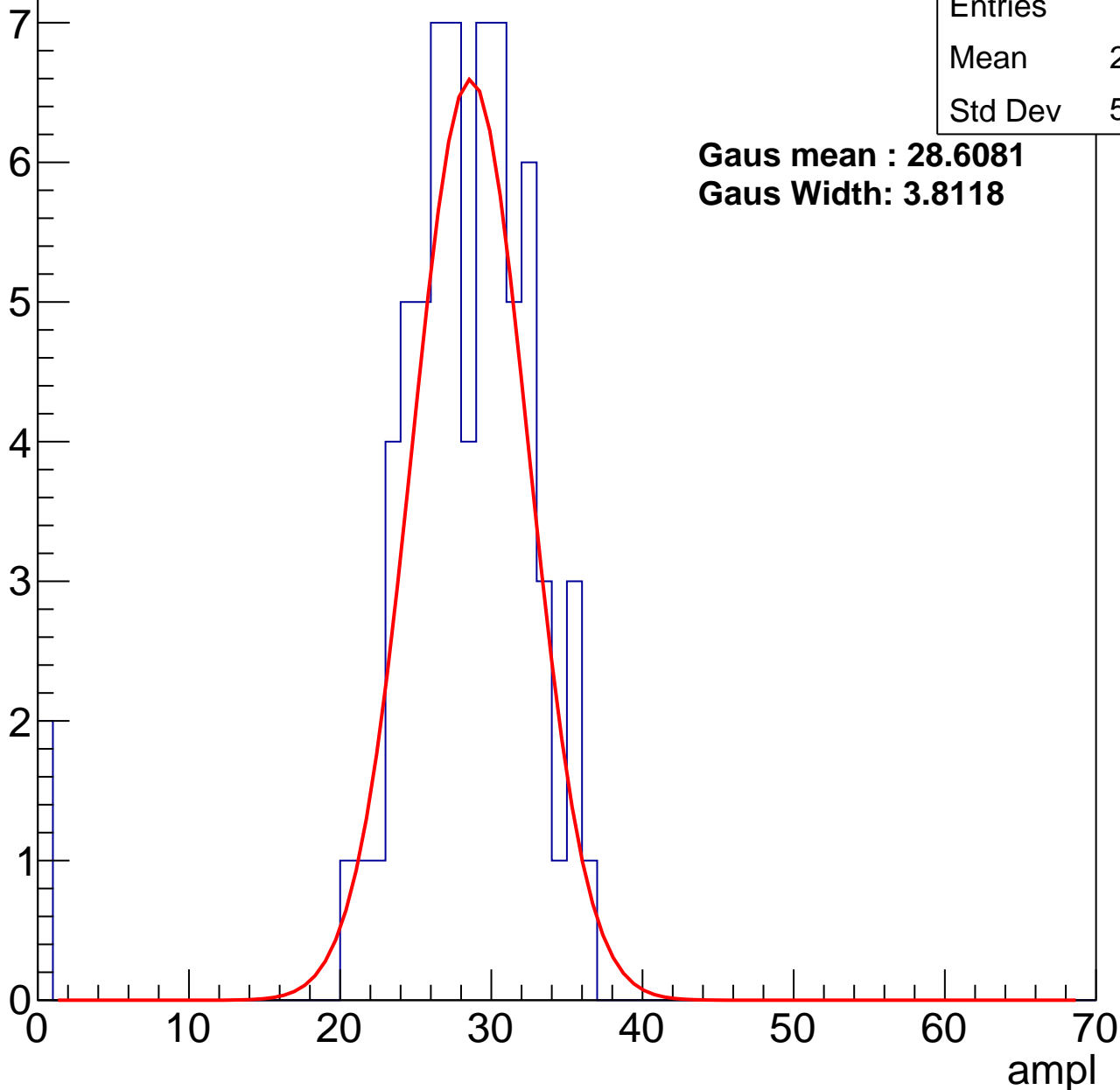
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	27.39
Std Dev	5.914

Gaus mean : 28.6081

Gaus Width: 3.8118



B1L103S, U23-ch87, adc1

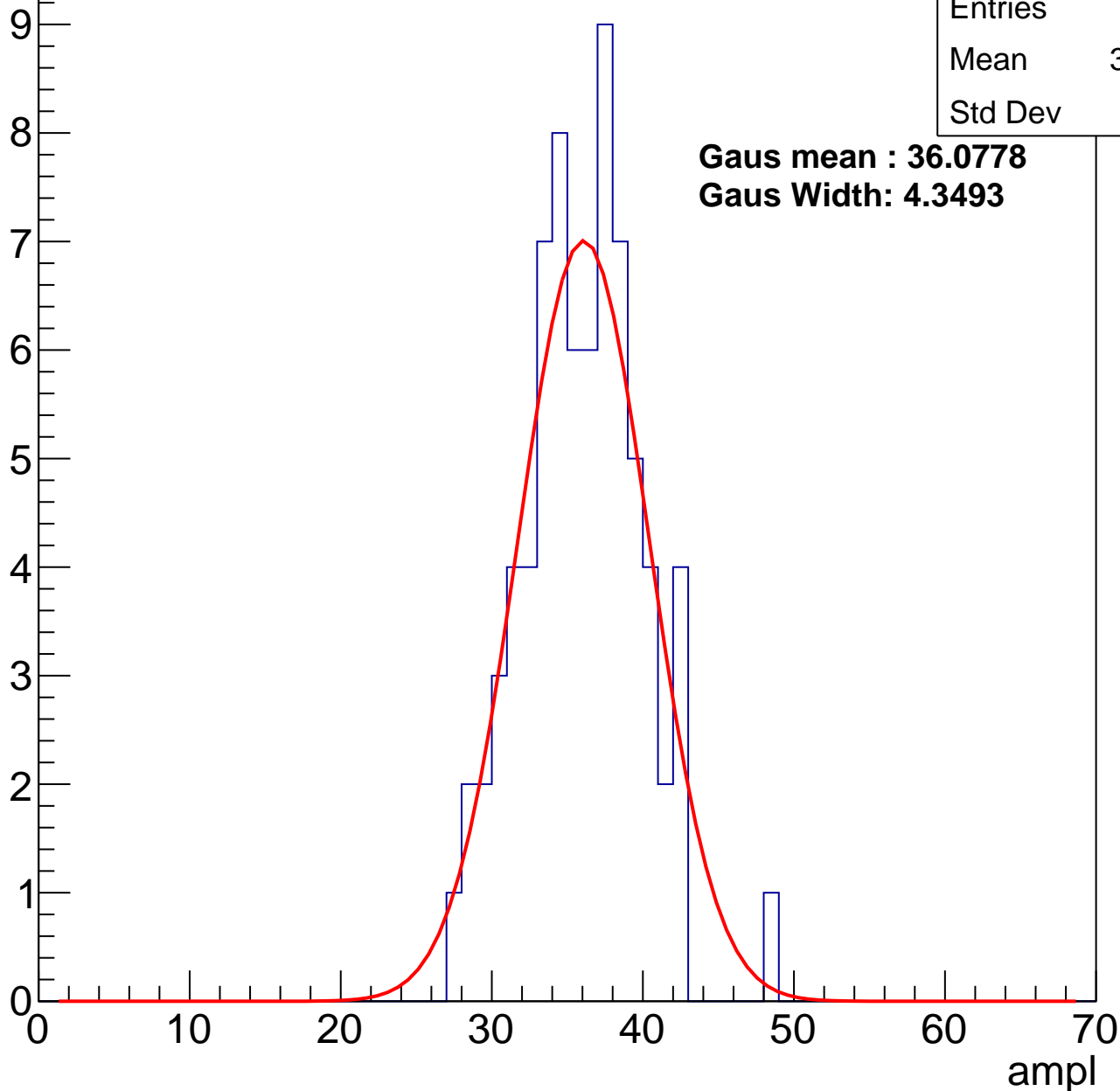
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	35.52
Std Dev	3.91

Gaus mean : 36.0778

Gaus Width: 4.3493



B1L103S, U23-ch87, adc2

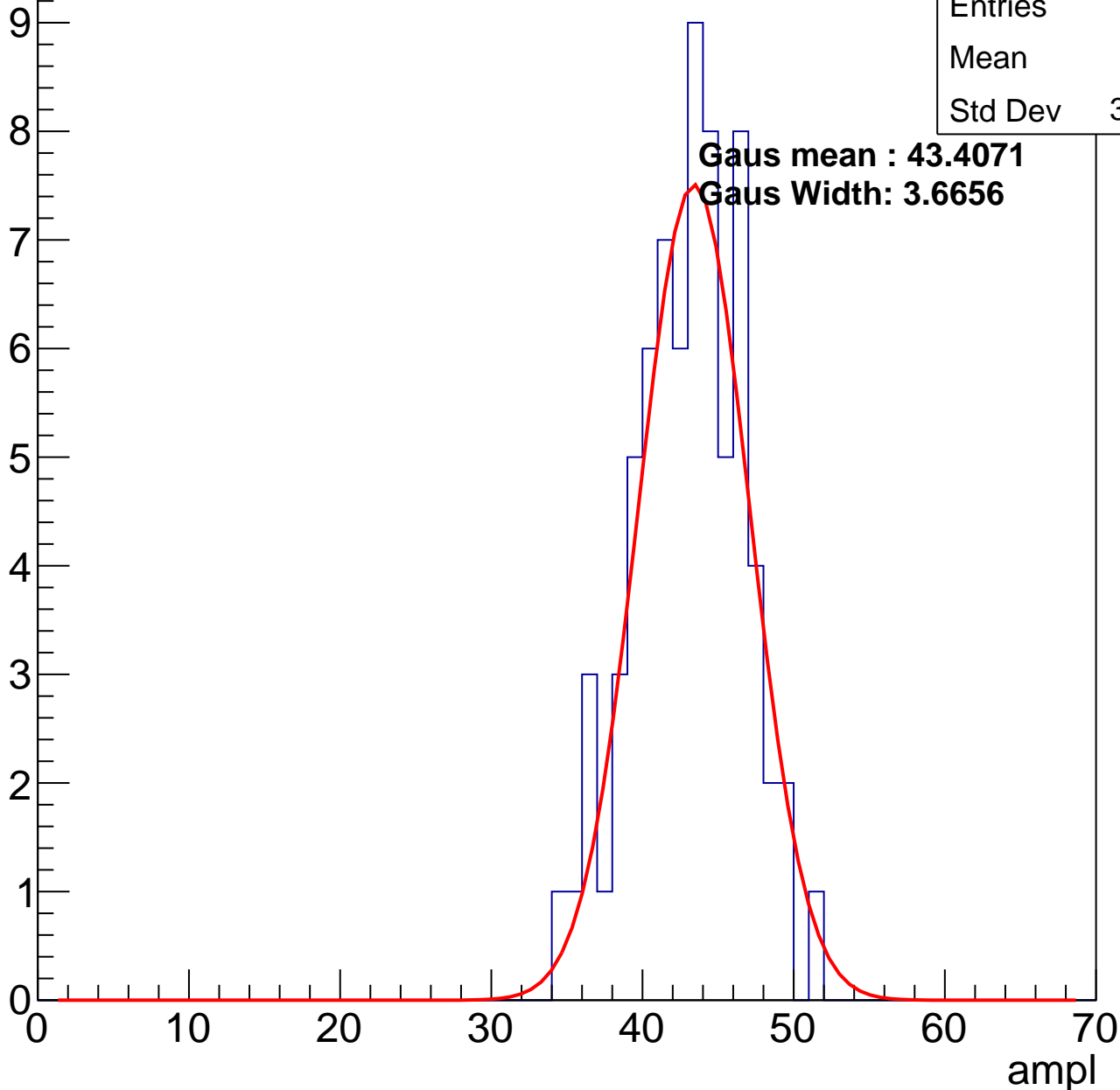
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.6
Std Dev	3.554

Gaus mean : 43.4071

Gaus Width: 3.6656

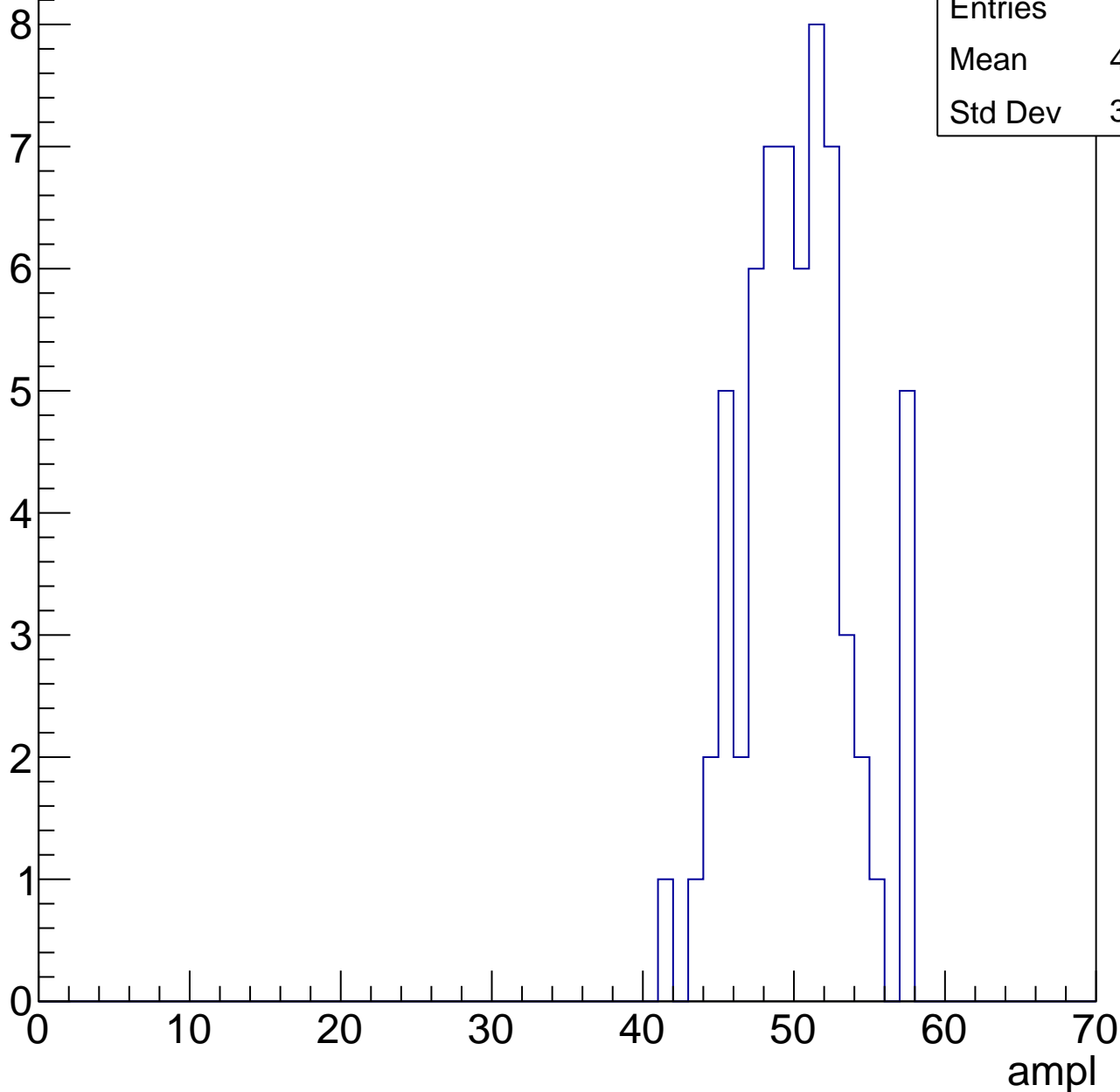


B1L103S, U23-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

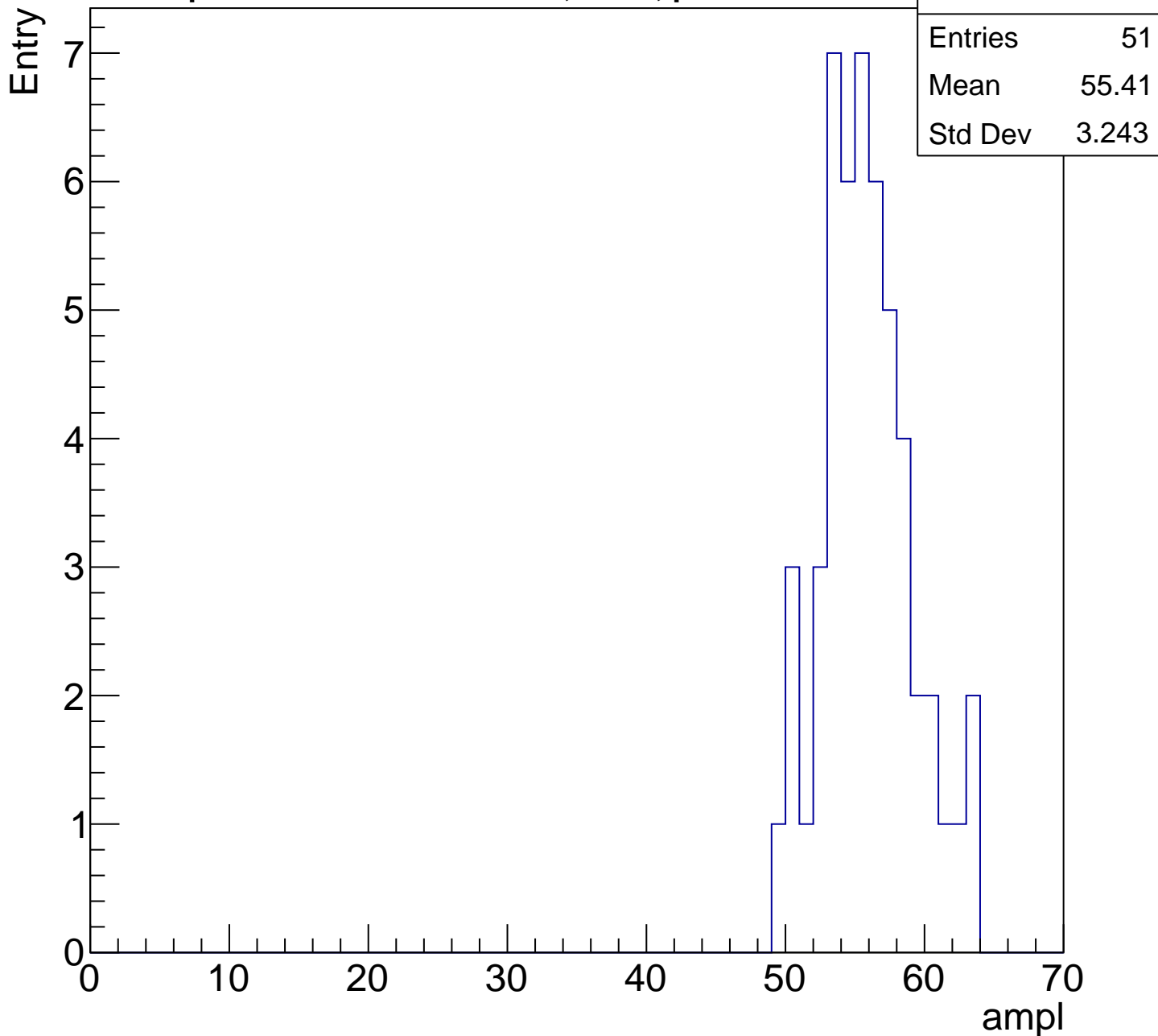
Entry

Entries	63
Mean	49.67
Std Dev	3.577



B1L103S, U23-ch87, adc4

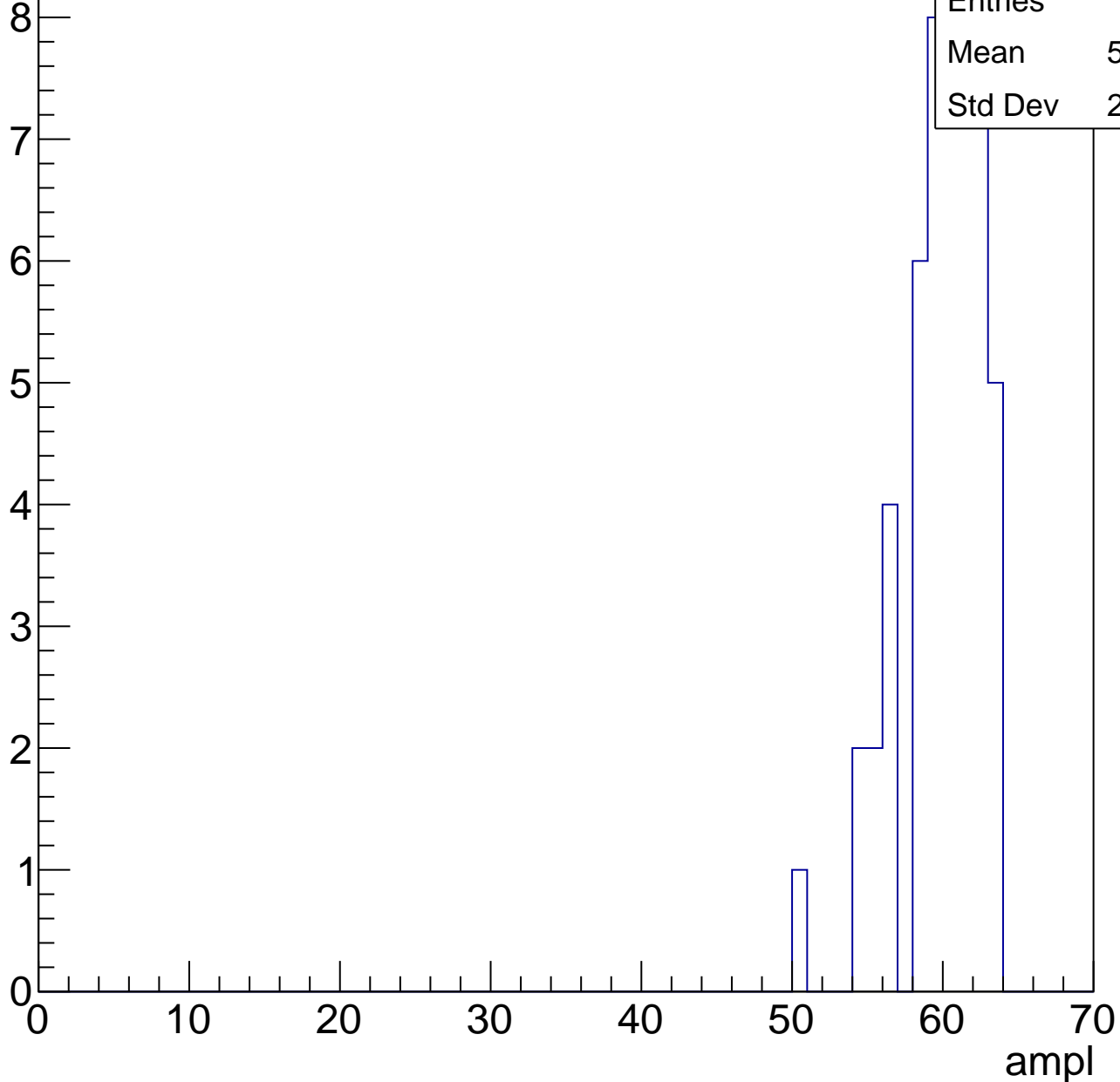
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U23-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L103S, U23-ch88, adc0

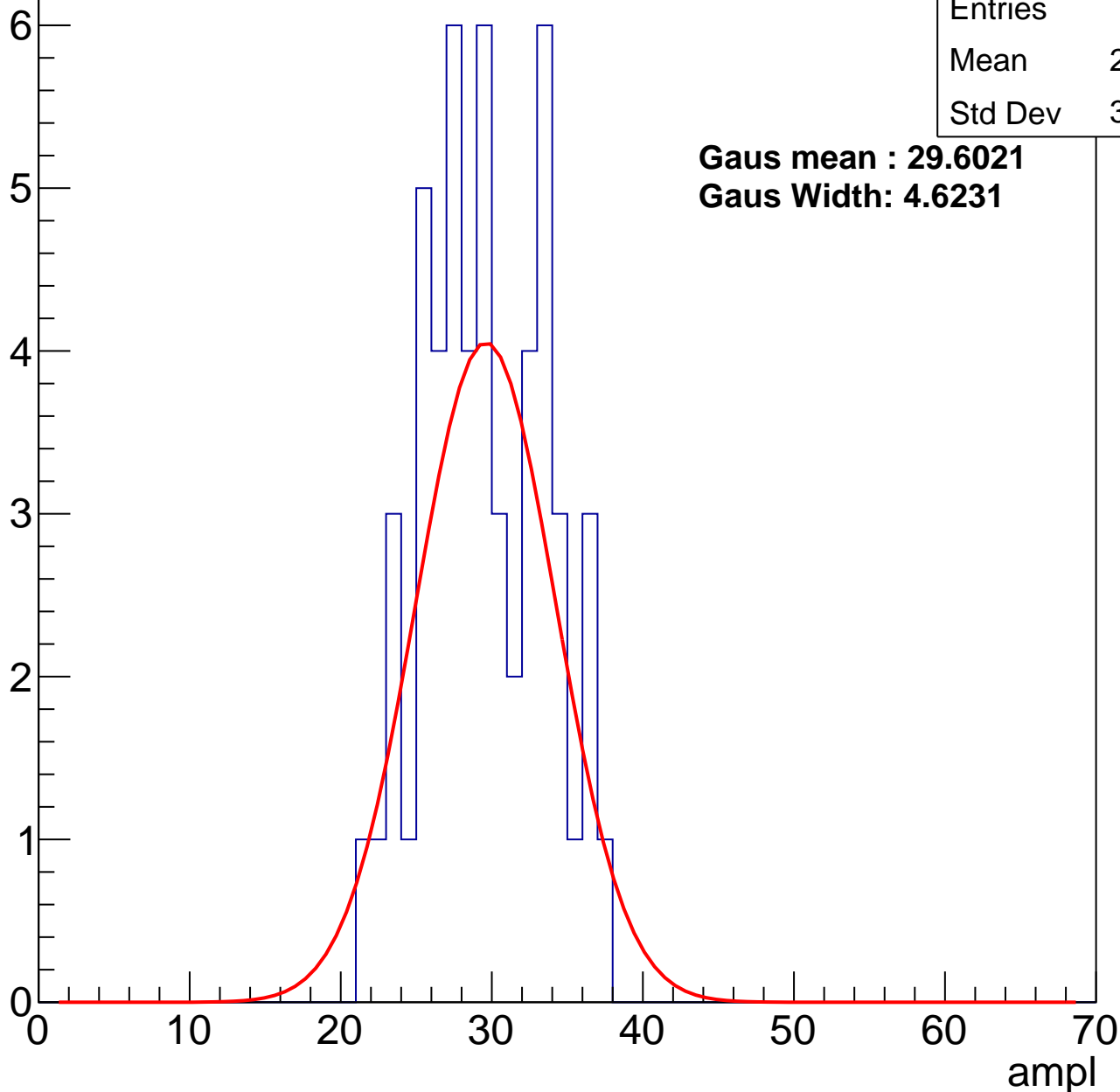
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	29.13
Std Dev	3.977

Gaus mean : 29.6021

Gaus Width: 4.6231



B1L103S, U23-ch88, adc1

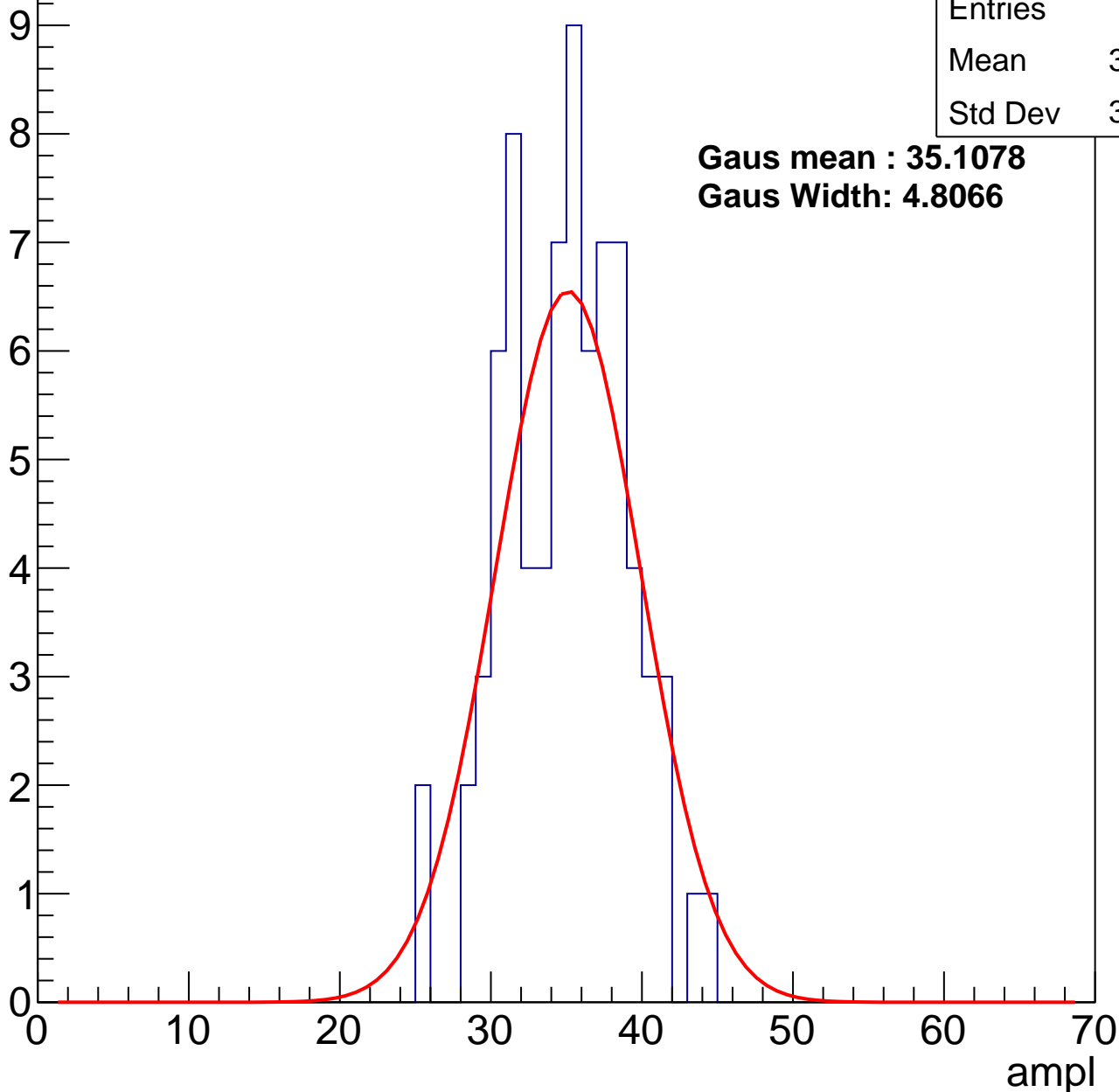
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	34.56
Std Dev	3.976

Gaus mean : 35.1078

Gaus Width: 4.8066



B1L103S, U23-ch88, adc2

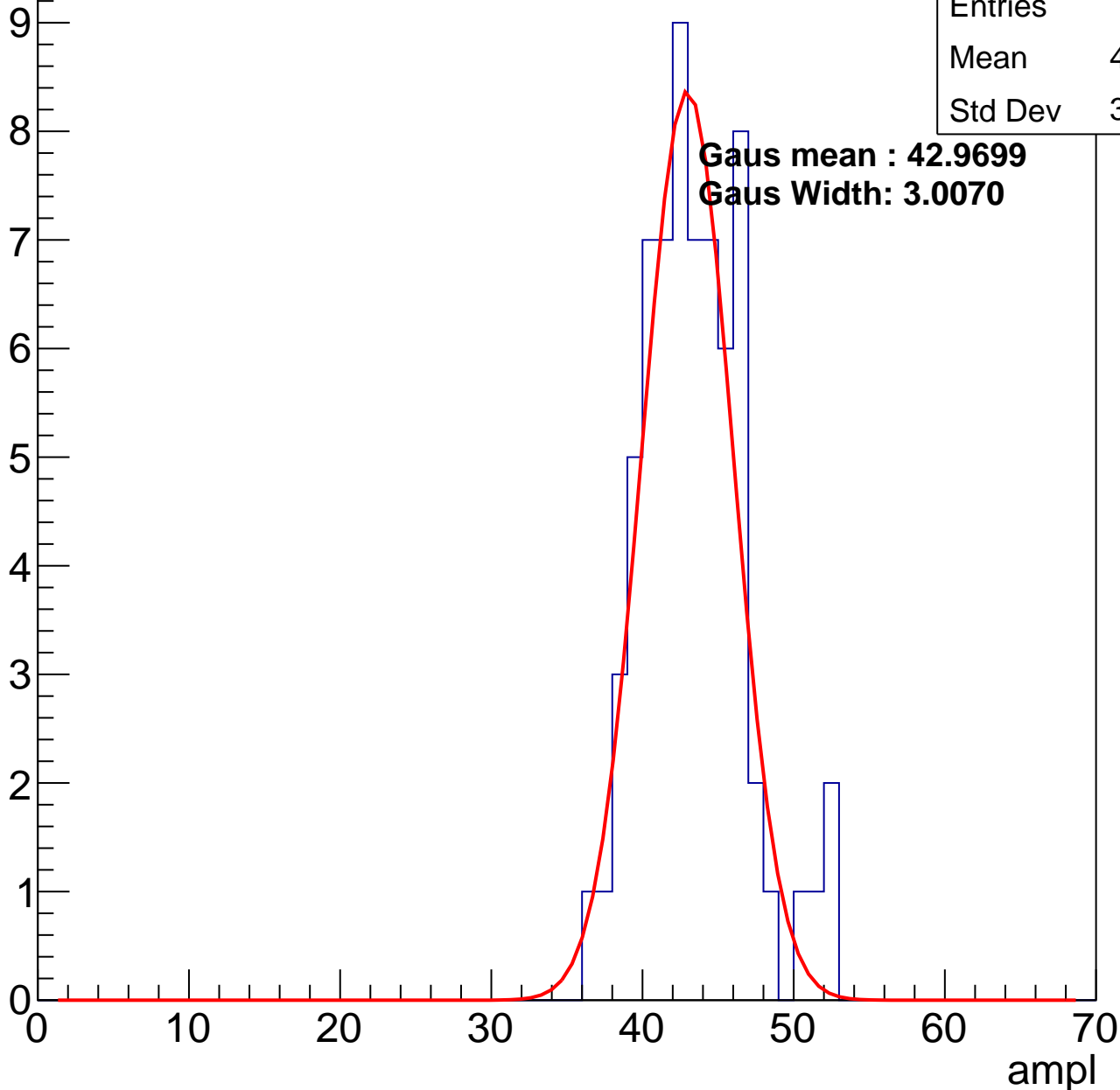
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.96
Std Dev	3.376

Gaus mean : 42.9699

Gaus Width: 3.0070

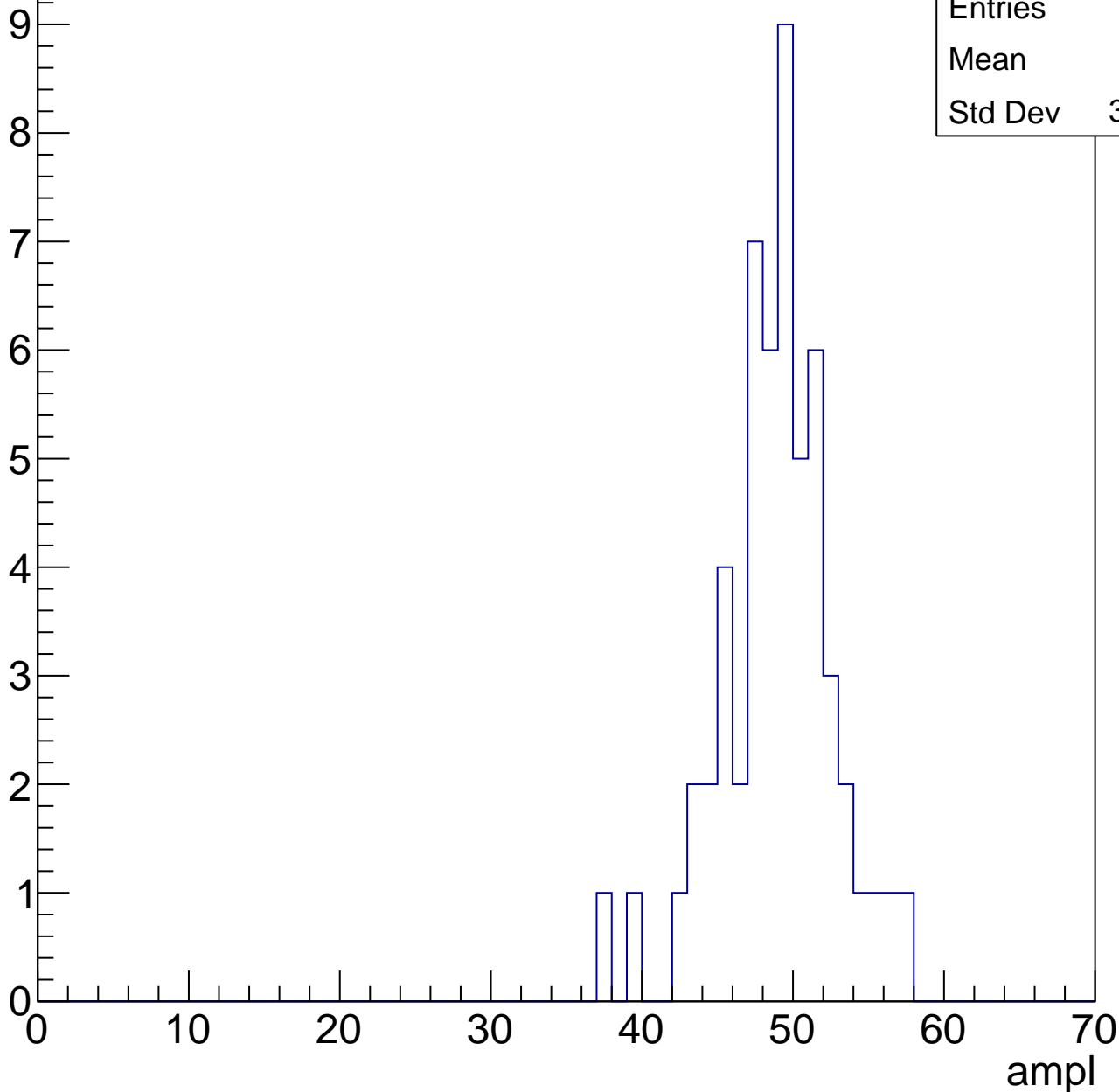


B1L103S, U23-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

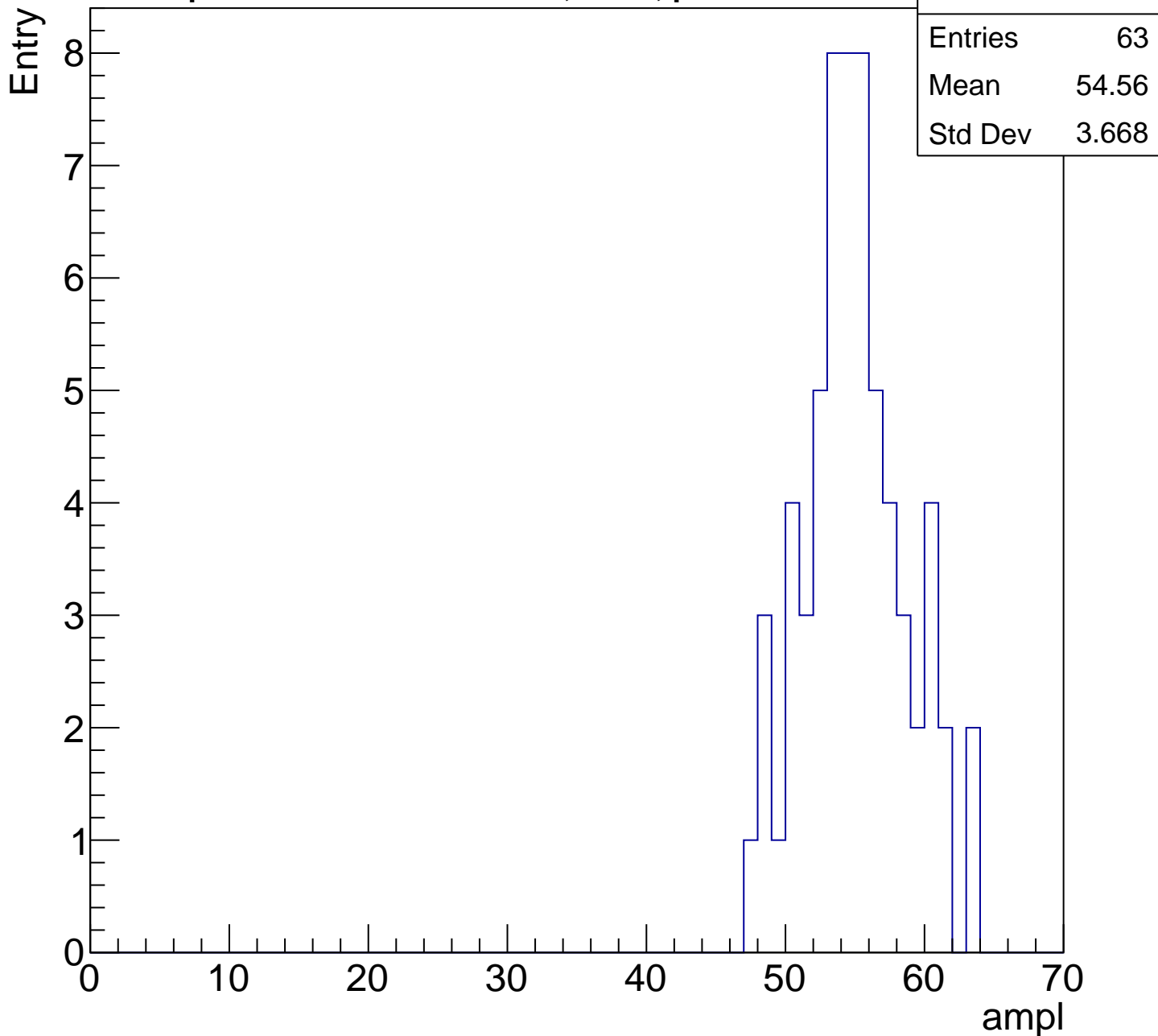
Entry

Entries	55
Mean	48.4
Std Dev	3.749



B1L103S, U23-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U23-ch88, adc5

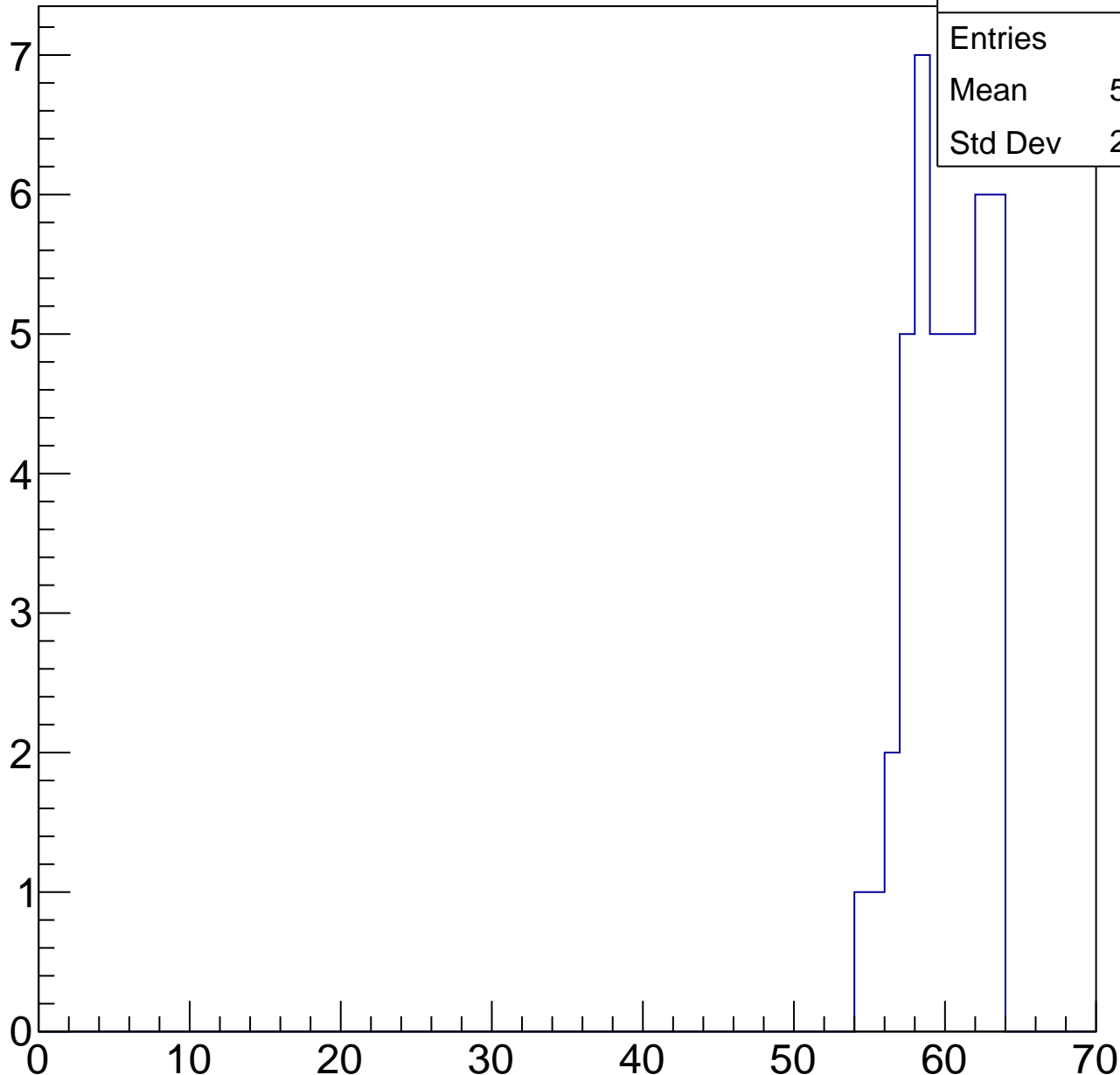
calib_packv5_042523_0143.root, FC#7, port C2

Entry

7
6
5
4
3
2
1
0

Entries	43
Mean	59.58
Std Dev	2.394

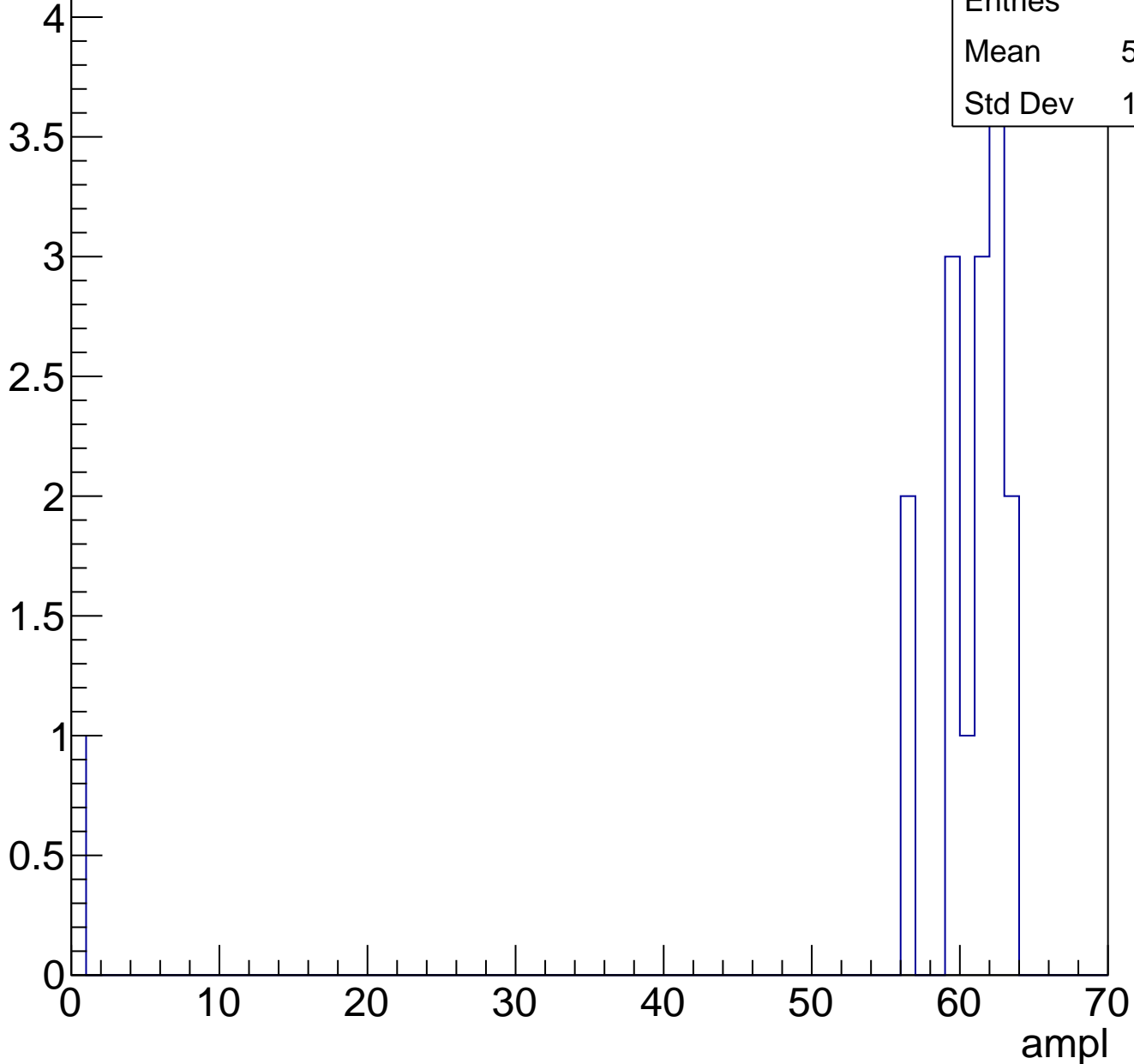
ampl



B1L103S, U23-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch89, adc0

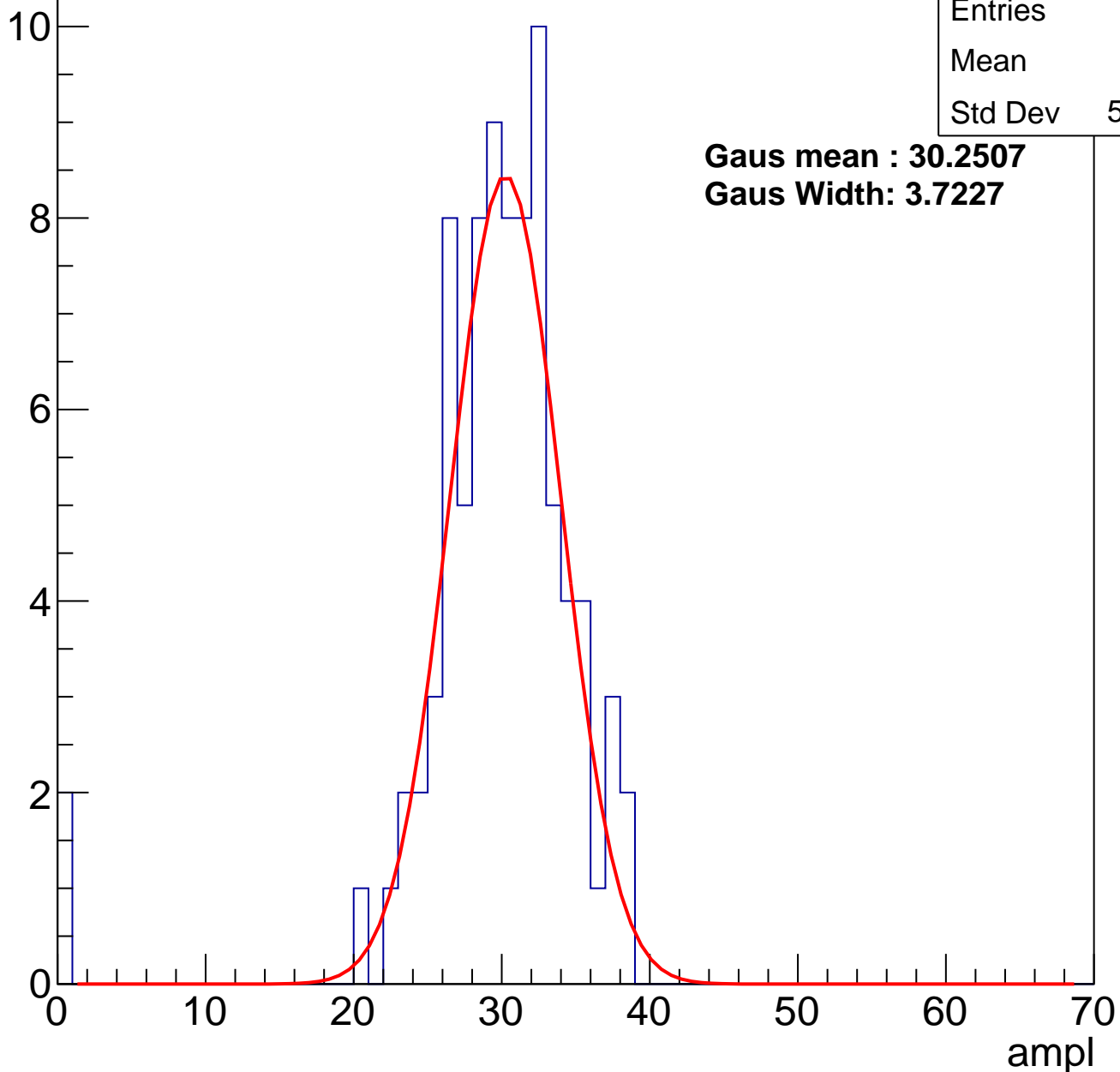
calib_packv5_042523_0143.root, FC#7, port C2

Entries	86
Mean	29.2
Std Dev	5.843

Gaus mean : 30.2507

Gaus Width: 3.7227

Entry



B1L103S, U23-ch89, adc1

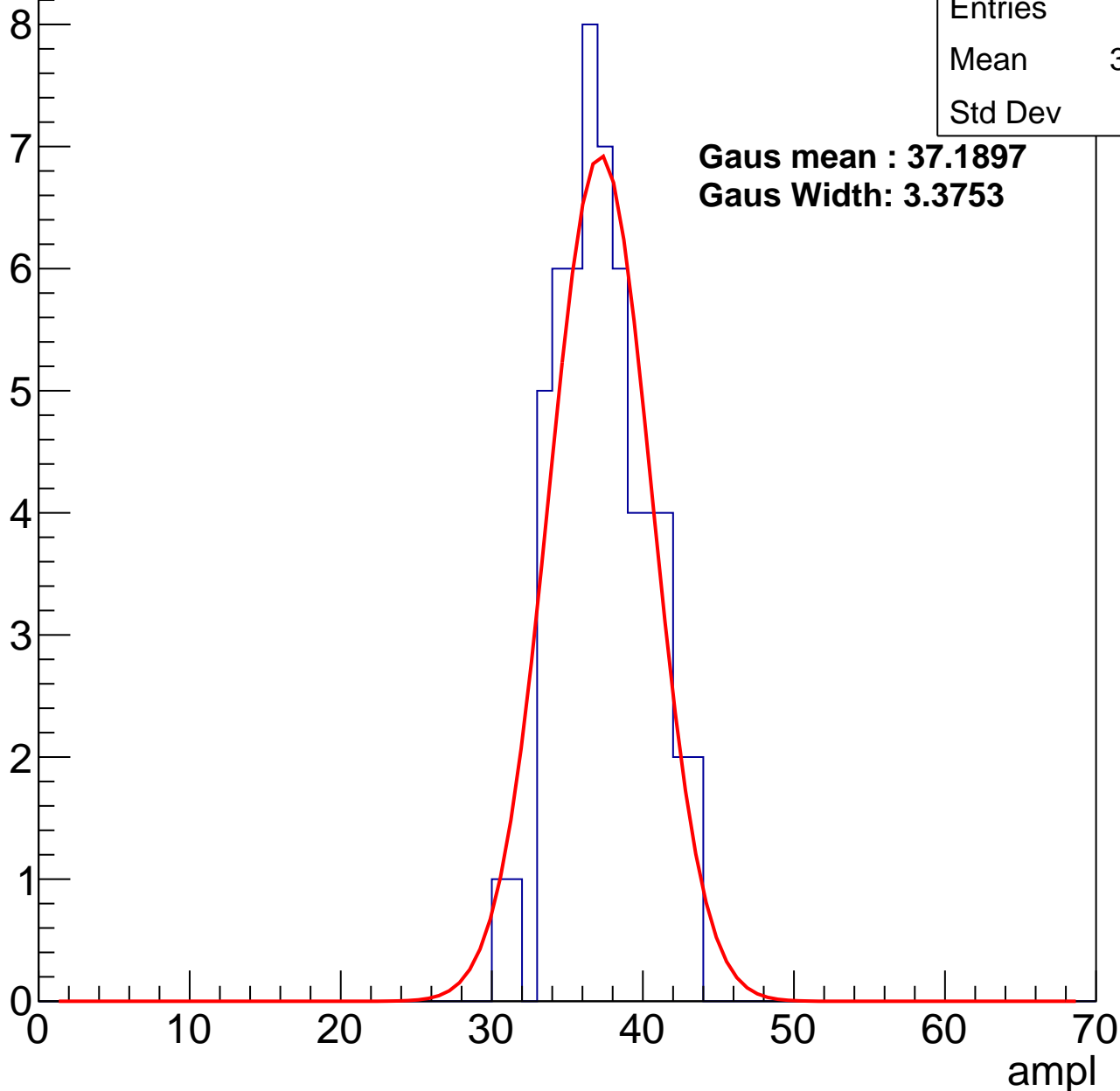
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	36.88
Std Dev	2.97

Gaus mean : 37.1897

Gaus Width: 3.3753



B1L103S, U23-ch89, adc2

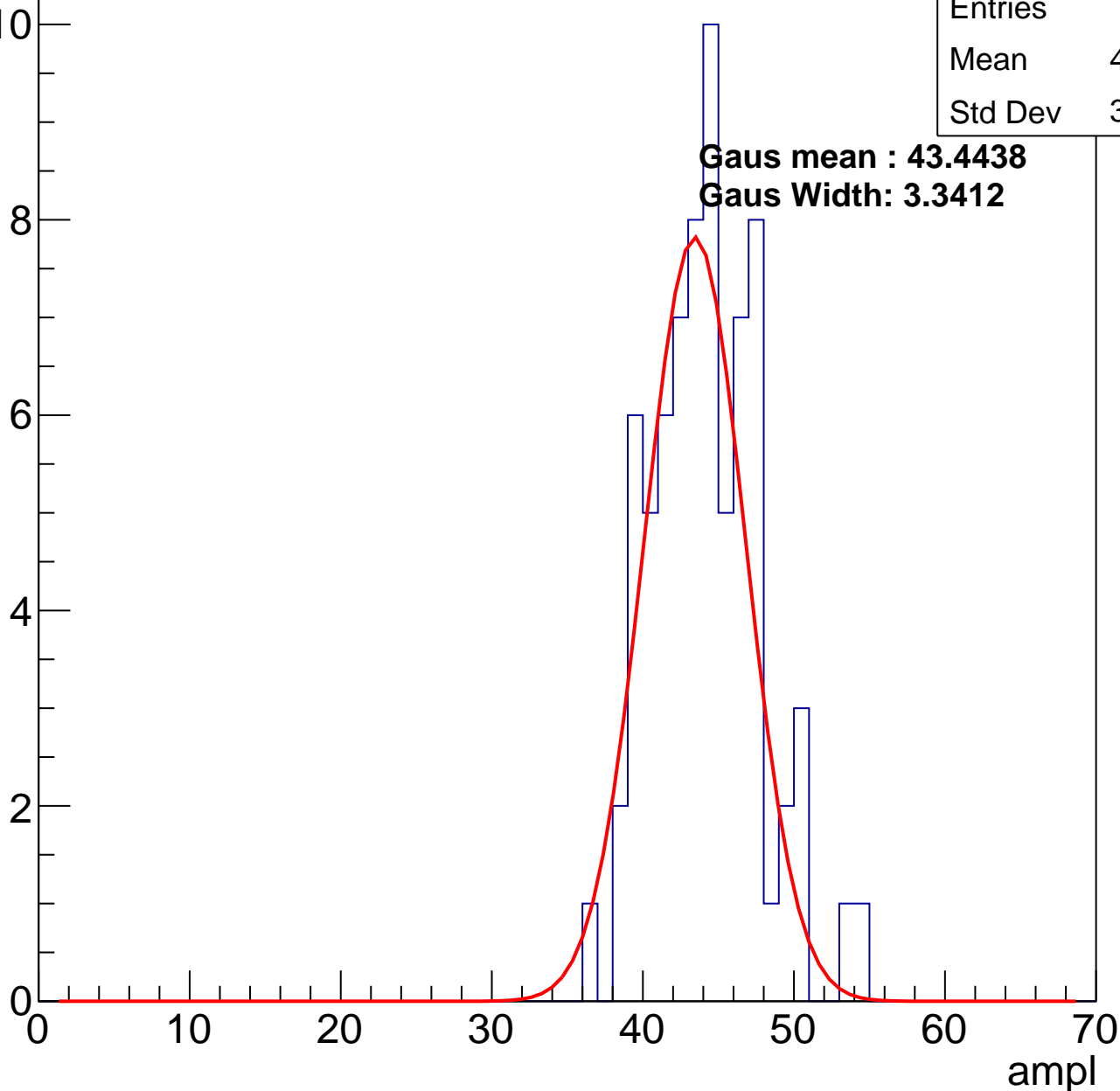
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.78
Std Dev	3.536

Gaus mean : 43.4438

Gaus Width: 3.3412

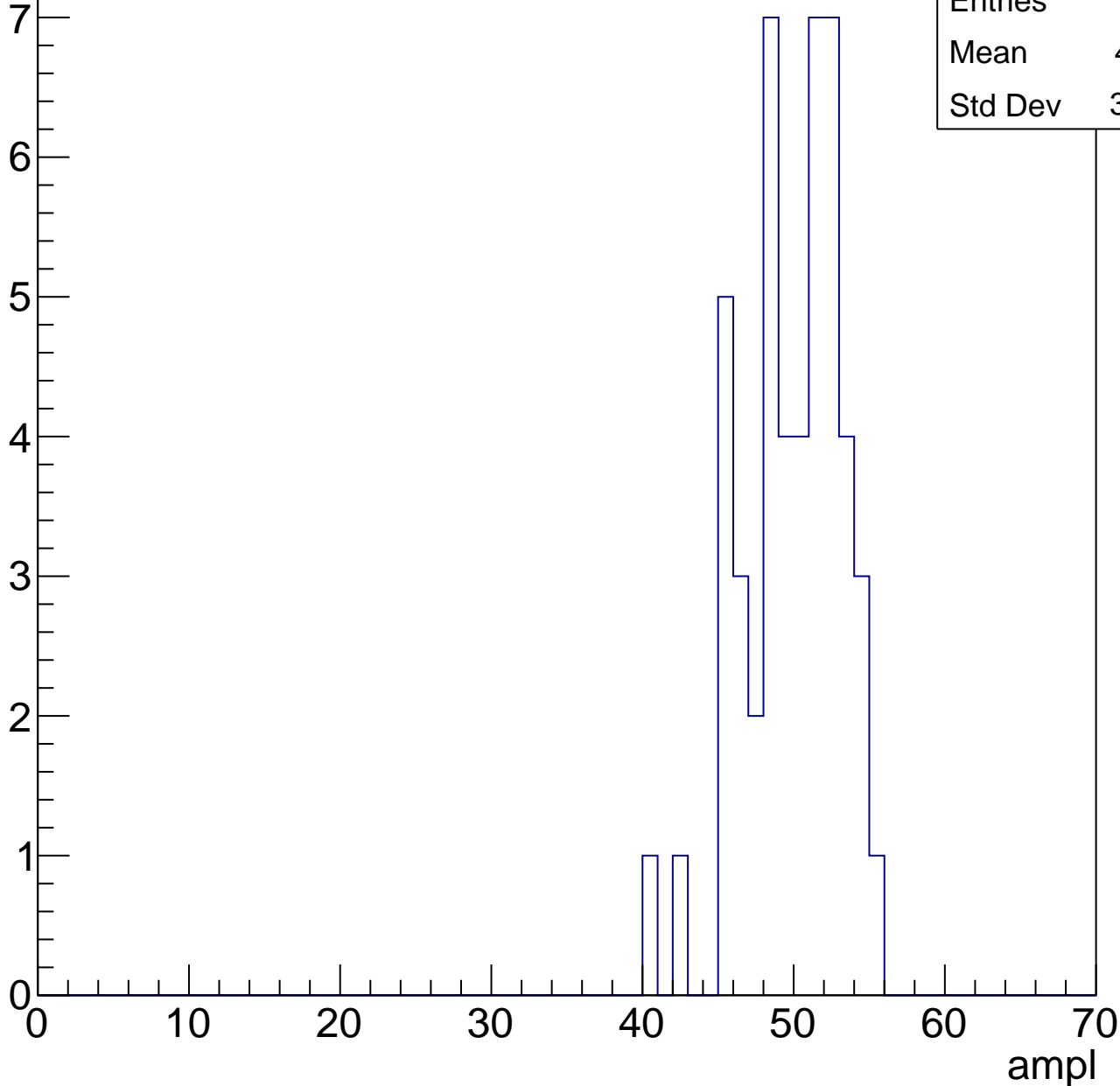


B1L103S, U23-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	49.41
Std Dev	3.238

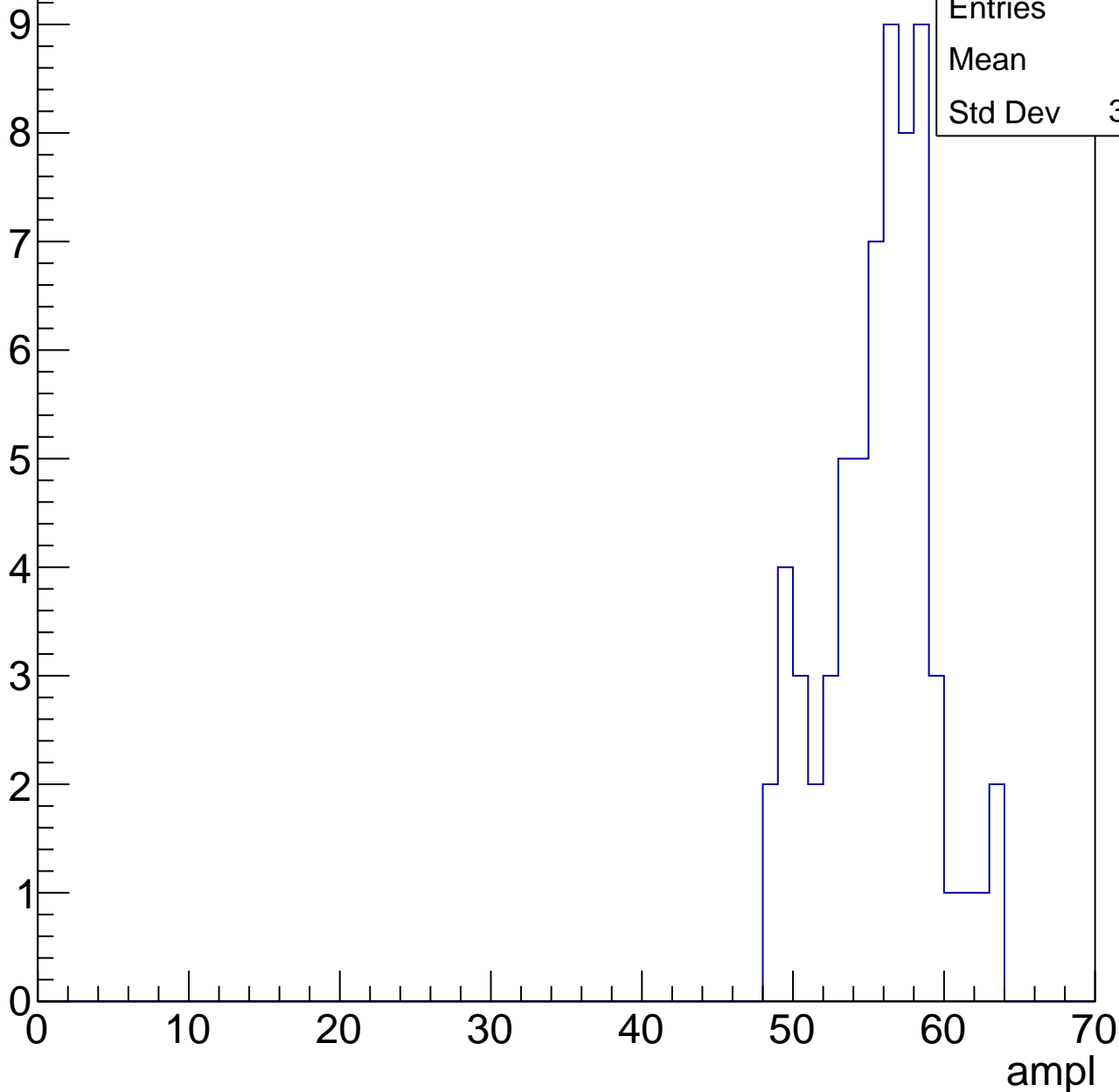


B1L103S, U23-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.2
Std Dev	3.522



B1L103S, U23-ch89, adc5

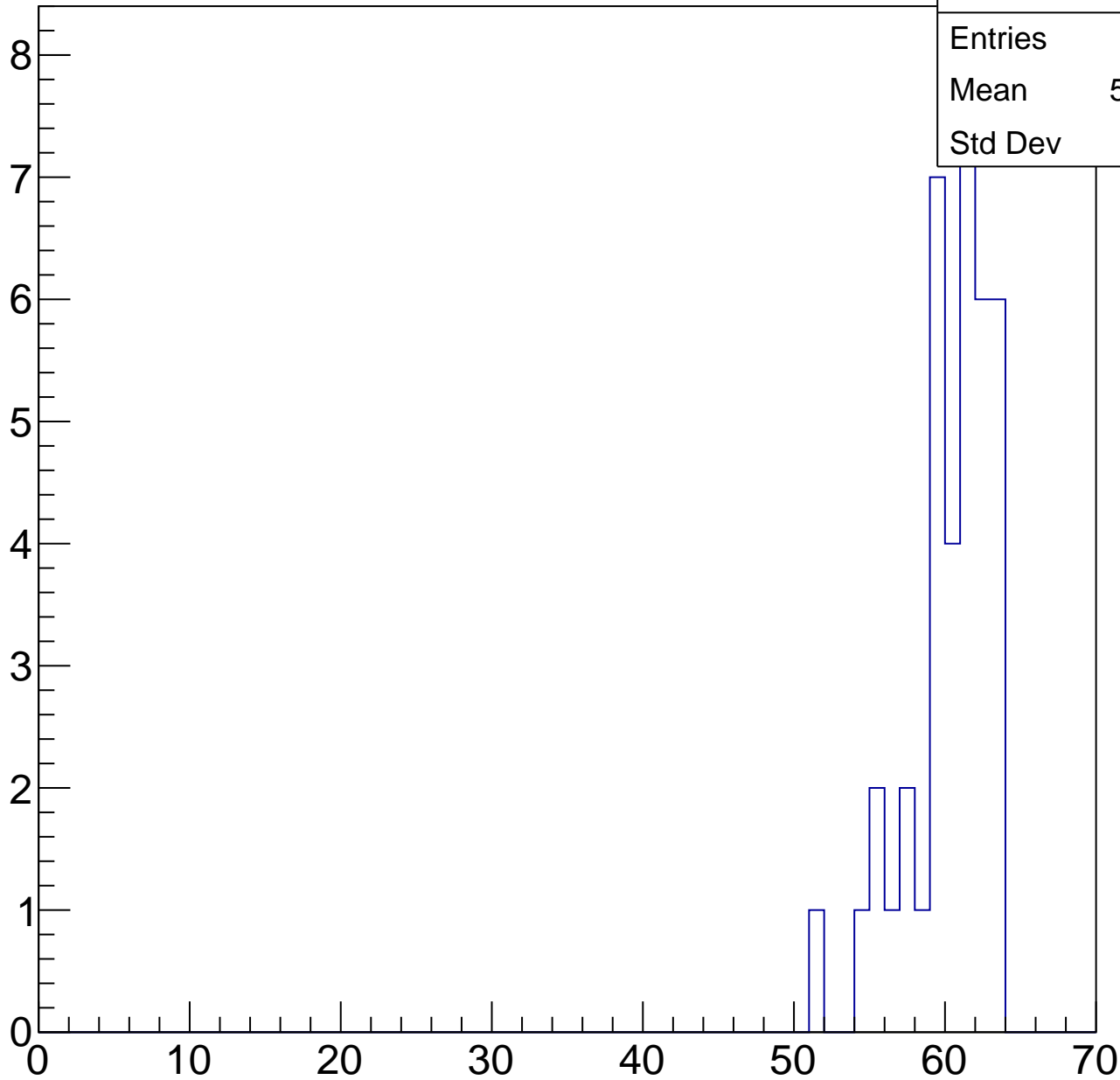
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.85
Std Dev	2.76

ampl



B1L103S, U23-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

5

4

3

2

1

0

Entries

12

Mean

56.08

Std Dev

16.95

ampl

0

10

20

30

40

50

60

70

B1L103S, U23-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch90, adc0

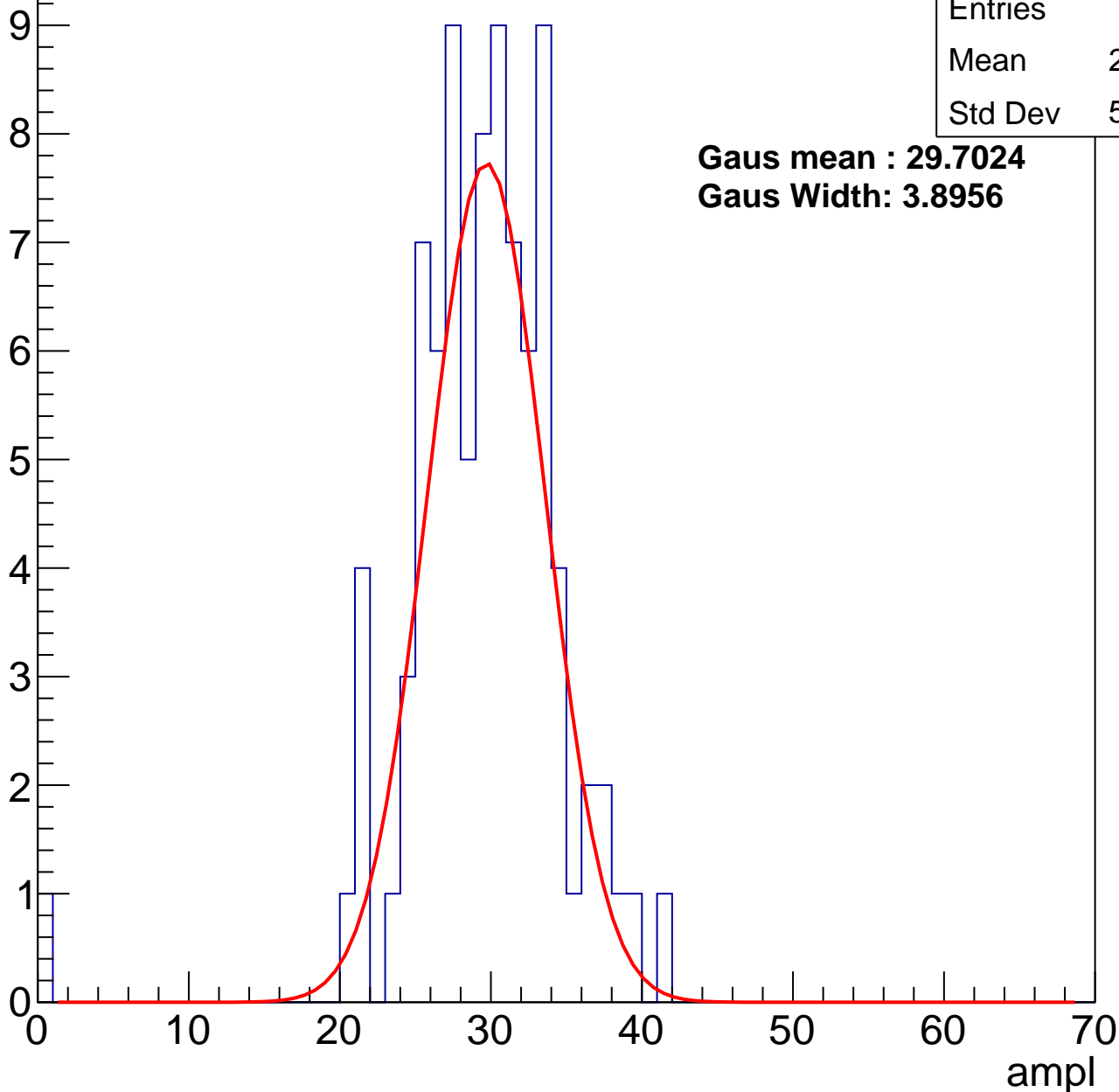
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	88
Mean	29.05
Std Dev	5.242

Gaus mean : 29.7024

Gaus Width: 3.8956



B1L103S, U23-ch90, adc1

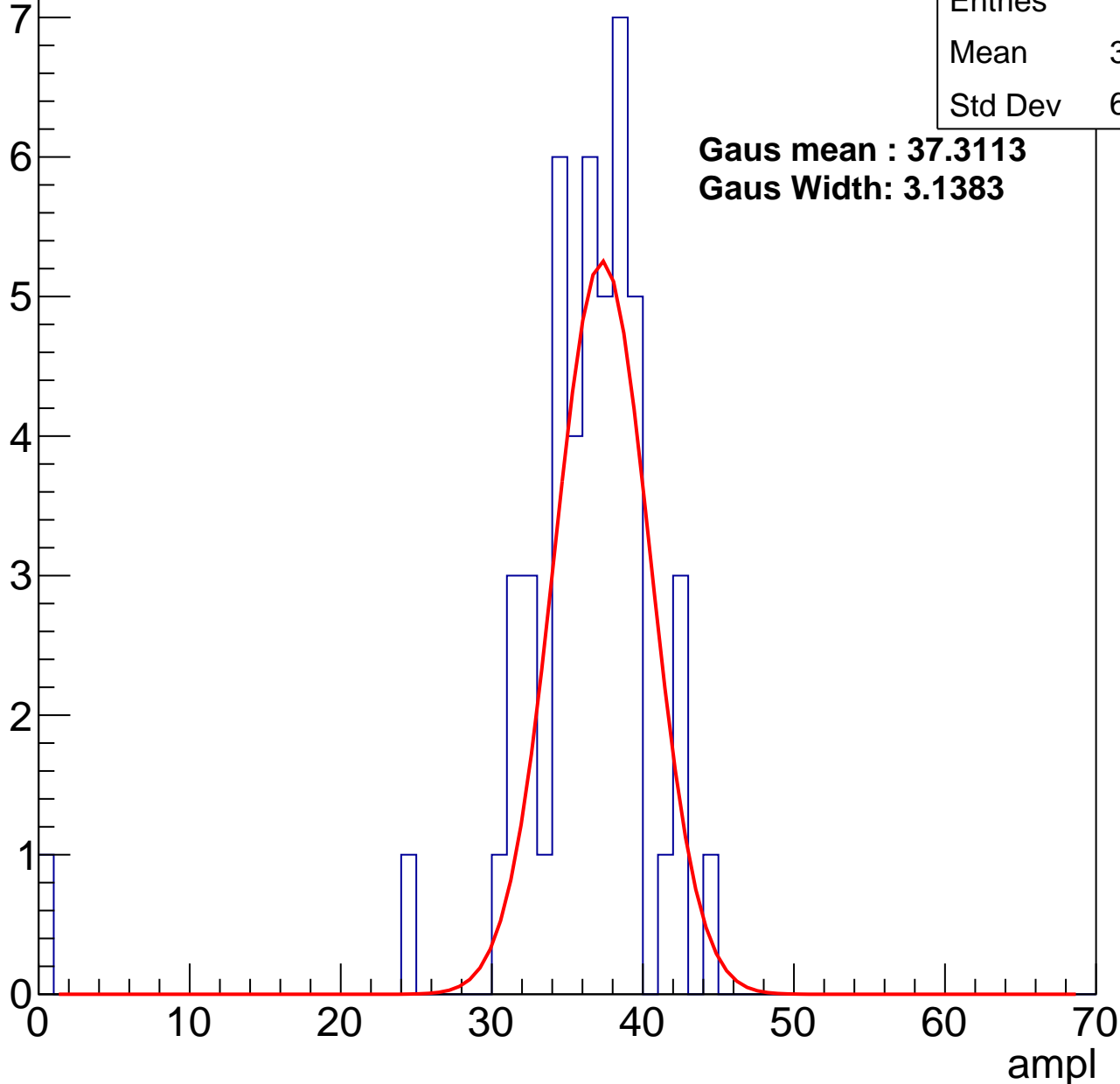
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	35.27
Std Dev	6.267

Gaus mean : 37.3113

Gaus Width: 3.1383



B1L103S, U23-ch90, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	42.21
Std Dev	3.901

Gaus mean : 43.4785

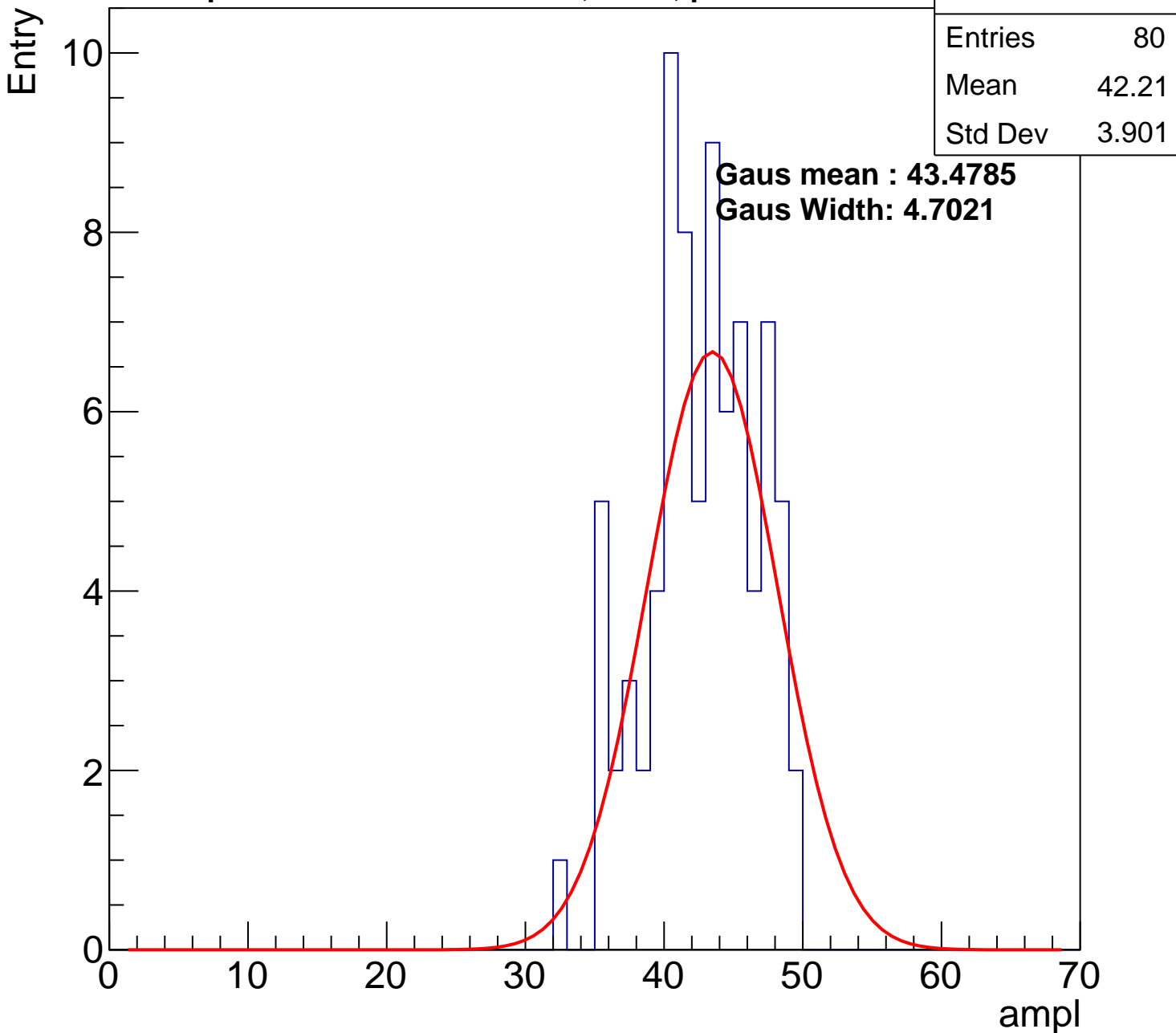
Gaus Width: 4.7021

Entry

10
8
6
4
2
0

ampl

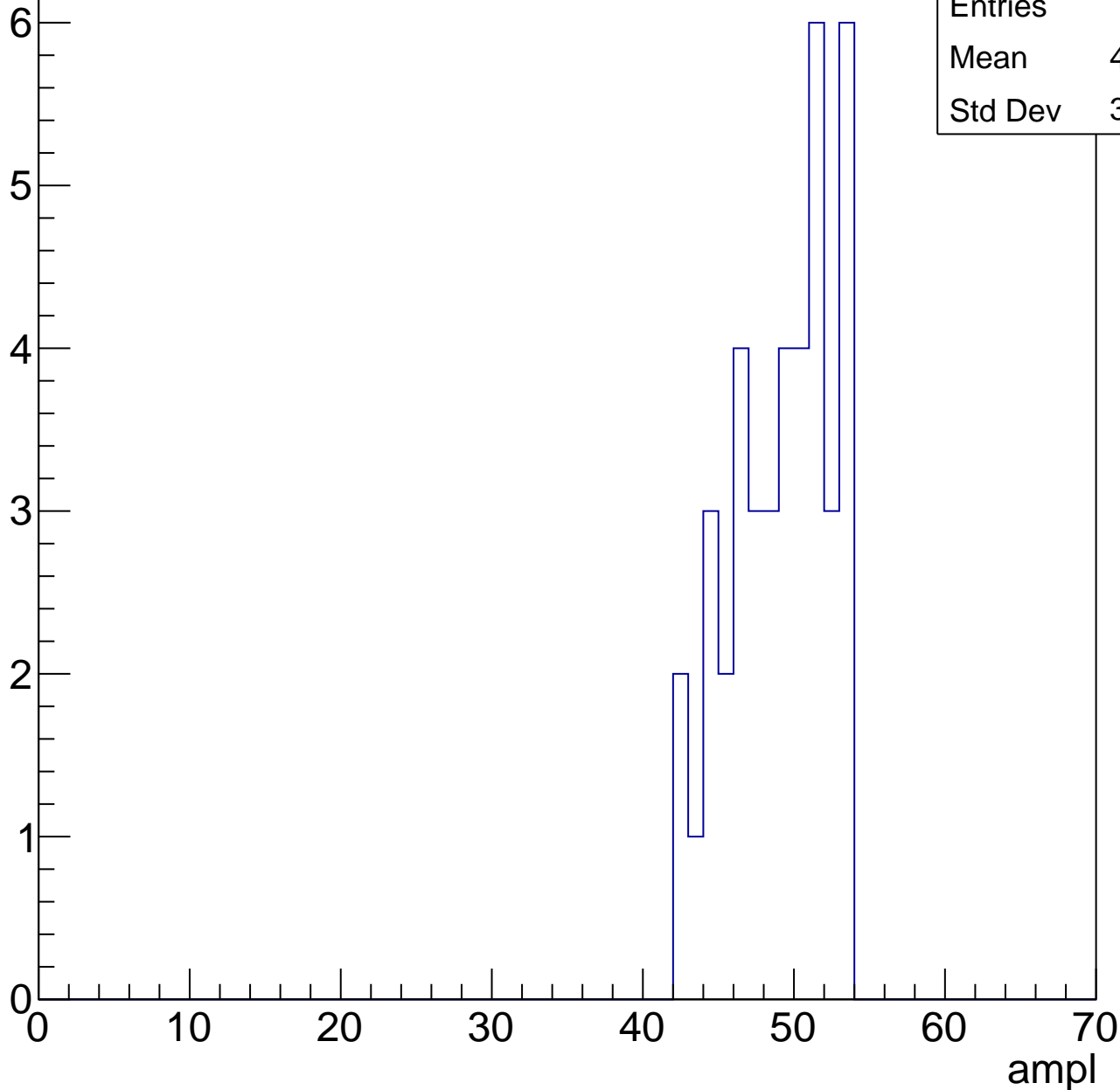
0 10 20 30 40 50 60 70



B1L103S, U23-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



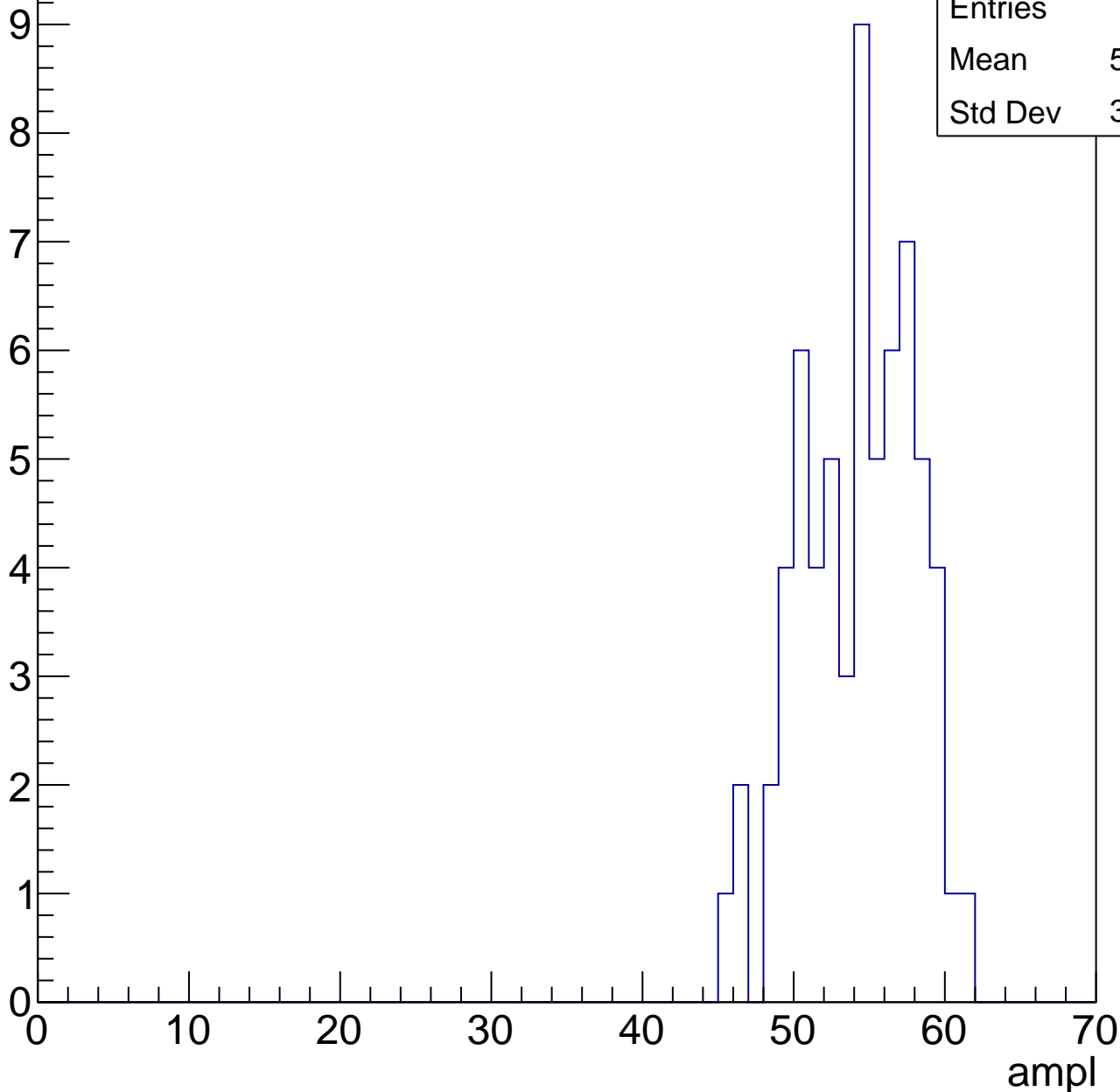
Entries	41
Mean	48.63
Std Dev	3.267

B1L103S, U23-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	53.77
Std Dev	3.699

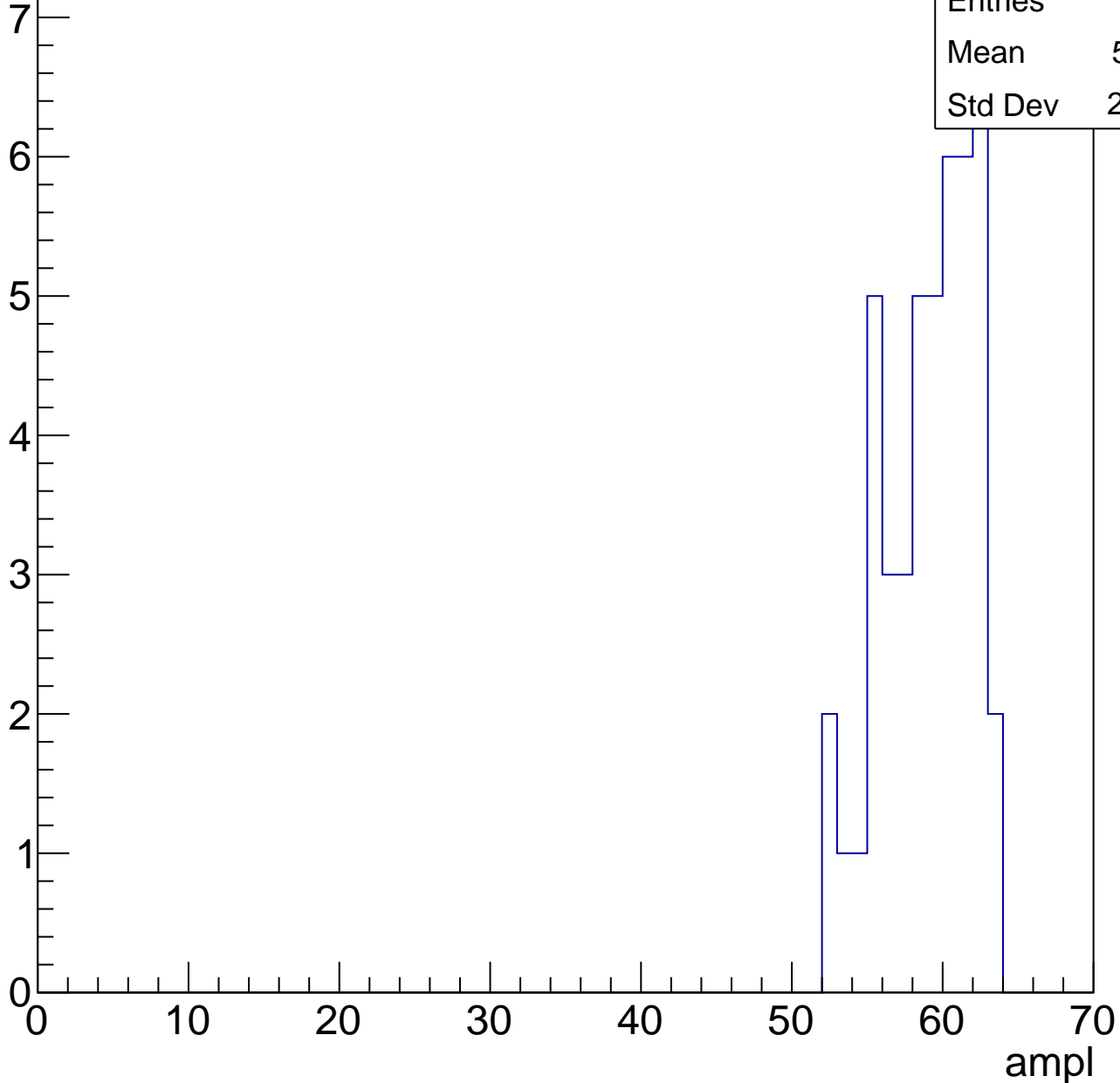


B1L103S, U23-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.61
Std Dev	2.952

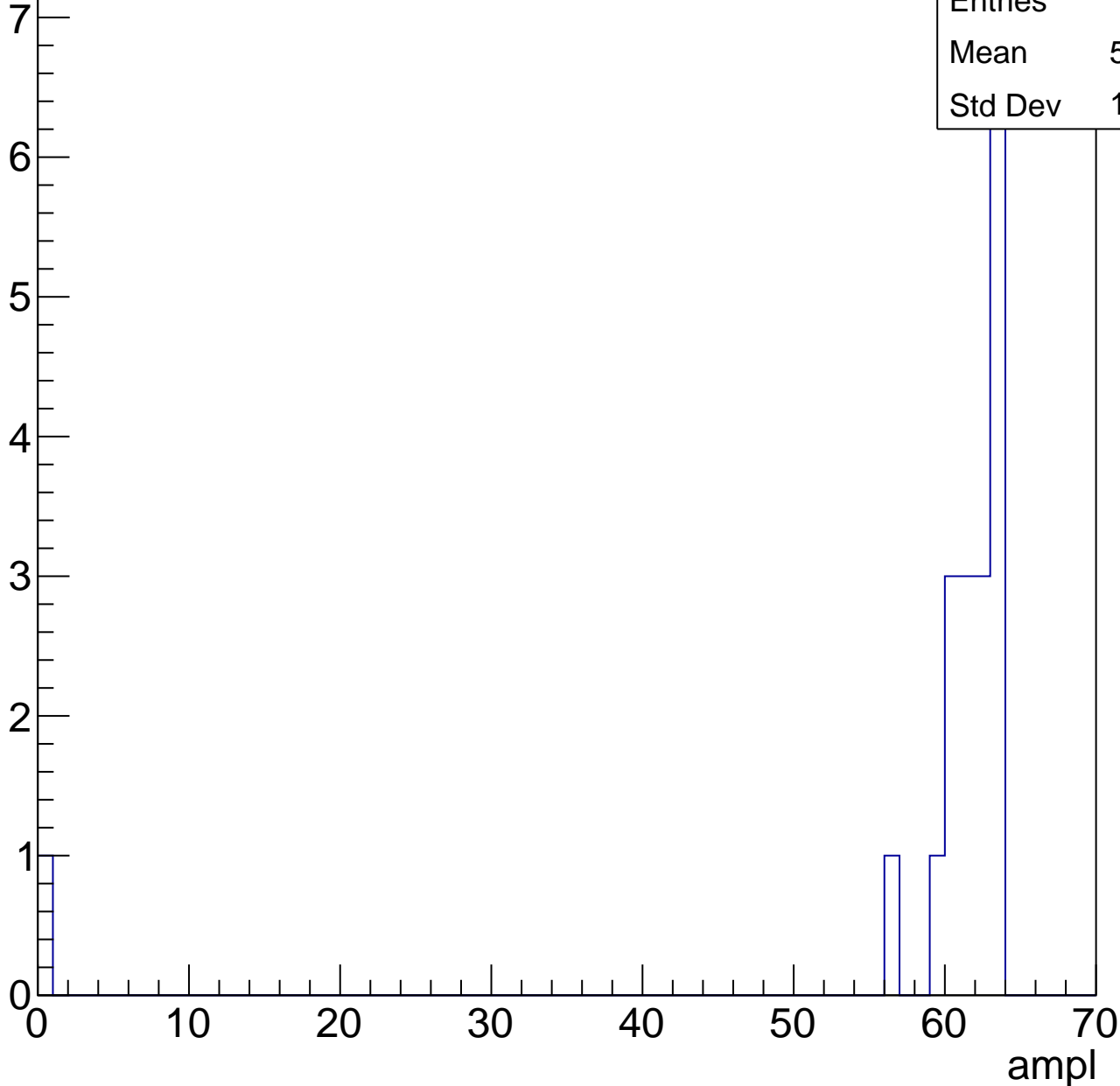


B1L103S, U23-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	58.16
Std Dev	13.82



B1L103S, U23-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch91, adc0

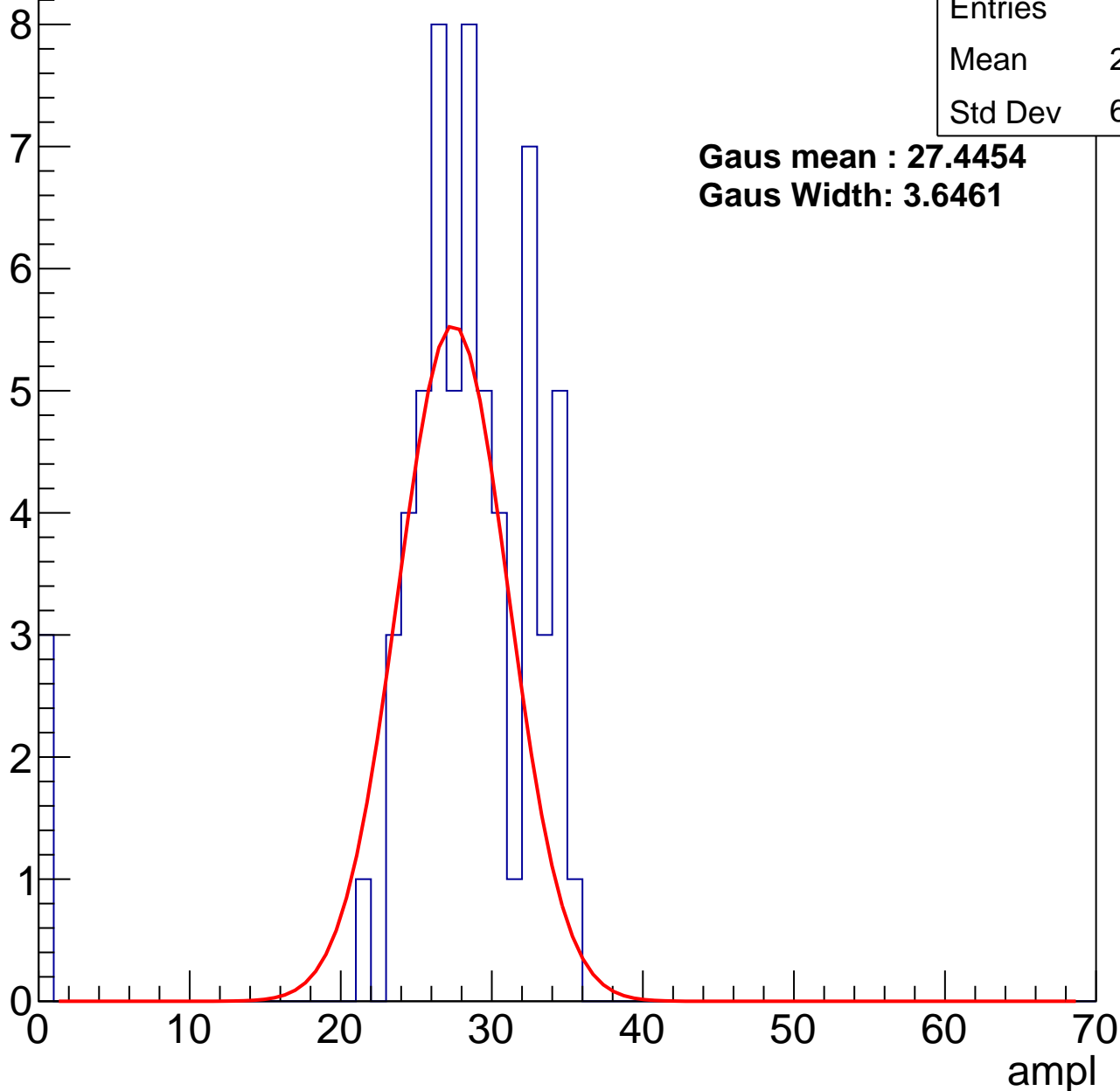
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	27.02
Std Dev	6.909

Gaus mean : 27.4454

Gaus Width: 3.6461



B1L103S, U23-ch91, adc1

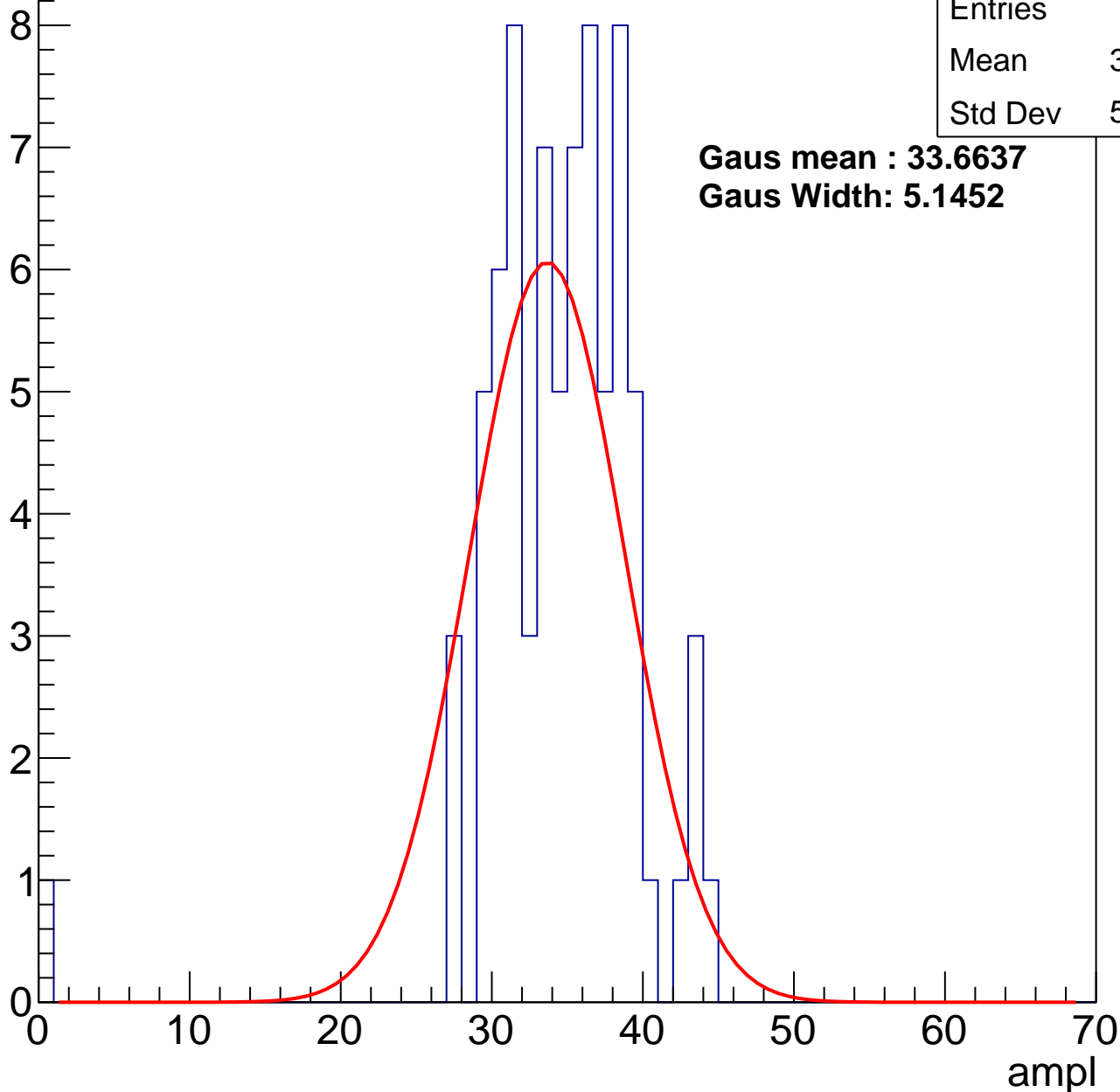
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	34.06
Std Dev	5.577

Gaus mean : 33.6637

Gaus Width: 5.1452



B1L103S, U23-ch91, adc2

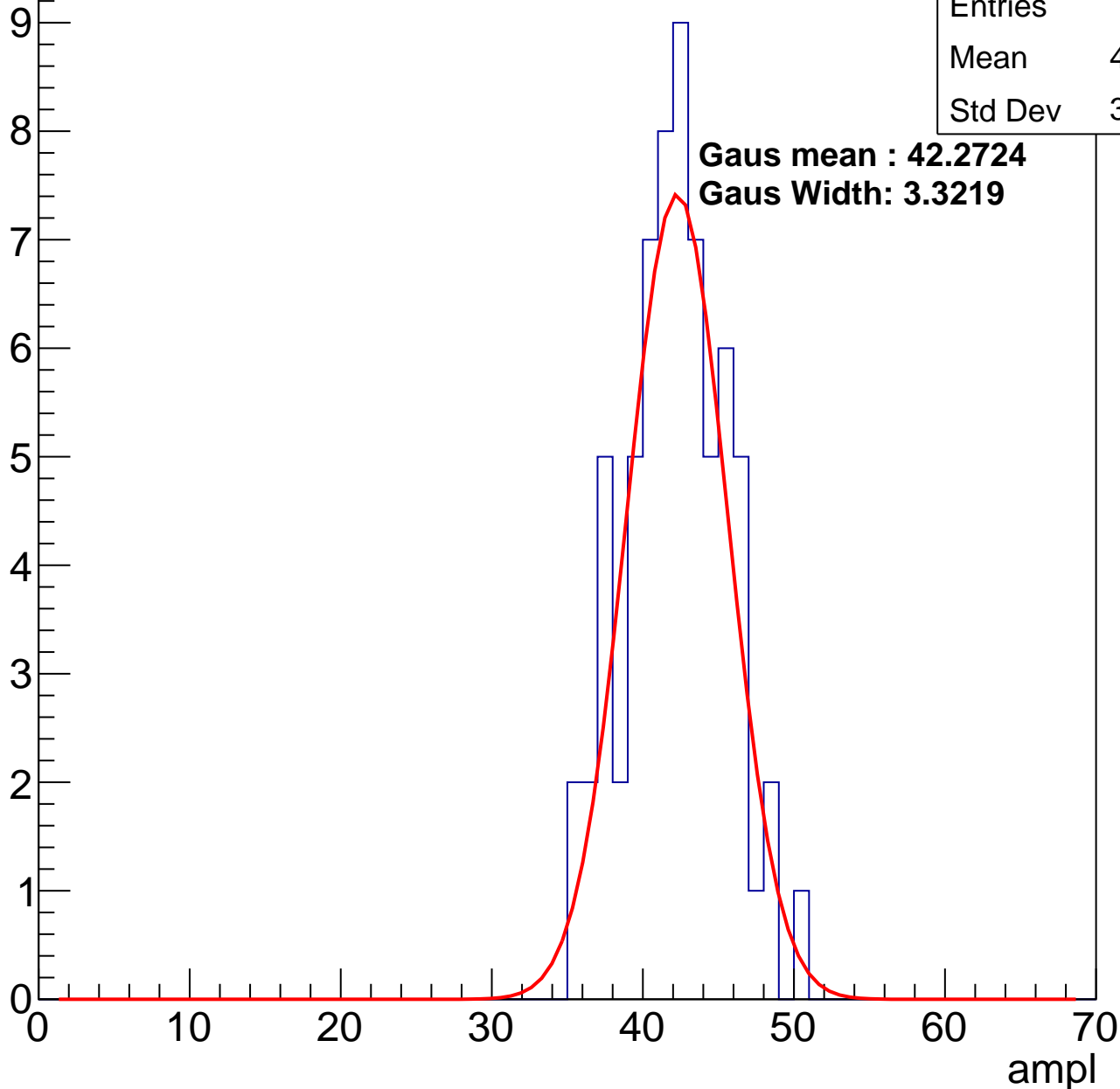
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	41.76
Std Dev	3.292

Gaus mean : 42.2724

Gaus Width: 3.3219

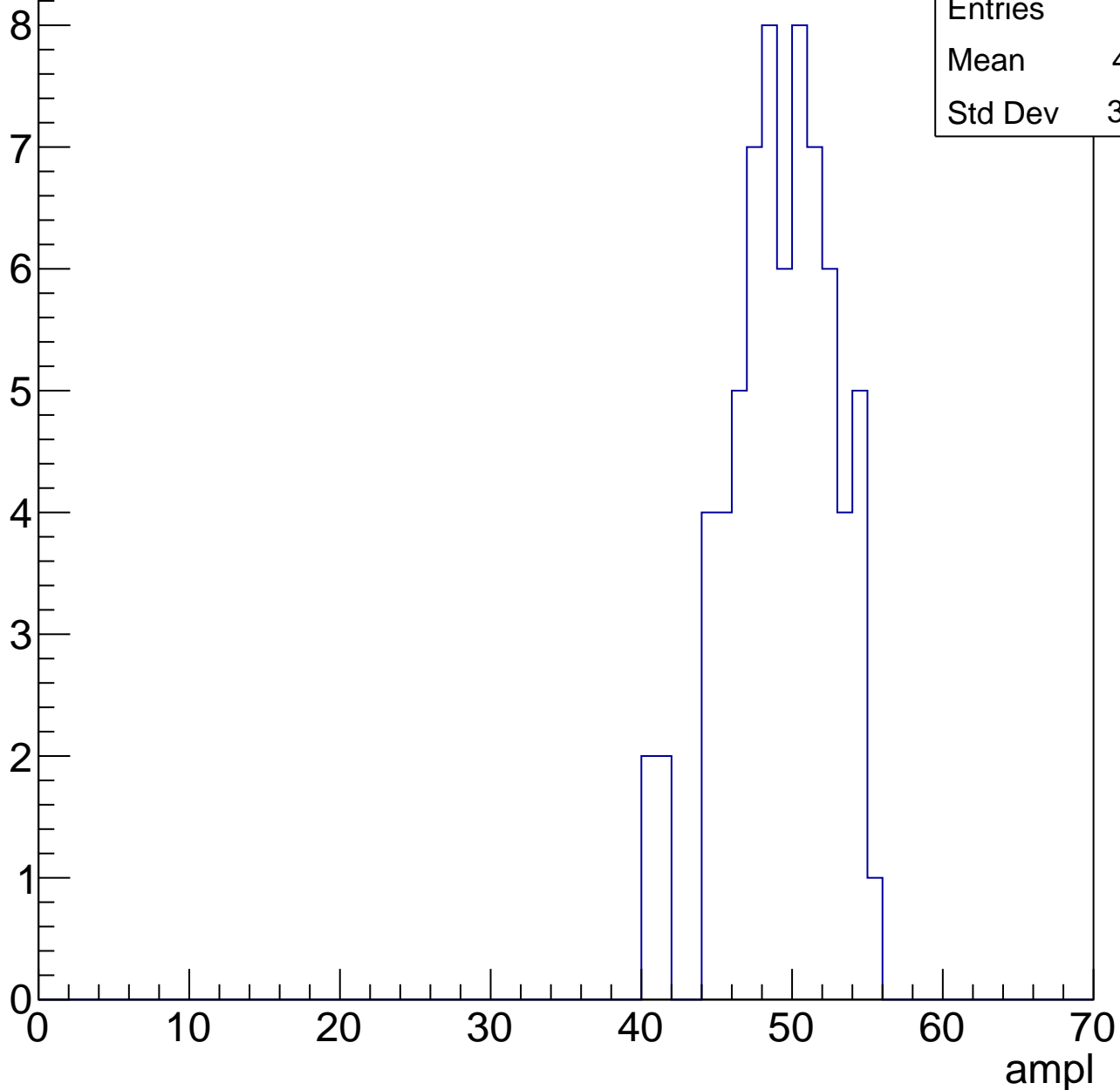


B1L103S, U23-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

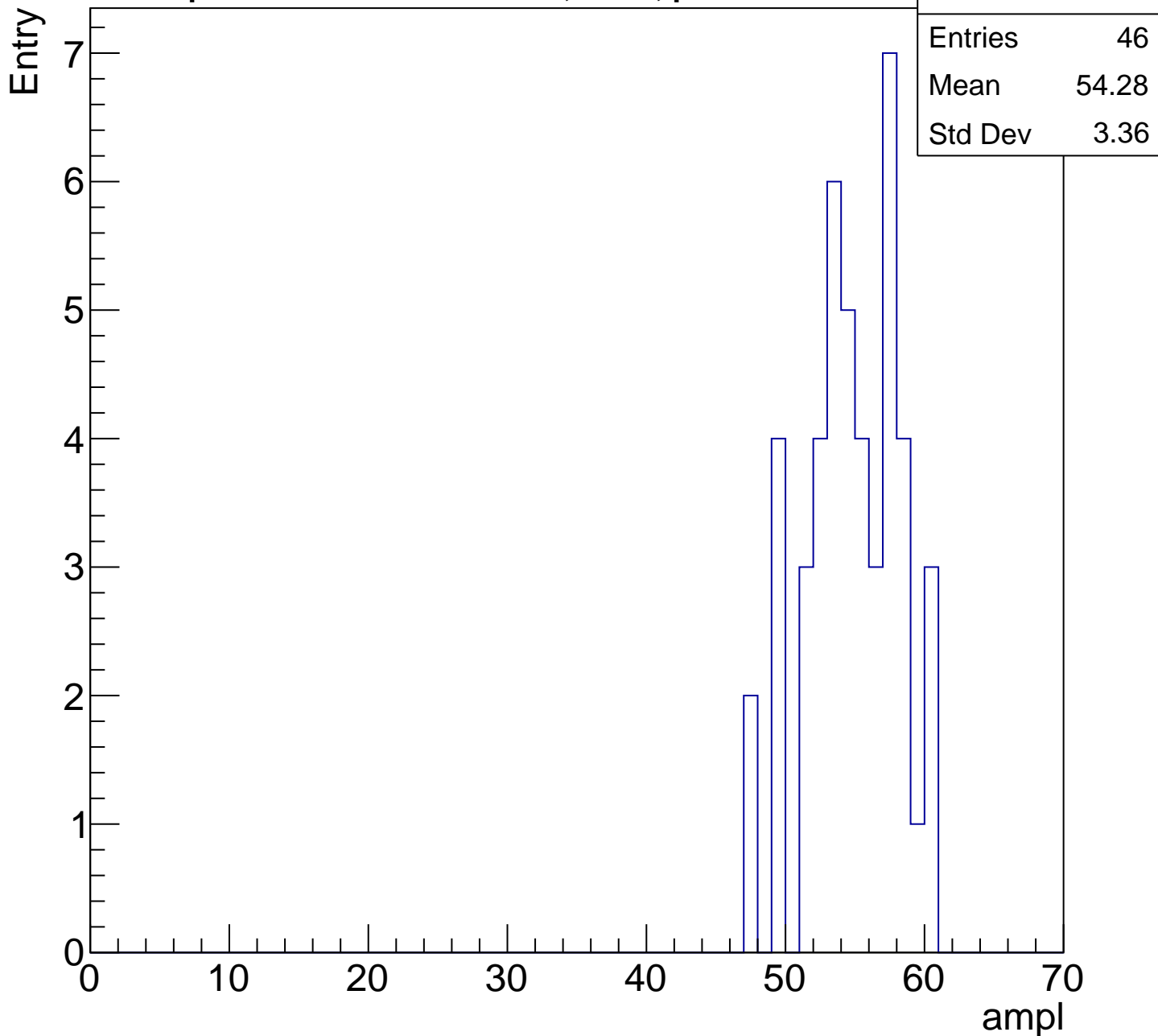
Entry

Entries	69
Mean	48.71
Std Dev	3.498



B1L103S, U23-ch91, adc4

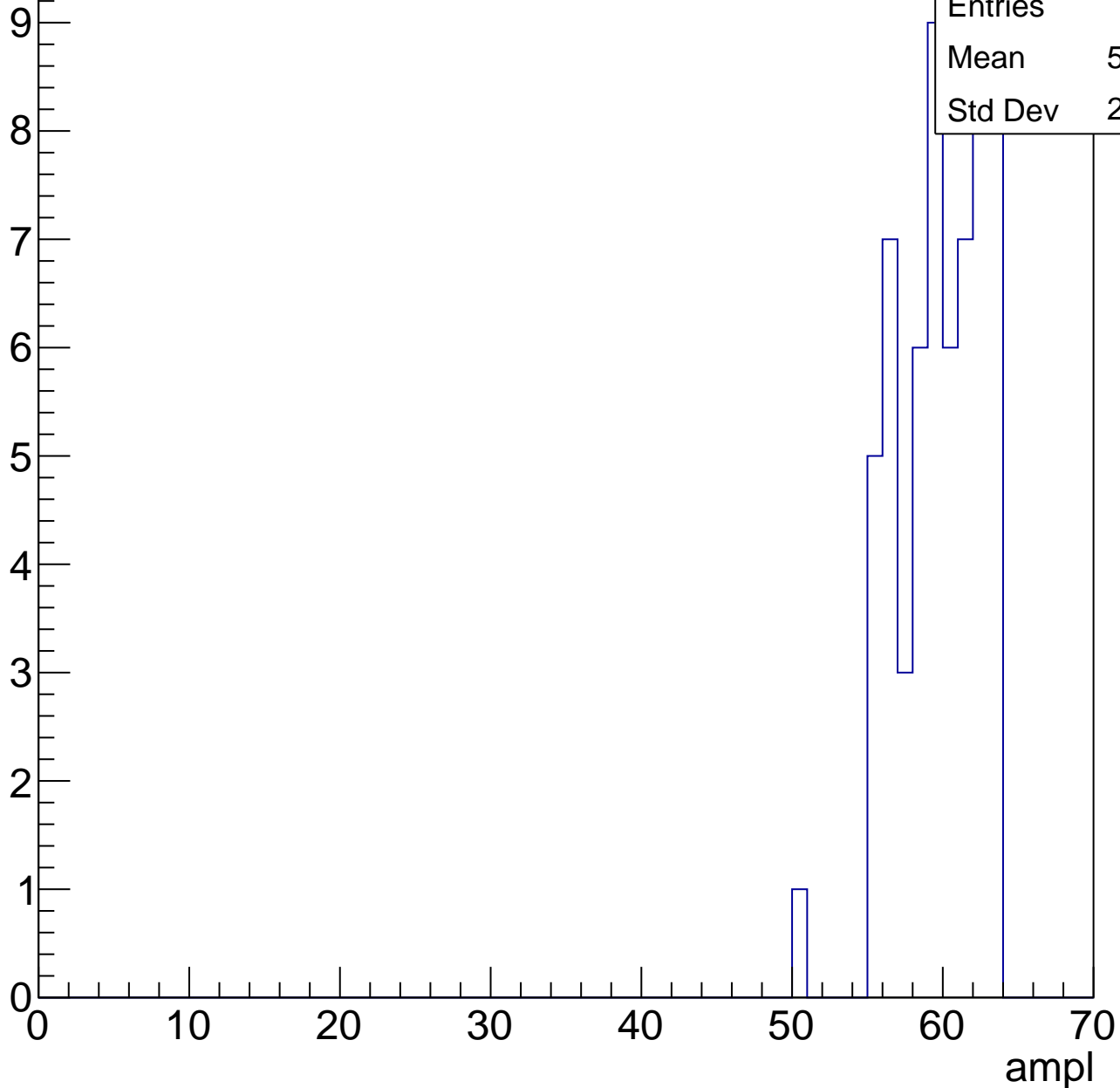
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U23-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

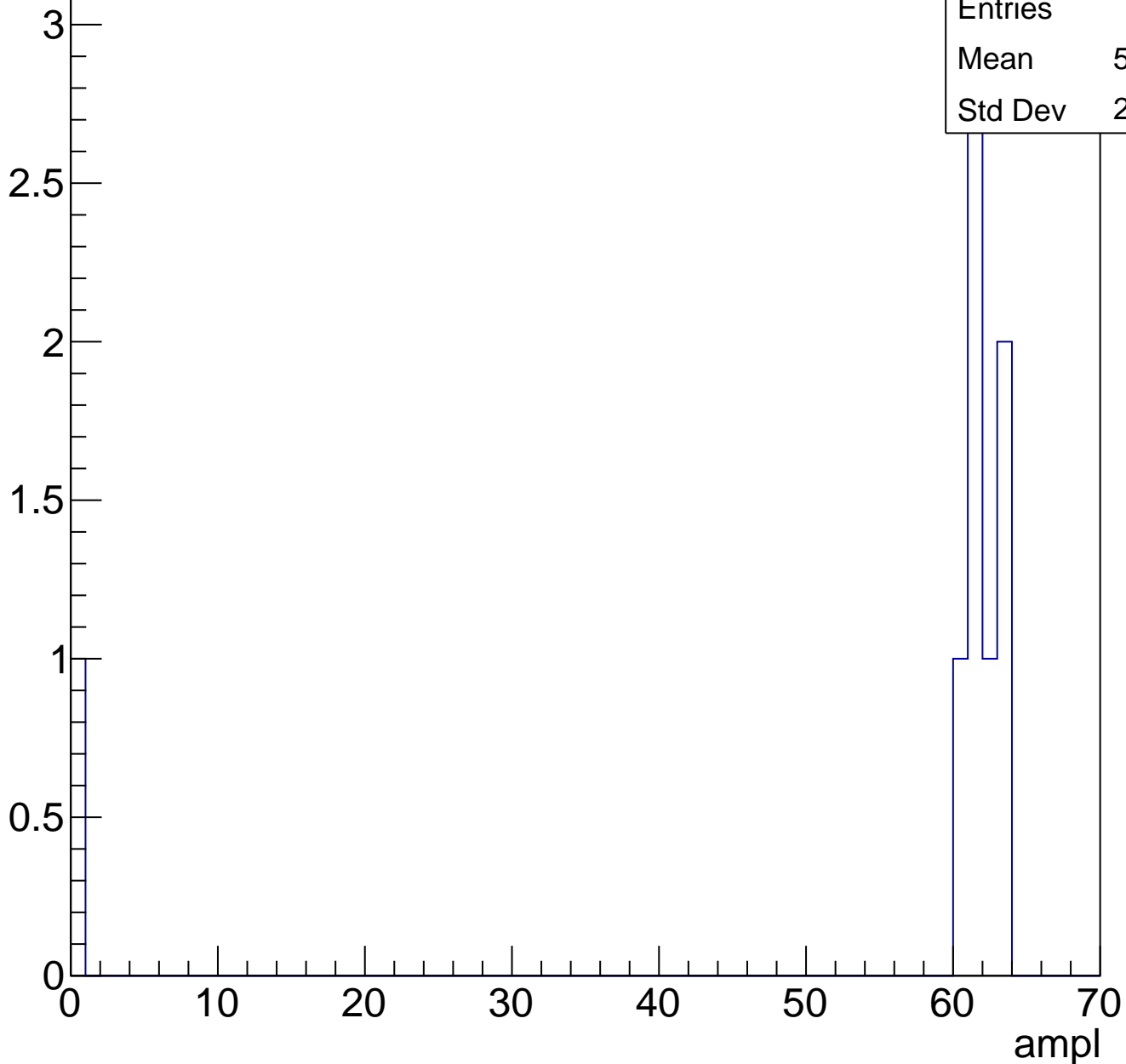
Entry



B1L103S, U23-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch92, adc0

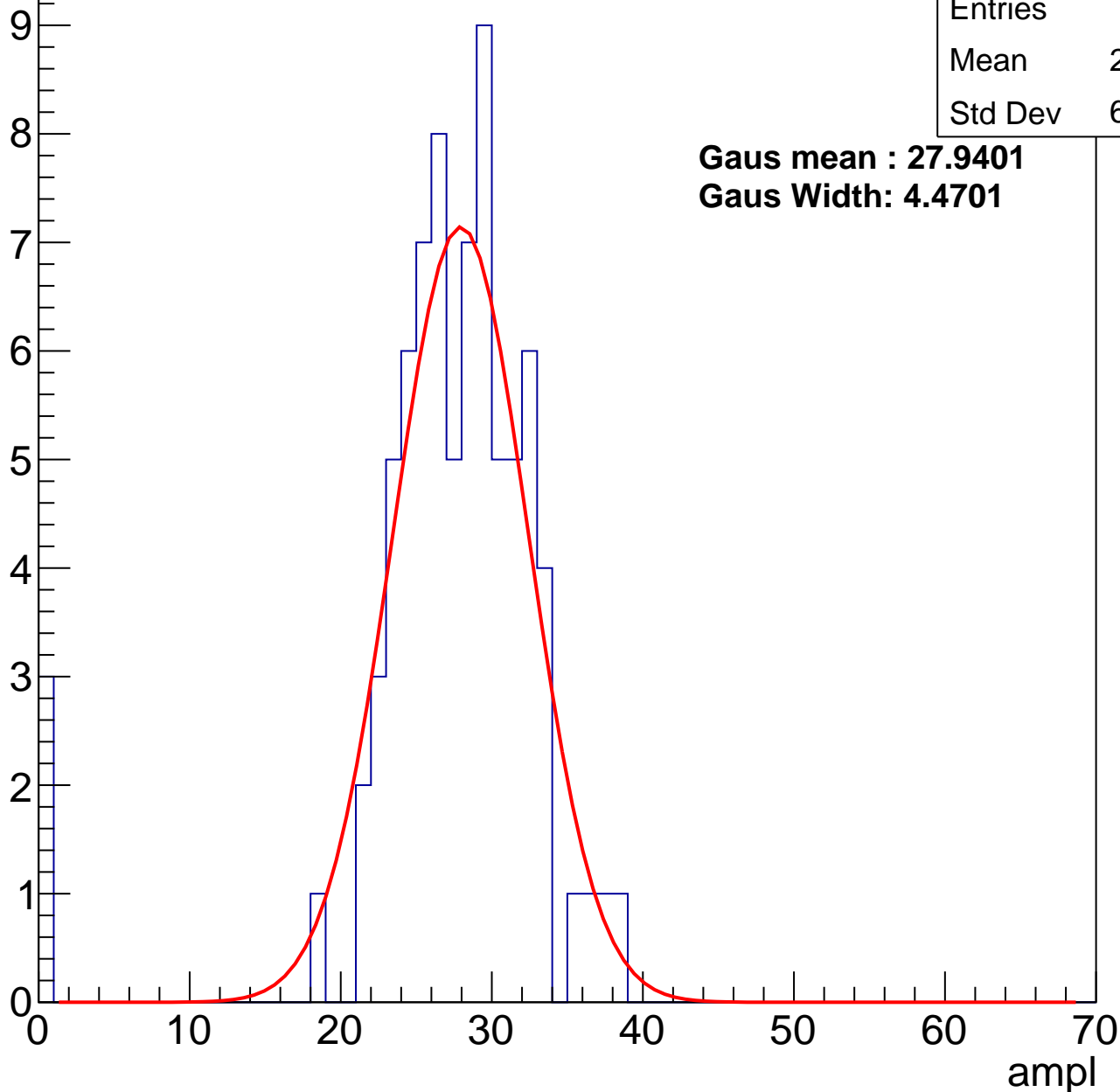
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	26.69
Std Dev	6.524

Gaus mean : 27.9401

Gaus Width: 4.4701



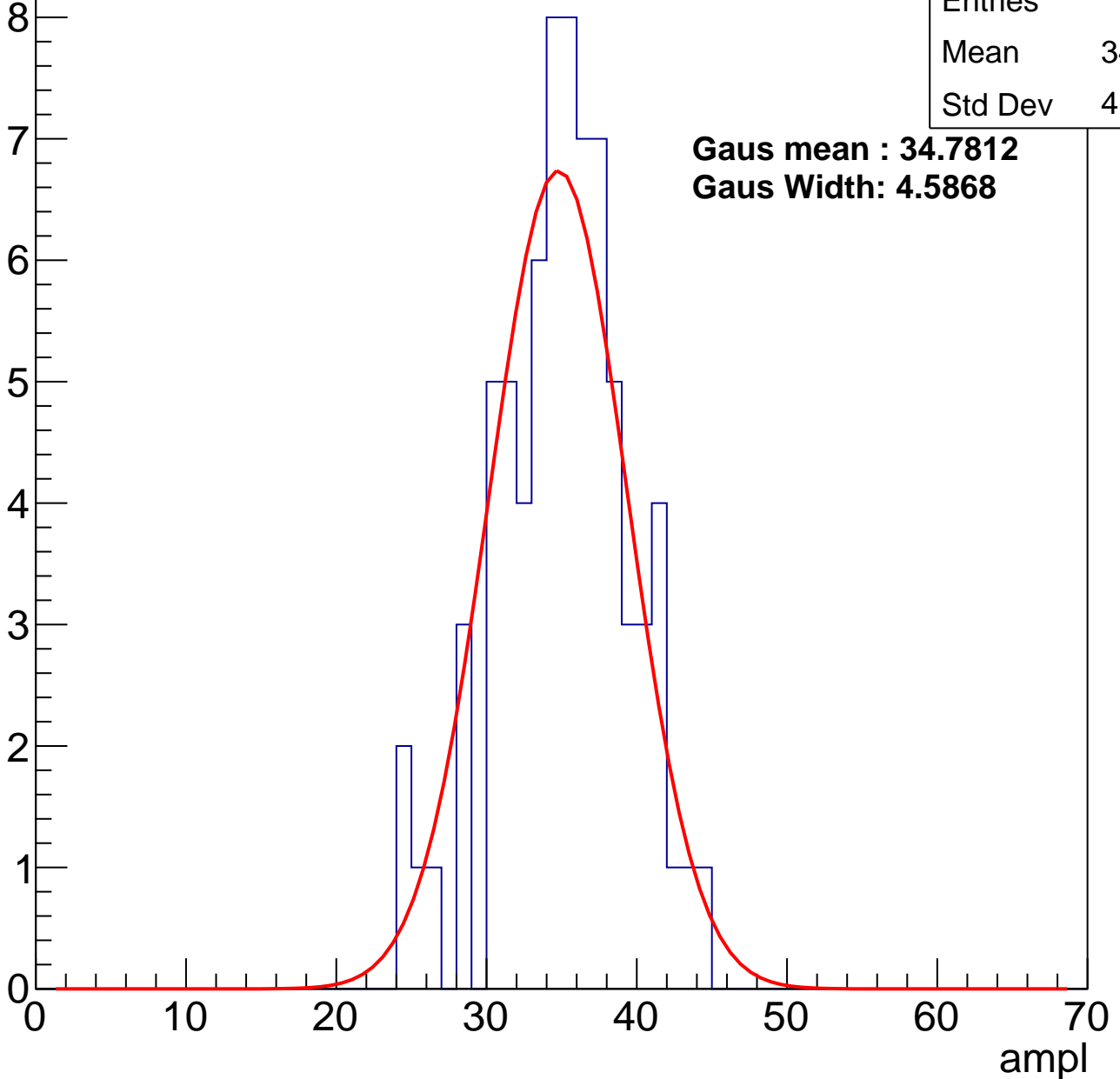
B1L103S, U23-ch92, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	34.63
Std Dev	4.279

Gaus mean : 34.7812
Gaus Width: 4.5868



B1L103S, U23-ch92, adc2

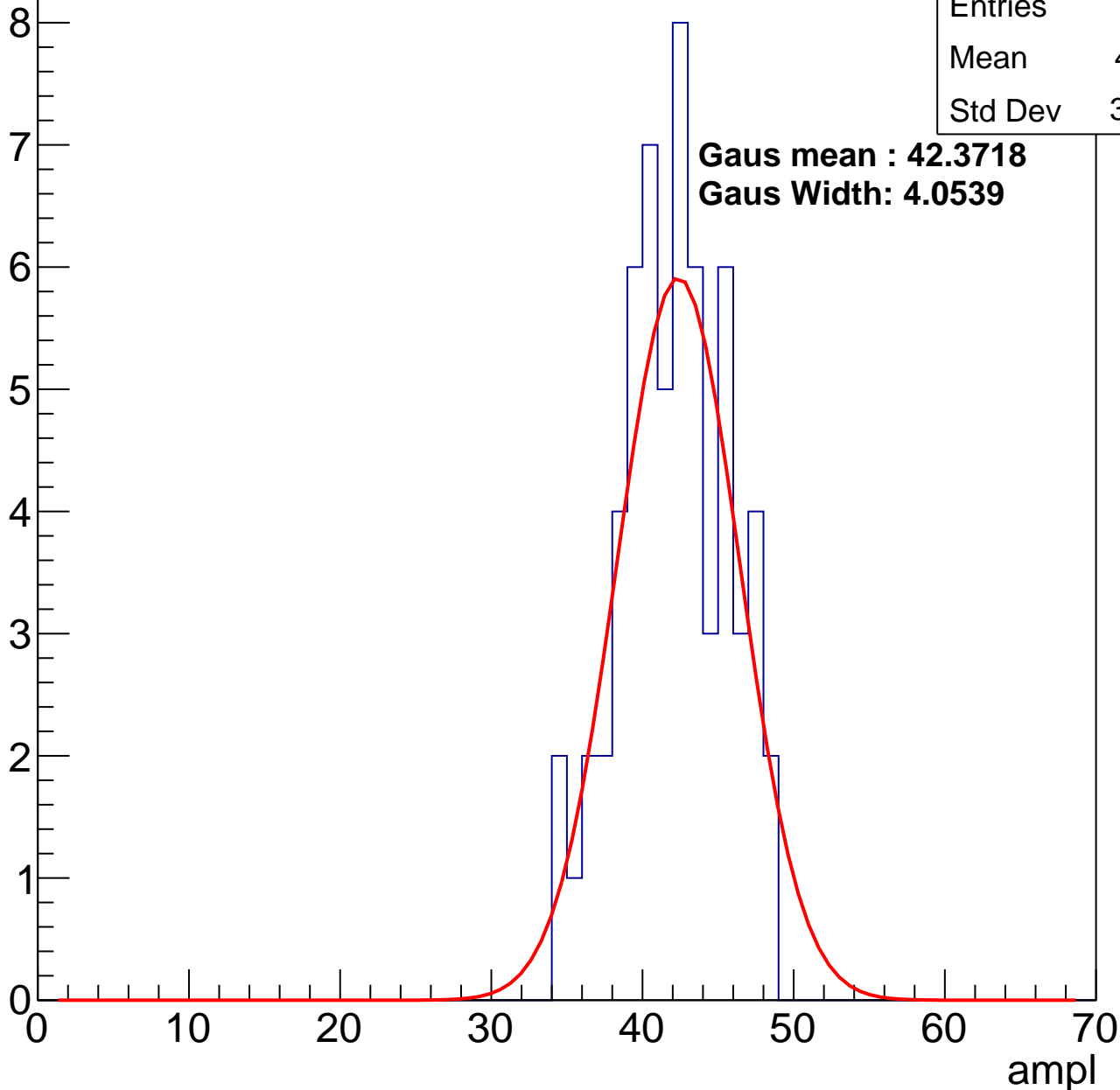
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.61
Std Dev	3.465

Gaus mean : 42.3718

Gaus Width: 4.0539

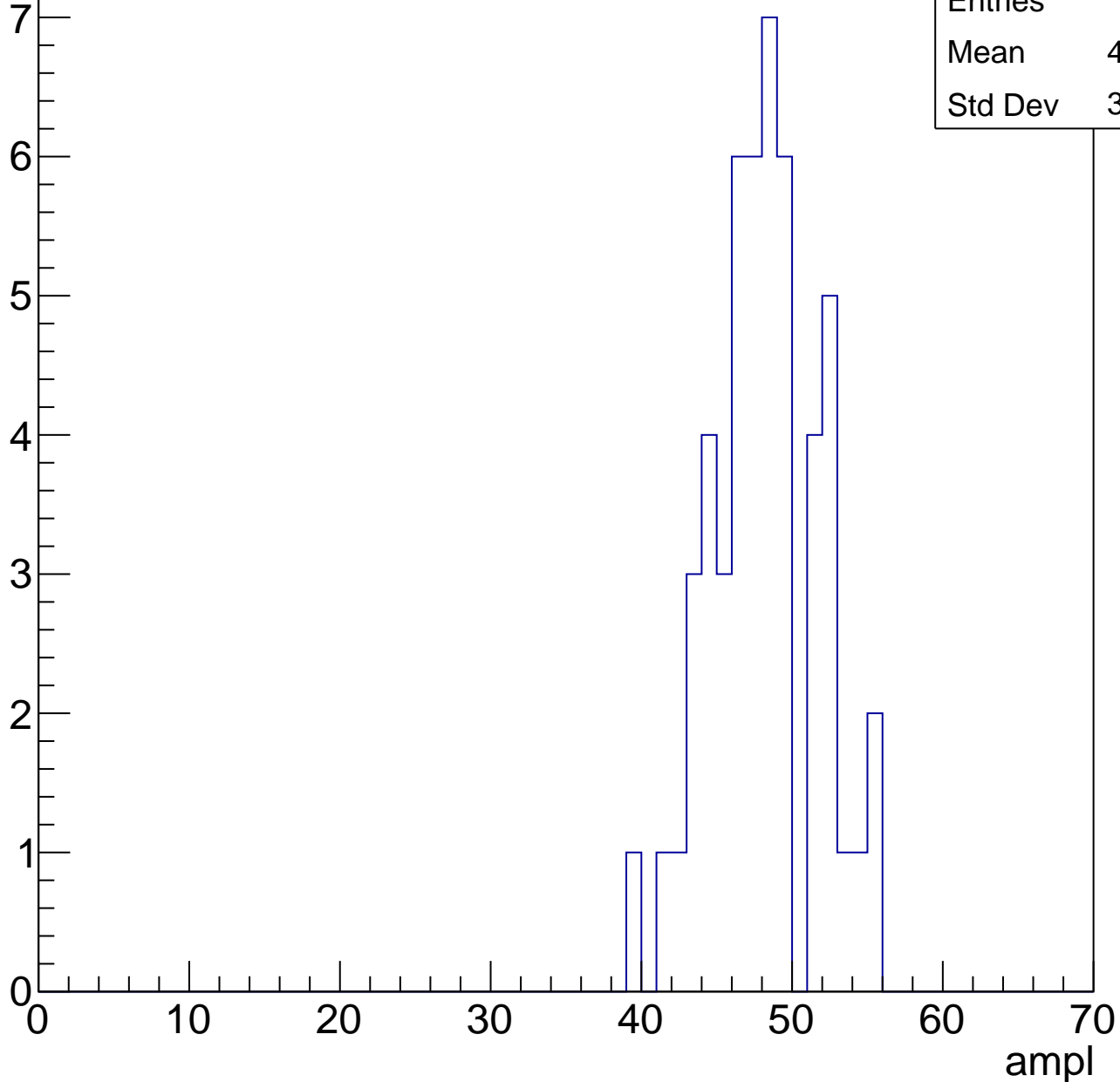


B1L103S, U23-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	47.67
Std Dev	3.535

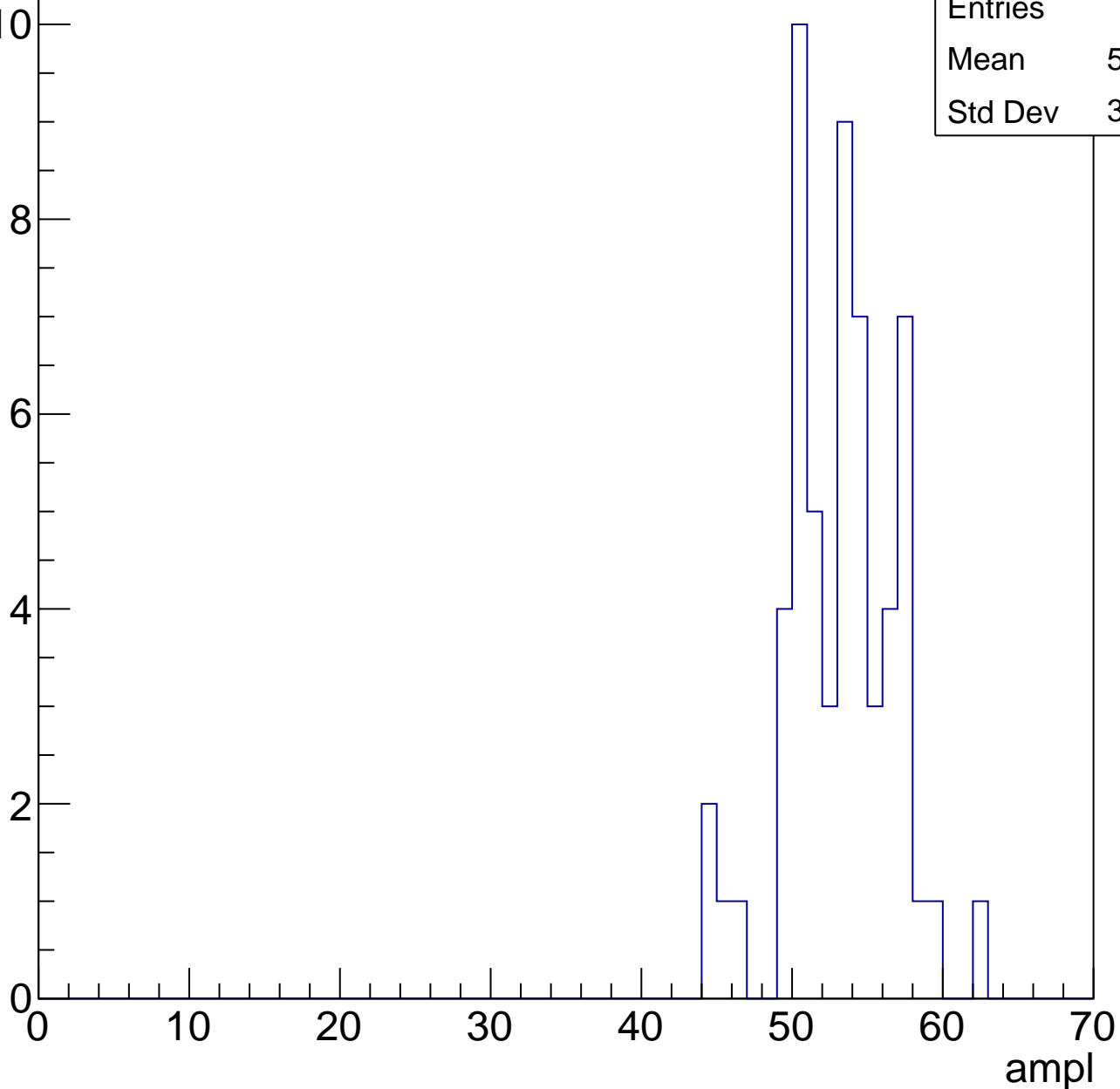


B1L103S, U23-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	52.68
Std Dev	3.577

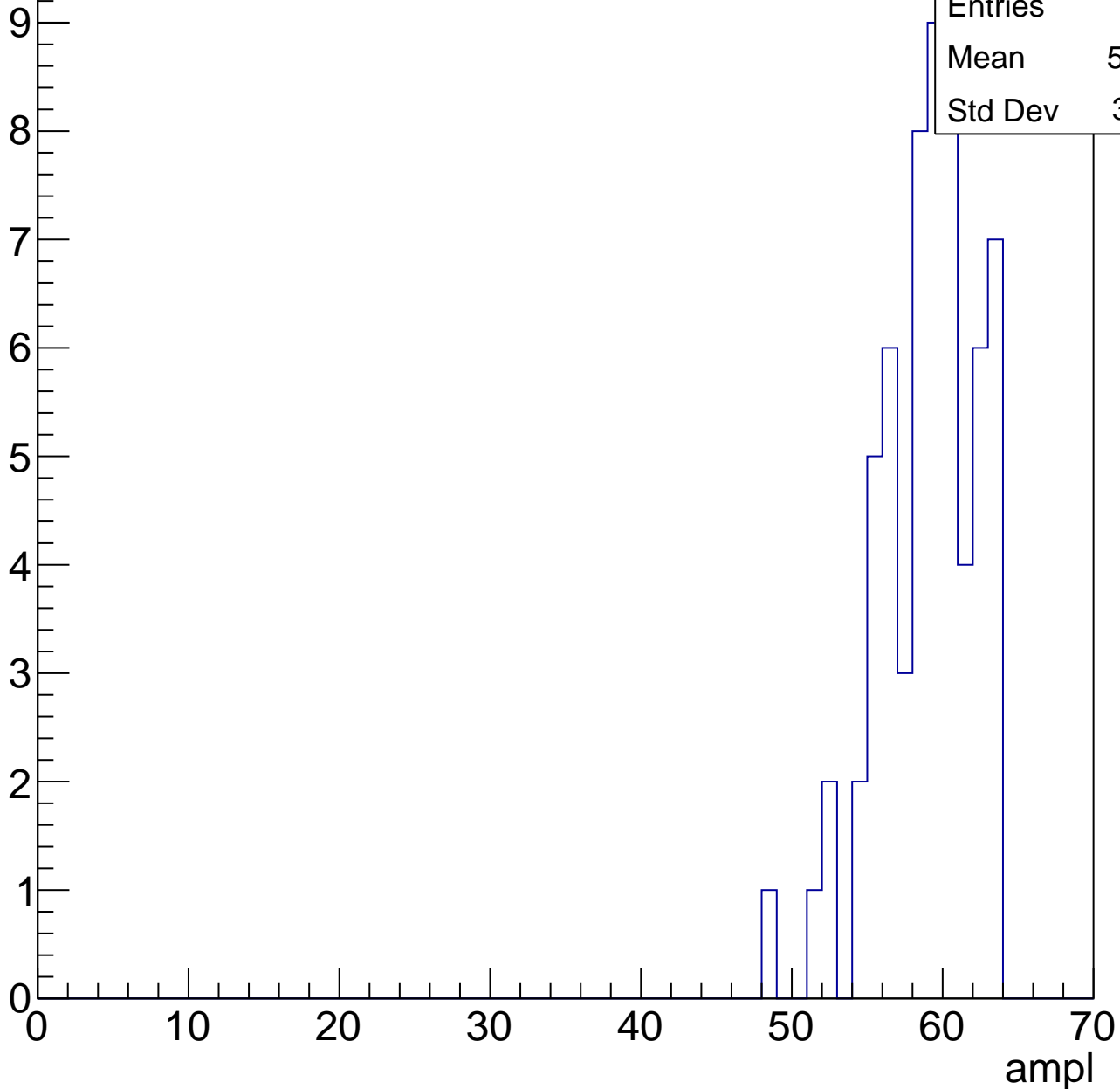


B1L103S, U23-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	58.47
Std Dev	3.261

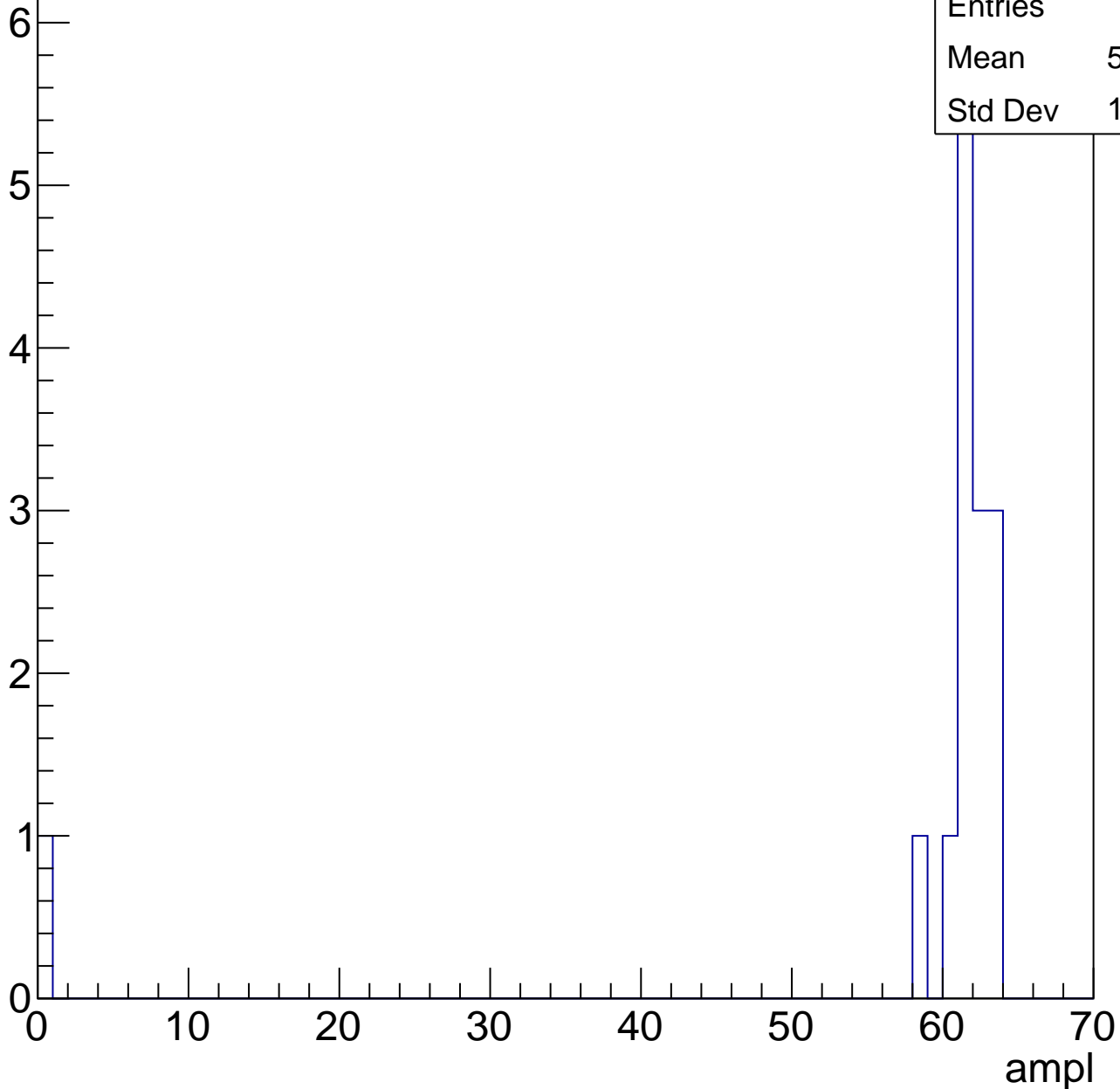


B1L103S, U23-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	57.27
Std Dev	15.36



B1L103S, U23-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

60

Std Dev

0

B1L103S, U23-ch93, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	28.48
Std Dev	5.895

Gaus mean : 29.8880

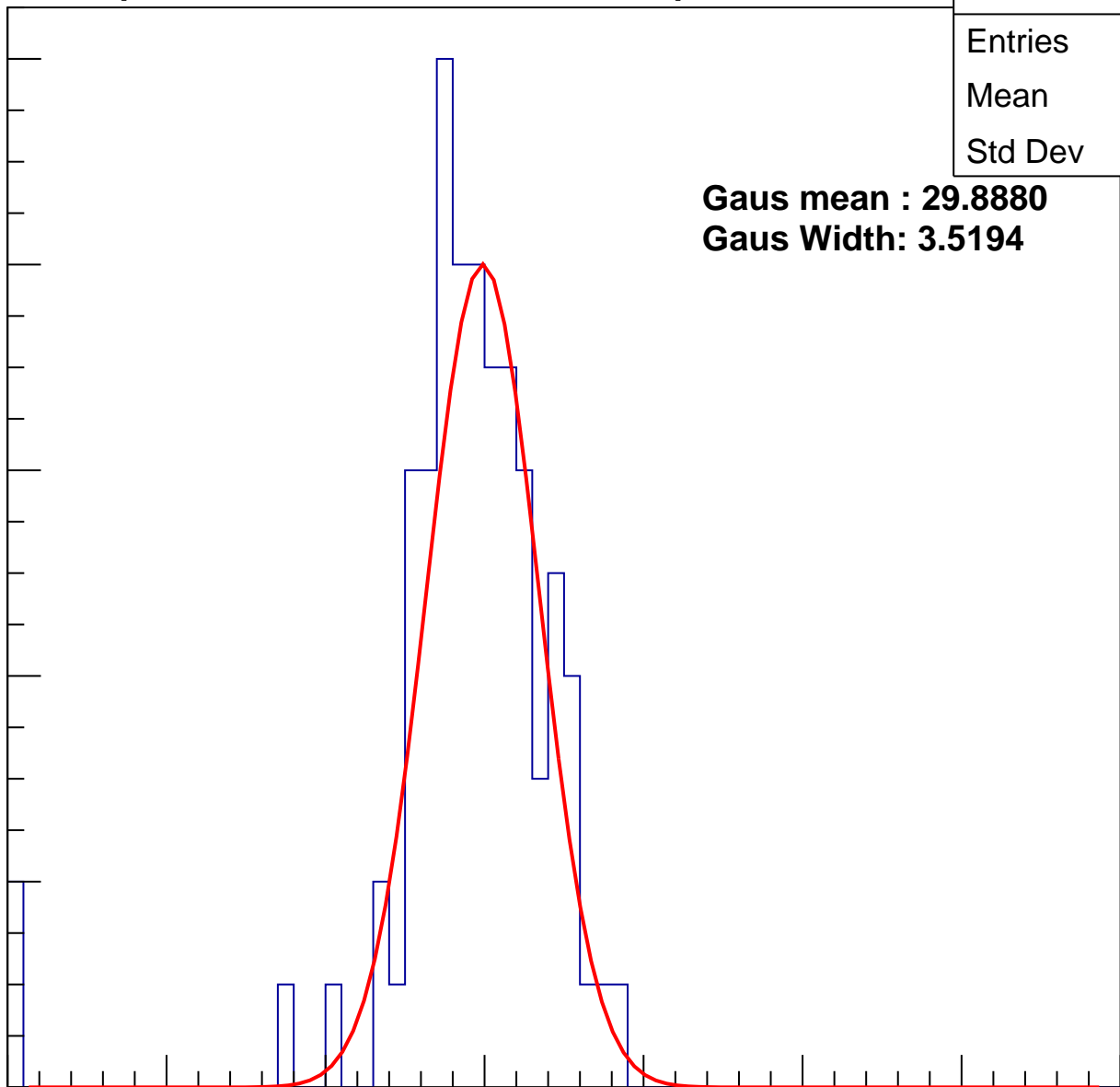
Gaus Width: 3.5194

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch93, adc1

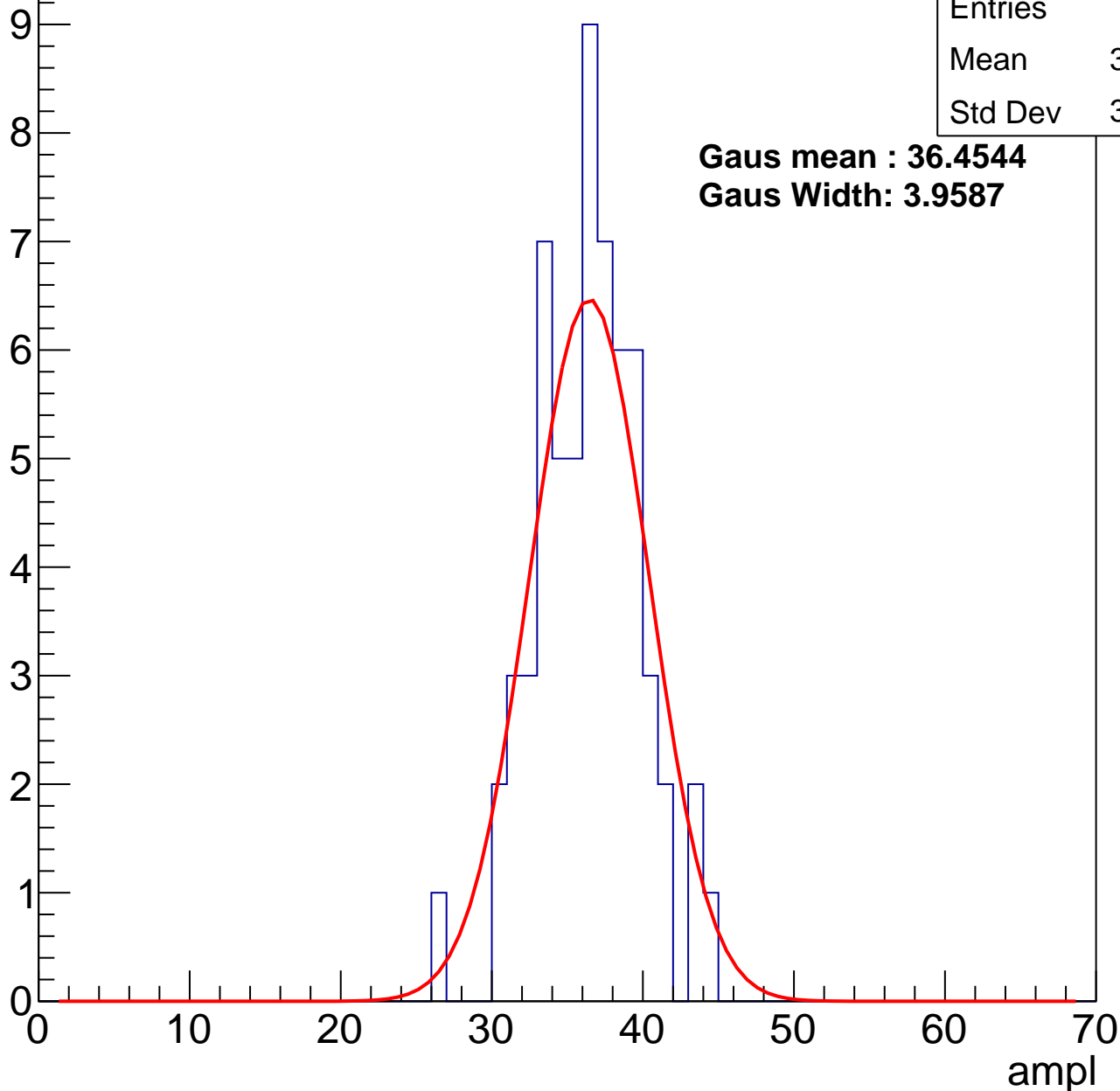
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.94
Std Dev	3.426

Gaus mean : 36.4544

Gaus Width: 3.9587



B1L103S, U23-ch93, adc2

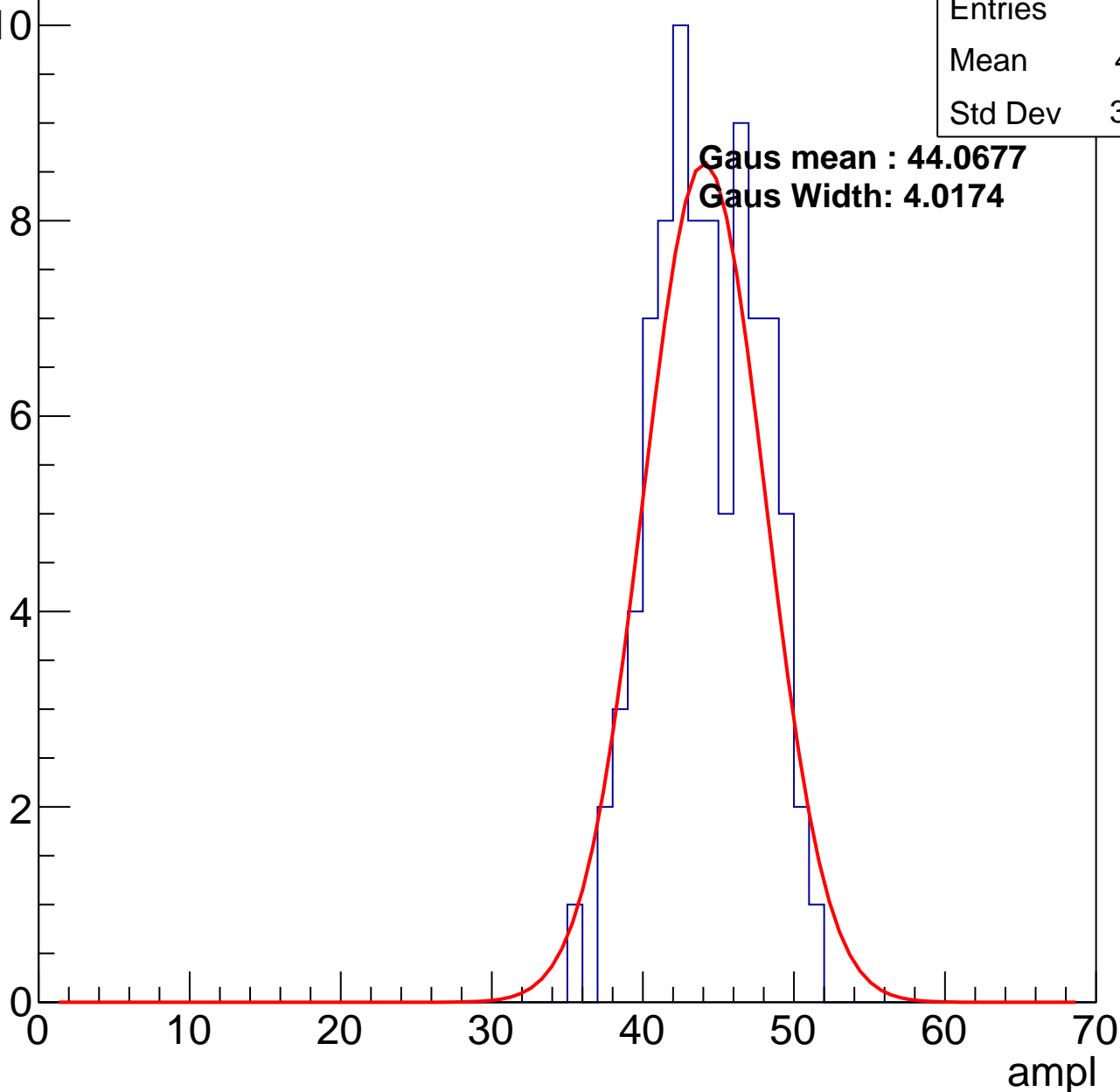
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	43.71
Std Dev	3.513

Gaus mean : 44.0677

Gaus Width: 4.0174

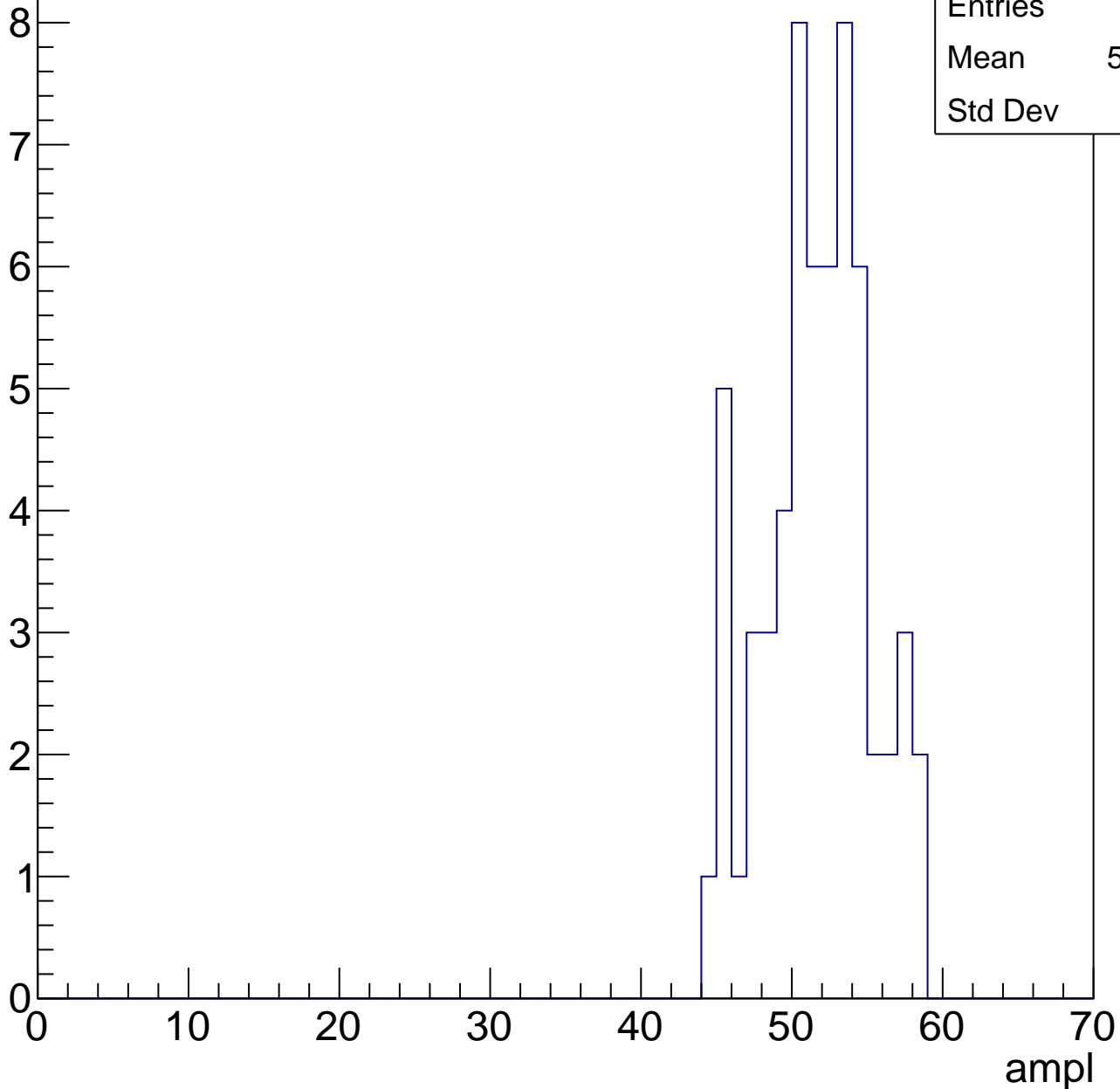


B1L103S, U23-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

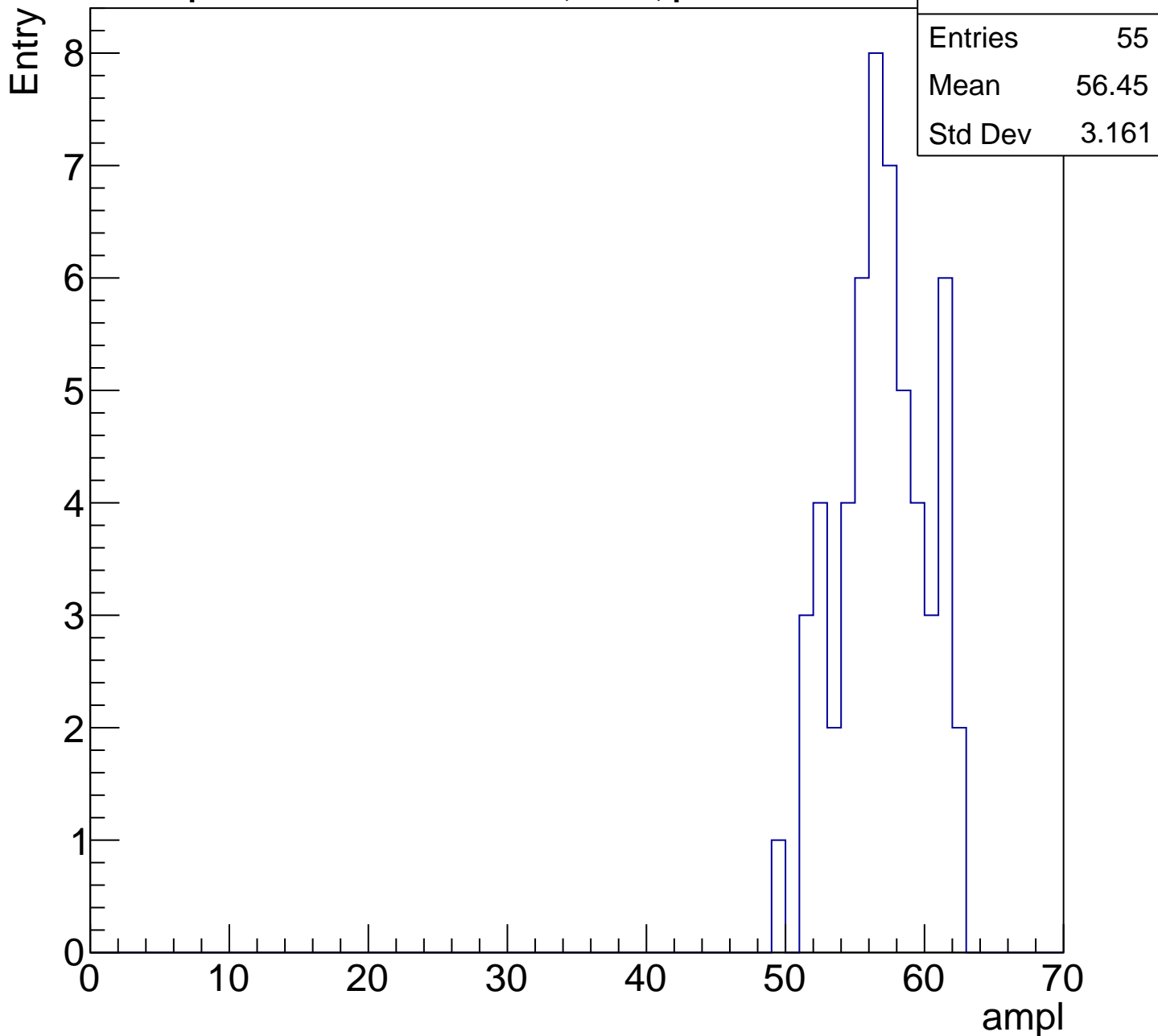
Entry

Entries	60
Mean	51.18
Std Dev	3.49



B1L103S, U23-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

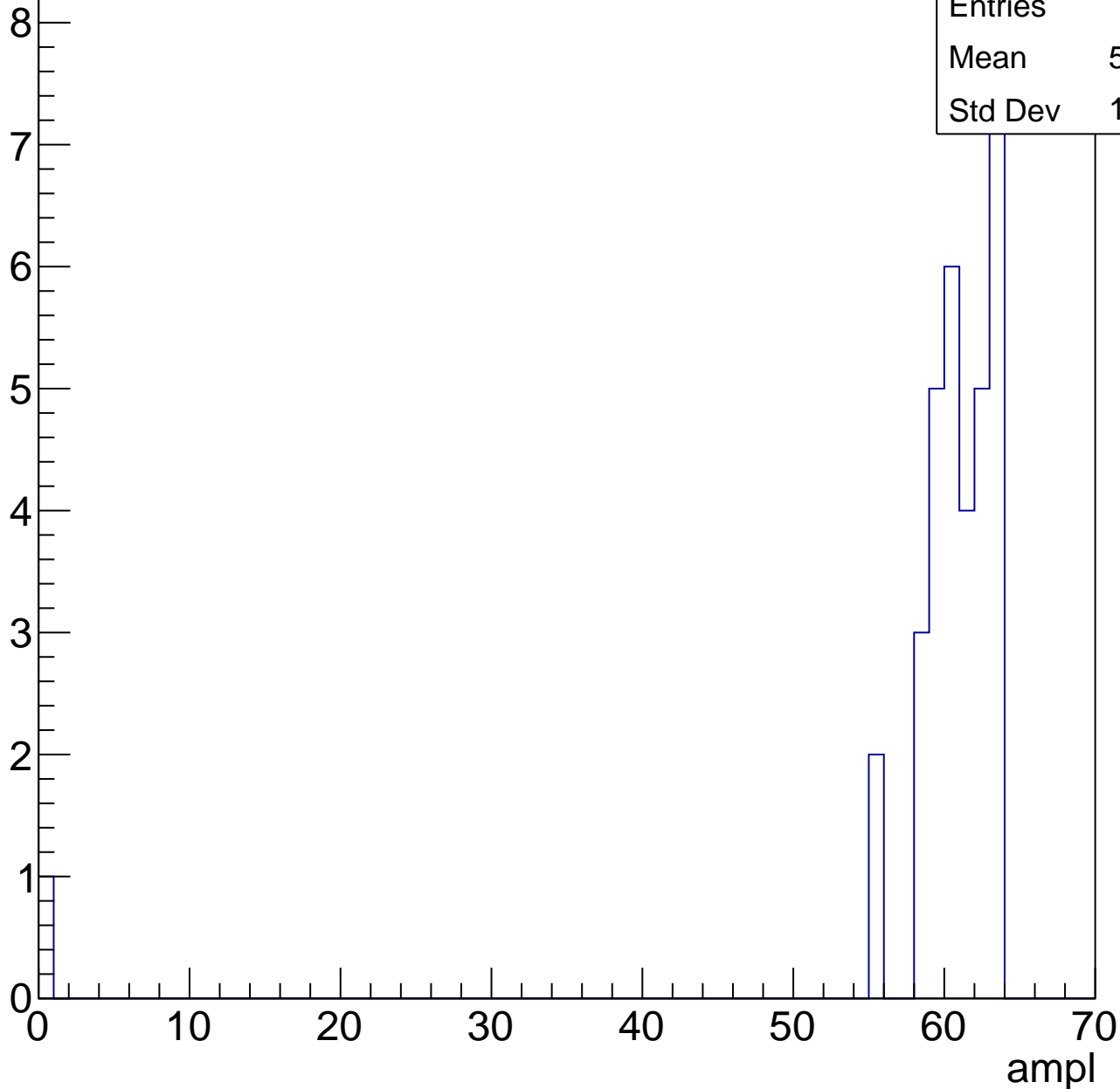


B1L103S, U23-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

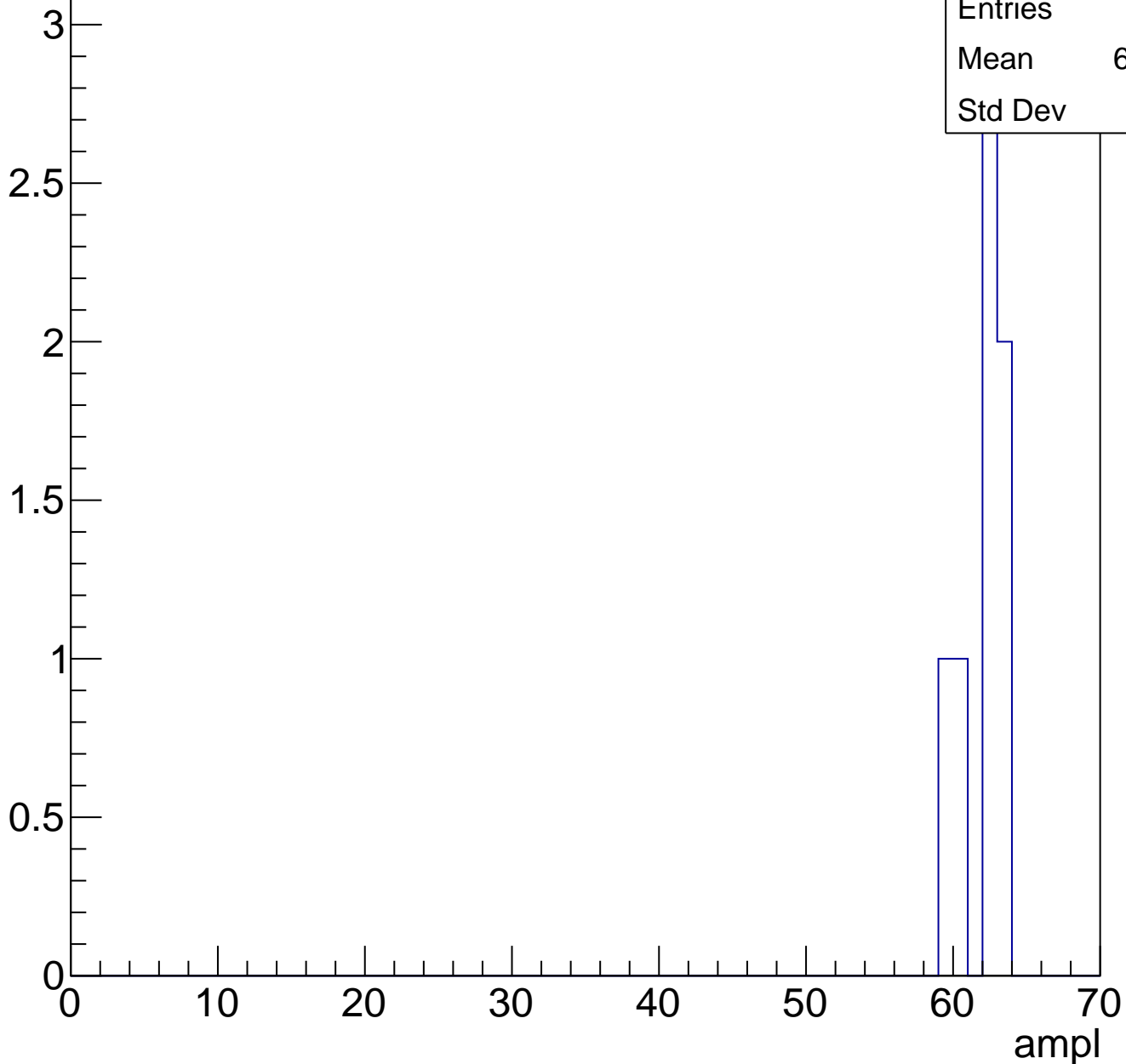
Entries	34
Mean	58.74
Std Dev	10.44



B1L103S, U23-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	7
Mean	61.57
Std Dev	1.4

B1L103S, U23-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch94, adc0

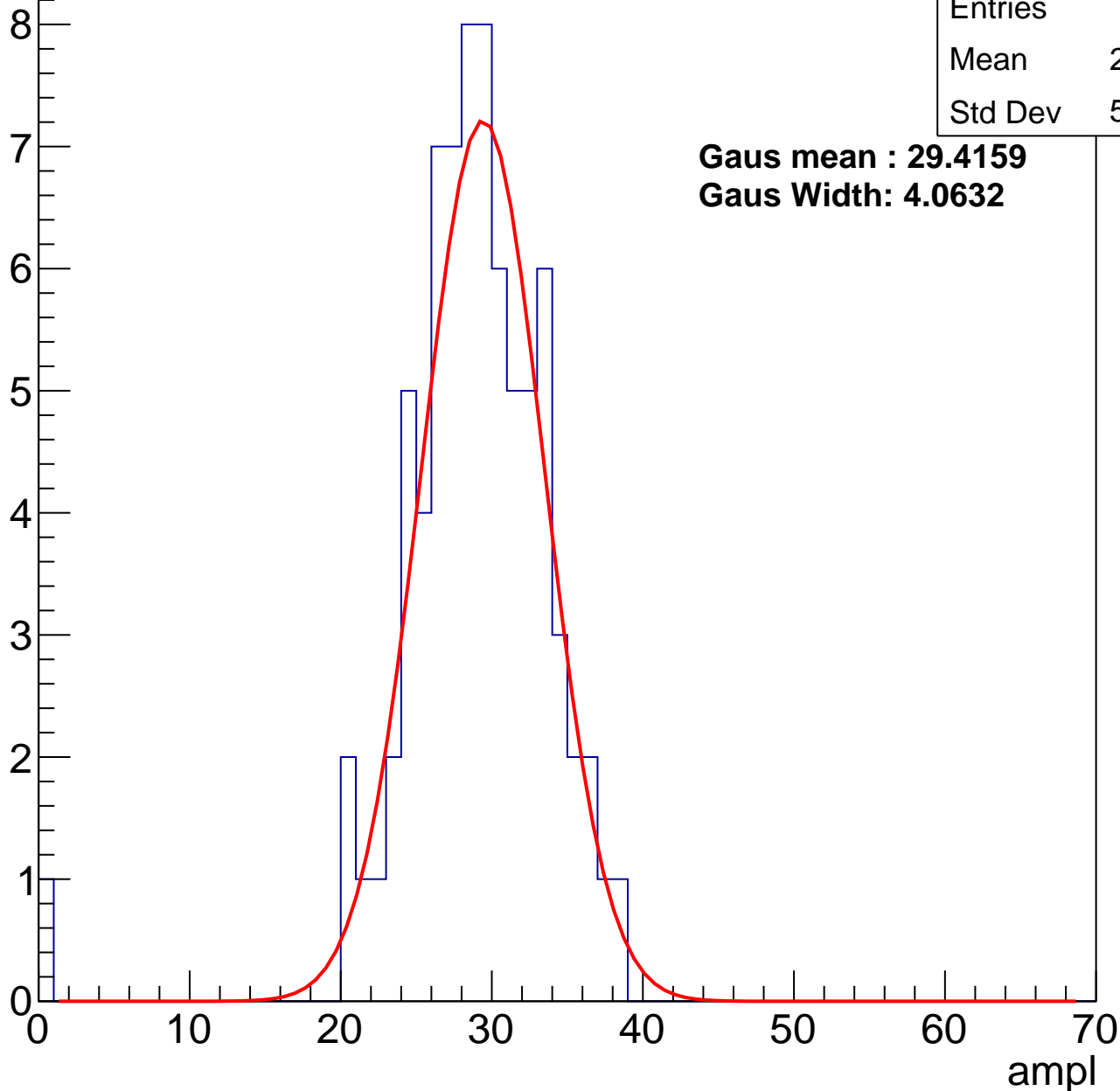
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	28.42
Std Dev	5.105

Gaus mean : 29.4159

Gaus Width: 4.0632



B1L103S, U23-ch94, adc1

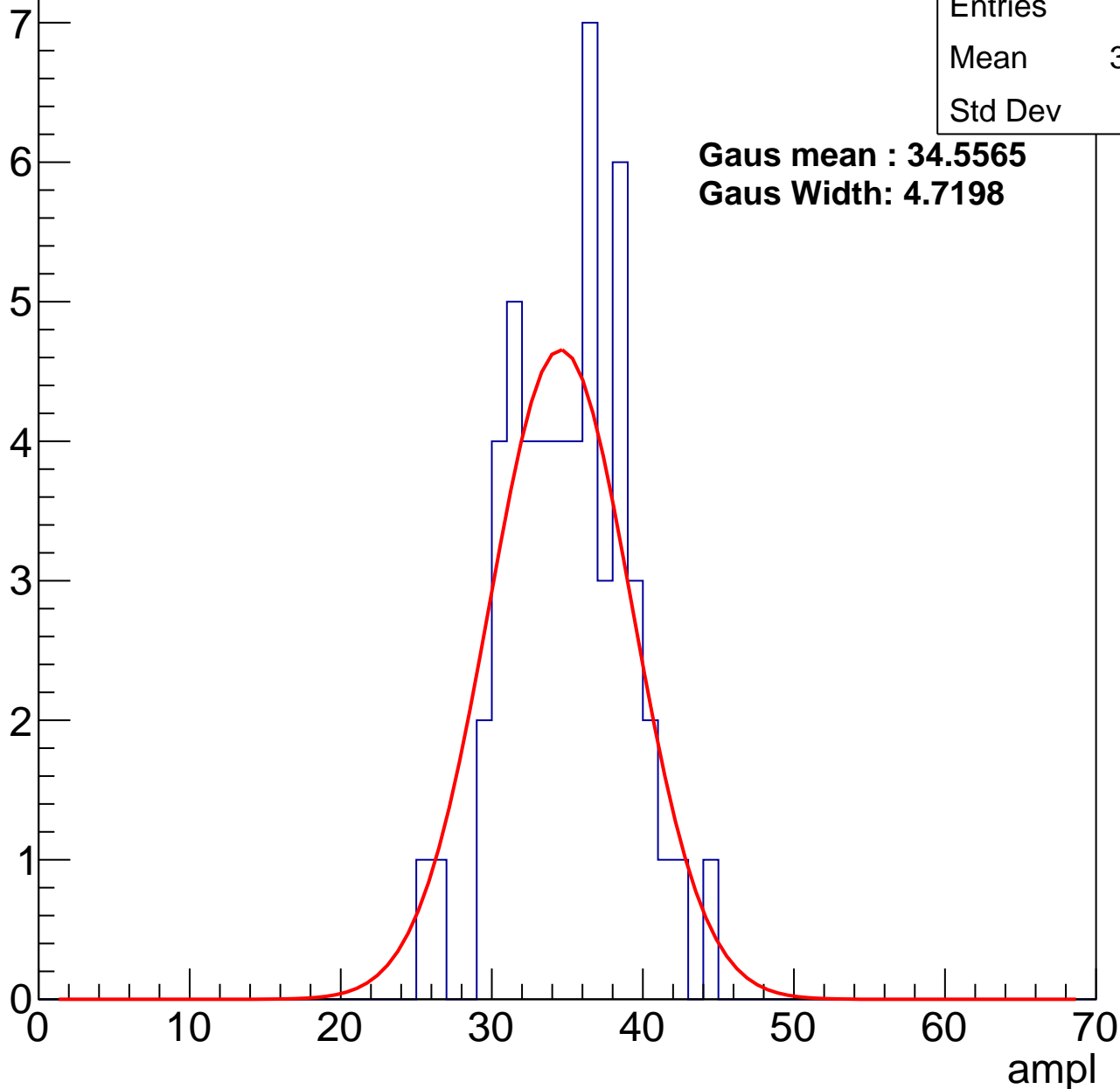
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	34.62
Std Dev	3.93

Gaus mean : 34.5565

Gaus Width: 4.7198



B1L103S, U23-ch94, adc2

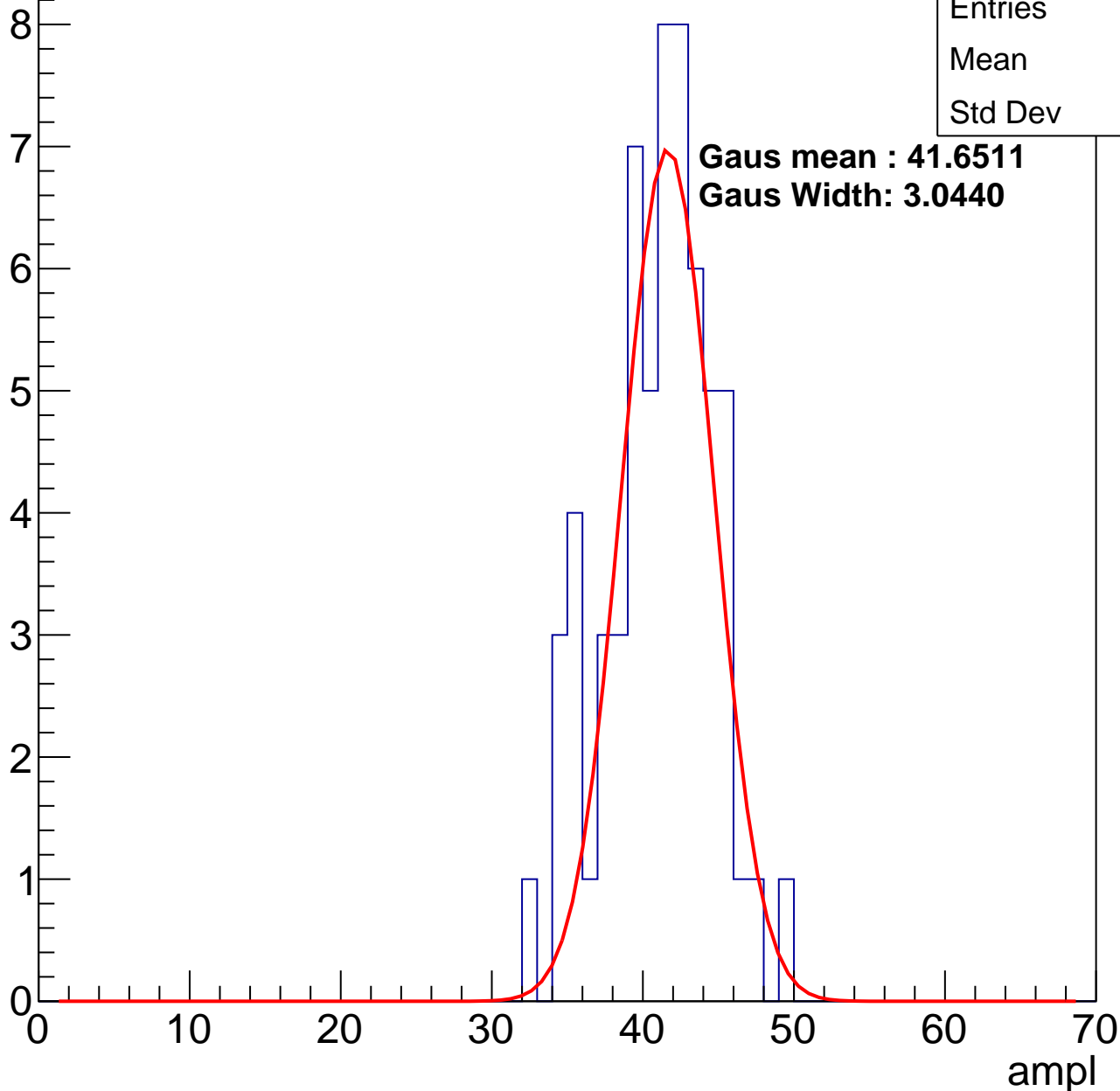
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	40.6
Std Dev	3.54

Gaus mean : 41.6511

Gaus Width: 3.0440

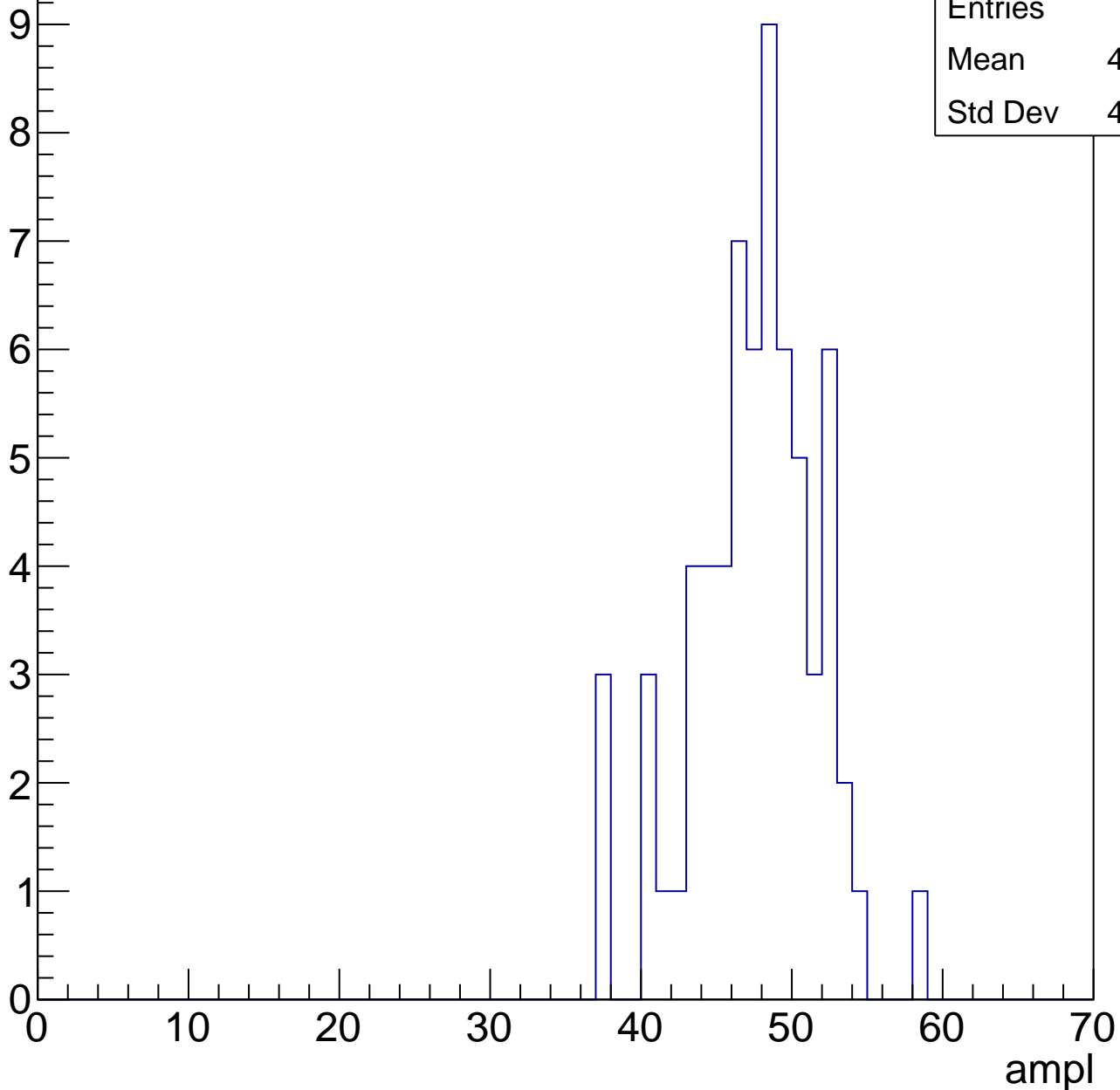


B1L103S, U23-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

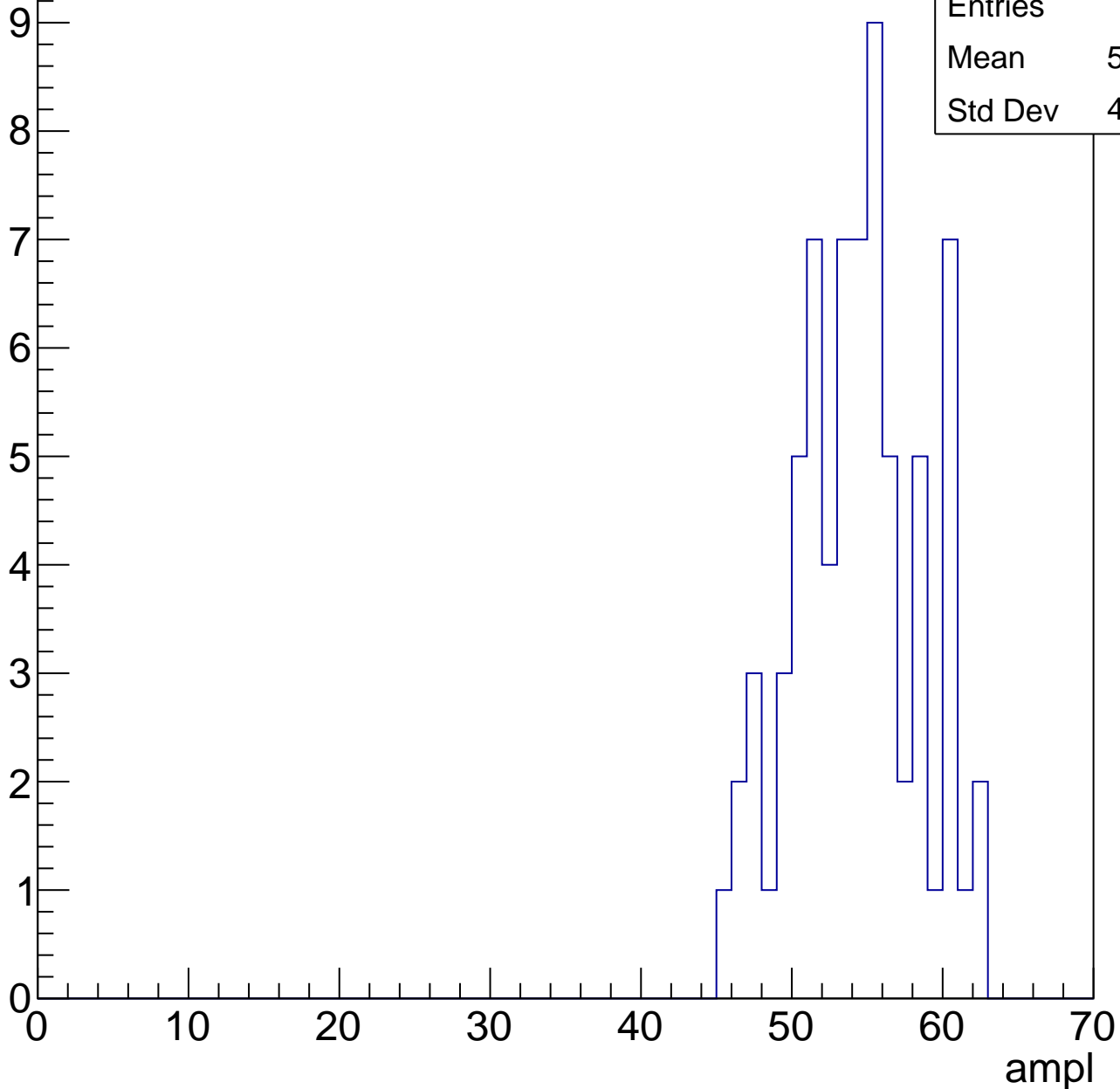
Entries	66
Mean	47.05
Std Dev	4.172



B1L103S, U23-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 42

Mean 58.81

Std Dev 2.745

0

10

20

30

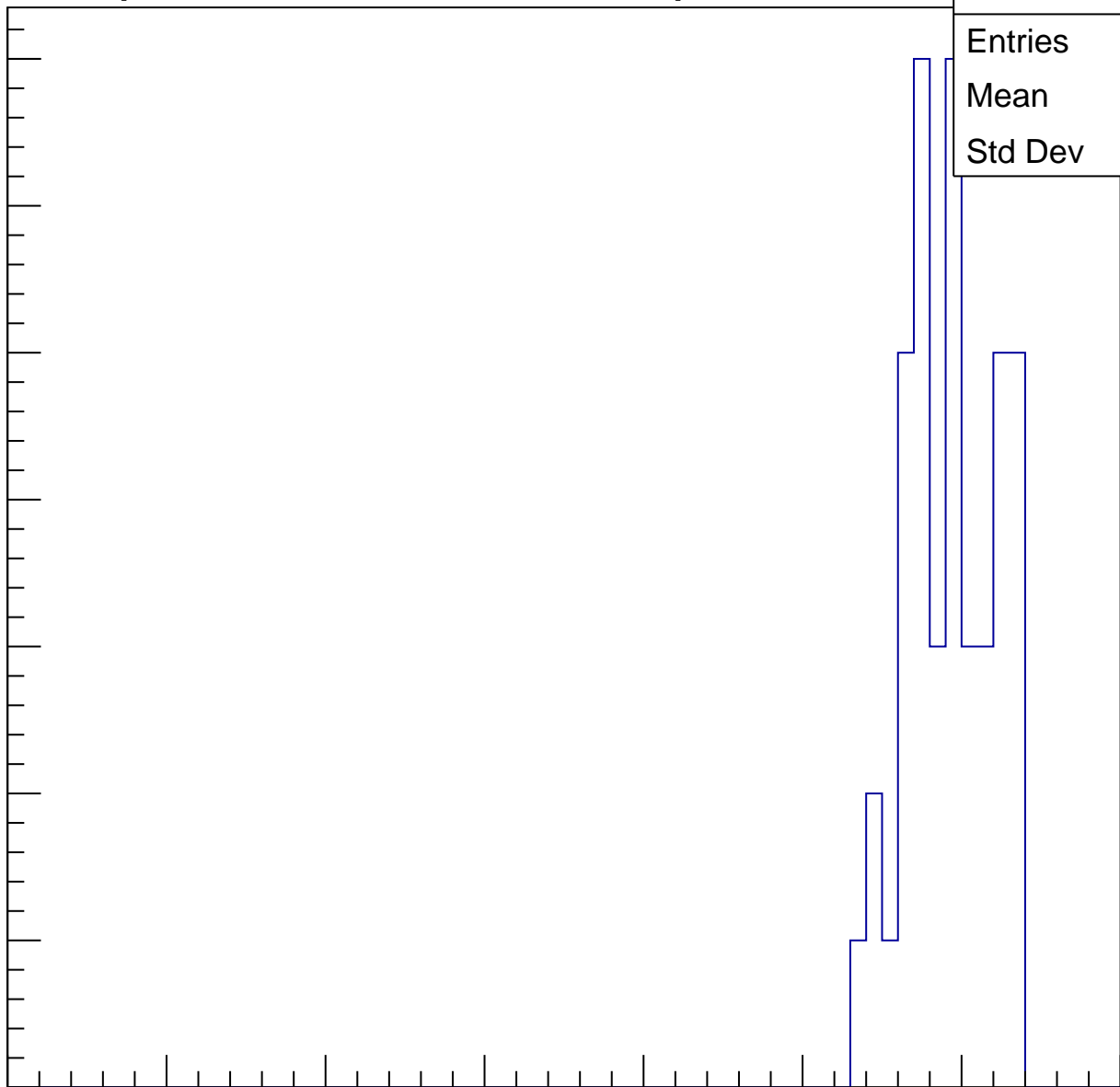
40

50

60

70

ampl

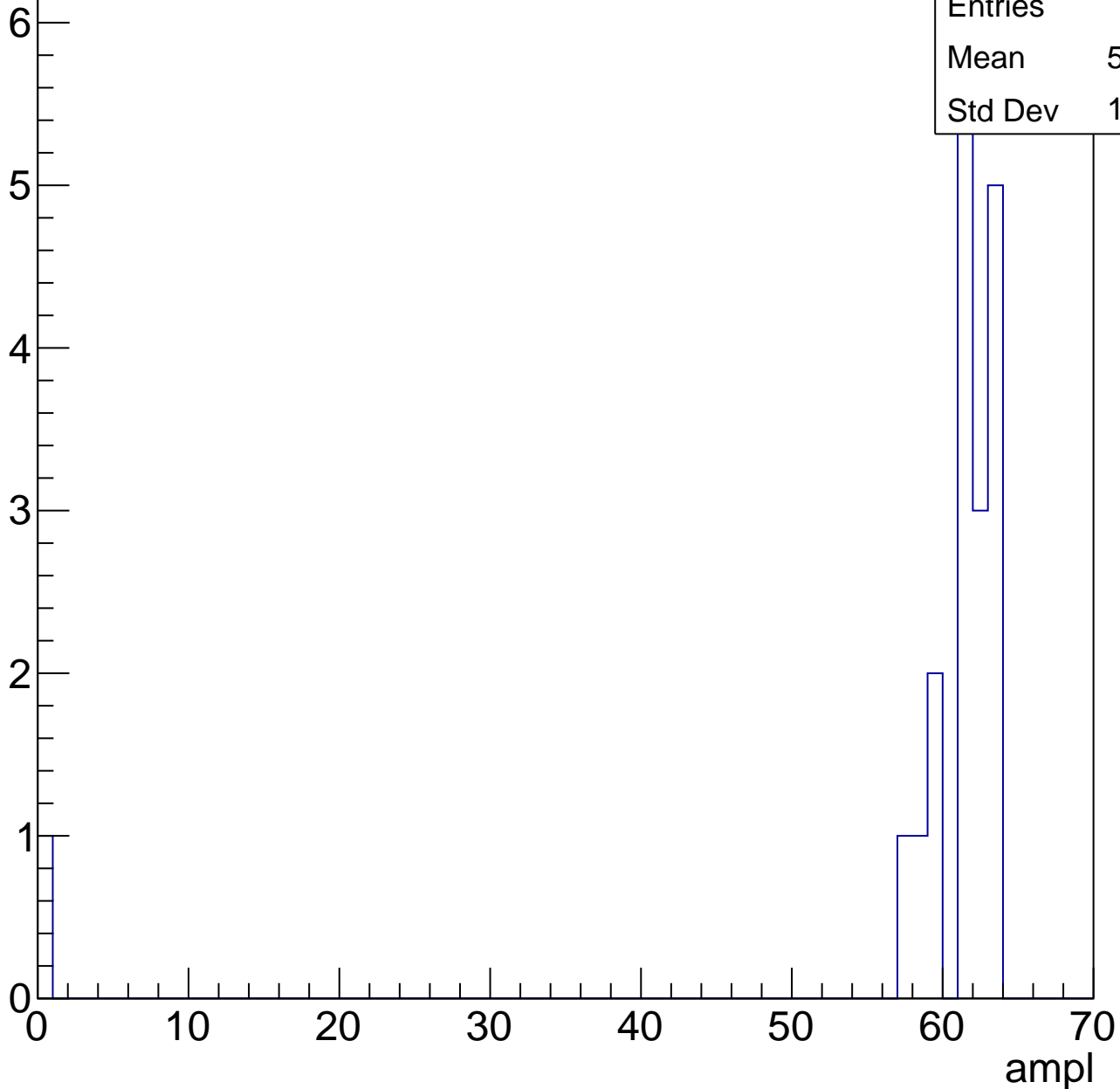


B1L103S, U23-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	57.89
Std Dev	13.75



B1L103S, U23-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch95, adc0

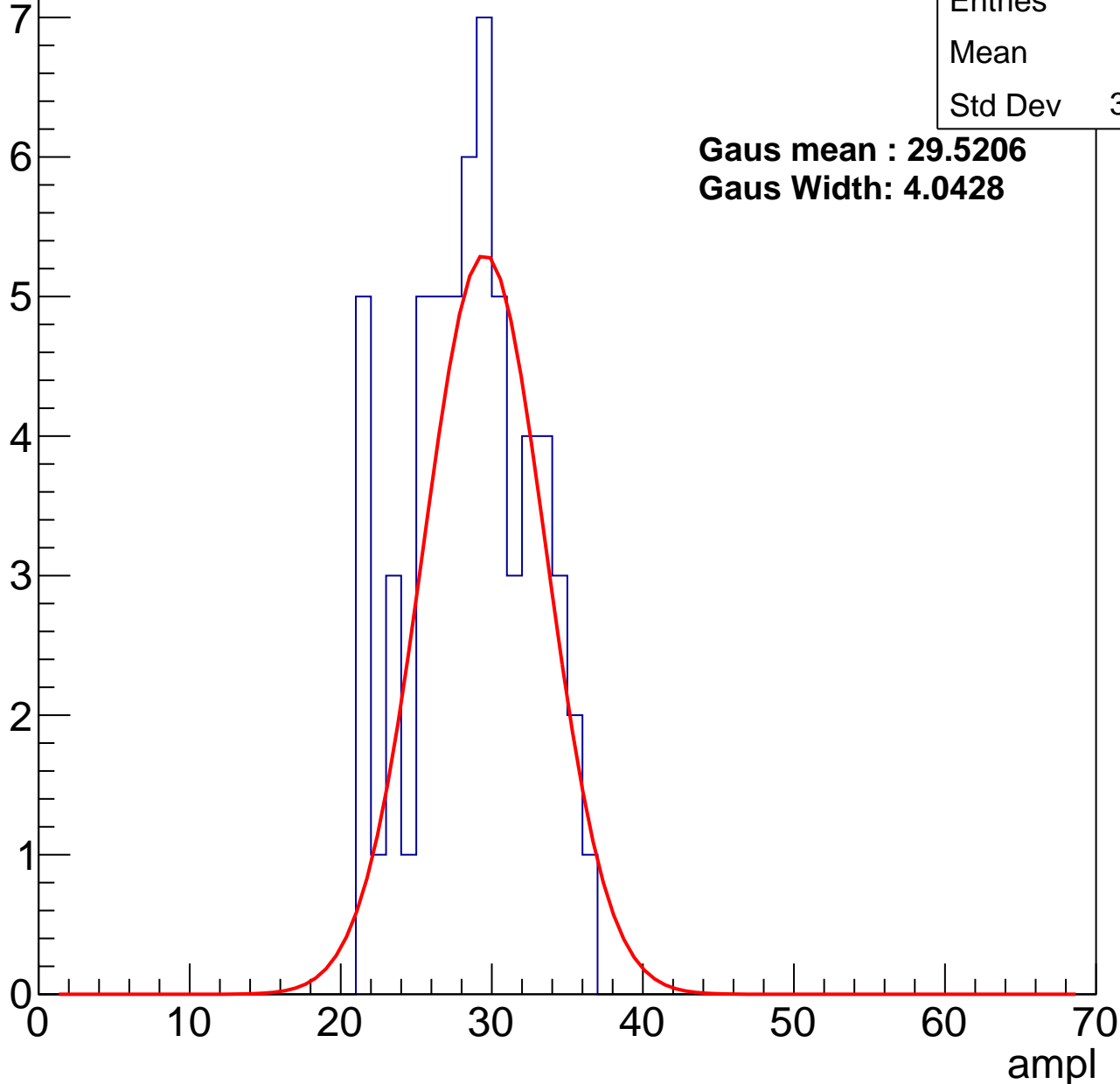
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.2
Std Dev	3.932

Gaus mean : 29.5206

Gaus Width: 4.0428



B1L103S, U23-ch95, adc1

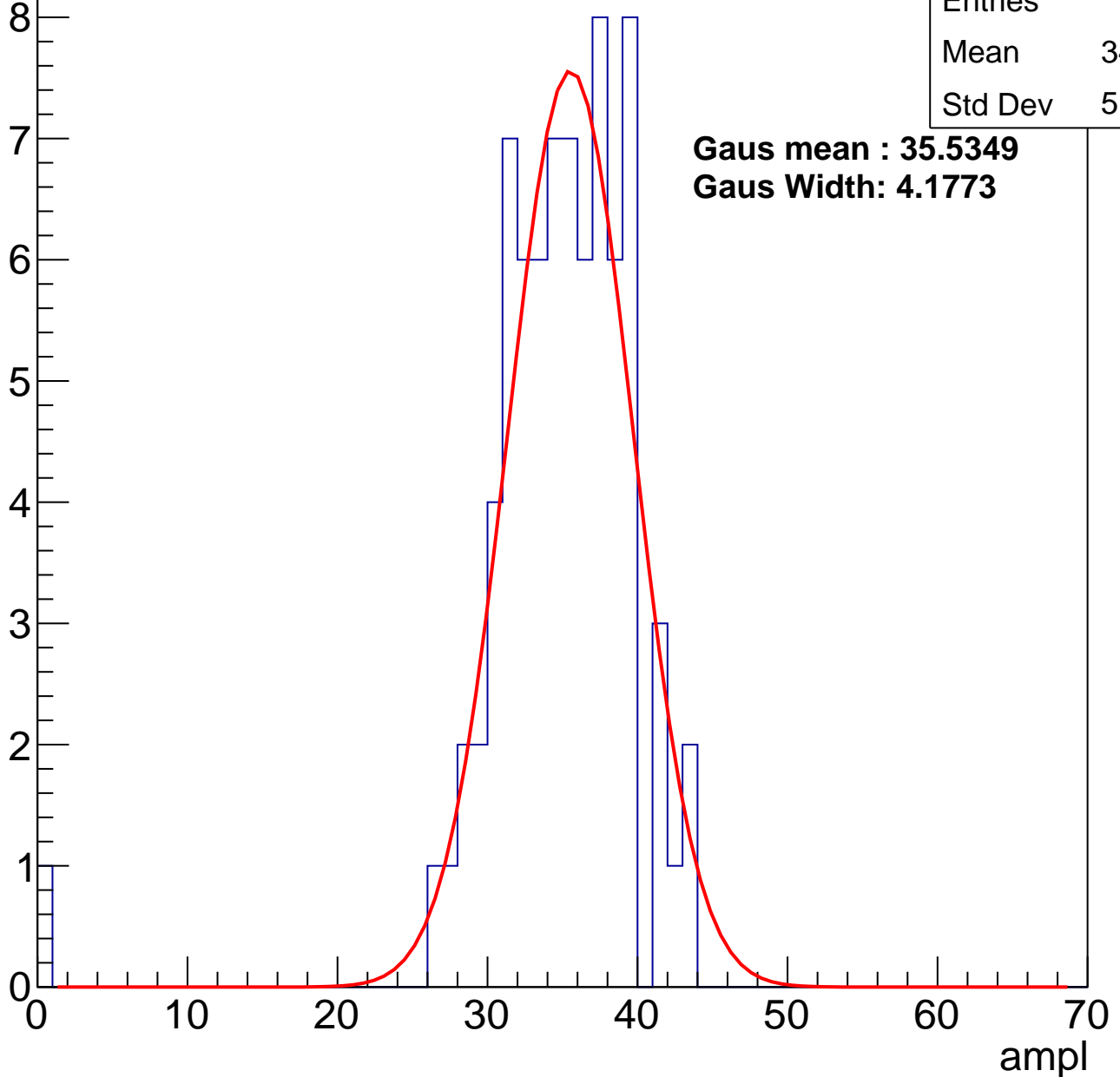
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	34.36
Std Dev	5.442

Gaus mean : 35.5349

Gaus Width: 4.1773



B1L103S, U23-ch95, adc2

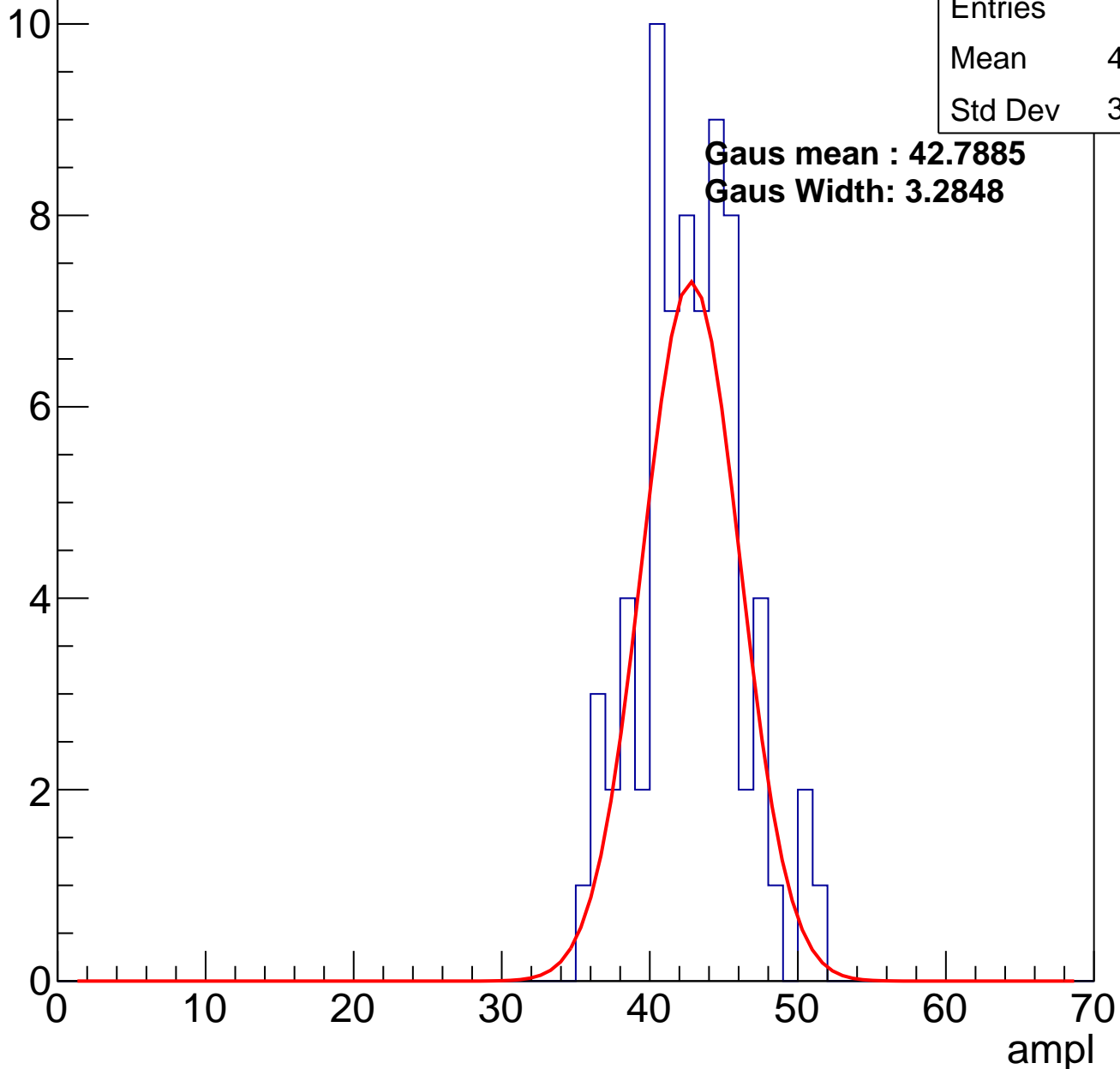
calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	42.34
Std Dev	3.398

Gaus mean : 42.7885

Gaus Width: 3.2848

Entry

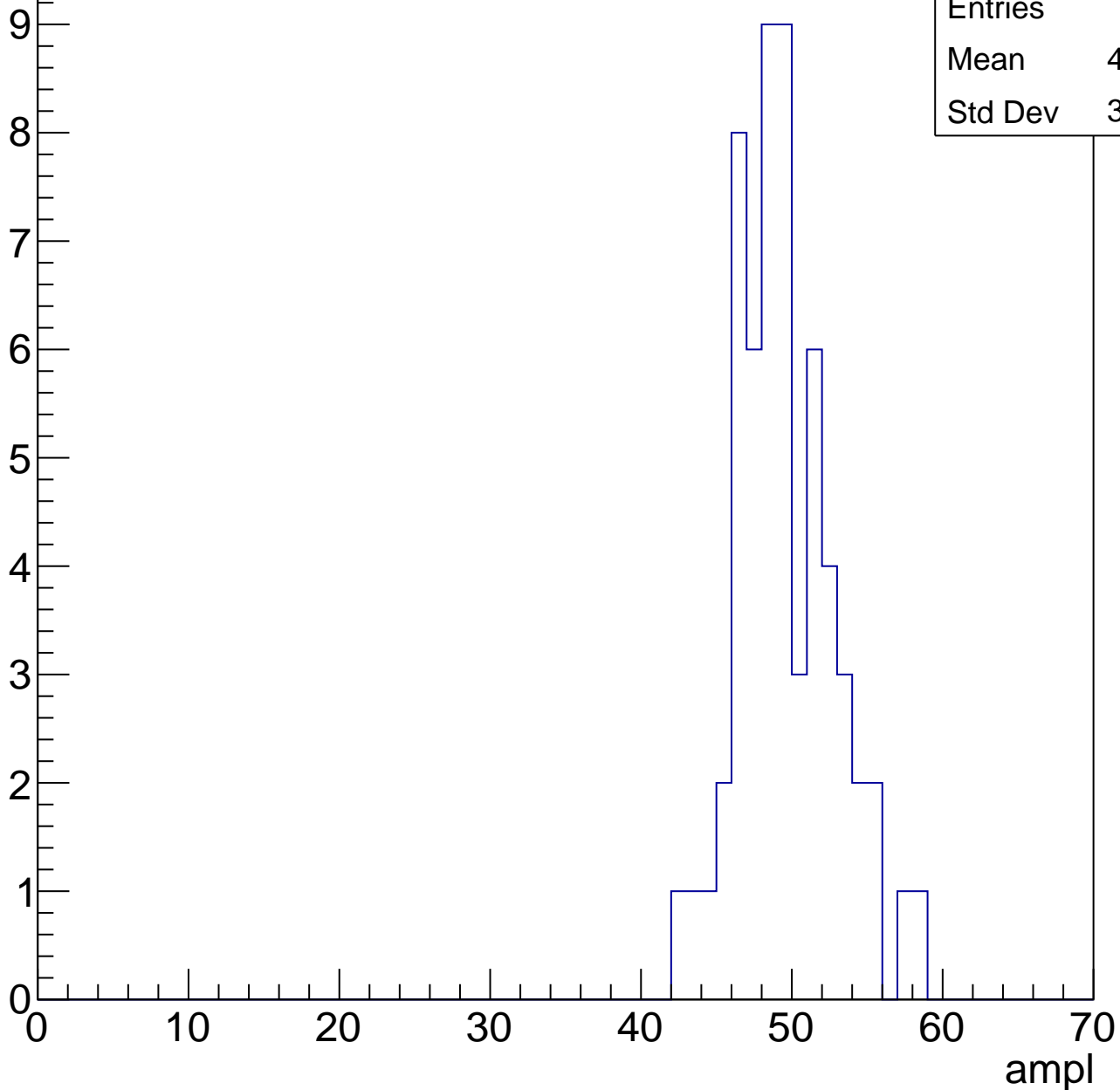


B1L103S, U23-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

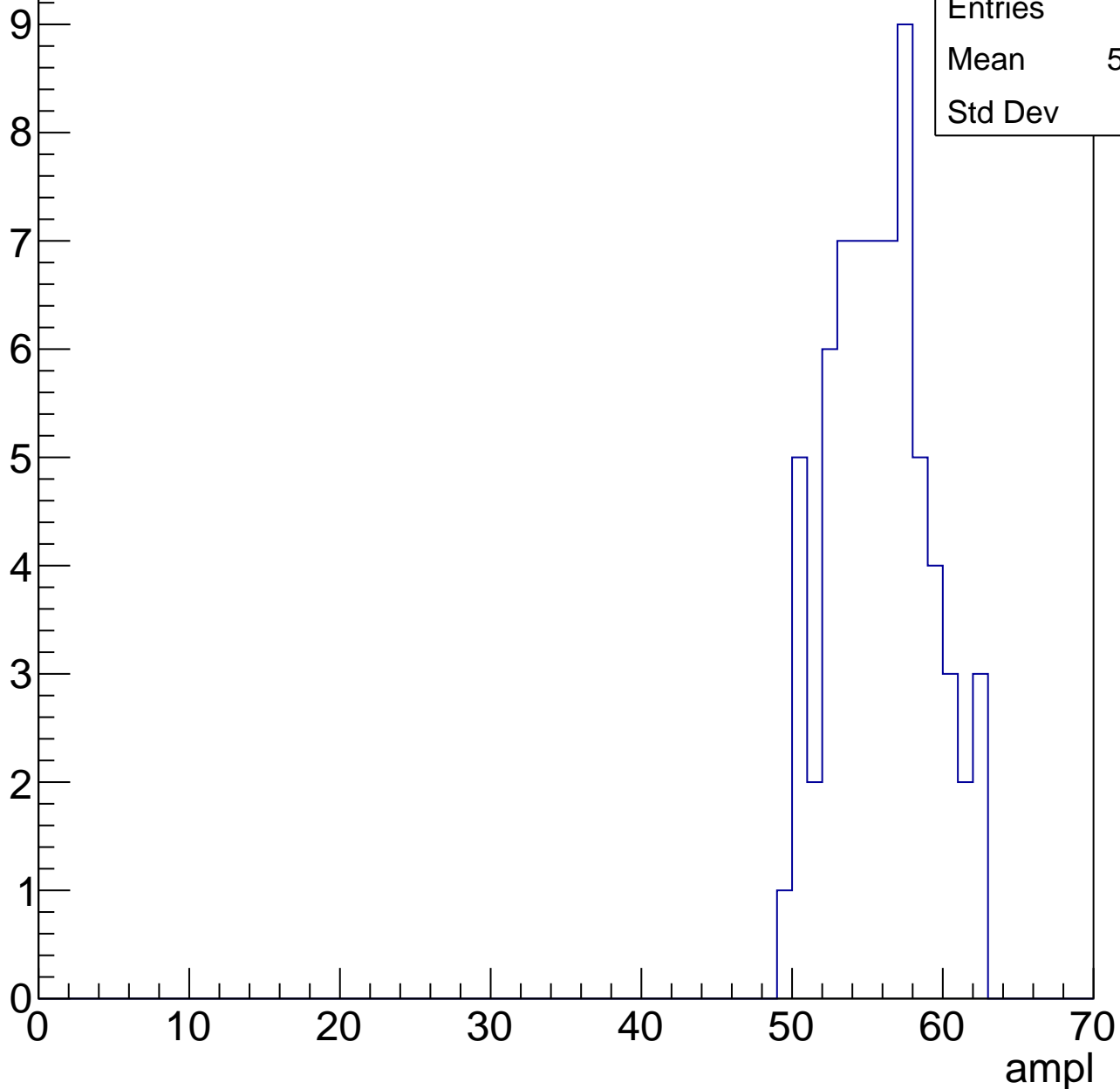
Entries	59
Mean	49.12
Std Dev	3.253



B1L103S, U23-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

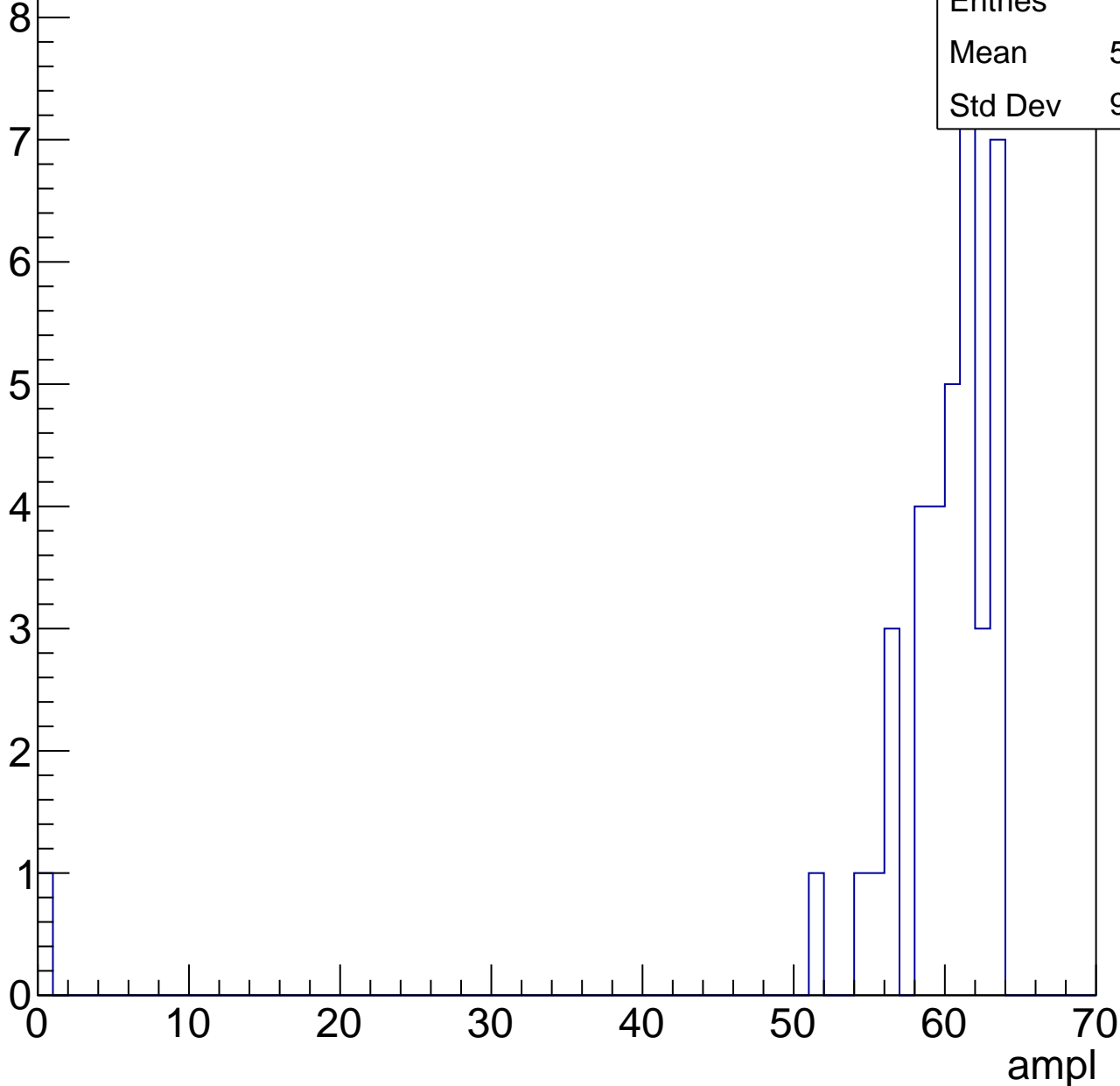


B1L103S, U23-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

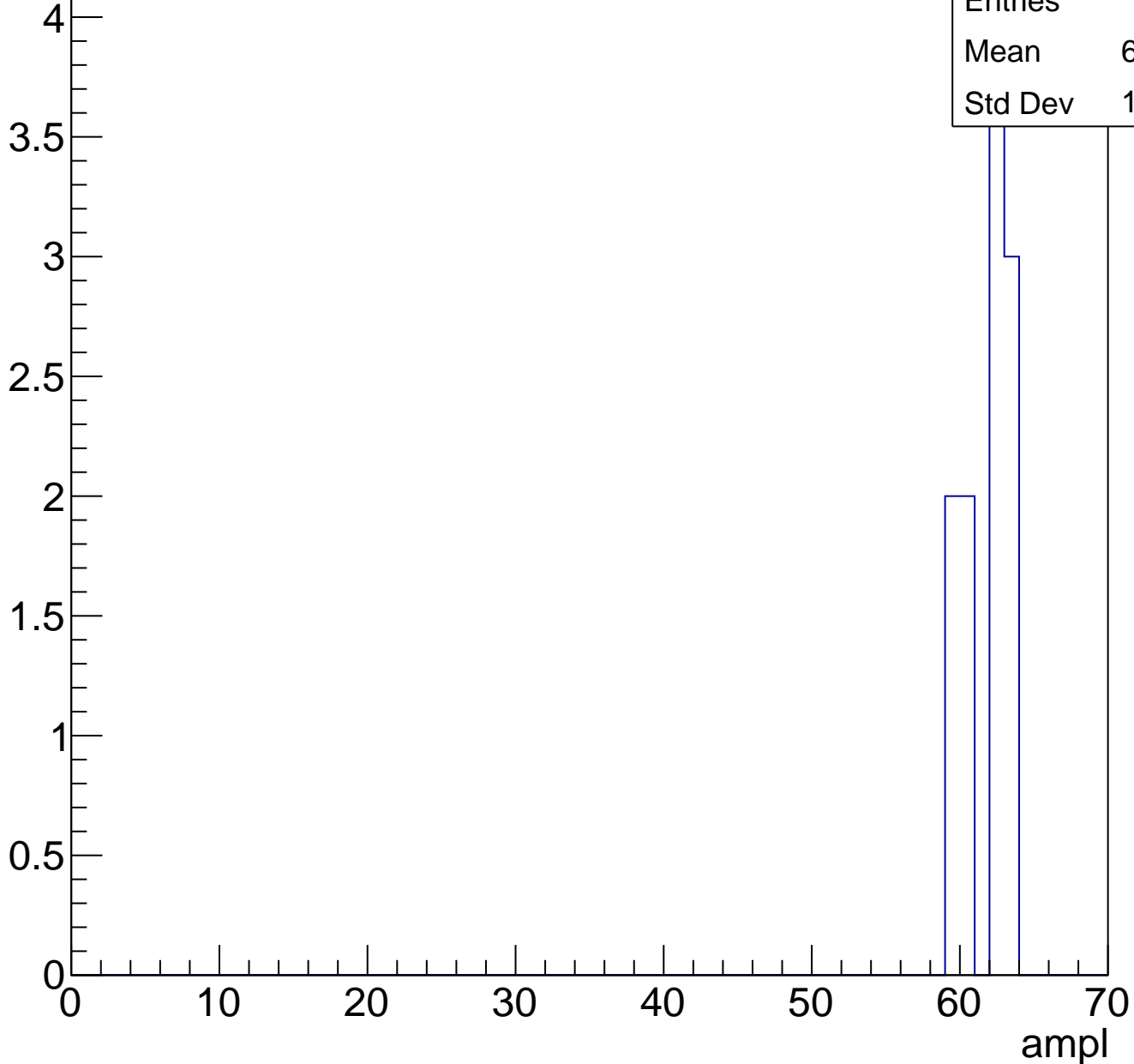
Entries	38
Mean	58.18
Std Dev	9.957



B1L103S, U23-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch96, adc0

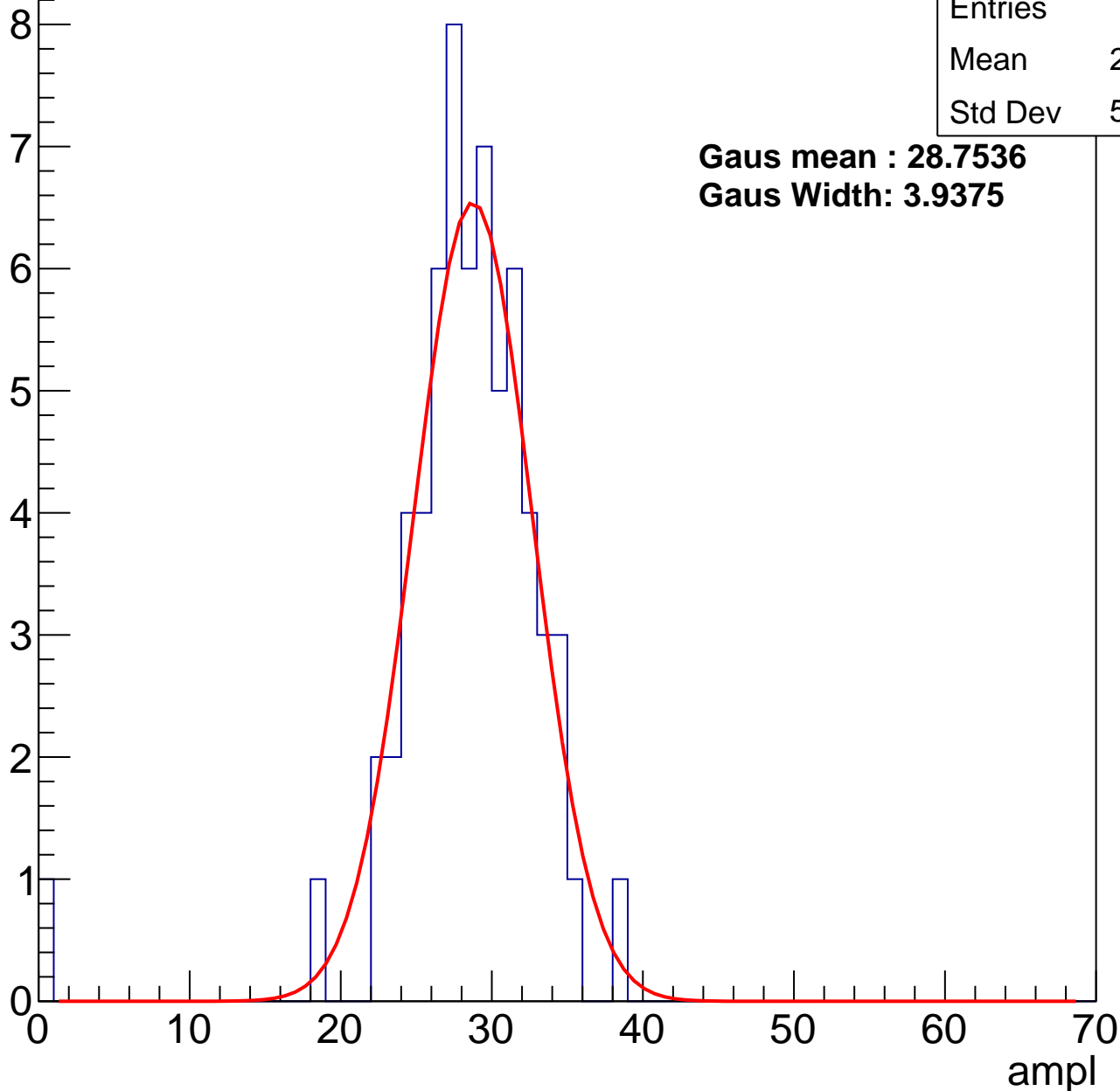
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	27.89
Std Dev	5.022

Gaus mean : 28.7536

Gaus Width: 3.9375



B1L103S, U23-ch96, adc1

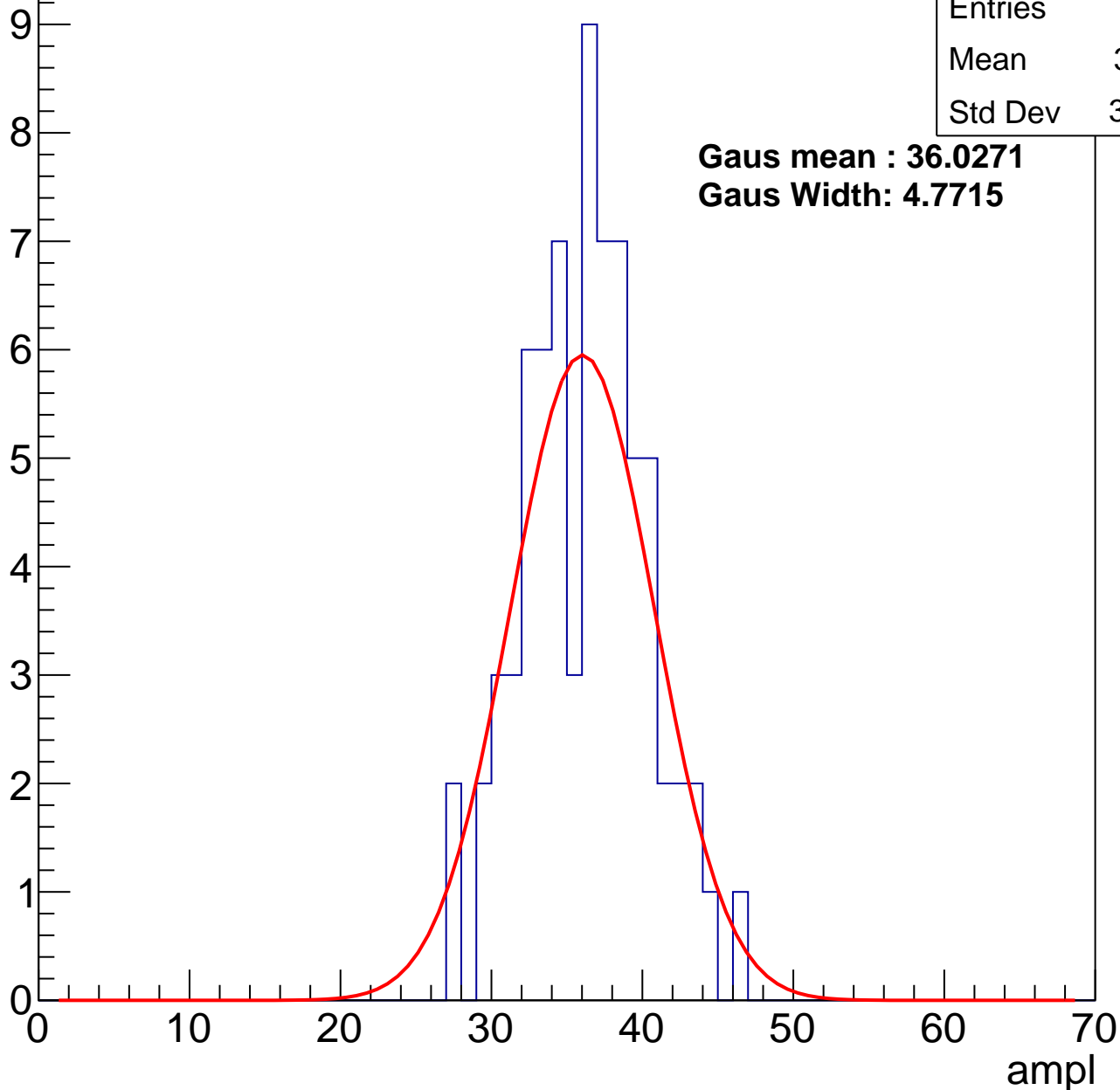
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.81
Std Dev	3.995

Gaus mean : 36.0271

Gaus Width: 4.7715



B1L103S, U23-ch96, adc2

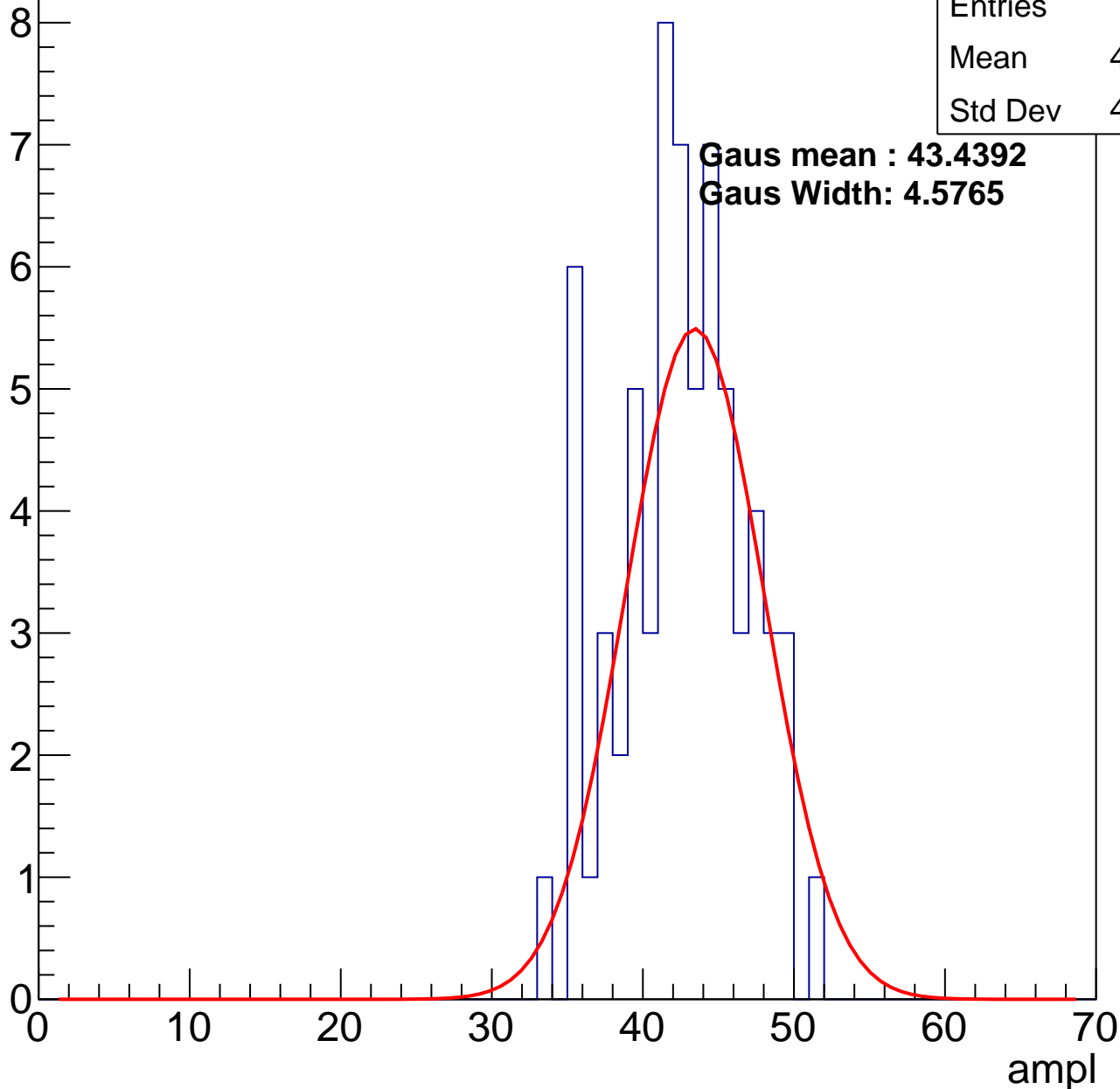
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.07
Std Dev	4.133

Gaus mean : 43.4392

Gaus Width: 4.5765

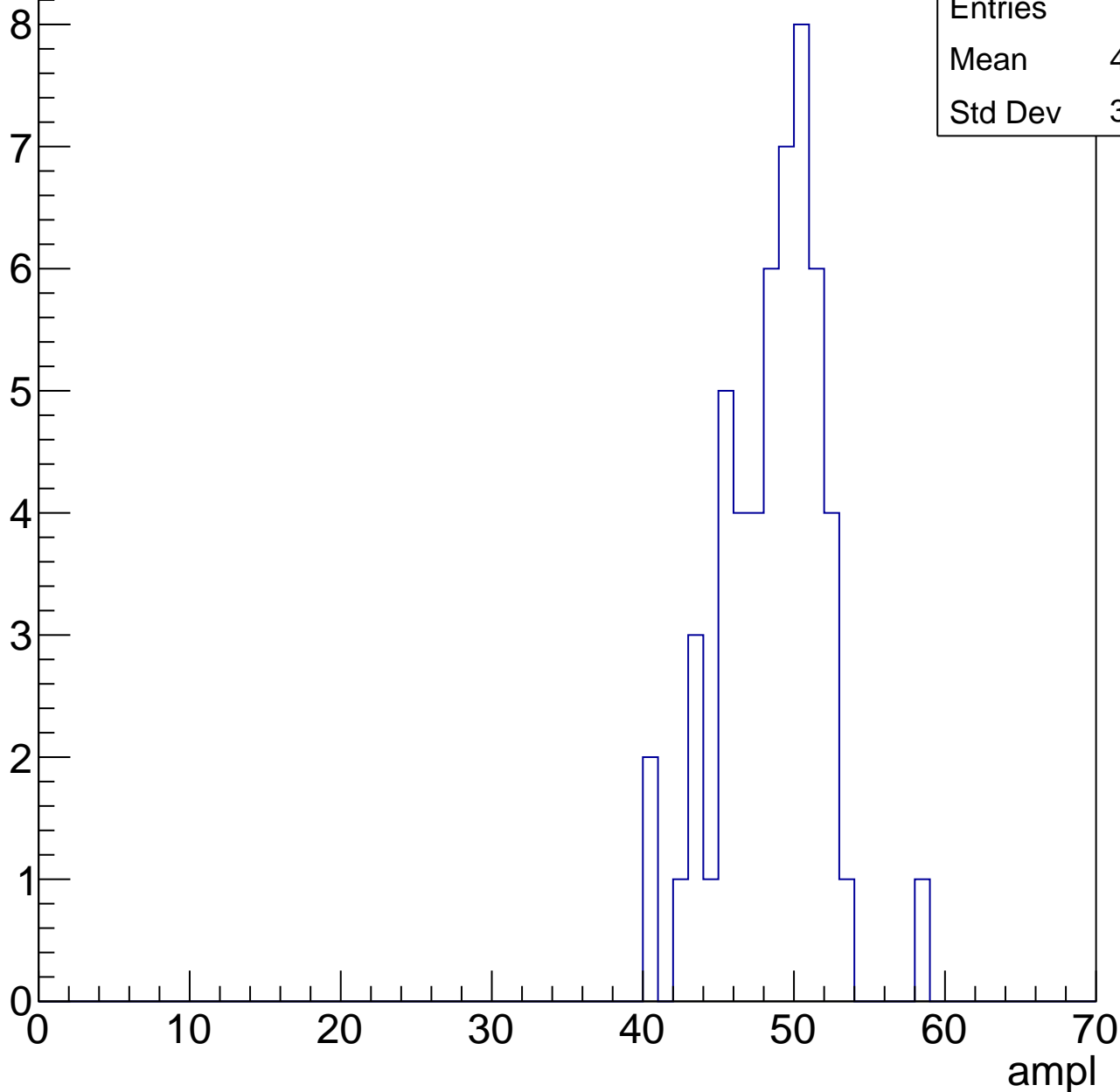


B1L103S, U23-ch96, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.08
Std Dev	3.369

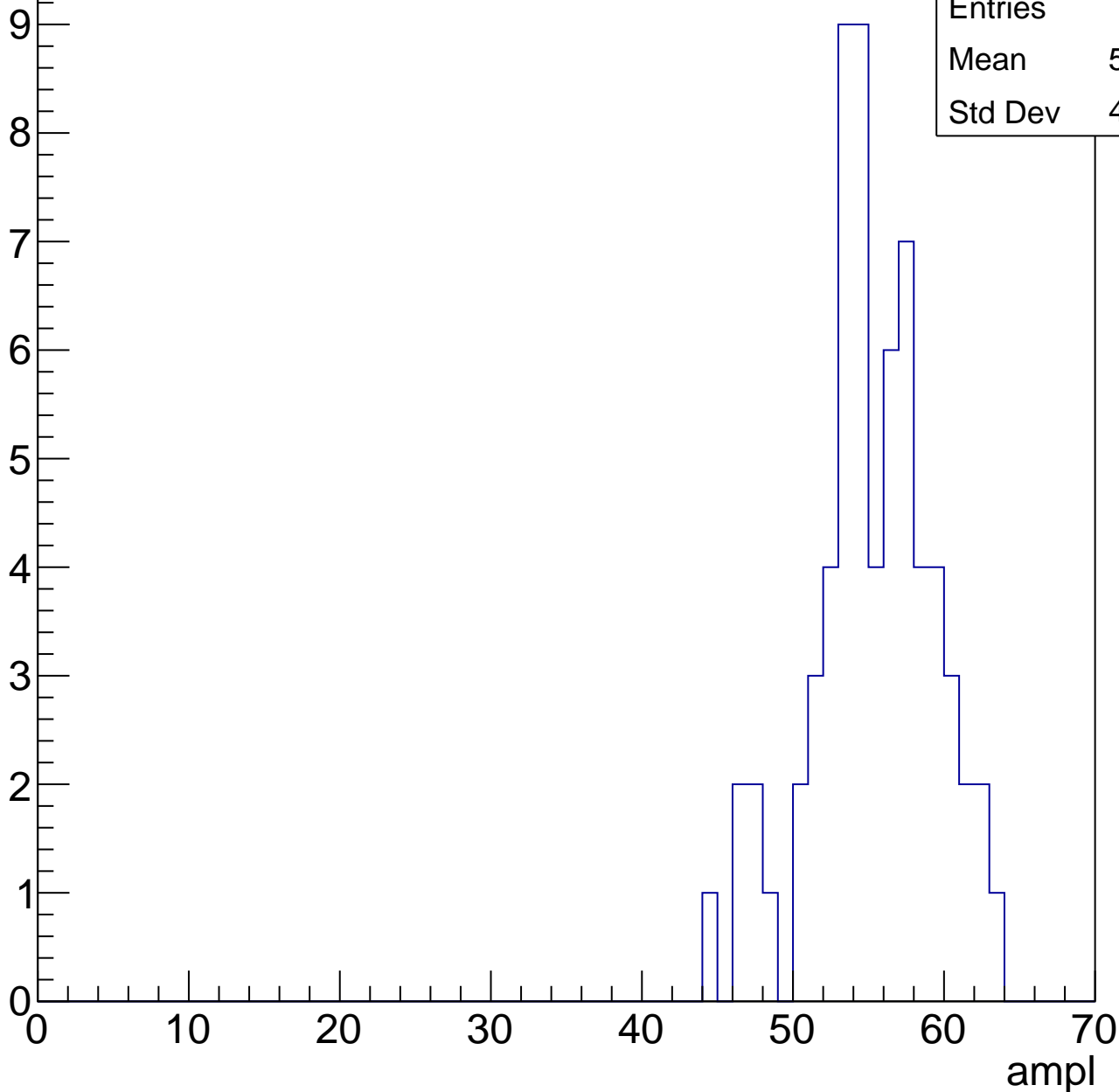


B1L103S, U23-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	54.76
Std Dev	4.042

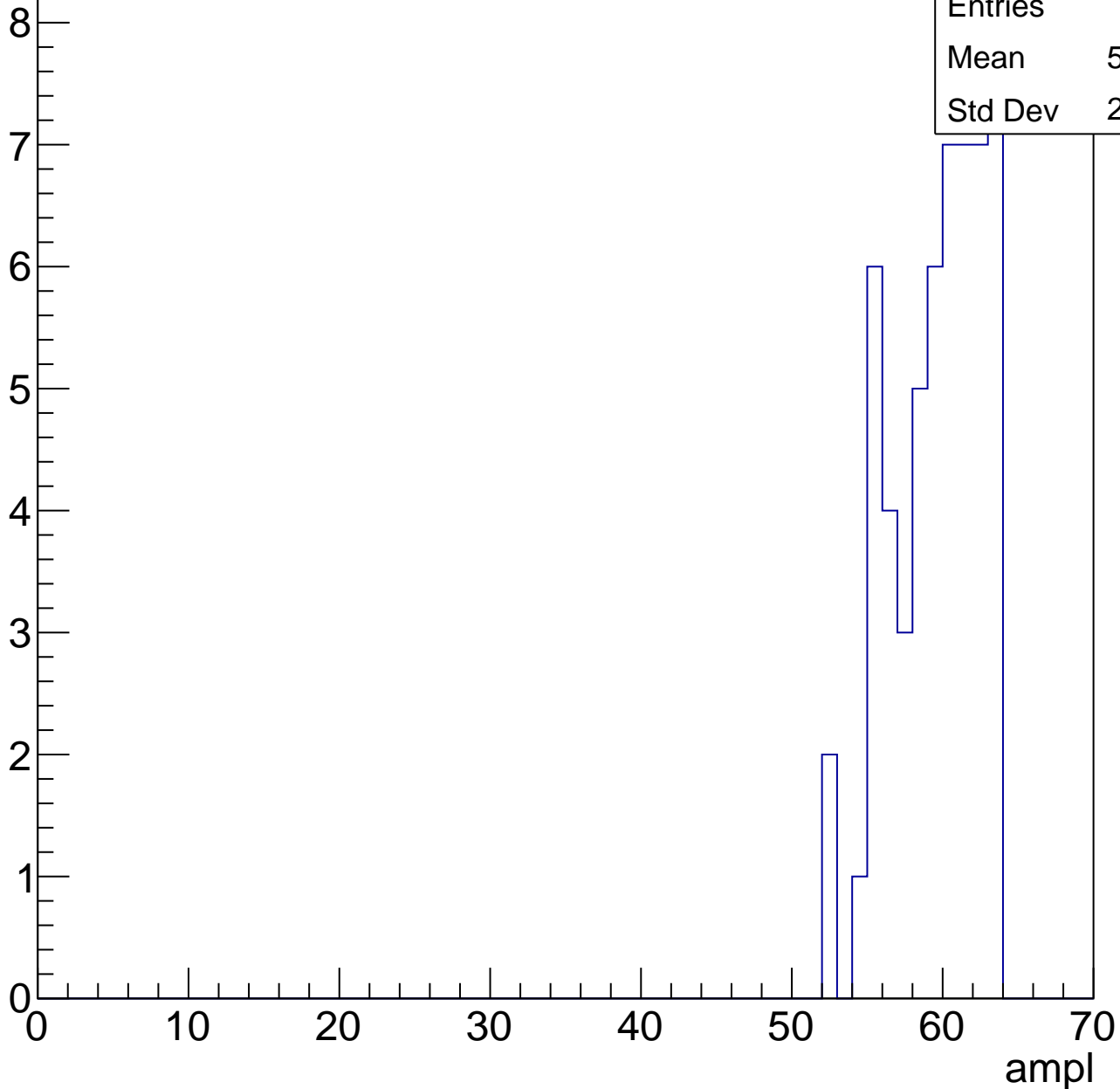


B1L103S, U23-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	59.14
Std Dev	2.979



B1L103S, U23-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch97, adc0

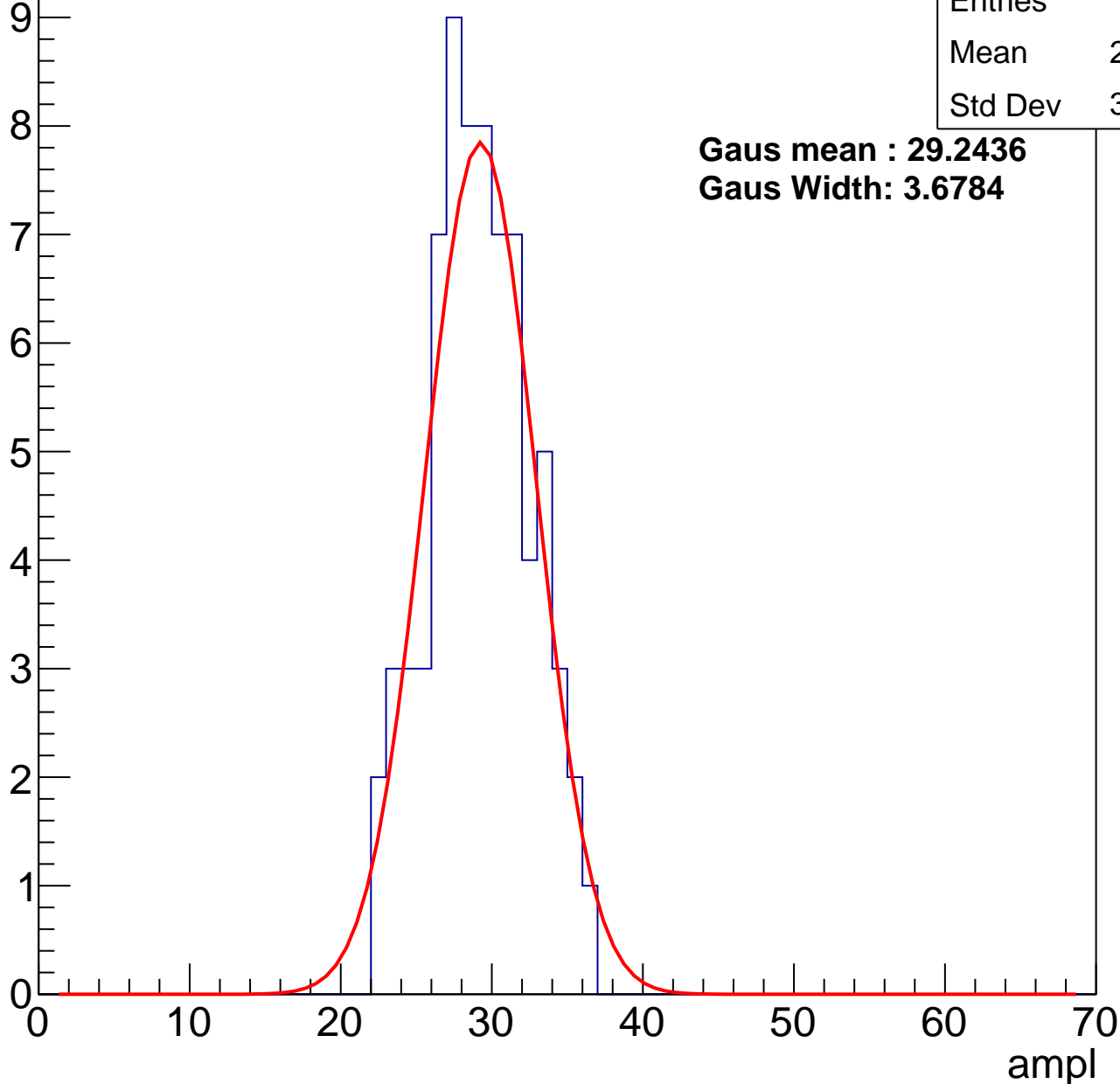
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.74
Std Dev	3.287

Gaus mean : 29.2436

Gaus Width: 3.6784



B1L103S, U23-ch97, adc1

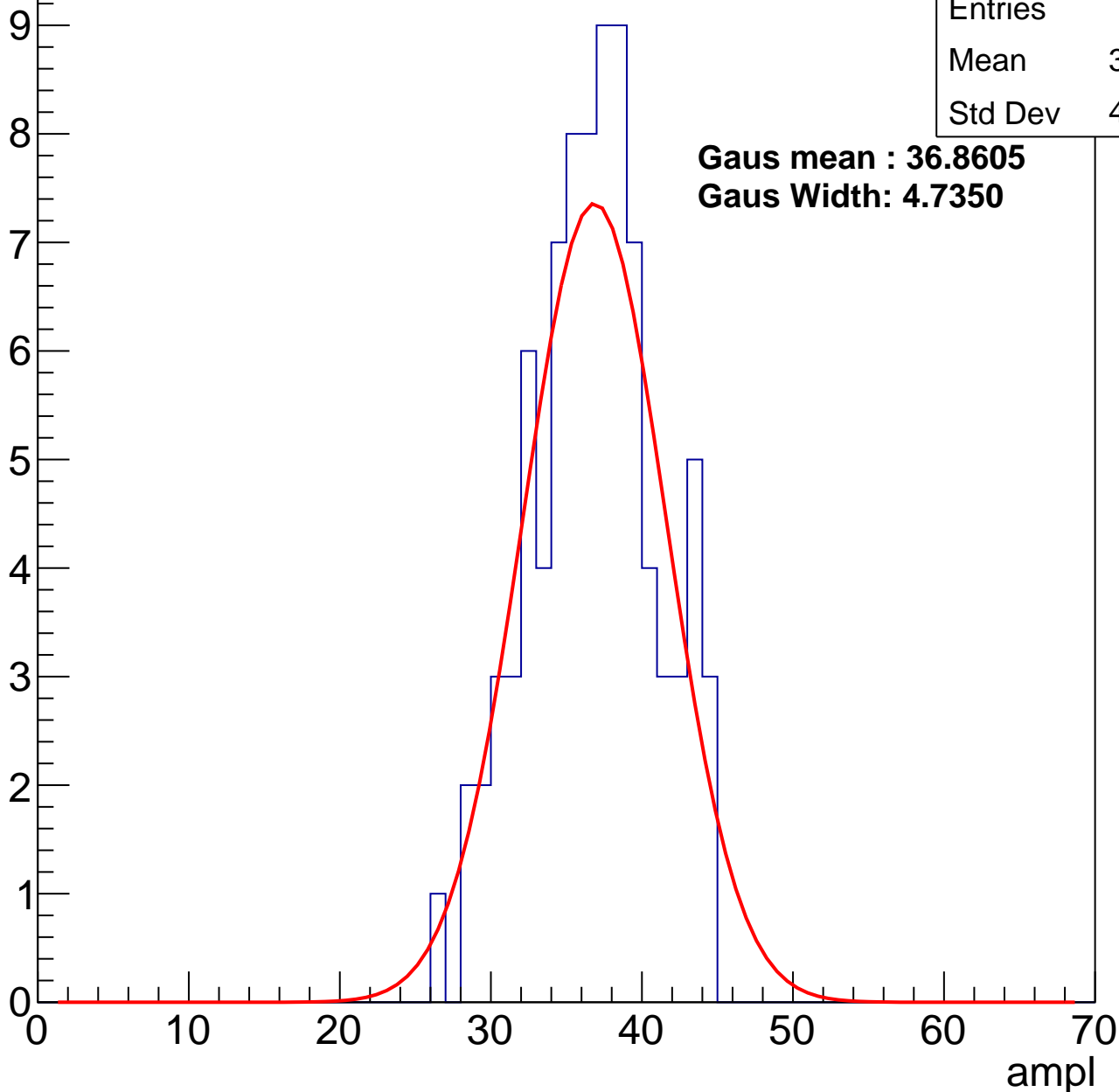
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	36.29
Std Dev	4.099

Gaus mean : 36.8605

Gaus Width: 4.7350



B1L103S, U23-ch97, adc2

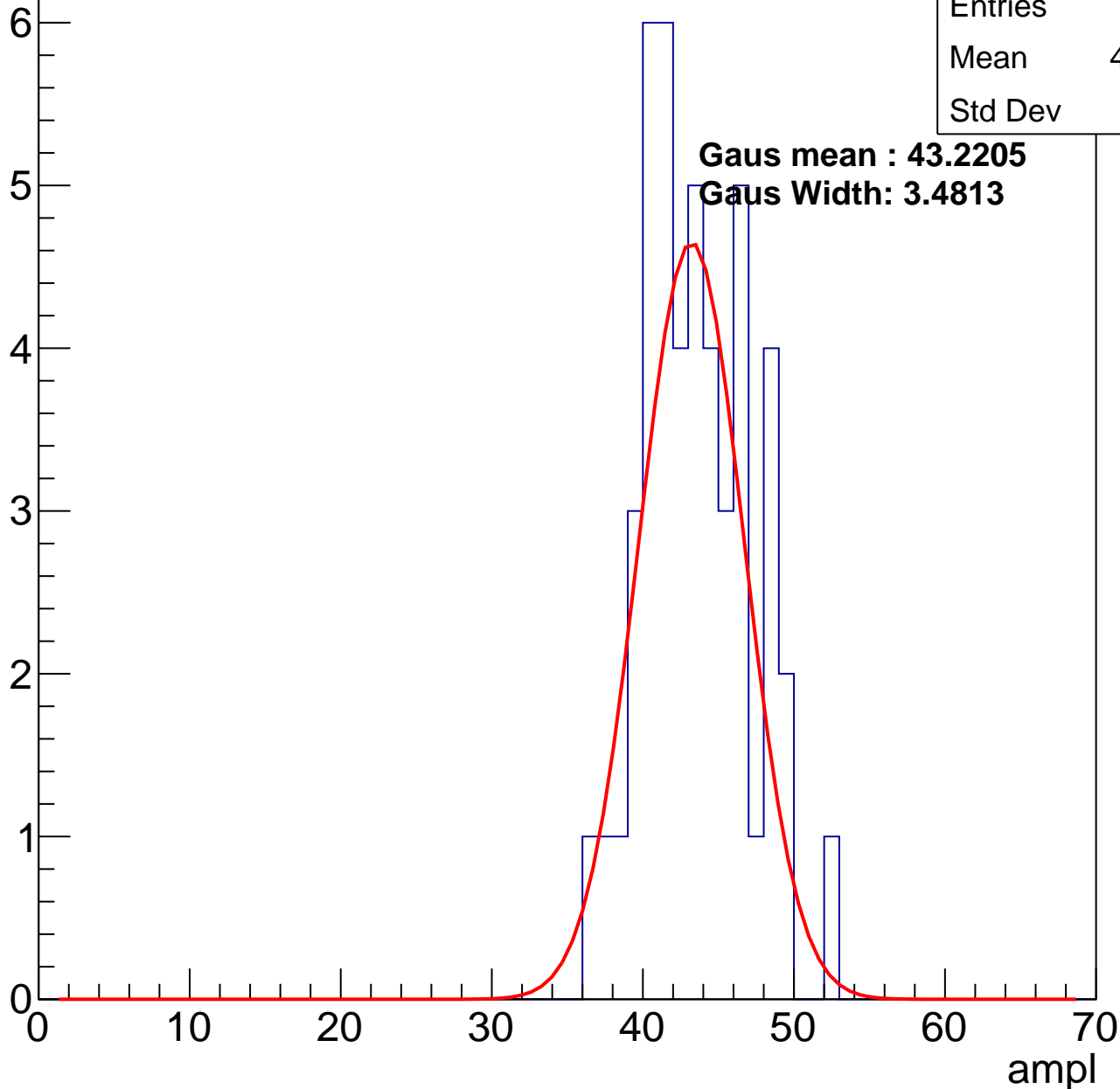
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	43.13
Std Dev	3.48

Gaus mean : 43.2205

Gaus Width: 3.4813

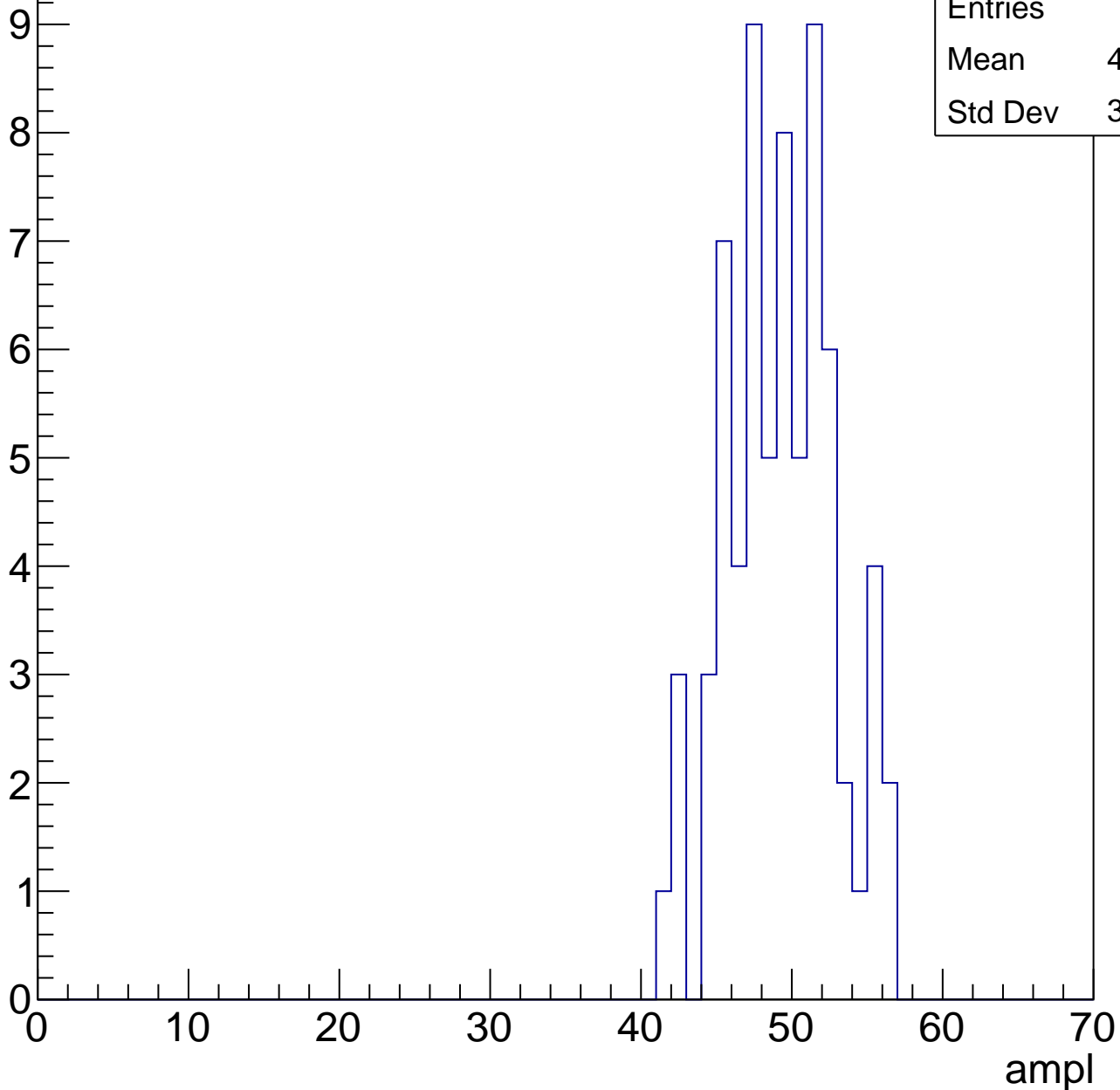


B1L103S, U23-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.78
Std Dev	3.542

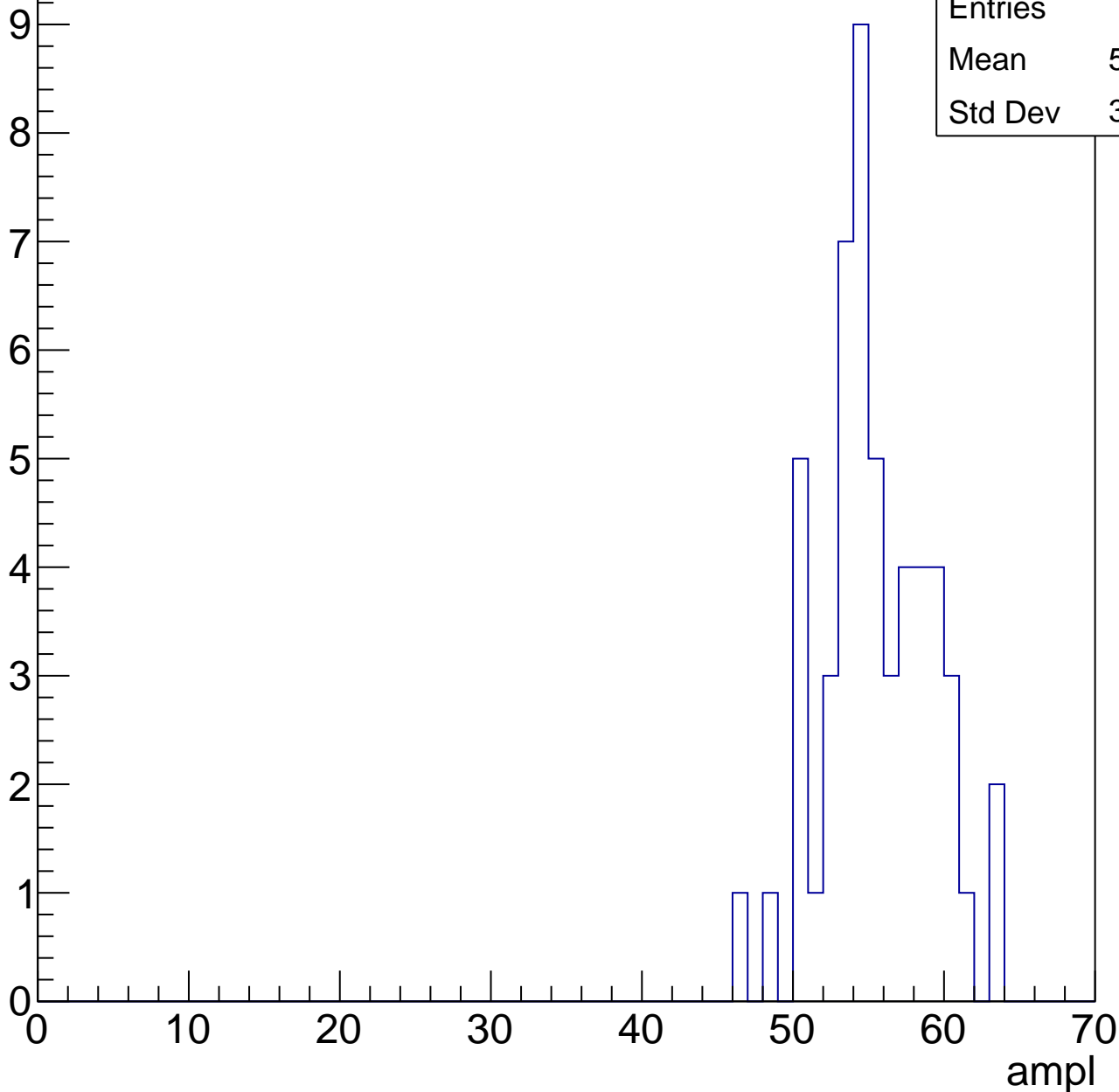


B1L103S, U23-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	54.98
Std Dev	3.616

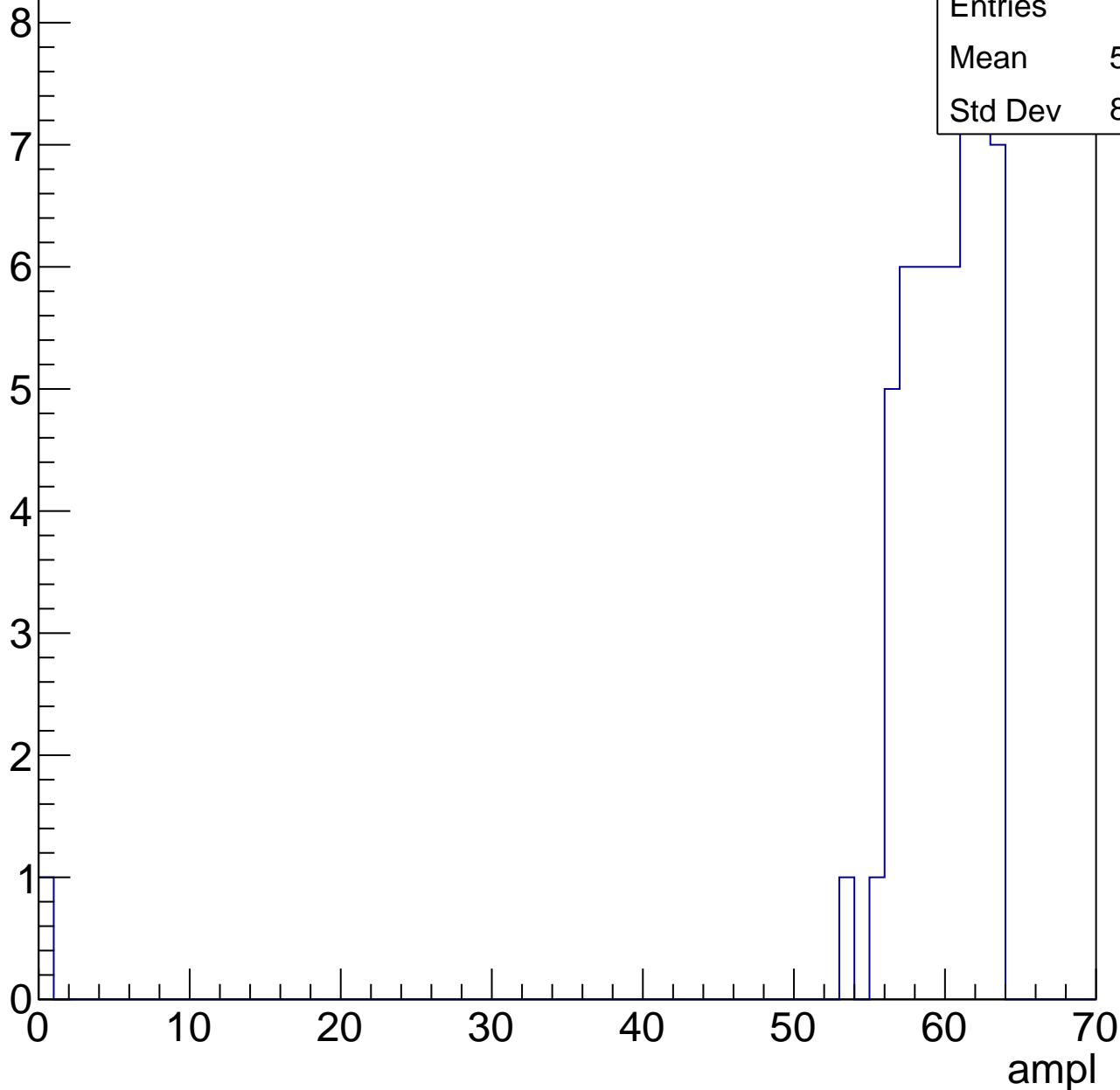


B1L103S, U23-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	58.49
Std Dev	8.329



B1L103S, U23-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

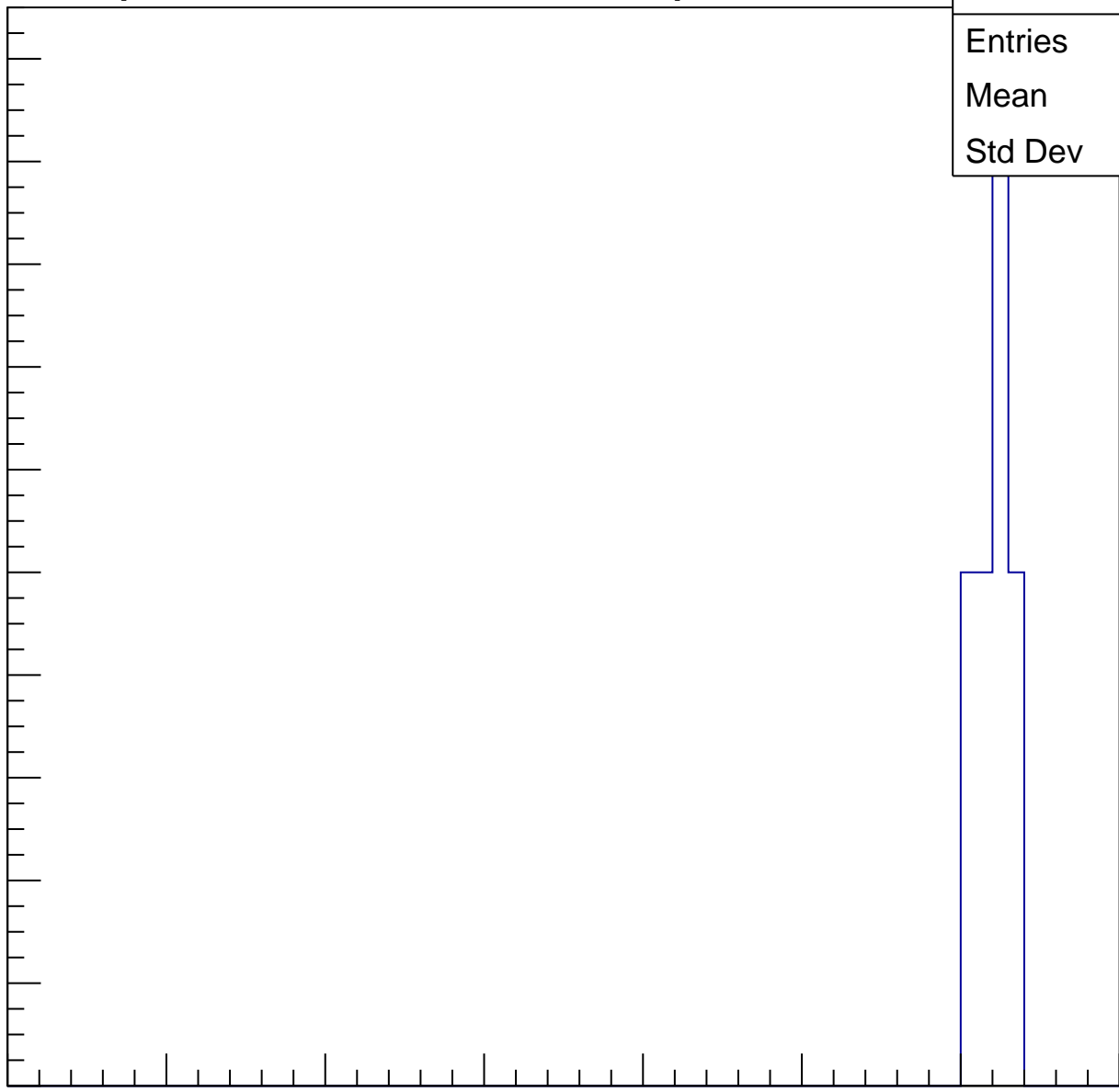
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.02

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch98, adc0

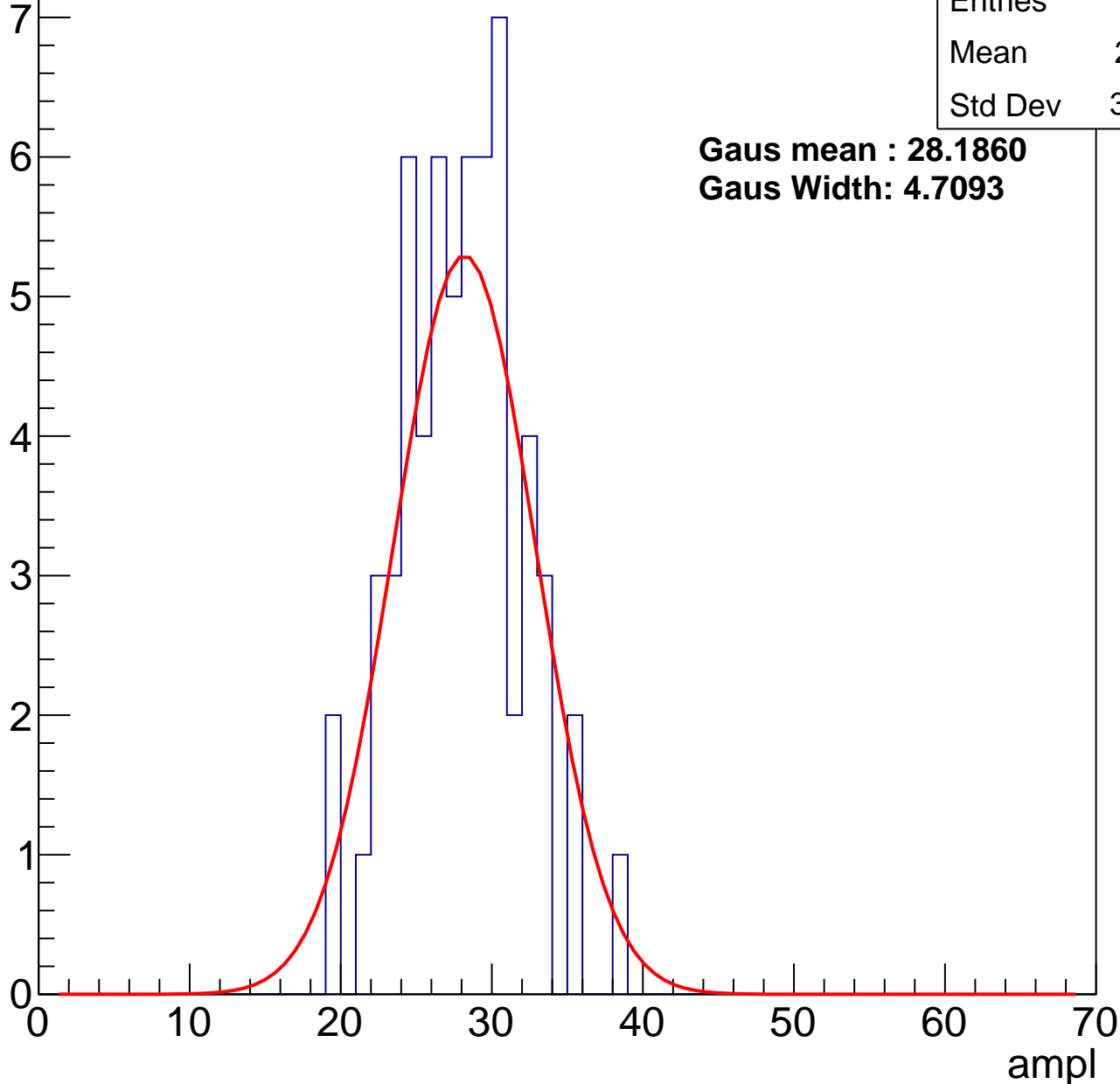
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	27.51
Std Dev	3.903

Gaus mean : 28.1860

Gaus Width: 4.7093



B1L103S, U23-ch98, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	34.25
Std Dev	4.375

Gaus mean : 34.7522

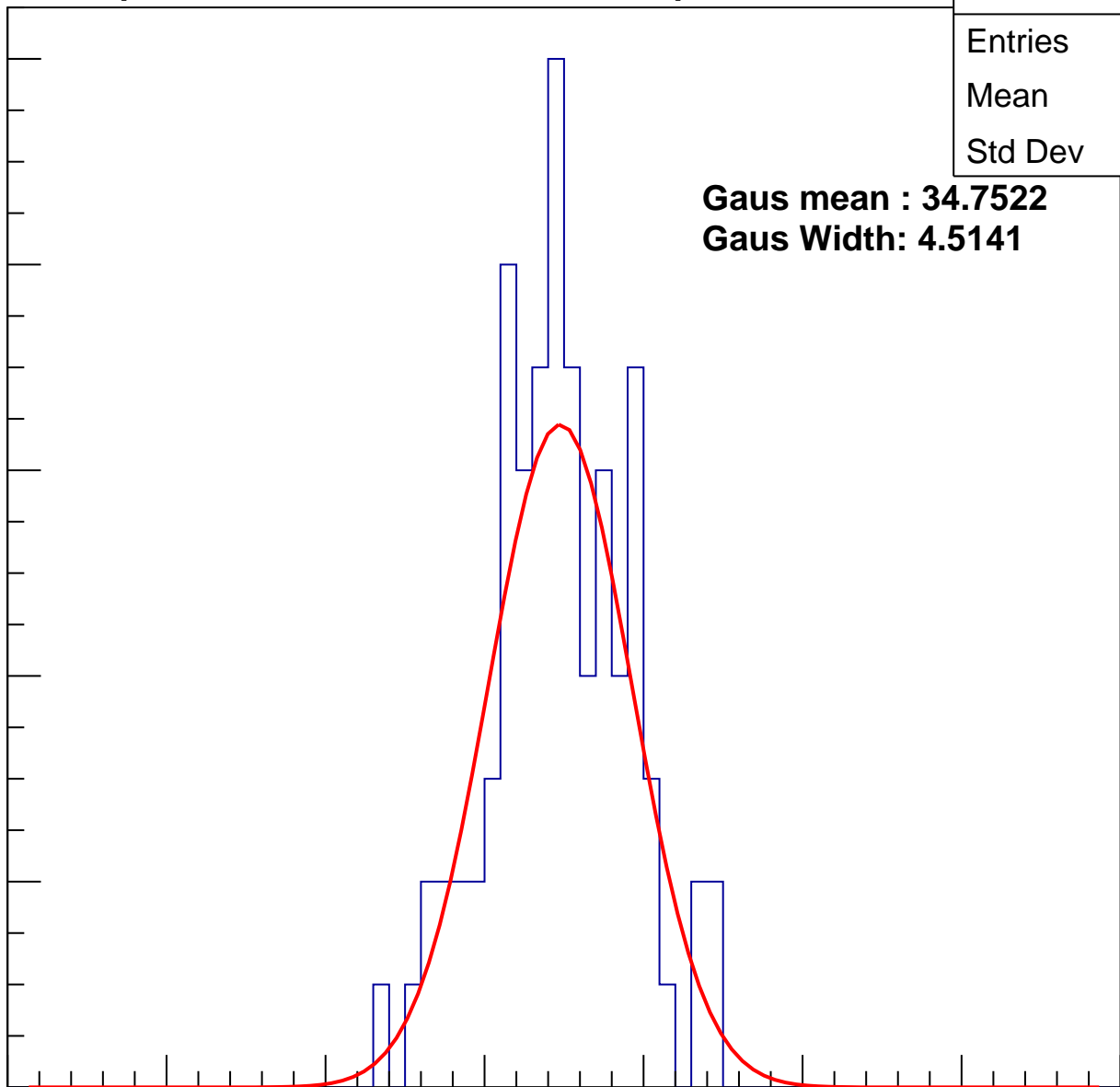
Gaus Width: 4.5141

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch98, adc2

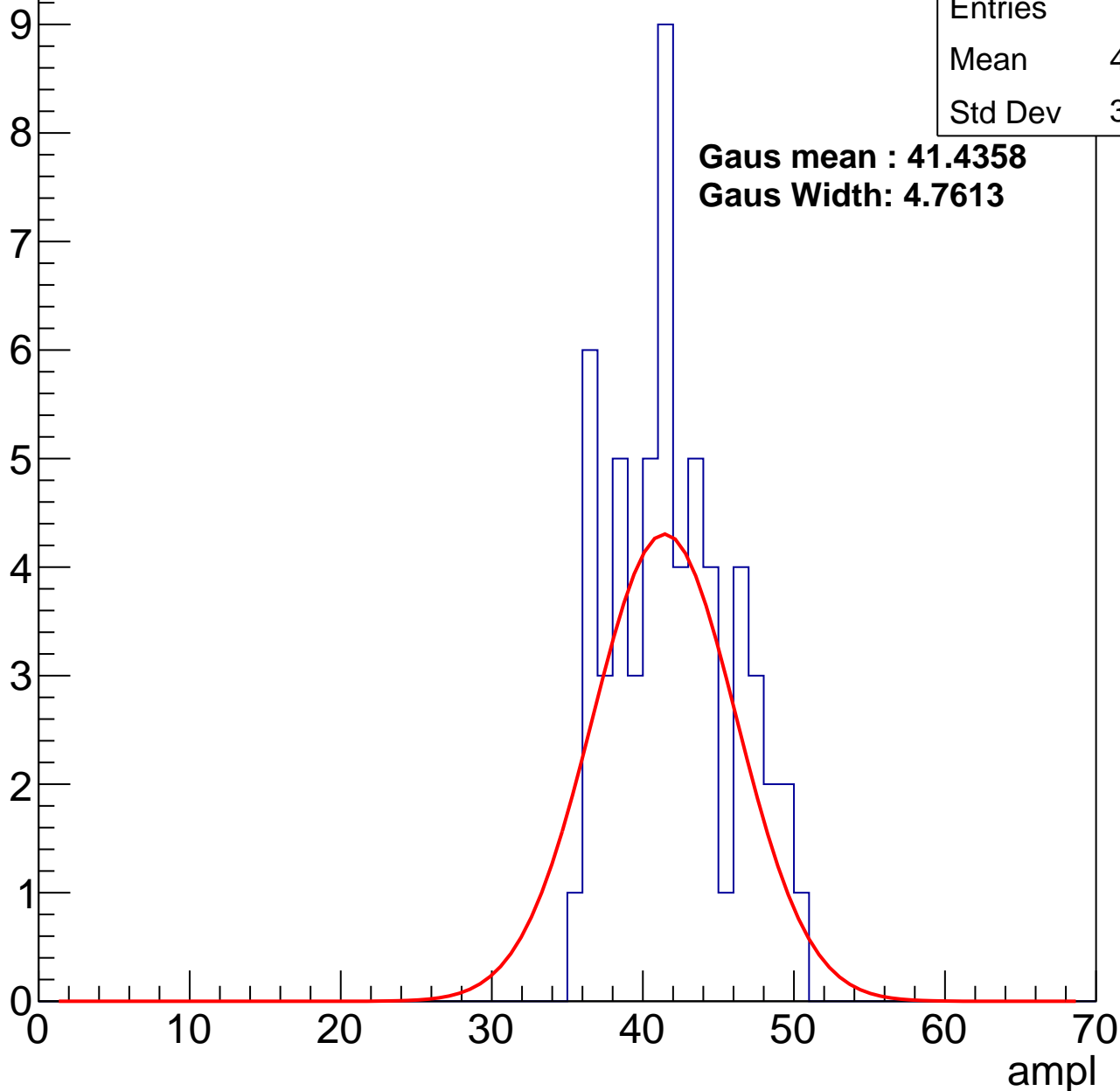
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	41.57
Std Dev	3.869

Gaus mean : 41.4358

Gaus Width: 4.7613

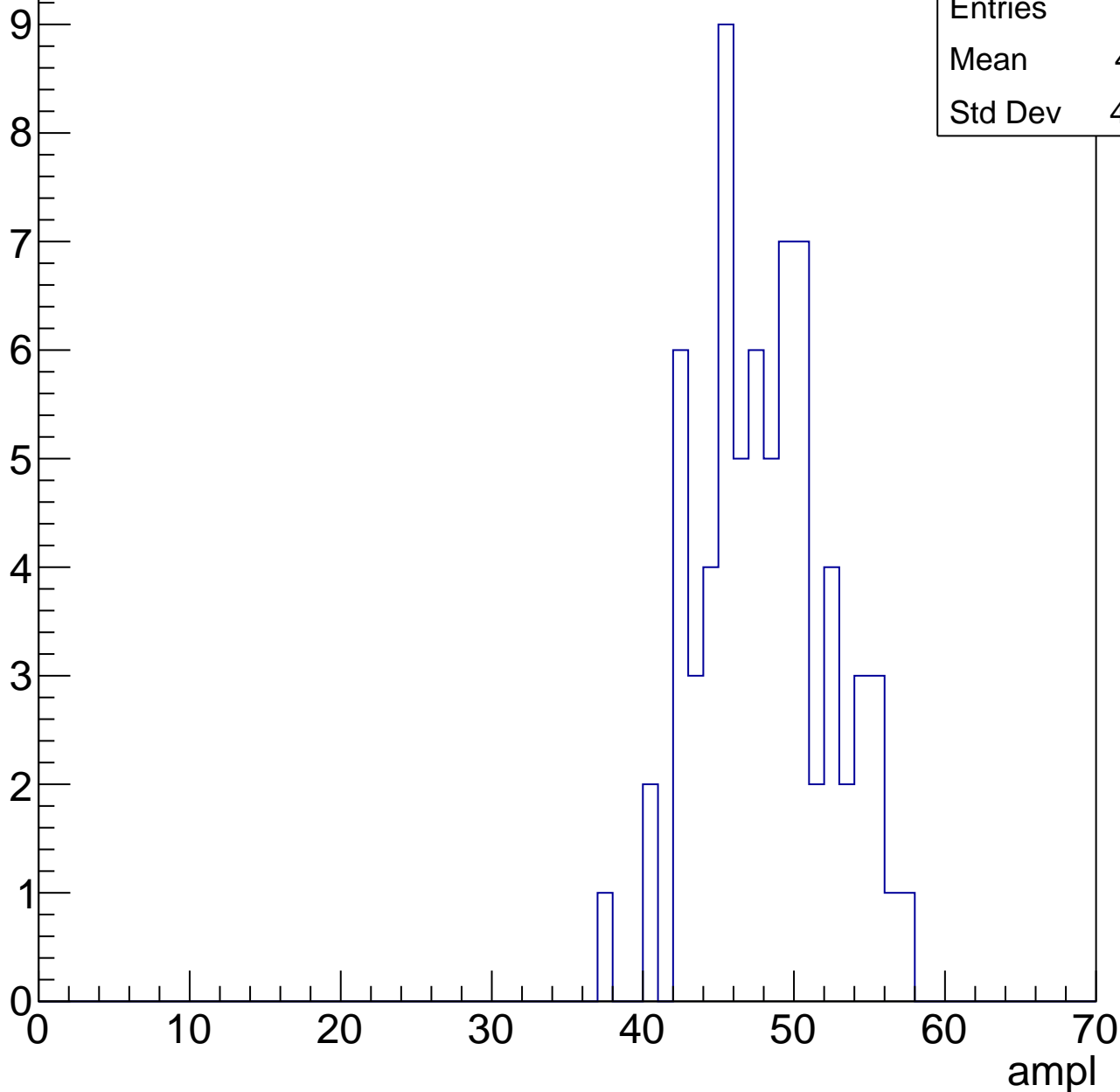


B1L103S, U23-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	47.61
Std Dev	4.224

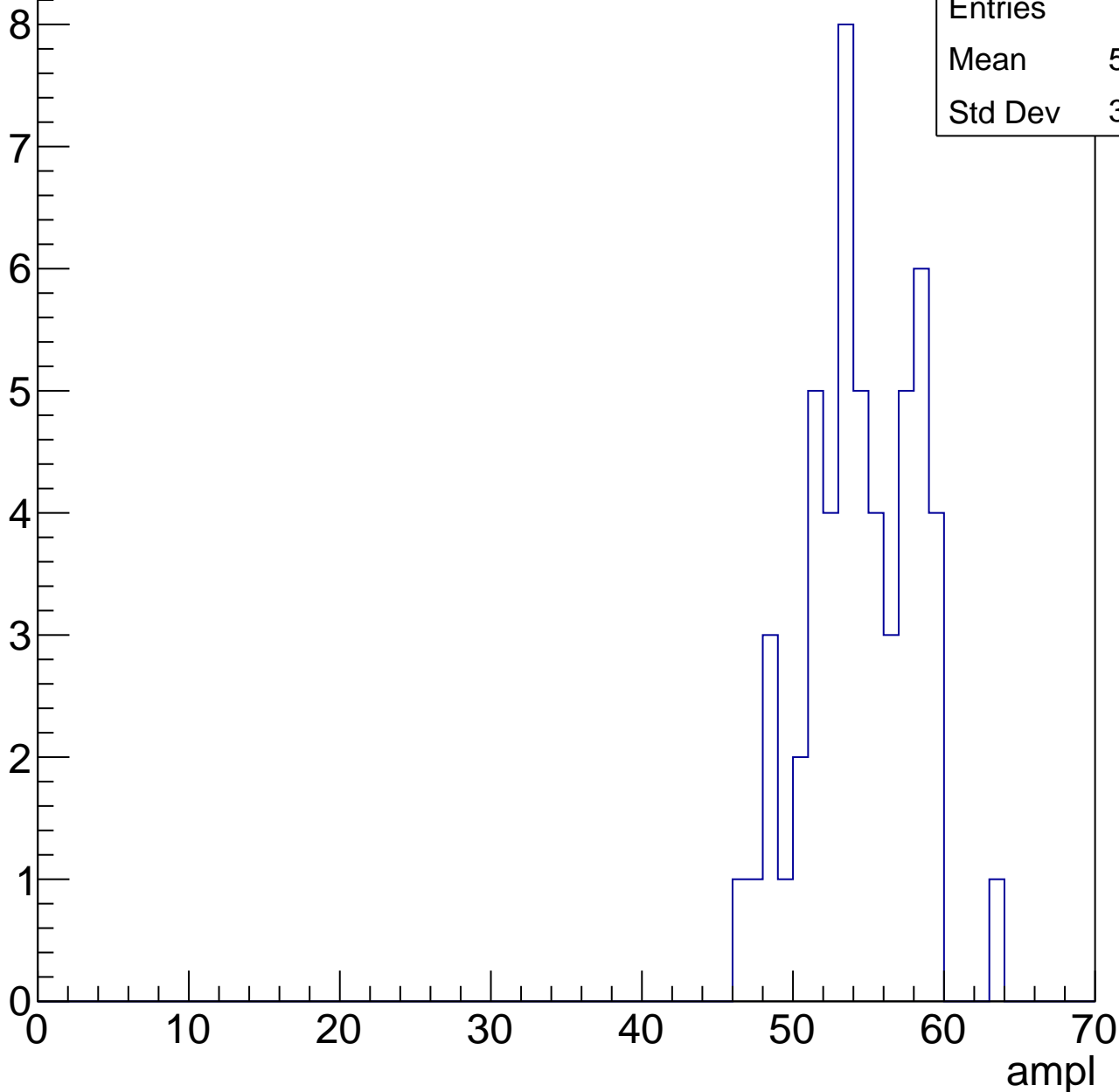


B1L103S, U23-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	54.02
Std Dev	3.595

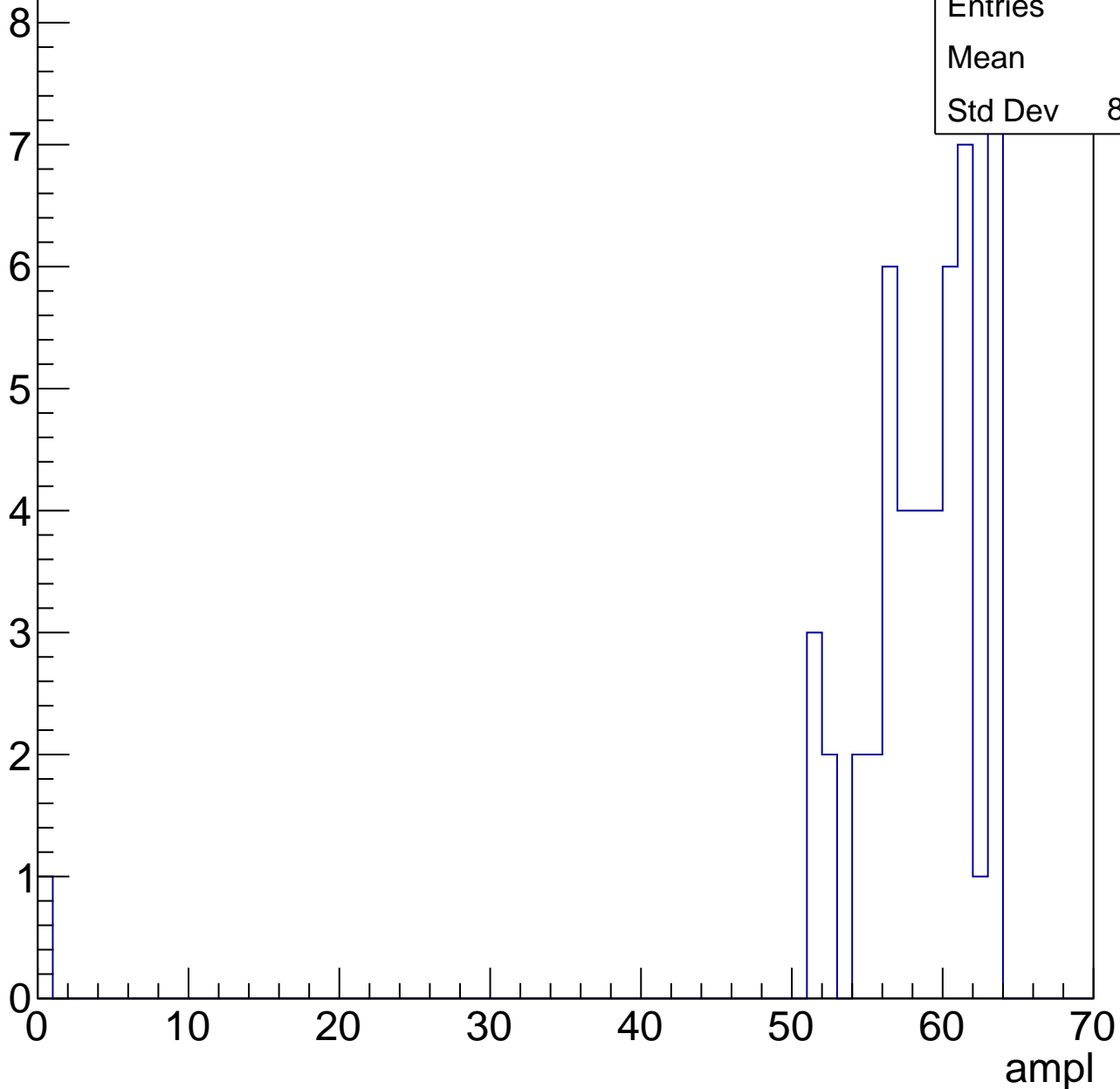


B1L103S, U23-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	57.2
Std Dev	8.872

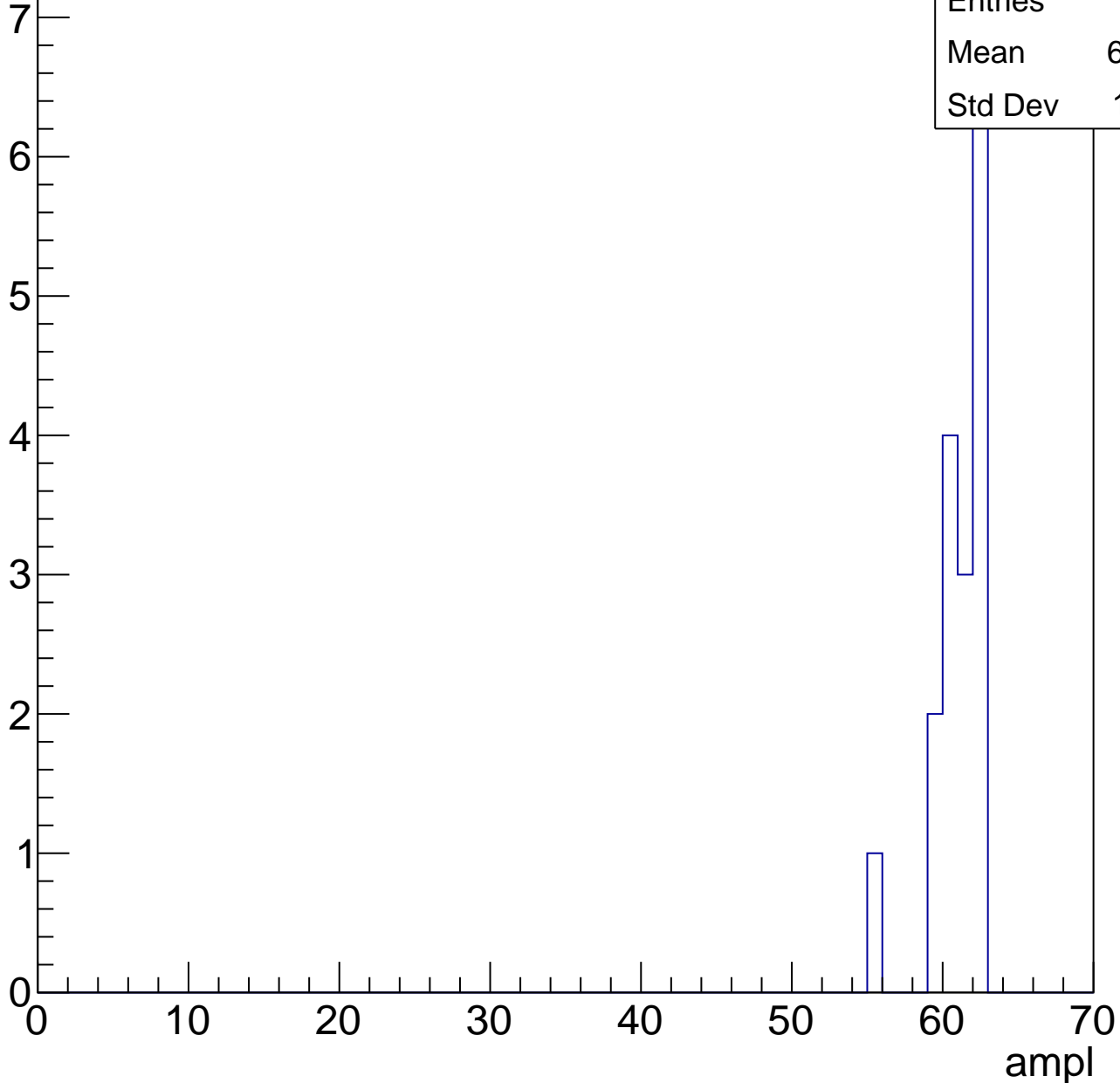


B1L103S, U23-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

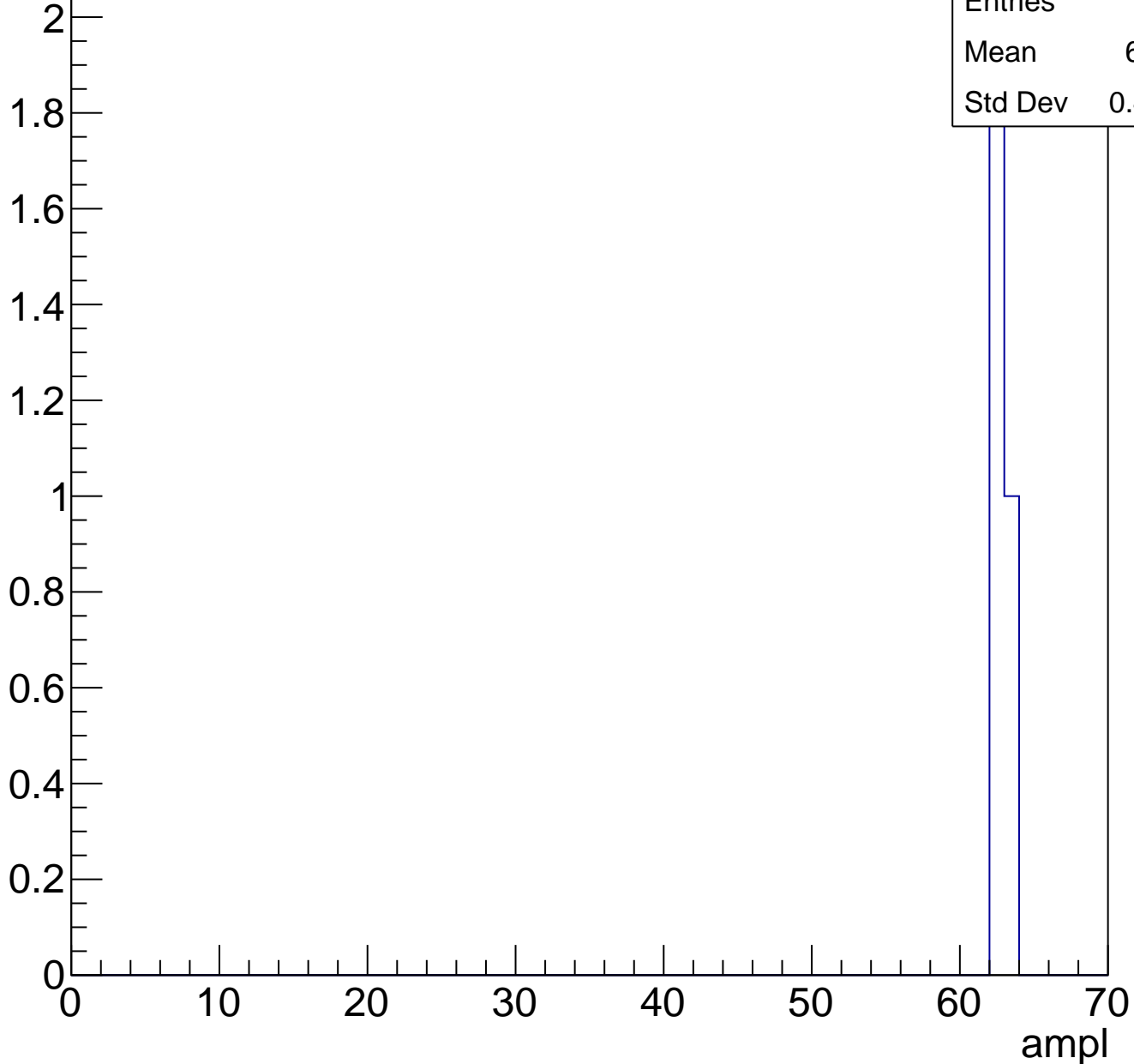
Entries	17
Mean	60.59
Std Dev	1.751



B1L103S, U23-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch99, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	28.86
Std Dev	3.849

Gaus mean : 29.7661

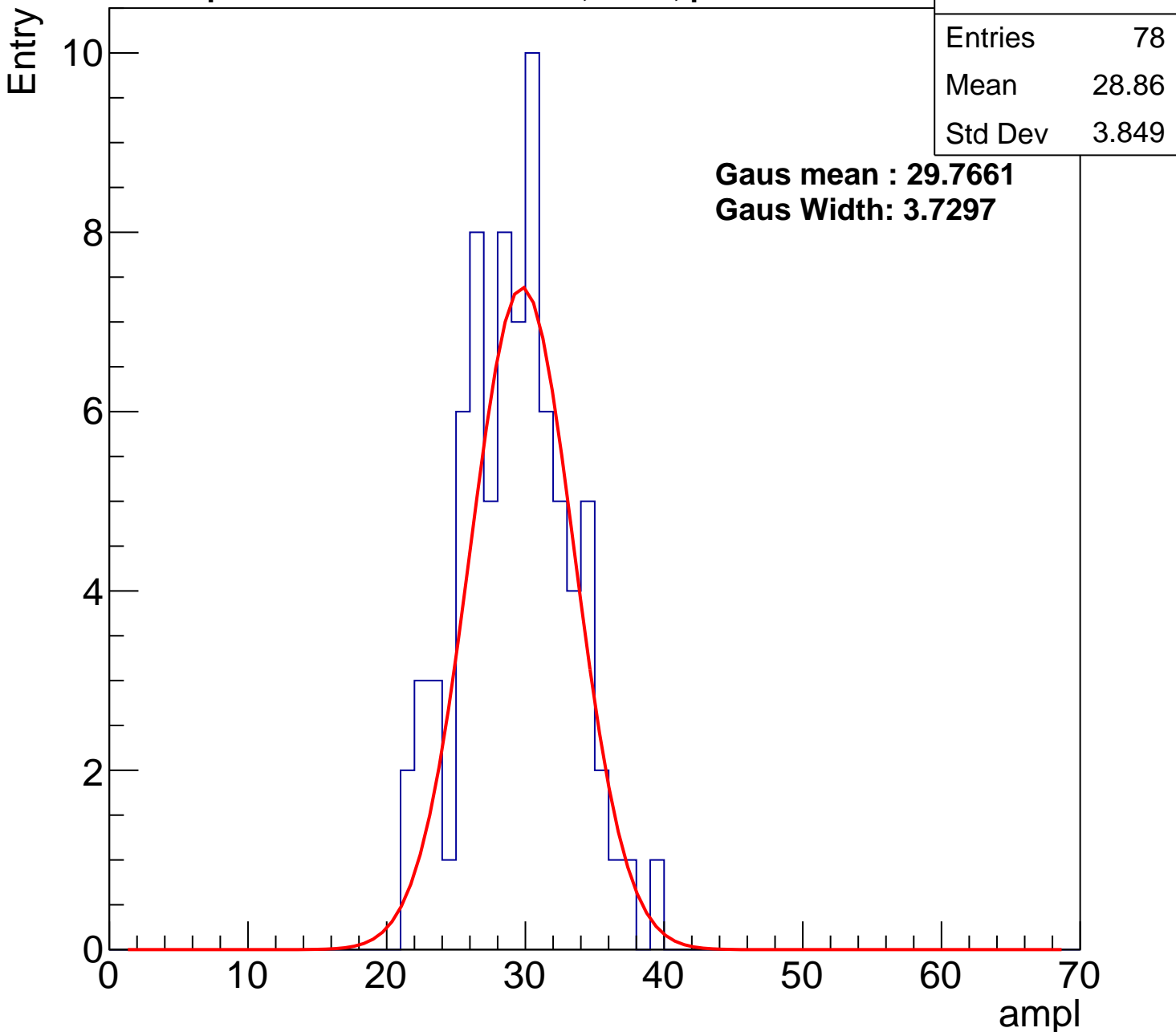
Gaus Width: 3.7297

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch99, adc1

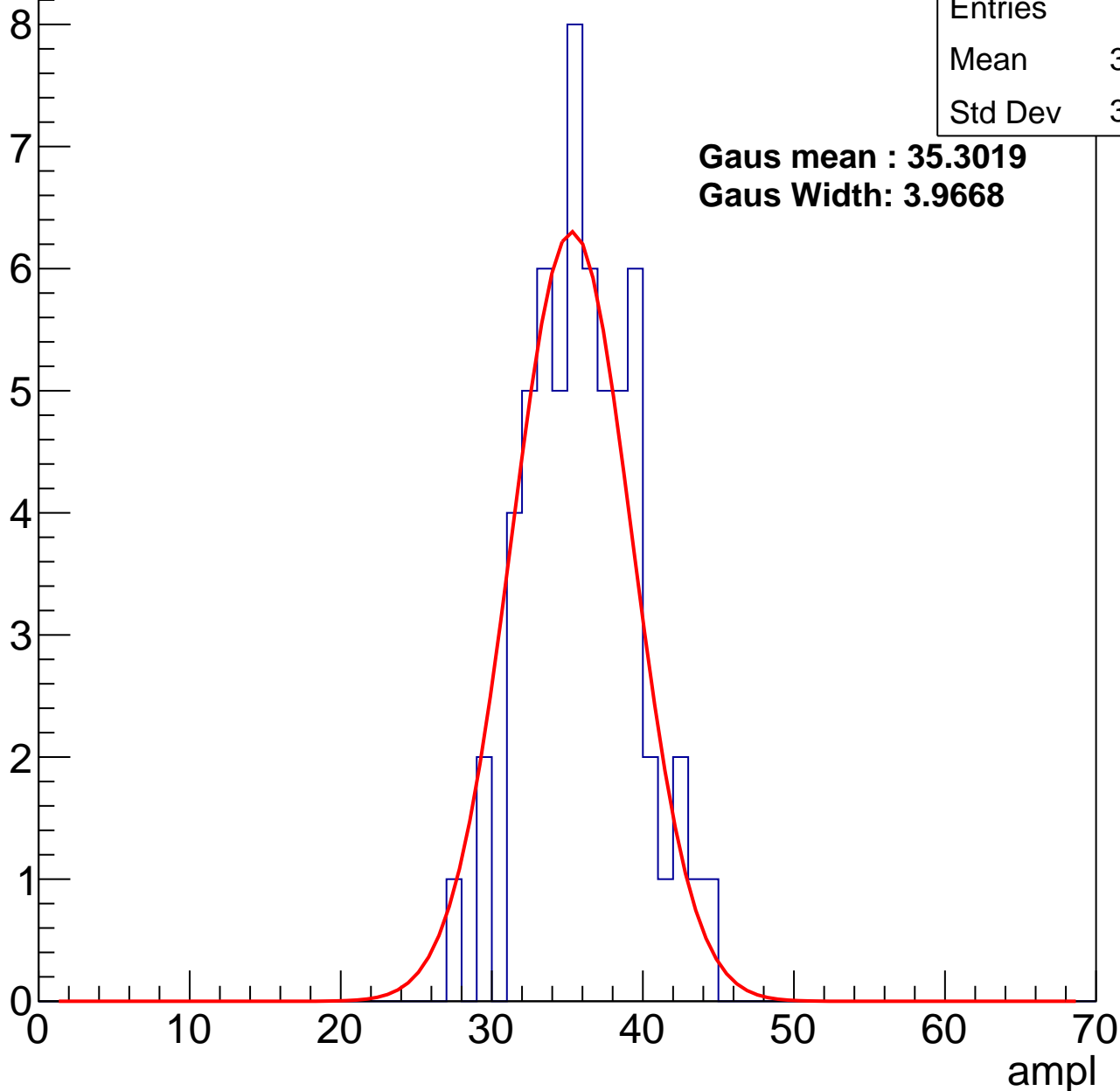
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.57
Std Dev	3.537

Gaus mean : 35.3019

Gaus Width: 3.9668



B1L103S, U23-ch99, adc2

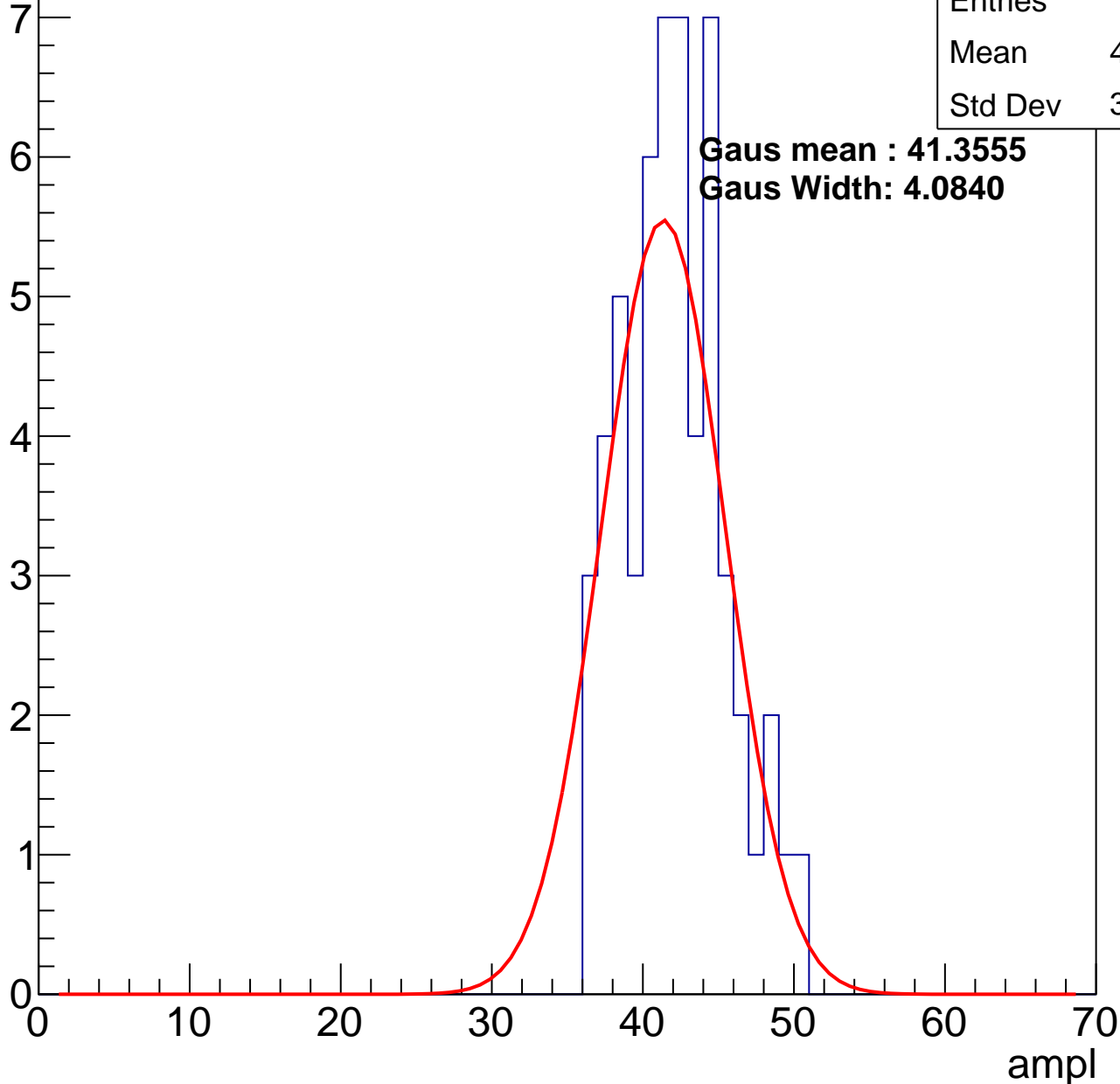
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	41.66
Std Dev	3.382

Gaus mean : 41.3555

Gaus Width: 4.0840

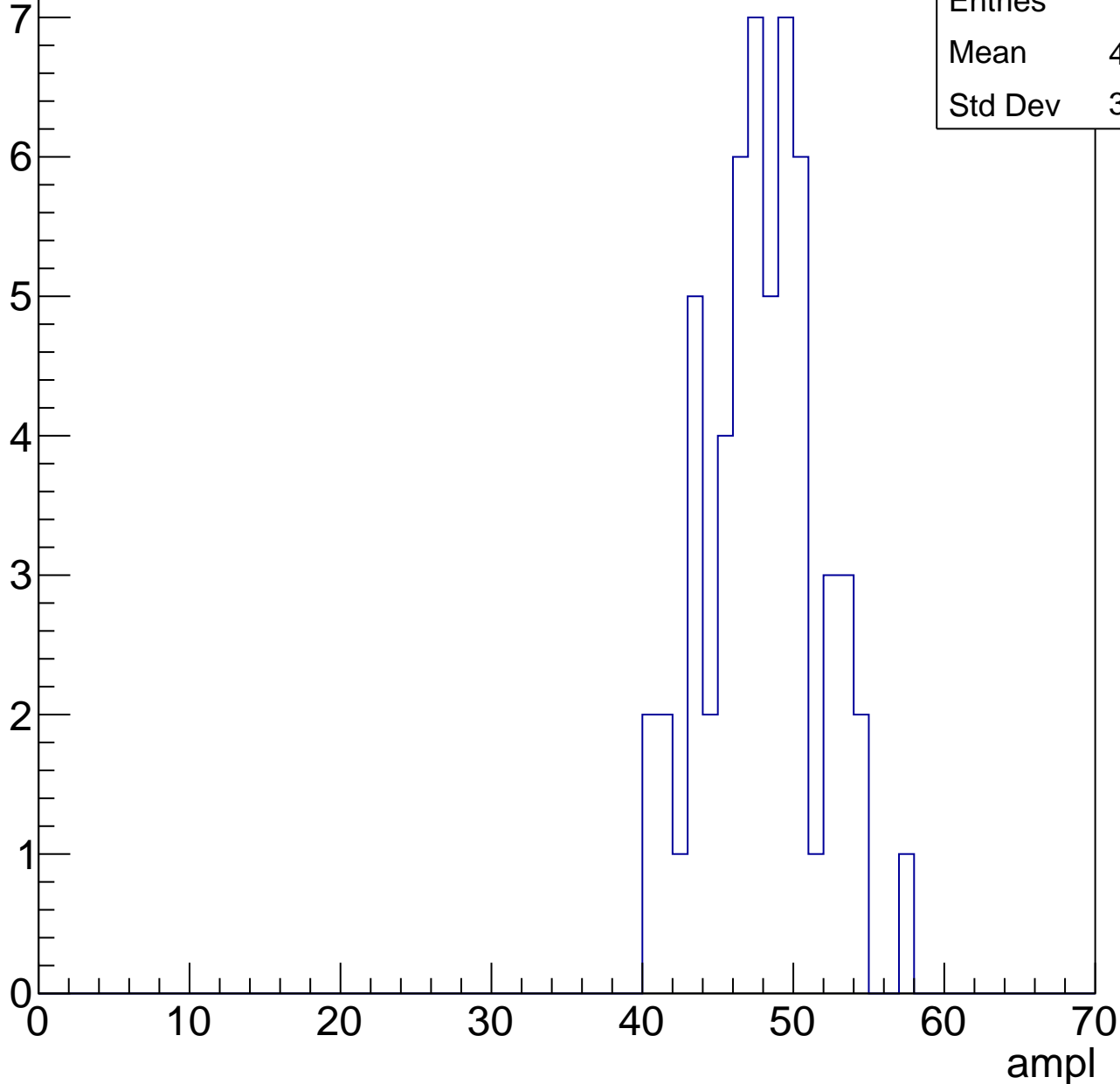


B1L103S, U23-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	47.47
Std Dev	3.714

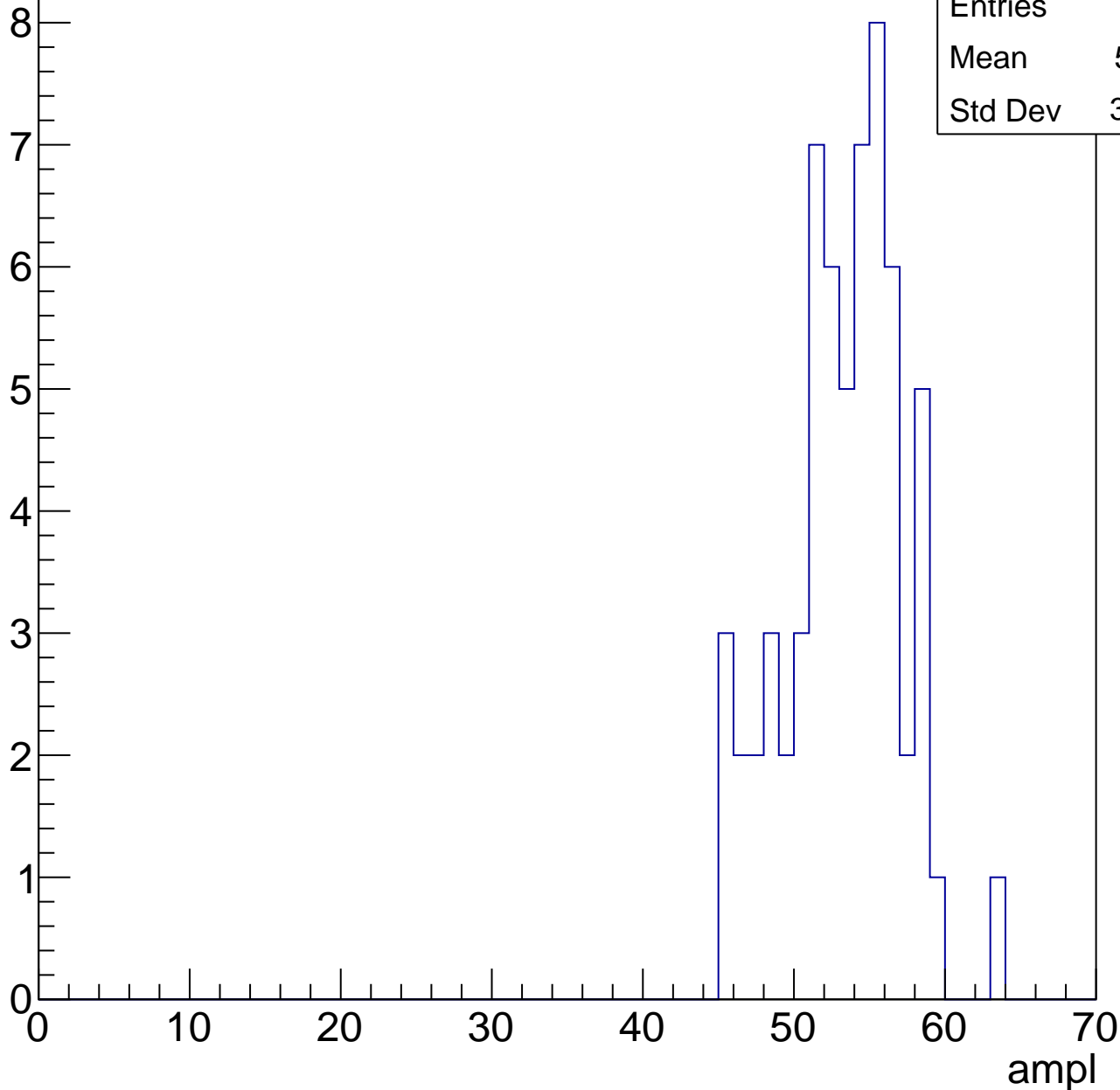


B1L103S, U23-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	52.81
Std Dev	3.808

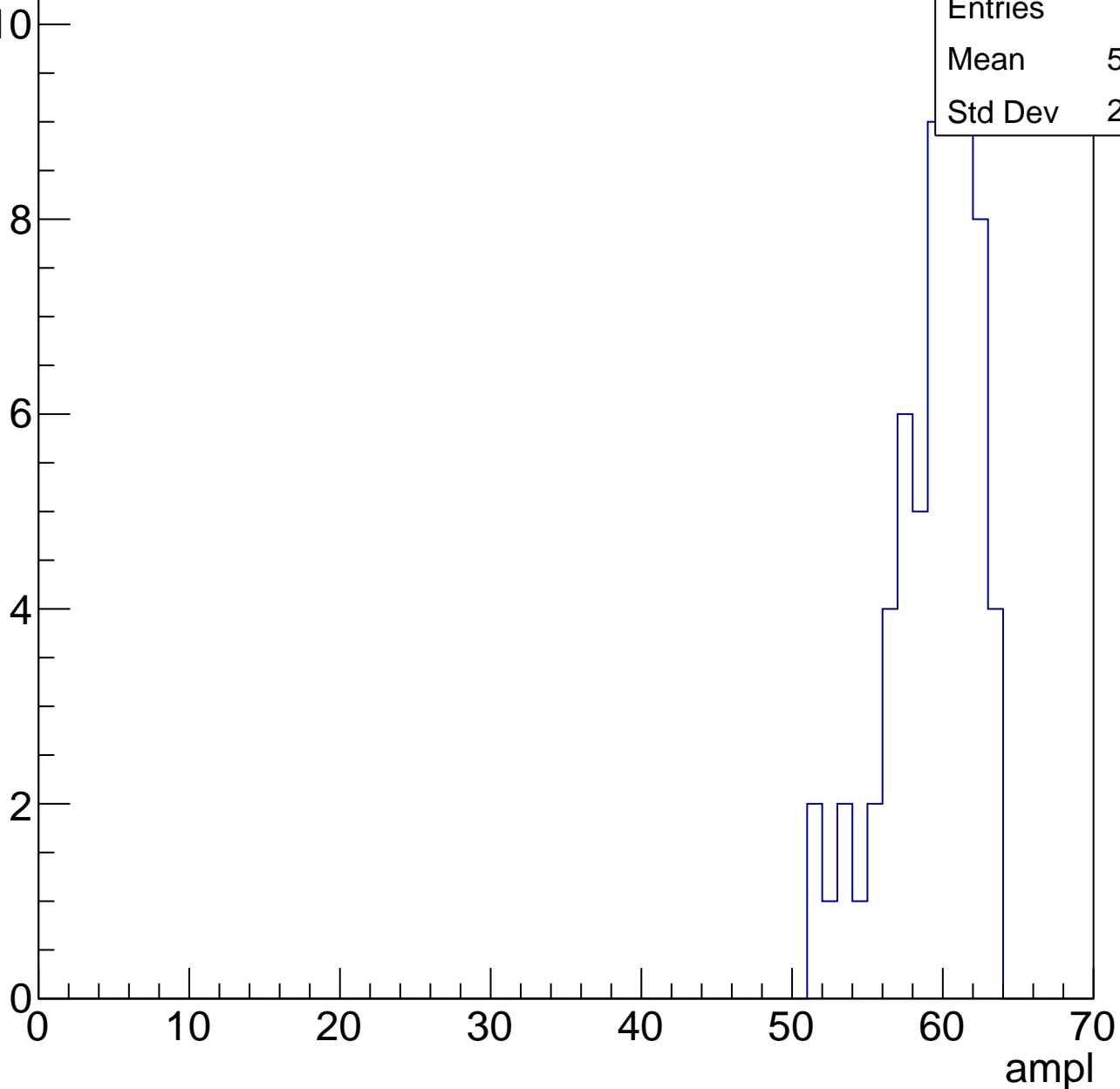


B1L103S, U23-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

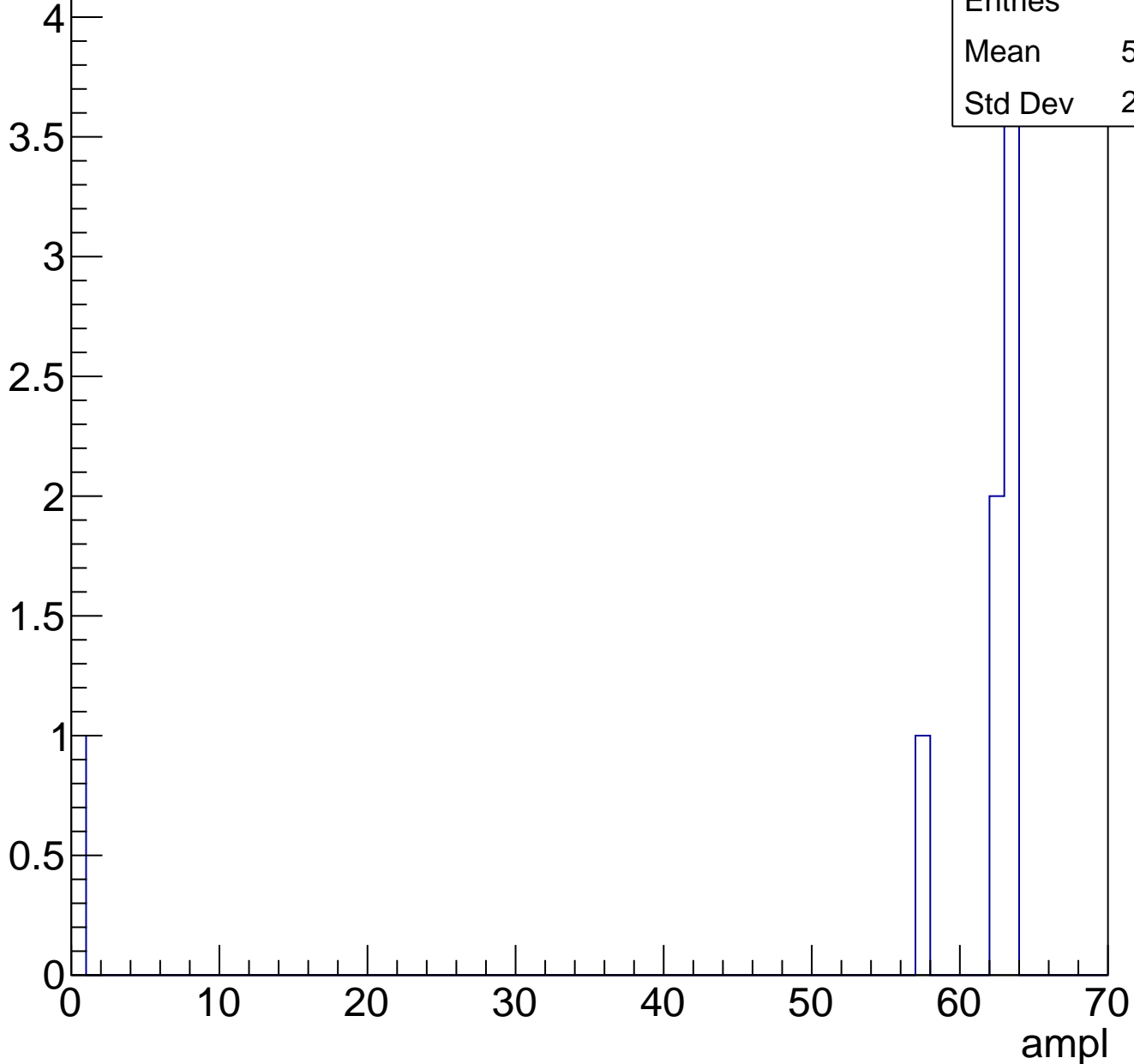
Entries	63
Mean	58.86
Std Dev	2.959



B1L103S, U23-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch100, adc0

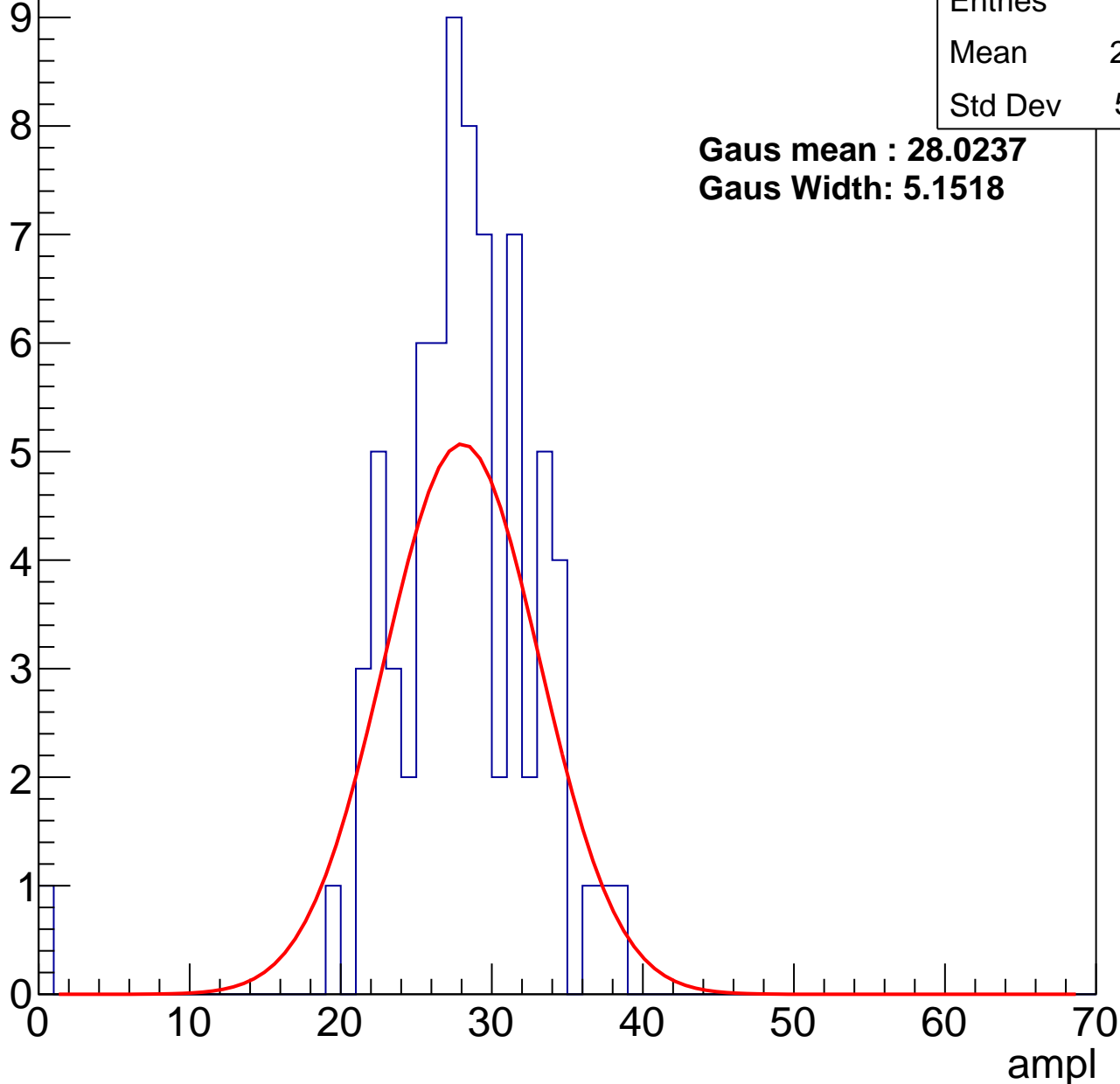
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.54
Std Dev	5.191

Gaus mean : 28.0237

Gaus Width: 5.1518



B1L103S, U23-ch100, adc1

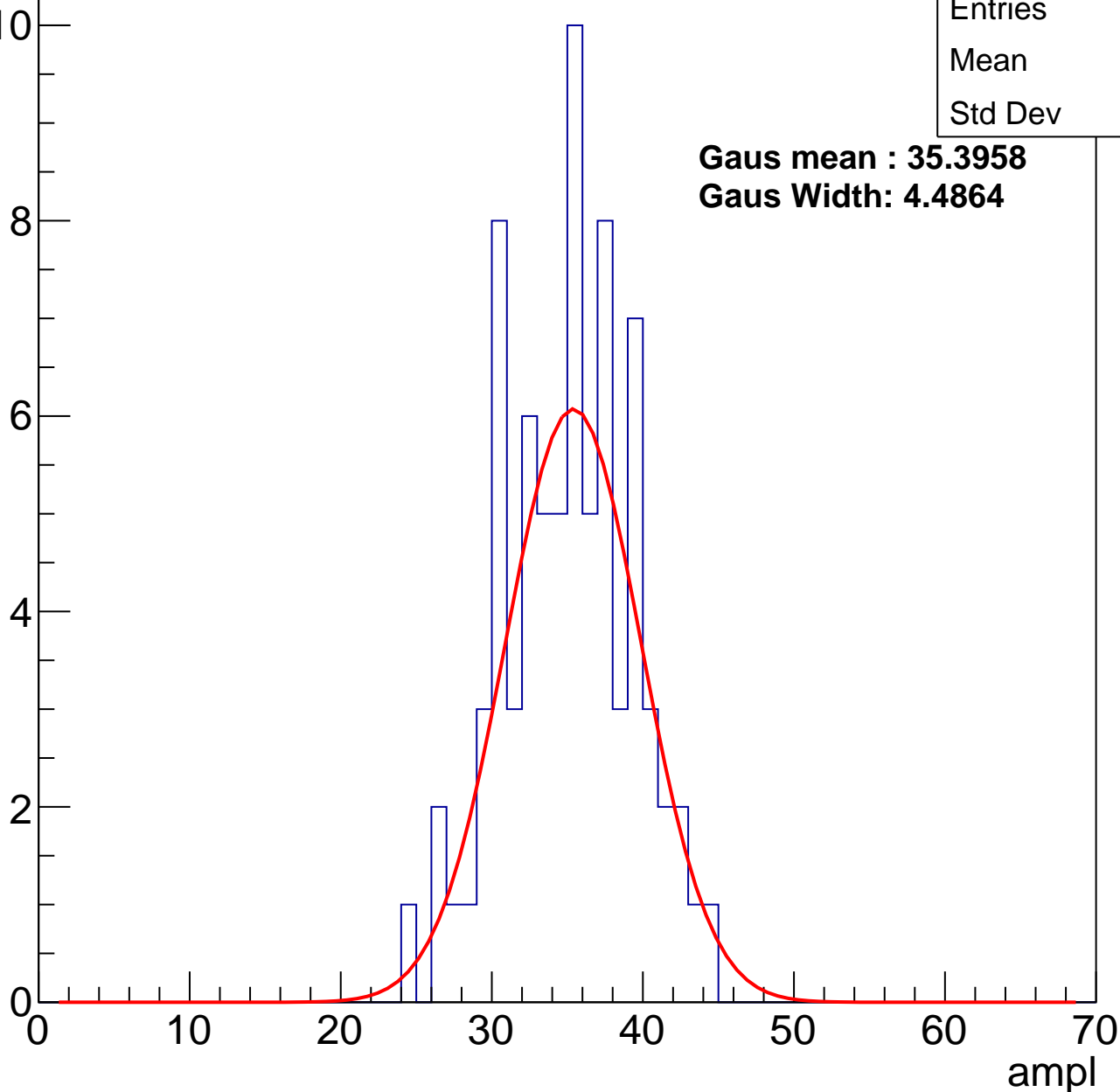
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	34.6
Std Dev	4.24

Gaus mean : 35.3958

Gaus Width: 4.4864



B1L103S, U23-ch100, adc2

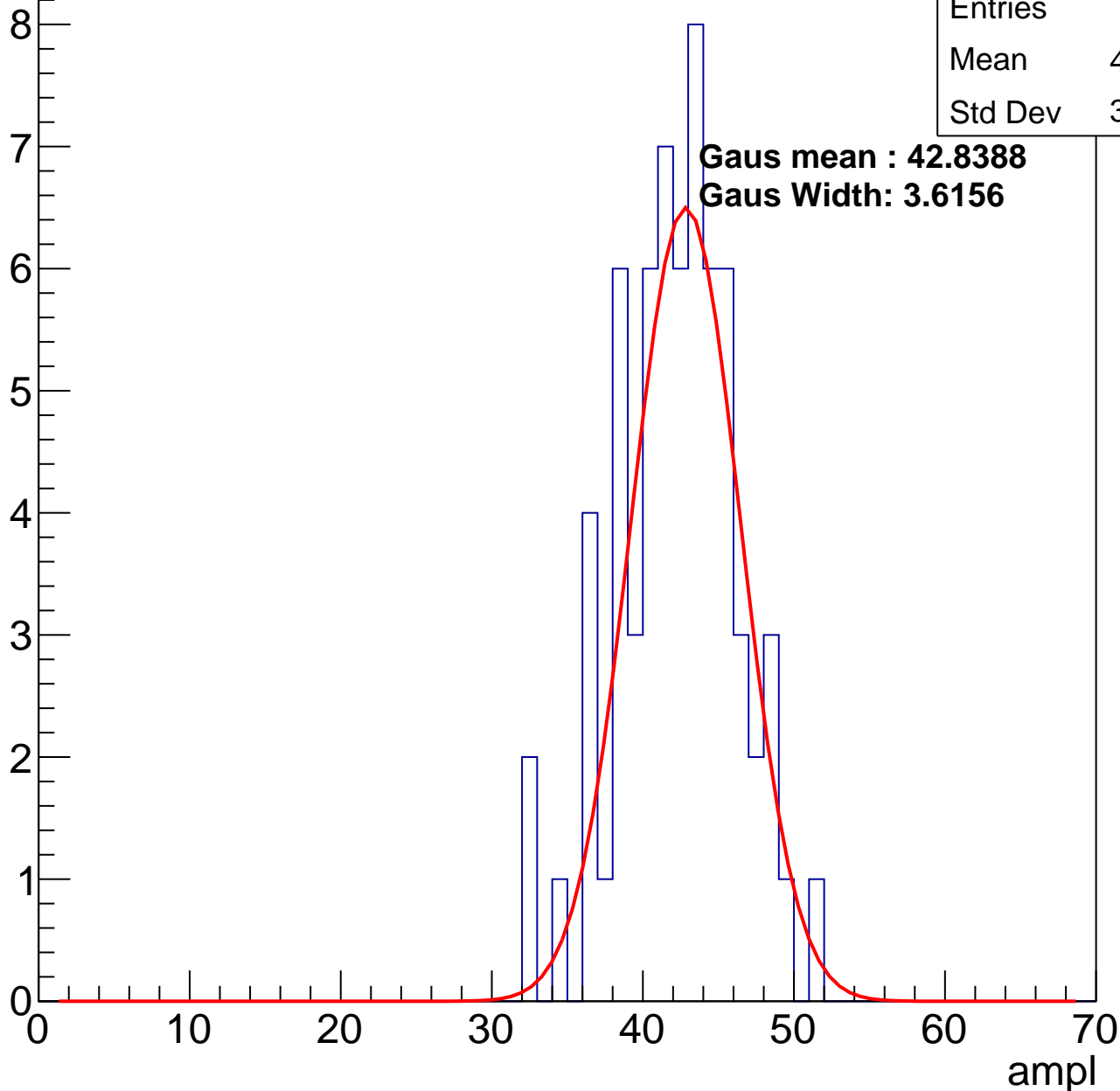
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	41.77
Std Dev	3.903

Gaus mean : 42.8388

Gaus Width: 3.6156

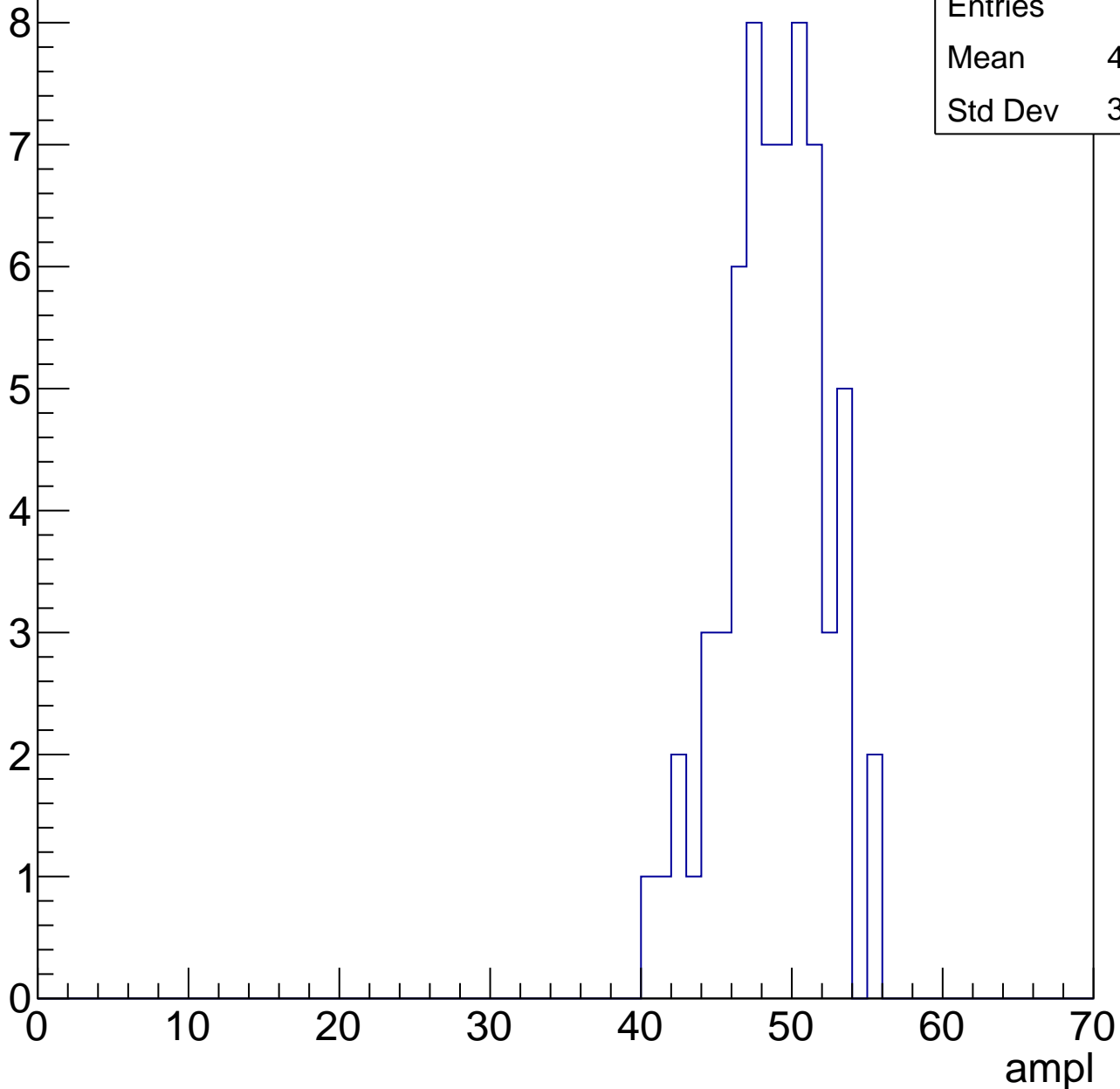


B1L103S, U23-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	48.34
Std Dev	3.265

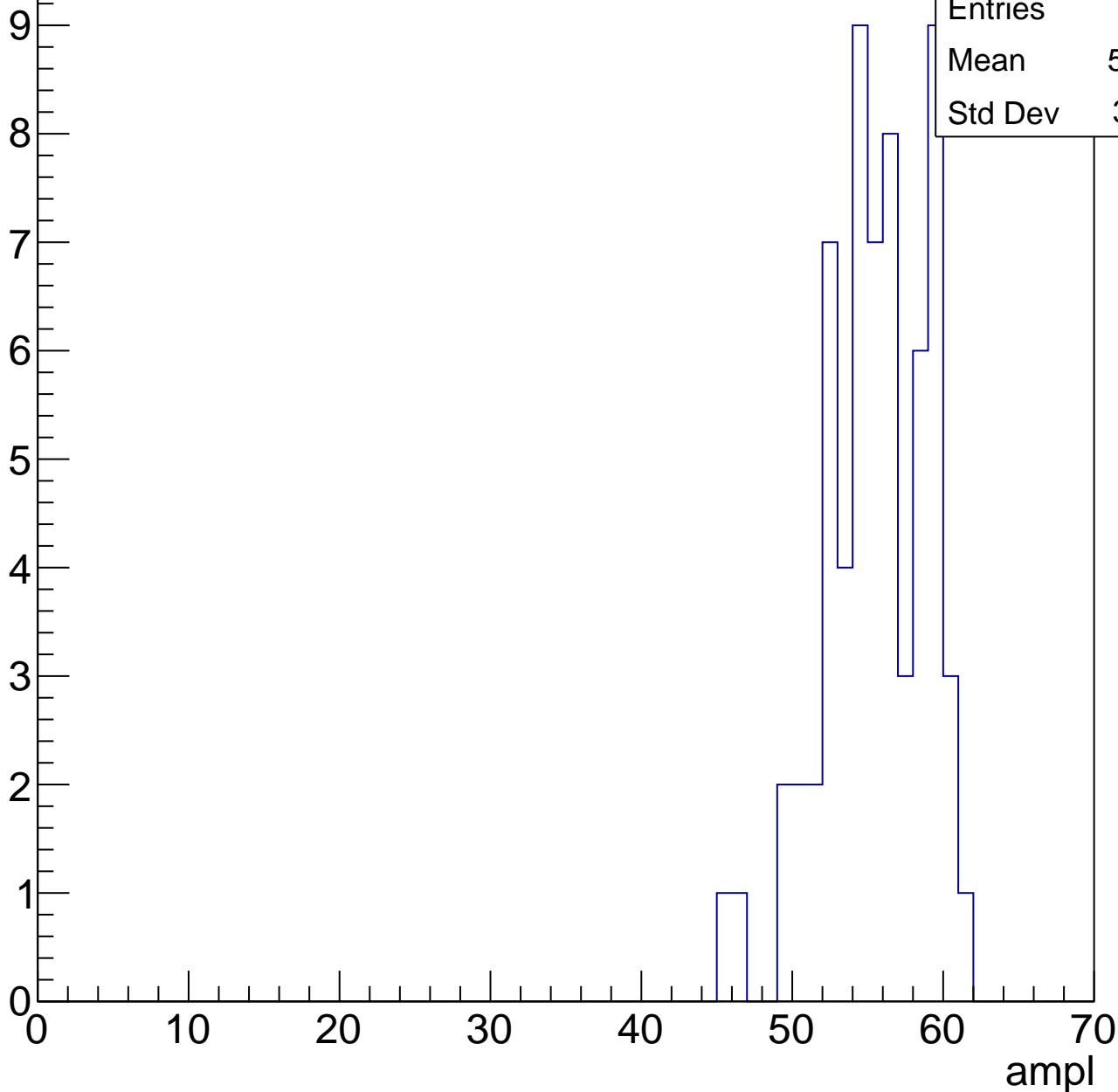


B1L103S, U23-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.03
Std Dev	3.401



B1L103S, U23-ch100, adc5

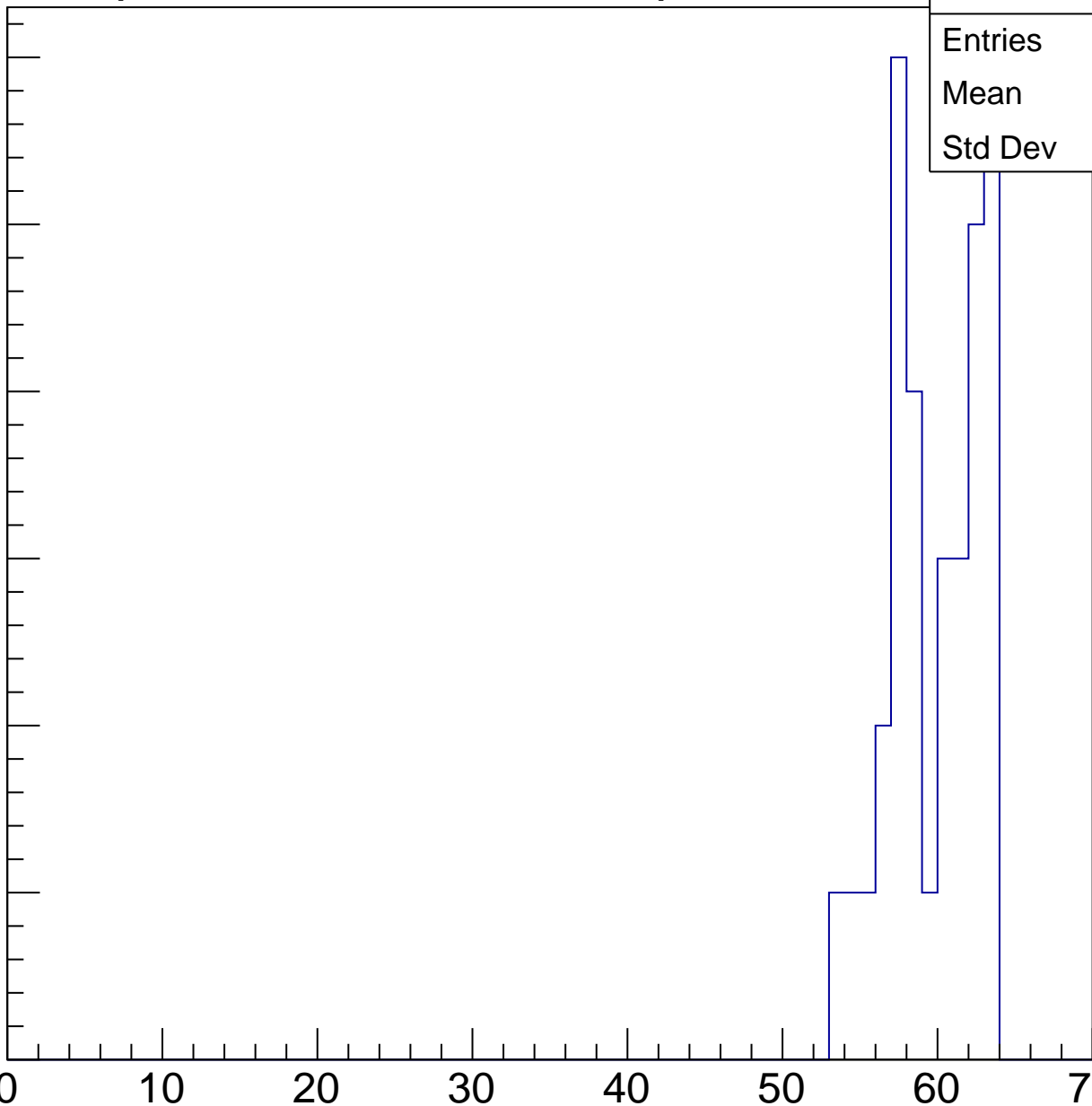
calib_packv5_042523_0143.root, FC#7, port C2

Entry

6
5
4
3
2
1
0

Entries	33
Mean	59.33
Std Dev	2.889

ampl

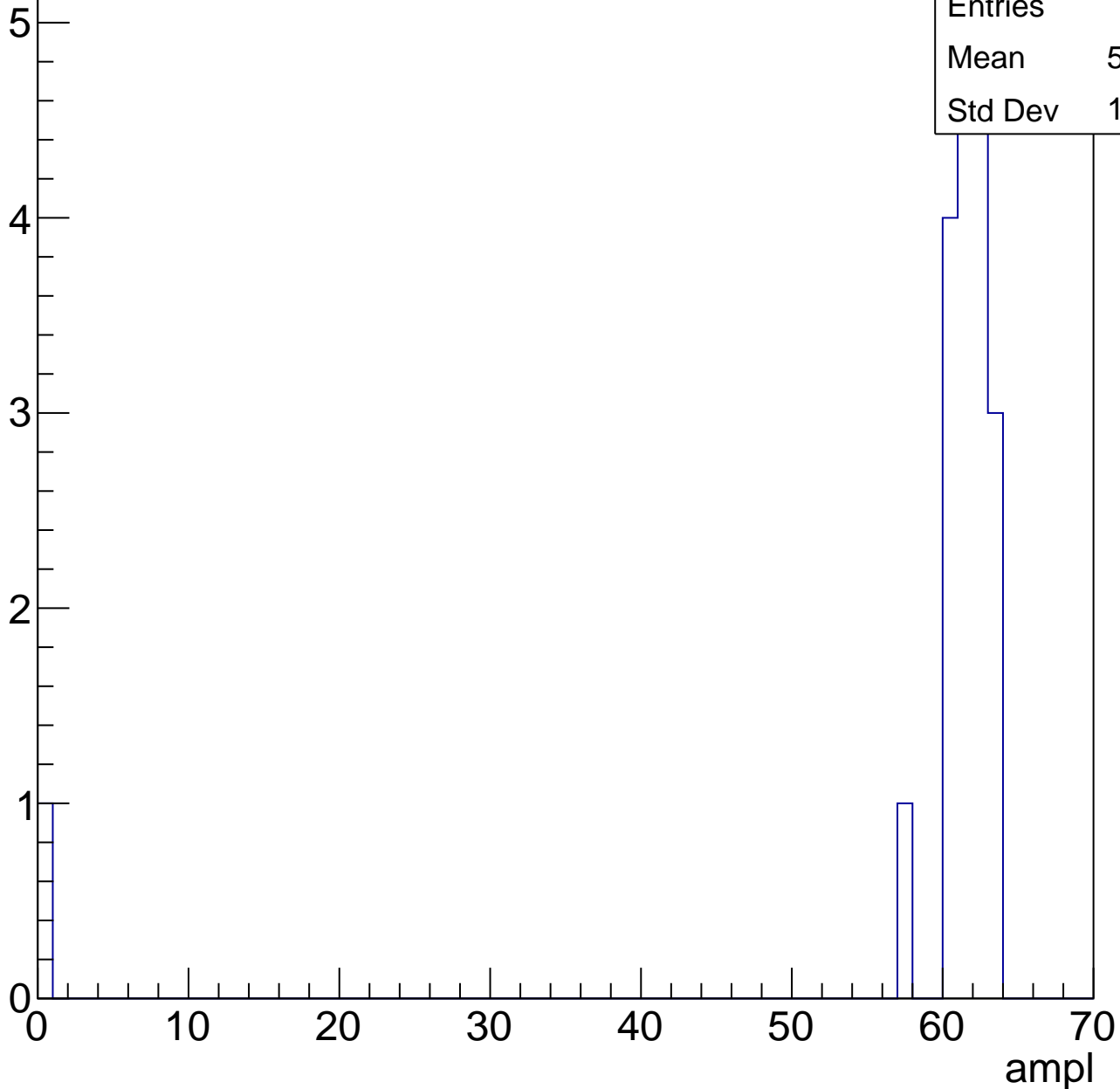


B1L103S, U23-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

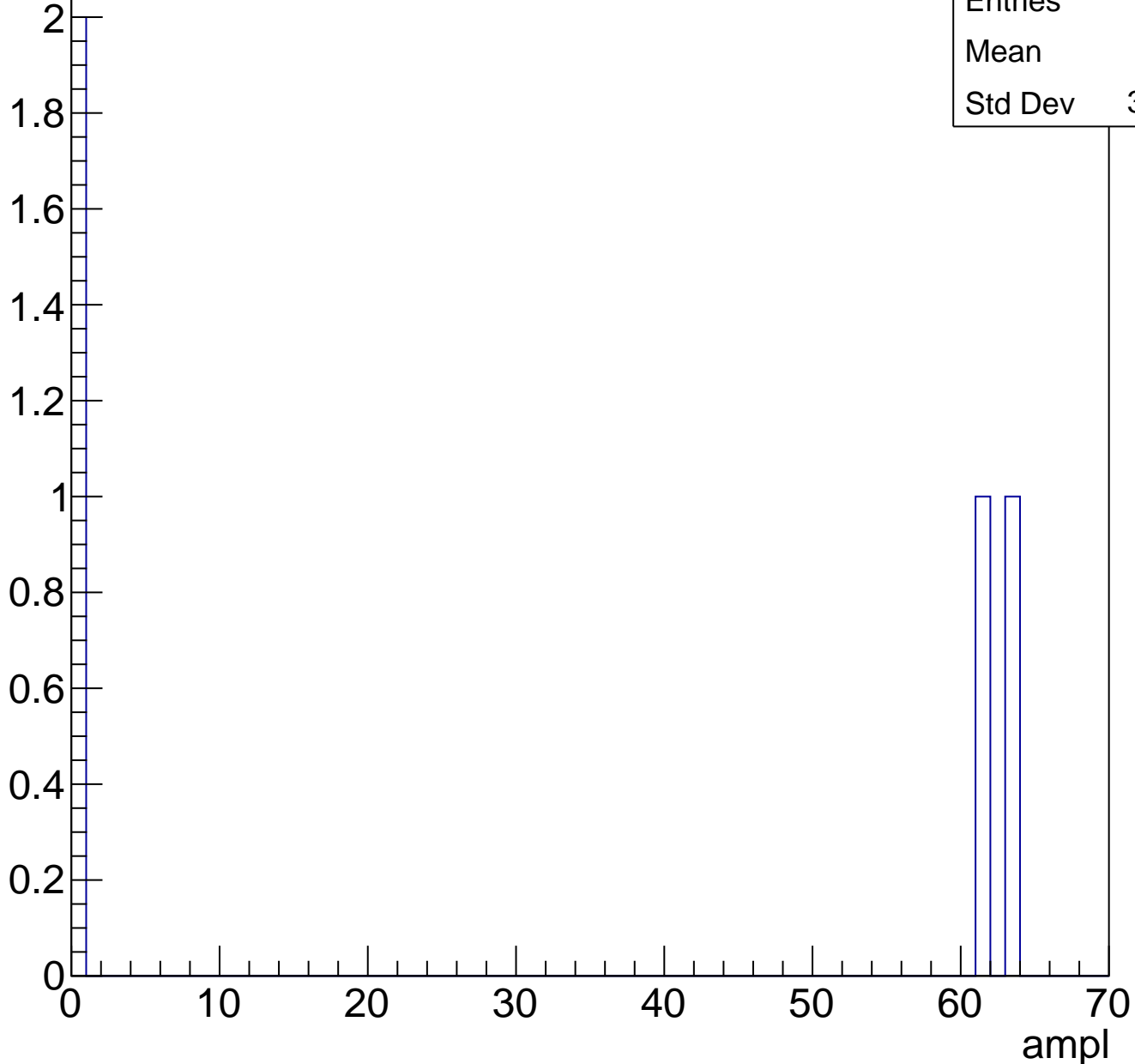
Entries	19
Mean	57.95
Std Dev	13.73



B1L103S, U23-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	31
Std Dev	31.01

B1L103S, U23-ch101, adc0

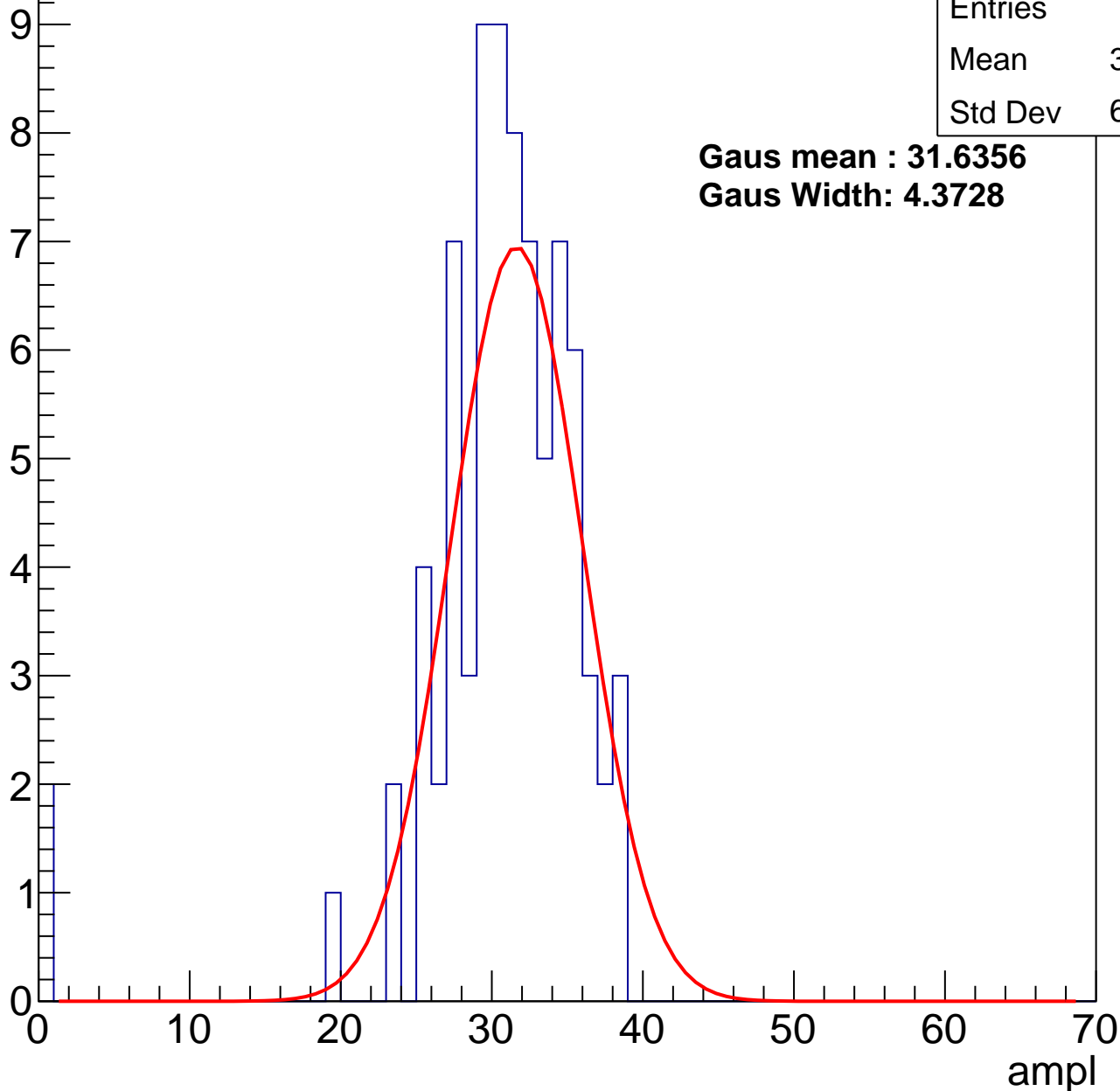
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	30.02
Std Dev	6.099

Gaus mean : 31.6356

Gaus Width: 4.3728



B1L103S, U23-ch101, adc1

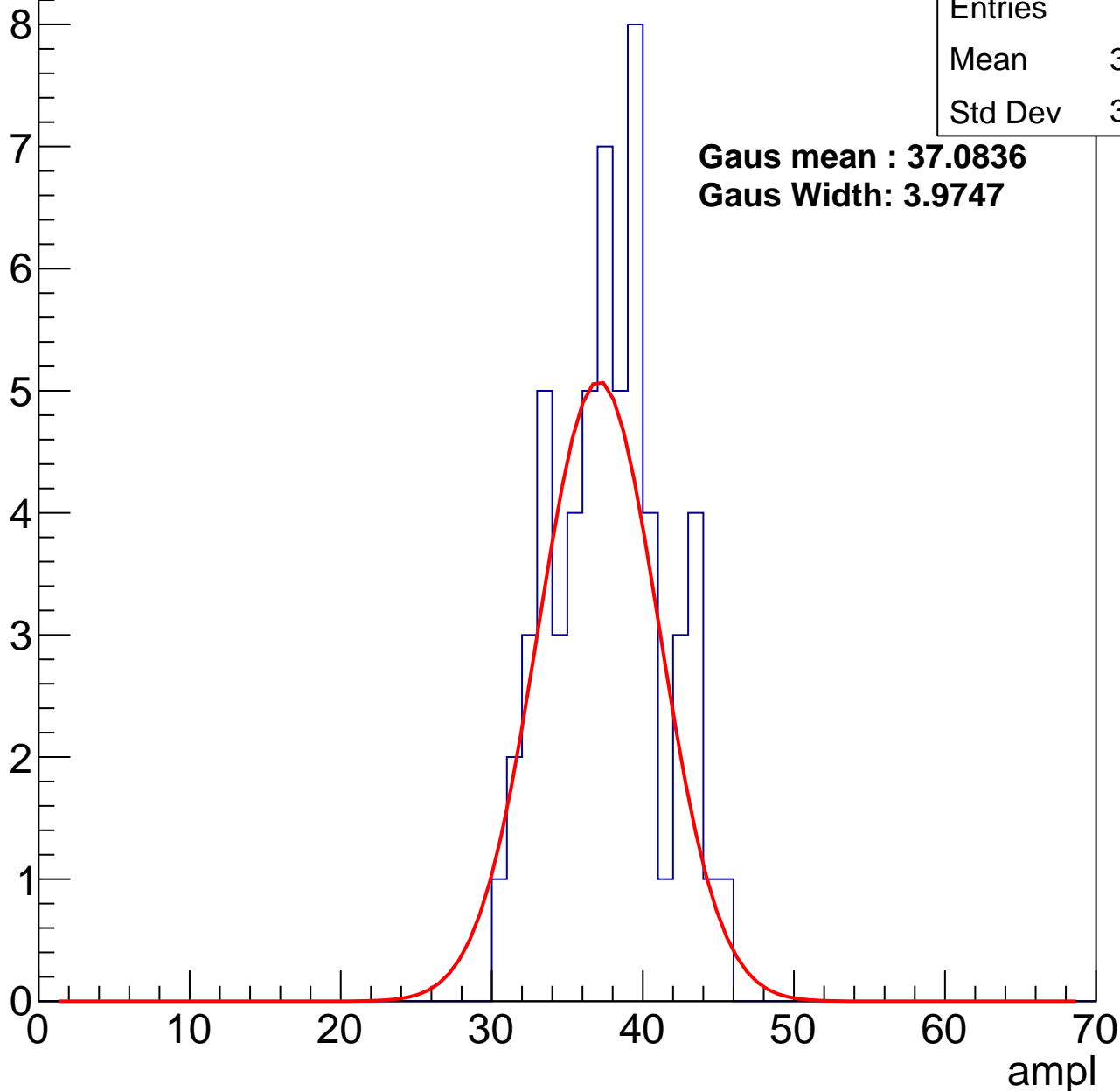
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	37.26
Std Dev	3.606

Gaus mean : 37.0836

Gaus Width: 3.9747



B1L103S, U23-ch101, adc2

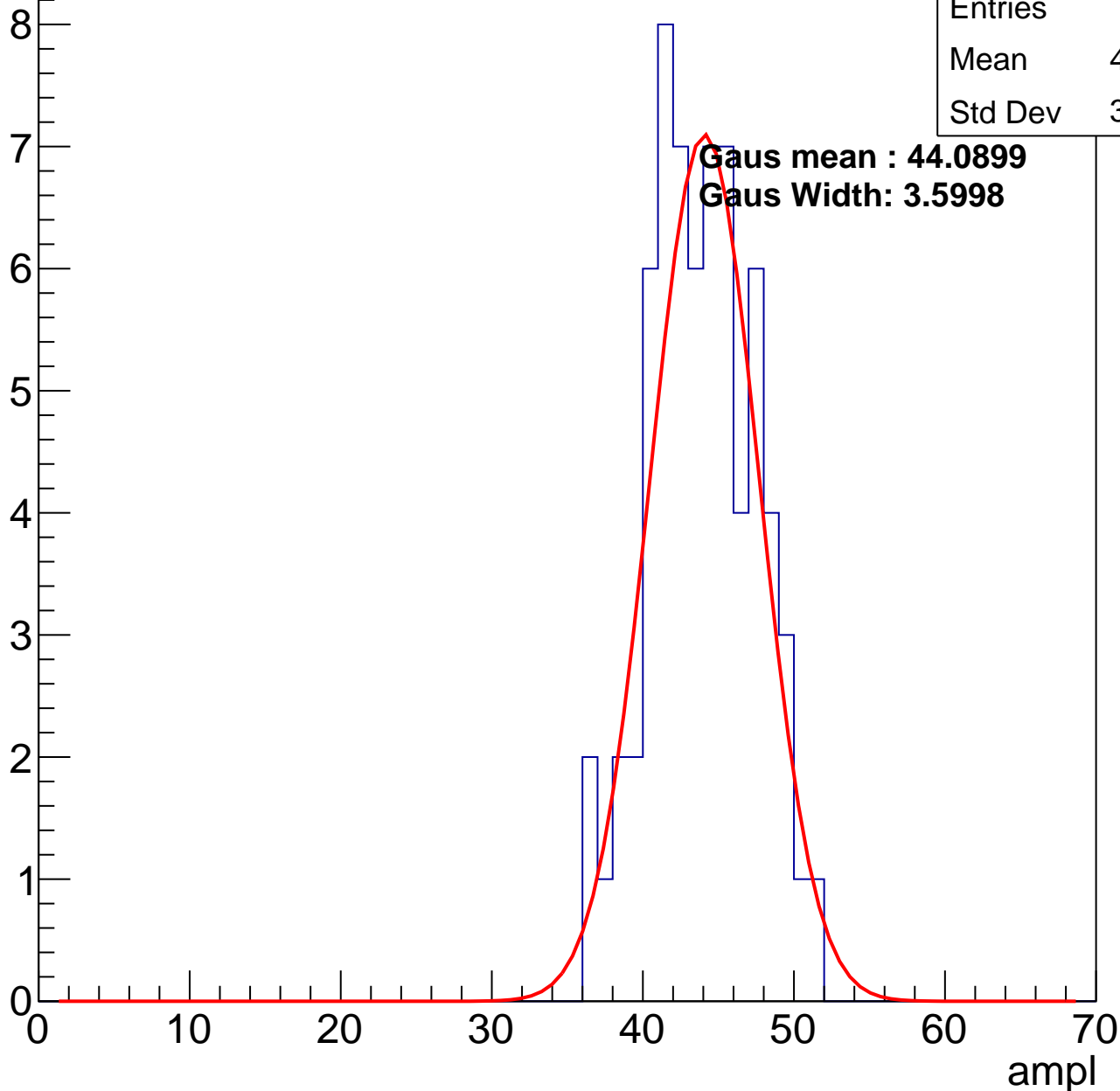
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.46
Std Dev	3.418

Gaus mean : 44.0899

Gaus Width: 3.5998

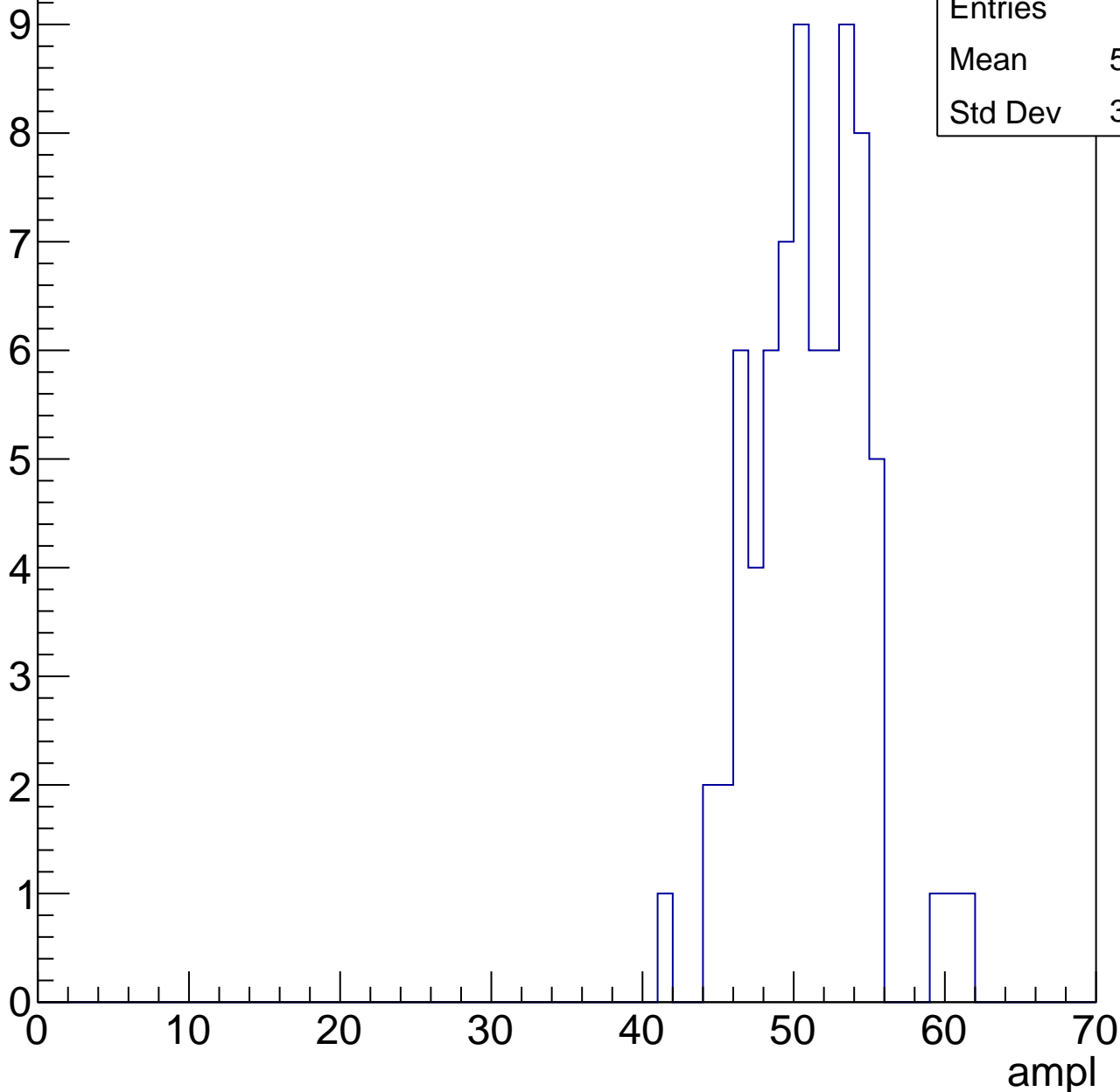


B1L103S, U23-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	50.62
Std Dev	3.682

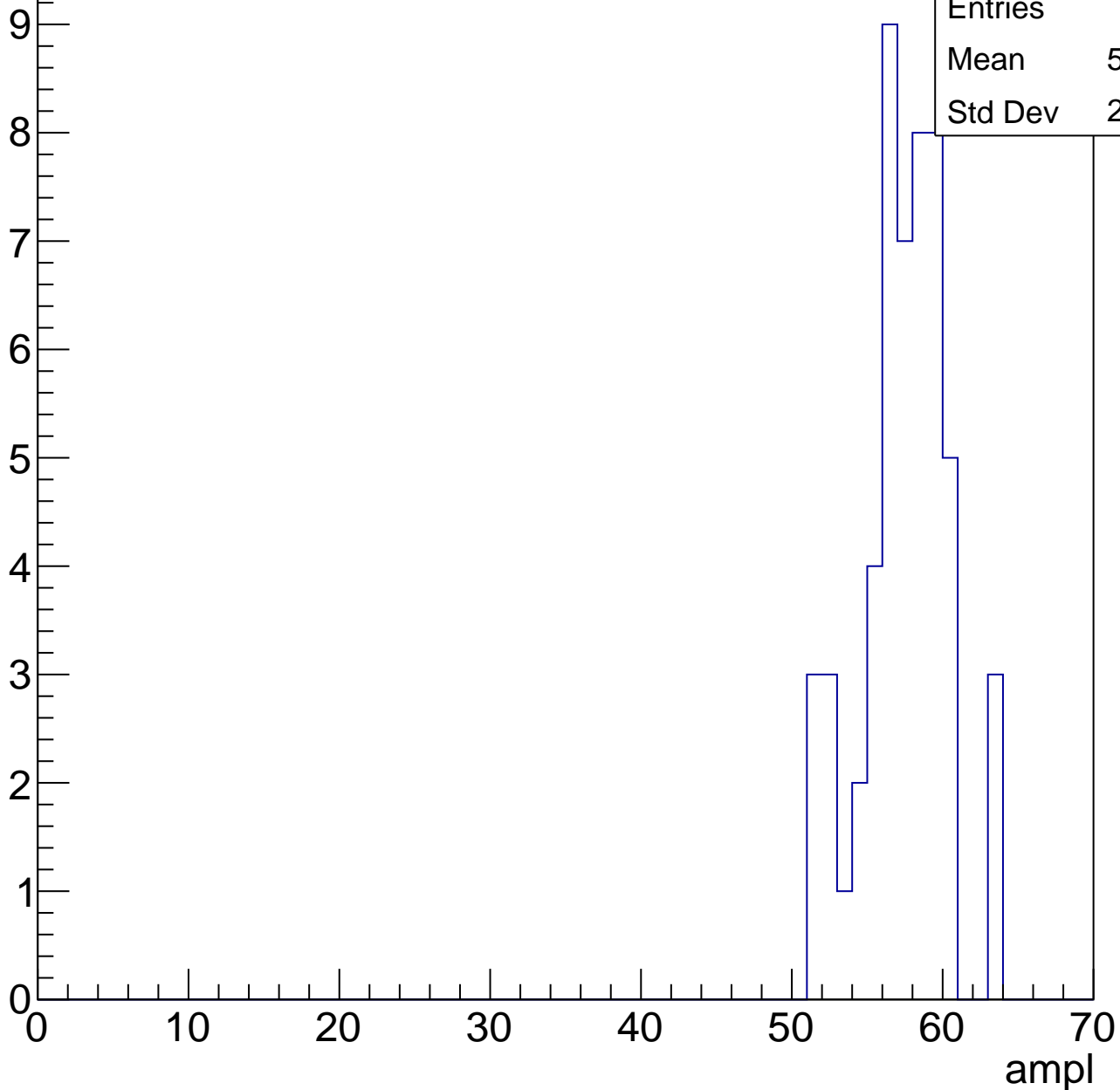


B1L103S, U23-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	56.94
Std Dev	2.864

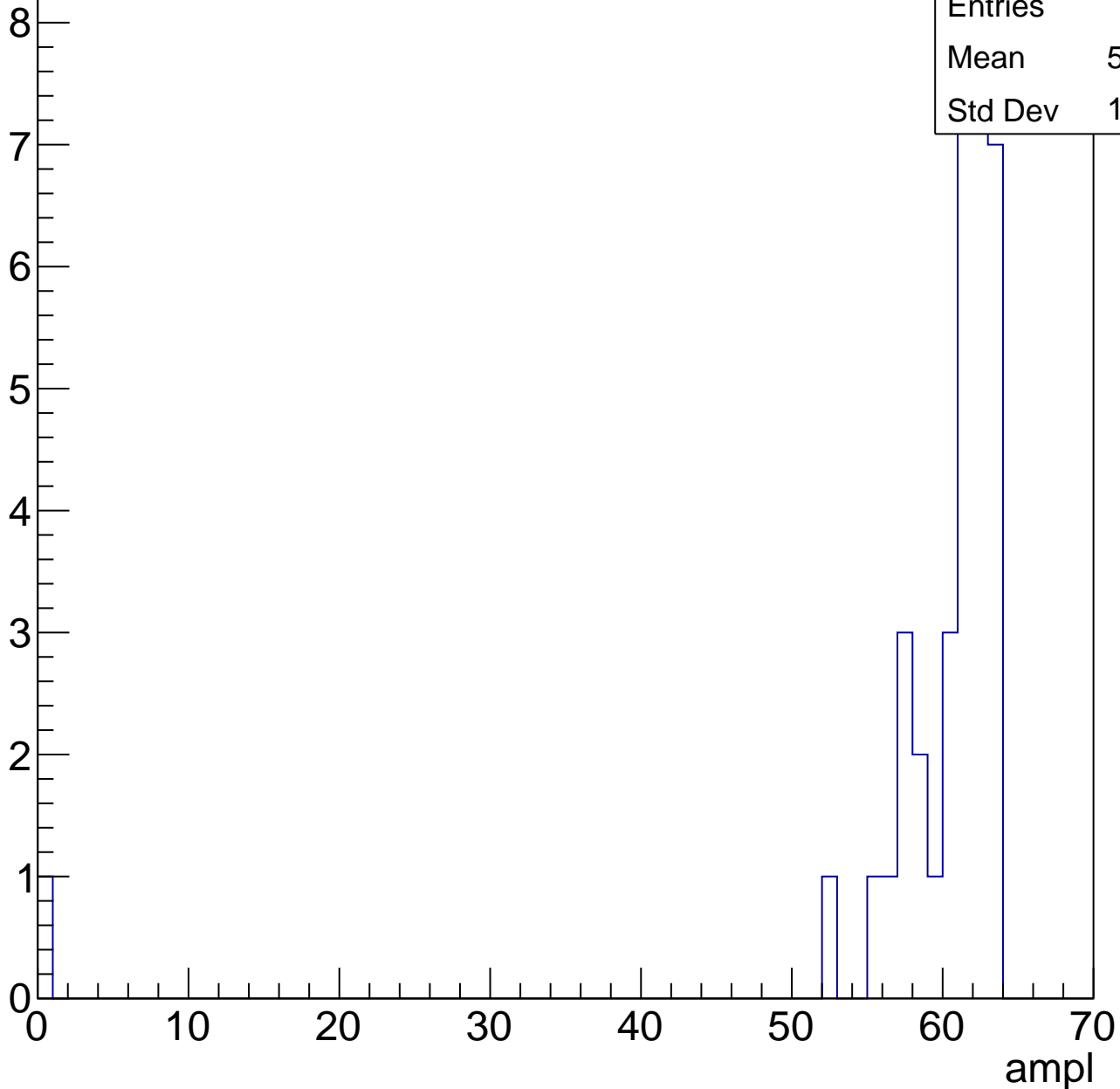


B1L103S, U23-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.72
Std Dev	10.25



B1L103S, U23-ch101, adc6

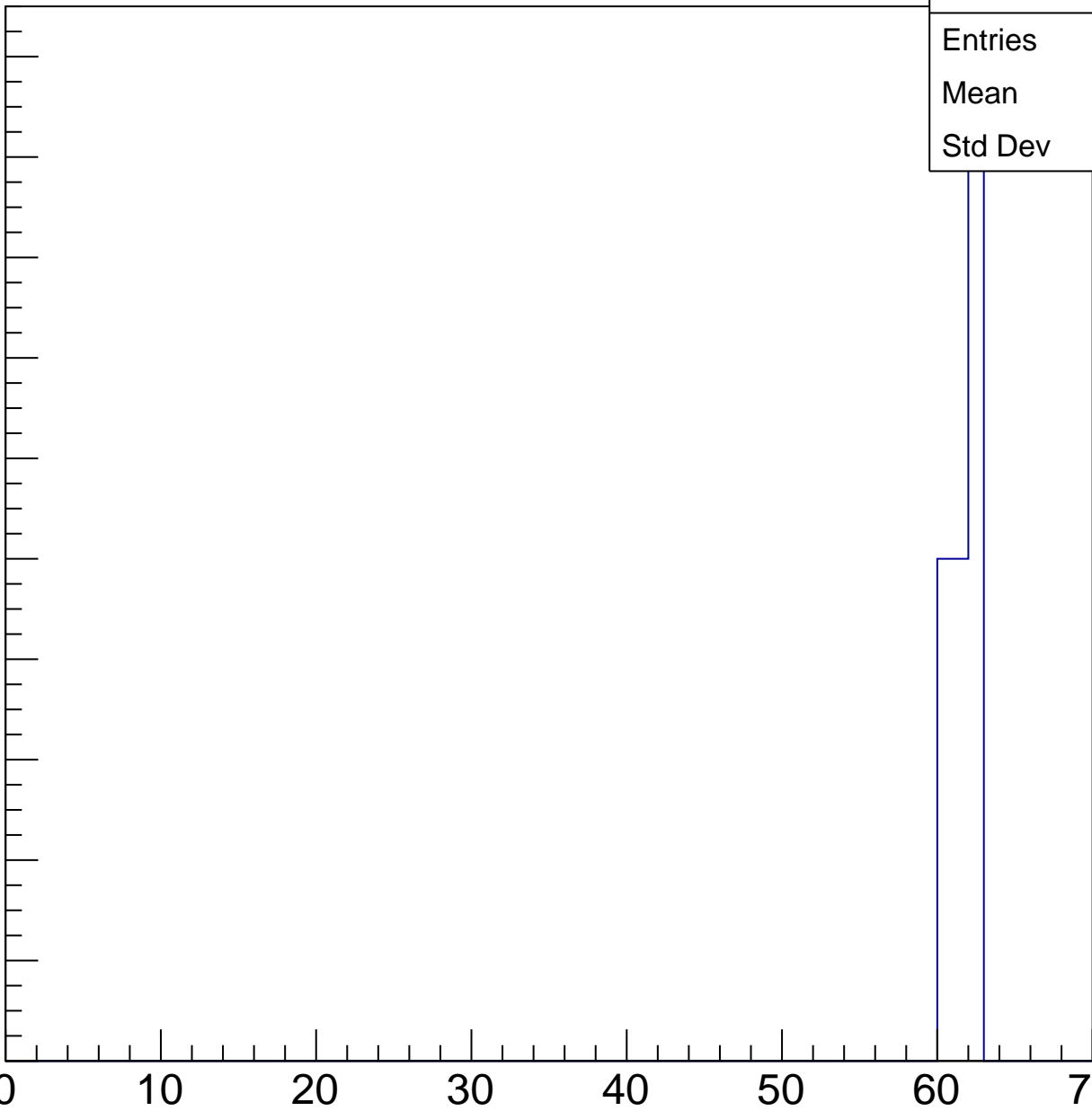
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	0.8292

ampl



B1L103S, U23-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch102, adc0

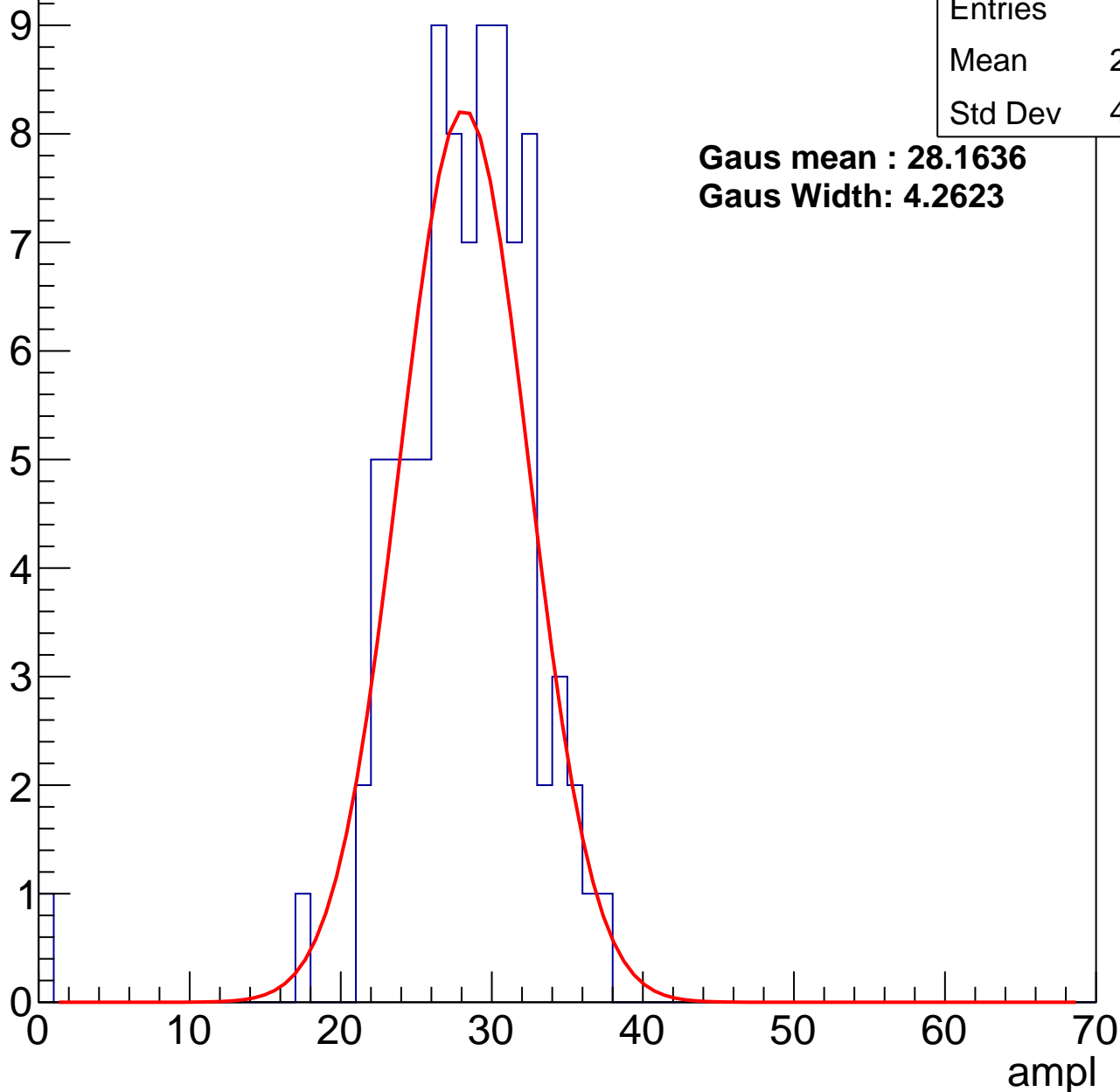
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	27.67
Std Dev	4.835

Gaus mean : 28.1636

Gaus Width: 4.2623



B1L103S, U23-ch102, adc1

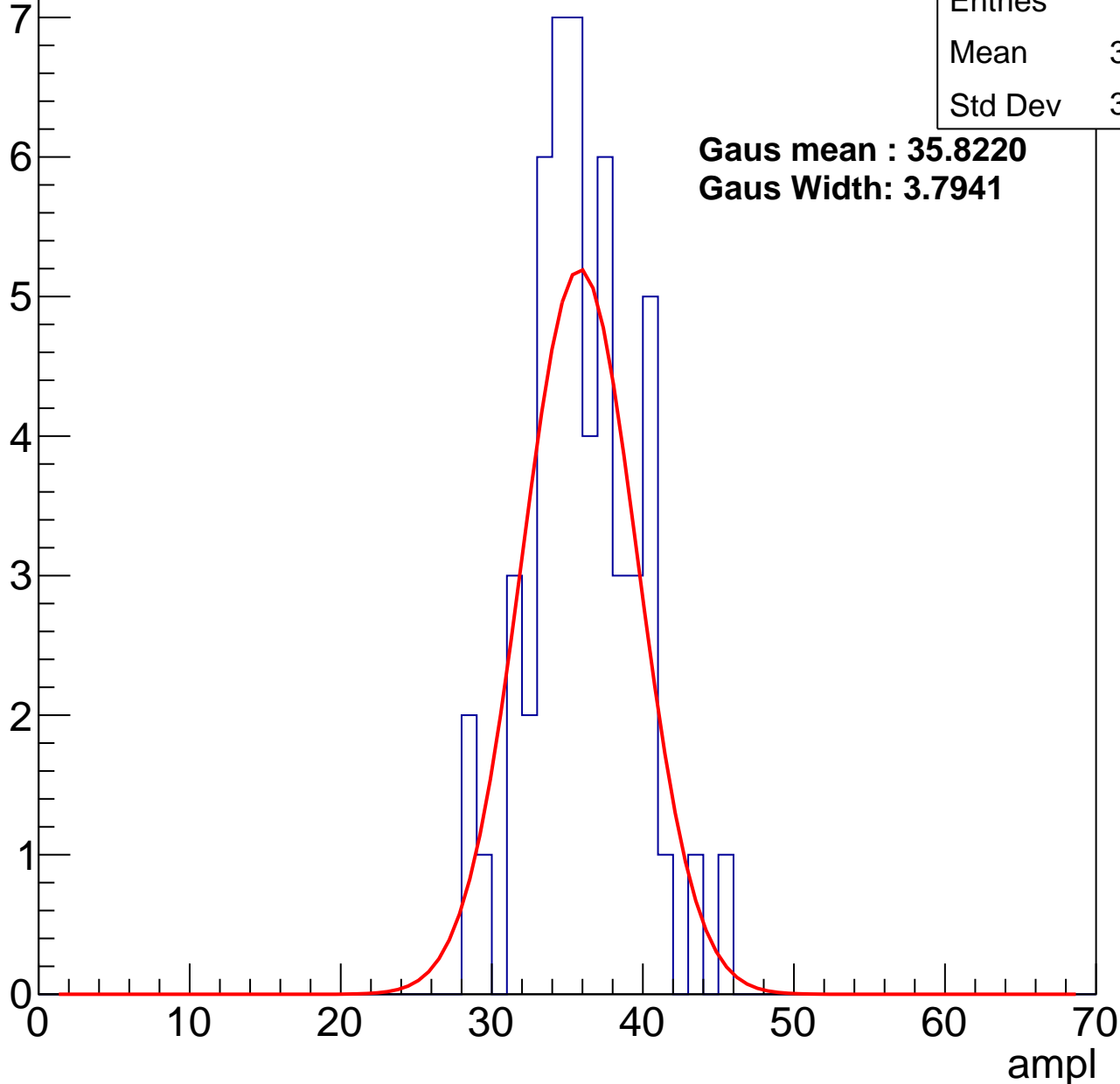
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	35.56
Std Dev	3.516

Gaus mean : 35.8220

Gaus Width: 3.7941

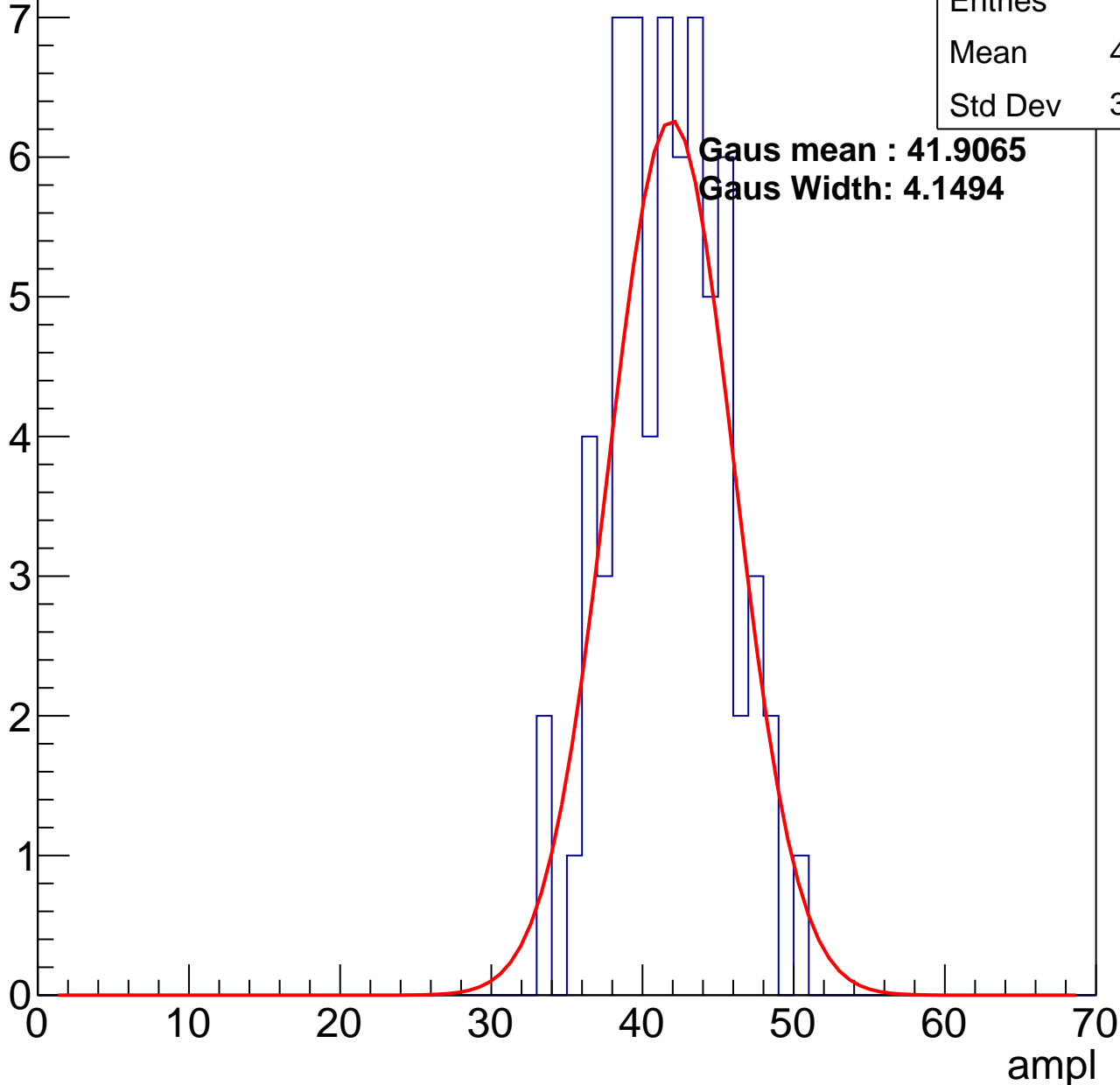


B1L103S, U23-ch102, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	41.25
Std Dev	3.695

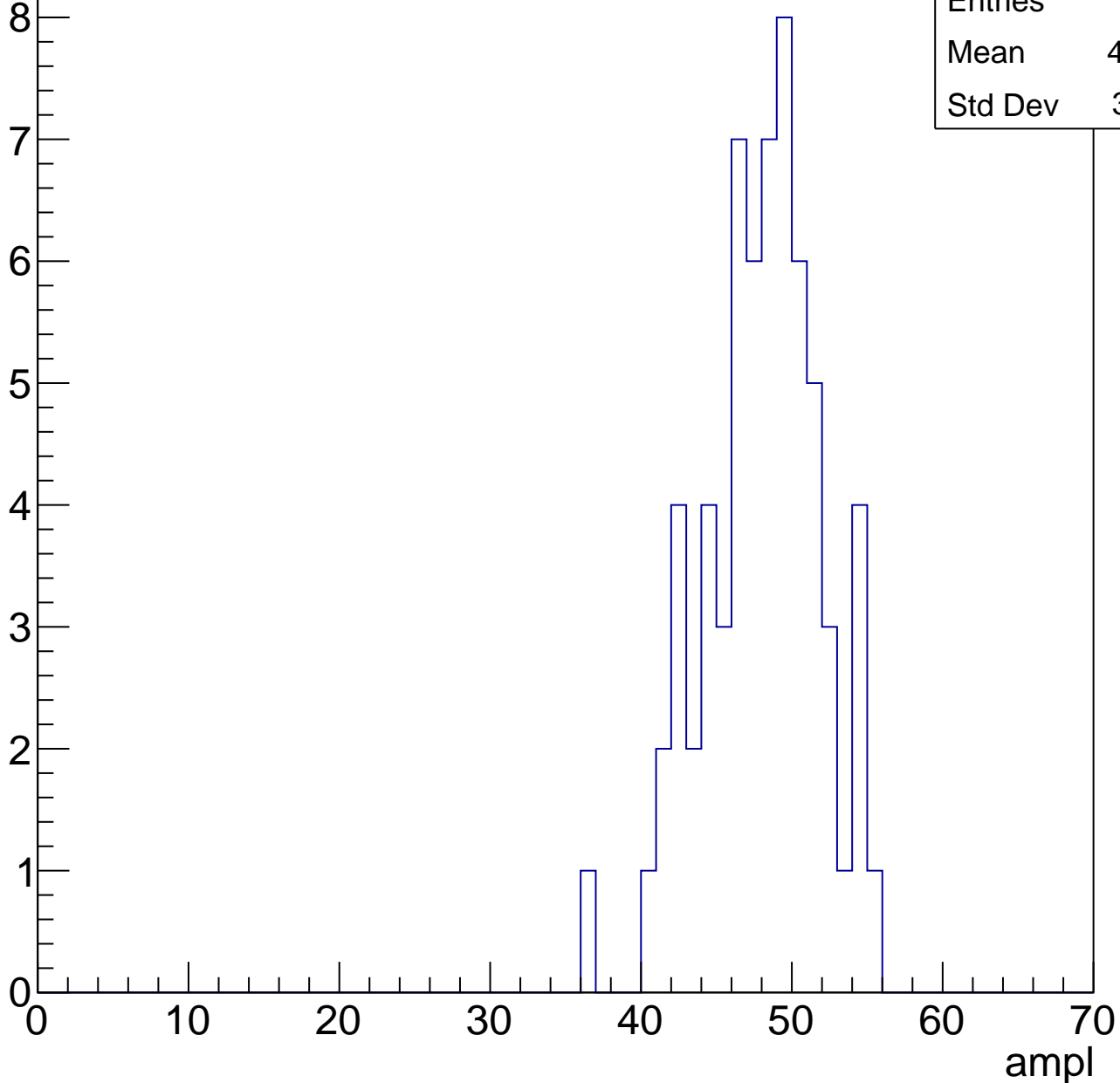


B1L103S, U23-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	47.54
Std Dev	3.831

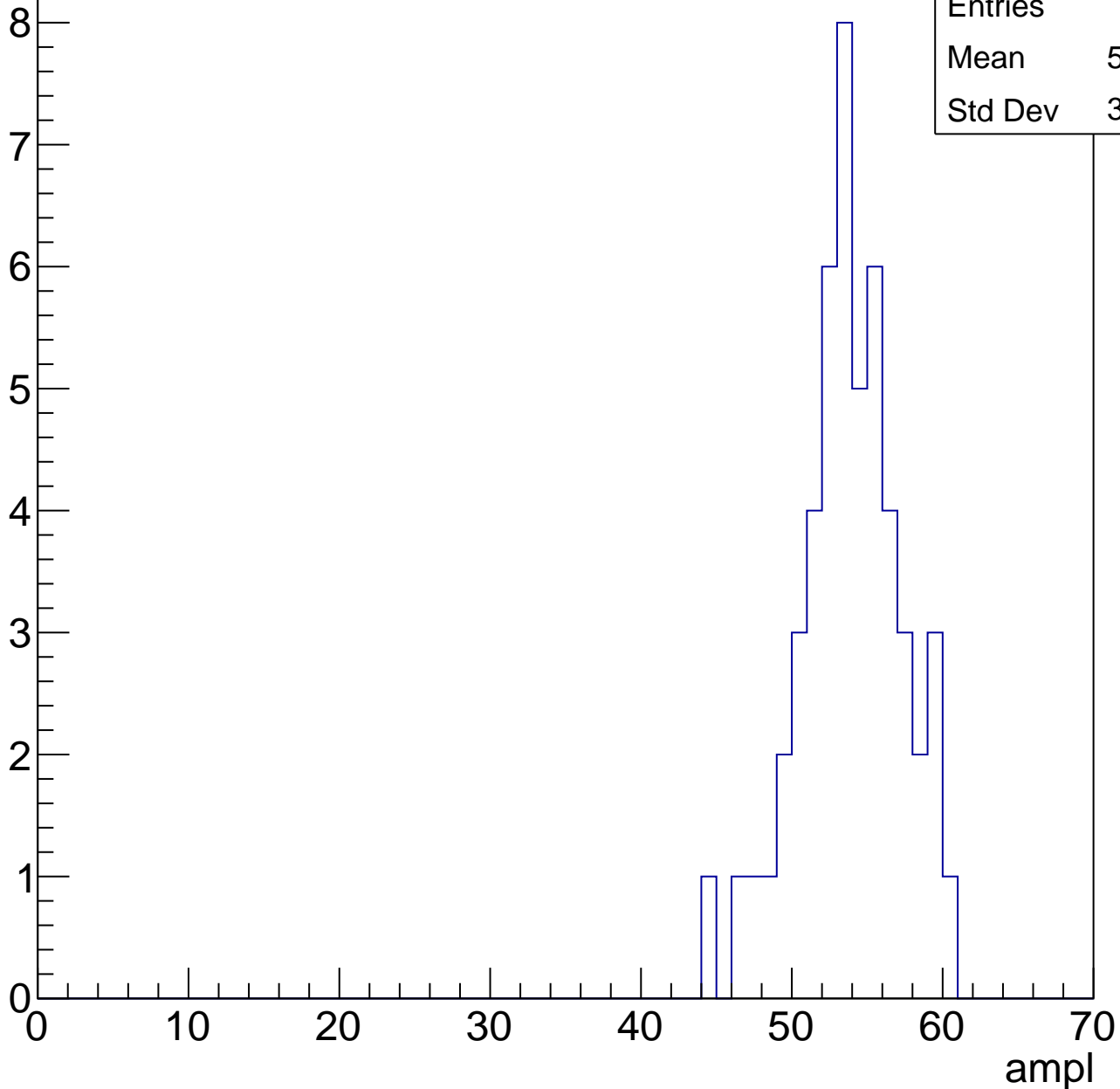


B1L103S, U23-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

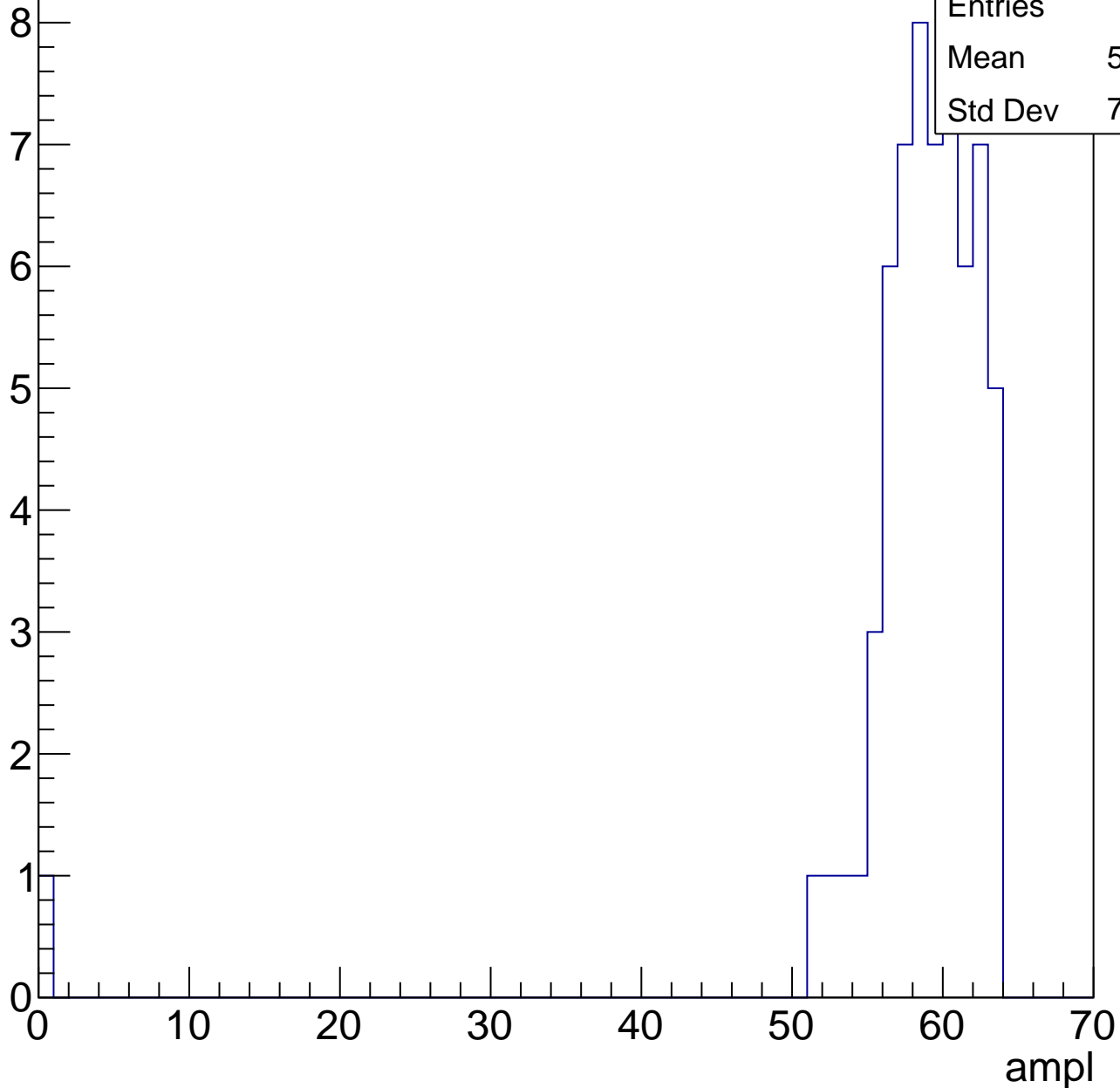
Entries	51
Mean	53.35
Std Dev	3.389



B1L103S, U23-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

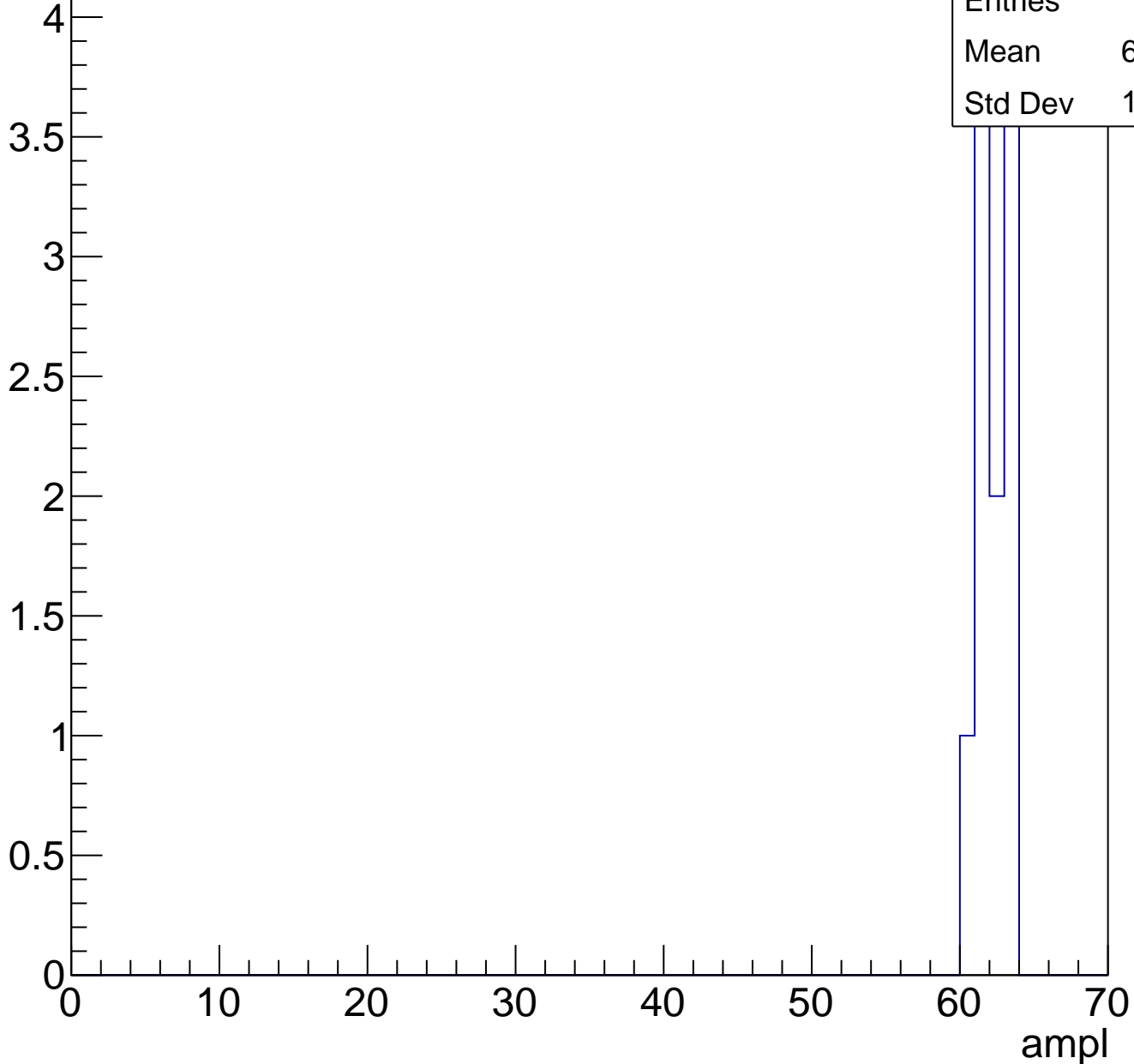
Entry



B1L103S, U23-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch103, adc0

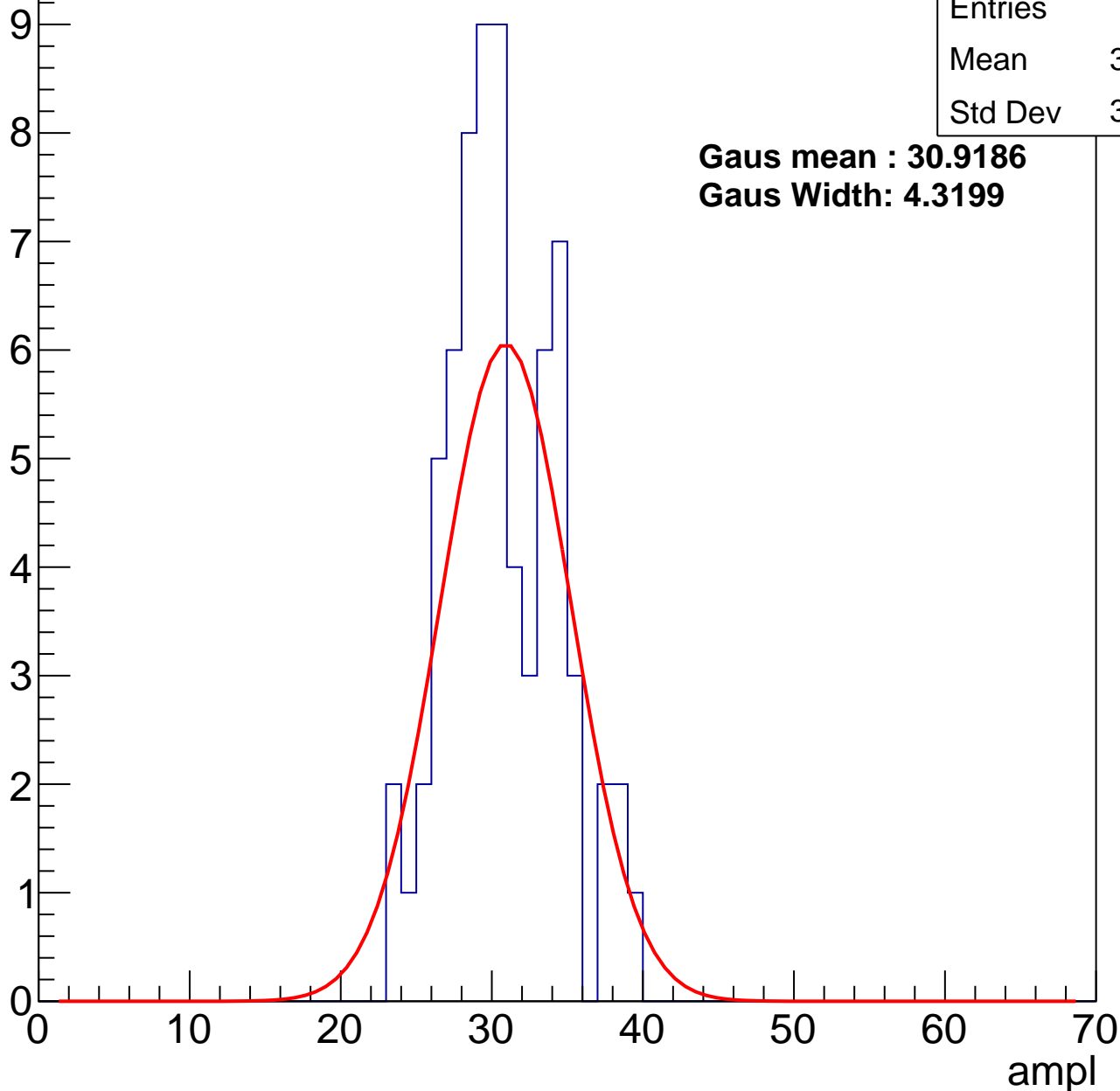
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	30.24
Std Dev	3.623

Gaus mean : 30.9186

Gaus Width: 4.3199



B1L103S, U23-ch103, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	65
Mean	36.6
Std Dev	3.81

Gaus mean : 36.7140

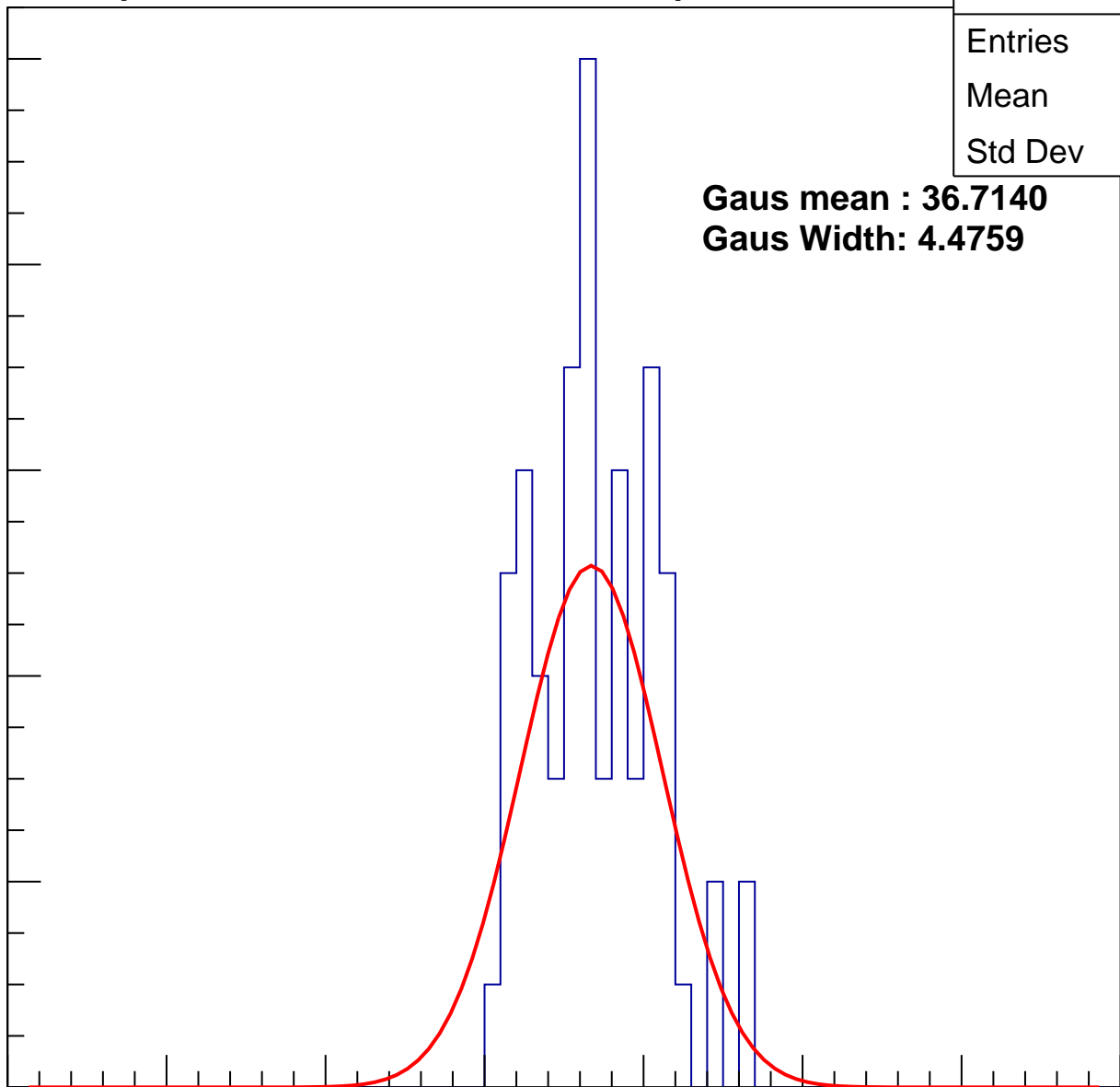
Gaus Width: 4.4759

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch103, adc2

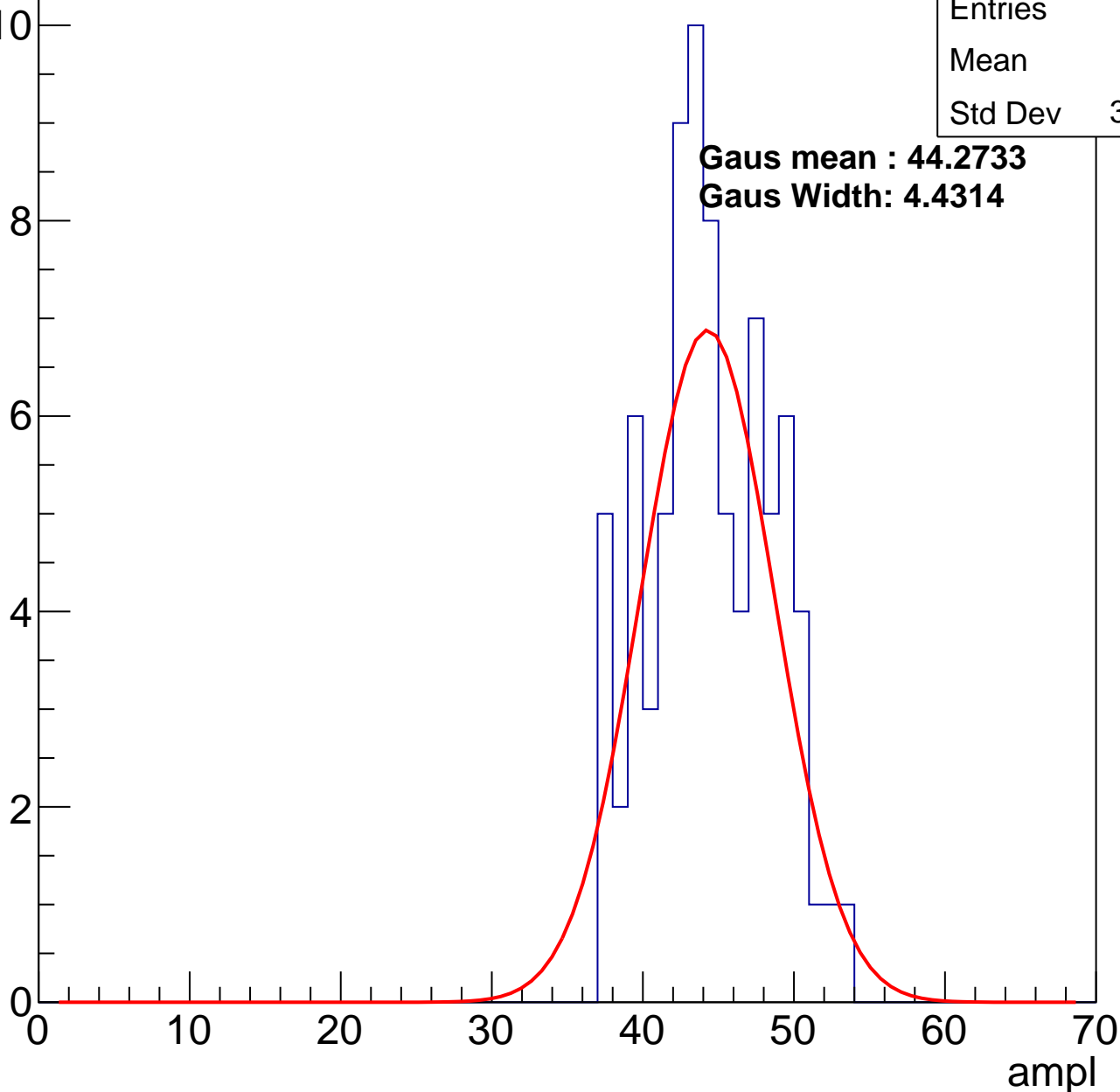
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	44
Std Dev	3.917

Gaus mean : 44.2733

Gaus Width: 4.4314

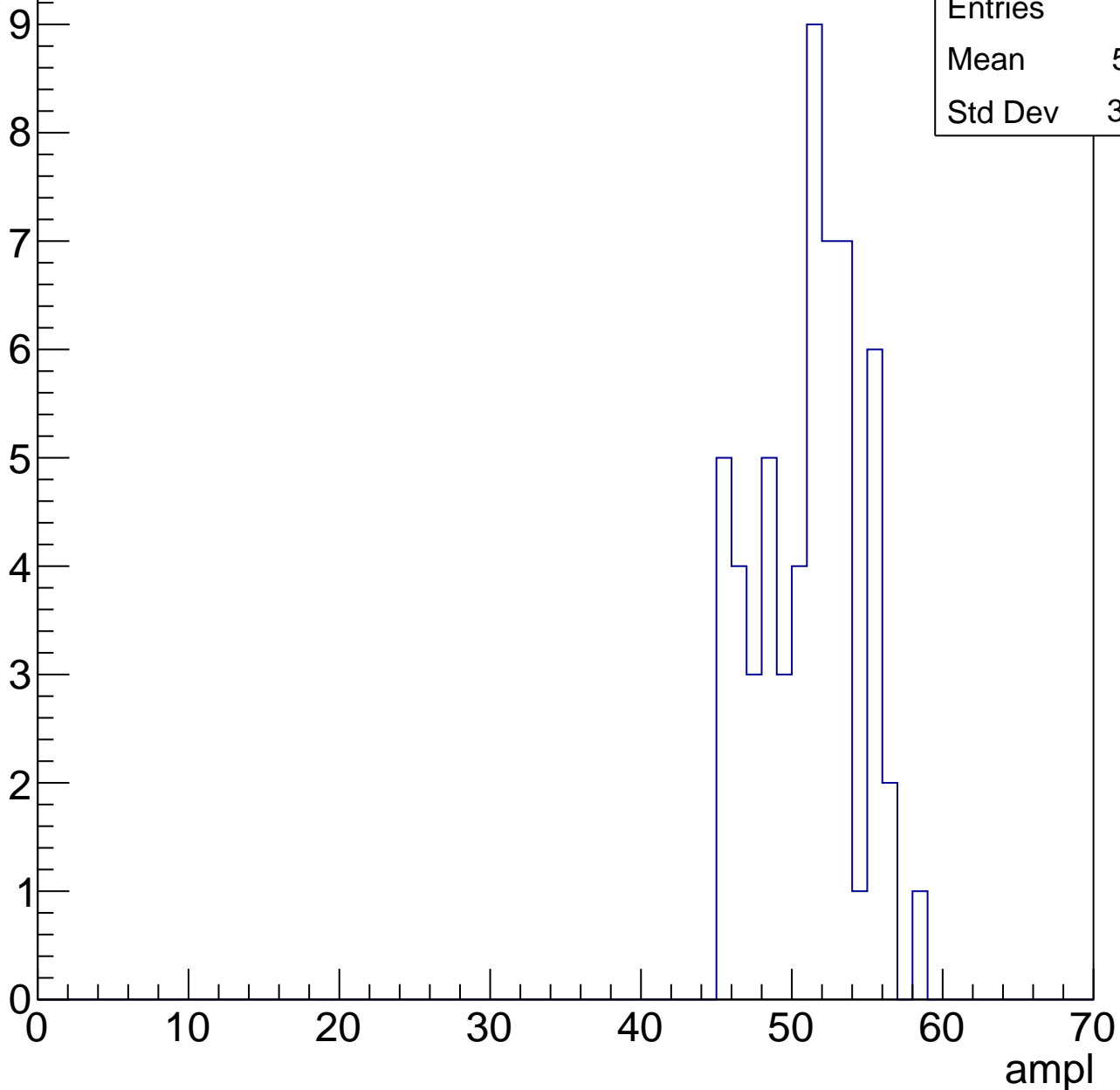


B1L103S, U23-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

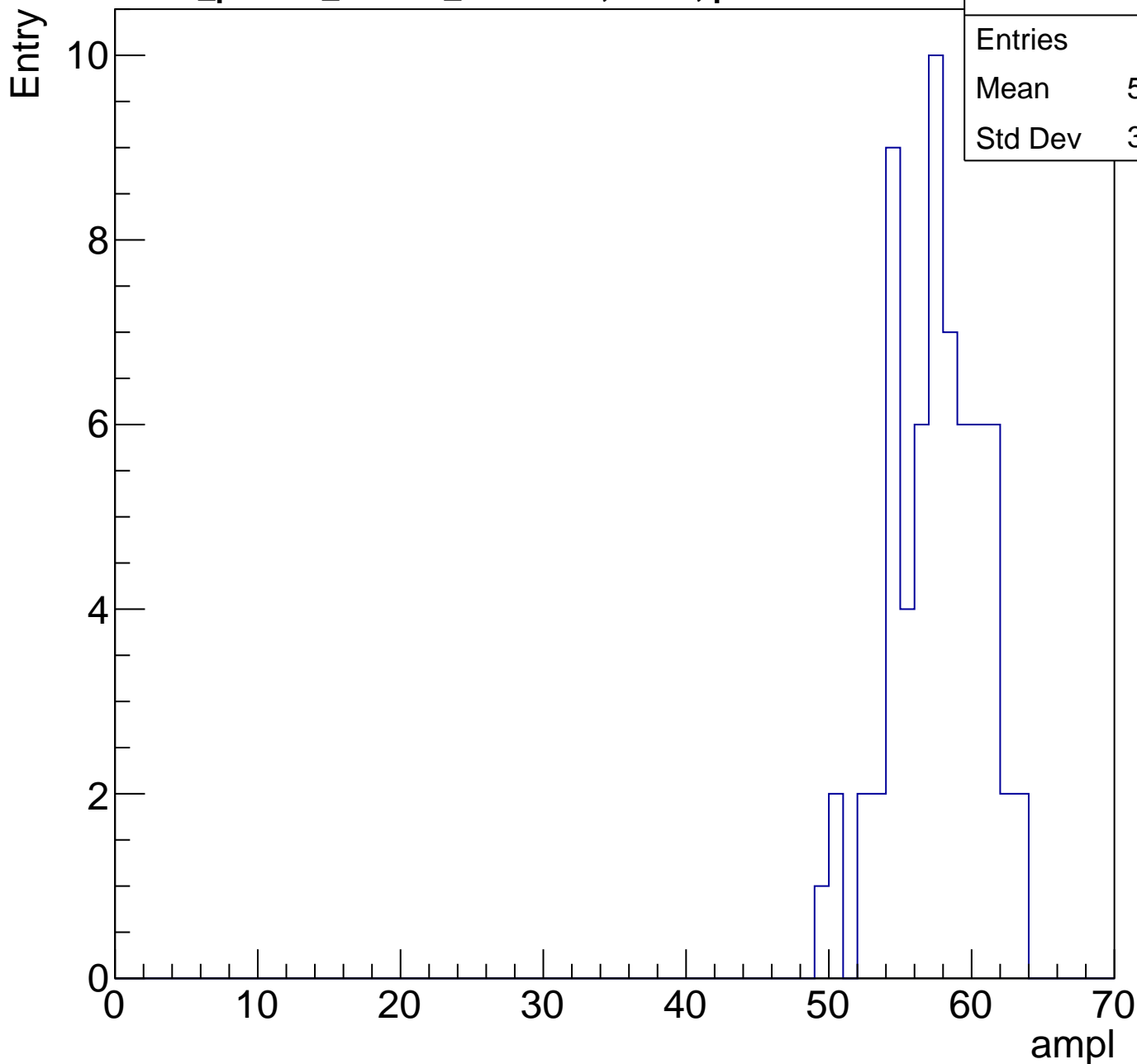
Entries	57
Mean	50.61
Std Dev	3.297



B1L103S, U23-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	65
Mean	57.03
Std Dev	3.162

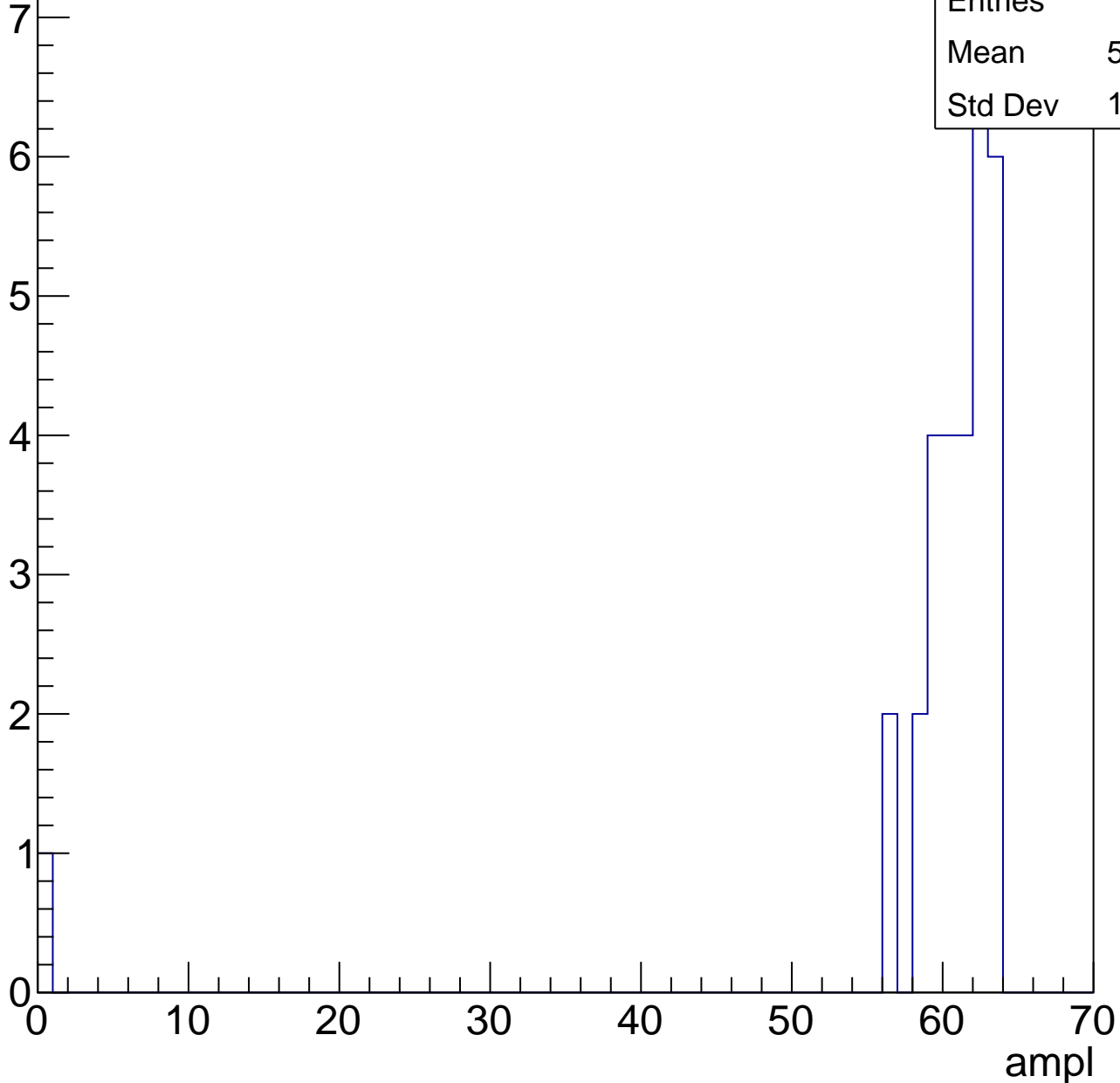


B1L103S, U23-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	58.67
Std Dev	11.07



B1L103S, U23-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

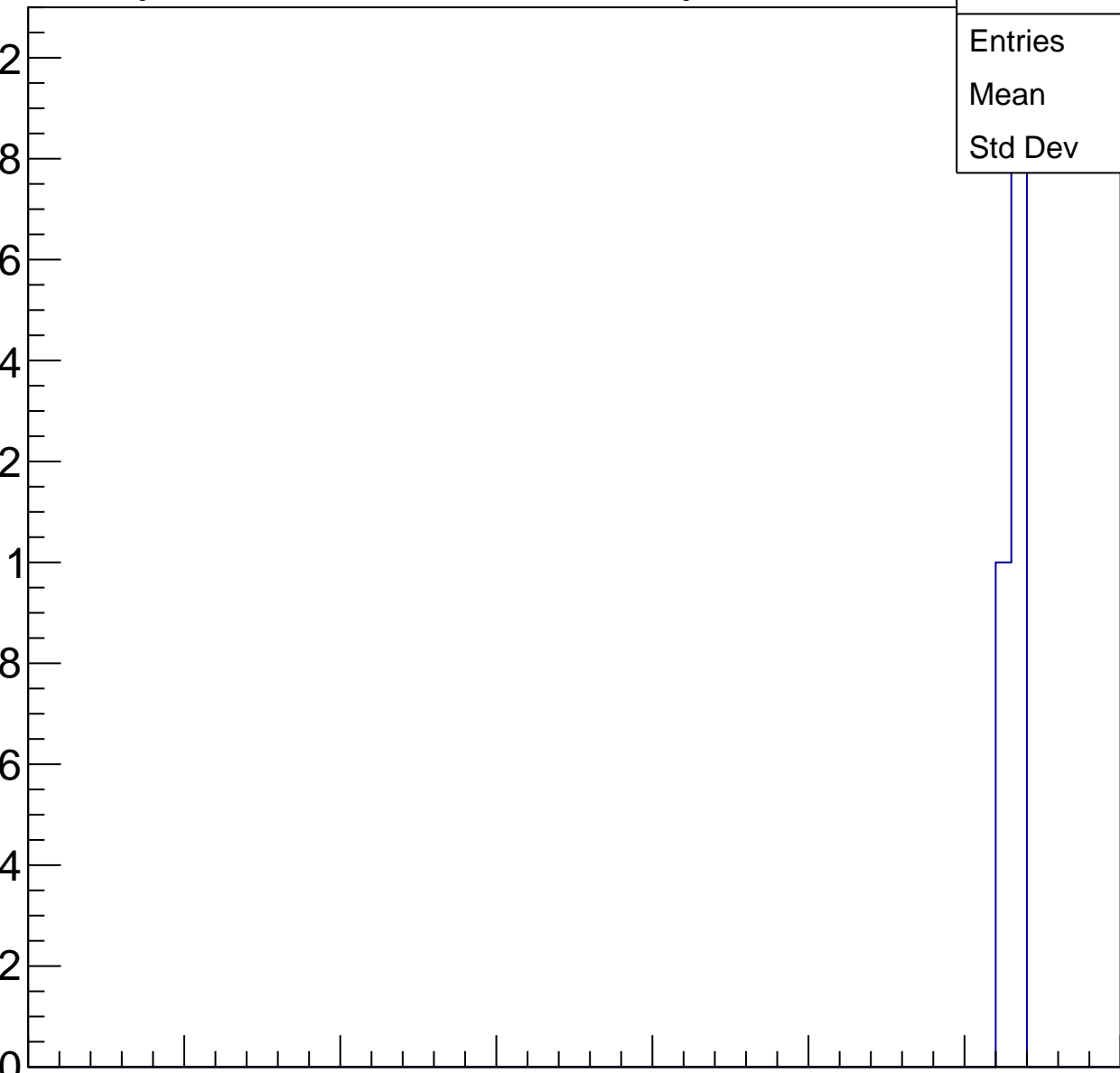
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L103S, U23-ch104, adc0

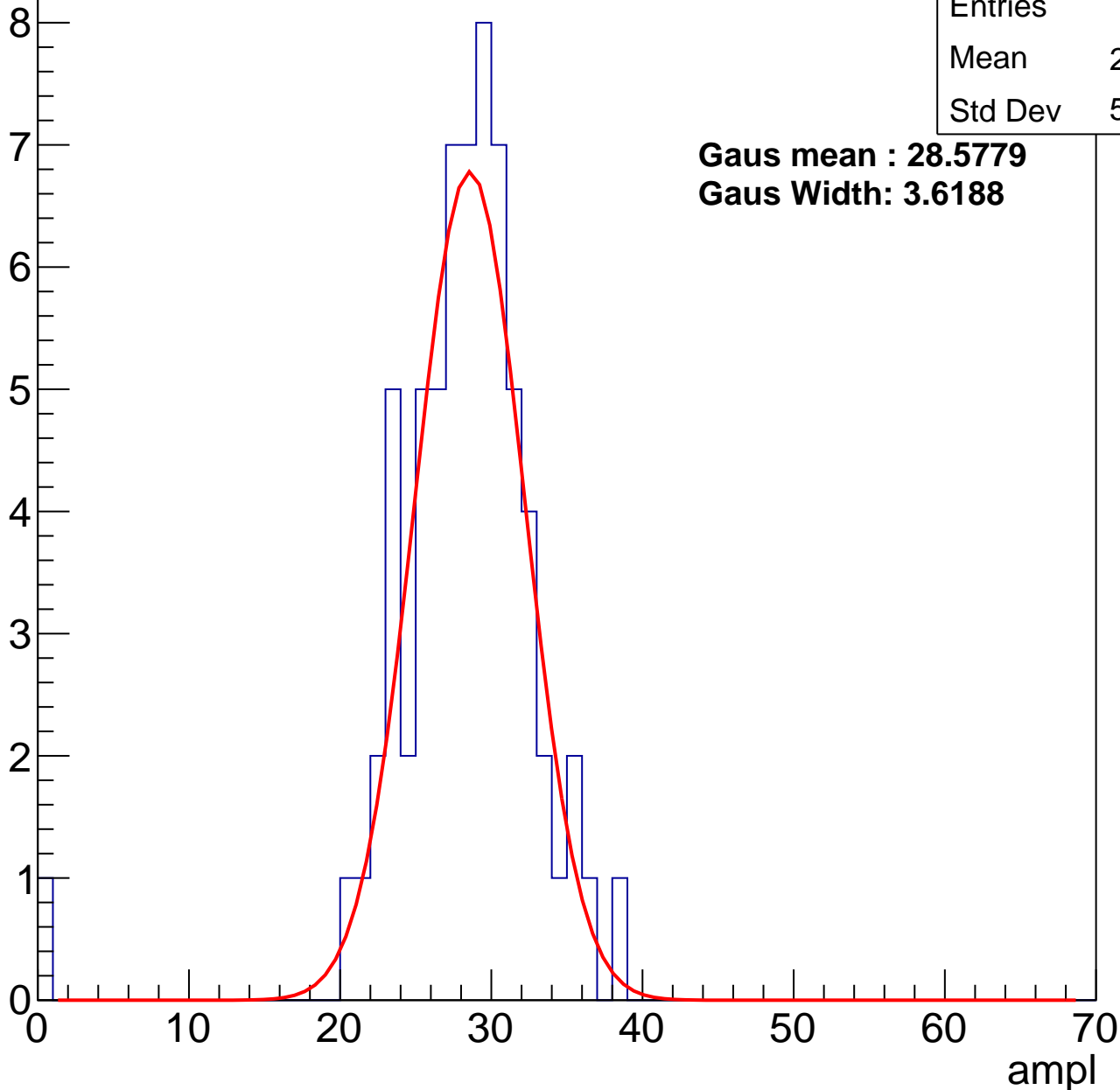
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.72
Std Dev	5.016

Gaus mean : 28.5779

Gaus Width: 3.6188



B1L103S, U23-ch104, adc1

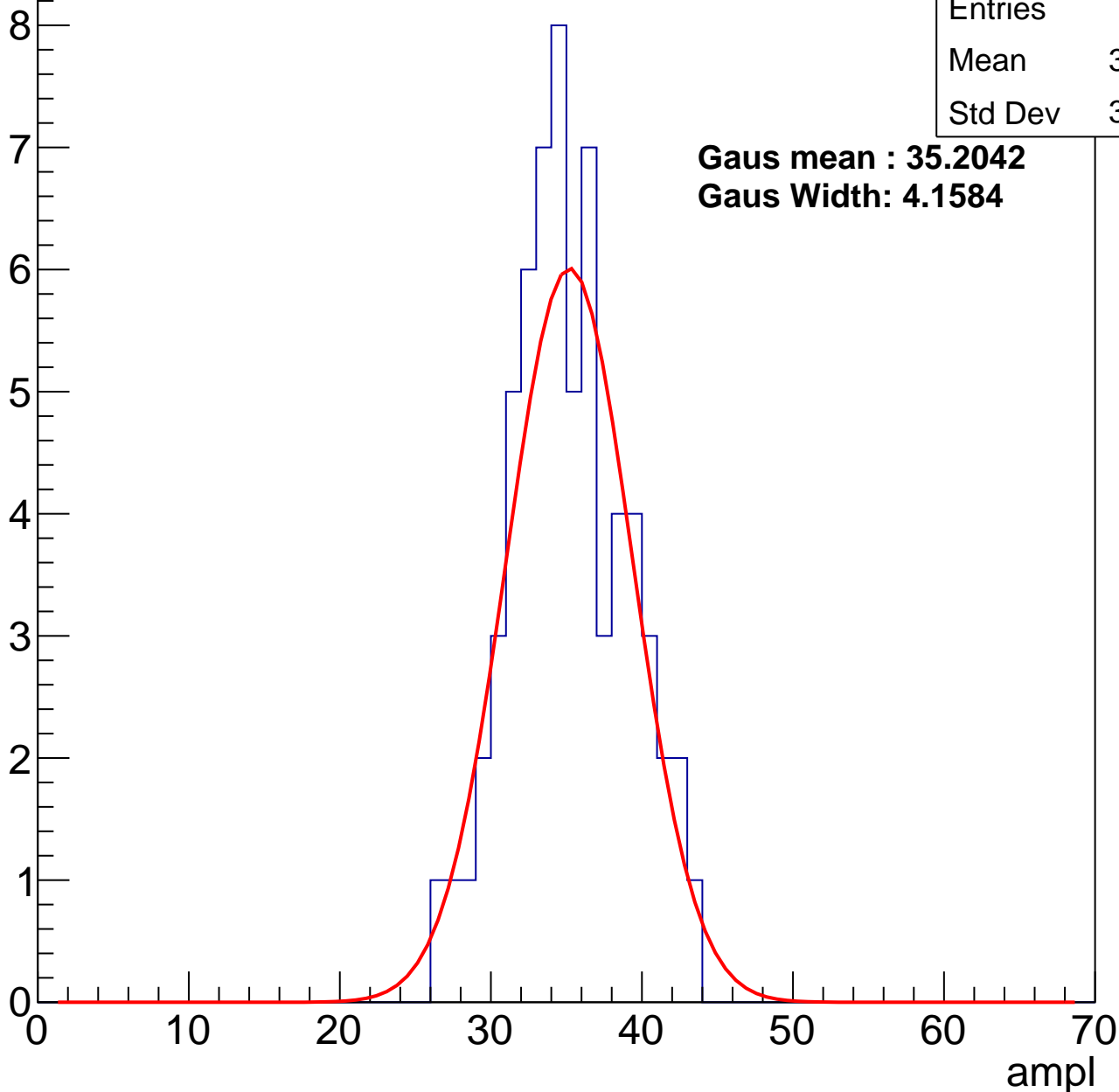
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	34.68
Std Dev	3.787

Gaus mean : 35.2042

Gaus Width: 4.1584



B1L103S, U23-ch104, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	41.61
Std Dev	4.02

Gaus mean : 41.7935

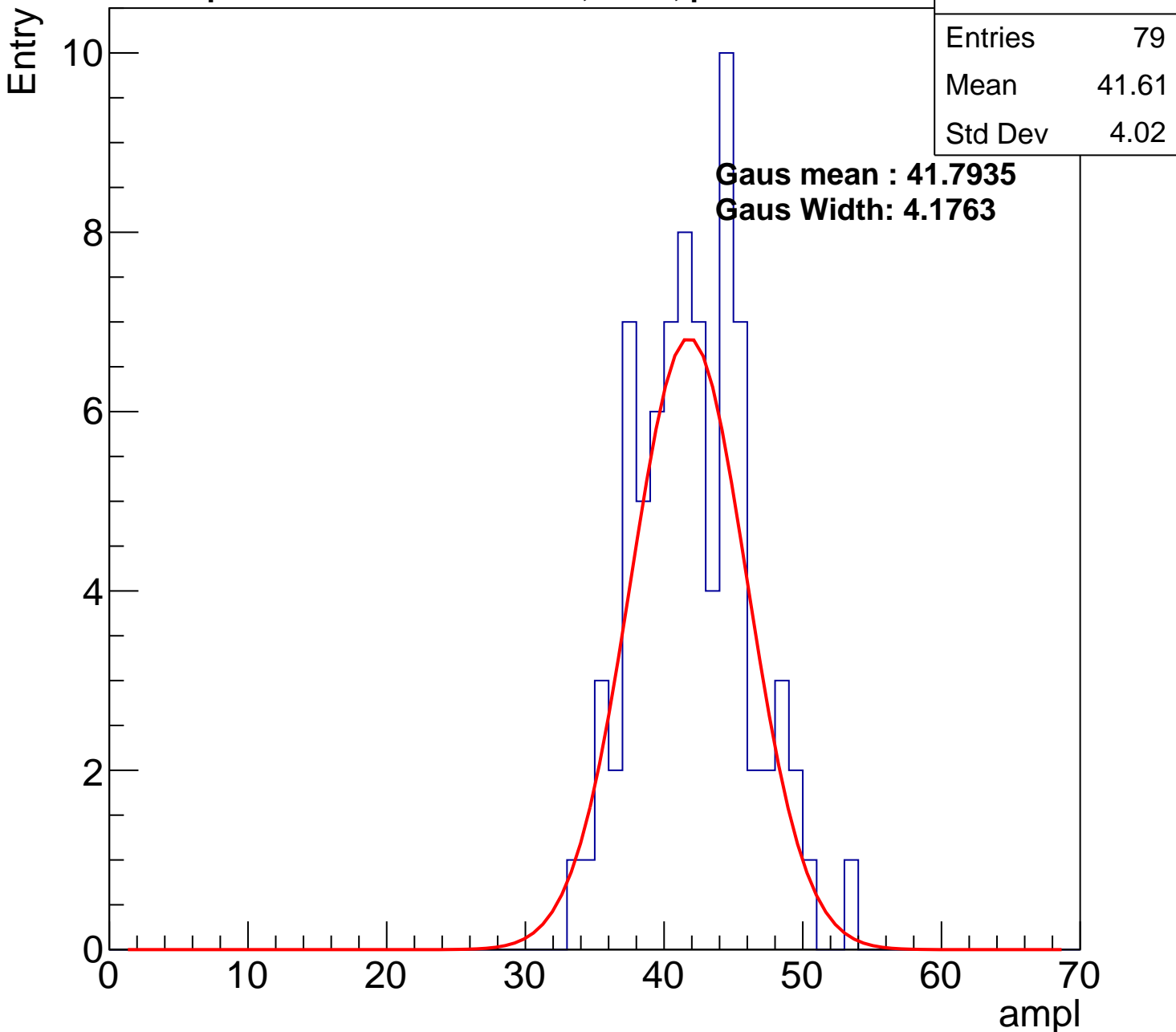
Gaus Width: 4.1763

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

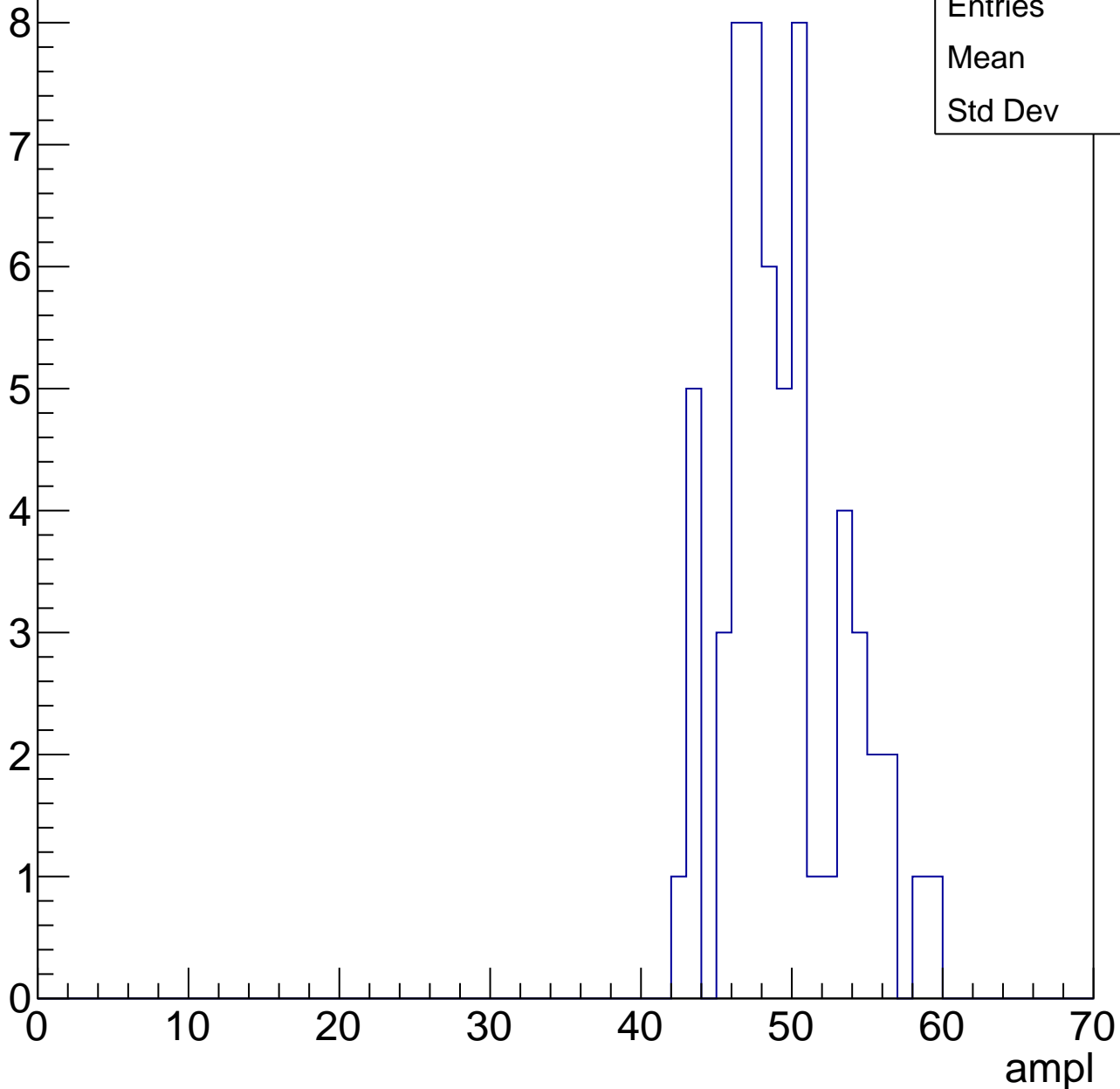


B1L103S, U23-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	48.9
Std Dev	3.9

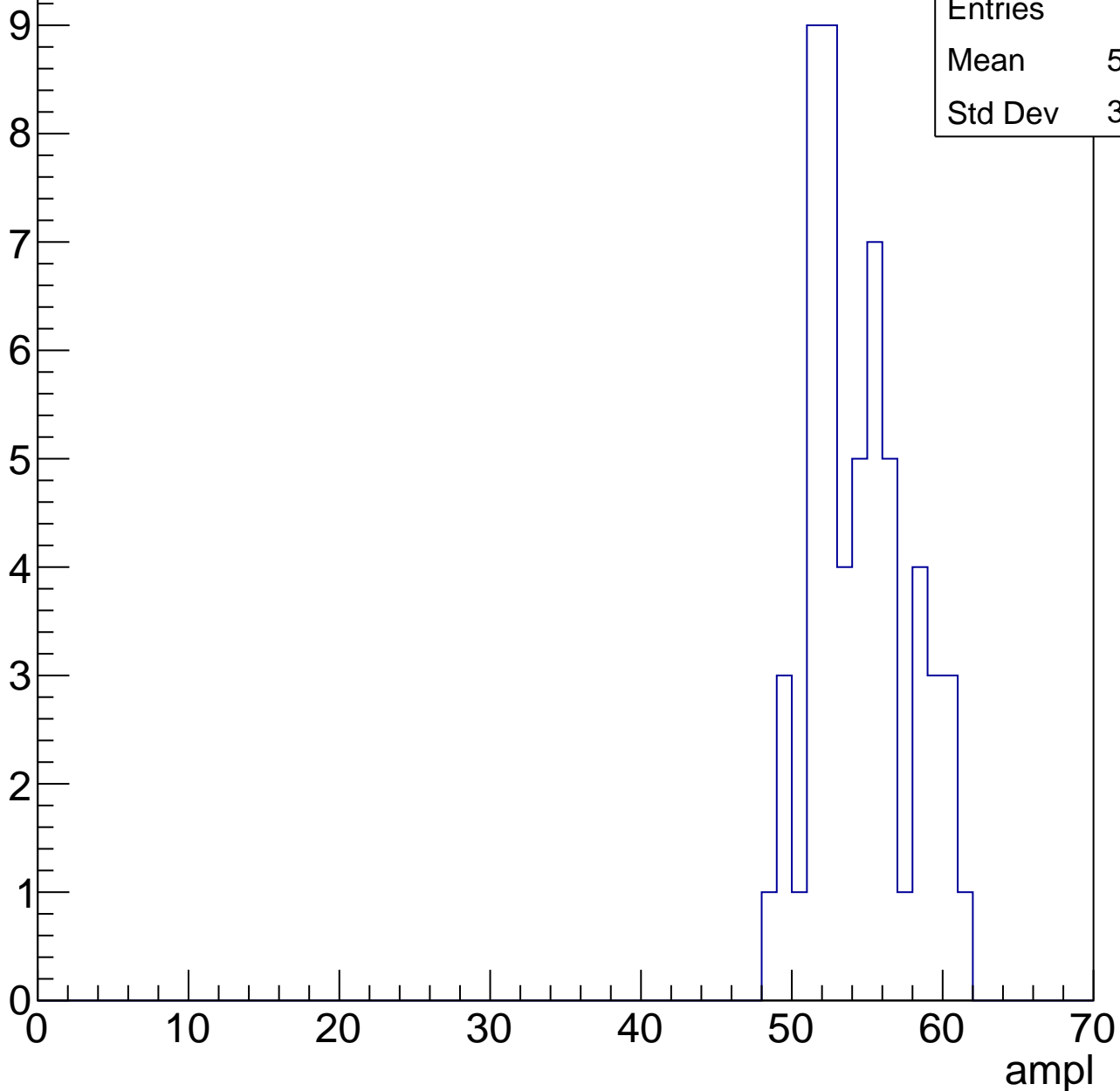


B1L103S, U23-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

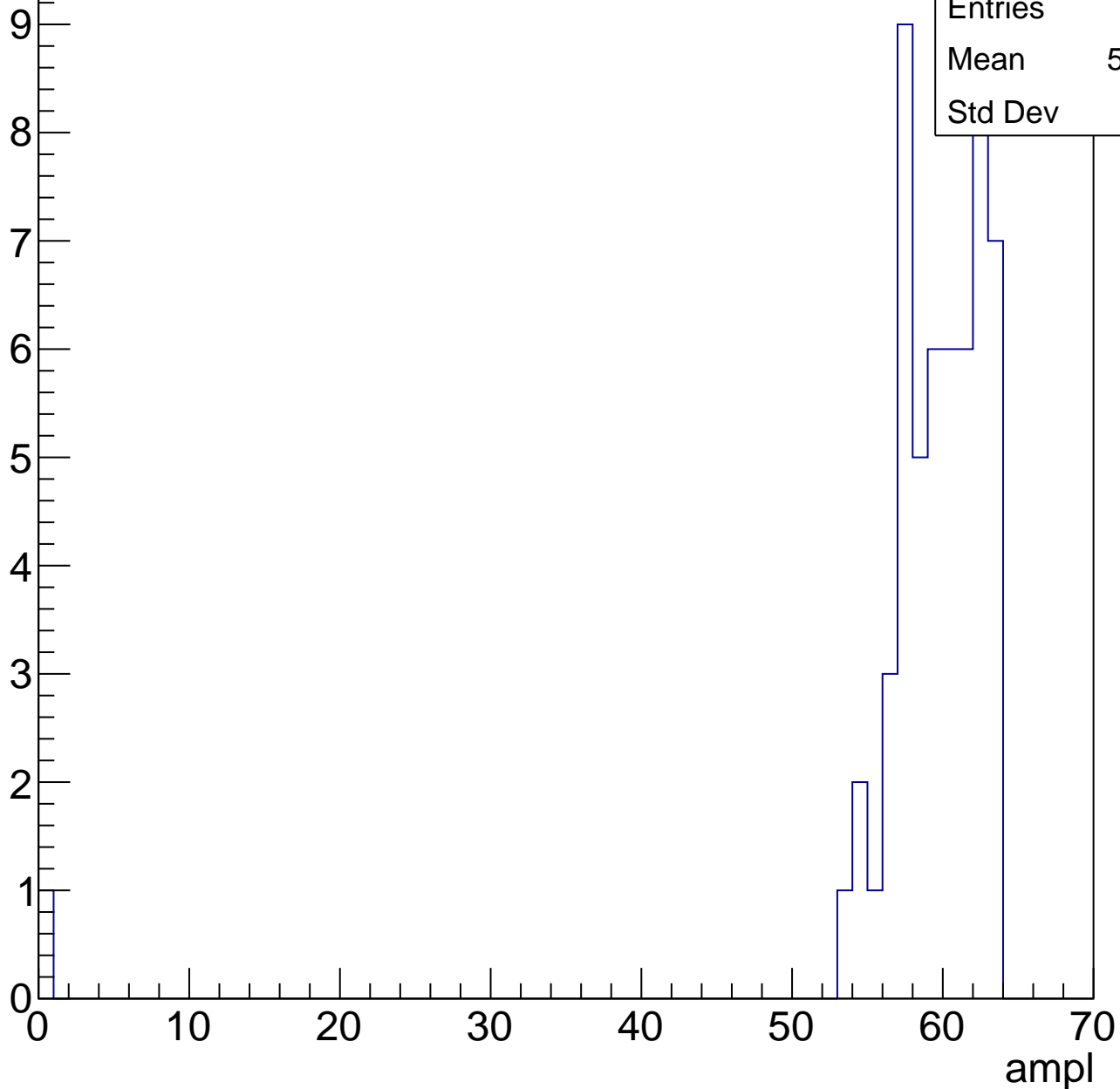
Entries	56
Mean	54.04
Std Dev	3.218



B1L103S, U23-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

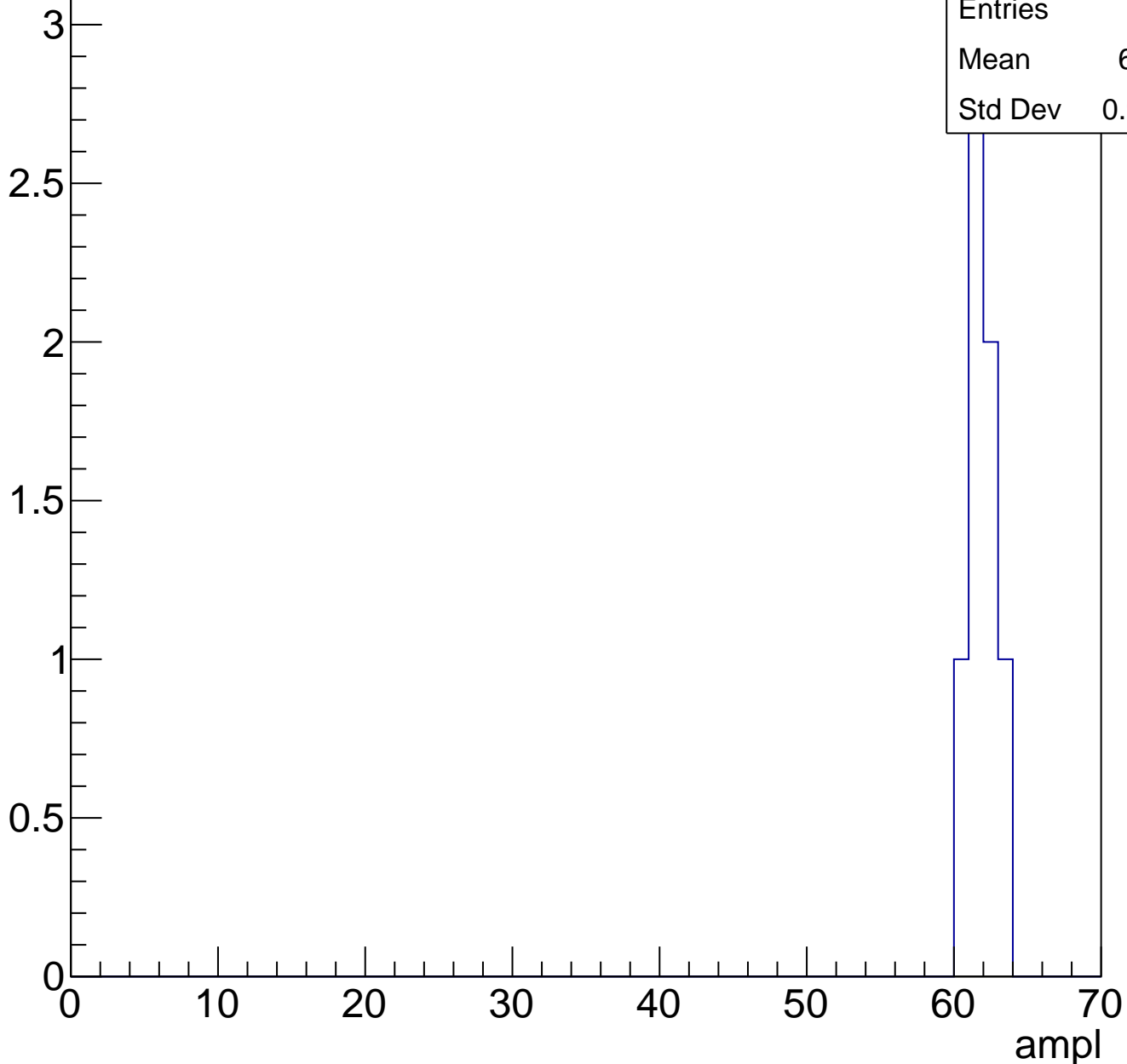
Entry



B1L103S, U23-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch105, adc0

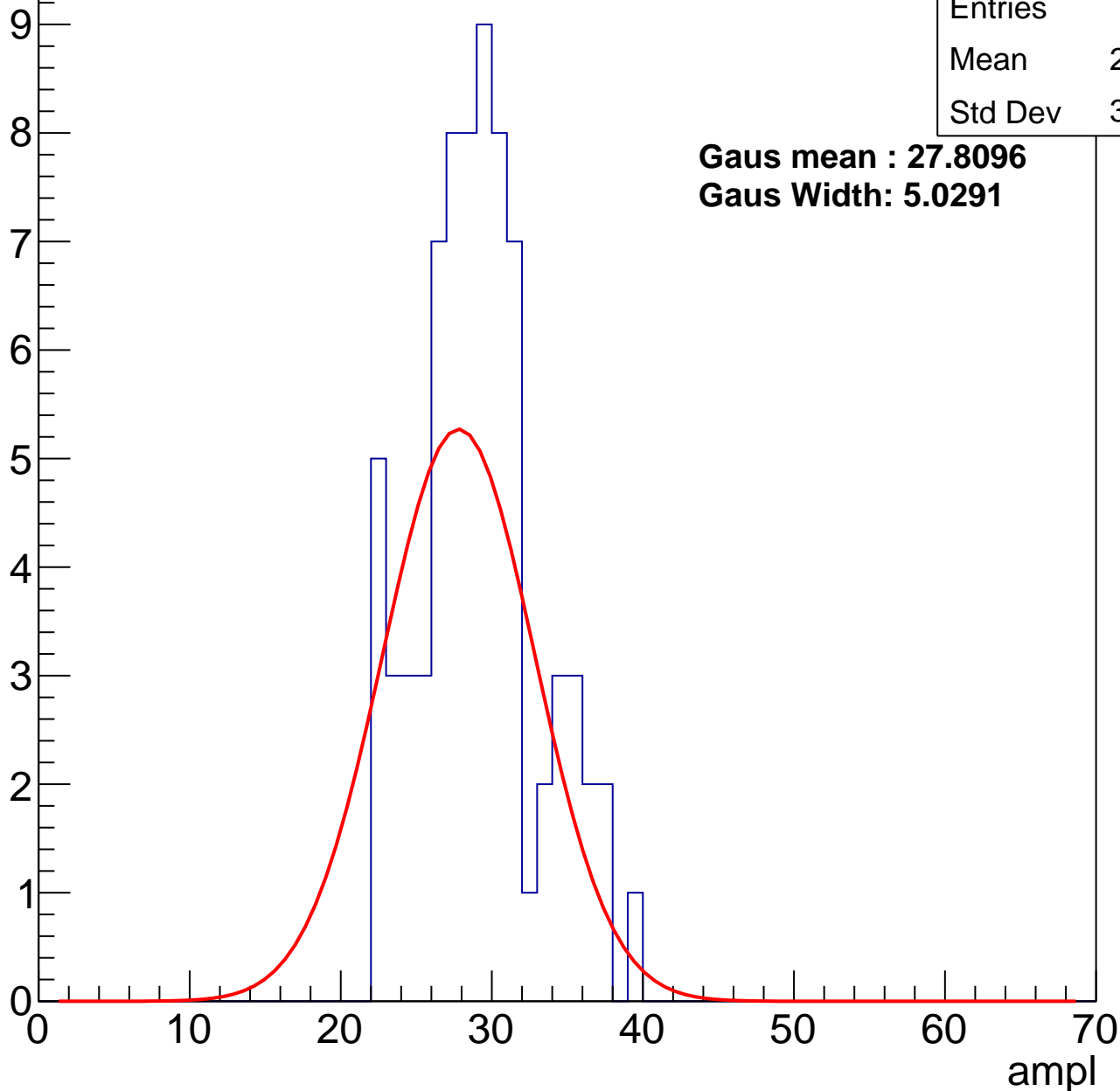
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.75
Std Dev	3.933

Gaus mean : 27.8096

Gaus Width: 5.0291



B1L103S, U23-ch105, adc1

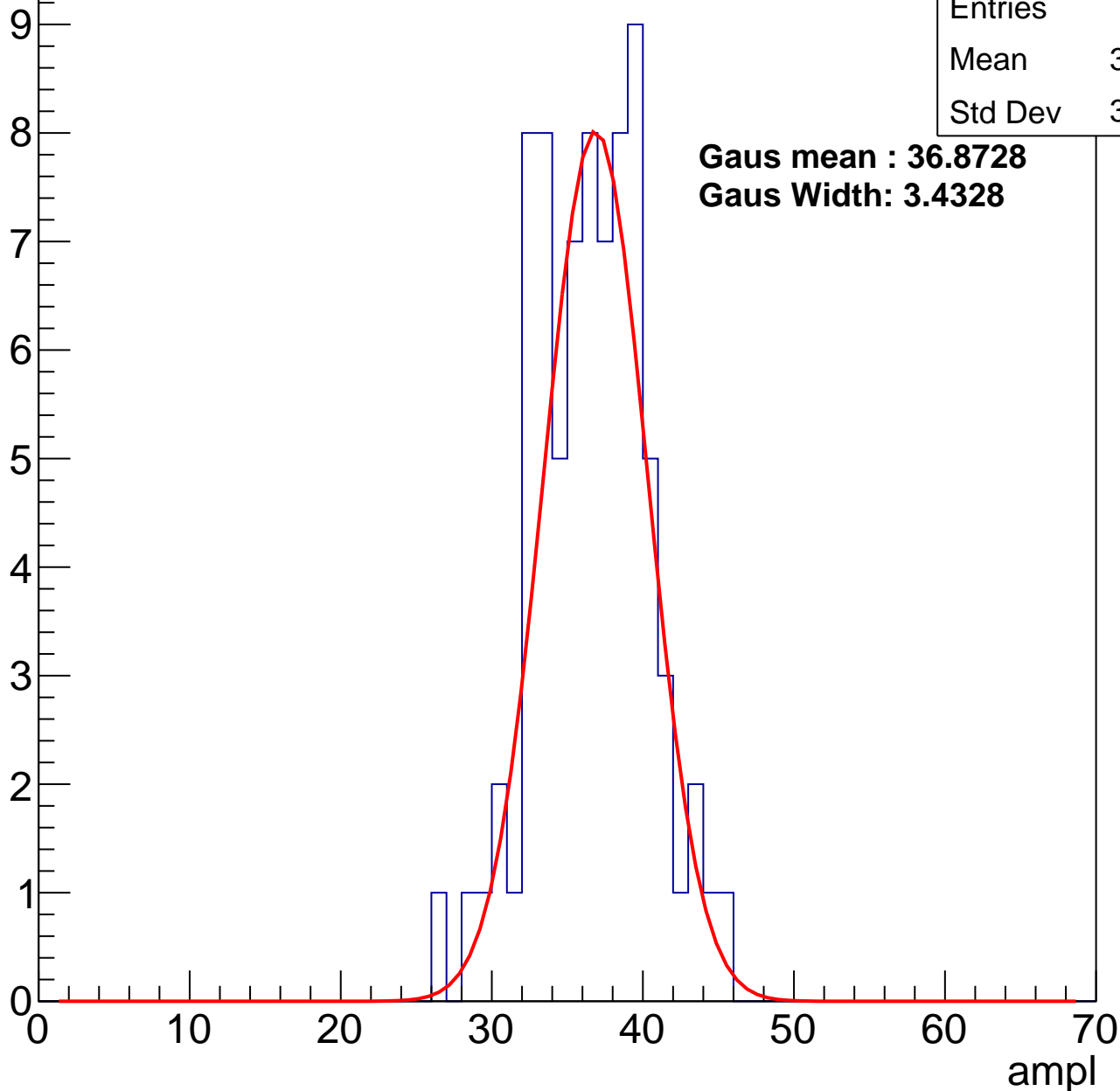
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	36.09
Std Dev	3.715

Gaus mean : 36.8728

Gaus Width: 3.4328



B1L103S, U23-ch105, adc2

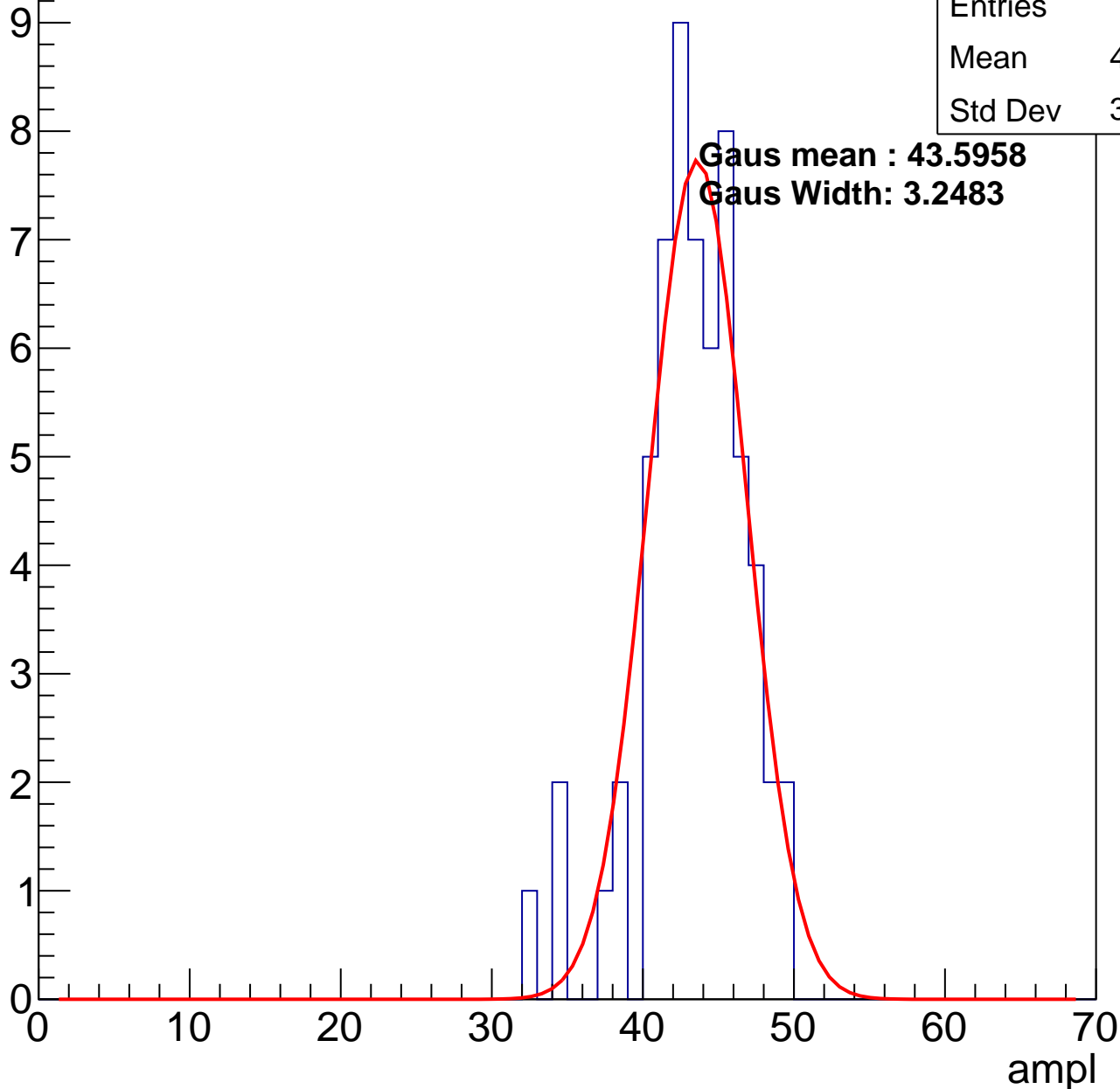
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.87
Std Dev	3.433

Gaus mean : 43.5958

Gaus Width: 3.2483

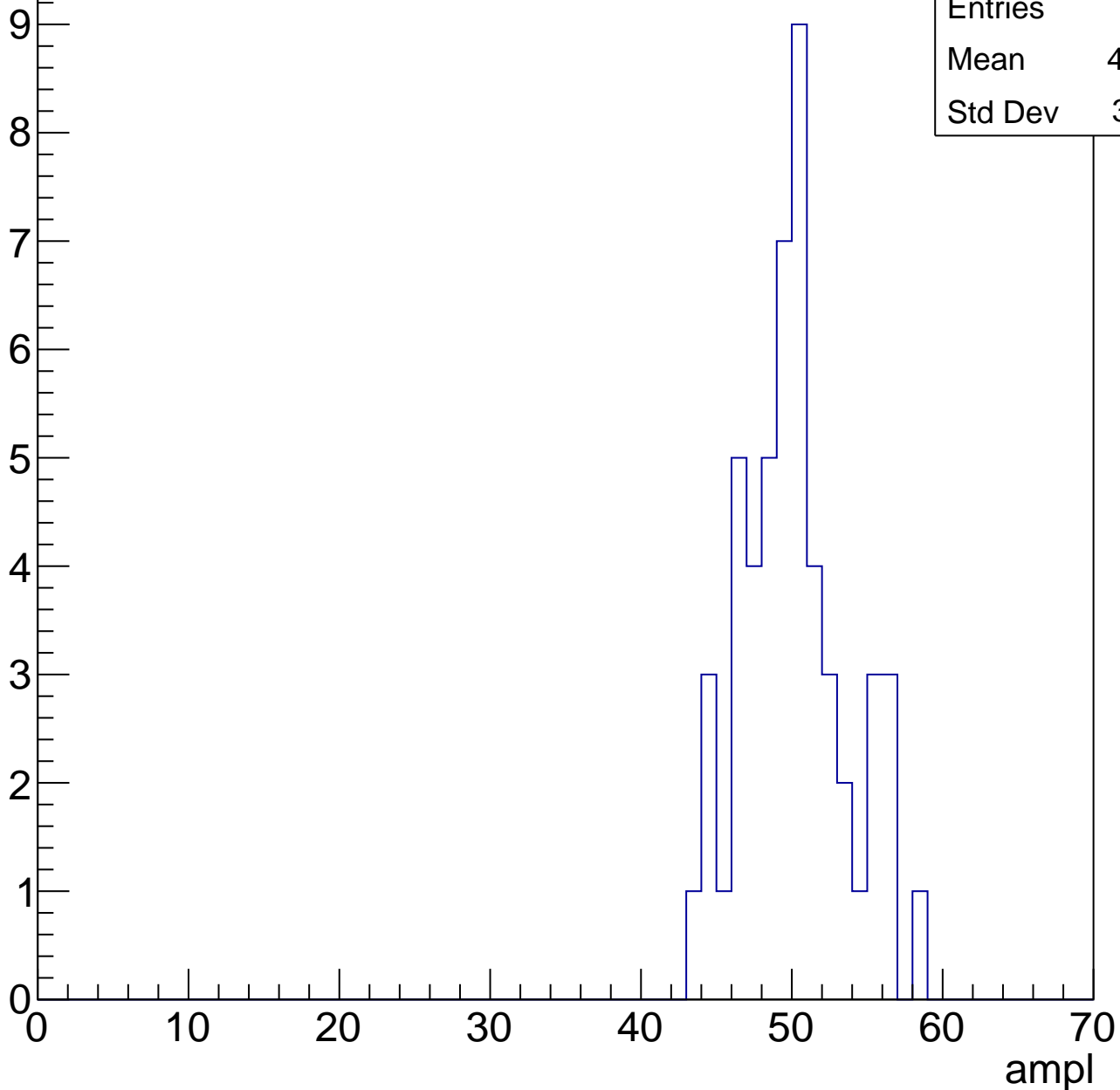


B1L103S, U23-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

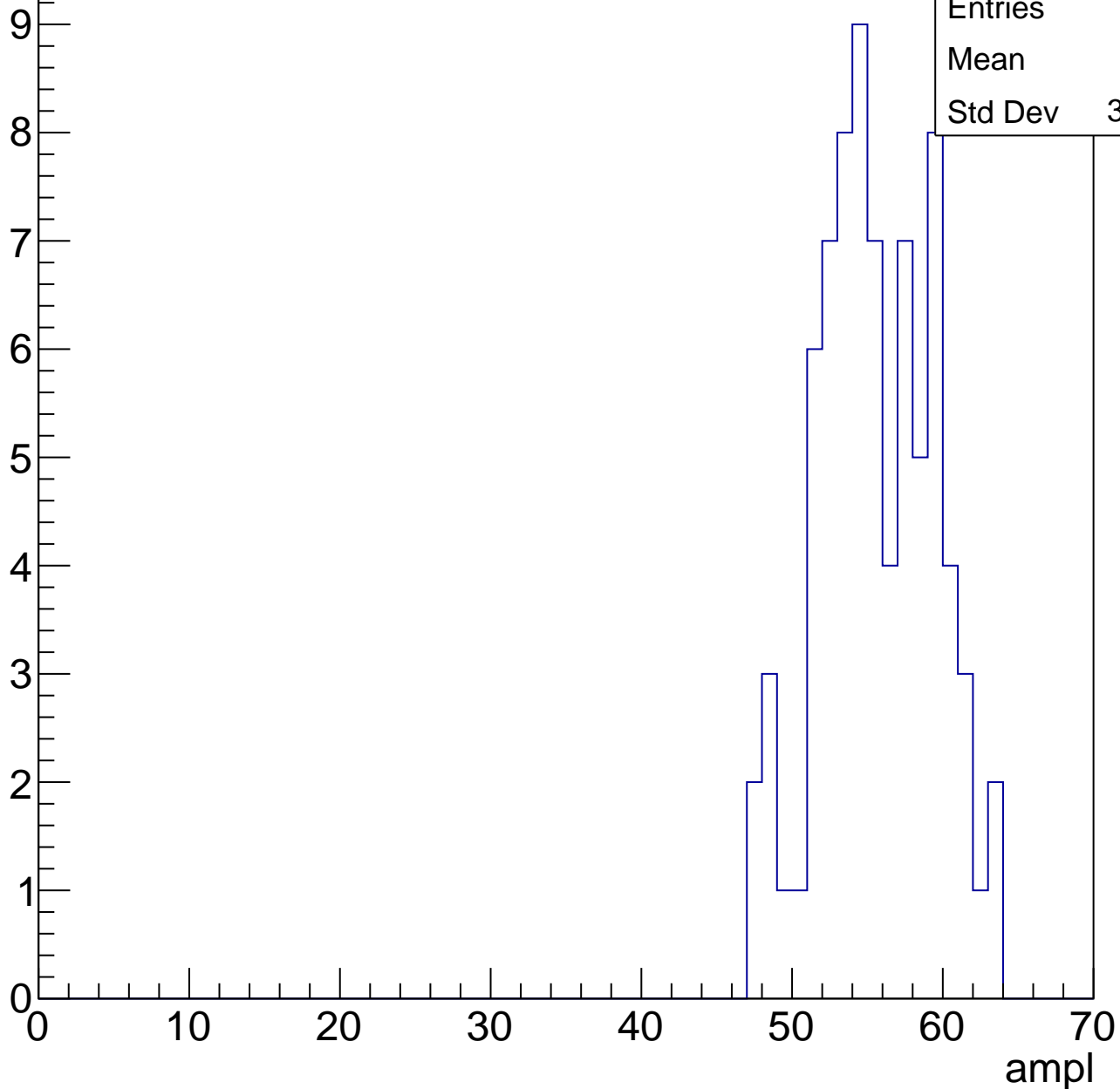
Entries	52
Mean	49.65
Std Dev	3.441



B1L103S, U23-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



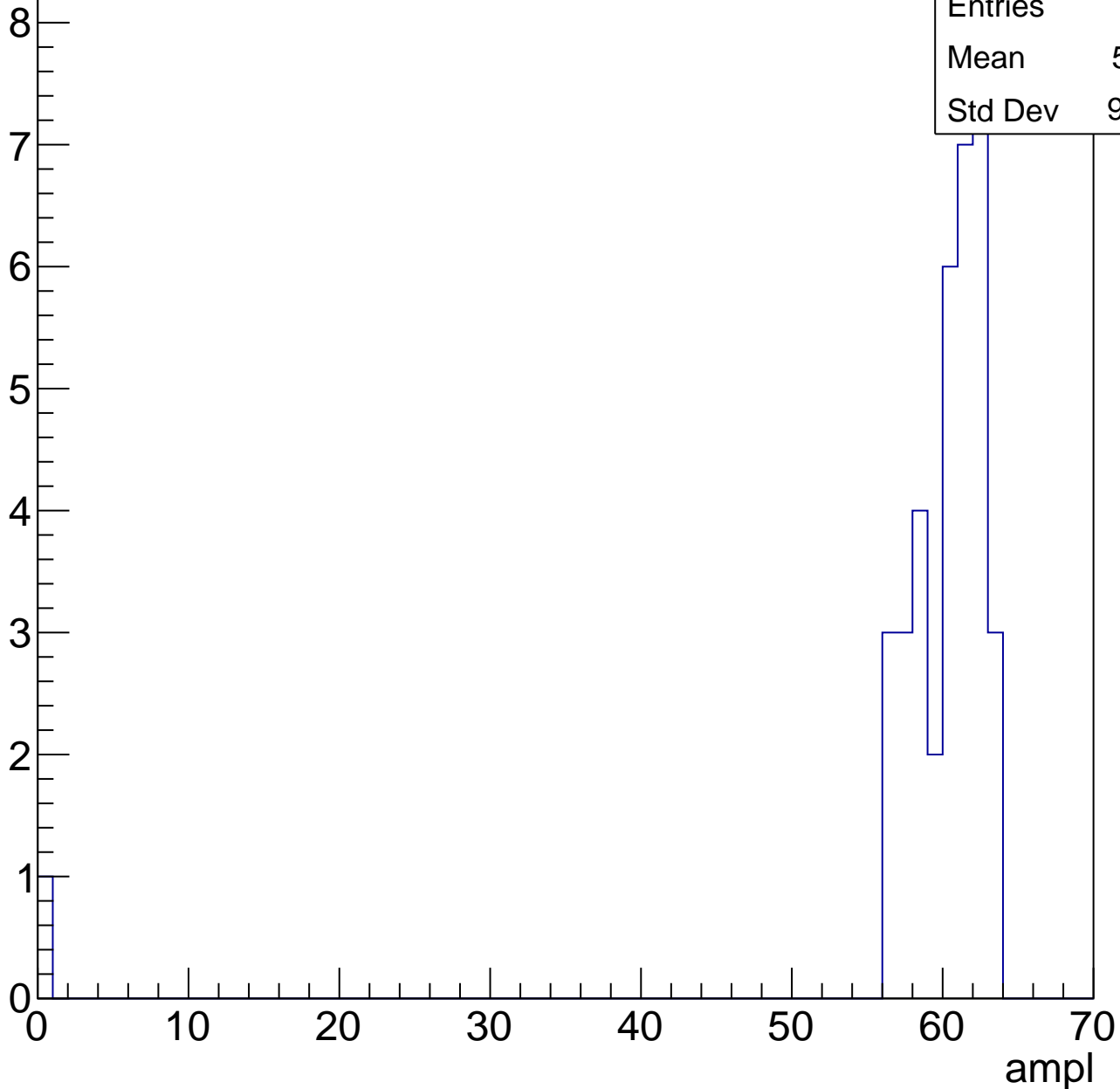
Entries	78
Mean	55.1
Std Dev	3.798

B1L103S, U23-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.41
Std Dev	9.952



B1L103S, U23-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

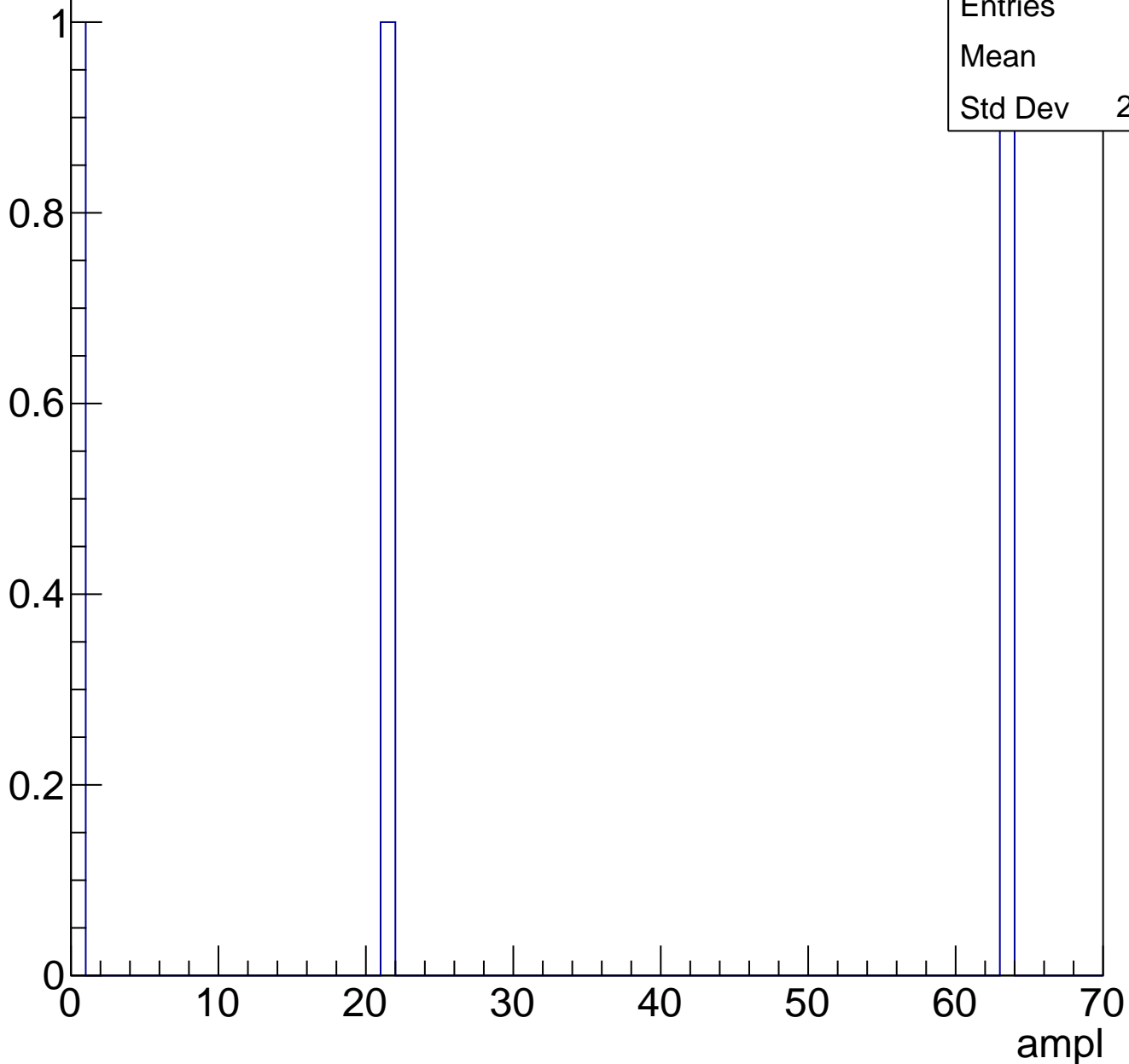


Entries	5
Mean	62.8
Std Dev	0.4

B1L103S, U23-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L103S, U23-ch106, adc0

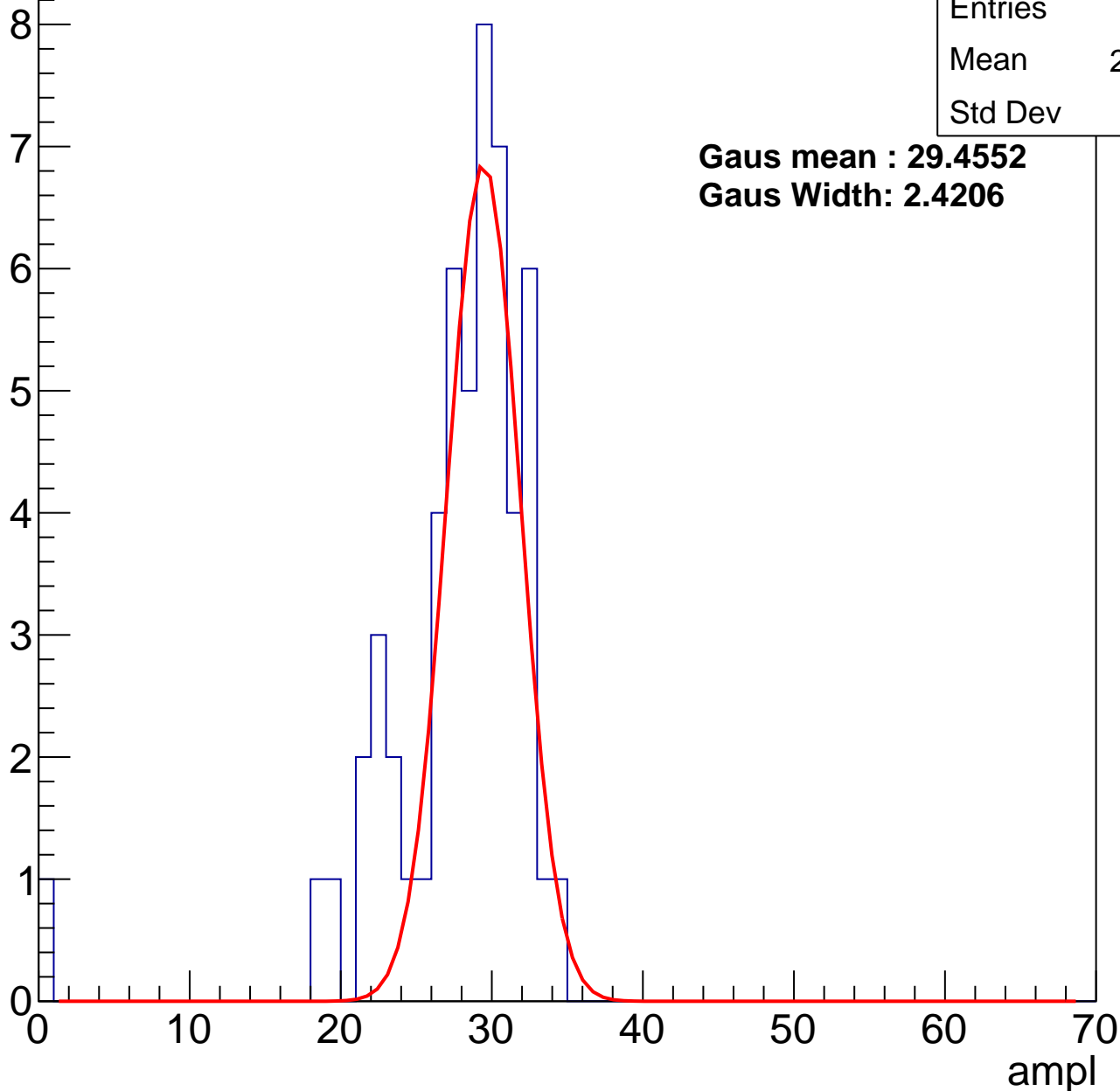
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	27.24
Std Dev	5.21

Gaus mean : 29.4552

Gaus Width: 2.4206



B1L103S, U23-ch106, adc1

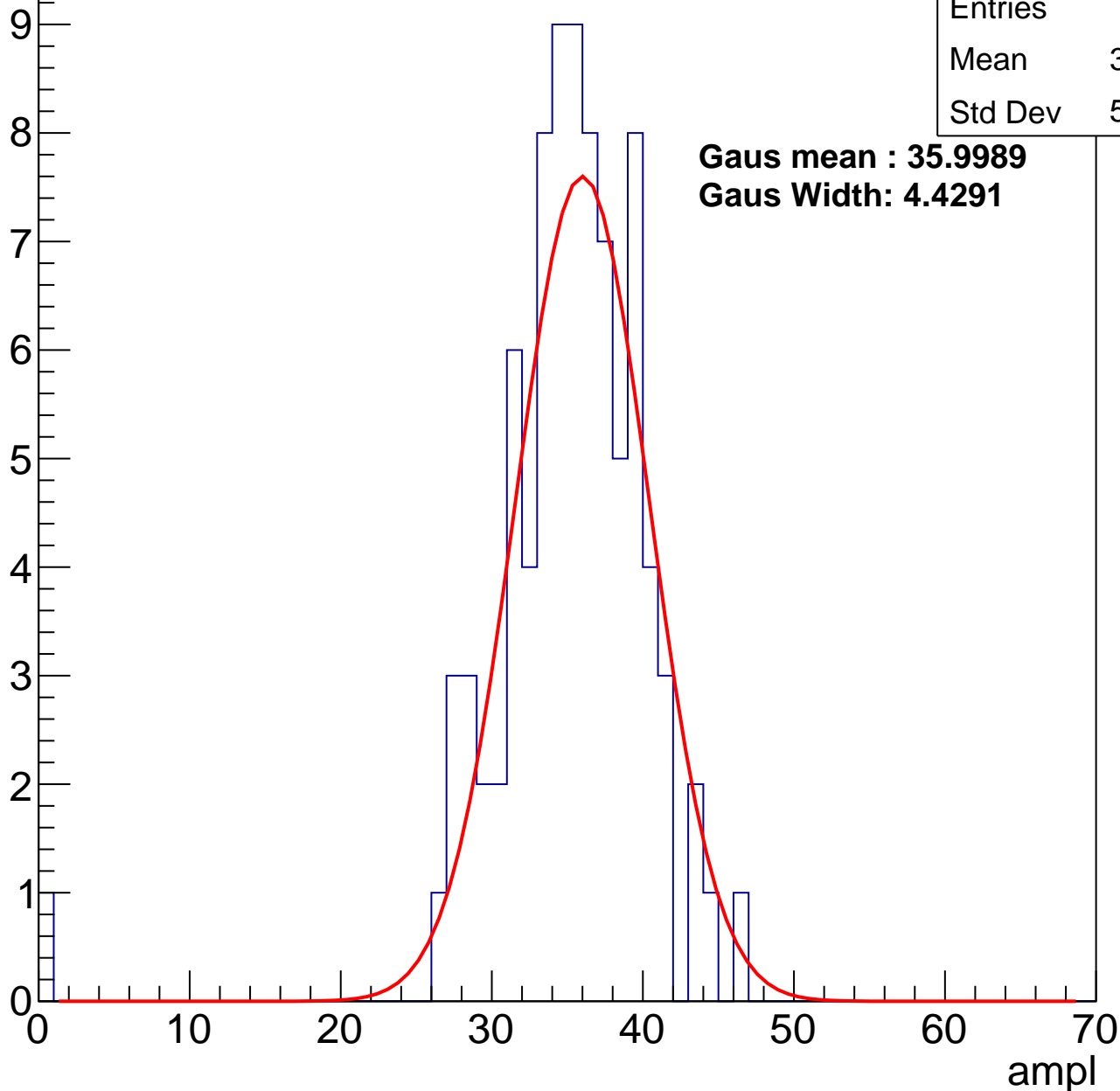
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	34.67
Std Dev	5.547

Gaus mean : 35.9989

Gaus Width: 4.4291



B1L103S, U23-ch106, adc2

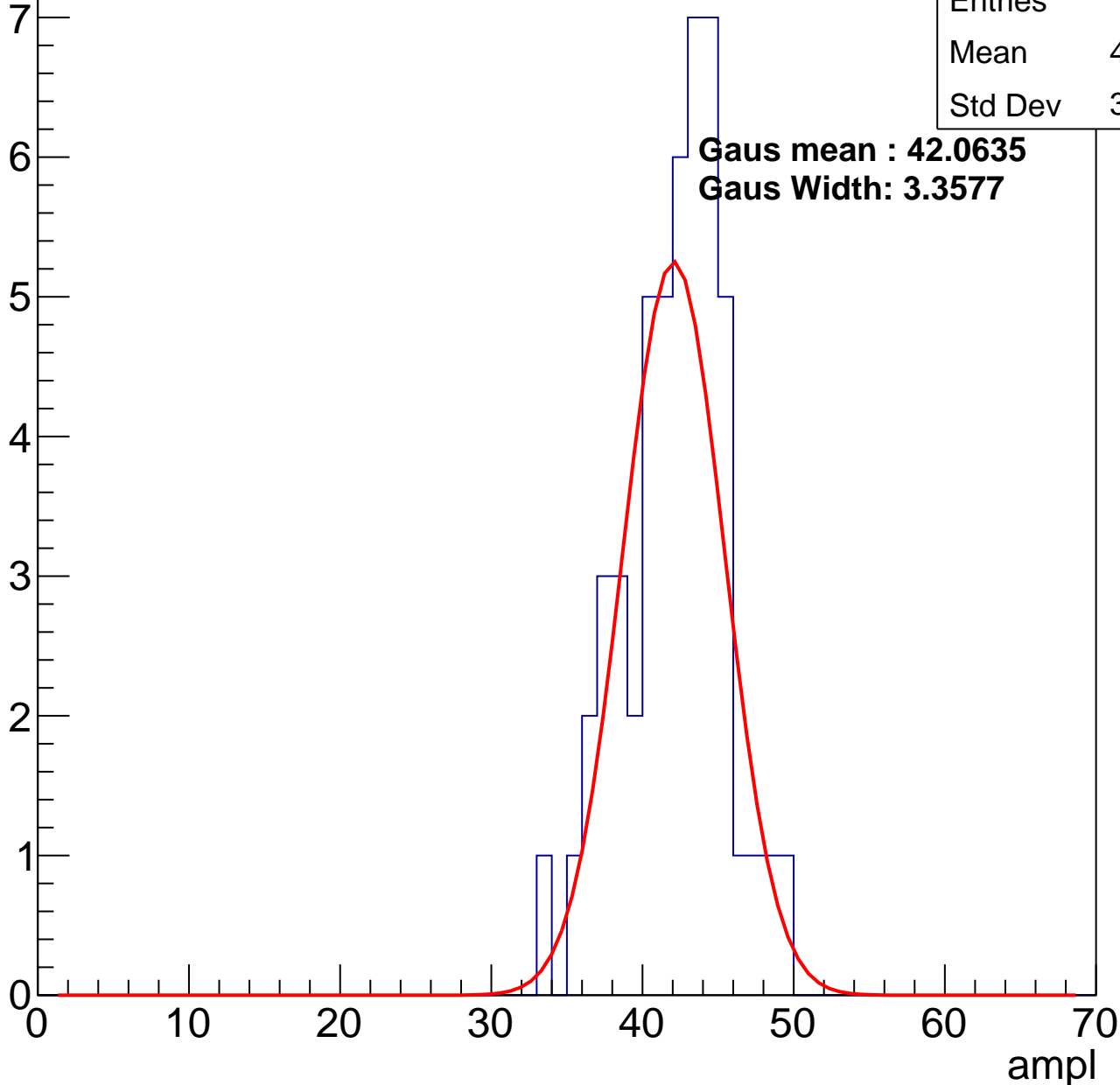
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.65
Std Dev	3.336

Gaus mean : 42.0635

Gaus Width: 3.3577

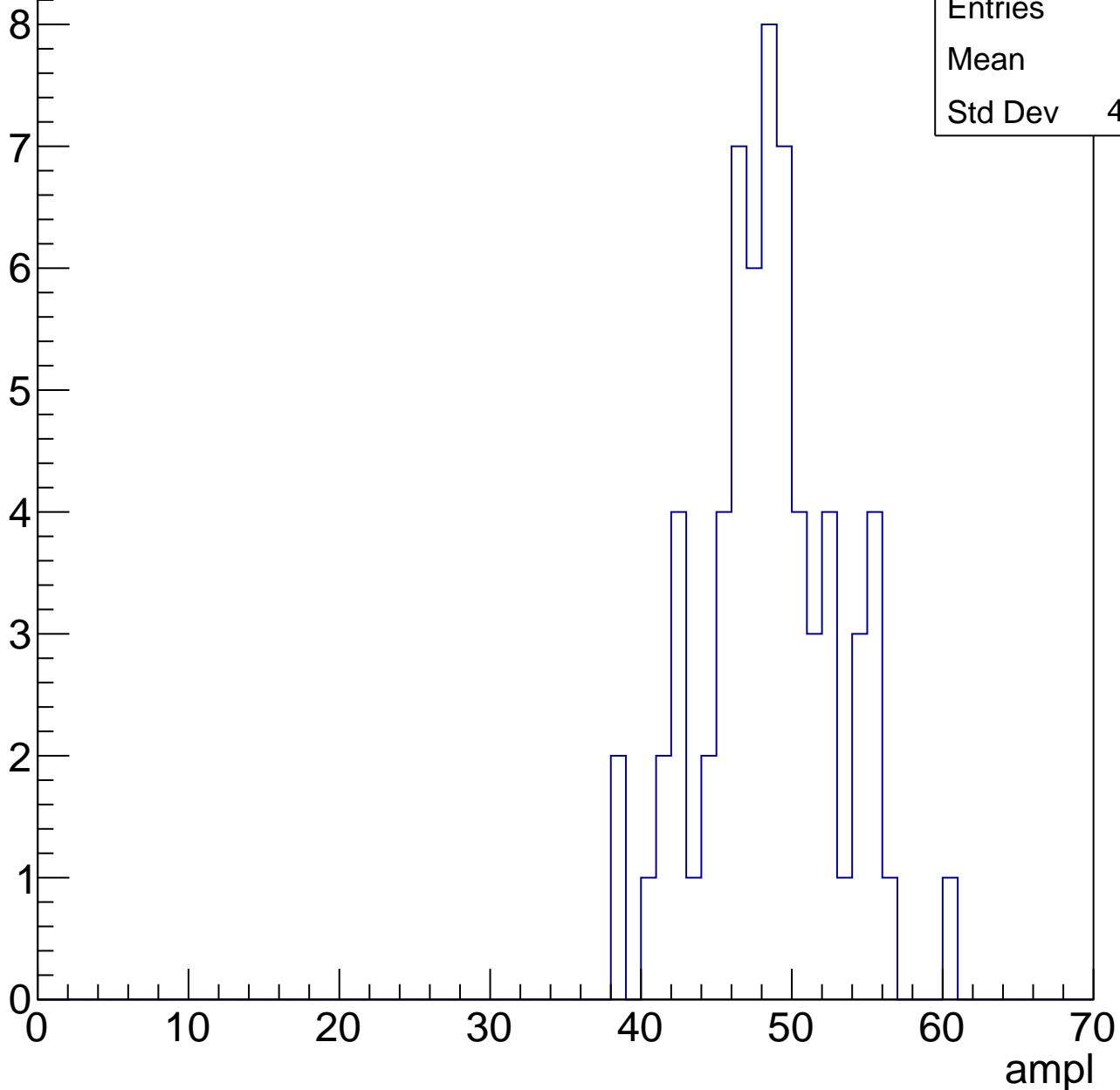


B1L103S, U23-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48
Std Dev	4.445

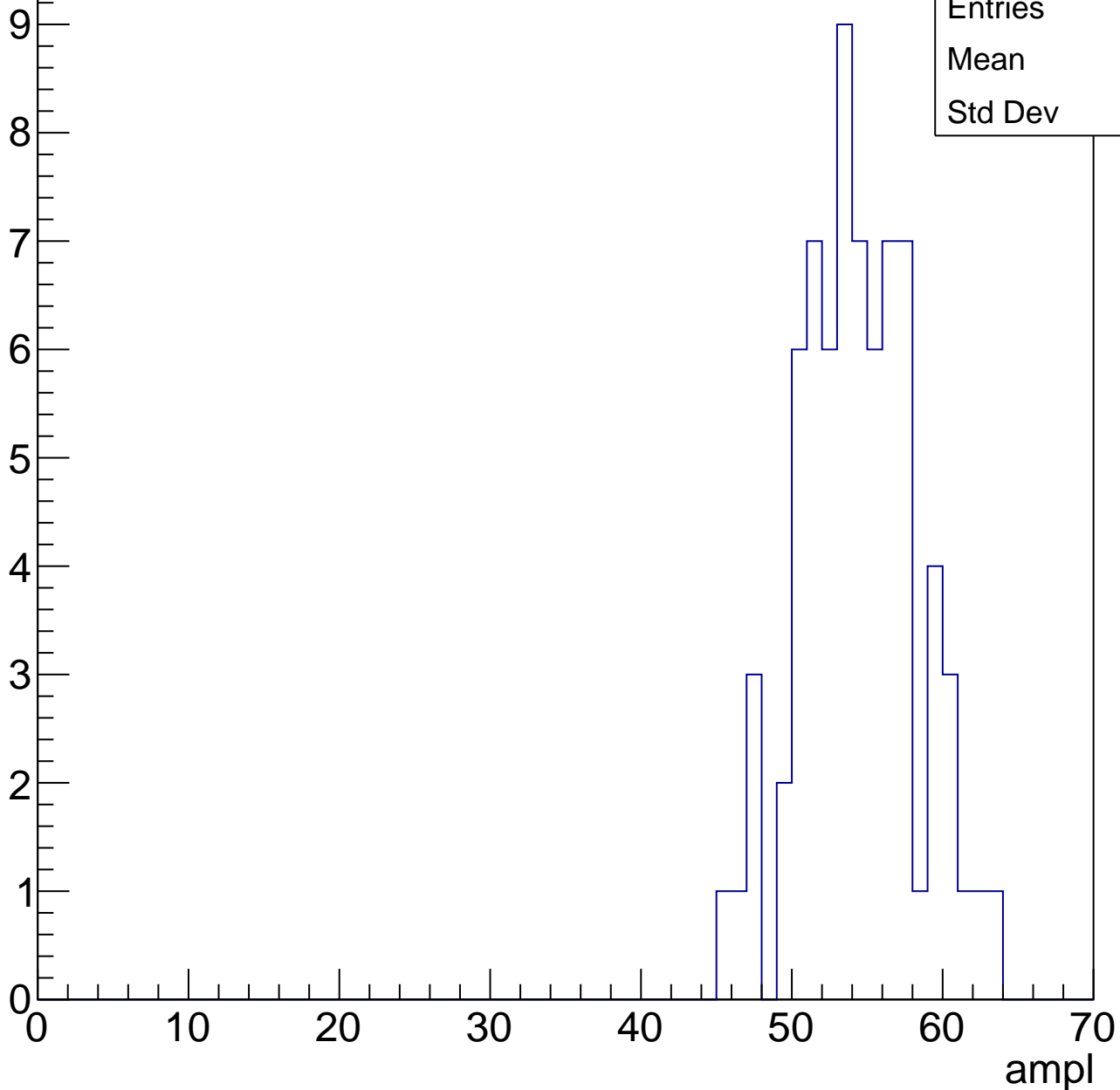


B1L103S, U23-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	53.9
Std Dev	3.79

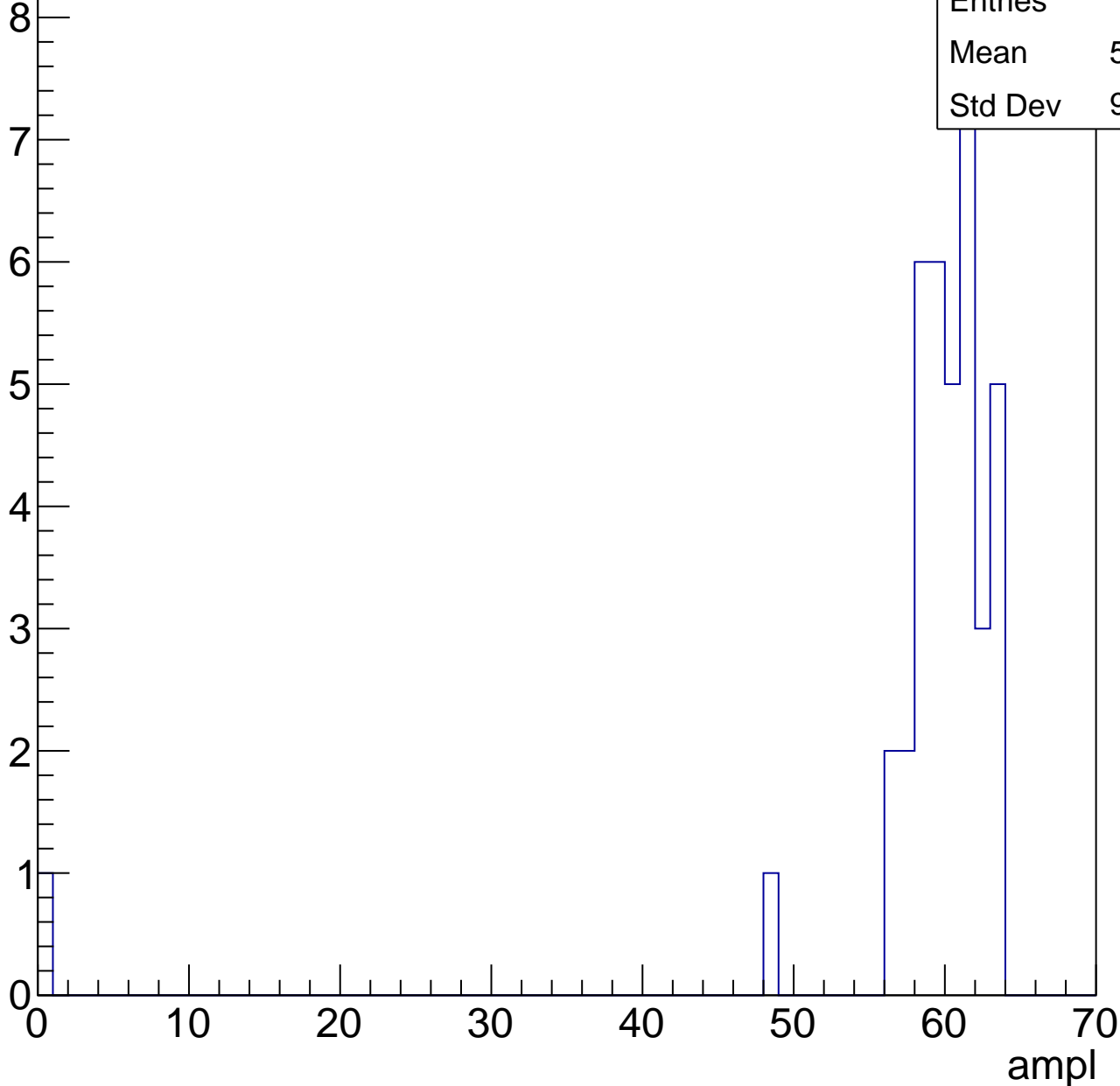


B1L103S, U23-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.08
Std Dev	9.799

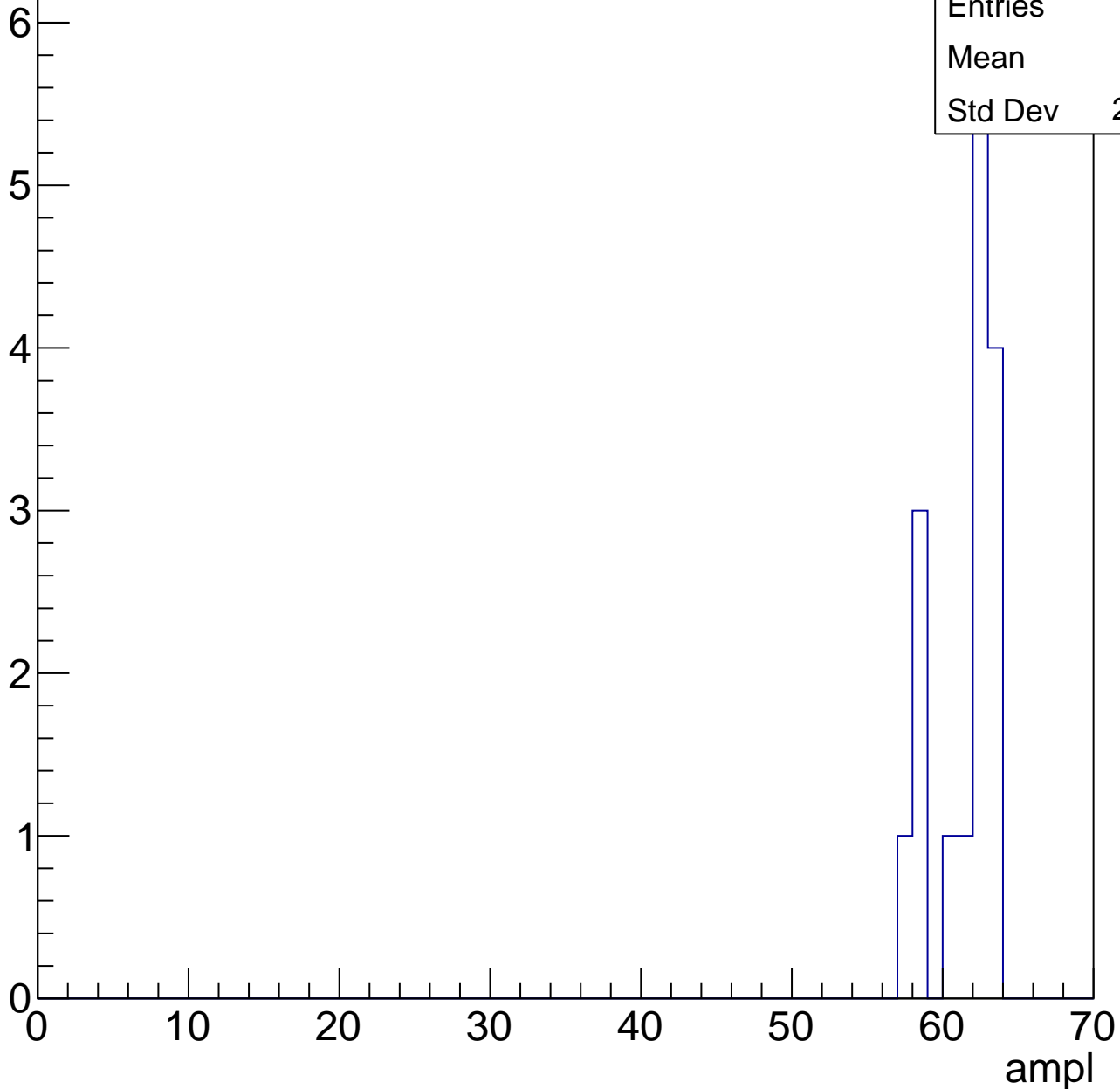


B1L103S, U23-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	61
Std Dev	2.031



B1L103S, U23-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch107, adc0

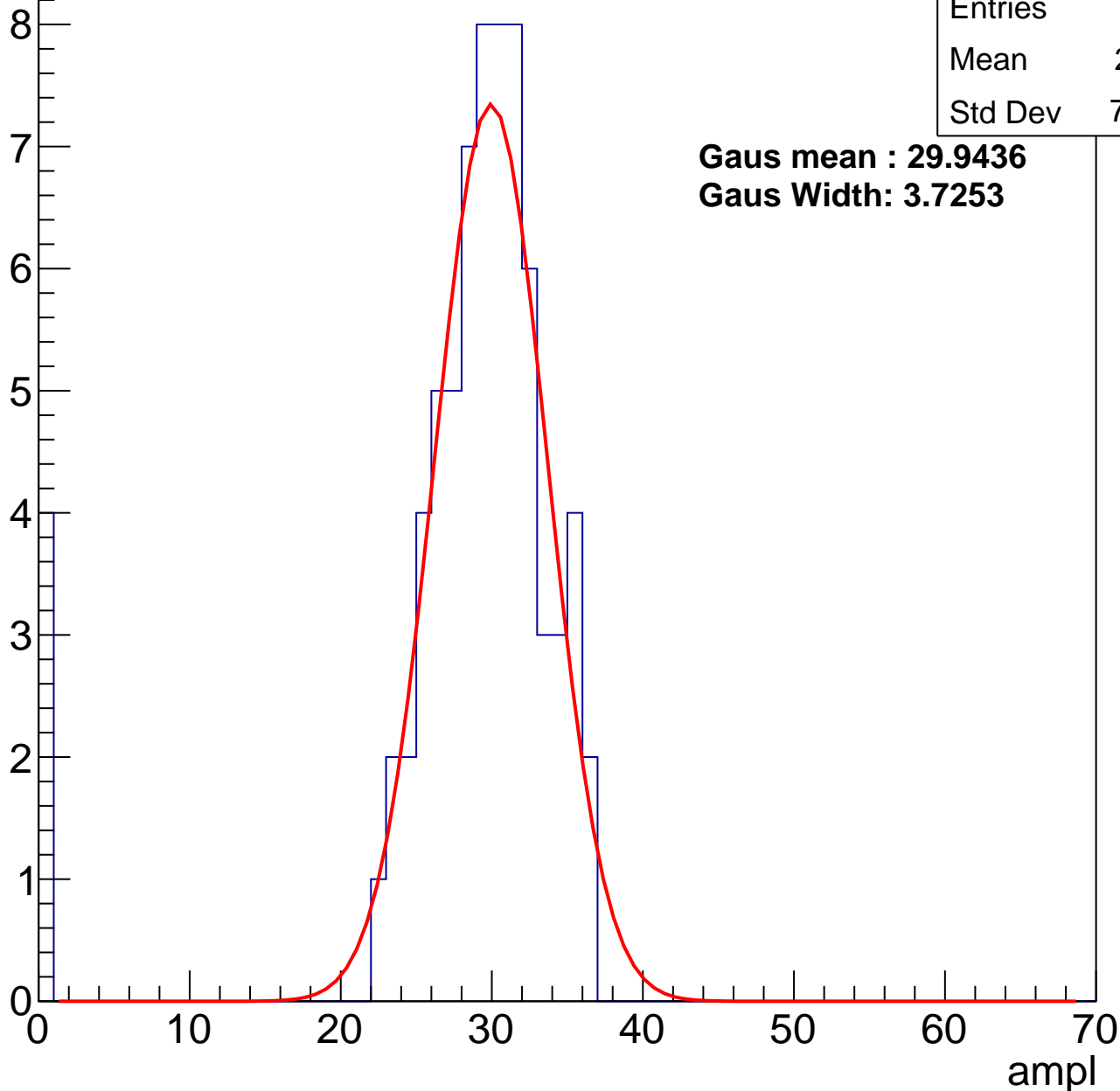
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.81
Std Dev	7.479

Gaus mean : 29.9436

Gaus Width: 3.7253



B1L103S, U23-ch107, adc1

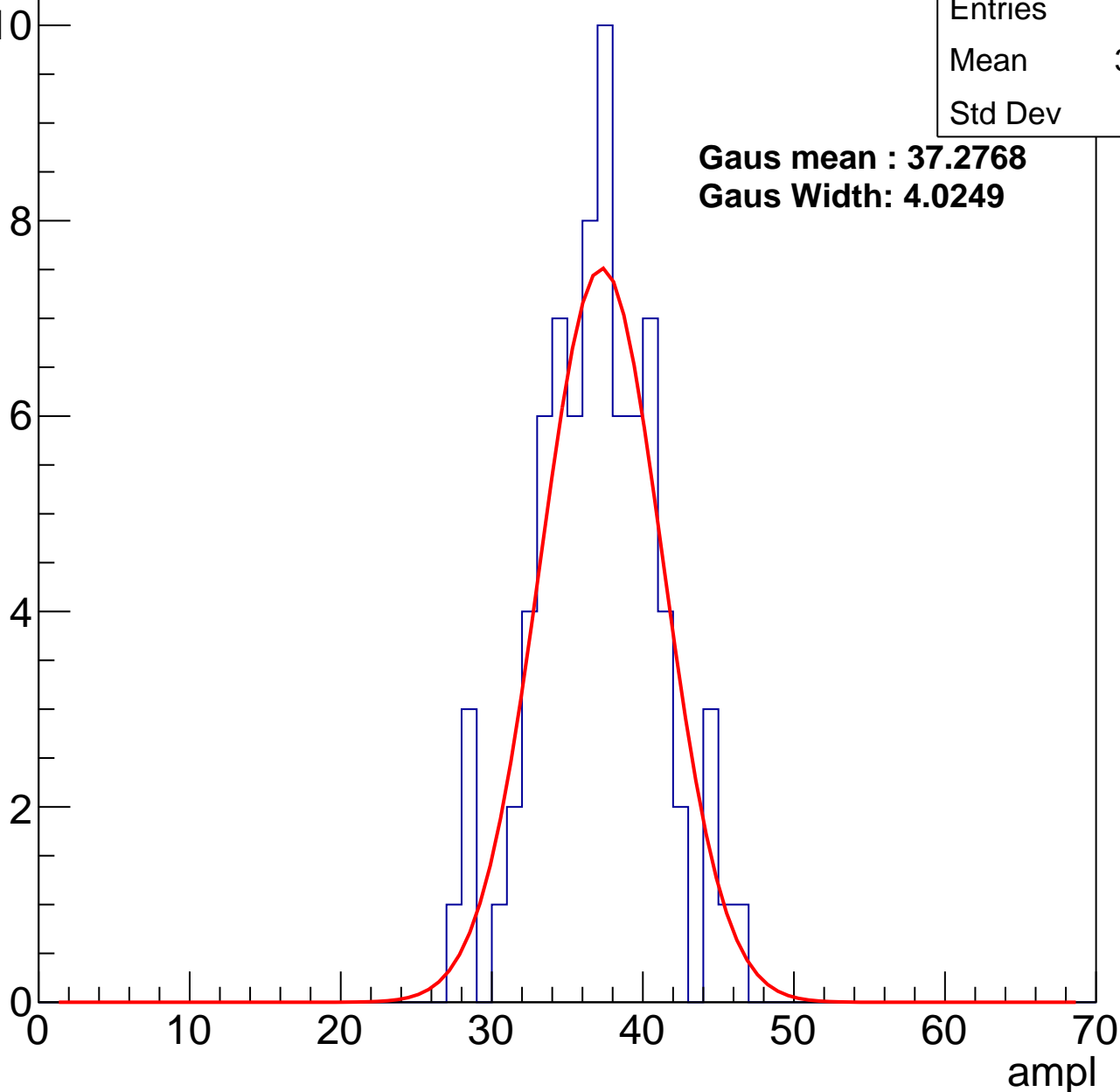
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	36.51
Std Dev	3.98

Gaus mean : 37.2768

Gaus Width: 4.0249



B1L103S, U23-ch107, adc2

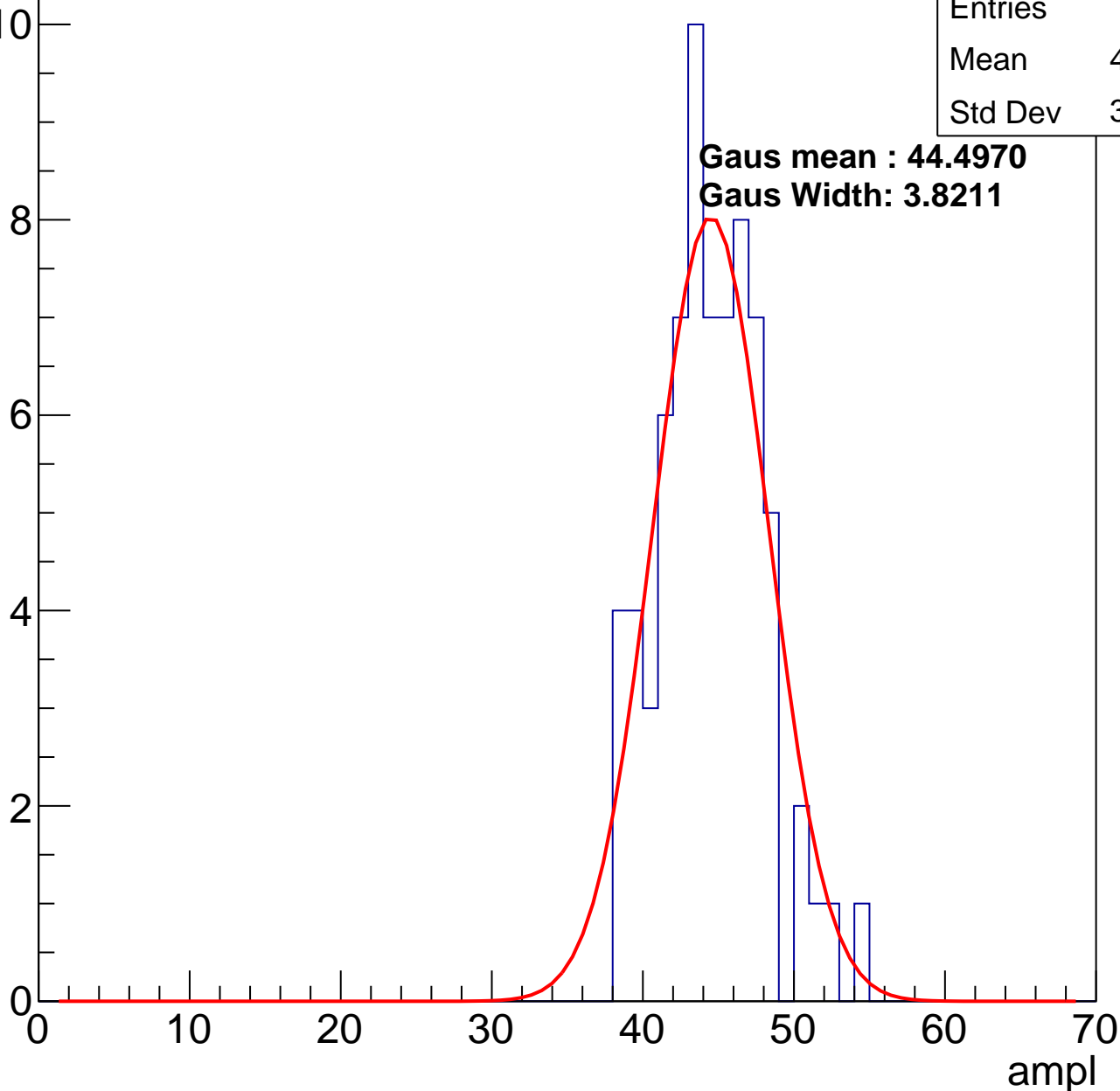
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	44.04
Std Dev	3.414

Gaus mean : 44.4970

Gaus Width: 3.8211

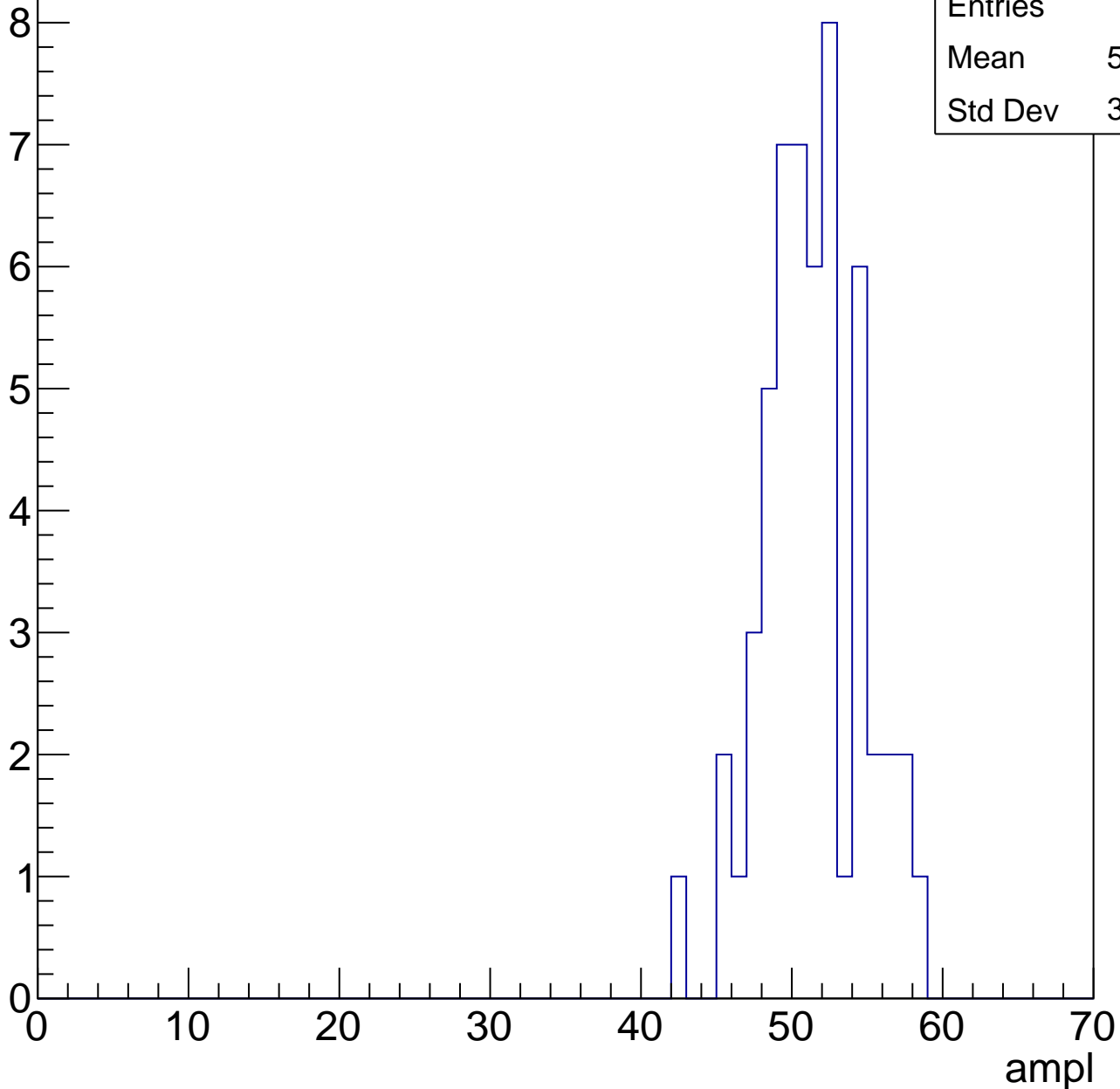


B1L103S, U23-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	50.83
Std Dev	3.259

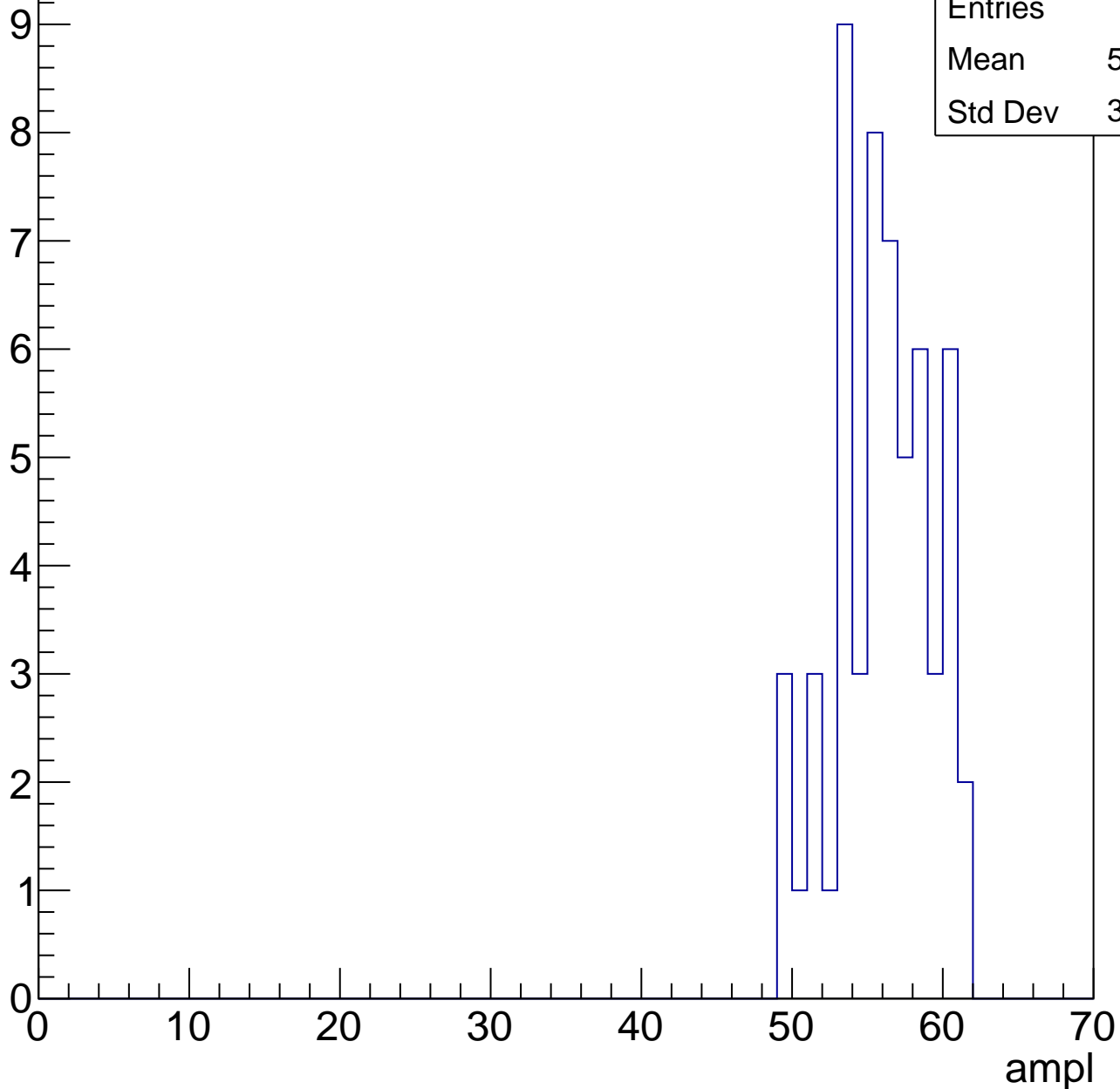


B1L103S, U23-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.53
Std Dev	3.146

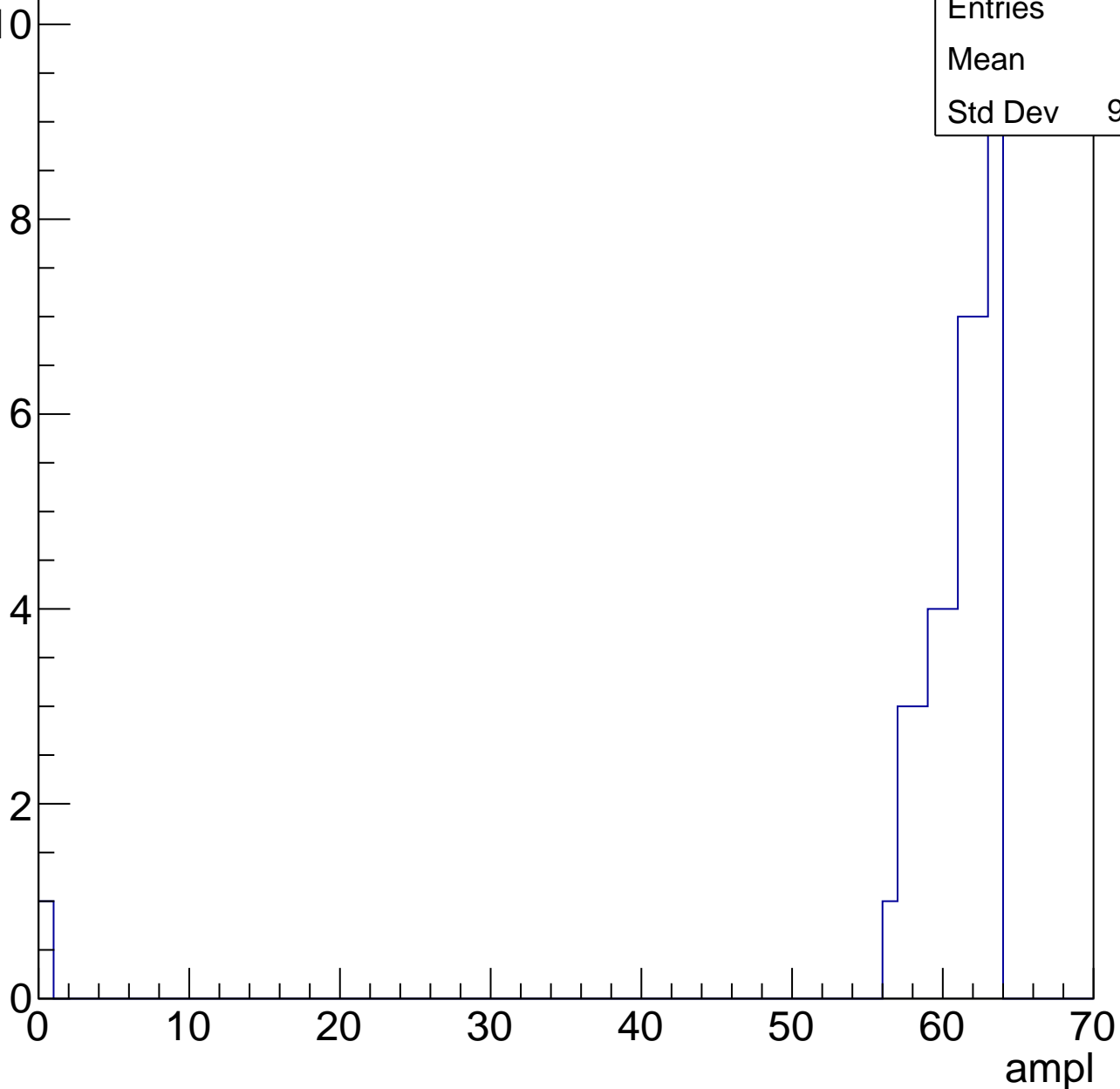


B1L103S, U23-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

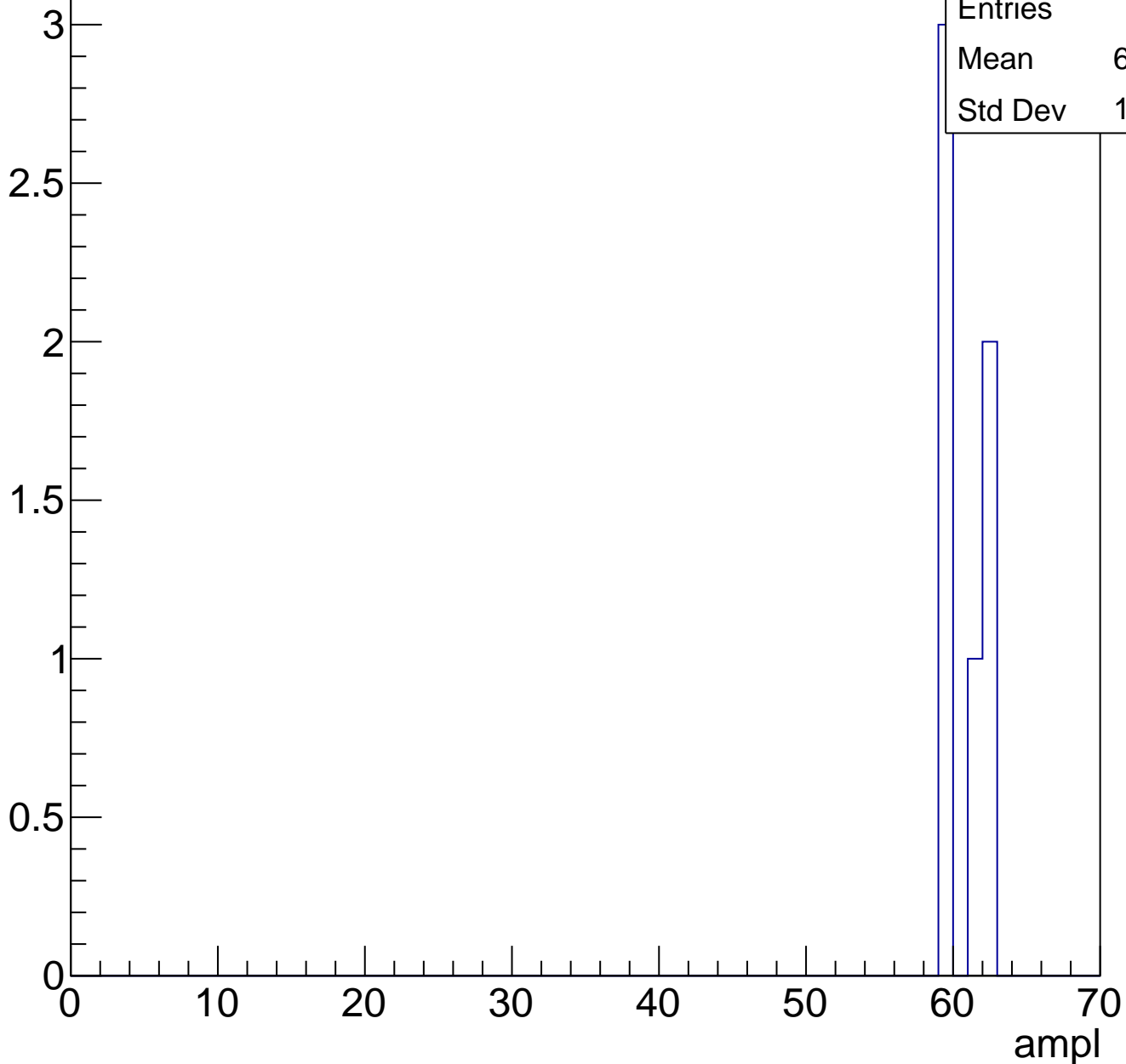
Entries	40
Mean	59.2
Std Dev	9.693



B1L103S, U23-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L103S, U23-ch108, adc0

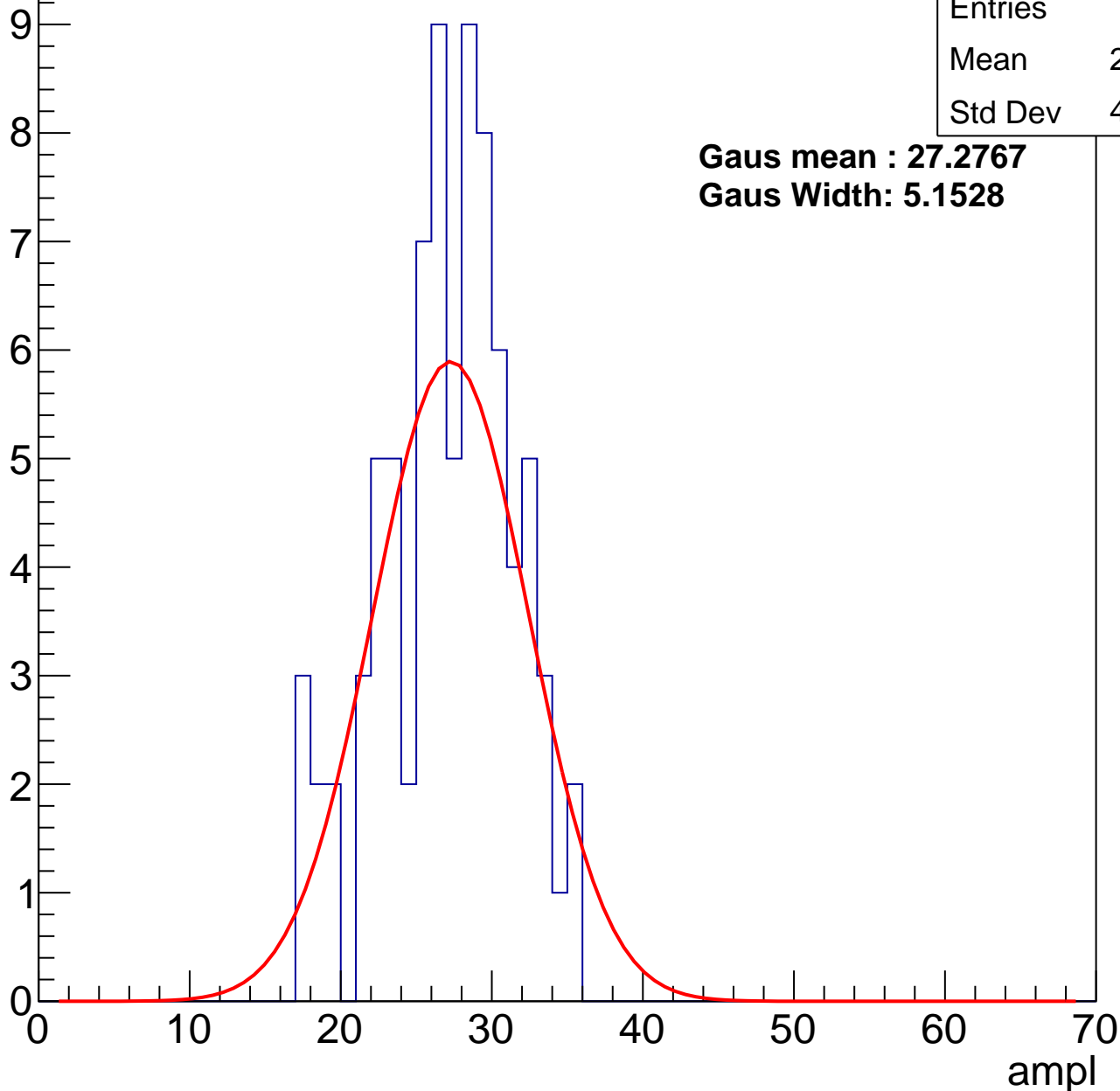
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	26.62
Std Dev	4.325

Gaus mean : 27.2767

Gaus Width: 5.1528



B1L103S, U23-ch108, adc1

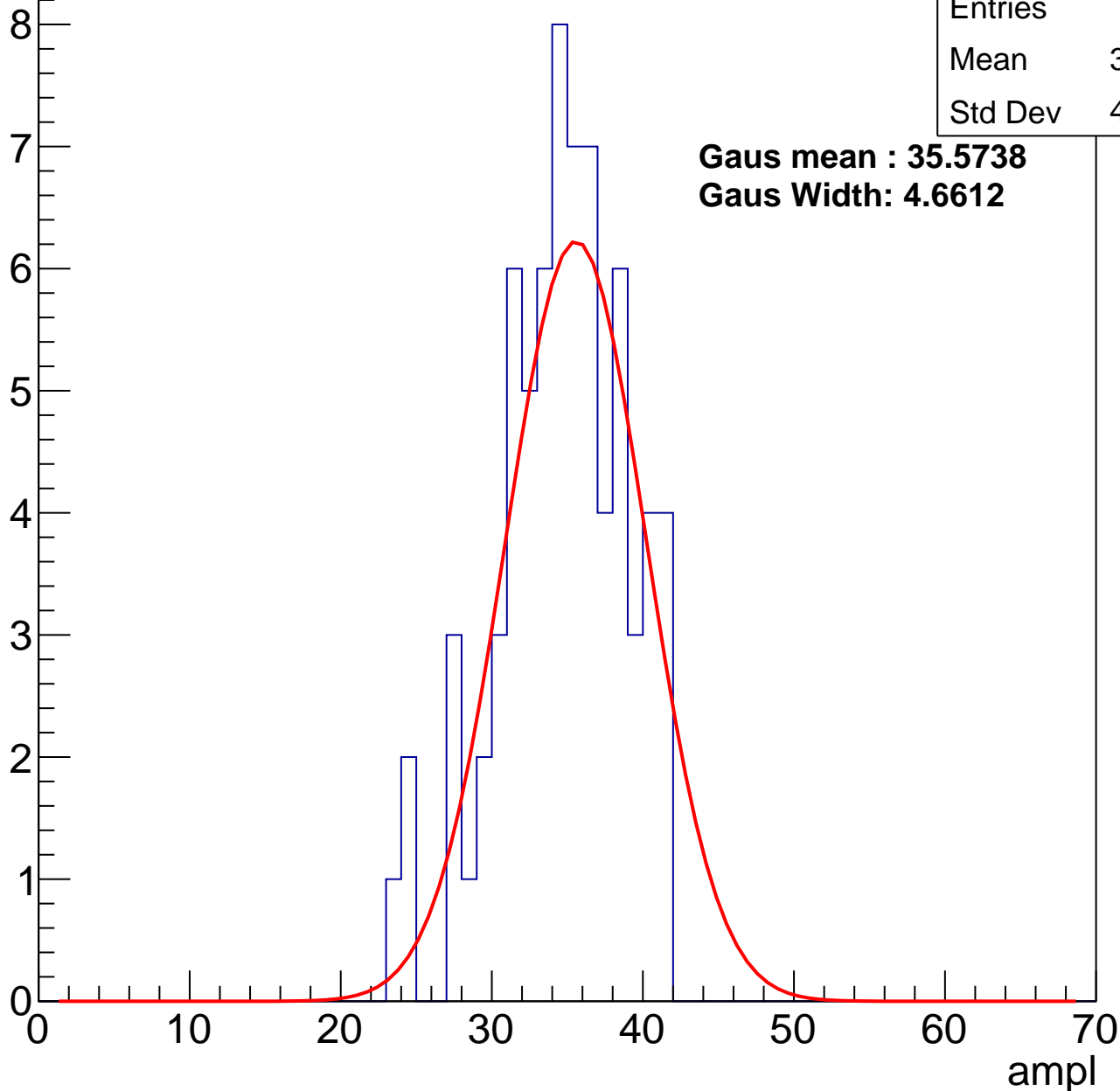
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	34.14
Std Dev	4.188

Gaus mean : 35.5738

Gaus Width: 4.6612



B1L103S, U23-ch108, adc2

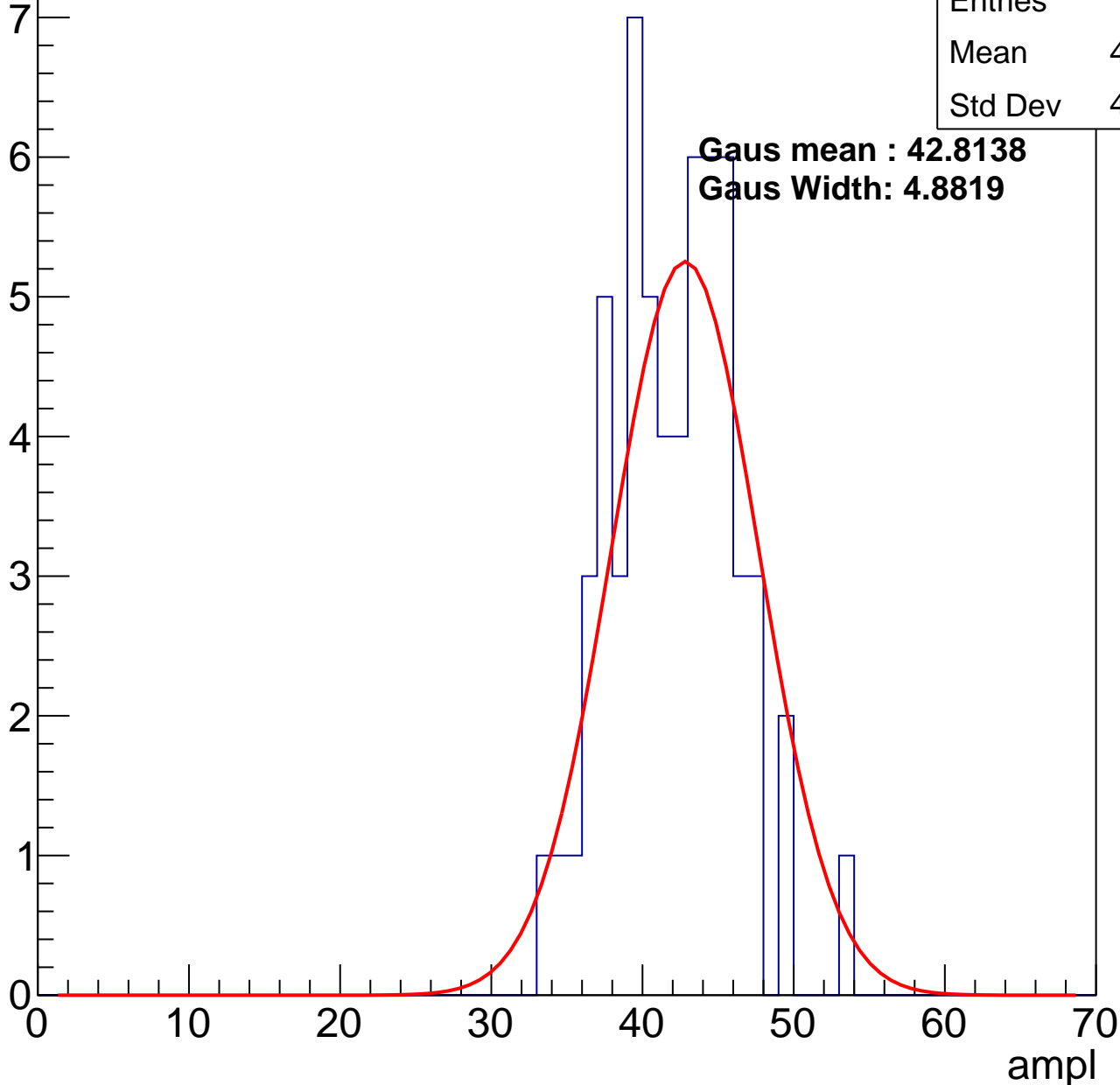
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.57
Std Dev	4.002

Gaus mean : 42.8138

Gaus Width: 4.8819

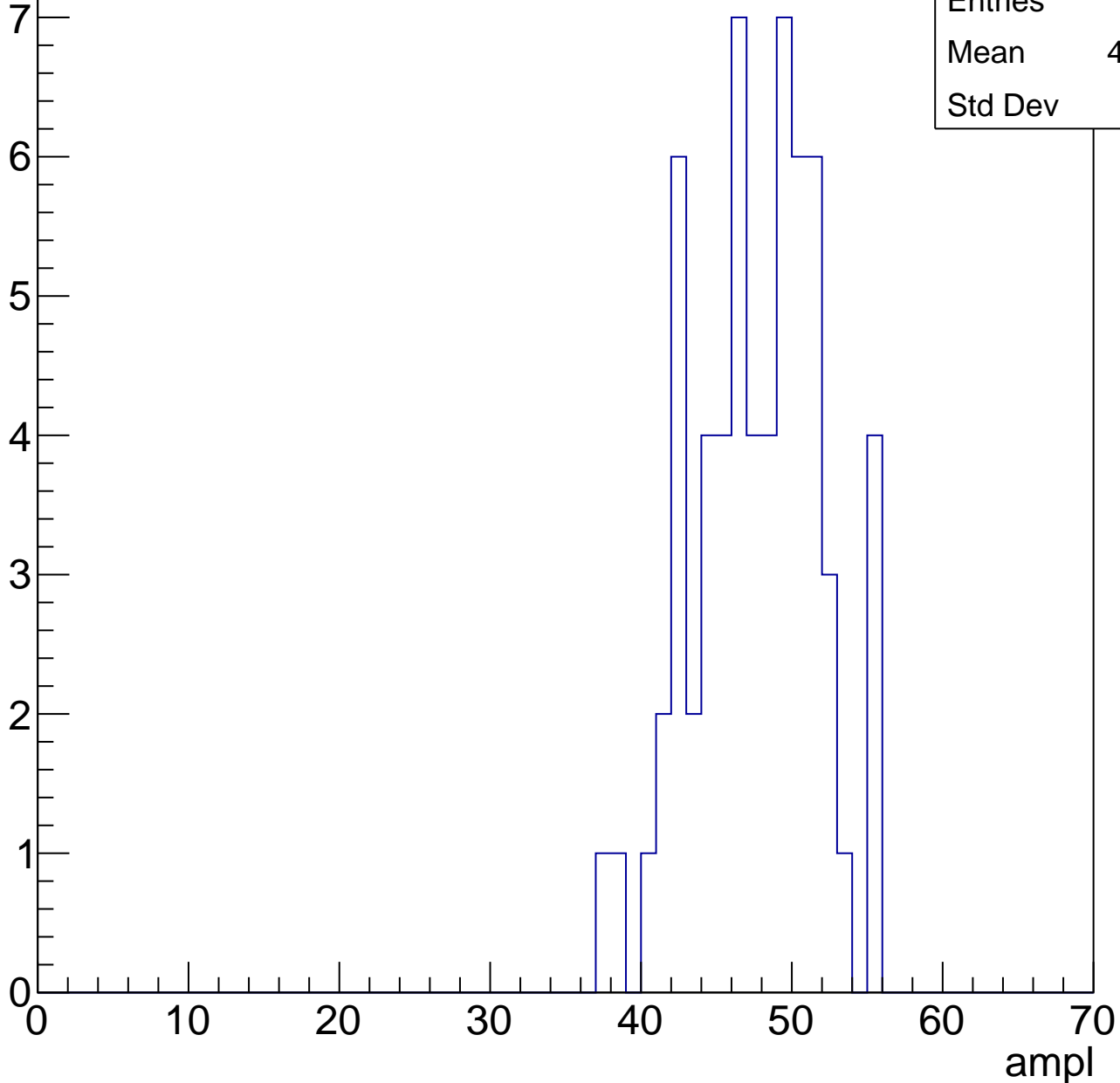


B1L103S, U23-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	47.16
Std Dev	4.16

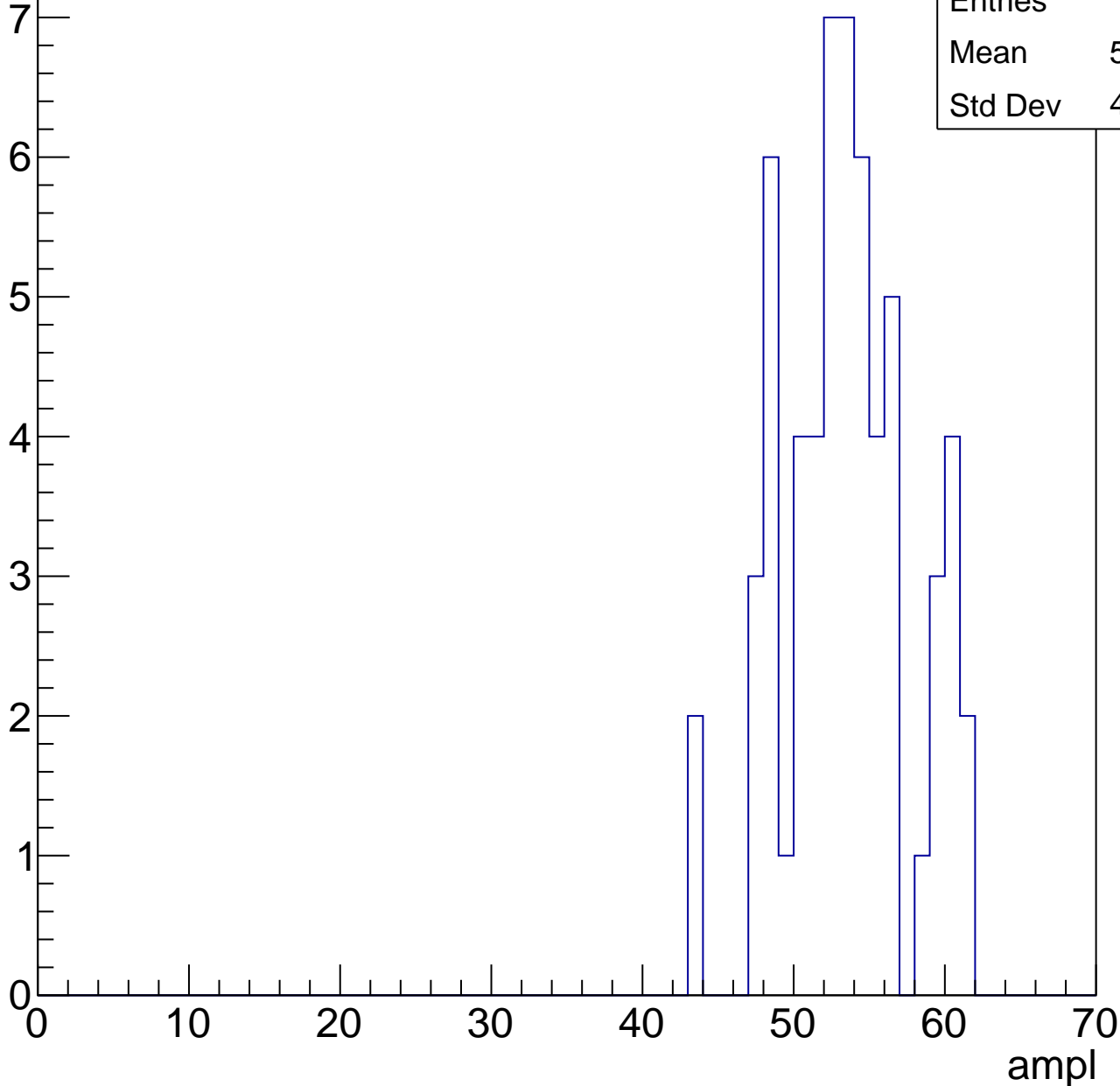


B1L103S, U23-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

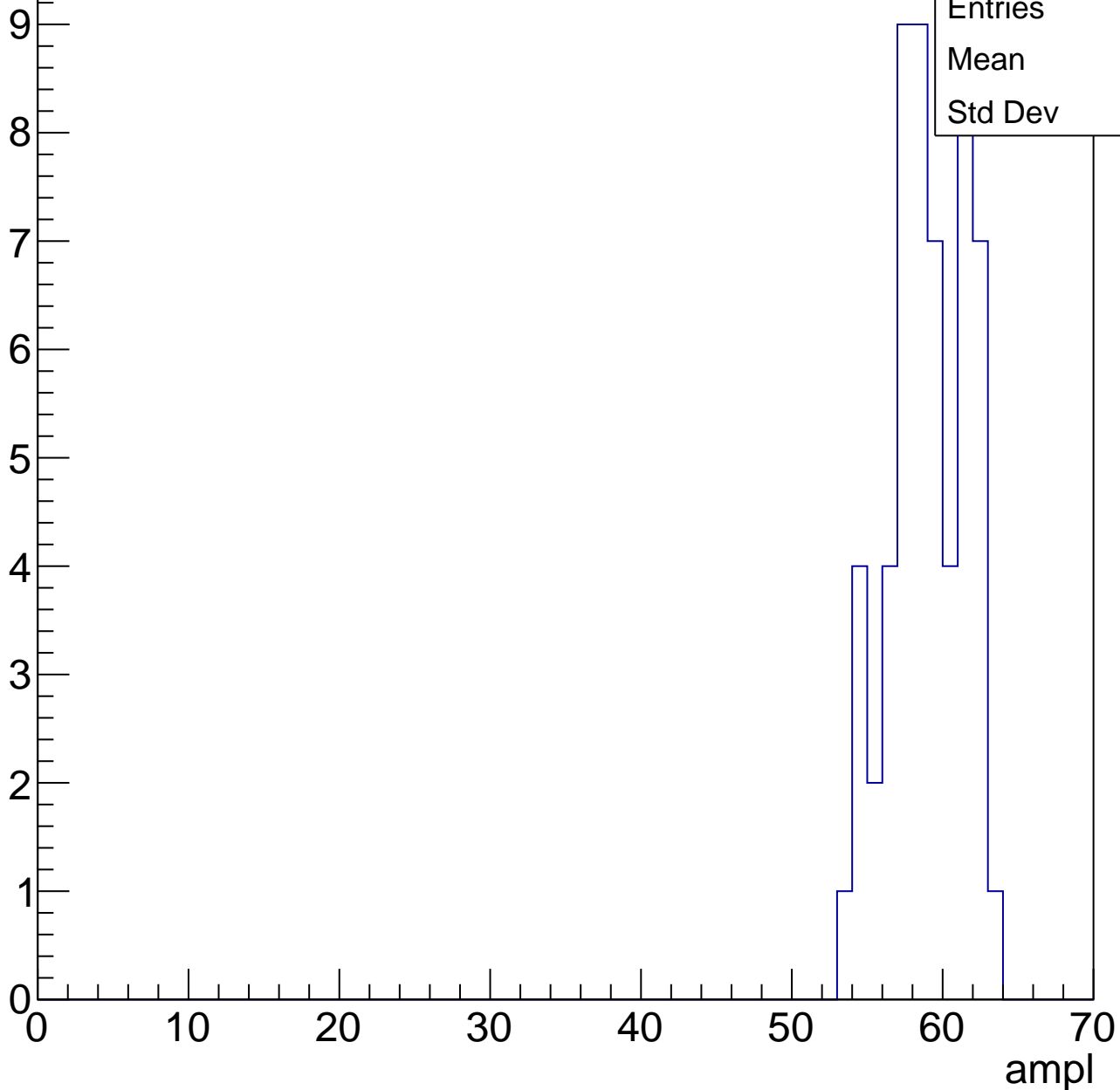
Entries	59
Mean	52.95
Std Dev	4.232



B1L103S, U23-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



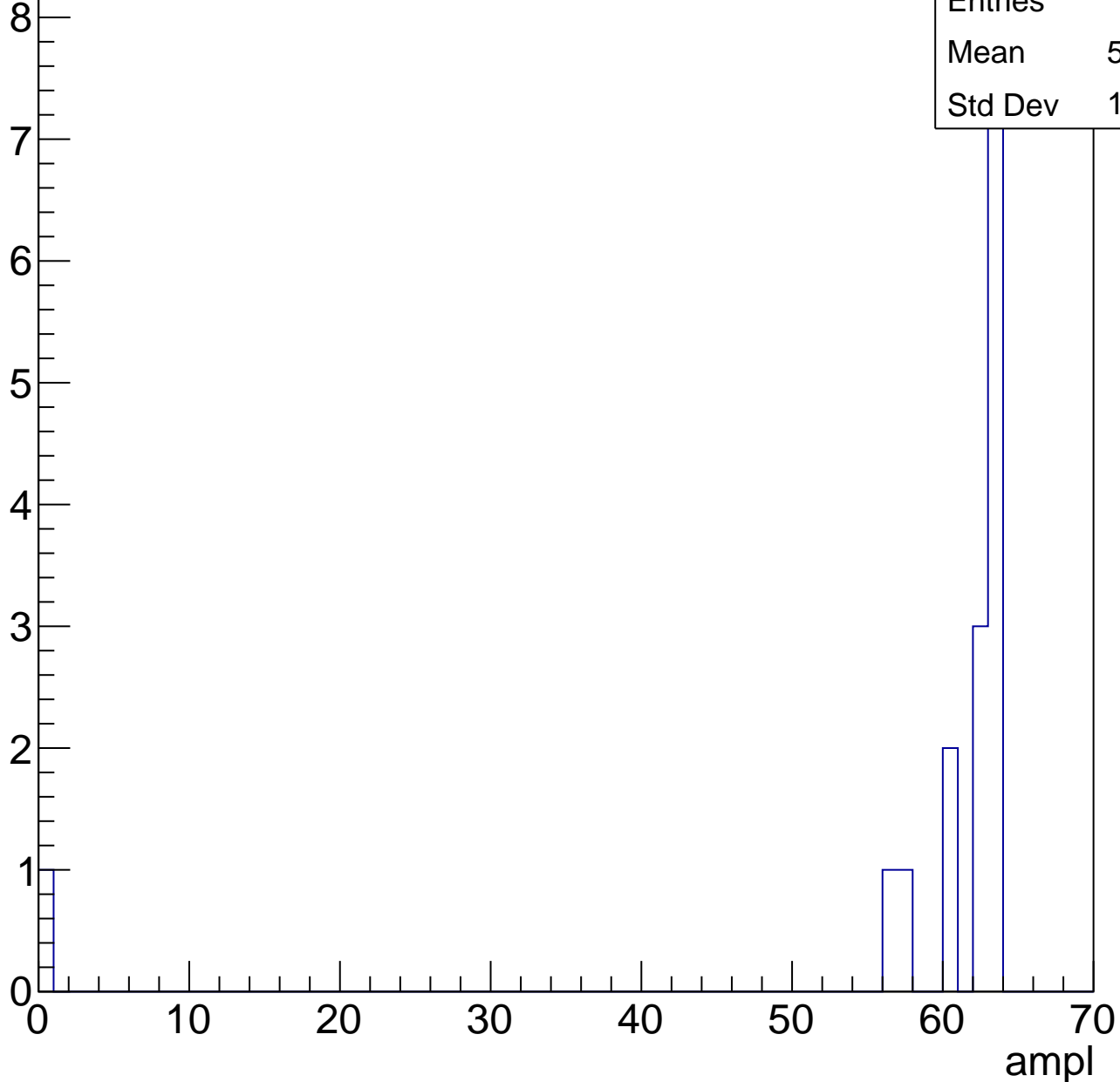
Entries	56
Mean	58.5
Std Dev	2.5

B1L103S, U23-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	57.69
Std Dev	15.05



B1L103S, U23-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch109, adc0

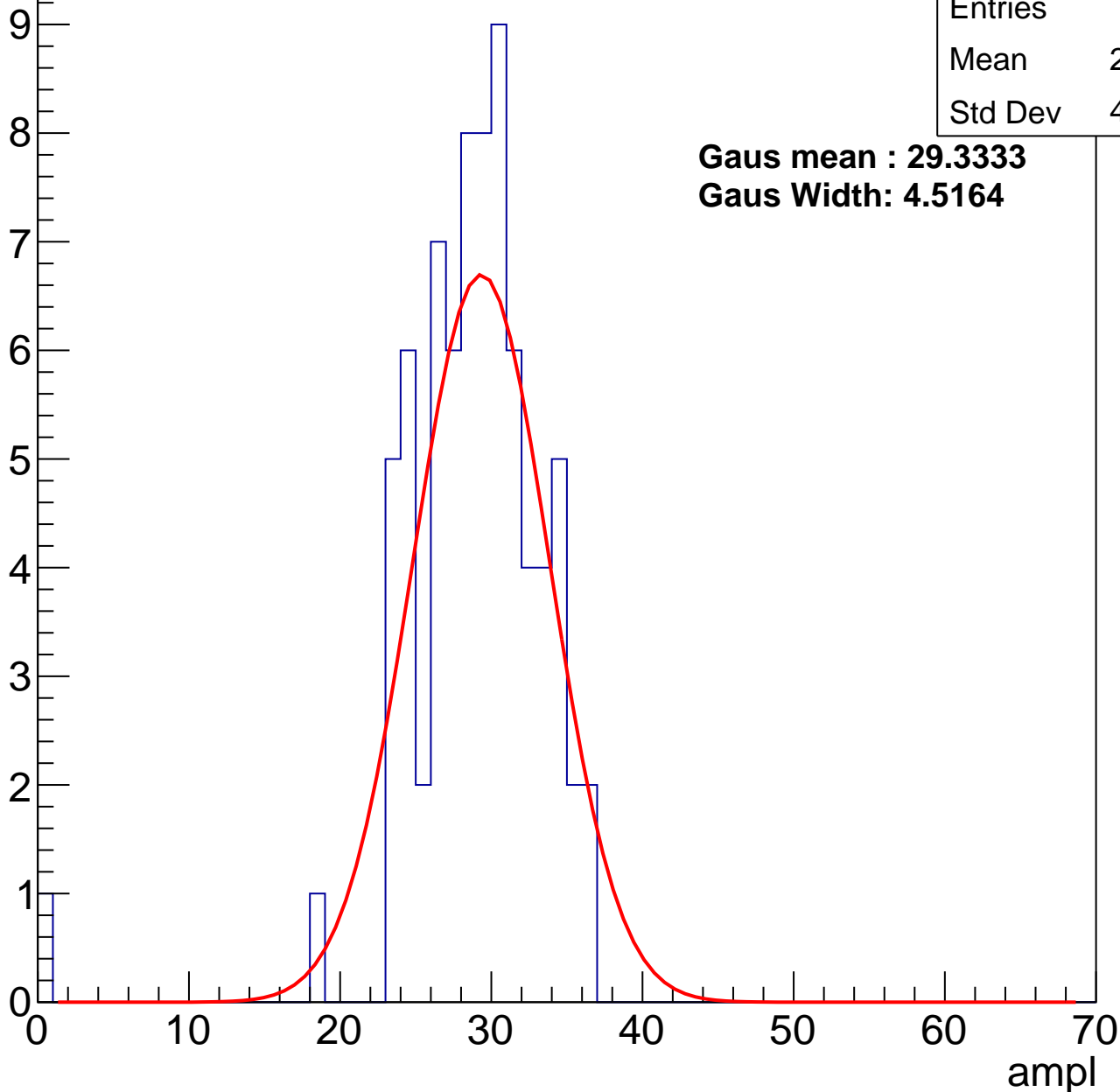
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.36
Std Dev	4.887

Gaus mean : 29.3333

Gaus Width: 4.5164



B1L103S, U23-ch109, adc1

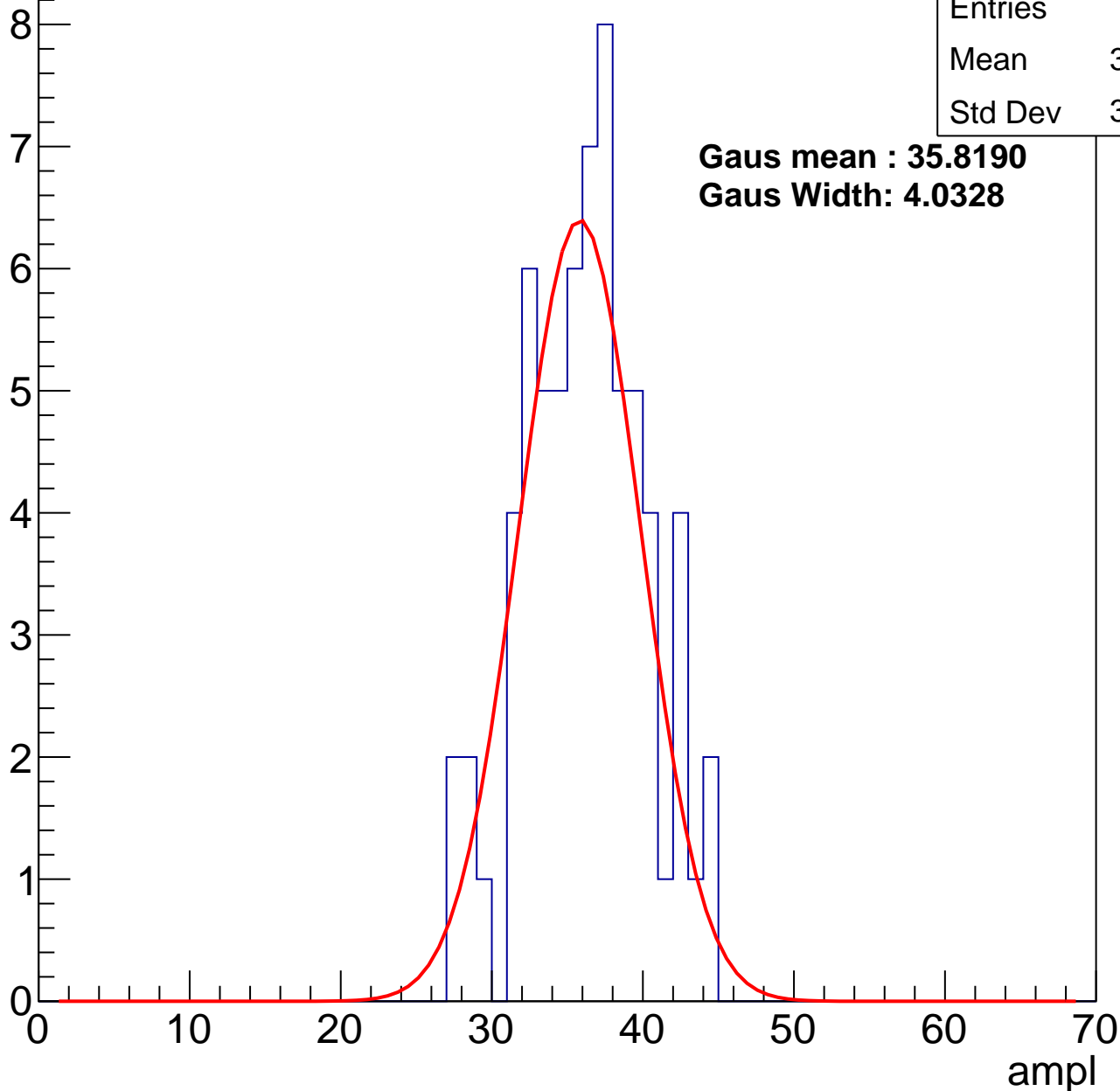
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.78
Std Dev	3.996

Gaus mean : 35.8190

Gaus Width: 4.0328



B1L103S, U23-ch109, adc2

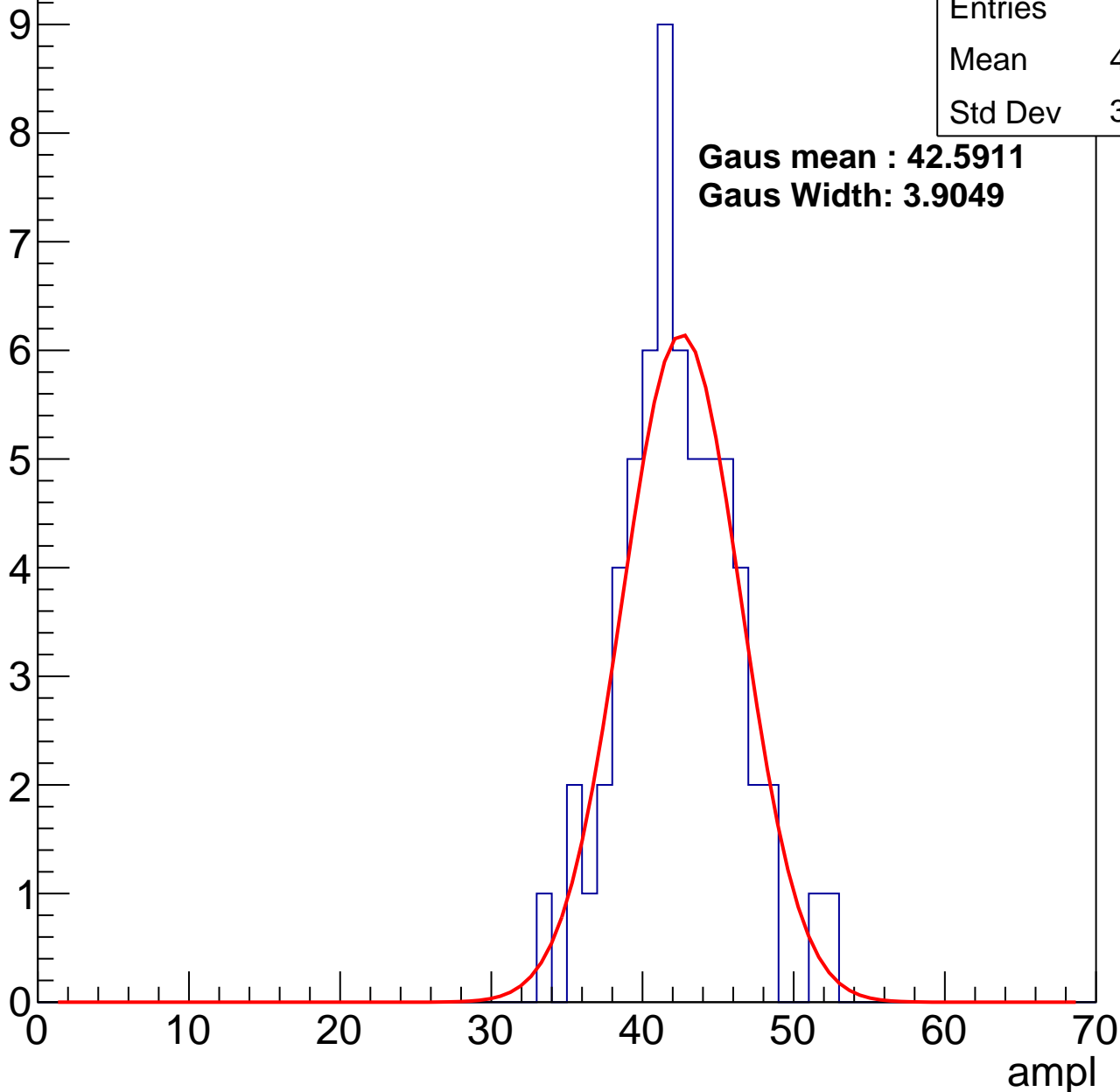
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.93
Std Dev	3.719

Gaus mean : 42.5911

Gaus Width: 3.9049

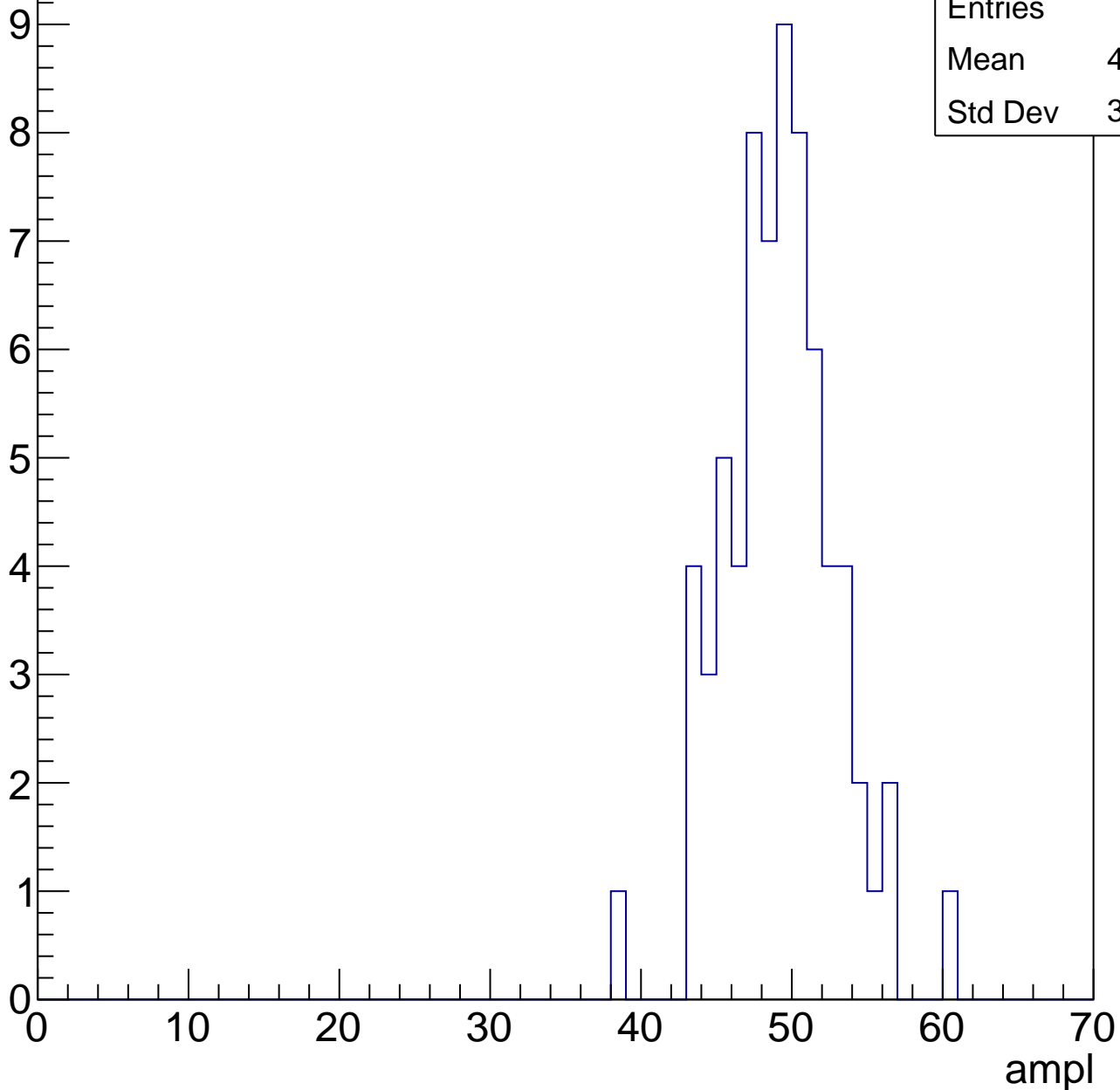


B1L103S, U23-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

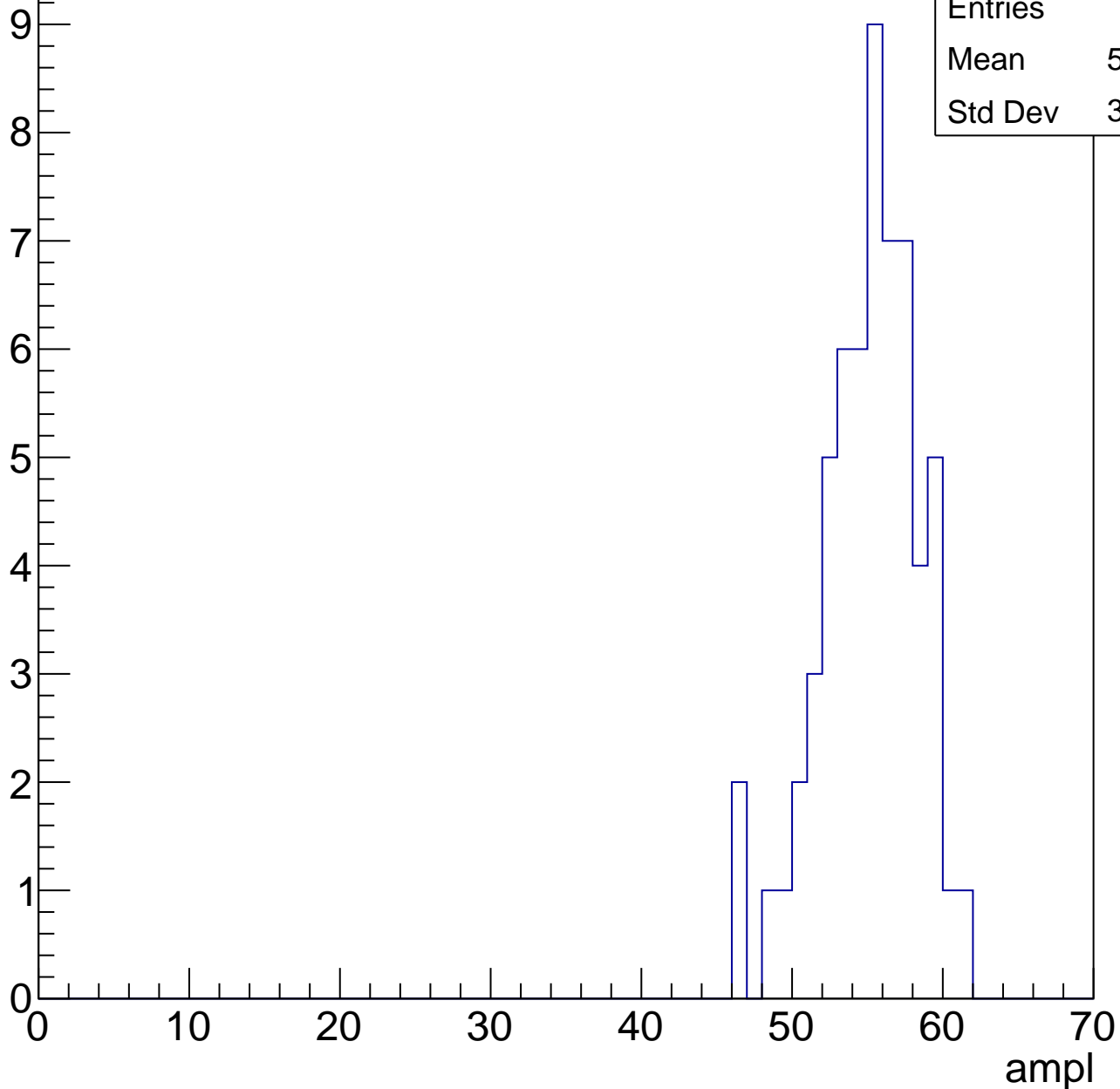
Entries	69
Mean	48.77
Std Dev	3.668



B1L103S, U23-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



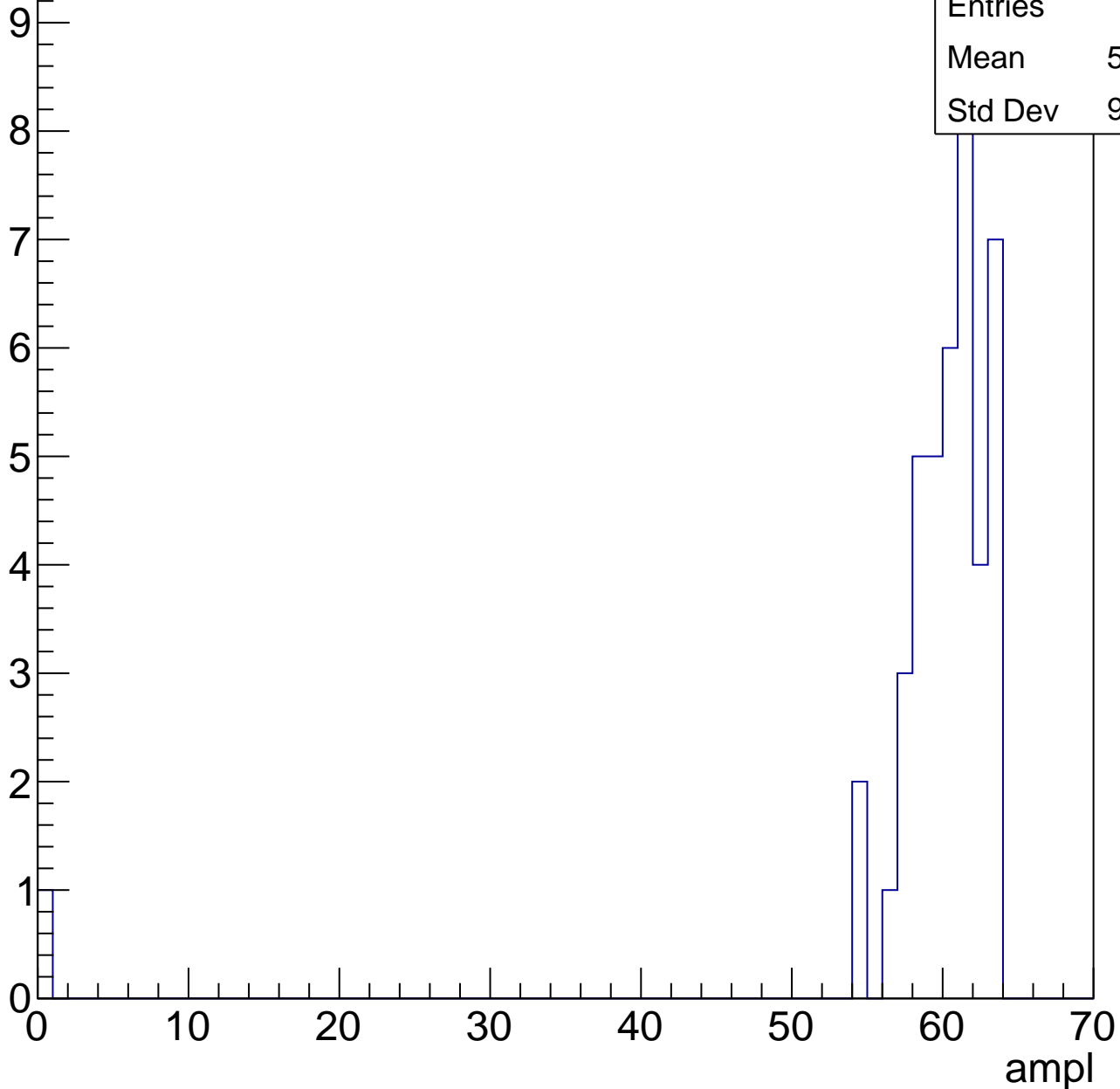
Entries	60
Mean	54.63
Std Dev	3.225

B1L103S, U23-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.56
Std Dev	9.324

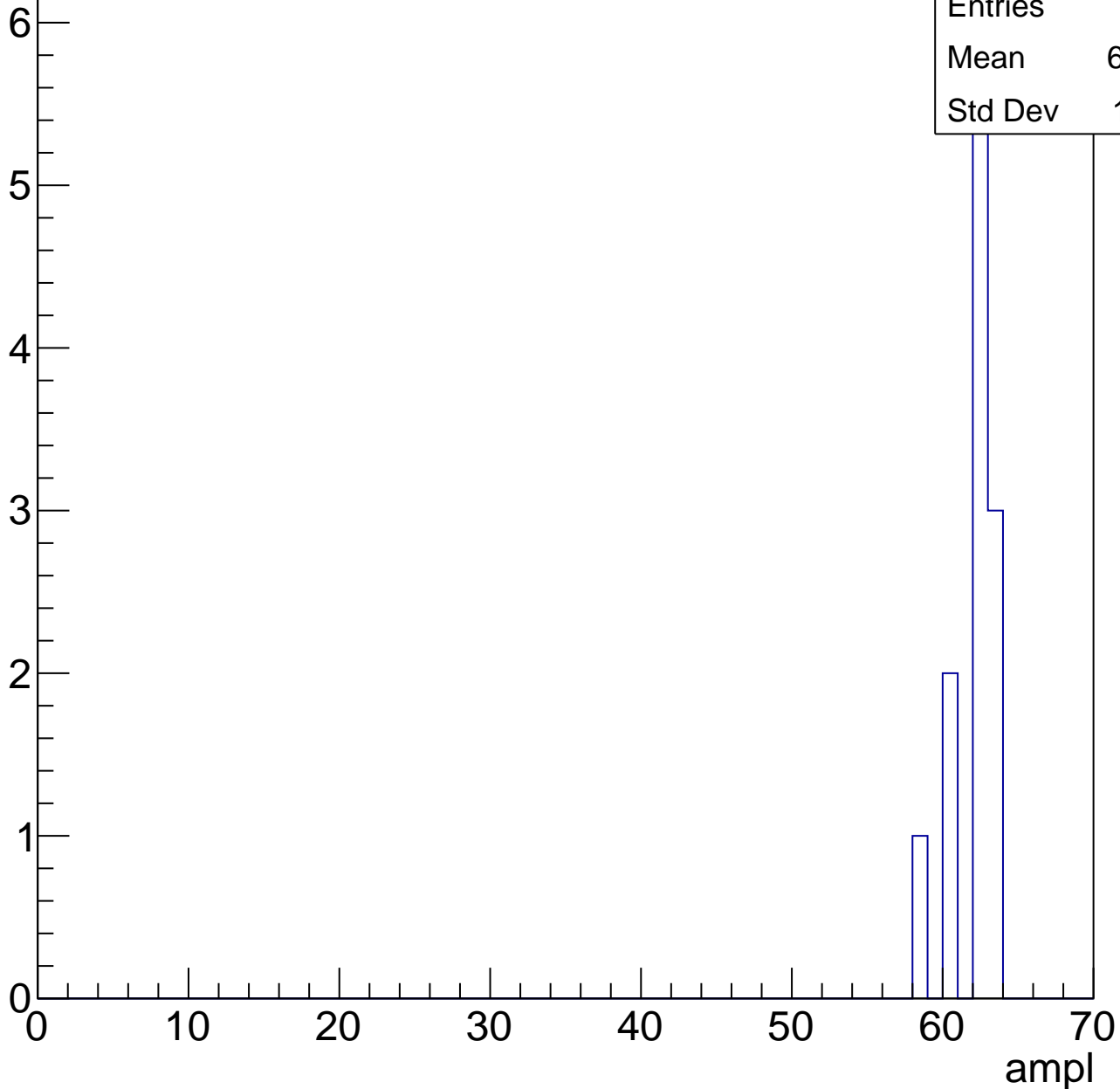


B1L103S, U23-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.441



B1L103S, U23-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch110, adc0

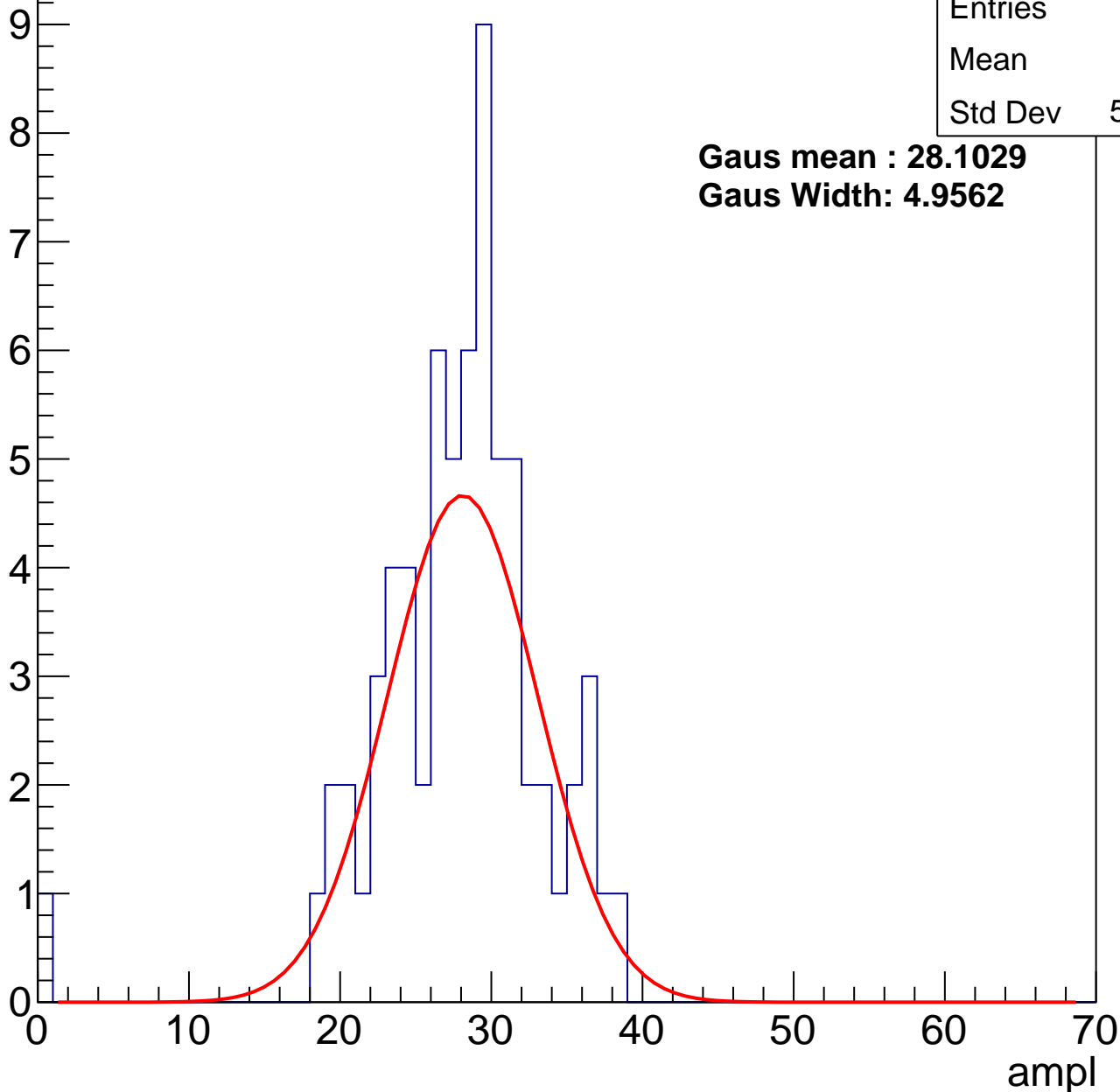
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.4
Std Dev	5.662

Gaus mean : 28.1029

Gaus Width: 4.9562



B1L103S, U23-ch110, adc1

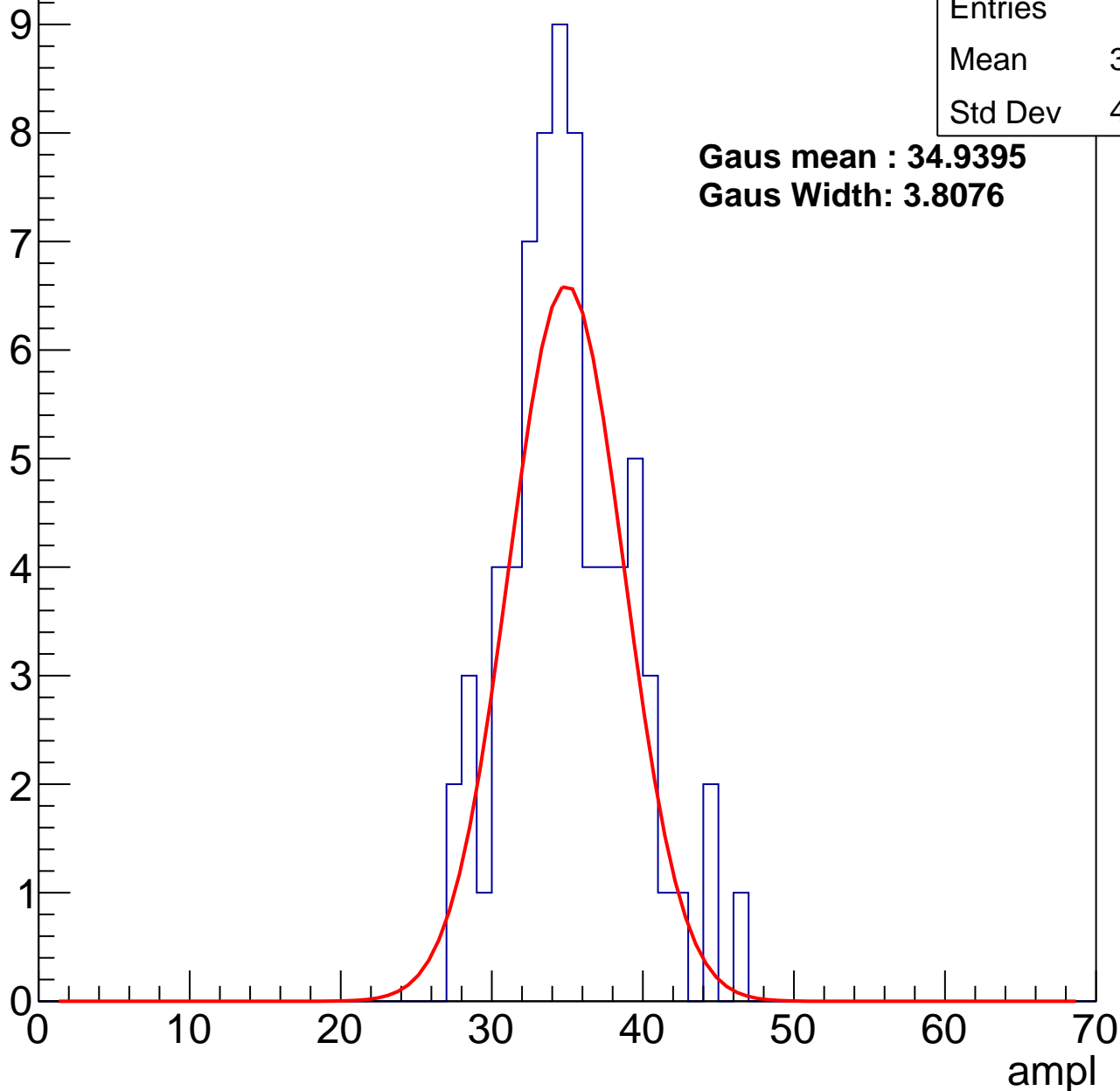
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	34.66
Std Dev	4.024

Gaus mean : 34.9395

Gaus Width: 3.8076



B1L103S, U23-ch110, adc2

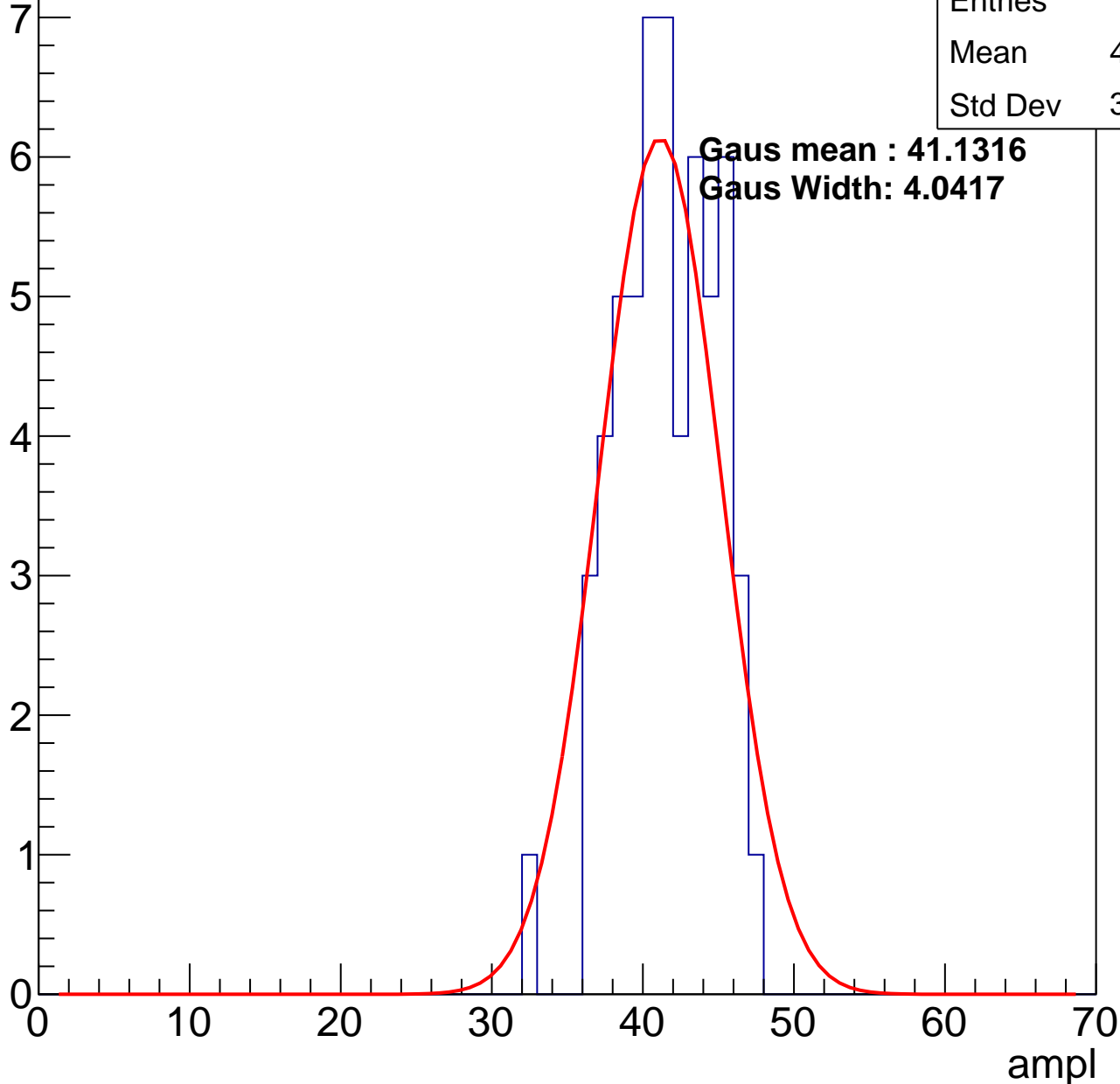
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.07
Std Dev	3.167

Gaus mean : 41.1316

Gaus Width: 4.0417

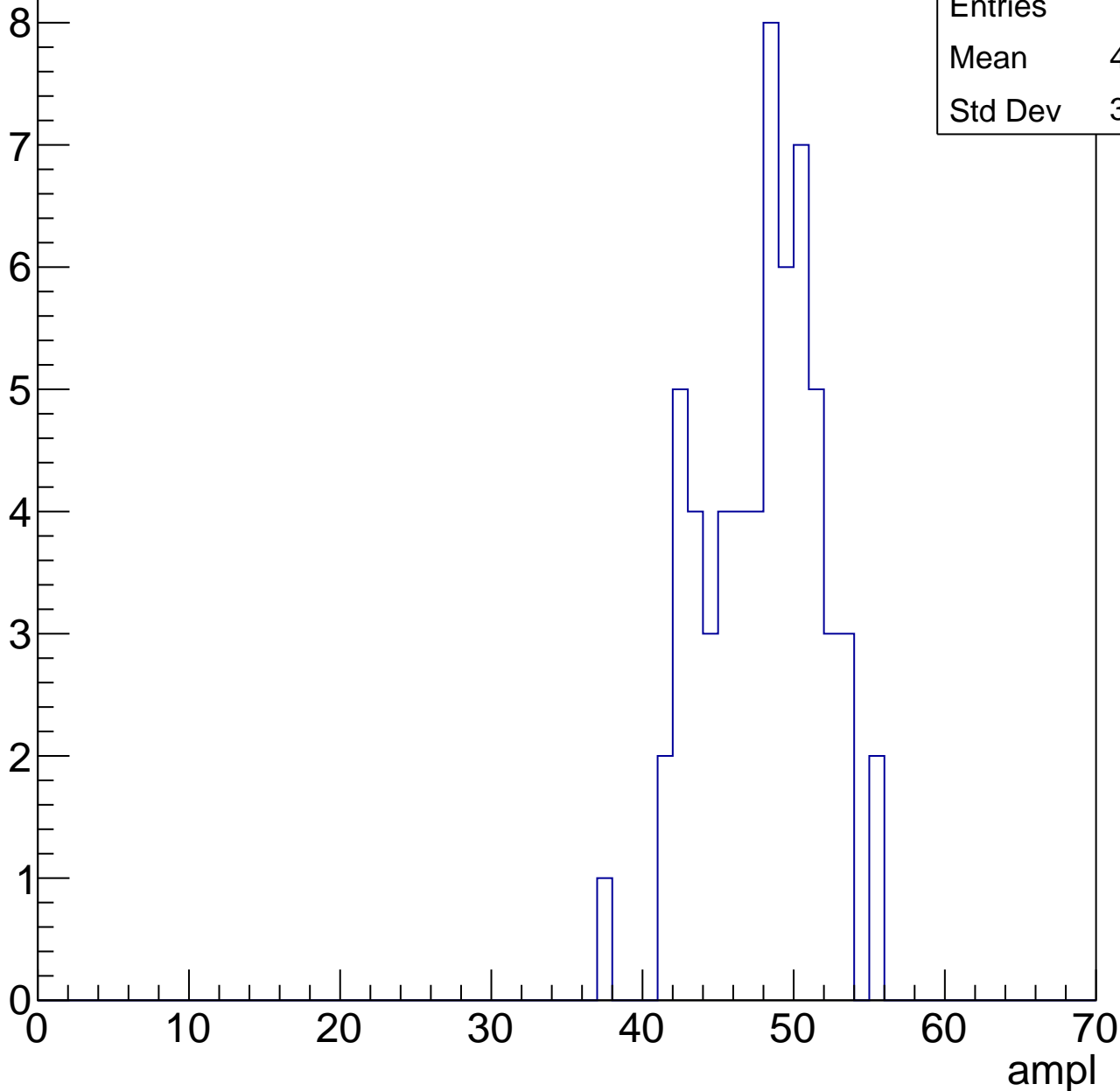


B1L103S, U23-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	47.43
Std Dev	3.804

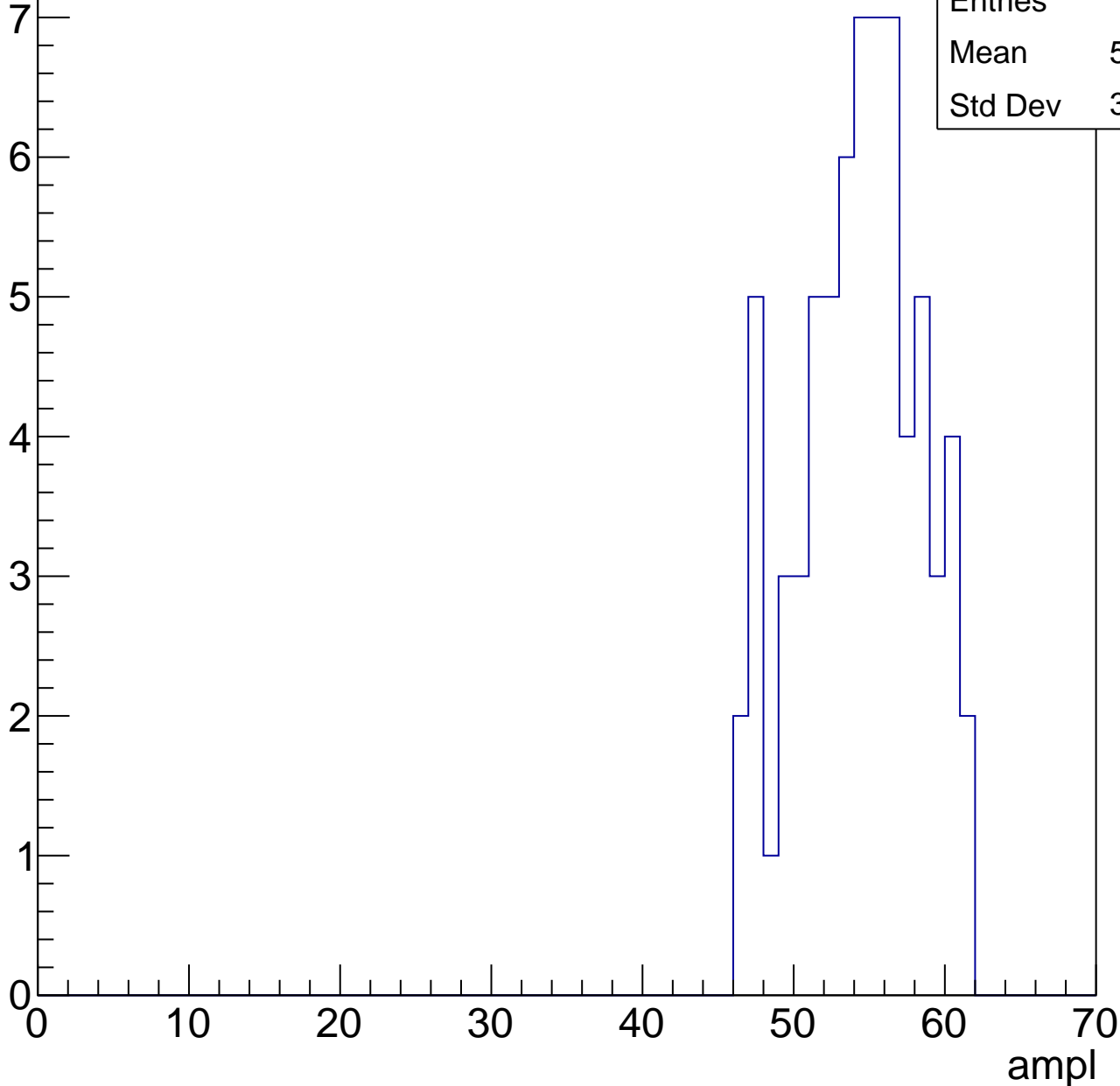


B1L103S, U23-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	53.87
Std Dev	3.938

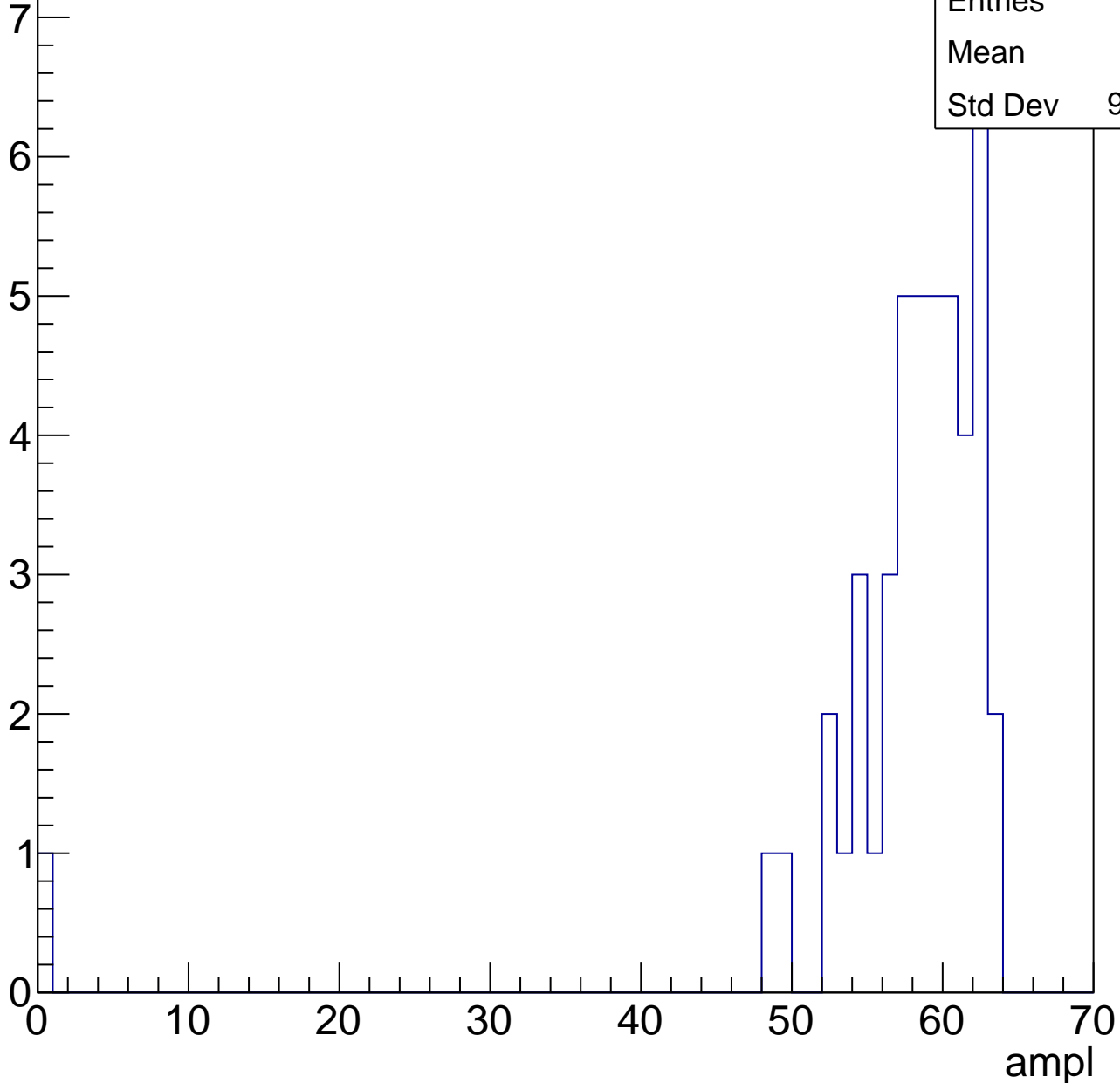


B1L103S, U23-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	56.8
Std Dev	9.176

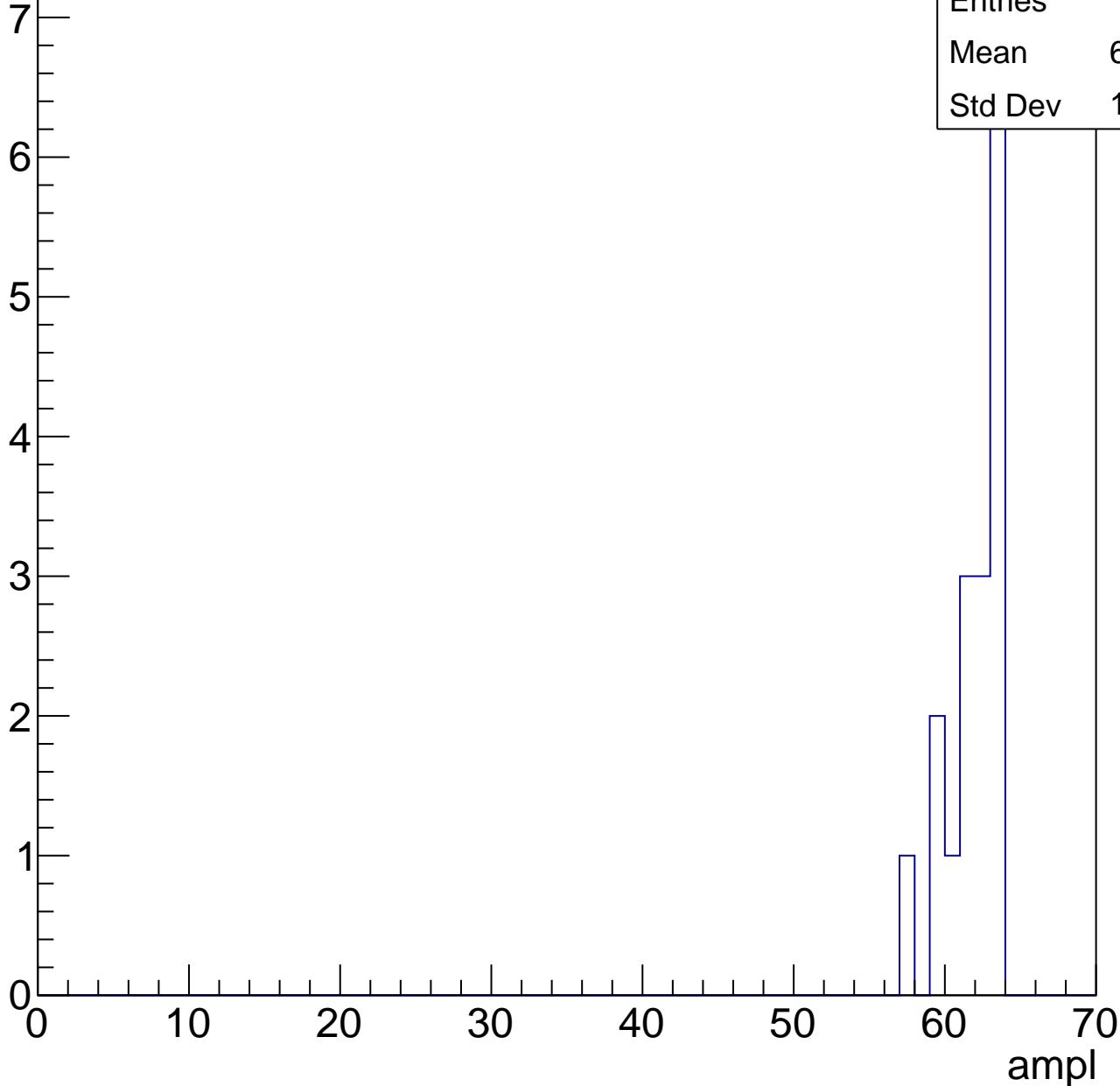


B1L103S, U23-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	61.47
Std Dev	1.753



B1L103S, U23-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch111, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	28.27
Std Dev	6.906

Gaus mean : 29.9566

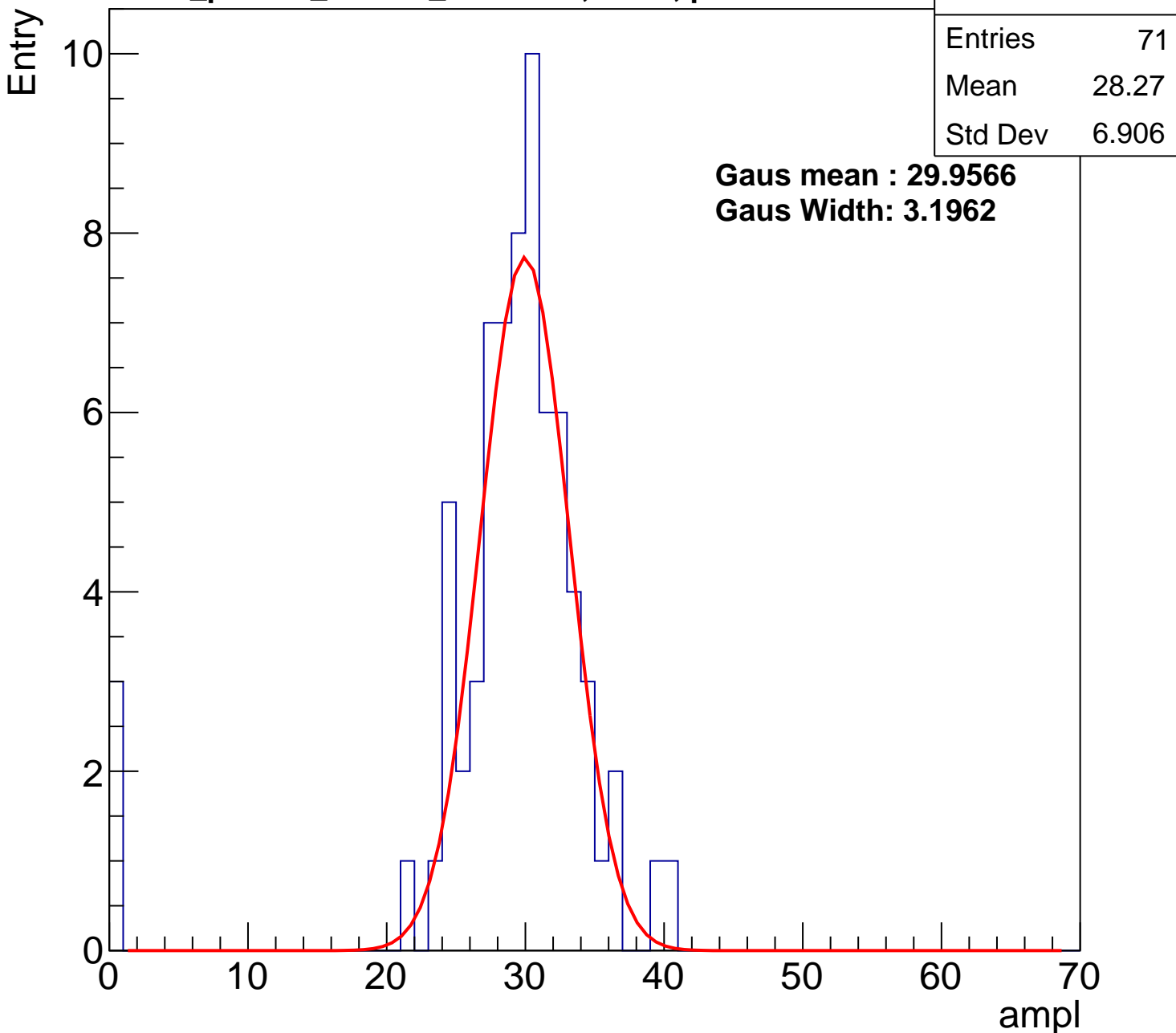
Gaus Width: 3.1962

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch111, adc1

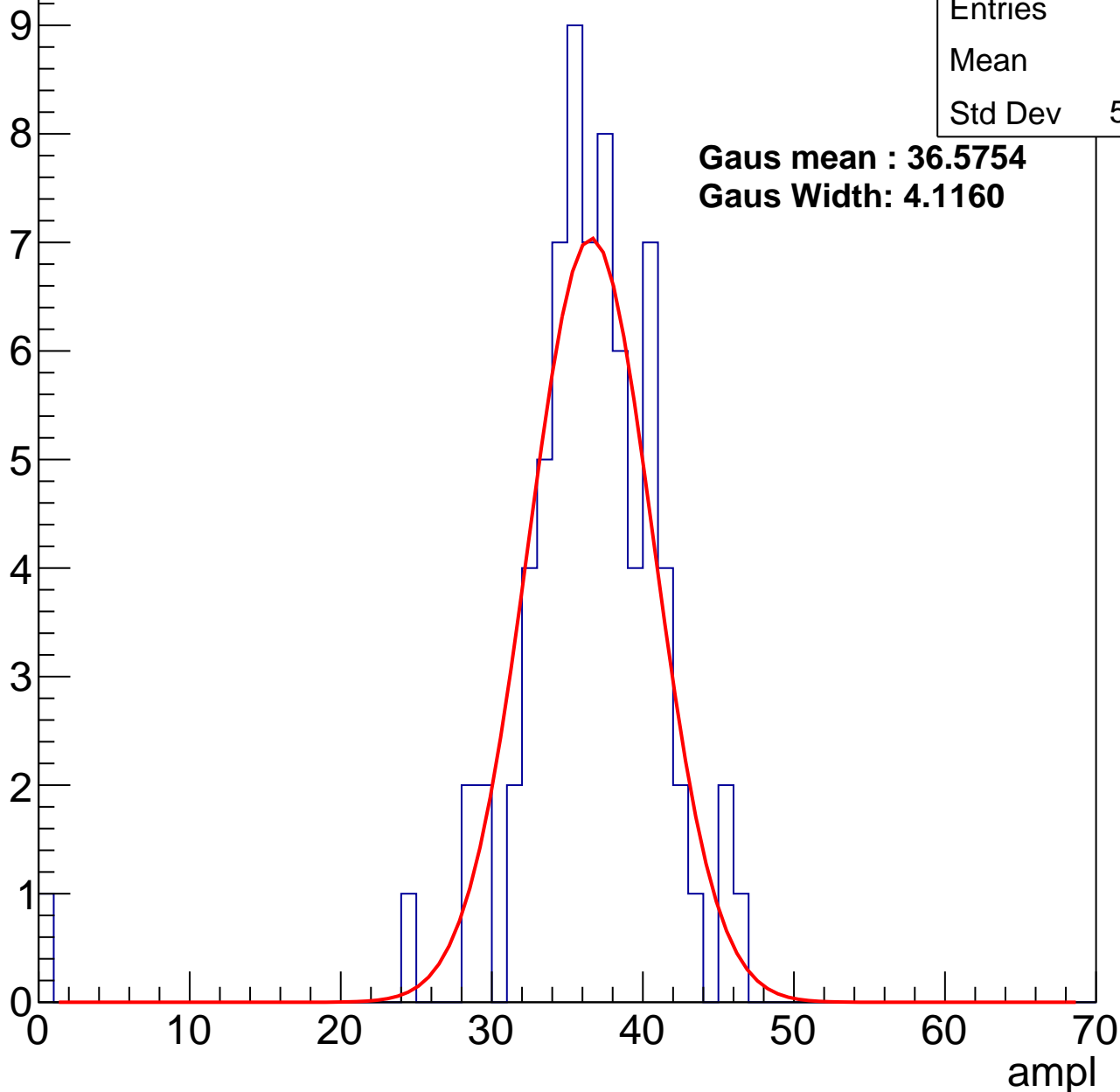
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	35.8
Std Dev	5.799

Gaus mean : 36.5754

Gaus Width: 4.1160



B1L103S, U23-ch111, adc2

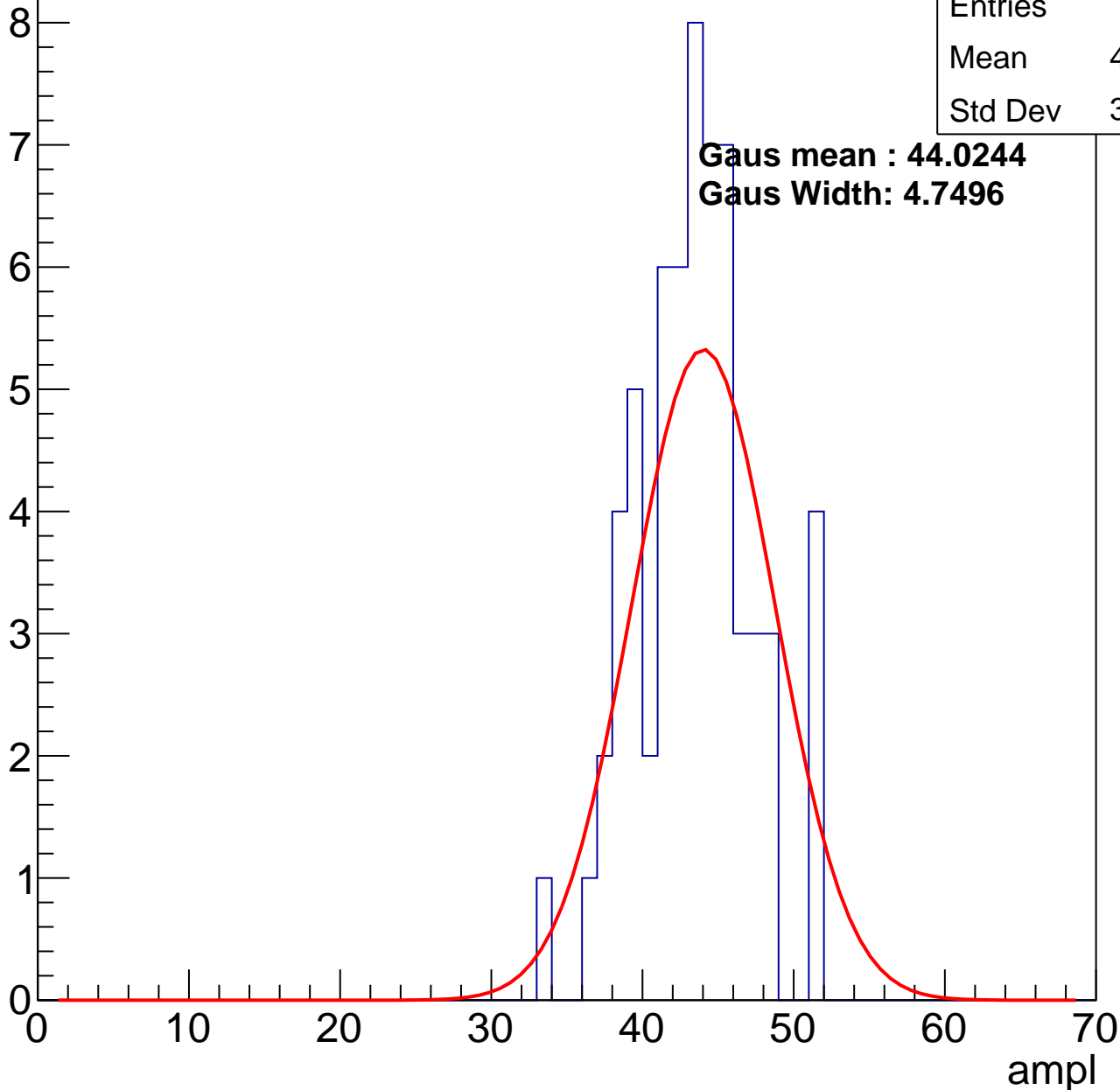
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.94
Std Dev	3.788

Gaus mean : 44.0244

Gaus Width: 4.7496



B1L103S, U23-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

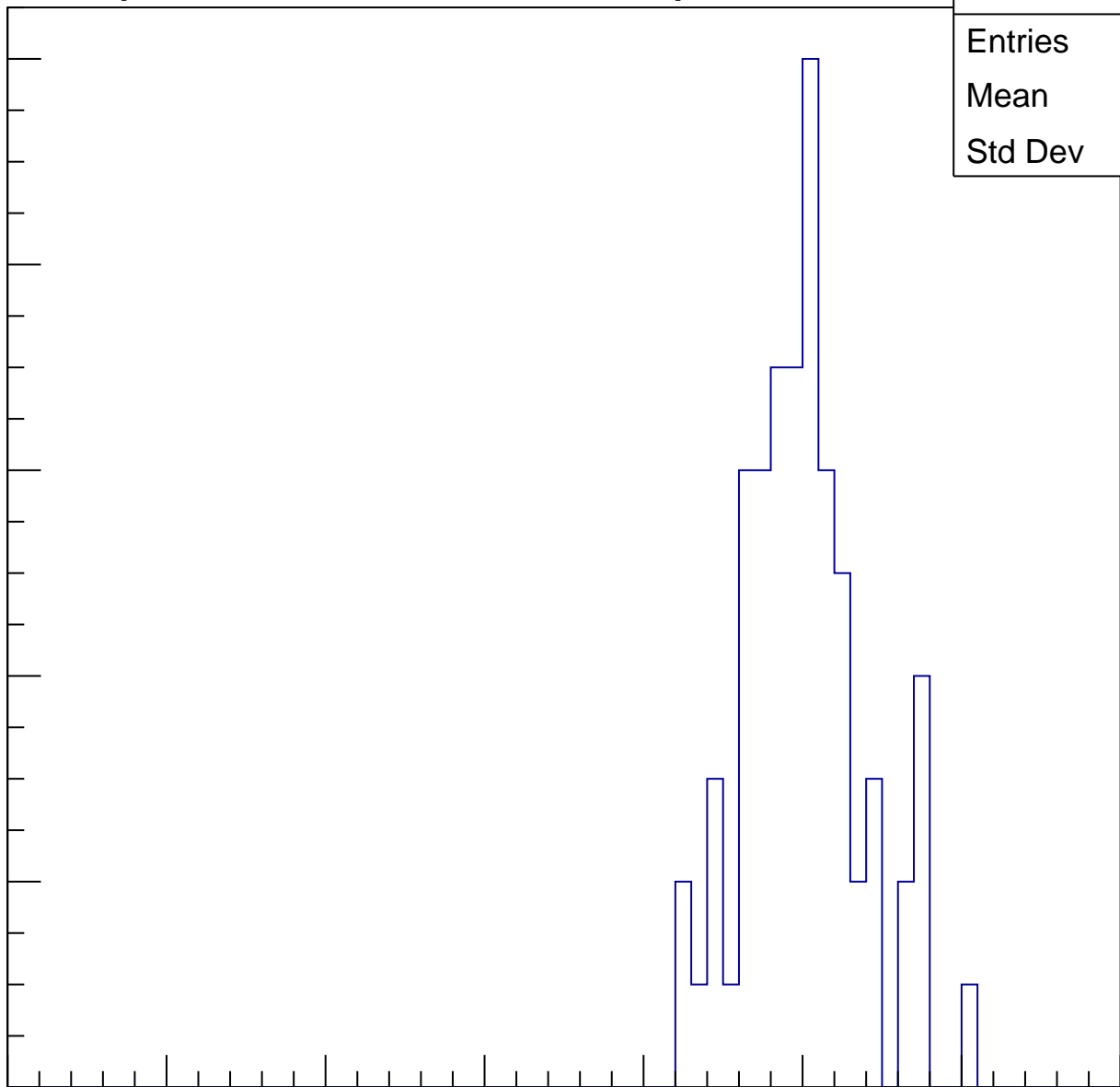
Entries	66
Mean	49.62
Std Dev	3.785

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

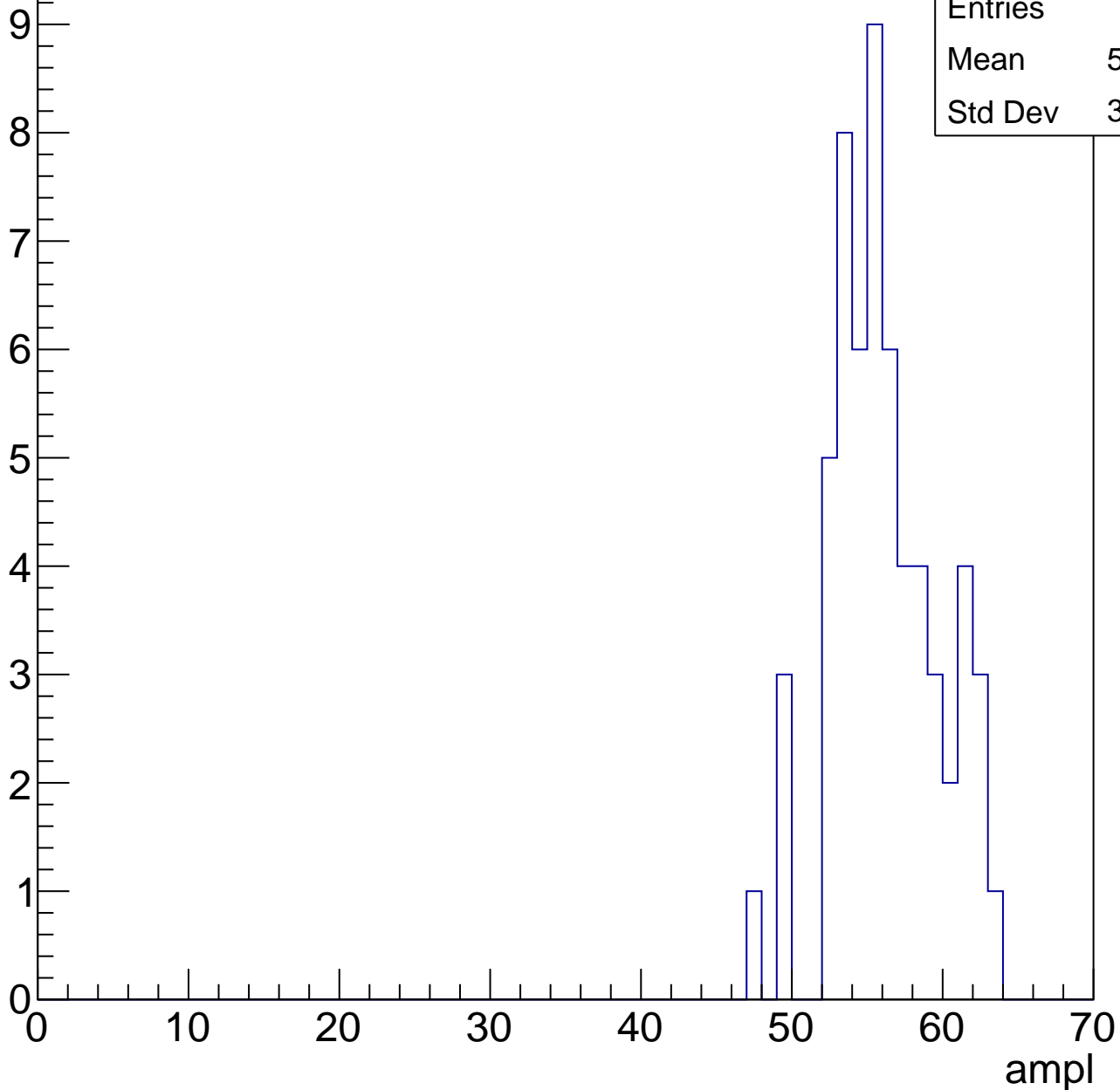


B1L103S, U23-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.64
Std Dev	3.545

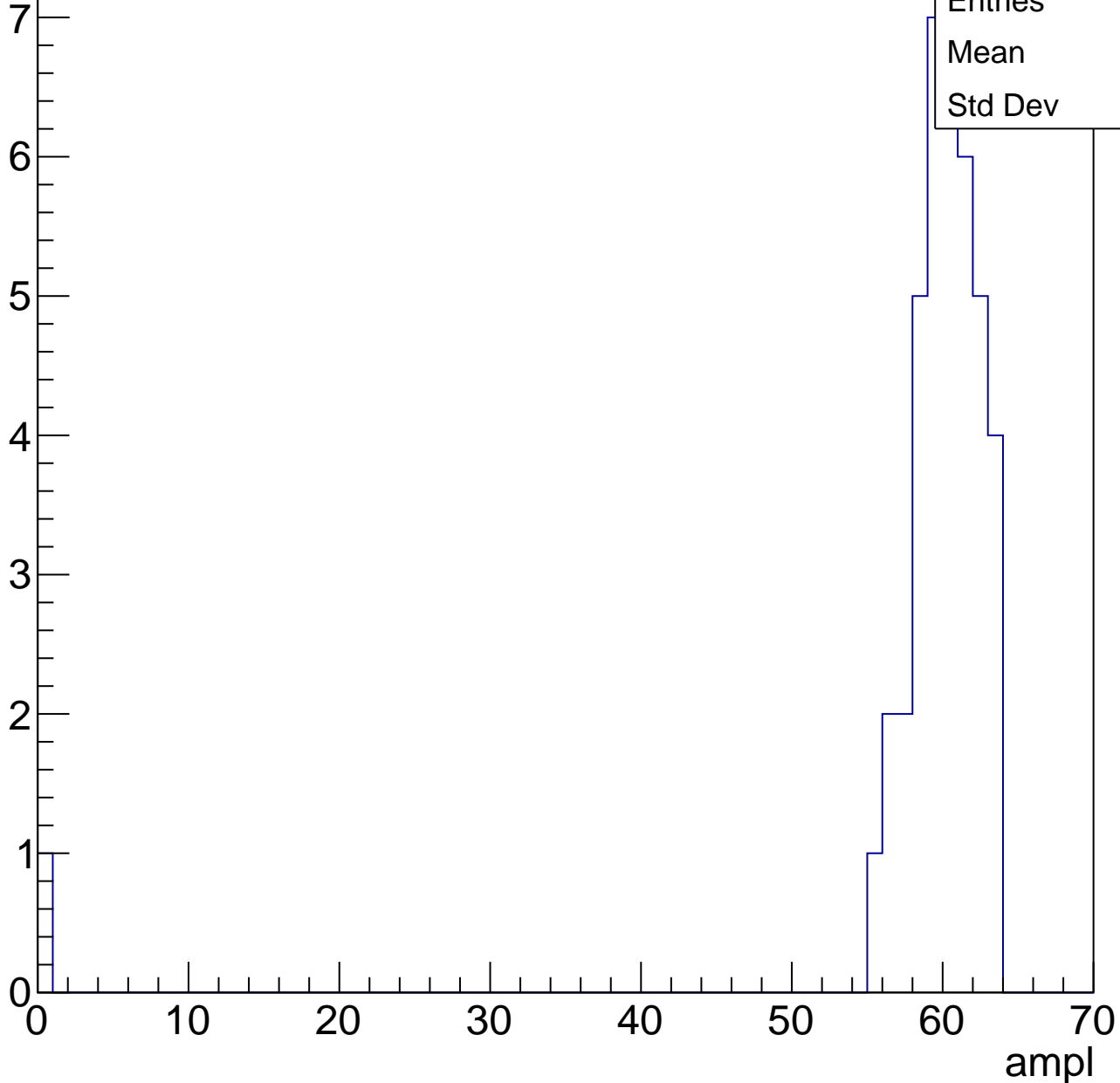


B1L103S, U23-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.3
Std Dev	9.55

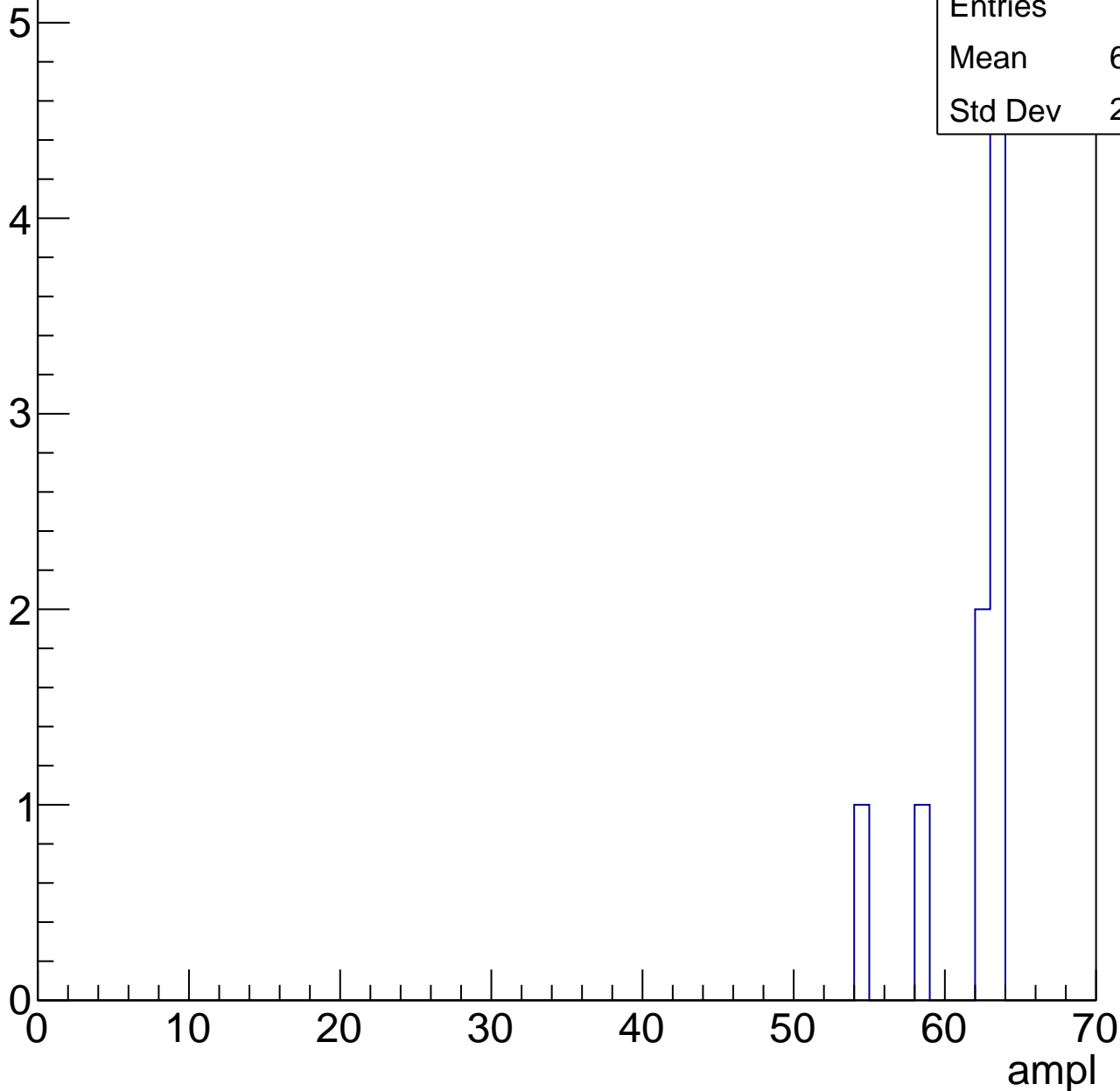


B1L103S, U23-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	61.22
Std Dev	2.973



B1L103S, U23-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch112, adc0

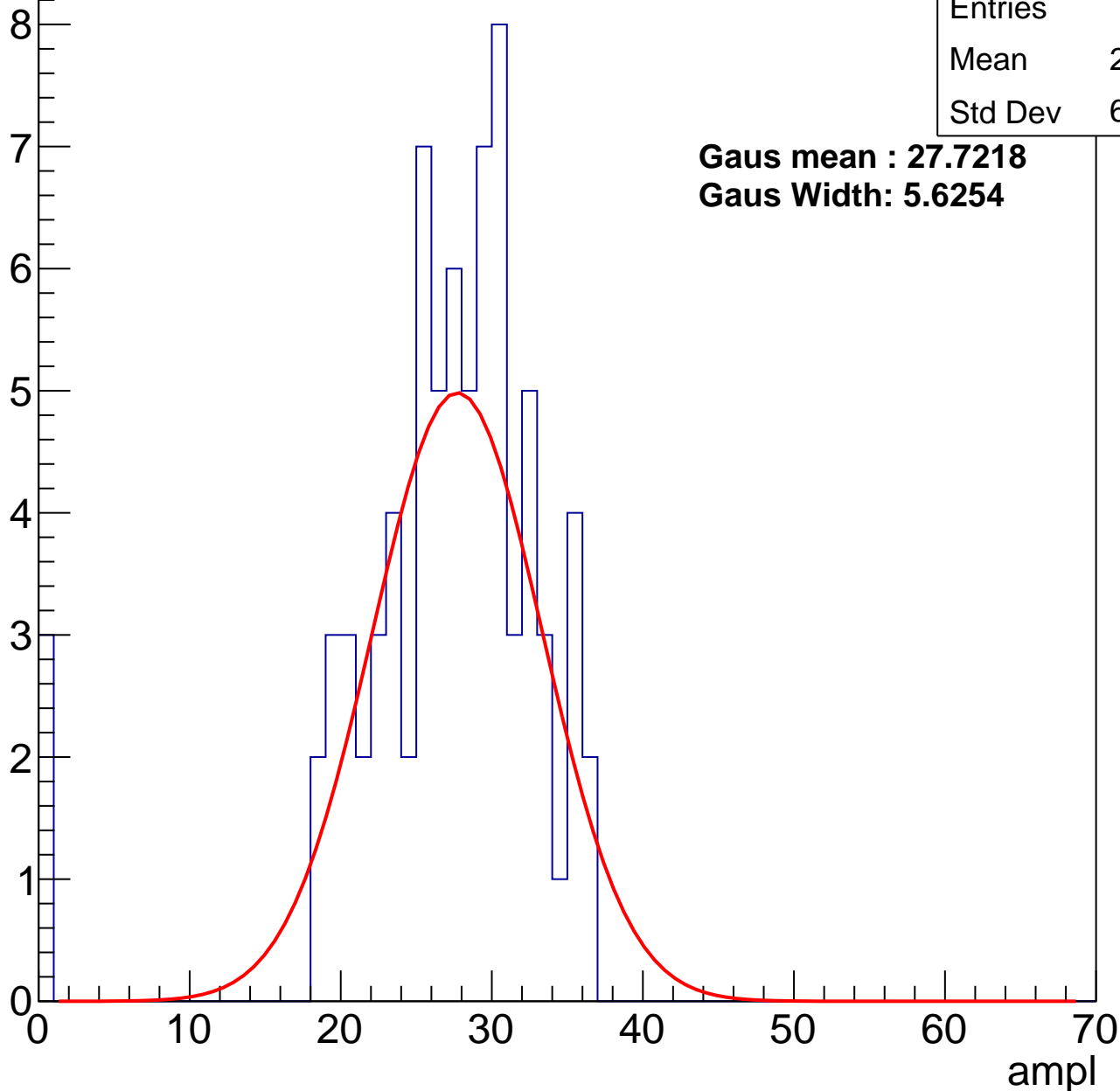
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	26.27
Std Dev	6.944

Gaus mean : 27.7218

Gaus Width: 5.6254



B1L103S, U23-ch112, adc1

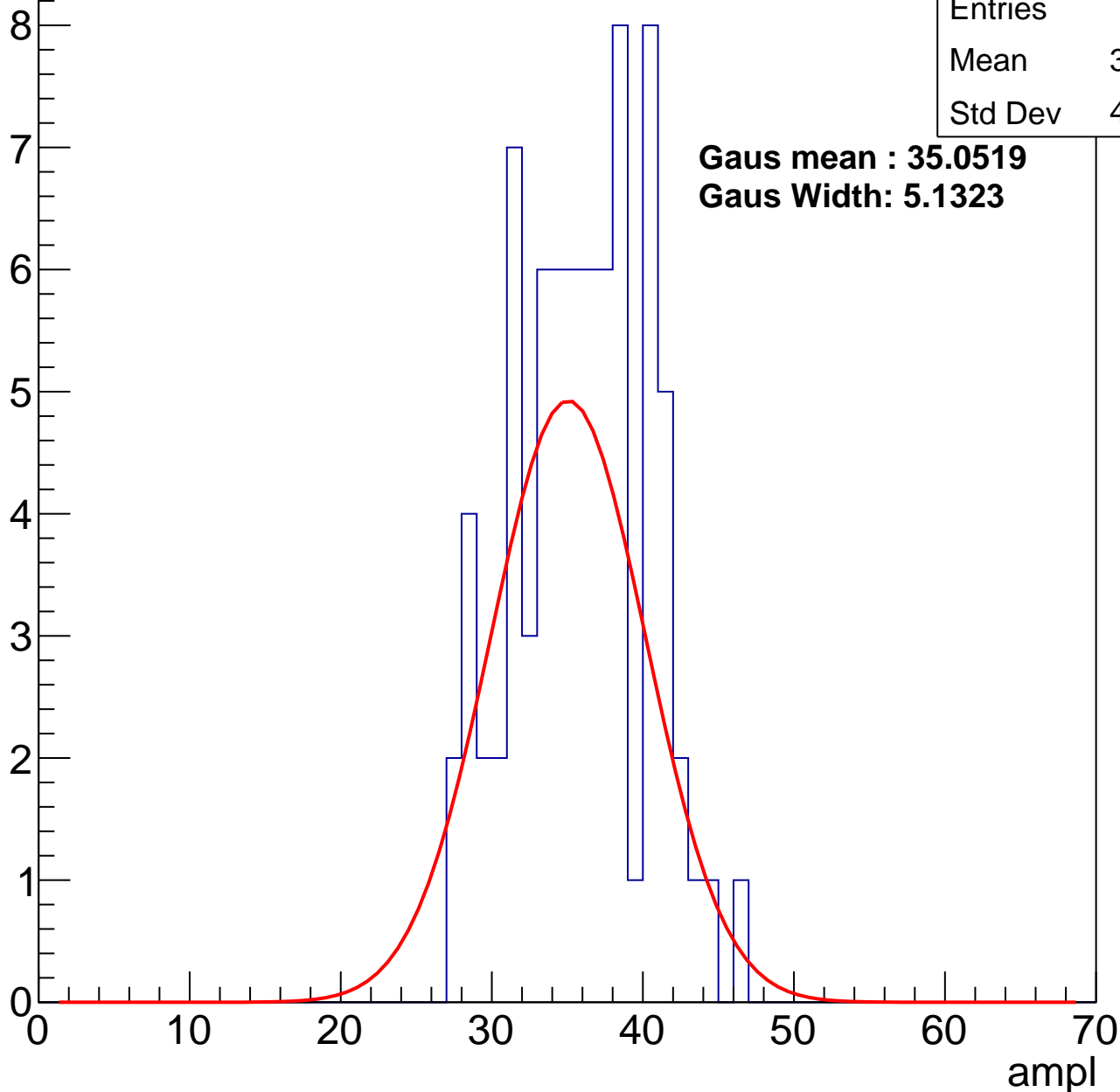
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	35.48
Std Dev	4.353

Gaus mean : 35.0519

Gaus Width: 5.1323



B1L103S, U23-ch112, adc2

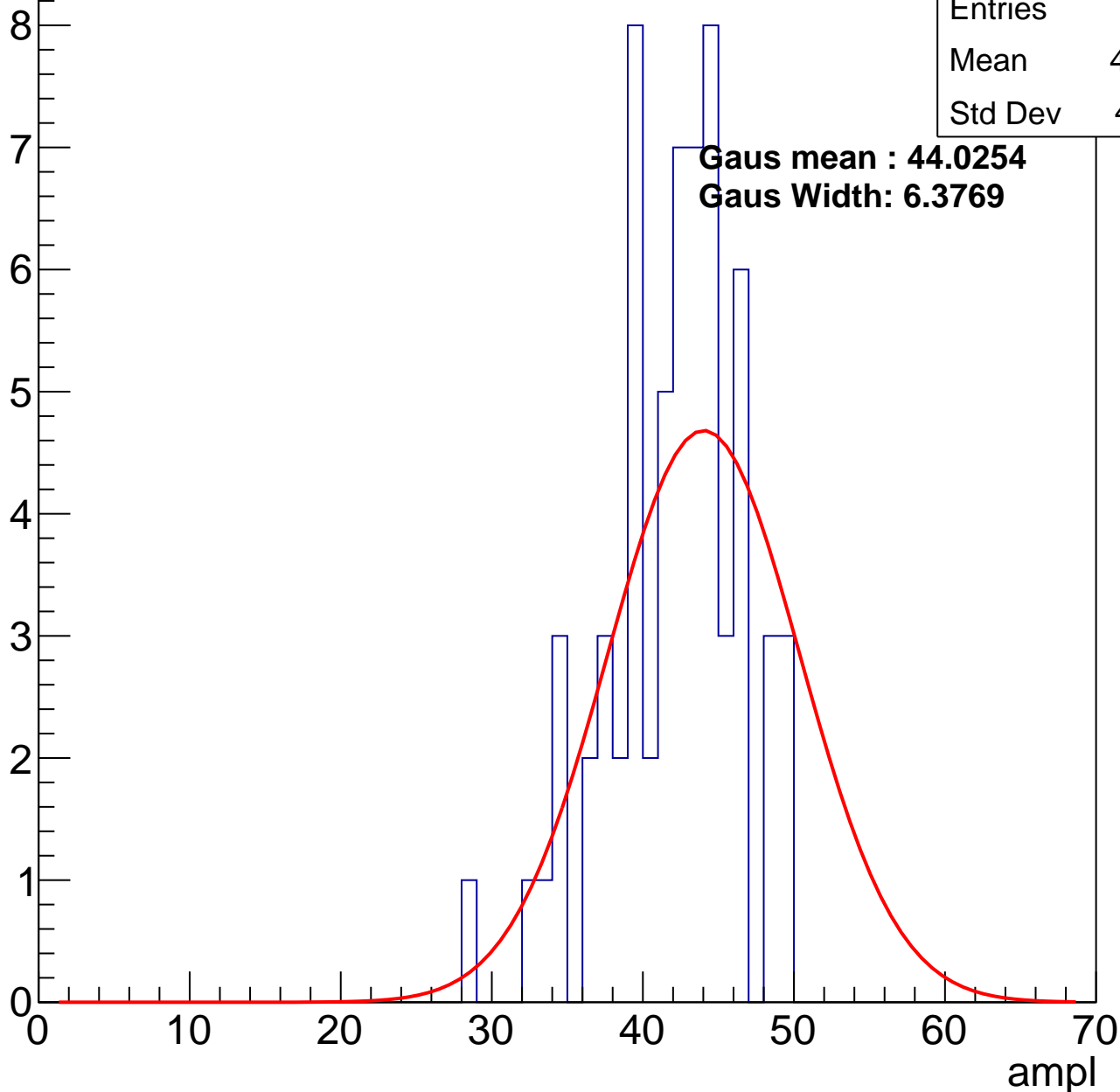
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.54
Std Dev	4.361

Gaus mean : 44.0254

Gaus Width: 6.3769

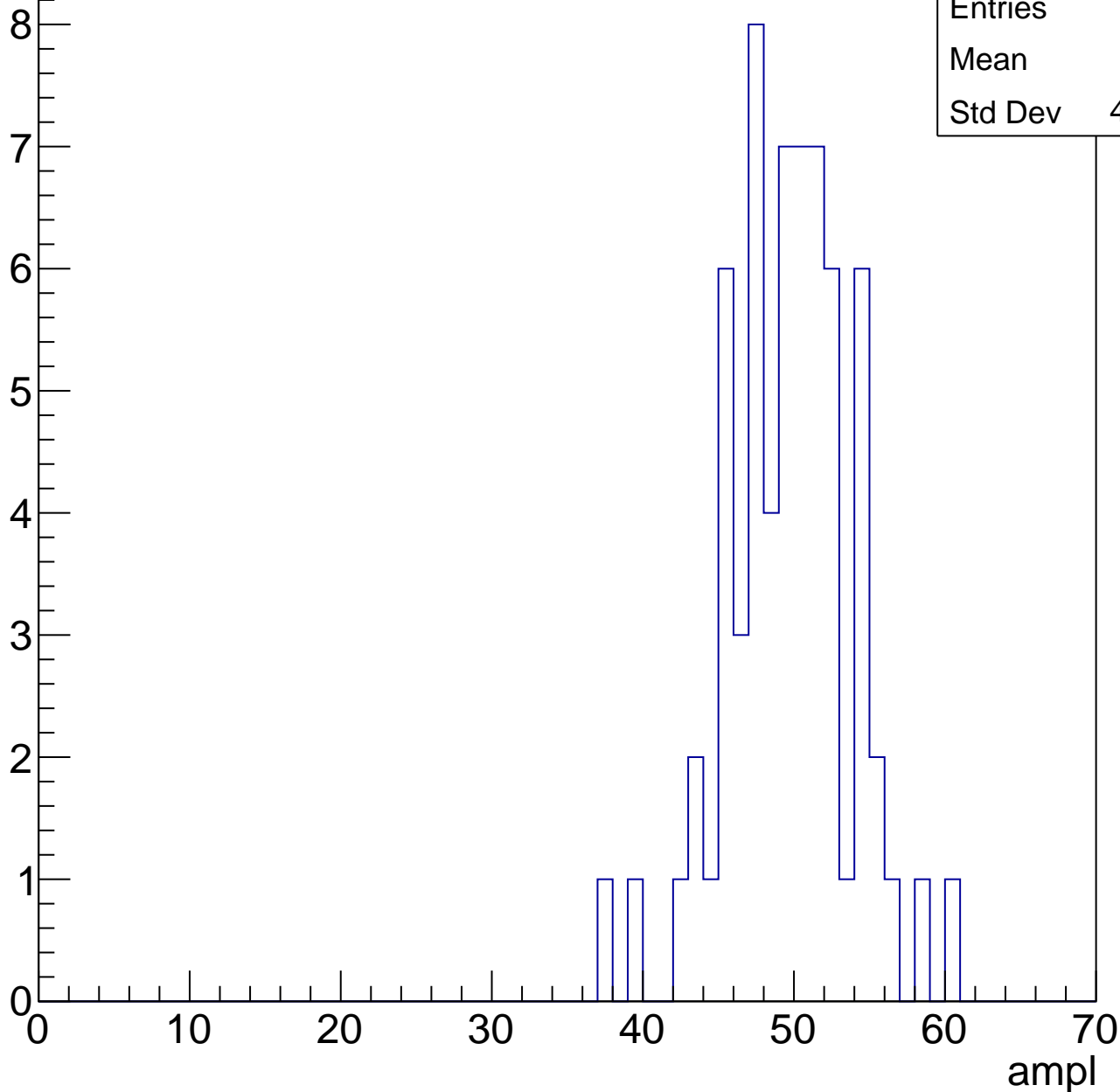


B1L103S, U23-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49.2
Std Dev	4.128

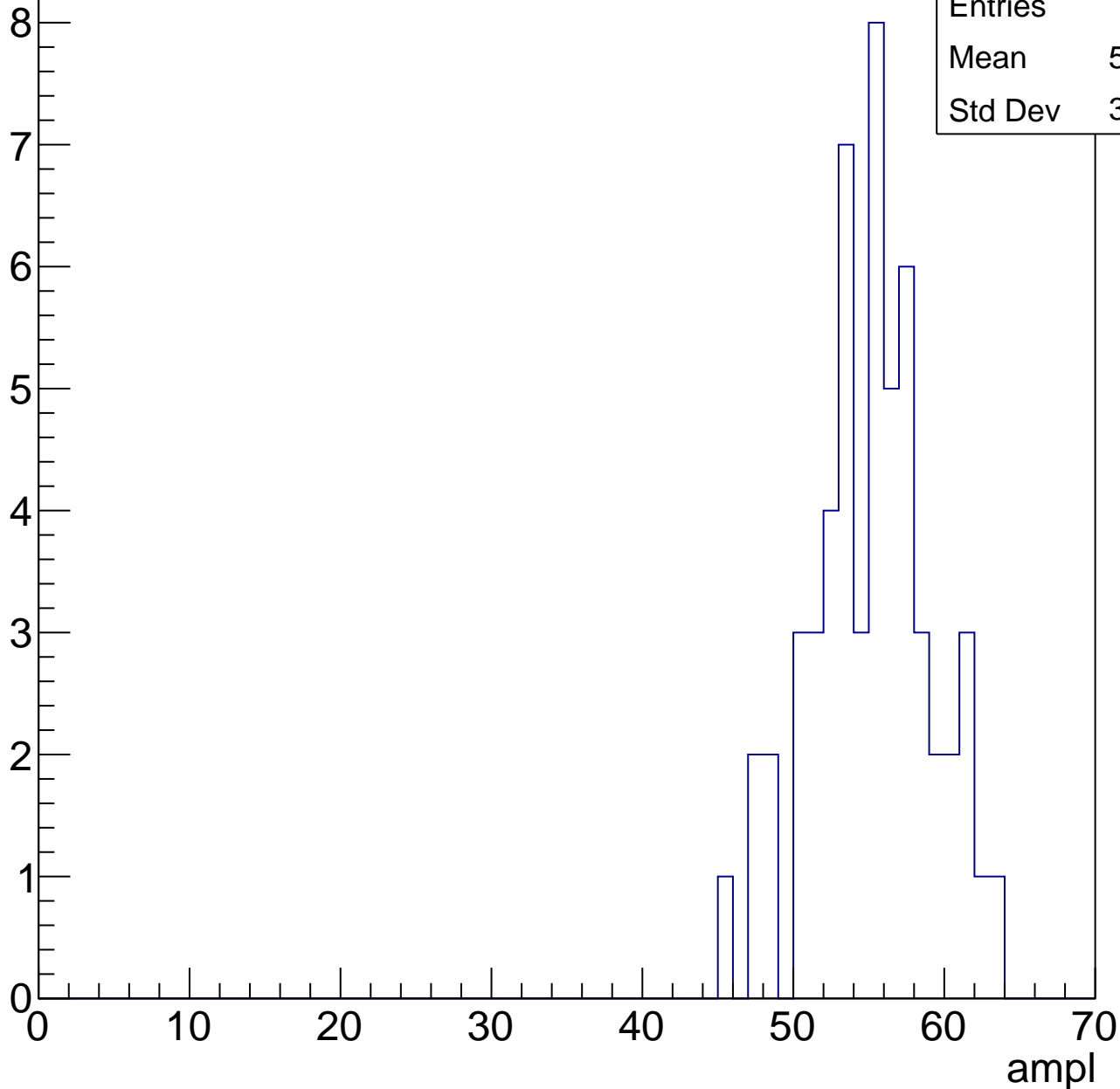


B1L103S, U23-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	54.66
Std Dev	3.938

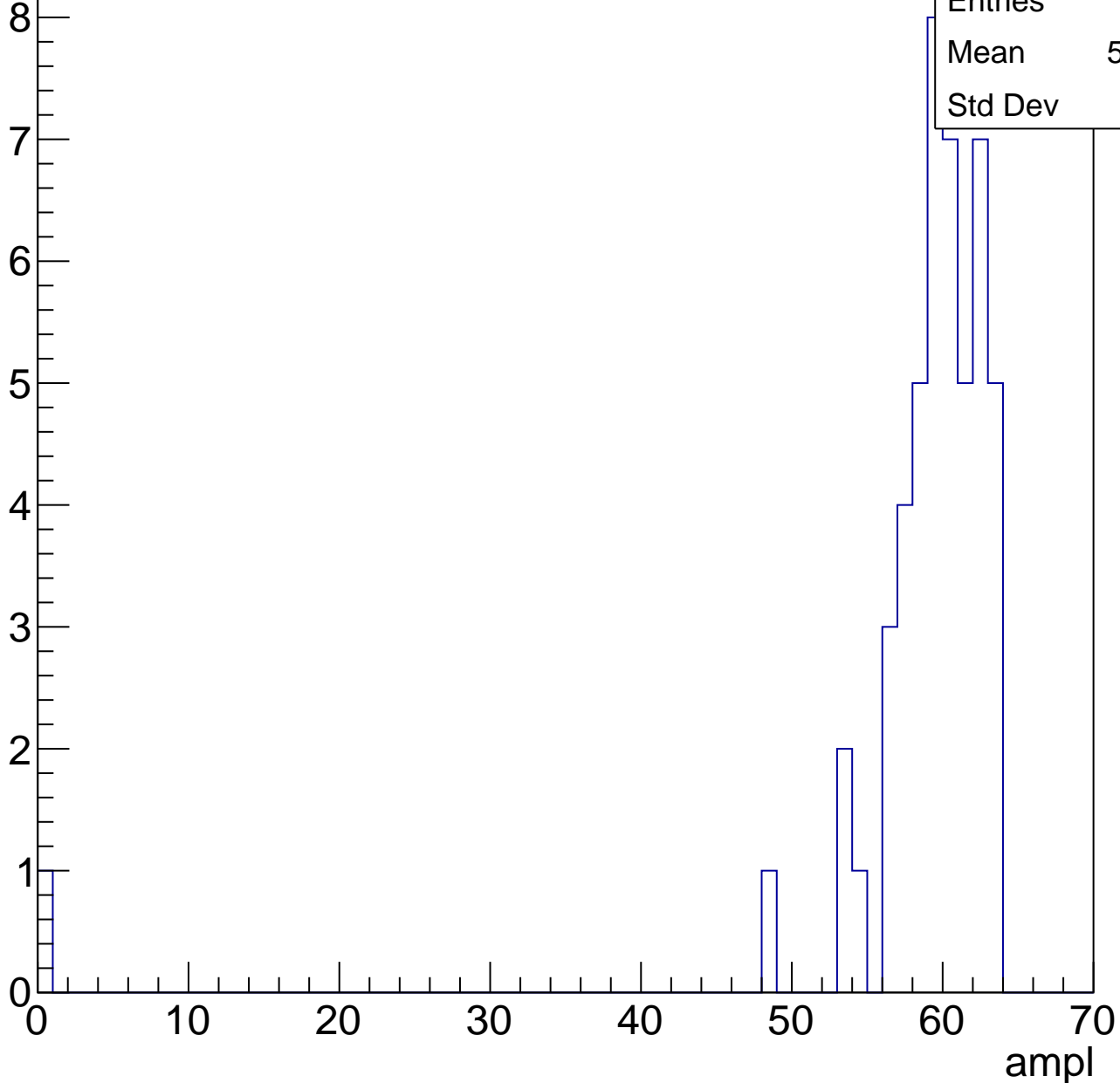


B1L103S, U23-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

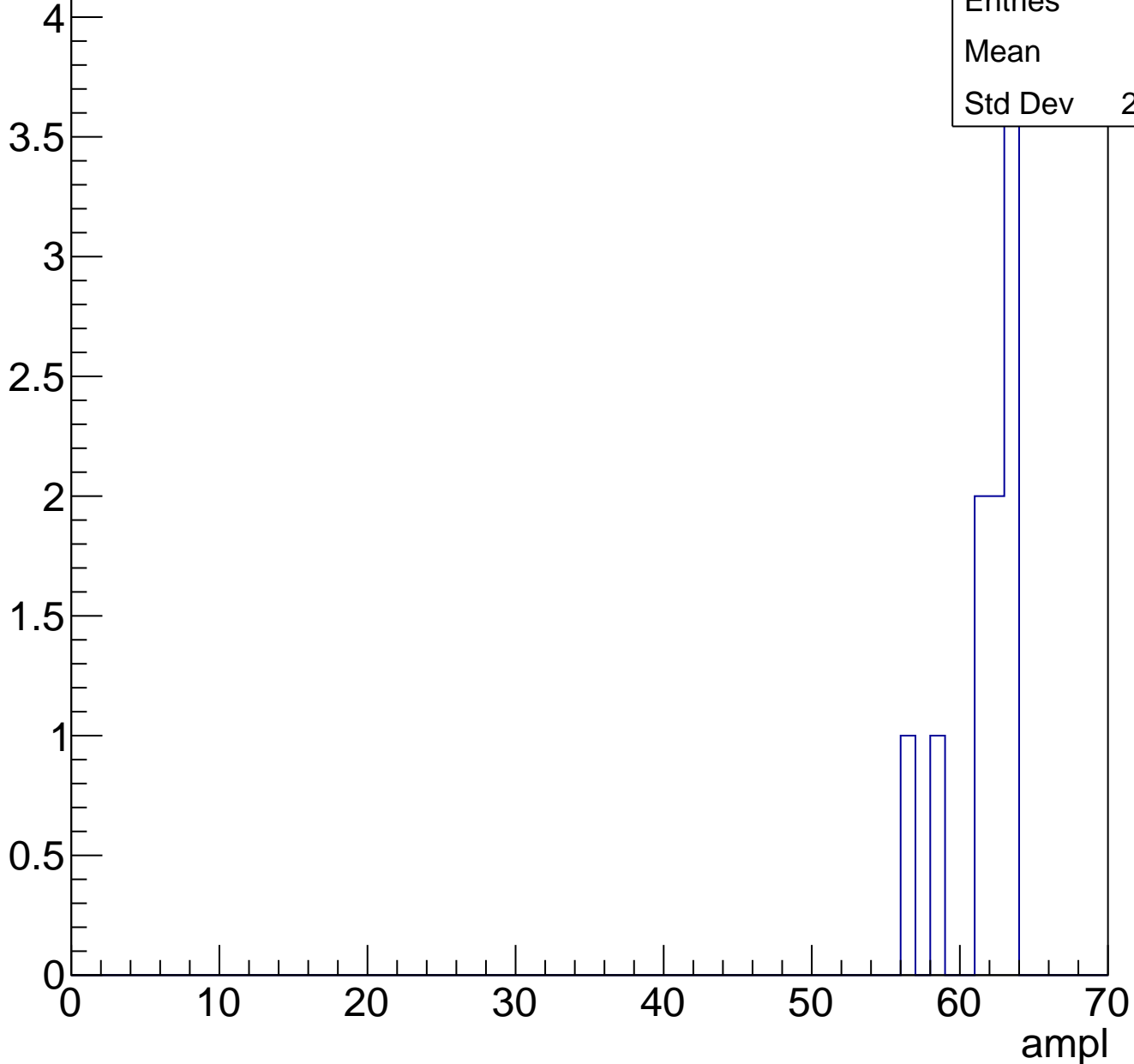
Entries	49
Mean	57.96
Std Dev	8.88



B1L103S, U23-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch113, adc0

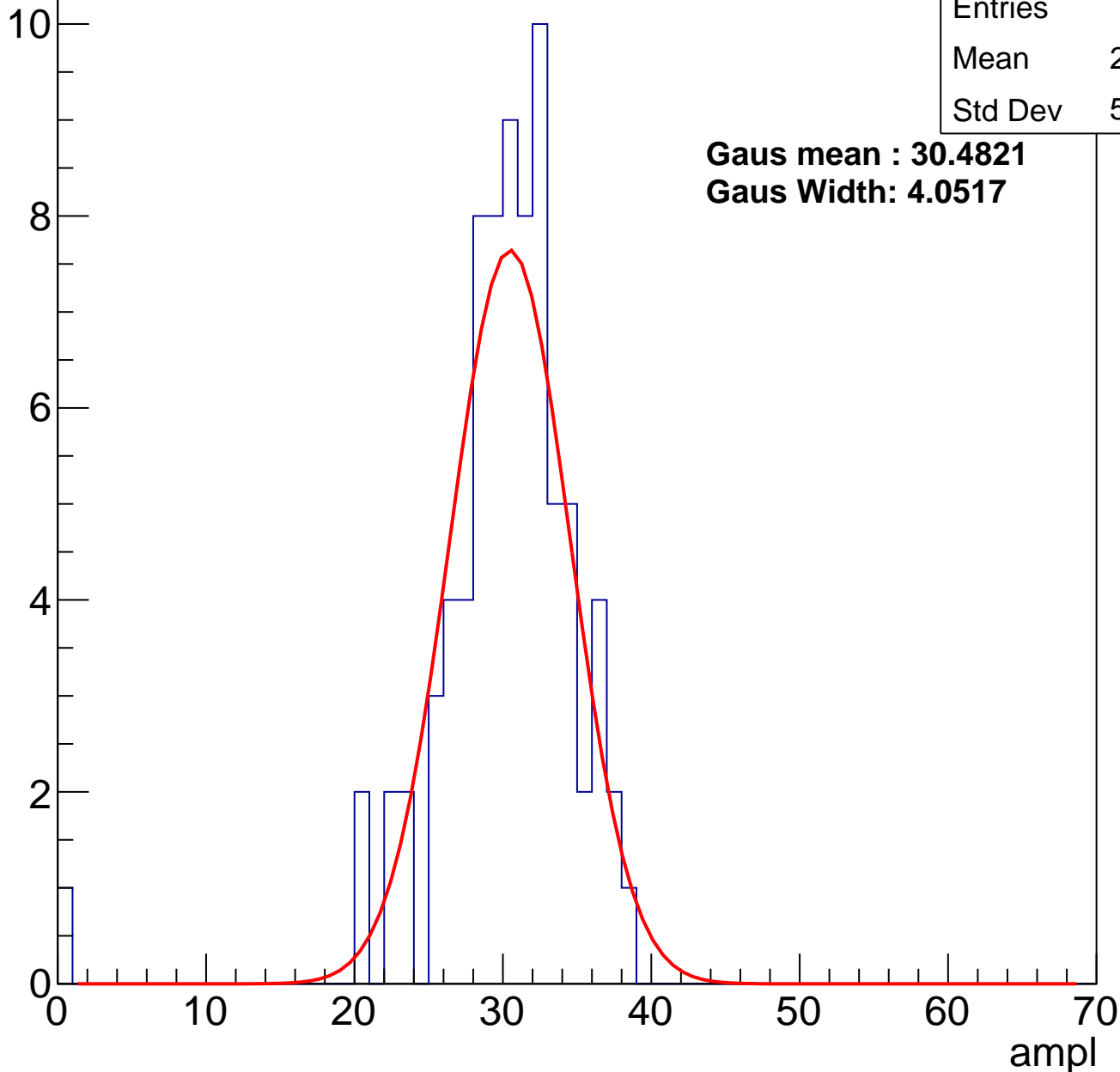
calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	29.65
Std Dev	5.075

Gaus mean : 30.4821

Gaus Width: 4.0517

Entry



B1L103S, U23-ch113, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	38.73
Std Dev	3.721

Gaus mean : 38.9327

Gaus Width: 4.1789

10

8

6

4

2

0

0

10

20

30

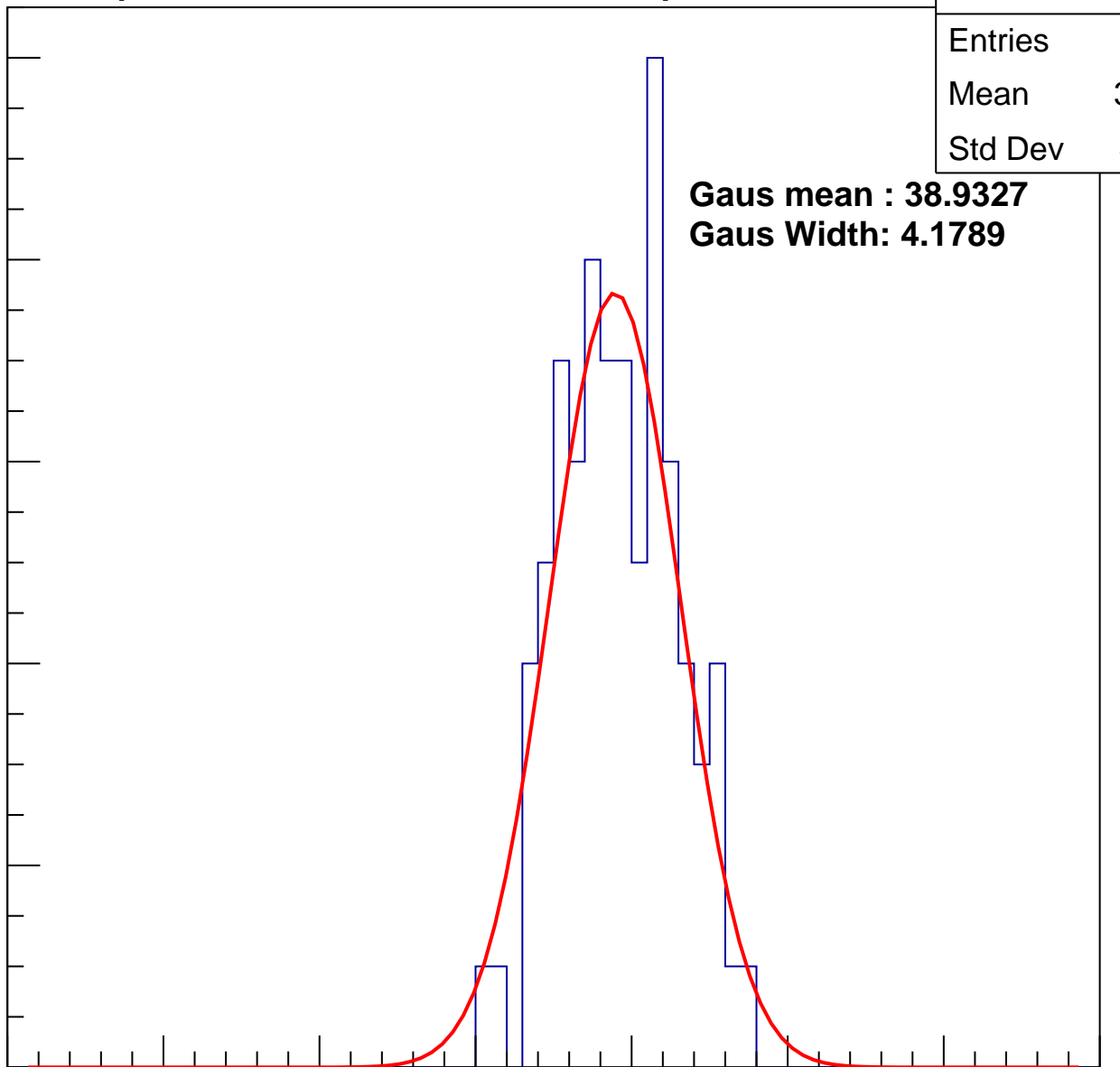
40

50

60

70

ampl



B1L103S, U23-ch113, adc2

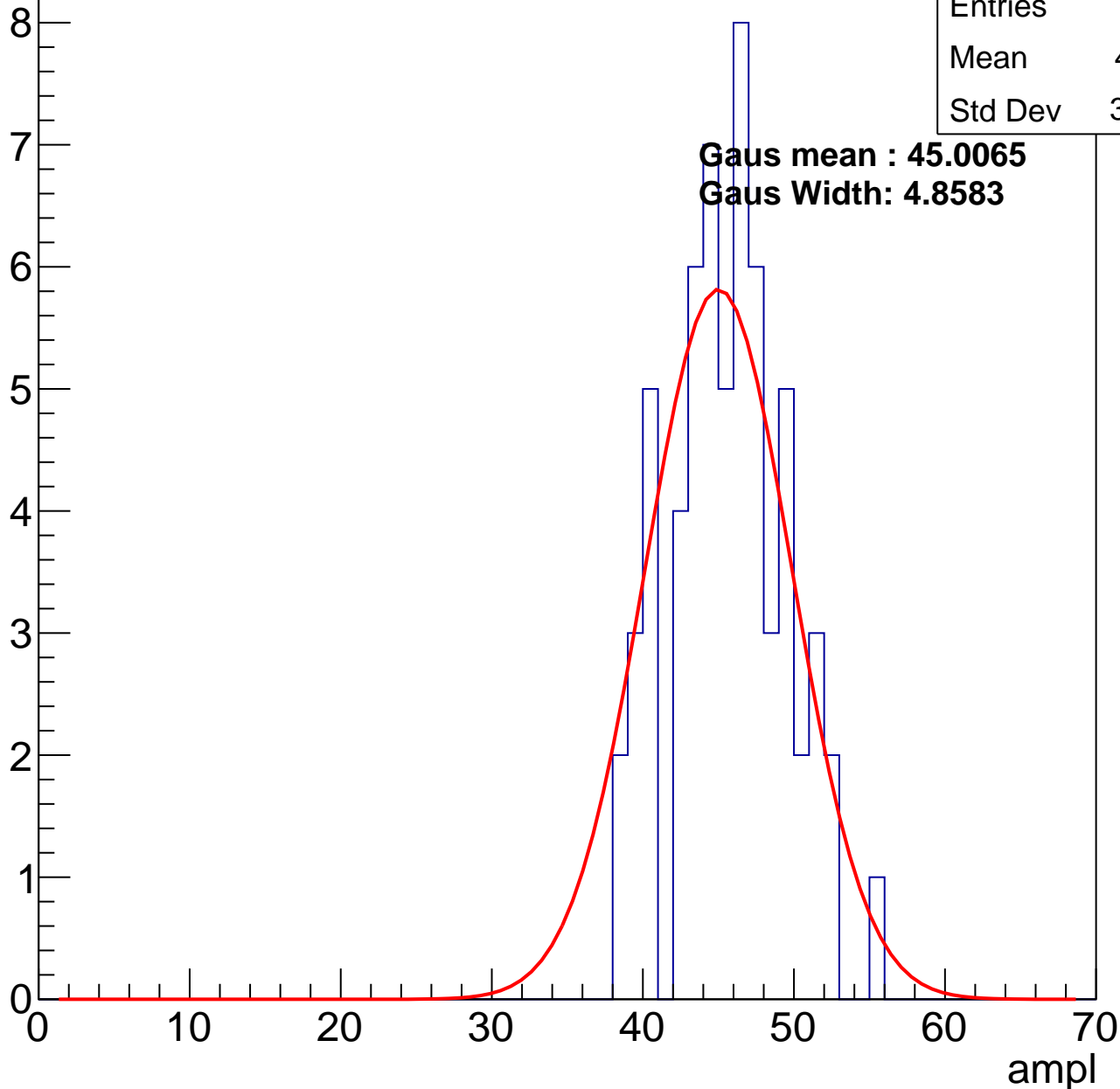
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	45.21
Std Dev	3.789

Gaus mean : 45.0065

Gaus Width: 4.8583

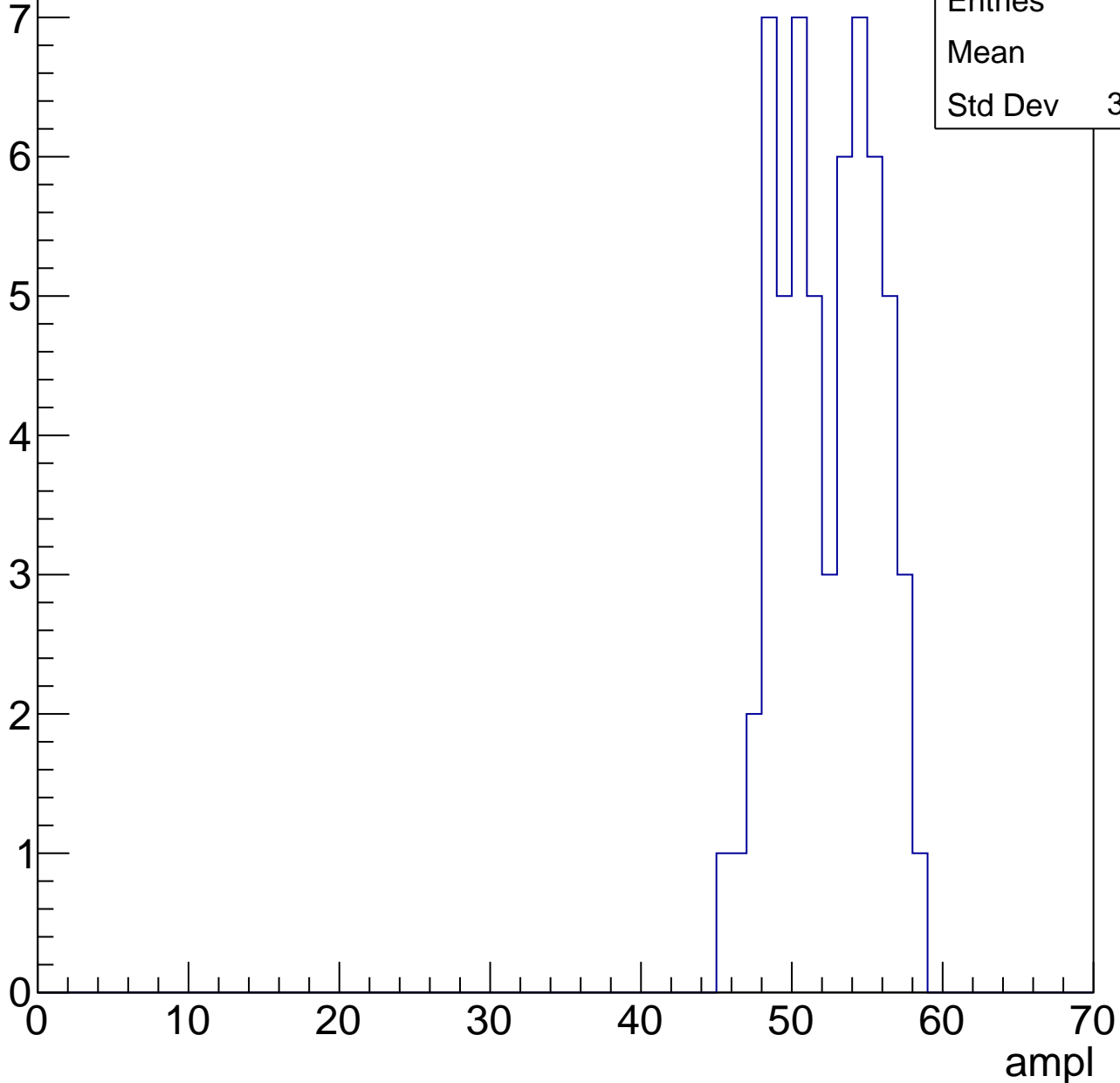


B1L103S, U23-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

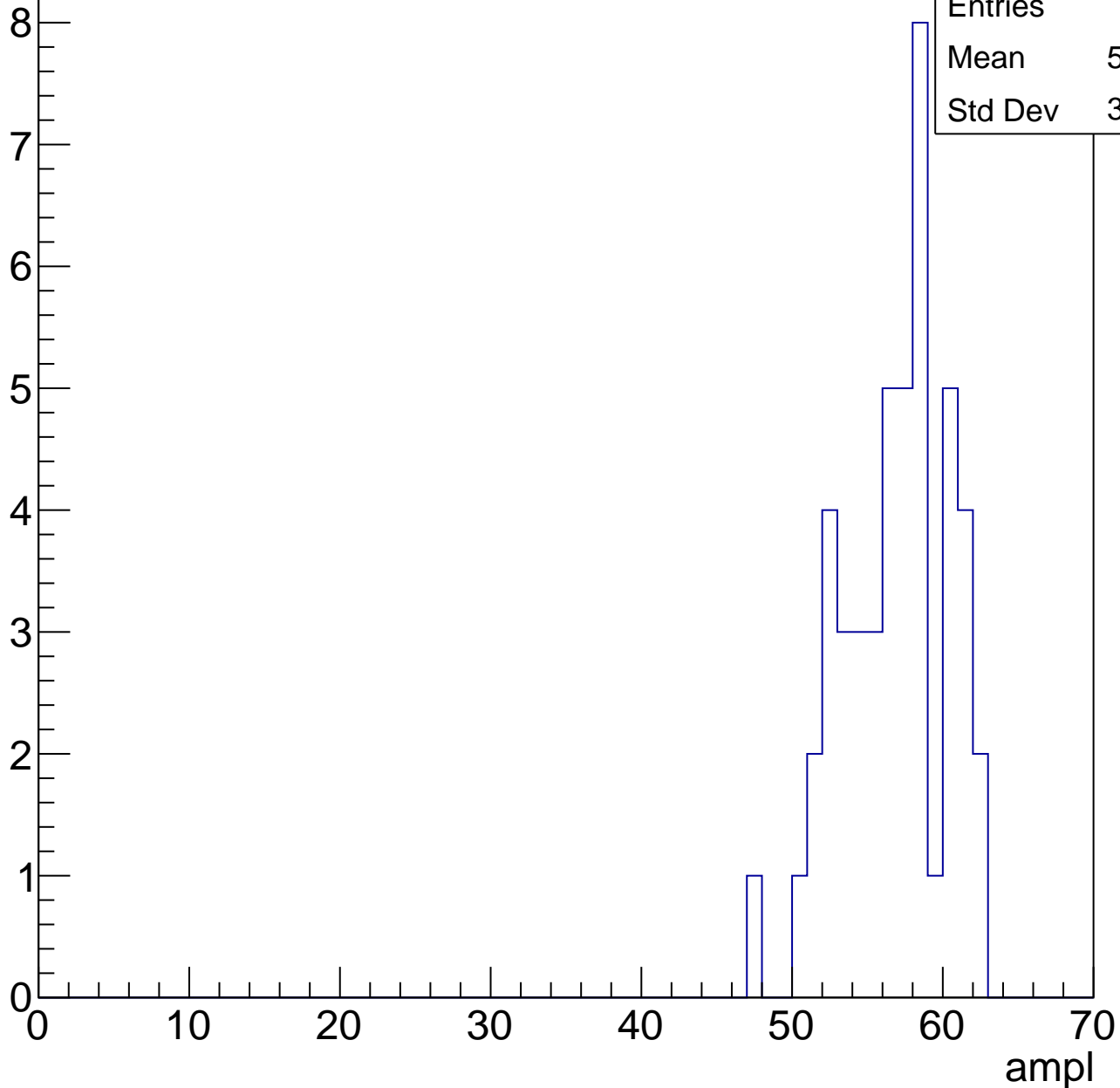
Entries	59
Mean	51.9
Std Dev	3.198



B1L103S, U23-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



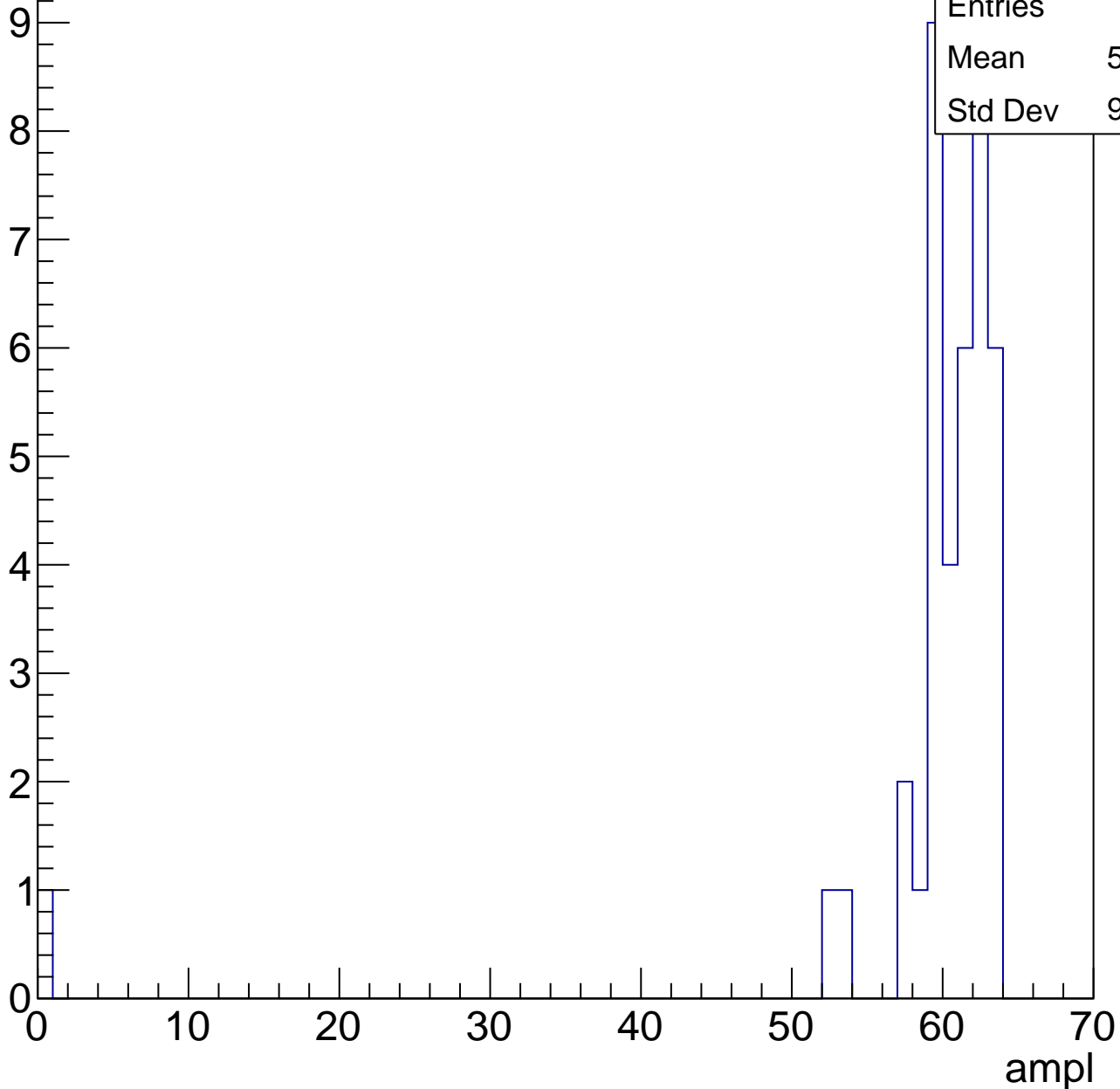
Entries	47
Mean	56.36
Std Dev	3.467

B1L103S, U23-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

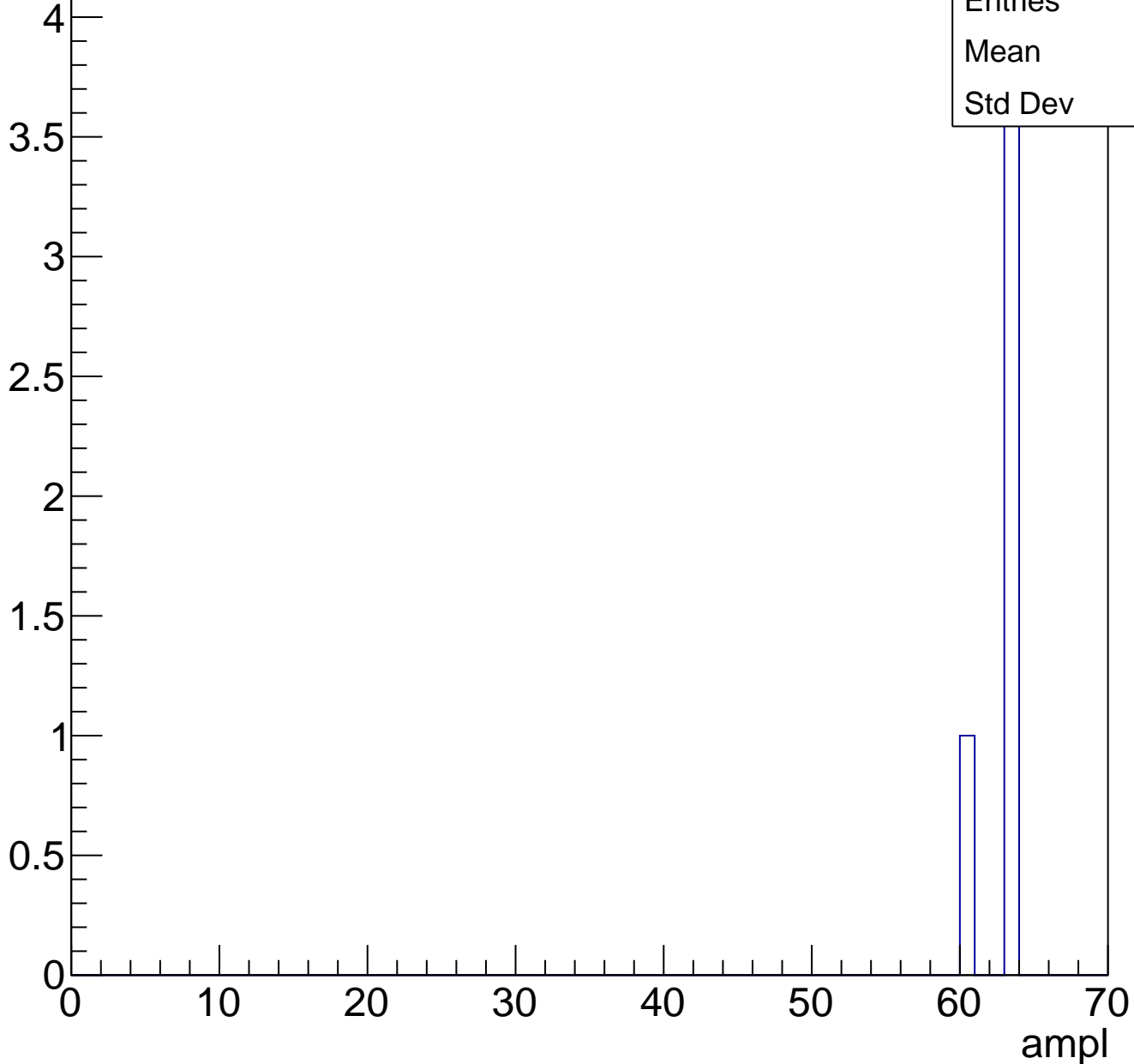
Entries	39
Mean	58.67
Std Dev	9.828



B1L103S, U23-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch114, adc0

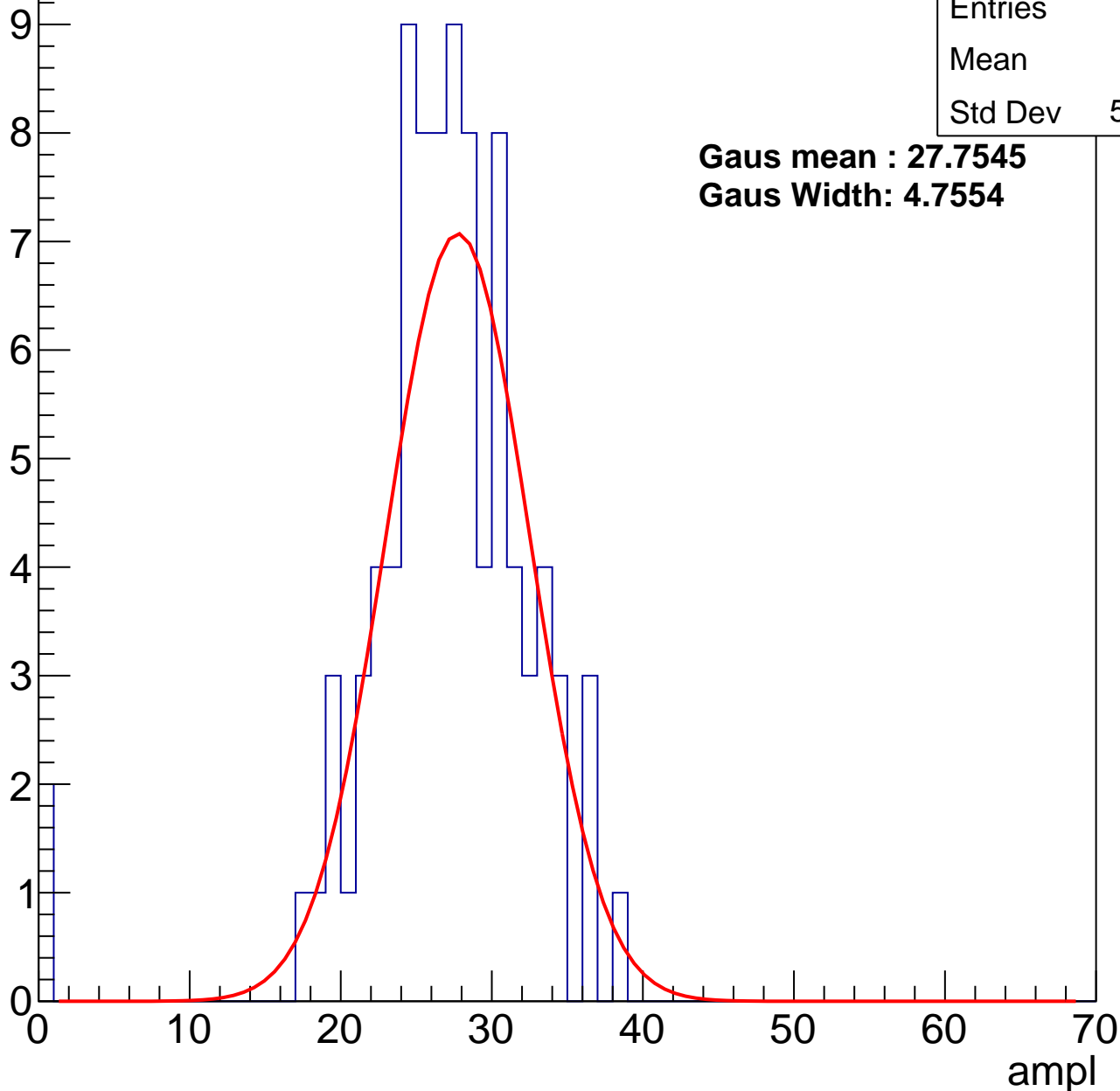
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	91
Mean	26.4
Std Dev	5.865

Gaus mean : 27.7545

Gaus Width: 4.7554



B1L103S, U23-ch114, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	34.9
Std Dev	4.03

Gaus mean : 36.3608

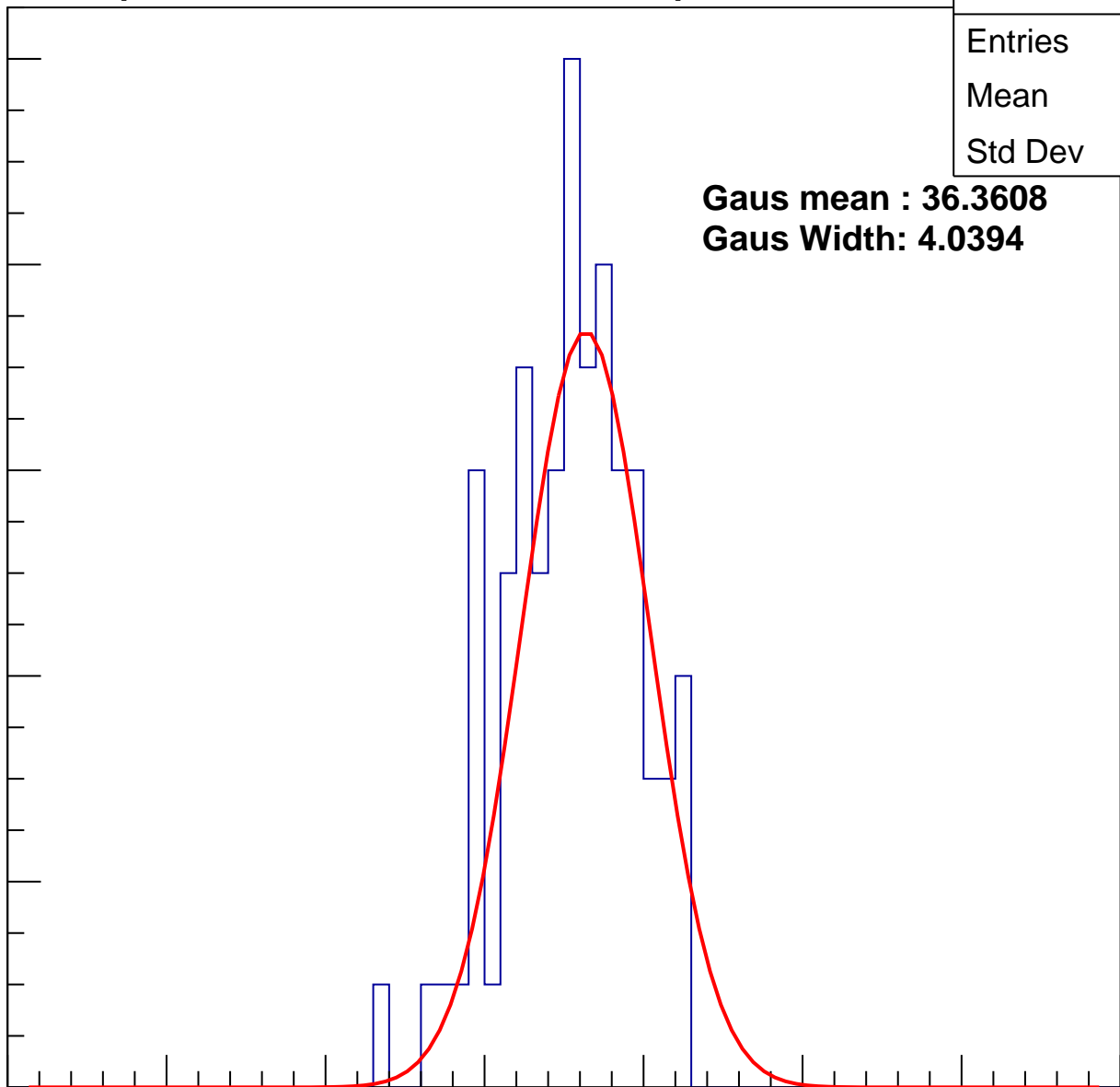
Gaus Width: 4.0394

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch114, adc2

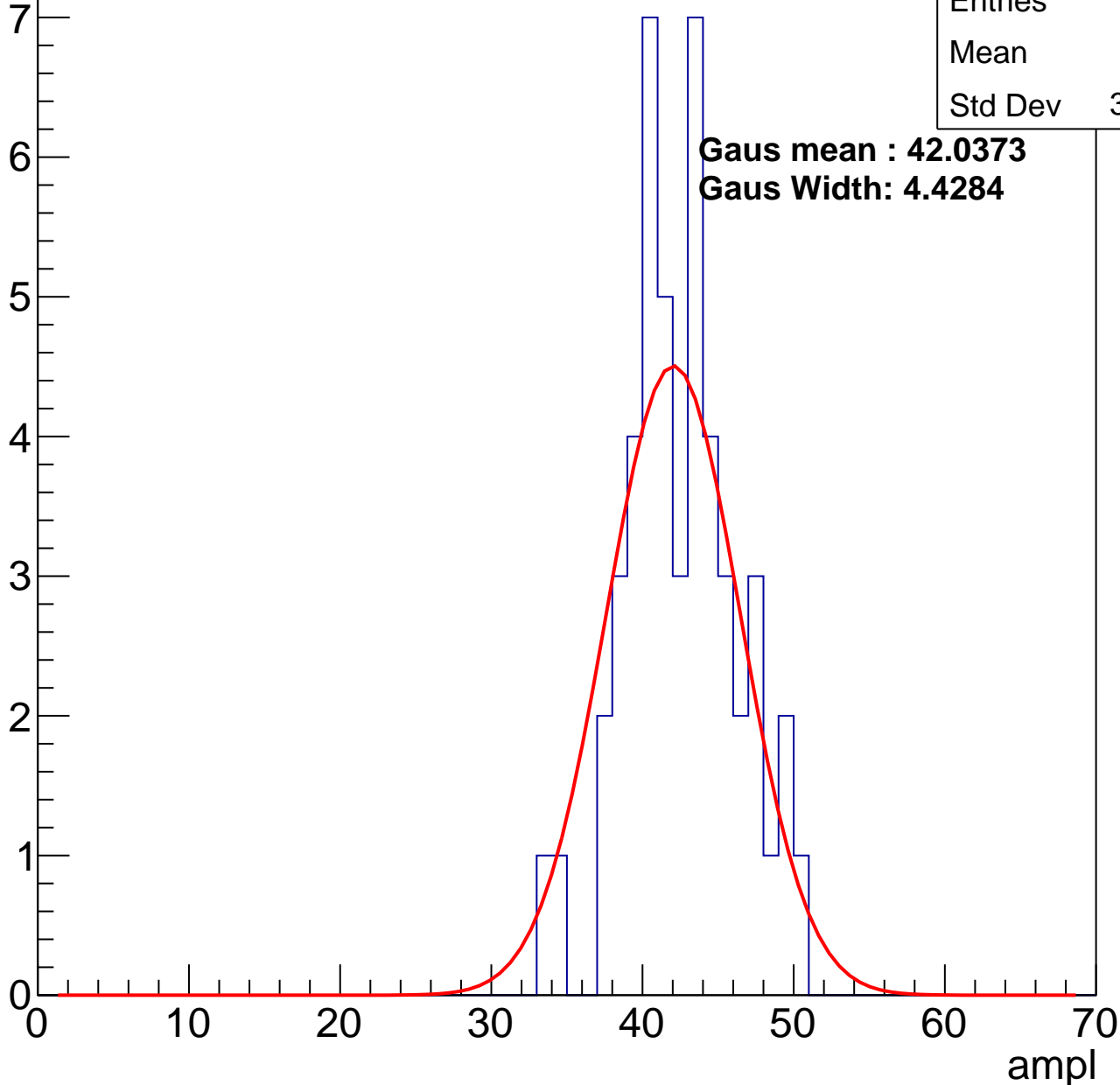
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	42.1
Std Dev	3.688

Gaus mean : 42.0373

Gaus Width: 4.4284

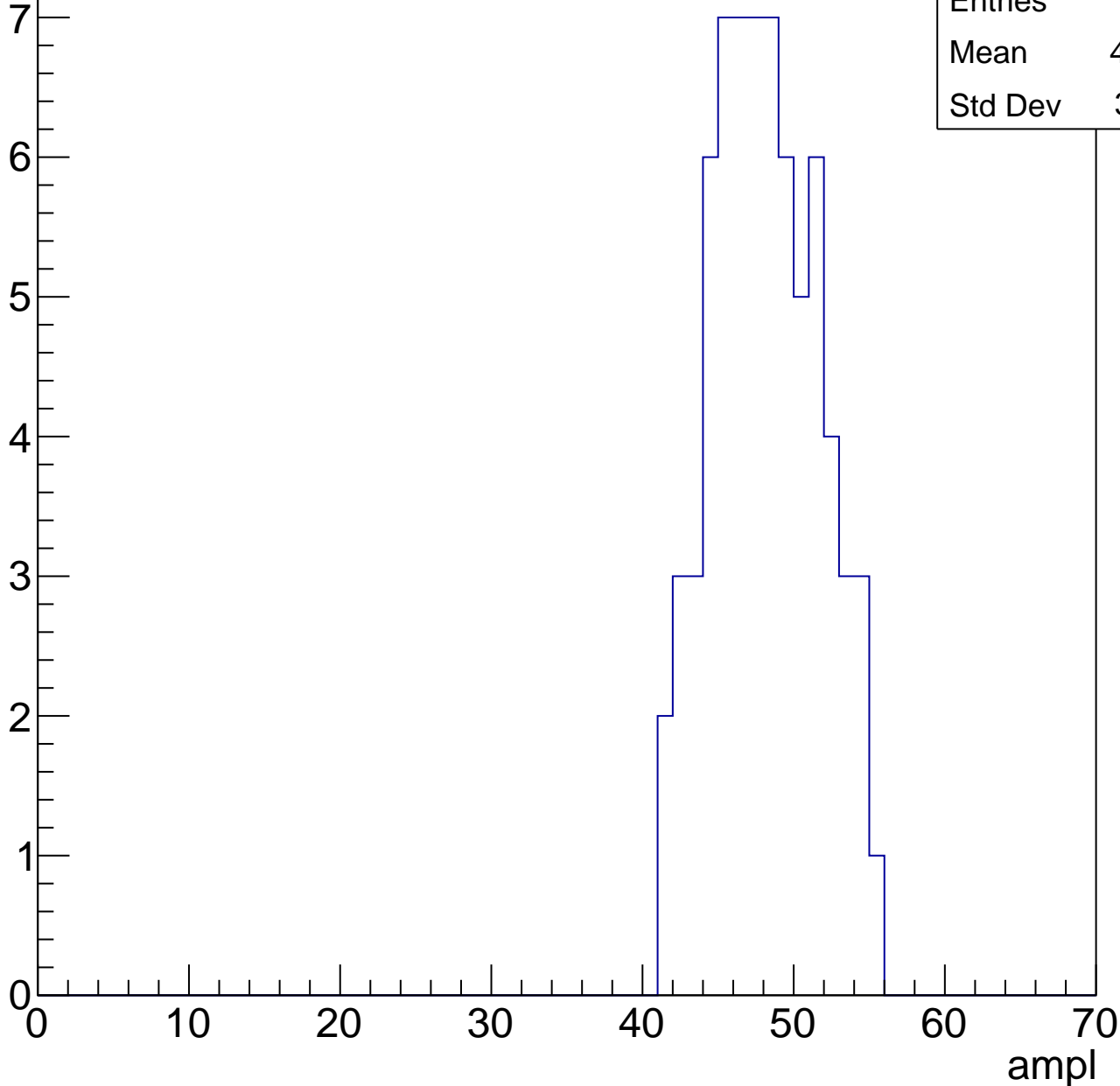


B1L103S, U23-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

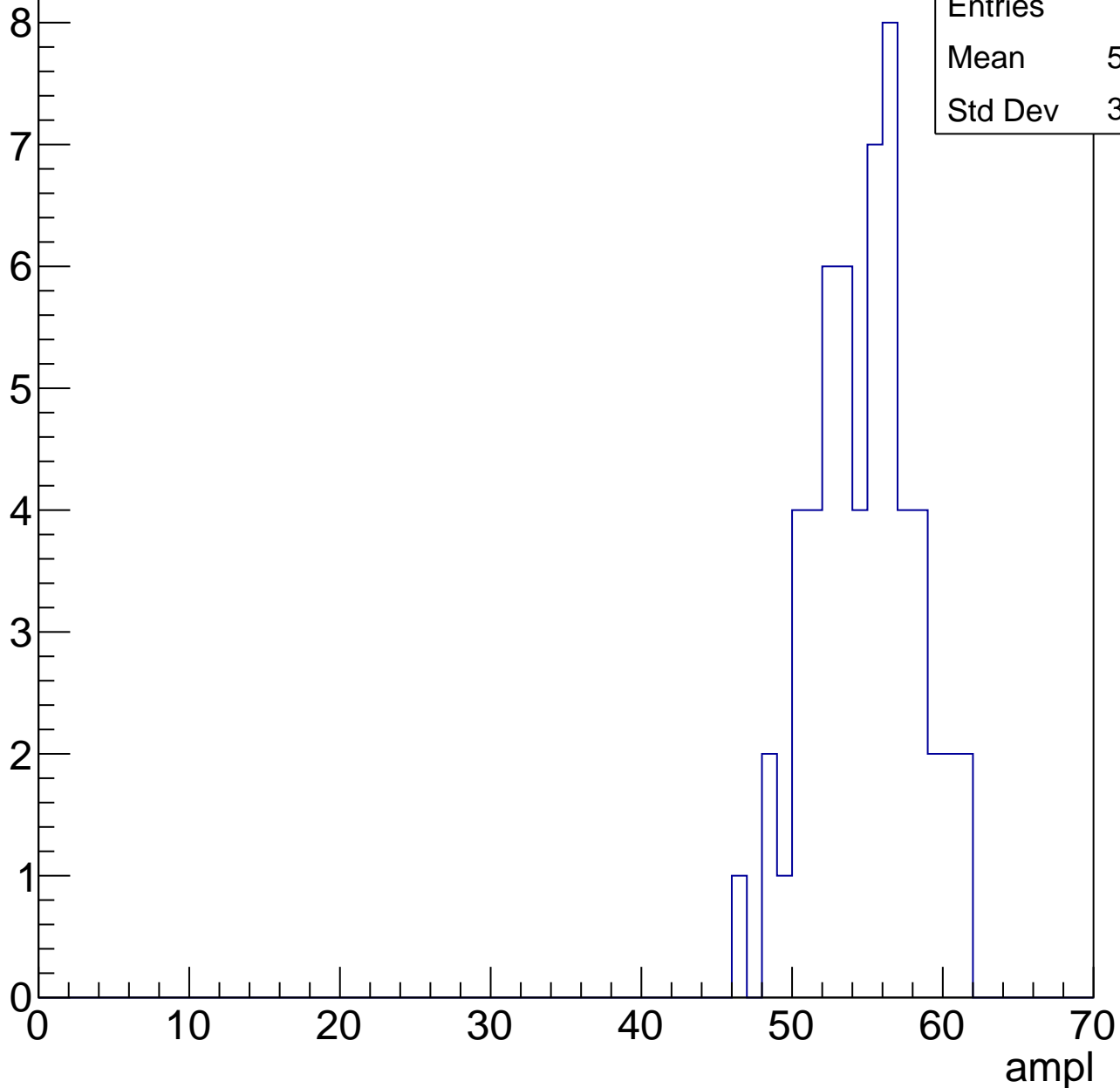
Entries	70
Mean	47.67
Std Dev	3.471



B1L103S, U23-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



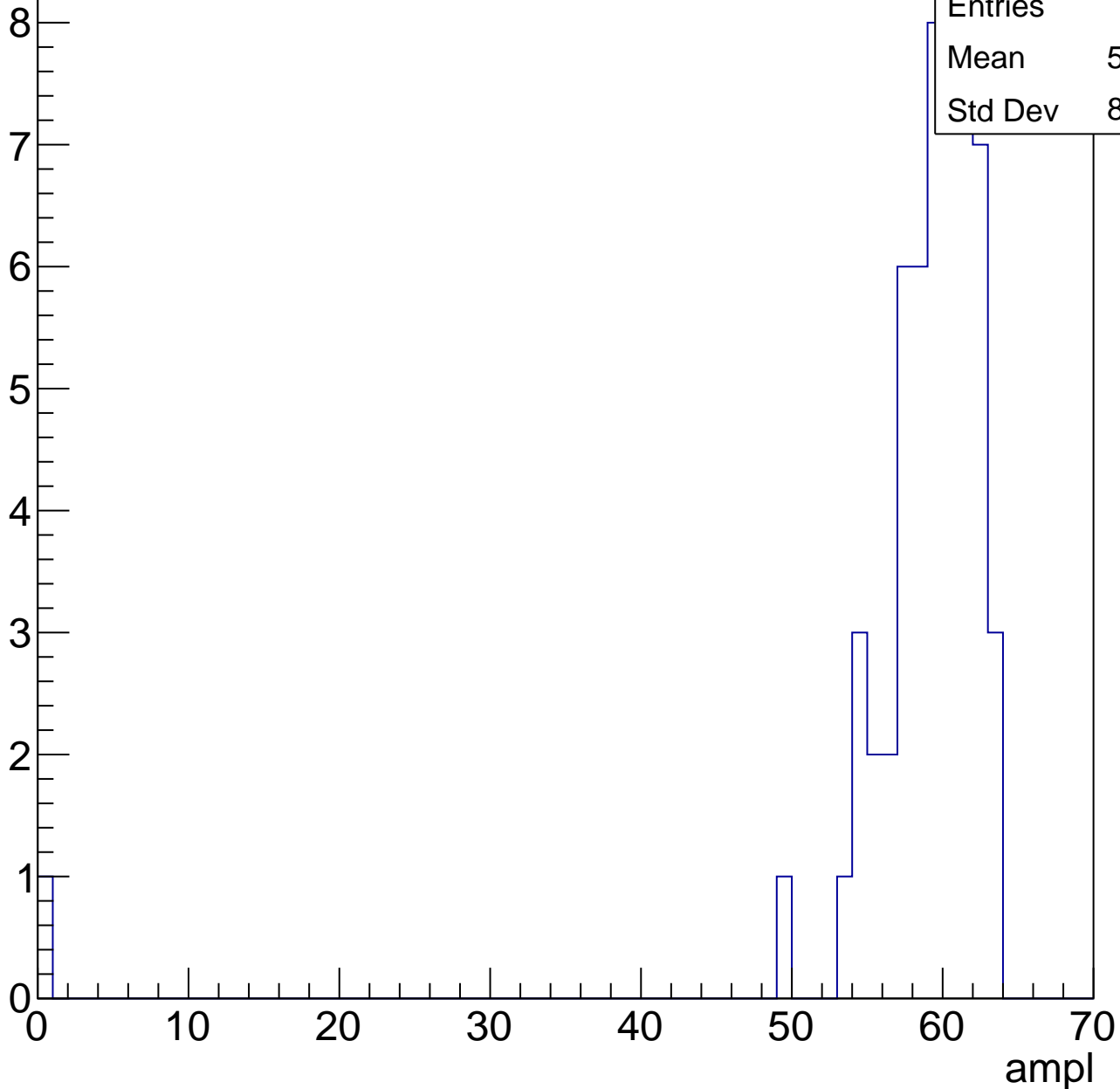
Entries	57
Mean	54.28
Std Dev	3.365

B1L103S, U23-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	57.84
Std Dev	8.289

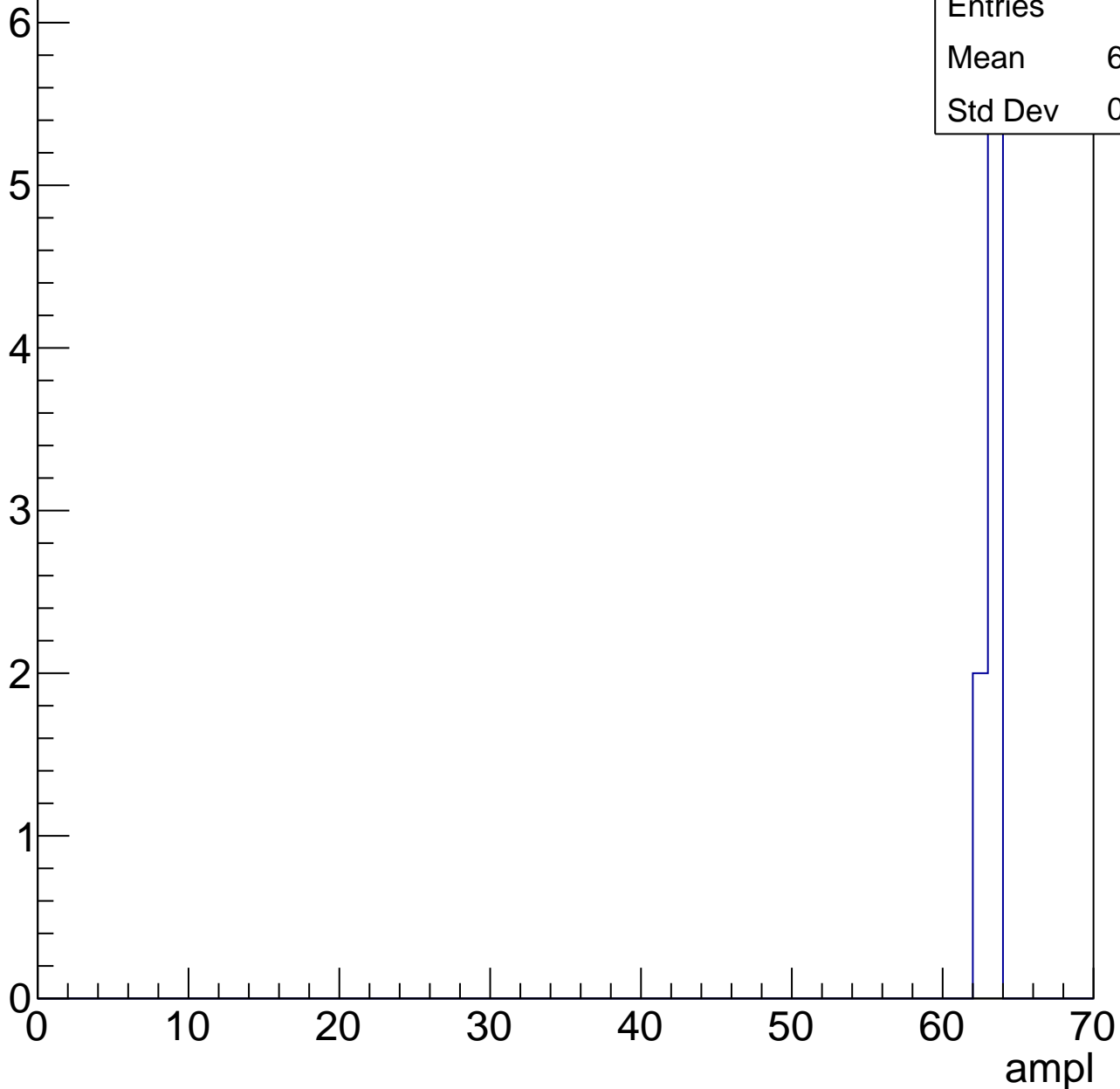


B1L103S, U23-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	62.75
Std Dev	0.433



B1L103S, U23-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch115, adc0

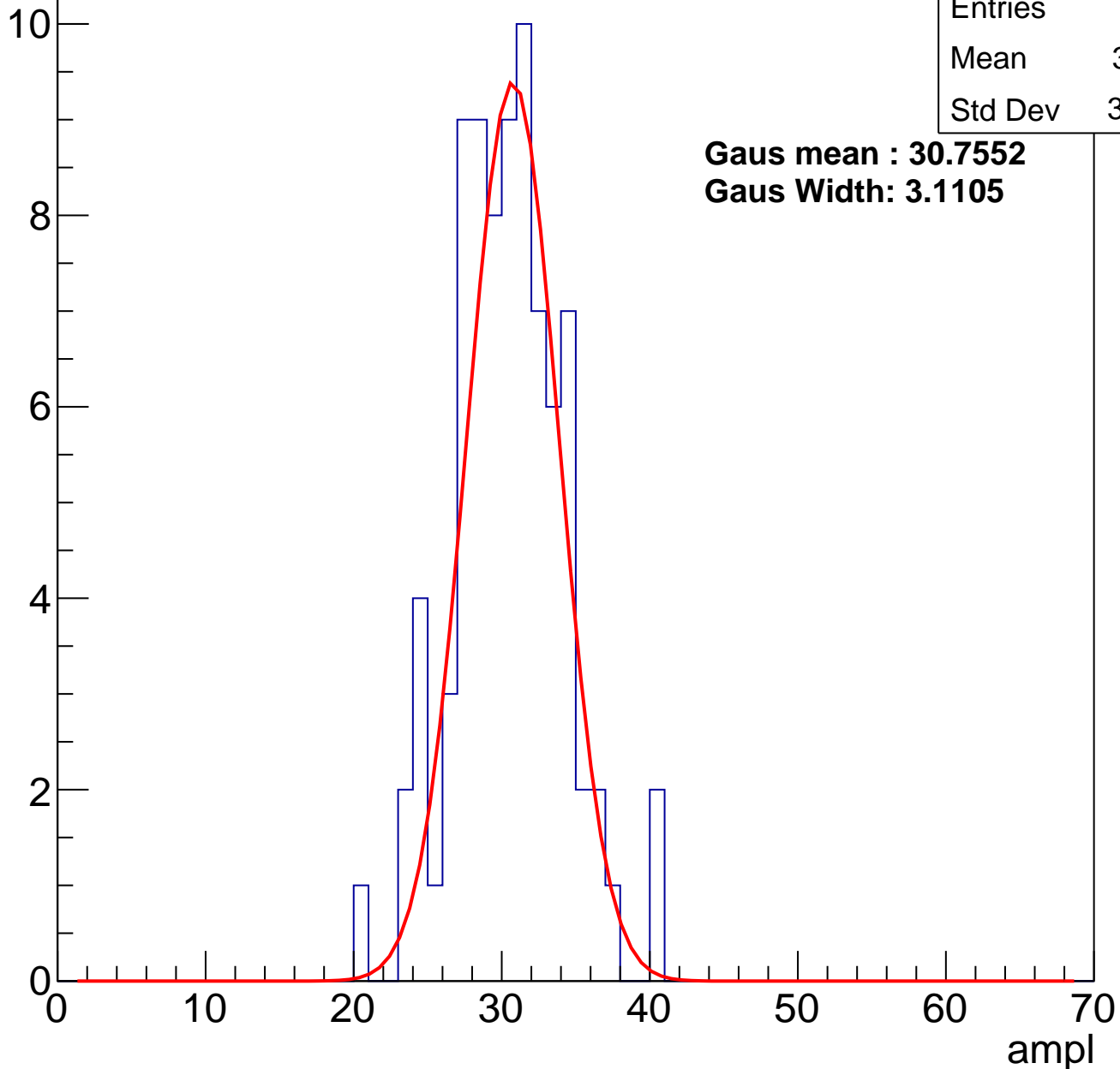
calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	30.01
Std Dev	3.665

Gaus mean : 30.7552

Gaus Width: 3.1105

Entry



B1L103S, U23-ch115, adc1

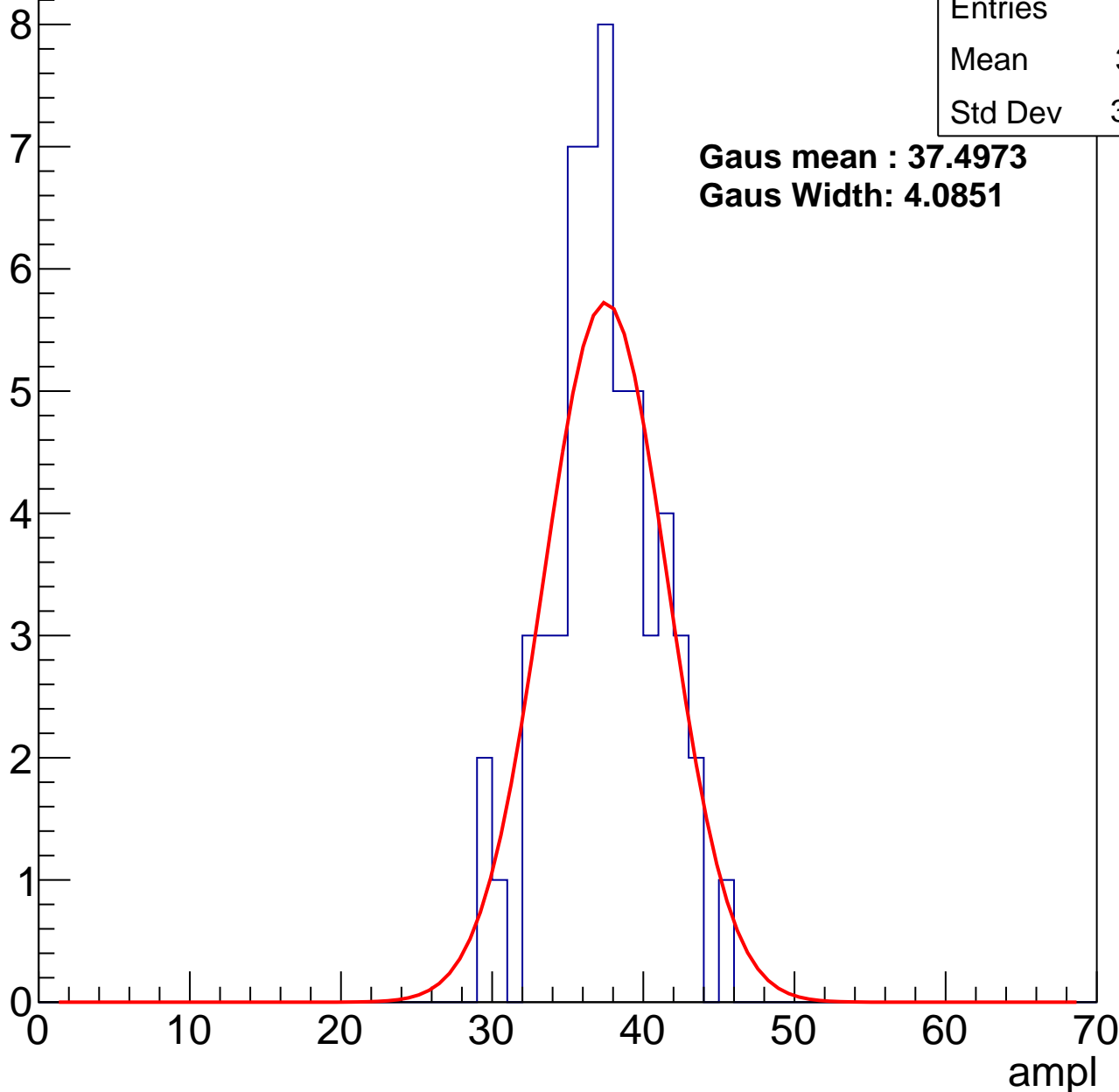
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.91
Std Dev	3.476

Gaus mean : 37.4973

Gaus Width: 4.0851



B1L103S, U23-ch115, adc2

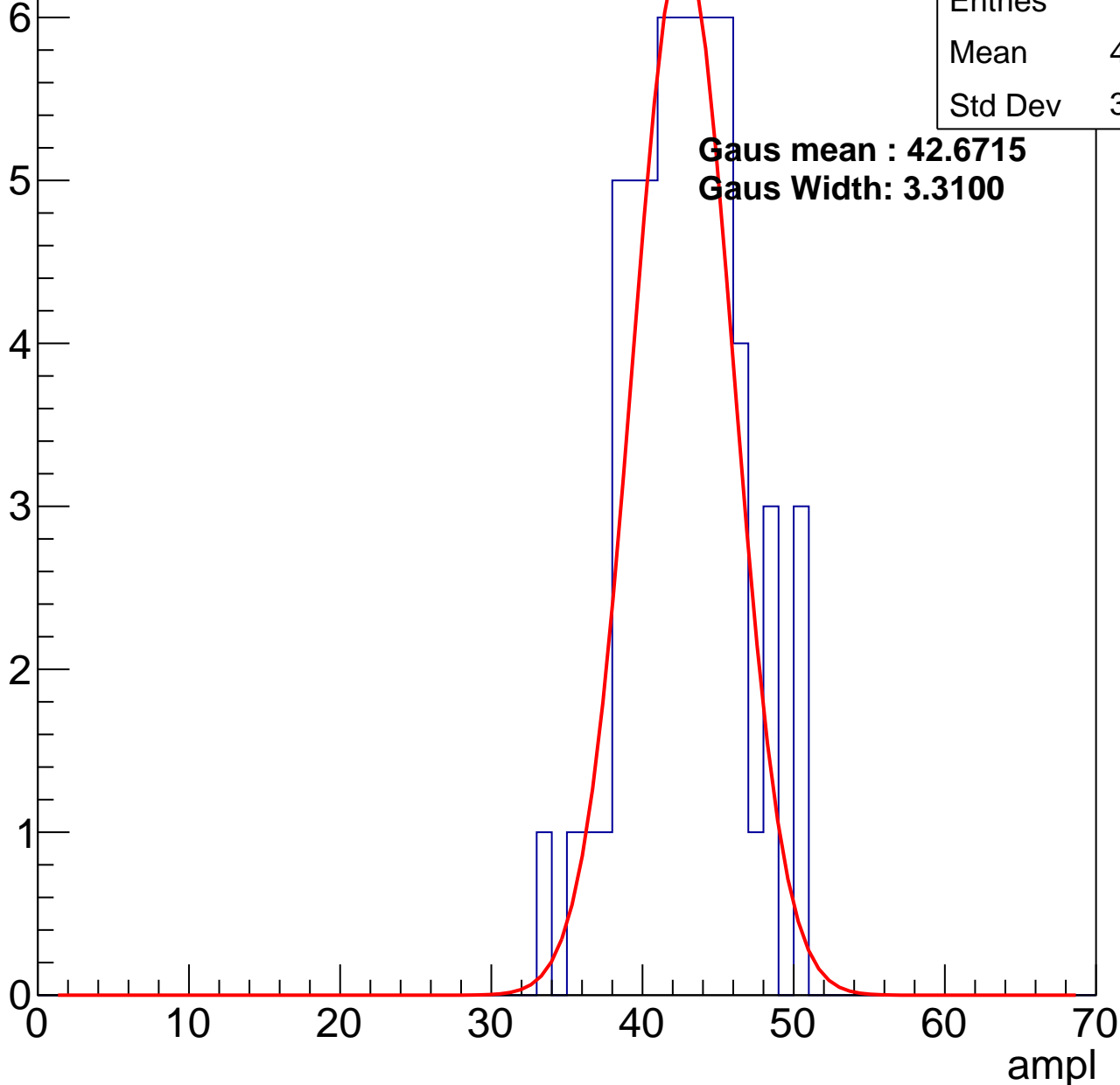
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.35
Std Dev	3.669

Gaus mean : 42.6715

Gaus Width: 3.3100

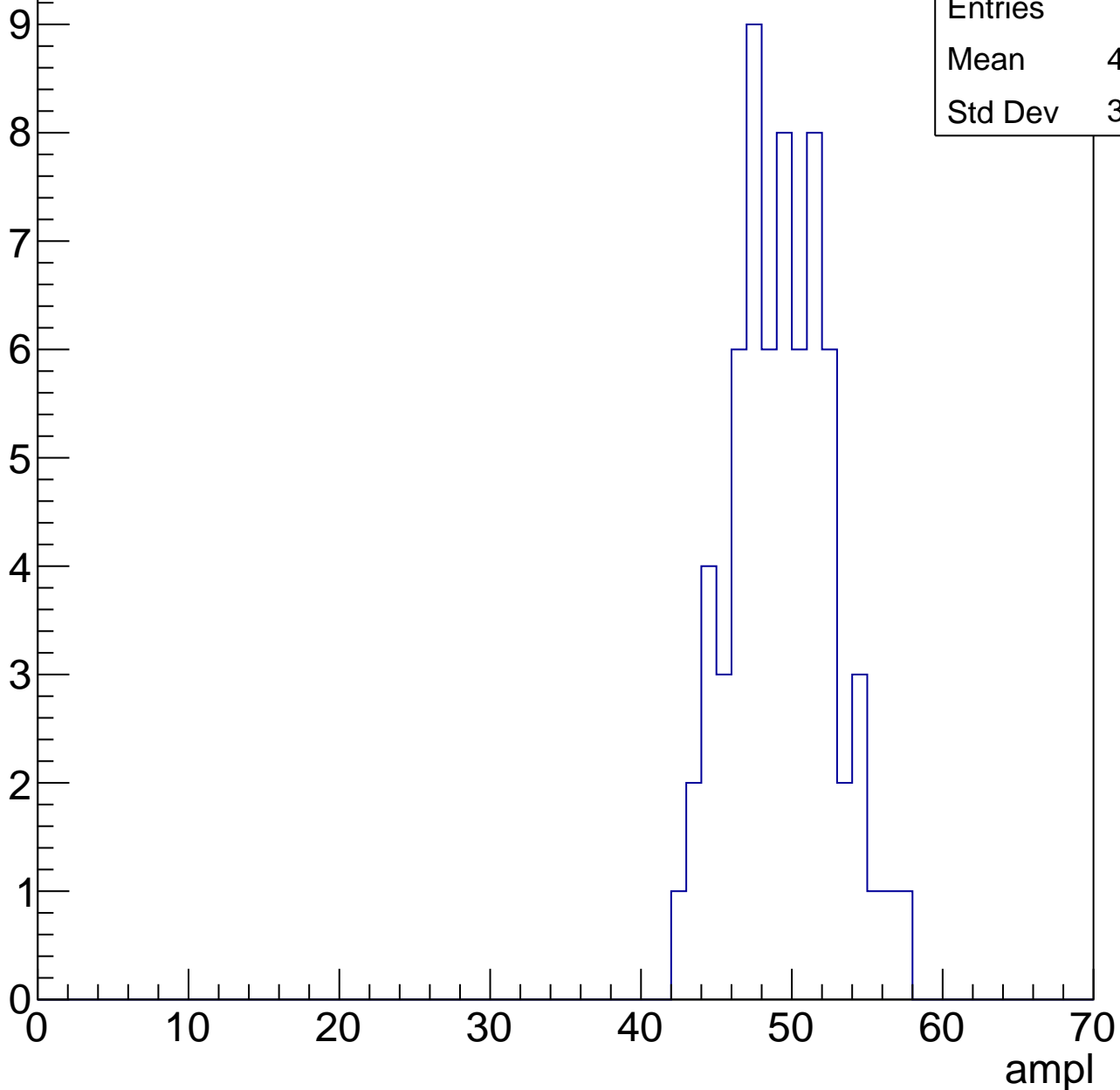


B1L103S, U23-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.87
Std Dev	3.259

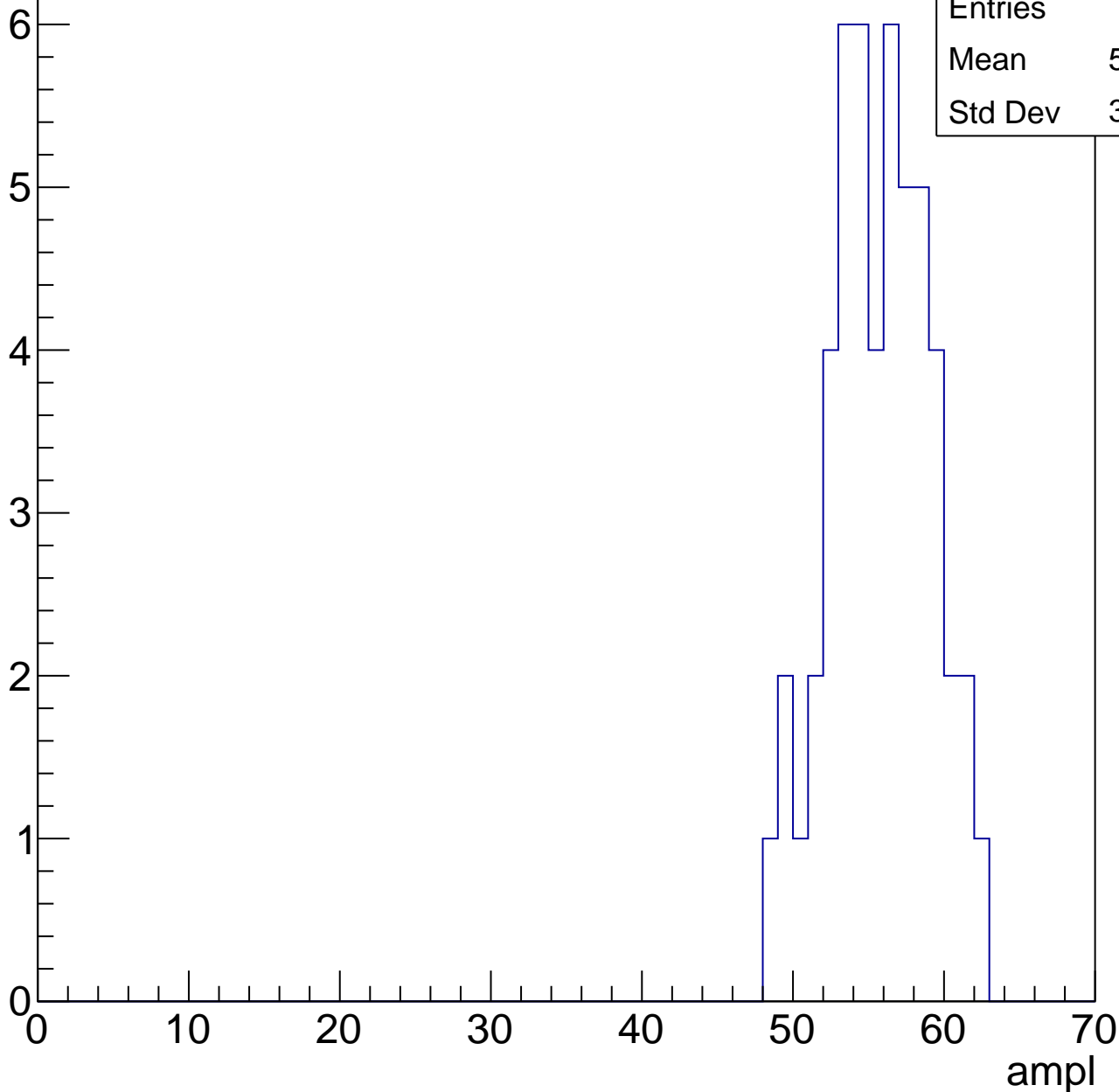


B1L103S, U23-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	55.27
Std Dev	3.272

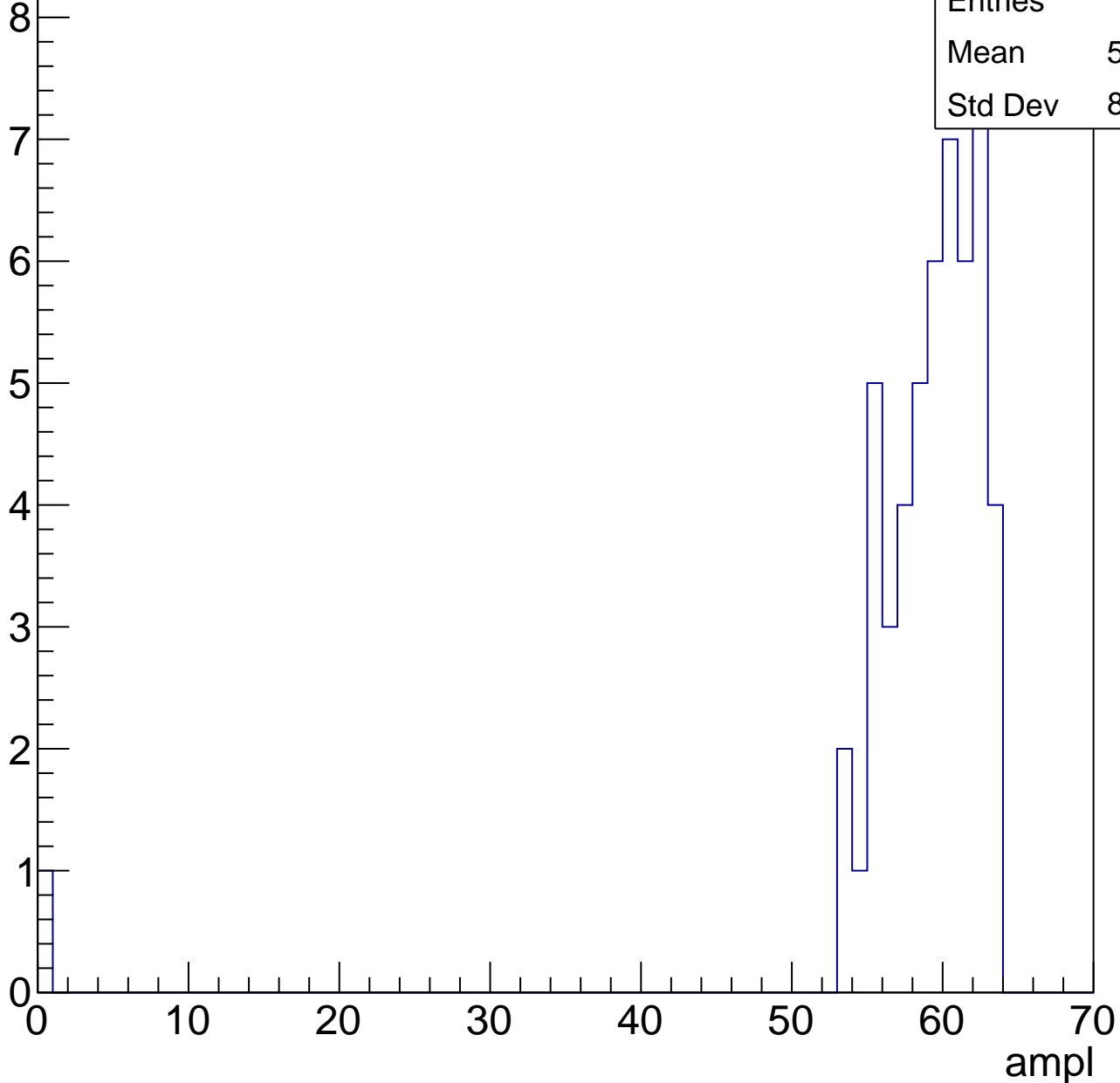


B1L103S, U23-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	57.87
Std Dev	8.555

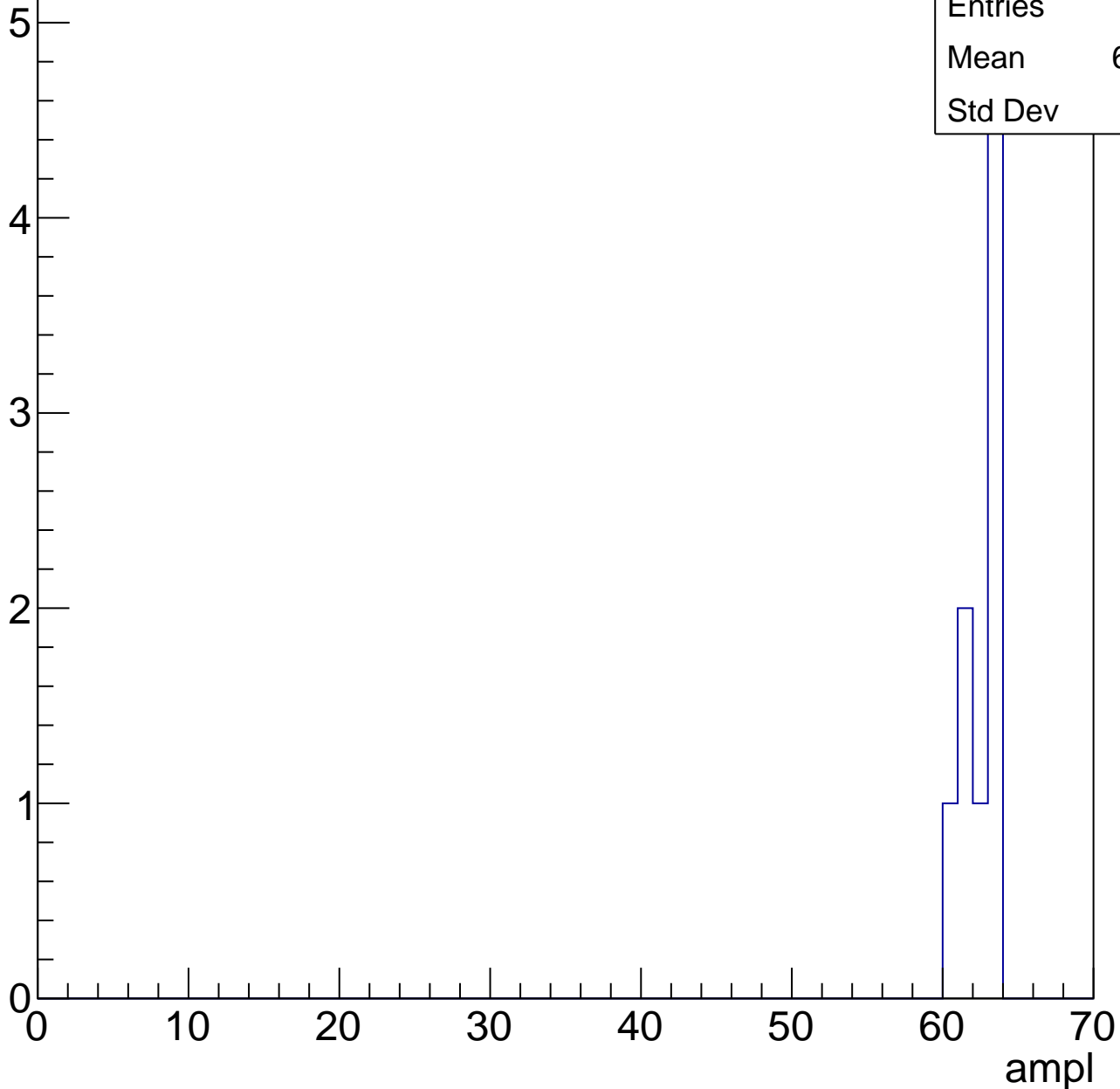


B1L103S, U23-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

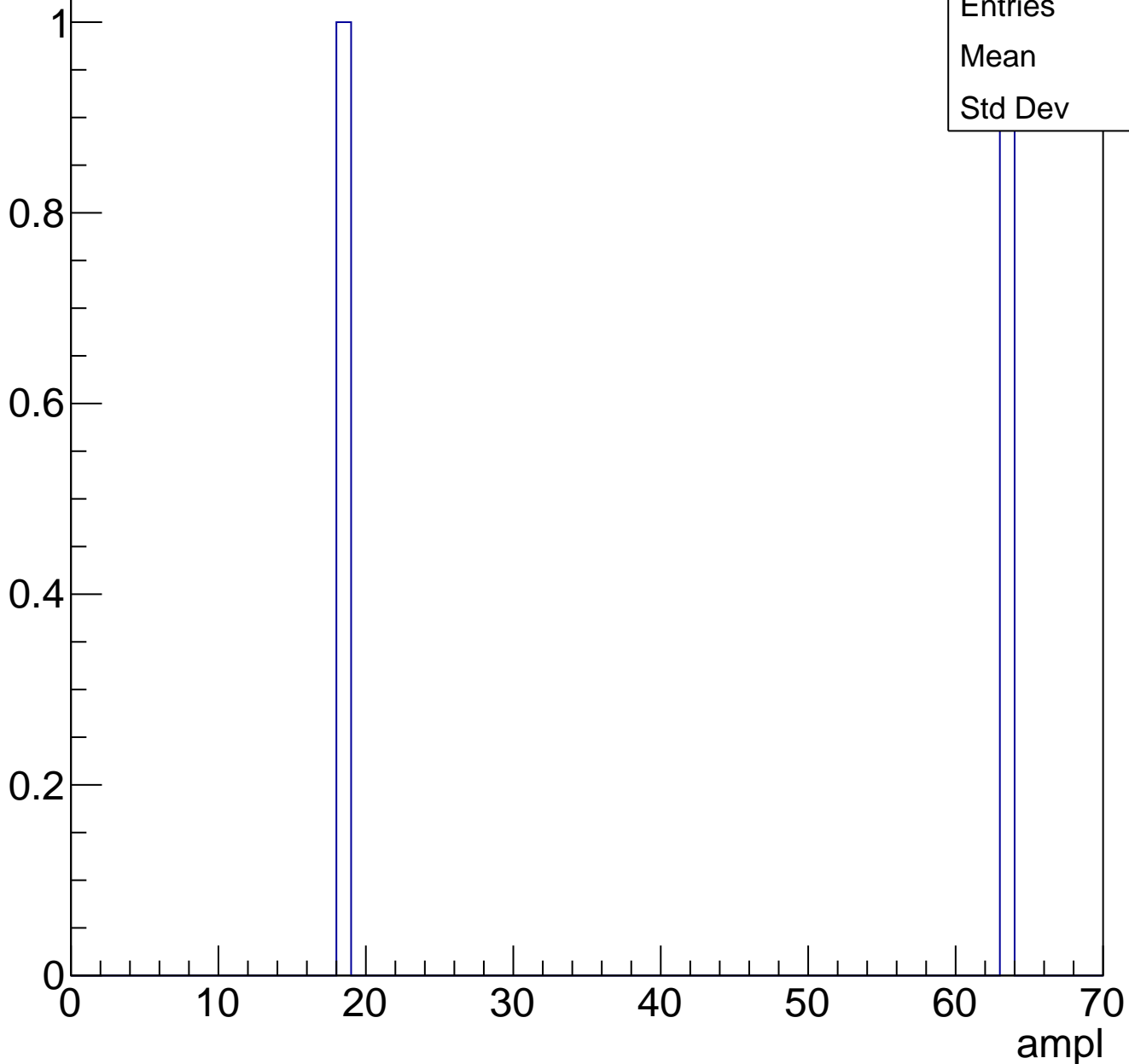
Entries	9
Mean	62.11
Std Dev	1.1



B1L103S, U23-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	40.5
Std Dev	22.5

B1L103S, U23-ch116, adc0

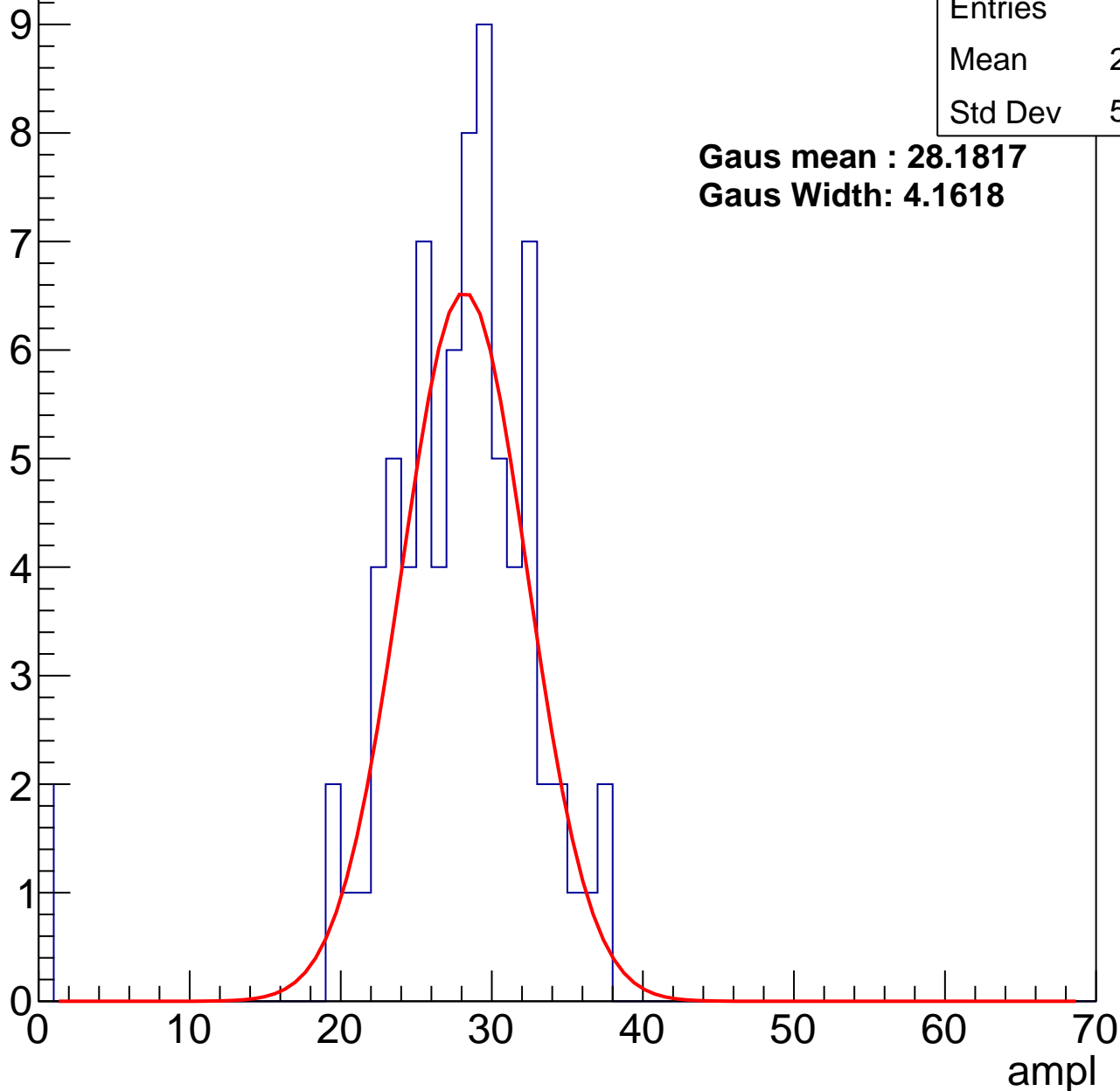
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	27.03
Std Dev	5.989

Gaus mean : 28.1817

Gaus Width: 4.1618



B1L103S, U23-ch116, adc1

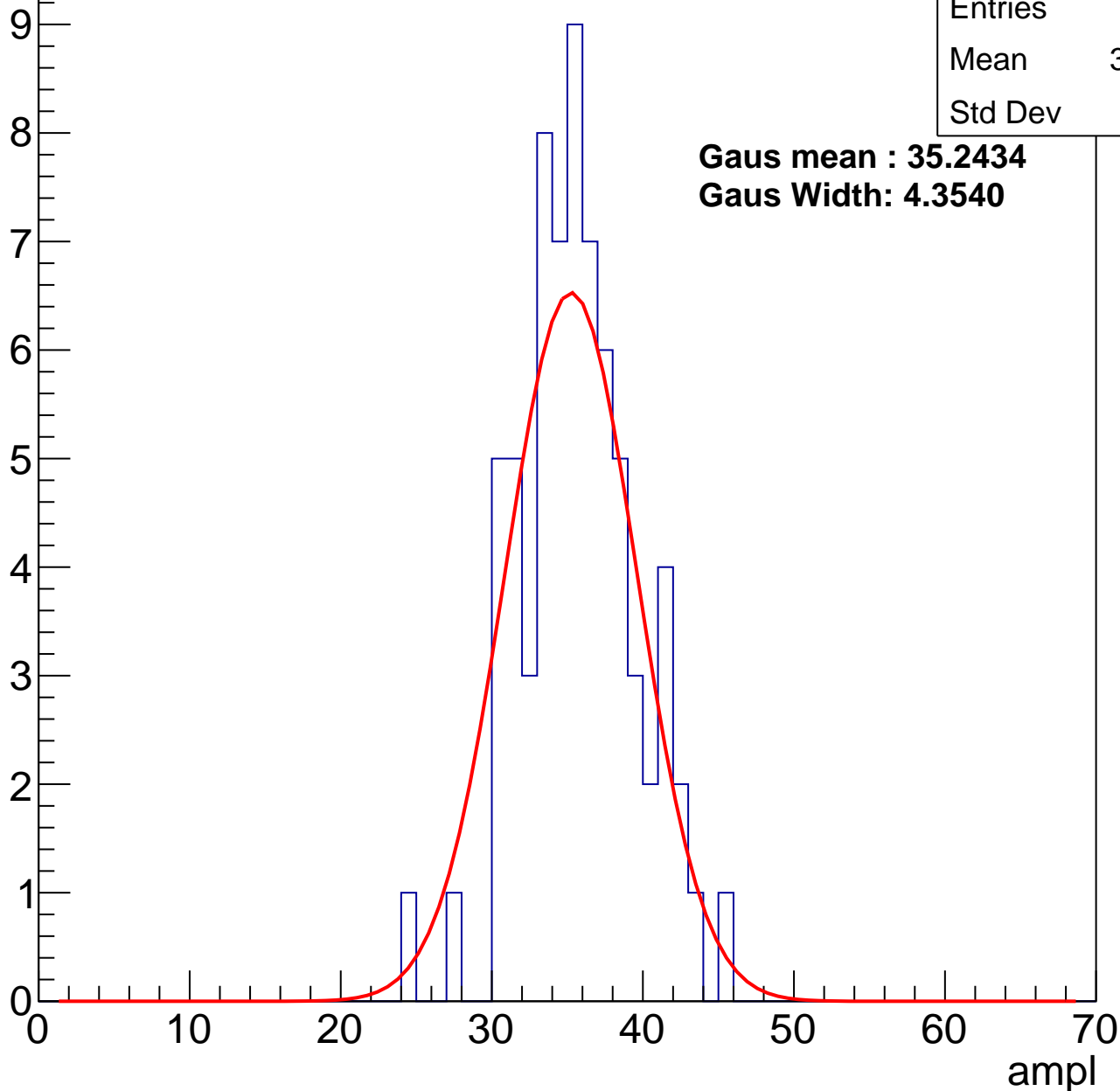
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.23
Std Dev	3.84

Gaus mean : 35.2434

Gaus Width: 4.3540



B1L103S, U23-ch116, adc2

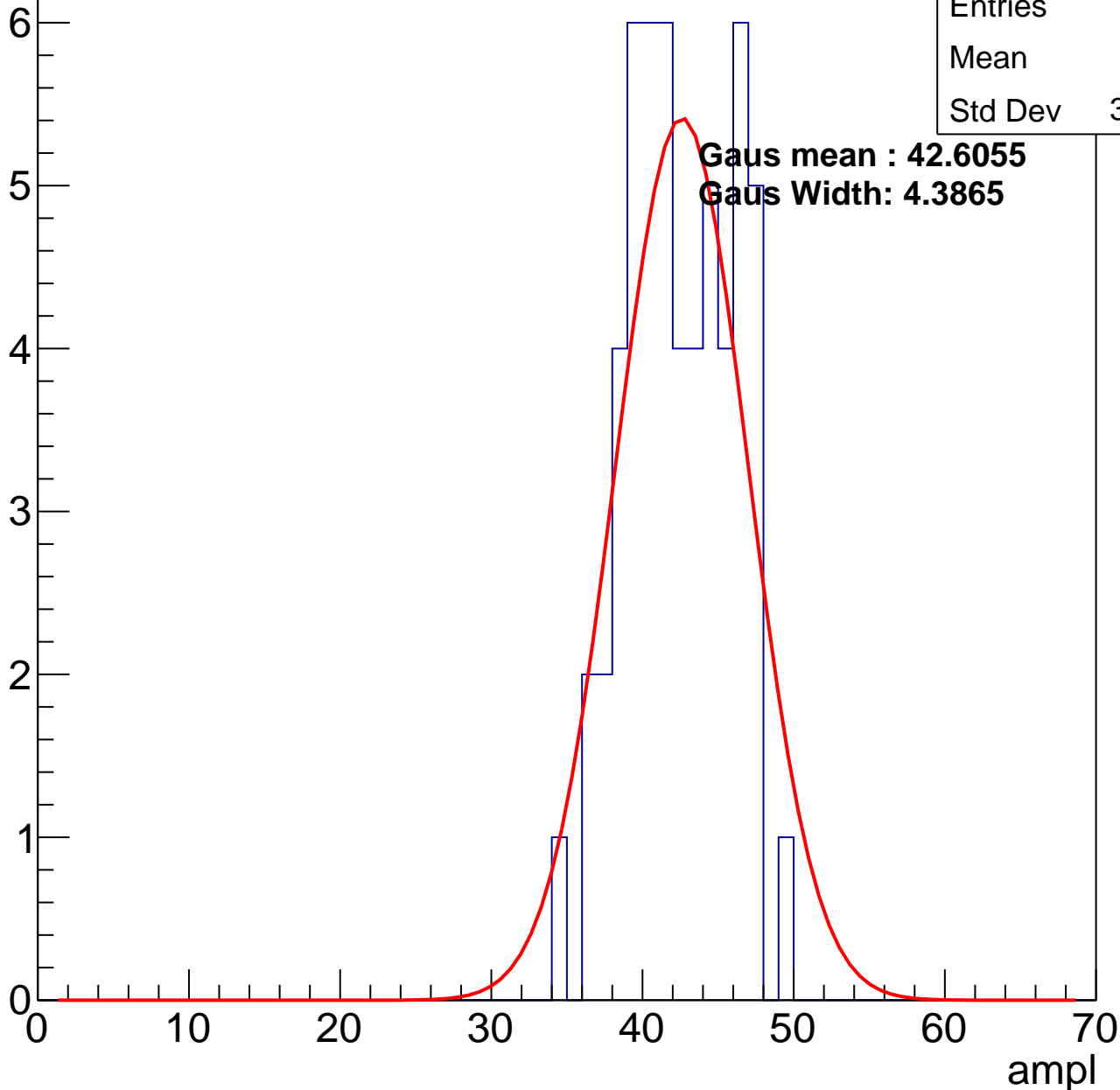
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	42
Std Dev	3.443

Gaus mean : 42.6055

Gaus Width: 4.3865

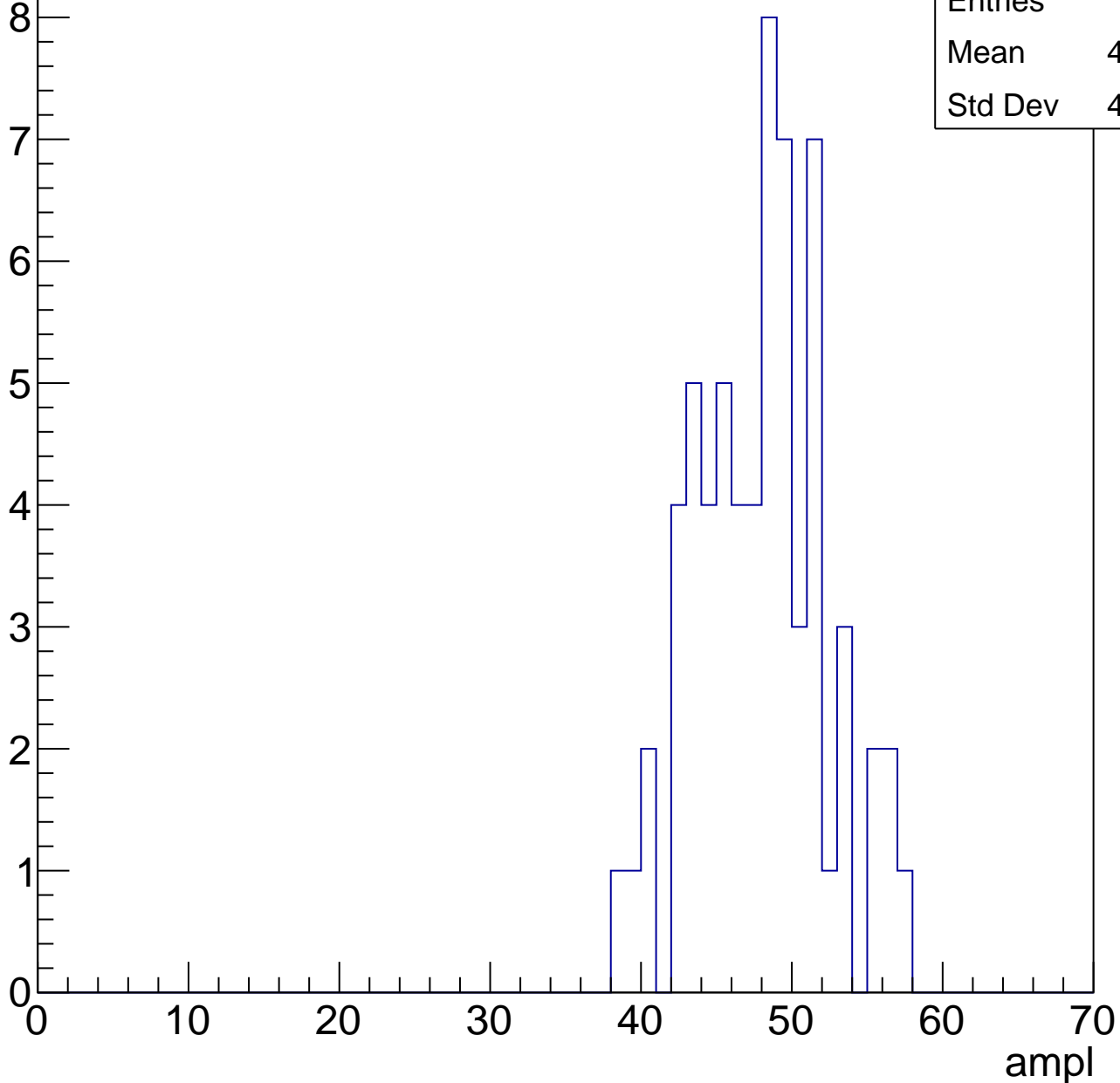


B1L103S, U23-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	47.45
Std Dev	4.272

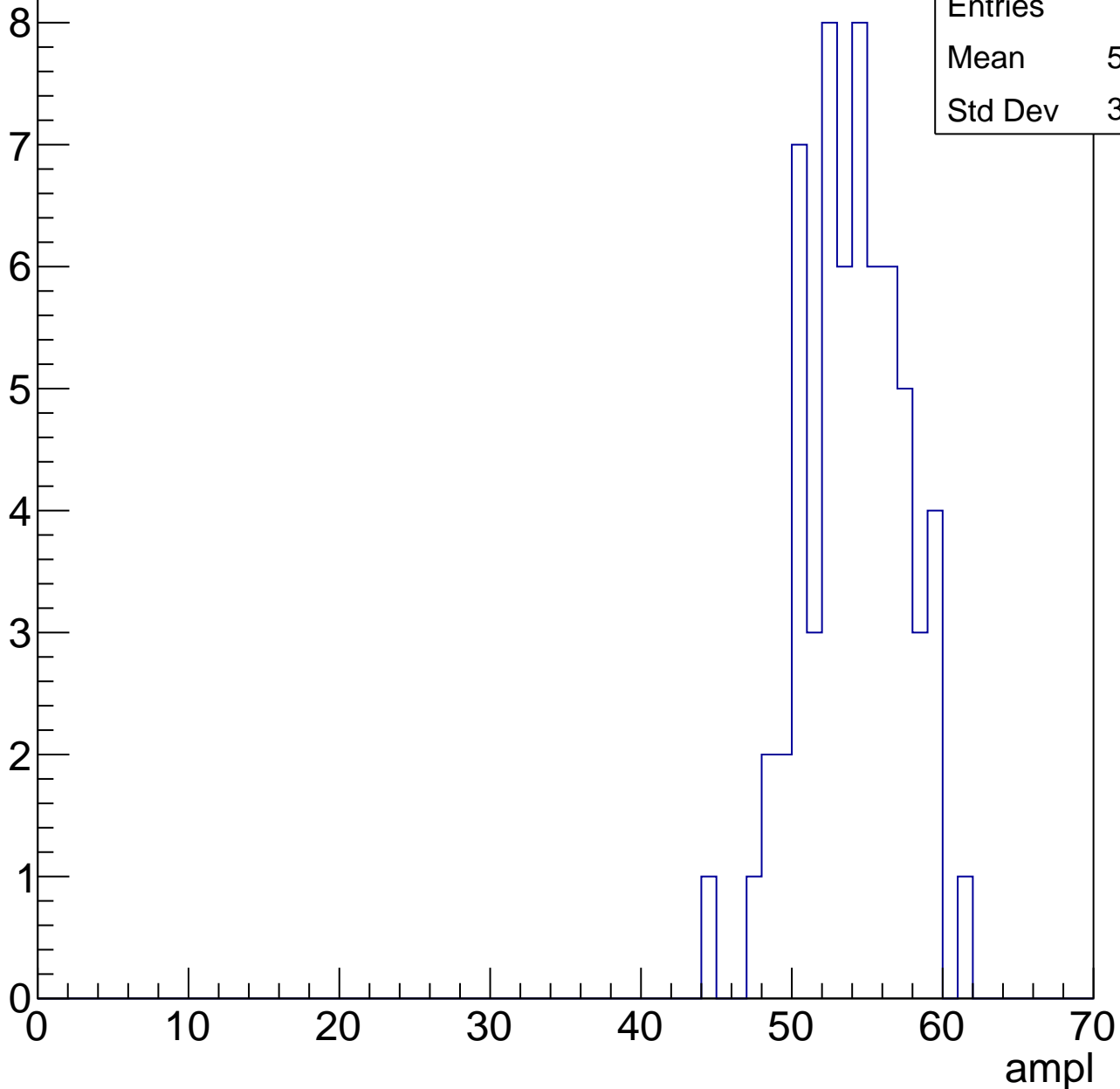


B1L103S, U23-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

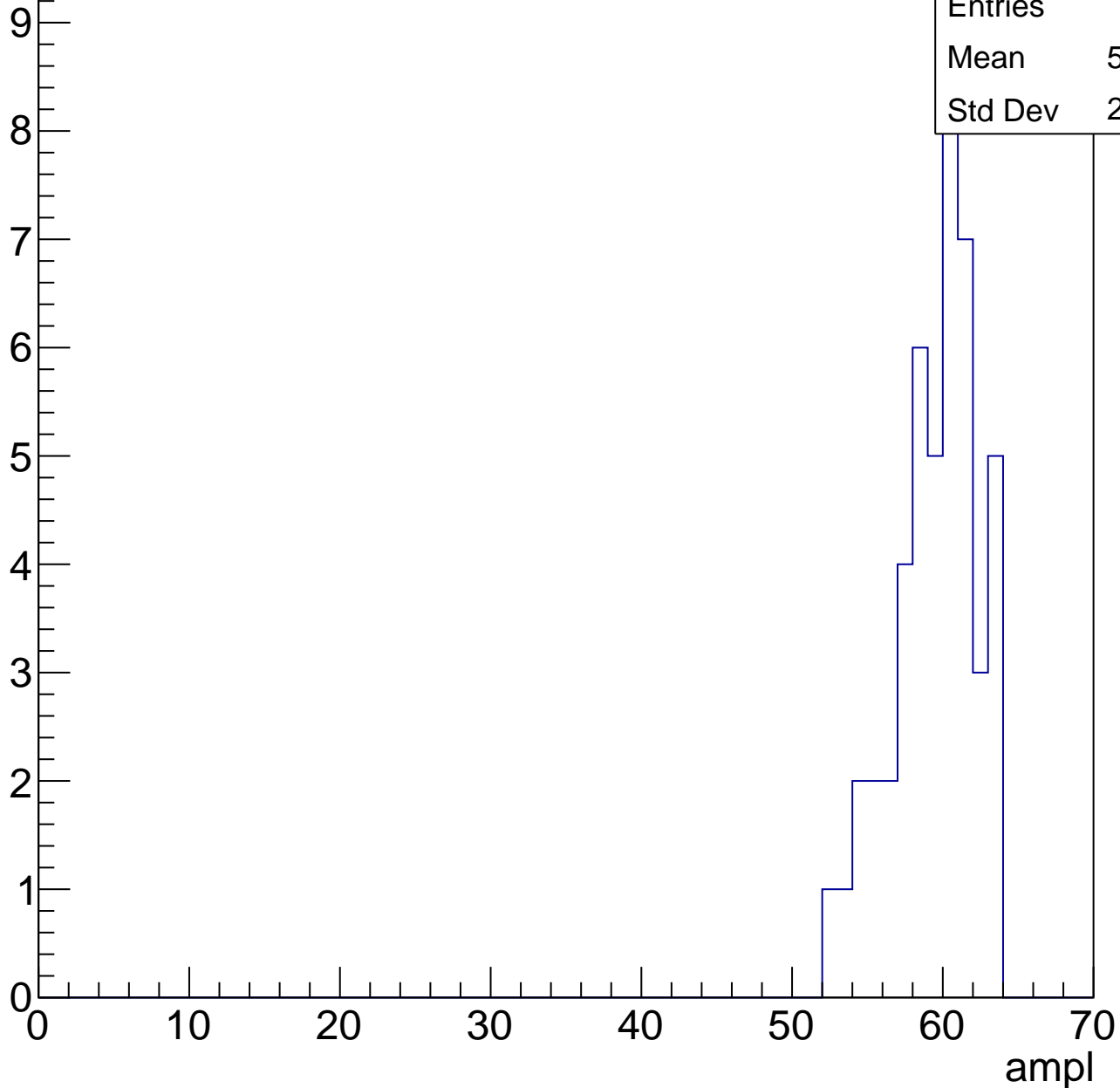
Entries	63
Mean	53.59
Std Dev	3.355



B1L103S, U23-ch116, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

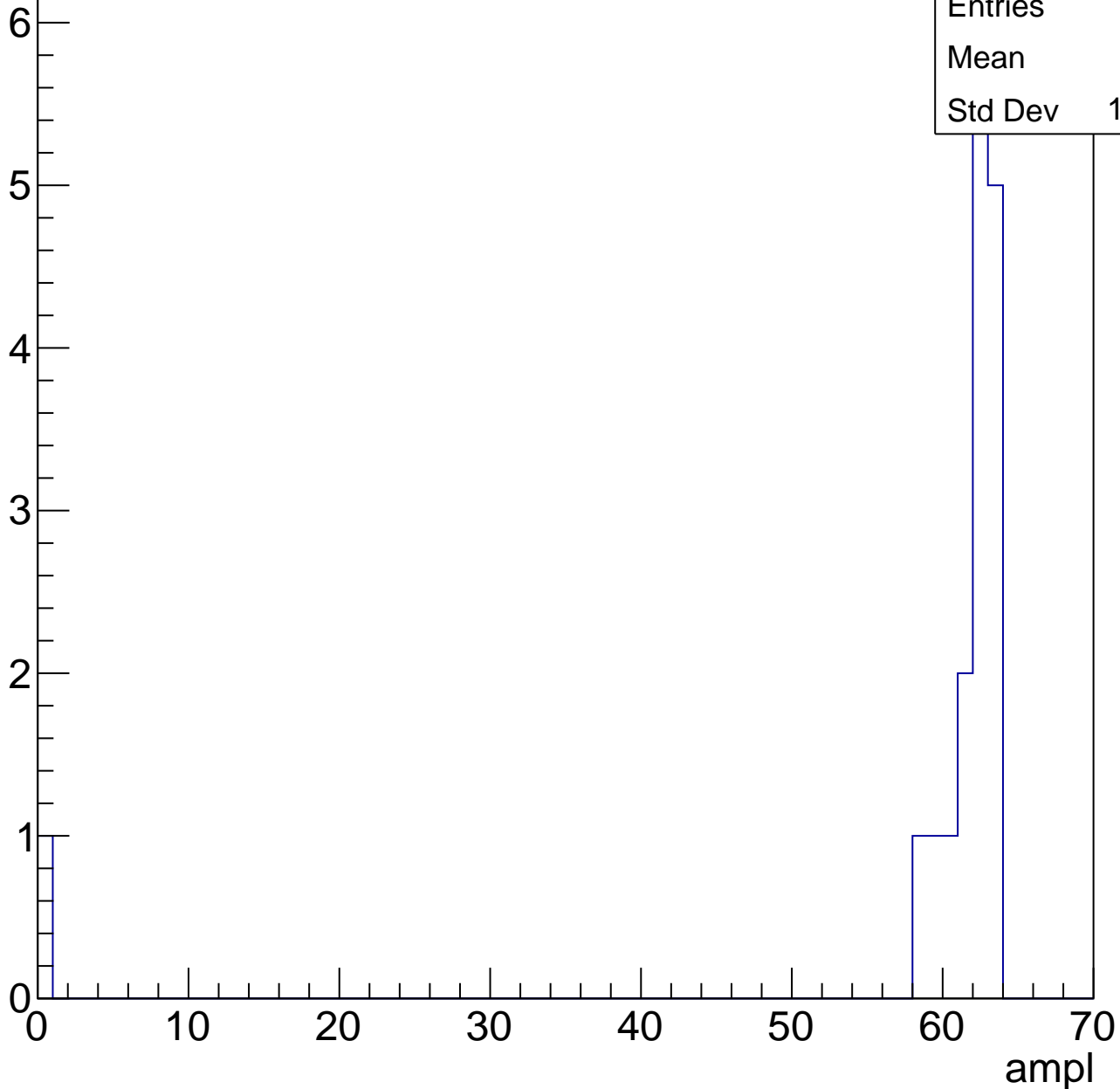


B1L103S, U23-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	58
Std Dev	14.57



B1L103S, U23-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	62
Std Dev	0

B1L103S, U23-ch117, adc0

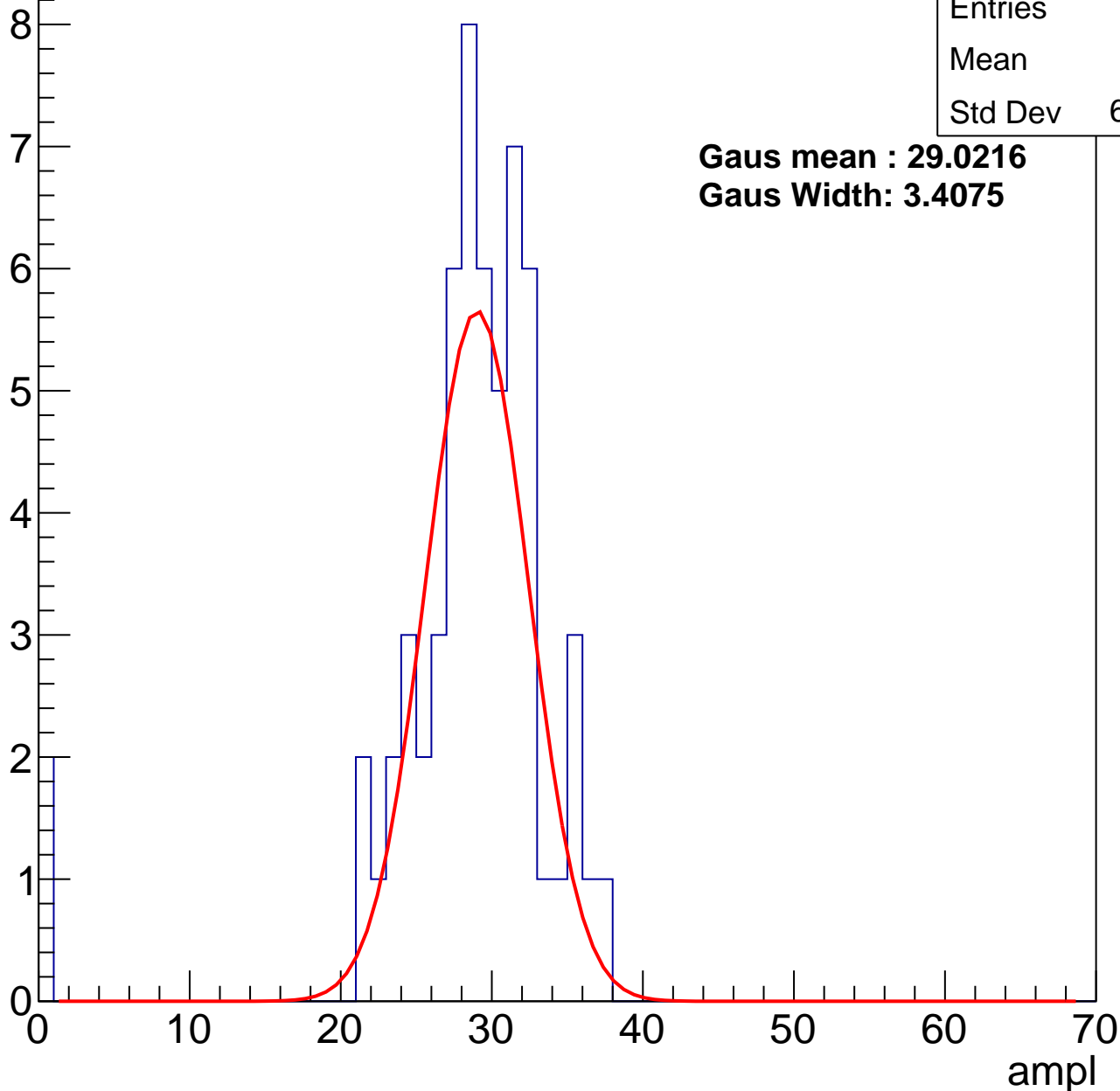
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	27.9
Std Dev	6.289

Gaus mean : 29.0216

Gaus Width: 3.4075



B1L103S, U23-ch117, adc1

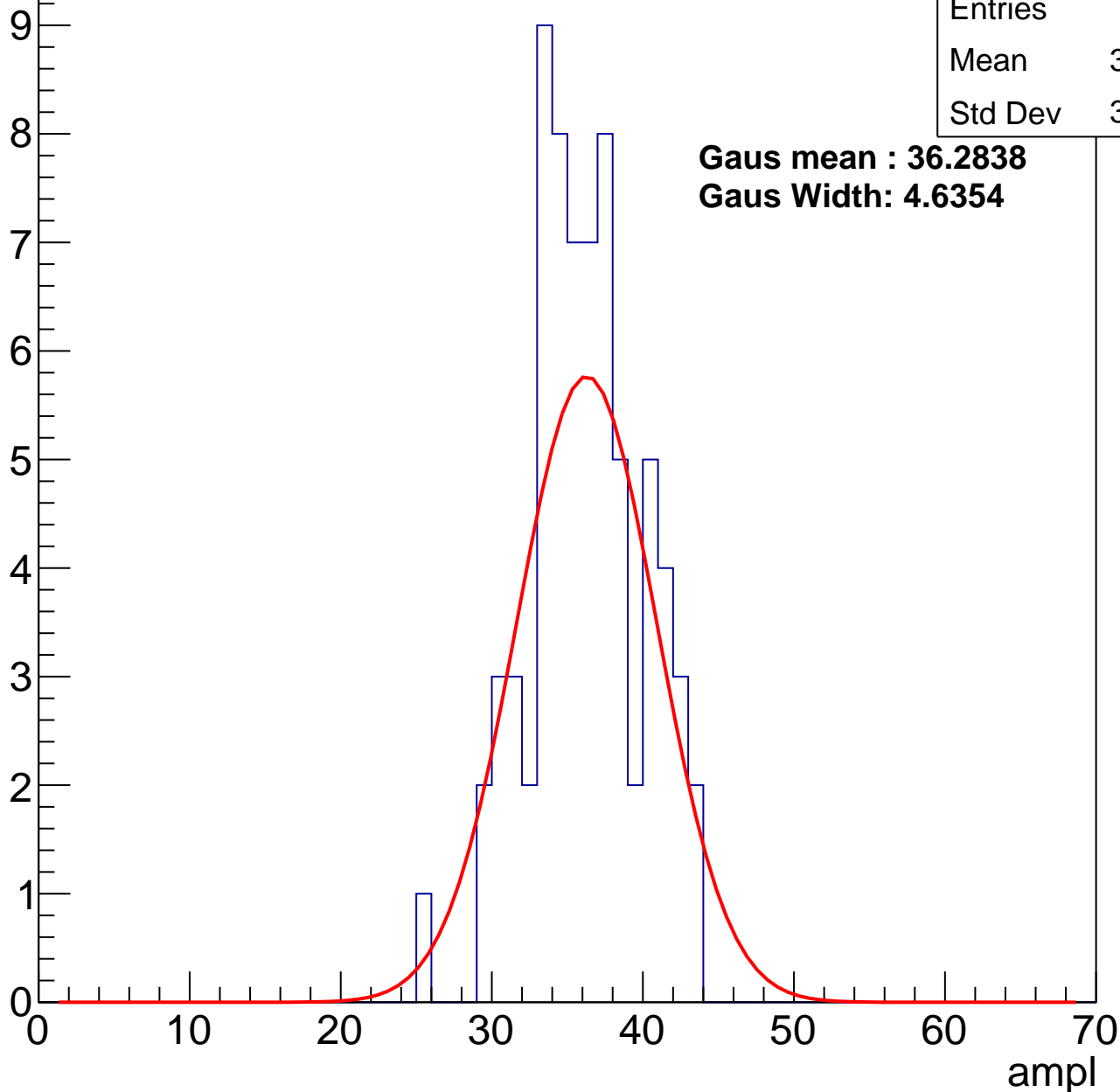
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.72
Std Dev	3.716

Gaus mean : 36.2838

Gaus Width: 4.6354



B1L103S, U23-ch117, adc2

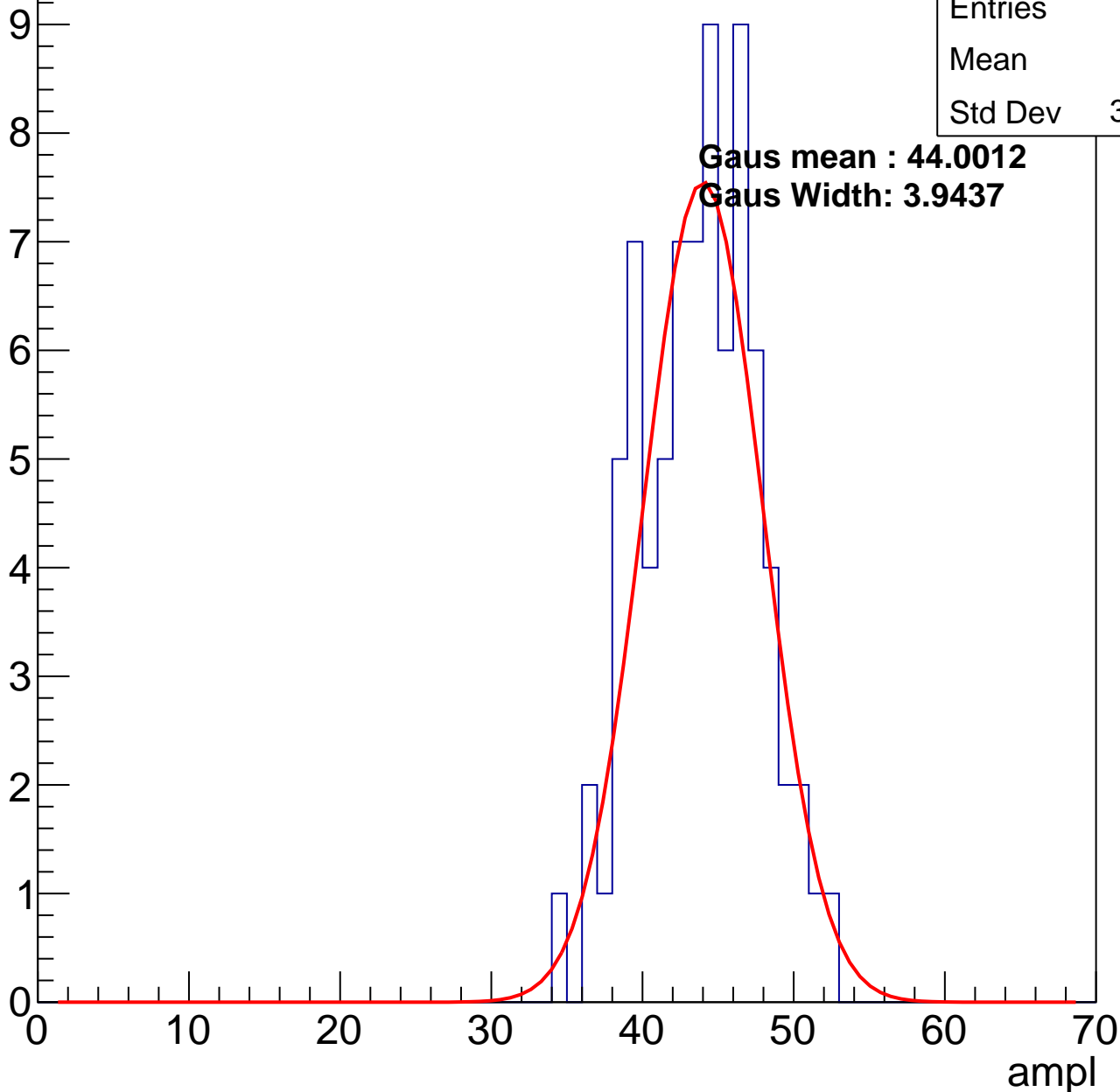
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	43.3
Std Dev	3.787

Gaus mean : 44.0012

Gaus Width: 3.9437

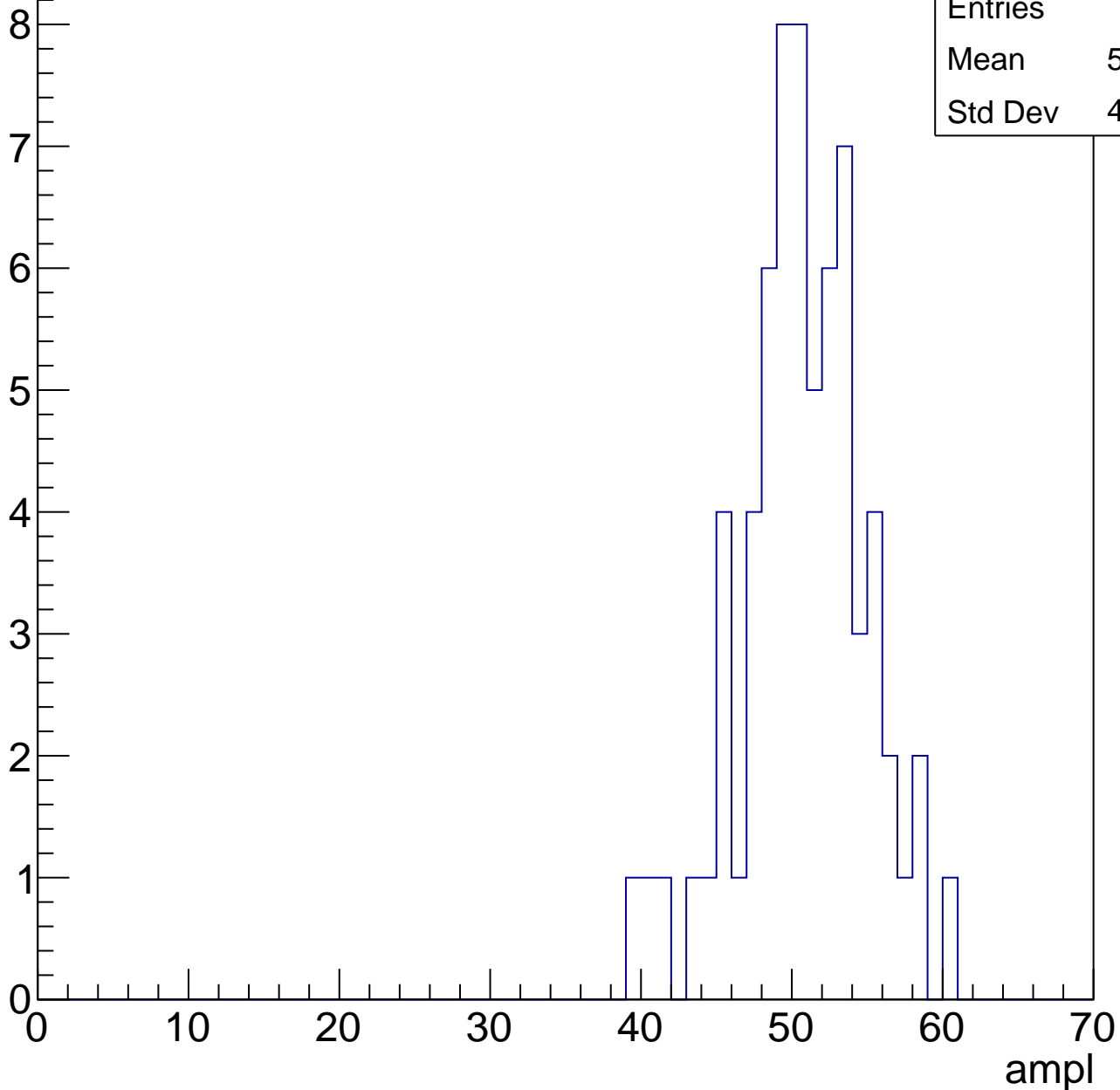


B1L103S, U23-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	50.24
Std Dev	4.154

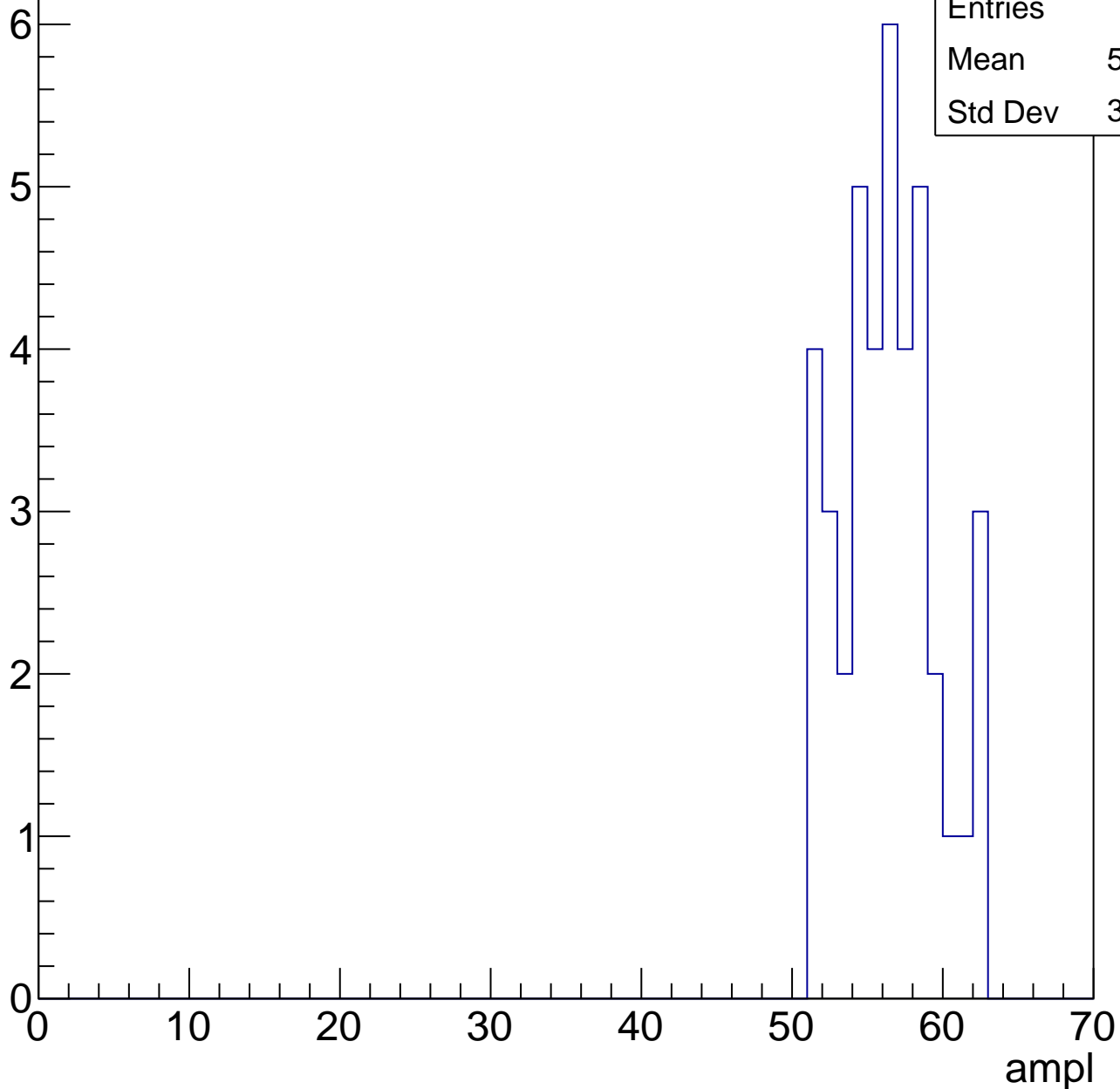


B1L103S, U23-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

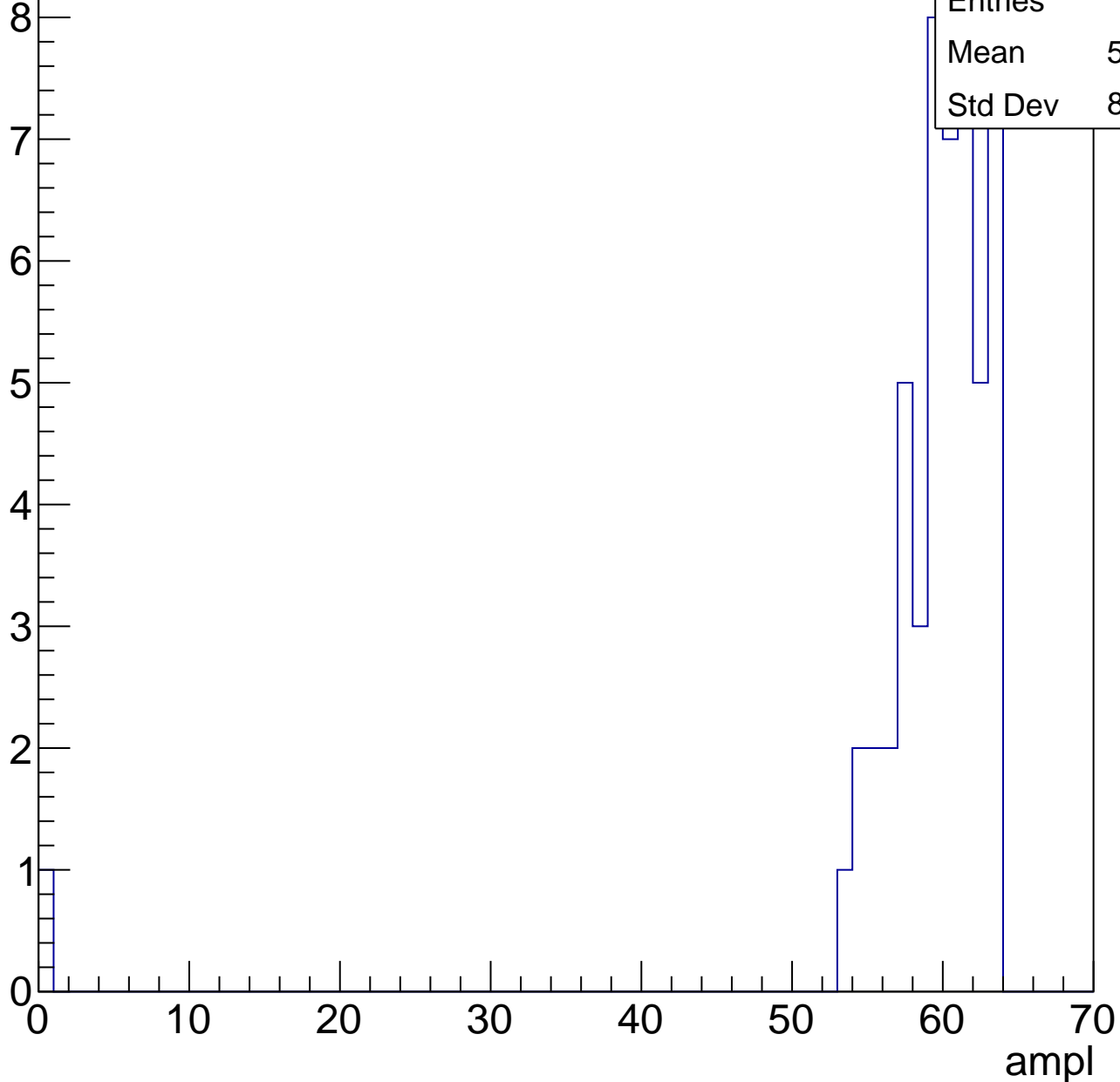
Entries	40
Mean	55.88
Std Dev	3.084



B1L103S, U23-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch117, adc6

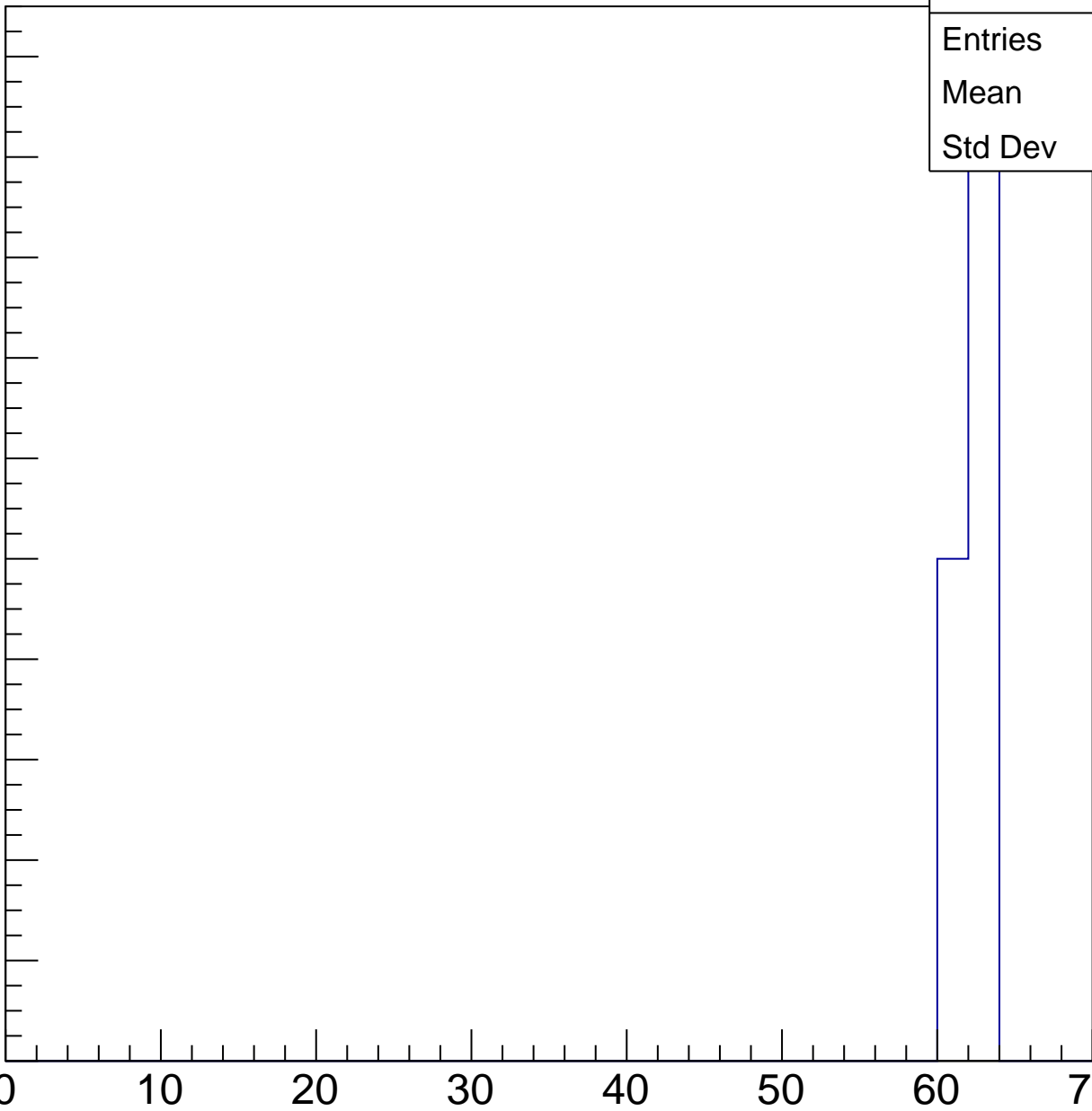
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl



B1L103S, U23-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch118, adc0

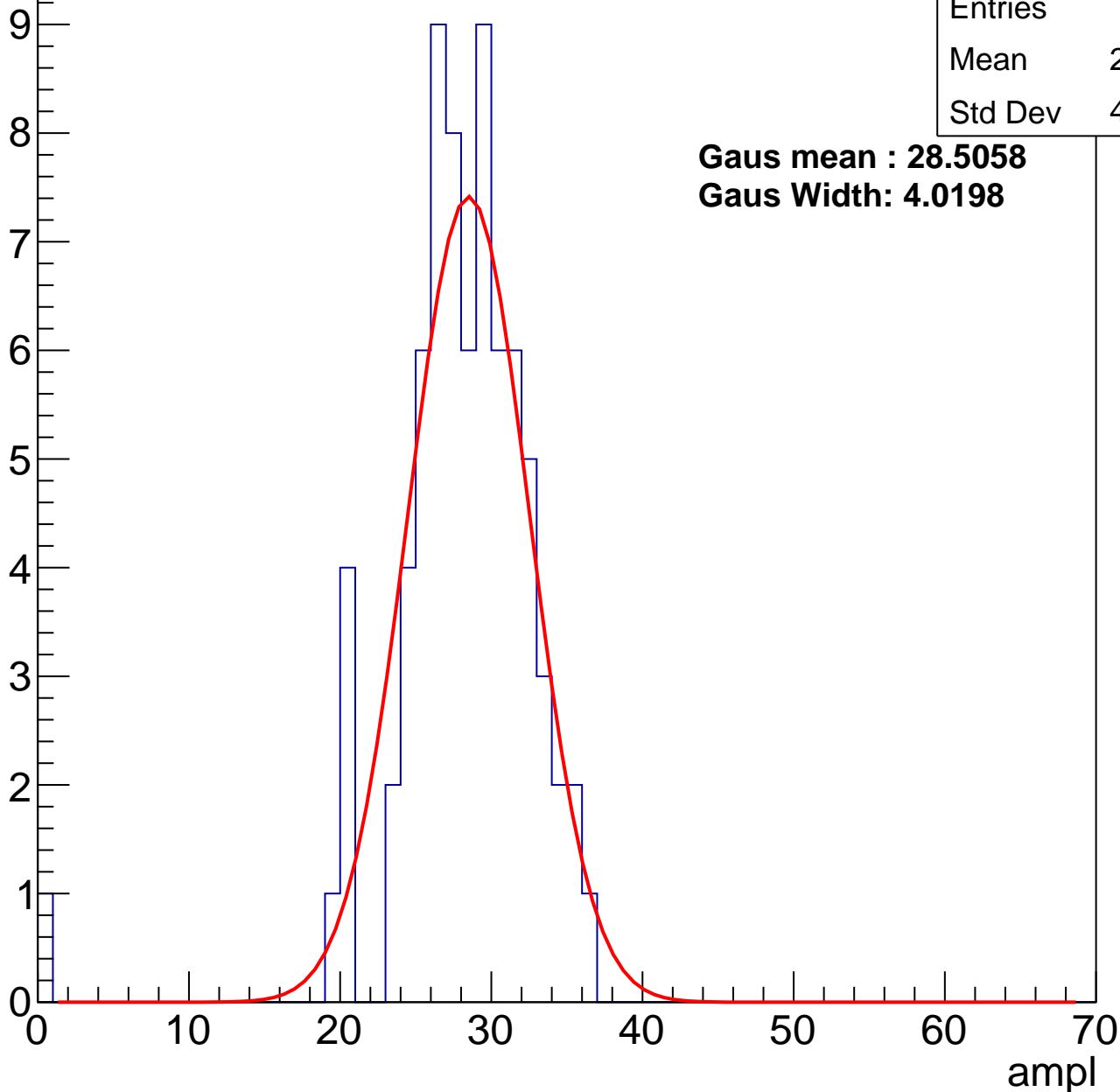
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	27.59
Std Dev	4.894

Gaus mean : 28.5058

Gaus Width: 4.0198



B1L103S, U23-ch118, adc1

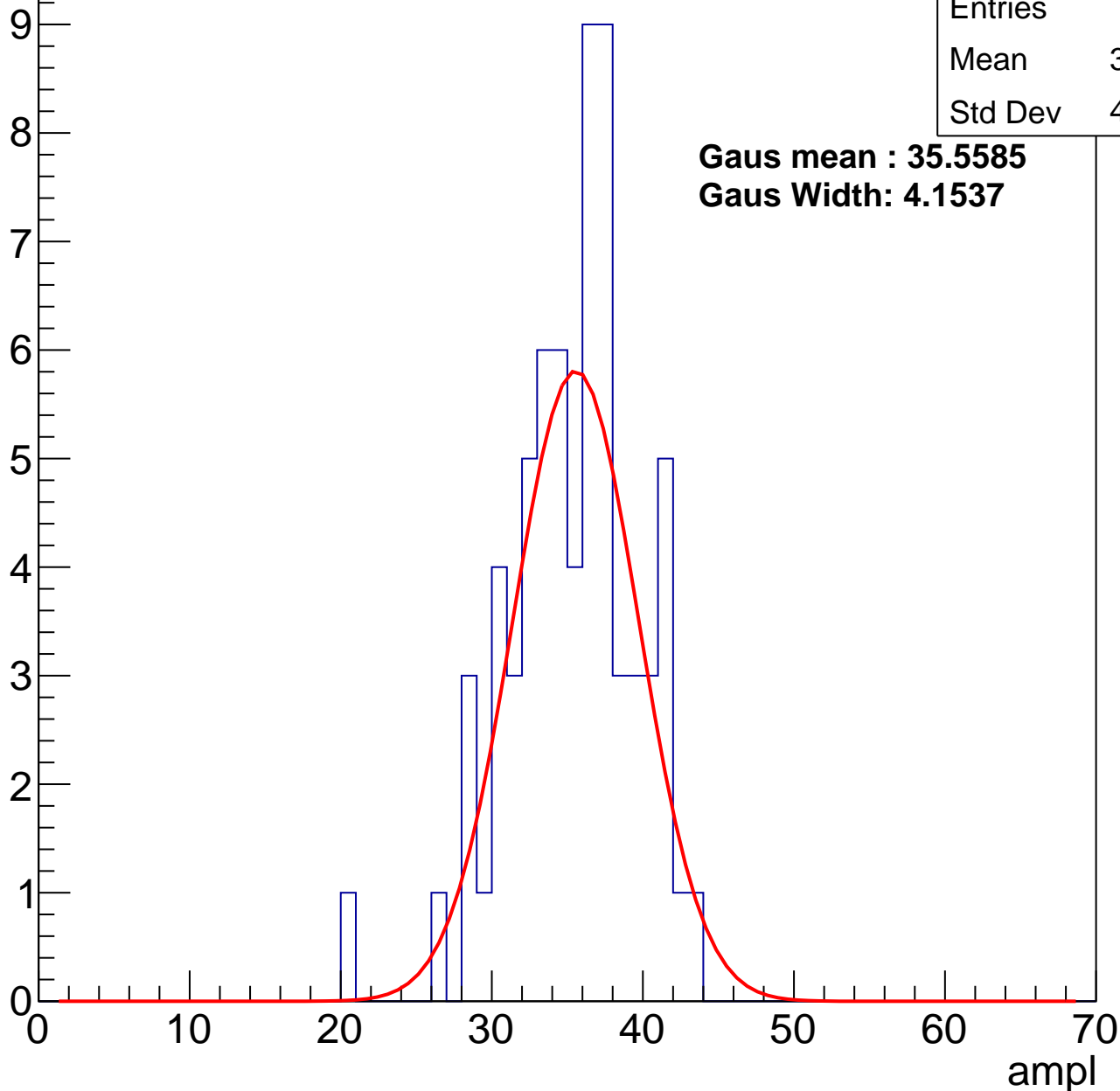
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	34.88
Std Dev	4.199

Gaus mean : 35.5585

Gaus Width: 4.1537



B1L103S, U23-ch118, adc2

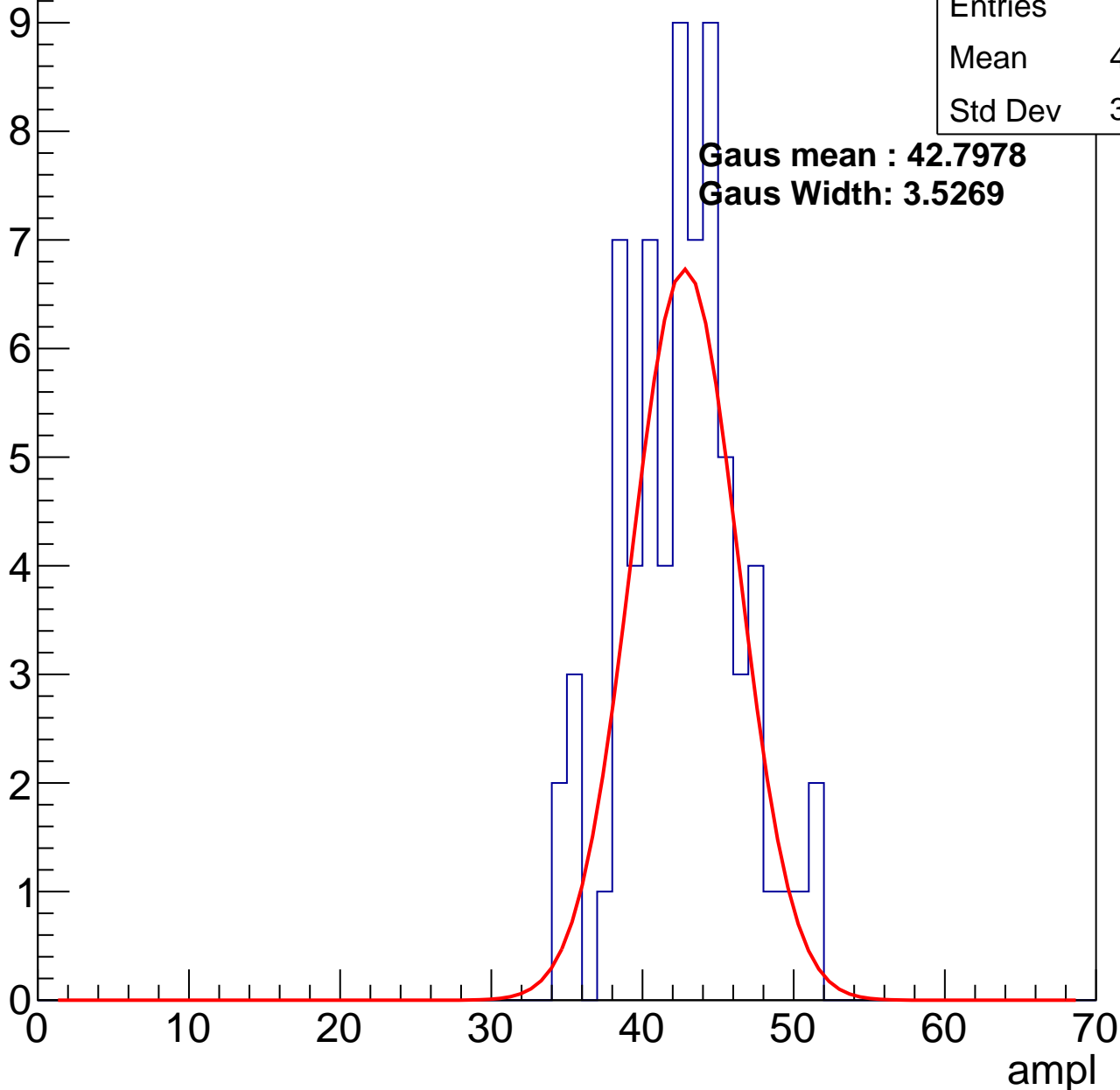
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.16
Std Dev	3.827

Gaus mean : 42.7978

Gaus Width: 3.5269

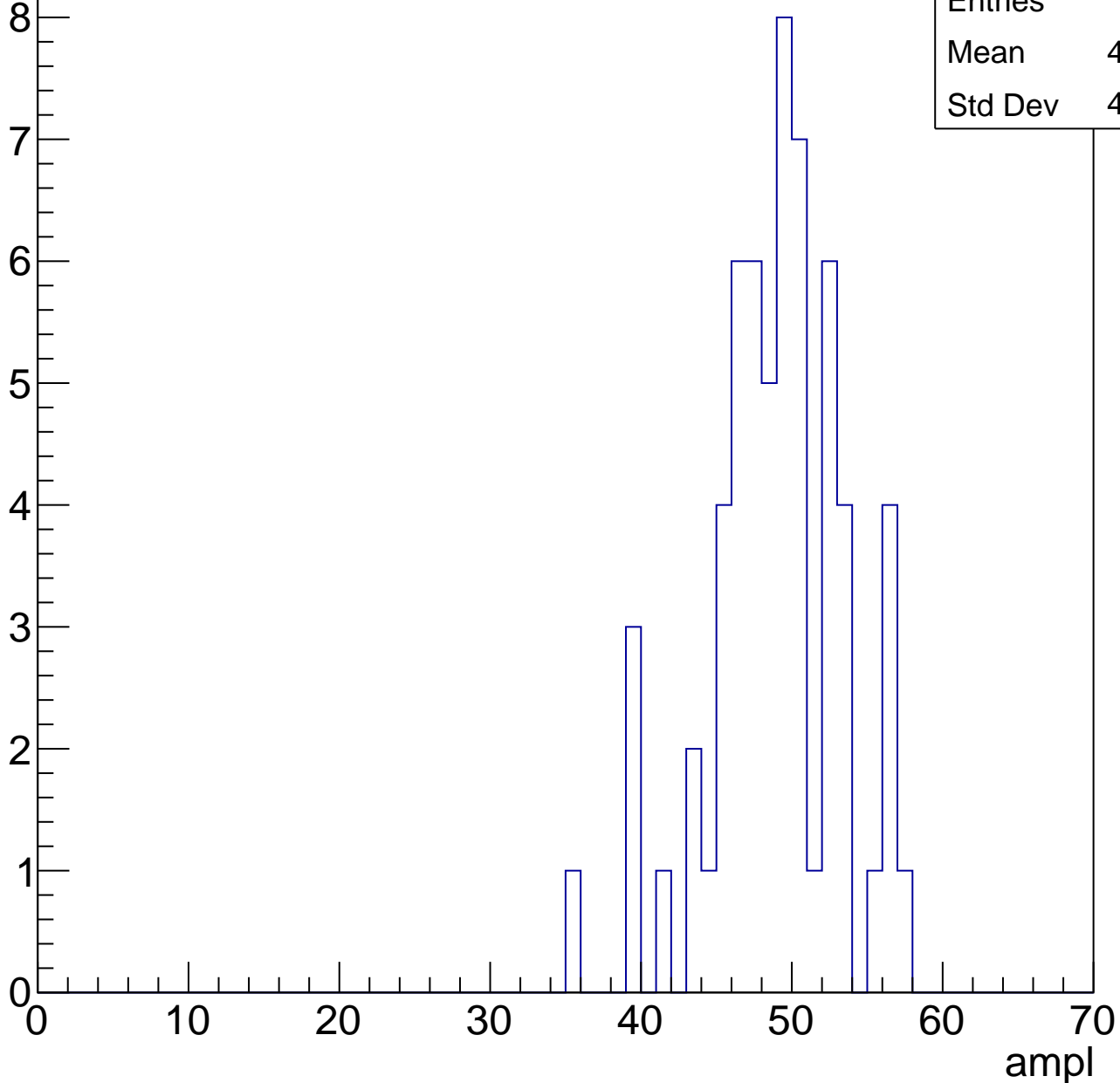


B1L103S, U23-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.43
Std Dev	4.452

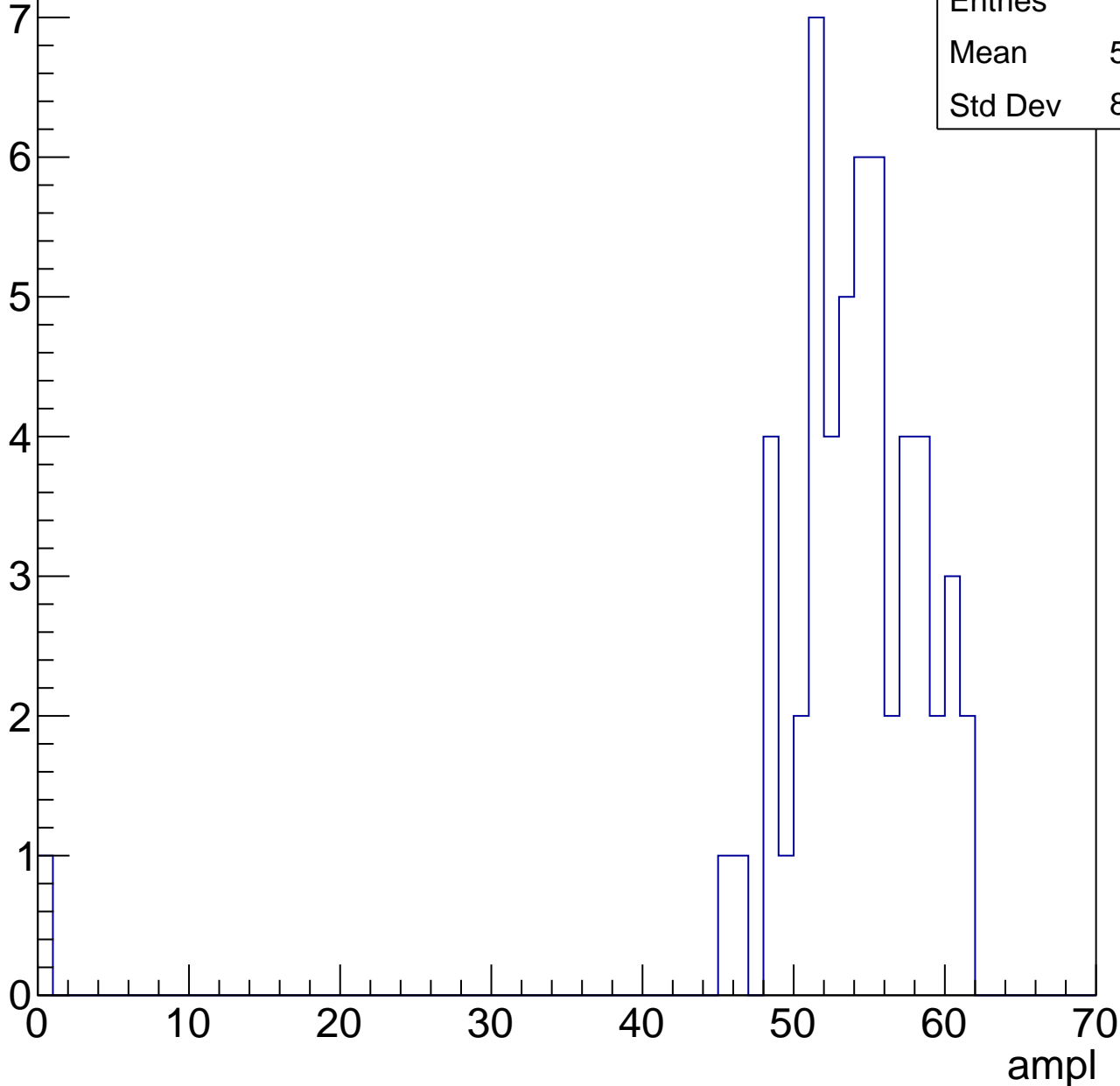


B1L103S, U23-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	52.87
Std Dev	8.142

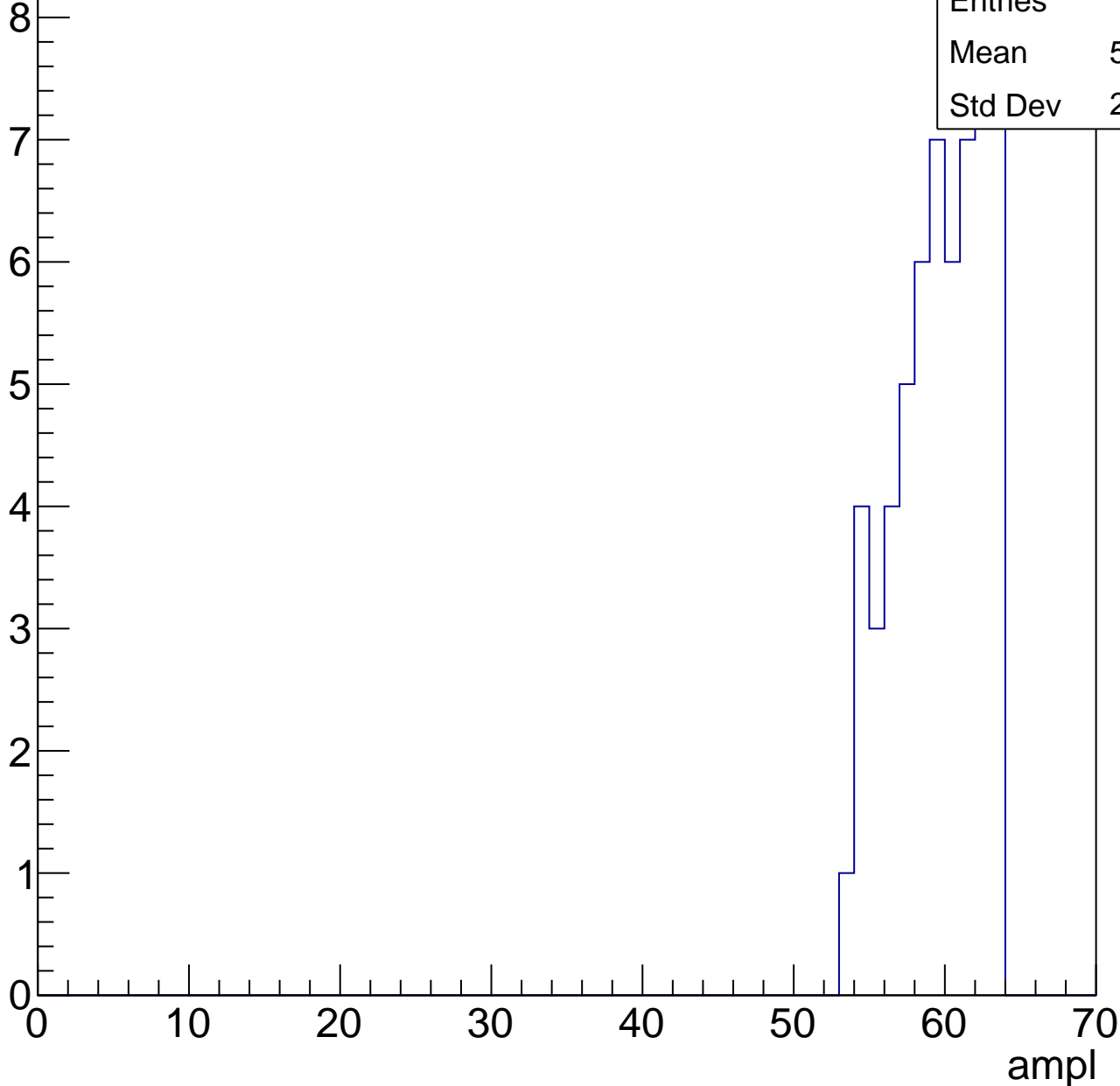


B1L103S, U23-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	59.17
Std Dev	2.847



B1L103S, U23-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

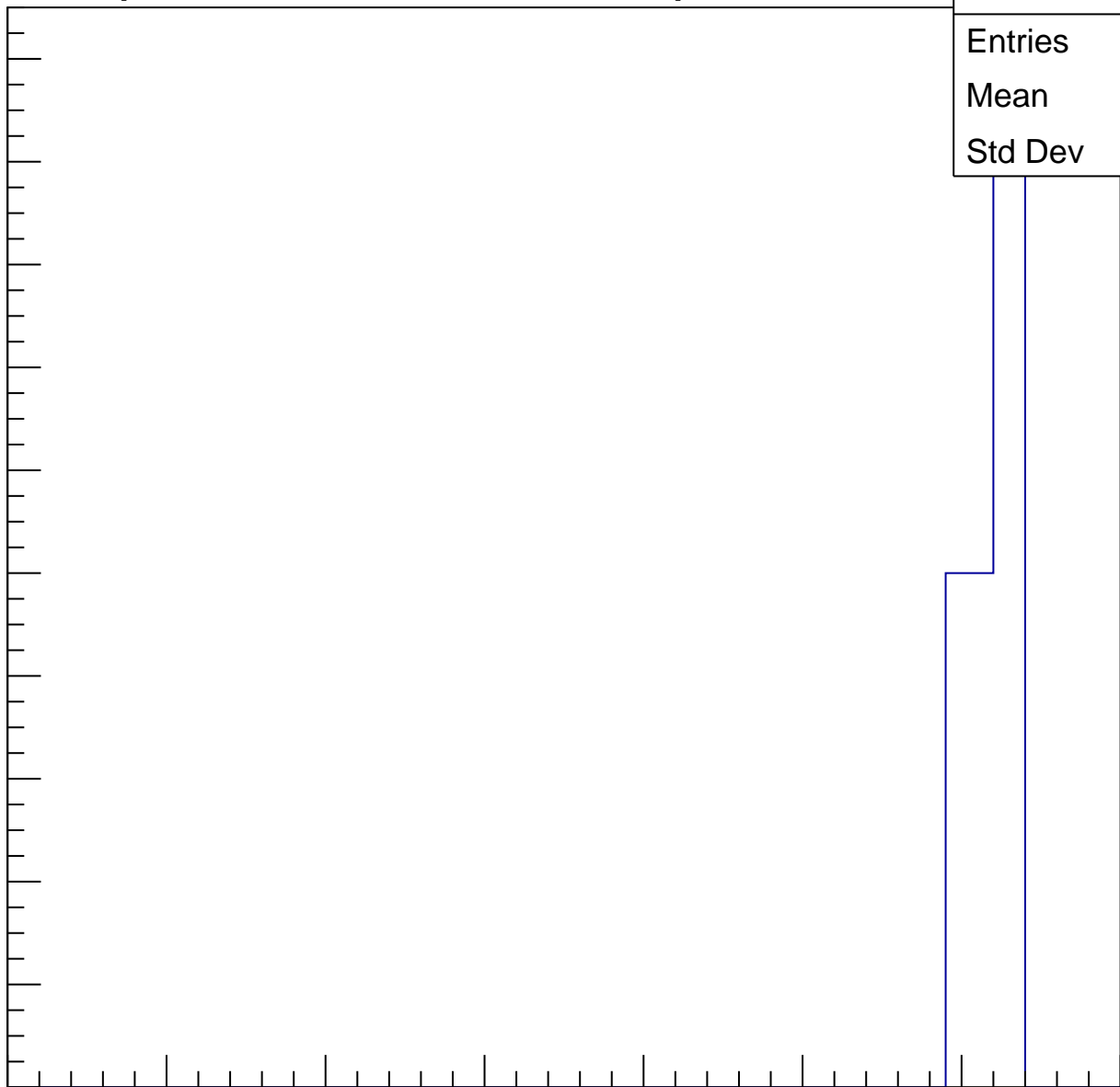
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.43
Std Dev	1.4

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch119, adc0

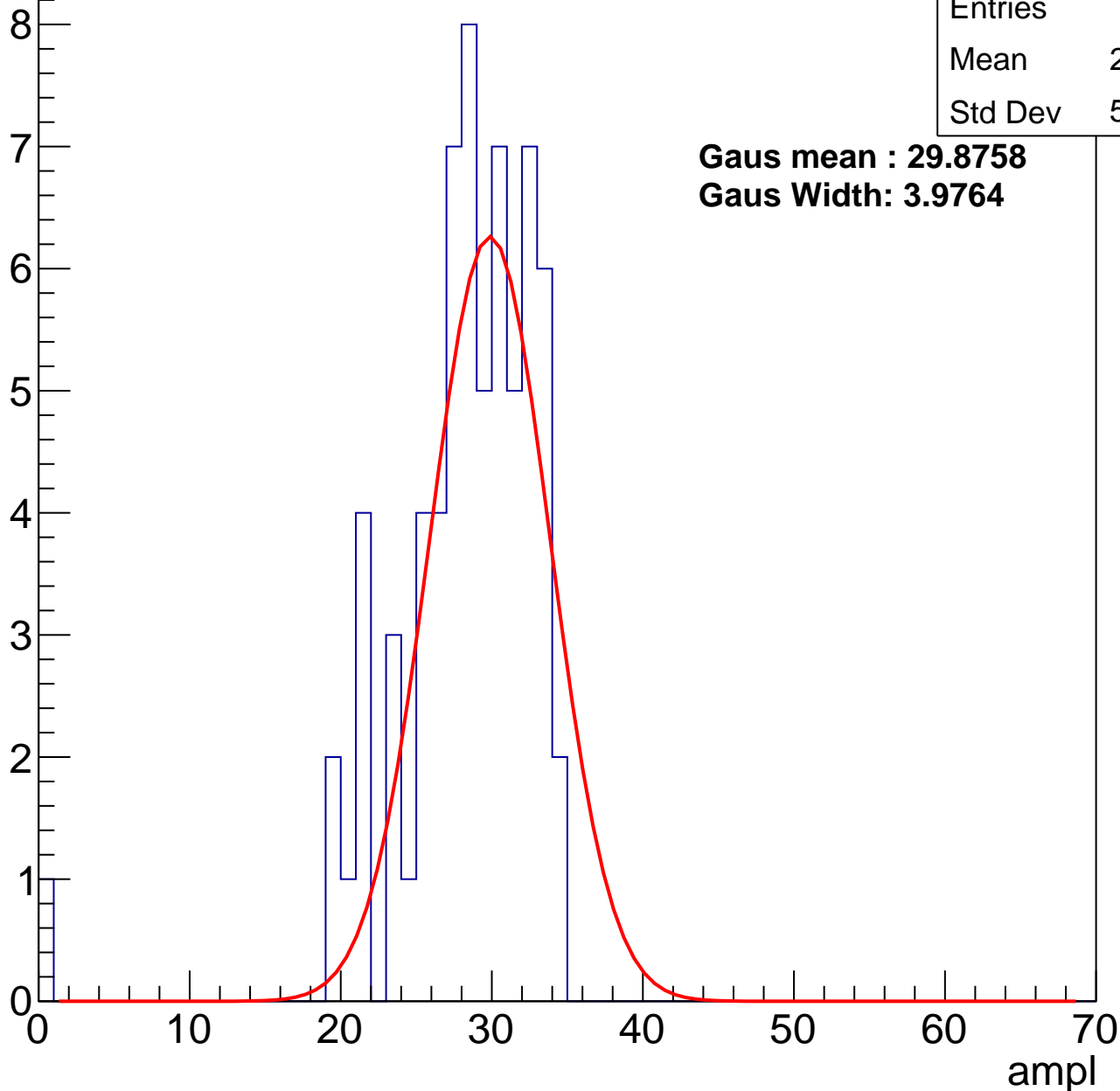
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.64
Std Dev	5.124

Gaus mean : 29.8758

Gaus Width: 3.9764



B1L103S, U23-ch119, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	35.9
Std Dev	3.899

Gaus mean : 36.5864

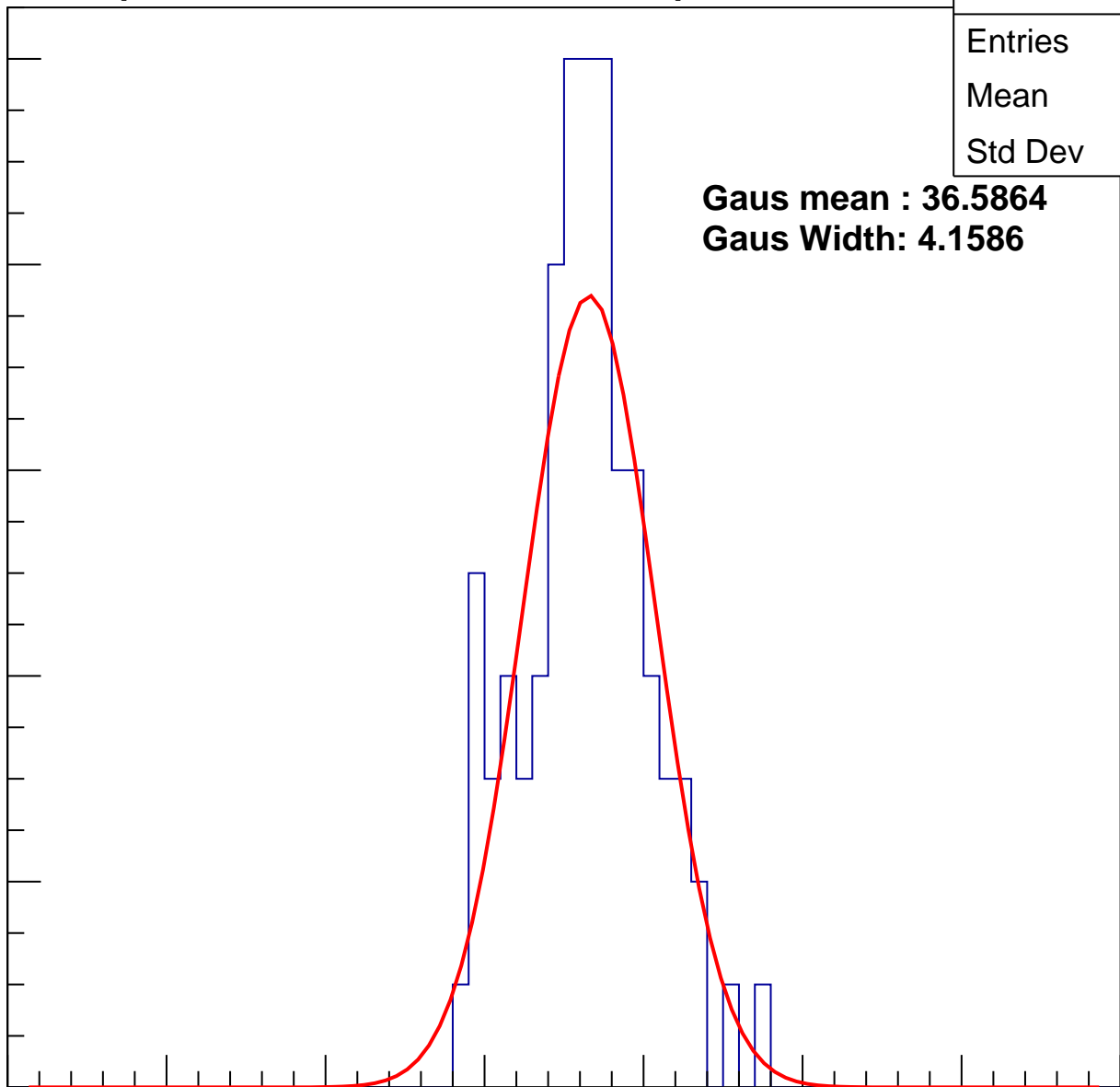
Gaus Width: 4.1586

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch119, adc2

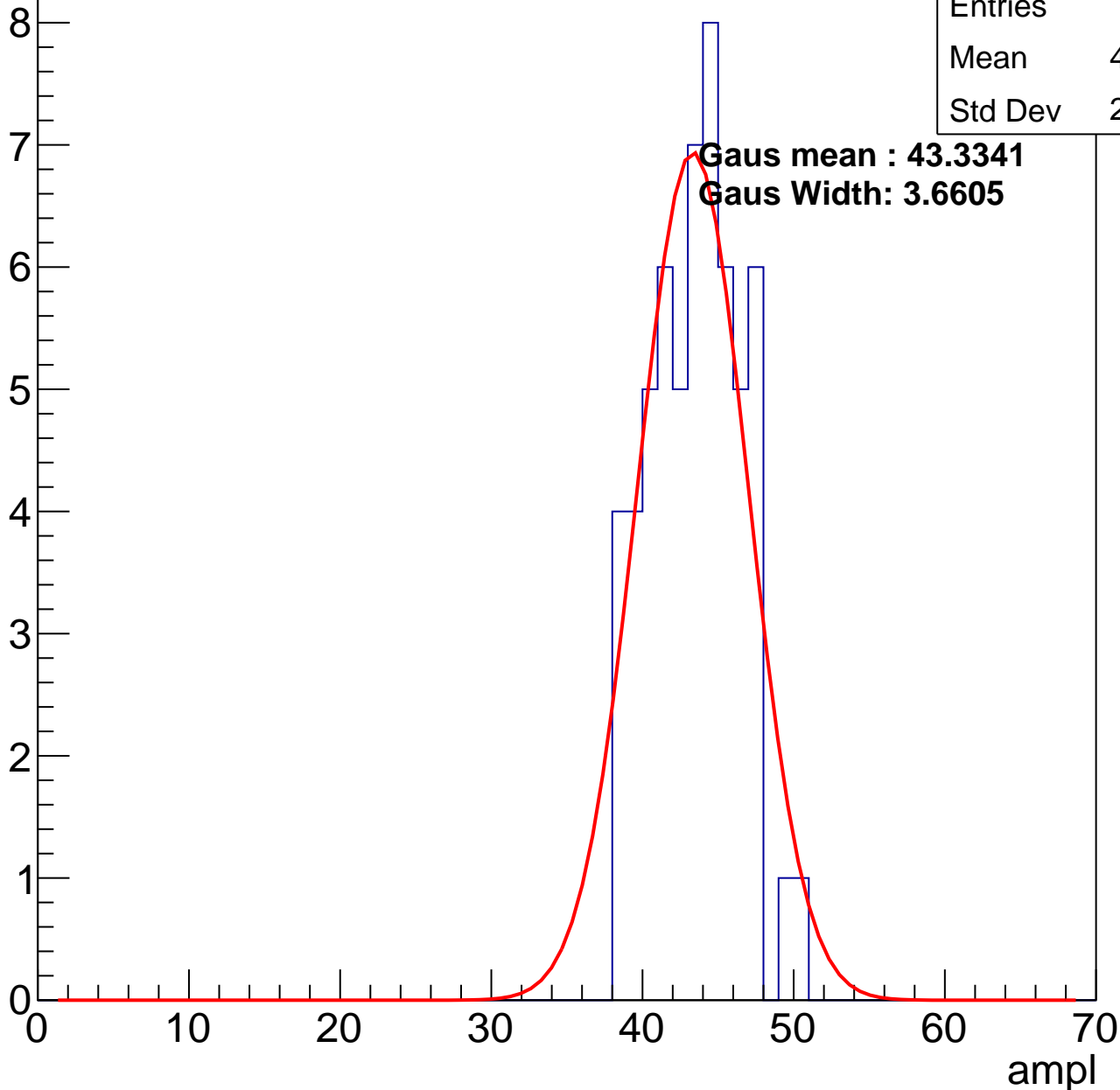
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.07
Std Dev	2.924

Gaus mean : 43.3341

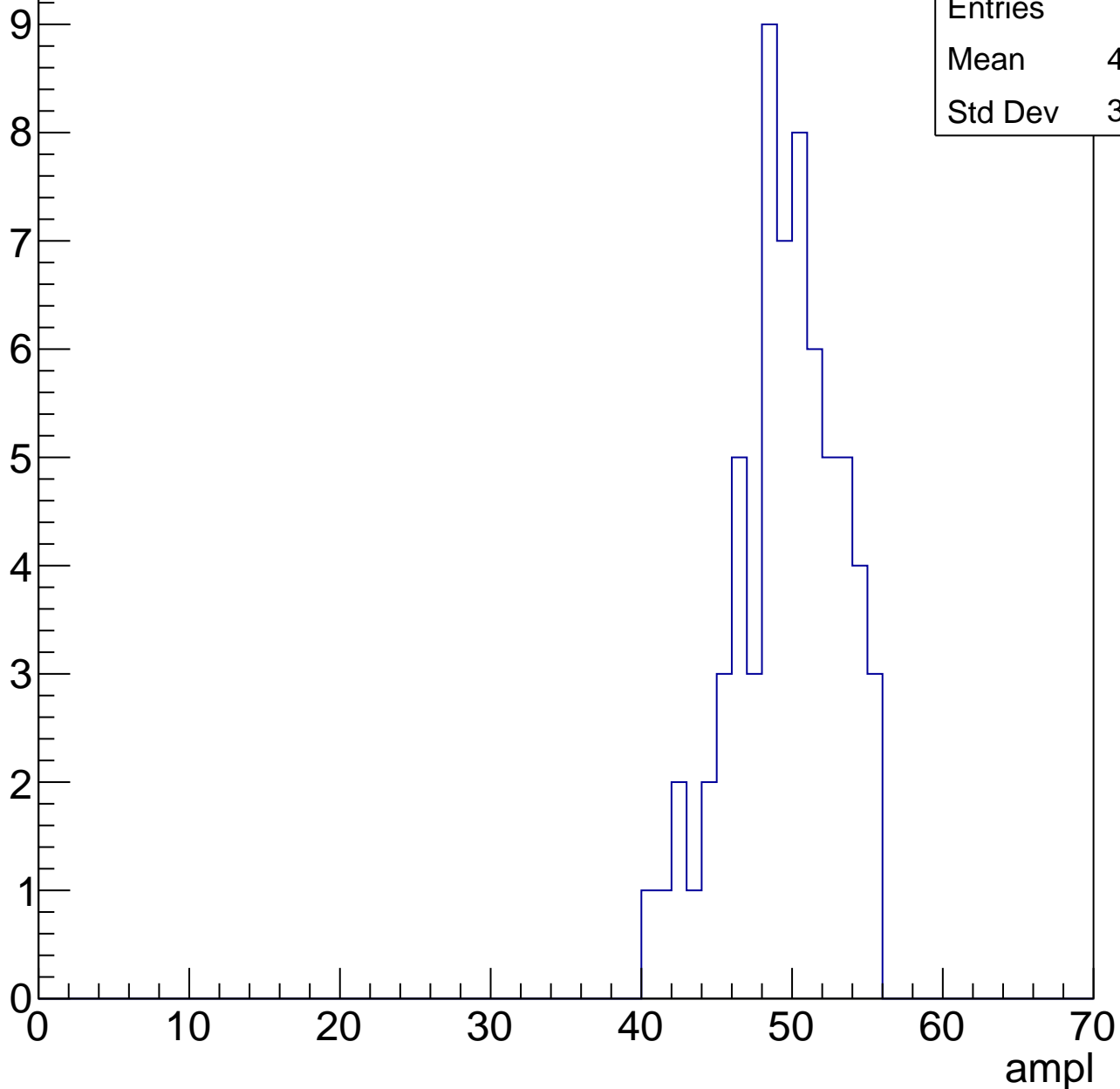
Gaus Width: 3.6605



B1L103S, U23-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



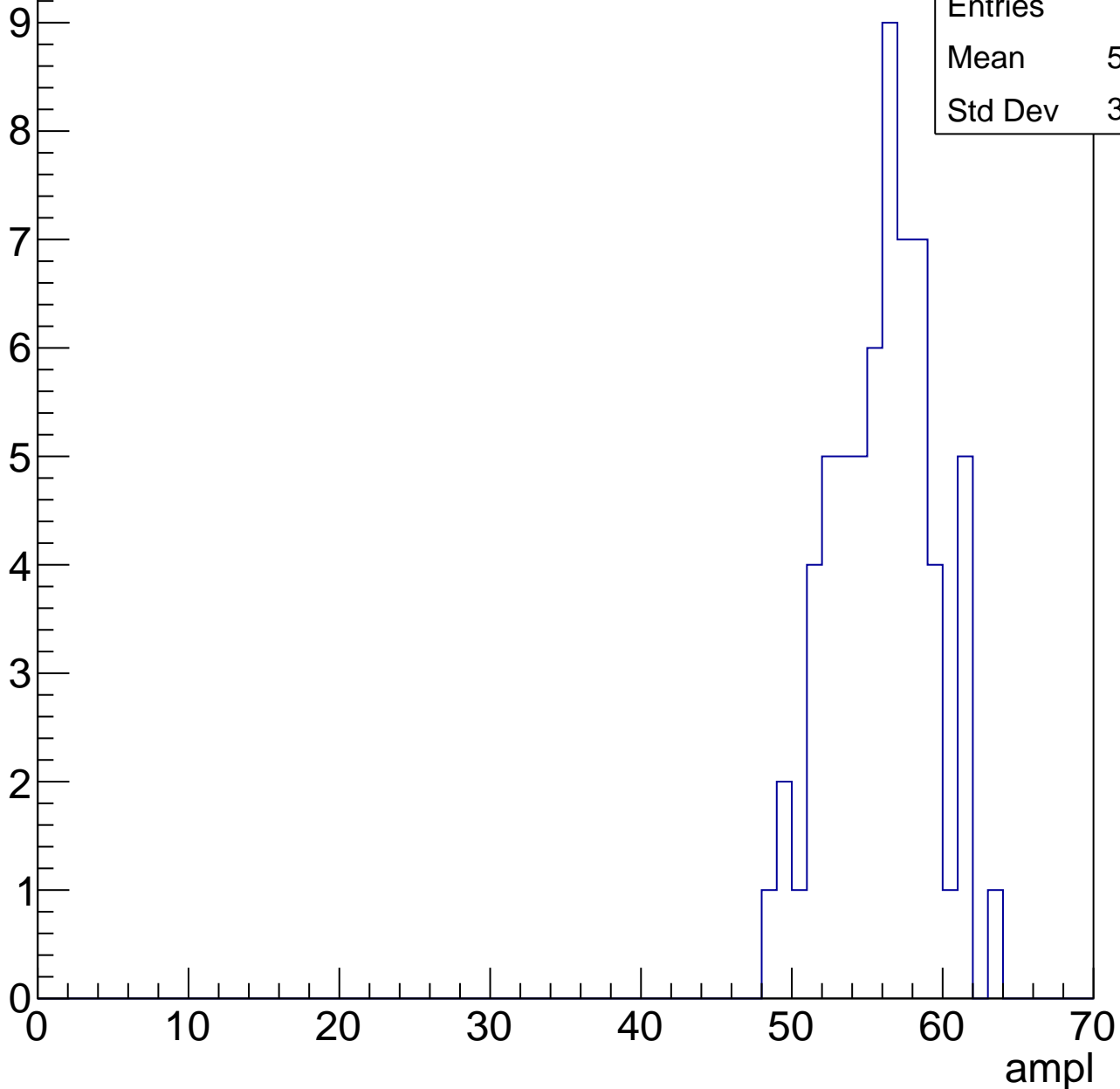
Entries	65
Mean	49.06
Std Dev	3.525

B1L103S, U23-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

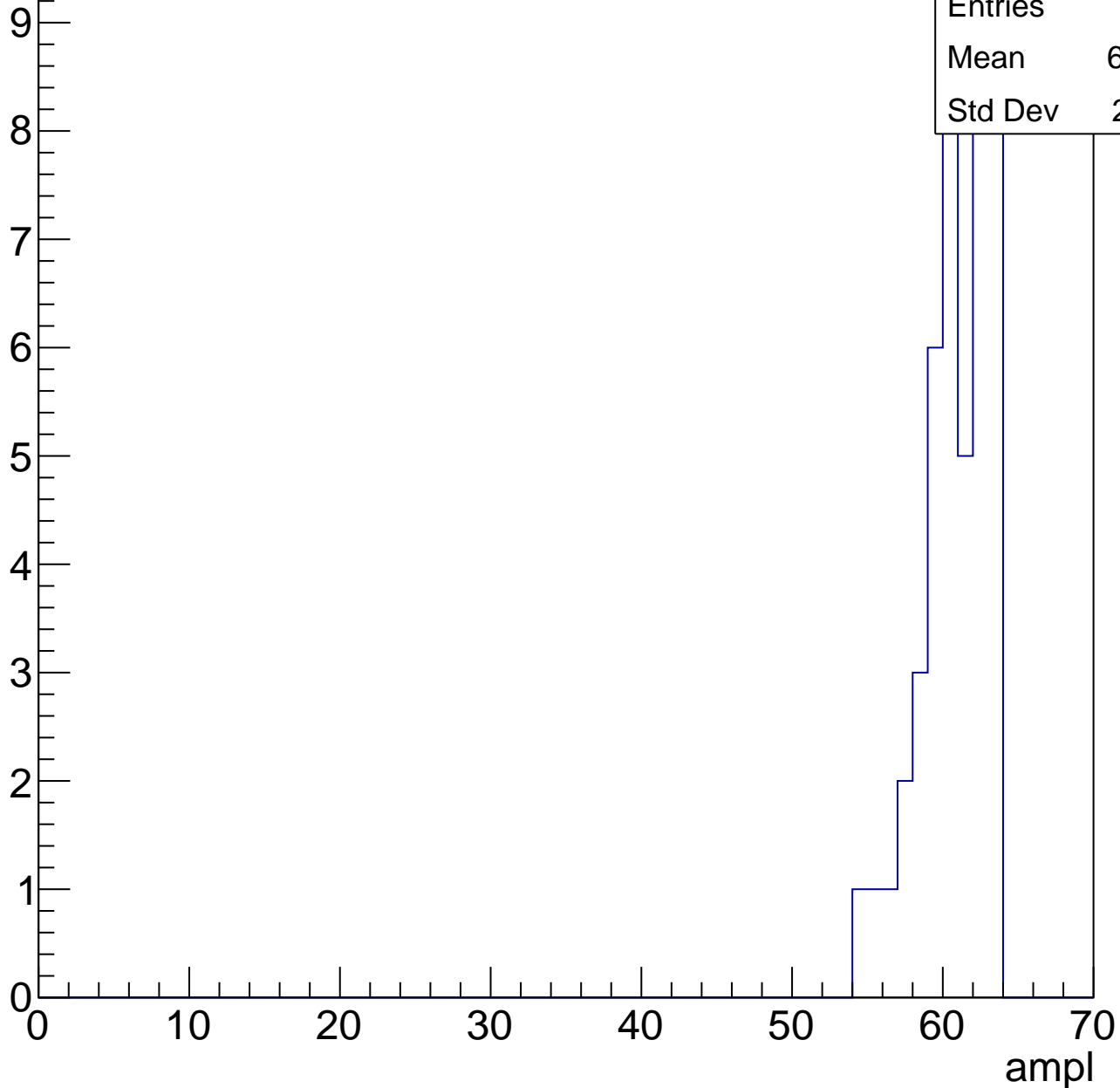
Entries	63
Mean	55.52
Std Dev	3.323



B1L103S, U23-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

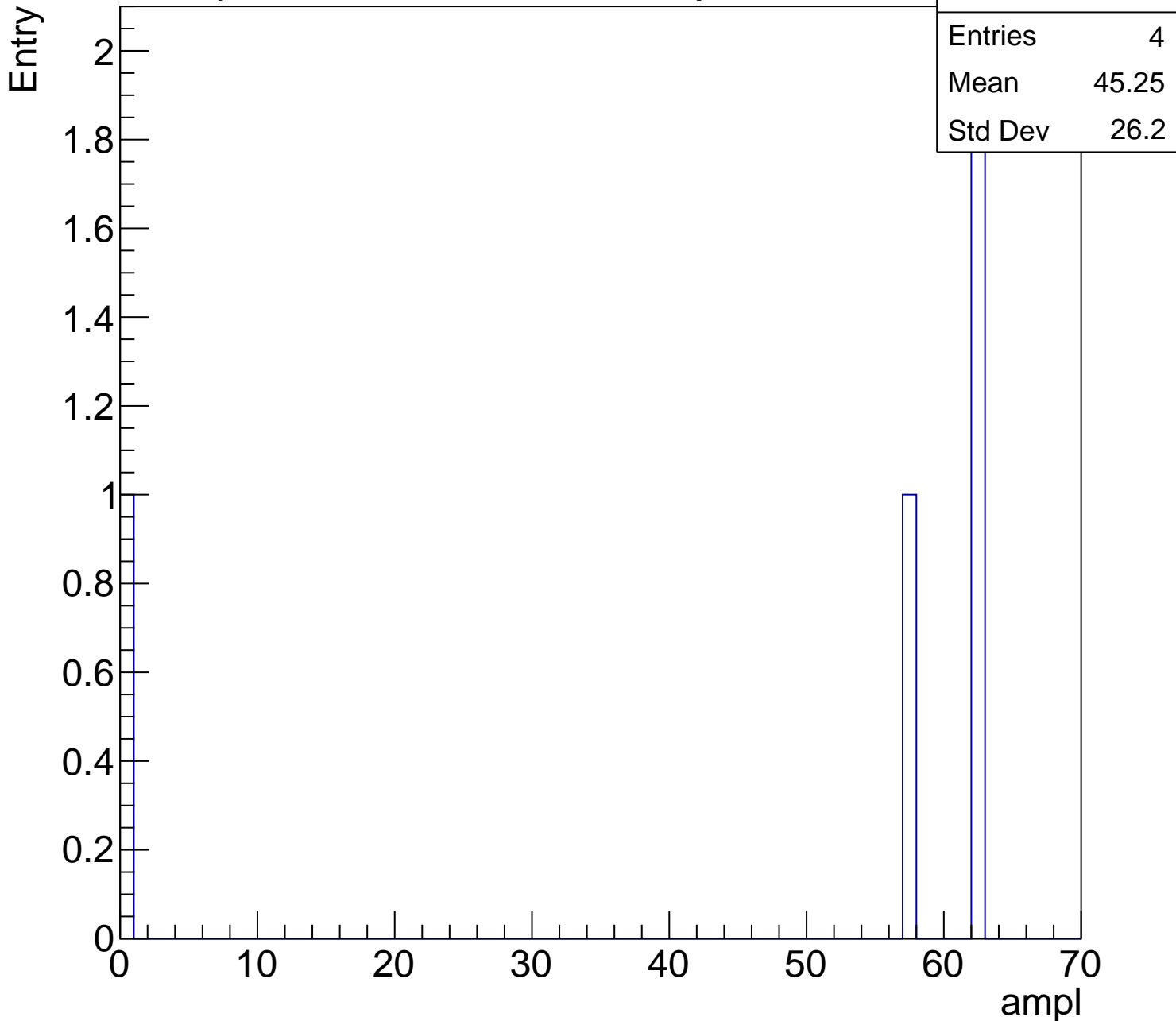
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	45.25
Std Dev	26.2

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U23-ch120, adc0

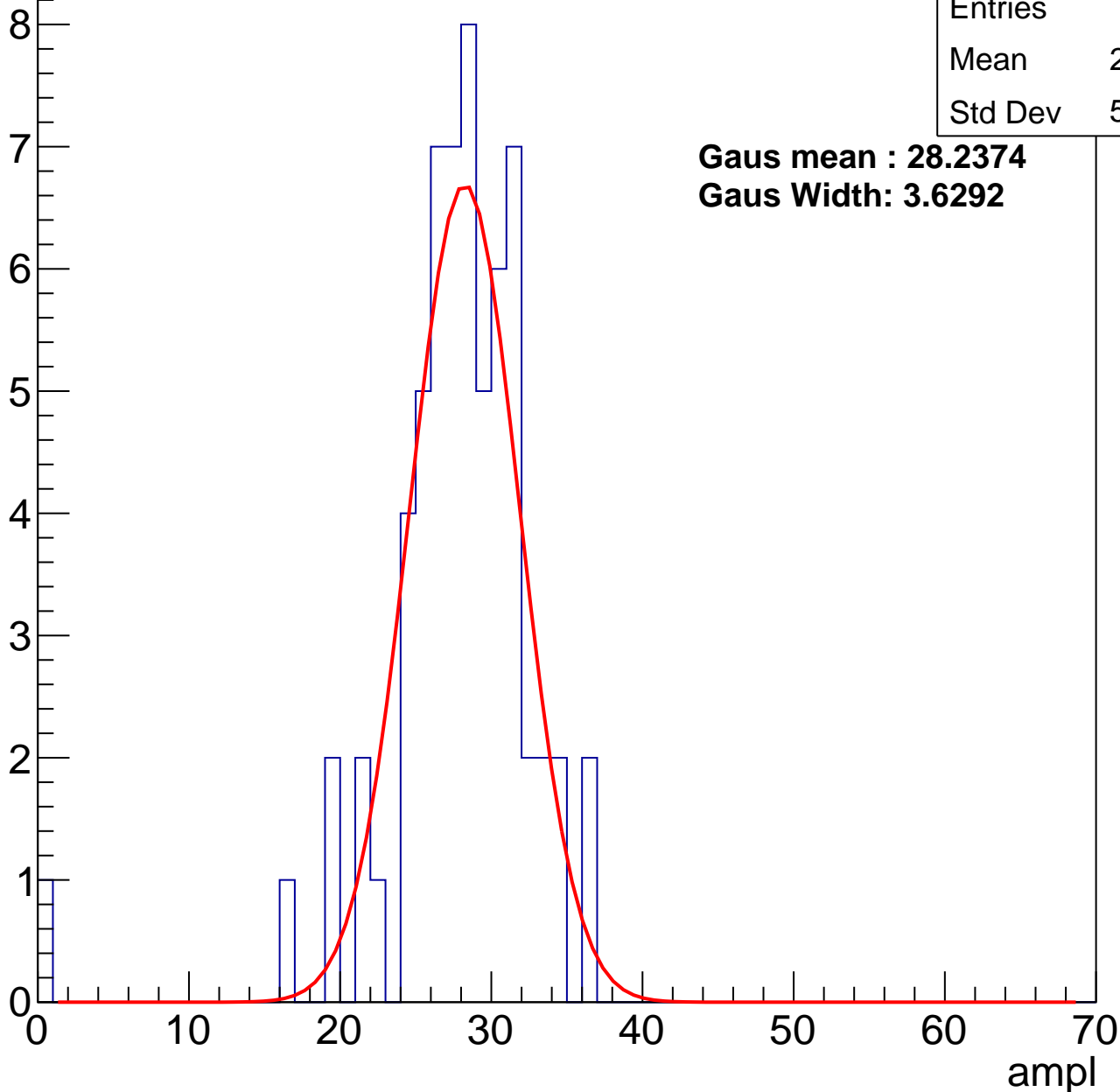
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	27.28
Std Dev	5.167

Gaus mean : 28.2374

Gaus Width: 3.6292



B1L103S, U23-ch120, adc1

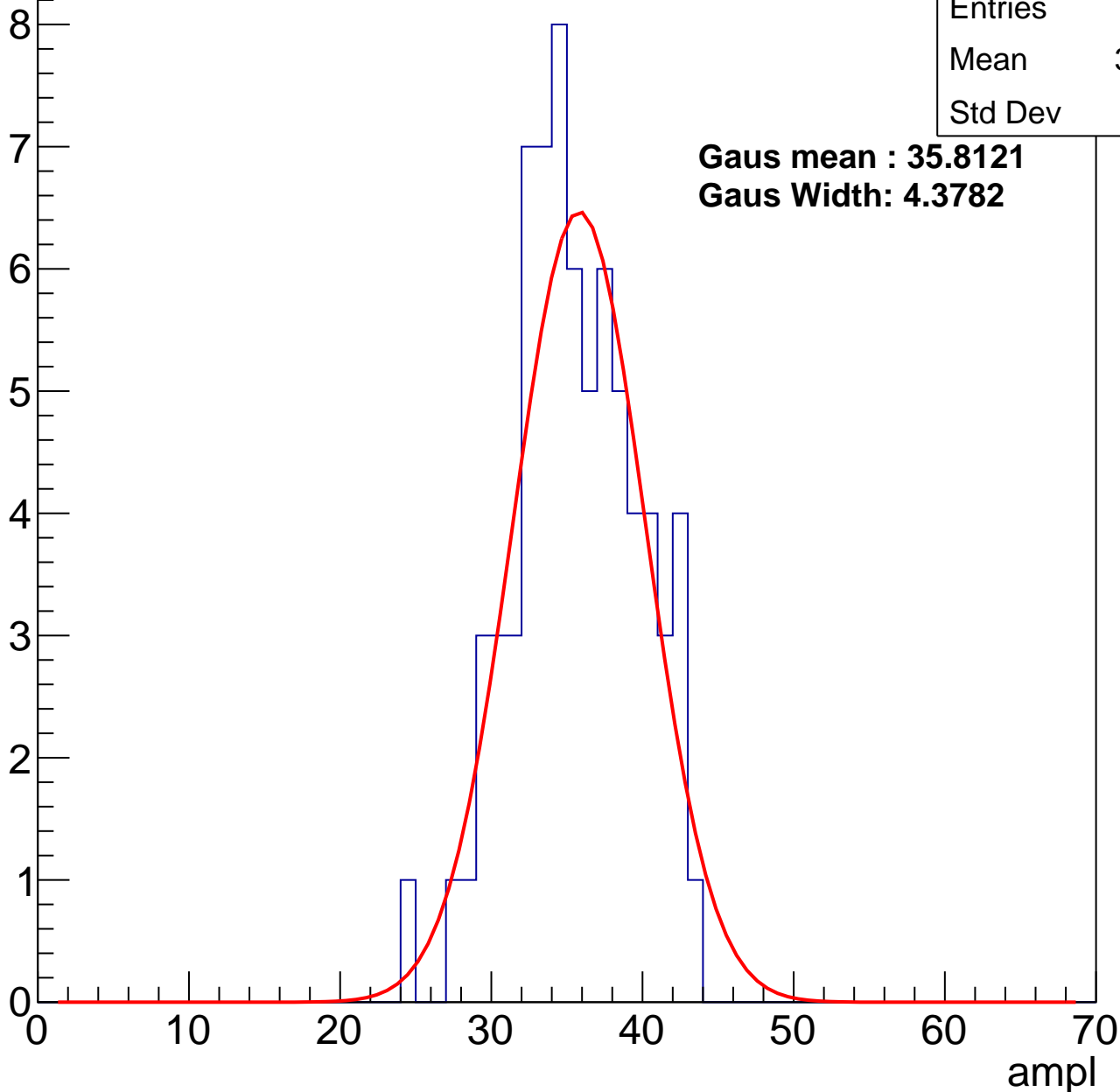
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	35.11
Std Dev	4.03

Gaus mean : 35.8121

Gaus Width: 4.3782



B1L103S, U23-ch120, adc2

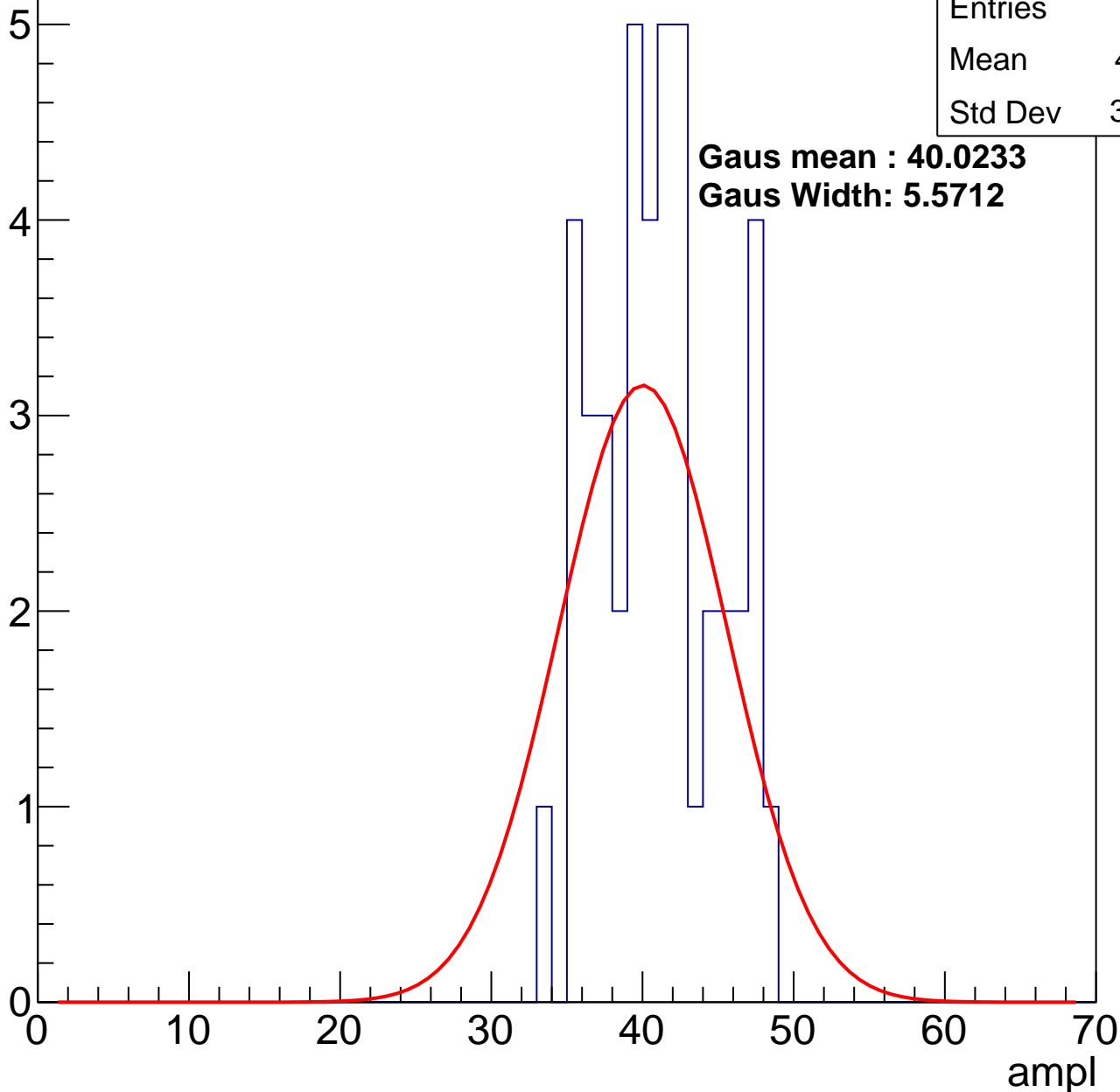
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	40.61
Std Dev	3.898

Gaus mean : 40.0233

Gaus Width: 5.5712

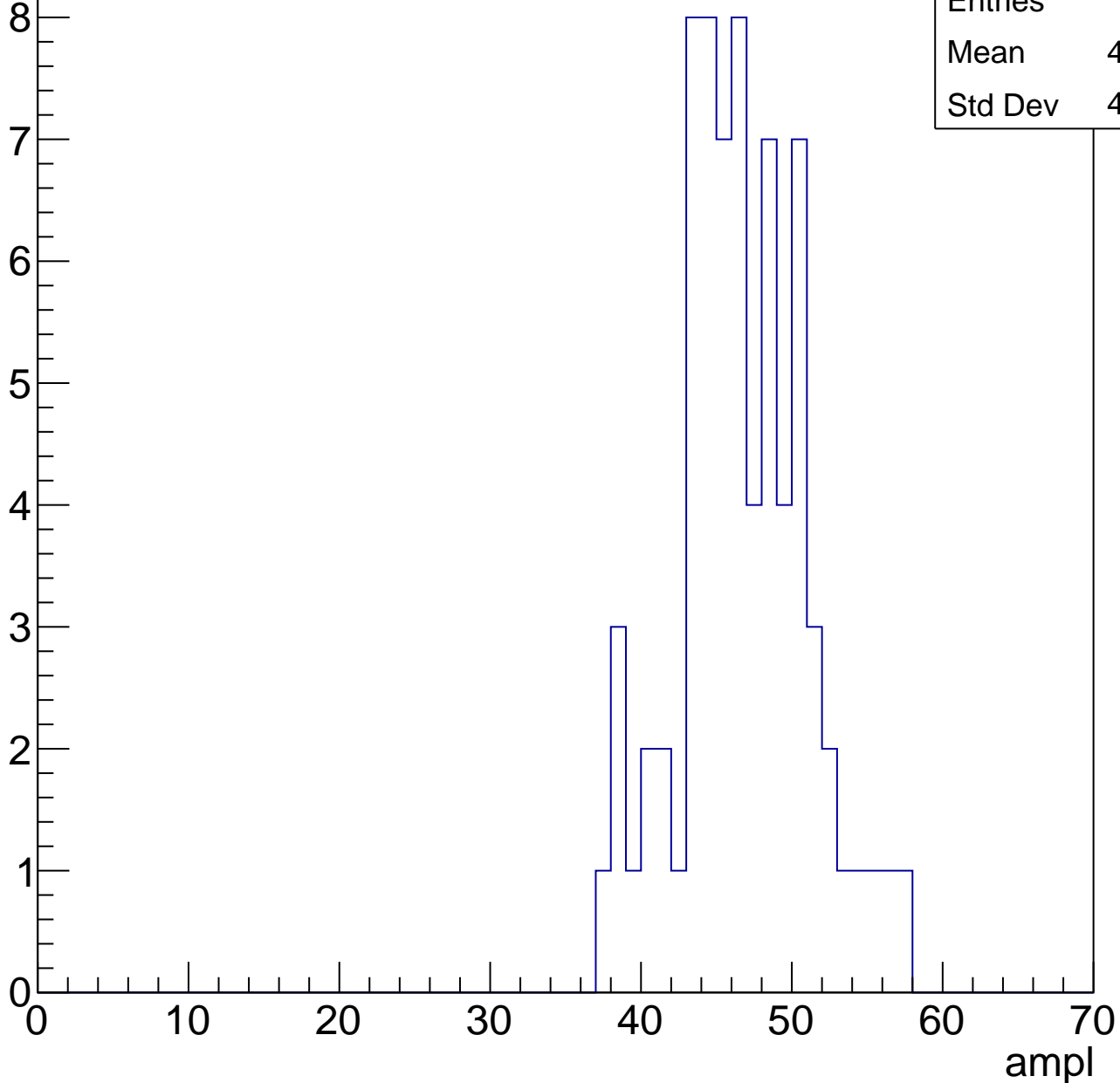


B1L103S, U23-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	46.23
Std Dev	4.235

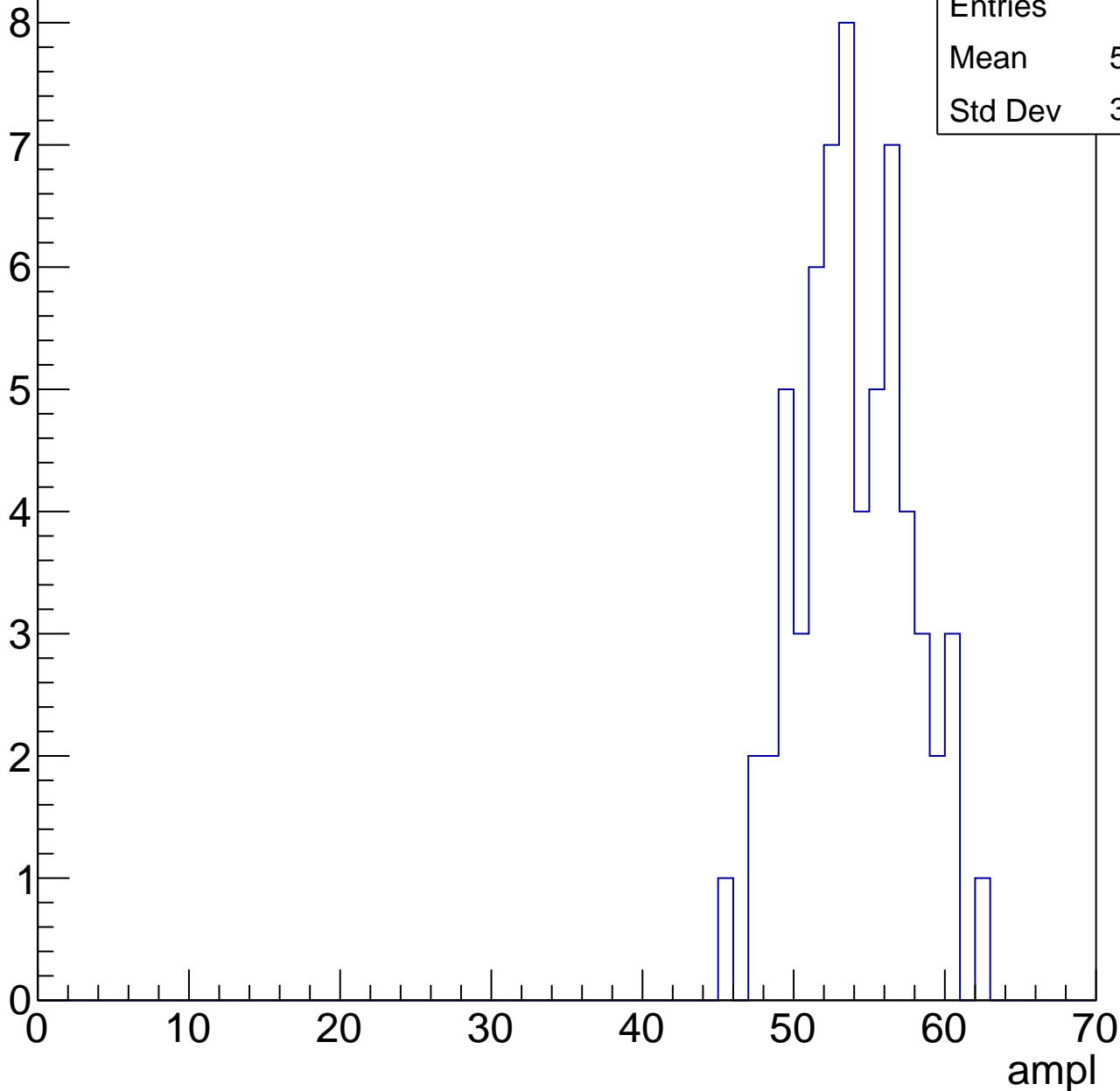


B1L103S, U23-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	53.48
Std Dev	3.642

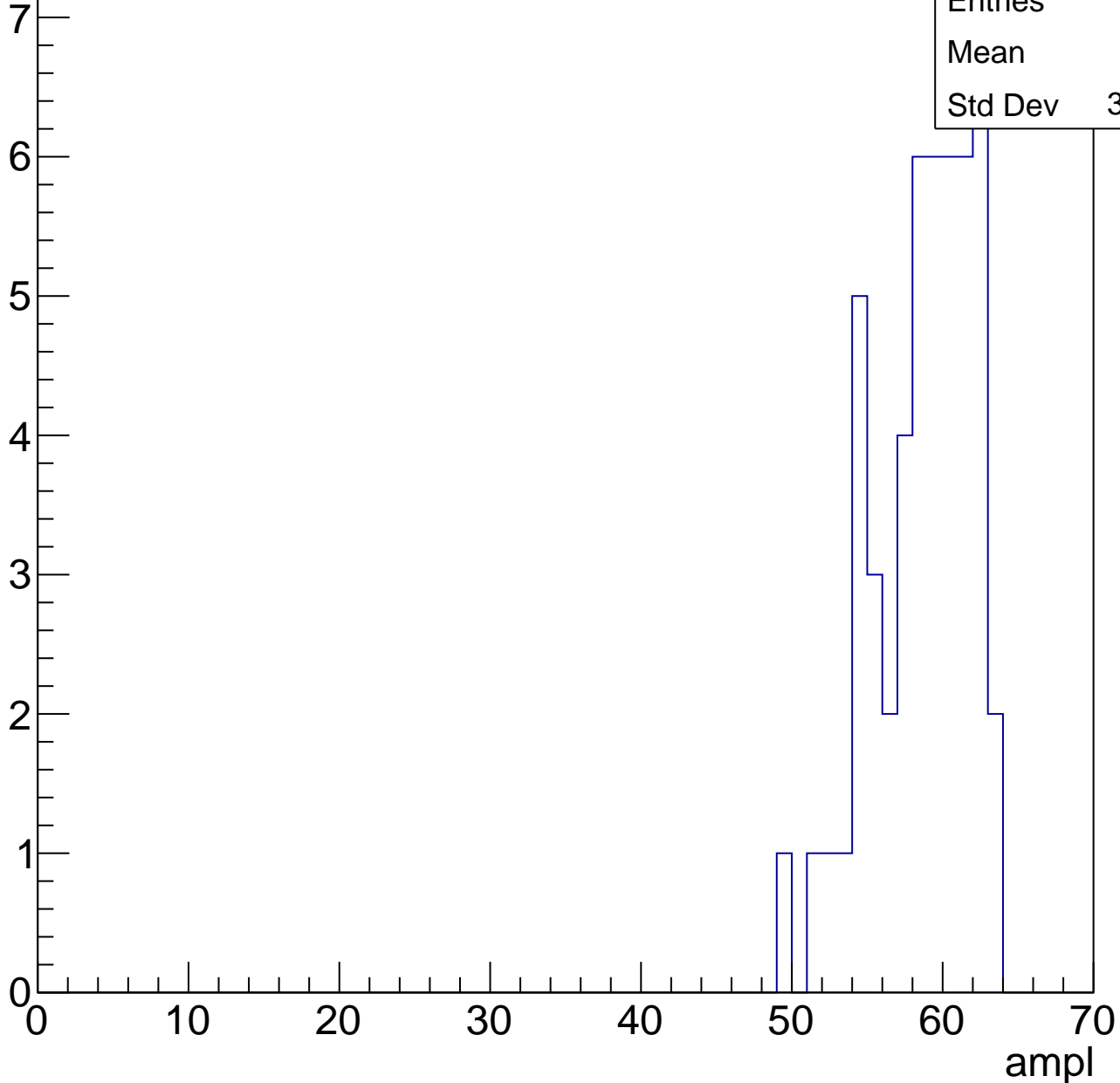


B1L103S, U23-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.2
Std Dev	3.296

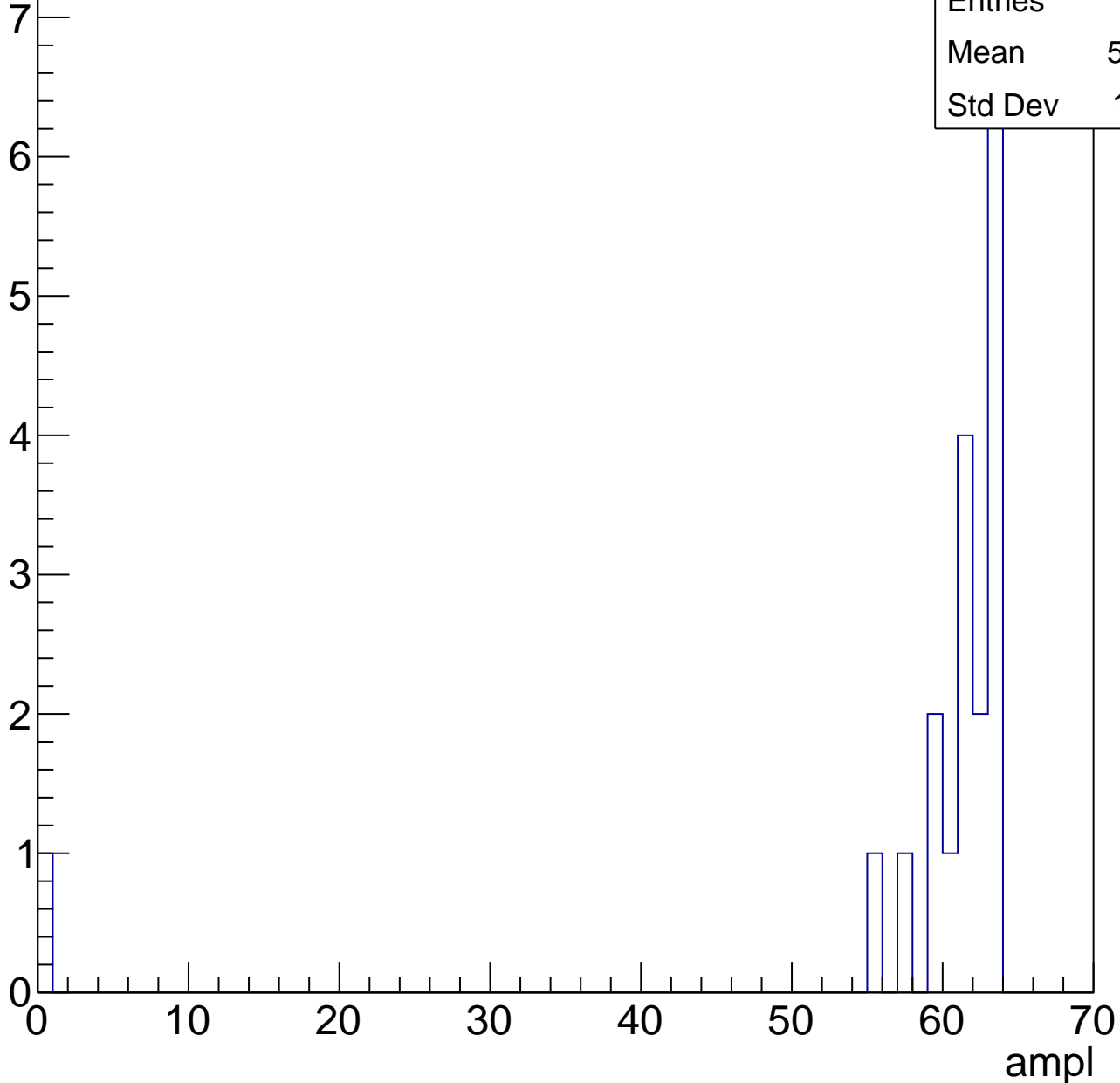


B1L103S, U23-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	57.84
Std Dev	13.81



B1L103S, U23-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch121, adc0

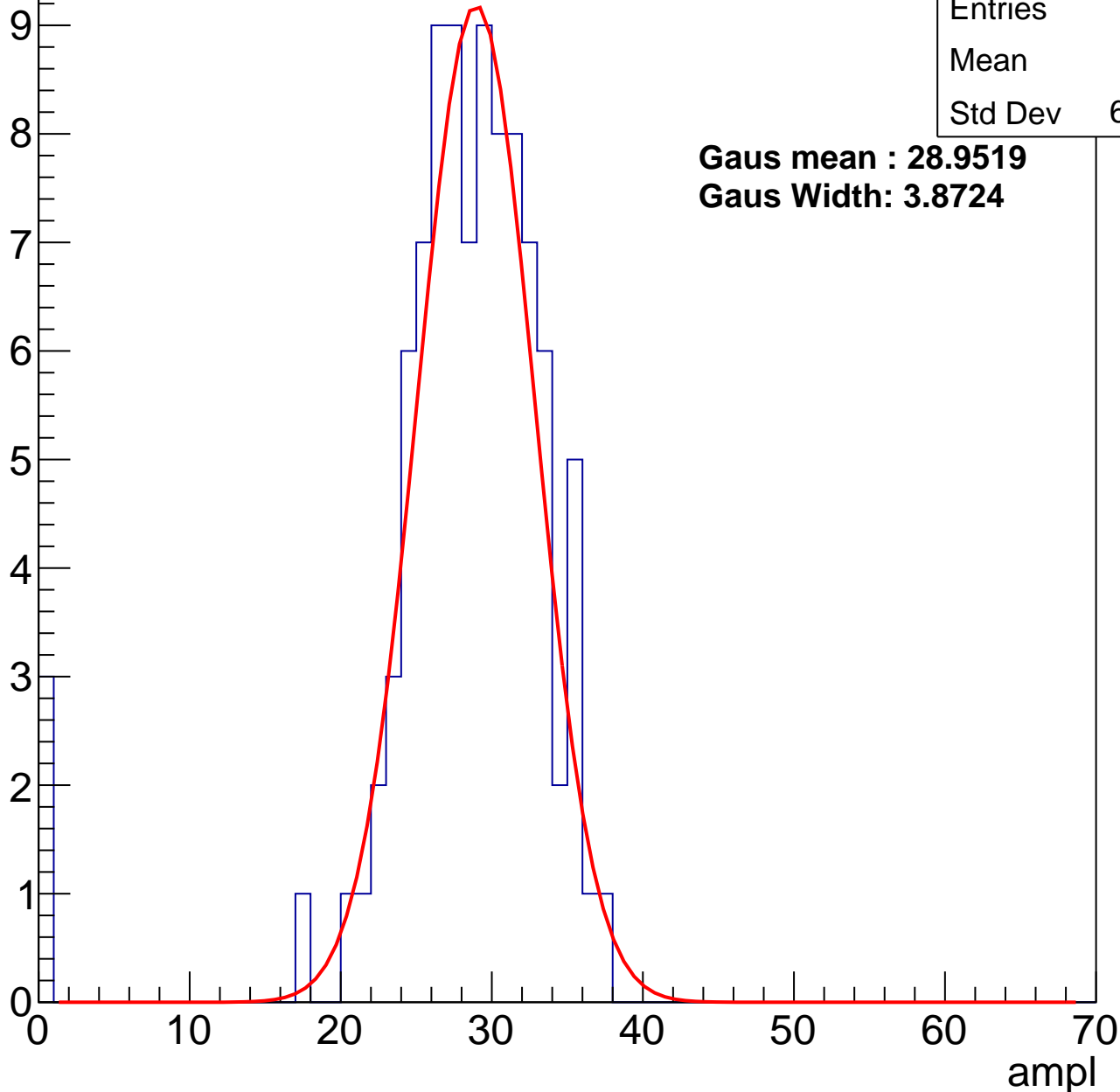
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	96
Mean	27.6
Std Dev	6.257

Gaus mean : 28.9519

Gaus Width: 3.8724



B1L103S, U23-ch121, adc1

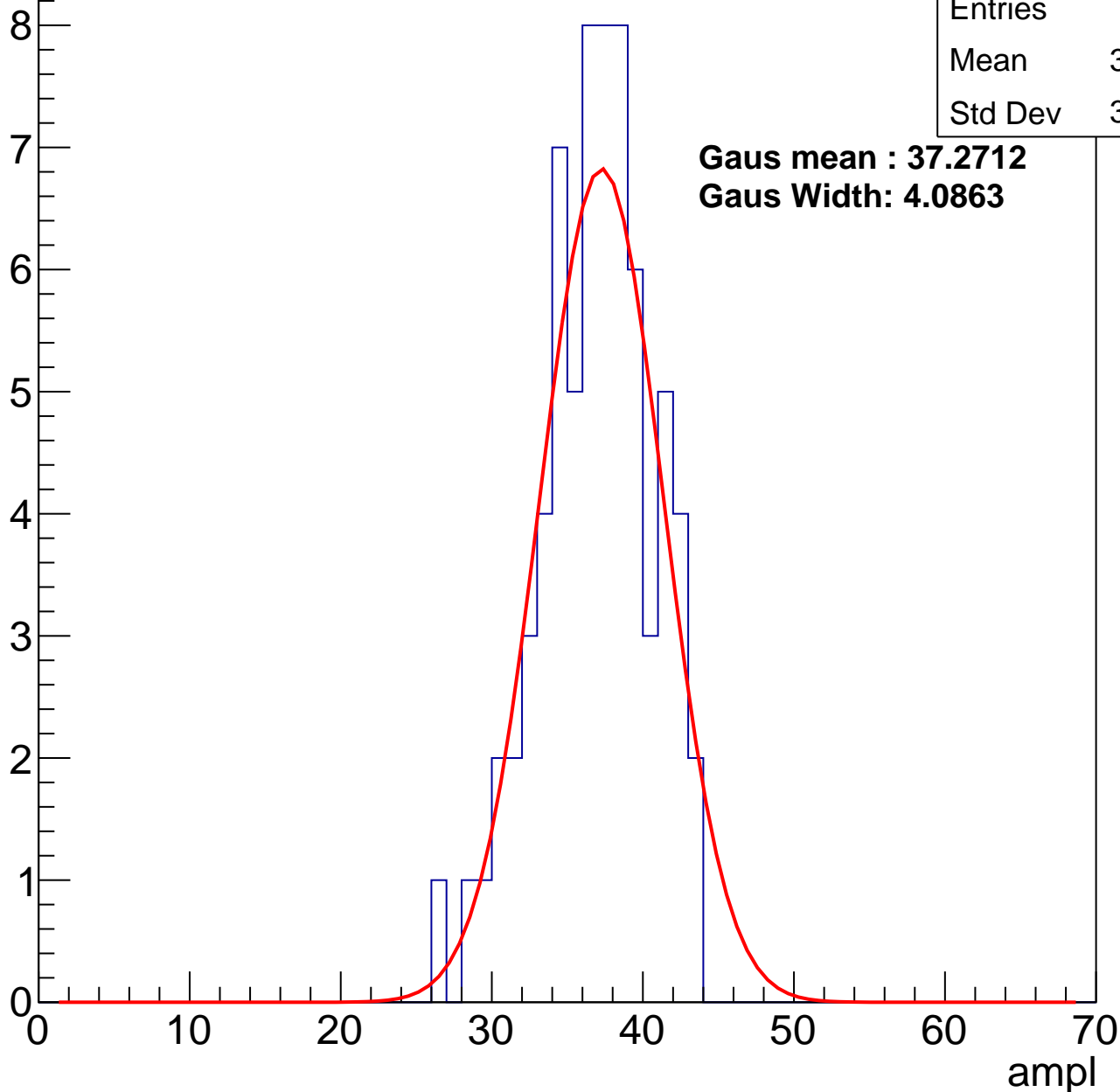
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.39
Std Dev	3.685

Gaus mean : 37.2712

Gaus Width: 4.0863



B1L103S, U23-ch121, adc2

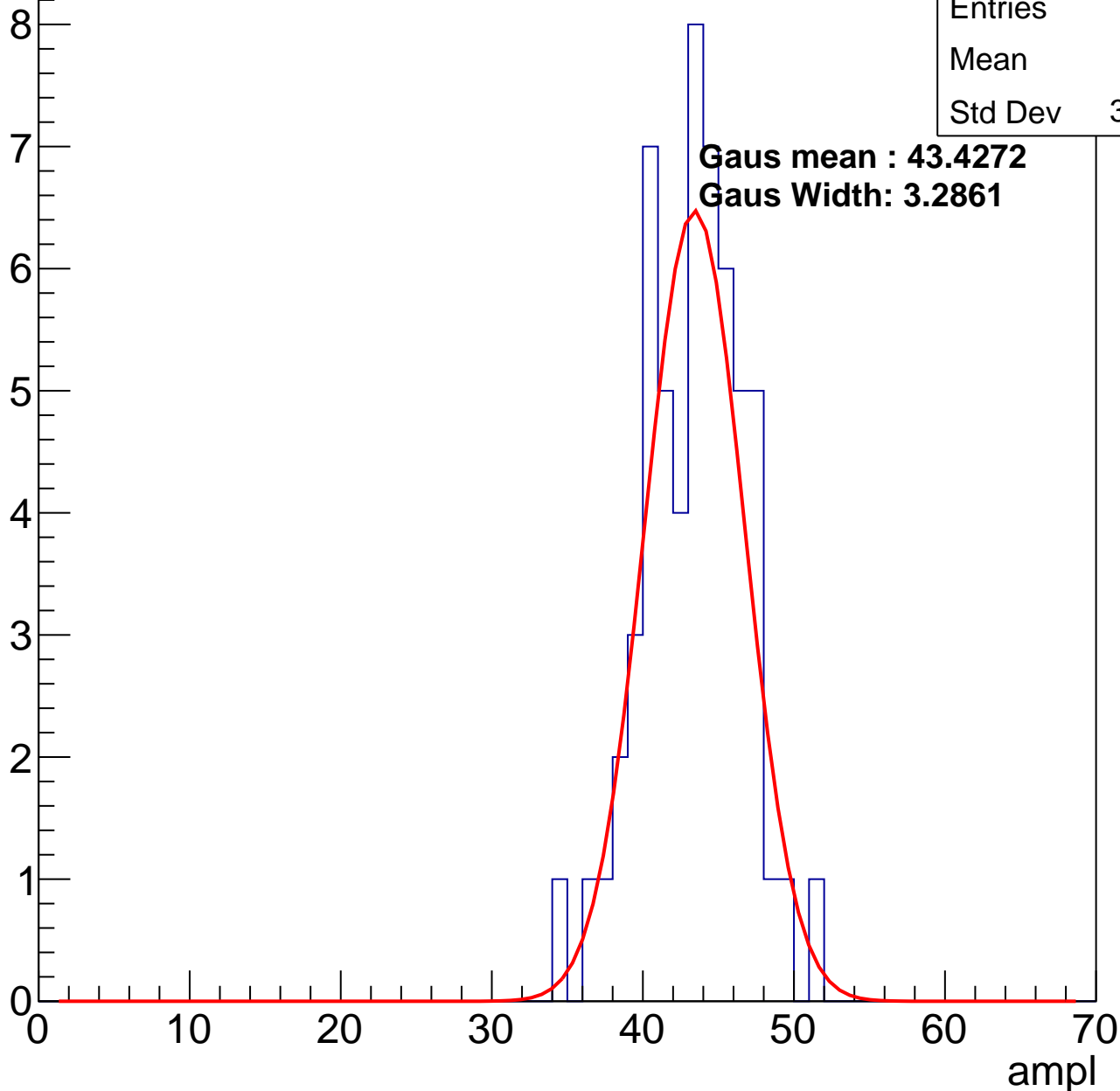
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.9
Std Dev	3.299

Gaus mean : 43.4272

Gaus Width: 3.2861

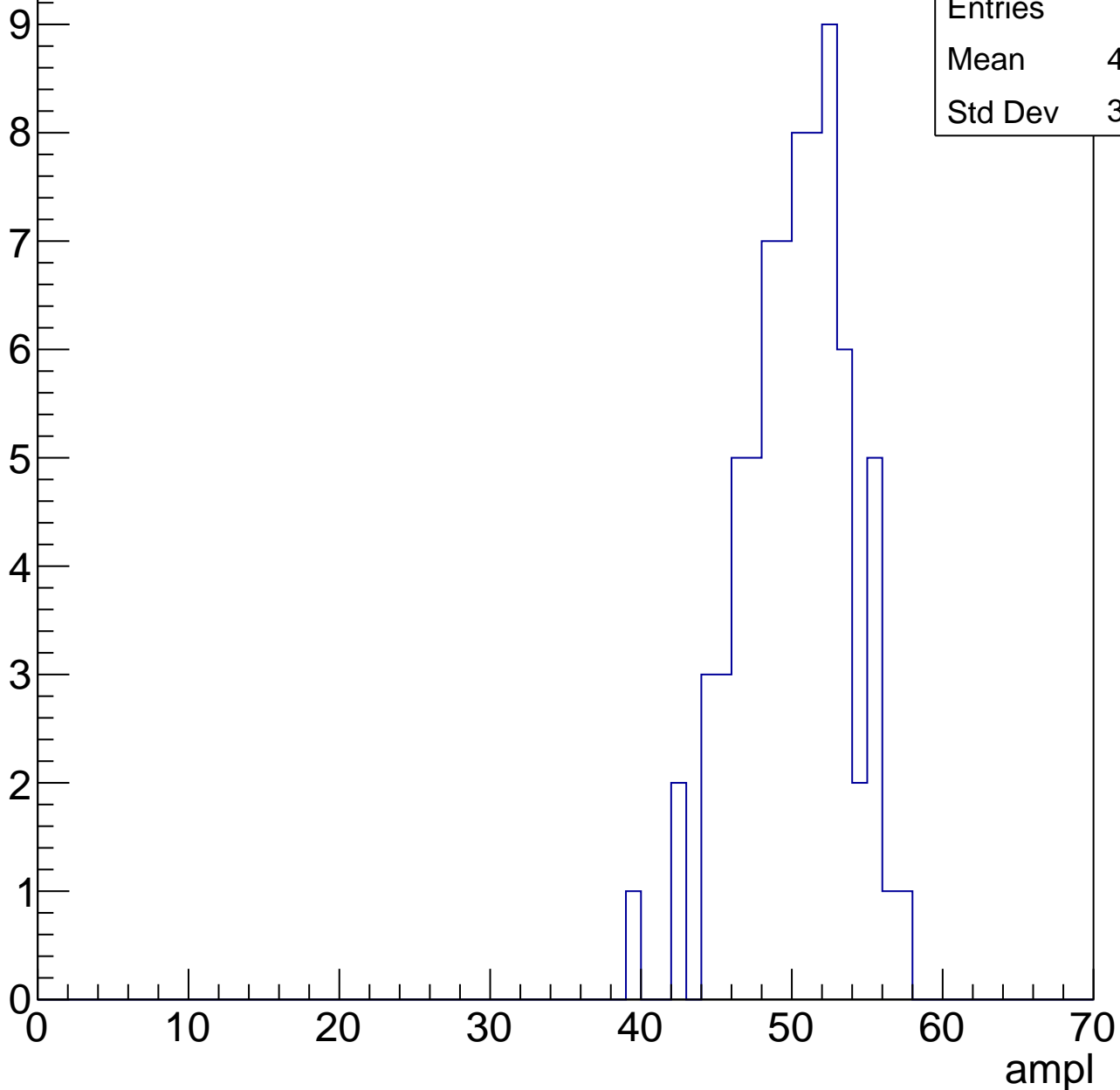


B1L103S, U23-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	49.64
Std Dev	3.555

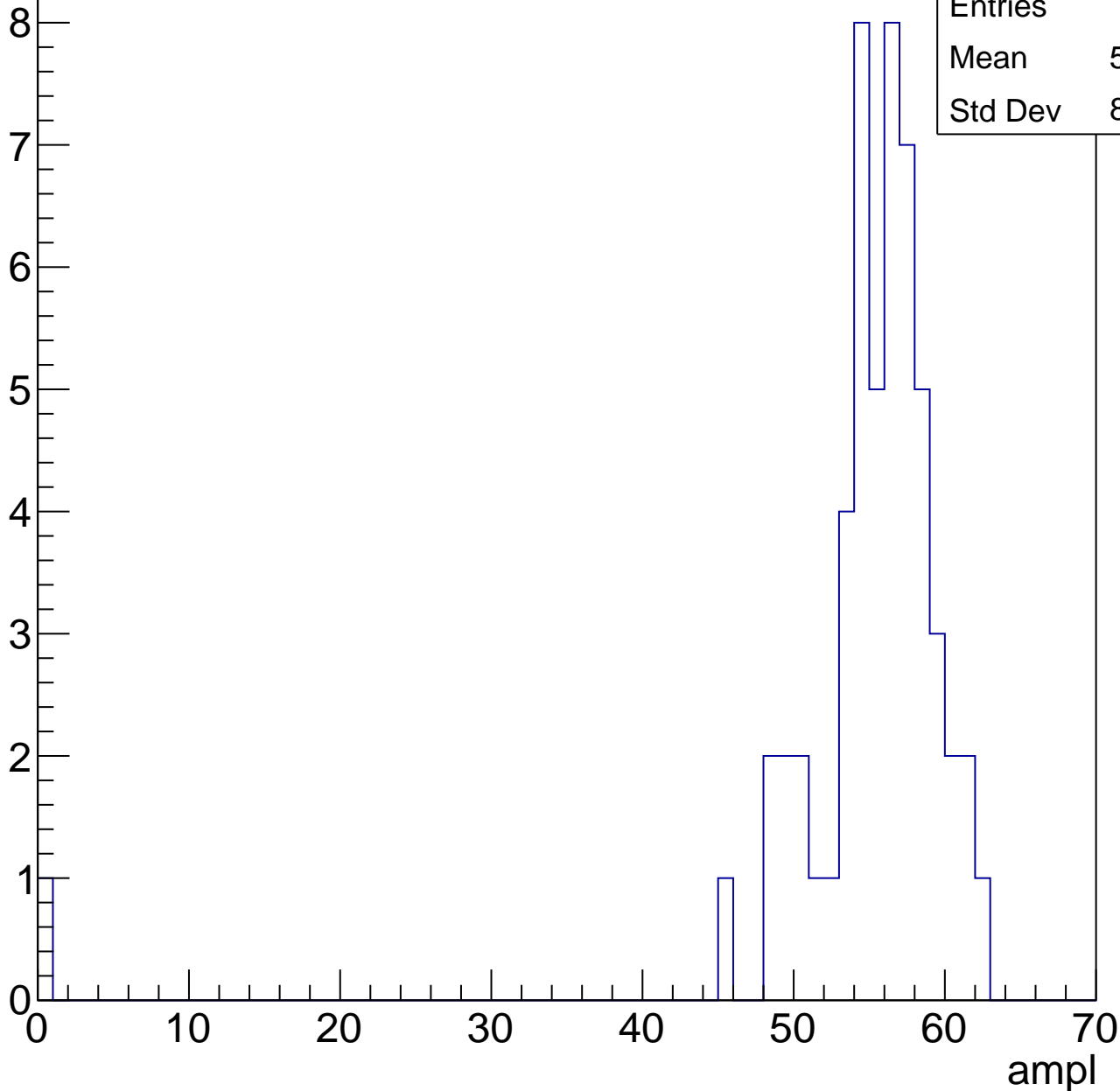


B1L103S, U23-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.16
Std Dev	8.153



B1L103S, U23-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

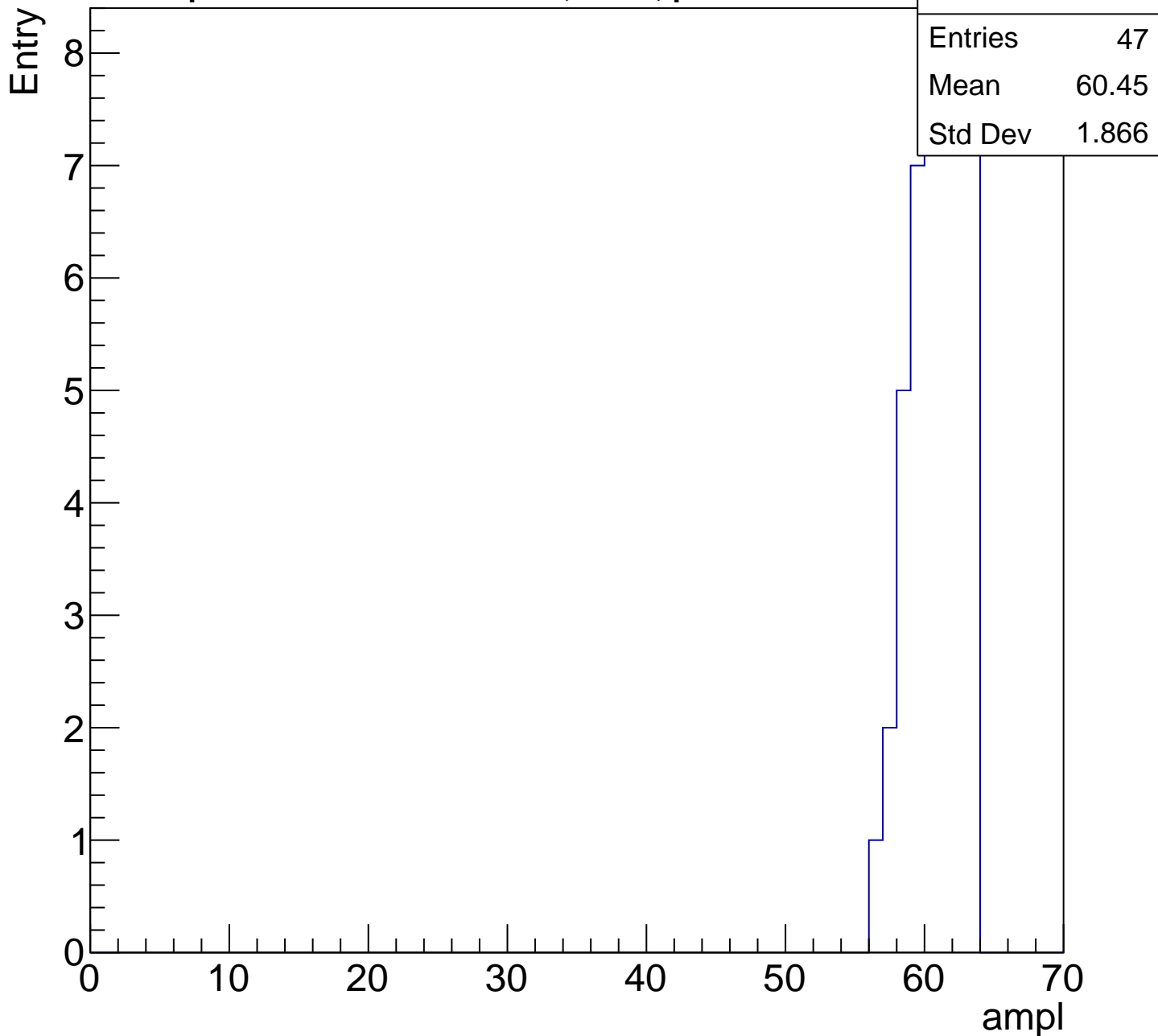
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	60.45
Std Dev	1.866

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch121, adc6

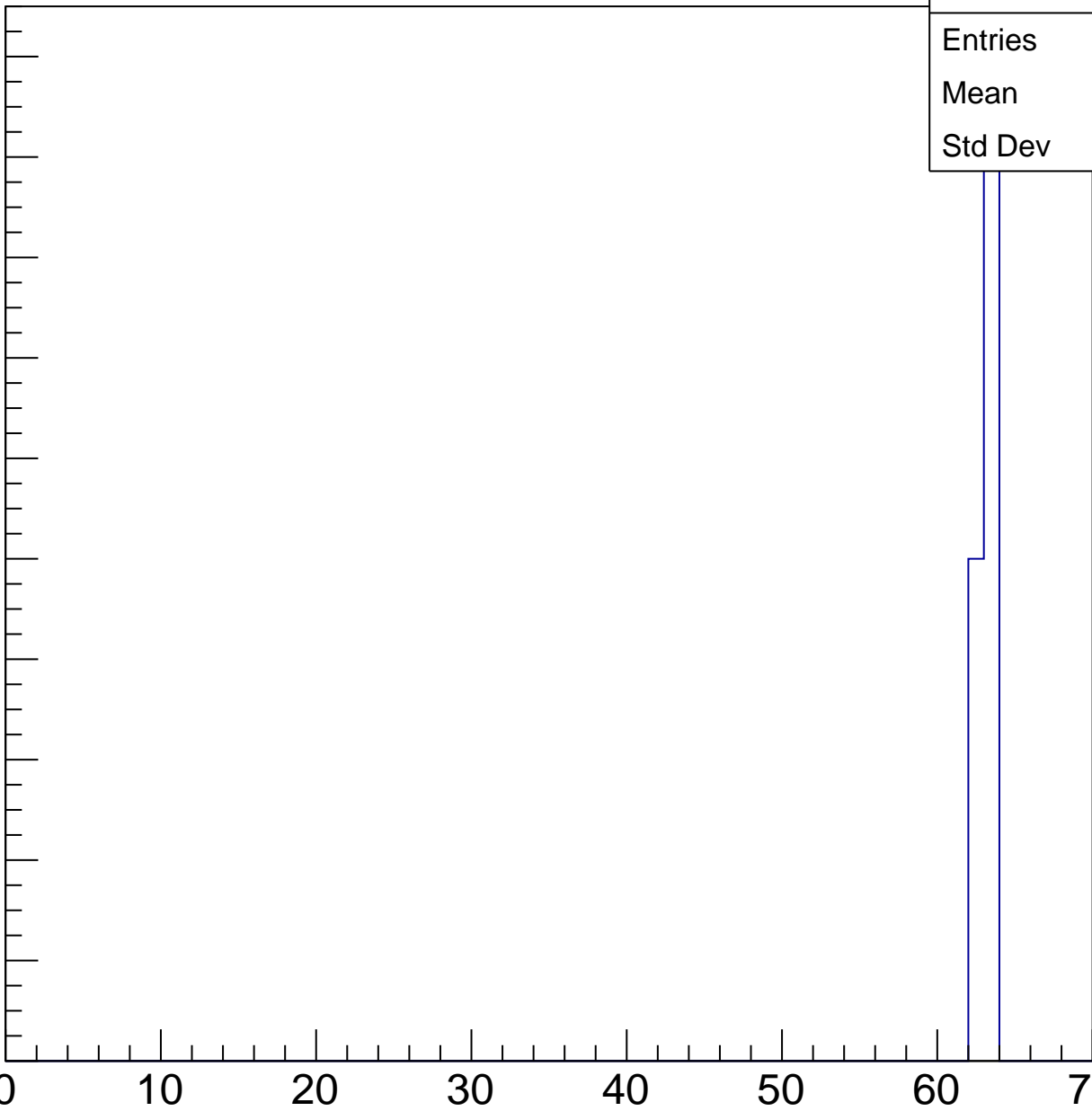
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L103S, U23-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch122, adc0

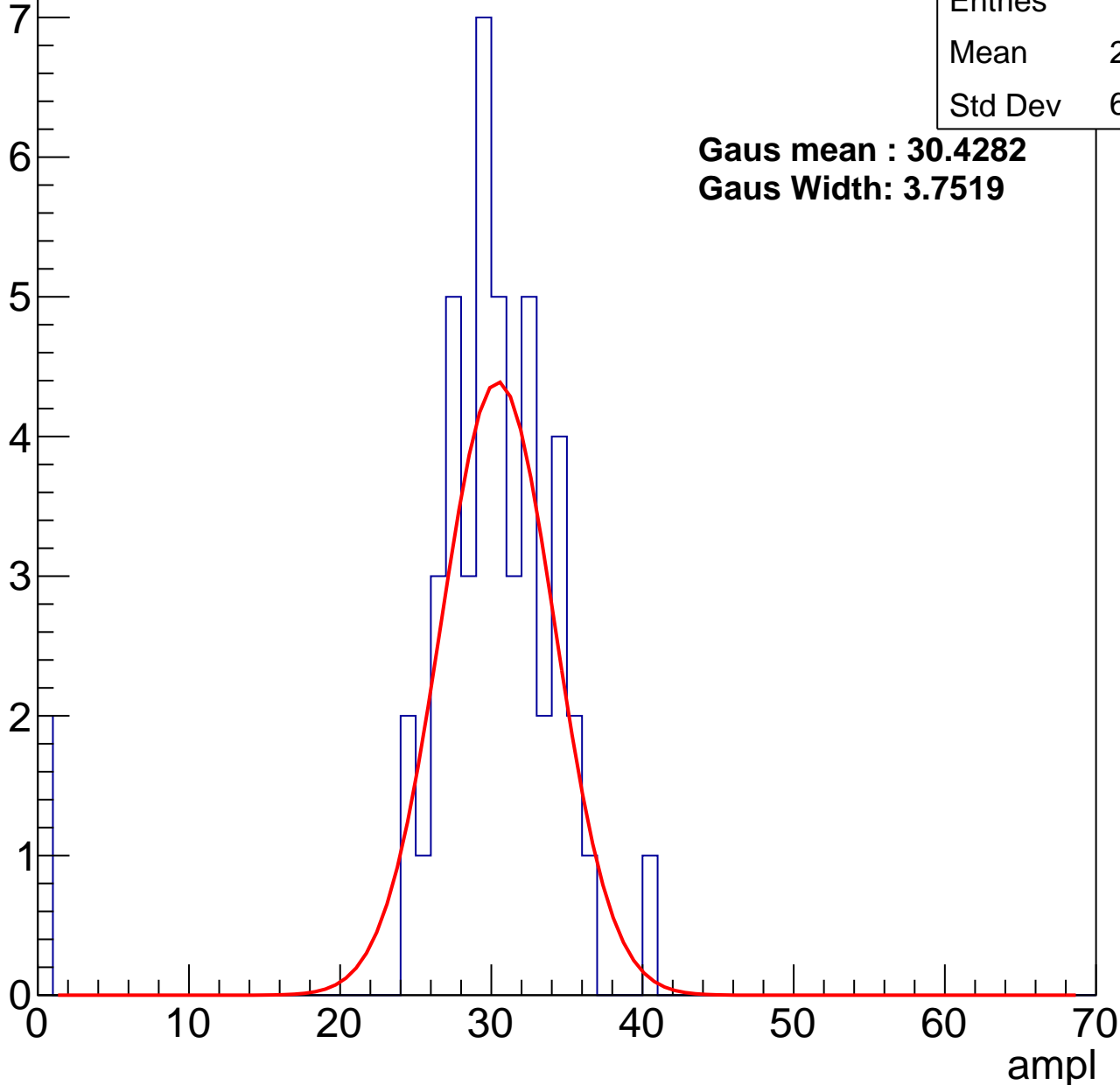
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	28.78
Std Dev	6.965

Gaus mean : 30.4282

Gaus Width: 3.7519



B1L103S, U23-ch122, adc1

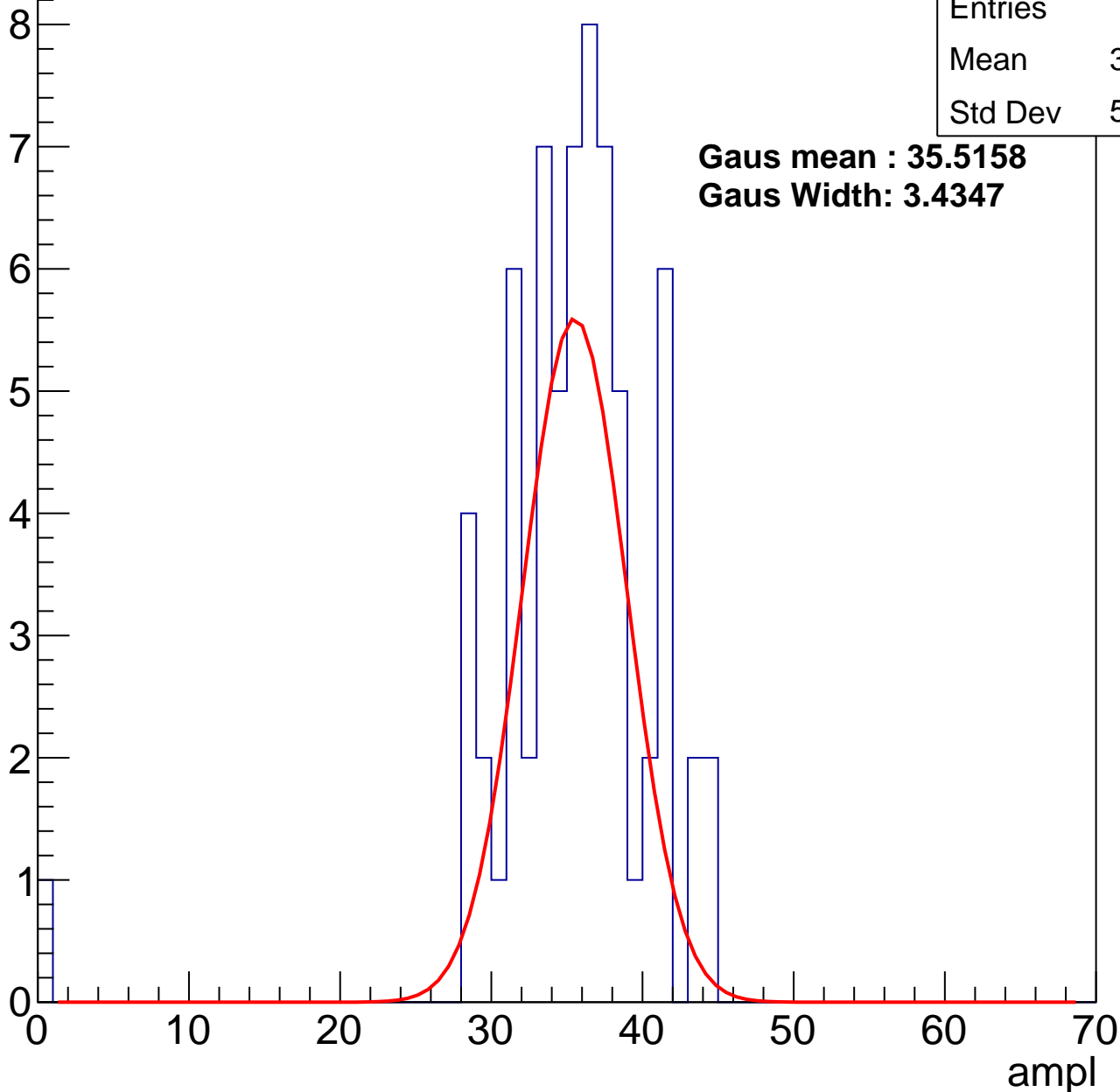
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	34.88
Std Dev	5.837

Gaus mean : 35.5158

Gaus Width: 3.4347



B1L103S, U23-ch122, adc2

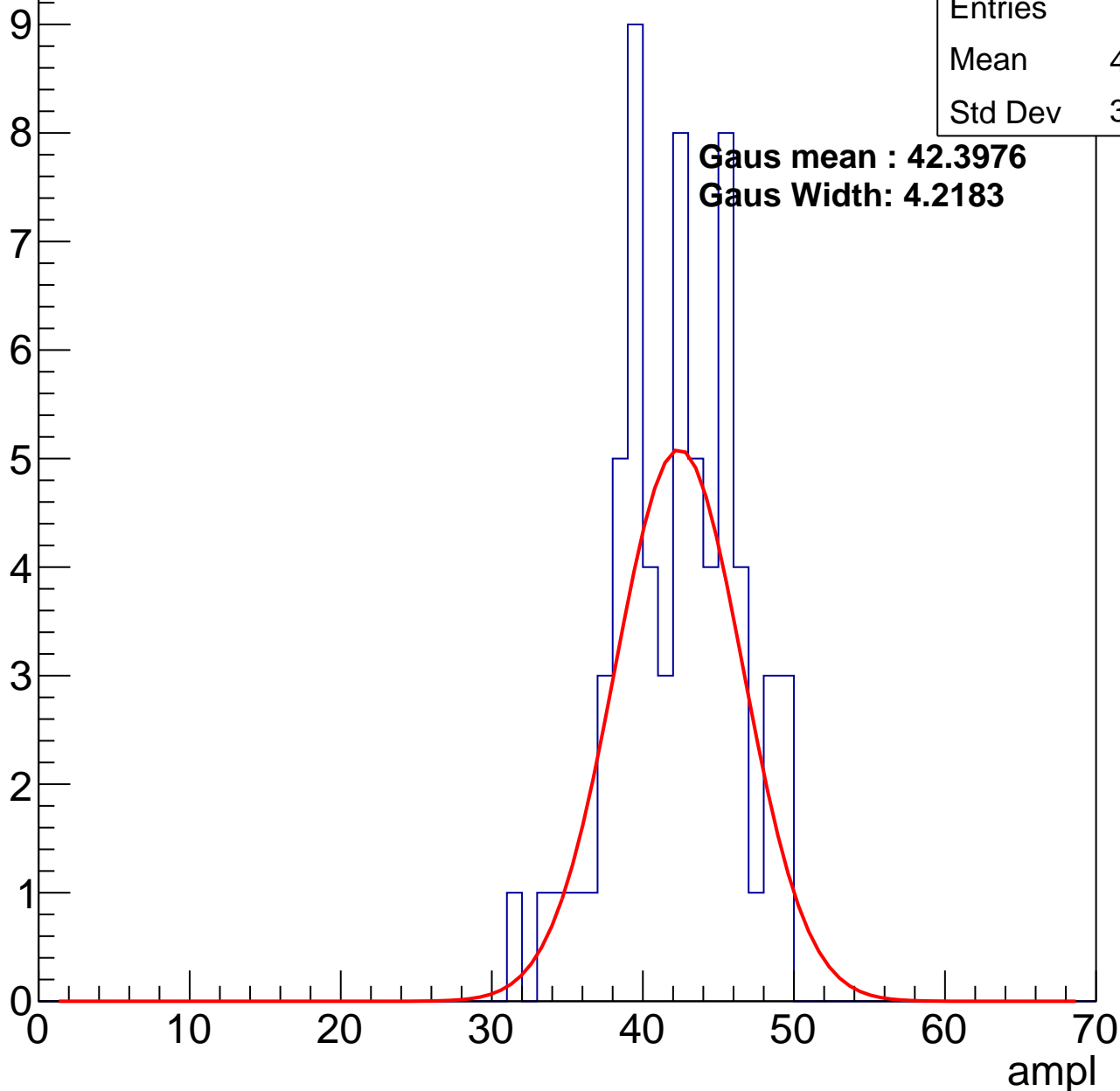
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	41.74
Std Dev	3.997

Gaus mean : 42.3976

Gaus Width: 4.2183

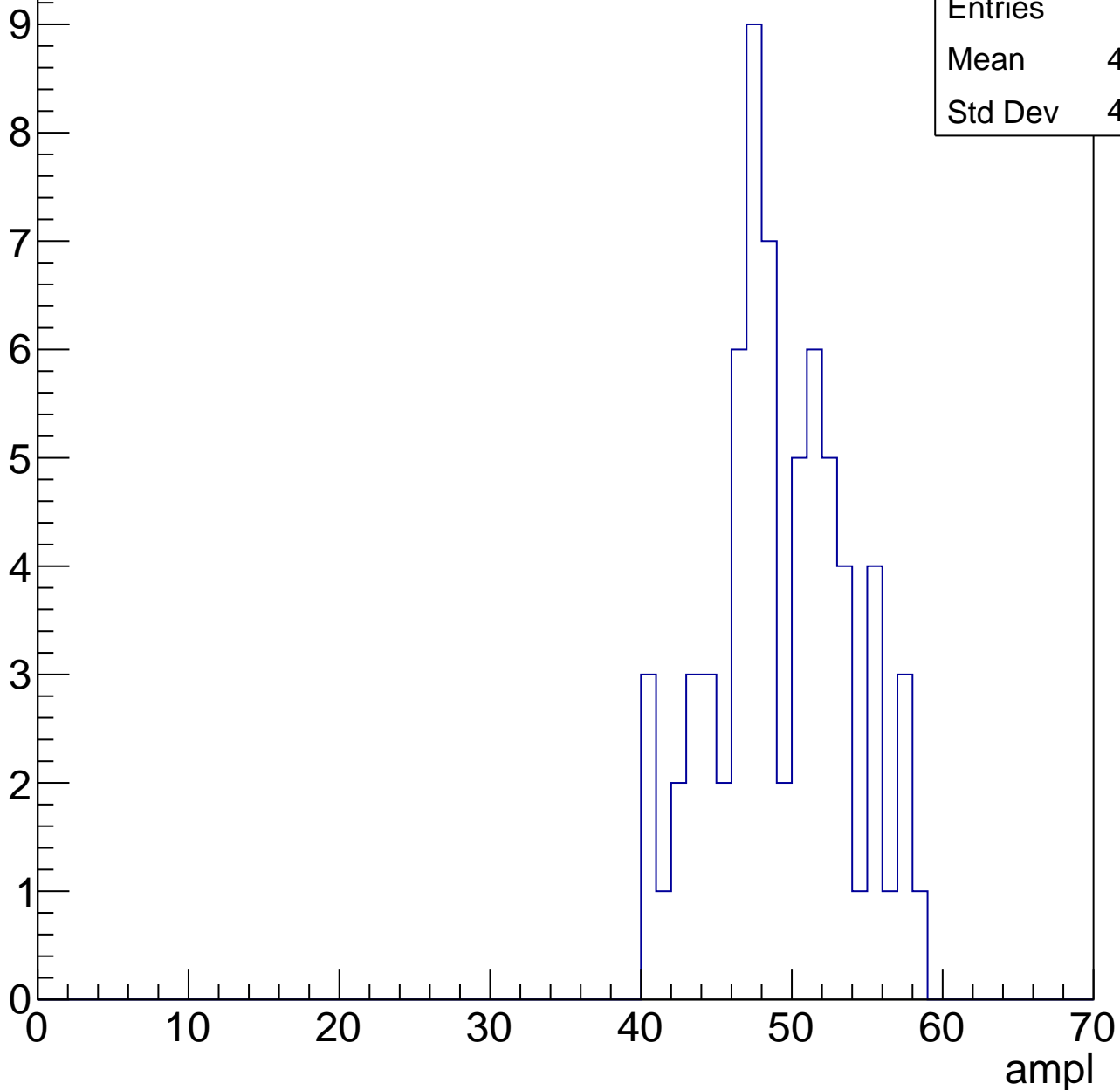


B1L103S, U23-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	48.76
Std Dev	4.443

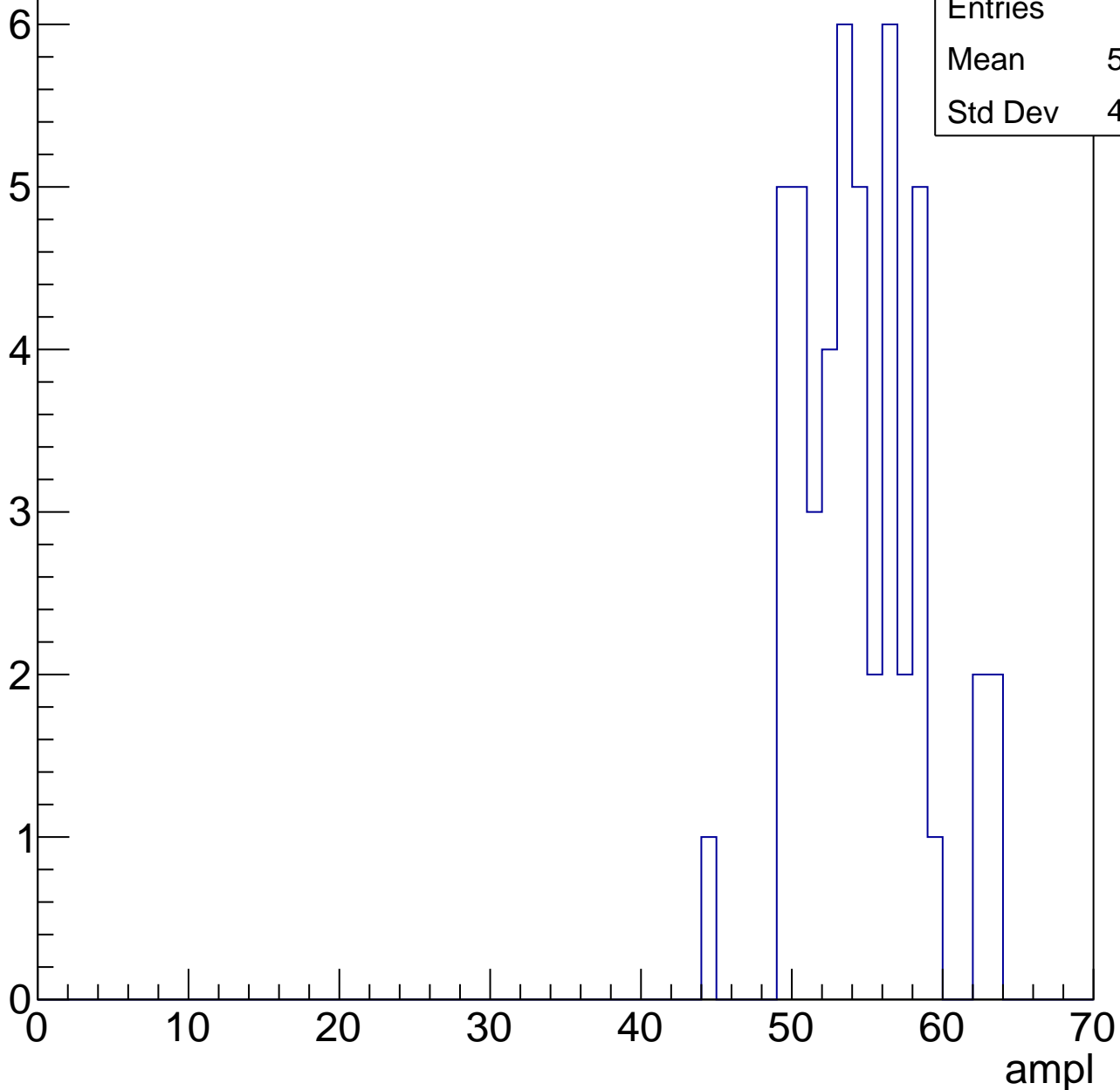


B1L103S, U23-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	54.02
Std Dev	4.023

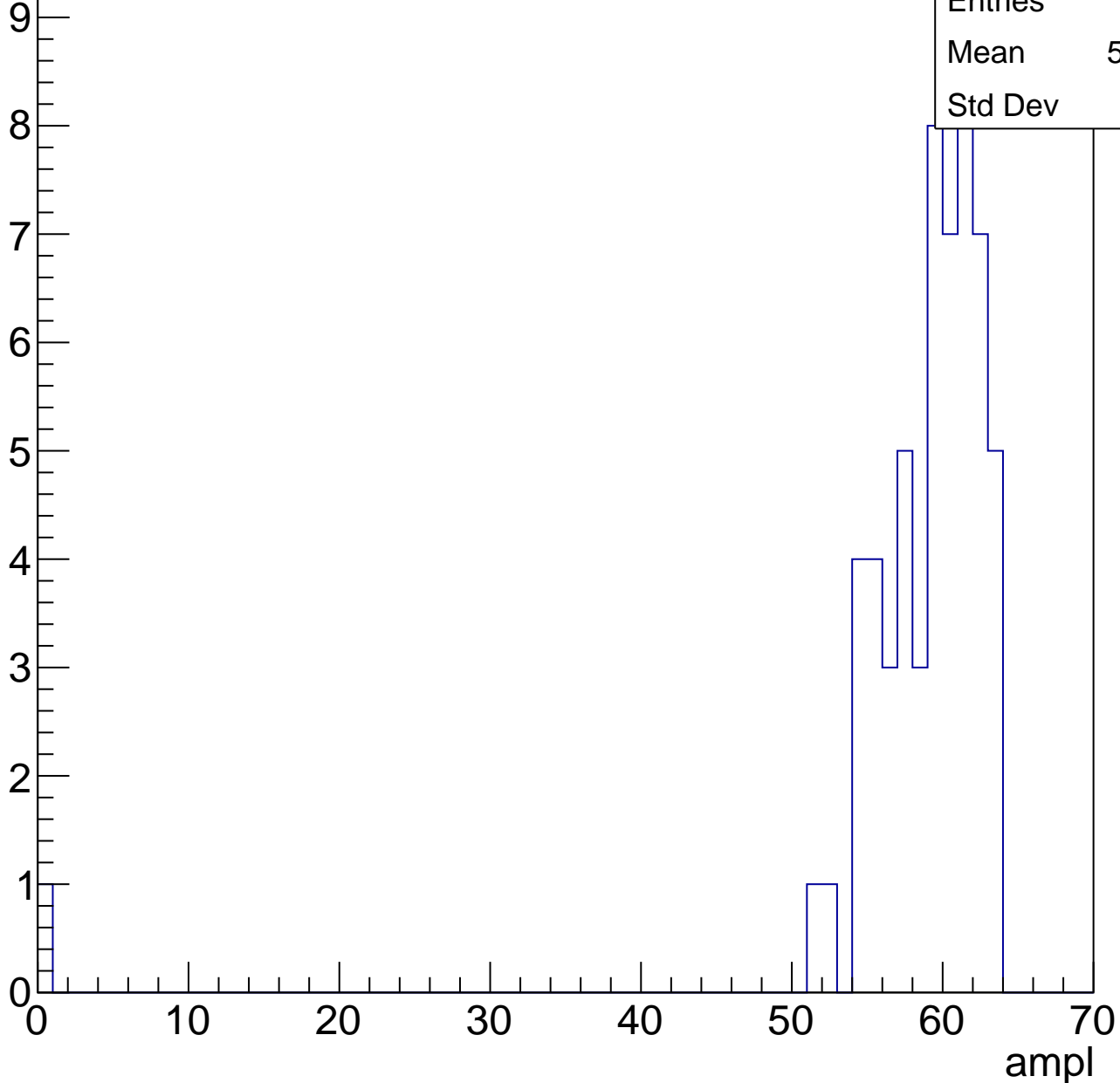


B1L103S, U23-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	57.86
Std Dev	8.22



B1L103S, U23-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

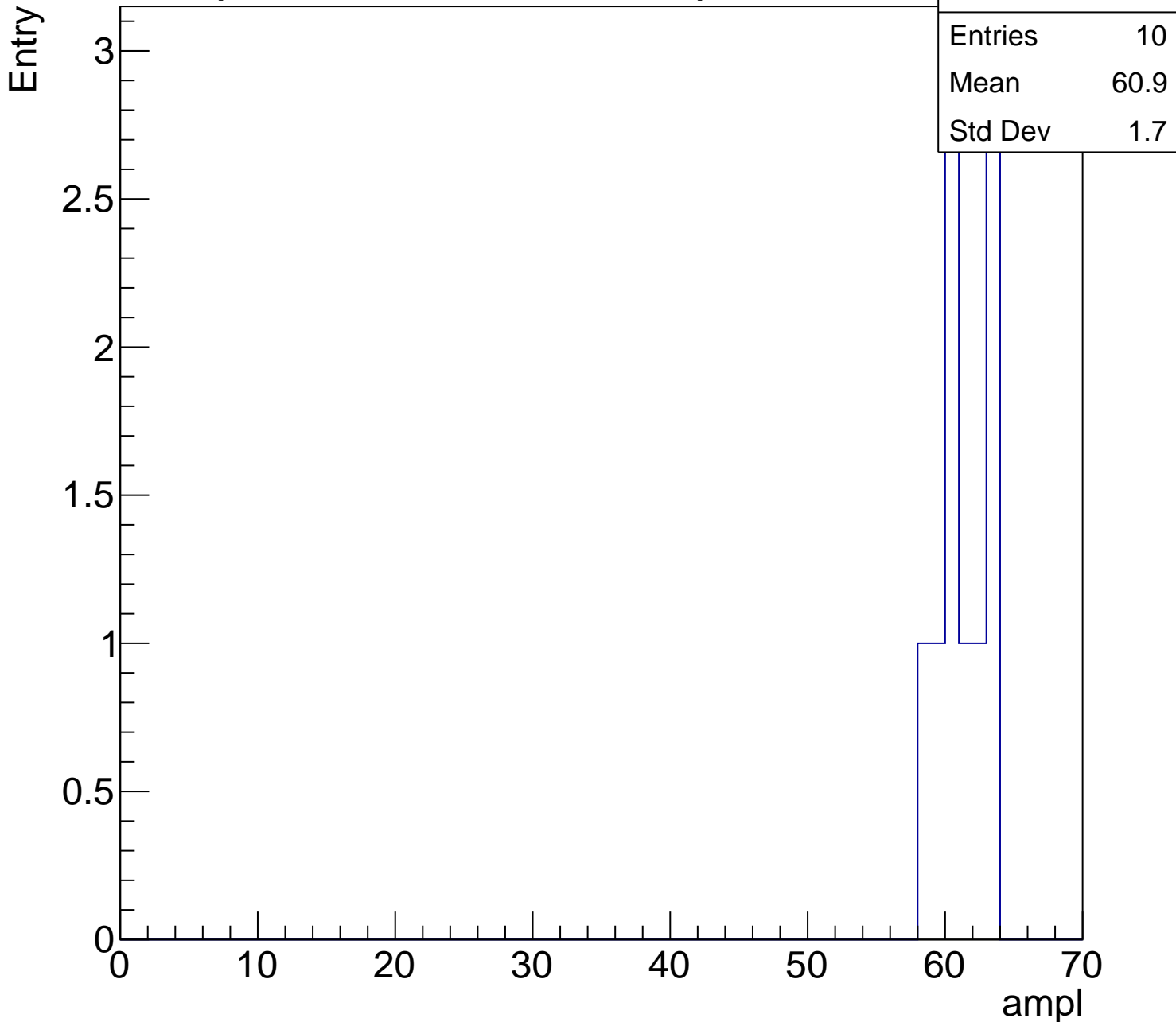
10

Mean

60.9

Std Dev

1.7



B1L103S, U23-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch123, adc0

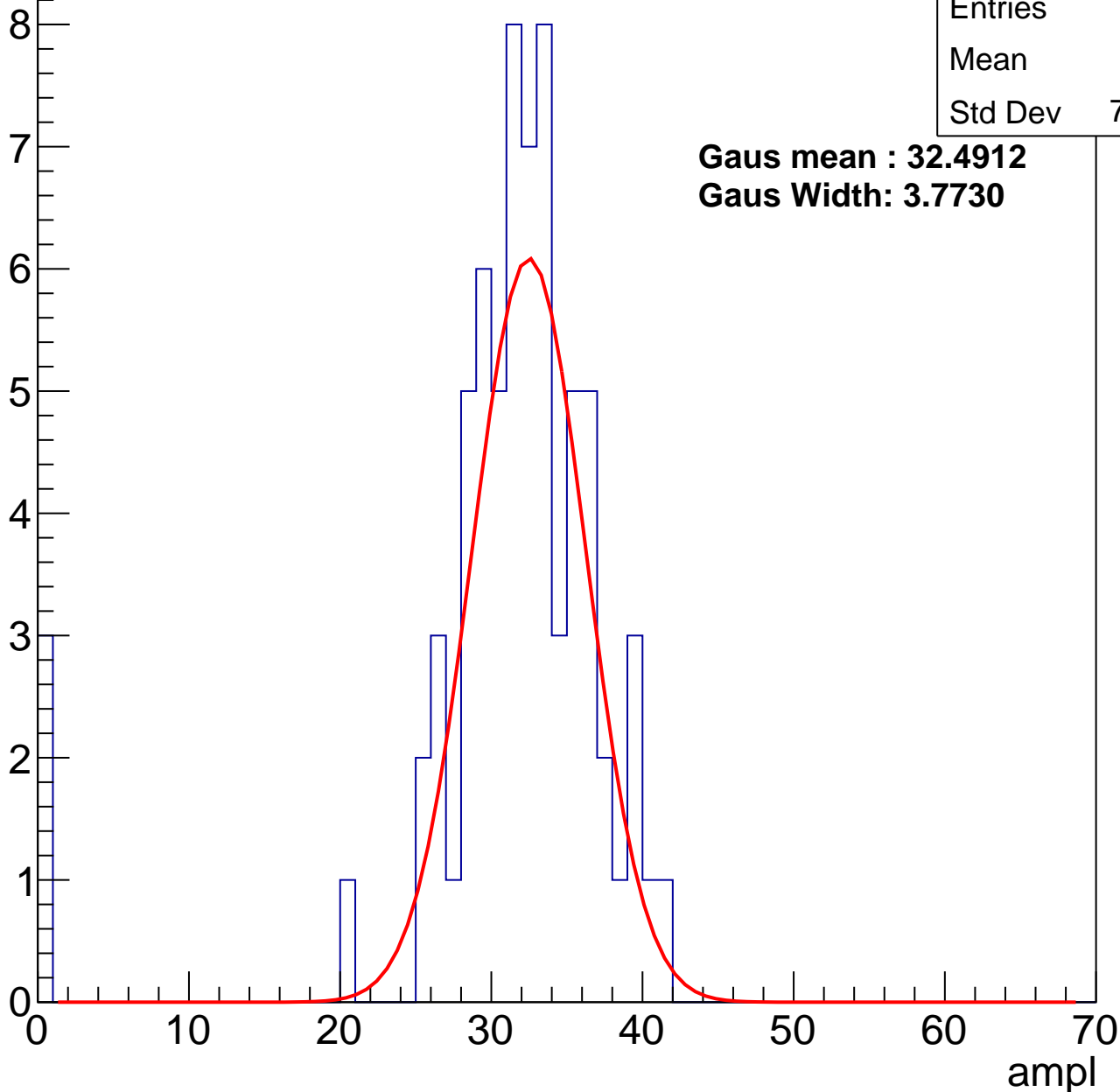
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	30.6
Std Dev	7.554

Gaus mean : 32.4912

Gaus Width: 3.7730



B1L103S, U23-ch123, adc1

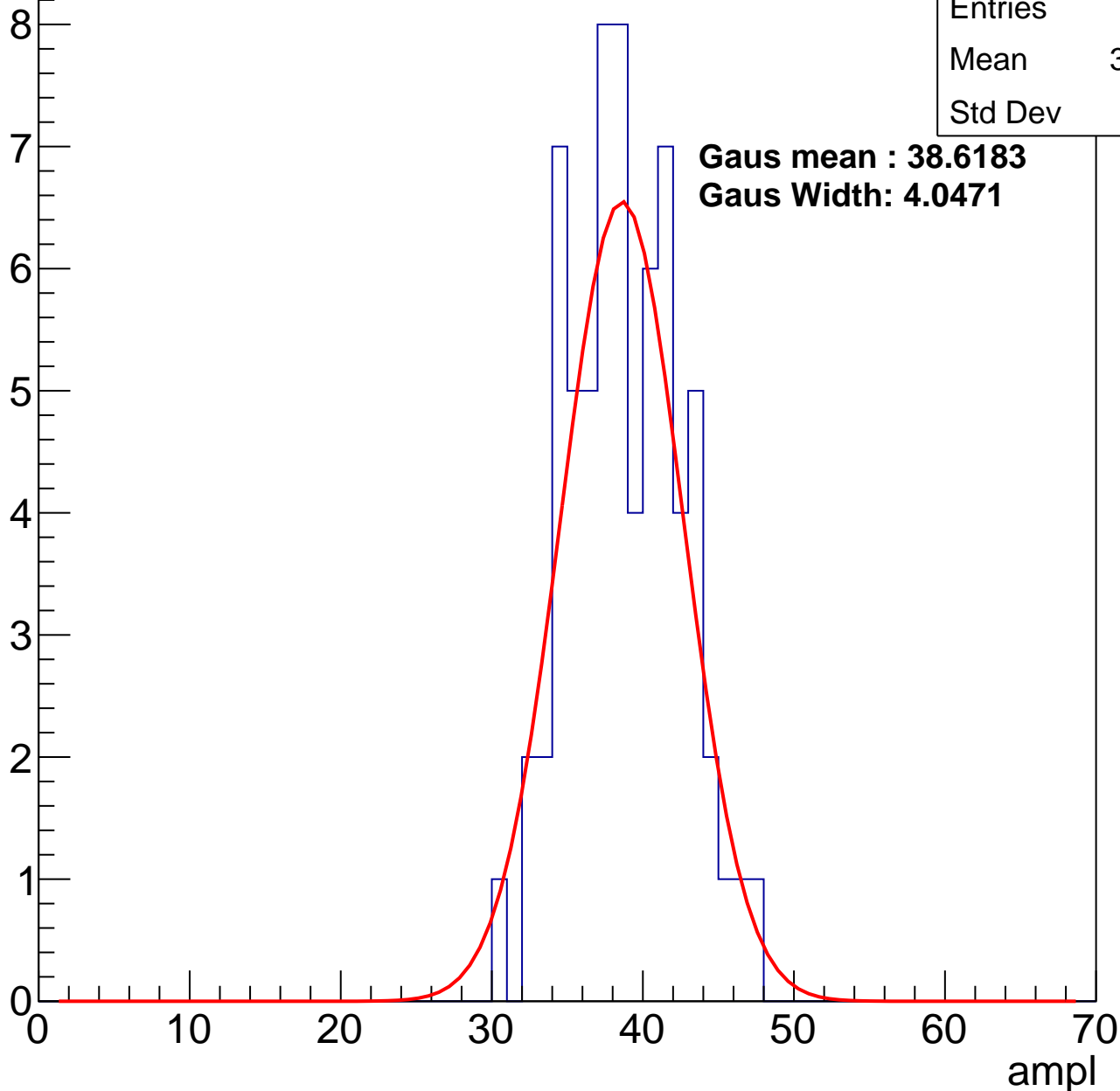
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	38.33
Std Dev	3.63

Gaus mean : 38.6183

Gaus Width: 4.0471



B1L103S, U23-ch123, adc2

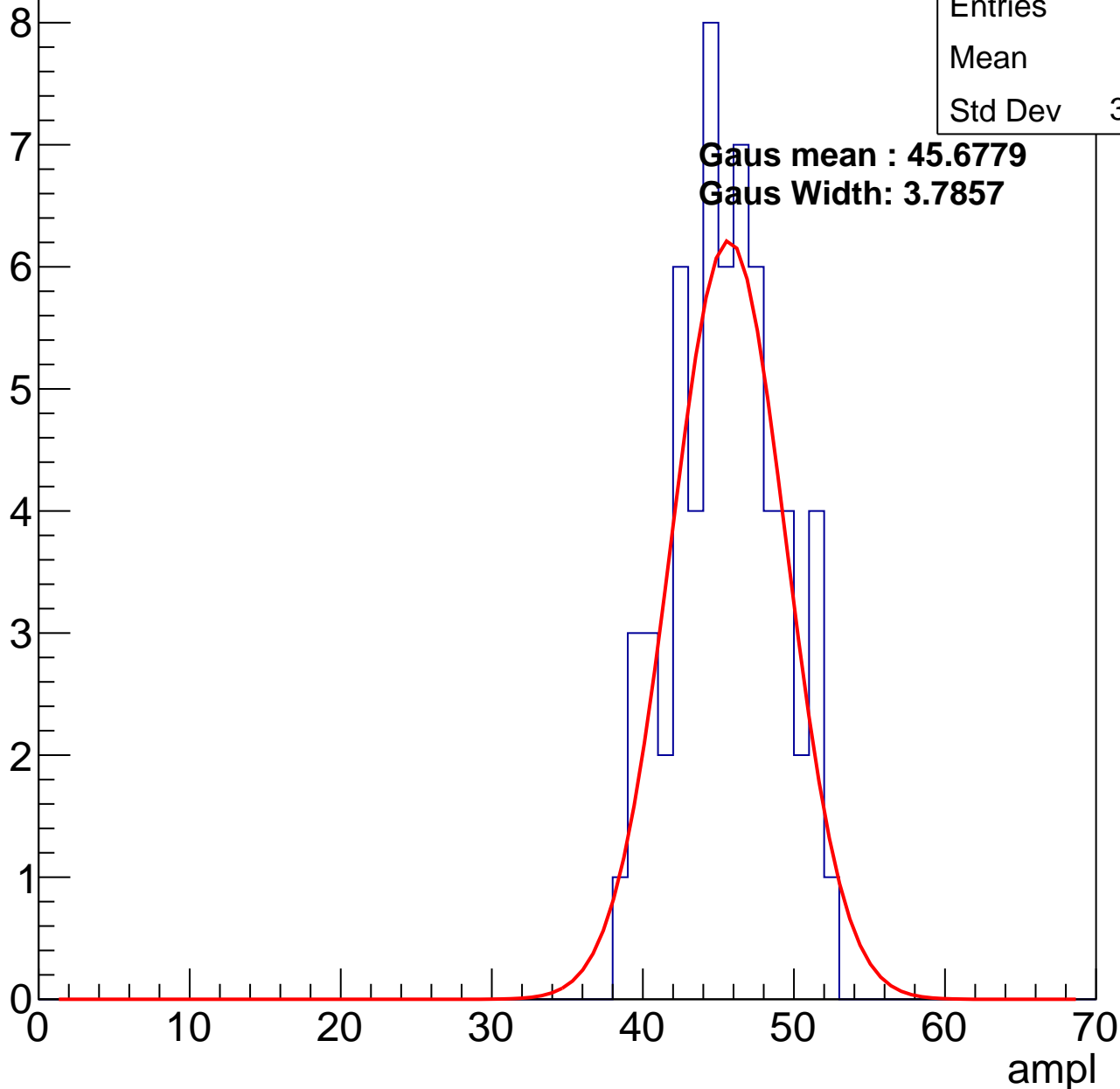
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	45.1
Std Dev	3.425

Gaus mean : 45.6779

Gaus Width: 3.7857

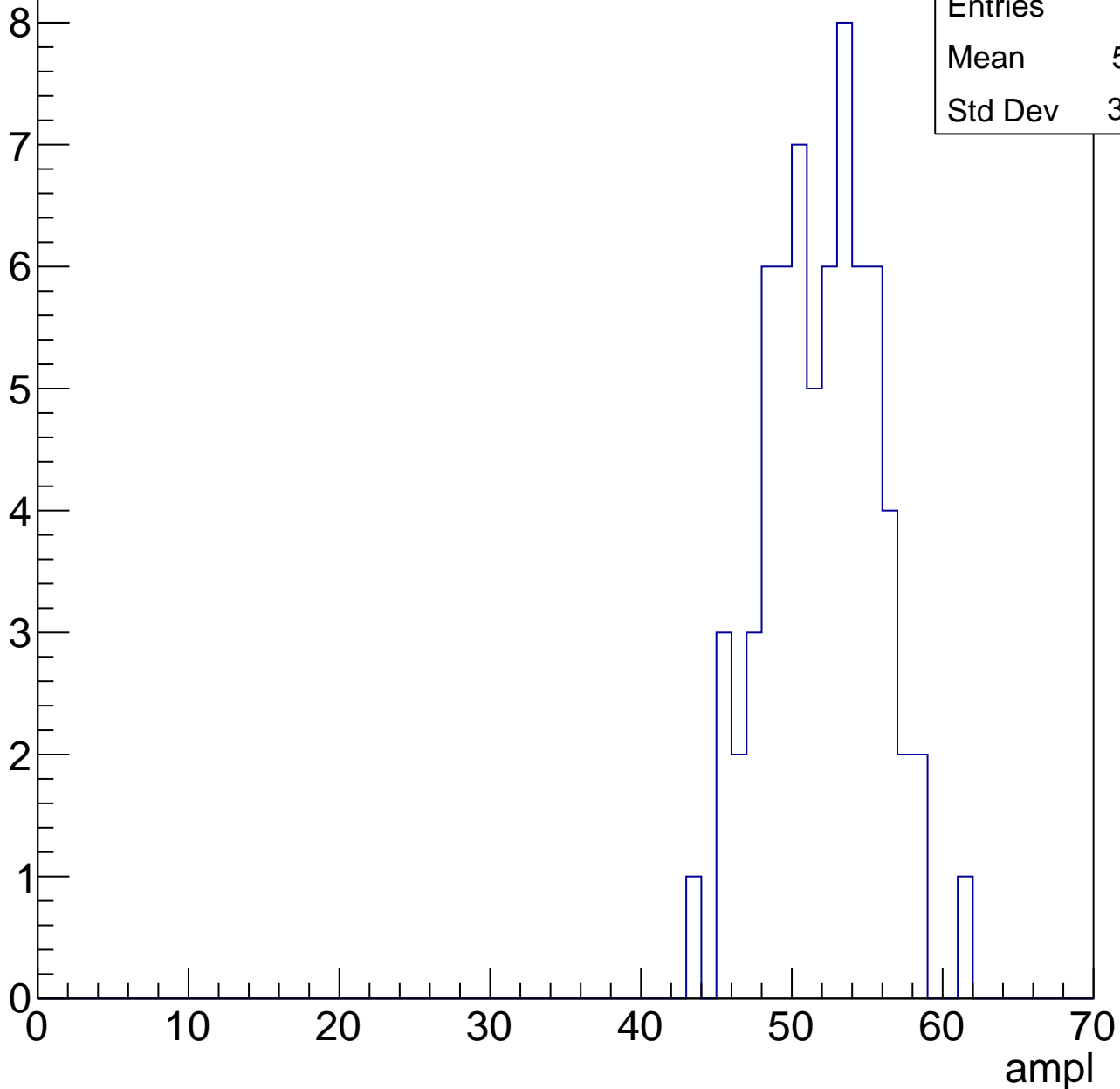


B1L103S, U23-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	51.51
Std Dev	3.628

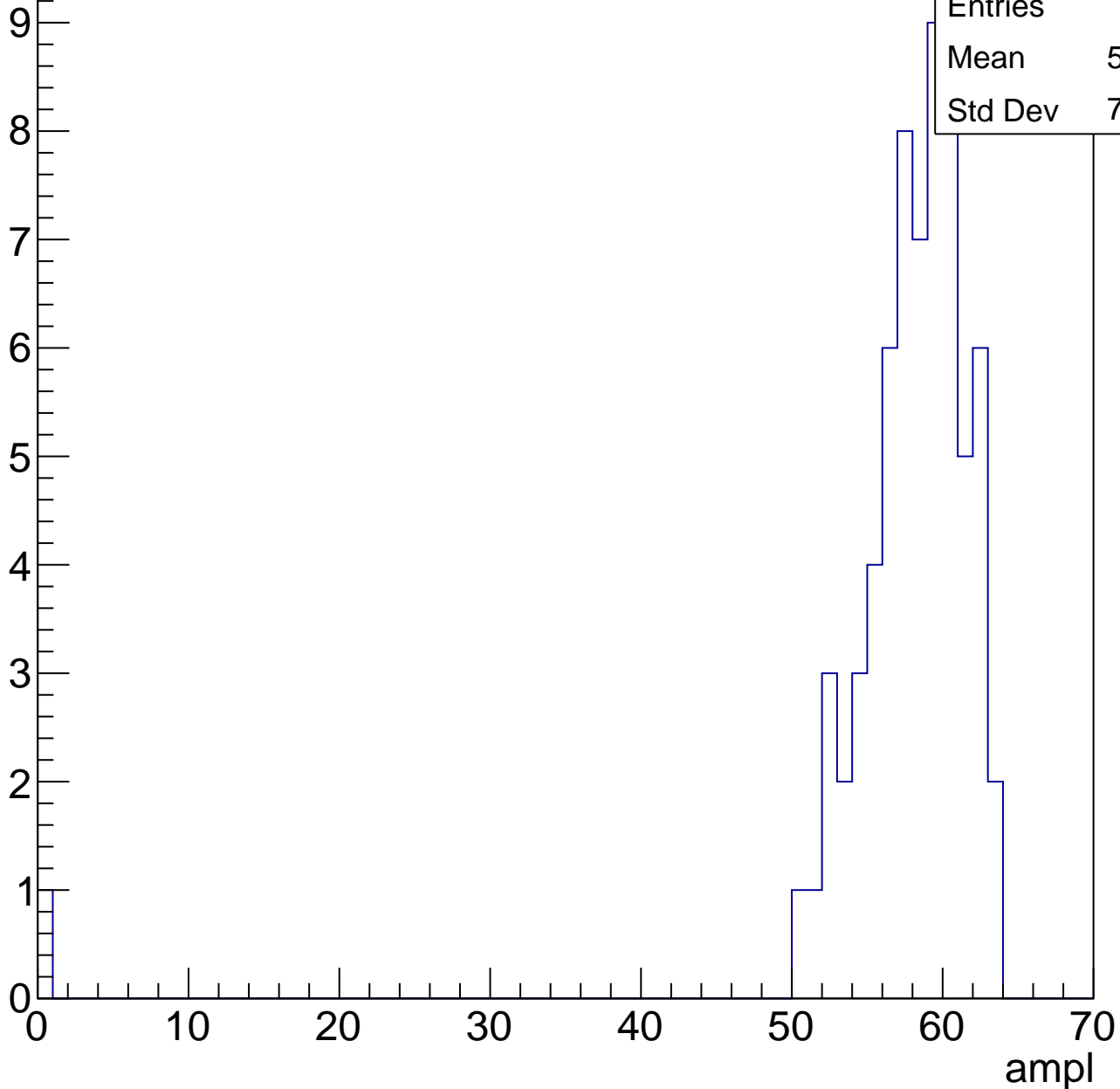


B1L103S, U23-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	56.92
Std Dev	7.694

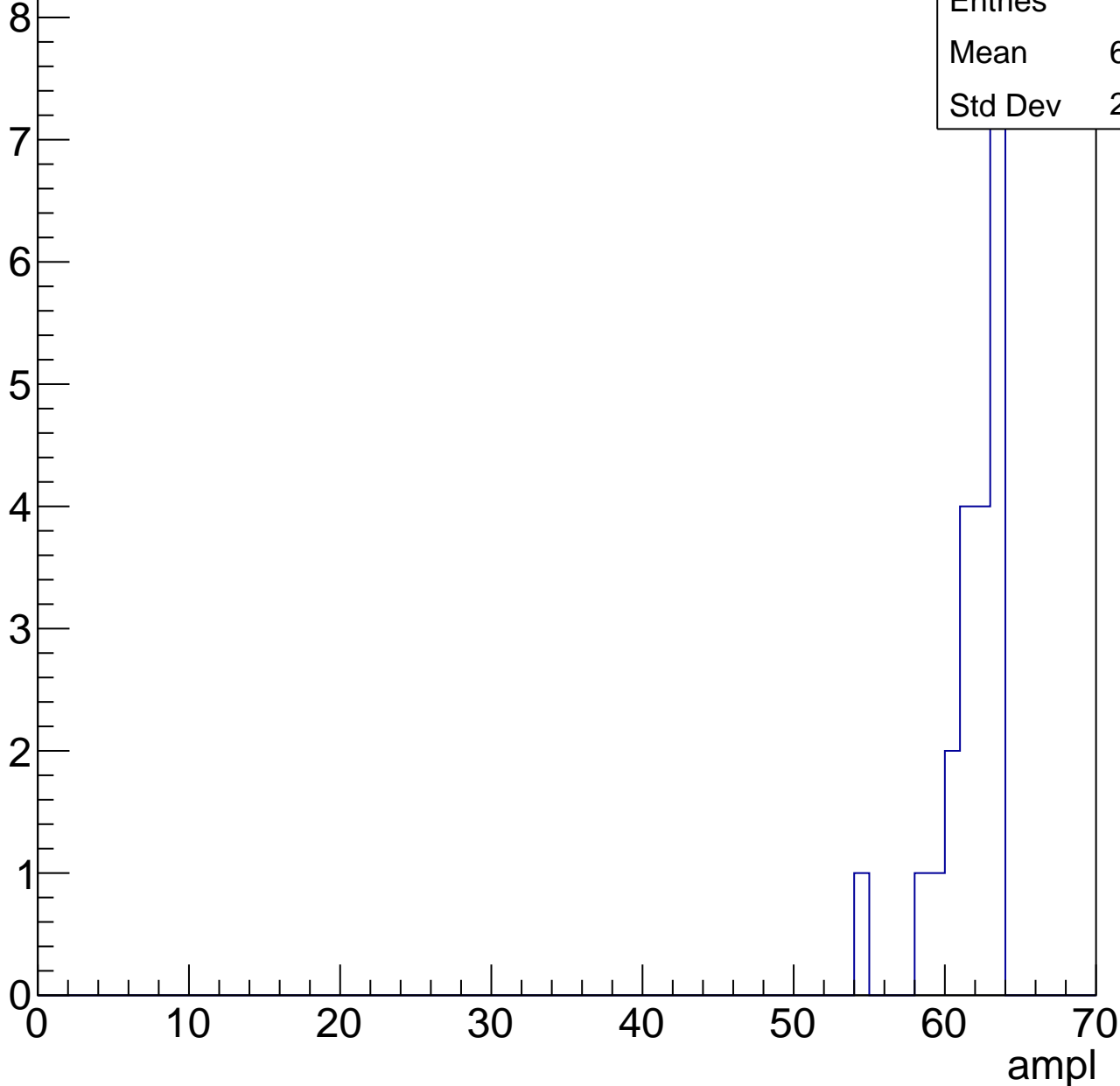


B1L103S, U23-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	61.29
Std Dev	2.163



B1L103S, U23-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch124, adc0

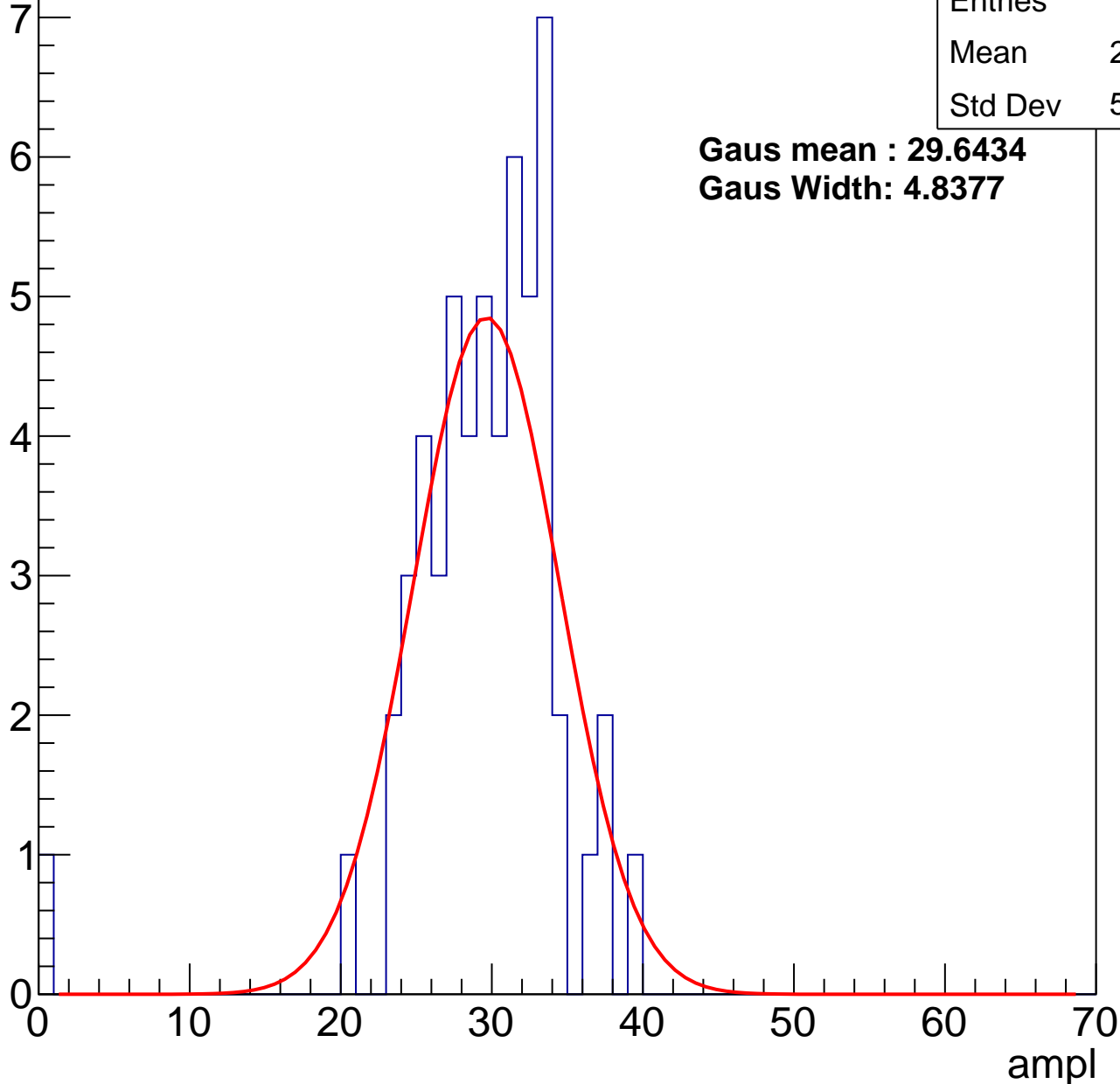
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	28.96
Std Dev	5.503

Gaus mean : 29.6434

Gaus Width: 4.8377



B1L103S, U23-ch124, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	36.49
Std Dev	3.87

Gaus mean : 37.6378

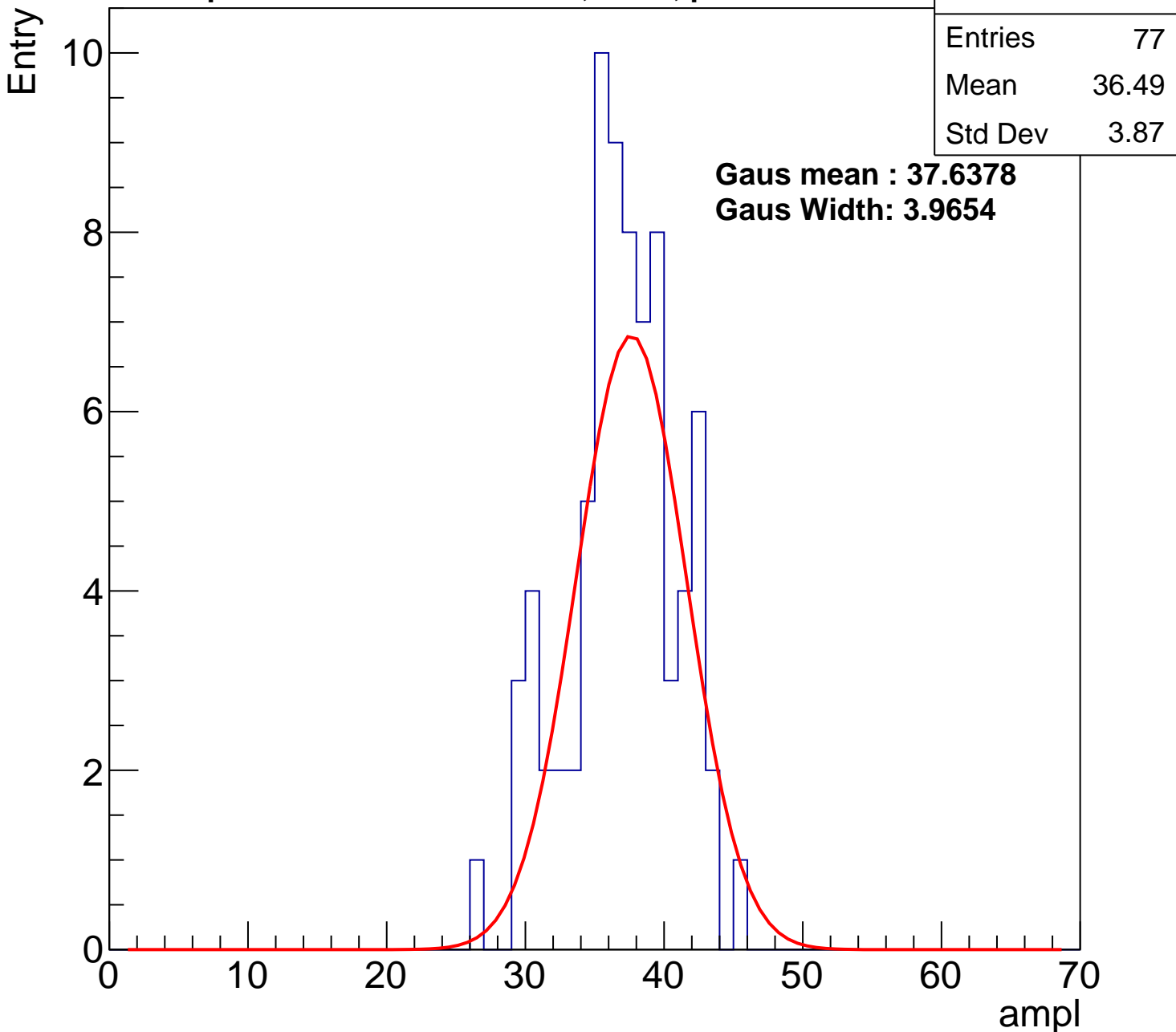
Gaus Width: 3.9654

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch124, adc2

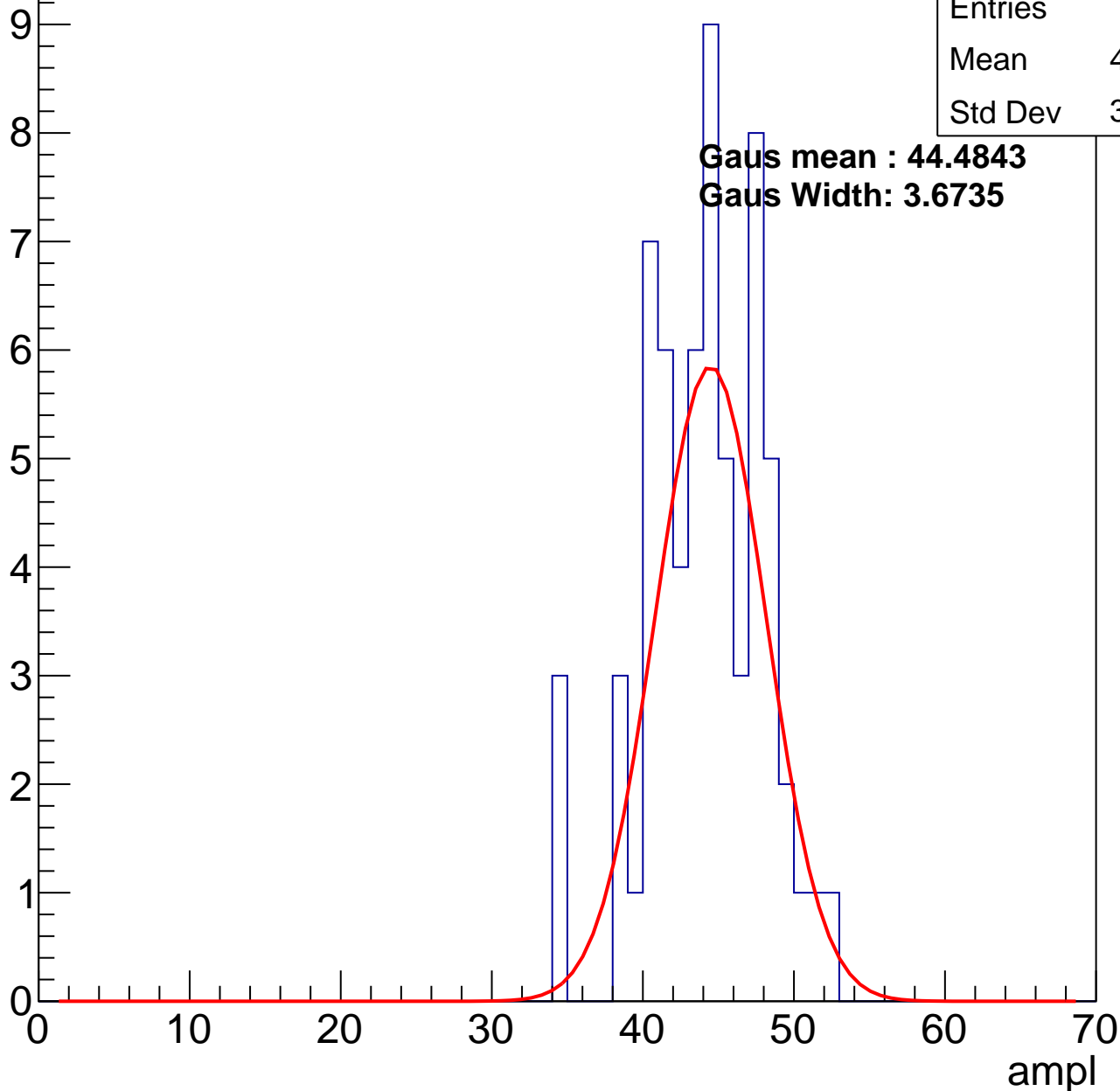
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	43.58
Std Dev	3.879

Gaus mean : 44.4843

Gaus Width: 3.6735

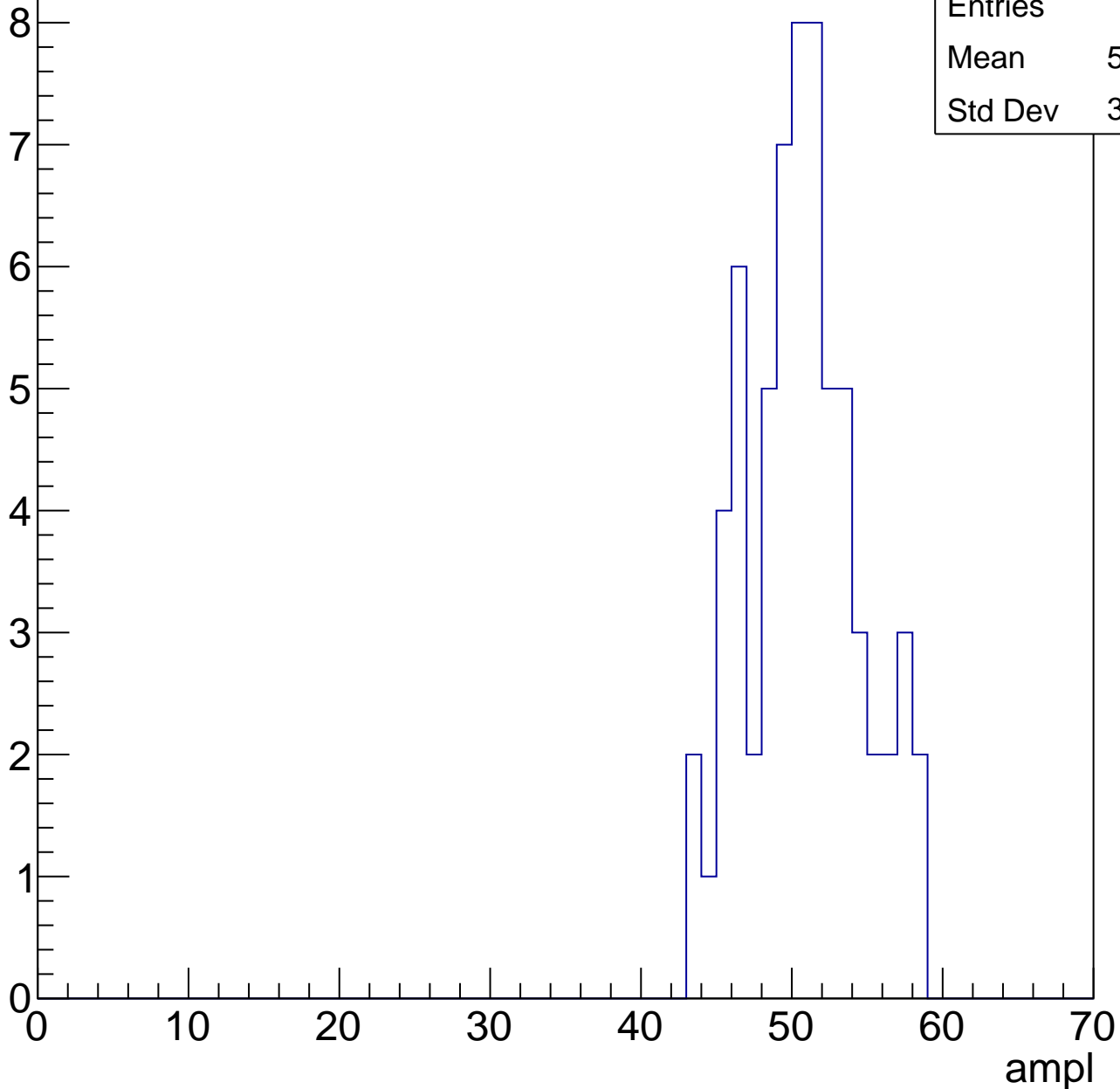


B1L103S, U23-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	50.26
Std Dev	3.697

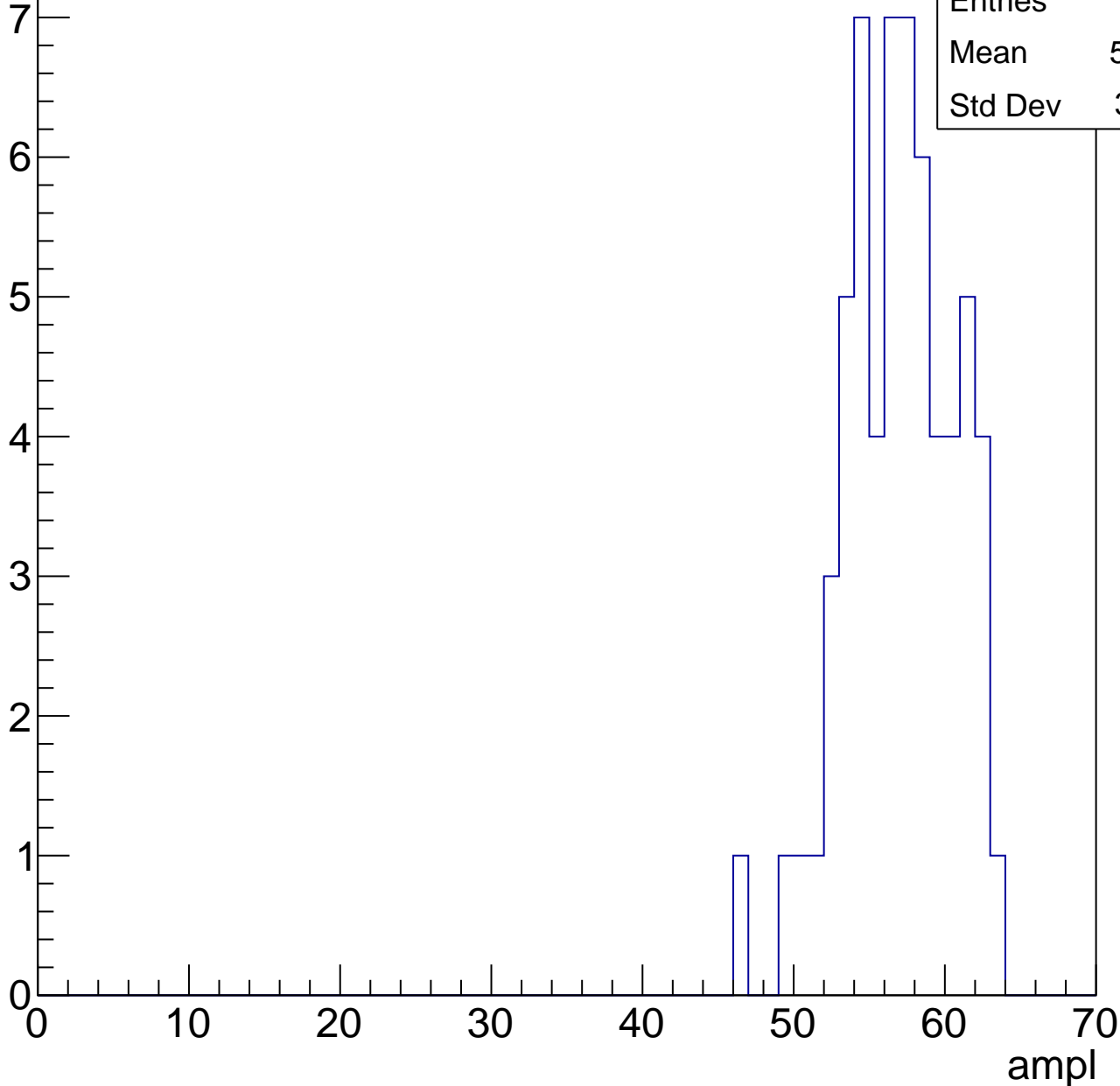


B1L103S, U23-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	56.49
Std Dev	3.551

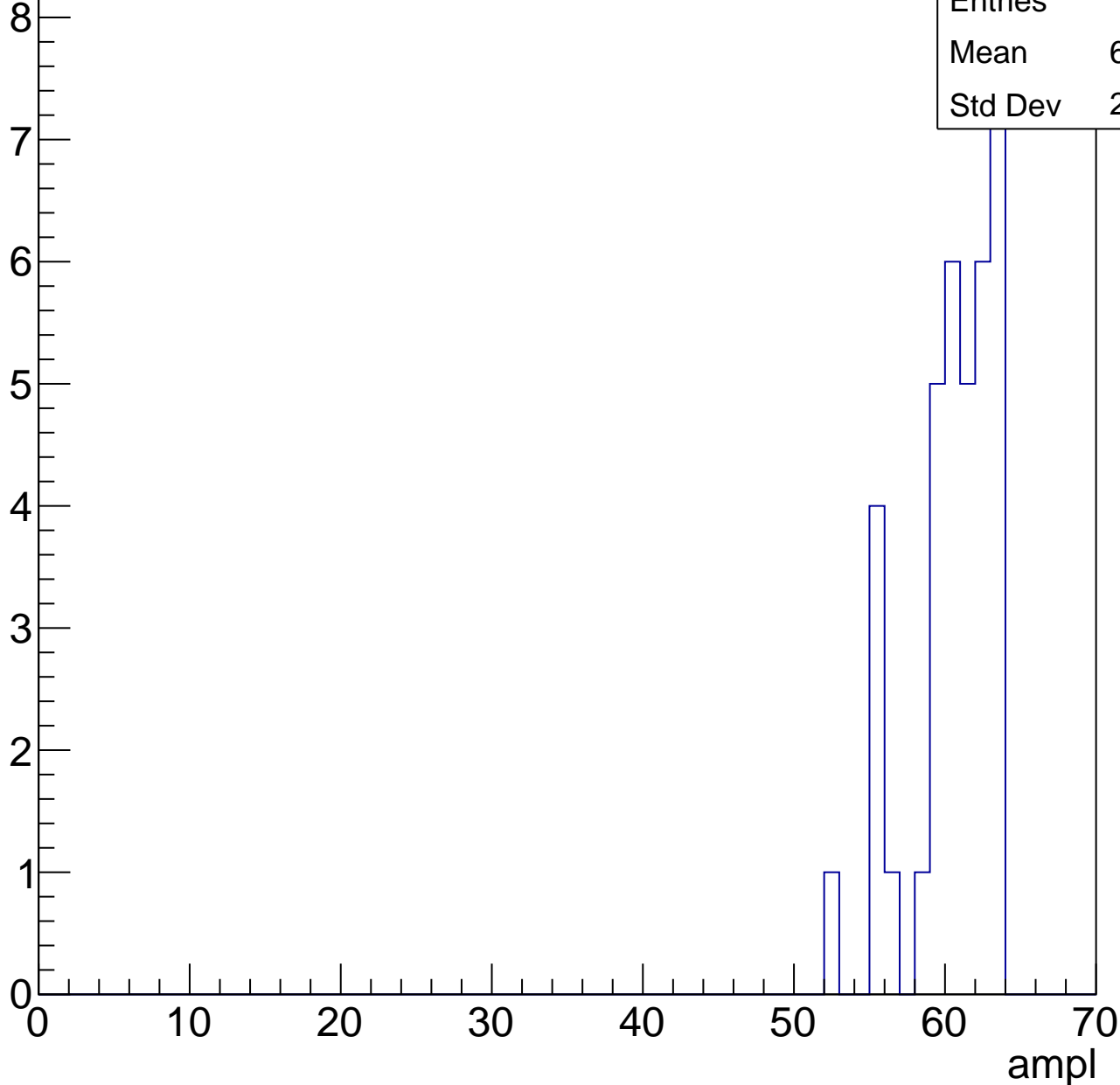


B1L103S, U23-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

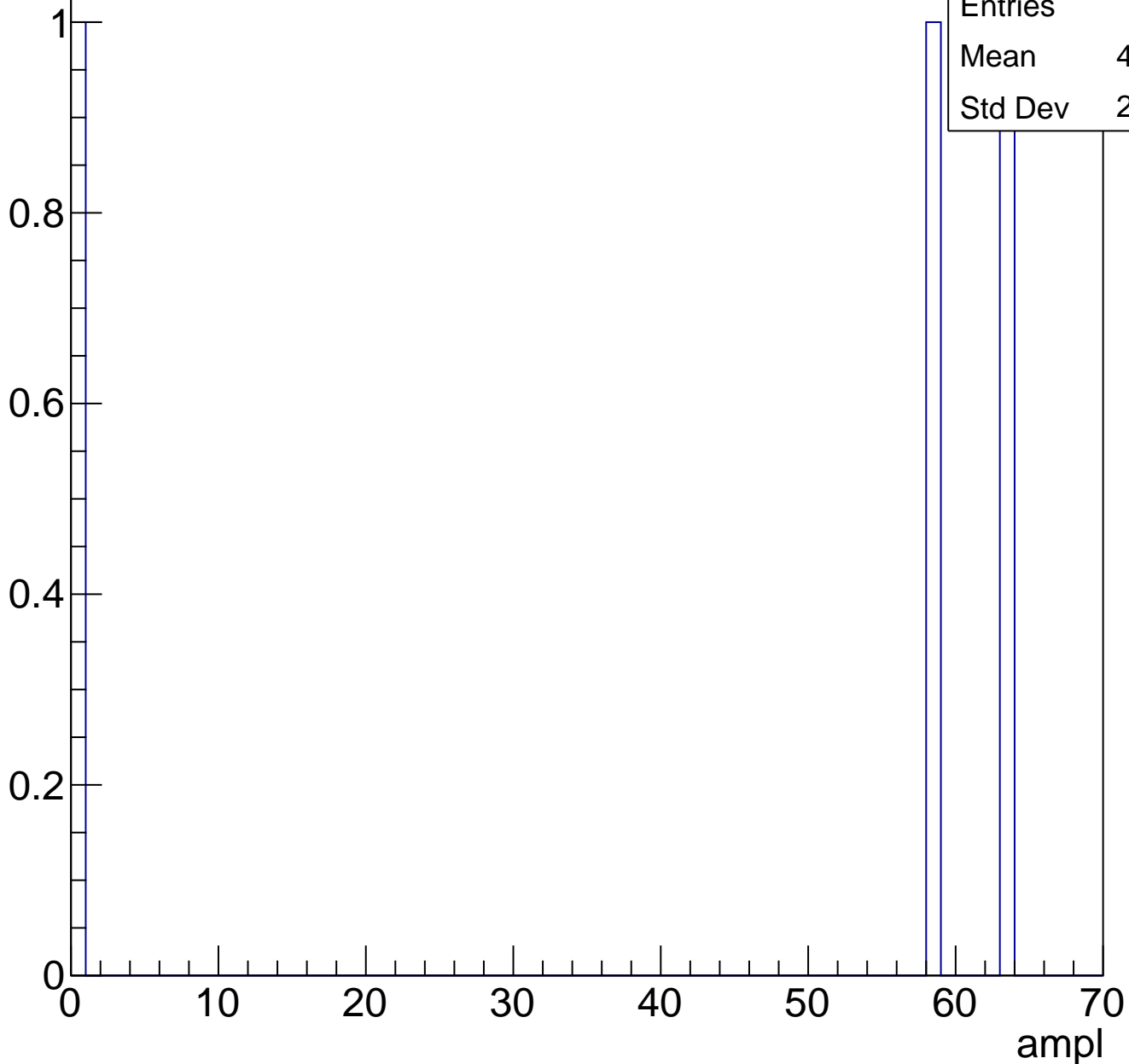
Entries	37
Mean	60.05
Std Dev	2.799



B1L103S, U23-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

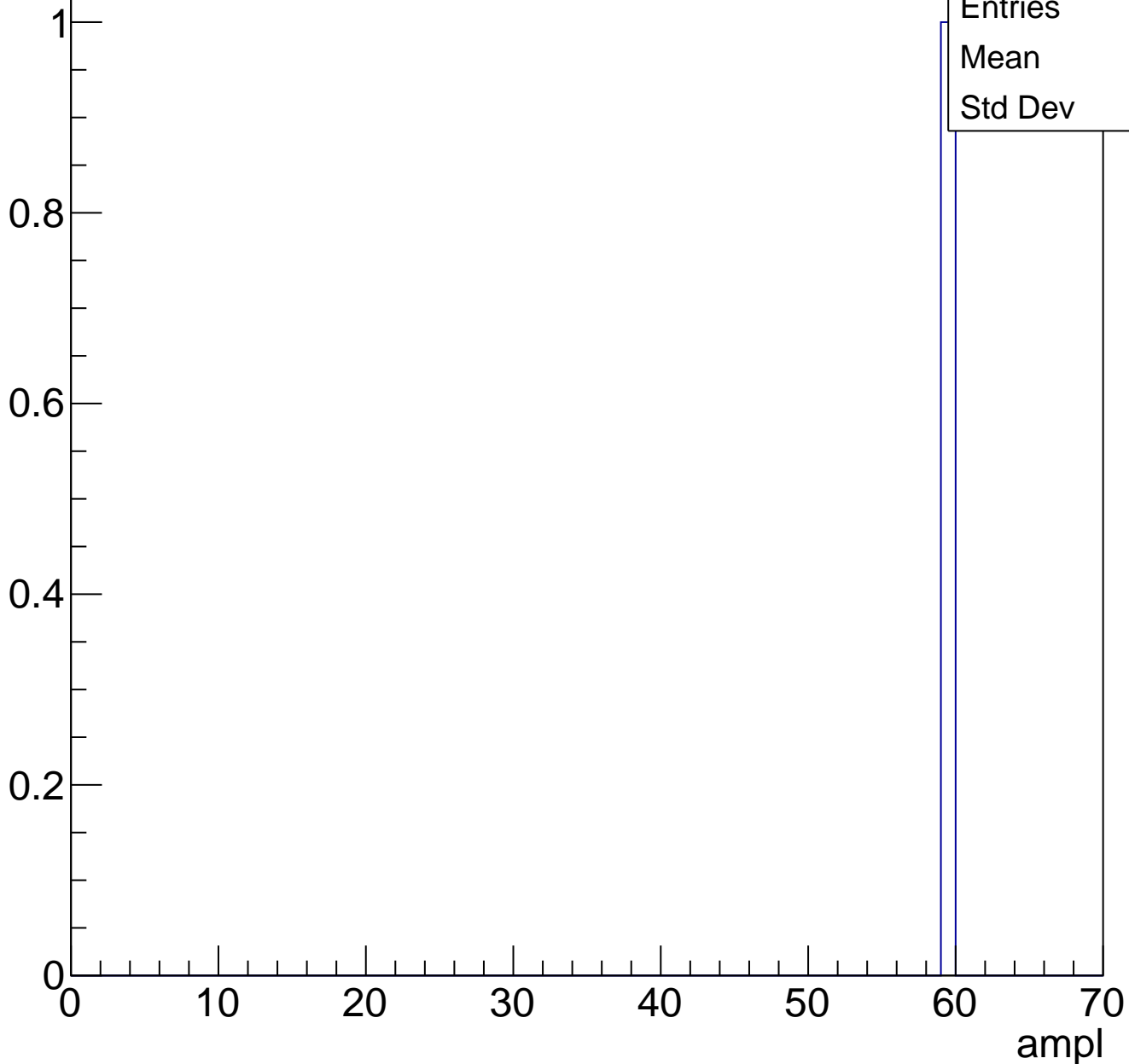
Entry



B1L103S, U23-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch125, adc0

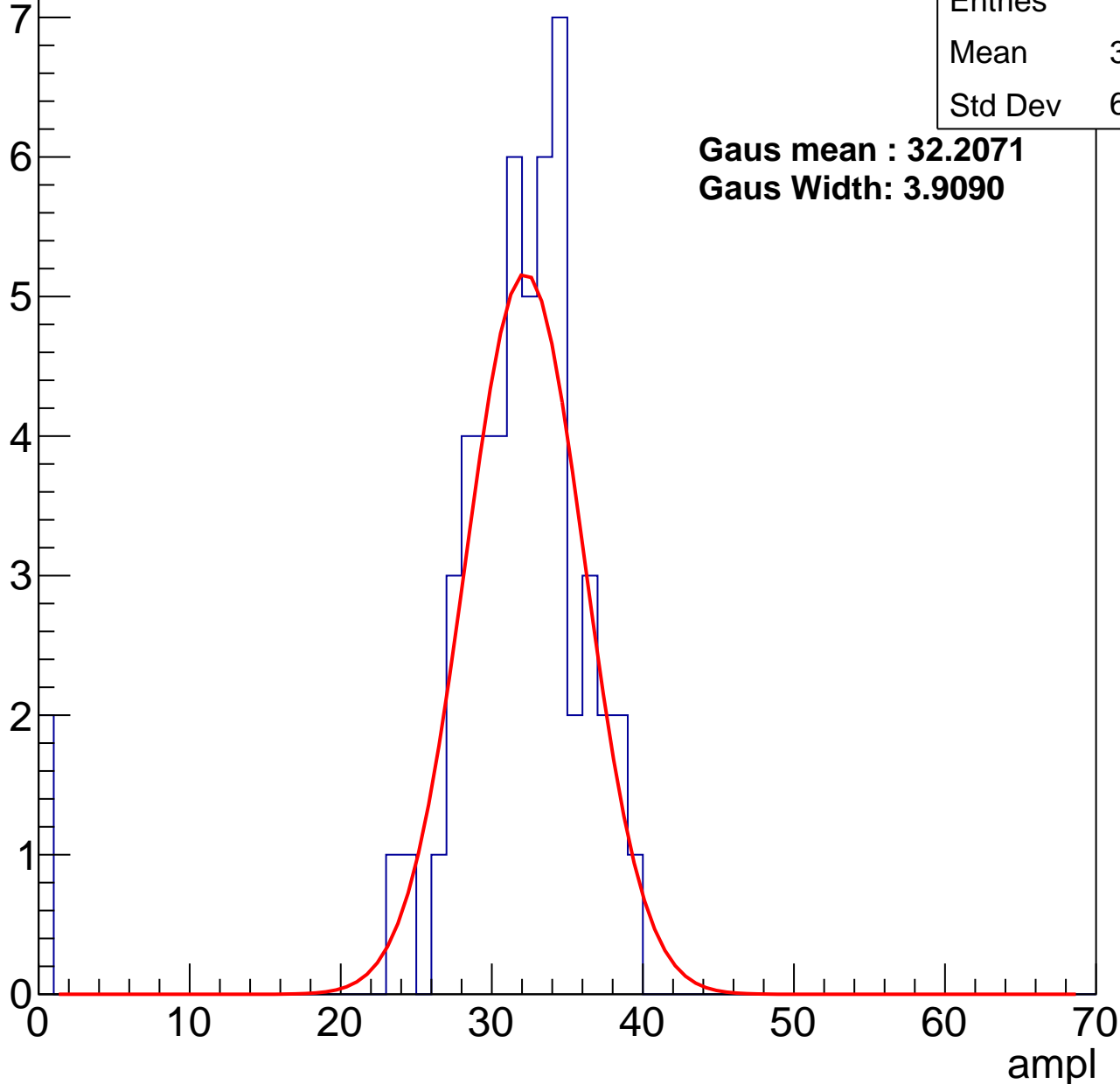
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	30.57
Std Dev	6.922

Gaus mean : 32.2071

Gaus Width: 3.9090



B1L103S, U23-ch125, adc1

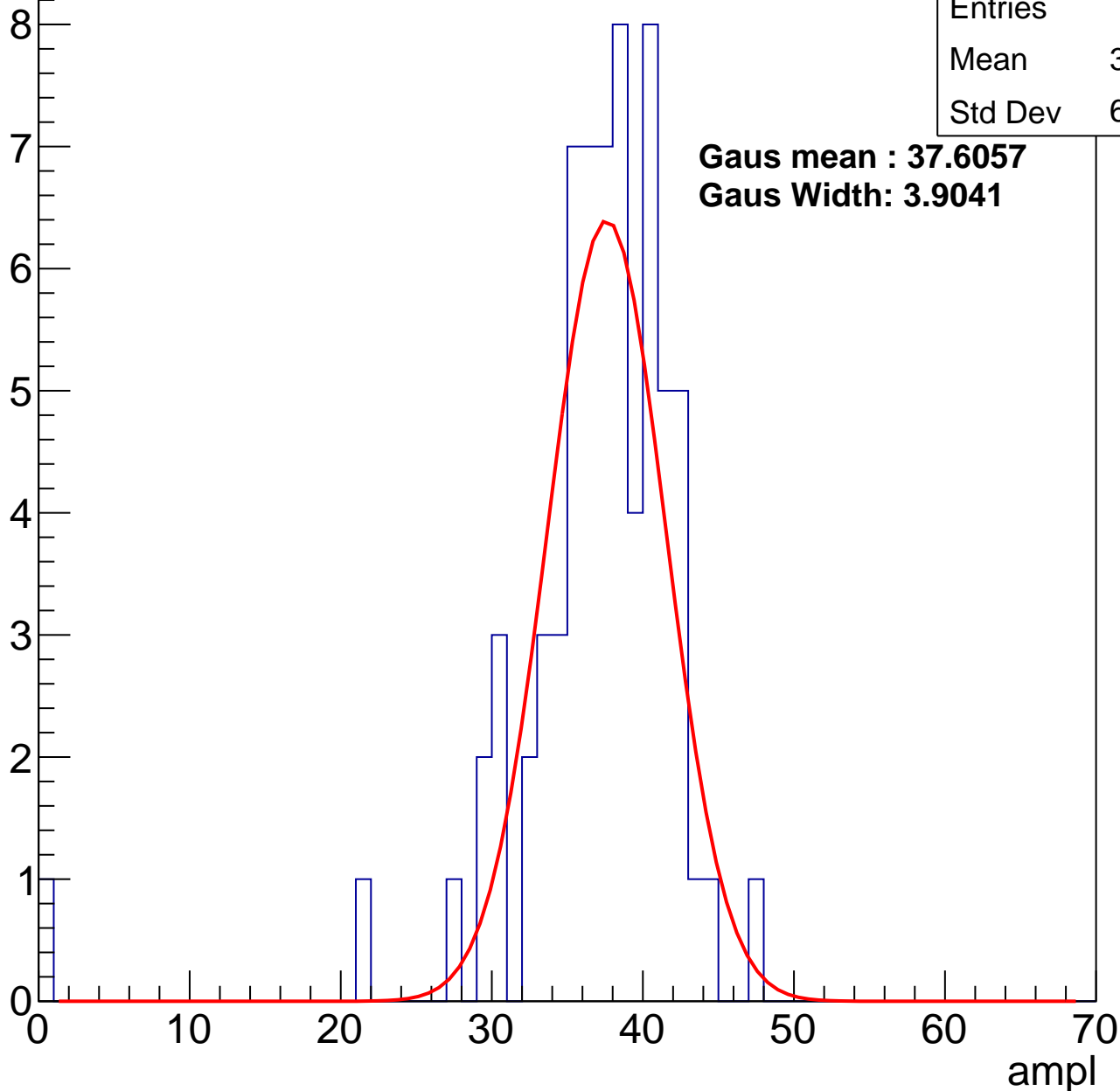
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.37
Std Dev	6.116

Gaus mean : 37.6057

Gaus Width: 3.9041



B1L103S, U23-ch125, adc2

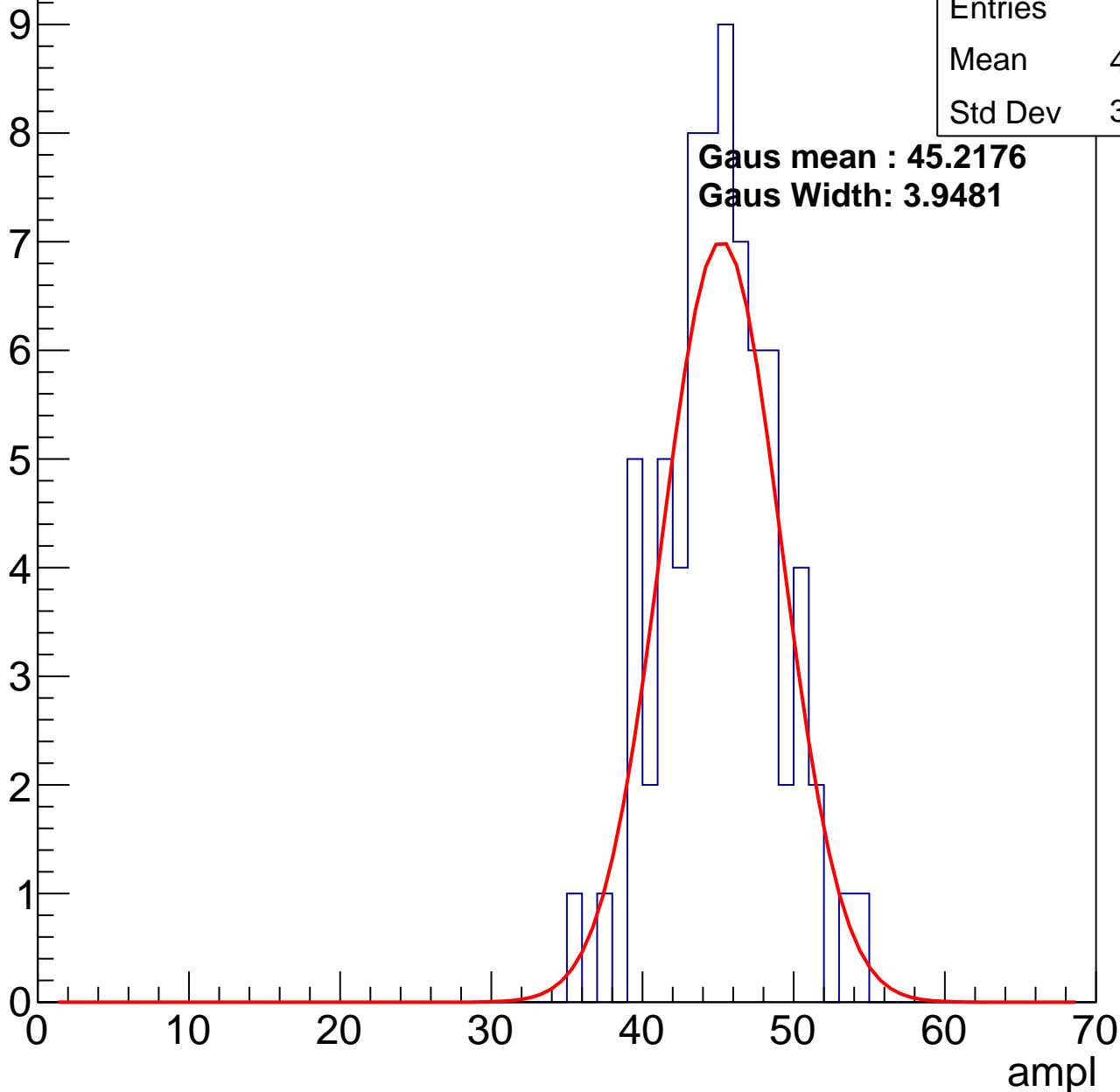
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	44.72
Std Dev	3.686

Gaus mean : 45.2176

Gaus Width: 3.9481

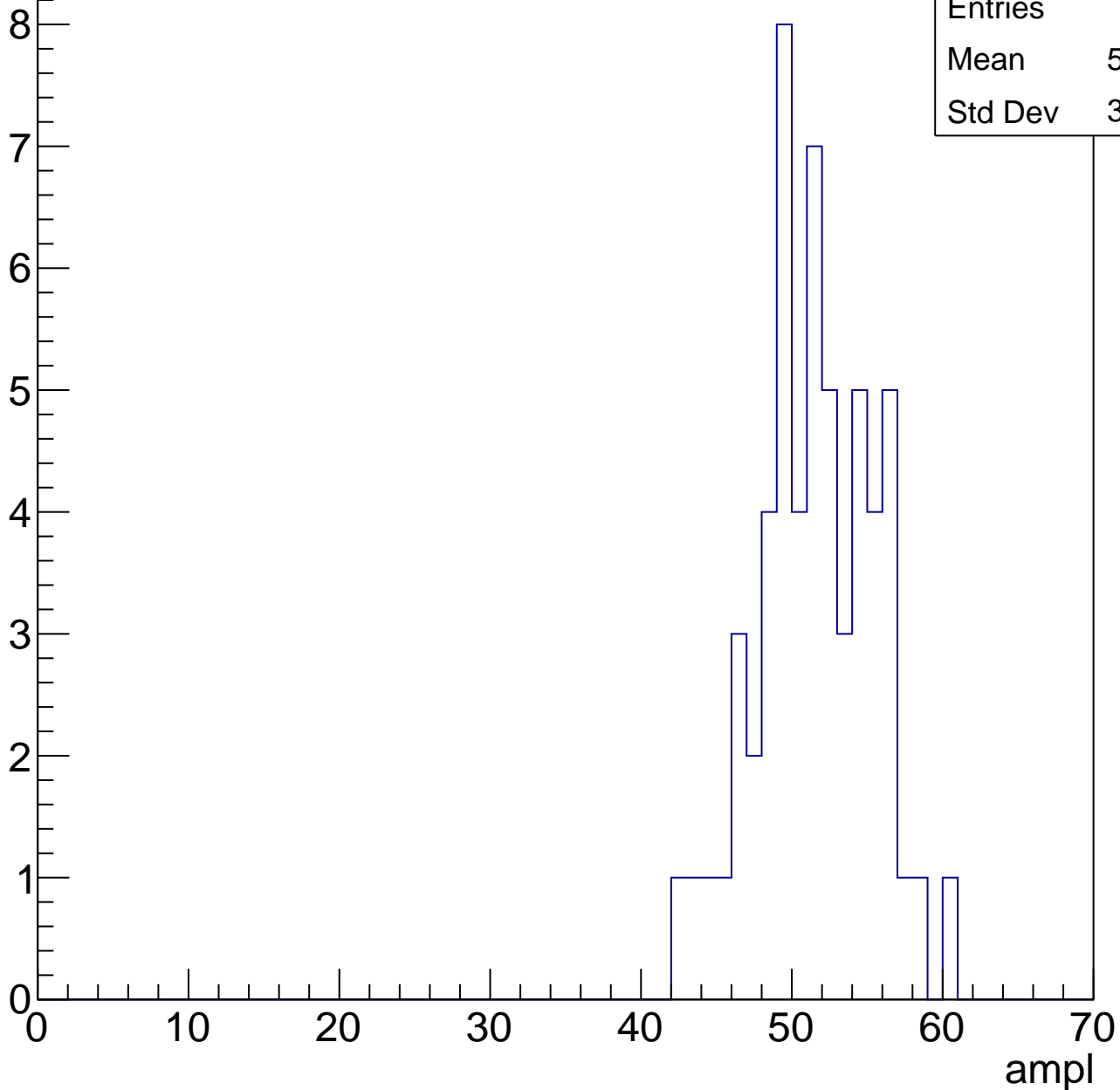


B1L103S, U23-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

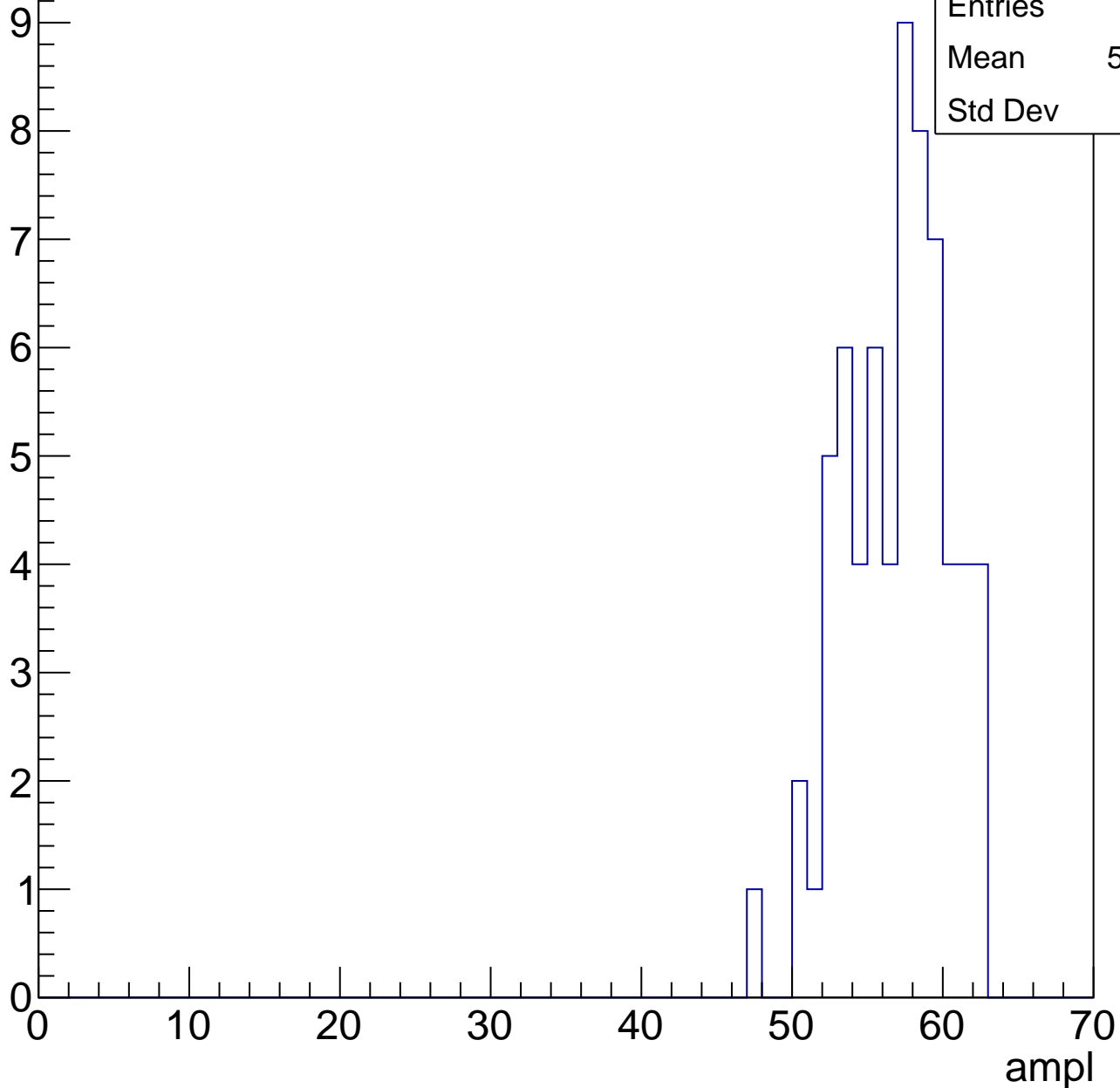
Entries	57
Mean	51.07
Std Dev	3.806



B1L103S, U23-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

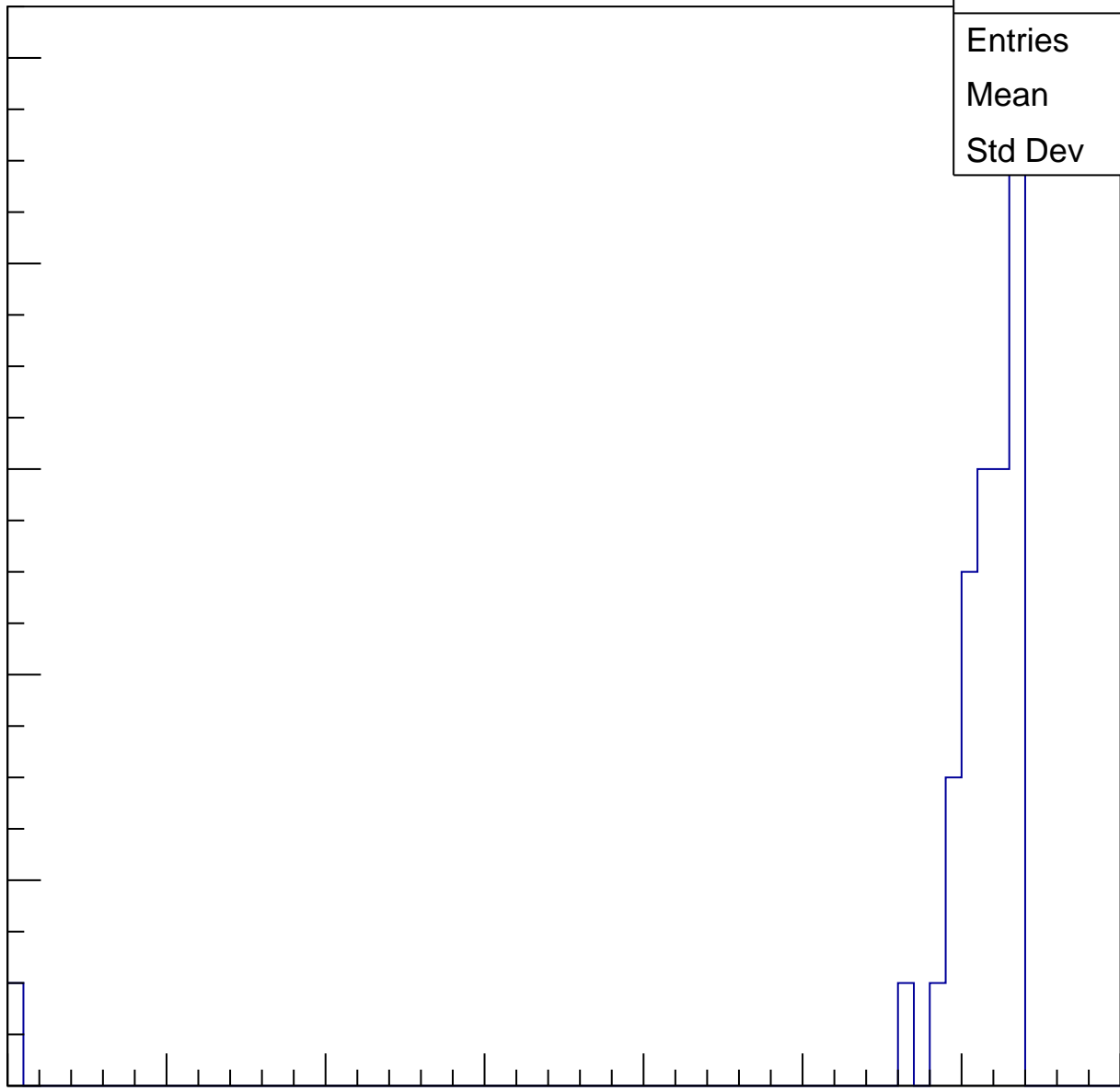
Entries	33
Mean	59.36
Std Dev	10.63

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U23-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U23-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U23-ch126, adc0

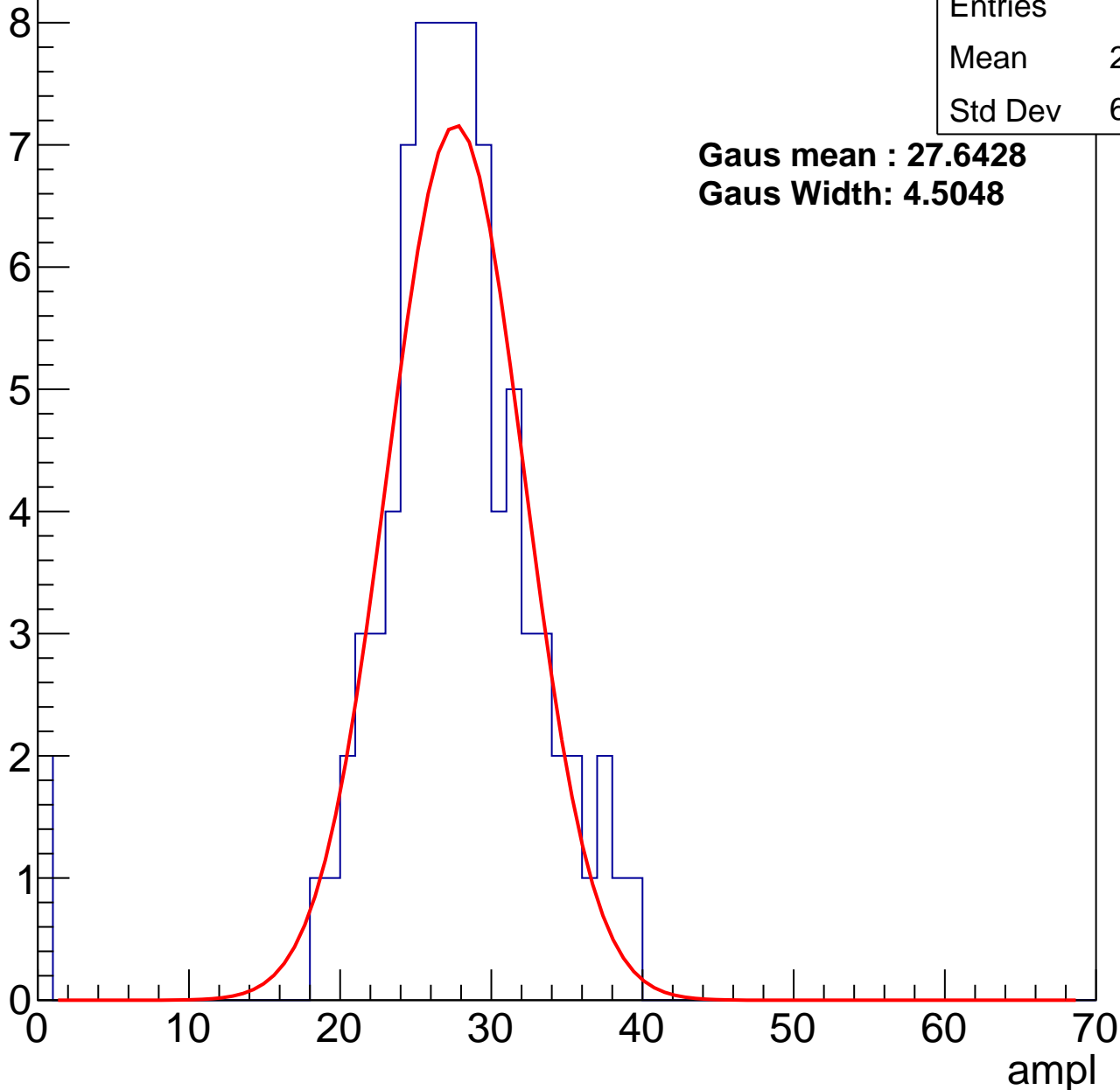
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	26.88
Std Dev	6.059

Gaus mean : 27.6428

Gaus Width: 4.5048



B1L103S, U23-ch126, adc1

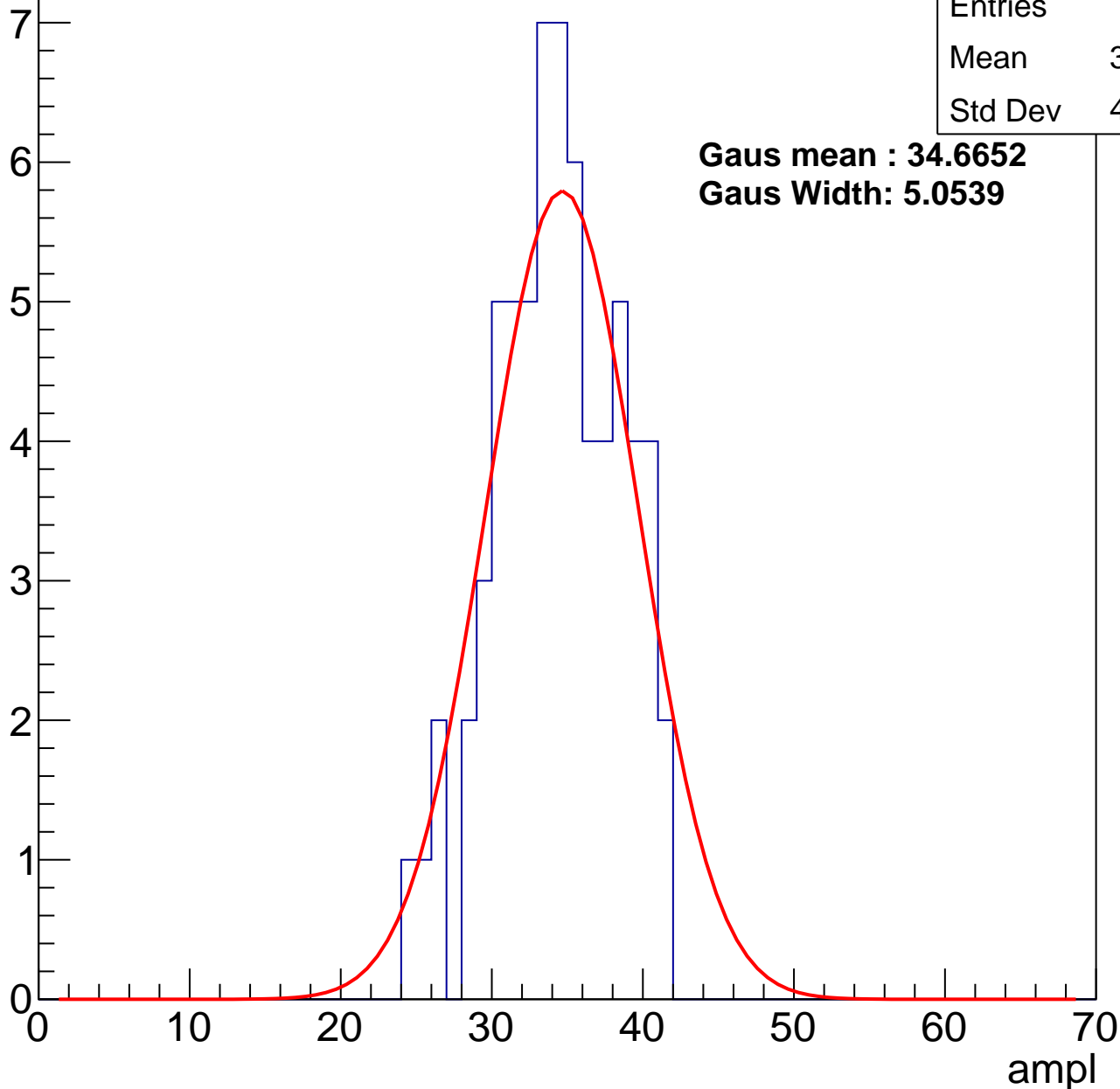
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	33.85
Std Dev	4.027

Gaus mean : 34.6652

Gaus Width: 5.0539



B1L103S, U23-ch126, adc2

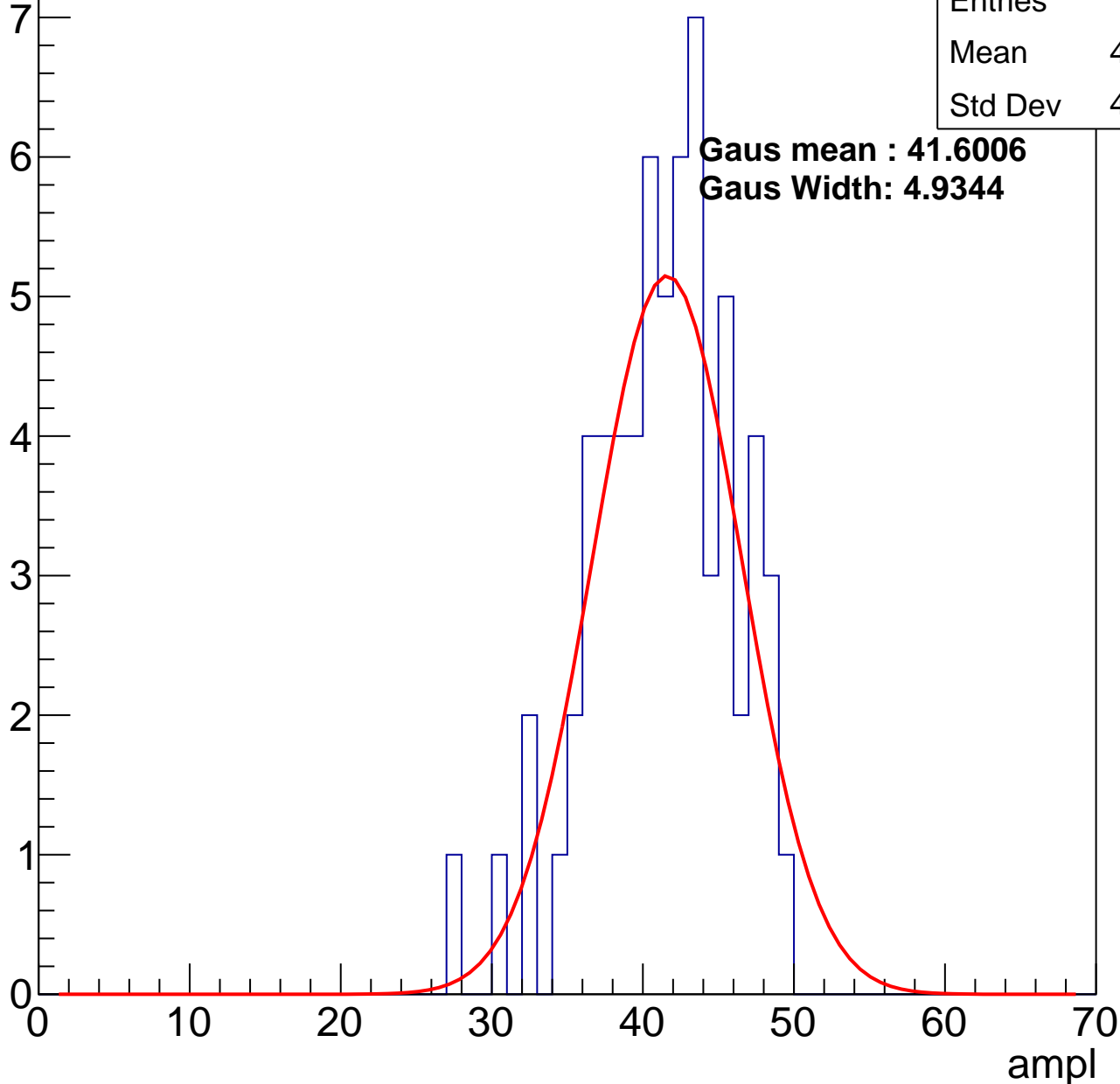
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	40.82
Std Dev	4.584

Gaus mean : 41.6006

Gaus Width: 4.9344

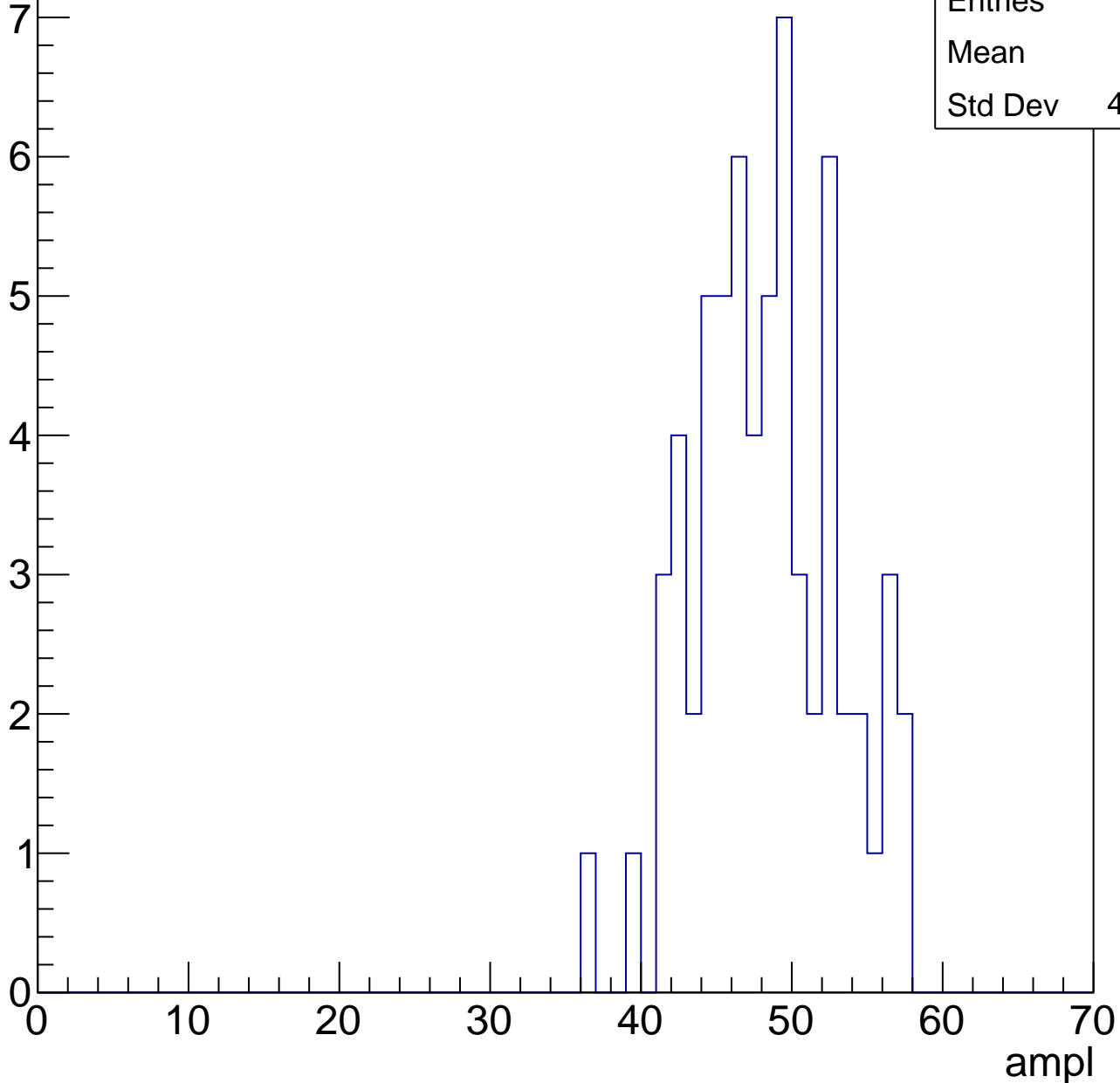


B1L103S, U23-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	47.8
Std Dev	4.634

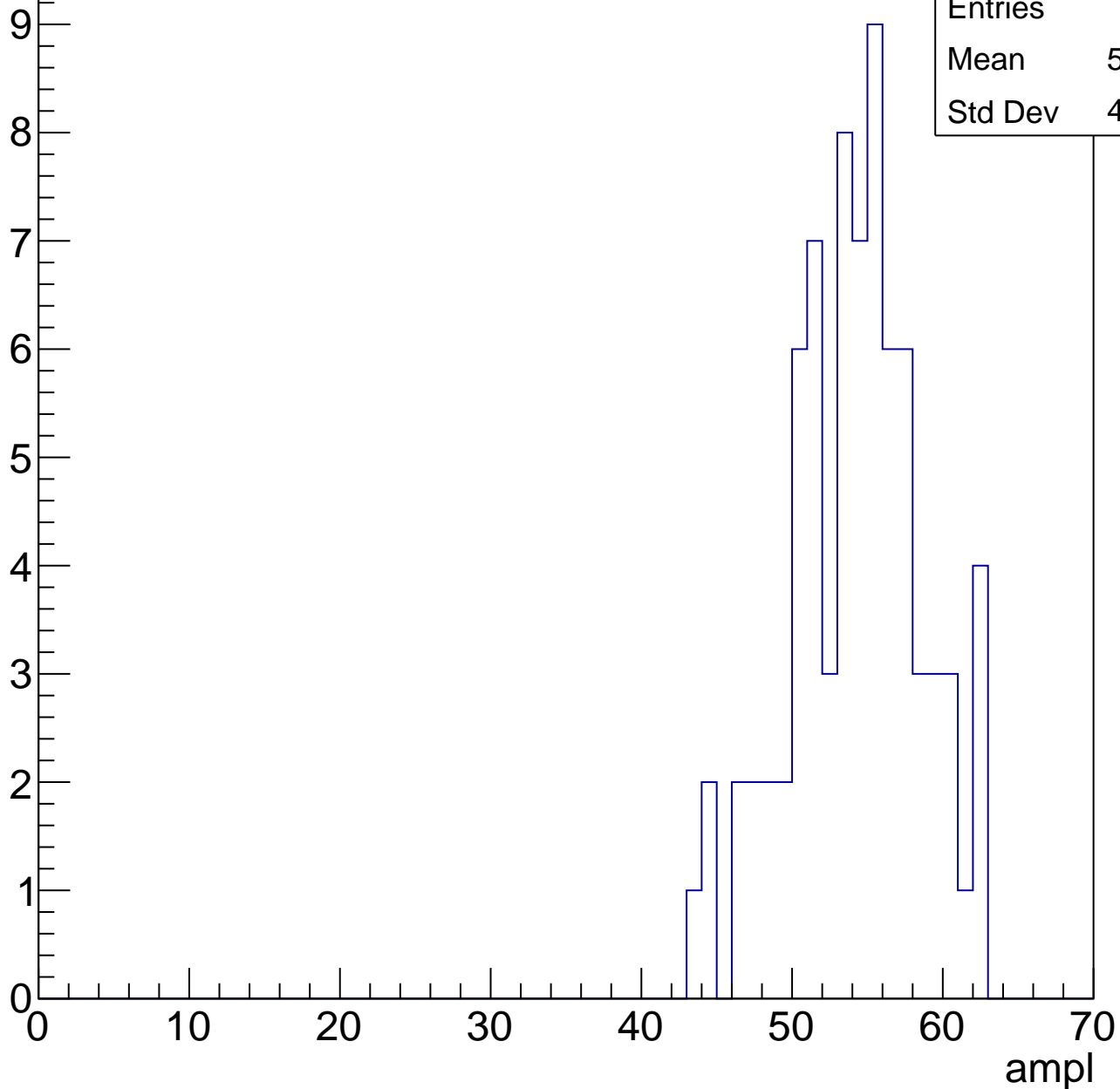


B1L103S, U23-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	53.75
Std Dev	4.364

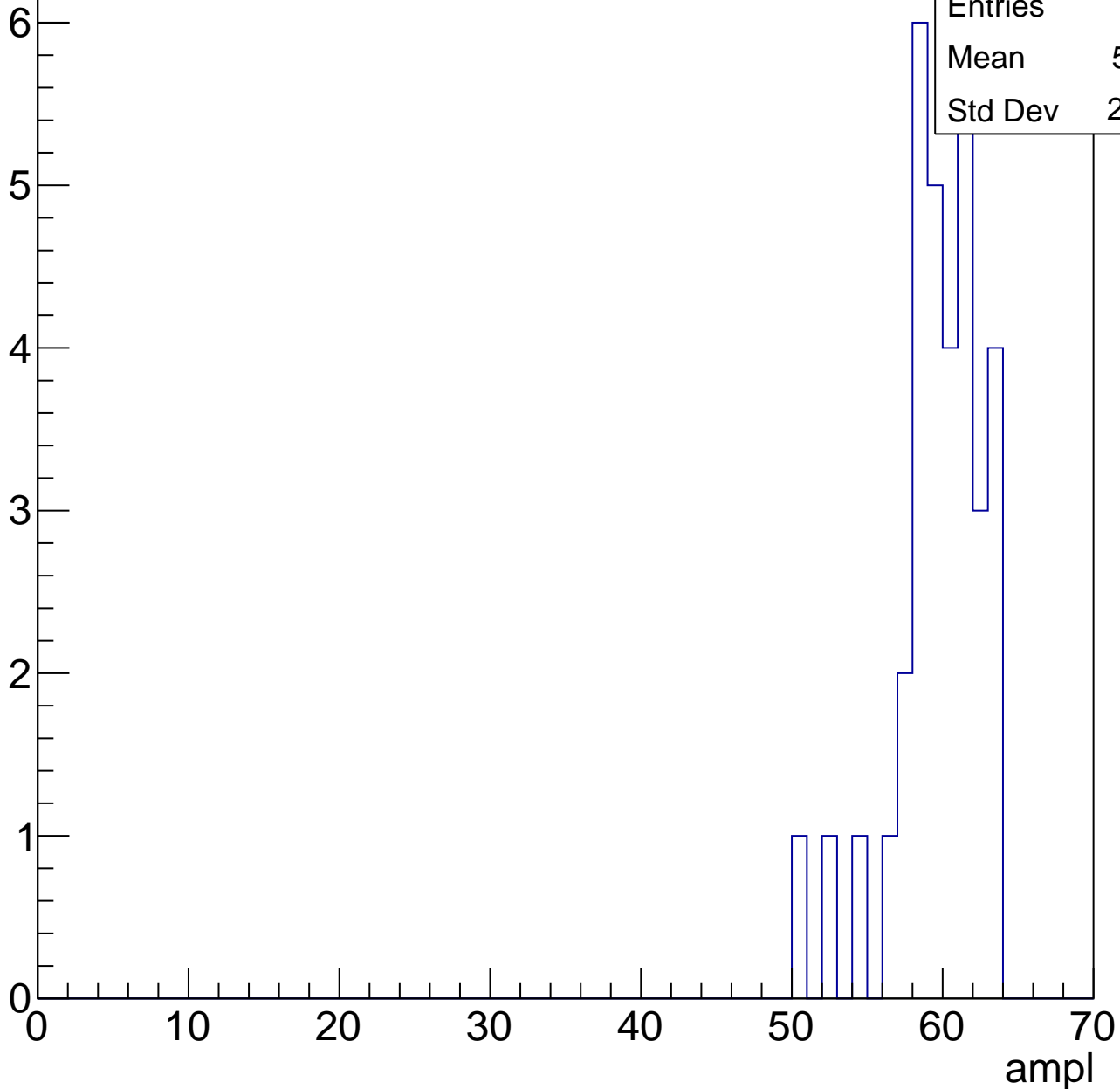


B1L103S, U23-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	59.21
Std Dev	2.948

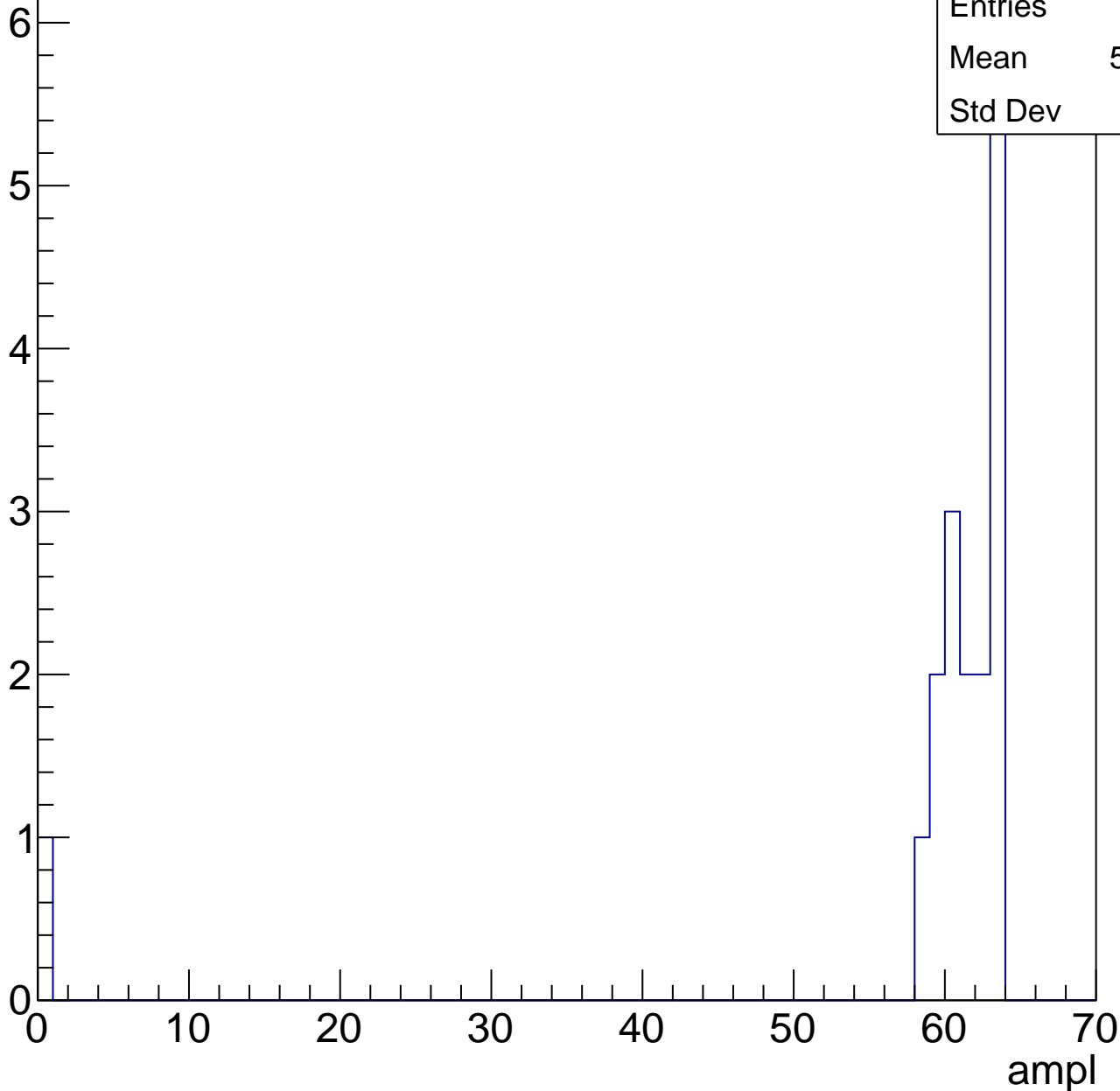


B1L103S, U23-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	57.65
Std Dev	14.5



B1L103S, U23-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U23-ch127, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	94
Mean	28.5
Std Dev	6.023

Gaus mean : 29.0245

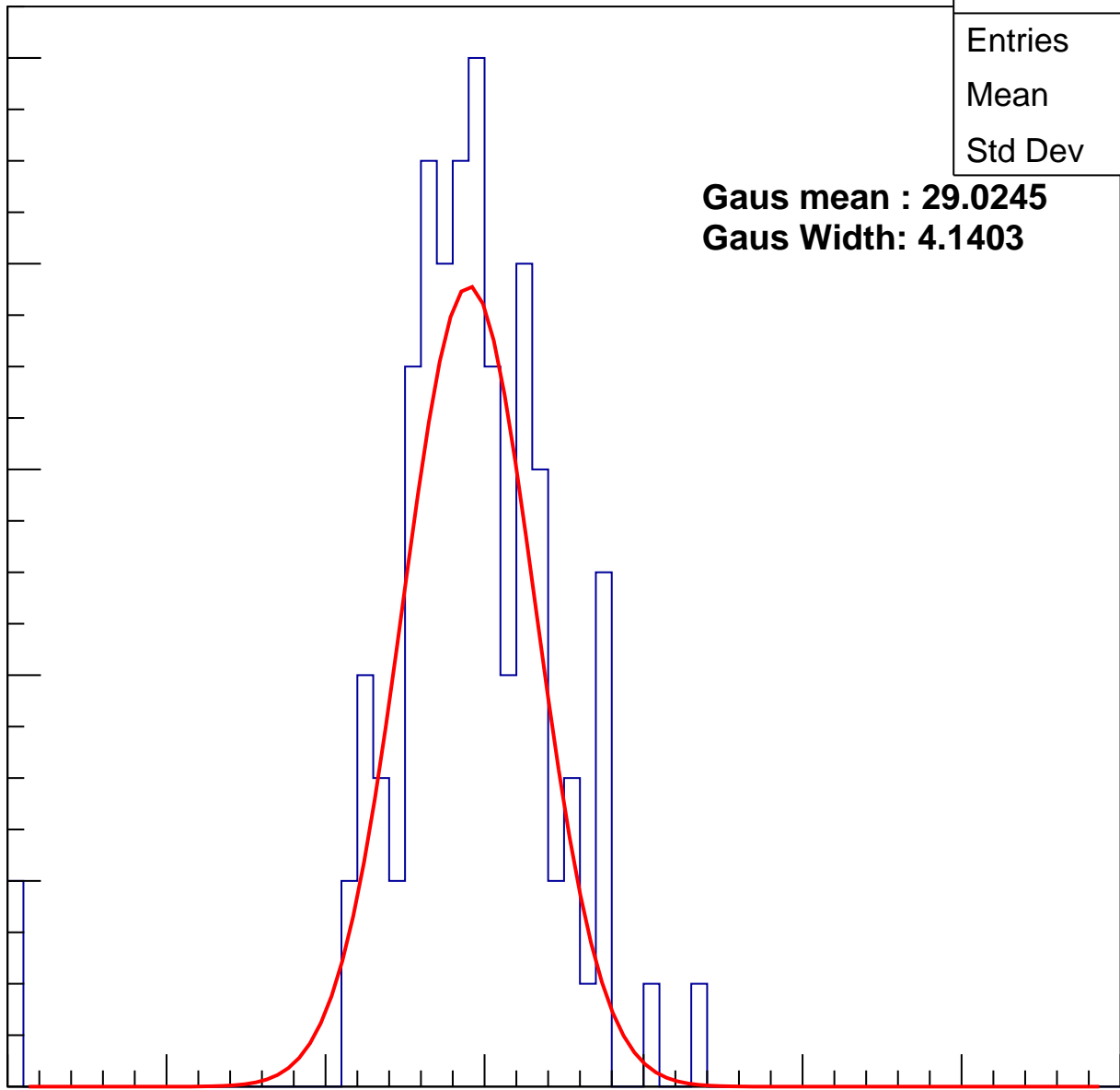
Gaus Width: 4.1403

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch127, adc1

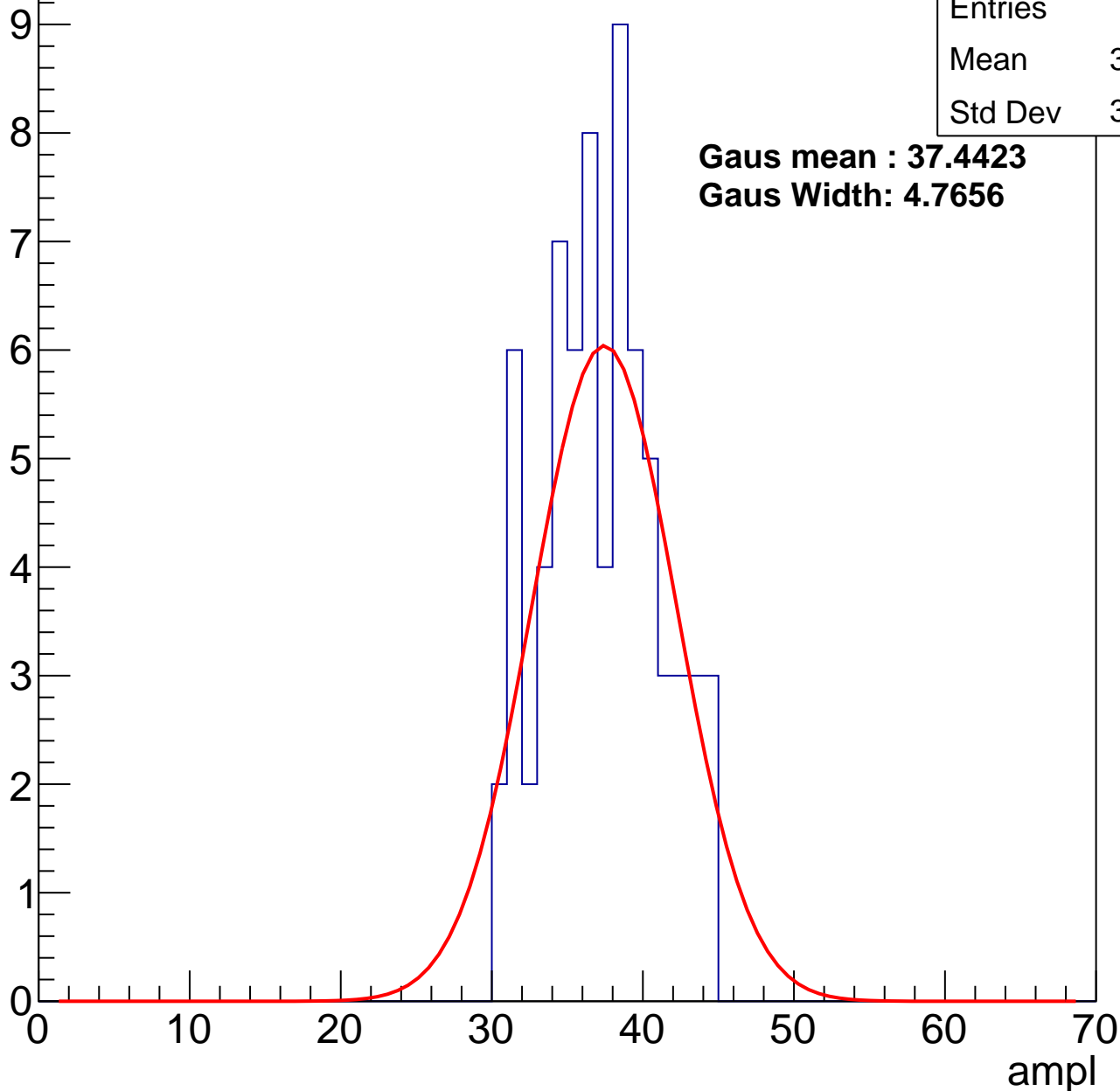
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.79
Std Dev	3.707

Gaus mean : 37.4423

Gaus Width: 4.7656



B1L103S, U23-ch127, adc2

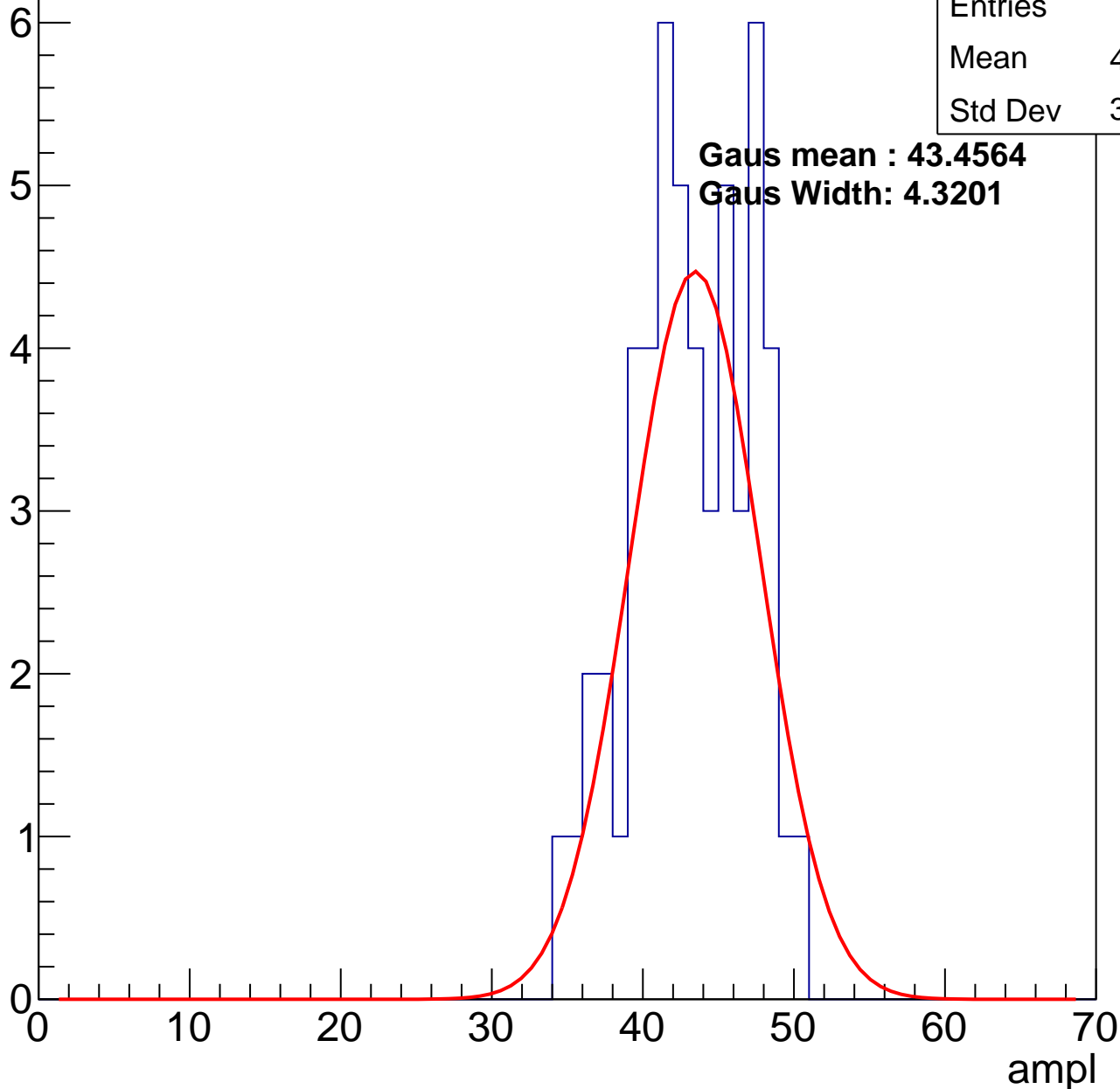
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.74
Std Dev	3.876

Gaus mean : 43.4564

Gaus Width: 4.3201

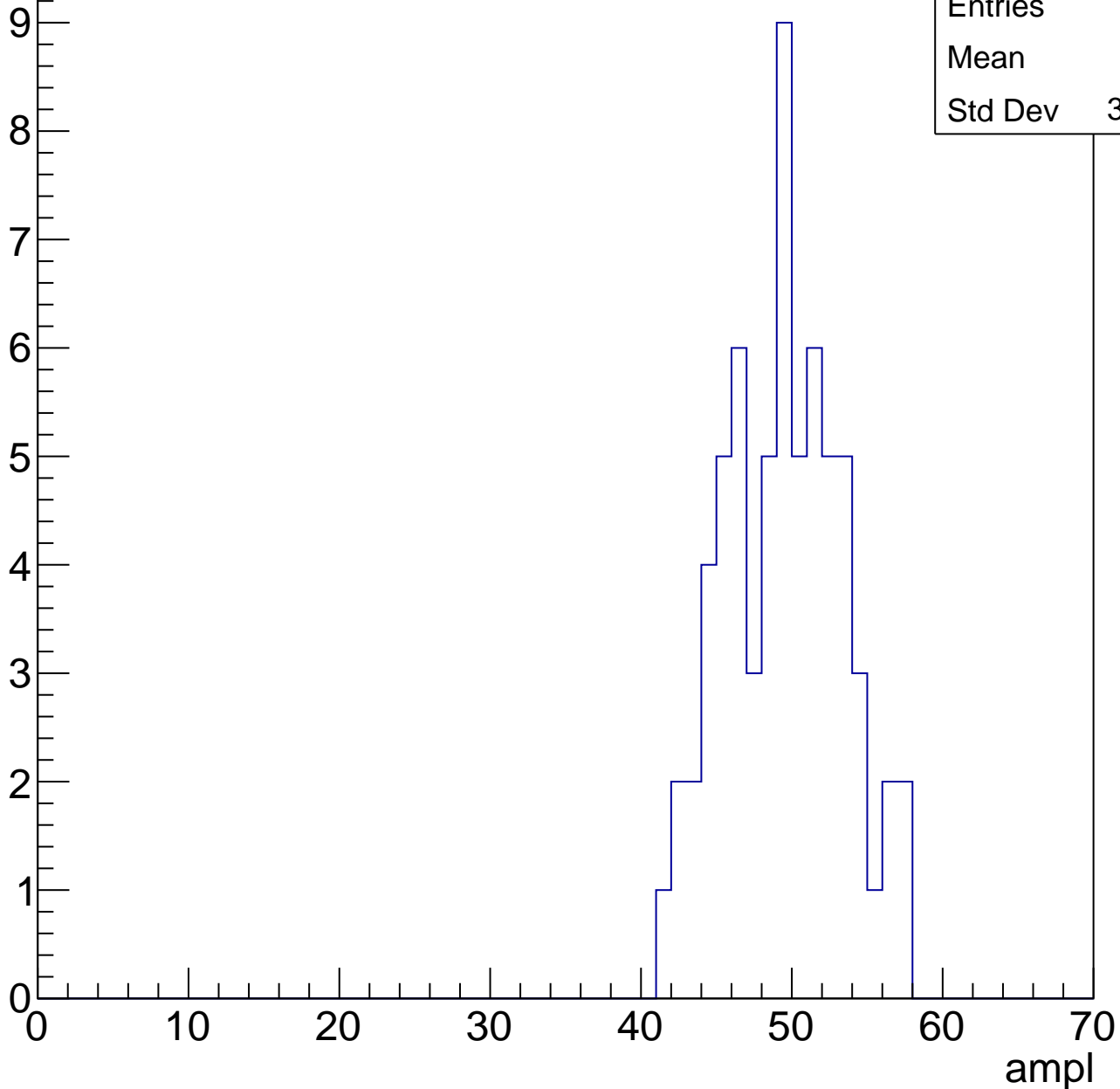


B1L103S, U23-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49
Std Dev	3.846

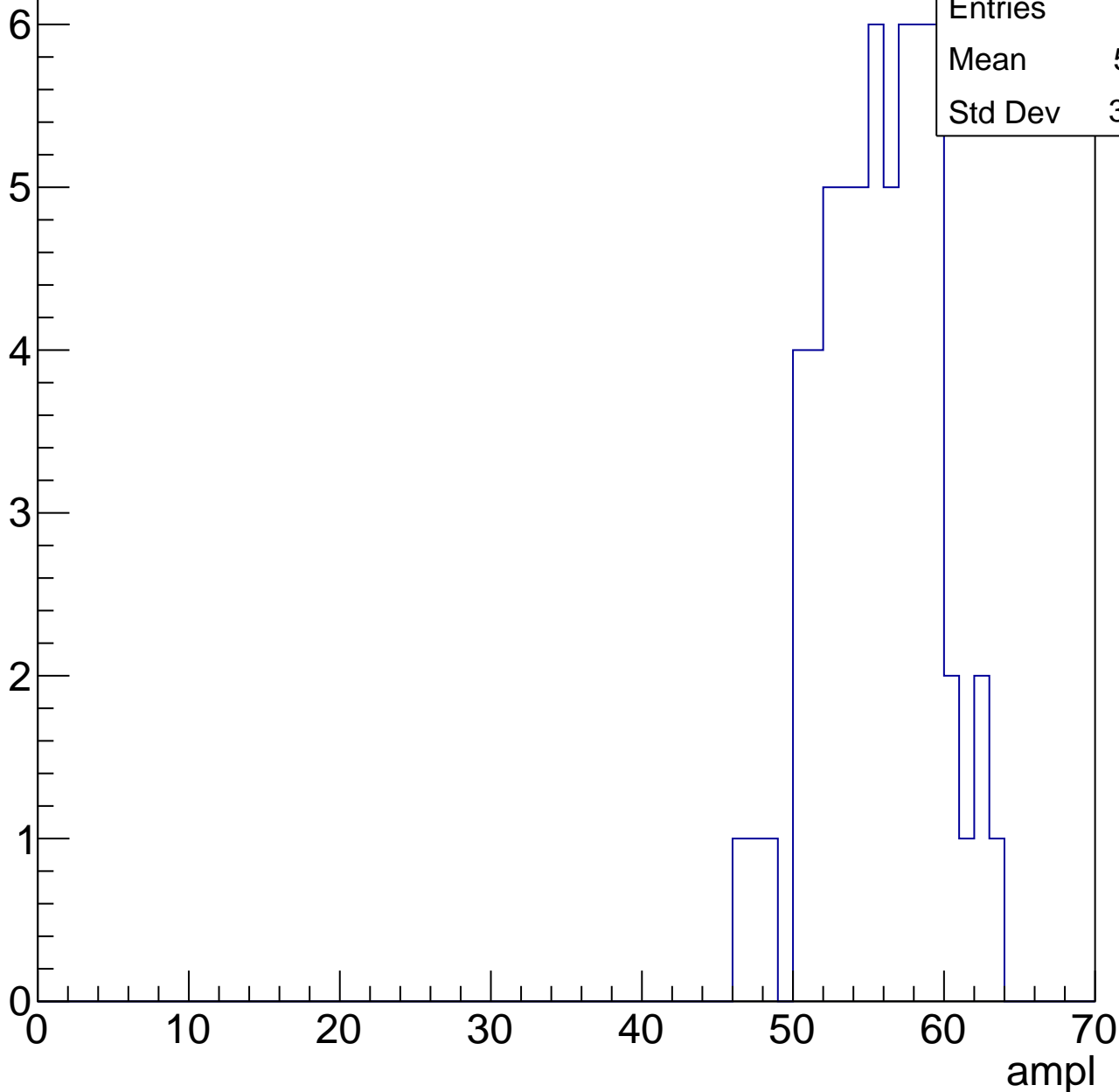


B1L103S, U23-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.11
Std Dev	3.742

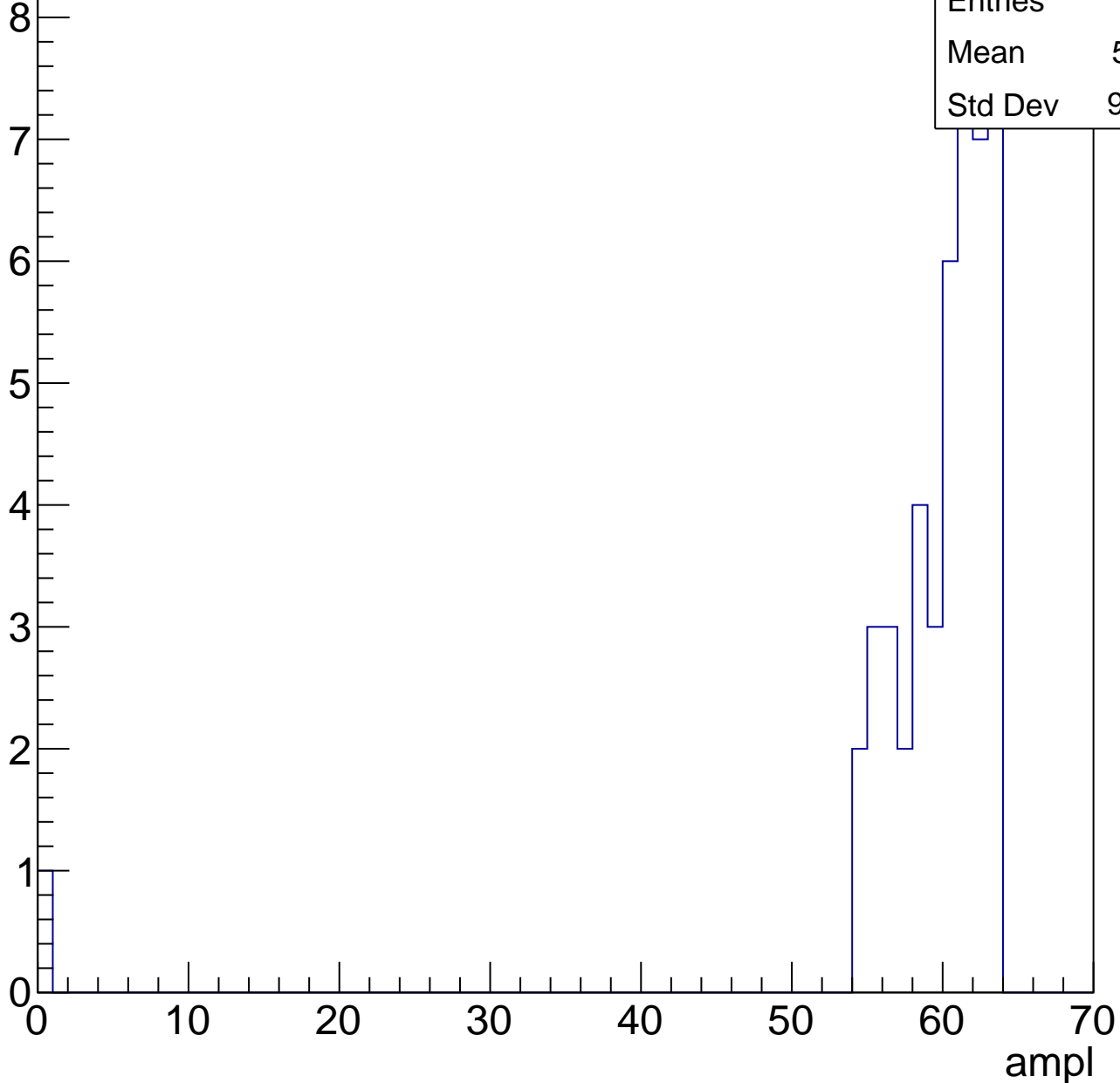


B1L103S, U23-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	58.51
Std Dev	9.034



B1L103S, U23-ch127, adc6

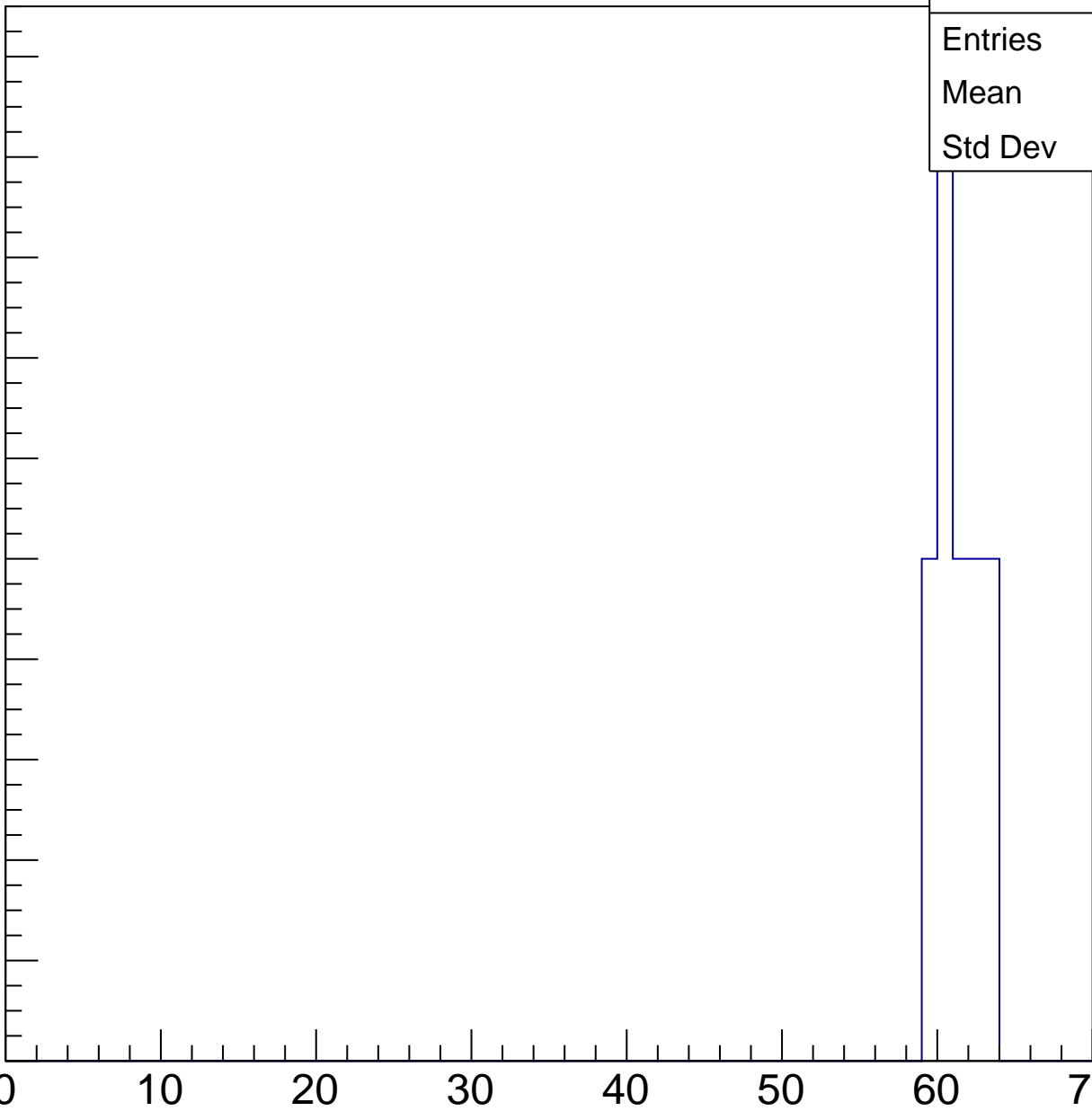
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.83
Std Dev	1.344

ampl

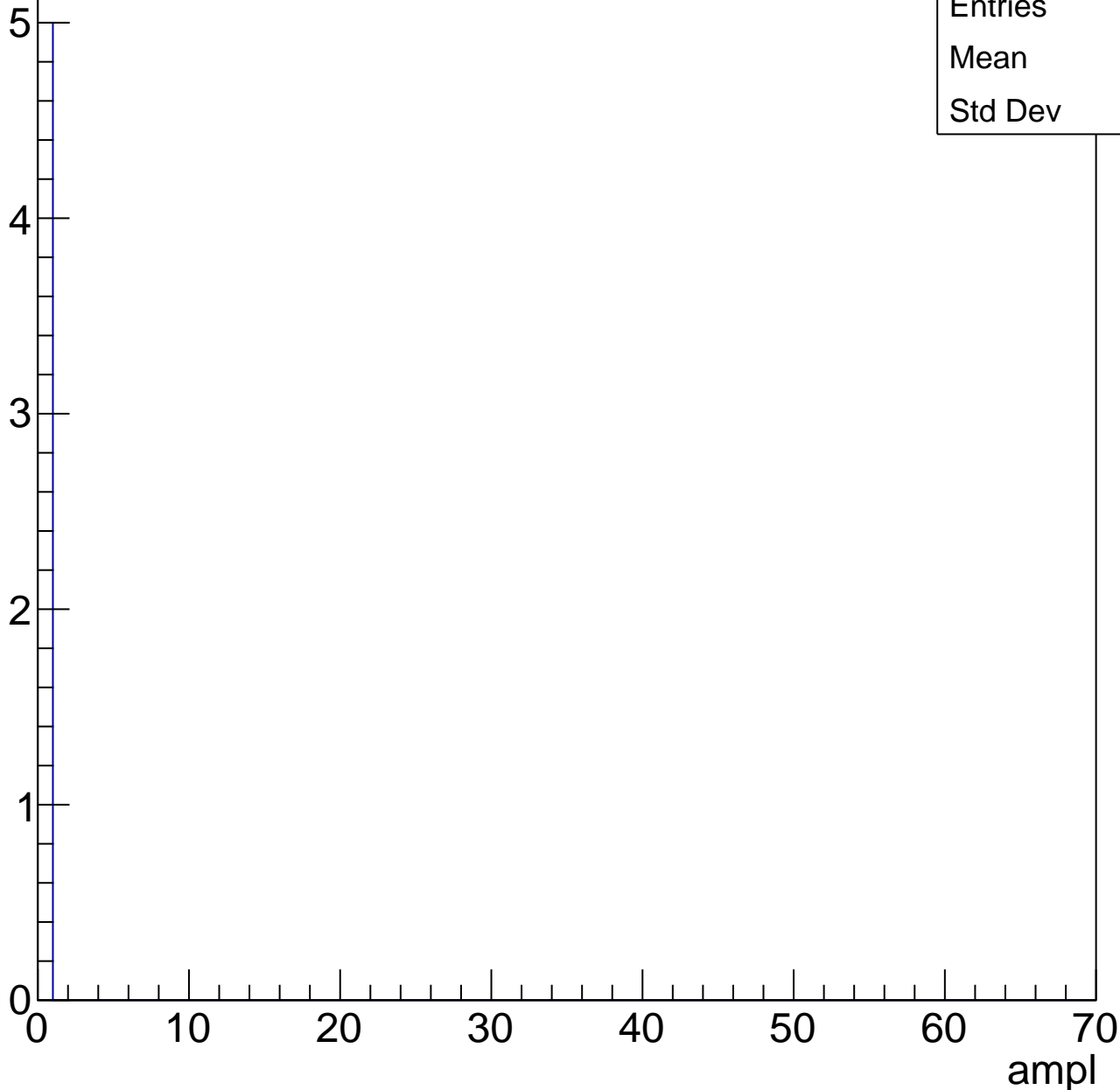


B1L103S, U23-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	5
Mean	0
Std Dev	0



B1L103S, U23-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	5
Mean	0
Std Dev	0

