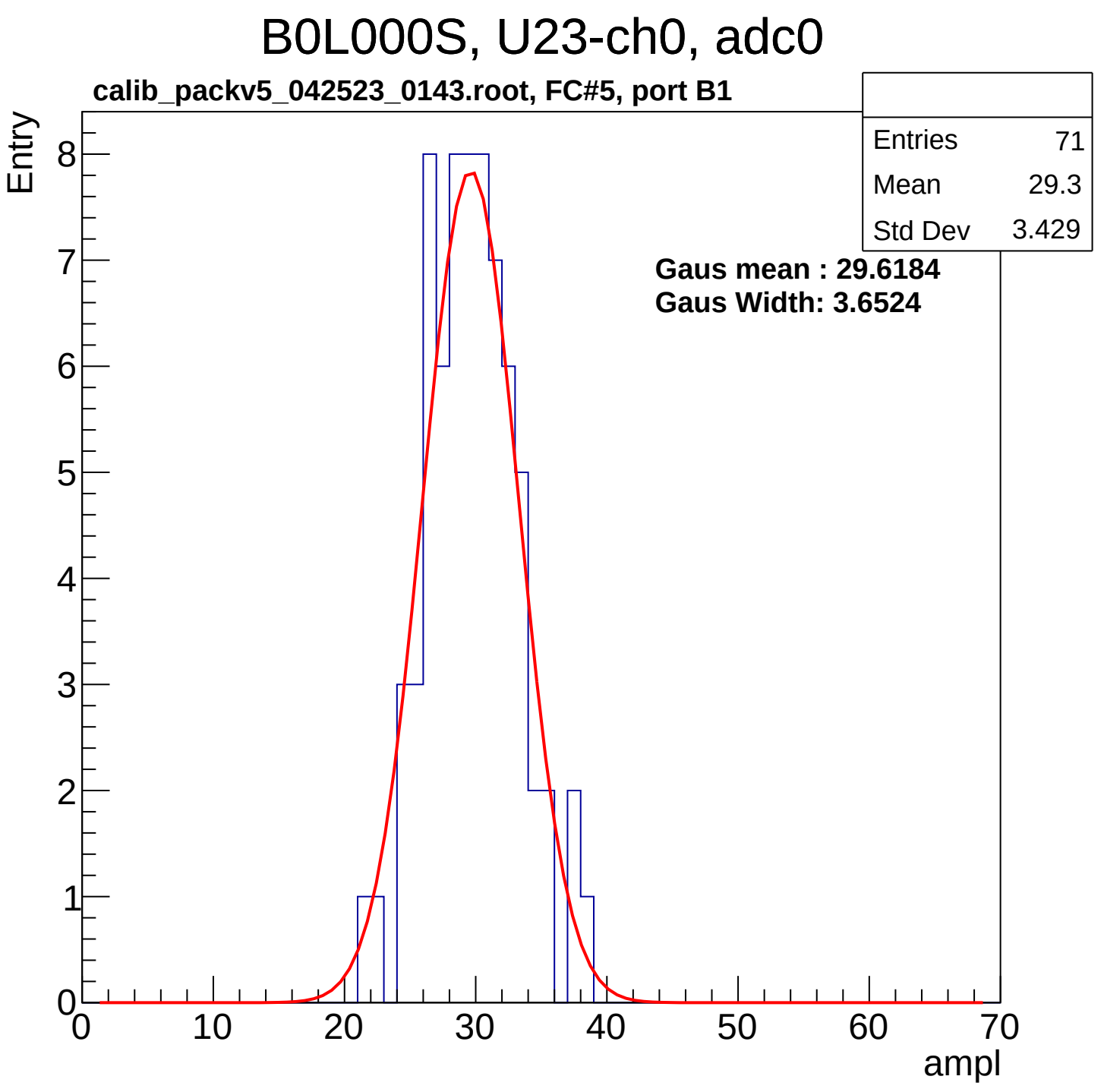
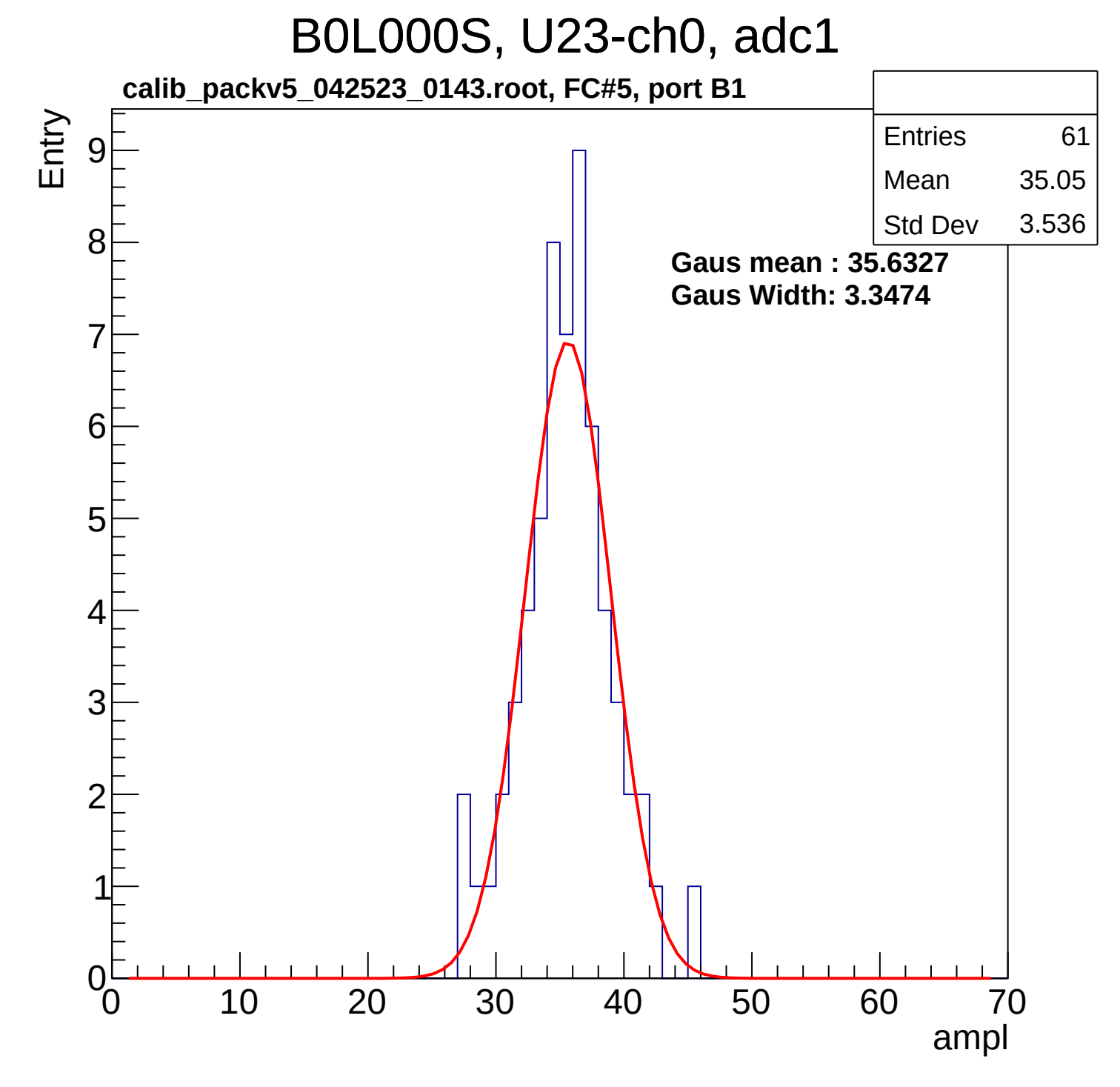
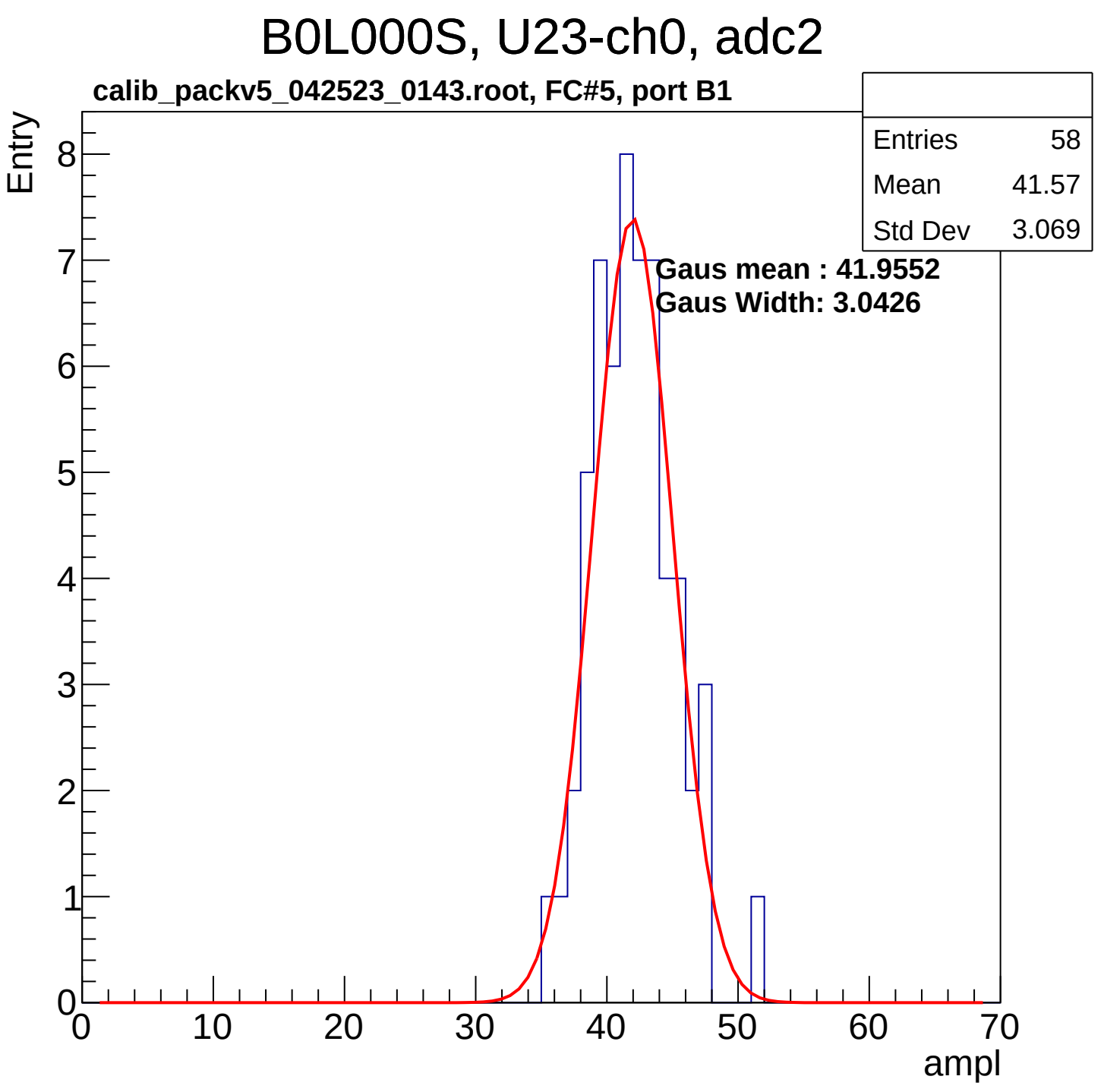
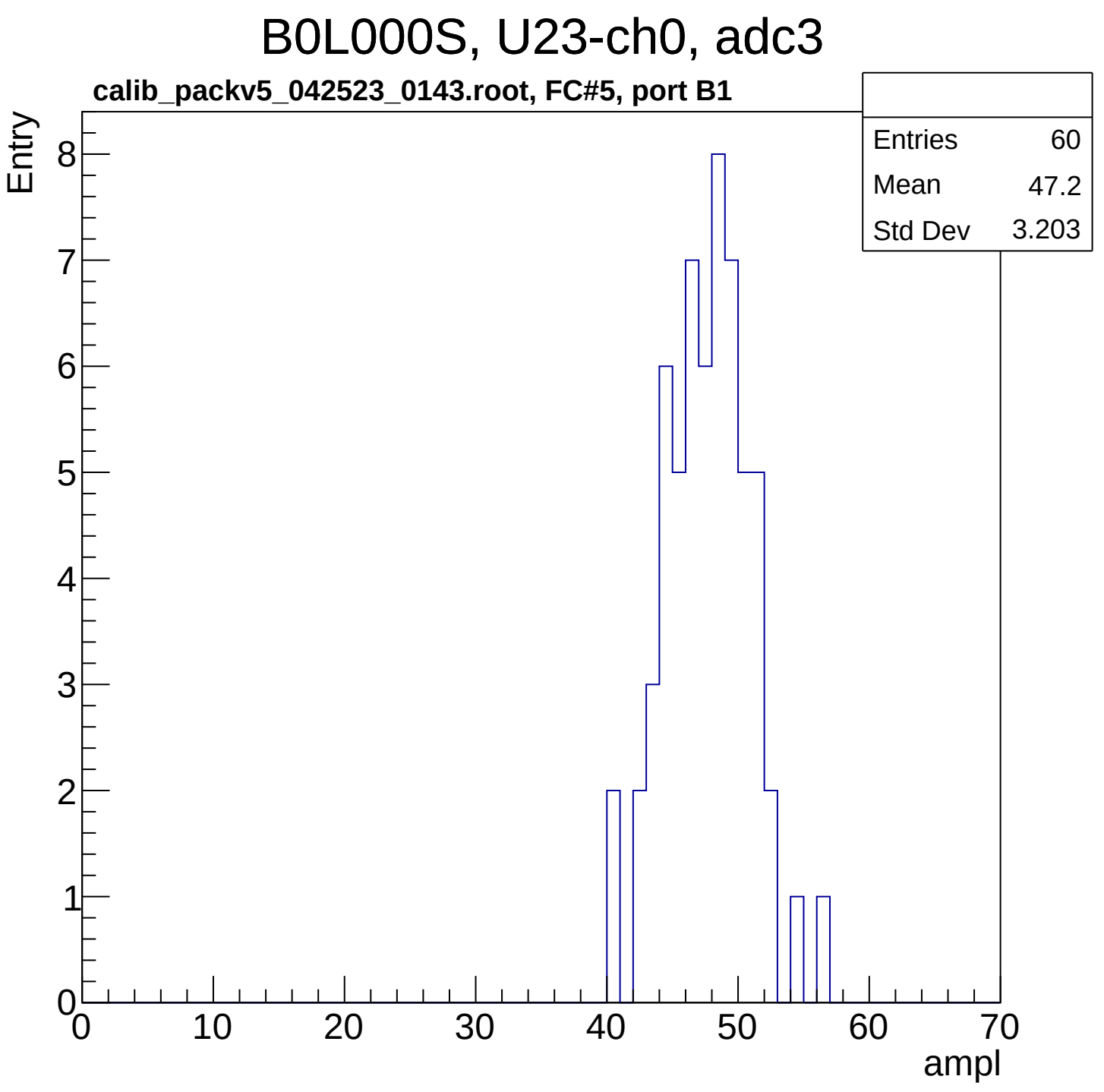
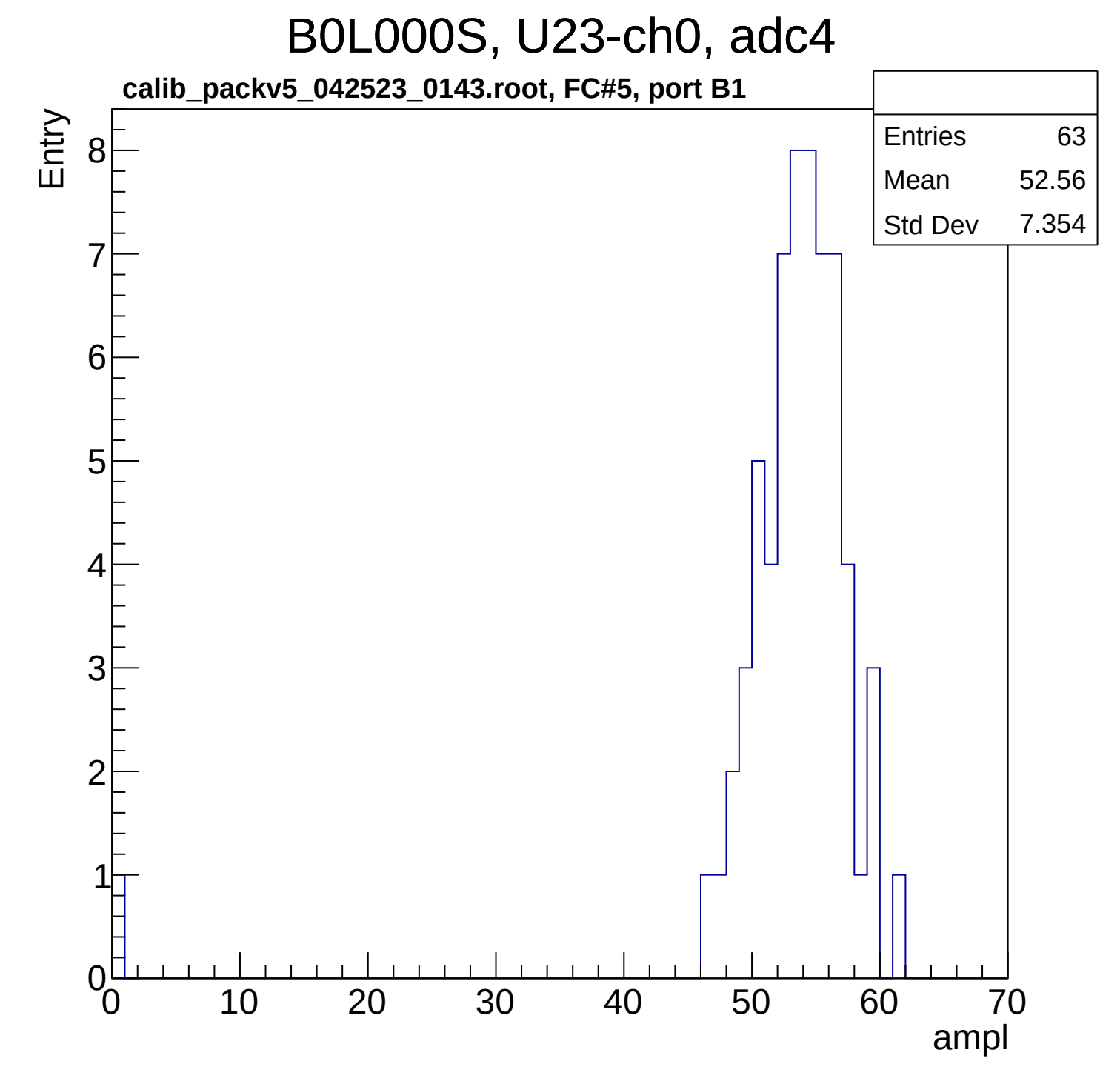
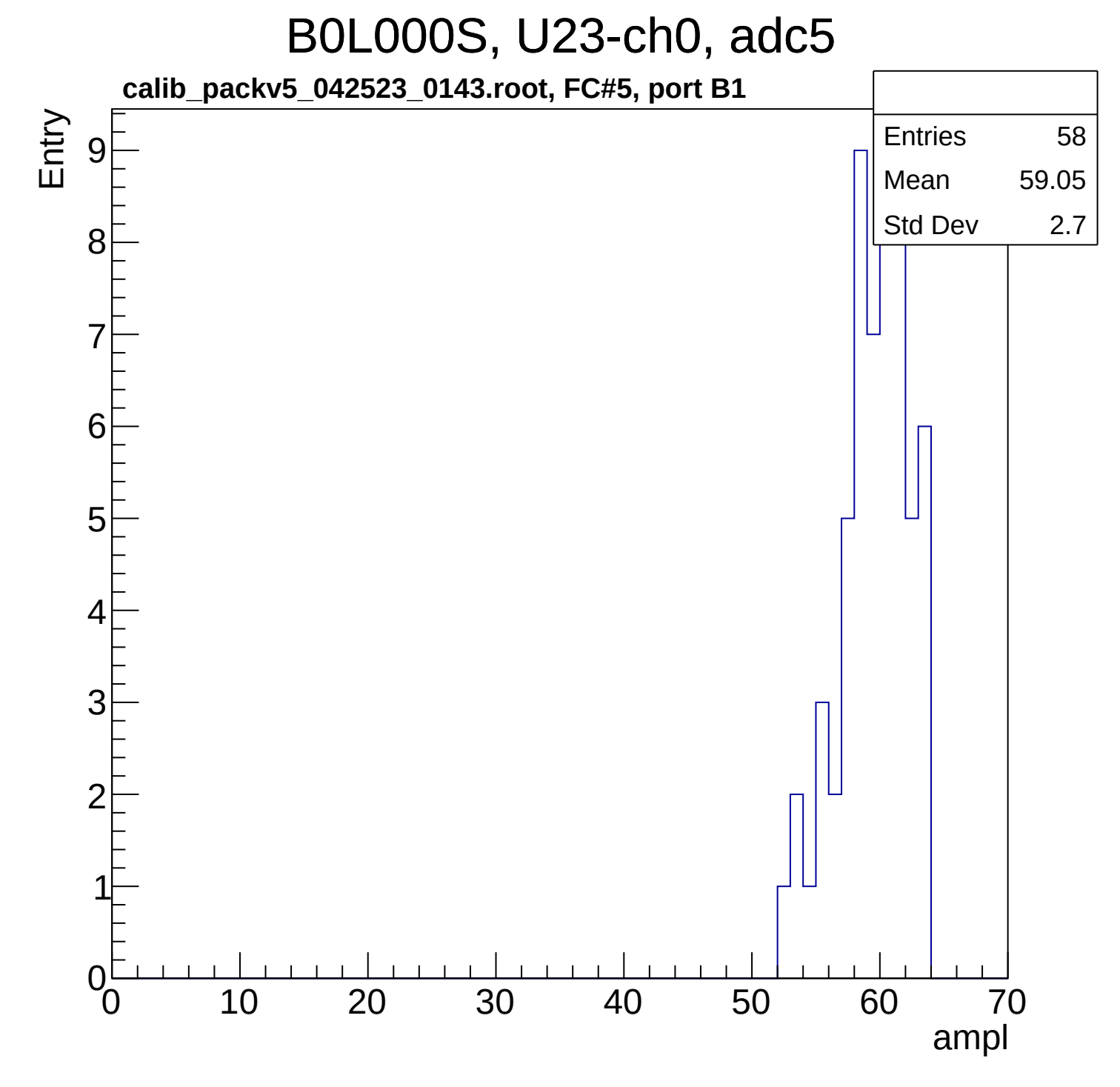

B0L000S, U23-ch0, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.3
Std Dev 3.429
Gaus mean : 29.6184
Gaus Width: 3.6524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

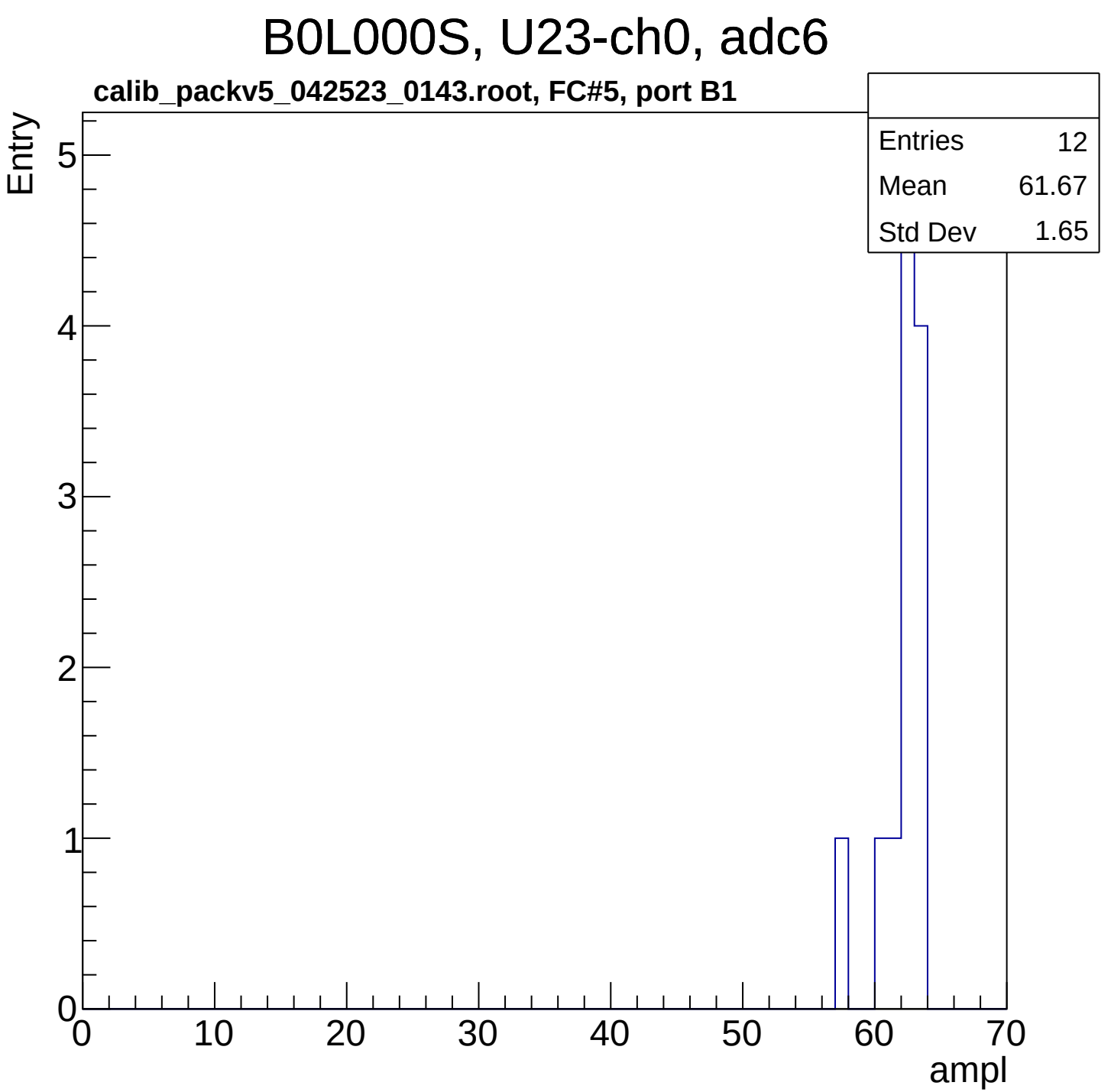

B0L000S, U23-ch0, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.05
Std Dev 3.536
Gaus mean : 35.6327
Gaus Width: 3.3474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

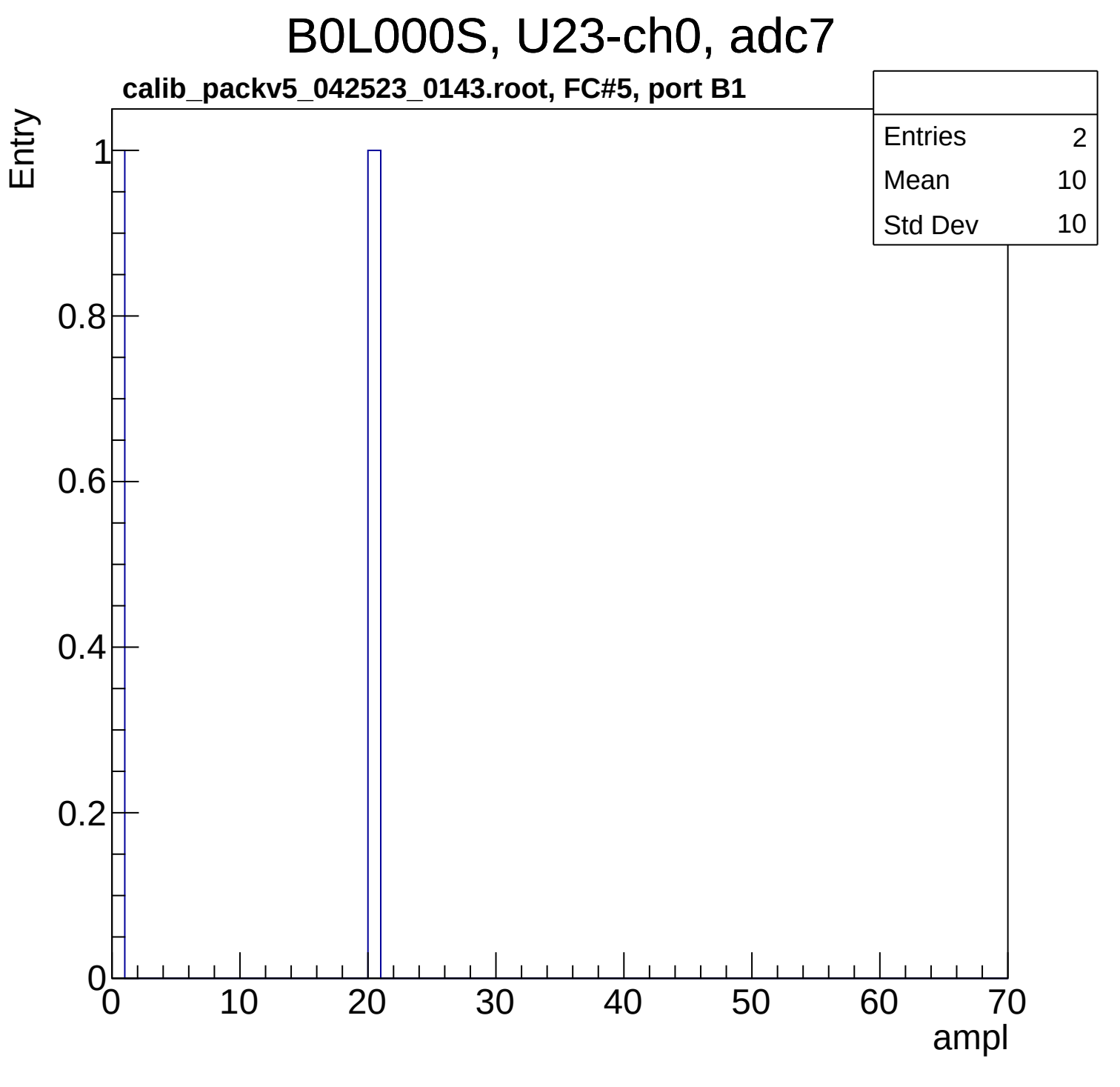

B0L000S, U23-ch0, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 41.57
Std Dev 3.069
Gaus mean : 41.9552
Gaus Width: 3.0426
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

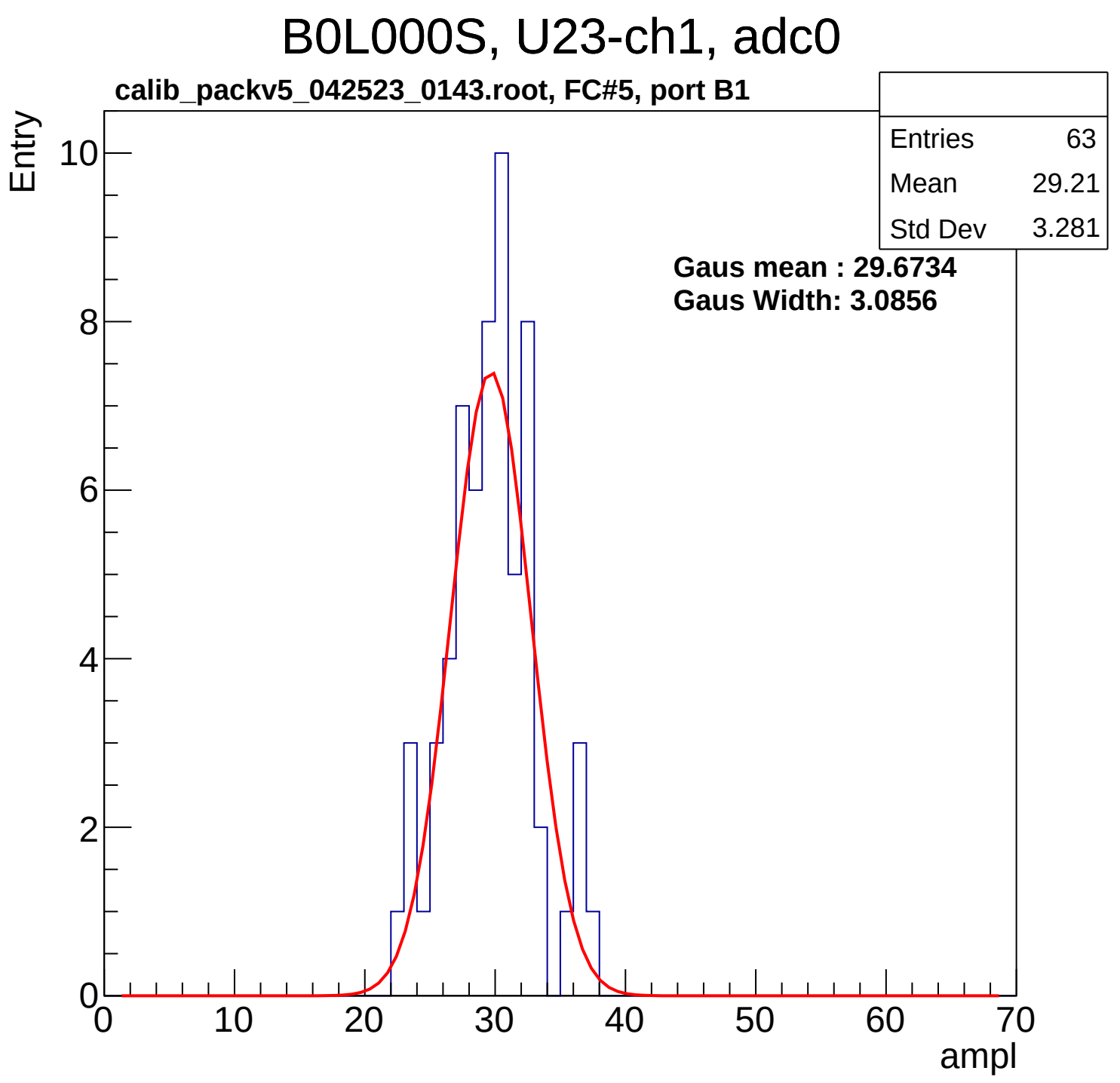

B0L000S, U23-ch0, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 47.2
Std Dev 3.203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

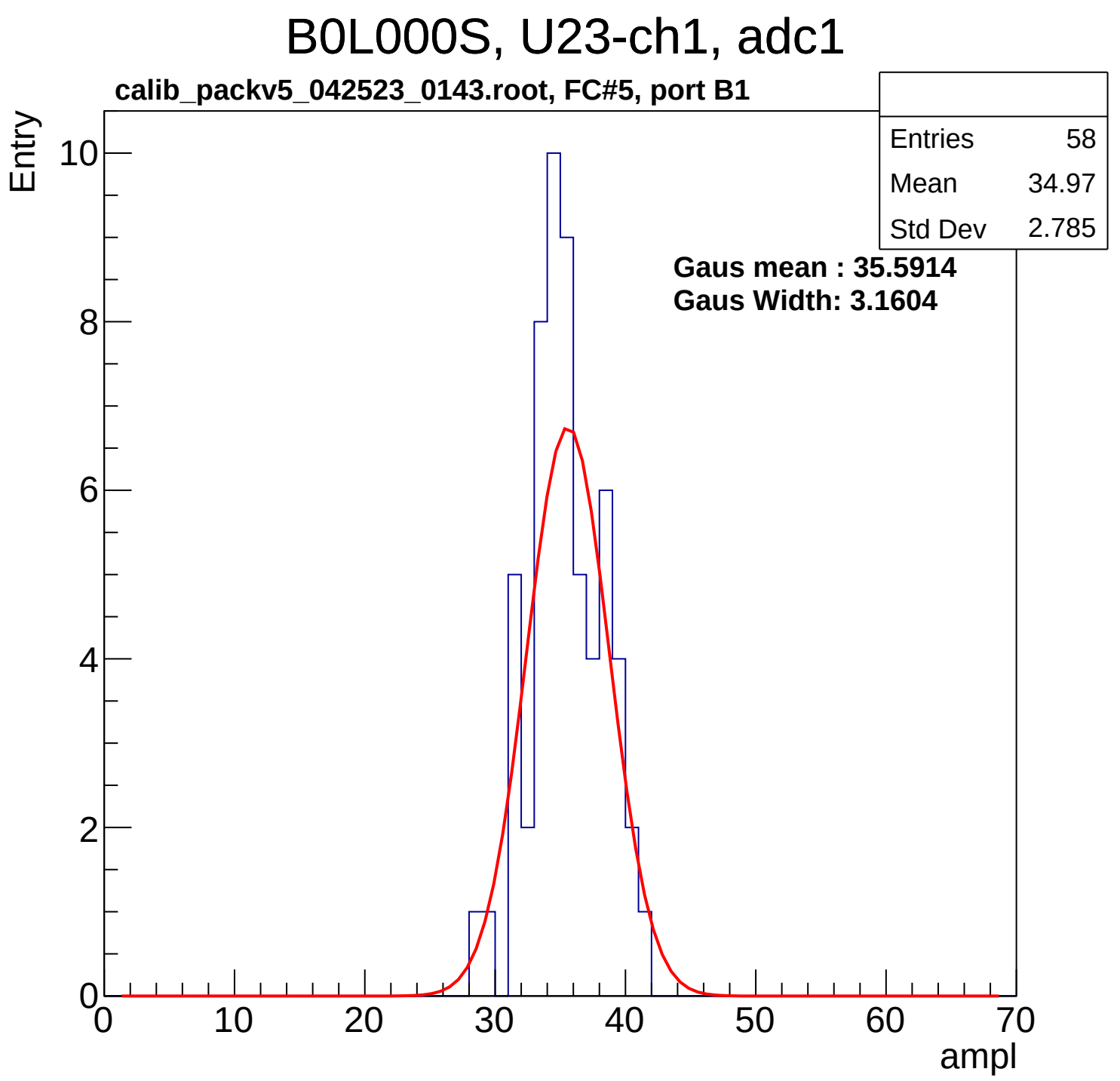

B0L000S, U23-ch0, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 52.56
Std Dev 7.354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

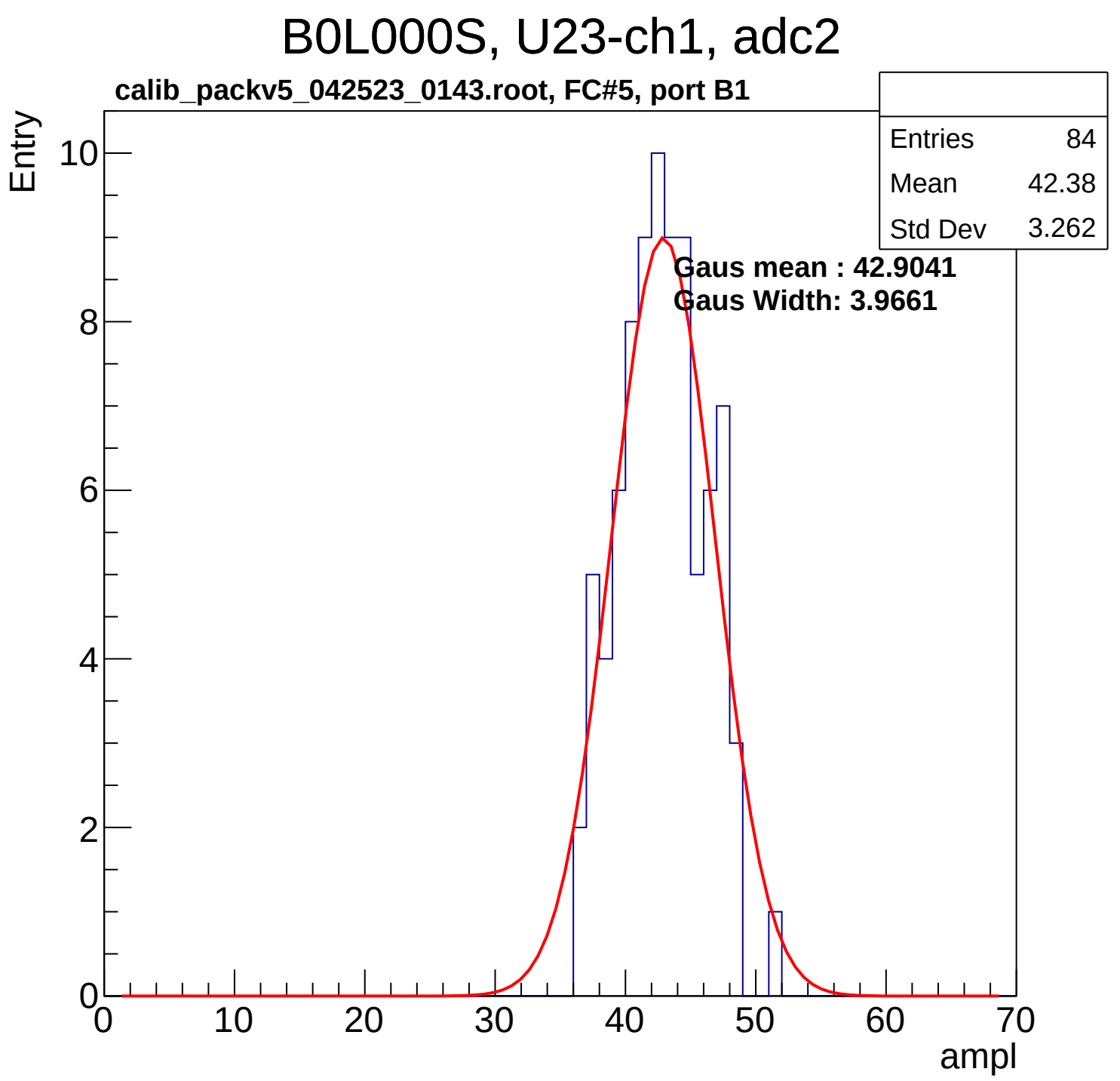

B0L000S, U23-ch0, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 59.05
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

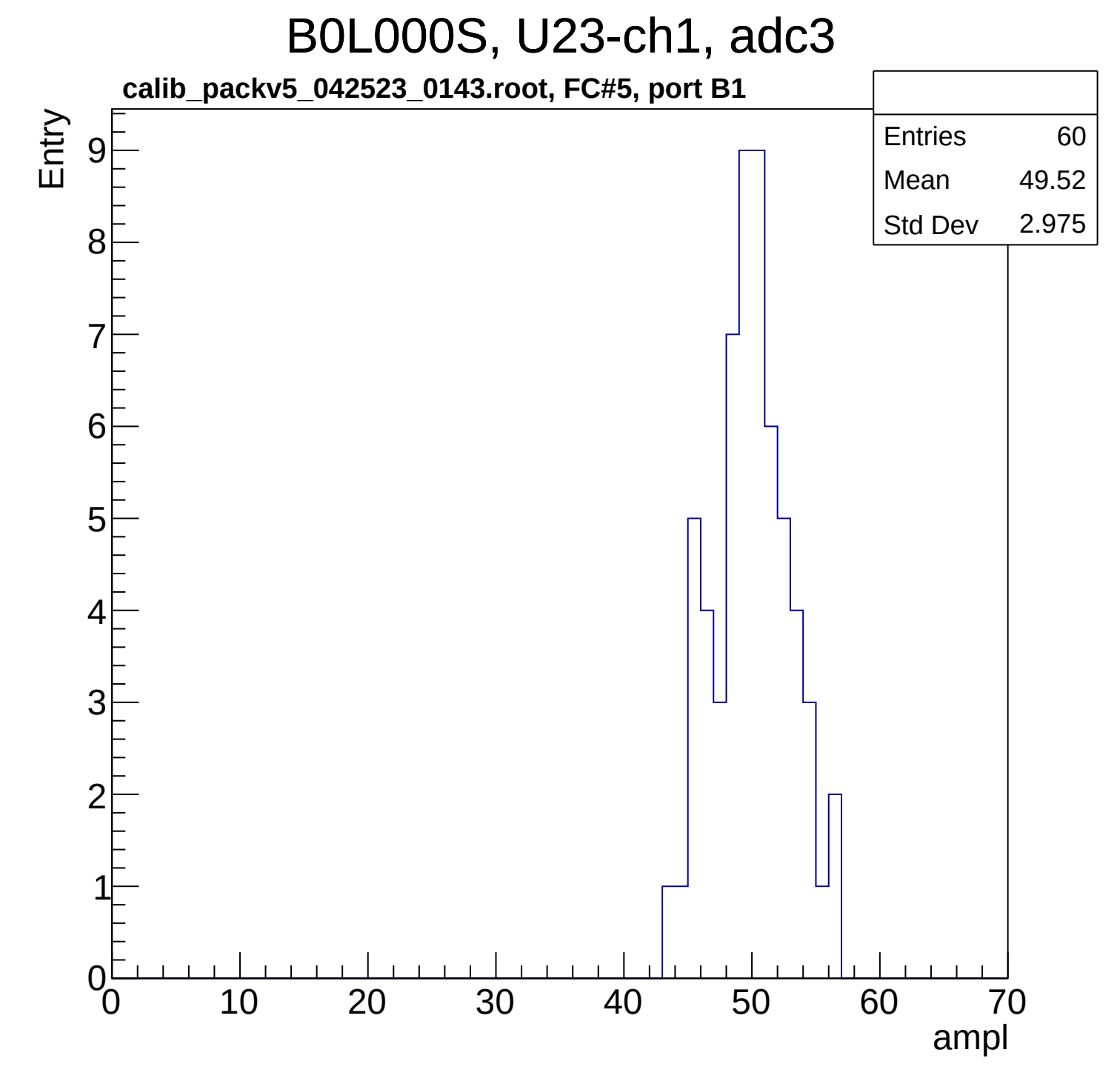

B0L000S, U23-ch0, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.67
Std Dev 1.65
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

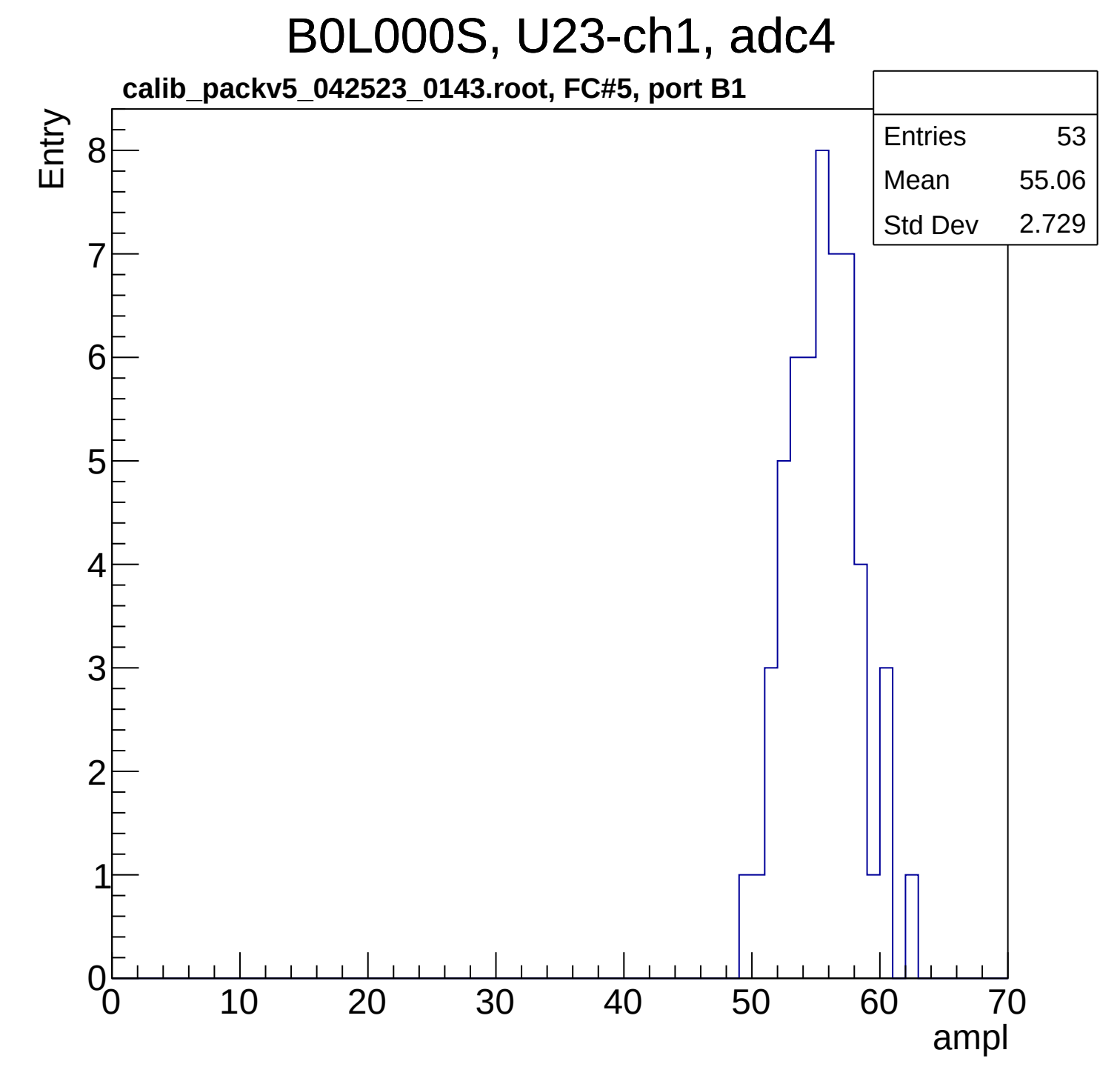

B0L000S, U23-ch0, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

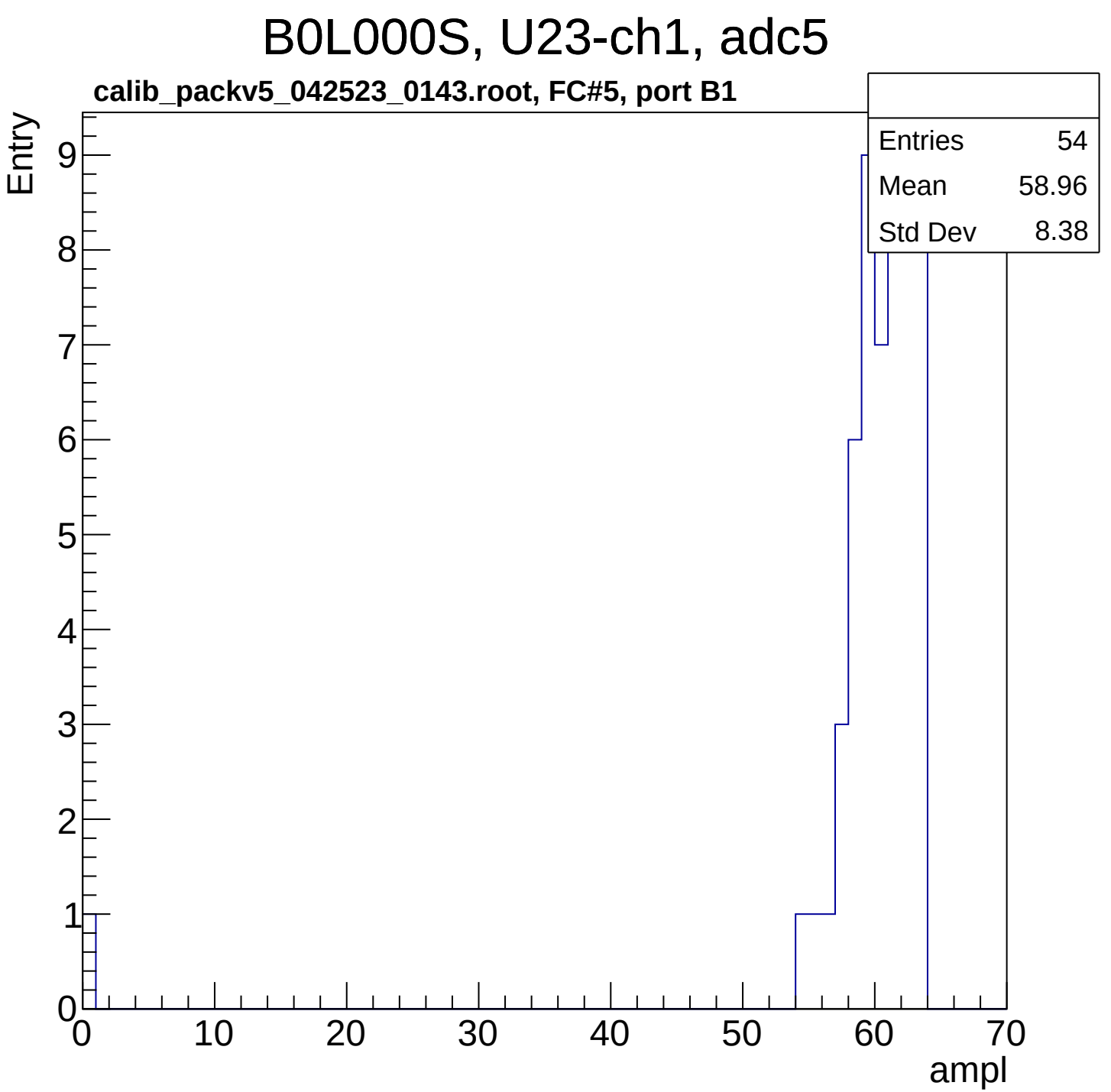

B0L000S, U23-ch1, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.21
Std Dev 3.281
Gaus mean : 29.6734
Gaus Width: 3.0856
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

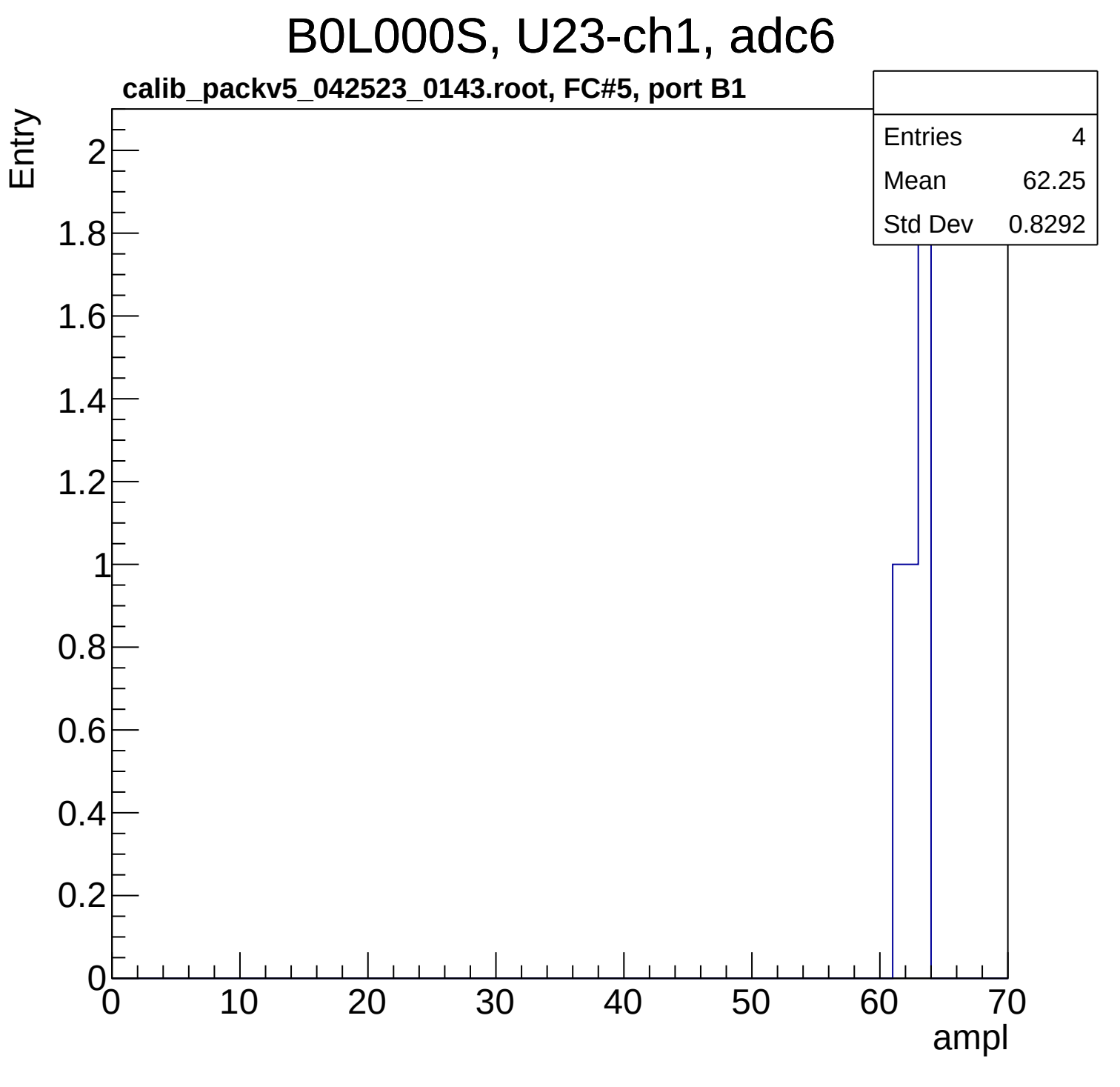

B0L000S, U23-ch1, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 34.97
Std Dev 2.785
Gaus mean : 35.5914
Gaus Width: 3.1604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

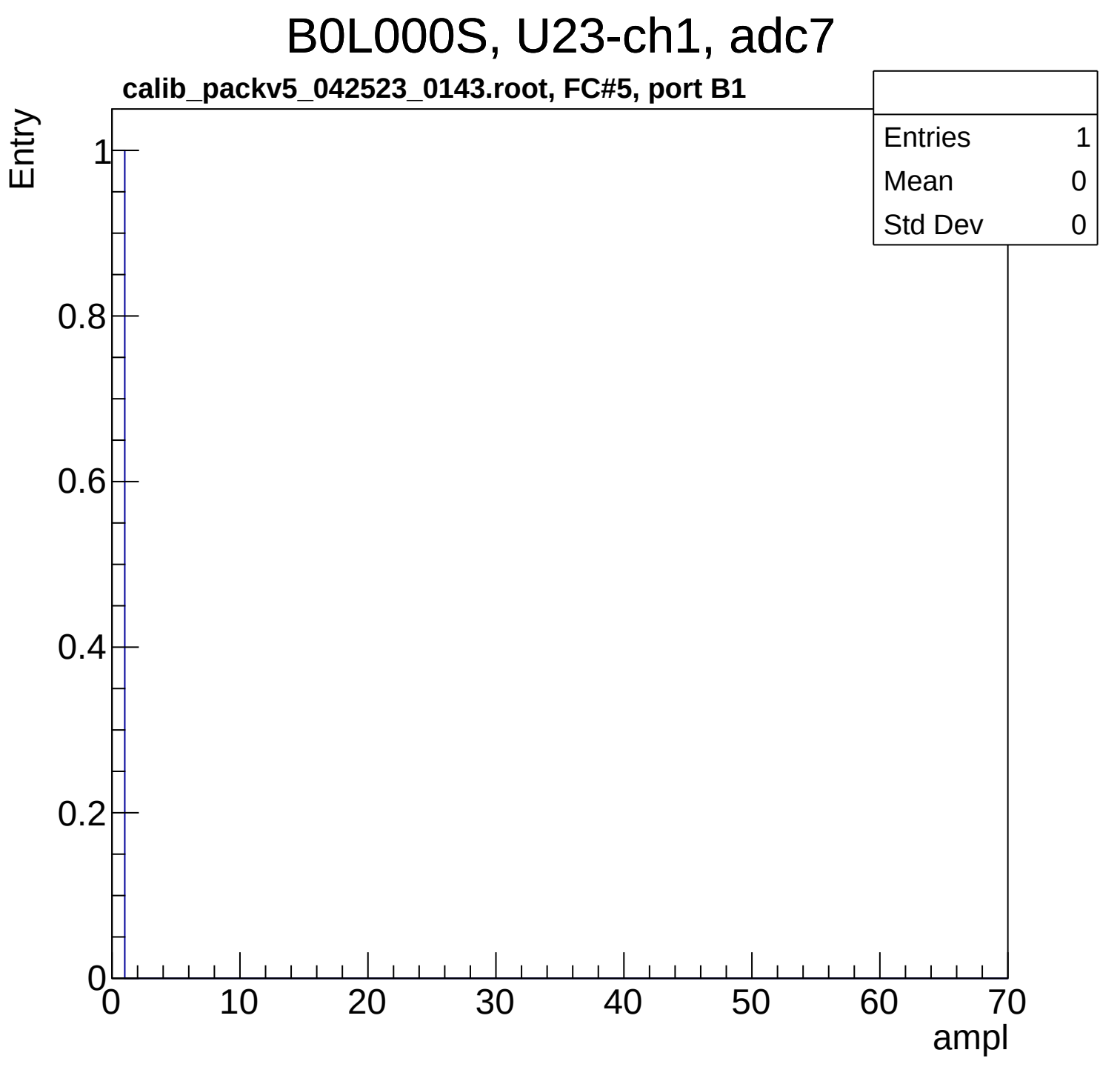

B0L000S, U23-ch1, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 42.38
Std Dev 3.262
Gaus mean : 42.9041
Gaus Width: 3.9661
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

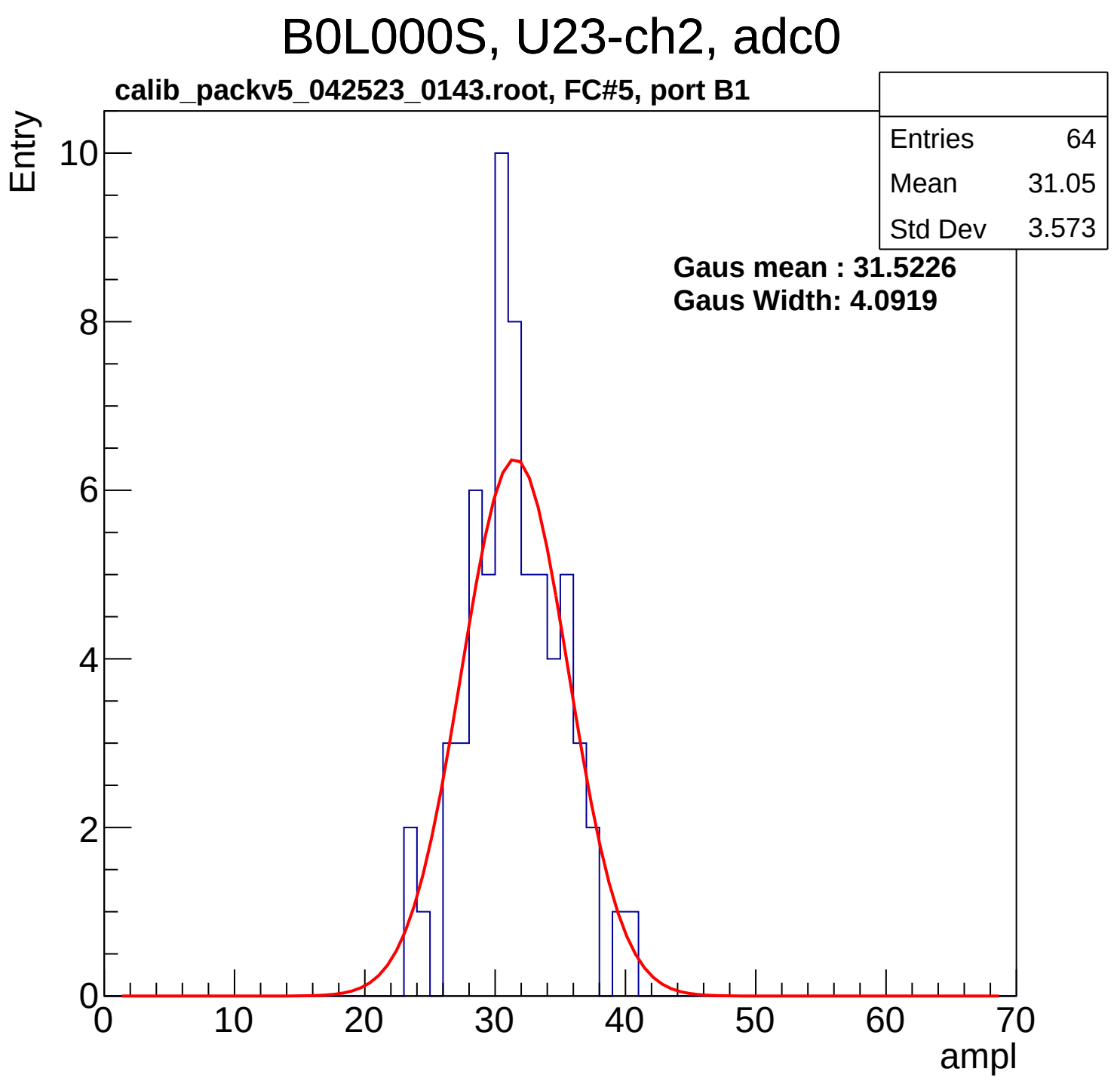

B0L000S, U23-ch1, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 49.52
Std Dev 2.975
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

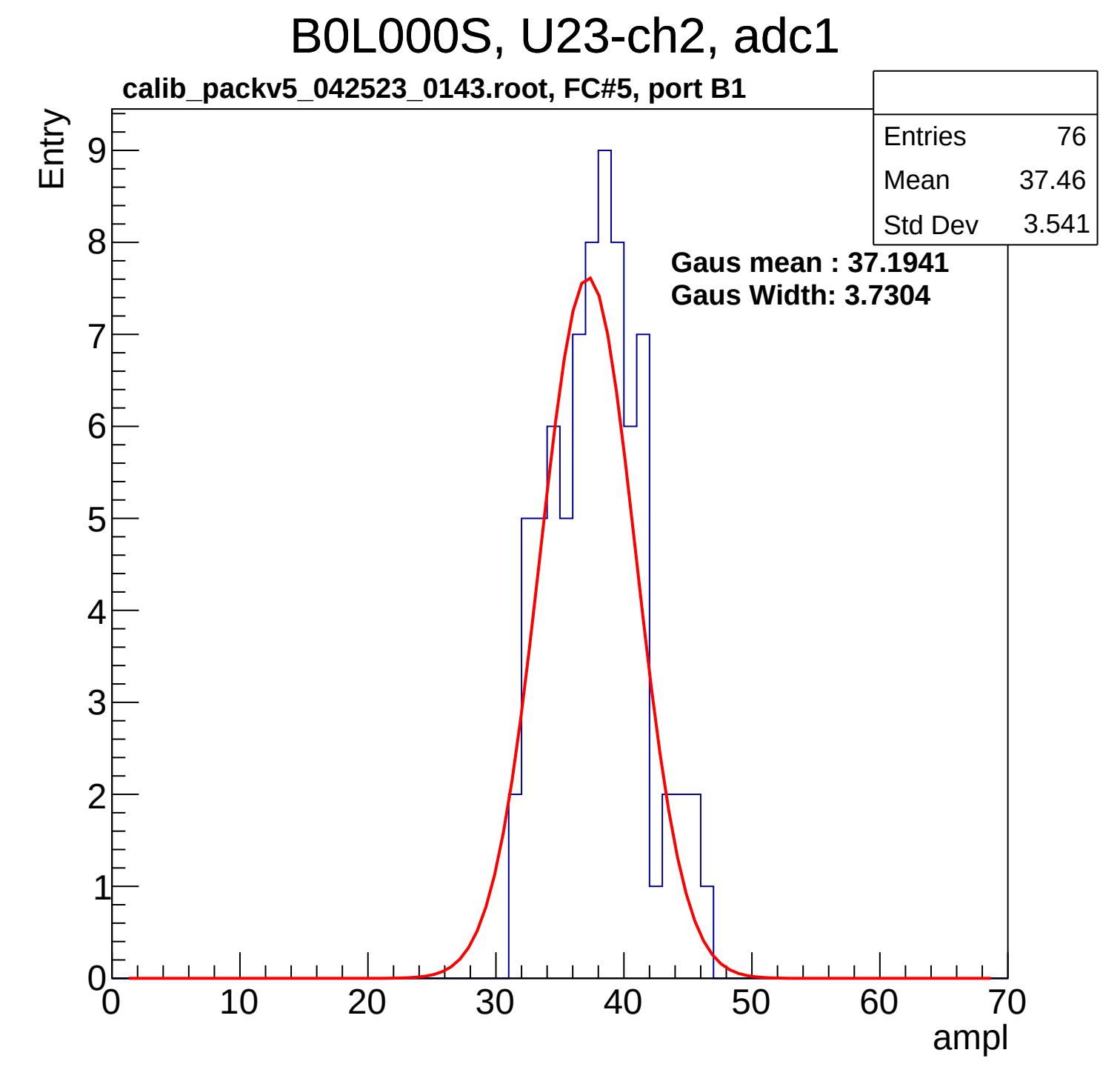

B0L000S, U23-ch1, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.06
Std Dev 2.729
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

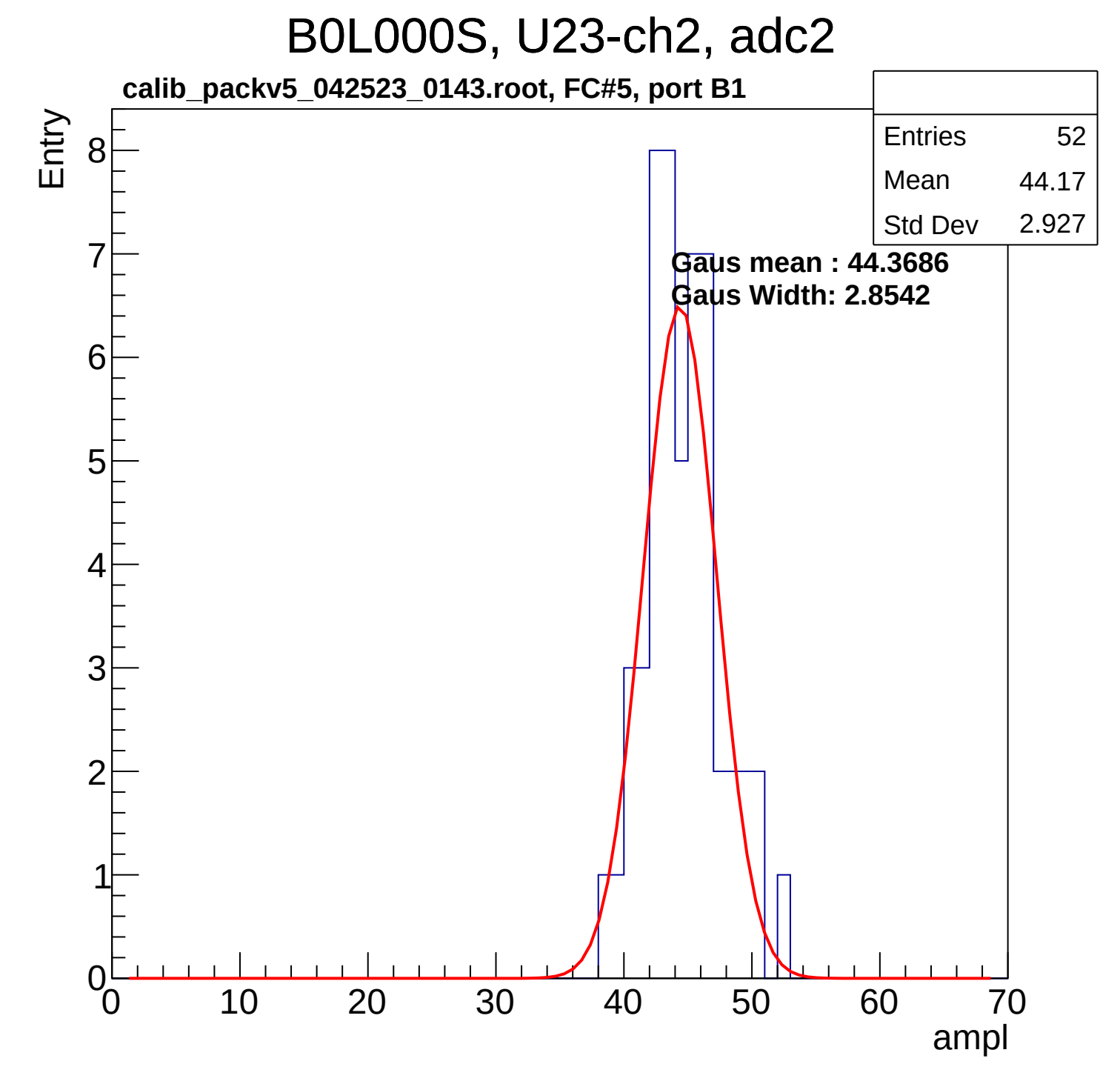

B0L000S, U23-ch1, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.96
Std Dev 8.38
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

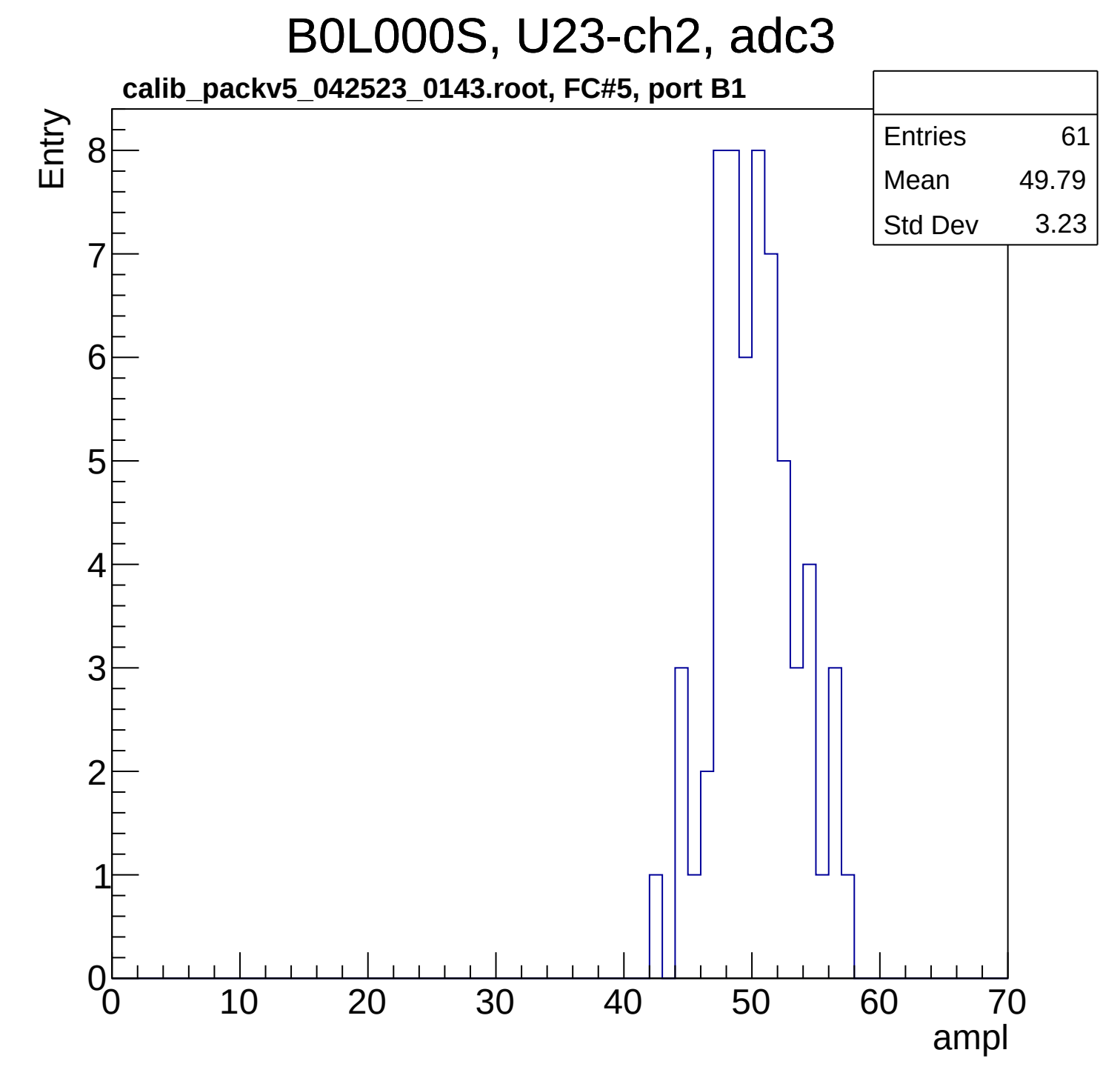

B0L000S, U23-ch1, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

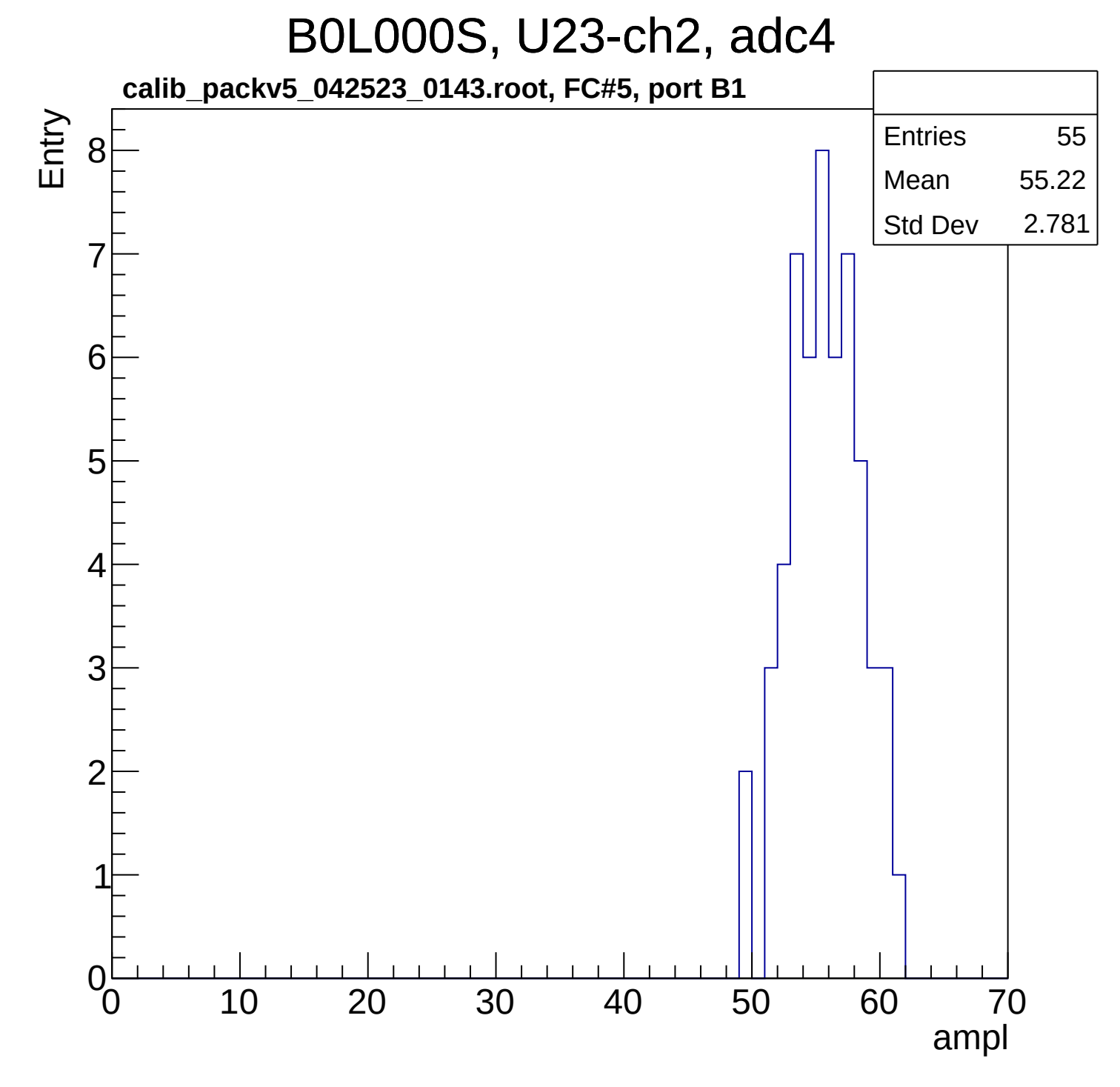

B0L000S, U23-ch1, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

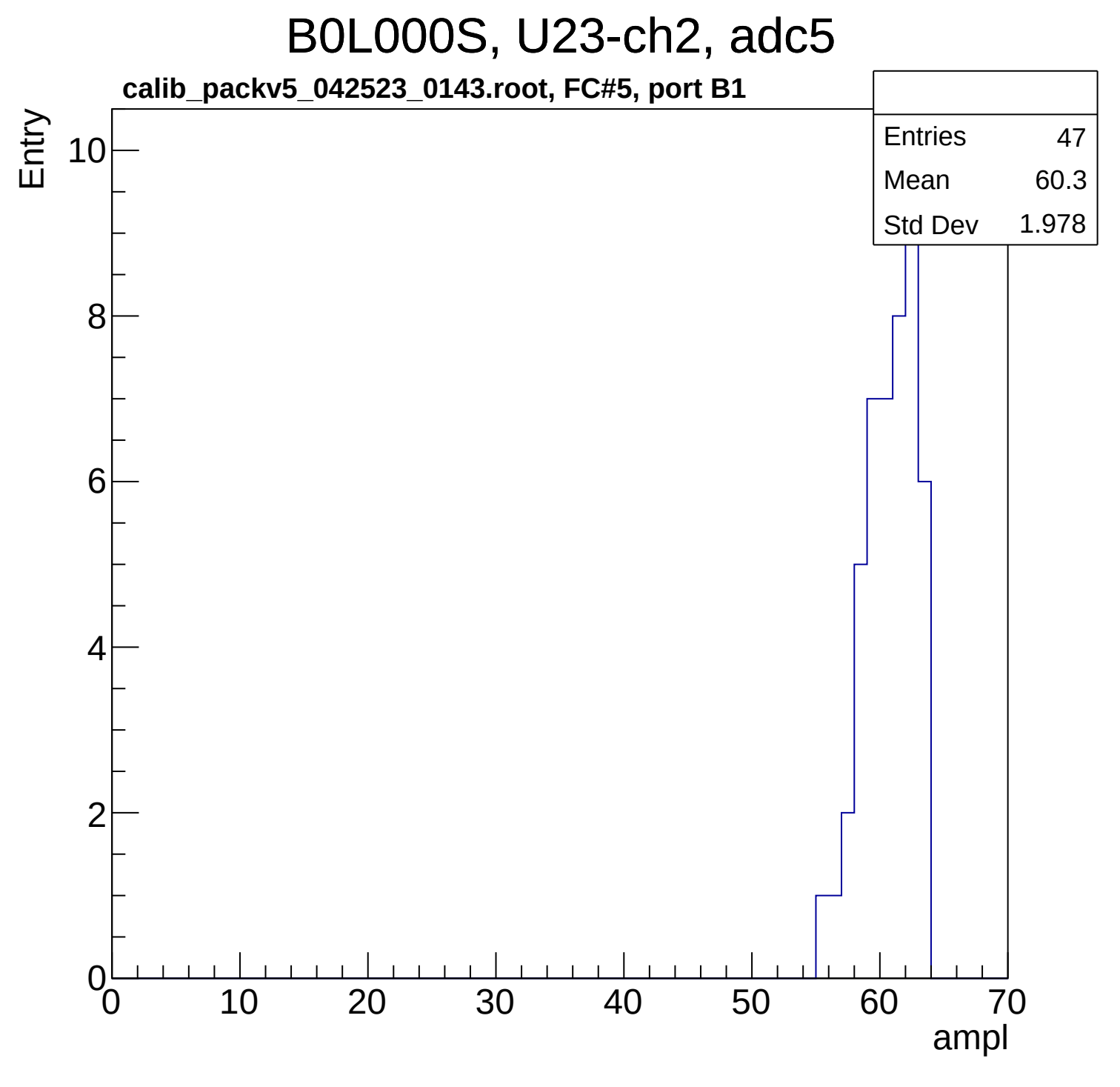

B0L000S, U23-ch2, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 31.05
Std Dev 3.573
Gaus mean : 31.5226
Gaus Width: 4.0919
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

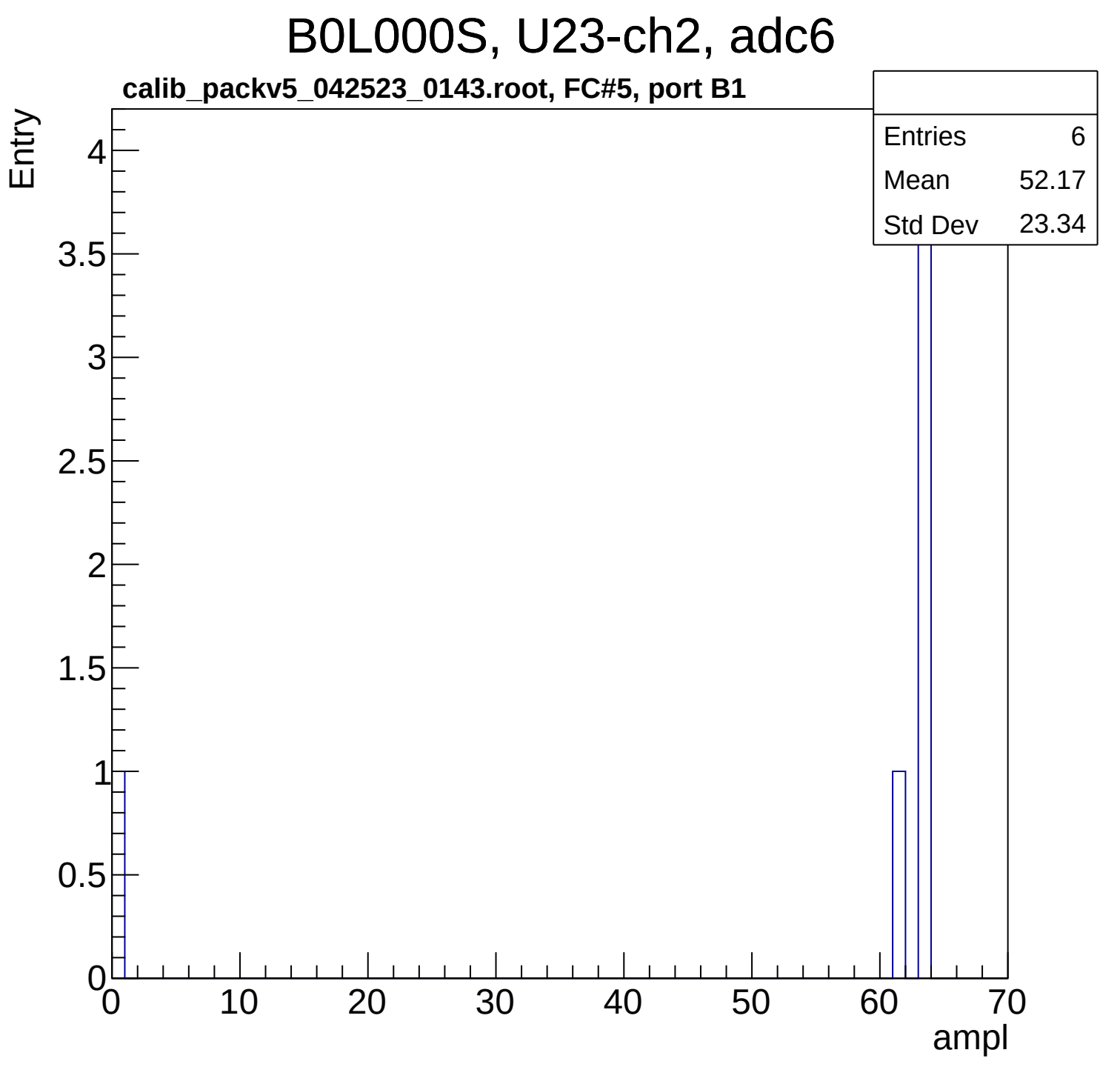

B0L000S, U23-ch2, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 37.46
Std Dev 3.541
Gaus mean : 37.1941
Gaus Width: 3.7304
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

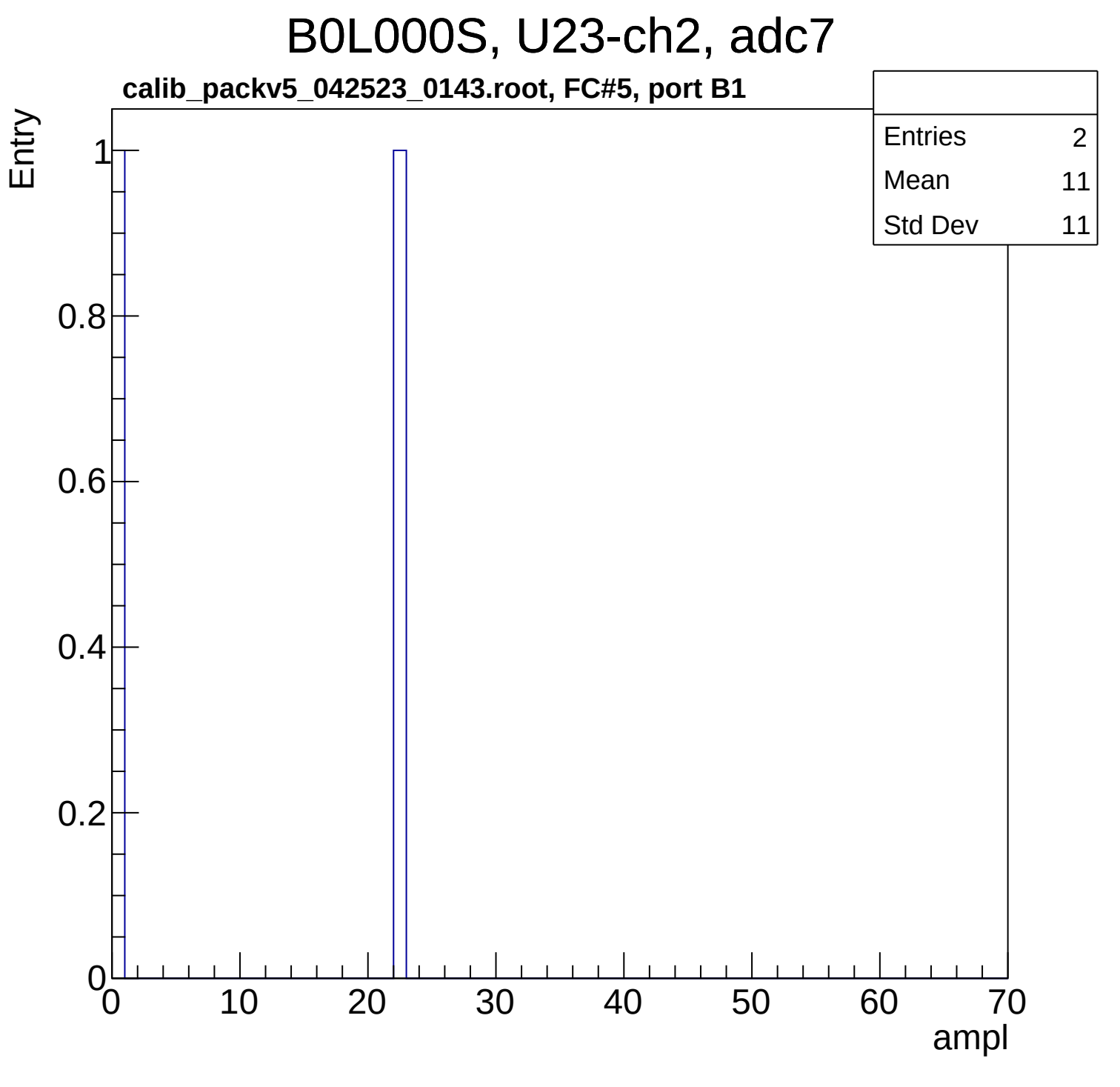

B0L000S, U23-ch2, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 44.17
Std Dev 2.927
Gaus mean : 44.3686
Gaus Width: 2.8542
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

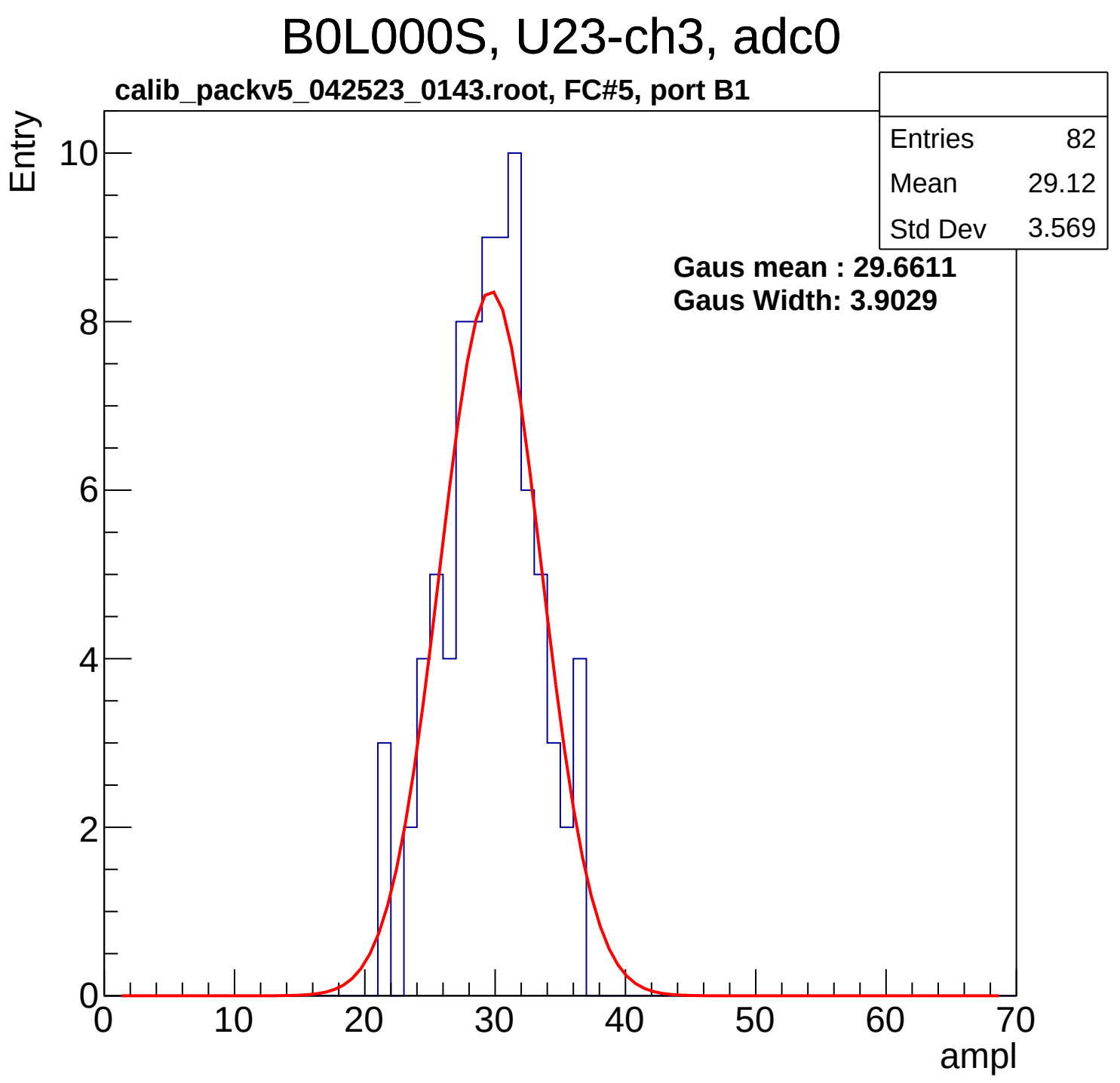

B0L000S, U23-ch2, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 49.79
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

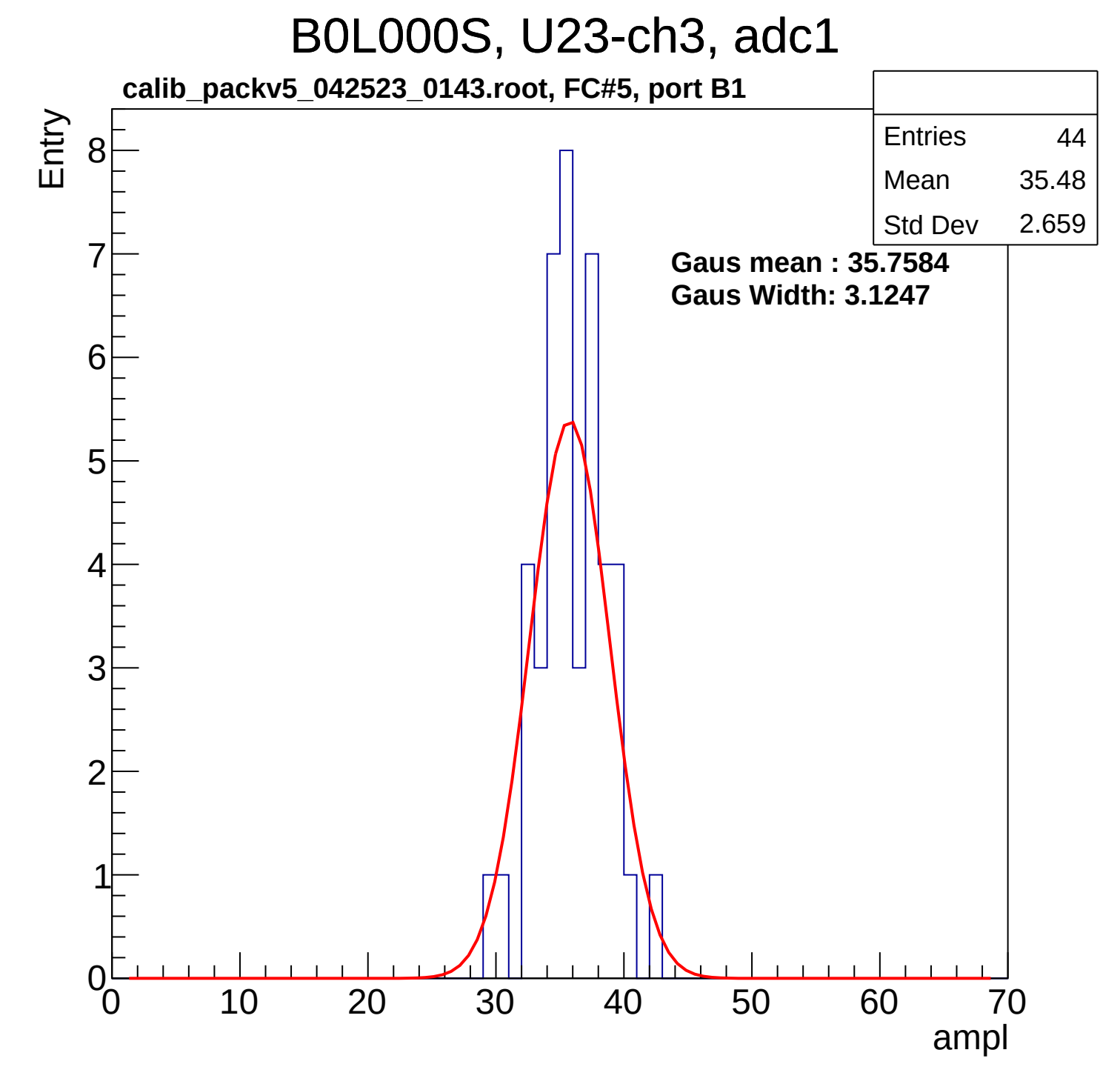

B0L000S, U23-ch2, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.22
Std Dev 2.781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

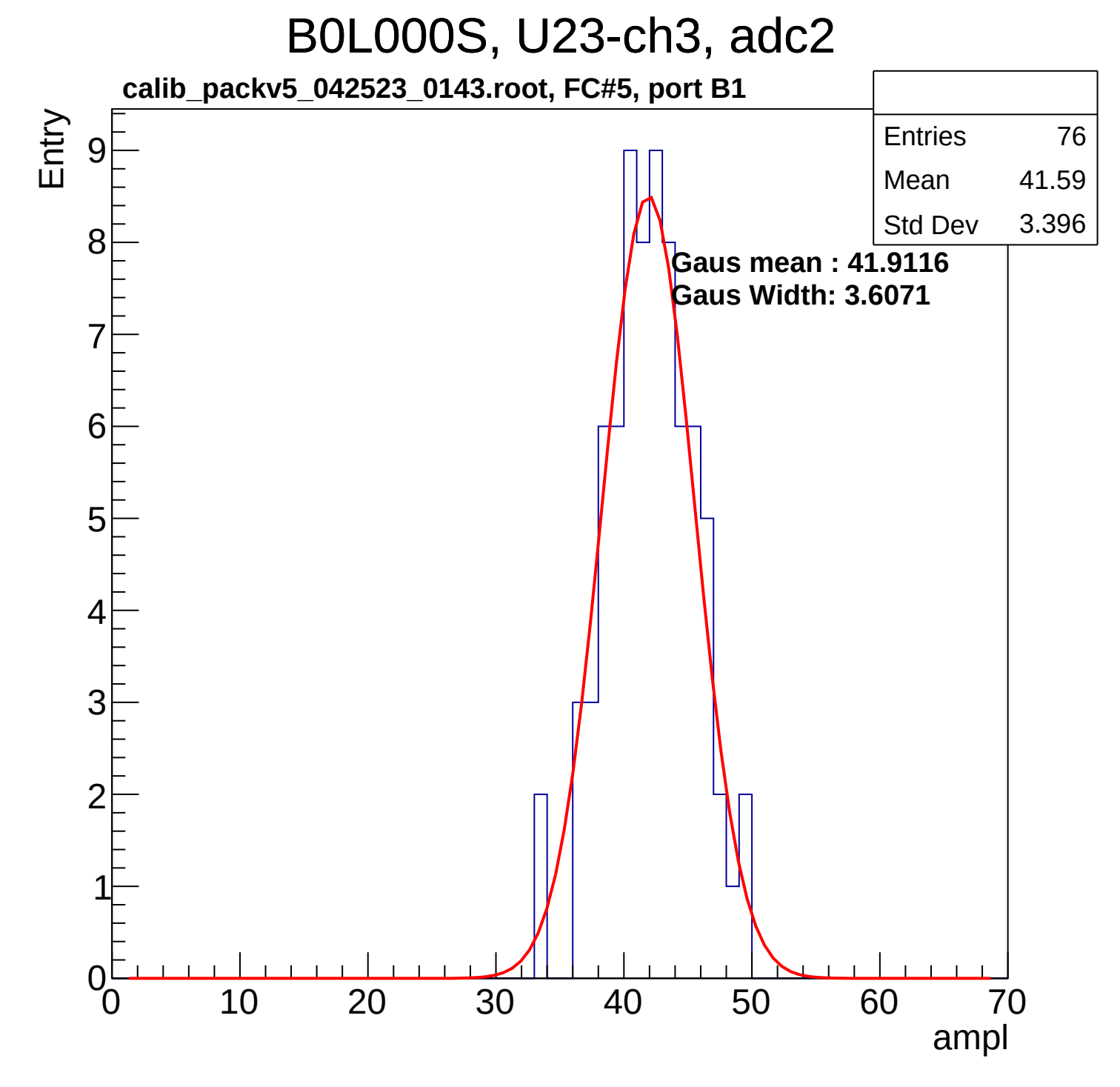

B0L000S, U23-ch2, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 60.3
Std Dev 1.978
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

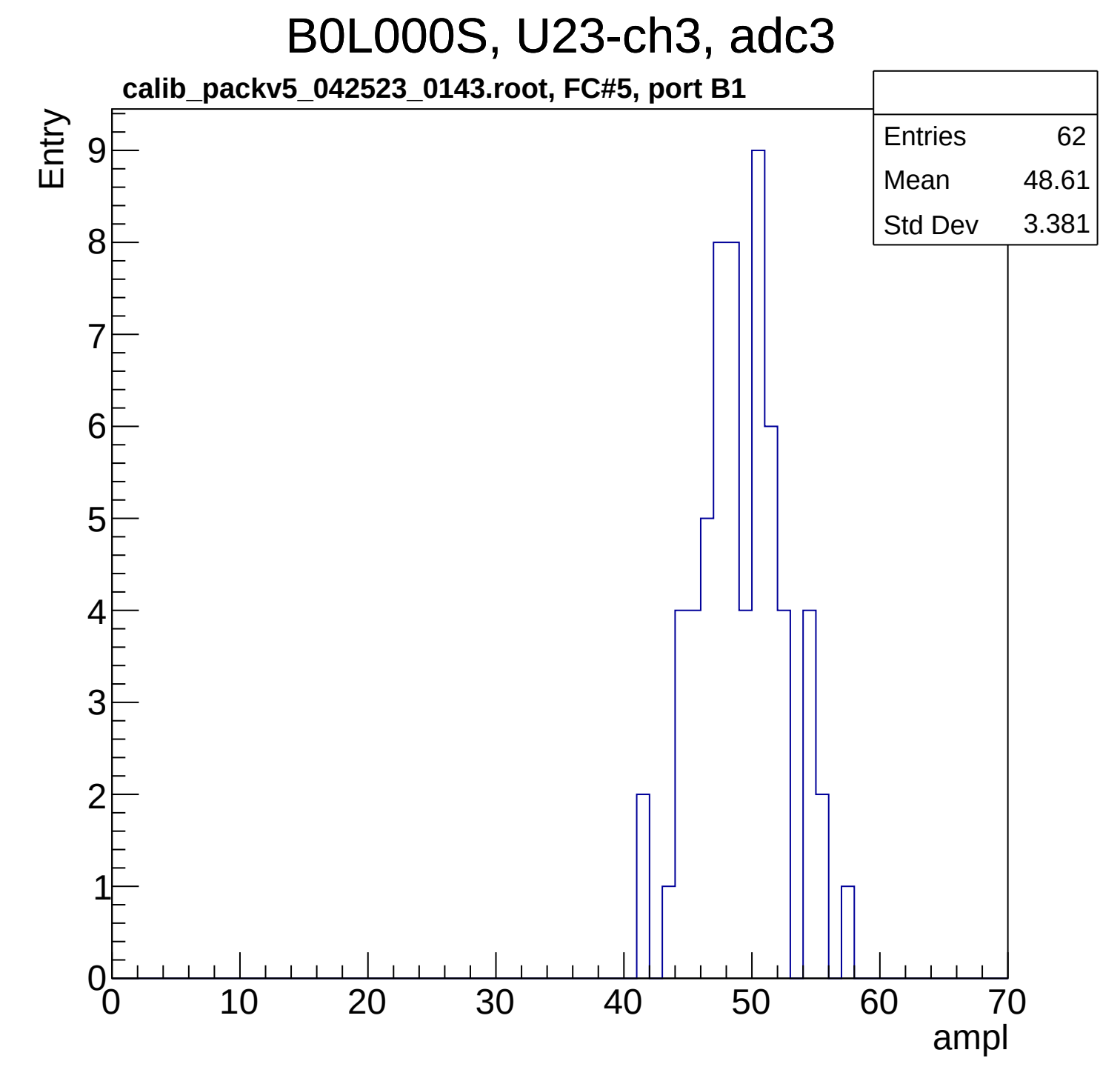

B0L000S, U23-ch2, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 52.17
Std Dev 23.34
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

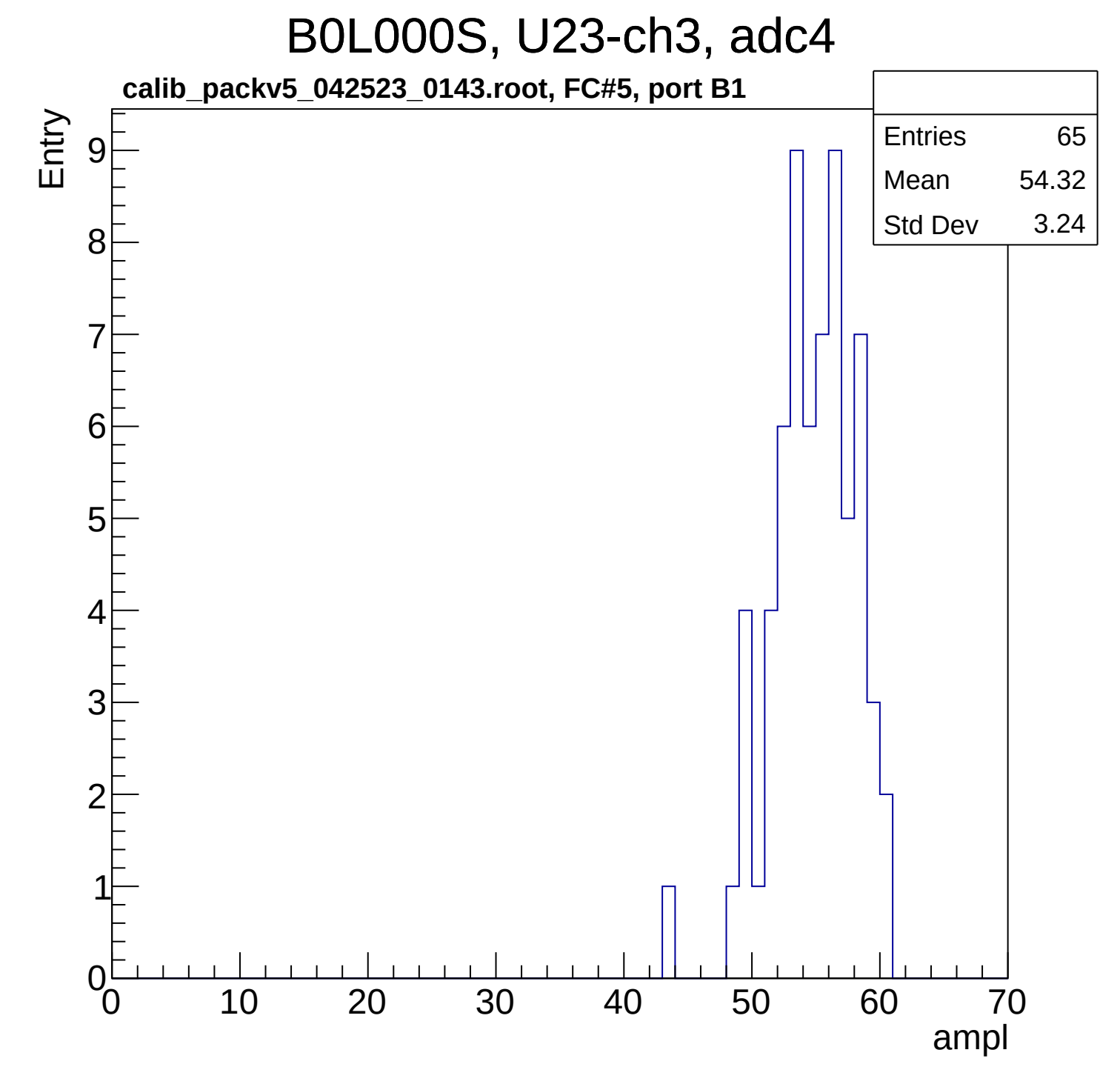

B0L000S, U23-ch2, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

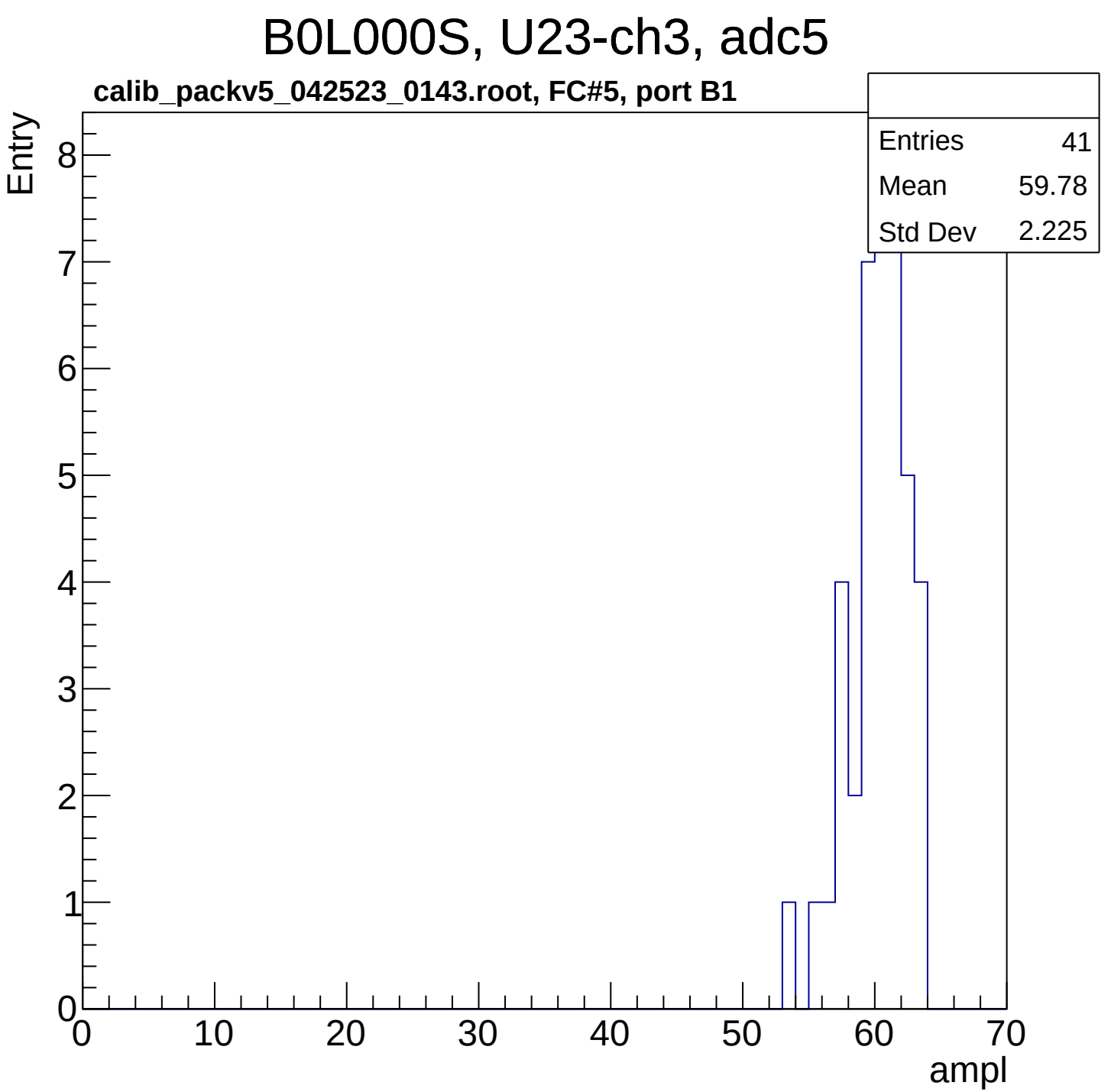

B0L000S, U23-ch3, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 29.12
Std Dev 3.569
Gaus mean : 29.6611
Gaus Width: 3.9029
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

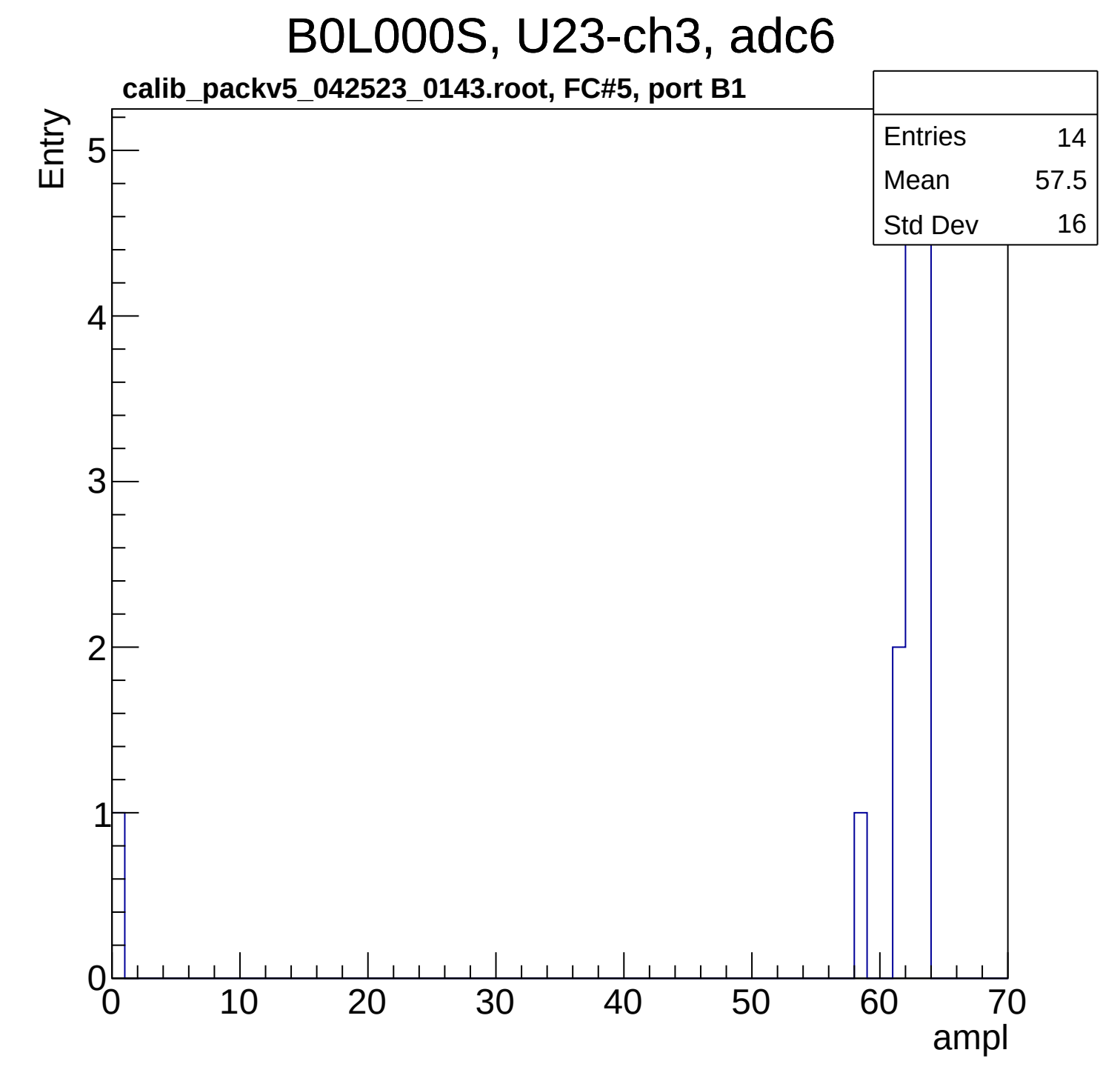

B0L000S, U23-ch3, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 35.48
Std Dev 2.659
Gaus mean : 35.7584
Gaus Width: 3.1247
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

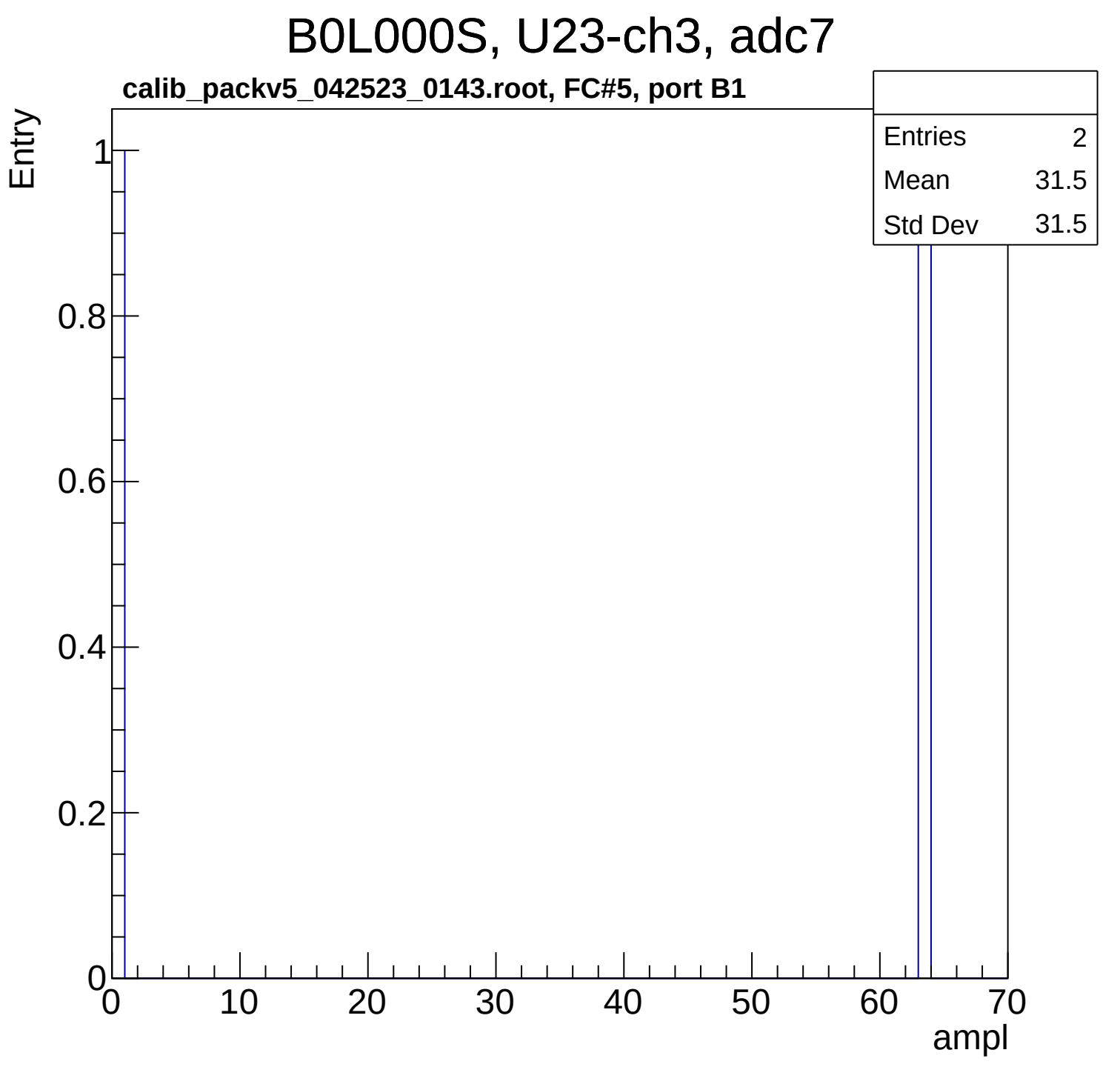

B0L000S, U23-ch3, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 41.59
Std Dev 3.396
Gaus mean : 41.9116
Gaus Width: 3.6071
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

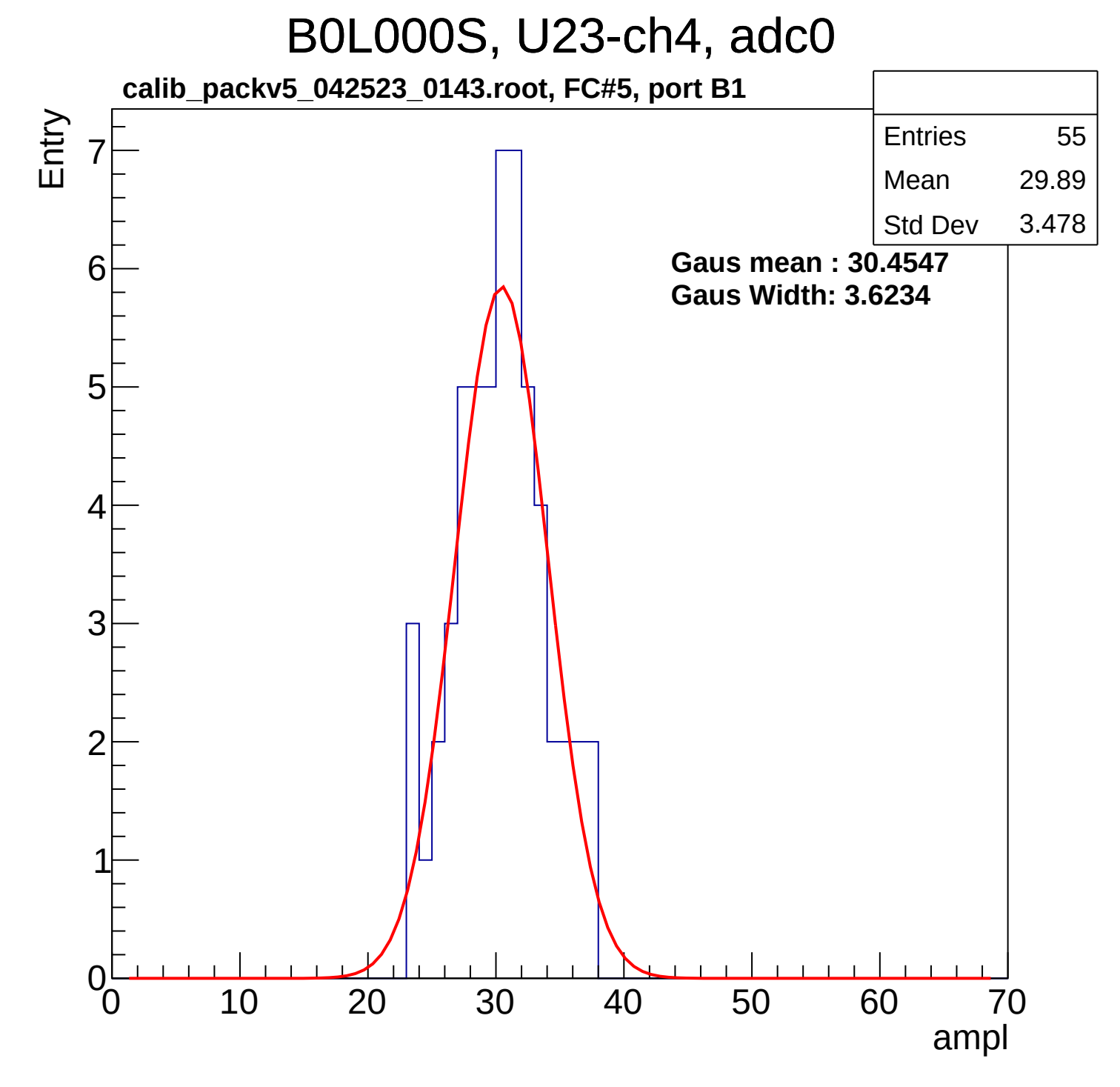

B0L000S, U23-ch3, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.61
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

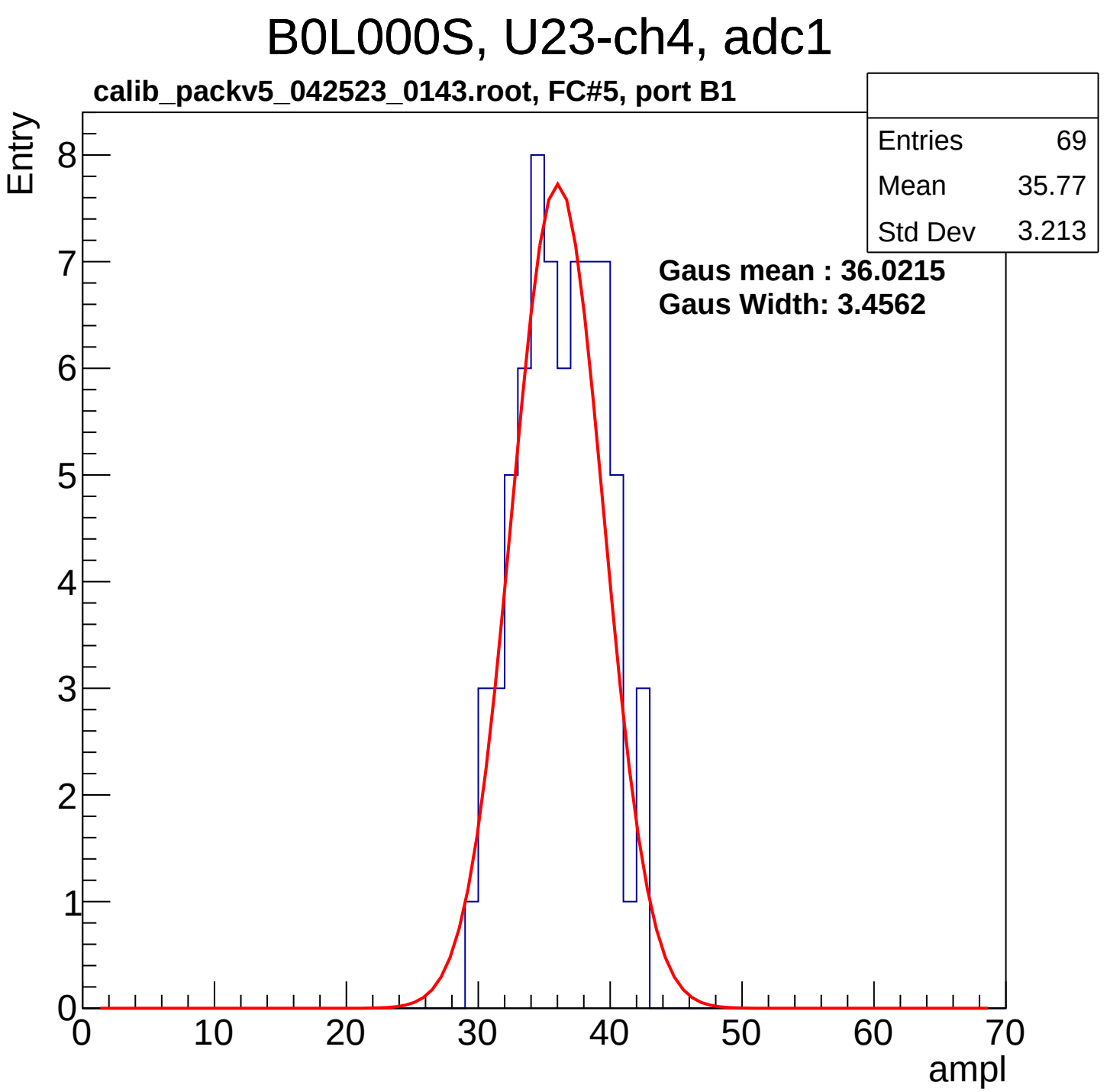

B0L000S, U23-ch3, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 54.32
Std Dev 3.24
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

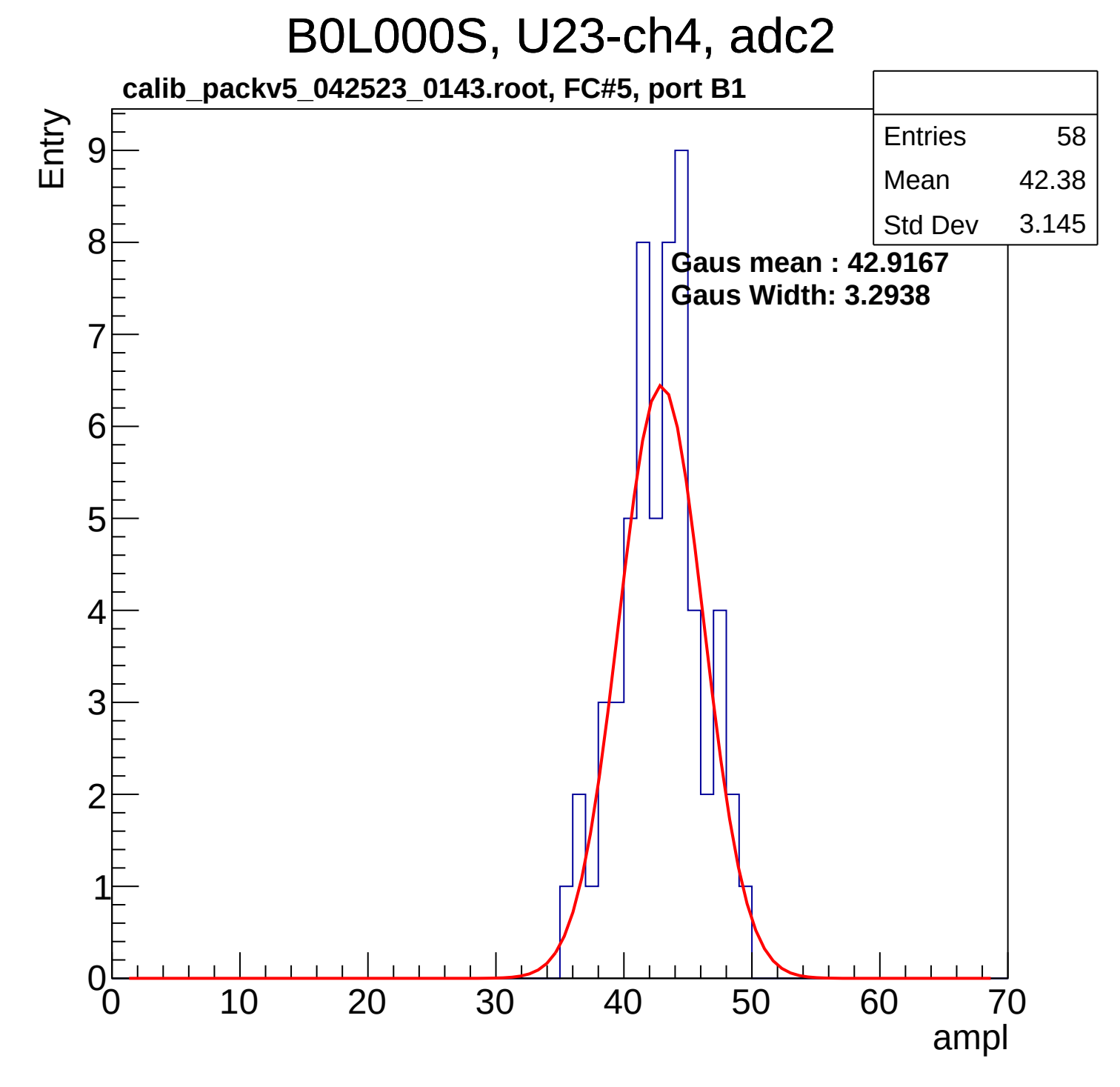

B0L000S, U23-ch3, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.78
Std Dev 2.225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

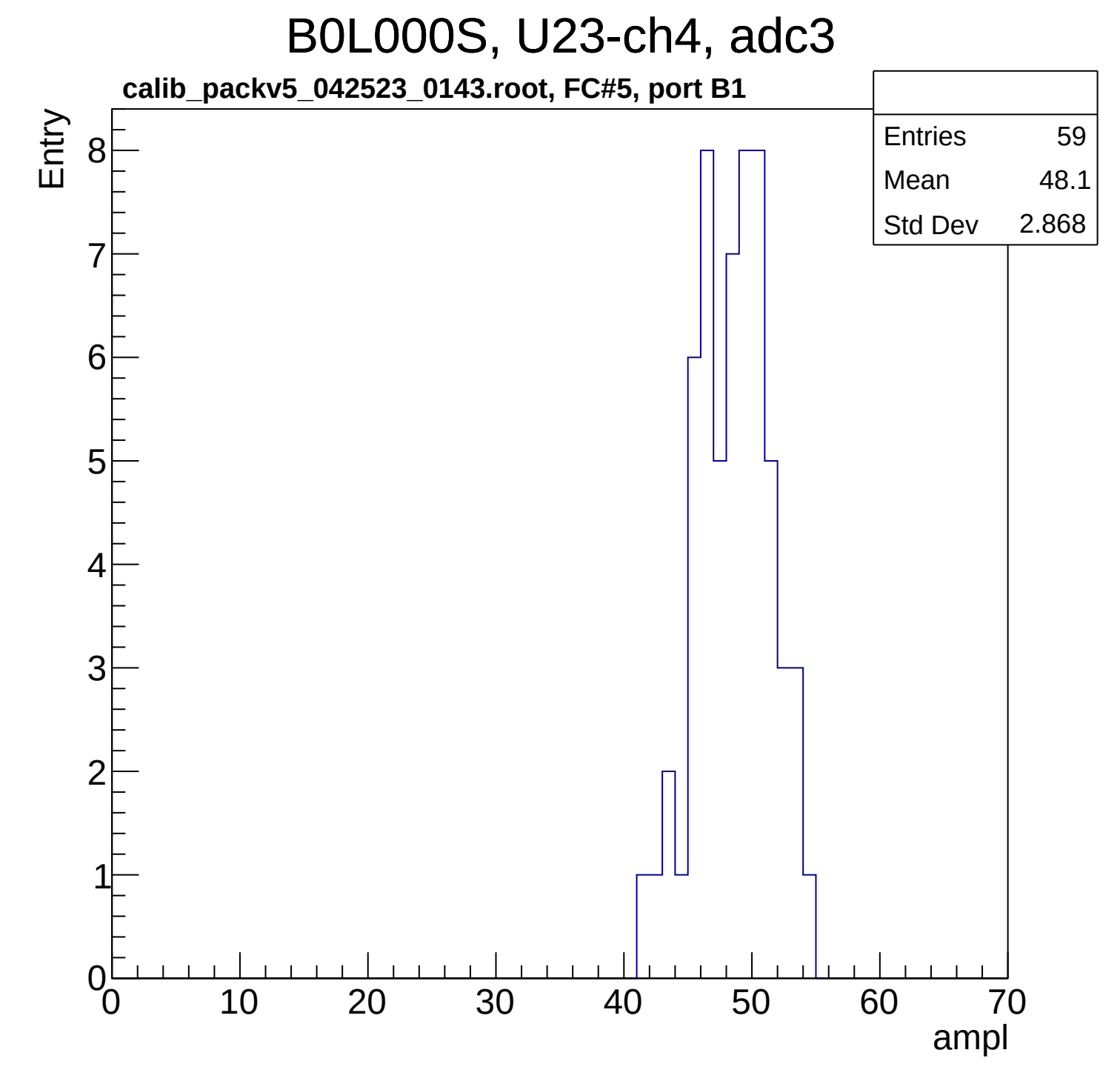

B0L000S, U23-ch3, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.5
Std Dev 16
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

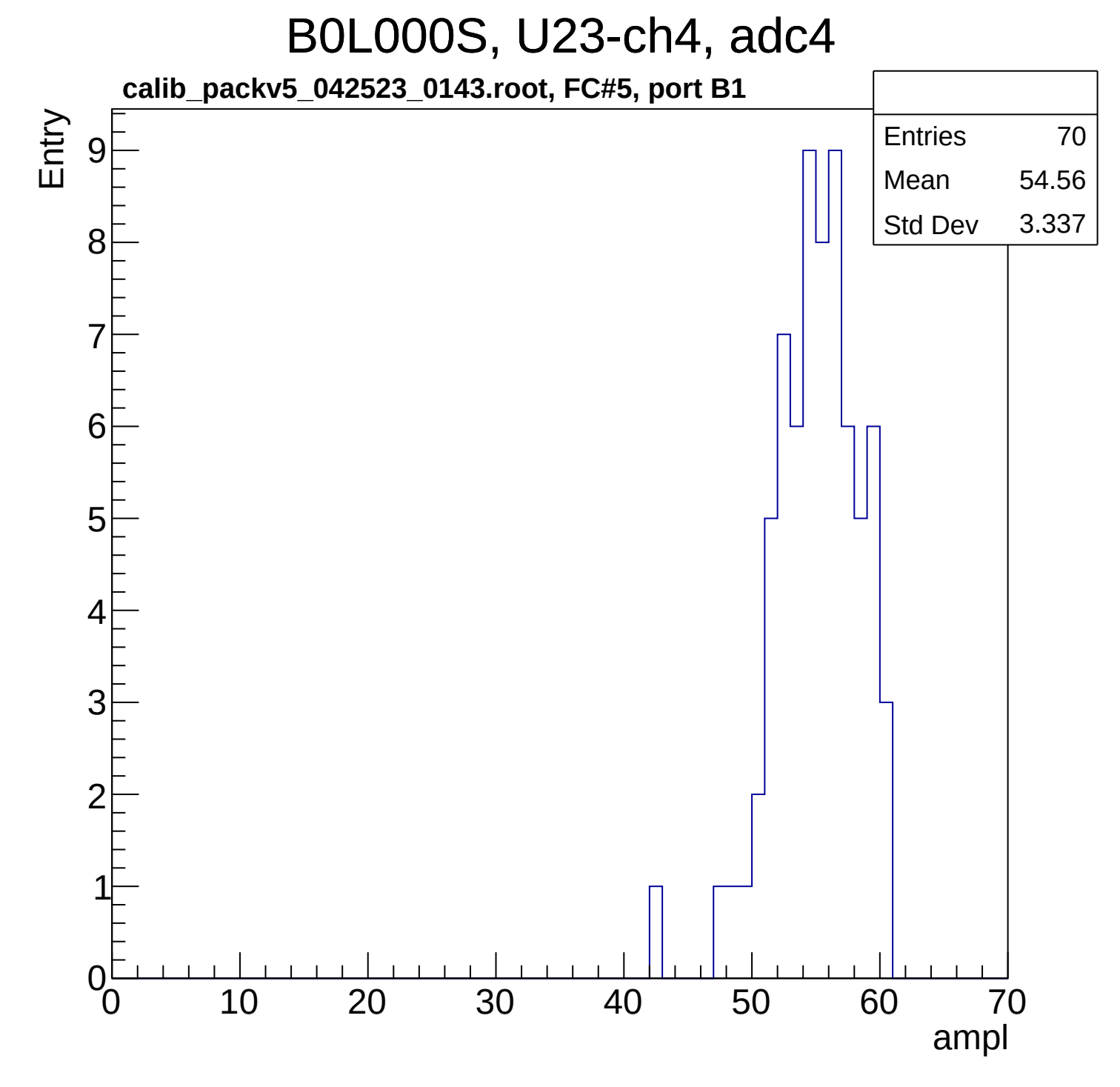

B0L000S, U23-ch3, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

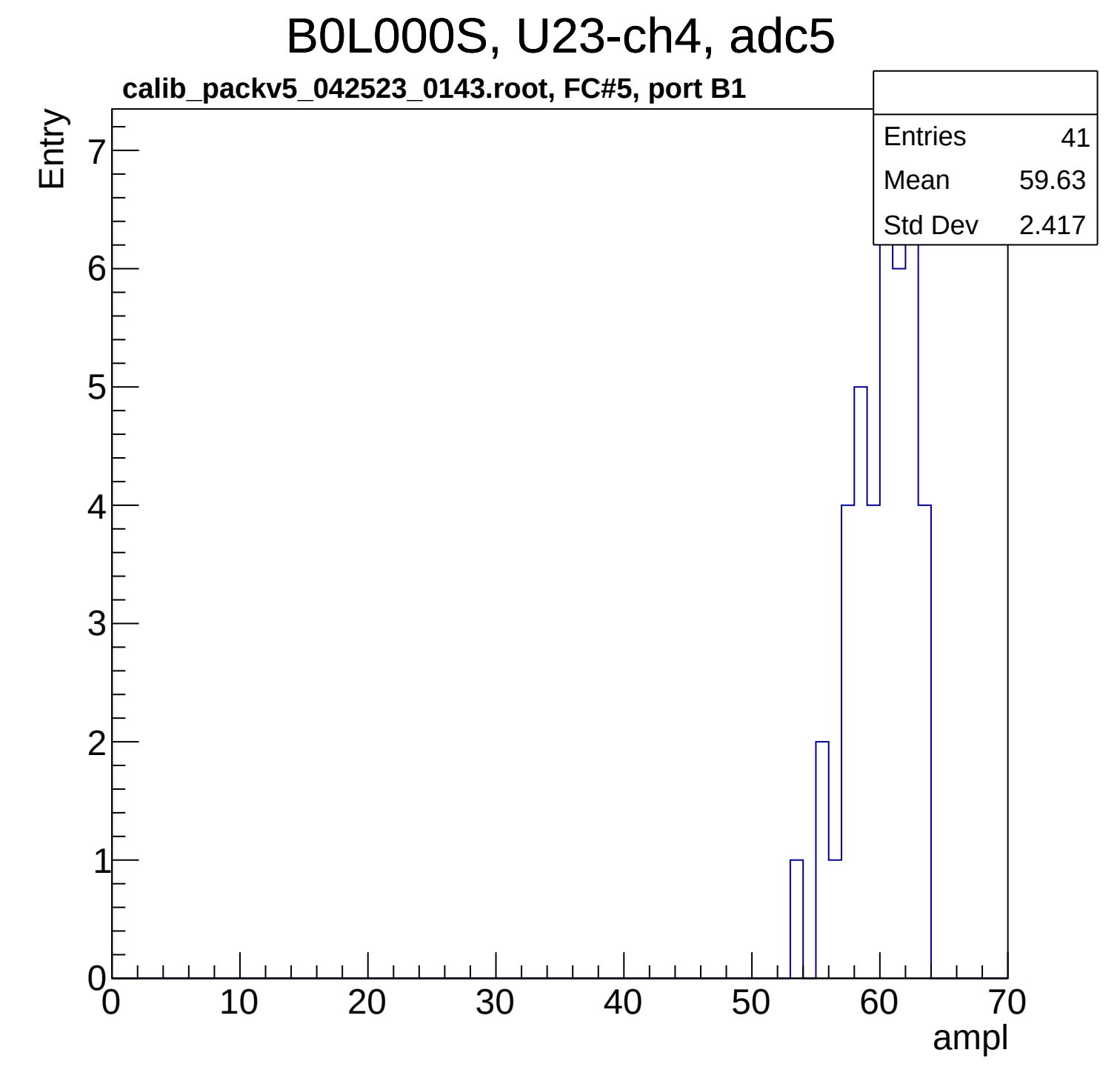

B0L000S, U23-ch4, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.89
Std Dev 3.478
Gaus mean : 30.4547
Gaus Width: 3.6234
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

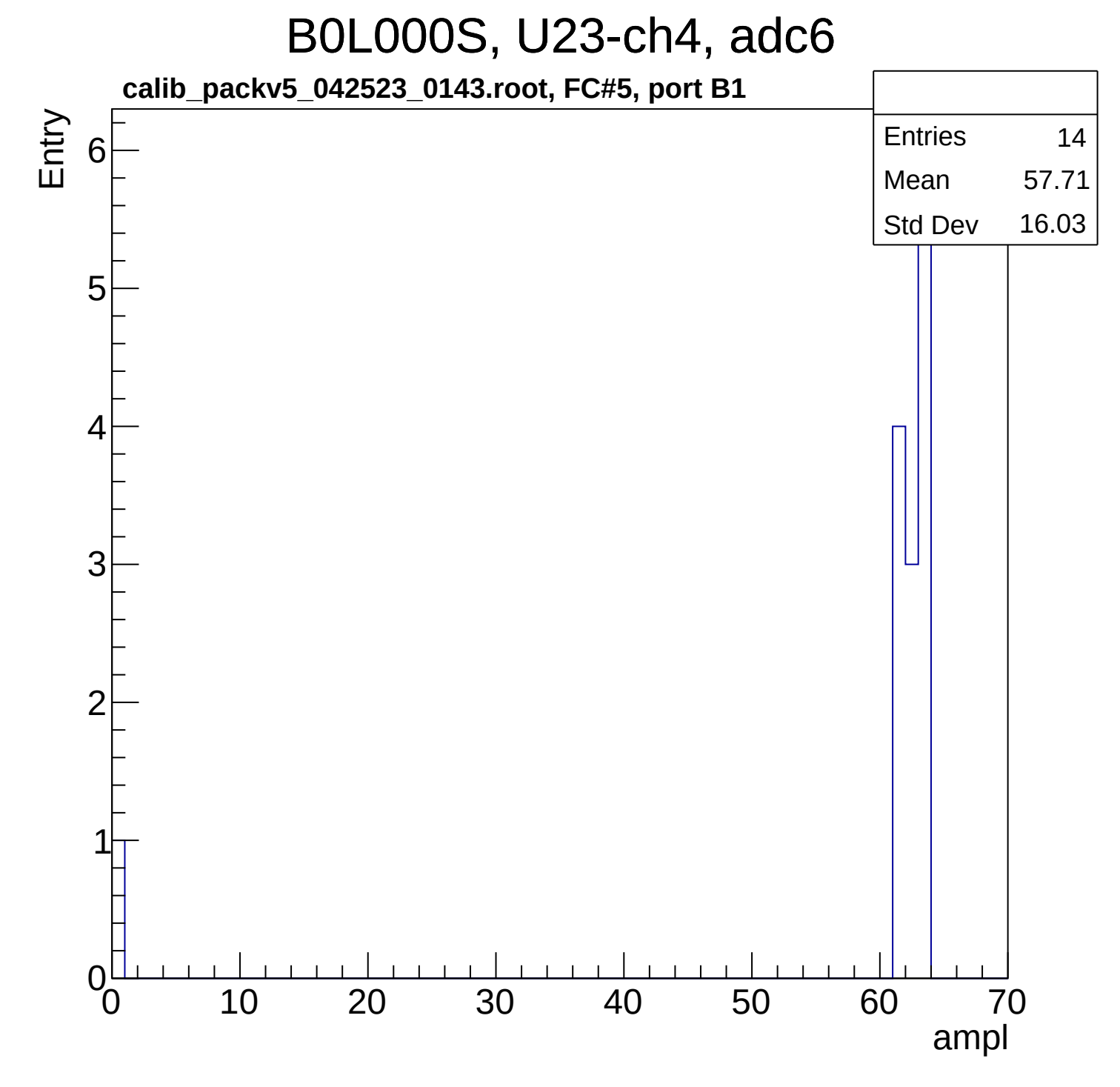

B0L000S, U23-ch4, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.77
Std Dev 3.213
Gaus mean : 36.0215
Gaus Width: 3.4562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

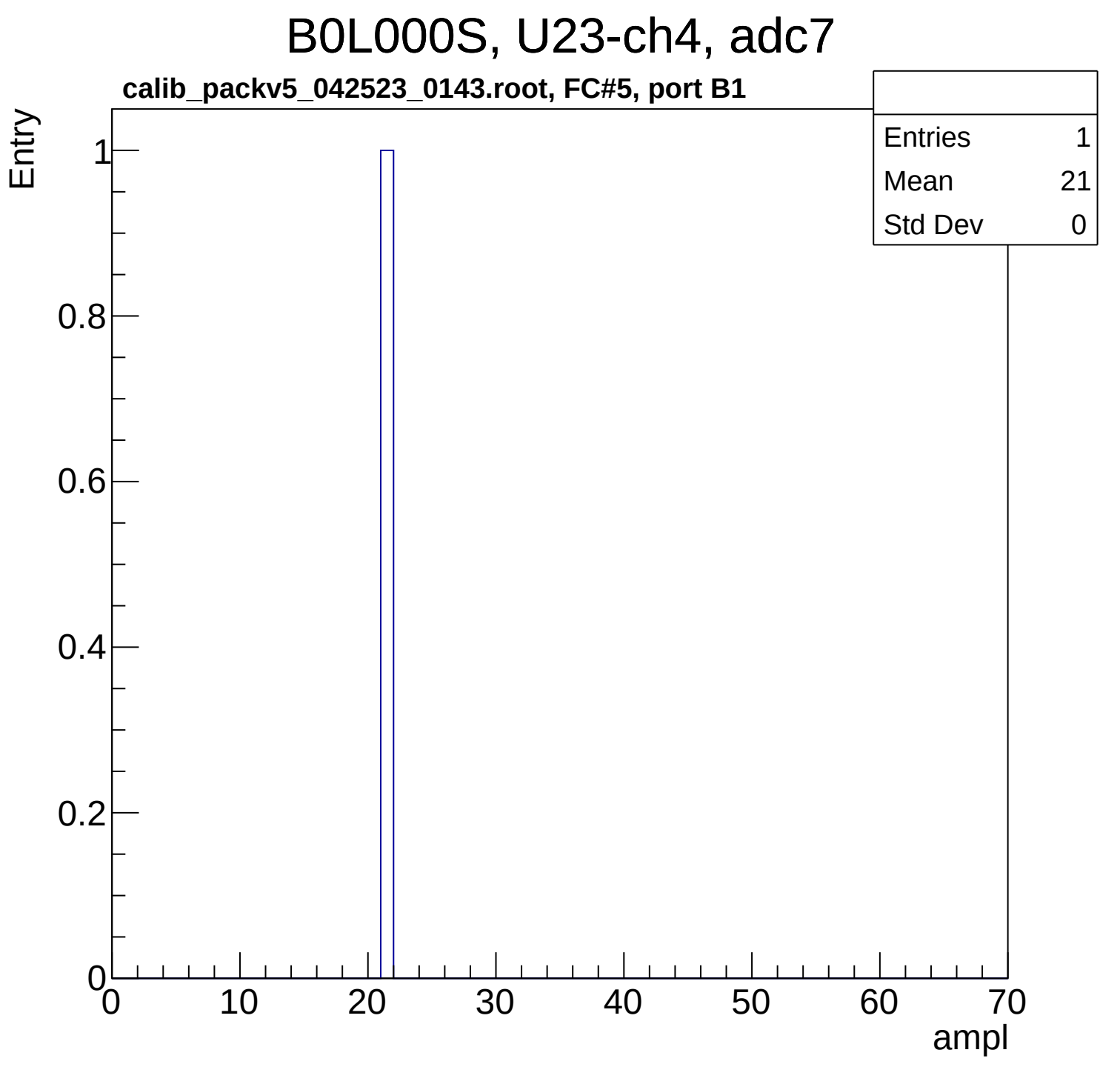

B0L000S, U23-ch4, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 42.38
Std Dev 3.145
Gaus mean : 42.9167
Gaus Width: 3.2938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

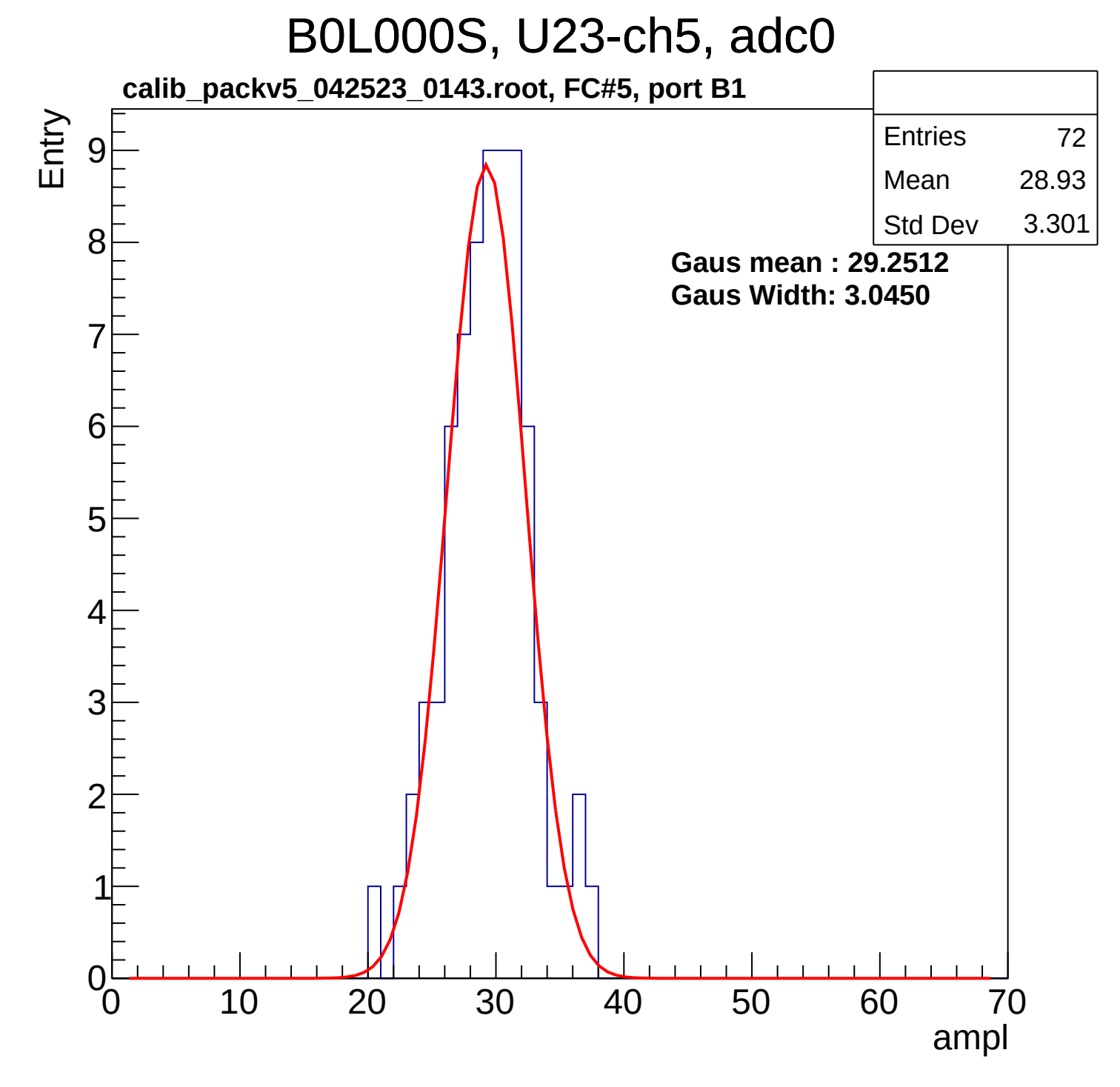

B0L000S, U23-ch4, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48.1
Std Dev 2.868
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

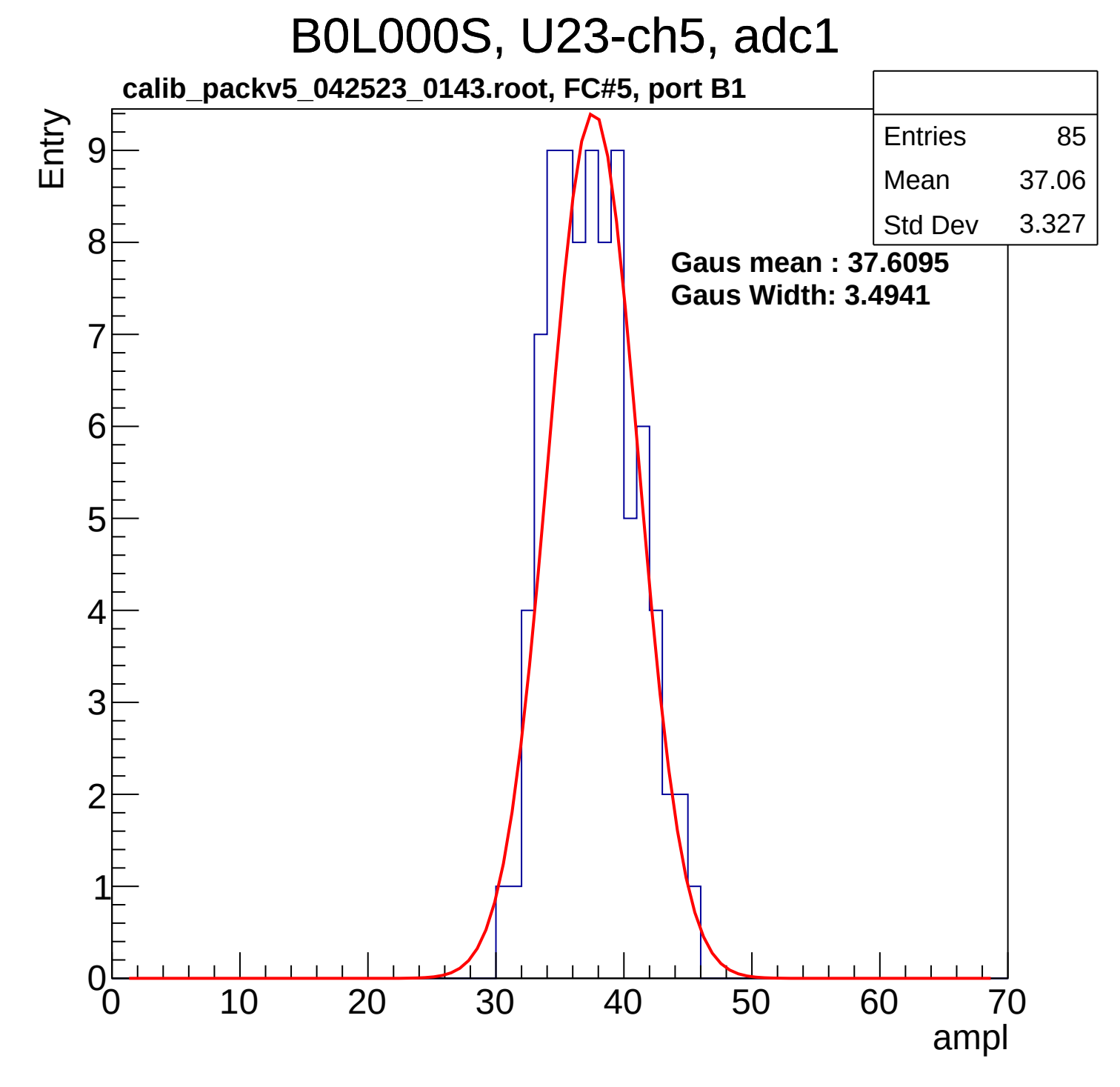

B0L000S, U23-ch4, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 54.56
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

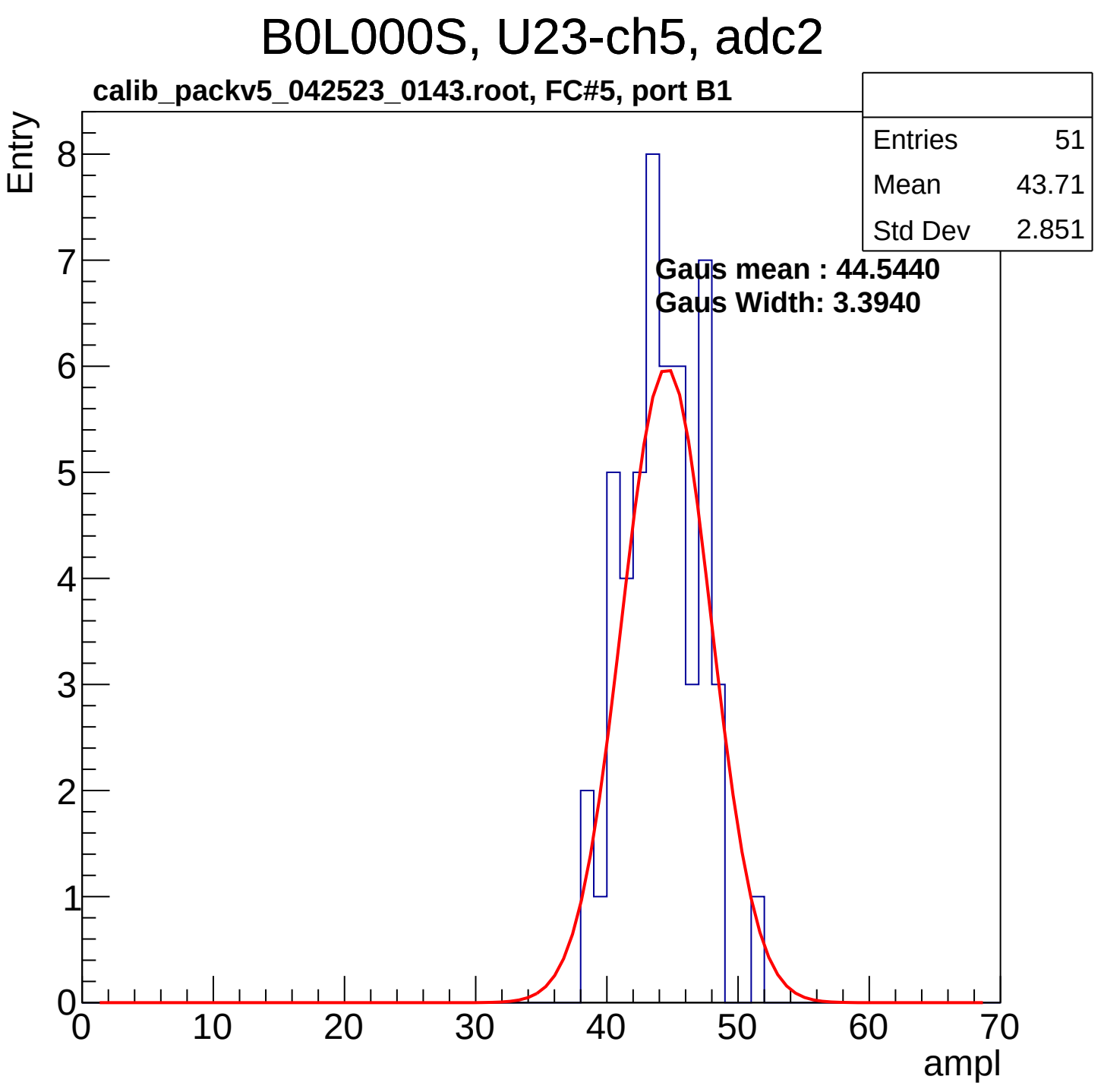

B0L000S, U23-ch4, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.63
Std Dev 2.417
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

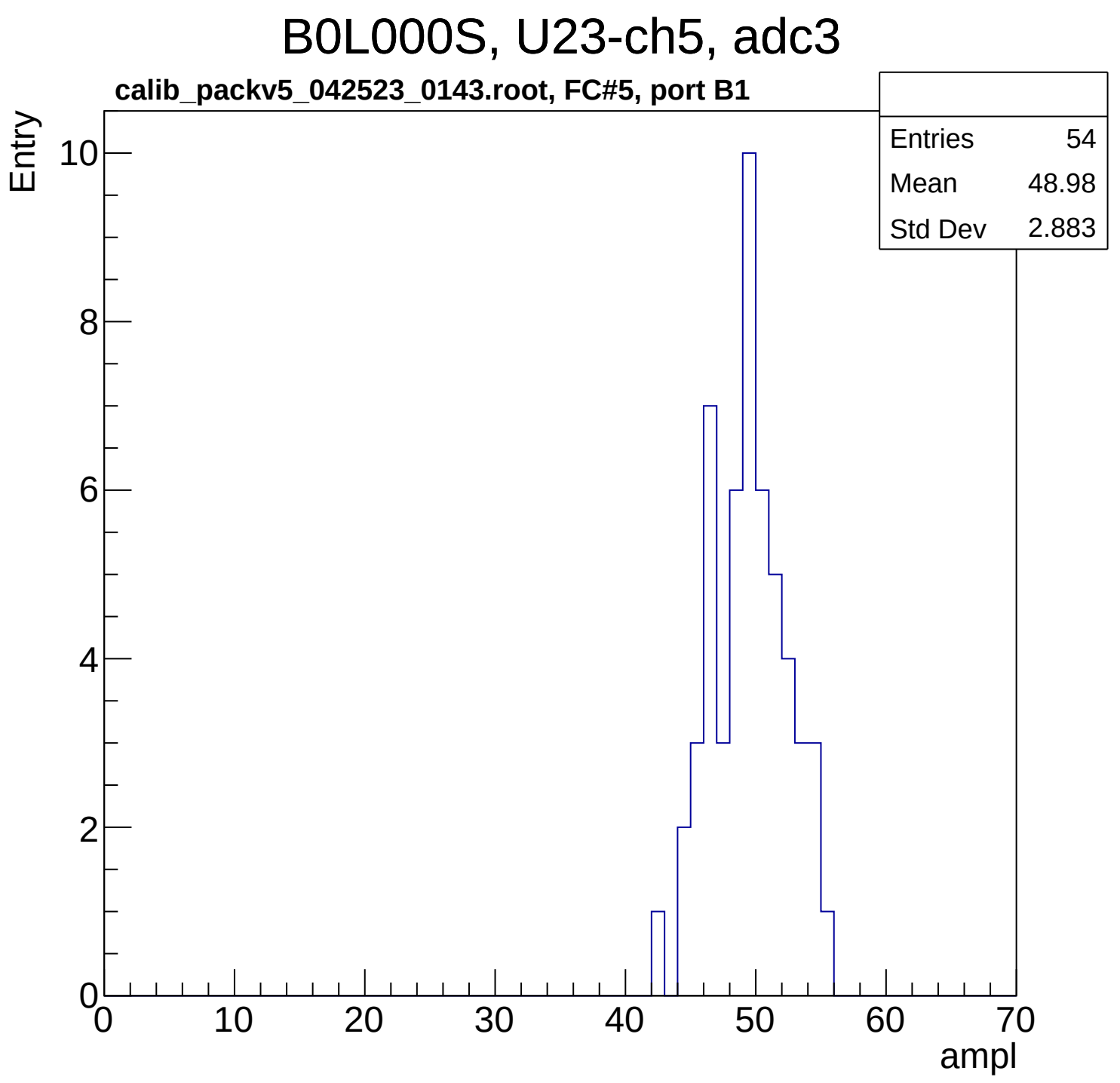

B0L000S, U23-ch4, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.71
Std Dev 16.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

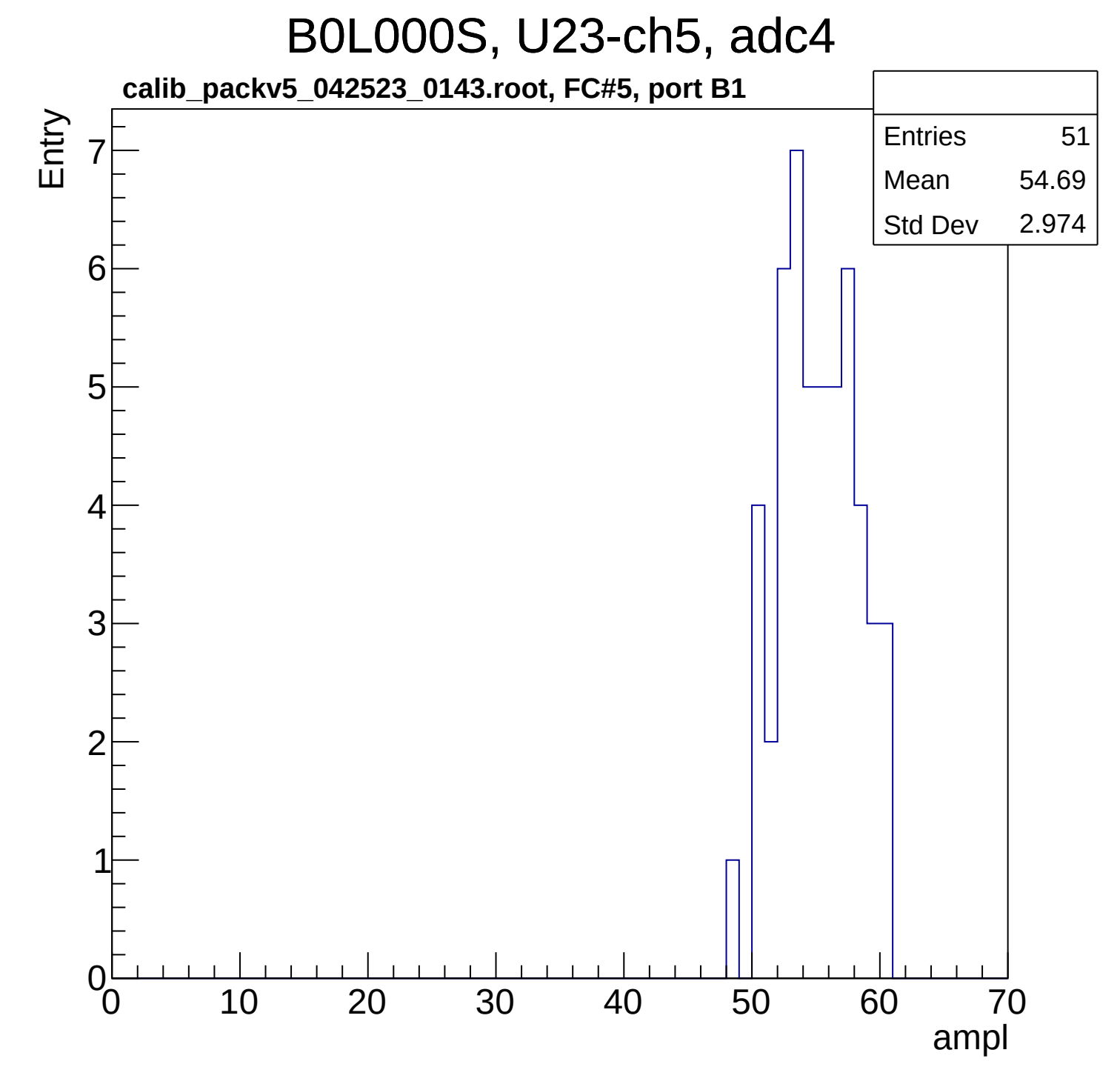

B0L000S, U23-ch4, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

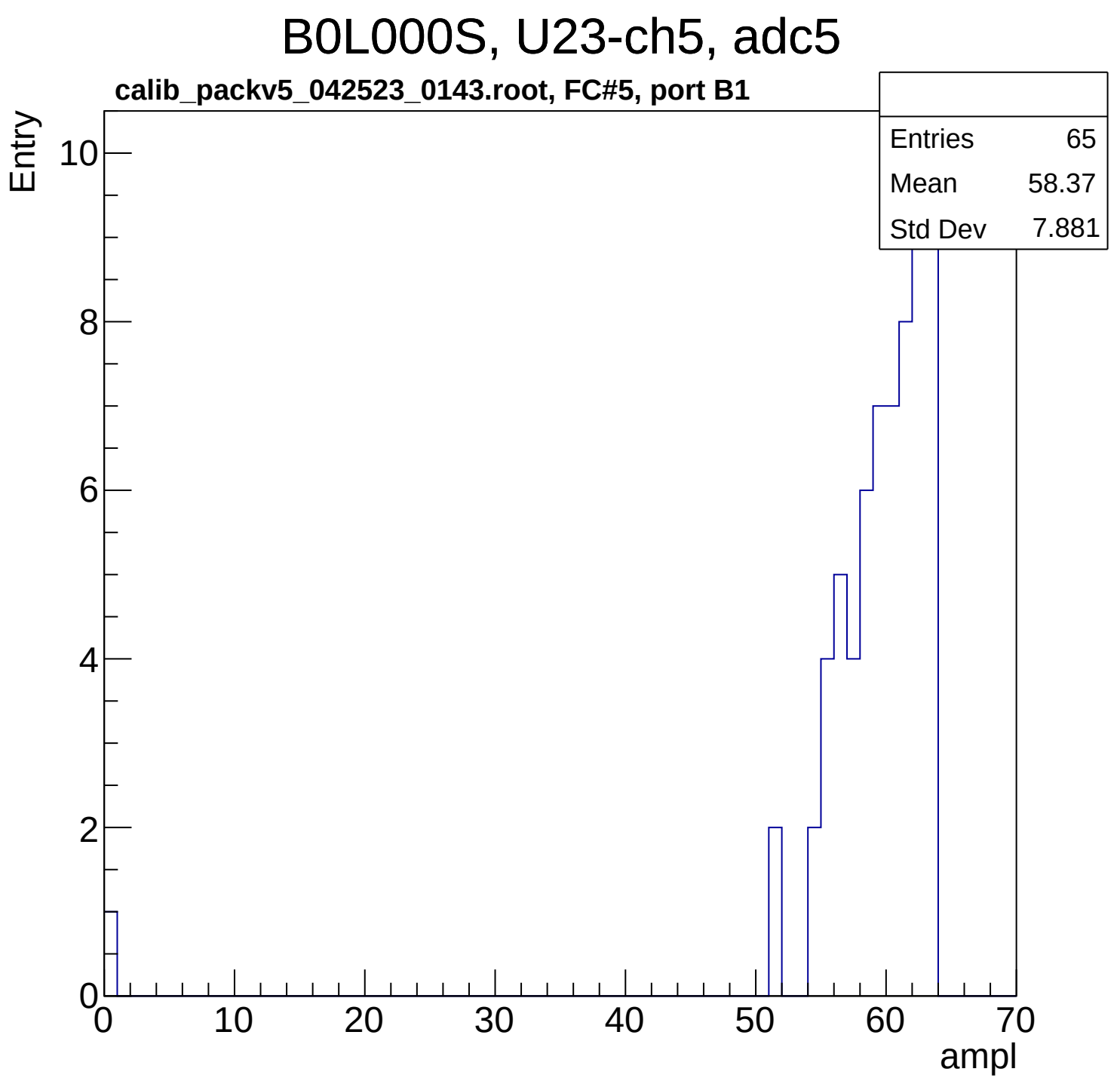

B0L000S, U23-ch5, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 28.93
Std Dev 3.301
Gaus mean : 29.2512
Gaus Width: 3.0450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

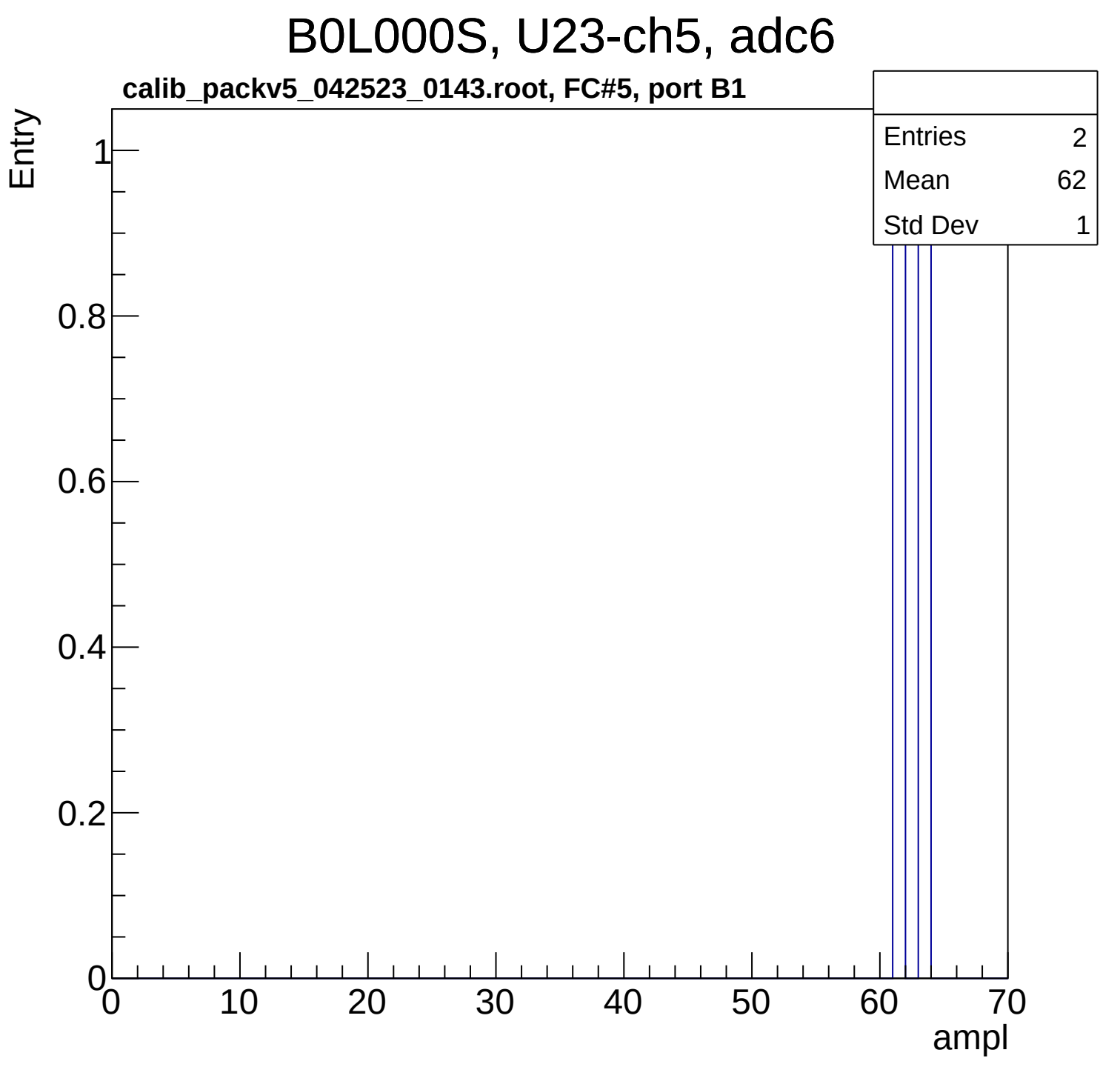

B0L000S, U23-ch5, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 37.06
Std Dev 3.327
Gaus mean : 37.6095
Gaus Width: 3.4941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

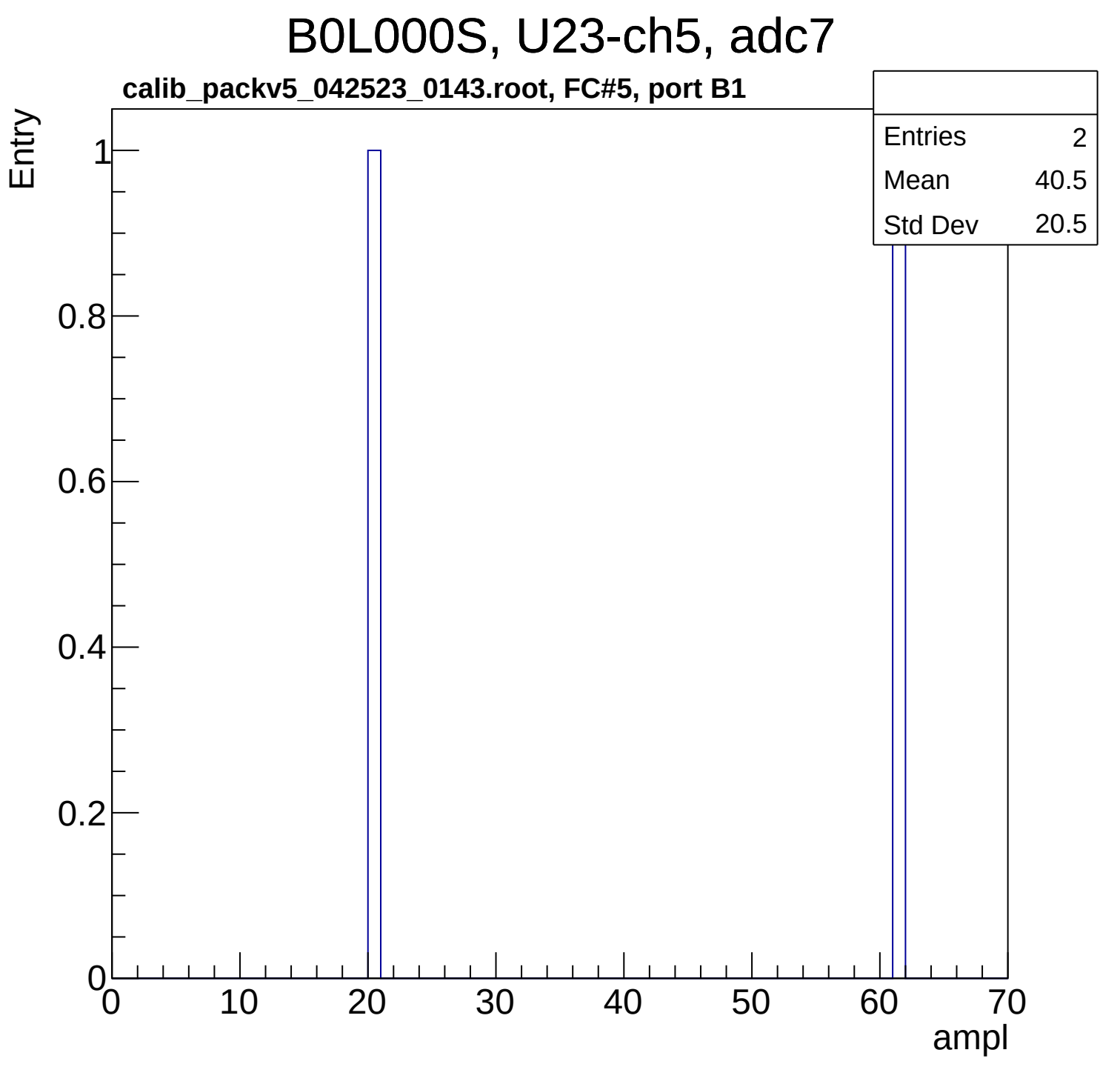

B0L000S, U23-ch5, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 43.71
Std Dev 2.851
Gaus mean : 44.5440
Gaus Width: 3.3940
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

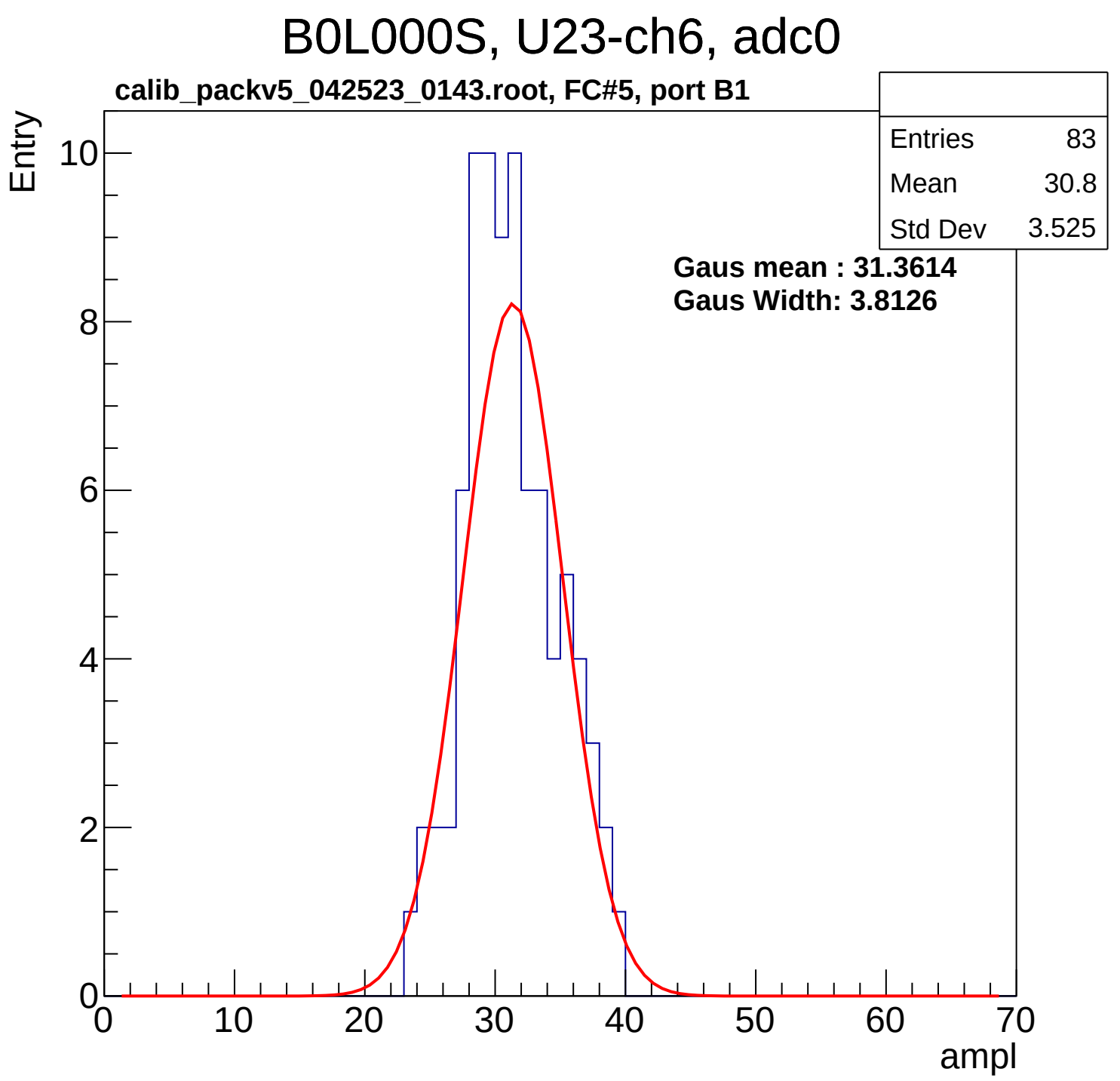

B0L000S, U23-ch5, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 48.98
Std Dev 2.883
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

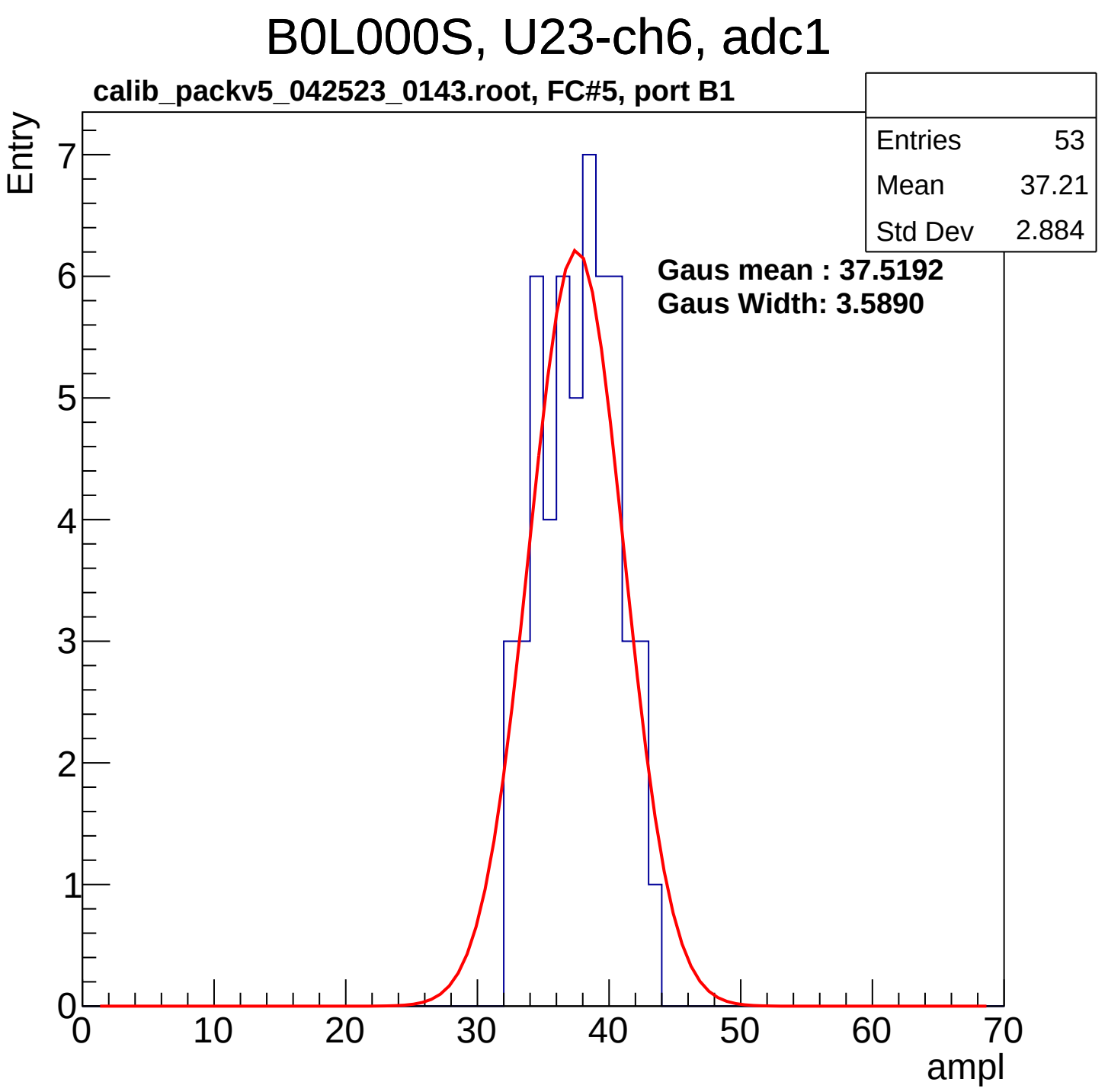

B0L000S, U23-ch5, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.69
Std Dev 2.974
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

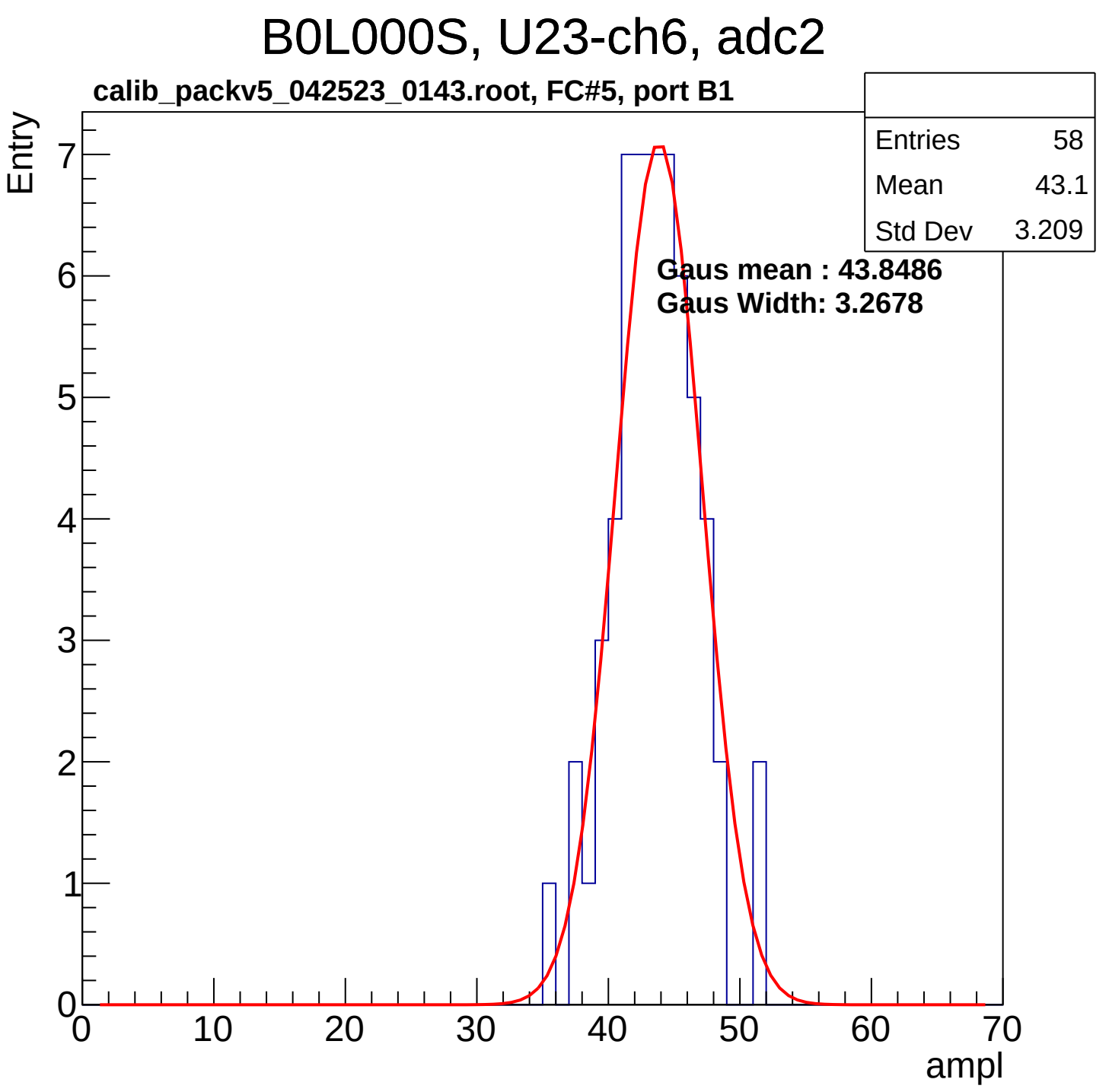

B0L000S, U23-ch5, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 58.37
Std Dev 7.881
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

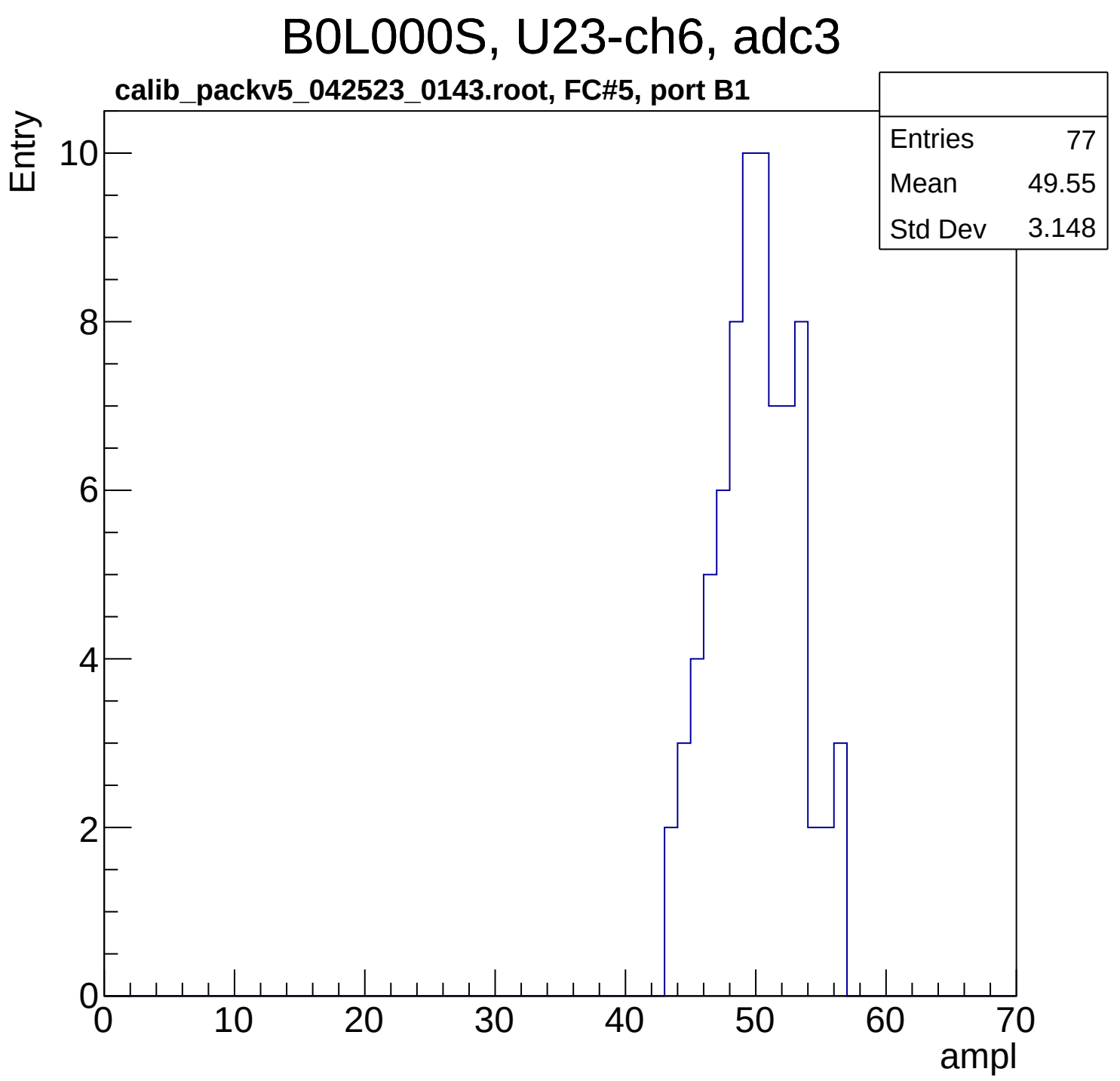

B0L000S, U23-ch5, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

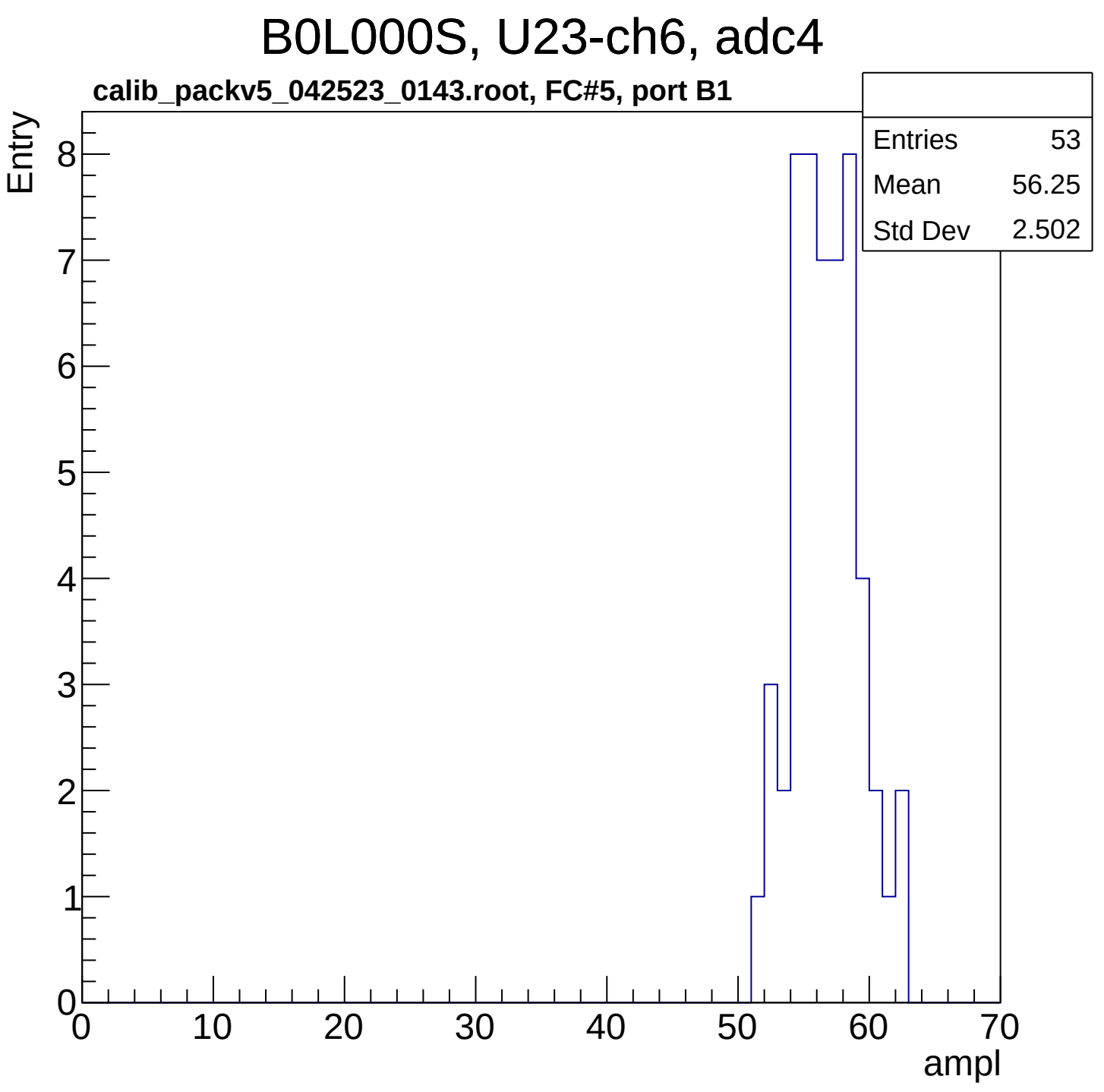

B0L000S, U23-ch5, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 40.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

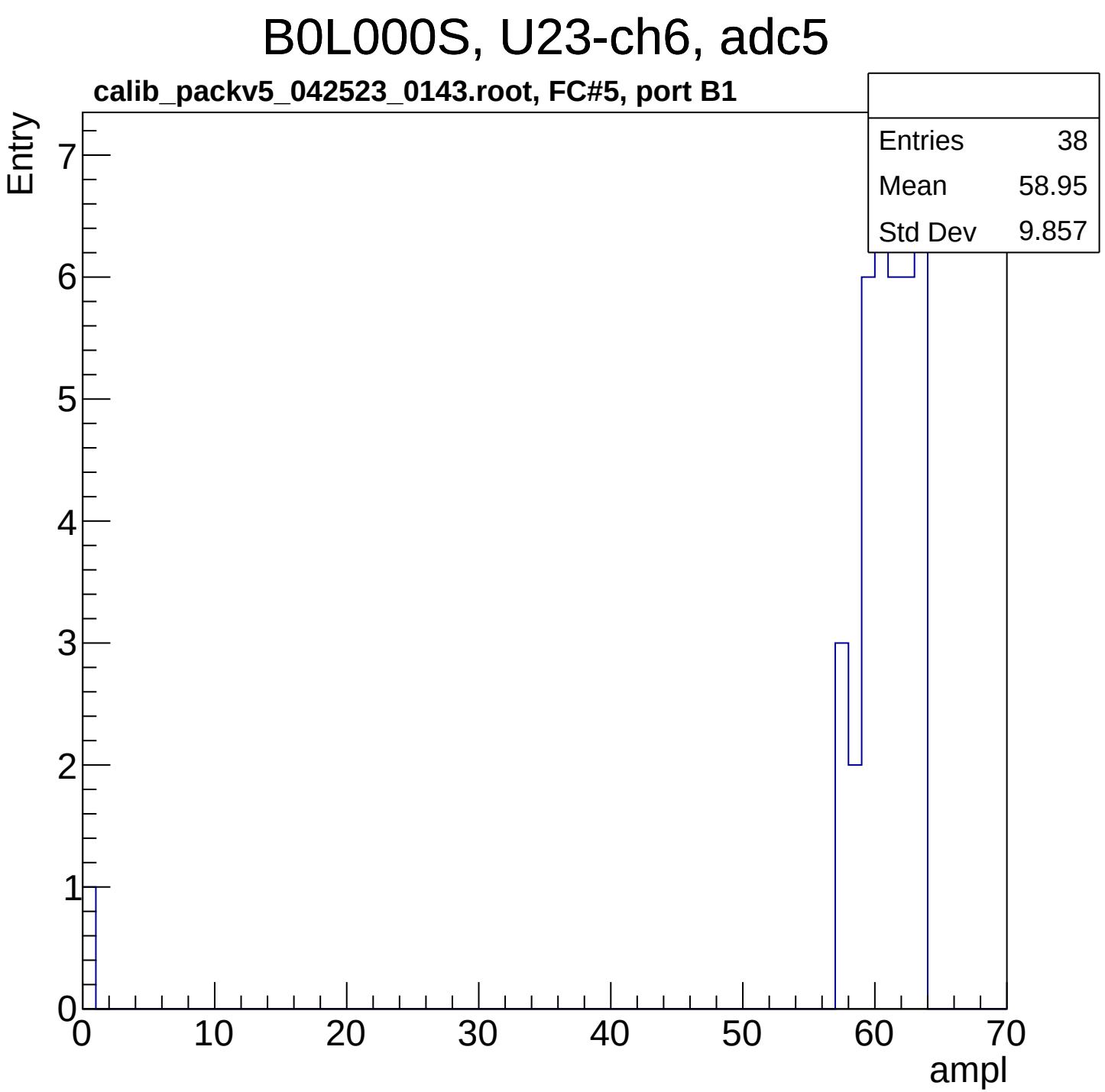

B0L000S, U23-ch6, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 30.8
Std Dev 3.525
Gaus mean : 31.3614
Gaus Width: 3.8126
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

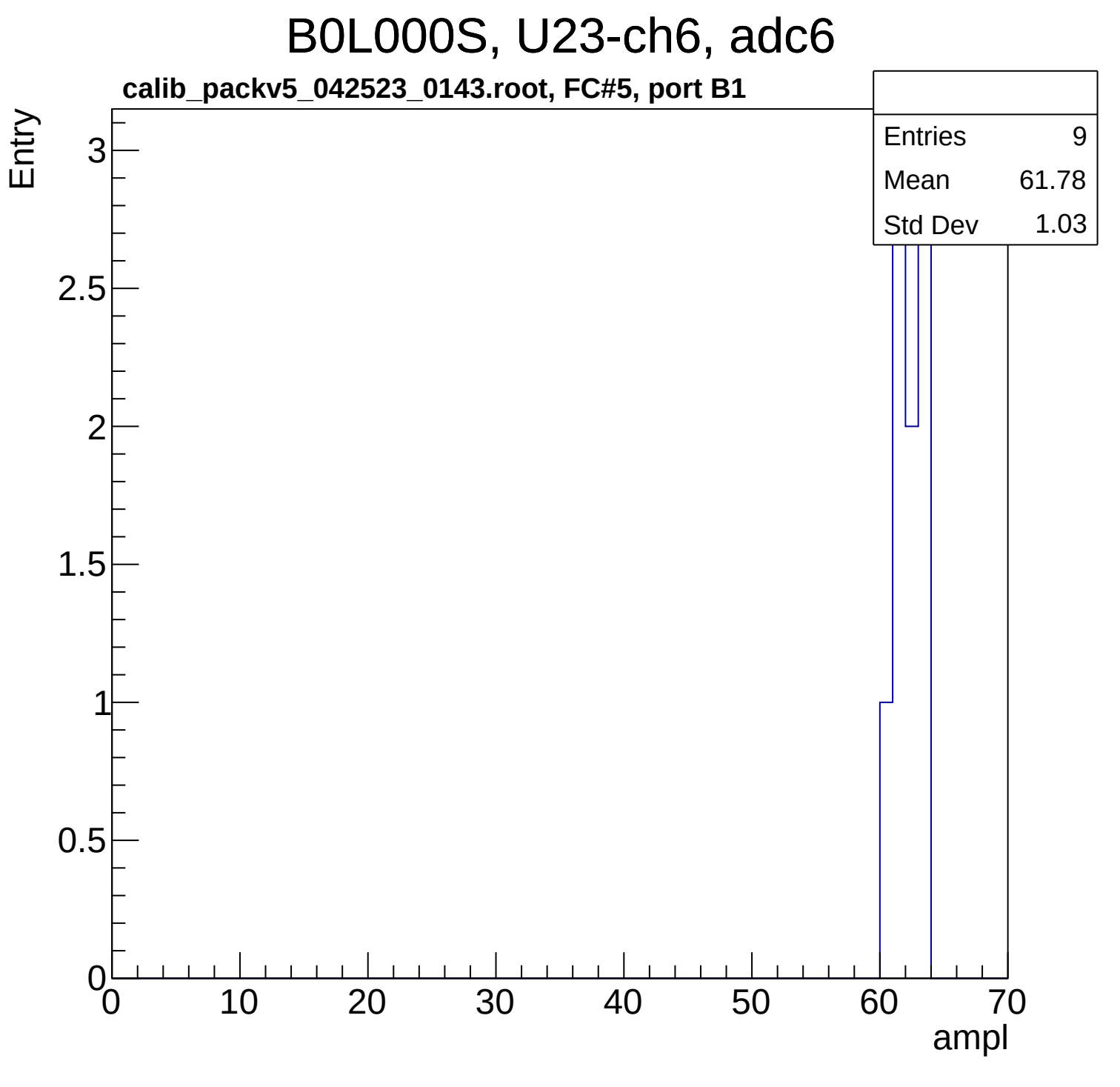

B0L000S, U23-ch6, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 37.21
Std Dev 2.884
Gaus mean : 37.5192
Gaus Width: 3.5890
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

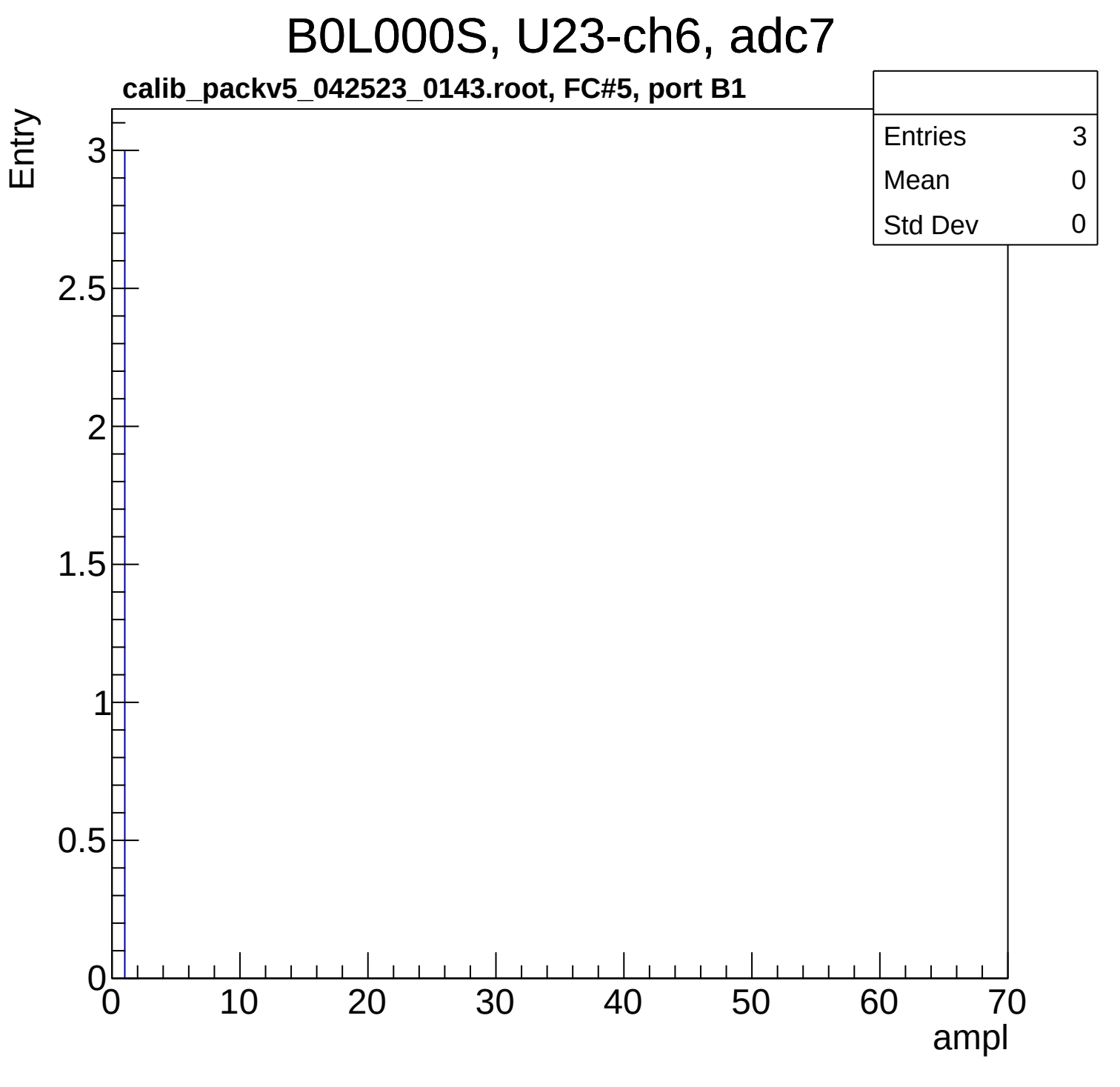

B0L000S, U23-ch6, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.1
Std Dev 3.209
Gaus mean : 43.8486
Gaus Width: 3.2678
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

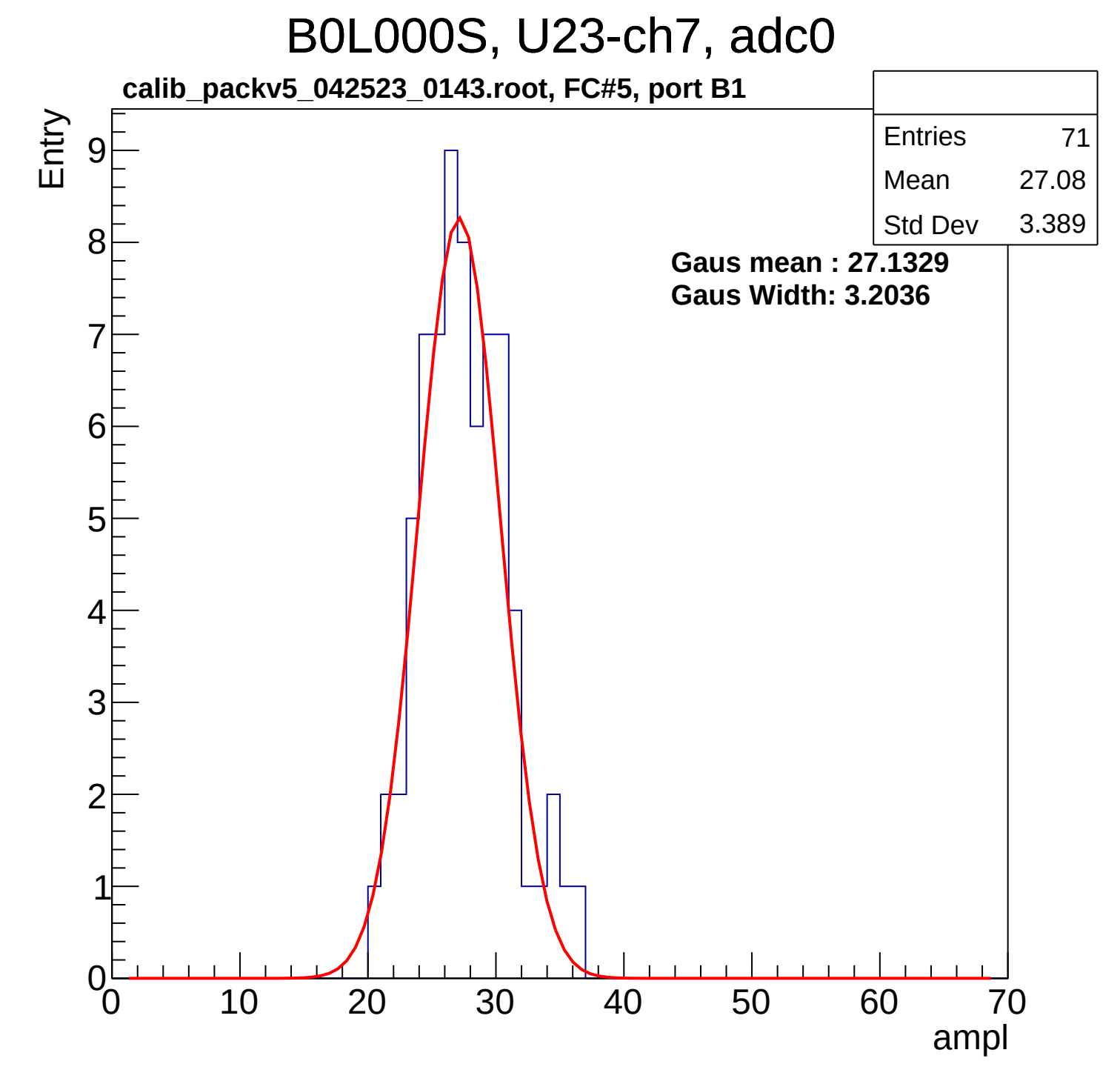

B0L000S, U23-ch6, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 49.55
Std Dev 3.148
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

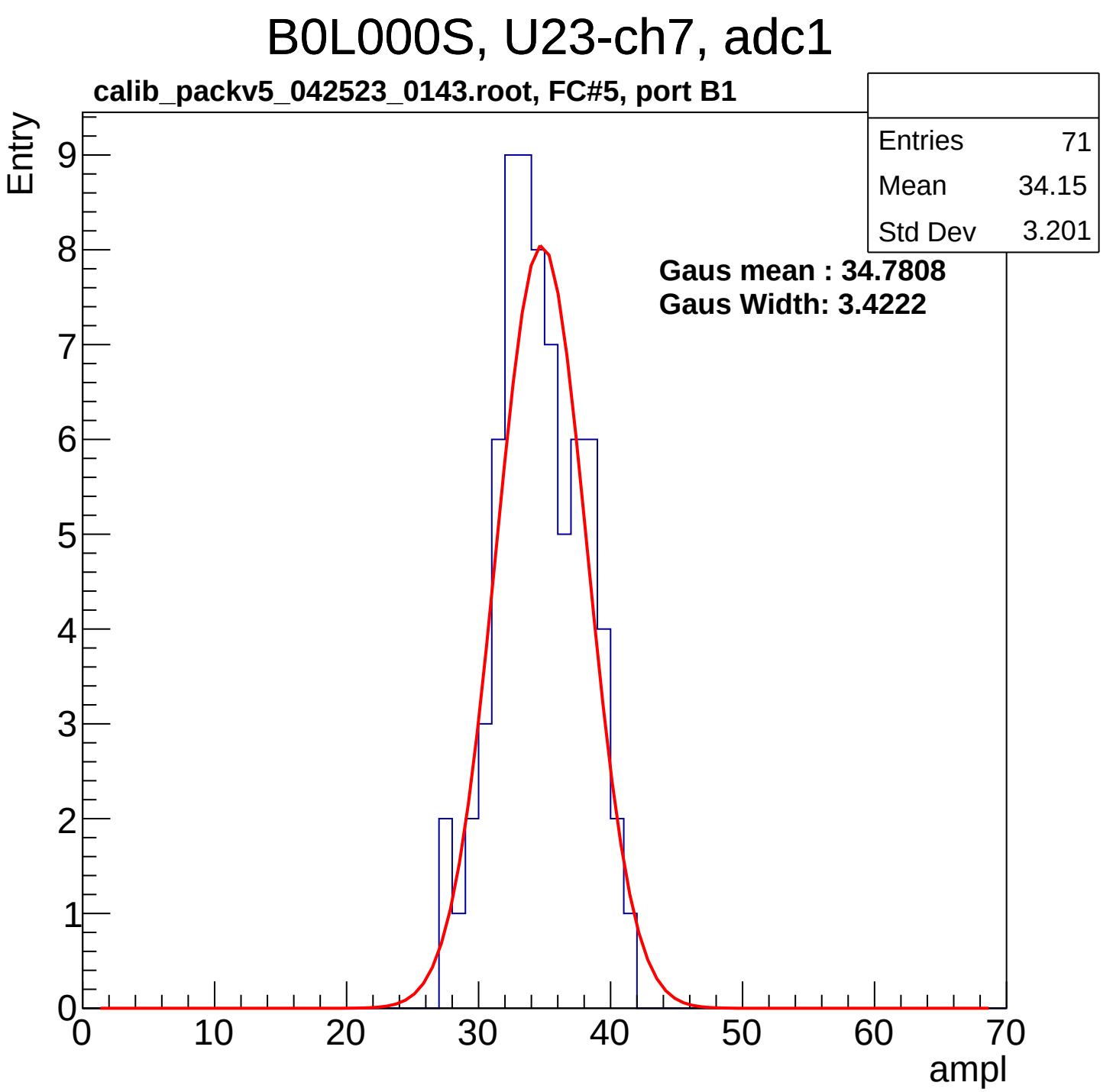

B0L000S, U23-ch6, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.25
Std Dev 2.502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

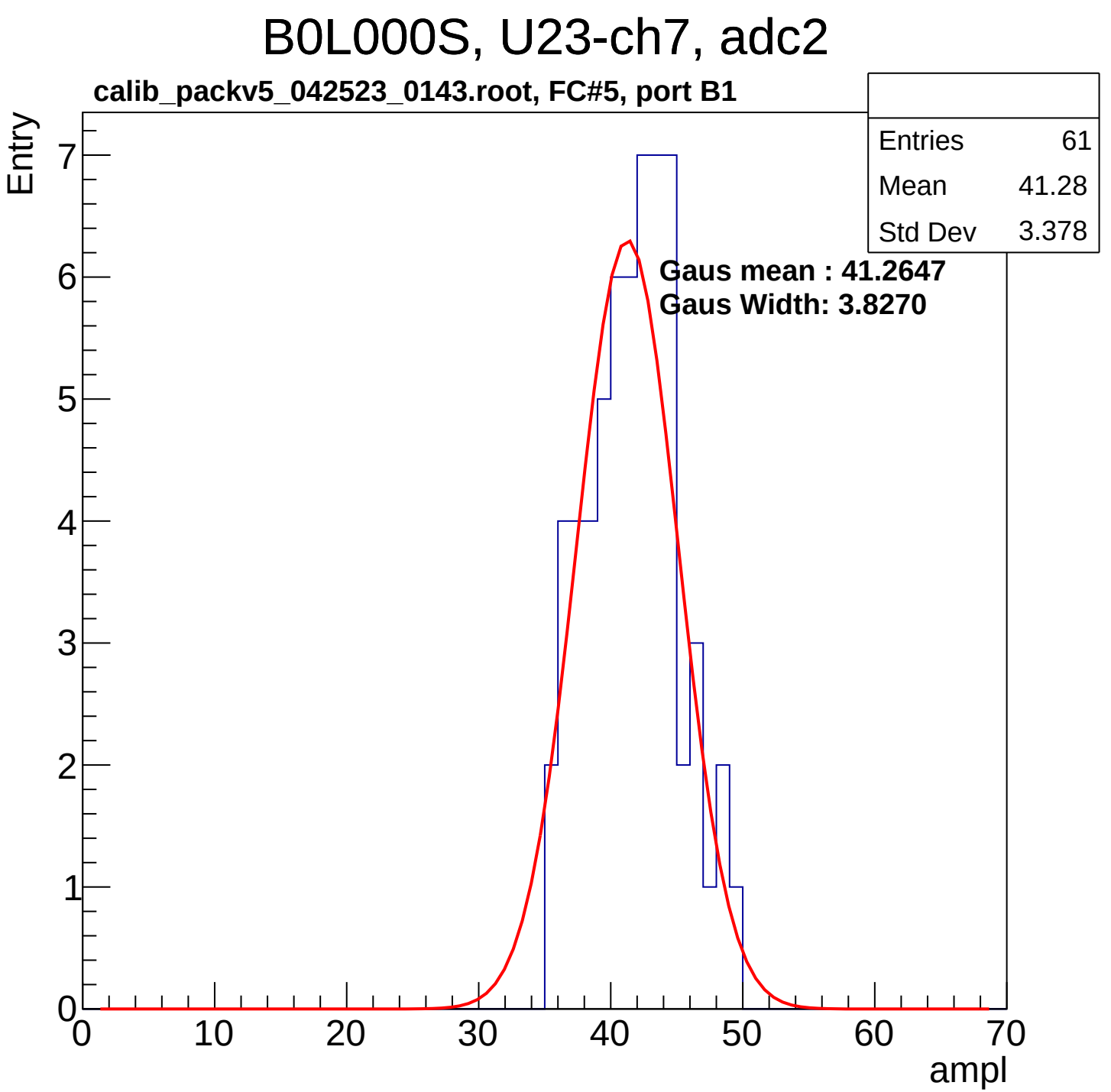

B0L000S, U23-ch6, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.95
Std Dev 9.857
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

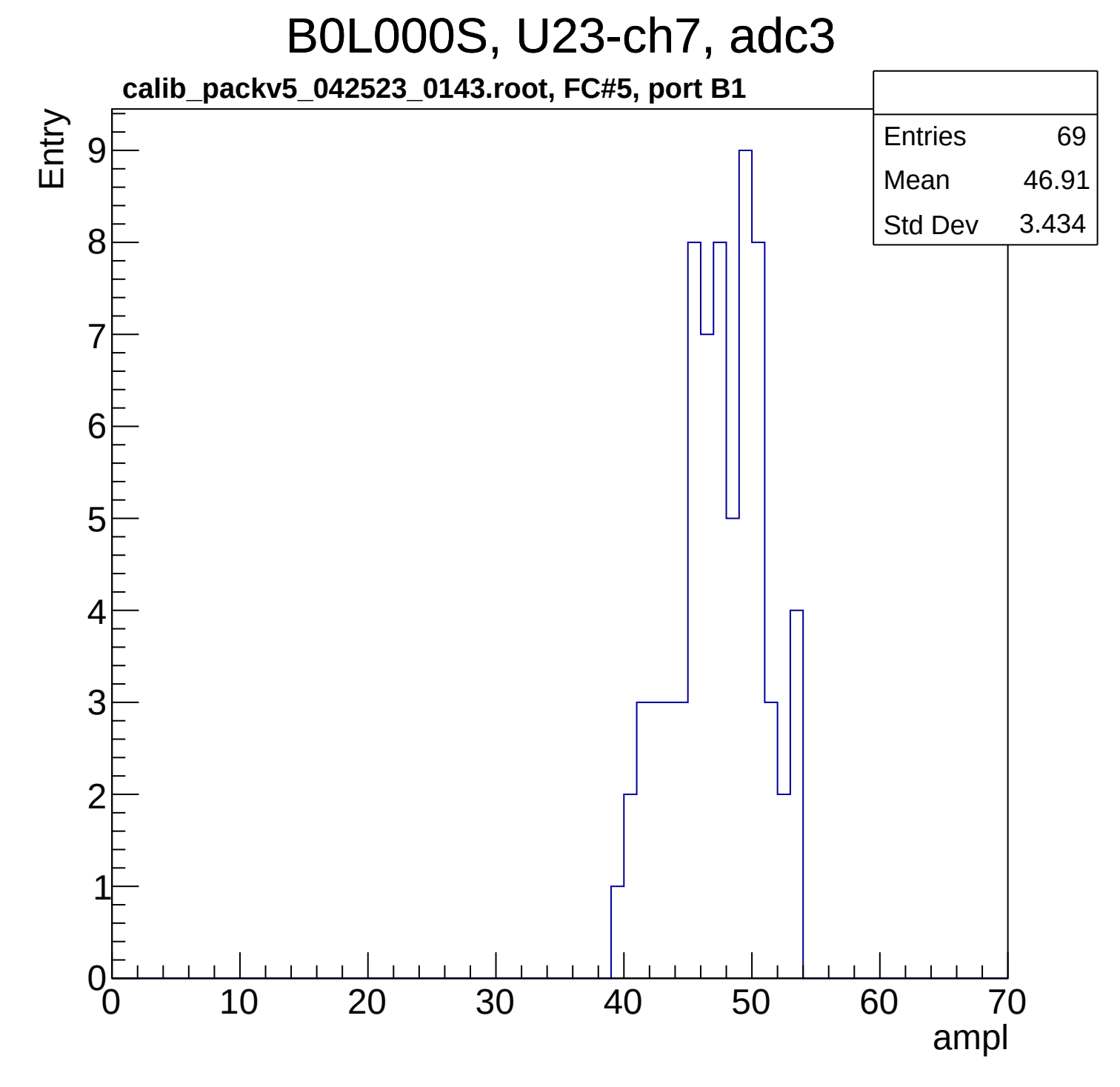

B0L000S, U23-ch6, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.78
Std Dev 1.03
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

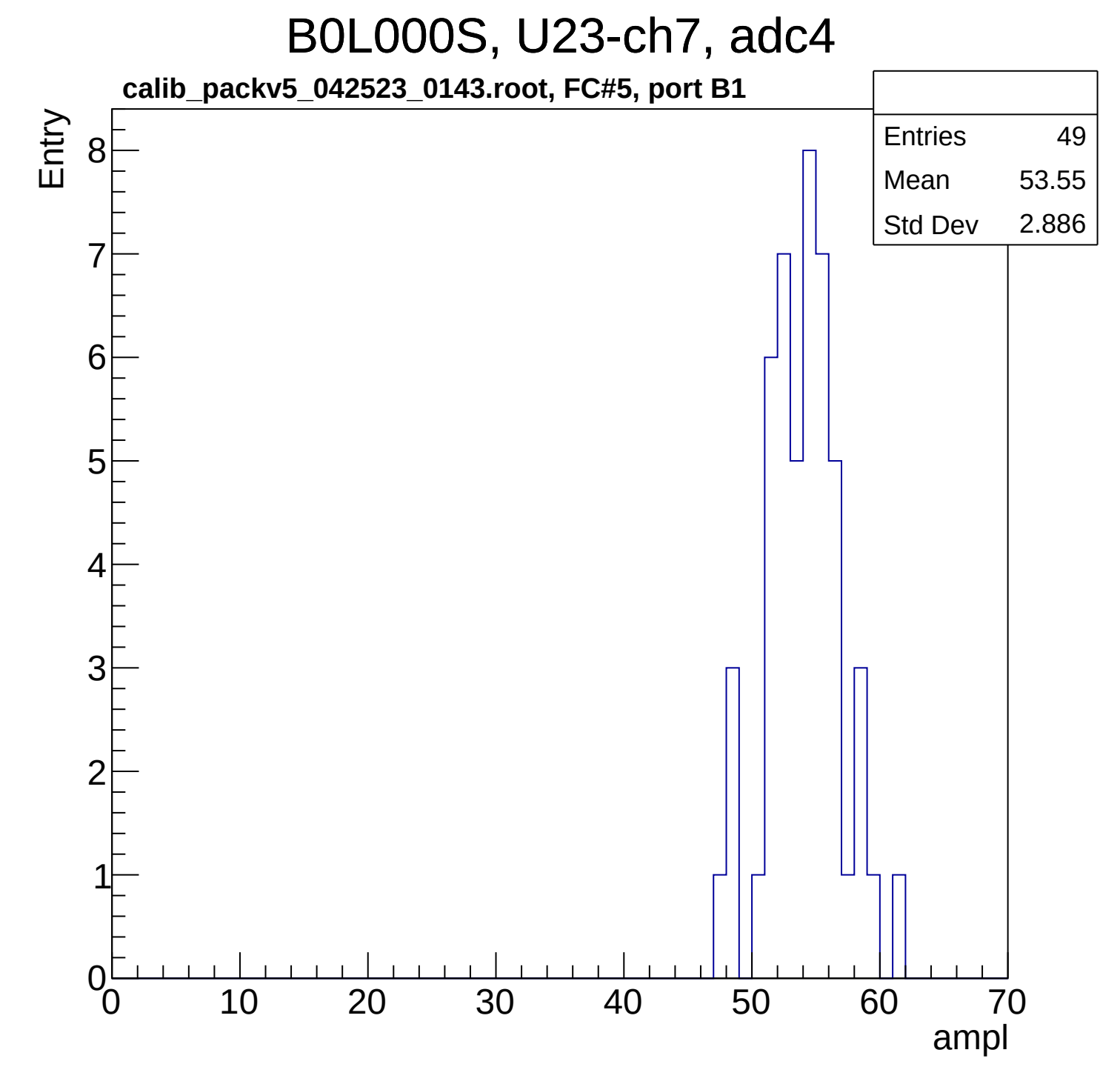

B0L000S, U23-ch6, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

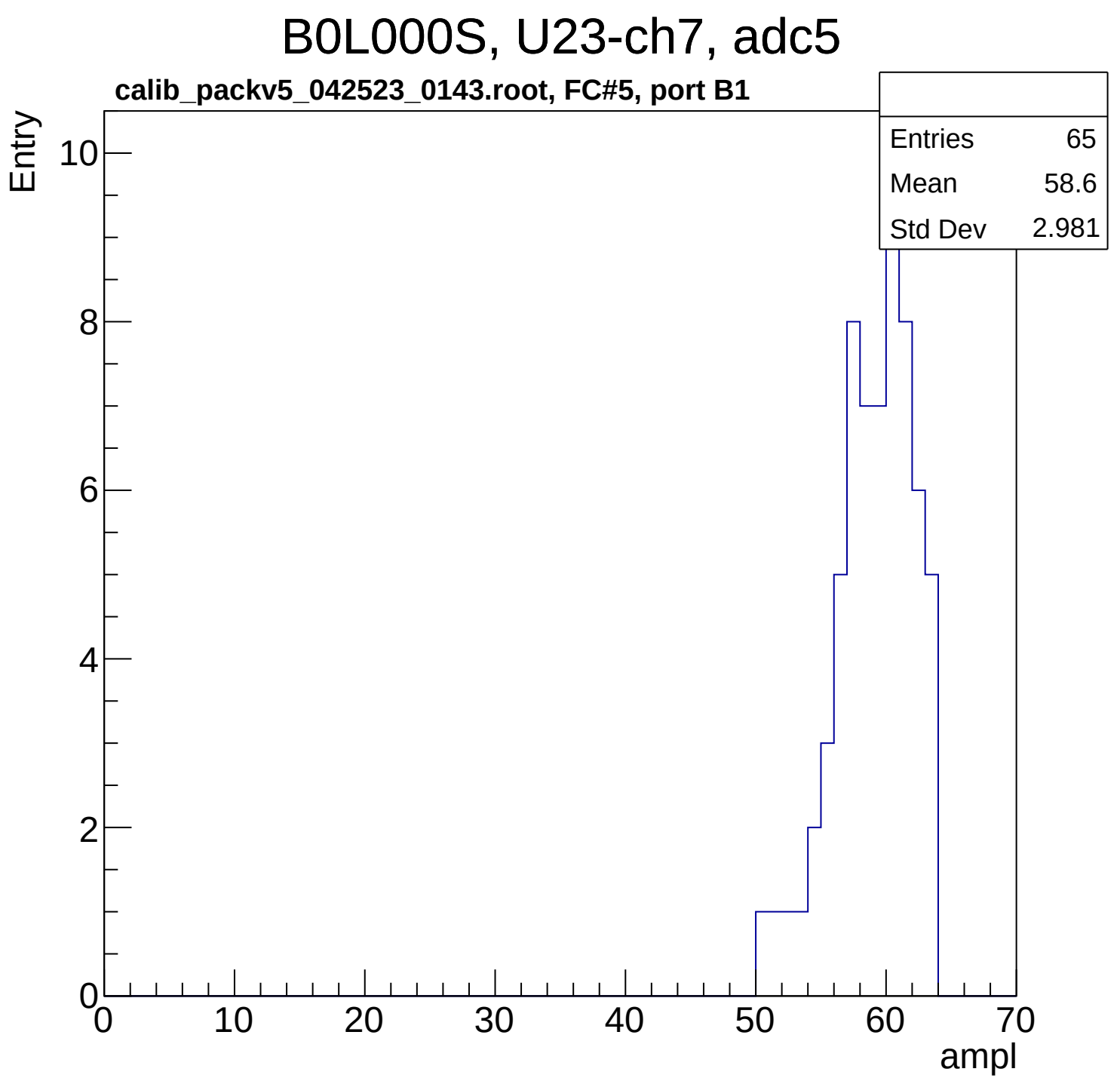

B0L000S, U23-ch7, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 27.08
Std Dev 3.389
Gaus mean : 27.1329
Gaus Width: 3.2036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

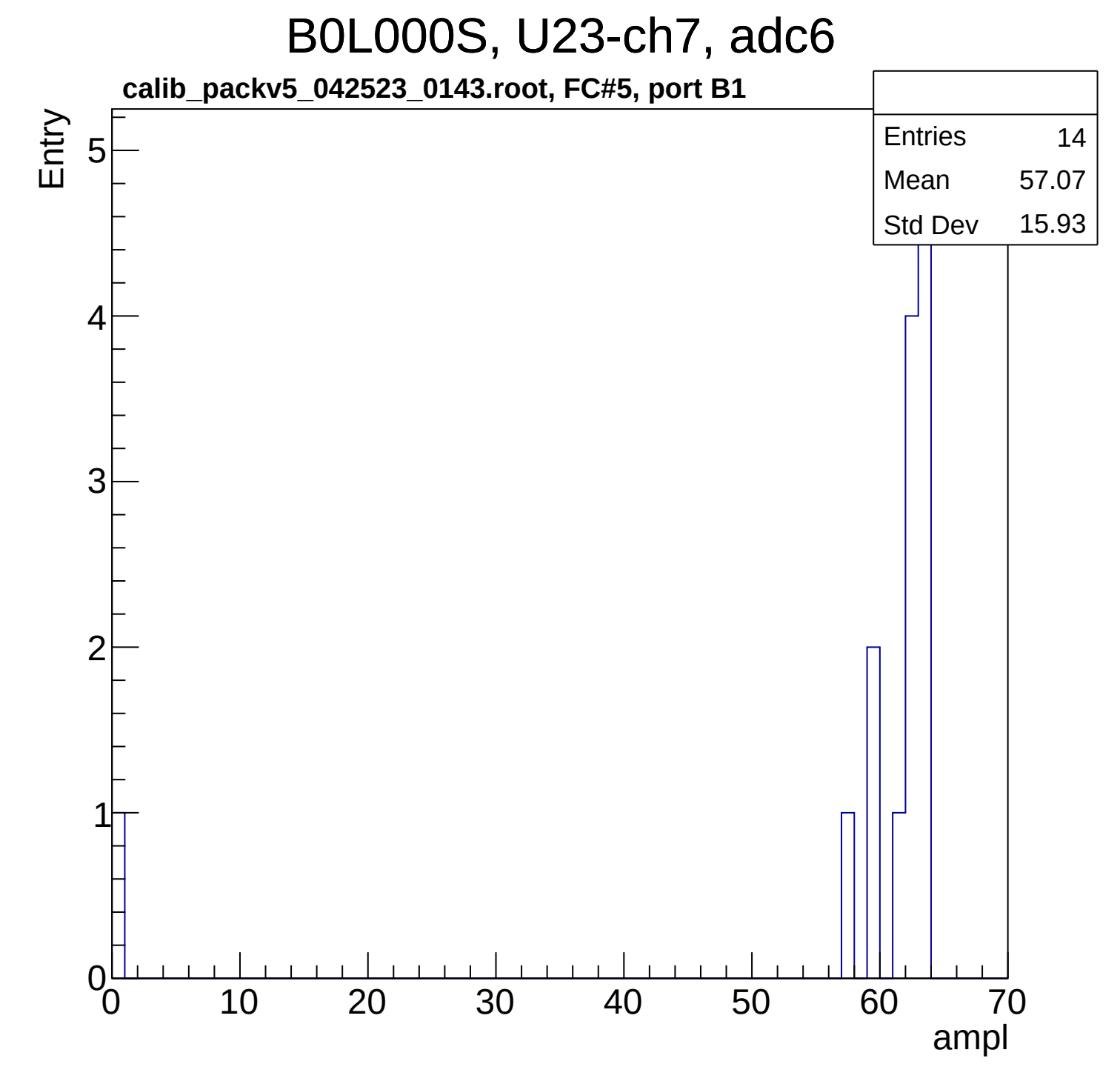

B0L000S, U23-ch7, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 34.15
Std Dev 3.201
Gaus mean : 34.7808
Gaus Width: 3.4222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

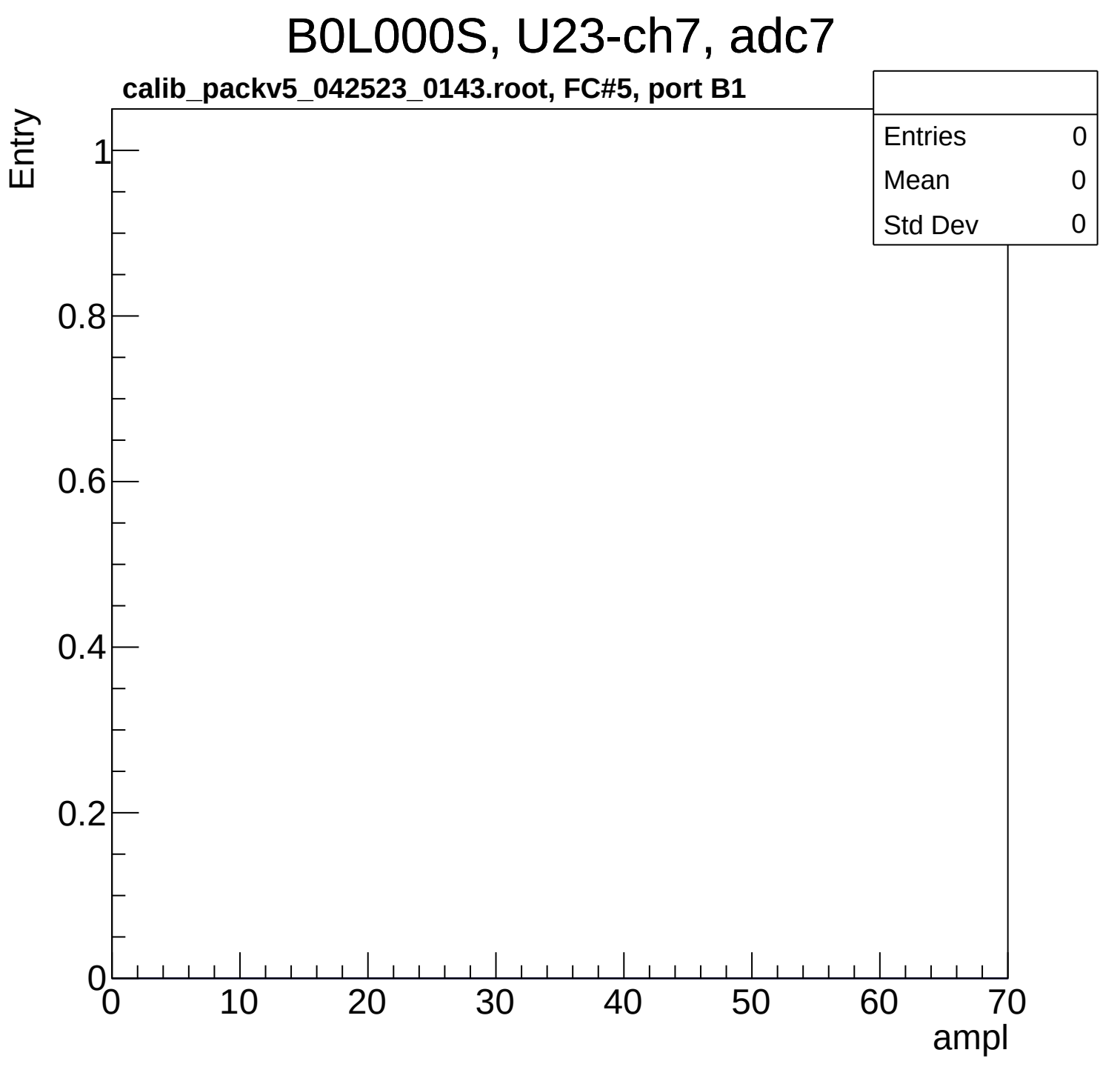

B0L000S, U23-ch7, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 41.28
Std Dev 3.378
Gaus mean : 41.2647
Gaus Width: 3.8270
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

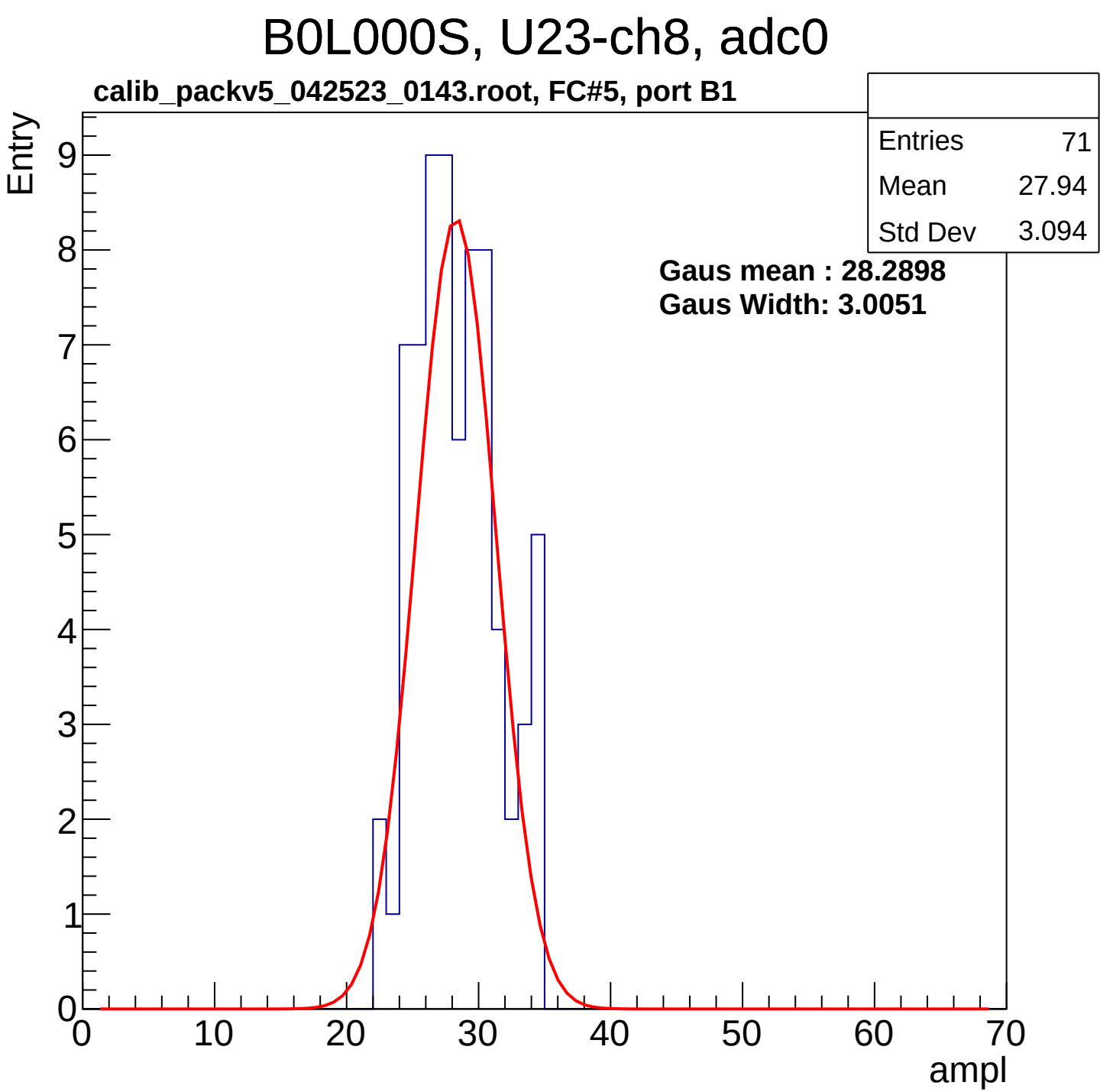

B0L000S, U23-ch7, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 46.91
Std Dev 3.434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

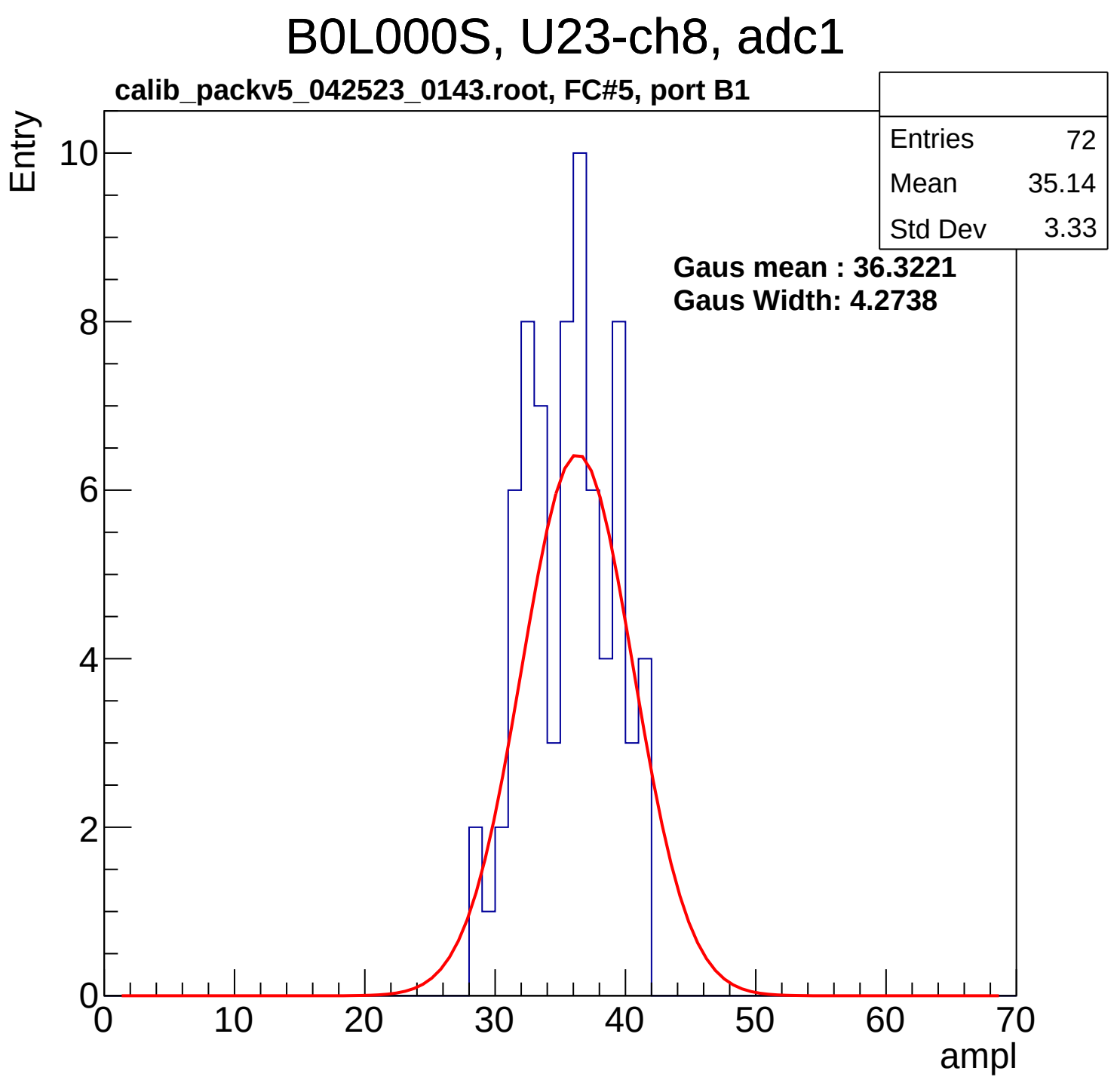

B0L000S, U23-ch7, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 53.55
Std Dev 2.886
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

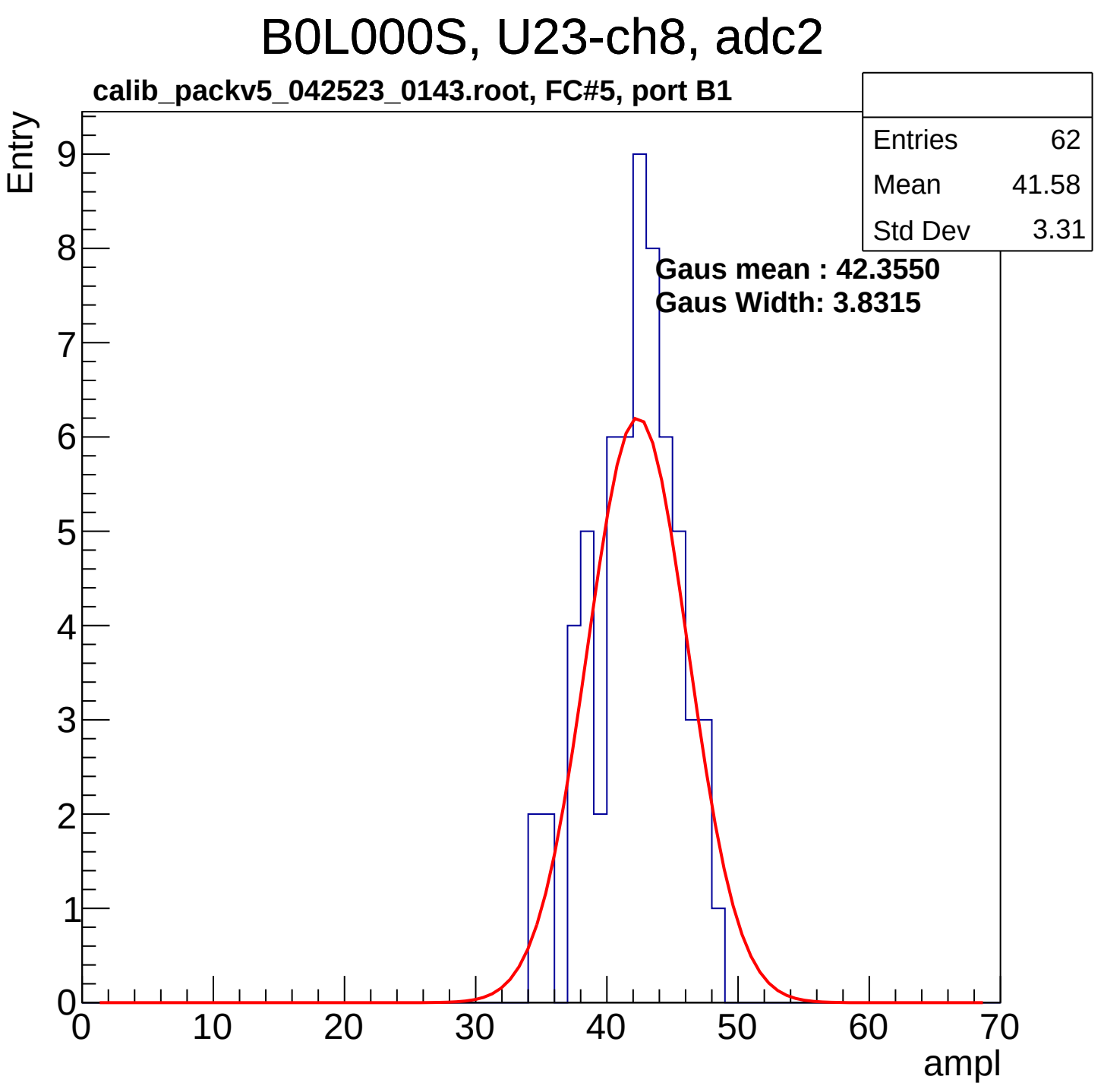

B0L000S, U23-ch7, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 58.6
Std Dev 2.981
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

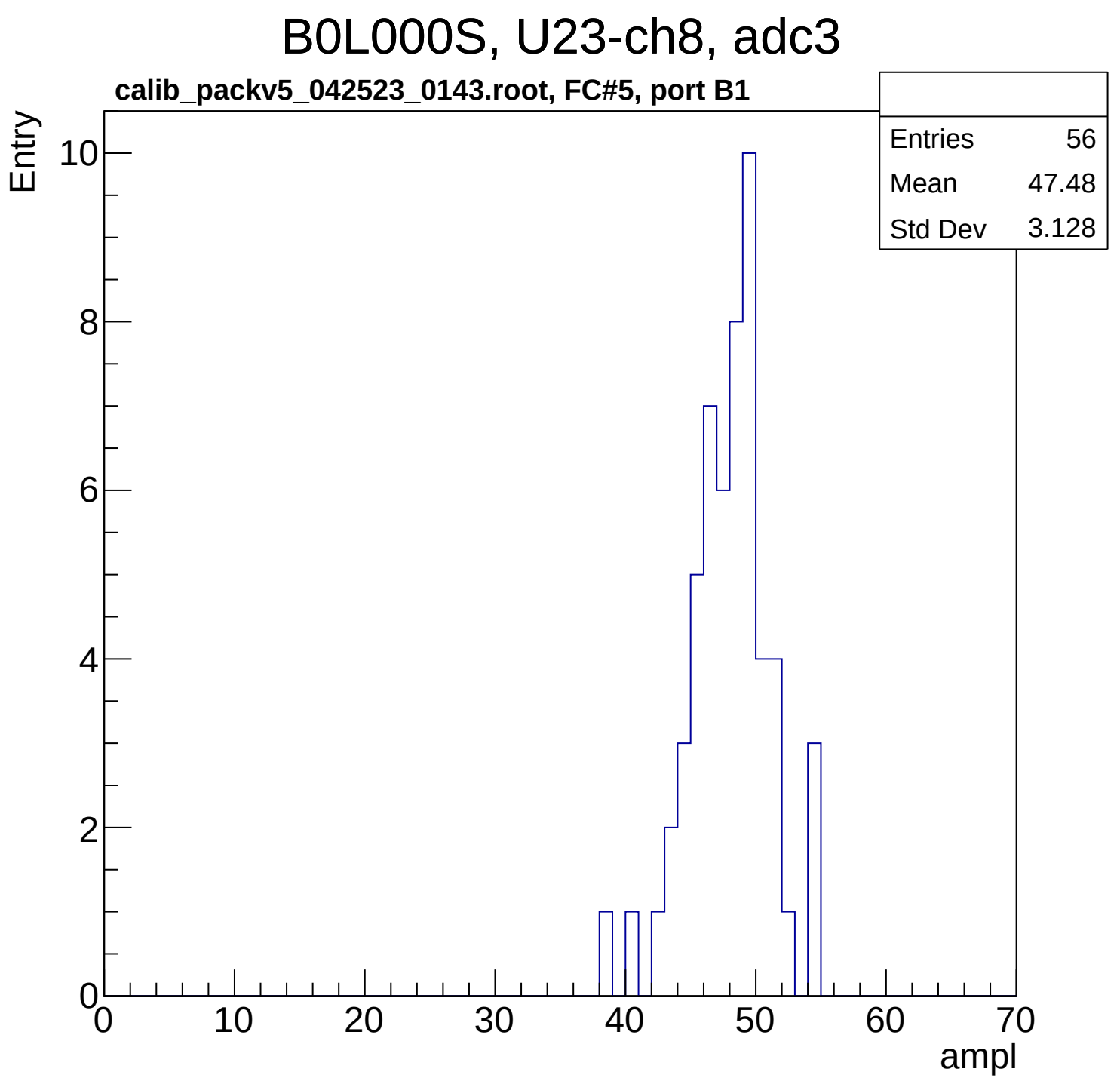

B0L000S, U23-ch7, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.07
Std Dev 15.93
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

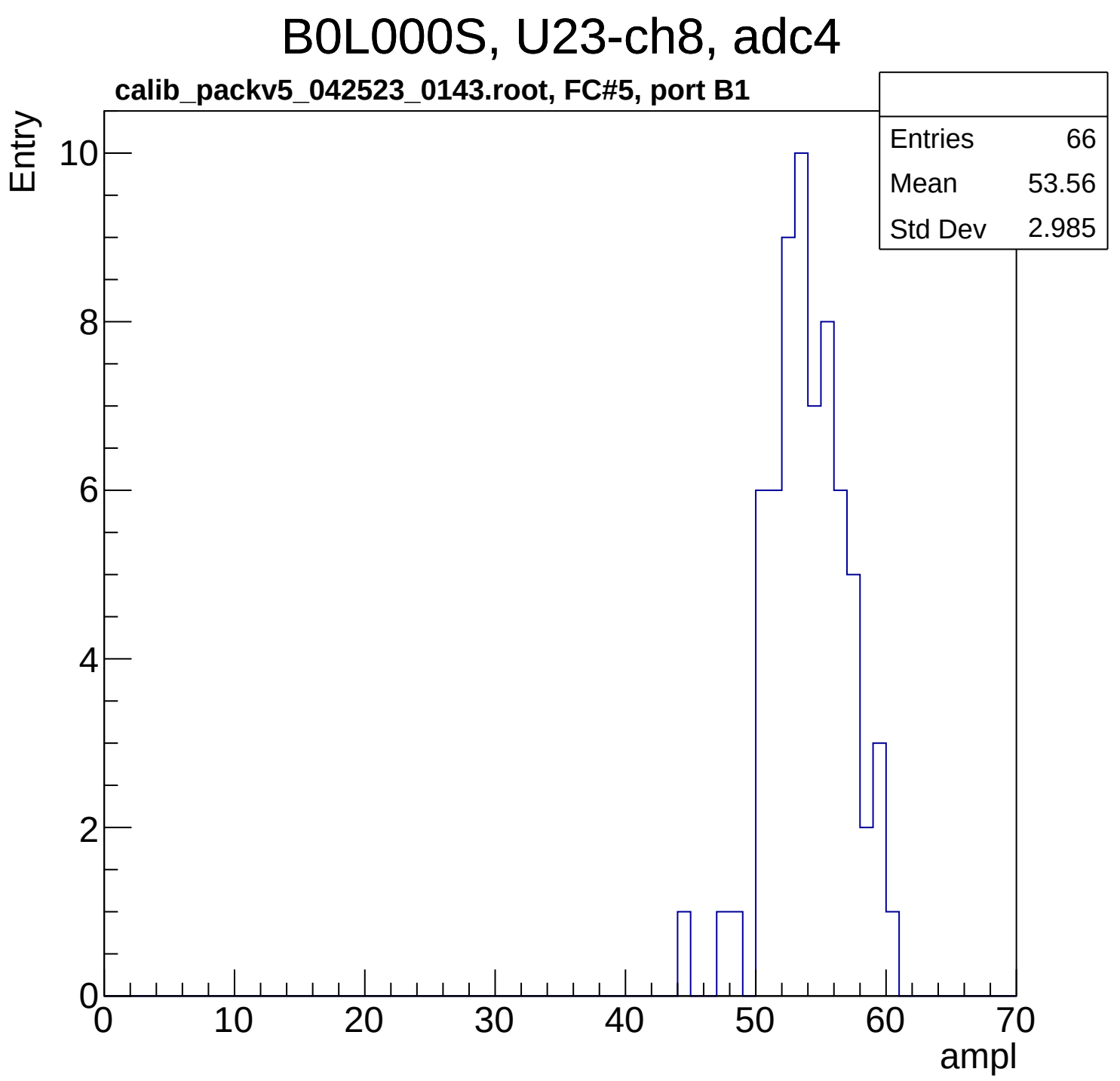

B0L000S, U23-ch7, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

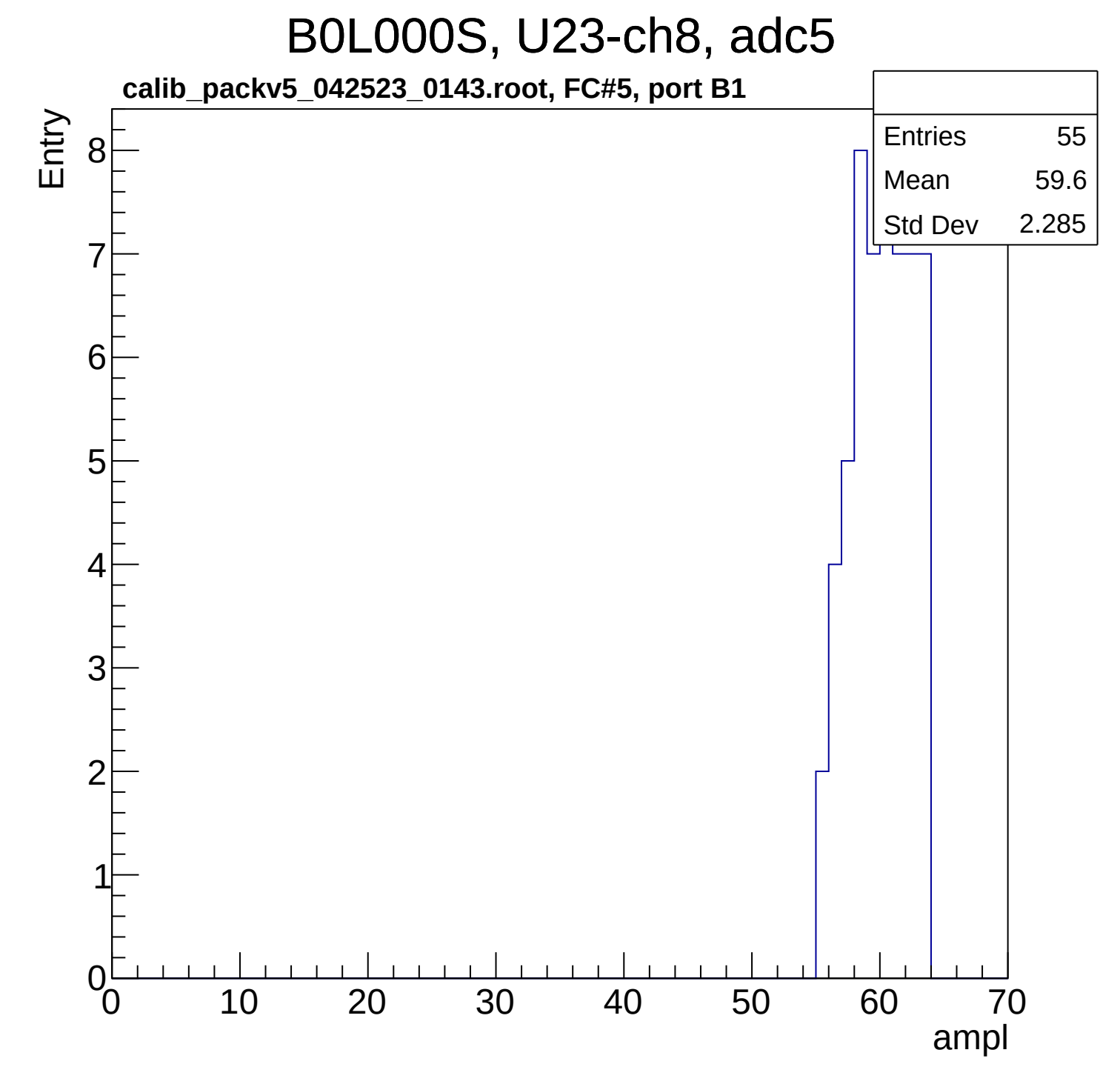

B0L000S, U23-ch8, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 27.94
Std Dev 3.094
Gaus mean : 28.2898
Gaus Width: 3.0051
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

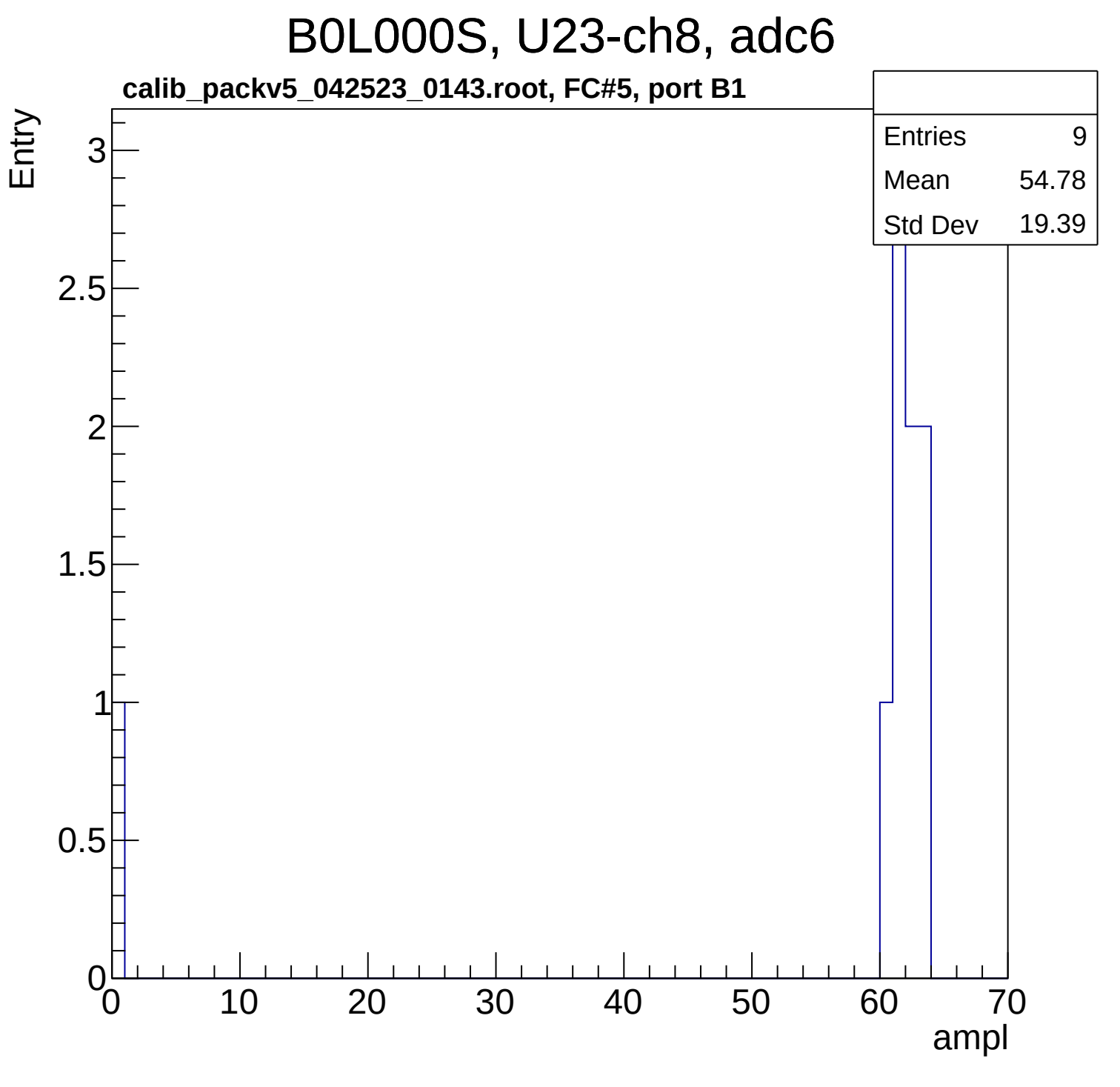

B0L000S, U23-ch8, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.14
Std Dev 3.33
Gaus mean : 36.3221
Gaus Width: 4.2738
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

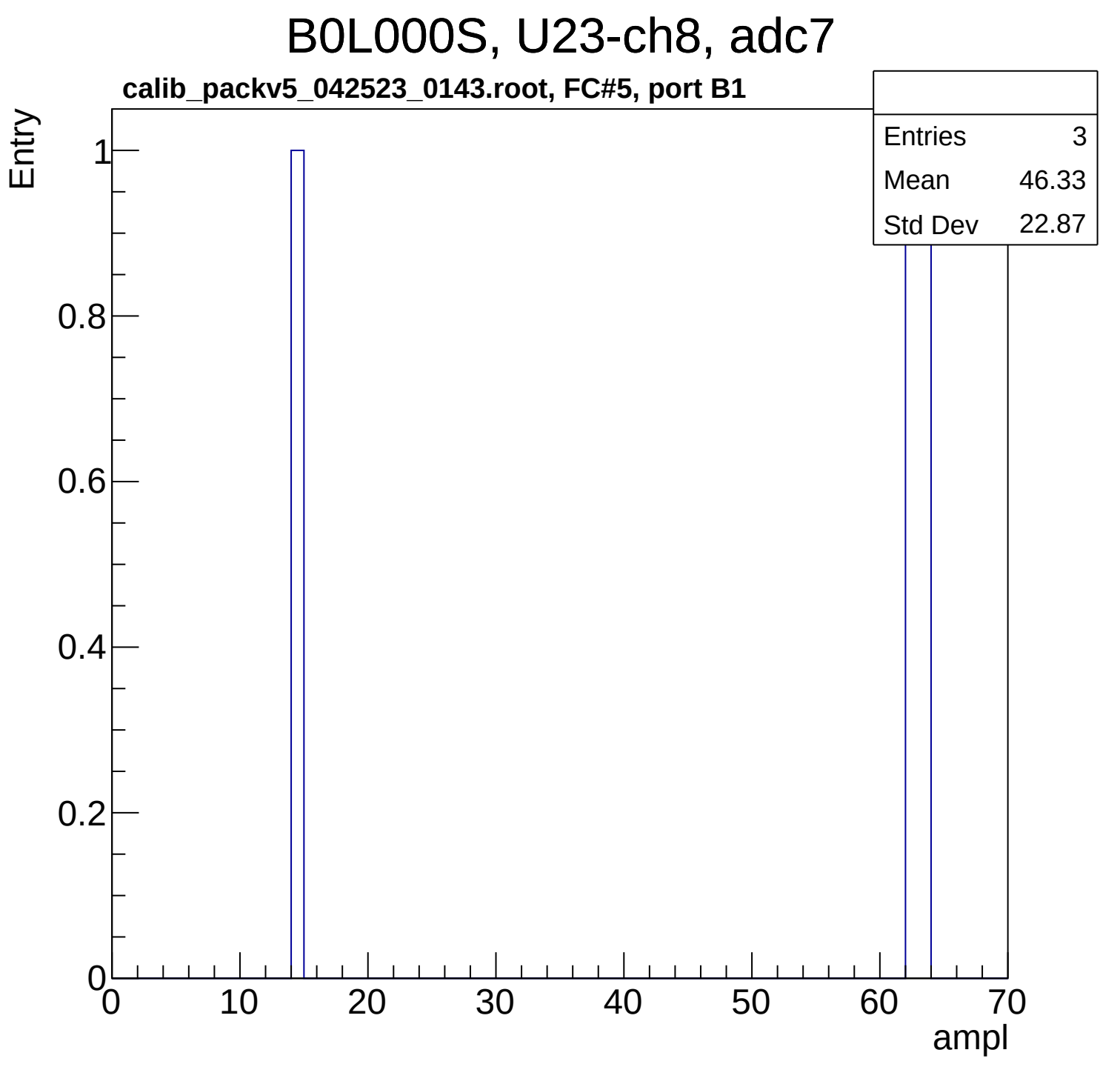

B0L000S, U23-ch8, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 41.58
Std Dev 3.31
Gaus mean : 42.3550
Gaus Width: 3.8315
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

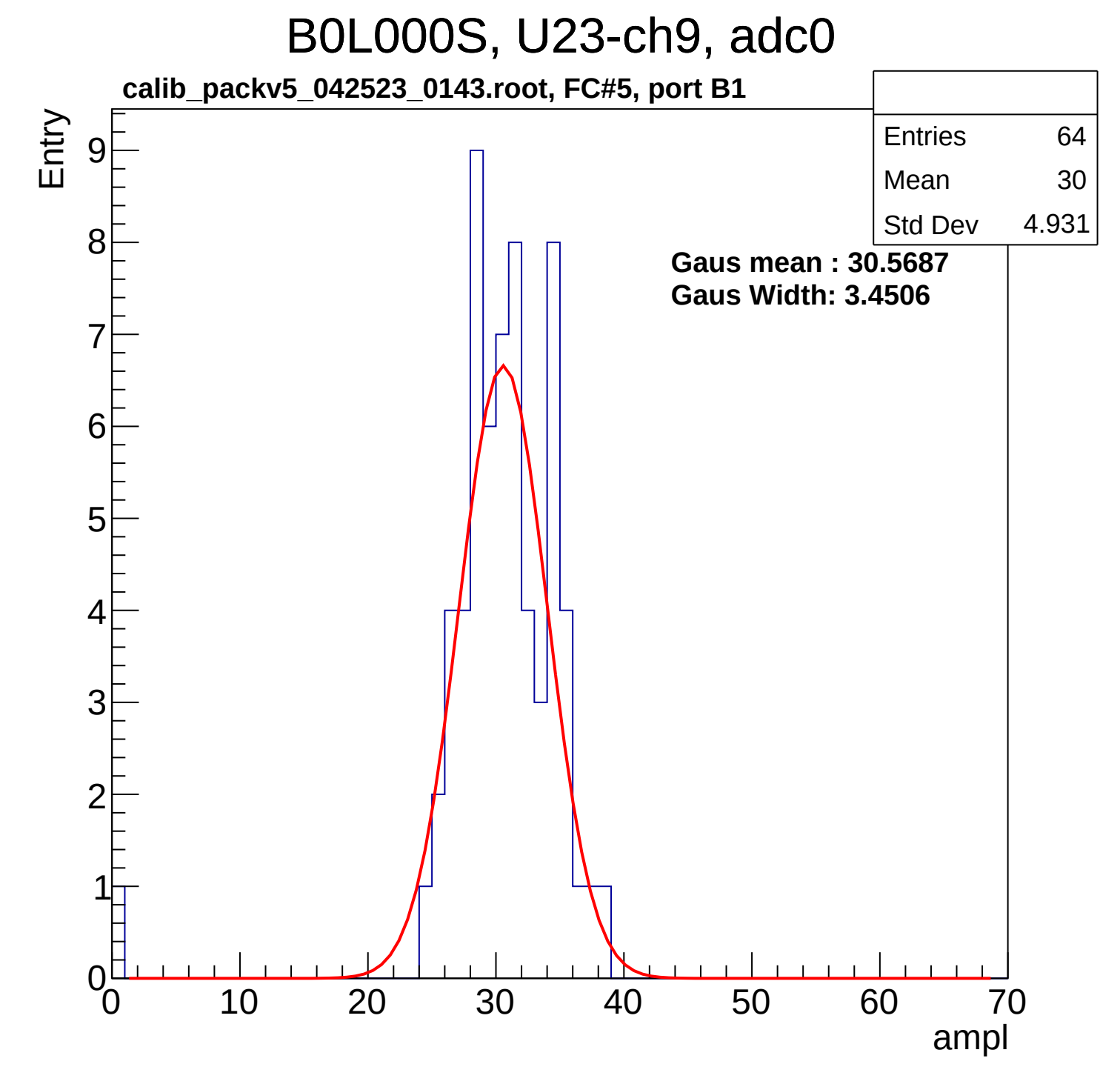

B0L000S, U23-ch8, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 47.48
Std Dev 3.128
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

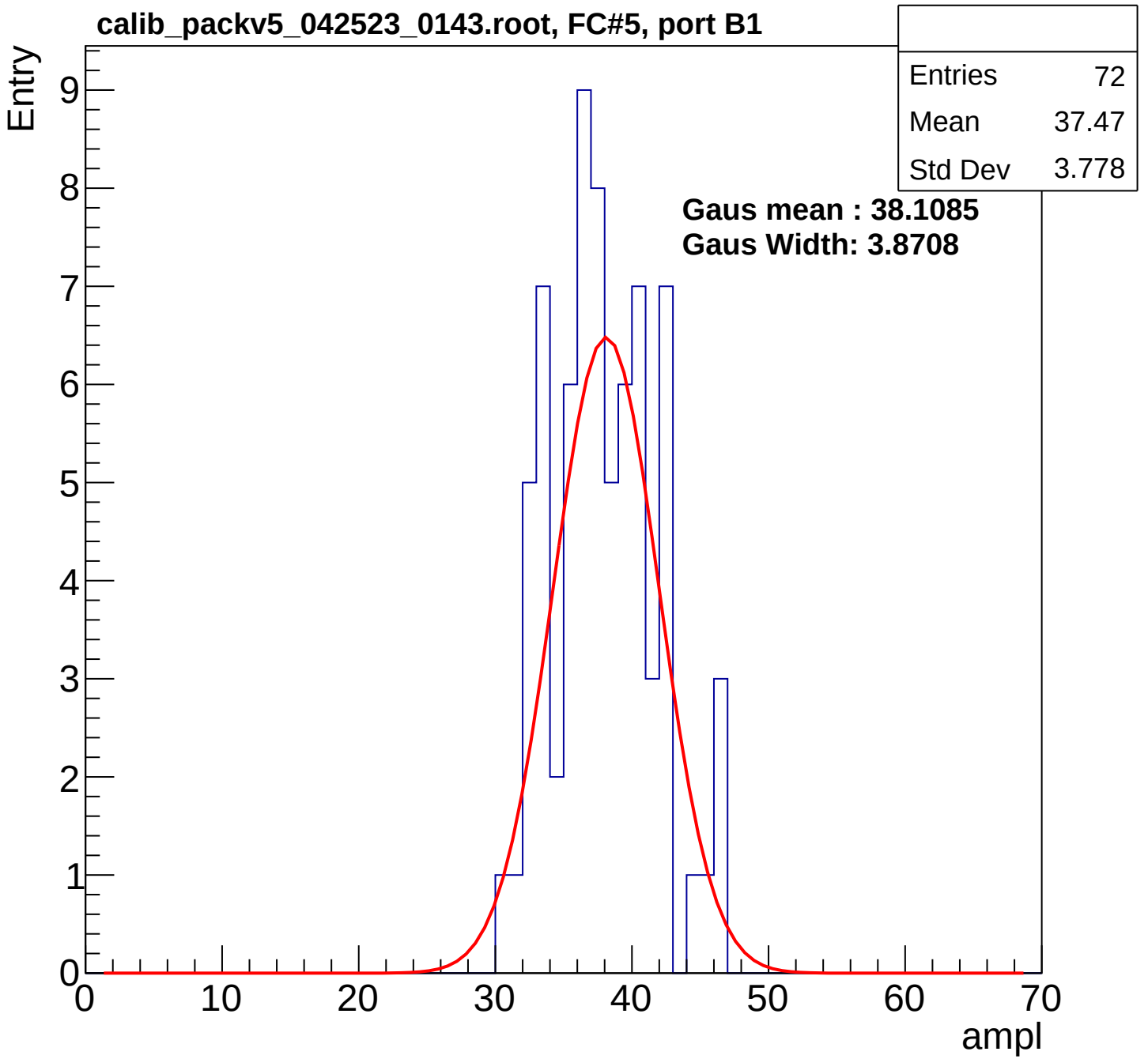

B0L000S, U23-ch8, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 53.56
Std Dev 2.985
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

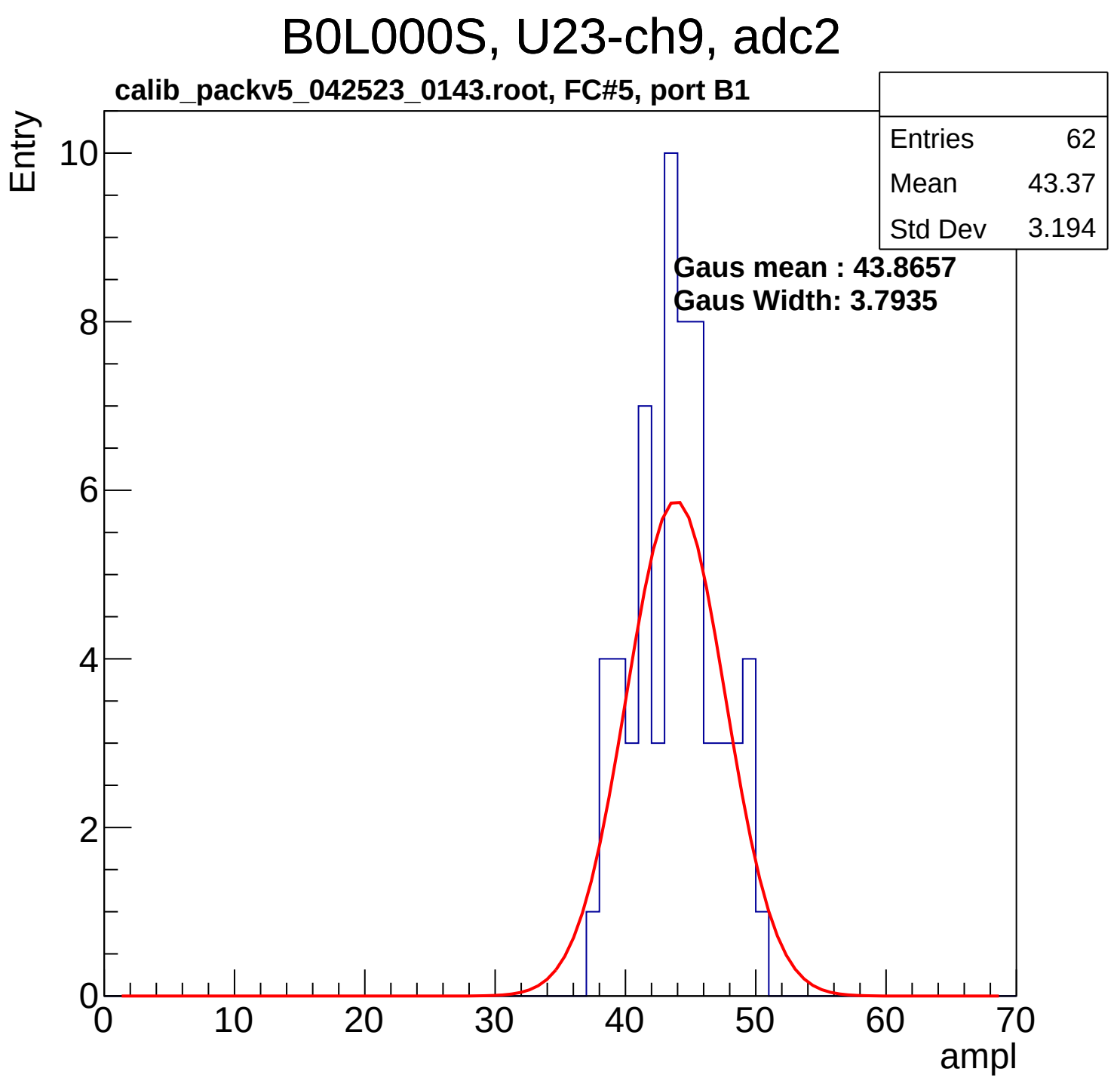

B0L000S, U23-ch8, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 59.6
Std Dev 2.285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

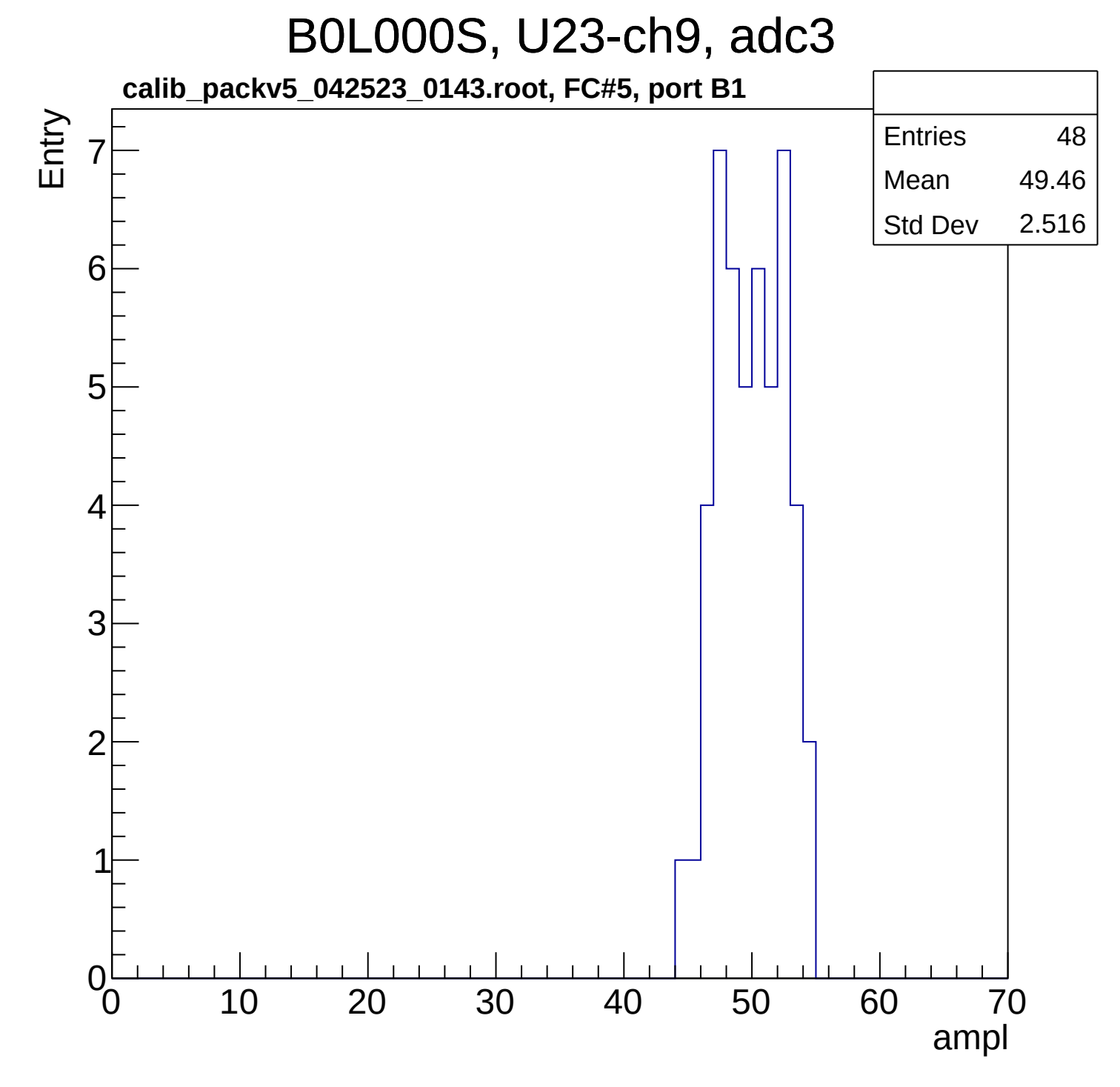

B0L000S, U23-ch8, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 54.78
Std Dev 19.39
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

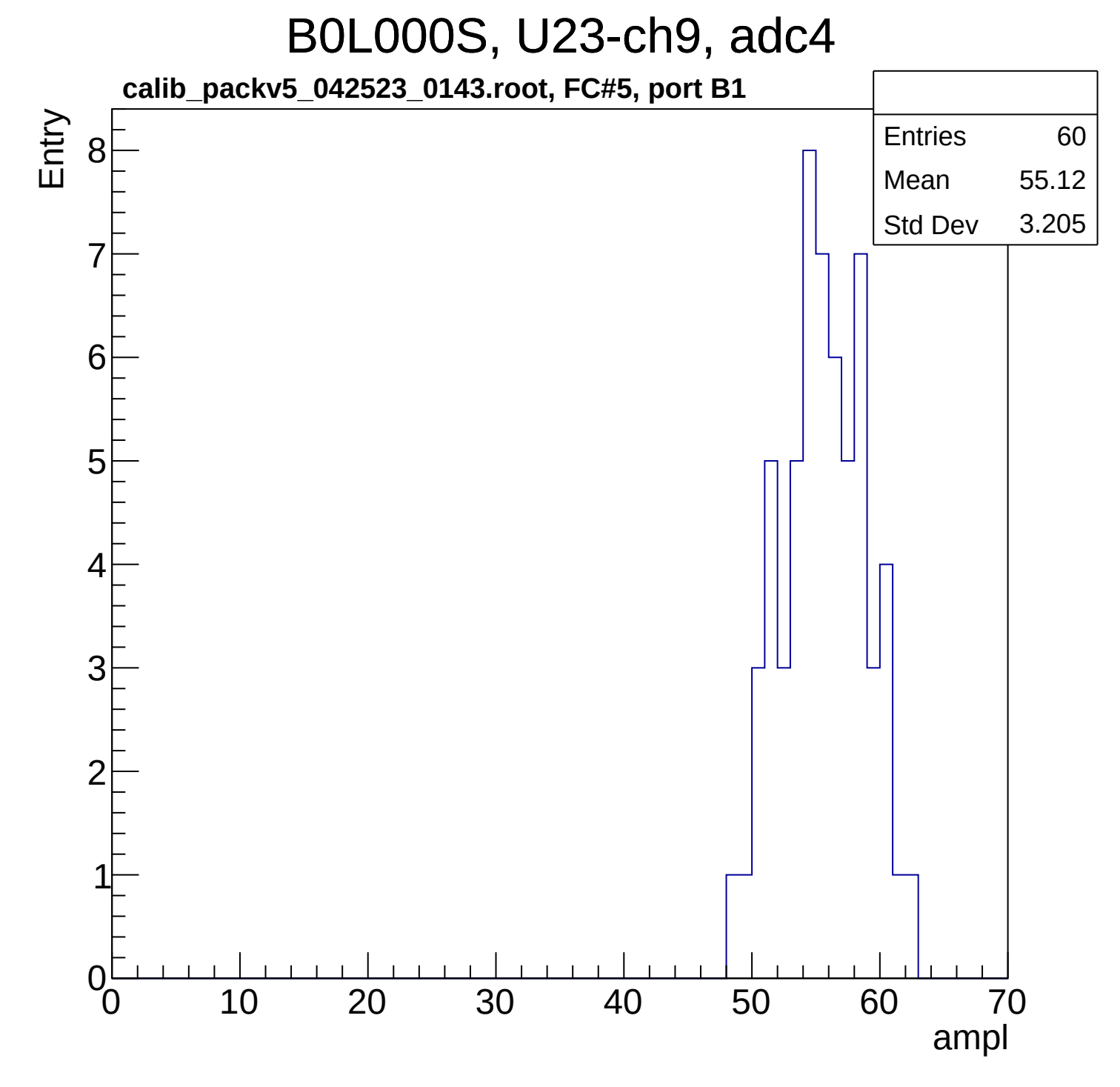

B0L000S, U23-ch8, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 46.33
Std Dev 22.87
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

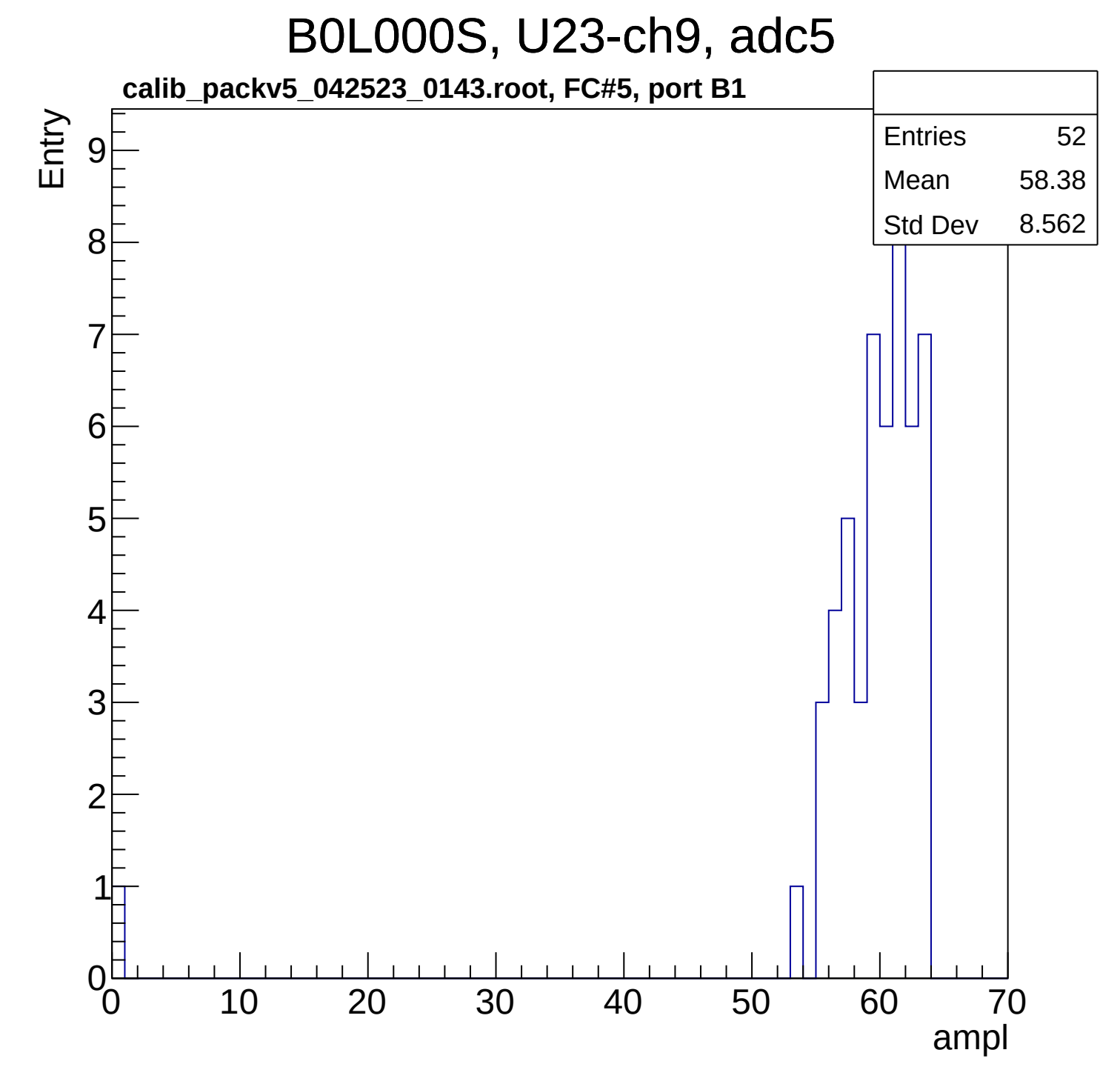

B0L000S, U23-ch9, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 30
Std Dev 4.931
Gaus mean : 30.5687
Gaus Width: 3.4506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

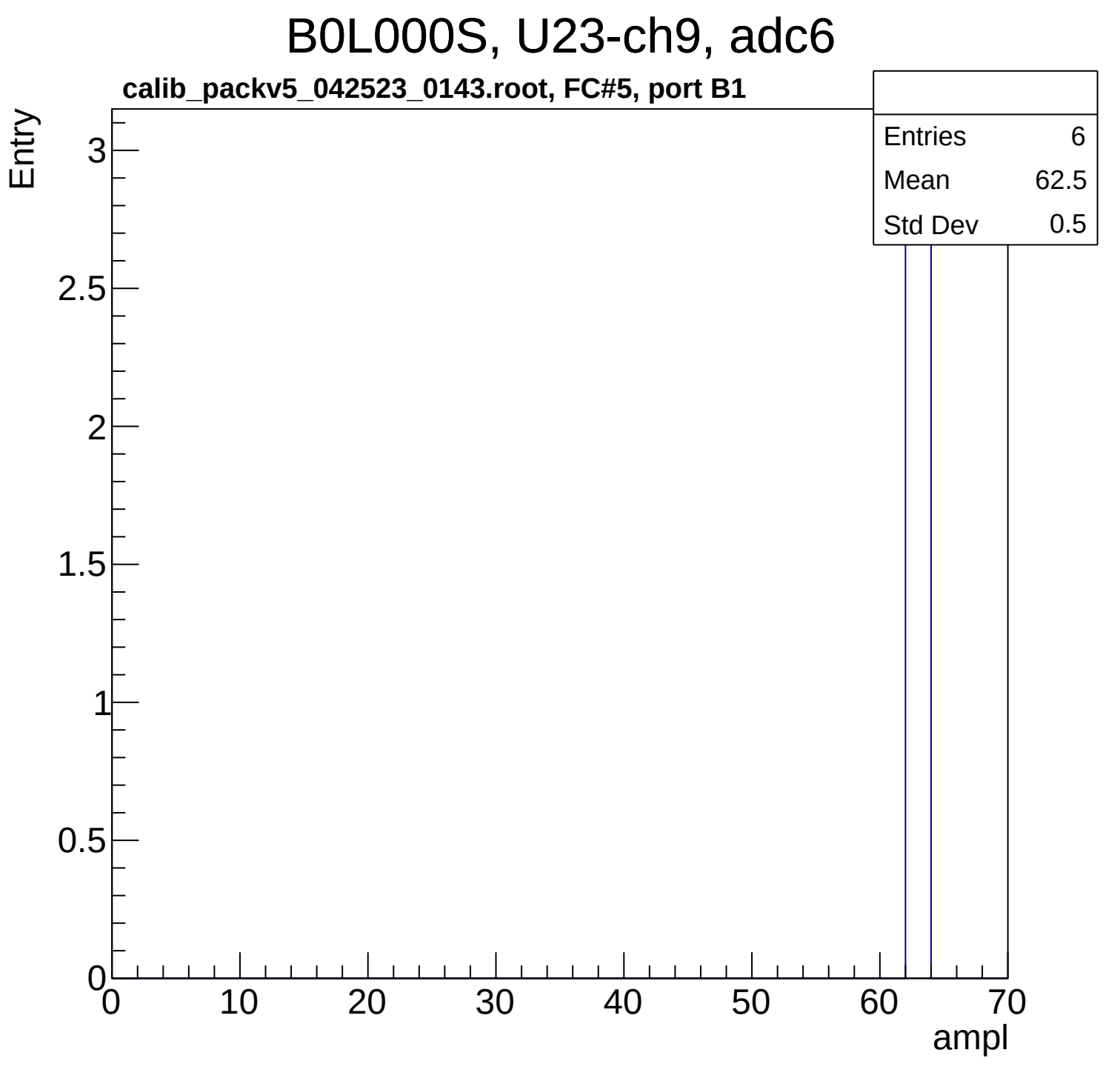

B0L000S, U23-ch9, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.47
Std Dev 3.778
Gaus mean : 38.1085
Gaus Width: 3.8708
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

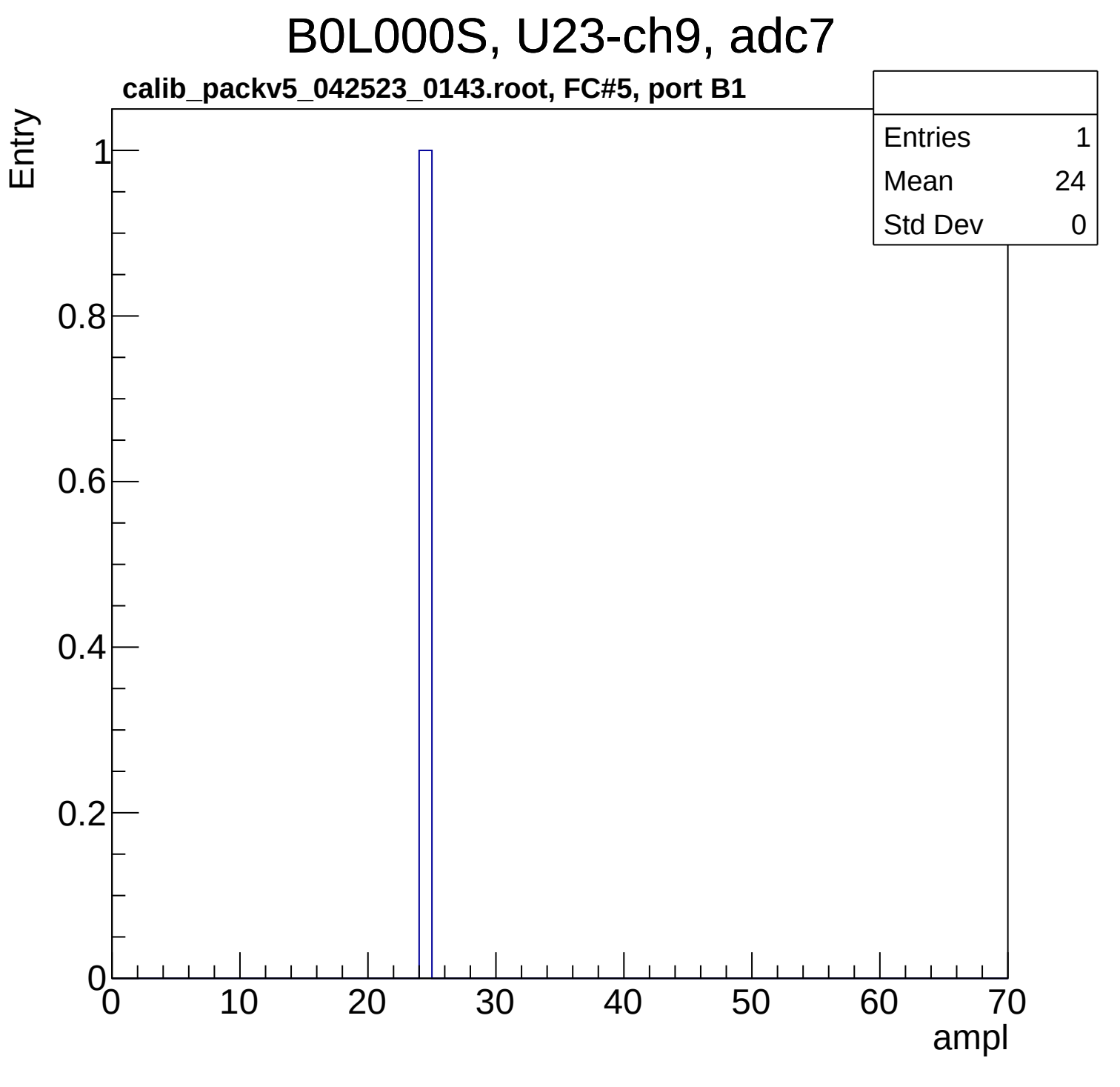

B0L000S, U23-ch9, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.37
Std Dev 3.194
Gaus mean : 43.8657
Gaus Width: 3.7935
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

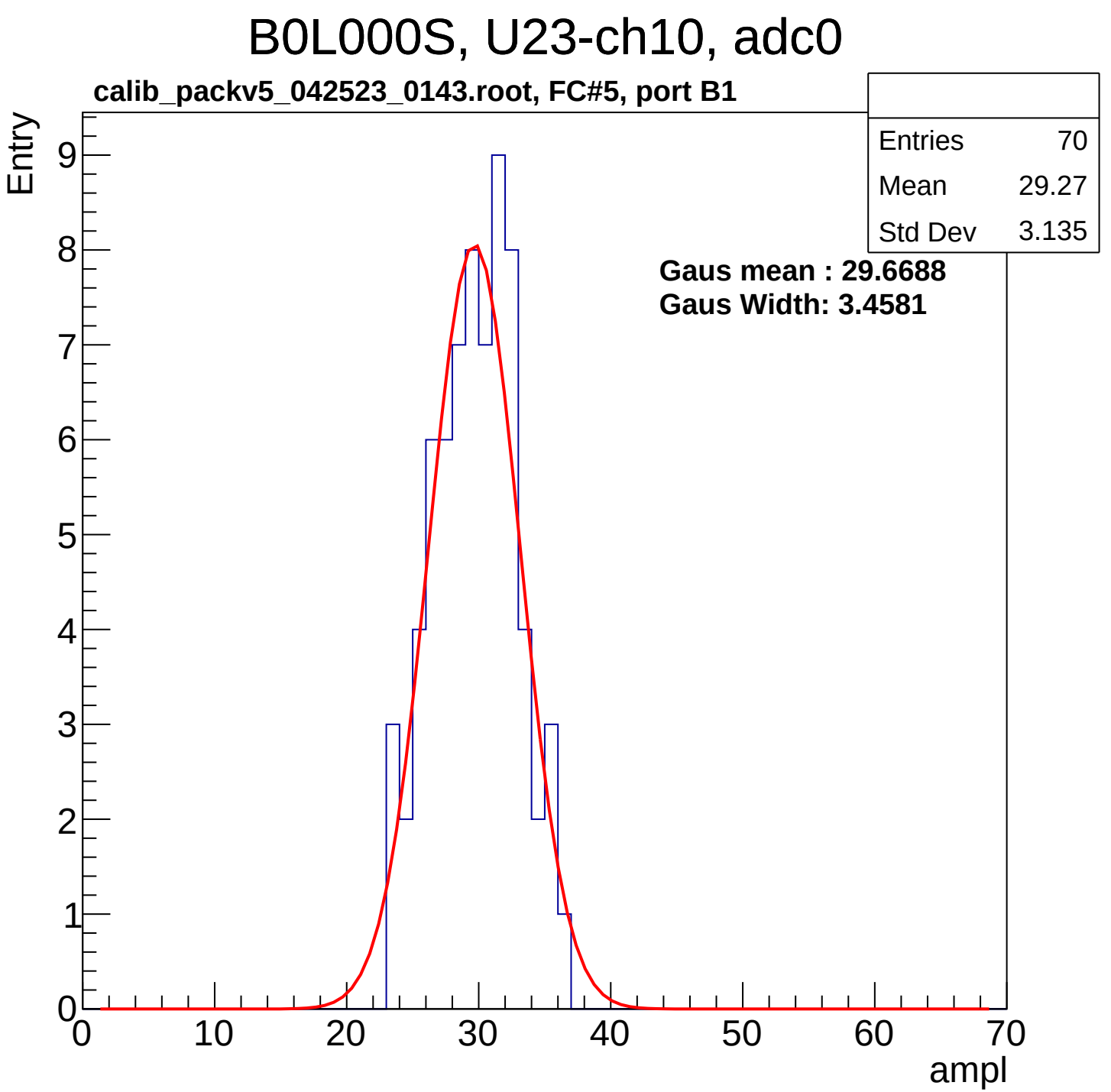

B0L000S, U23-ch9, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 49.46
Std Dev 2.516
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

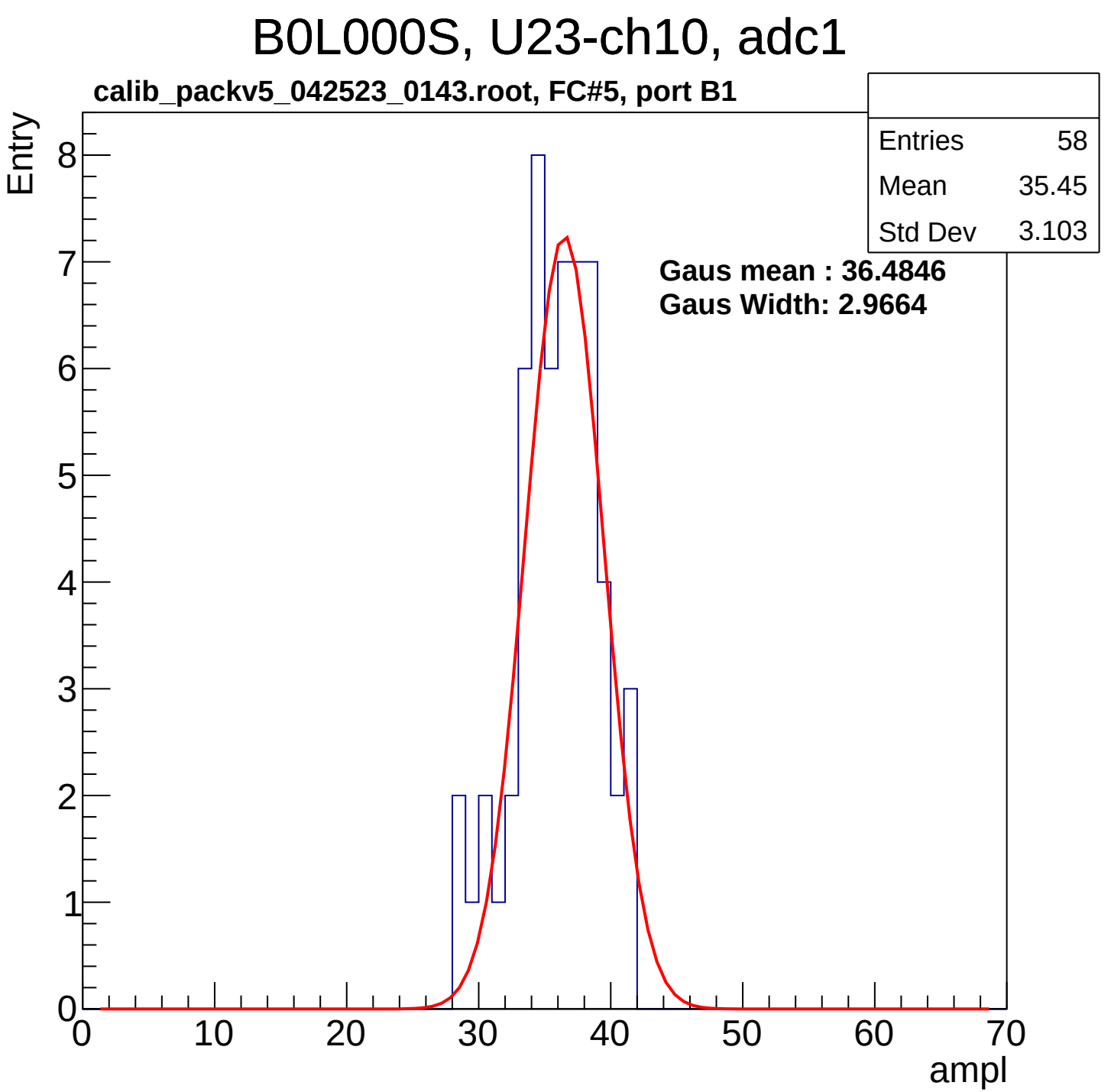

B0L000S, U23-ch9, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 55.12
Std Dev 3.205
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

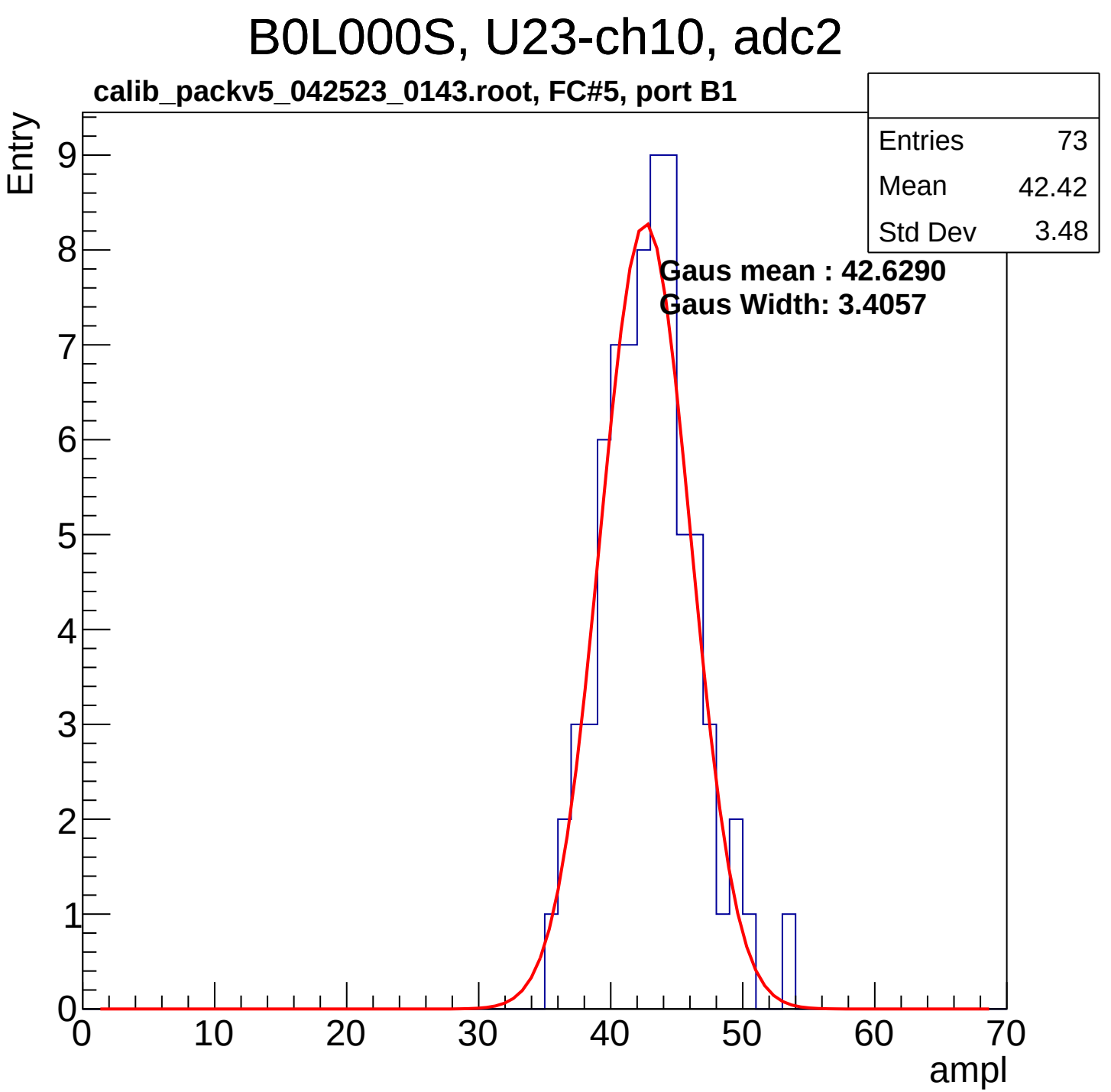

B0L000S, U23-ch9, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.38
Std Dev 8.562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

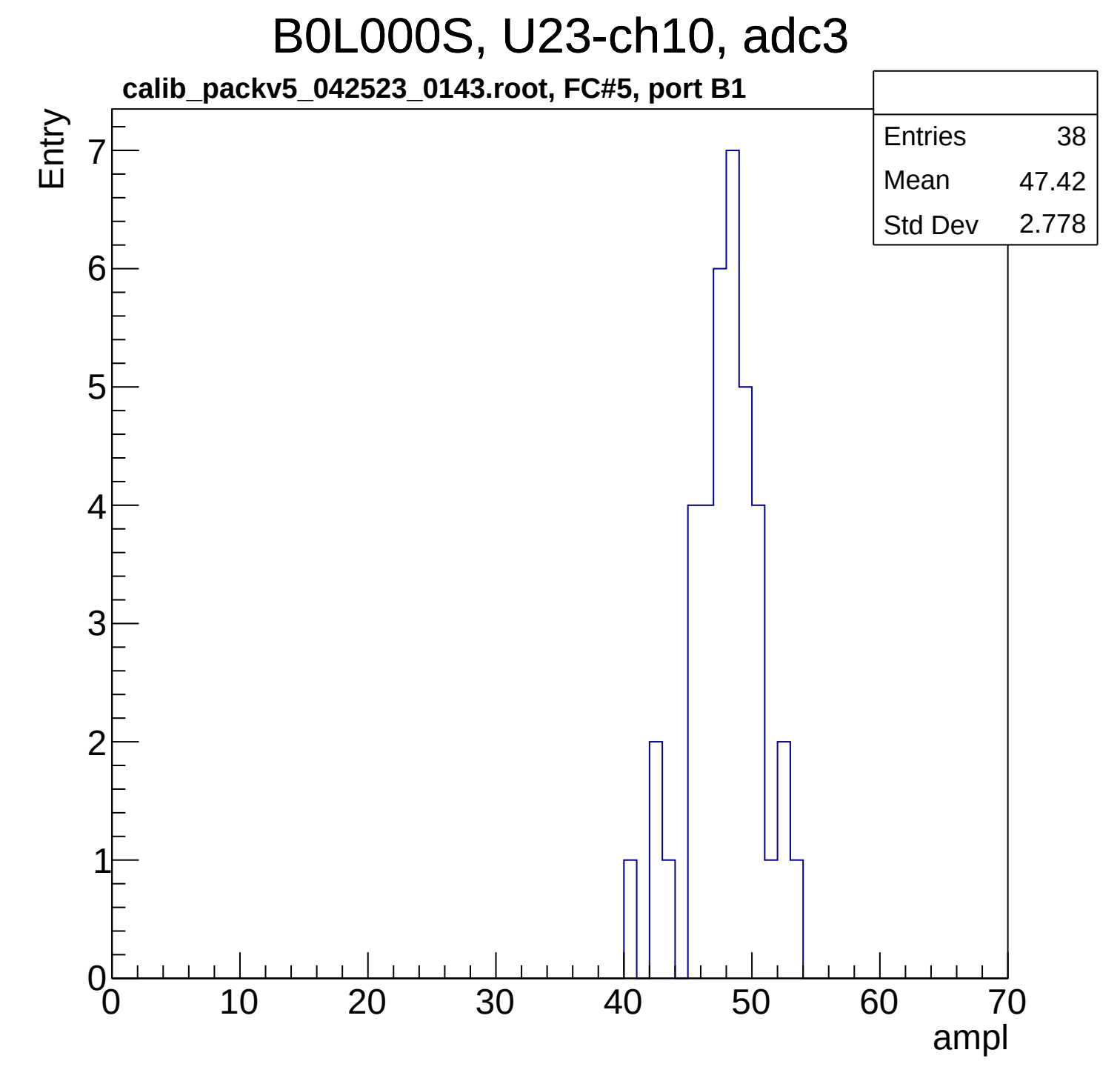

B0L000S, U23-ch9, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.5
Std Dev 0.5
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

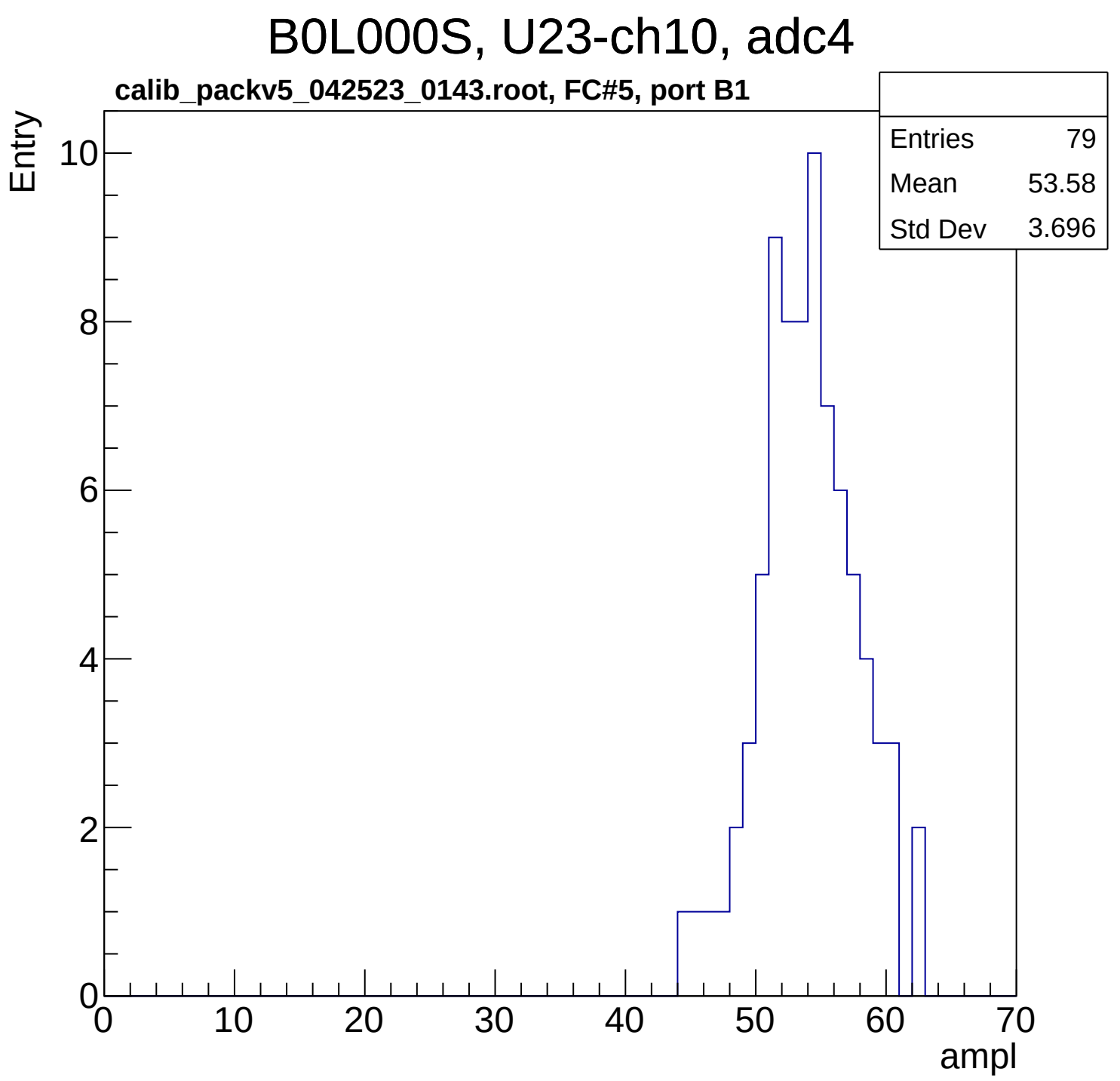

B0L000S, U23-ch9, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

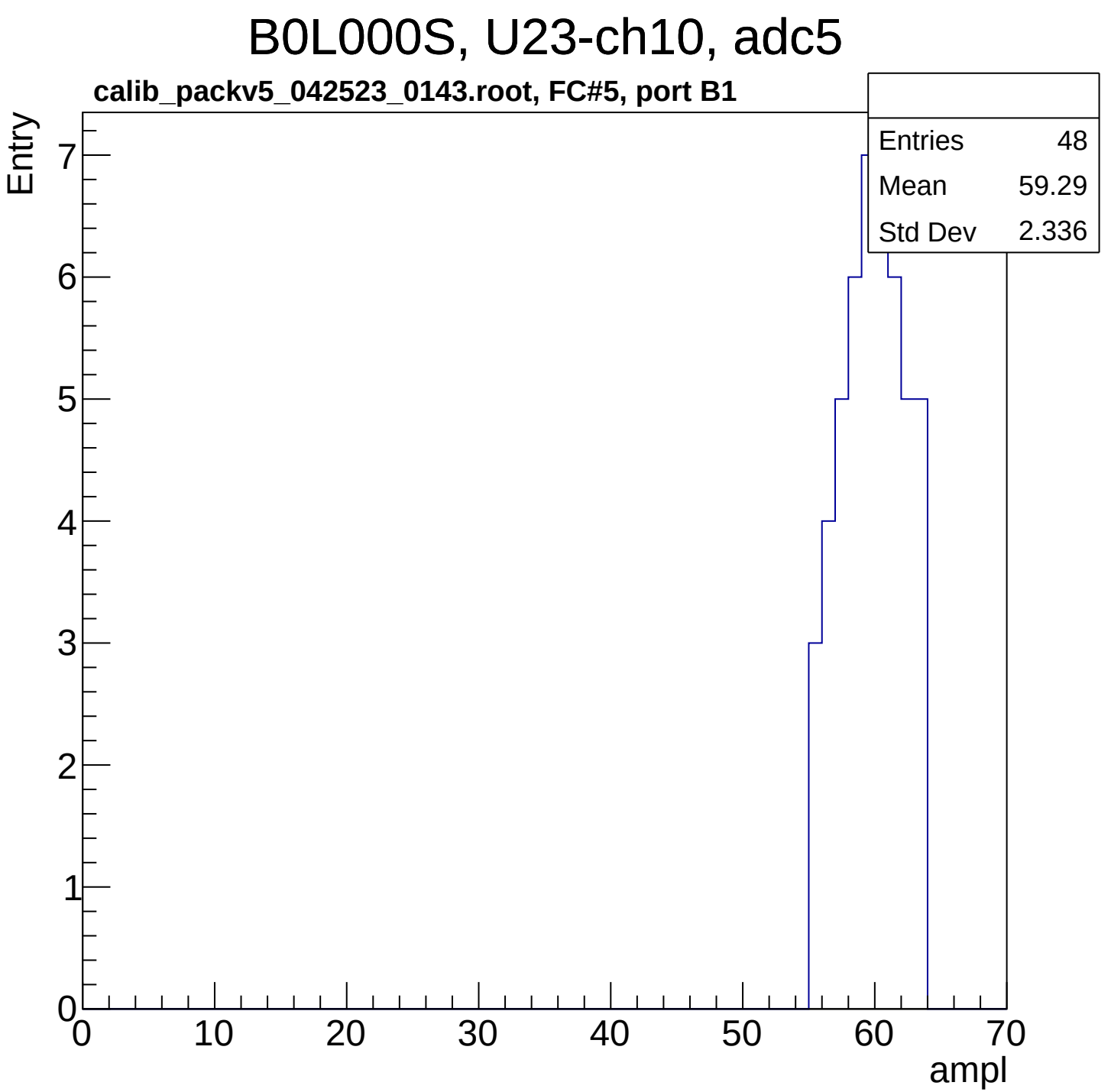

B0L000S, U23-ch10, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.27
Std Dev 3.135
Gaus mean : 29.6688
Gaus Width: 3.4581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

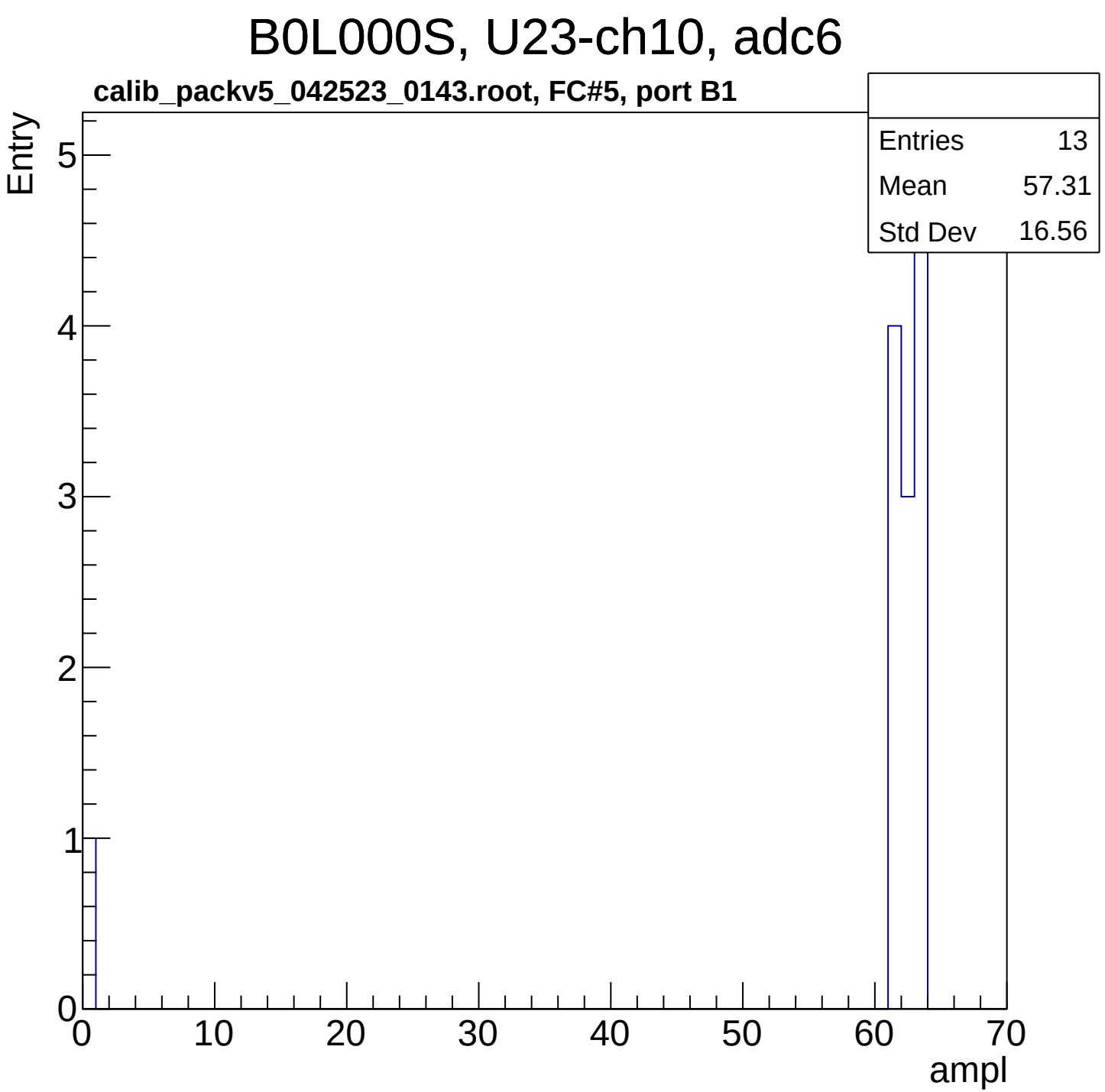

B0L000S, U23-ch10, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.45
Std Dev 3.103
Gaus mean : 36.4846
Gaus Width: 2.9664
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

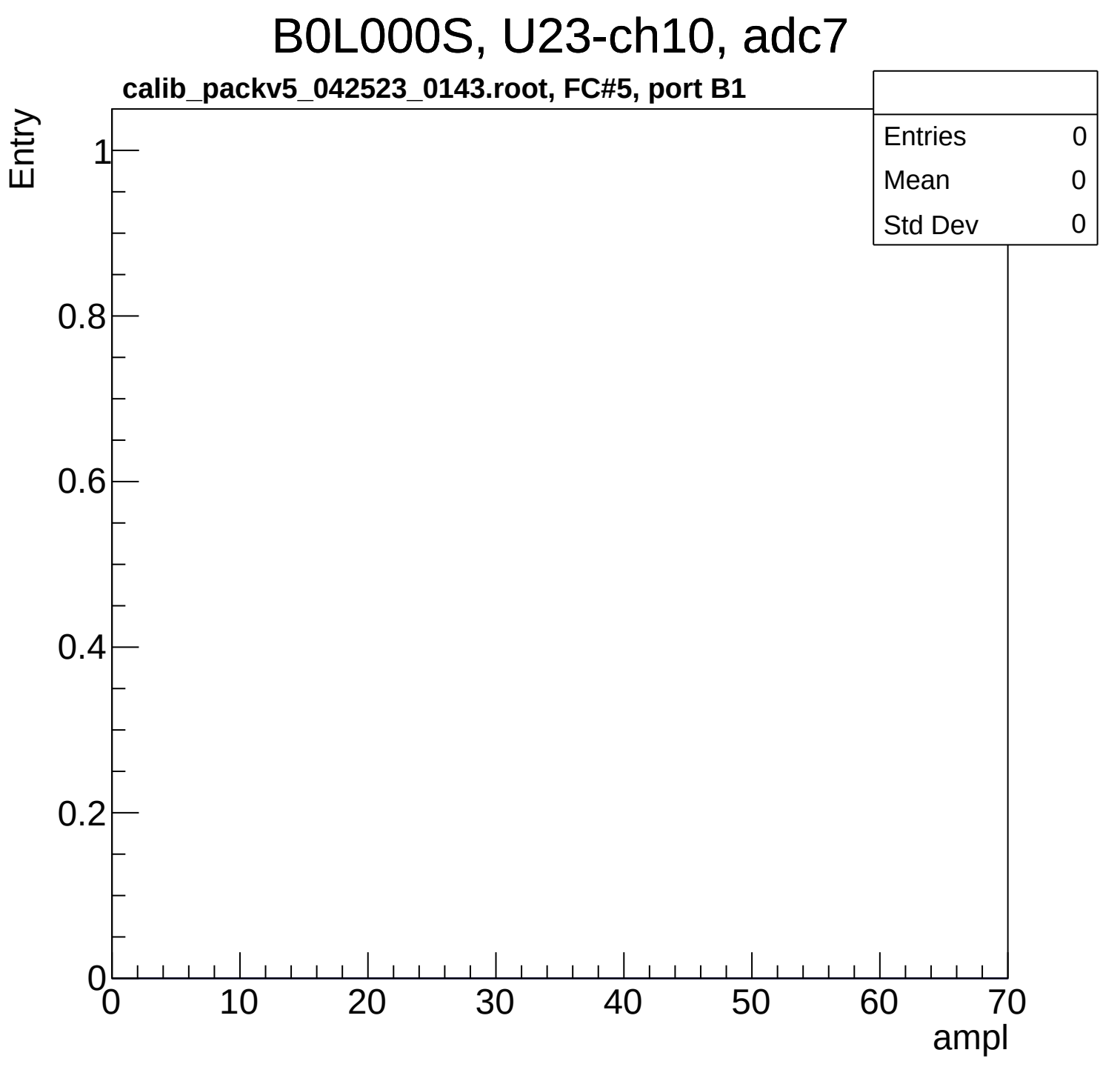

B0L000S, U23-ch10, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.42
Std Dev 3.48
Gaus mean : 42.6290
Gaus Width: 3.4057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

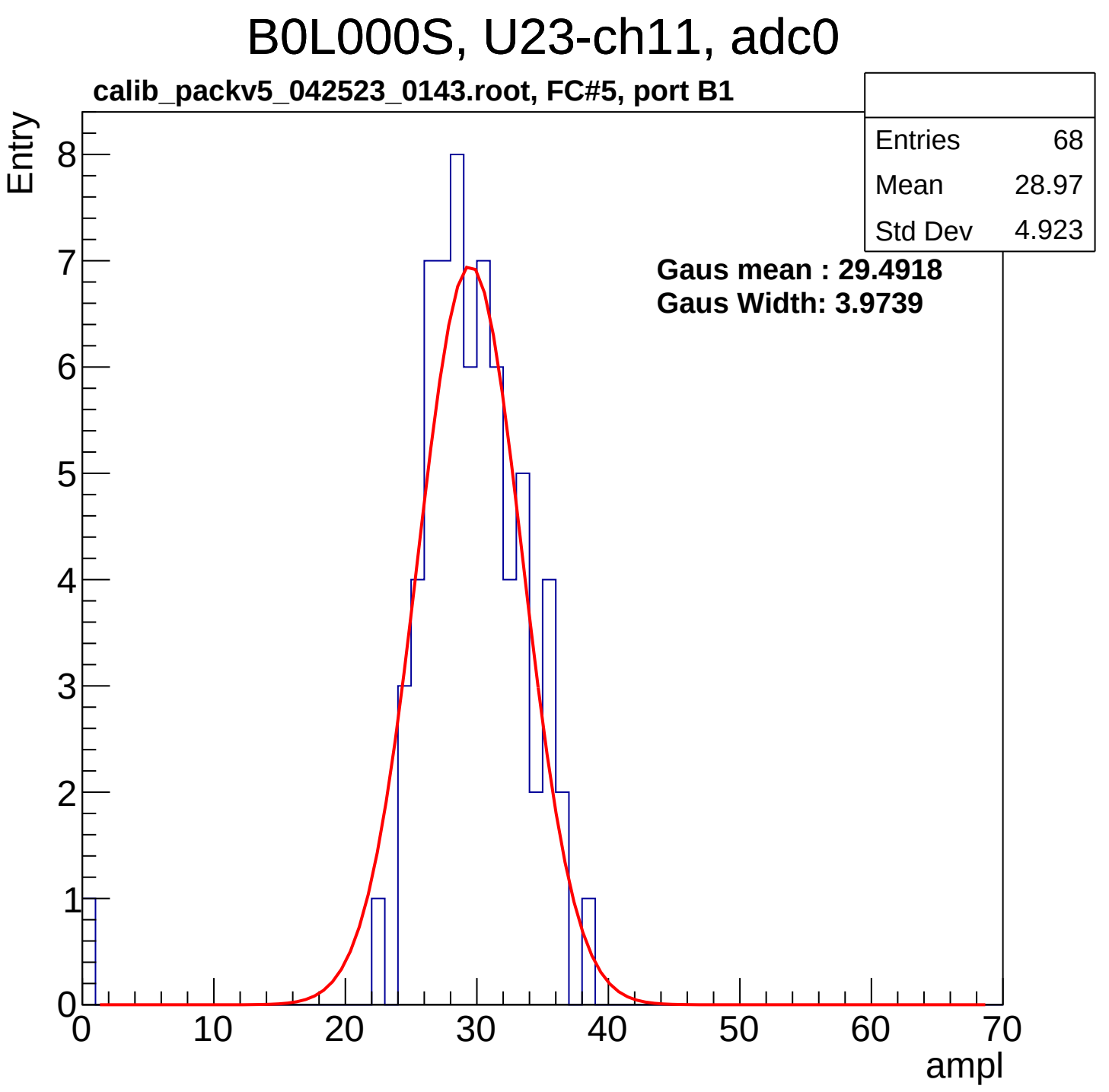

B0L000S, U23-ch10, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 47.42
Std Dev 2.778
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

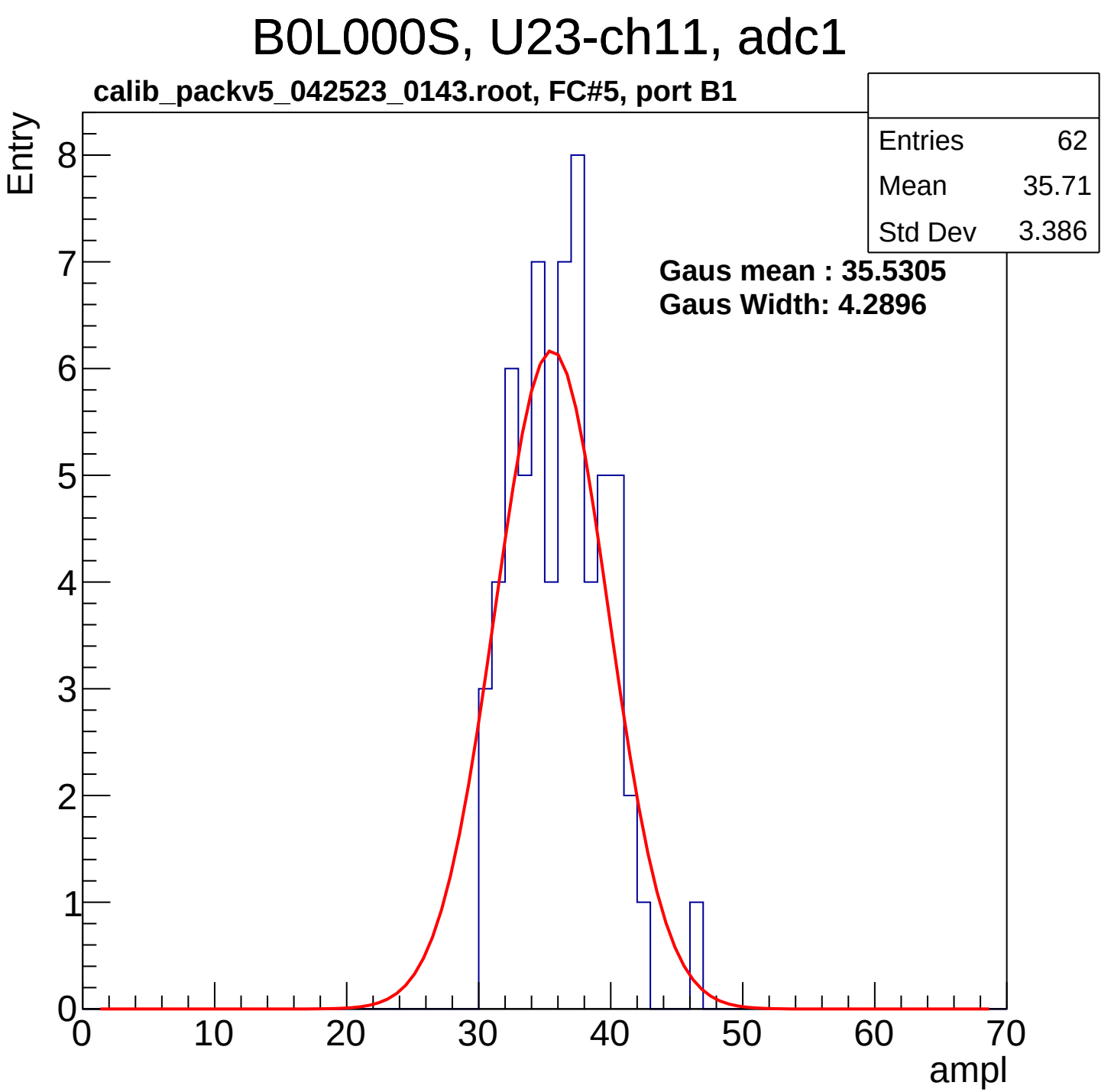

B0L000S, U23-ch10, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 53.58
Std Dev 3.696
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

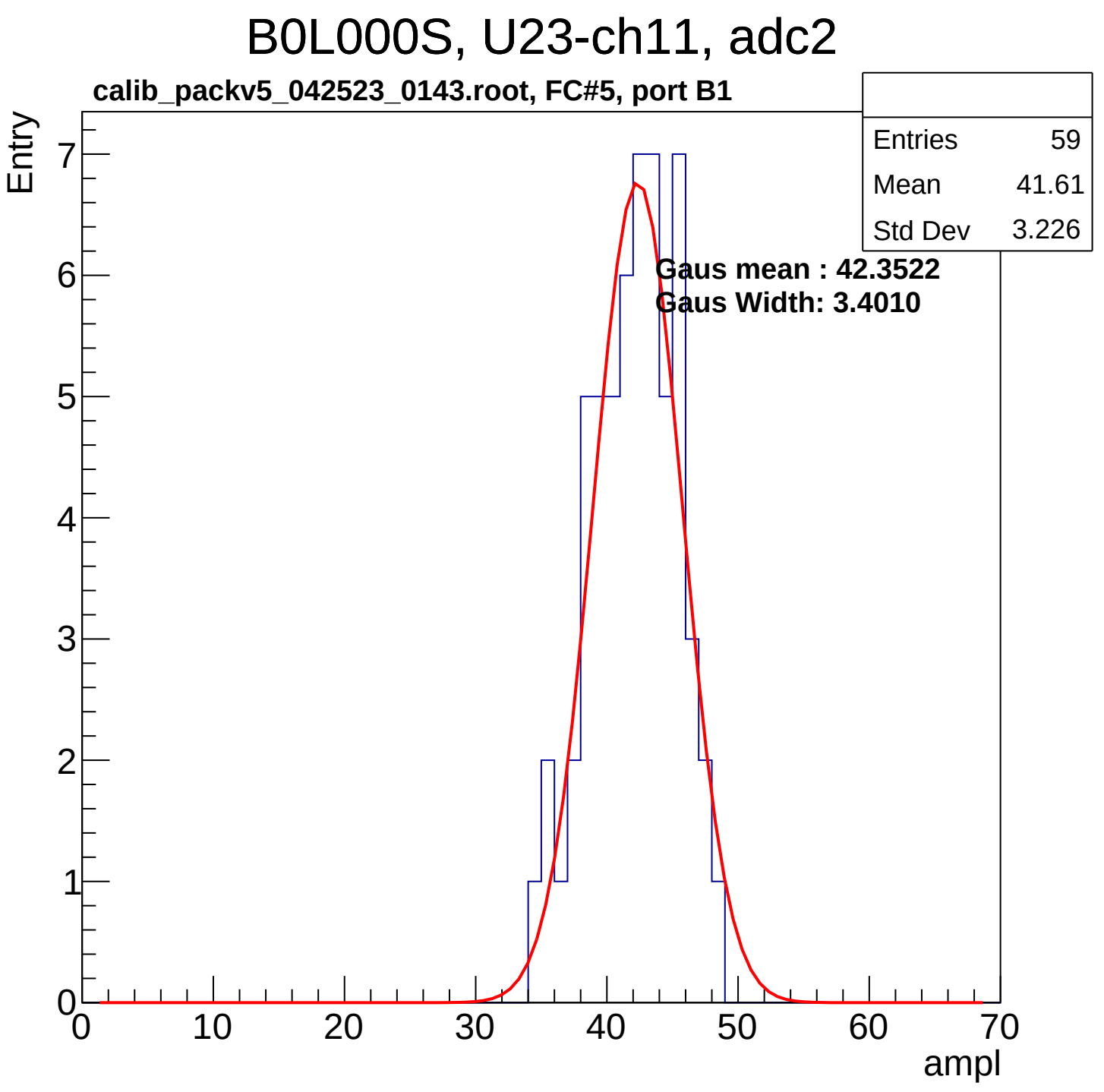

B0L000S, U23-ch10, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.29
Std Dev 2.336
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

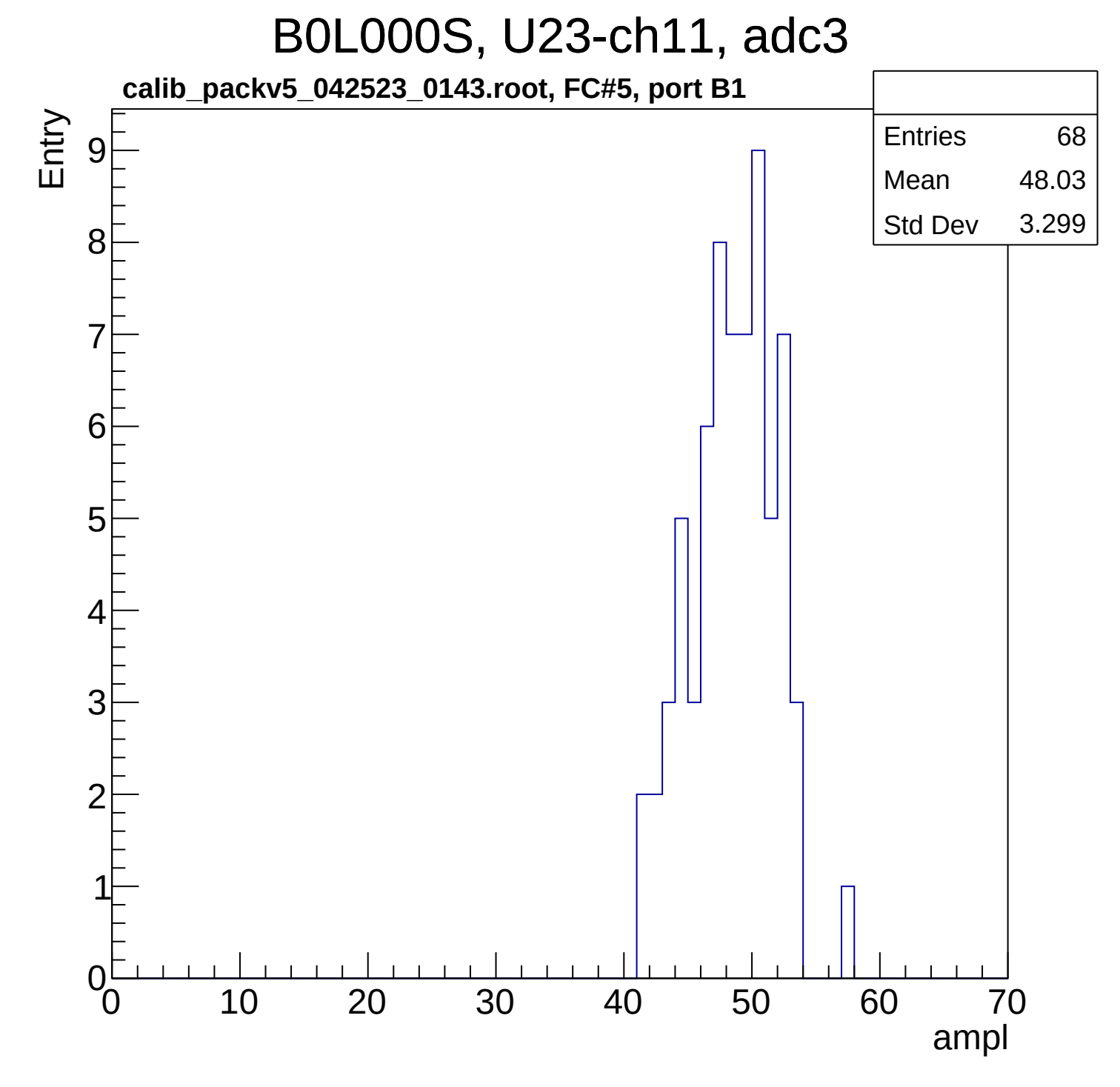

B0L000S, U23-ch10, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.31
Std Dev 16.56
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

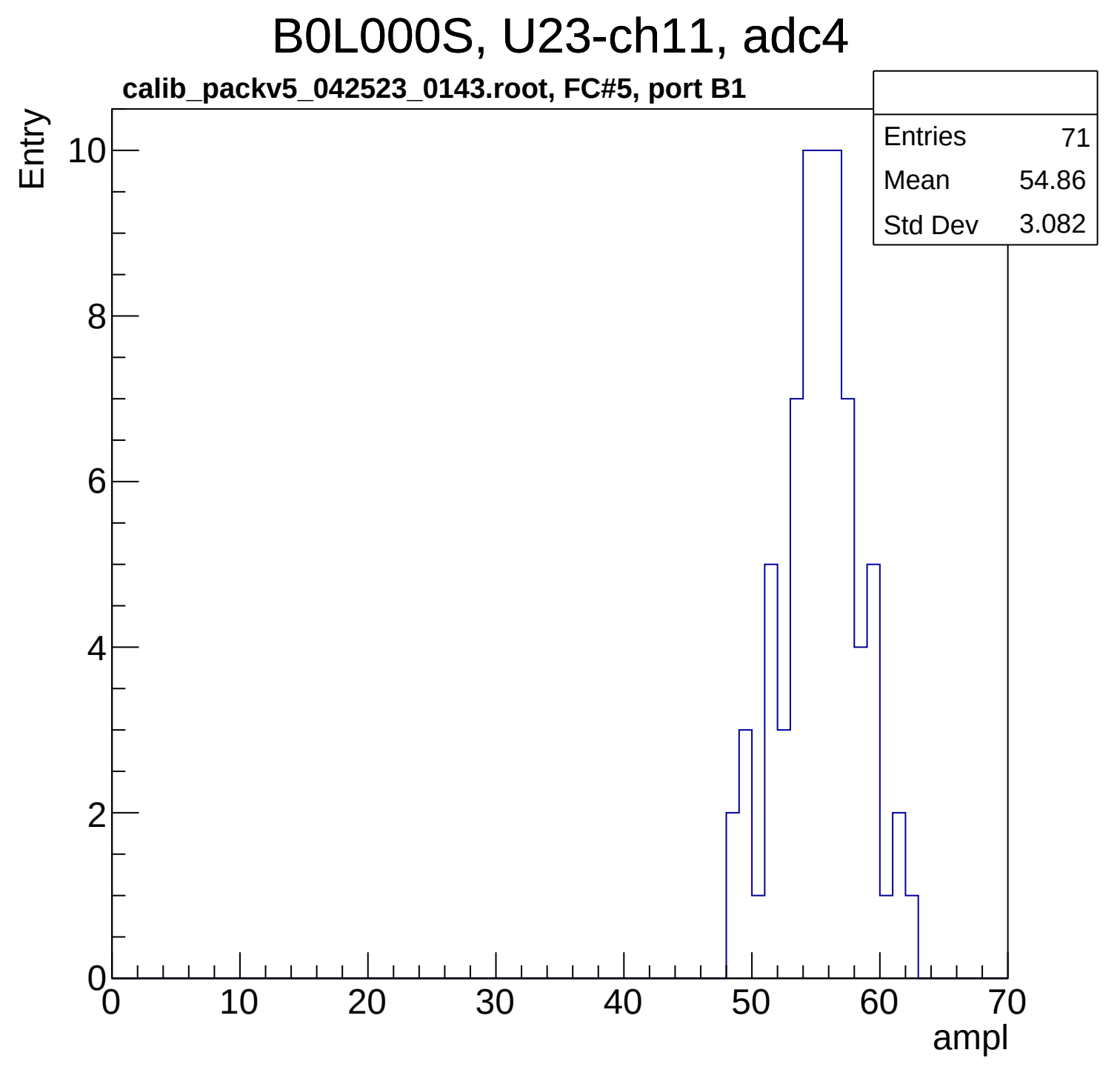

B0L000S, U23-ch10, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

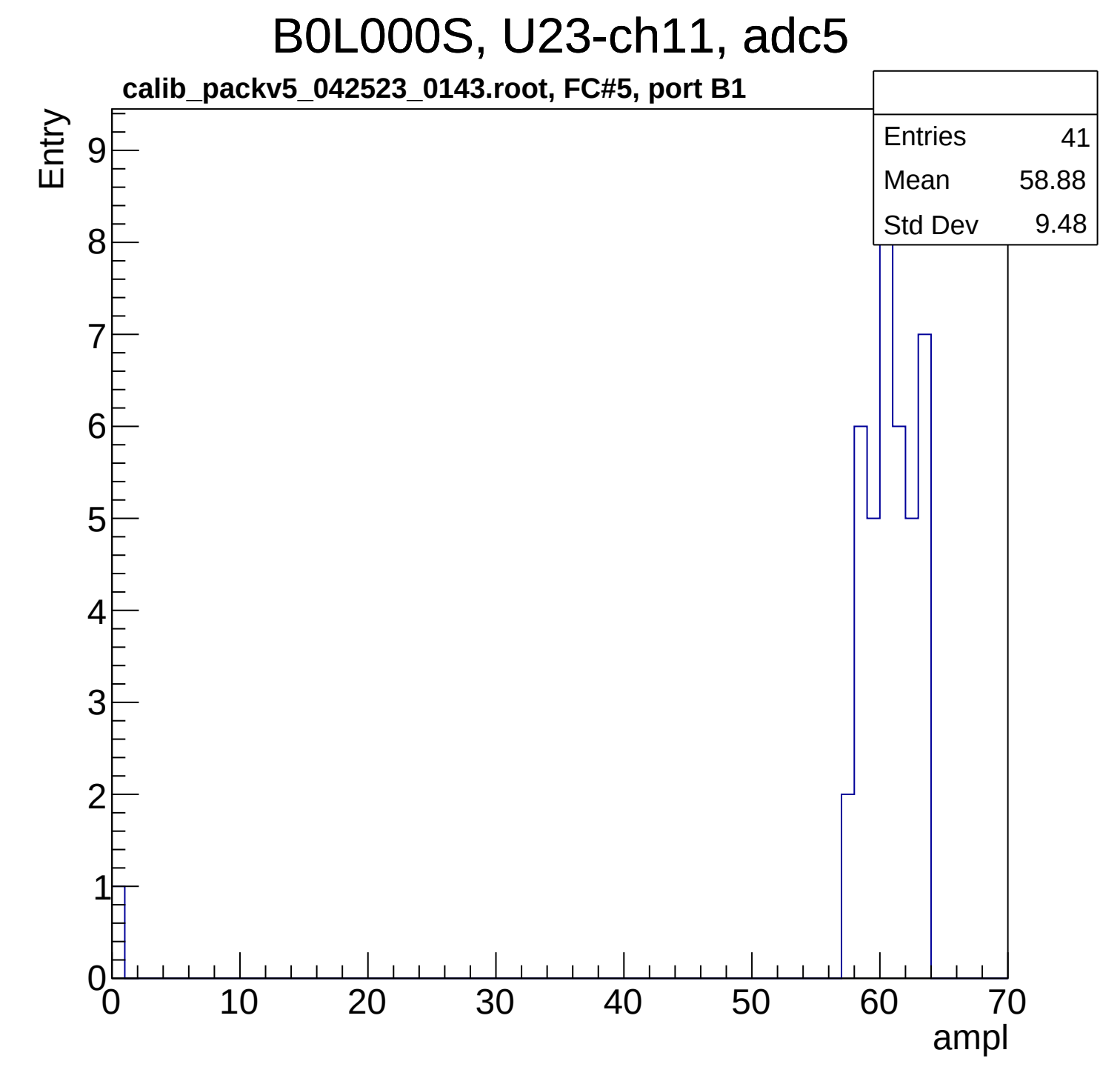

B0L000S, U23-ch11, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.97
Std Dev 4.923
Gaus mean : 29.4918
Gaus Width: 3.9739
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

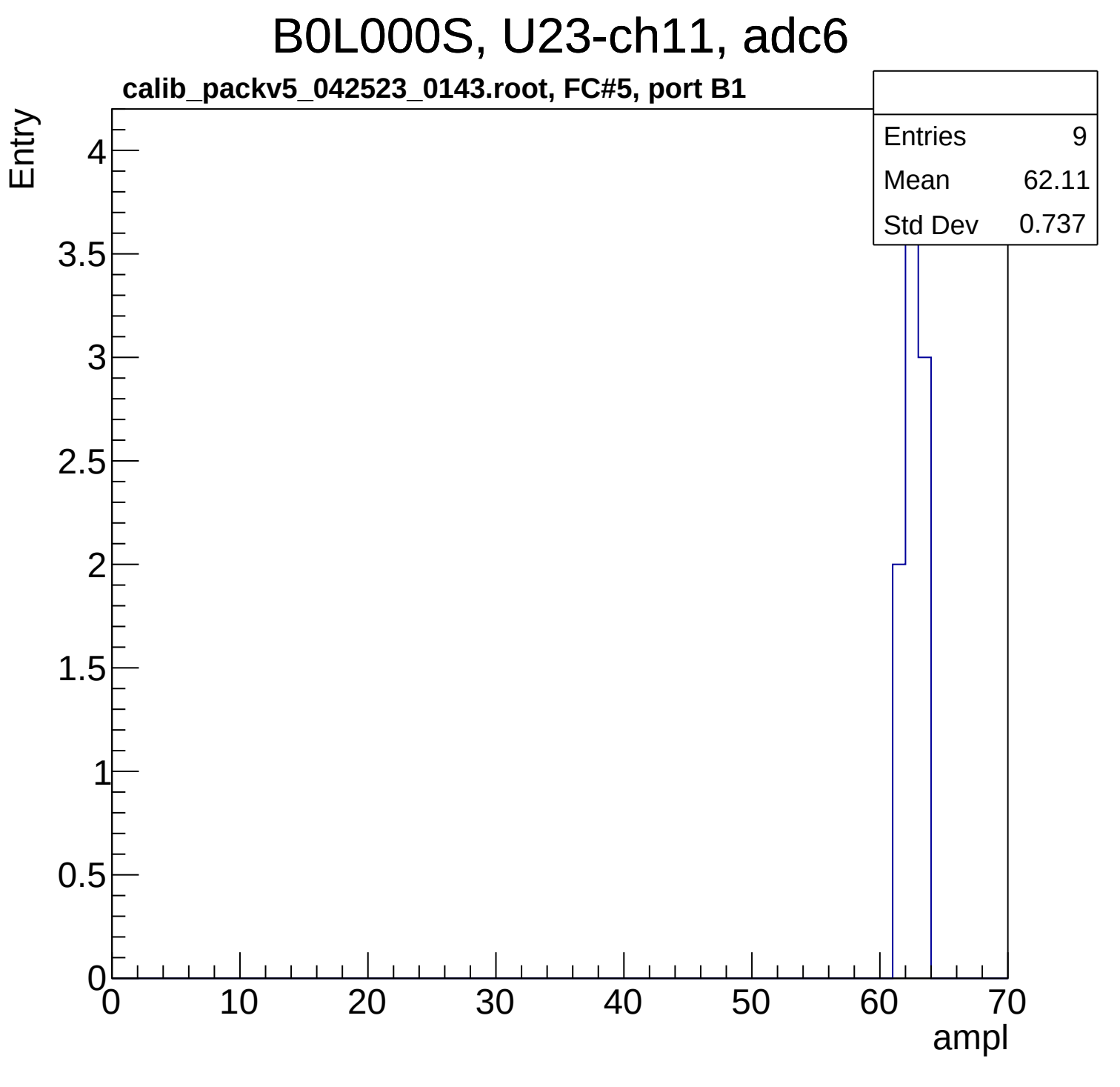

B0L000S, U23-ch11, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.71
Std Dev 3.386
Gaus mean : 35.5305
Gaus Width: 4.2896
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

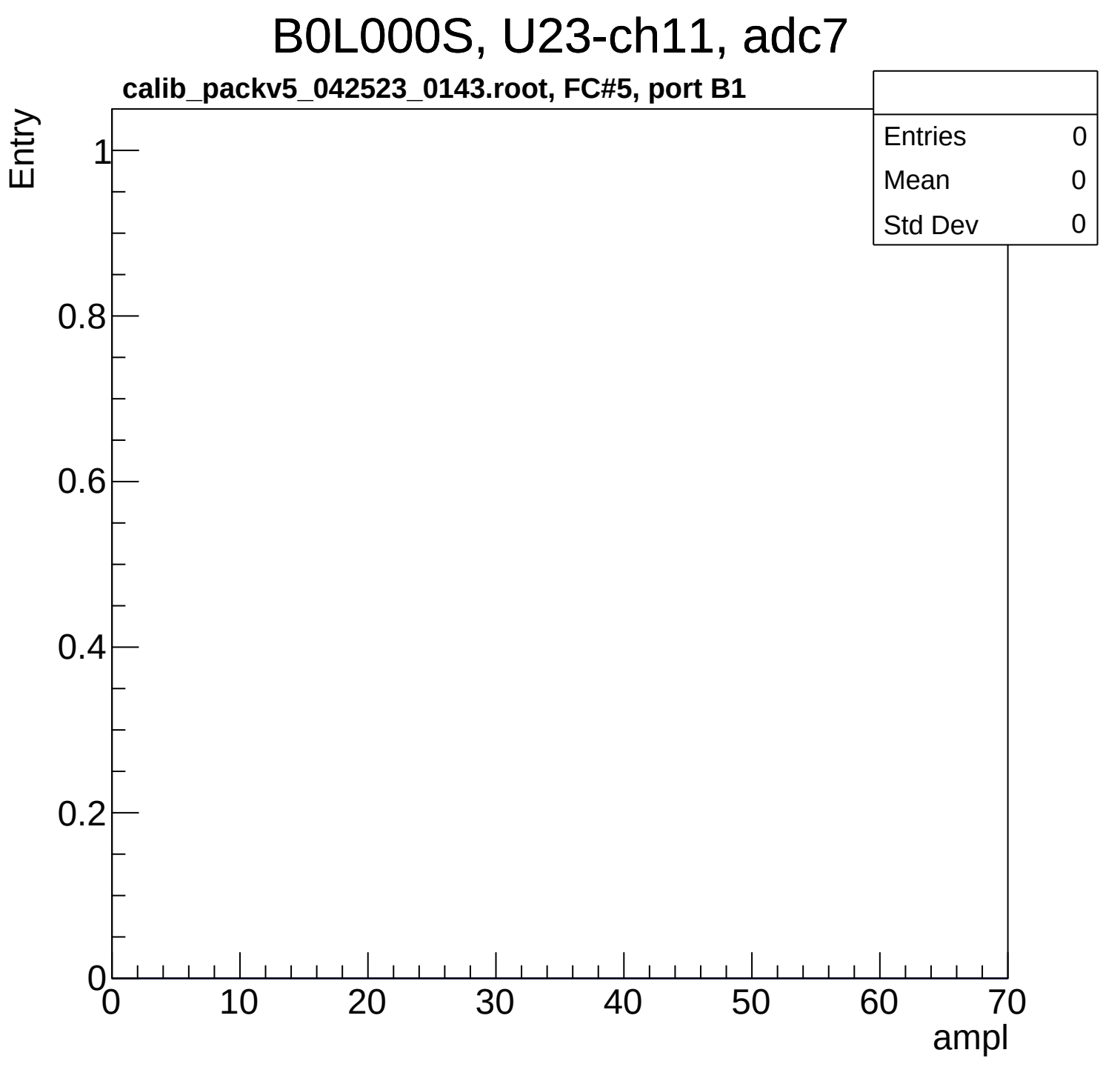

B0L000S, U23-ch11, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 41.61
Std Dev 3.226
Gaus mean : 42.3522
Gaus Width: 3.4010
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

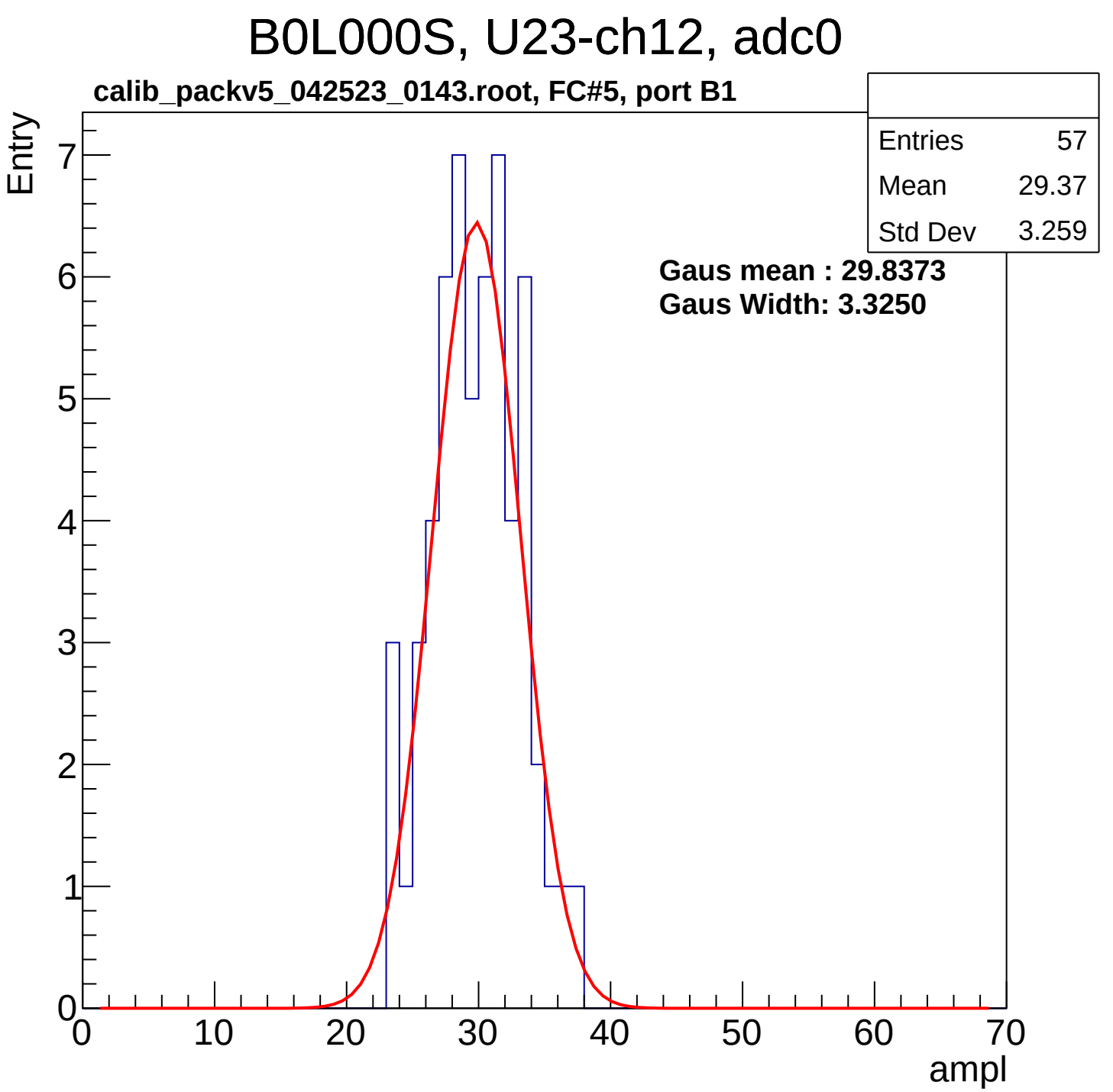

B0L000S, U23-ch11, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.03
Std Dev 3.299
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

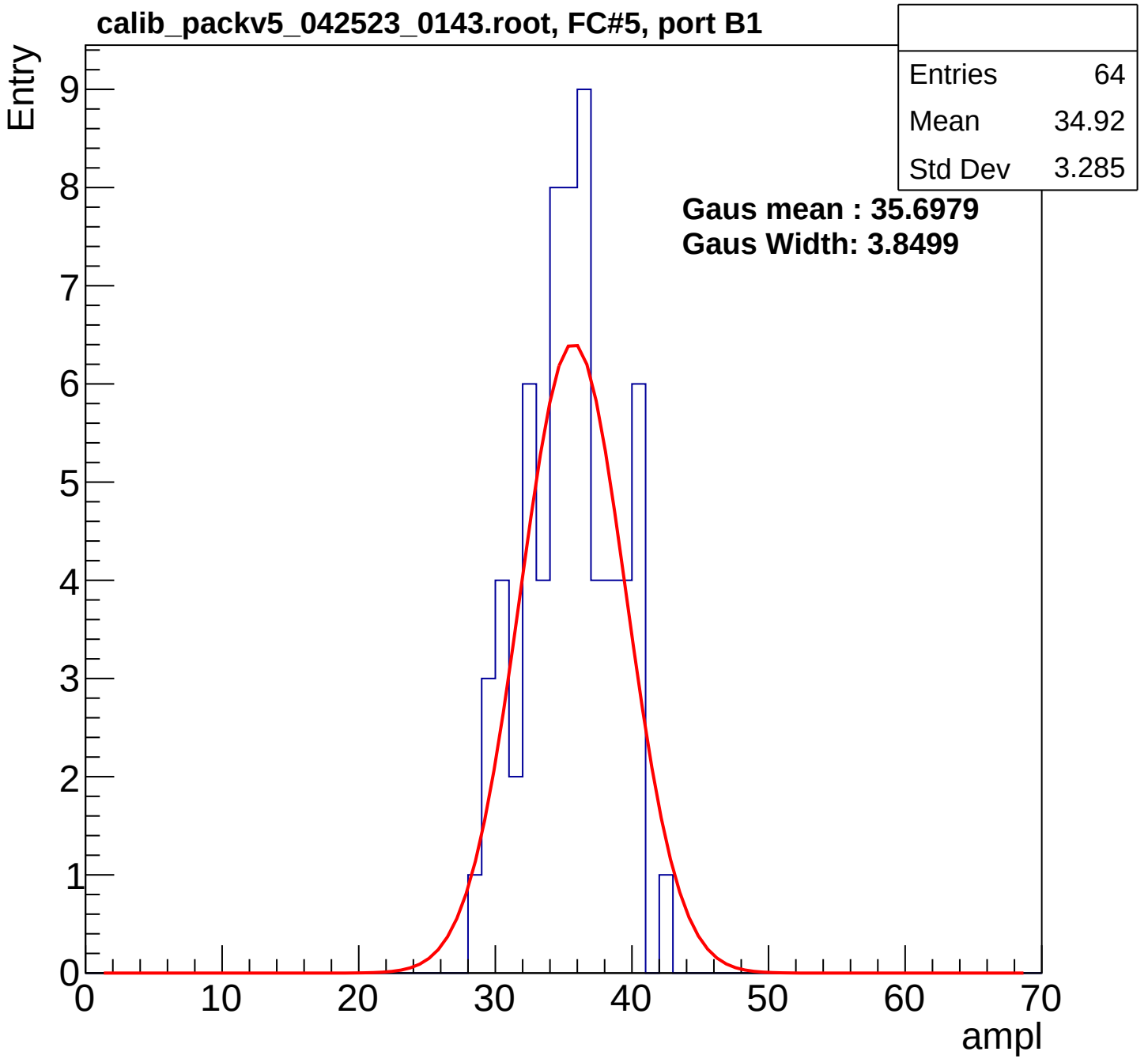

B0L000S, U23-ch11, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 54.86
Std Dev 3.082
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

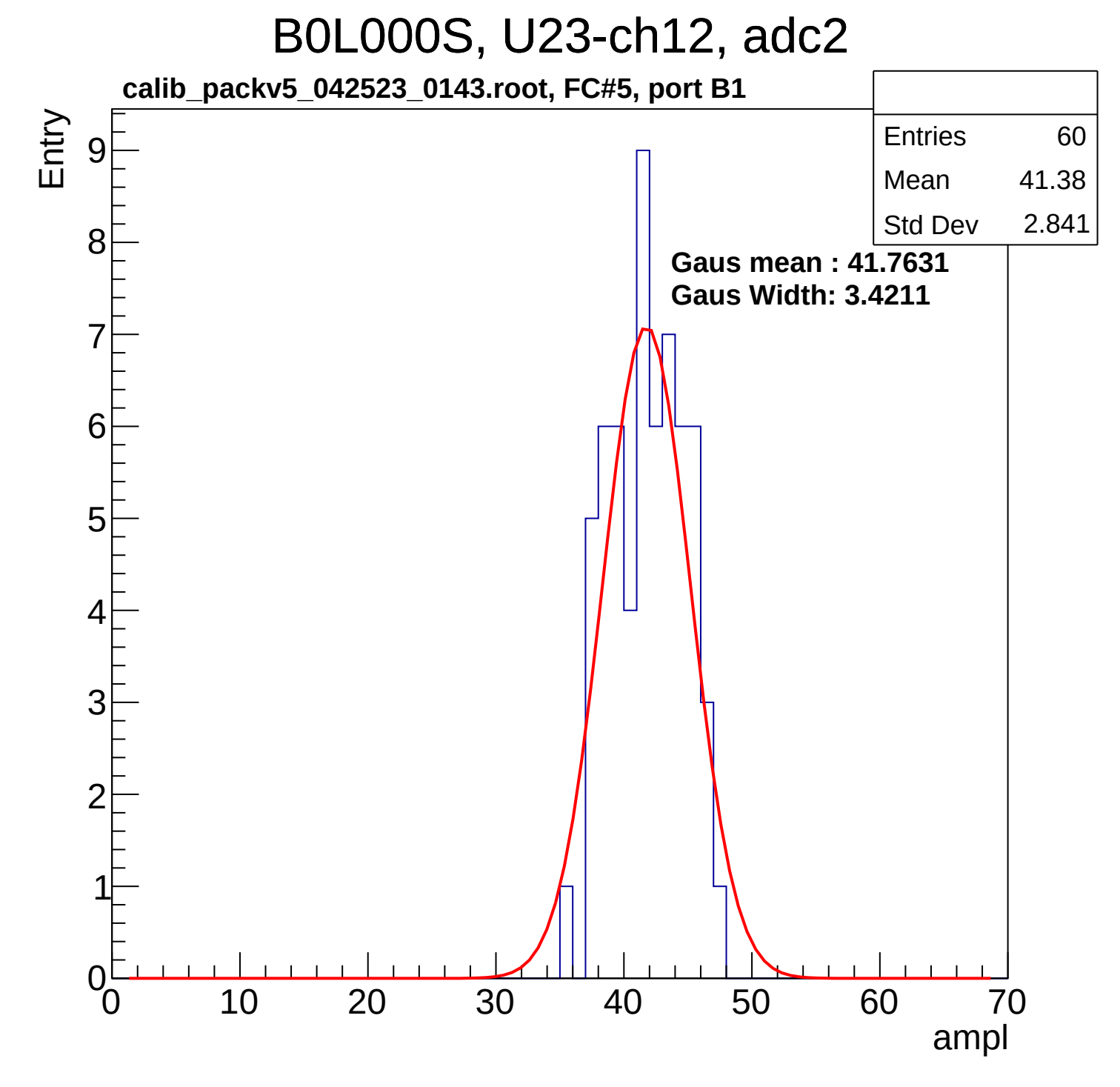

B0L000S, U23-ch11, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.88
Std Dev 9.48
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

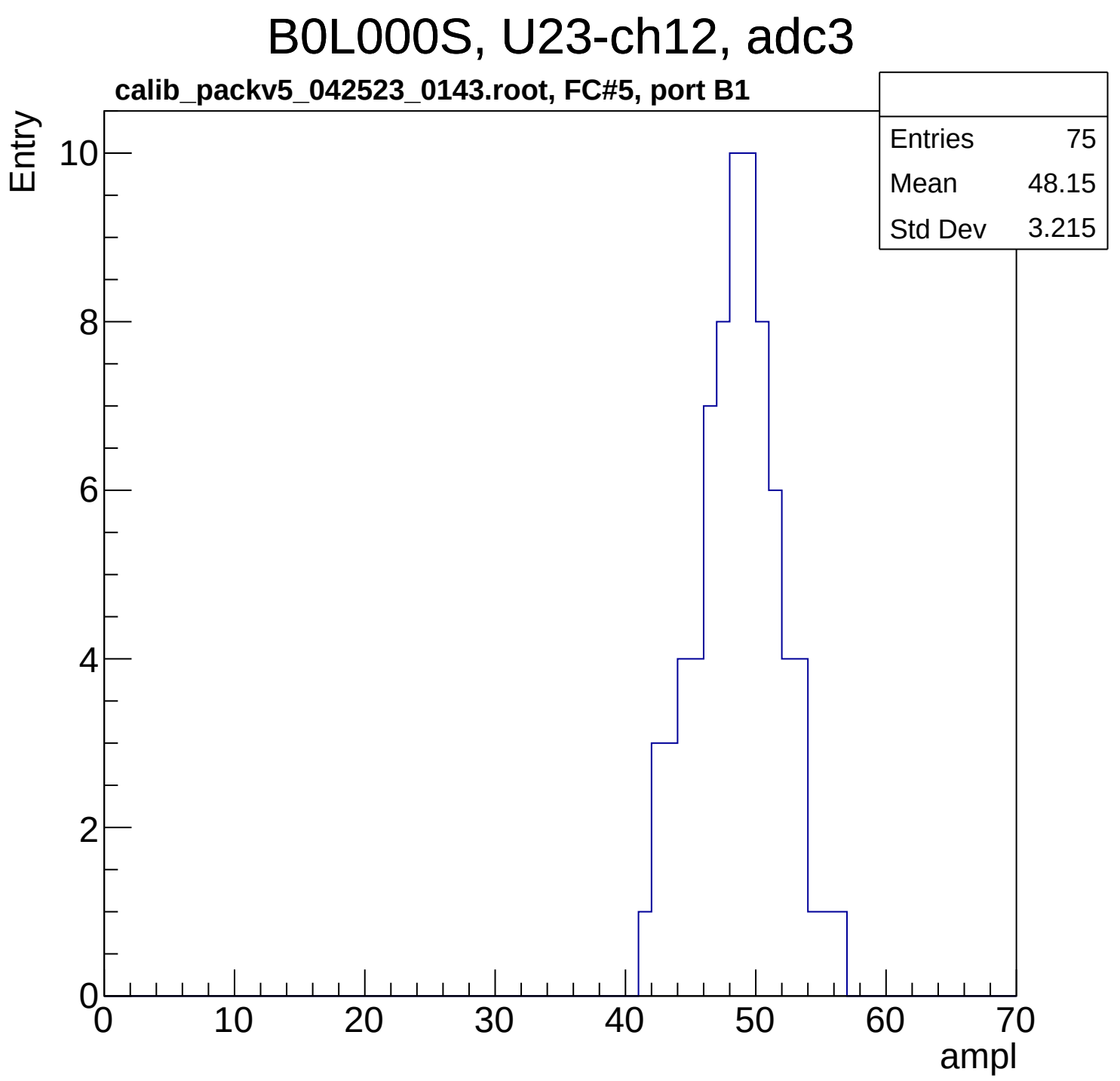

B0L000S, U23-ch11, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62.11
Std Dev 0.737
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

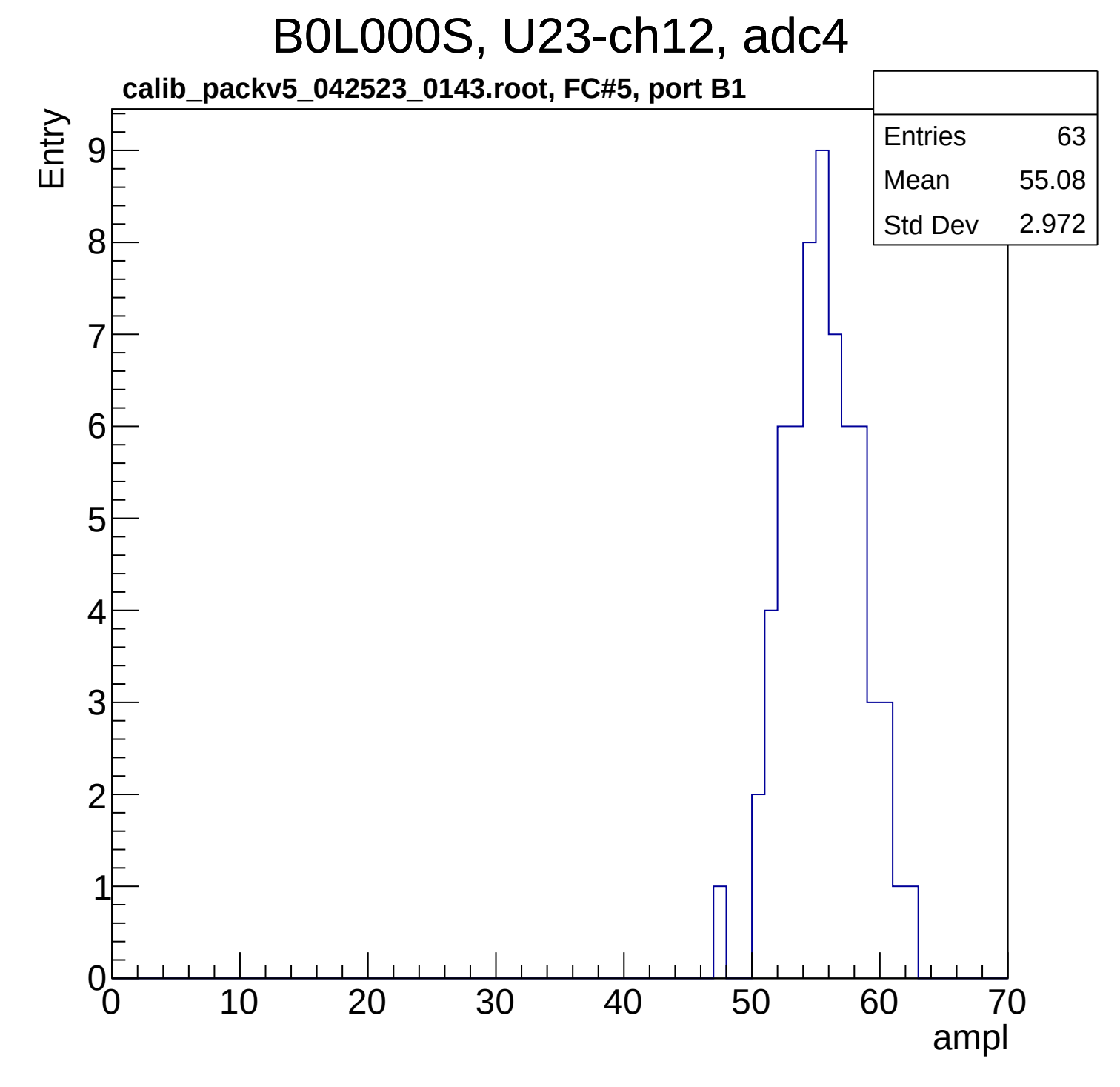

B0L000S, U23-ch11, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

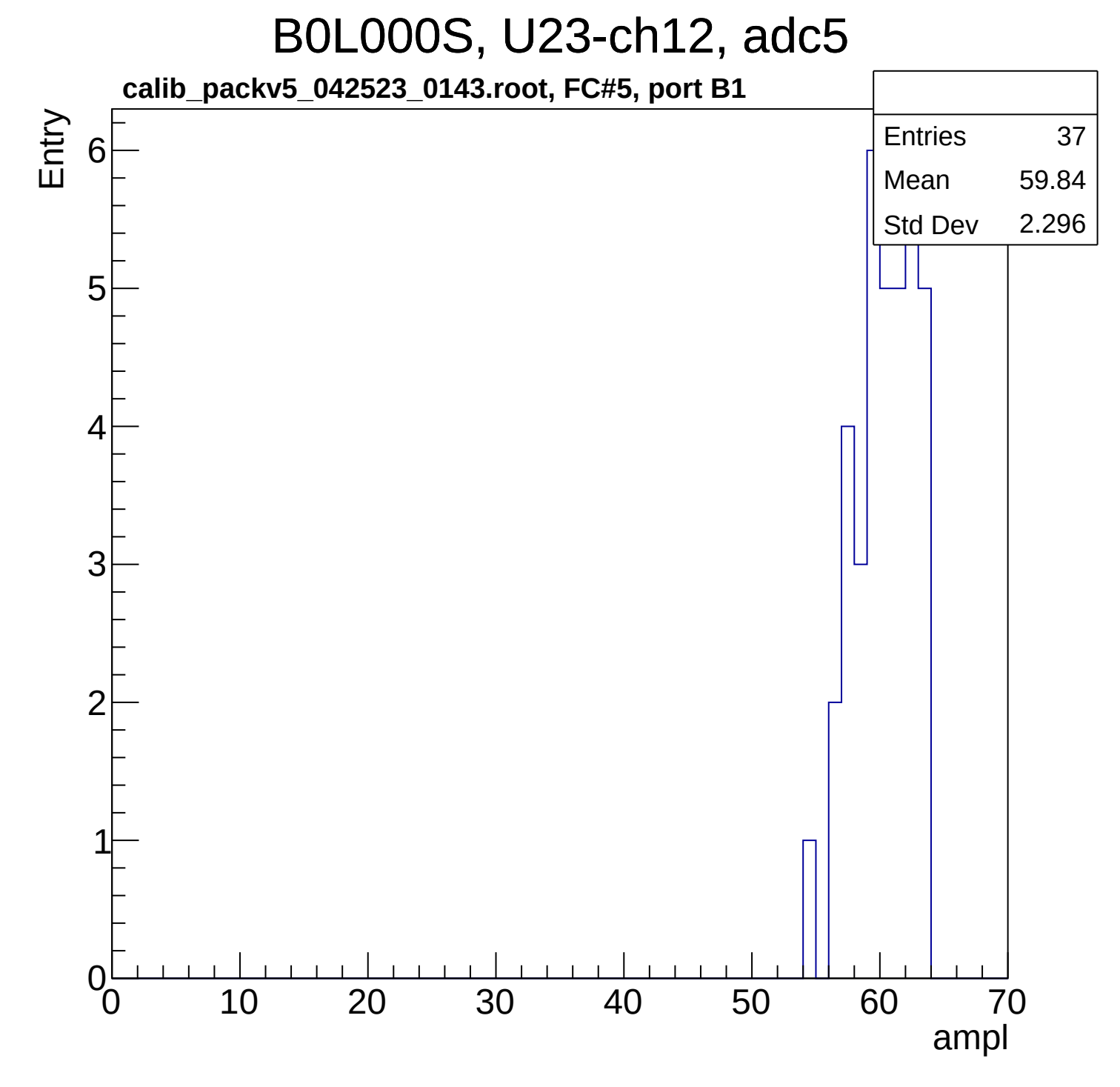

B0L000S, U23-ch12, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 29.37
Std Dev 3.259
Gaus mean : 29.8373
Gaus Width: 3.3250
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

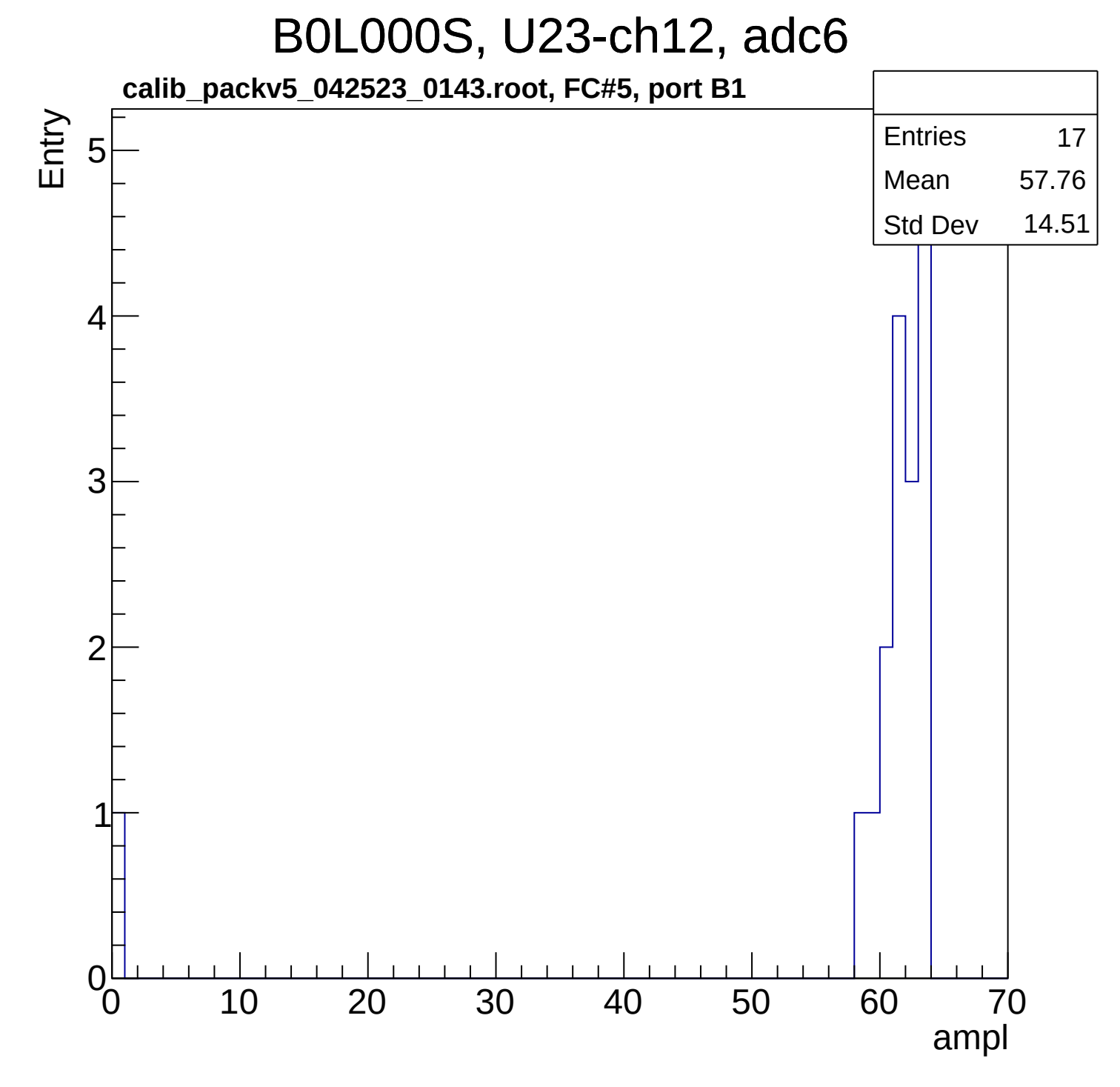

B0L000S, U23-ch12, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 34.92
Std Dev 3.285
Gaus mean : 35.6979
Gaus Width: 3.8499
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

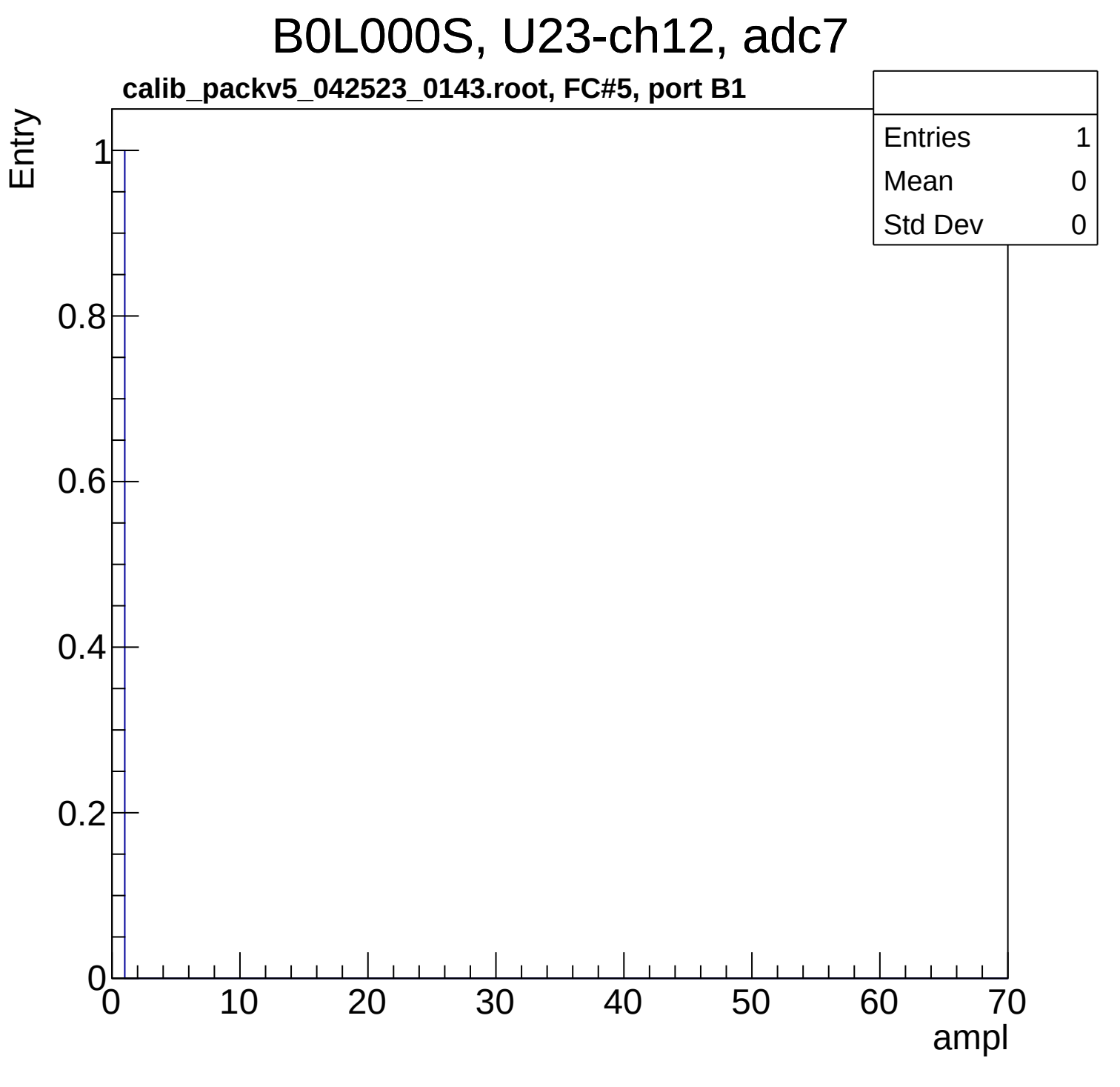

B0L000S, U23-ch12, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 41.38
Std Dev 2.841
Gaus mean : 41.7631
Gaus Width: 3.4211
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

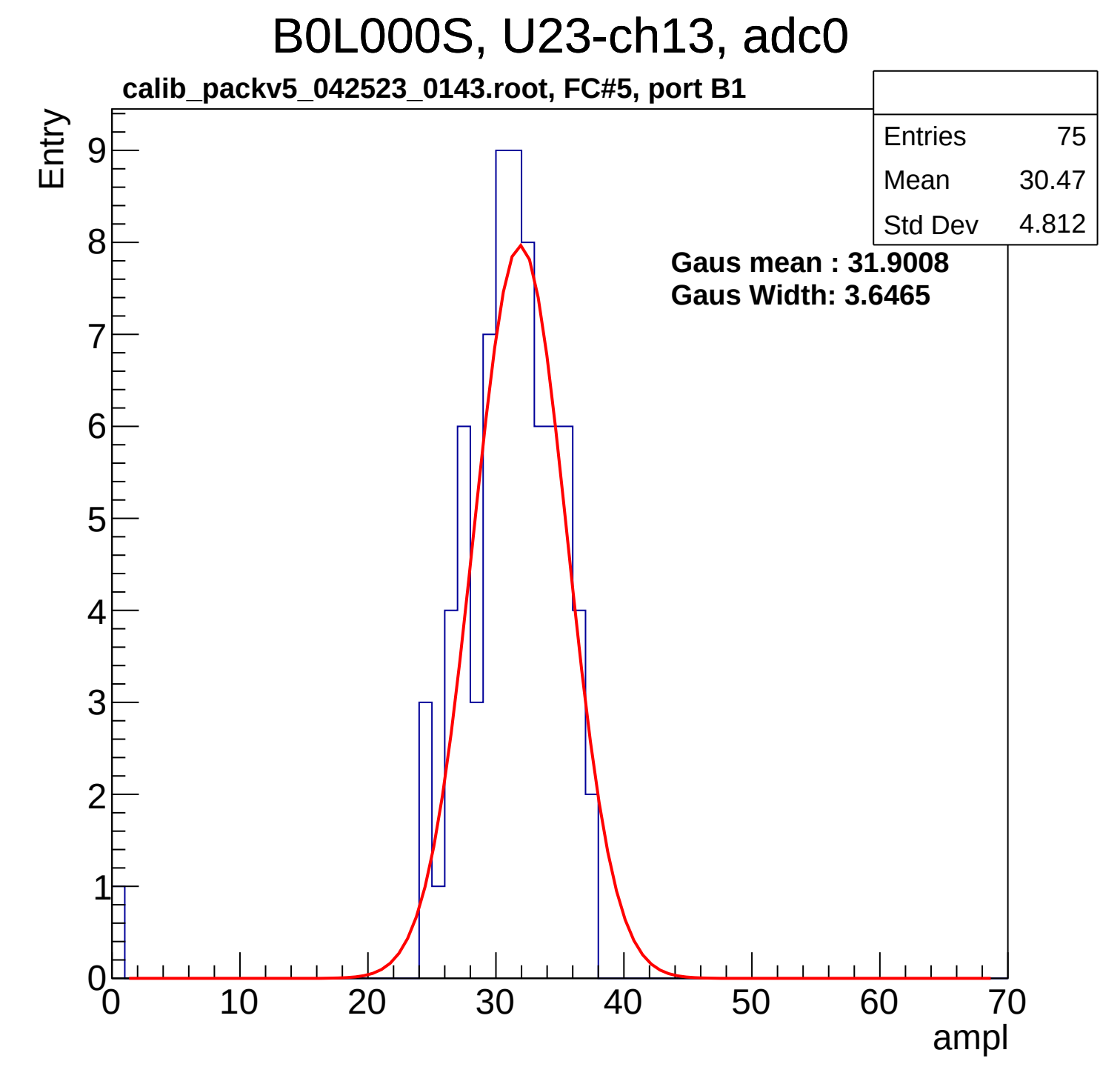

B0L000S, U23-ch12, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.15
Std Dev 3.215
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

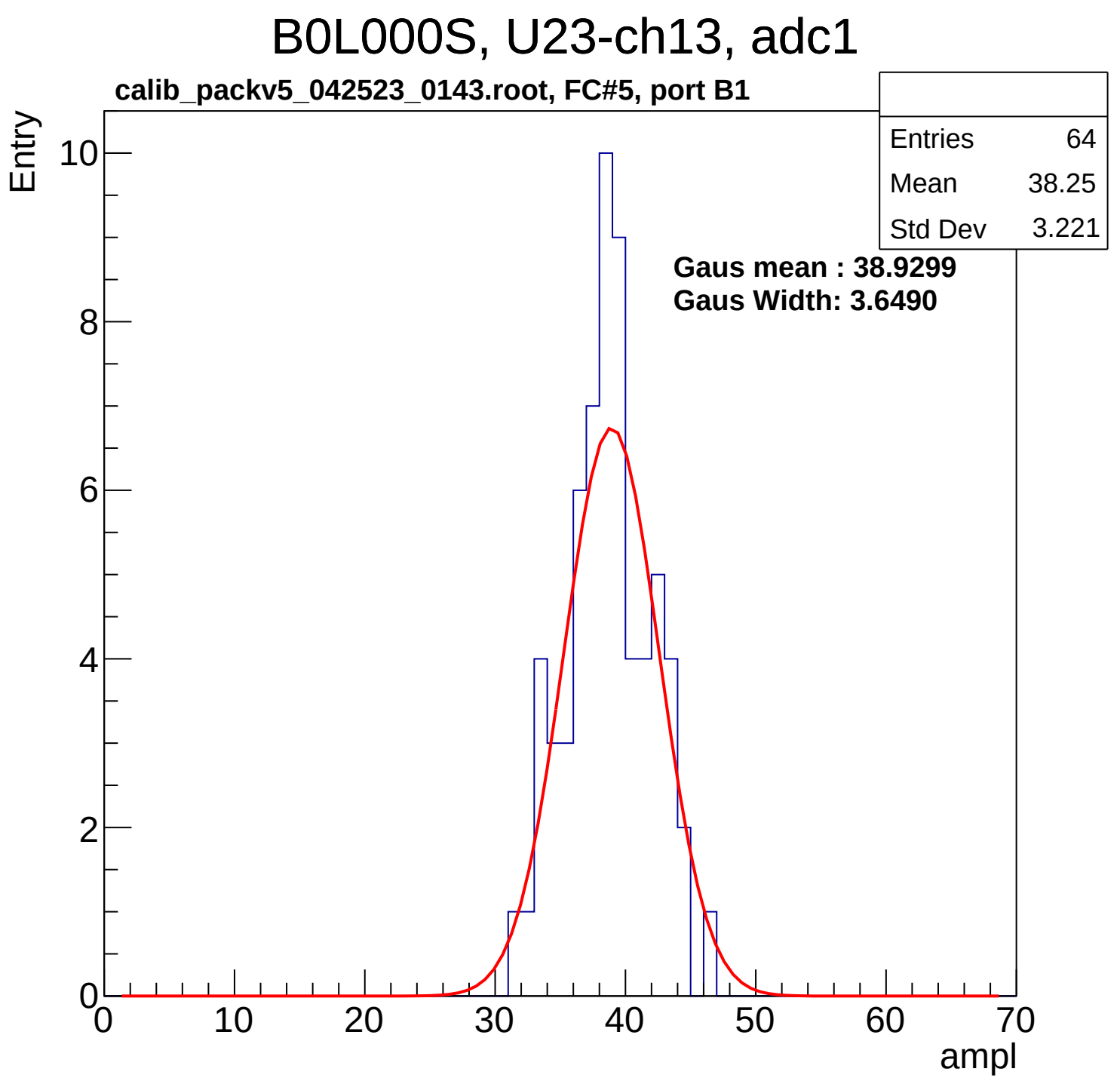

B0L000S, U23-ch12, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 55.08
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

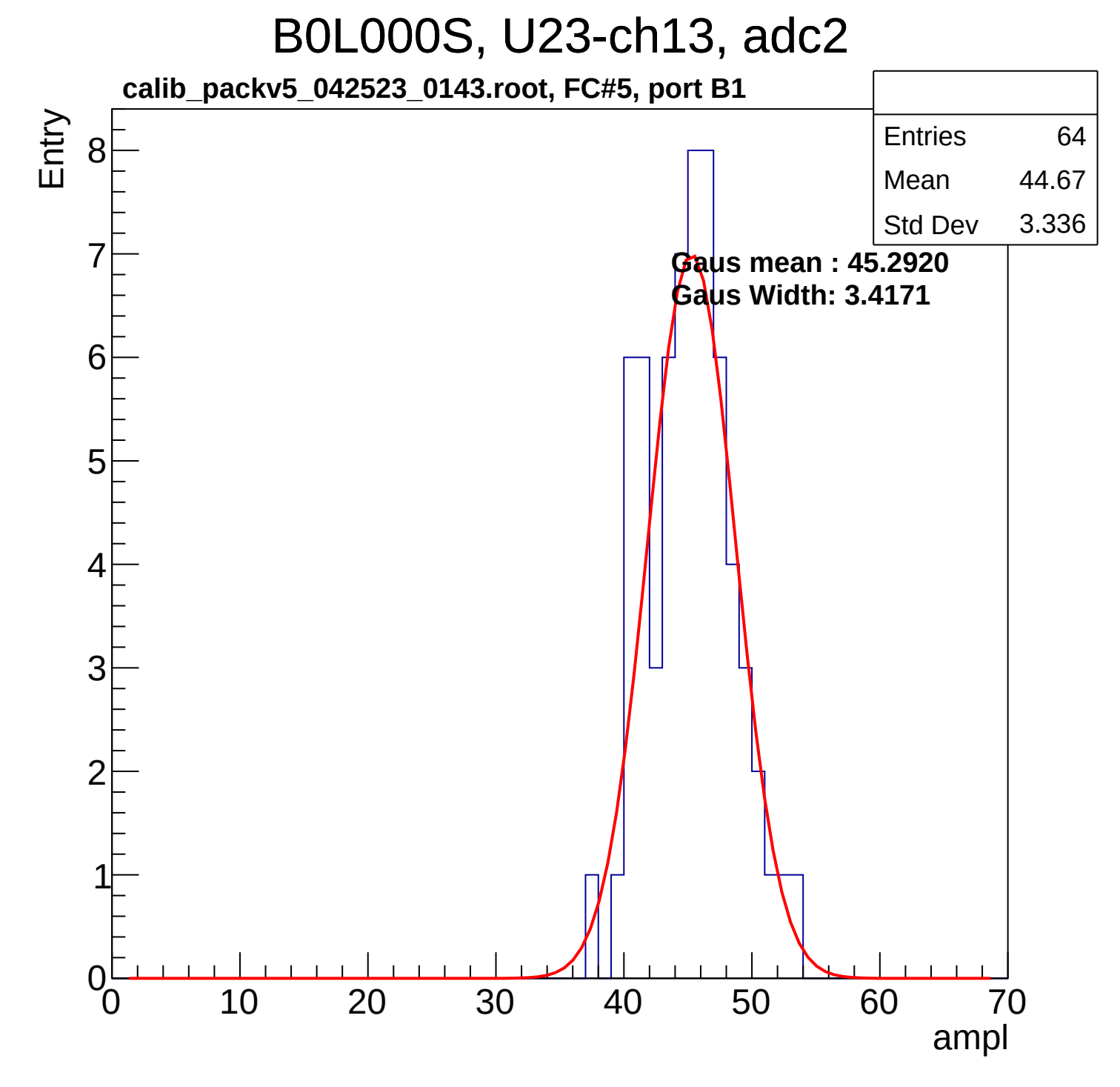

B0L000S, U23-ch12, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.84
Std Dev 2.296
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

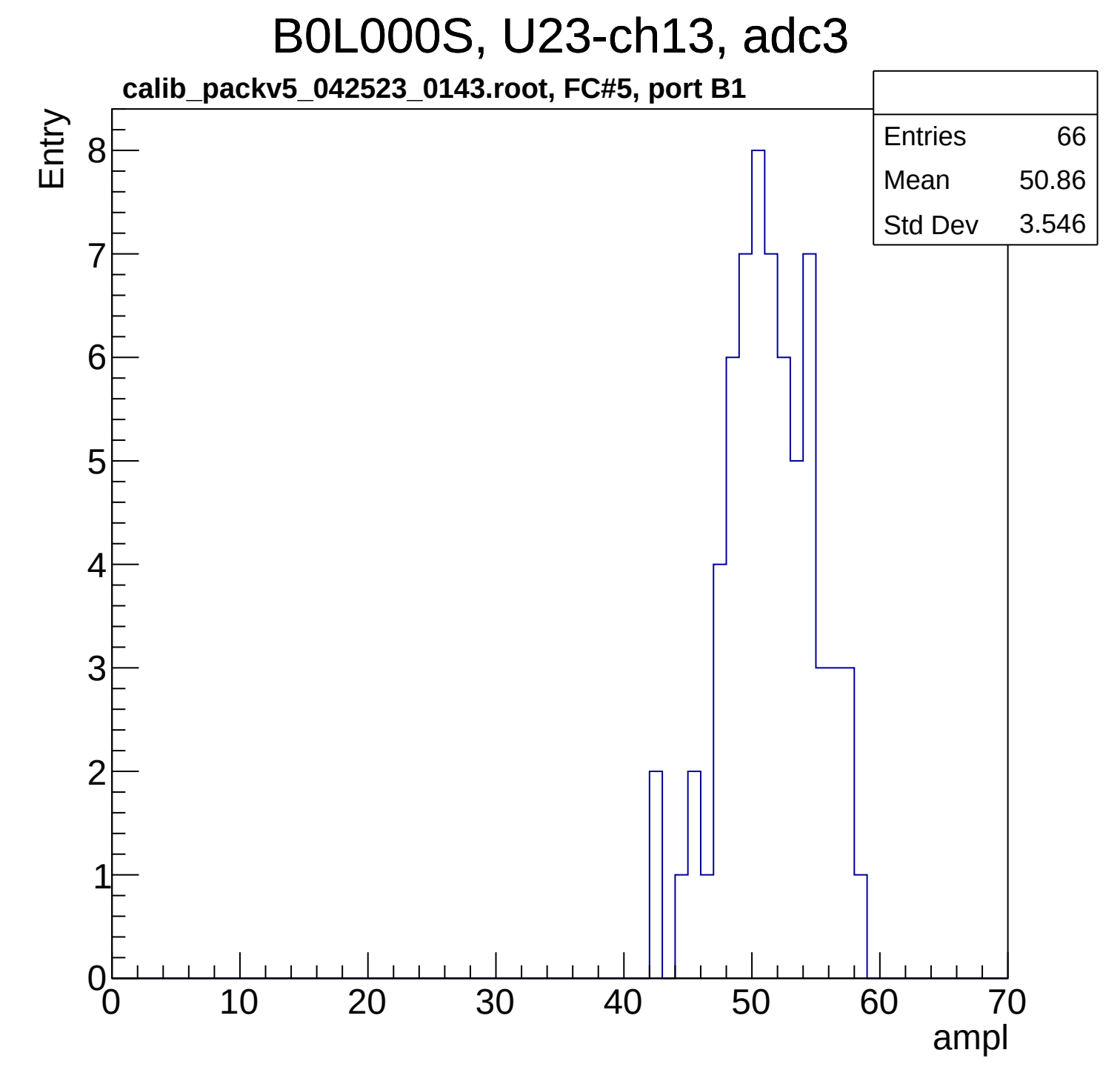

B0L000S, U23-ch12, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 57.76
Std Dev 14.51
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

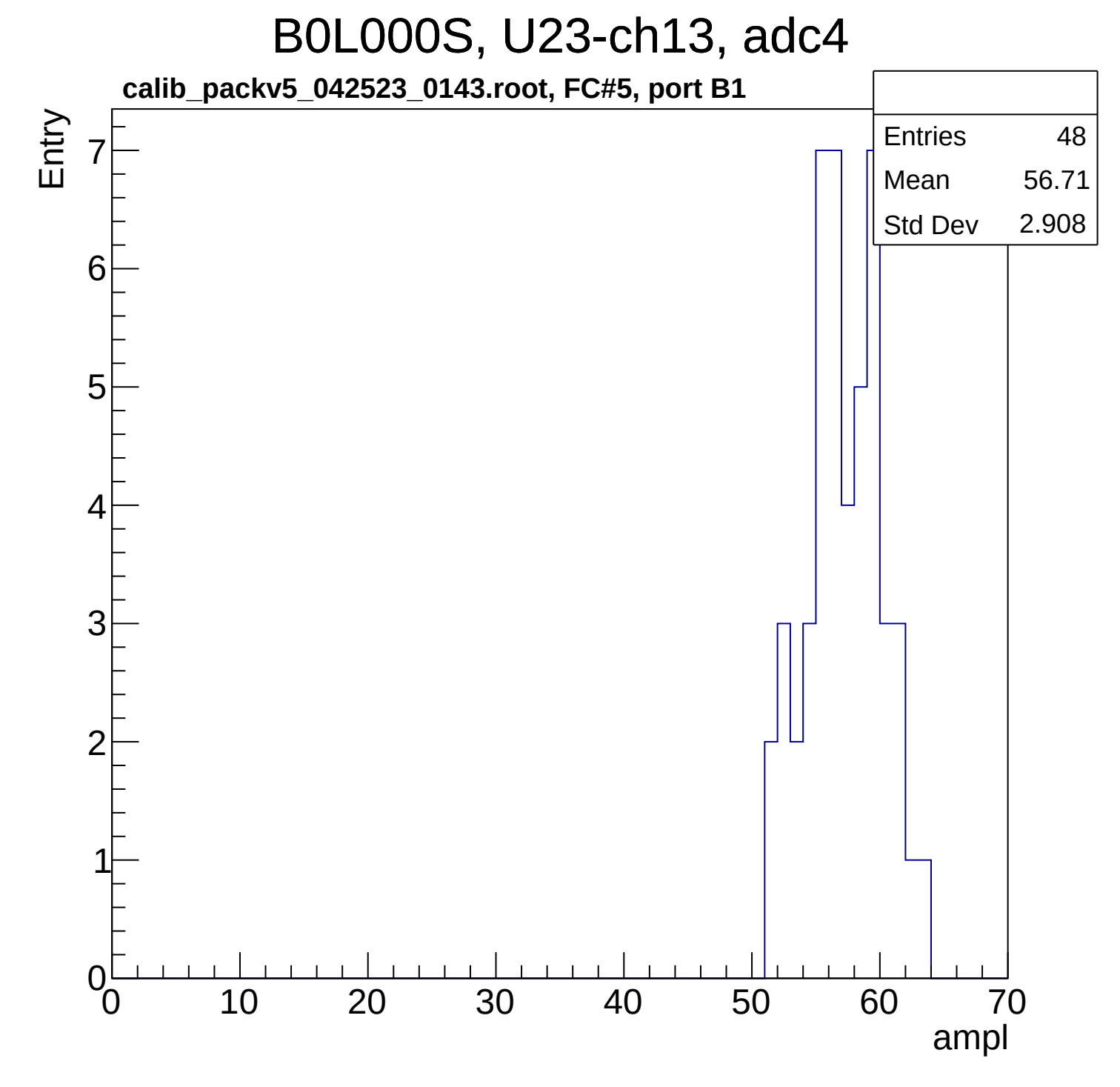

B0L000S, U23-ch12, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

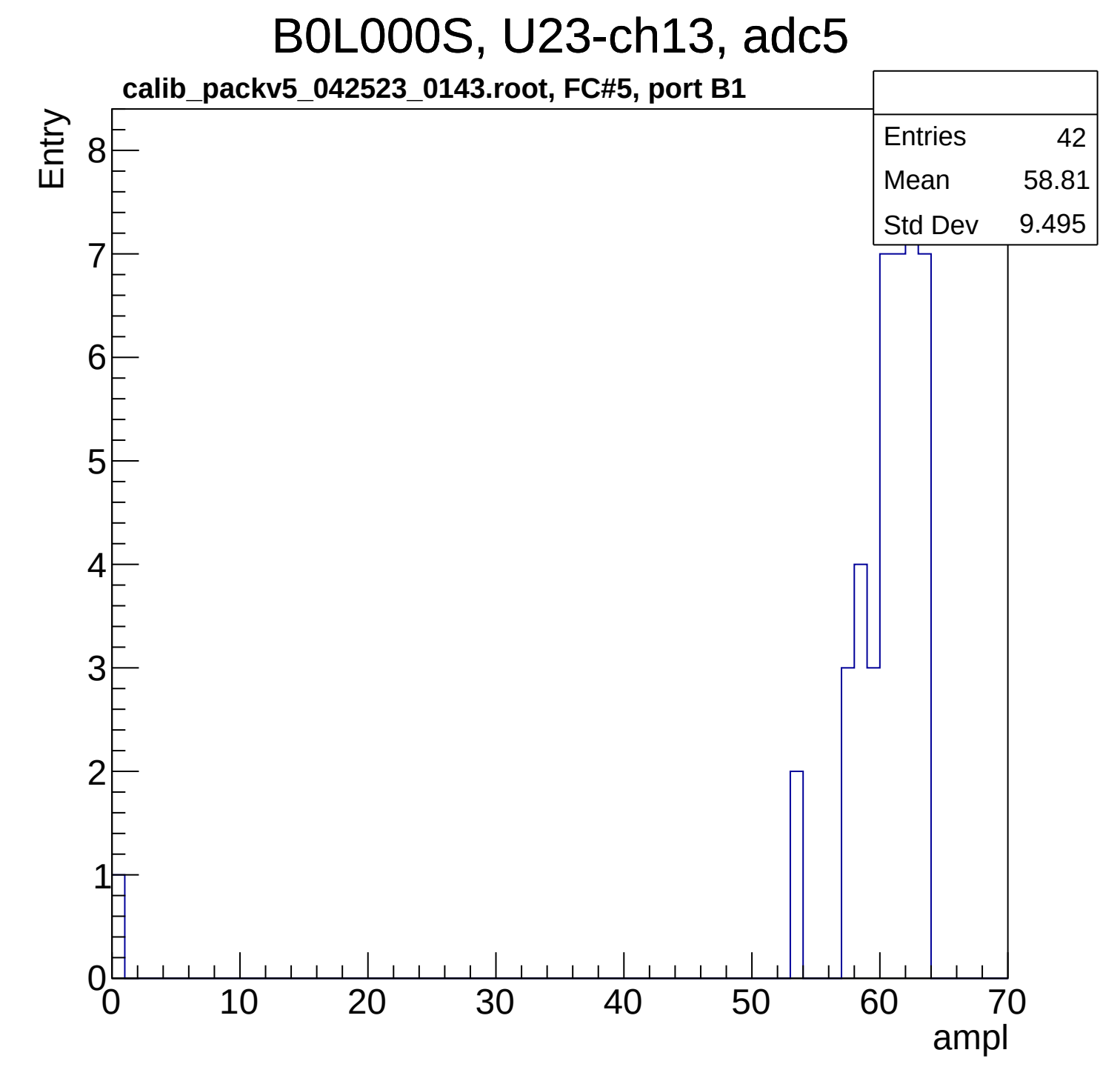

B0L000S, U23-ch13, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 30.47
Std Dev 4.812
Gaus mean : 31.9008
Gaus Width: 3.6465
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

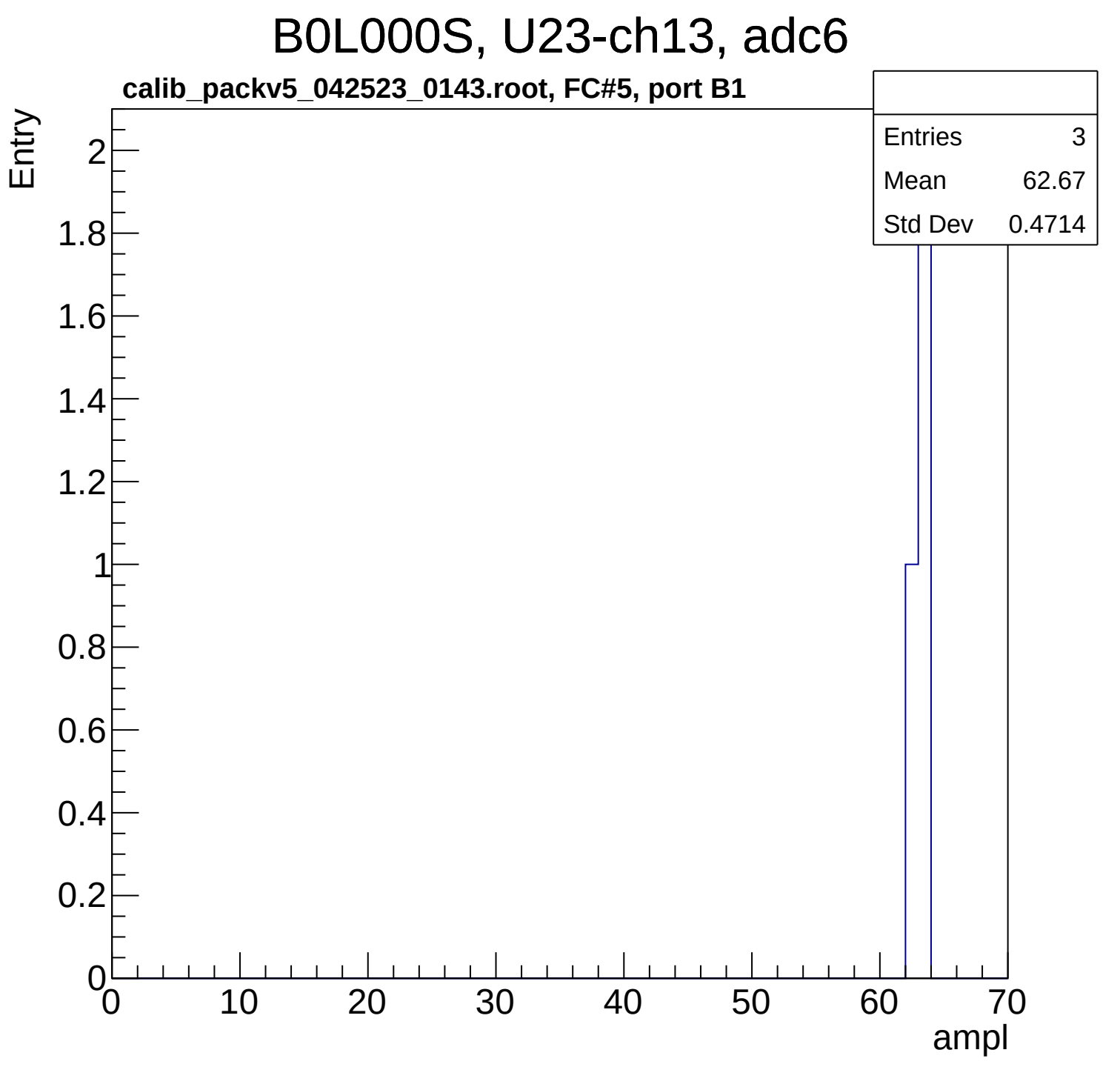

B0L000S, U23-ch13, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 38.25
Std Dev 3.221
Gaus mean : 38.9299
Gaus Width: 3.6490
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

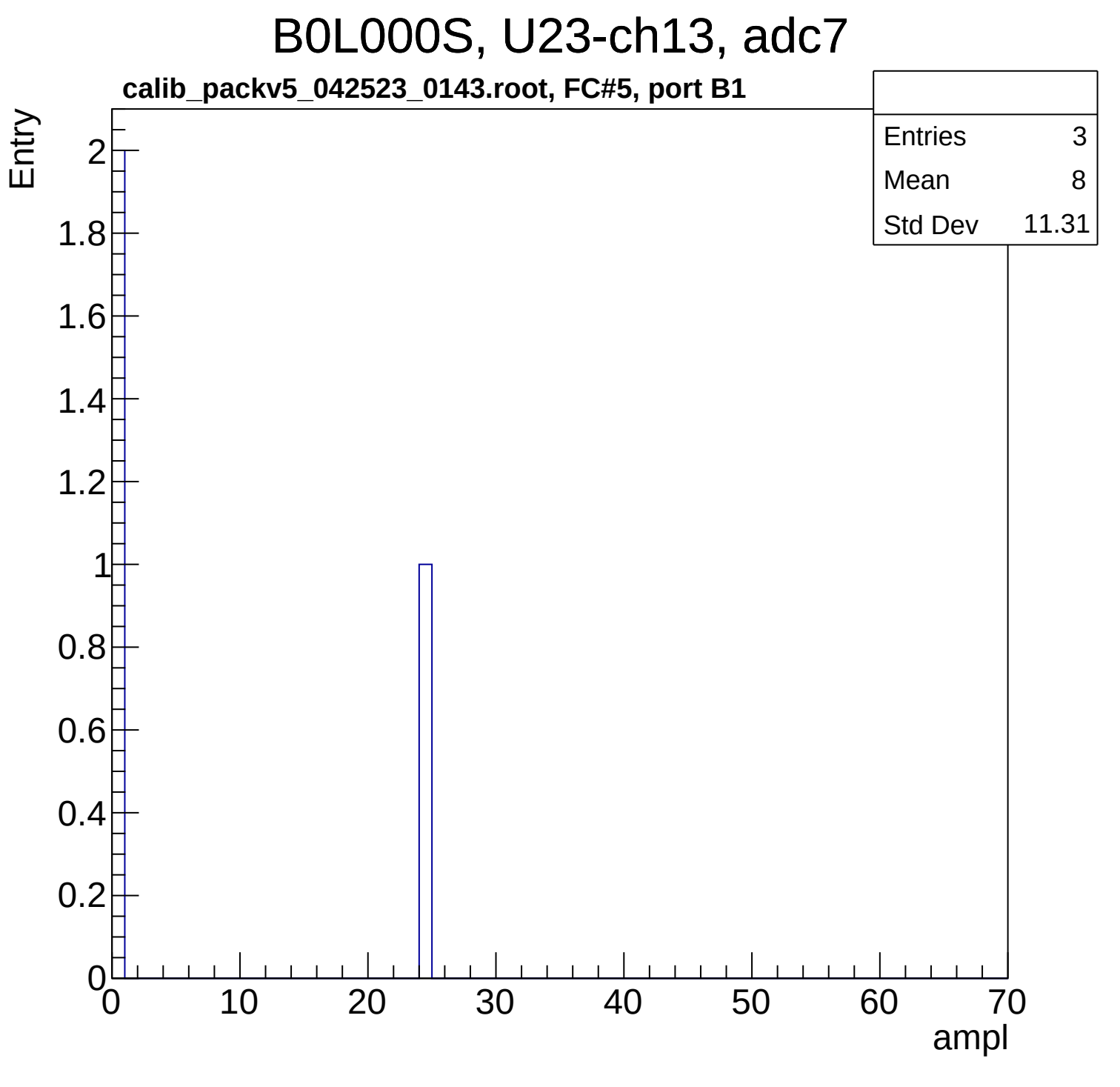

B0L000S, U23-ch13, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.67
Std Dev 3.336
Gaus mean : 45.2920
Gaus Width: 3.4171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

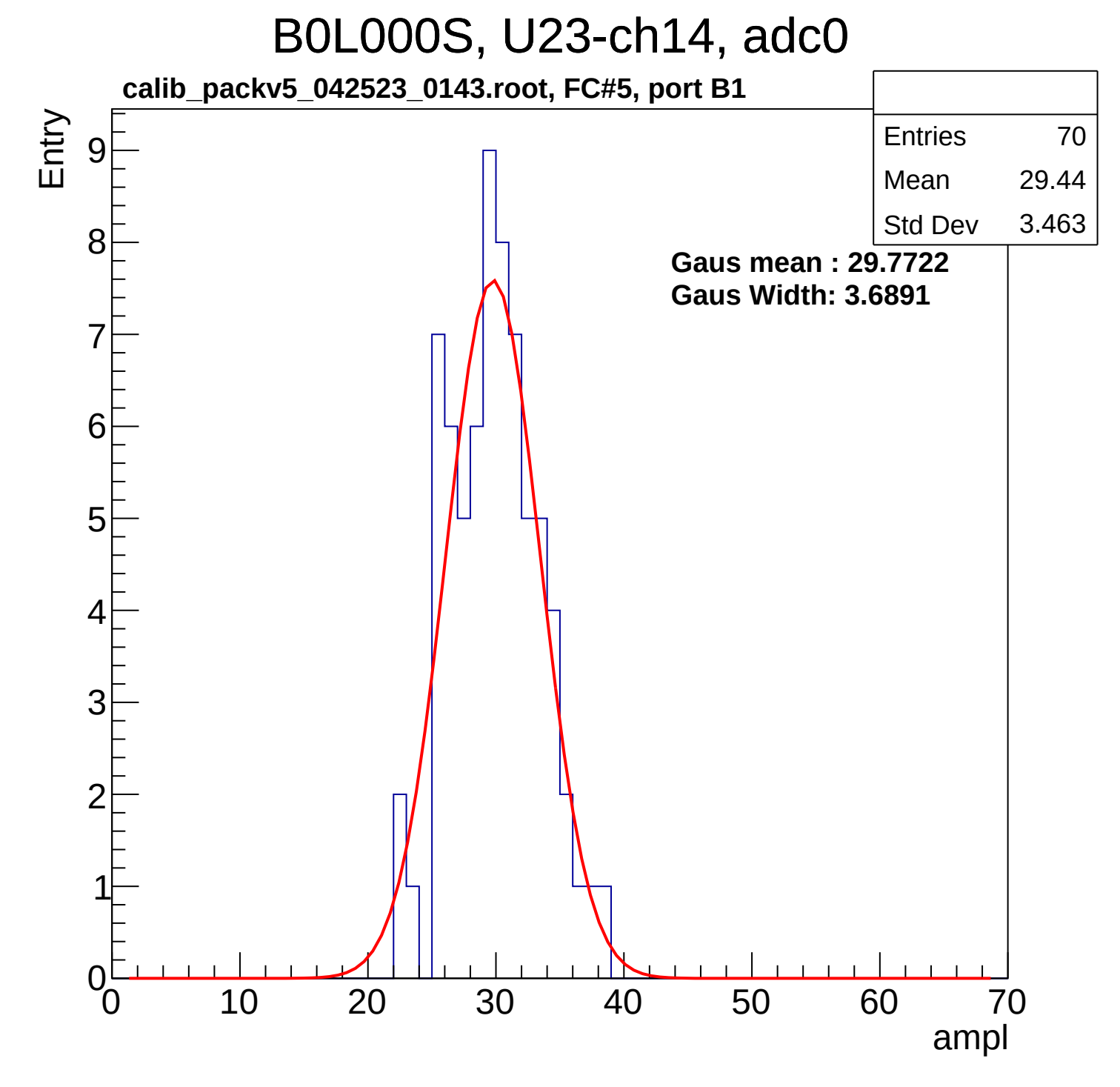

B0L000S, U23-ch13, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.86
Std Dev 3.546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

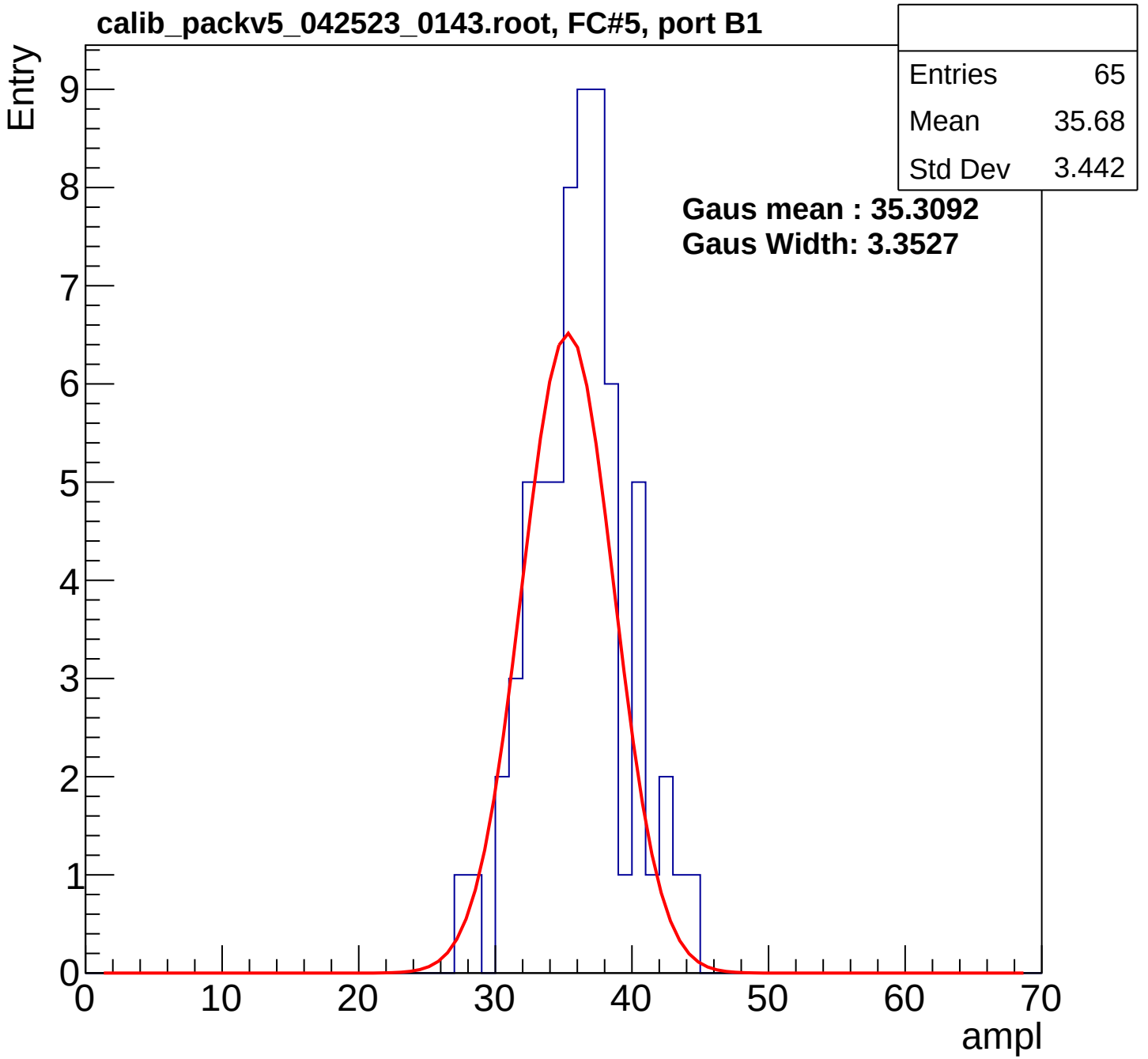

B0L000S, U23-ch13, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 56.71
Std Dev 2.908
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

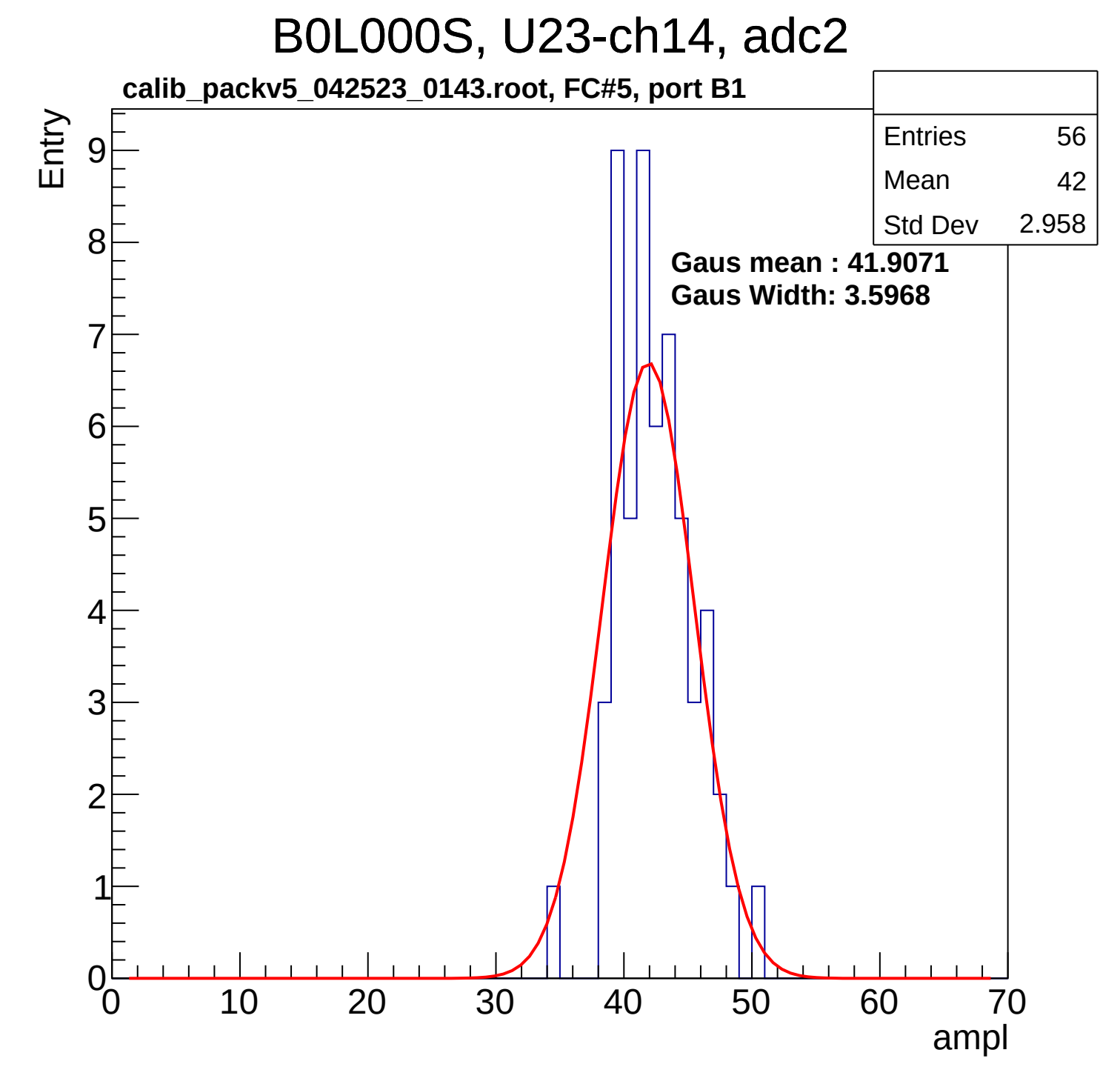

B0L000S, U23-ch13, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.81
Std Dev 9.495
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

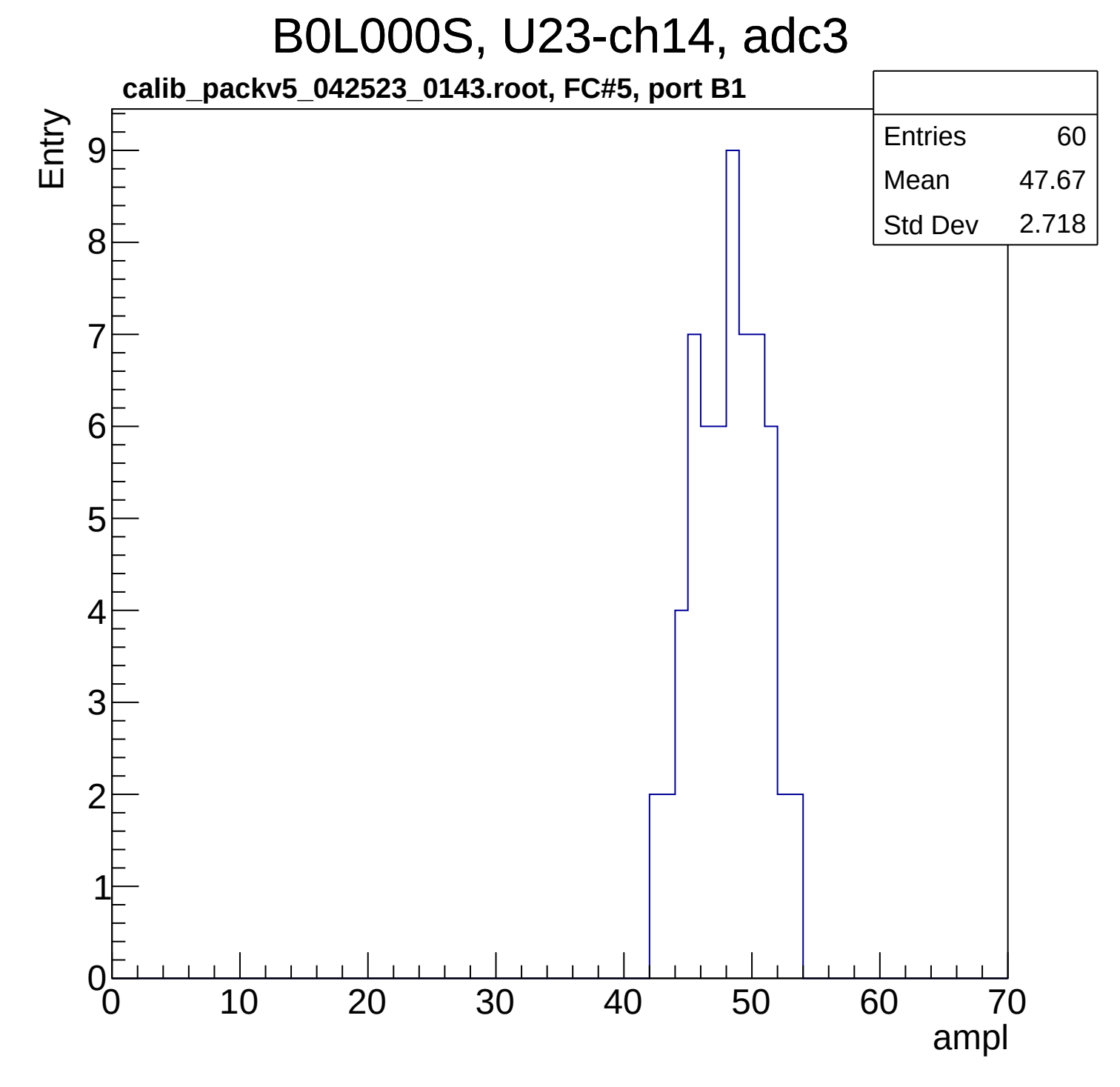

B0L000S, U23-ch13, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

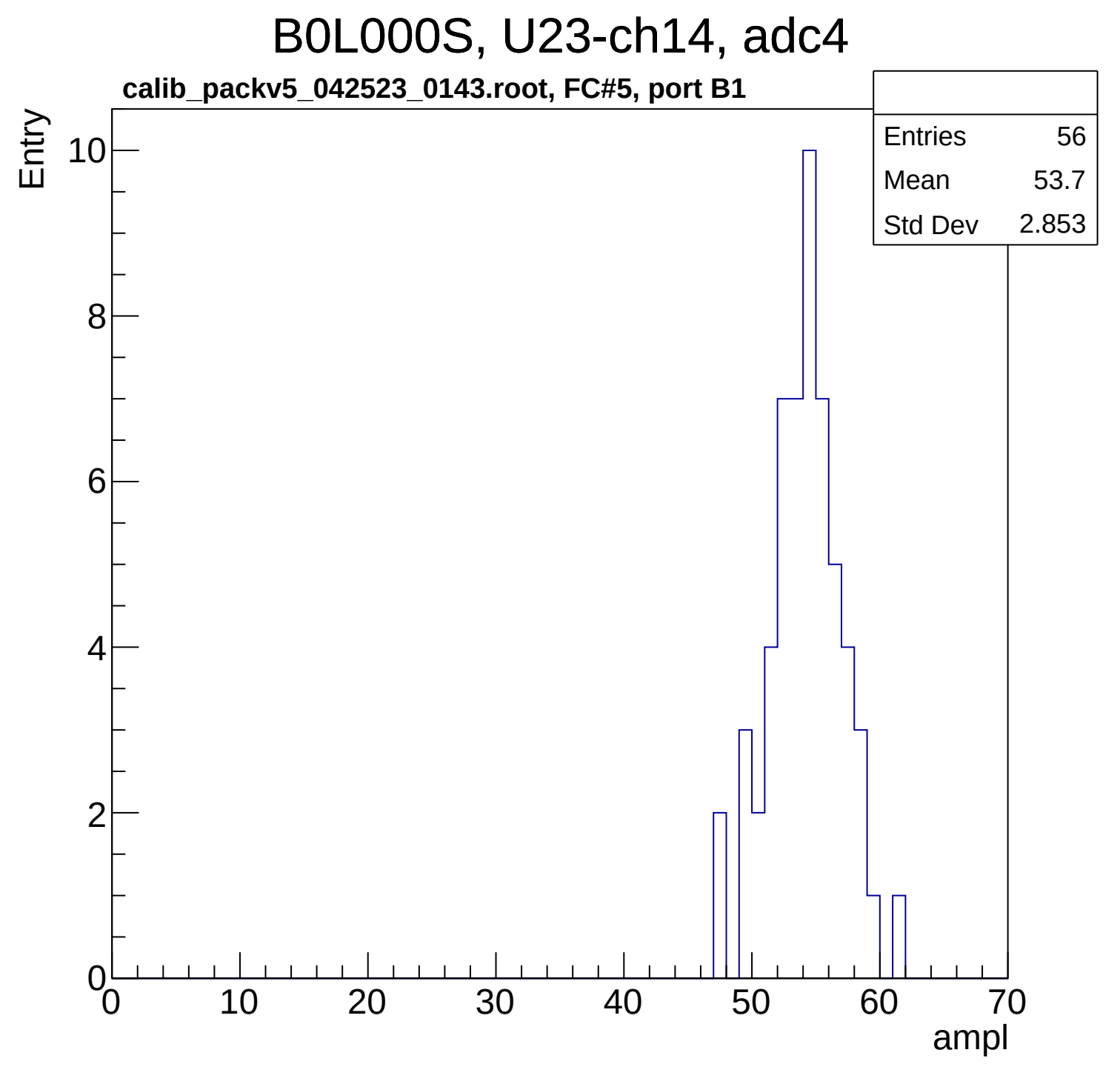

B0L000S, U23-ch13, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

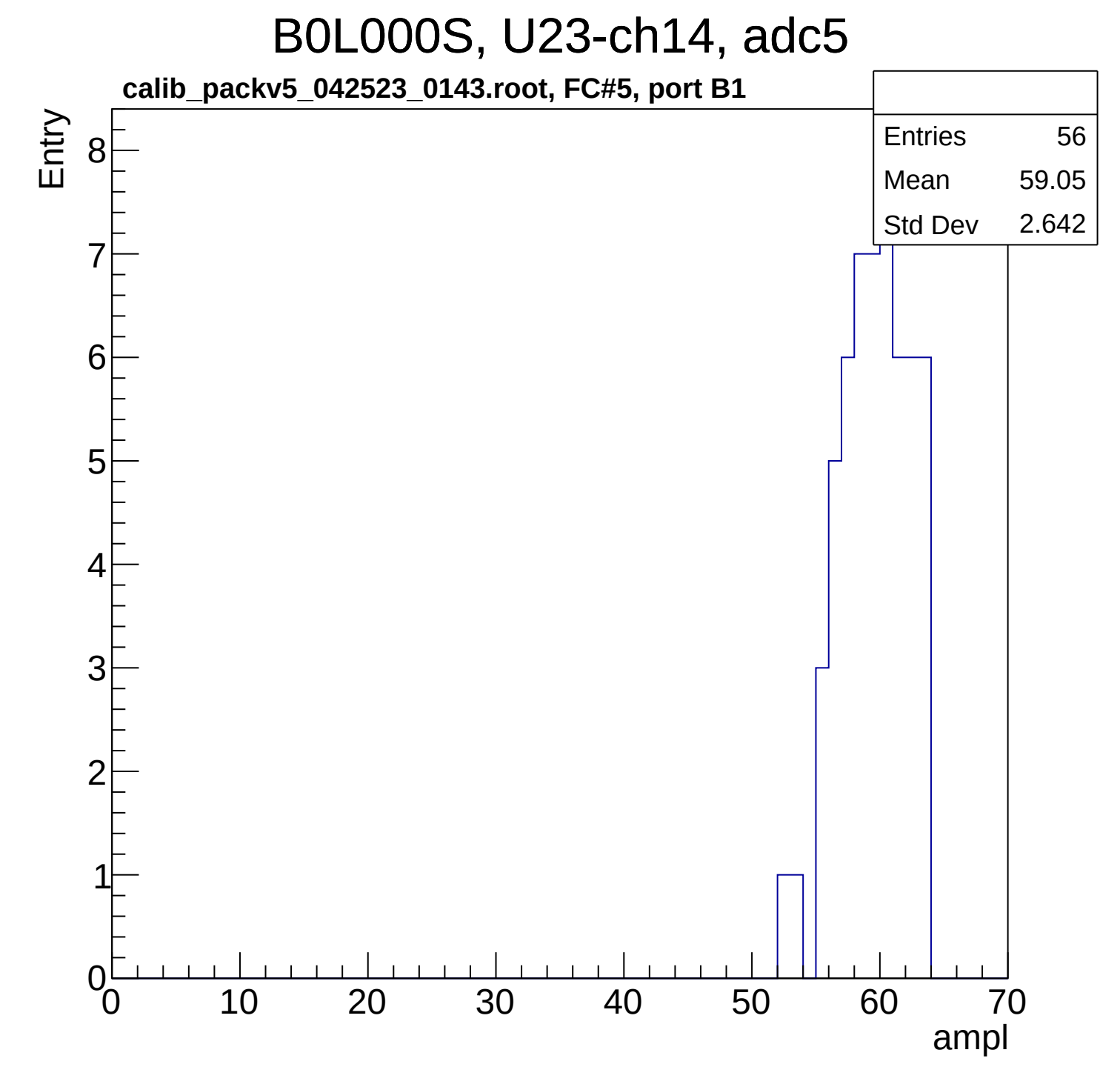

B0L000S, U23-ch14, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.44
Std Dev 3.463
Gaus mean : 29.7722
Gaus Width: 3.6891
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

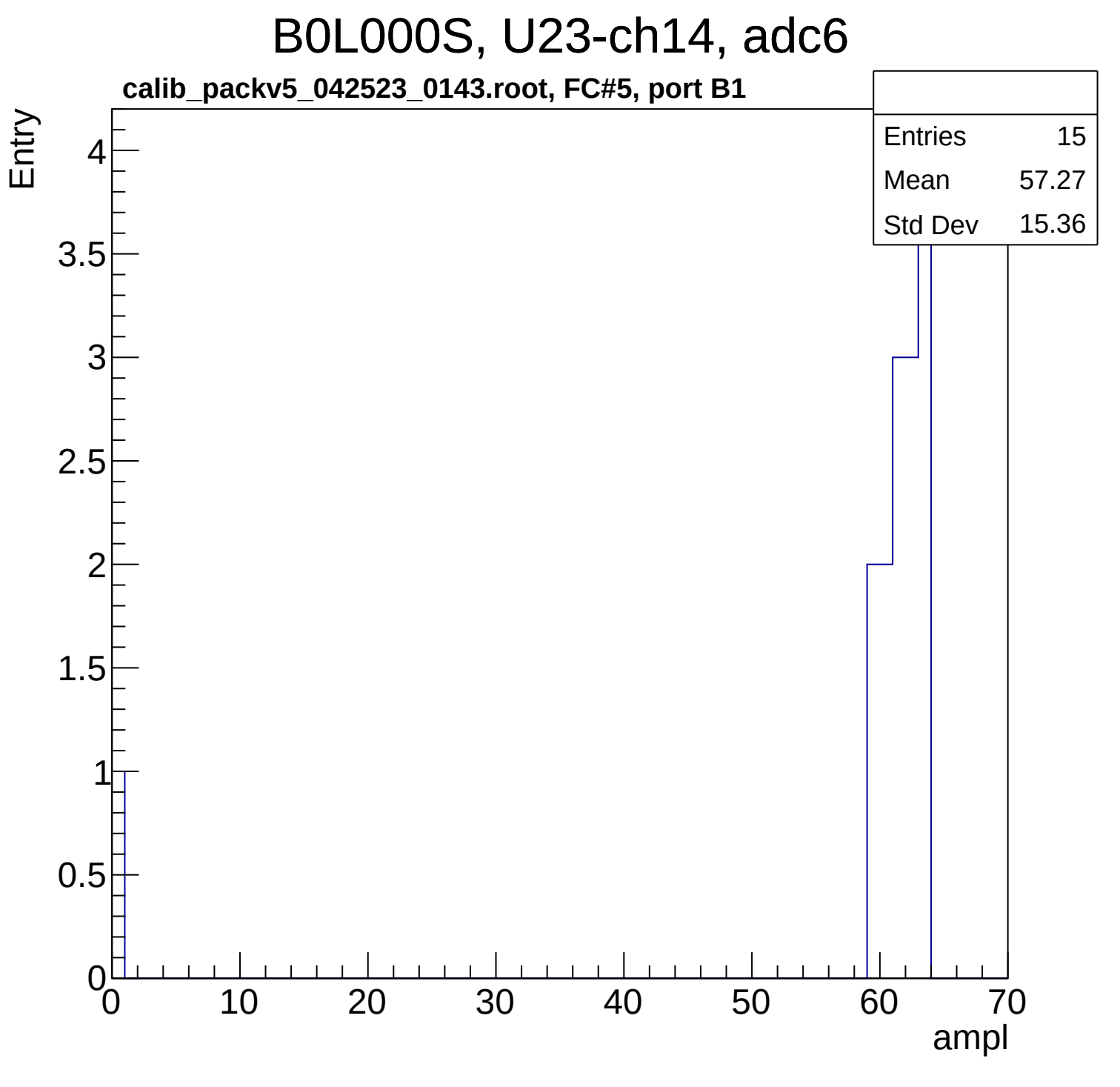

B0L000S, U23-ch14, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 35.68
Std Dev 3.442
Gaus mean : 35.3092
Gaus Width: 3.3527
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

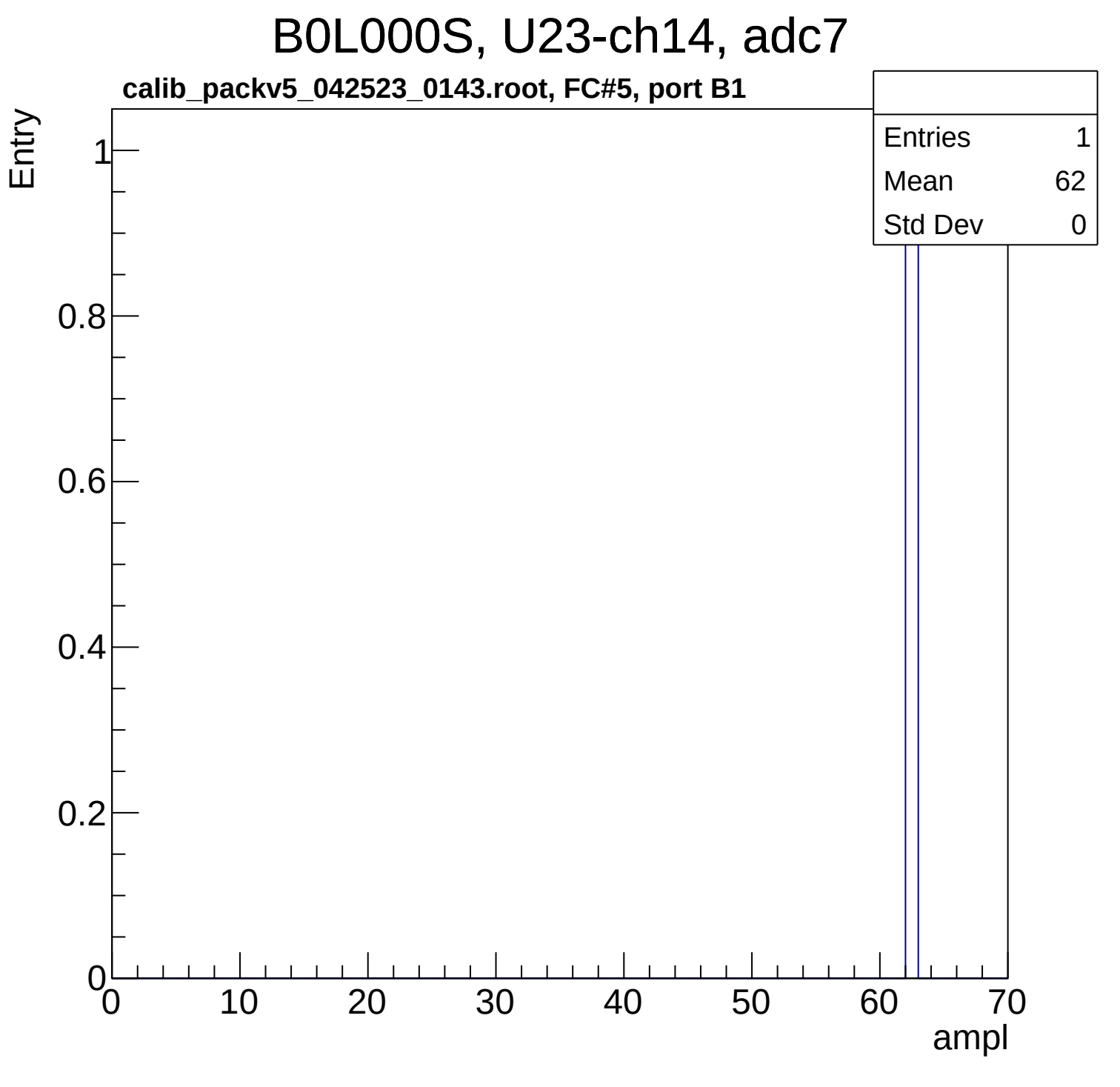

B0L000S, U23-ch14, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42
Std Dev 2.958
Gaus mean : 41.9071
Gaus Width: 3.5968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

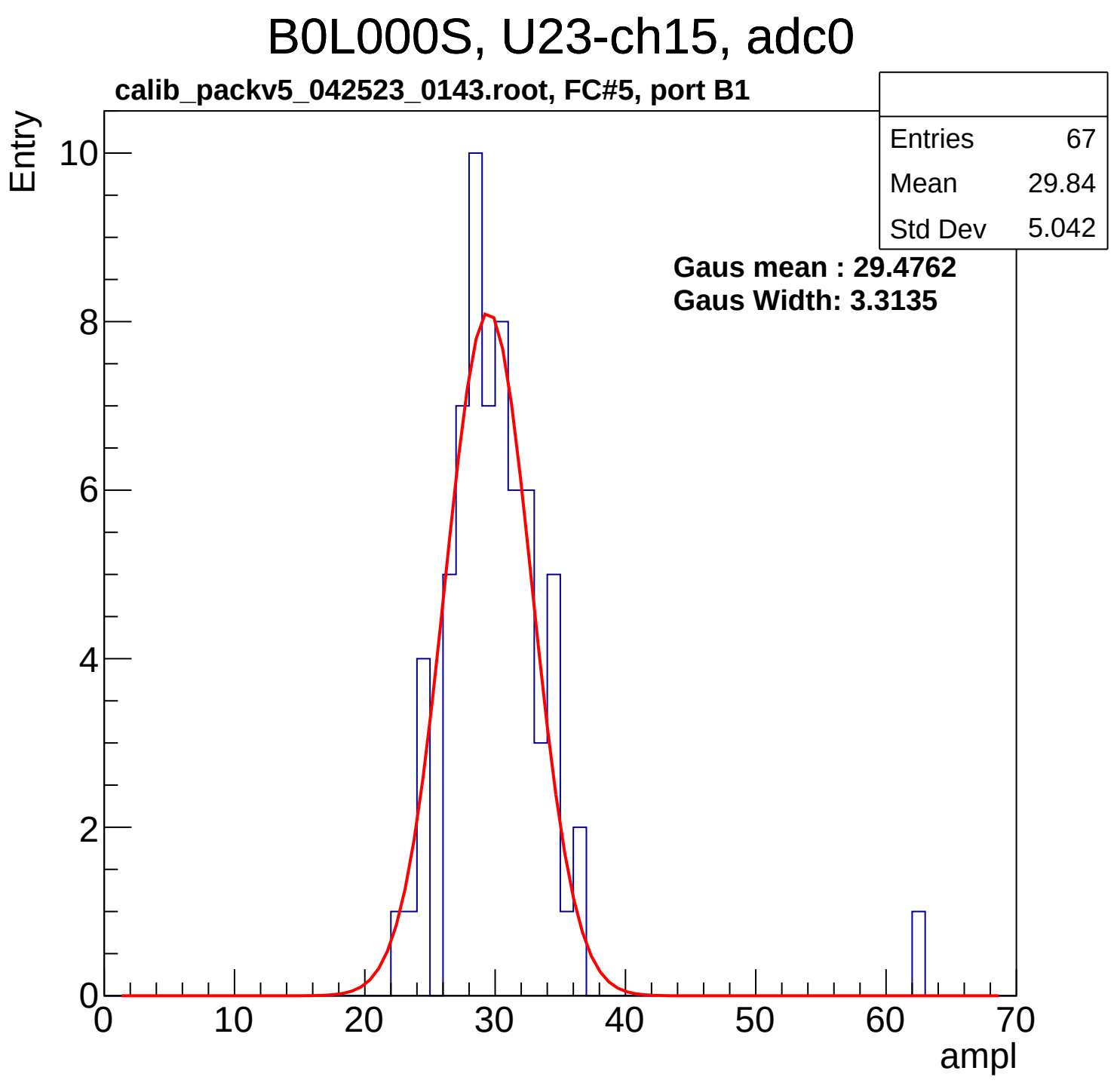

B0L000S, U23-ch14, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 47.67
Std Dev 2.718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

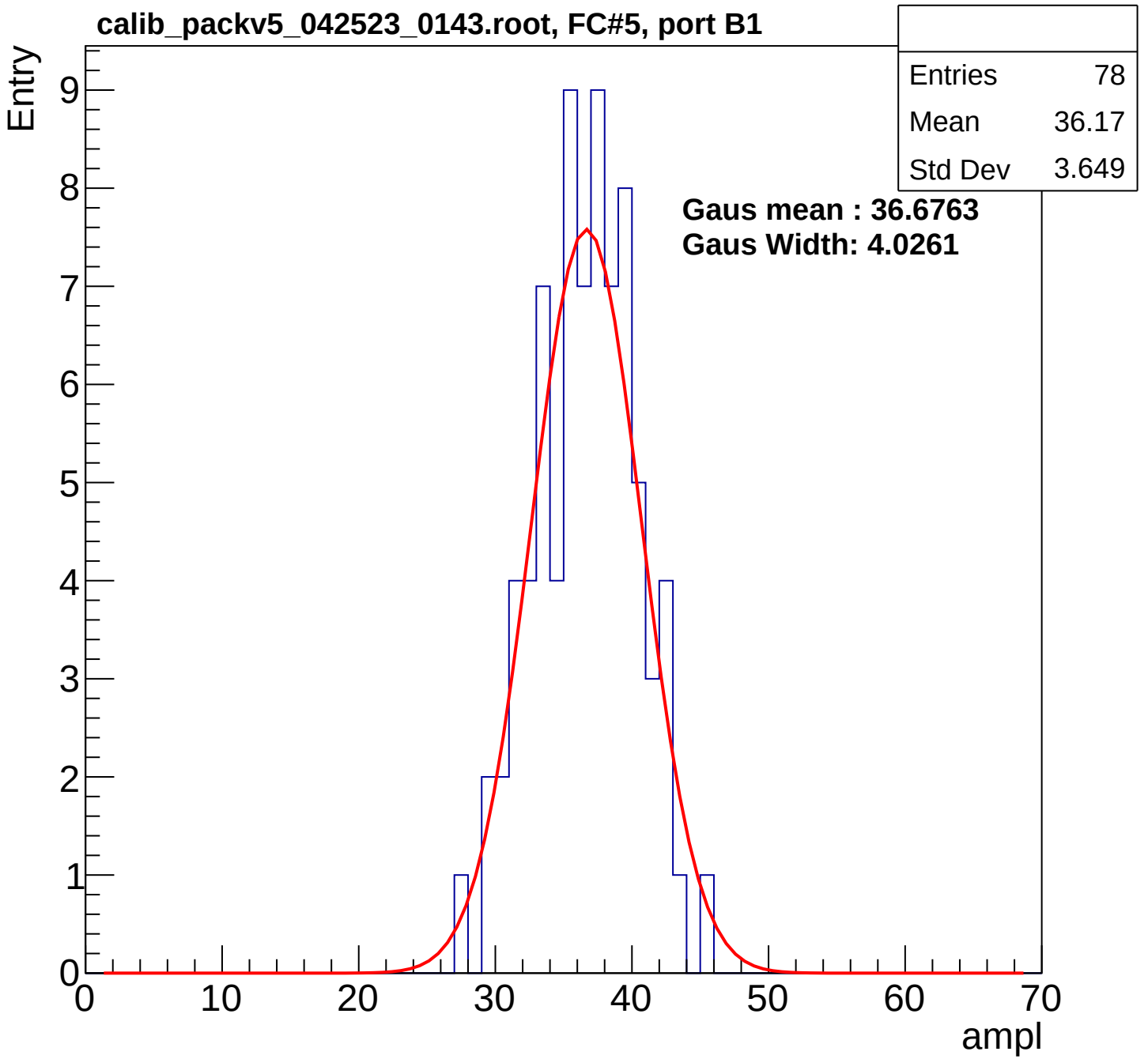

B0L000S, U23-ch14, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 53.7
Std Dev 2.853
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

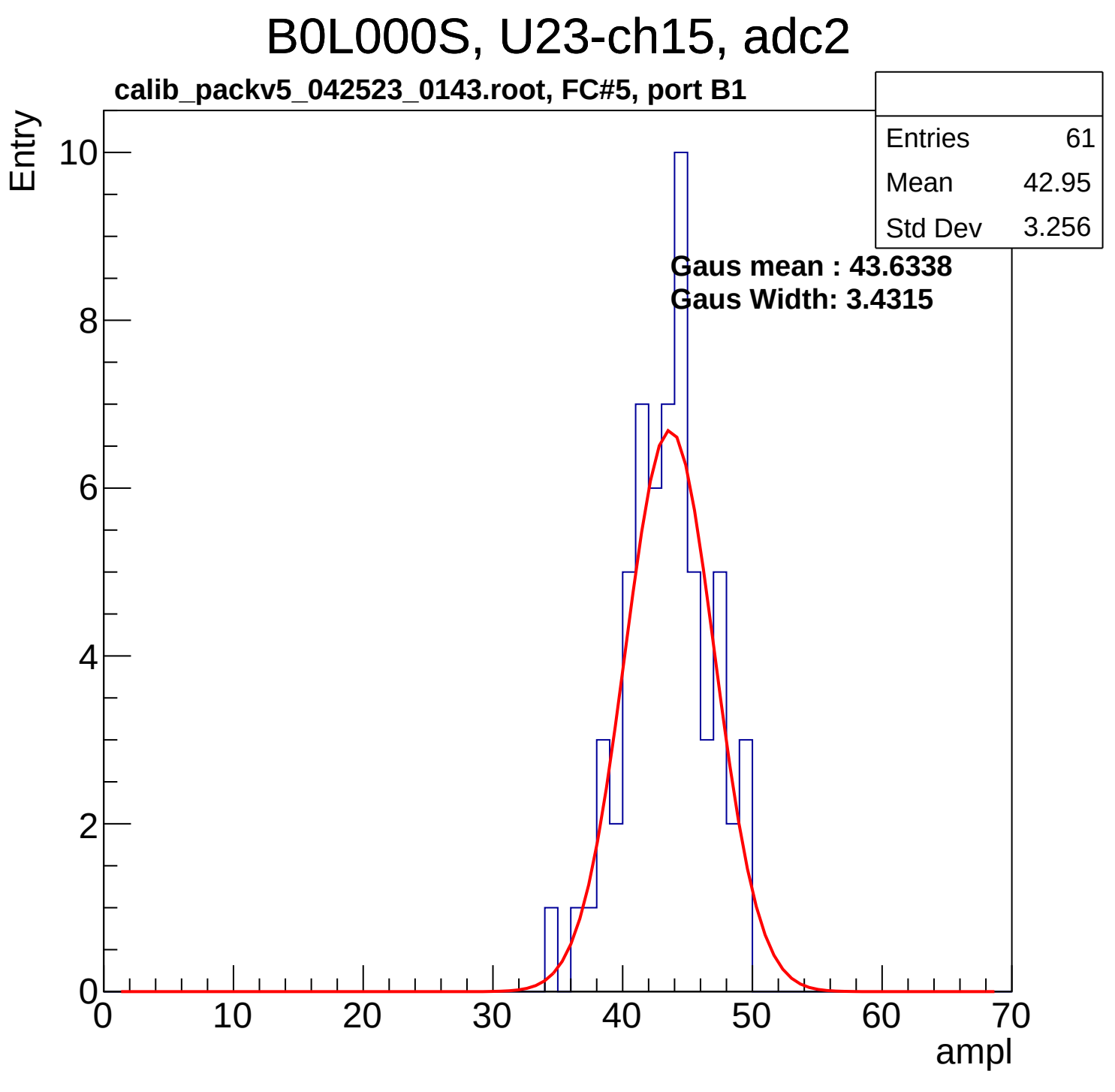

B0L000S, U23-ch14, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 59.05
Std Dev 2.642
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

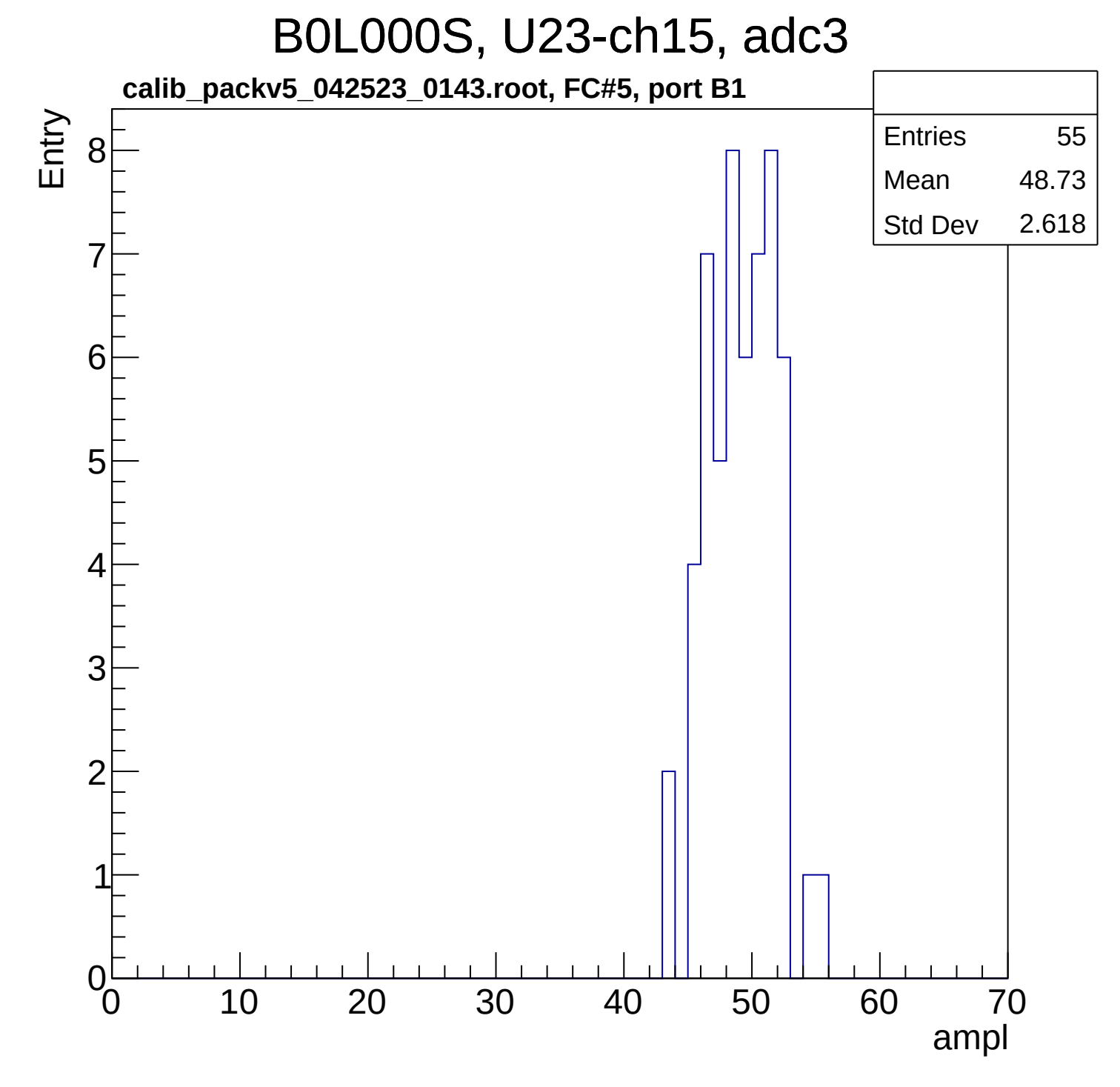

B0L000S, U23-ch14, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.27
Std Dev 15.36
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

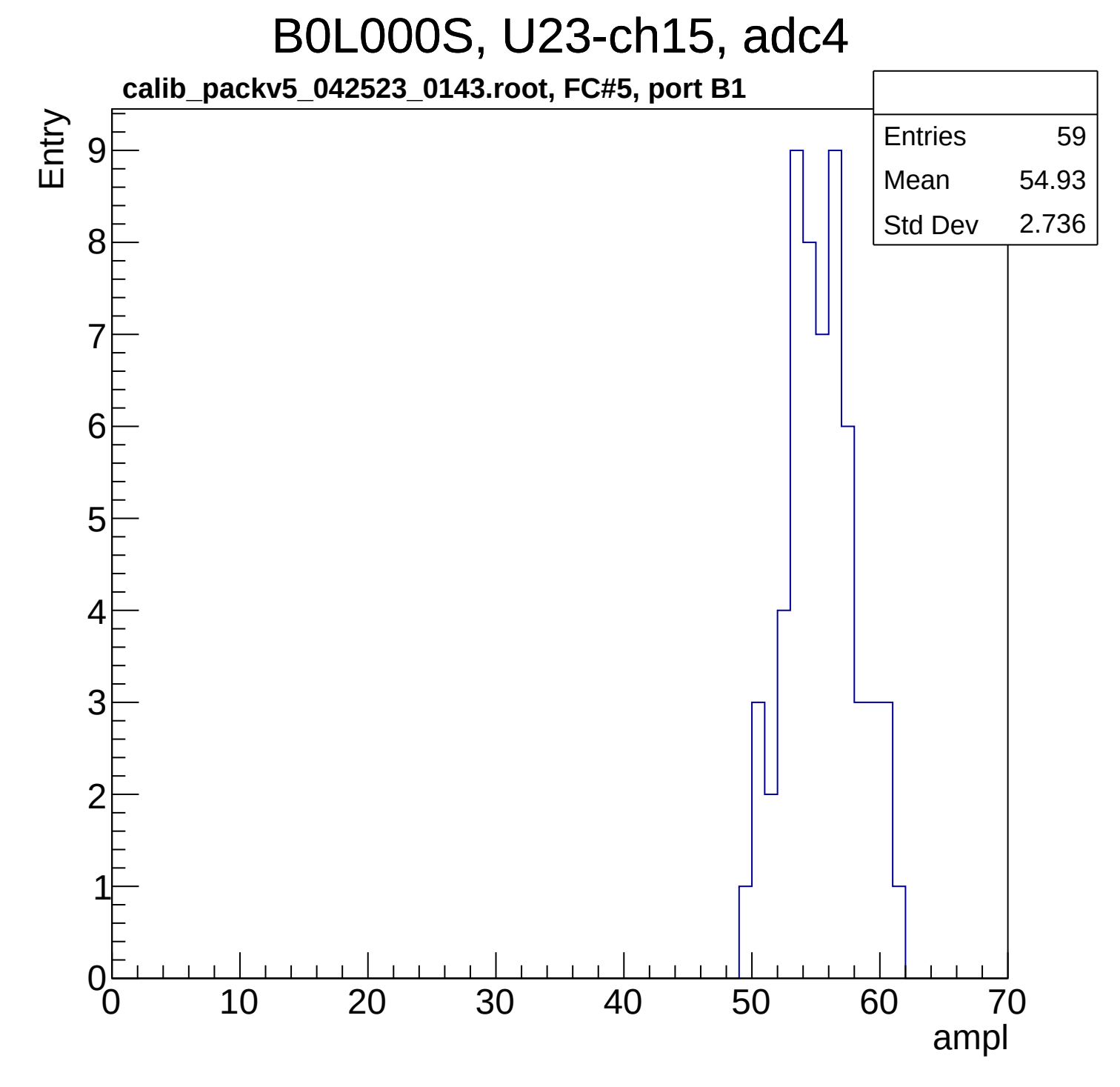

B0L000S, U23-ch14, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

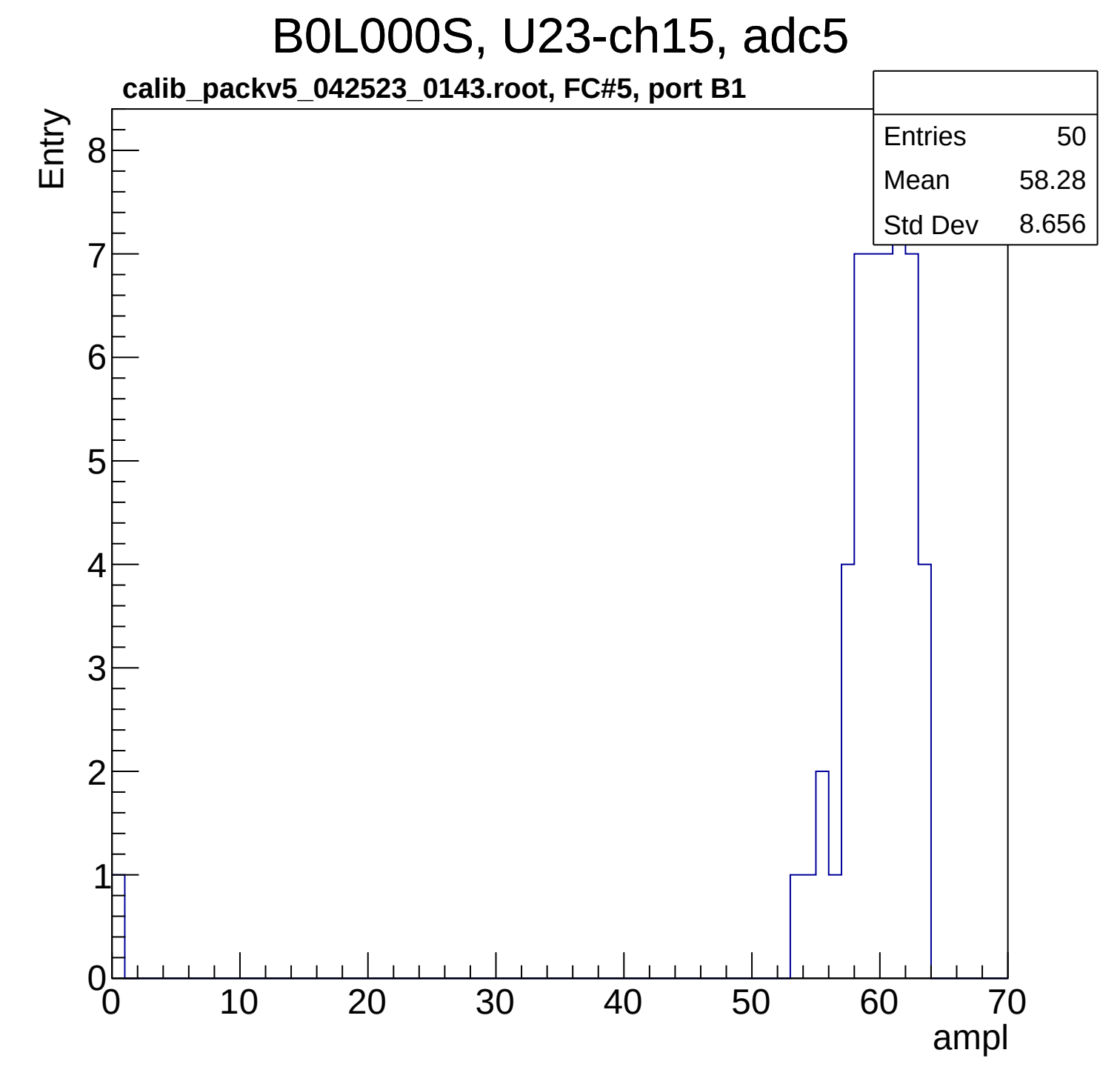

B0L000S, U23-ch15, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 29.84
Std Dev 5.042
Gaus mean : 29.4762
Gaus Width: 3.3135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

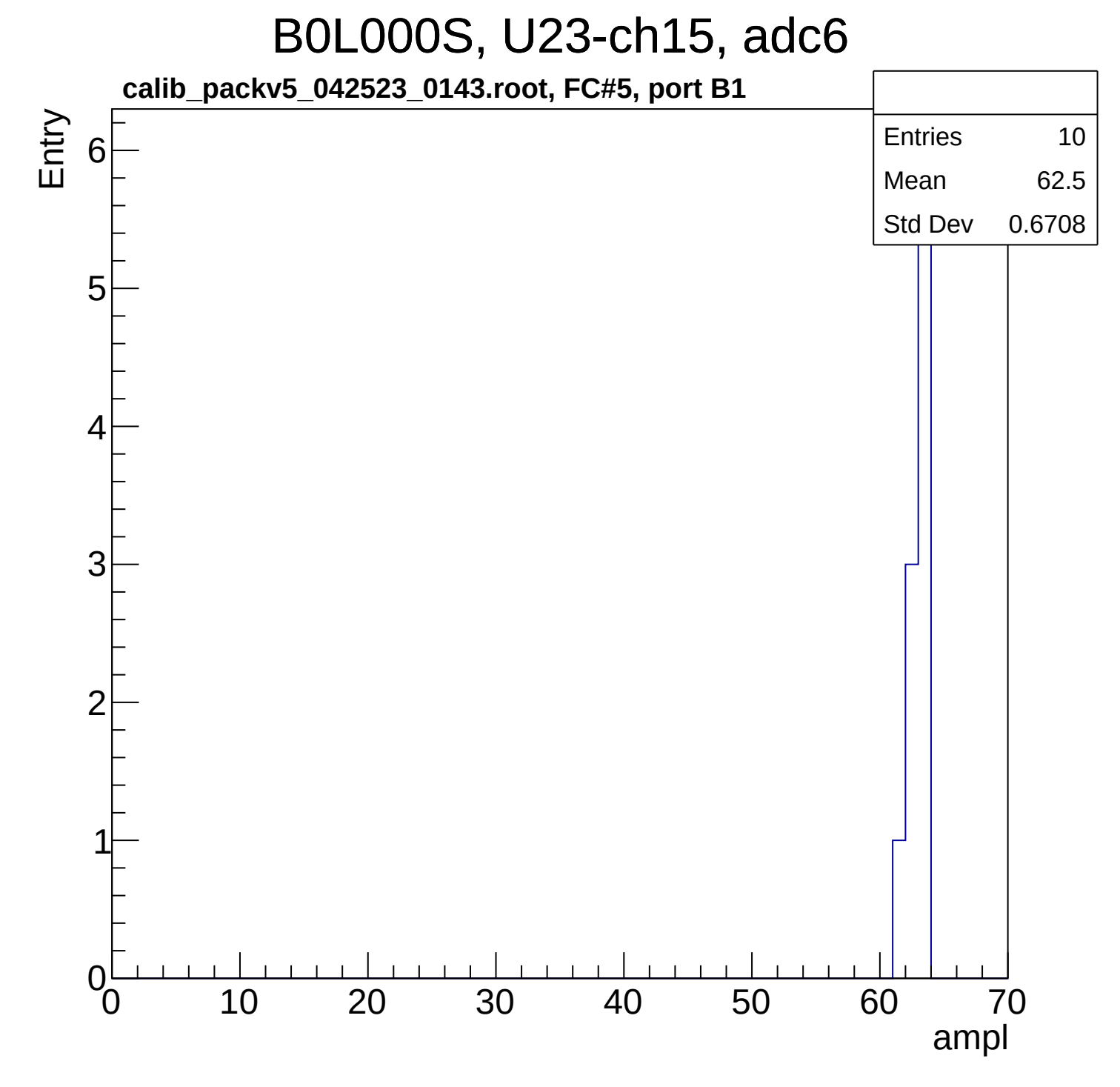

B0L000S, U23-ch15, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 36.17
Std Dev 3.649
Gaus mean : 36.6763
Gaus Width: 4.0261
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

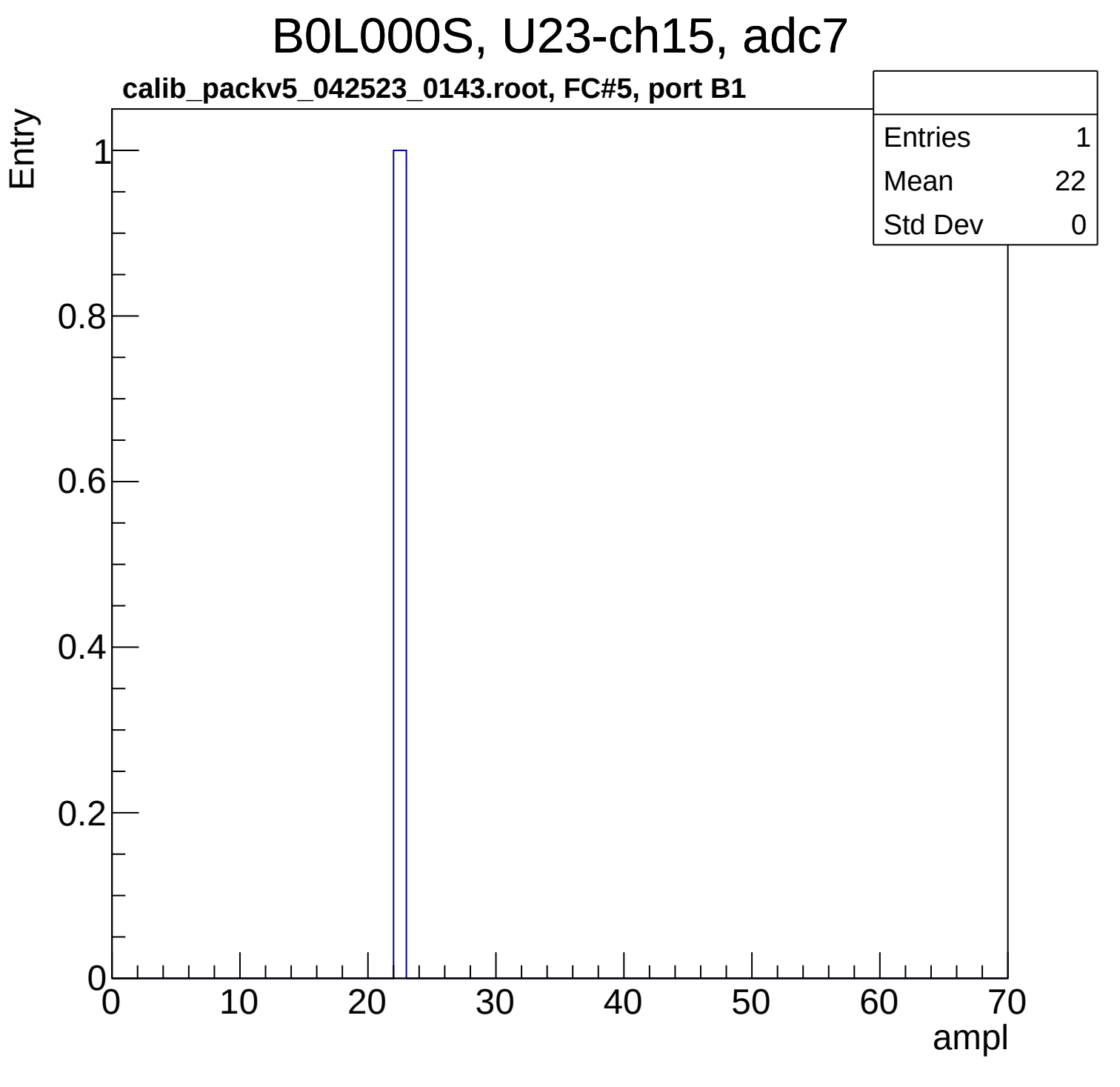

B0L000S, U23-ch15, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.95
Std Dev 3.256
Gaus mean : 43.6338
Gaus Width: 3.4315
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

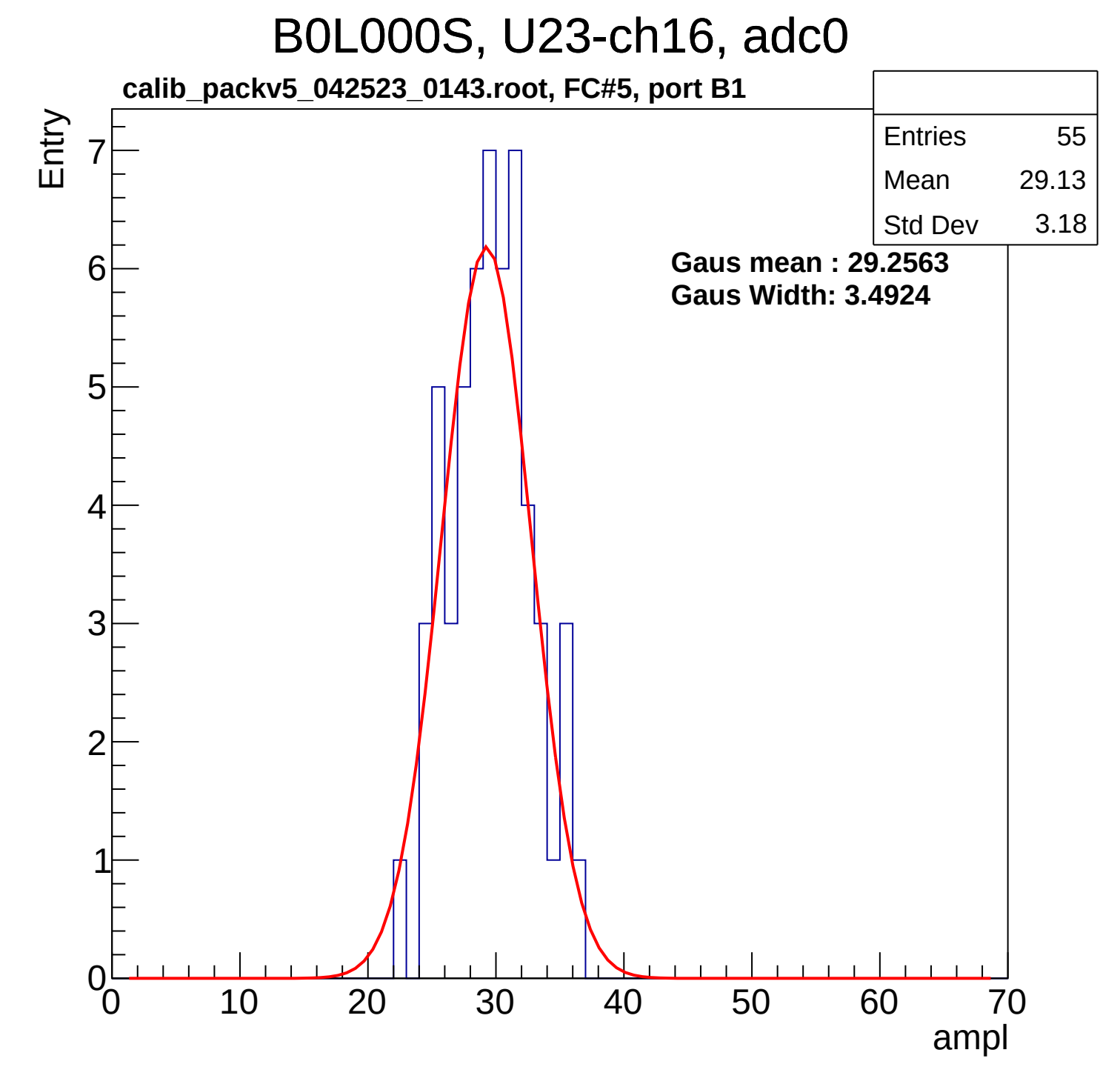

B0L000S, U23-ch15, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 48.73
Std Dev 2.618
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

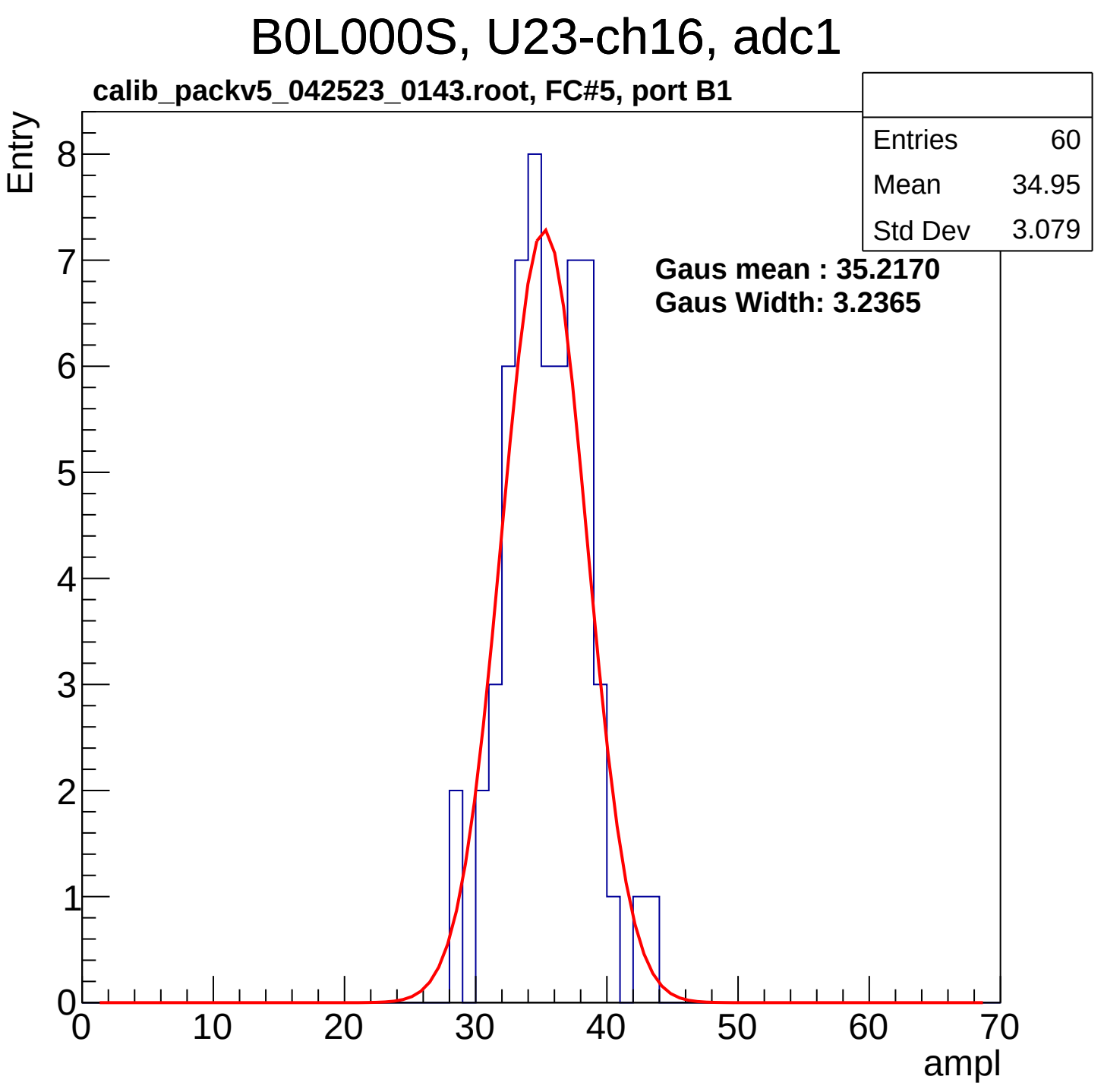

B0L000S, U23-ch15, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.93
Std Dev 2.736
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

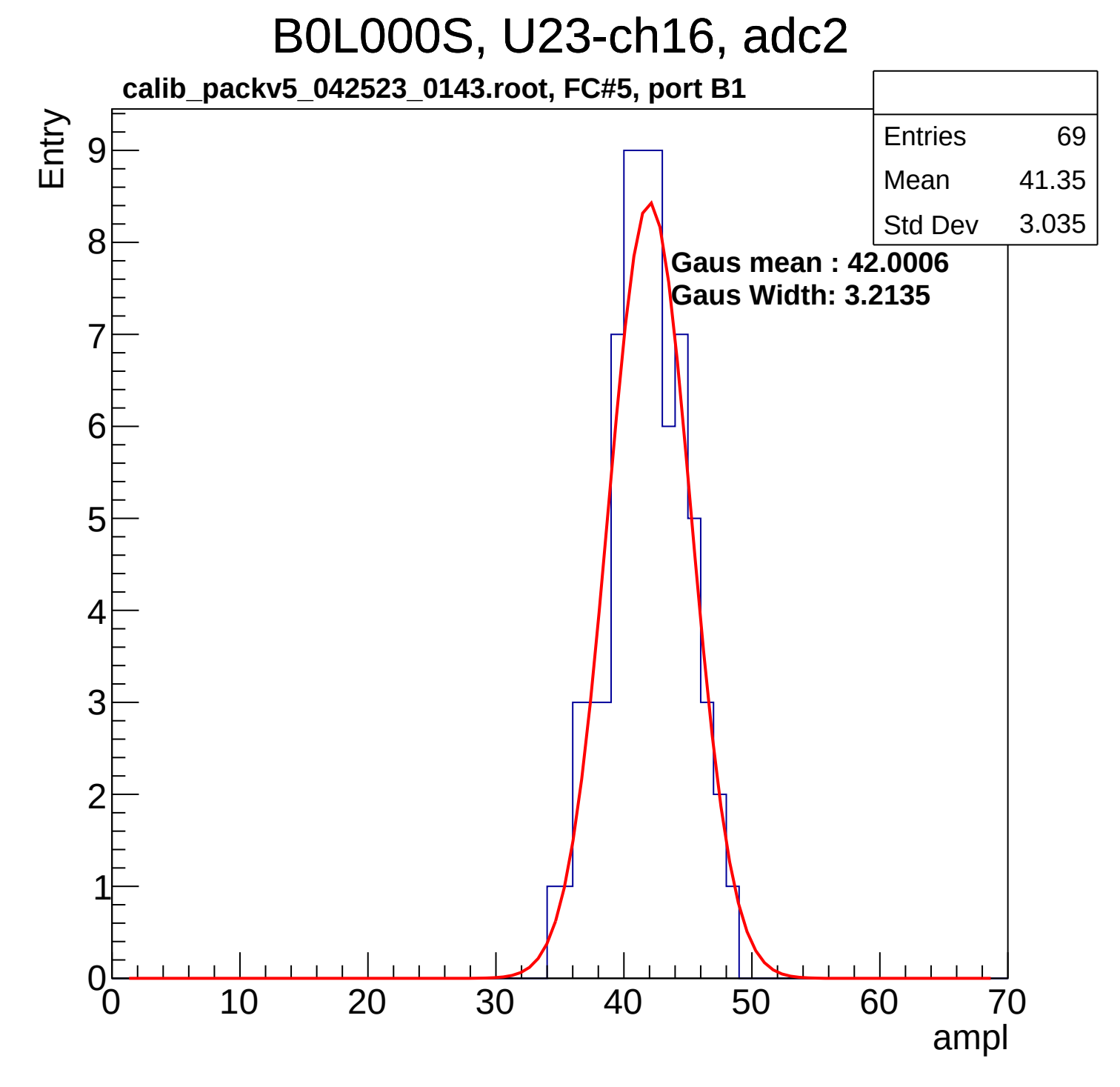

B0L000S, U23-ch15, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.28
Std Dev 8.656
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

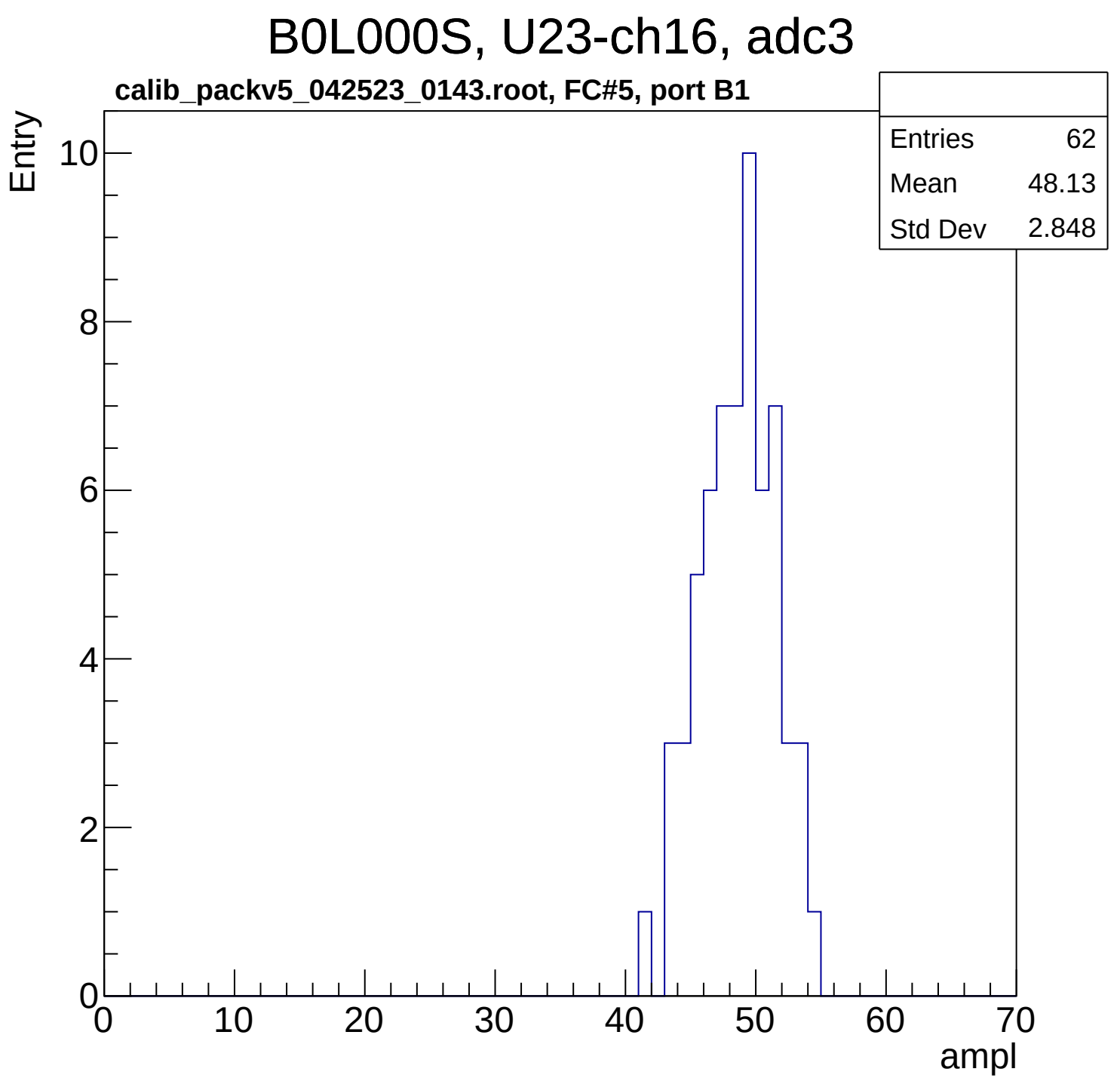

B0L000S, U23-ch15, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 62.5
Std Dev 0.6708
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

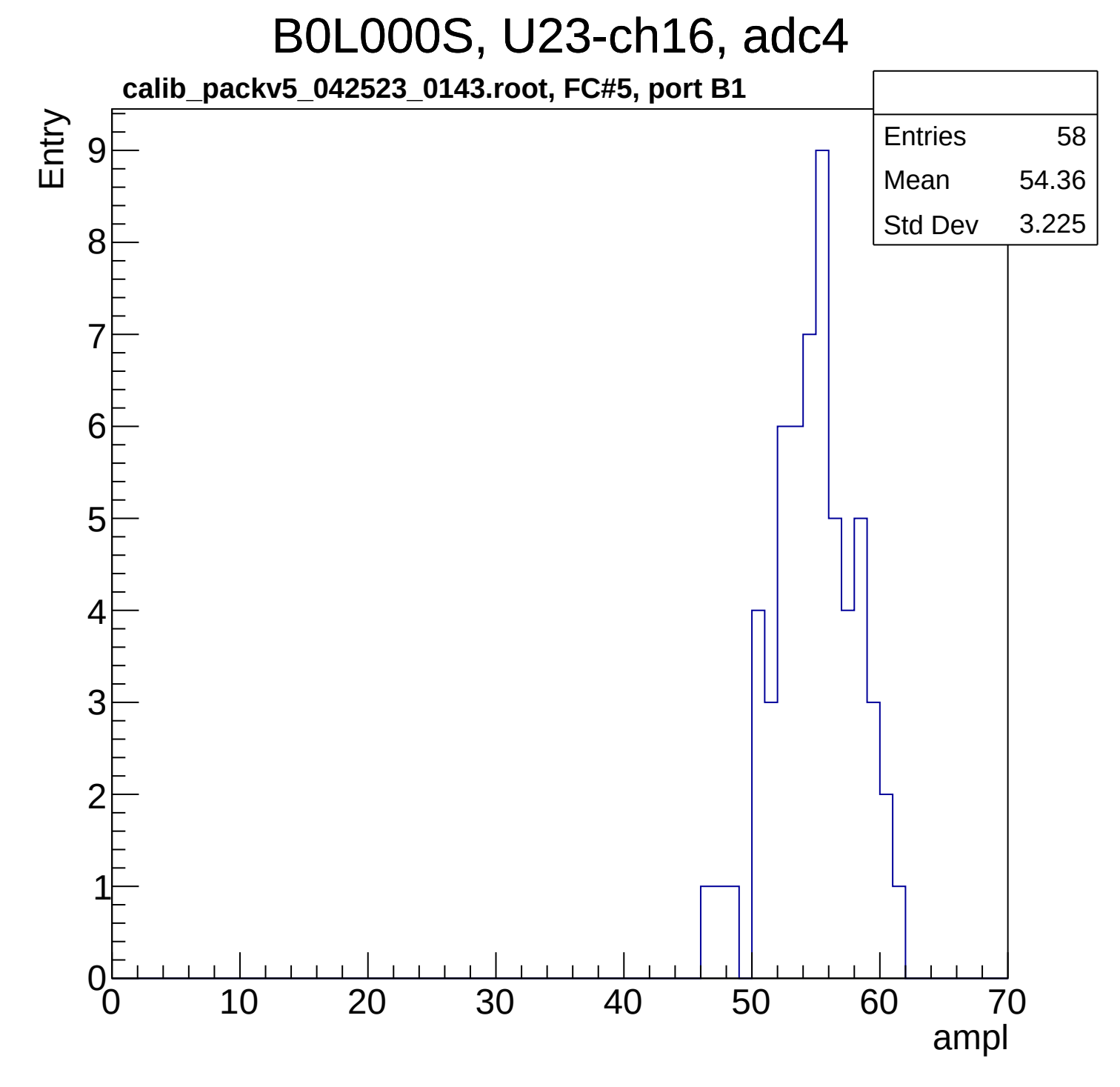

B0L000S, U23-ch15, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

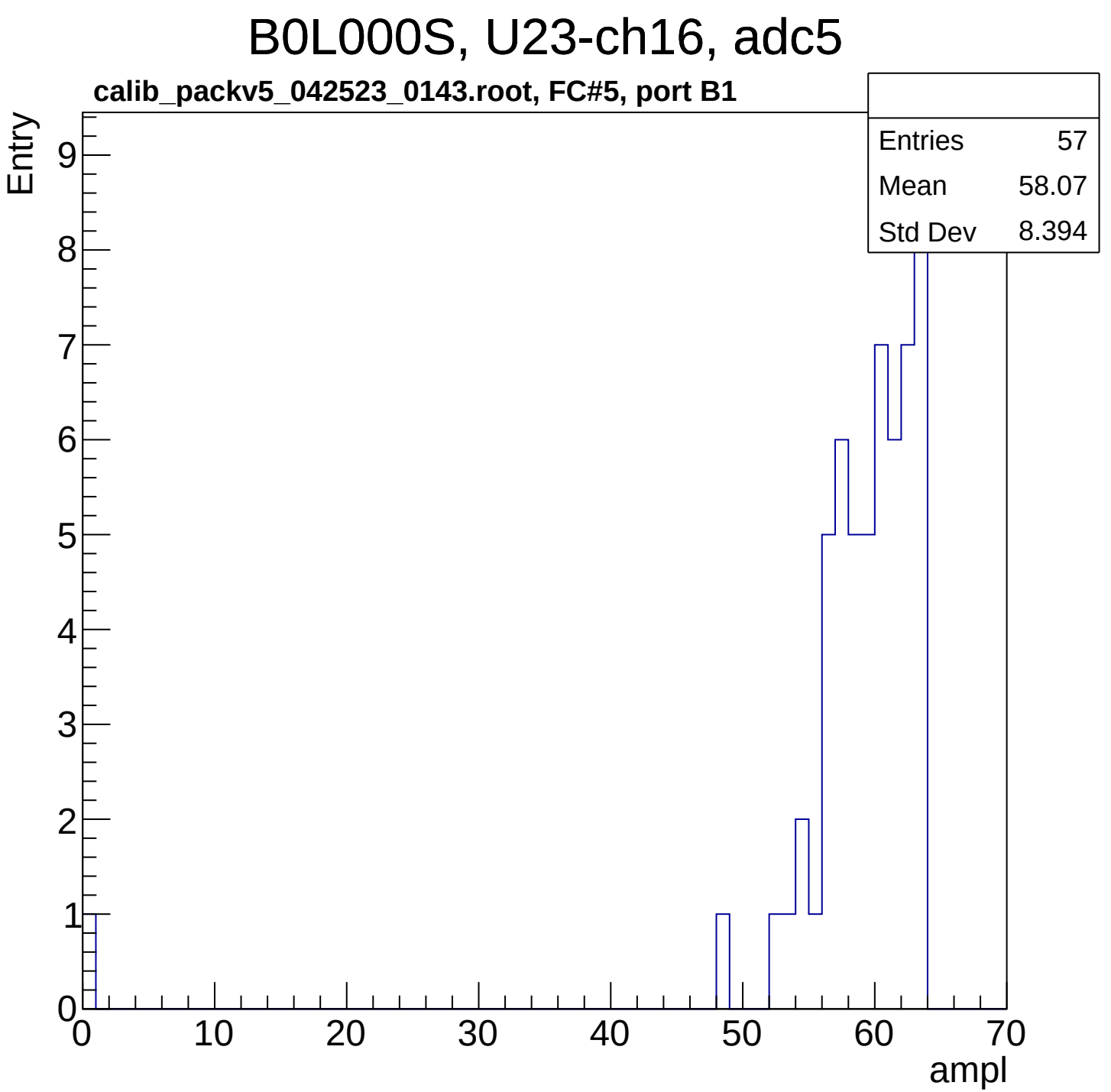

B0L000S, U23-ch16, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.13
Std Dev 3.18
Gaus mean : 29.2563
Gaus Width: 3.4924
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

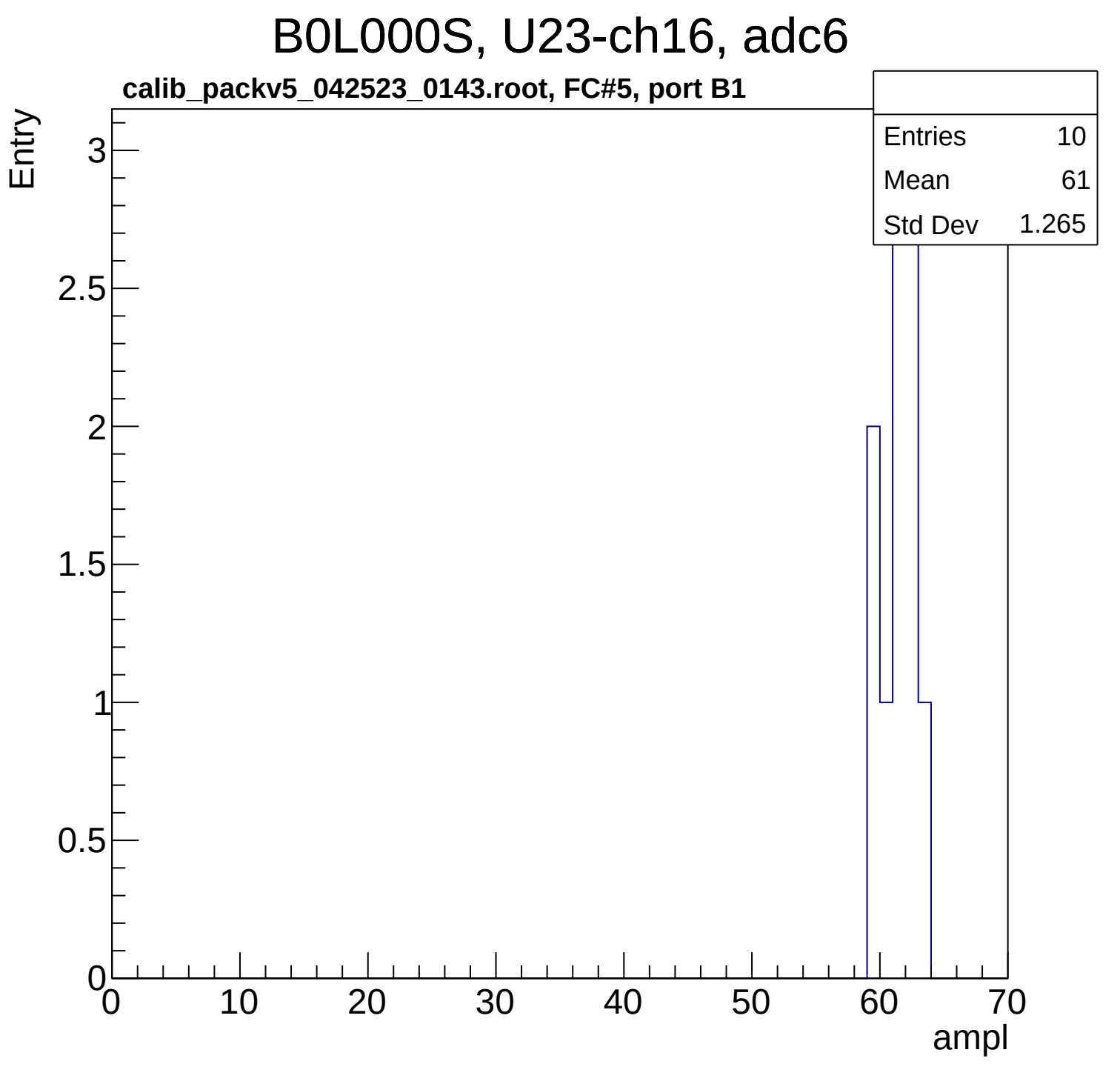

B0L000S, U23-ch16, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 34.95
Std Dev 3.079
Gaus mean : 35.2170
Gaus Width: 3.2365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

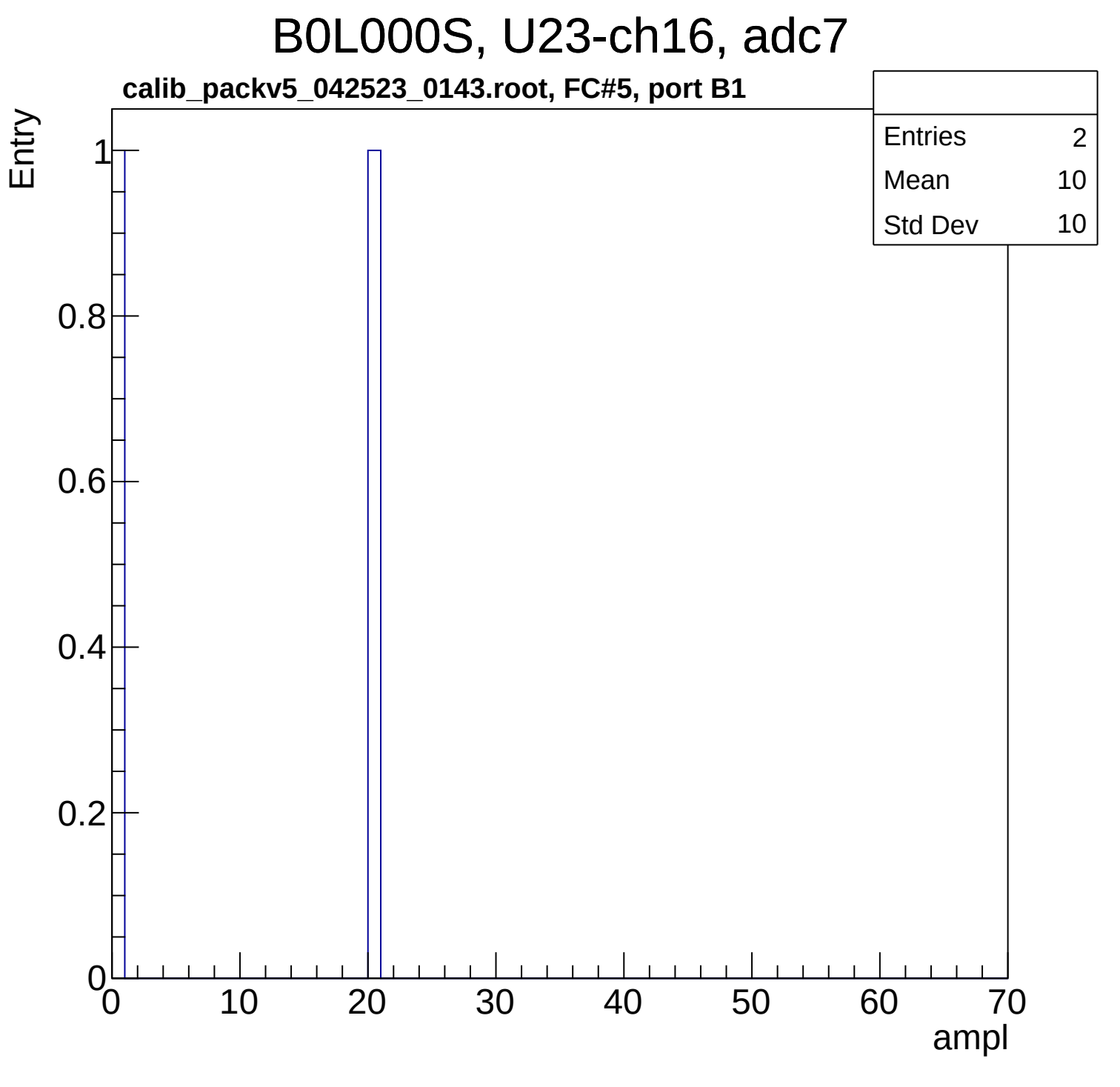

B0L000S, U23-ch16, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 41.35
Std Dev 3.035
Gaus mean : 42.0006
Gaus Width: 3.2135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

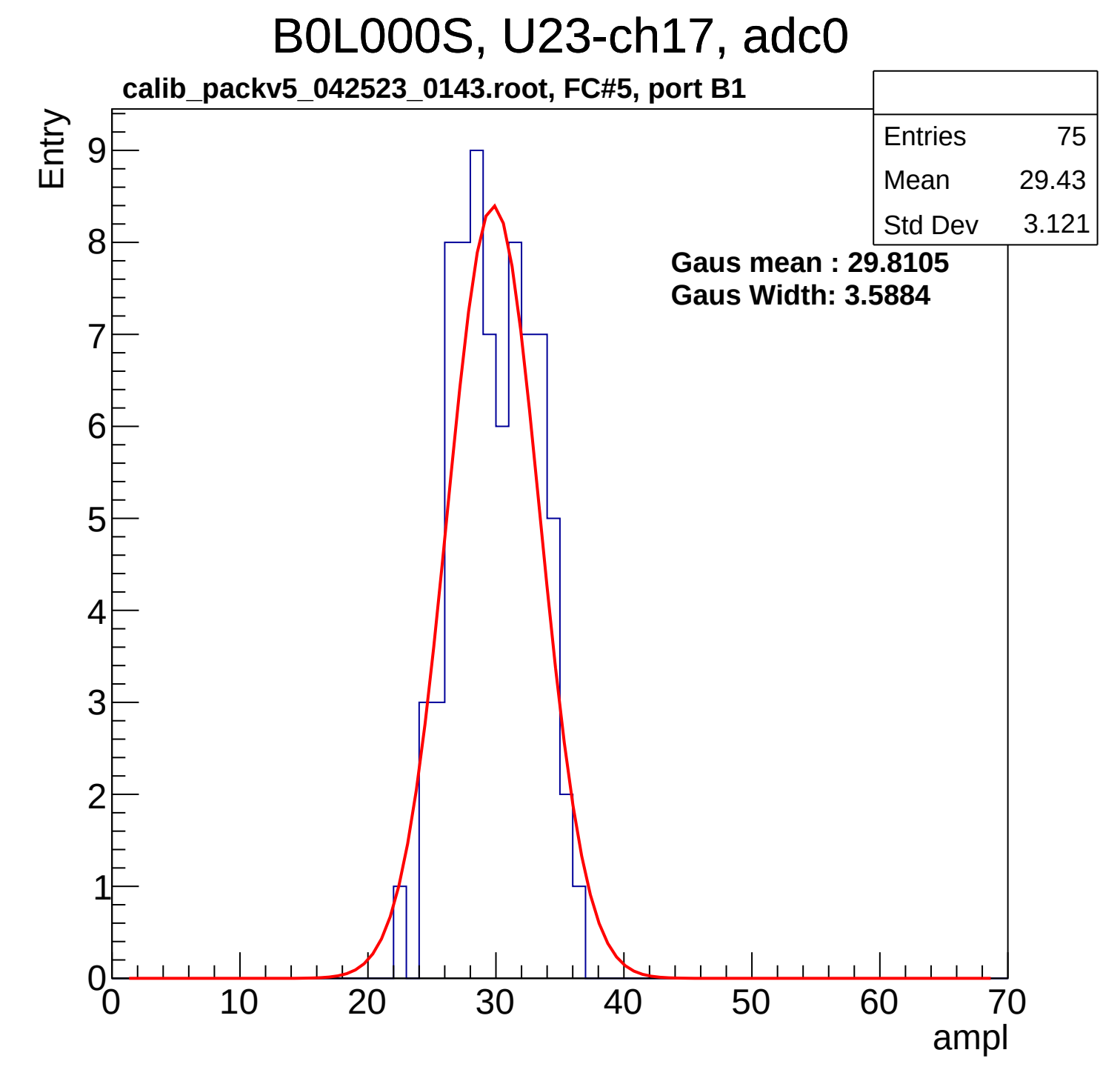

B0L000S, U23-ch16, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.13
Std Dev 2.848
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

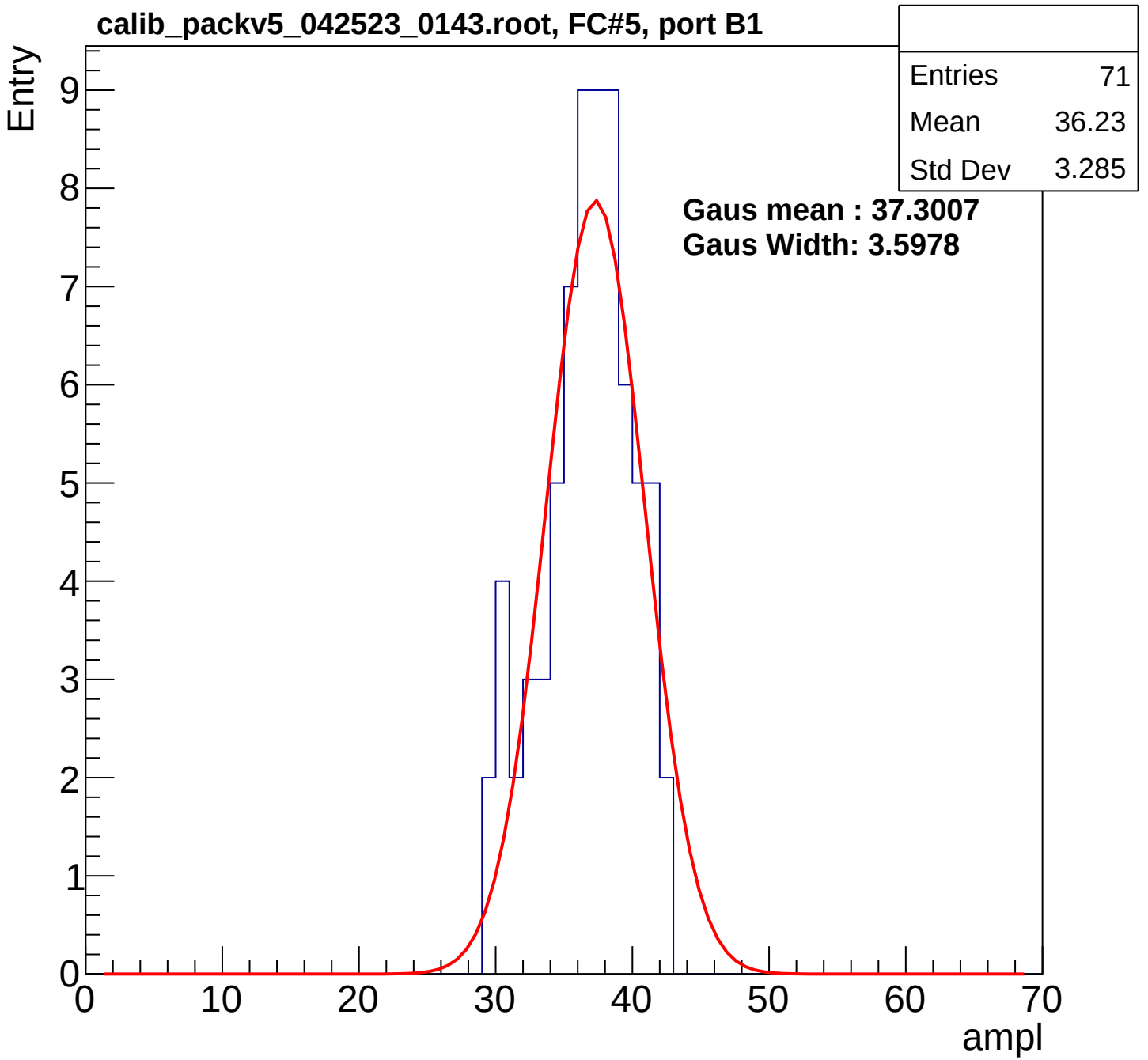

B0L000S, U23-ch16, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 54.36
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

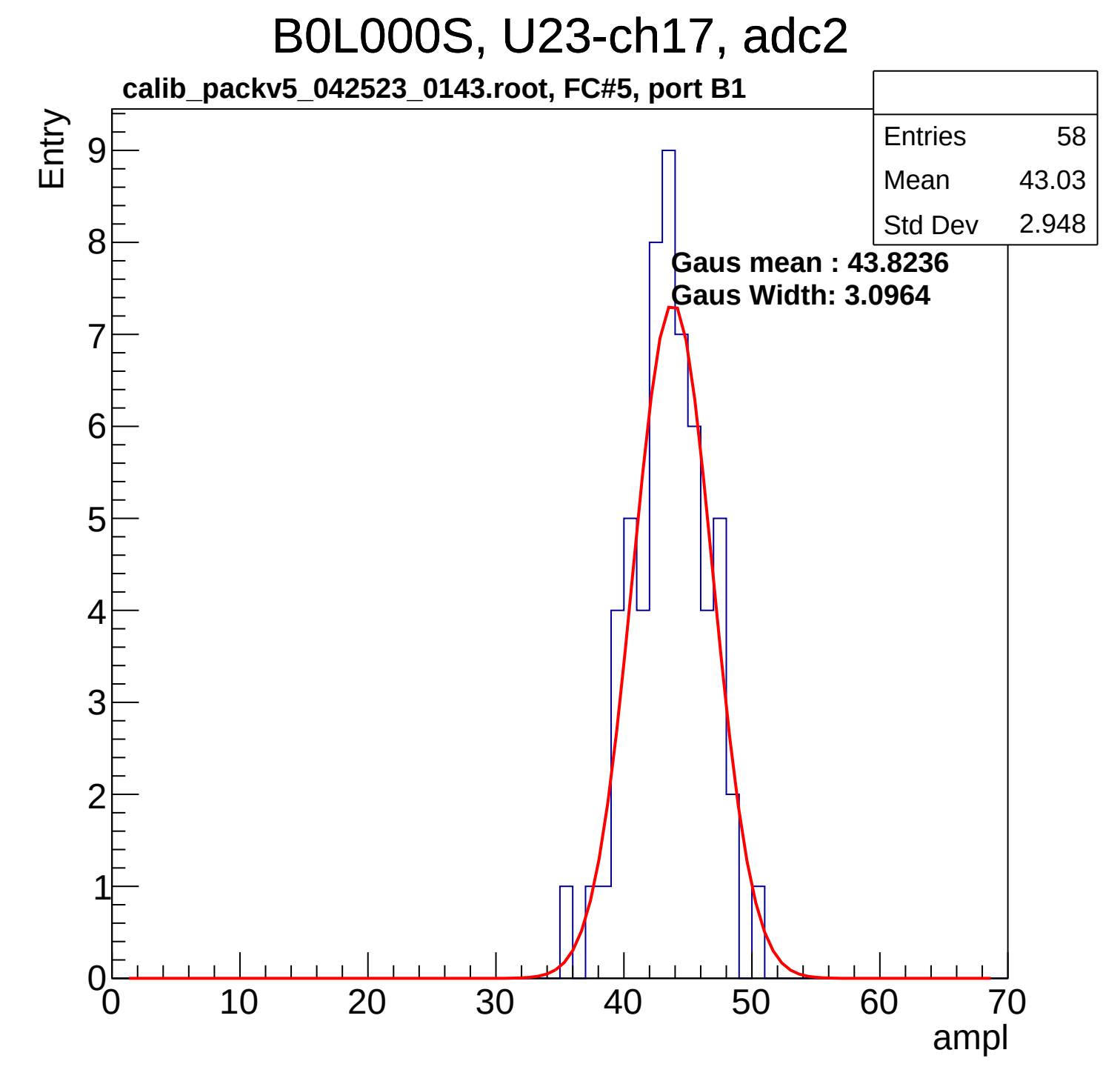

B0L000S, U23-ch16, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.07
Std Dev 8.394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

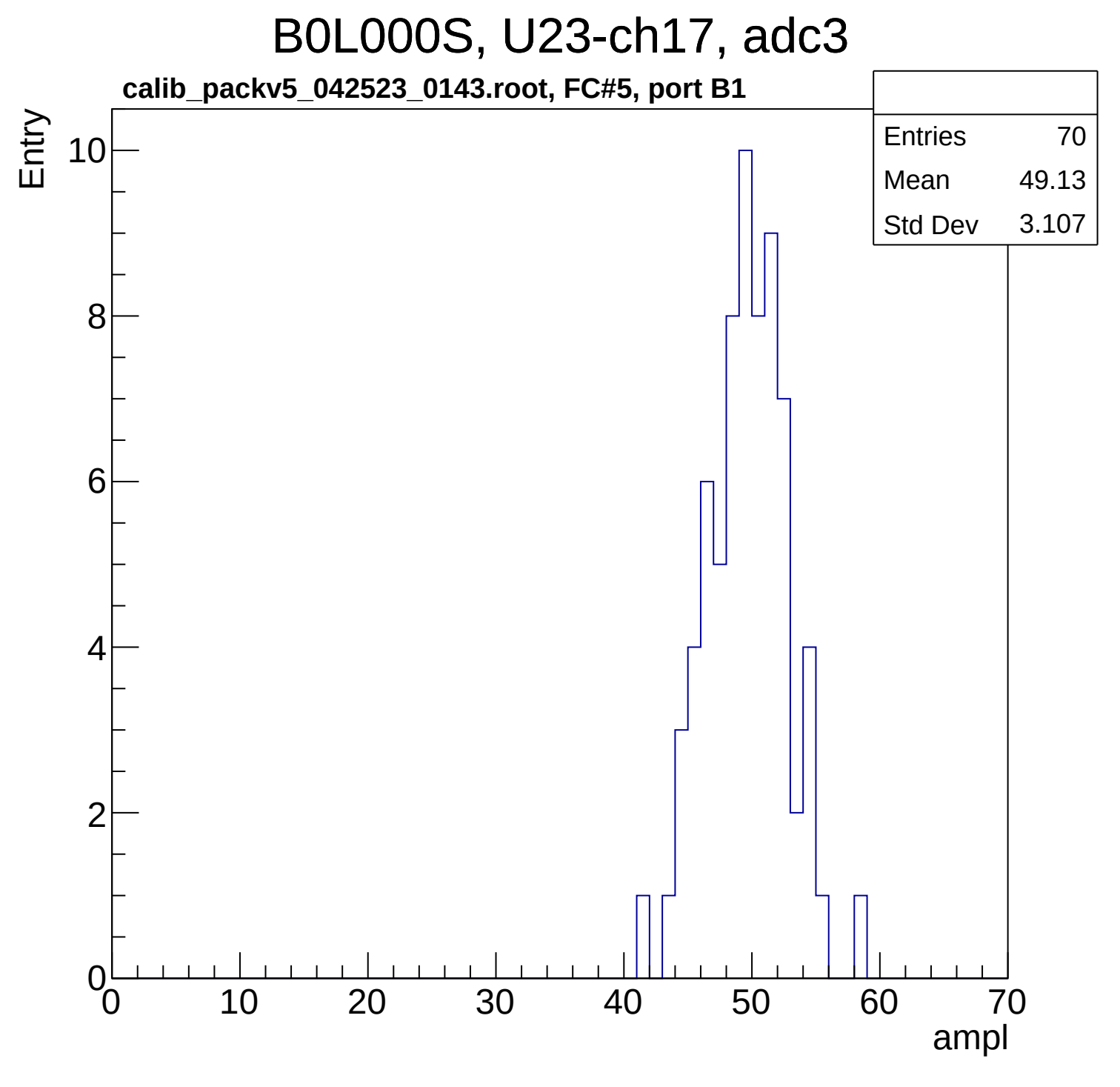

B0L000S, U23-ch16, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61
Std Dev 1.265
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

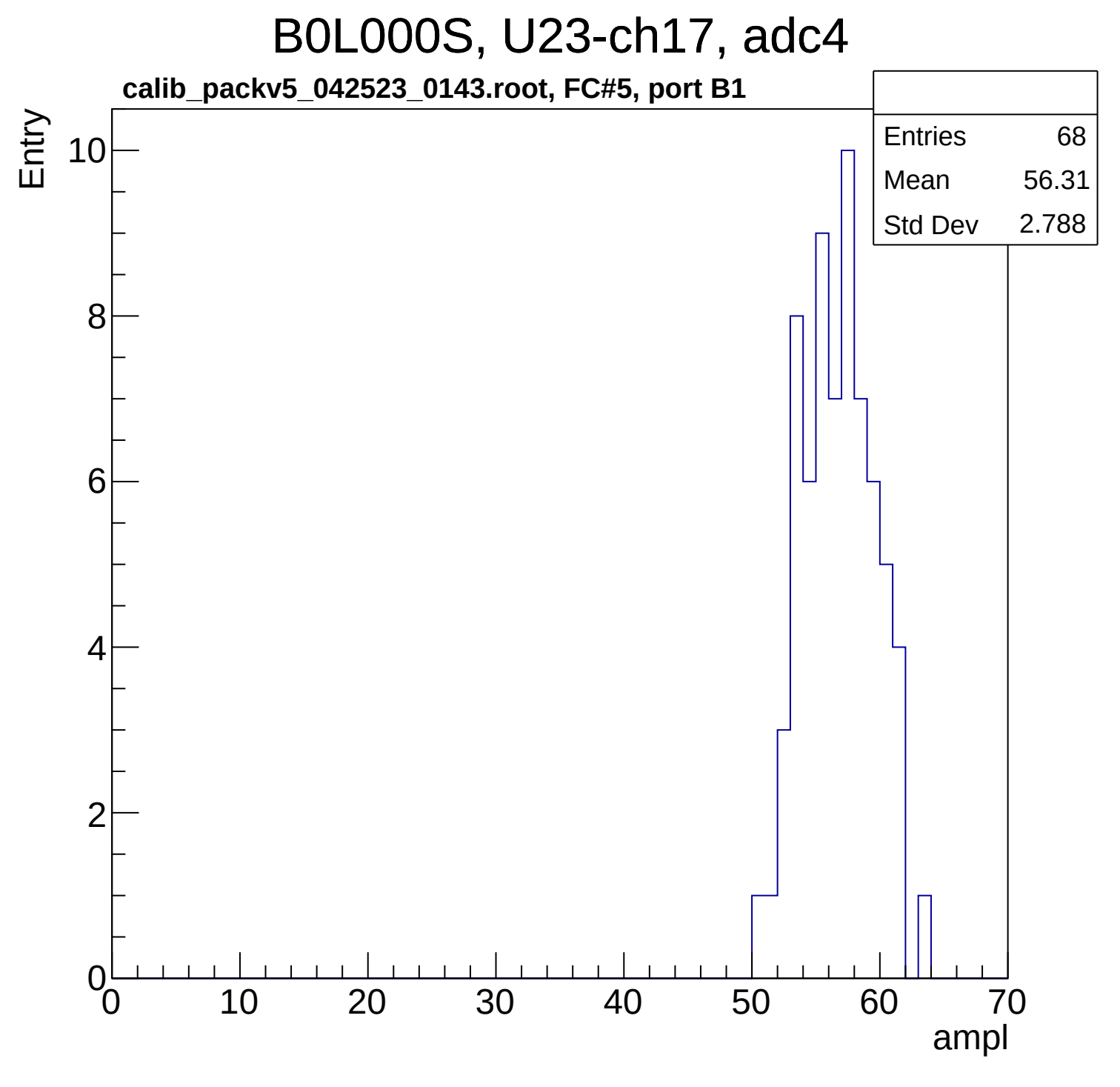

B0L000S, U23-ch16, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

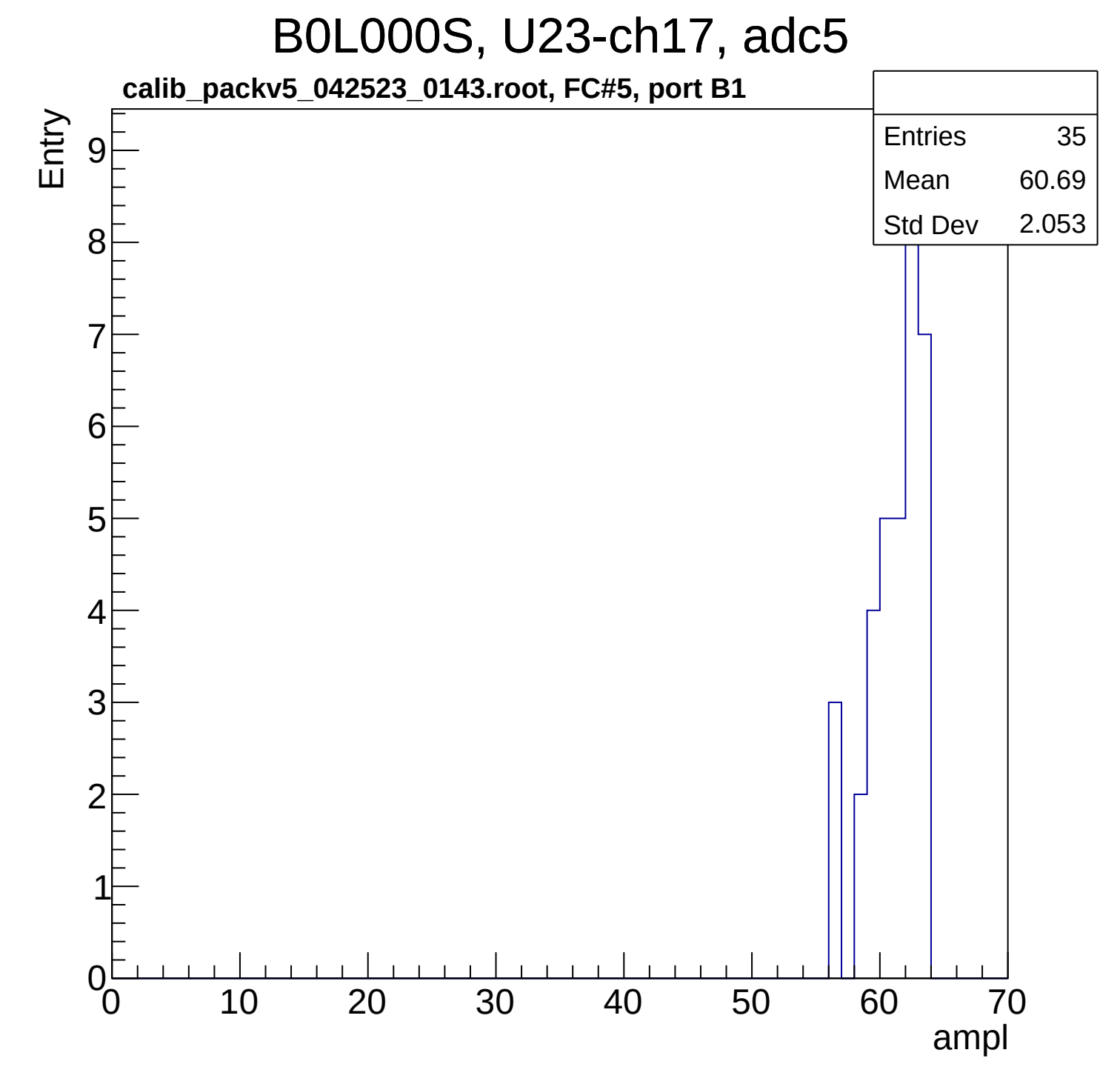

B0L000S, U23-ch17, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.43
Std Dev 3.121
Gaus mean : 29.8105
Gaus Width: 3.5884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

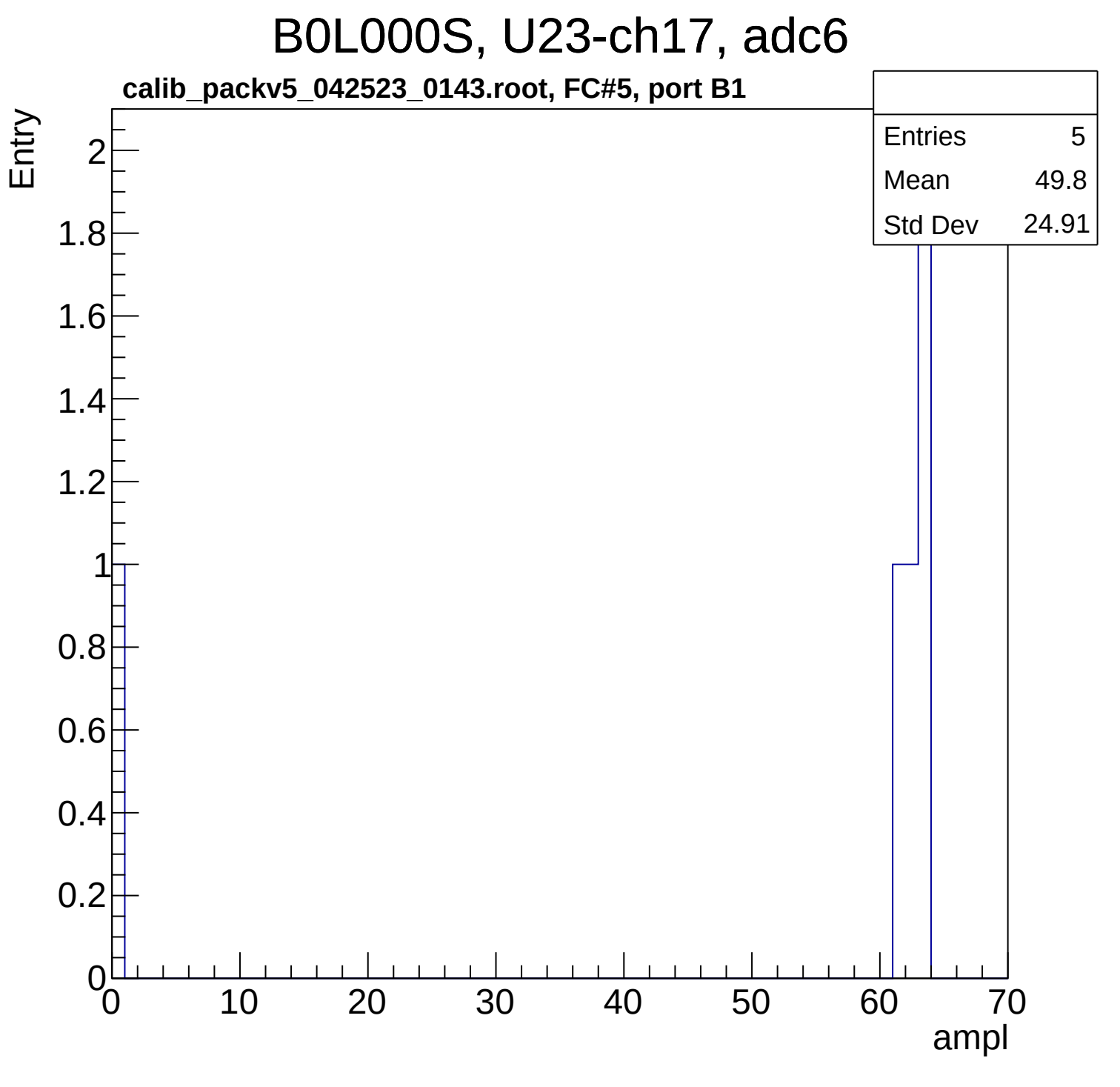

B0L000S, U23-ch17, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.23
Std Dev 3.285
Gaus mean : 37.3007
Gaus Width: 3.5978
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

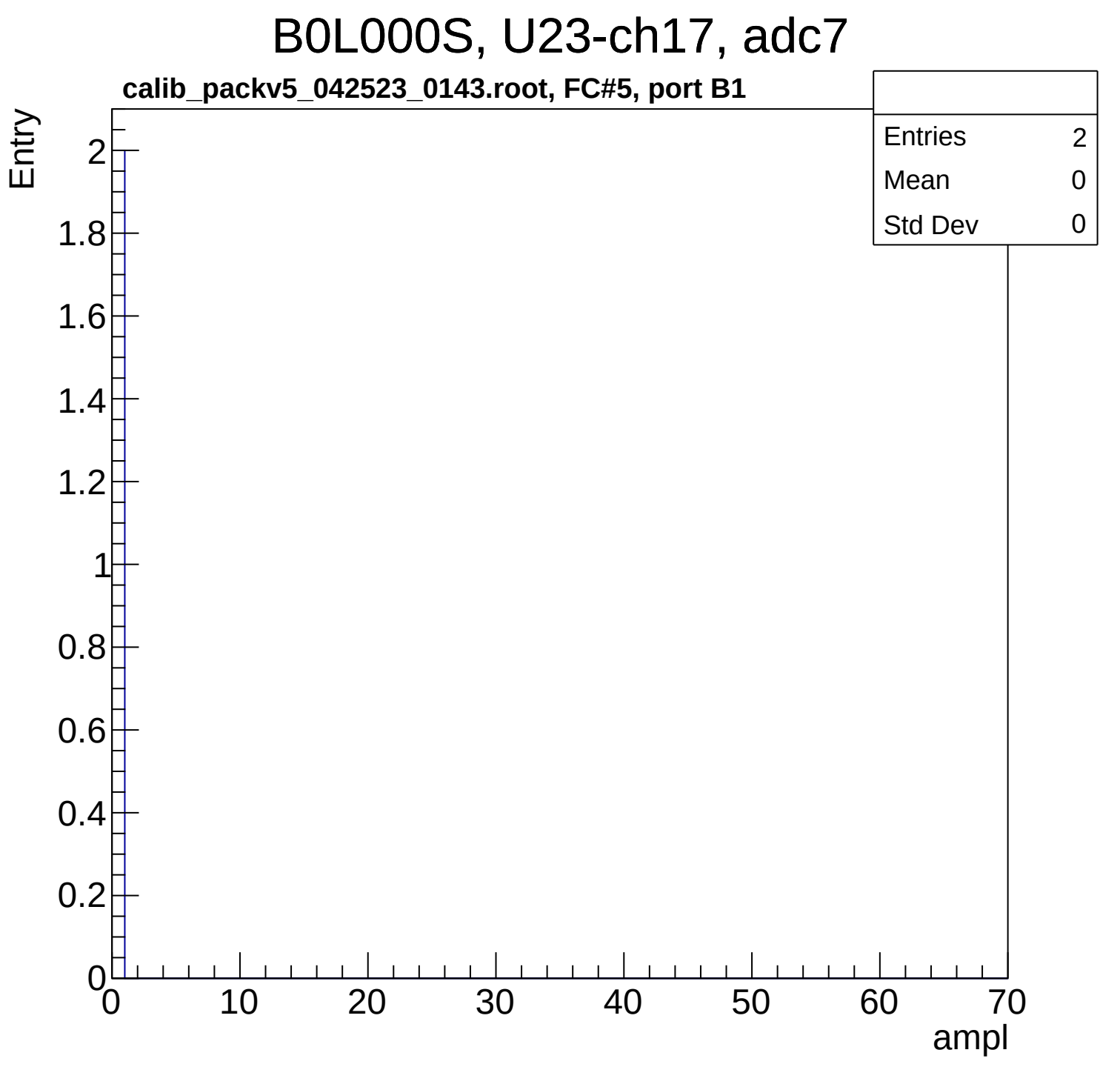

B0L000S, U23-ch17, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.03
Std Dev 2.948
Gaus mean : 43.8236
Gaus Width: 3.0964
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

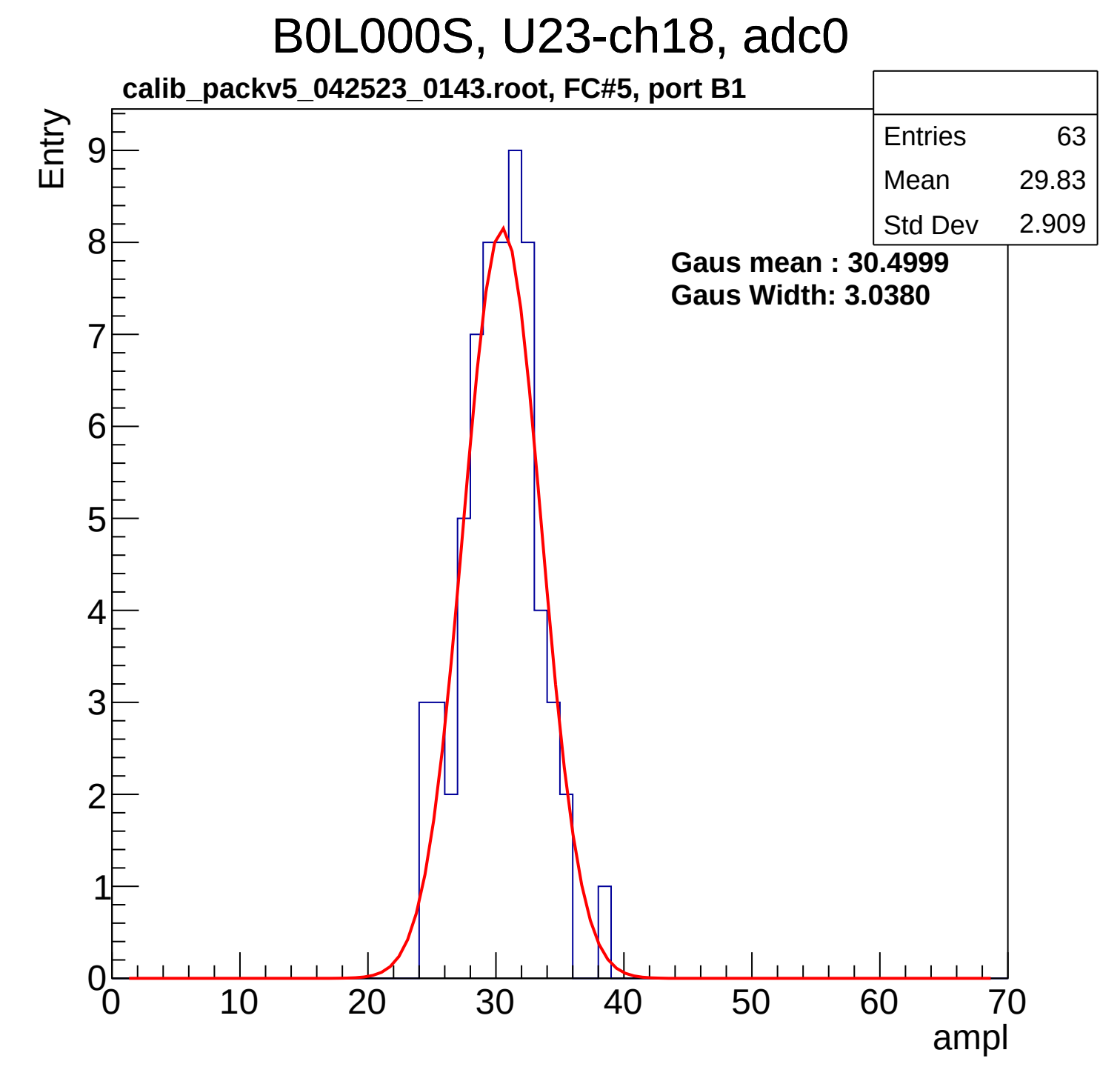

B0L000S, U23-ch17, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.13
Std Dev 3.107
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

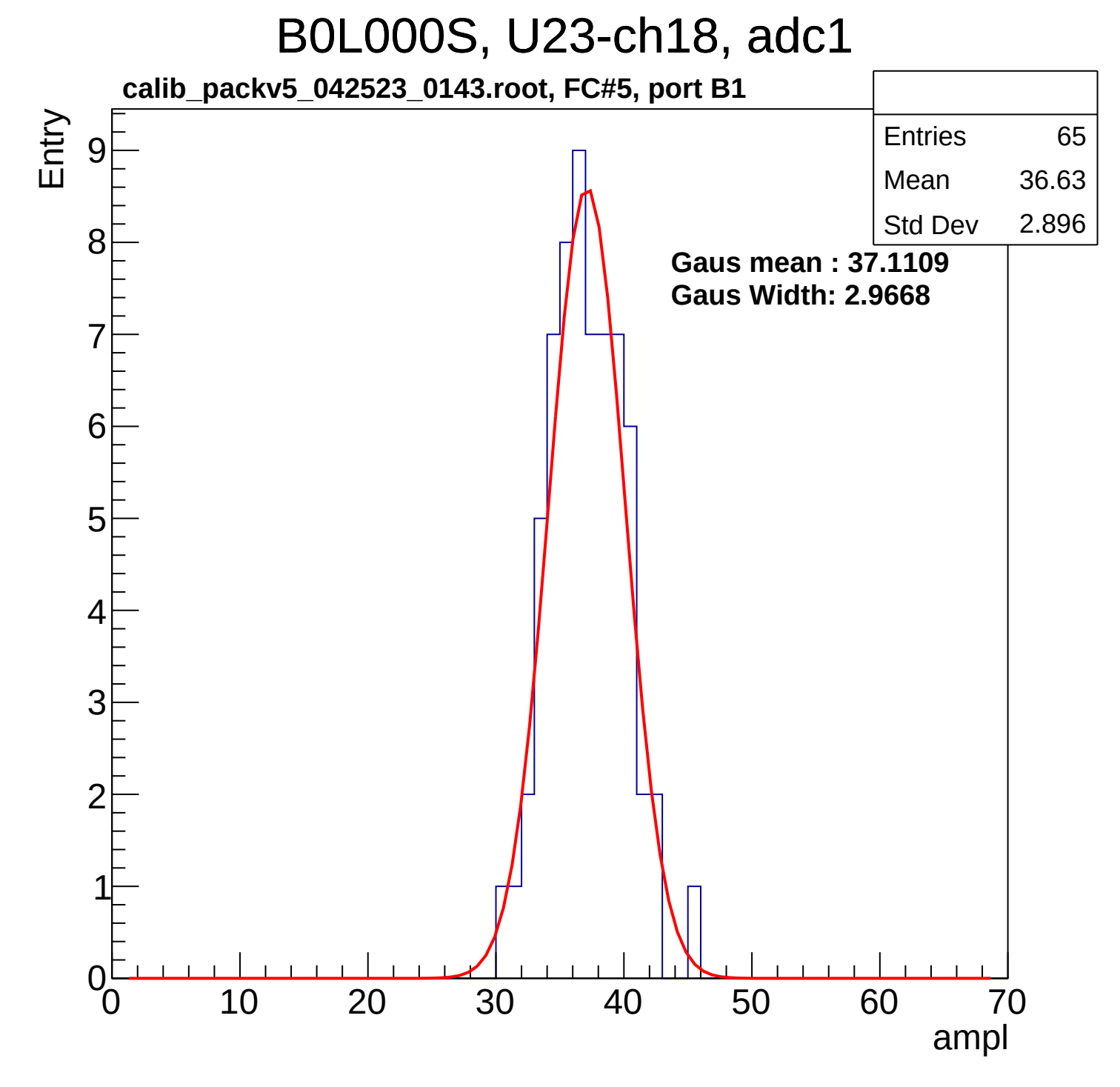

B0L000S, U23-ch17, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 56.31
Std Dev 2.788
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

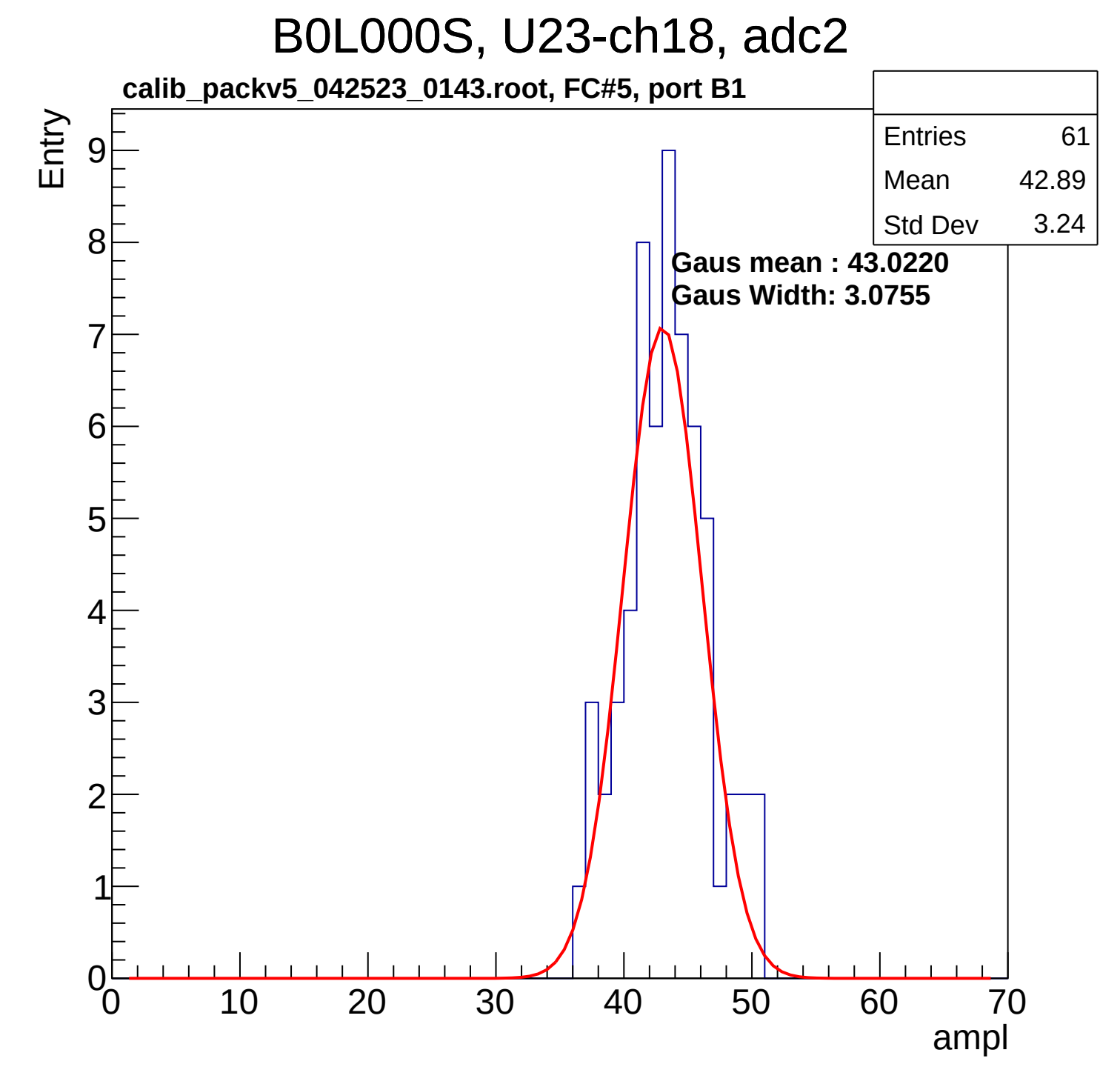

B0L000S, U23-ch17, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 60.69
Std Dev 2.053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

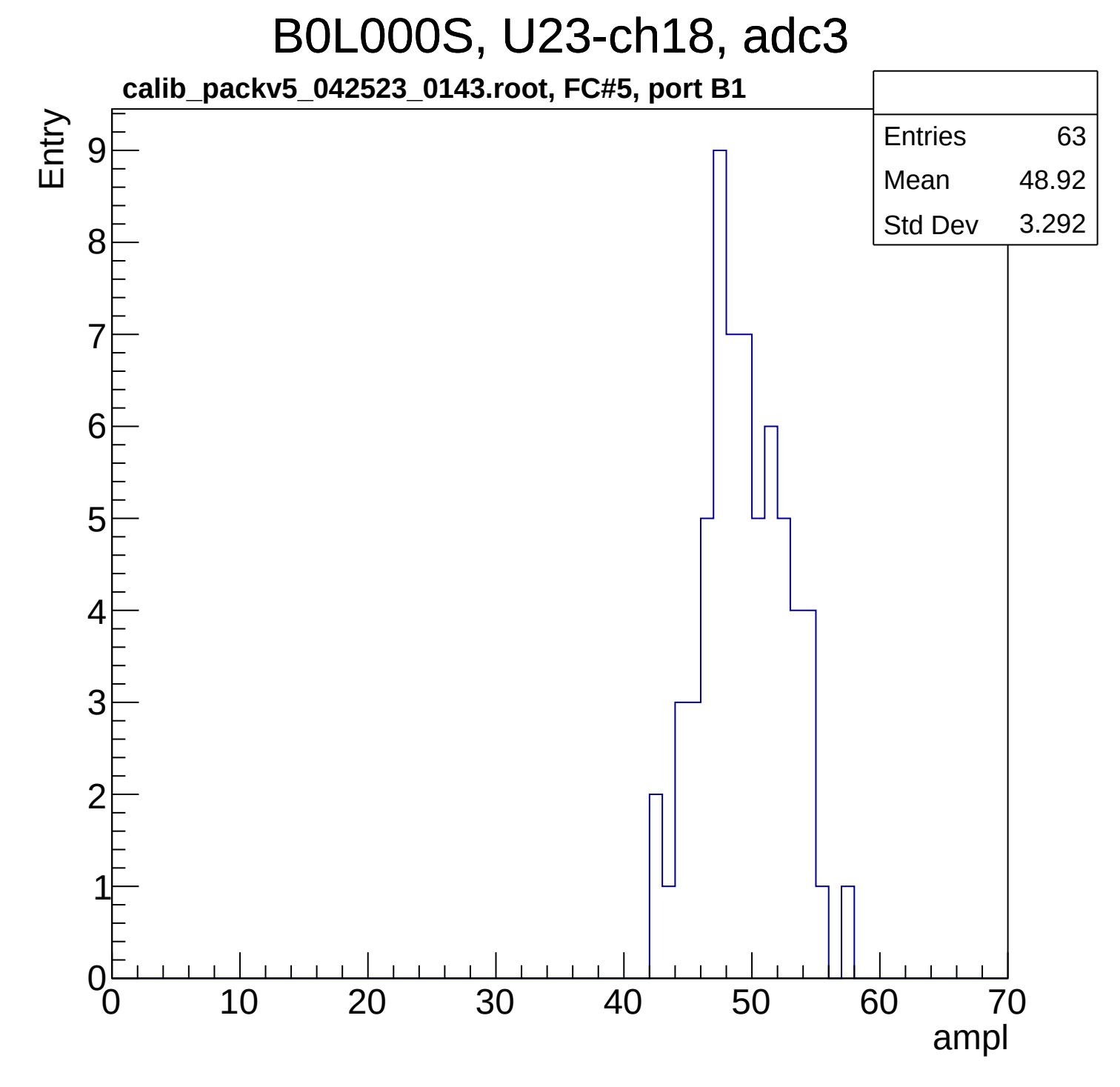

B0L000S, U23-ch17, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

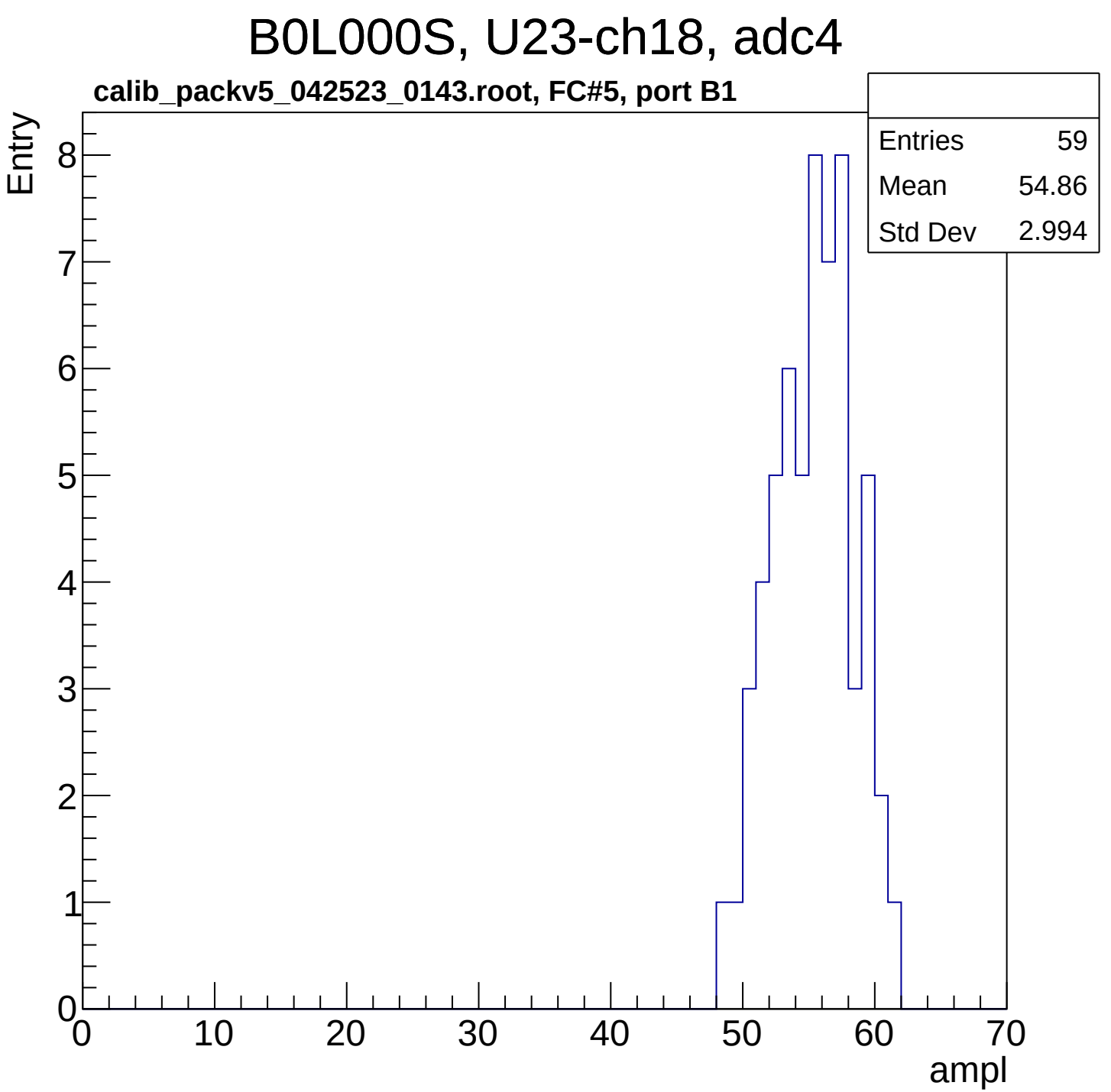

B0L000S, U23-ch17, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

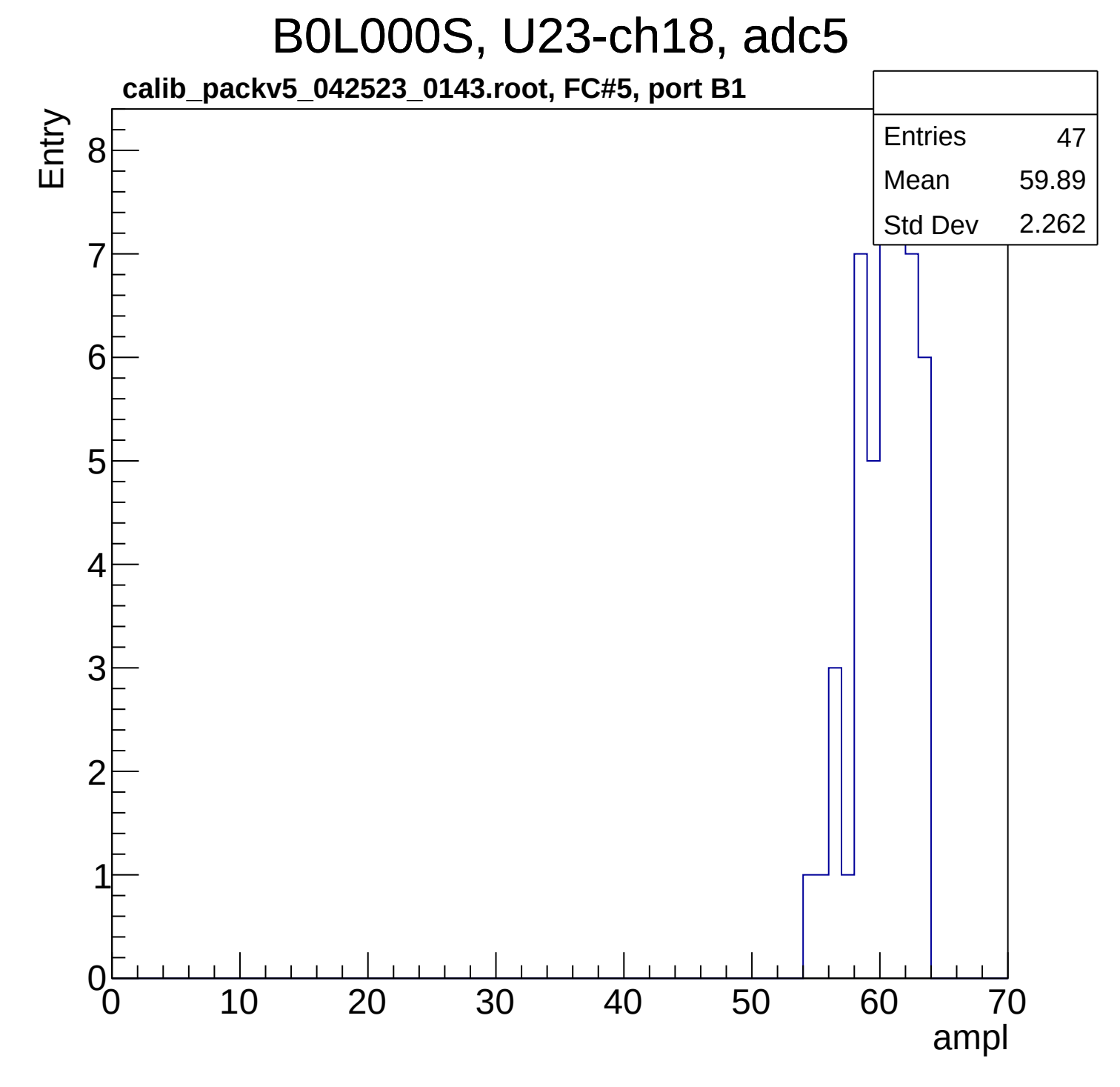

B0L000S, U23-ch18, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.83
Std Dev 2.909
Gaus mean : 30.4999
Gaus Width: 3.0380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

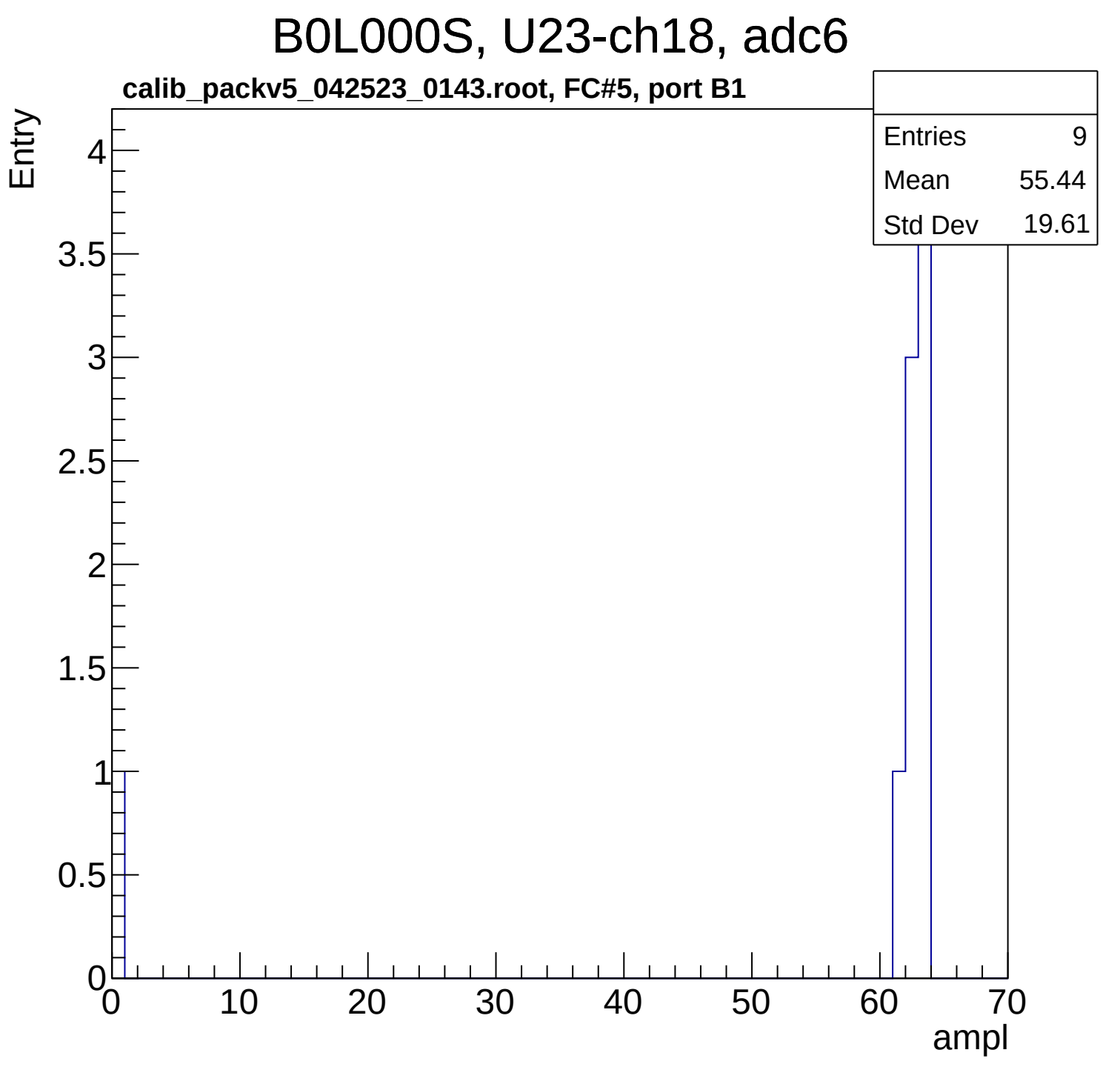

B0L000S, U23-ch18, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.63
Std Dev 2.896
Gaus mean : 37.1109
Gaus Width: 2.9668
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

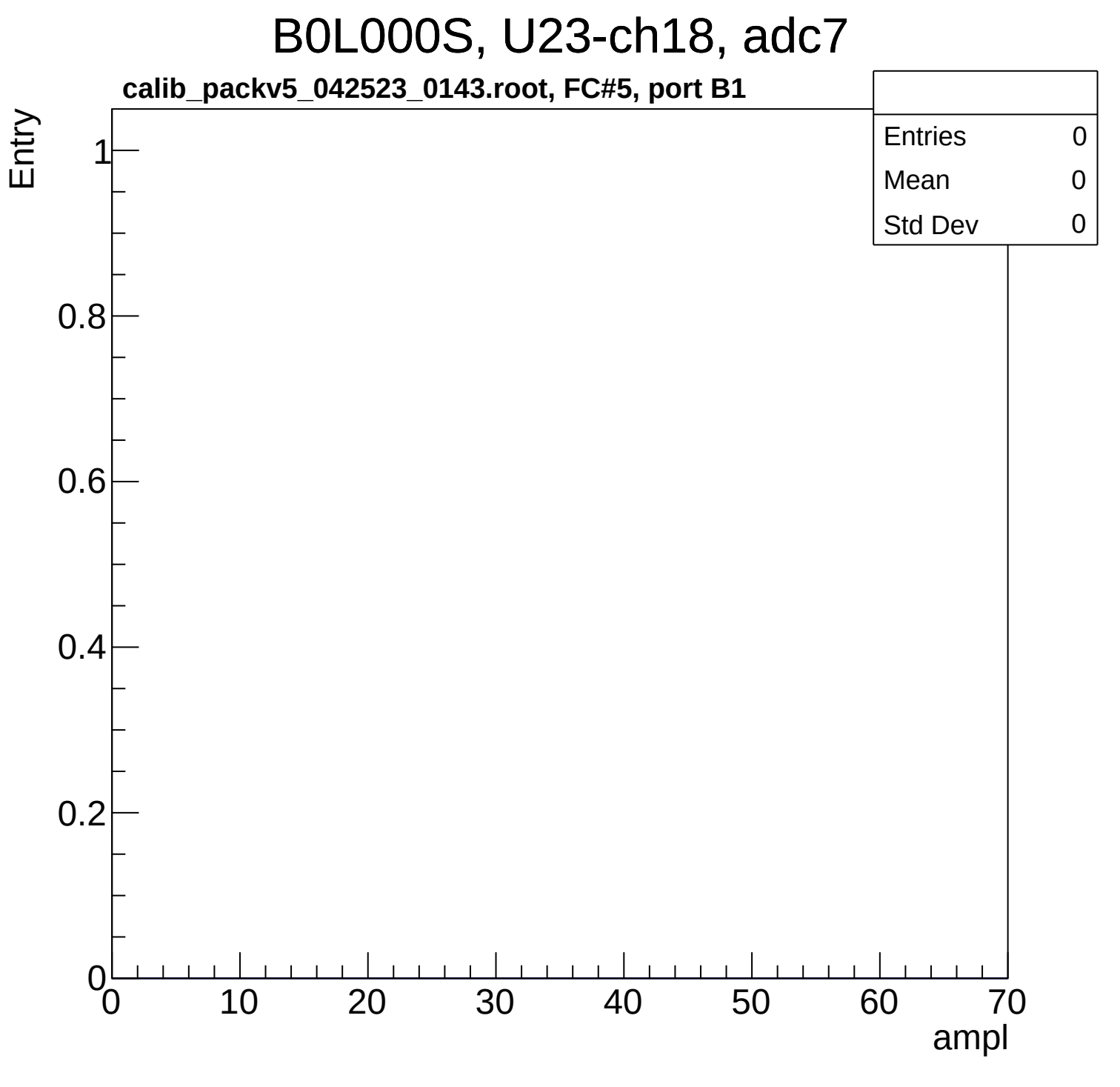

B0L000S, U23-ch18, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.89
Std Dev 3.24
Gaus mean : 43.0220
Gaus Width: 3.0755
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

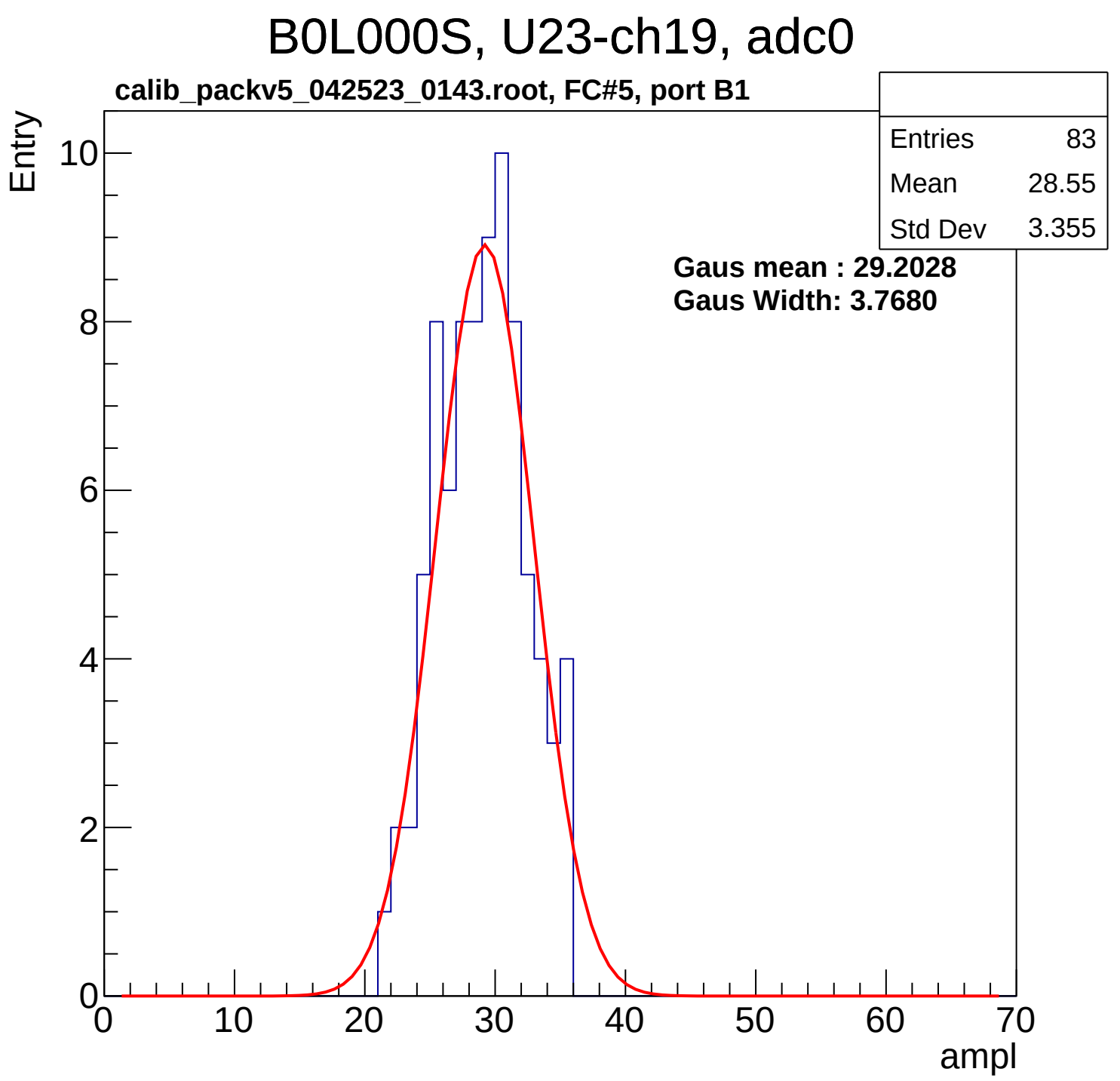

B0L000S, U23-ch18, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 48.92
Std Dev 3.292
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

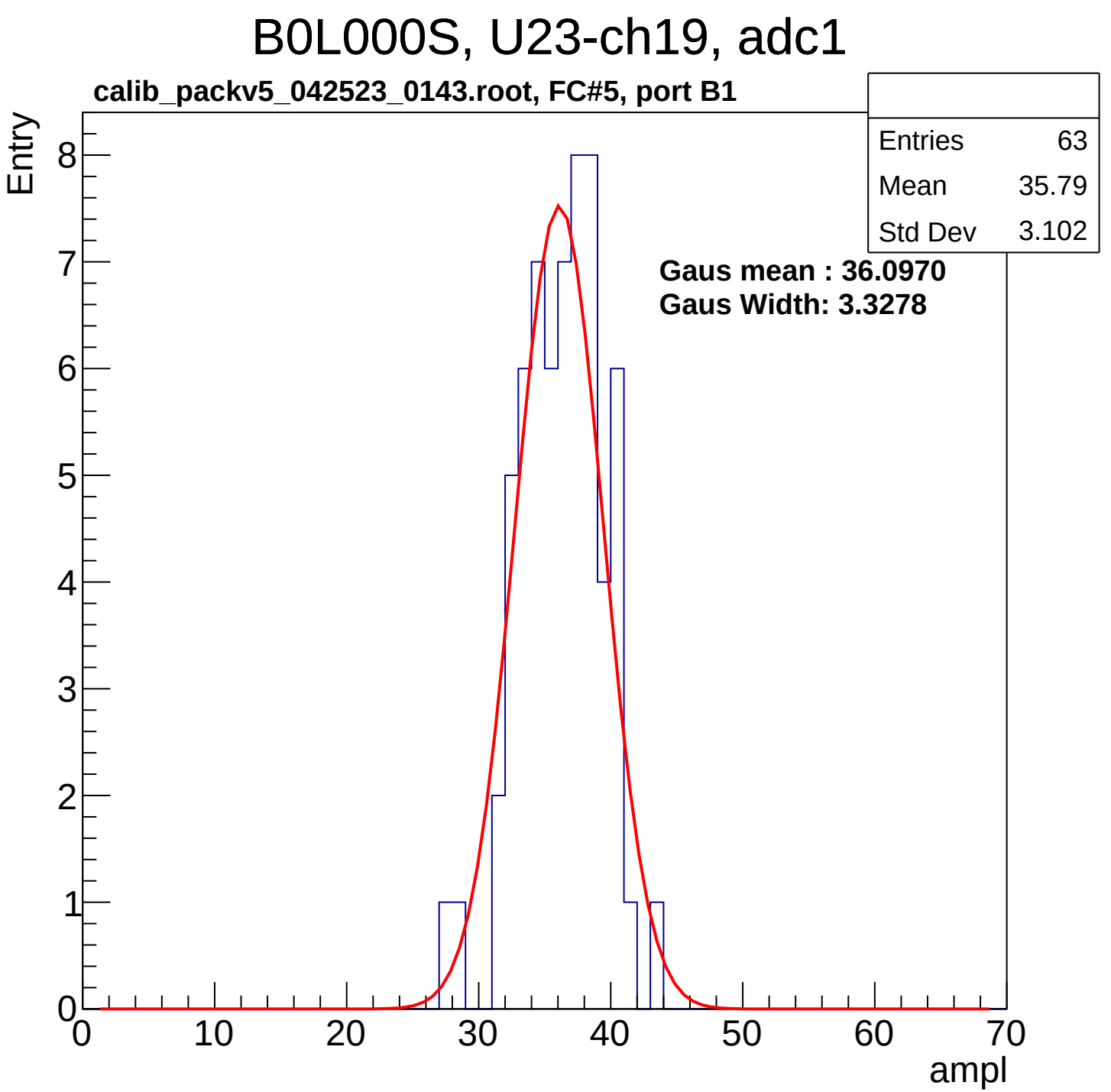

B0L000S, U23-ch18, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.86
Std Dev 2.994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

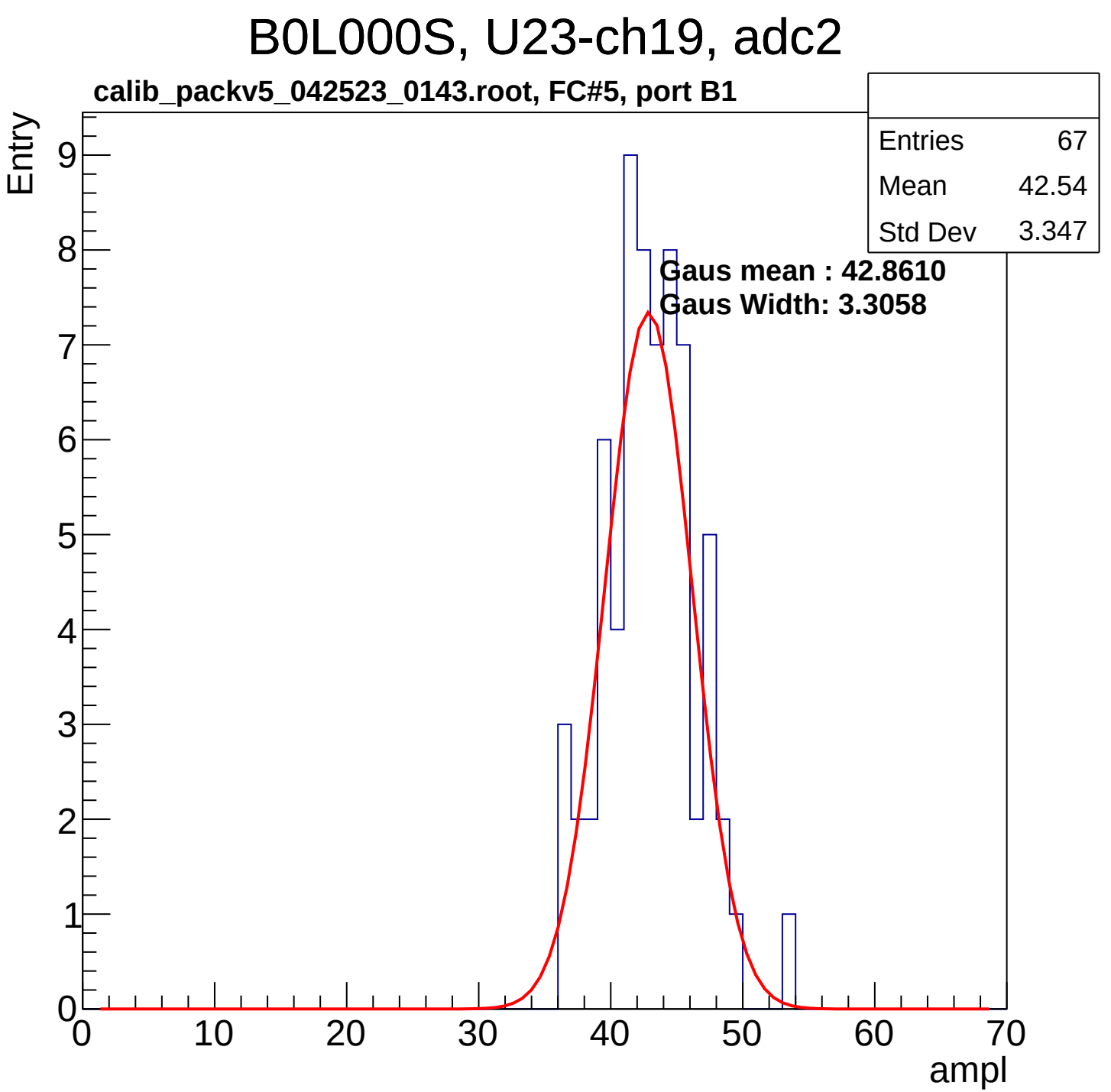

B0L000S, U23-ch18, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 59.89
Std Dev 2.262
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

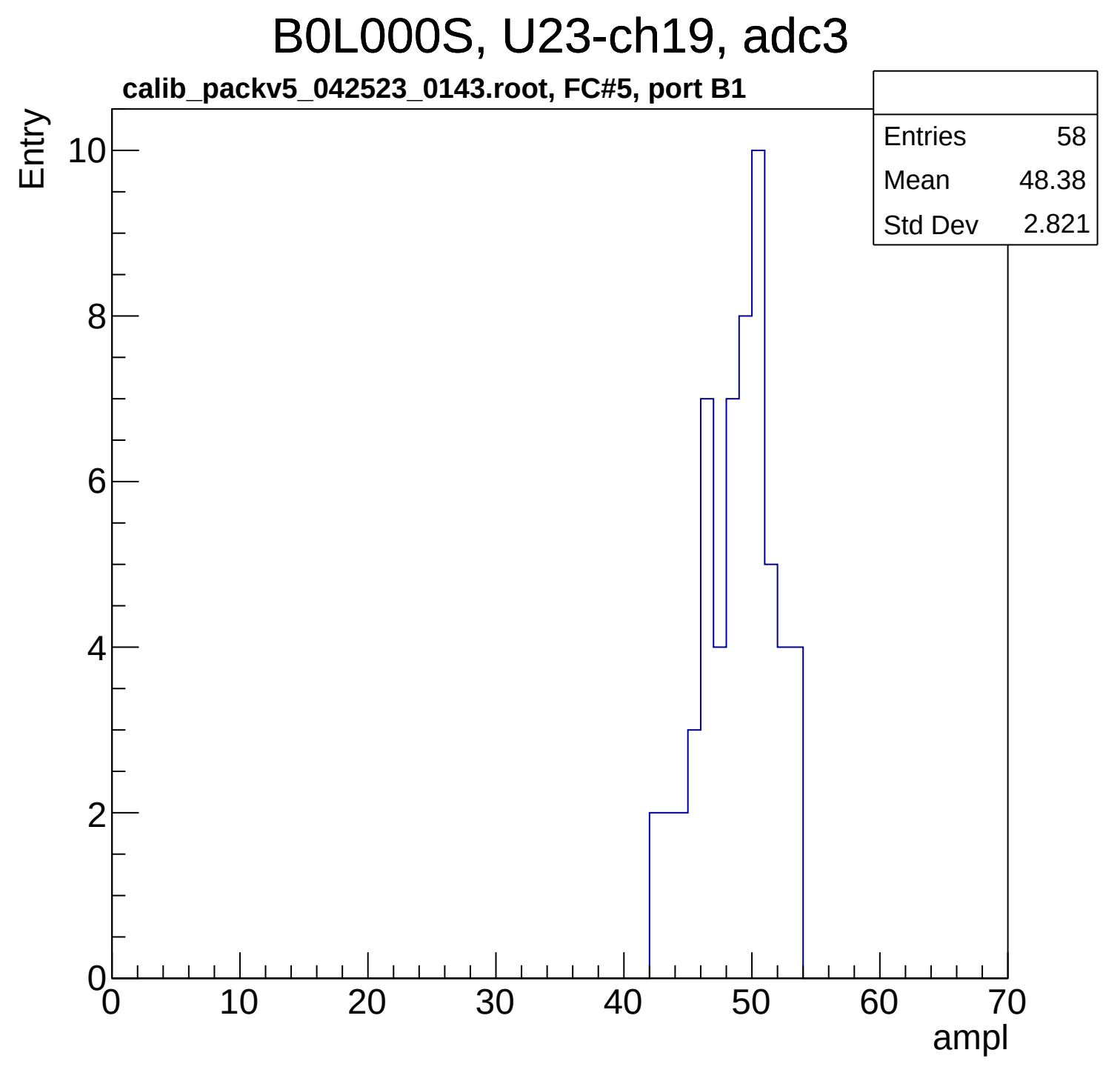

B0L000S, U23-ch18, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 55.44
Std Dev 19.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

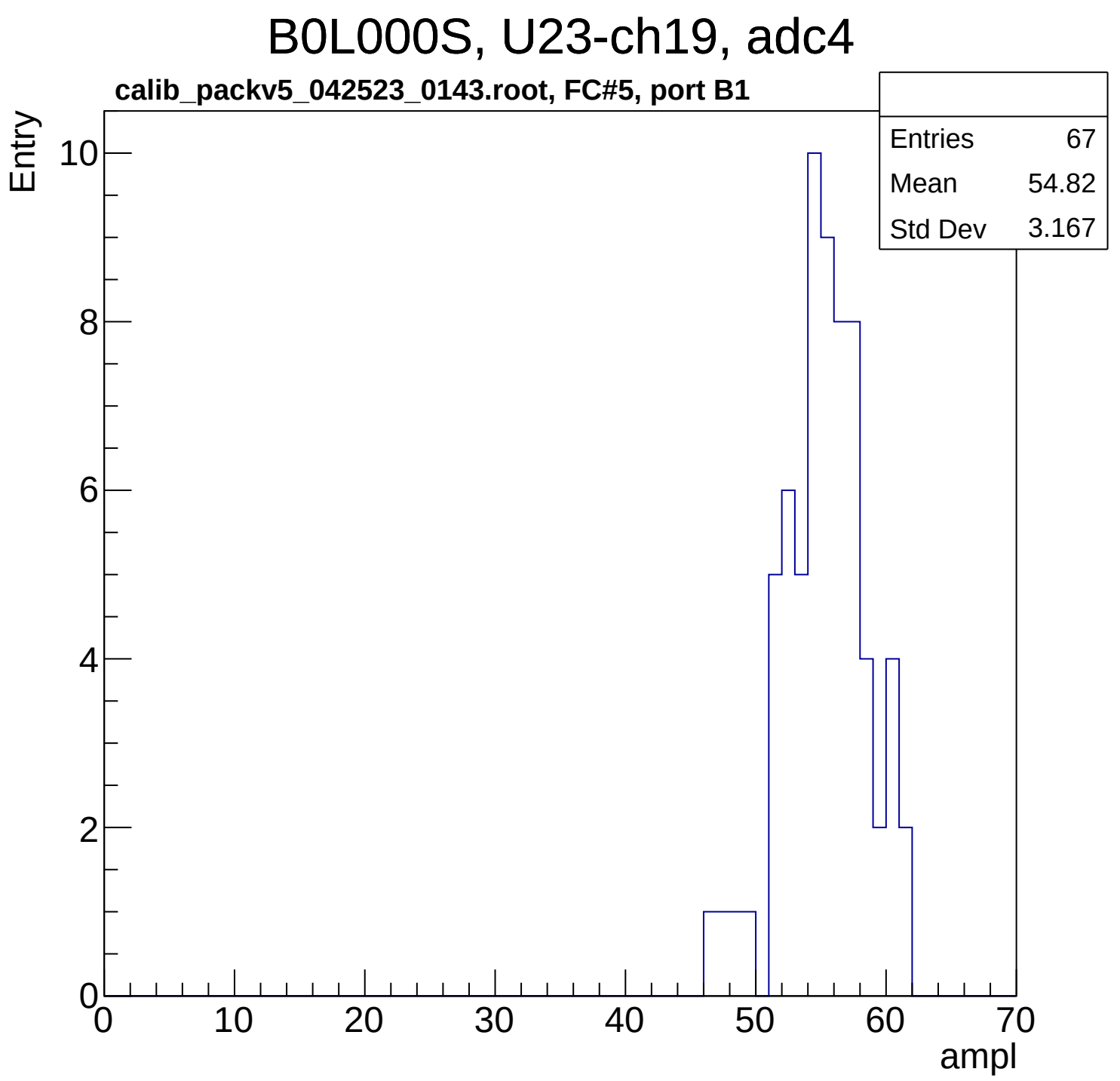

B0L000S, U23-ch18, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

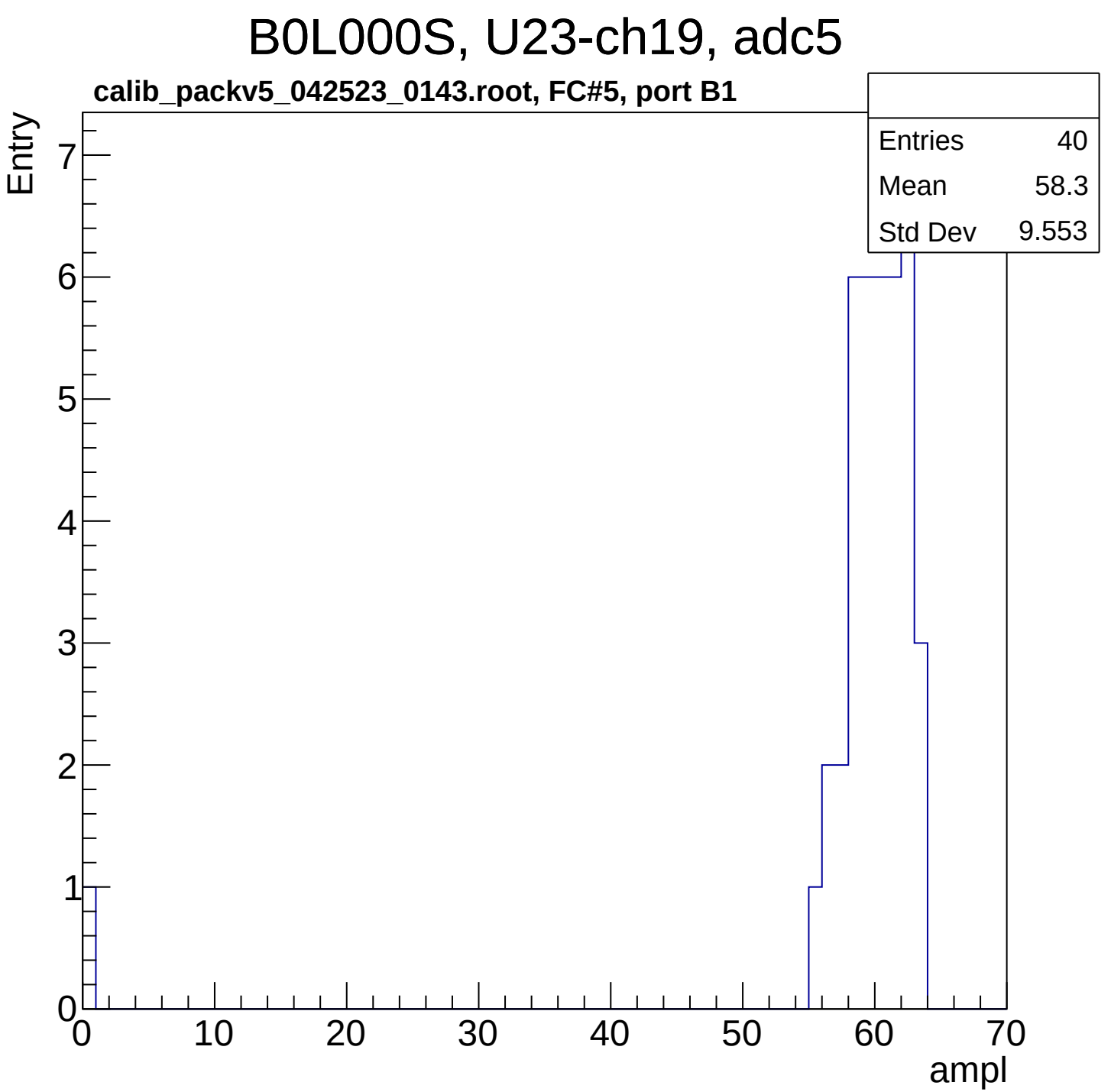

B0L000S, U23-ch19, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 28.55
Std Dev 3.355
Gaus mean : 29.2028
Gaus Width: 3.7680
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

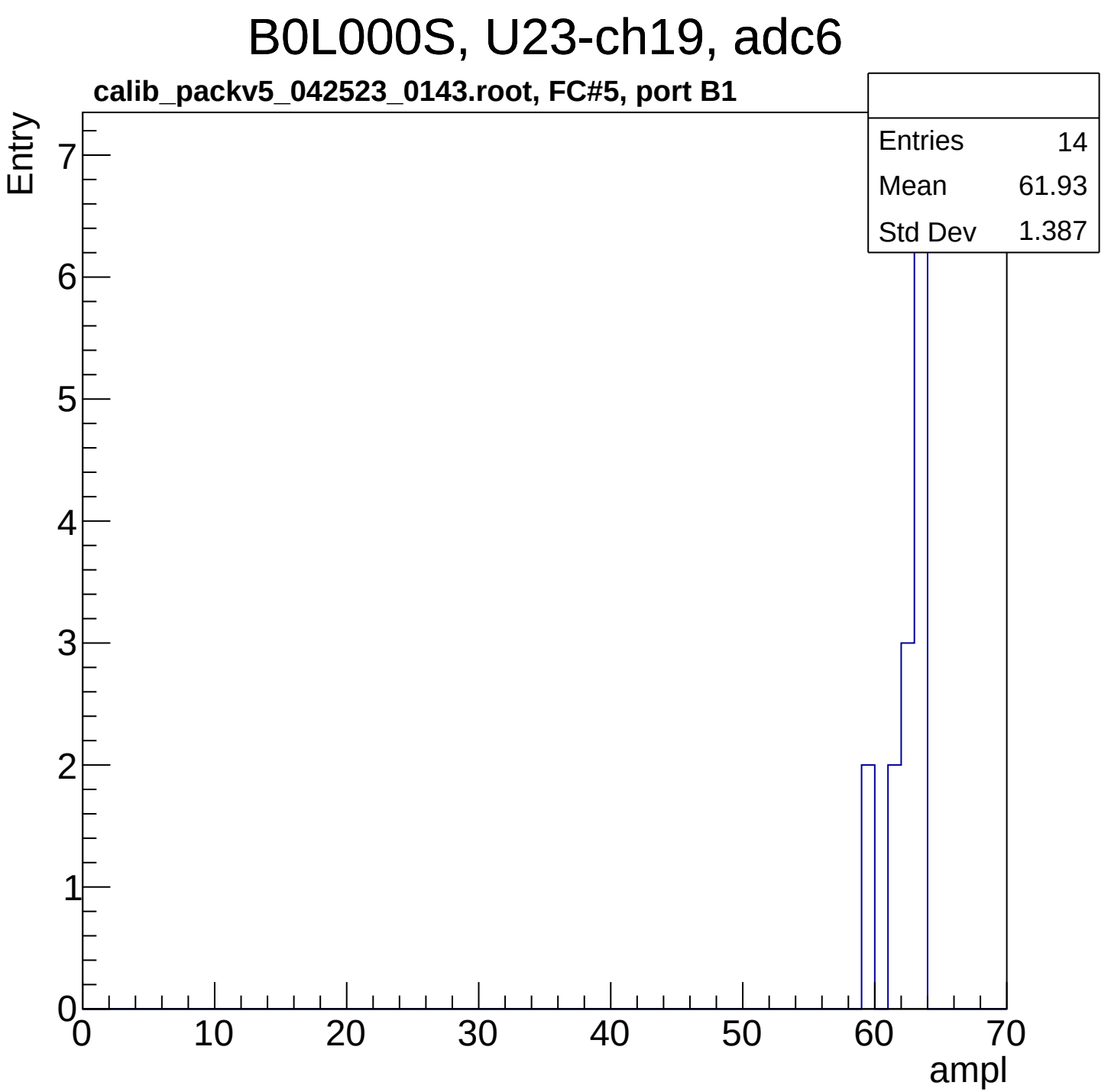

B0L000S, U23-ch19, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.79
Std Dev 3.102
Gaus mean : 36.0970
Gaus Width: 3.3278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

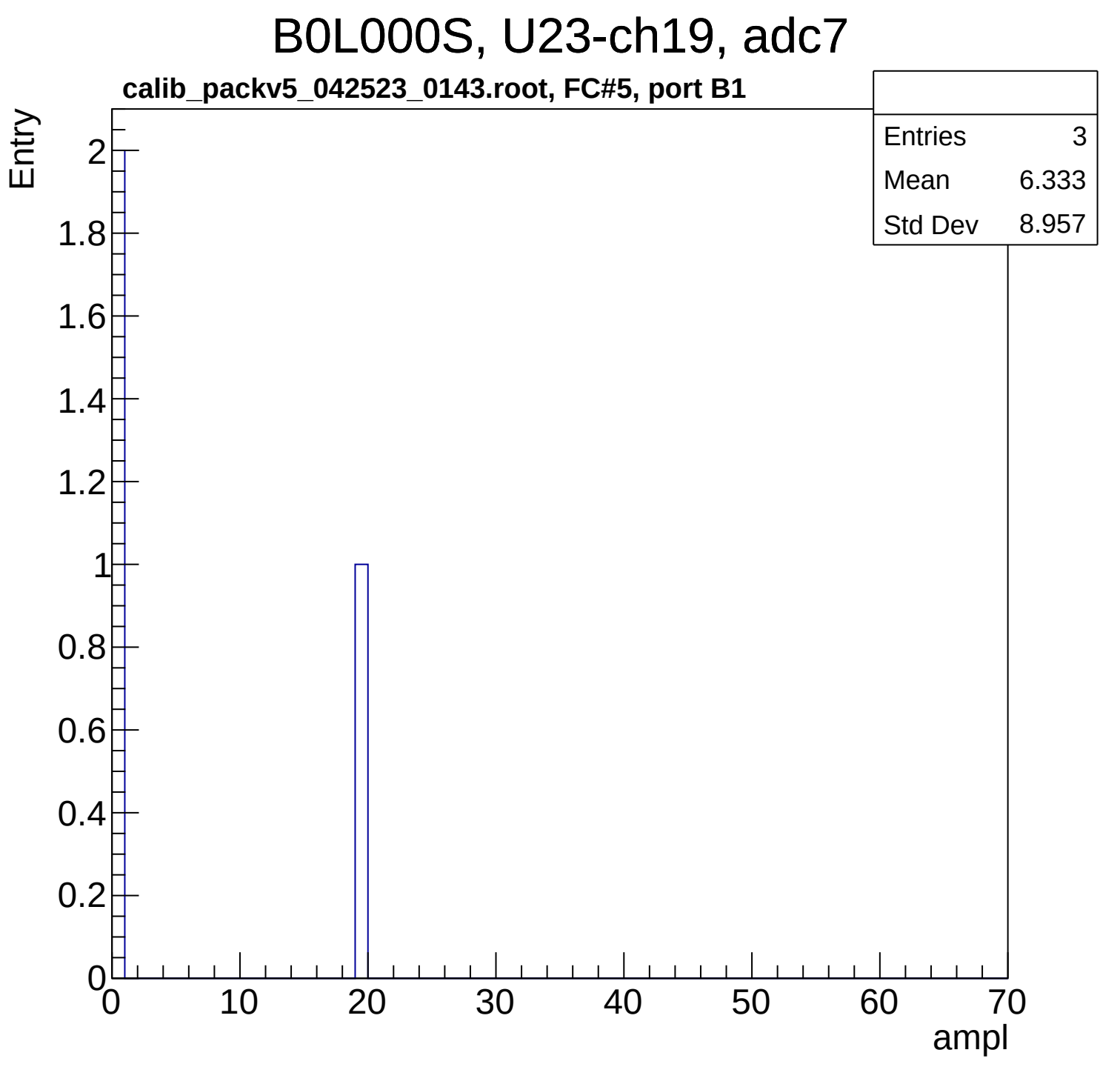

B0L000S, U23-ch19, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 42.54
Std Dev 3.347
Gaus mean : 42.8610
Gaus Width: 3.3058
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

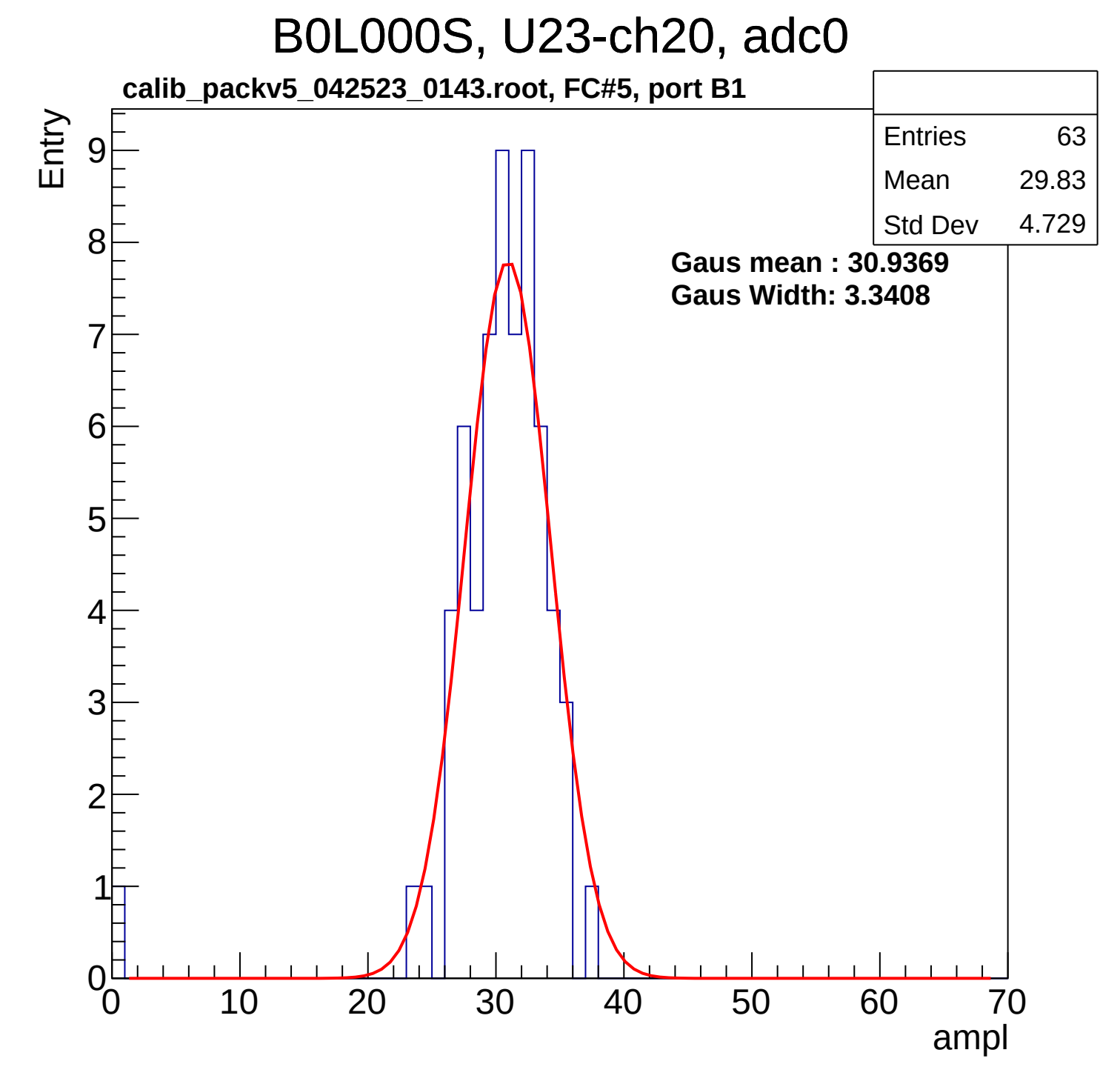

B0L000S, U23-ch19, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.38
Std Dev 2.821
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

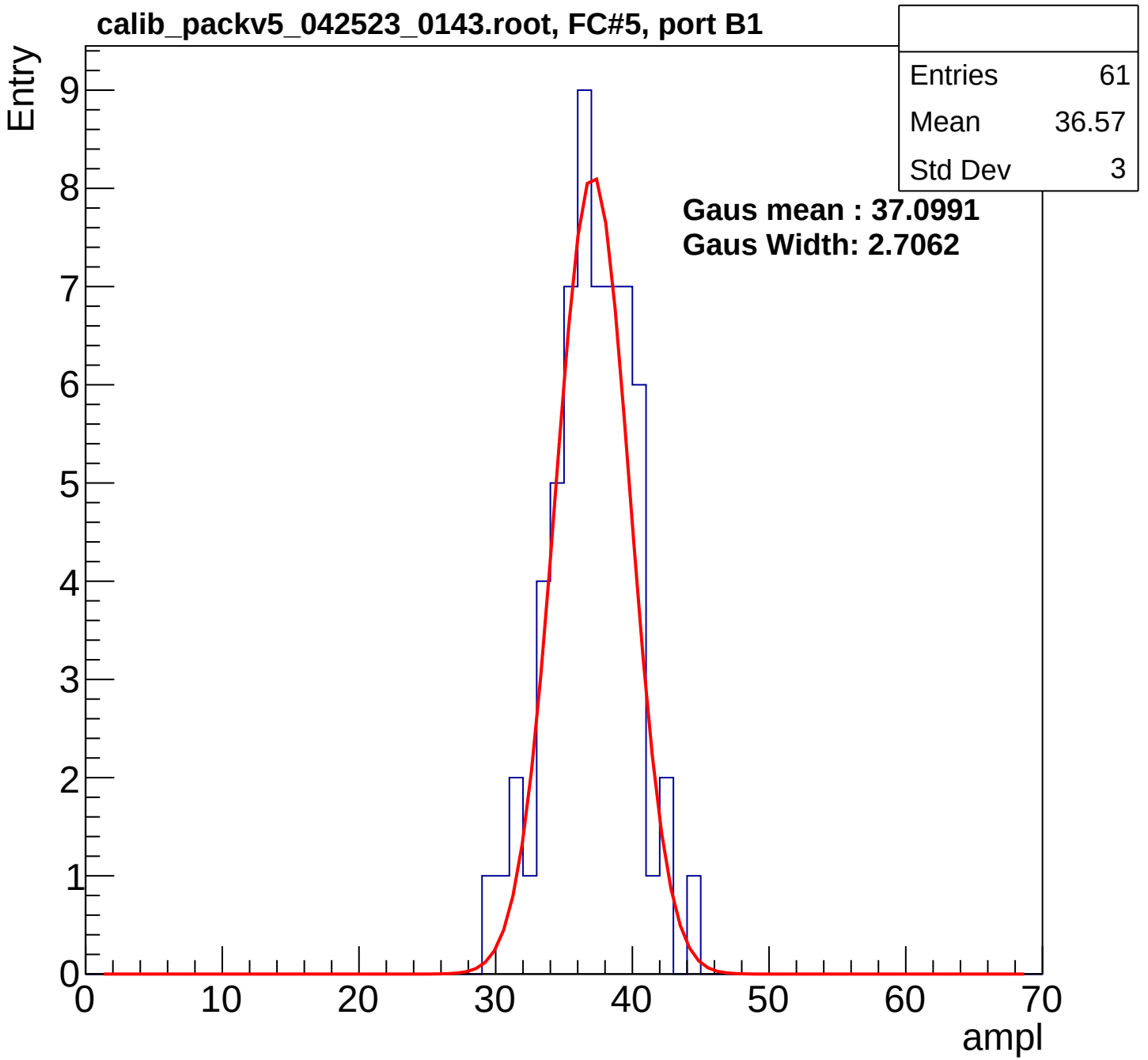

B0L000S, U23-ch19, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 54.82
Std Dev 3.167
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

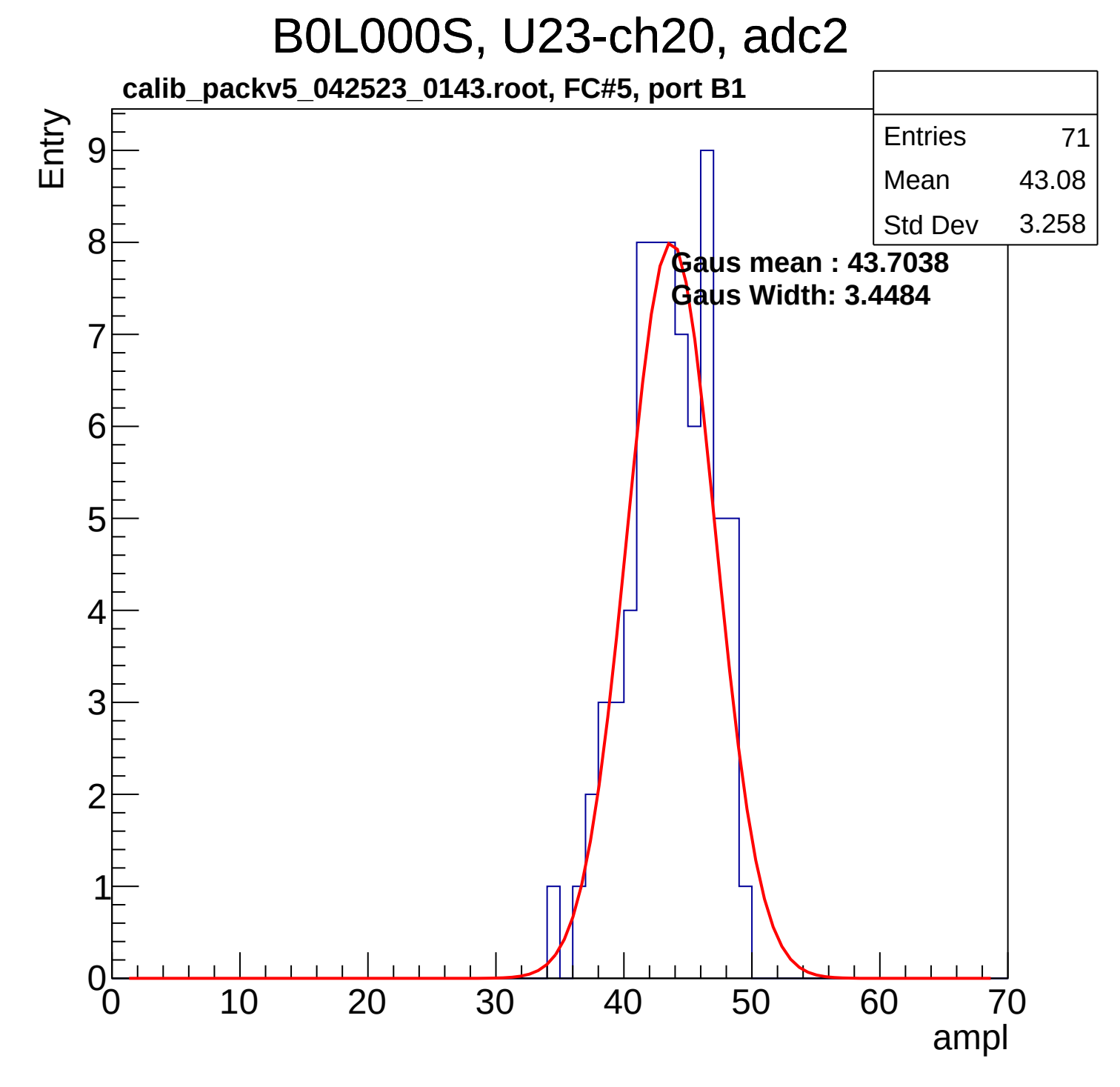

B0L000S, U23-ch19, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.3
Std Dev 9.553
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

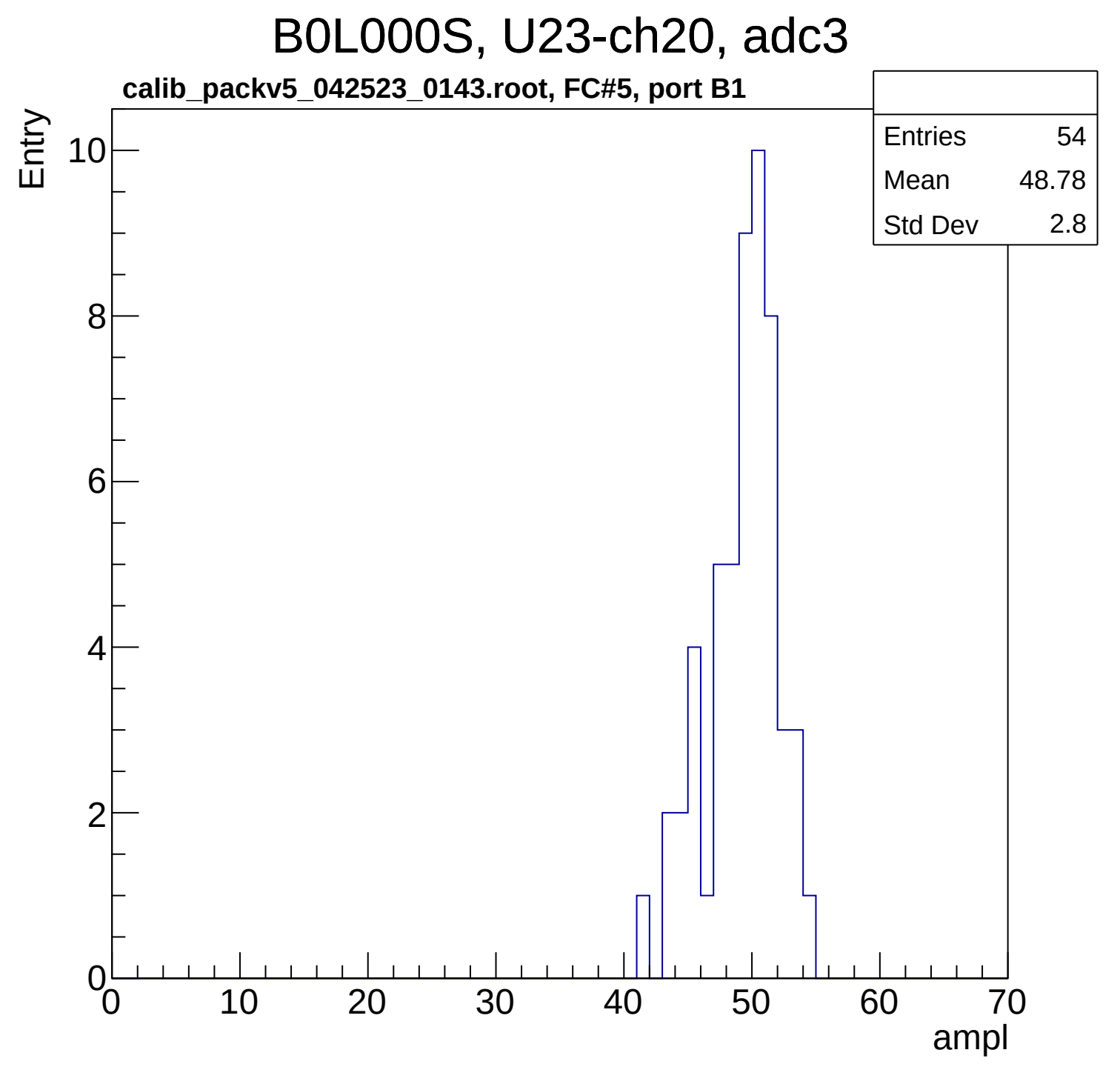

B0L000S, U23-ch19, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.93
Std Dev 1.387
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

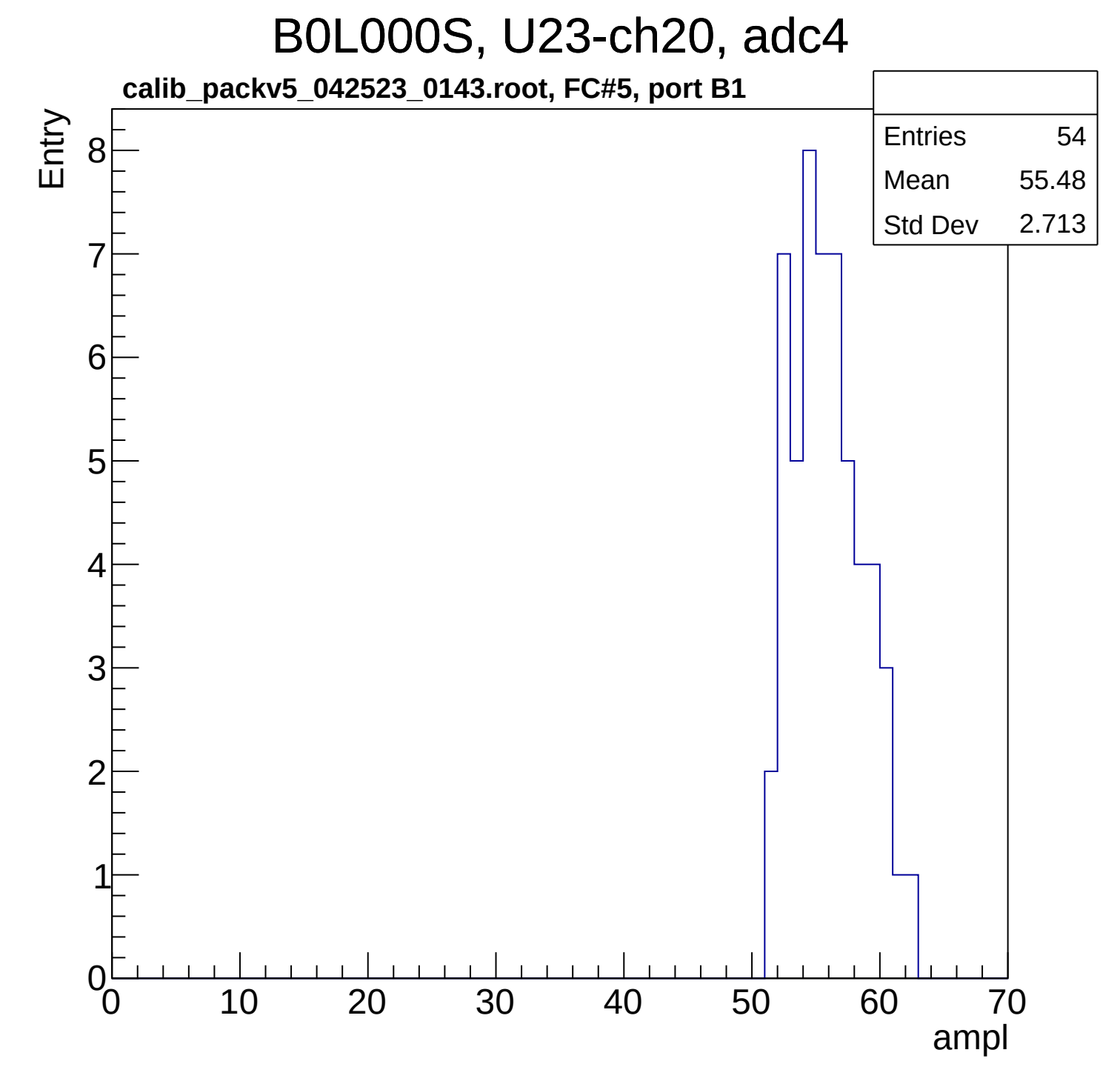

B0L000S, U23-ch19, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

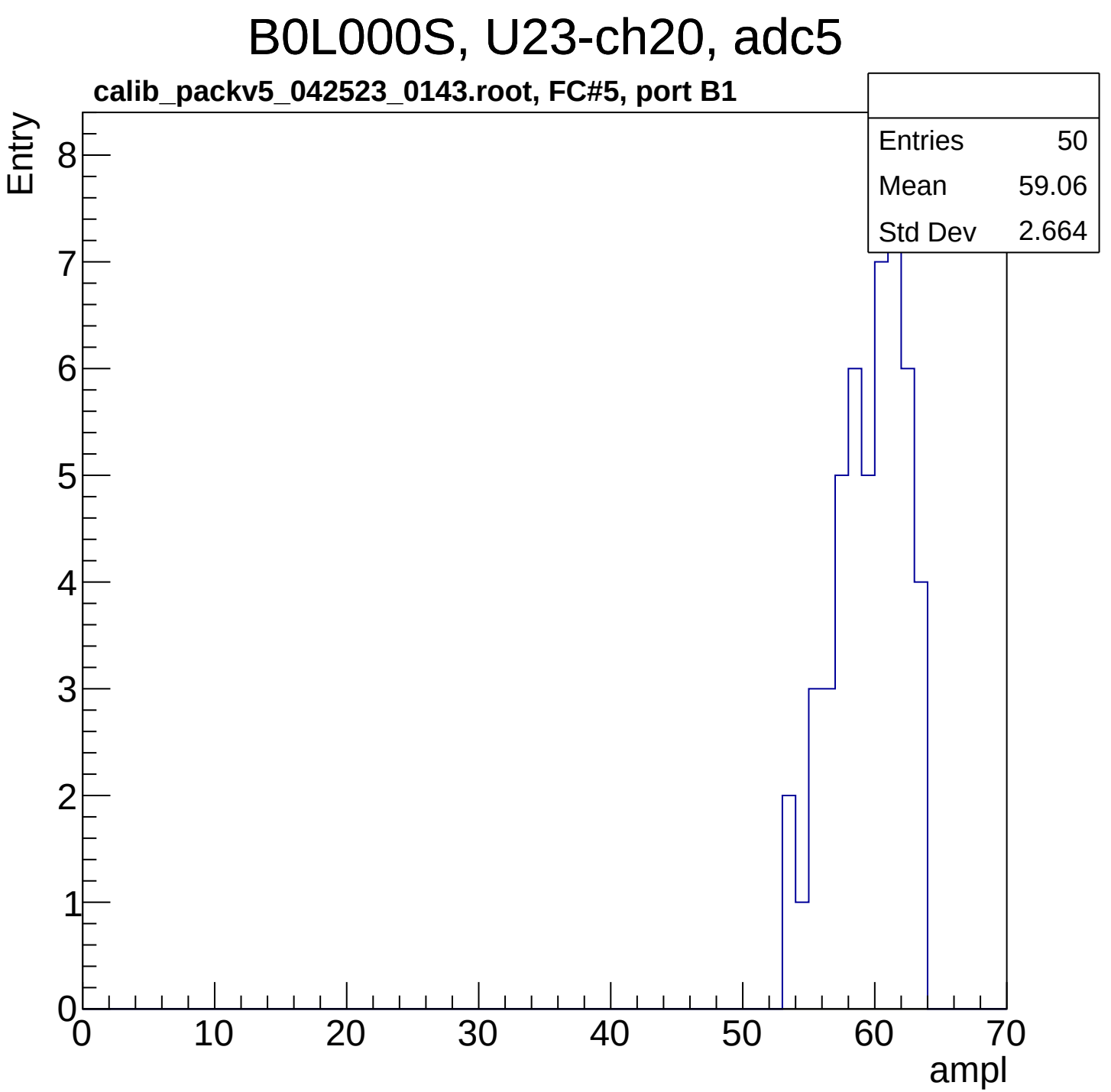

B0L000S, U23-ch20, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.83
Std Dev 4.729
Gaus mean : 30.9369
Gaus Width: 3.3408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

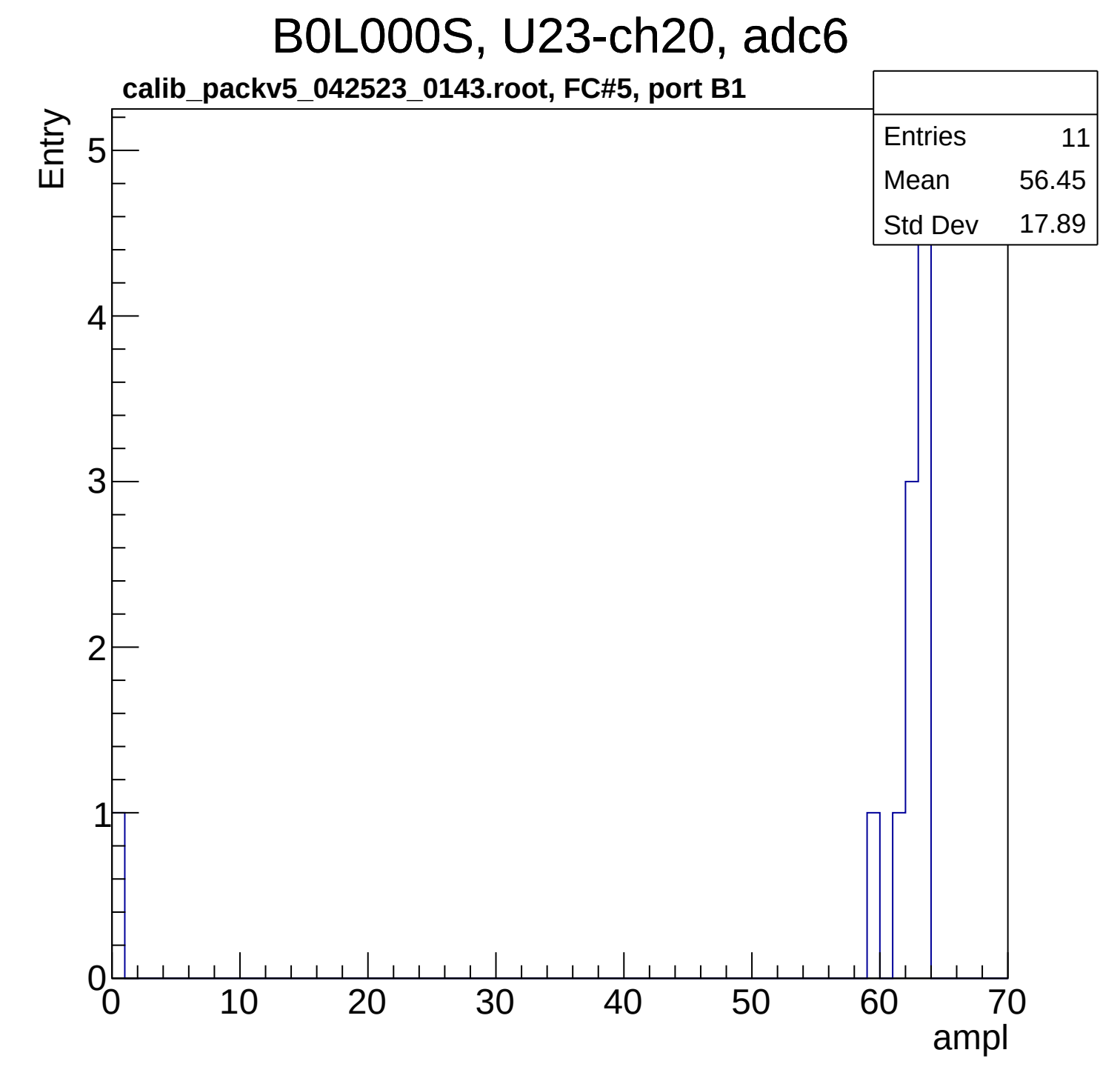

B0L000S, U23-ch20, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.57
Std Dev 3
Gaus mean : 37.0991
Gaus Width: 2.7062
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

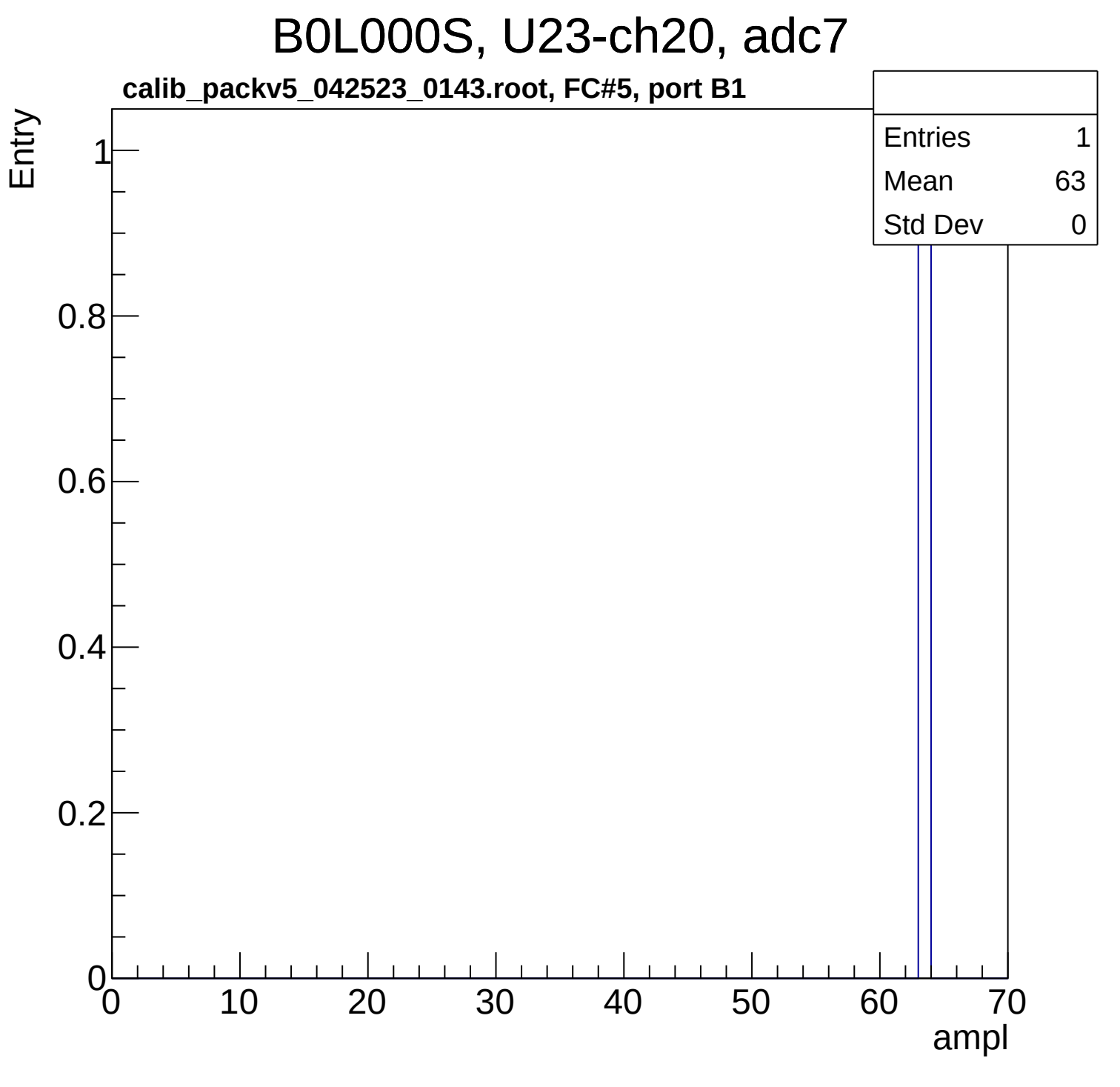

B0L000S, U23-ch20, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.08
Std Dev 3.258
Gaus mean : 43.7038
Gaus Width: 3.4484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

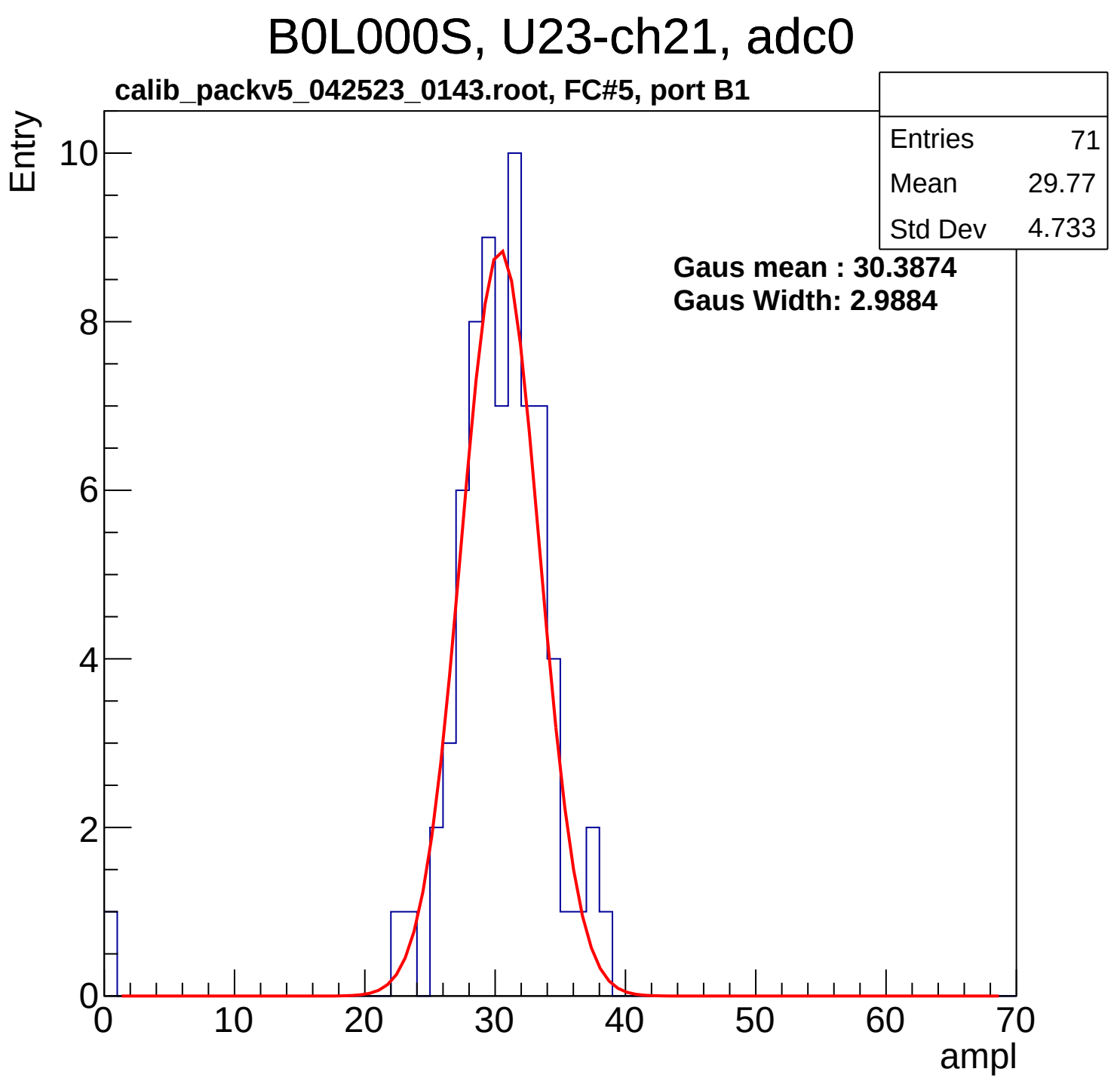

B0L000S, U23-ch20, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 48.78
Std Dev 2.8
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

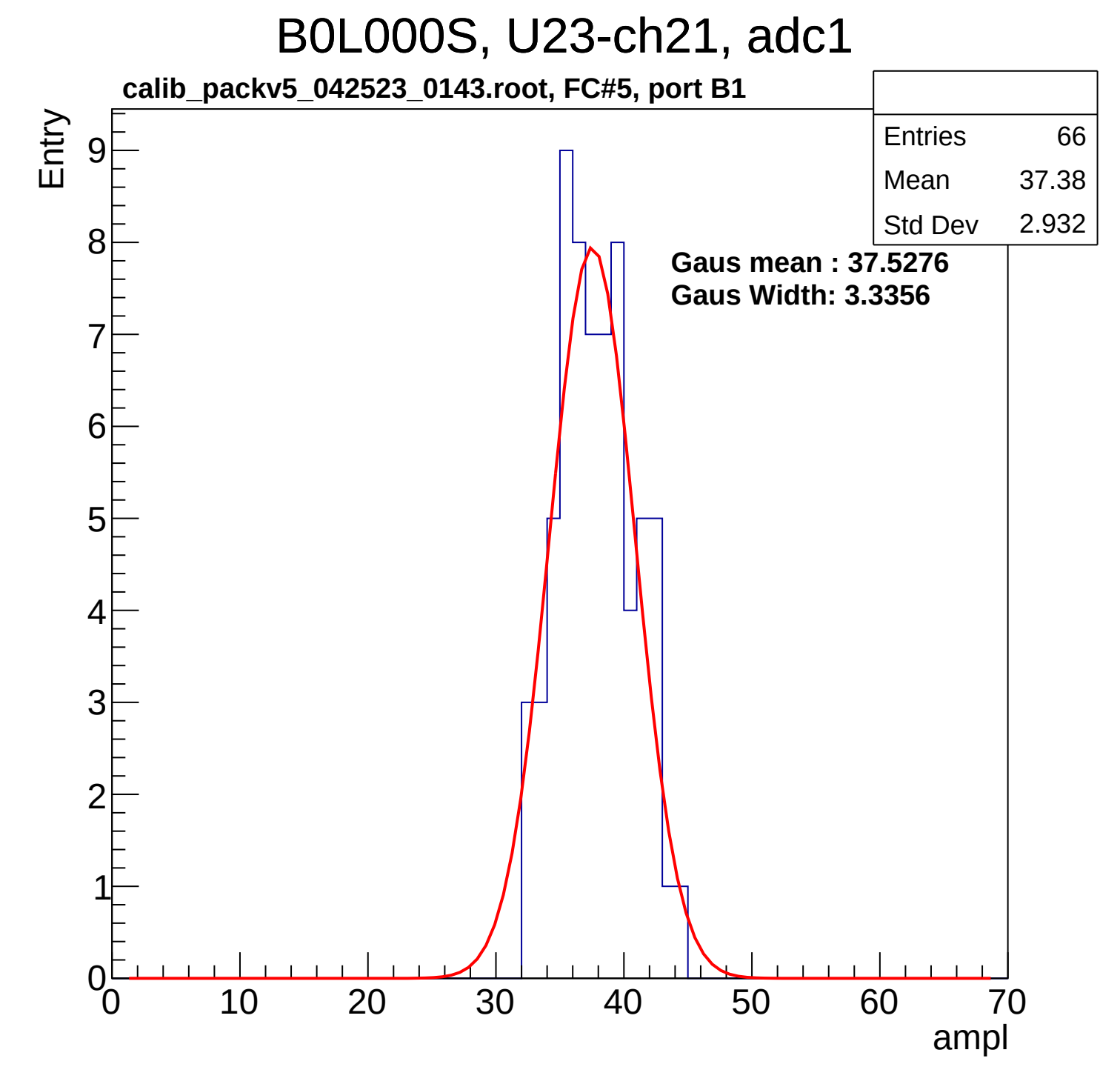

B0L000S, U23-ch20, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.48
Std Dev 2.713
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

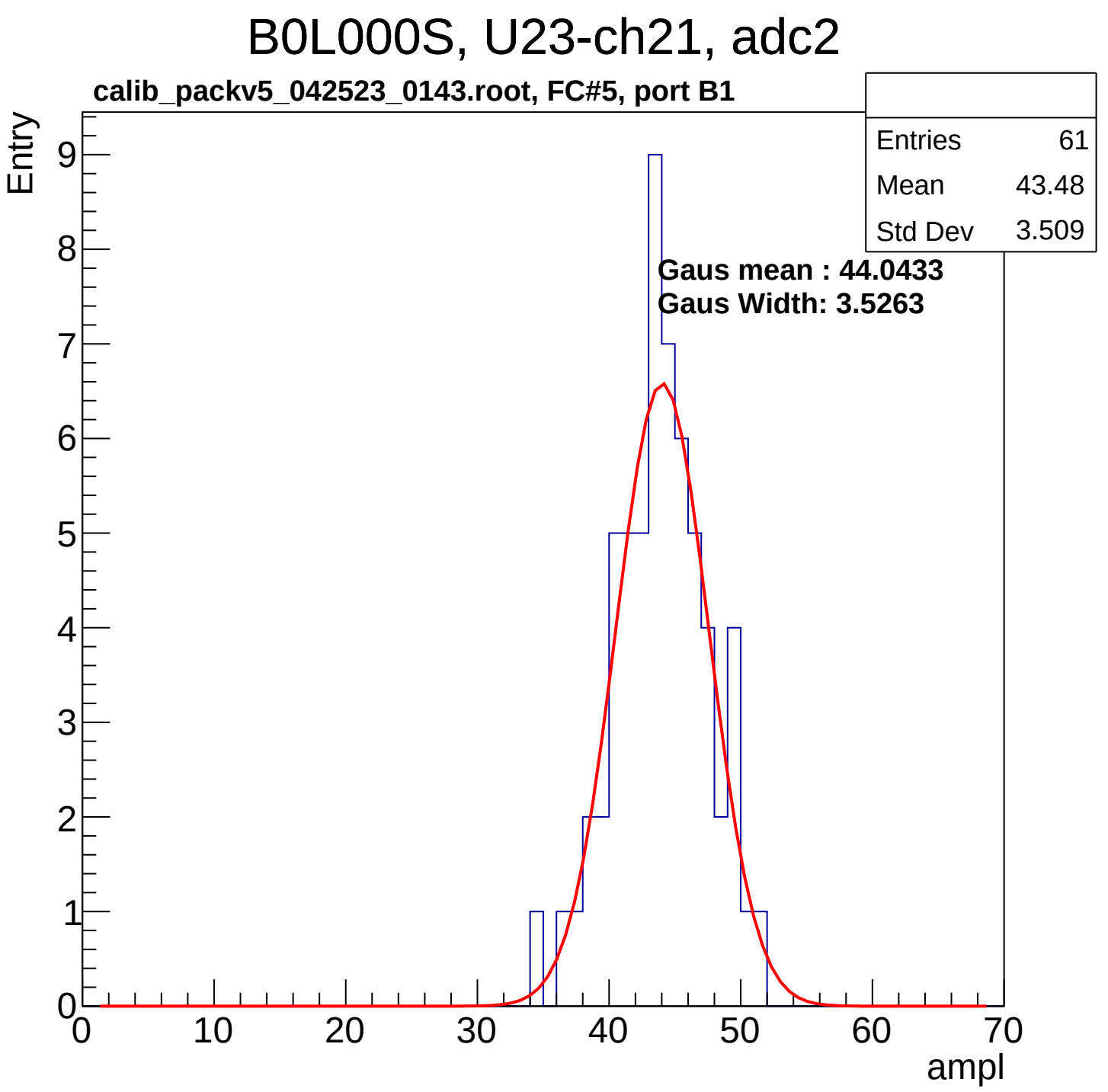

B0L000S, U23-ch20, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.06
Std Dev 2.664
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

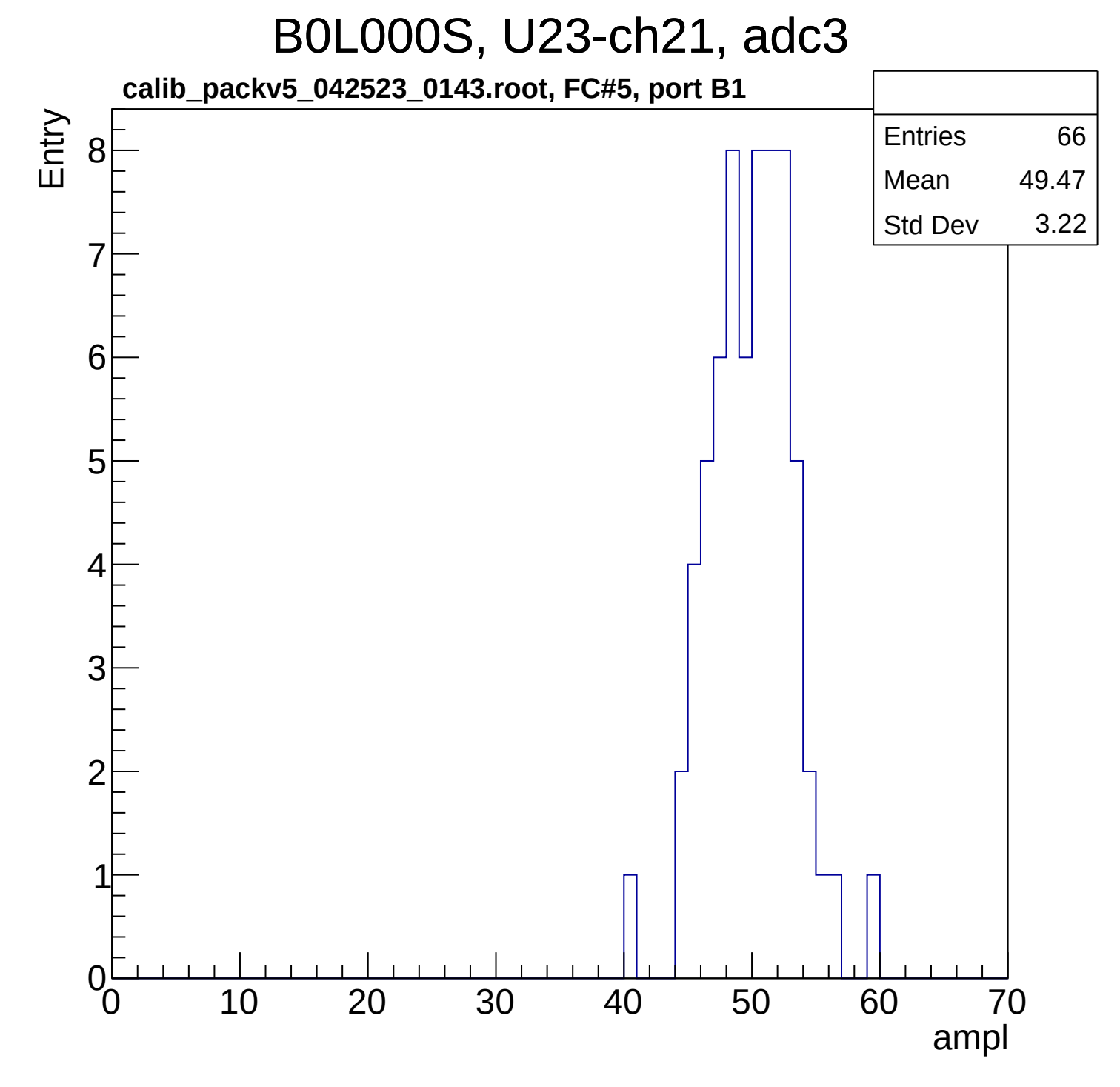

B0L000S, U23-ch20, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 56.45
Std Dev 17.89
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

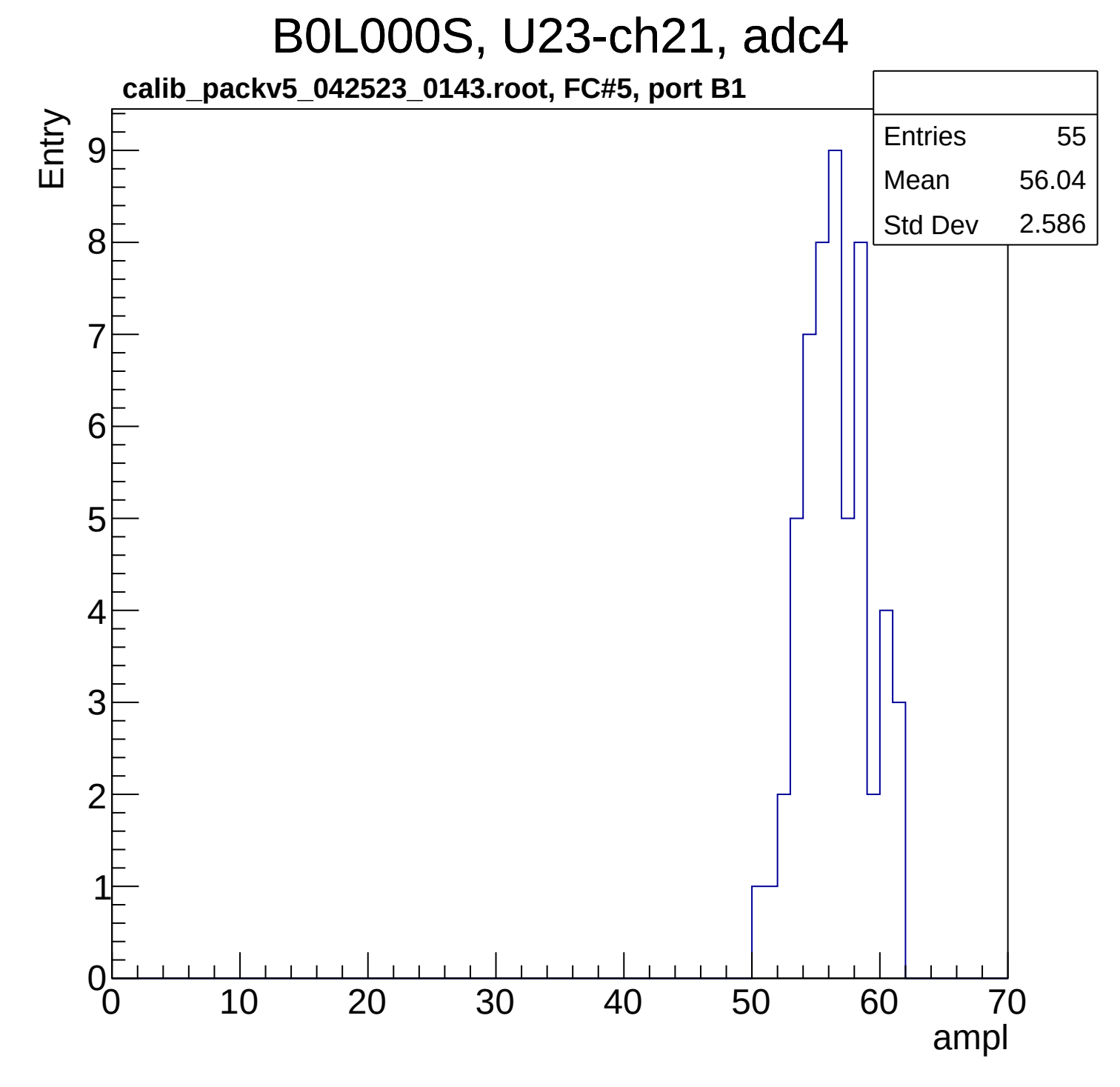

B0L000S, U23-ch20, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

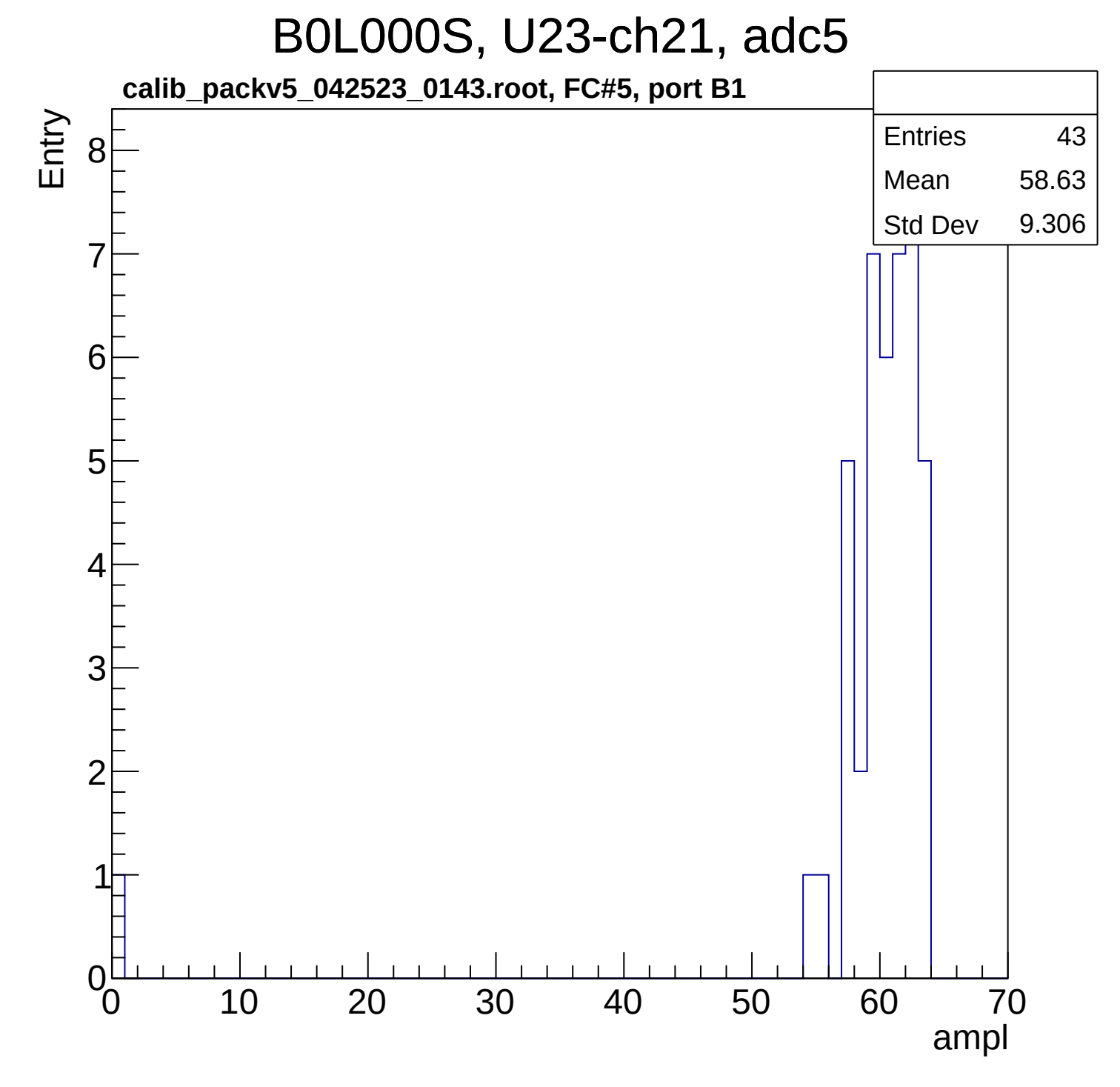

B0L000S, U23-ch21, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.77
Std Dev 4.733
Gaus mean : 30.3874
Gaus Width: 2.9884
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

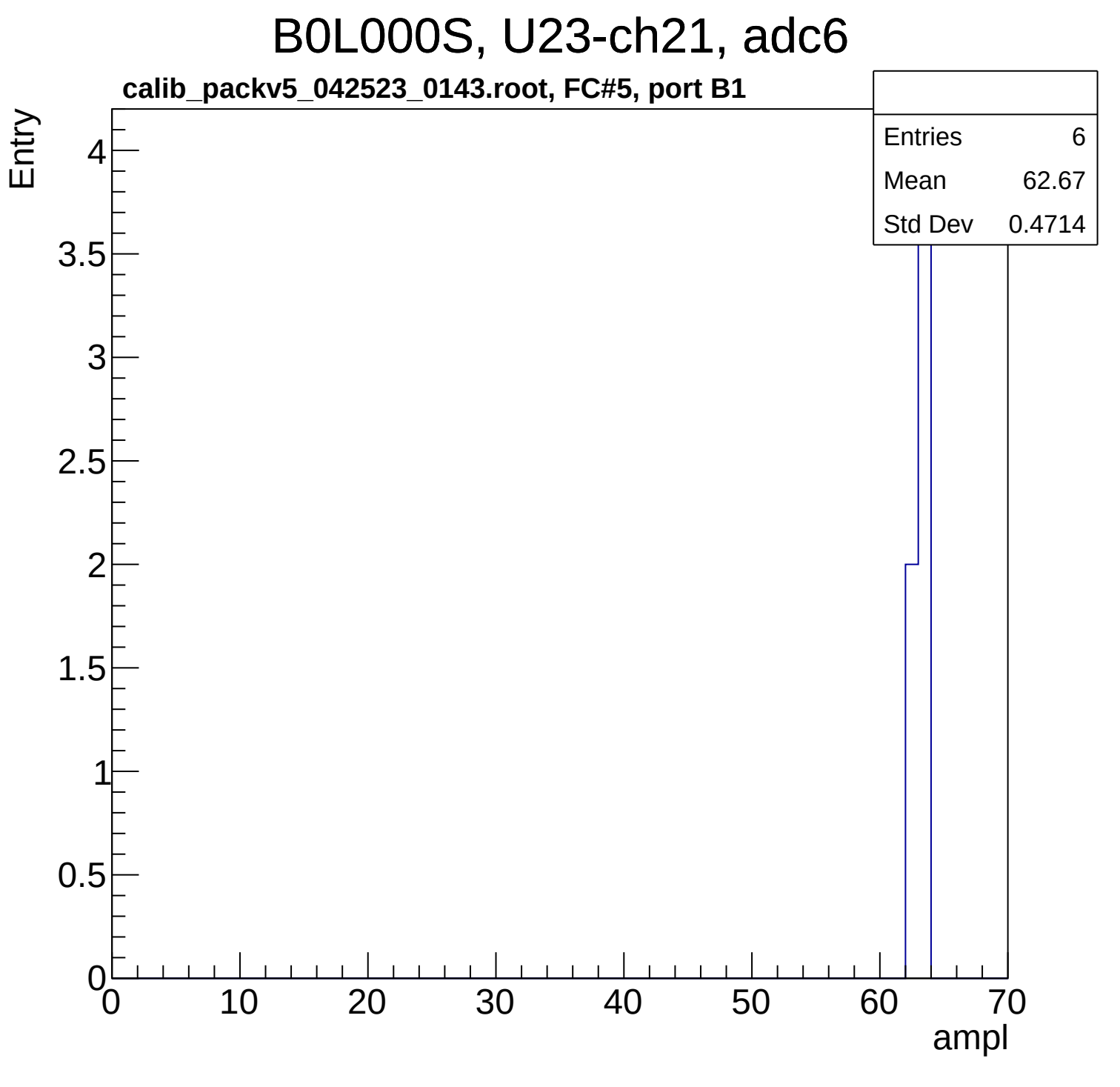

B0L000S, U23-ch21, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 37.38
Std Dev 2.932
Gaus mean : 37.5276
Gaus Width: 3.3356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

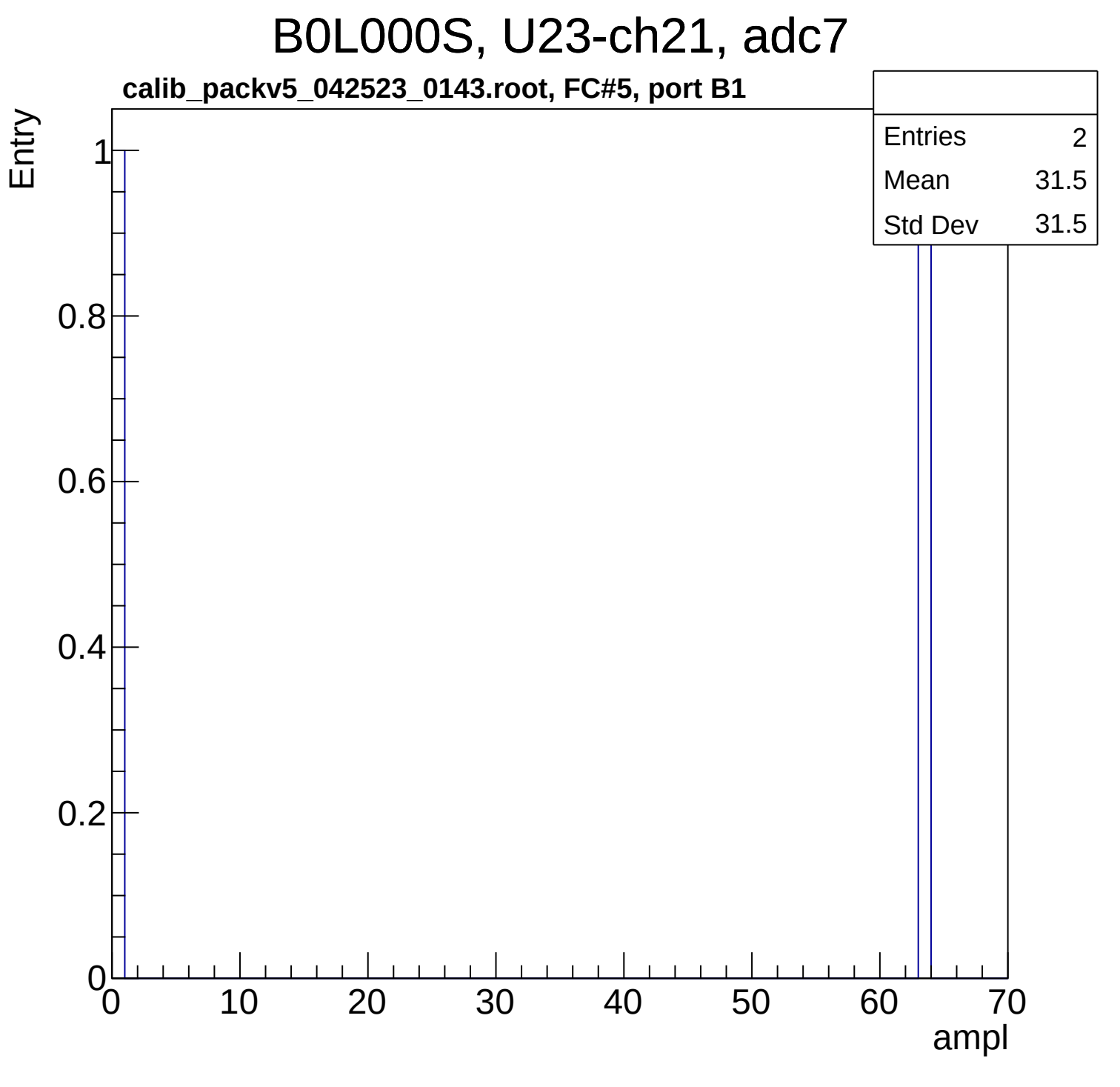

B0L000S, U23-ch21, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 43.48
Std Dev 3.509
Gaus mean : 44.0433
Gaus Width: 3.5263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

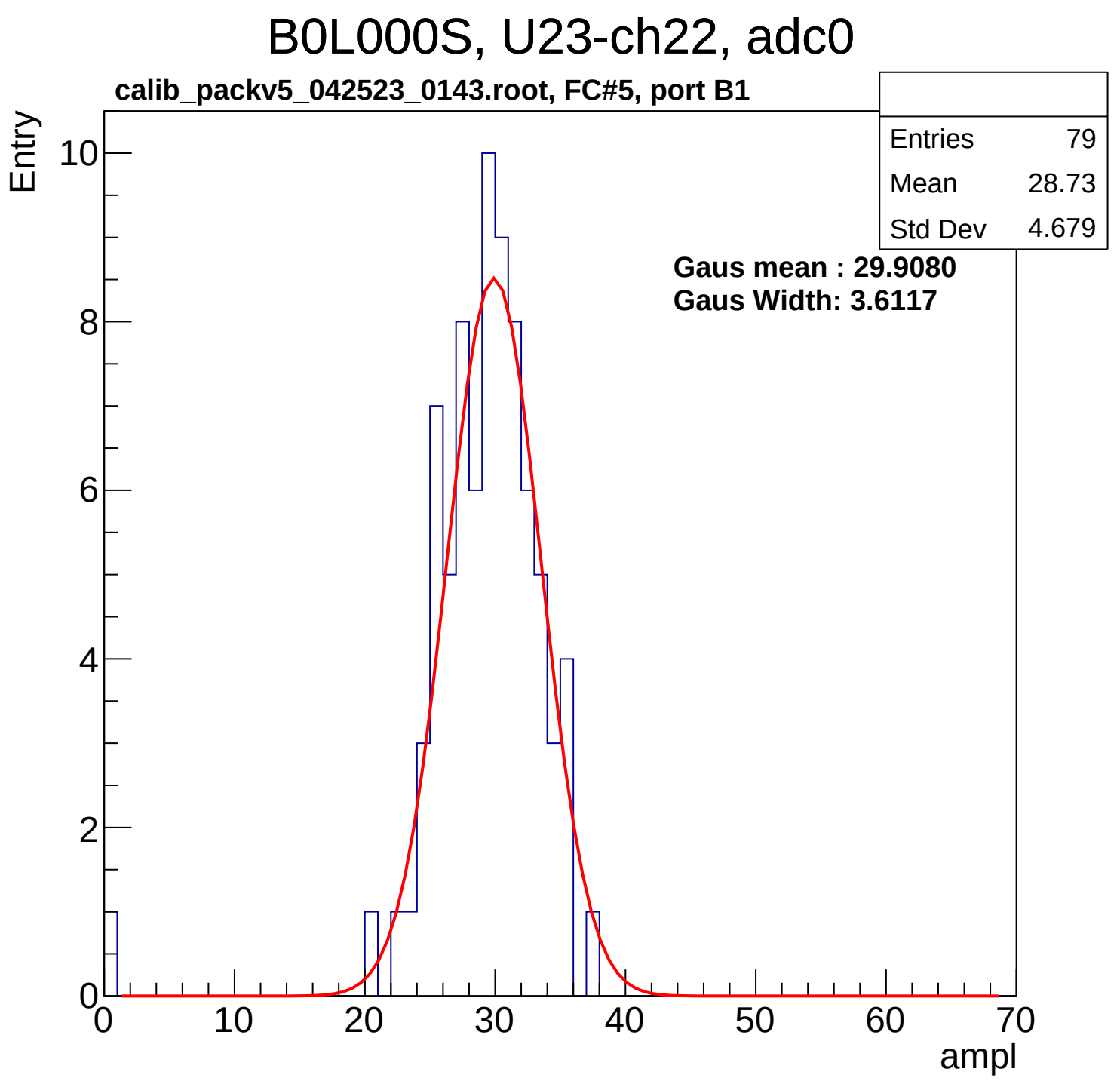

B0L000S, U23-ch21, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.47
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

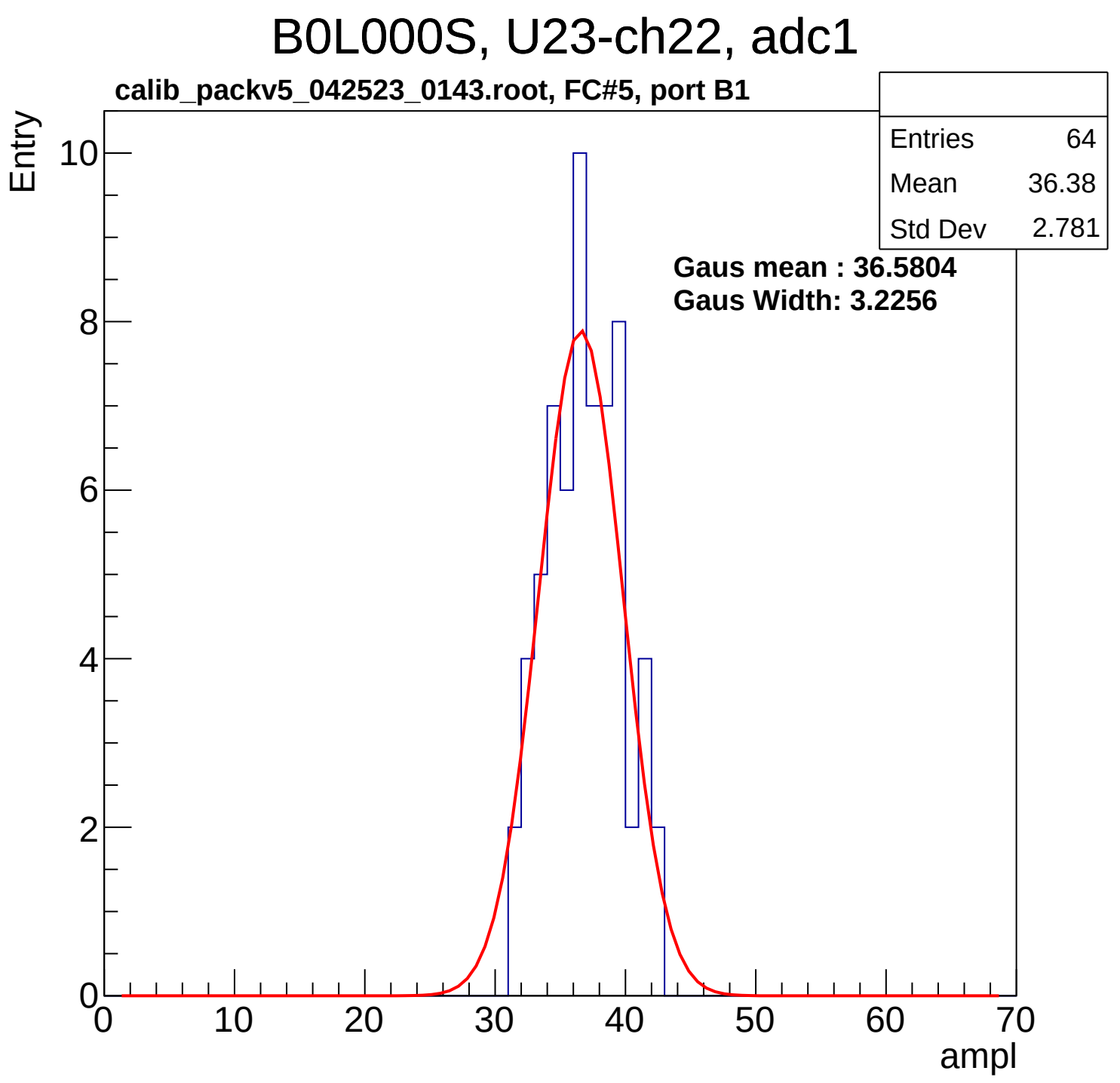

B0L000S, U23-ch21, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.04
Std Dev 2.586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

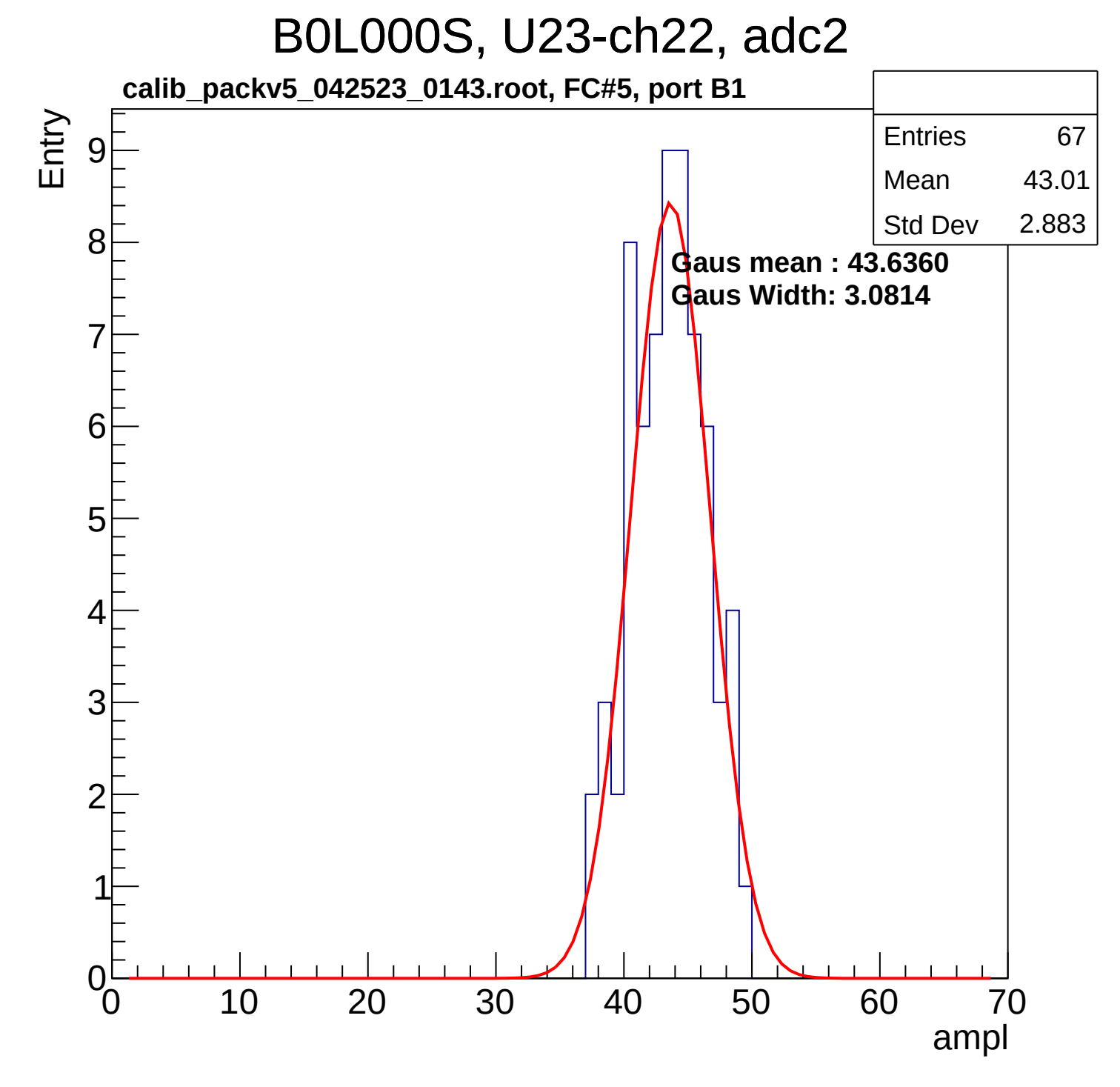

B0L000S, U23-ch21, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.63
Std Dev 9.306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

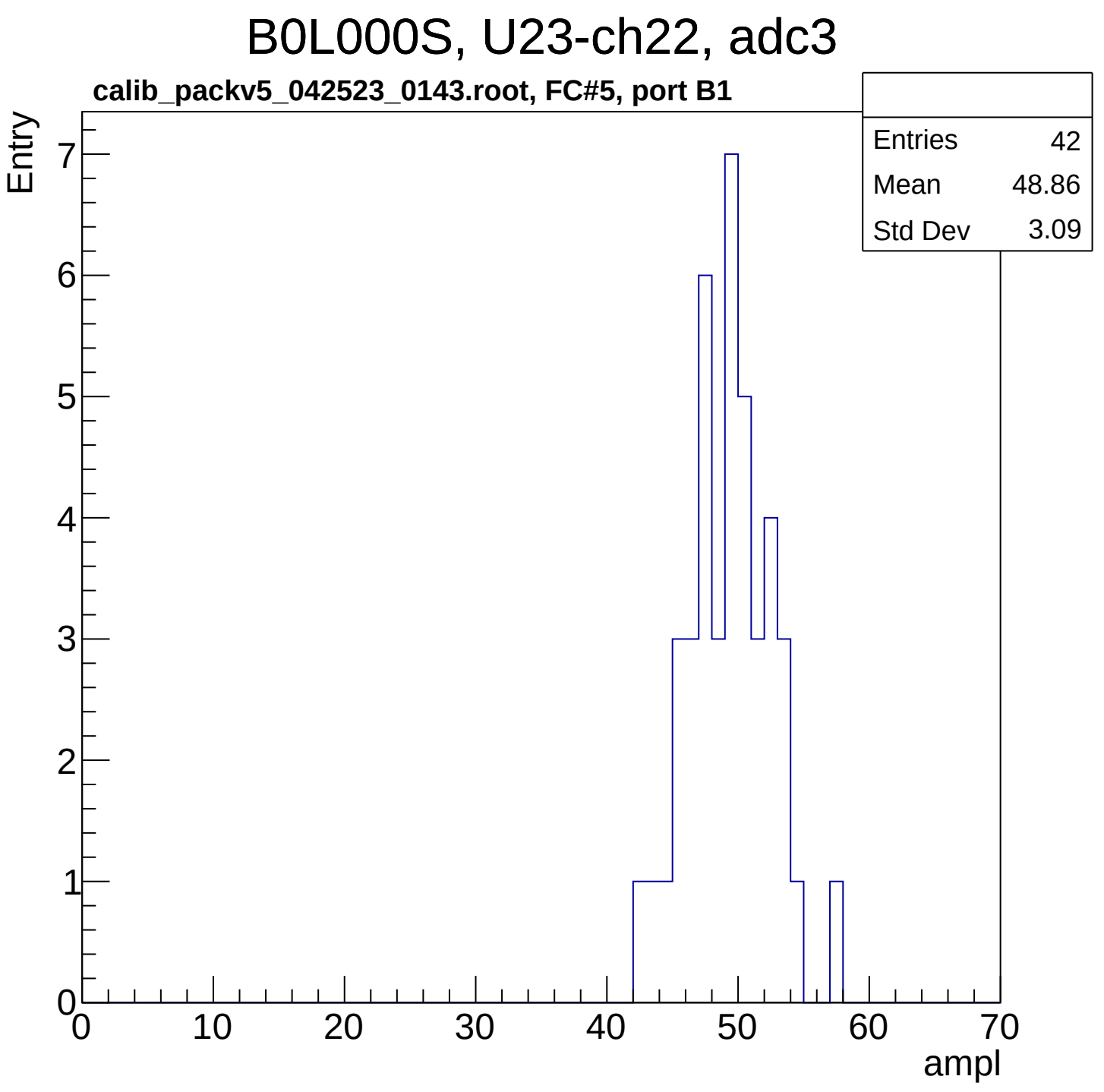

B0L000S, U23-ch21, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

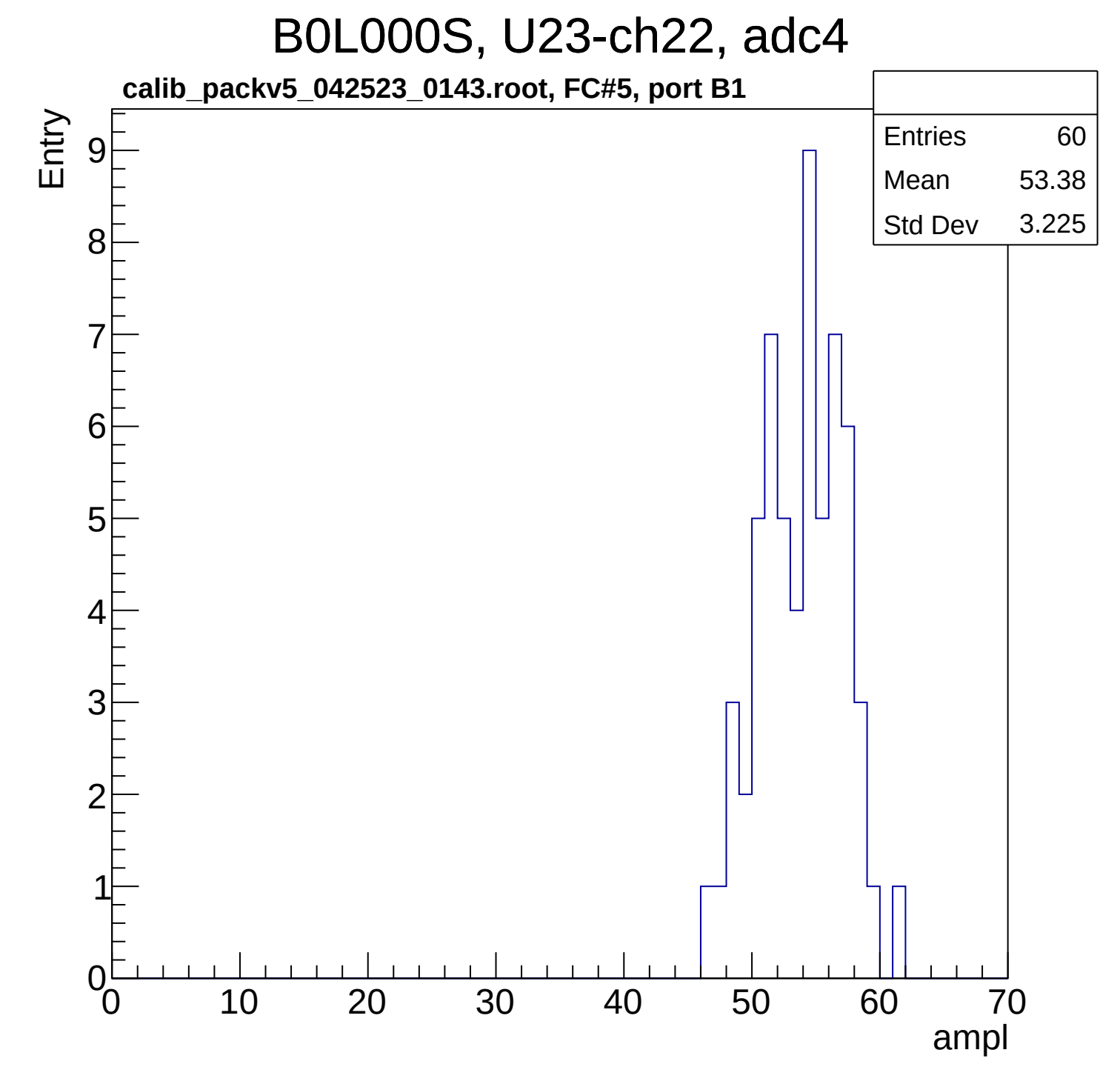

B0L000S, U23-ch21, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

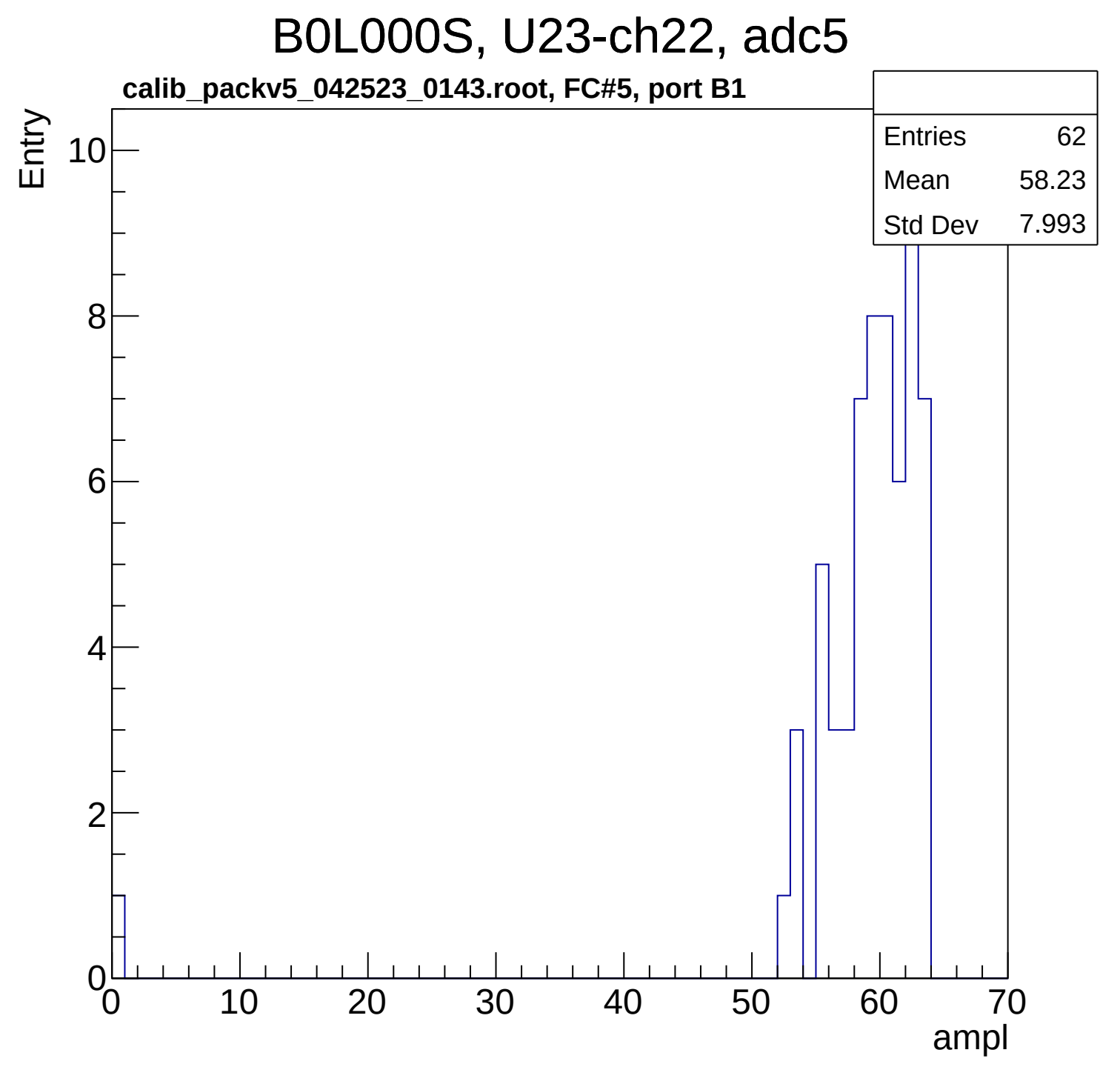

B0L000S, U23-ch22, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 28.73
Std Dev 4.679
Gaus mean : 29.9080
Gaus Width: 3.6117
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

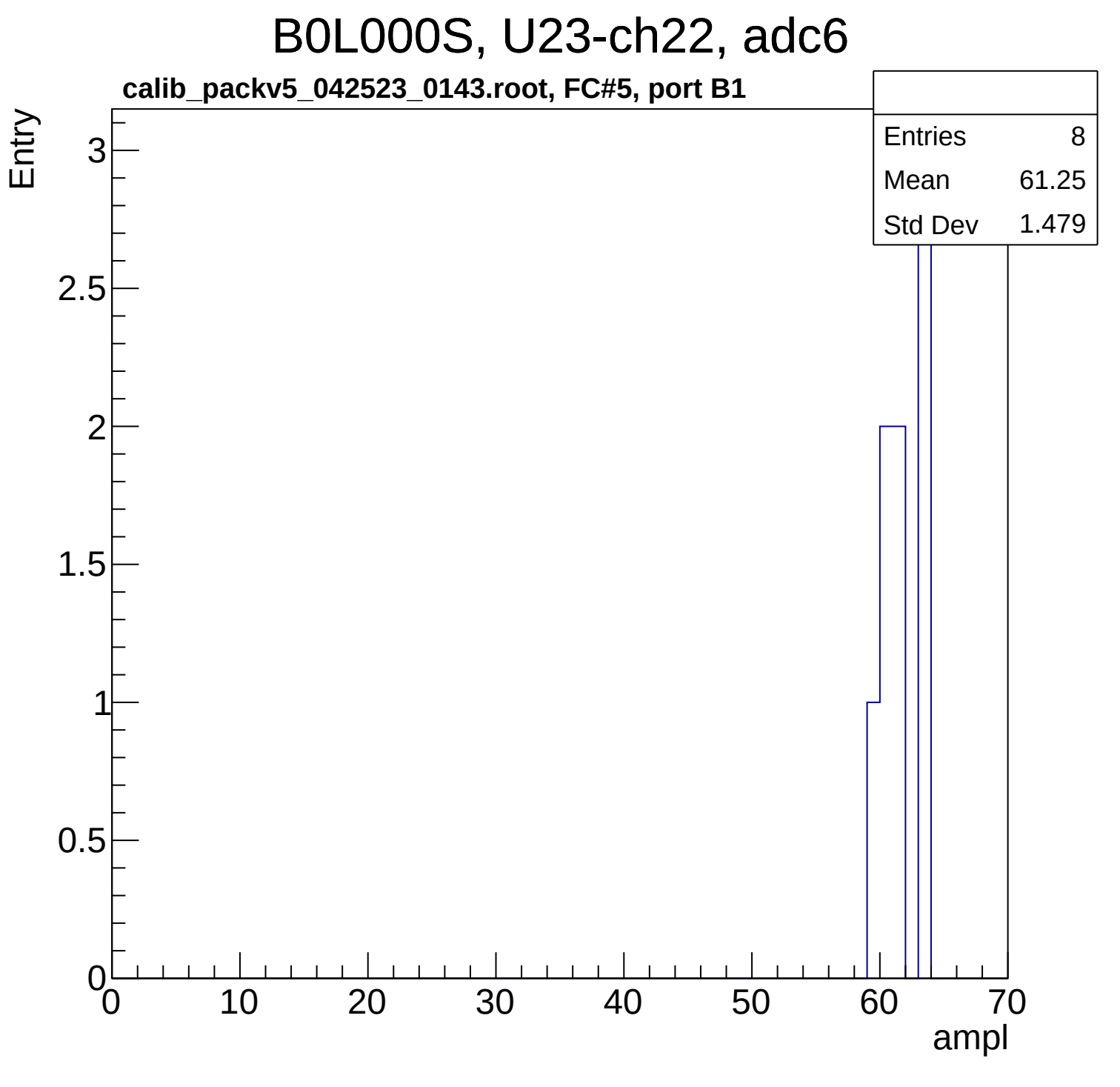

B0L000S, U23-ch22, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 36.38
Std Dev 2.781
Gaus mean : 36.5804
Gaus Width: 3.2256
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

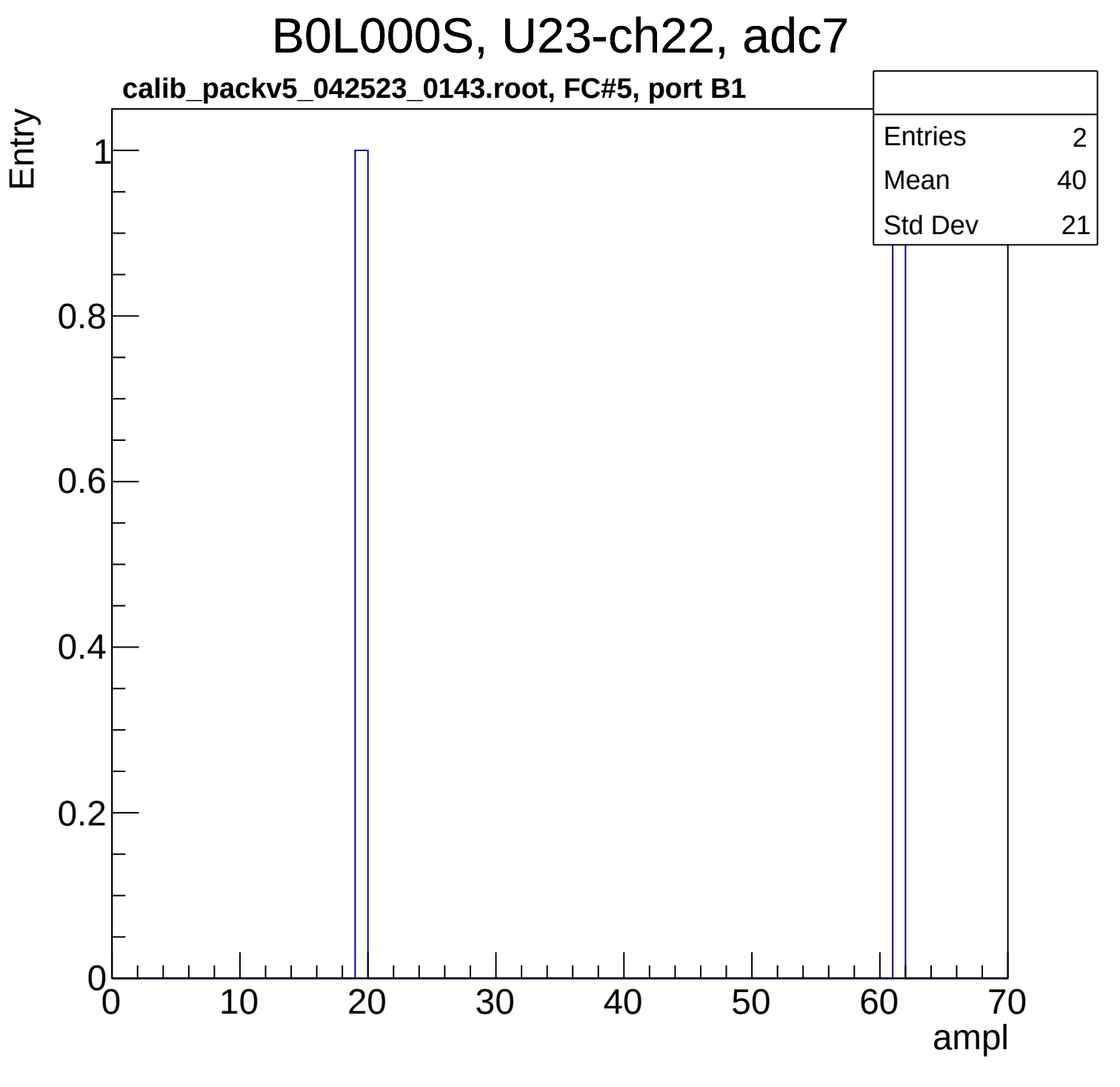

B0L000S, U23-ch22, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.01
Std Dev 2.883
Gaus mean : 43.6360
Gaus Width: 3.0814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

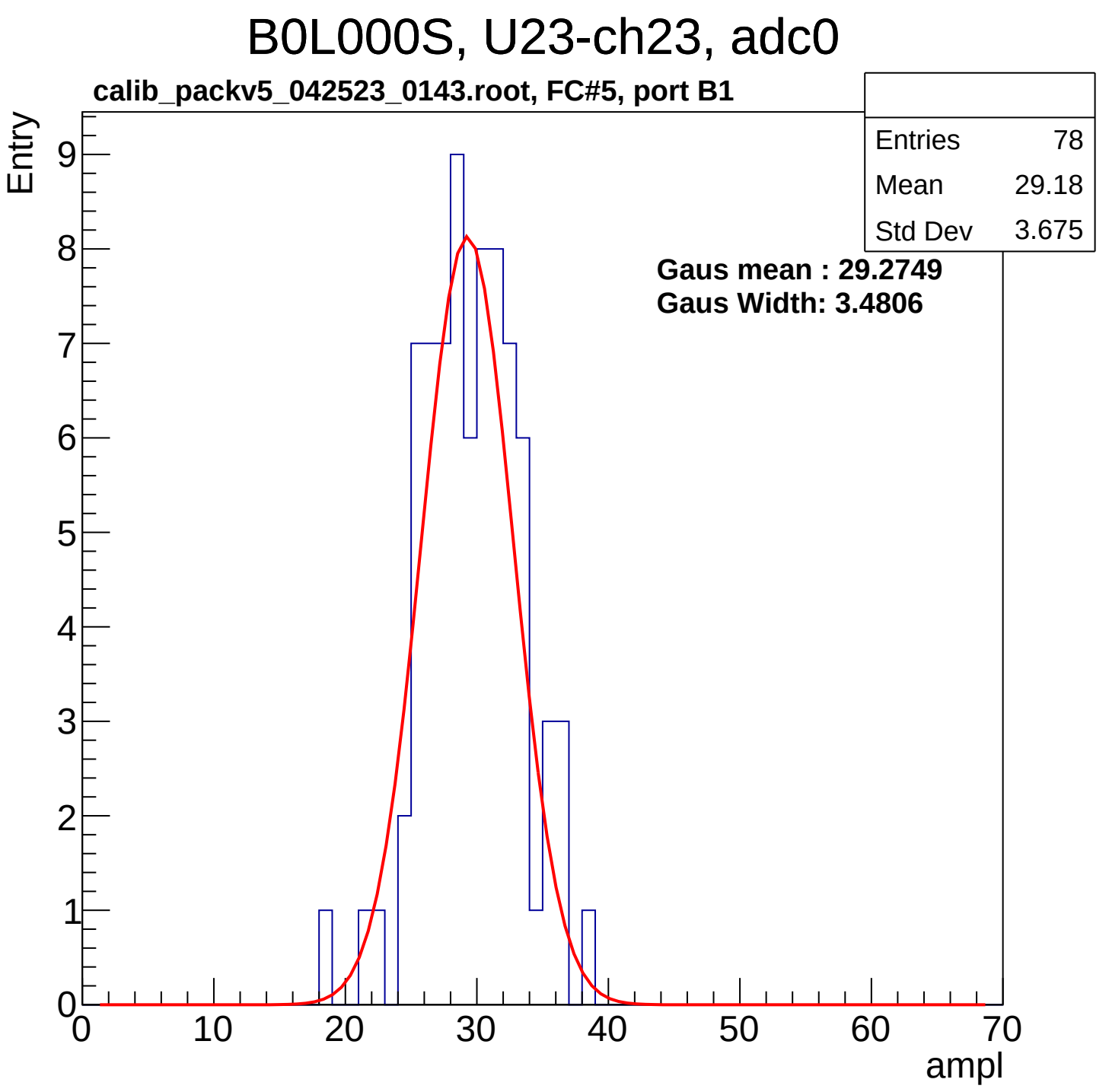

B0L000S, U23-ch22, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 48.86
Std Dev 3.09
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

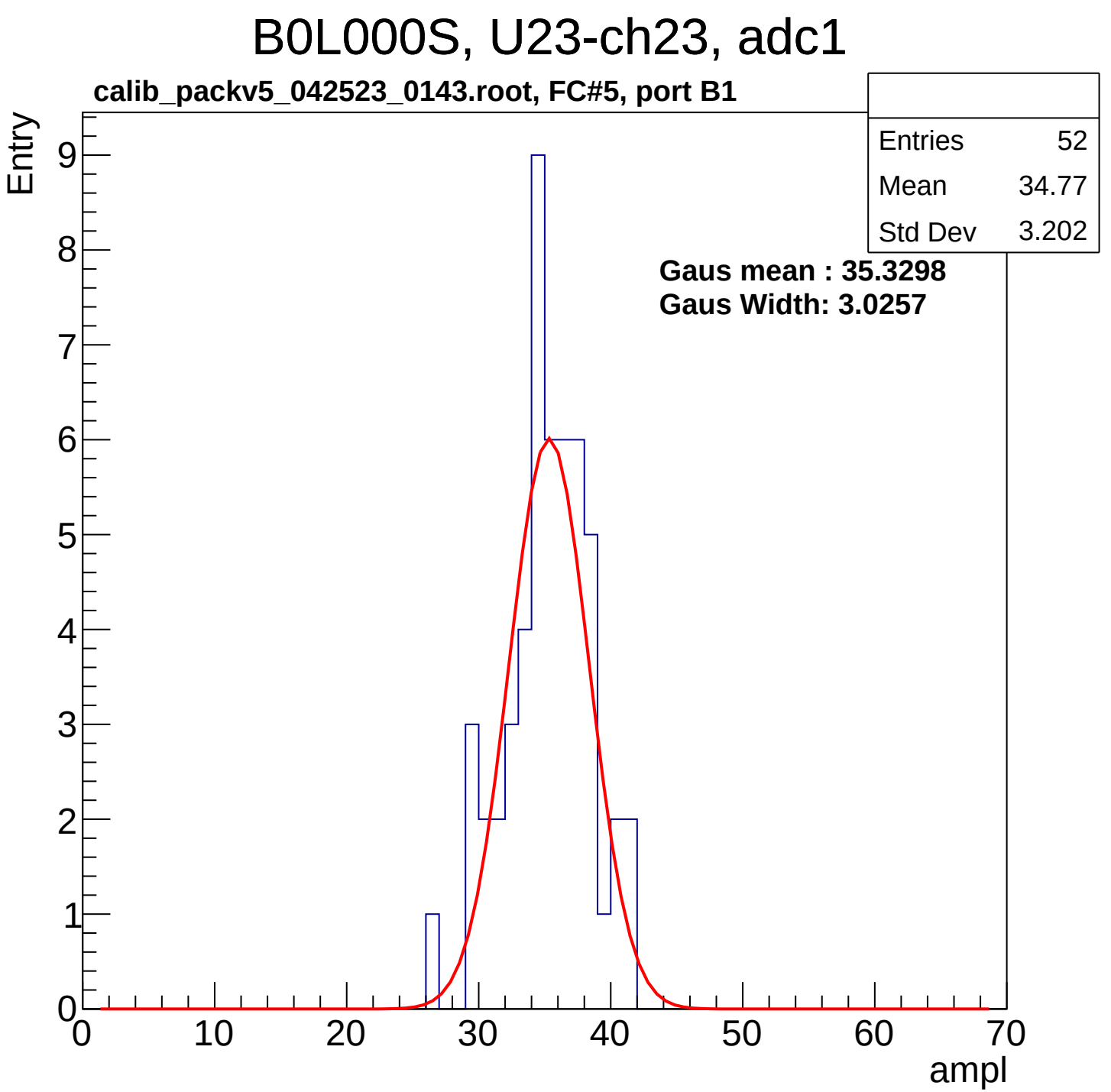

B0L000S, U23-ch22, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 53.38
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

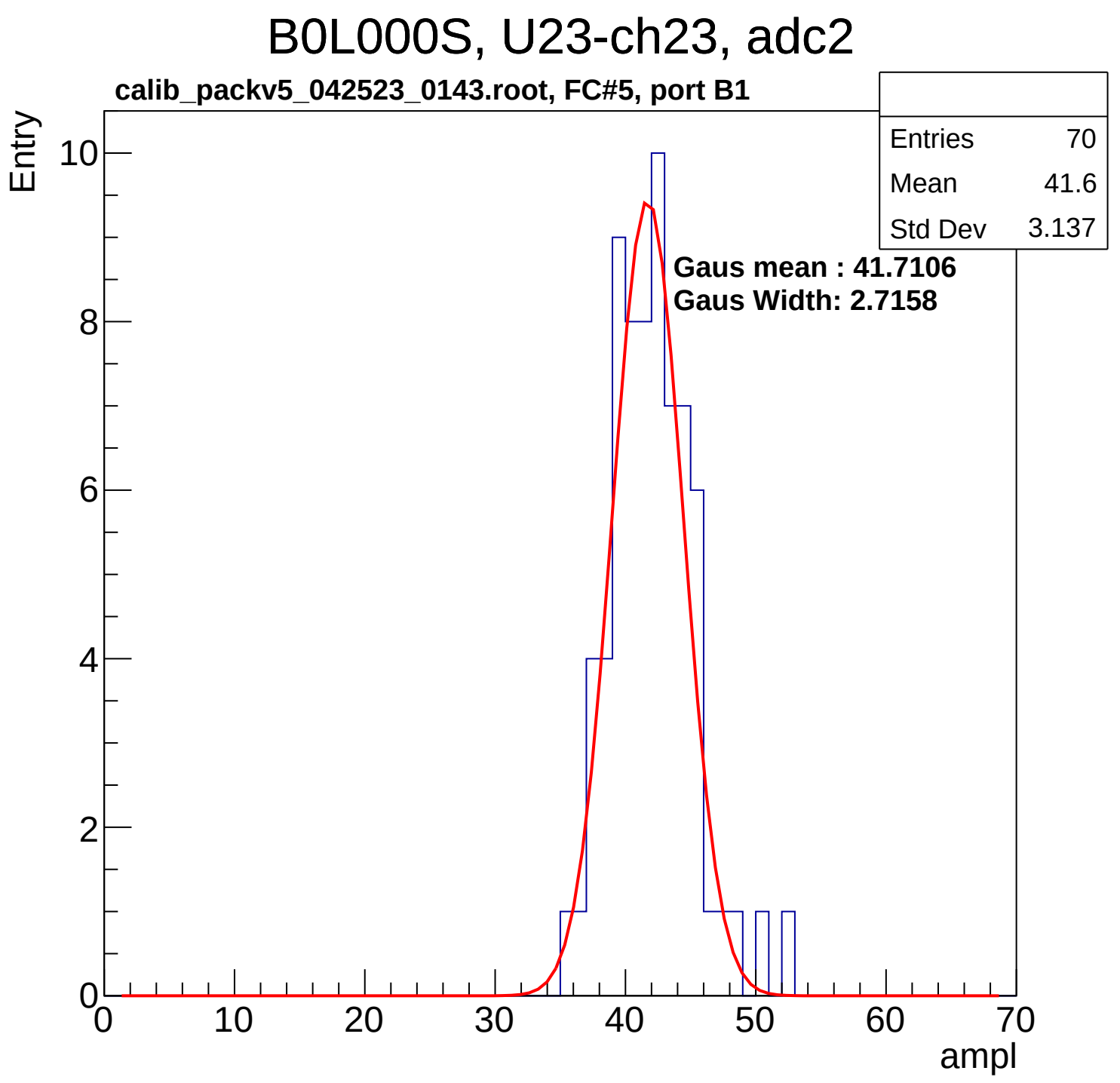

B0L000S, U23-ch22, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 58.23
Std Dev 7.993
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

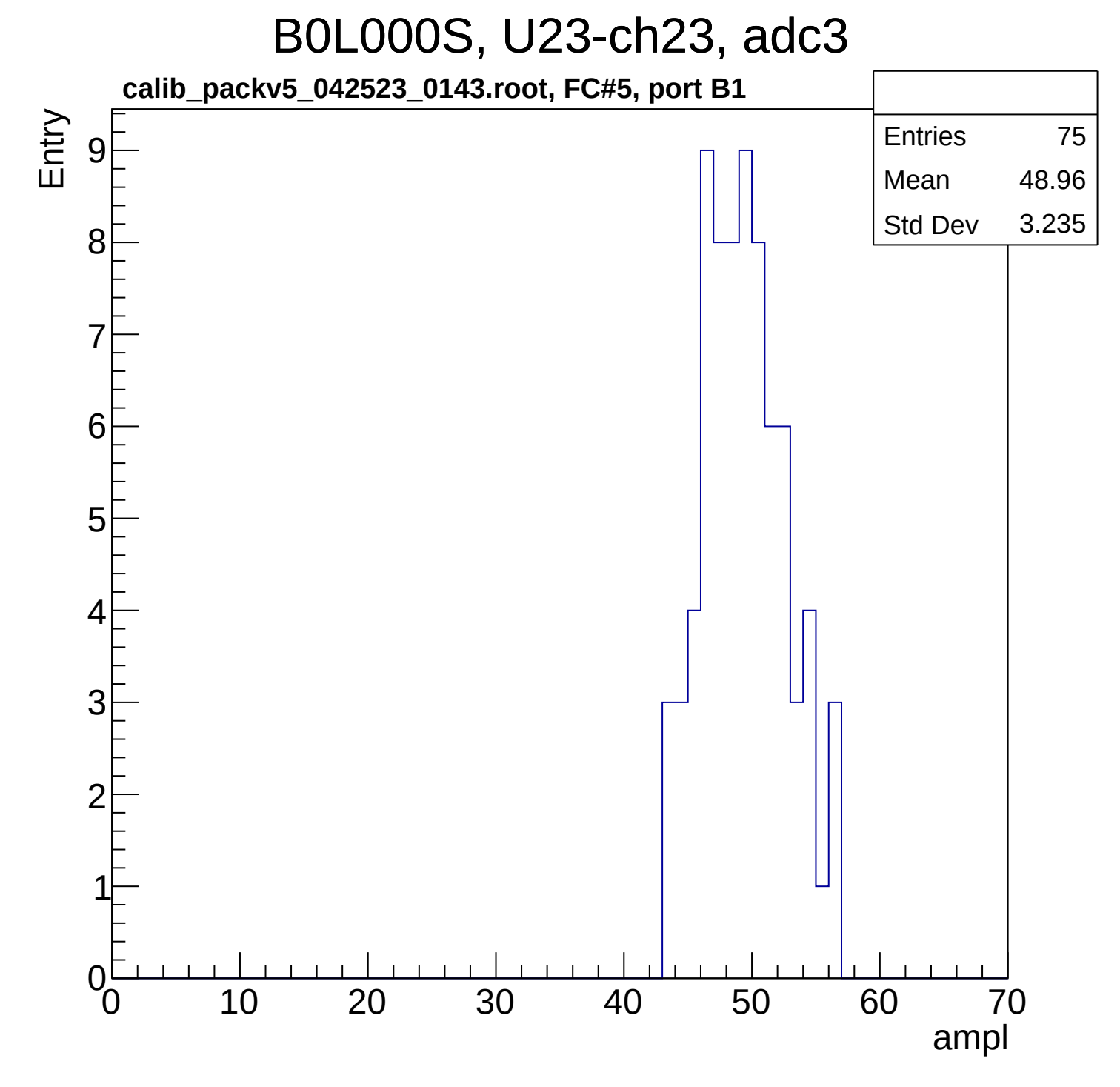

B0L000S, U23-ch22, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.25
Std Dev 1.479
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

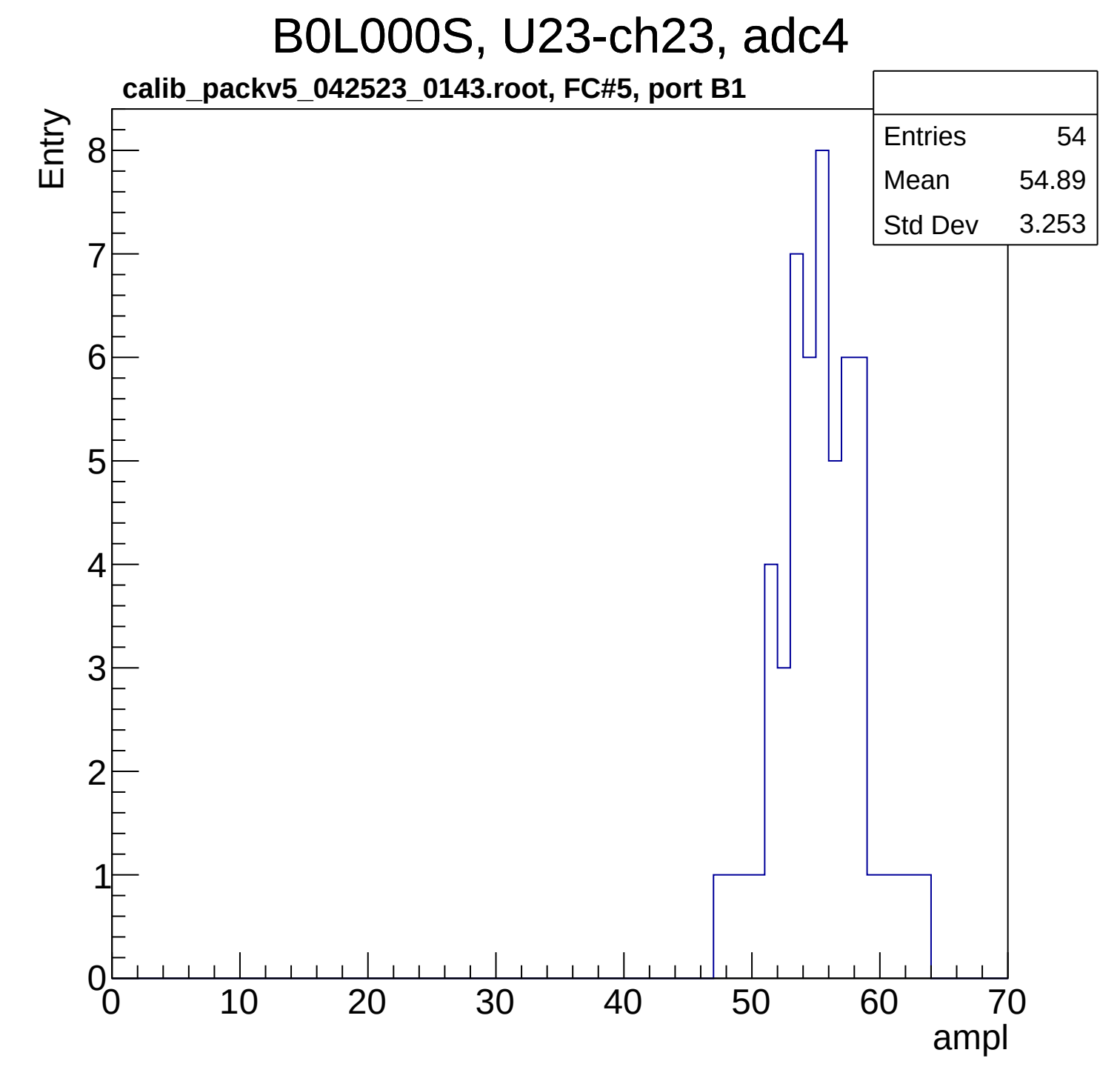

B0L000S, U23-ch22, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 40
Std Dev 21
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

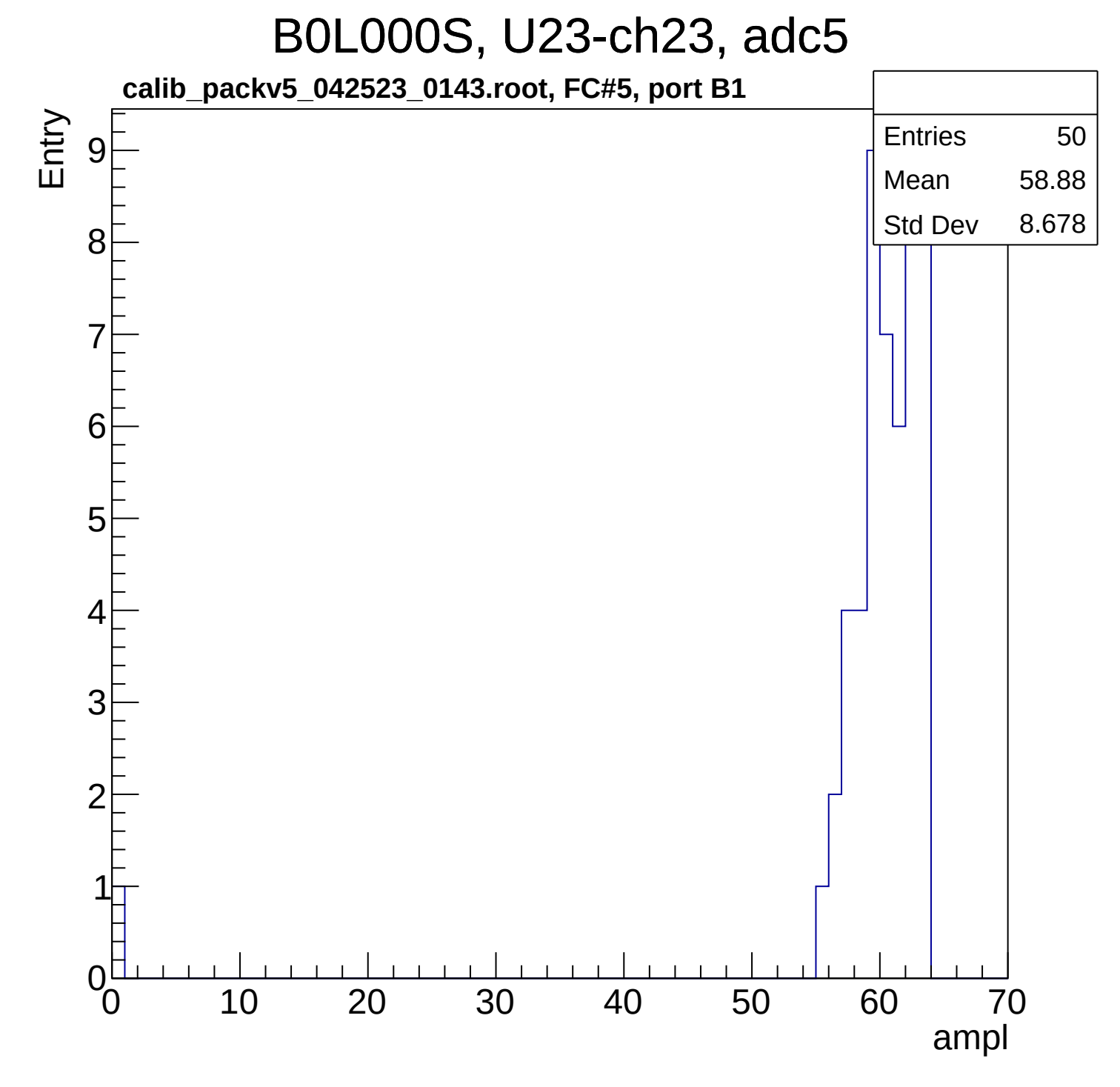

B0L000S, U23-ch23, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 29.18
Std Dev 3.675
Gaus mean : 29.2749
Gaus Width: 3.4806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

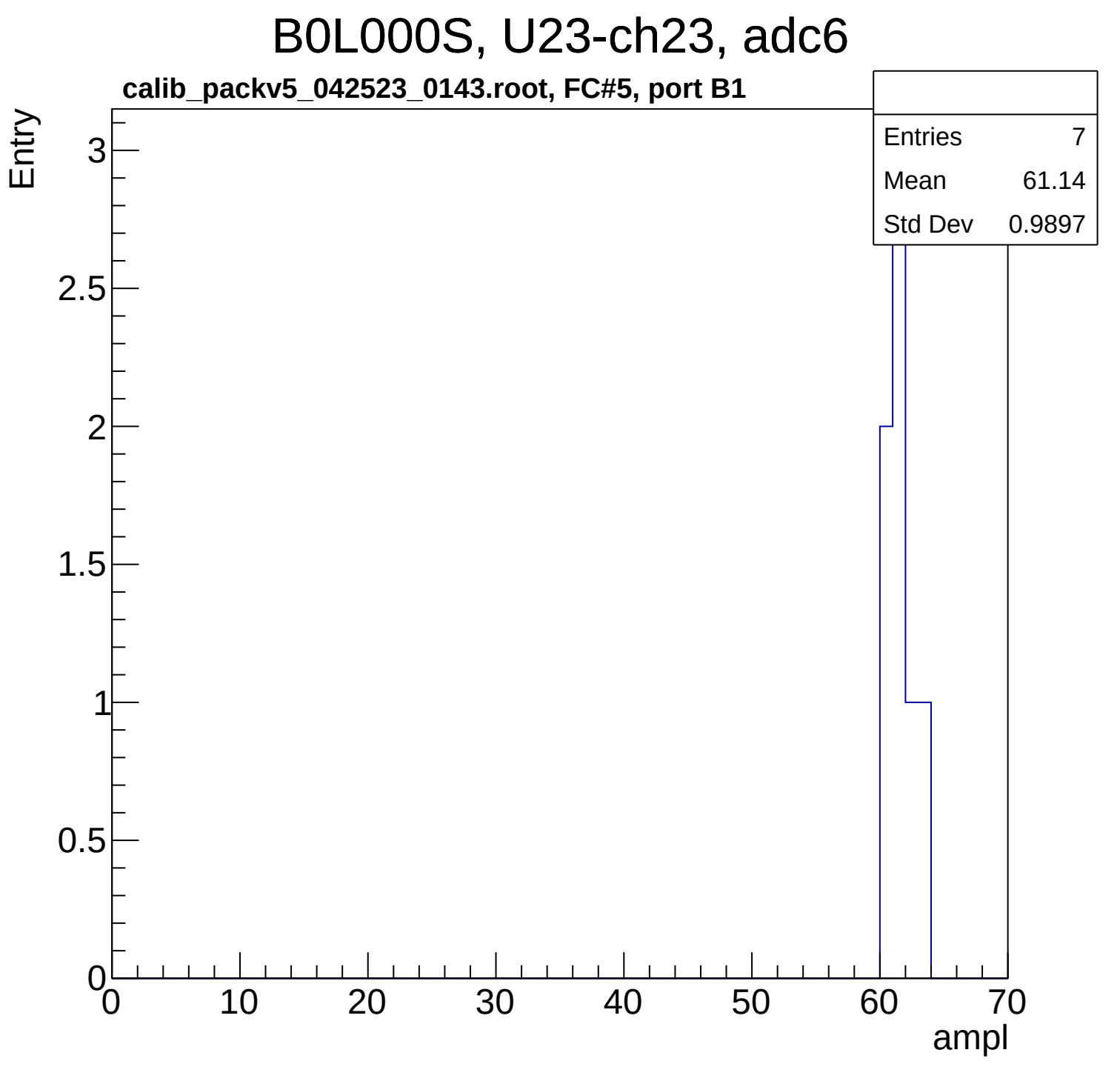

B0L000S, U23-ch23, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 34.77
Std Dev 3.202
Gaus mean : 35.3298
Gaus Width: 3.0257
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

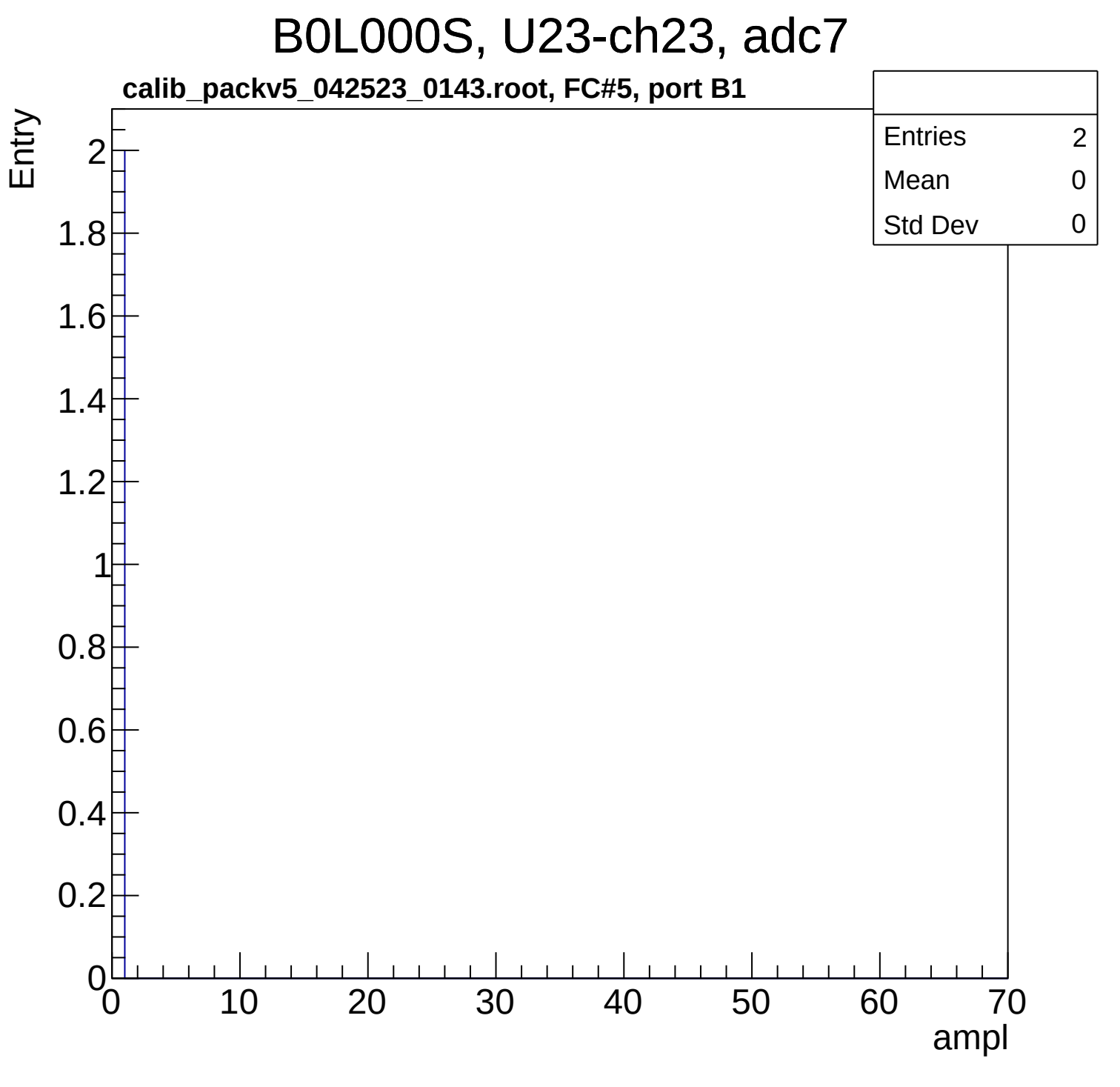

B0L000S, U23-ch23, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 41.6
Std Dev 3.137
Gaus mean : 41.7106
Gaus Width: 2.7158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

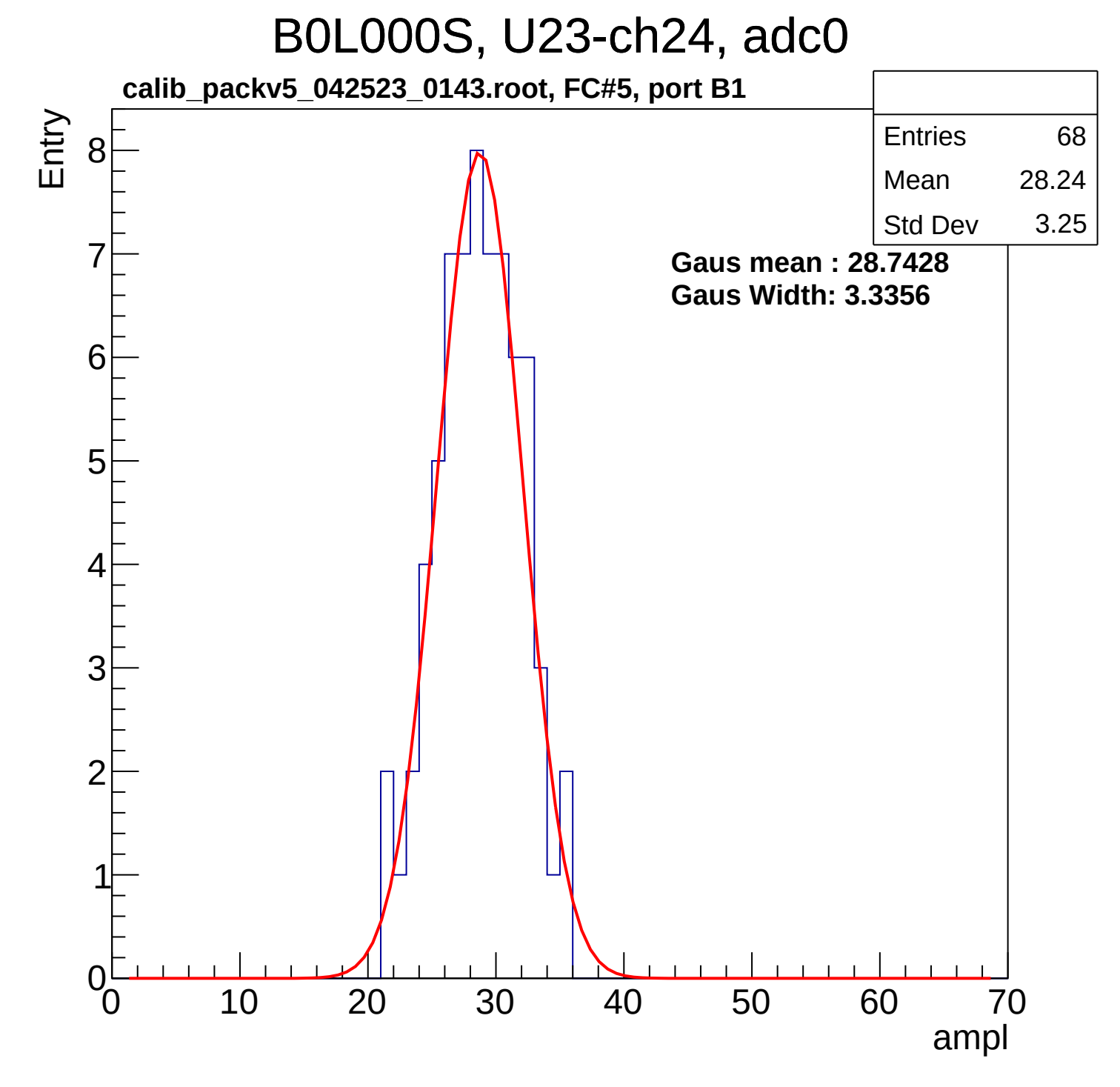

B0L000S, U23-ch23, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.96
Std Dev 3.235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

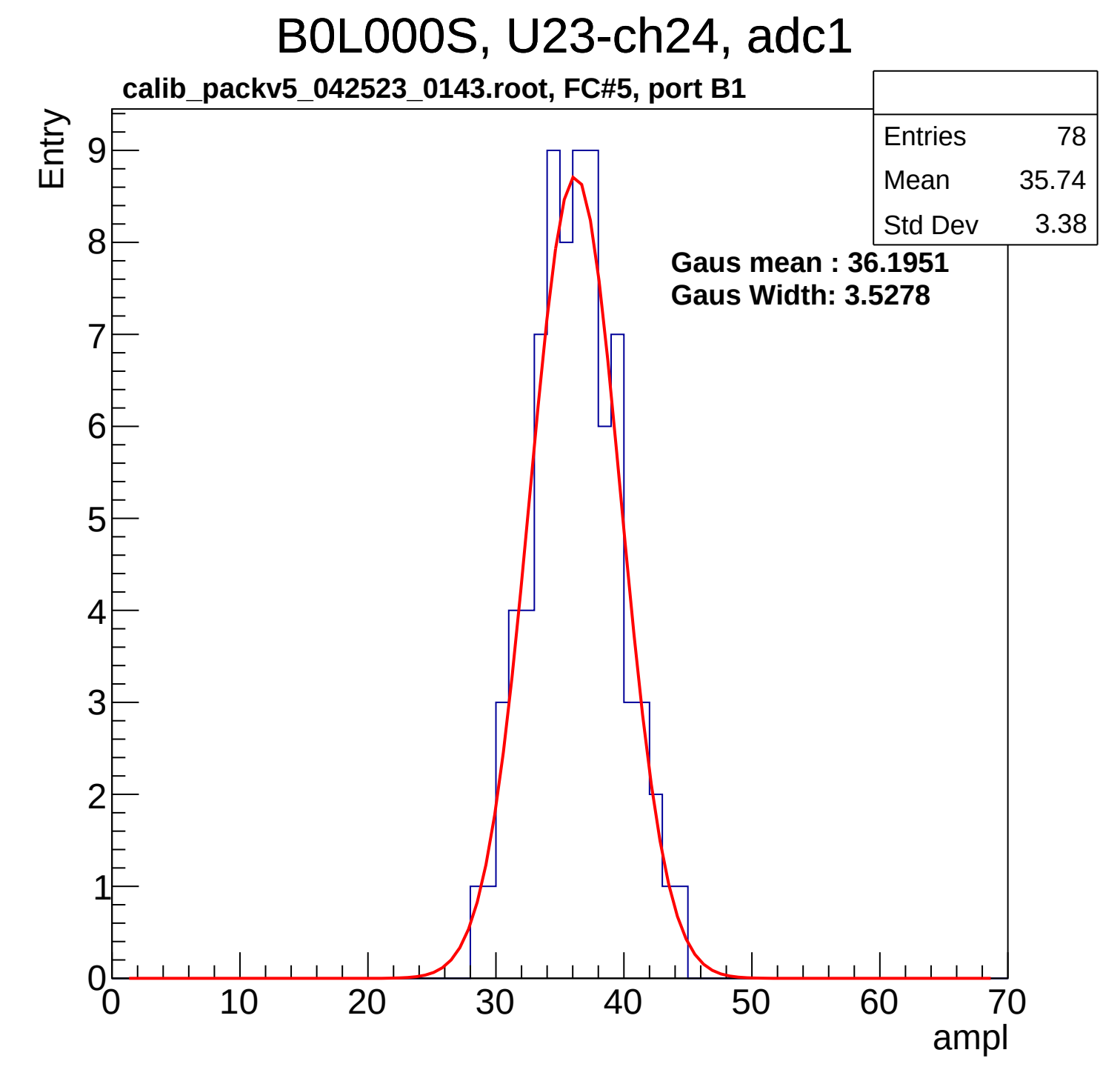

B0L000S, U23-ch23, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.89
Std Dev 3.253
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

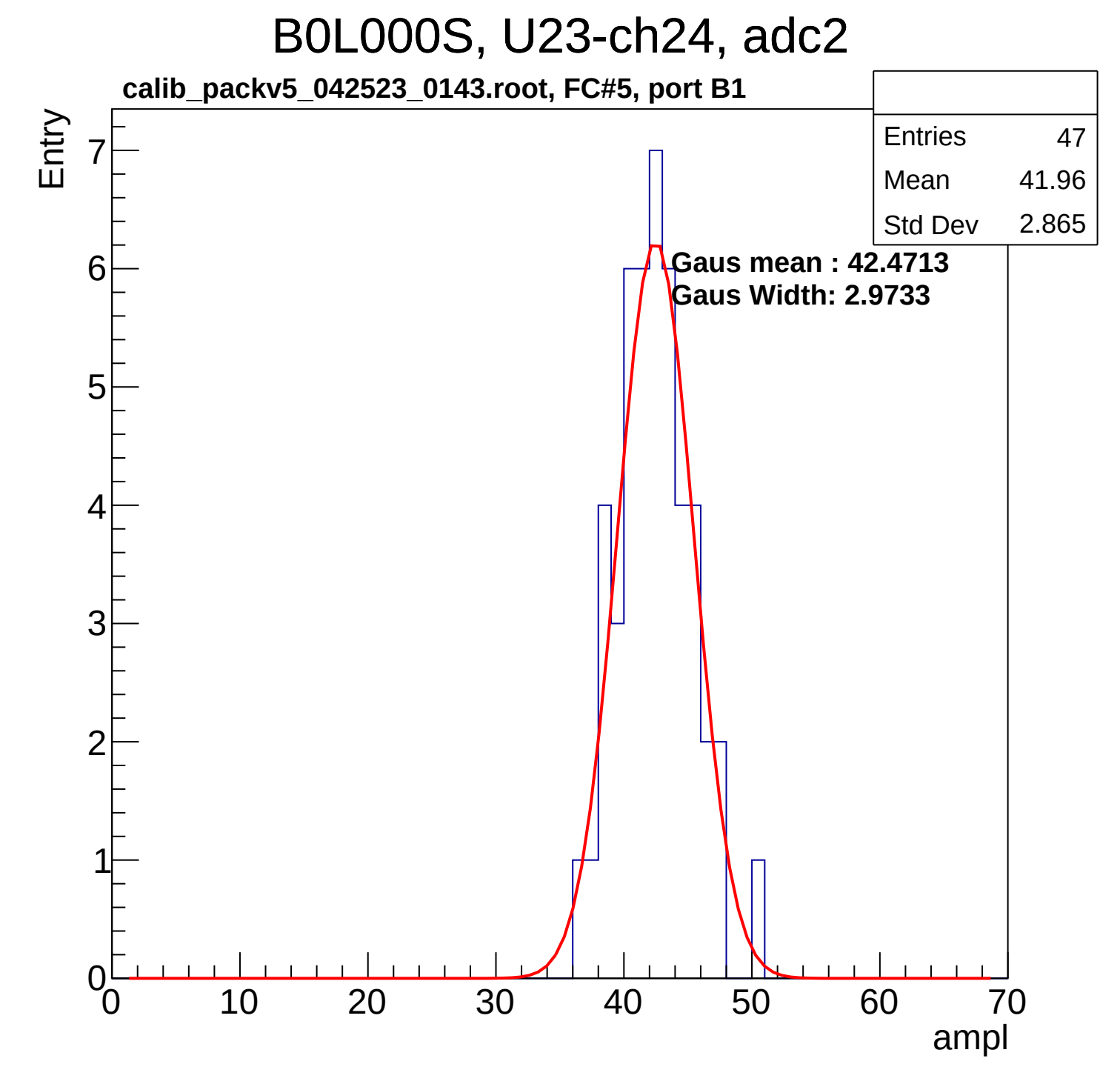

B0L000S, U23-ch23, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.88
Std Dev 8.678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

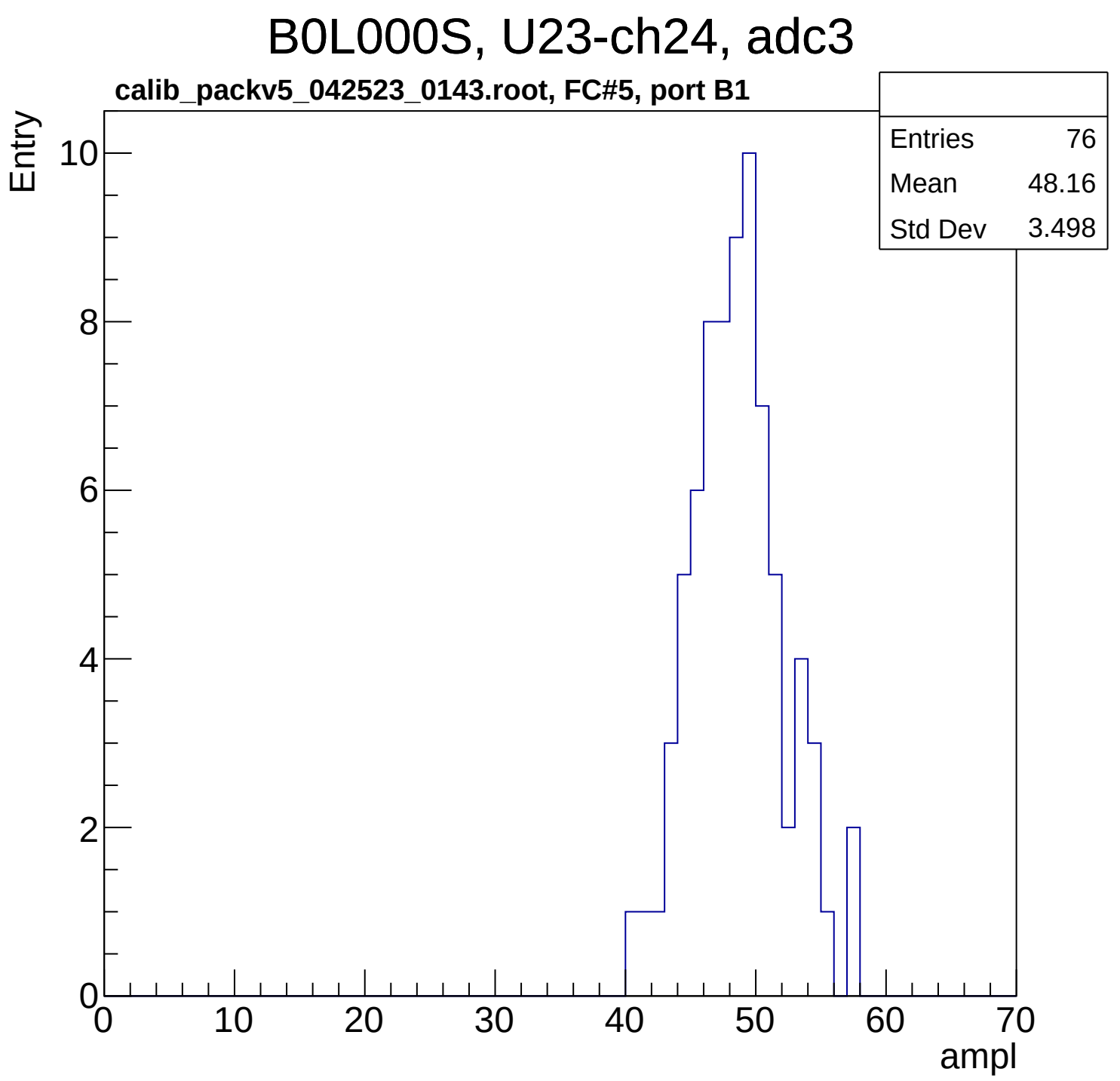

B0L000S, U23-ch23, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.14
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

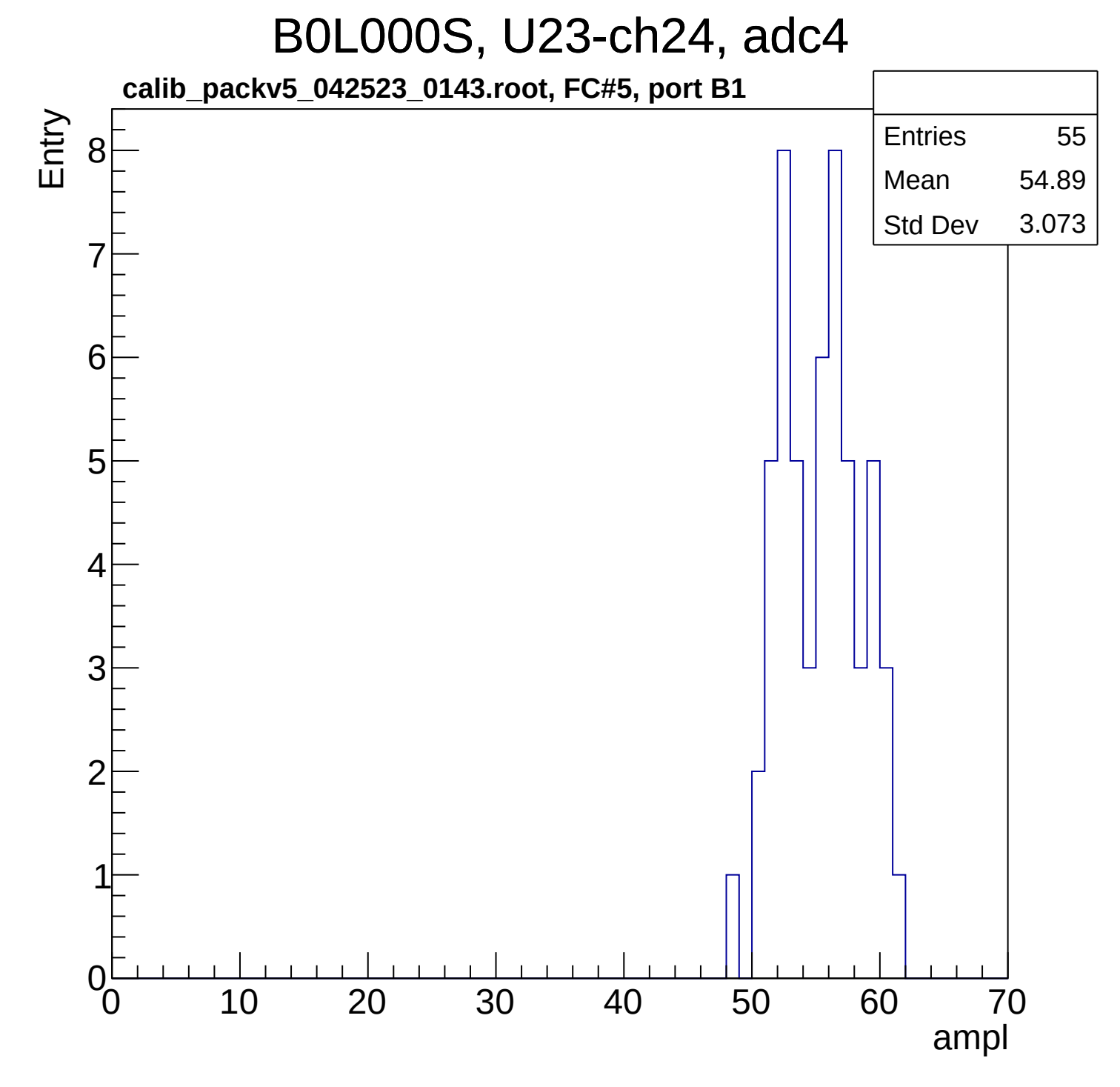

B0L000S, U23-ch23, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

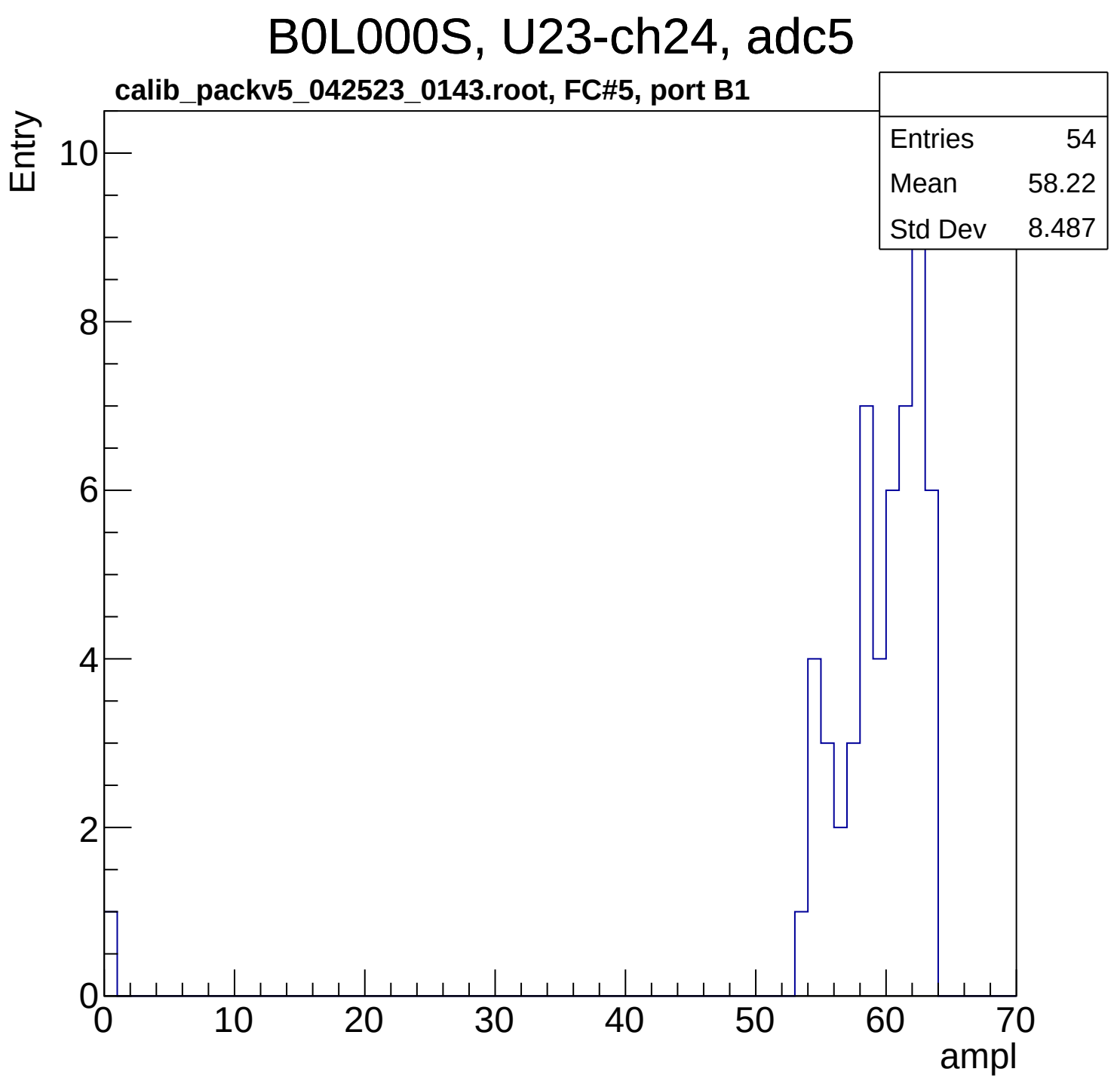

B0L000S, U23-ch24, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.24
Std Dev 3.25
Gaus mean : 28.7428
Gaus Width: 3.3356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

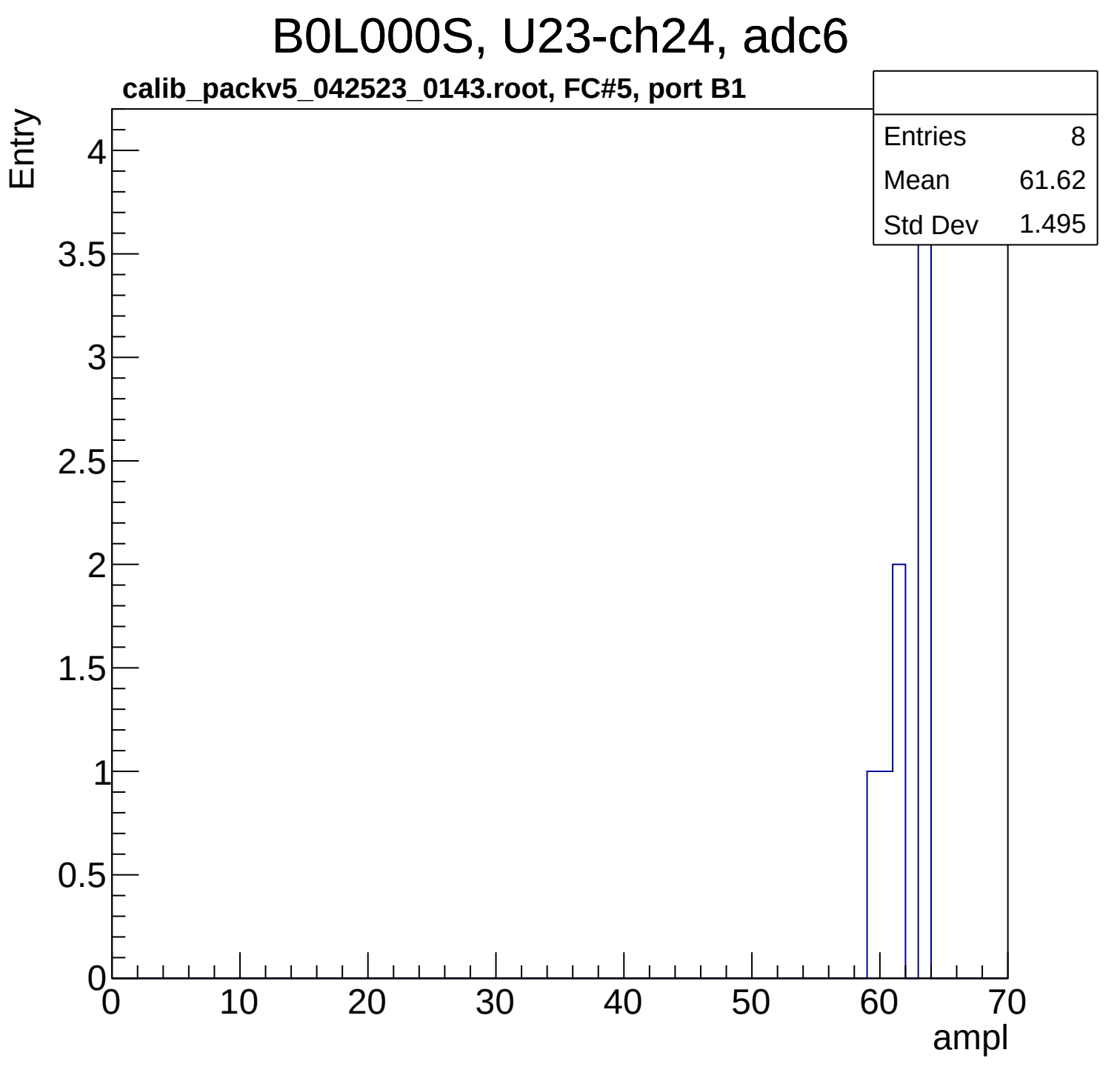

B0L000S, U23-ch24, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 35.74
Std Dev 3.38
Gaus mean : 36.1951
Gaus Width: 3.5278
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

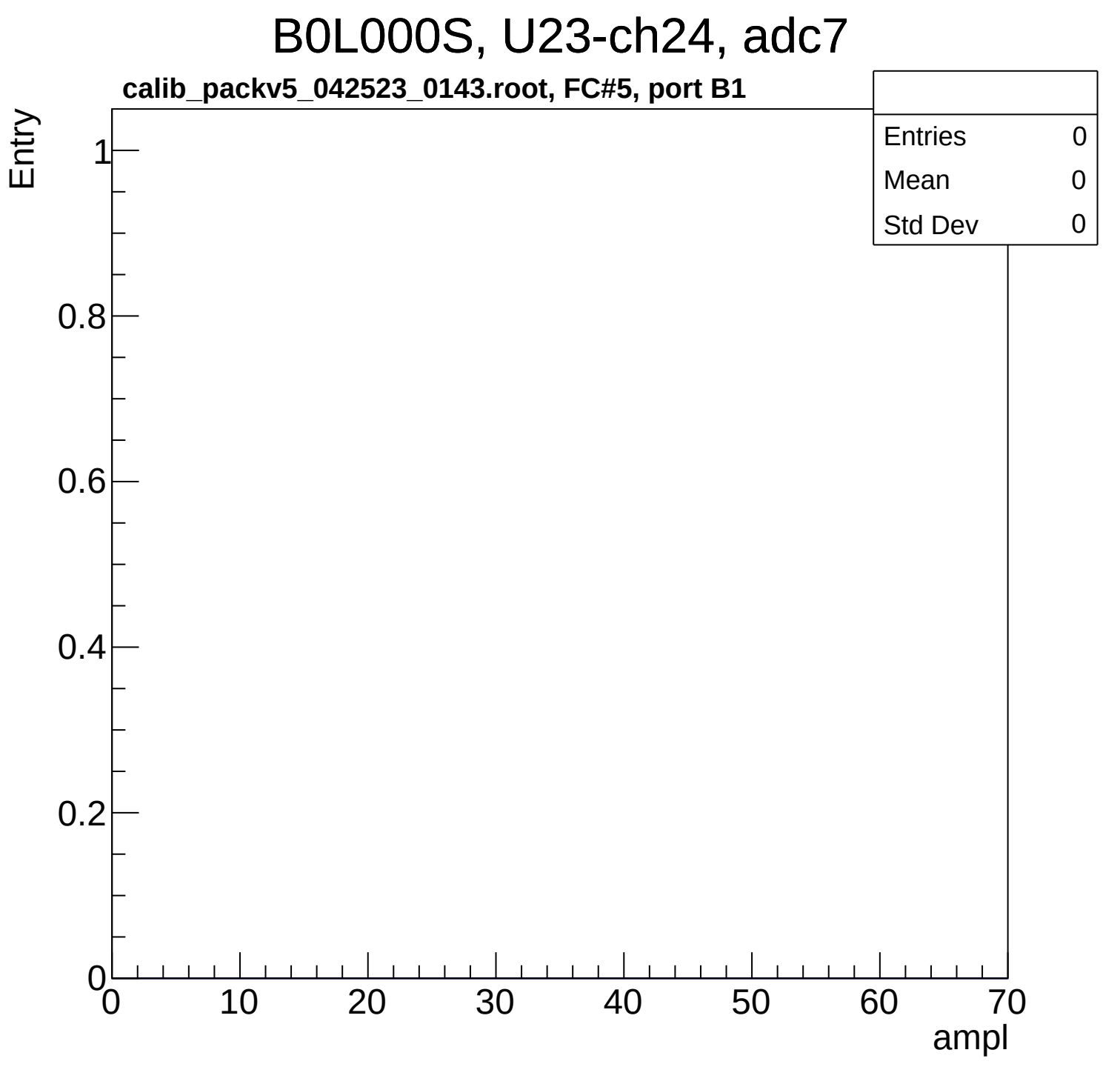

B0L000S, U23-ch24, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 41.96
Std Dev 2.865
Gaus mean : 42.4713
Gaus Width: 2.9733
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

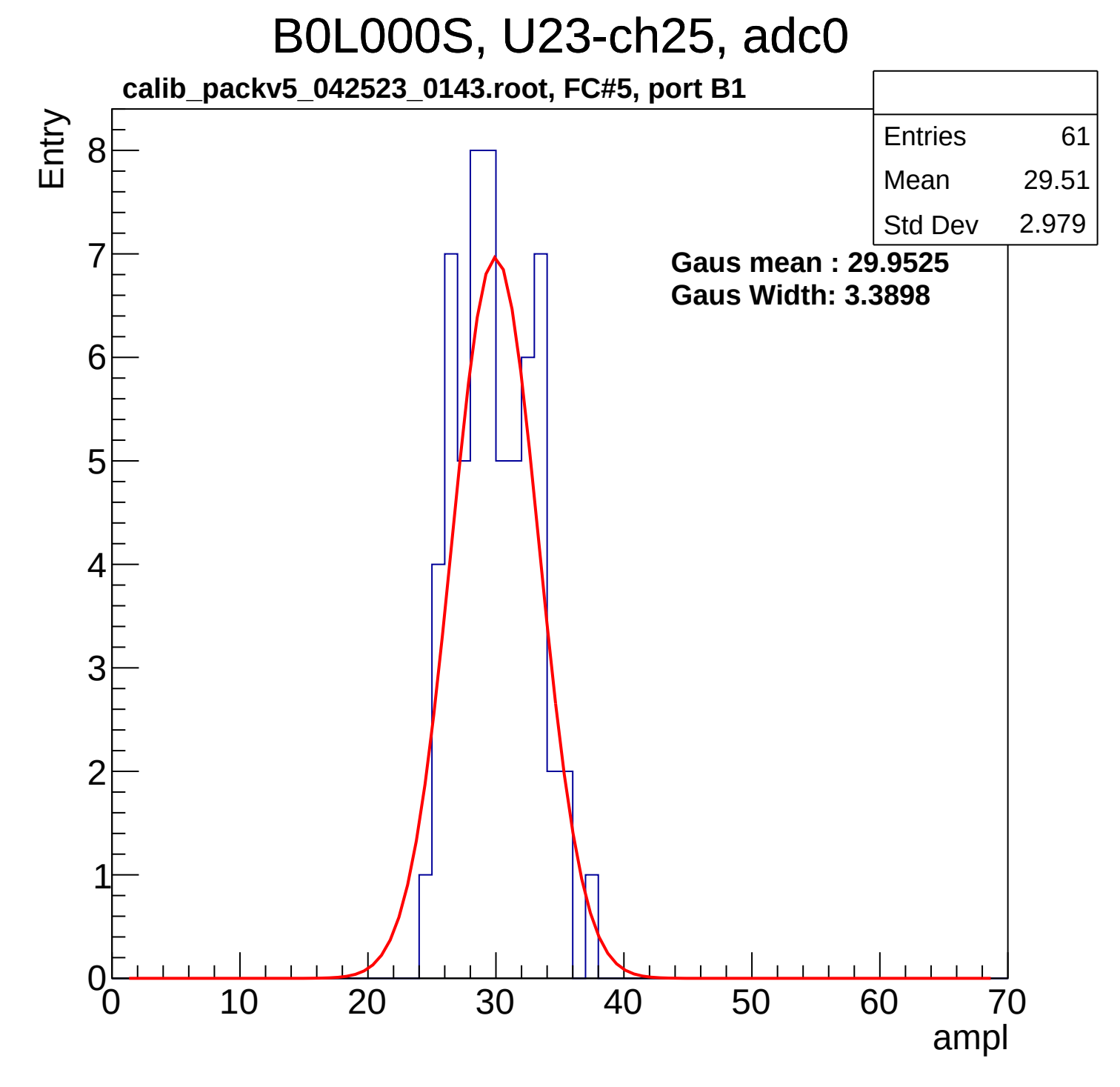

B0L000S, U23-ch24, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 48.16
Std Dev 3.498
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

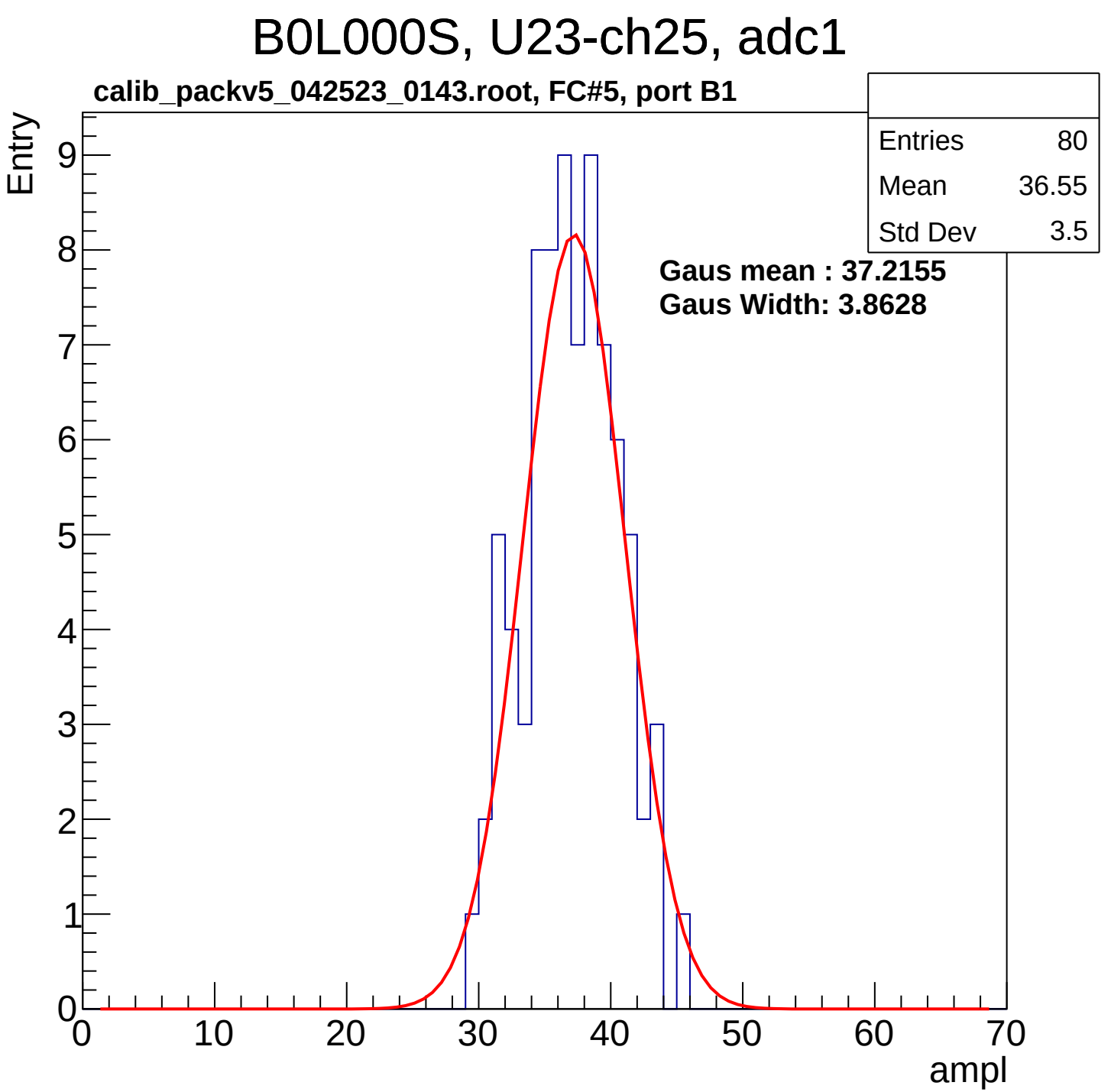

B0L000S, U23-ch24, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 54.89
Std Dev 3.073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

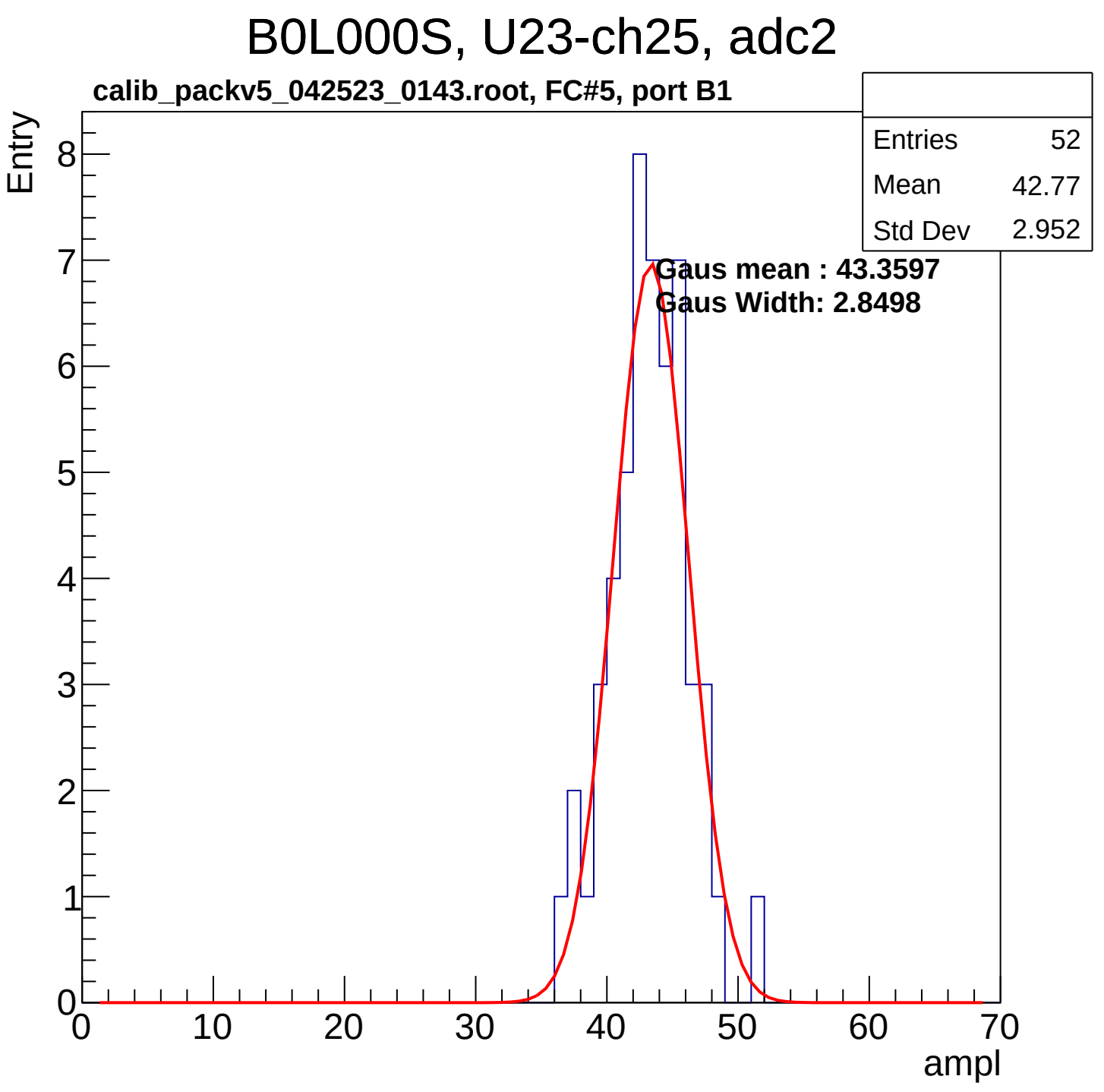

B0L000S, U23-ch24, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.22
Std Dev 8.487
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

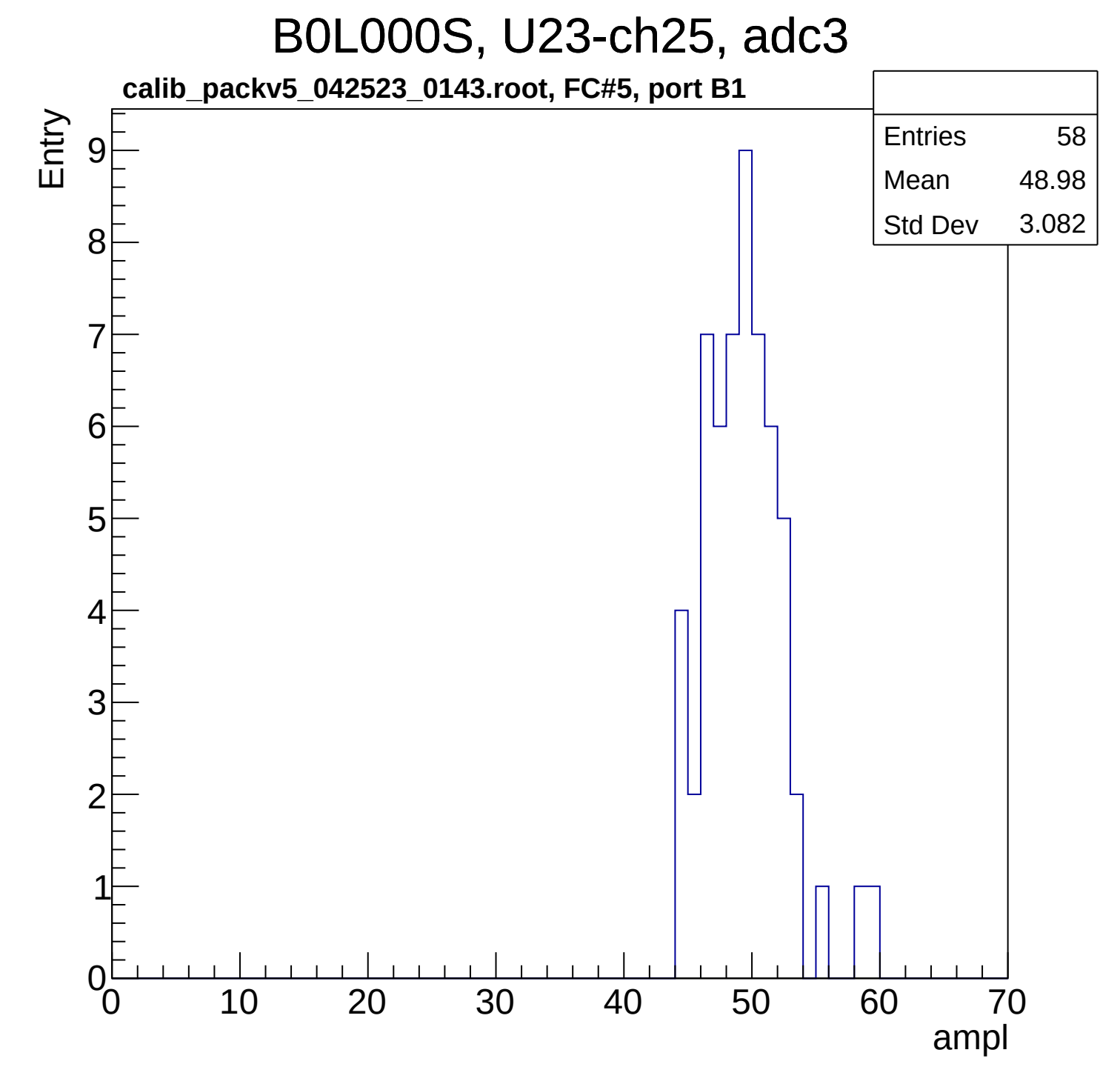

B0L000S, U23-ch24, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.62
Std Dev 1.495
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

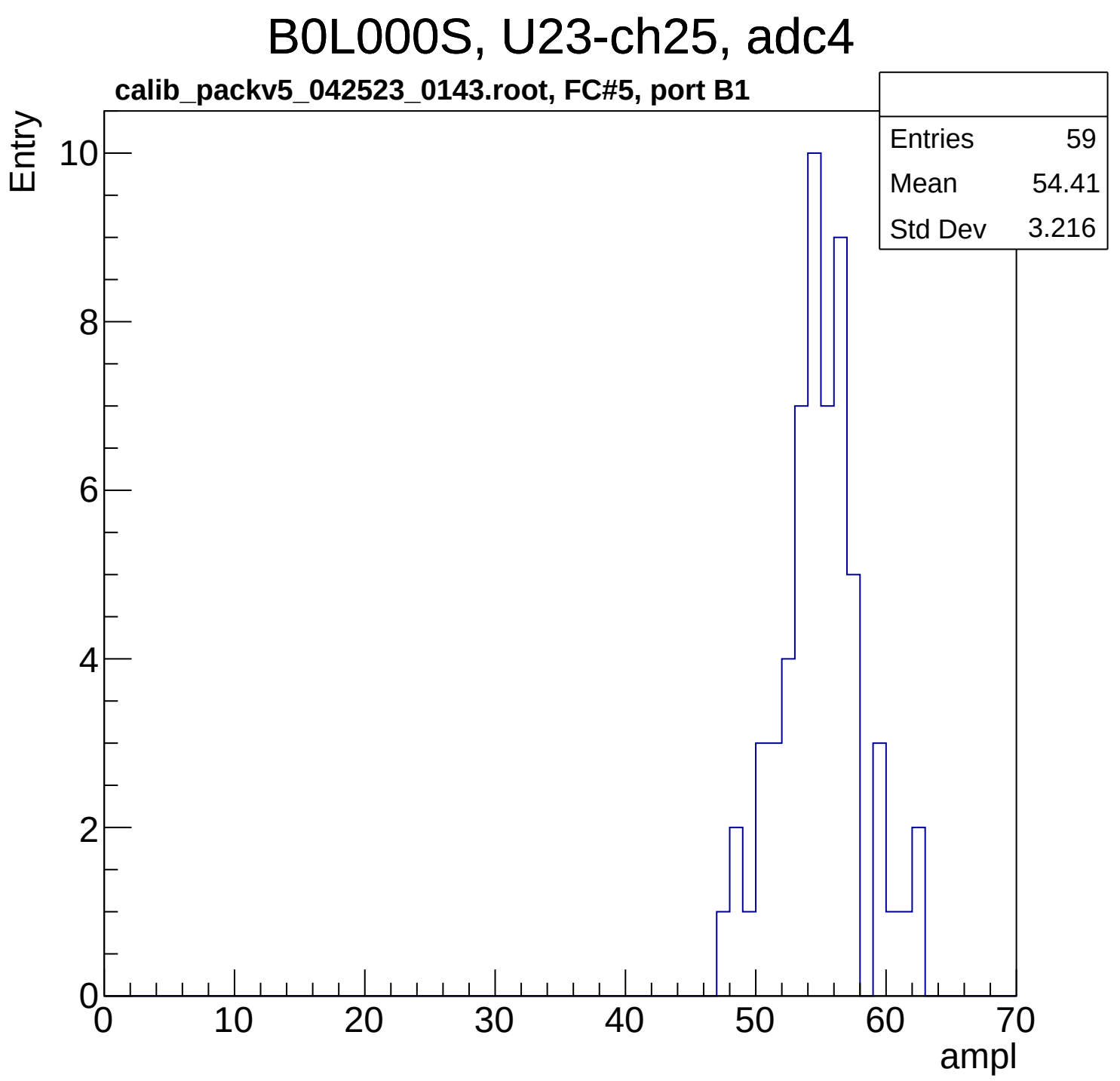

B0L000S, U23-ch24, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

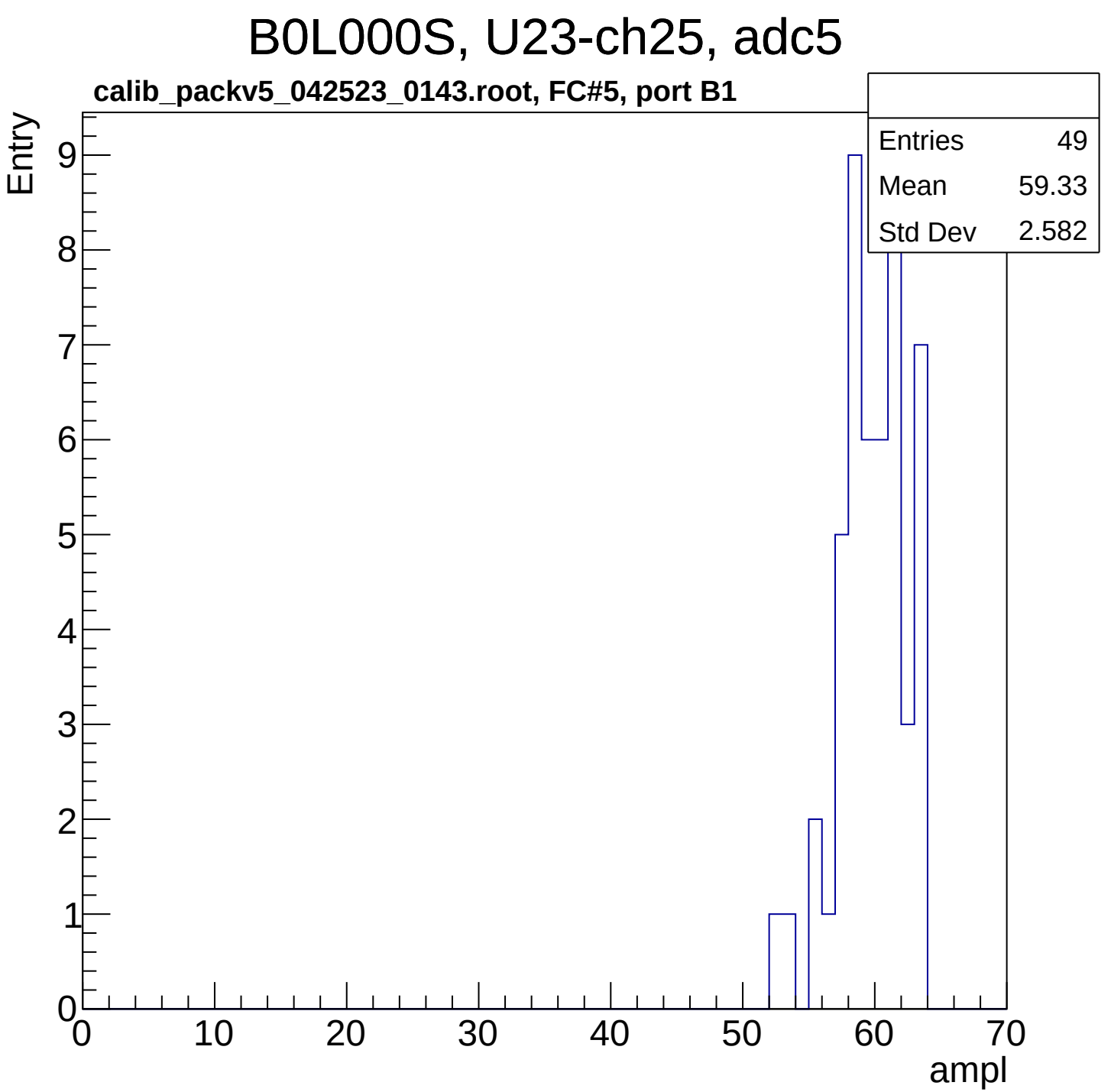

B0L000S, U23-ch25, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.51
Std Dev 2.979
Gaus mean : 29.9525
Gaus Width: 3.3898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

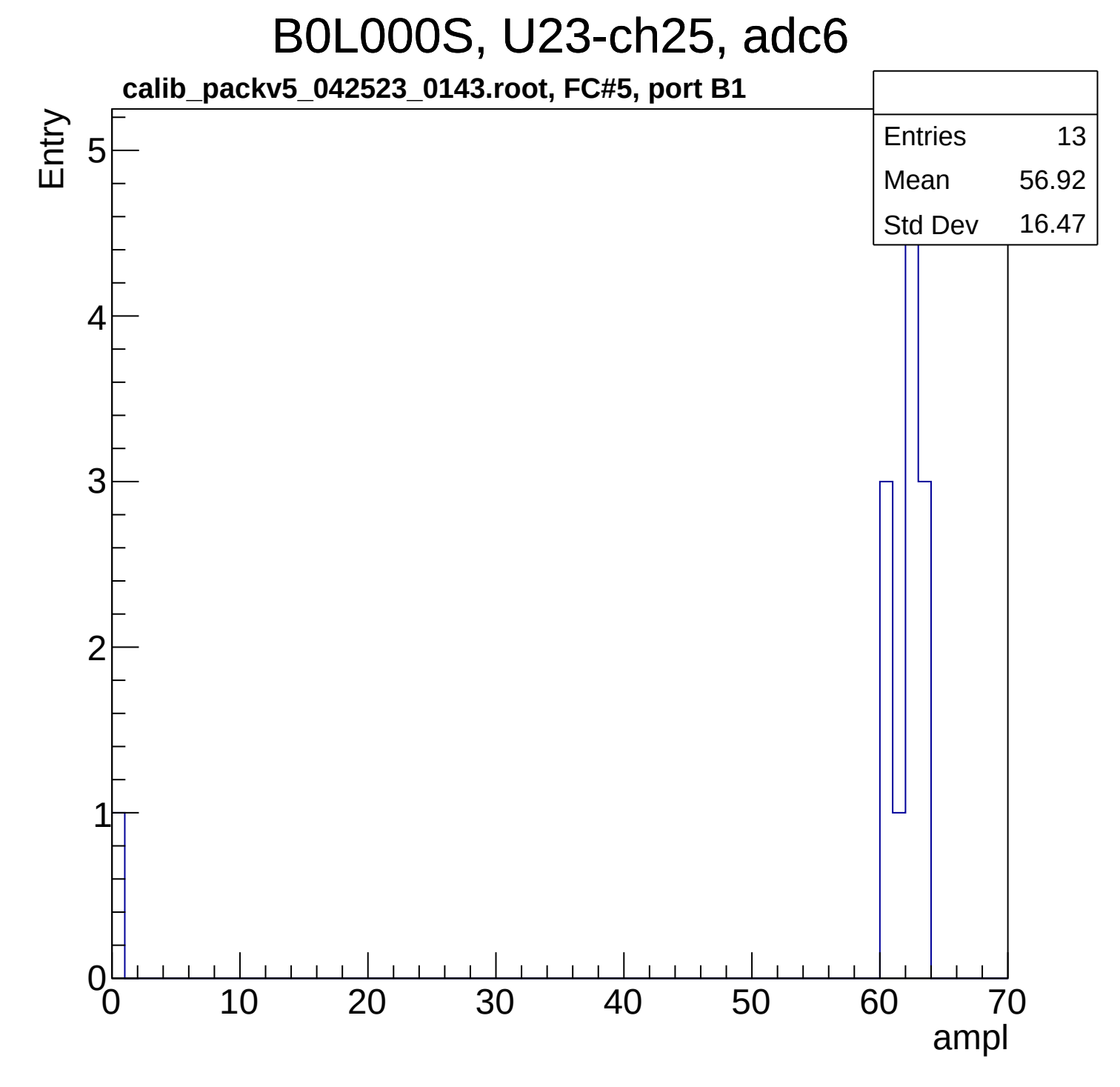

B0L000S, U23-ch25, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 36.55
Std Dev 3.5
Gaus mean : 37.2155
Gaus Width: 3.8628
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

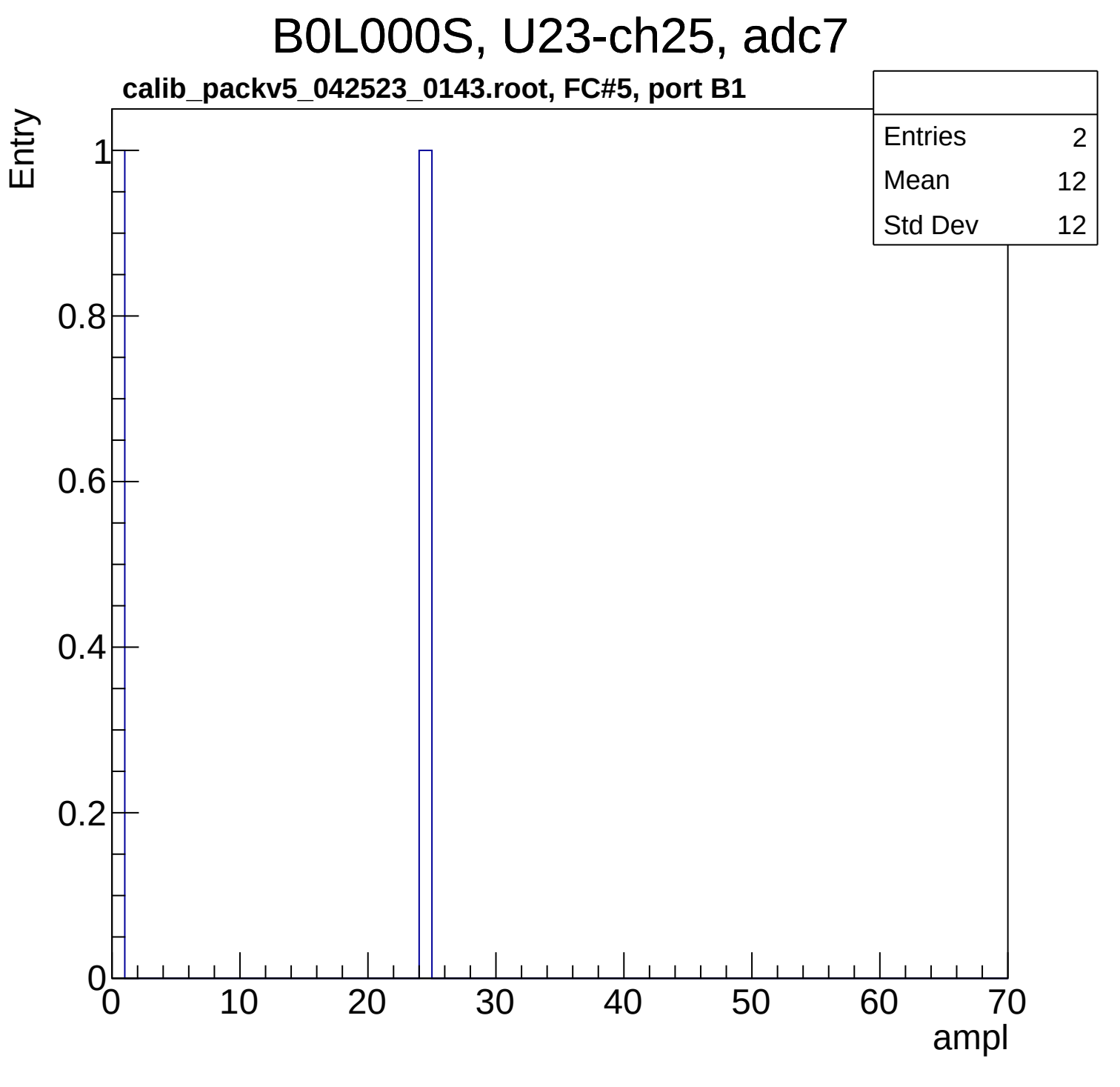

B0L000S, U23-ch25, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.77
Std Dev 2.952
Gaus mean : 43.3597
Gaus Width: 2.8498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

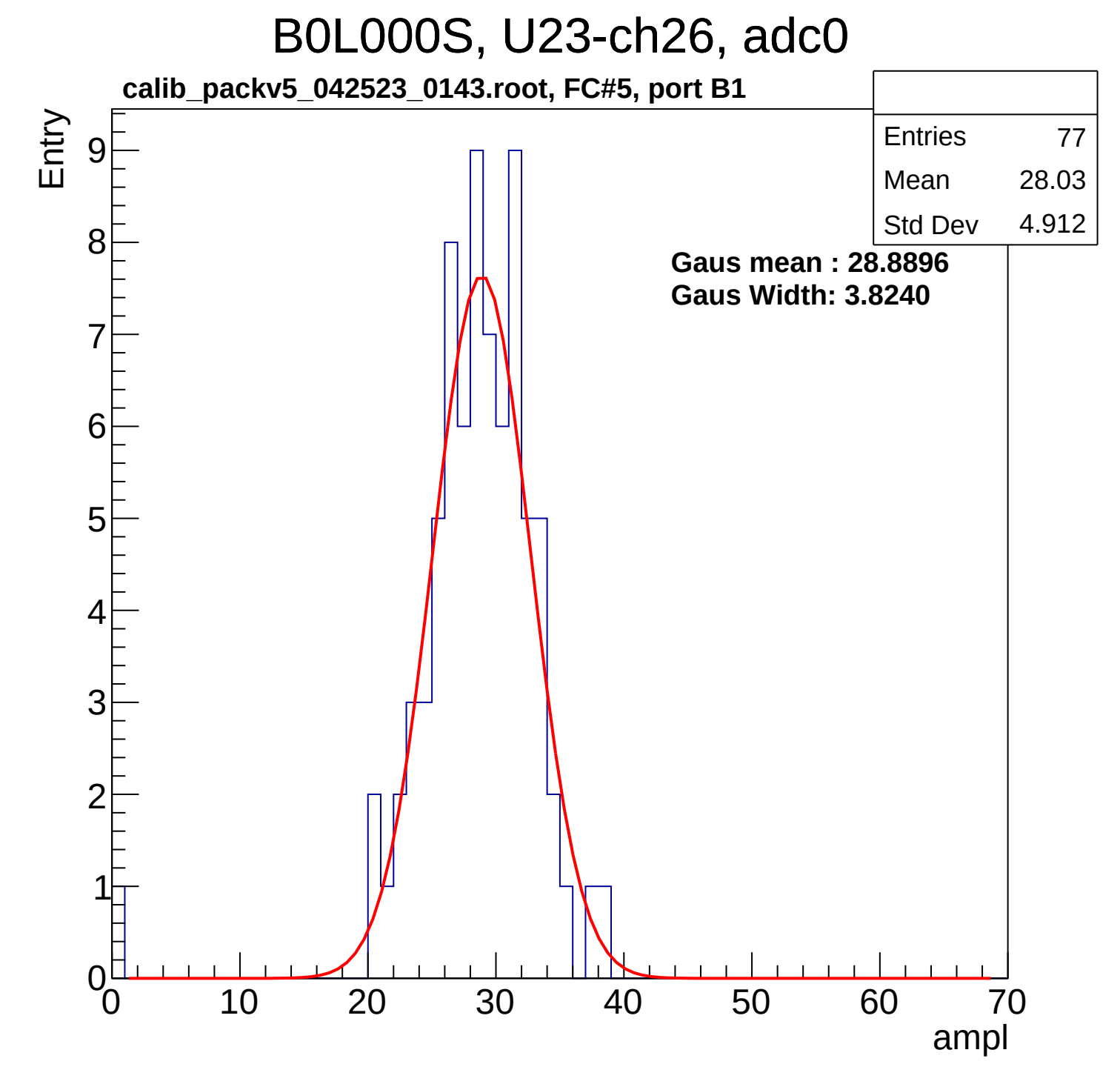

B0L000S, U23-ch25, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.98
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

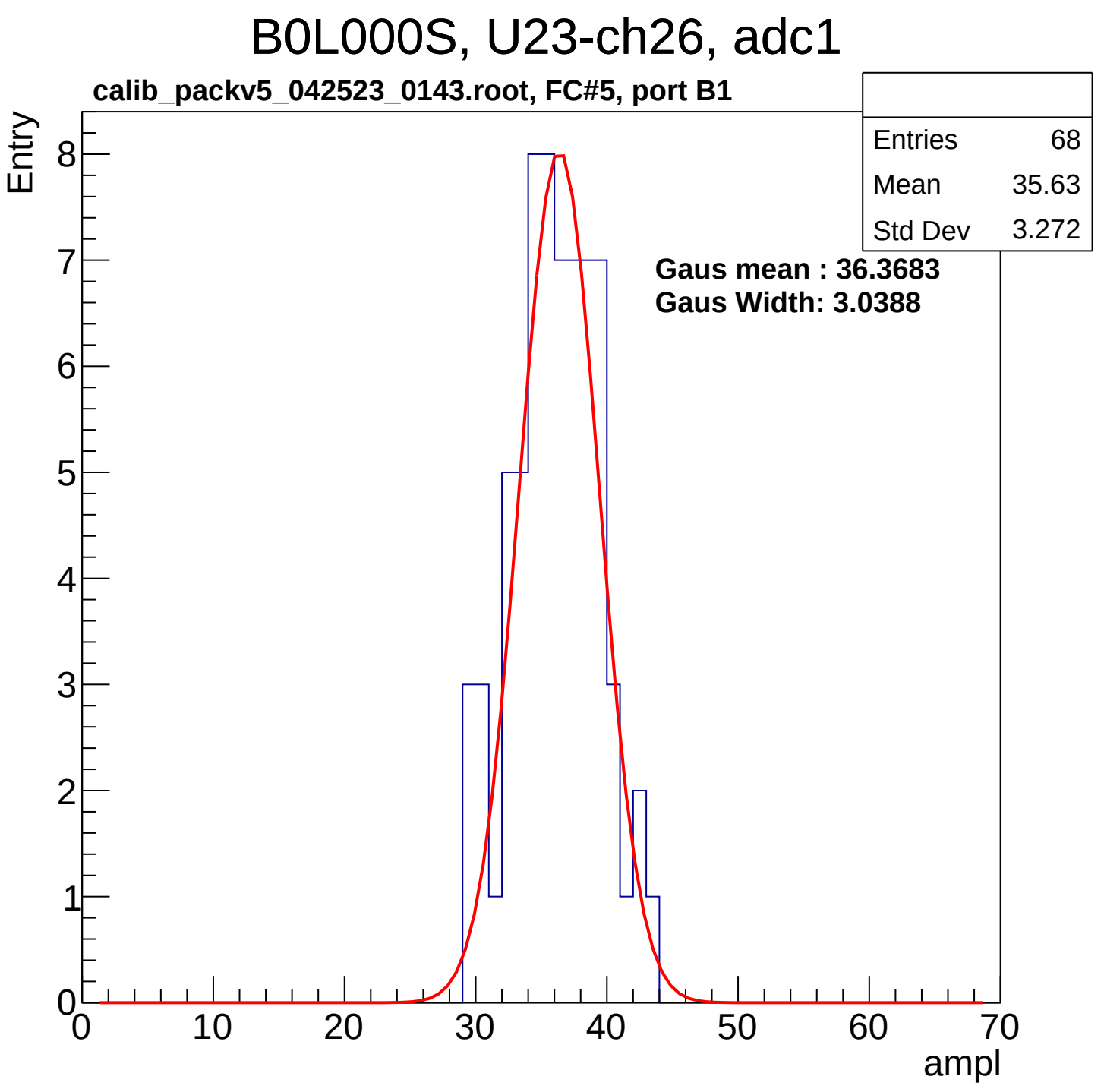

B0L000S, U23-ch25, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.41
Std Dev 3.216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

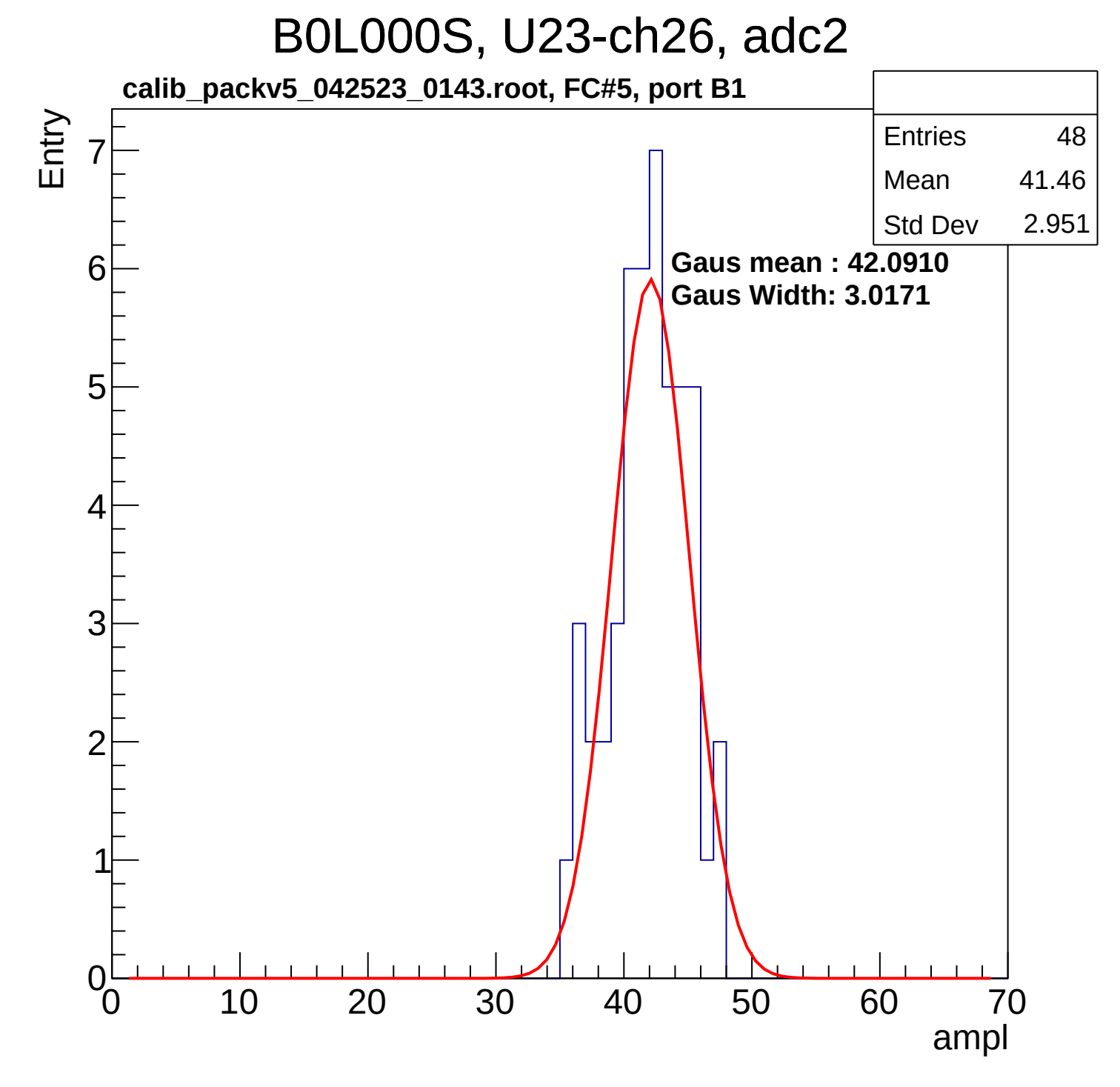

B0L000S, U23-ch25, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 59.33
Std Dev 2.582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

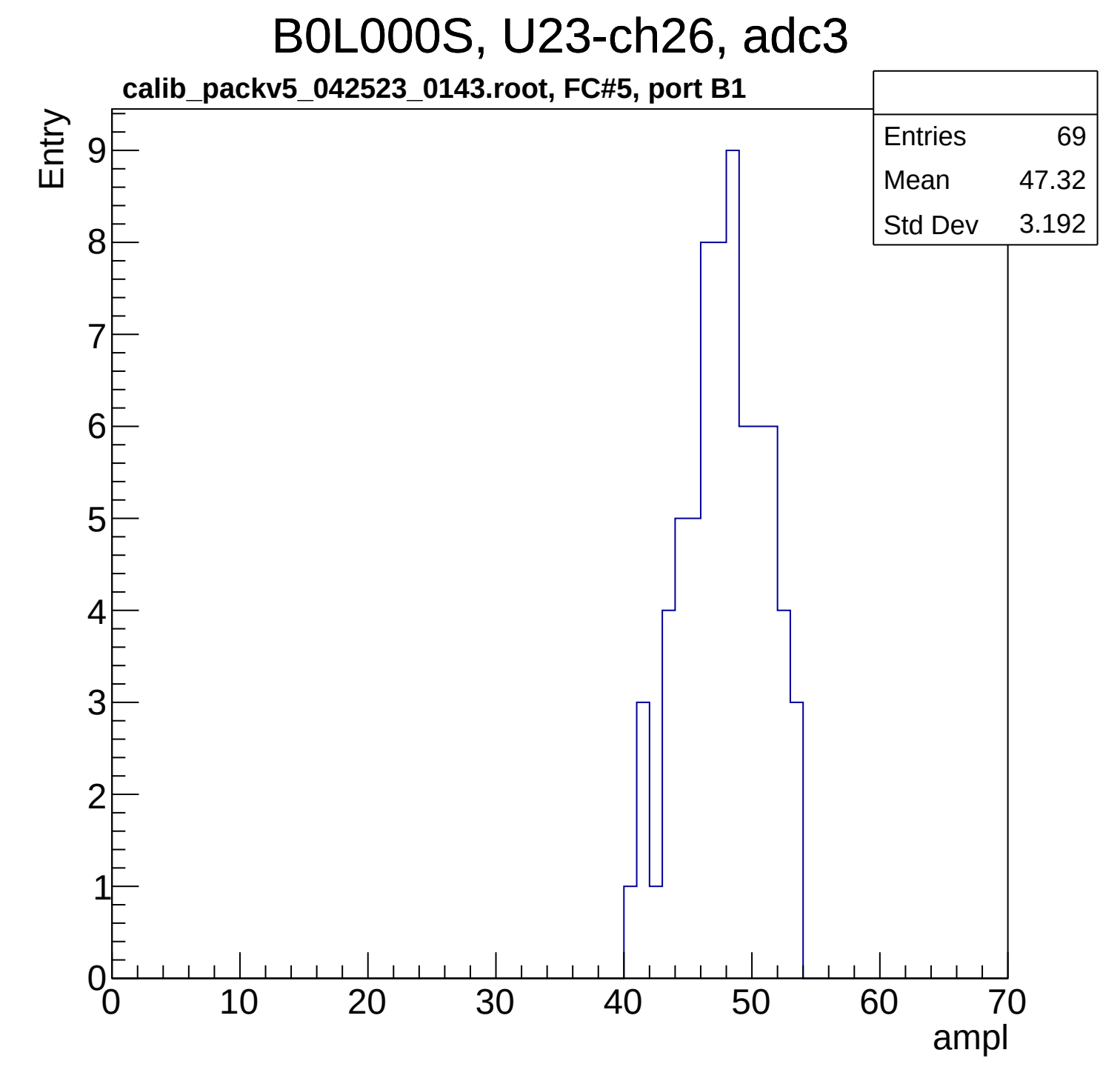

B0L000S, U23-ch25, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.92
Std Dev 16.47
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

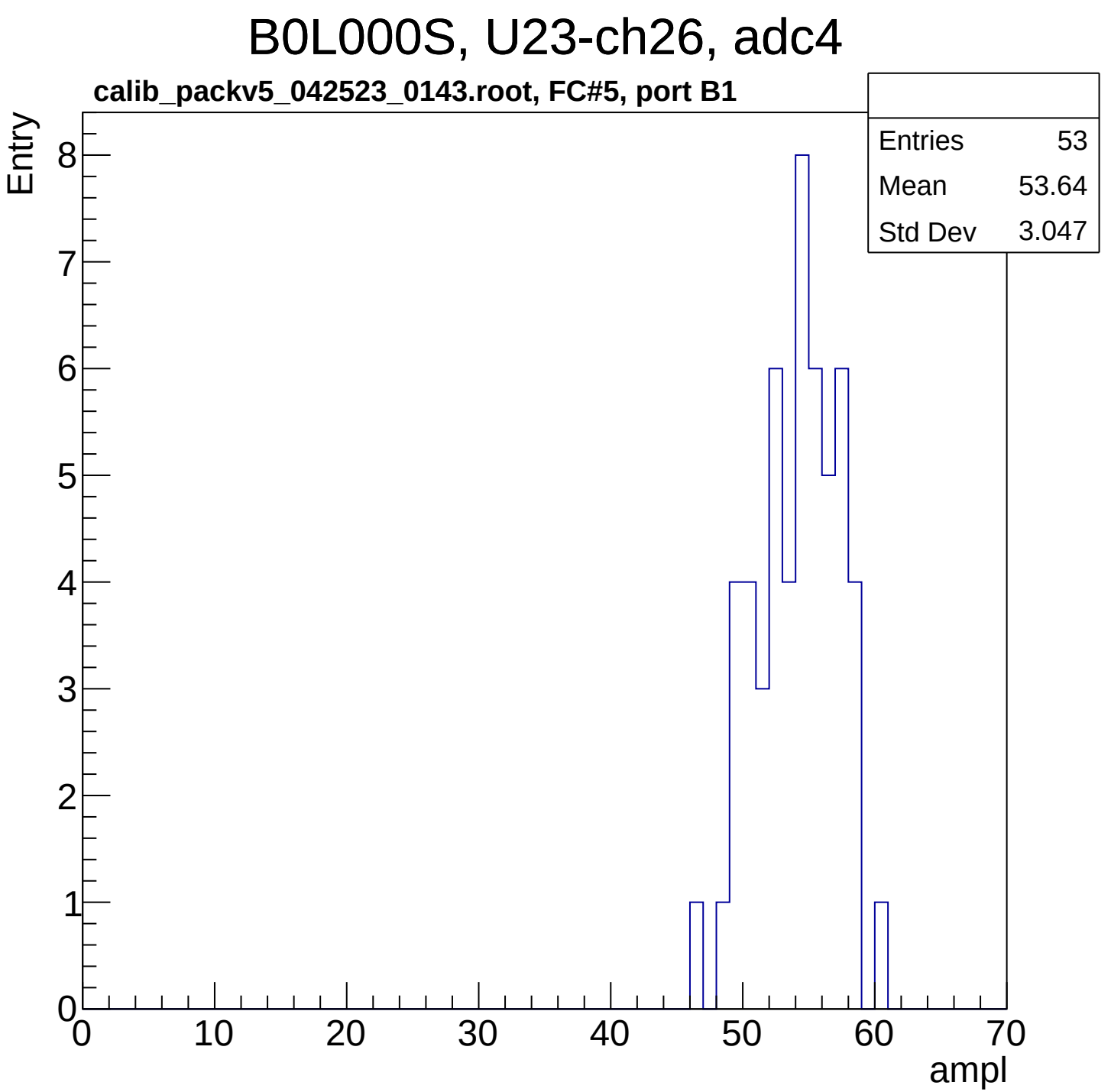

B0L000S, U23-ch25, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

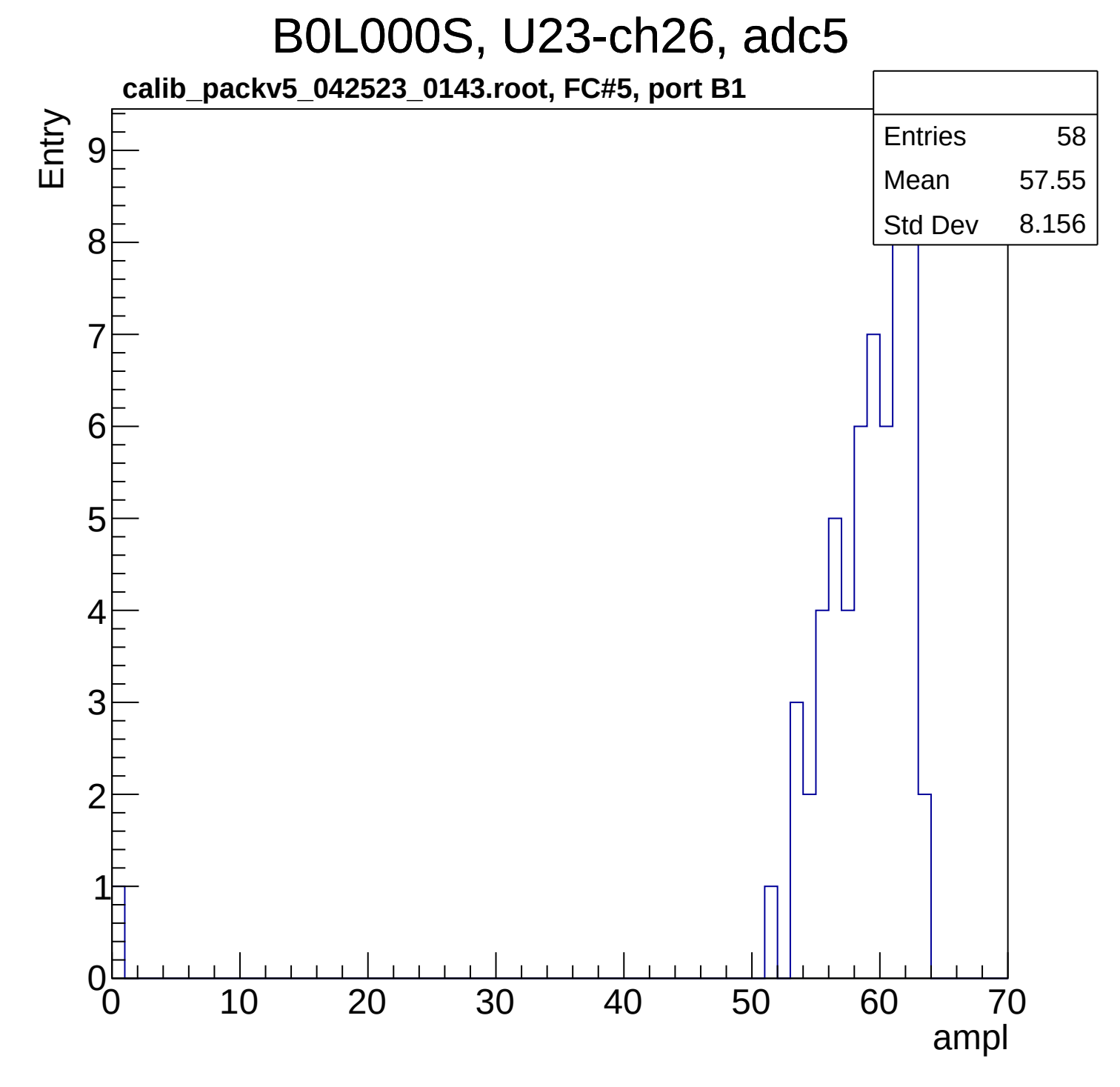

B0L000S, U23-ch26, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.03
Std Dev 4.912
Gaus mean : 28.8896
Gaus Width: 3.8240
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

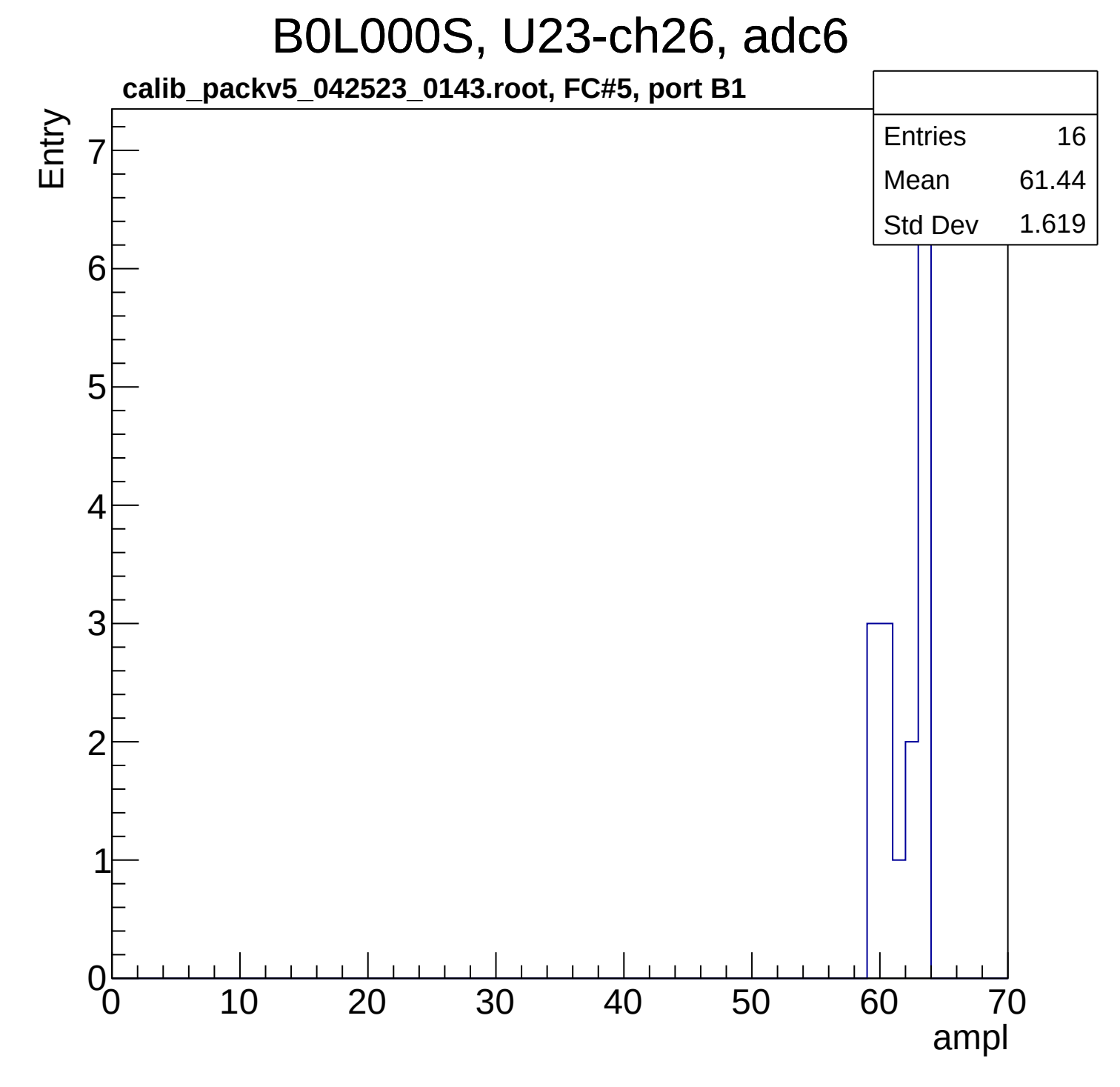

B0L000S, U23-ch26, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 35.63
Std Dev 3.272
Gaus mean : 36.3683
Gaus Width: 3.0388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

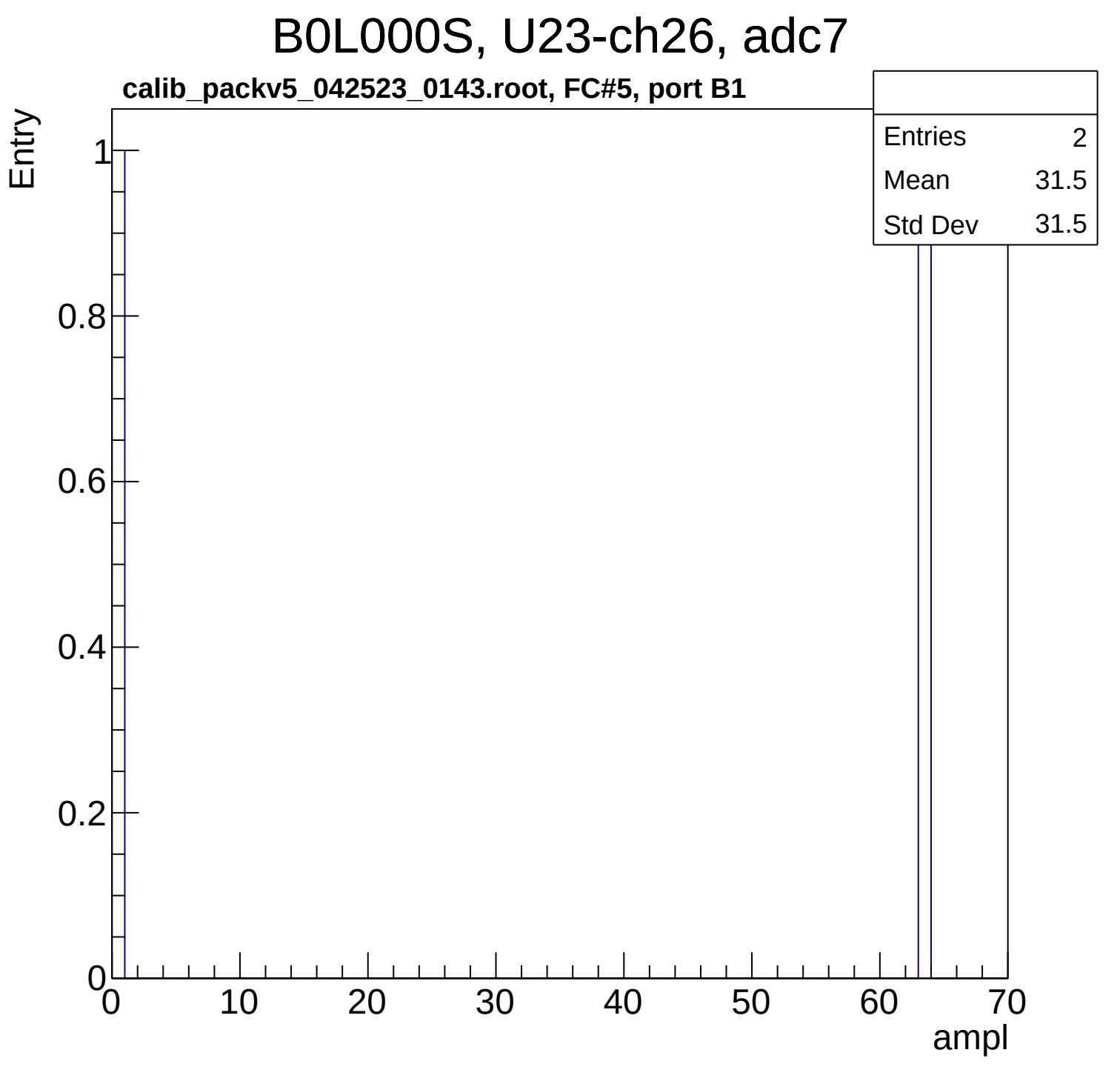

B0L000S, U23-ch26, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 41.46
Std Dev 2.951
Gaus mean : 42.0910
Gaus Width: 3.0171
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

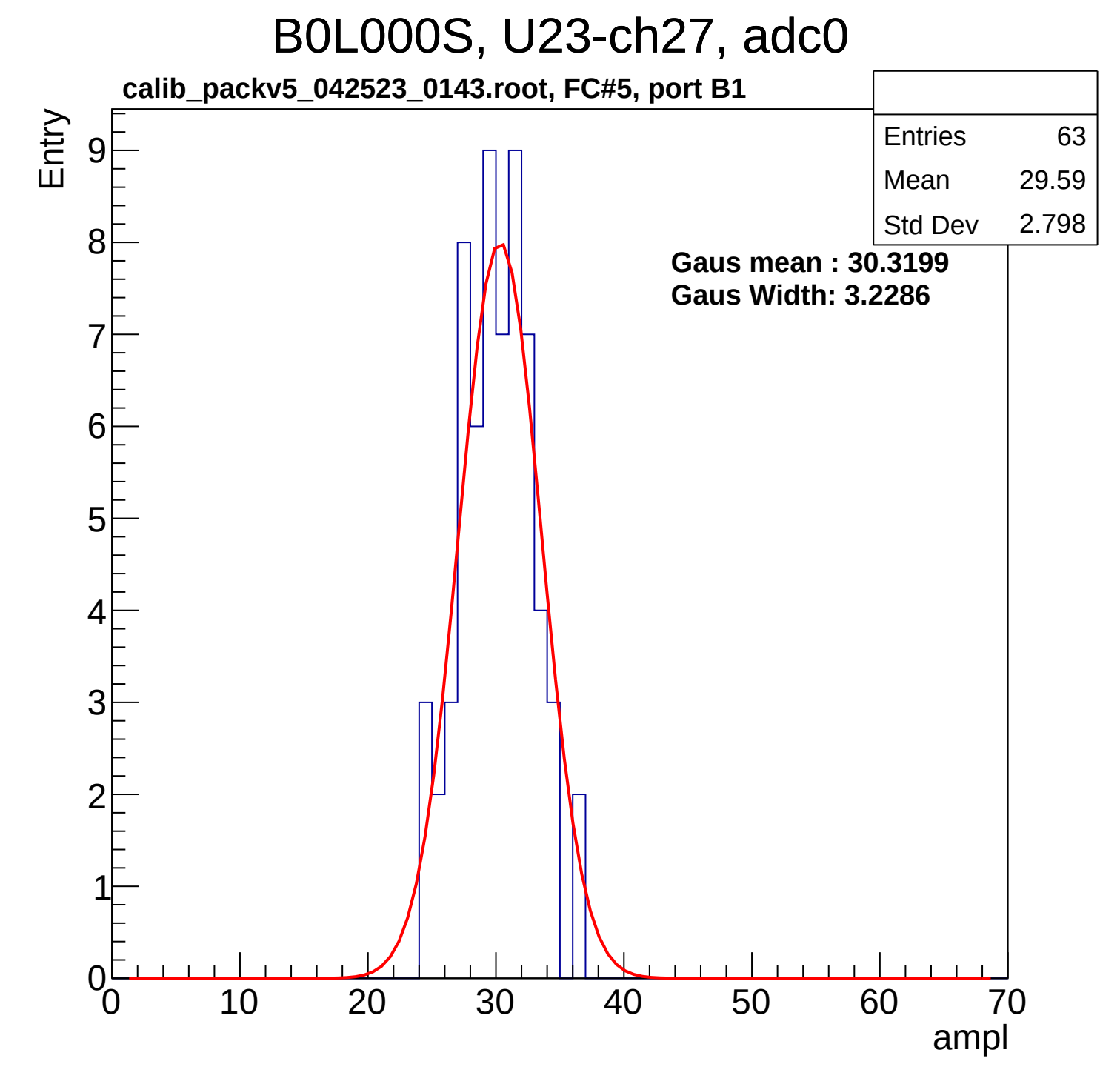

B0L000S, U23-ch26, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.32
Std Dev 3.192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

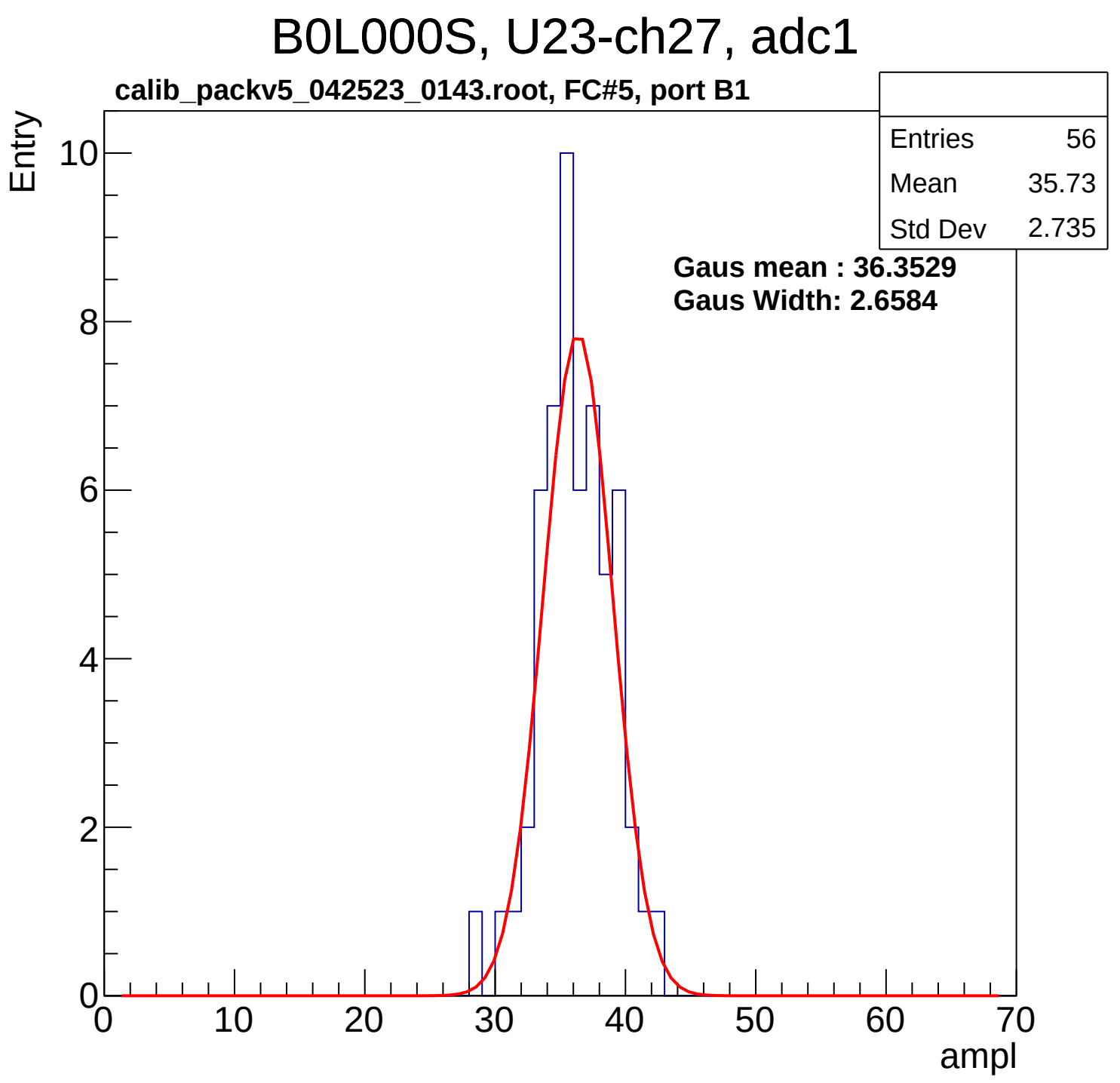

B0L000S, U23-ch26, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.64
Std Dev 3.047
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

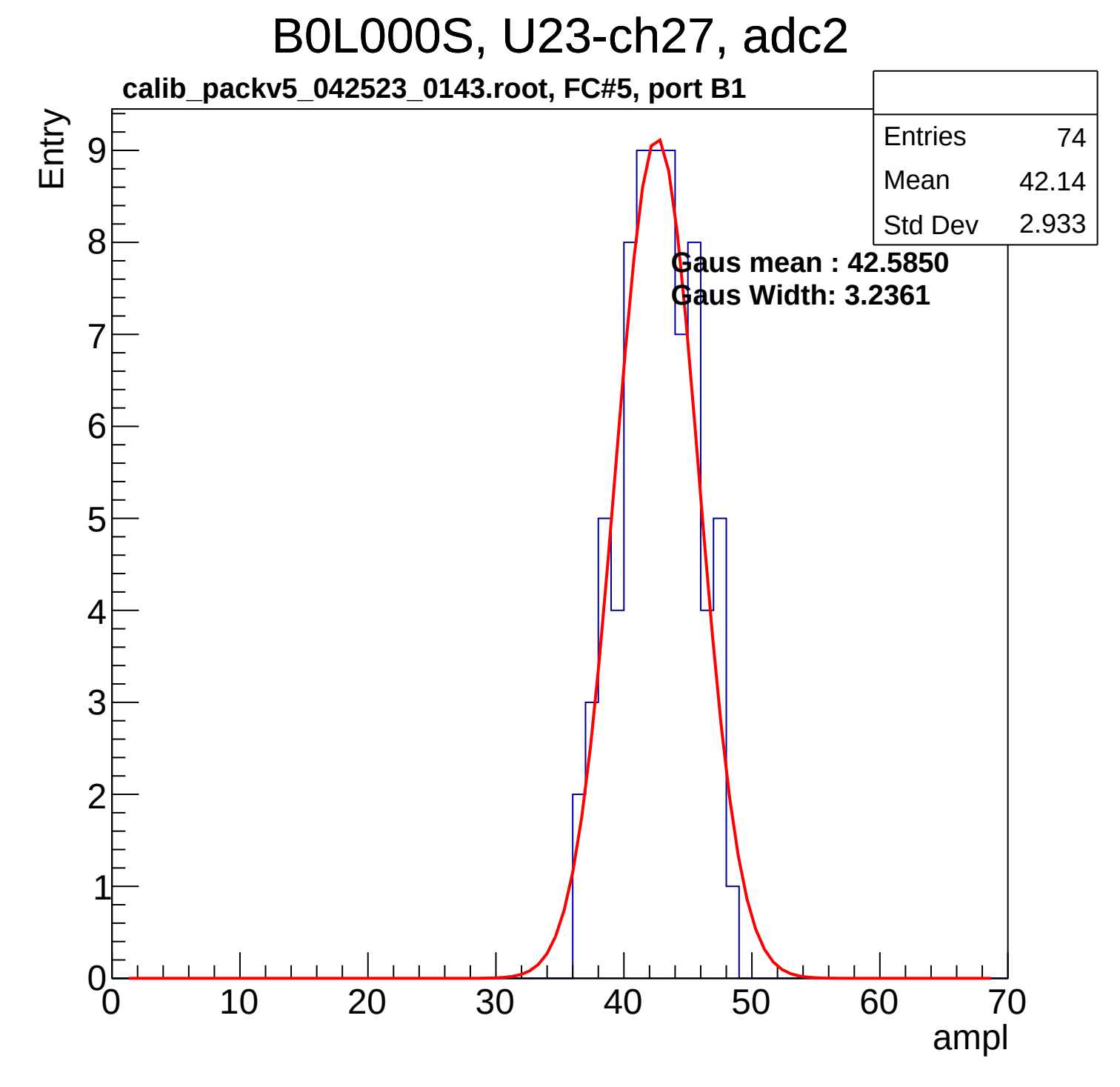

B0L000S, U23-ch26, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.55
Std Dev 8.156
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

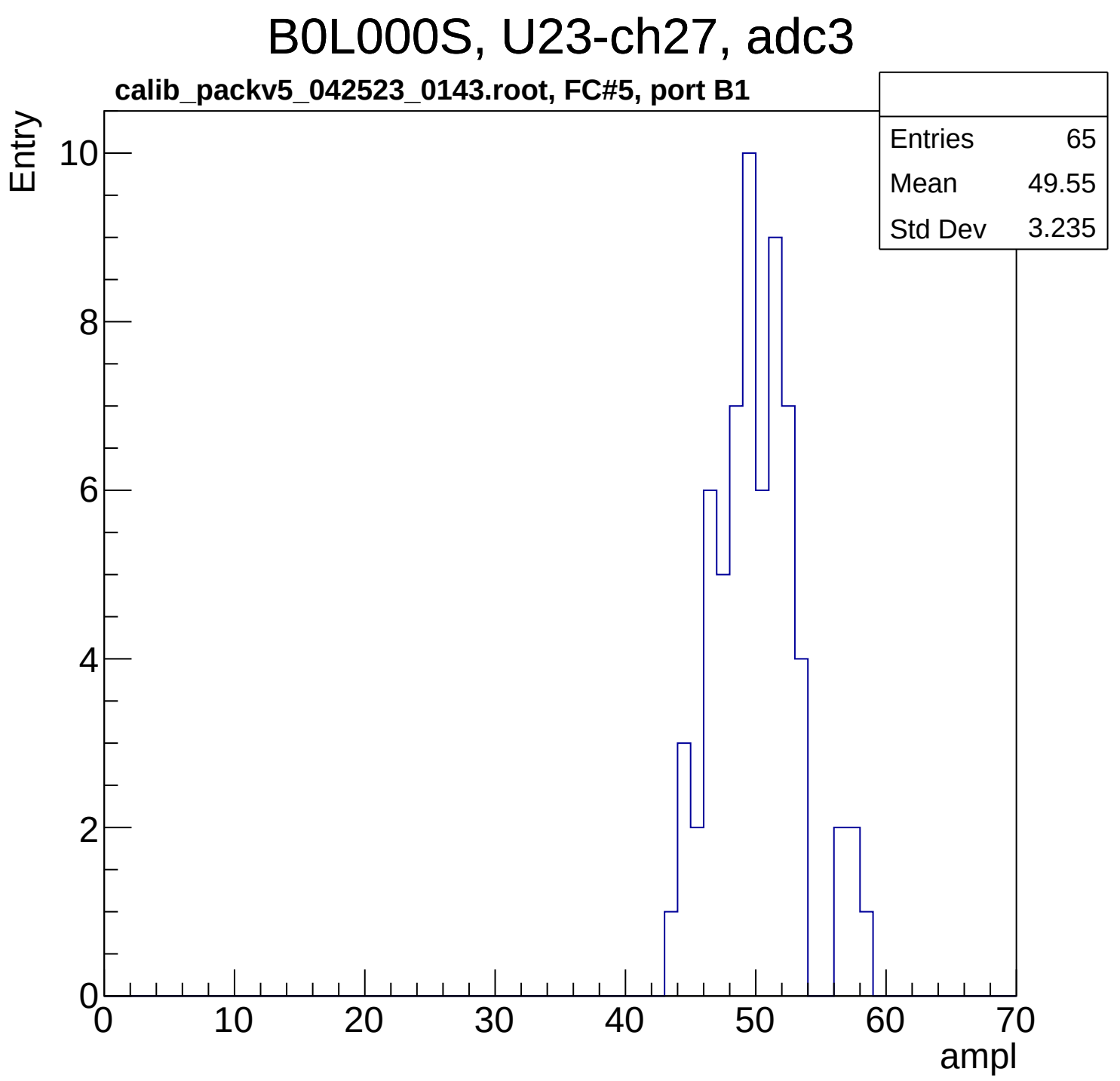

B0L000S, U23-ch26, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.44
Std Dev 1.619
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

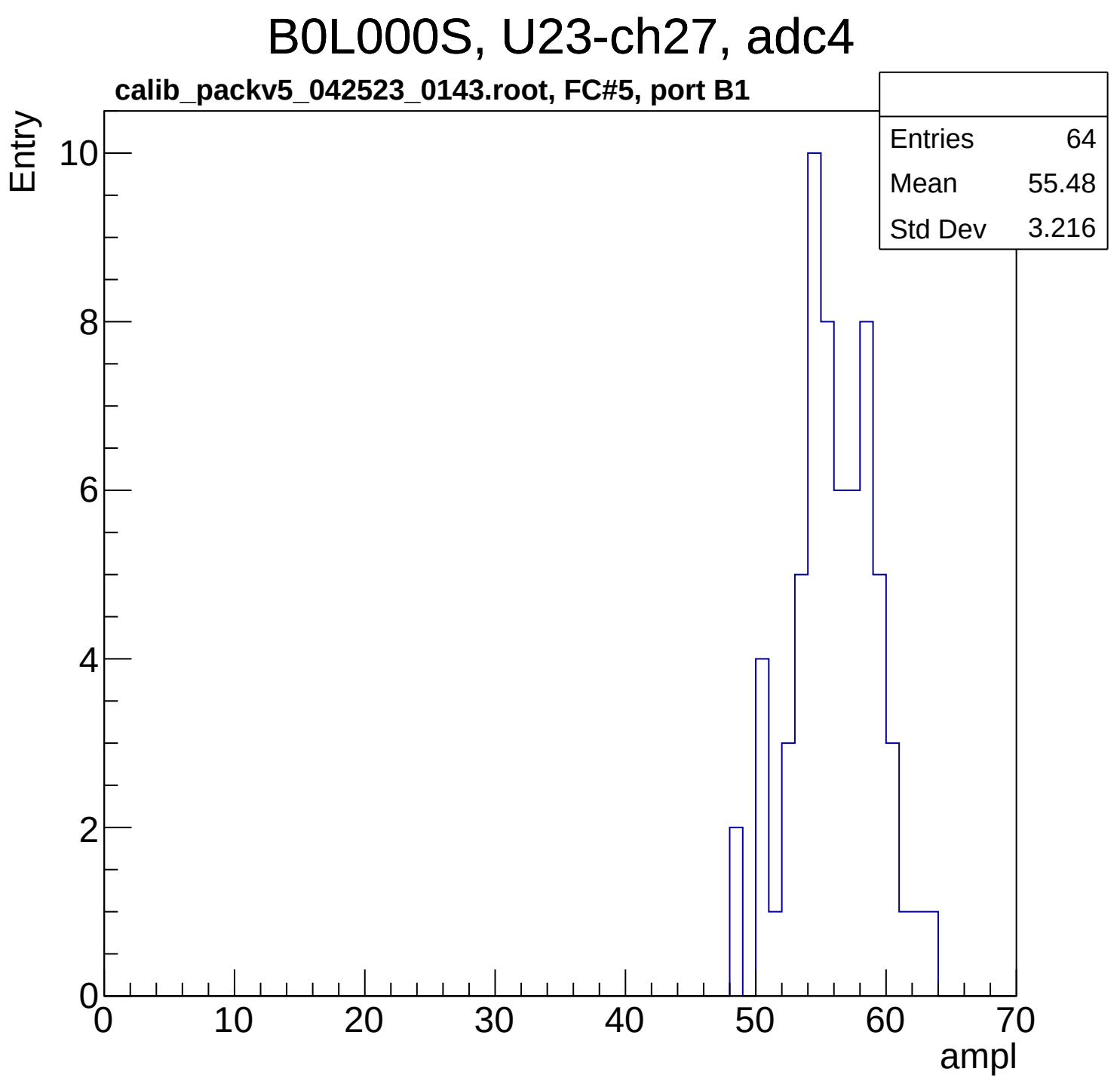

B0L000S, U23-ch26, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

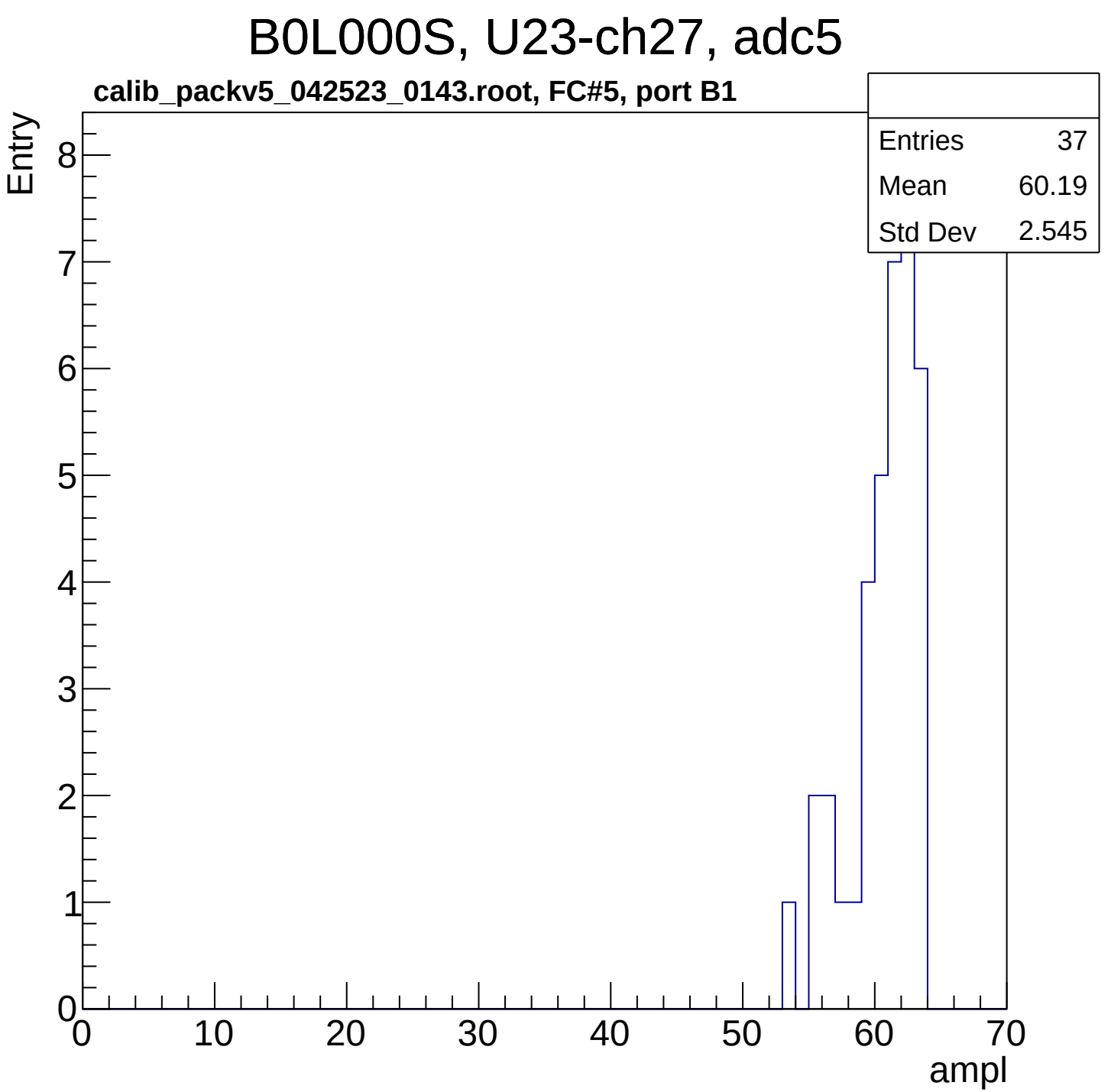

B0L000S, U23-ch27, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.59
Std Dev 2.798
Gaus mean : 30.3199
Gaus Width: 3.2286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

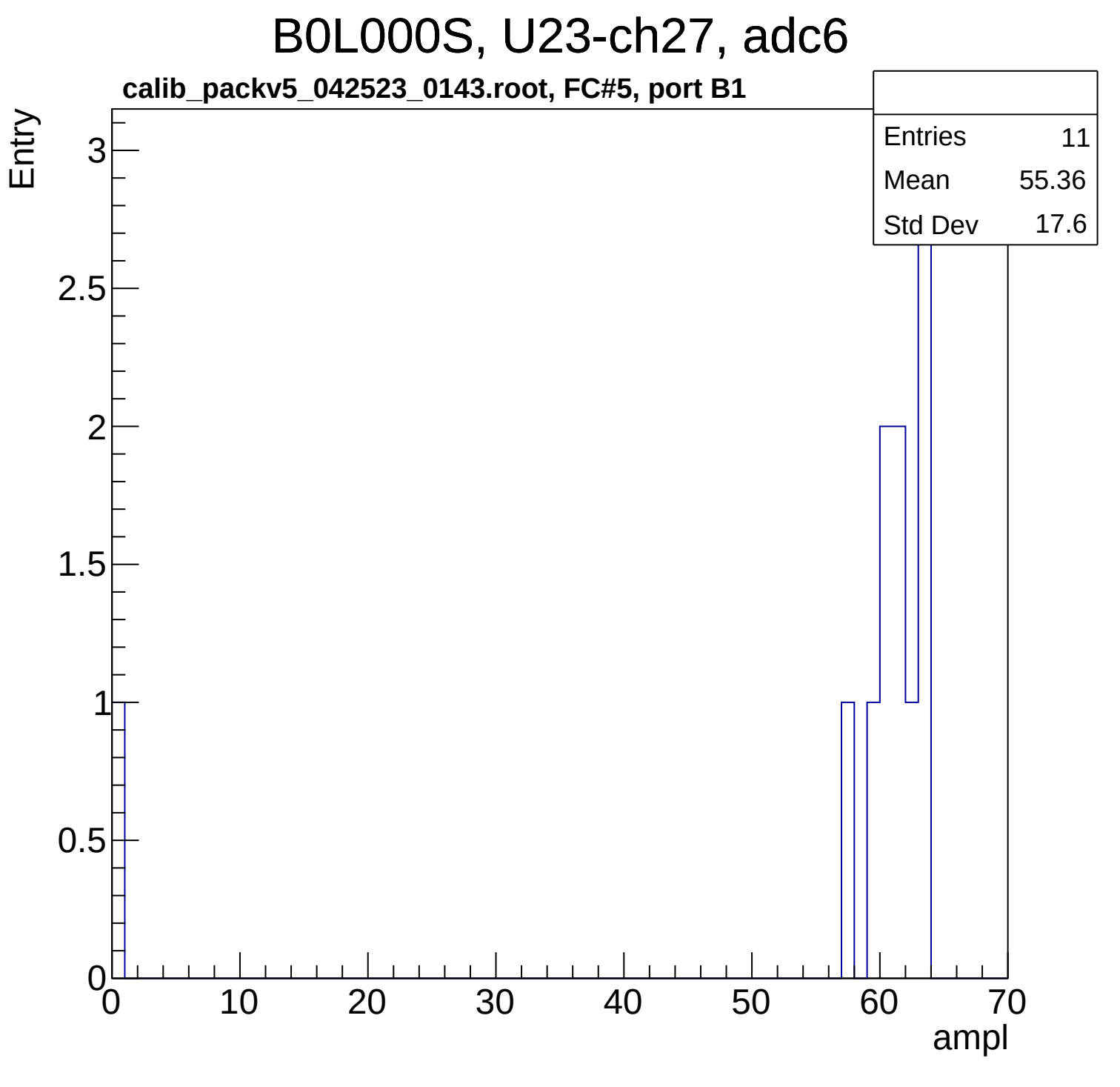

B0L000S, U23-ch27, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 35.73
Std Dev 2.735
Gaus mean : 36.3529
Gaus Width: 2.6584
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

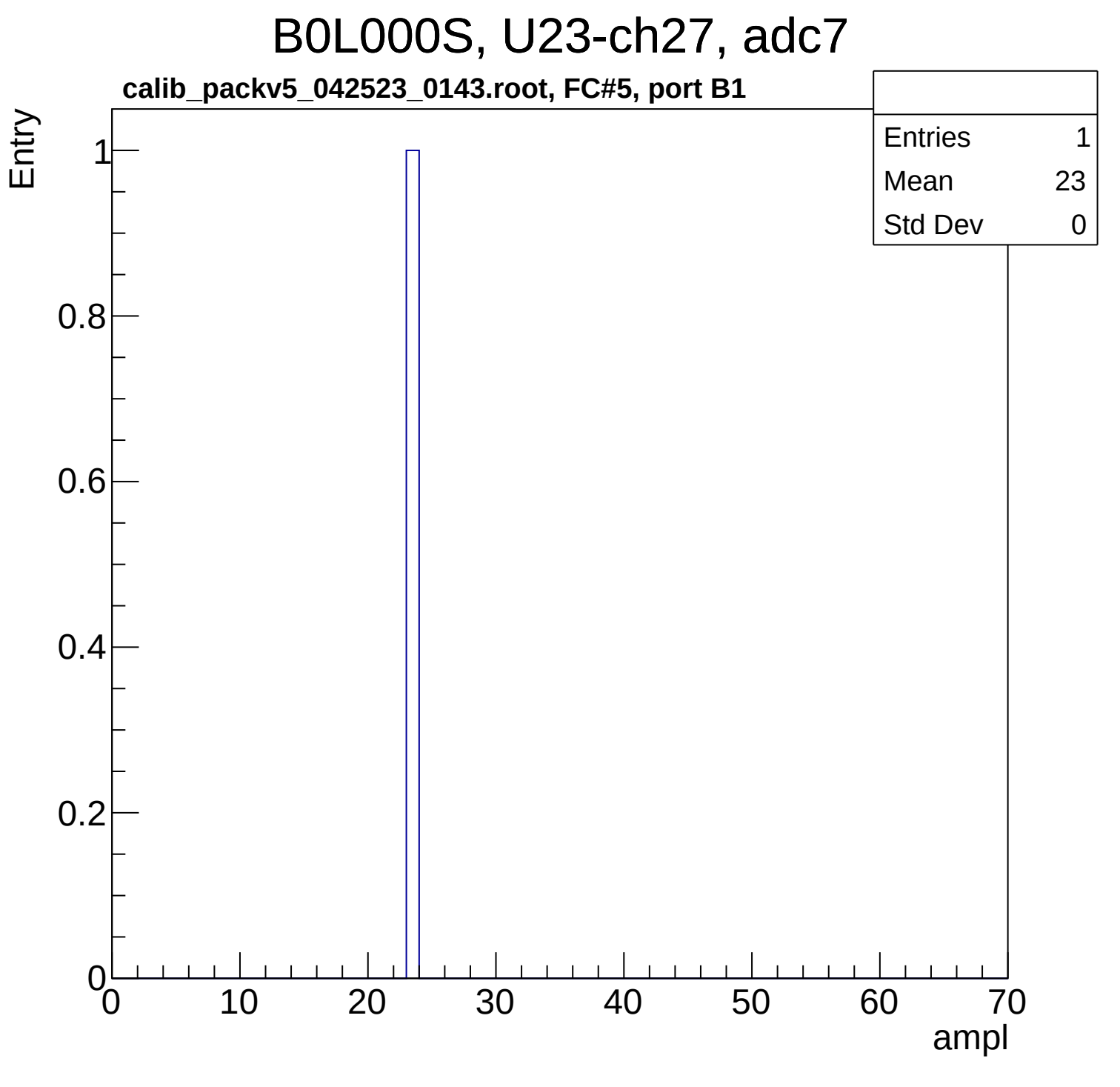

B0L000S, U23-ch27, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 42.14
Std Dev 2.933
Gaus mean : 42.5850
Gaus Width: 3.2361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

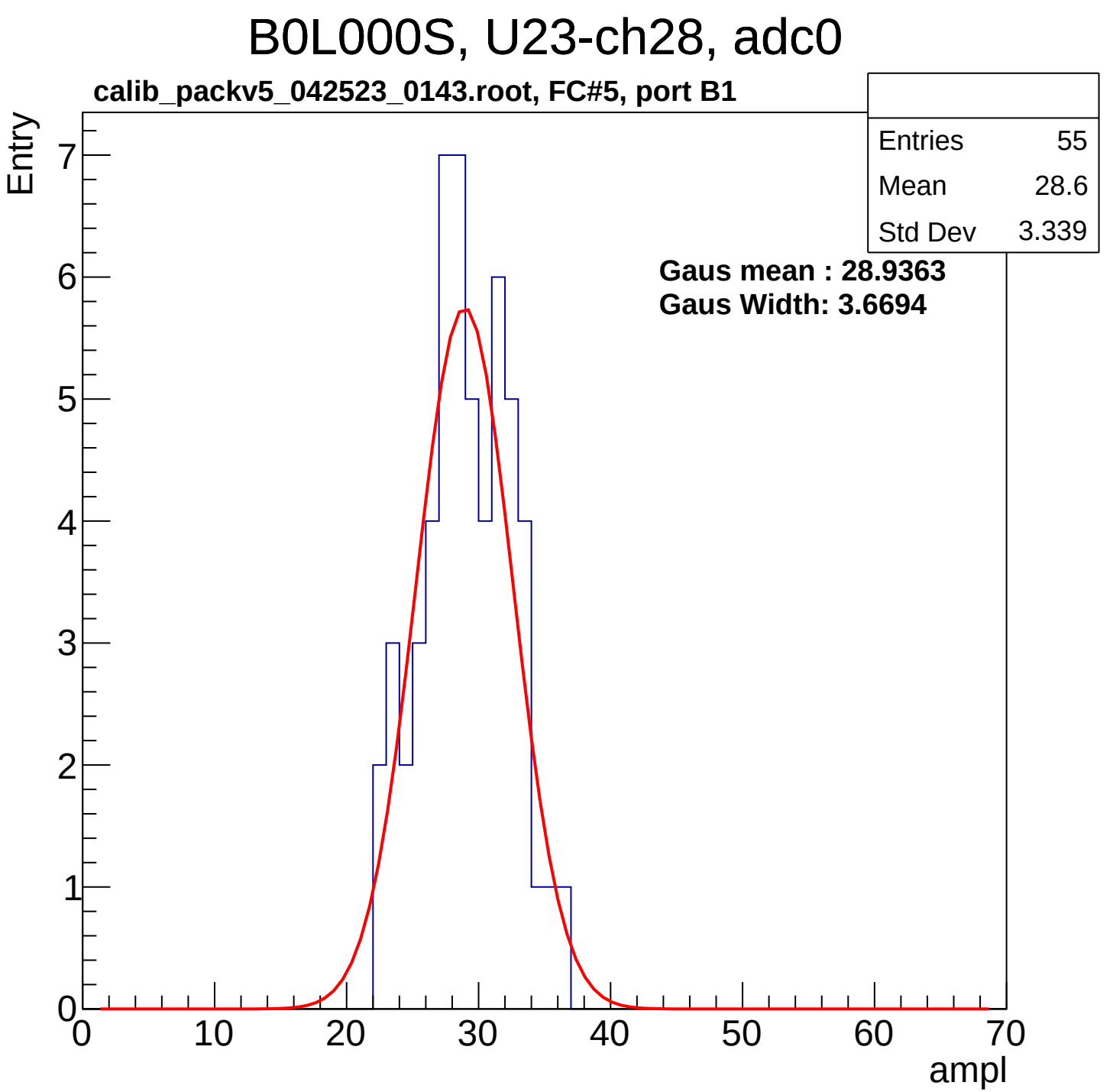

B0L000S, U23-ch27, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.55
Std Dev 3.235
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

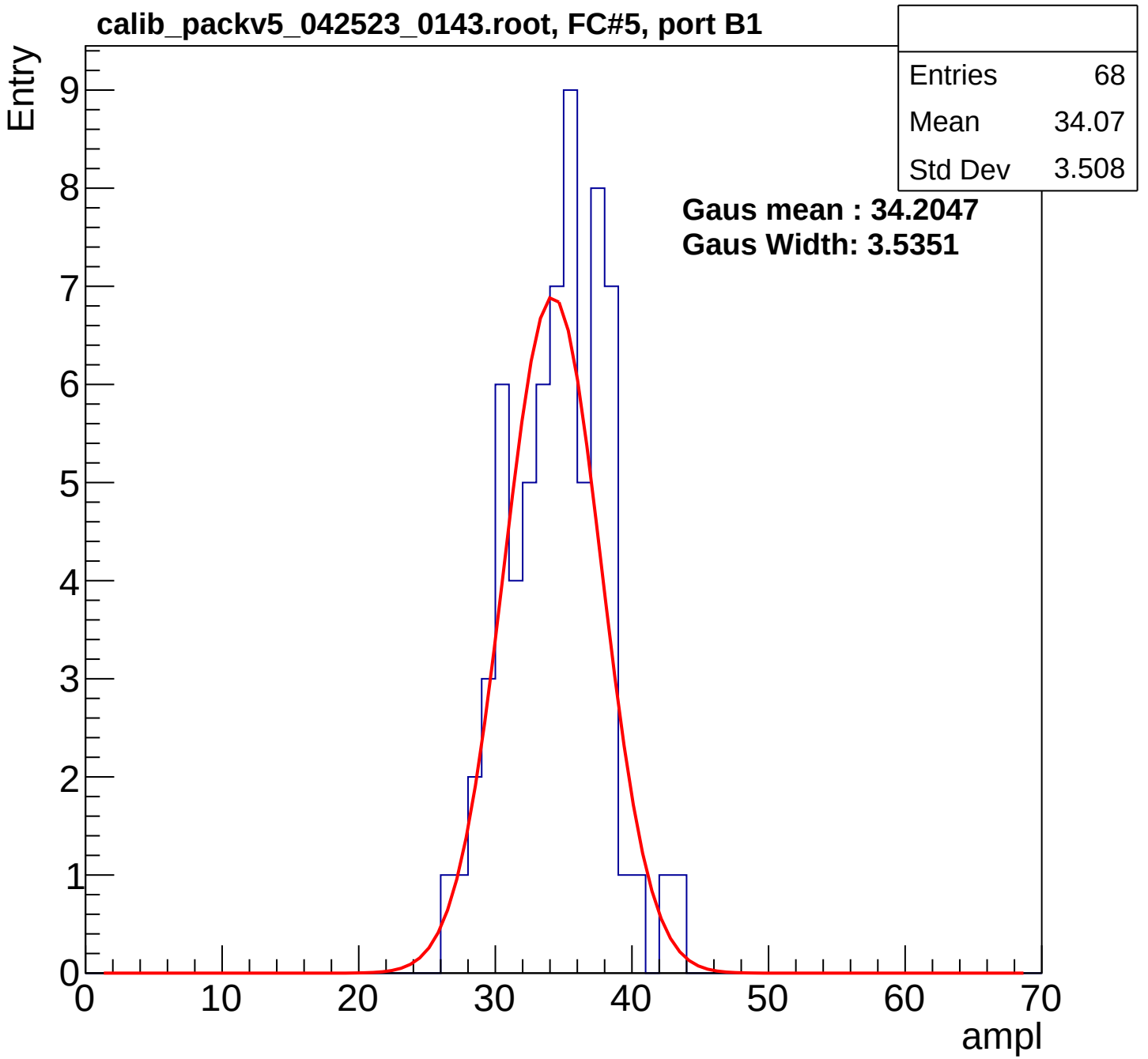

B0L000S, U23-ch27, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 55.48
Std Dev 3.216
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

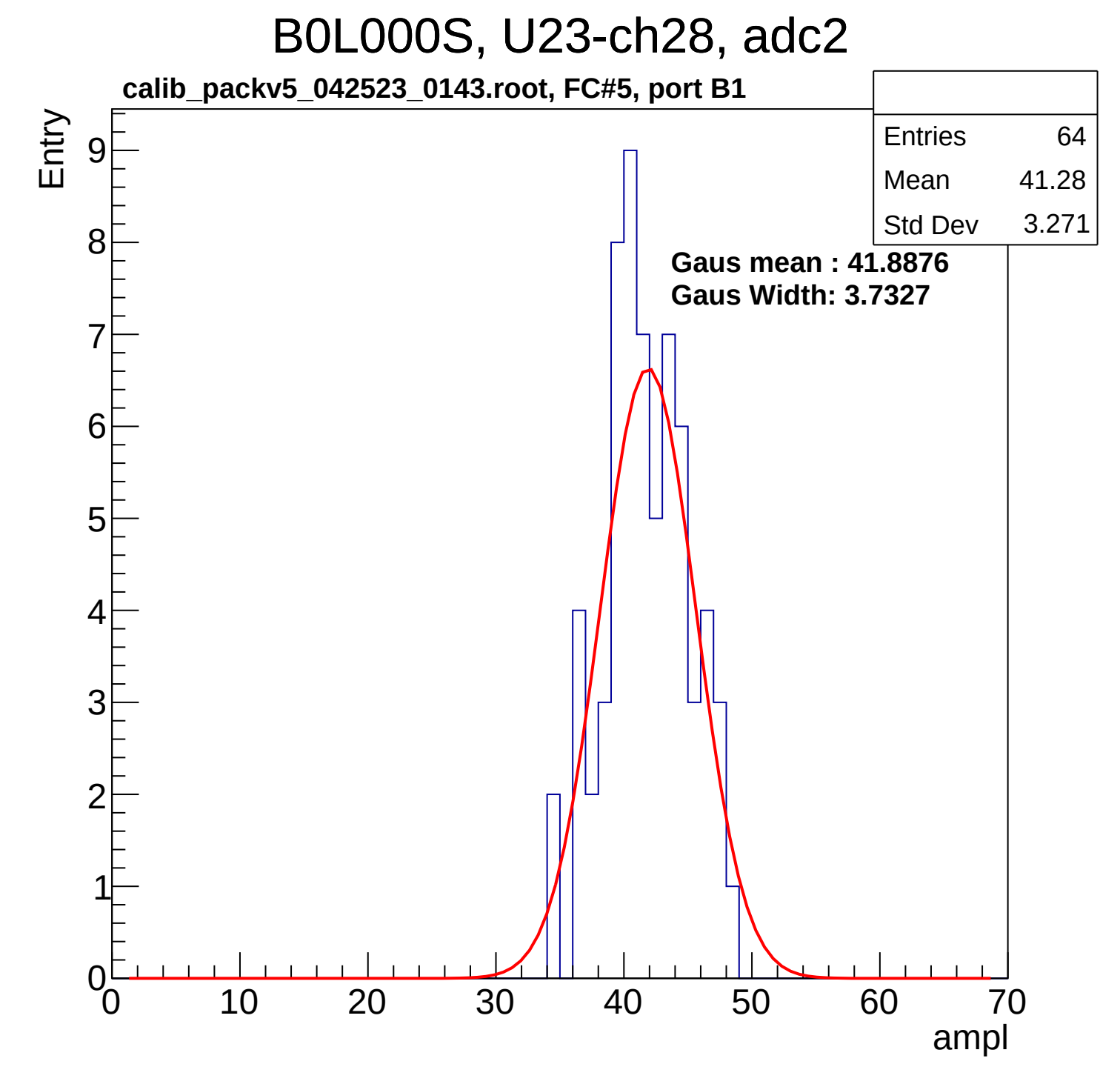

B0L000S, U23-ch27, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.19
Std Dev 2.545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

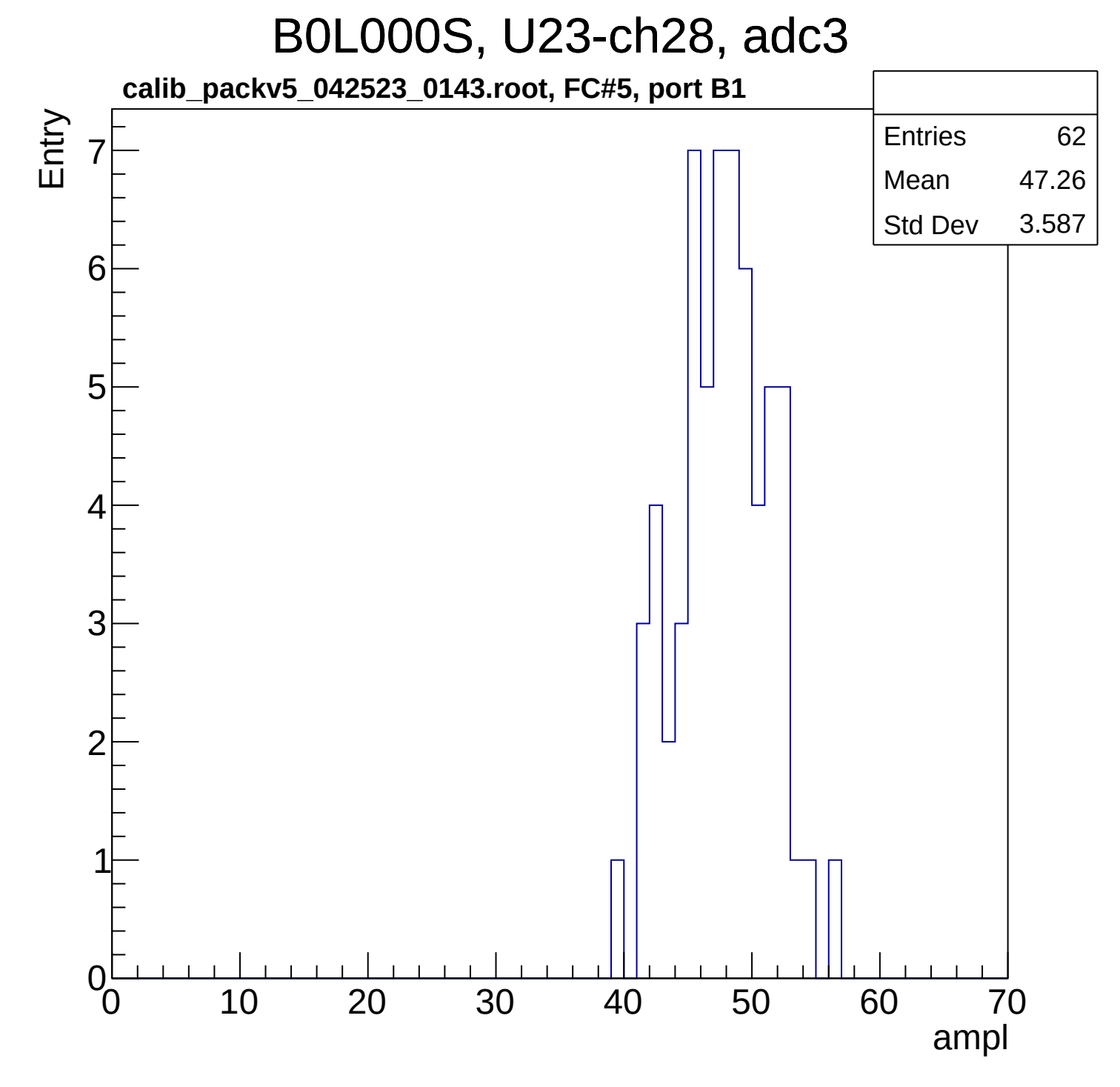

B0L000S, U23-ch27, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 55.36
Std Dev 17.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

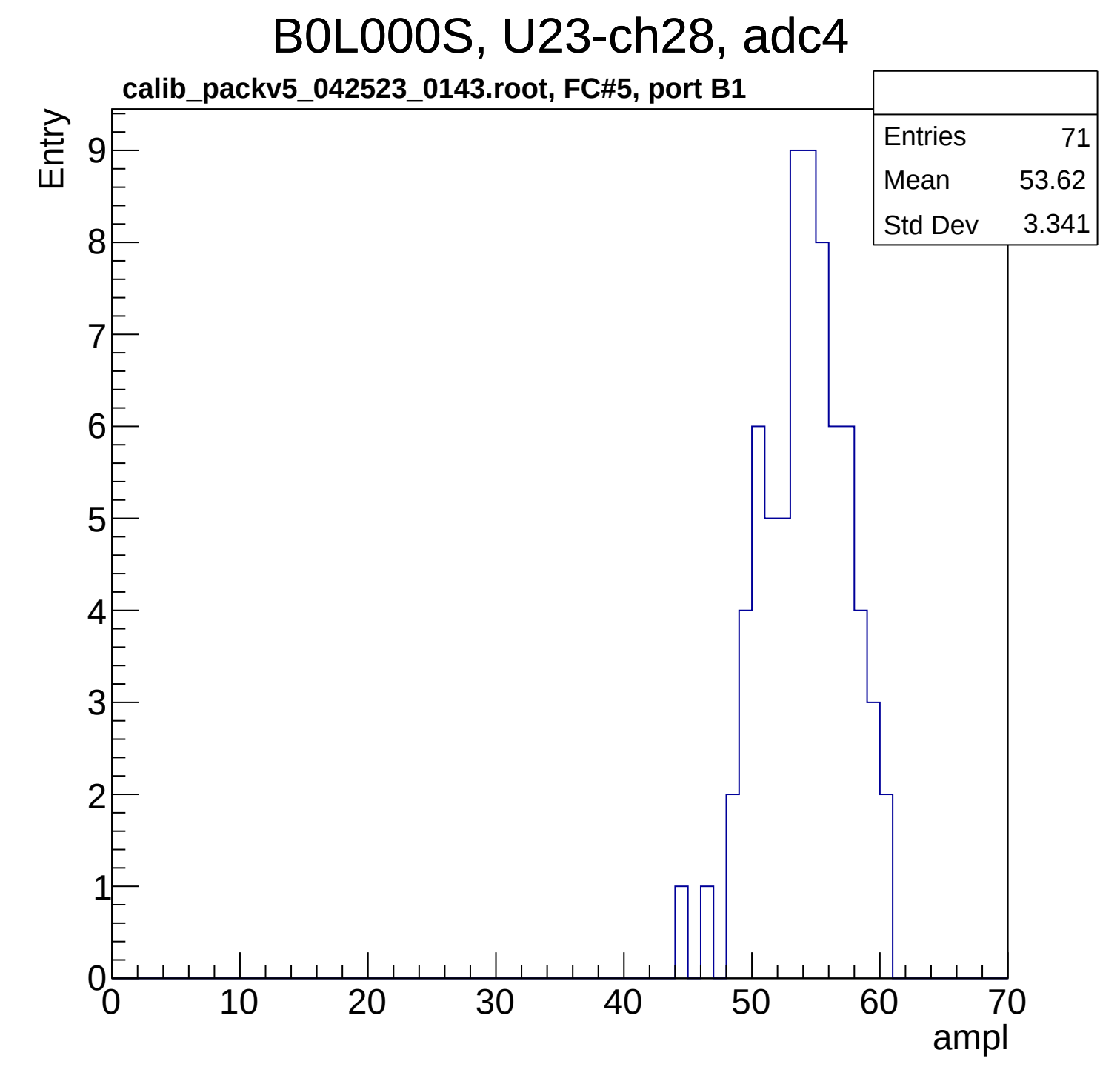

B0L000S, U23-ch27, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

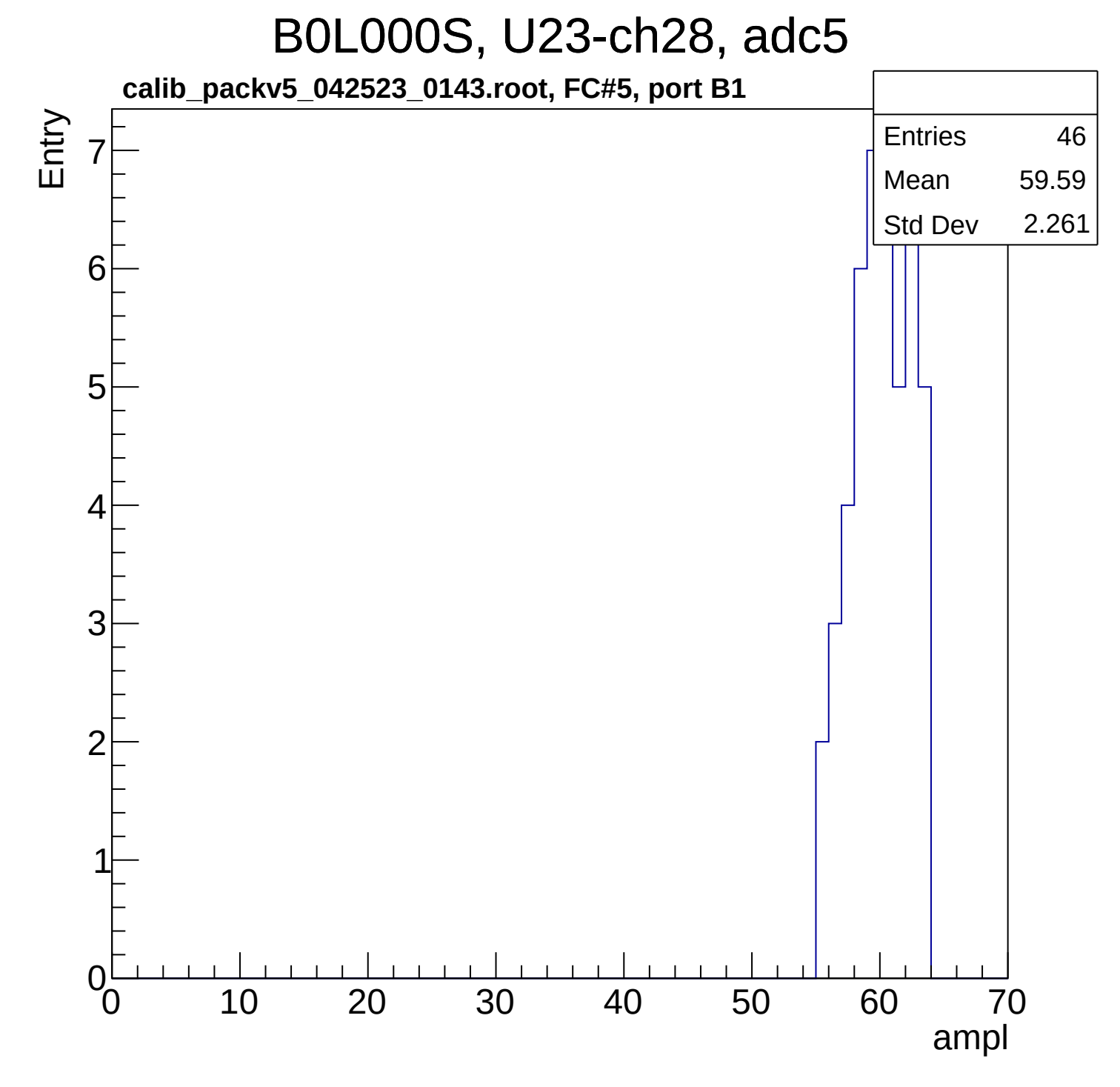

B0L000S, U23-ch28, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 28.6
Std Dev 3.339
Gaus mean : 28.9363
Gaus Width: 3.6694
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

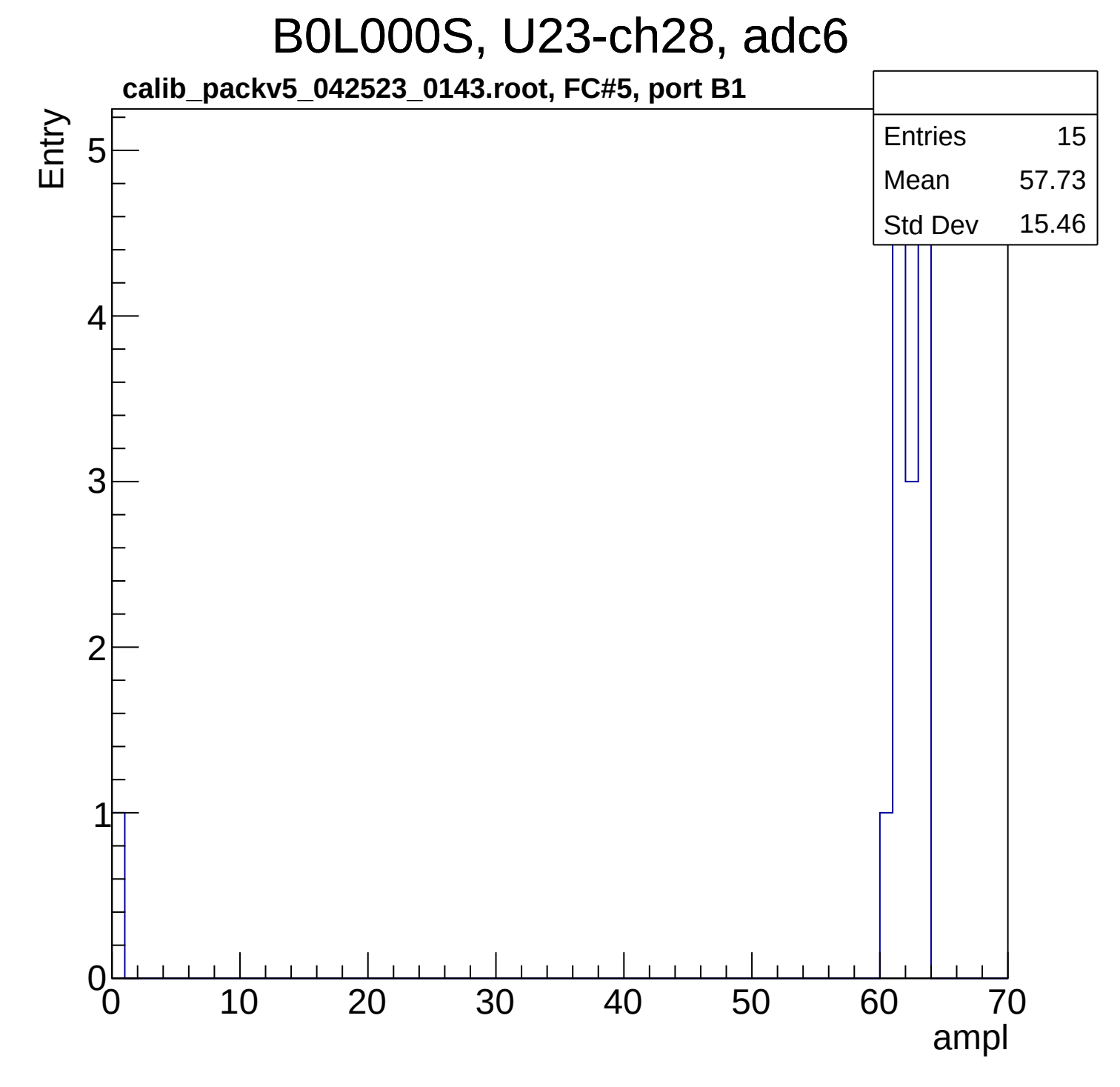

B0L000S, U23-ch28, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 34.07
Std Dev 3.508
Gaus mean : 34.2047
Gaus Width: 3.5351
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

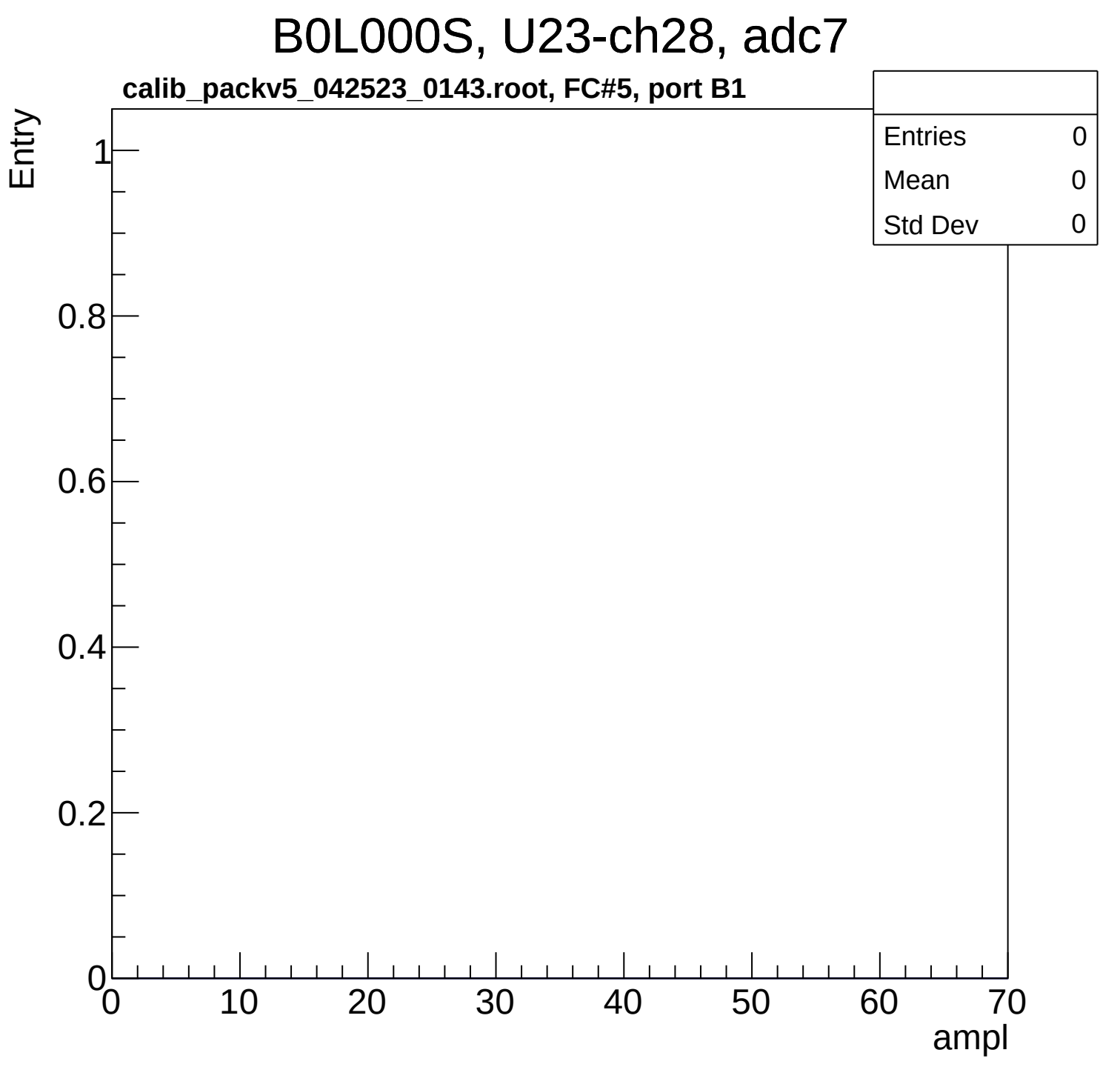

B0L000S, U23-ch28, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 41.28
Std Dev 3.271
Gaus mean : 41.8876
Gaus Width: 3.7327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

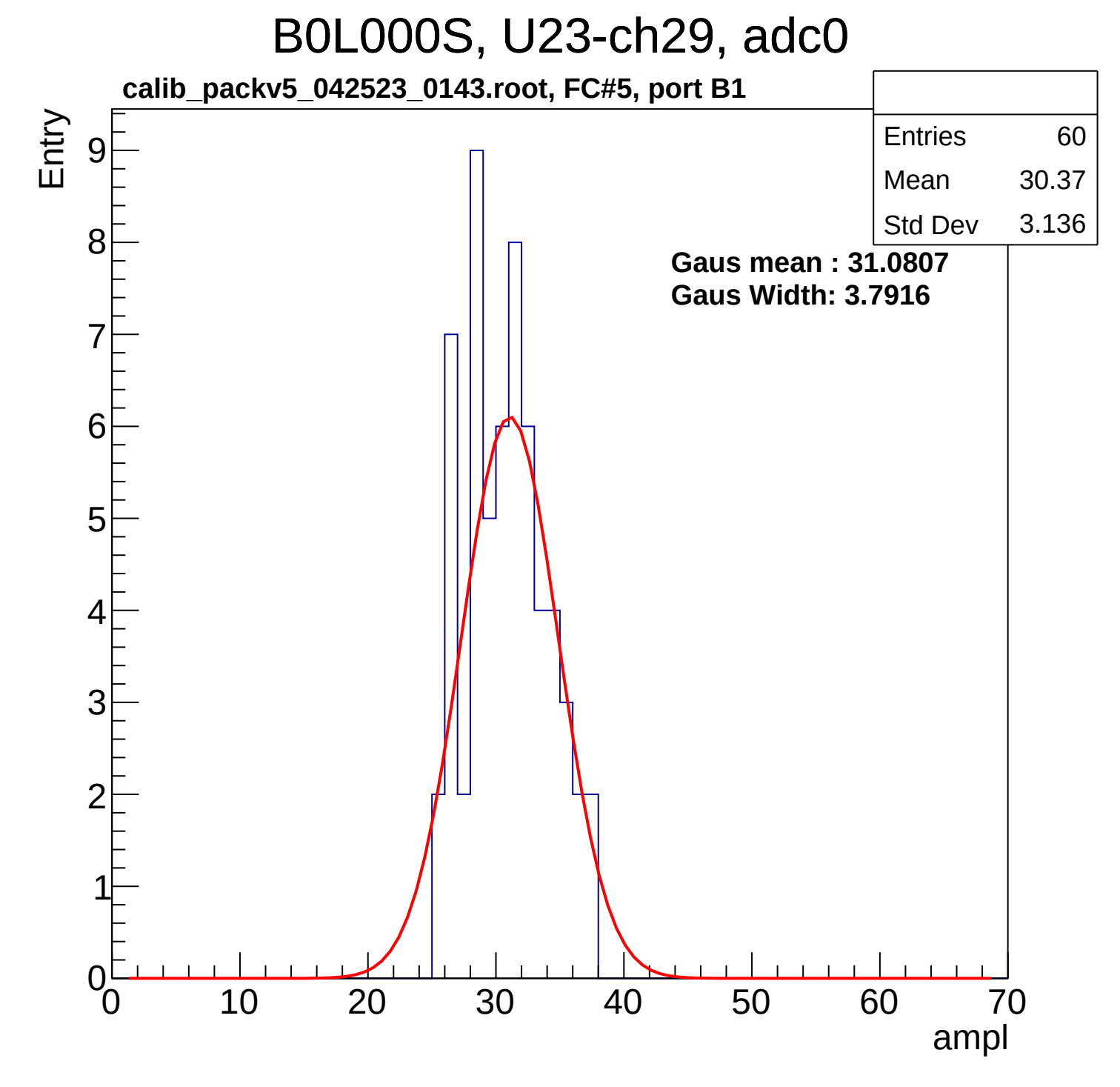

B0L000S, U23-ch28, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 47.26
Std Dev 3.587
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

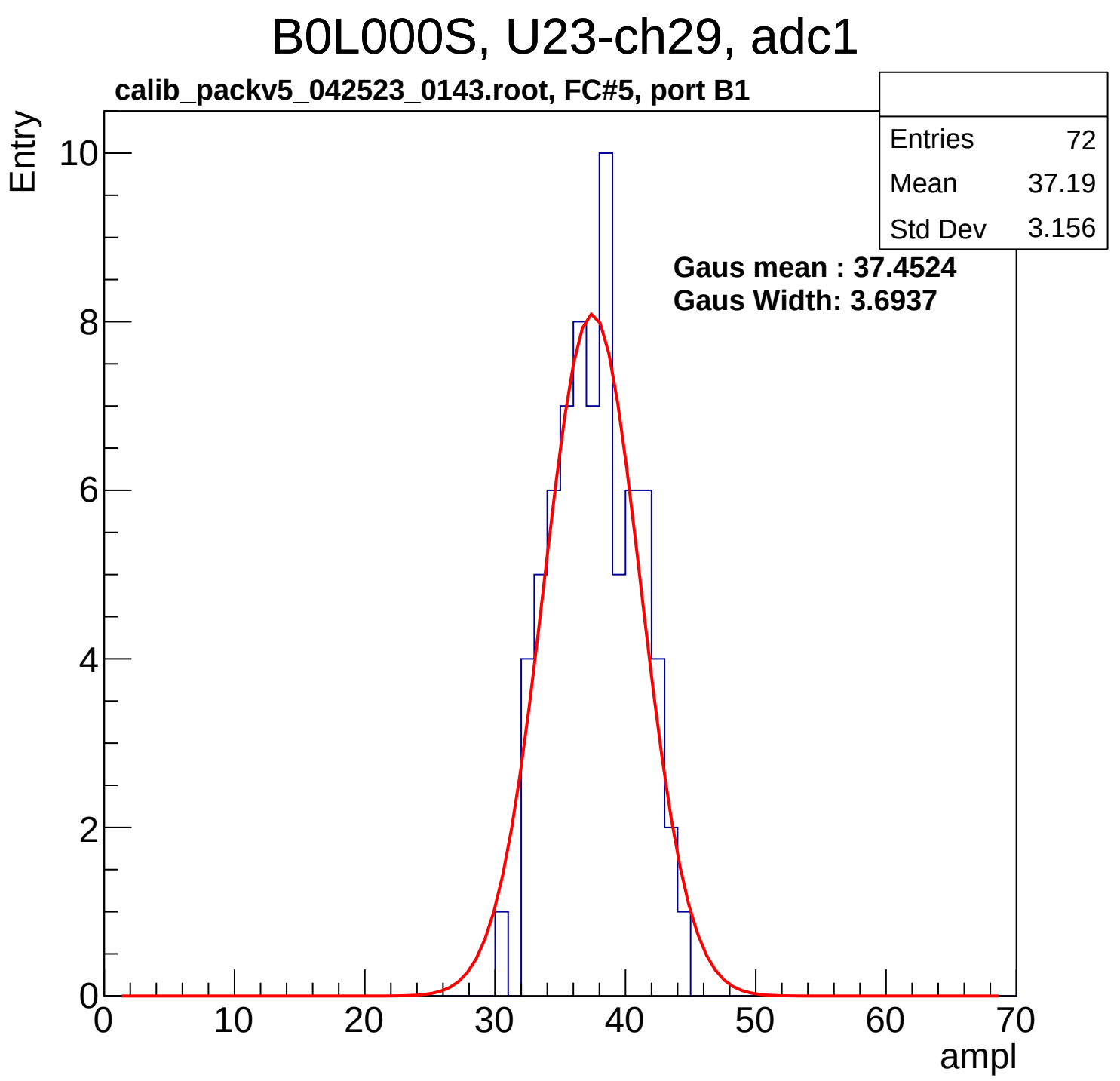

B0L000S, U23-ch28, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 53.62
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

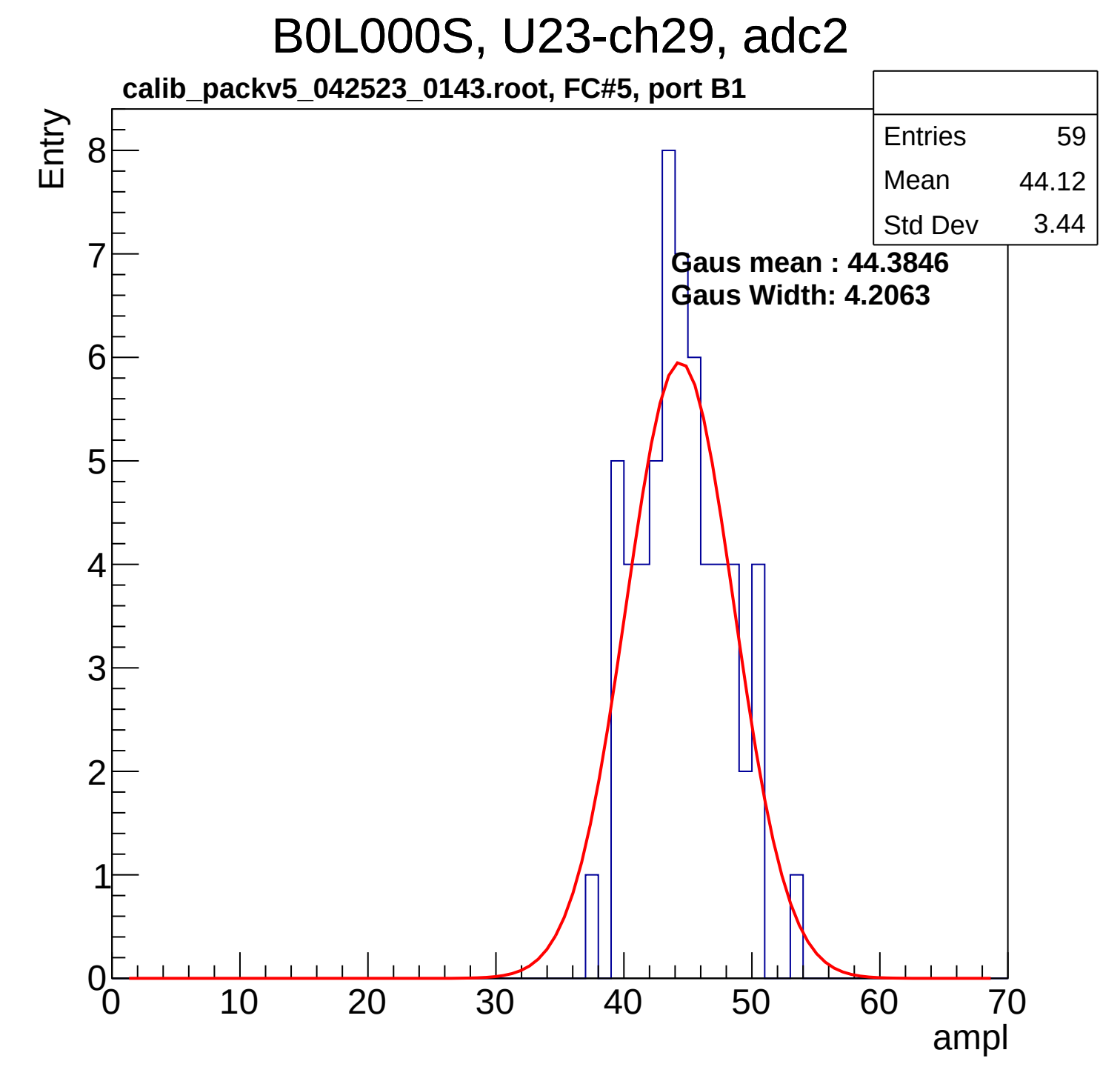

B0L000S, U23-ch28, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 59.59
Std Dev 2.261
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

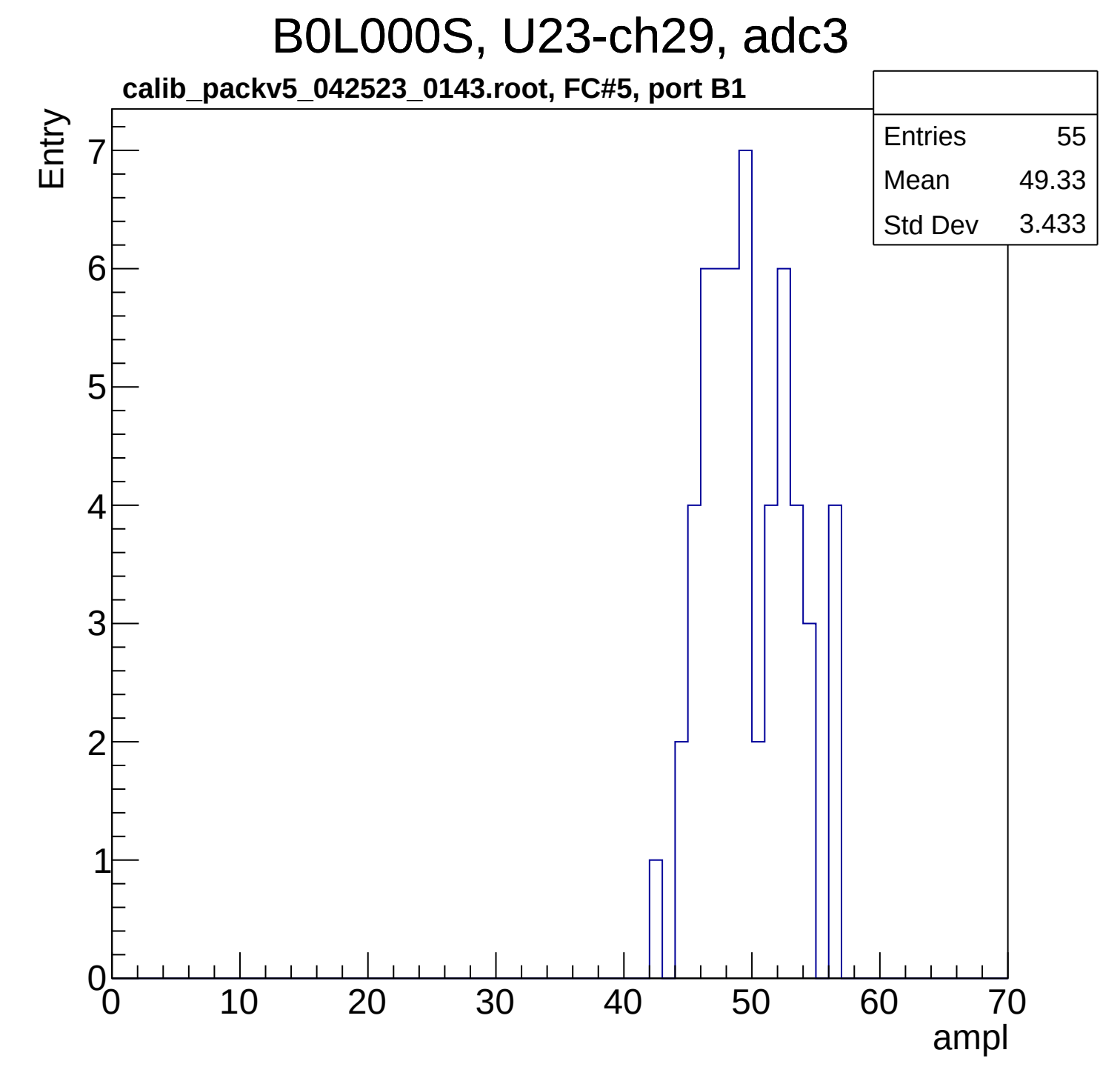

B0L000S, U23-ch28, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.73
Std Dev 15.46
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

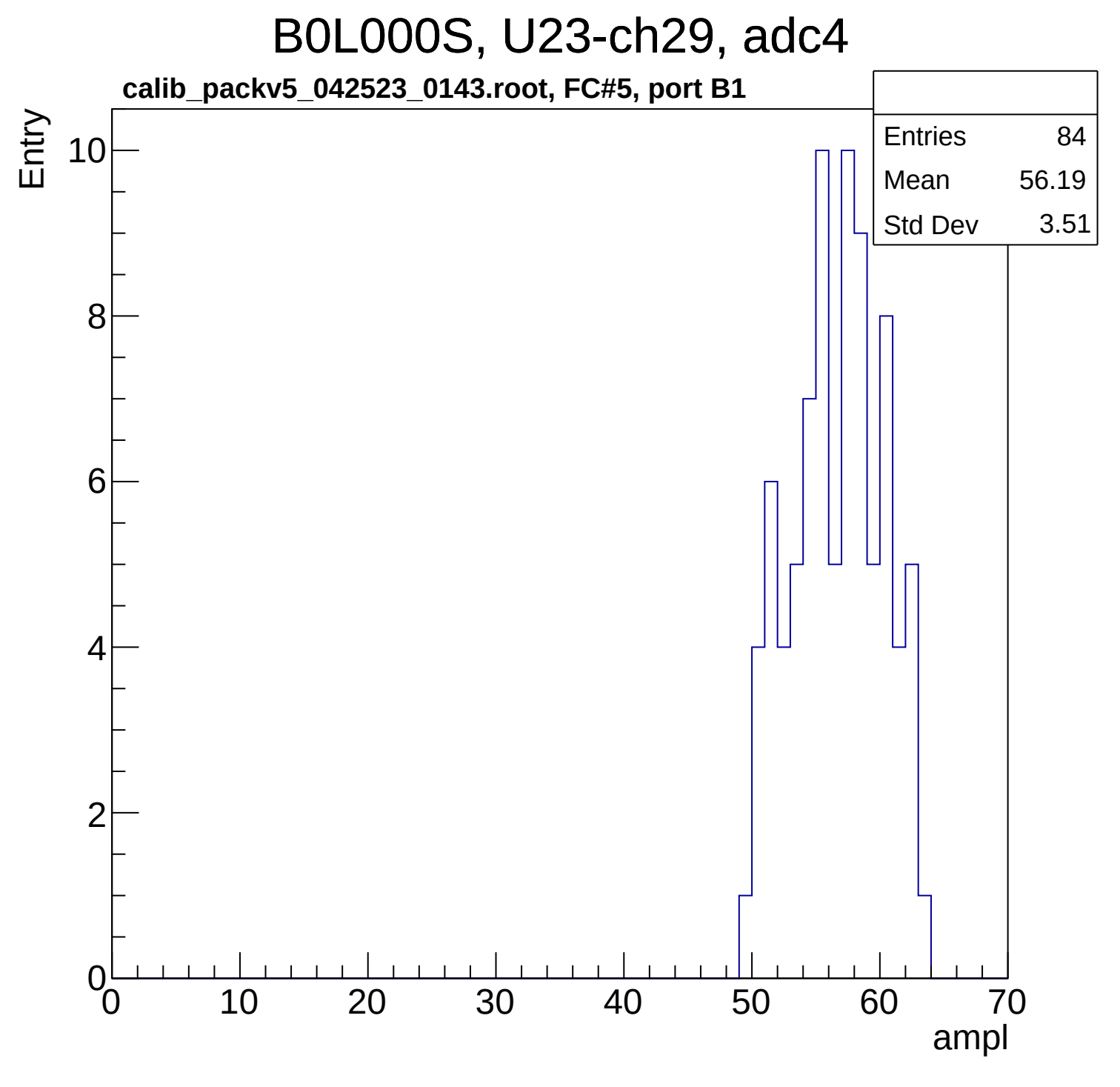

B0L000S, U23-ch28, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

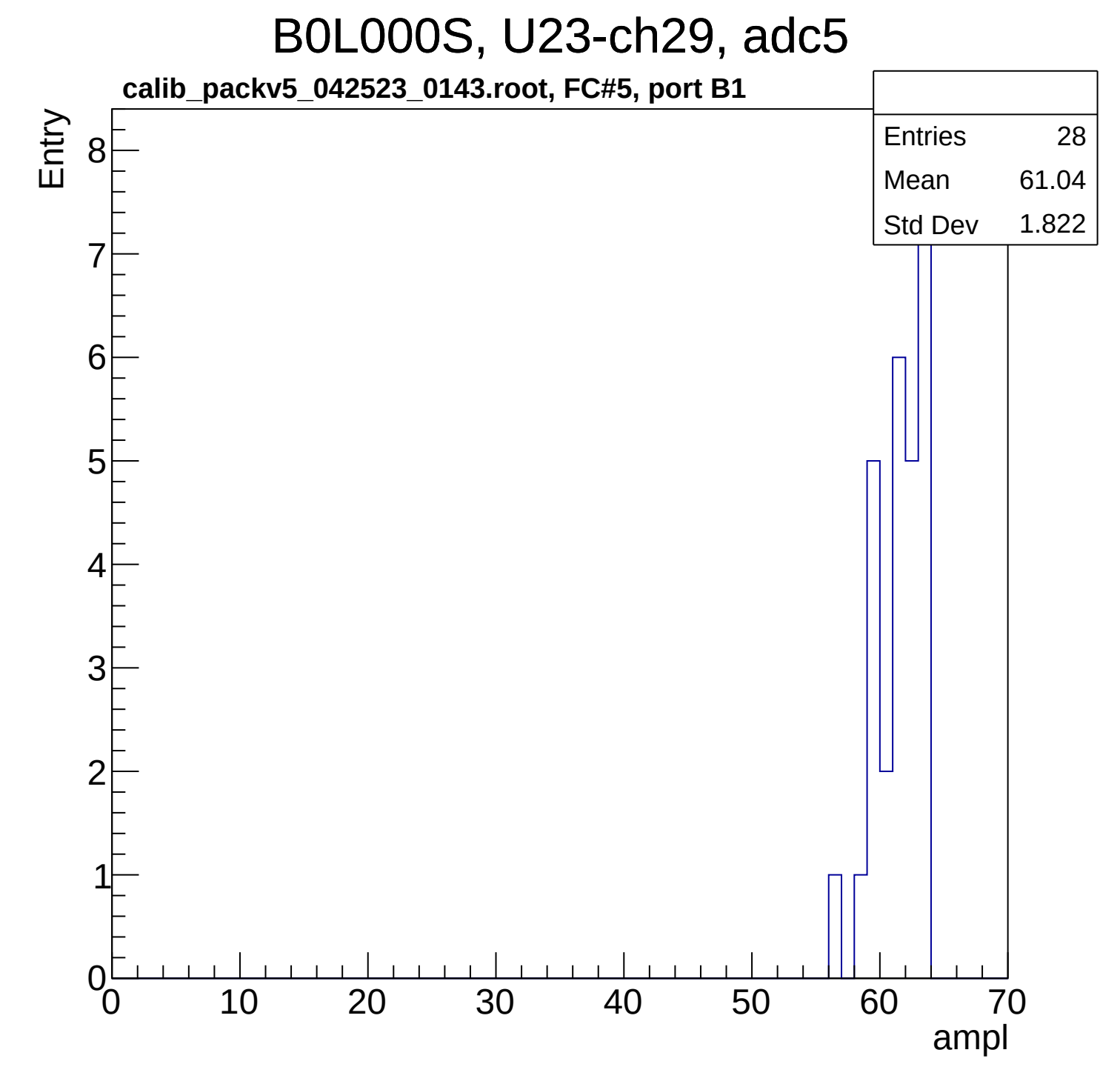

B0L000S, U23-ch29, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 30.37
Std Dev 3.136
Gaus mean : 31.0807
Gaus Width: 3.7916
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

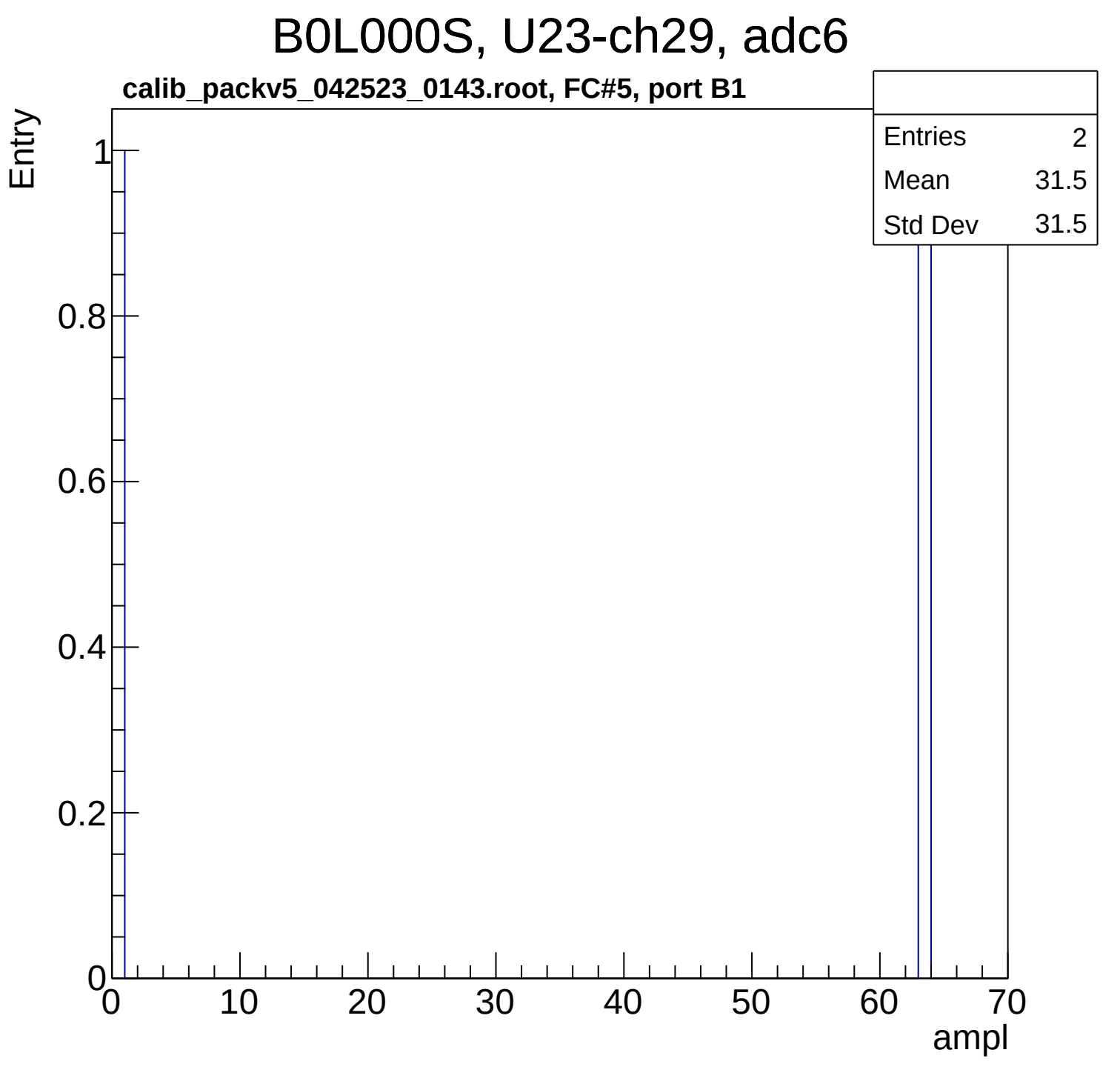

B0L000S, U23-ch29, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.19
Std Dev 3.156
Gaus mean : 37.4524
Gaus Width: 3.6937
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

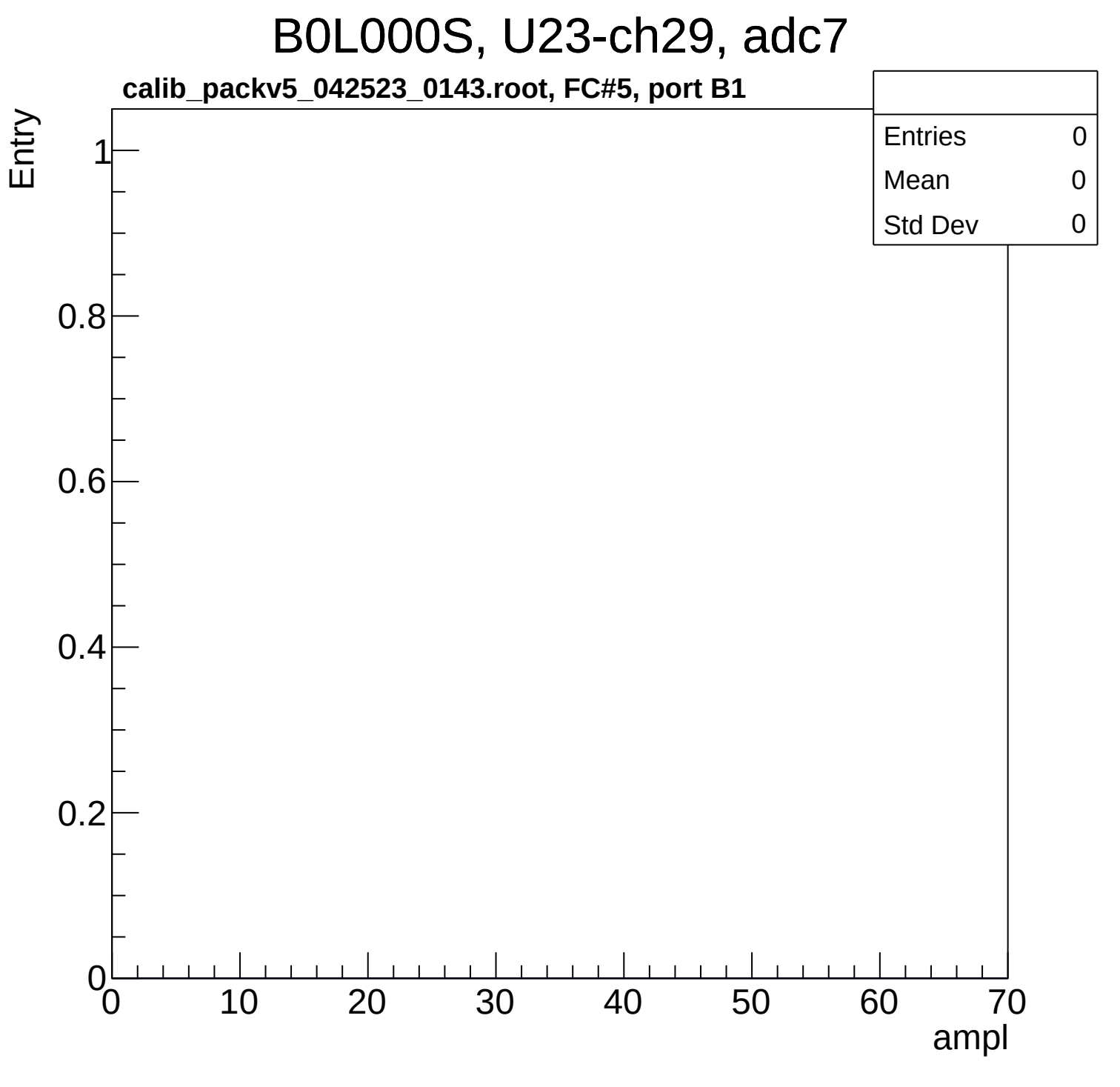

B0L000S, U23-ch29, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 44.12
Std Dev 3.44
Gaus mean : 44.3846
Gaus Width: 4.2063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

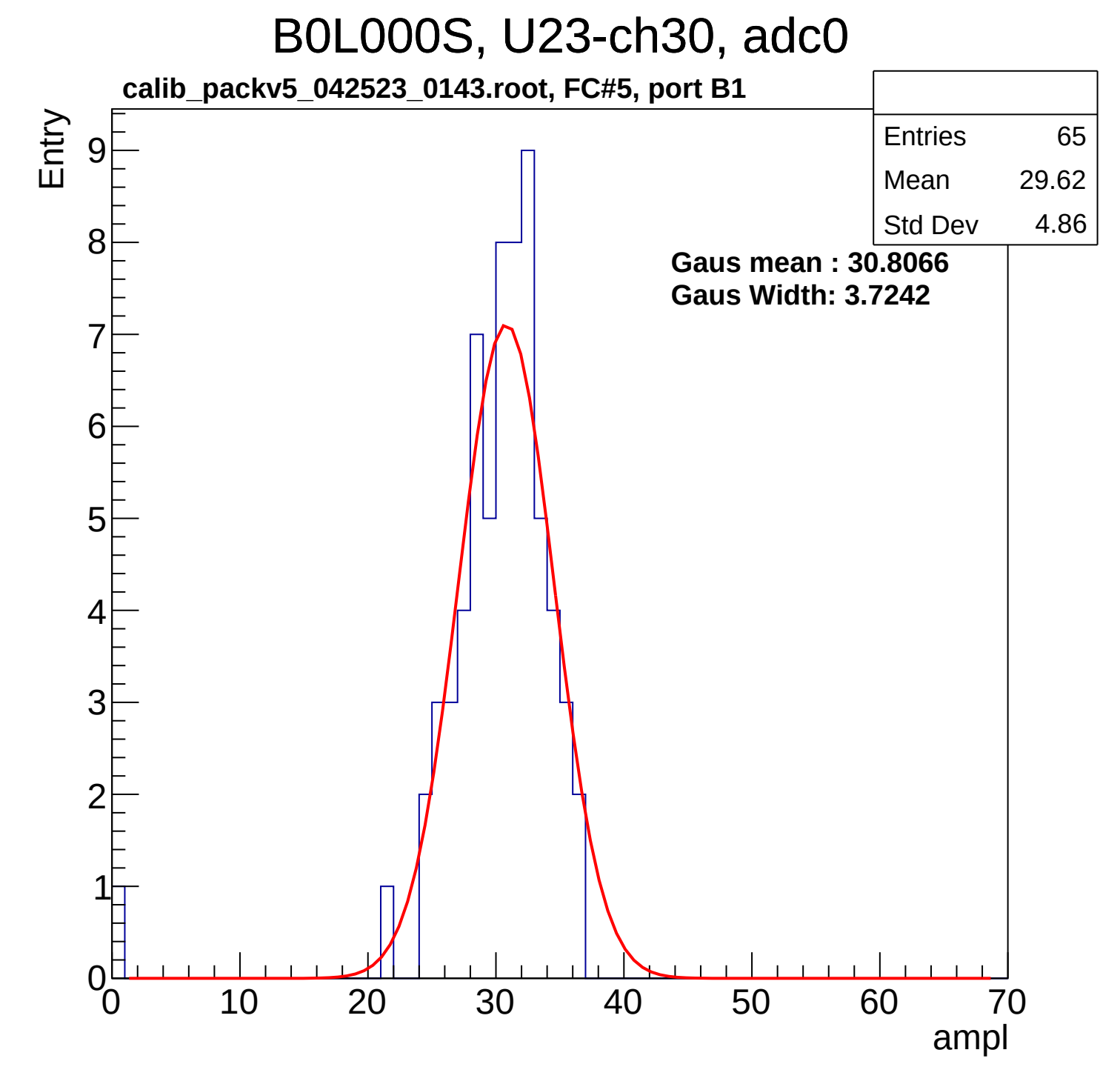

B0L000S, U23-ch29, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 49.33
Std Dev 3.433
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

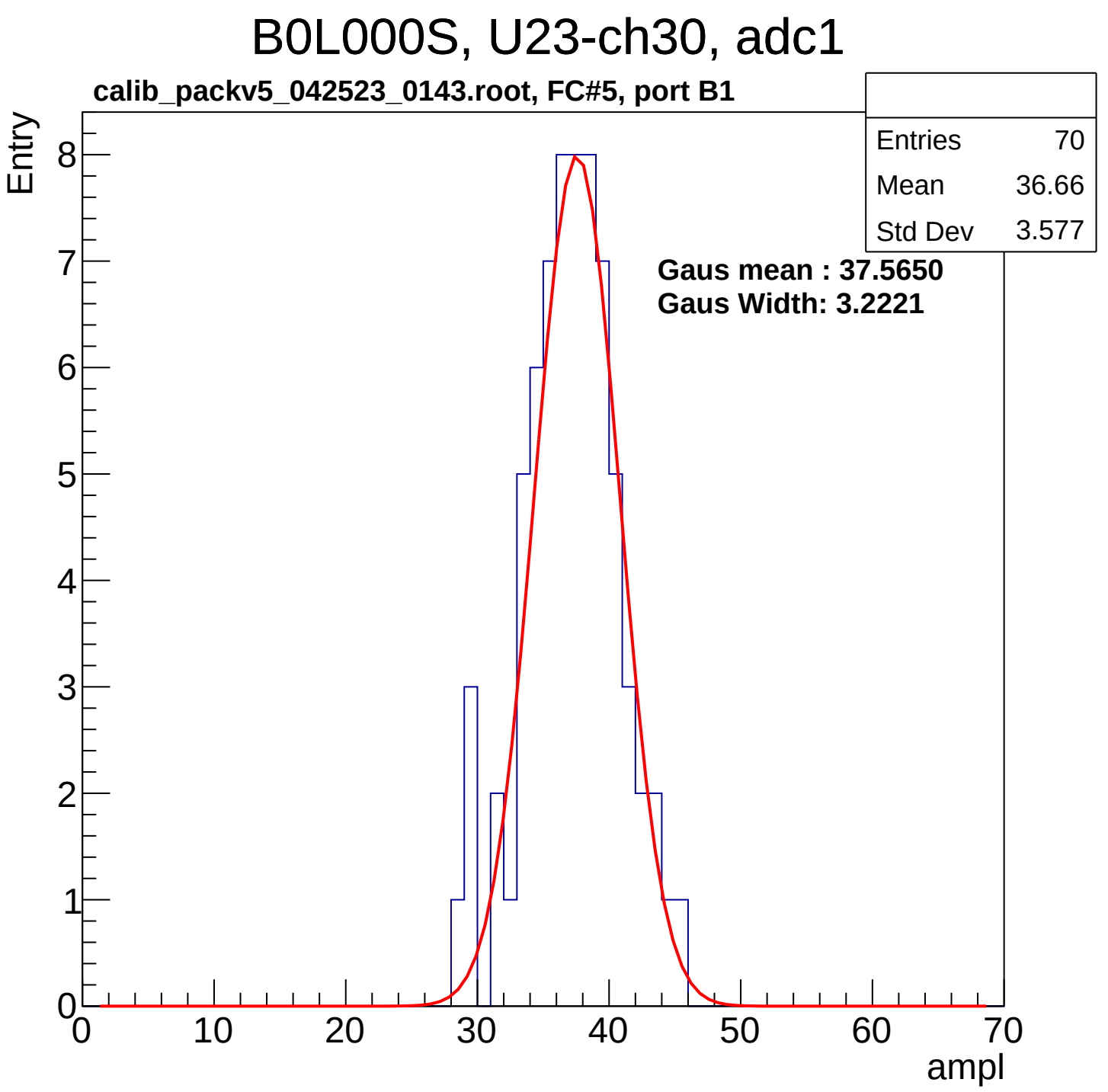

B0L000S, U23-ch29, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 56.19
Std Dev 3.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

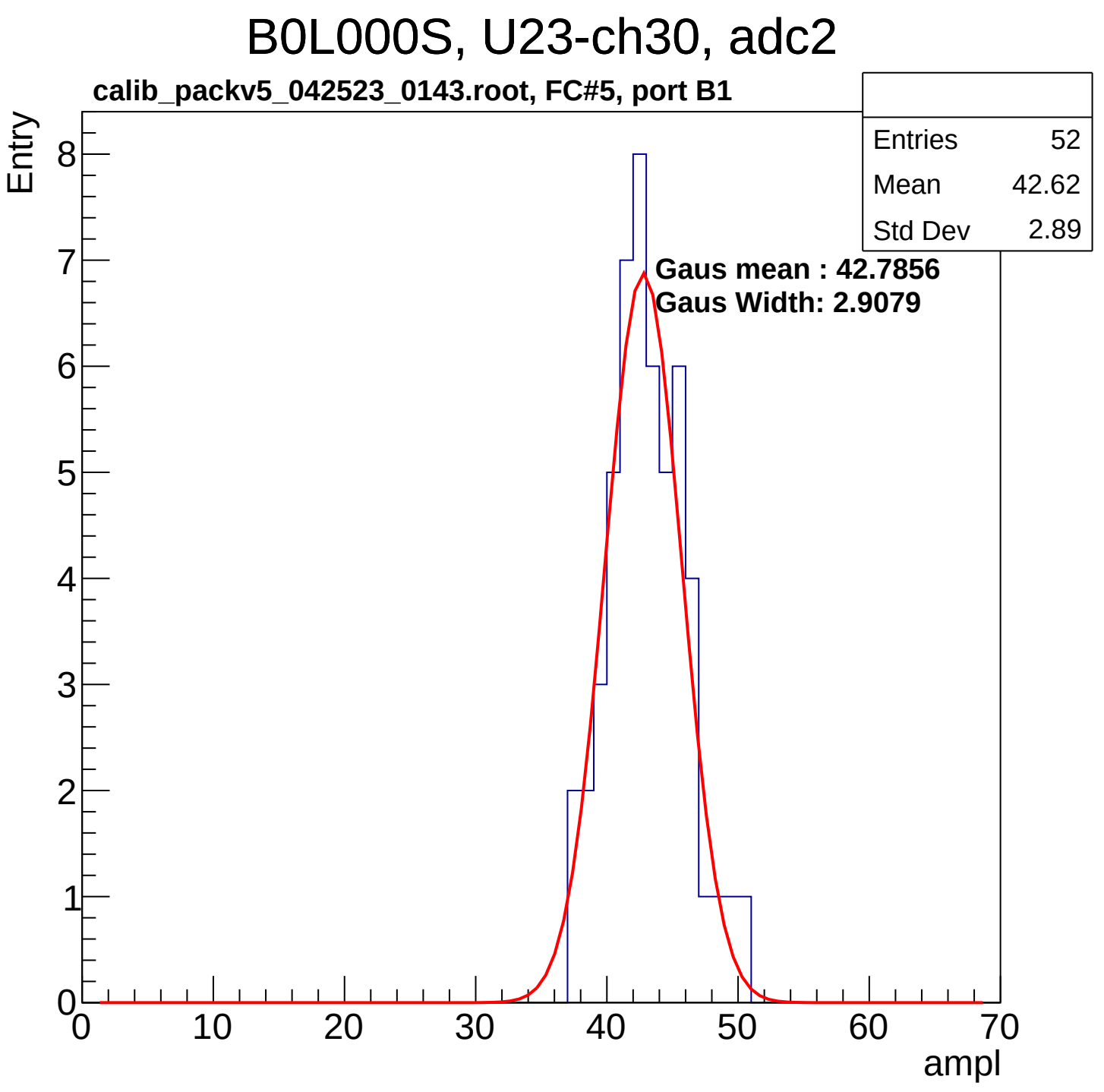

B0L000S, U23-ch29, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 61.04
Std Dev 1.822
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

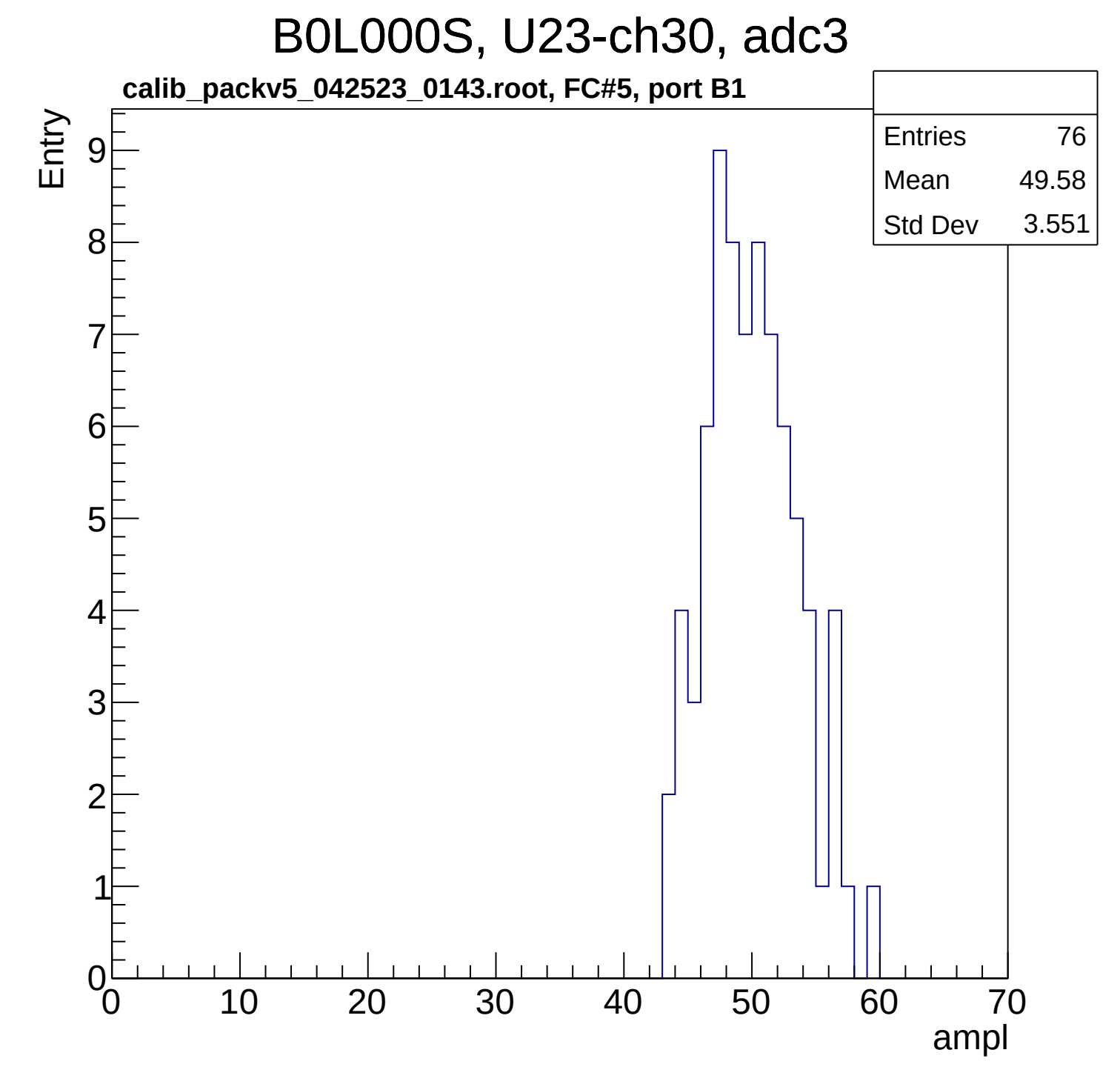

B0L000S, U23-ch29, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

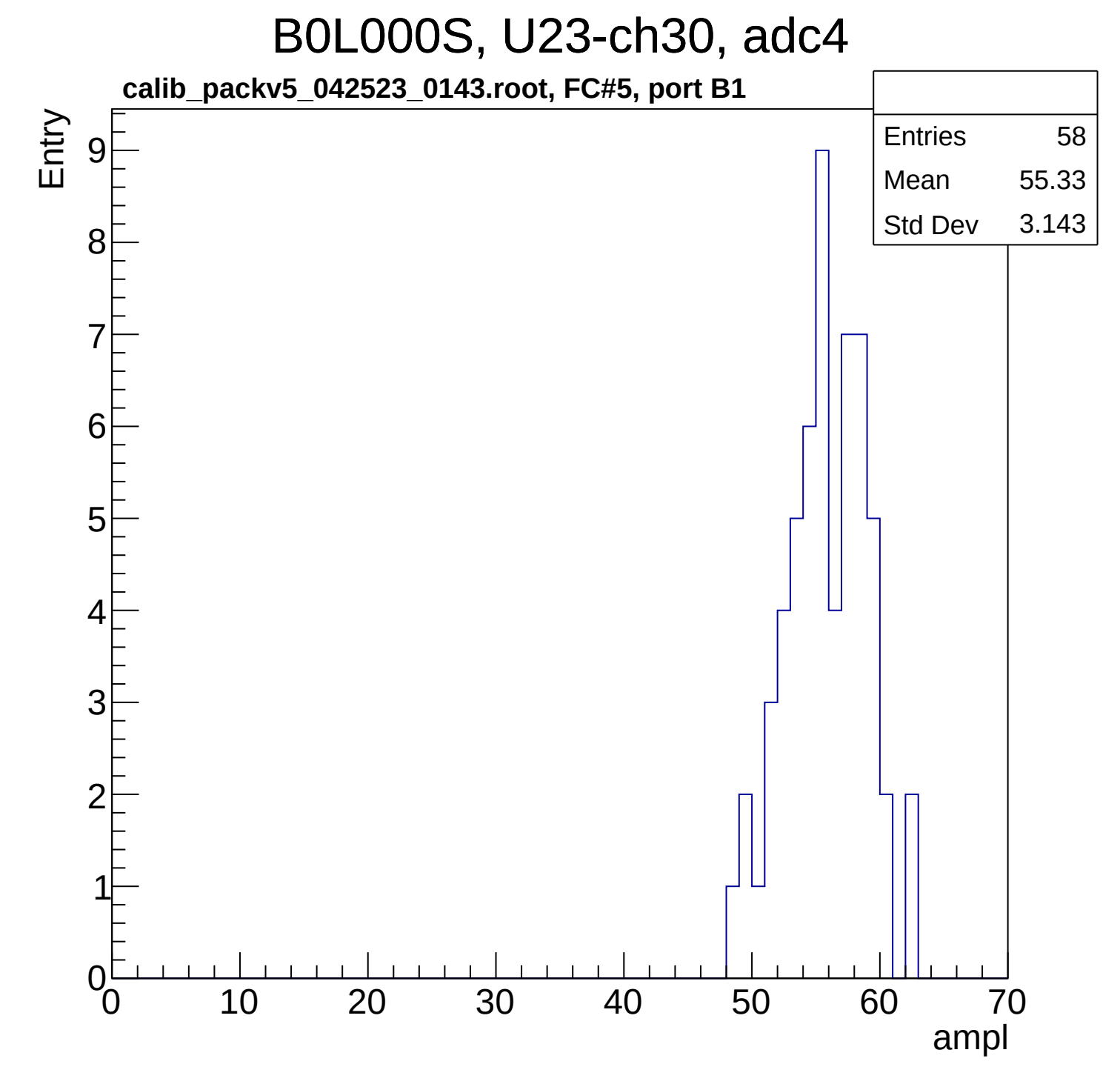

B0L000S, U23-ch29, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

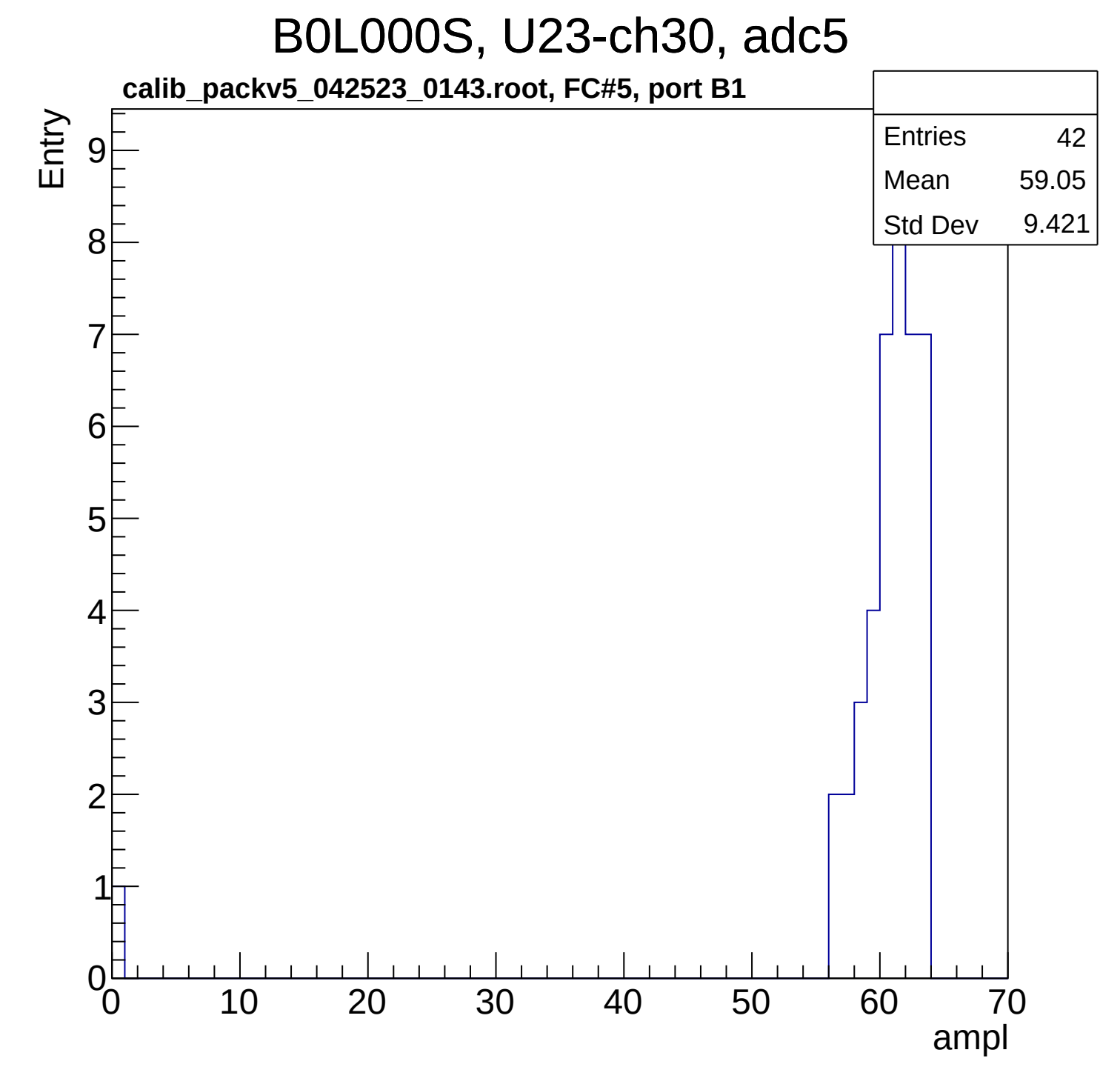

B0L000S, U23-ch30, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.62
Std Dev 4.86
Gaus mean : 30.8066
Gaus Width: 3.7242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

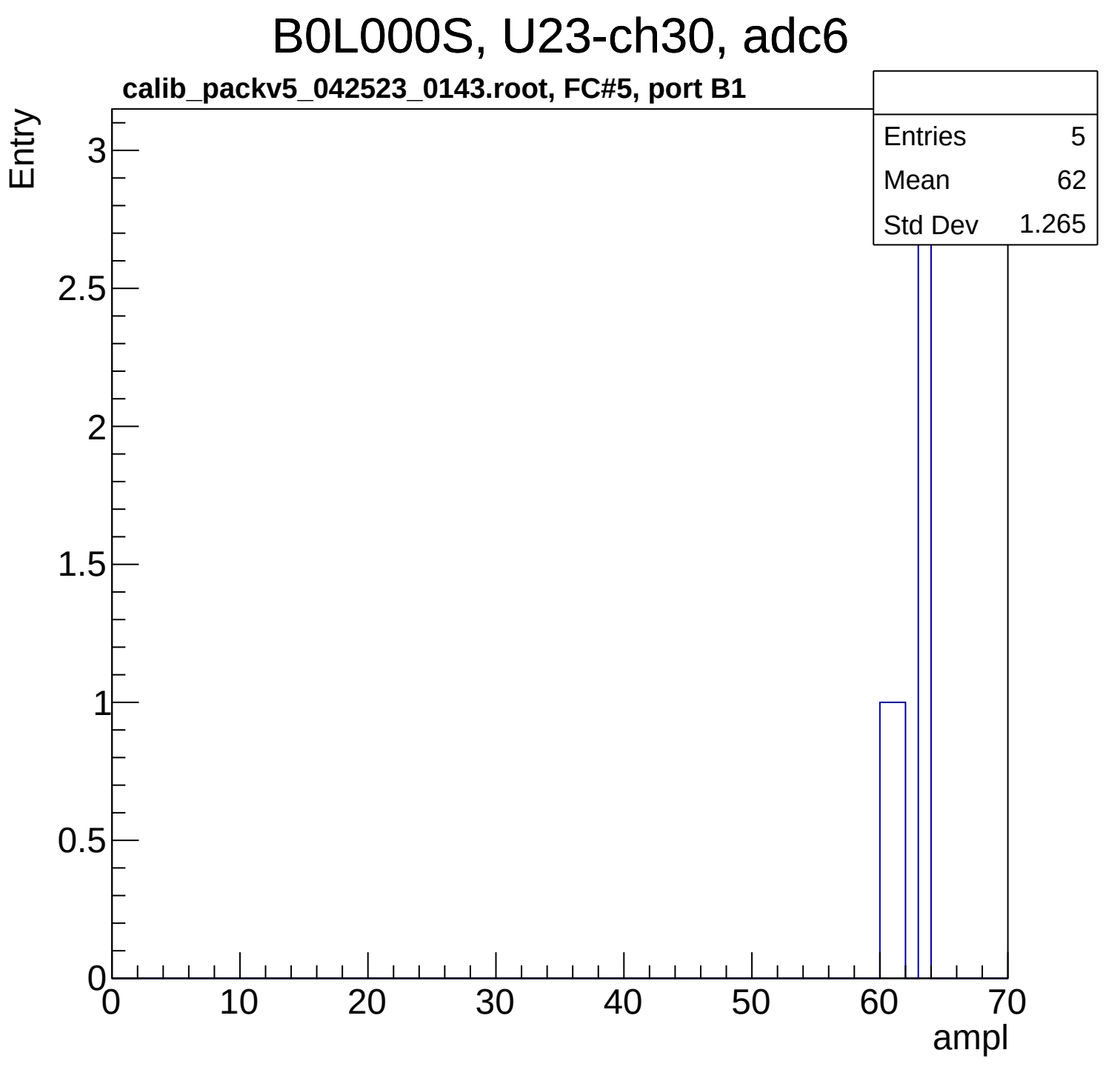

B0L000S, U23-ch30, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.66
Std Dev 3.577
Gaus mean : 37.5650
Gaus Width: 3.2221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

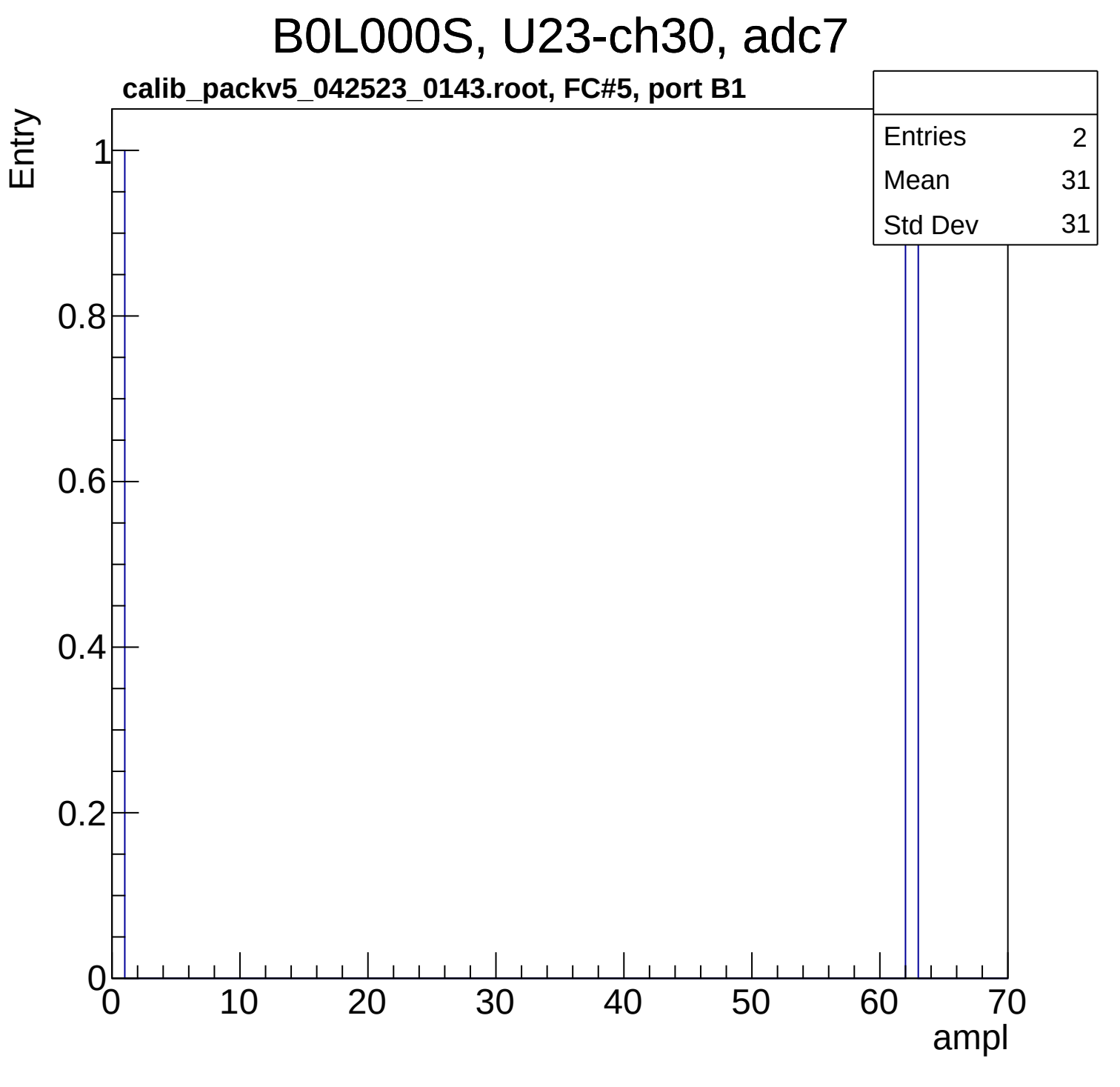

B0L000S, U23-ch30, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.62
Std Dev 2.89
Gaus mean : 42.7856
Gaus Width: 2.9079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

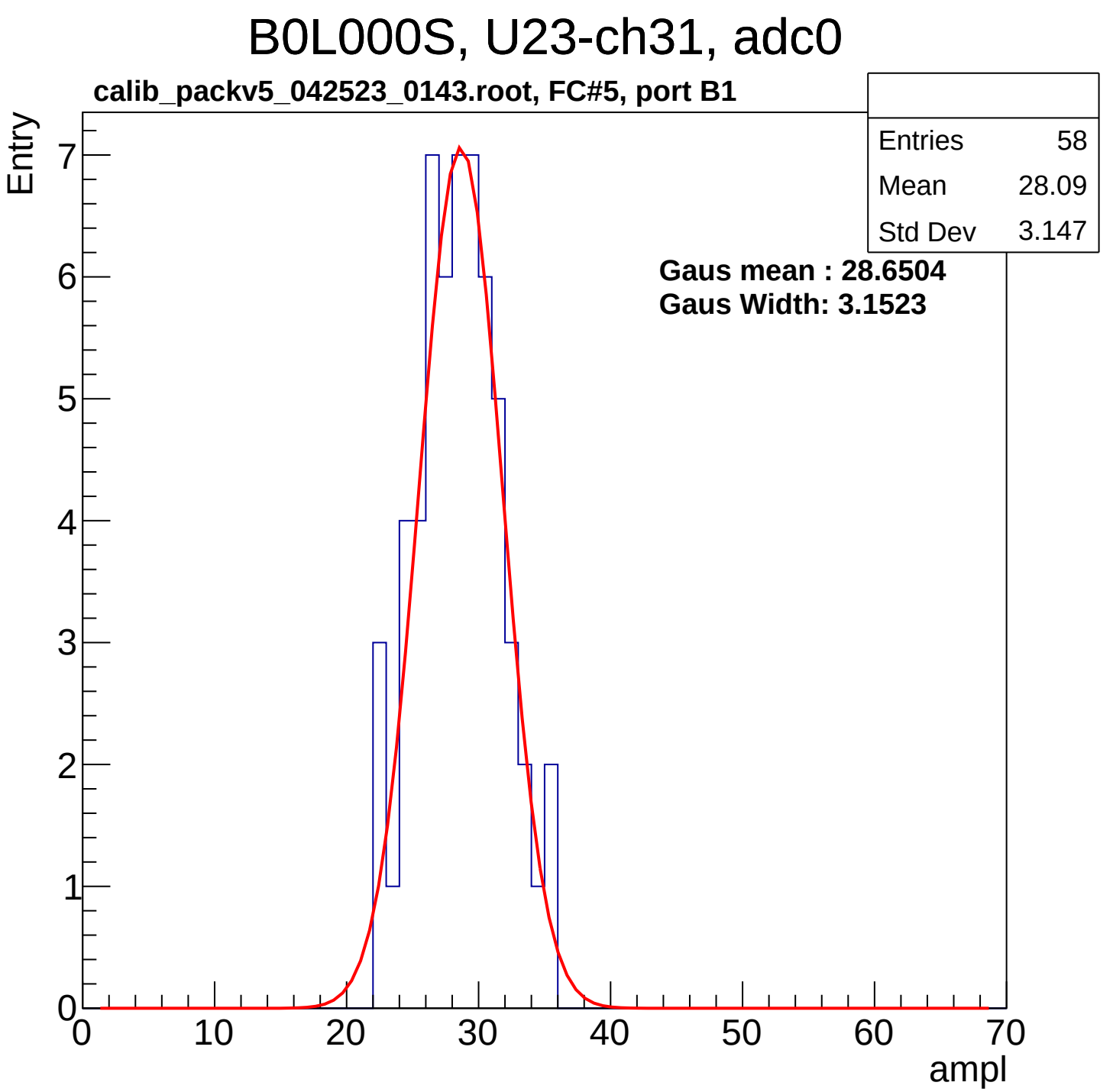

B0L000S, U23-ch30, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 49.58
Std Dev 3.551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

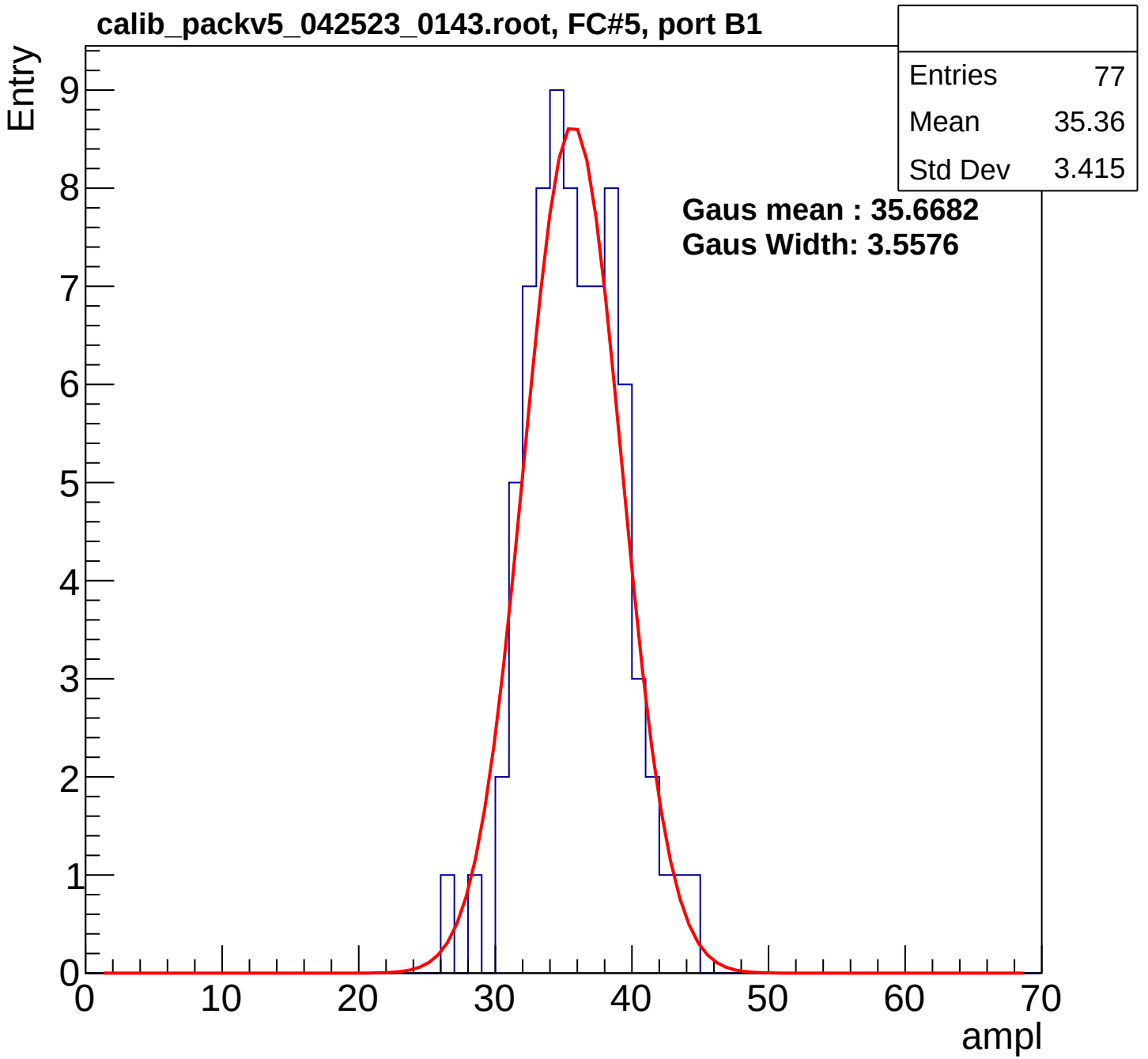

B0L000S, U23-ch30, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.33
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

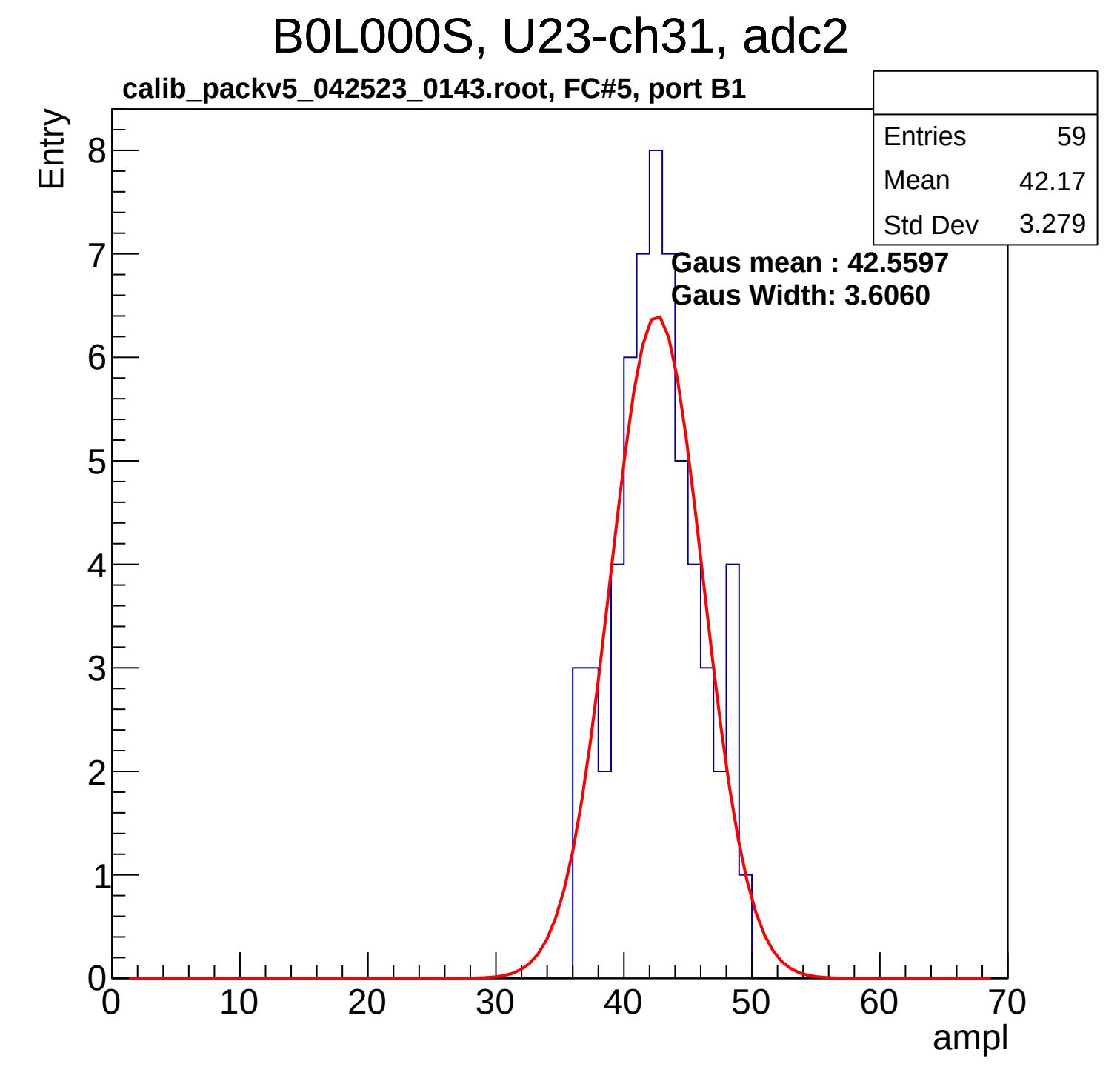

B0L000S, U23-ch30, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.05
Std Dev 9.421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

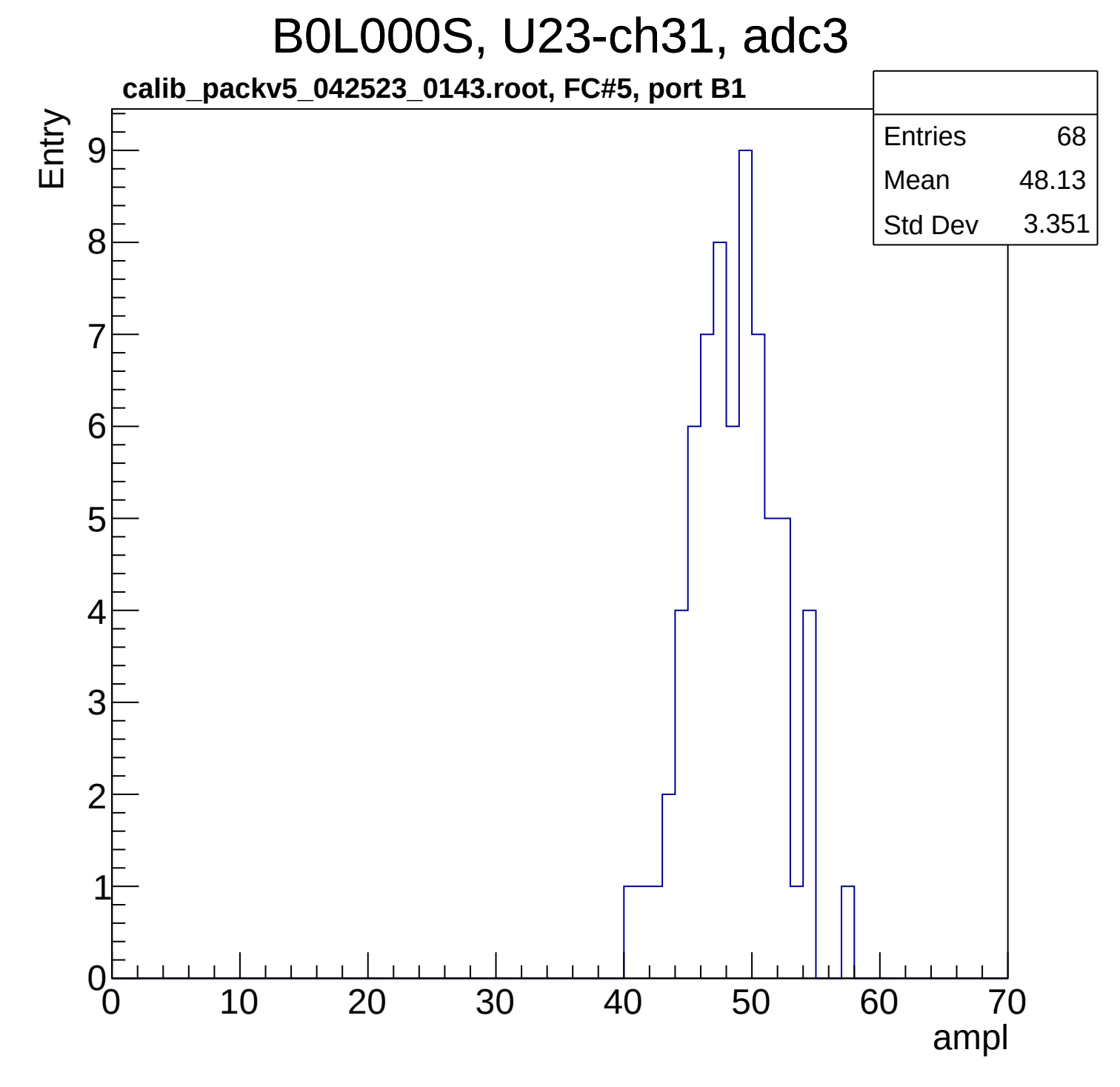

B0L000S, U23-ch30, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

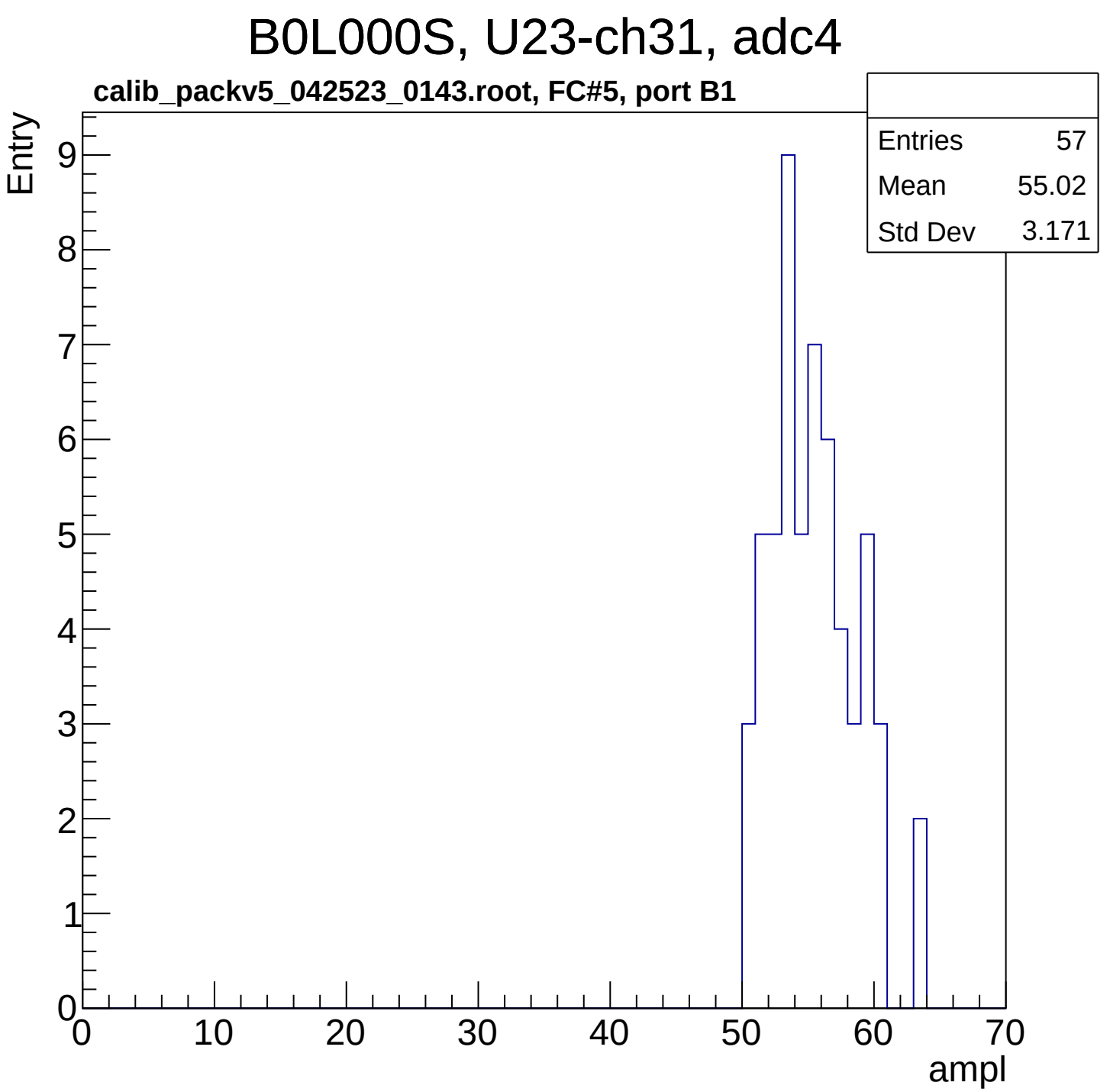

B0L000S, U23-ch30, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

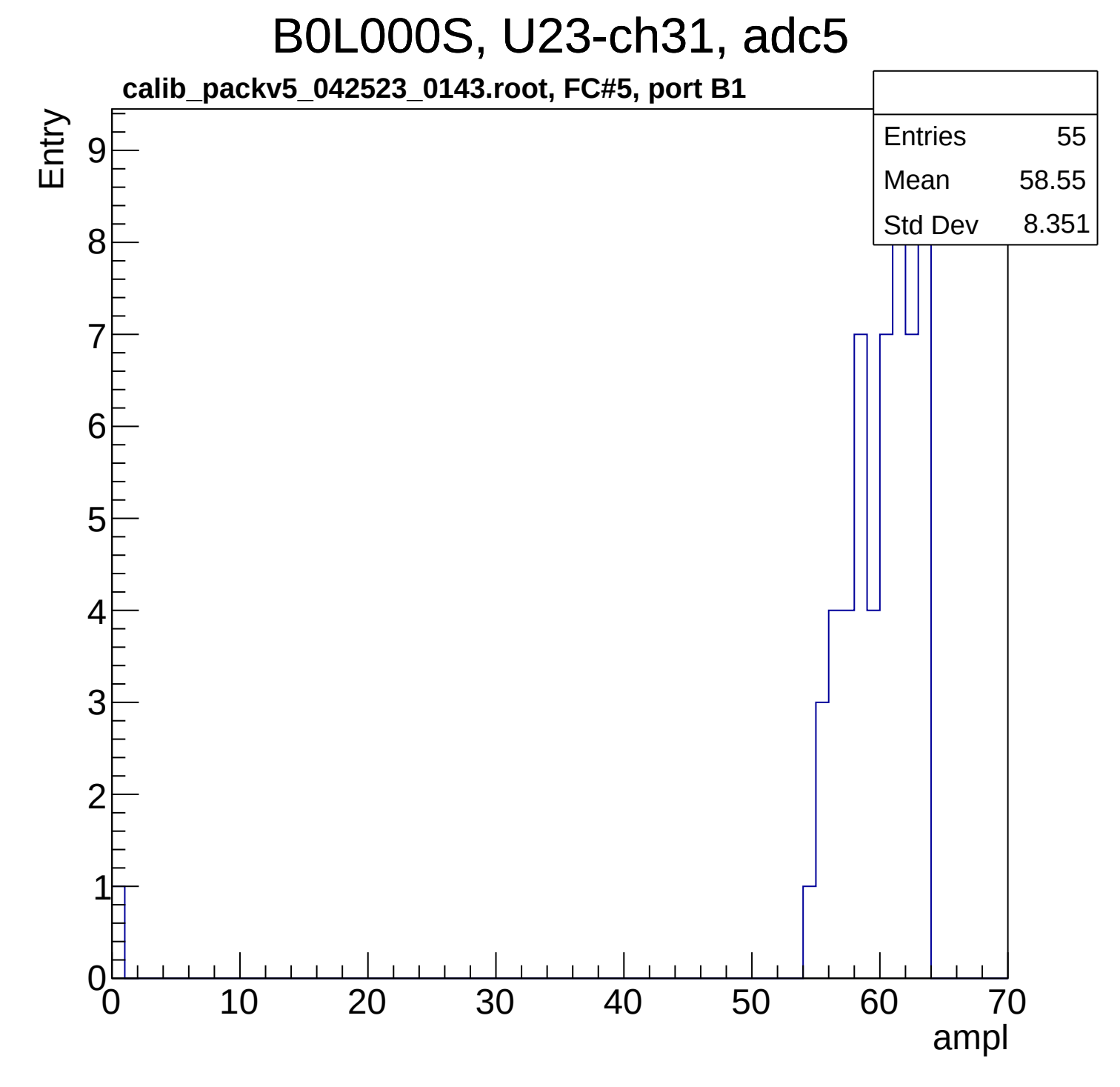

B0L000S, U23-ch31, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 28.09
Std Dev 3.147
Gaus mean : 28.6504
Gaus Width: 3.1523
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

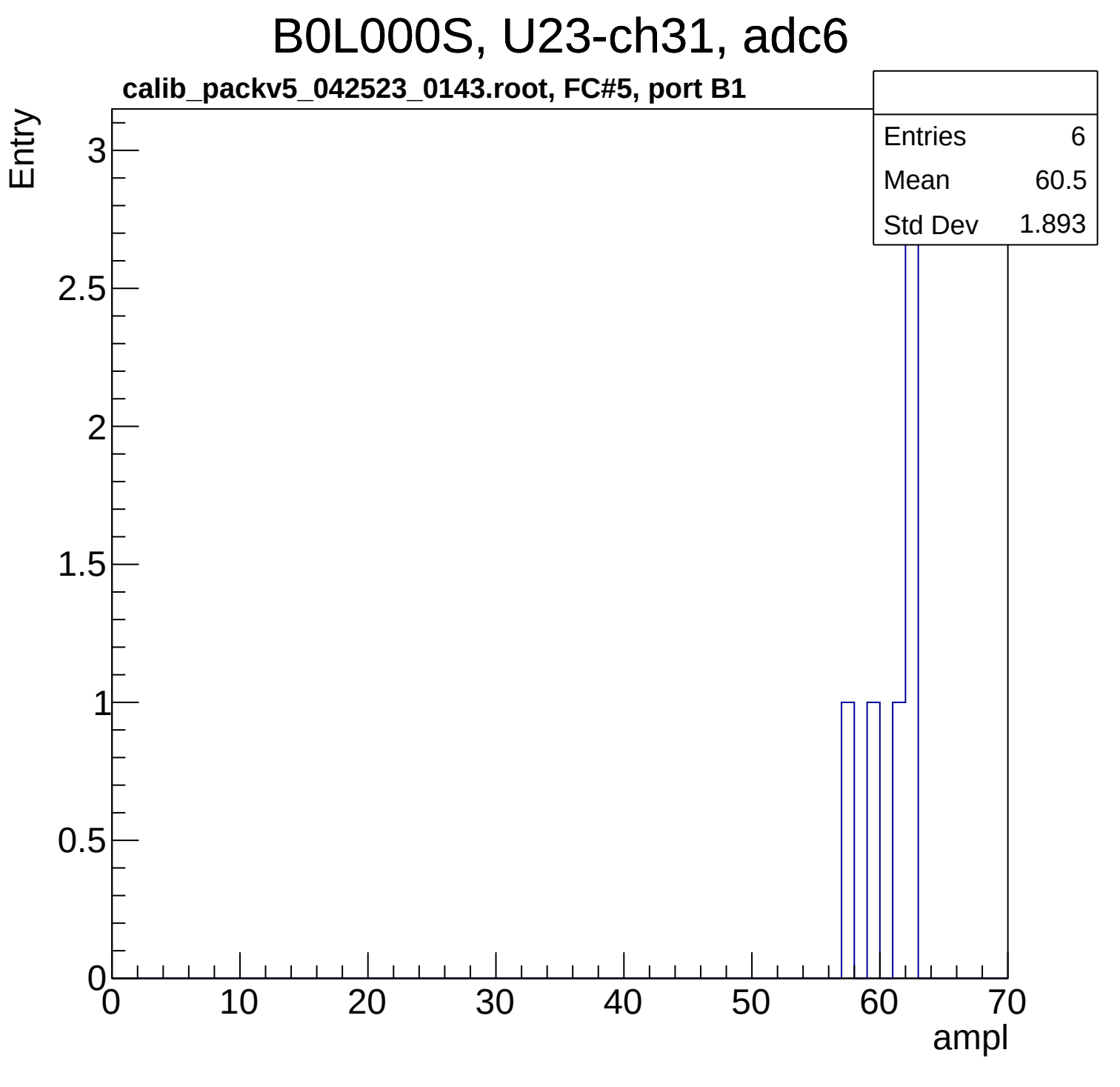

B0L000S, U23-ch31, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 35.36
Std Dev 3.415
Gaus mean : 35.6682
Gaus Width: 3.5576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

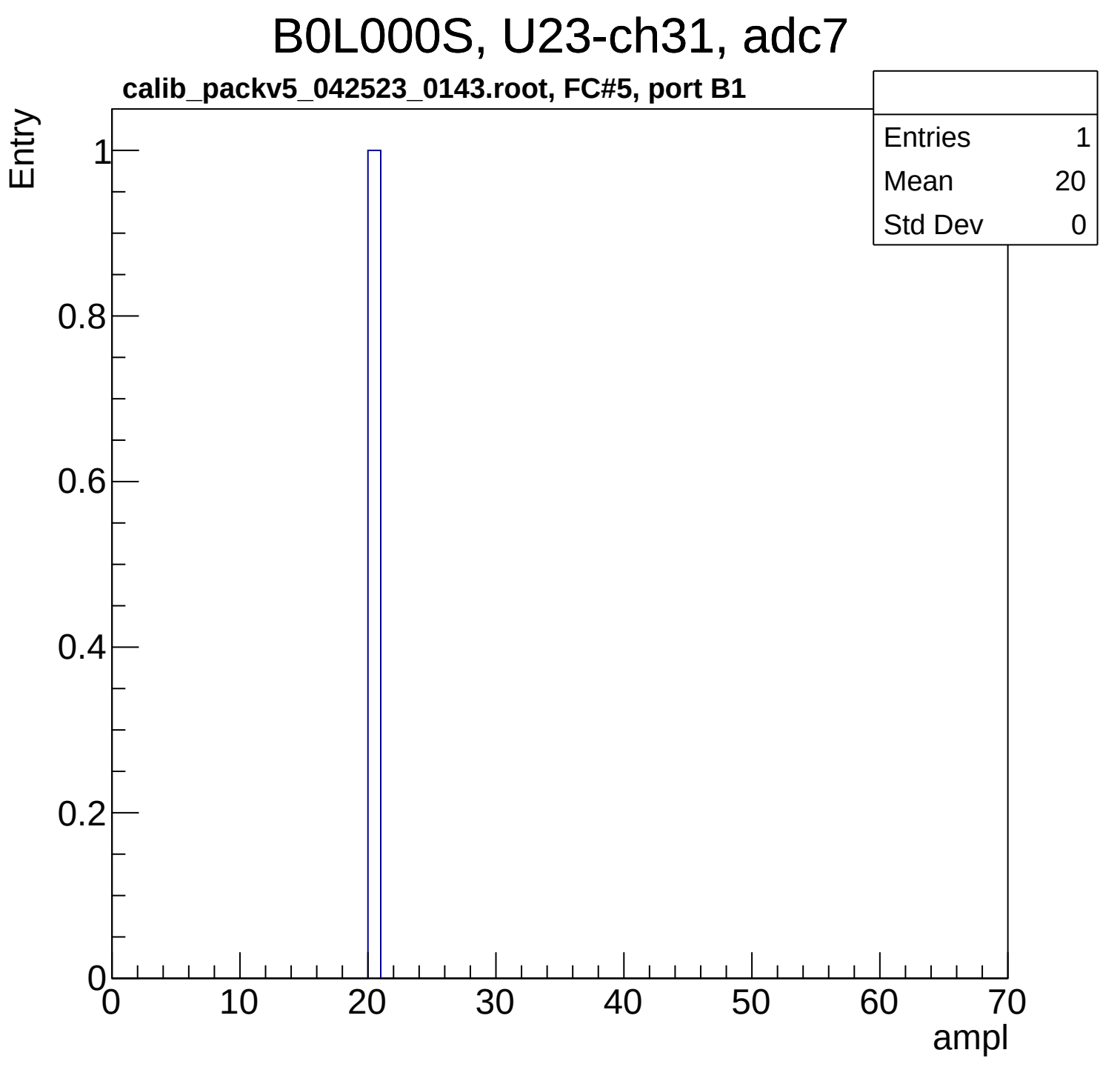

B0L000S, U23-ch31, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.17
Std Dev 3.279
Gaus mean : 42.5597
Gaus Width: 3.6060
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

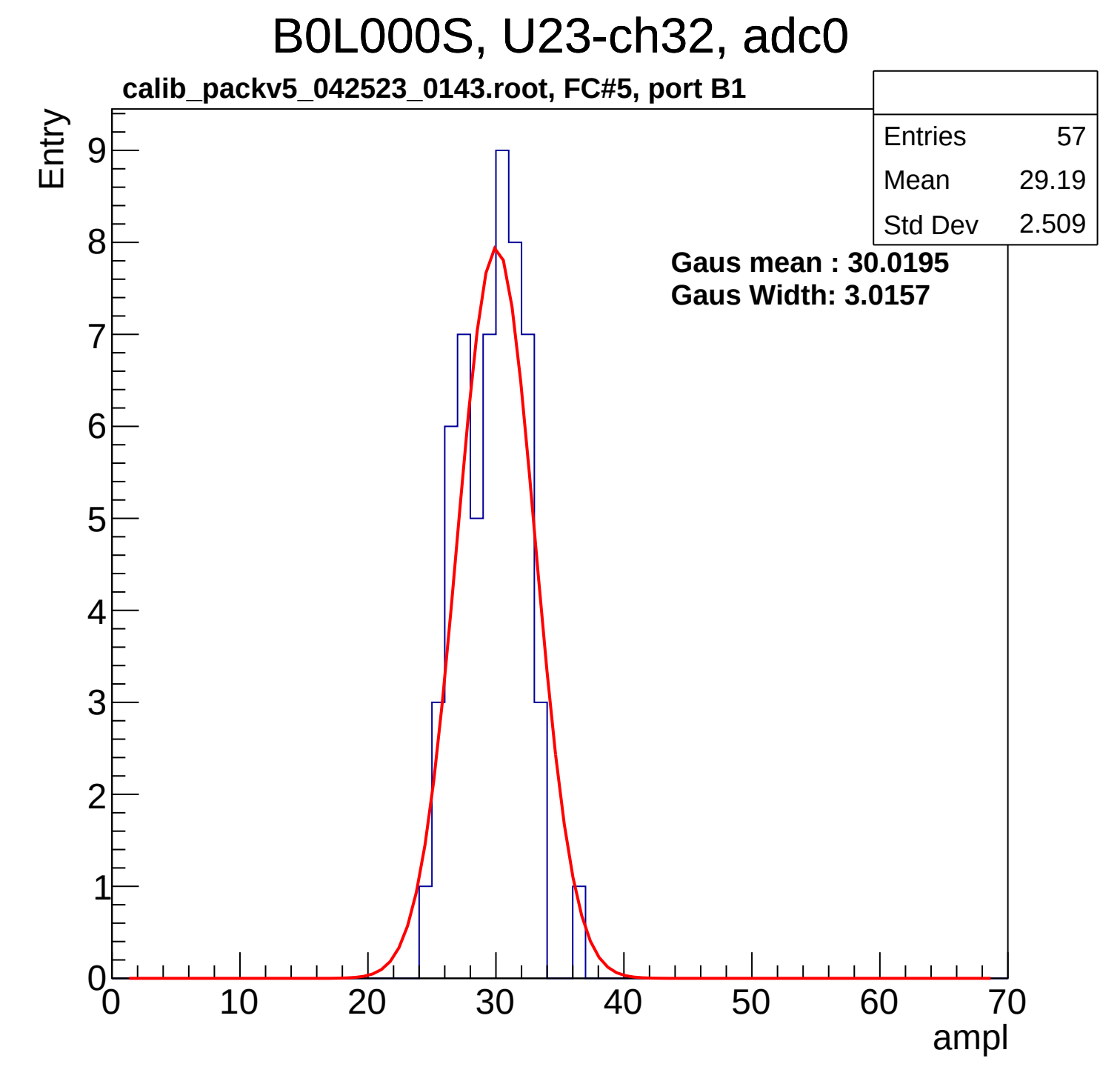

B0L000S, U23-ch31, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.13
Std Dev 3.351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

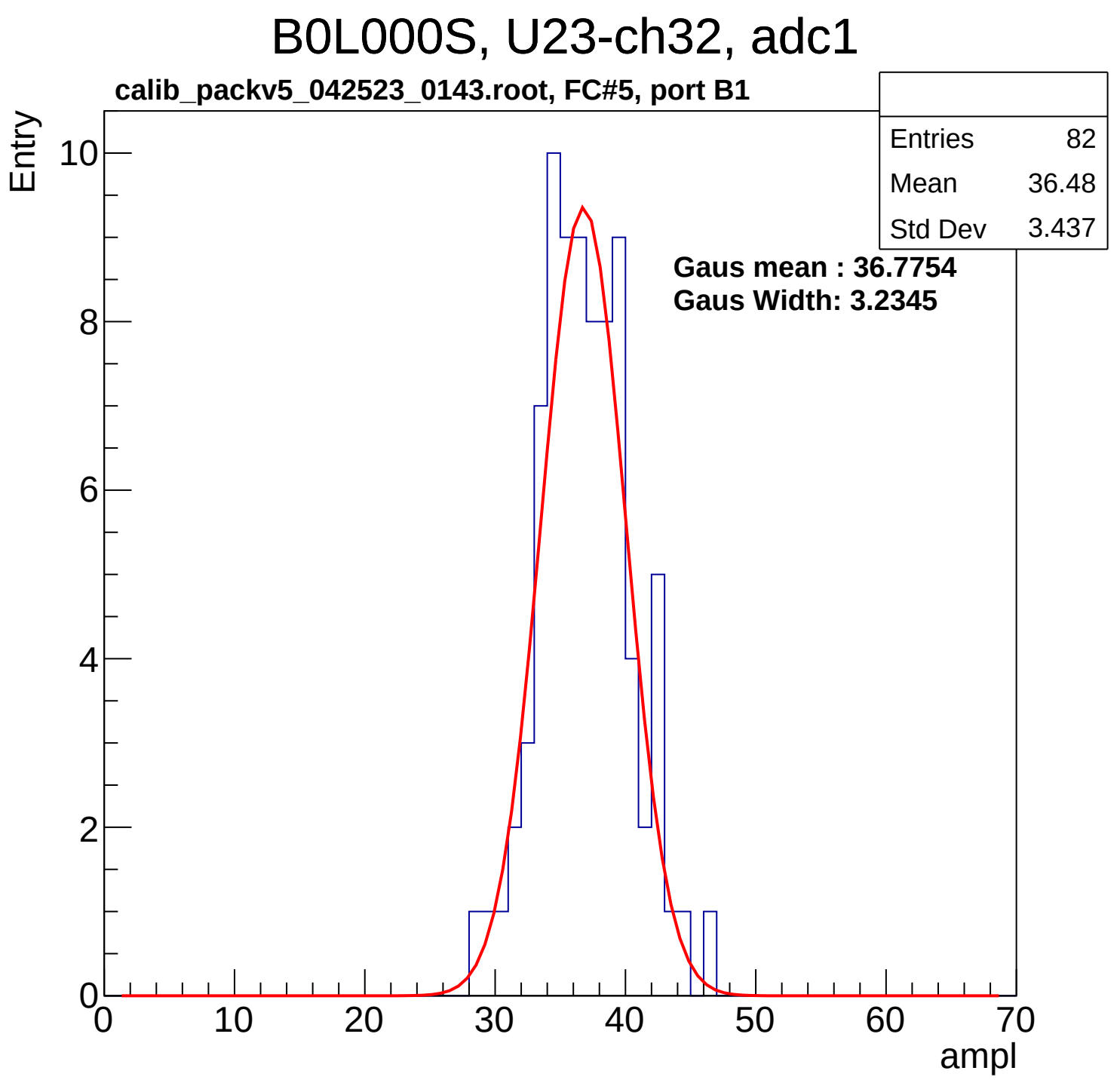

B0L000S, U23-ch31, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.02
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

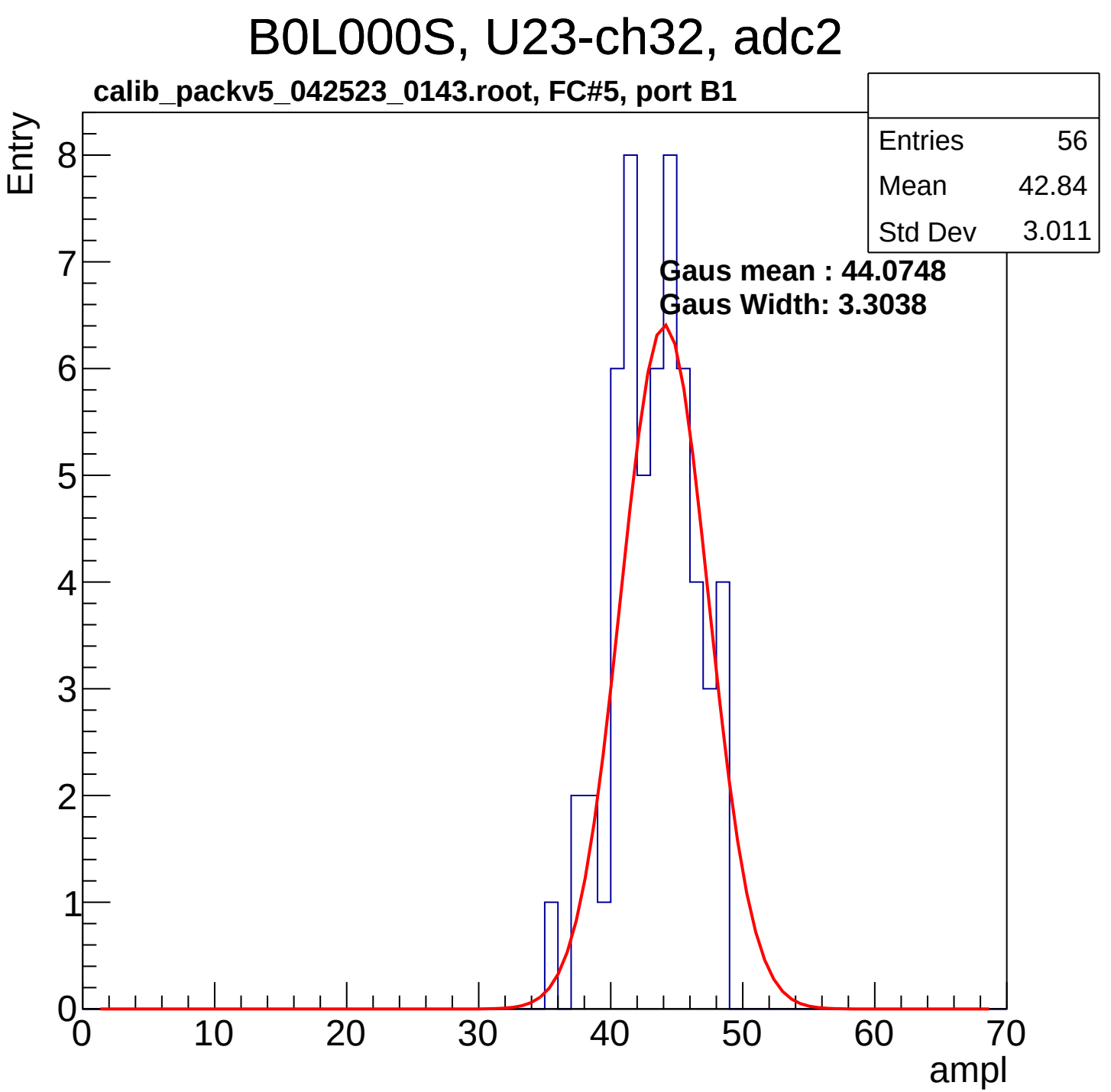

B0L000S, U23-ch31, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.55
Std Dev 8.351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

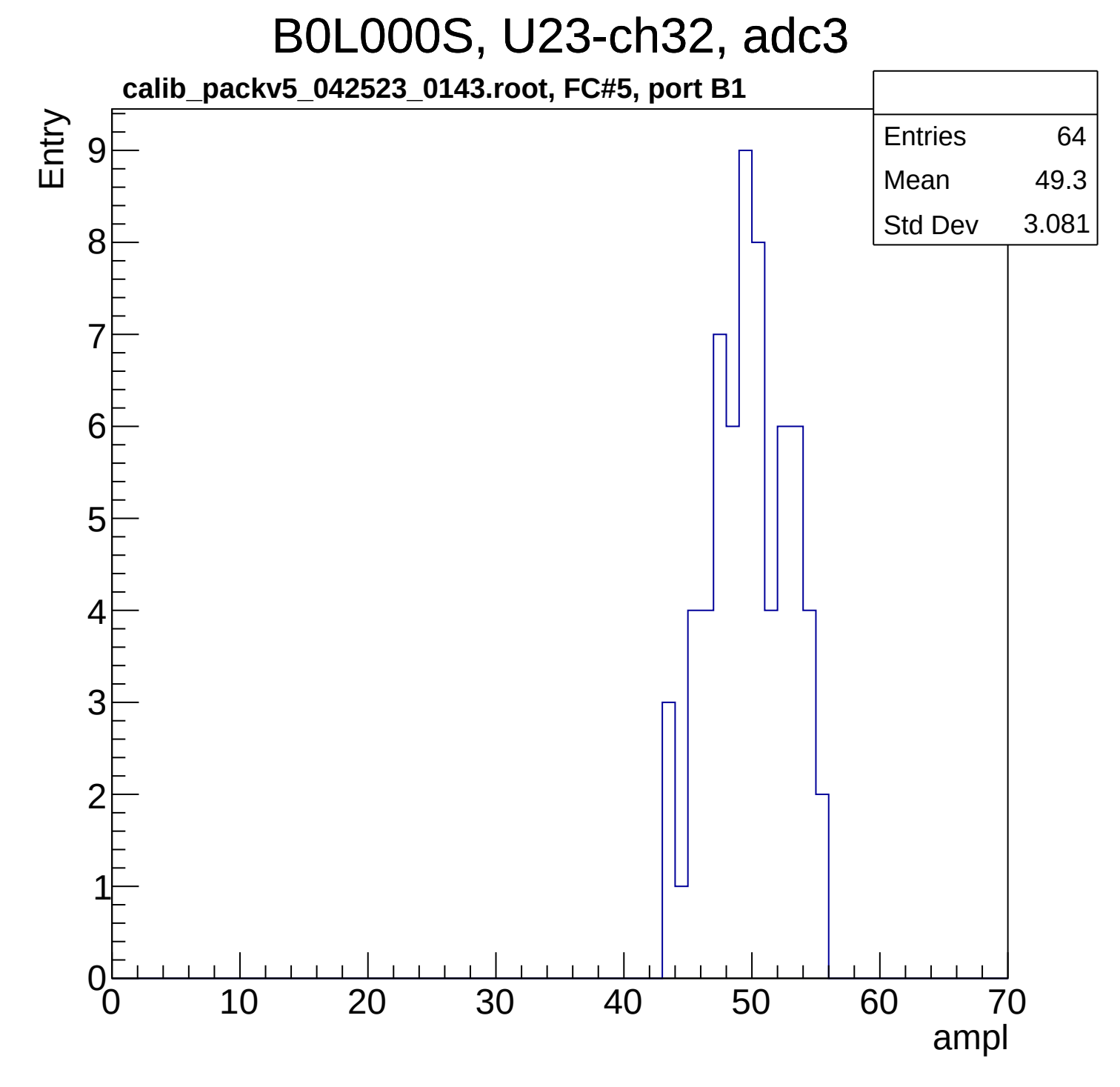

B0L000S, U23-ch31, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 60.5
Std Dev 1.893
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

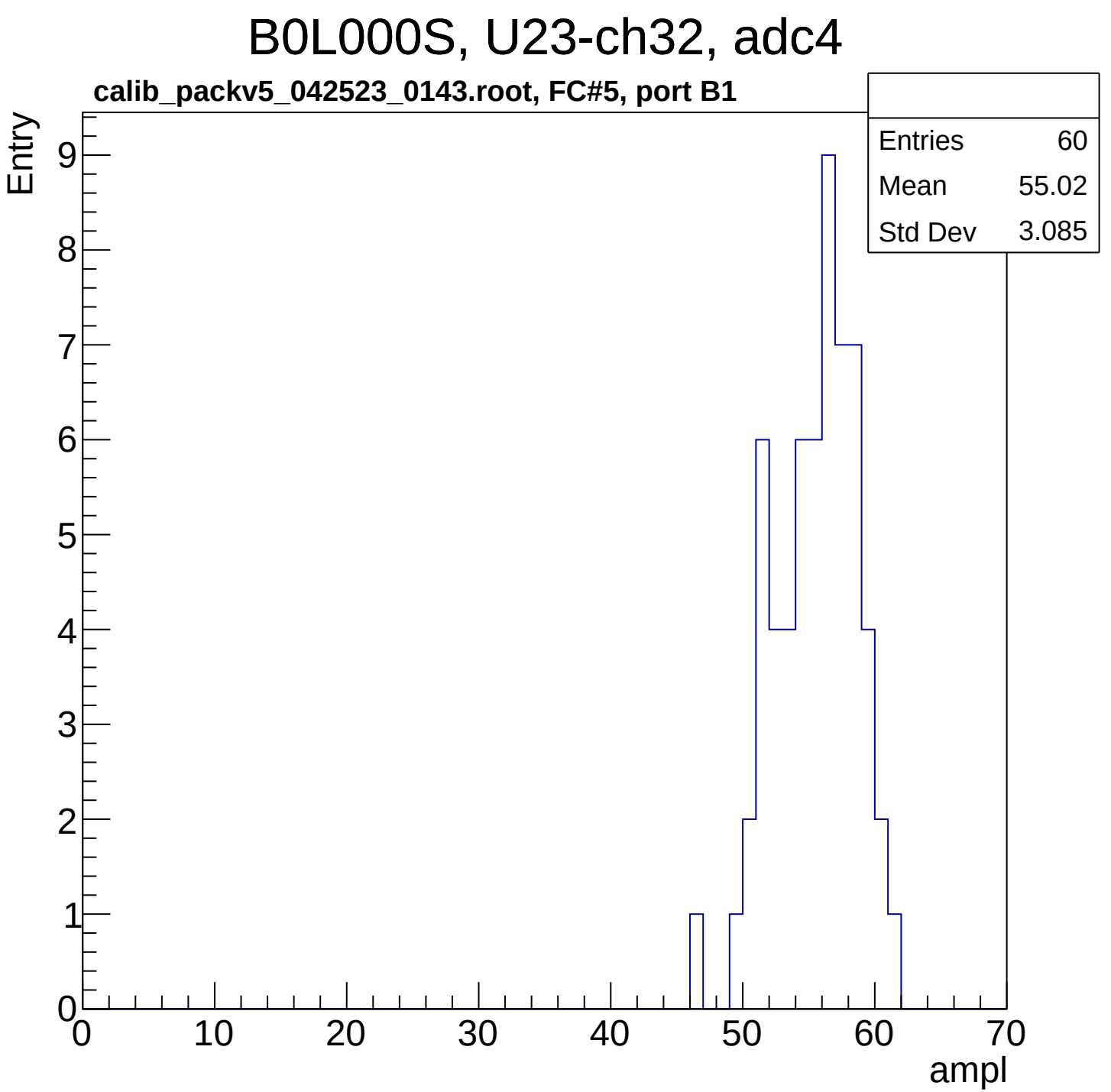

B0L000S, U23-ch31, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

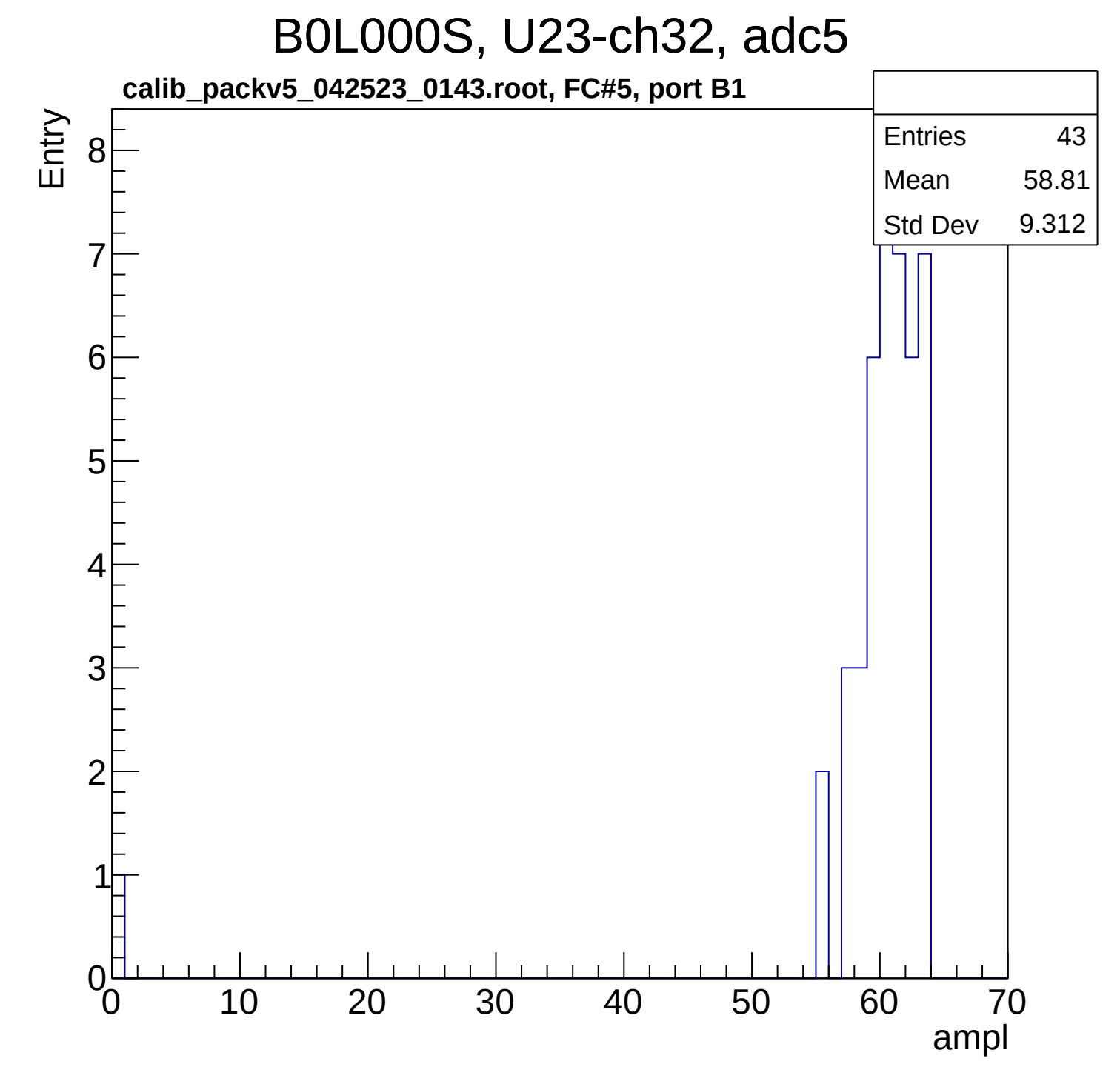

B0L000S, U23-ch32, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 29.19
Std Dev 2.509
Gaus mean : 30.0195
Gaus Width: 3.0157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

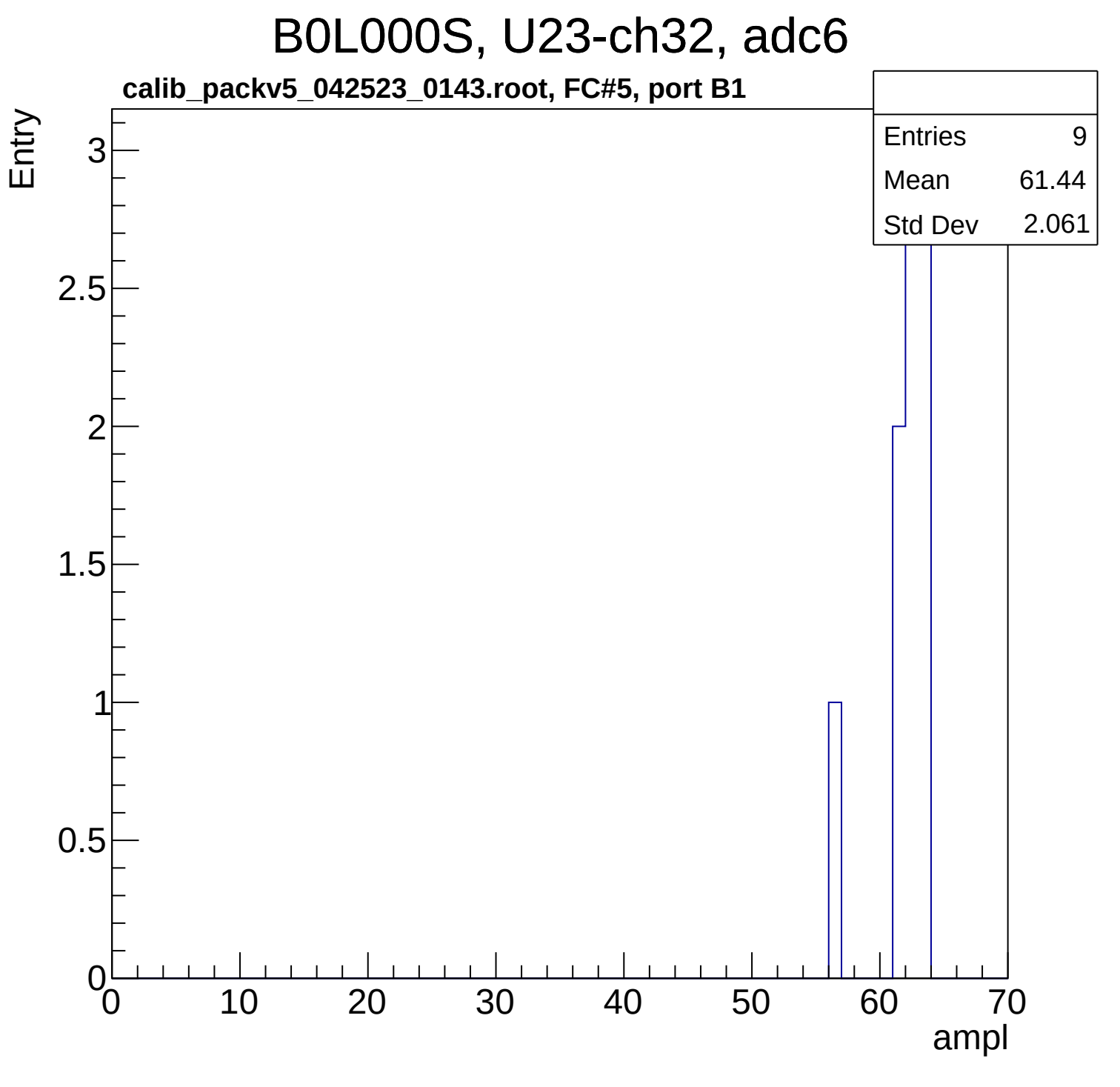

B0L000S, U23-ch32, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 36.48
Std Dev 3.437
Gaus mean : 36.7754
Gaus Width: 3.2345
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

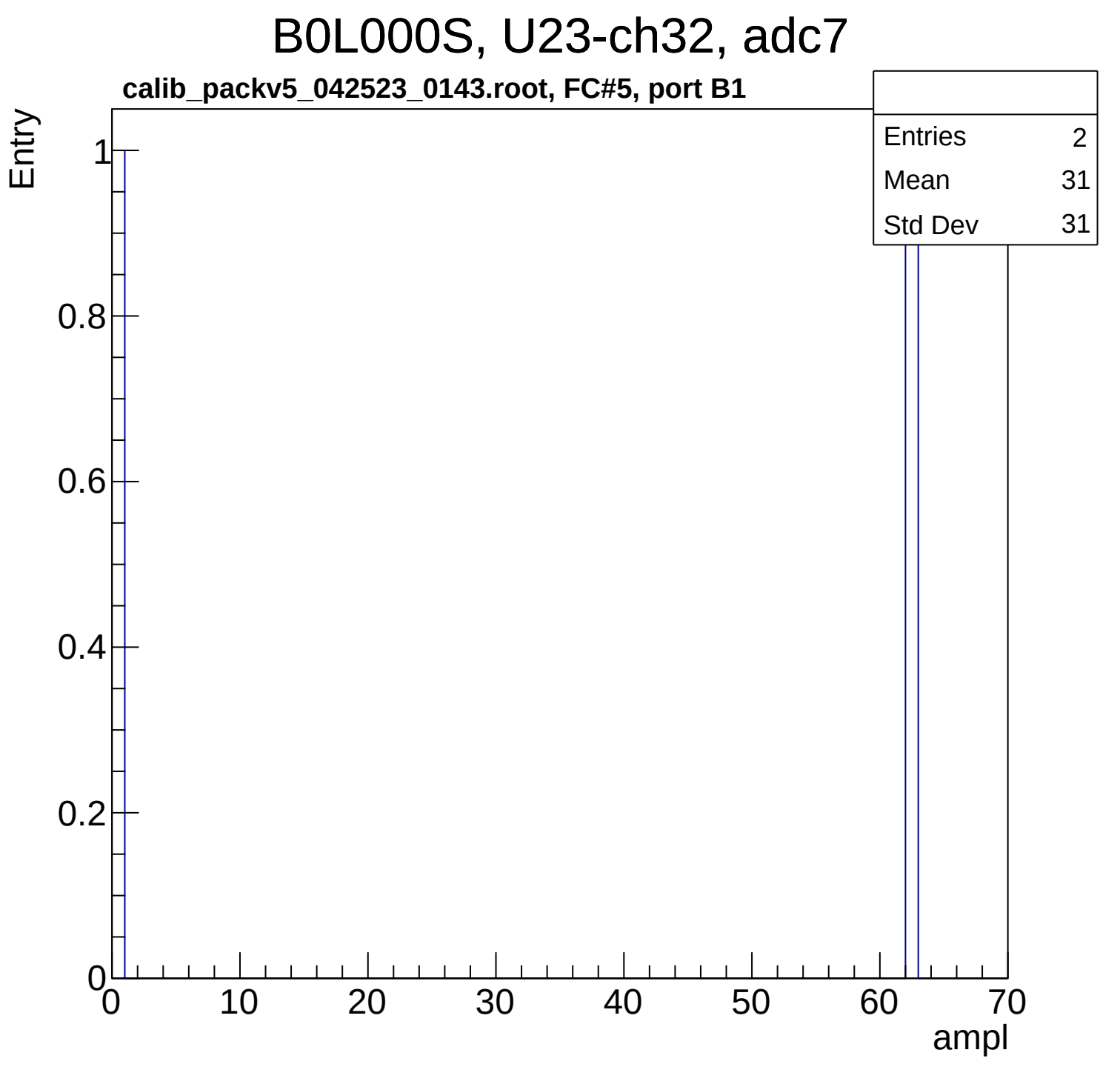

B0L000S, U23-ch32, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.84
Std Dev 3.011
Gaus mean : 44.0748
Gaus Width: 3.3038
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

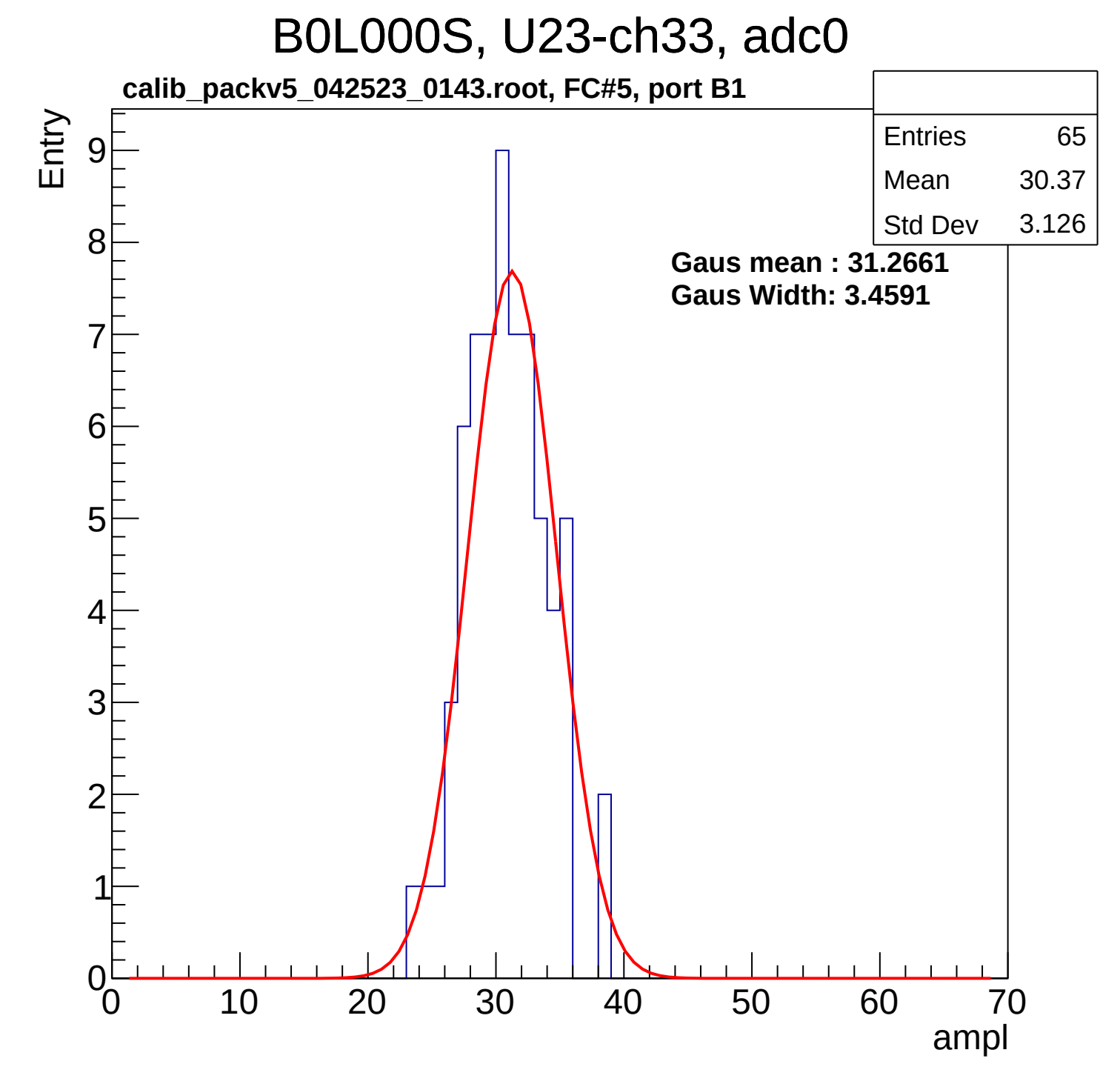

B0L000S, U23-ch32, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.3
Std Dev 3.081
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

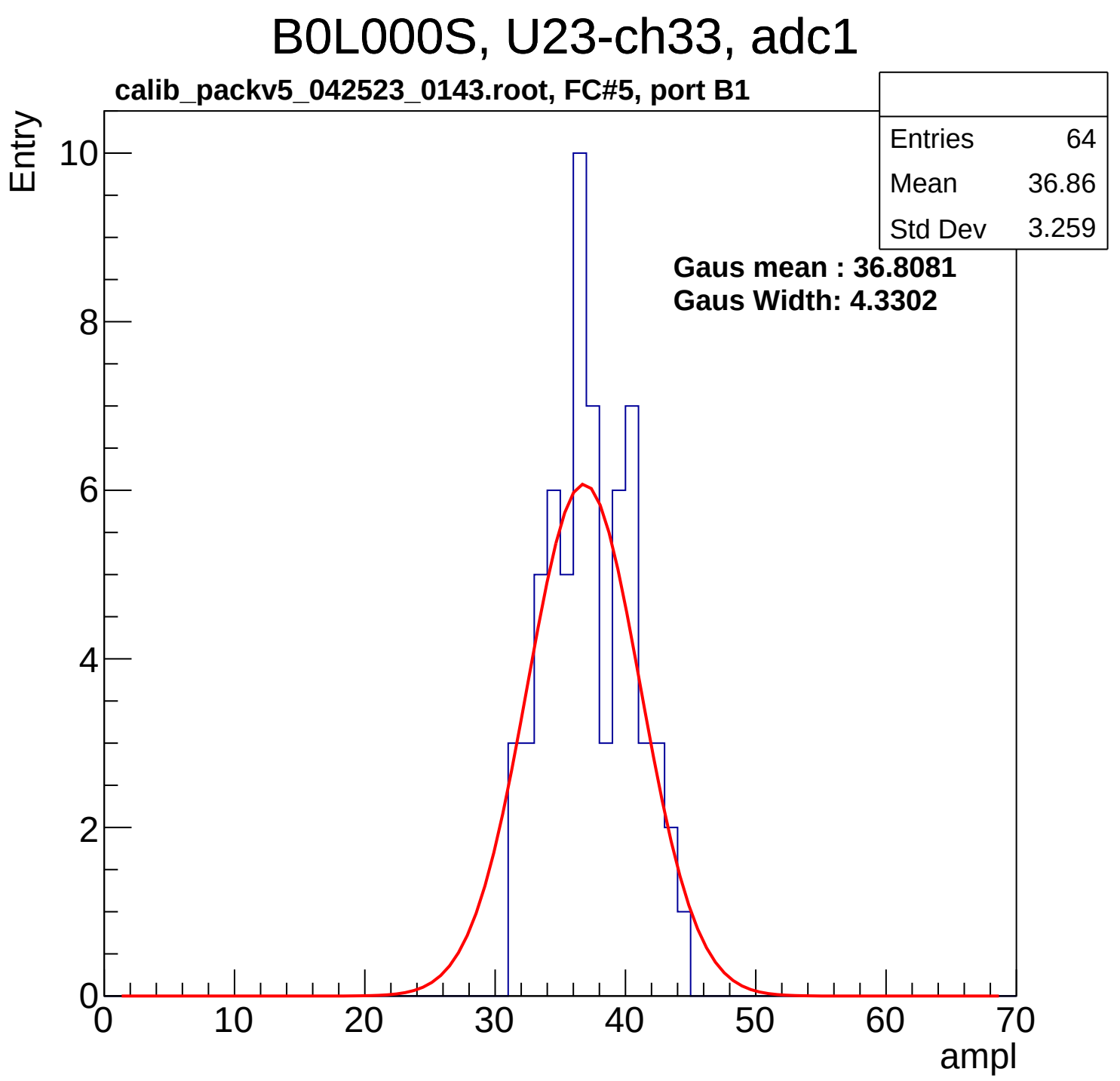

B0L000S, U23-ch32, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 55.02
Std Dev 3.085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

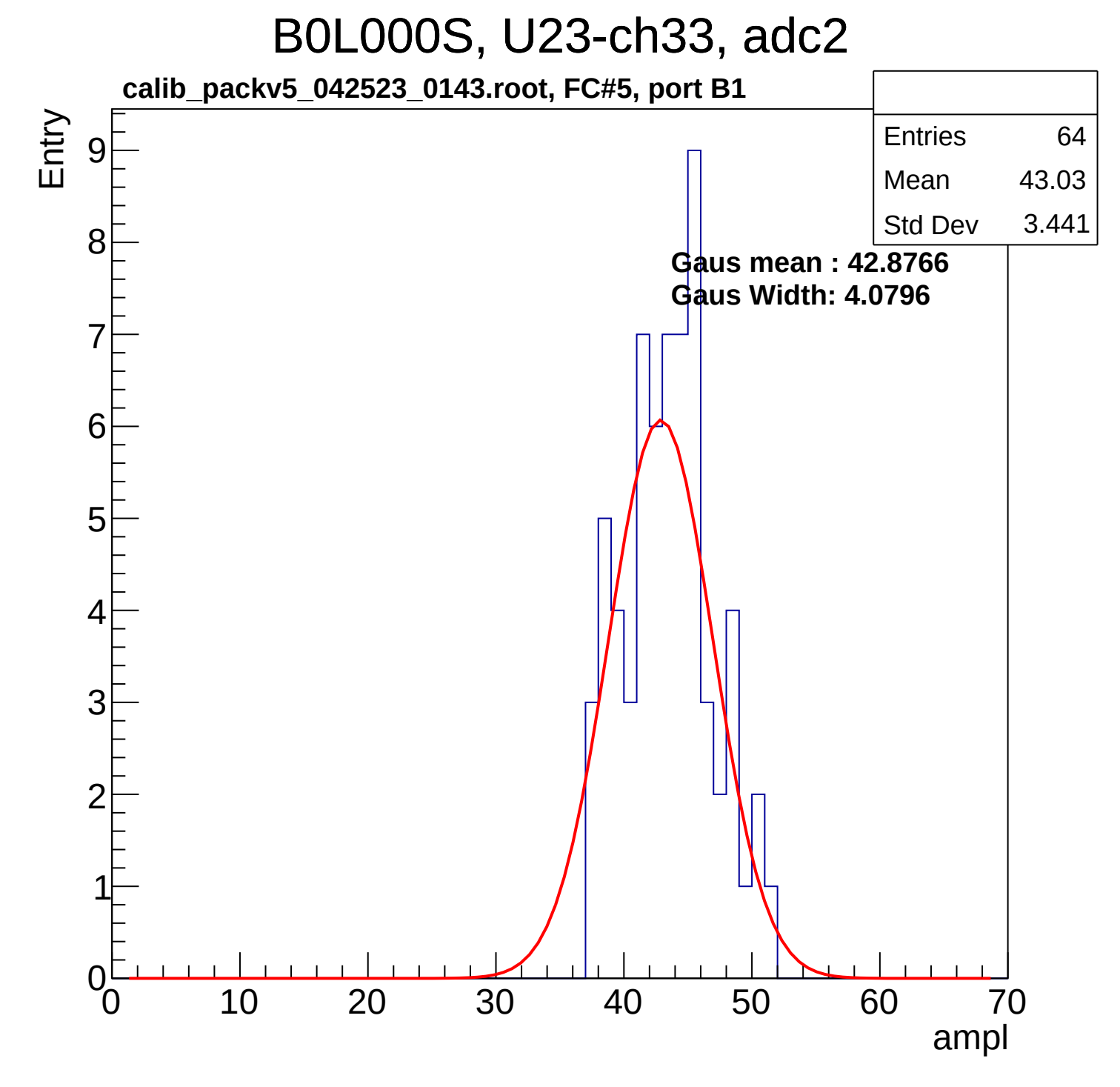

B0L000S, U23-ch32, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.81
Std Dev 9.312
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

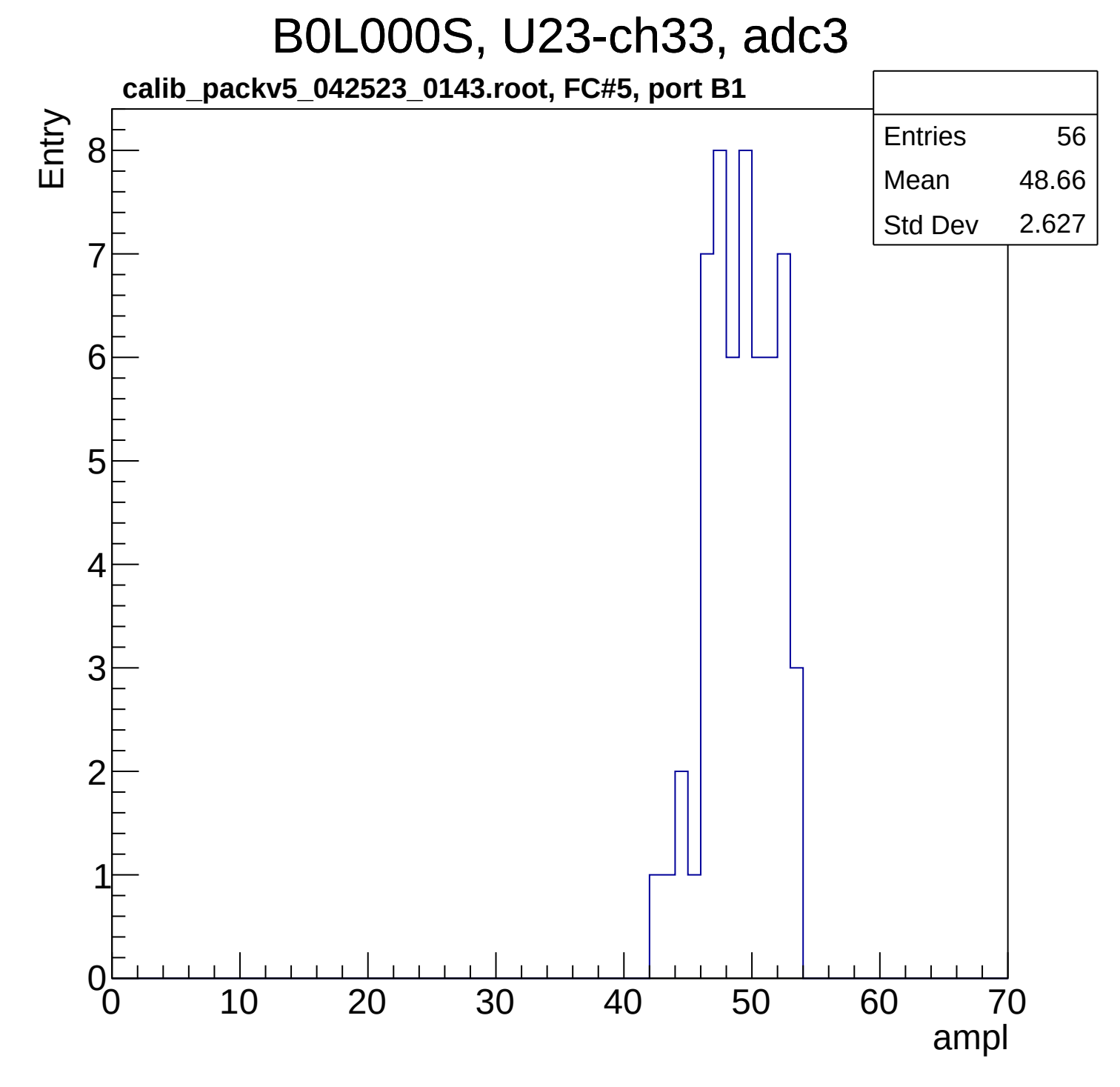

B0L000S, U23-ch32, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.44
Std Dev 2.061
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

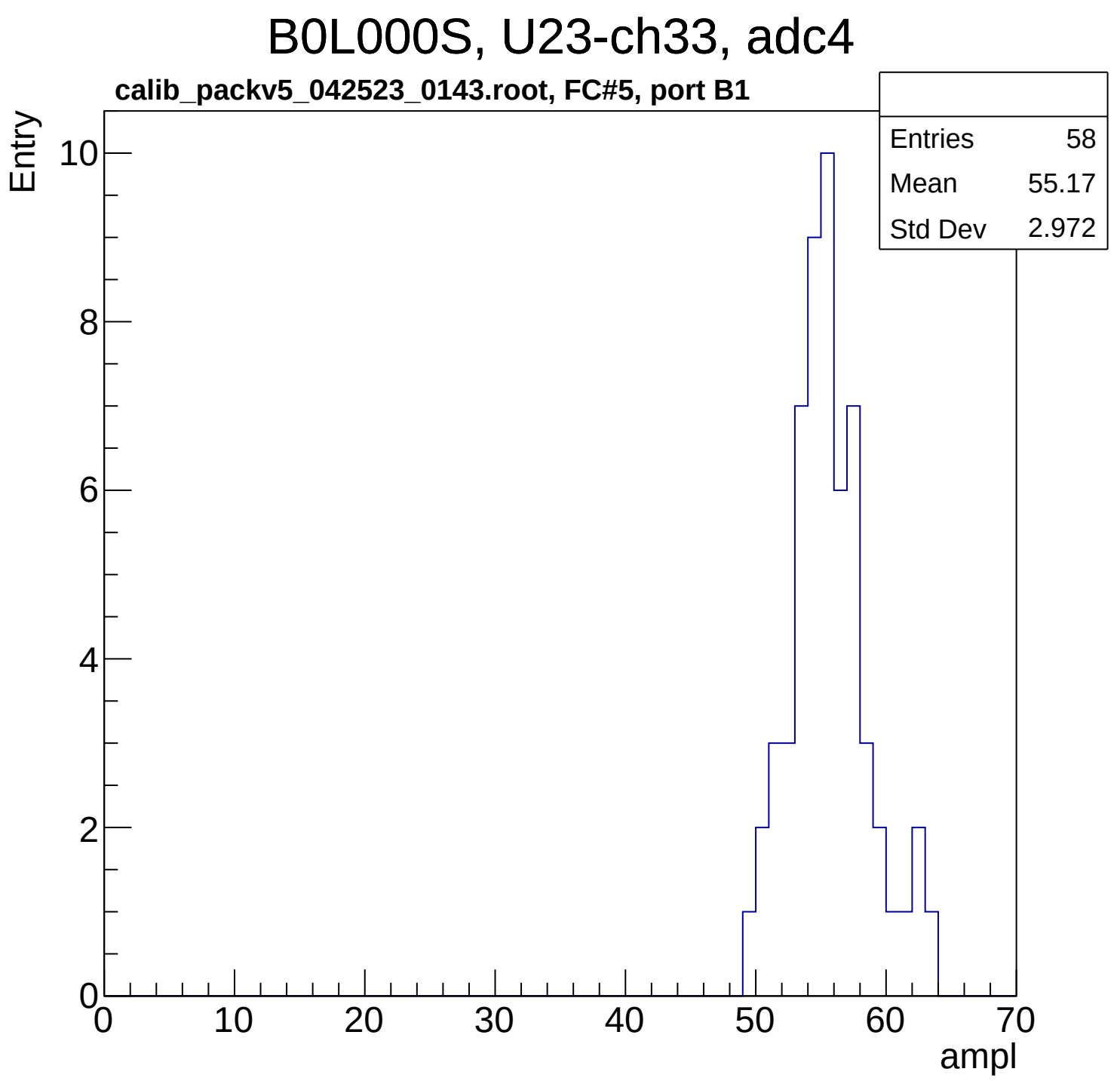

B0L000S, U23-ch32, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

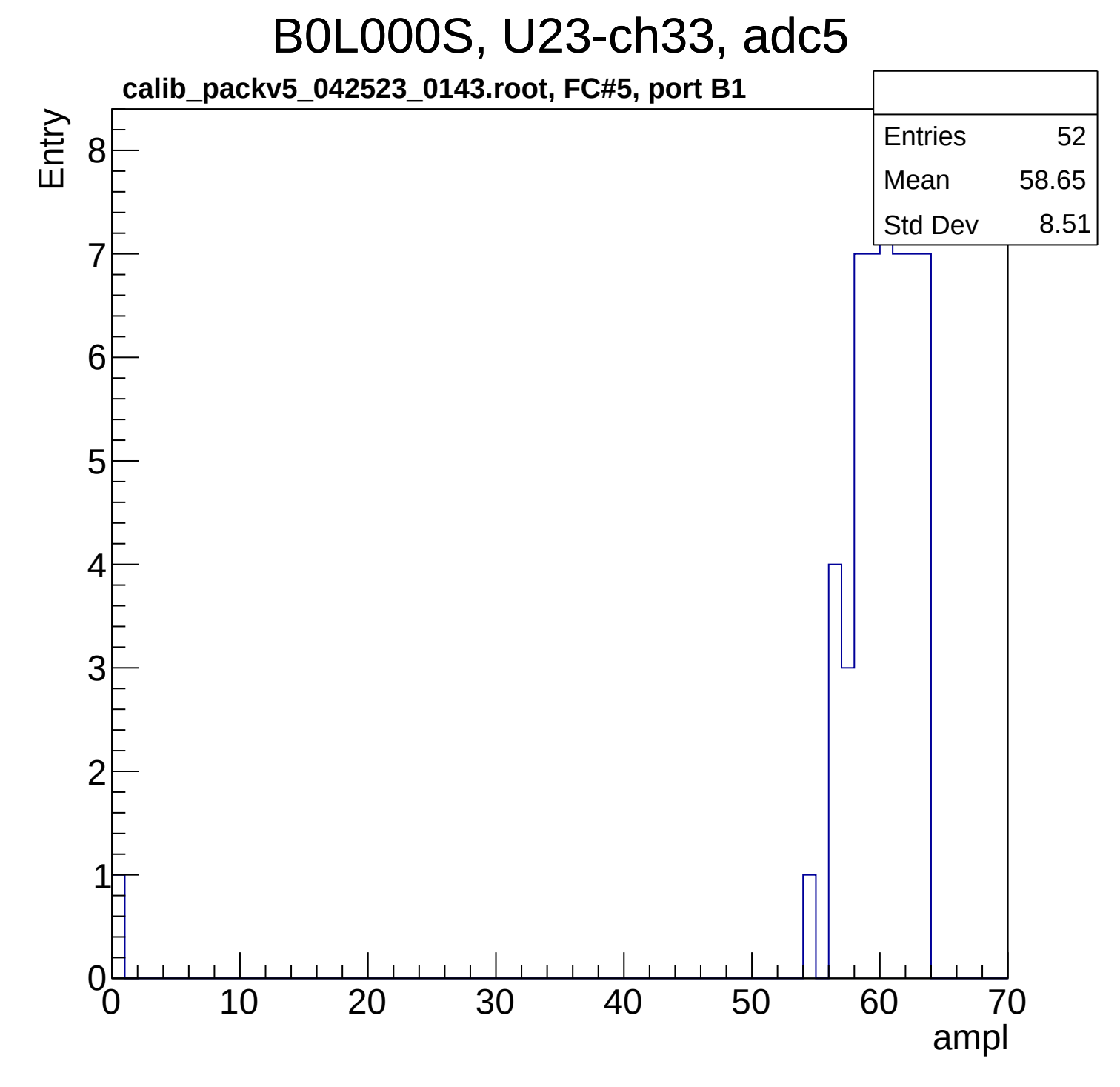

B0L000S, U23-ch33, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.37
Std Dev 3.126
Gaus mean : 31.2661
Gaus Width: 3.4591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

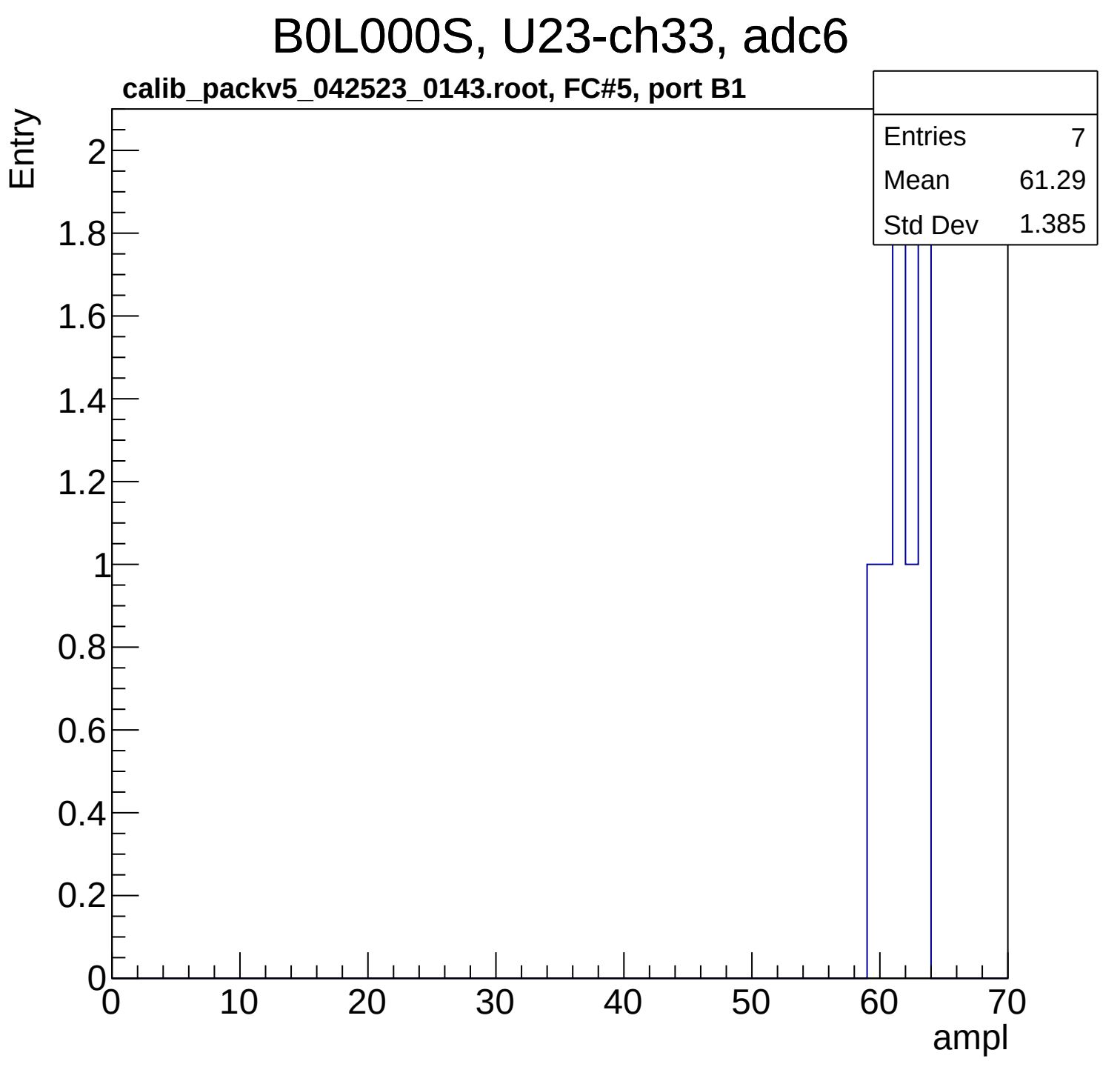

B0L000S, U23-ch33, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 36.86
Std Dev 3.259
Gaus mean : 36.8081
Gaus Width: 4.3302
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

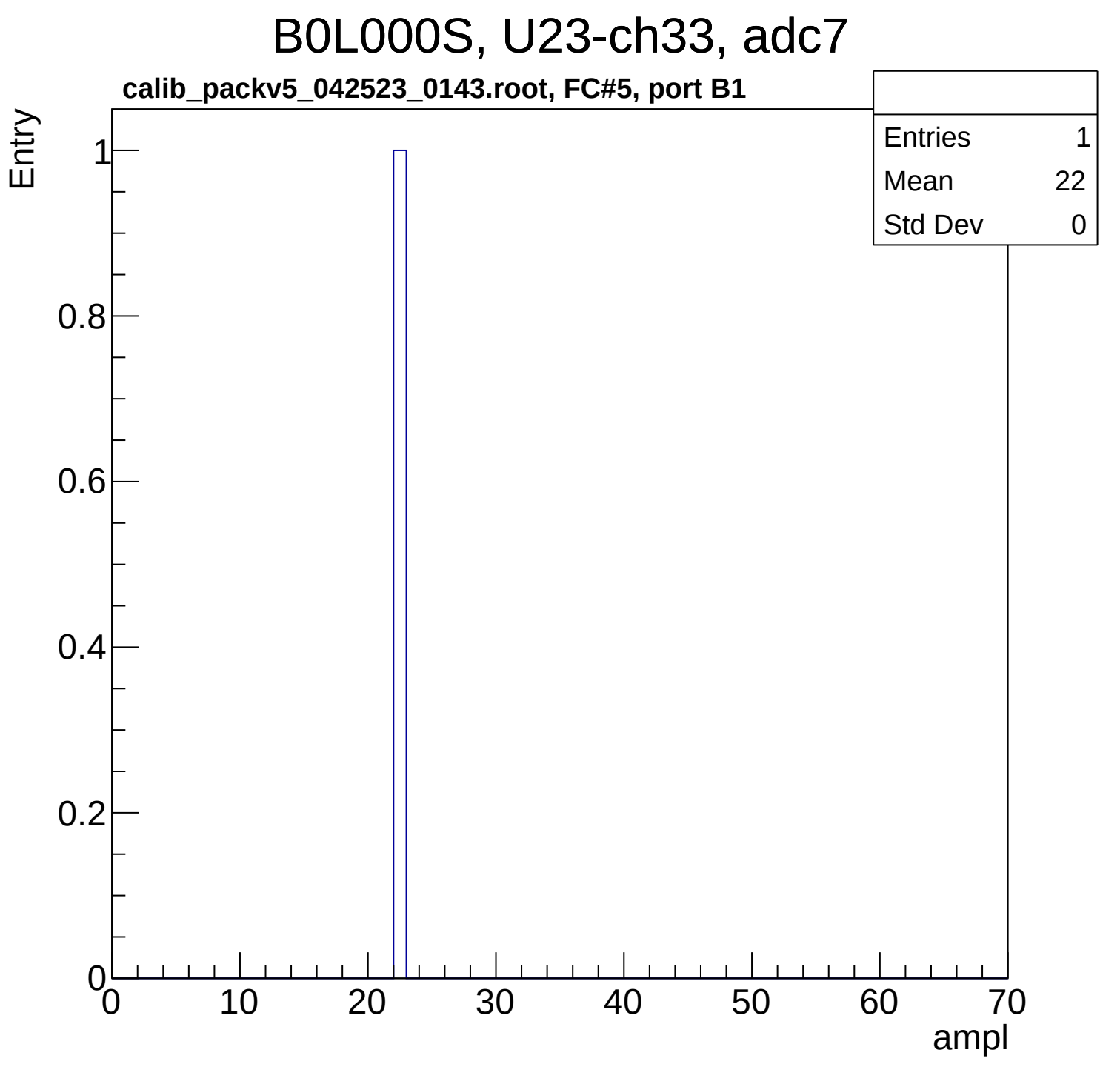

B0L000S, U23-ch33, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.03
Std Dev 3.441
Gaus mean : 42.8766
Gaus Width: 4.0796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

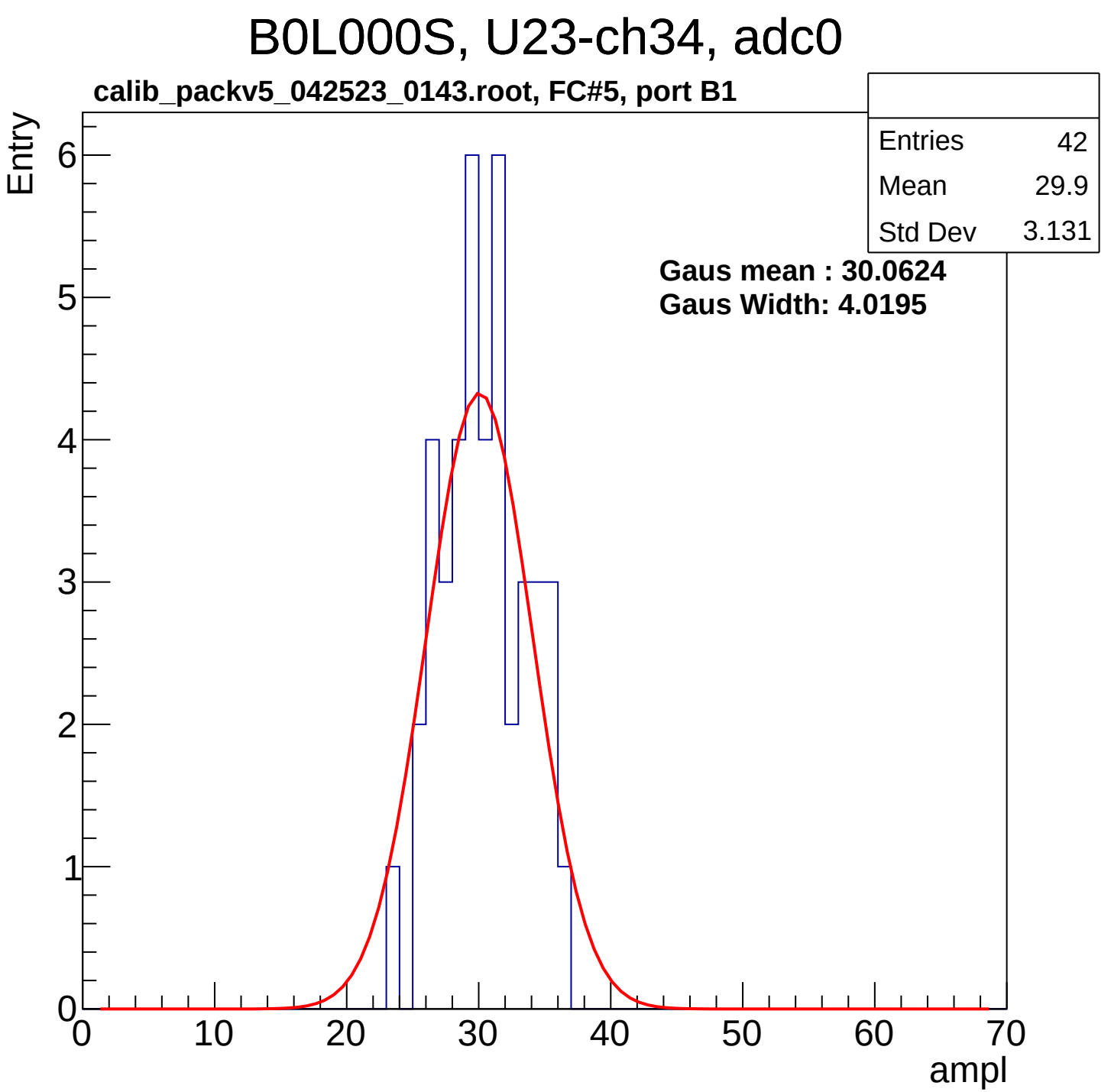

B0L000S, U23-ch33, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 48.66
Std Dev 2.627
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

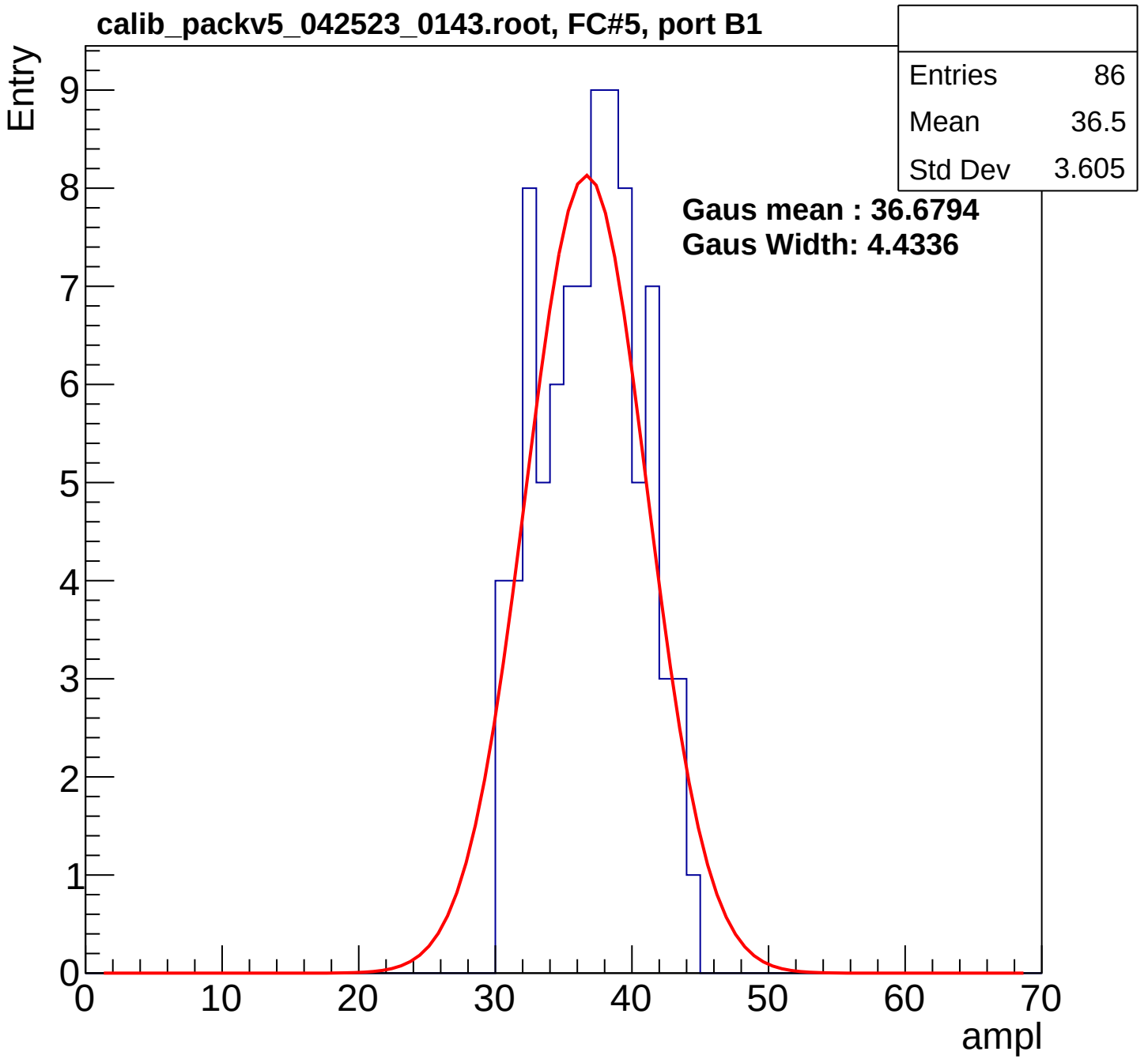

B0L000S, U23-ch33, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.17
Std Dev 2.972
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

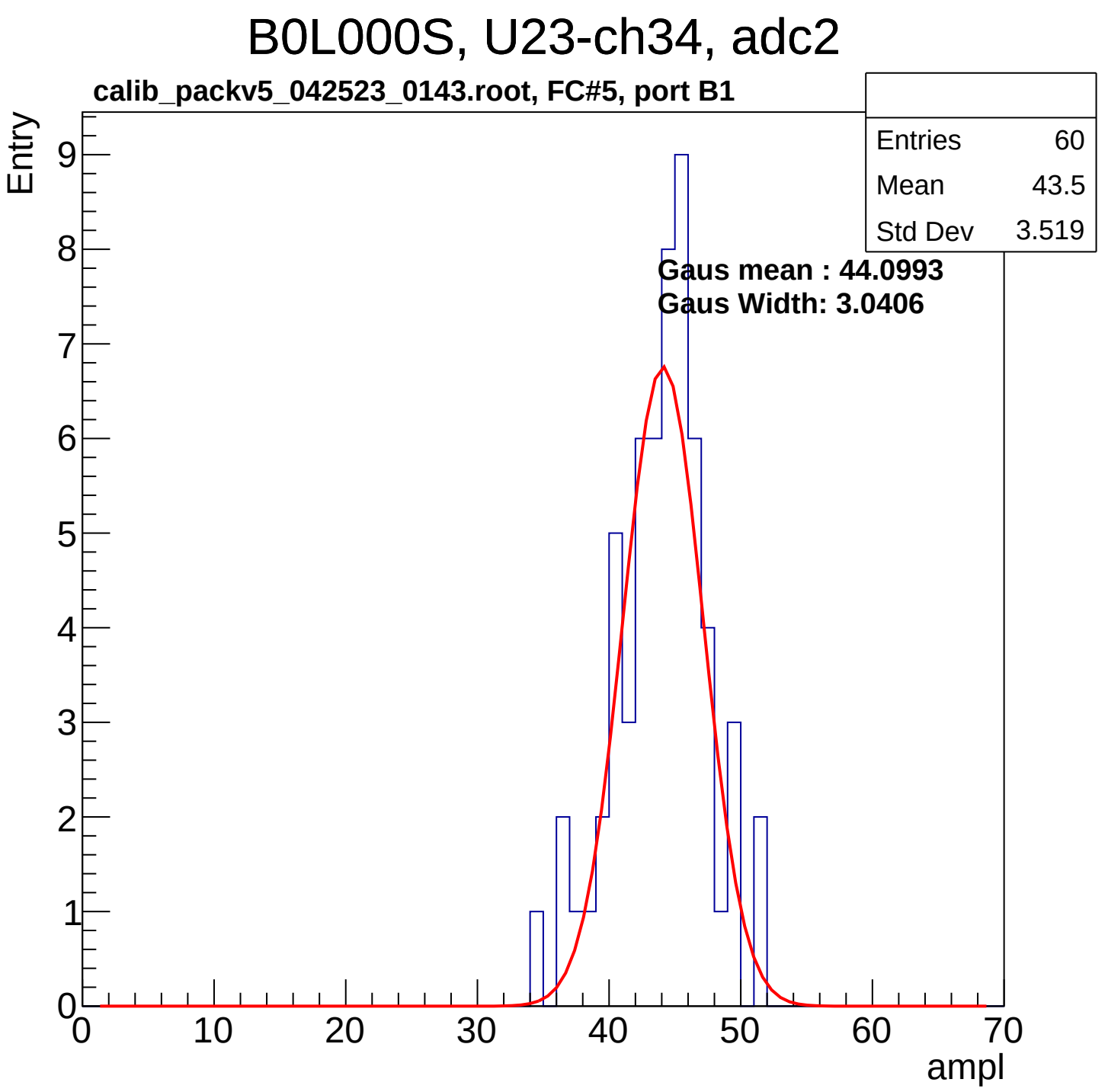

B0L000S, U23-ch33, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.65
Std Dev 8.51
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

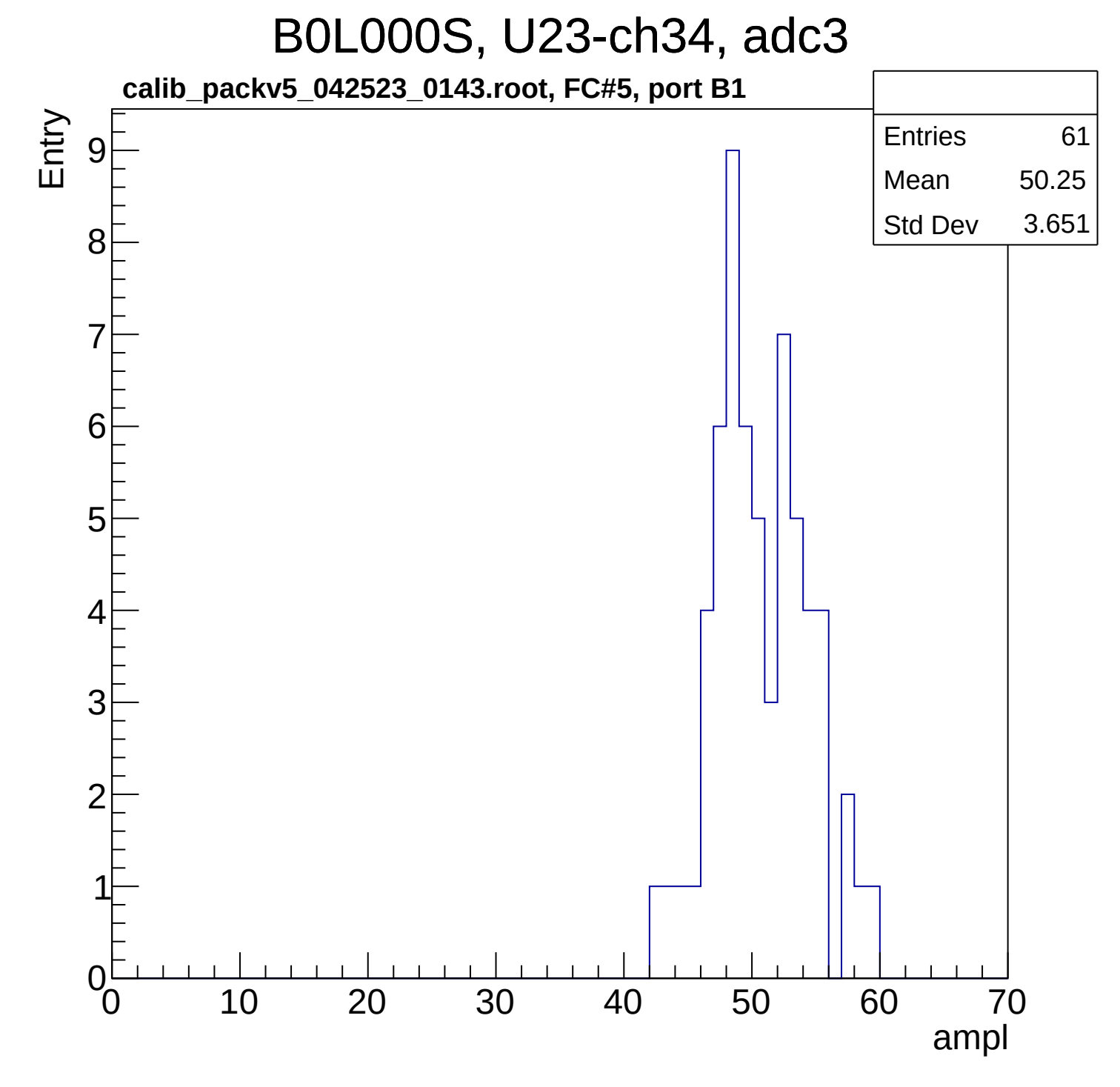

B0L000S, U23-ch33, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.29
Std Dev 1.385
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

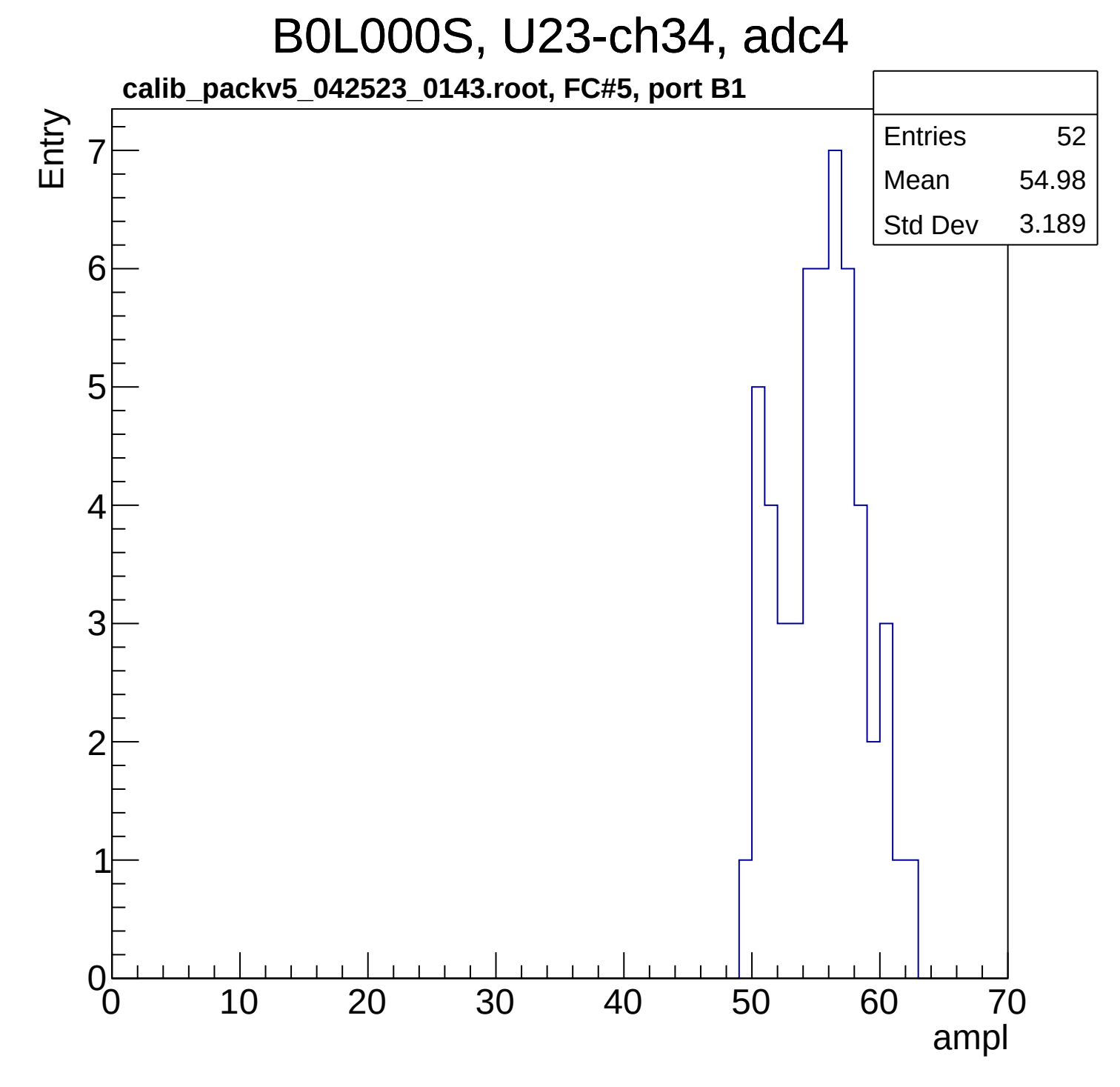

B0L000S, U23-ch33, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

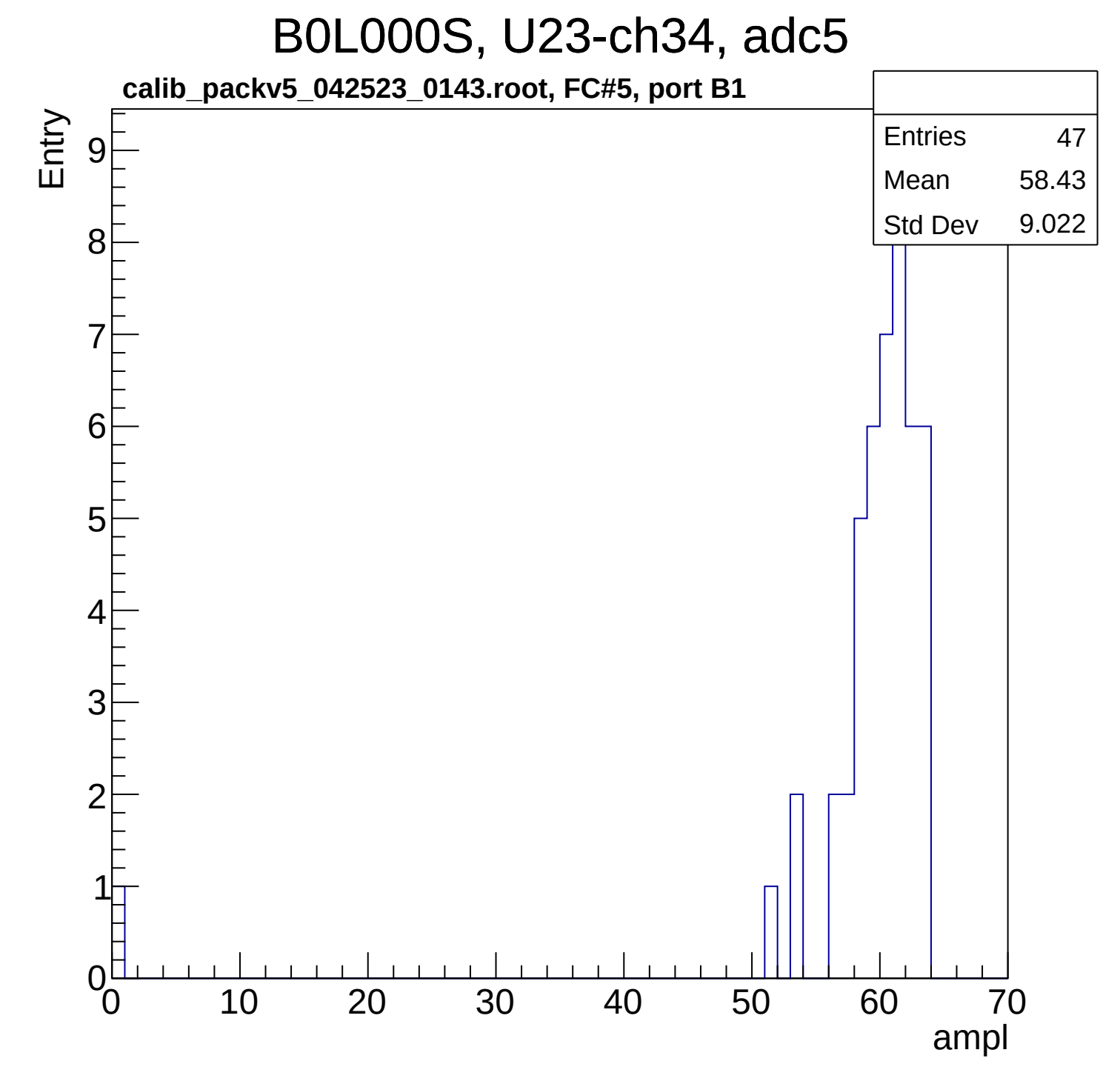

B0L000S, U23-ch34, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 29.9
Std Dev 3.131
Gaus mean : 30.0624
Gaus Width: 4.0195
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

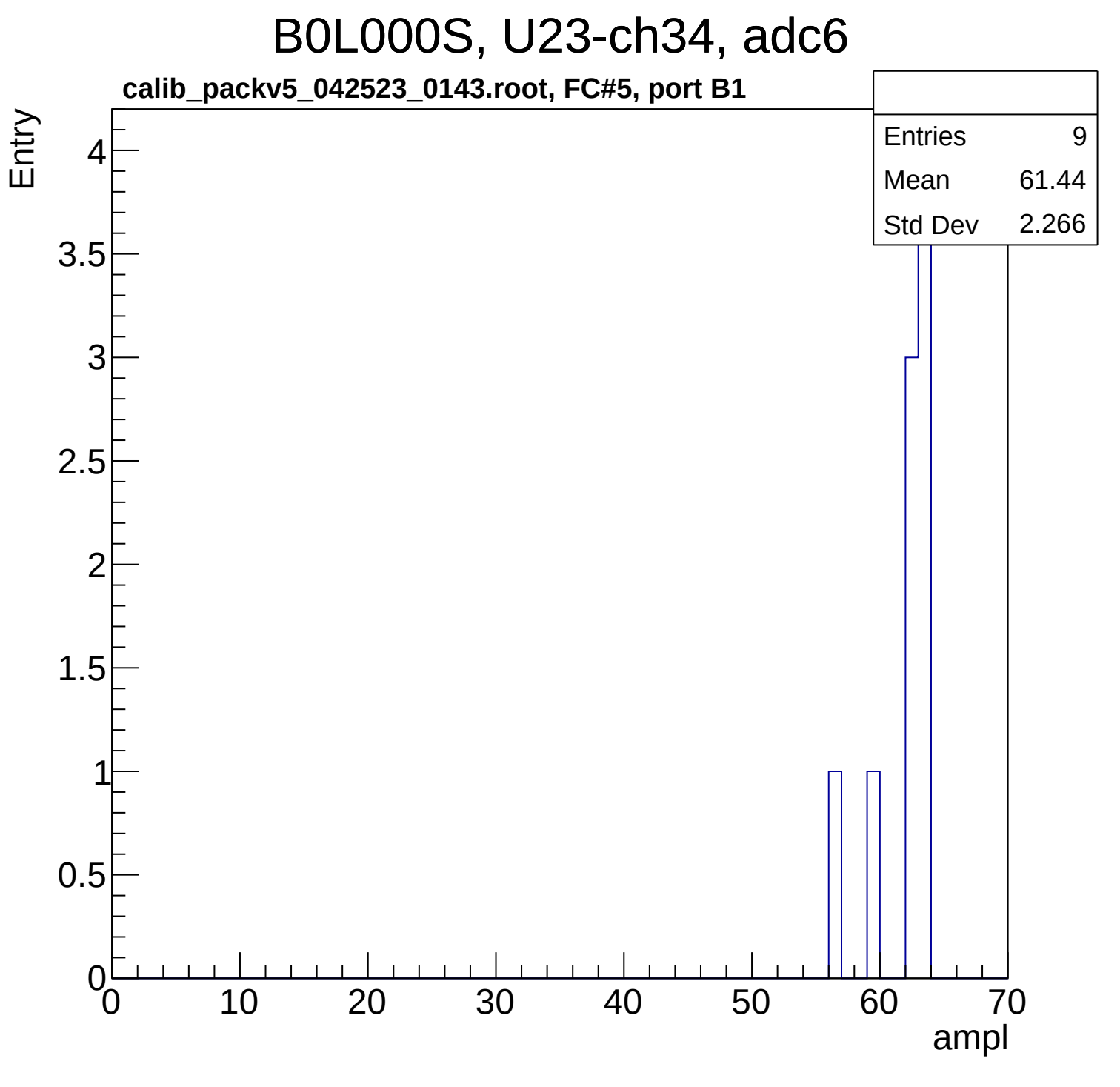

B0L000S, U23-ch34, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 36.5
Std Dev 3.605
Gaus mean : 36.6794
Gaus Width: 4.4336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

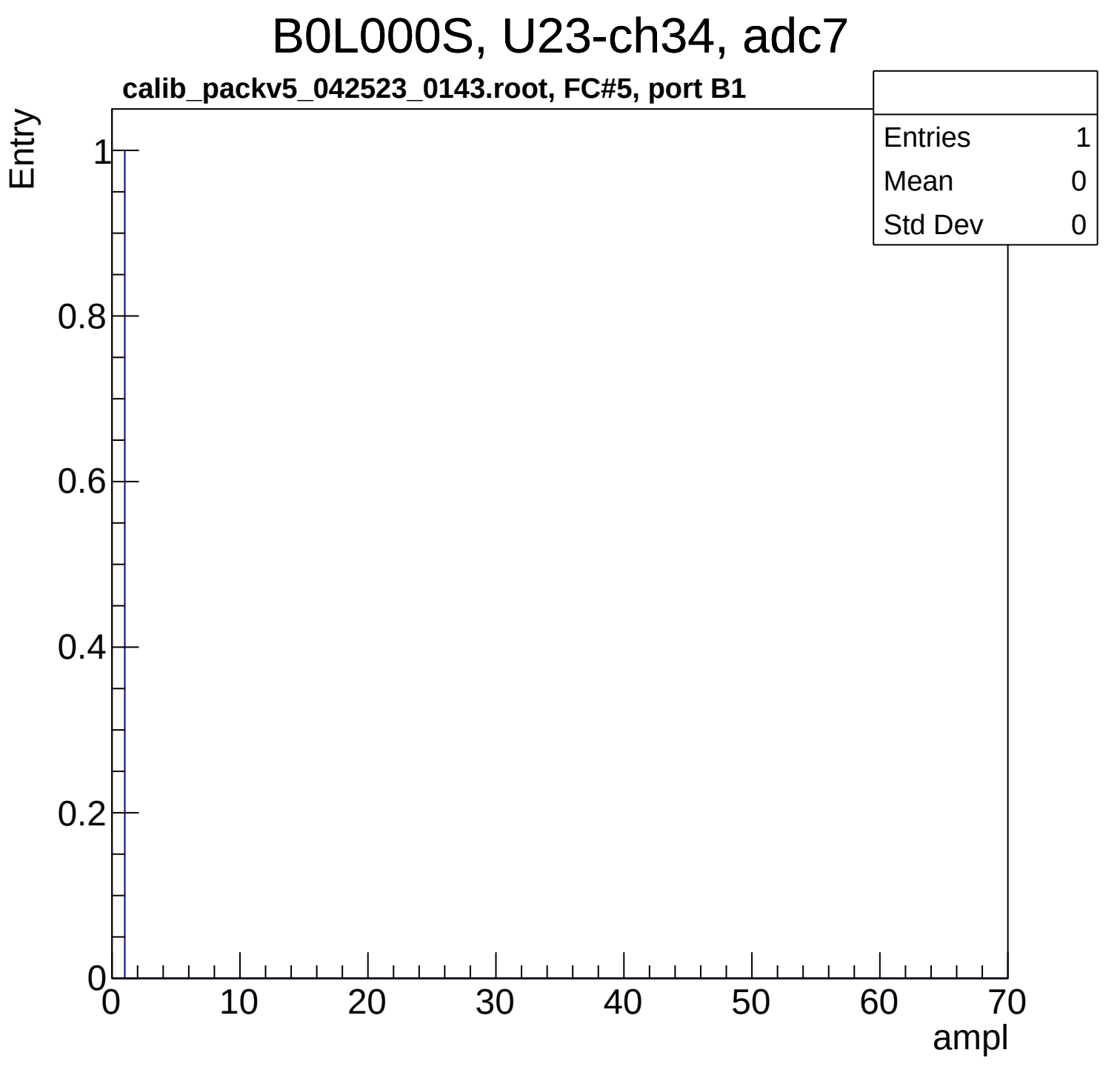

B0L000S, U23-ch34, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.5
Std Dev 3.519
Gaus mean : 44.0993
Gaus Width: 3.0406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

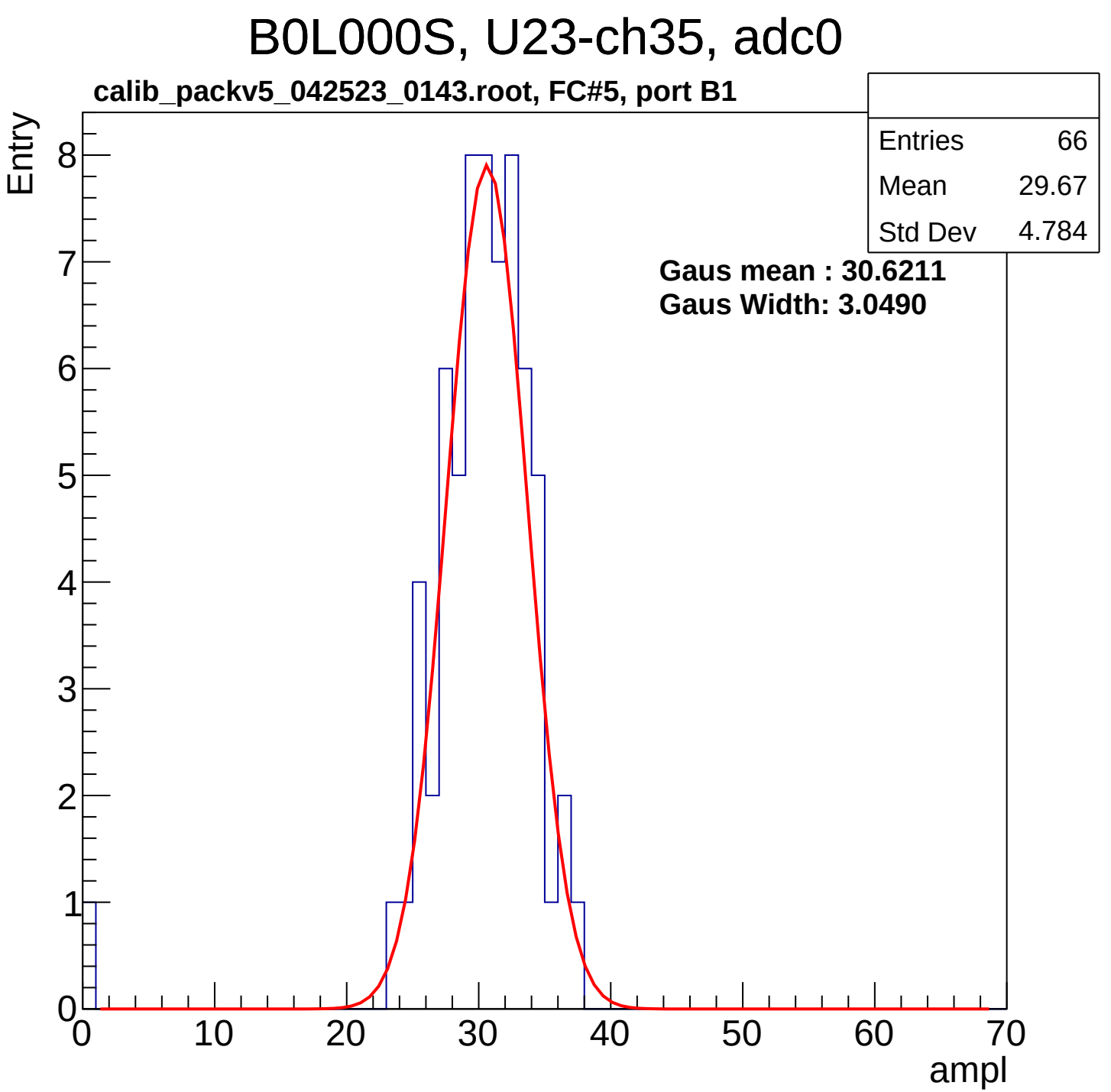

B0L000S, U23-ch34, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.25
Std Dev 3.651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

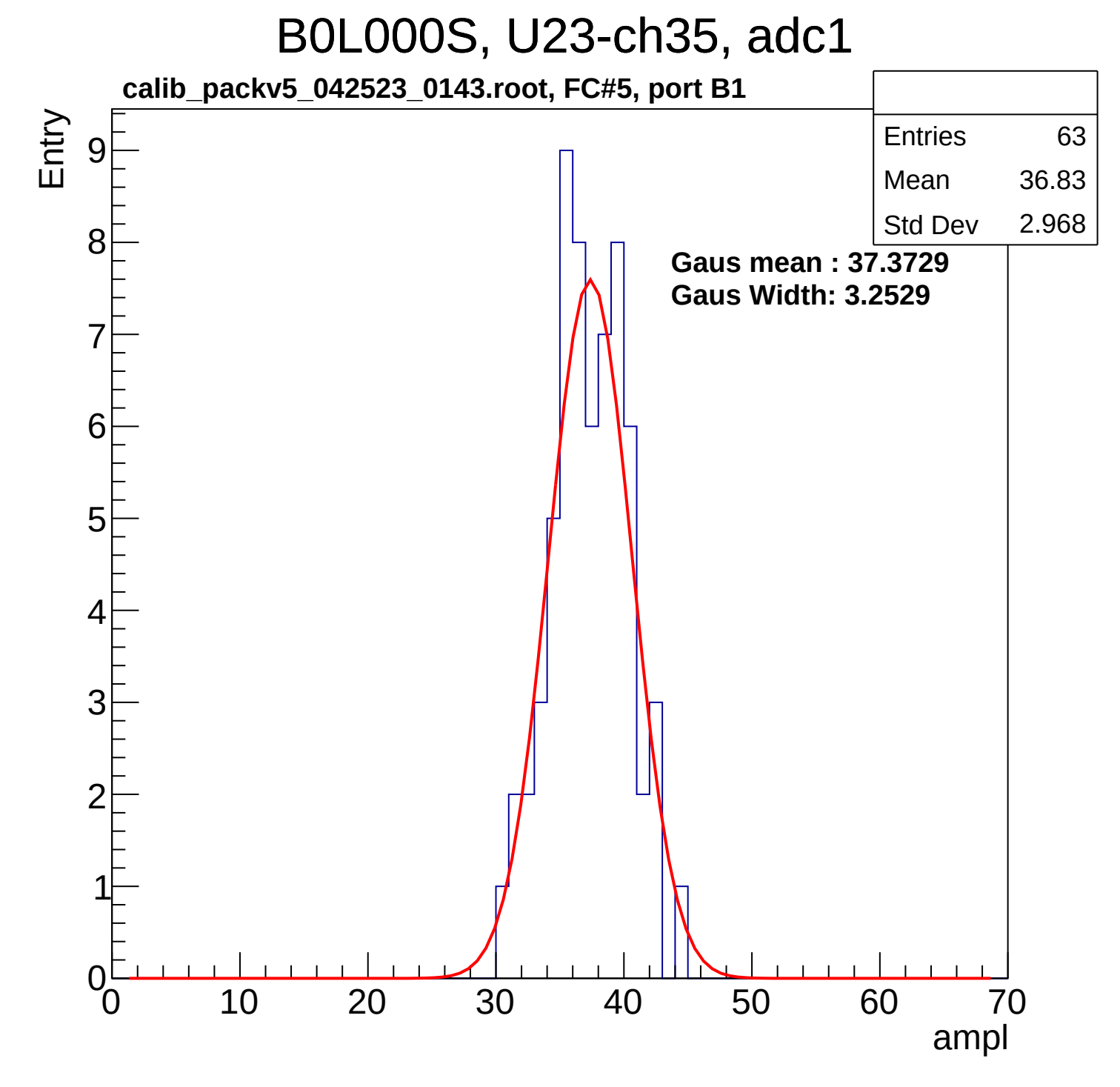

B0L000S, U23-ch34, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.98
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

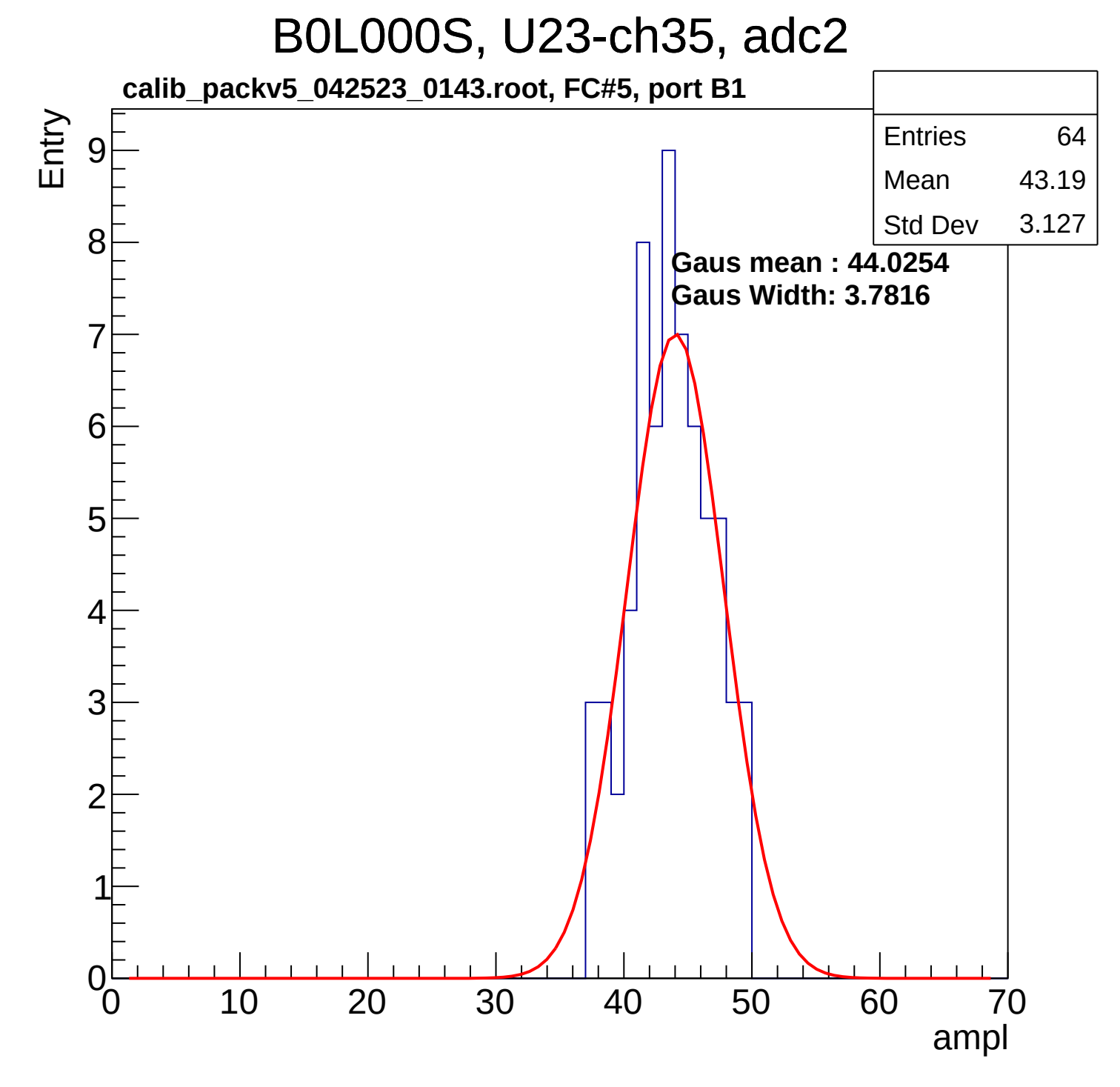

B0L000S, U23-ch34, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.43
Std Dev 9.022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

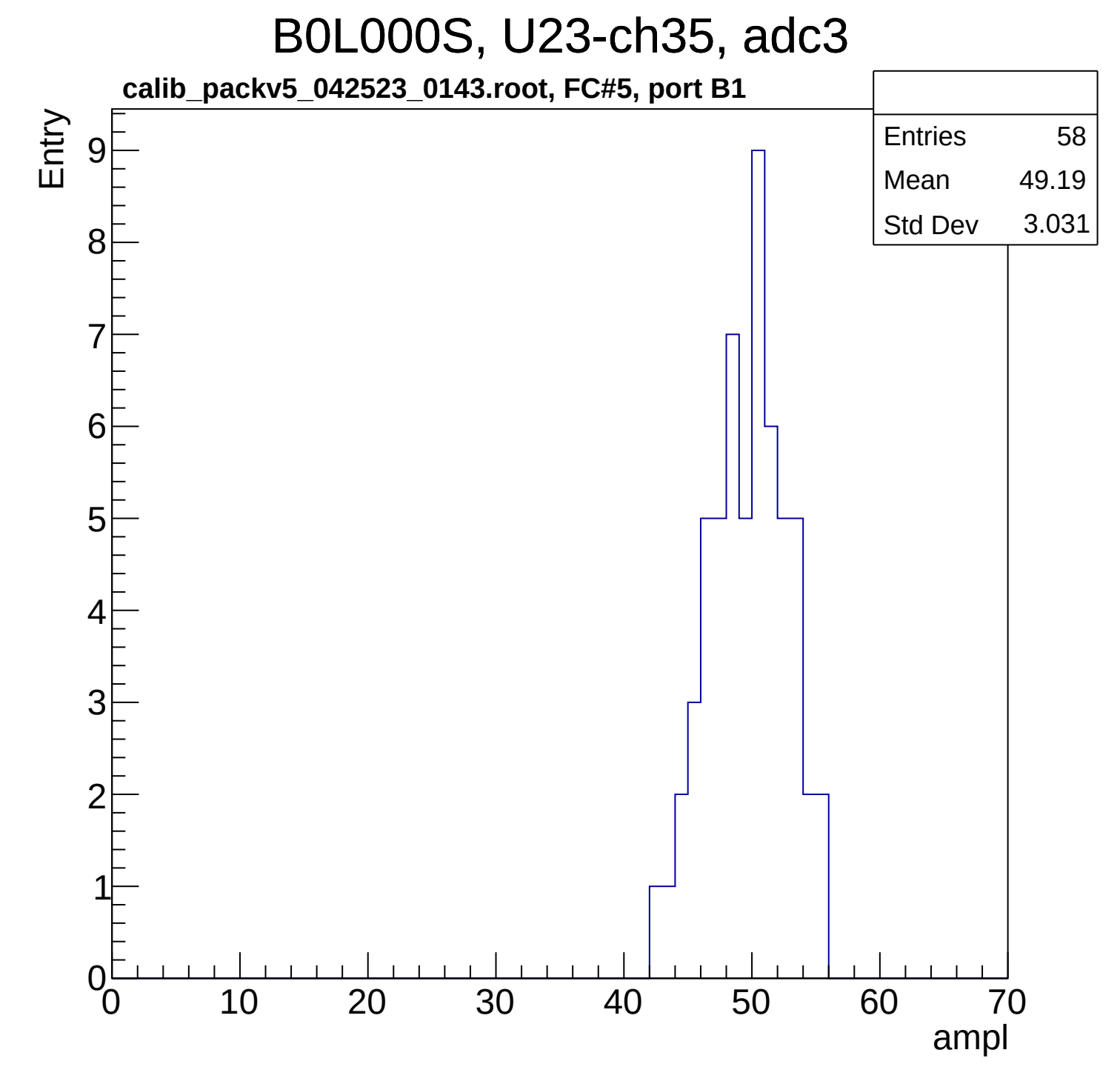

B0L000S, U23-ch34, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.44
Std Dev 2.266
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

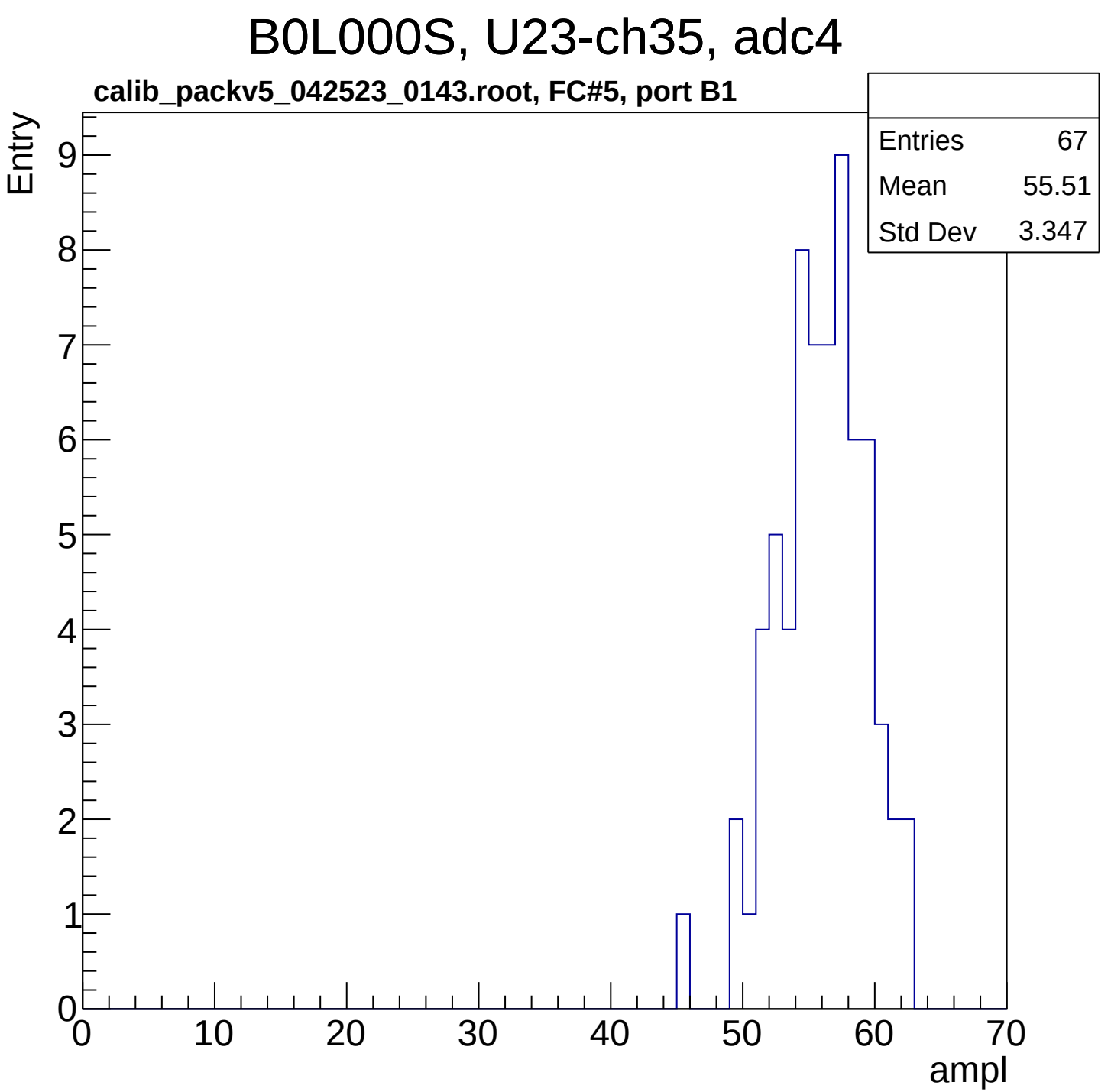

B0L000S, U23-ch34, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

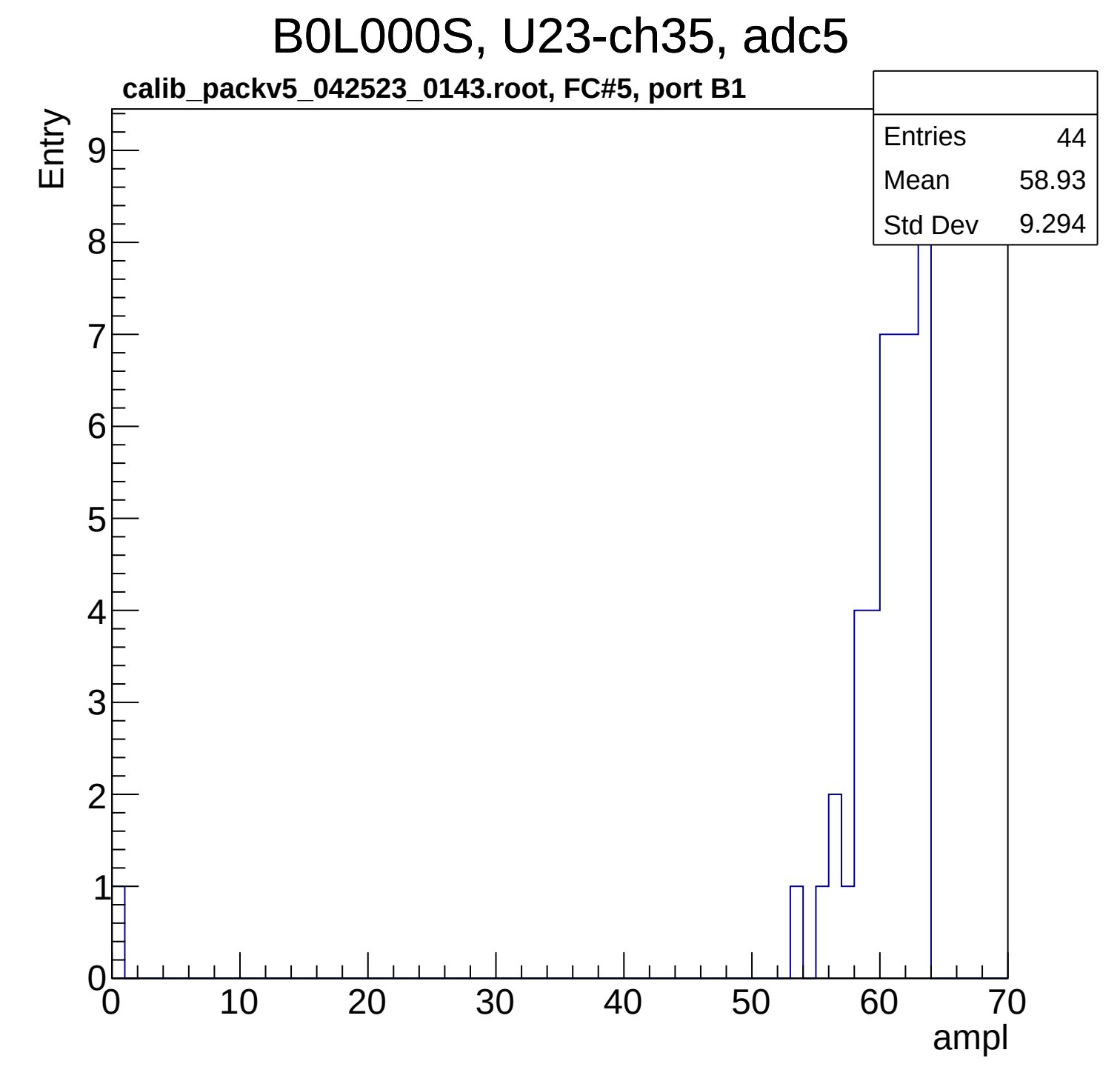

B0L000S, U23-ch35, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.67
Std Dev 4.784
Gaus mean : 30.6211
Gaus Width: 3.0490
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

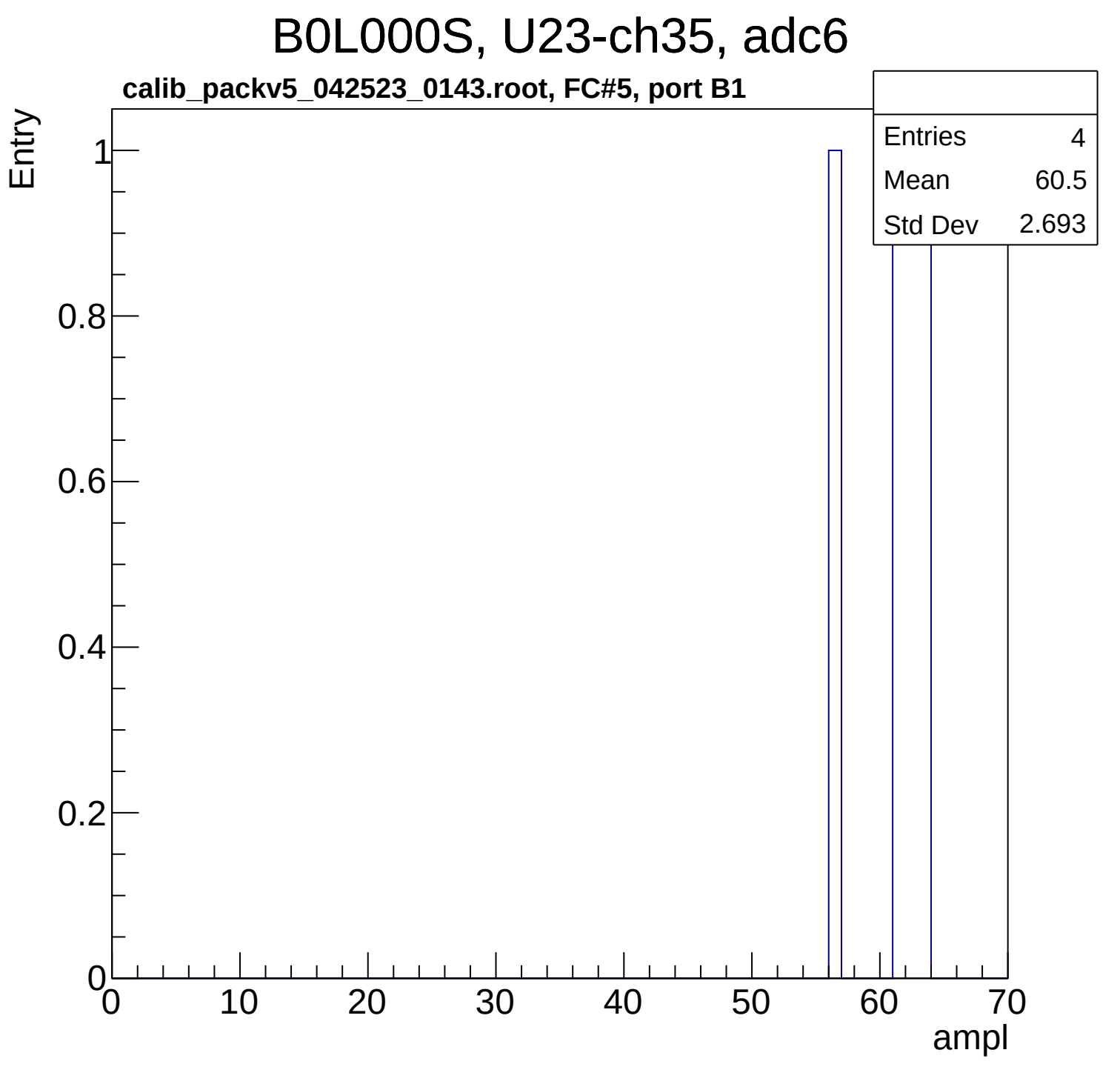

B0L000S, U23-ch35, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.83
Std Dev 2.968
Gaus mean : 37.3729
Gaus Width: 3.2529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

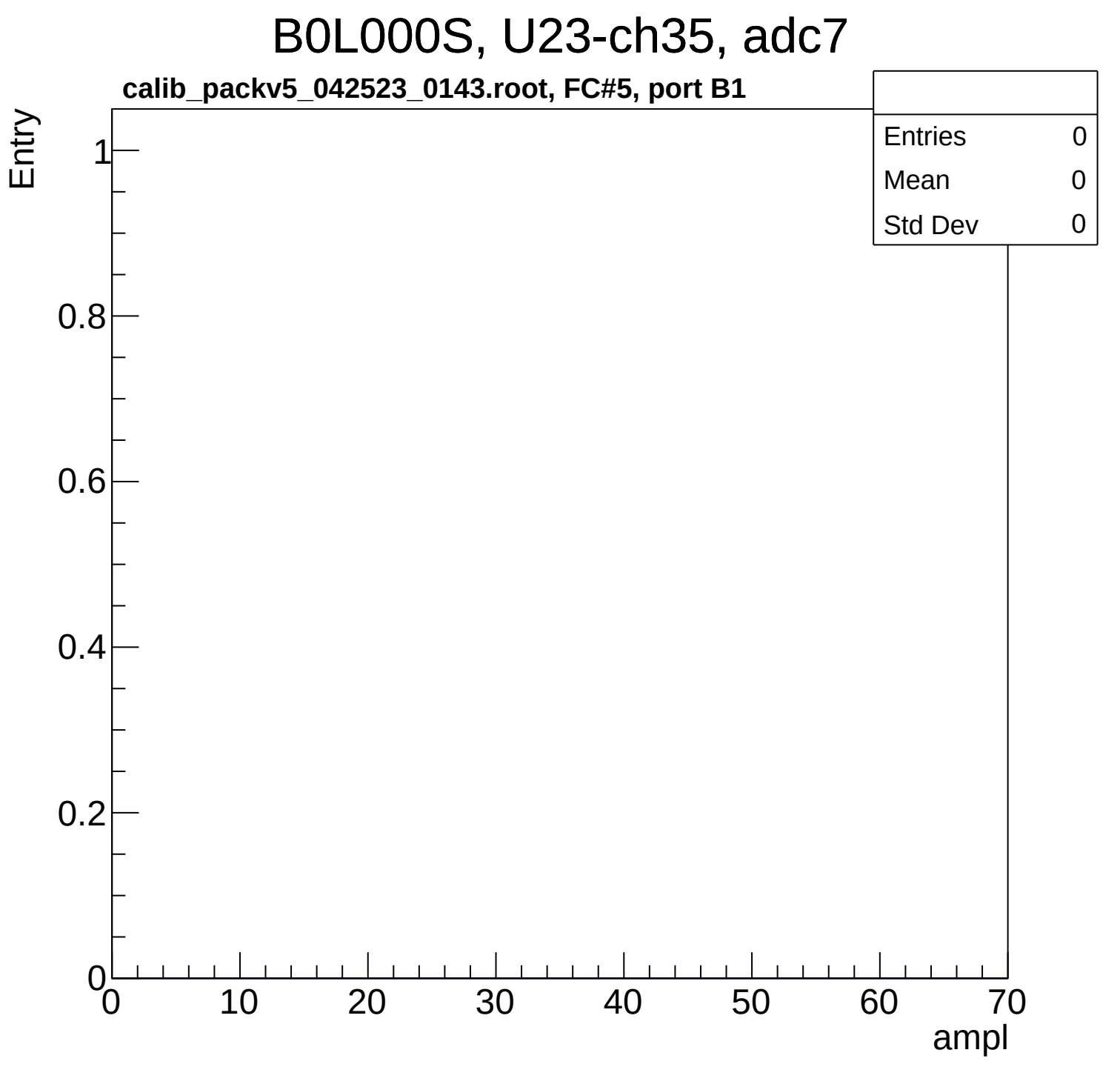

B0L000S, U23-ch35, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.19
Std Dev 3.127
Gaus mean : 44.0254
Gaus Width: 3.7816
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

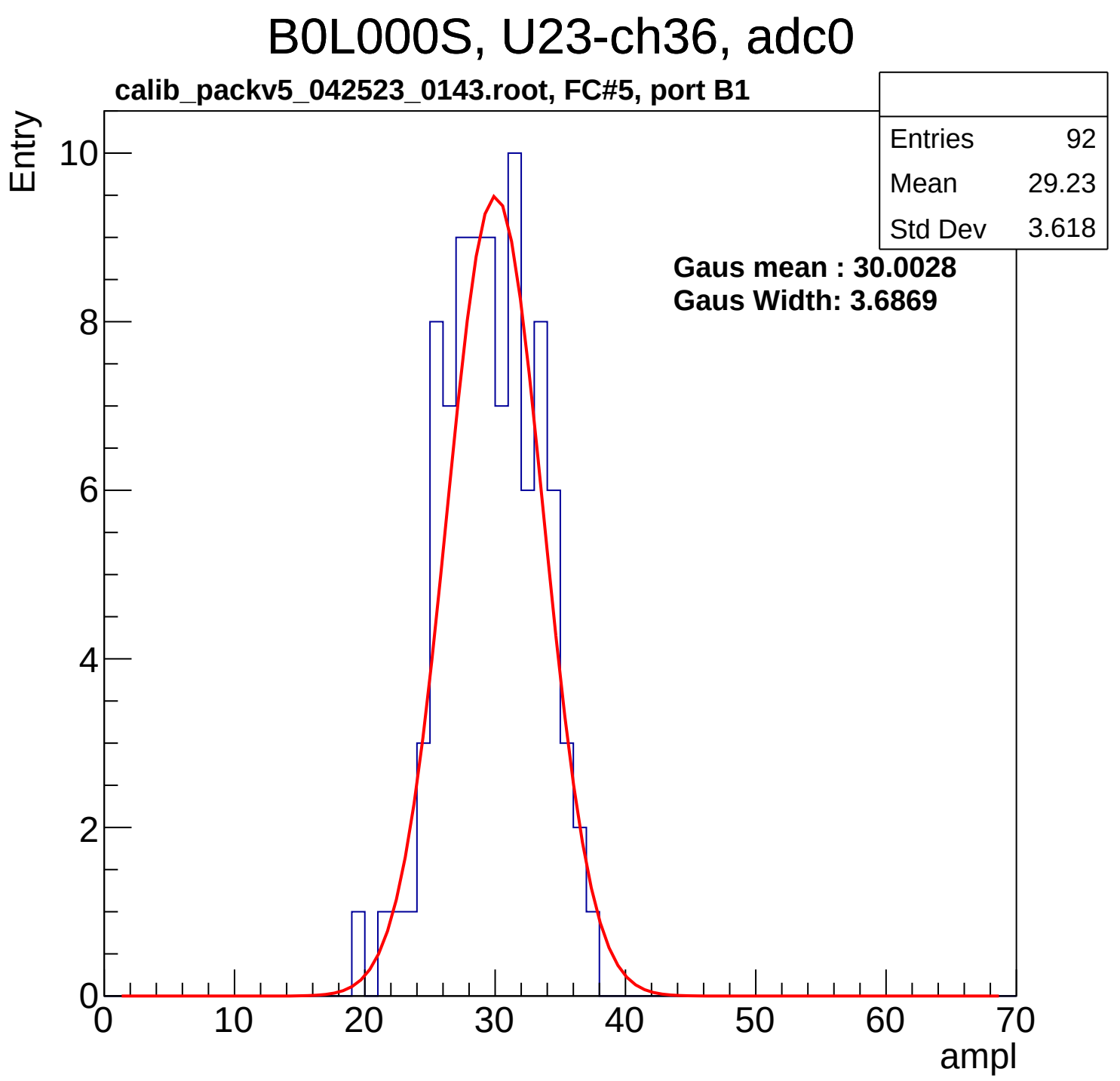

B0L000S, U23-ch35, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 49.19
Std Dev 3.031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

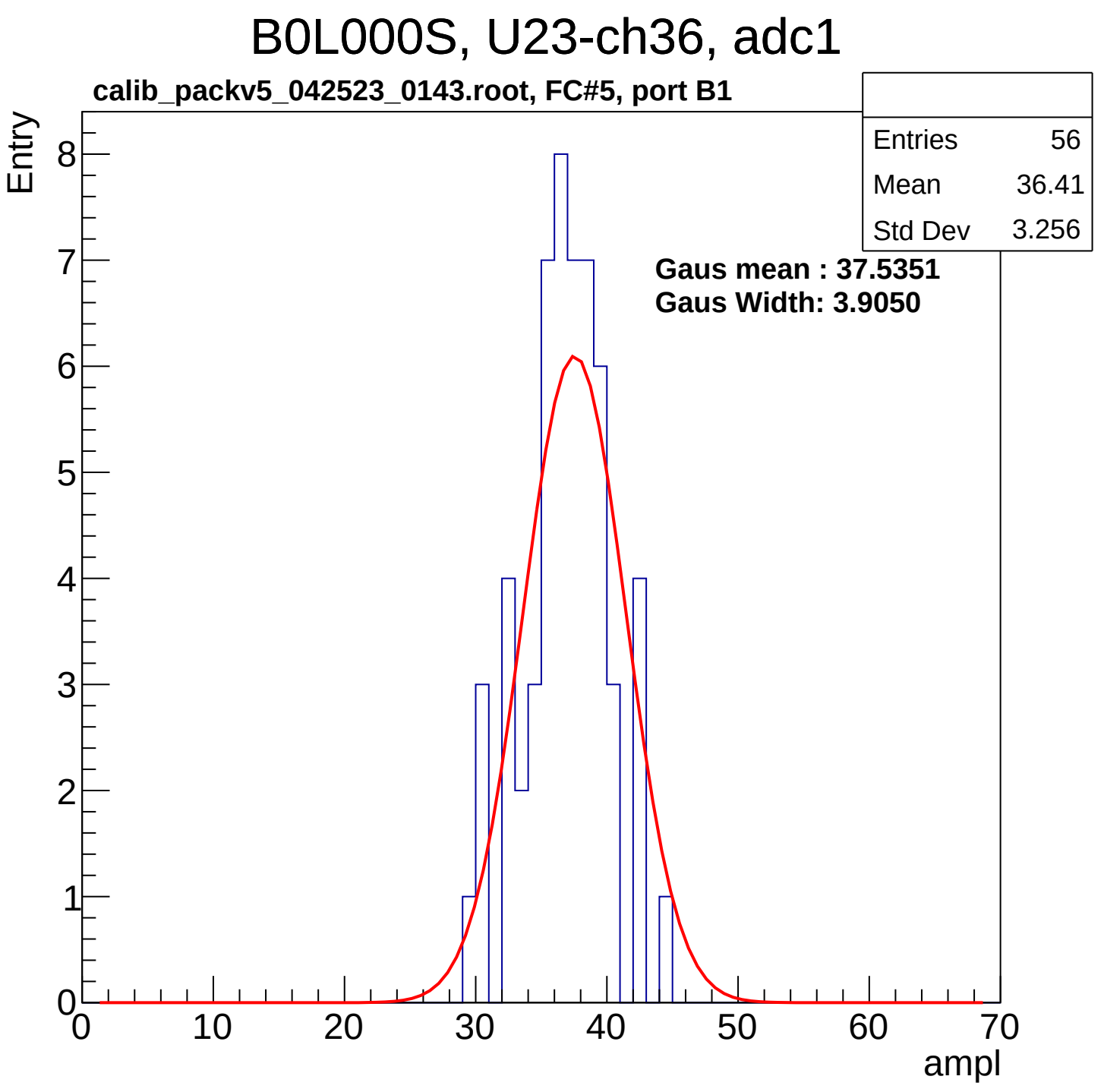

B0L000S, U23-ch35, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 55.51
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

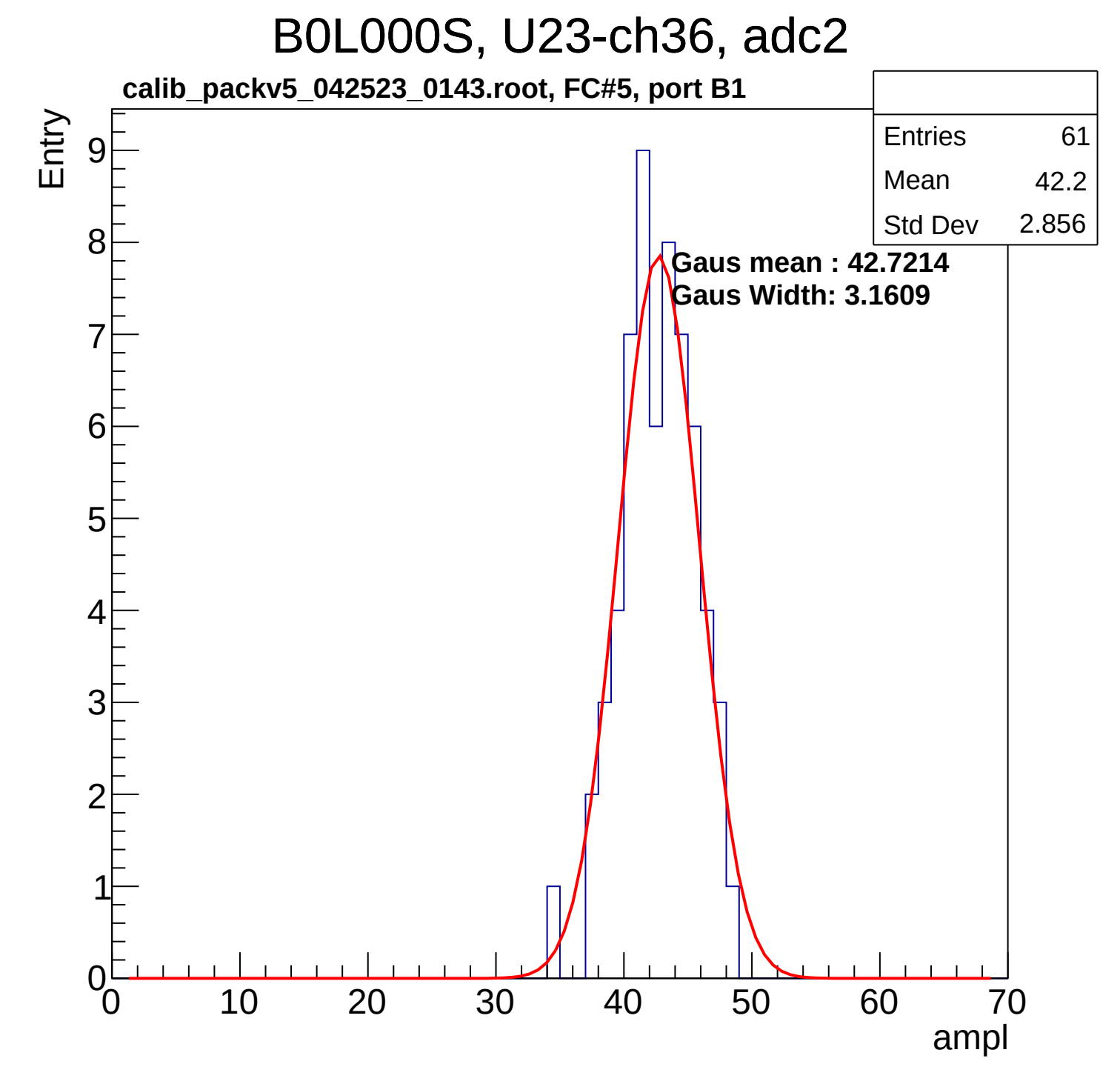

B0L000S, U23-ch35, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.93
Std Dev 9.294
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

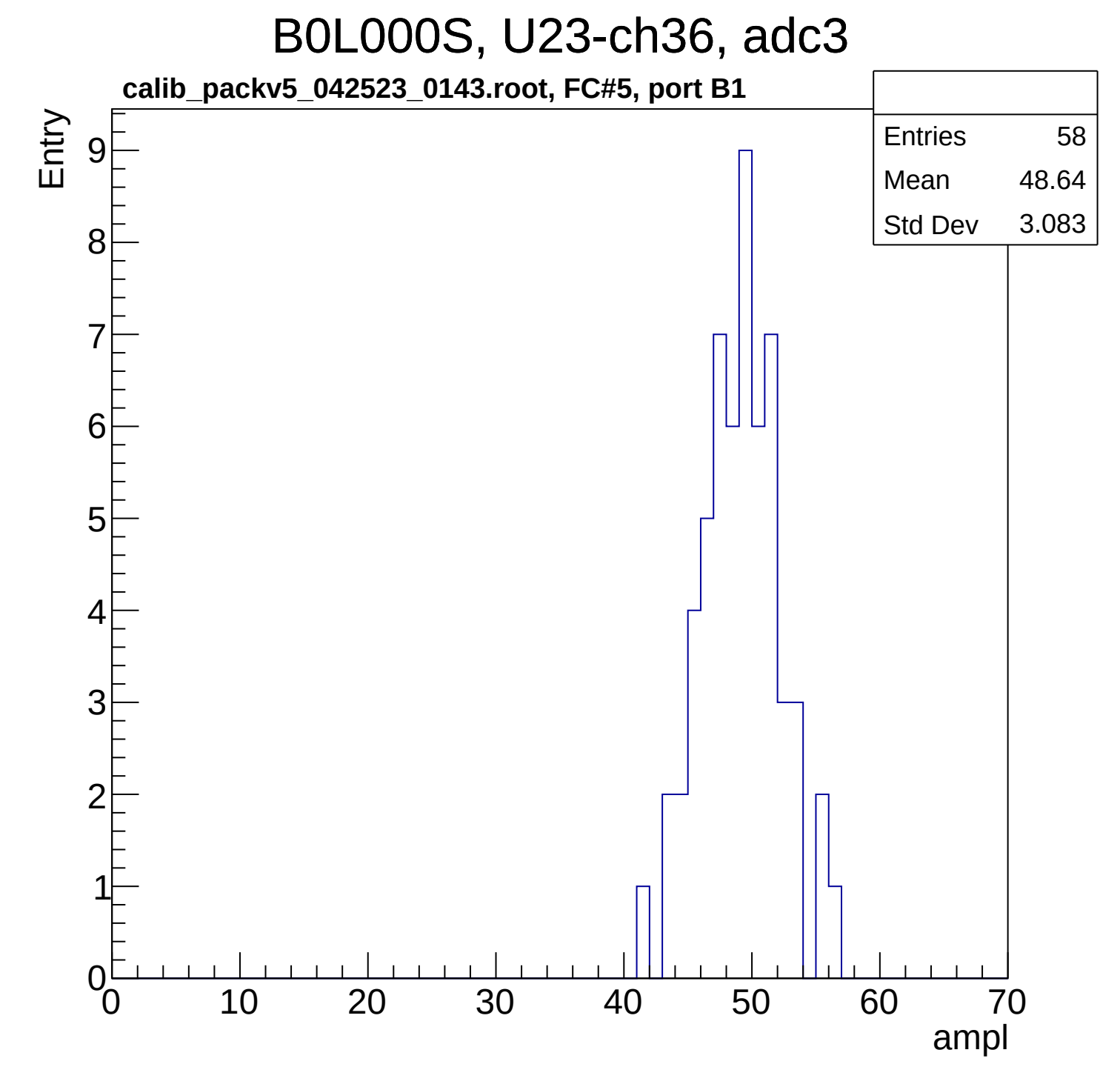

B0L000S, U23-ch35, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.5
Std Dev 2.693
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

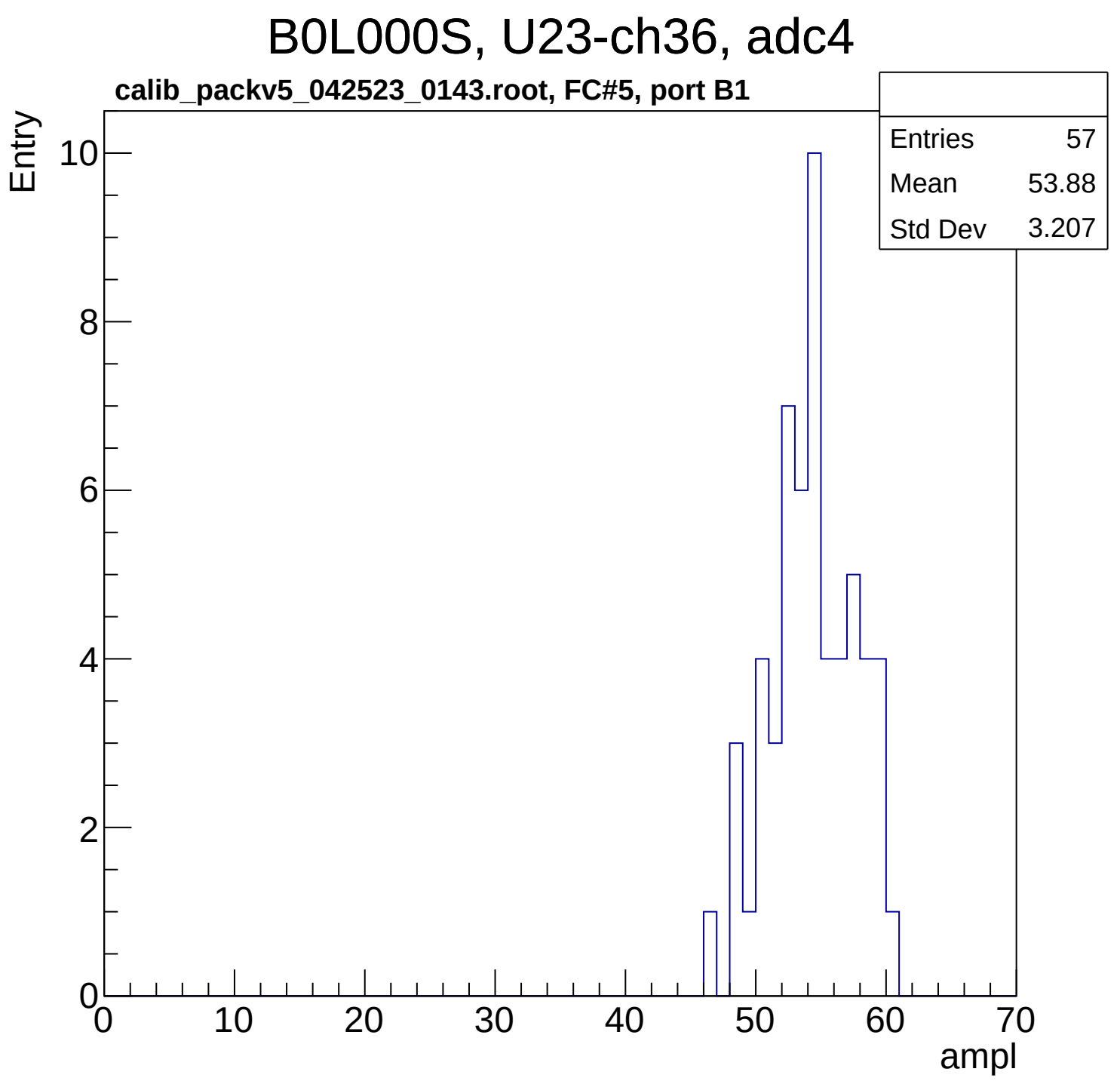

B0L000S, U23-ch35, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

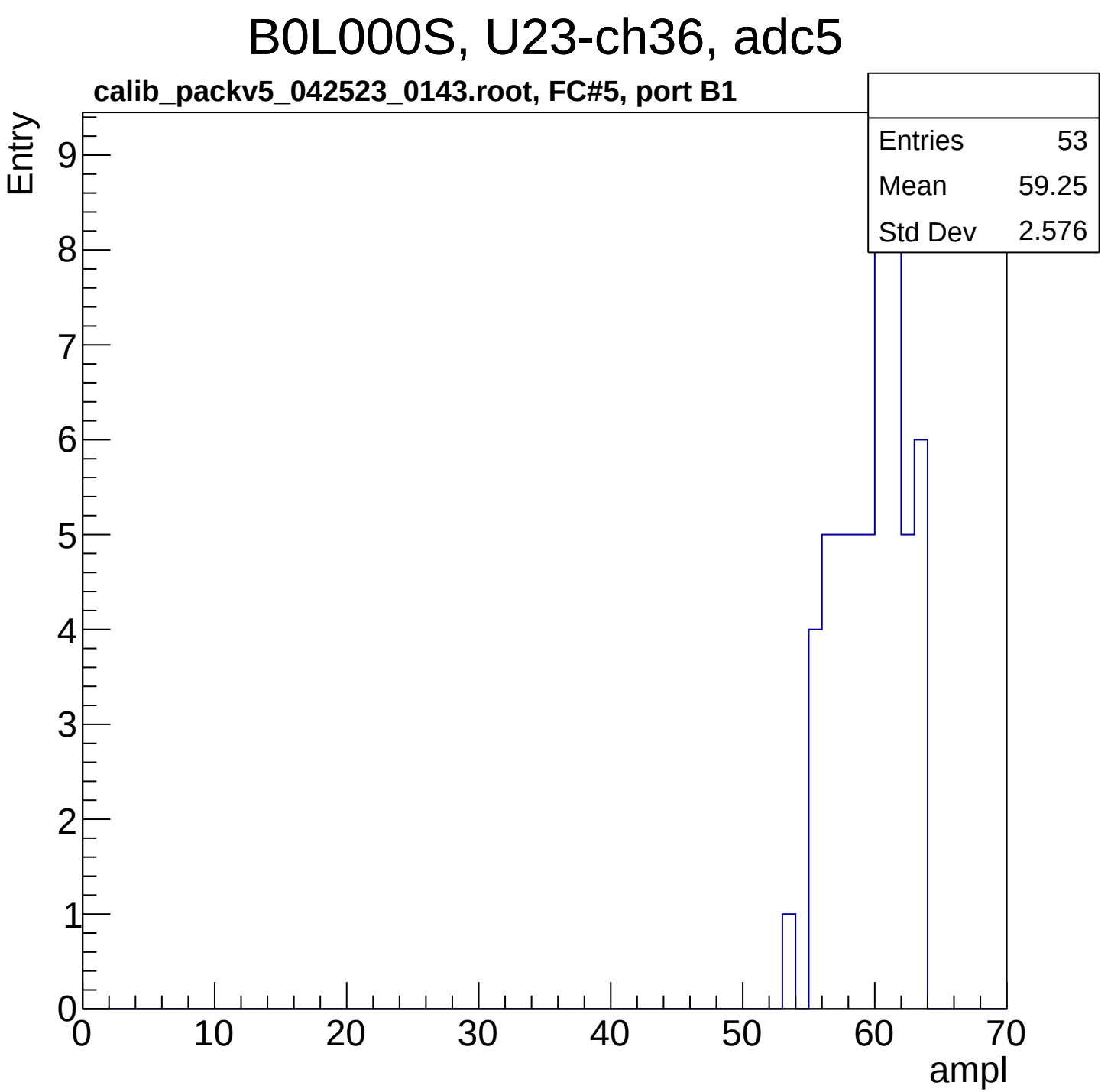

B0L000S, U23-ch36, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 92
Mean 29.23
Std Dev 3.618
Gaus mean : 30.0028
Gaus Width: 3.6869
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

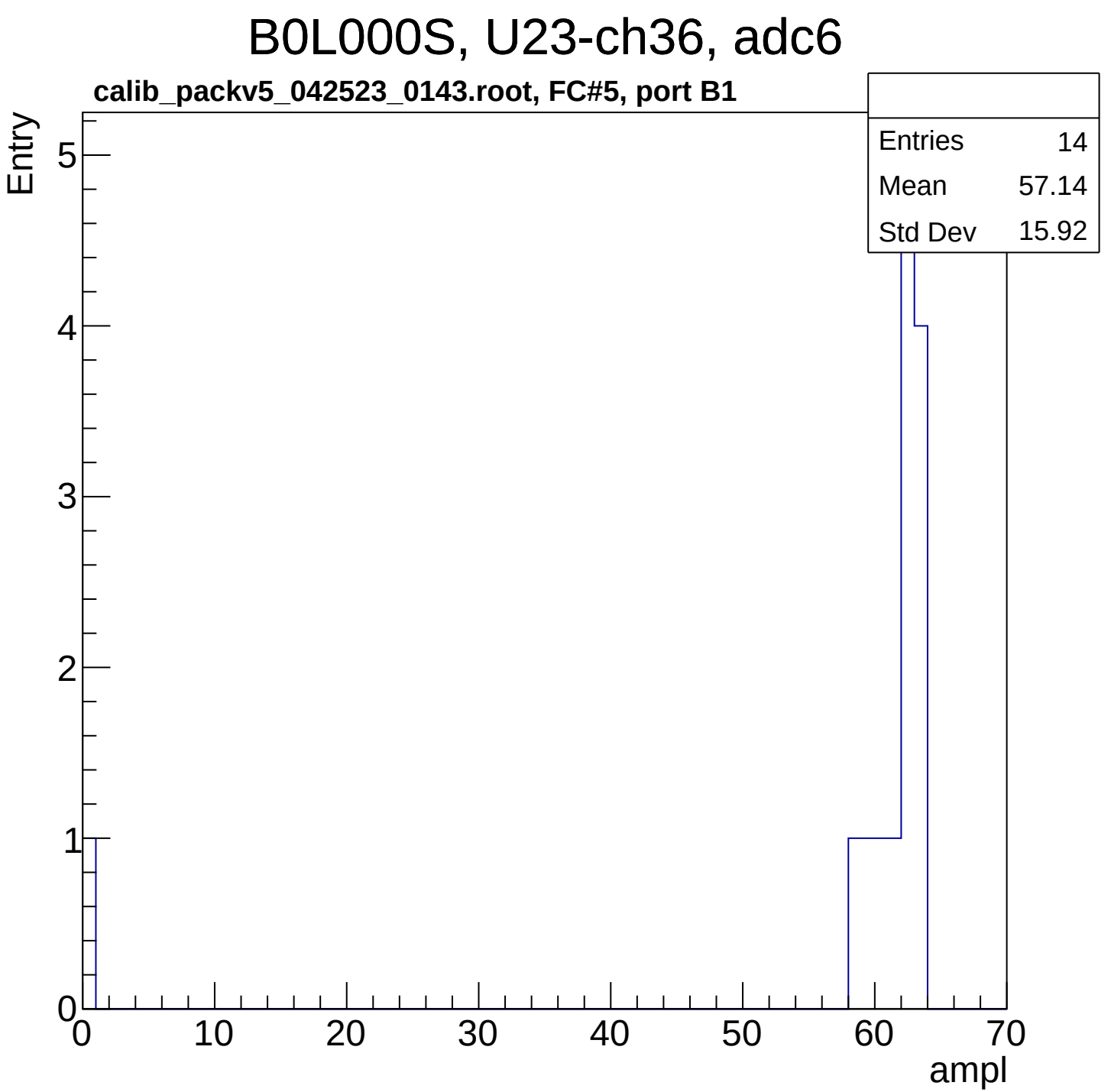

B0L000S, U23-ch36, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 36.41
Std Dev 3.256
Gaus mean : 37.5351
Gaus Width: 3.9050
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

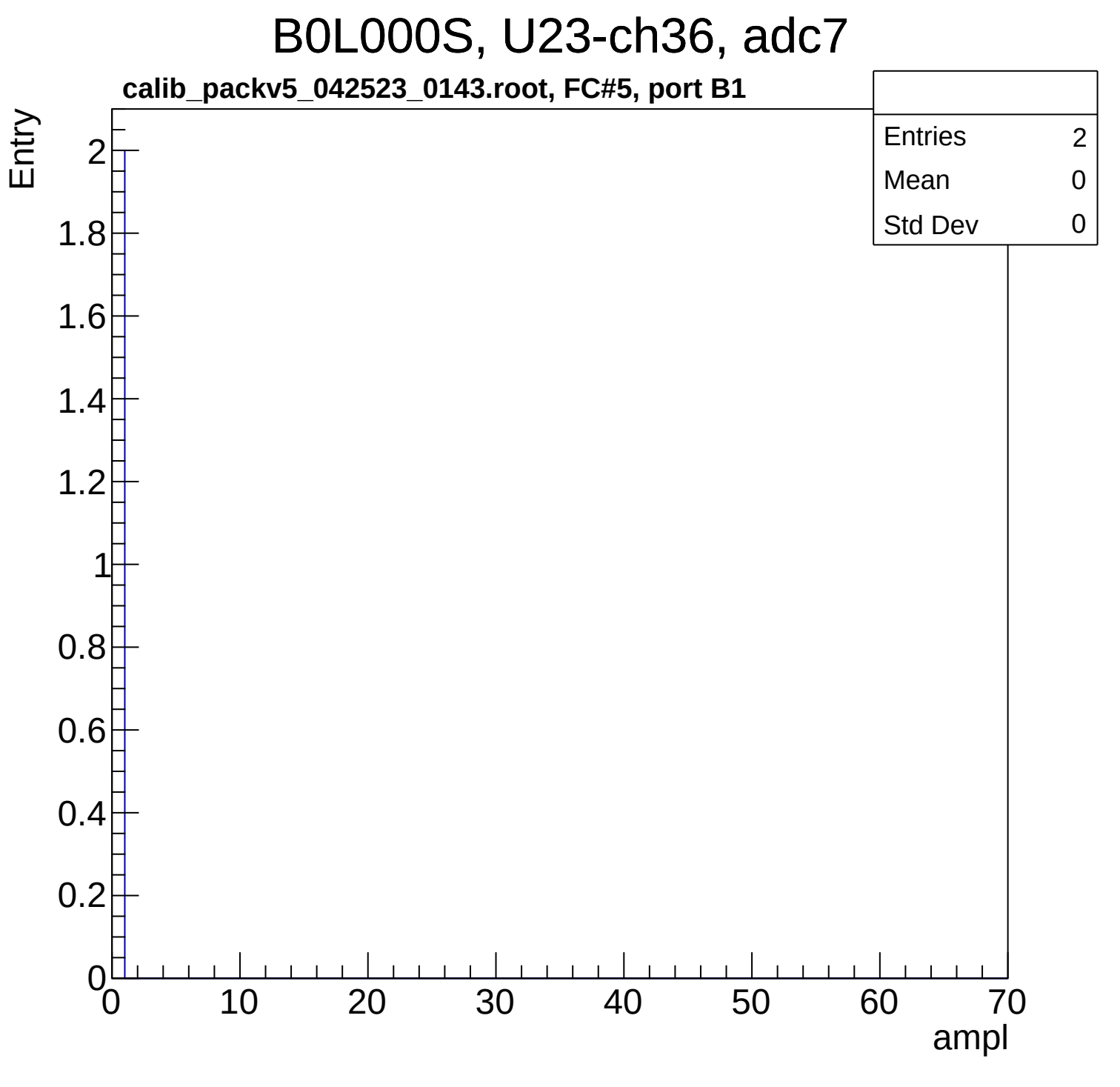

B0L000S, U23-ch36, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.2
Std Dev 2.856
Gaus mean : 42.7214
Gaus Width: 3.1609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

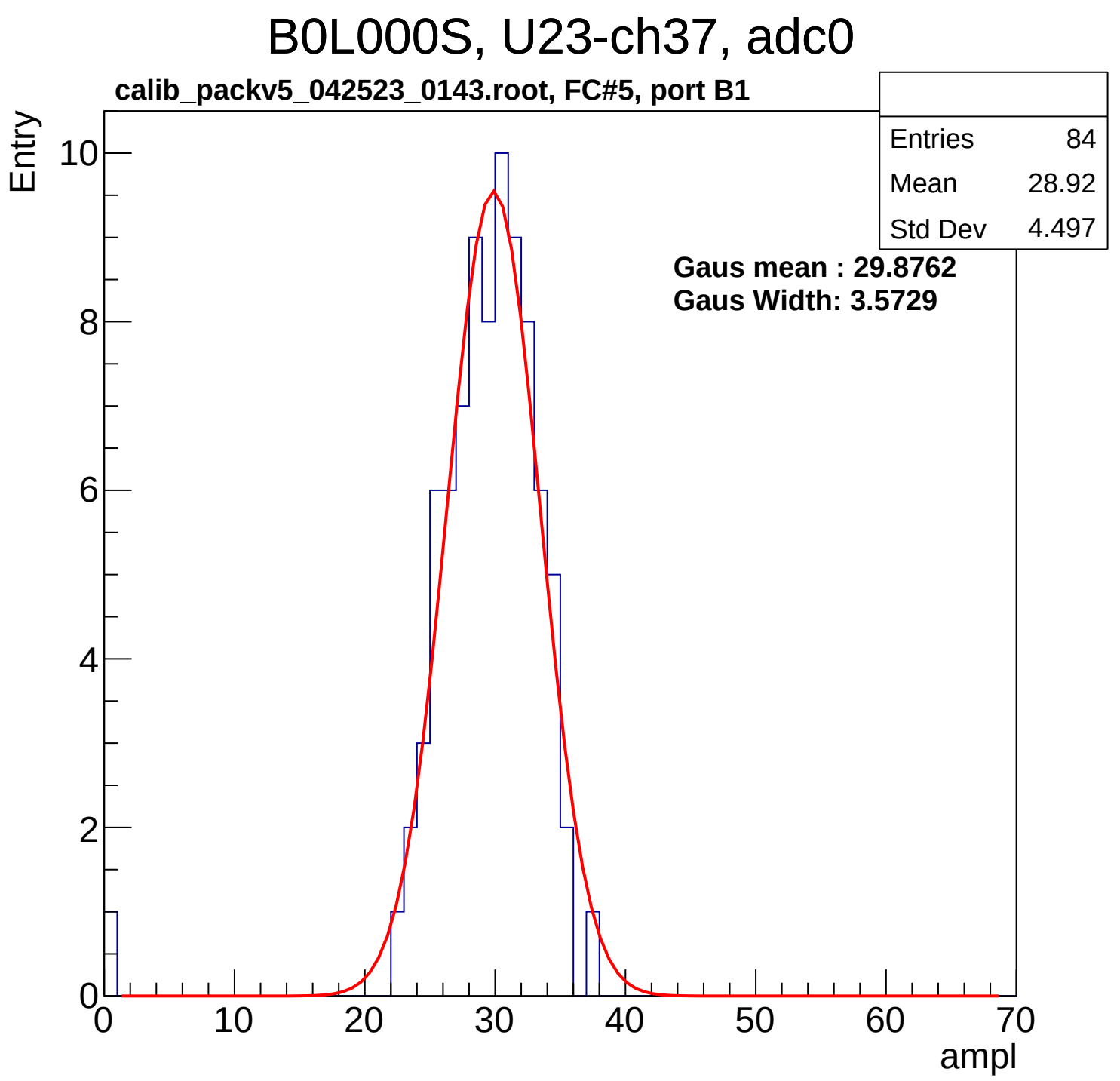

B0L000S, U23-ch36, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.64
Std Dev 3.083
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

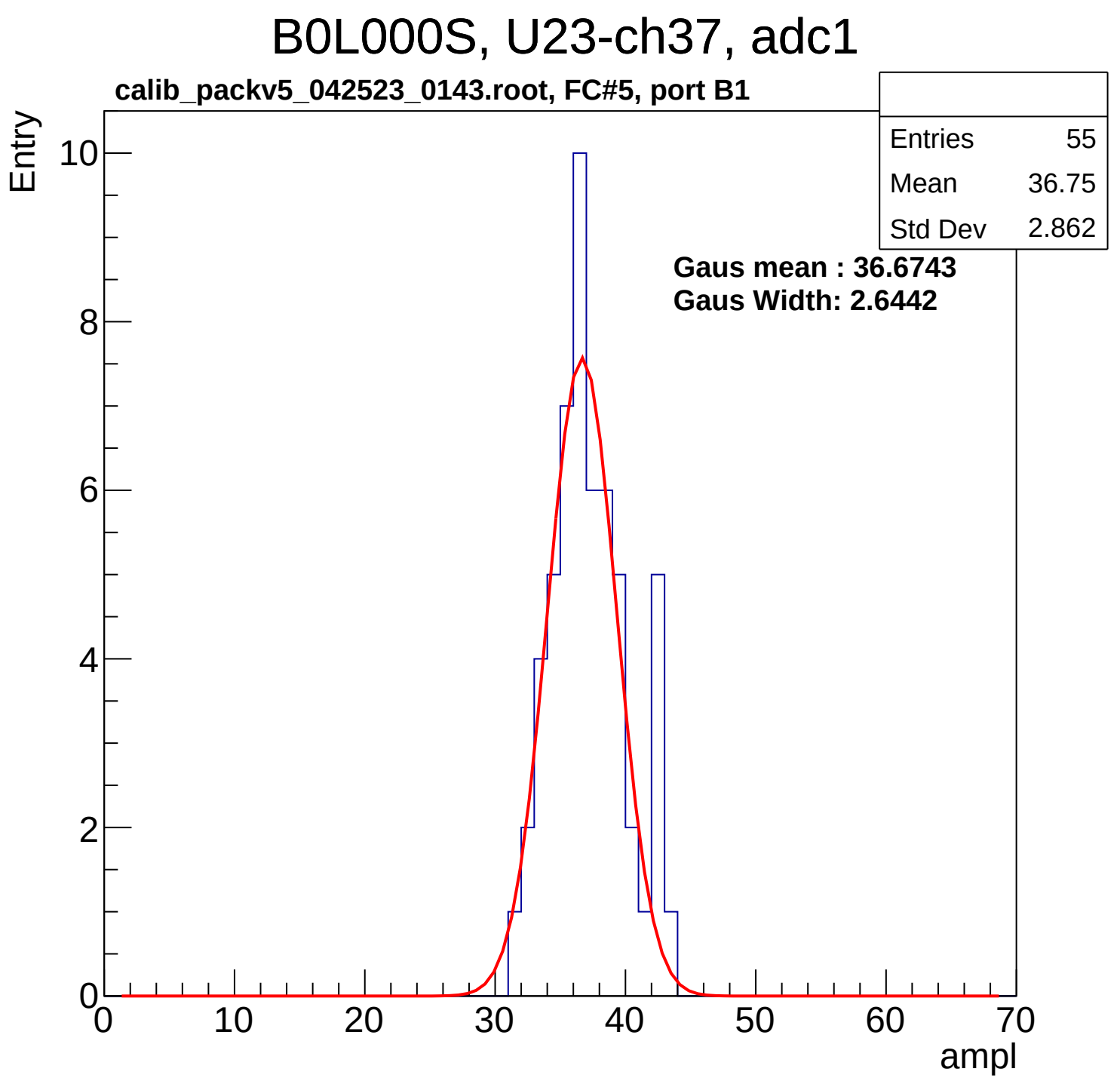

B0L000S, U23-ch36, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 53.88
Std Dev 3.207
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

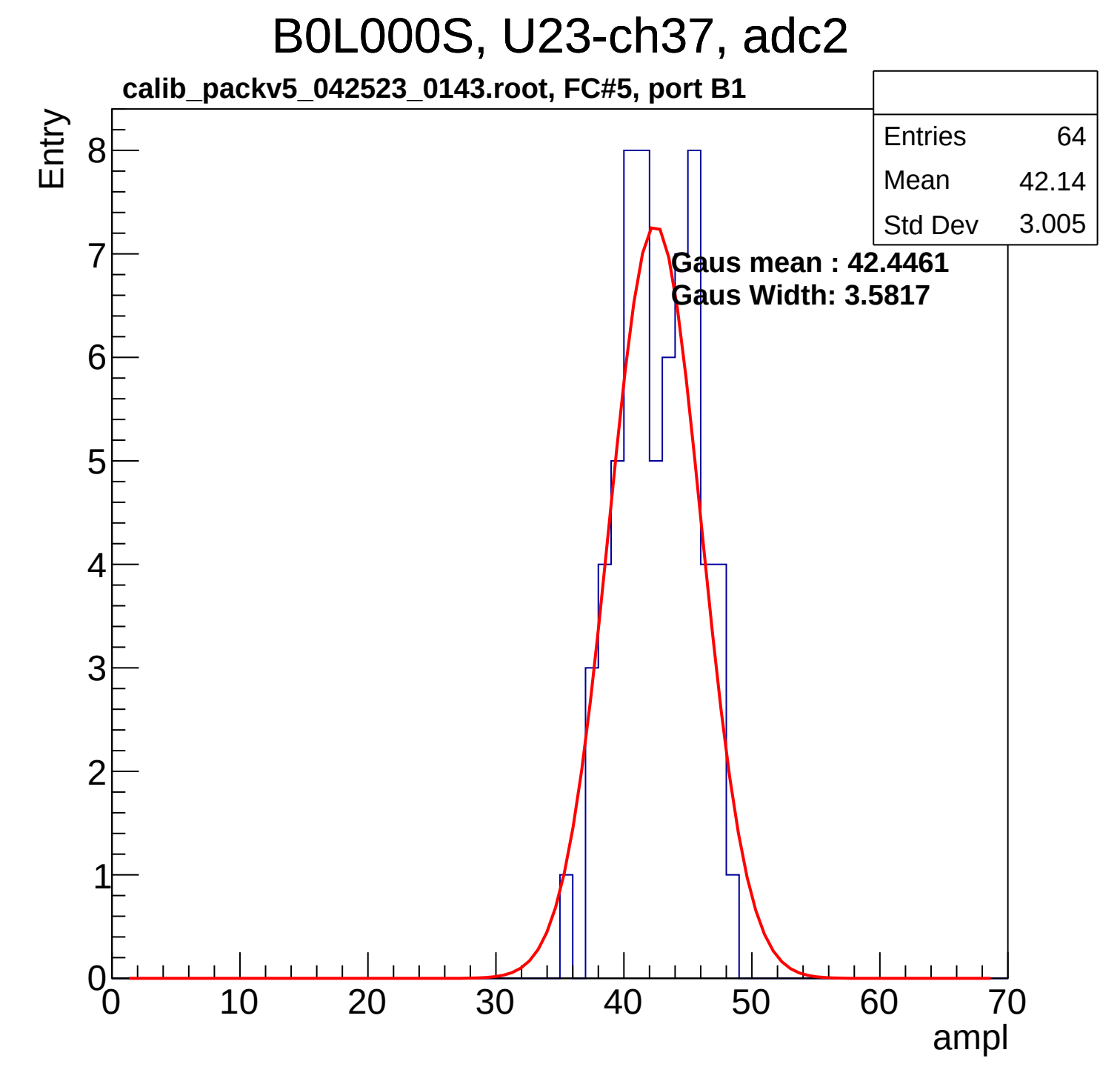

B0L000S, U23-ch36, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 59.25
Std Dev 2.576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

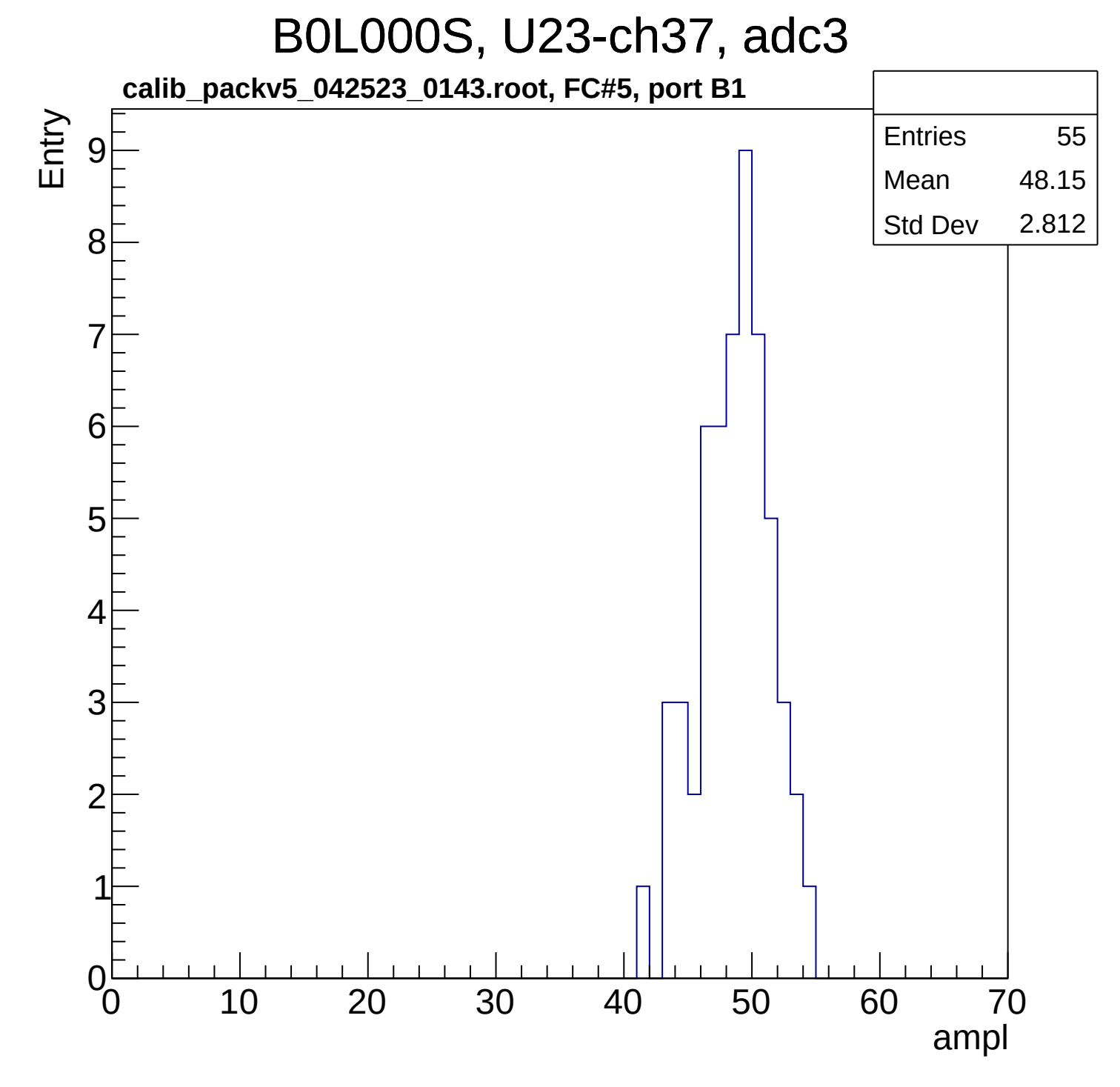

B0L000S, U23-ch36, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.14
Std Dev 15.92
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

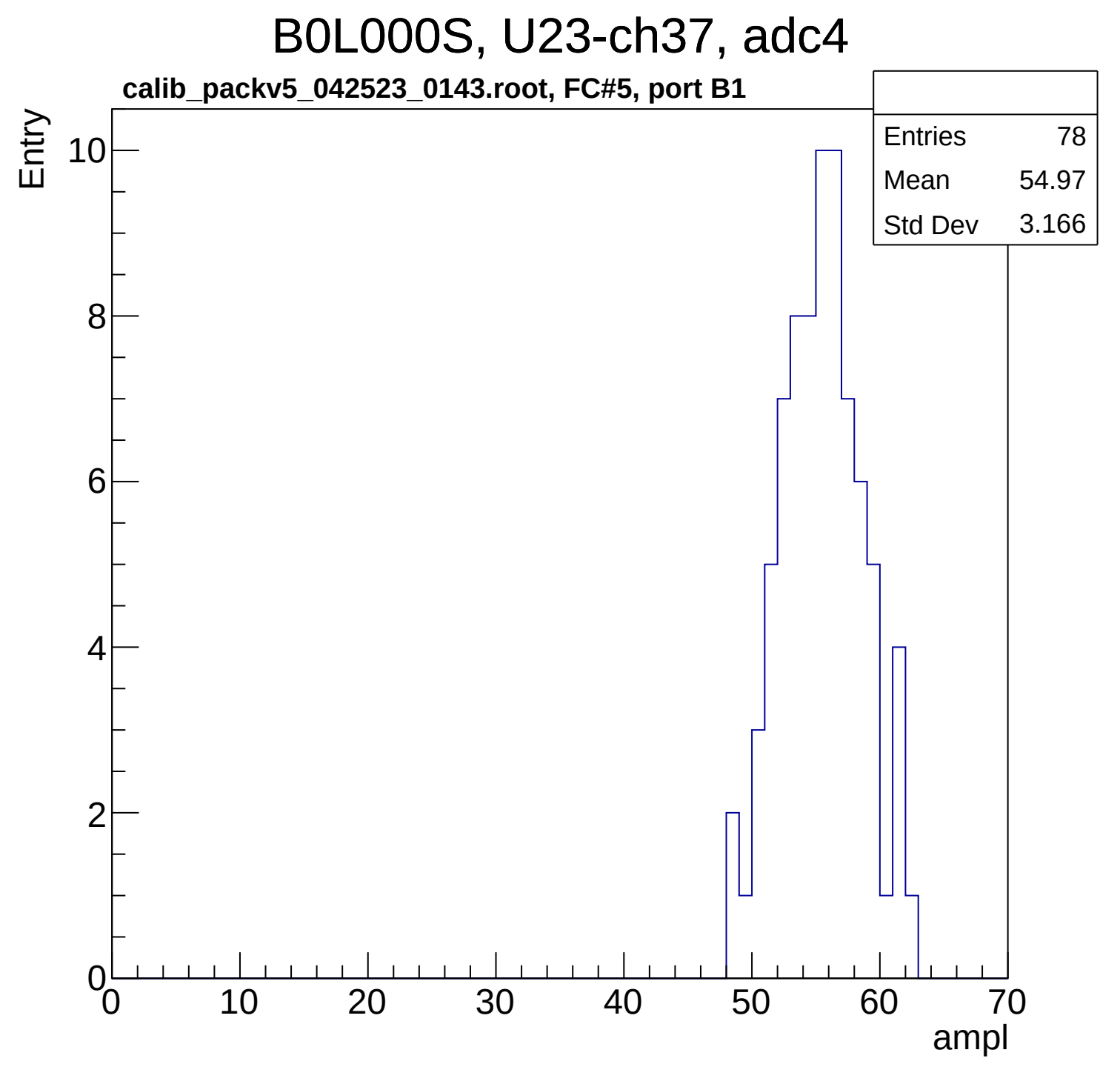

B0L000S, U23-ch36, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

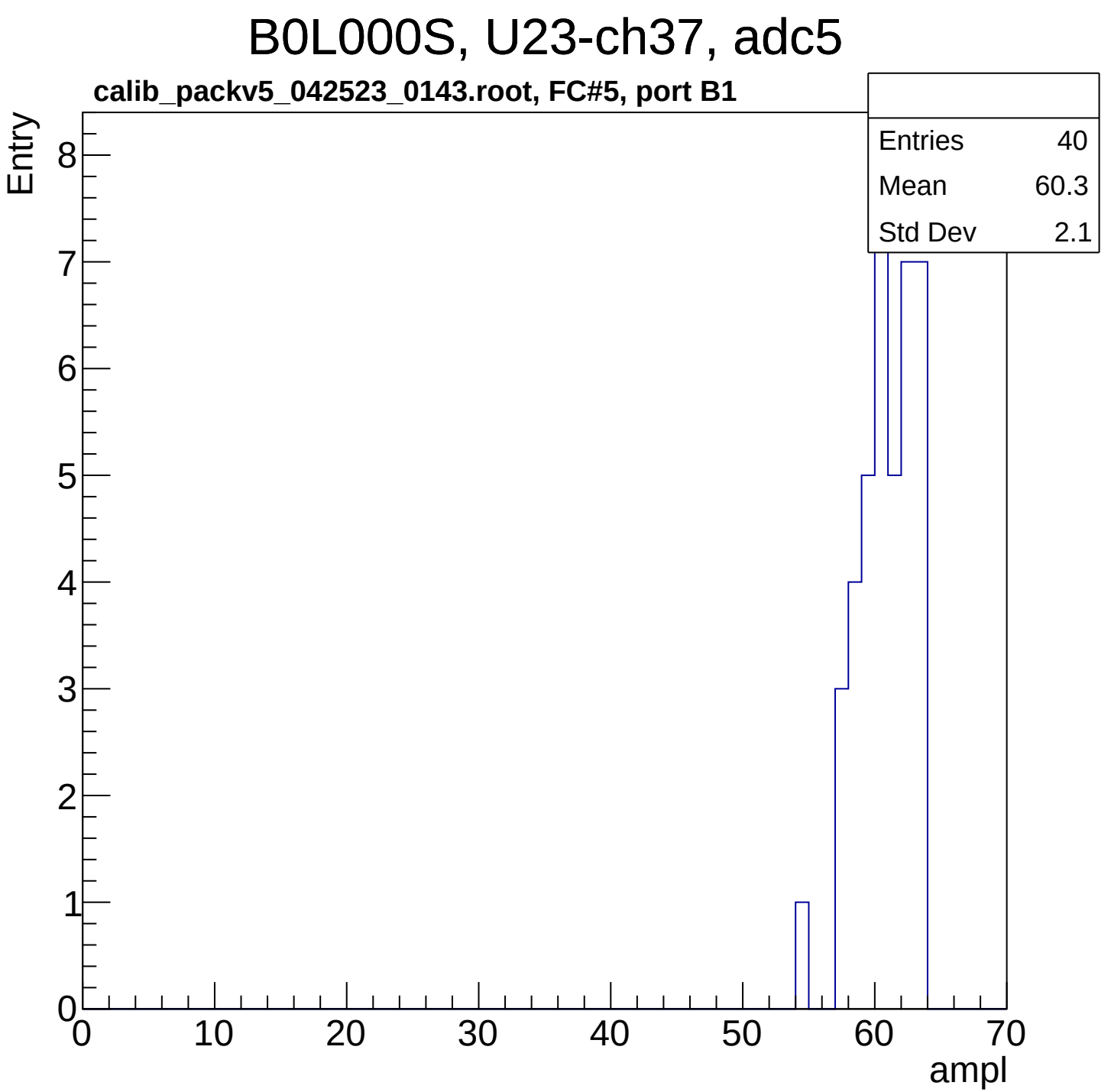

B0L000S, U23-ch37, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 28.92
Std Dev 4.497
Gaus mean : 29.8762
Gaus Width: 3.5729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

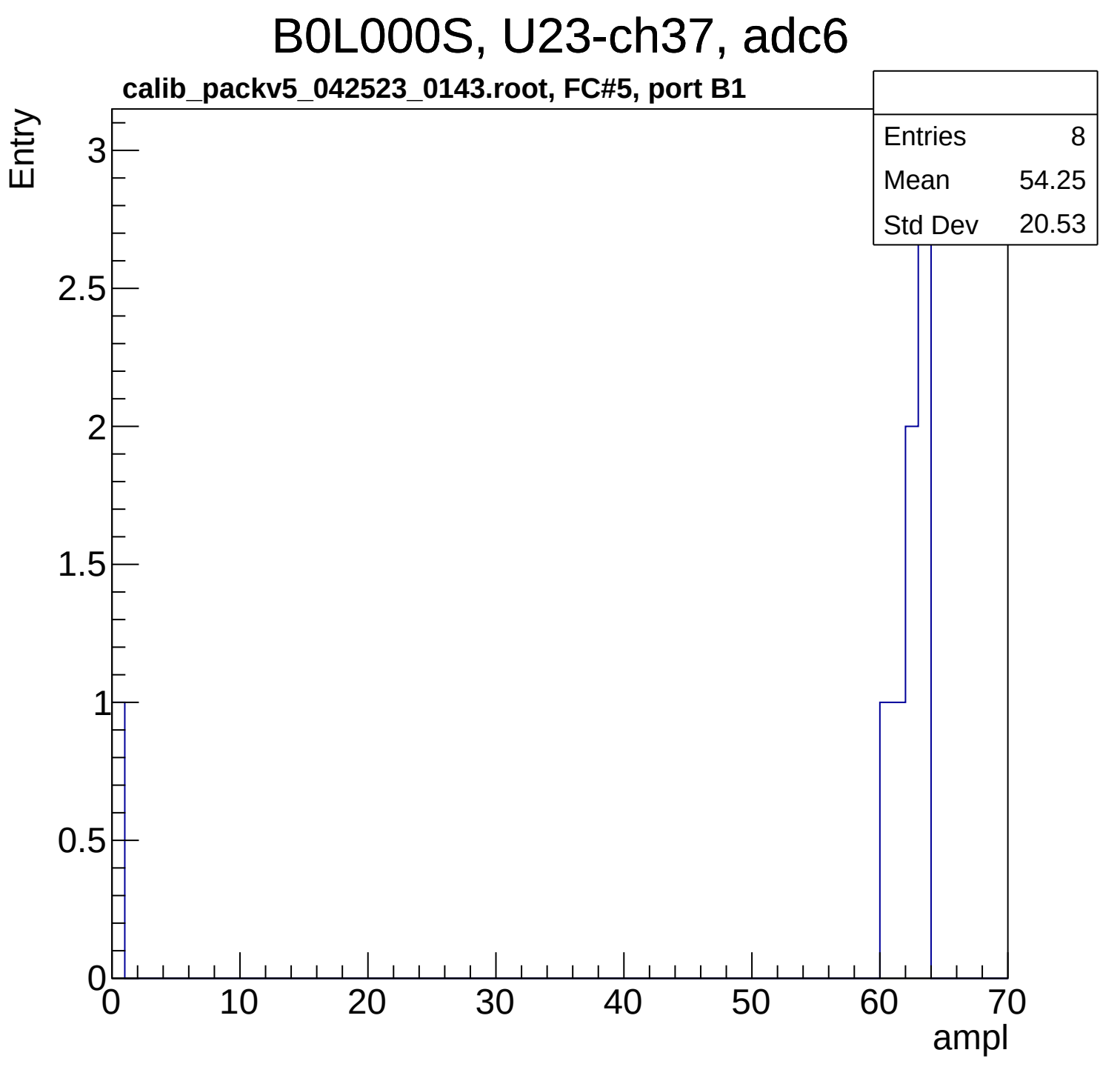

B0L000S, U23-ch37, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 36.75
Std Dev 2.862
Gaus mean : 36.6743
Gaus Width: 2.6442
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

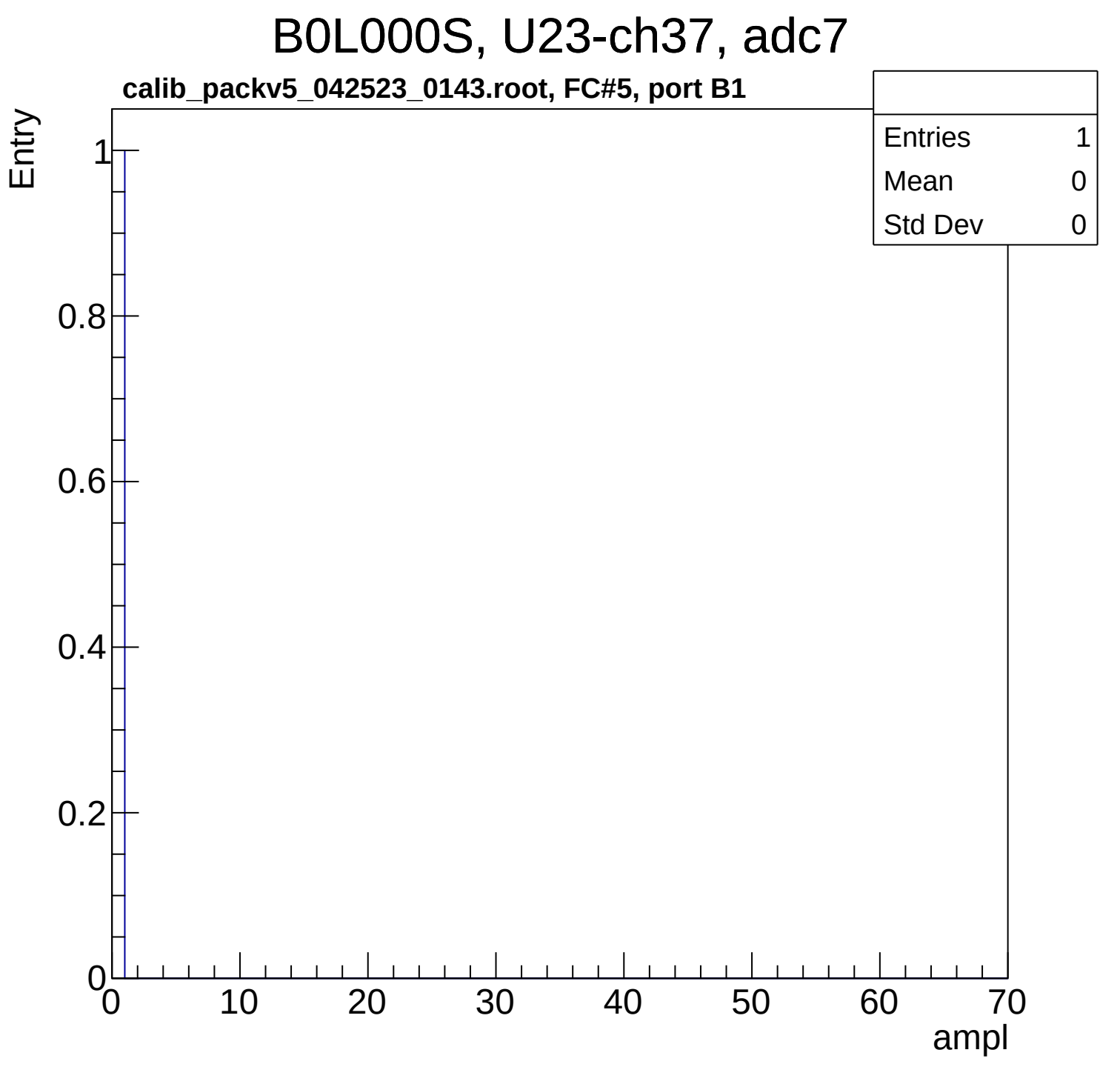

B0L000S, U23-ch37, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.14
Std Dev 3.005
Gaus mean : 42.4461
Gaus Width: 3.5817
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

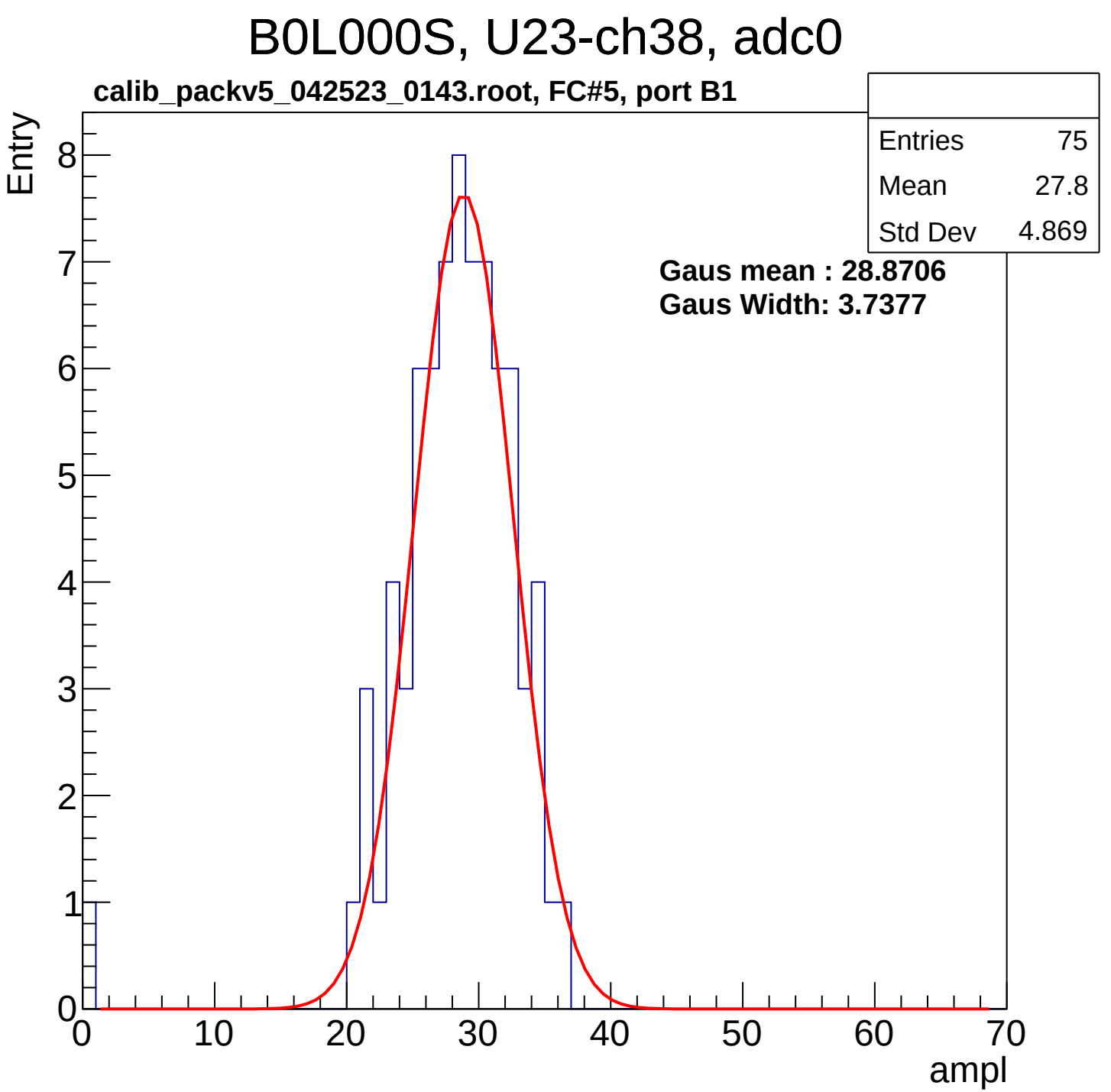

B0L000S, U23-ch37, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 48.15
Std Dev 2.812
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

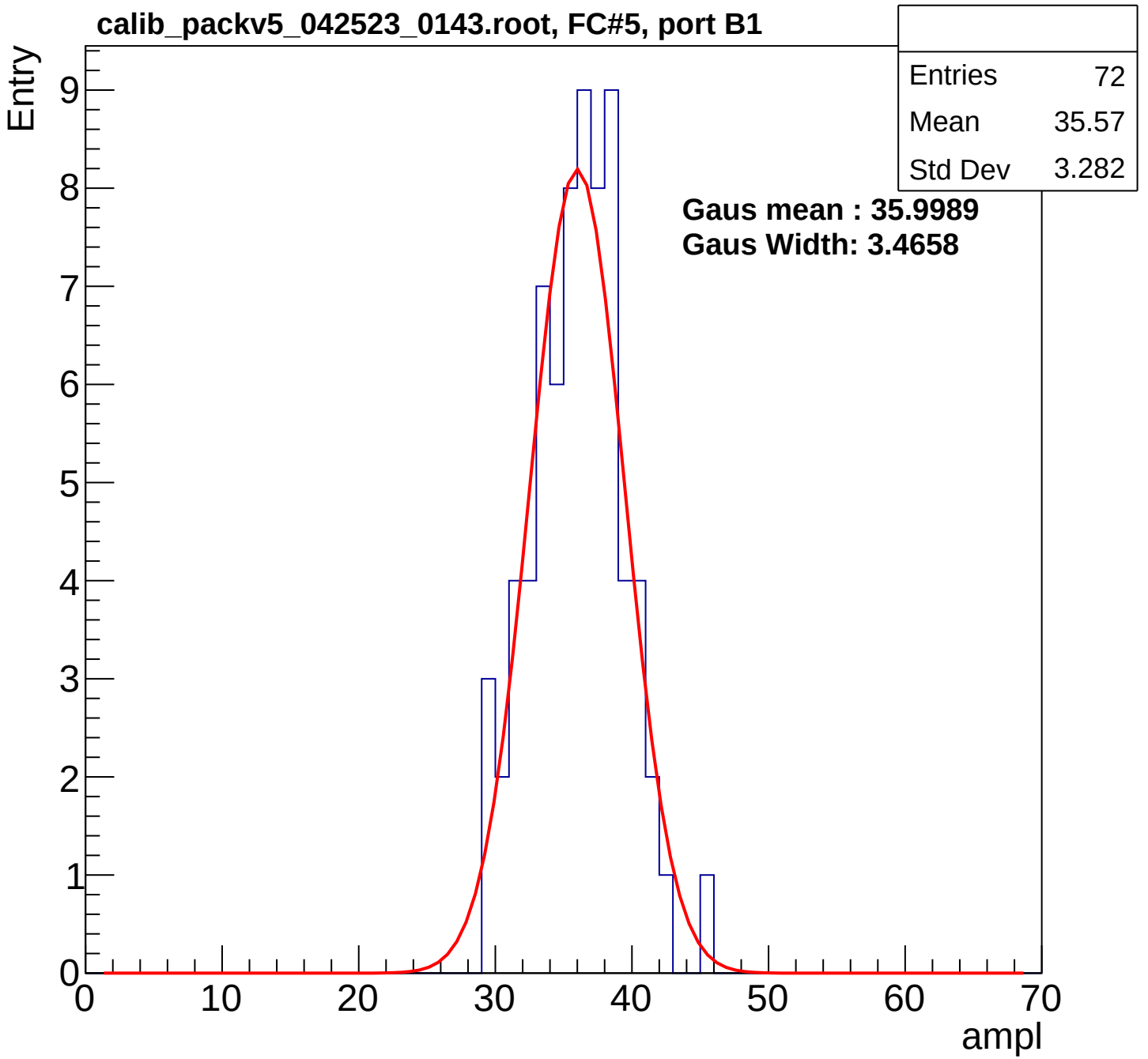

B0L000S, U23-ch37, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 54.97
Std Dev 3.166
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

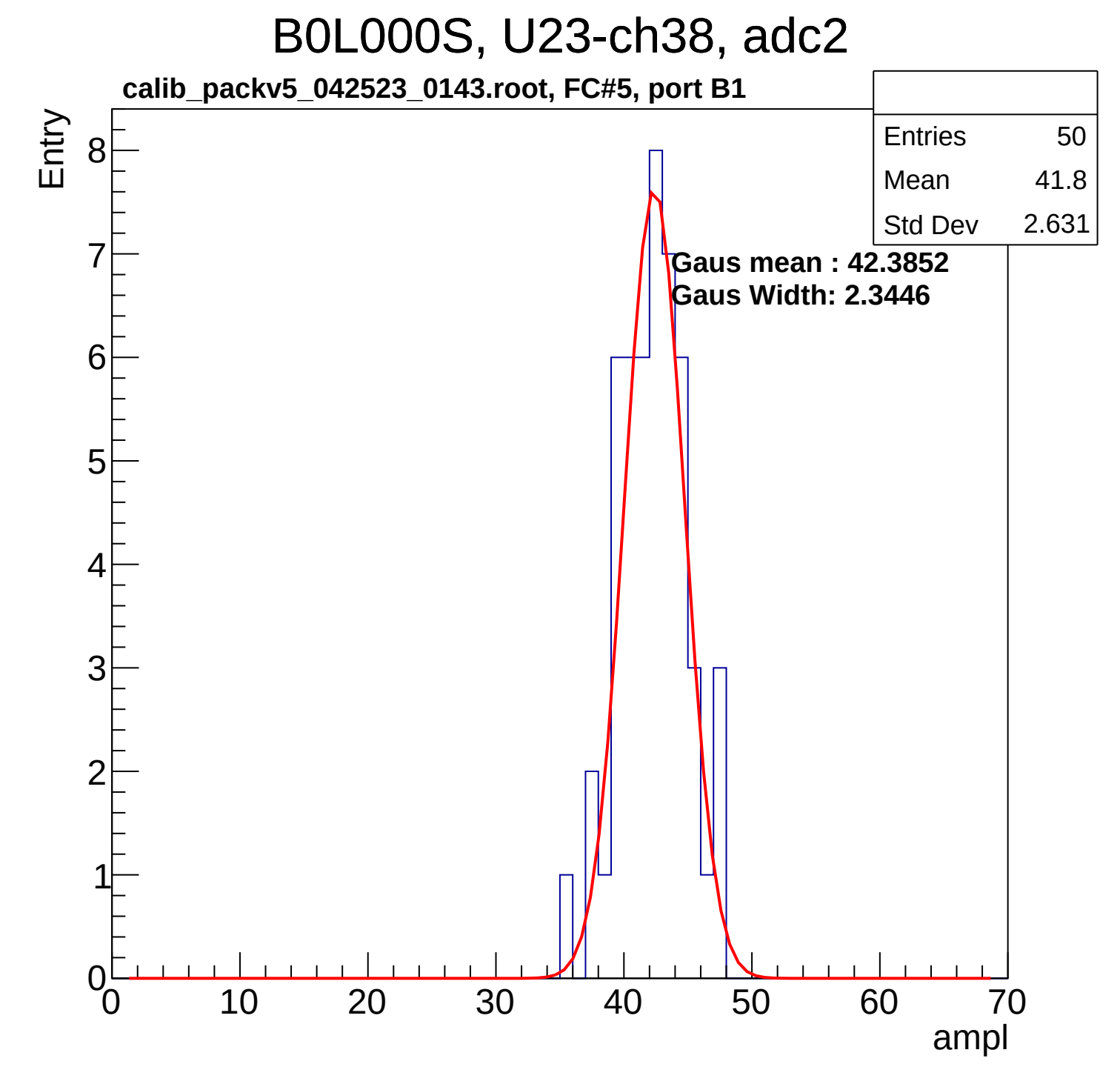

B0L000S, U23-ch37, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 60.3
Std Dev 2.1
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

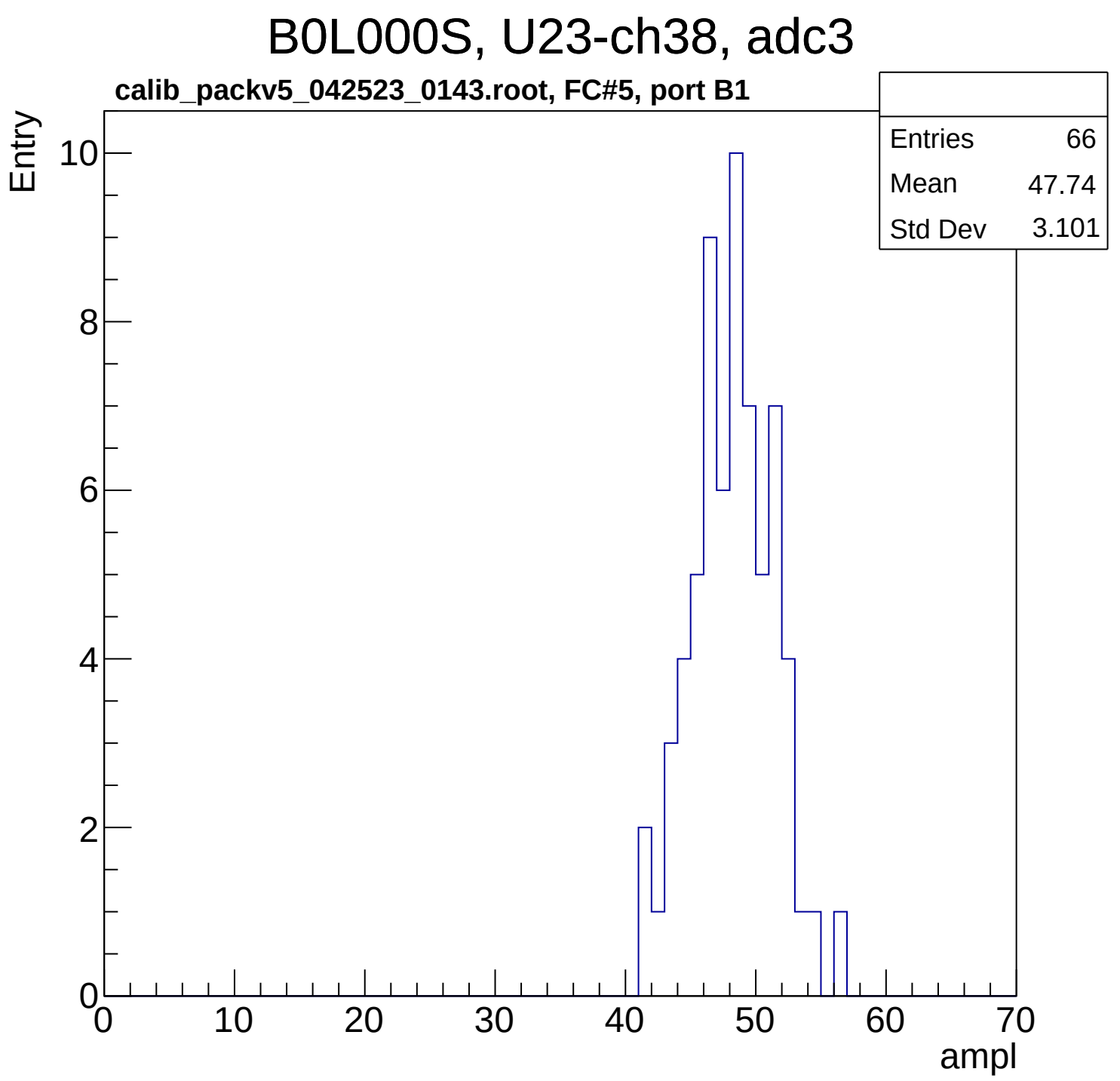

B0L000S, U23-ch37, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.25
Std Dev 20.53
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

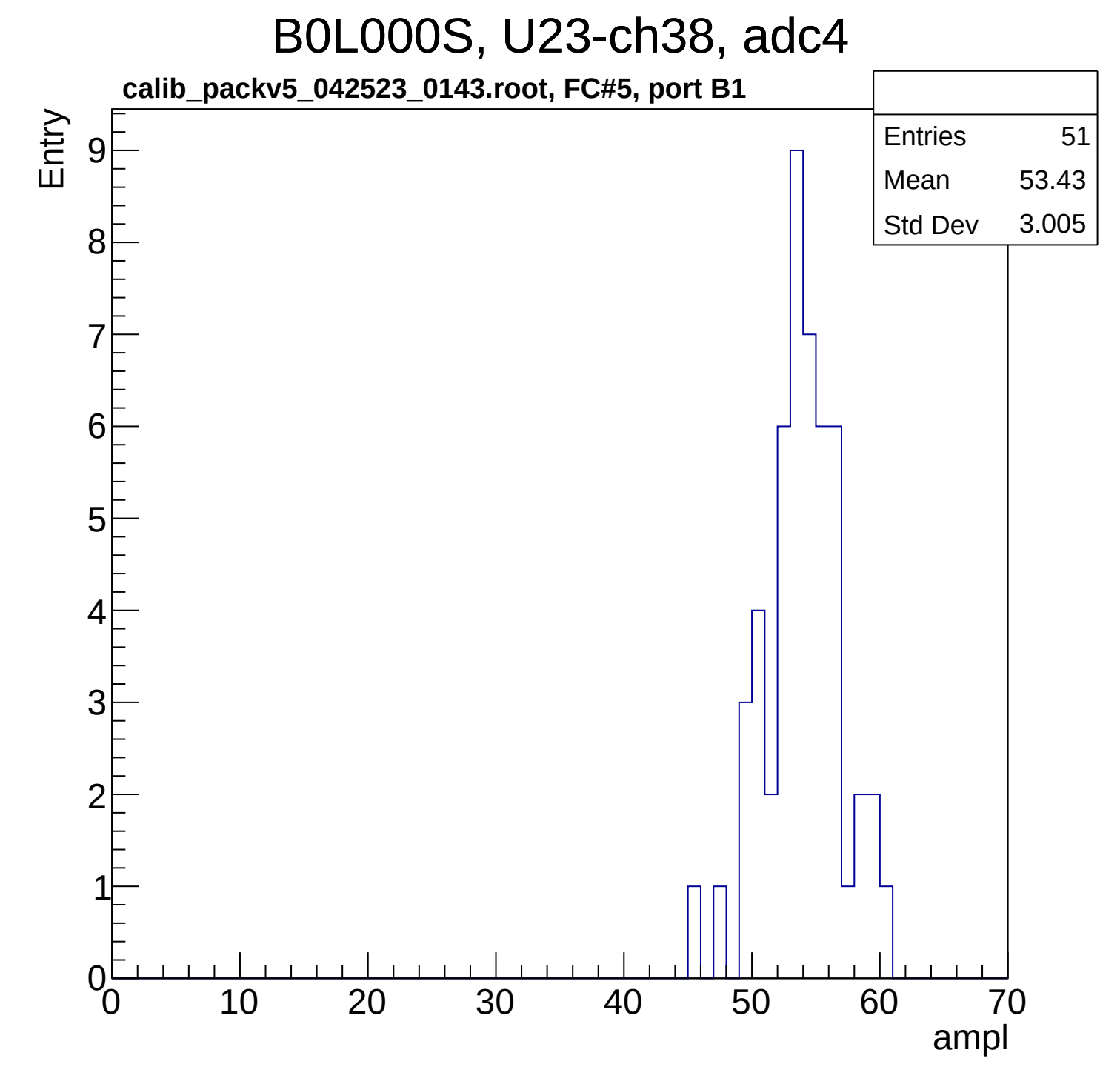

B0L000S, U23-ch37, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

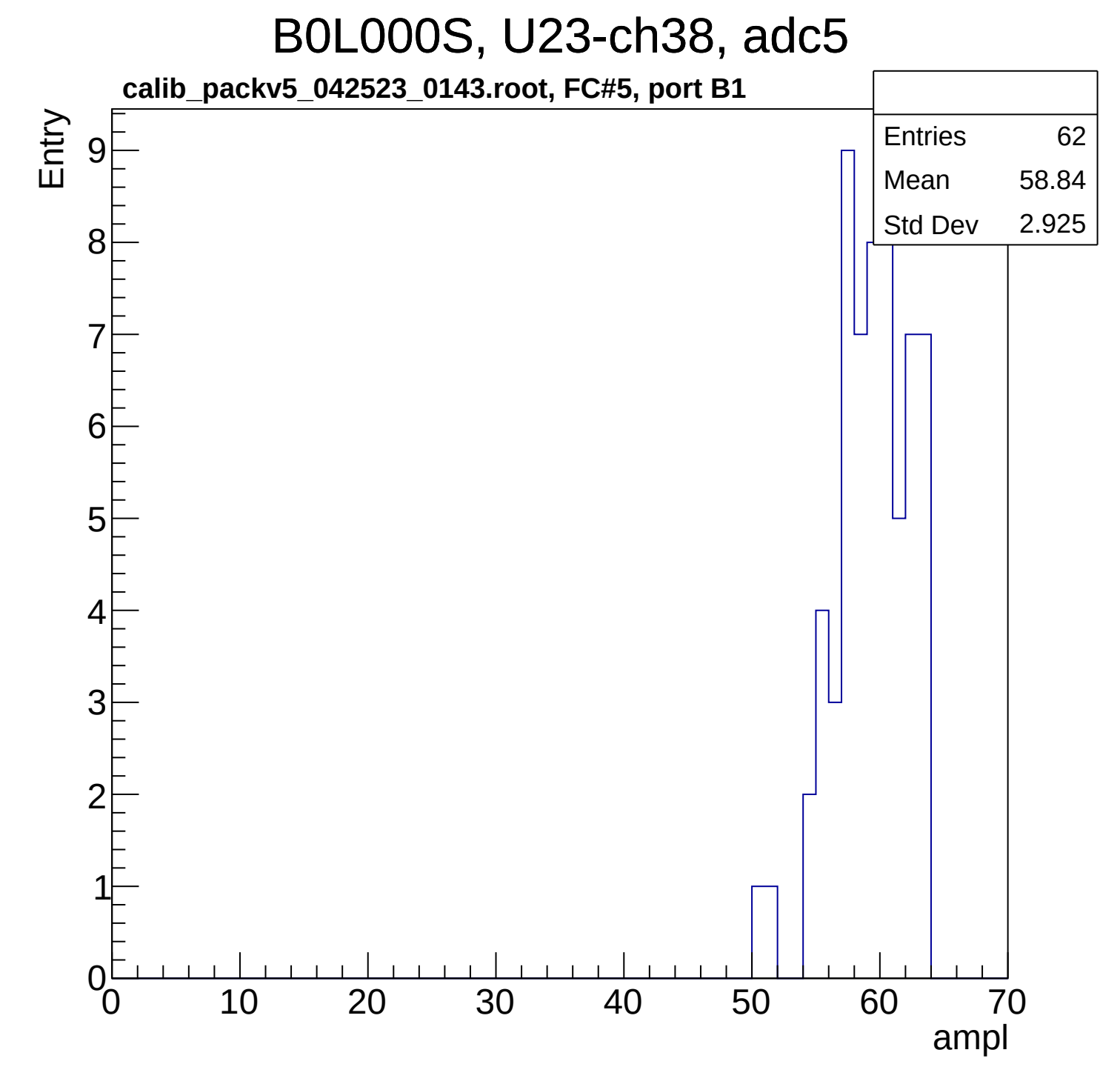

B0L000S, U23-ch38, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 27.8
Std Dev 4.869
Gaus mean : 28.8706
Gaus Width: 3.7377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

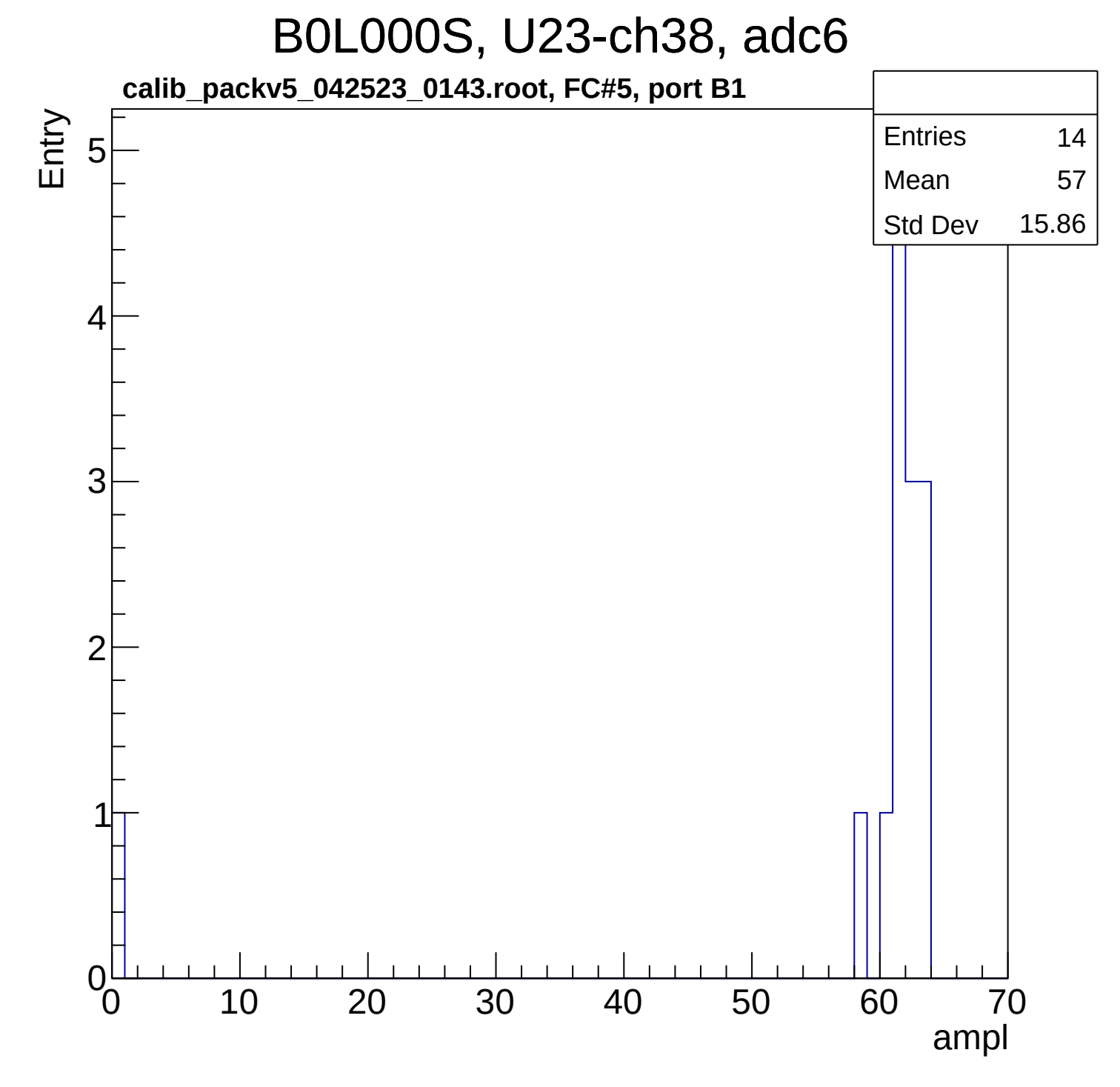

B0L000S, U23-ch38, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.57
Std Dev 3.282
Gaus mean : 35.9989
Gaus Width: 3.4658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

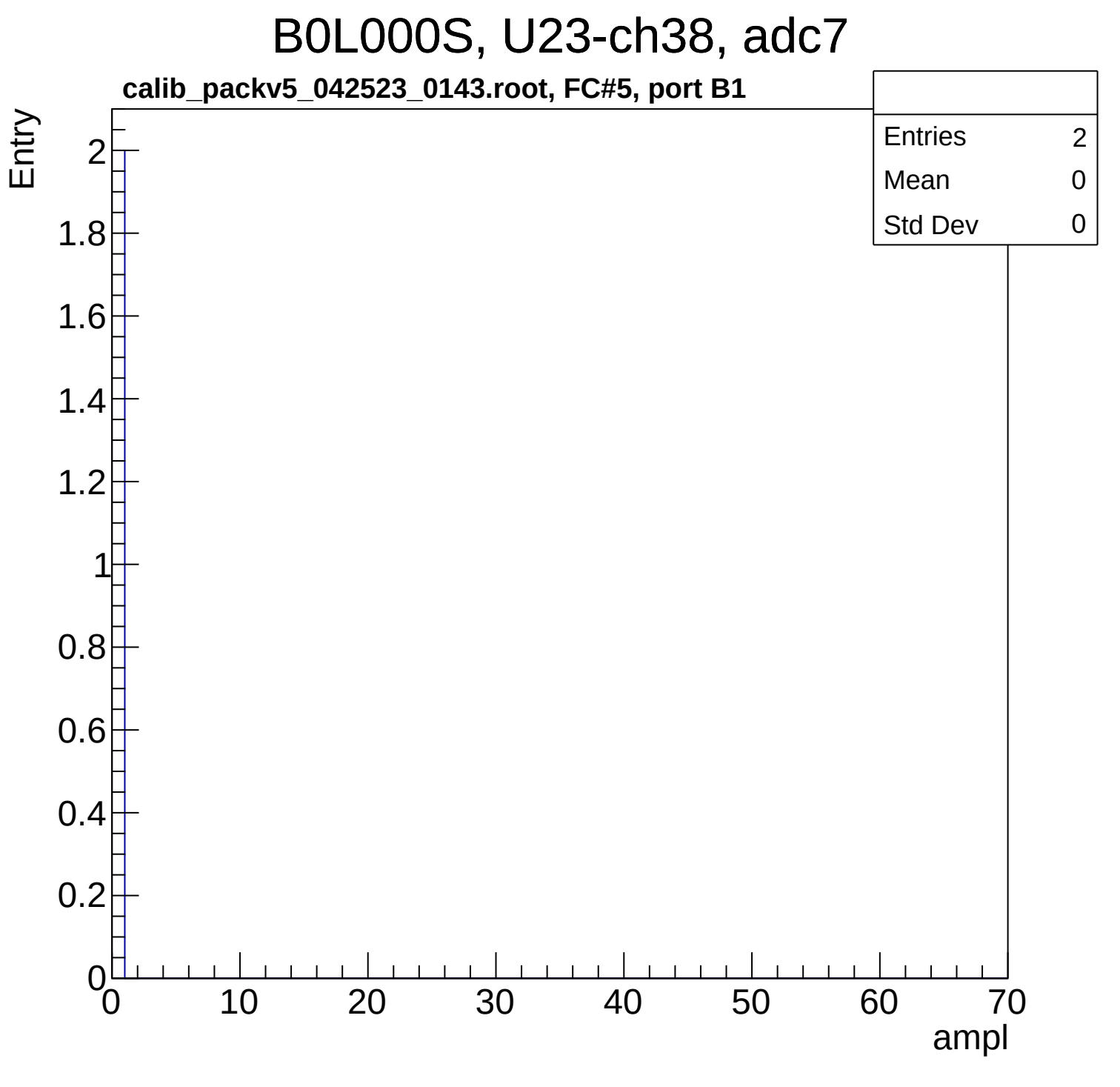

B0L000S, U23-ch38, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 41.8
Std Dev 2.631
Gaus mean : 42.3852
Gaus Width: 2.3446
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

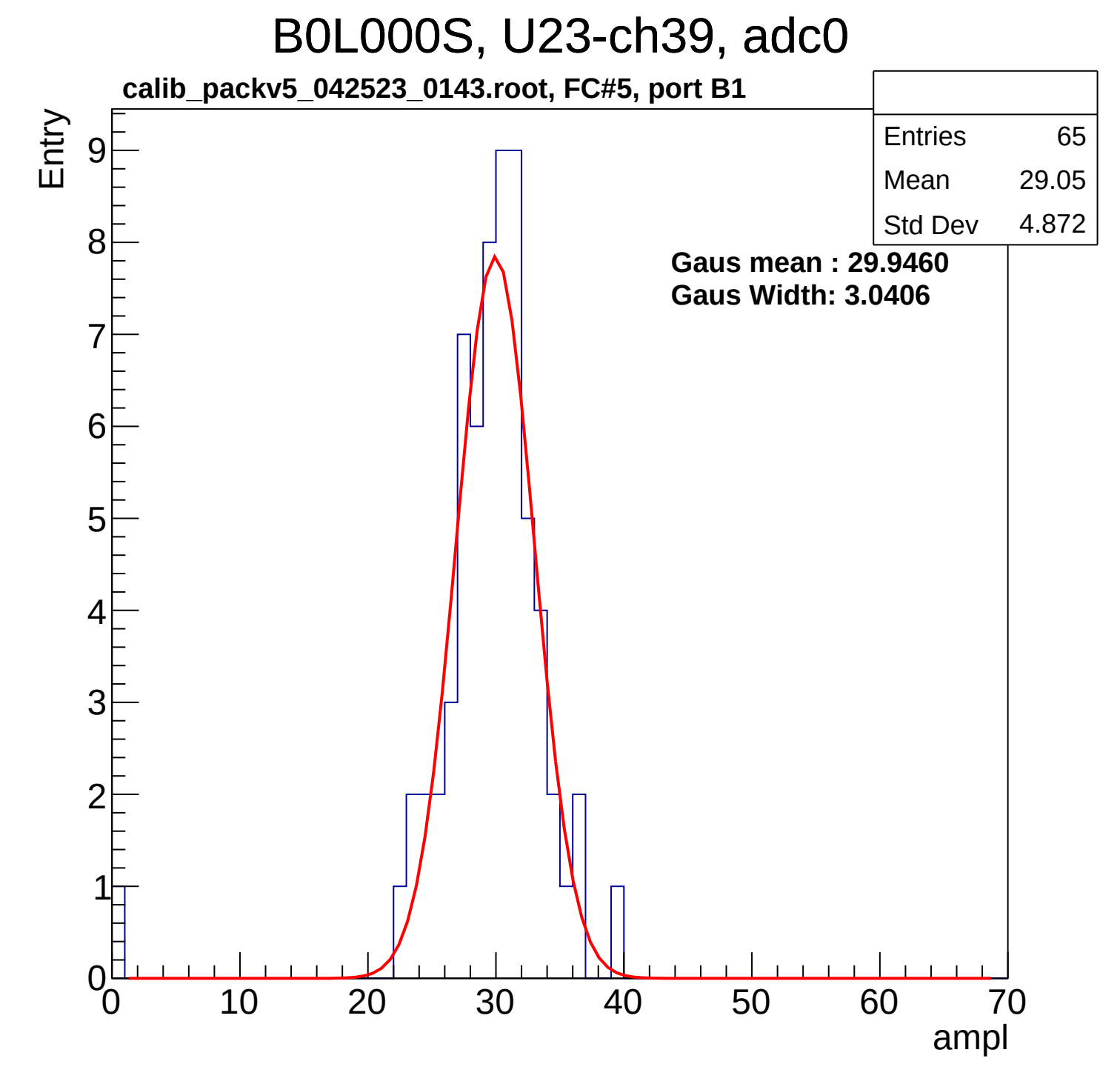

B0L000S, U23-ch38, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 47.74
Std Dev 3.101
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

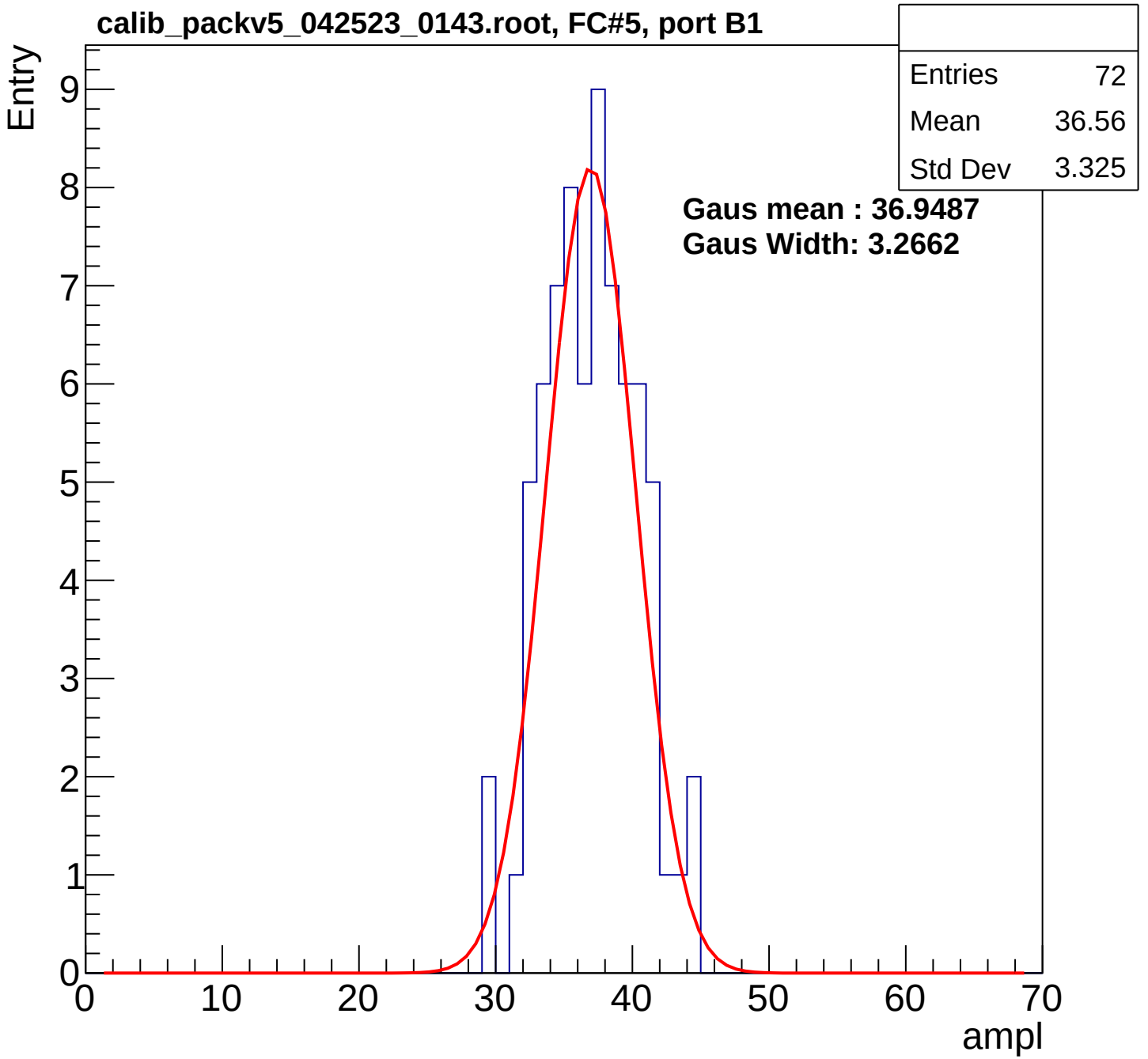

B0L000S, U23-ch38, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 53.43
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

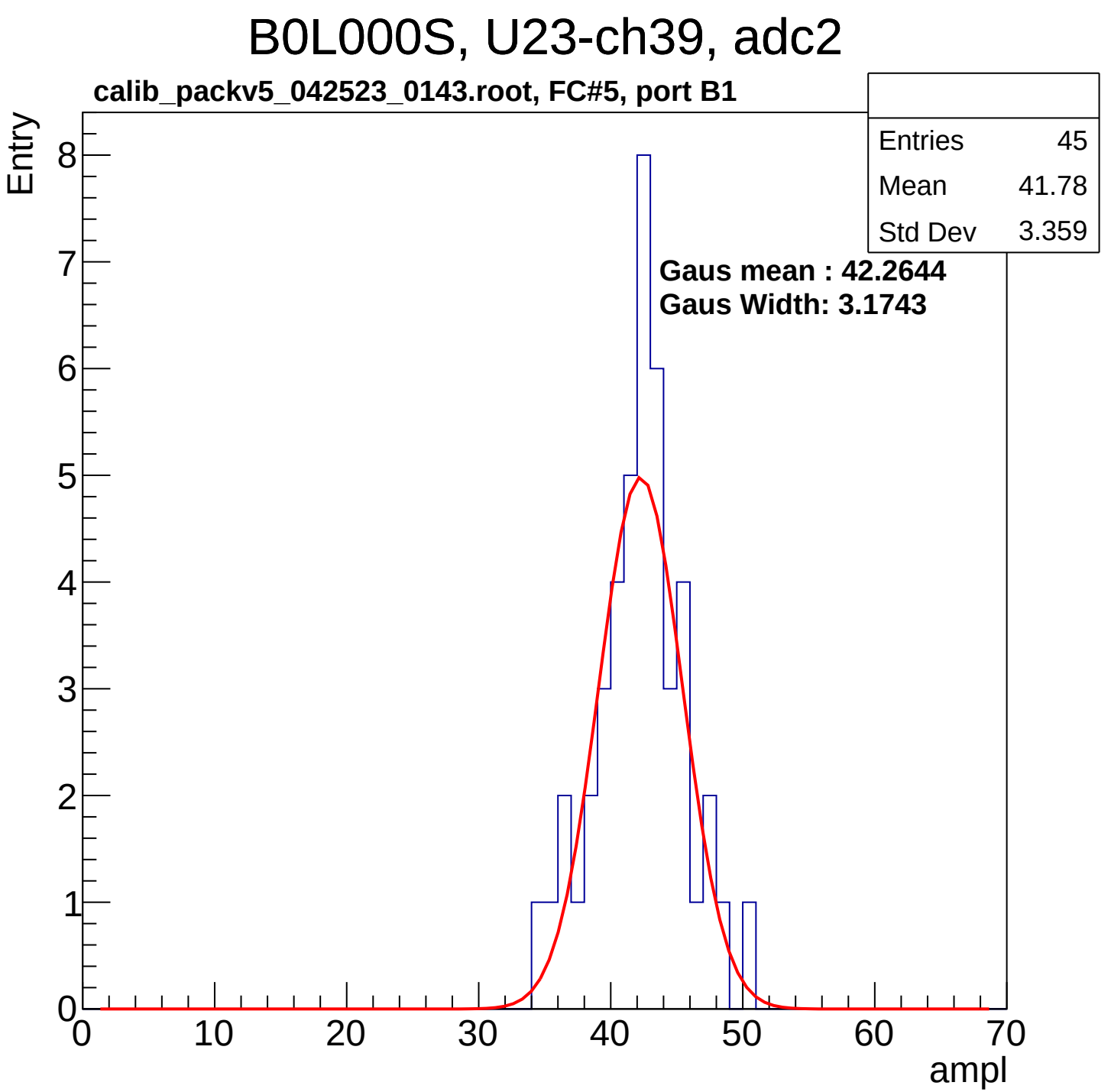

B0L000S, U23-ch38, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 58.84
Std Dev 2.925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

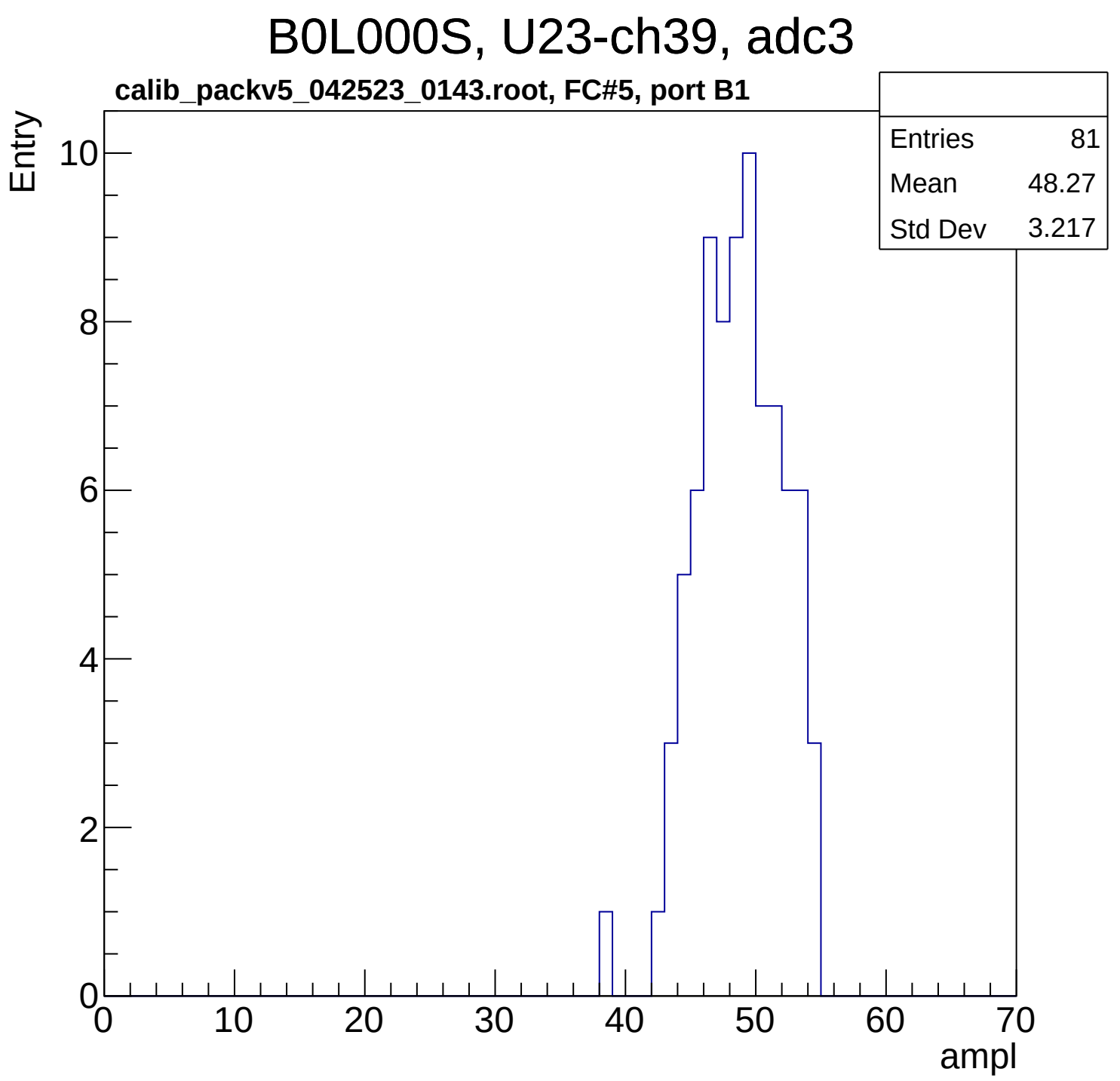

B0L000S, U23-ch38, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57
Std Dev 15.86
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

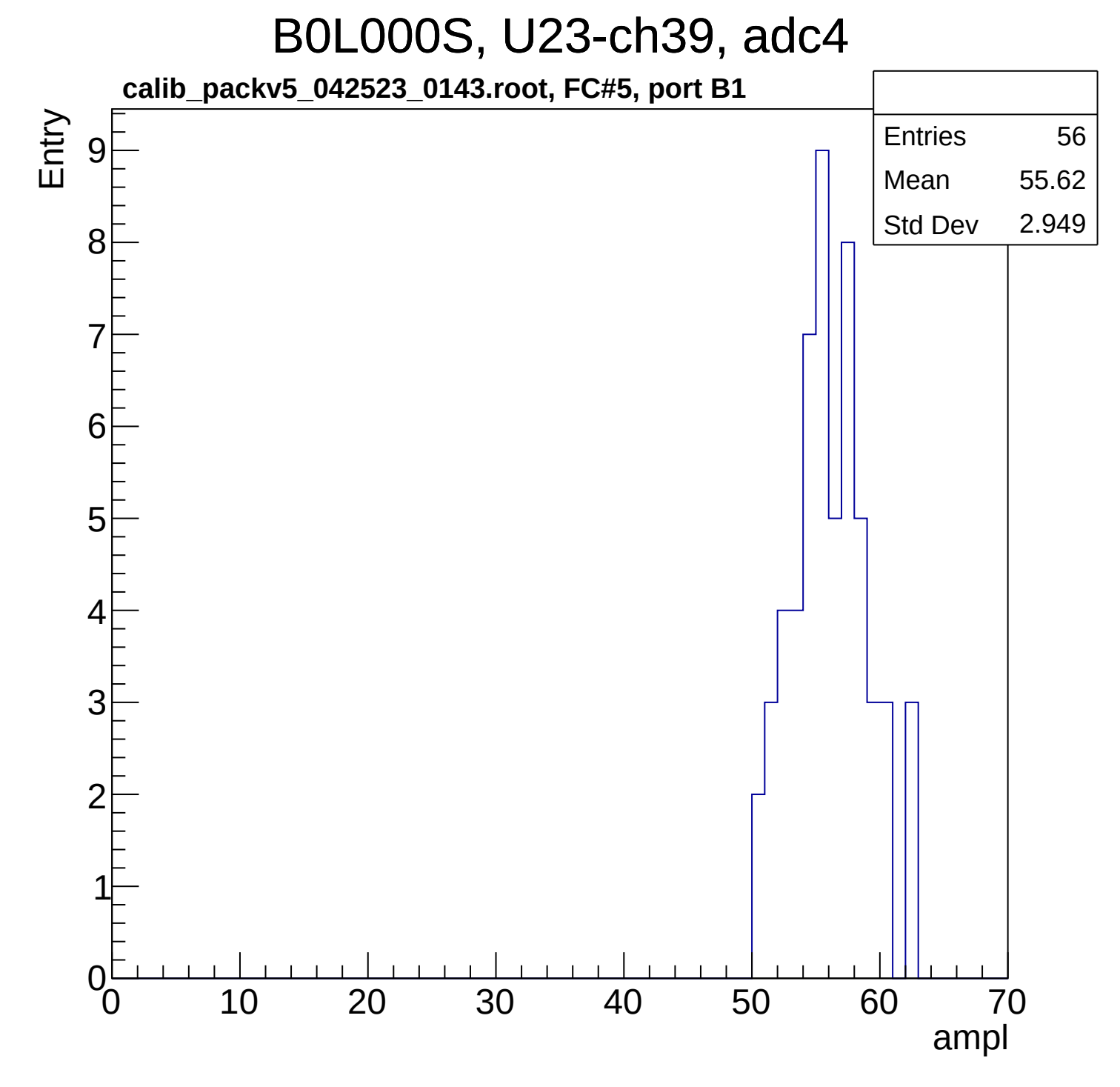

B0L000S, U23-ch38, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

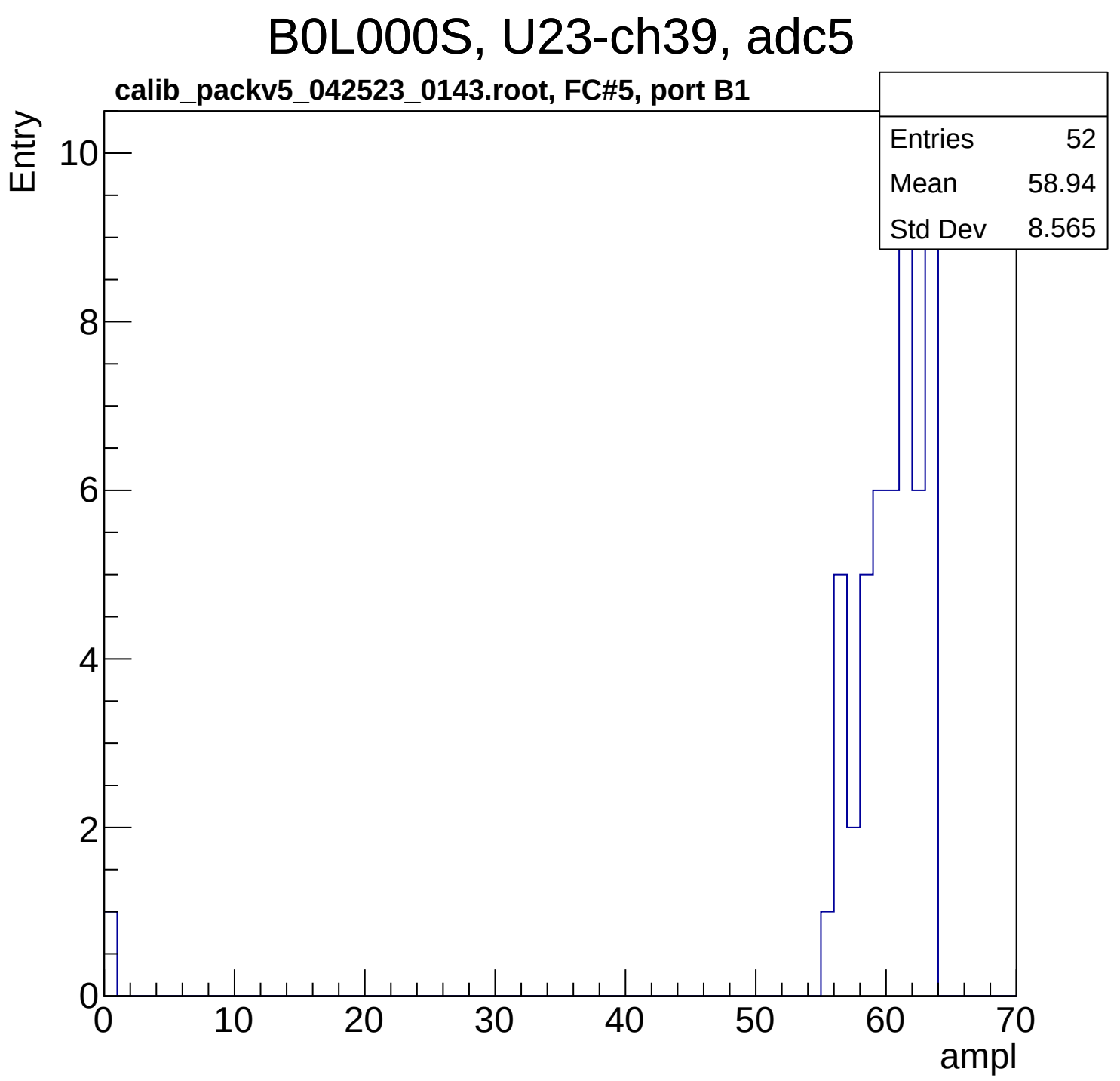

B0L000S, U23-ch39, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.05
Std Dev 4.872
Gaus mean : 29.9460
Gaus Width: 3.0406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

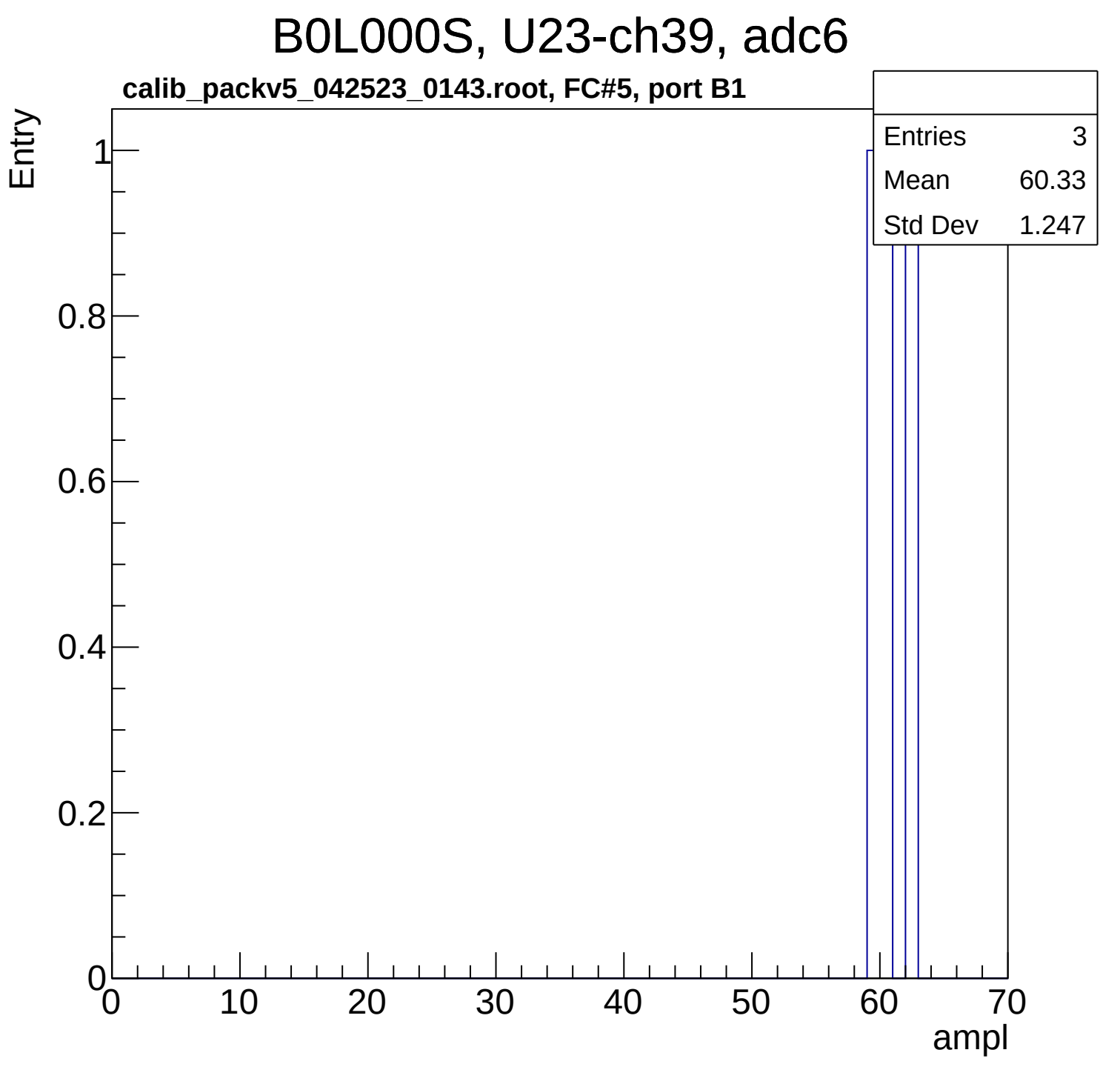

B0L000S, U23-ch39, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.56
Std Dev 3.325
Gaus mean : 36.9487
Gaus Width: 3.2662
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

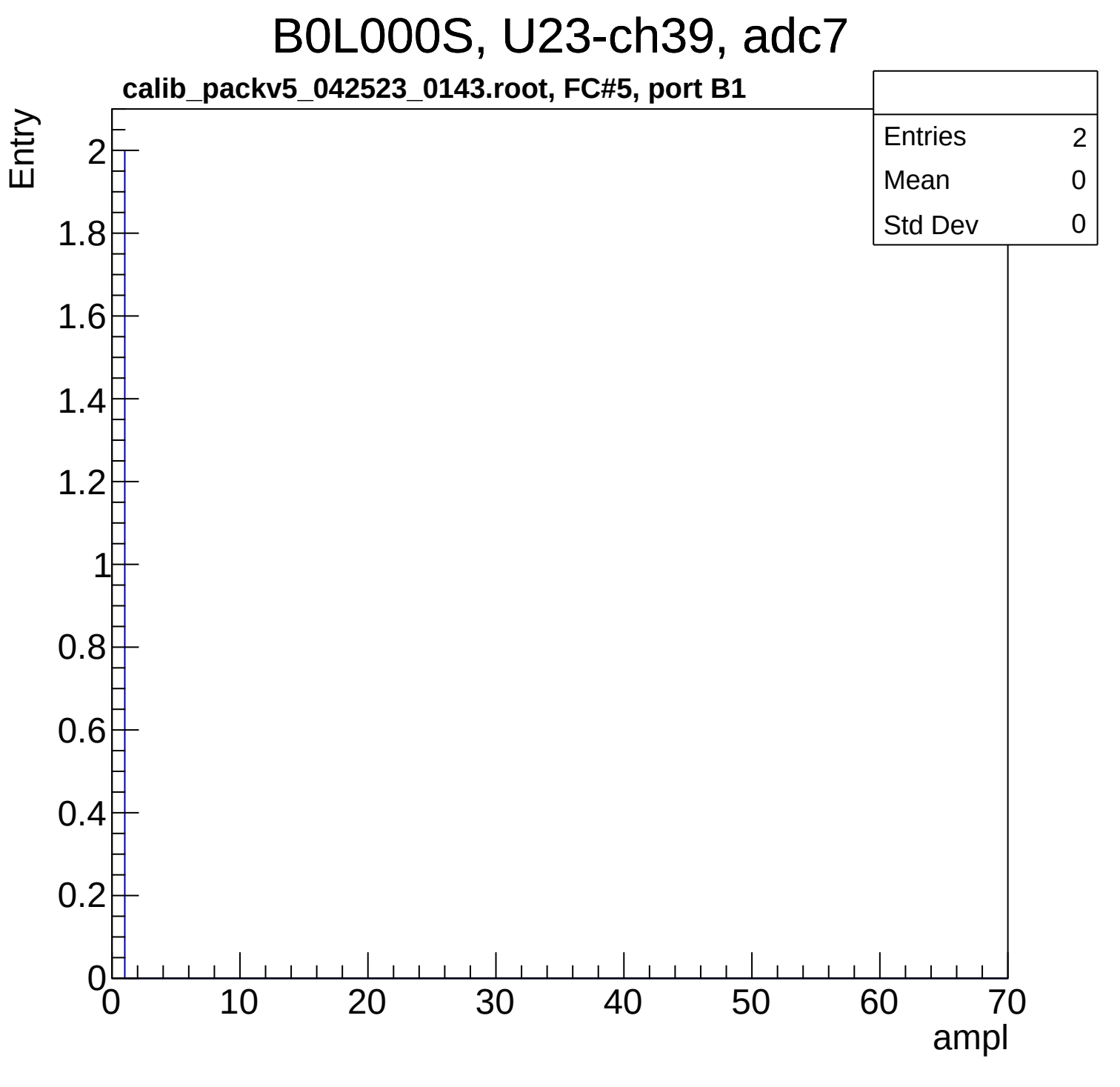

B0L000S, U23-ch39, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 41.78
Std Dev 3.359
Gaus mean : 42.2644
Gaus Width: 3.1743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

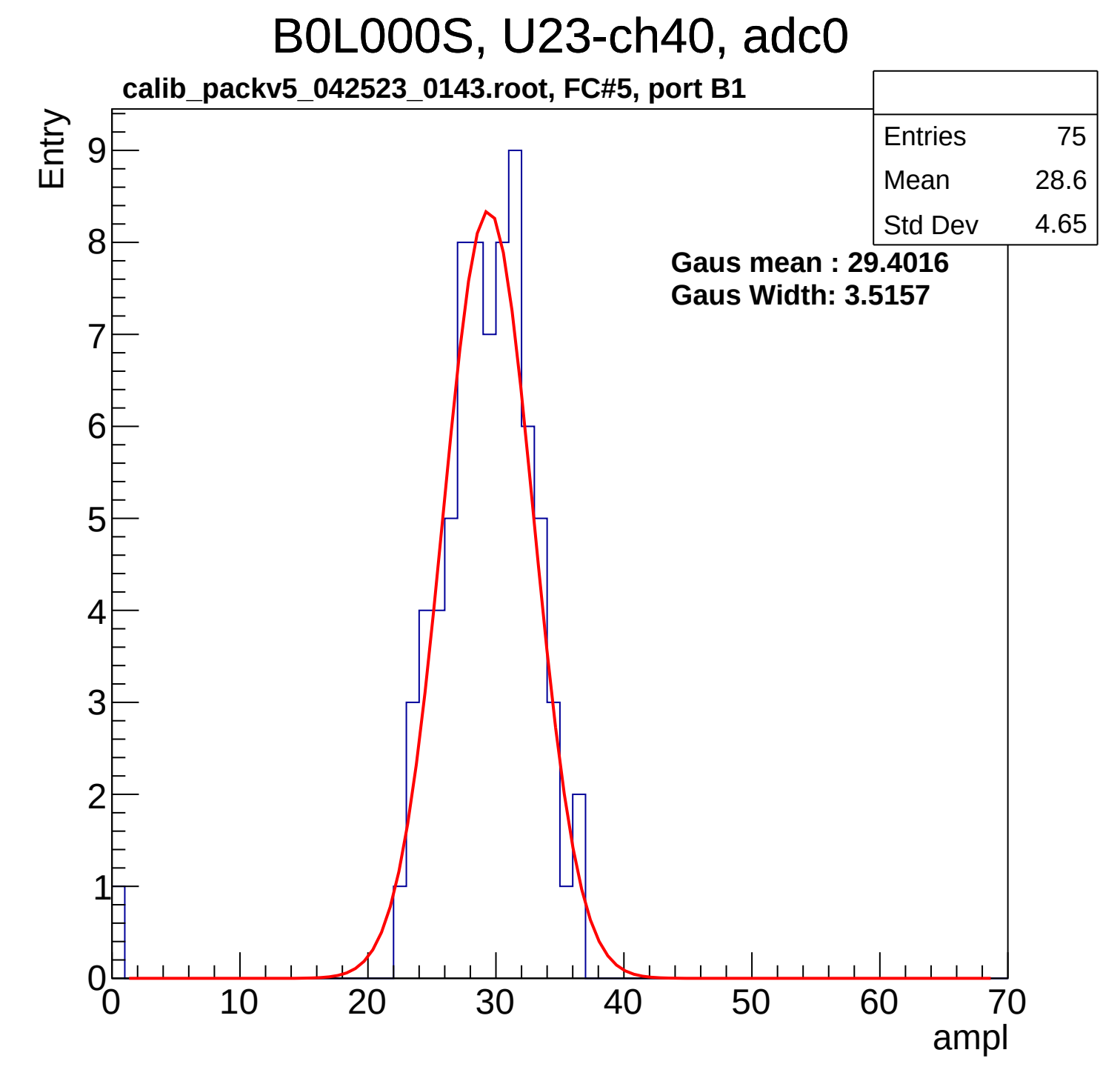

B0L000S, U23-ch39, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 48.27
Std Dev 3.217
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

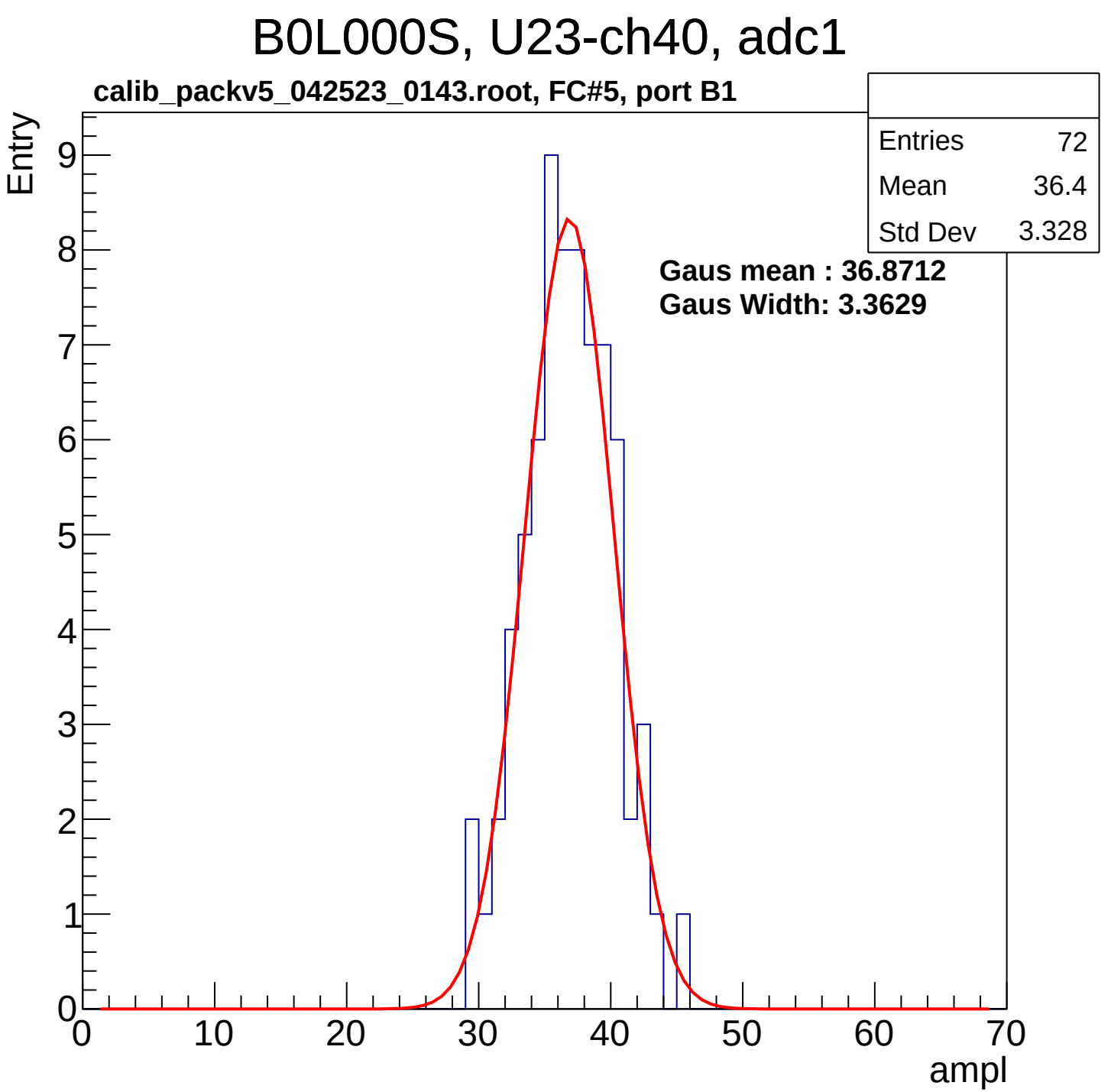

B0L000S, U23-ch39, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.62
Std Dev 2.949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

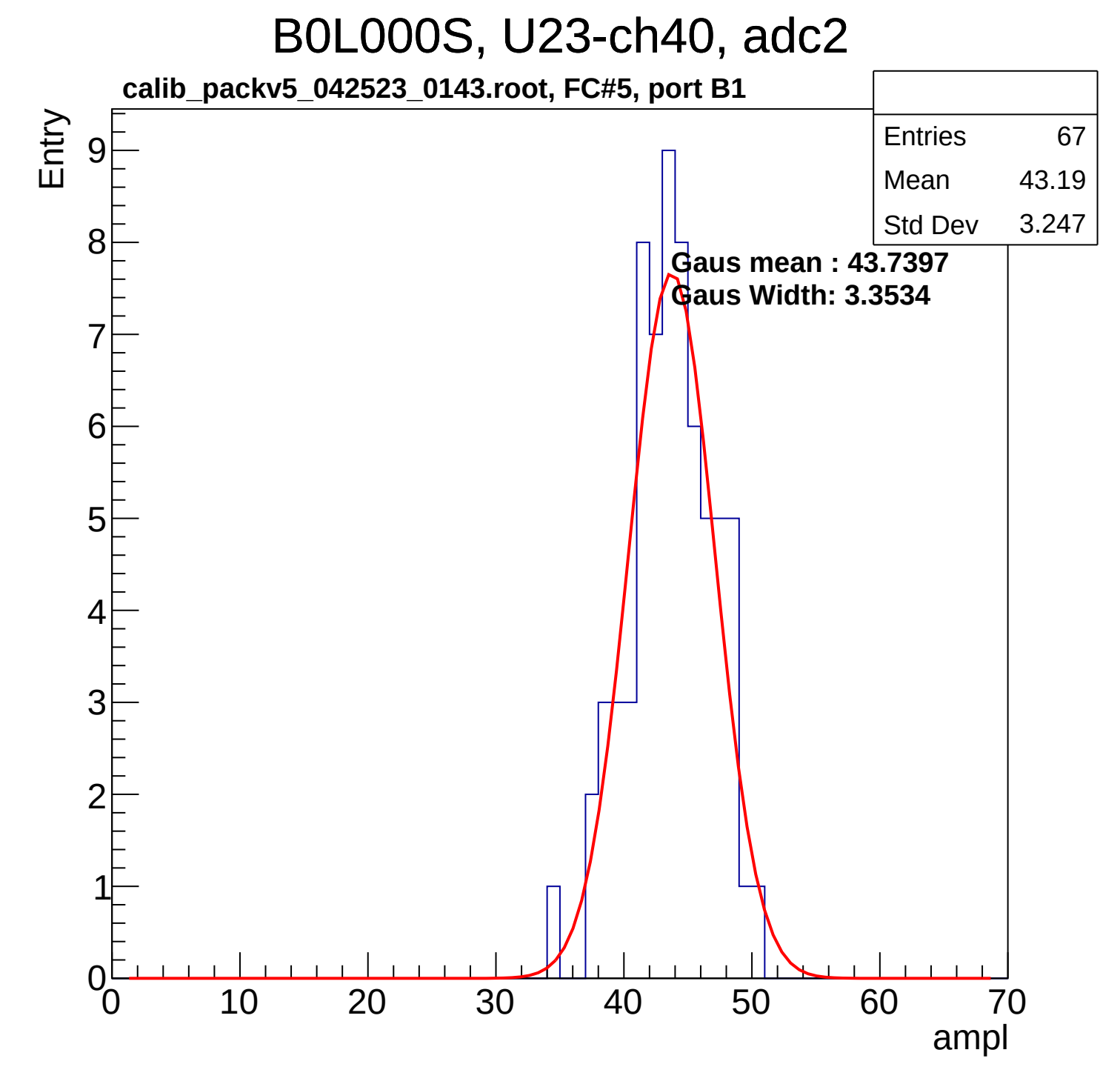

B0L000S, U23-ch39, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.94
Std Dev 8.565
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

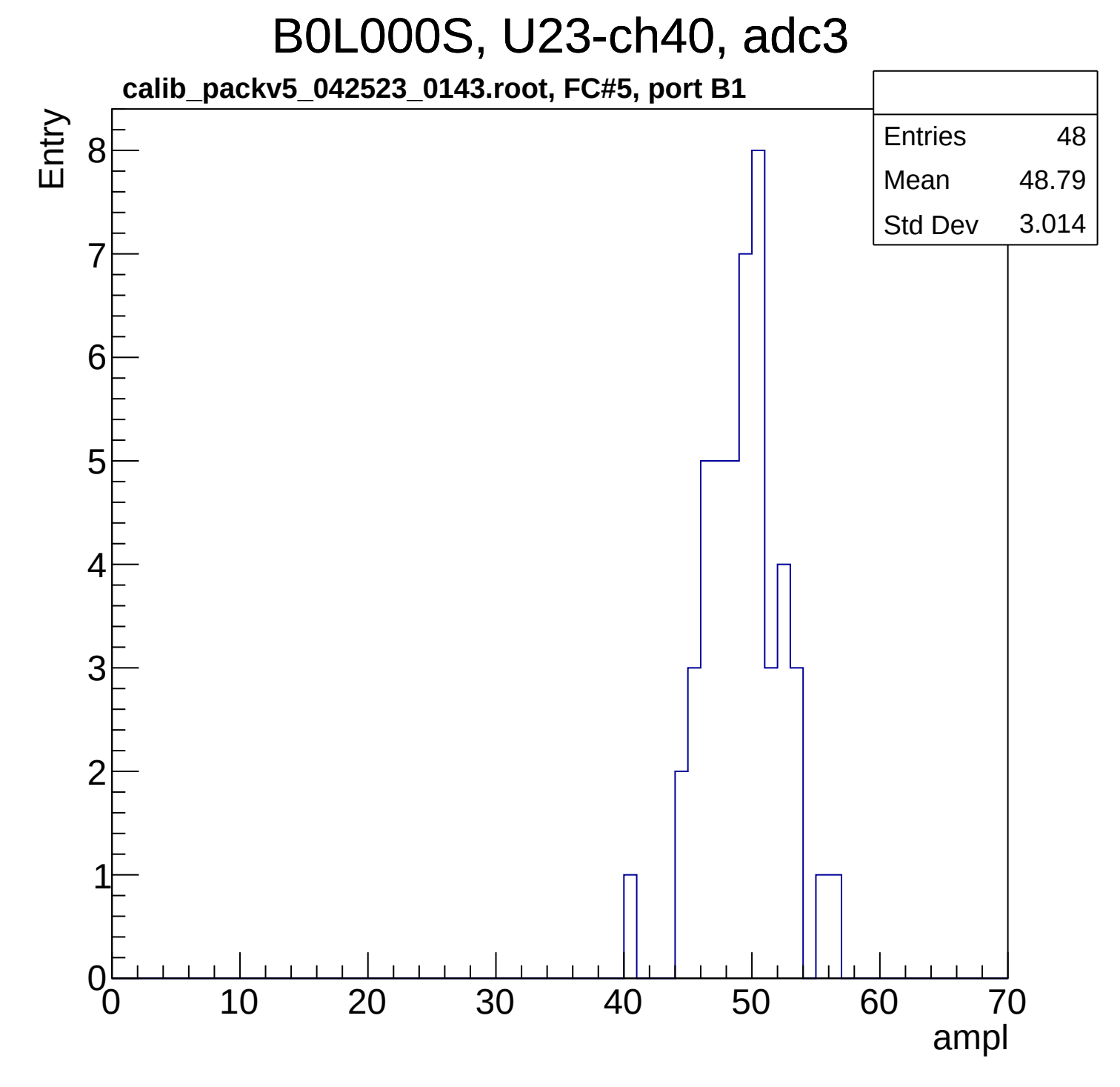

B0L000S, U23-ch39, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 60.33
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

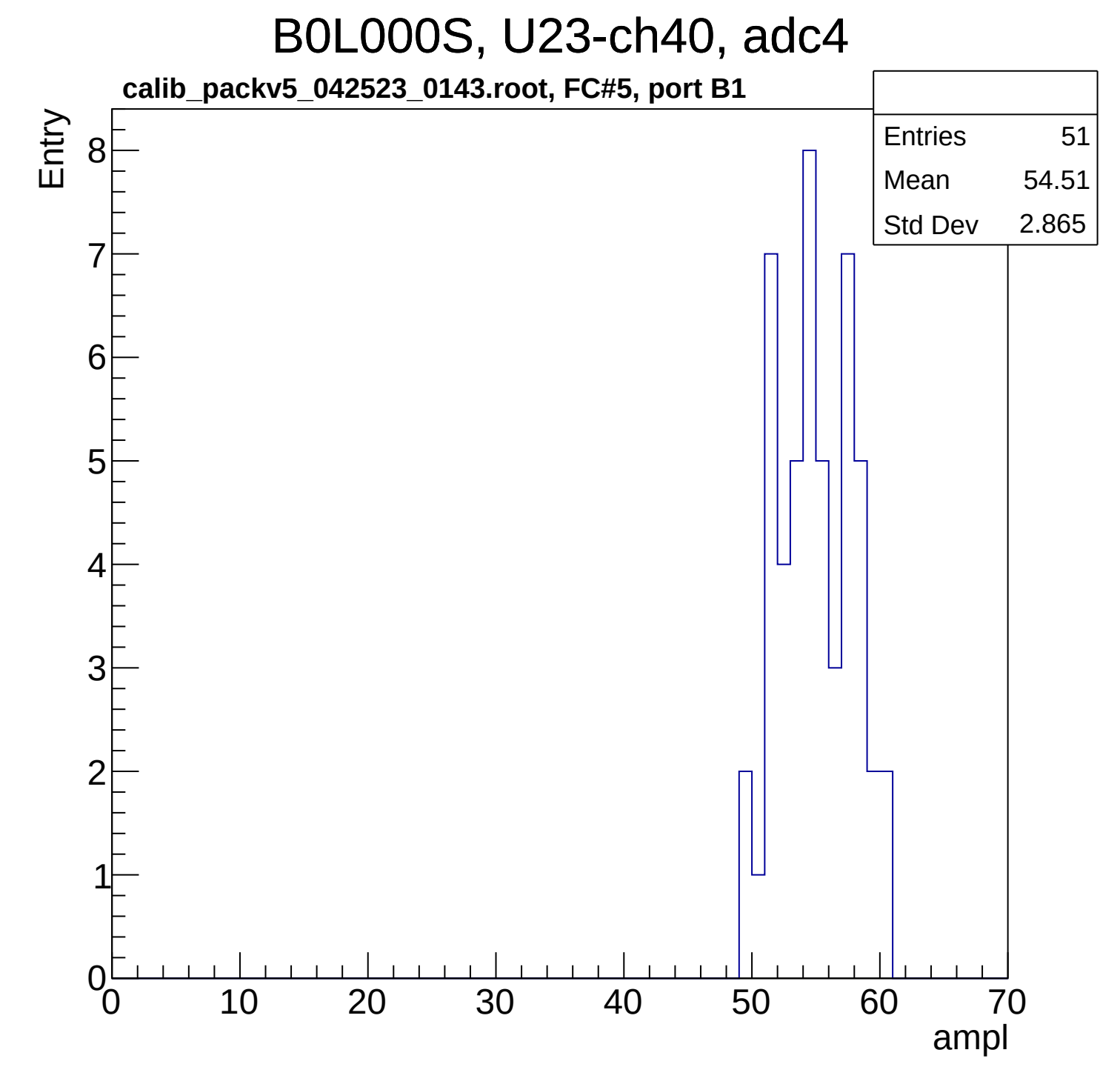

B0L000S, U23-ch39, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

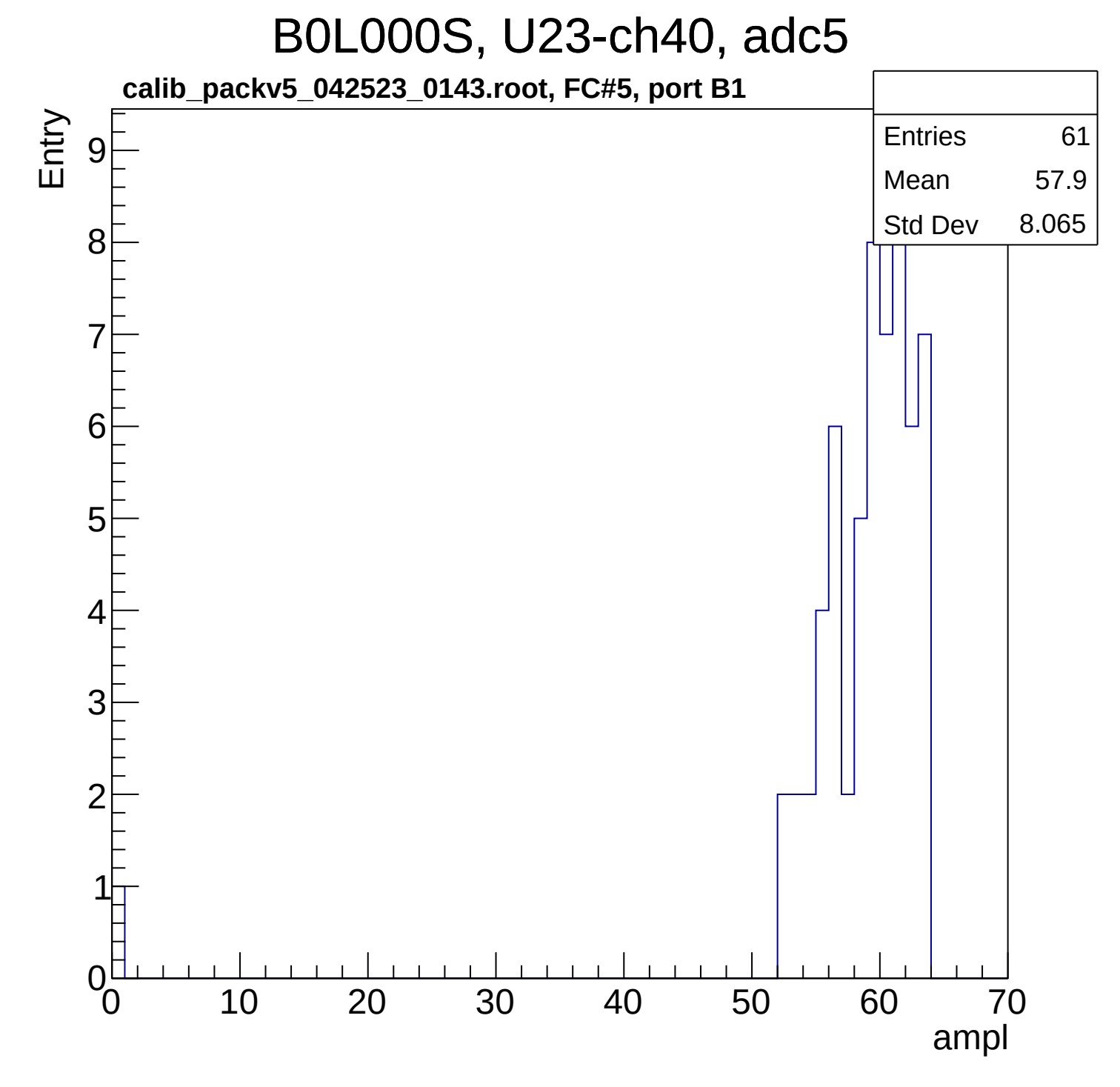

B0L000S, U23-ch40, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 28.6
Std Dev 4.65
Gaus mean : 29.4016
Gaus Width: 3.5157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

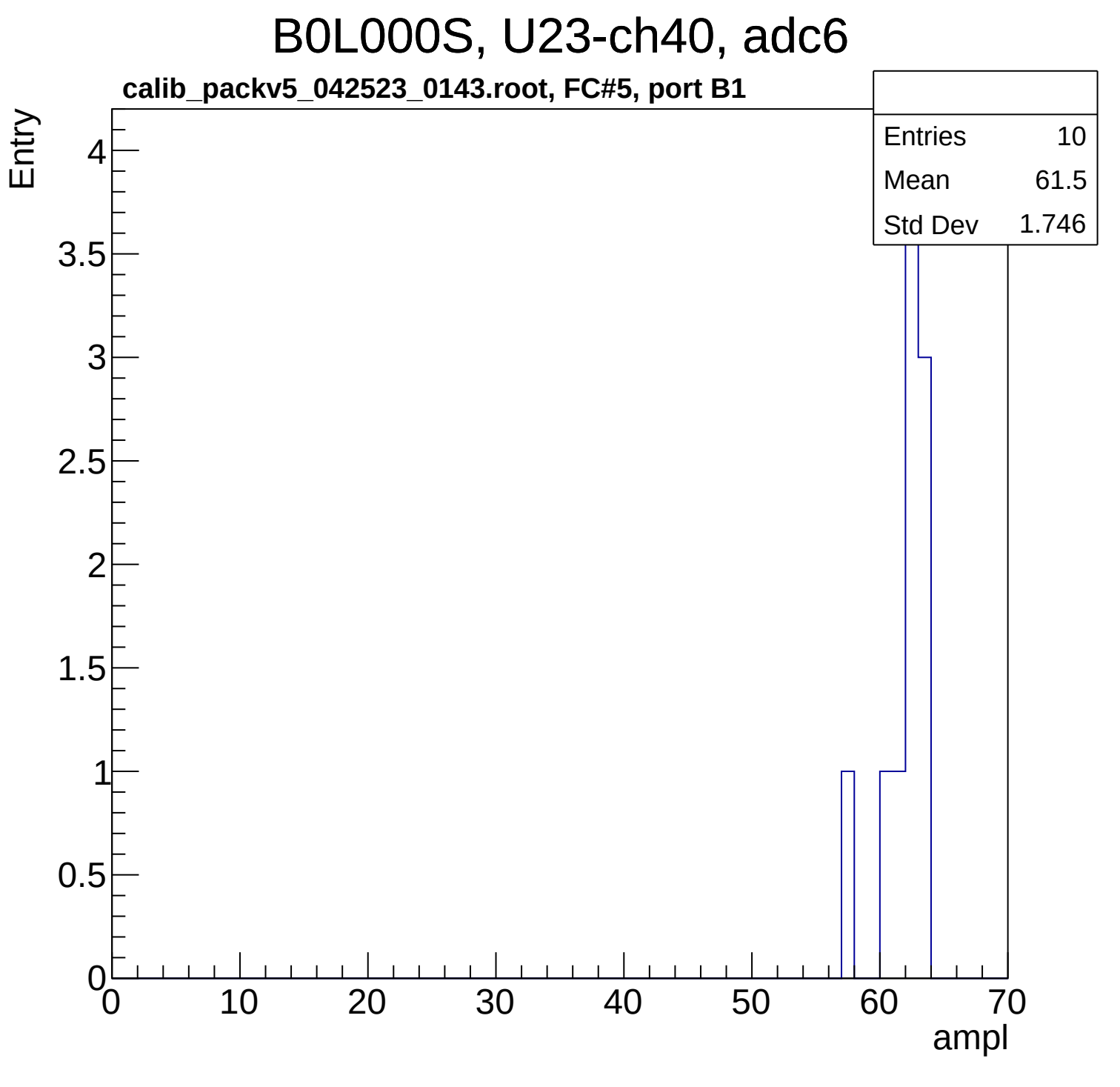

B0L000S, U23-ch40, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.4
Std Dev 3.328
Gaus mean : 36.8712
Gaus Width: 3.3629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

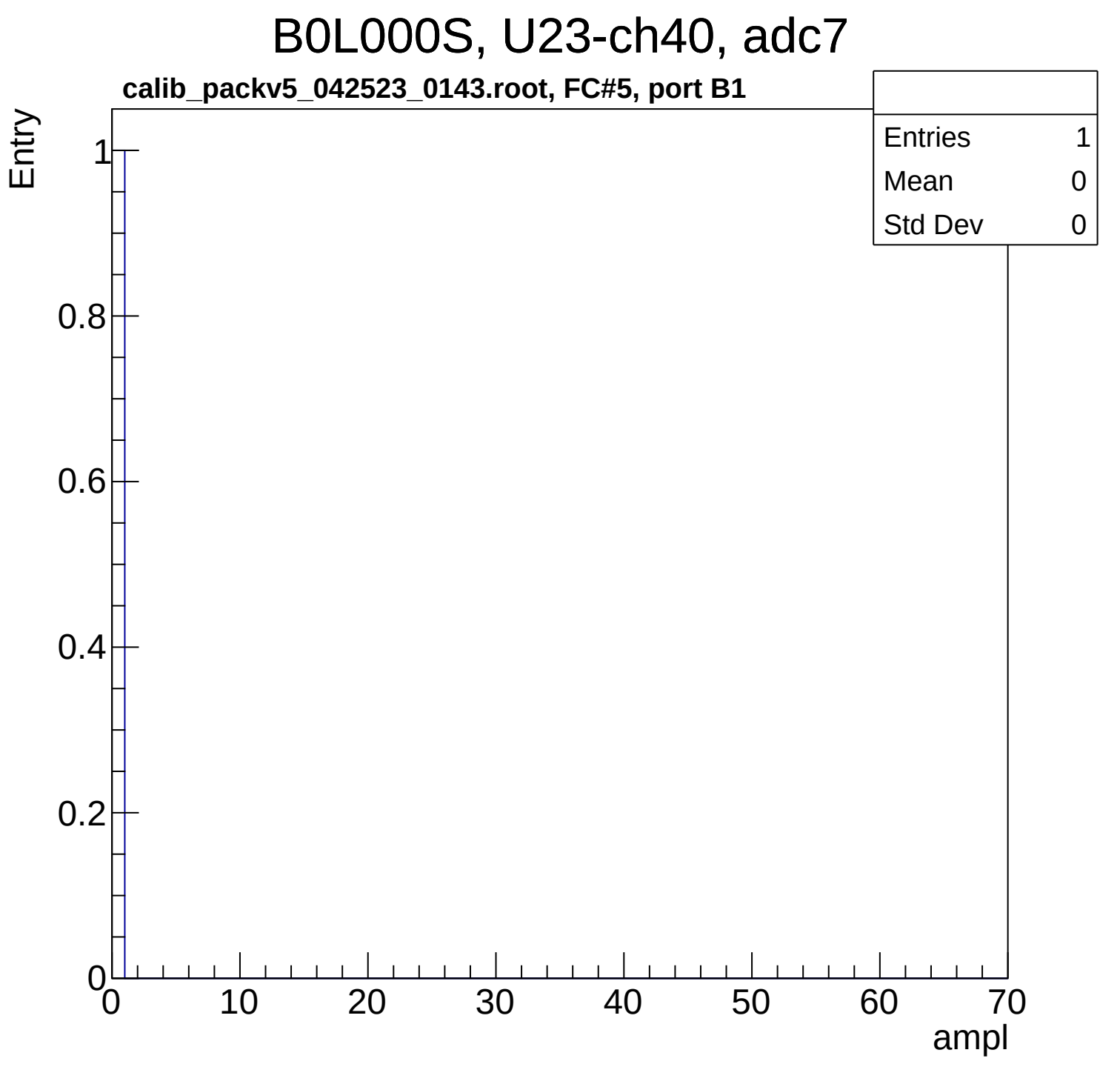

B0L000S, U23-ch40, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.19
Std Dev 3.247
Gaus mean : 43.7397
Gaus Width: 3.3534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

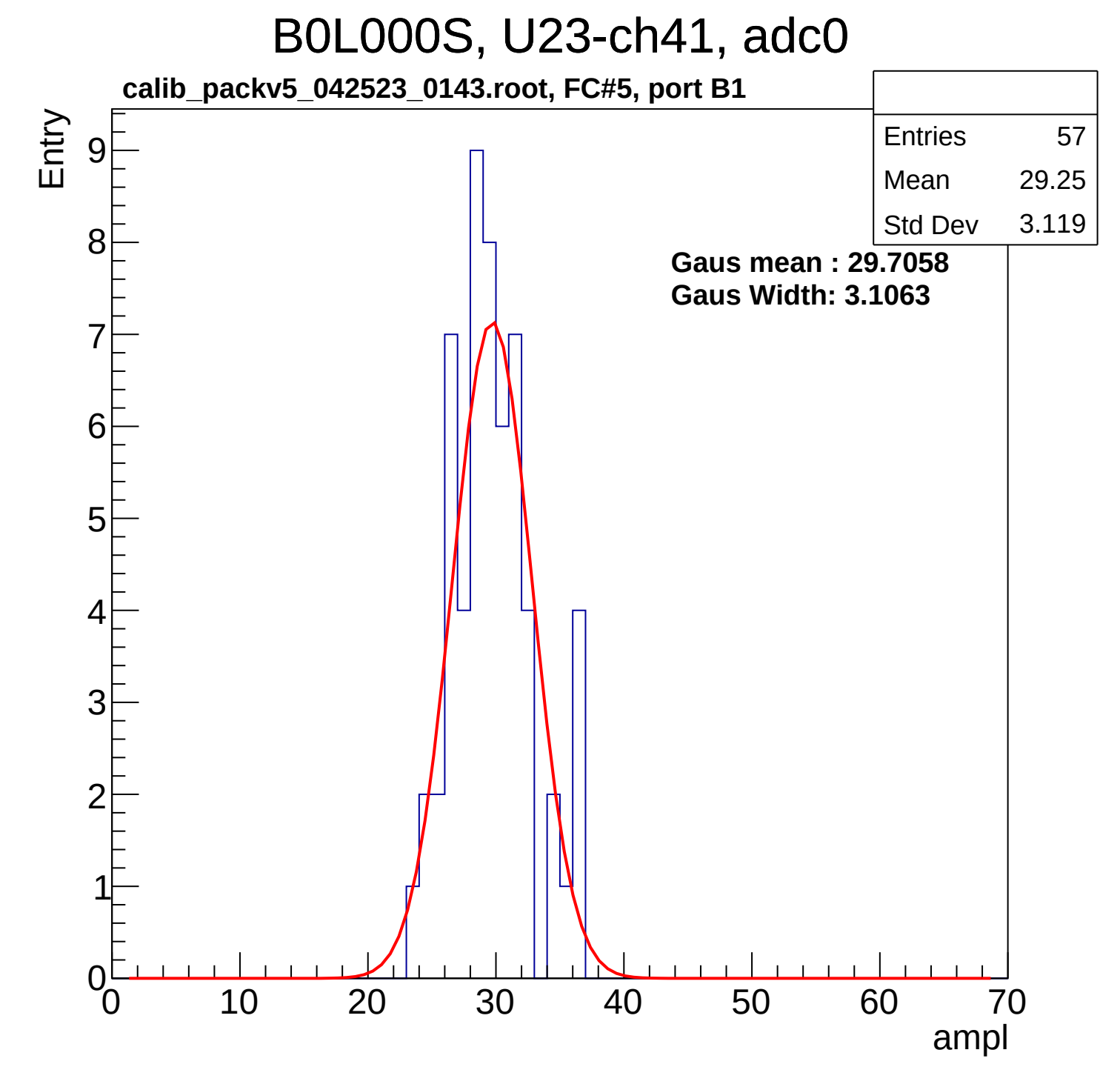

B0L000S, U23-ch40, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 48.79
Std Dev 3.014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

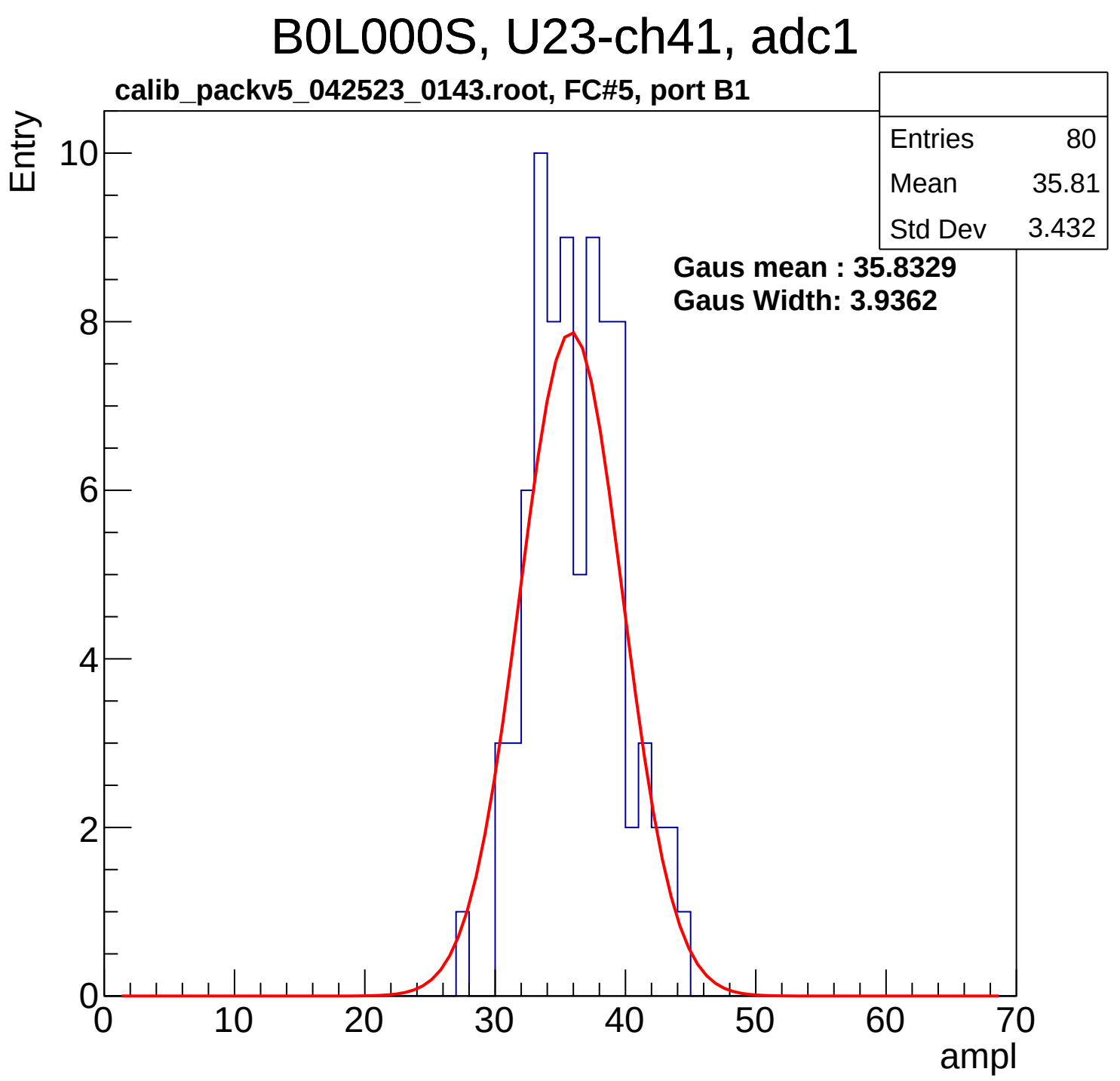

B0L000S, U23-ch40, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.51
Std Dev 2.865
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

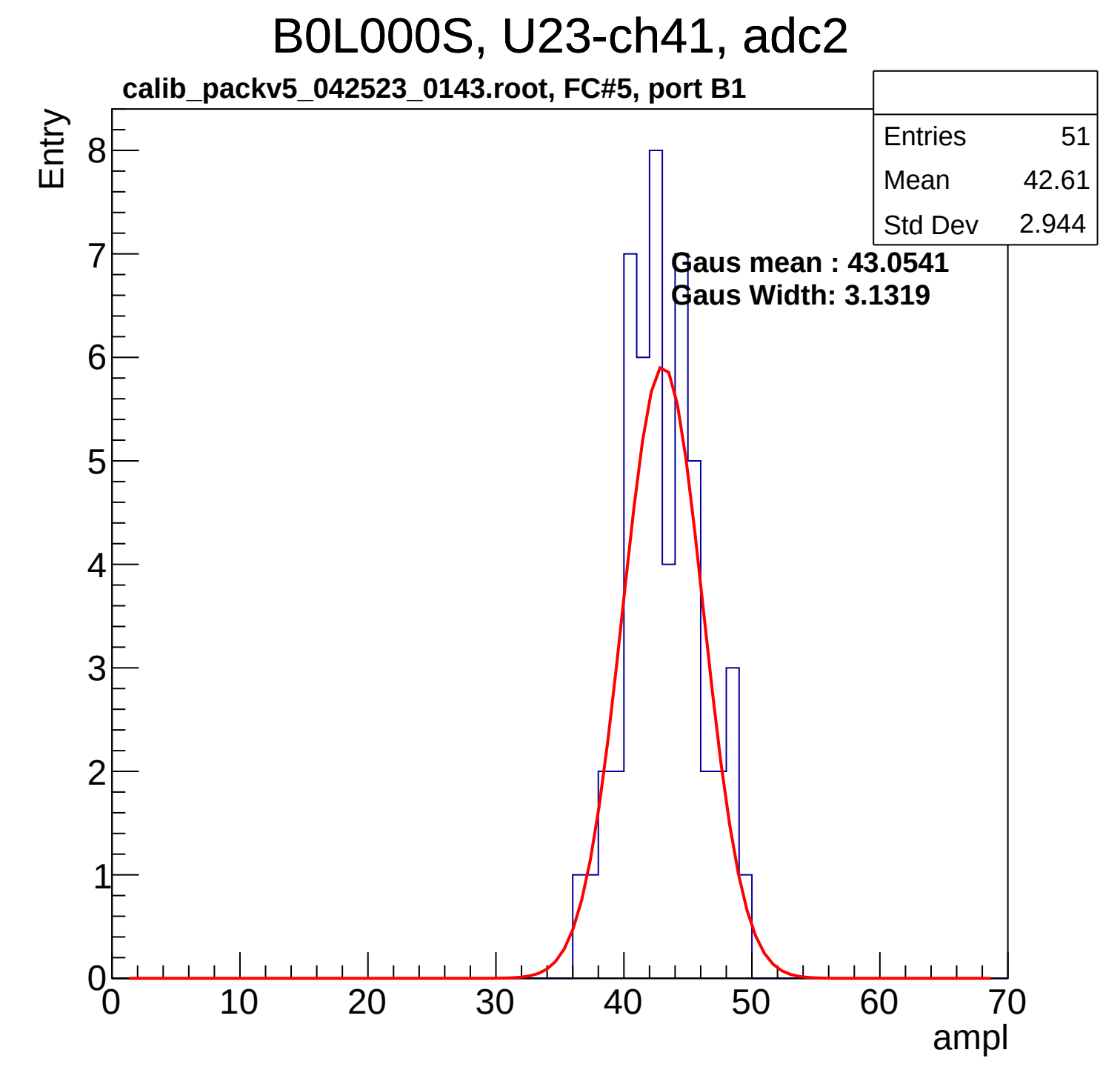

B0L000S, U23-ch40, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 57.9
Std Dev 8.065
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

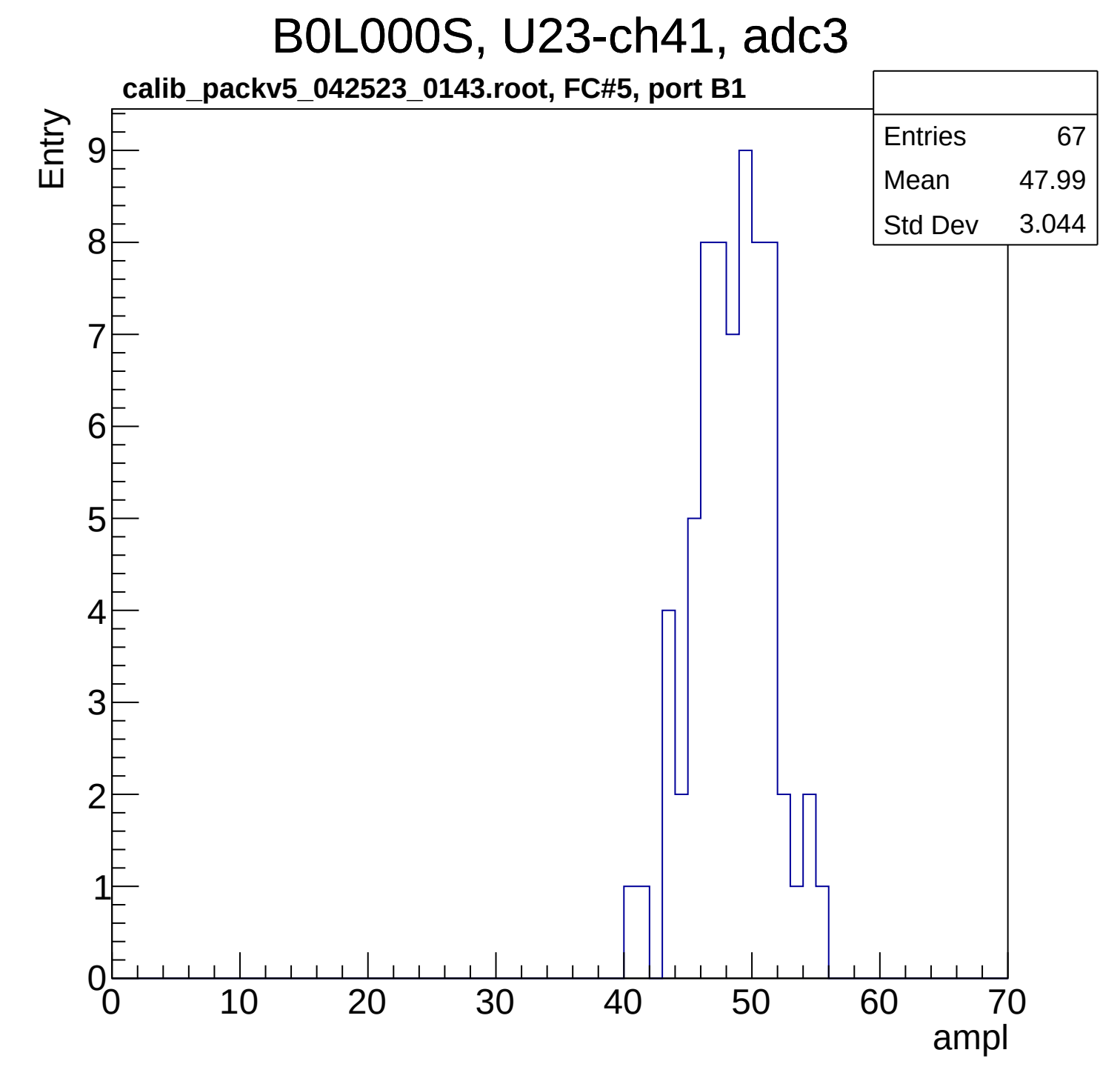

B0L000S, U23-ch40, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.5
Std Dev 1.746
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

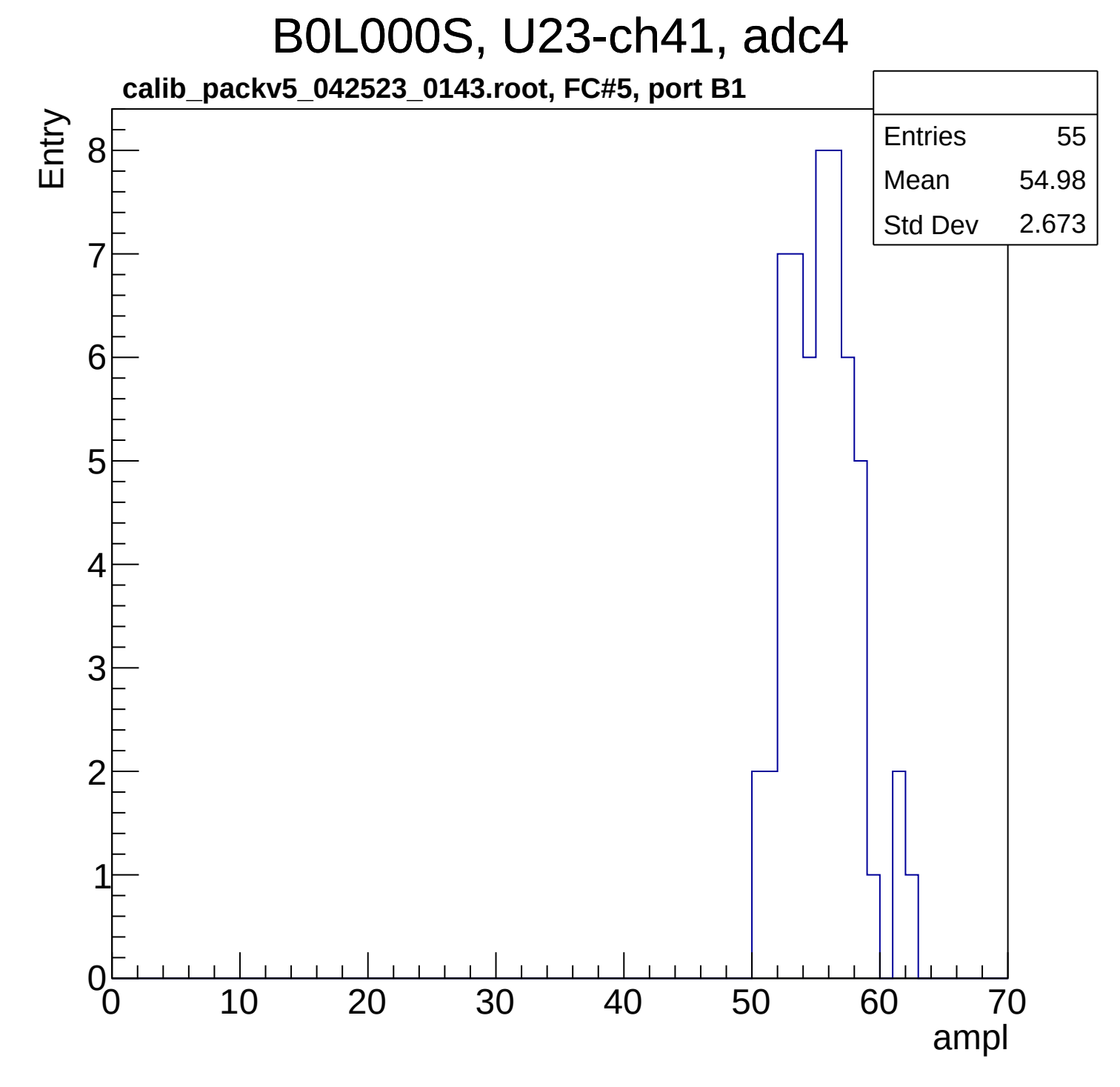

B0L000S, U23-ch40, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

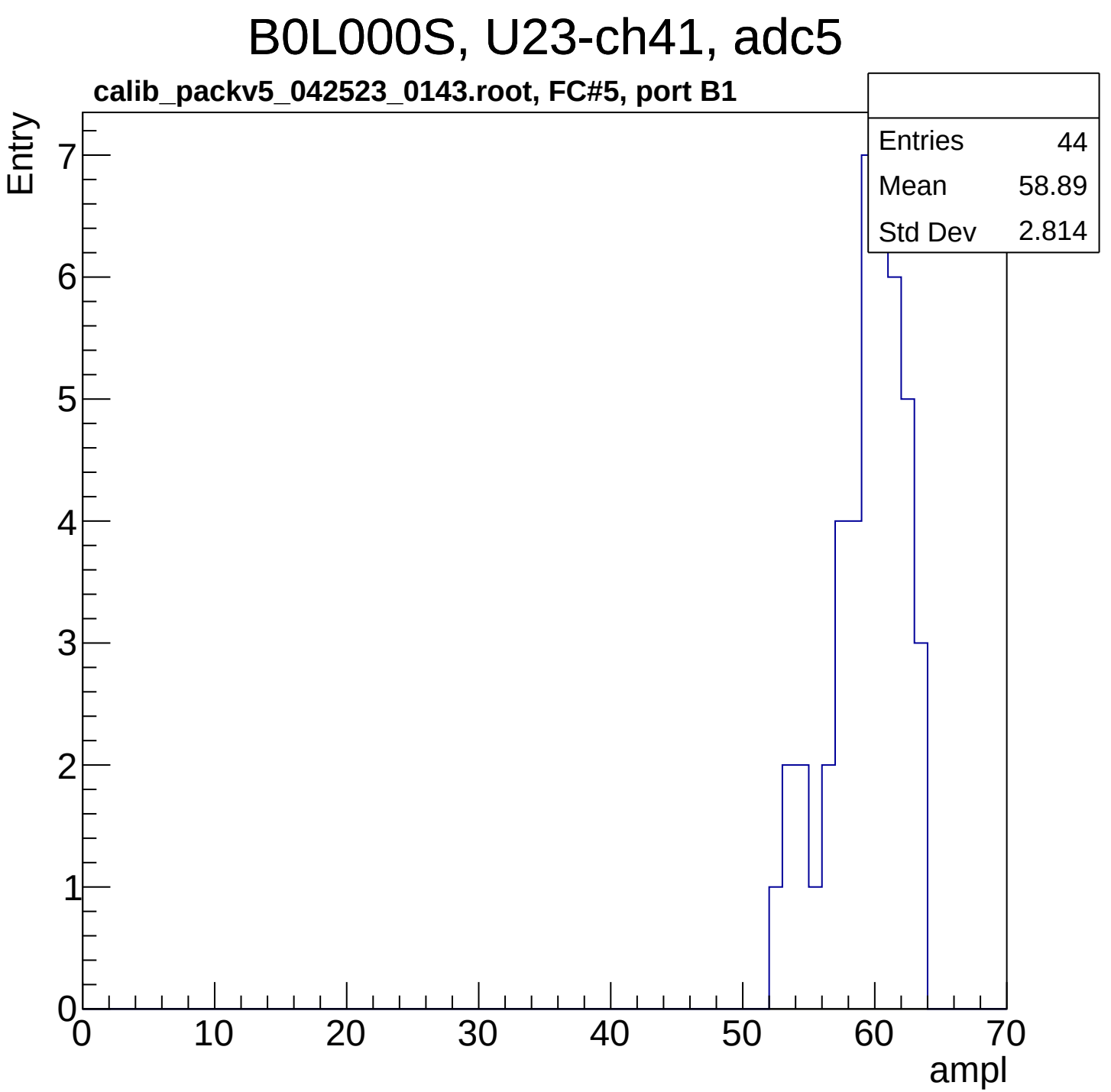

B0L000S, U23-ch41, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 29.25
Std Dev 3.119
Gaus mean : 29.7058
Gaus Width: 3.1063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

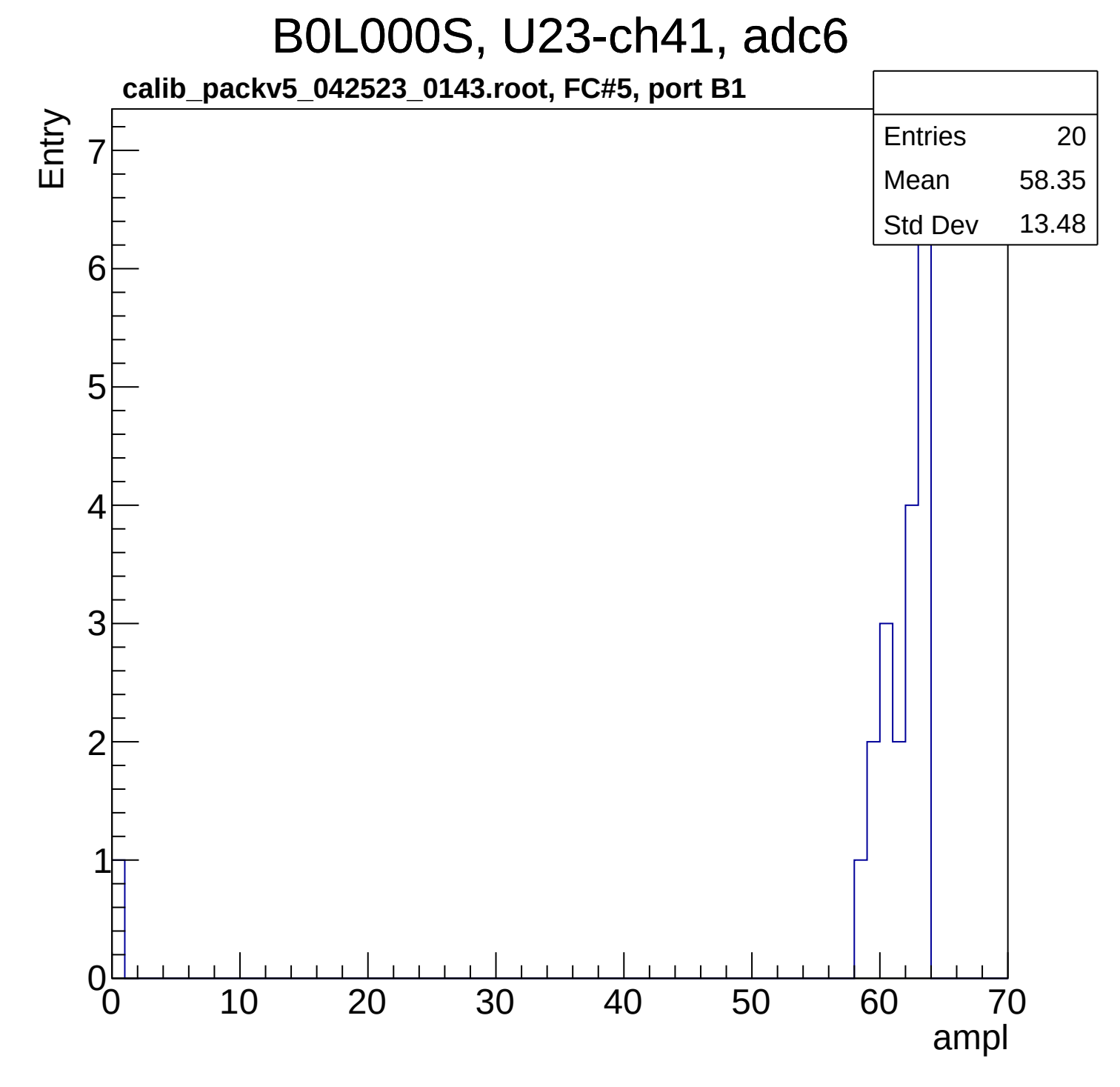

B0L000S, U23-ch41, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 35.81
Std Dev 3.432
Gaus mean : 35.8329
Gaus Width: 3.9362
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

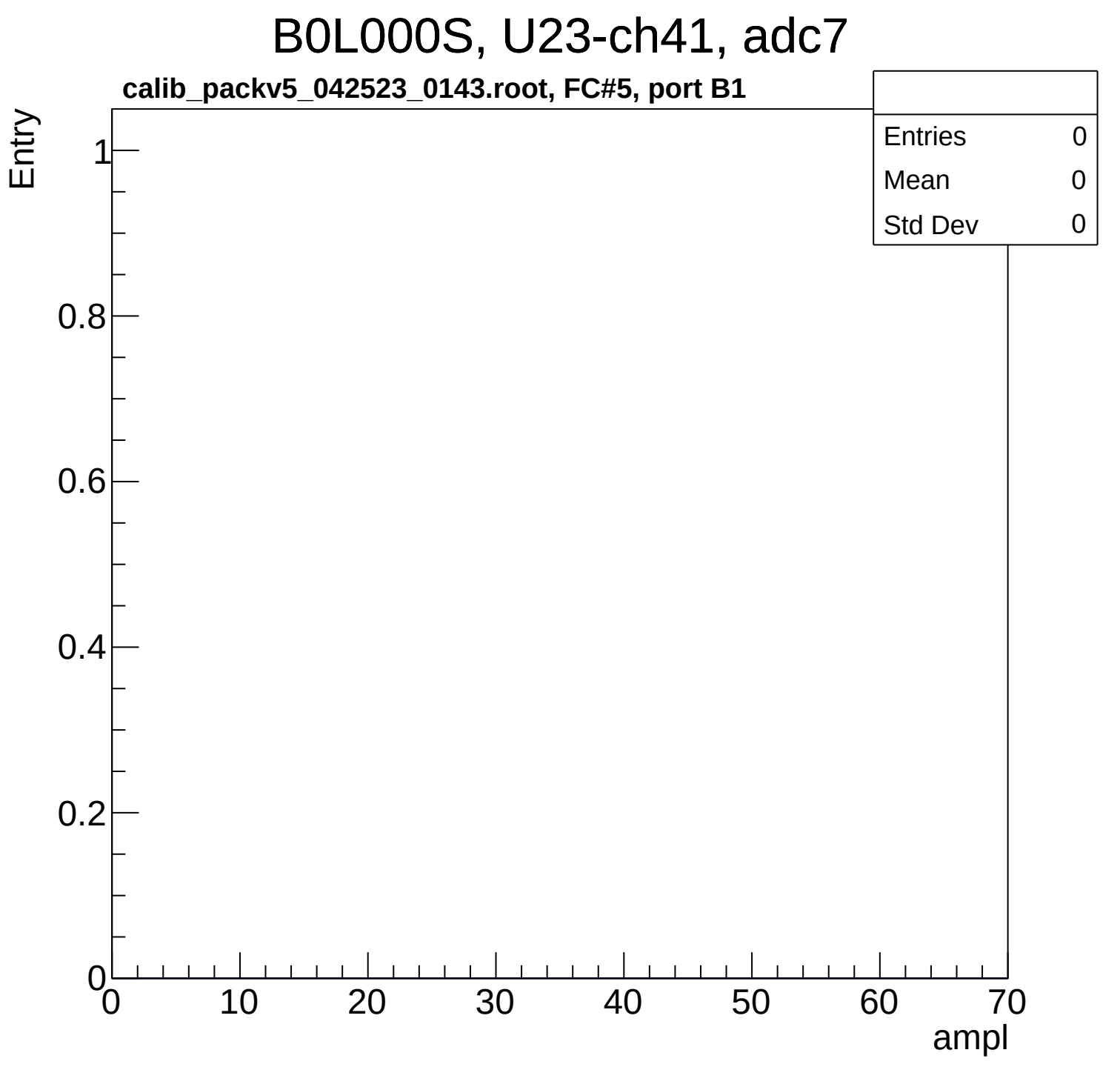

B0L000S, U23-ch41, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 42.61
Std Dev 2.944
Gaus mean : 43.0541
Gaus Width: 3.1319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

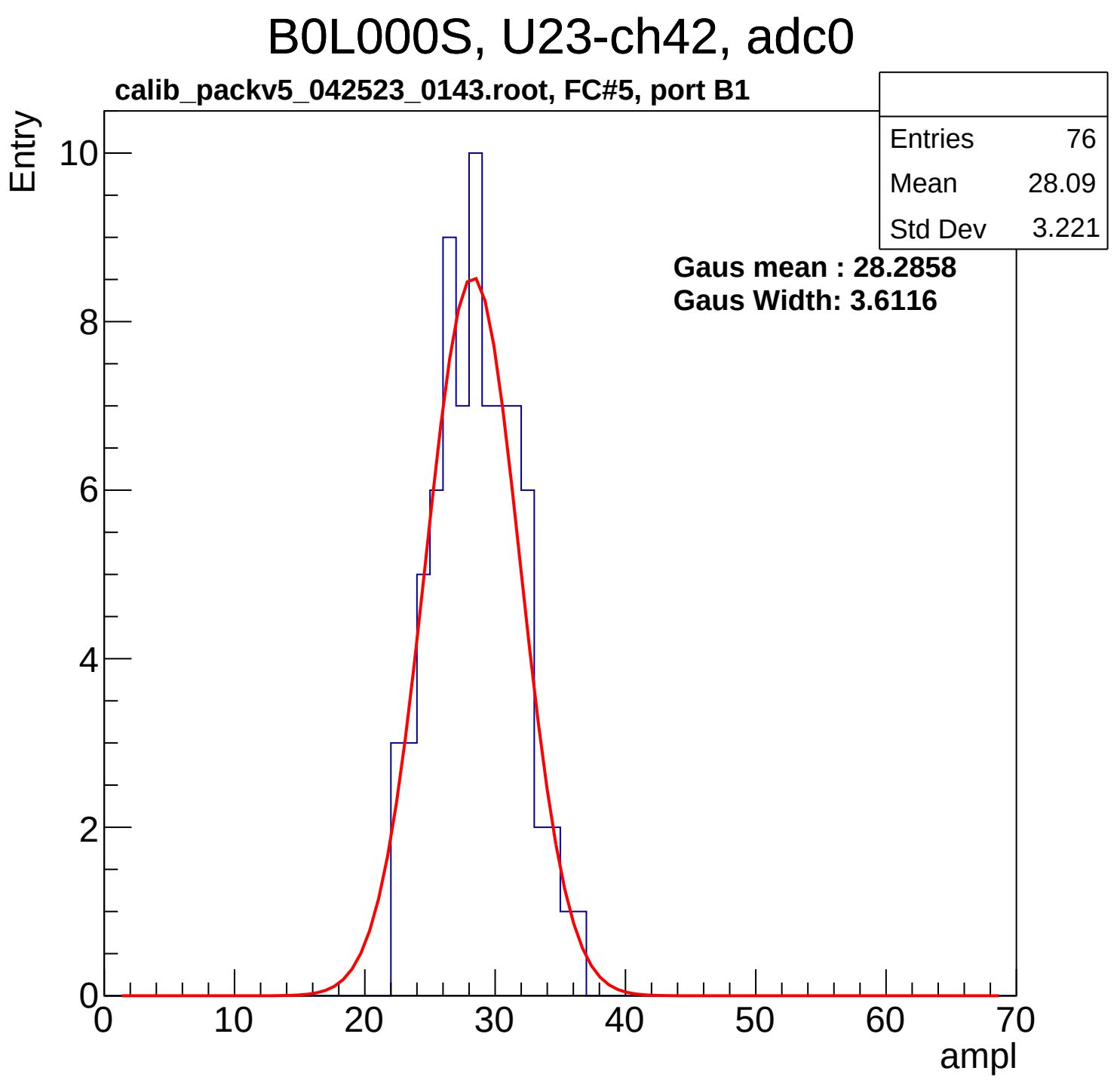

B0L000S, U23-ch41, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 47.99
Std Dev 3.044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

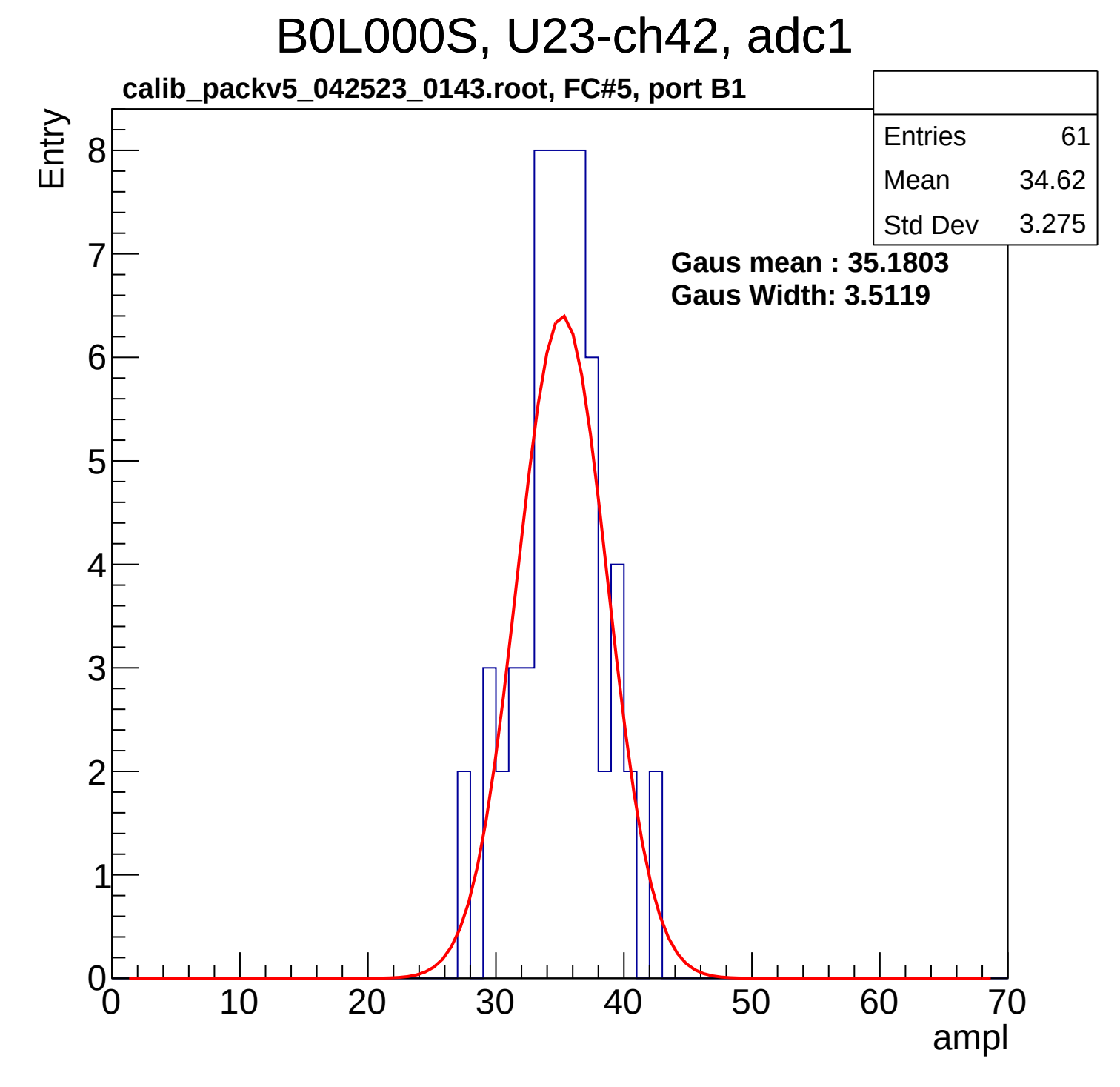

B0L000S, U23-ch41, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 54.98
Std Dev 2.673
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

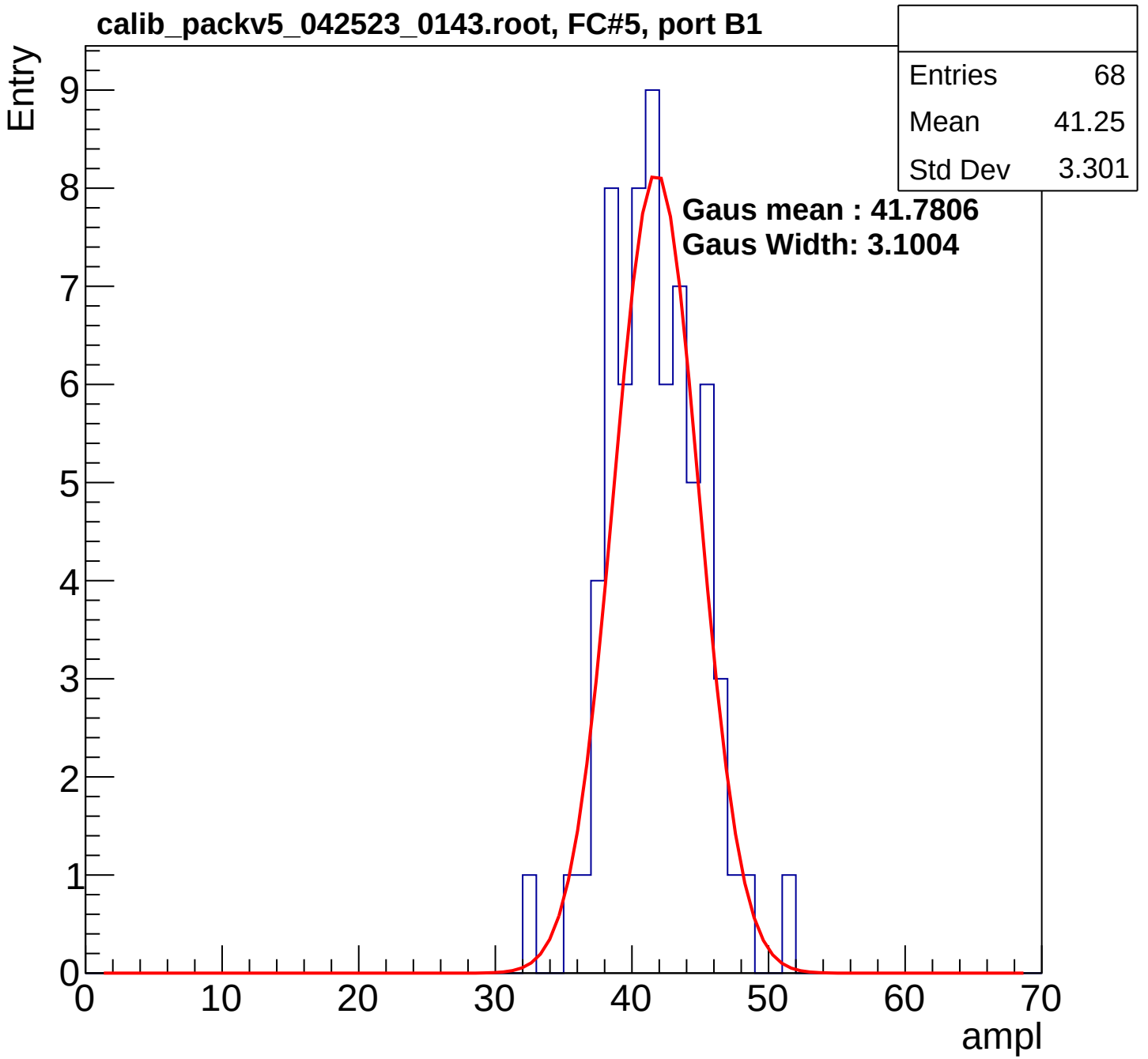

B0L000S, U23-ch41, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.89
Std Dev 2.814
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

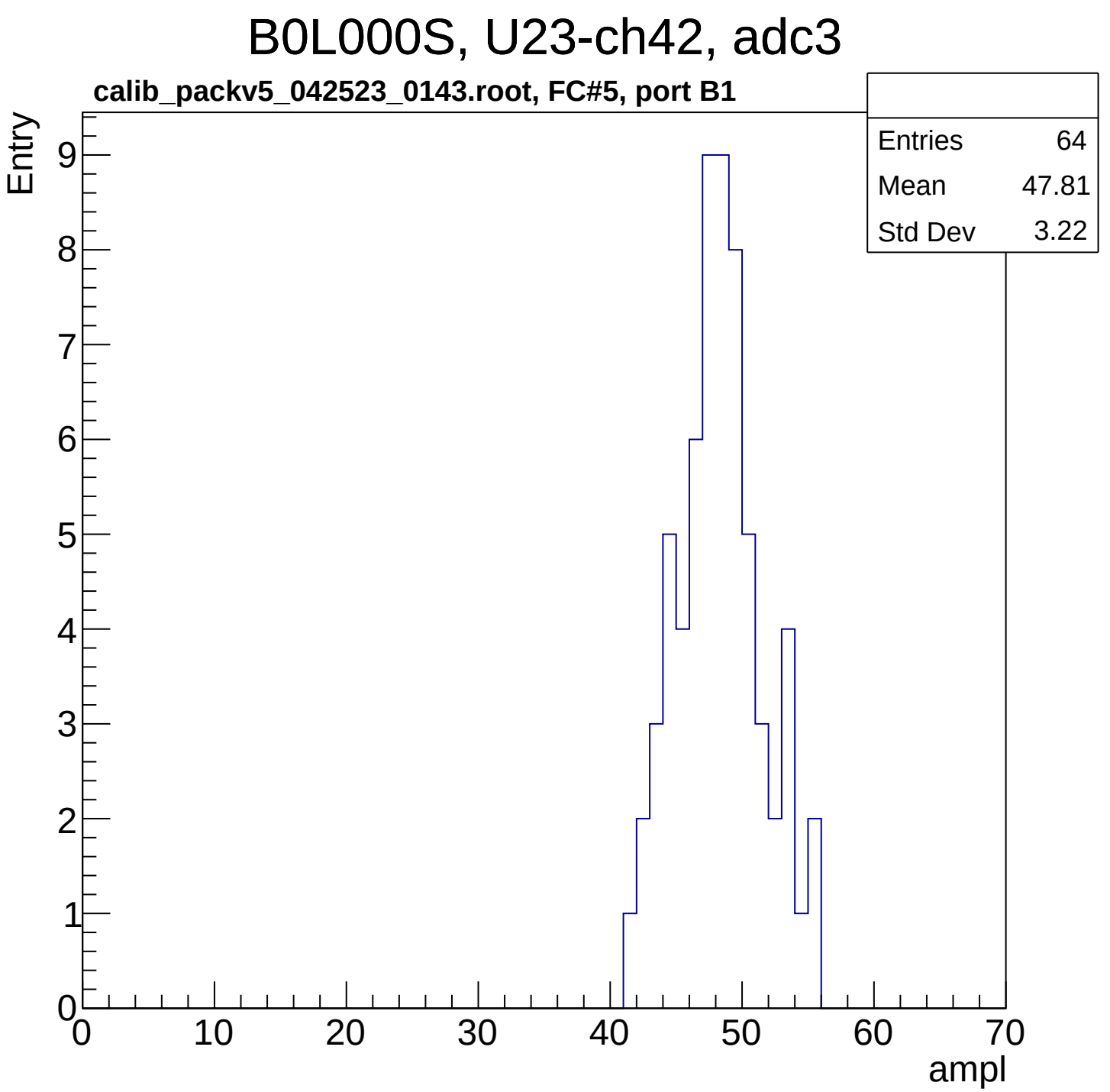

B0L000S, U23-ch41, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.35
Std Dev 13.48
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

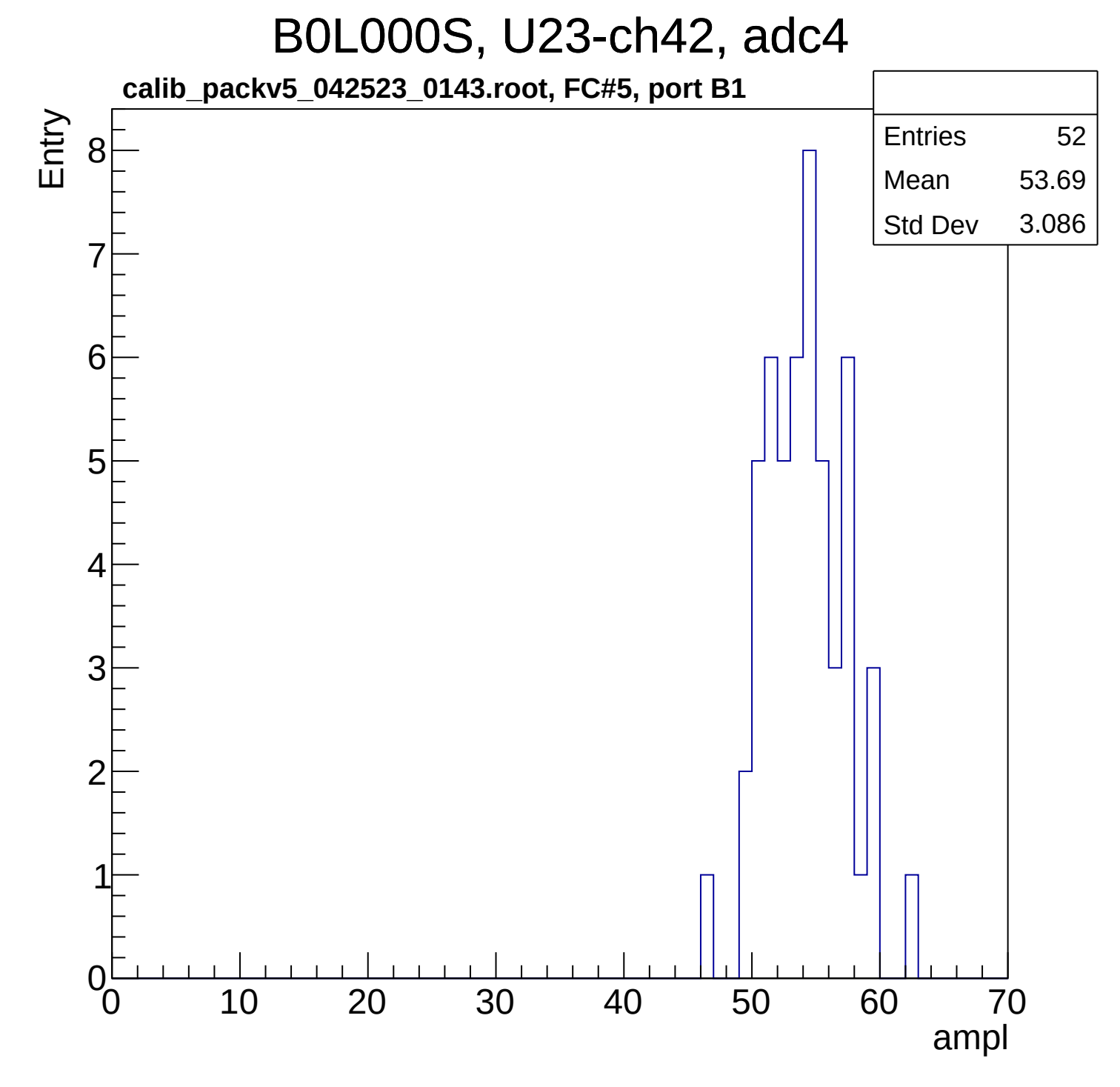

B0L000S, U23-ch41, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

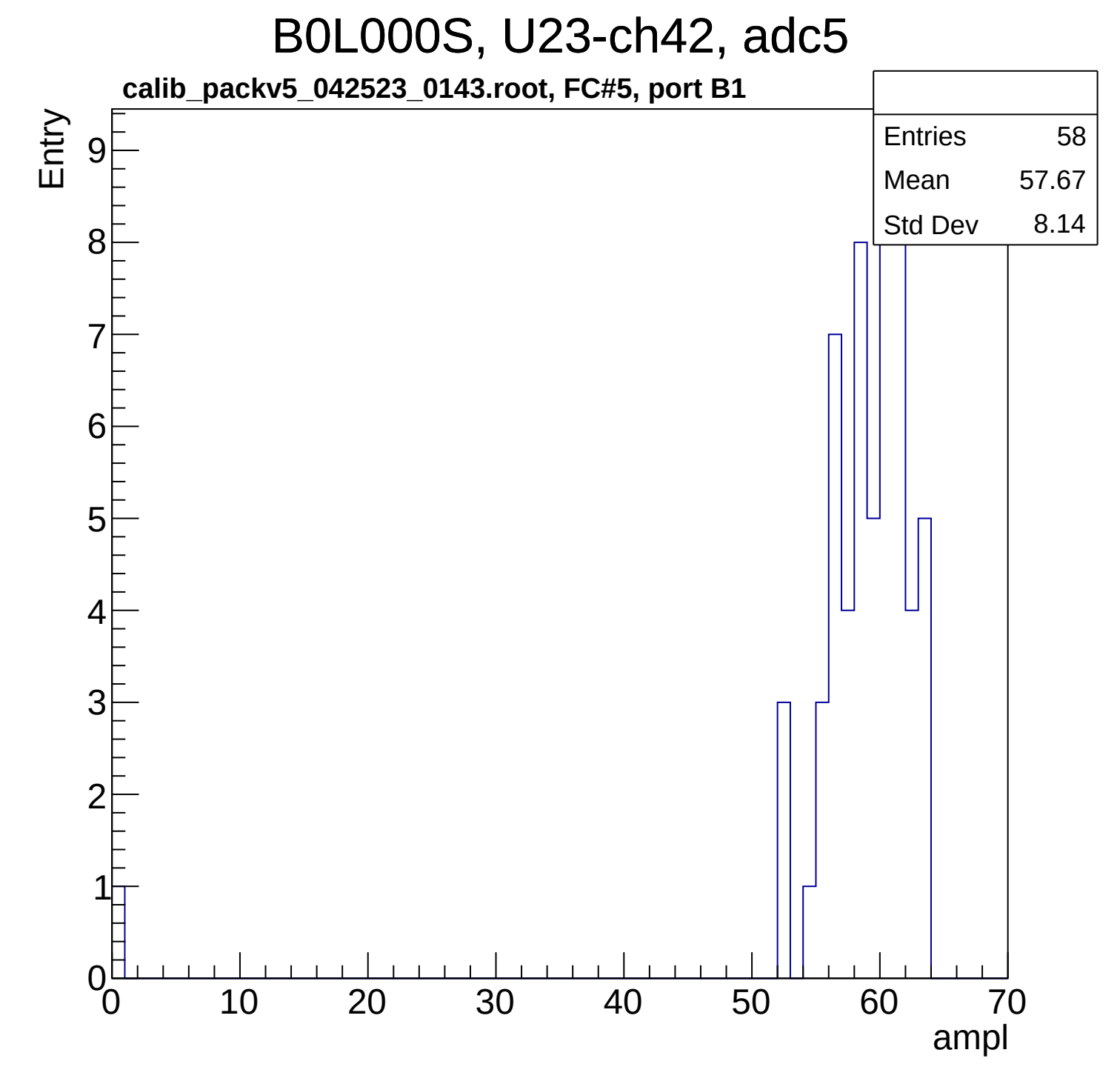

B0L000S, U23-ch42, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 28.09
Std Dev 3.221
Gaus mean : 28.2858
Gaus Width: 3.6116
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

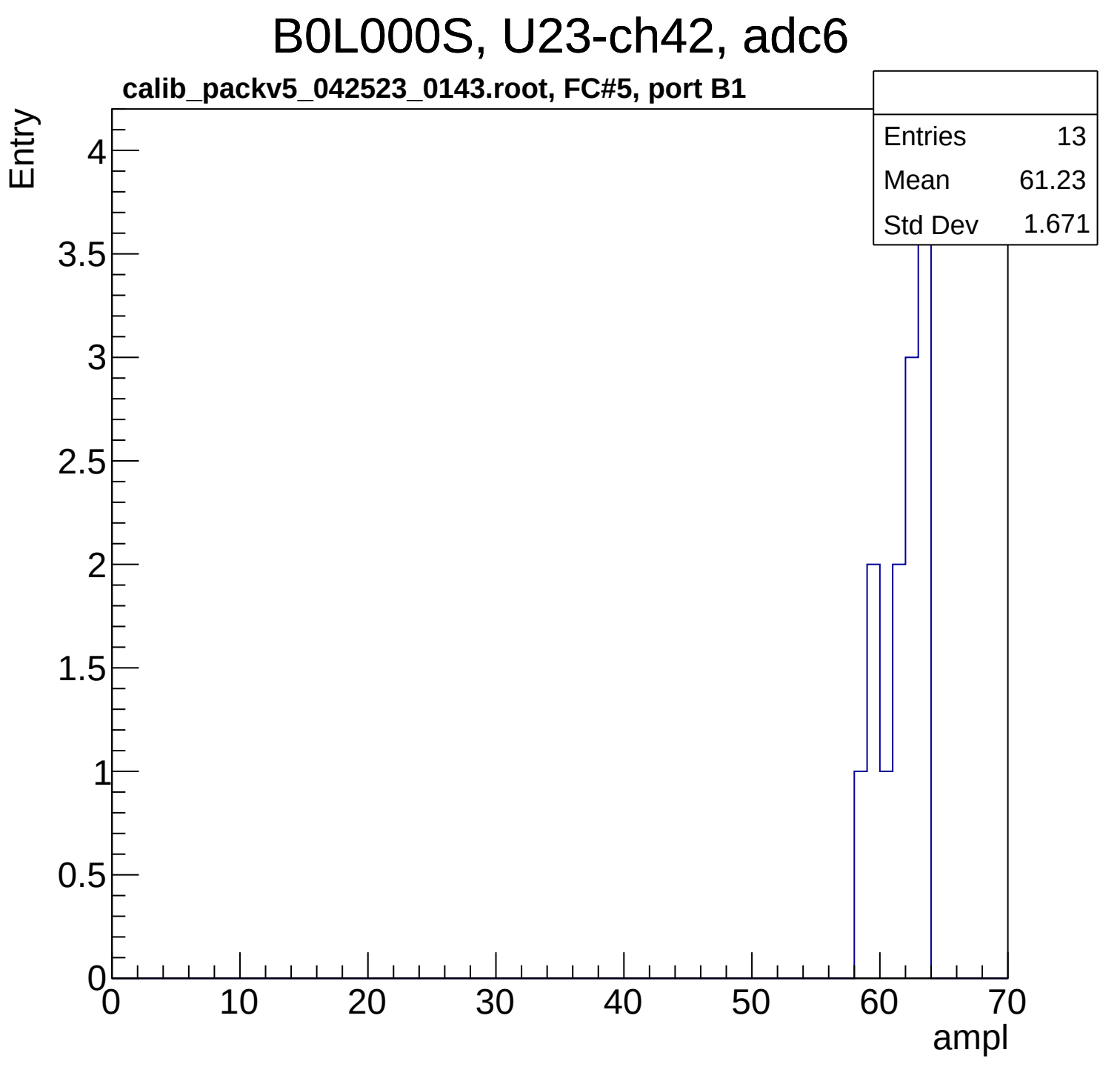

B0L000S, U23-ch42, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 34.62
Std Dev 3.275
Gaus mean : 35.1803
Gaus Width: 3.5119
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

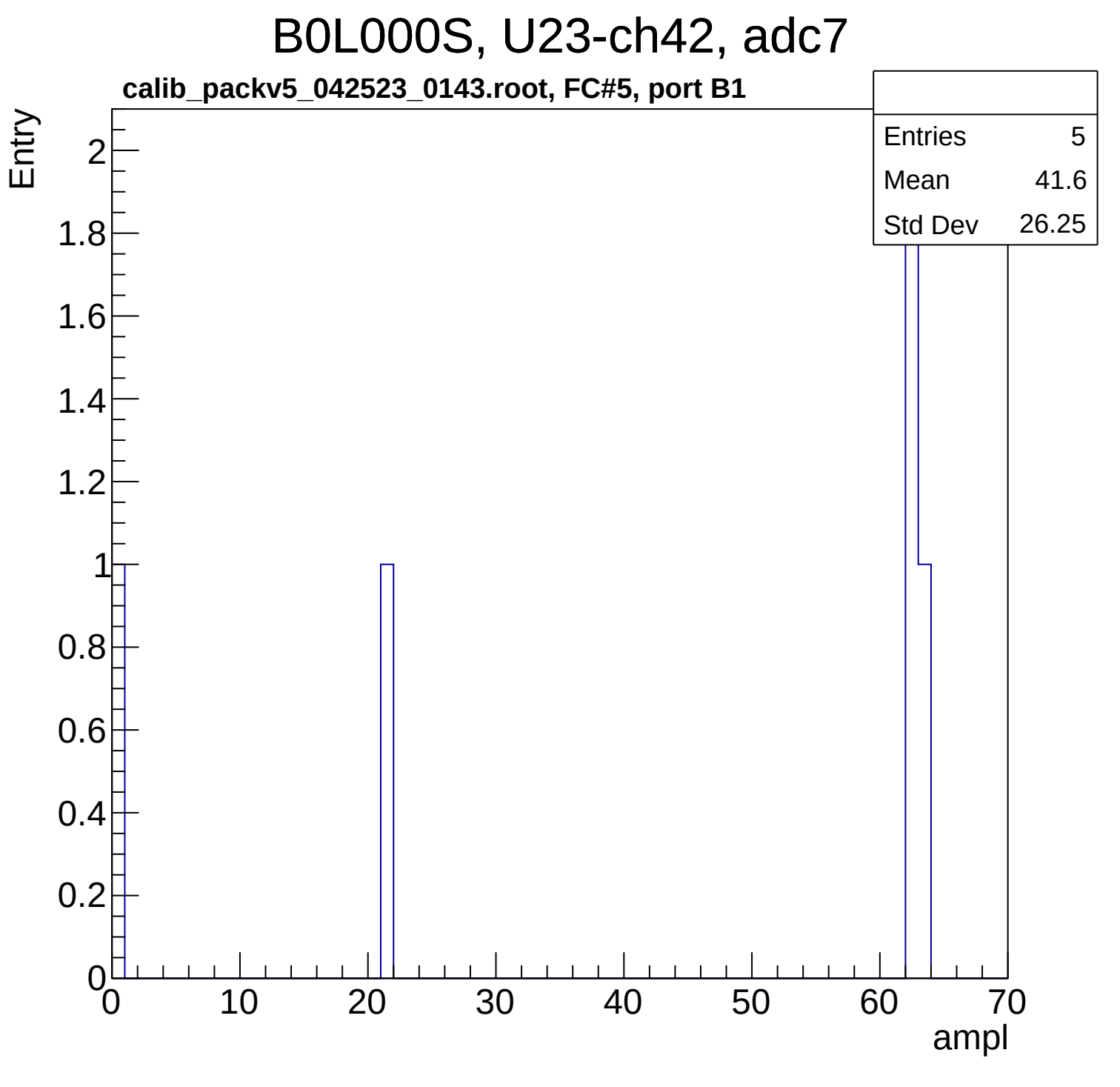

B0L000S, U23-ch42, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 41.25
Std Dev 3.301
Gaus mean : 41.7806
Gaus Width: 3.1004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

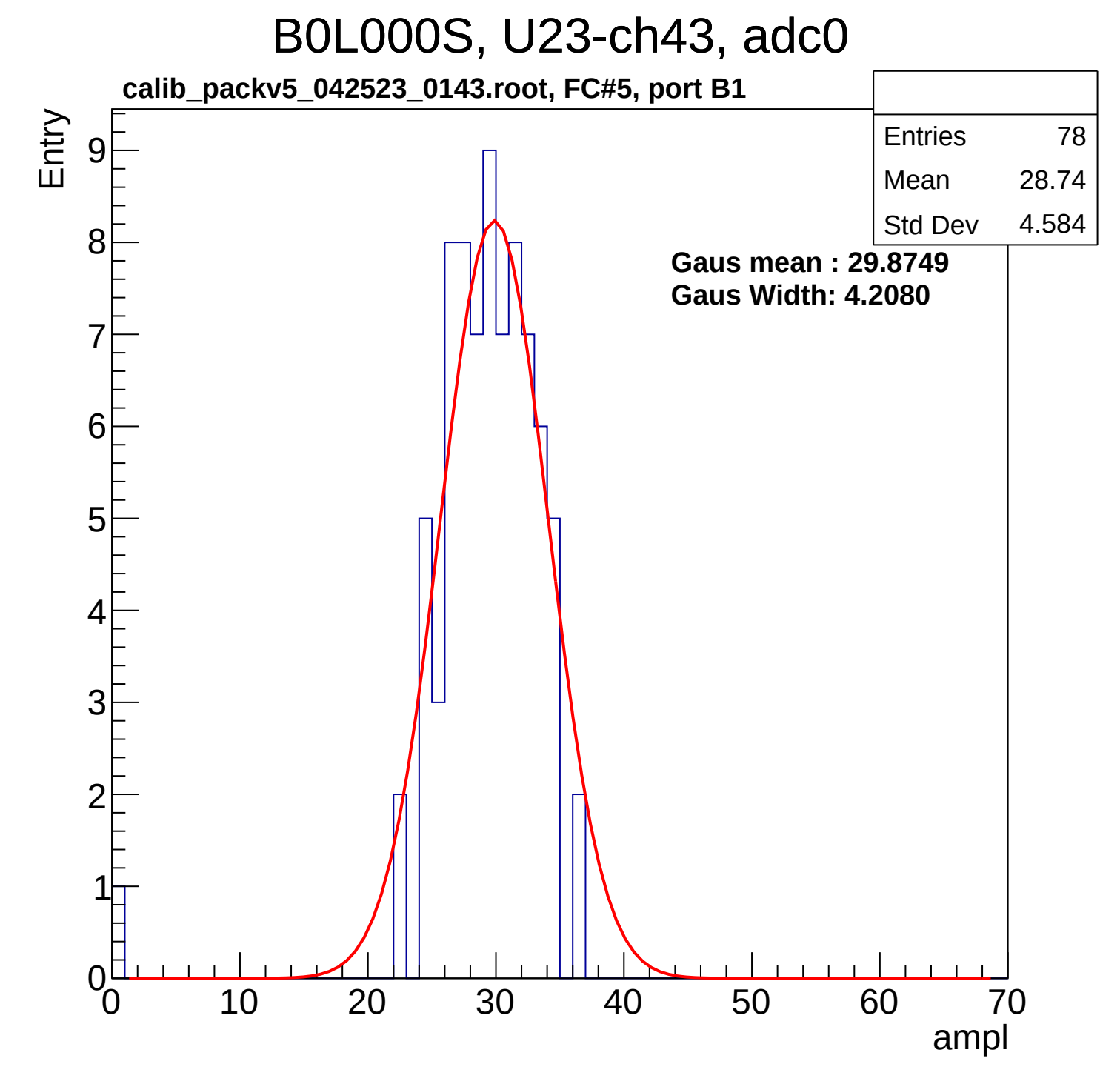

B0L000S, U23-ch42, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.81
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

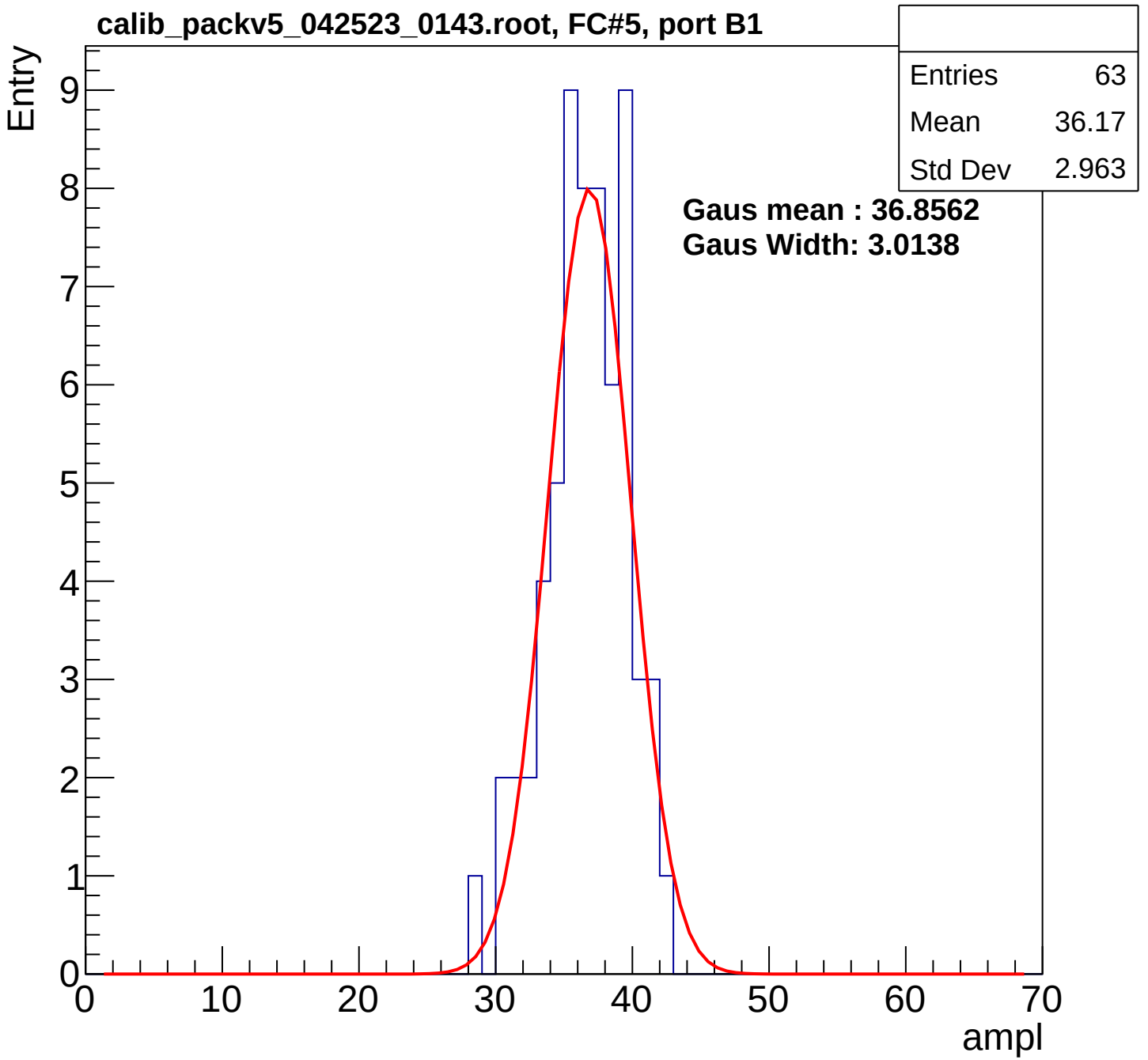

B0L000S, U23-ch42, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 53.69
Std Dev 3.086
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

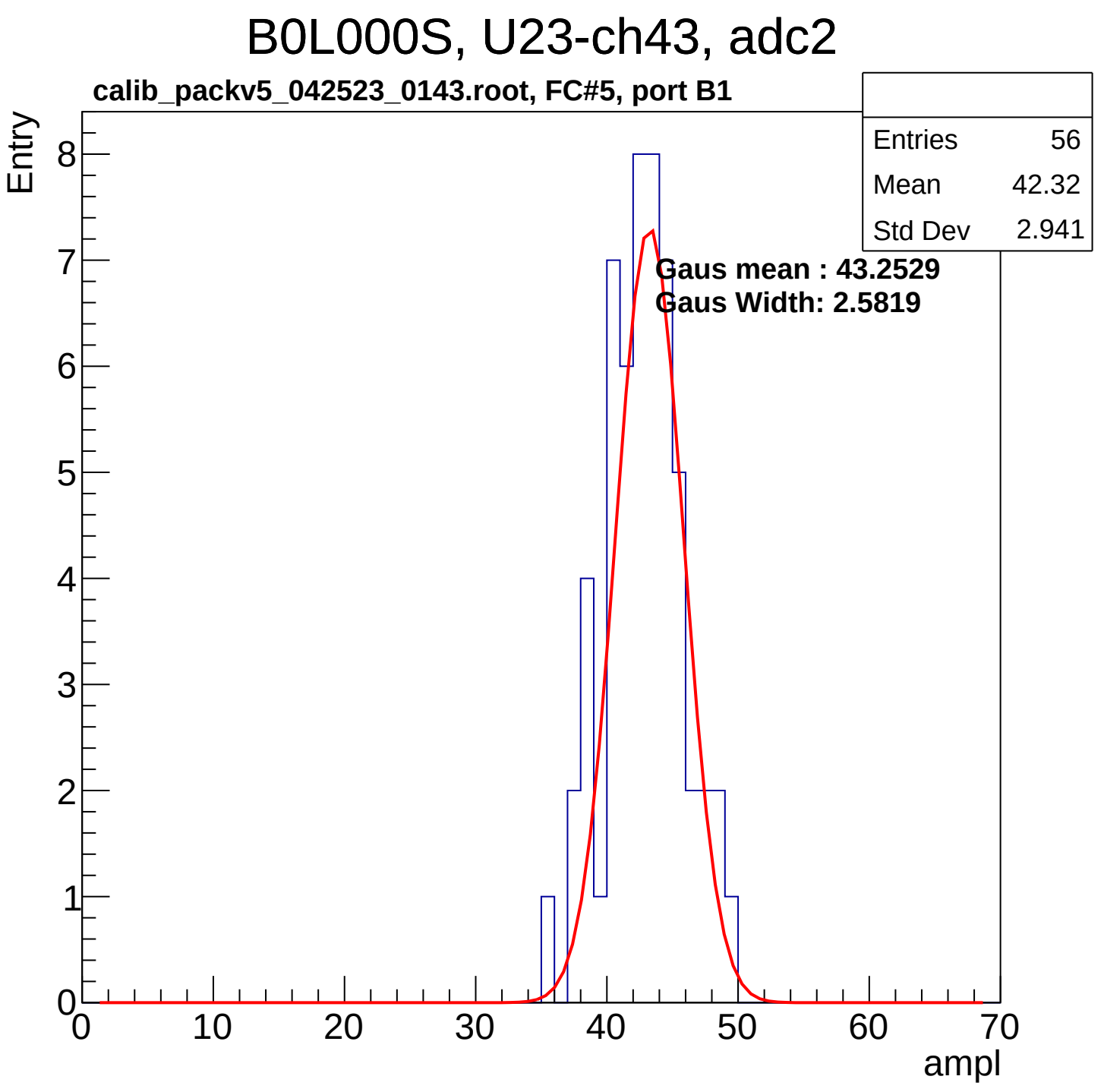

B0L000S, U23-ch42, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.67
Std Dev 8.14
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

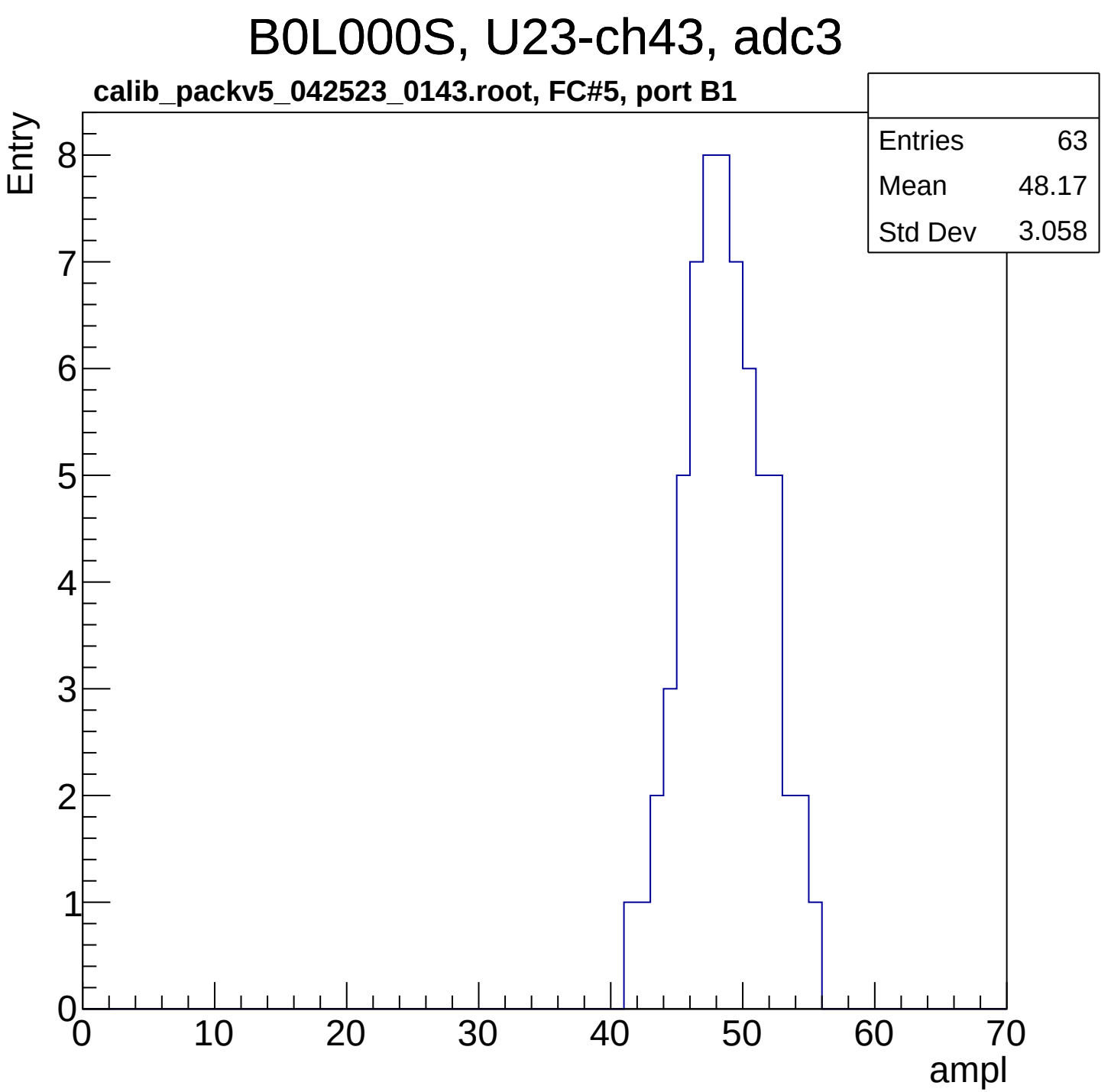

B0L000S, U23-ch42, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.23
Std Dev 1.671
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

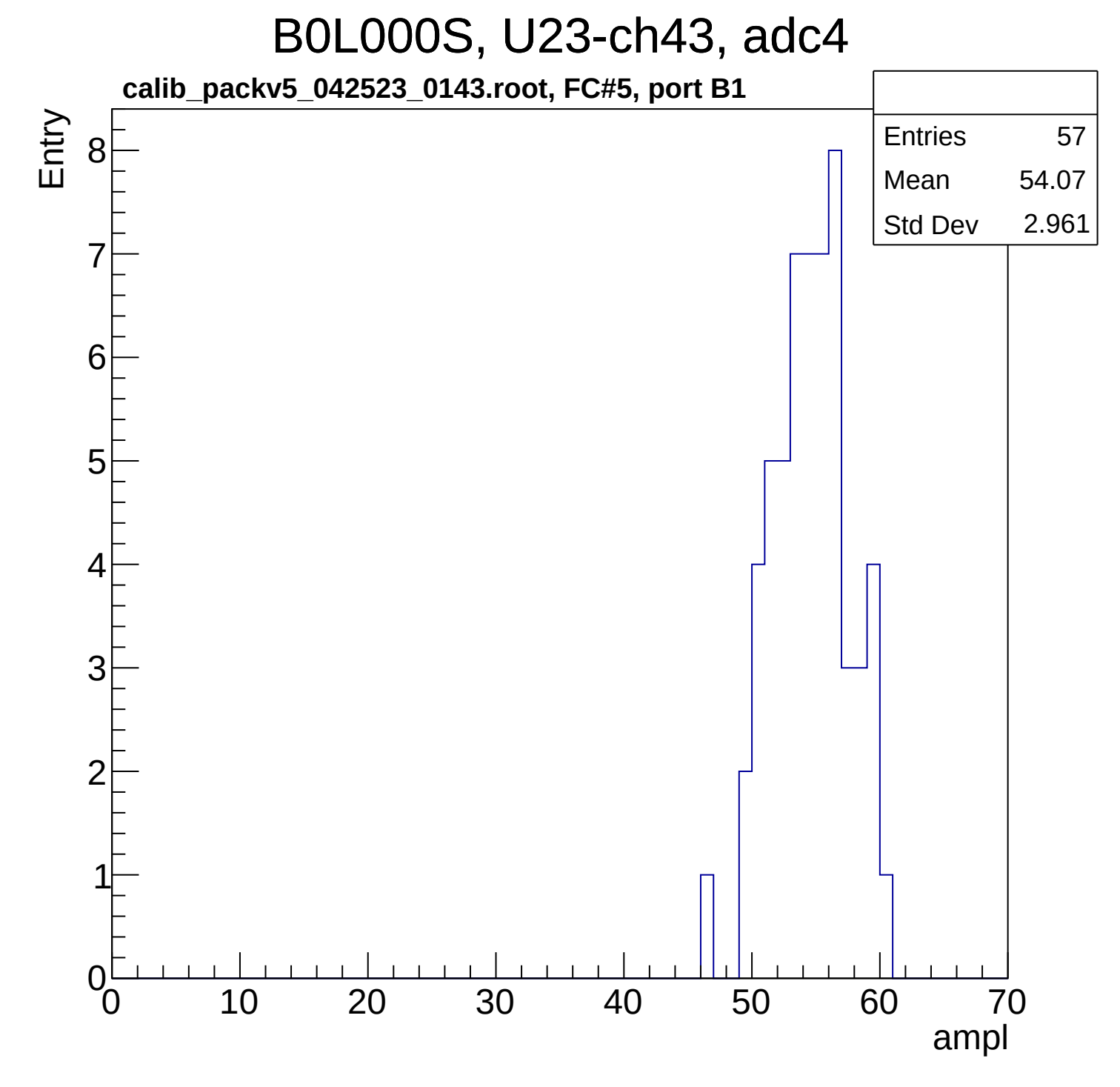

B0L000S, U23-ch42, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 41.6
Std Dev 26.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

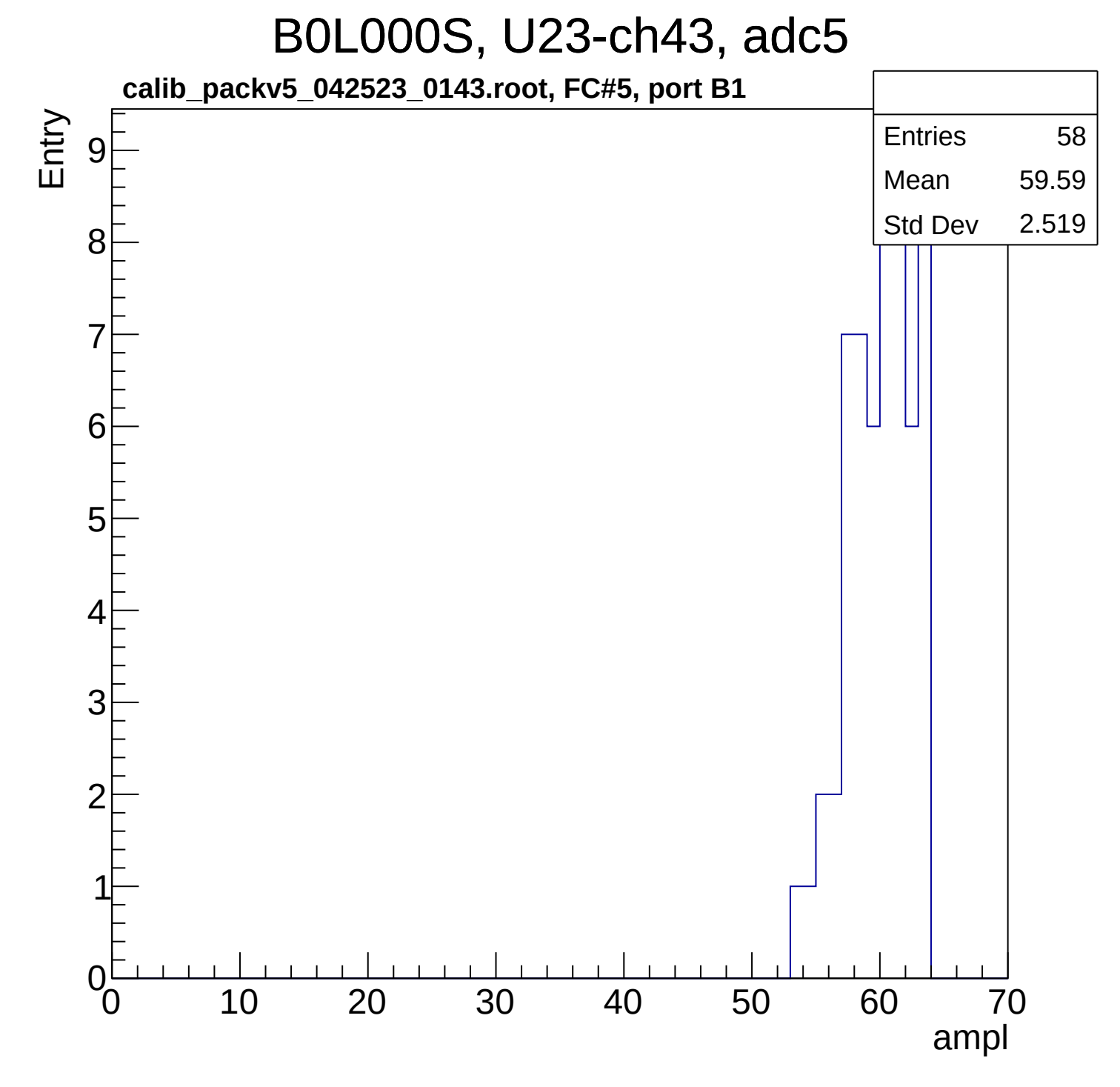

B0L000S, U23-ch43, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 28.74
Std Dev 4.584
Gaus mean : 29.8749
Gaus Width: 4.2080
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

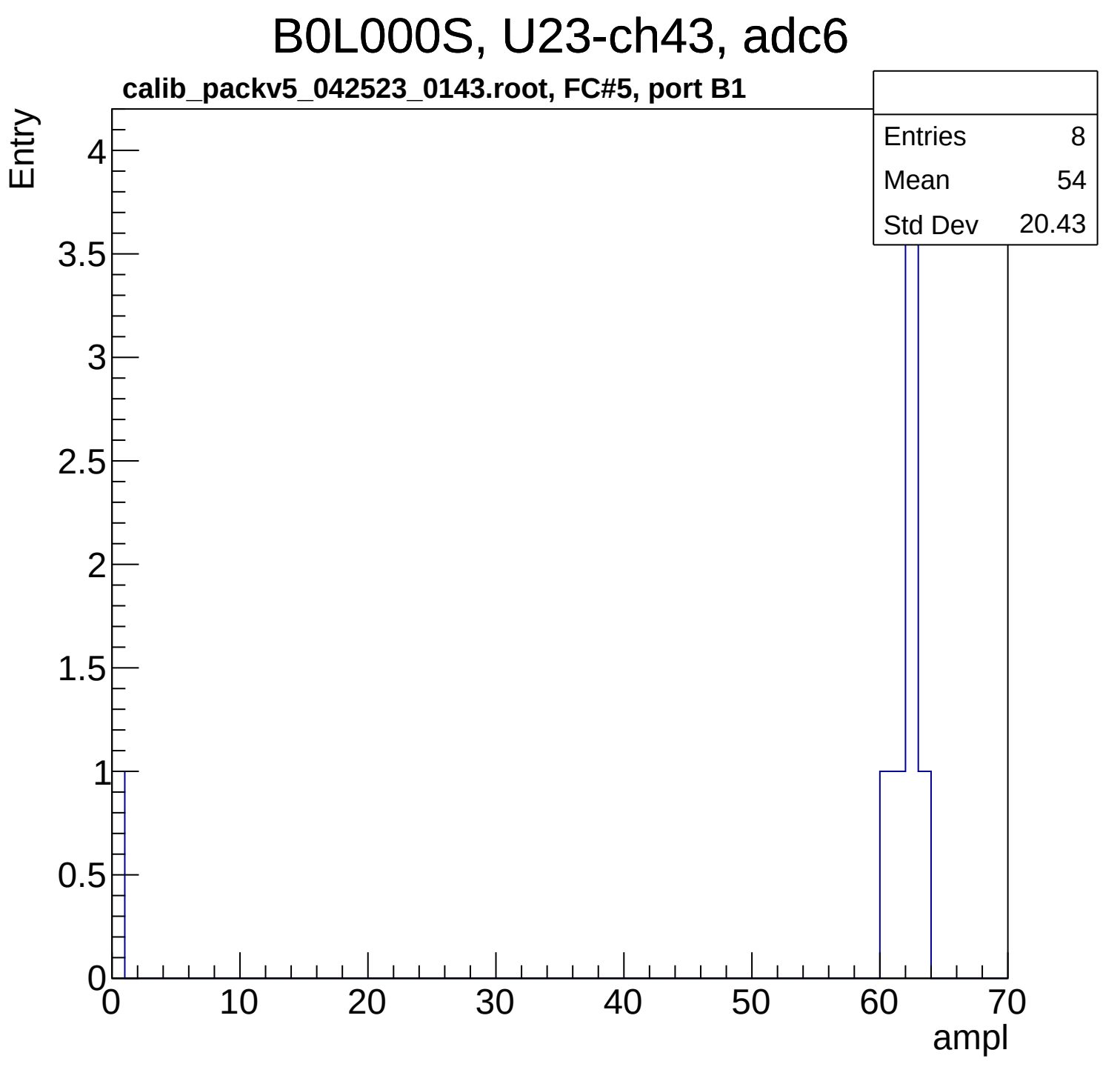

B0L000S, U23-ch43, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.17
Std Dev 2.963
Gaus mean : 36.8562
Gaus Width: 3.0138
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

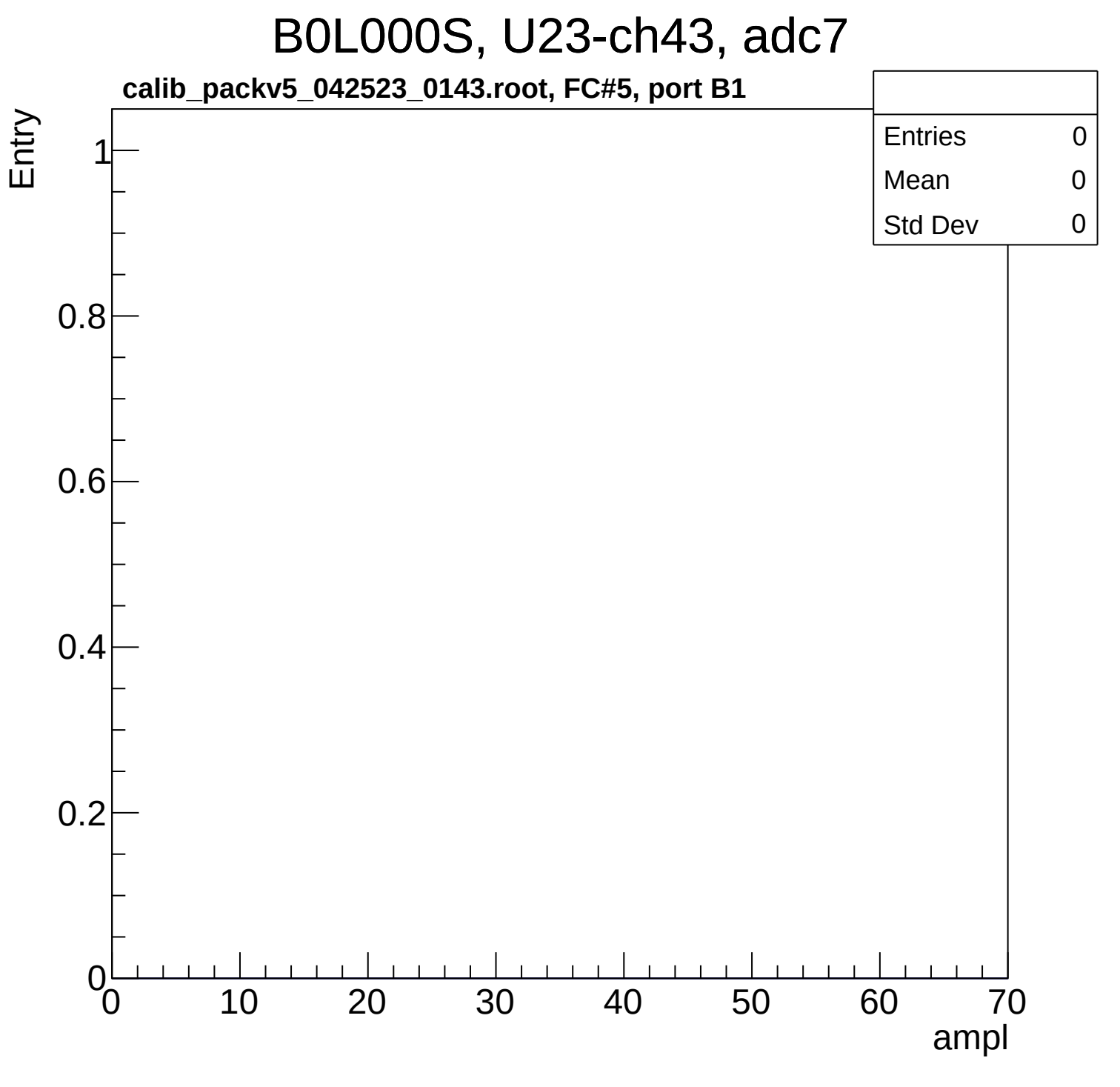

B0L000S, U23-ch43, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.32
Std Dev 2.941
Gaus mean : 43.2529
Gaus Width: 2.5819
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

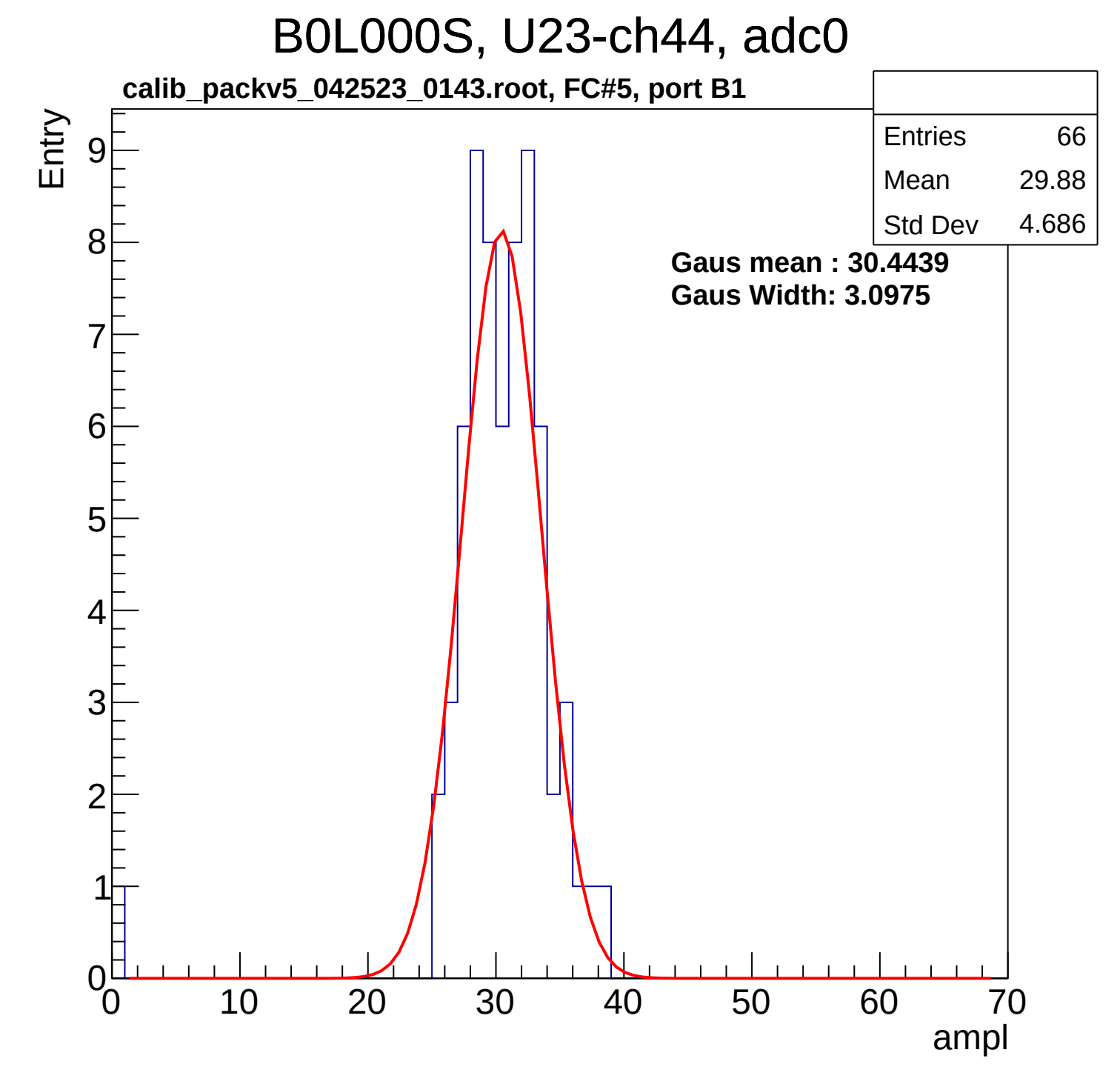

B0L000S, U23-ch43, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 48.17
Std Dev 3.058
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

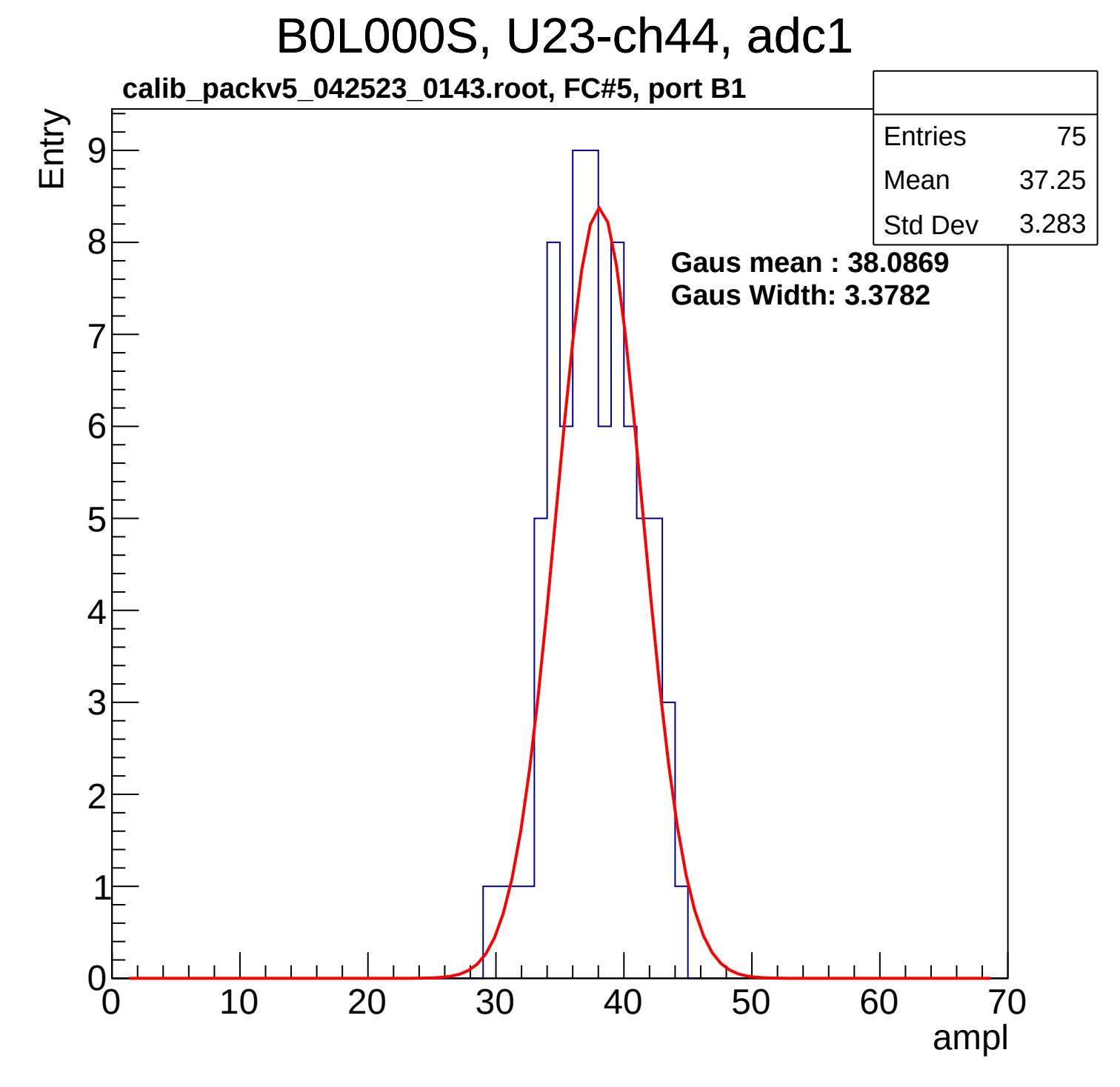

B0L000S, U23-ch43, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 54.07
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

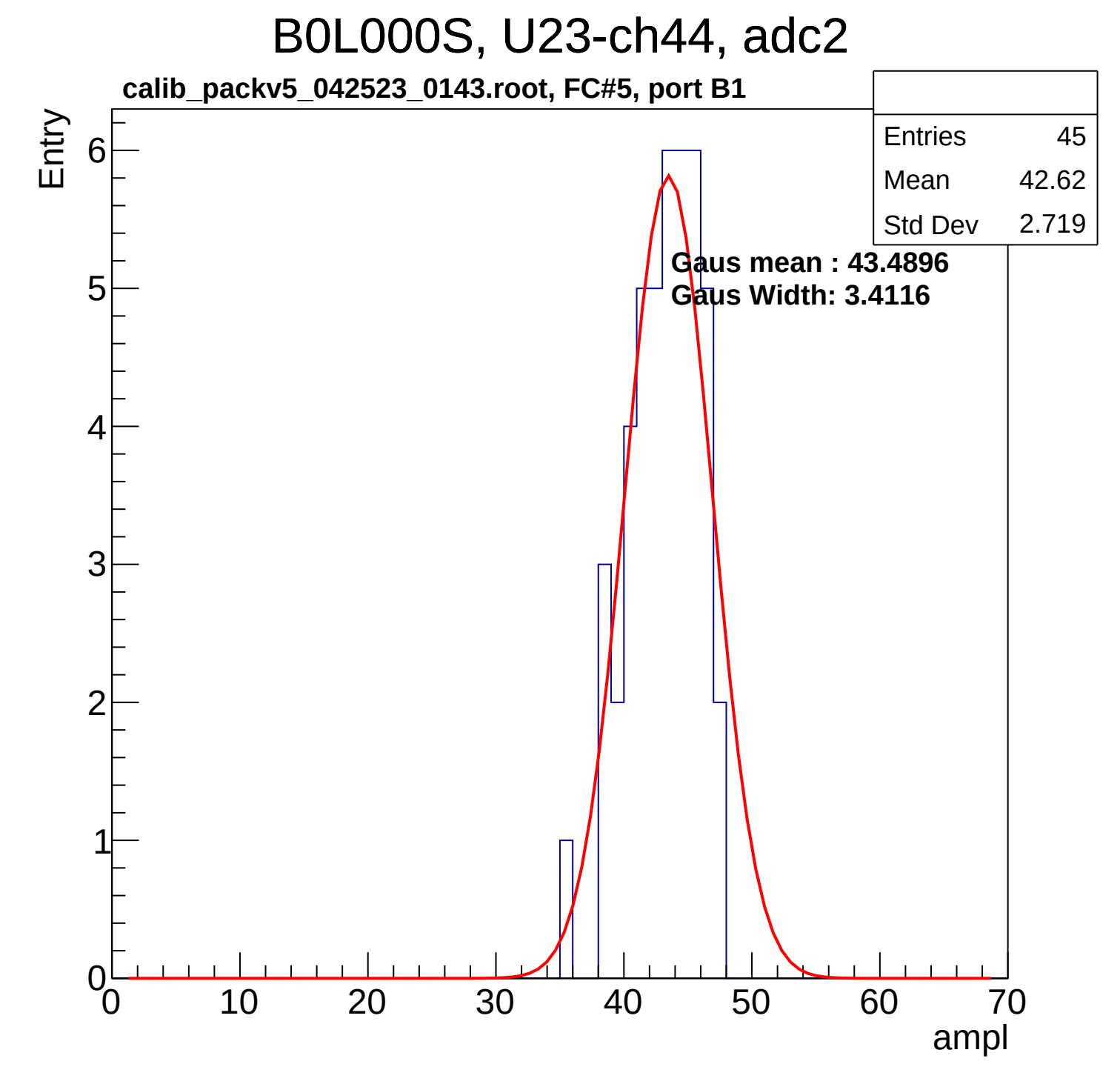

B0L000S, U23-ch43, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 59.59
Std Dev 2.519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

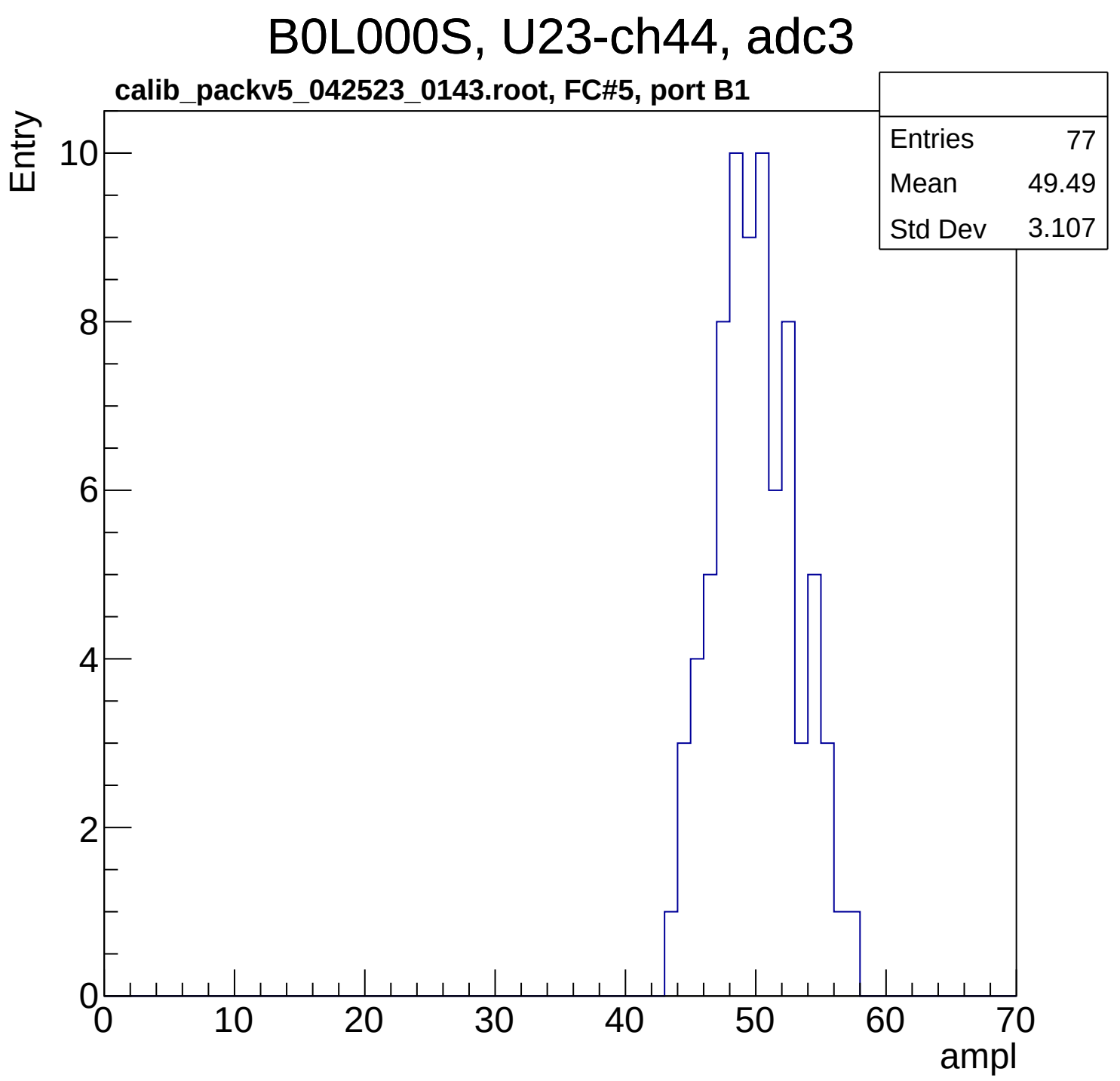

B0L000S, U23-ch43, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54
Std Dev 20.43
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

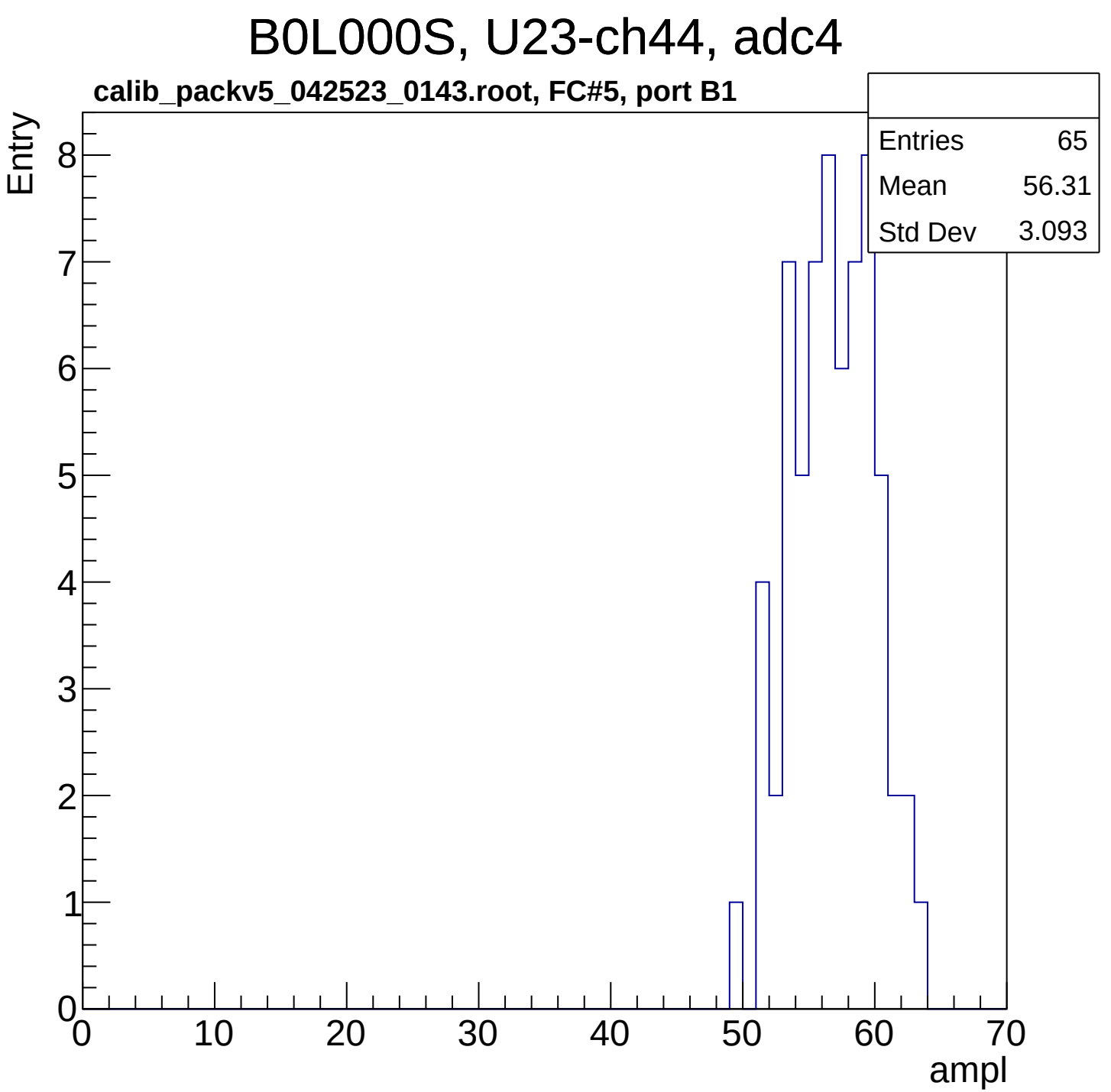

B0L000S, U23-ch43, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

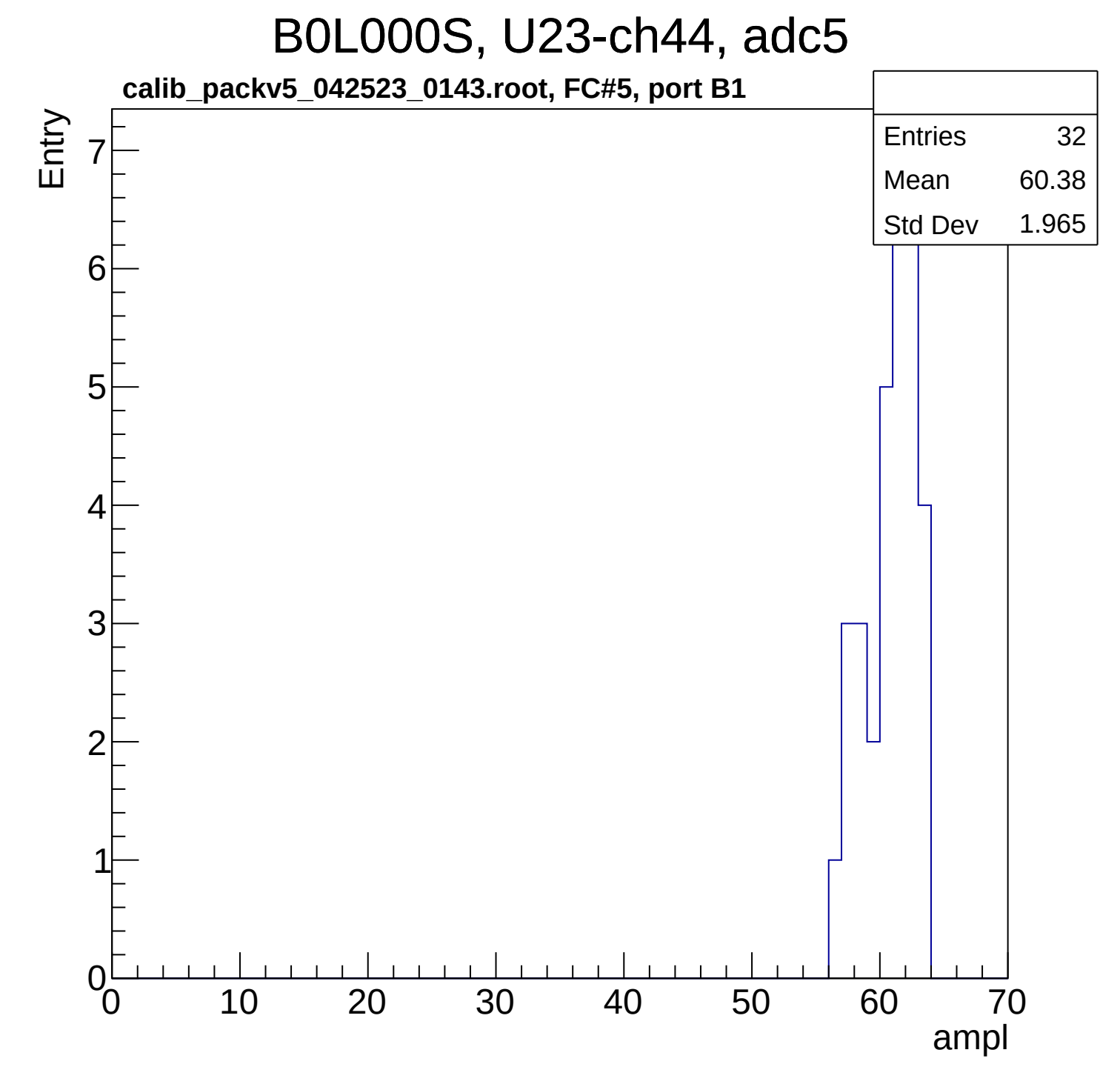

B0L000S, U23-ch44, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.88
Std Dev 4.686
Gaus mean : 30.4439
Gaus Width: 3.0975
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

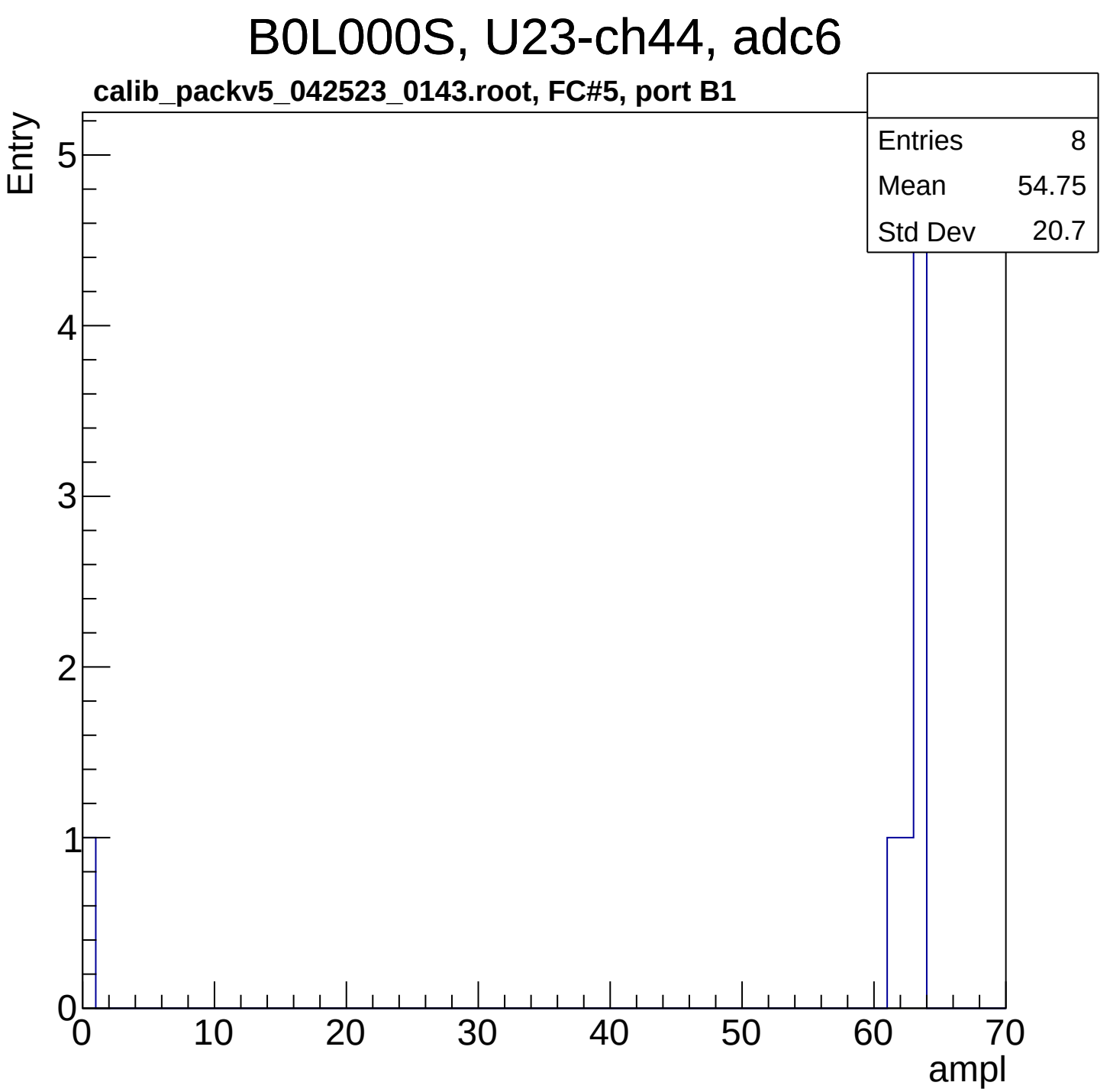

B0L000S, U23-ch44, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 37.25
Std Dev 3.283
Gaus mean : 38.0869
Gaus Width: 3.3782
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

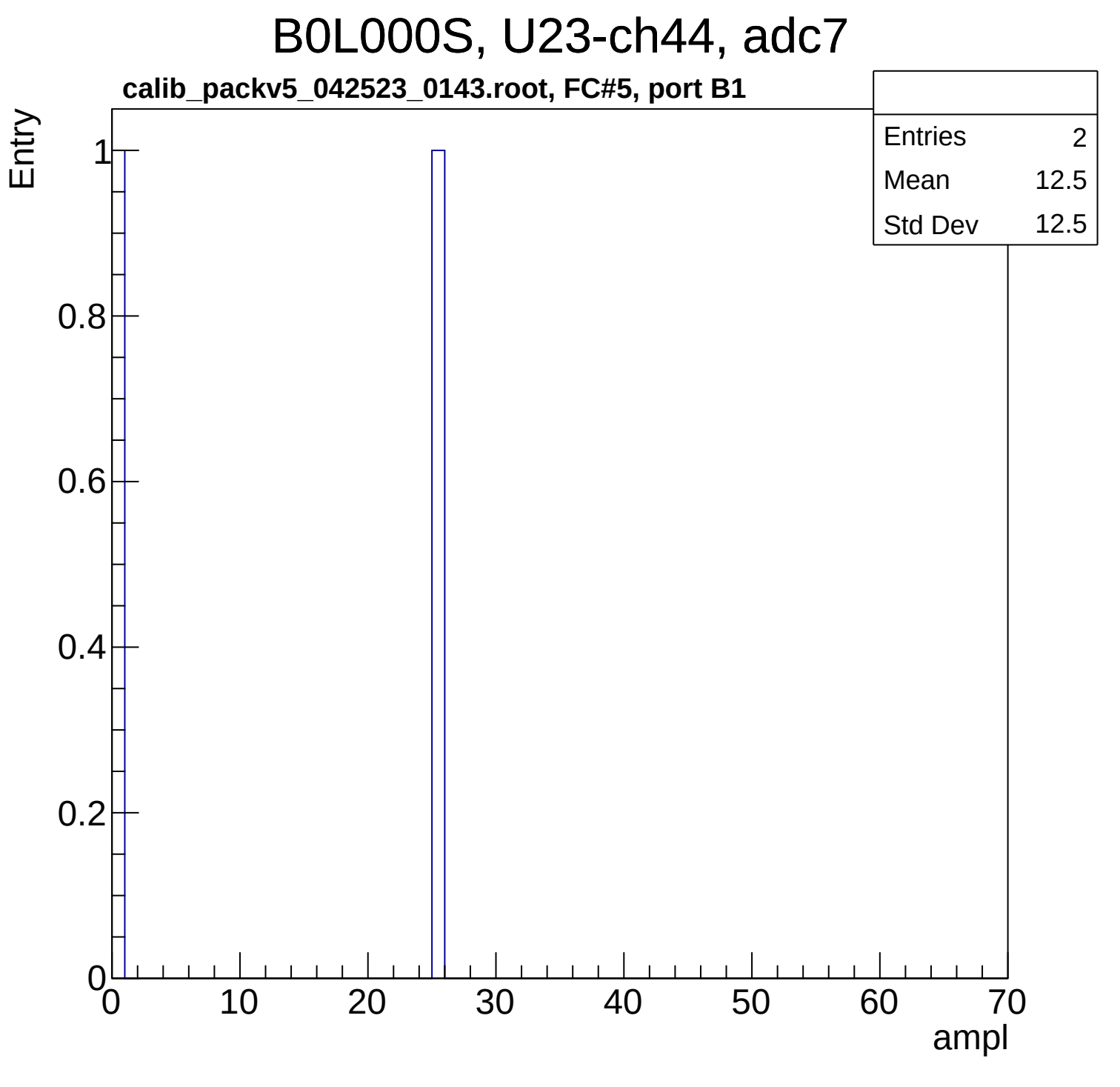

B0L000S, U23-ch44, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 42.62
Std Dev 2.719
Gaus mean : 43.4896
Gaus Width: 3.4116
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

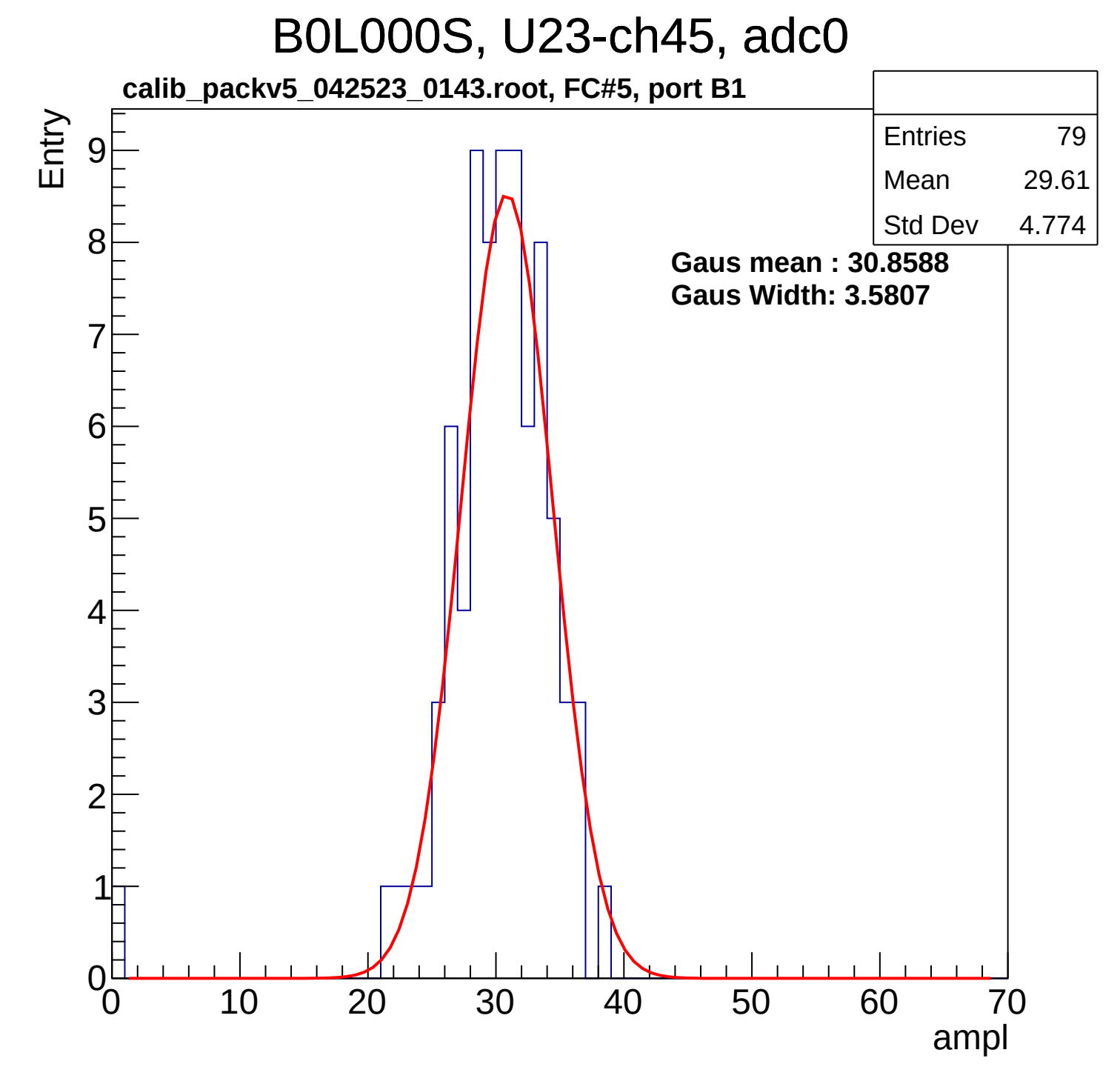

B0L000S, U23-ch44, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 49.49
Std Dev 3.107
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

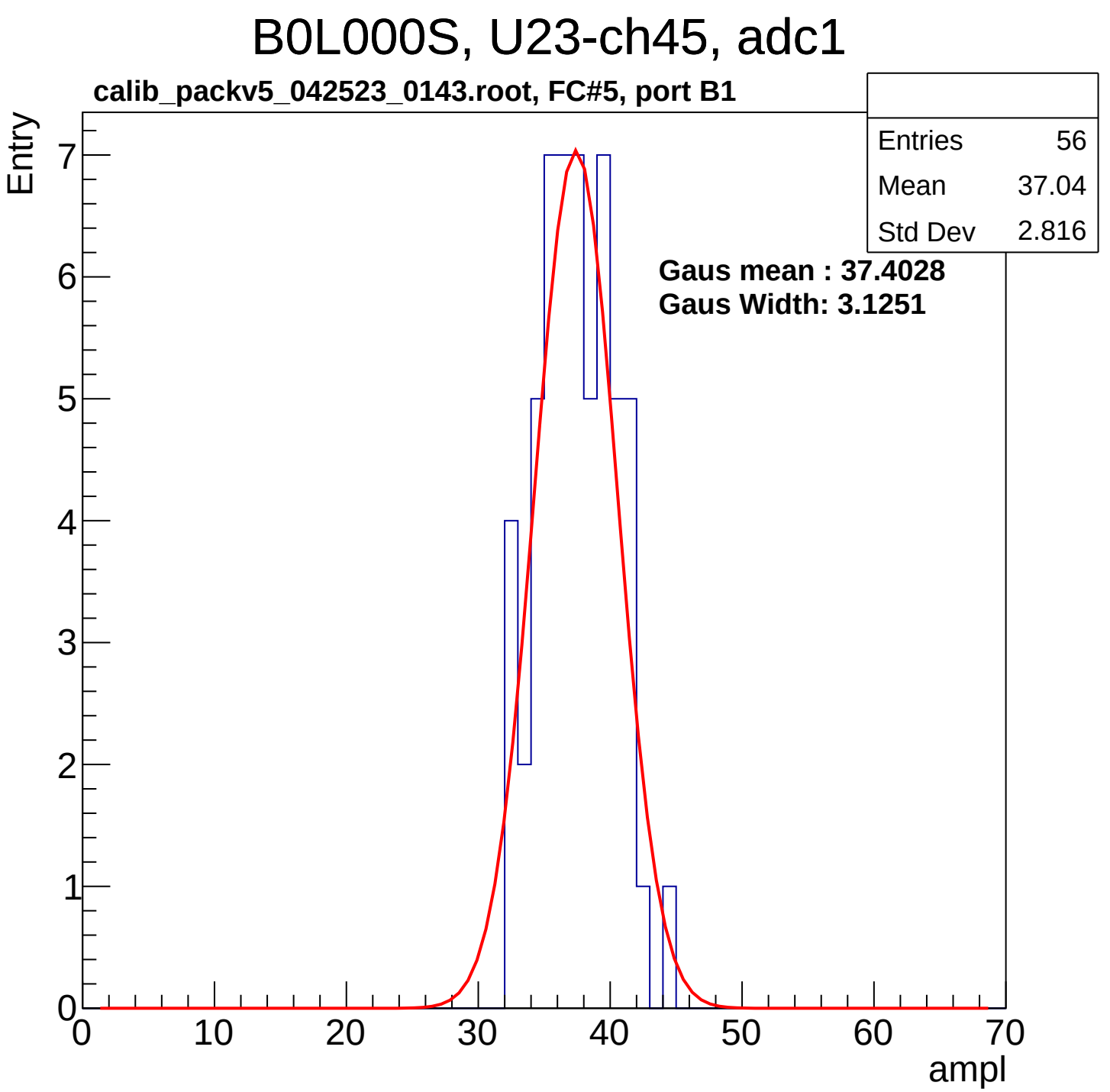

B0L000S, U23-ch44, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 56.31
Std Dev 3.093
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

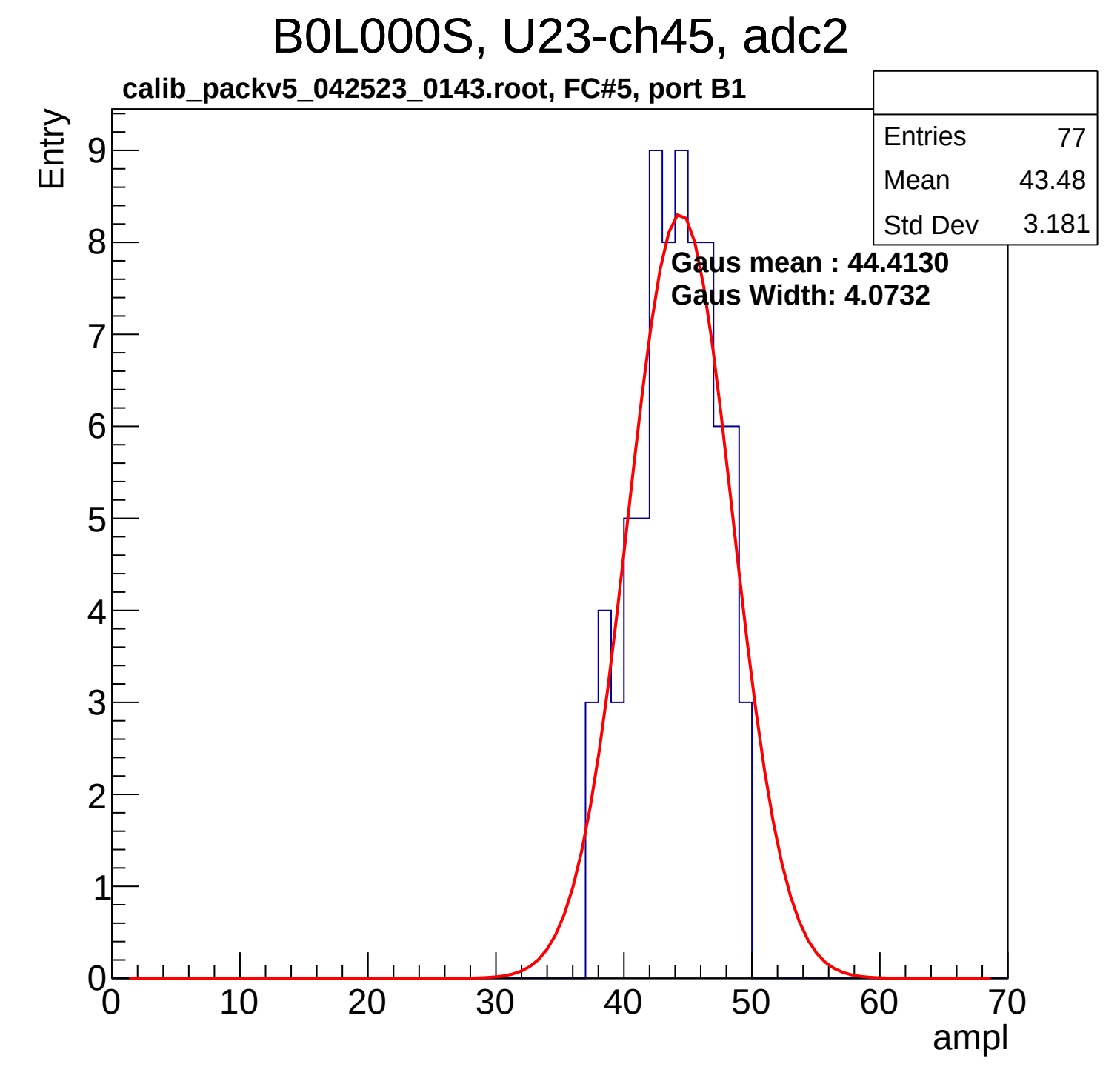

B0L000S, U23-ch44, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 60.38
Std Dev 1.965
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

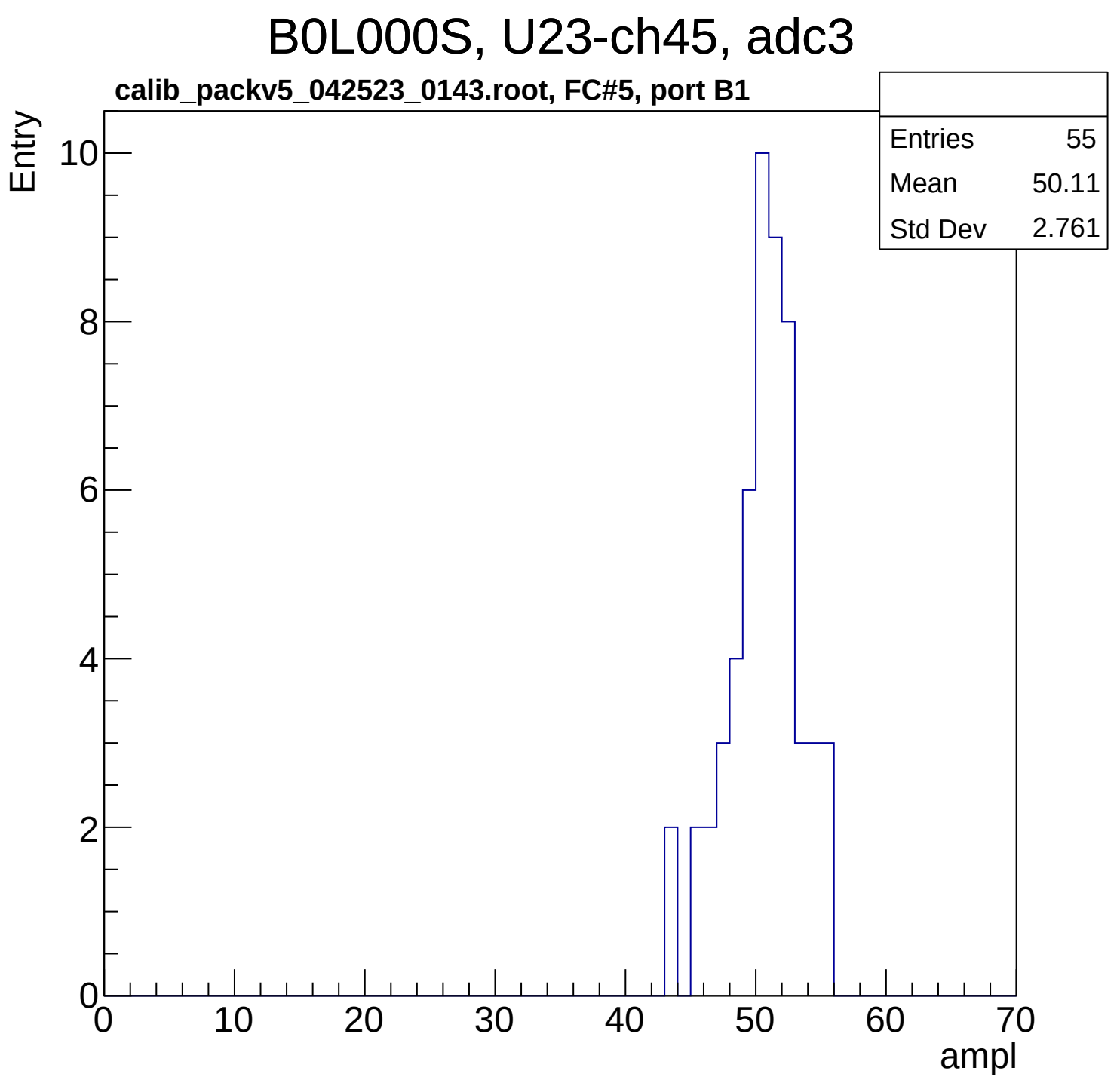

B0L000S, U23-ch44, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.75
Std Dev 20.7
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

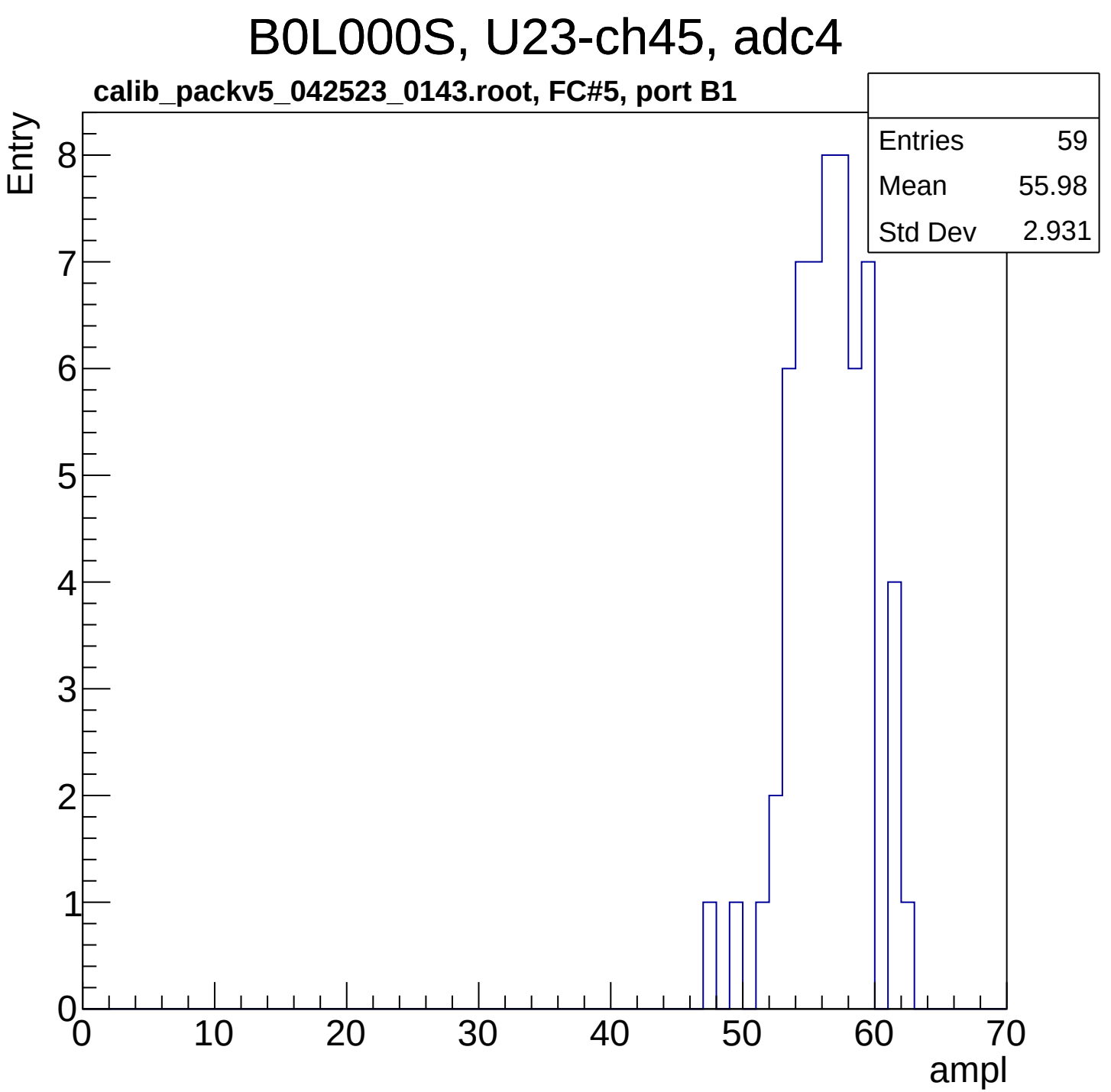

B0L000S, U23-ch44, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

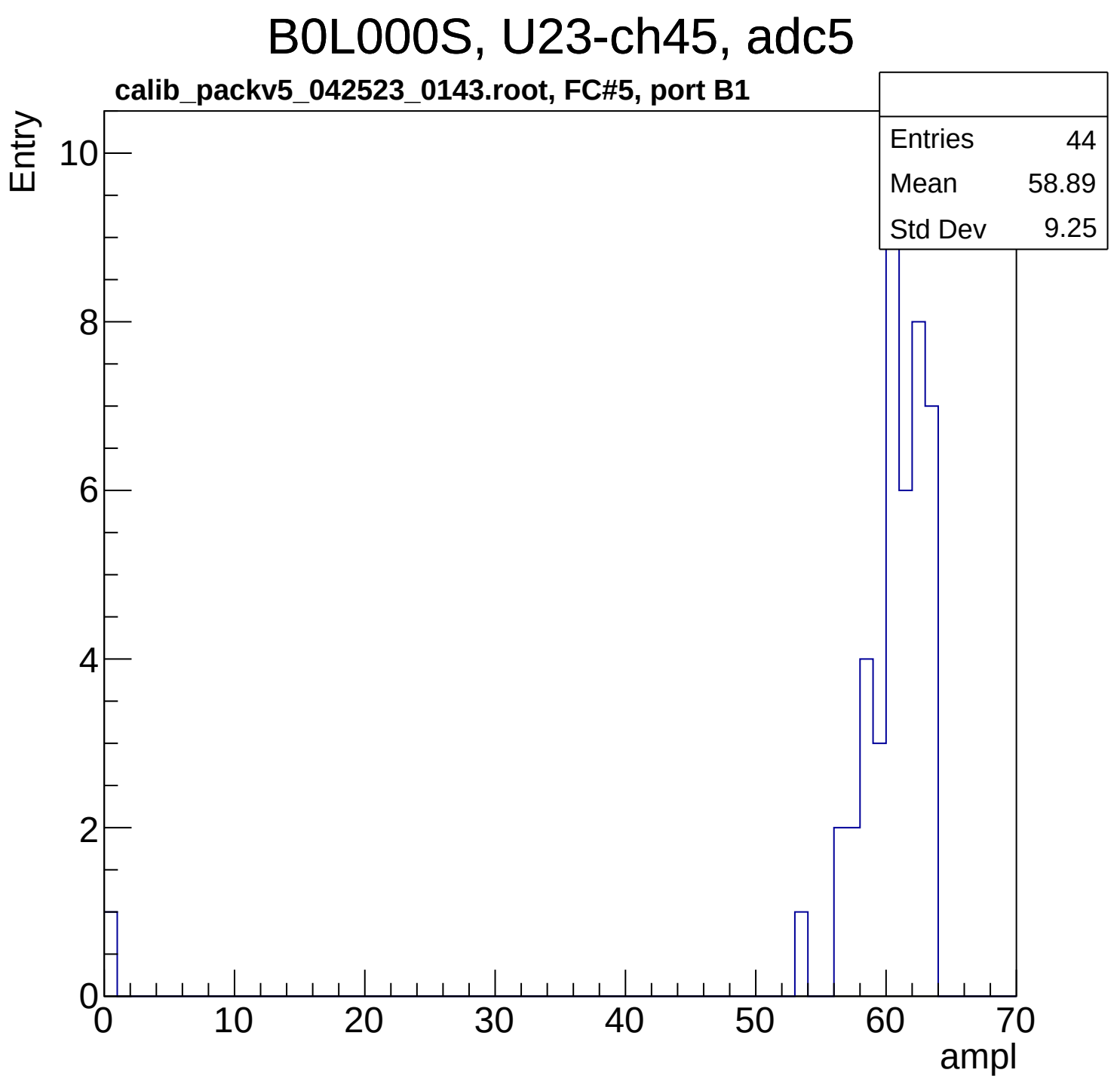

B0L000S, U23-ch45, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 29.61
Std Dev 4.774
Gaus mean : 30.8588
Gaus Width: 3.5807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

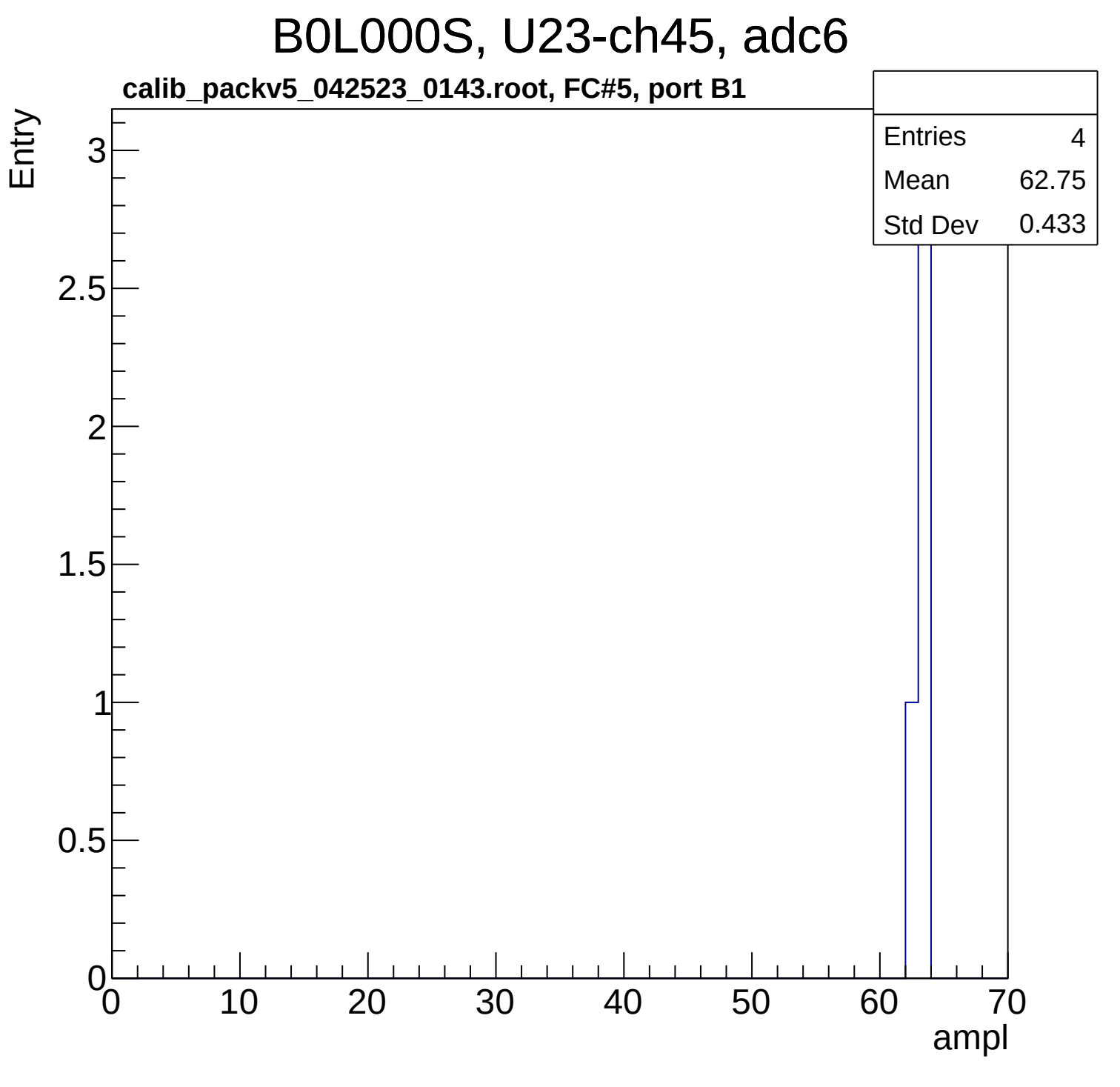

B0L000S, U23-ch45, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.04
Std Dev 2.816
Gaus mean : 37.4028
Gaus Width: 3.1251
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

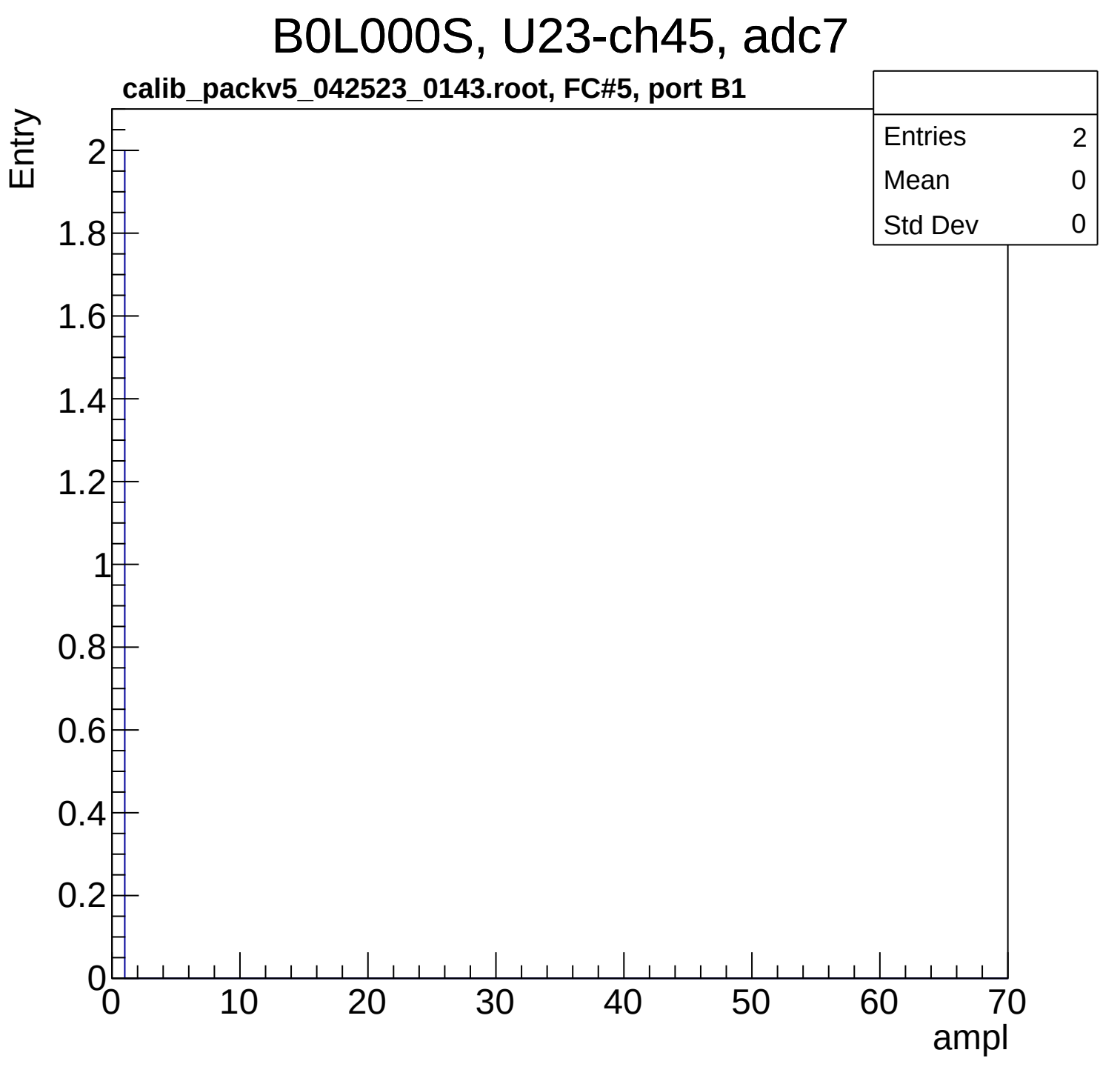

B0L000S, U23-ch45, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 43.48
Std Dev 3.181
Gaus mean : 44.4130
Gaus Width: 4.0732
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

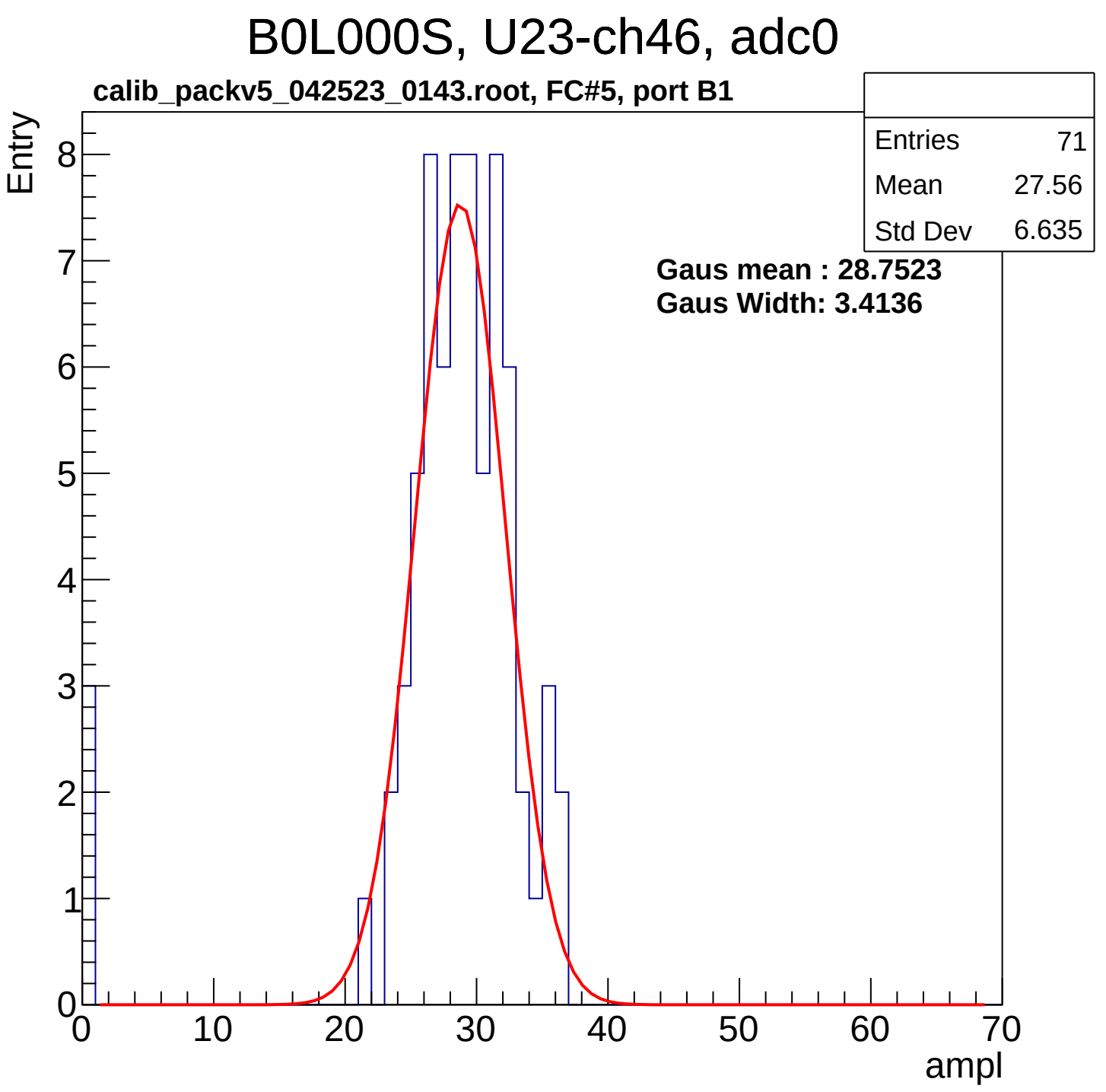

B0L000S, U23-ch45, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 50.11
Std Dev 2.761
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

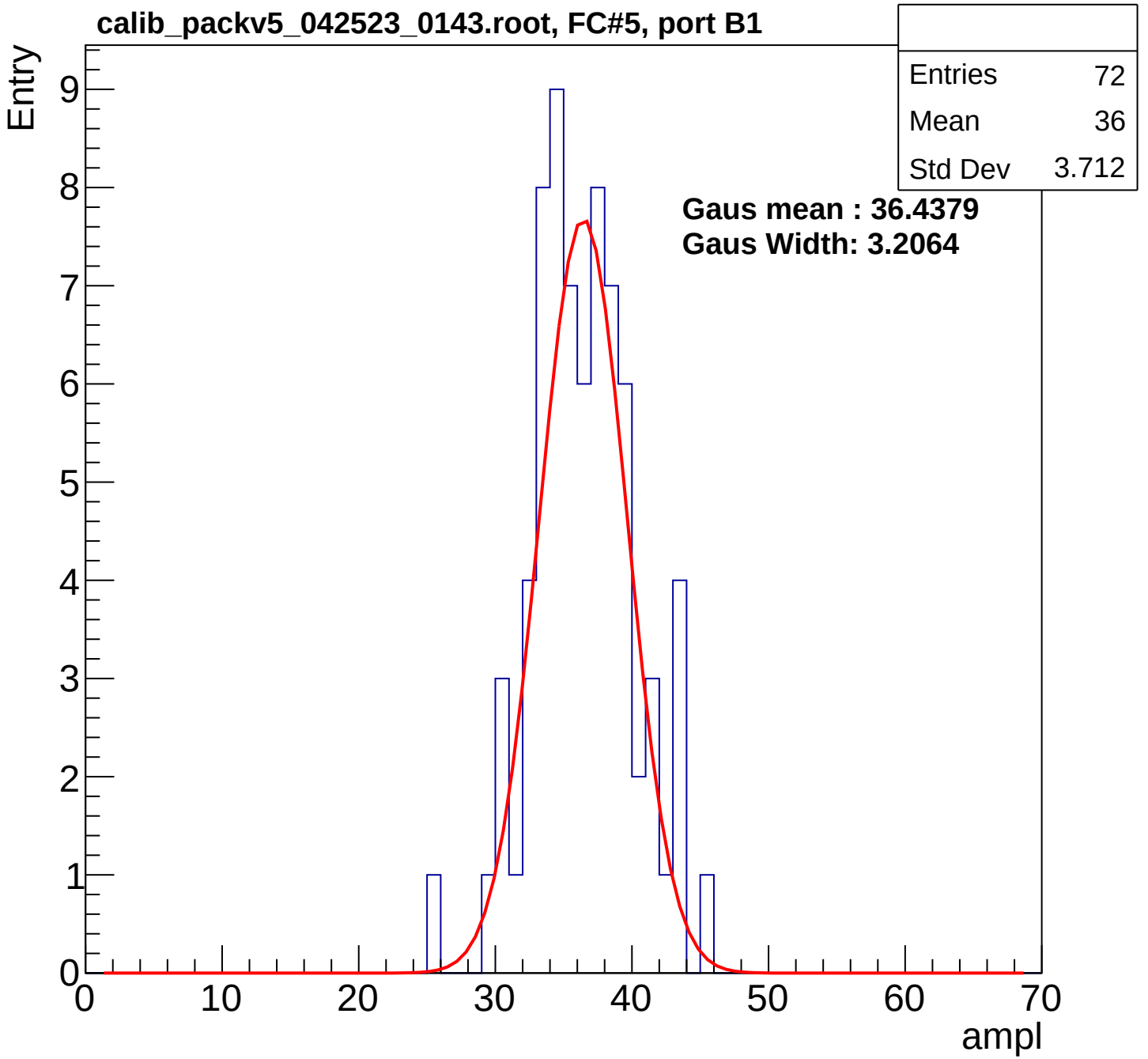

B0L000S, U23-ch45, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.98
Std Dev 2.931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

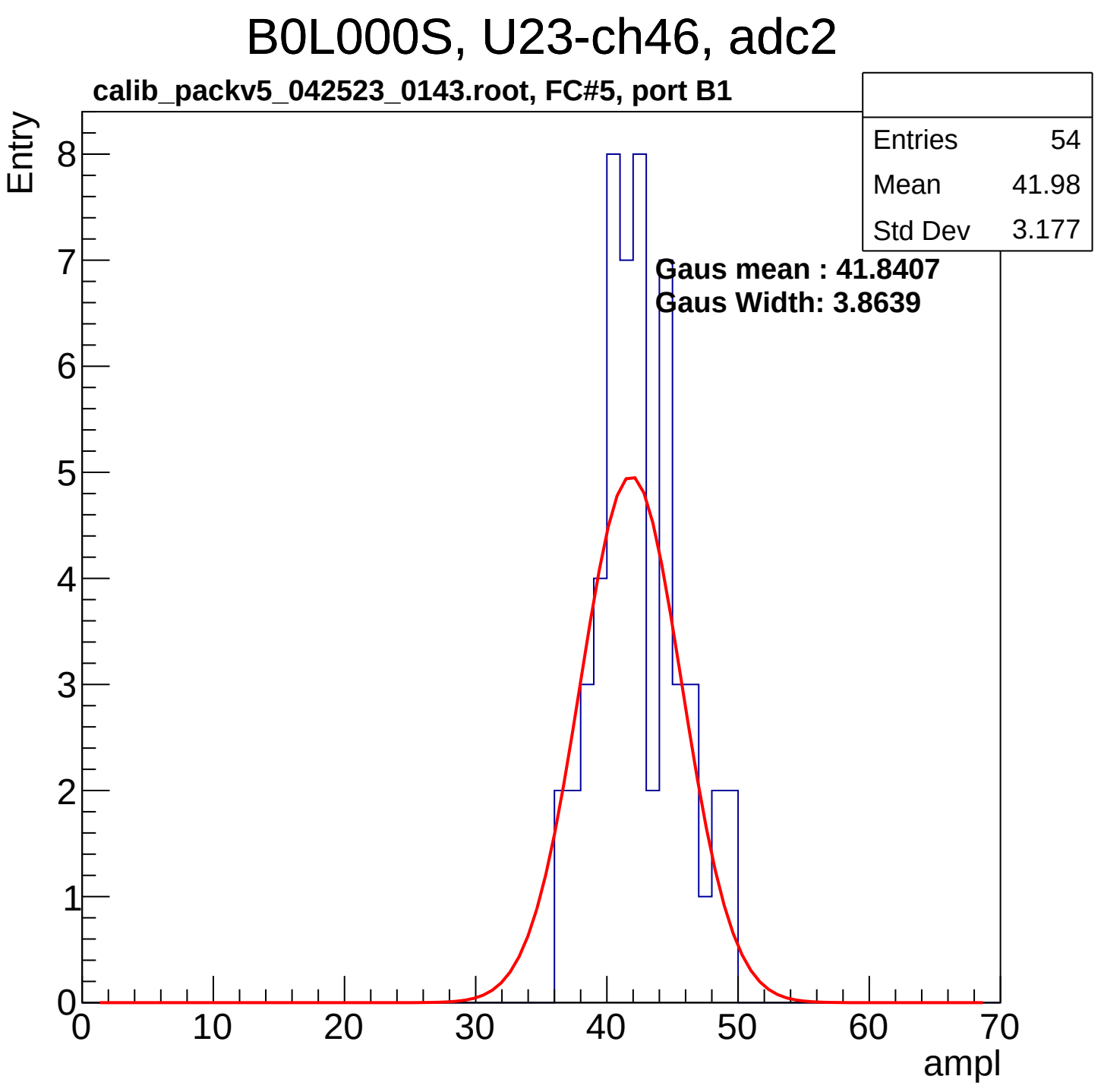

B0L000S, U23-ch45, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.89
Std Dev 9.25
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

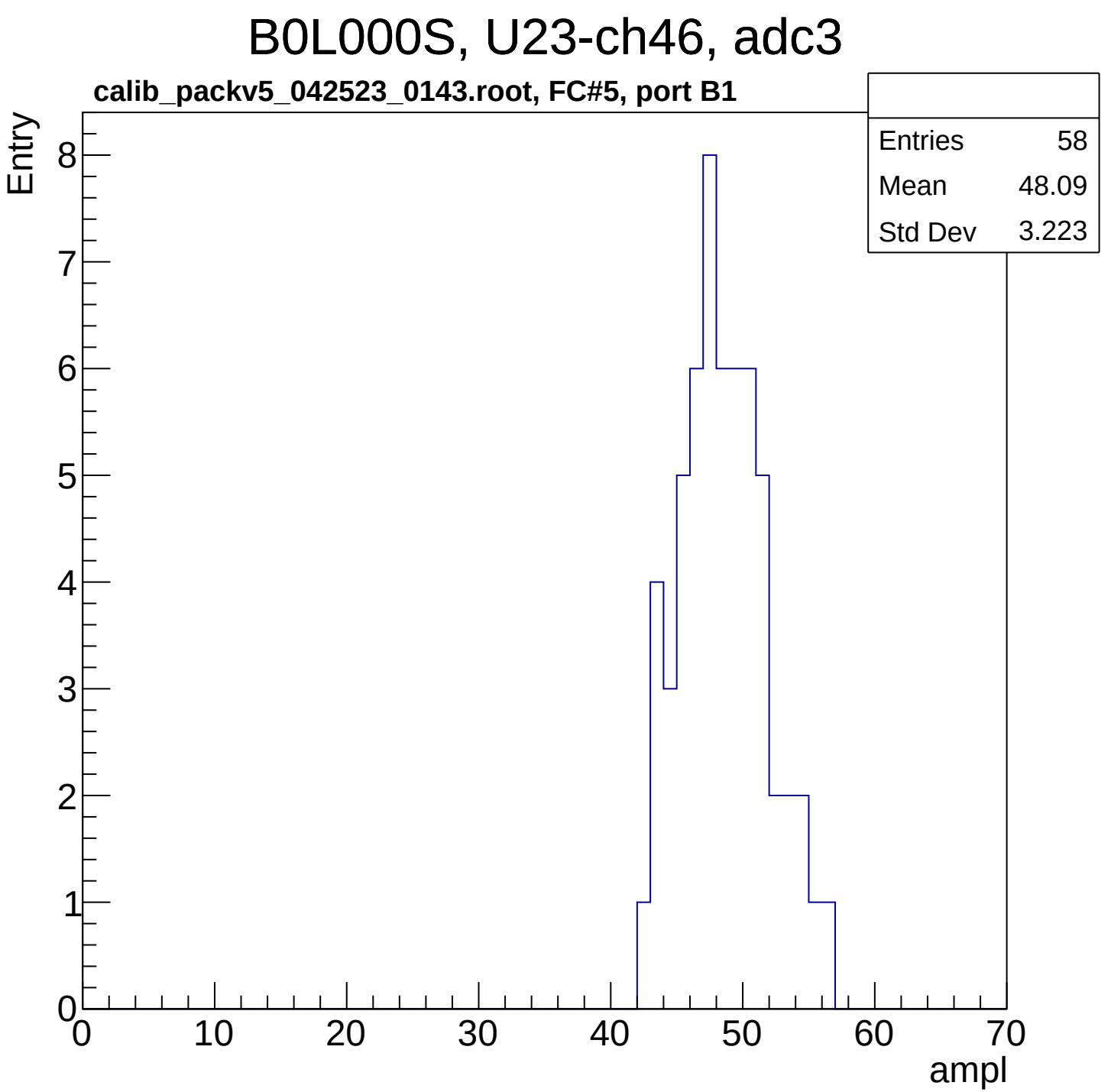

B0L000S, U23-ch45, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

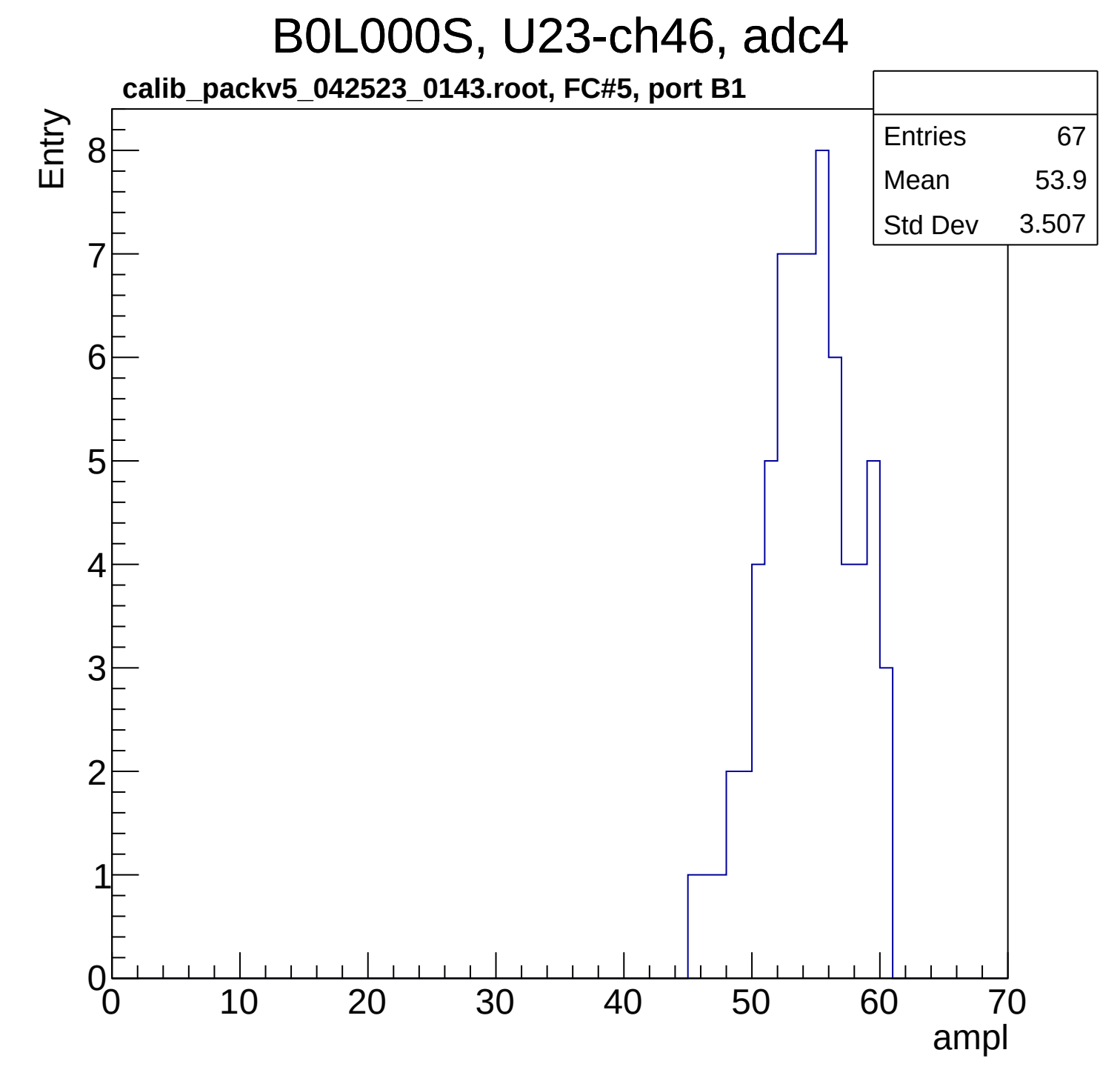

B0L000S, U23-ch45, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

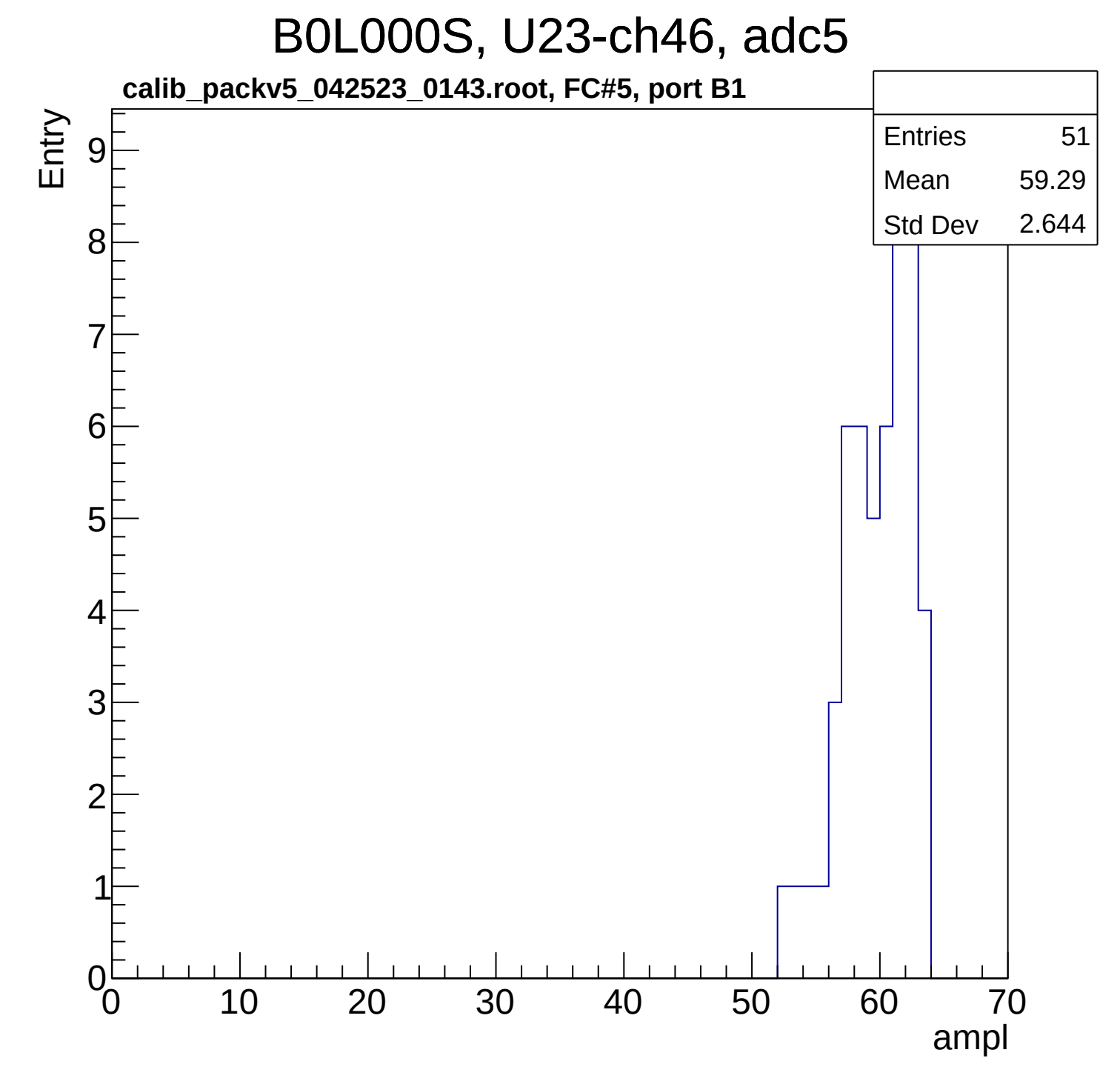

B0L000S, U23-ch46, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 27.56
Std Dev 6.635
Gaus mean : 28.7523
Gaus Width: 3.4136
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

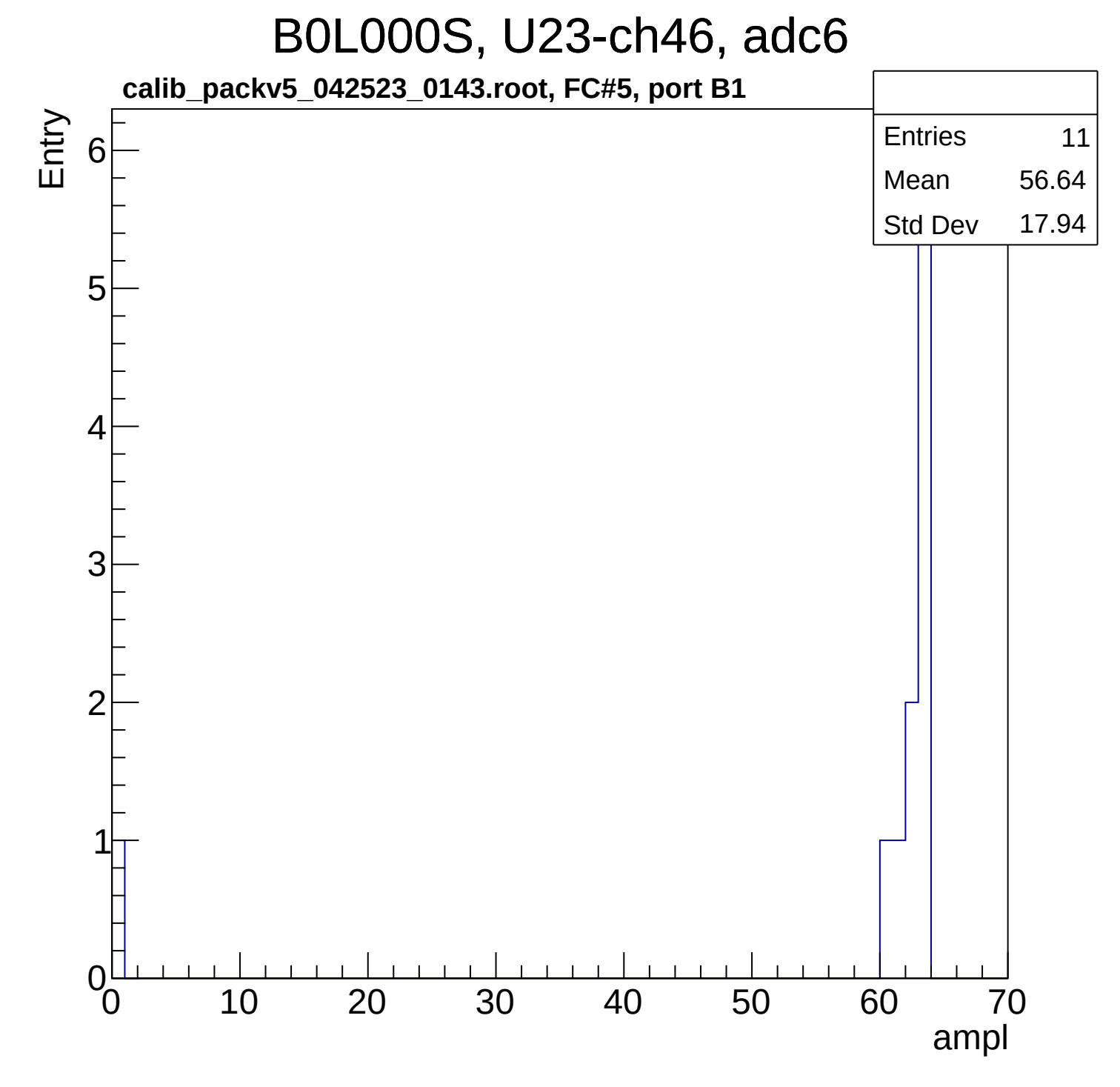

B0L000S, U23-ch46, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36
Std Dev 3.712
Gaus mean : 36.4379
Gaus Width: 3.2064
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

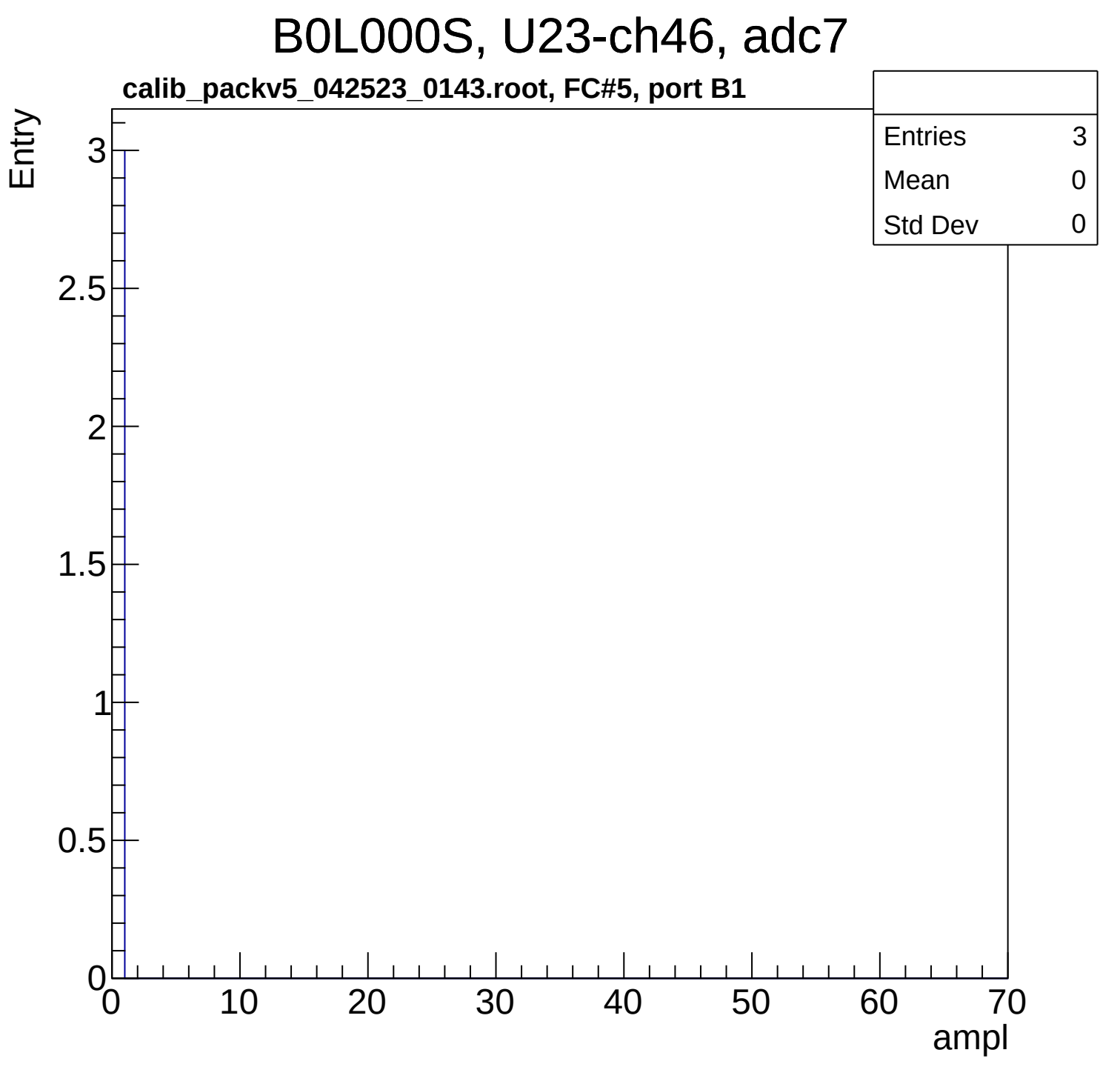

B0L000S, U23-ch46, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 41.98
Std Dev 3.177
Gaus mean : 41.8407
Gaus Width: 3.8639
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

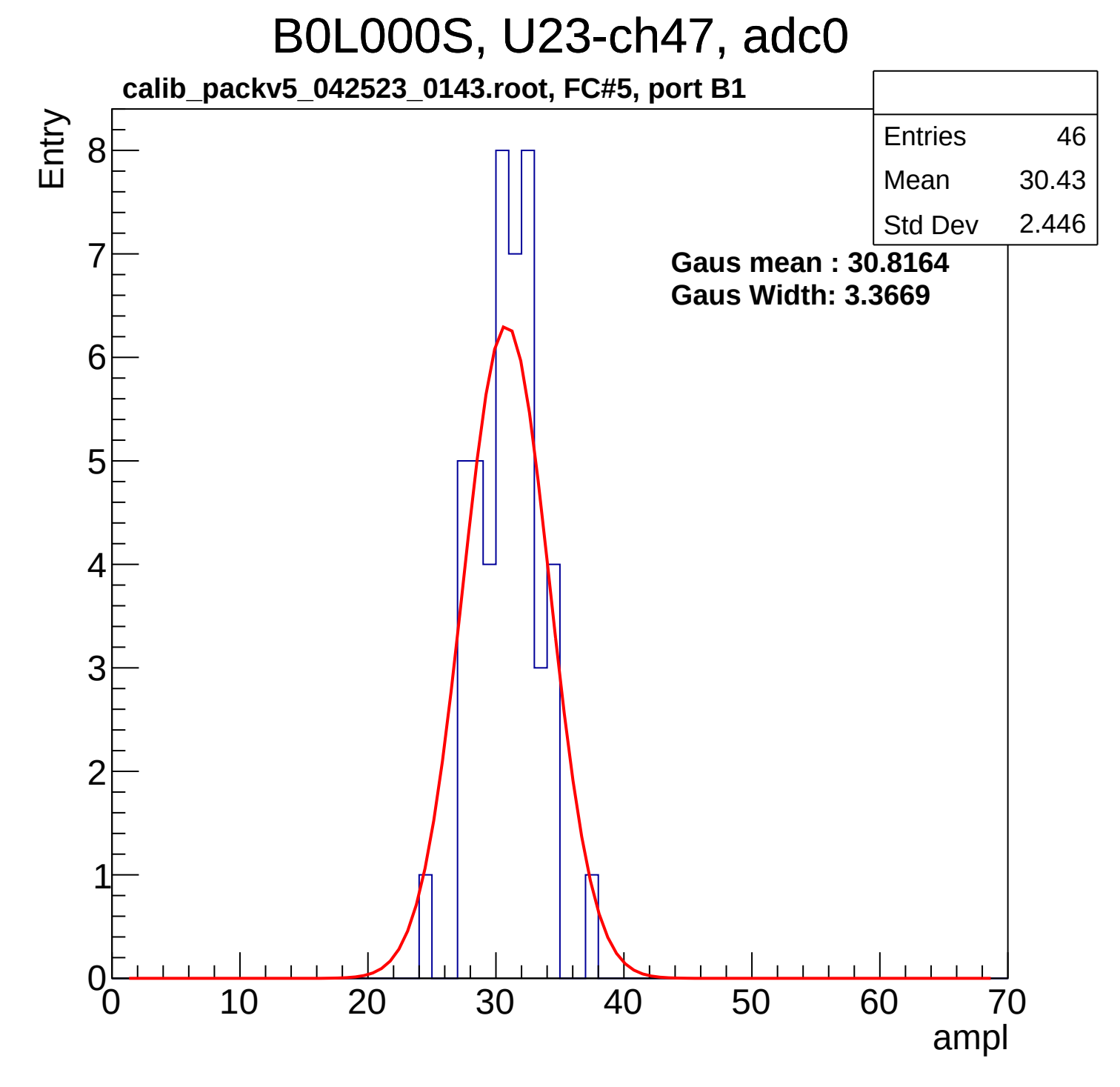

B0L000S, U23-ch46, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.09
Std Dev 3.223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

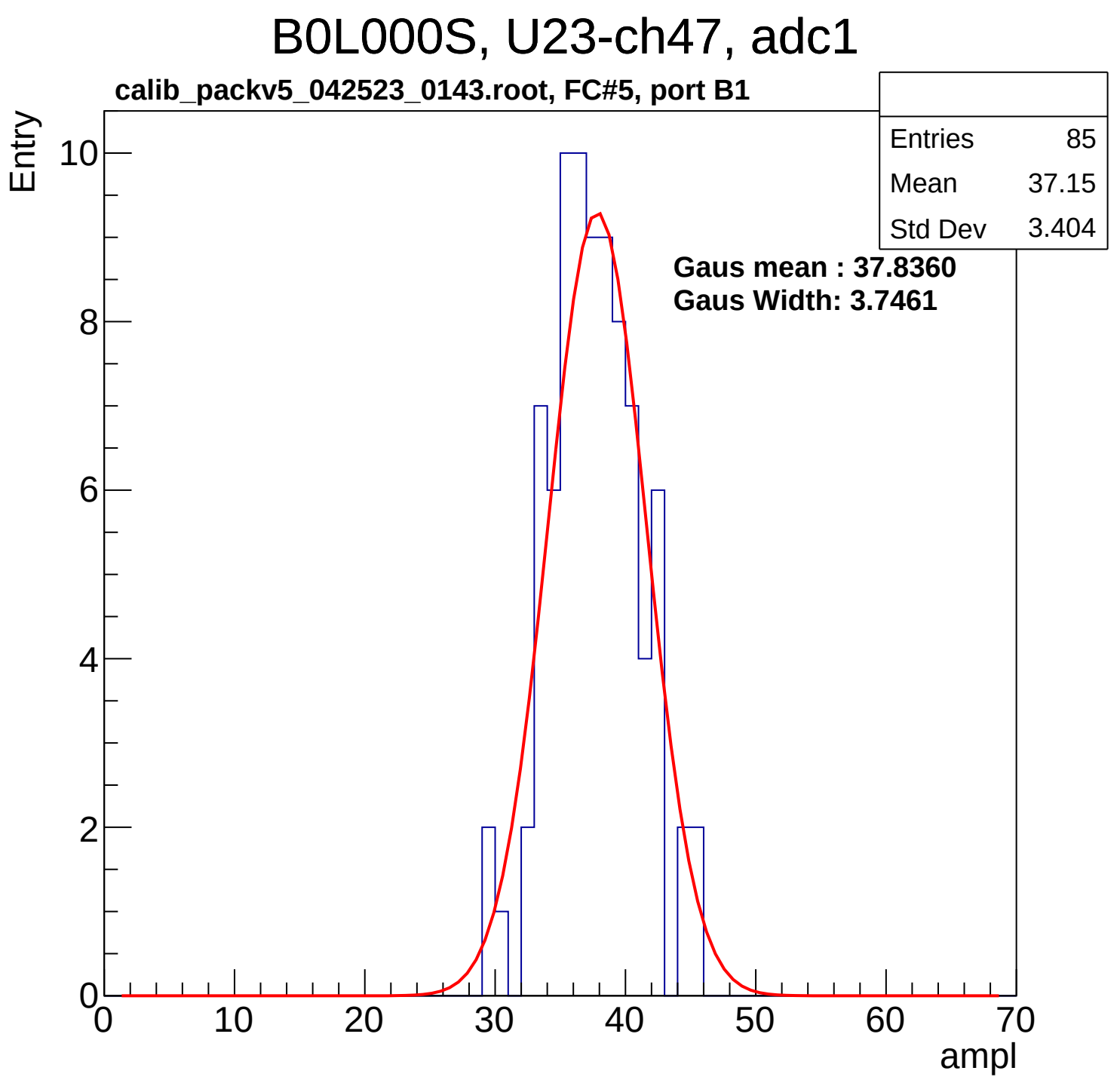

B0L000S, U23-ch46, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 53.9
Std Dev 3.507
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

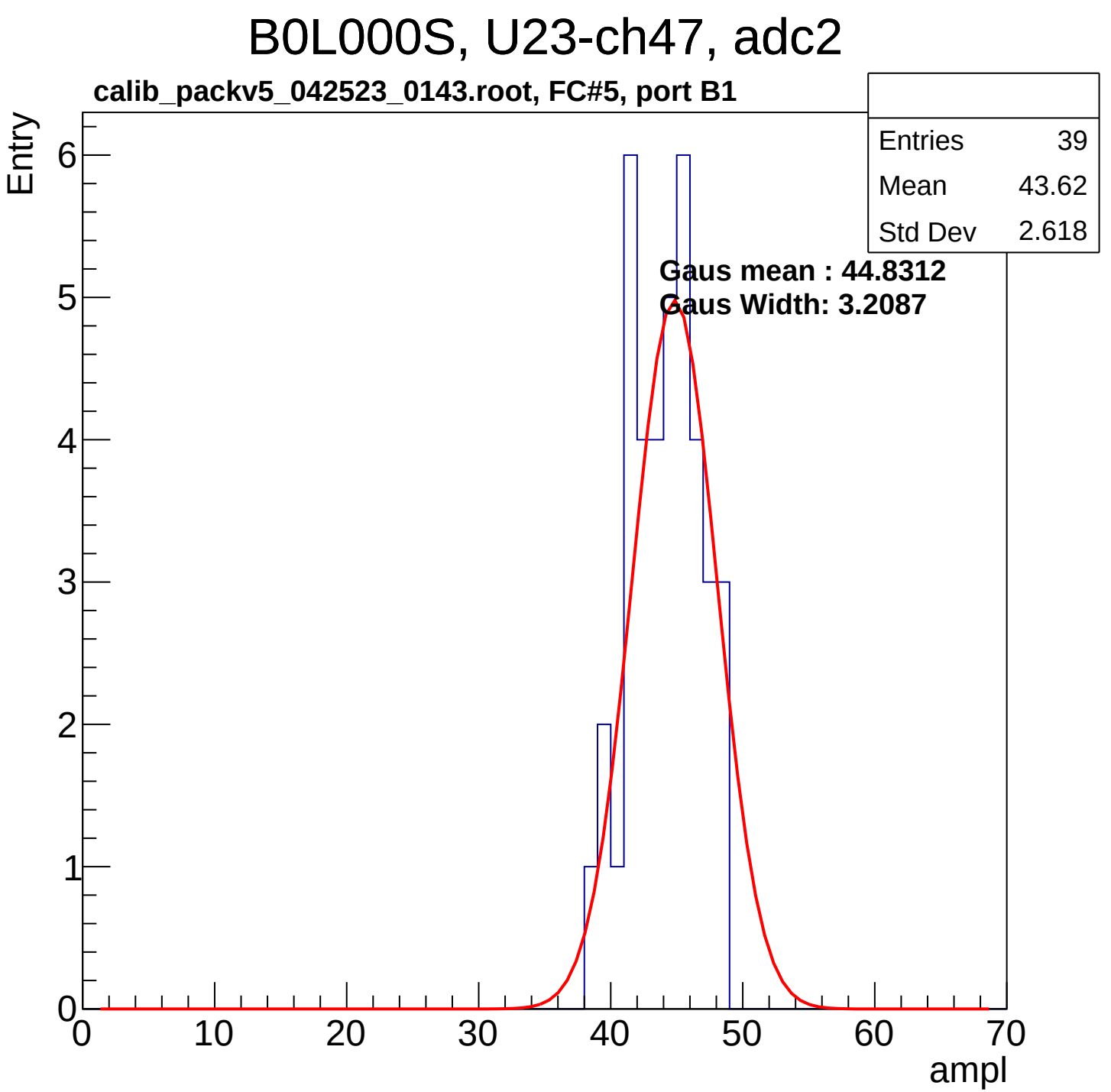

B0L000S, U23-ch46, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.29
Std Dev 2.644
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

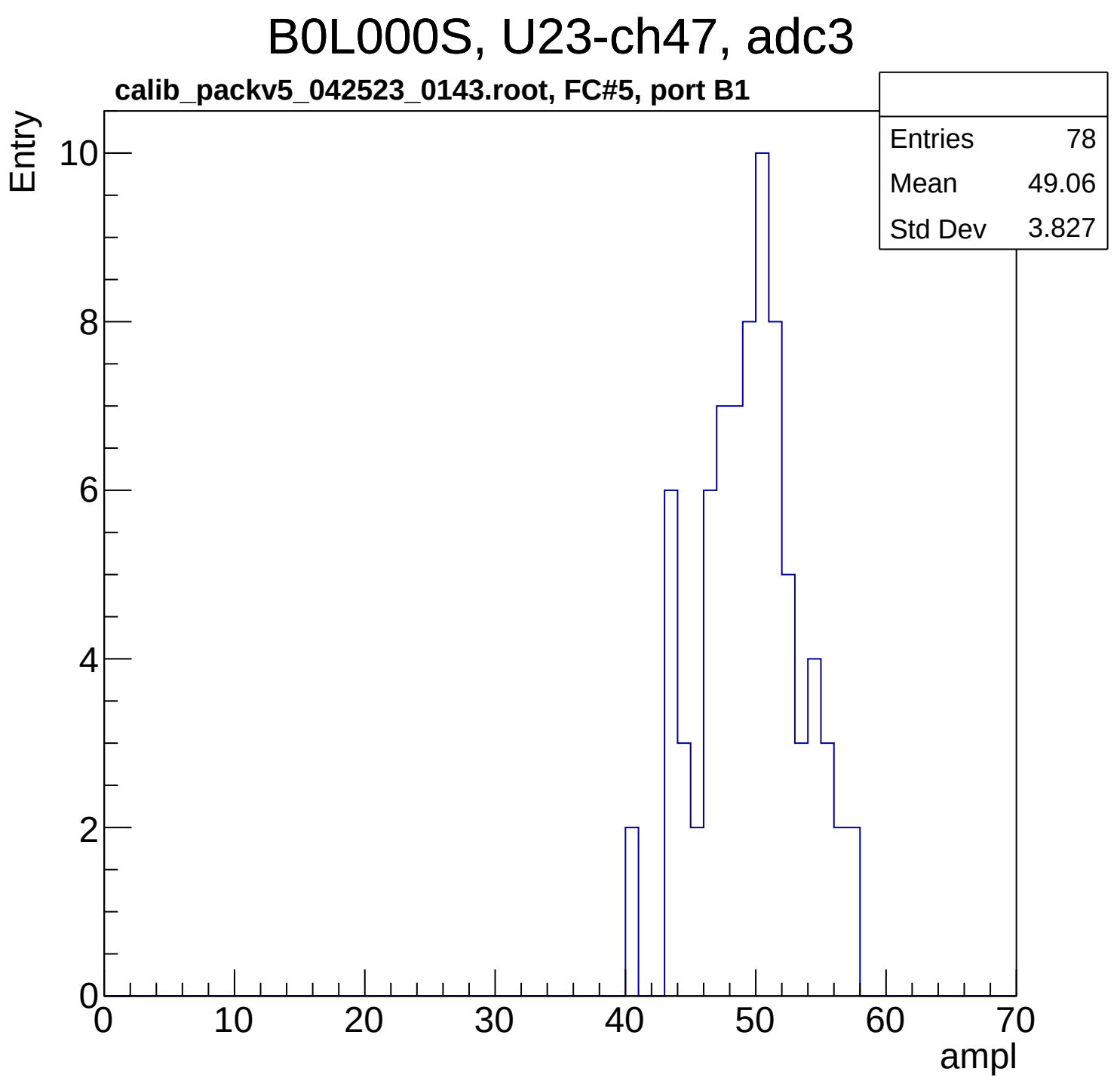

B0L000S, U23-ch46, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 56.64
Std Dev 17.94
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

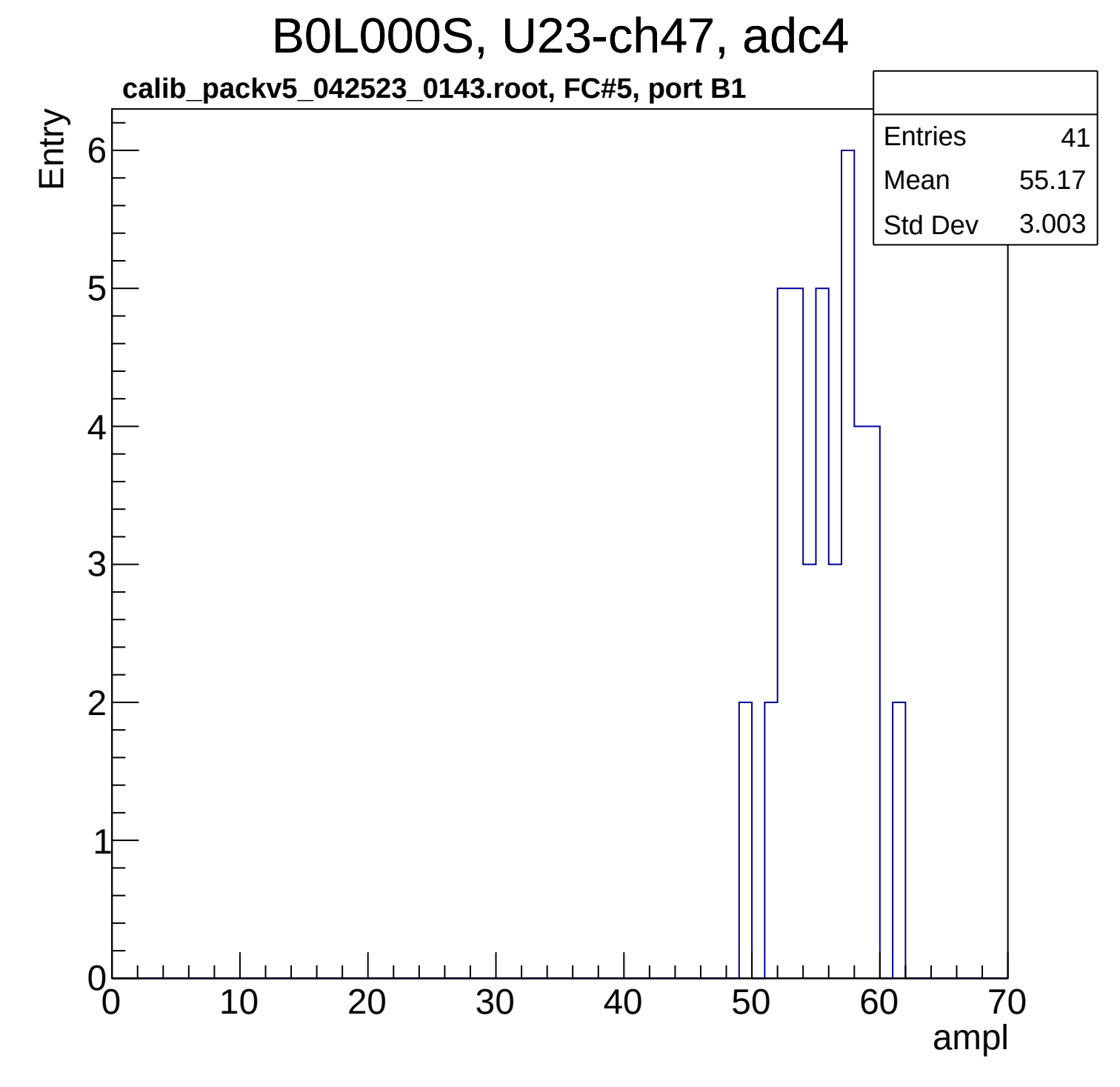

B0L000S, U23-ch46, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

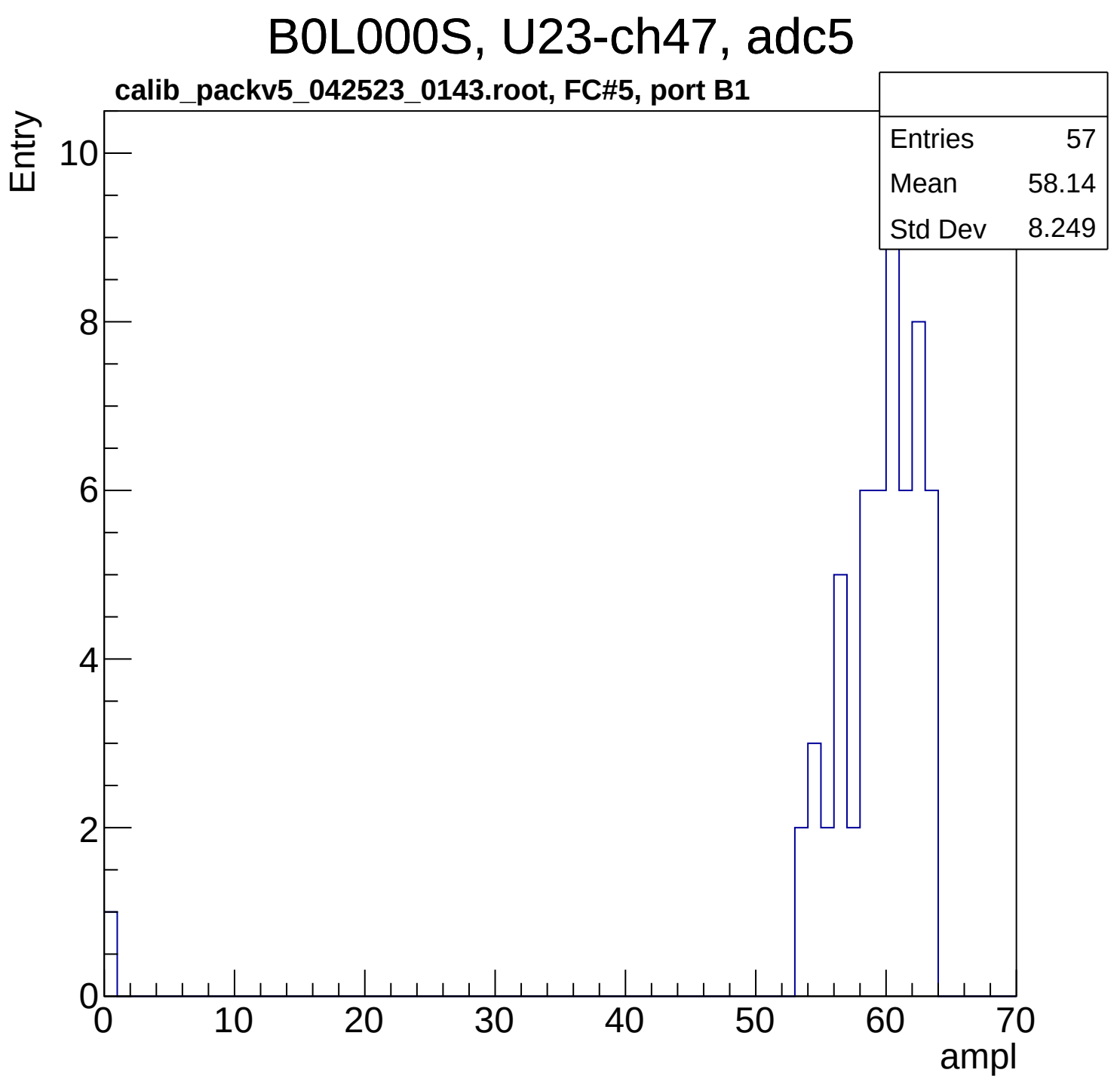

B0L000S, U23-ch47, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 30.43
Std Dev 2.446
Gaus mean : 30.8164
Gaus Width: 3.3669
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

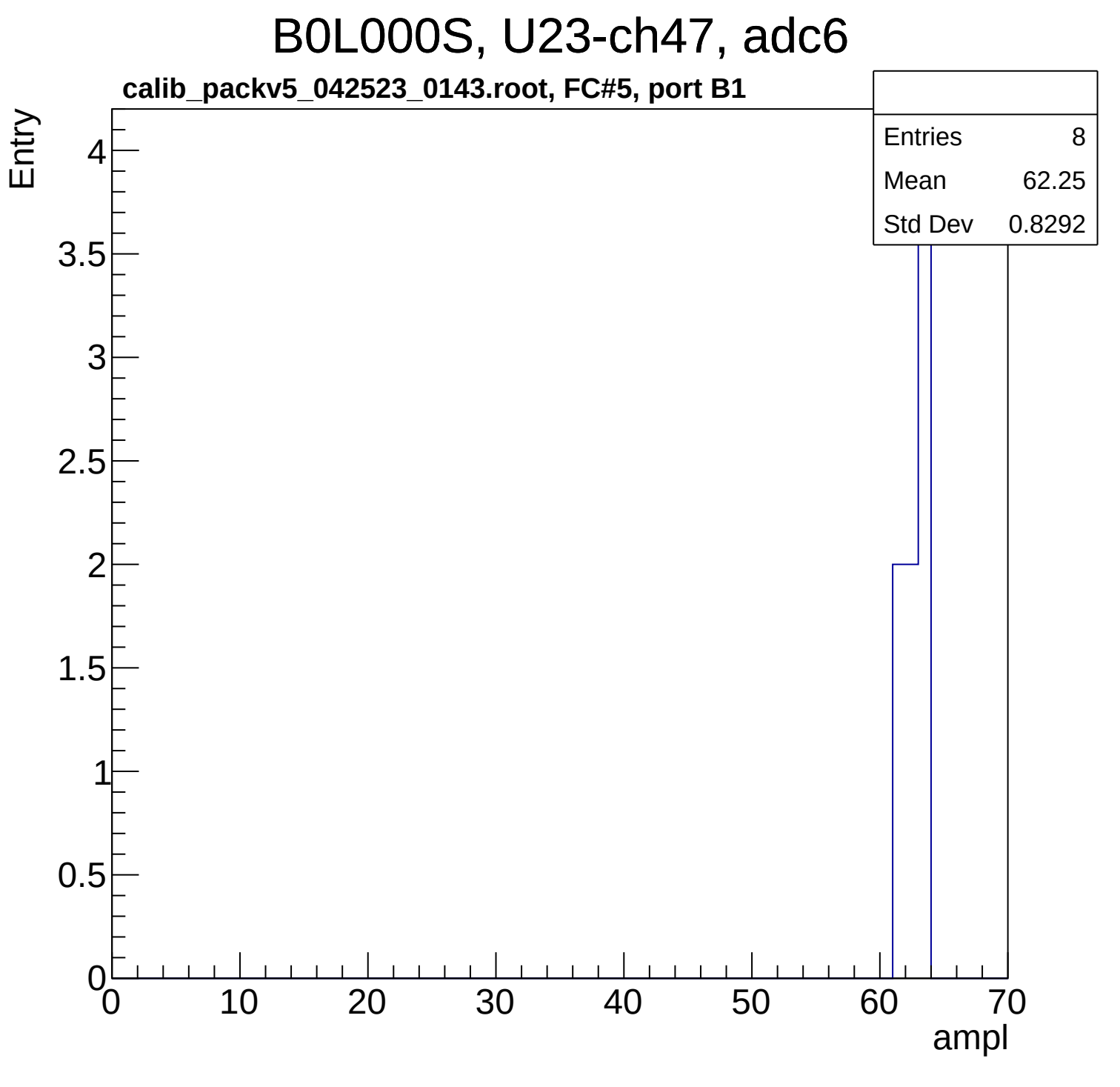

B0L000S, U23-ch47, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 37.15
Std Dev 3.404
Gaus mean : 37.8360
Gaus Width: 3.7461
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

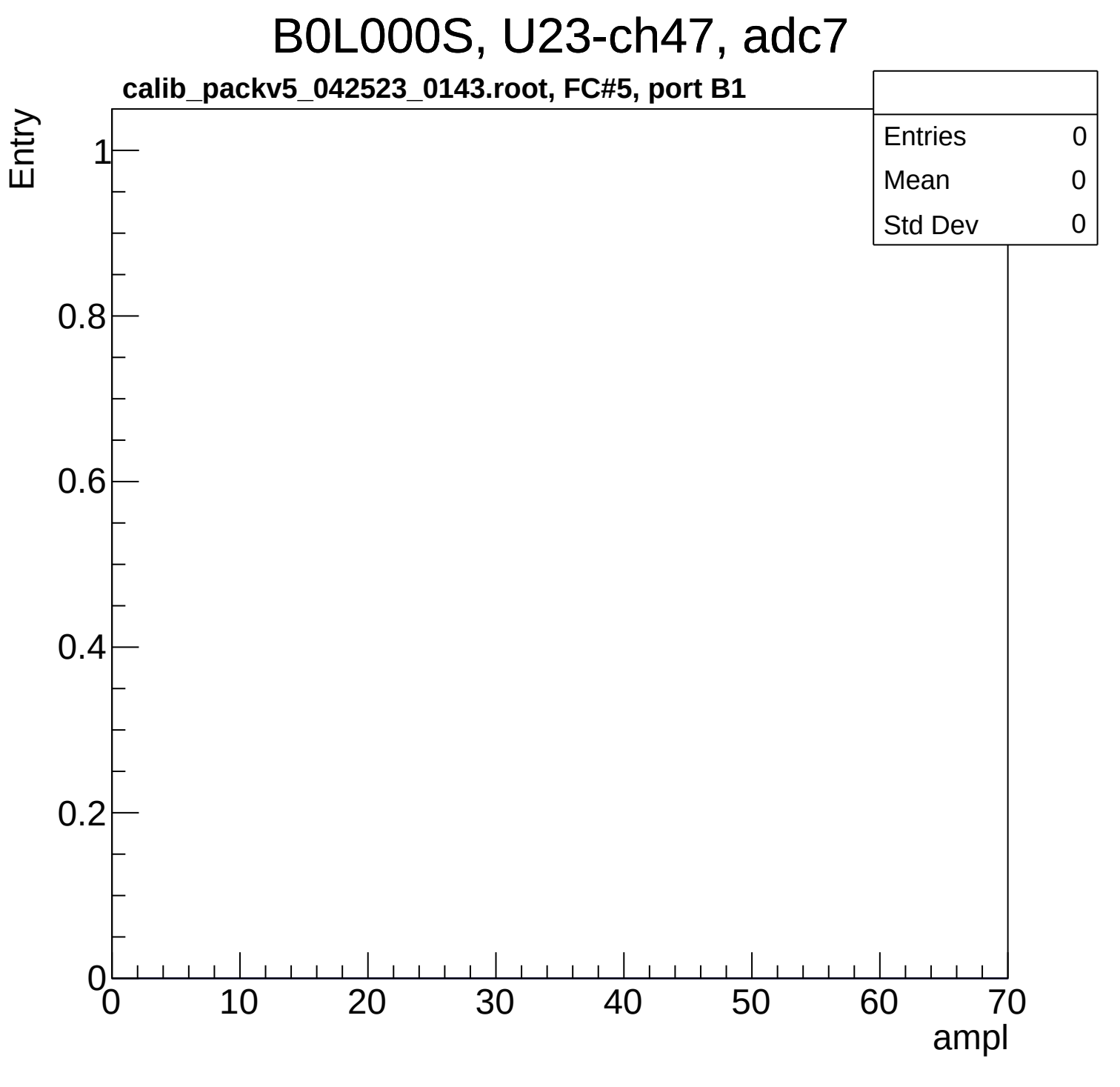

B0L000S, U23-ch47, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 43.62
Std Dev 2.618
Gaus mean : 44.8312
Gaus Width: 3.2087
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

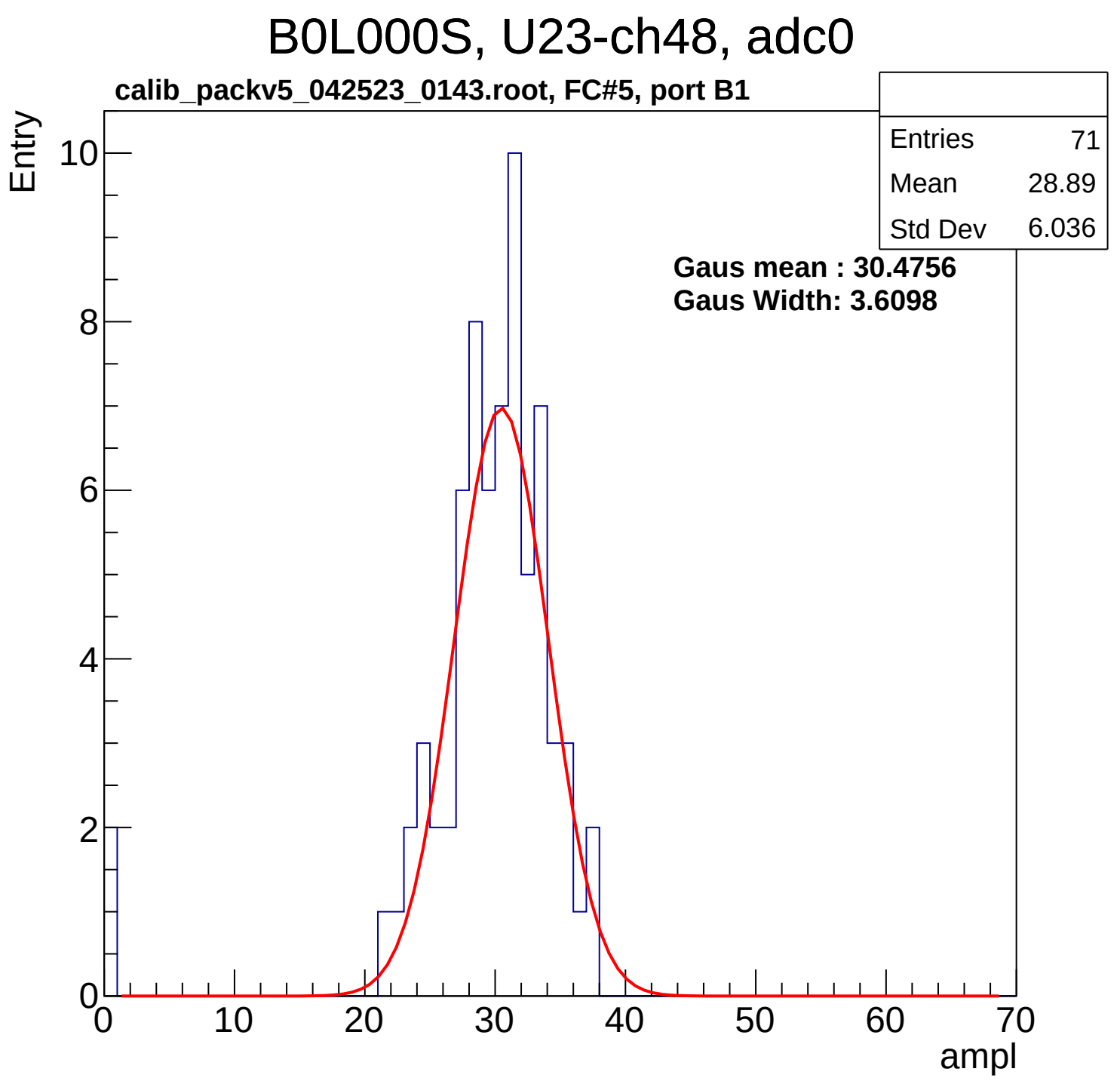

B0L000S, U23-ch47, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 49.06
Std Dev 3.827
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

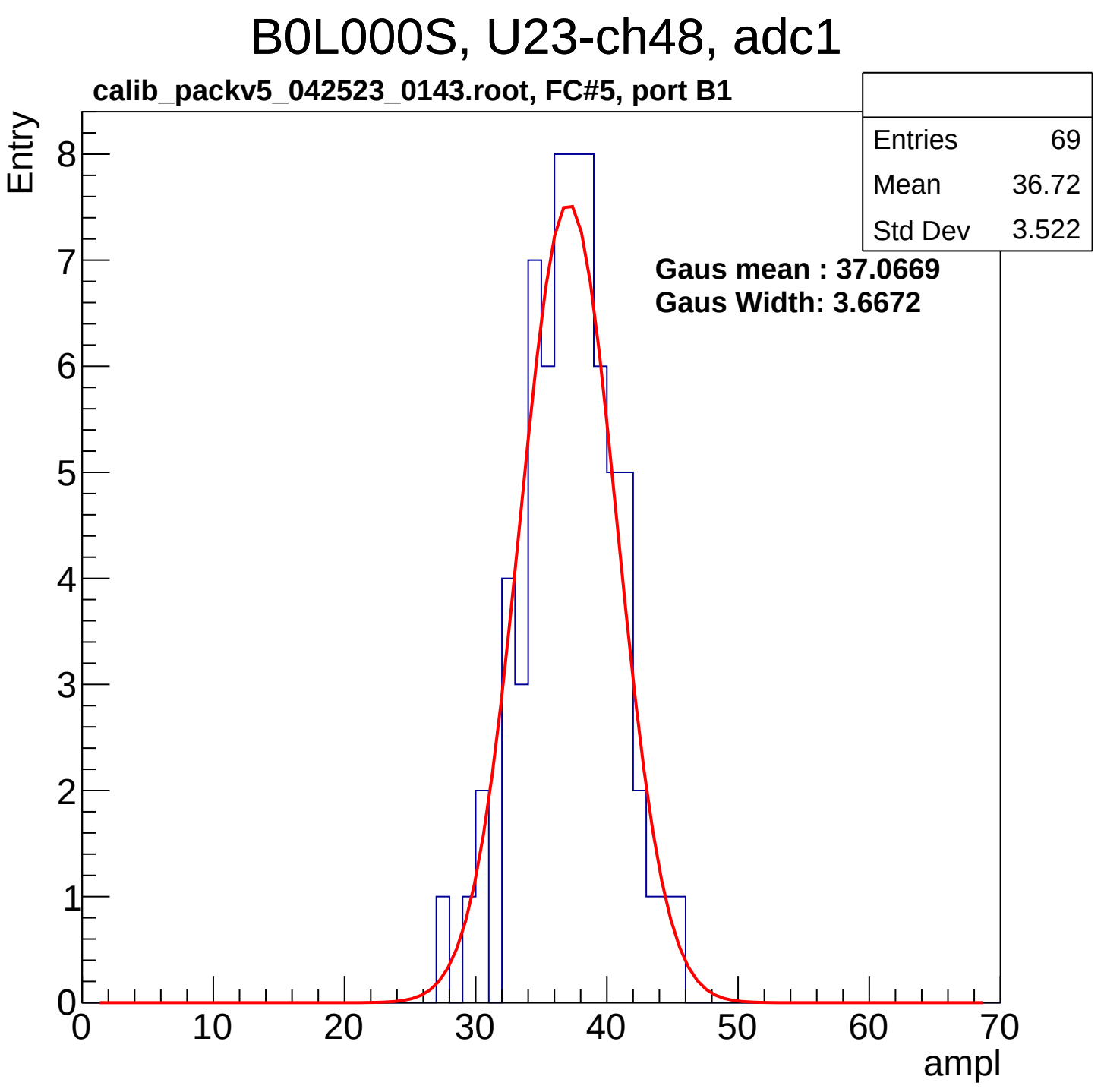

B0L000S, U23-ch47, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 55.17
Std Dev 3.003
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

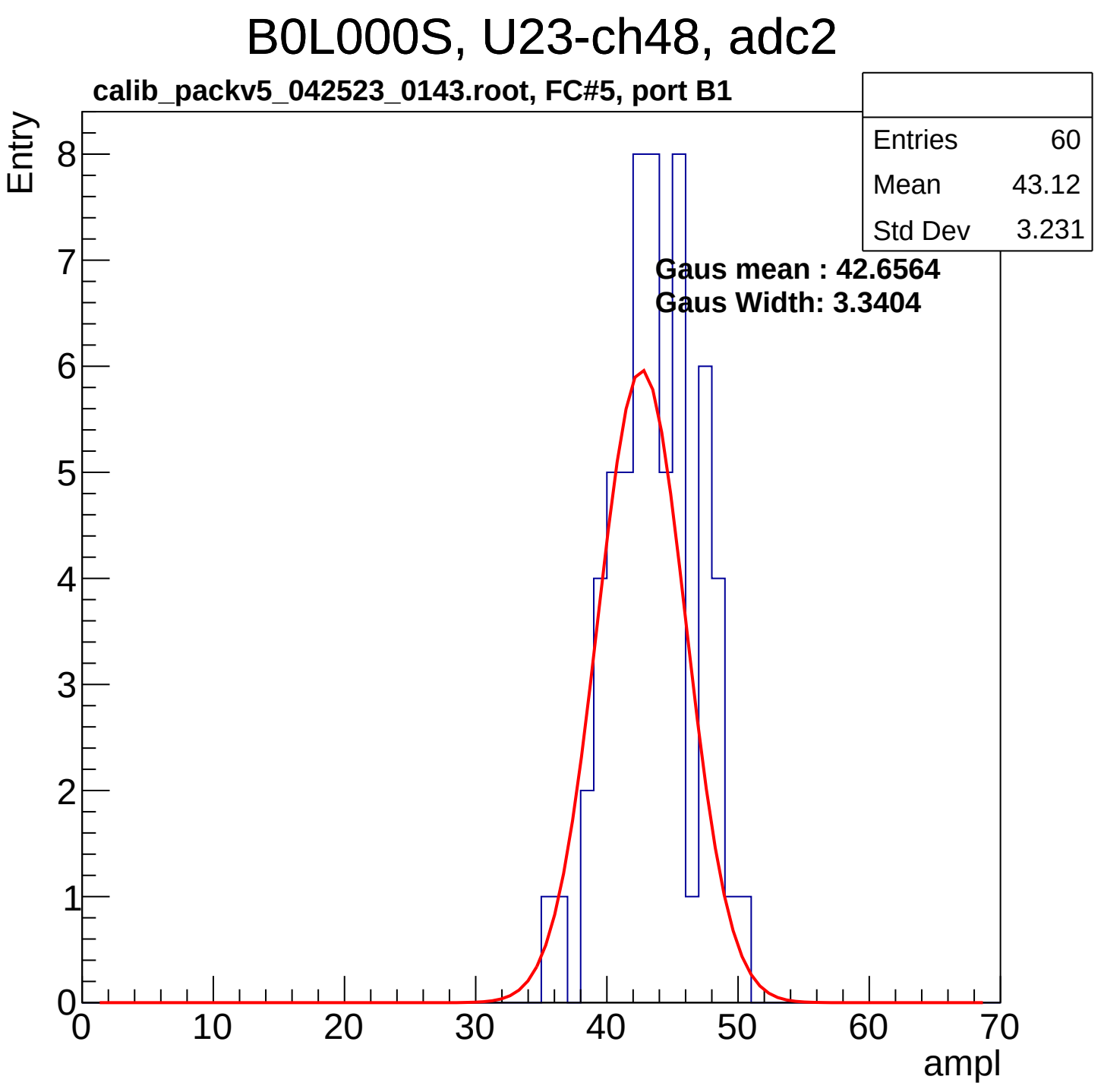

B0L000S, U23-ch47, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.14
Std Dev 8.249
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

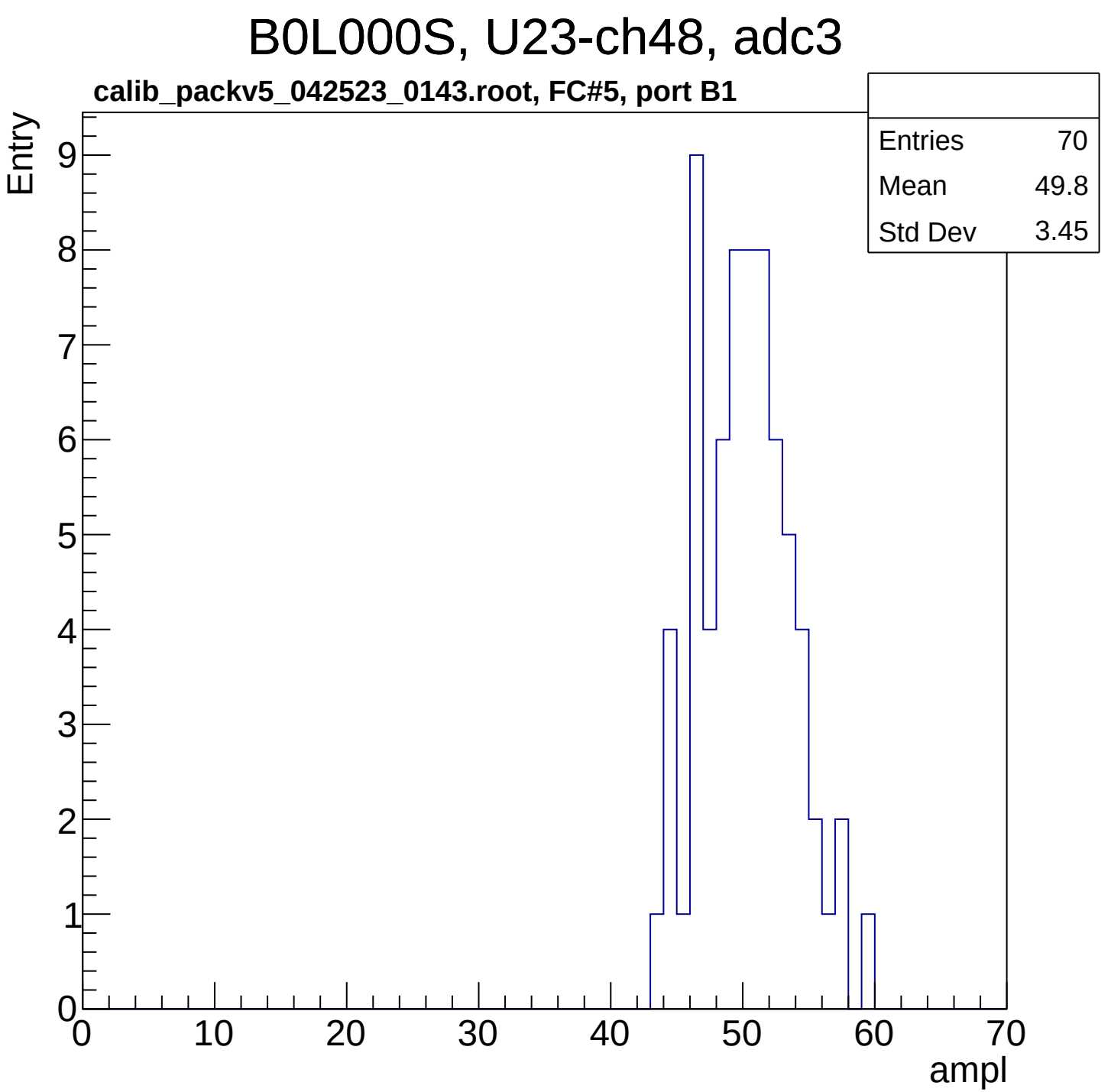

B0L000S, U23-ch47, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

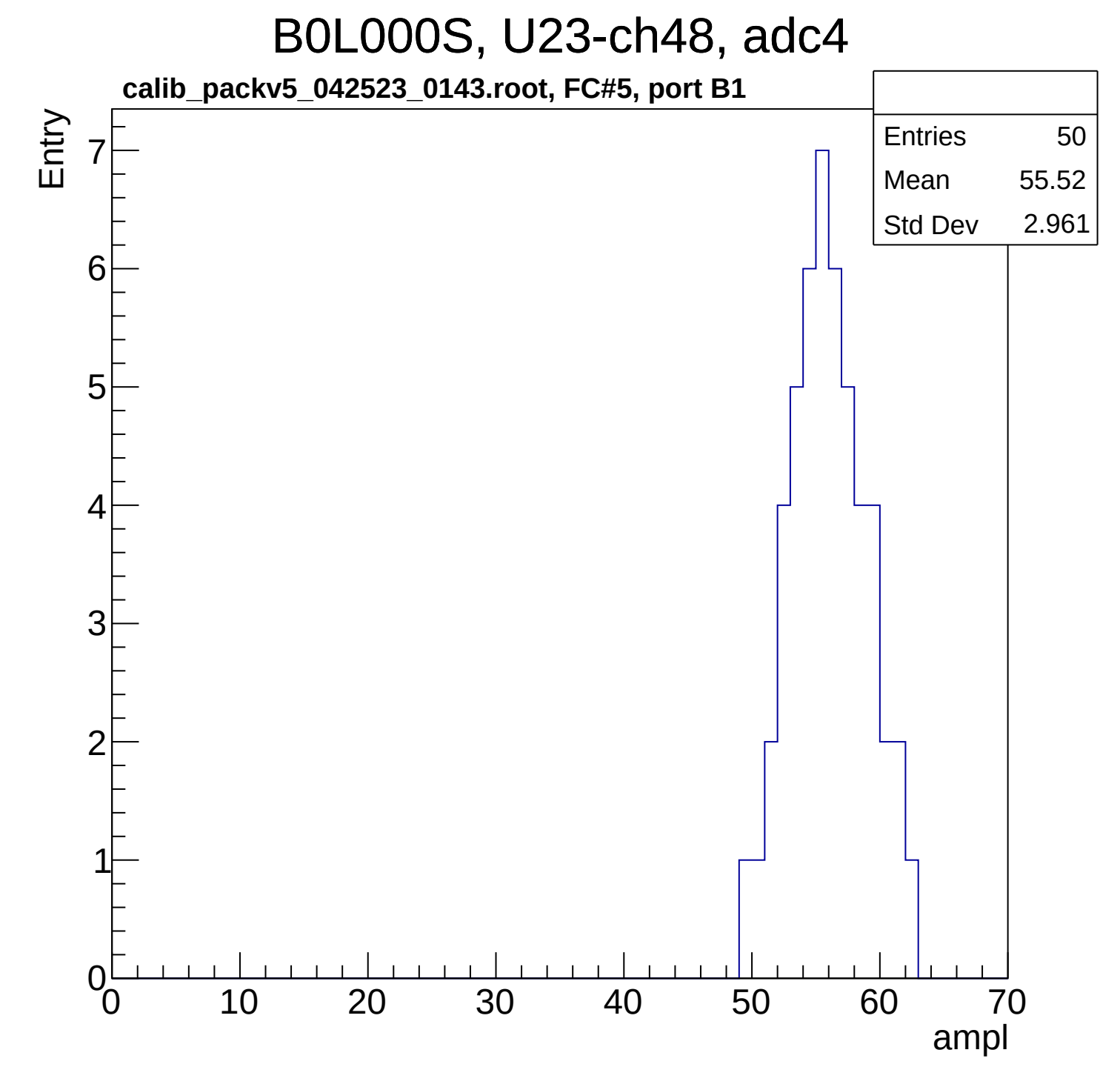

B0L000S, U23-ch47, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

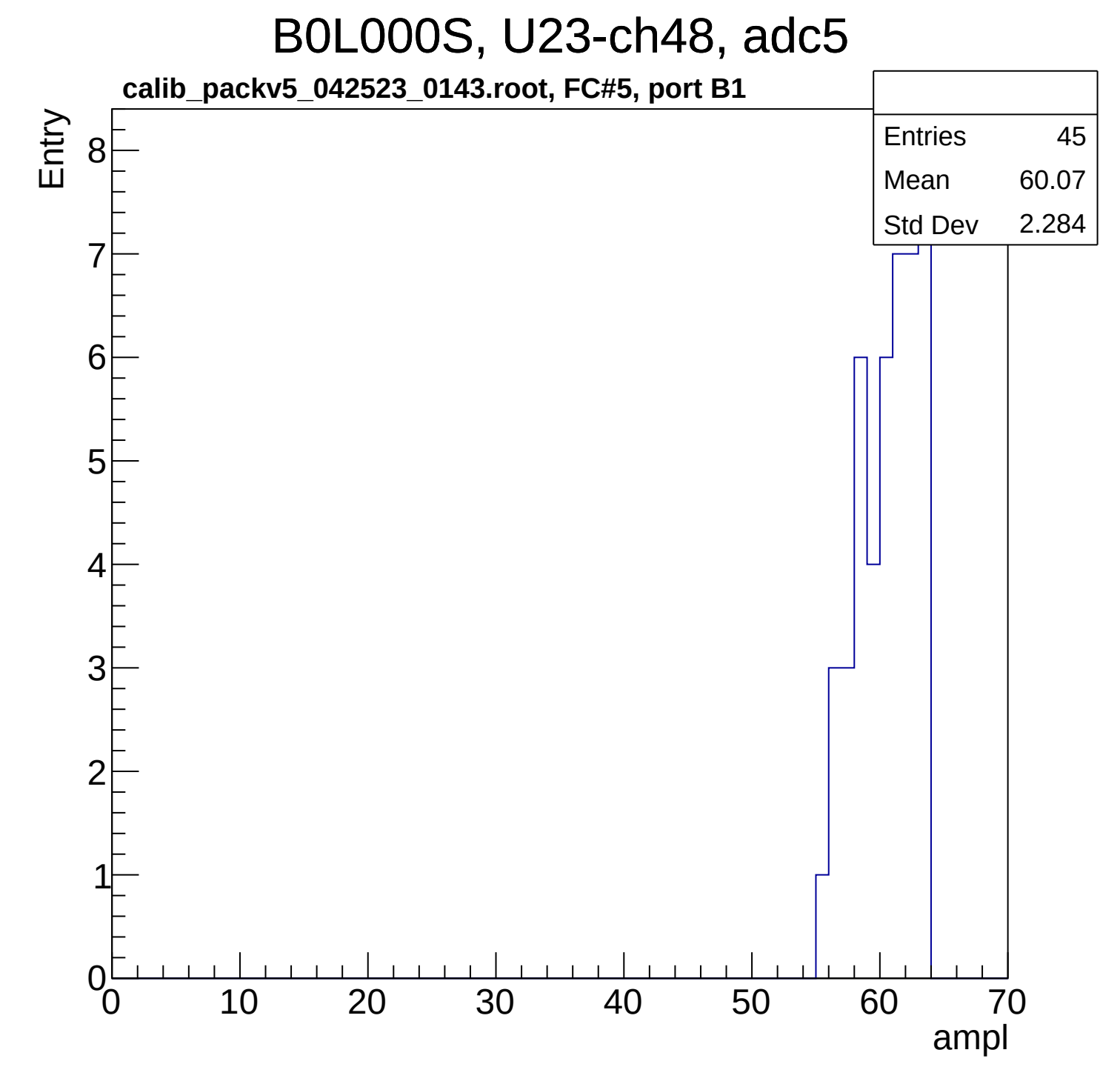

B0L000S, U23-ch48, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 28.89
Std Dev 6.036
Gaus mean : 30.4756
Gaus Width: 3.6098
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

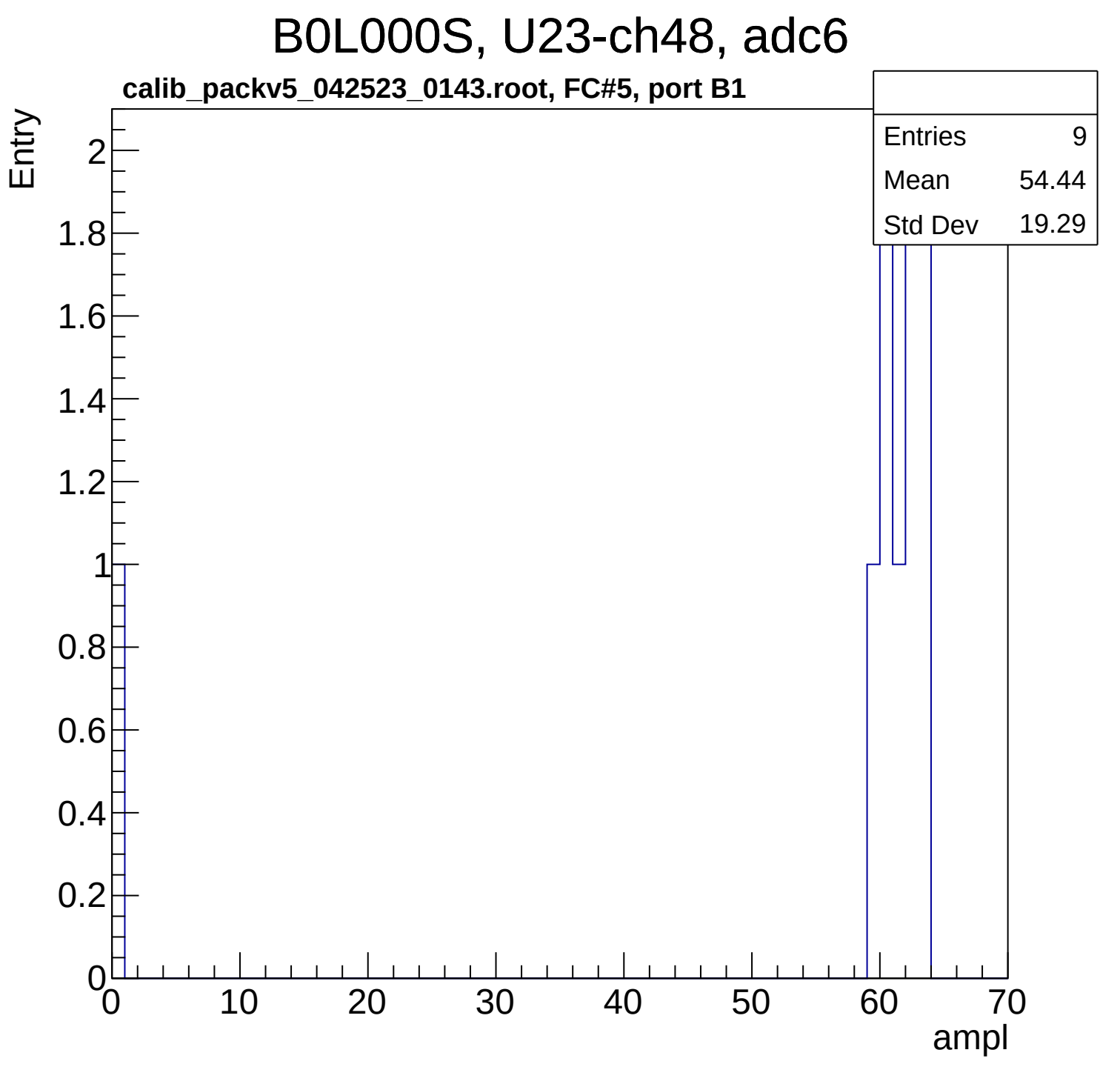

B0L000S, U23-ch48, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.72
Std Dev 3.522
Gaus mean : 37.0669
Gaus Width: 3.6672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

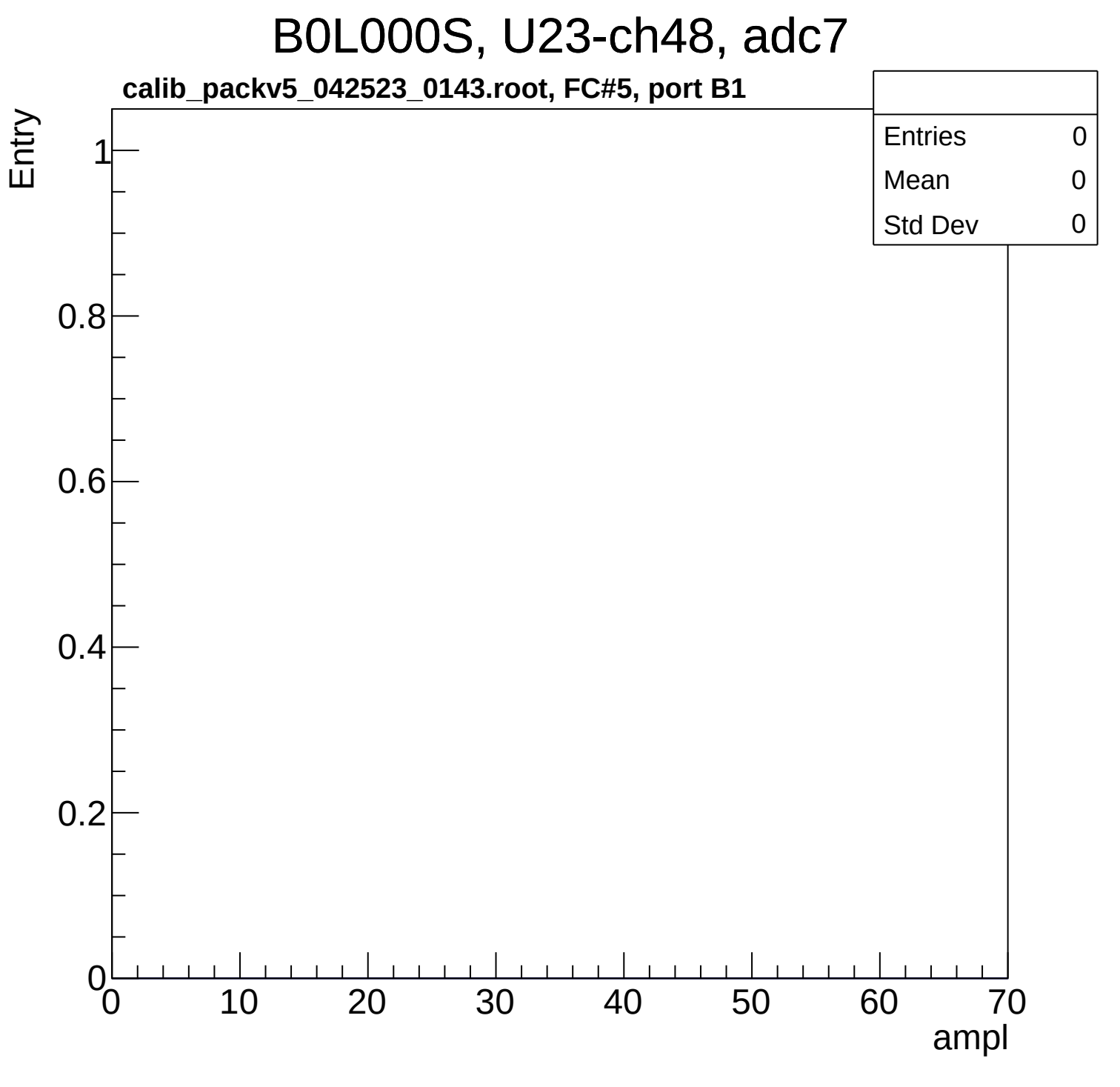

B0L000S, U23-ch48, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.12
Std Dev 3.231
Gaus mean : 42.6564
Gaus Width: 3.3404
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

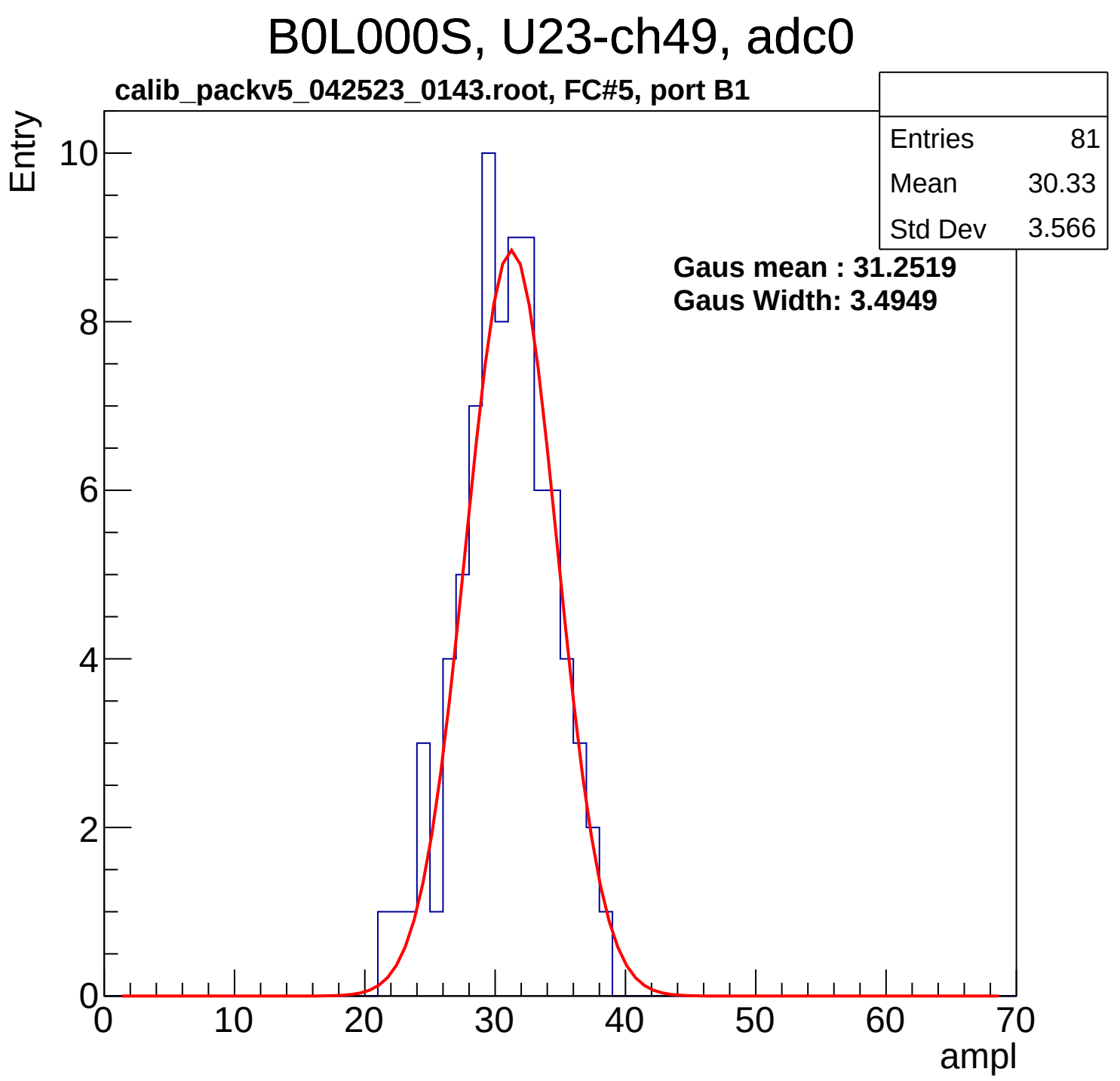

B0L000S, U23-ch48, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.8
Std Dev 3.45
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

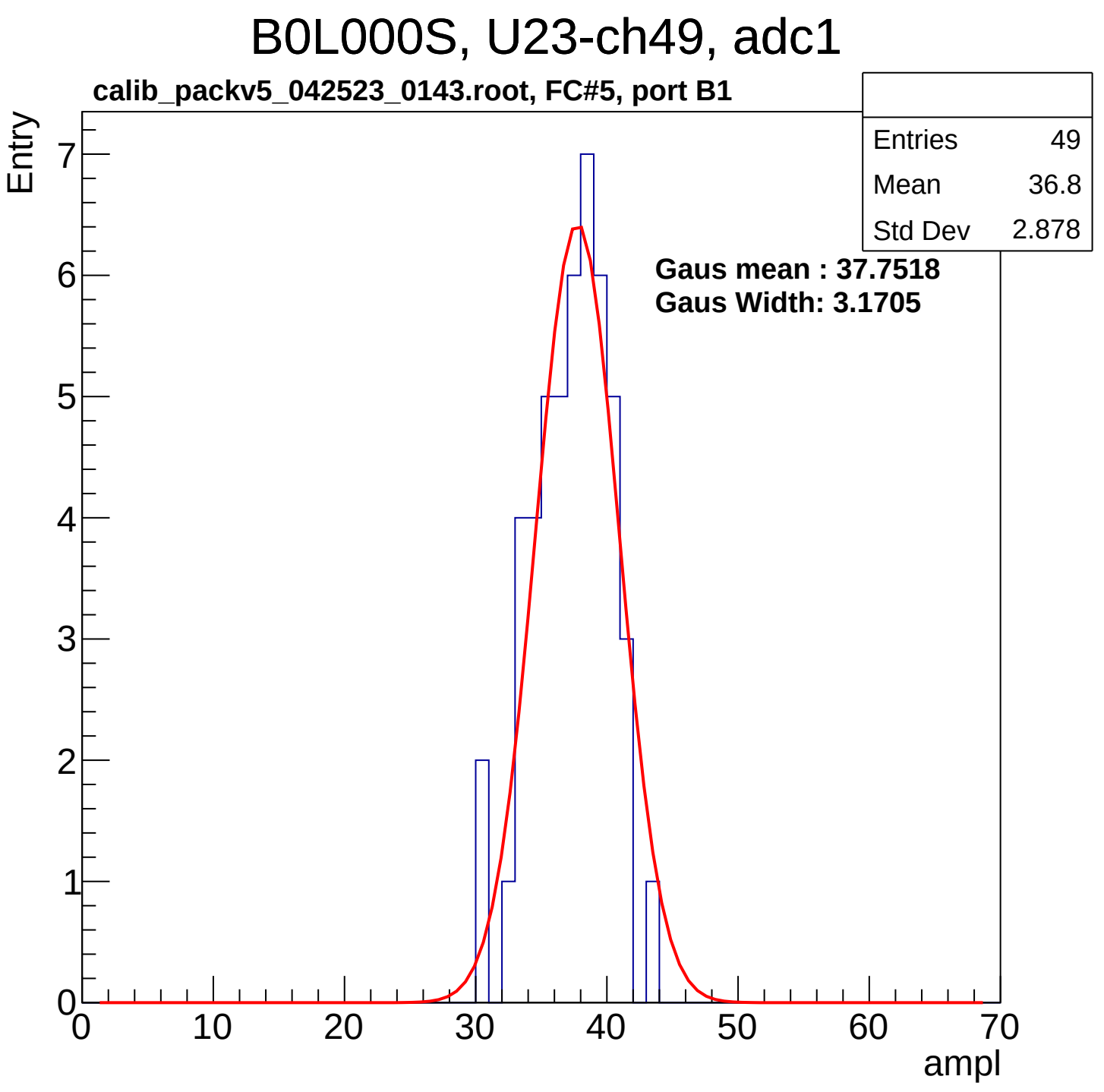

B0L000S, U23-ch48, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.52
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

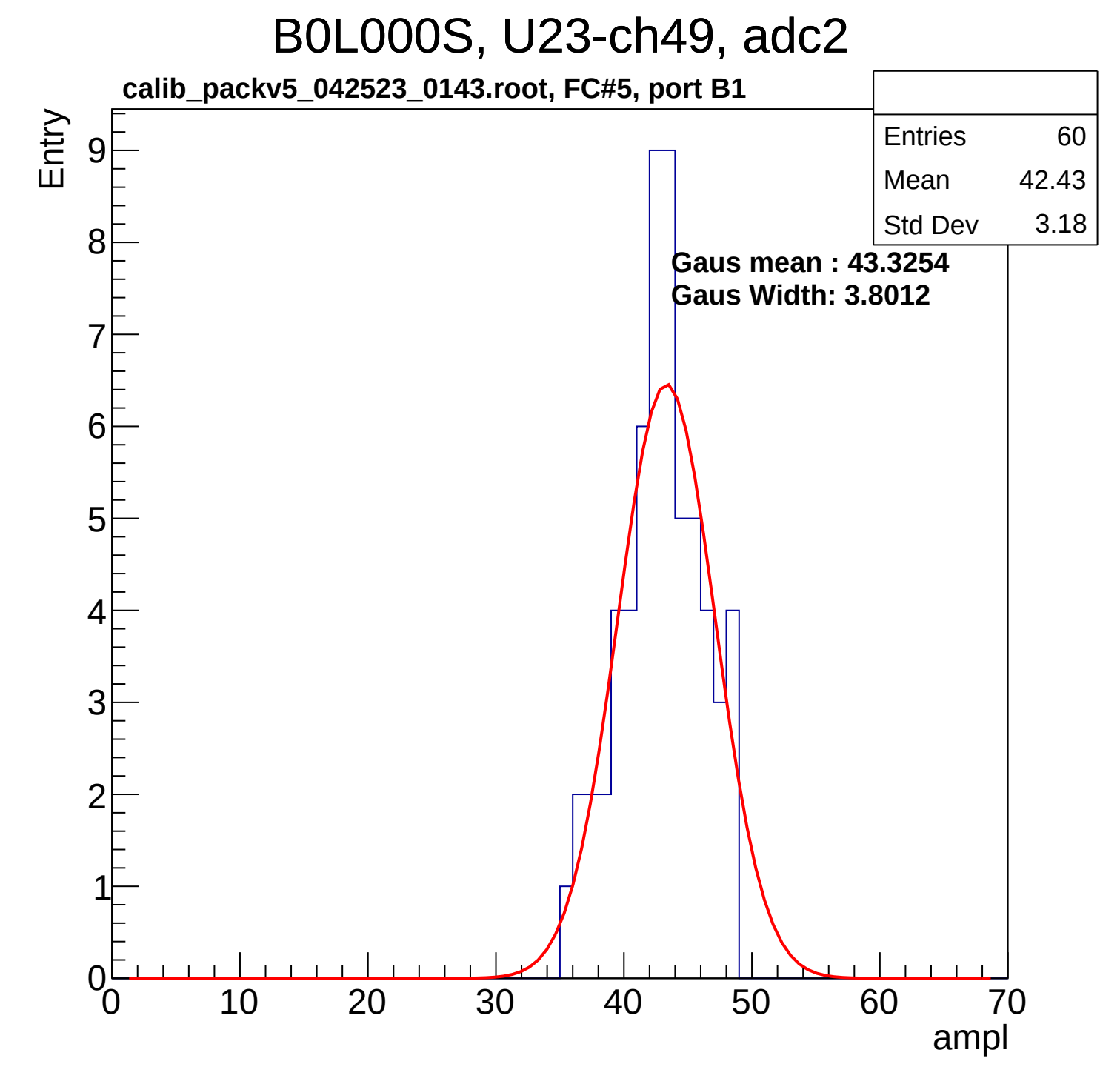

B0L000S, U23-ch48, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.07
Std Dev 2.284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

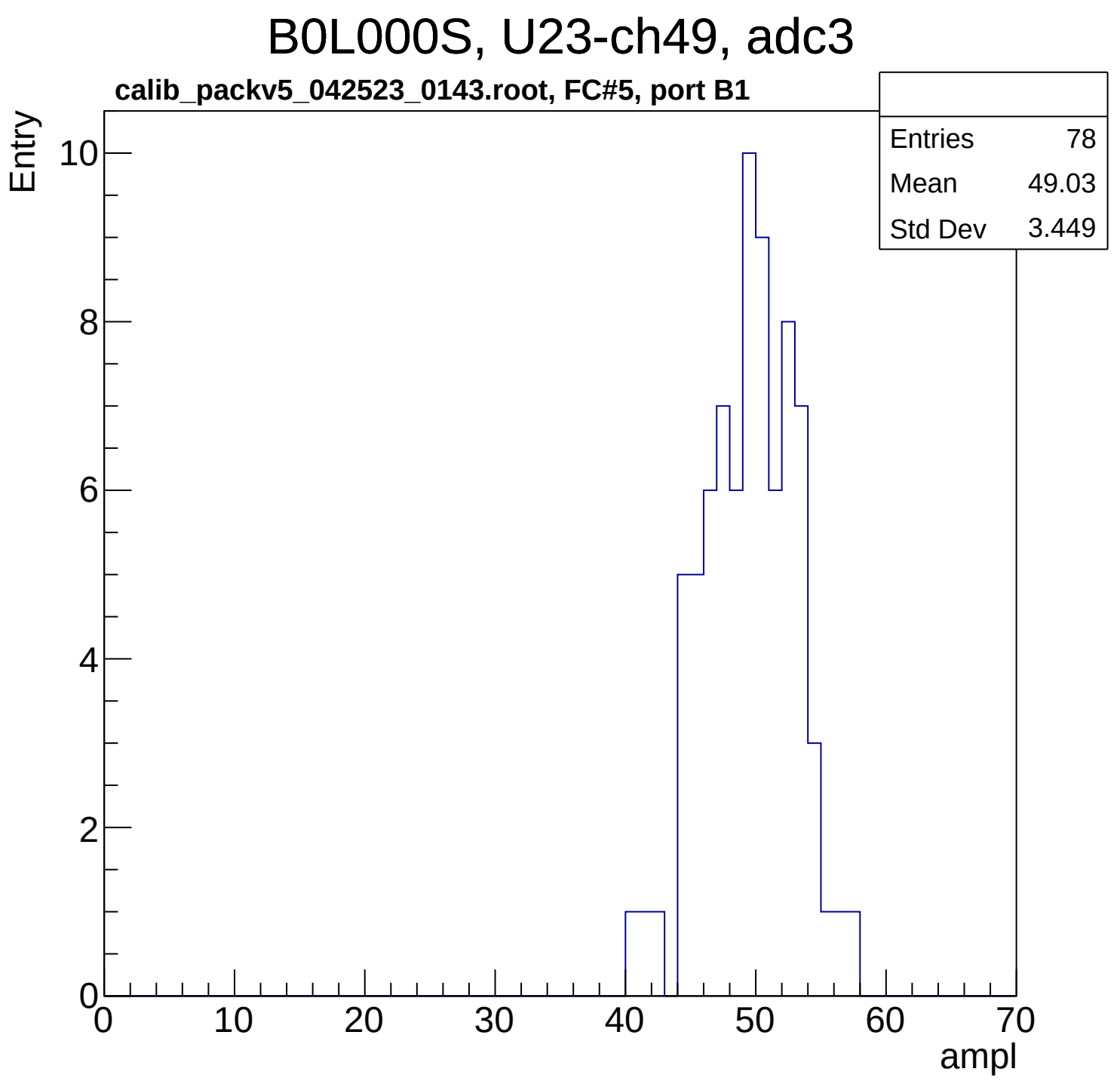

B0L000S, U23-ch48, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 54.44
Std Dev 19.29
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

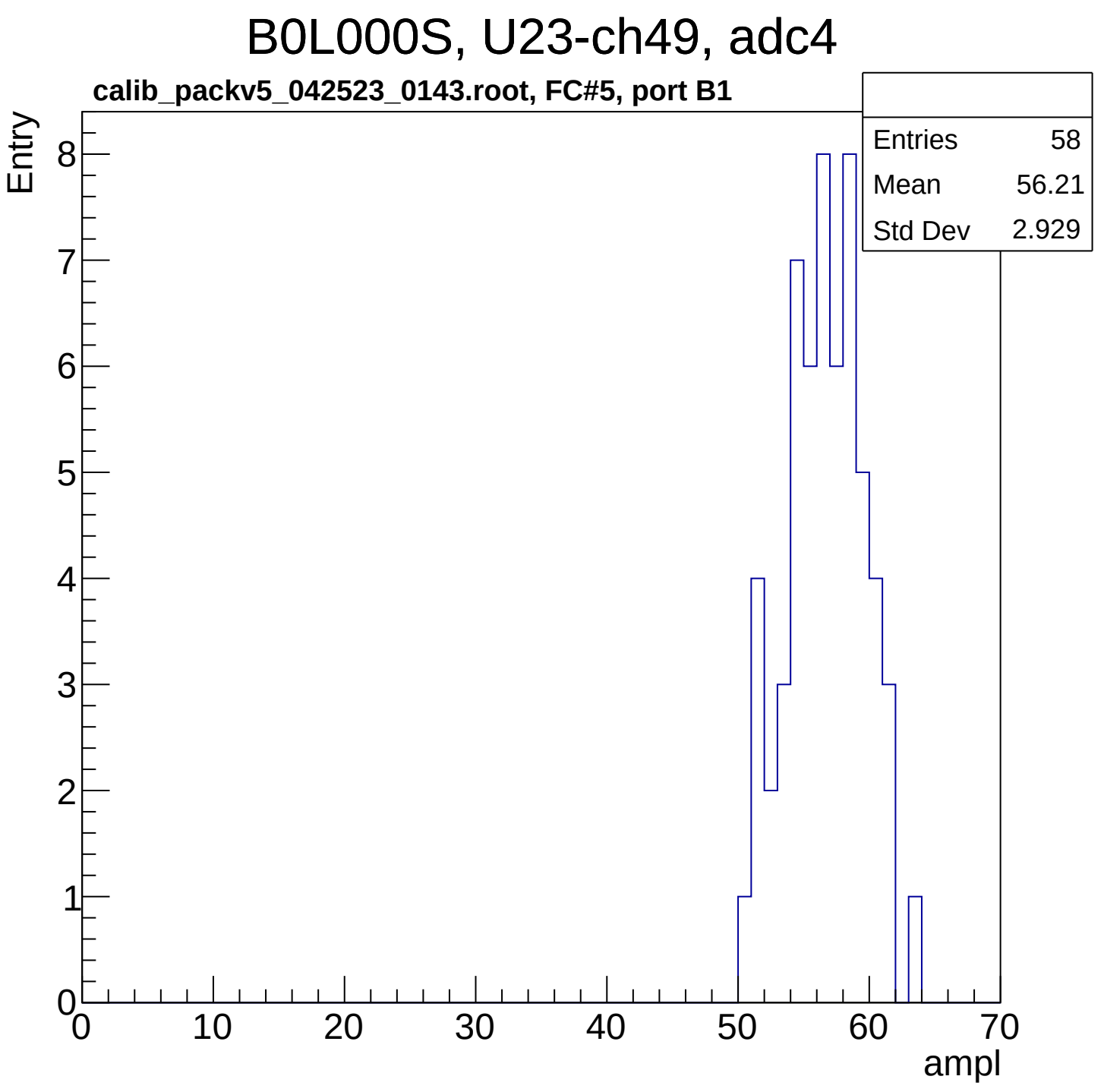

B0L000S, U23-ch48, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

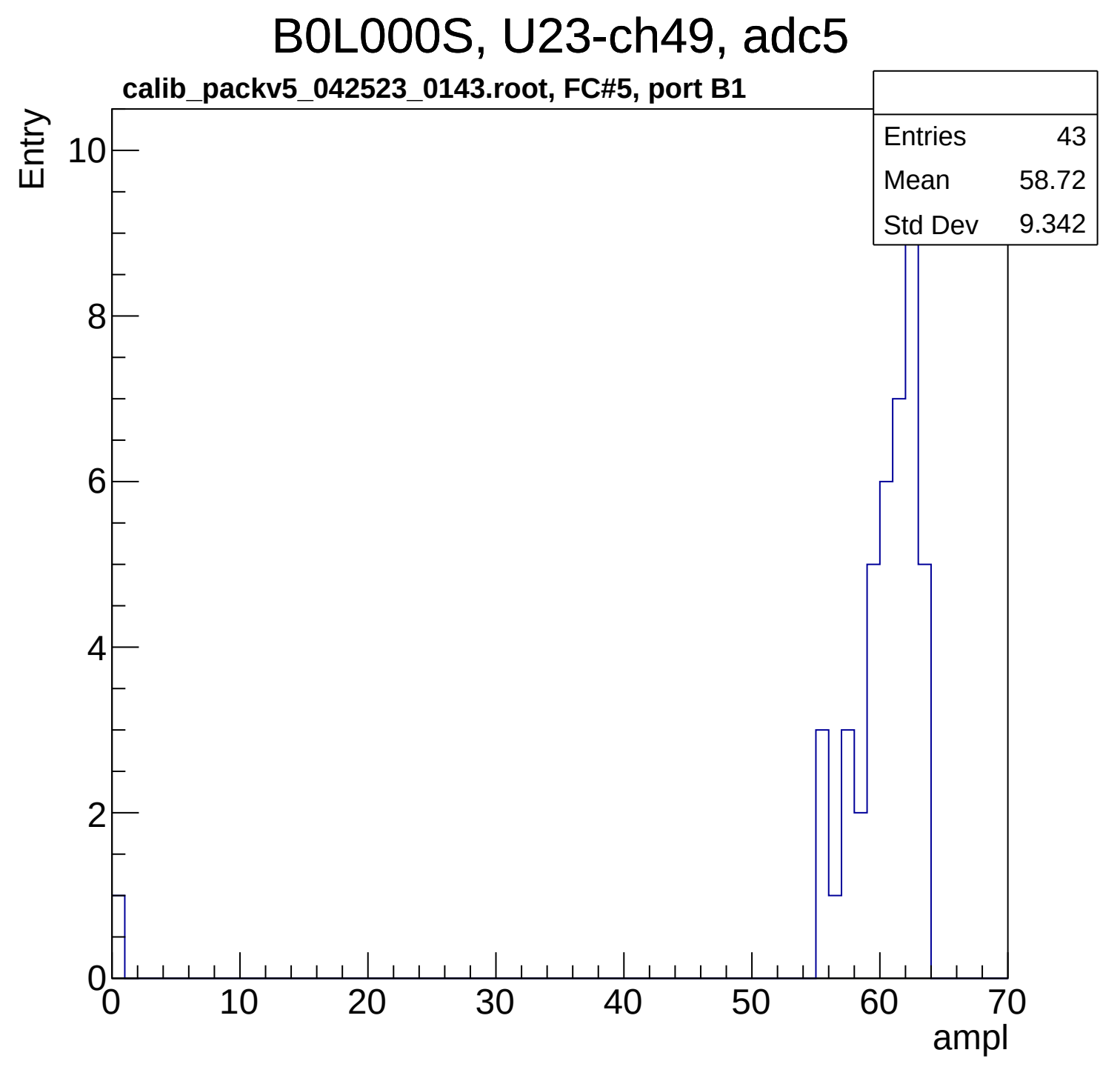

B0L000S, U23-ch49, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 30.33
Std Dev 3.566
Gaus mean : 31.2519
Gaus Width: 3.4949
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

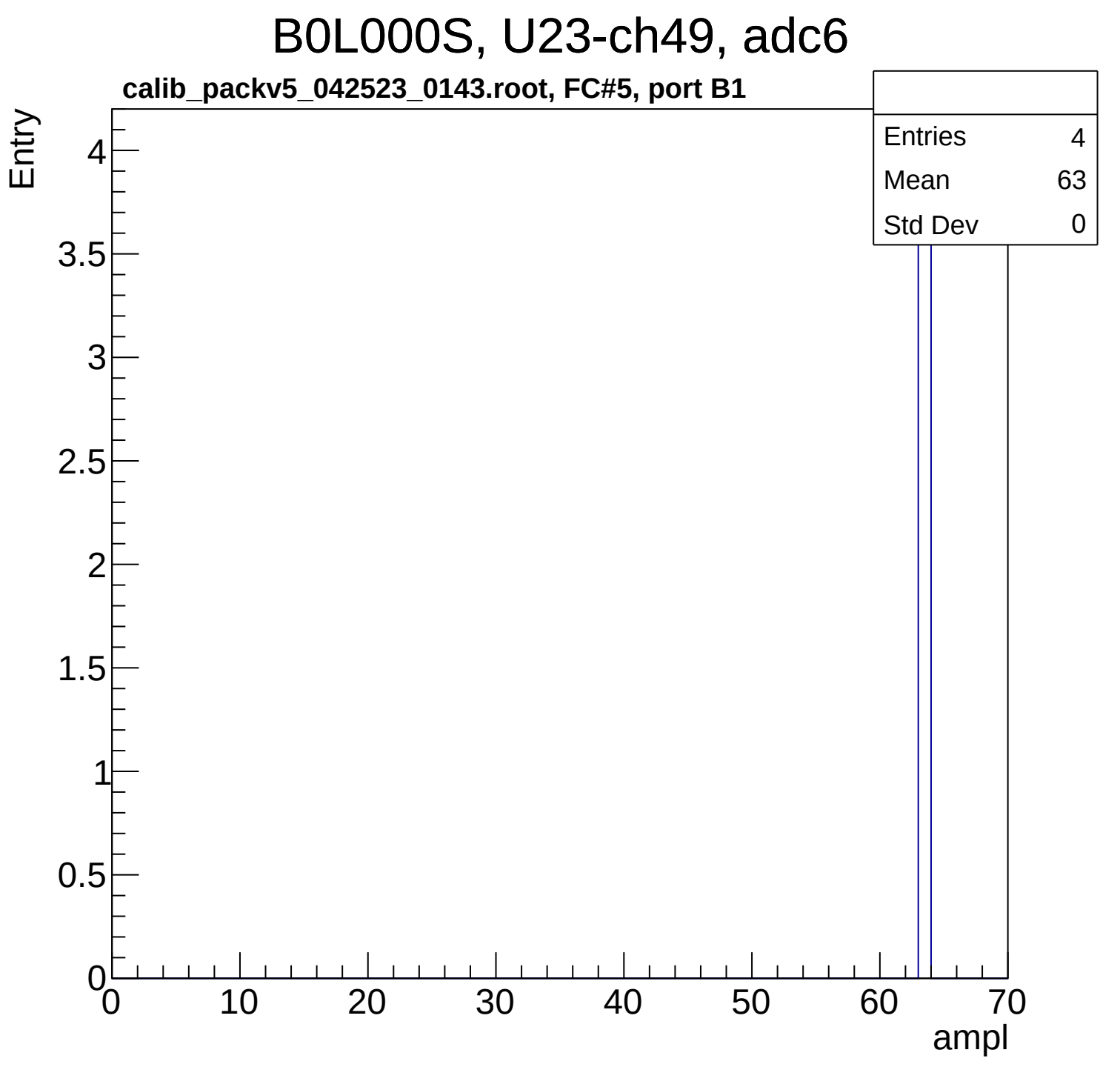

B0L000S, U23-ch49, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 36.8
Std Dev 2.878
Gaus mean : 37.7518
Gaus Width: 3.1705
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

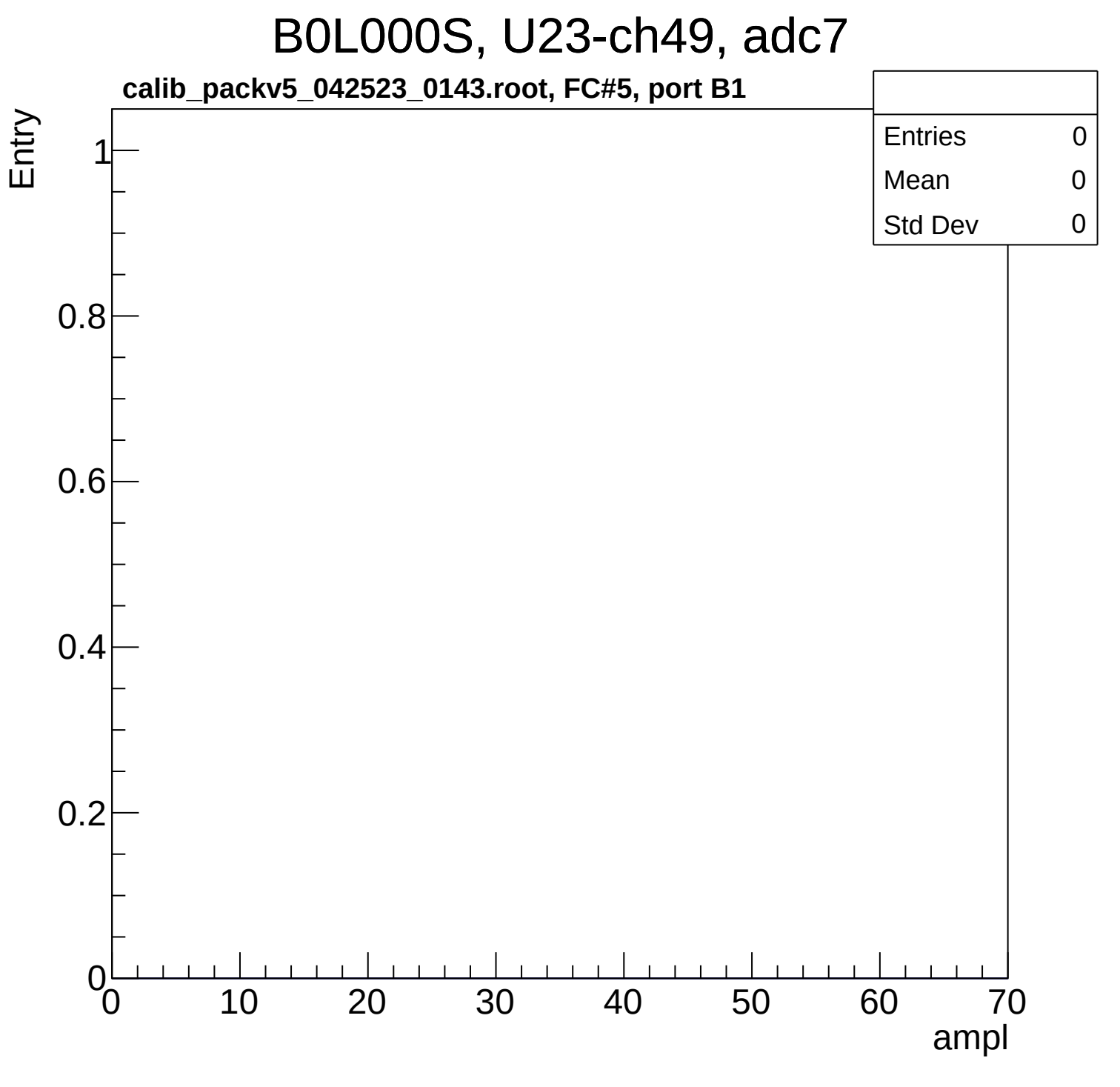

B0L000S, U23-ch49, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 42.43
Std Dev 3.18
Gaus mean : 43.3254
Gaus Width: 3.8012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

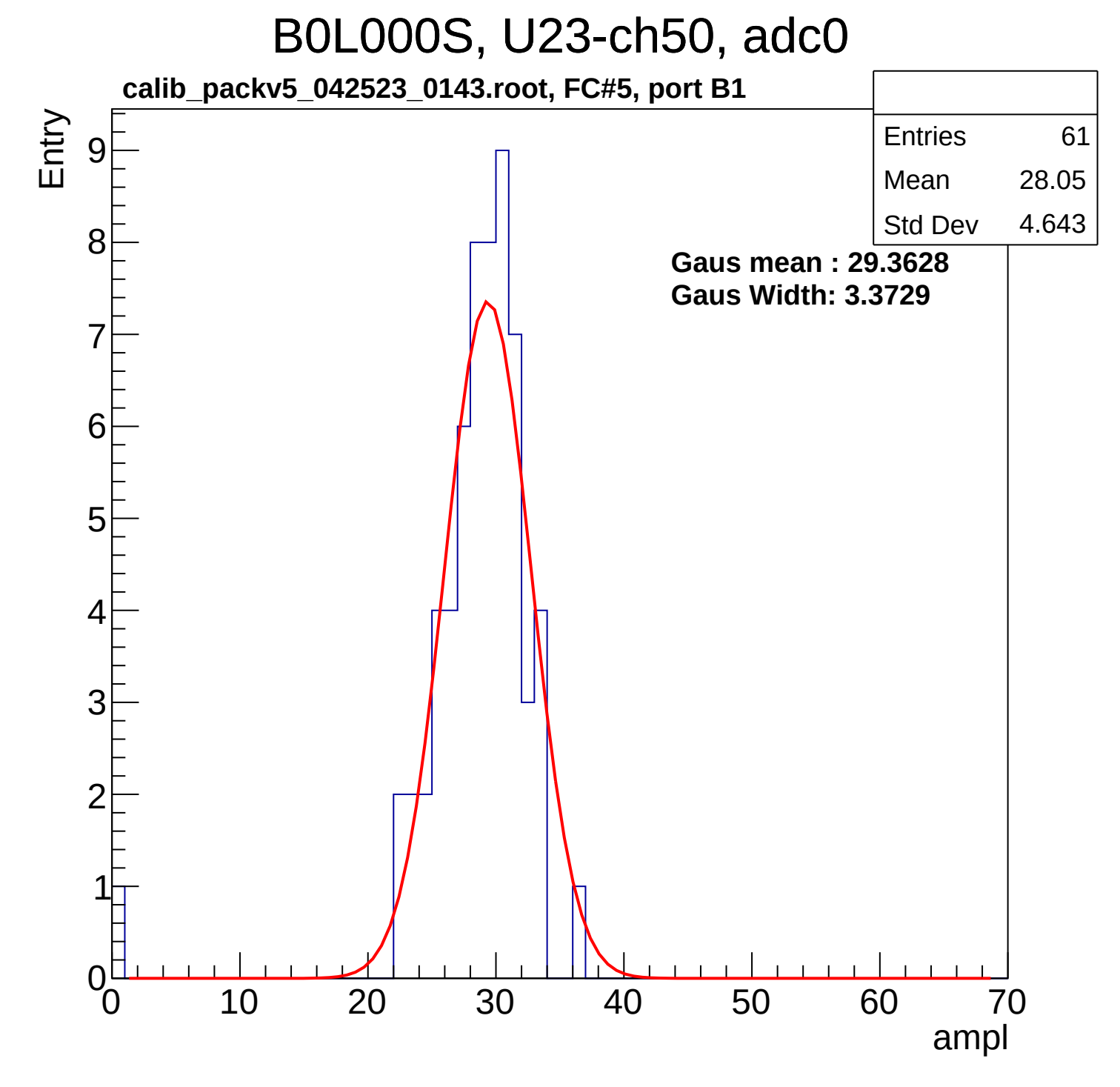

B0L000S, U23-ch49, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 49.03
Std Dev 3.449
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

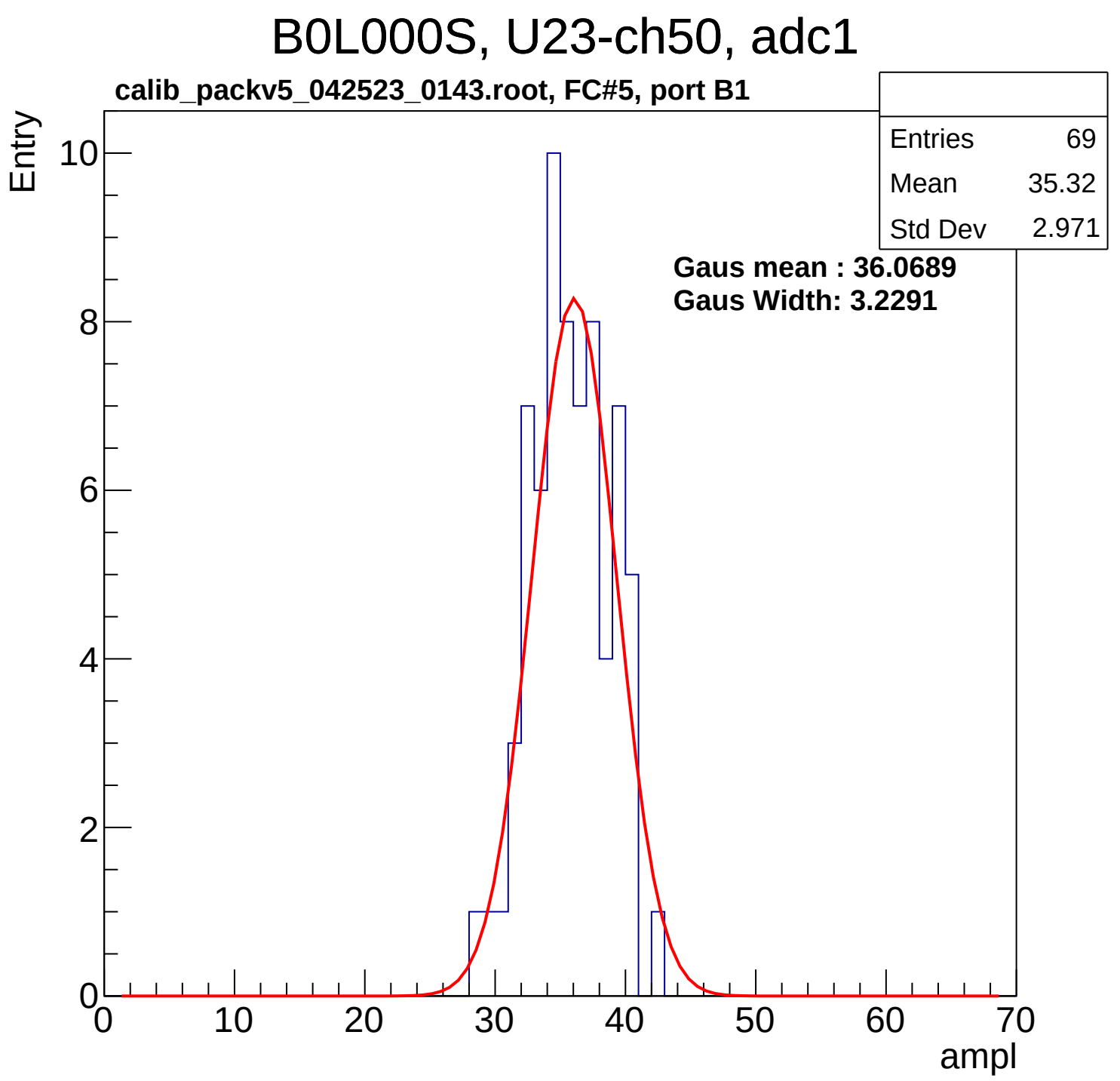

B0L000S, U23-ch49, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.21
Std Dev 2.929
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

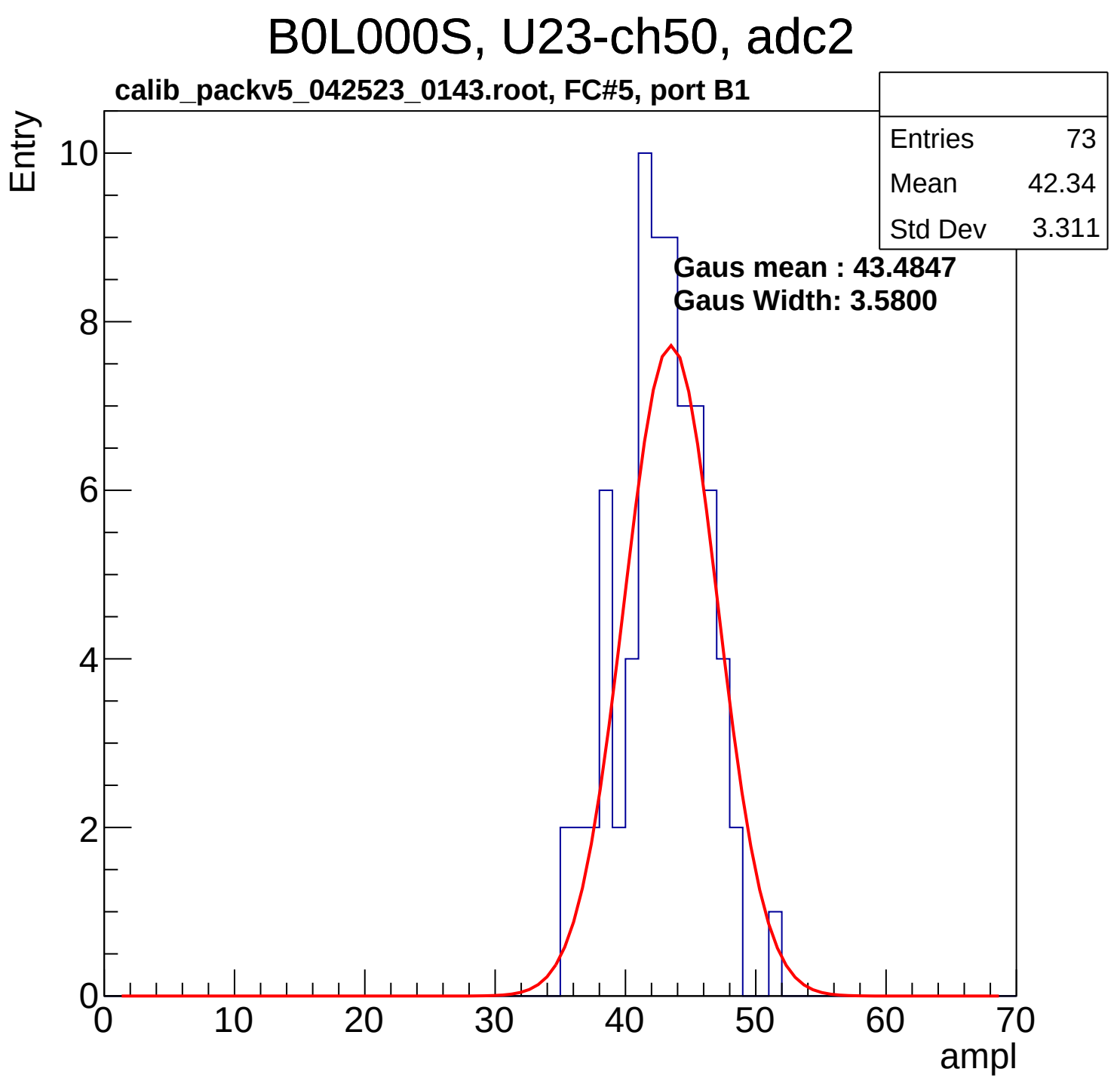

B0L000S, U23-ch49, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.72
Std Dev 9.342
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

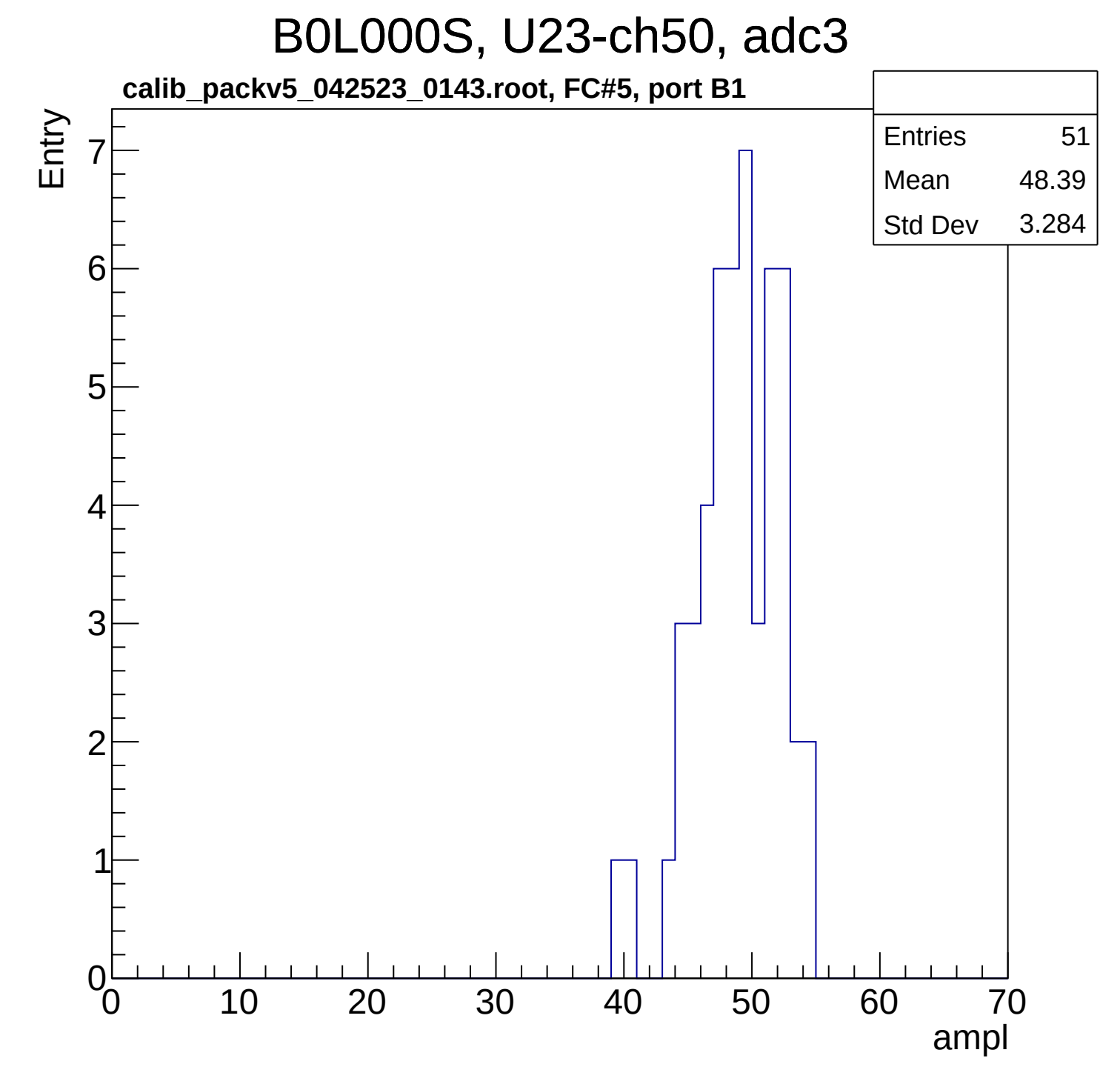

B0L000S, U23-ch49, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

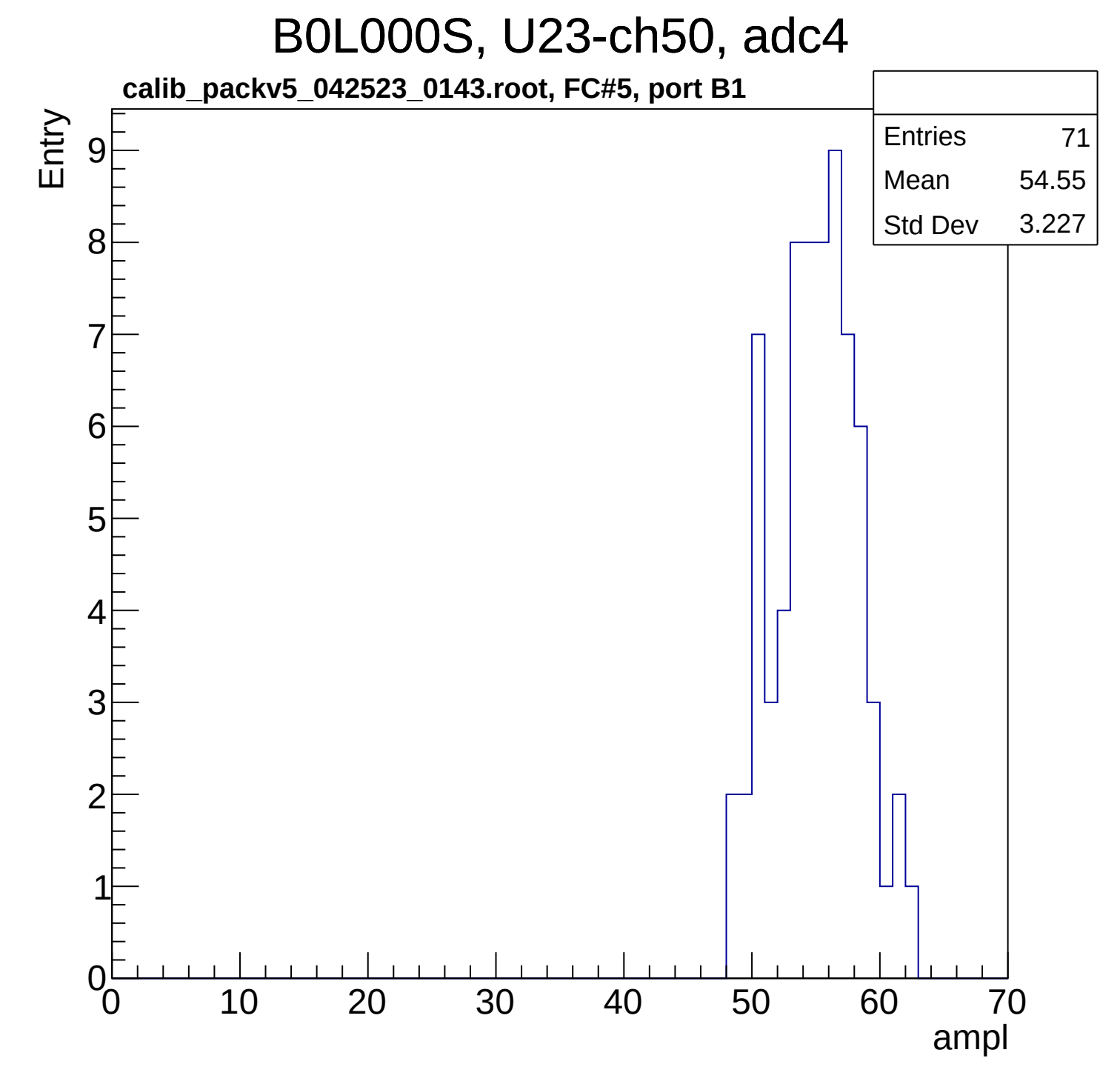

B0L000S, U23-ch49, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

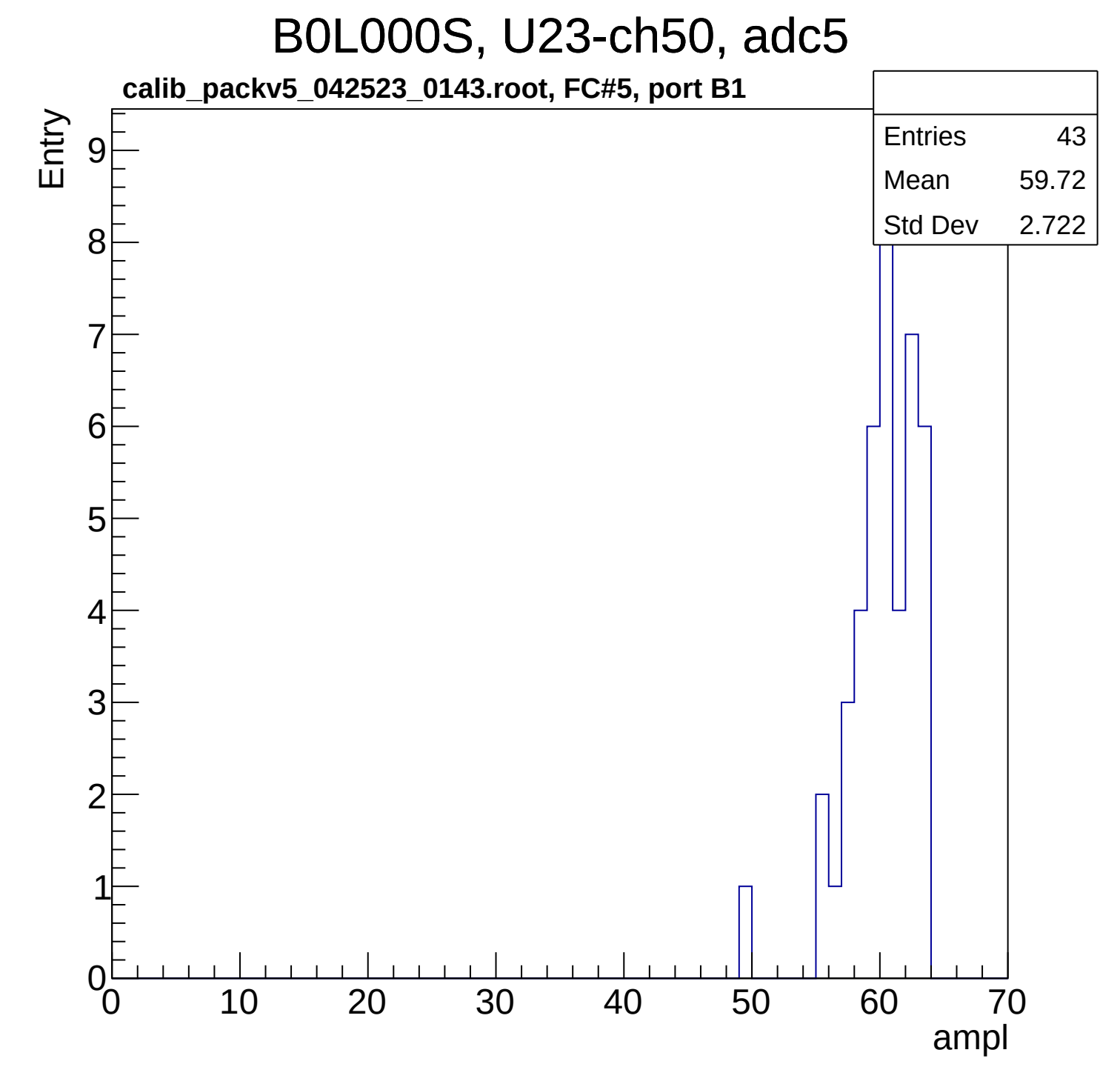

B0L000S, U23-ch50, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.05
Std Dev 4.643
Gaus mean : 29.3628
Gaus Width: 3.3729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

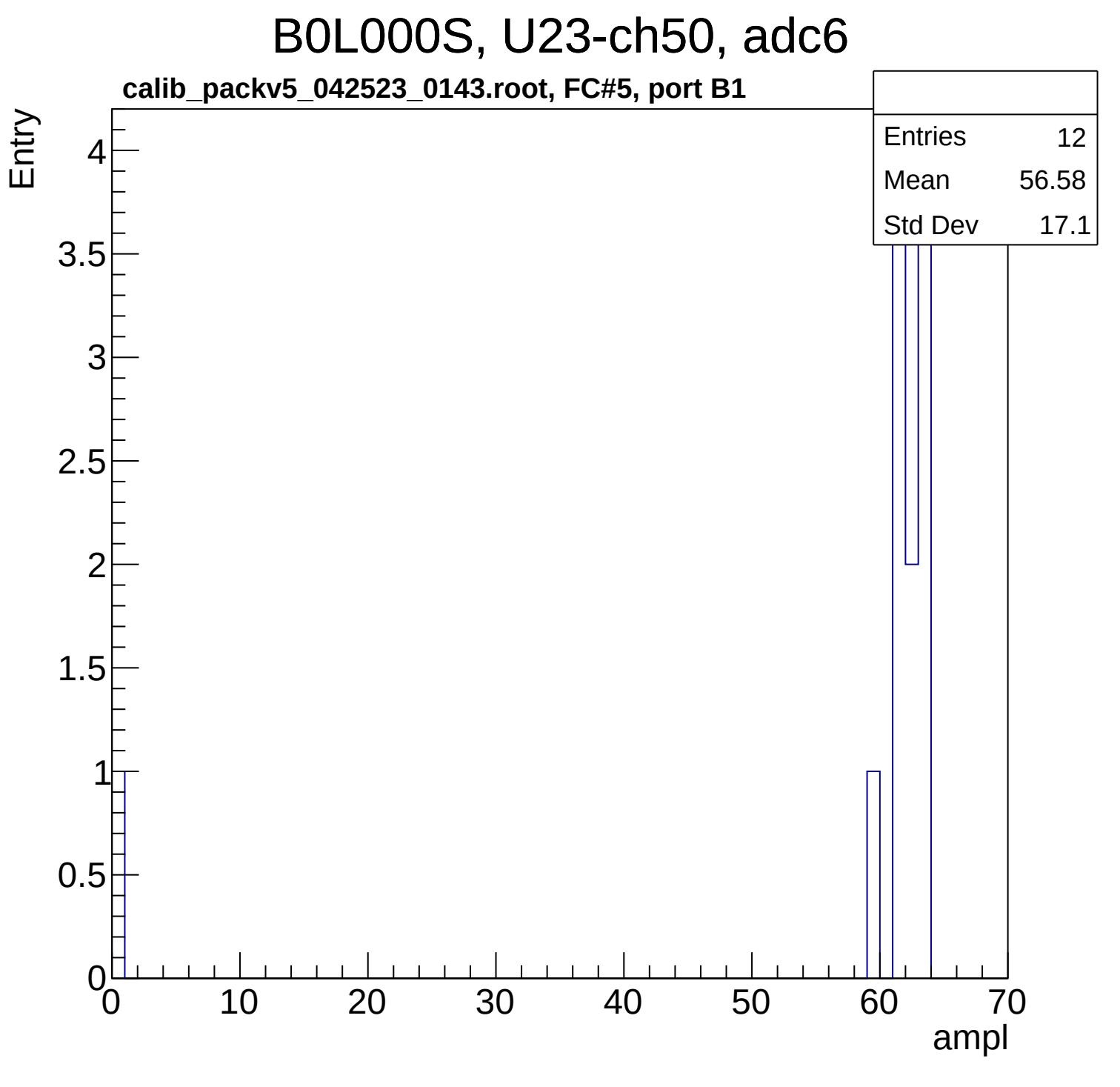

B0L000S, U23-ch50, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.32
Std Dev 2.971
Gaus mean : 36.0689
Gaus Width: 3.2291
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

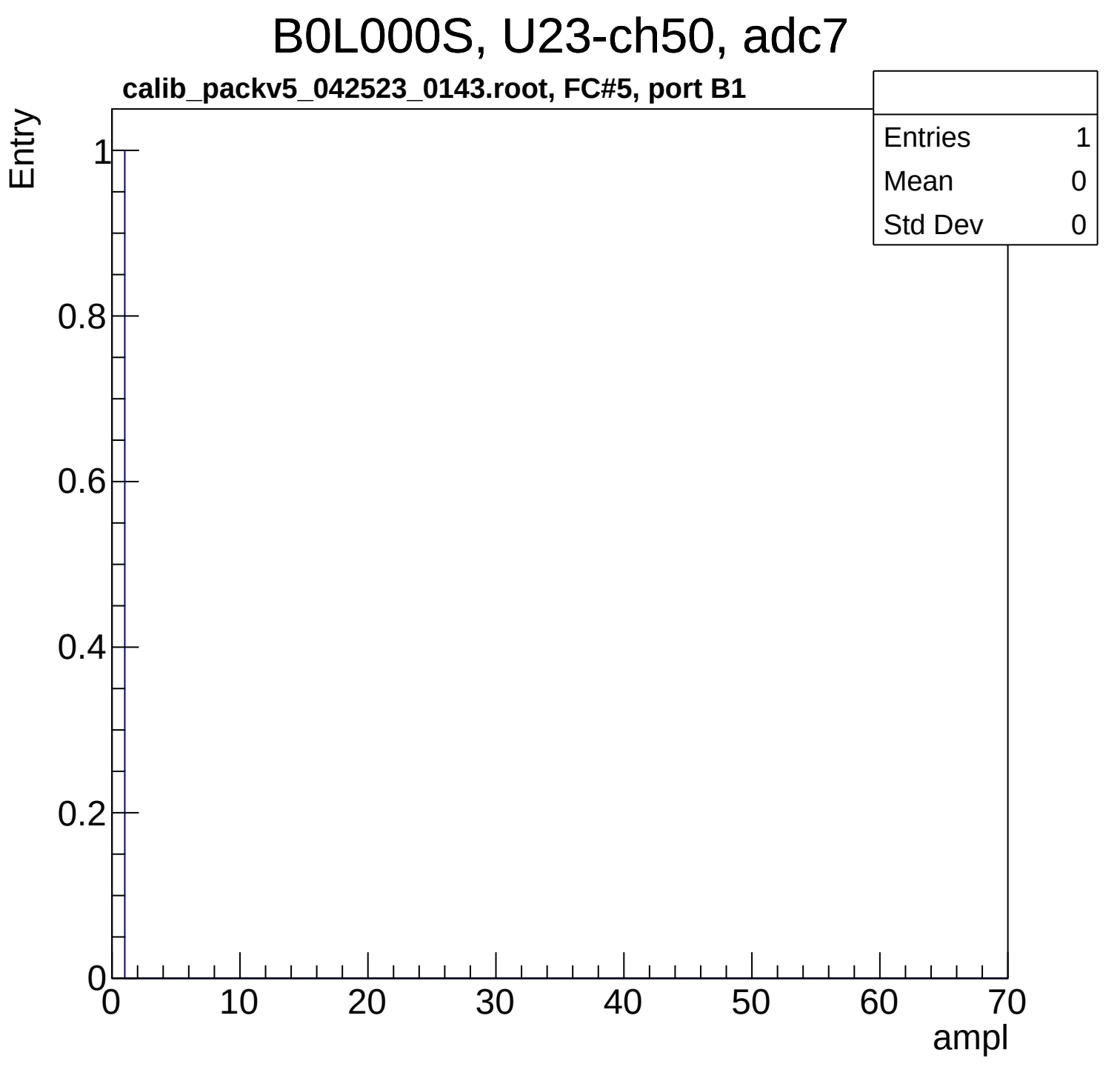

B0L000S, U23-ch50, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.34
Std Dev 3.311
Gaus mean : 43.4847
Gaus Width: 3.5800
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

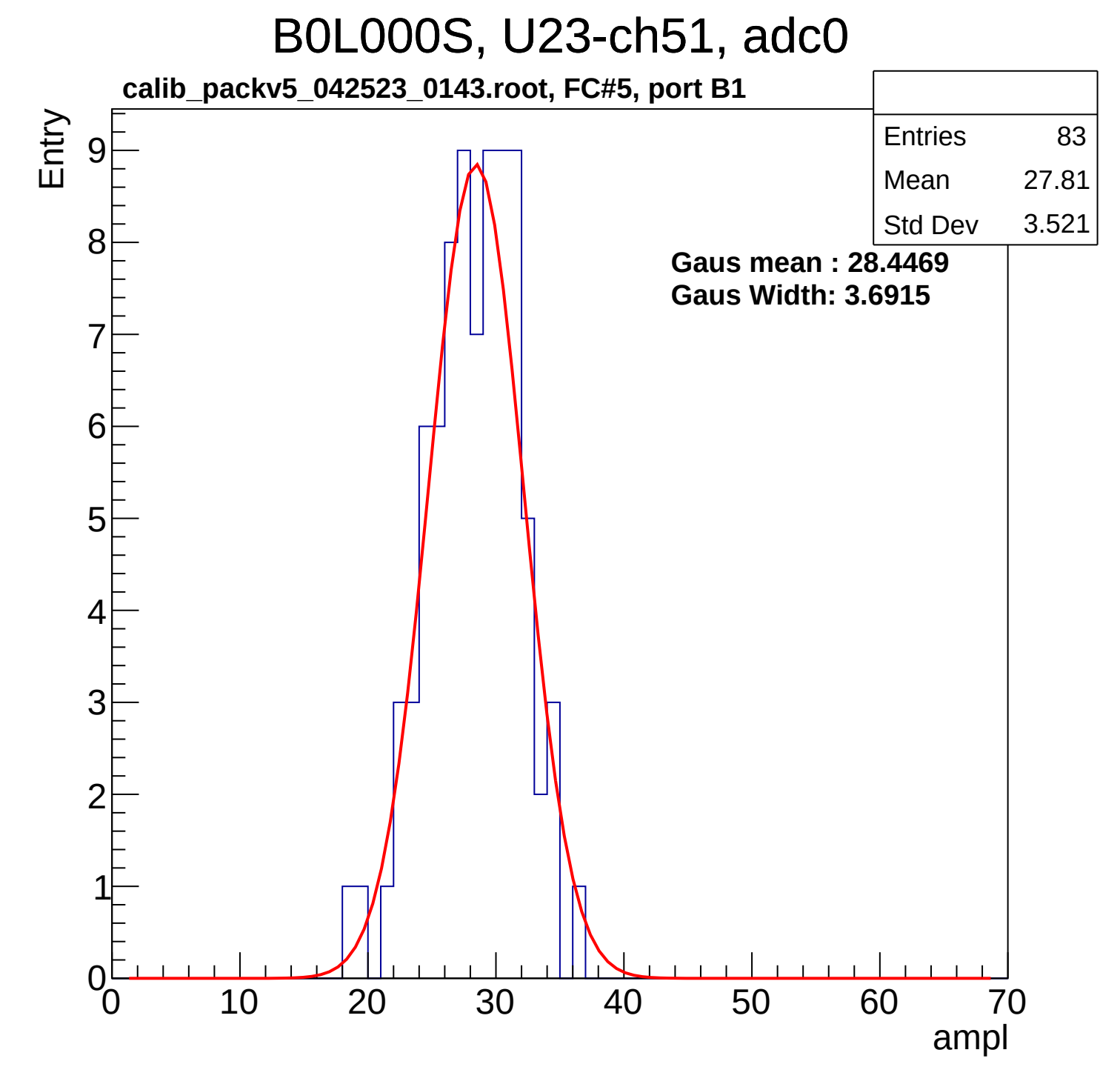

B0L000S, U23-ch50, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 48.39
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

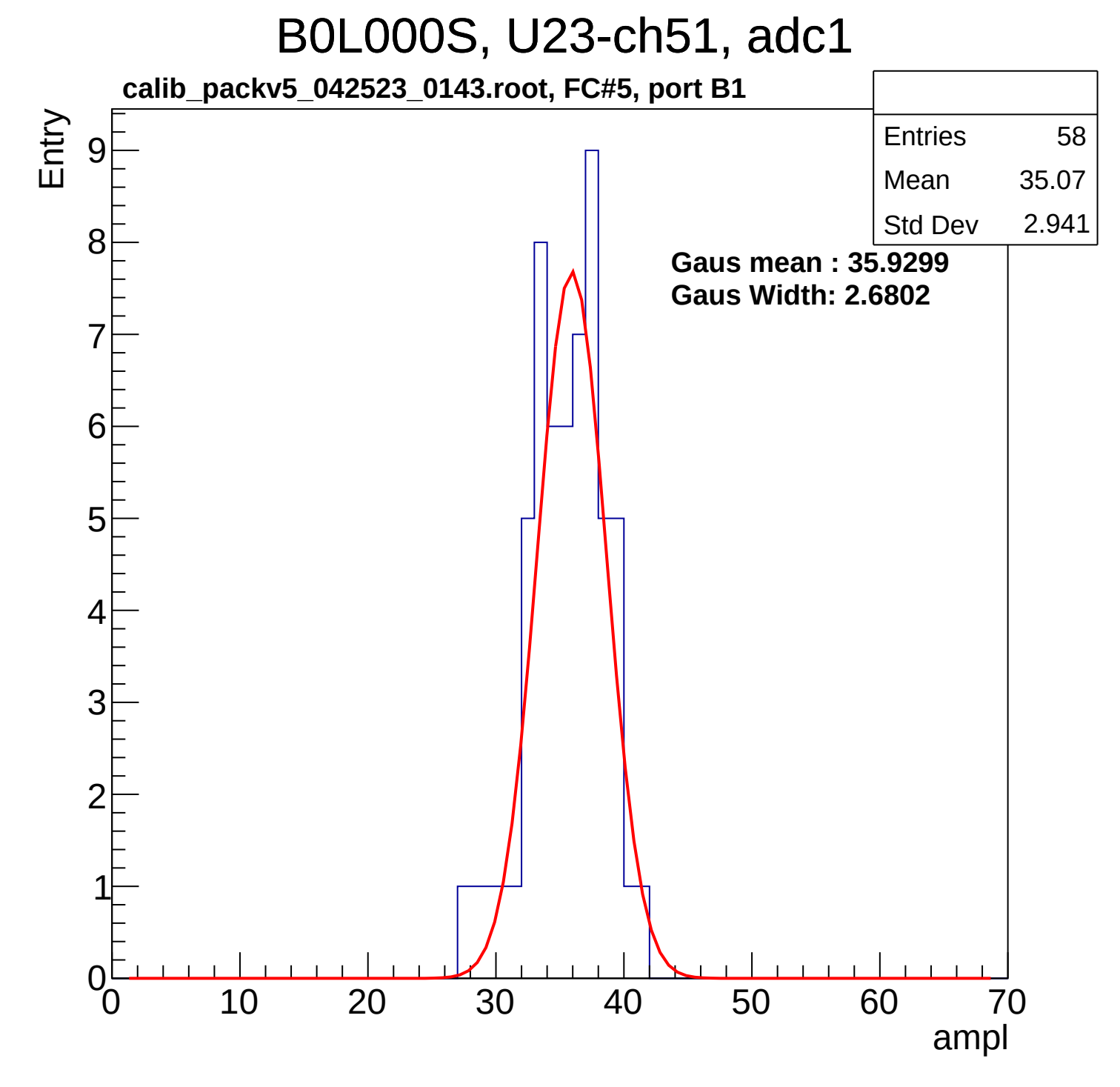

B0L000S, U23-ch50, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 54.55
Std Dev 3.227
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

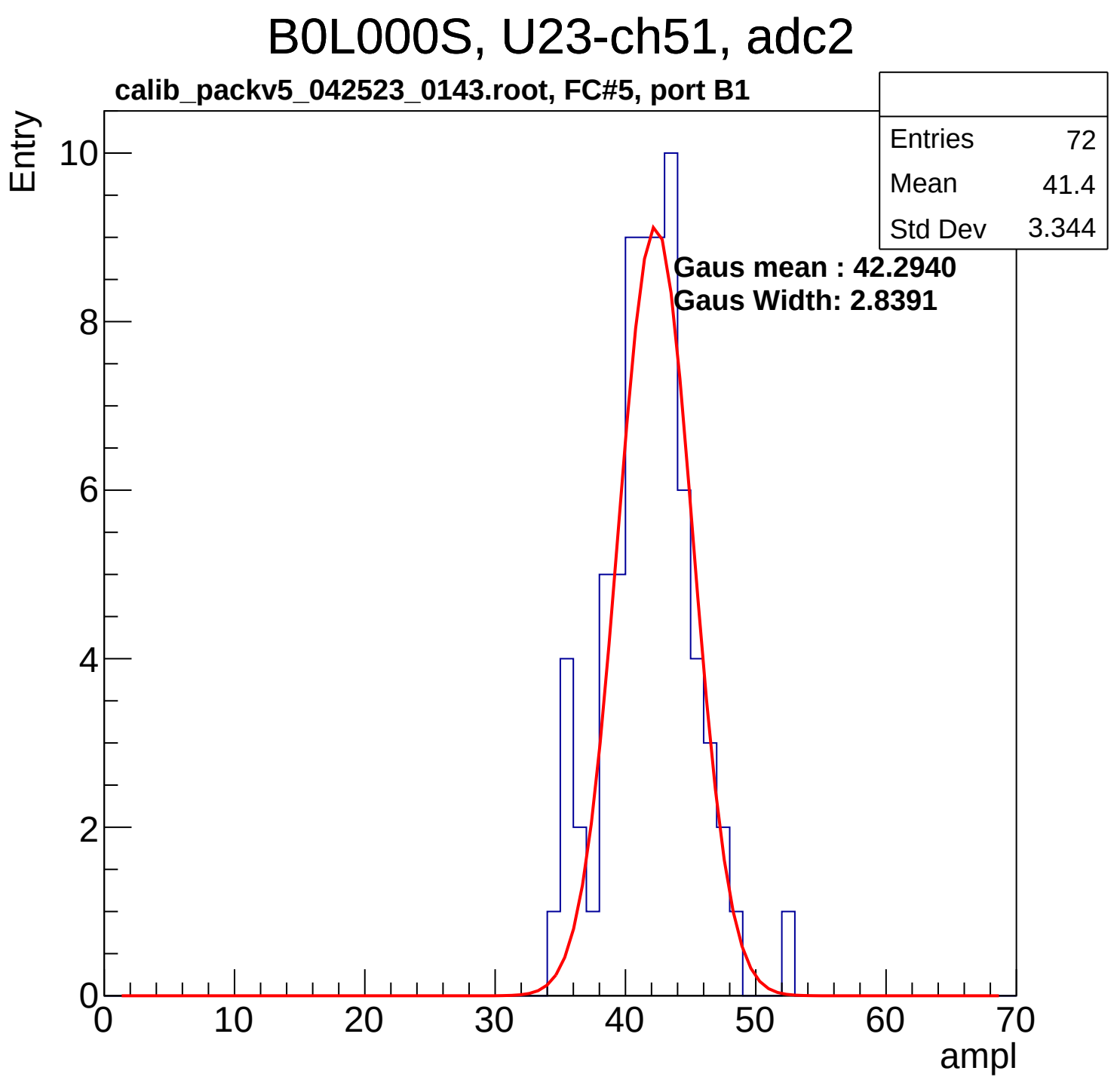

B0L000S, U23-ch50, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.72
Std Dev 2.722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

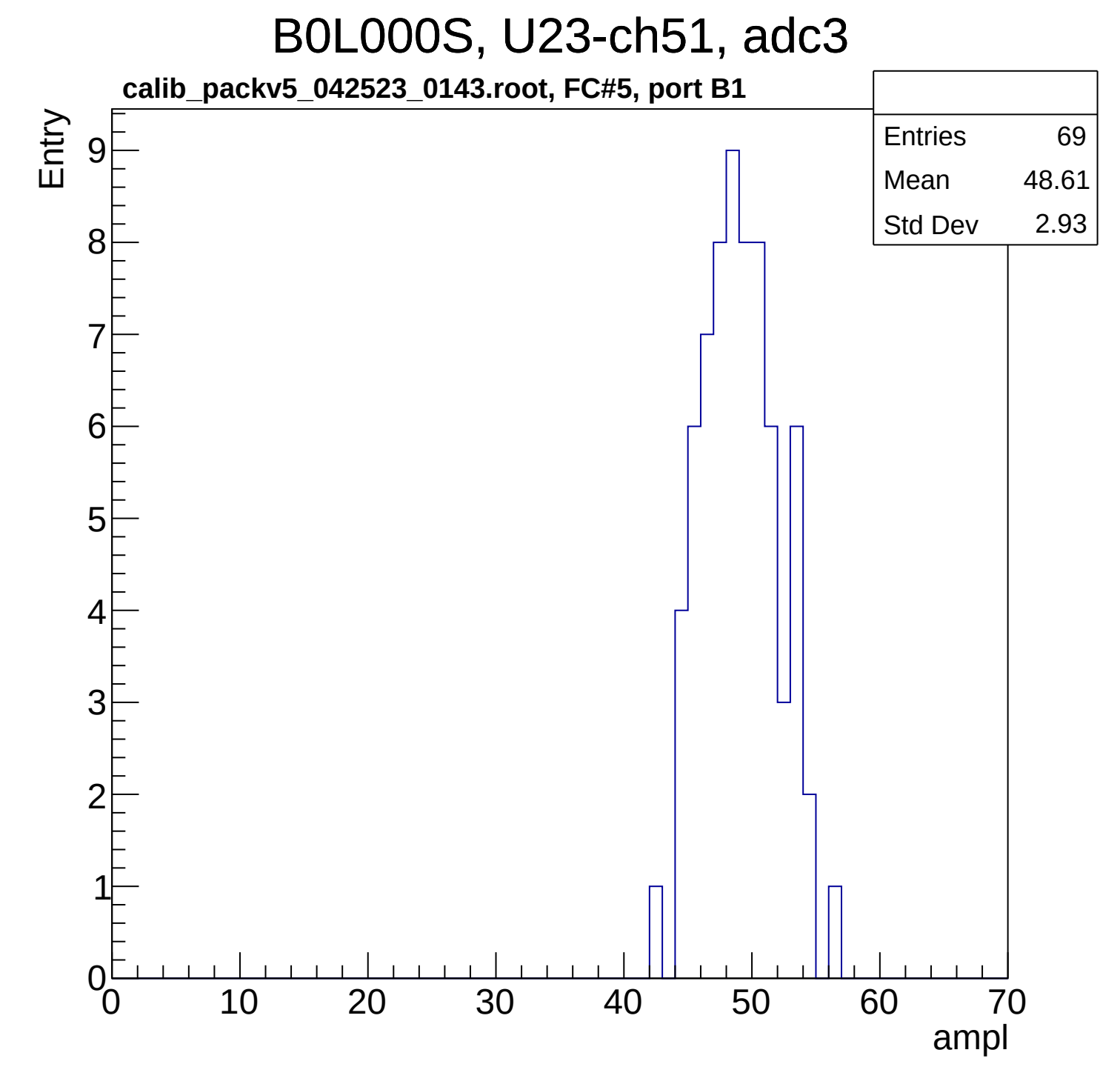

B0L000S, U23-ch50, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.58
Std Dev 17.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

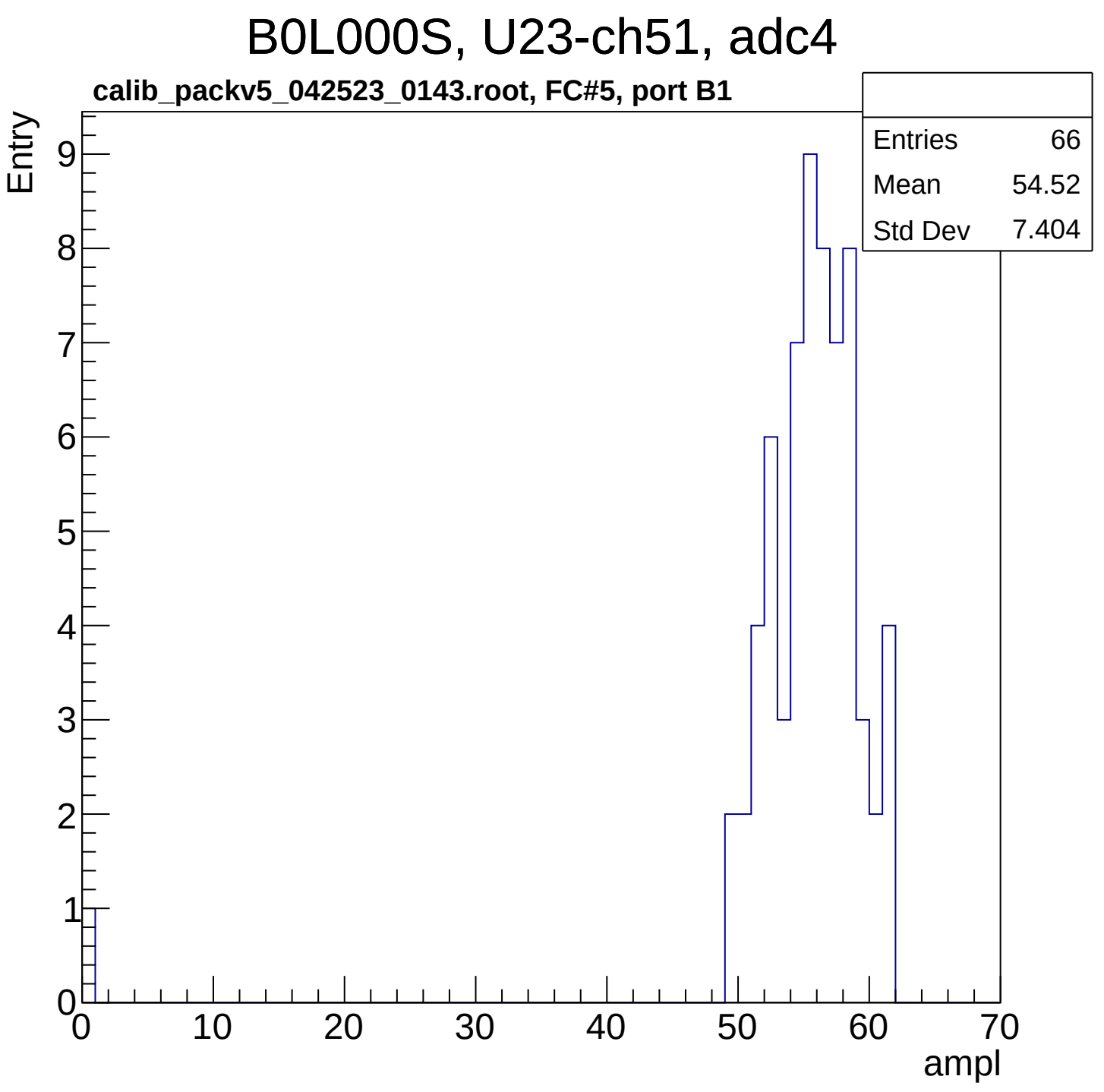

B0L000S, U23-ch50, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

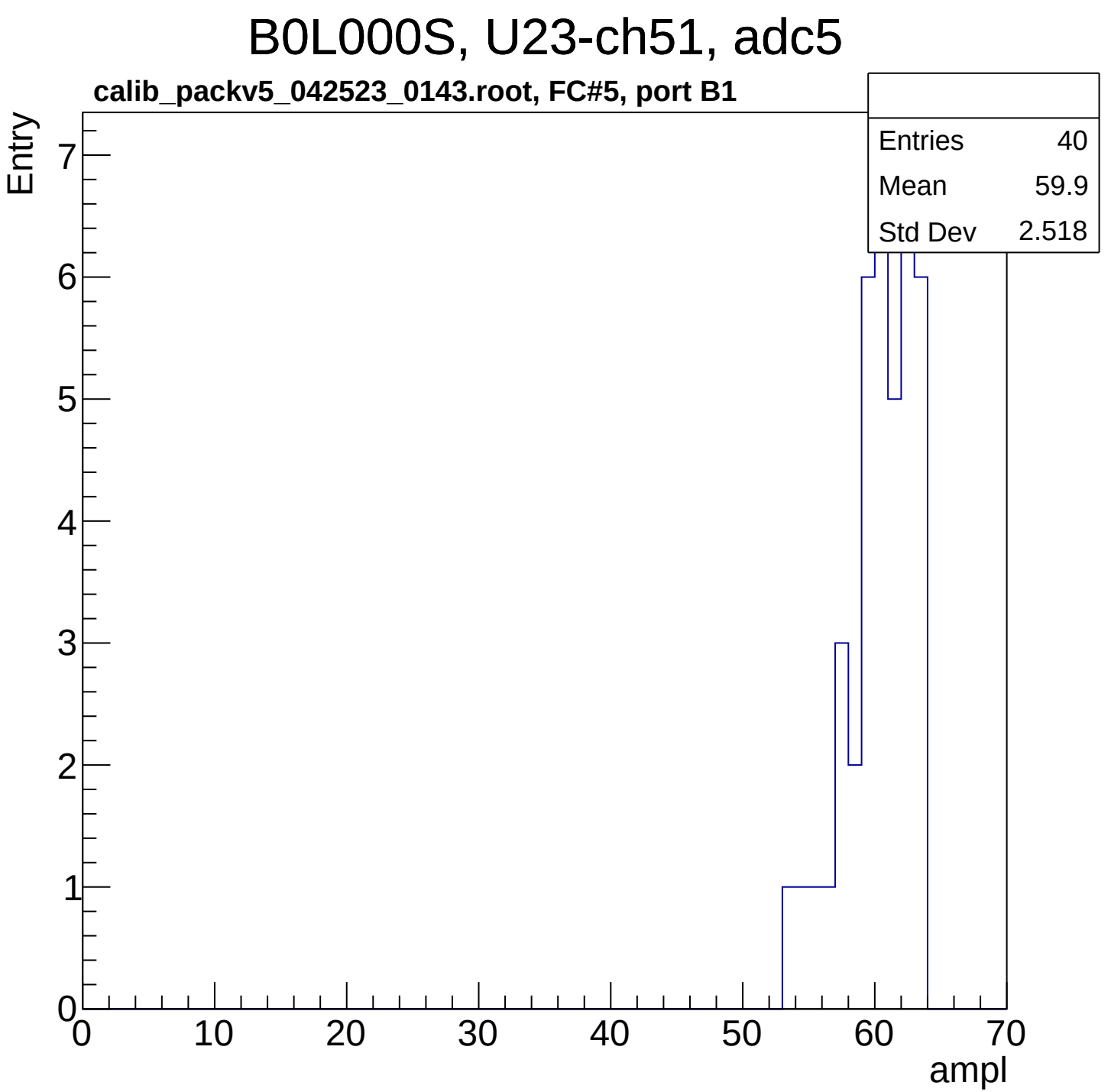

B0L000S, U23-ch51, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 27.81
Std Dev 3.521
Gaus mean : 28.4469
Gaus Width: 3.6915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

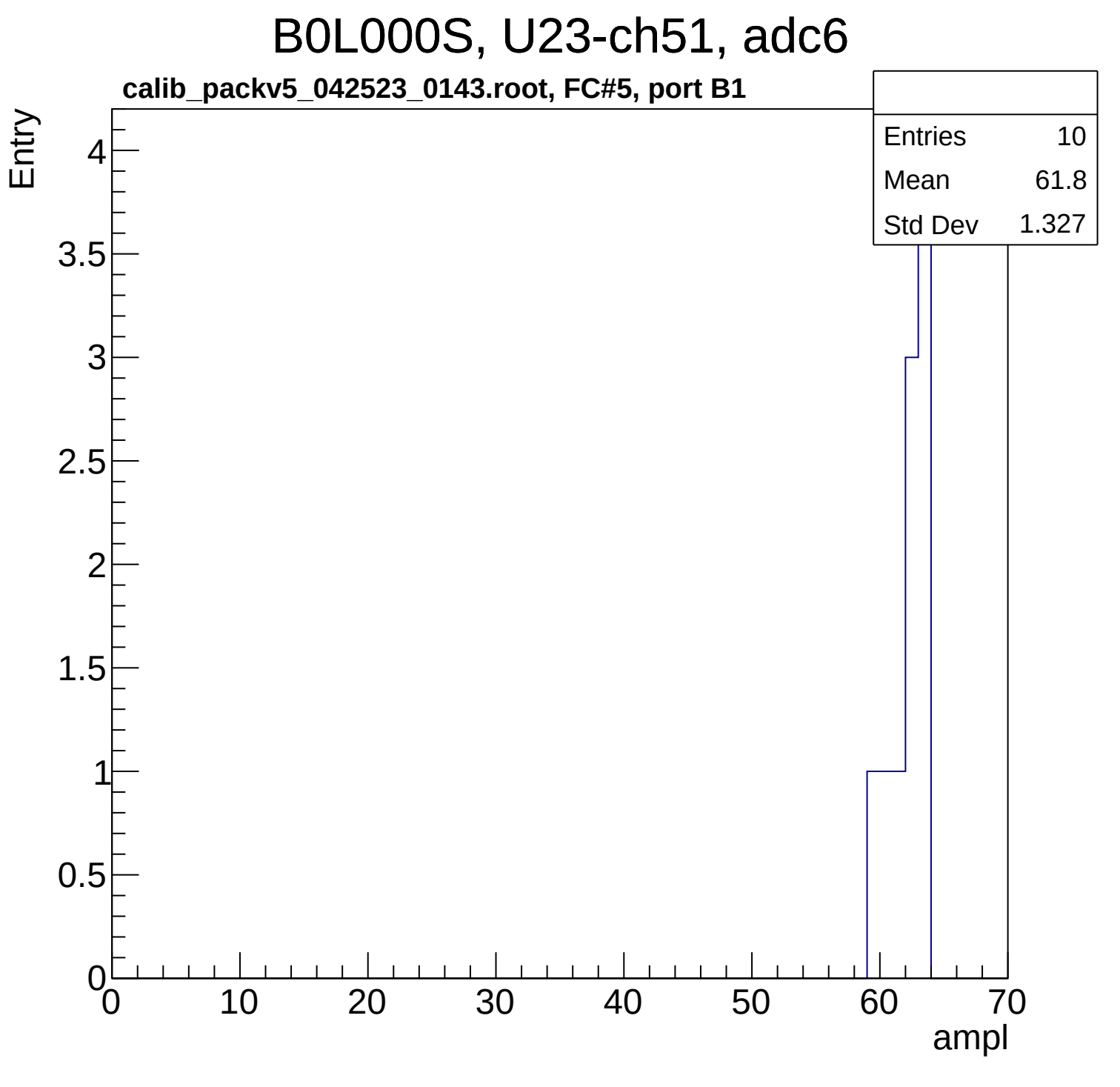

B0L000S, U23-ch51, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.07
Std Dev 2.941
Gaus mean : 35.9299
Gaus Width: 2.6802
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

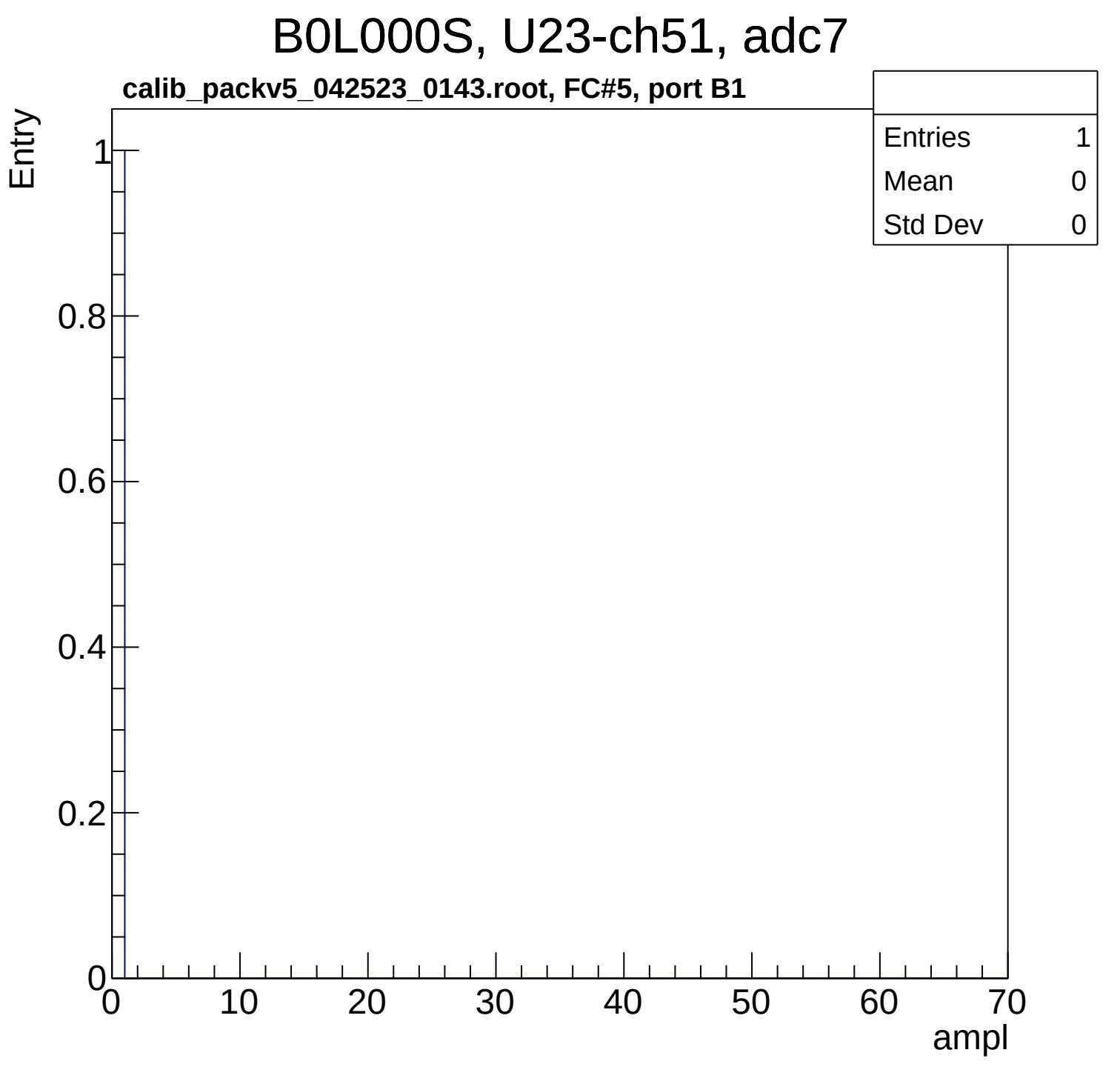

B0L000S, U23-ch51, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 41.4
Std Dev 3.344
Gaus mean : 42.2940
Gaus Width: 2.8391
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

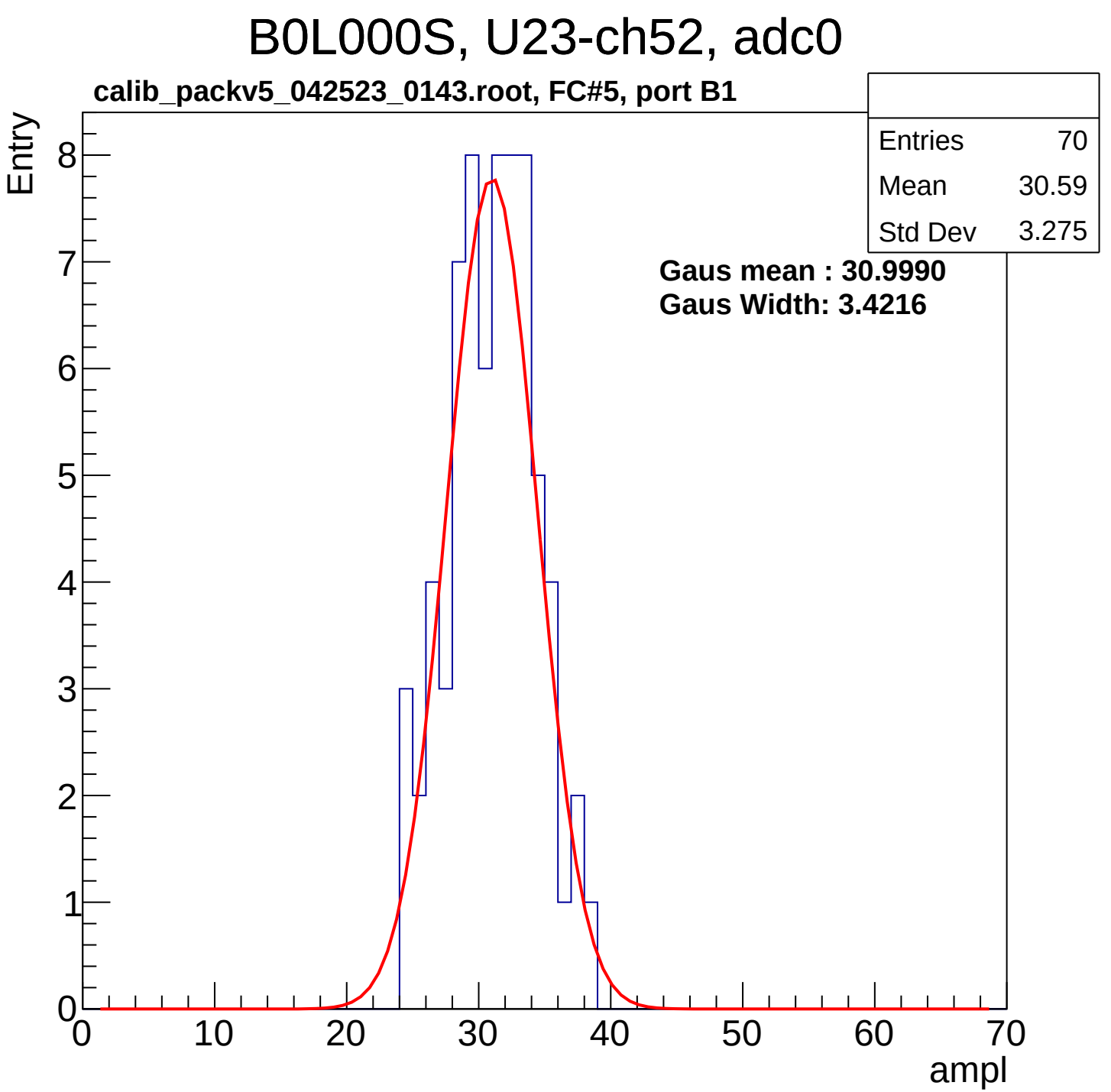

B0L000S, U23-ch51, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 48.61
Std Dev 2.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

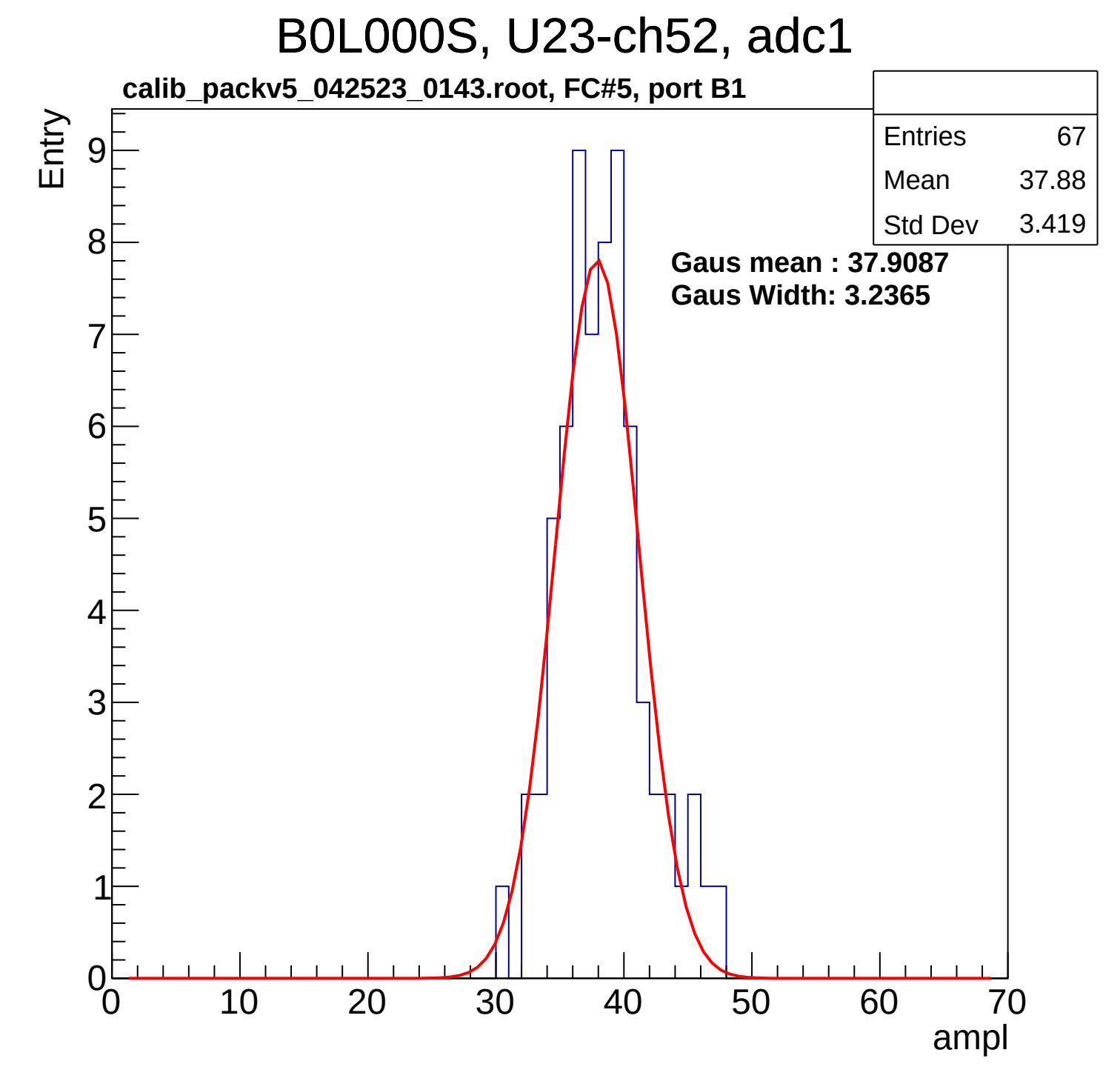

B0L000S, U23-ch51, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.52
Std Dev 7.404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

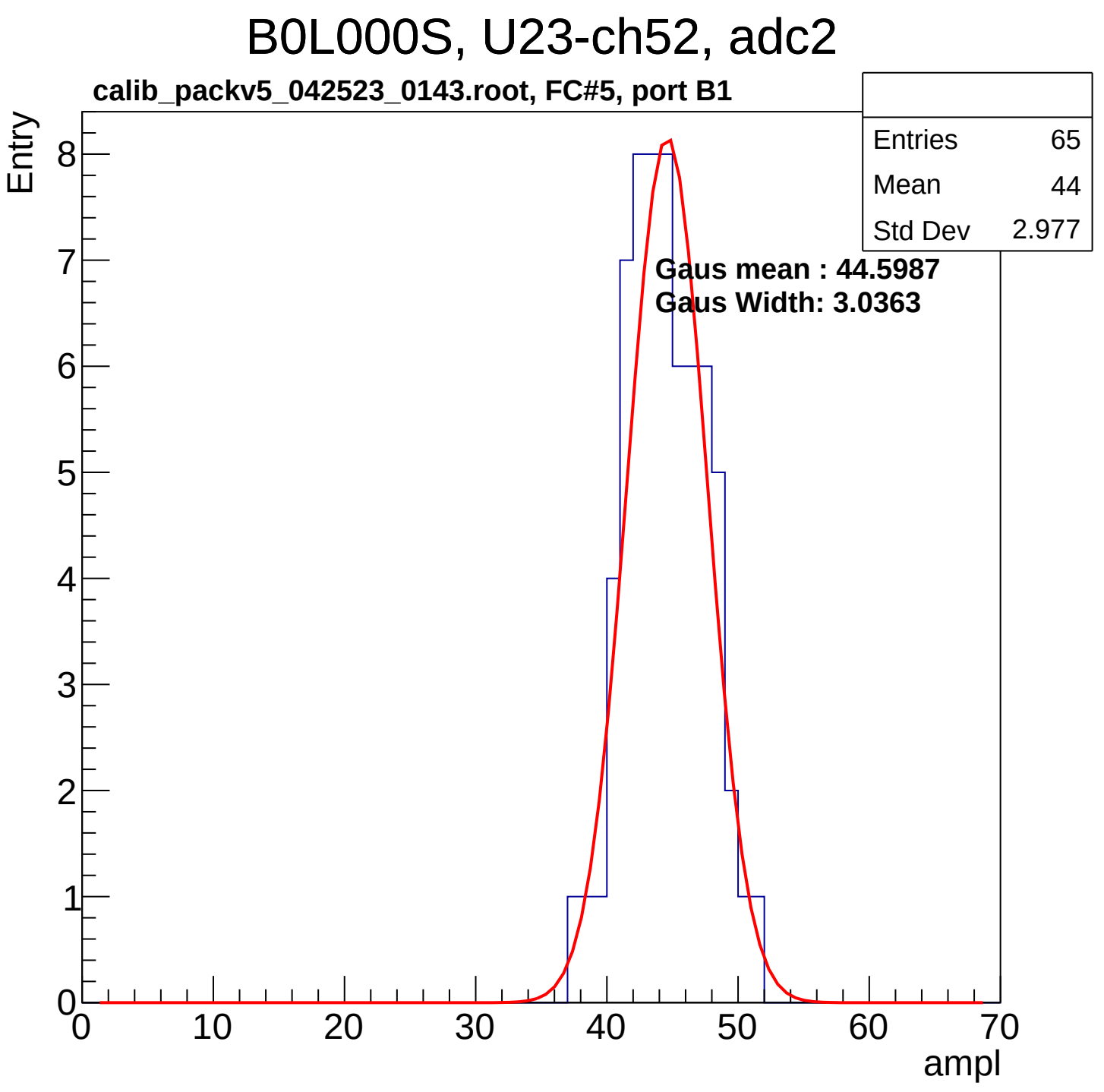

B0L000S, U23-ch51, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.9
Std Dev 2.518
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

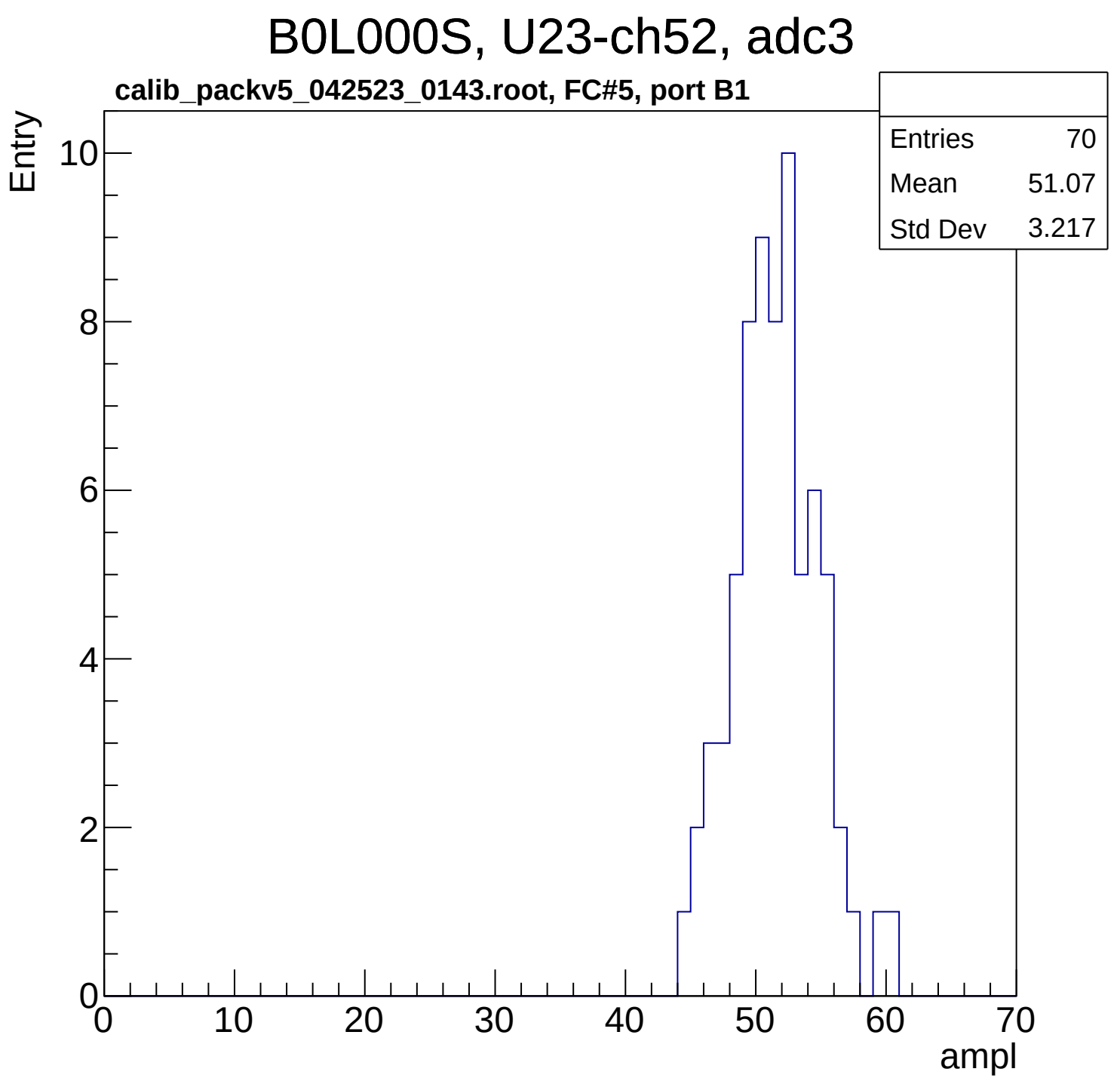

B0L000S, U23-ch51, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.8
Std Dev 1.327
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

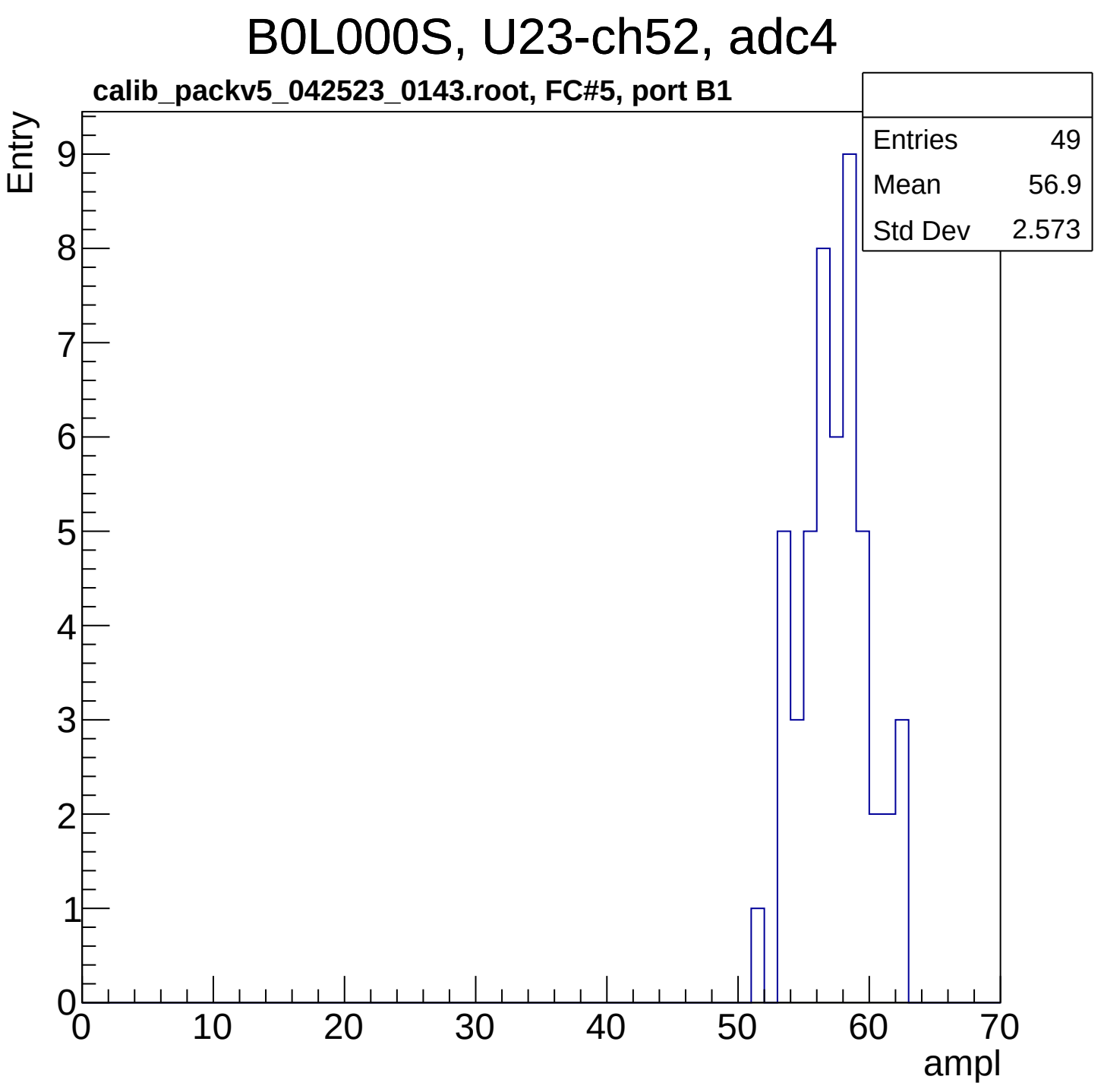

B0L000S, U23-ch51, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

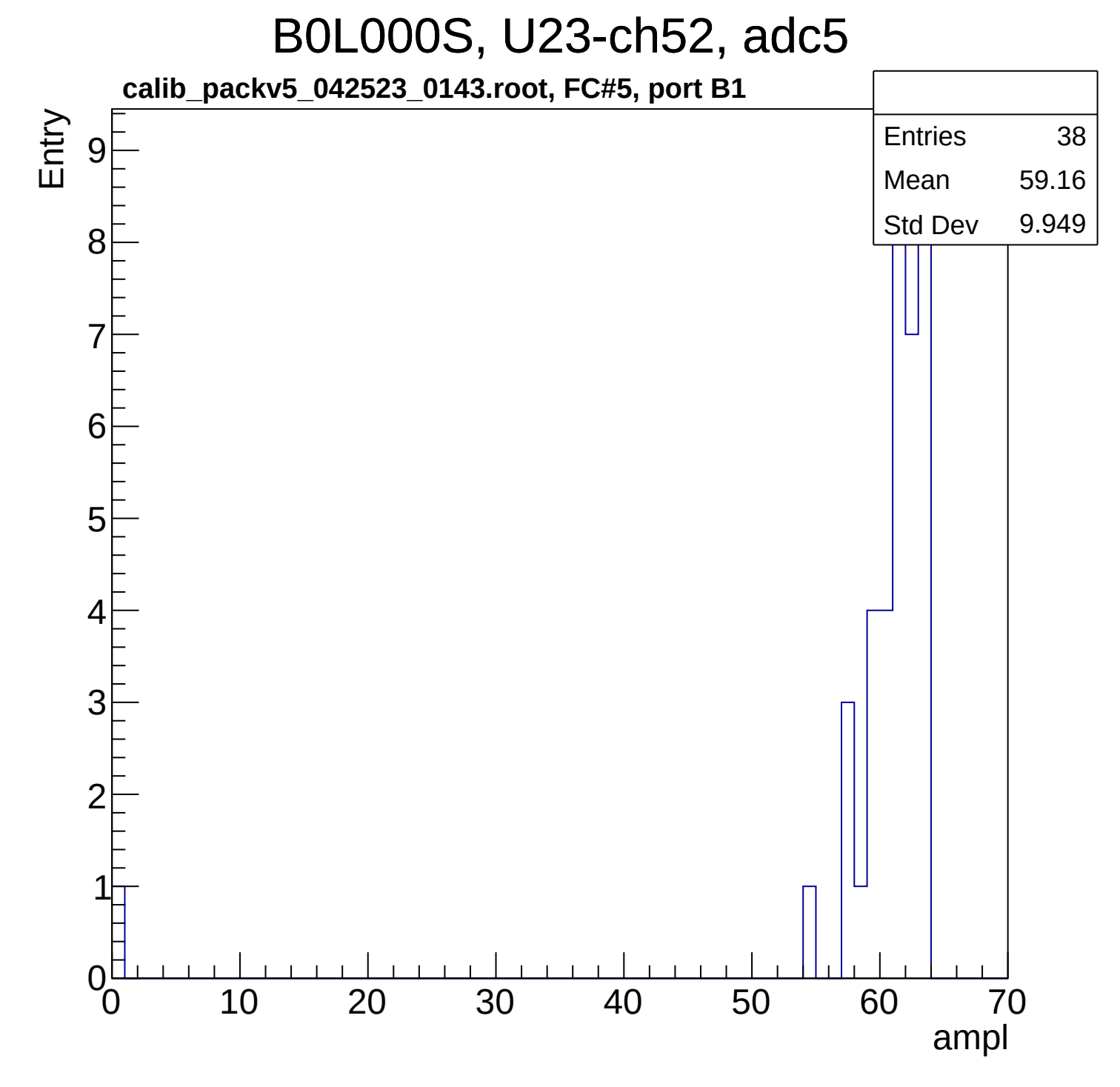

B0L000S, U23-ch52, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 30.59
Std Dev 3.275
Gaus mean : 30.9990
Gaus Width: 3.4216
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

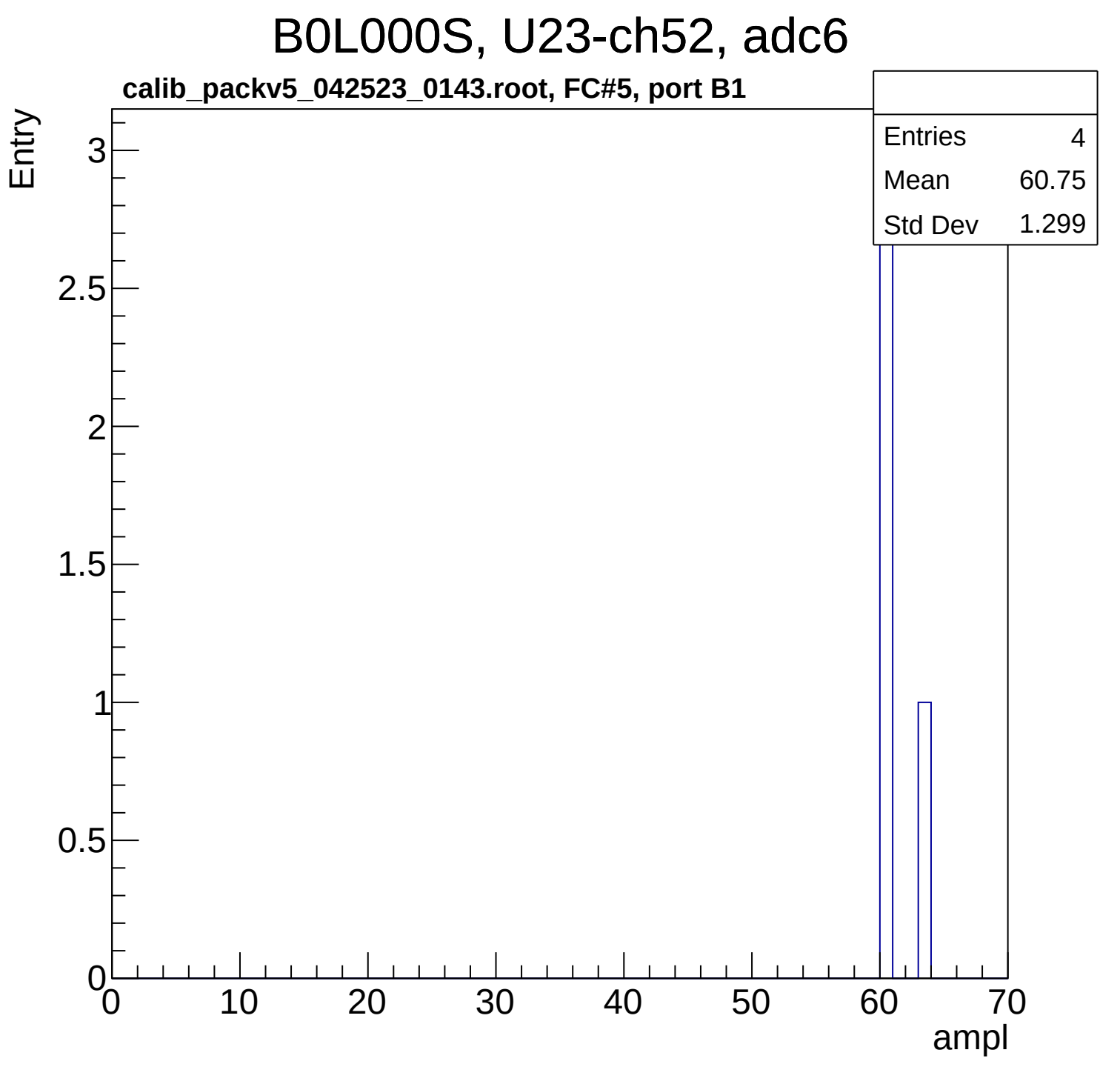

B0L000S, U23-ch52, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.88
Std Dev 3.419
Gaus mean : 37.9087
Gaus Width: 3.2365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

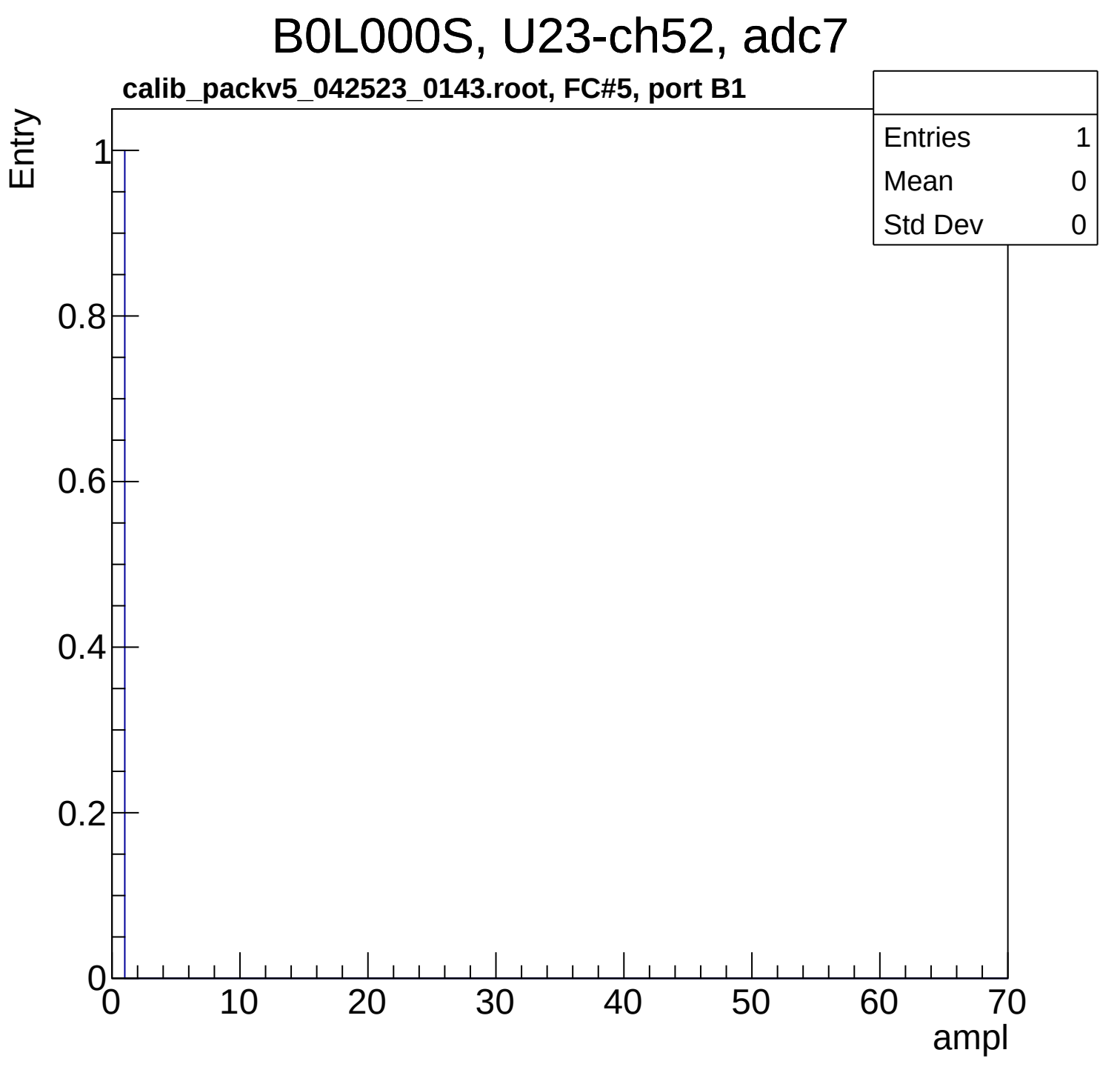

B0L000S, U23-ch52, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 44
Std Dev 2.977
Gaus mean : 44.5987
Gaus Width: 3.0363
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

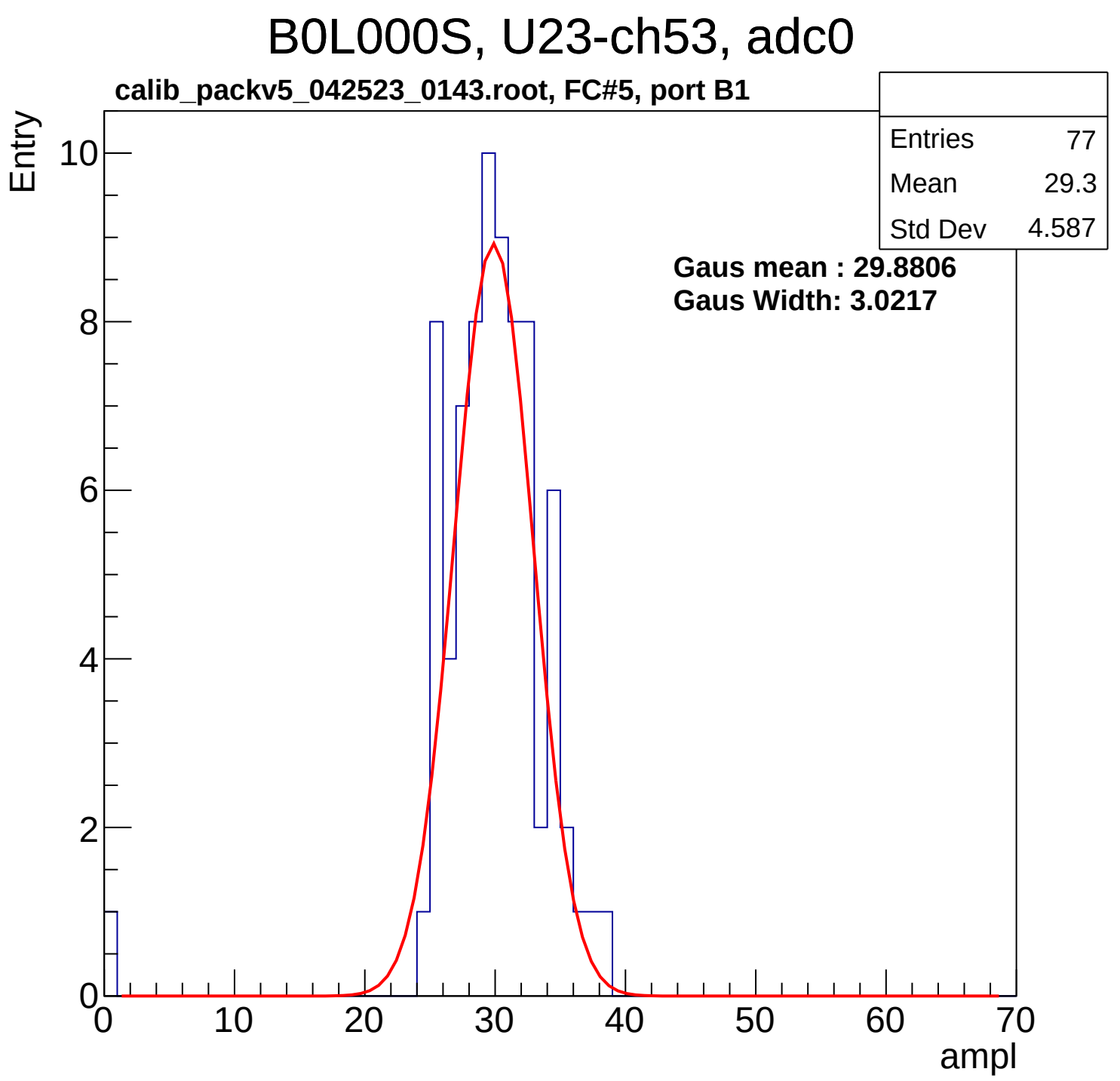

B0L000S, U23-ch52, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 51.07
Std Dev 3.217
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

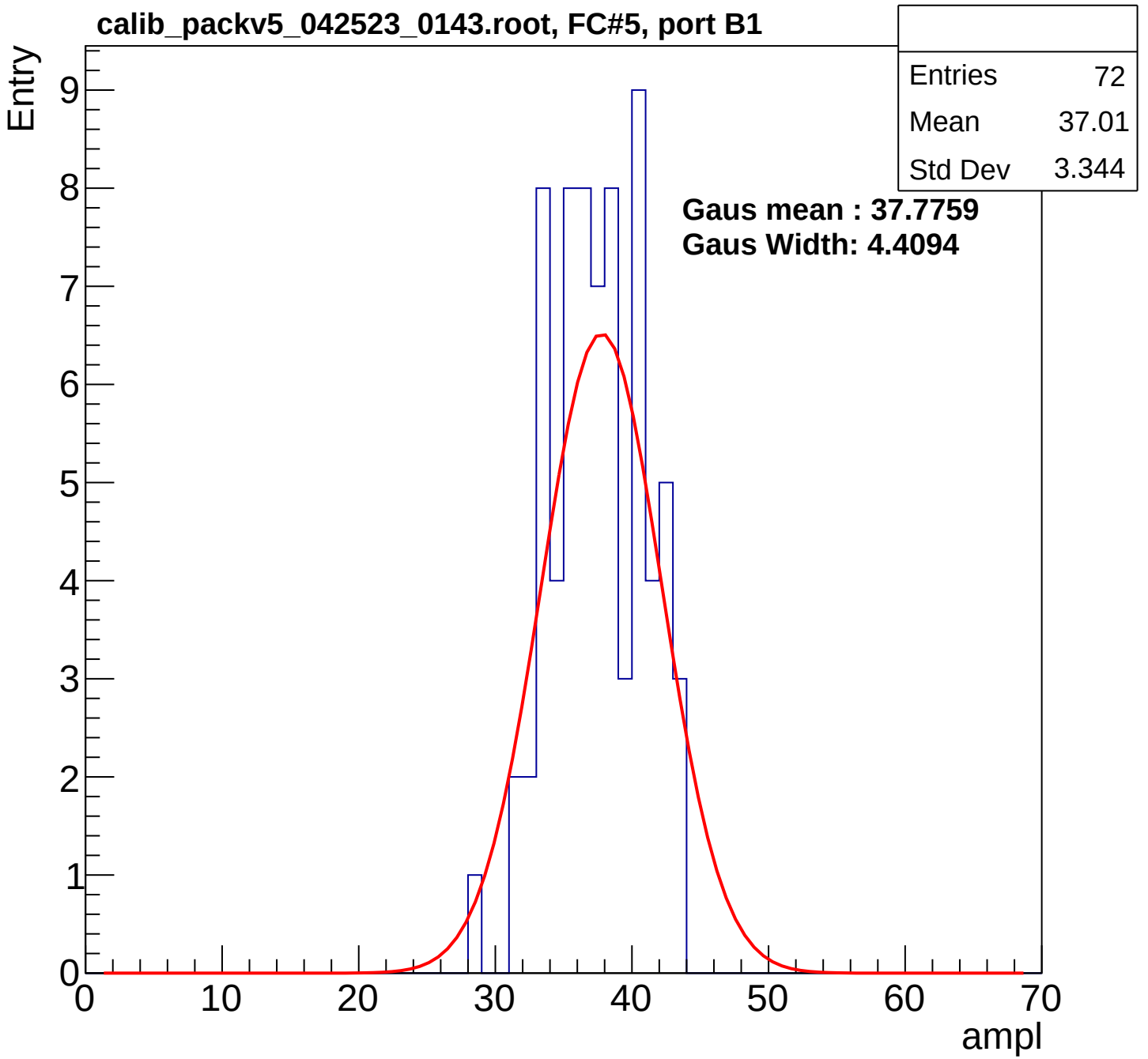

B0L000S, U23-ch52, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 56.9
Std Dev 2.573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

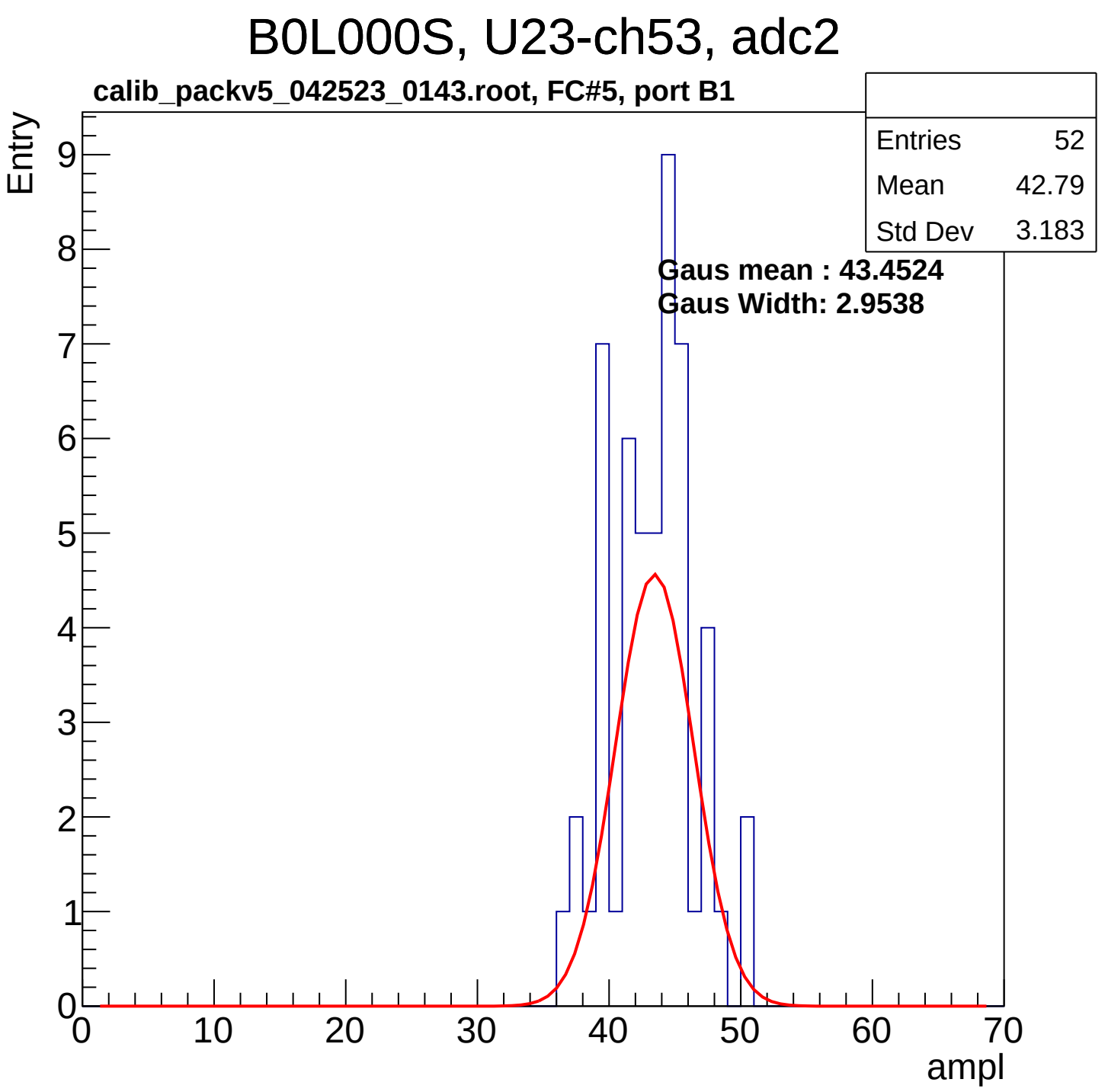

B0L000S, U23-ch52, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 59.16
Std Dev 9.949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

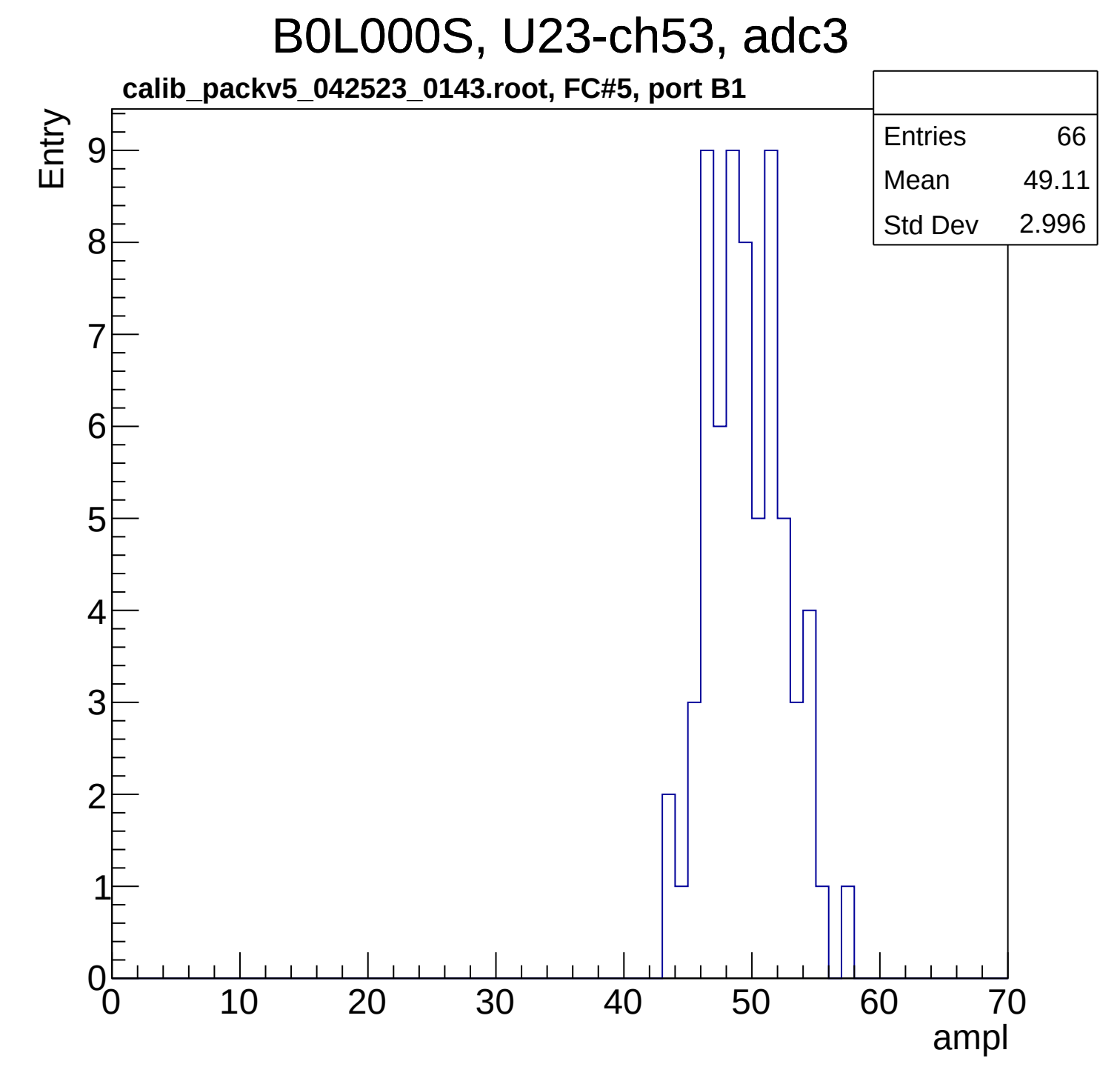

B0L000S, U23-ch52, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.75
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

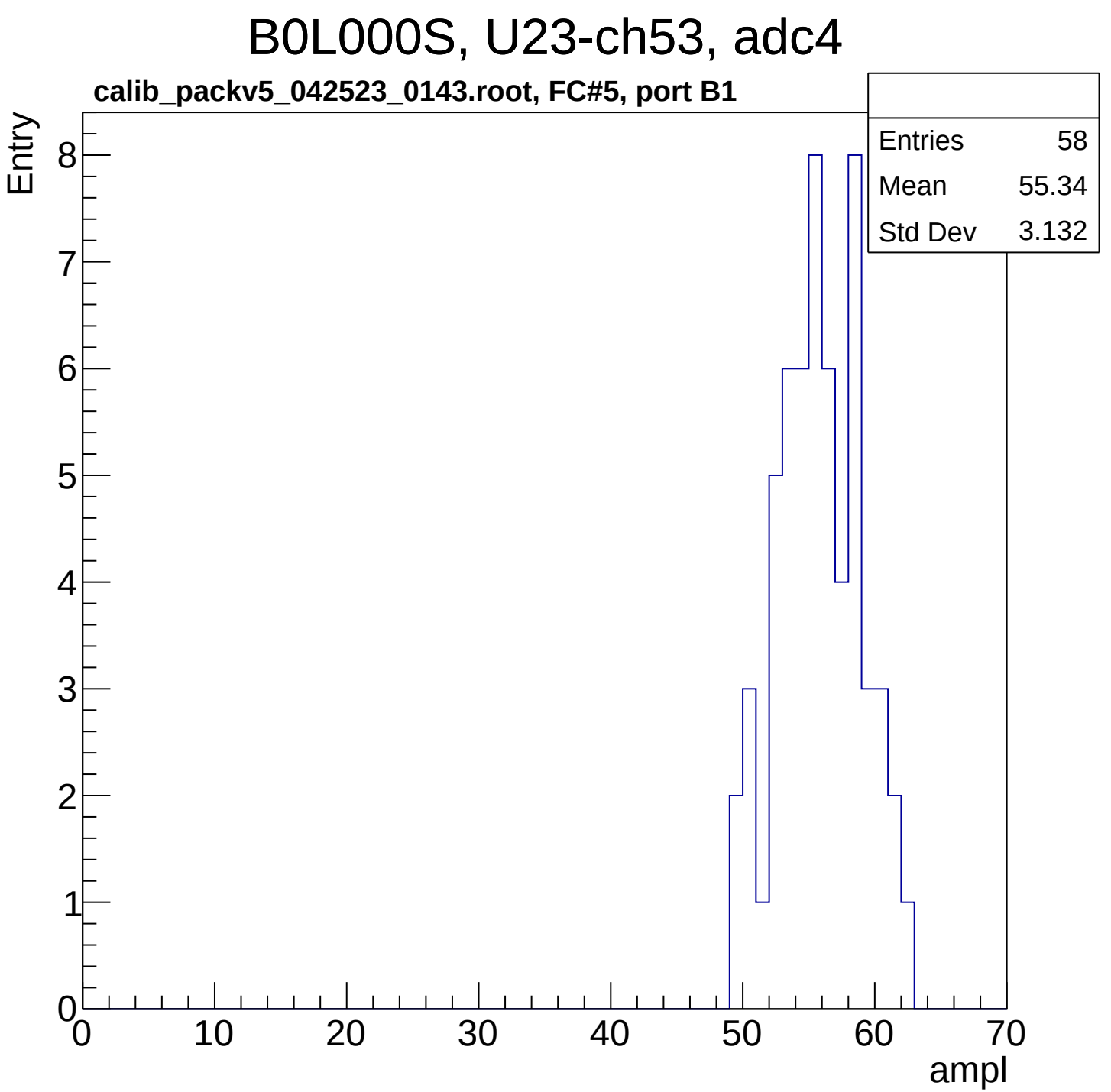

B0L000S, U23-ch52, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

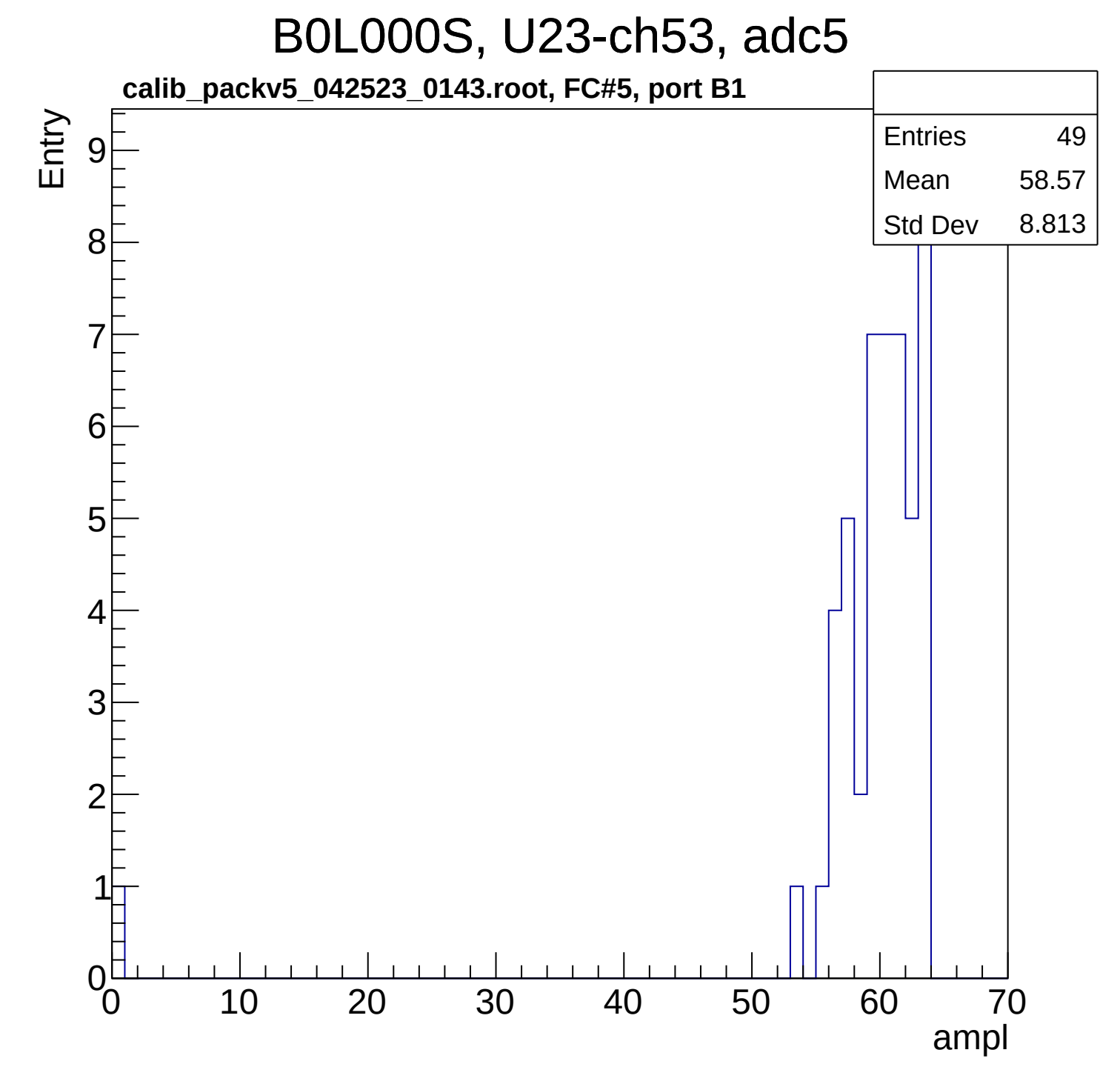

B0L000S, U23-ch53, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29.3
Std Dev 4.587
Gaus mean : 29.8806
Gaus Width: 3.0217
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

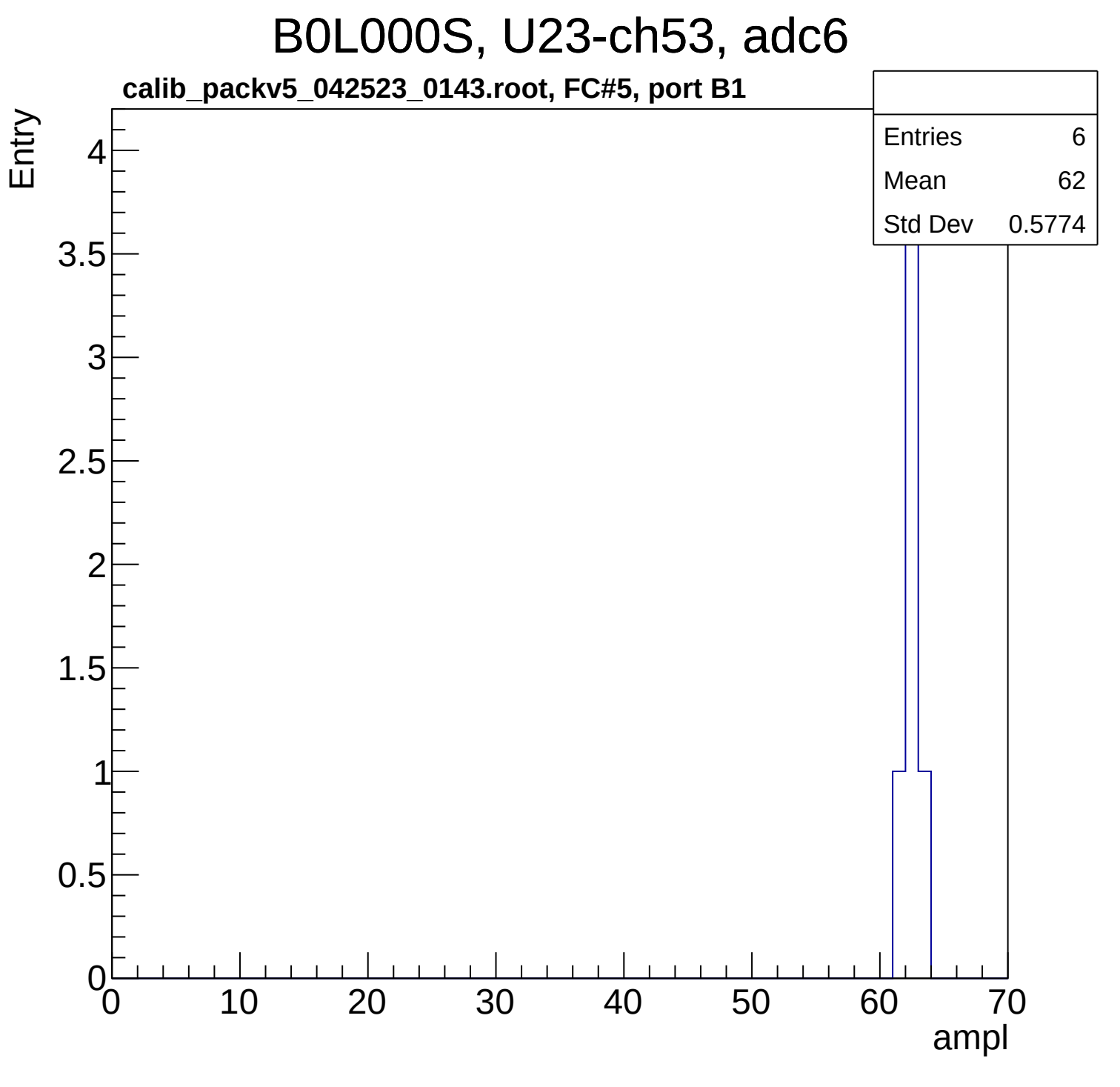

B0L000S, U23-ch53, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.01
Std Dev 3.344
Gaus mean : 37.7759
Gaus Width: 4.4094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

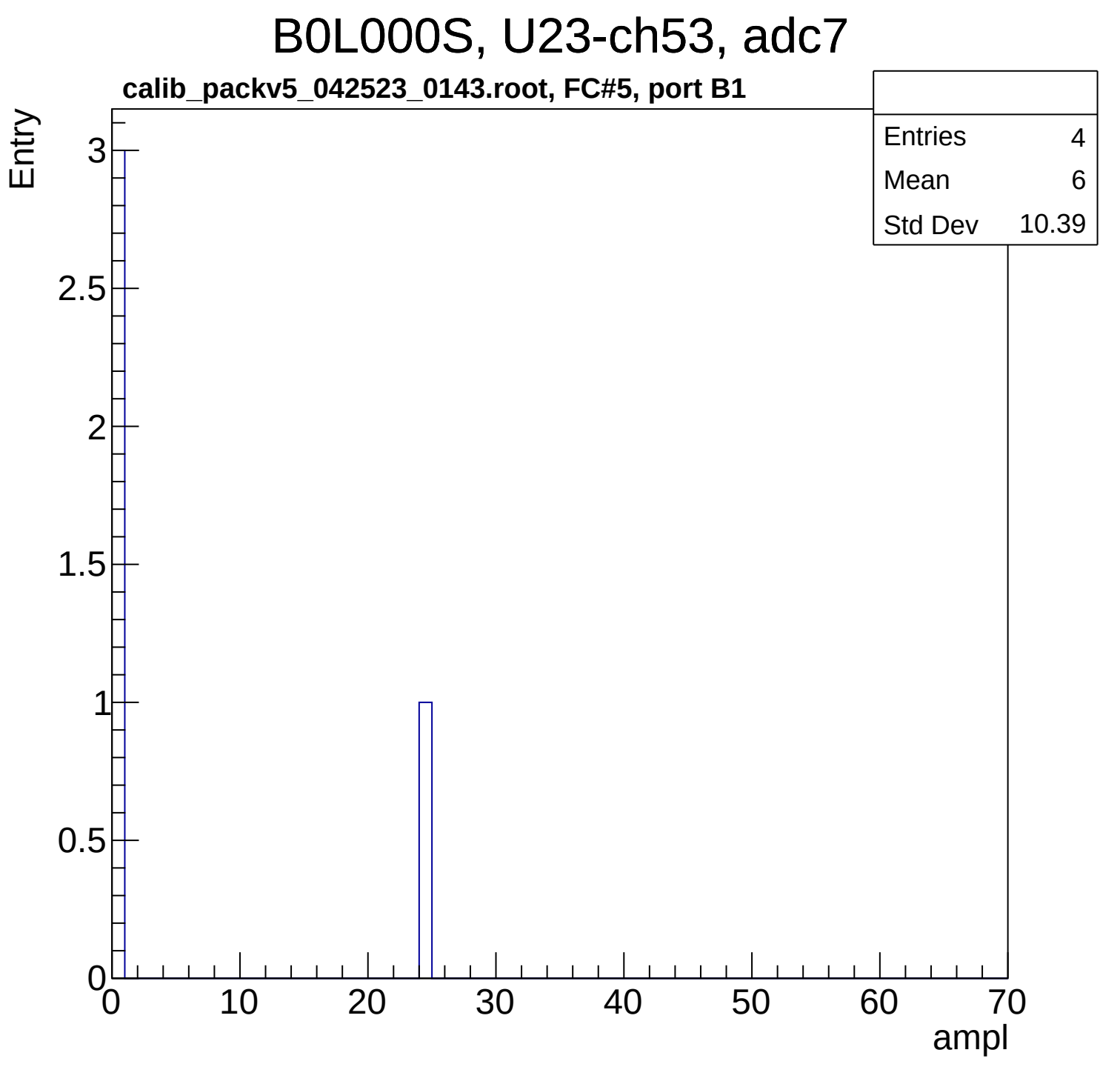

B0L000S, U23-ch53, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.79
Std Dev 3.183
Gaus mean : 43.4524
Gaus Width: 2.9538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

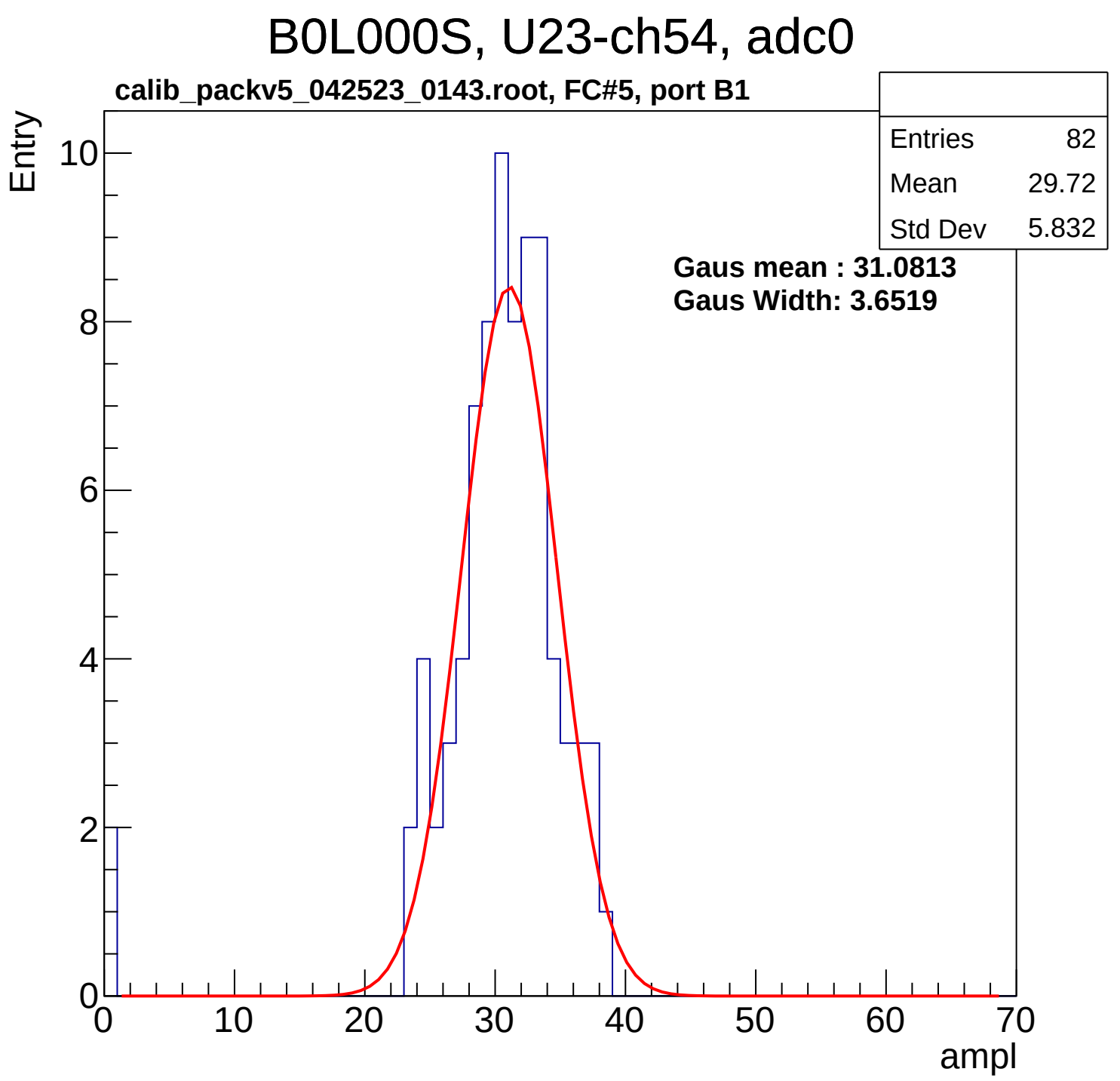

B0L000S, U23-ch53, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.11
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

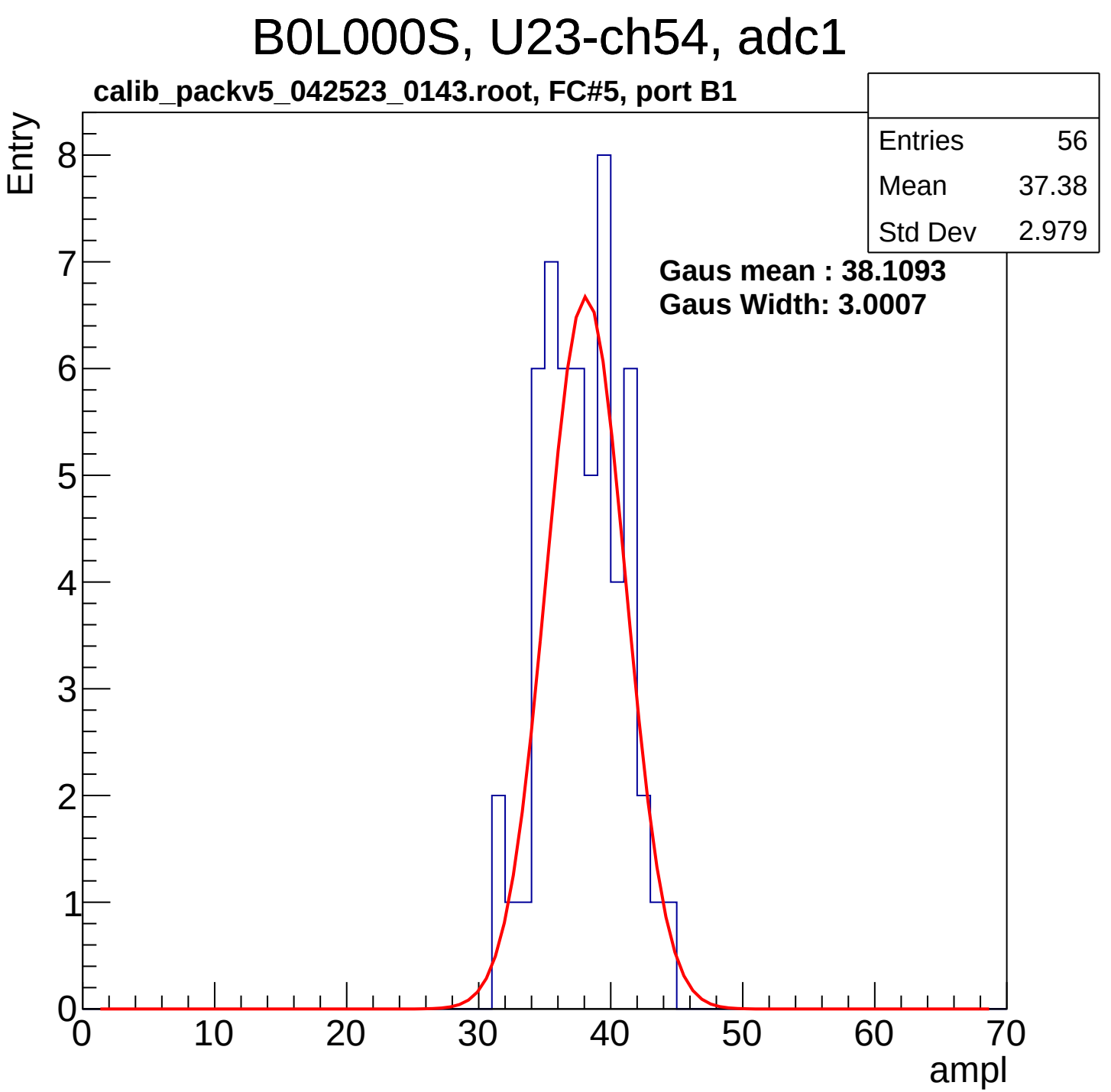

B0L000S, U23-ch53, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.34
Std Dev 3.132
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

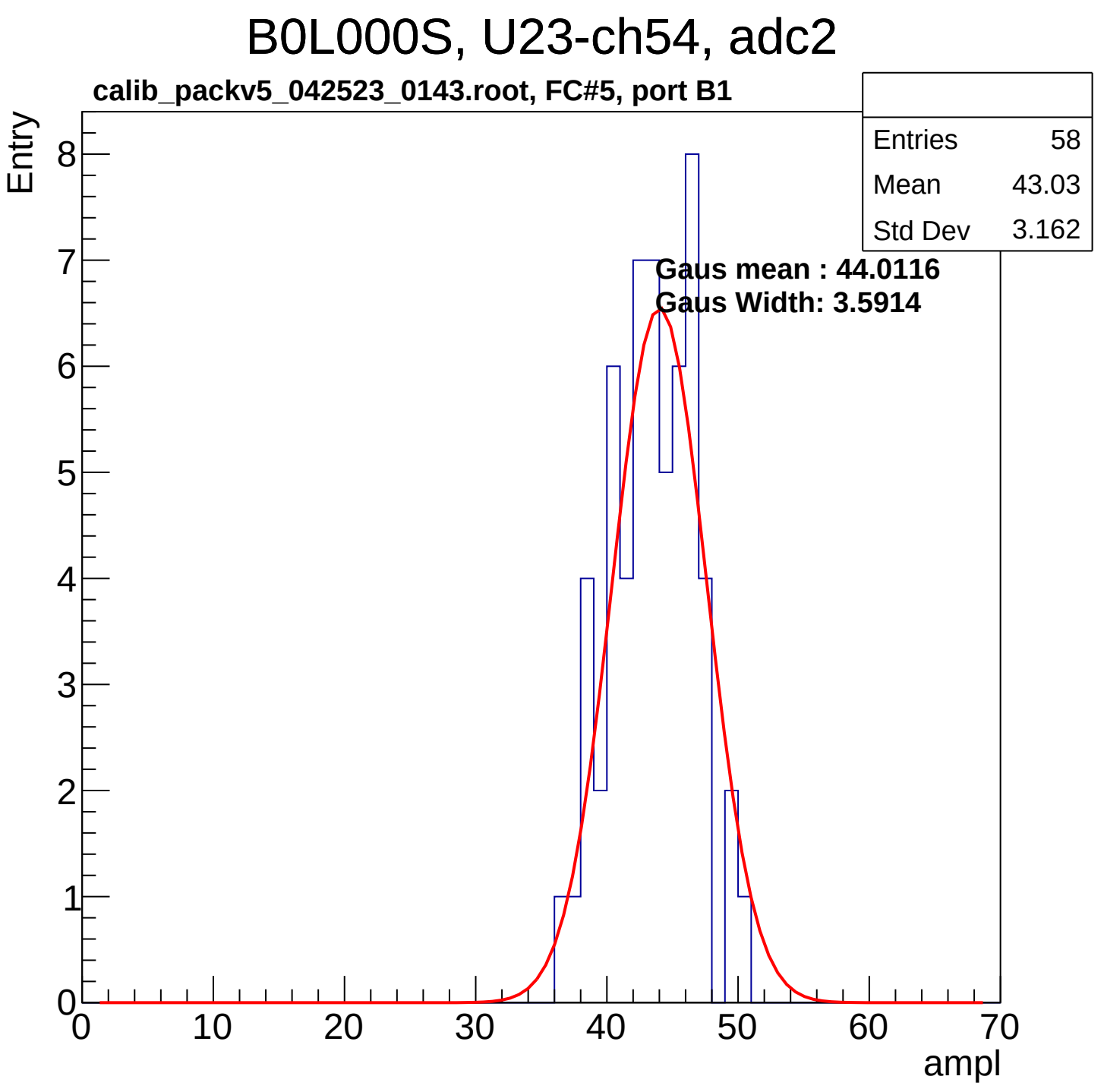

B0L000S, U23-ch53, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.57
Std Dev 8.813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

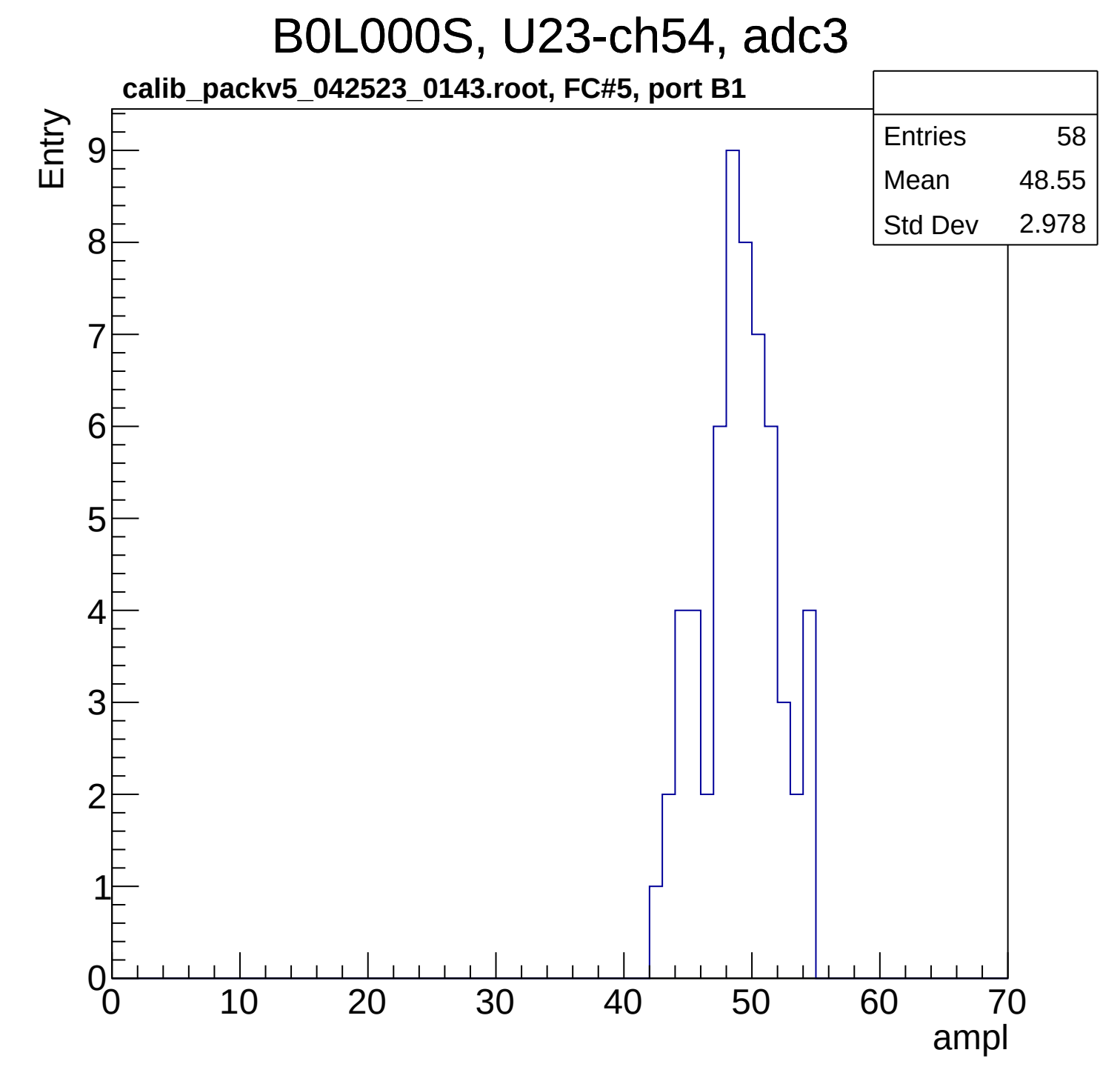

B0L000S, U23-ch53, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62
Std Dev 0.5774
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

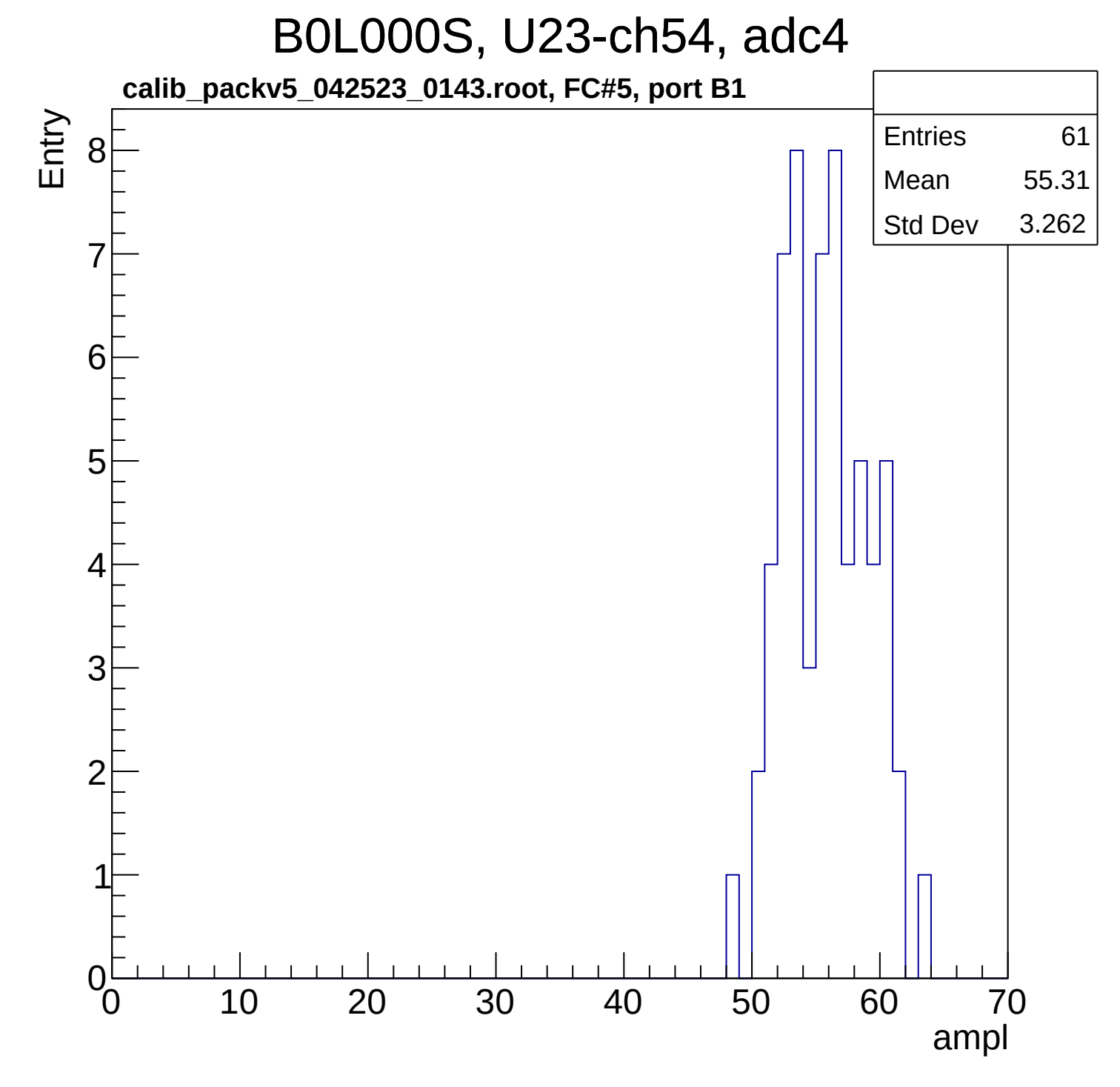

B0L000S, U23-ch53, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 6
Std Dev 10.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

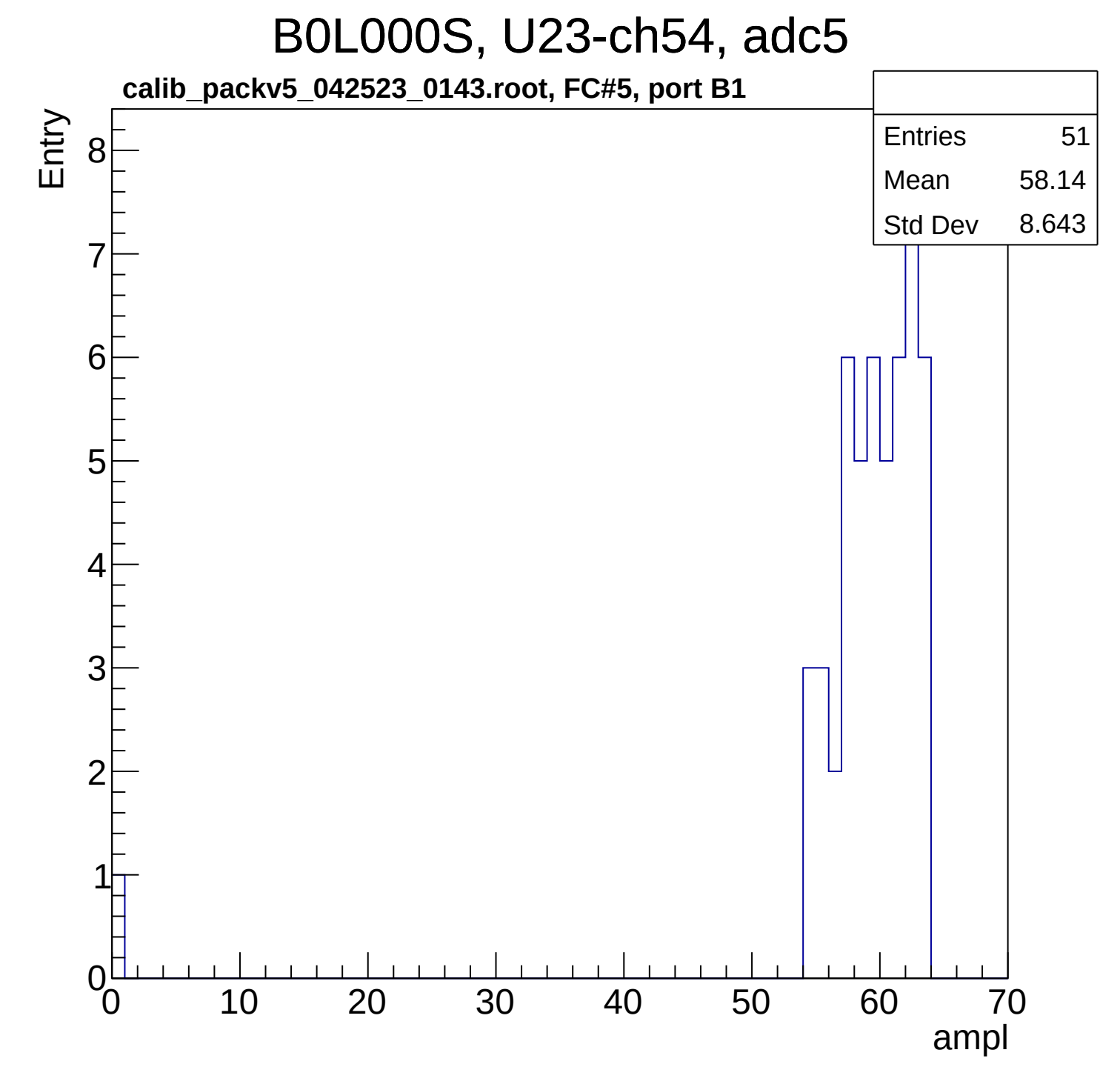

B0L000S, U23-ch54, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 29.72
Std Dev 5.832
Gaus mean : 31.0813
Gaus Width: 3.6519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

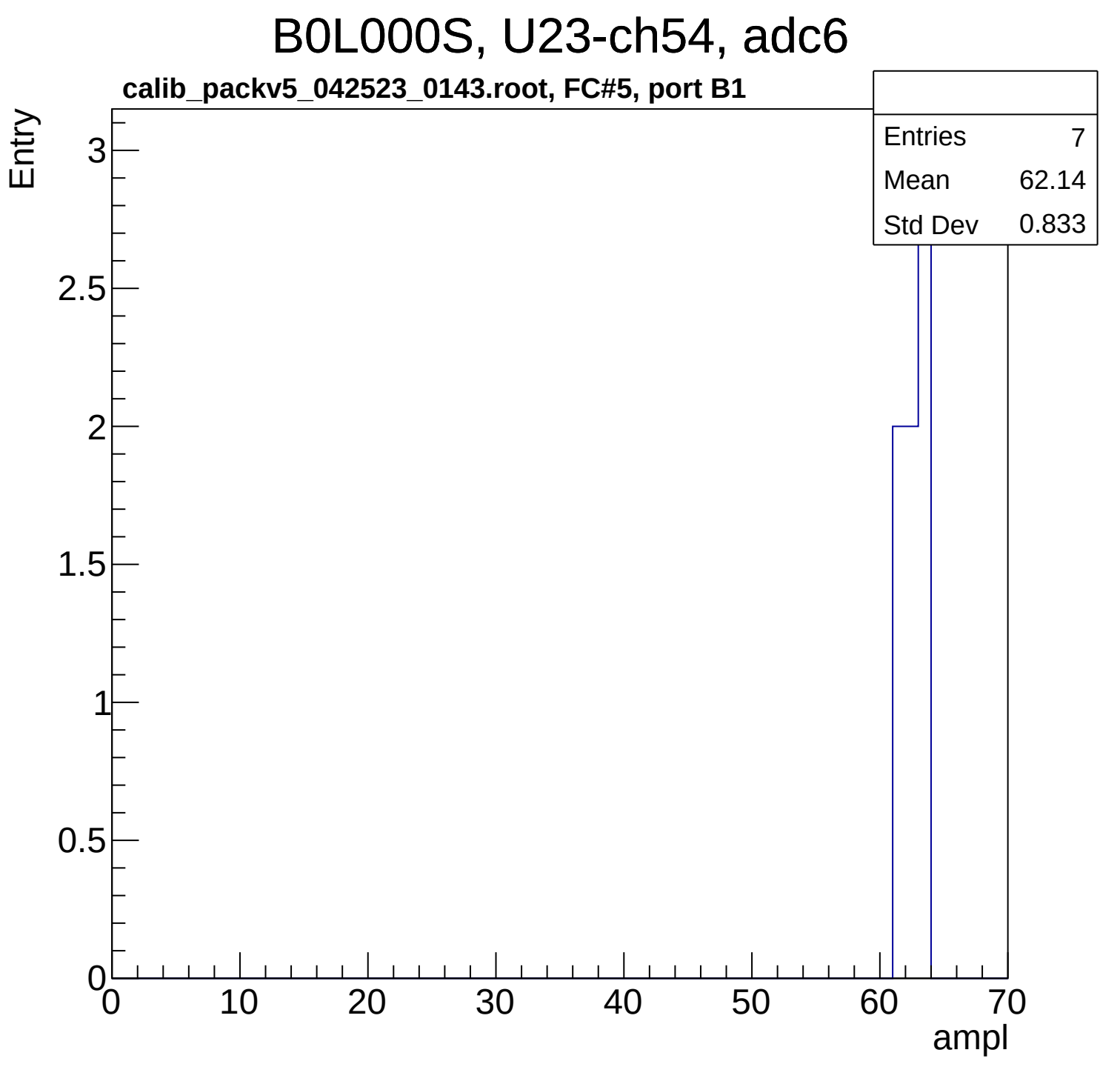

B0L000S, U23-ch54, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.38
Std Dev 2.979
Gaus mean : 38.1093
Gaus Width: 3.0007
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

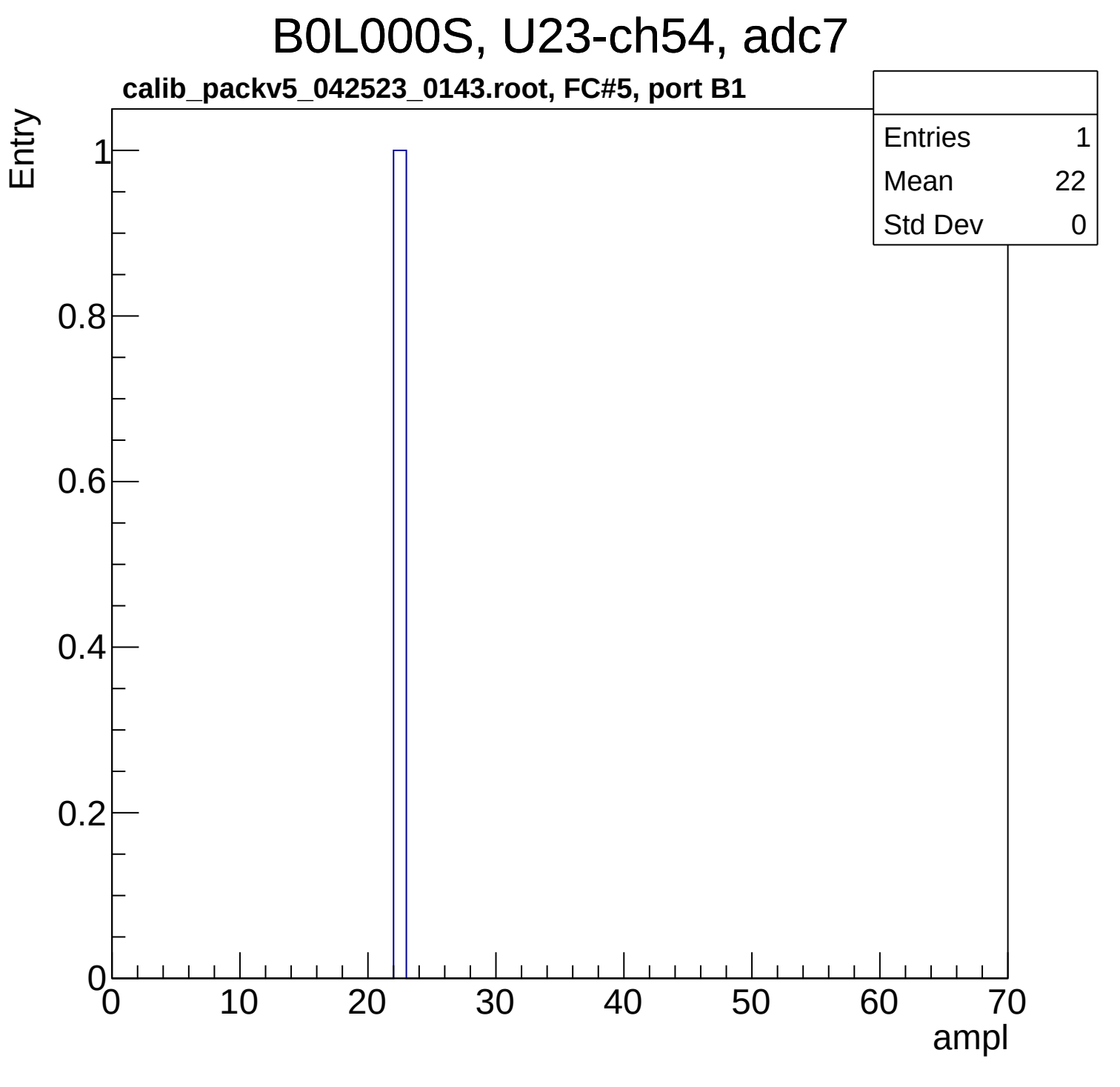

B0L000S, U23-ch54, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.03
Std Dev 3.162
Gaus mean : 44.0116
Gaus Width: 3.5914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

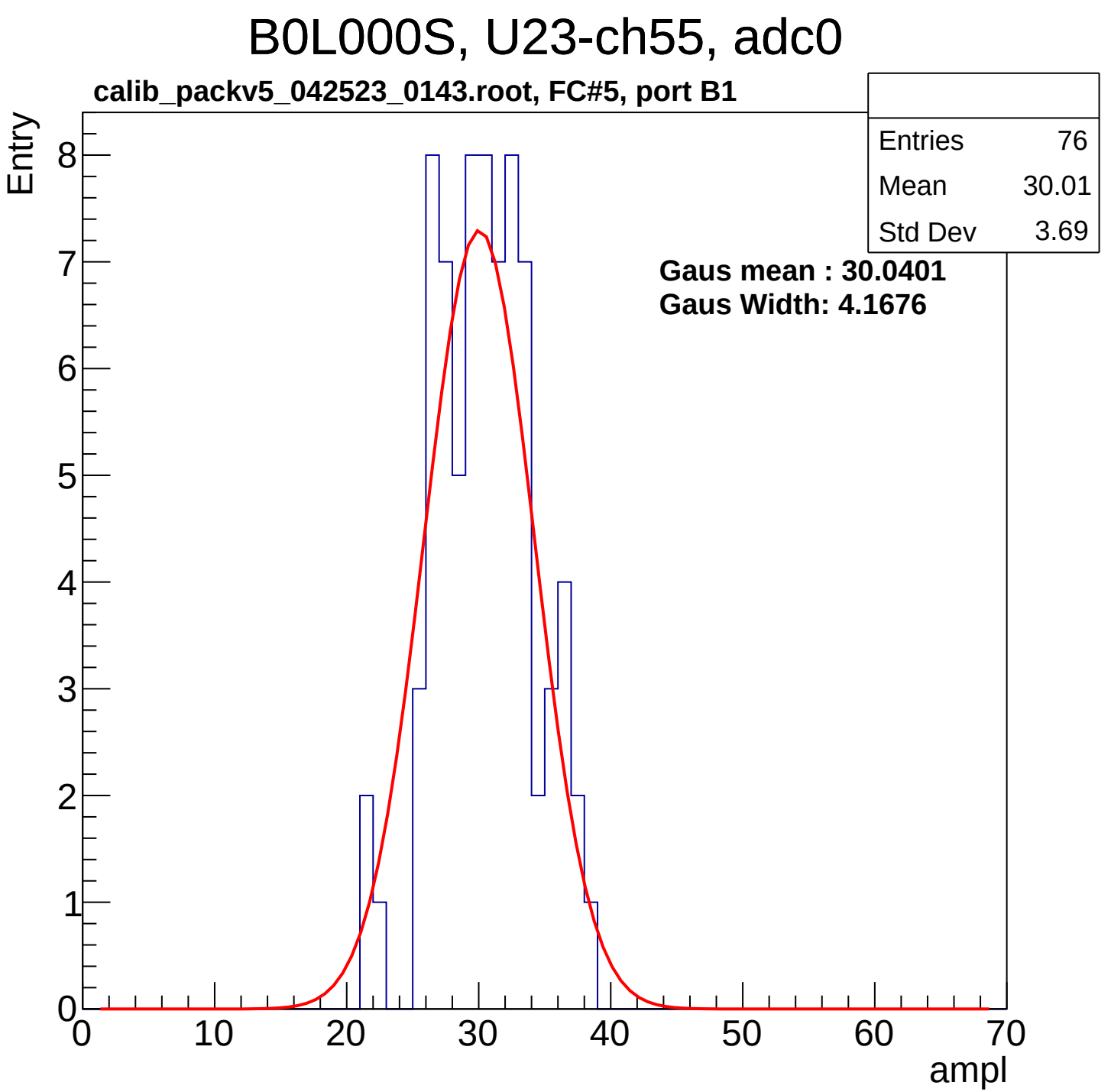

B0L000S, U23-ch54, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.55
Std Dev 2.978
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

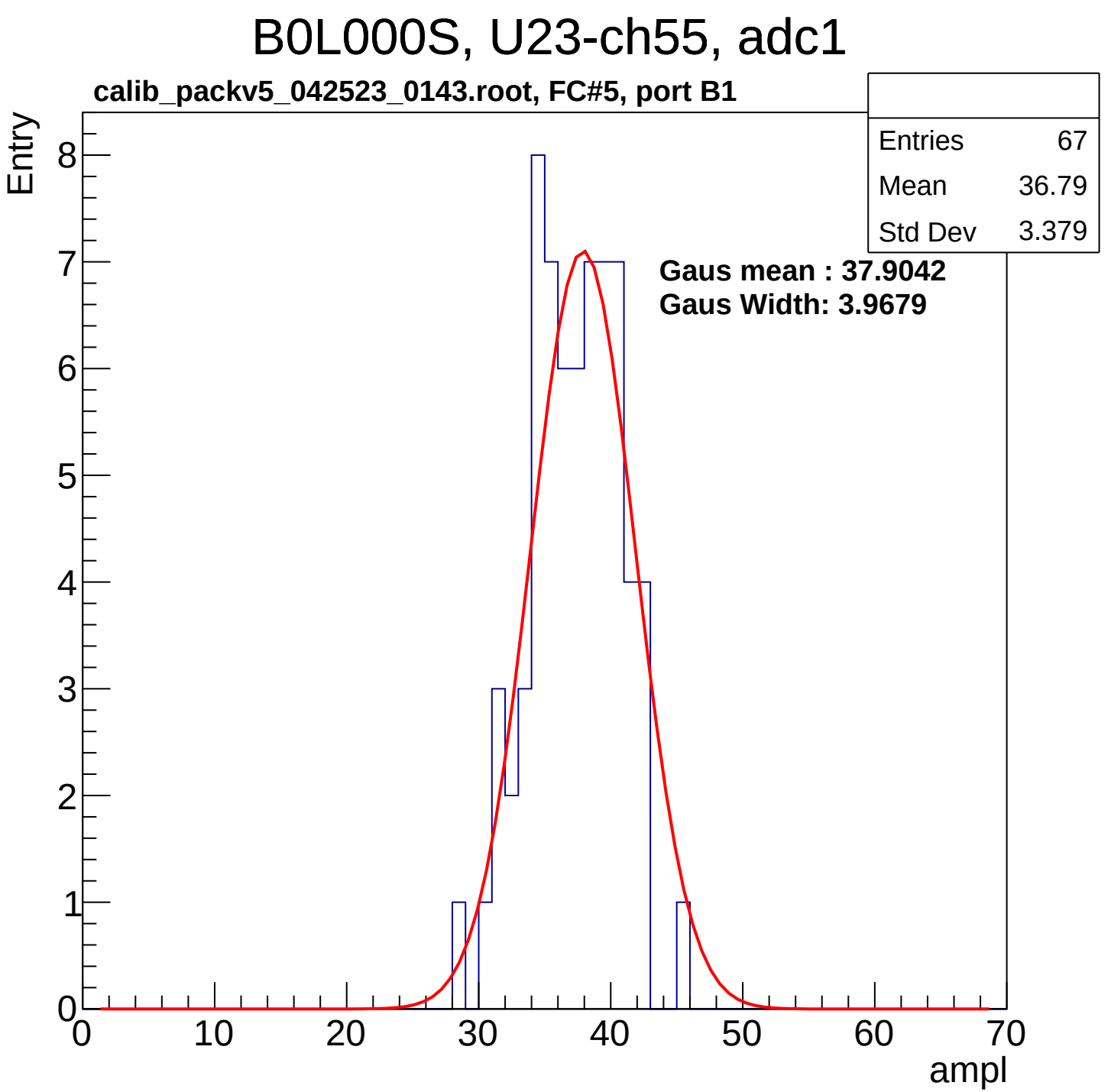

B0L000S, U23-ch54, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.31
Std Dev 3.262
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

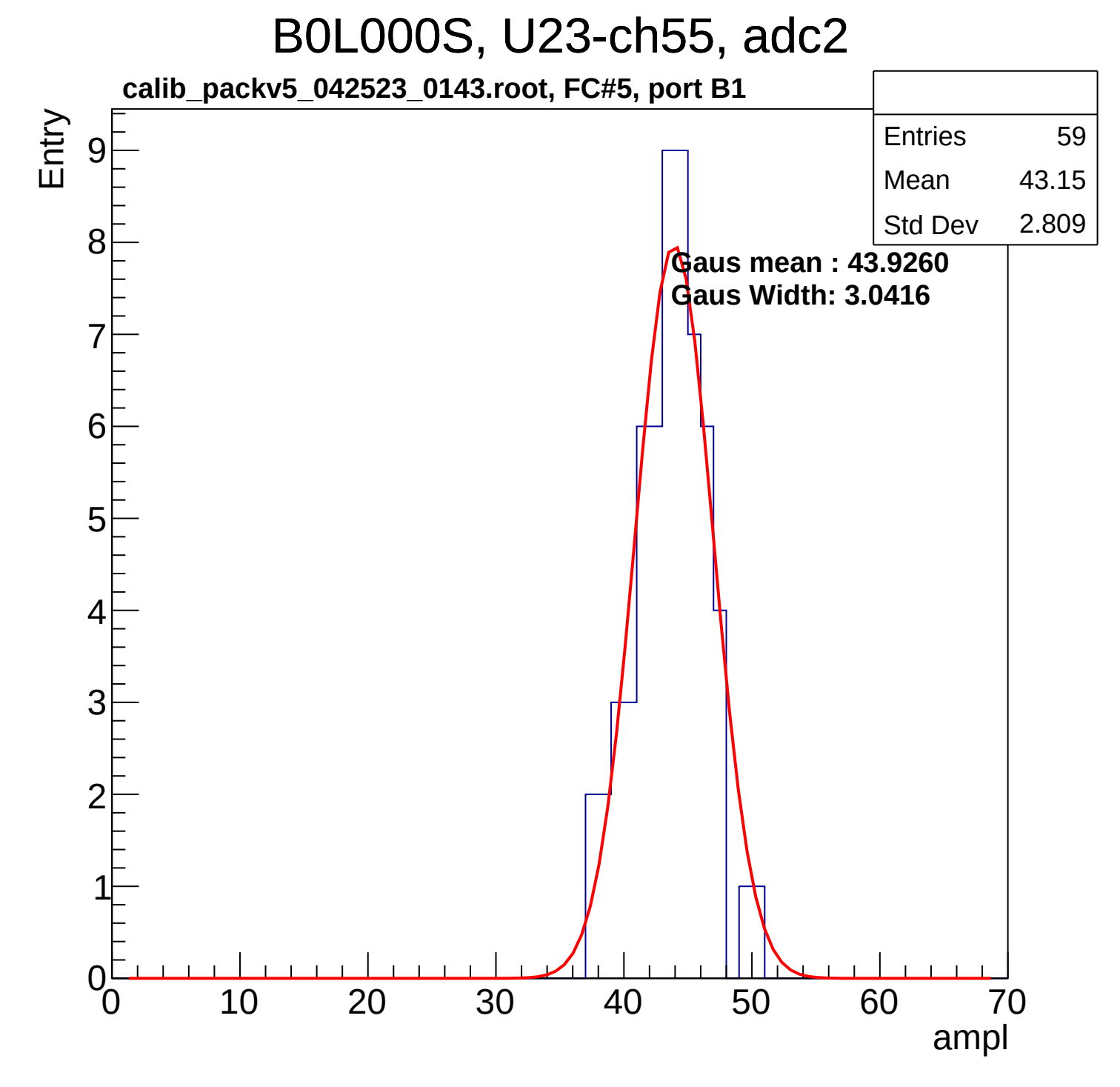

B0L000S, U23-ch54, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.14
Std Dev 8.643
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

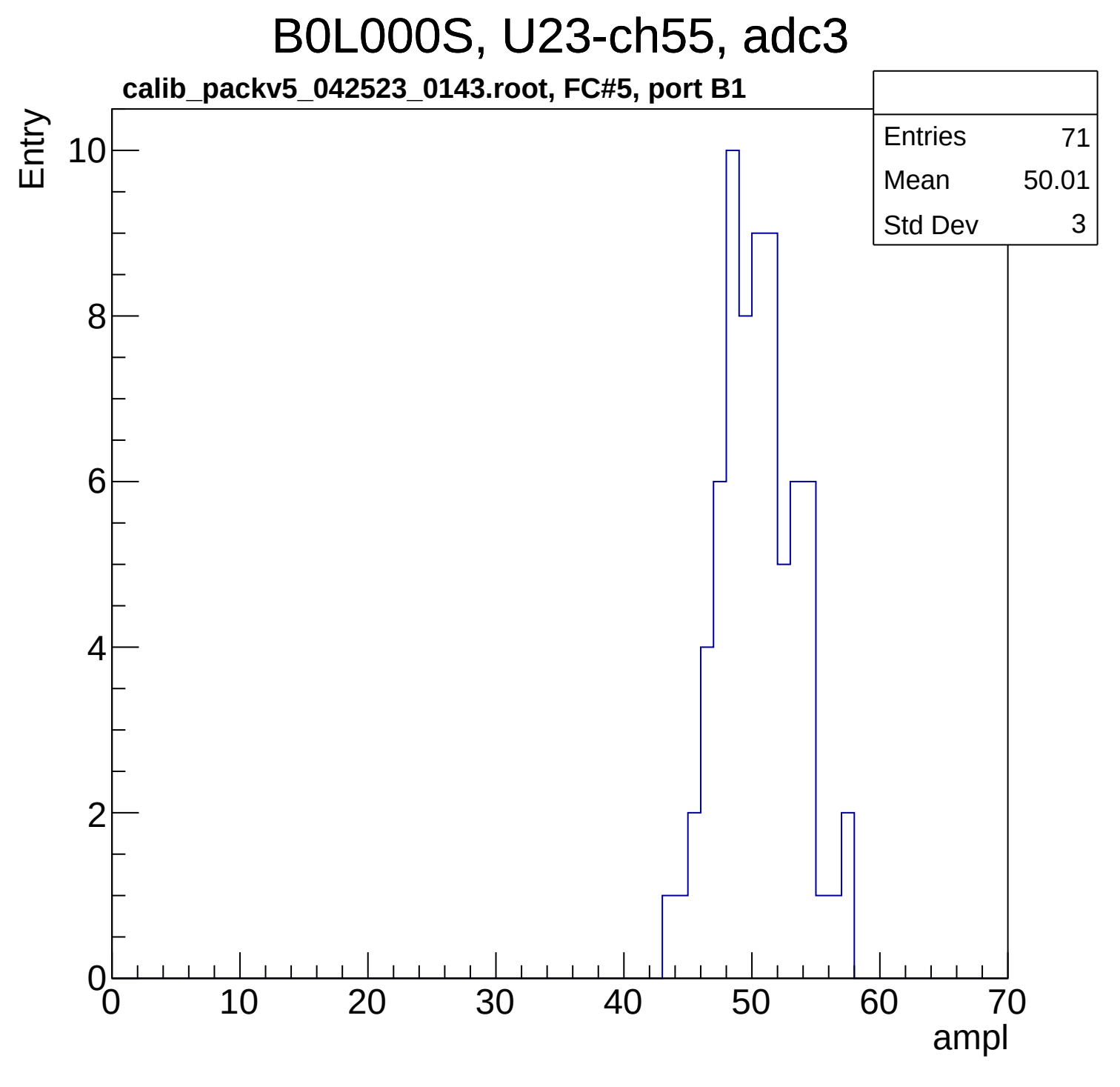

B0L000S, U23-ch54, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

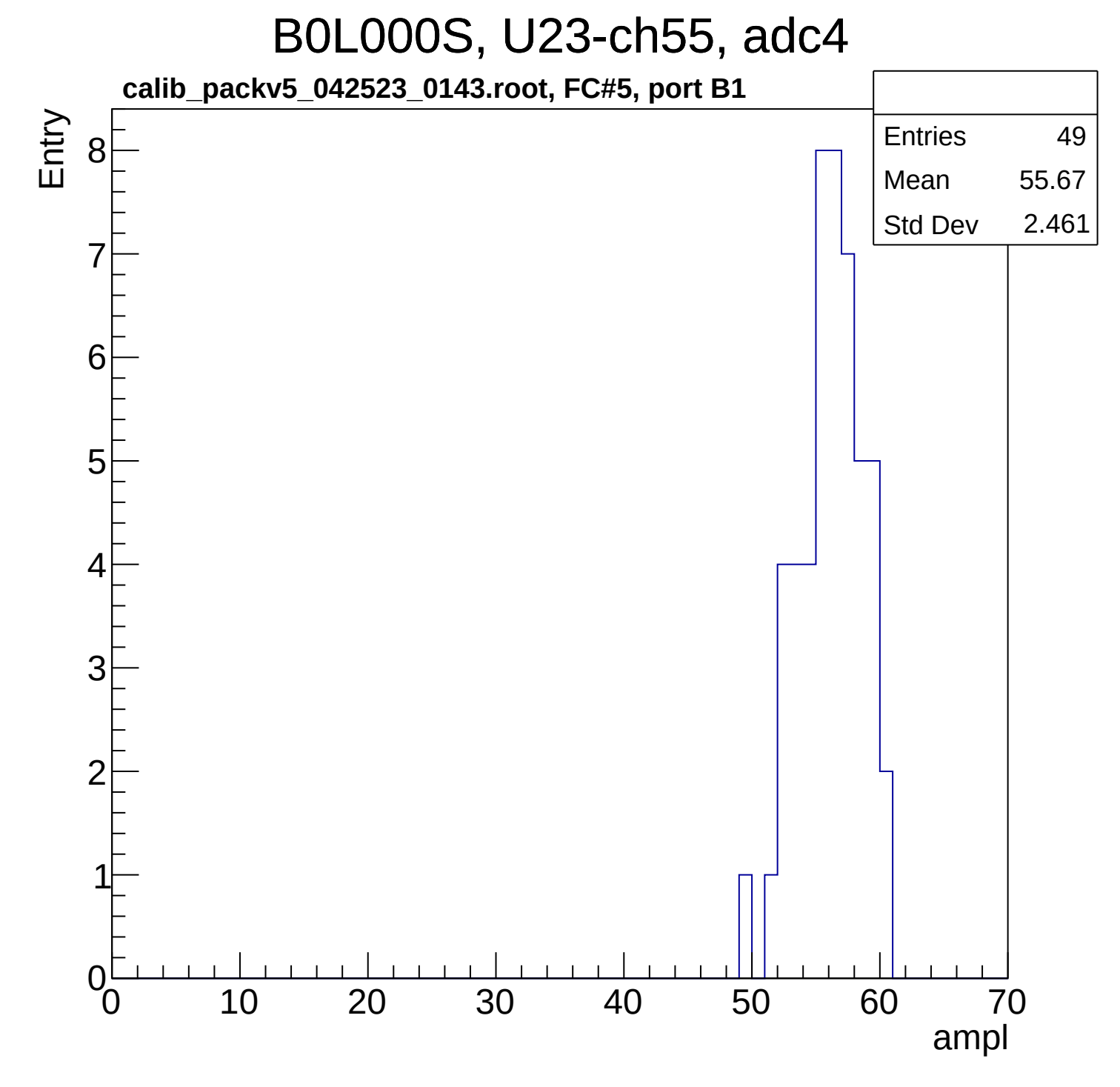

B0L000S, U23-ch54, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

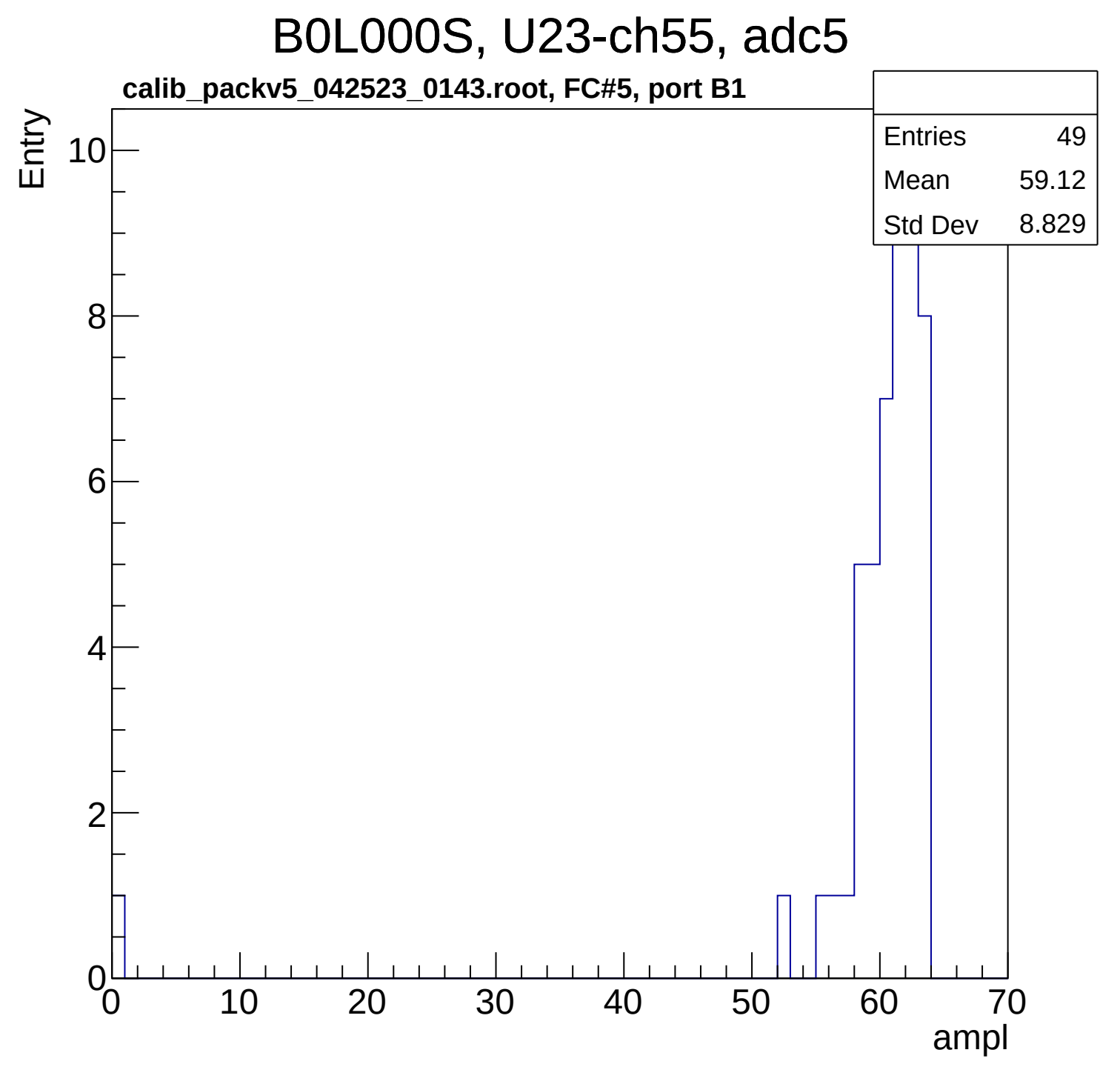

B0L000S, U23-ch55, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 30.01
Std Dev 3.69
Gaus mean : 30.0401
Gaus Width: 4.1676
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

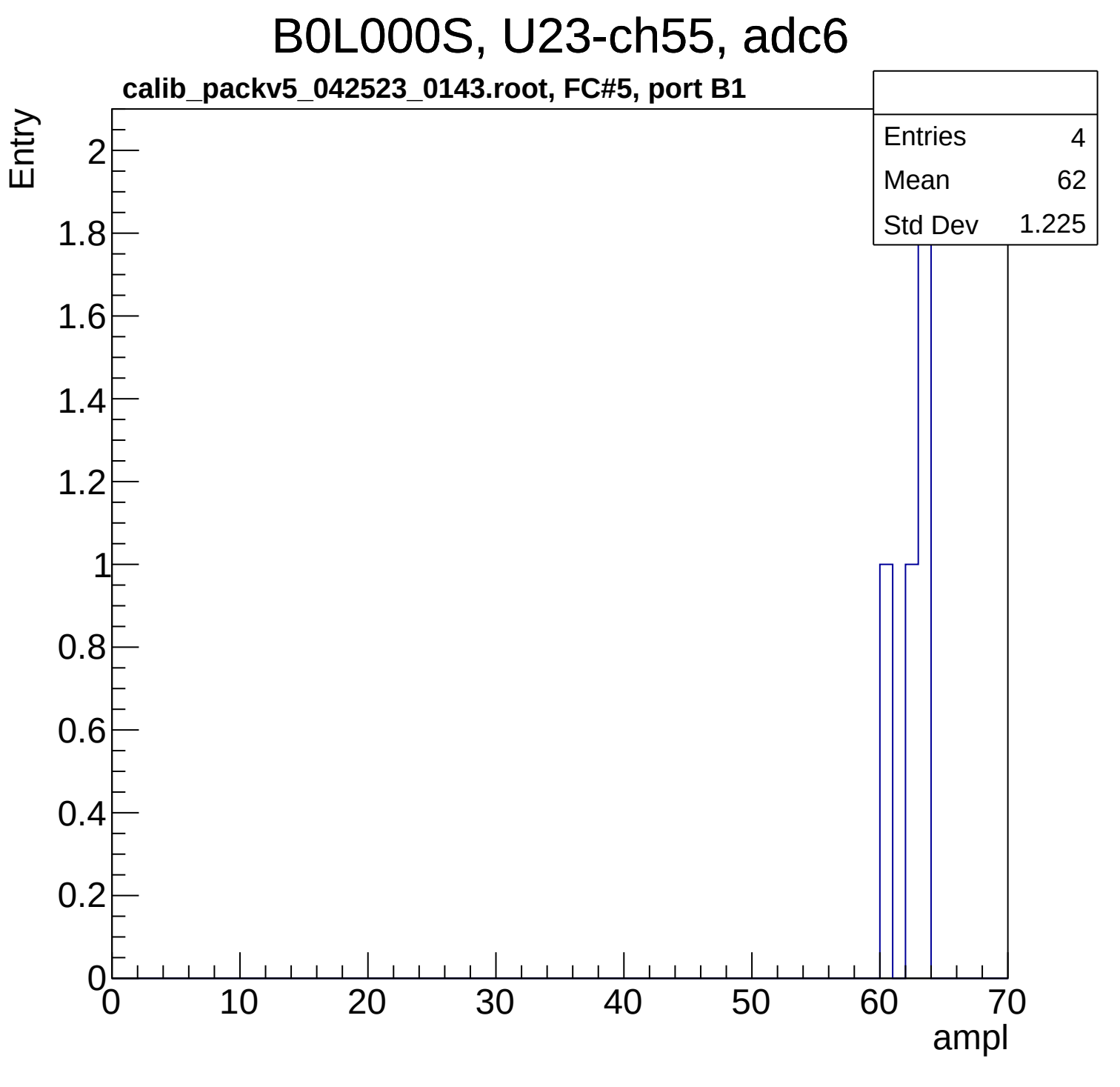

B0L000S, U23-ch55, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.79
Std Dev 3.379
Gaus mean : 37.9042
Gaus Width: 3.9679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

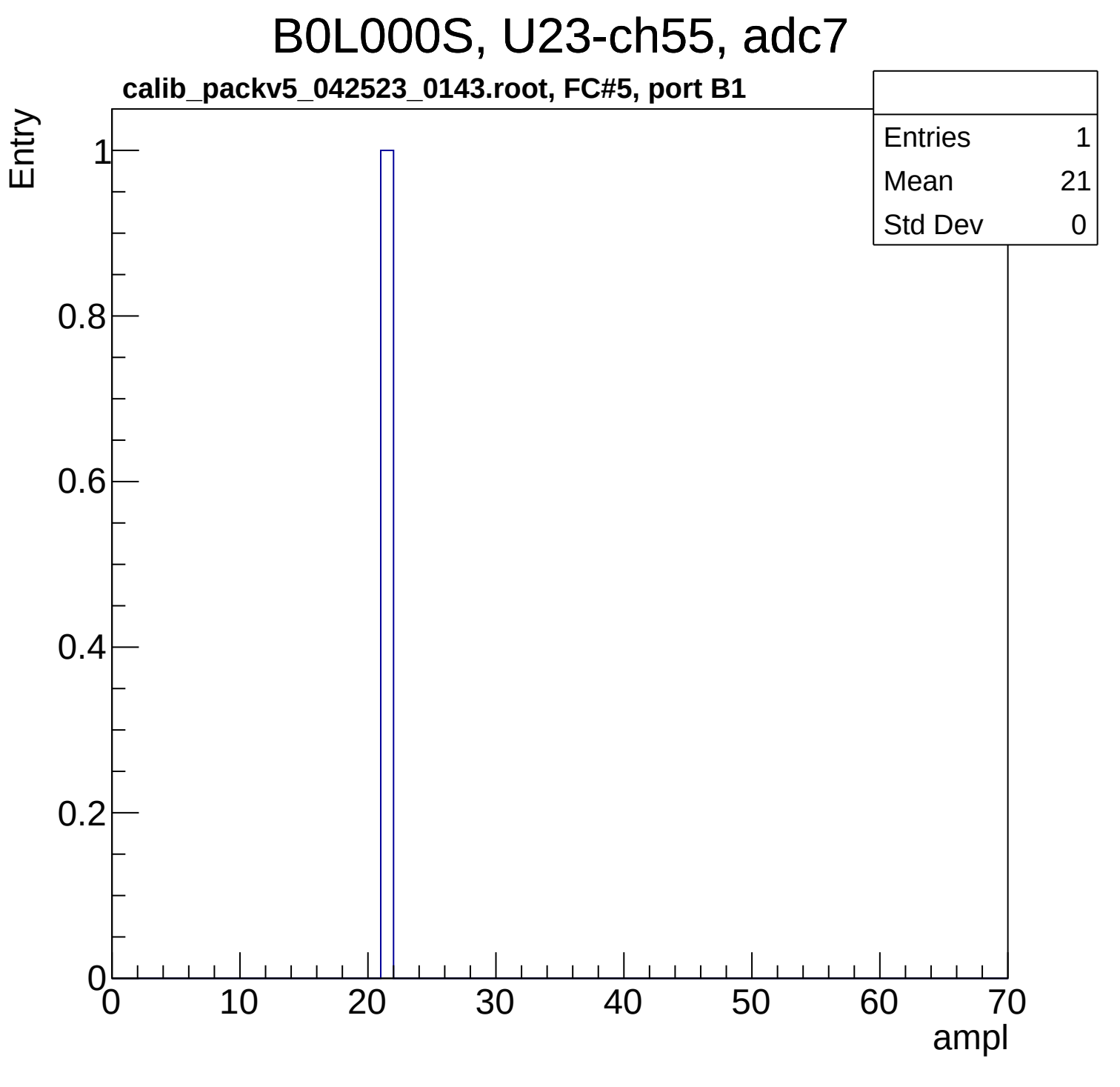

B0L000S, U23-ch55, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.15
Std Dev 2.809
Gaus mean : 43.9260
Gaus Width: 3.0416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

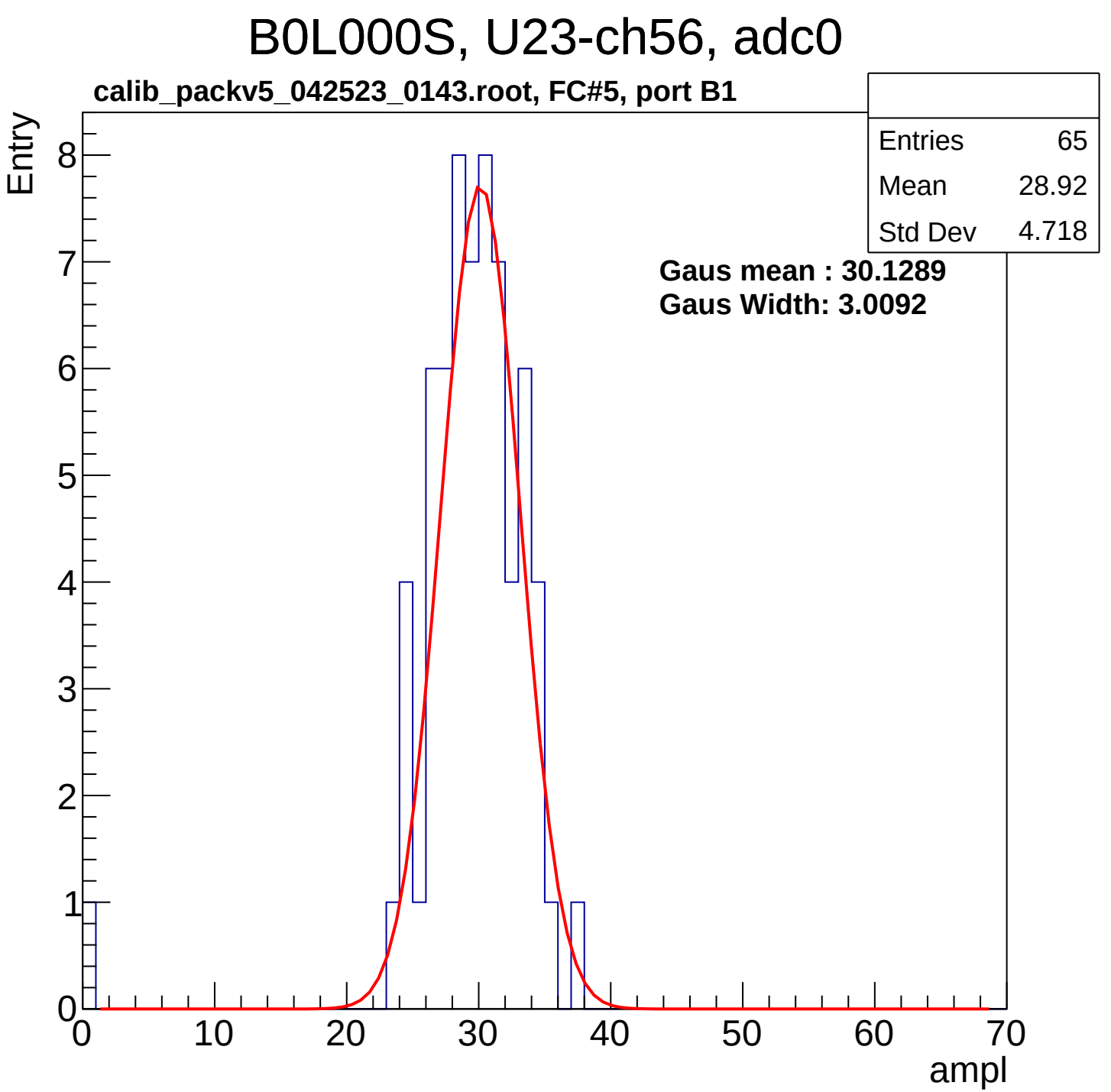

B0L000S, U23-ch55, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.01
Std Dev 3
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

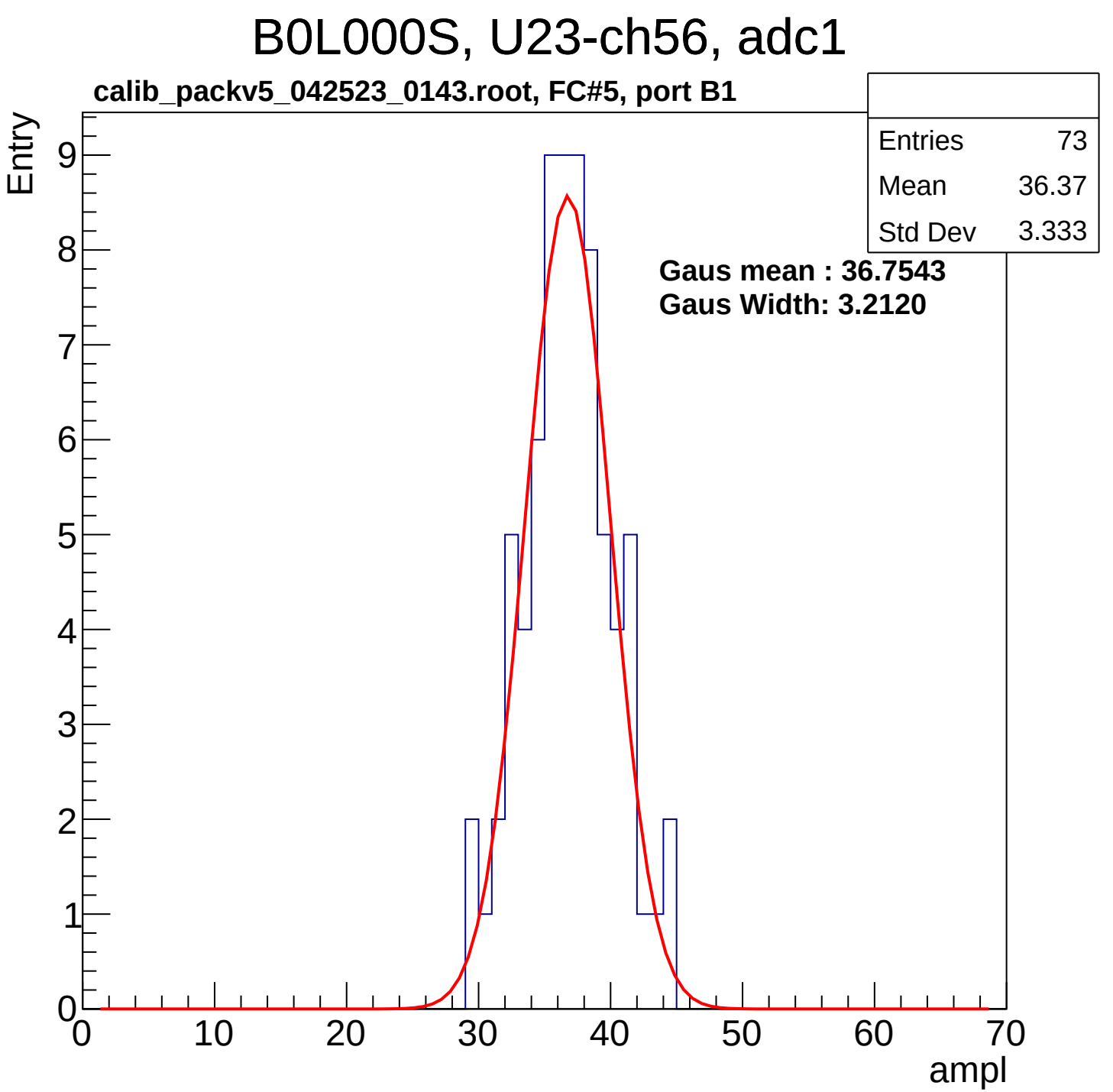

B0L000S, U23-ch55, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.67
Std Dev 2.461
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

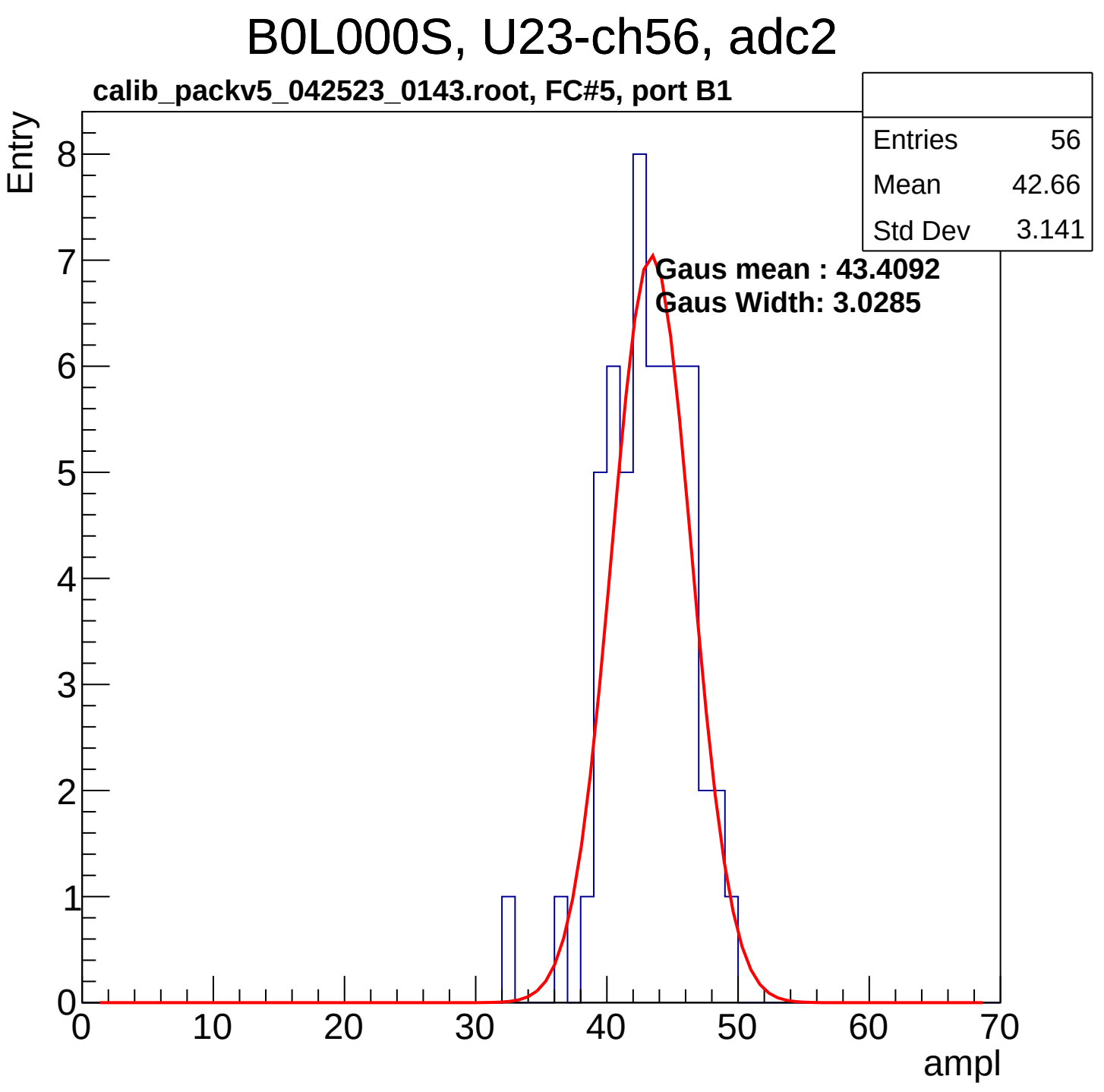

B0L000S, U23-ch55, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 59.12
Std Dev 8.829
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

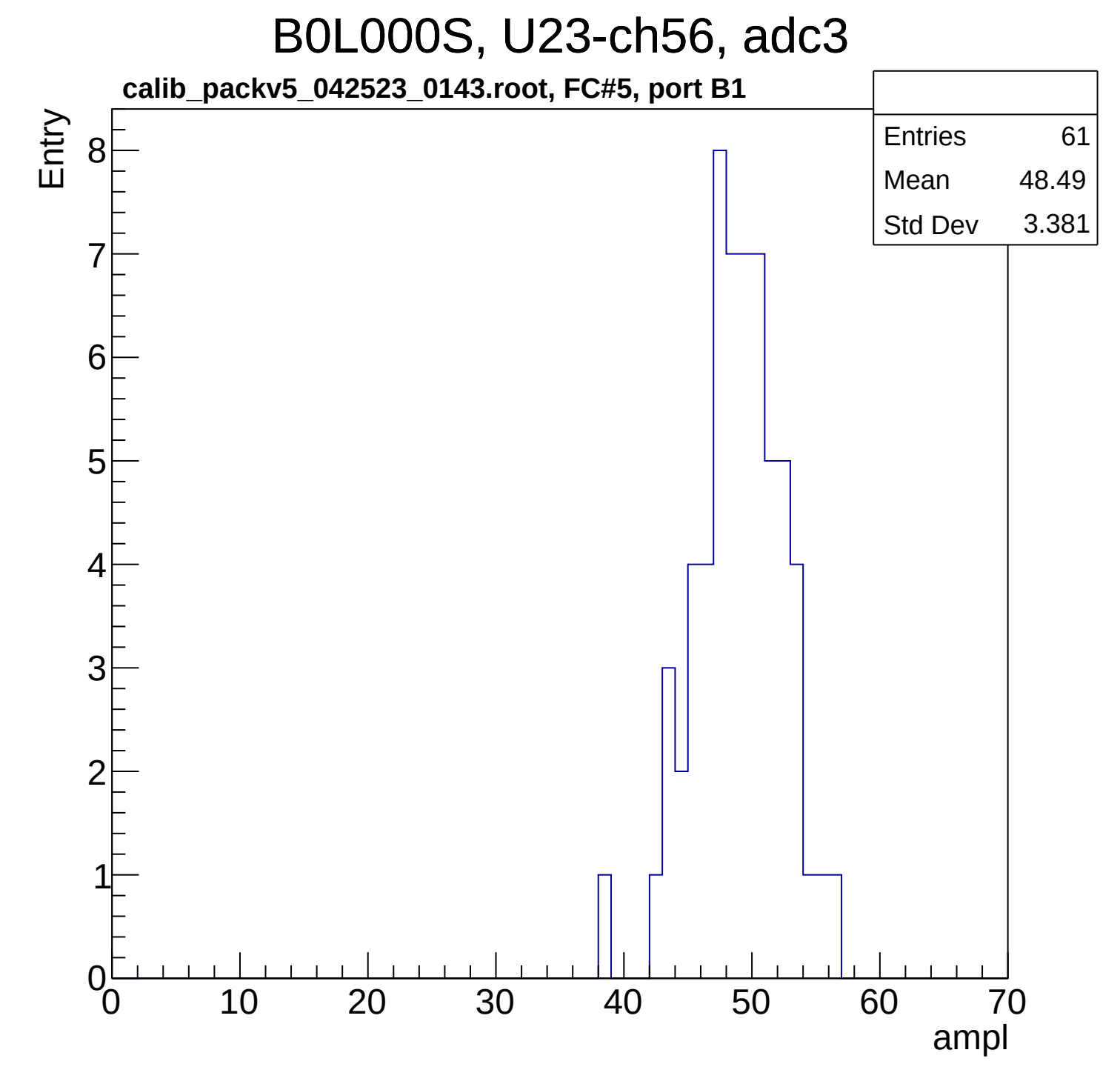

B0L000S, U23-ch55, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

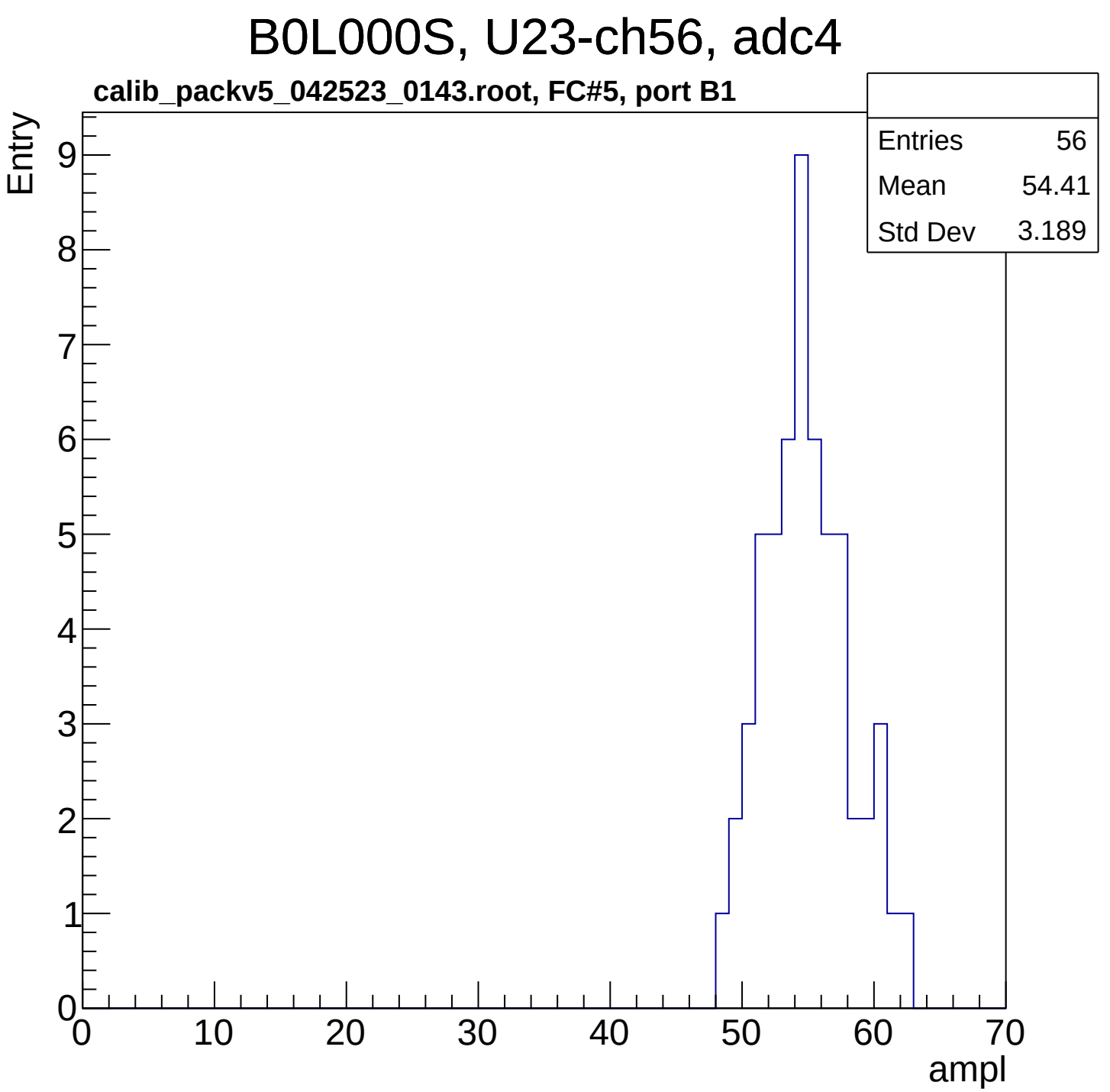

B0L000S, U23-ch55, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

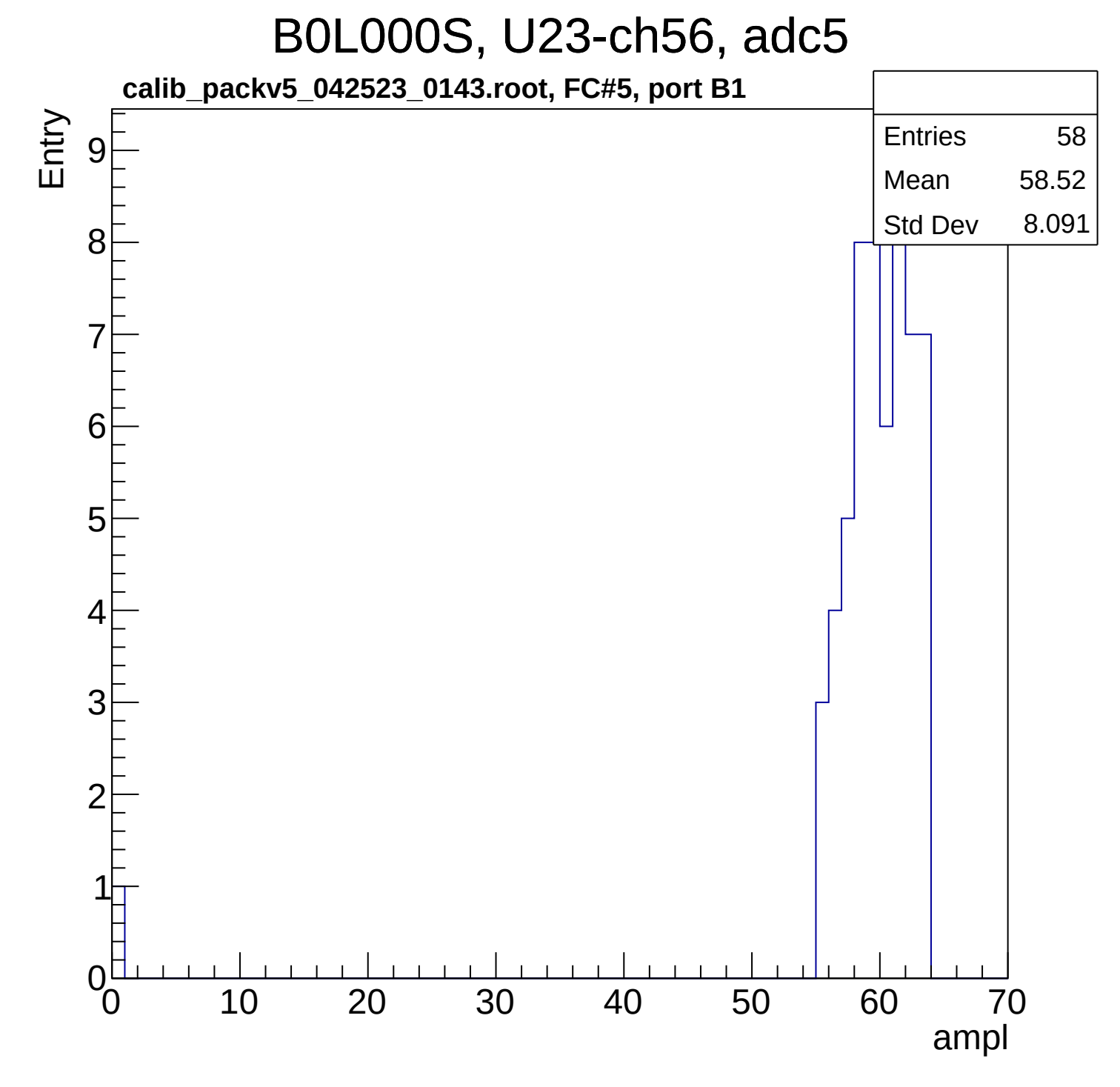

B0L000S, U23-ch56, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.92
Std Dev 4.718
Gaus mean : 30.1289
Gaus Width: 3.0092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

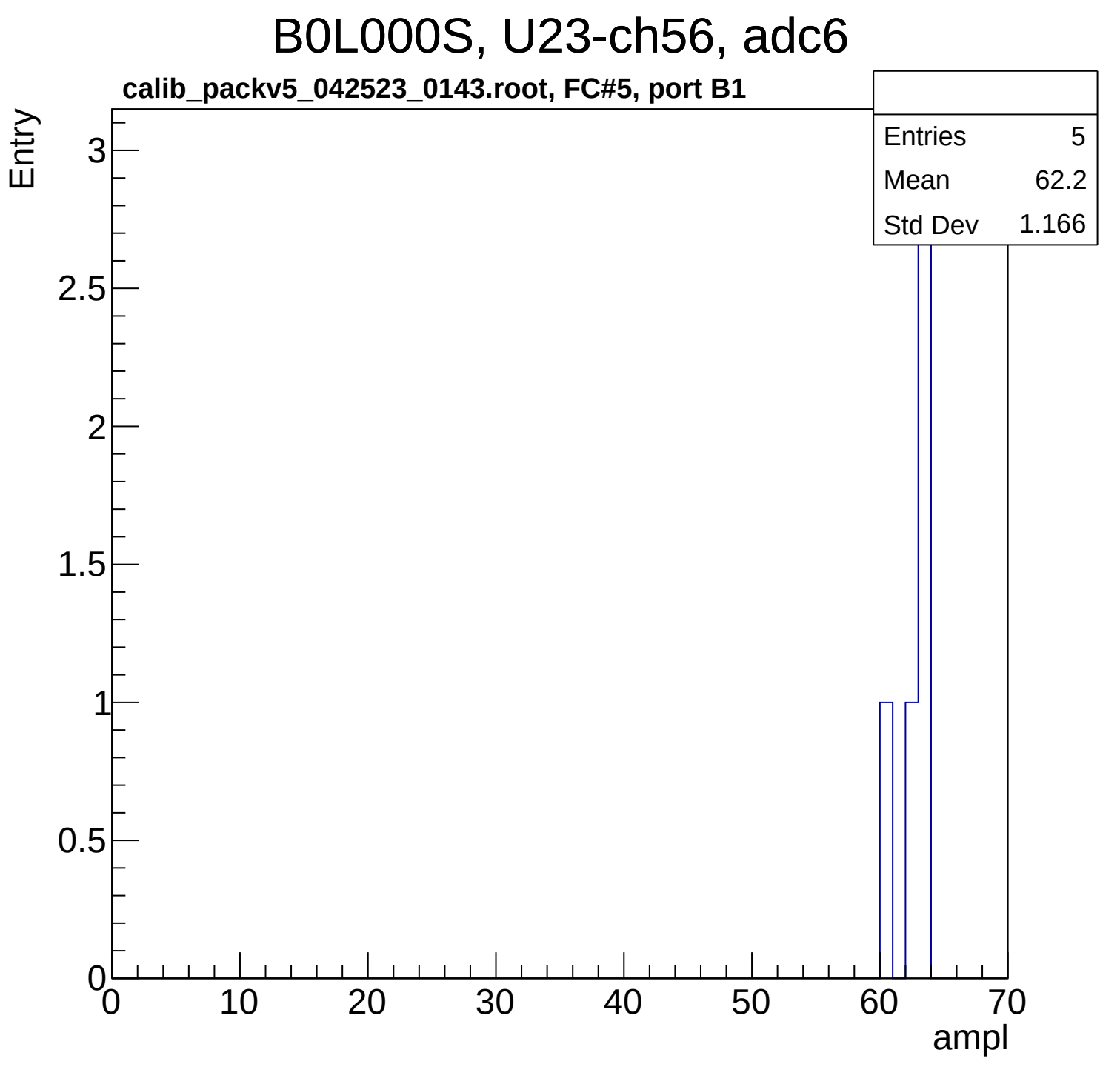

B0L000S, U23-ch56, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.37
Std Dev 3.333
Gaus mean : 36.7543
Gaus Width: 3.2120
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

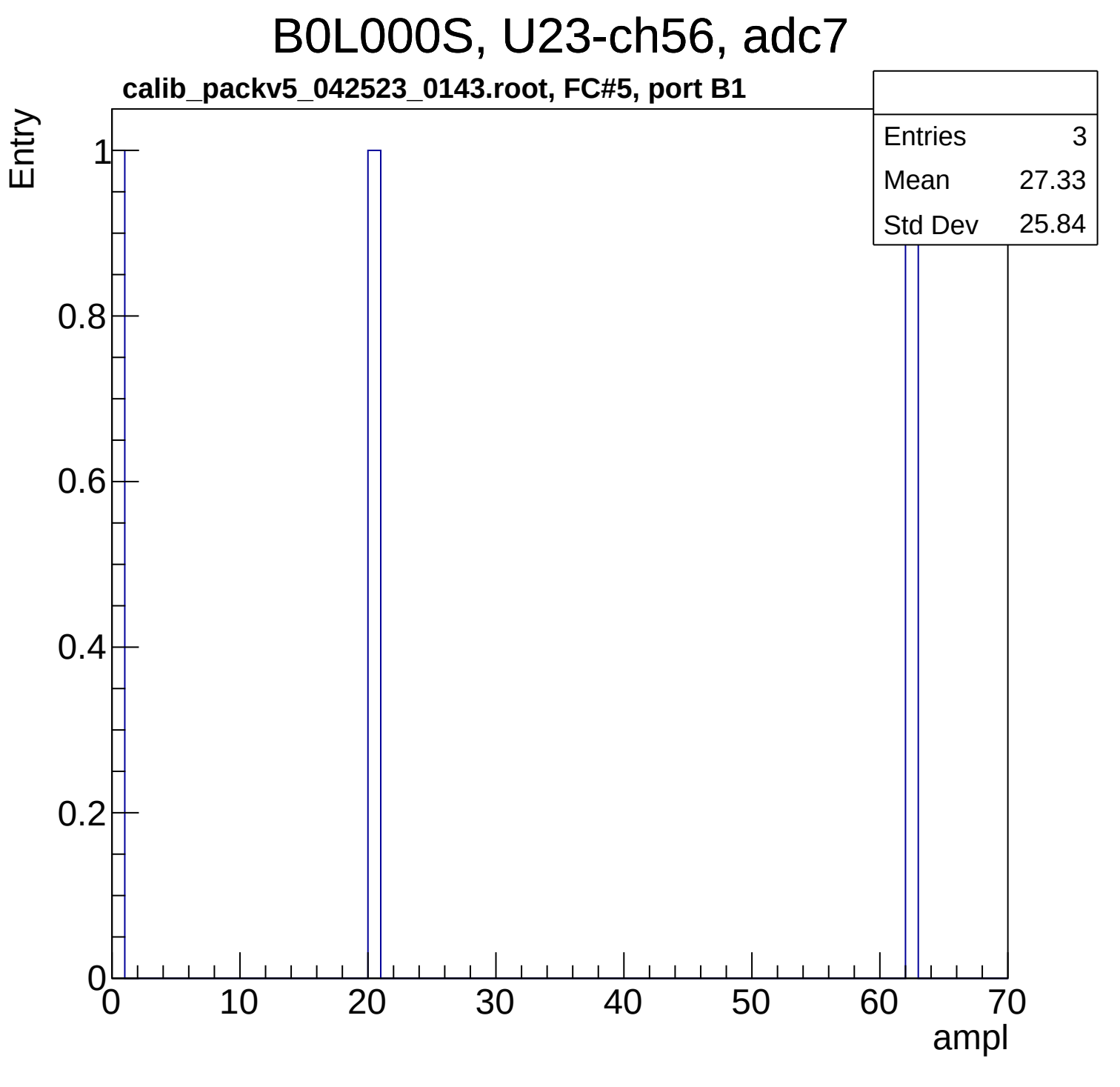

B0L000S, U23-ch56, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.66
Std Dev 3.141
Gaus mean : 43.4092
Gaus Width: 3.0285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

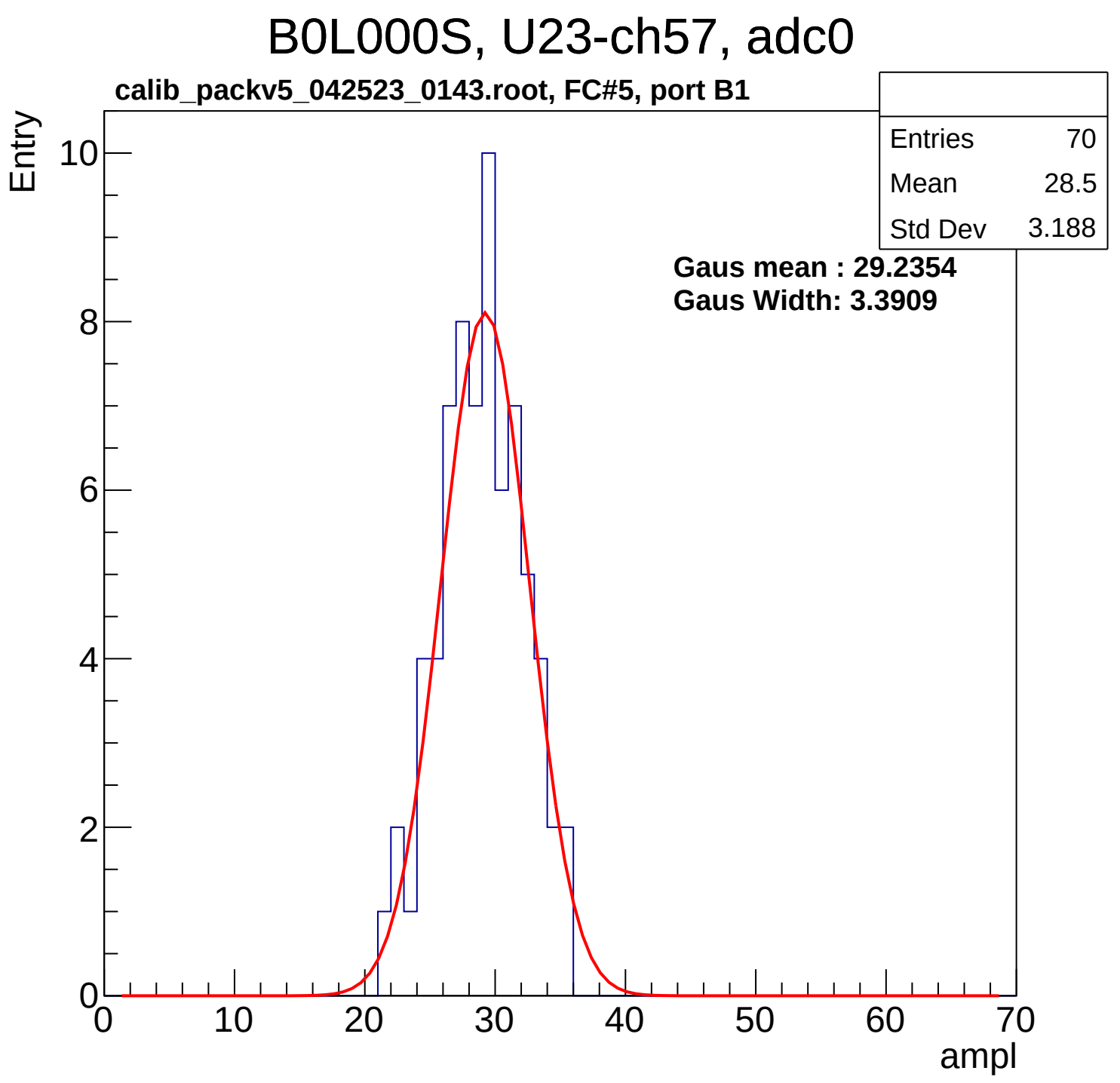

B0L000S, U23-ch56, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.49
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

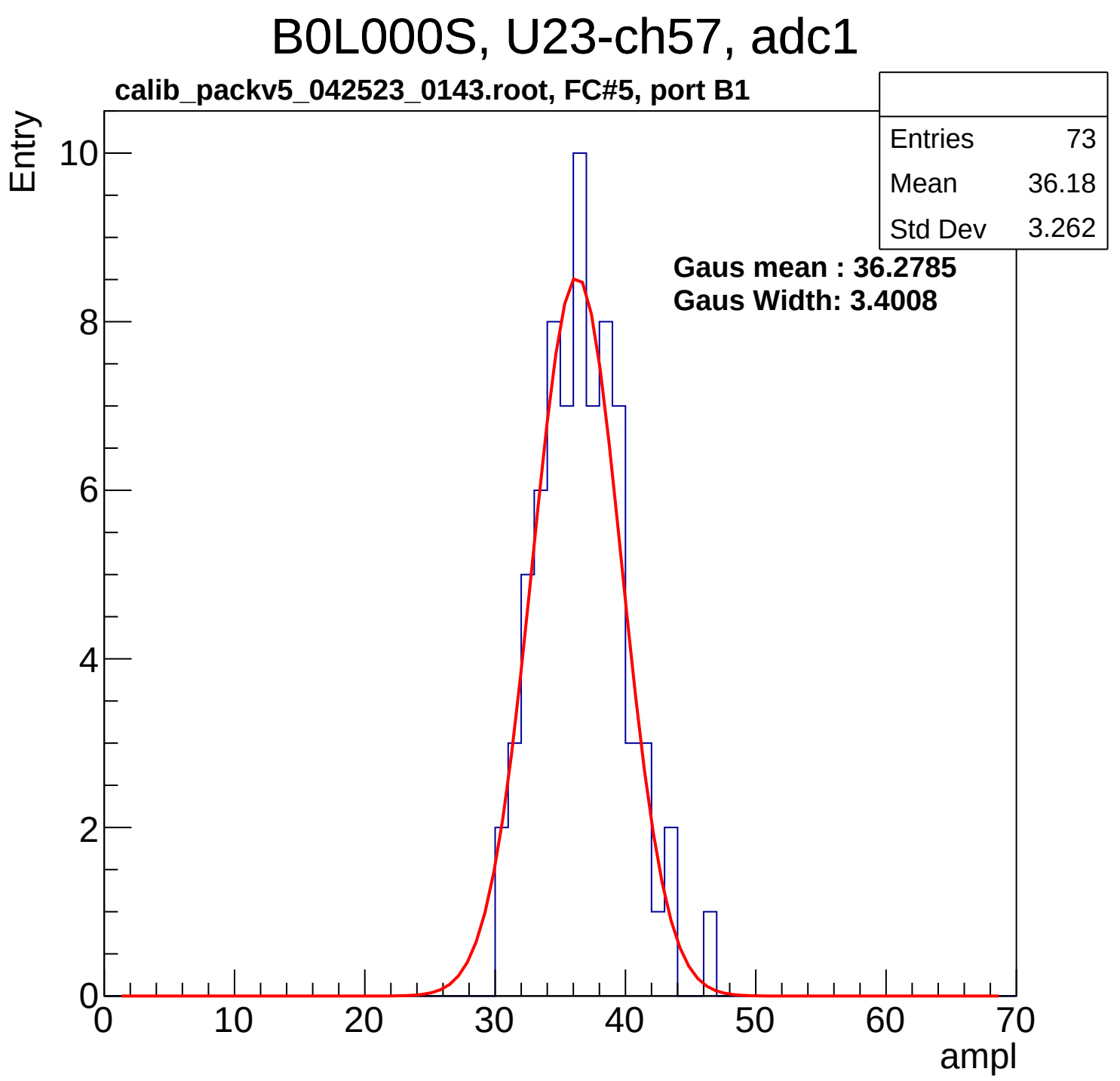

B0L000S, U23-ch56, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.41
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

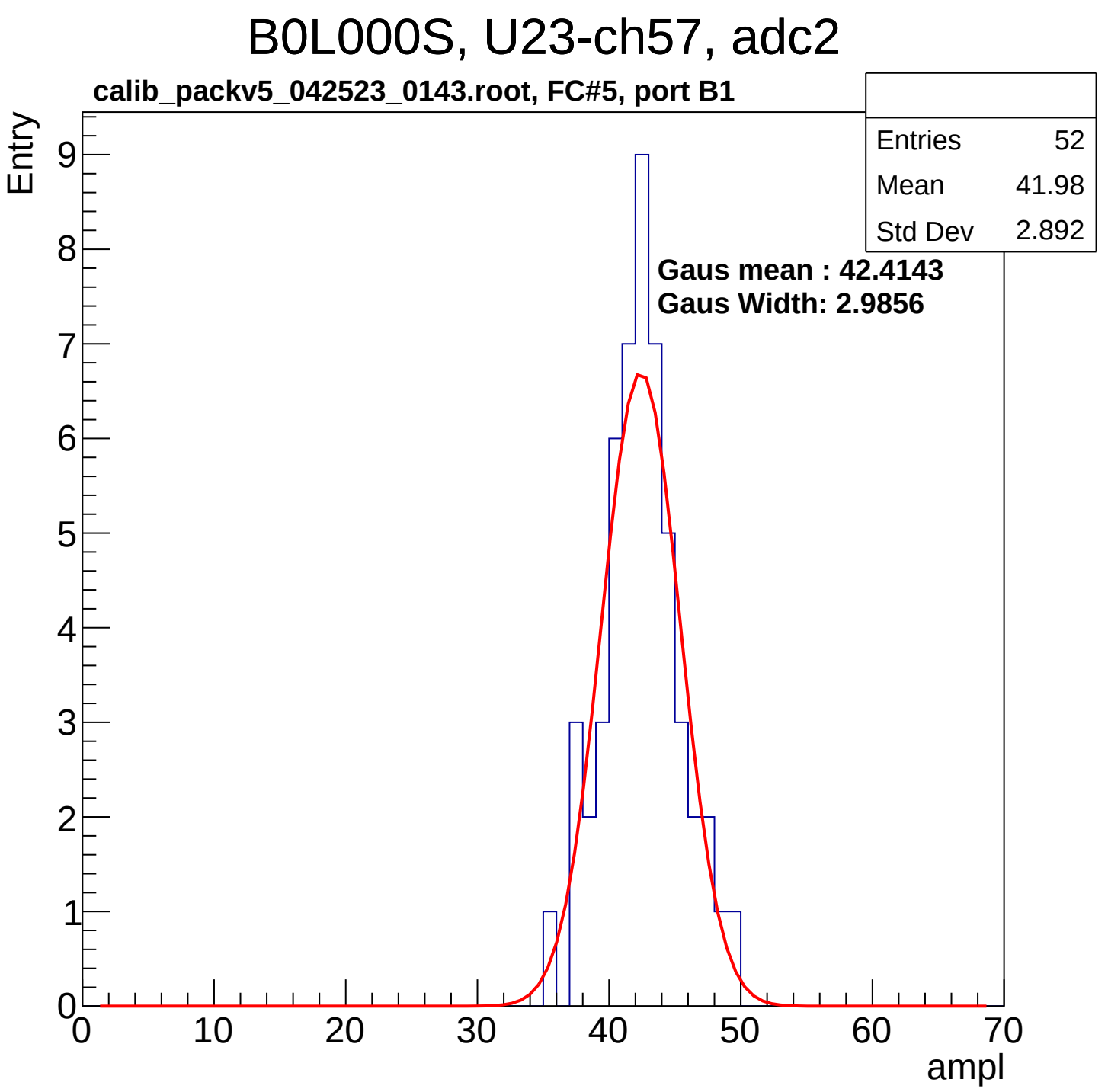

B0L000S, U23-ch56, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.52
Std Dev 8.091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

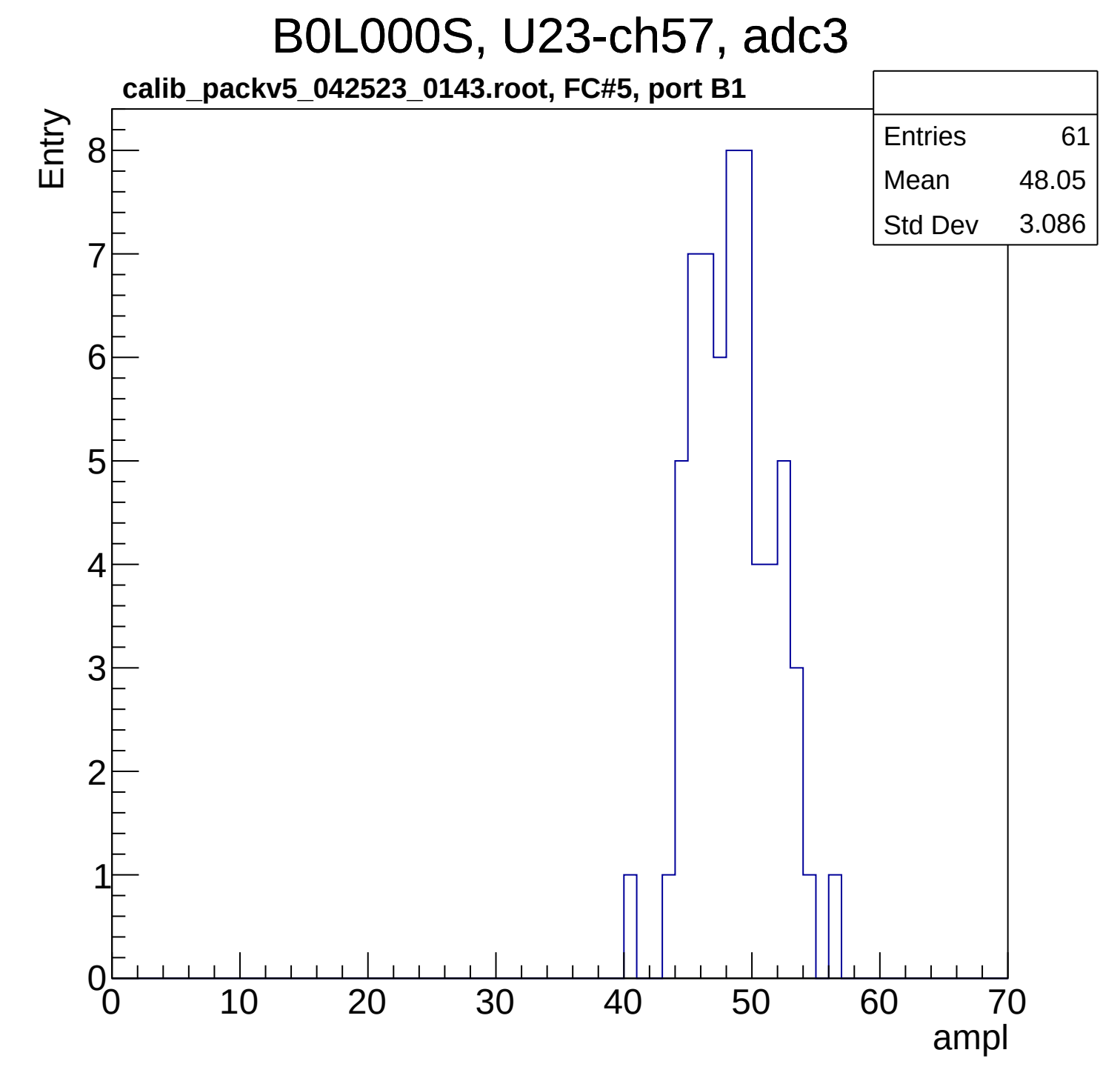

B0L000S, U23-ch56, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

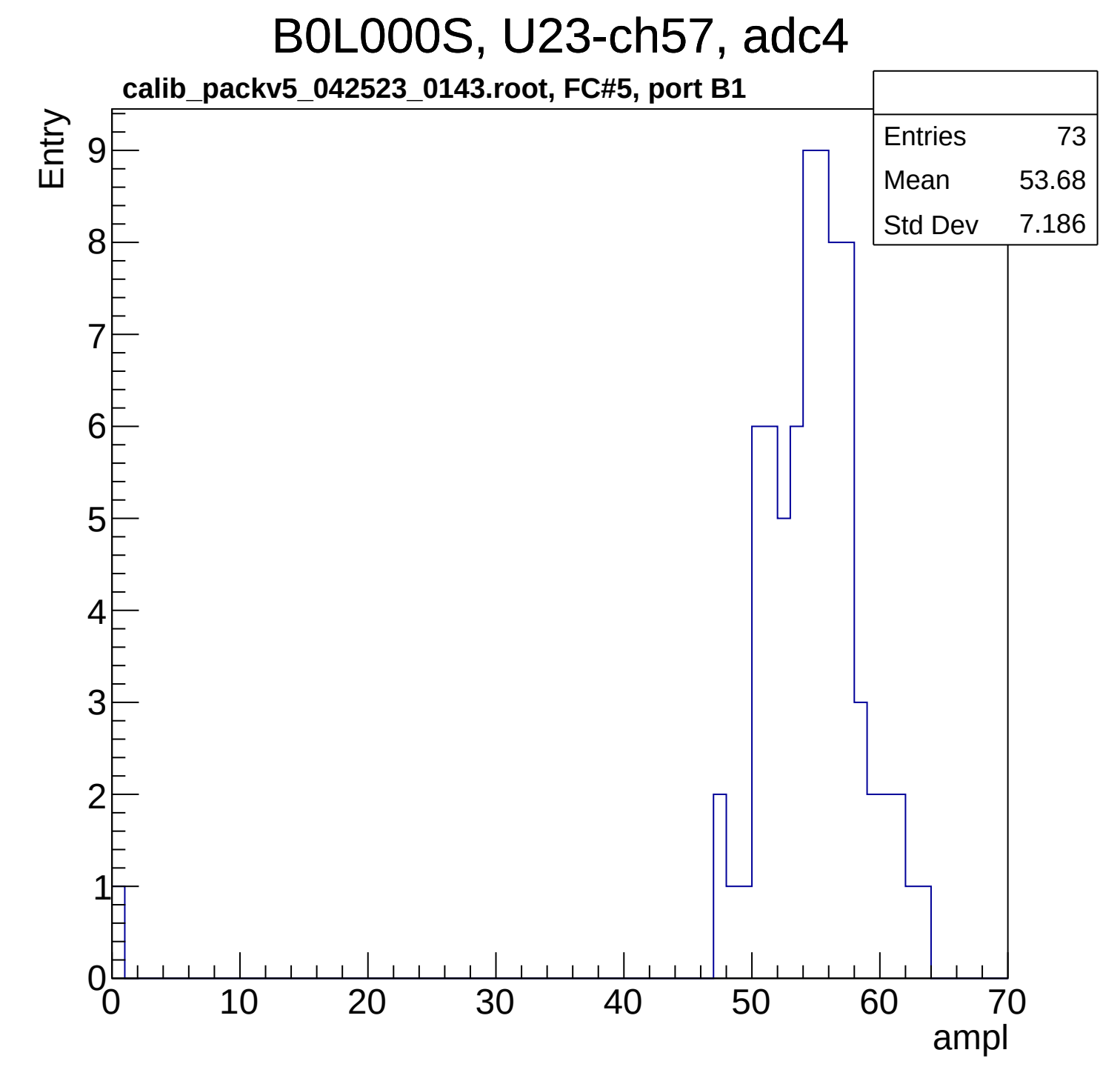

B0L000S, U23-ch56, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

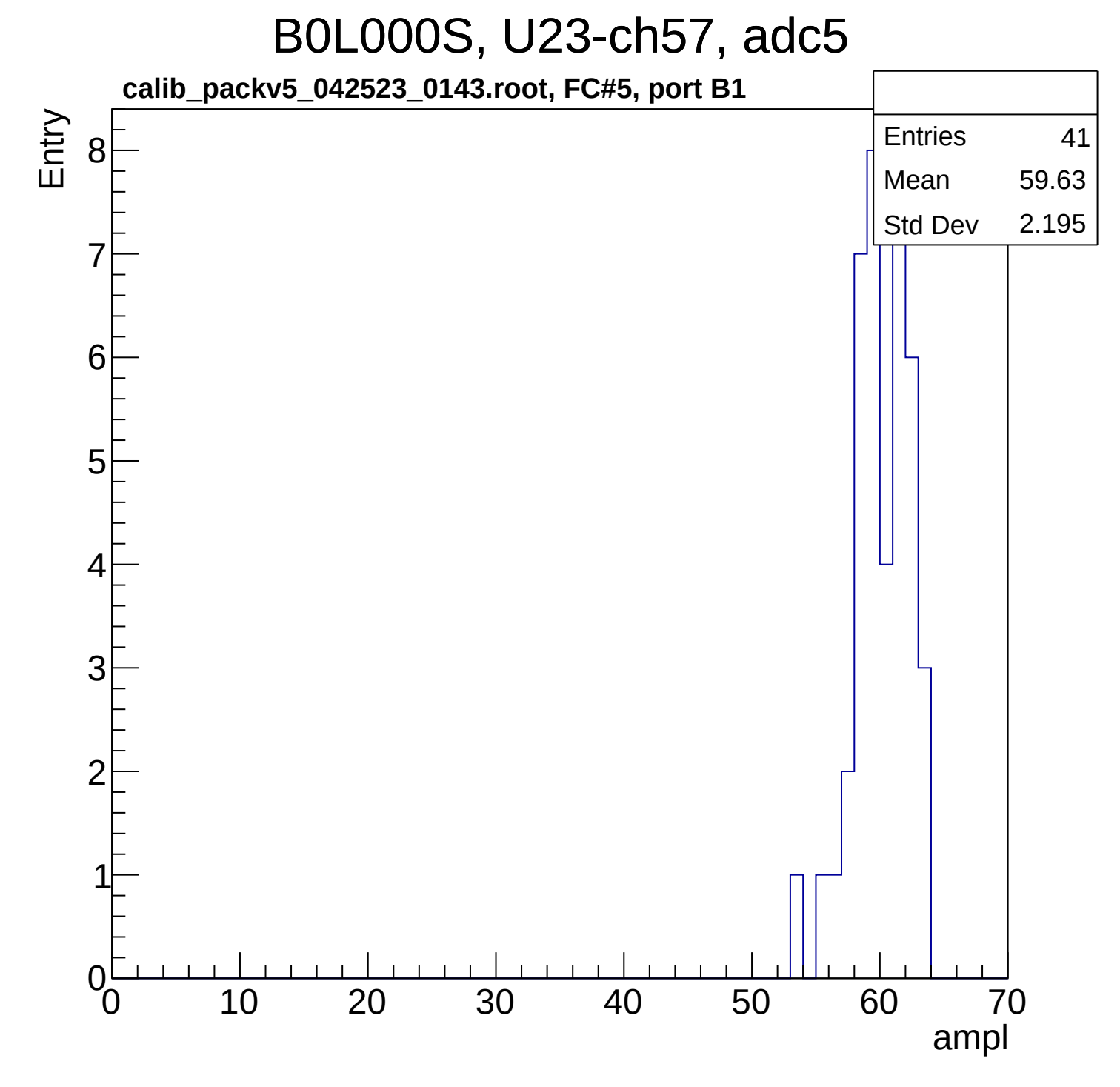

B0L000S, U23-ch57, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.5
Std Dev 3.188
Gaus mean : 29.2354
Gaus Width: 3.3909
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

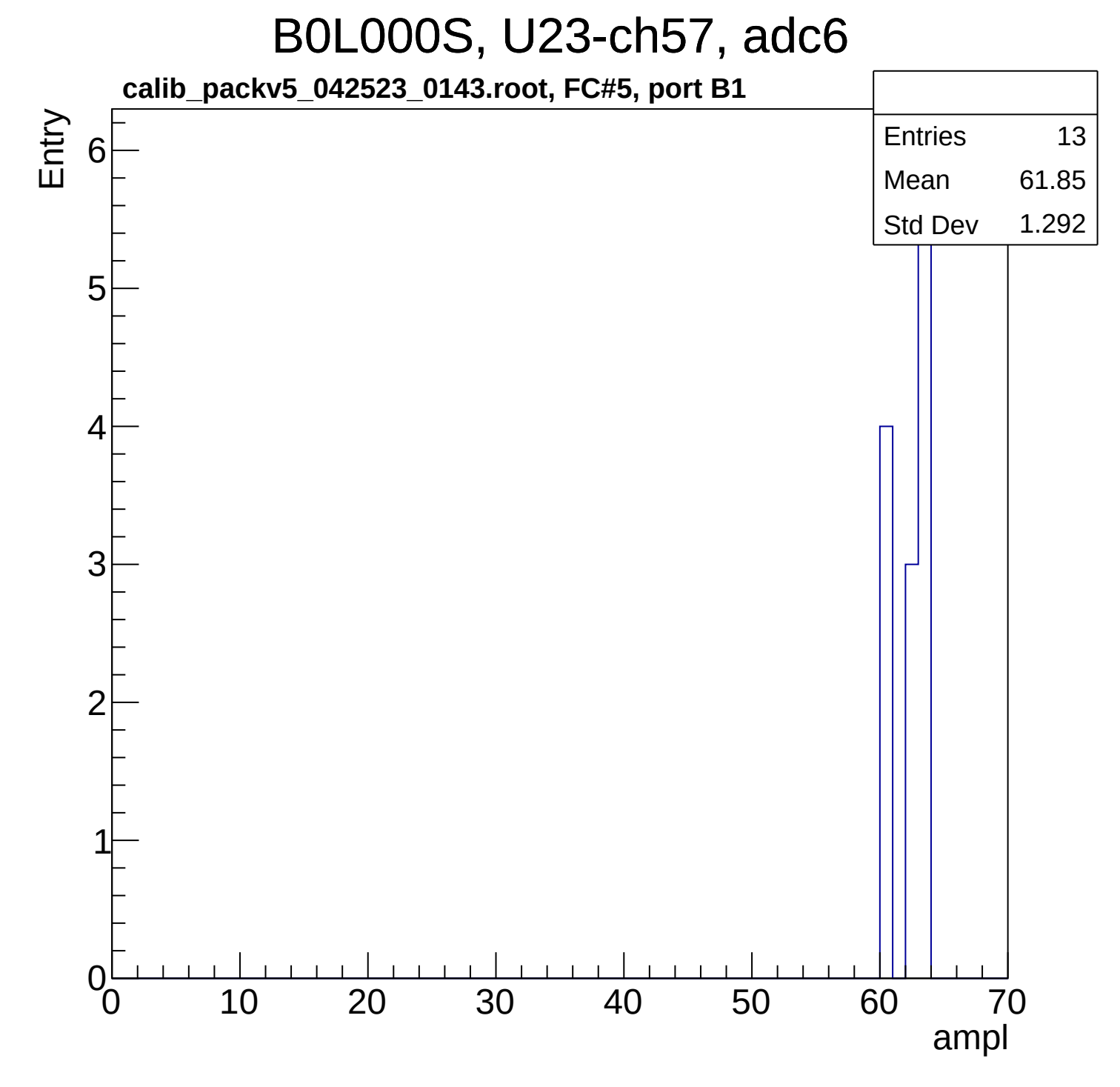

B0L000S, U23-ch57, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.18
Std Dev 3.262
Gaus mean : 36.2785
Gaus Width: 3.4008
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

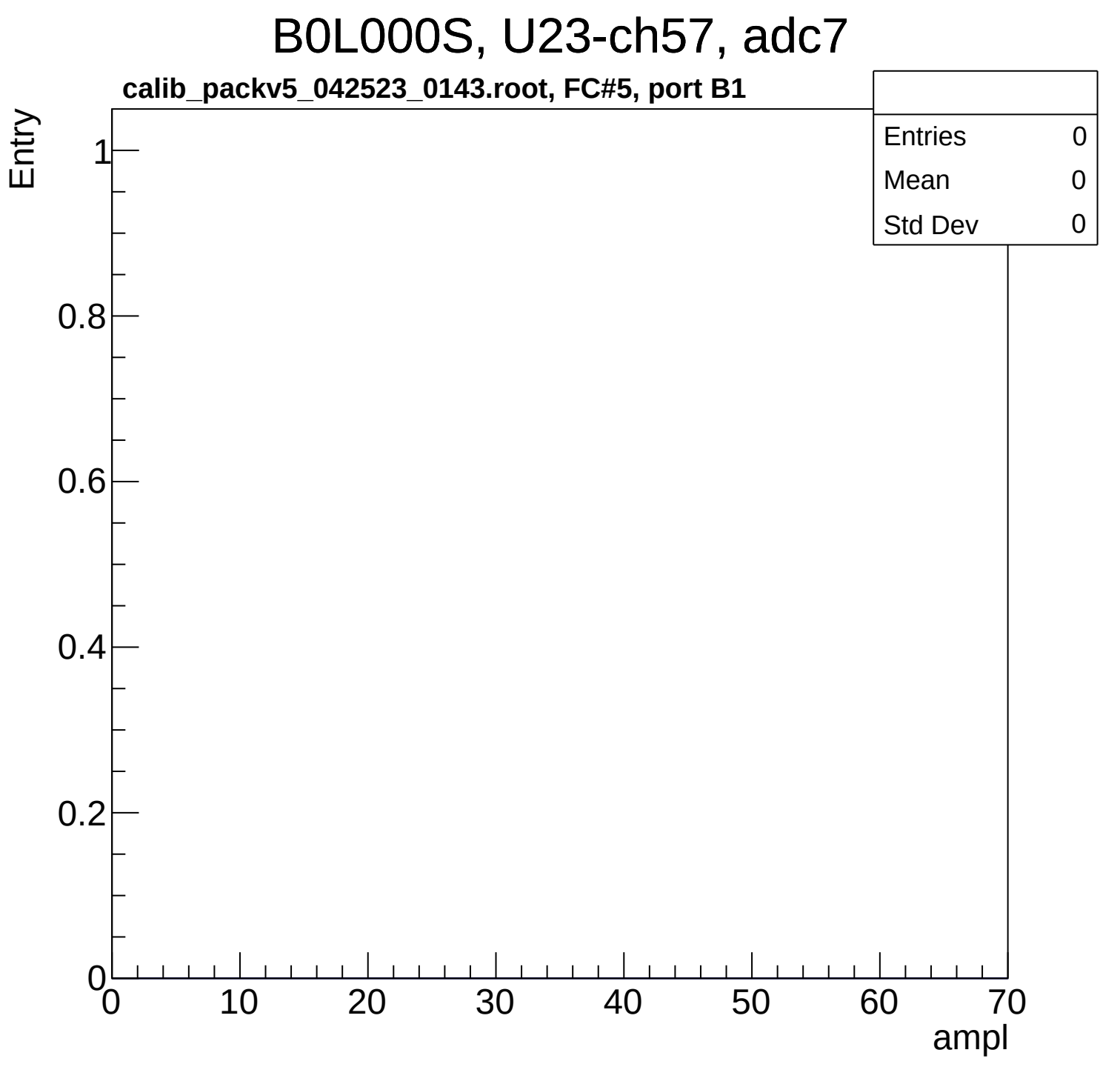

B0L000S, U23-ch57, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 41.98
Std Dev 2.892
Gaus mean : 42.4143
Gaus Width: 2.9856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

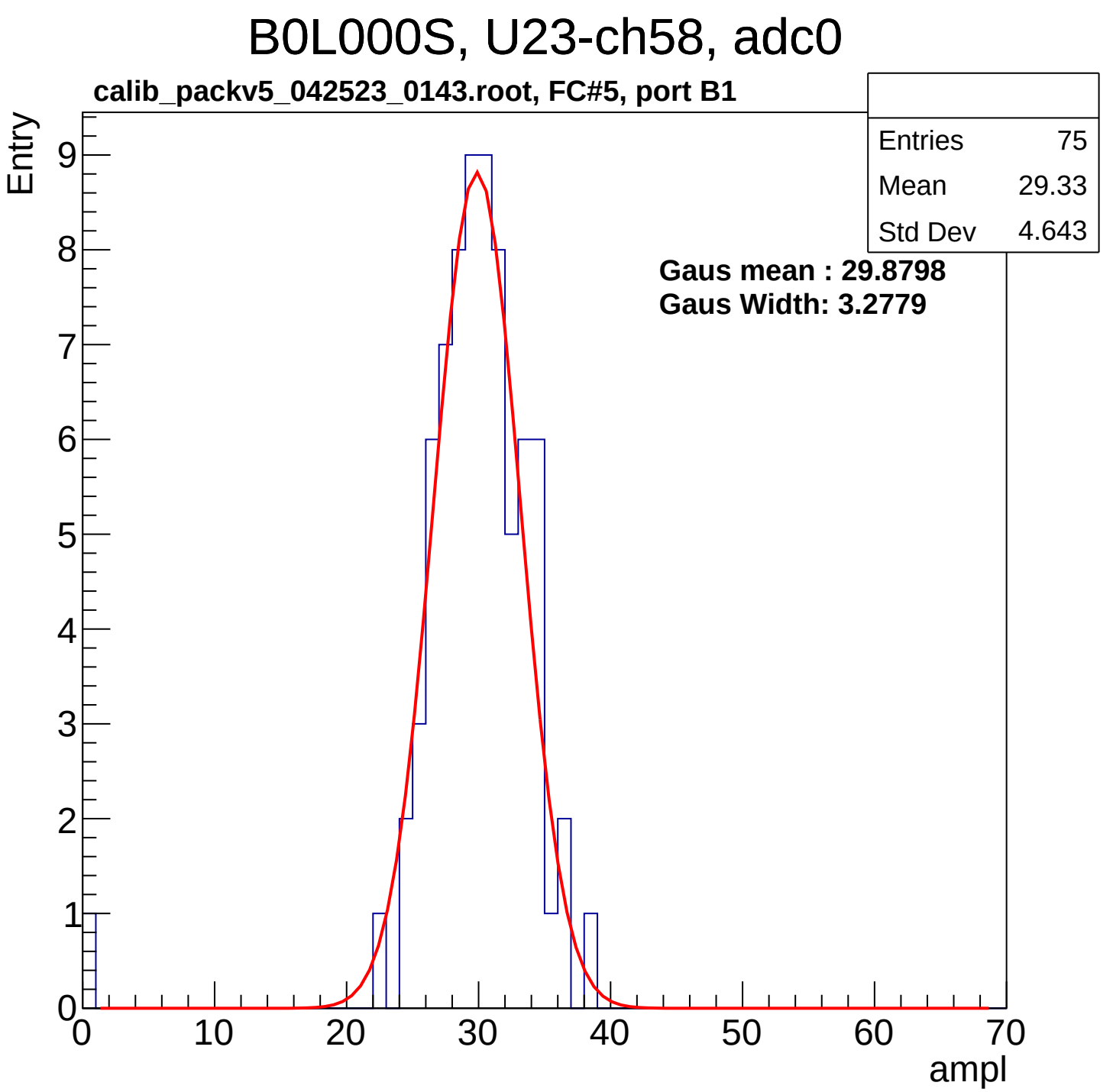

B0L000S, U23-ch57, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.05
Std Dev 3.086
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

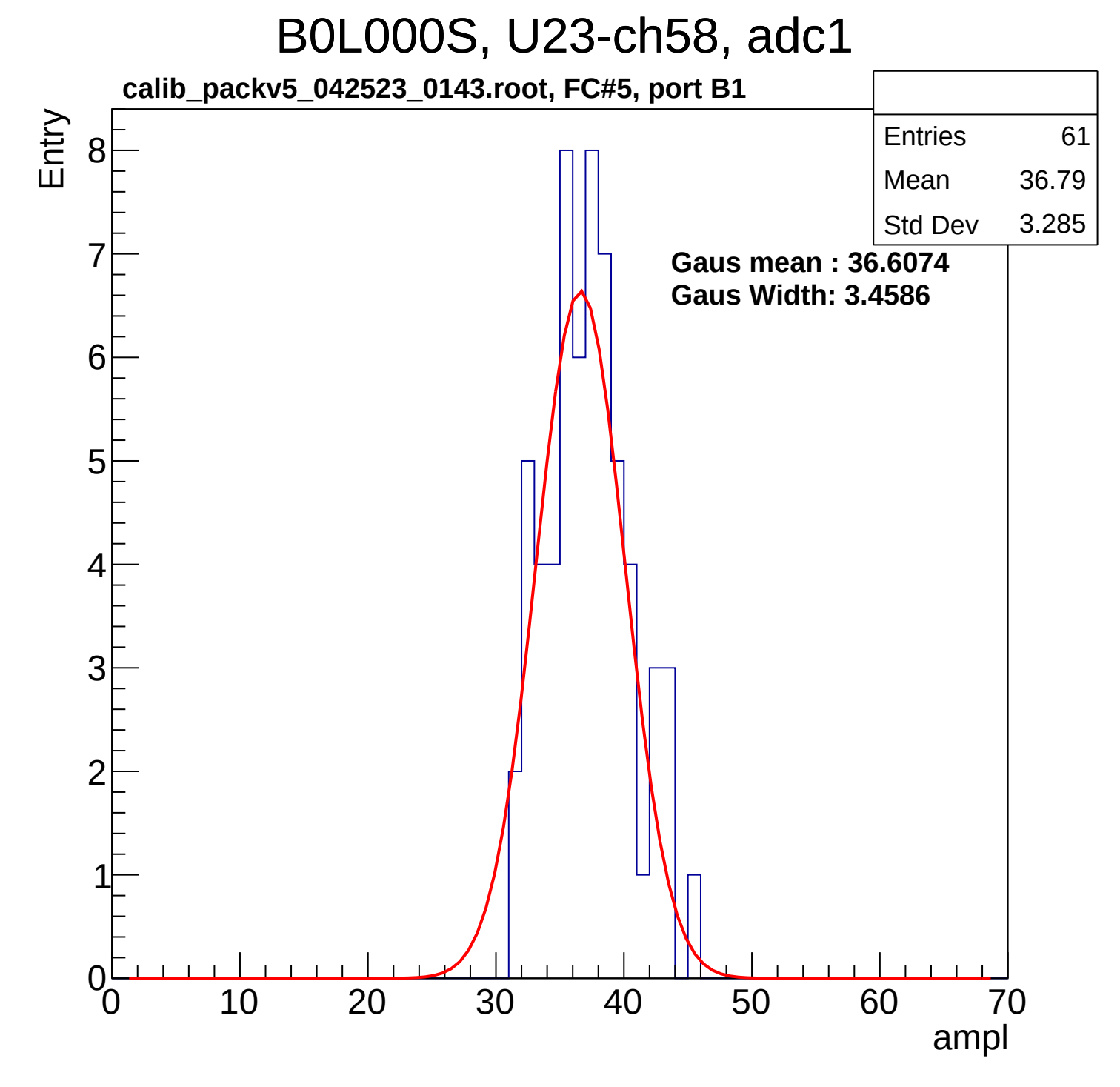

B0L000S, U23-ch57, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 53.68
Std Dev 7.186
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

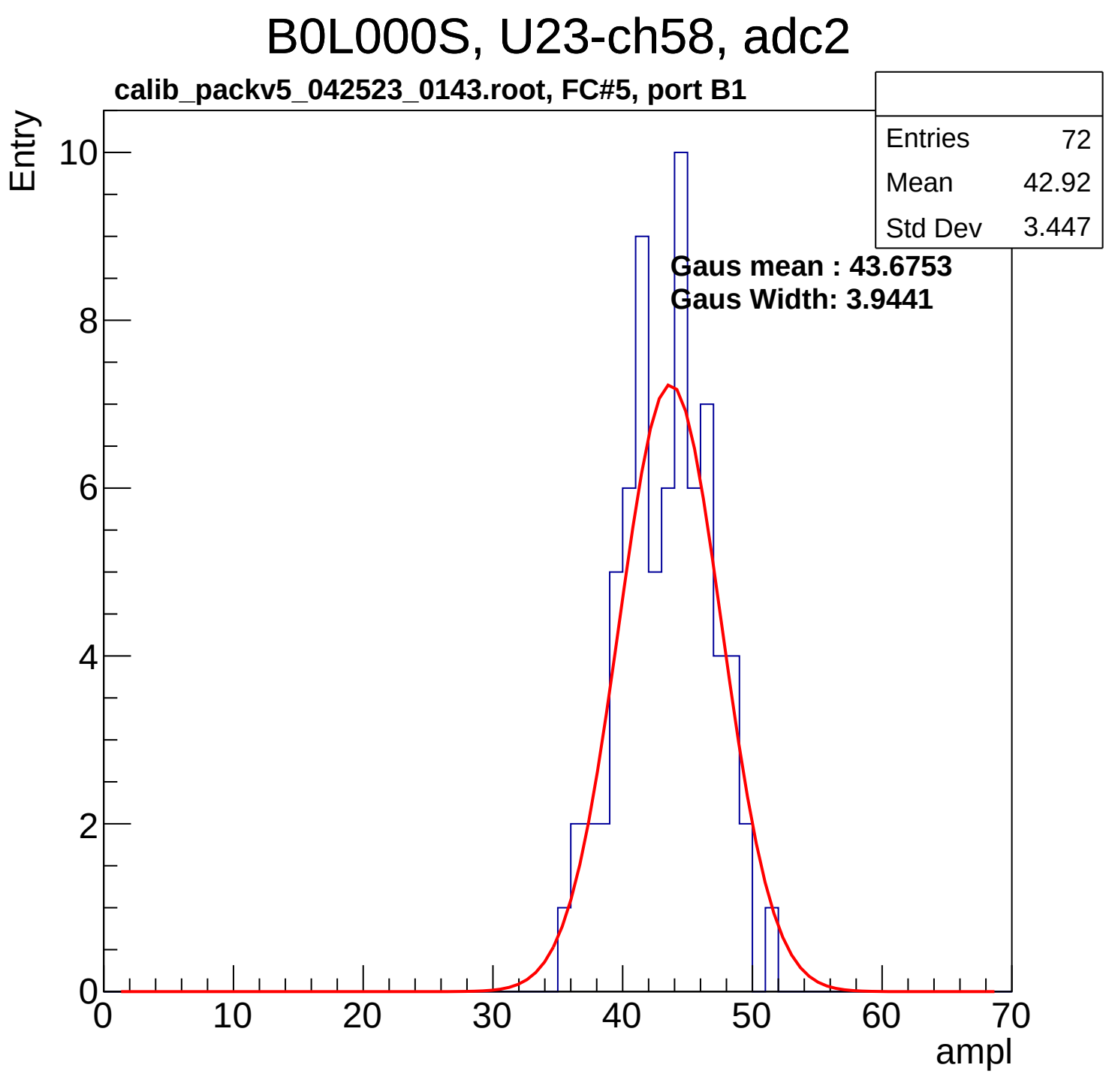

B0L000S, U23-ch57, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.63
Std Dev 2.195
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

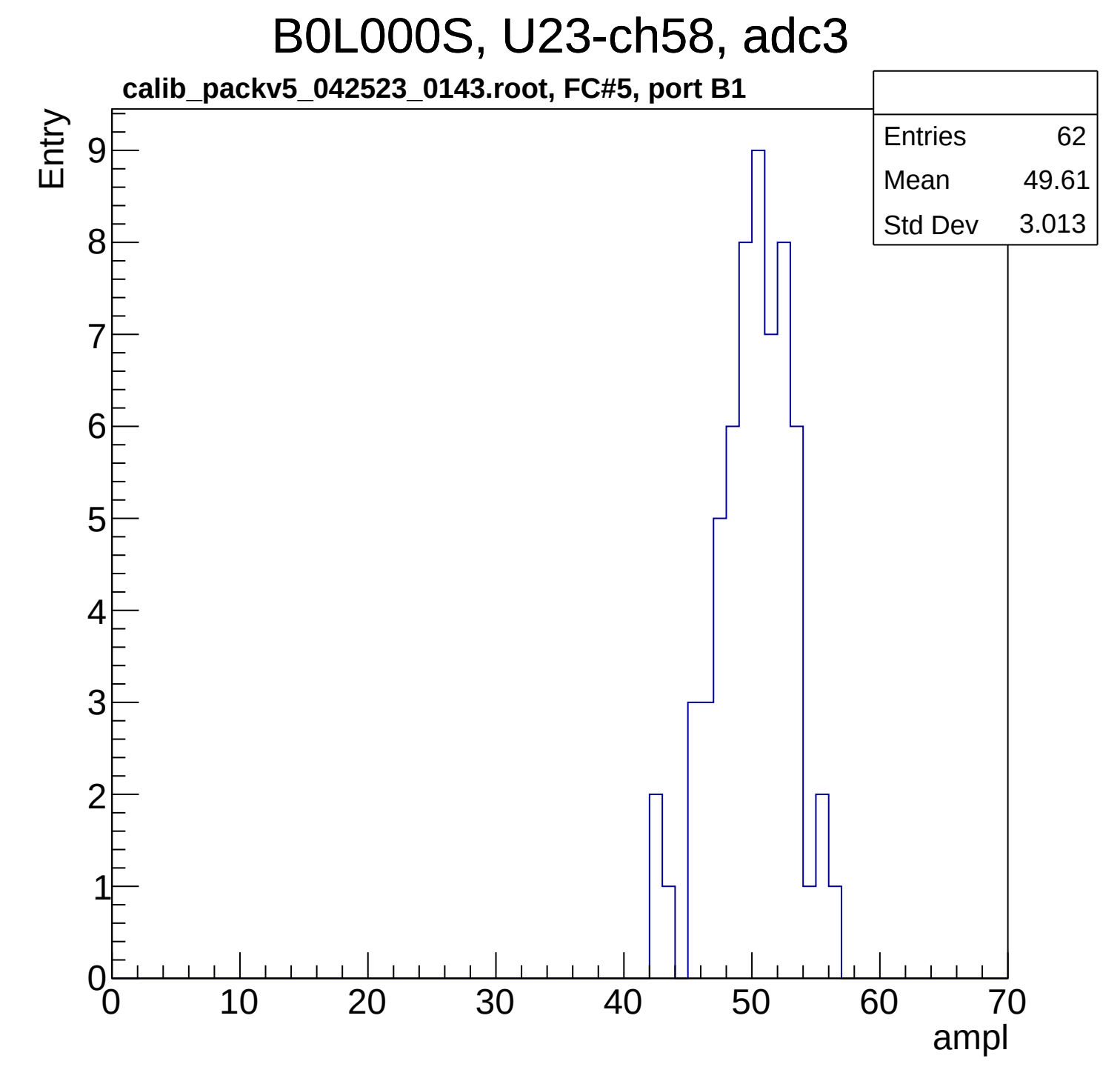

B0L000S, U23-ch57, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.85
Std Dev 1.292
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

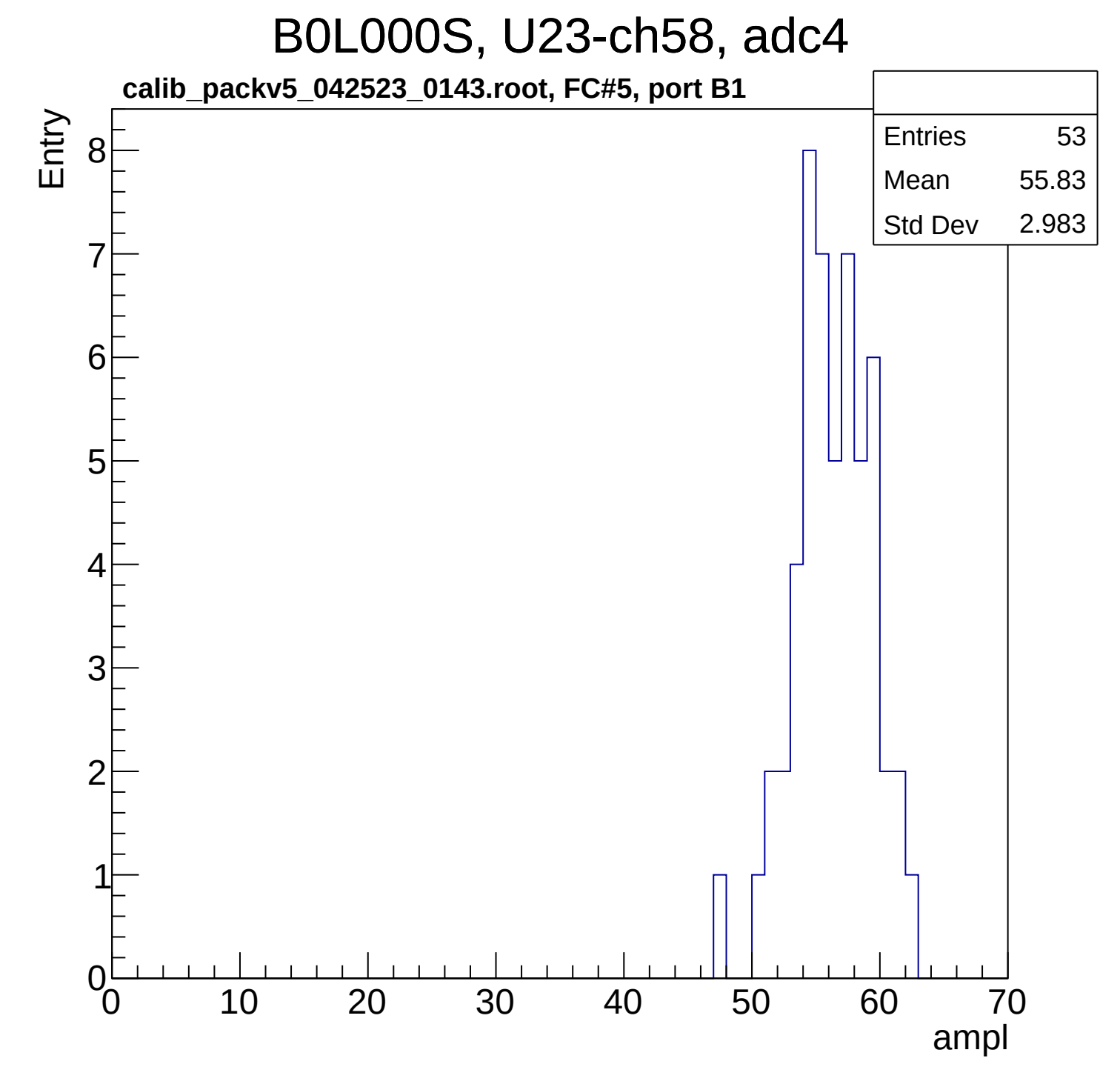

B0L000S, U23-ch57, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

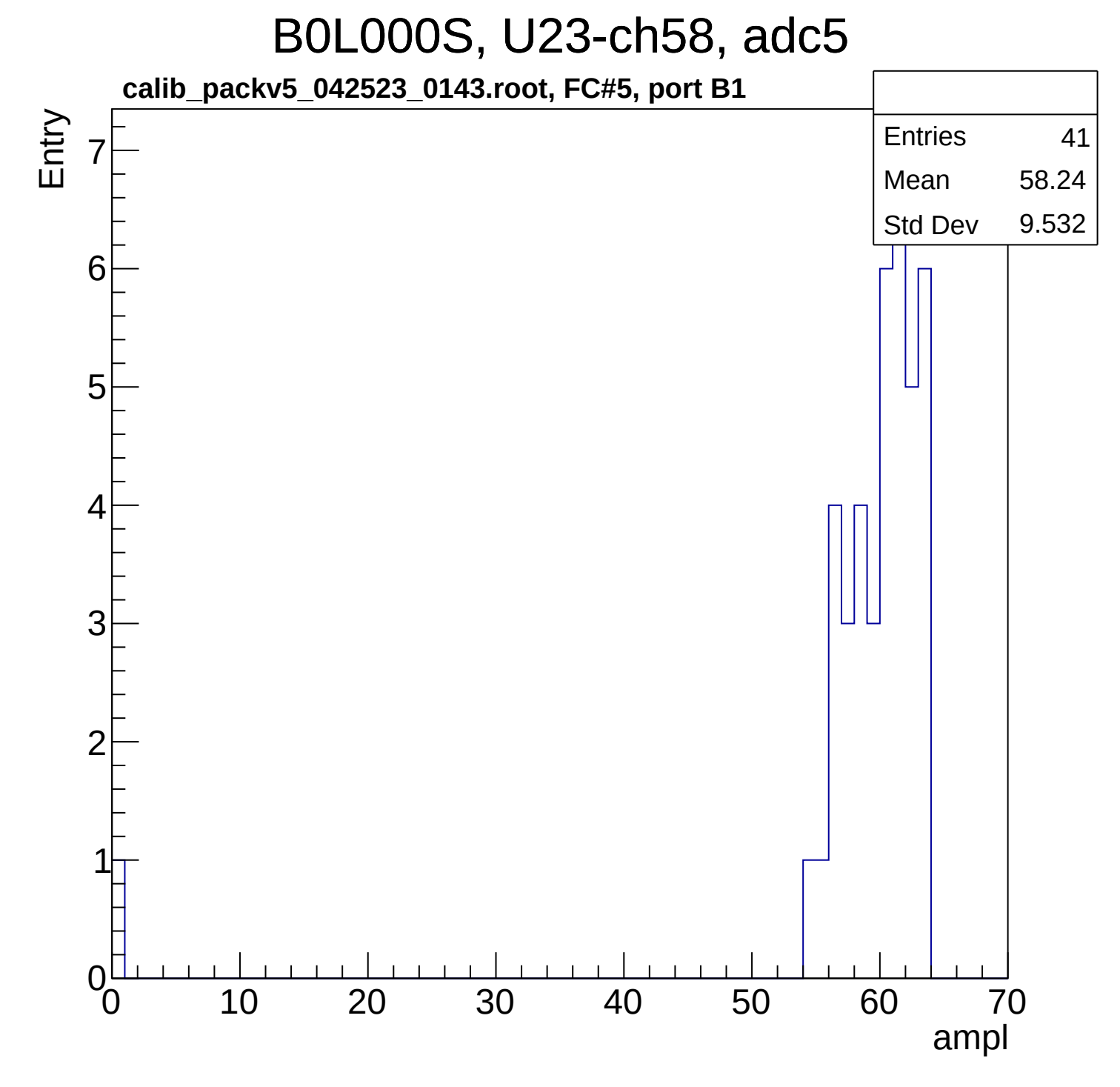

B0L000S, U23-ch58, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.33
Std Dev 4.643
Gaus mean : 29.8798
Gaus Width: 3.2779
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

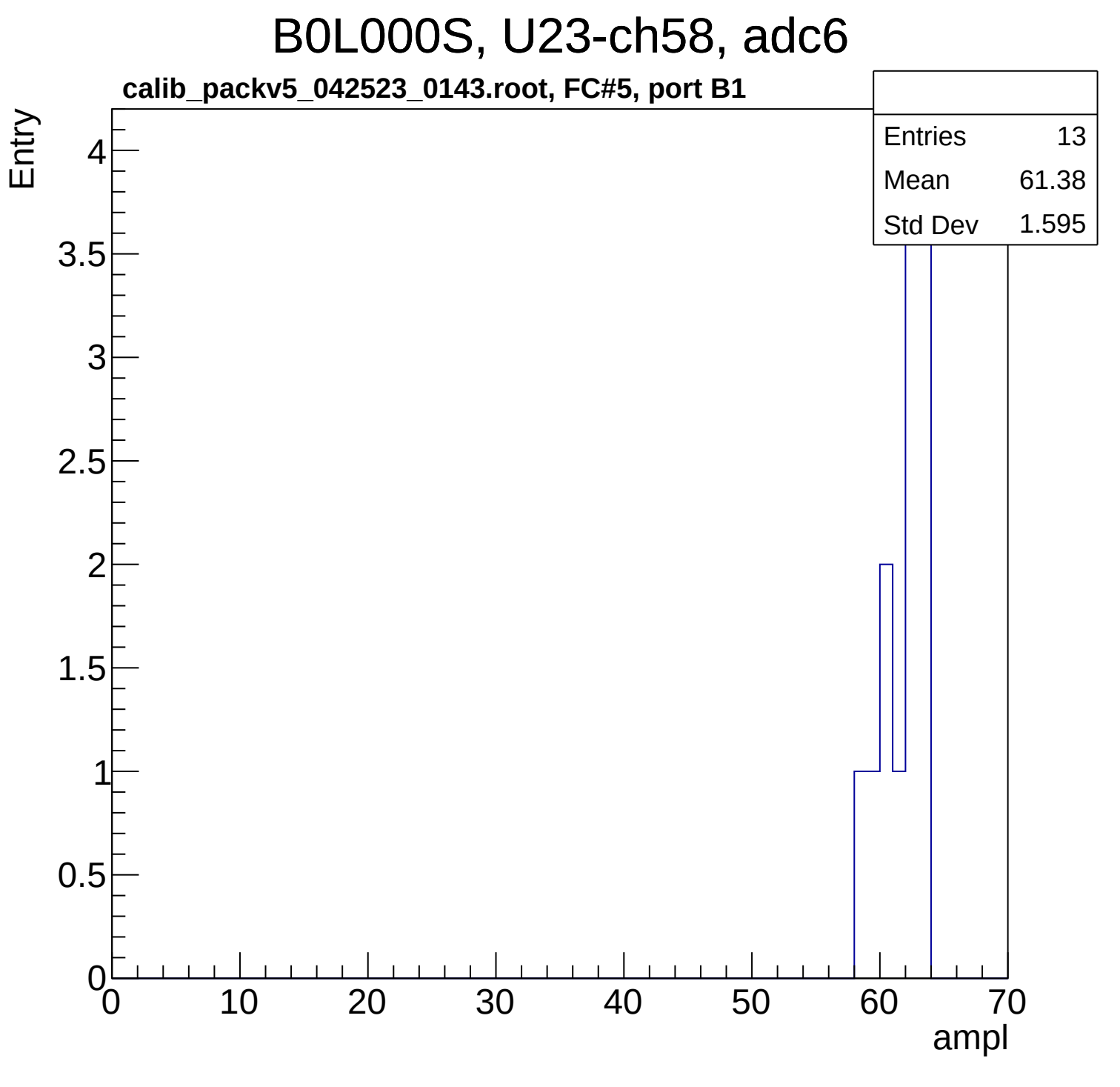

B0L000S, U23-ch58, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.79
Std Dev 3.285
Gaus mean : 36.6074
Gaus Width: 3.4586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

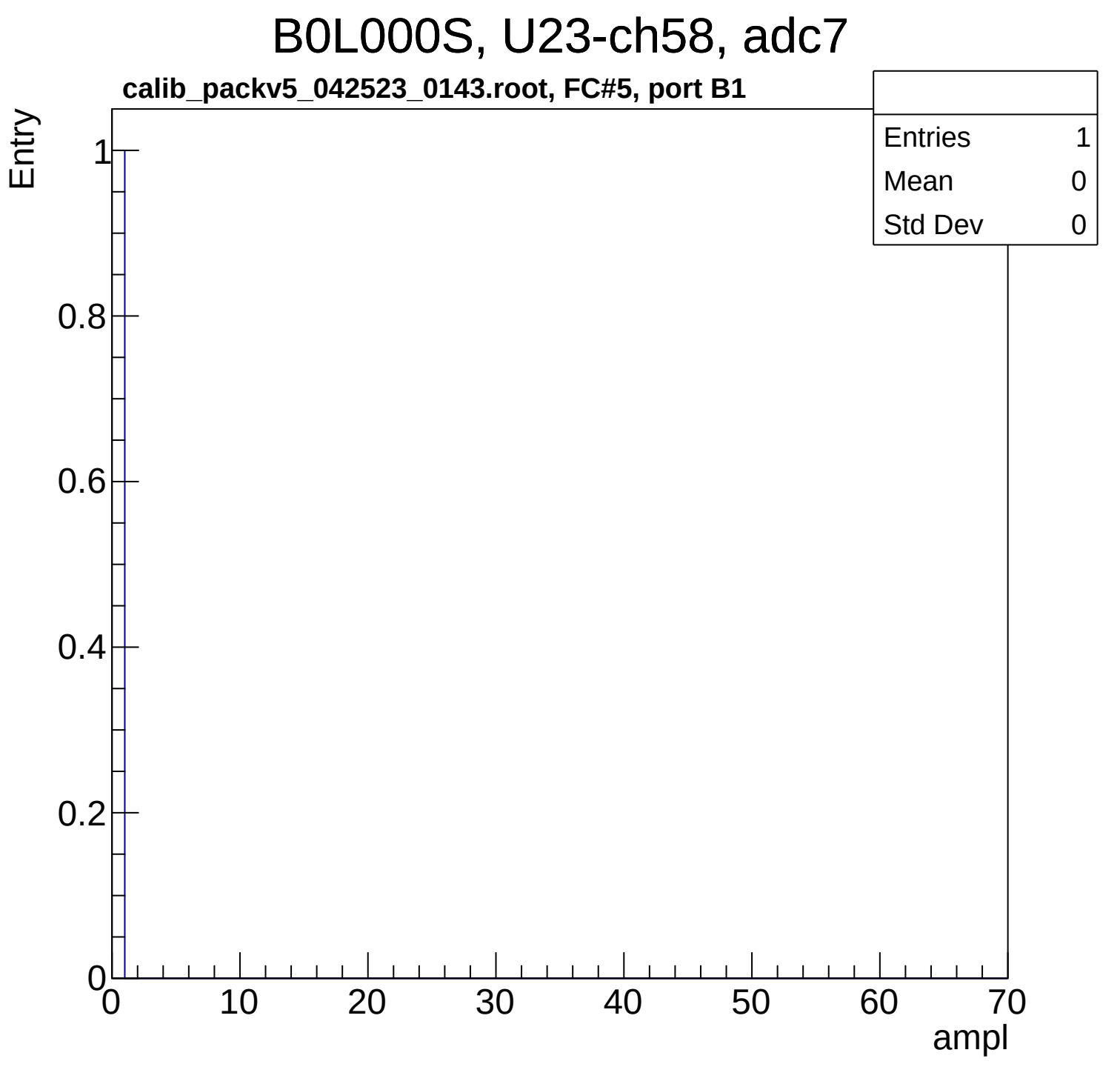

B0L000S, U23-ch58, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 42.92
Std Dev 3.447
Gaus mean : 43.6753
Gaus Width: 3.9441
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

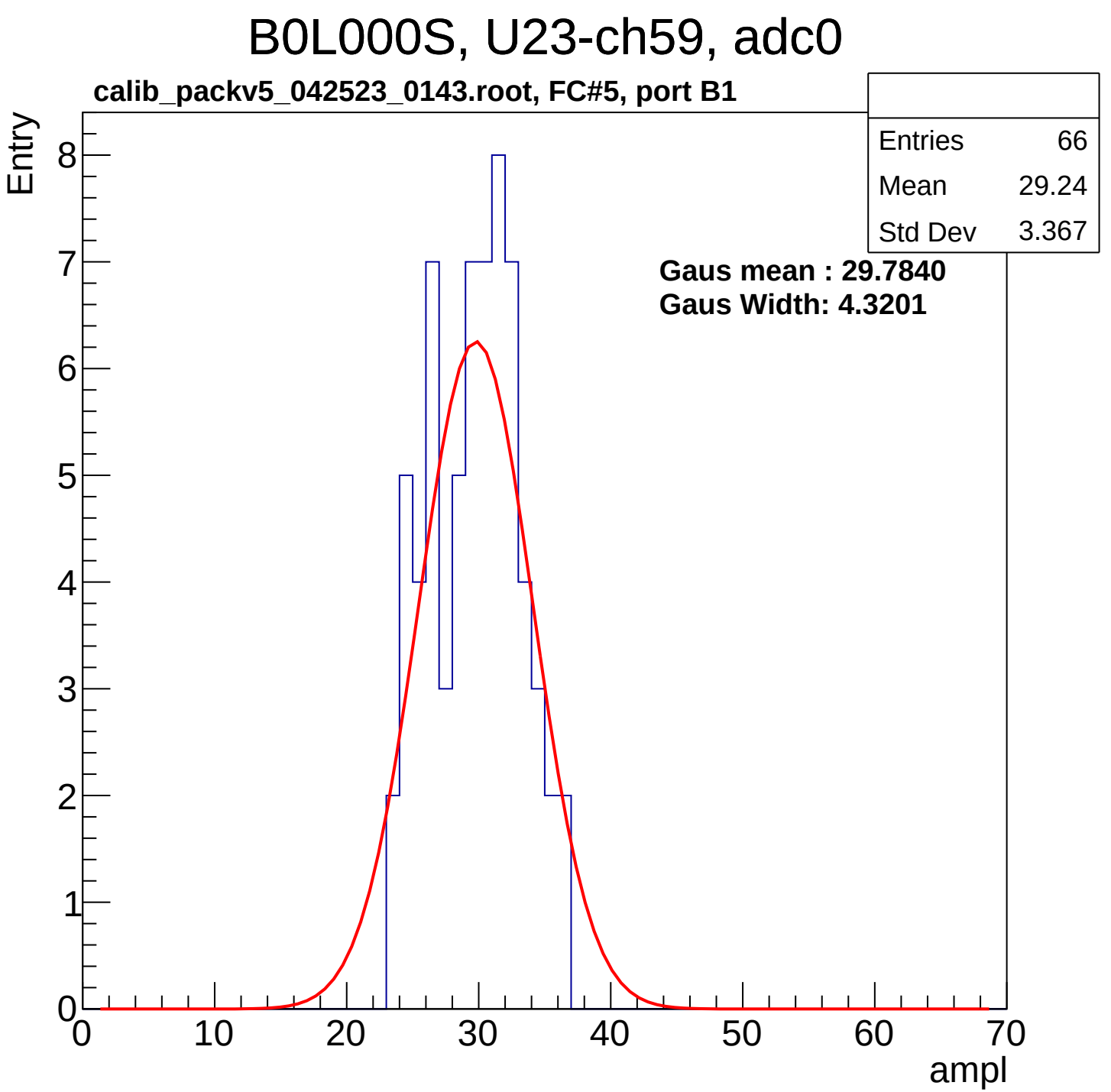

B0L000S, U23-ch58, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.61
Std Dev 3.013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

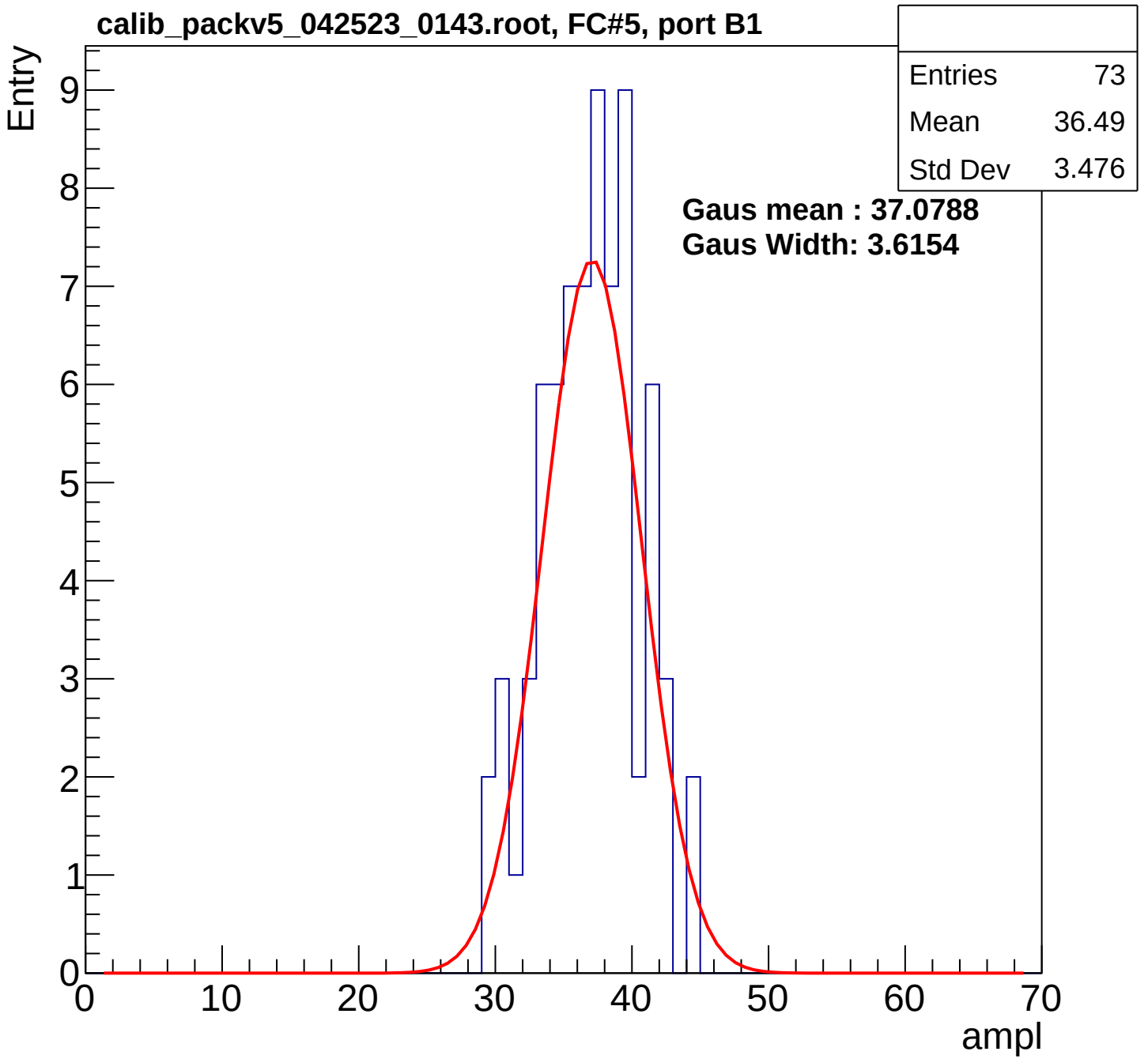

B0L000S, U23-ch58, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.83
Std Dev 2.983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

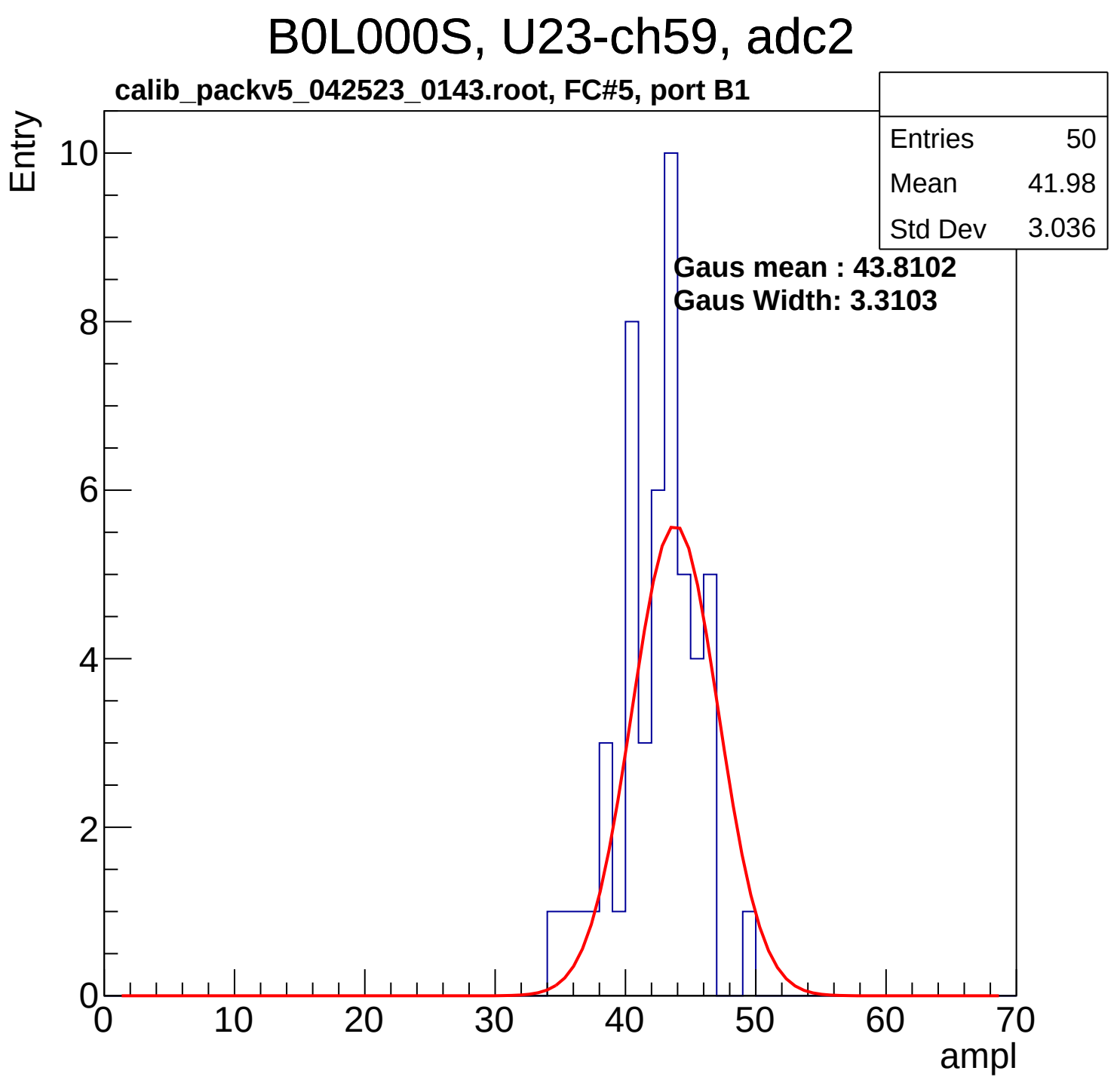

B0L000S, U23-ch58, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.24
Std Dev 9.532
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

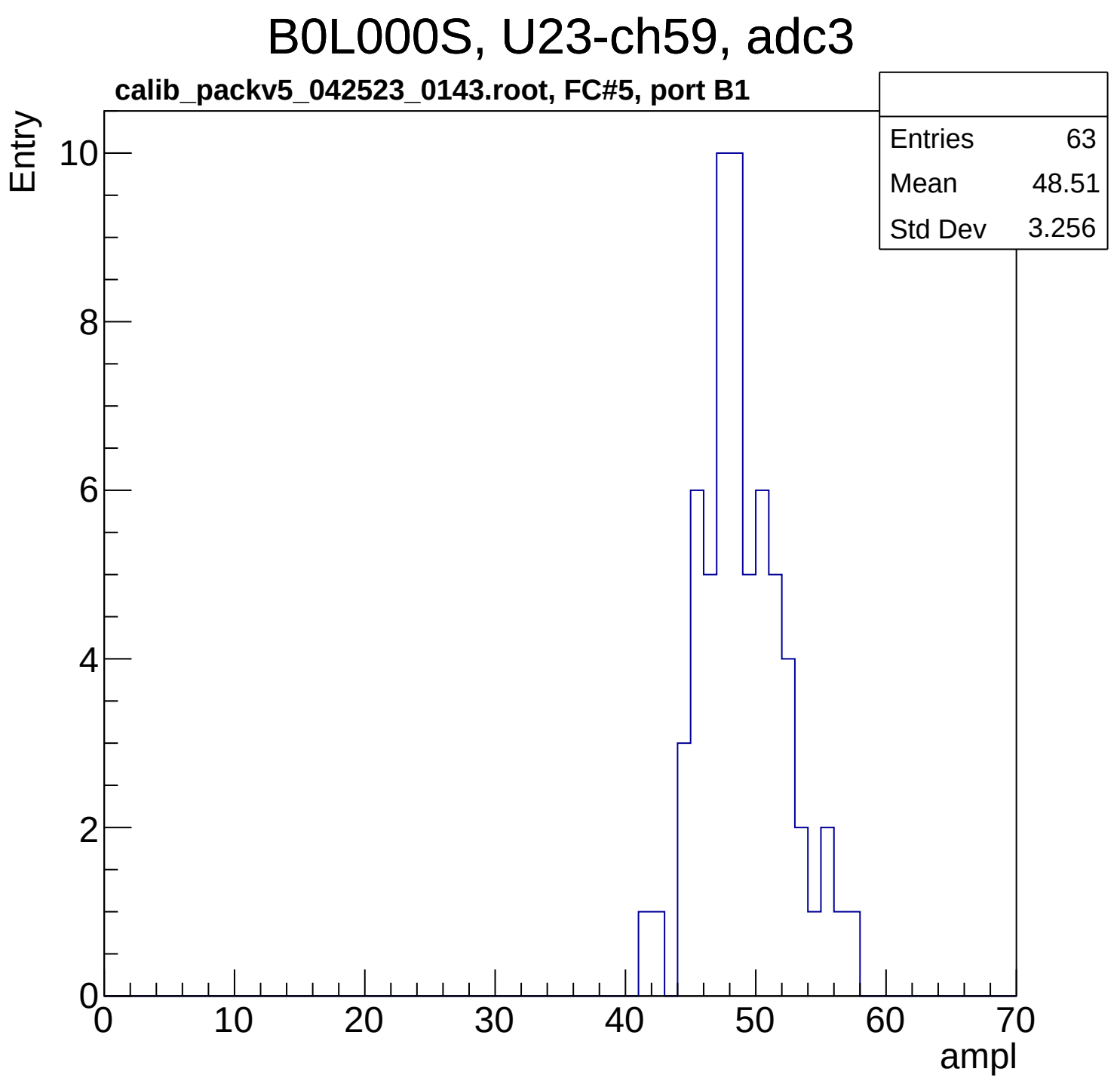

B0L000S, U23-ch58, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.38
Std Dev 1.595
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

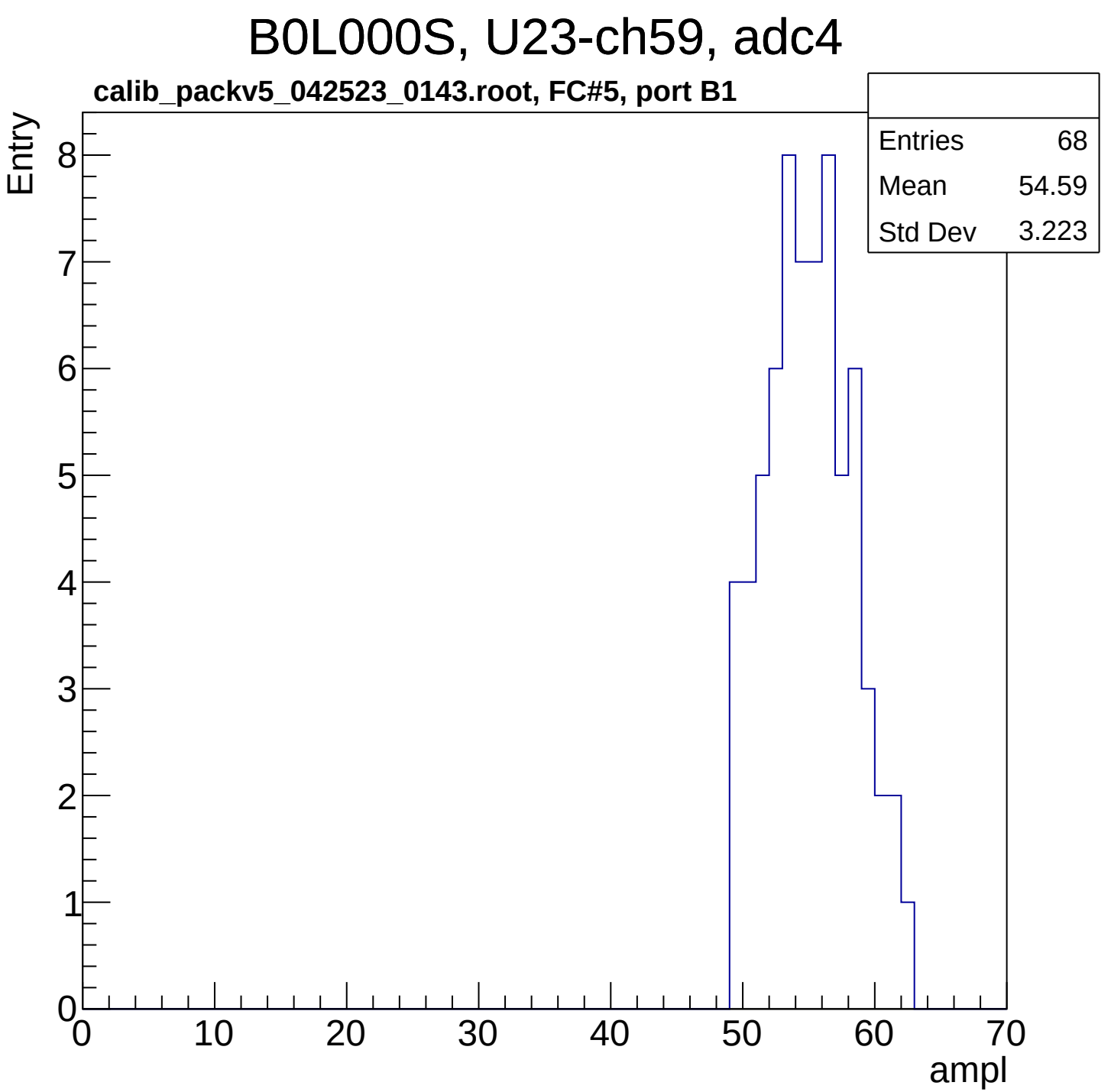

B0L000S, U23-ch58, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

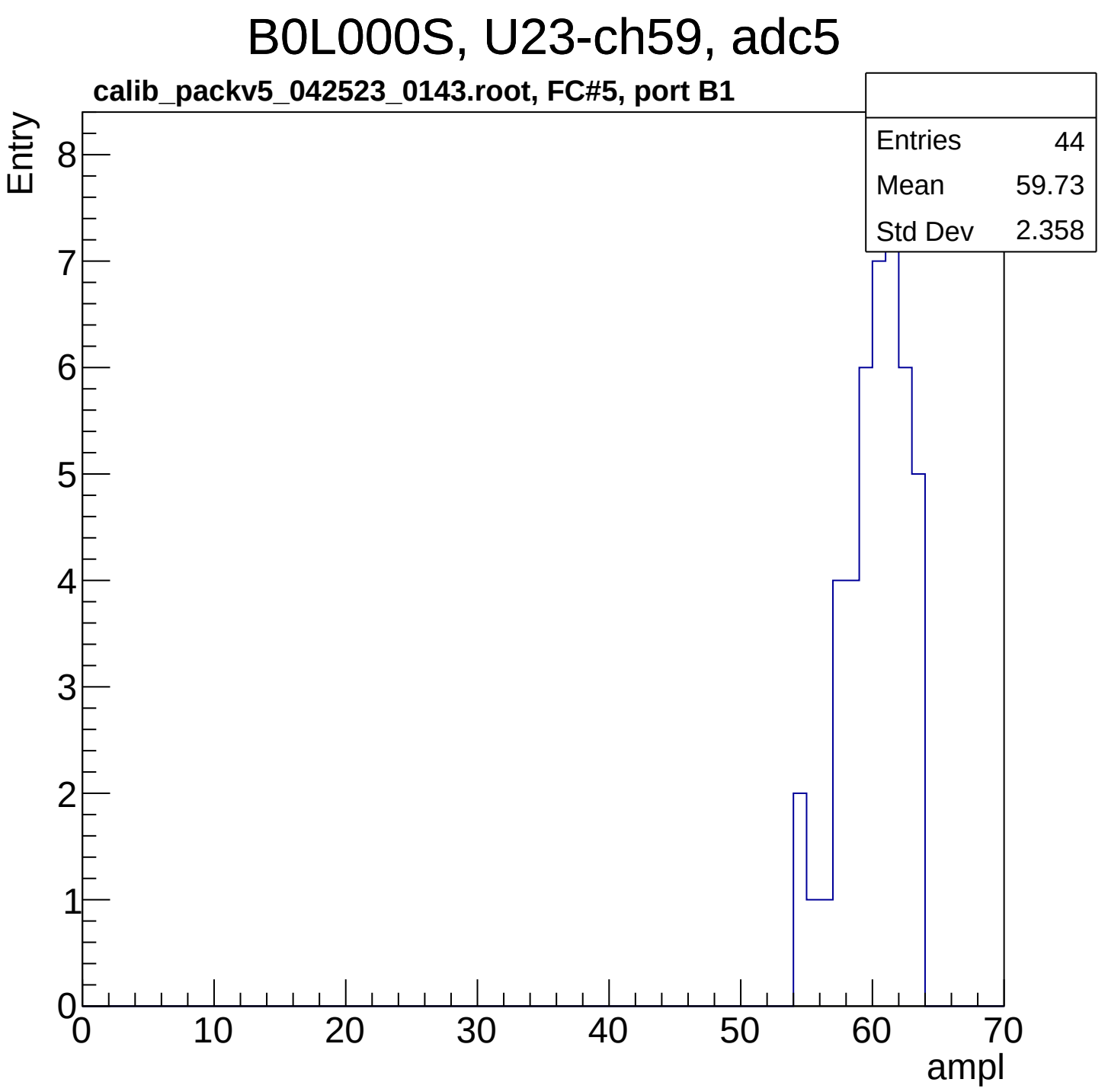

B0L000S, U23-ch59, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.24
Std Dev 3.367
Gaus mean : 29.7840
Gaus Width: 4.3201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

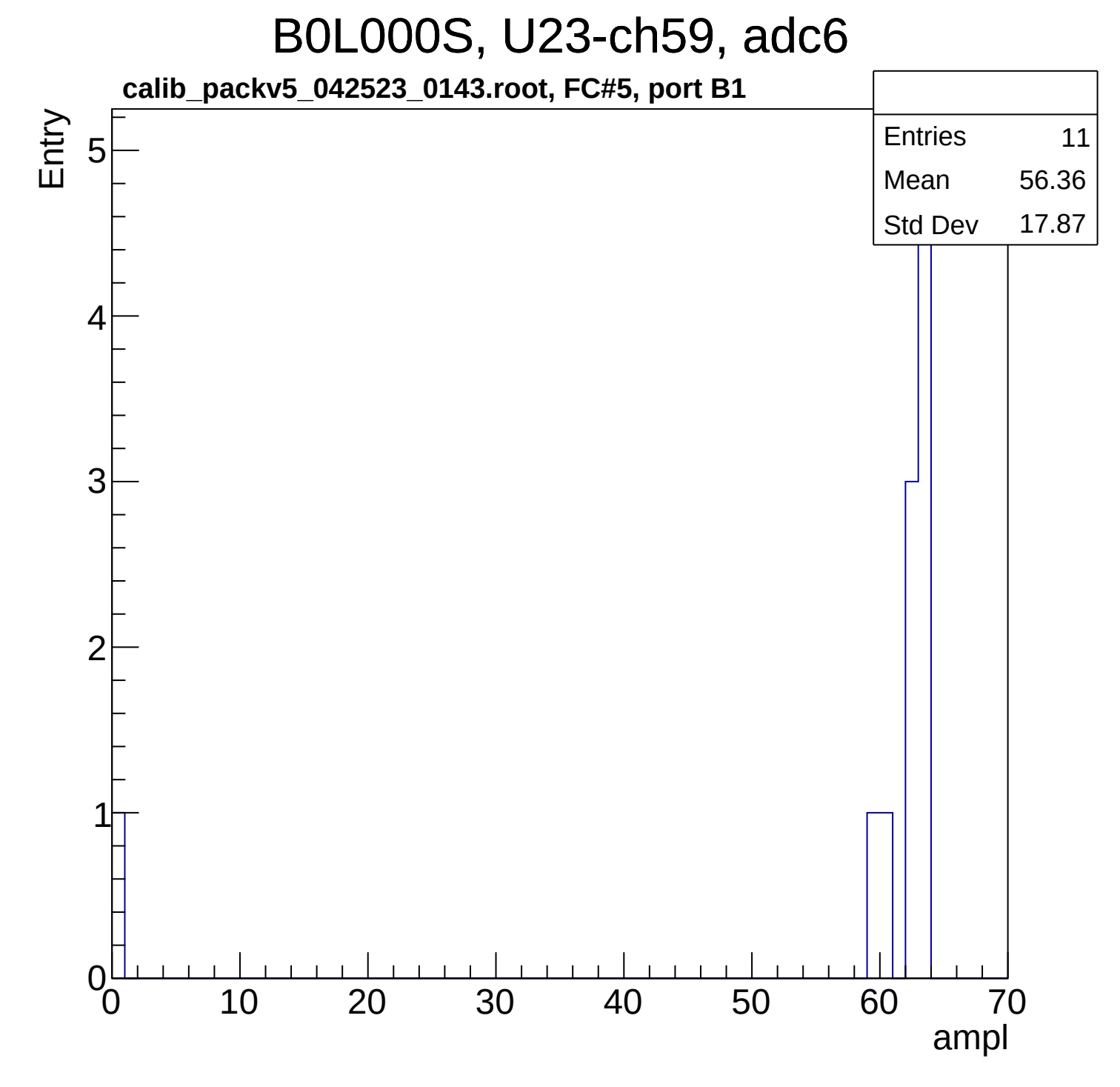

B0L000S, U23-ch59, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.49
Std Dev 3.476
Gaus mean : 37.0788
Gaus Width: 3.6154
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

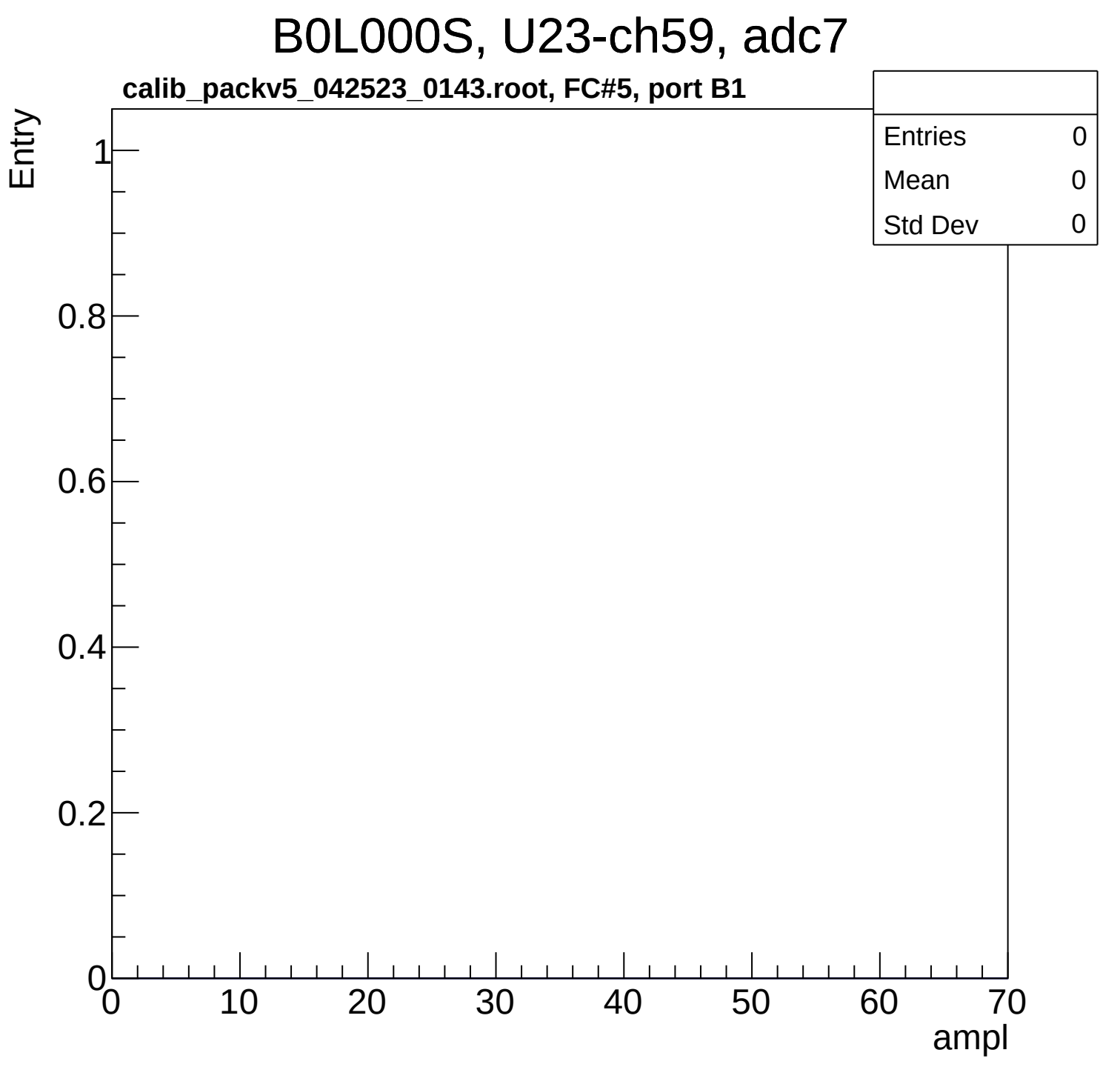

B0L000S, U23-ch59, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 41.98
Std Dev 3.036
Gaus mean : 43.8102
Gaus Width: 3.3103
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

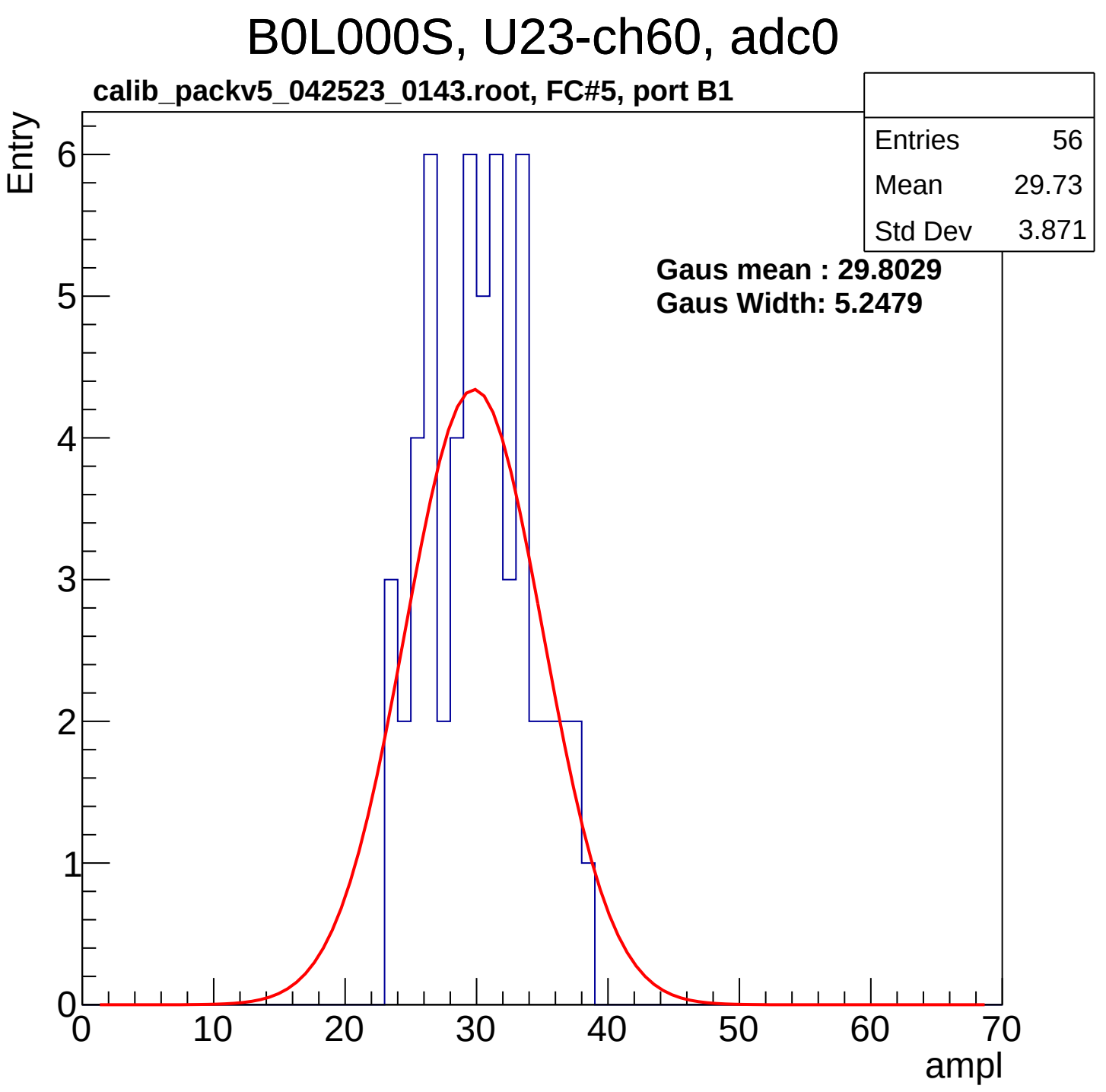

B0L000S, U23-ch59, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 48.51
Std Dev 3.256
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

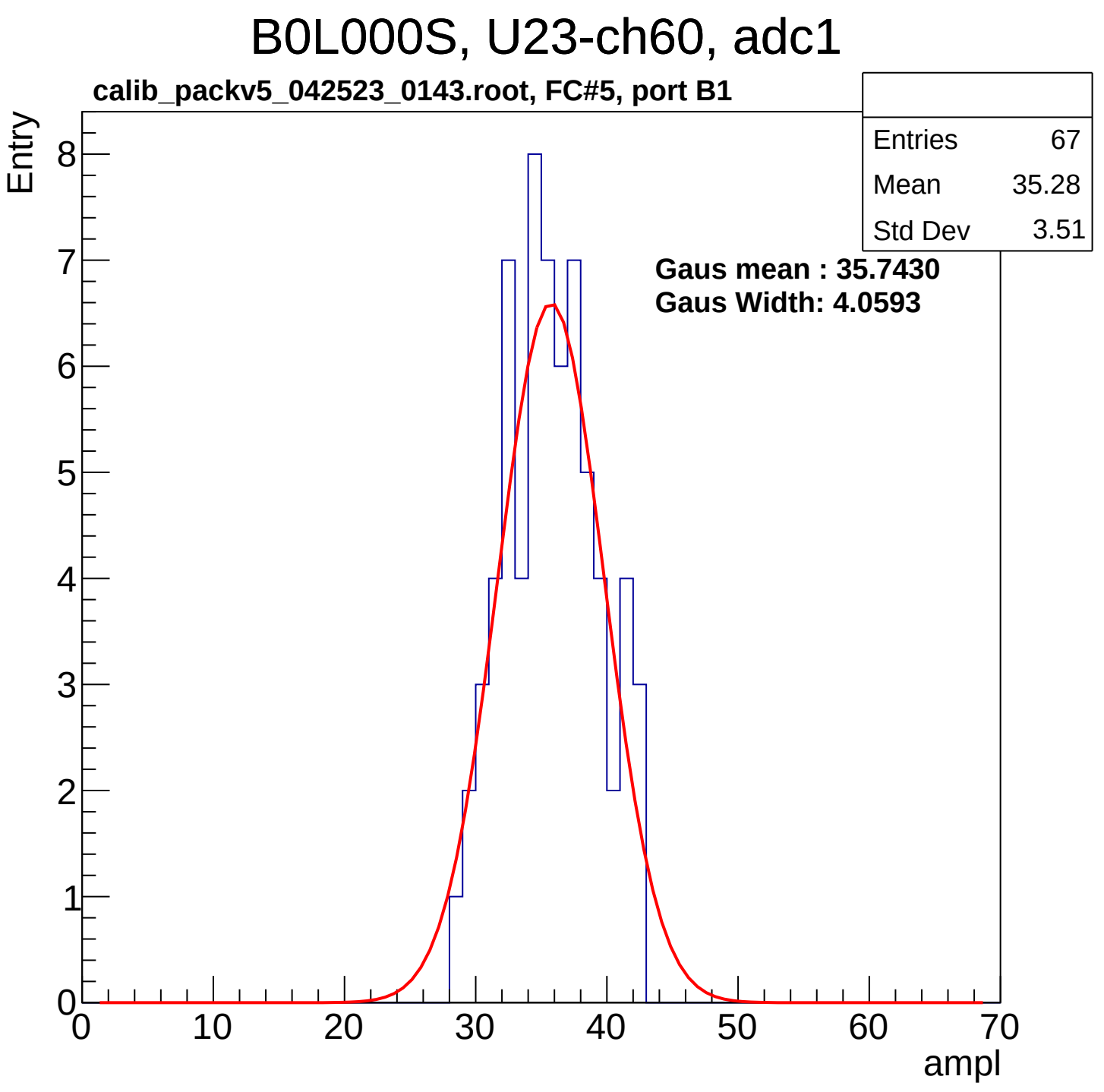

B0L000S, U23-ch59, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 54.59
Std Dev 3.223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

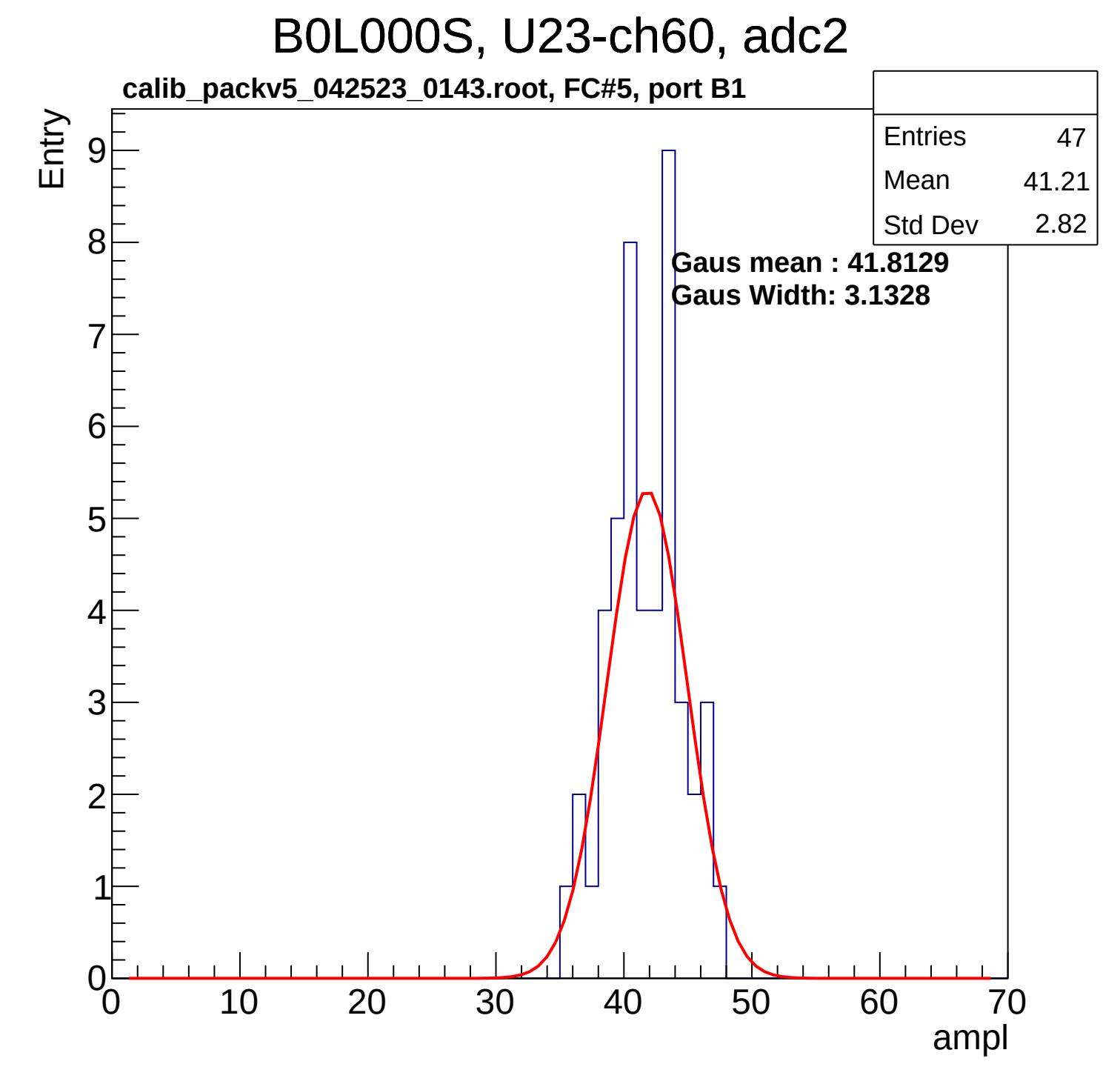

B0L000S, U23-ch59, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.73
Std Dev 2.358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

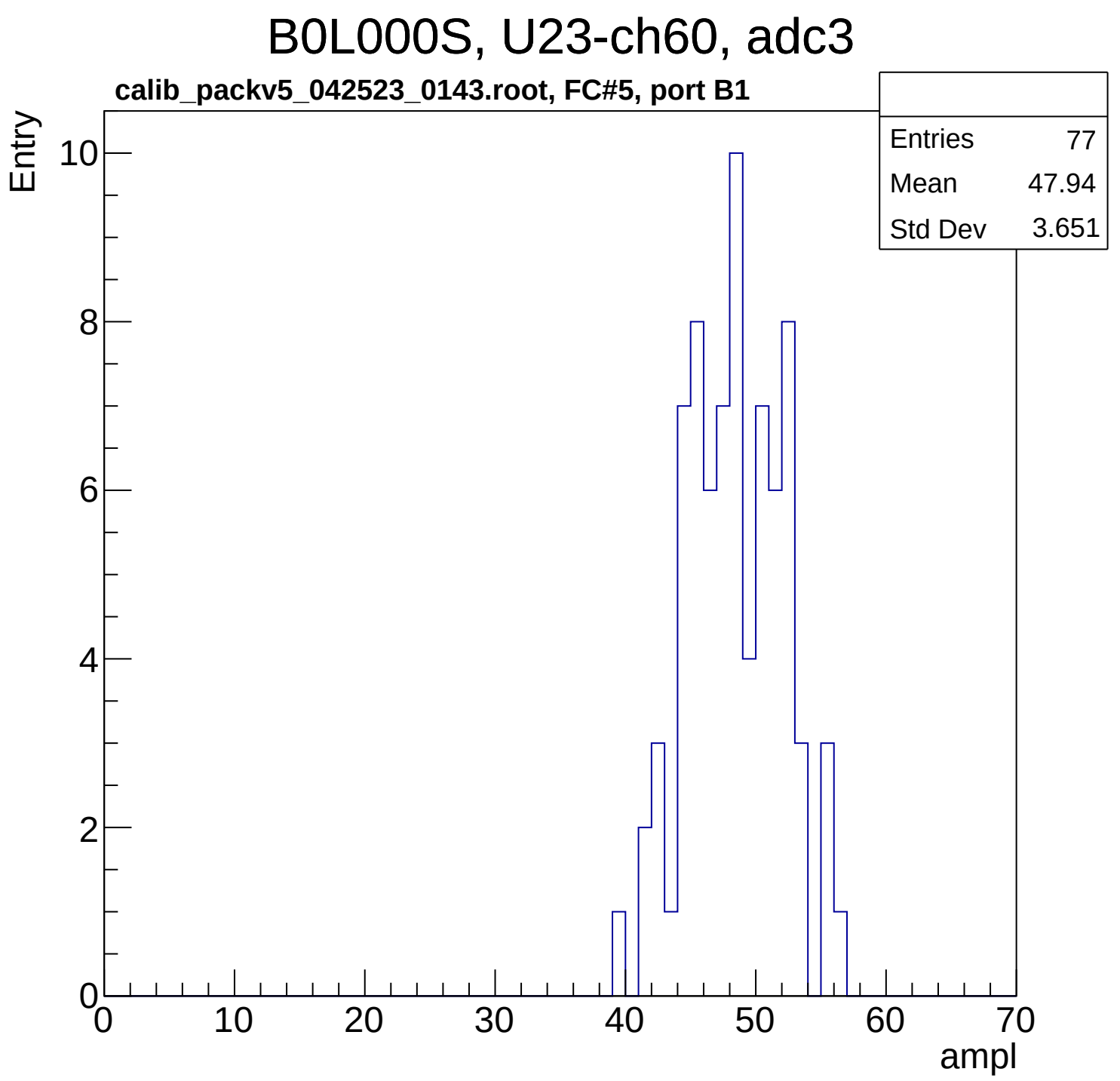

B0L000S, U23-ch59, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 56.36
Std Dev 17.87
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

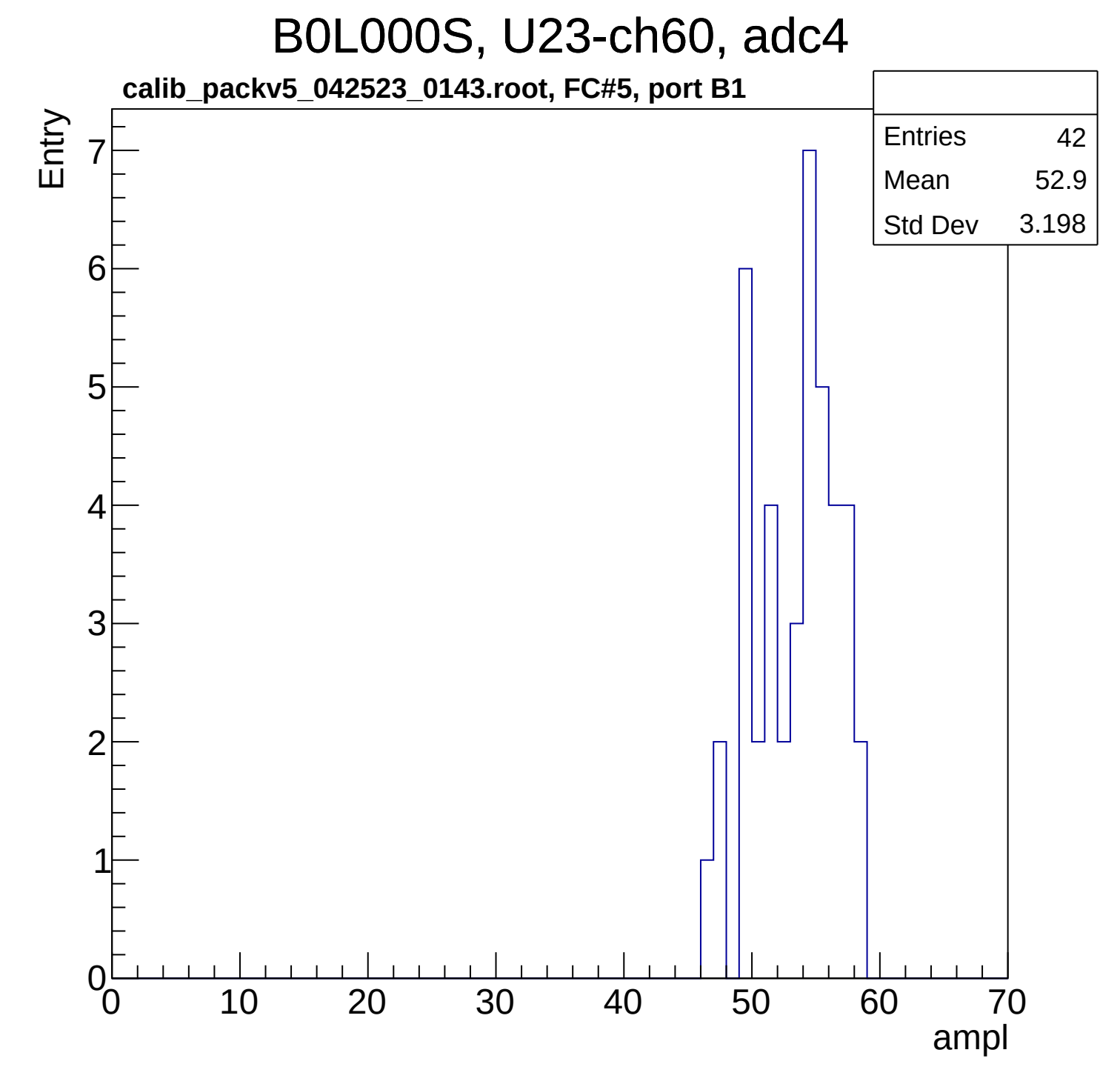

B0L000S, U23-ch59, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

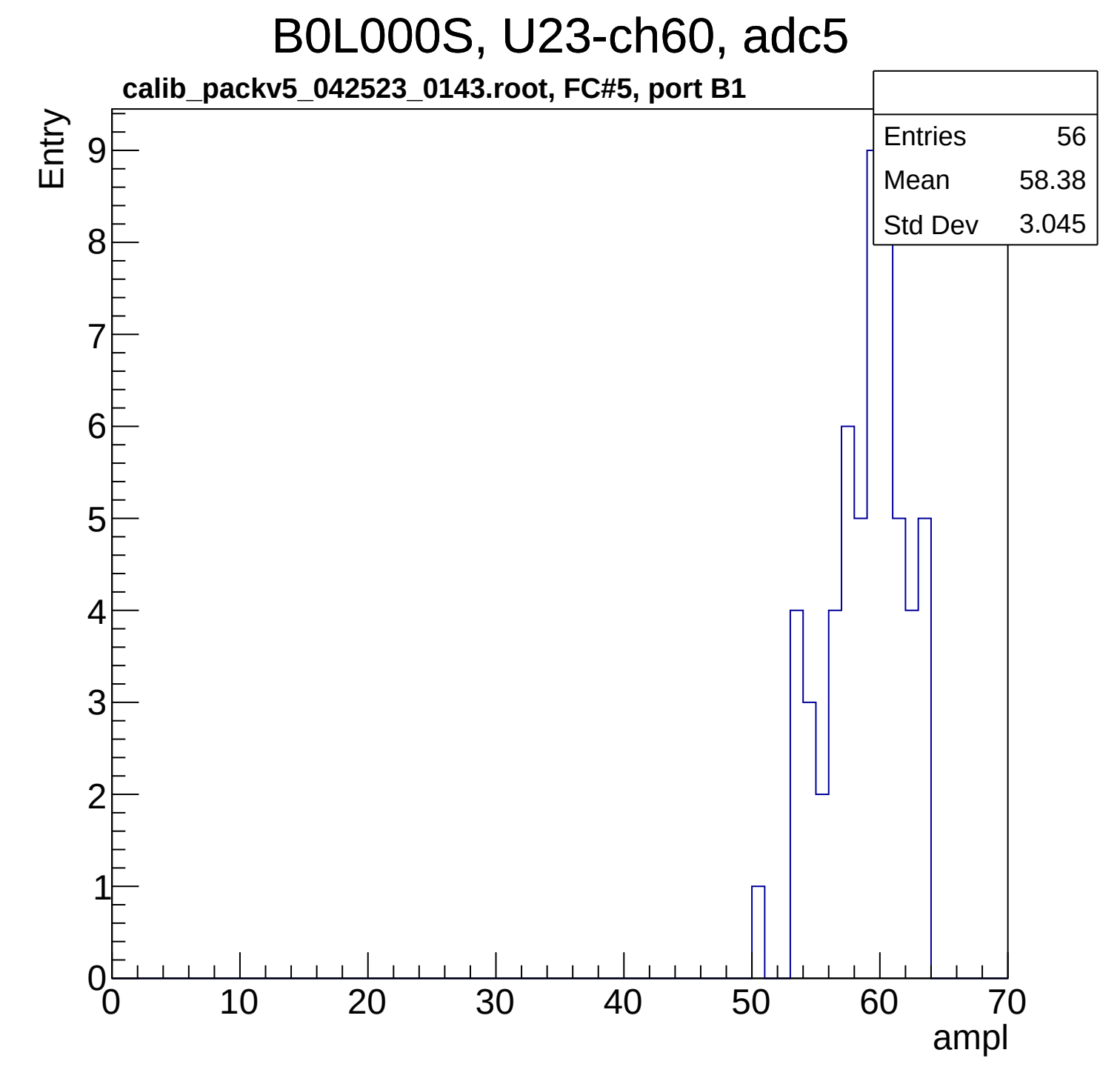

B0L000S, U23-ch60, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 29.73
Std Dev 3.871
Gaus mean : 29.8029
Gaus Width: 5.2479
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

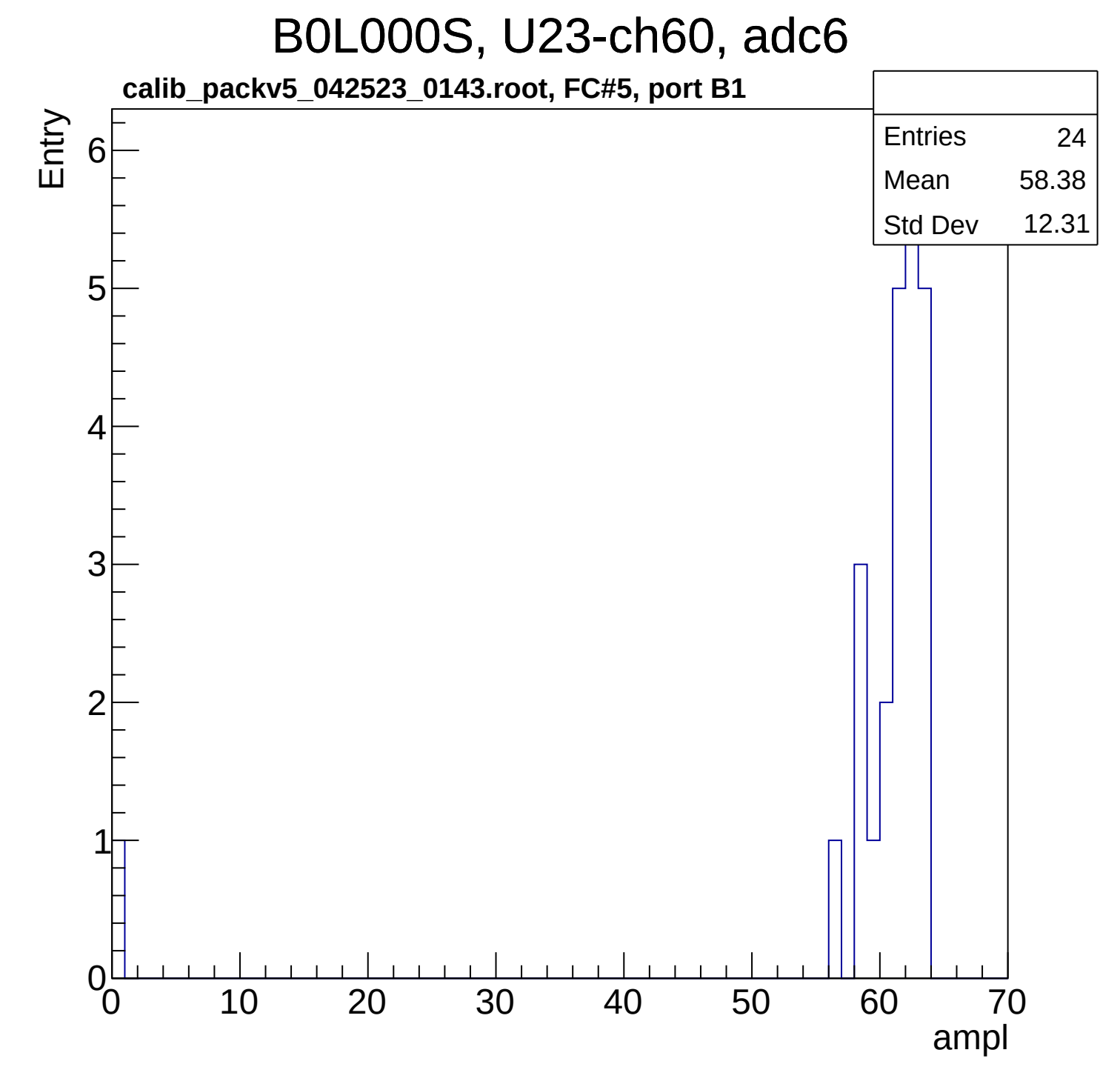

B0L000S, U23-ch60, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 35.28
Std Dev 3.51
Gaus mean : 35.7430
Gaus Width: 4.0593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

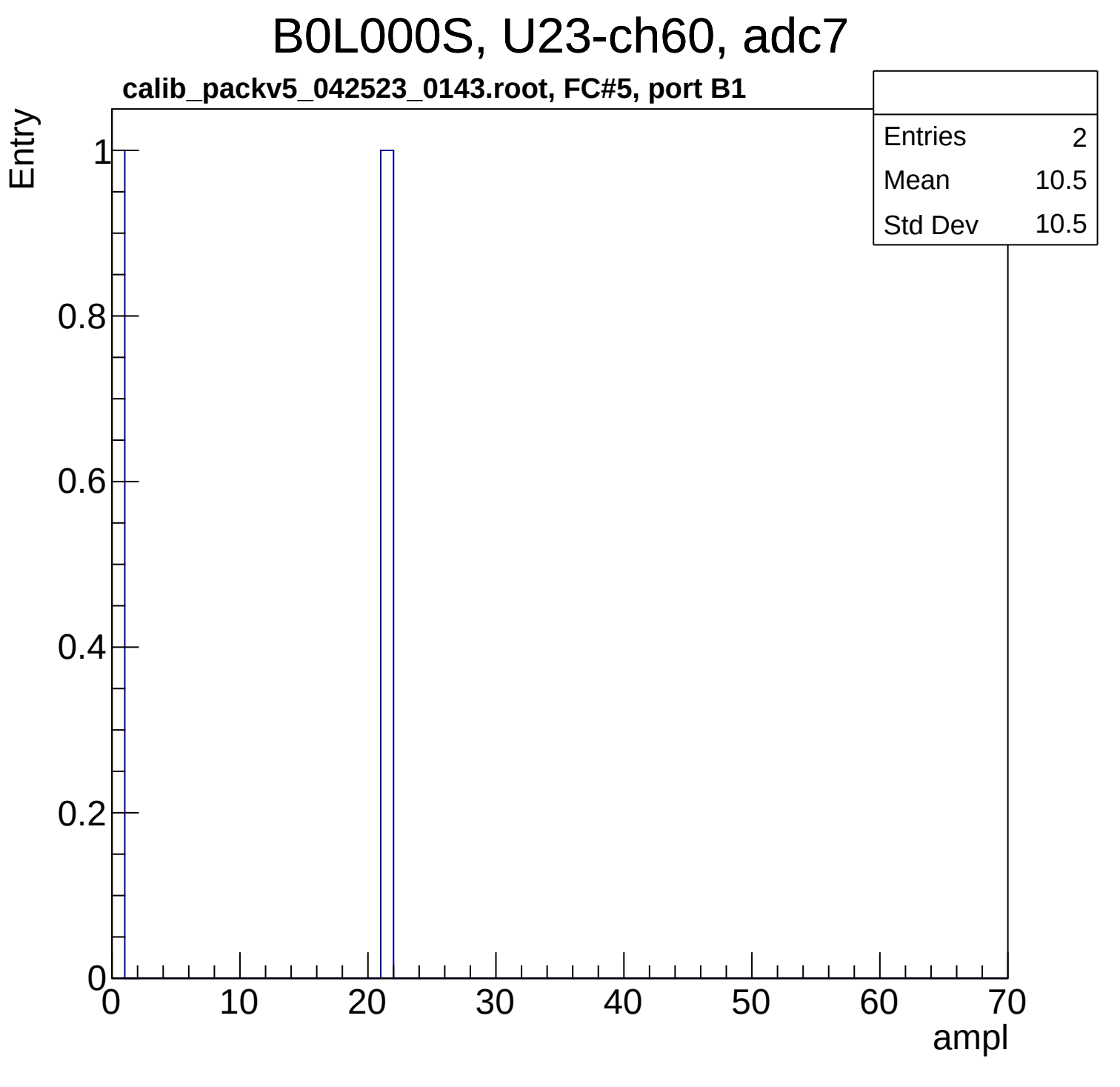

B0L000S, U23-ch60, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 41.21
Std Dev 2.82
Gaus mean : 41.8129
Gaus Width: 3.1328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

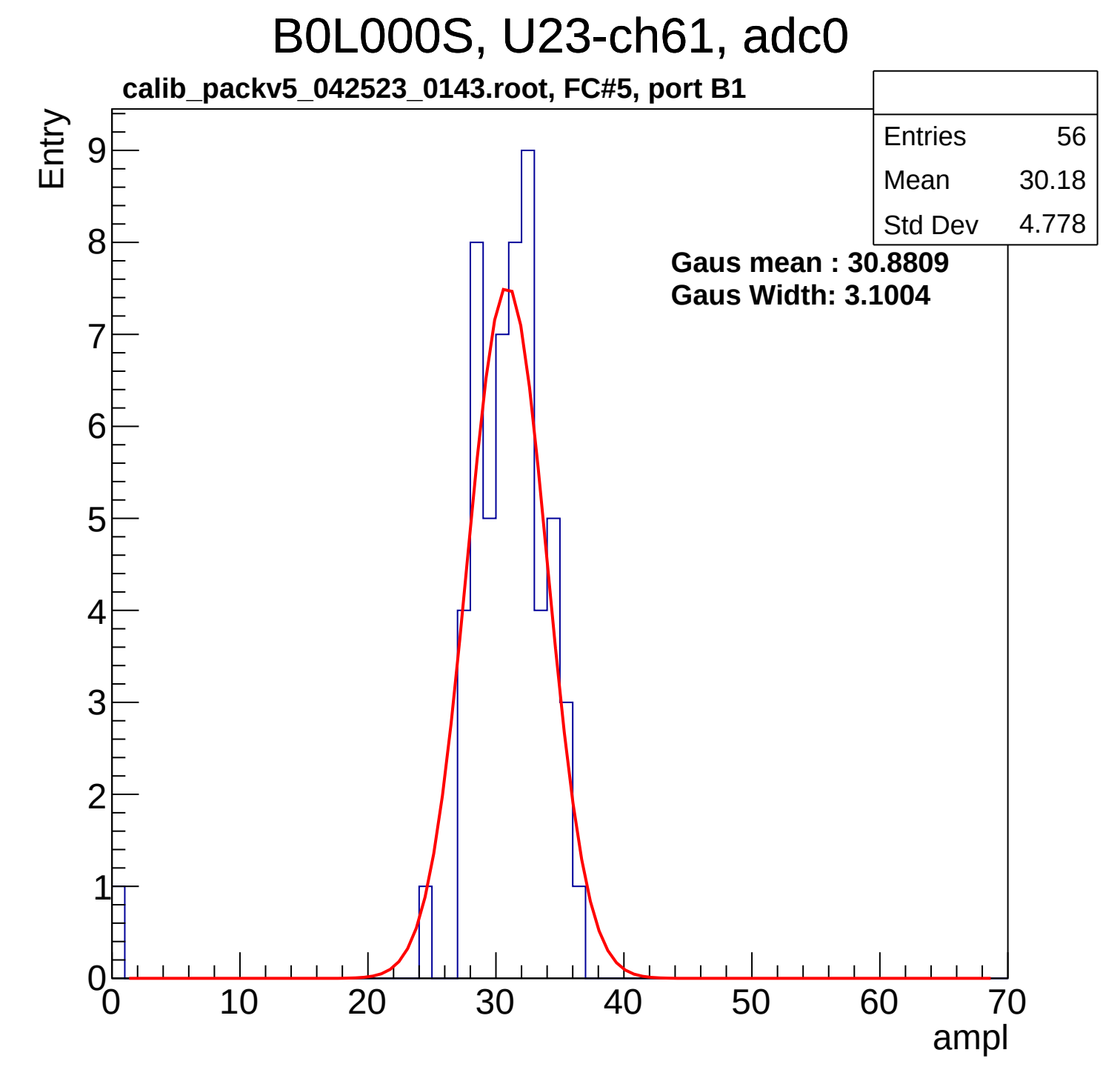

B0L000S, U23-ch60, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 47.94
Std Dev 3.651
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

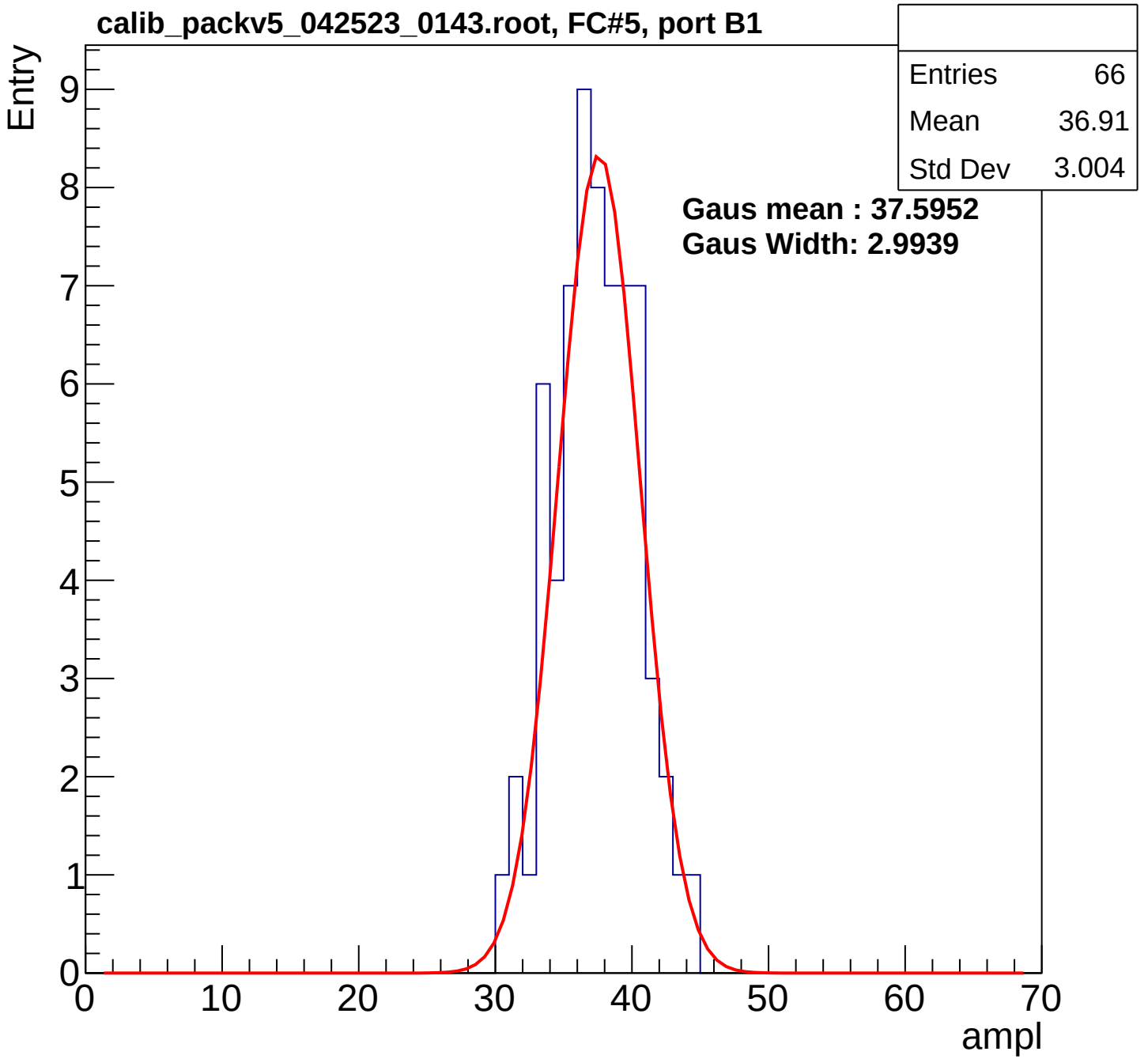

B0L000S, U23-ch60, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 52.9
Std Dev 3.198
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

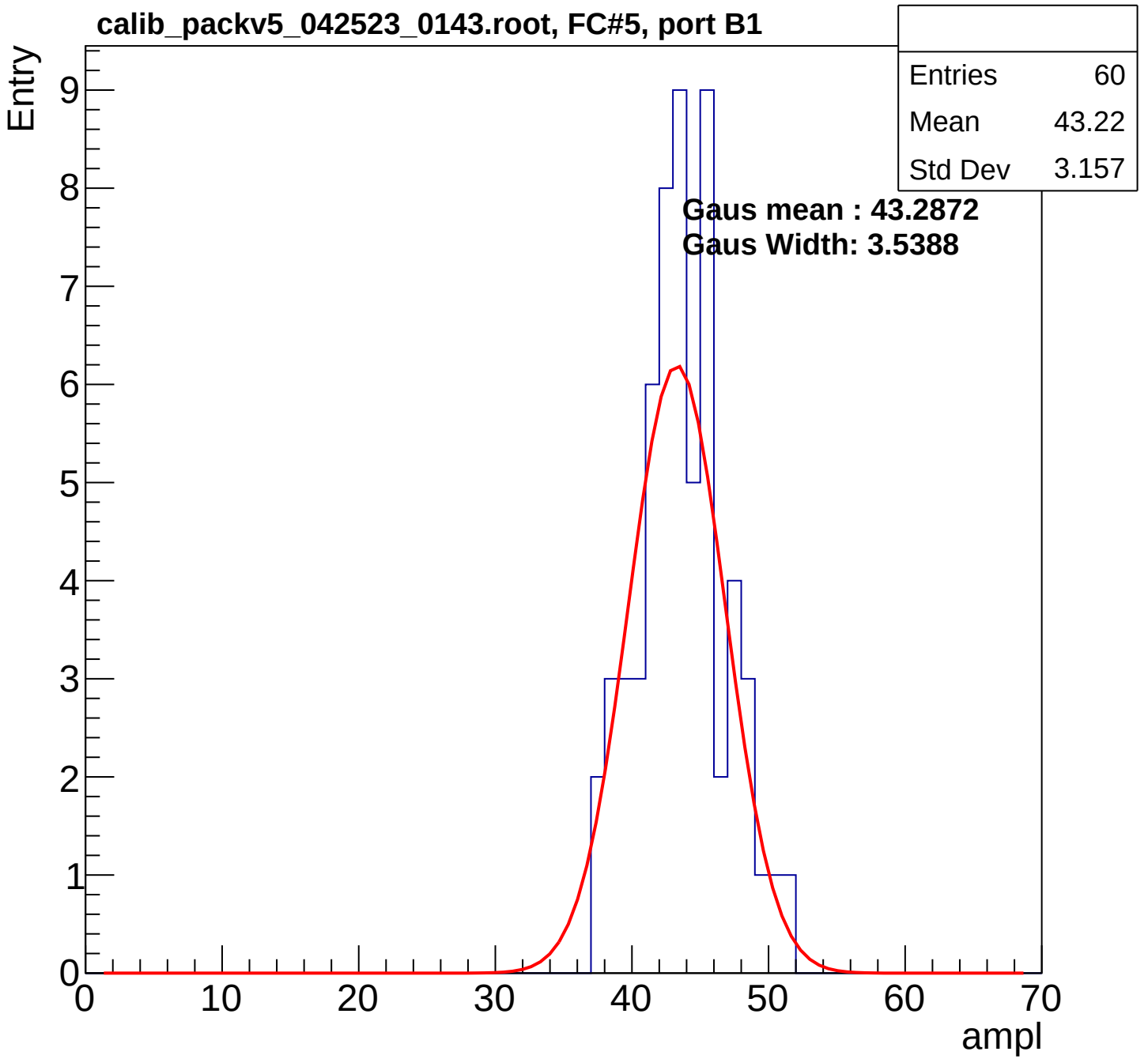

B0L000S, U23-ch60, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.38
Std Dev 3.045
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

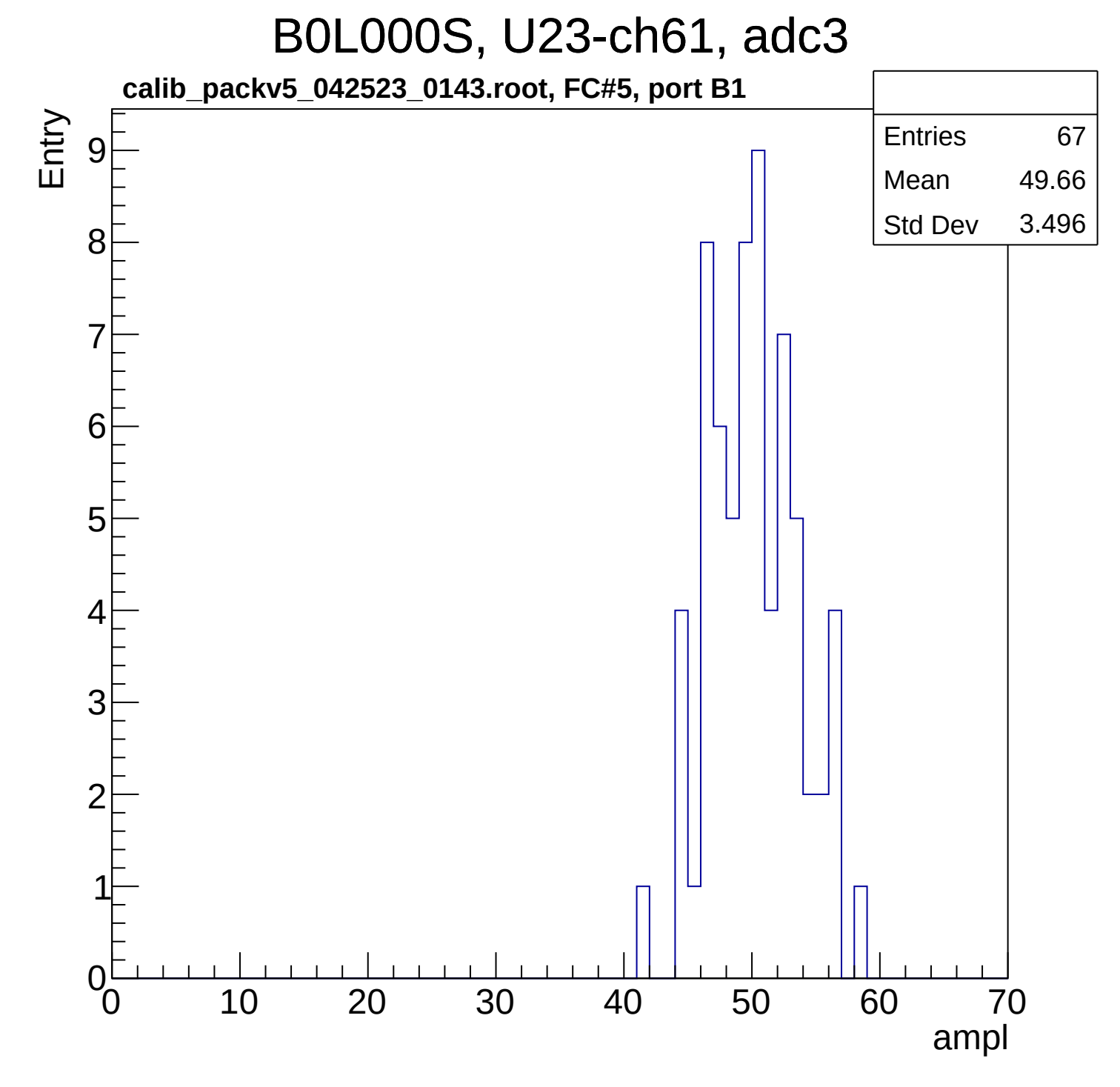

B0L000S, U23-ch60, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.38
Std Dev 12.31
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

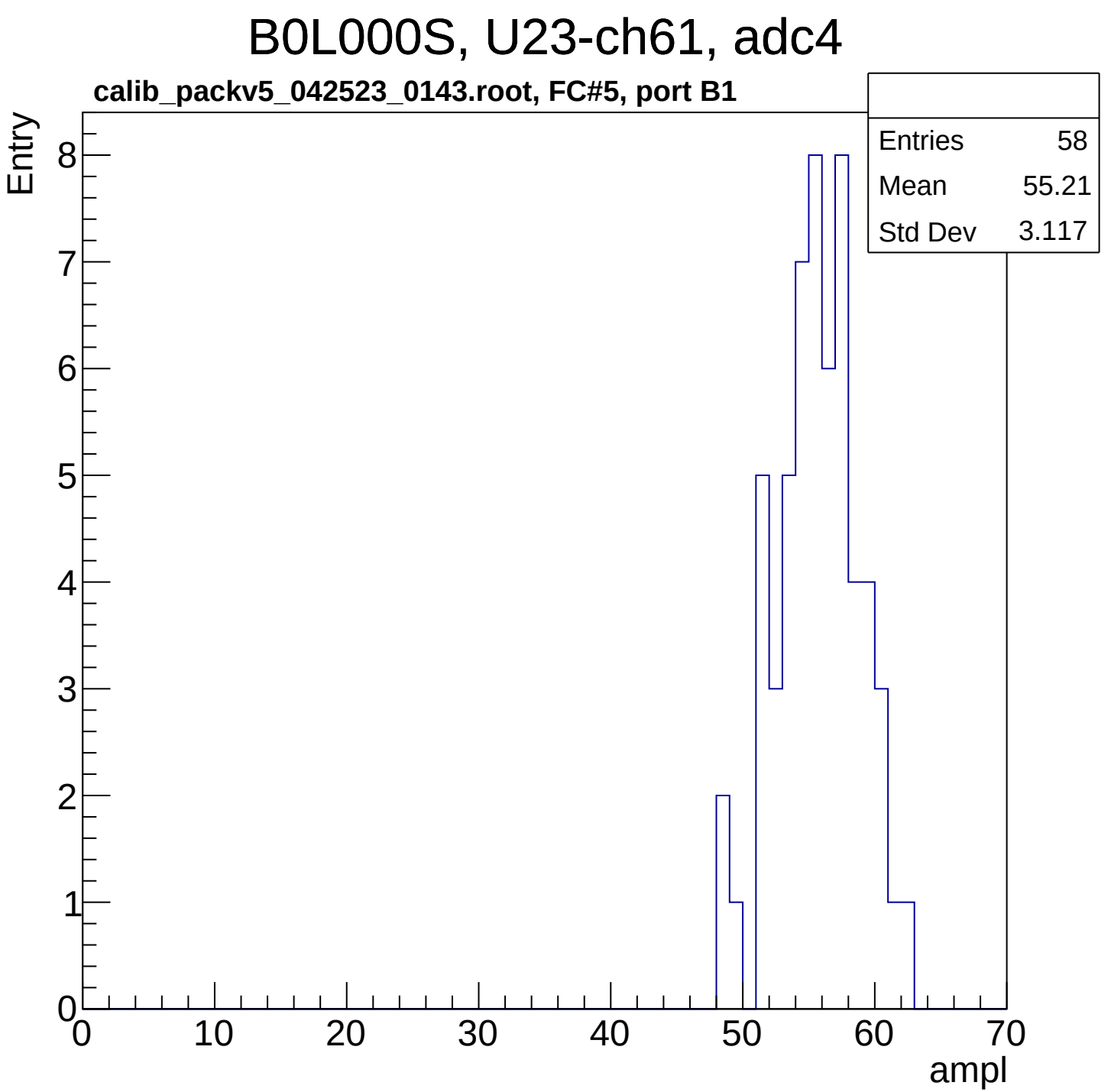

B0L000S, U23-ch60, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

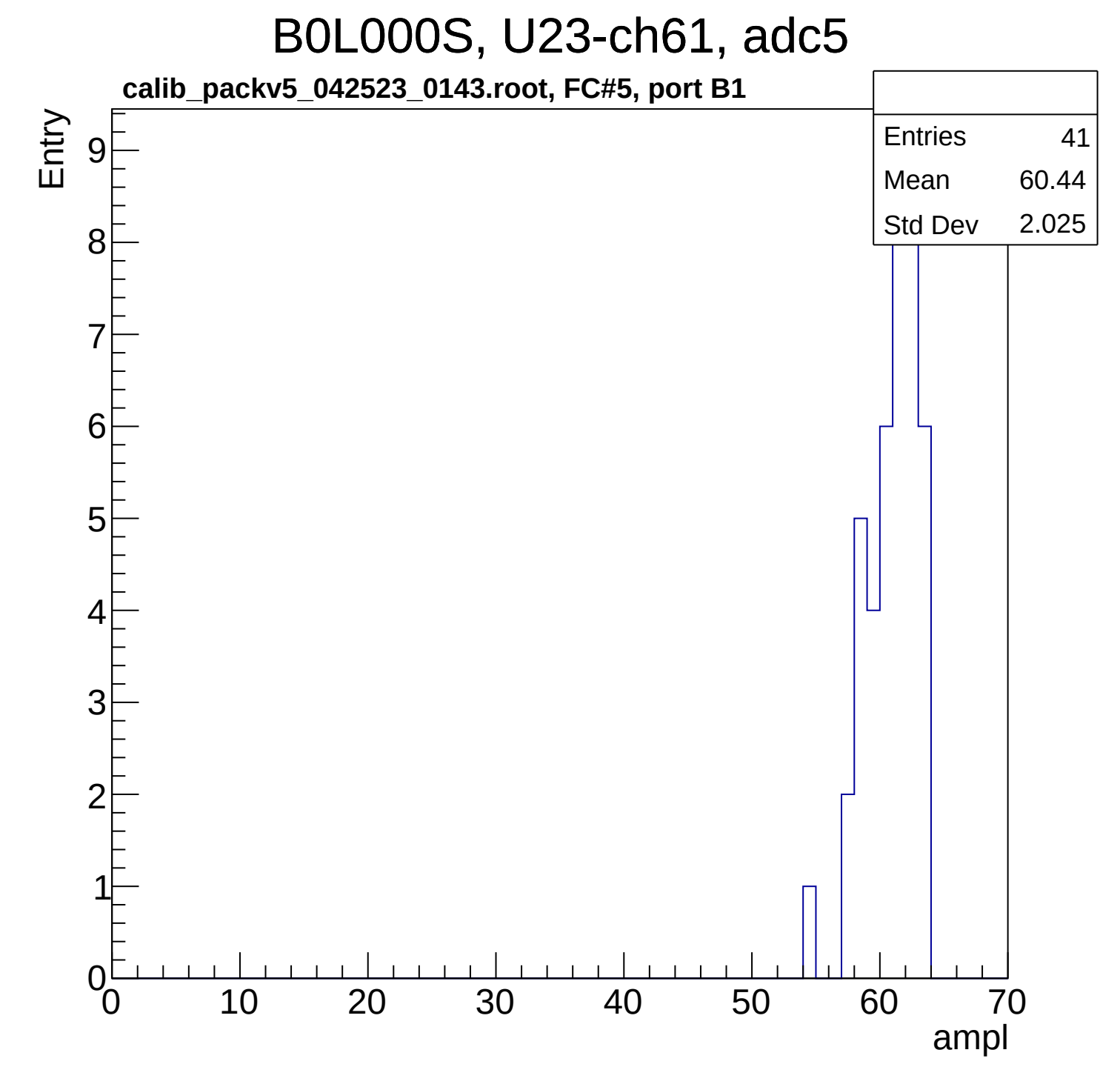

B0L000S, U23-ch61, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 30.18
Std Dev 4.778
Gaus mean : 30.8809
Gaus Width: 3.1004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

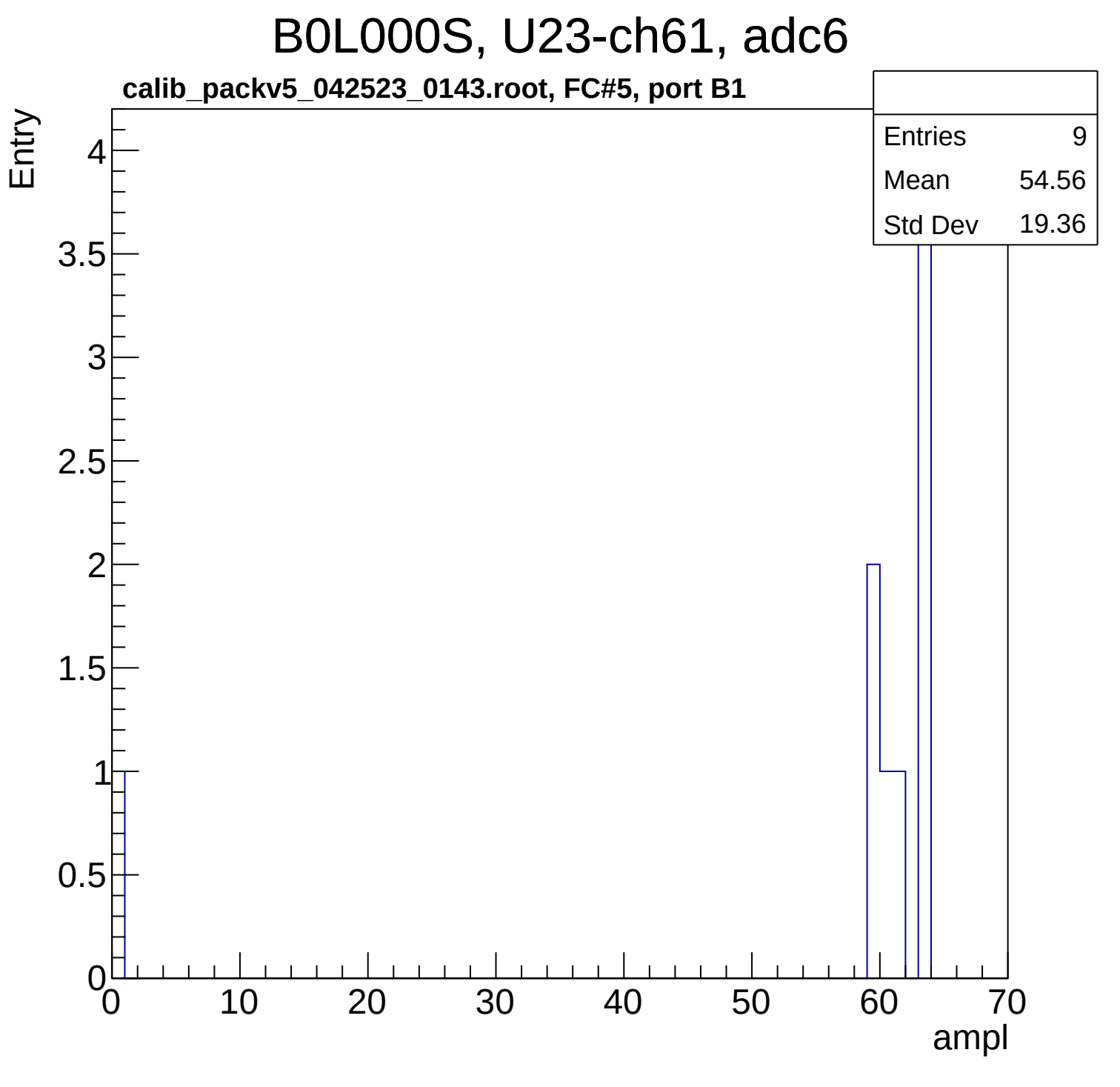

B0L000S, U23-ch61, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.91
Std Dev 3.004
Gaus mean : 37.5952
Gaus Width: 2.9939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

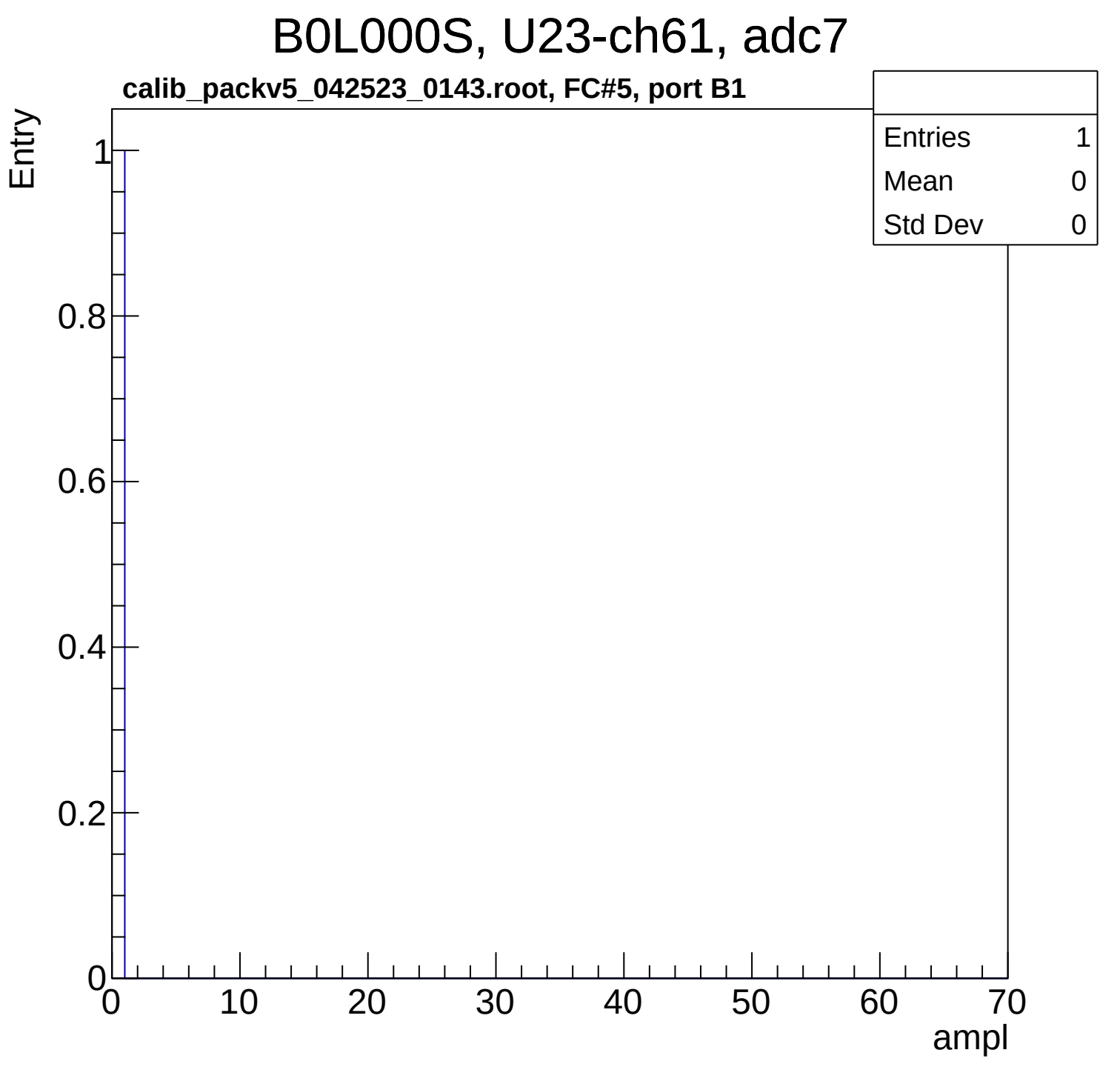

B0L000S, U23-ch61, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.22
Std Dev 3.157
Gaus mean : 43.2872
Gaus Width: 3.5388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

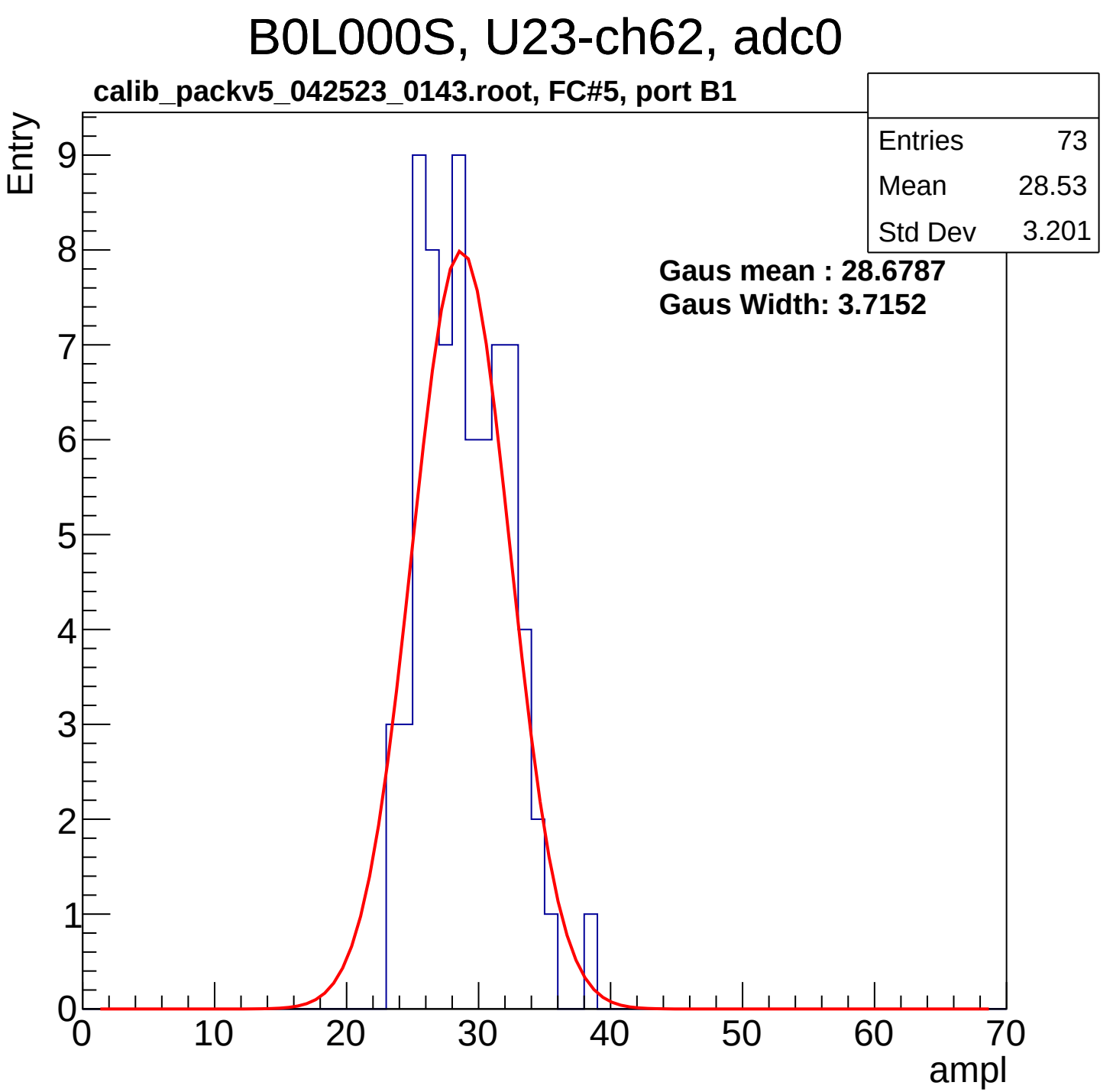

B0L000S, U23-ch61, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.66
Std Dev 3.496
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

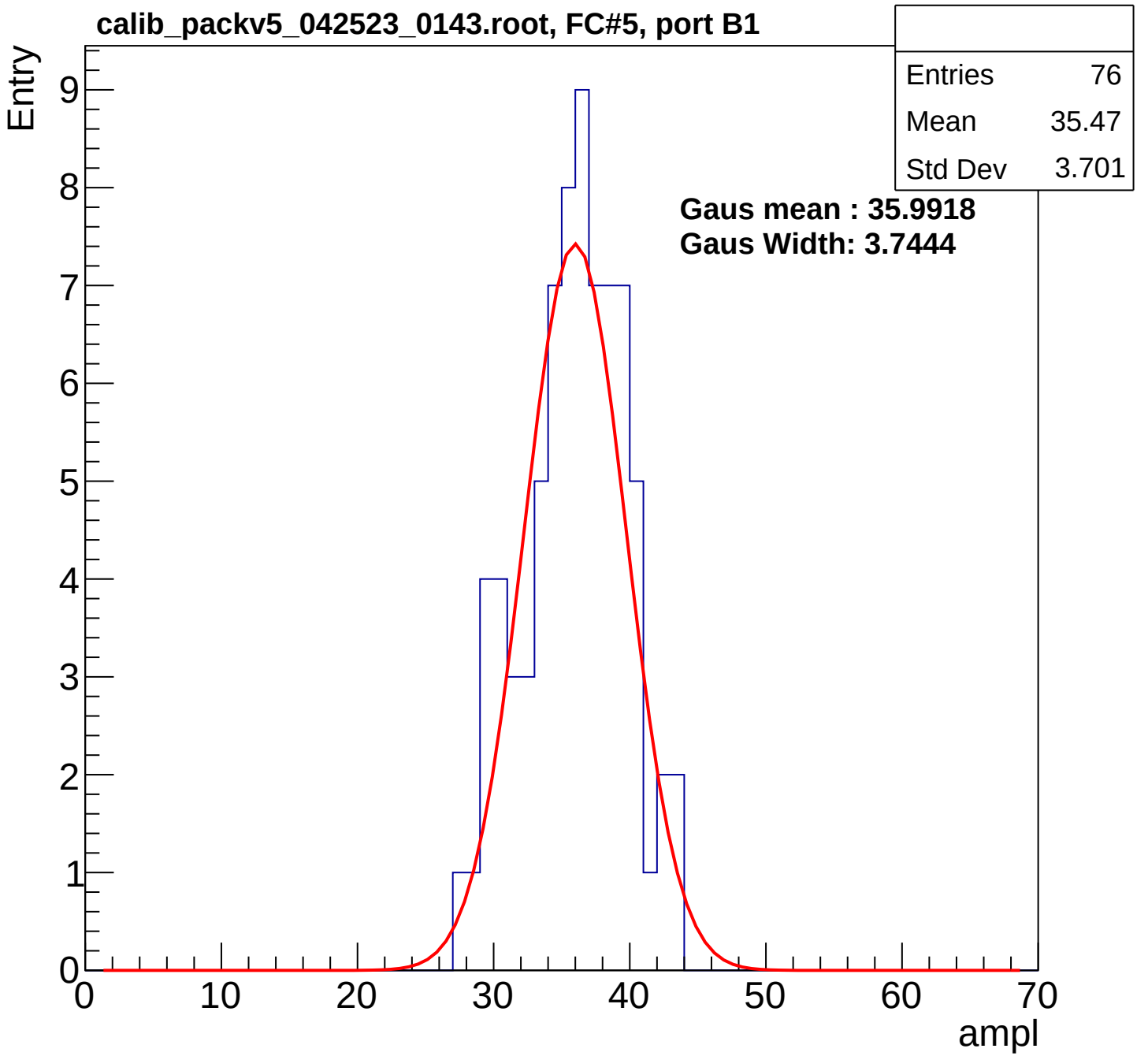

B0L000S, U23-ch61, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.21
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

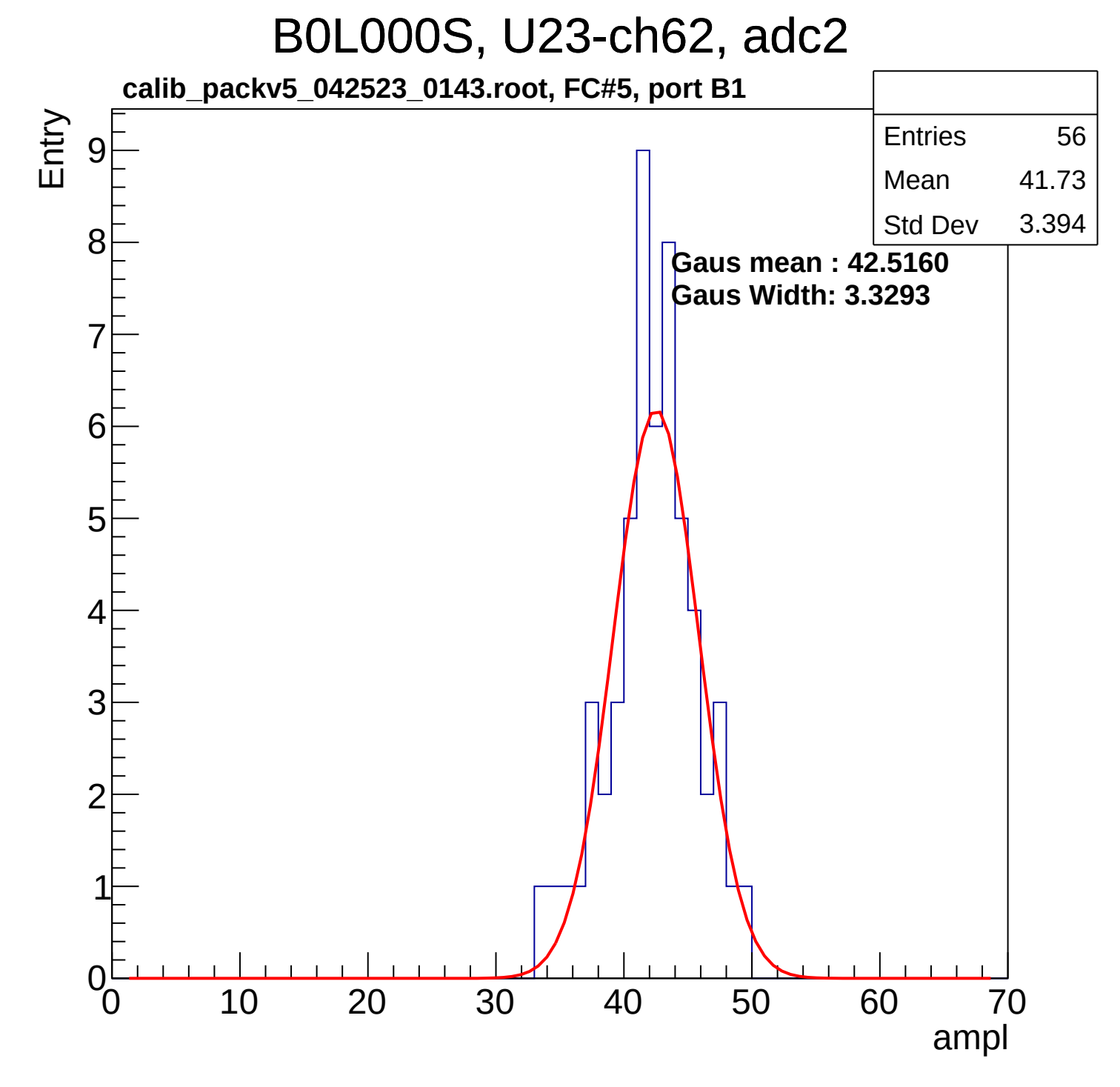

B0L000S, U23-ch61, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 60.44
Std Dev 2.025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

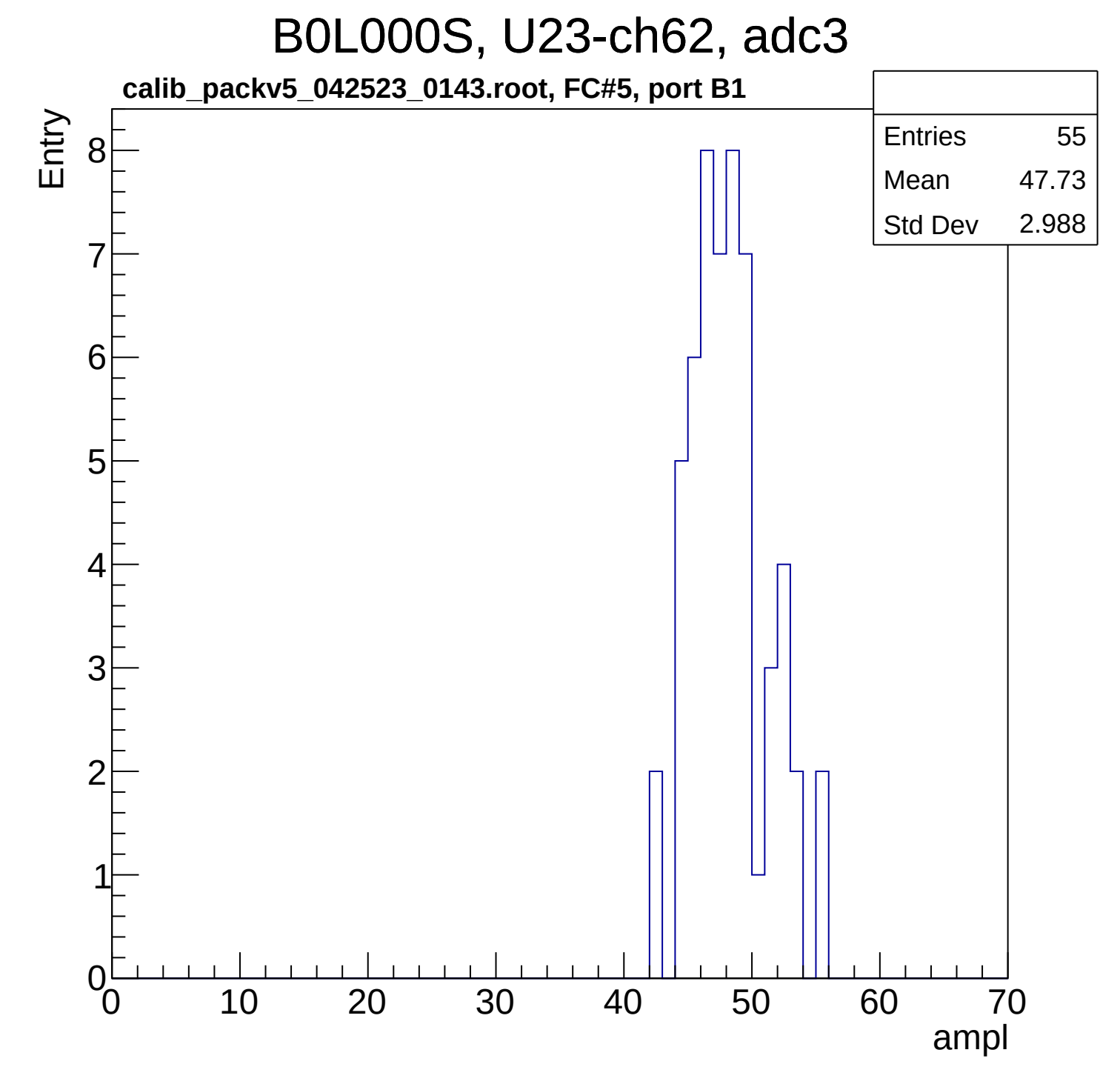

B0L000S, U23-ch61, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 54.56
Std Dev 19.36
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

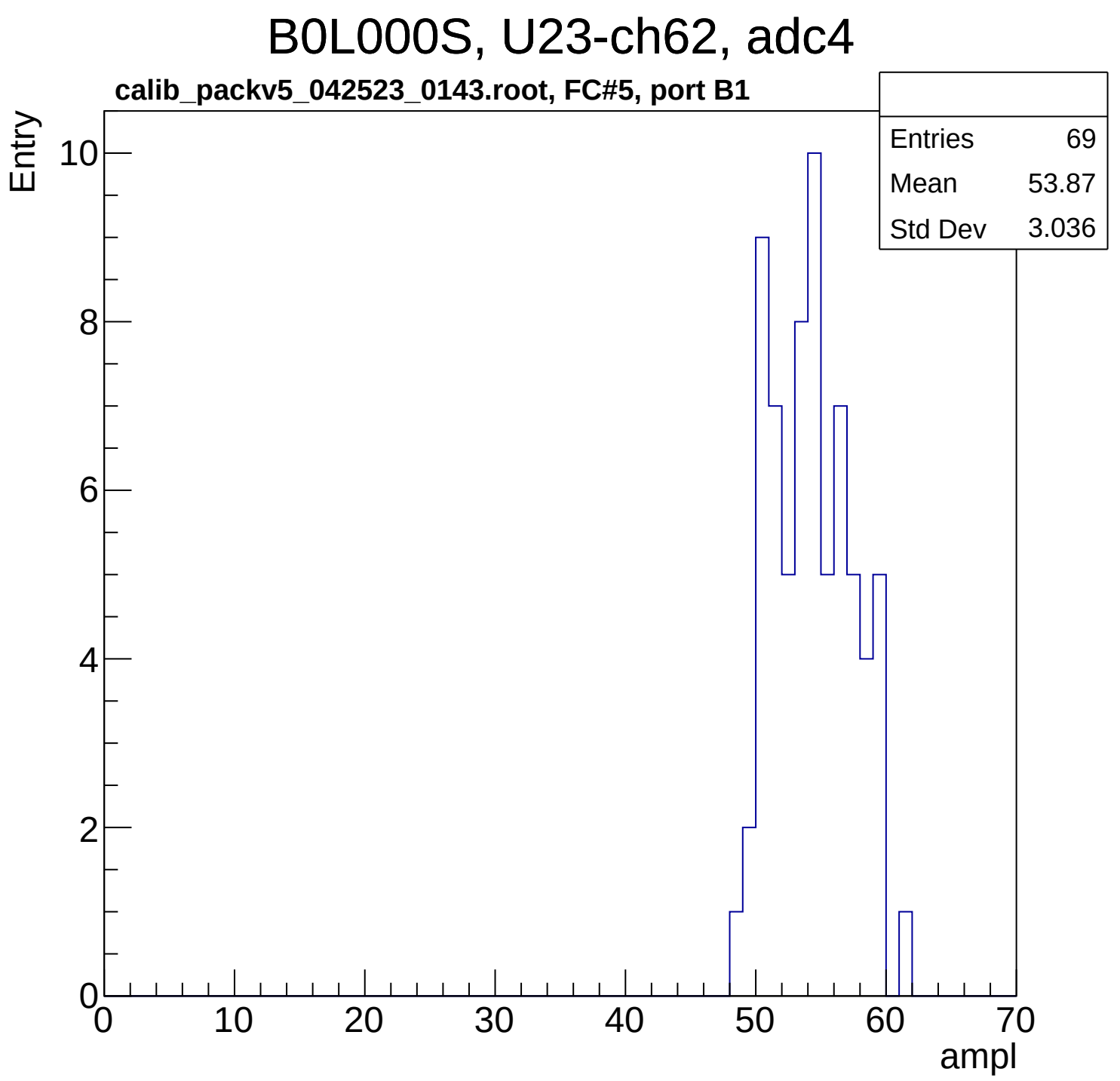

B0L000S, U23-ch61, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

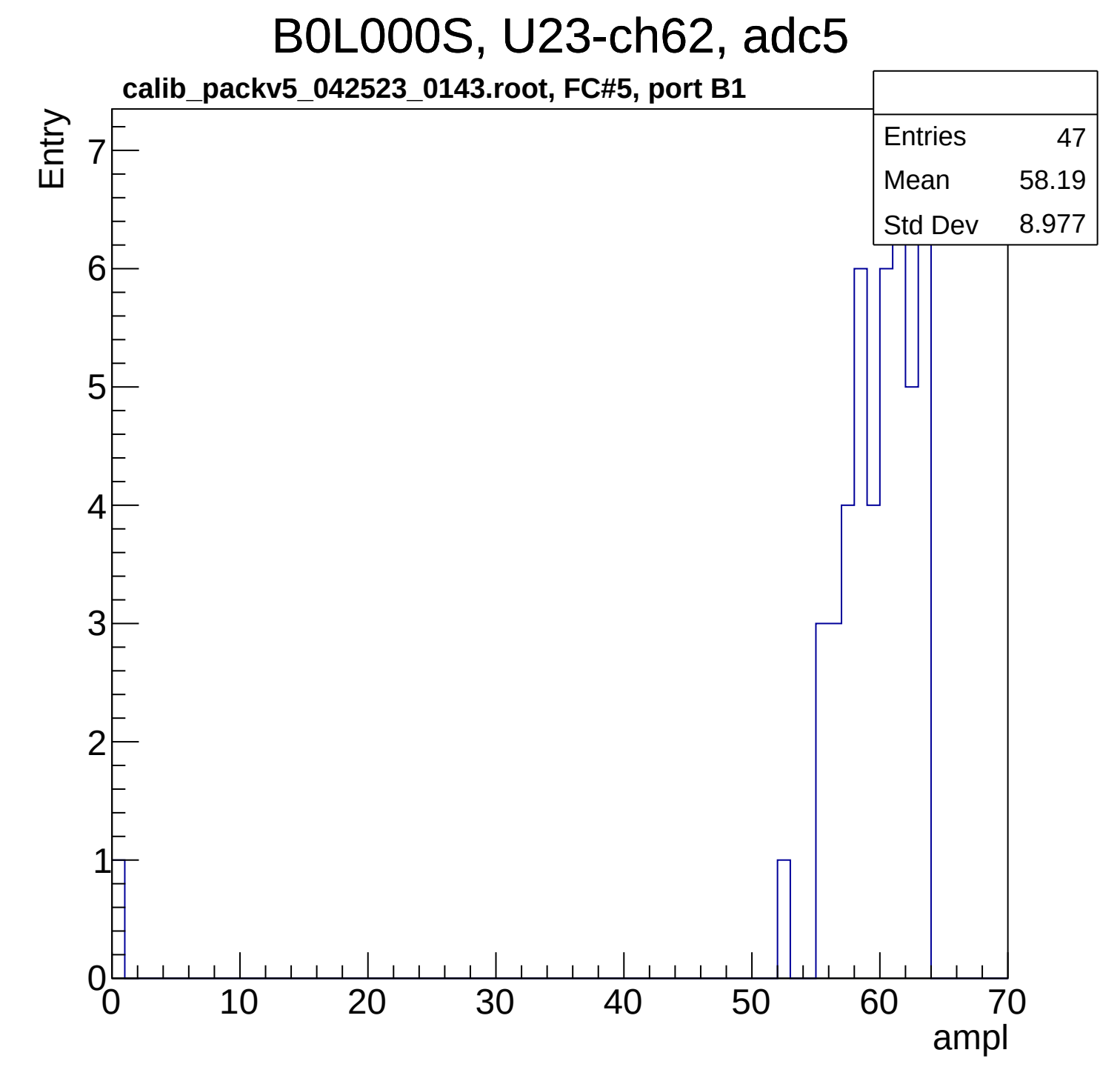

B0L000S, U23-ch62, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 28.53
Std Dev 3.201
Gaus mean : 28.6787
Gaus Width: 3.7152
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

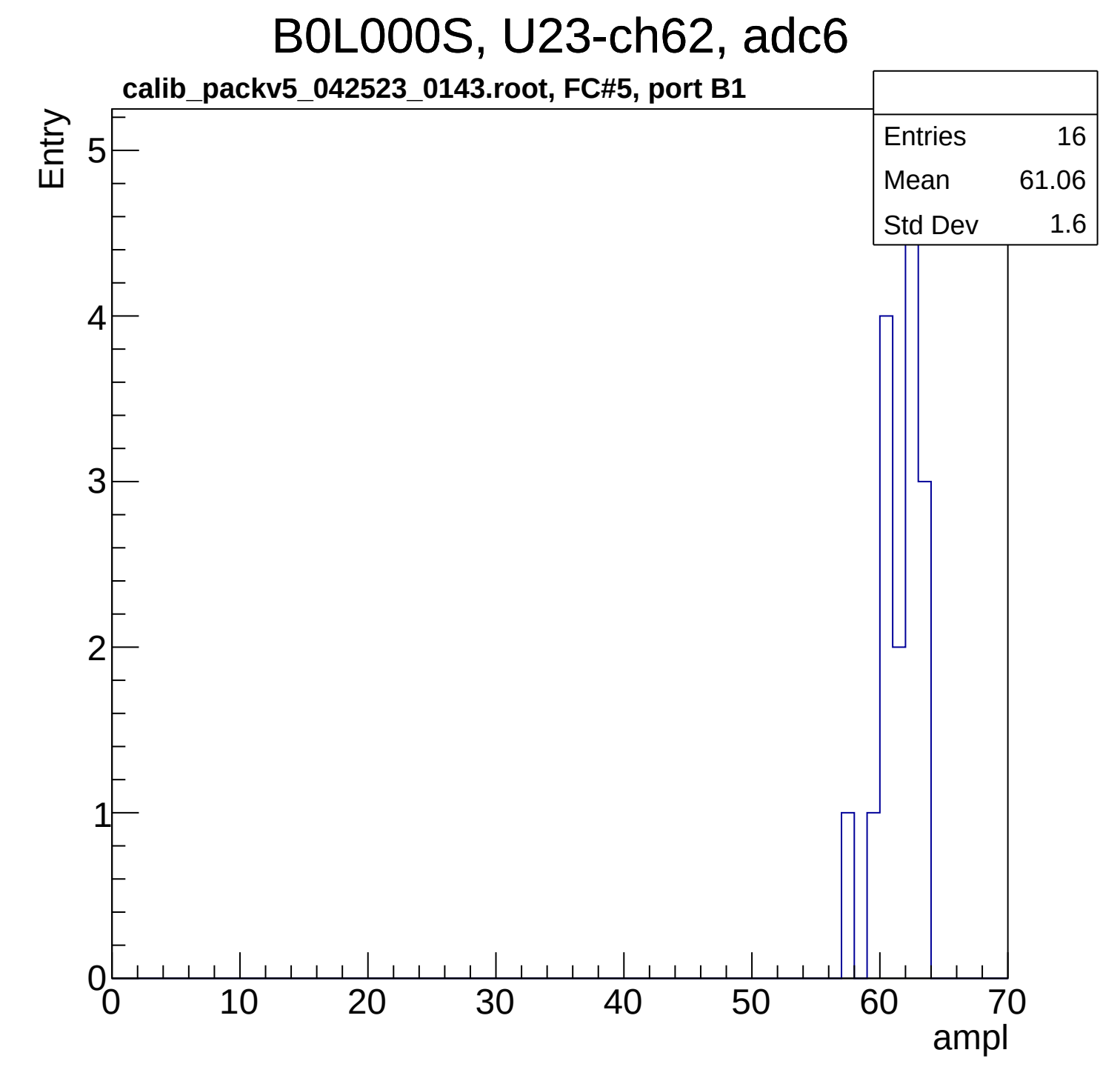

B0L000S, U23-ch62, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 35.47
Std Dev 3.701
Gaus mean : 35.9918
Gaus Width: 3.7444
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

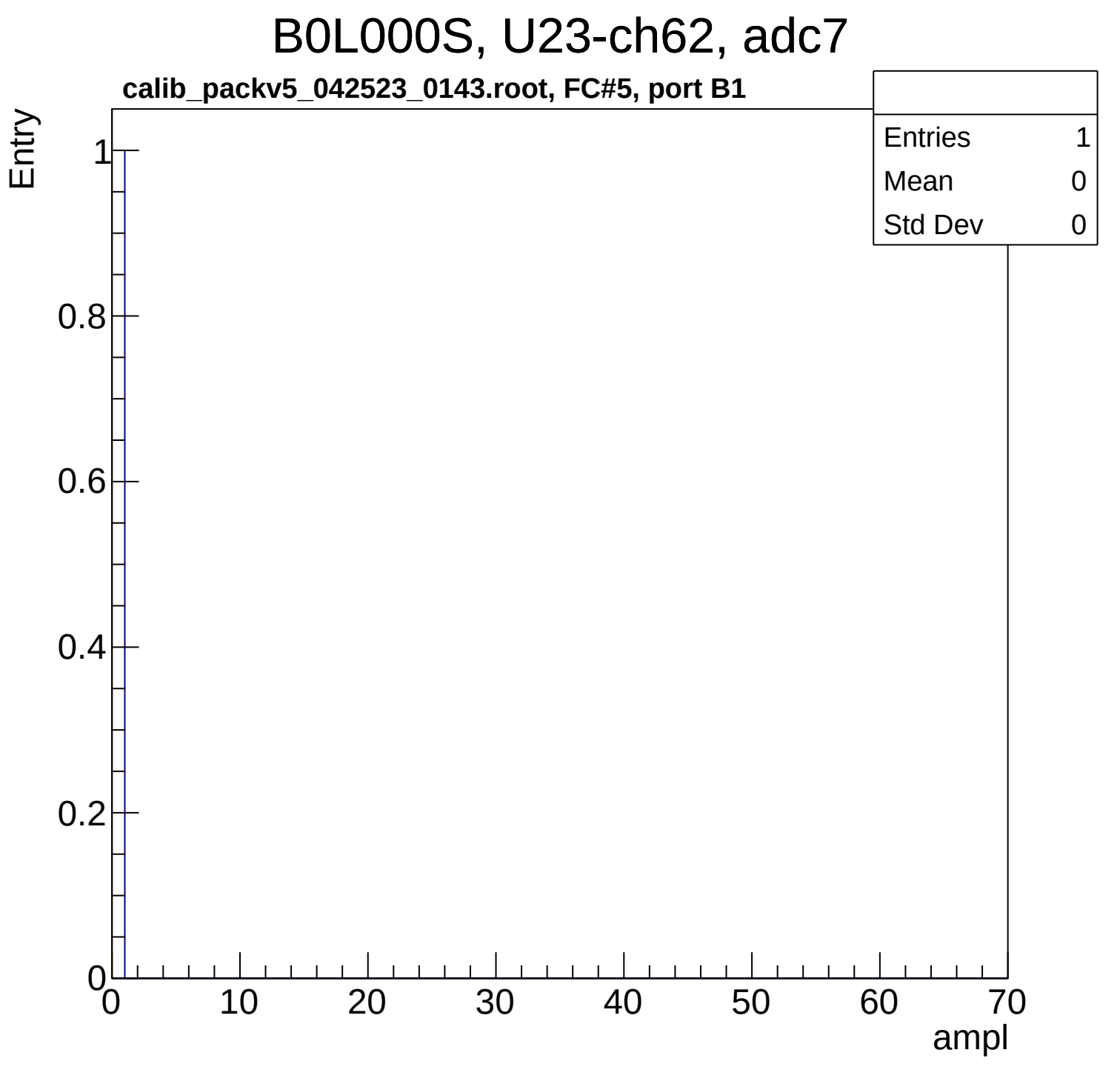

B0L000S, U23-ch62, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 41.73
Std Dev 3.394
Gaus mean : 42.5160
Gaus Width: 3.3293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

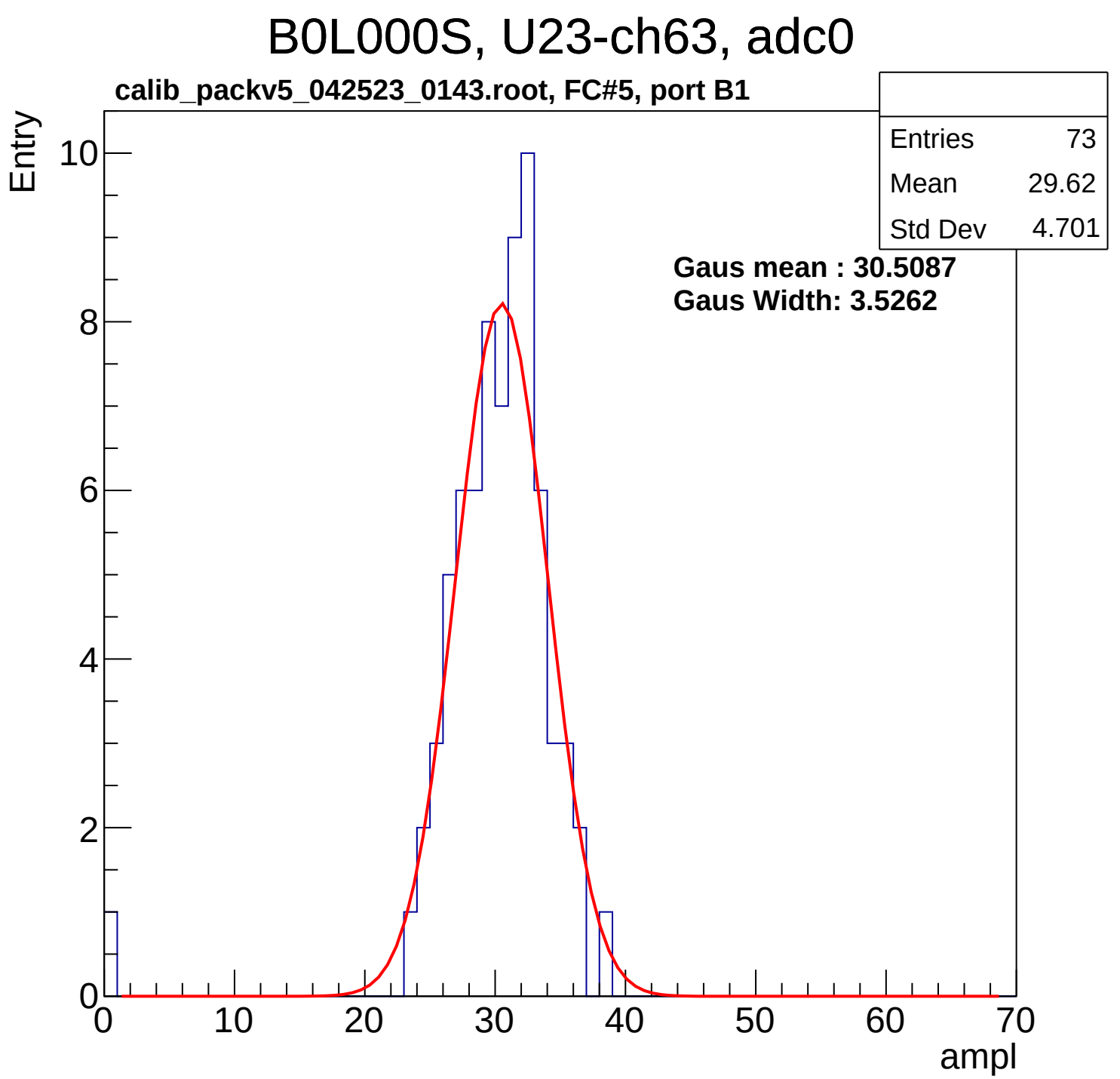

B0L000S, U23-ch62, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 47.73
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

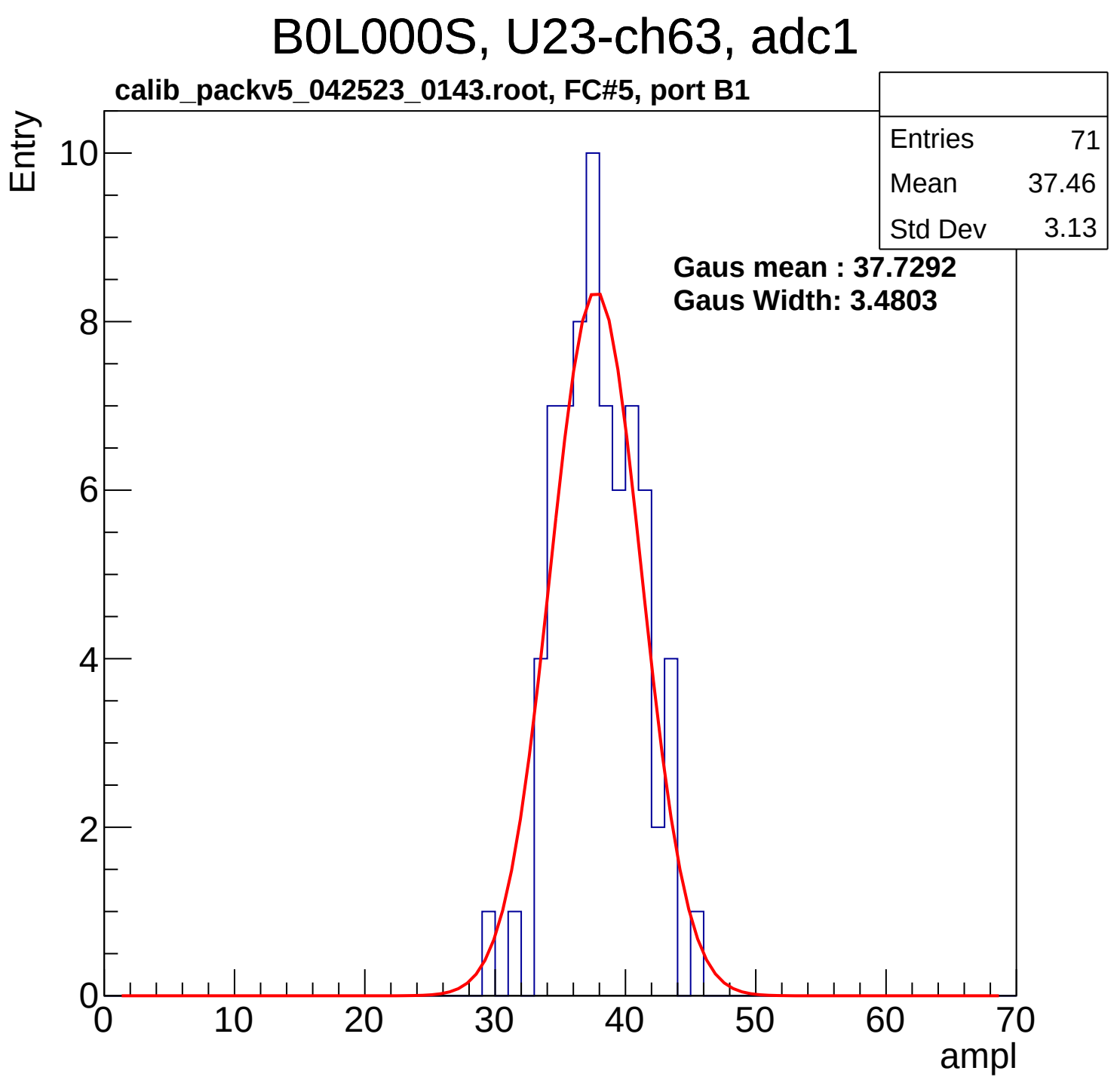

B0L000S, U23-ch62, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 53.87
Std Dev 3.036
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

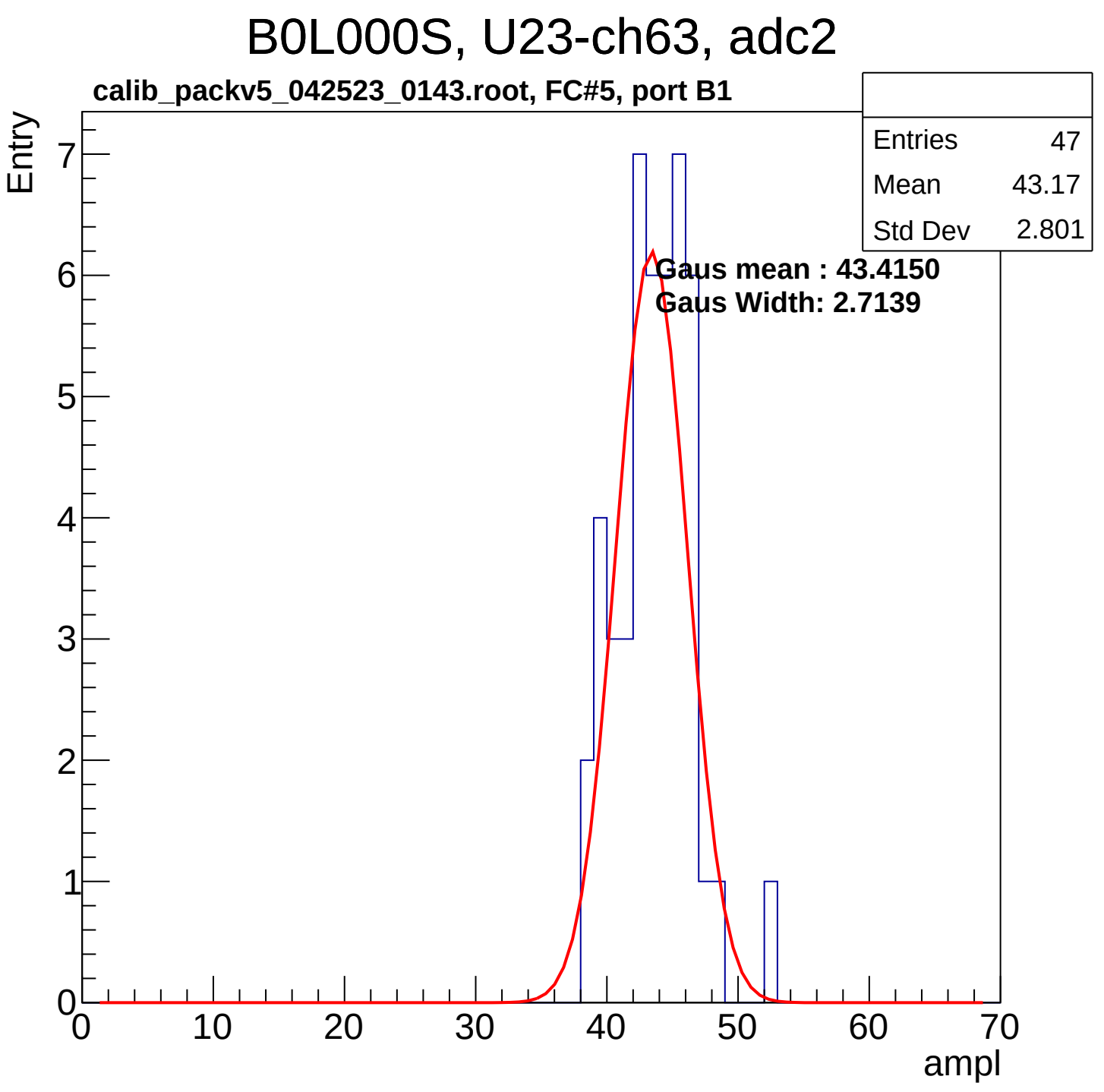

B0L000S, U23-ch62, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.19
Std Dev 8.977
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

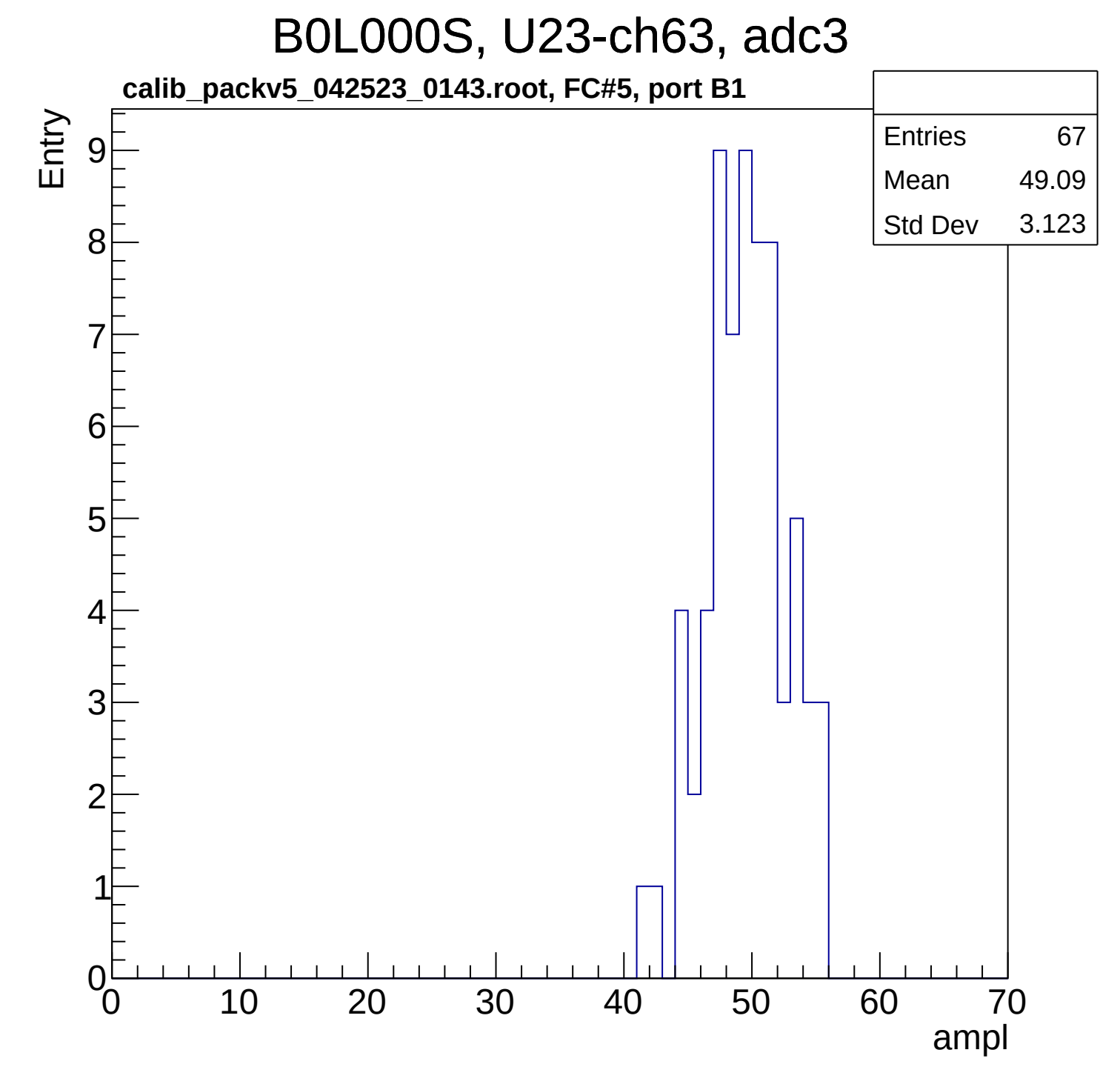

B0L000S, U23-ch62, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.06
Std Dev 1.6
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

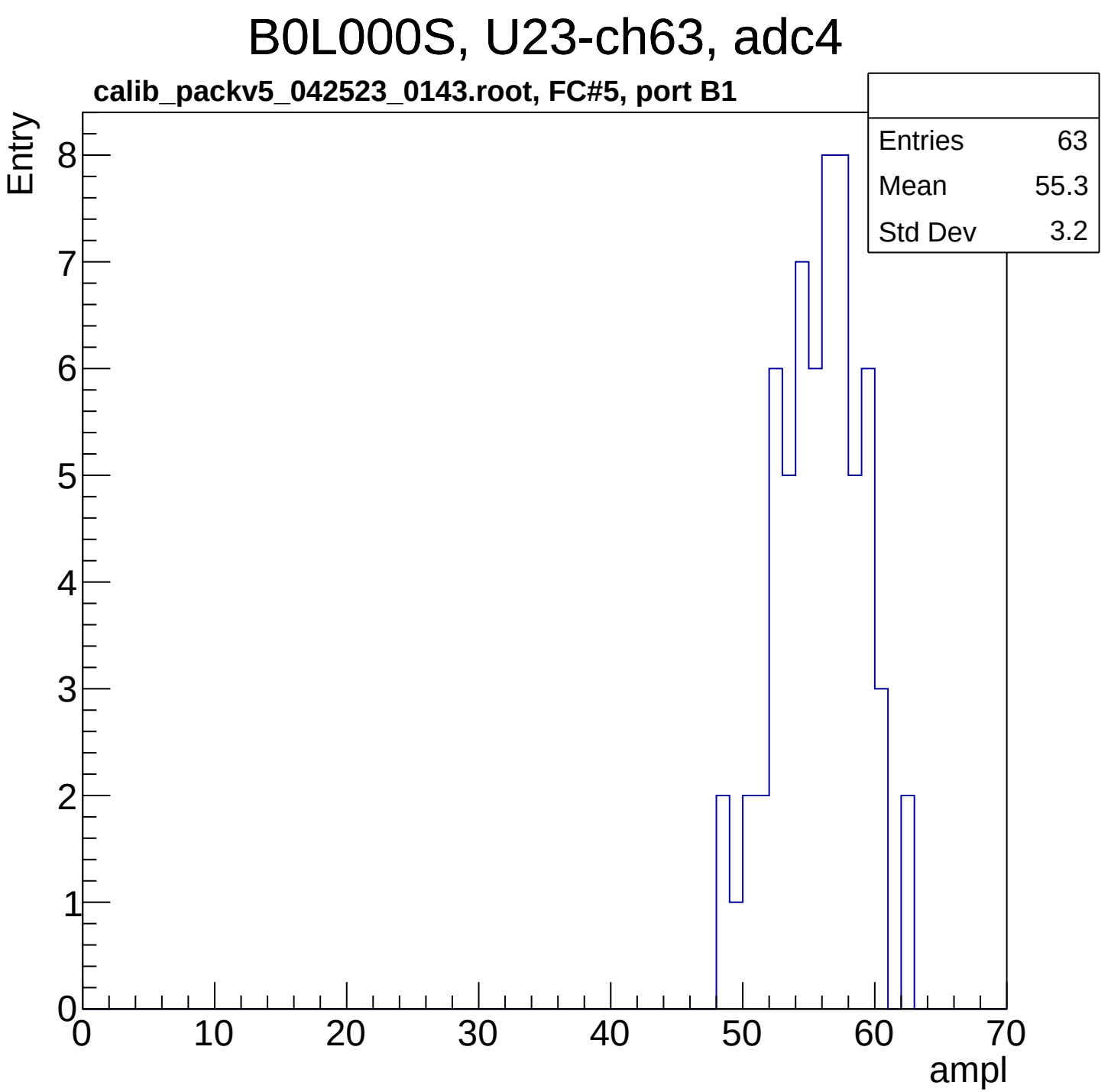

B0L000S, U23-ch62, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

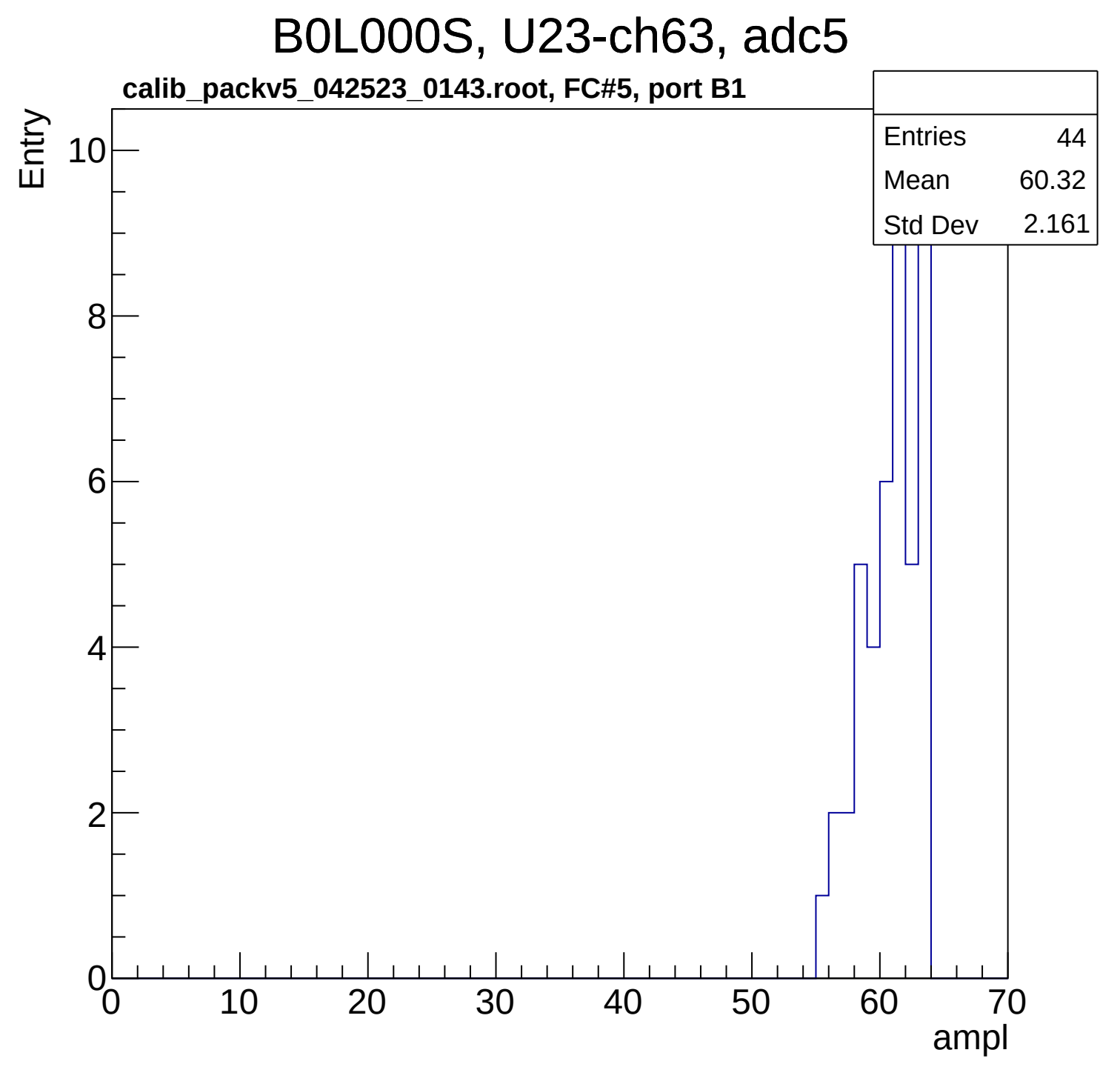

B0L000S, U23-ch63, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.62
Std Dev 4.701
Gaus mean : 30.5087
Gaus Width: 3.5262
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

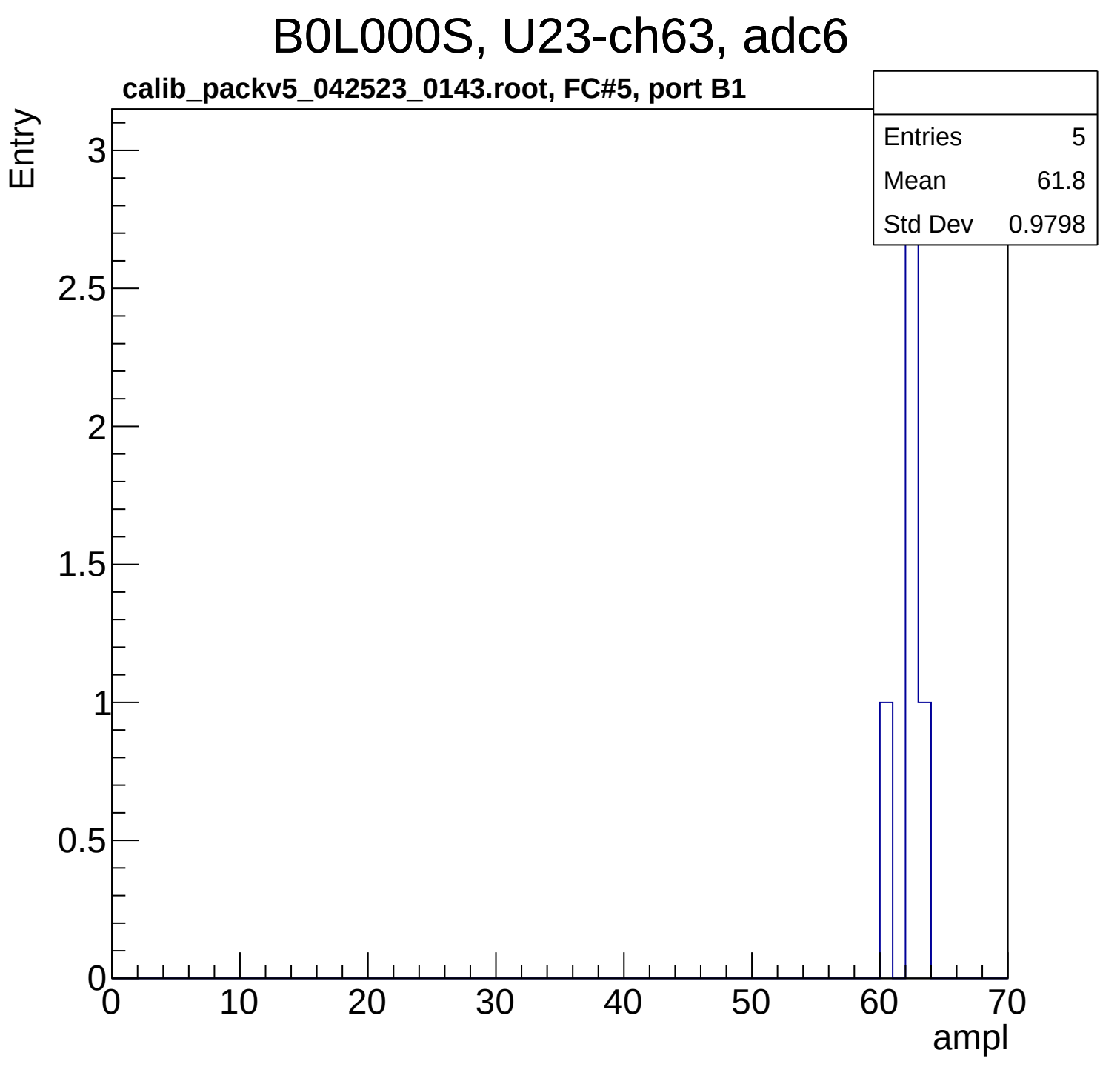

B0L000S, U23-ch63, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 37.46
Std Dev 3.13
Gaus mean : 37.7292
Gaus Width: 3.4803
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

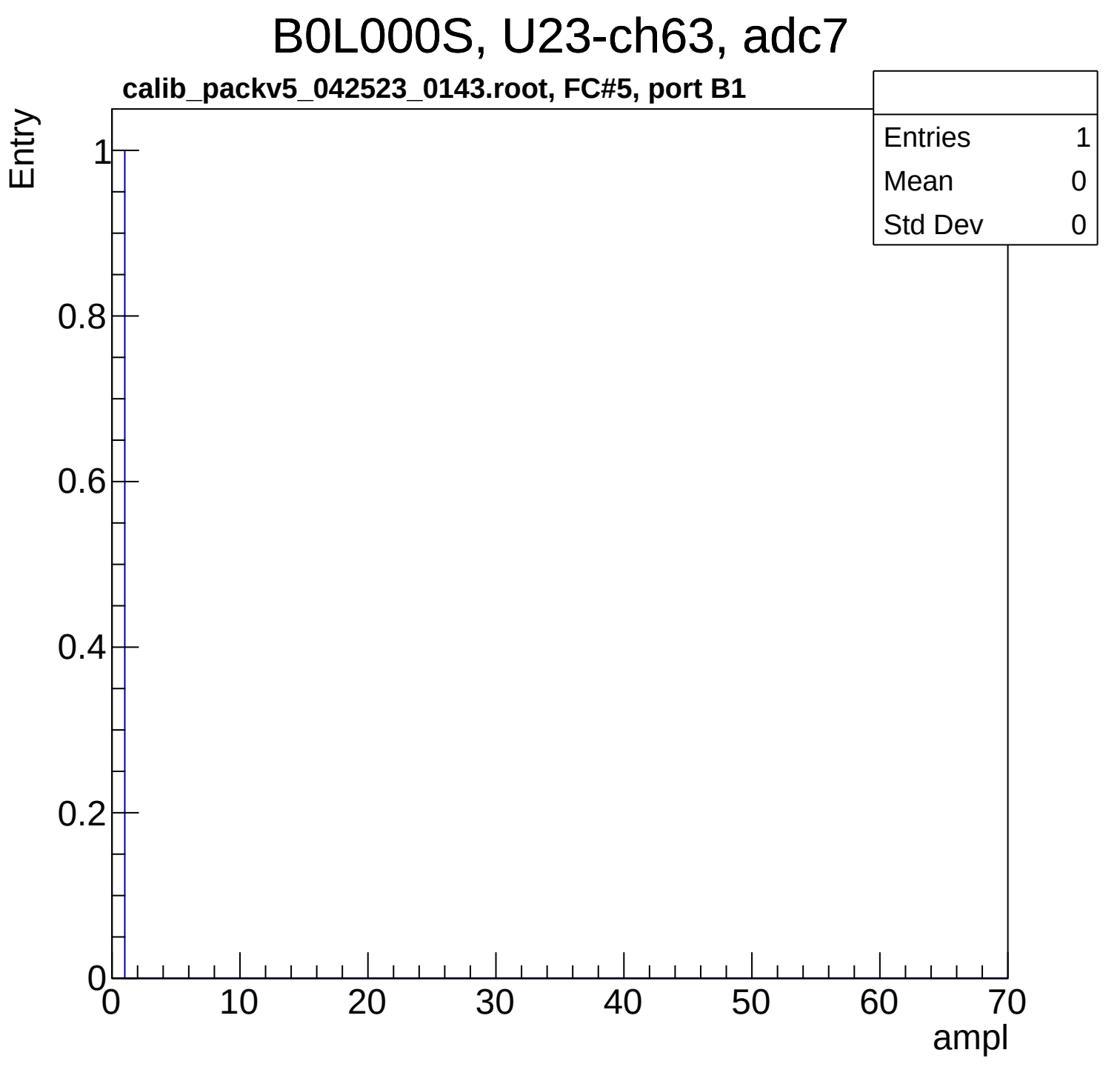

B0L000S, U23-ch63, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 43.17
Std Dev 2.801
Gaus mean : 43.4150
Gaus Width: 2.7139
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

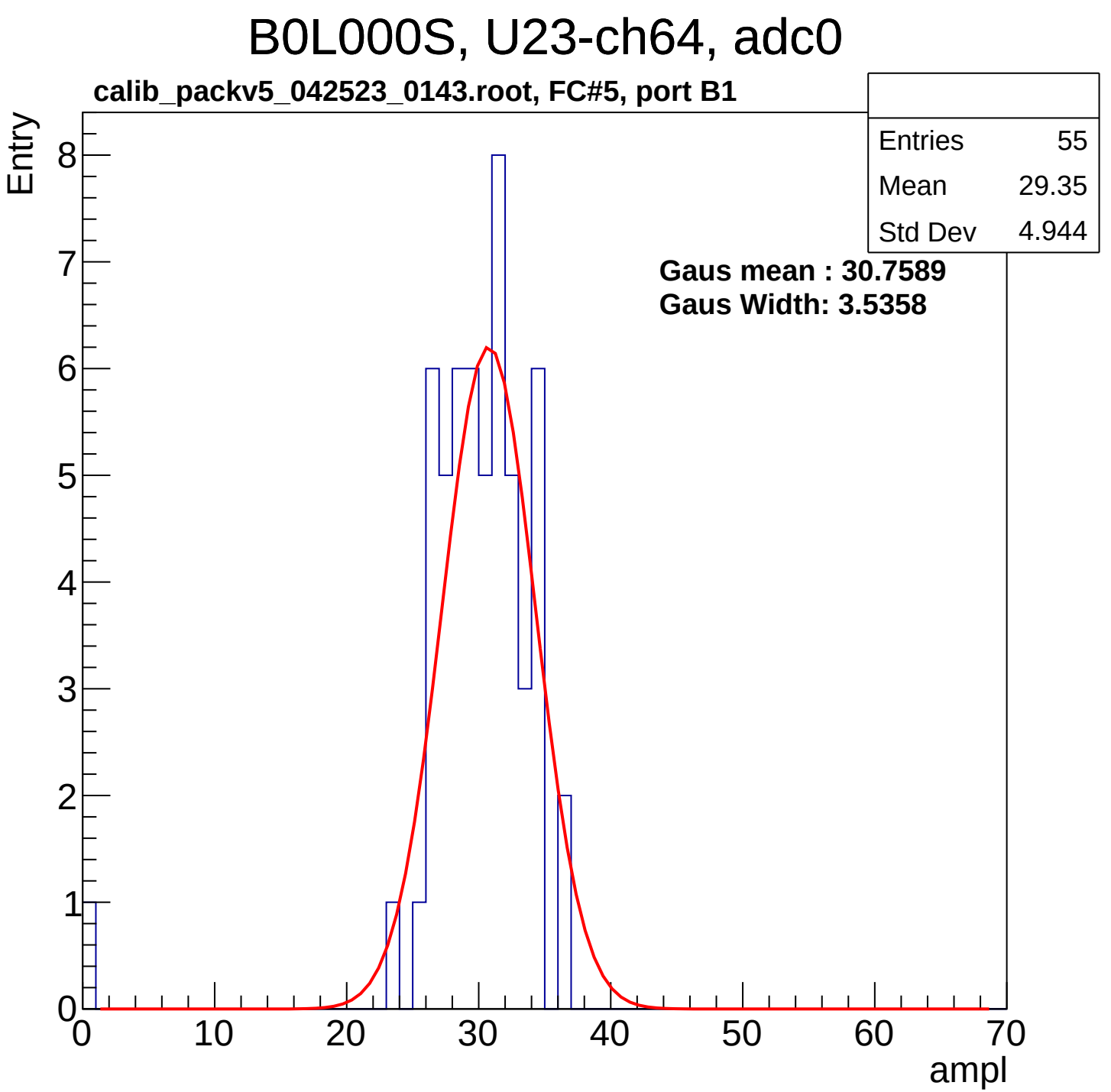

B0L000S, U23-ch63, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.09
Std Dev 3.123
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

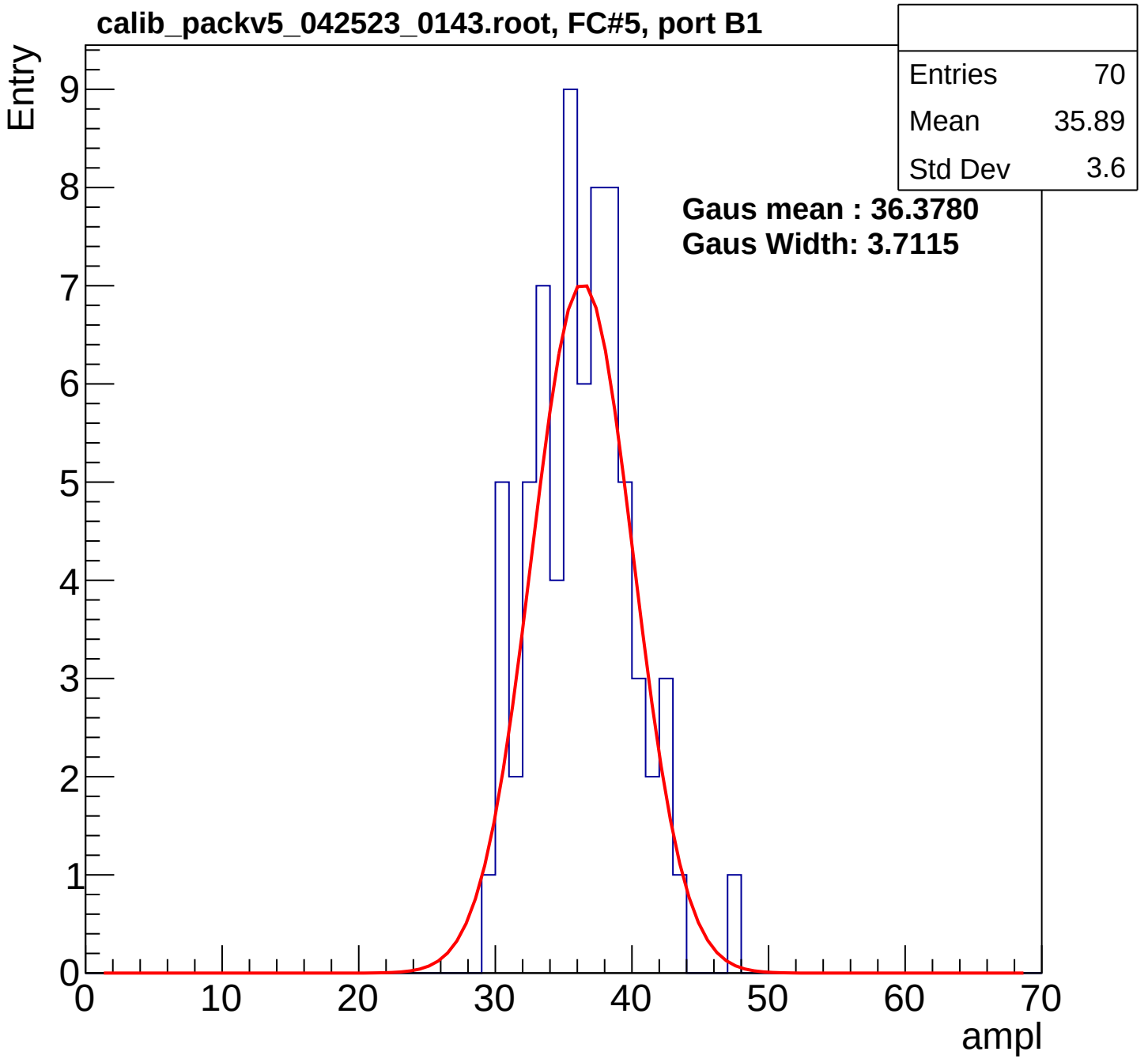

B0L000S, U23-ch63, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 55.3
Std Dev 3.2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

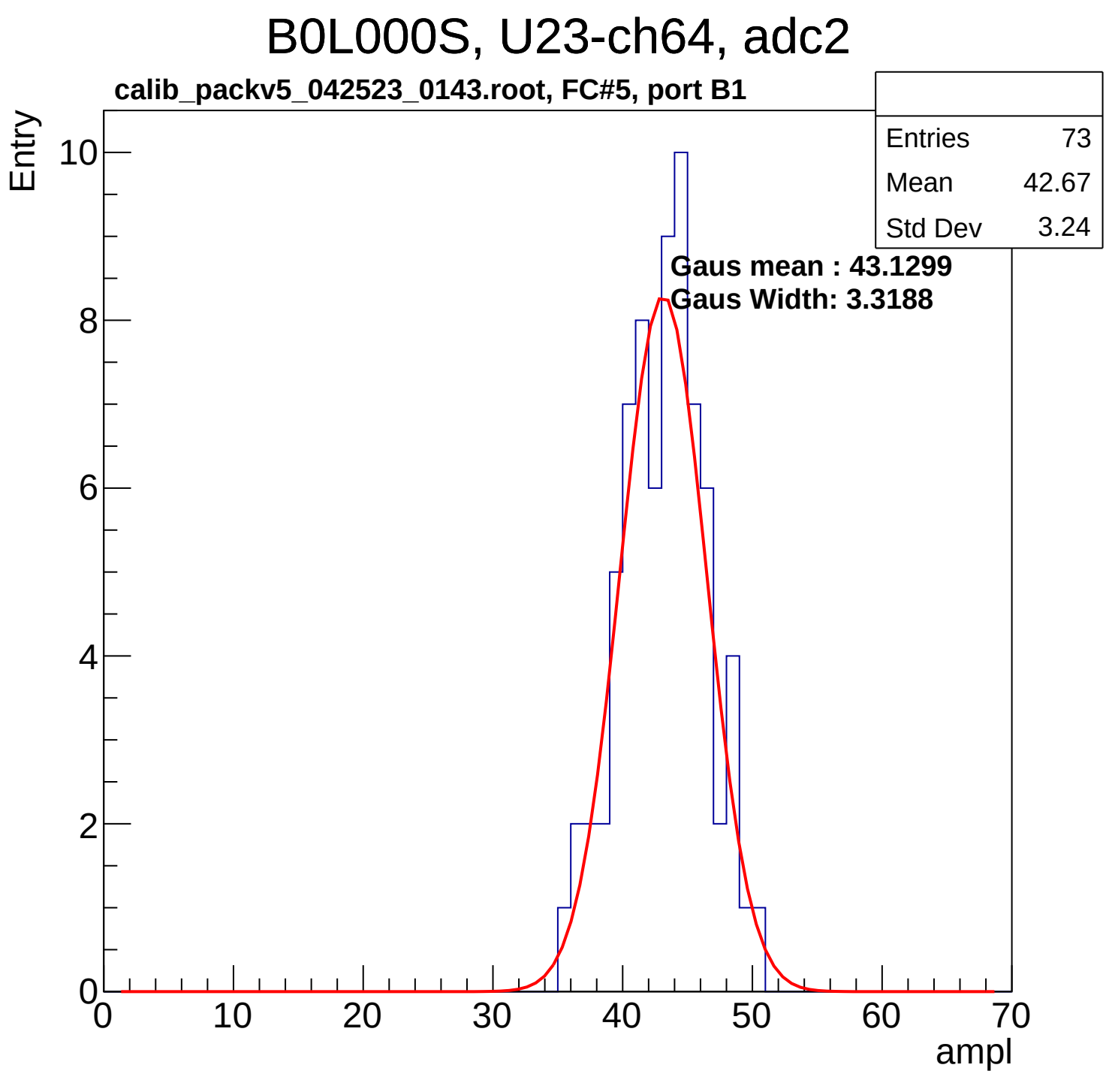

B0L000S, U23-ch63, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 60.32
Std Dev 2.161
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

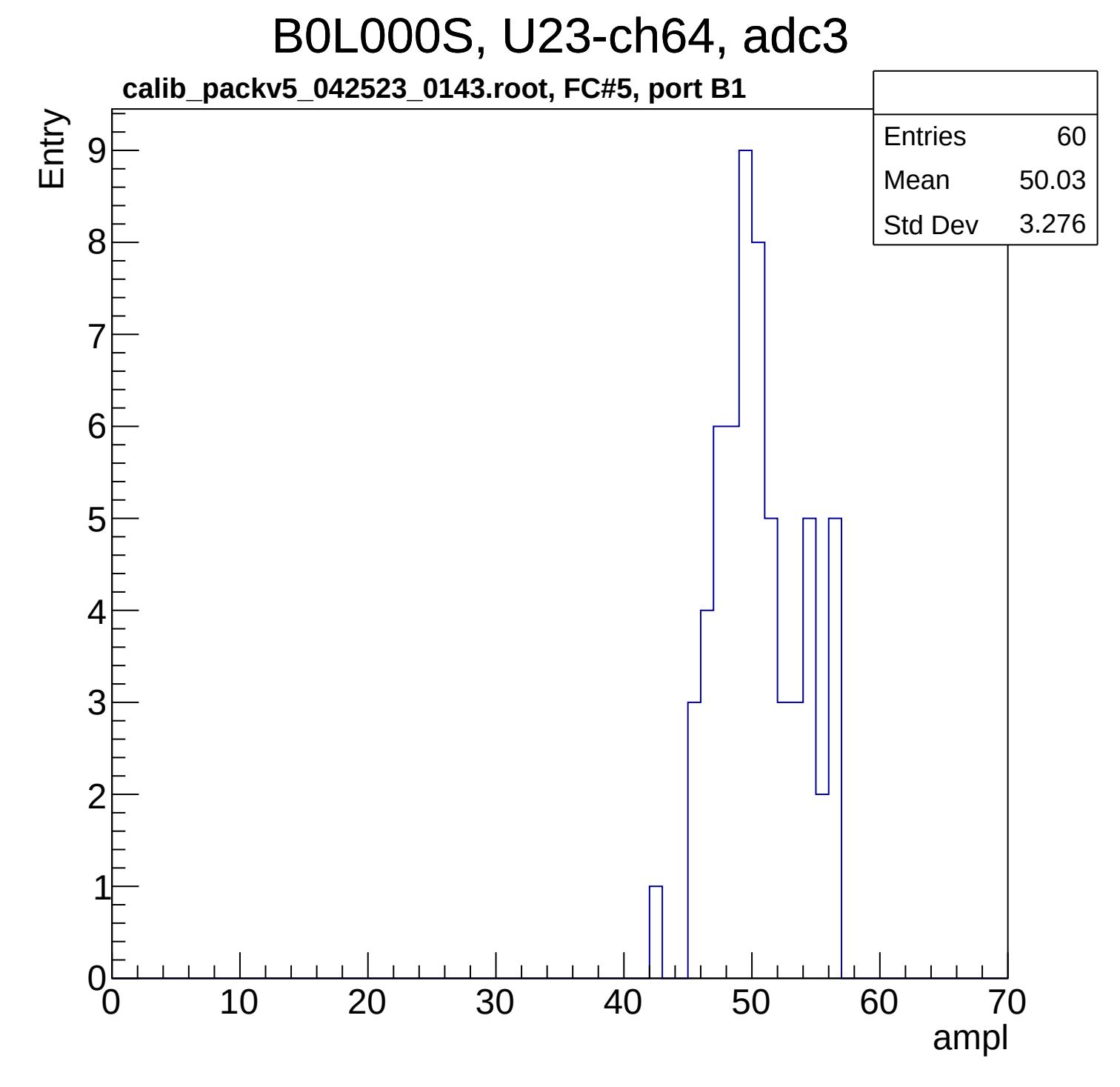

B0L000S, U23-ch63, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.8
Std Dev 0.9798
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

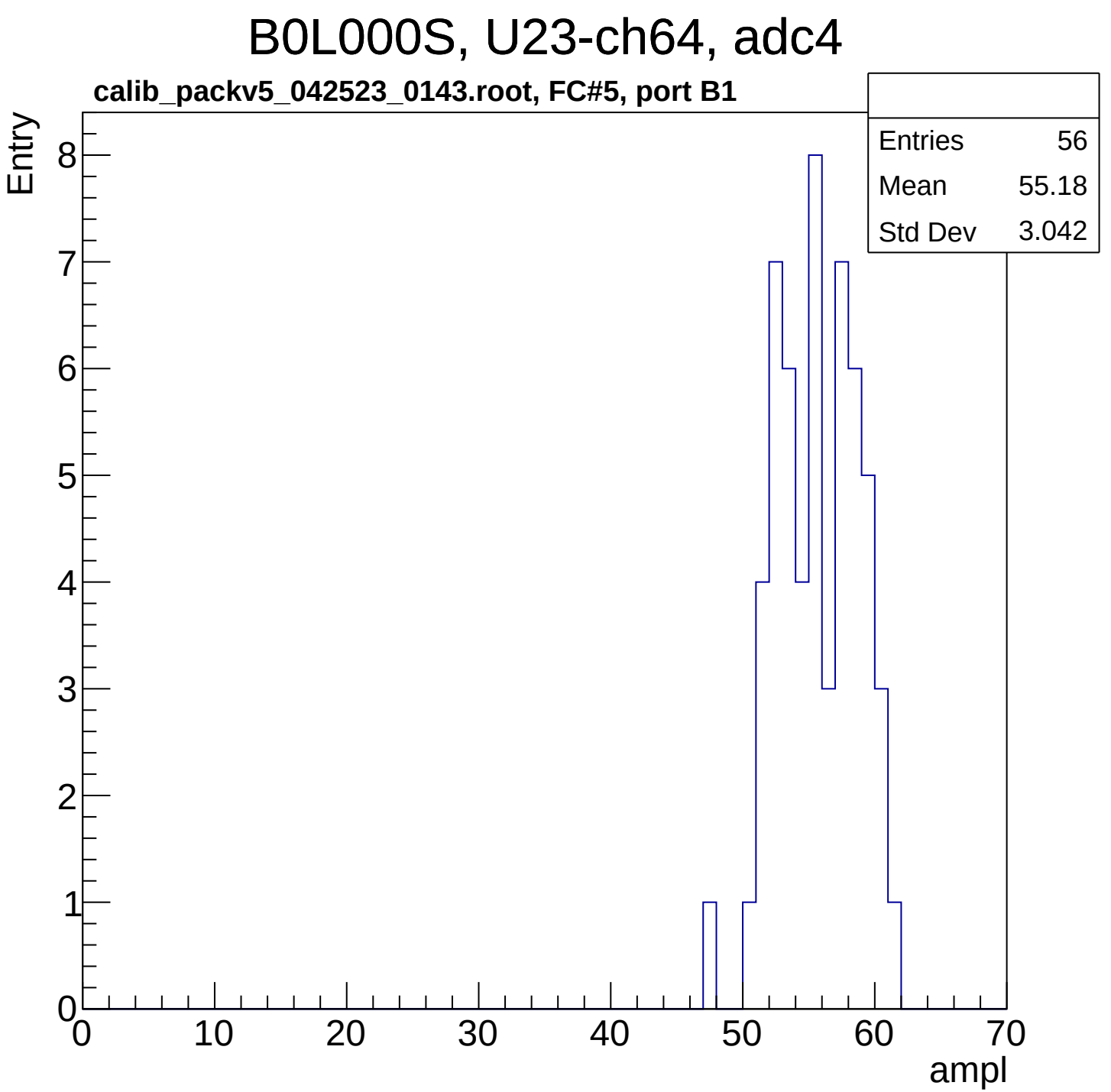

B0L000S, U23-ch63, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

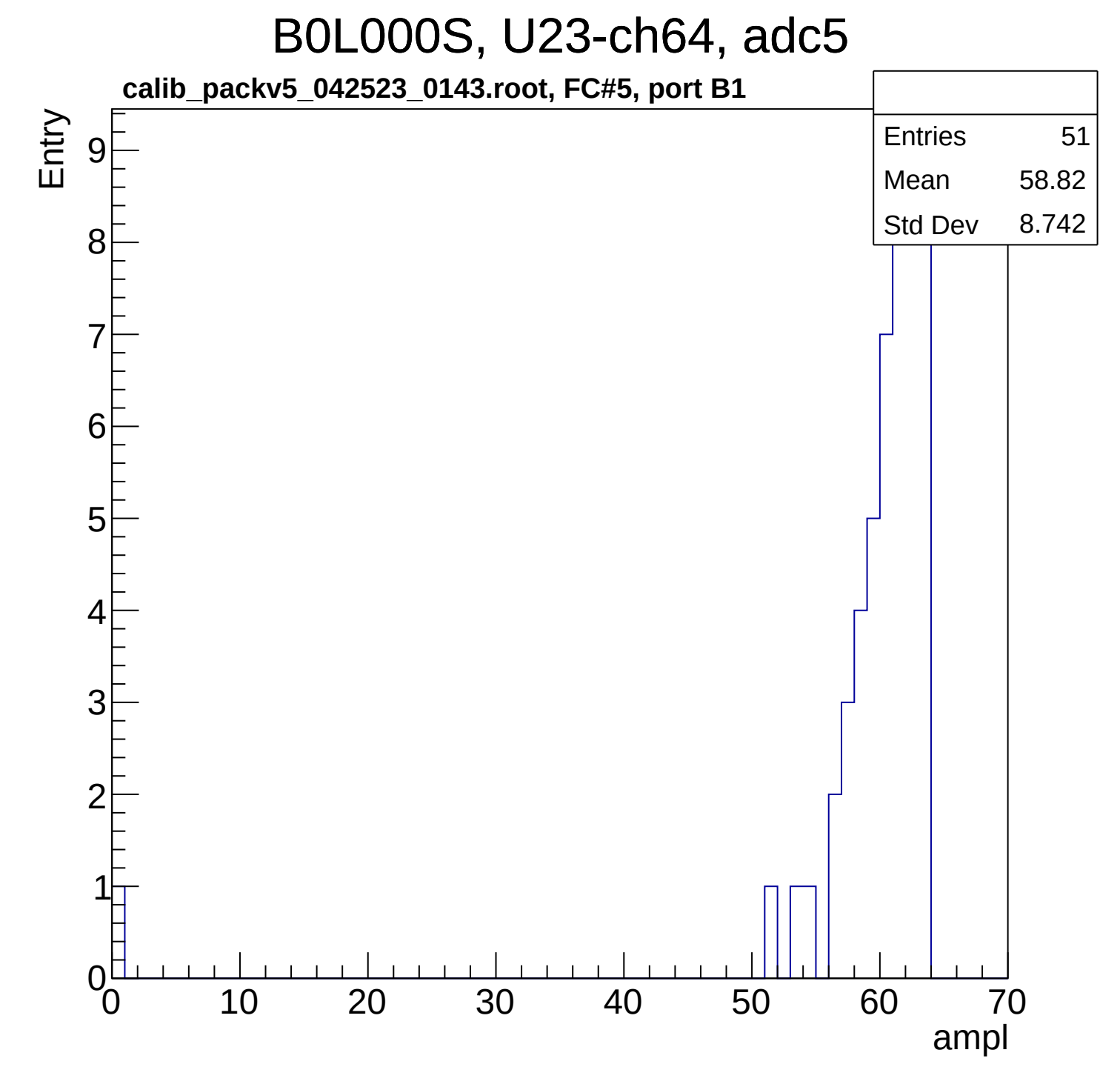

B0L000S, U23-ch64, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.35
Std Dev 4.944
Gaus mean : 30.7589
Gaus Width: 3.5358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

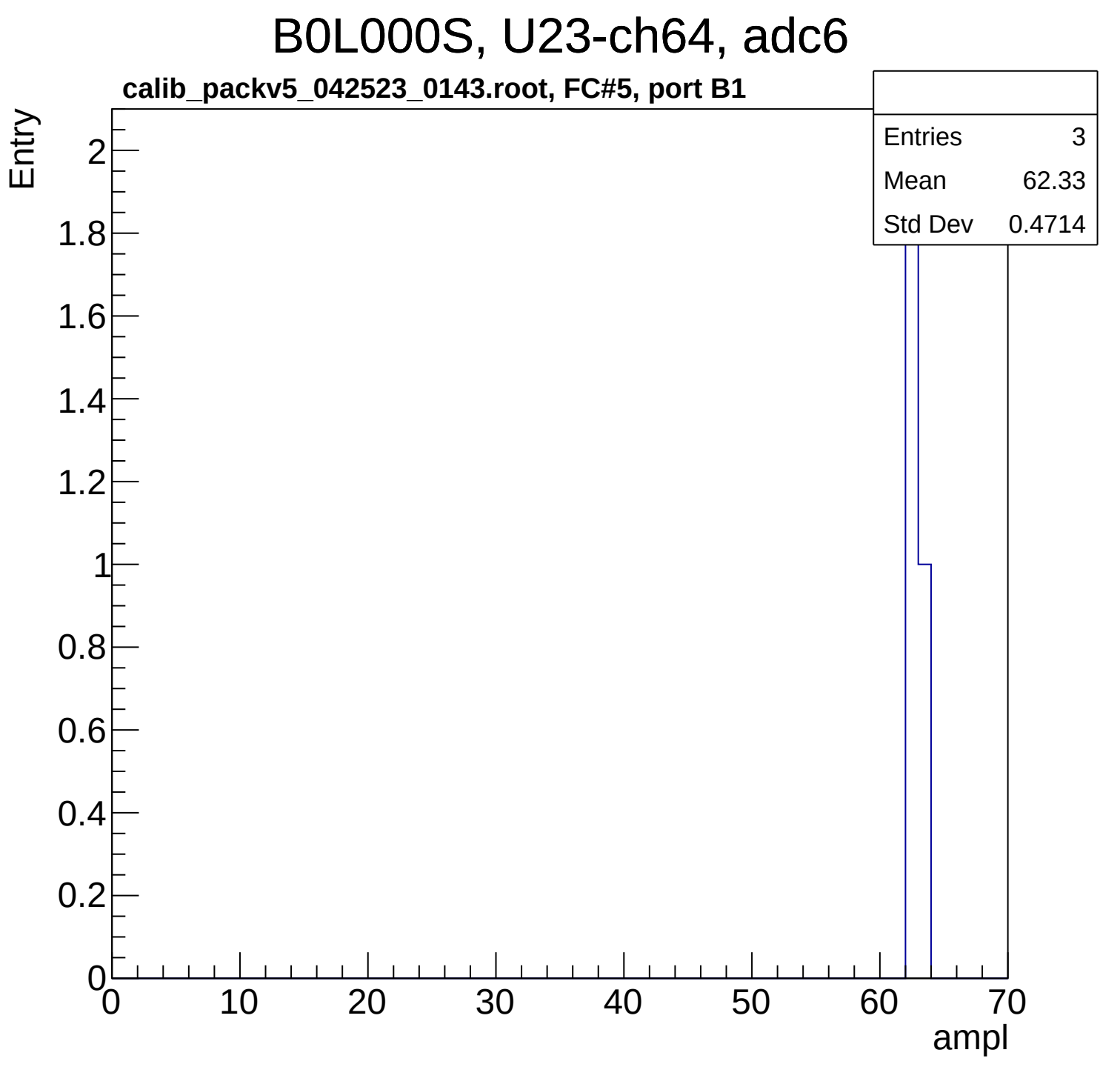

B0L000S, U23-ch64, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 35.89
Std Dev 3.6
Gaus mean : 36.3780
Gaus Width: 3.7115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

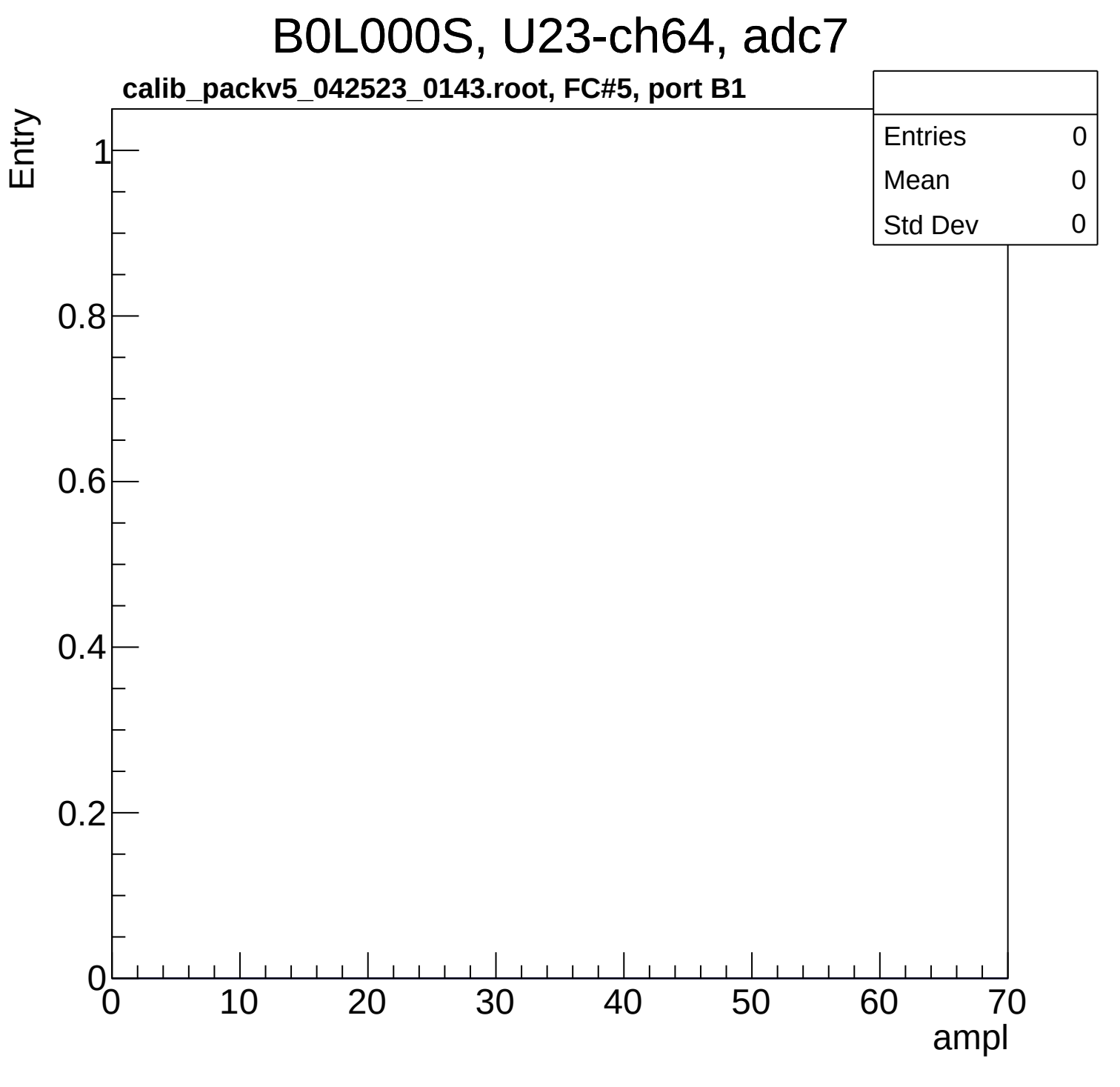

B0L000S, U23-ch64, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.67
Std Dev 3.24
Gaus mean : 43.1299
Gaus Width: 3.3188
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

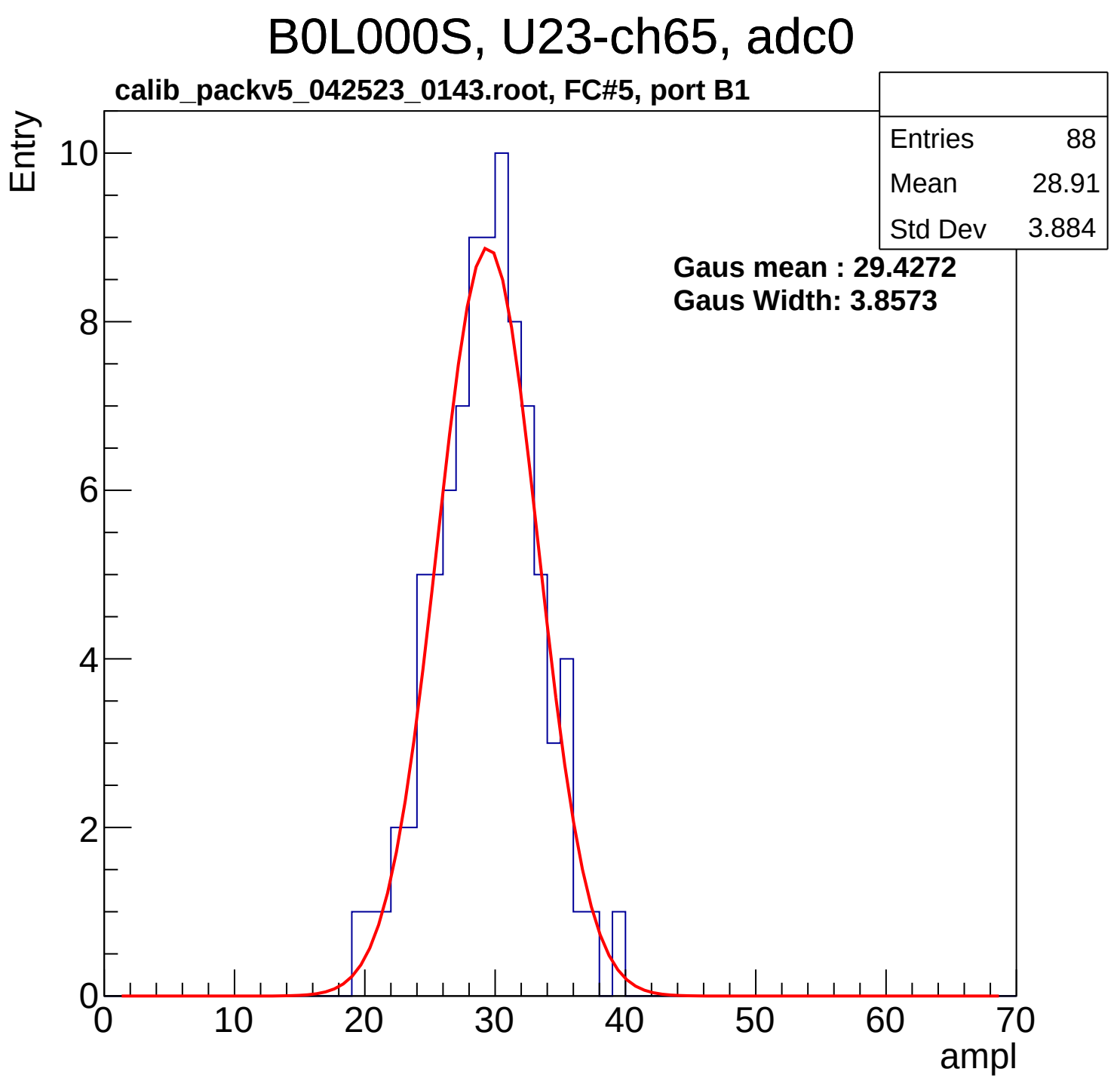

B0L000S, U23-ch64, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 50.03
Std Dev 3.276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

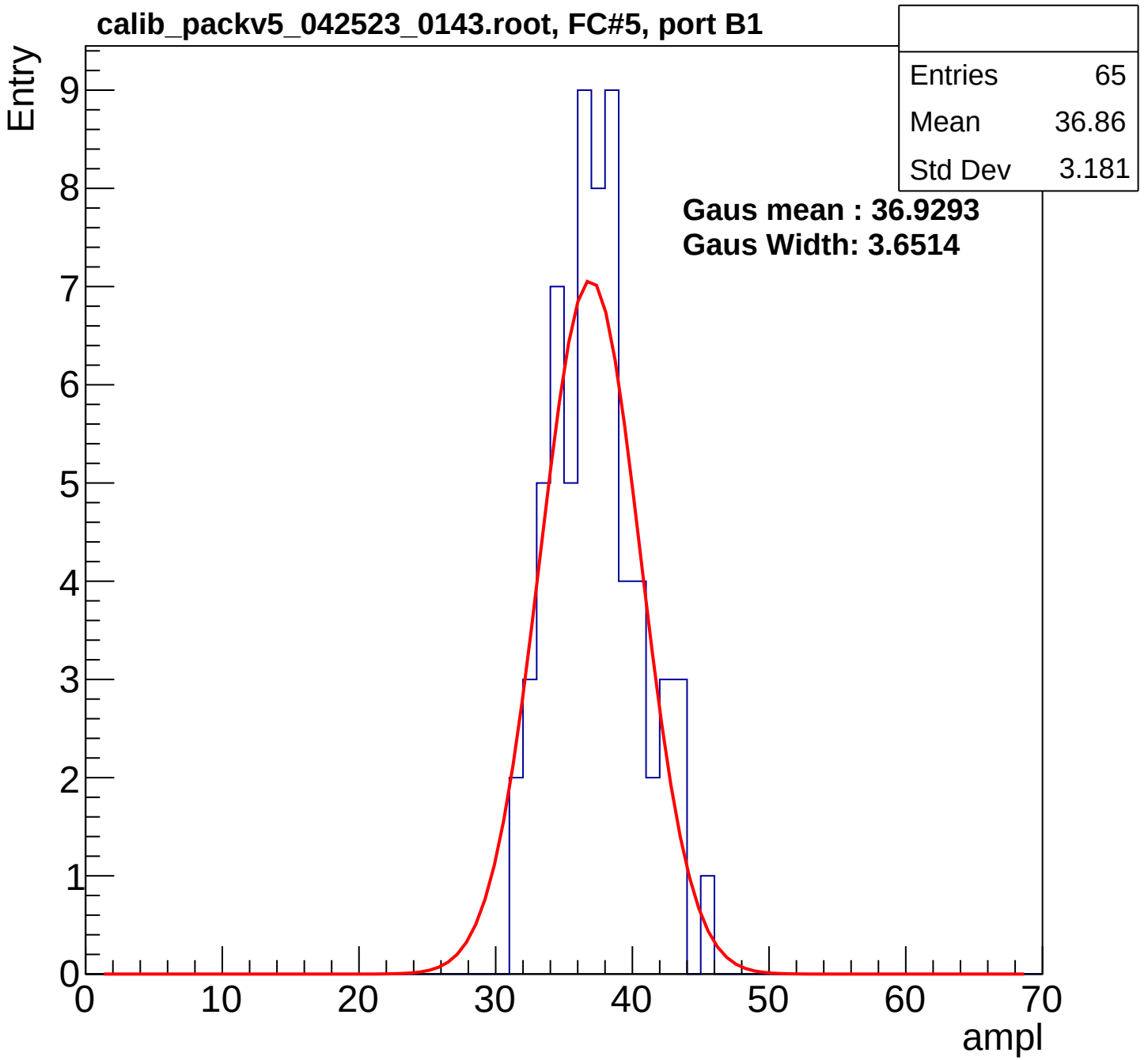

B0L000S, U23-ch64, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.18
Std Dev 3.042
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

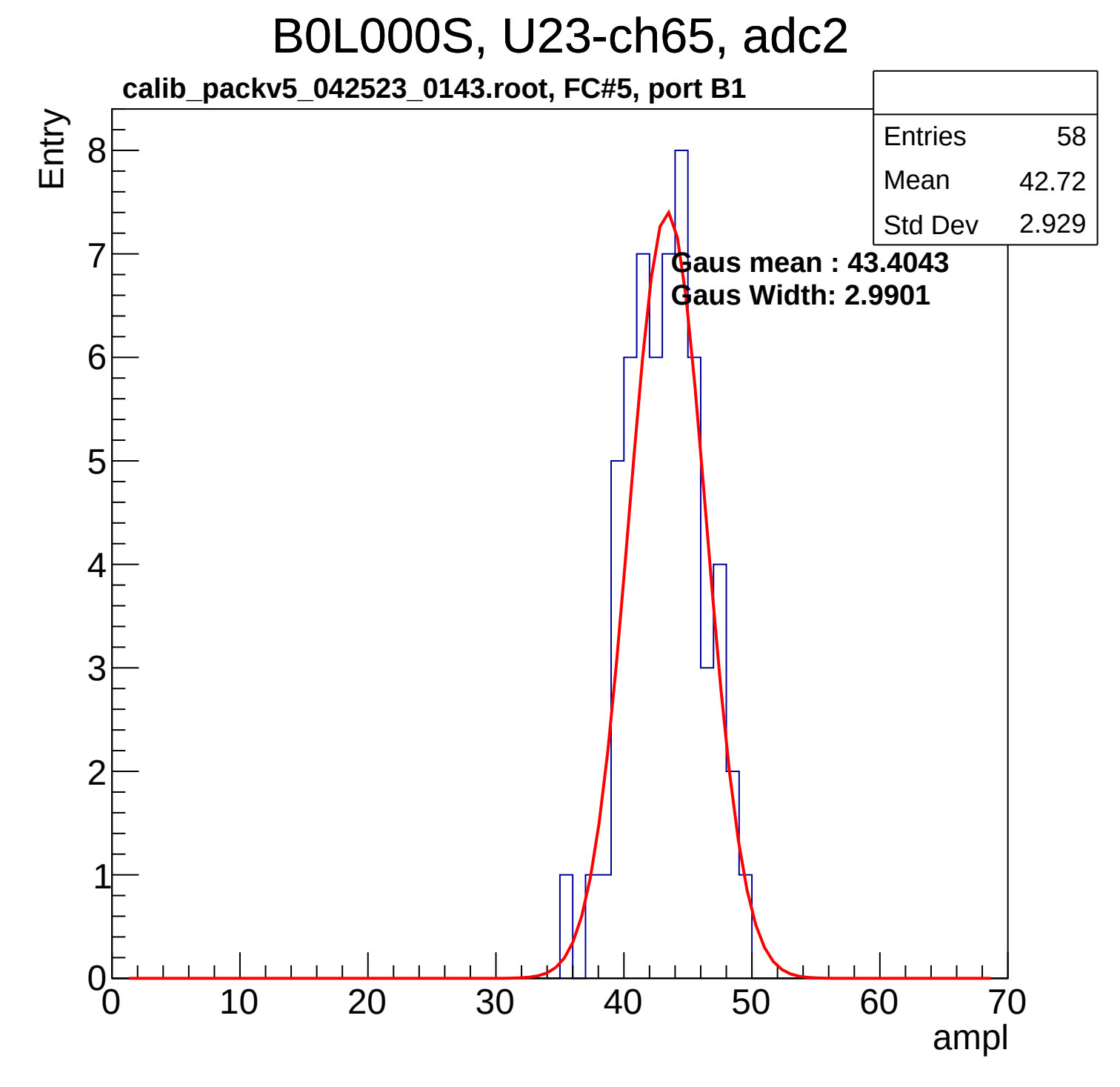

B0L000S, U23-ch64, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.82
Std Dev 8.742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

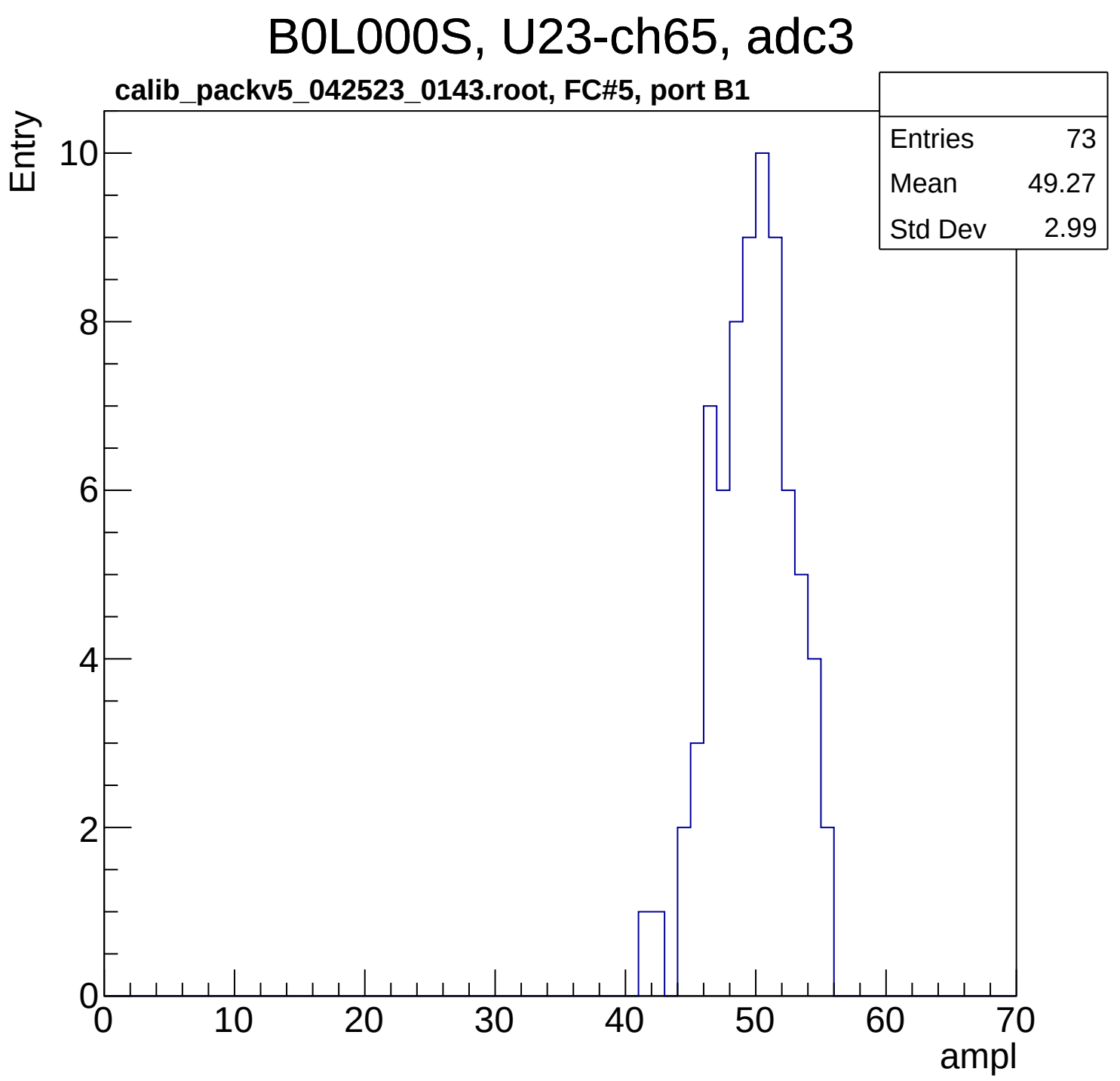

B0L000S, U23-ch64, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

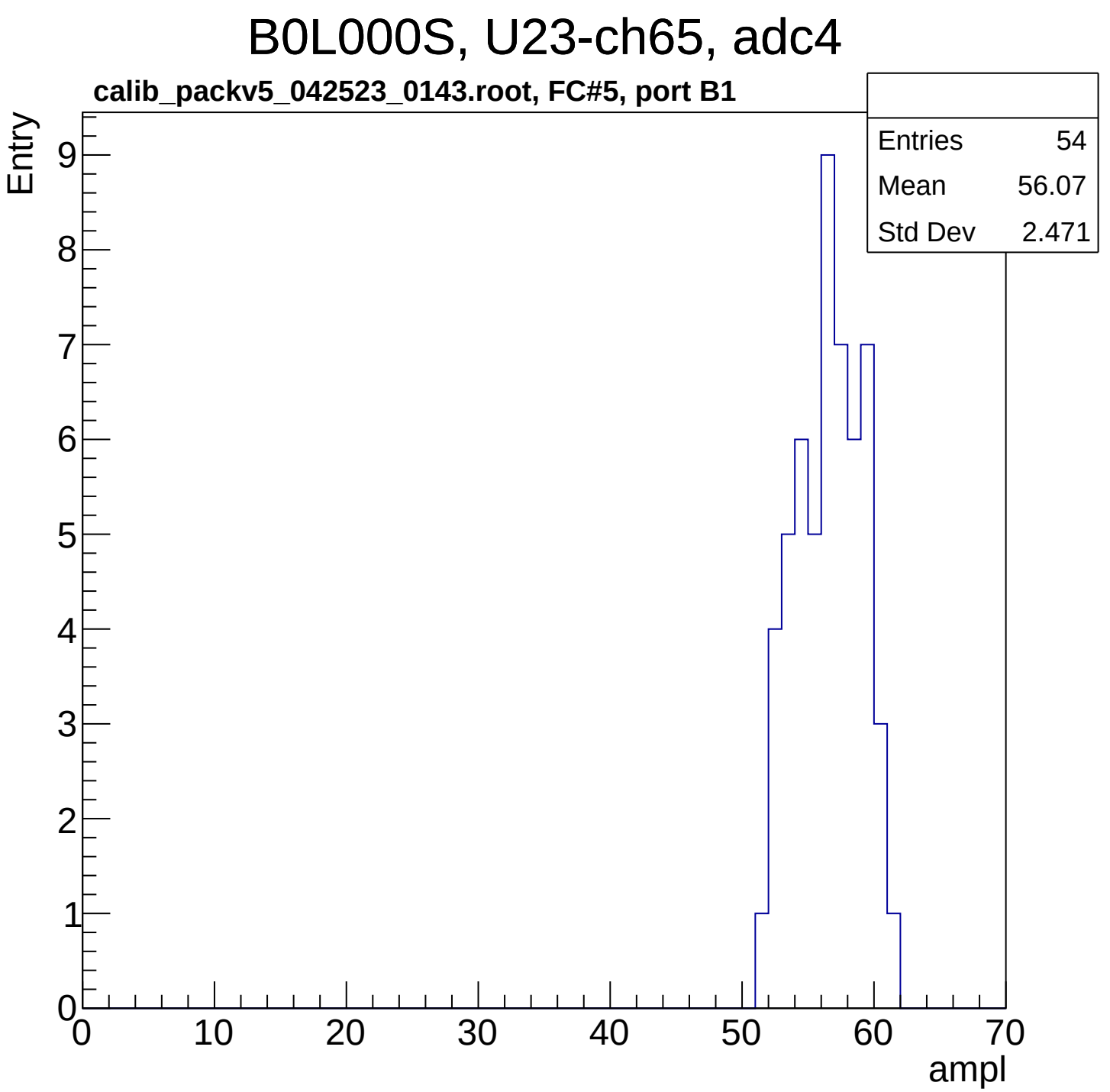

B0L000S, U23-ch64, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

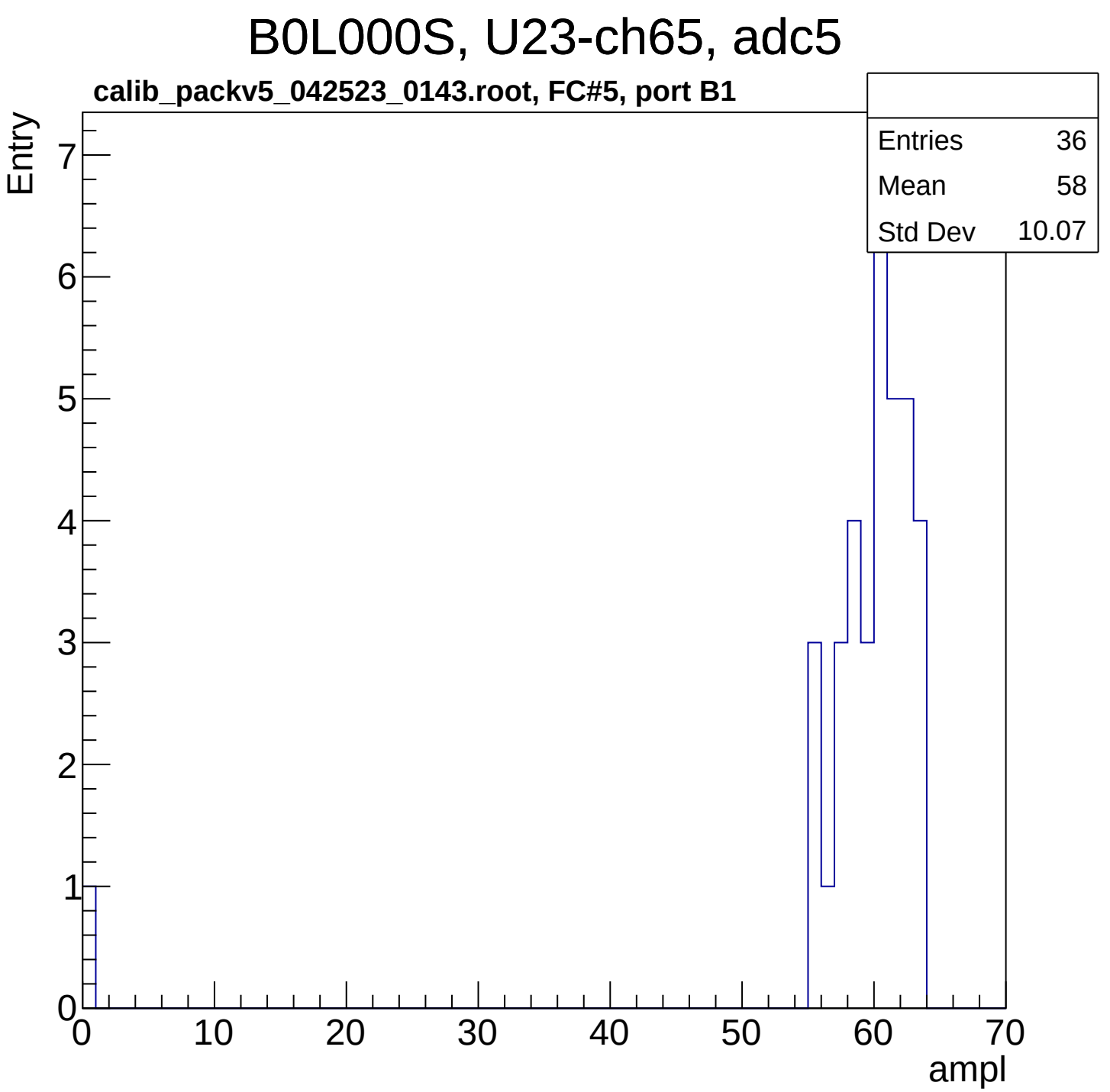

B0L000S, U23-ch65, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 88
Mean 28.91
Std Dev 3.884
Gaus mean : 29.4272
Gaus Width: 3.8573
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

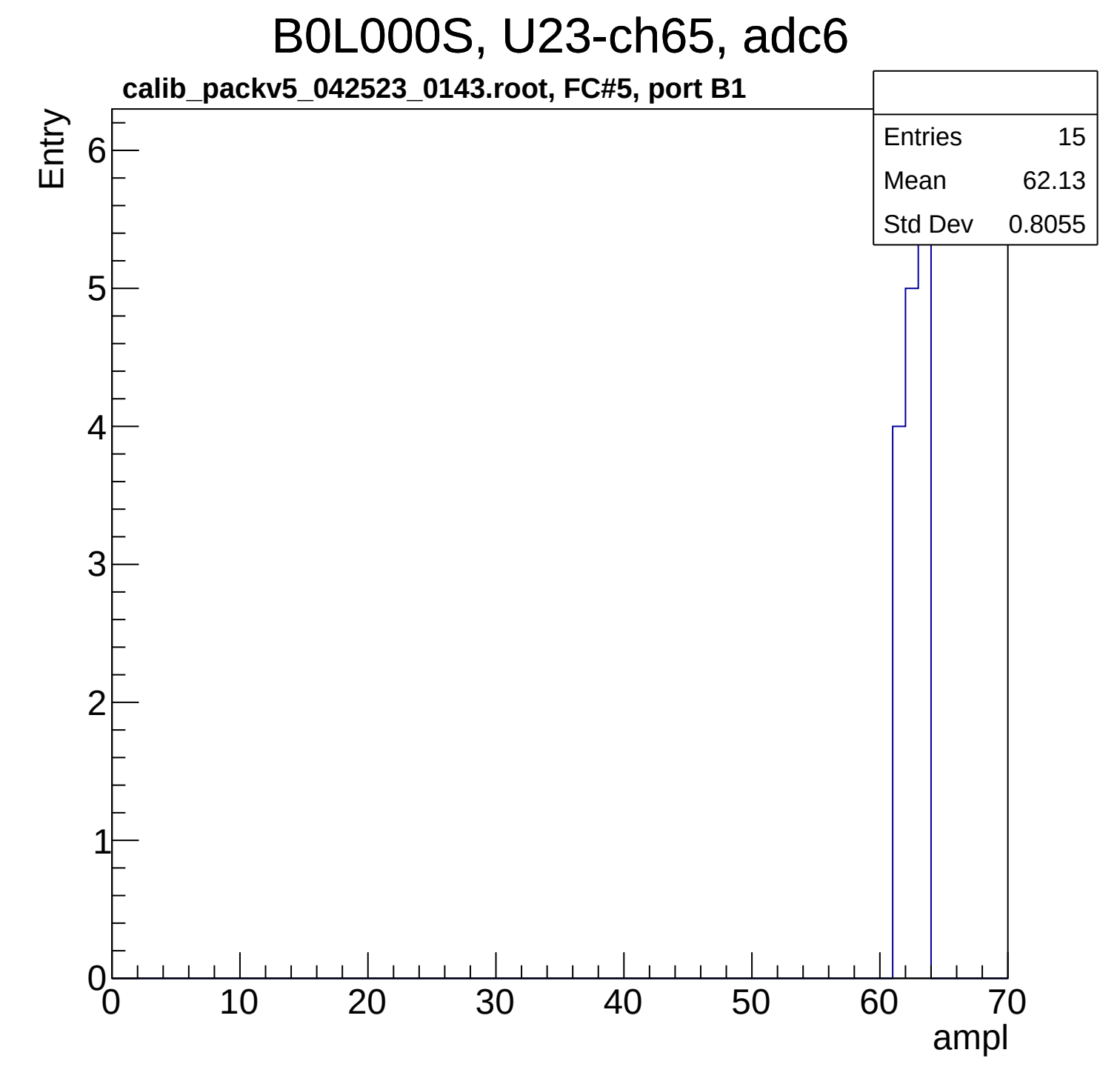

B0L000S, U23-ch65, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.86
Std Dev 3.181
Gaus mean : 36.9293
Gaus Width: 3.6514
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

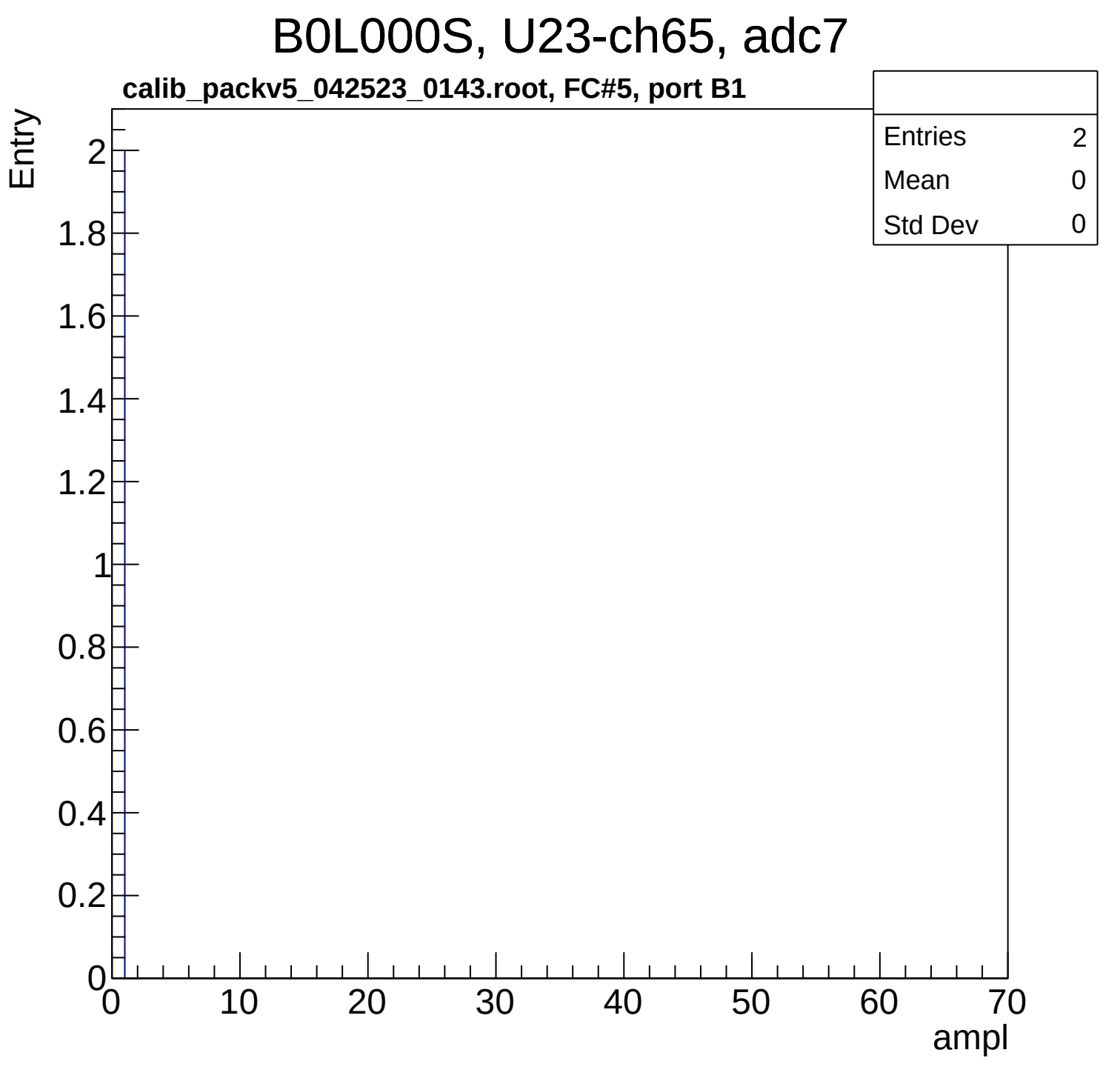

B0L000S, U23-ch65, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 42.72
Std Dev 2.929
Gaus mean : 43.4043
Gaus Width: 2.9901
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

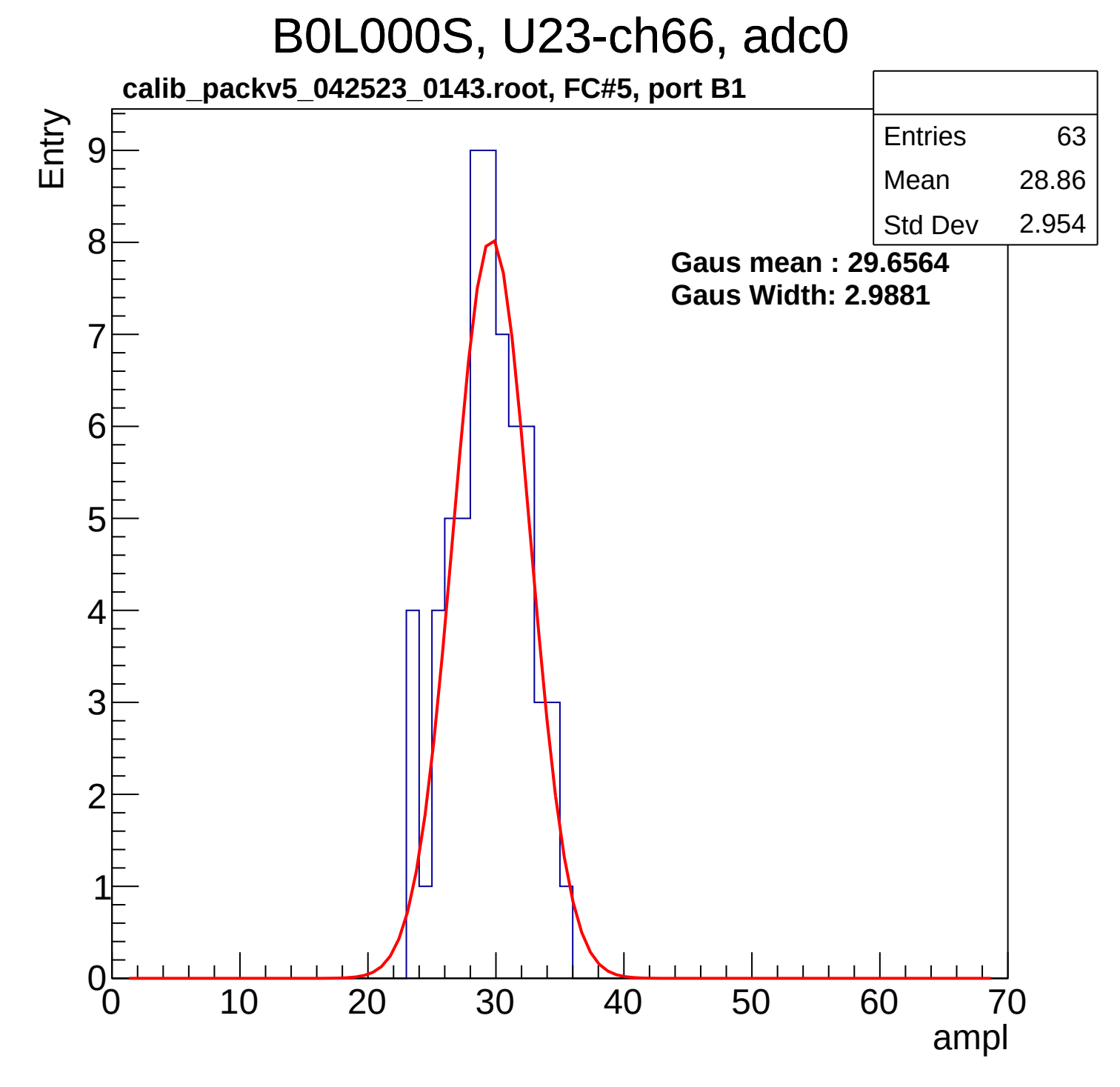

B0L000S, U23-ch65, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.27
Std Dev 2.99
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

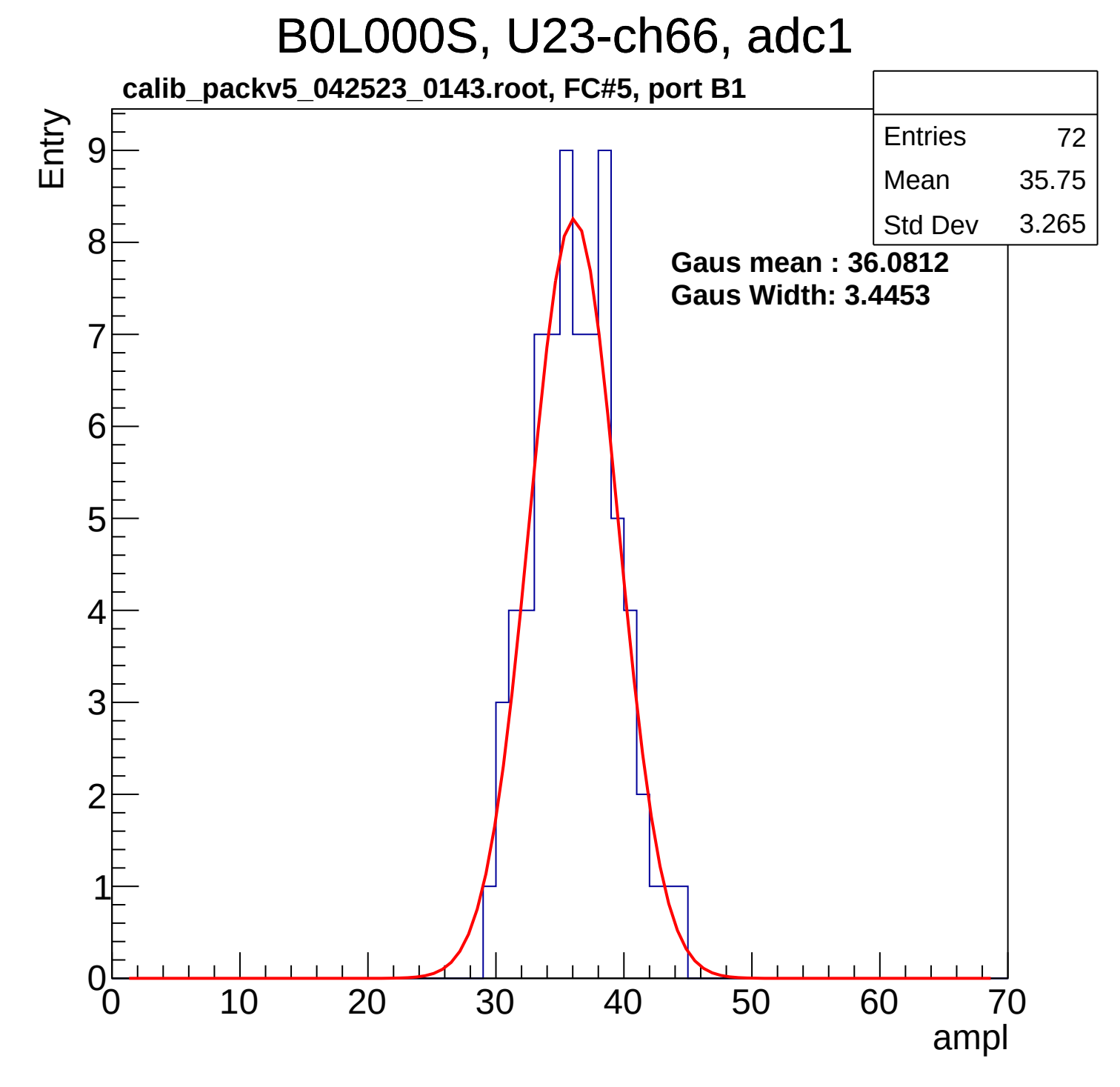

B0L000S, U23-ch65, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.07
Std Dev 2.471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

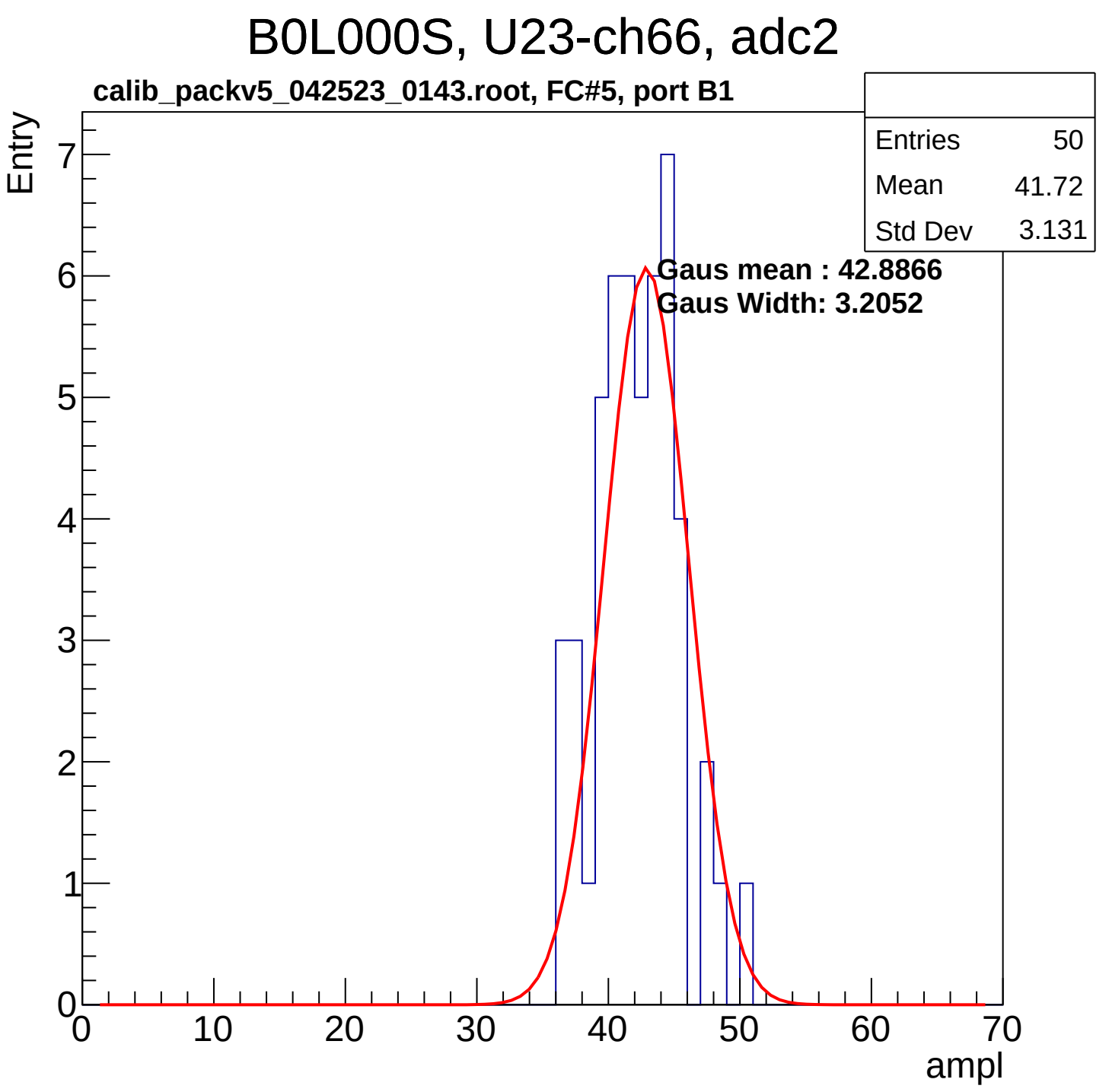

B0L000S, U23-ch65, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58
Std Dev 10.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

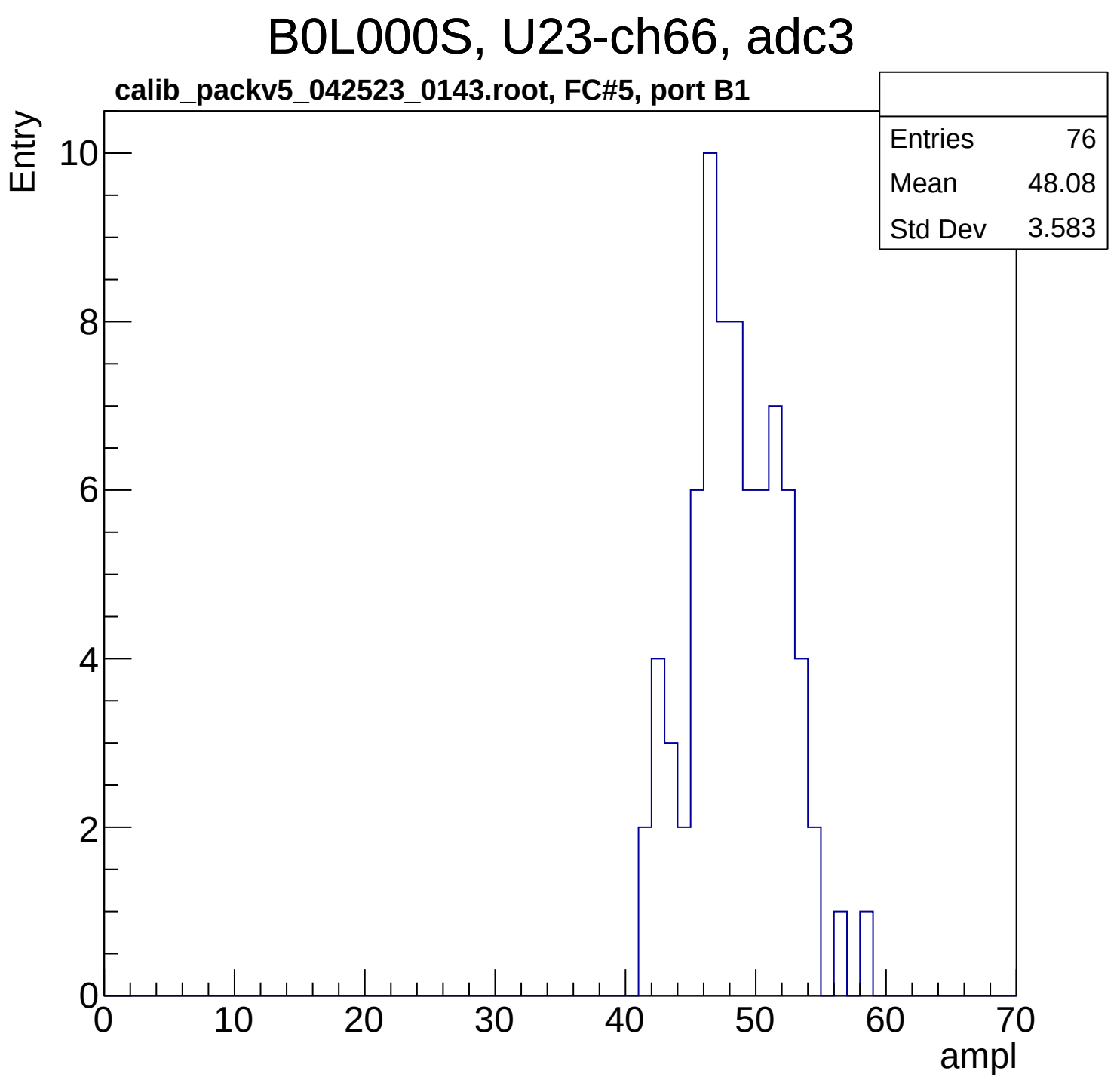

B0L000S, U23-ch65, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 62.13
Std Dev 0.8055
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

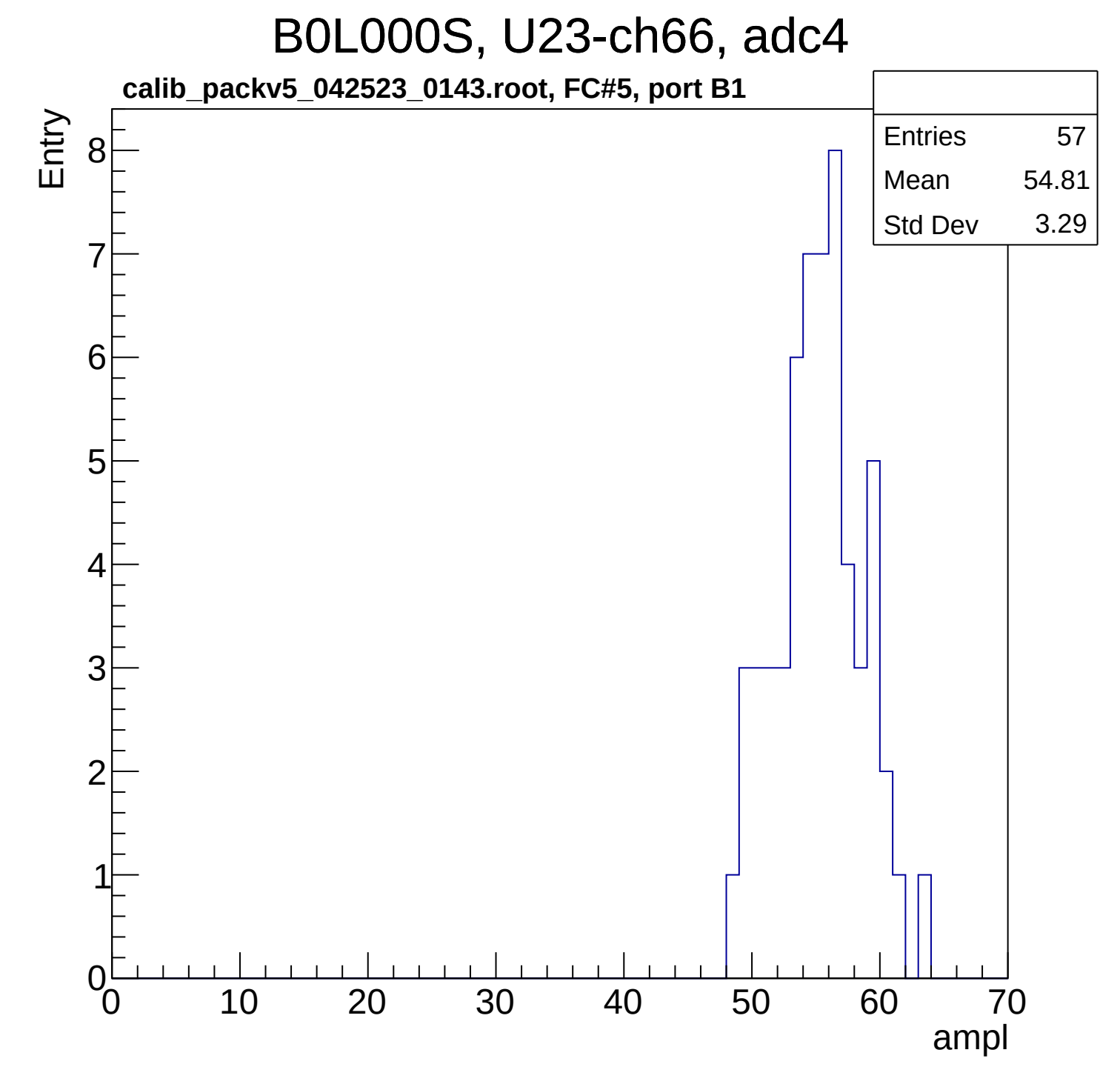

B0L000S, U23-ch65, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

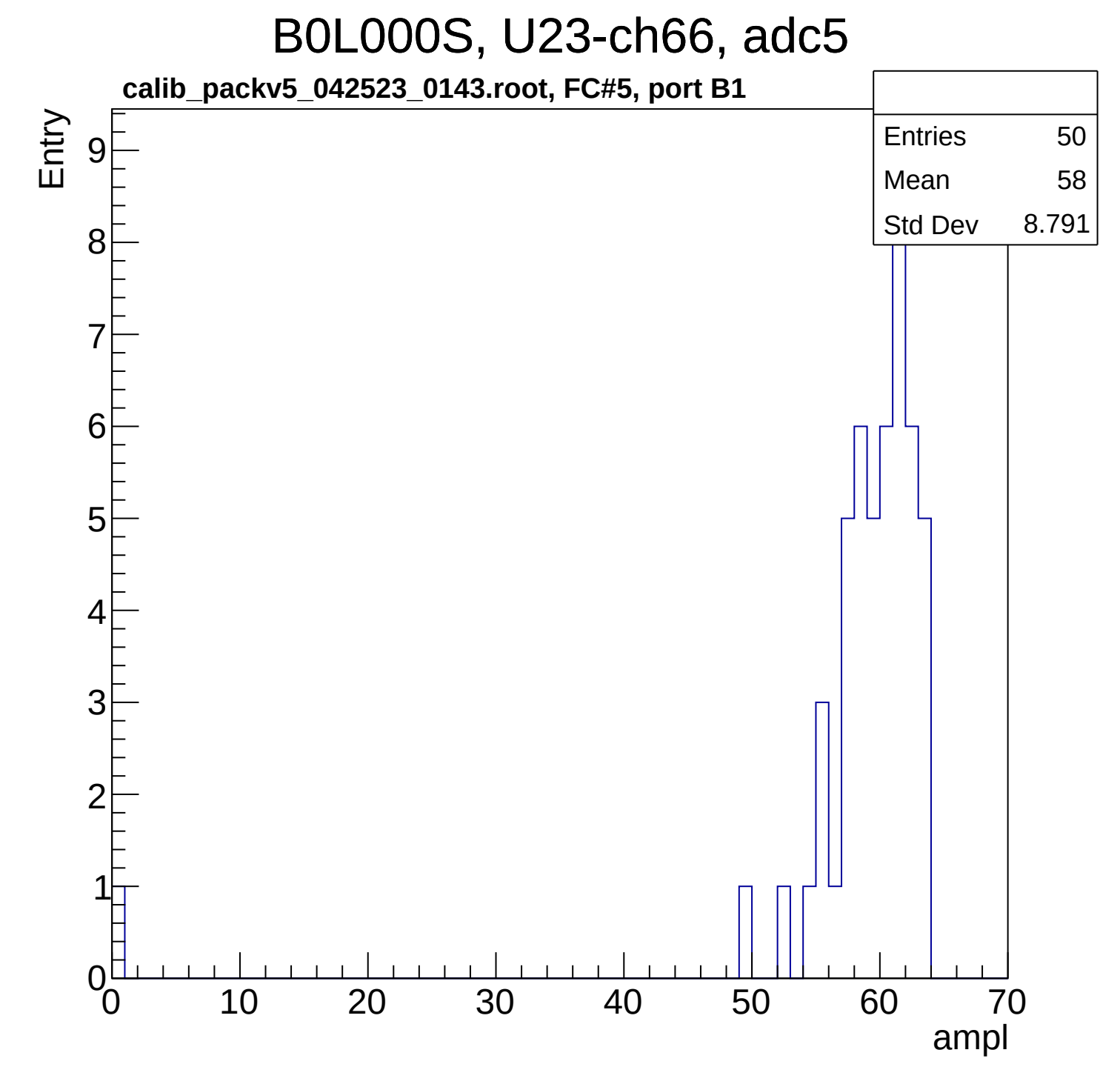

B0L000S, U23-ch66, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 28.86
Std Dev 2.954
Gaus mean : 29.6564
Gaus Width: 2.9881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

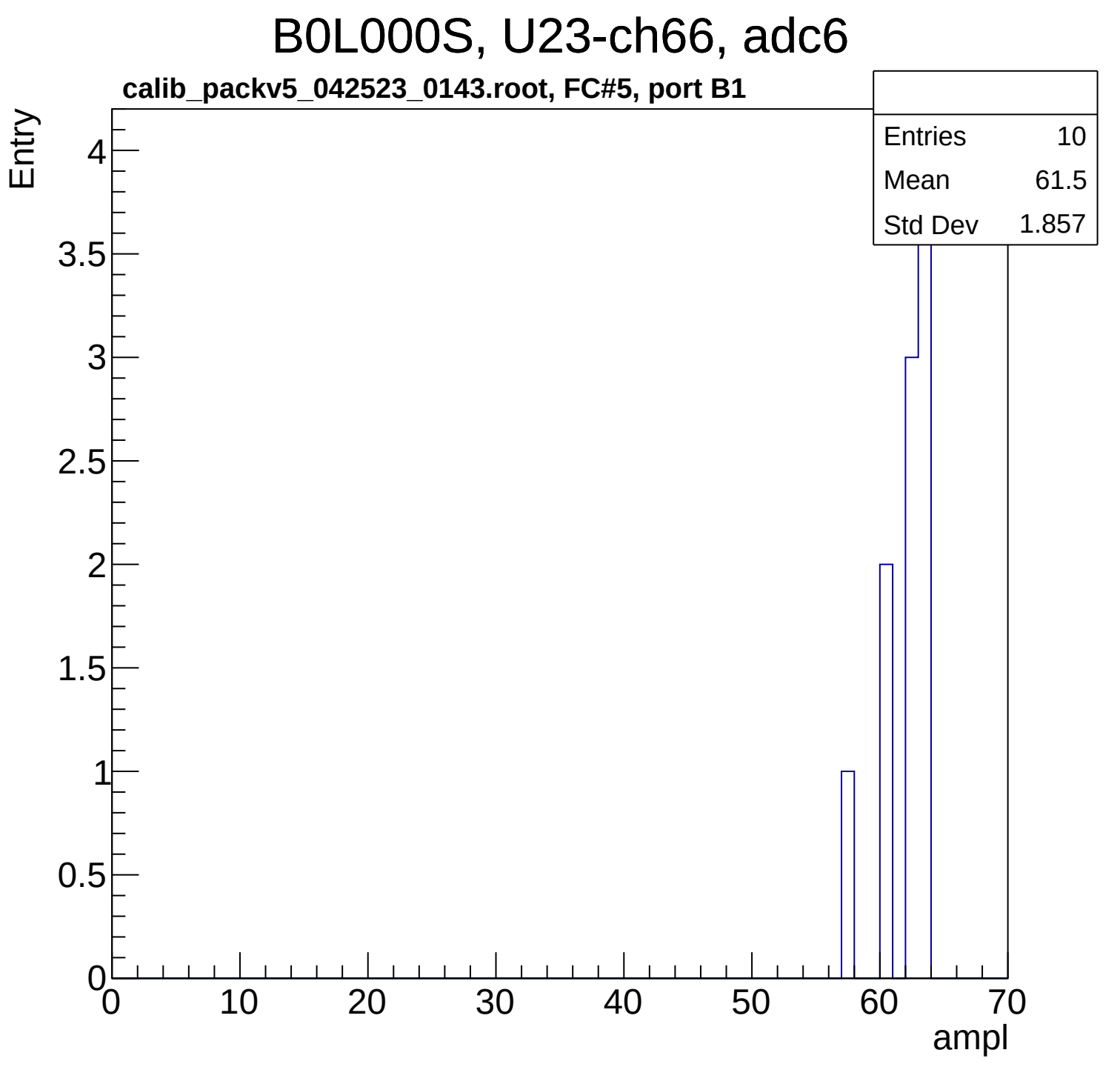

B0L000S, U23-ch66, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.75
Std Dev 3.265
Gaus mean : 36.0812
Gaus Width: 3.4453
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

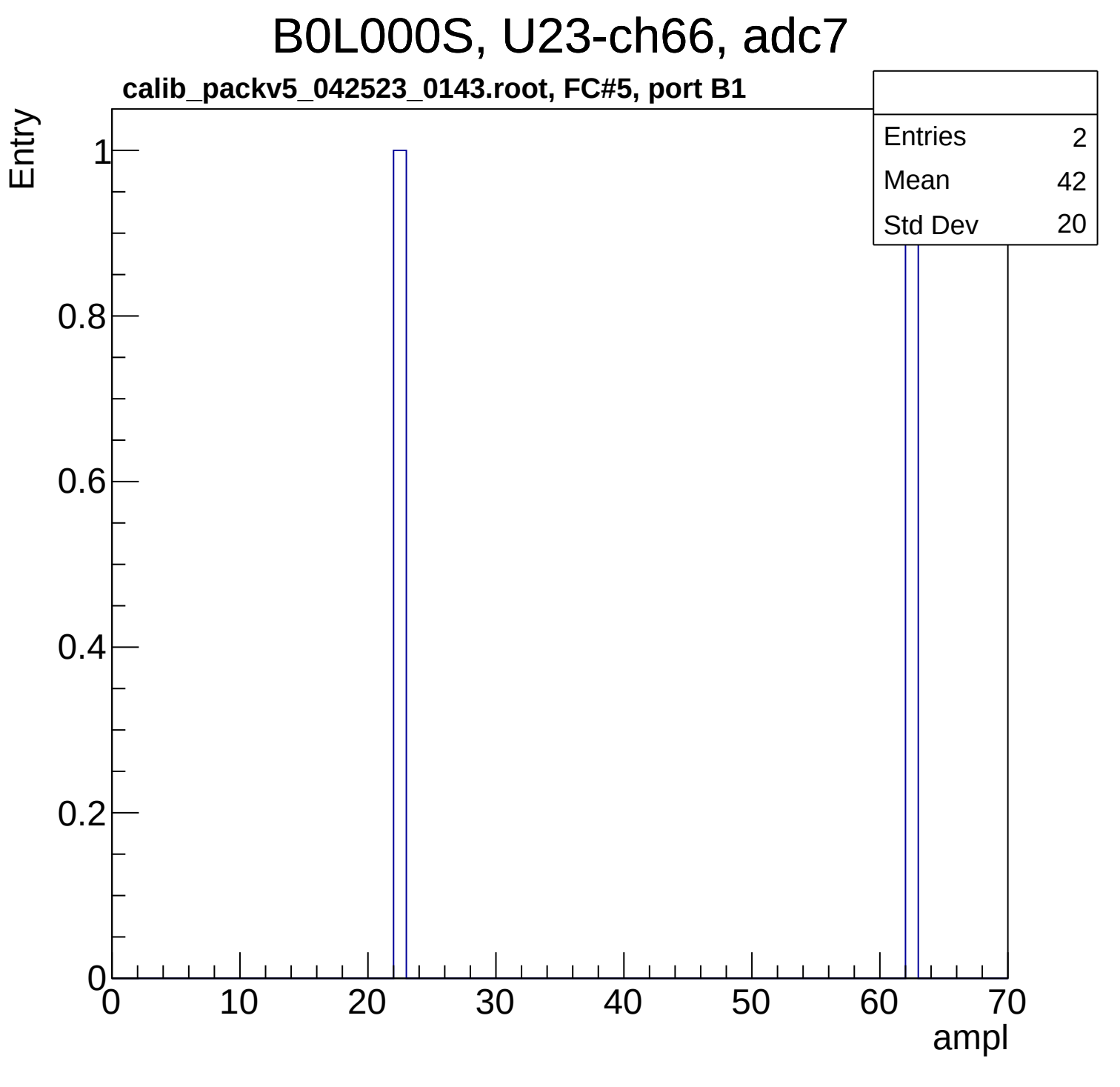

B0L000S, U23-ch66, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 41.72
Std Dev 3.131
Gaus mean : 42.8866
Gaus Width: 3.2052
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

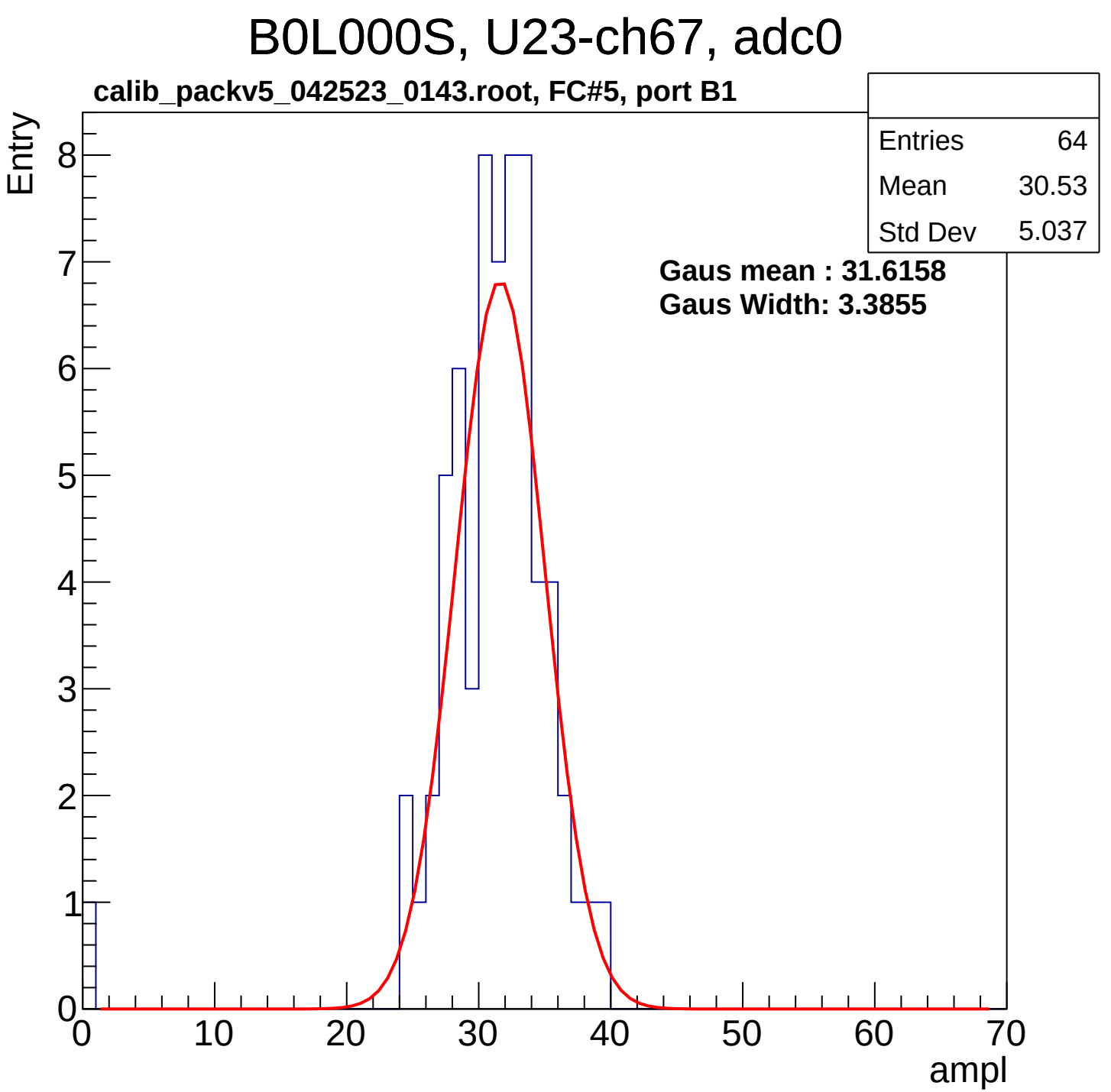

B0L000S, U23-ch66, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 48.08
Std Dev 3.583
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

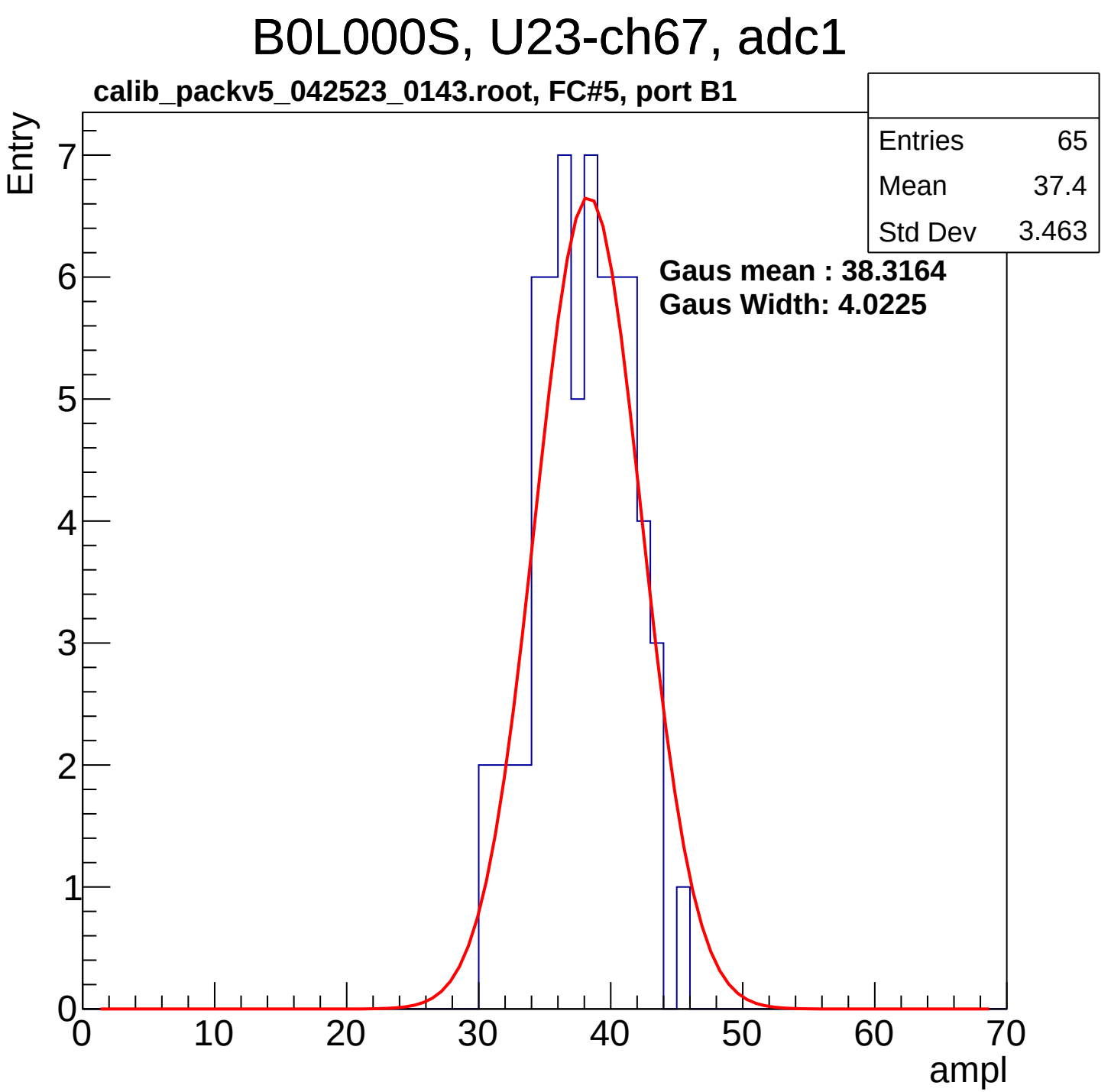

B0L000S, U23-ch66, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 54.81
Std Dev 3.29
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

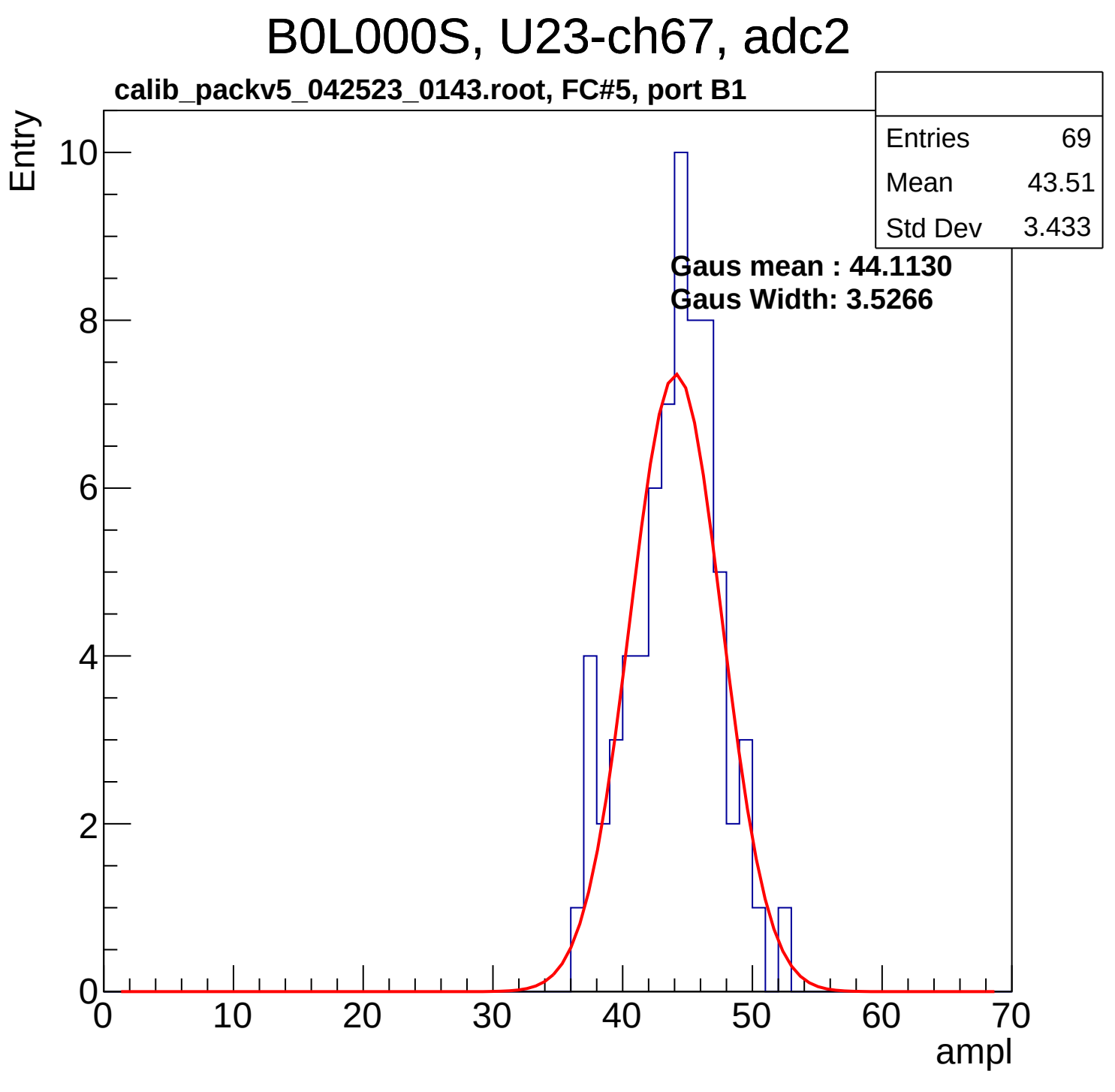

B0L000S, U23-ch66, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58
Std Dev 8.791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

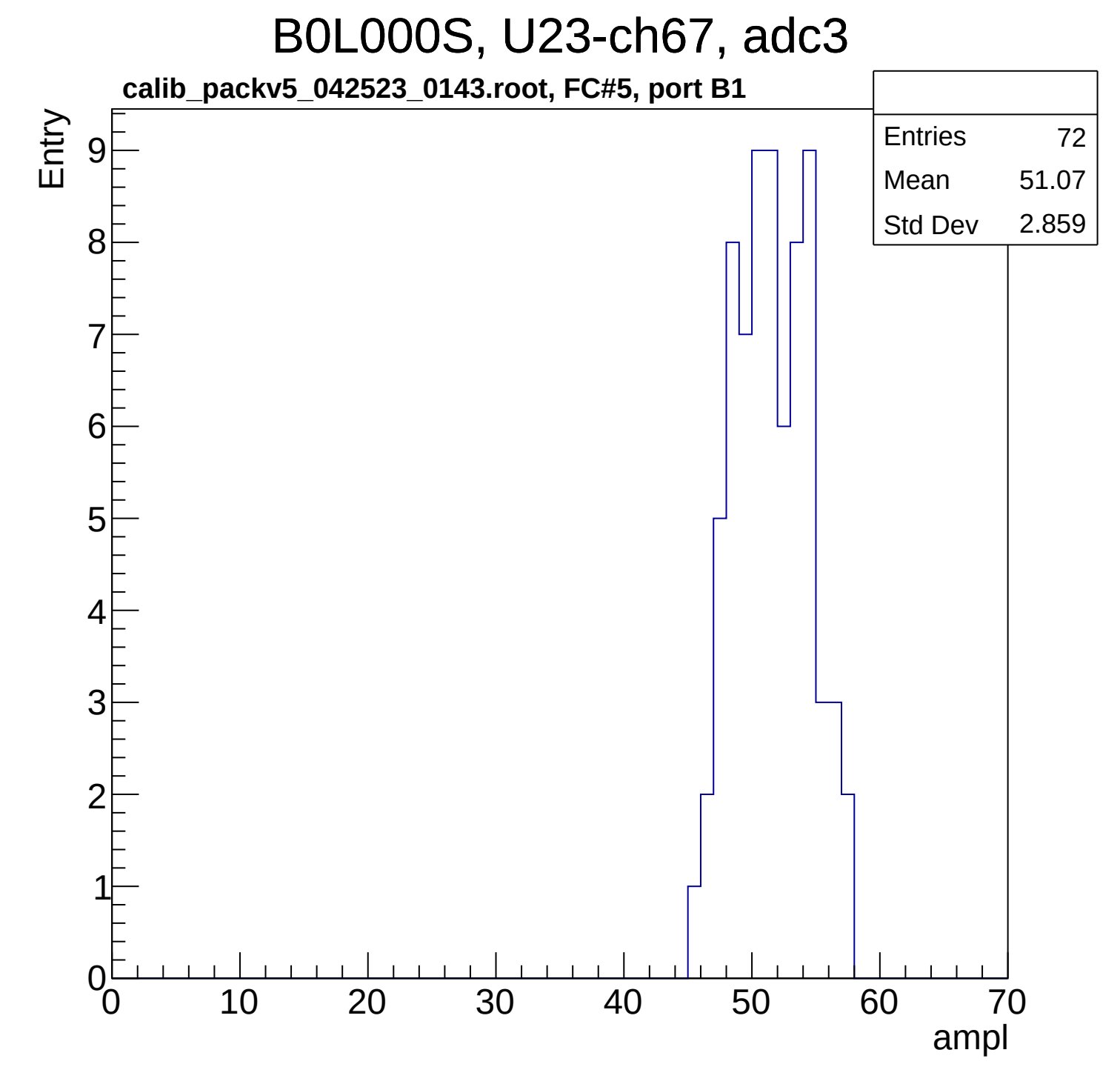

B0L000S, U23-ch66, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.5
Std Dev 1.857
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

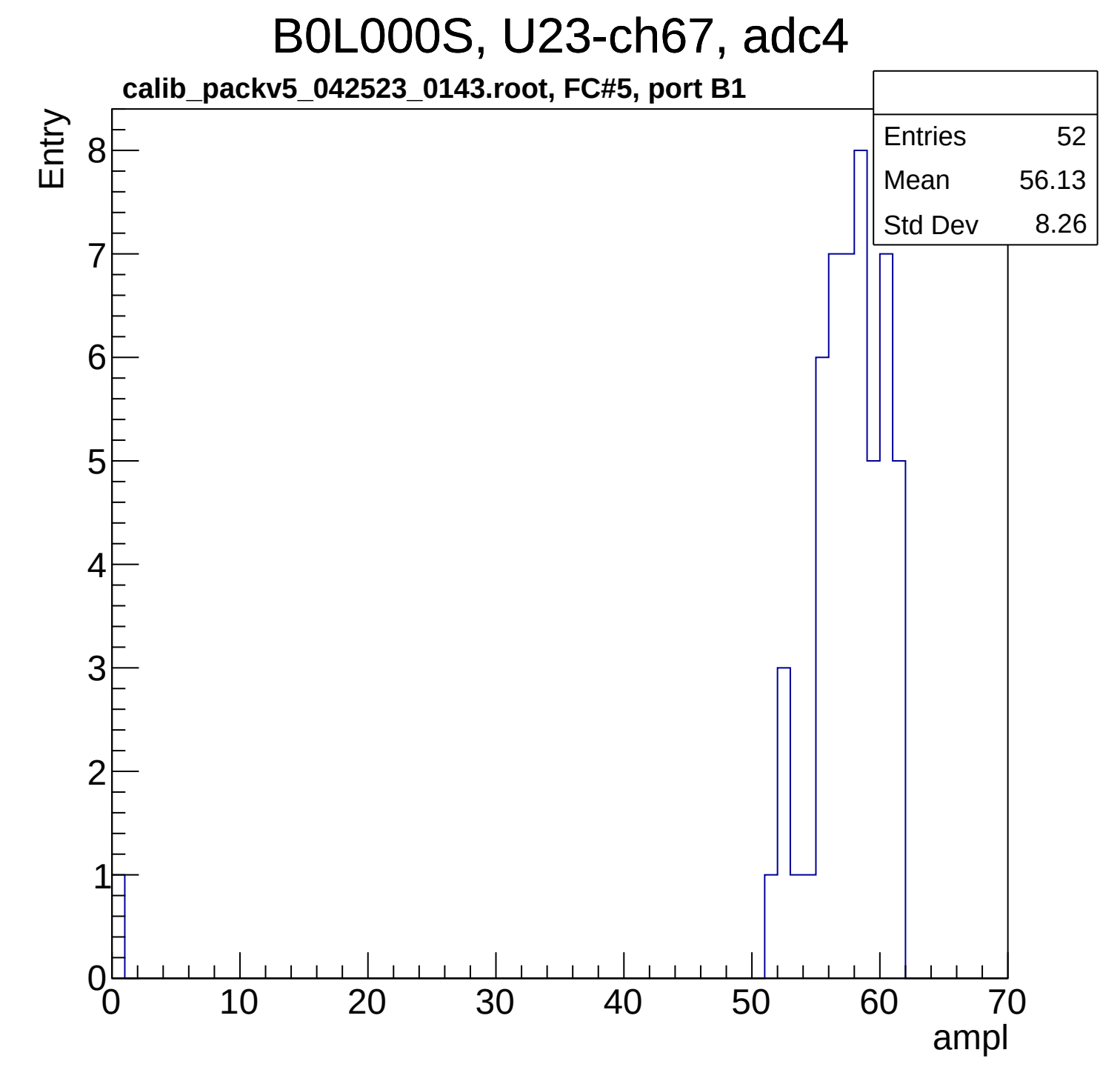

B0L000S, U23-ch66, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 42
Std Dev 20
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

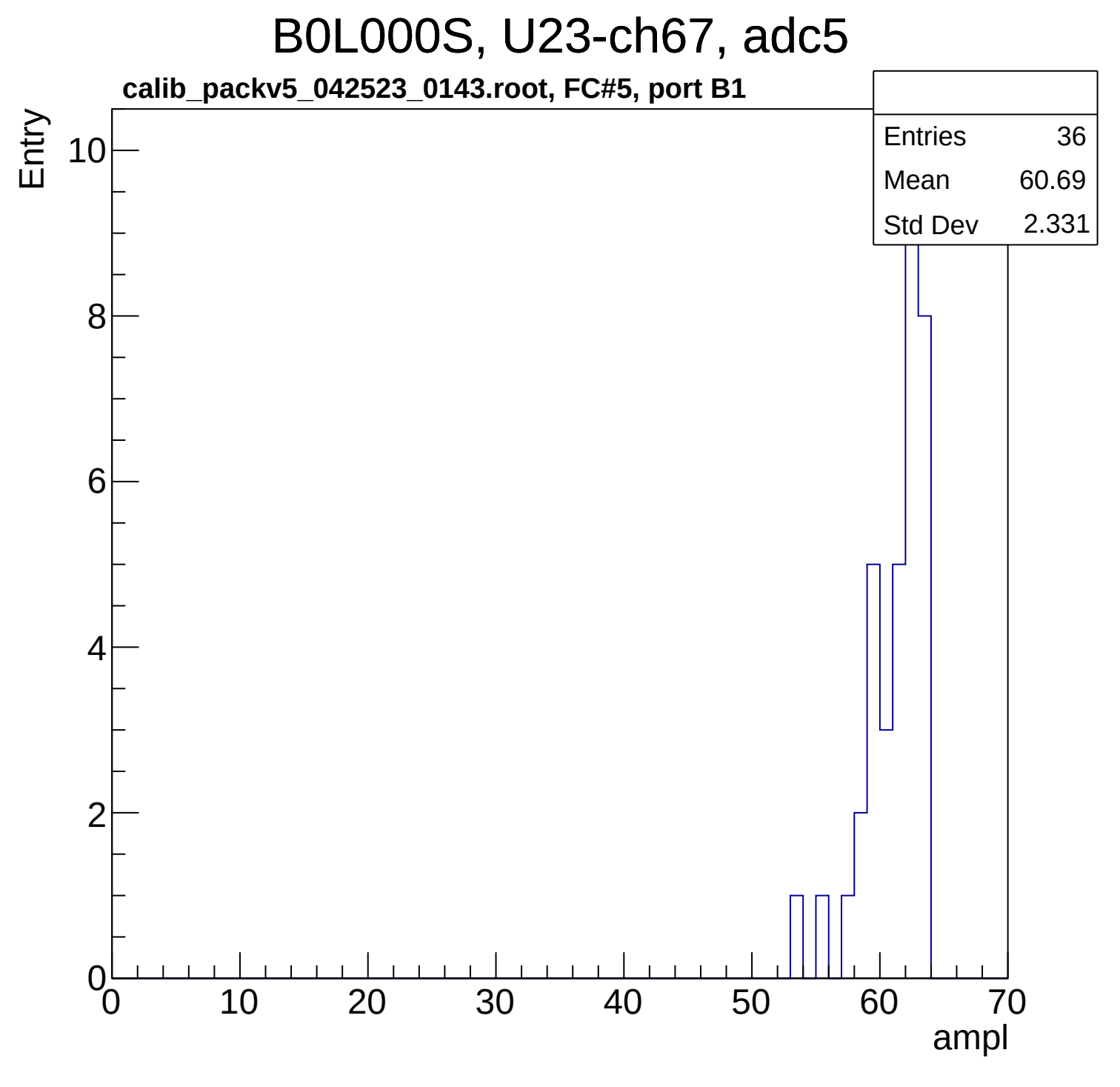

B0L000S, U23-ch67, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 30.53
Std Dev 5.037
Gaus mean : 31.6158
Gaus Width: 3.3855
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

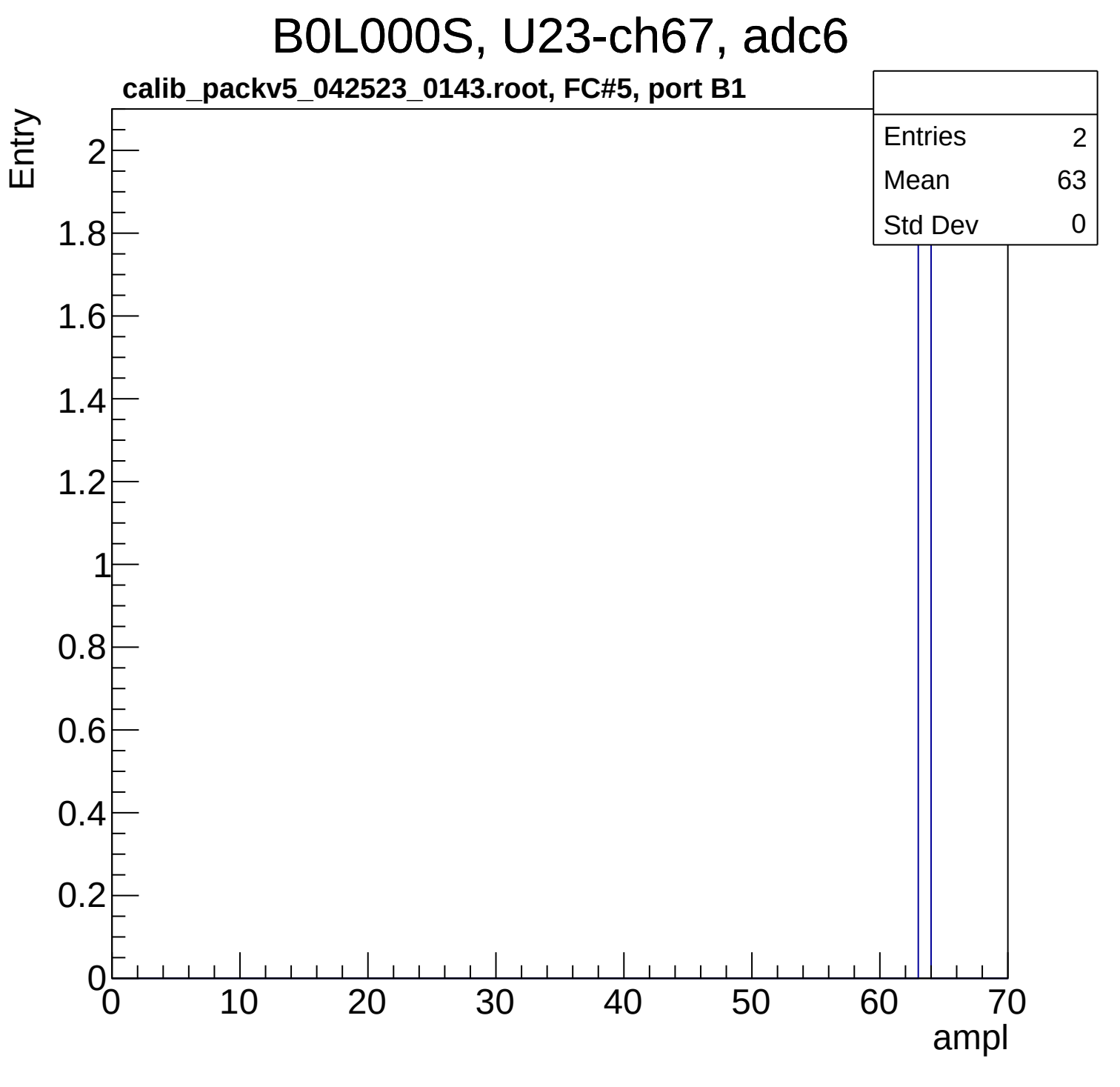

B0L000S, U23-ch67, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 37.4
Std Dev 3.463
Gaus mean : 38.3164
Gaus Width: 4.0225
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

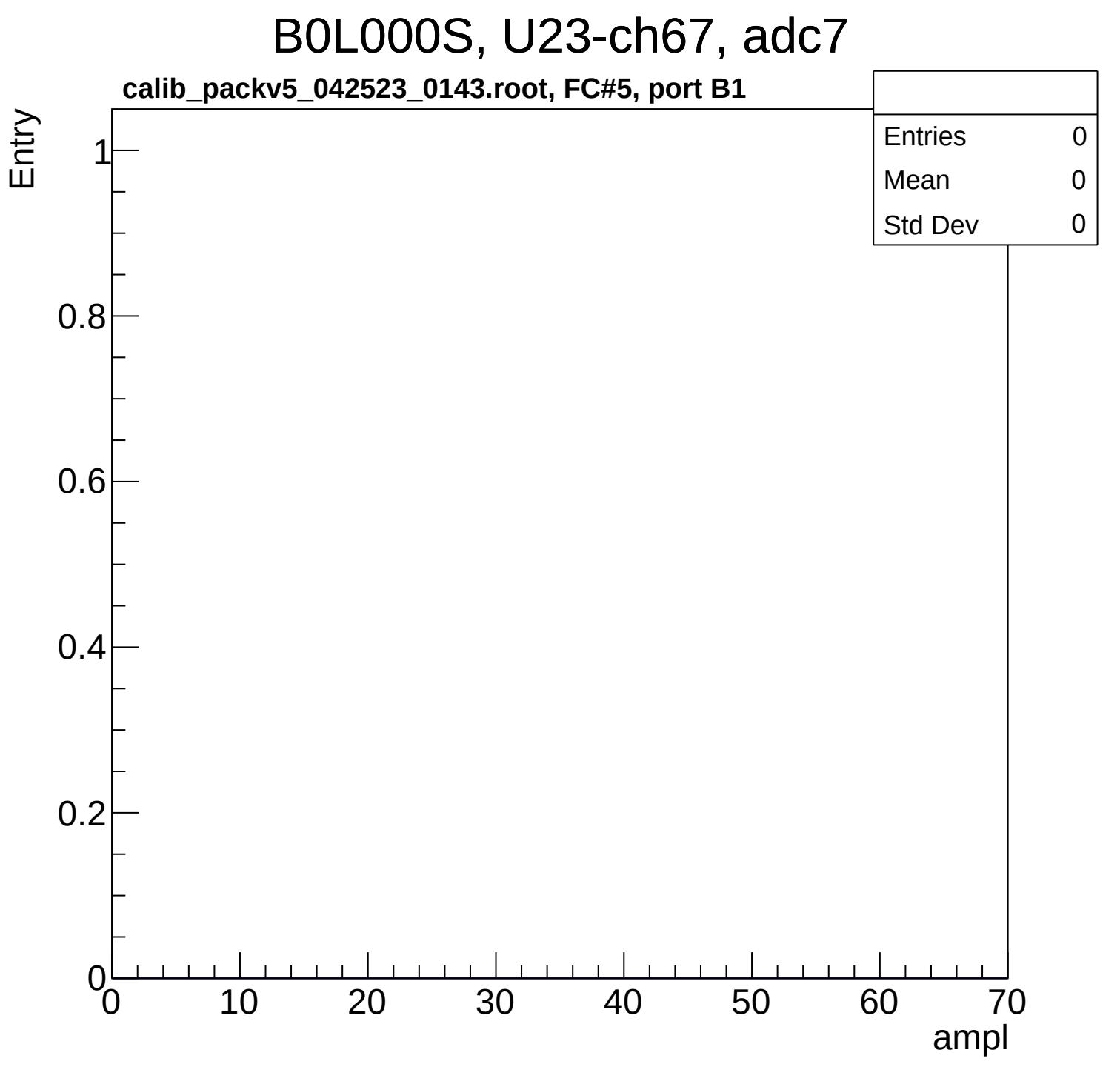

B0L000S, U23-ch67, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.51
Std Dev 3.433
Gaus mean : 44.1130
Gaus Width: 3.5266
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

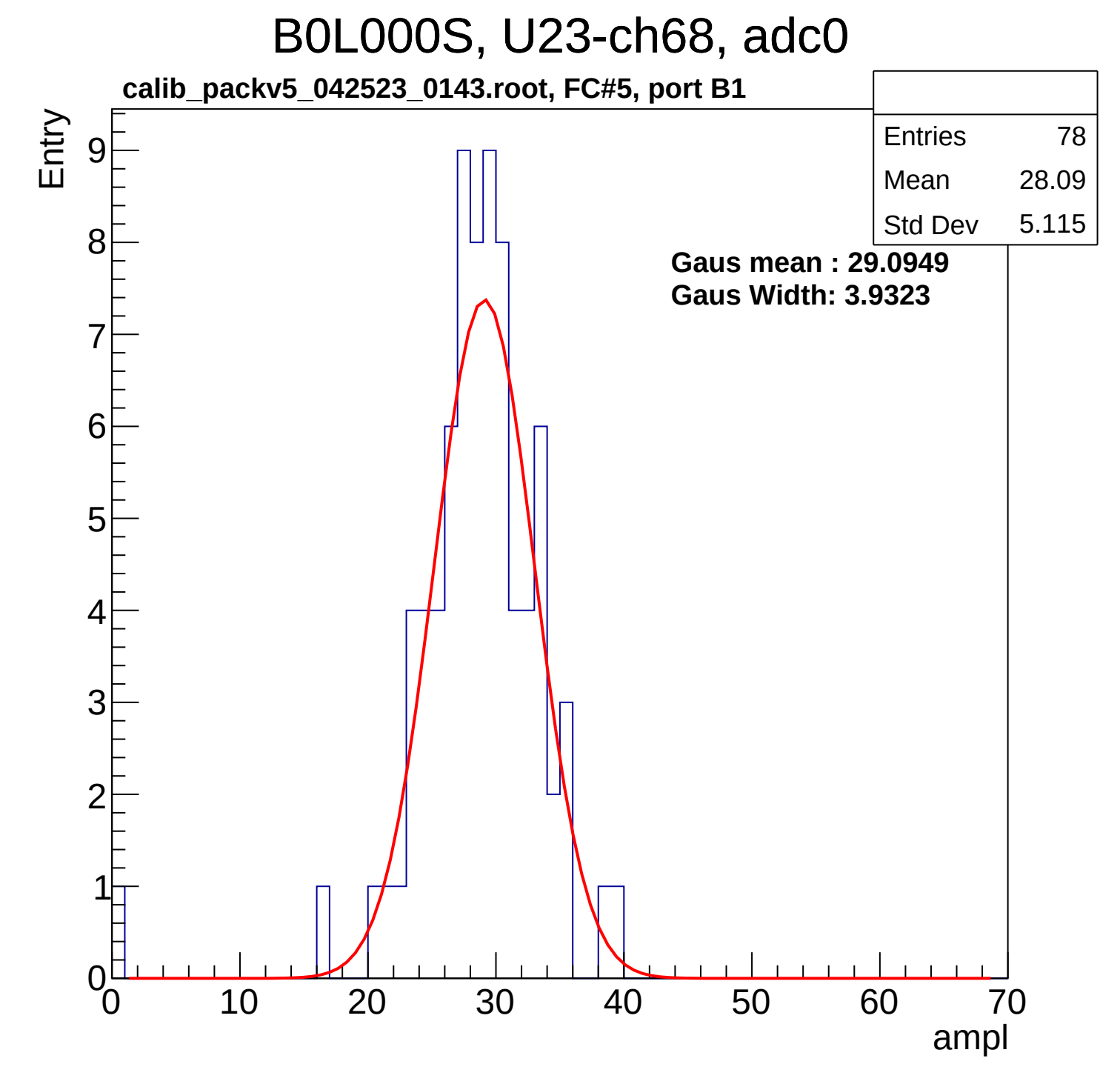

B0L000S, U23-ch67, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 51.07
Std Dev 2.859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

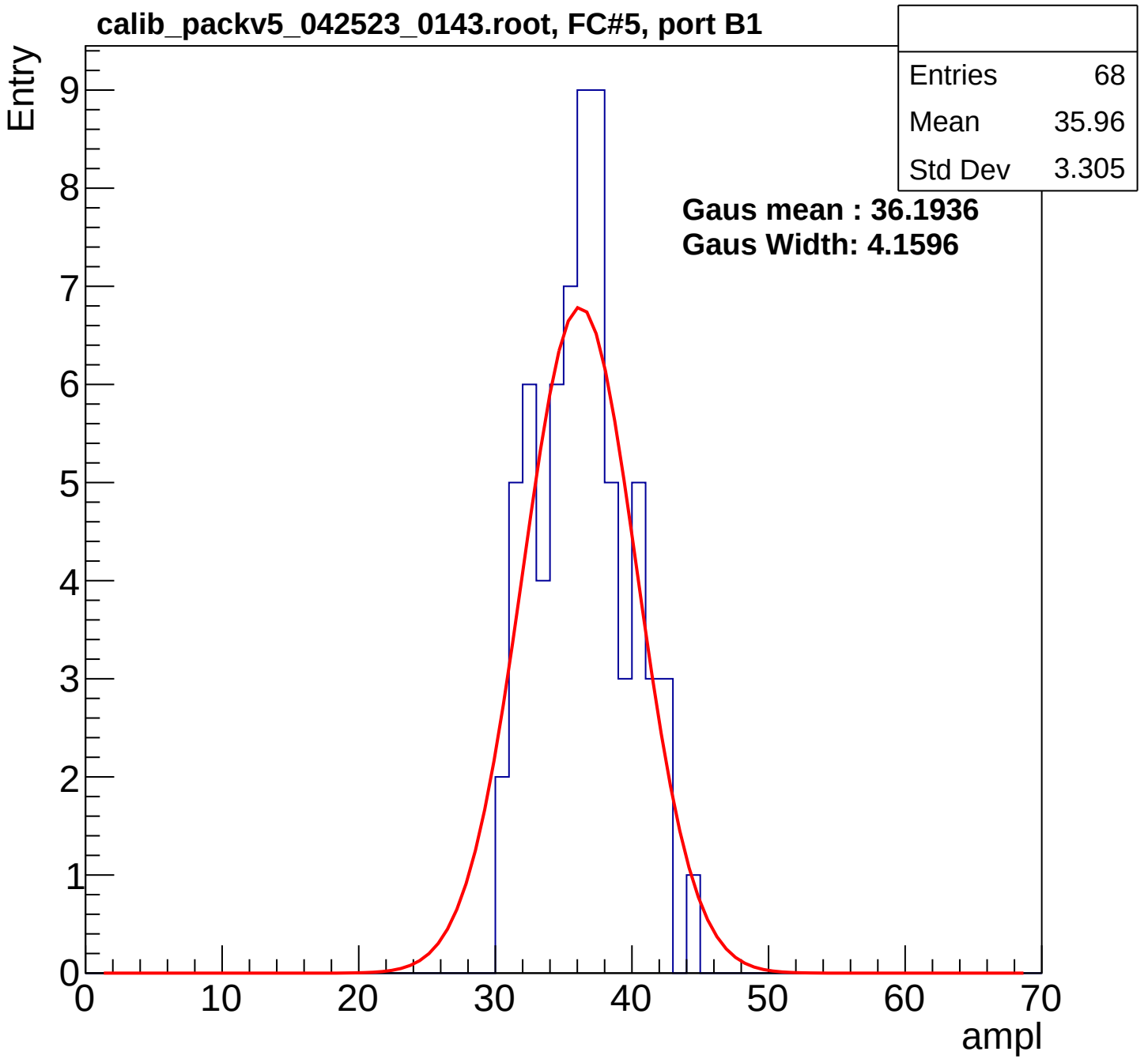

B0L000S, U23-ch67, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.13
Std Dev 8.26
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

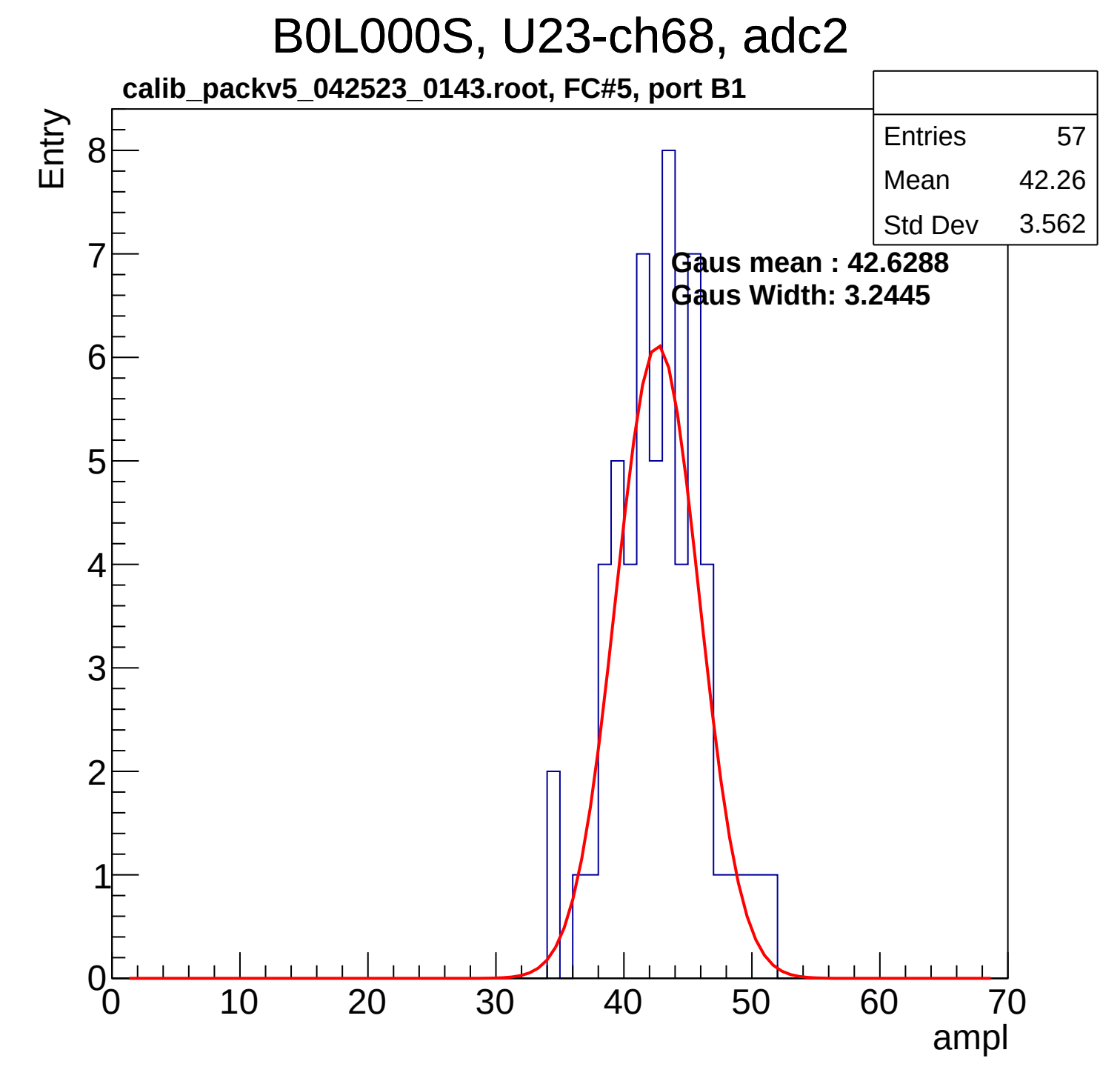

B0L000S, U23-ch67, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 60.69
Std Dev 2.331
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

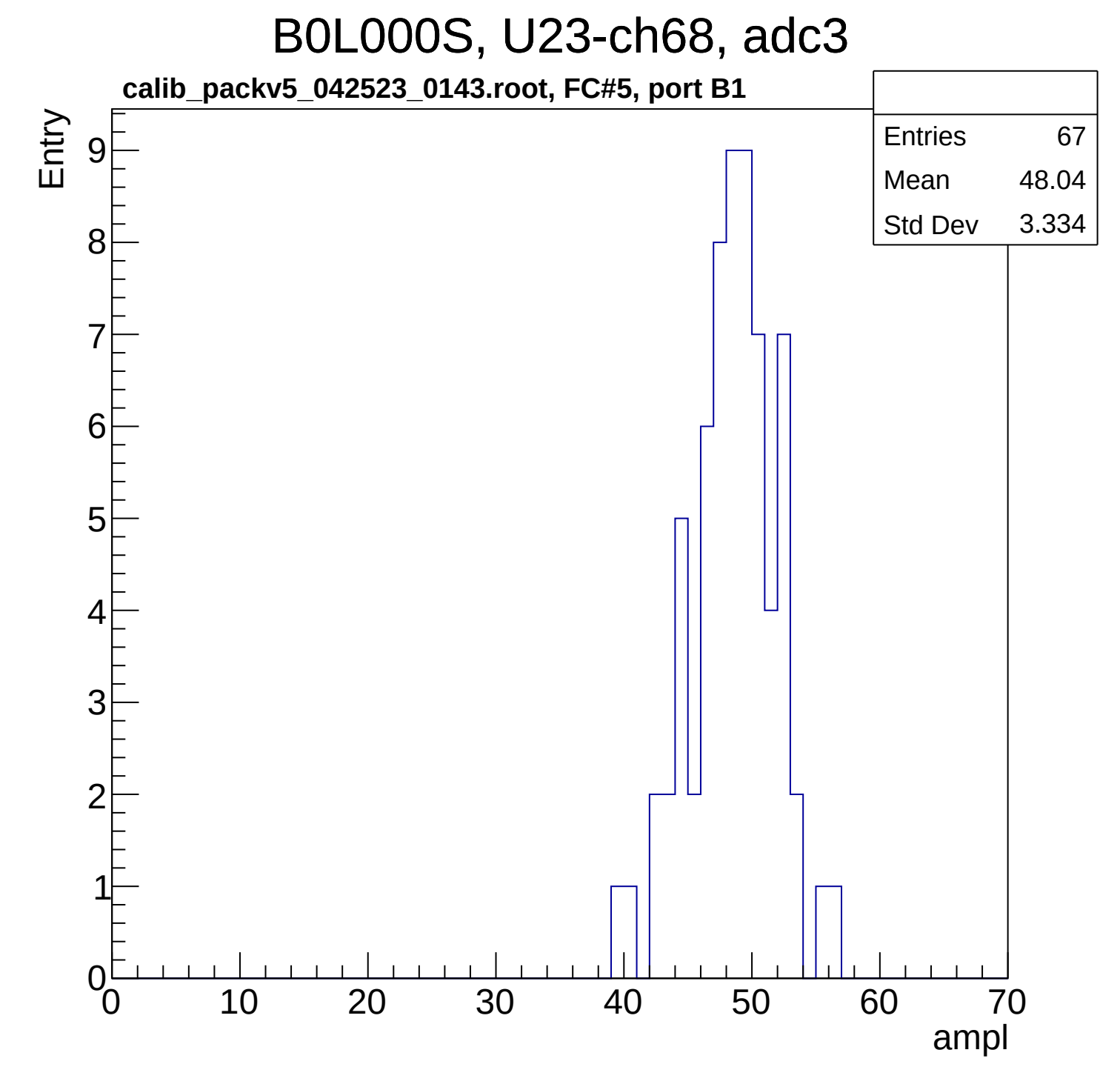

B0L000S, U23-ch67, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

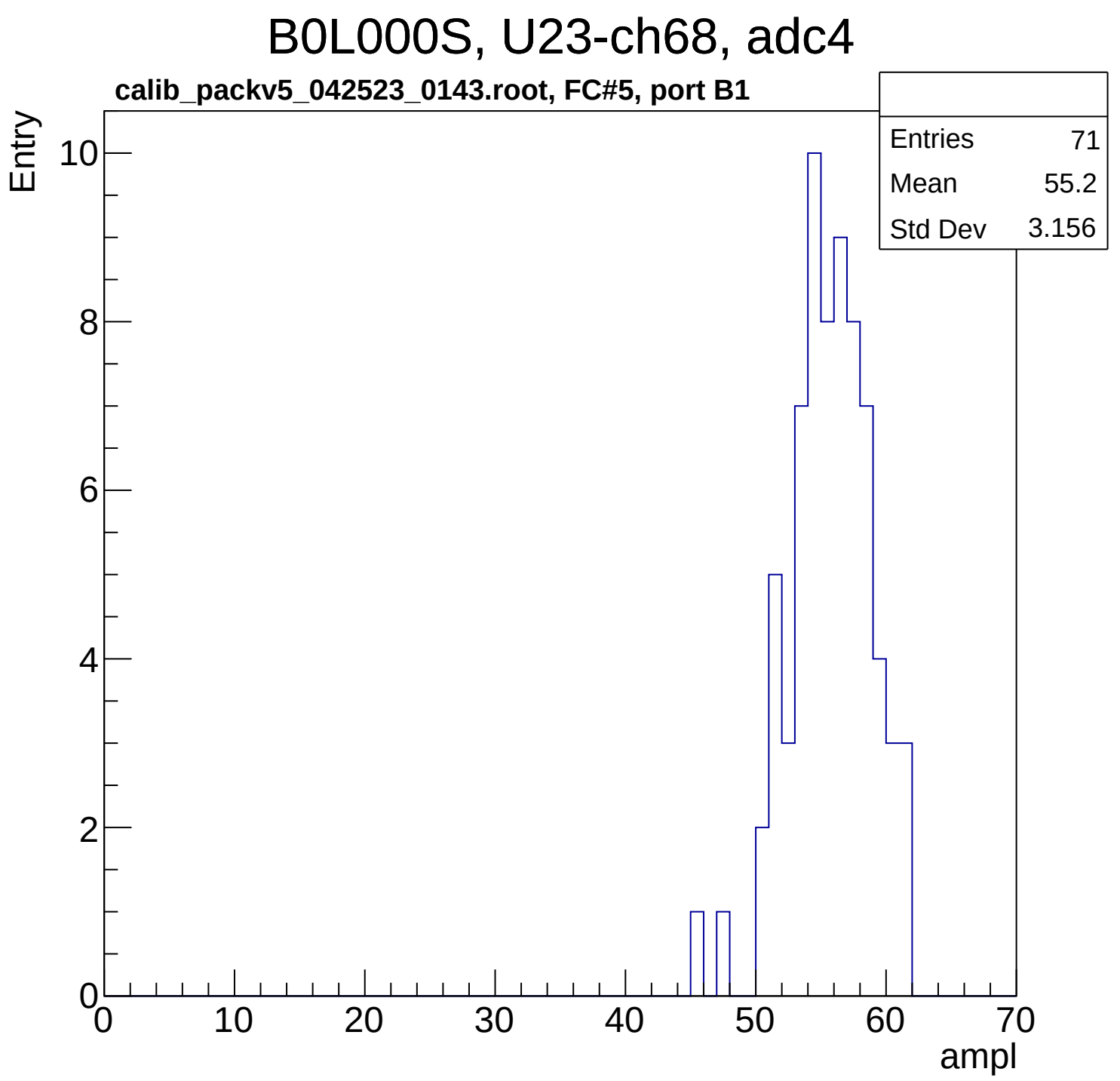

B0L000S, U23-ch67, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

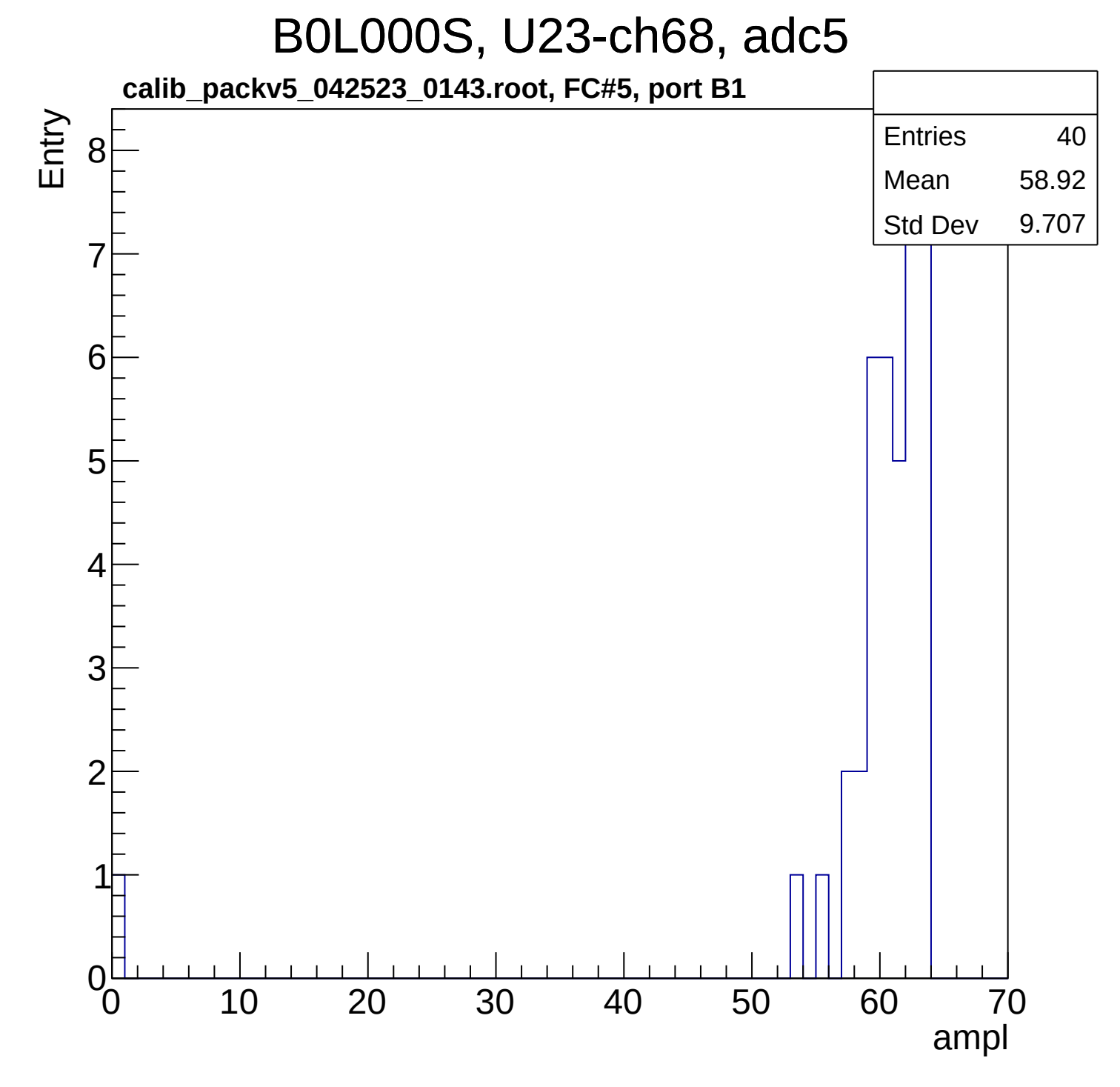

B0L000S, U23-ch68, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 28.09
Std Dev 5.115
Gaus mean : 29.0949
Gaus Width: 3.9323
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

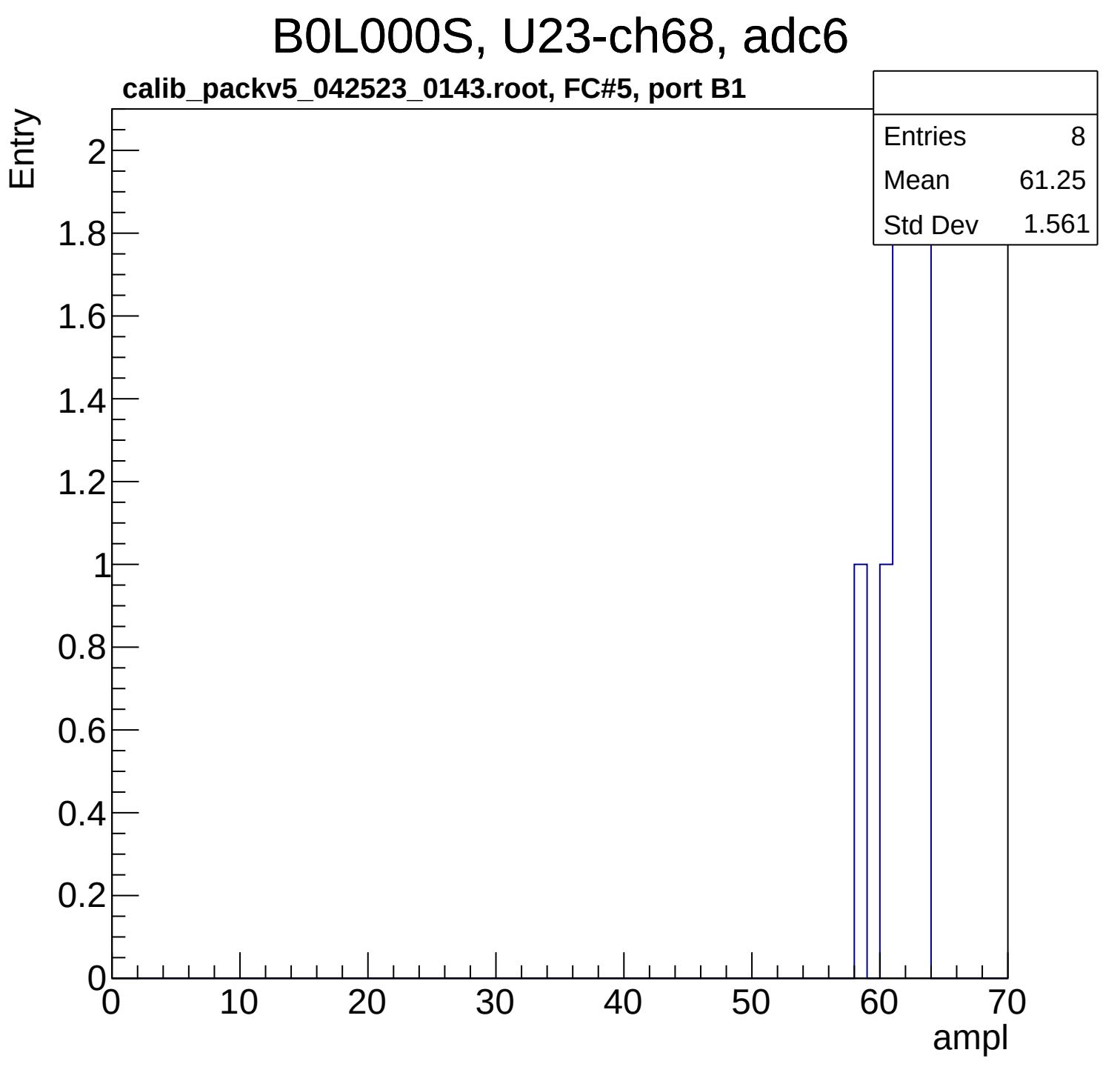

B0L000S, U23-ch68, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 35.96
Std Dev 3.305
Gaus mean : 36.1936
Gaus Width: 4.1596
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

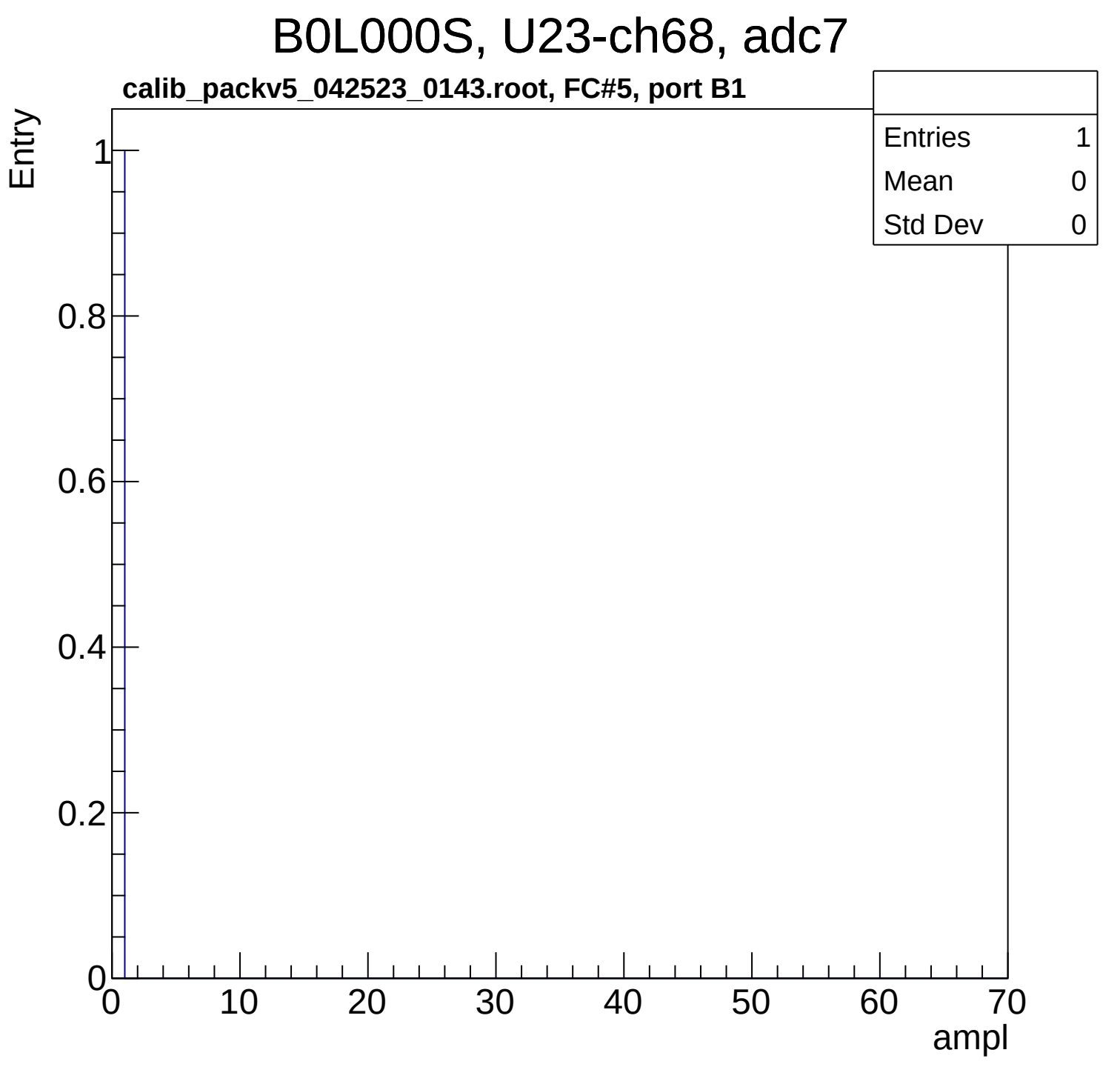

B0L000S, U23-ch68, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 42.26
Std Dev 3.562
Gaus mean : 42.6288
Gaus Width: 3.2445
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

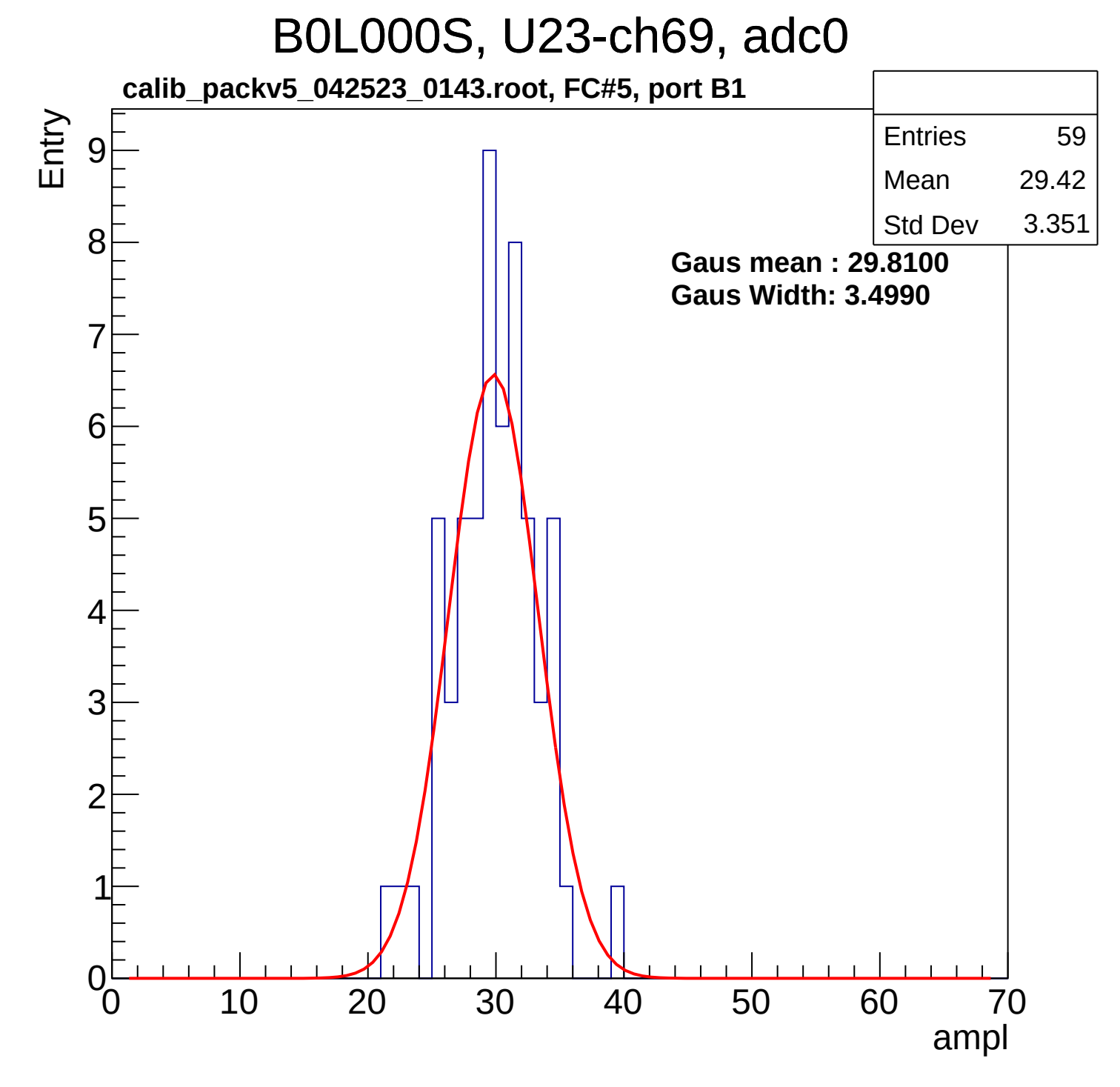

B0L000S, U23-ch68, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.04
Std Dev 3.334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

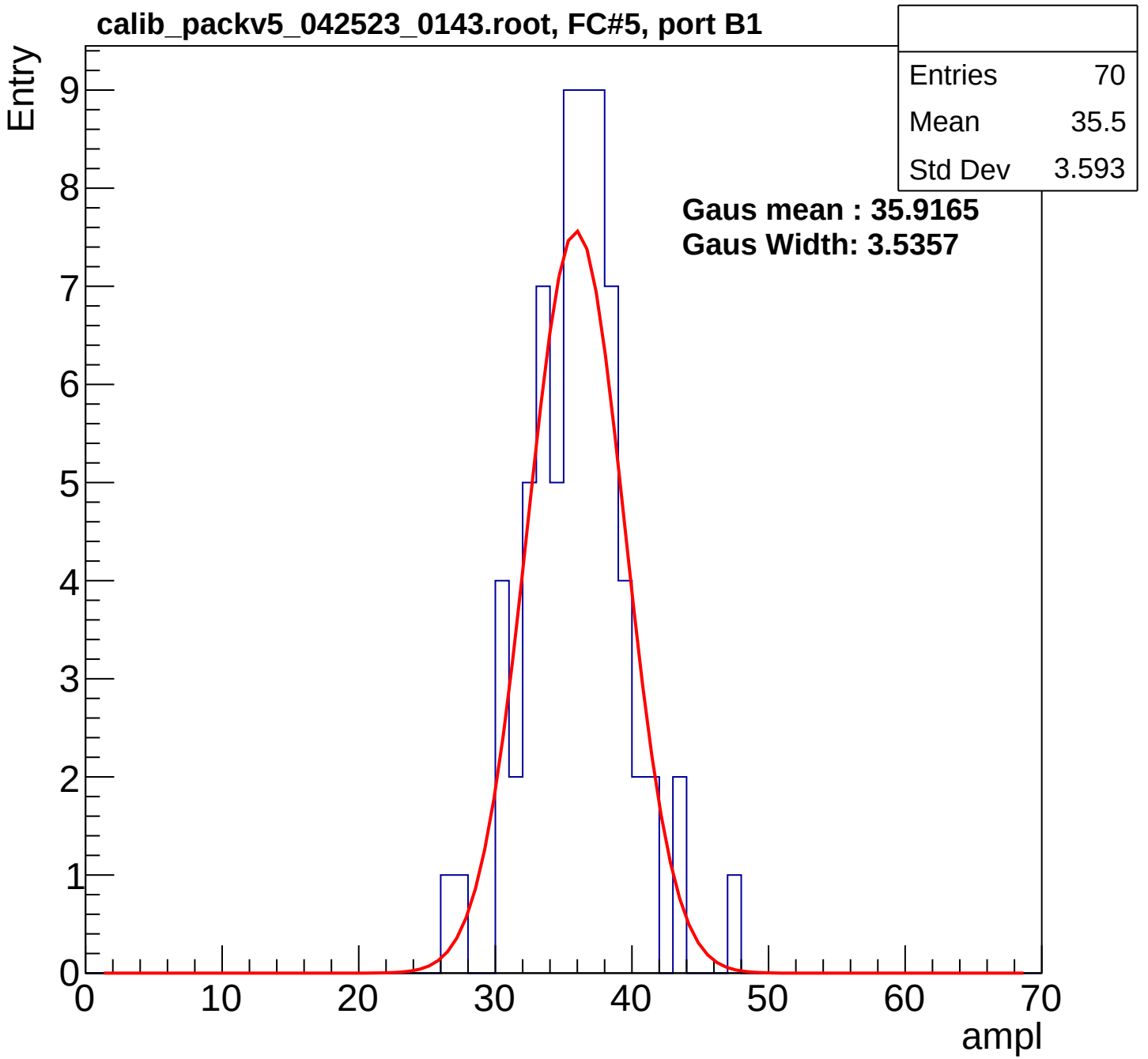

B0L000S, U23-ch68, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 55.2
Std Dev 3.156
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

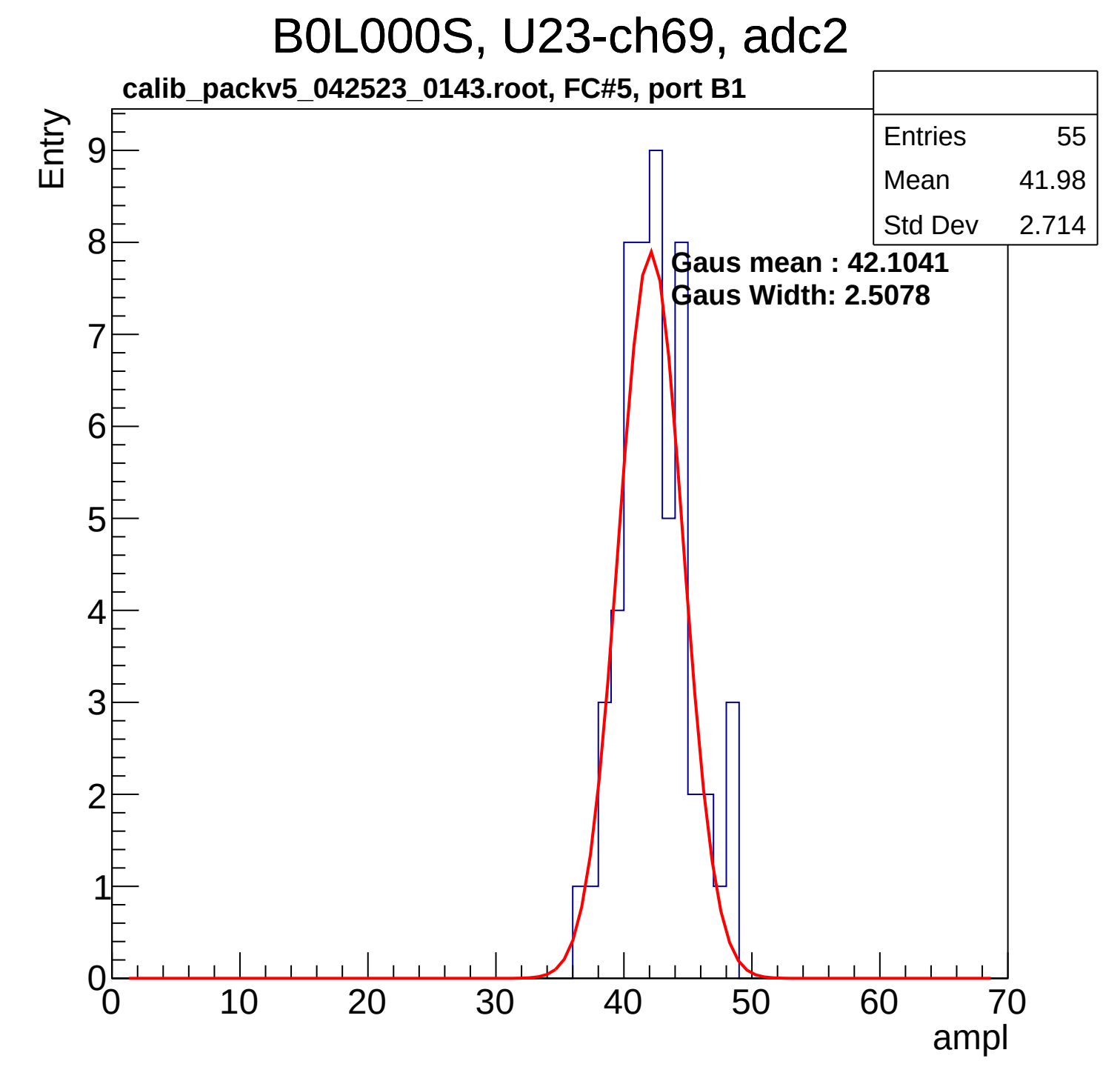

B0L000S, U23-ch68, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.92
Std Dev 9.707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

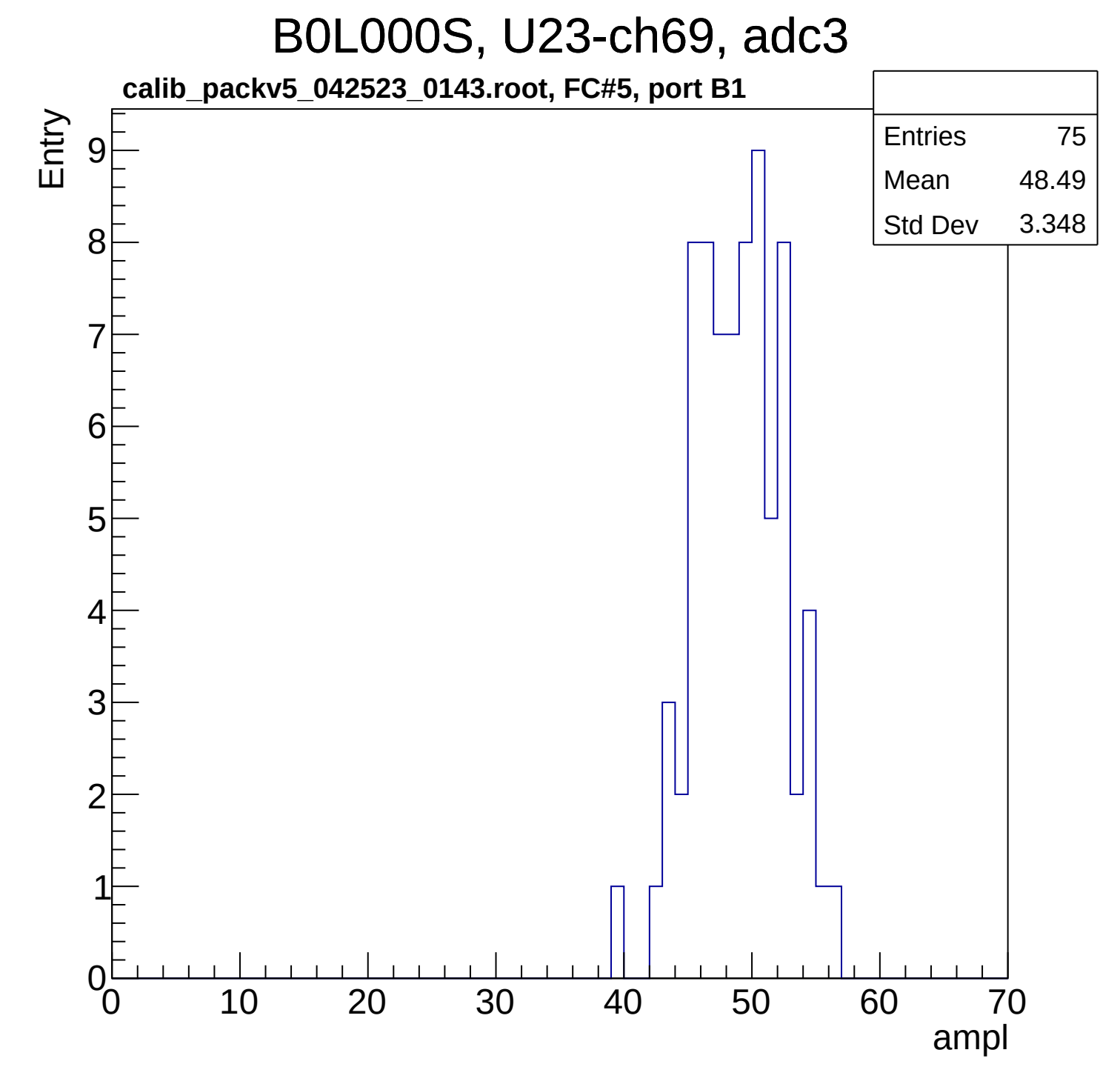

B0L000S, U23-ch68, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.25
Std Dev 1.561
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

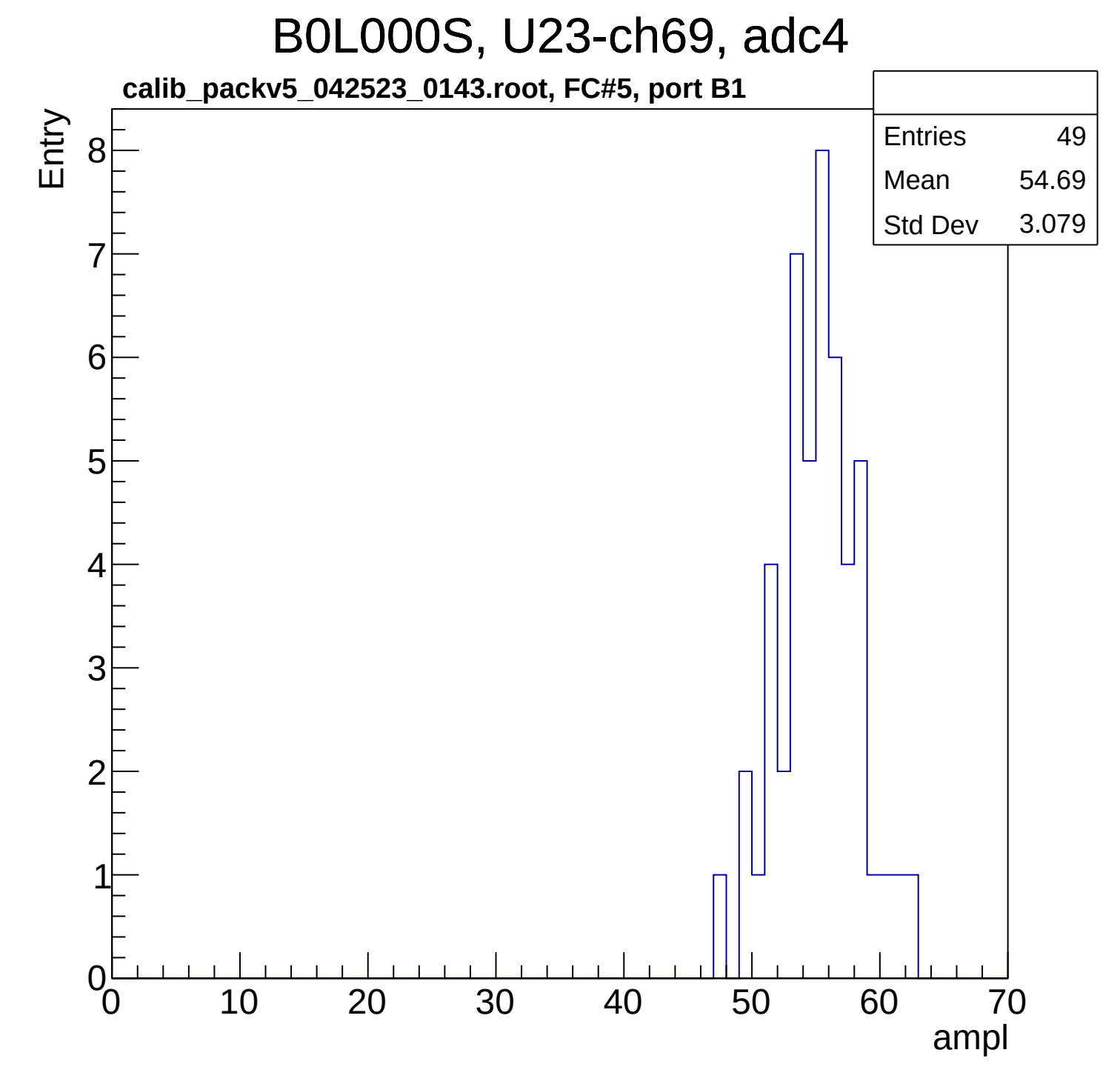

B0L000S, U23-ch68, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

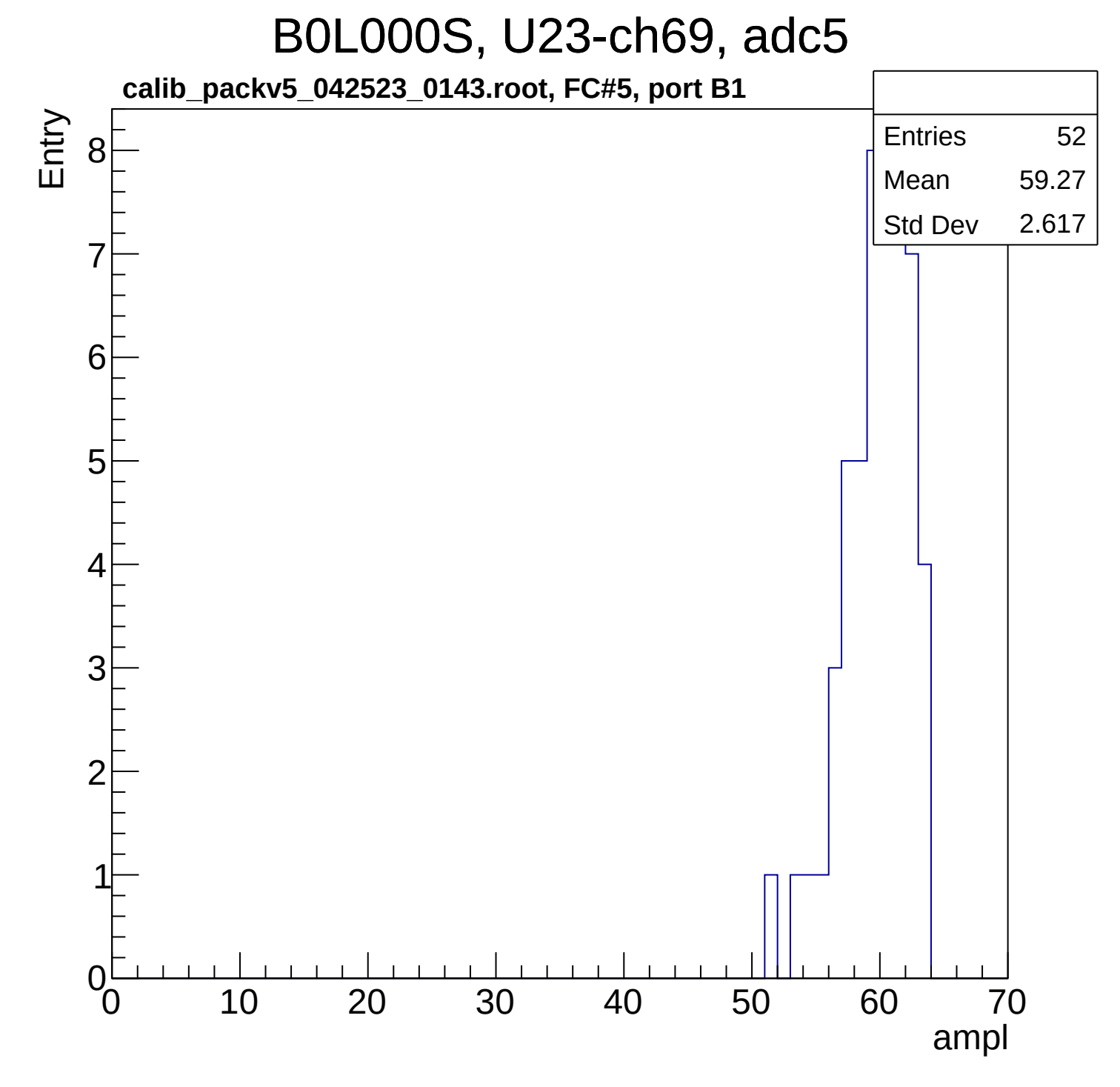

B0L000S, U23-ch69, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 29.42
Std Dev 3.351
Gaus mean : 29.8100
Gaus Width: 3.4990
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

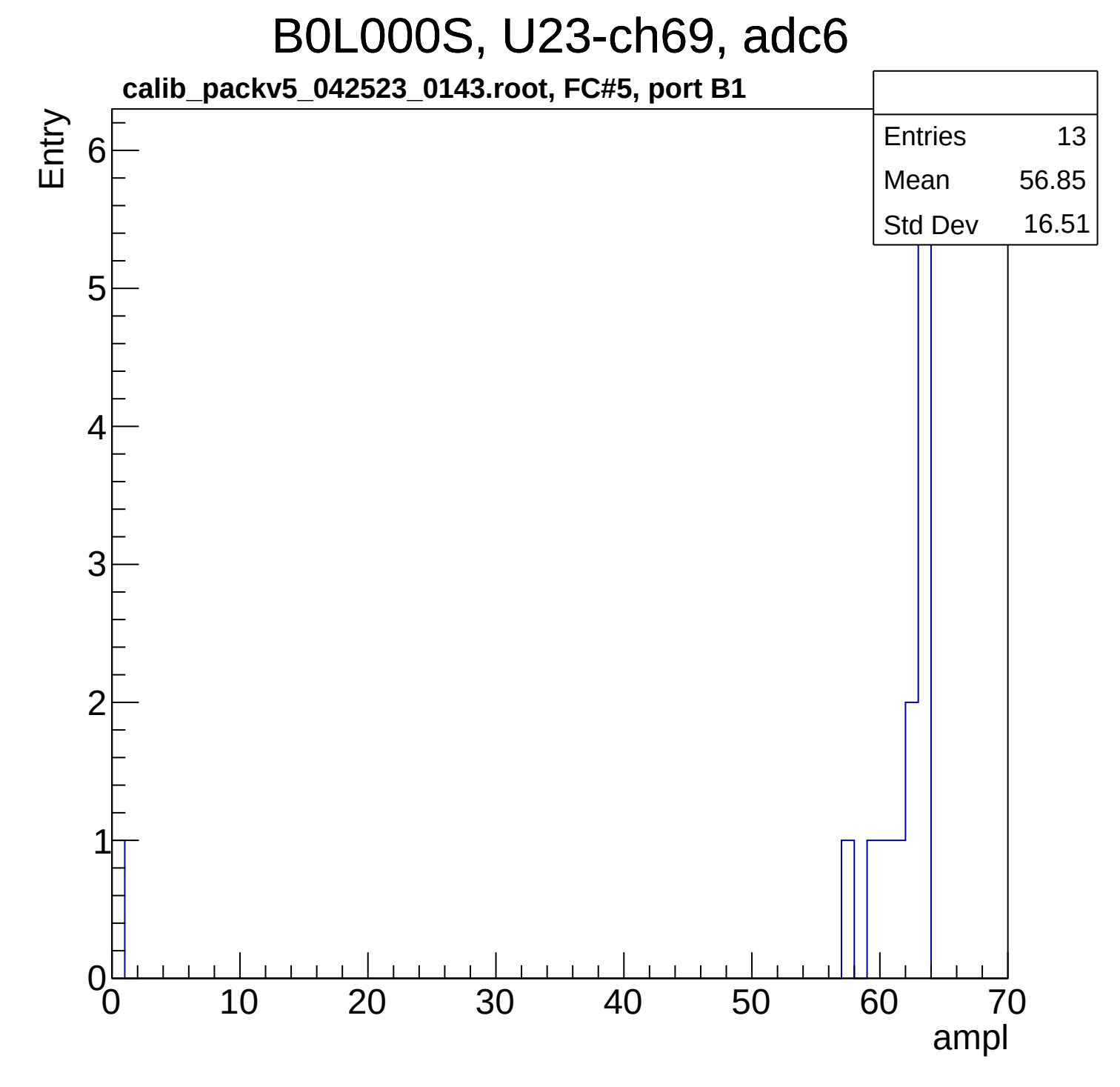

B0L000S, U23-ch69, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 35.5
Std Dev 3.593
Gaus mean : 35.9165
Gaus Width: 3.5357
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

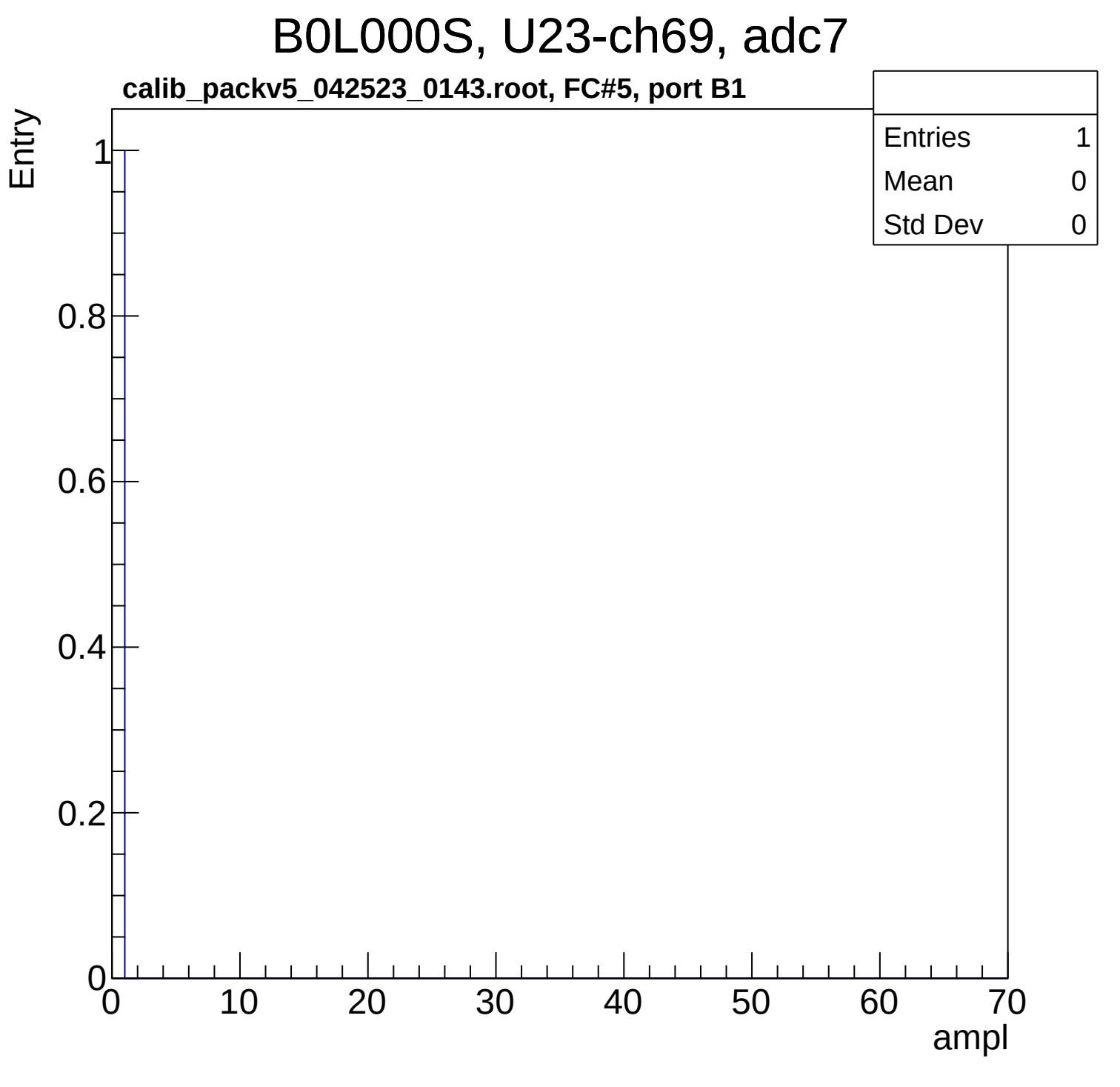

B0L000S, U23-ch69, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 41.98
Std Dev 2.714
Gaus mean : 42.1041
Gaus Width: 2.5078
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

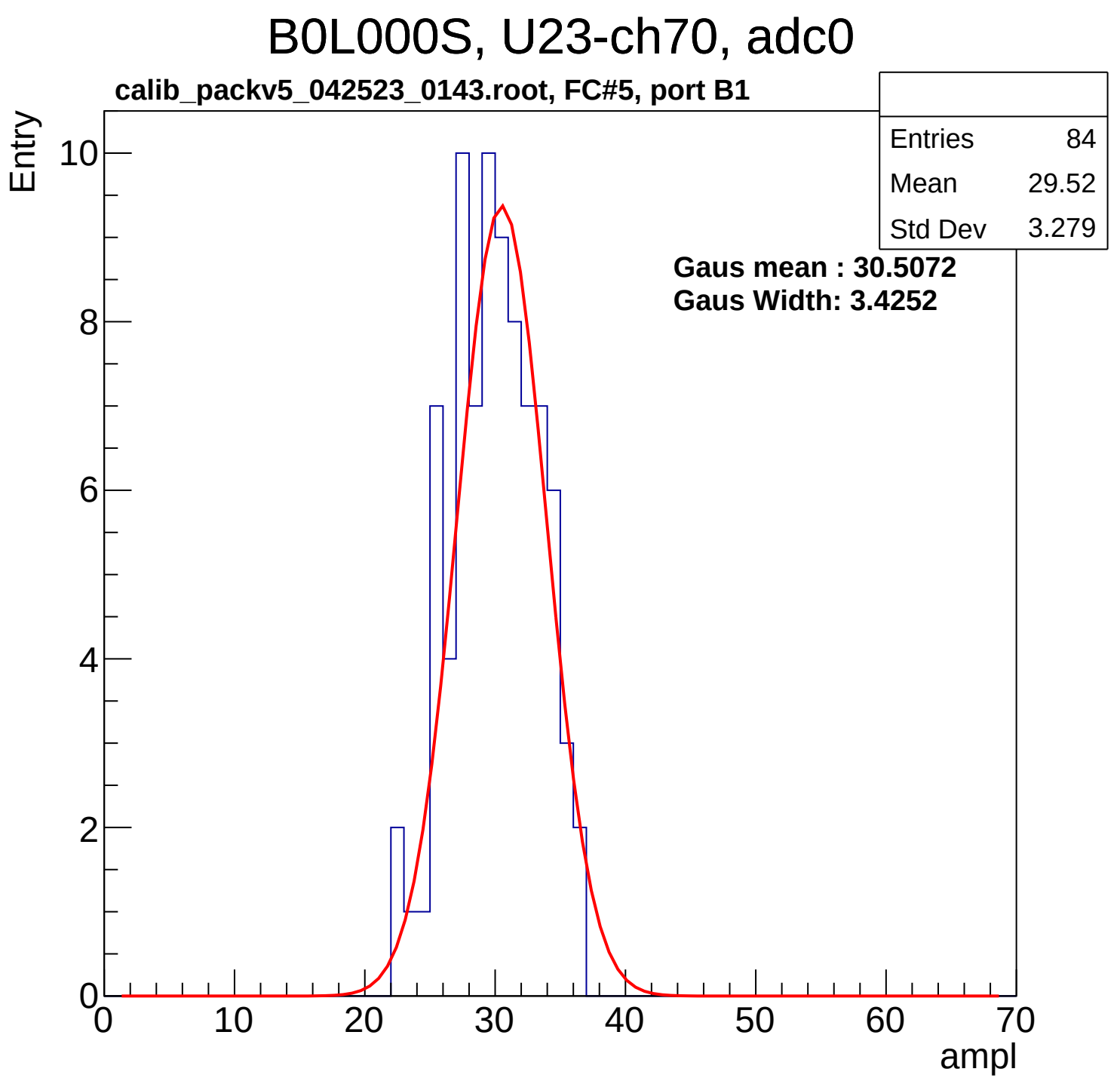

B0L000S, U23-ch69, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.49
Std Dev 3.348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

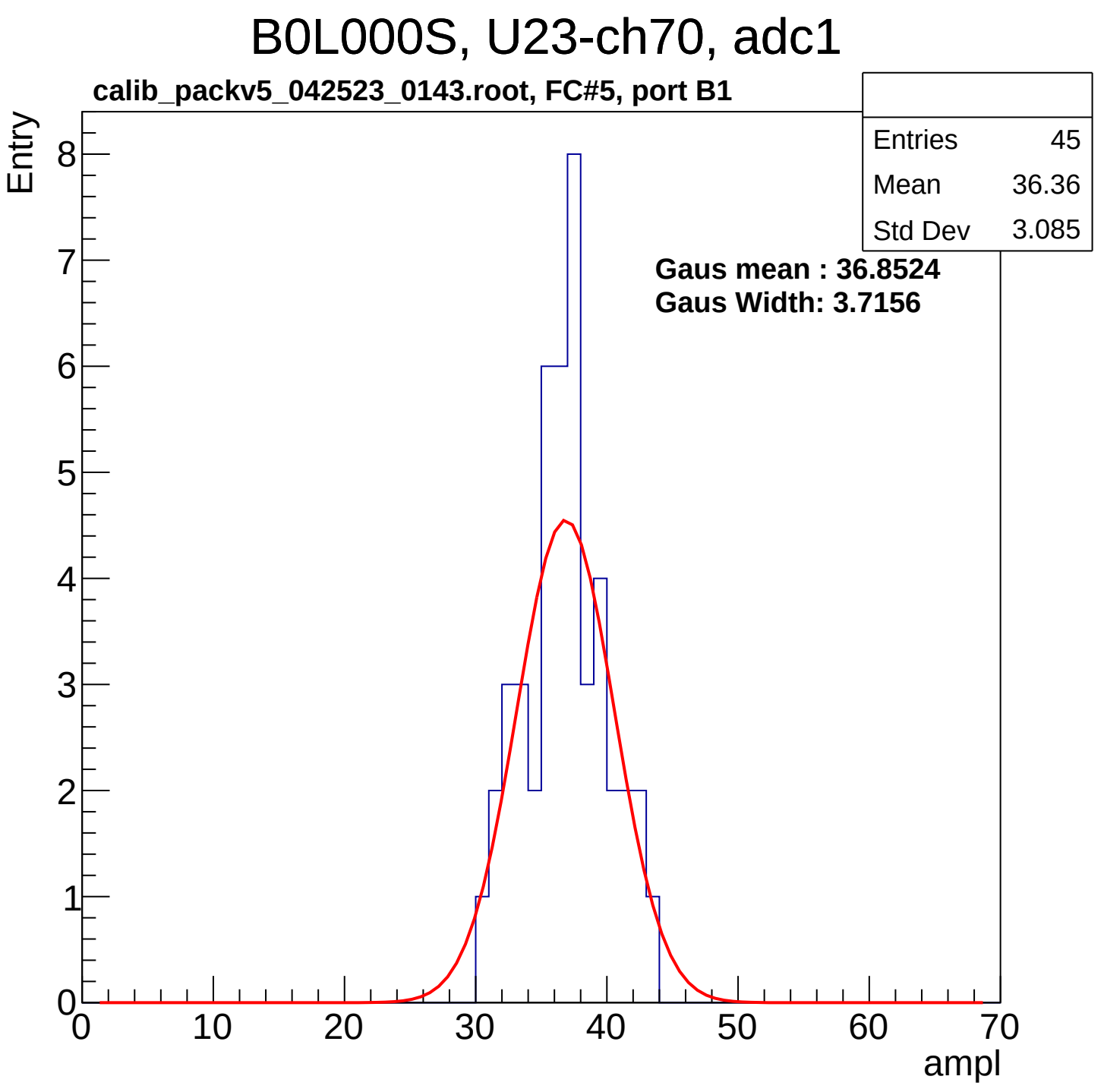

B0L000S, U23-ch69, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 54.69
Std Dev 3.079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

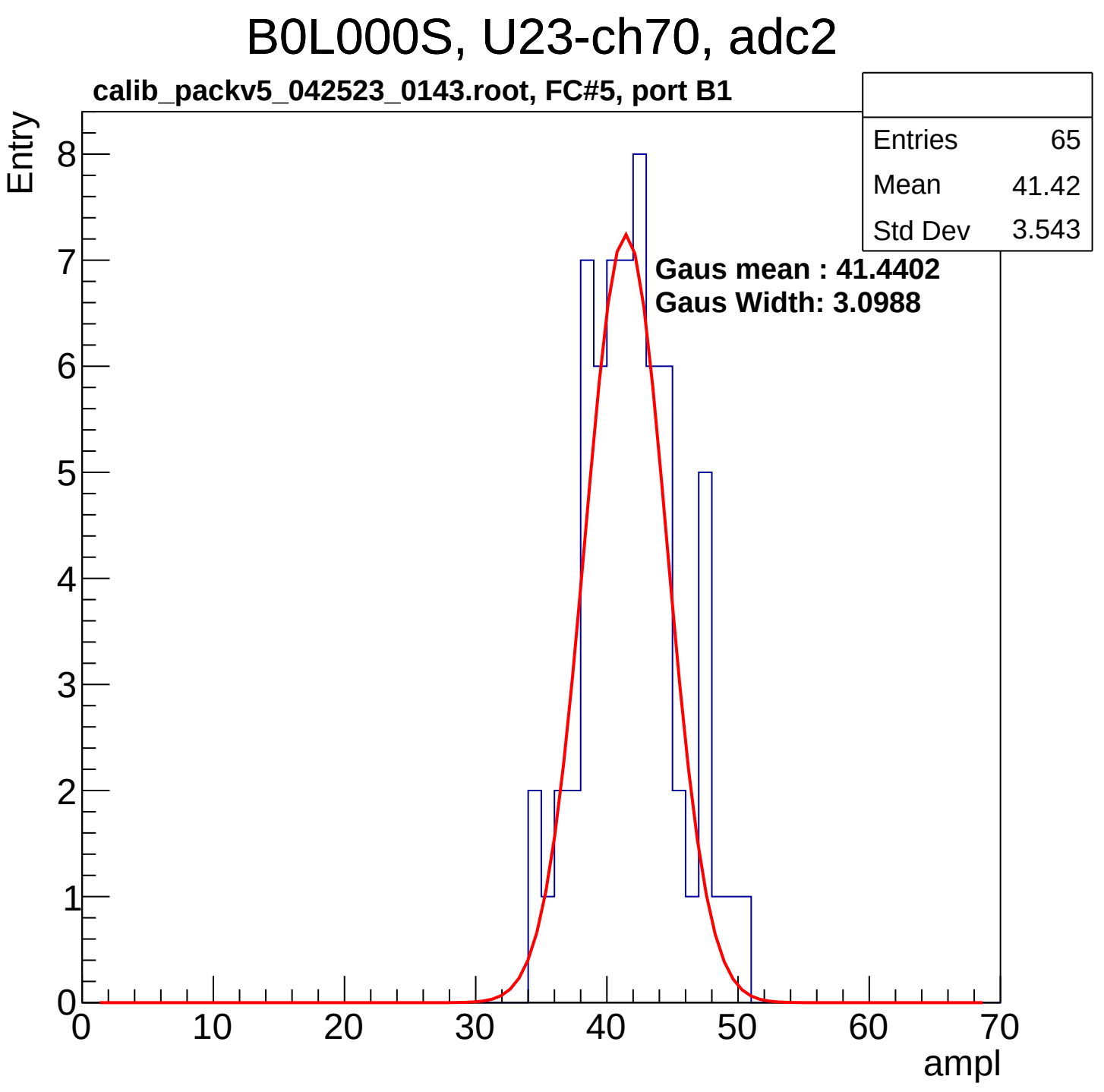

B0L000S, U23-ch69, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.27
Std Dev 2.617
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

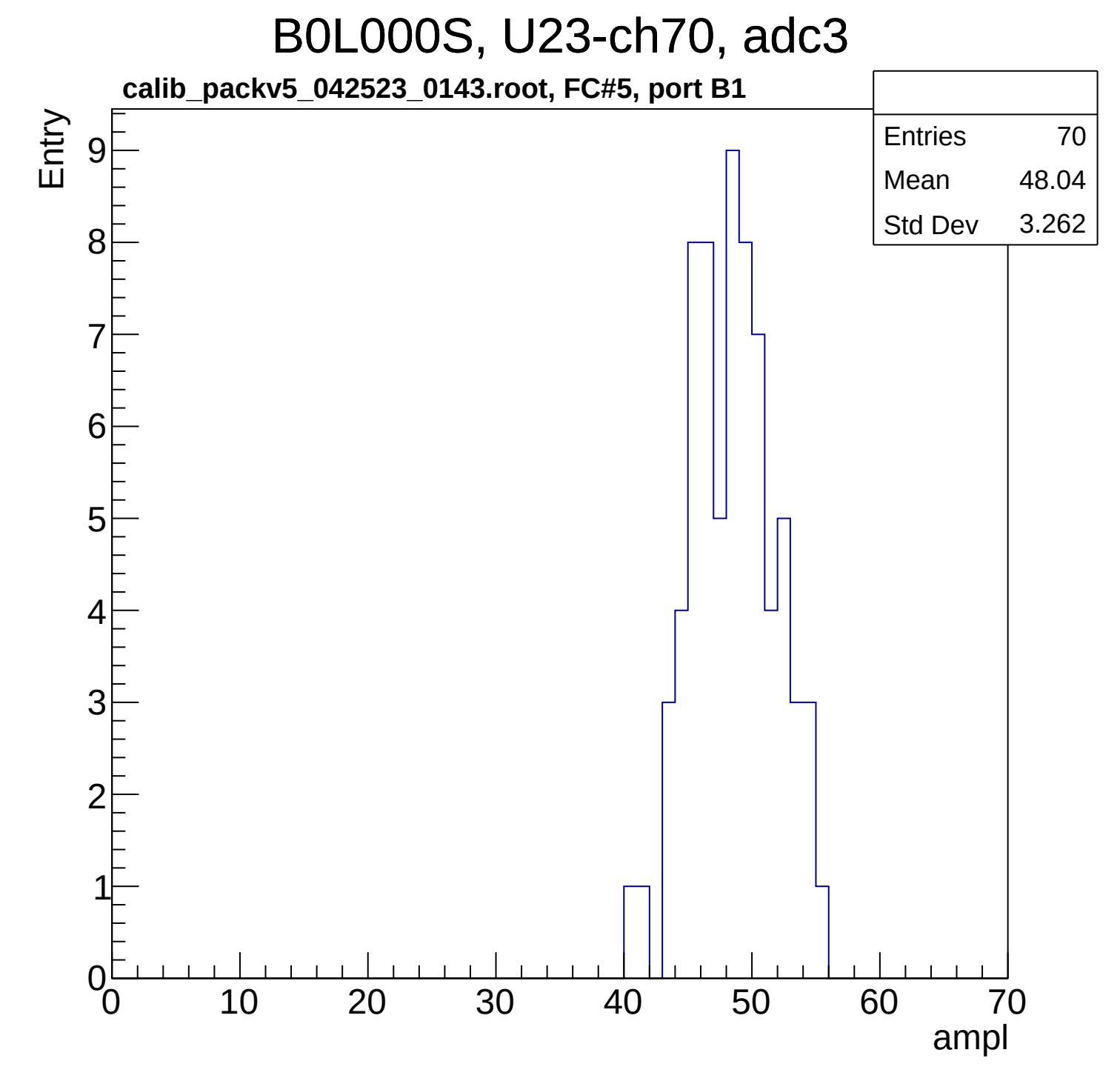

B0L000S, U23-ch69, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.85
Std Dev 16.51
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

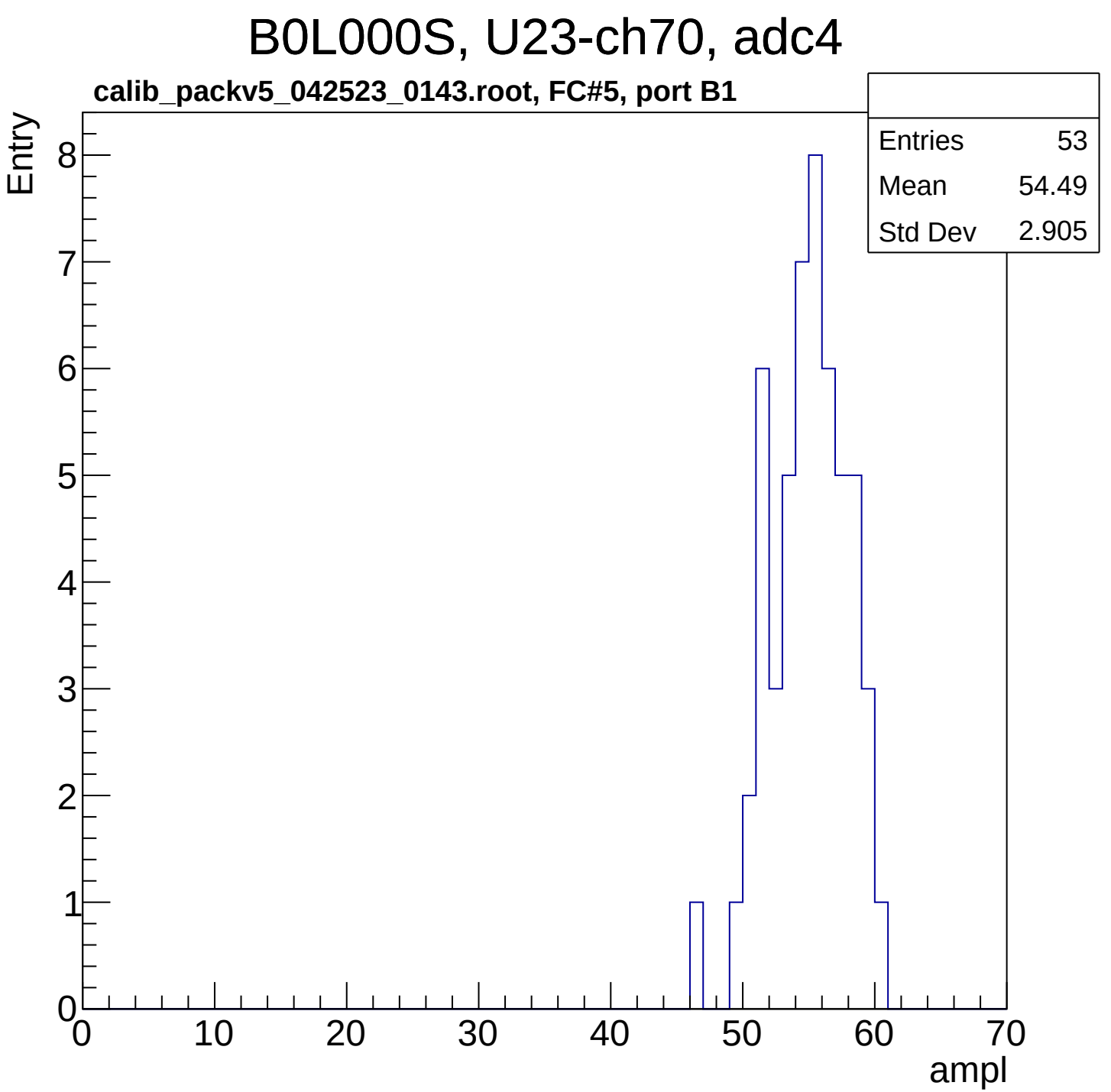

B0L000S, U23-ch69, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

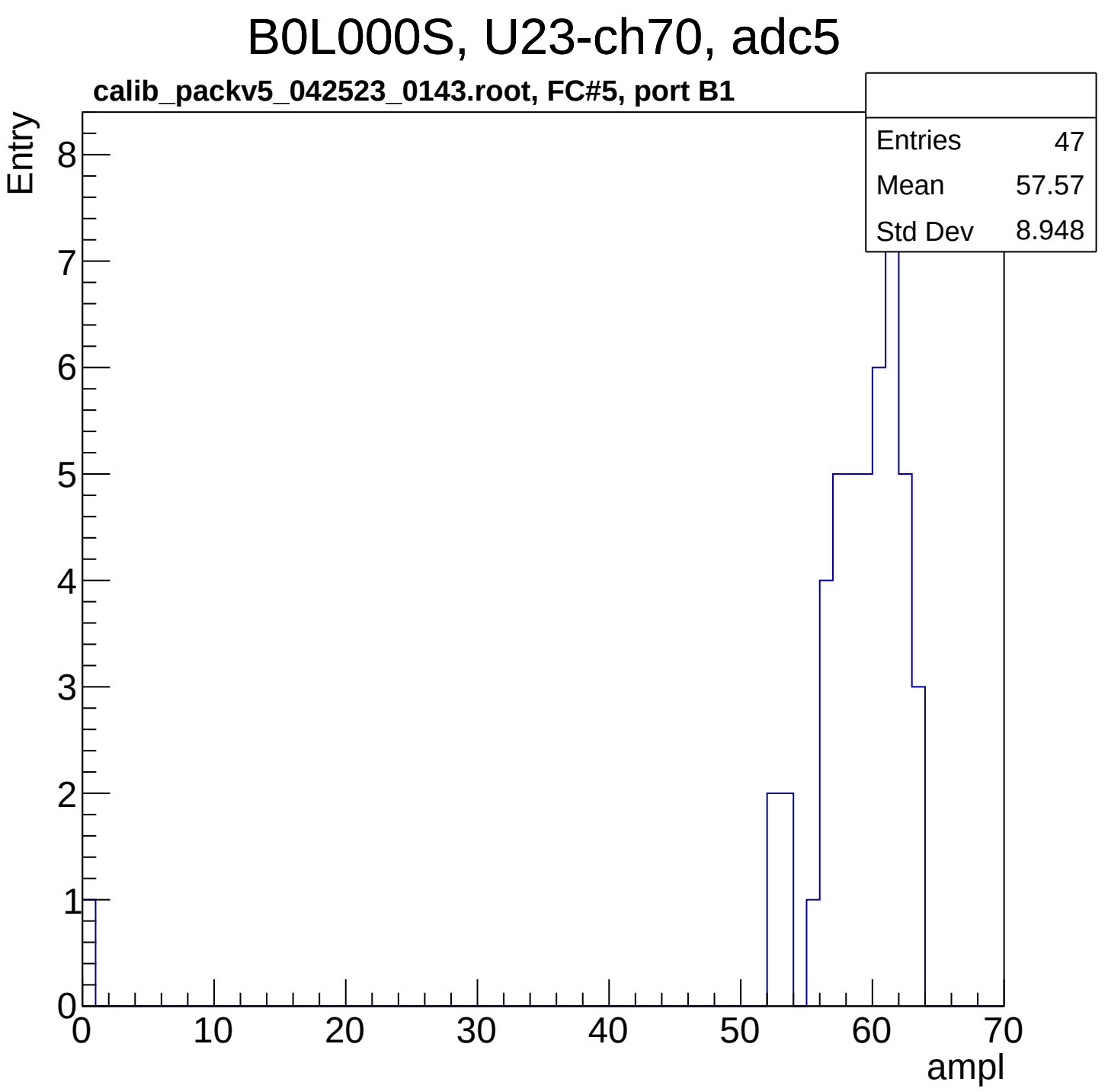

B0L000S, U23-ch70, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 29.52
Std Dev 3.279
Gaus mean : 30.5072
Gaus Width: 3.4252
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

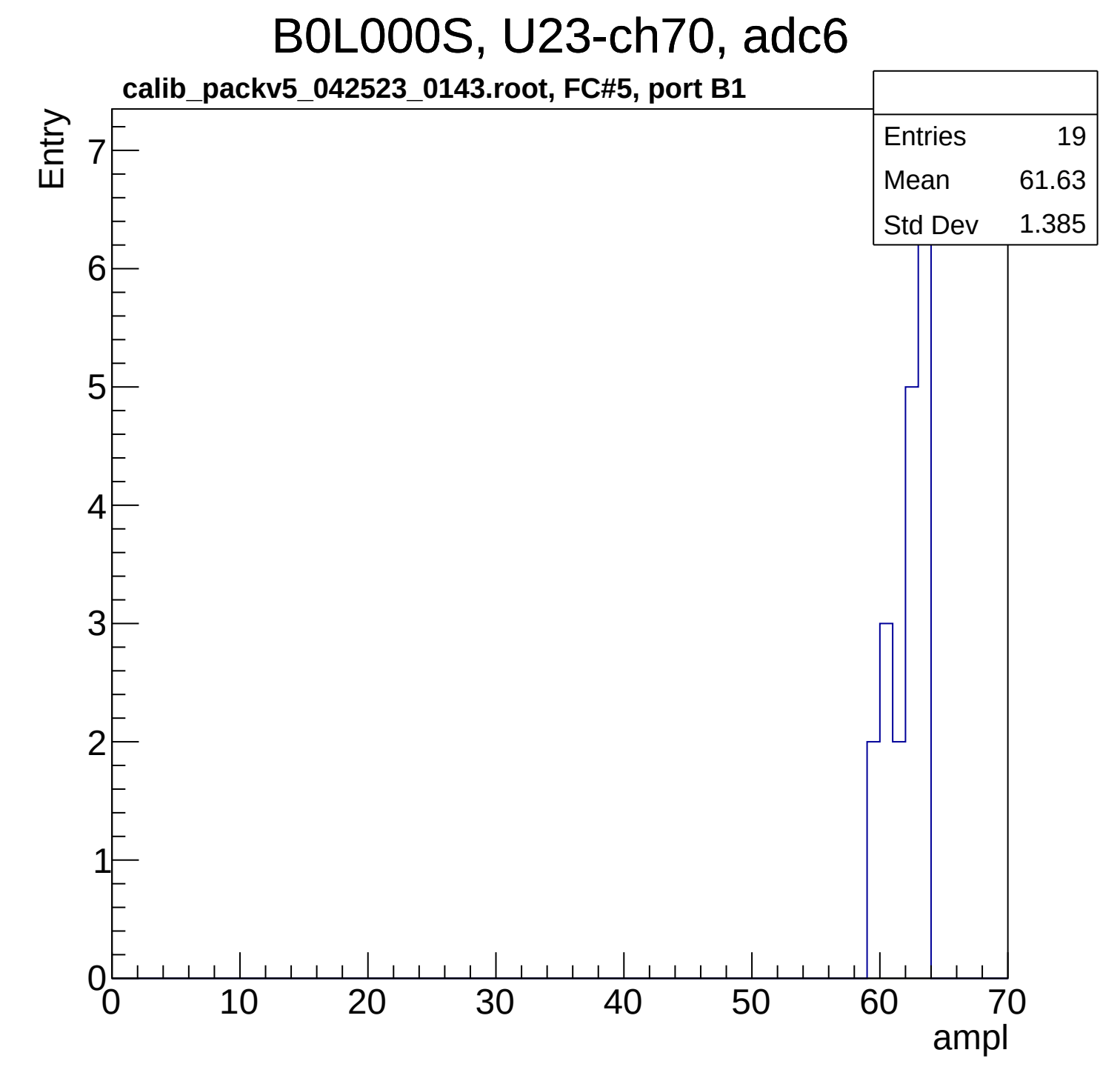

B0L000S, U23-ch70, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 36.36
Std Dev 3.085
Gaus mean : 36.8524
Gaus Width: 3.7156
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

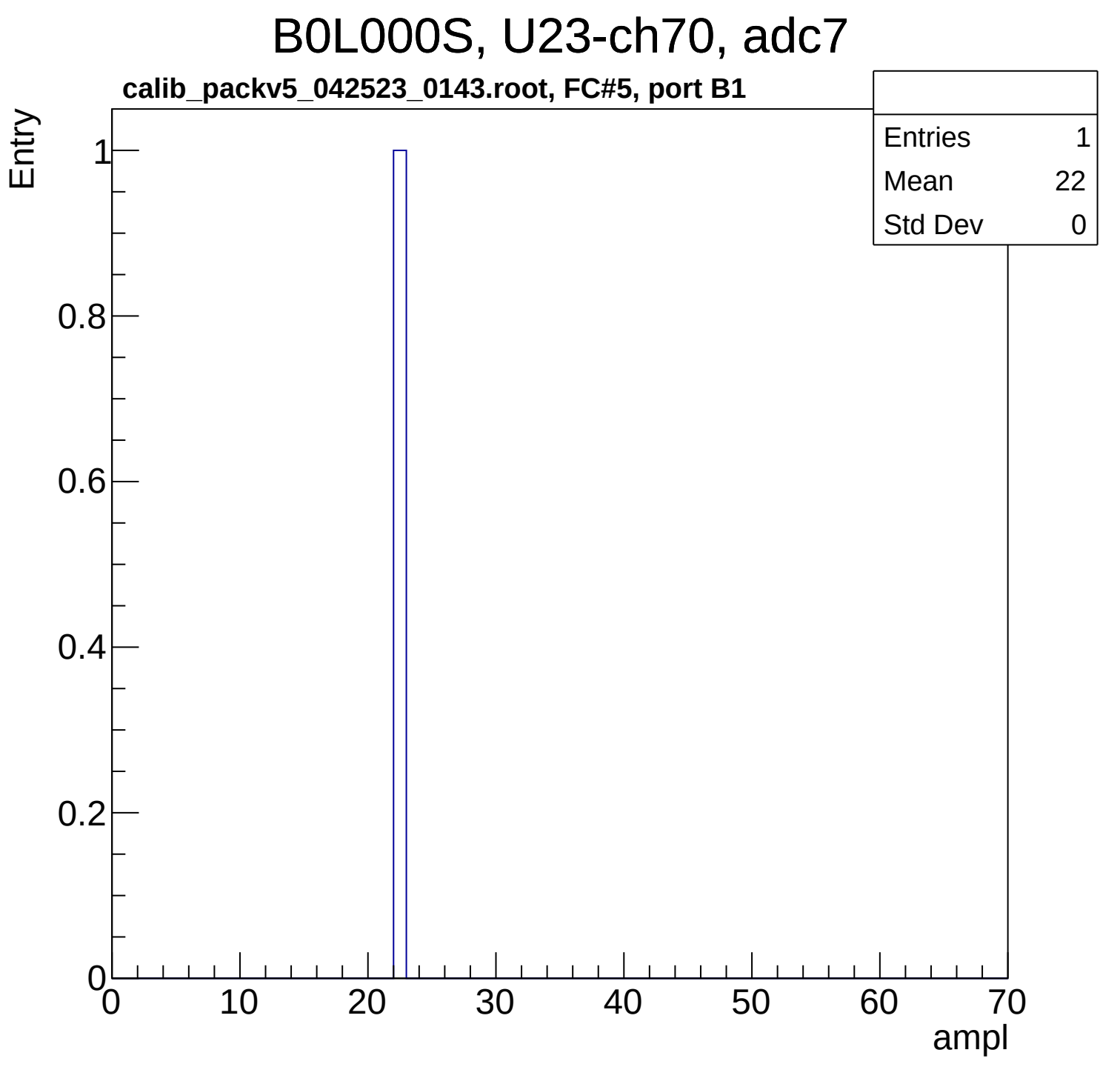

B0L000S, U23-ch70, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.42
Std Dev 3.543
Gaus mean : 41.4402
Gaus Width: 3.0988
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

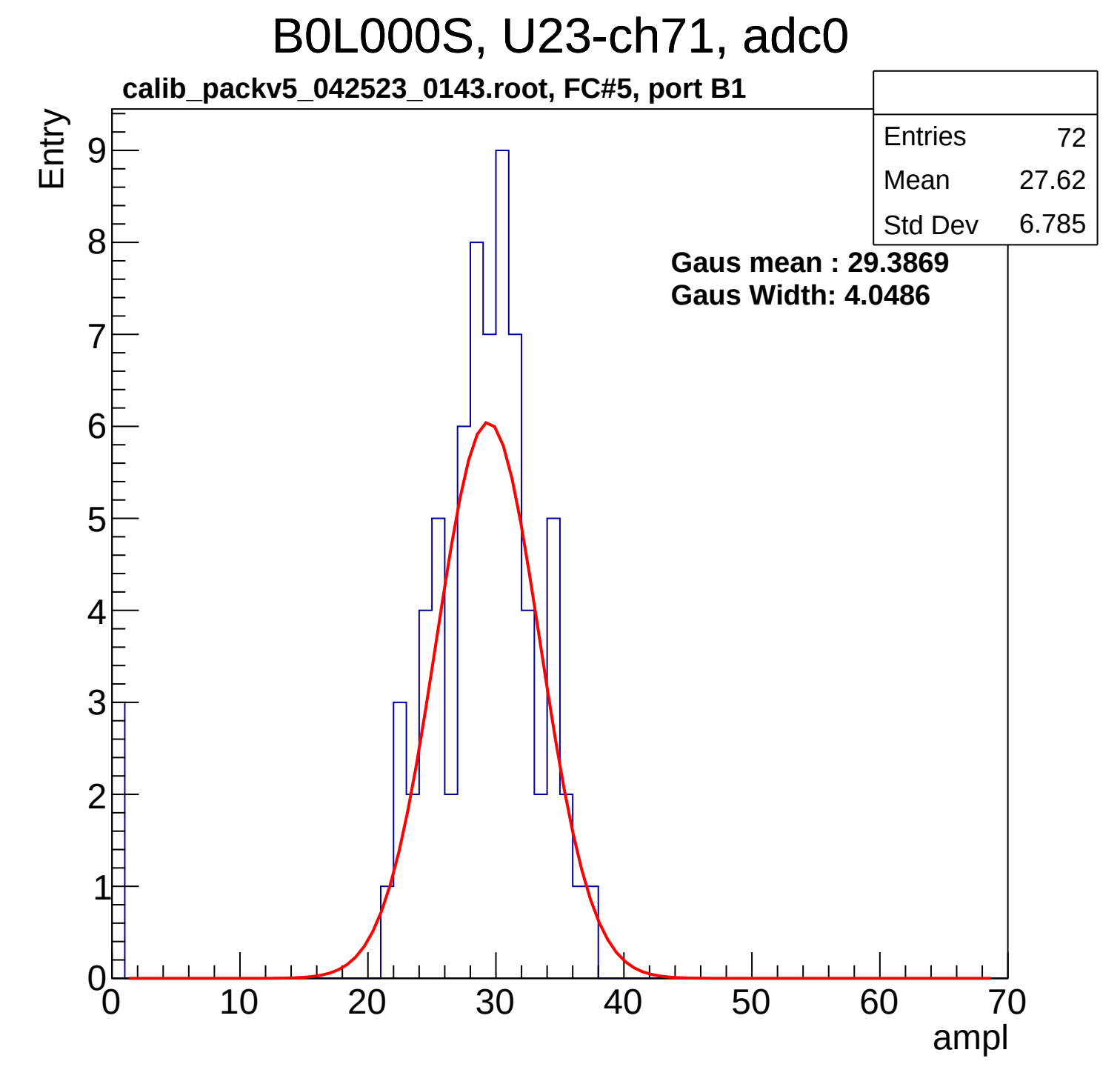

B0L000S, U23-ch70, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.04
Std Dev 3.262
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

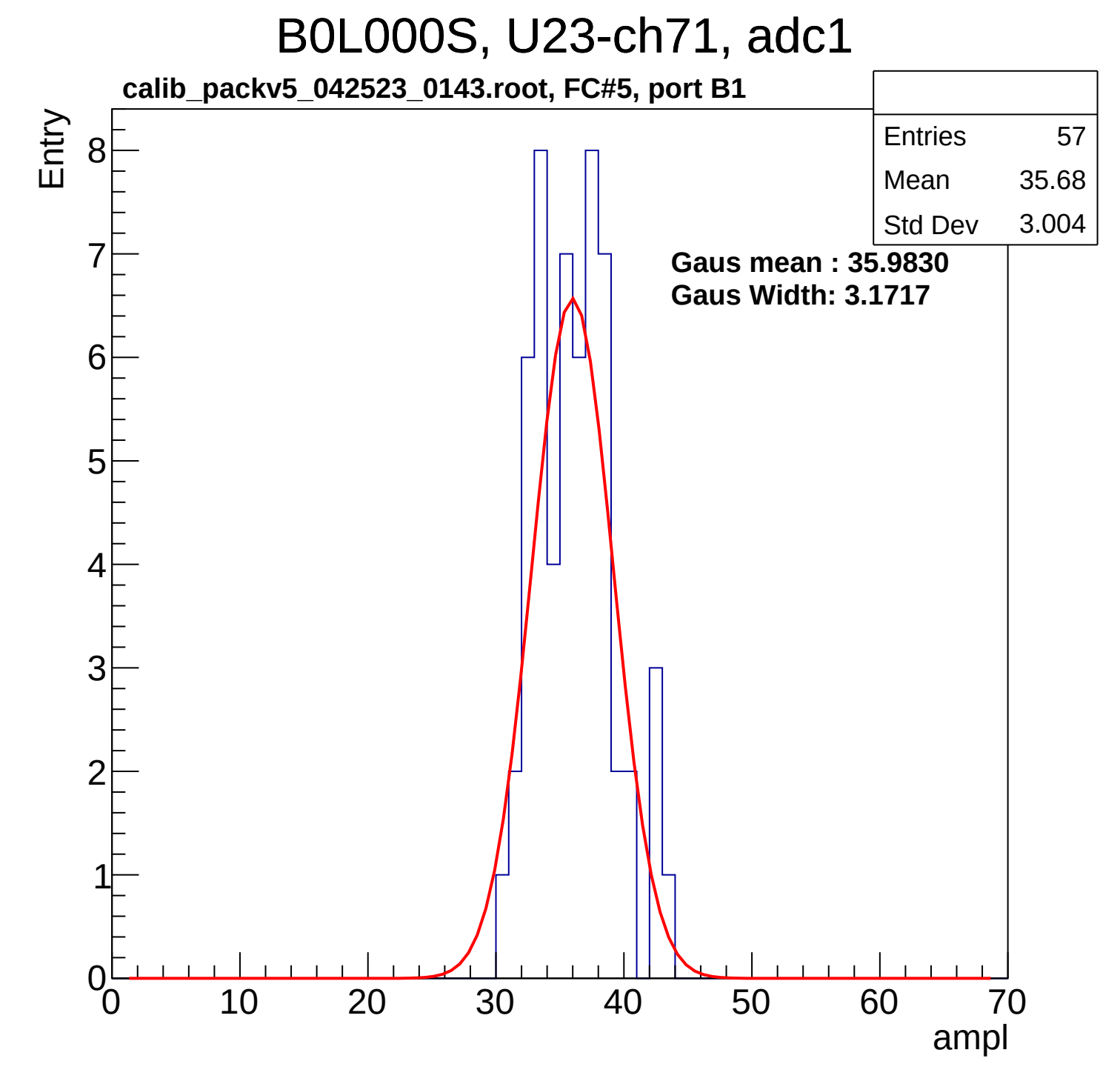

B0L000S, U23-ch70, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.49
Std Dev 2.905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

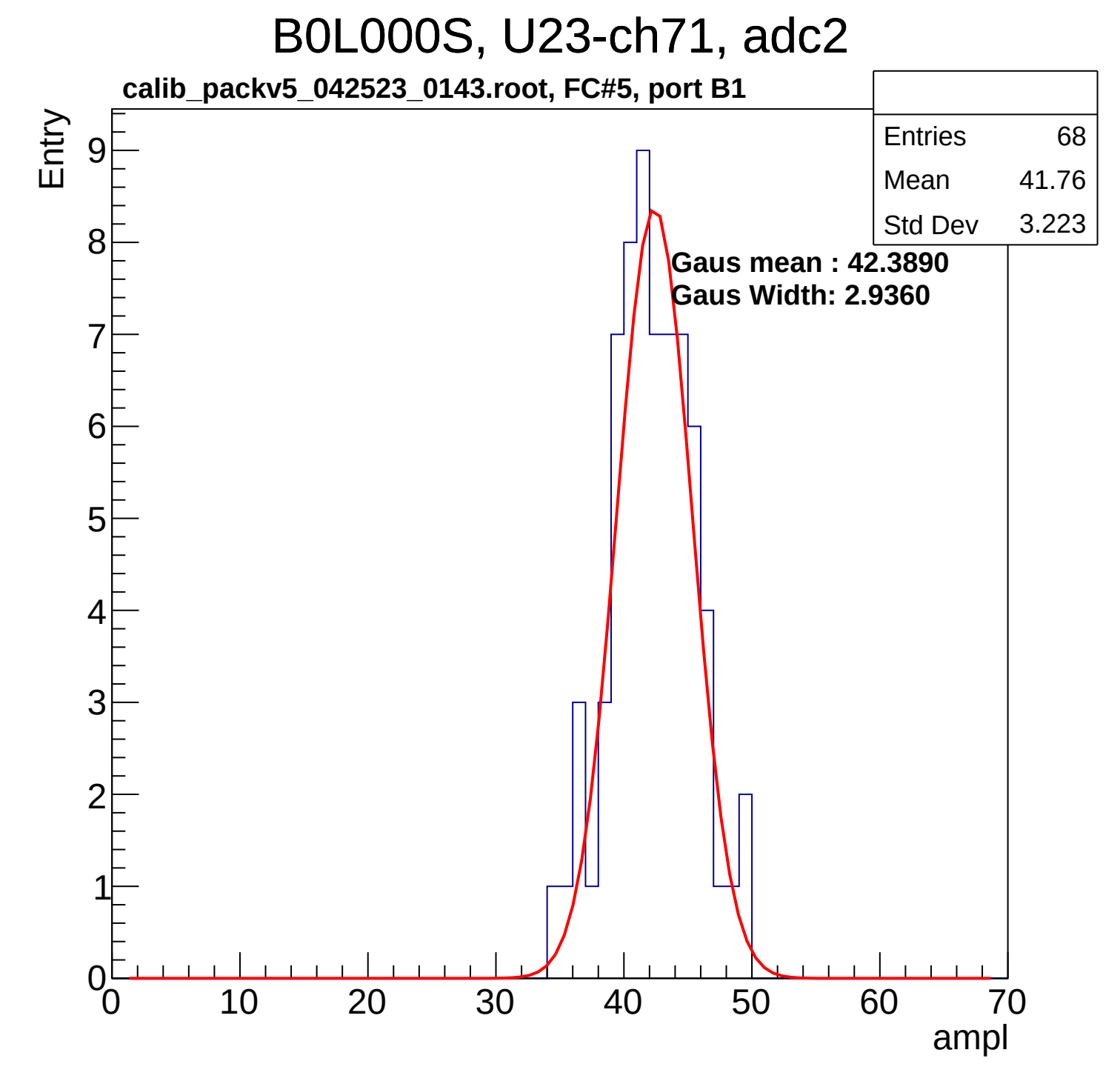

B0L000S, U23-ch70, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 57.57
Std Dev 8.948
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

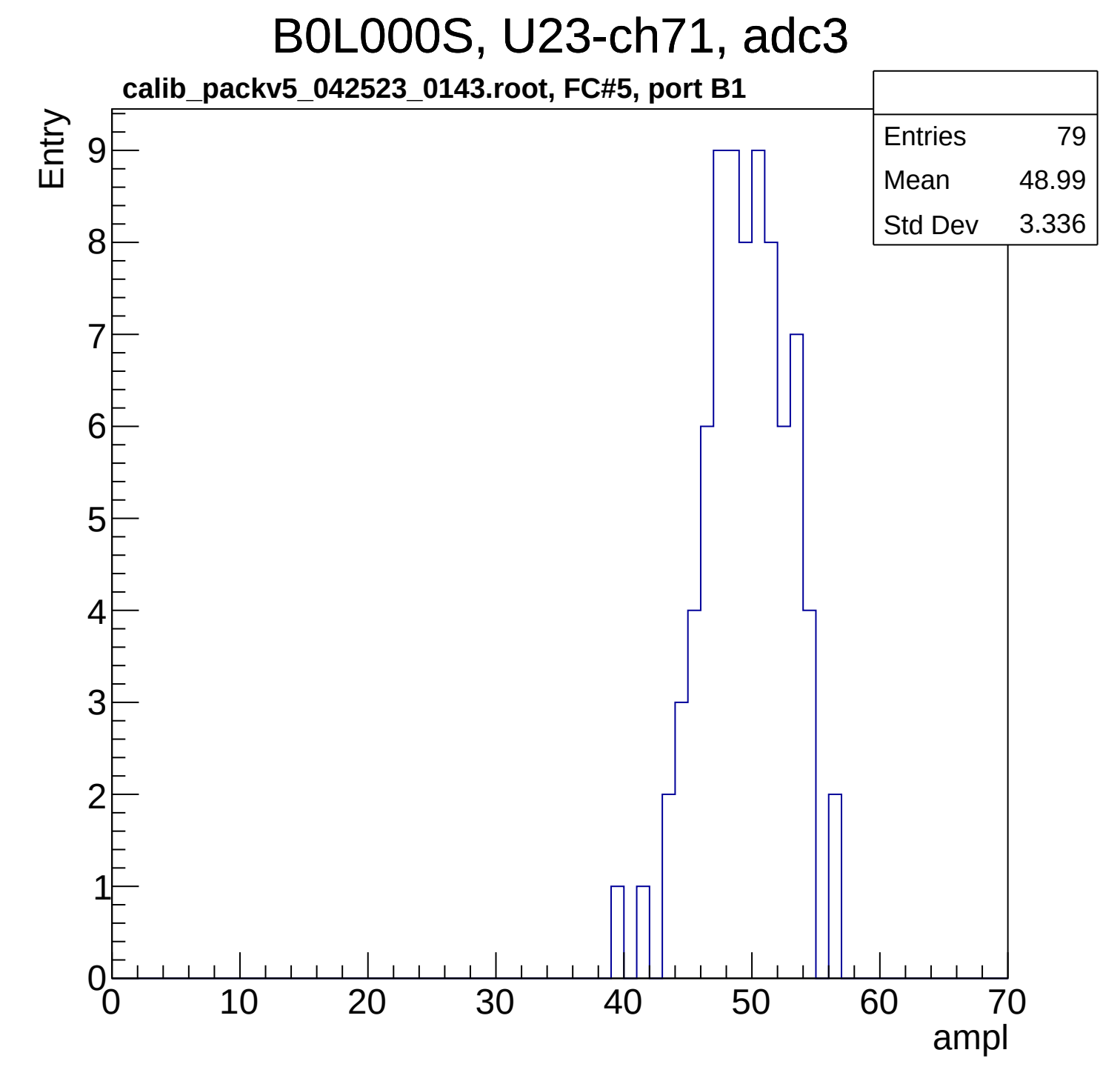

B0L000S, U23-ch70, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 61.63
Std Dev 1.385
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

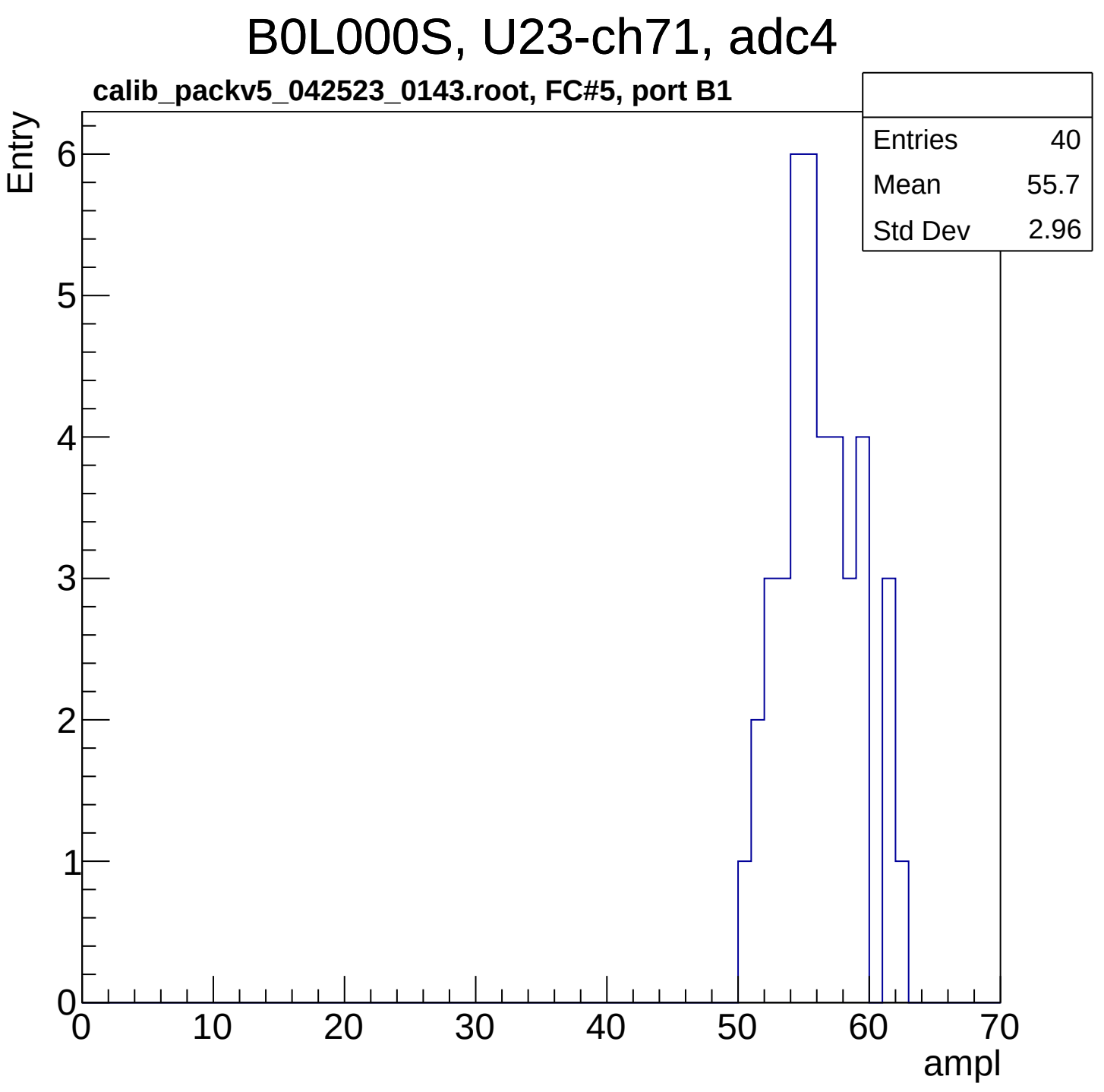

B0L000S, U23-ch70, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

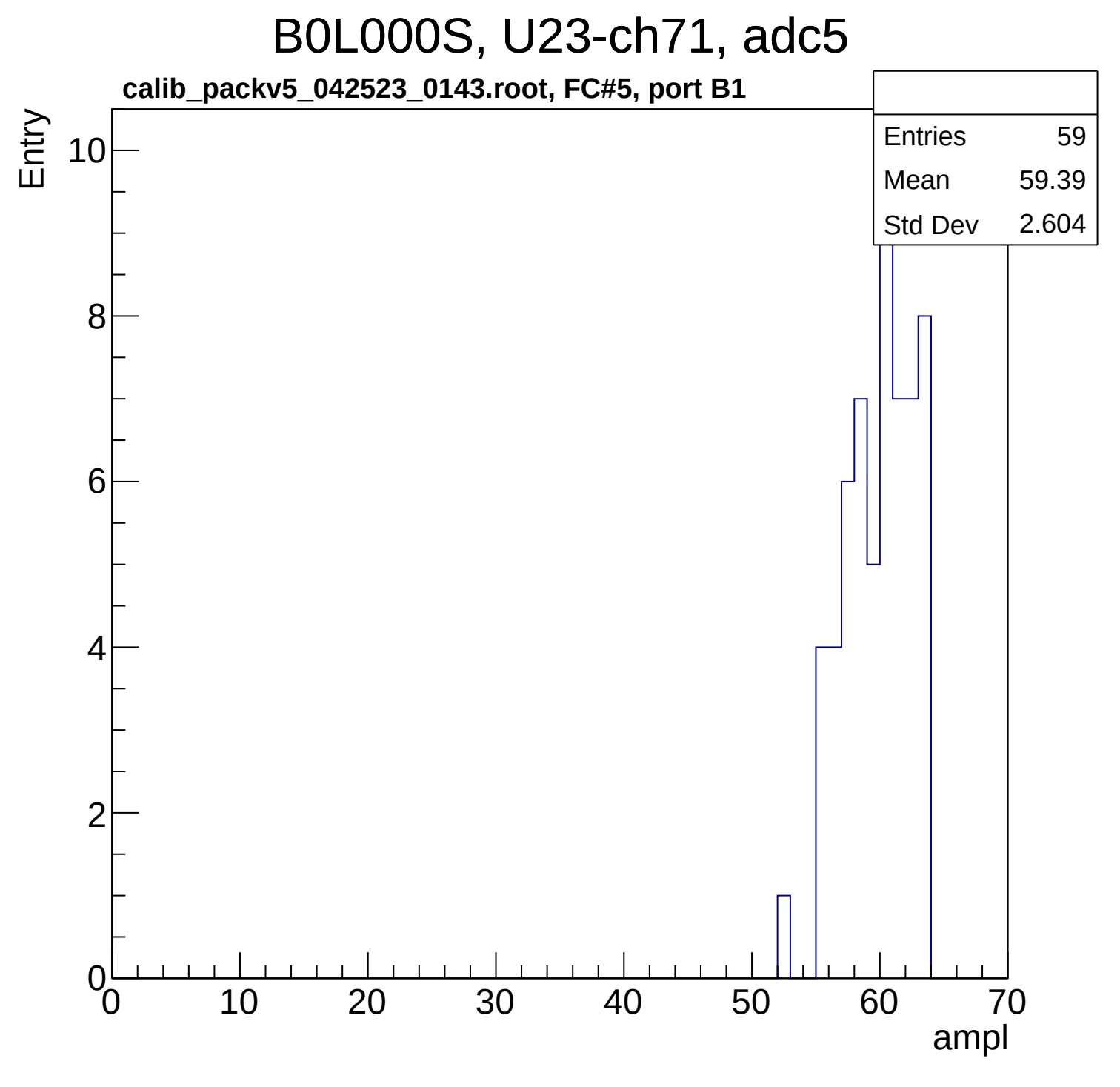

B0L000S, U23-ch71, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 27.62
Std Dev 6.785
Gaus mean : 29.3869
Gaus Width: 4.0486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

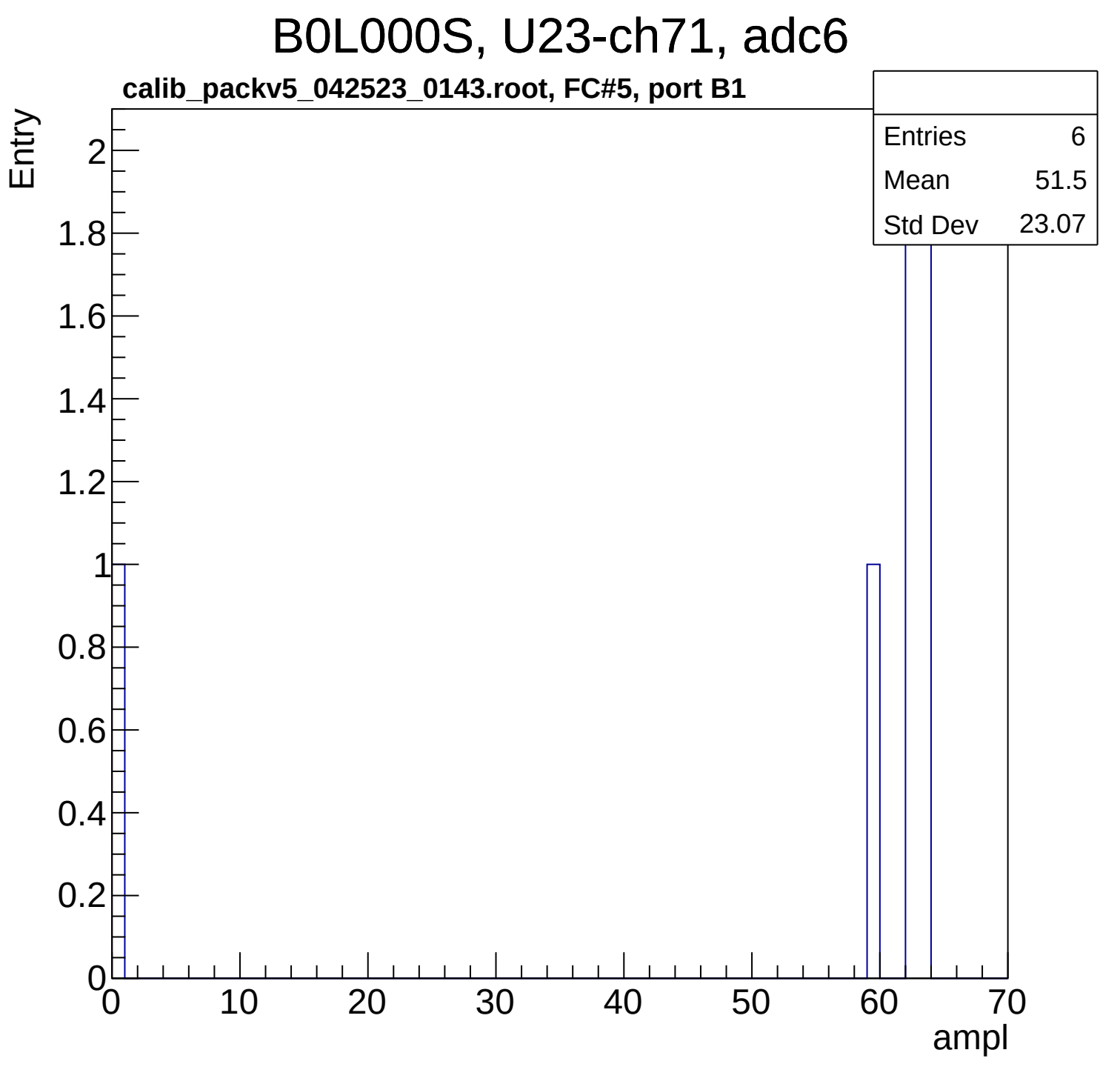

B0L000S, U23-ch71, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.68
Std Dev 3.004
Gaus mean : 35.9830
Gaus Width: 3.1717
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

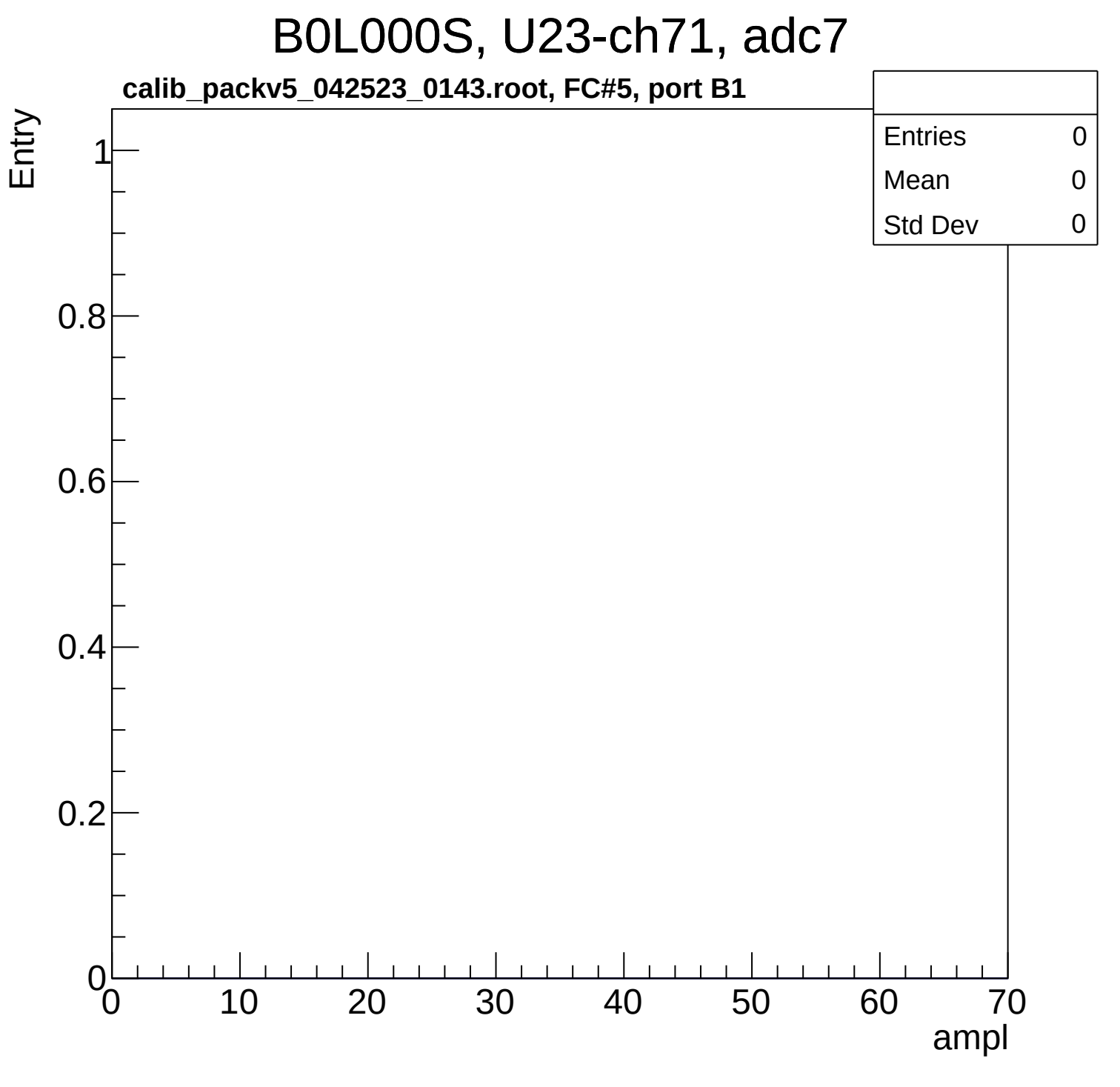

B0L000S, U23-ch71, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 41.76
Std Dev 3.223
Gaus mean : 42.3890
Gaus Width: 2.9360
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

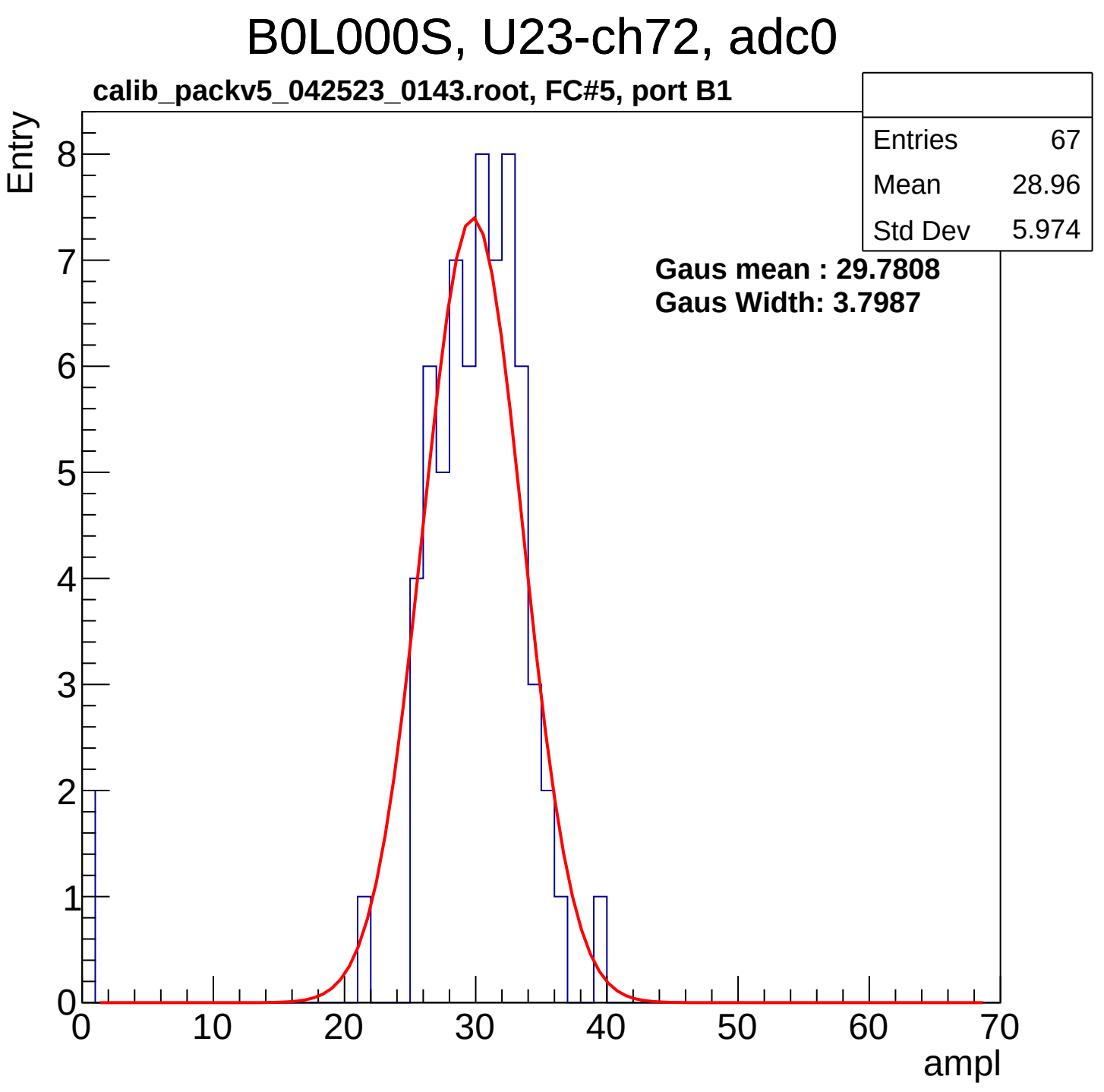

B0L000S, U23-ch71, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 48.99
Std Dev 3.336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

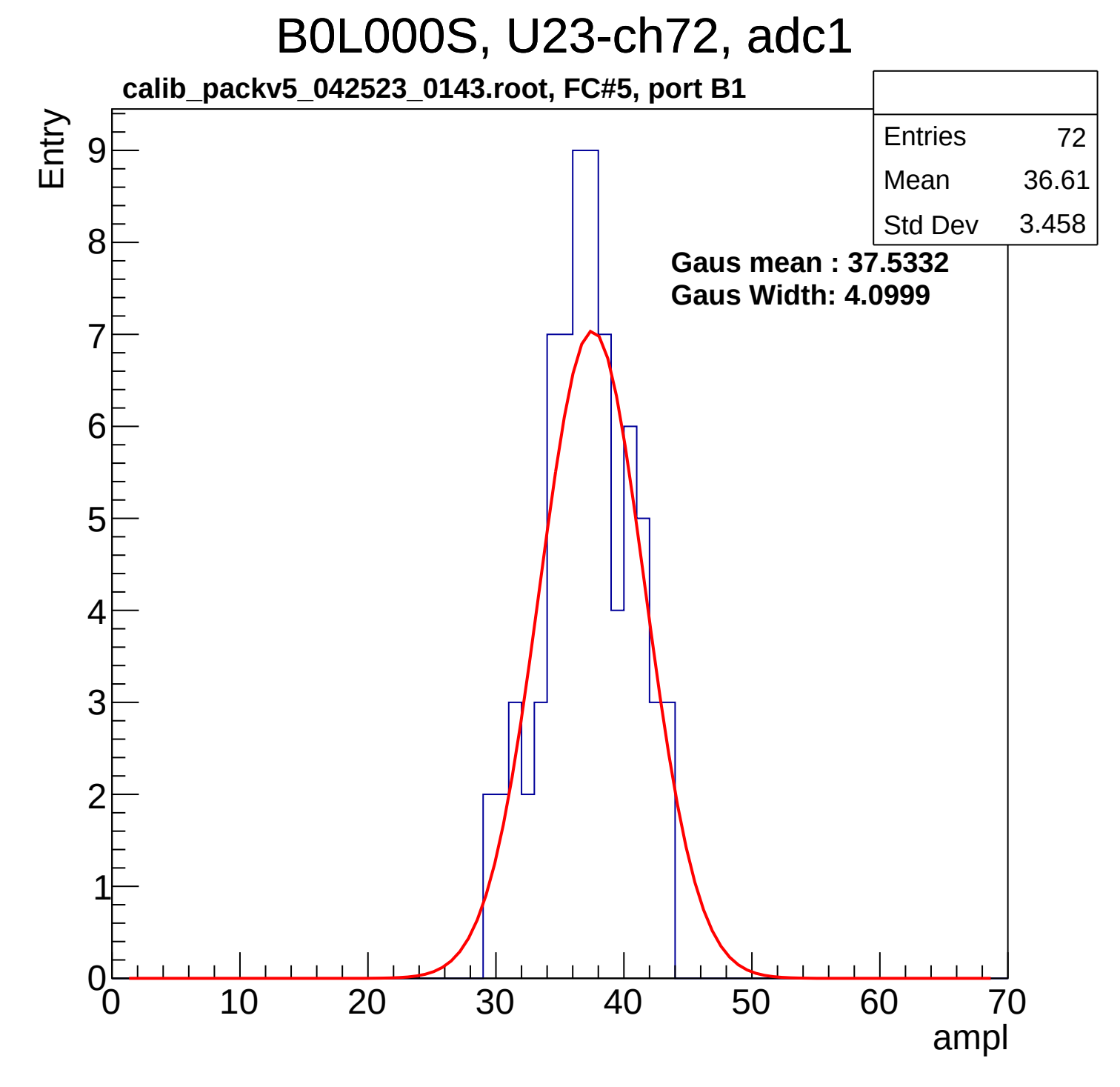

B0L000S, U23-ch71, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 55.7
Std Dev 2.96
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

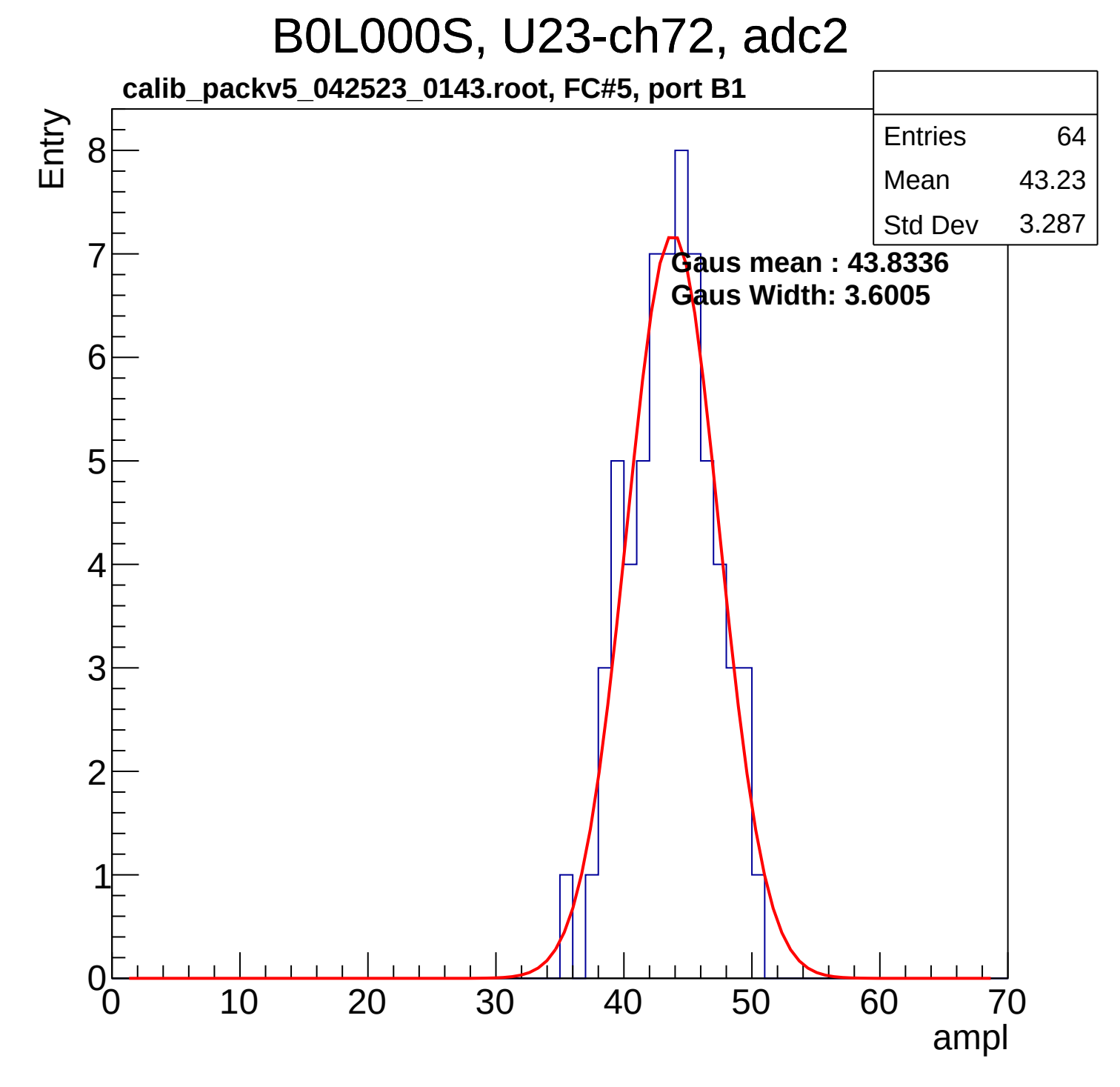

B0L000S, U23-ch71, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 59.39
Std Dev 2.604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

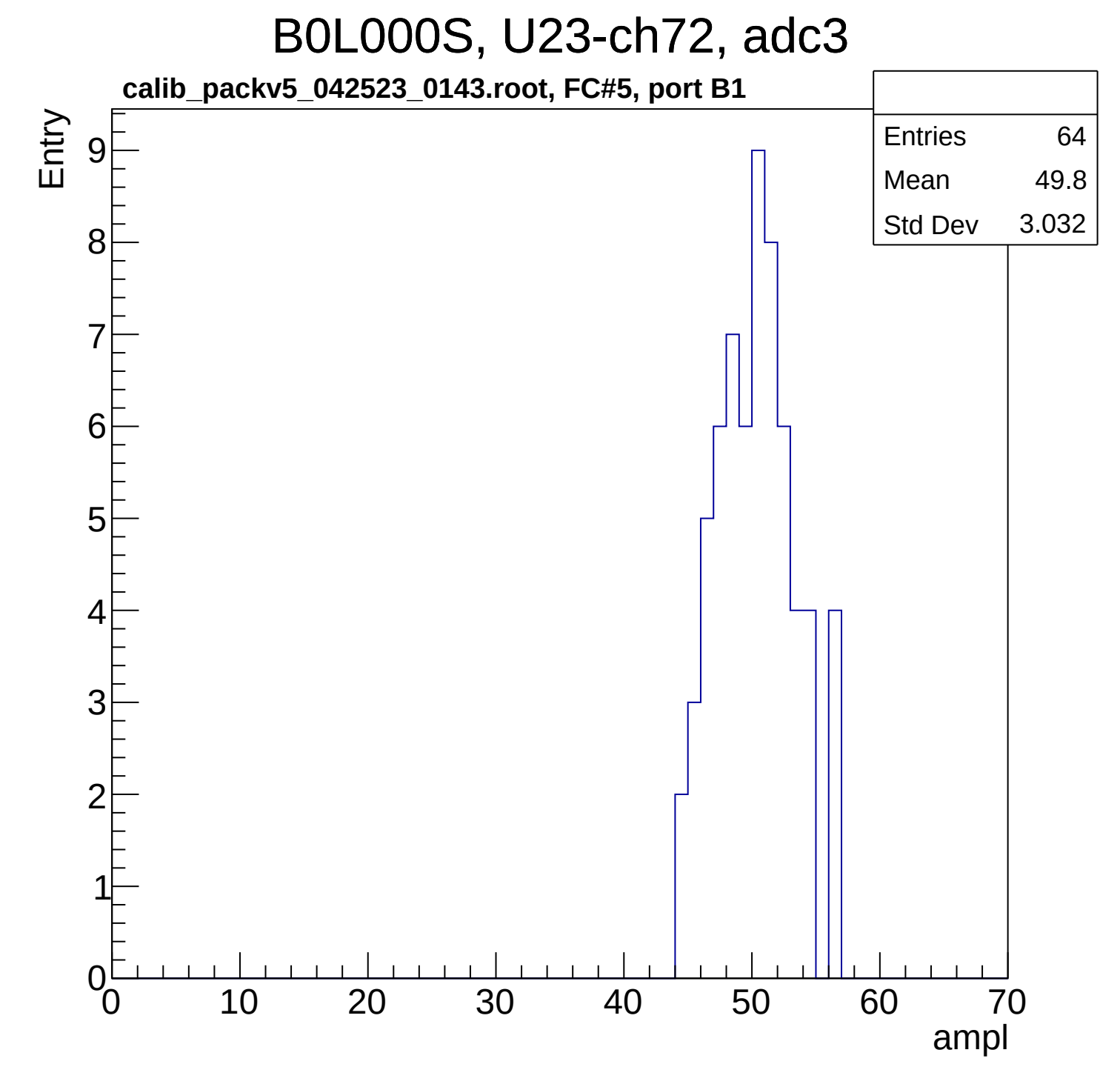

B0L000S, U23-ch71, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 51.5
Std Dev 23.07
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

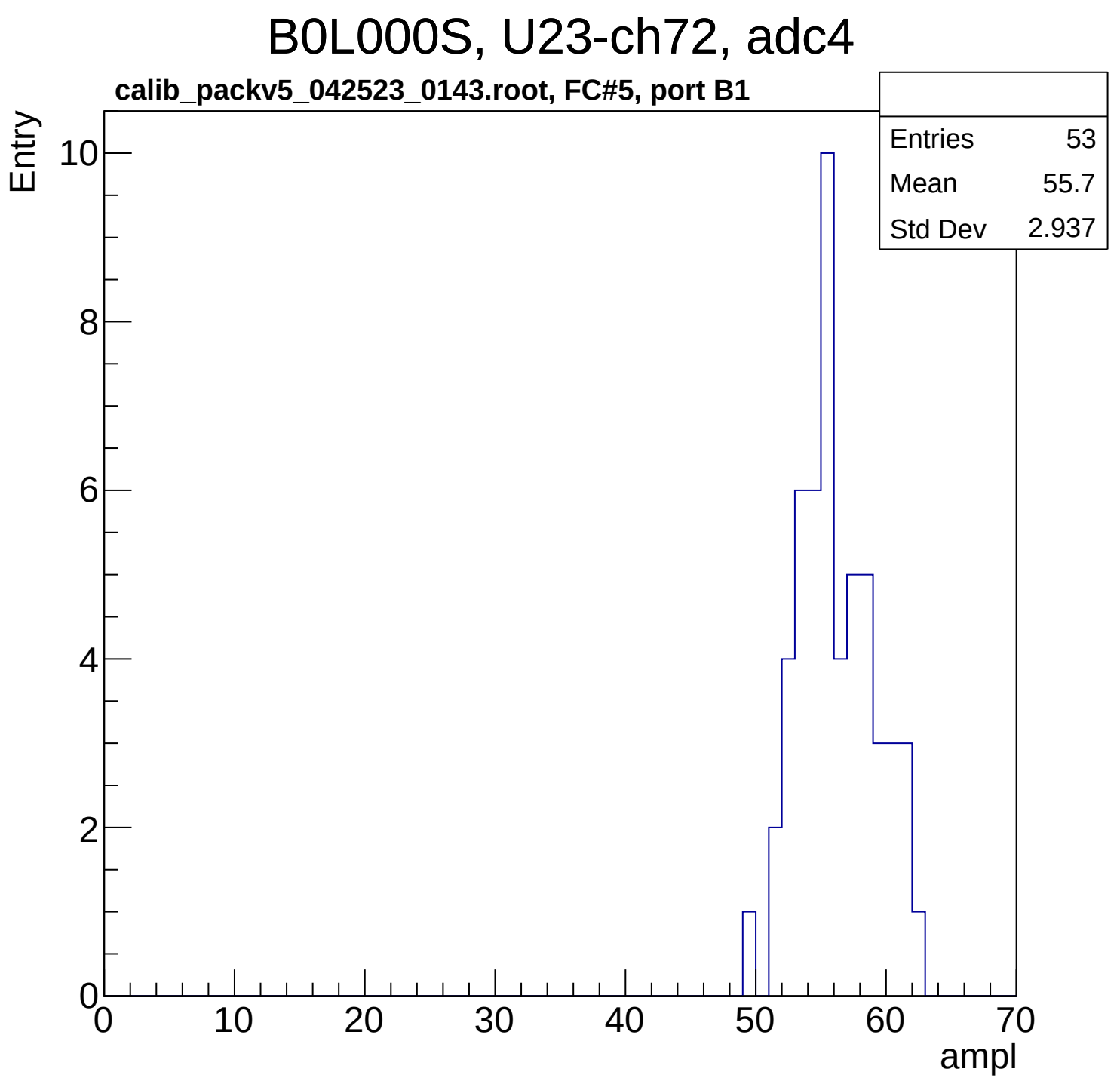

B0L000S, U23-ch71, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

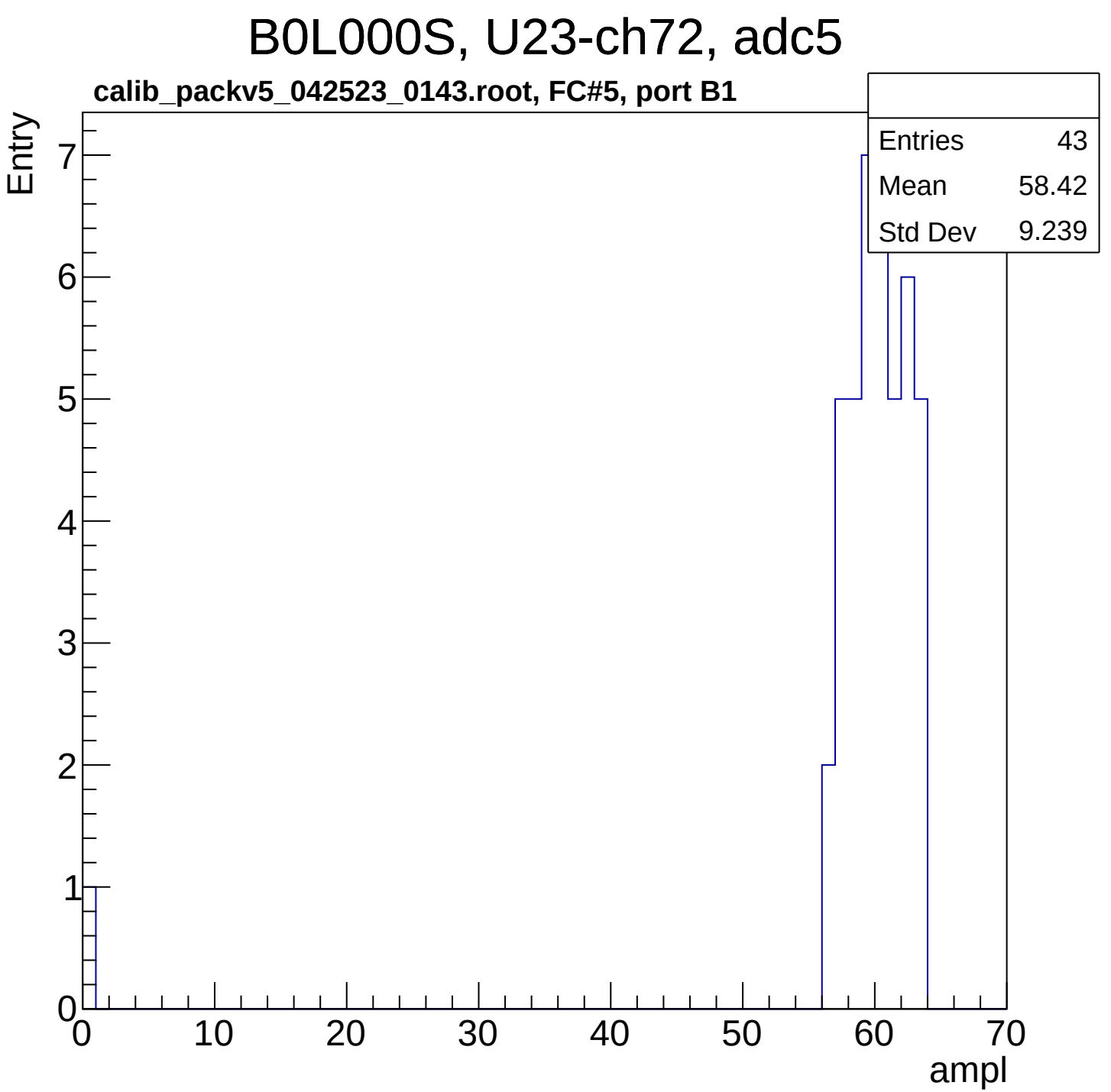

B0L000S, U23-ch72, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 28.96
Std Dev 5.974
Gaus mean : 29.7808
Gaus Width: 3.7987
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

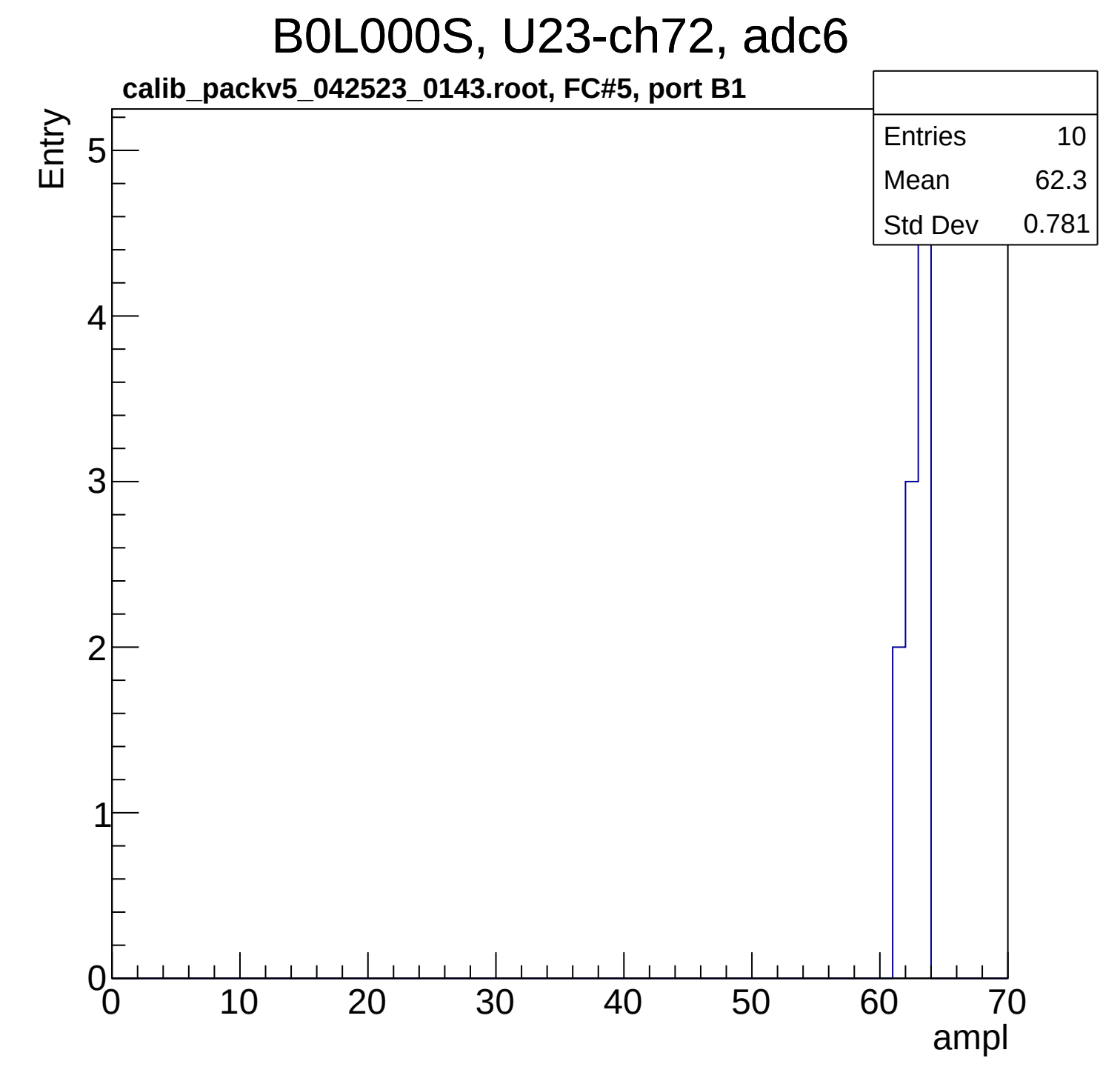

B0L000S, U23-ch72, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.61
Std Dev 3.458
Gaus mean : 37.5332
Gaus Width: 4.0999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

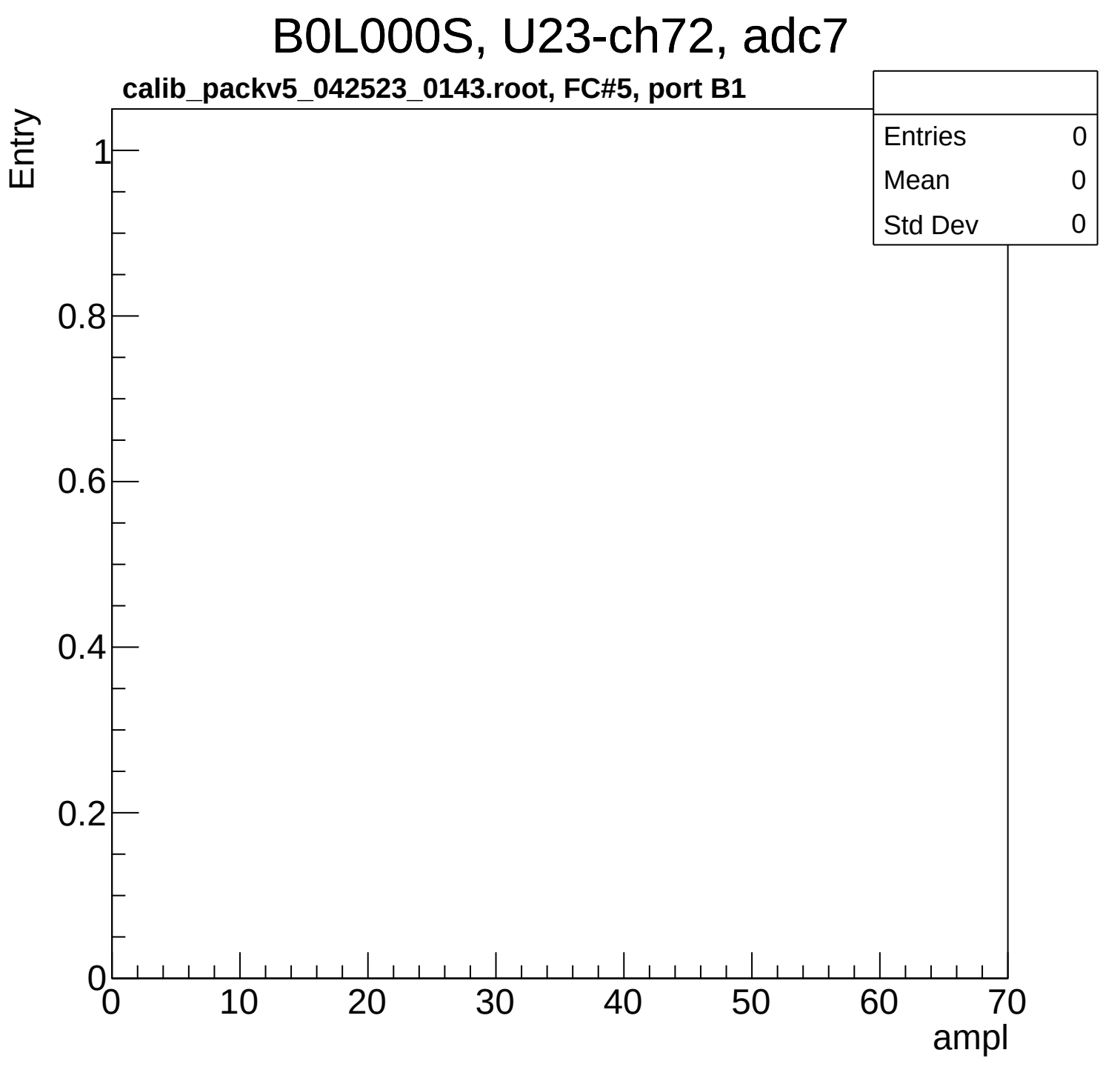

B0L000S, U23-ch72, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.23
Std Dev 3.287
Gaus mean : 43.8336
Gaus Width: 3.6005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

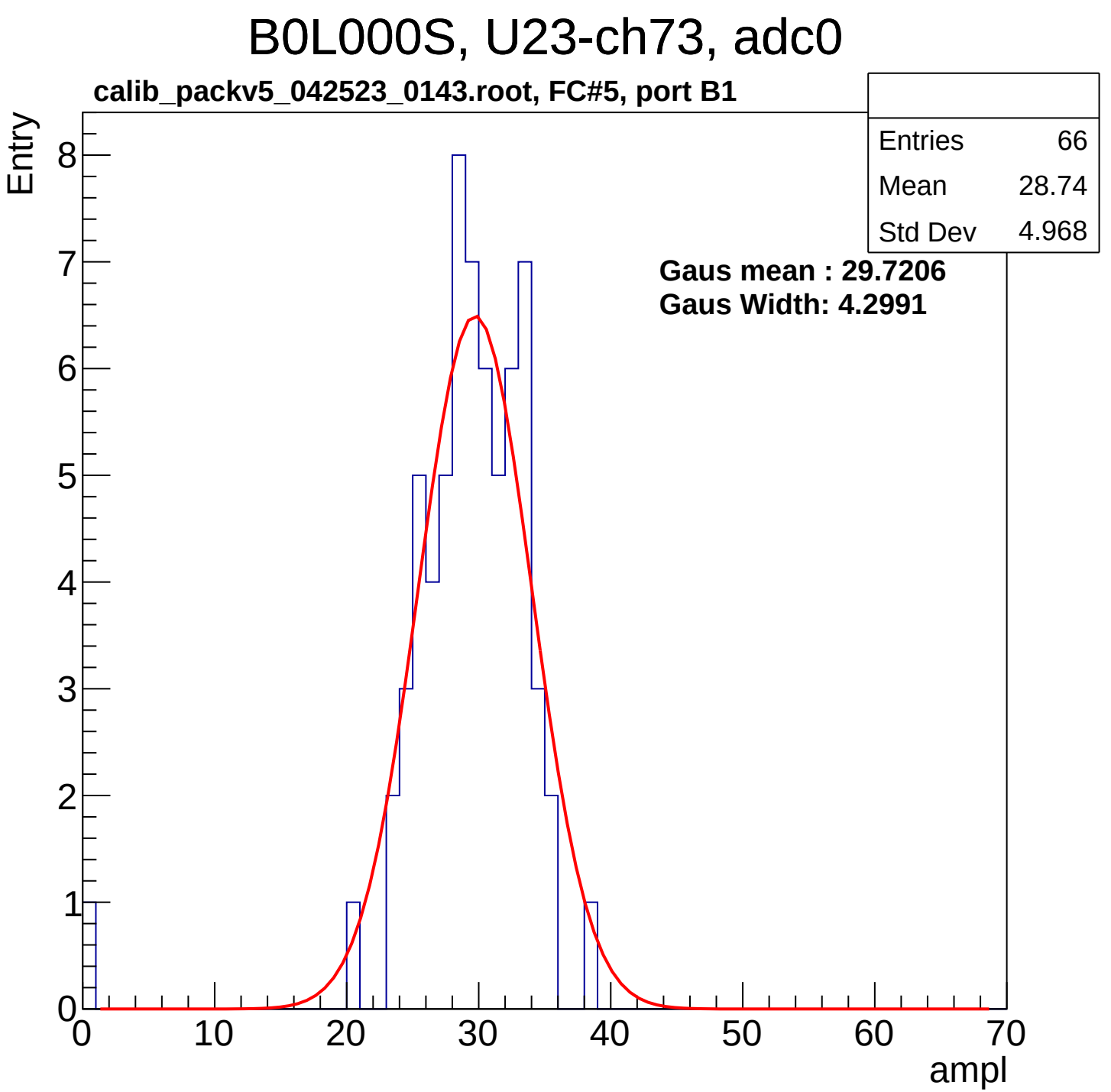

B0L000S, U23-ch72, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.8
Std Dev 3.032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

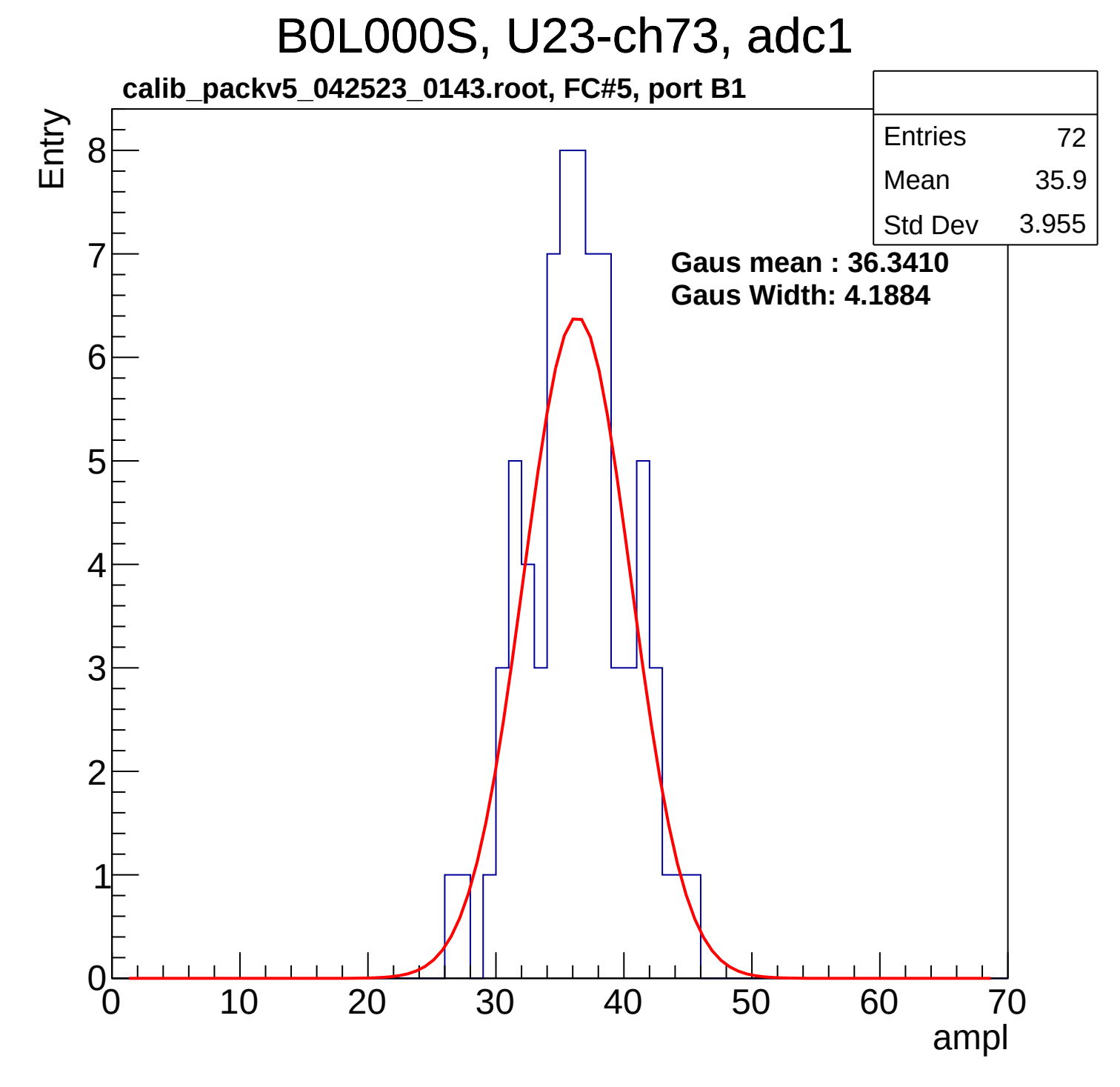

B0L000S, U23-ch72, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.7
Std Dev 2.937
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

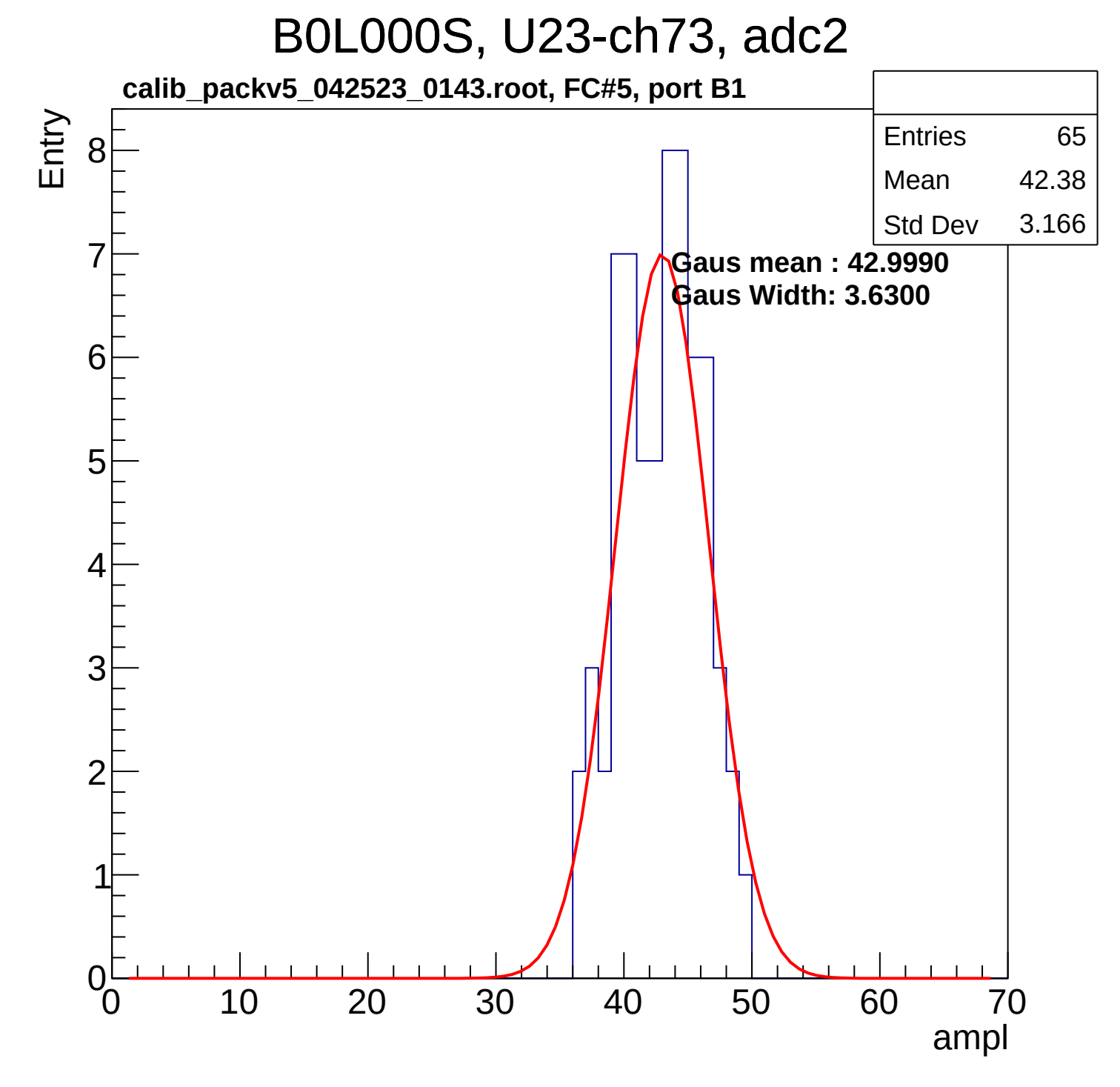

B0L000S, U23-ch72, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.42
Std Dev 9.239
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

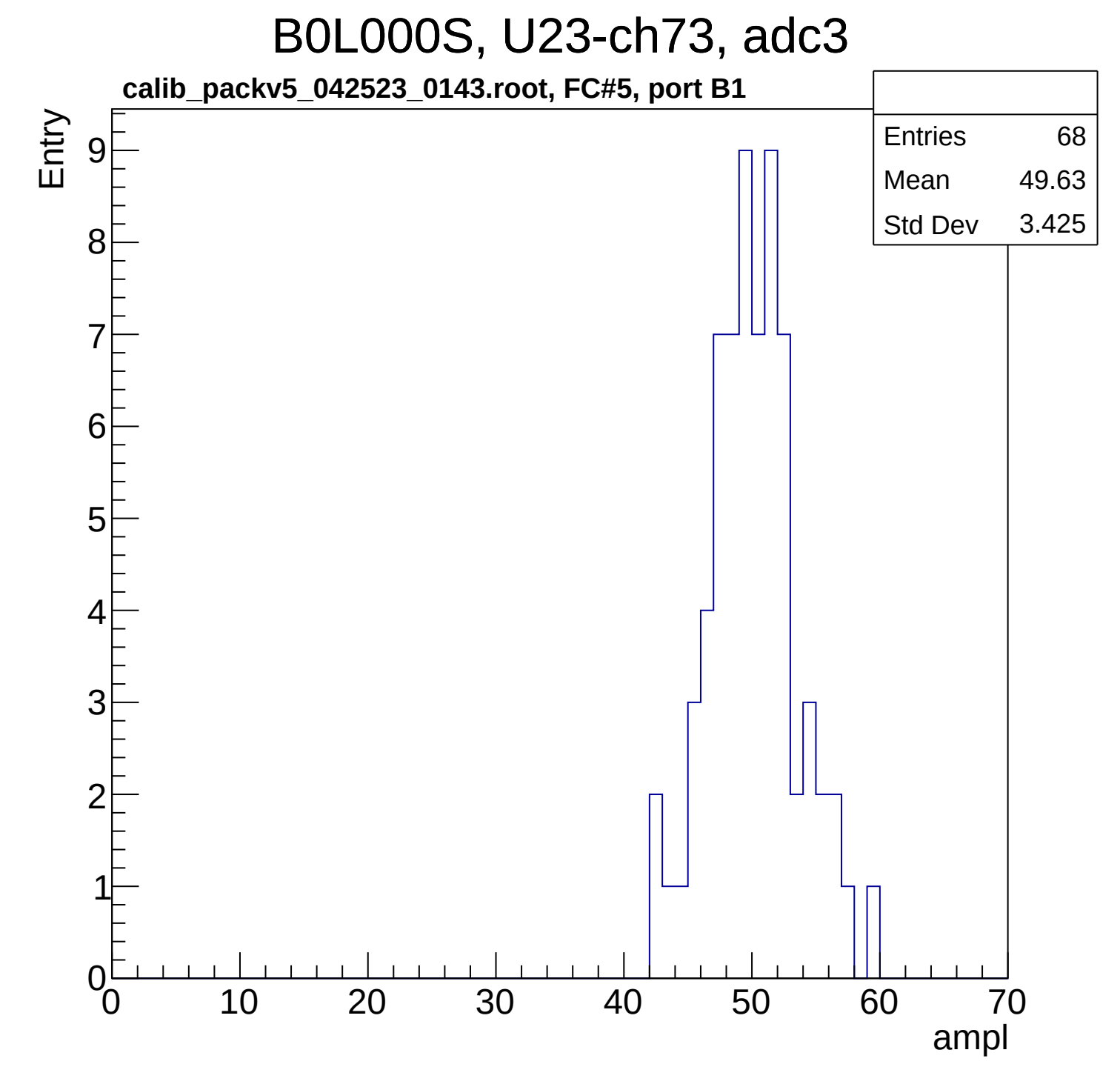

B0L000S, U23-ch72, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 62.3
Std Dev 0.781
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

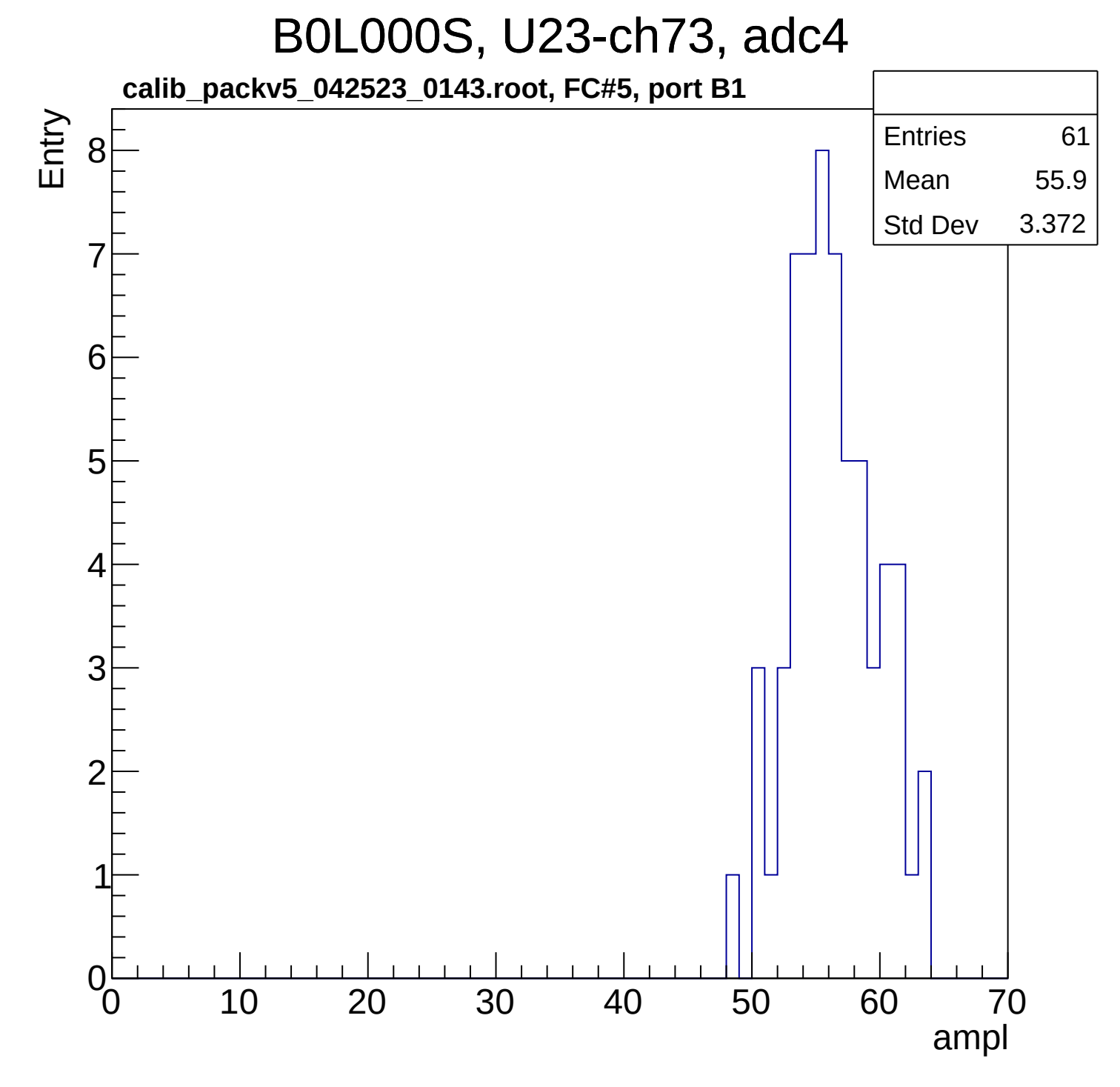

B0L000S, U23-ch72, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

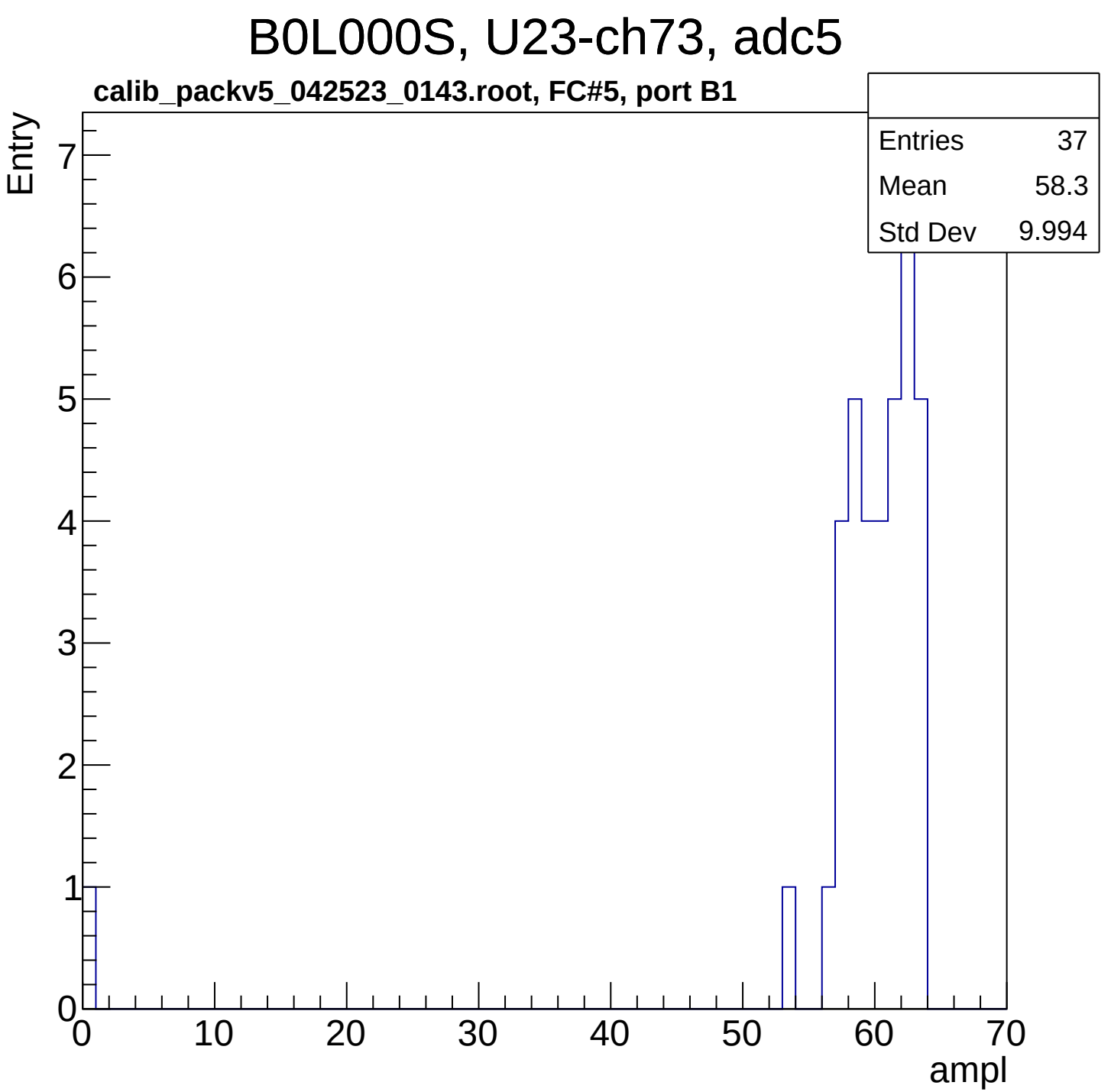

B0L000S, U23-ch73, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.74
Std Dev 4.968
Gaus mean : 29.7206
Gaus Width: 4.2991
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

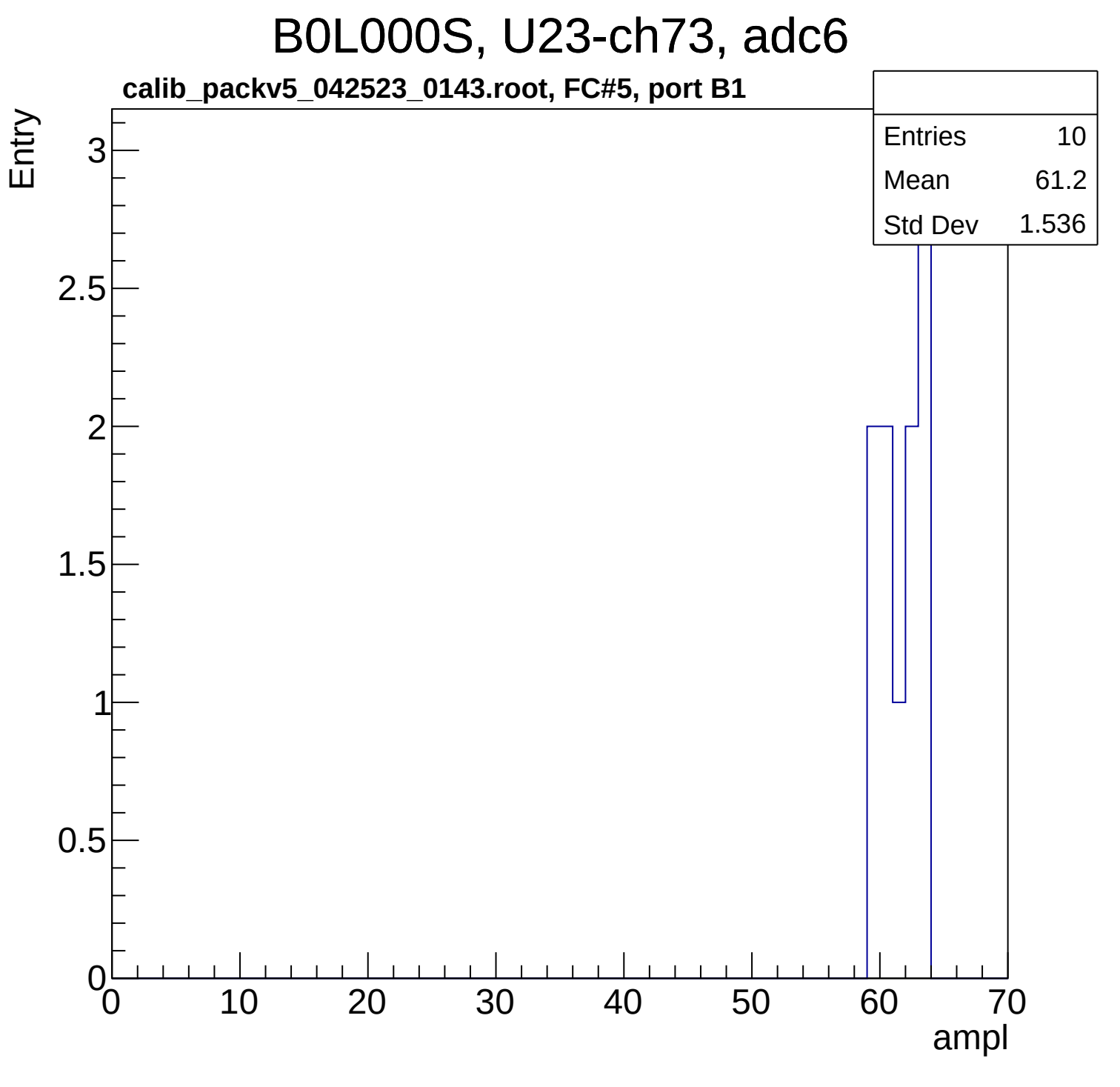

B0L000S, U23-ch73, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.9
Std Dev 3.955
Gaus mean : 36.3410
Gaus Width: 4.1884
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

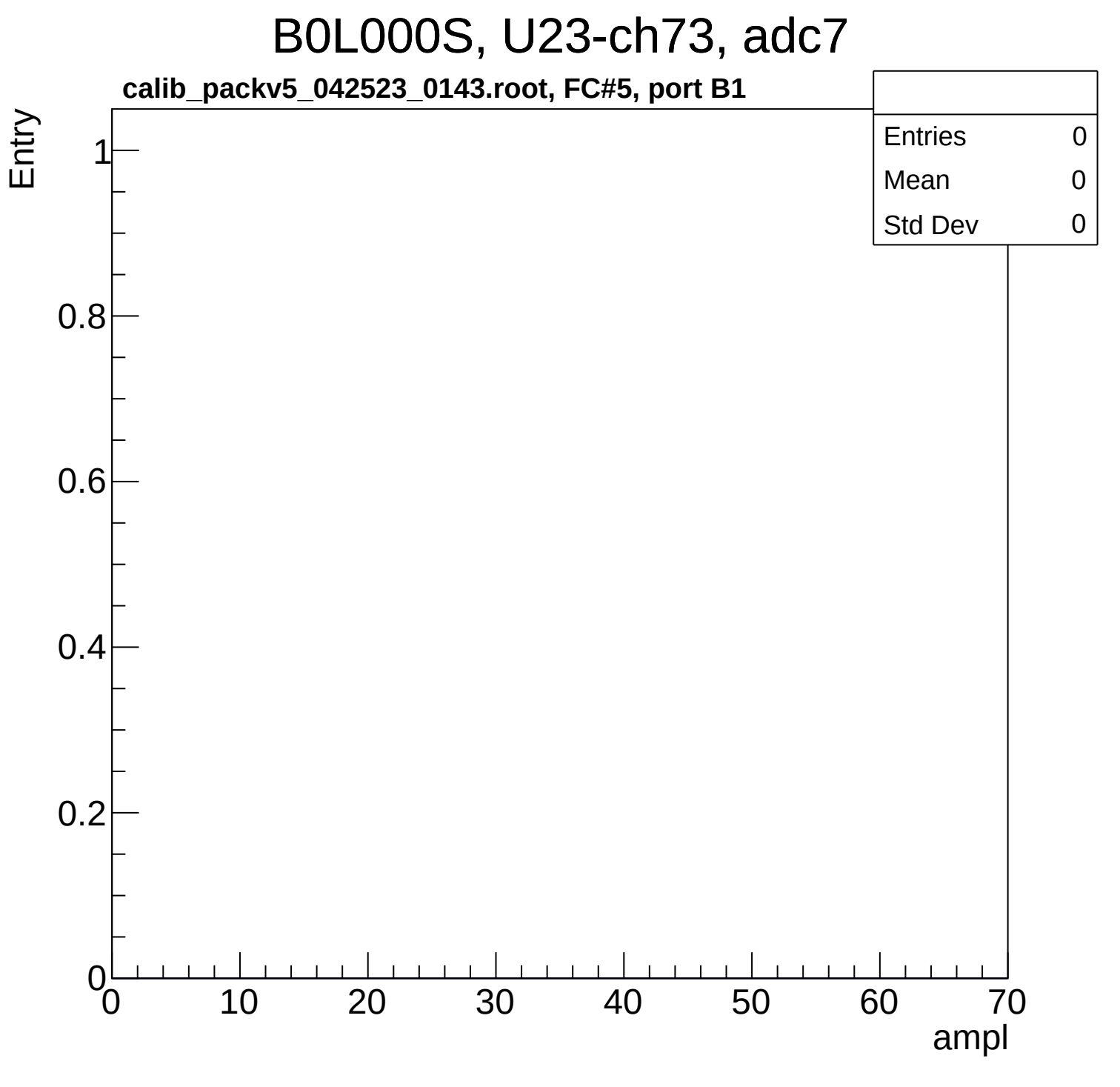

B0L000S, U23-ch73, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.38
Std Dev 3.166
Gaus mean : 42.9990
Gaus Width: 3.6300
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

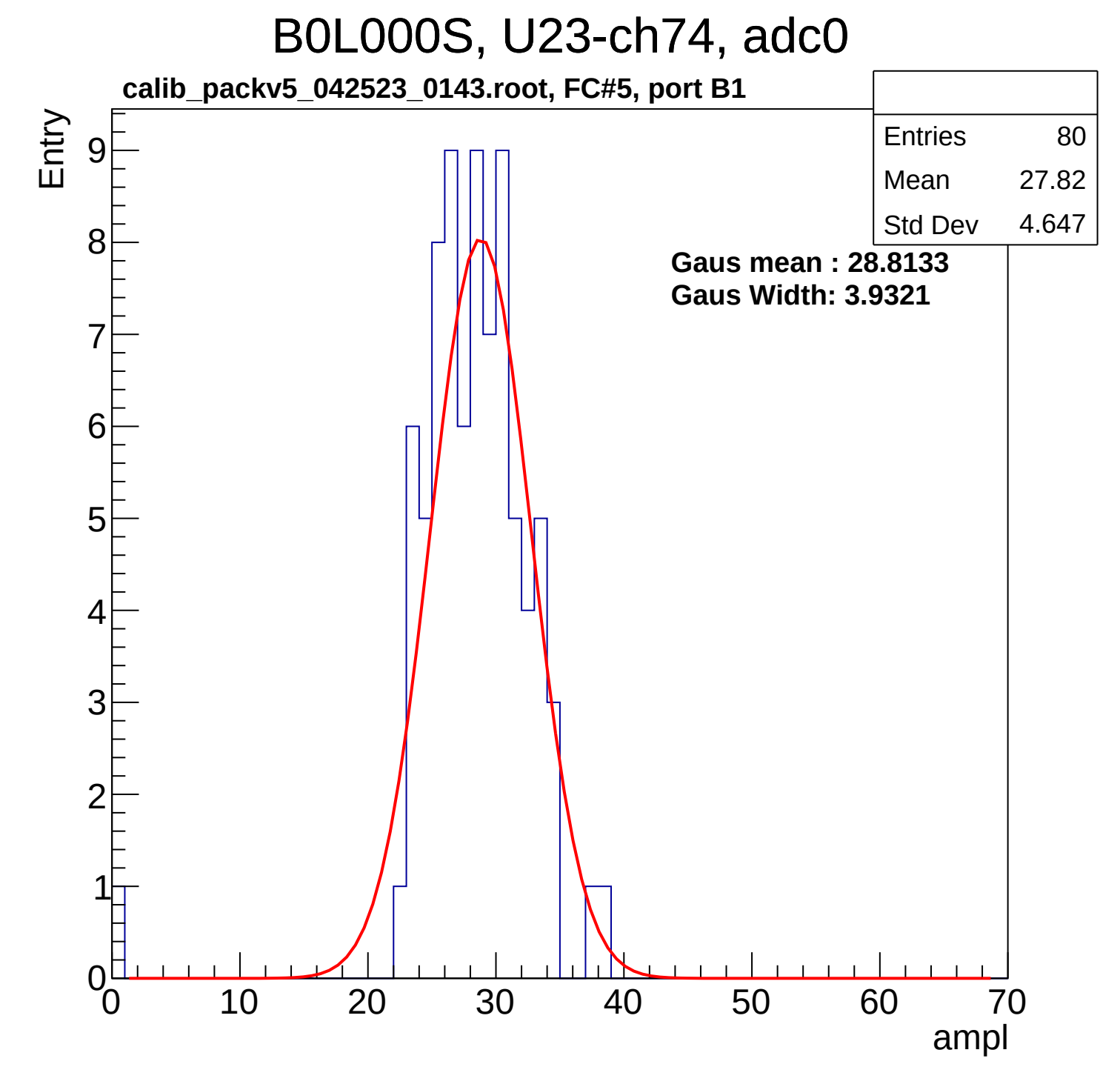

B0L000S, U23-ch73, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 49.63
Std Dev 3.425
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

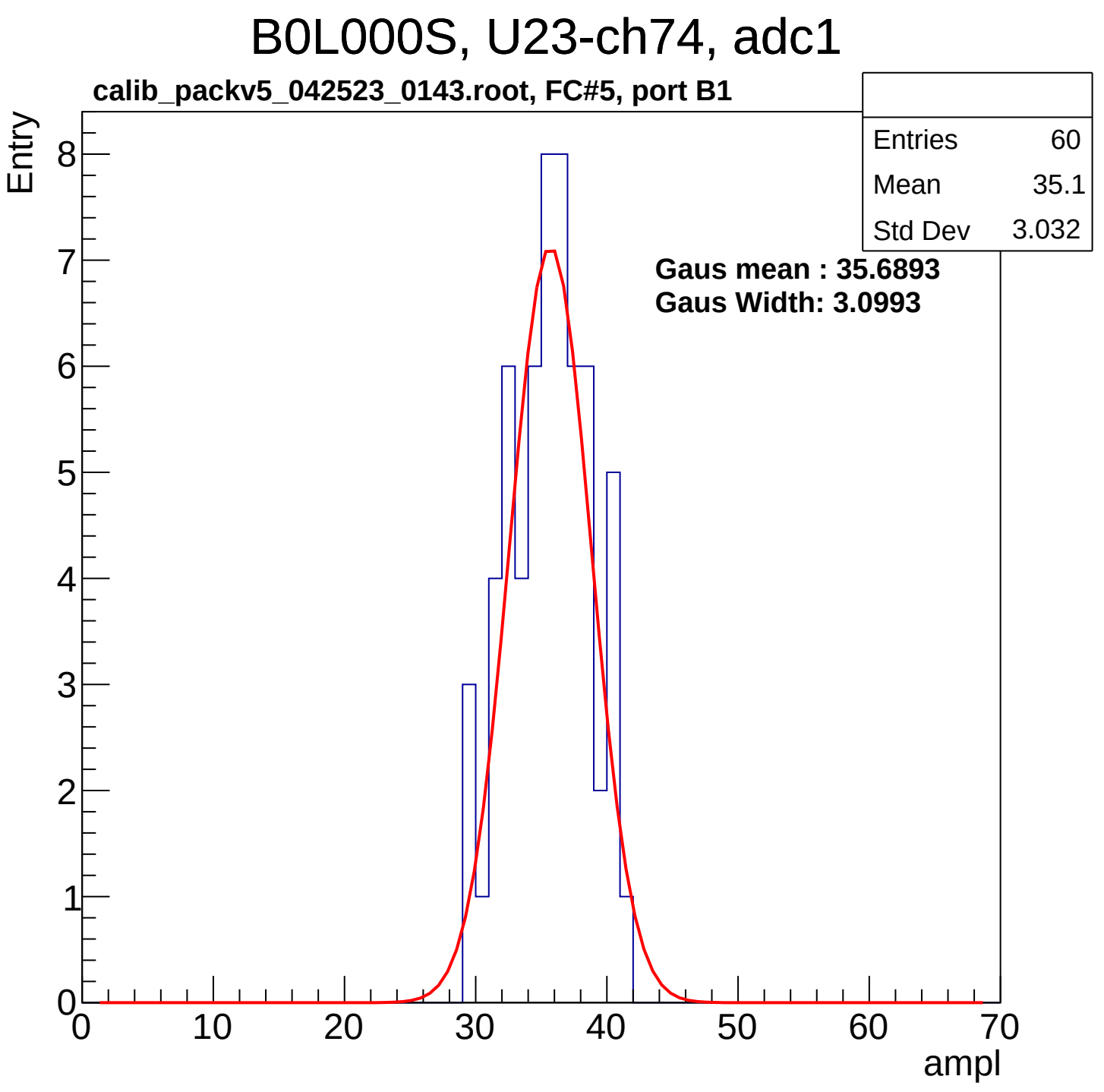

B0L000S, U23-ch73, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.9
Std Dev 3.372
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

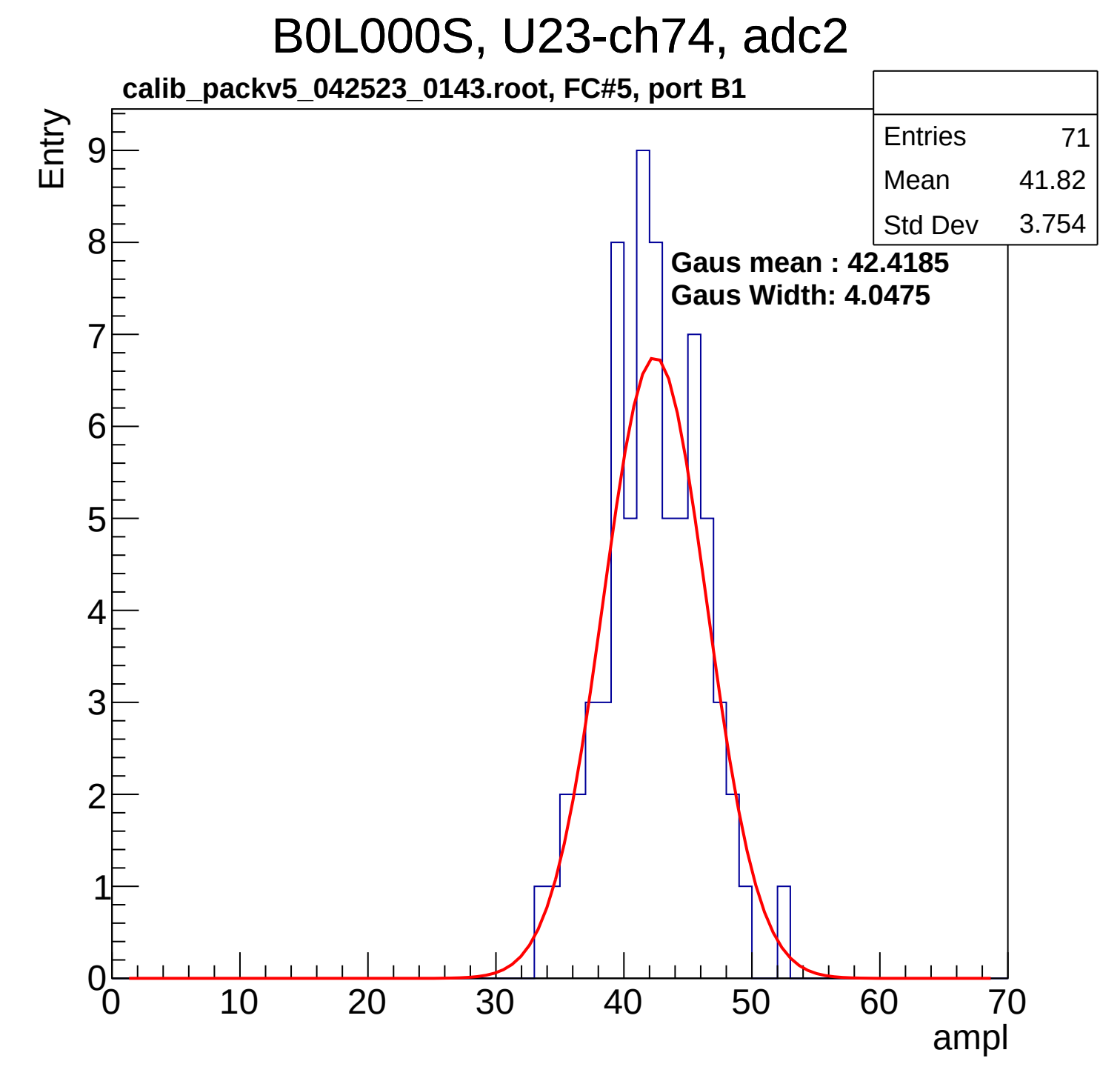

B0L000S, U23-ch73, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.3
Std Dev 9.994
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

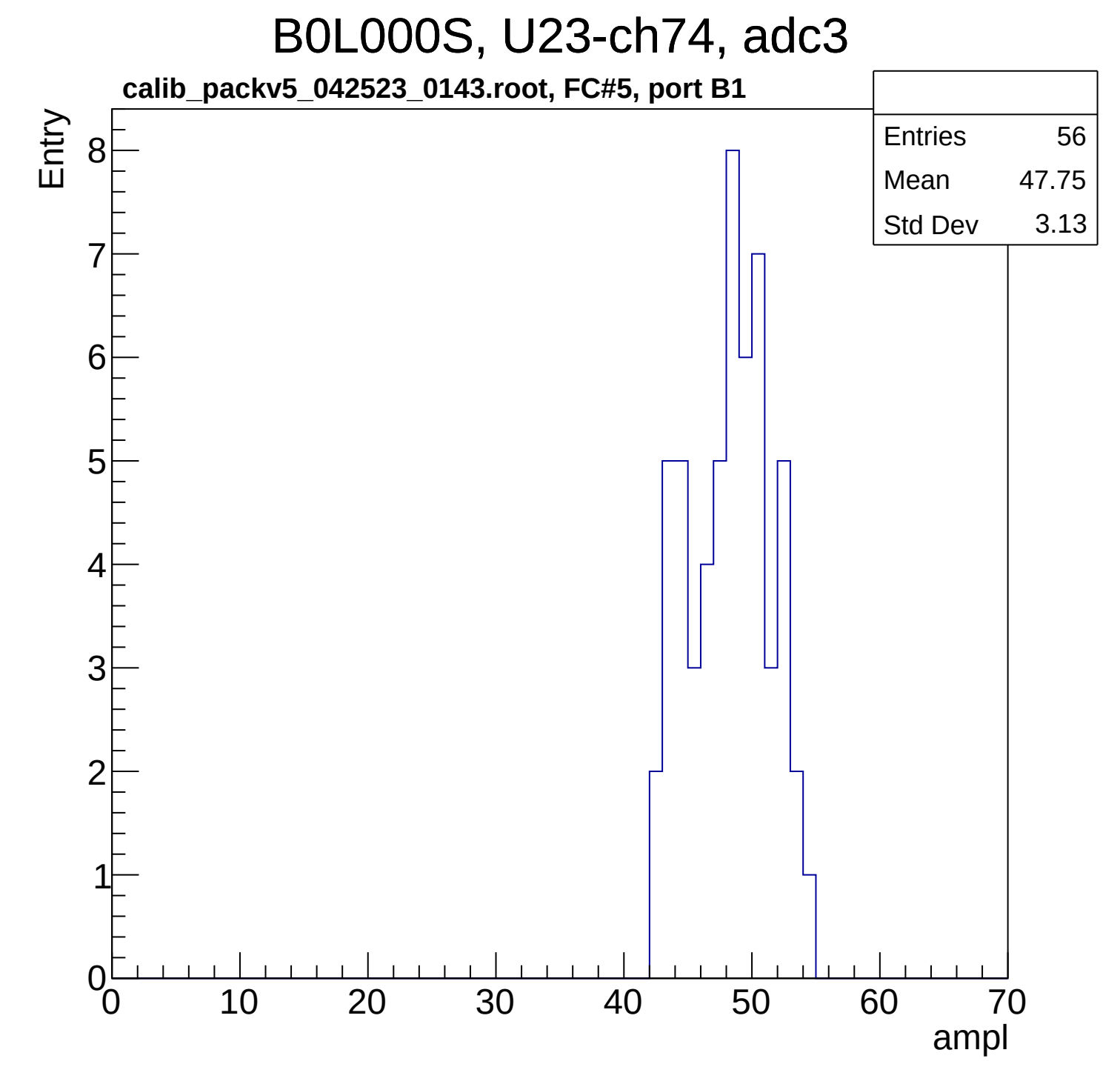

B0L000S, U23-ch73, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.2
Std Dev 1.536
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

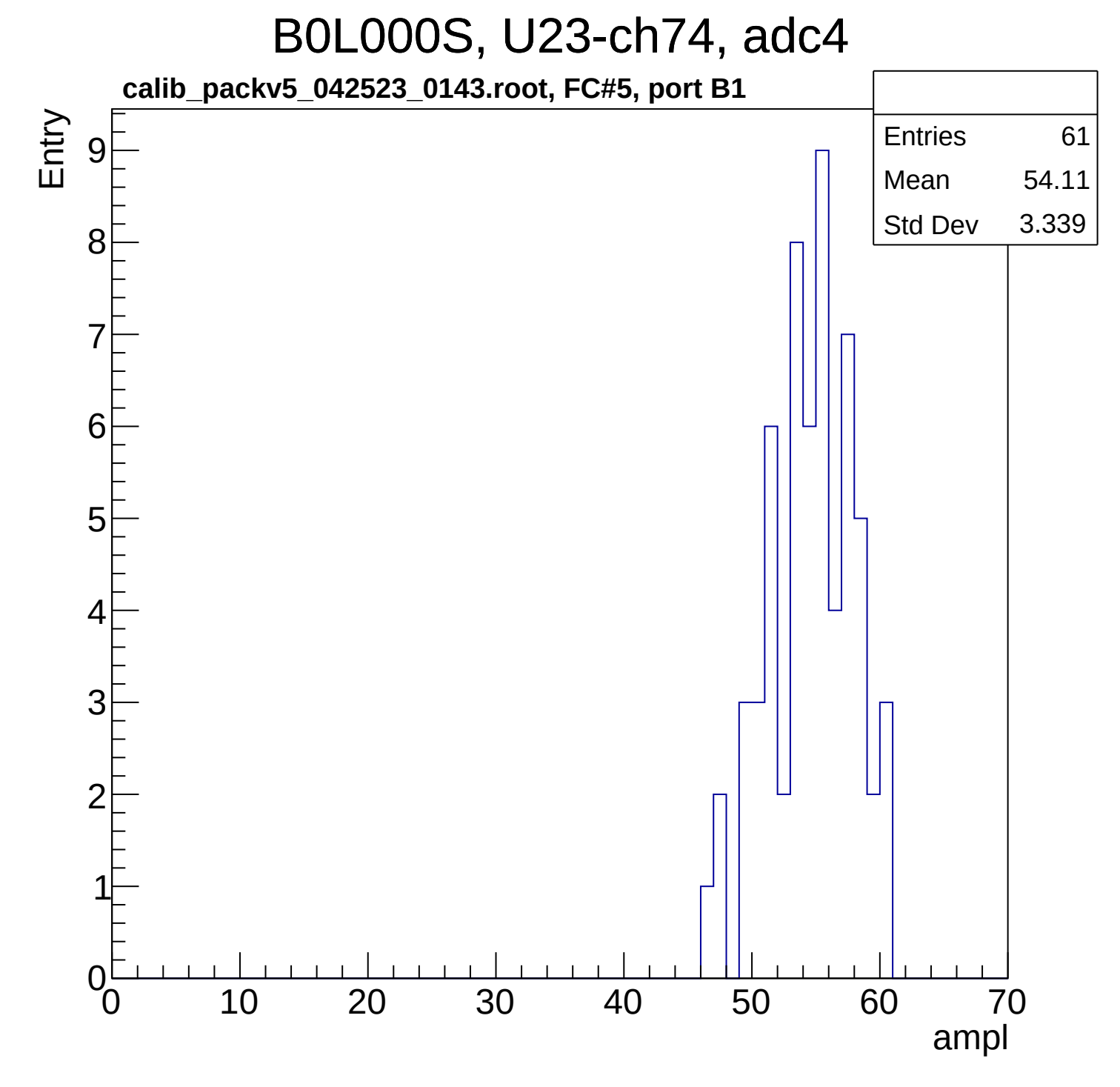

B0L000S, U23-ch73, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

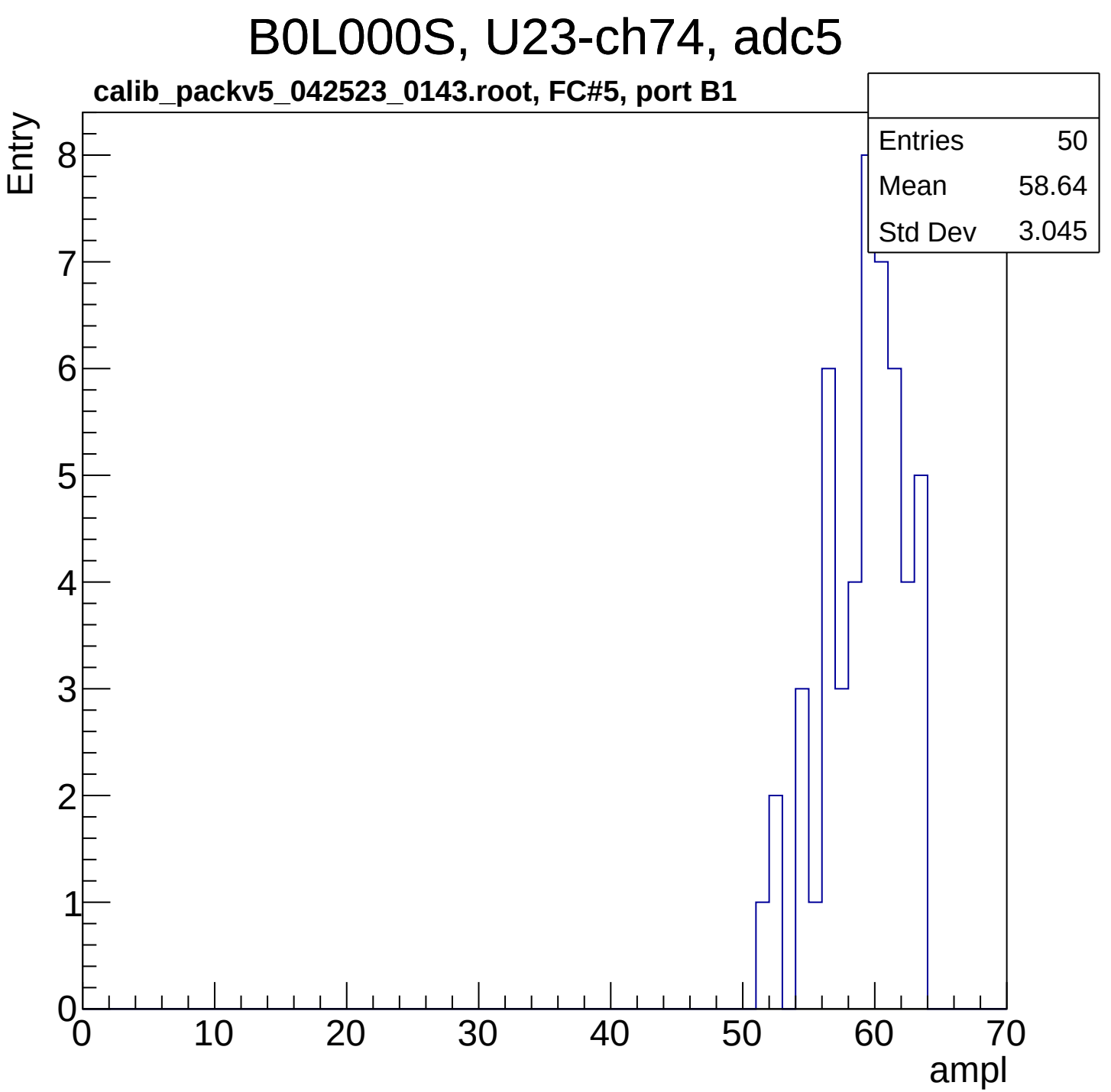

B0L000S, U23-ch74, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 27.82
Std Dev 4.647
Gaus mean : 28.8133
Gaus Width: 3.9321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

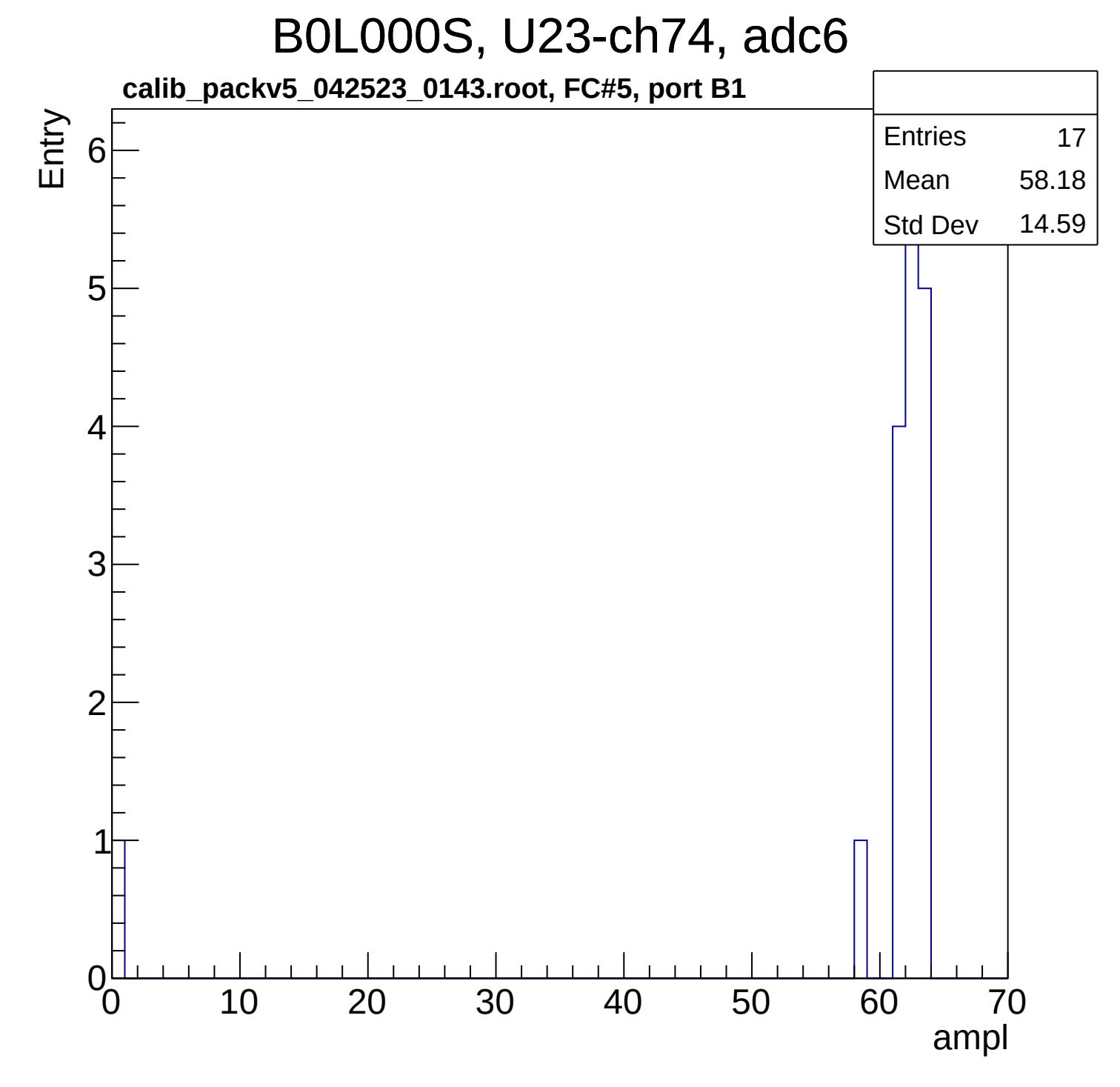

B0L000S, U23-ch74, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 35.1
Std Dev 3.032
Gaus mean : 35.6893
Gaus Width: 3.0993
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

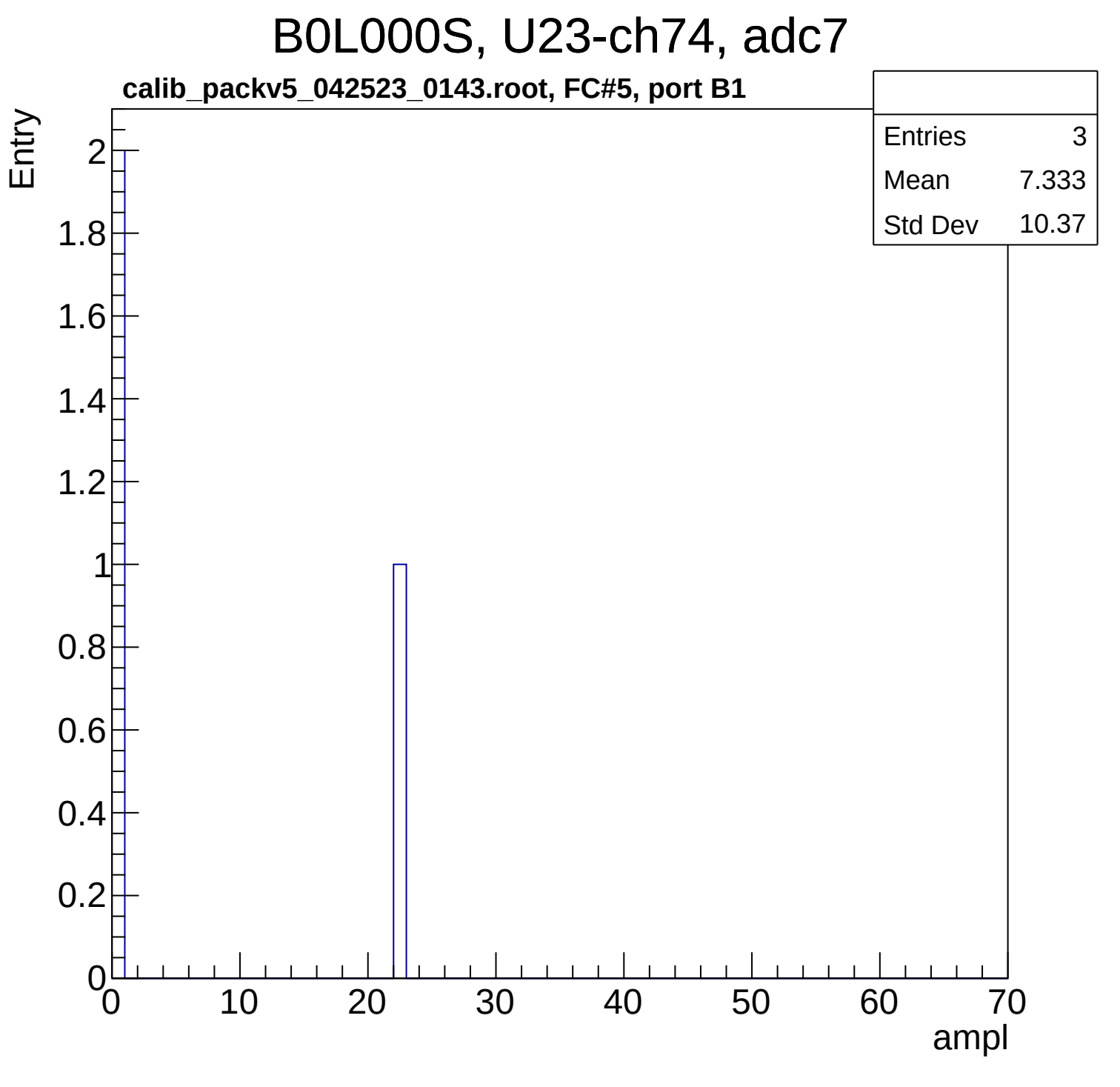

B0L000S, U23-ch74, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 41.82
Std Dev 3.754
Gaus mean : 42.4185
Gaus Width: 4.0475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

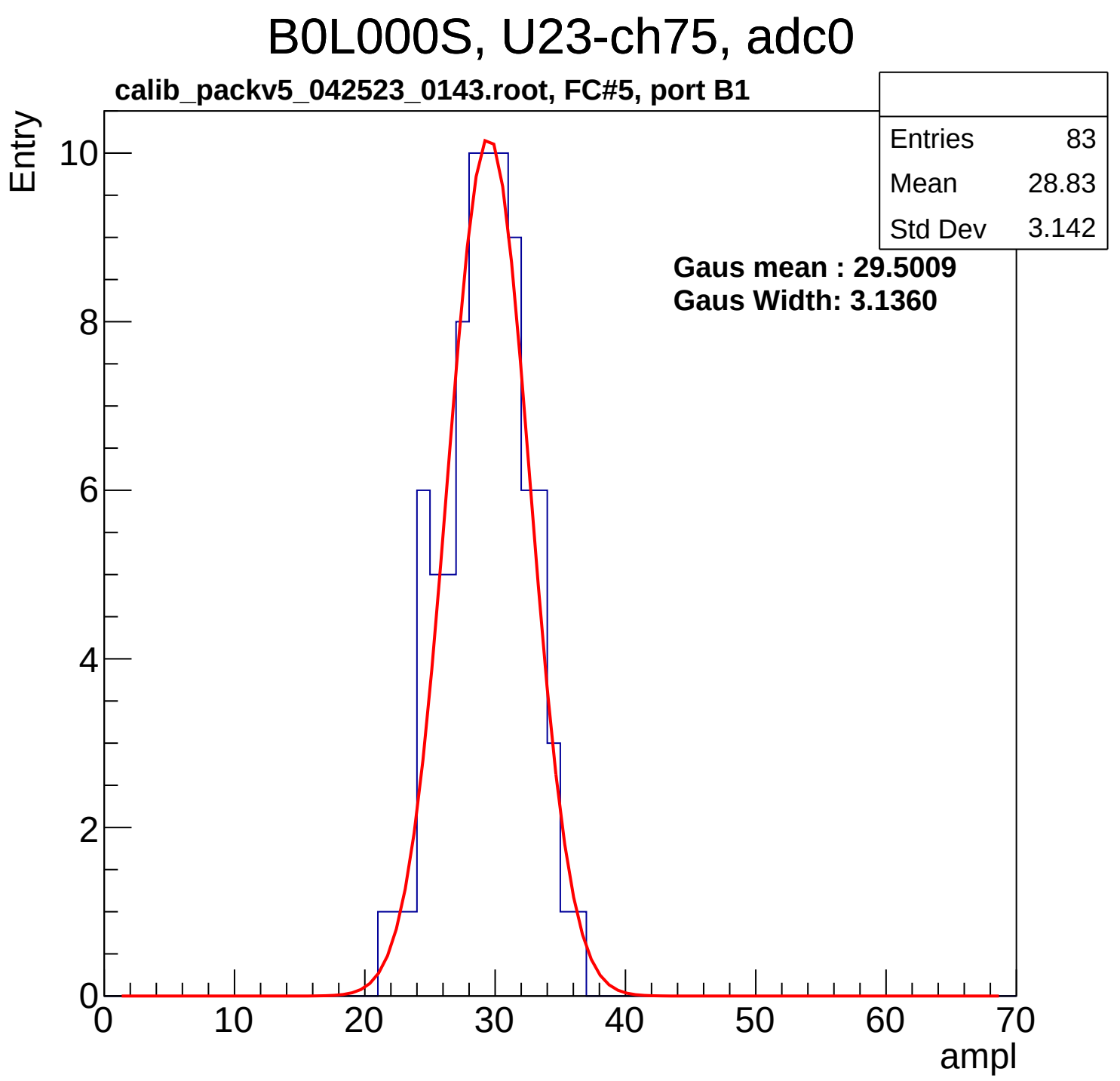

B0L000S, U23-ch74, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 47.75
Std Dev 3.13
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

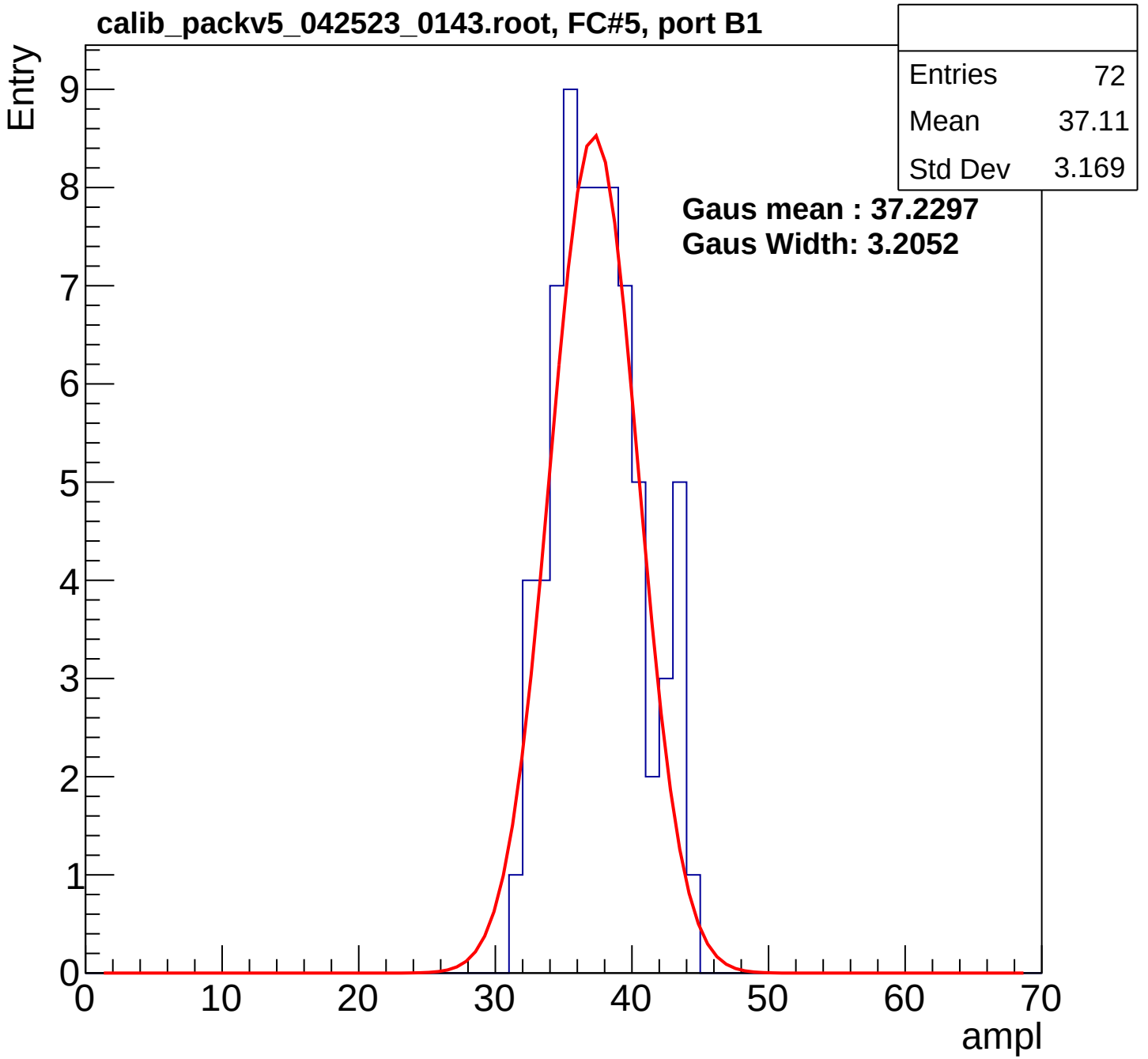

B0L000S, U23-ch74, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 54.11
Std Dev 3.339
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

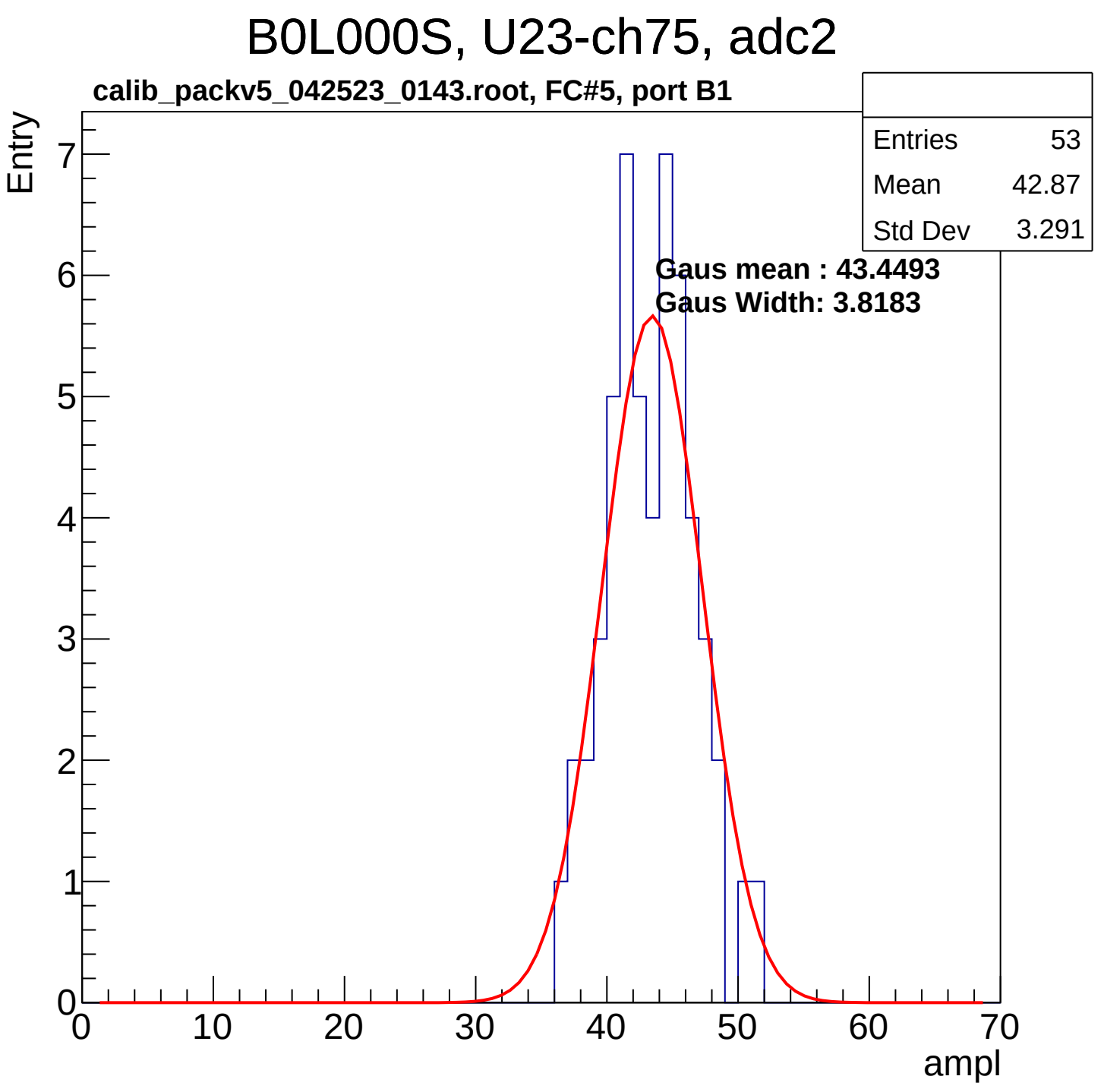

B0L000S, U23-ch74, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.64
Std Dev 3.045
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

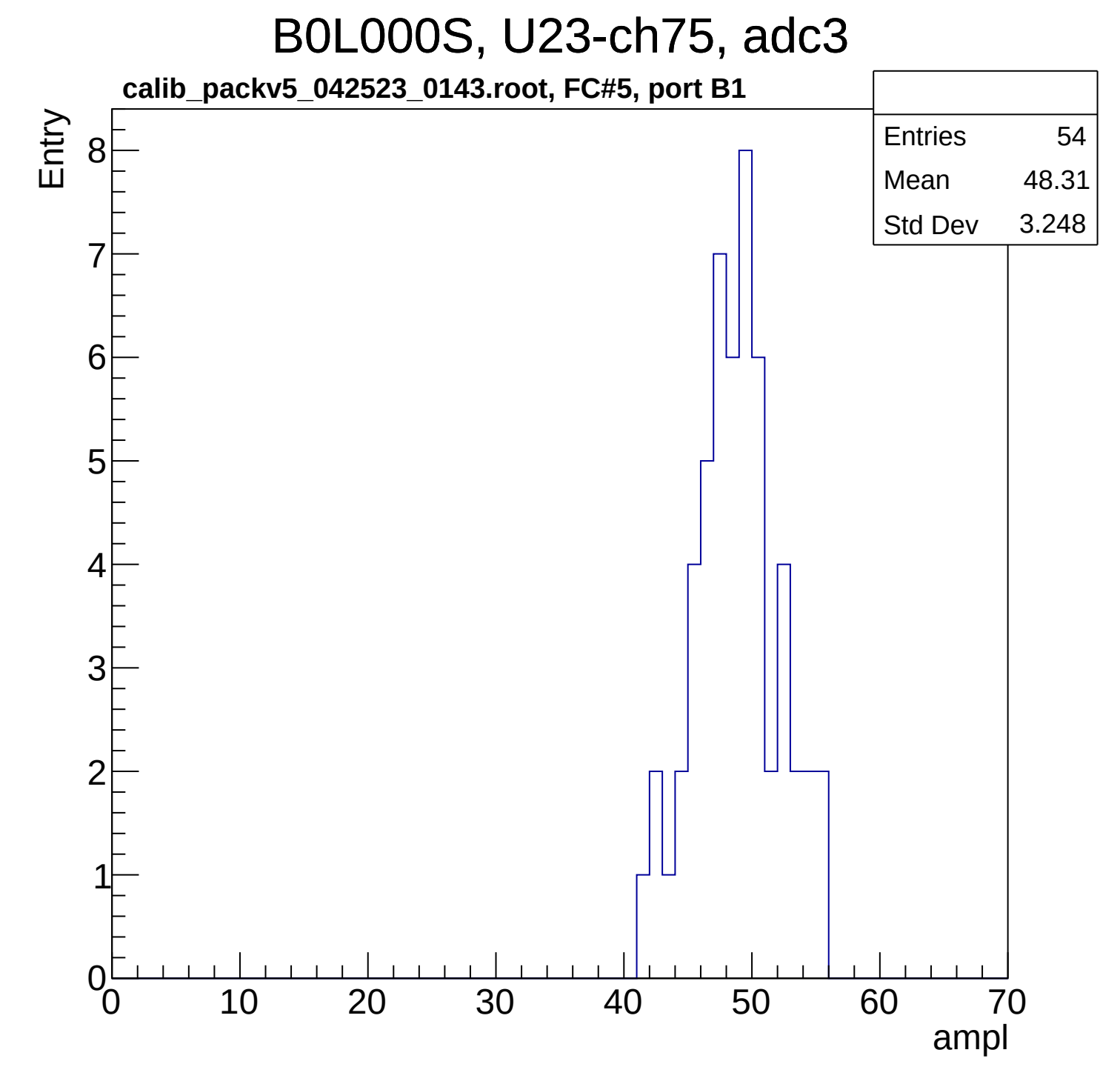

B0L000S, U23-ch74, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 58.18
Std Dev 14.59
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

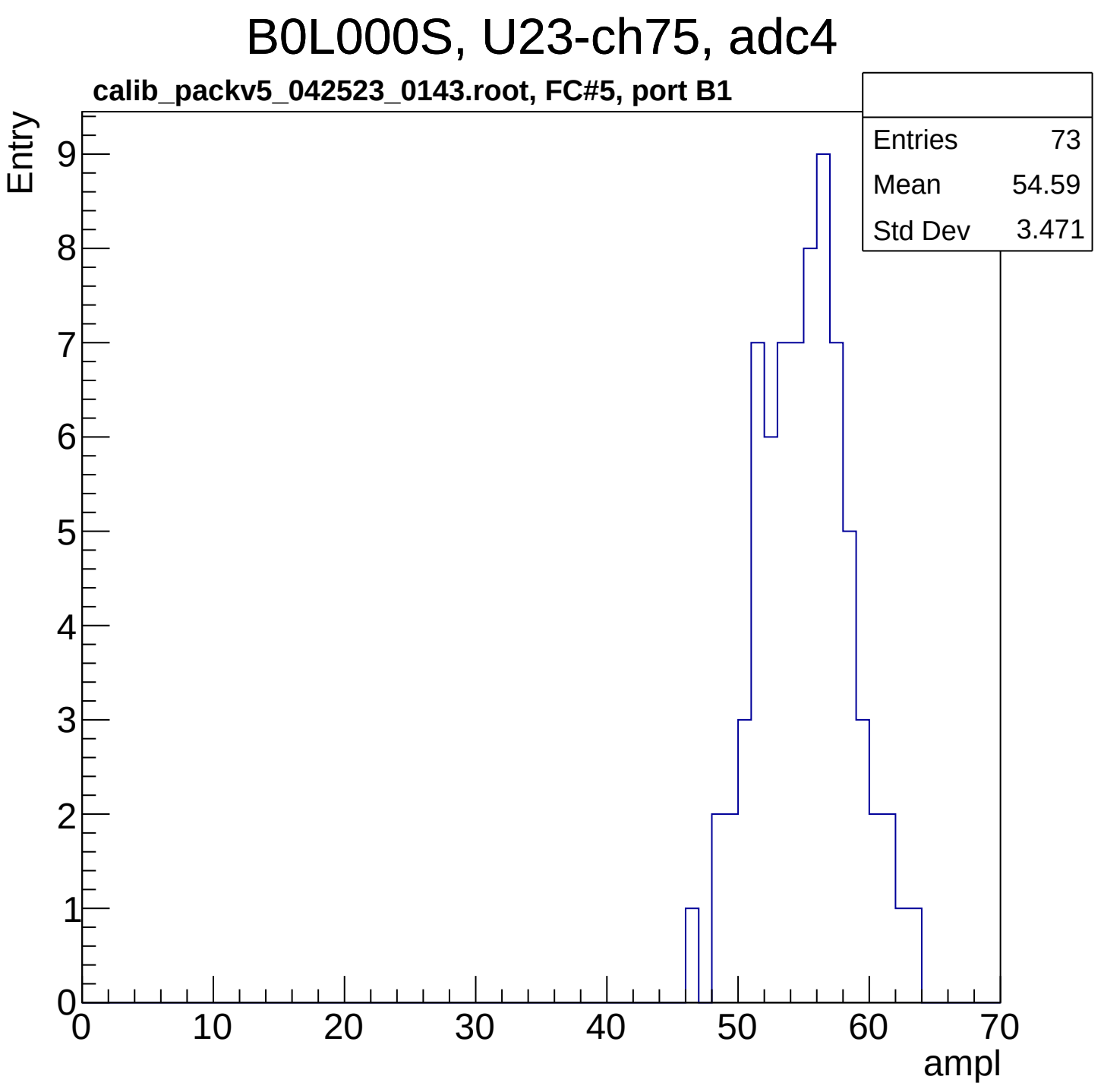

B0L000S, U23-ch74, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

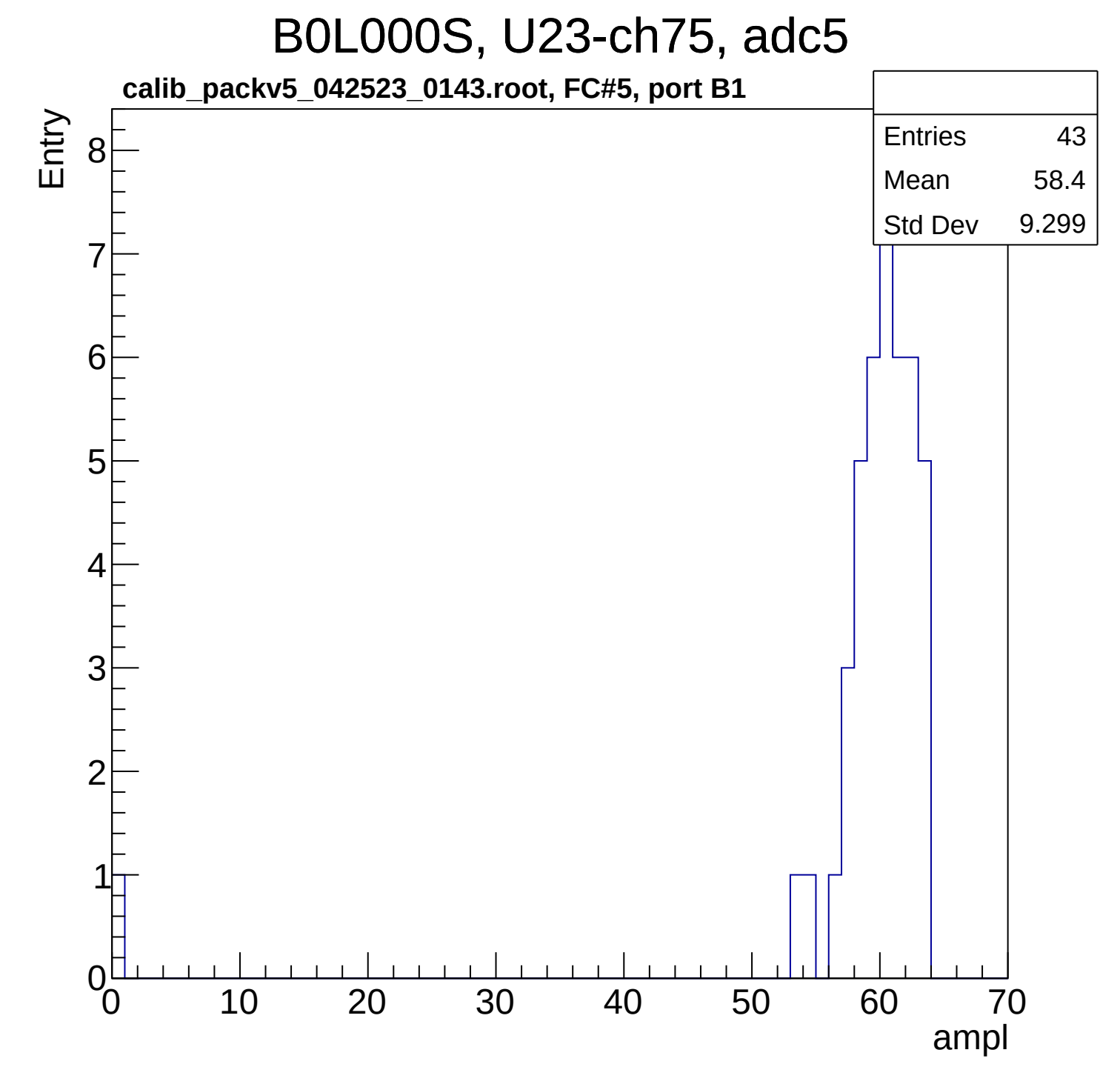

B0L000S, U23-ch75, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 28.83
Std Dev 3.142
Gaus mean : 29.5009
Gaus Width: 3.1360
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

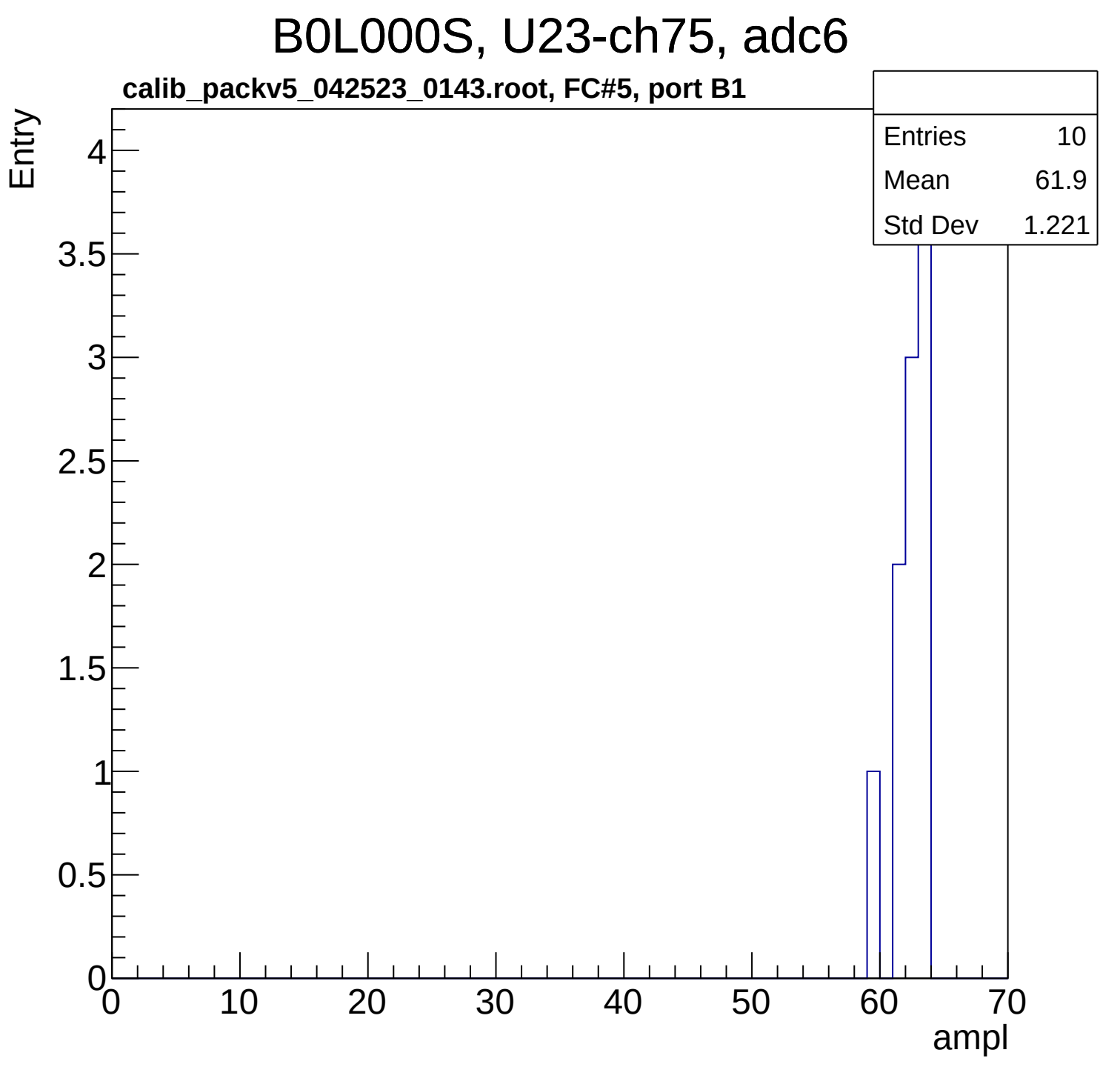

B0L000S, U23-ch75, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.11
Std Dev 3.169
Gaus mean : 37.2297
Gaus Width: 3.2052
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

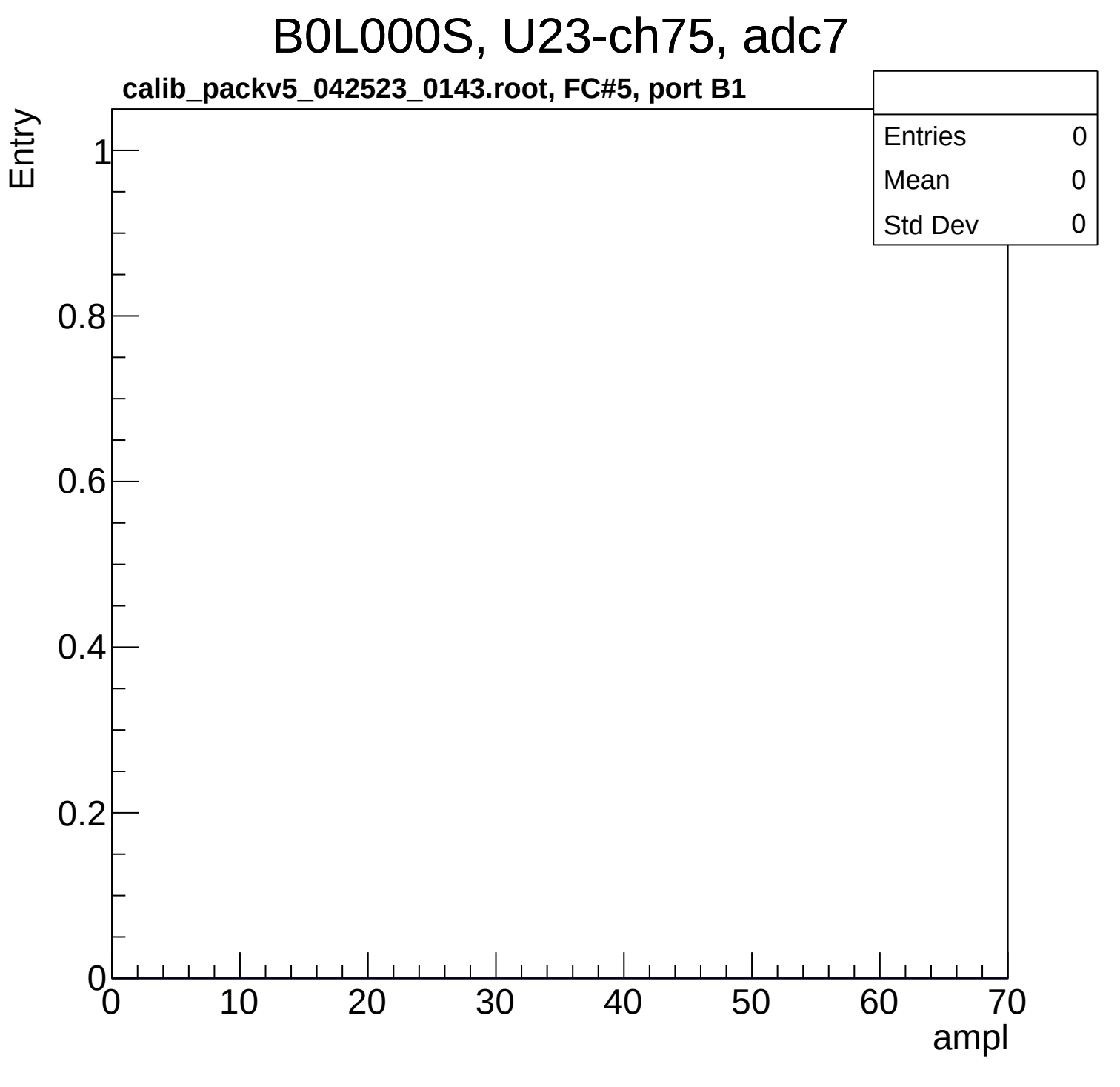

B0L000S, U23-ch75, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 42.87
Std Dev 3.291
Gaus mean : 43.4493
Gaus Width: 3.8183
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

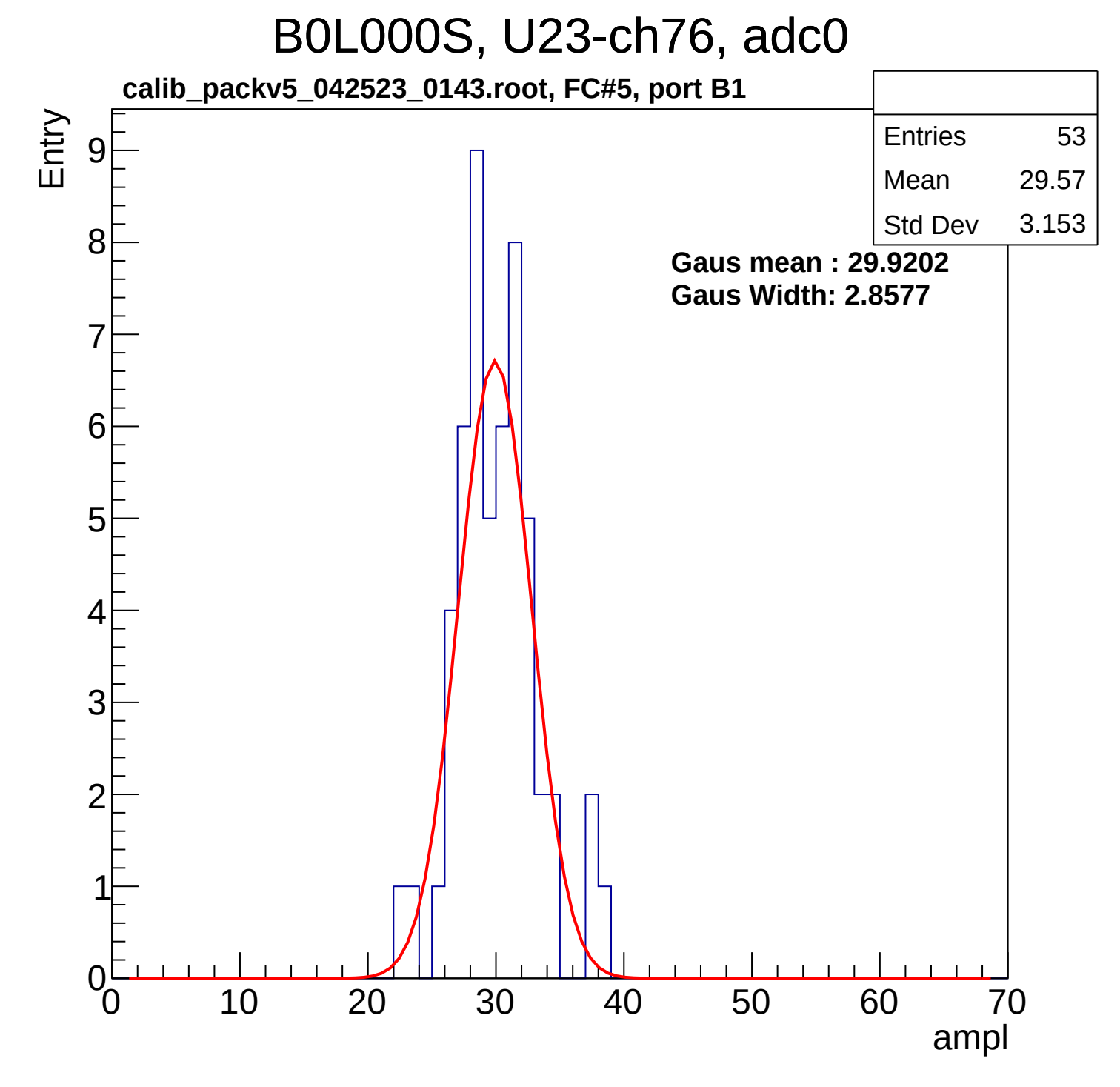

B0L000S, U23-ch75, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 48.31
Std Dev 3.248
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

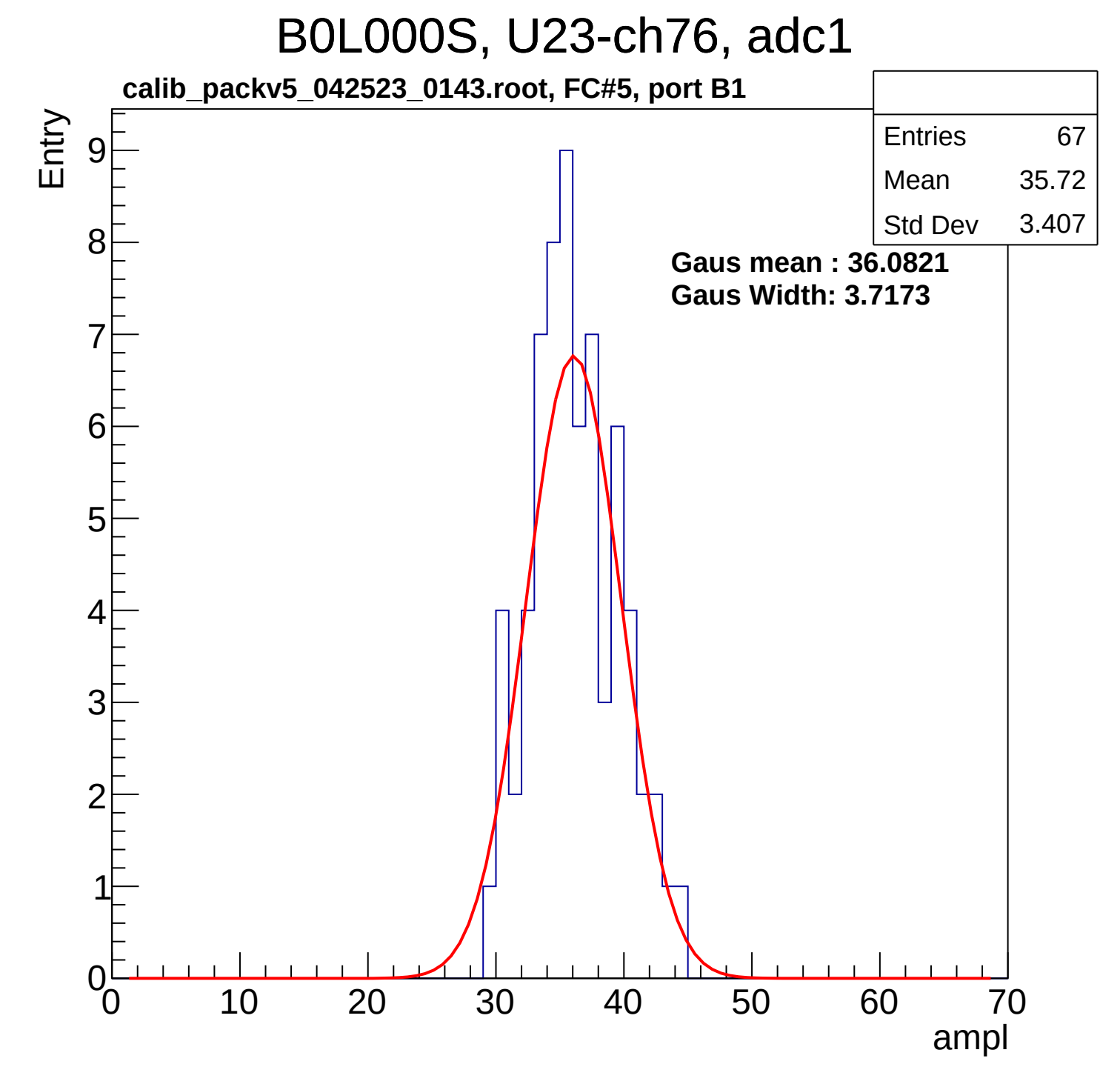

B0L000S, U23-ch75, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 54.59
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

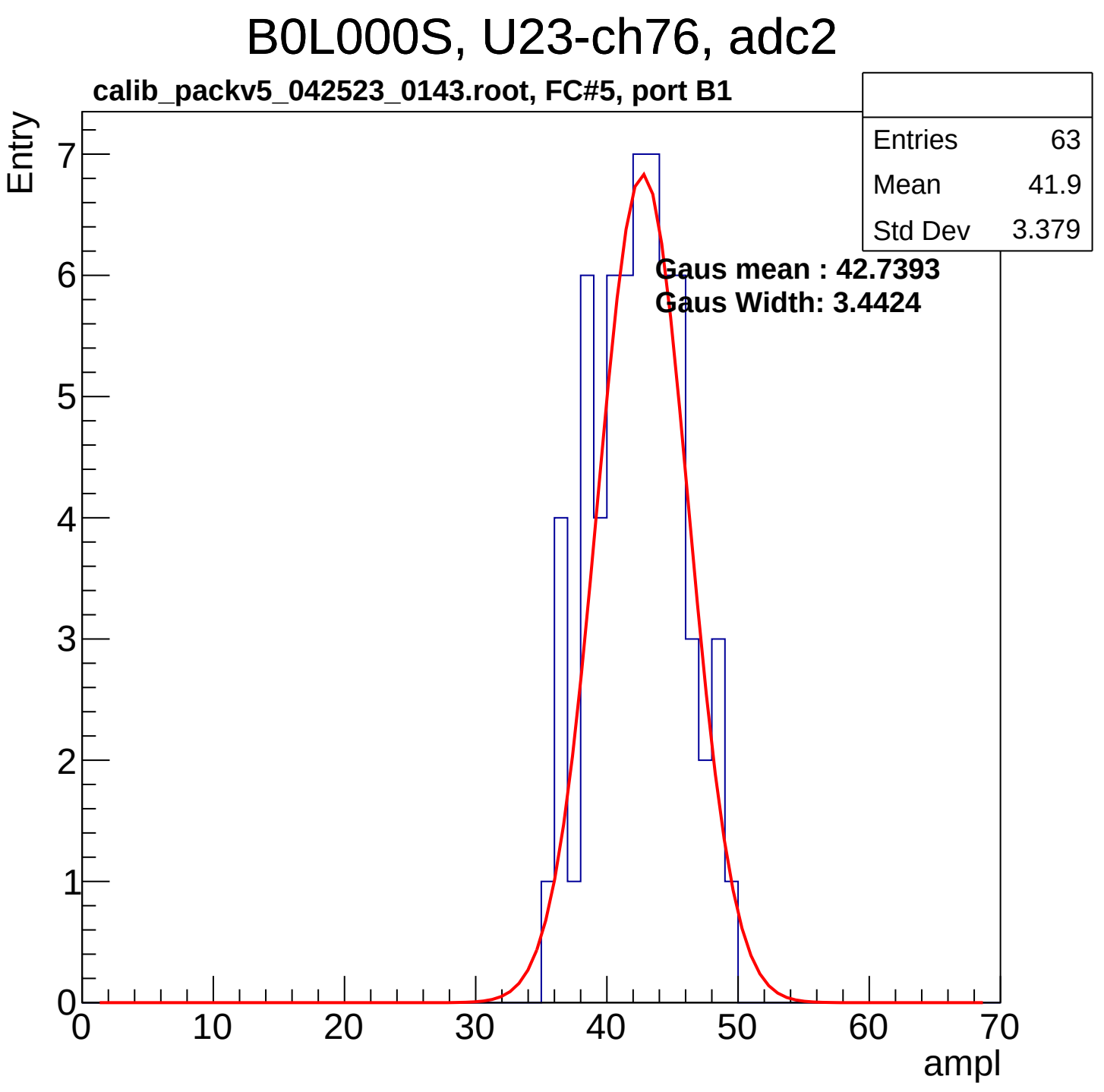

B0L000S, U23-ch75, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.4
Std Dev 9.299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

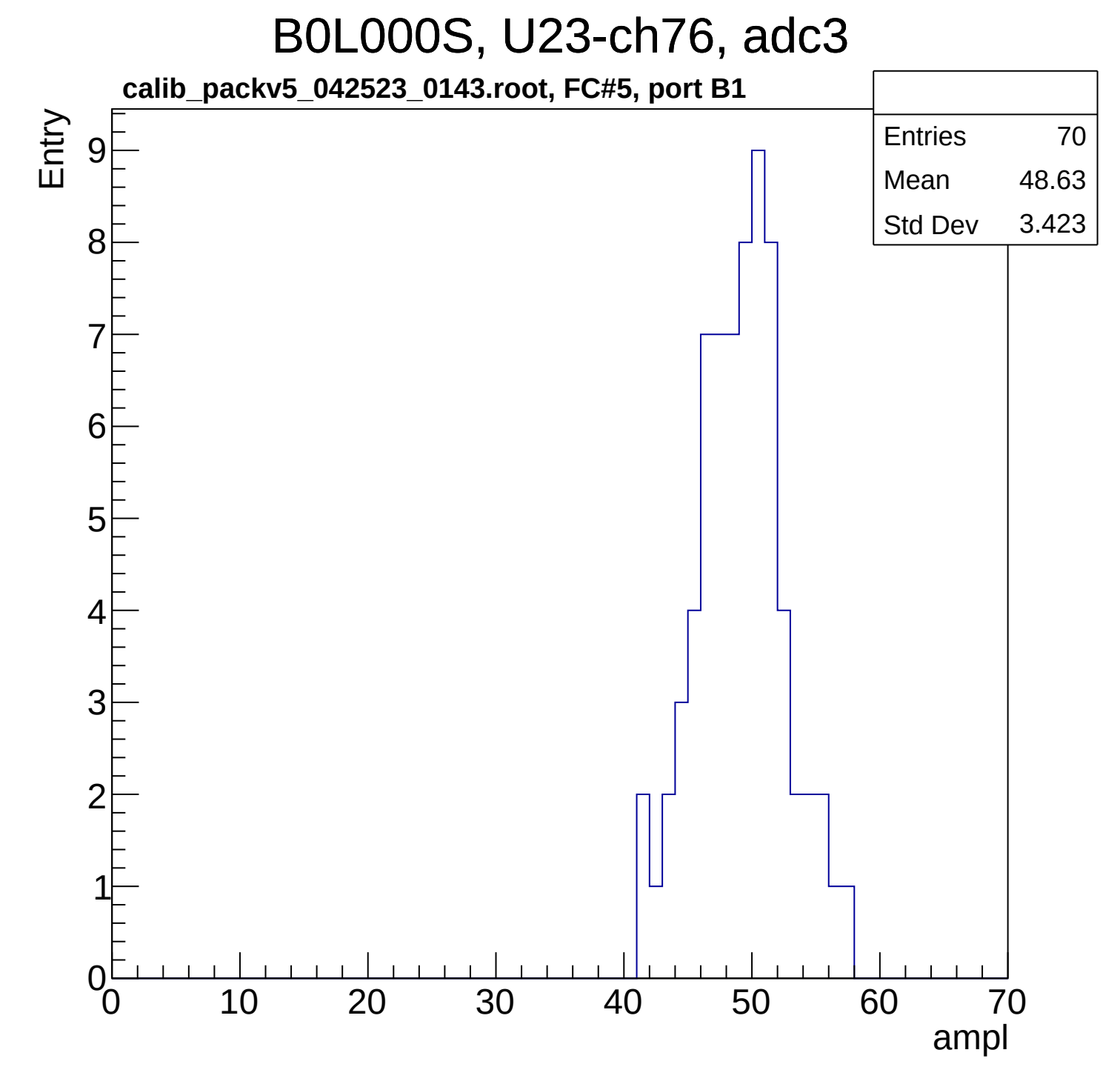

B0L000S, U23-ch75, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.9
Std Dev 1.221
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

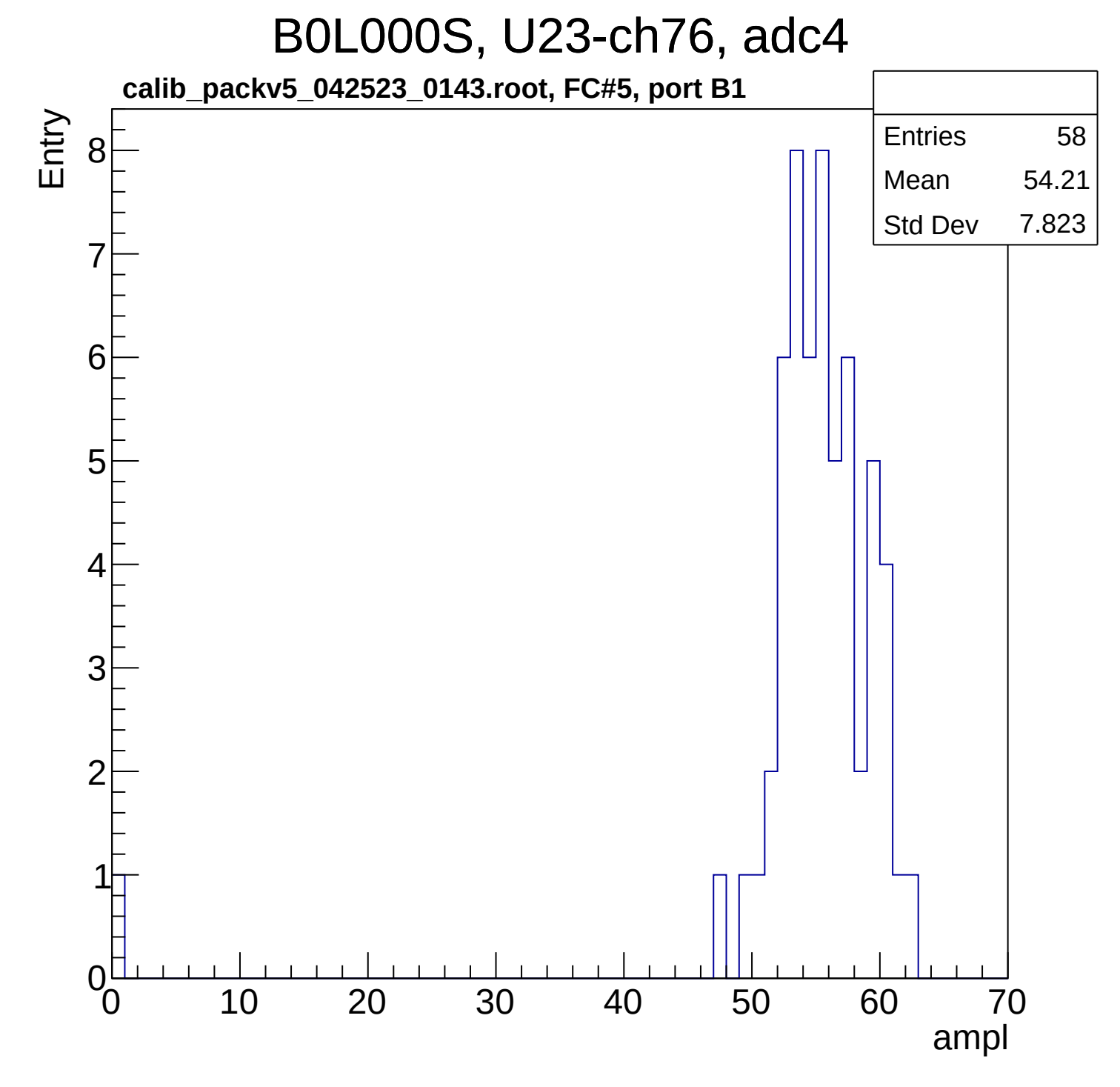

B0L000S, U23-ch75, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

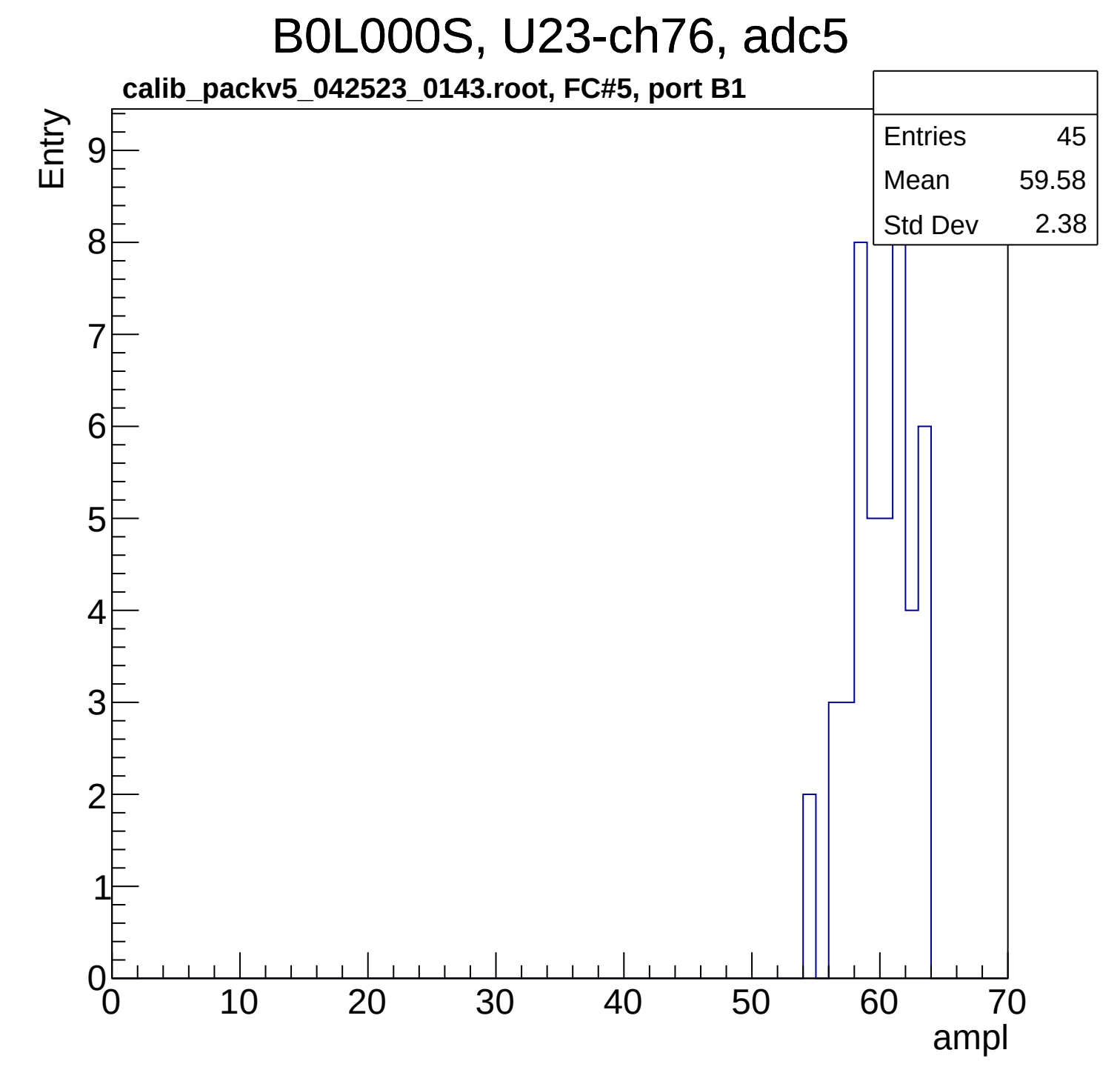

B0L000S, U23-ch76, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 29.57
Std Dev 3.153
Gaus mean : 29.9202
Gaus Width: 2.8577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

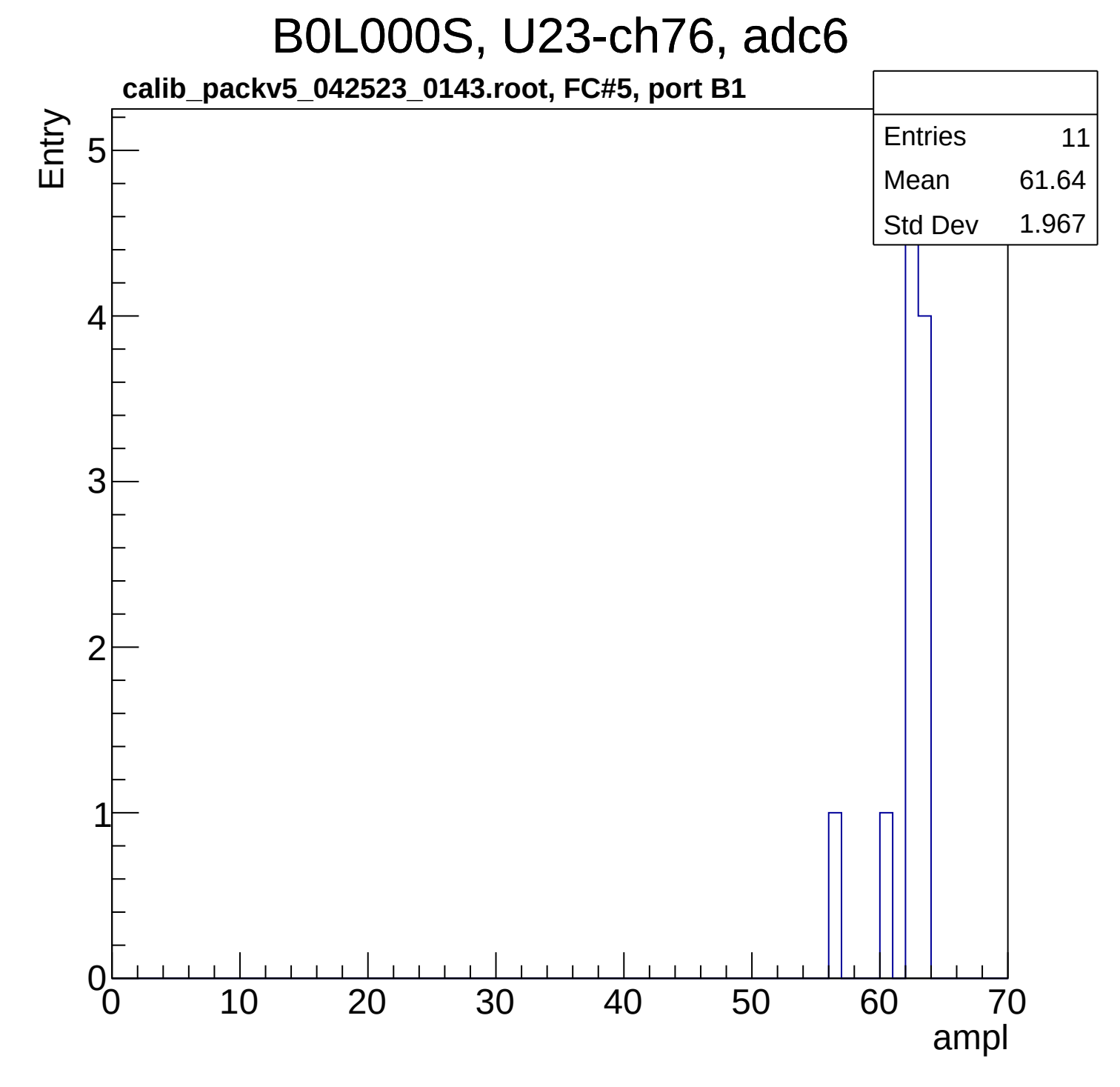

B0L000S, U23-ch76, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 35.72
Std Dev 3.407
Gaus mean : 36.0821
Gaus Width: 3.7173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

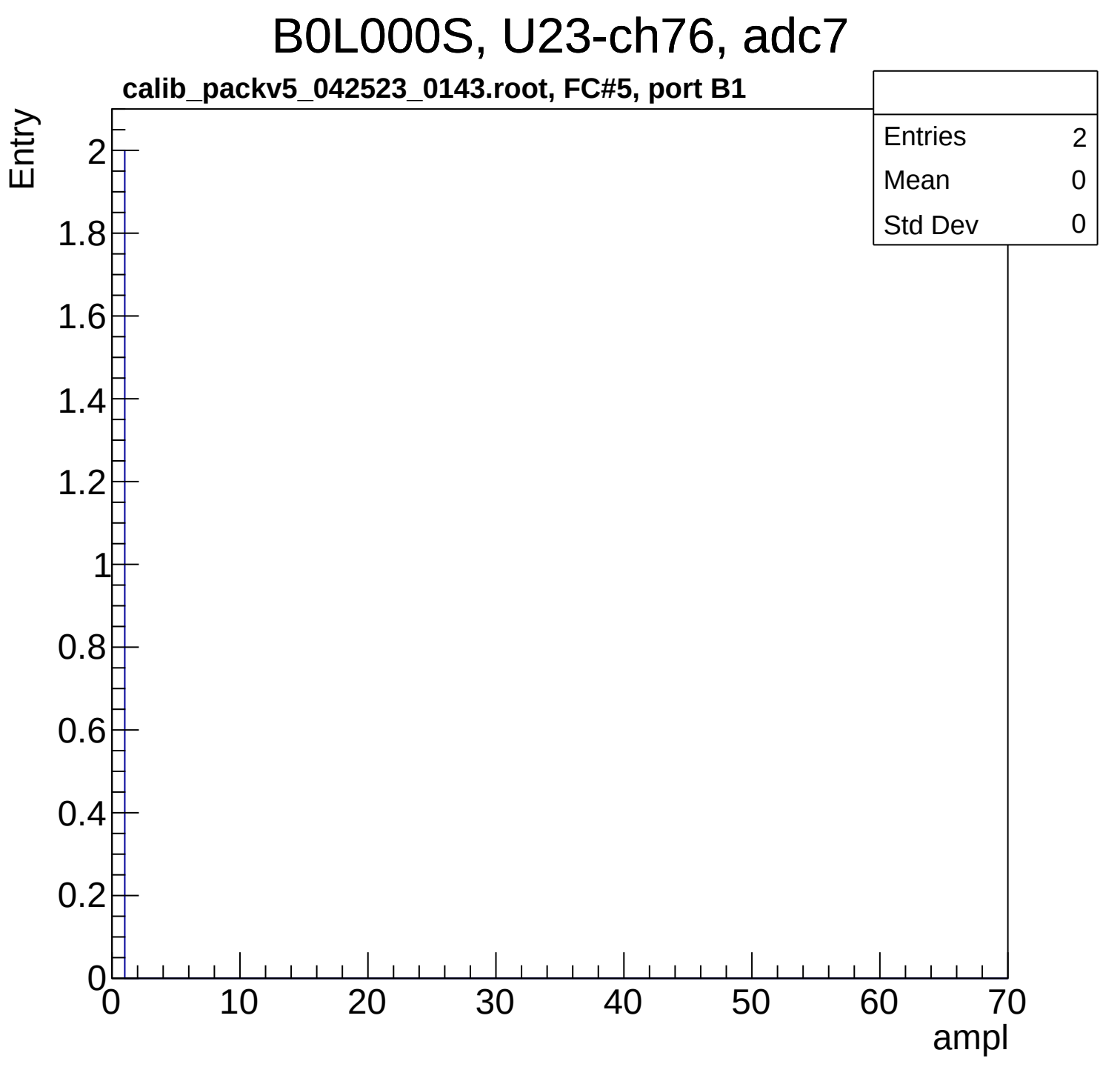

B0L000S, U23-ch76, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.9
Std Dev 3.379
Gaus mean : 42.7393
Gaus Width: 3.4424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

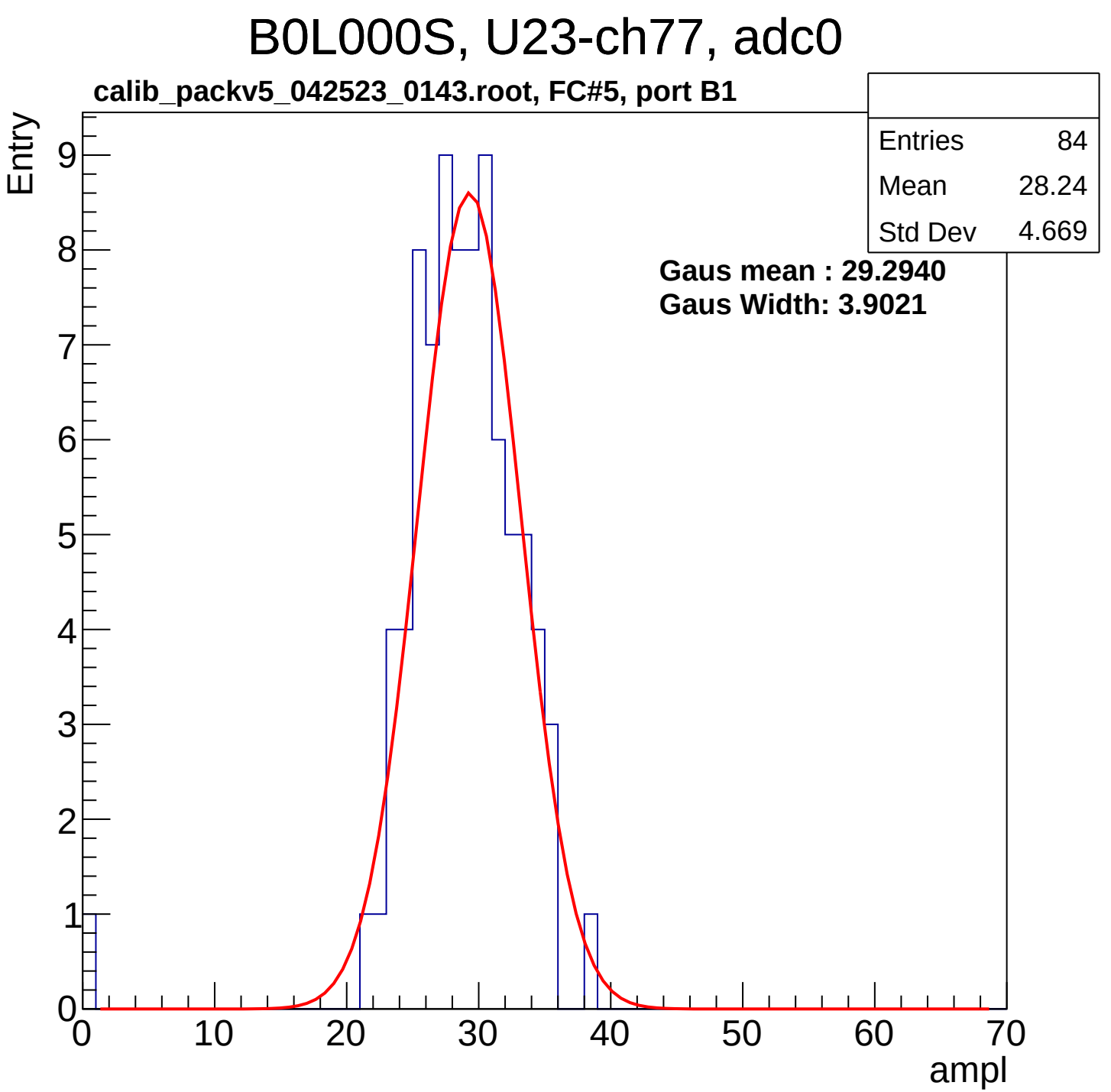

B0L000S, U23-ch76, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.63
Std Dev 3.423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

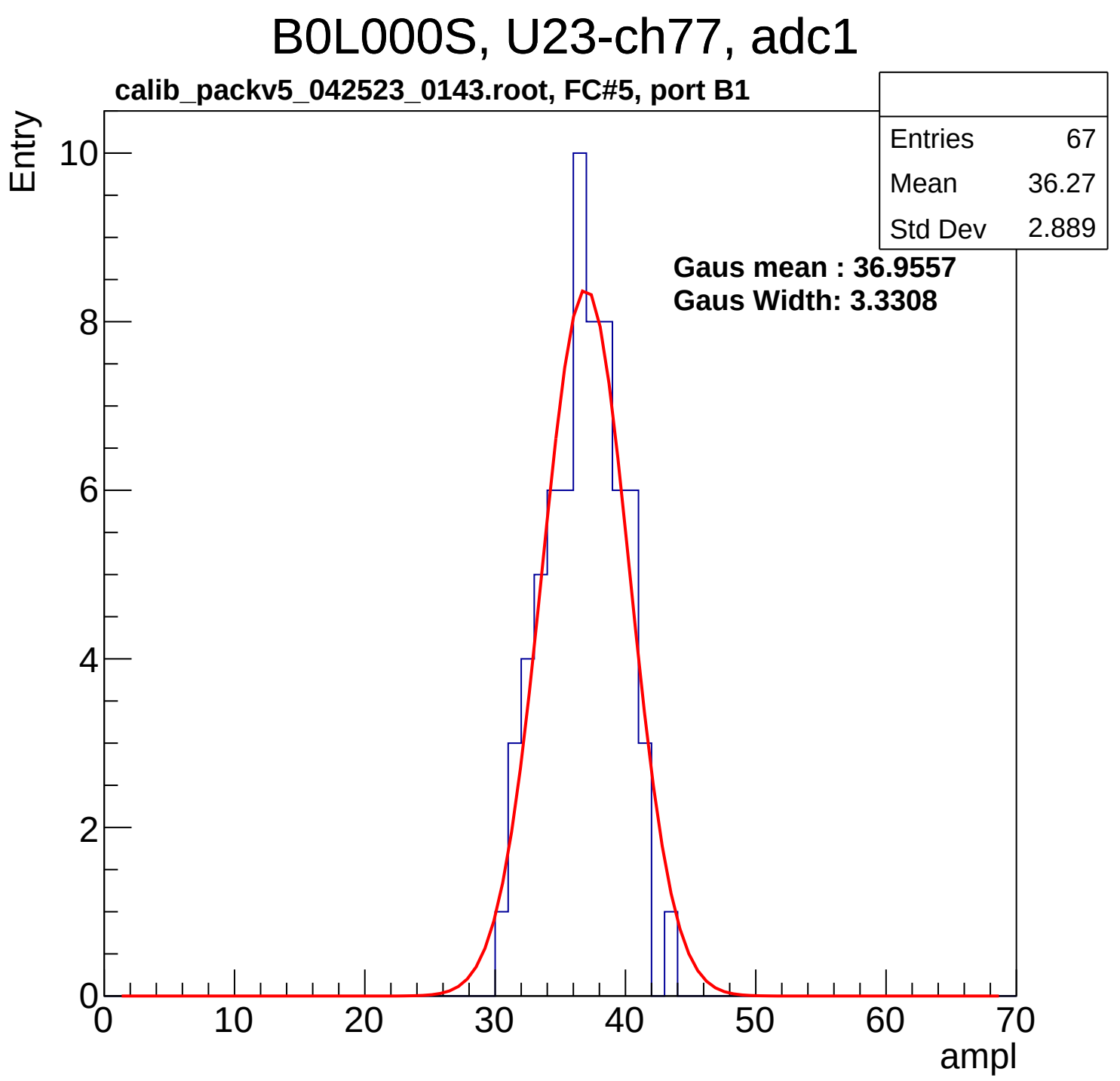

B0L000S, U23-ch76, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 54.21
Std Dev 7.823
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

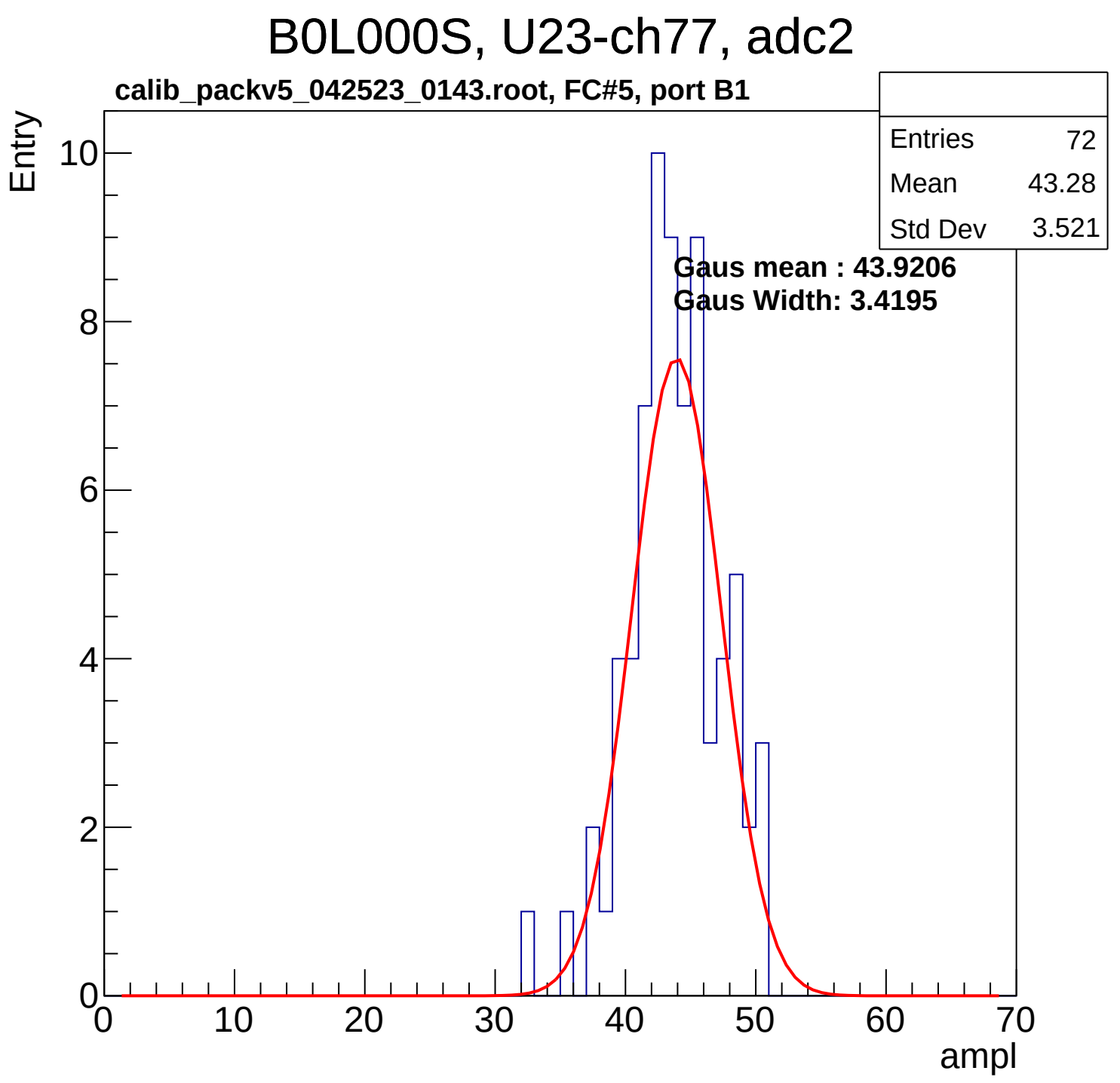

B0L000S, U23-ch76, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.58
Std Dev 2.38
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

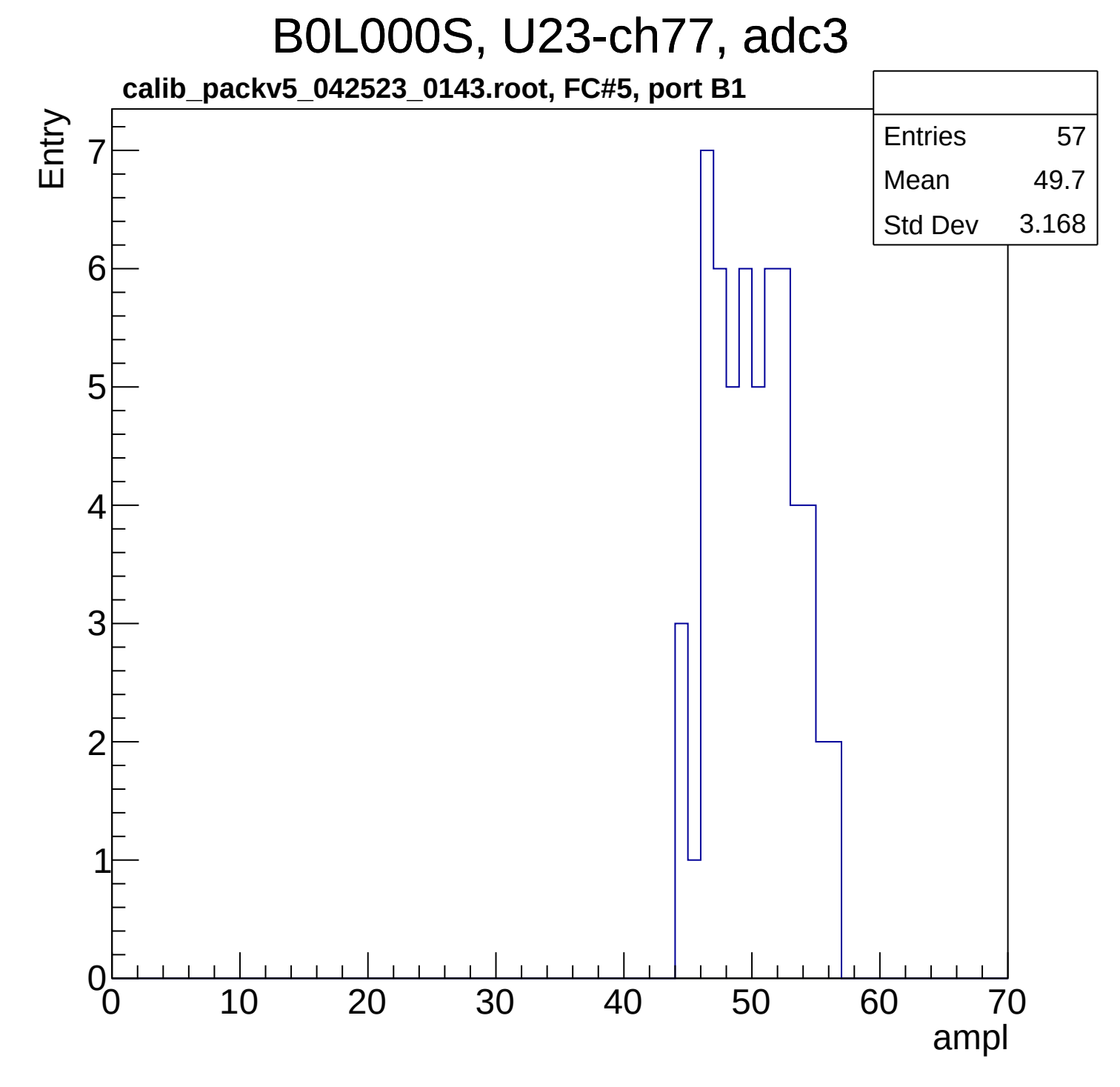

B0L000S, U23-ch76, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.64
Std Dev 1.967
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

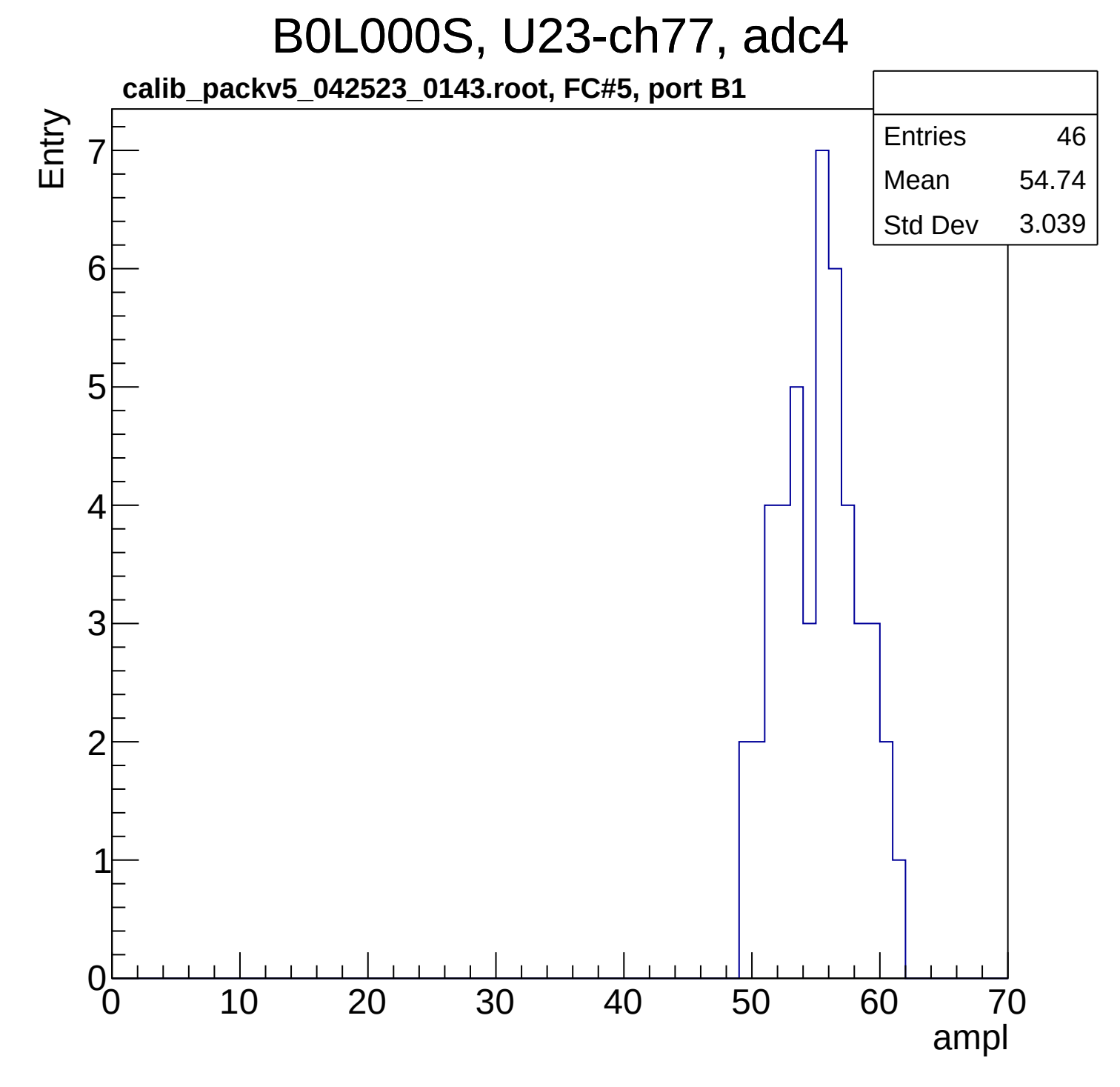

B0L000S, U23-ch76, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

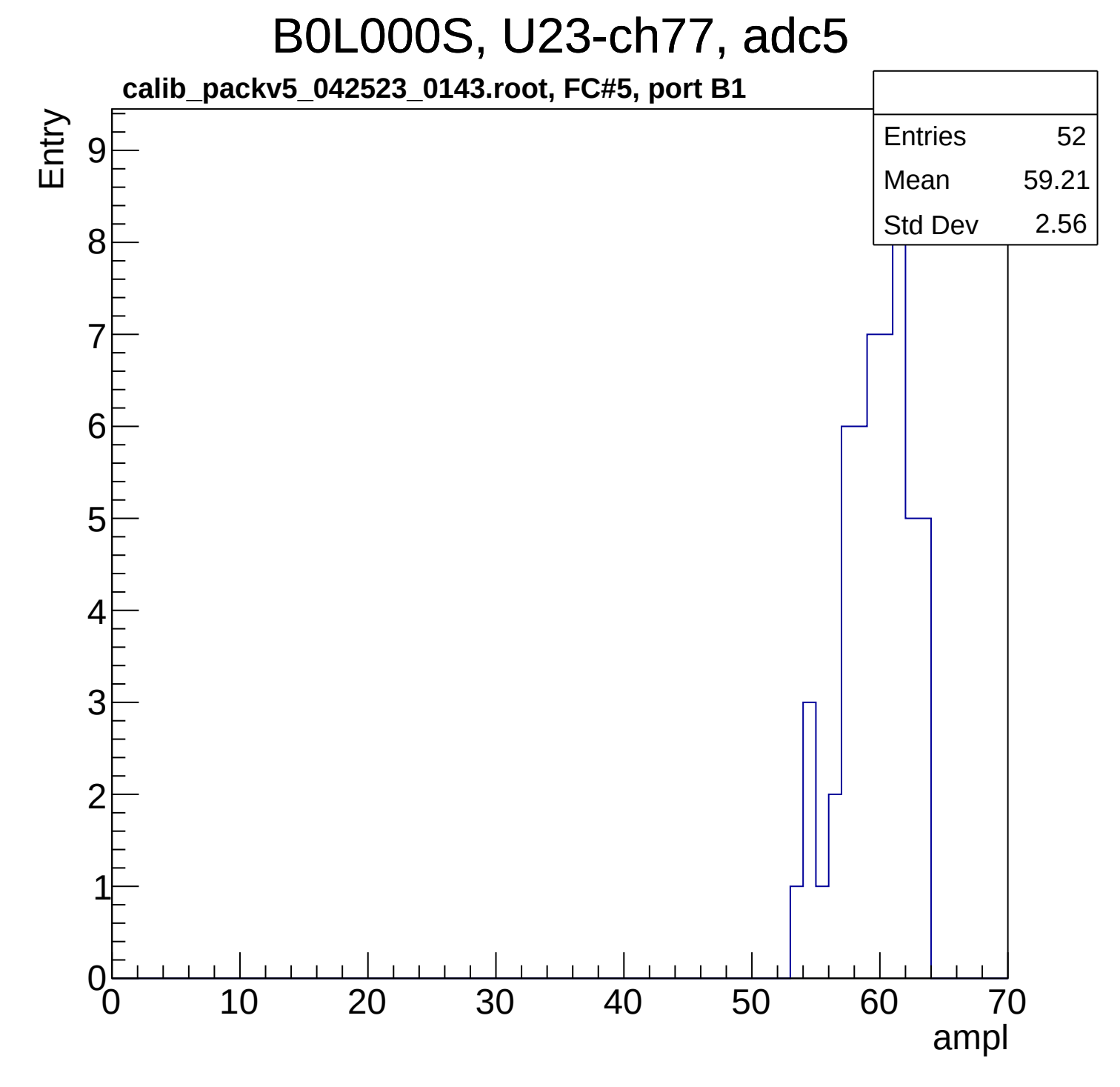

B0L000S, U23-ch77, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 28.24
Std Dev 4.669
Gaus mean : 29.2940
Gaus Width: 3.9021
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

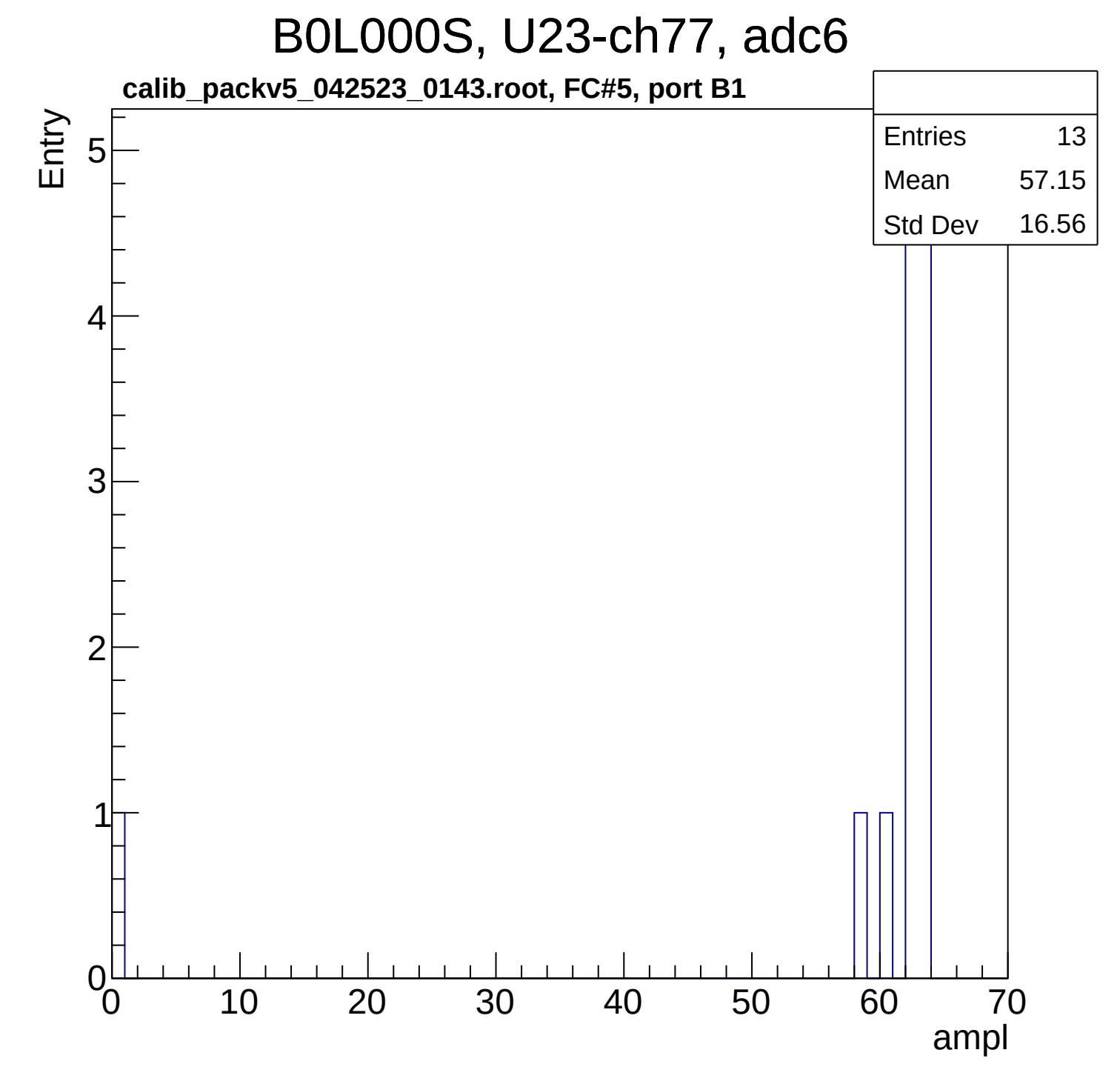

B0L000S, U23-ch77, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.27
Std Dev 2.889
Gaus mean : 36.9557
Gaus Width: 3.3308
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

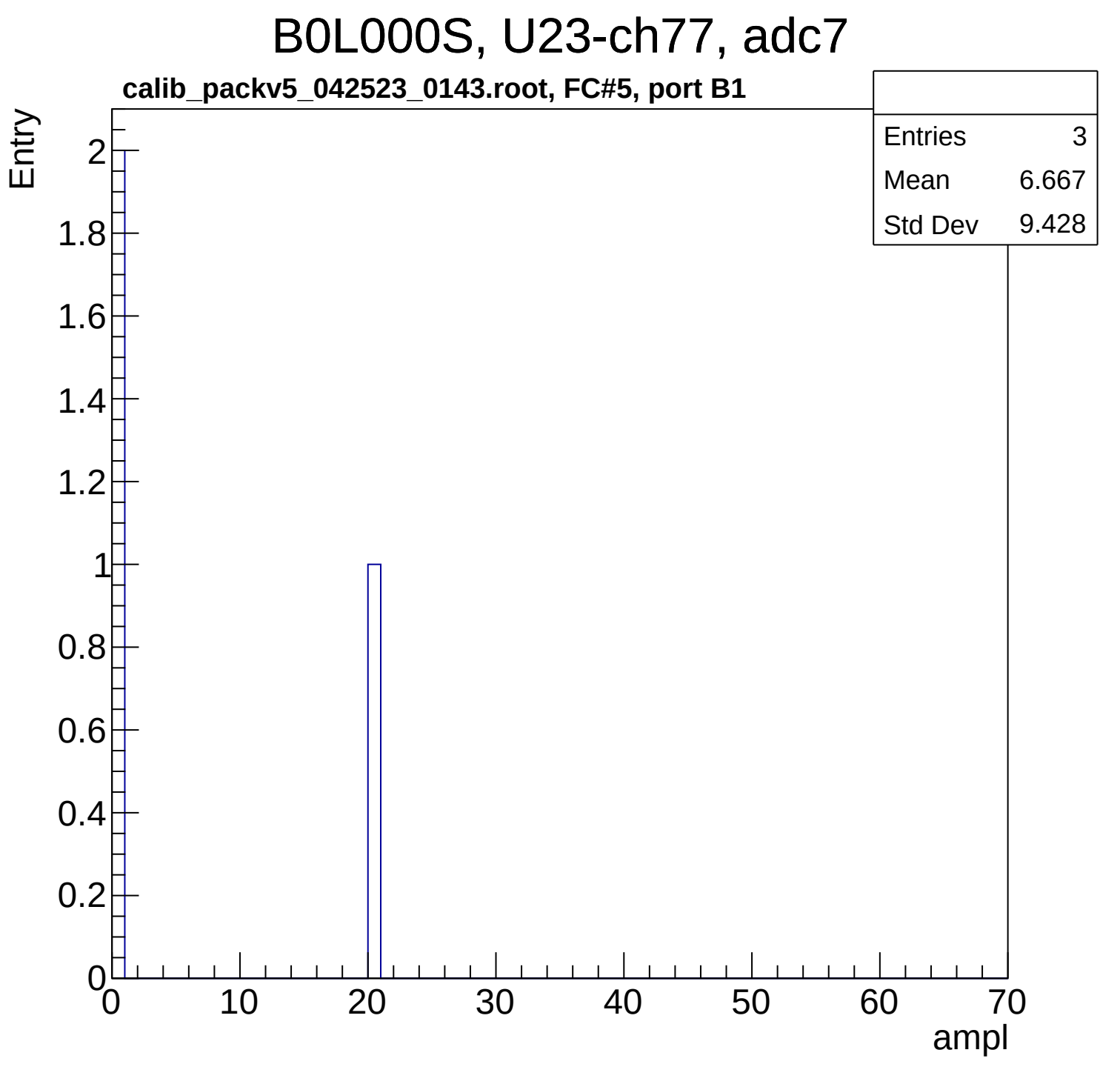

B0L000S, U23-ch77, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 43.28
Std Dev 3.521
Gaus mean : 43.9206
Gaus Width: 3.4195
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

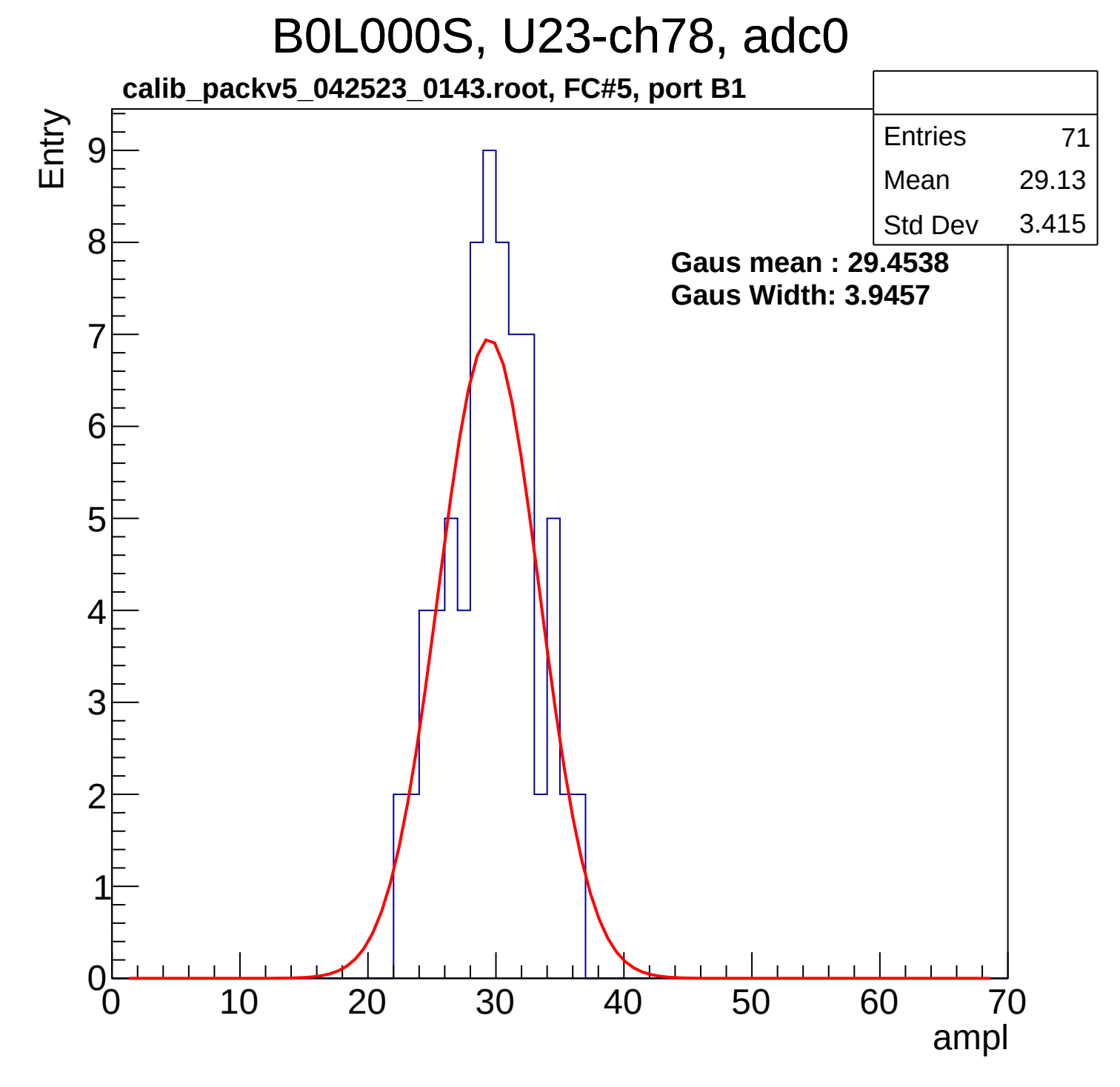

B0L000S, U23-ch77, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.7
Std Dev 3.168
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

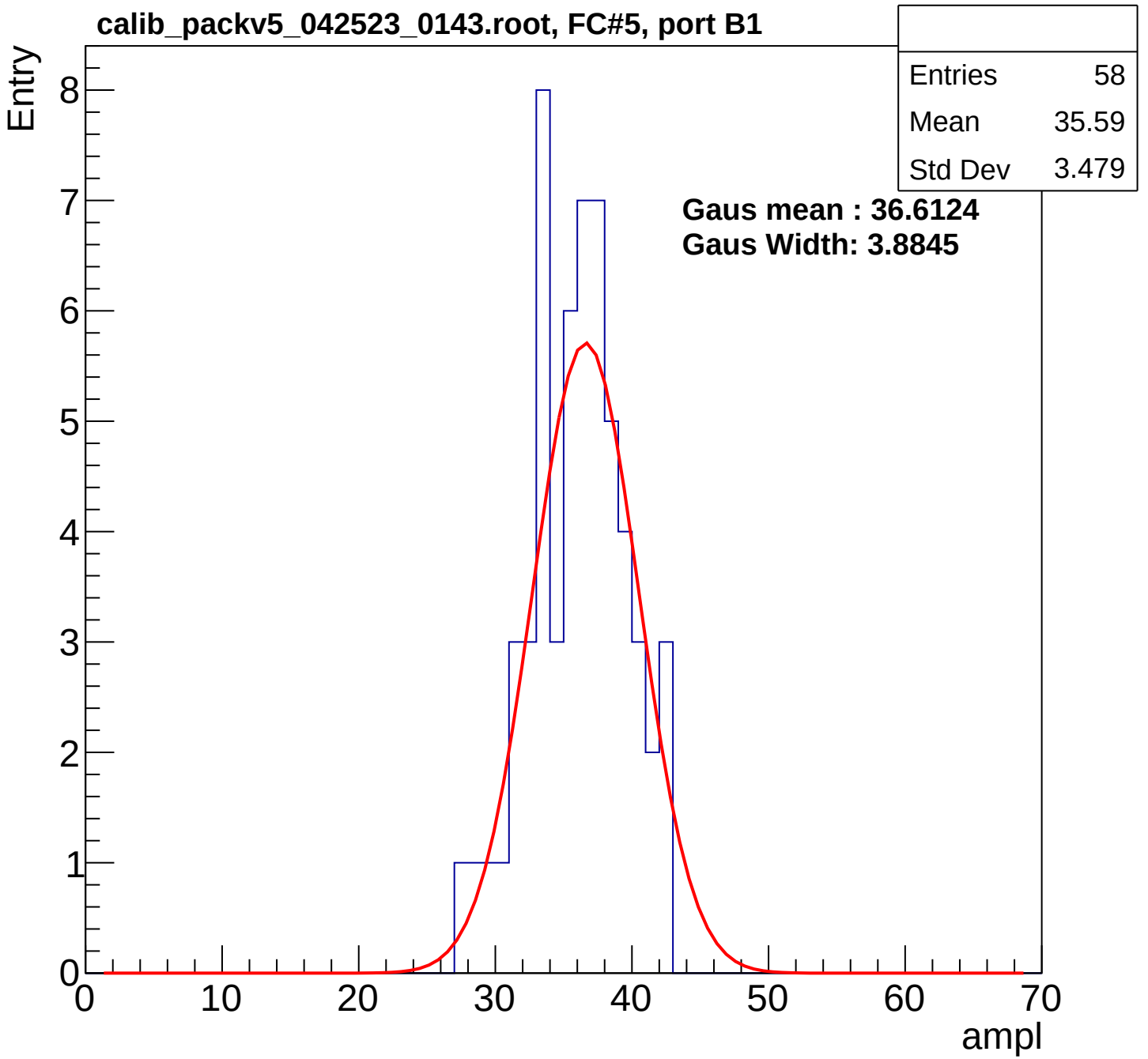

B0L000S, U23-ch77, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 54.74
Std Dev 3.039
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

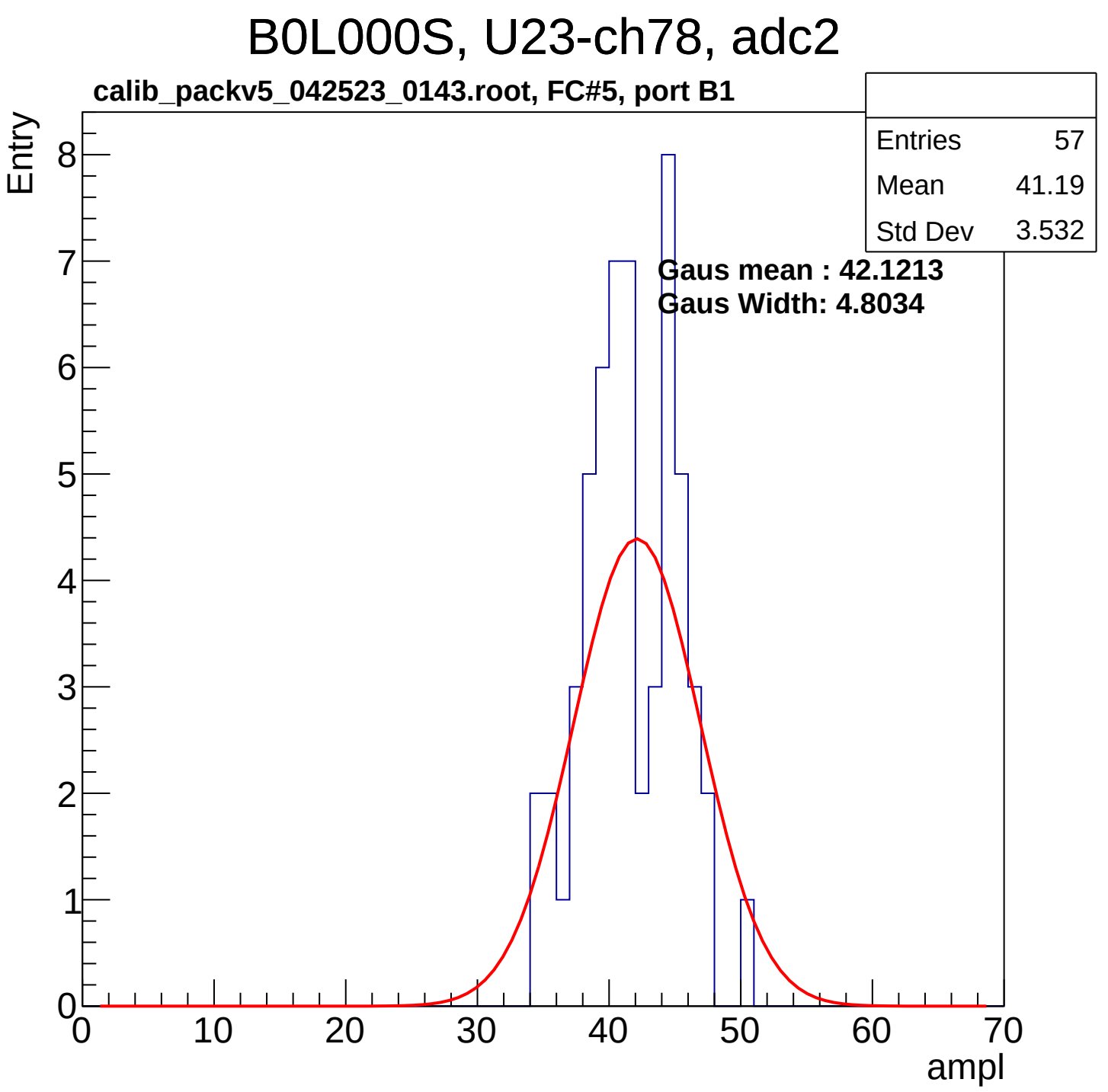

B0L000S, U23-ch77, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.21
Std Dev 2.56
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

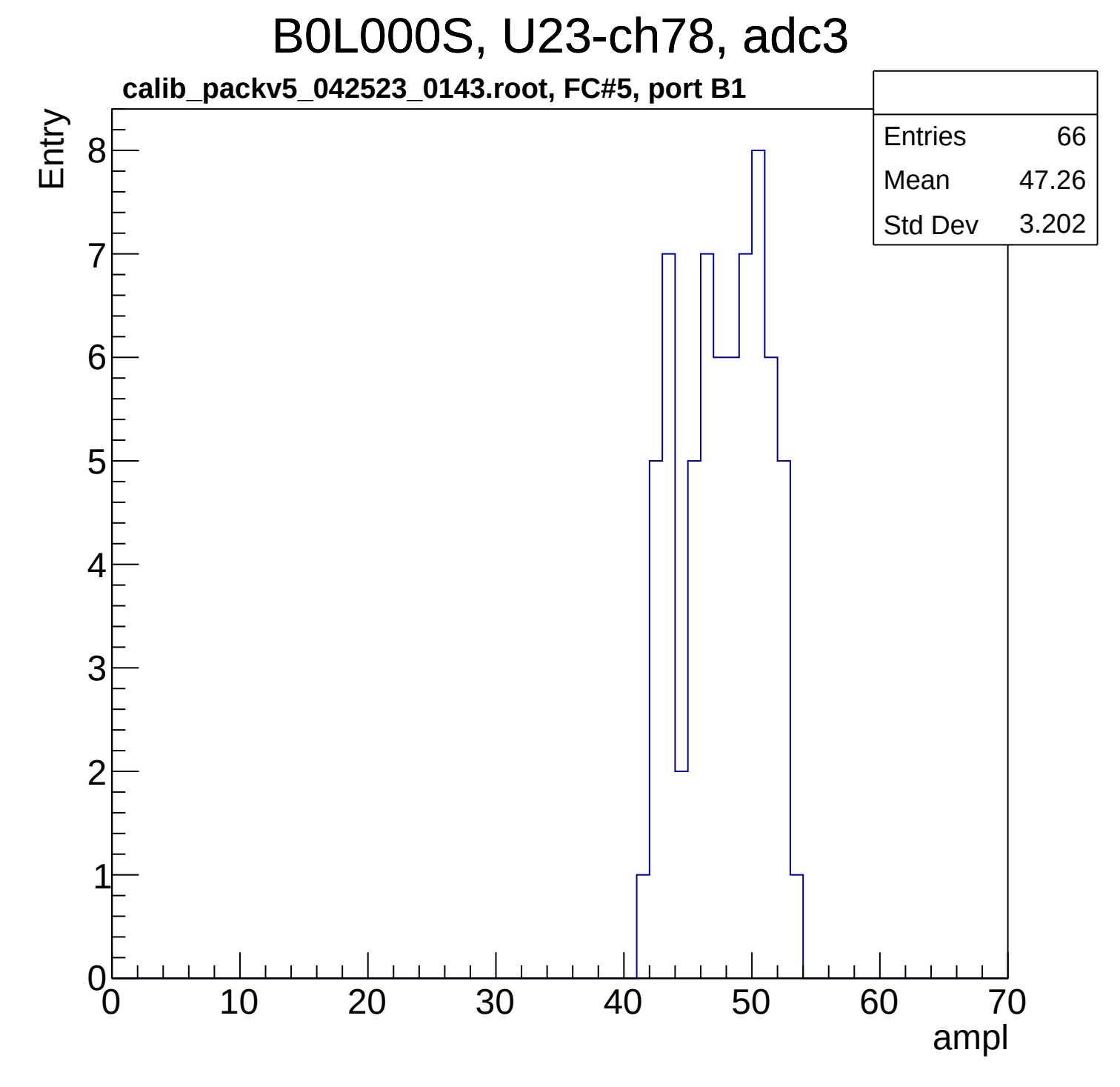

B0L000S, U23-ch77, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.15
Std Dev 16.56
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

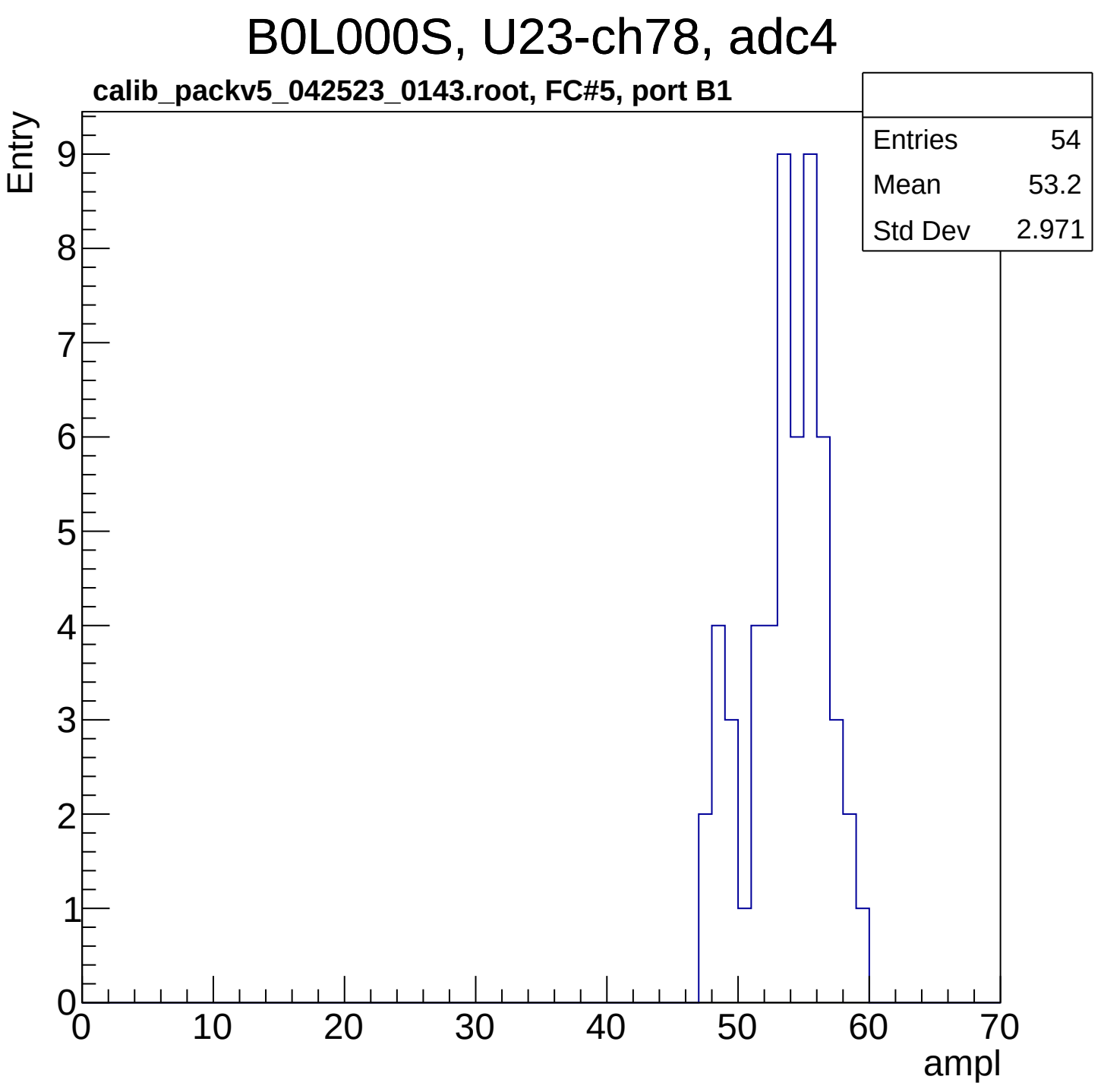

B0L000S, U23-ch77, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

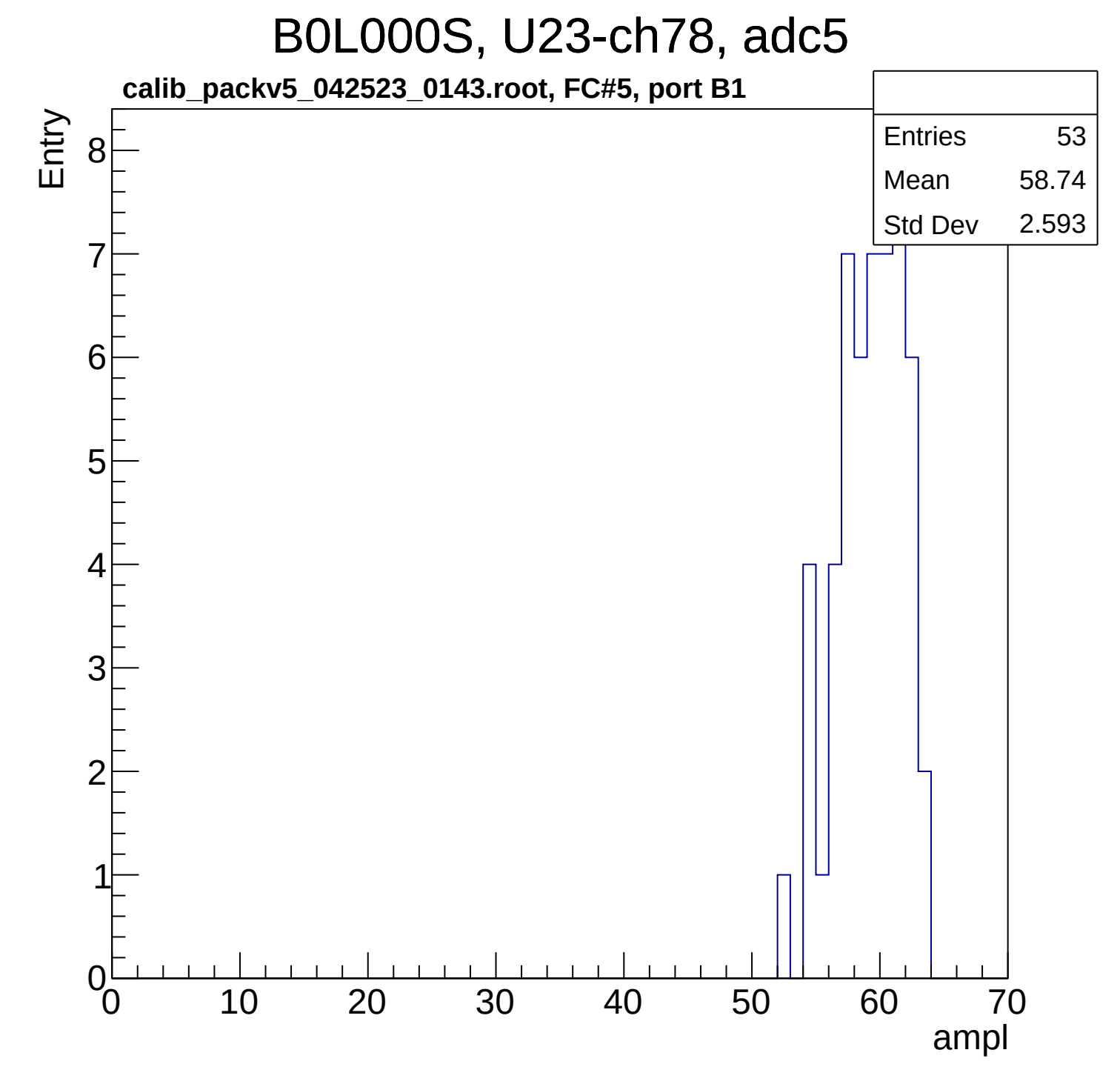

B0L000S, U23-ch78, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.13
Std Dev 3.415
Gaus mean : 29.4538
Gaus Width: 3.9457
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

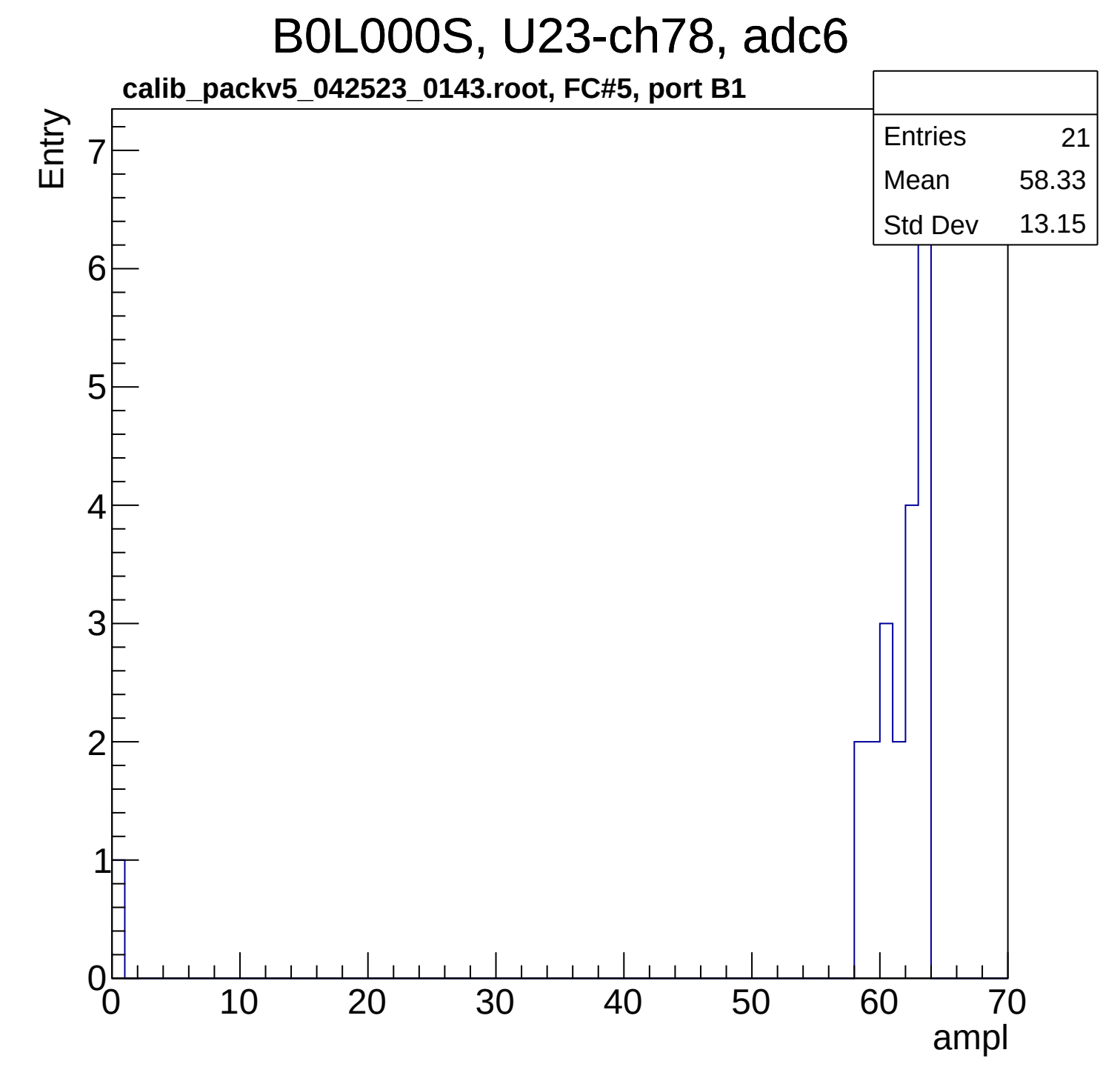

B0L000S, U23-ch78, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.59
Std Dev 3.479
Gaus mean : 36.6124
Gaus Width: 3.8845
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

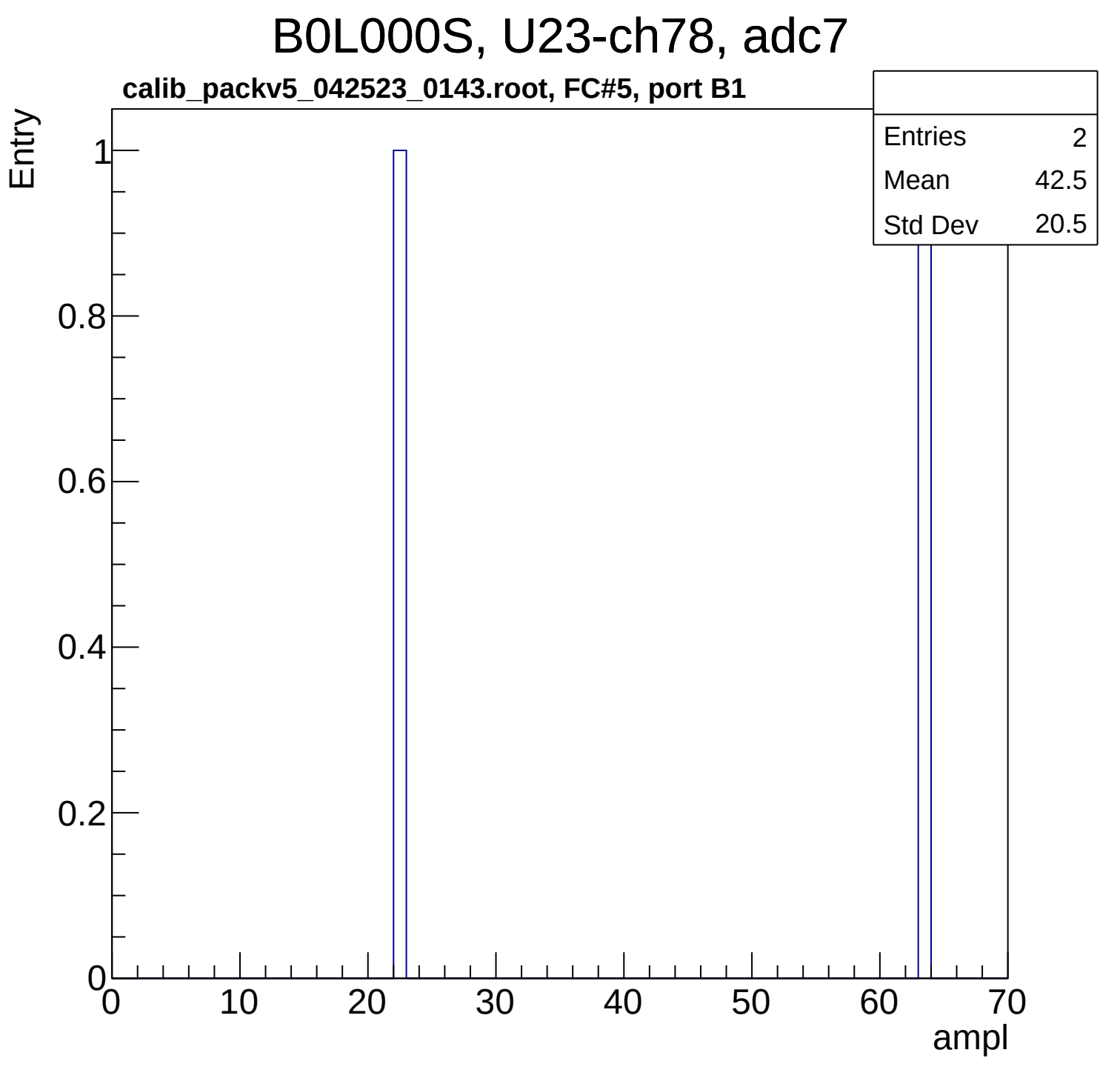

B0L000S, U23-ch78, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 41.19
Std Dev 3.532
Gaus mean : 42.1213
Gaus Width: 4.8034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

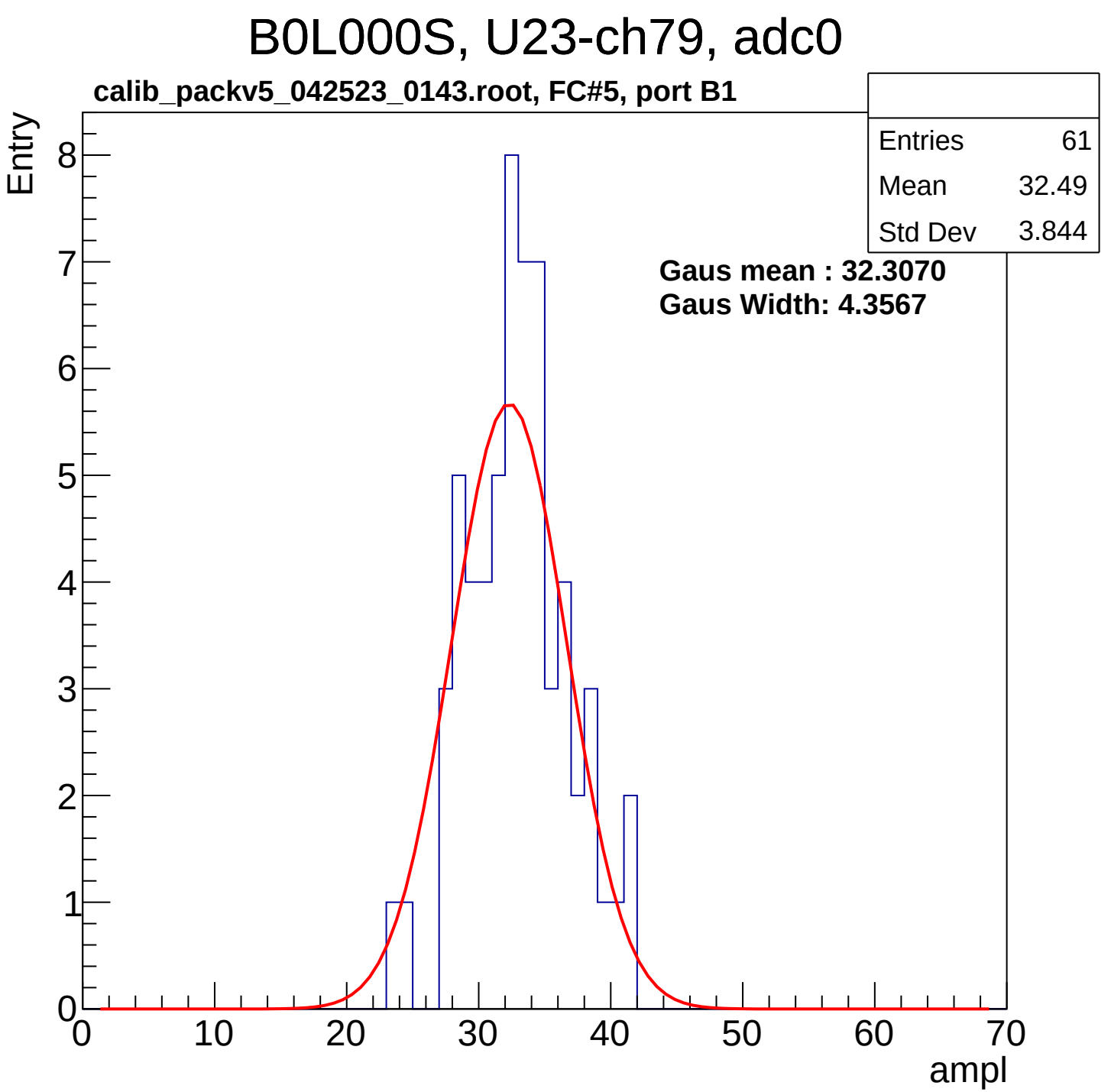

B0L000S, U23-ch78, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 47.26
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

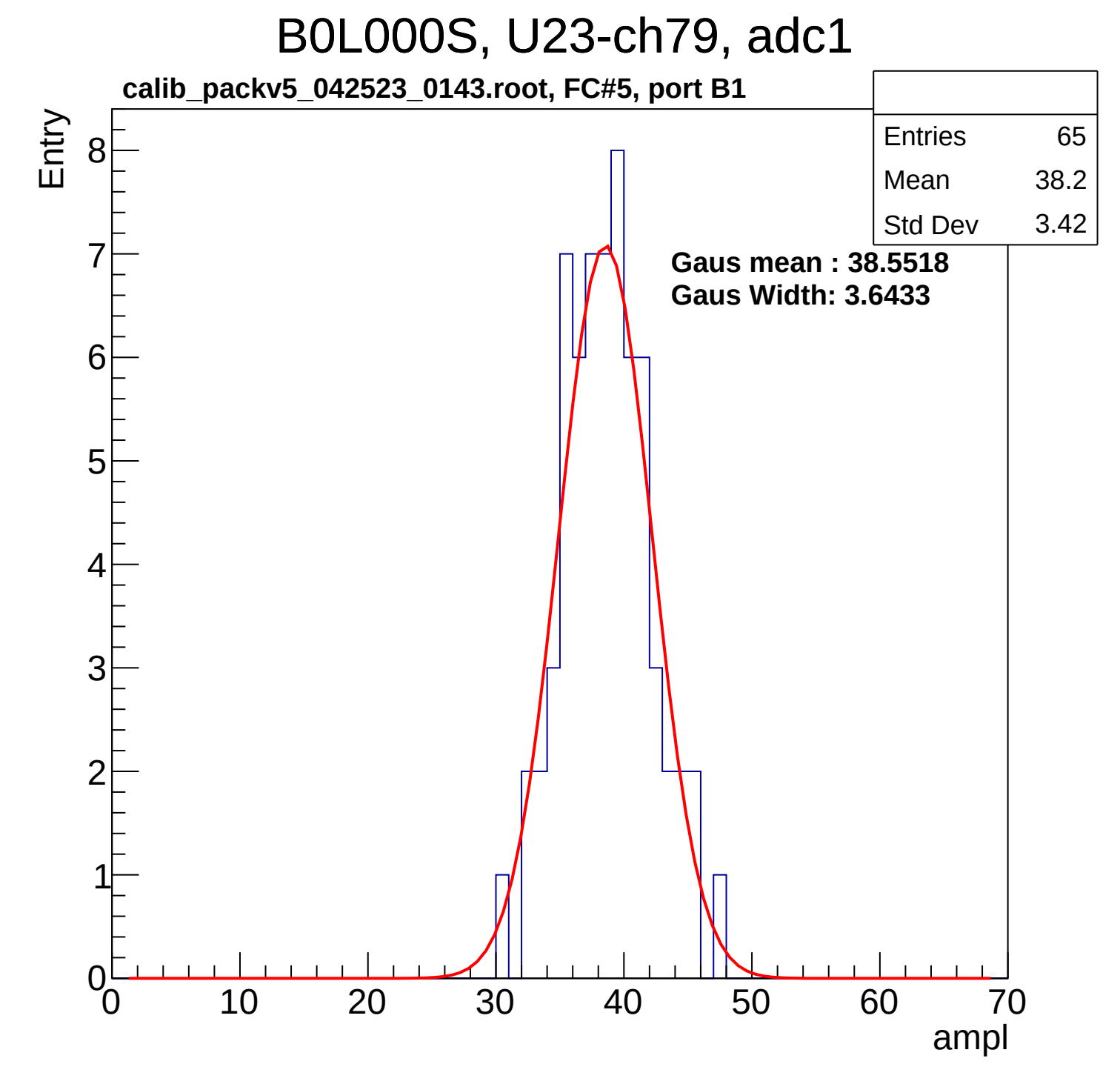

B0L000S, U23-ch78, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 53.2
Std Dev 2.971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

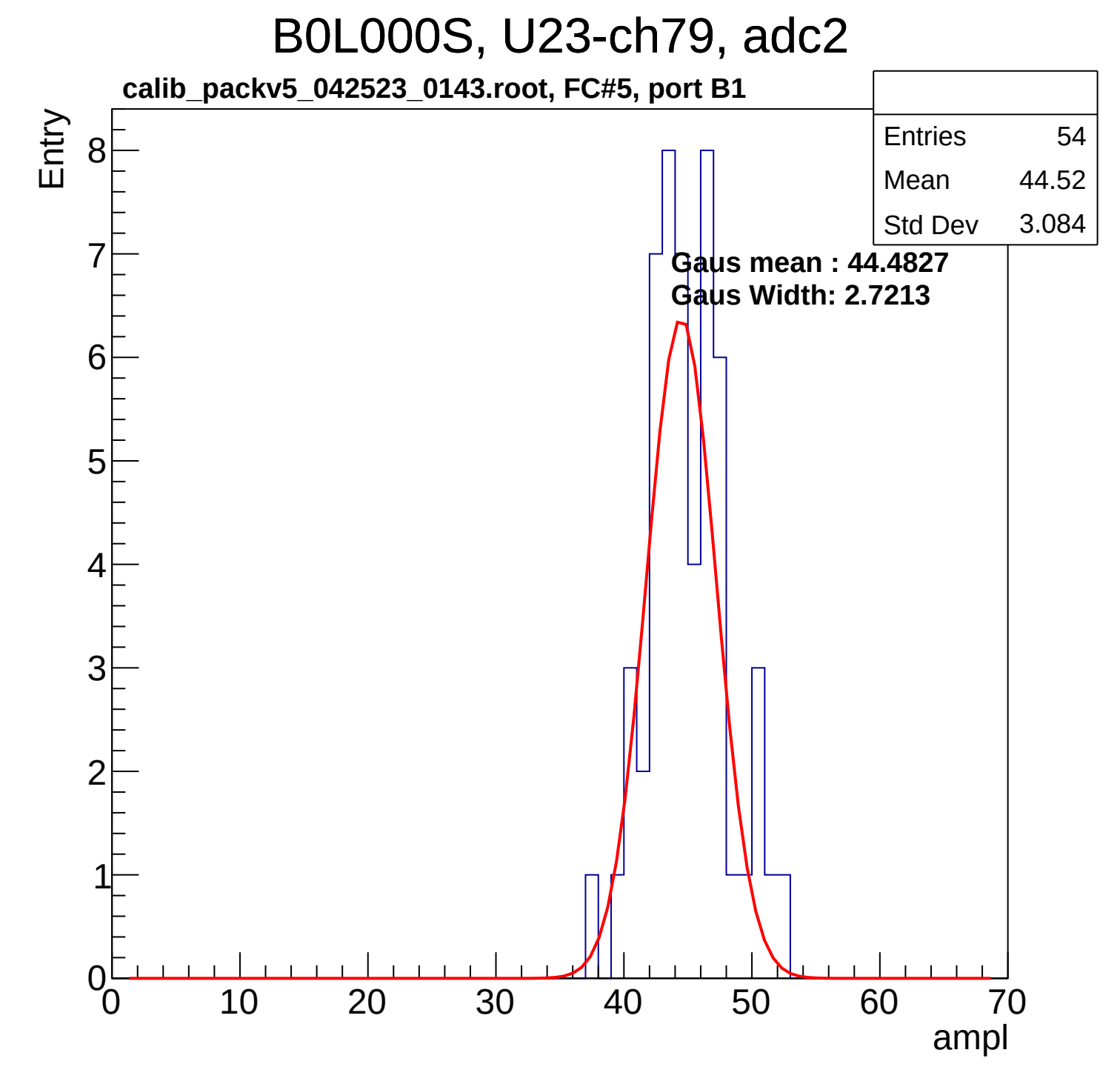

B0L000S, U23-ch78, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.74
Std Dev 2.593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

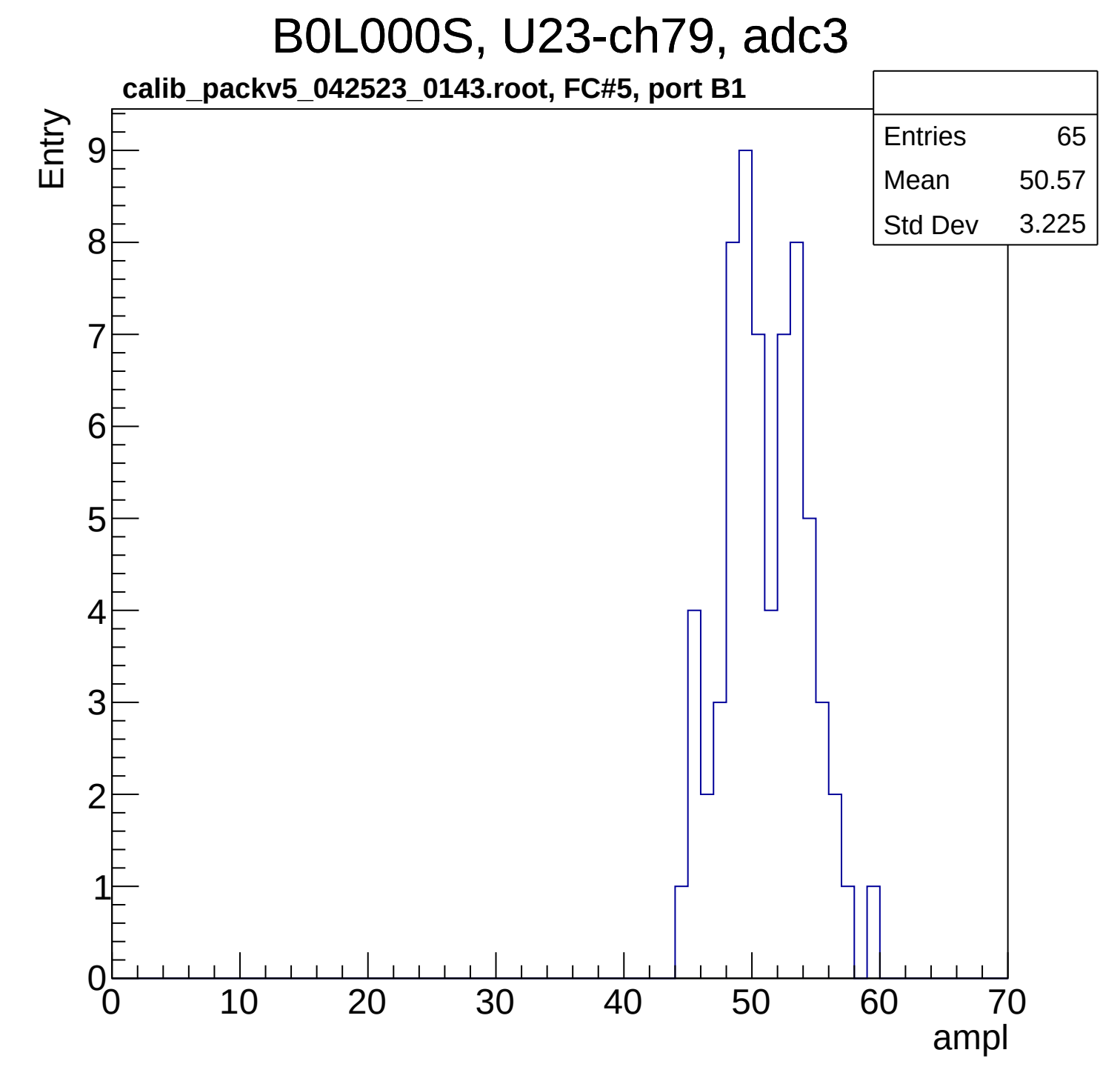

B0L000S, U23-ch78, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.33
Std Dev 13.15
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

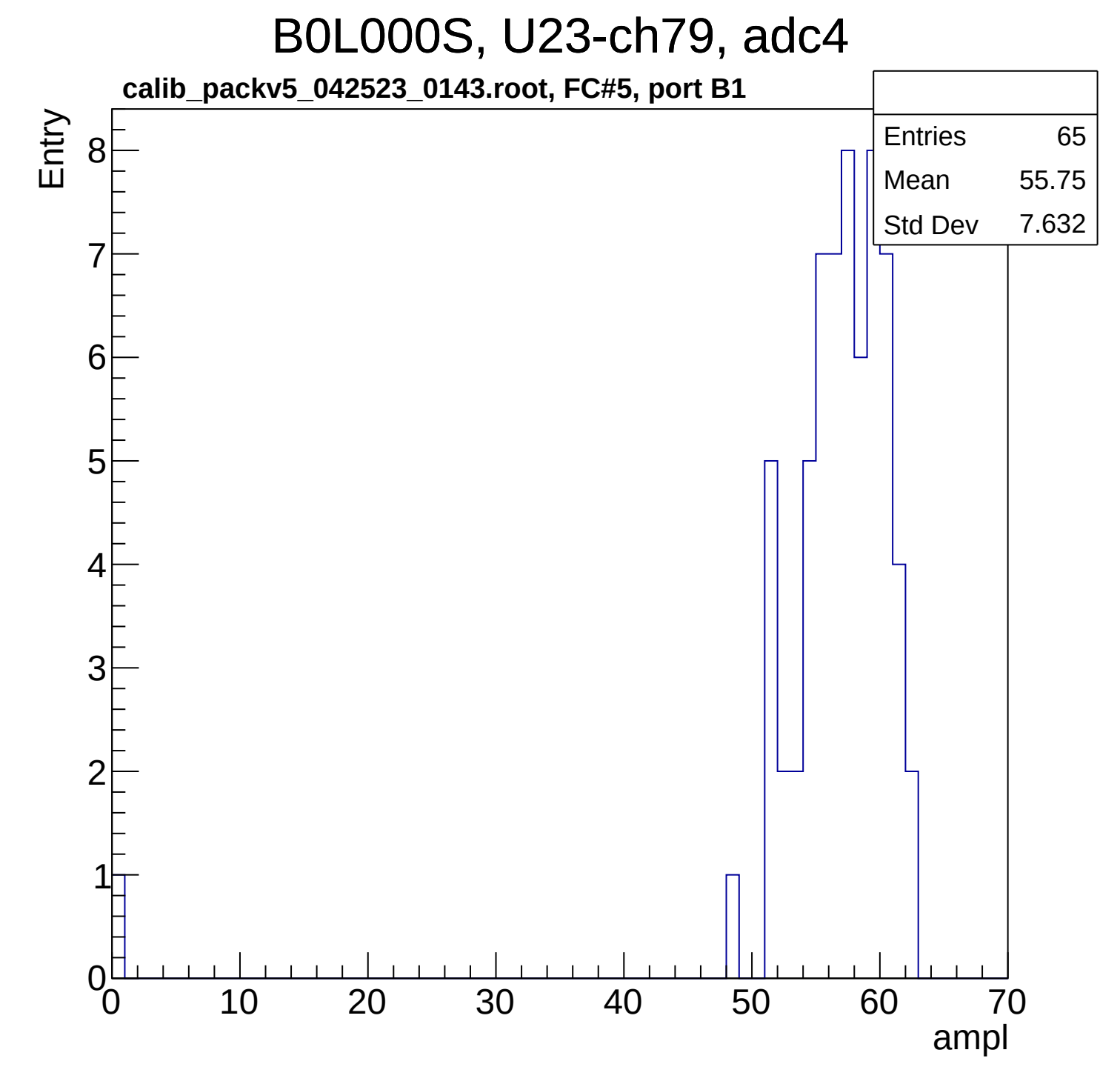

B0L000S, U23-ch78, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 42.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

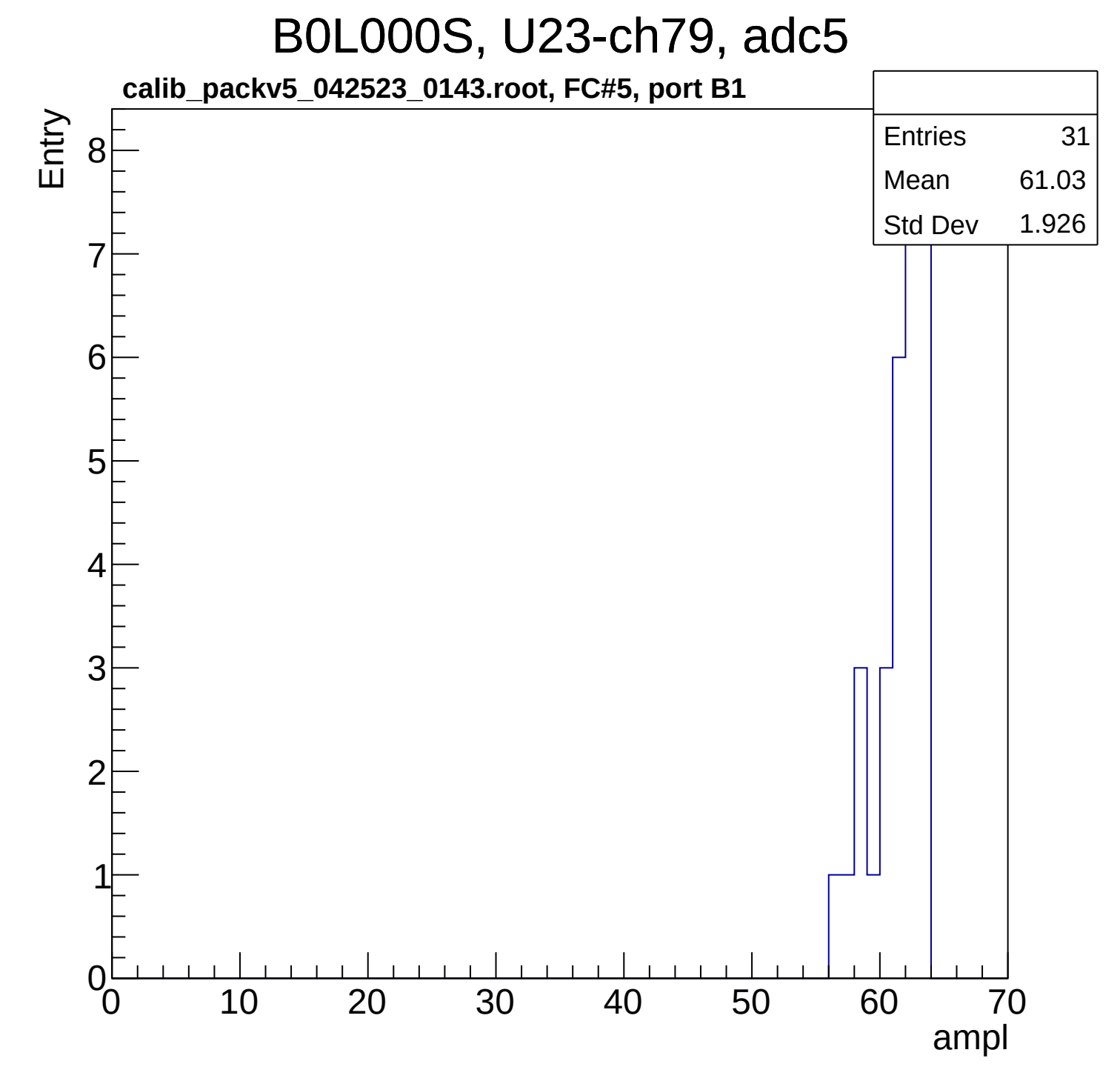

B0L000S, U23-ch79, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 32.49
Std Dev 3.844
Gaus mean : 32.3070
Gaus Width: 4.3567
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

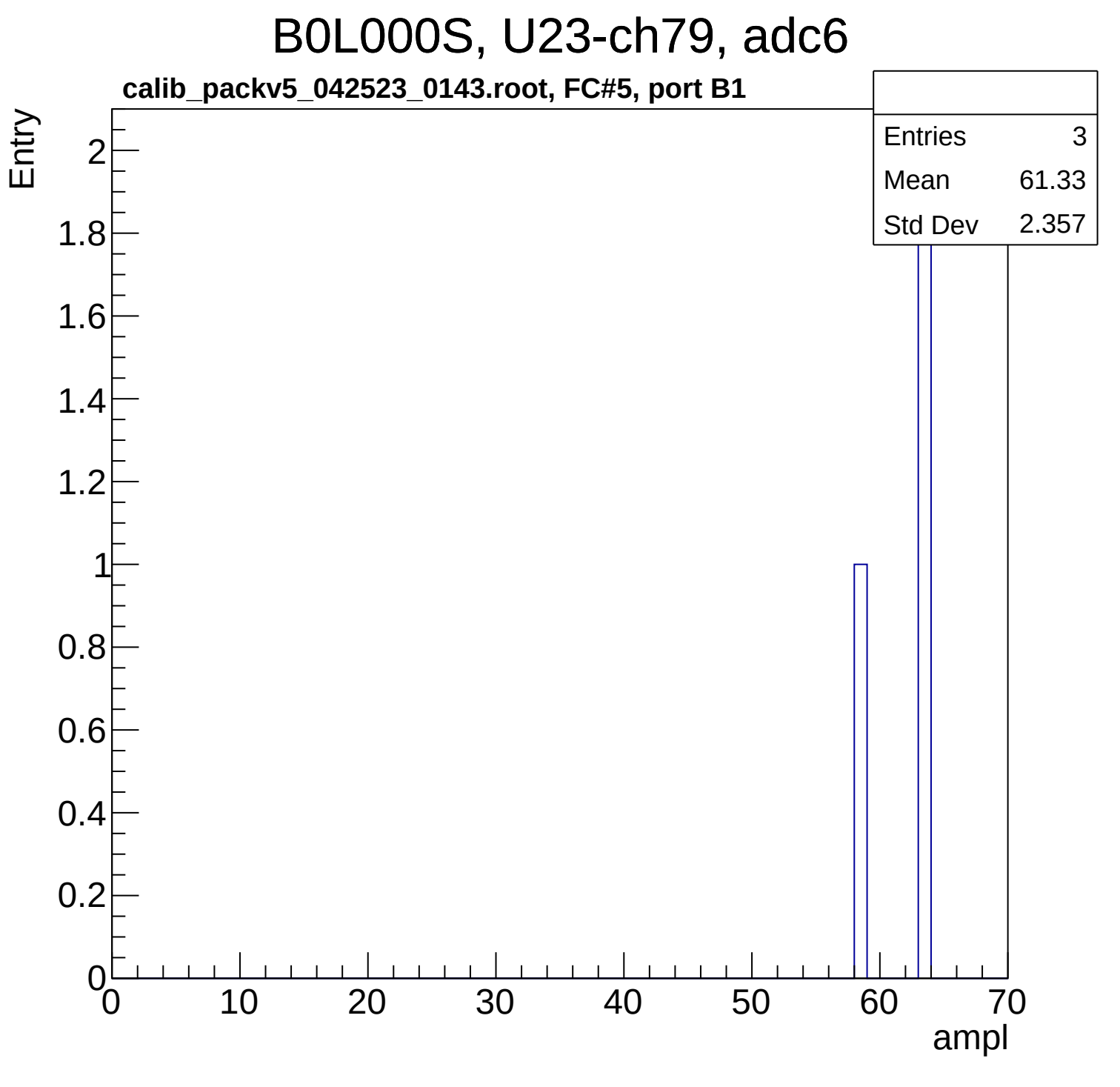

B0L000S, U23-ch79, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 38.2
Std Dev 3.42
Gaus mean : 38.5518
Gaus Width: 3.6433
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

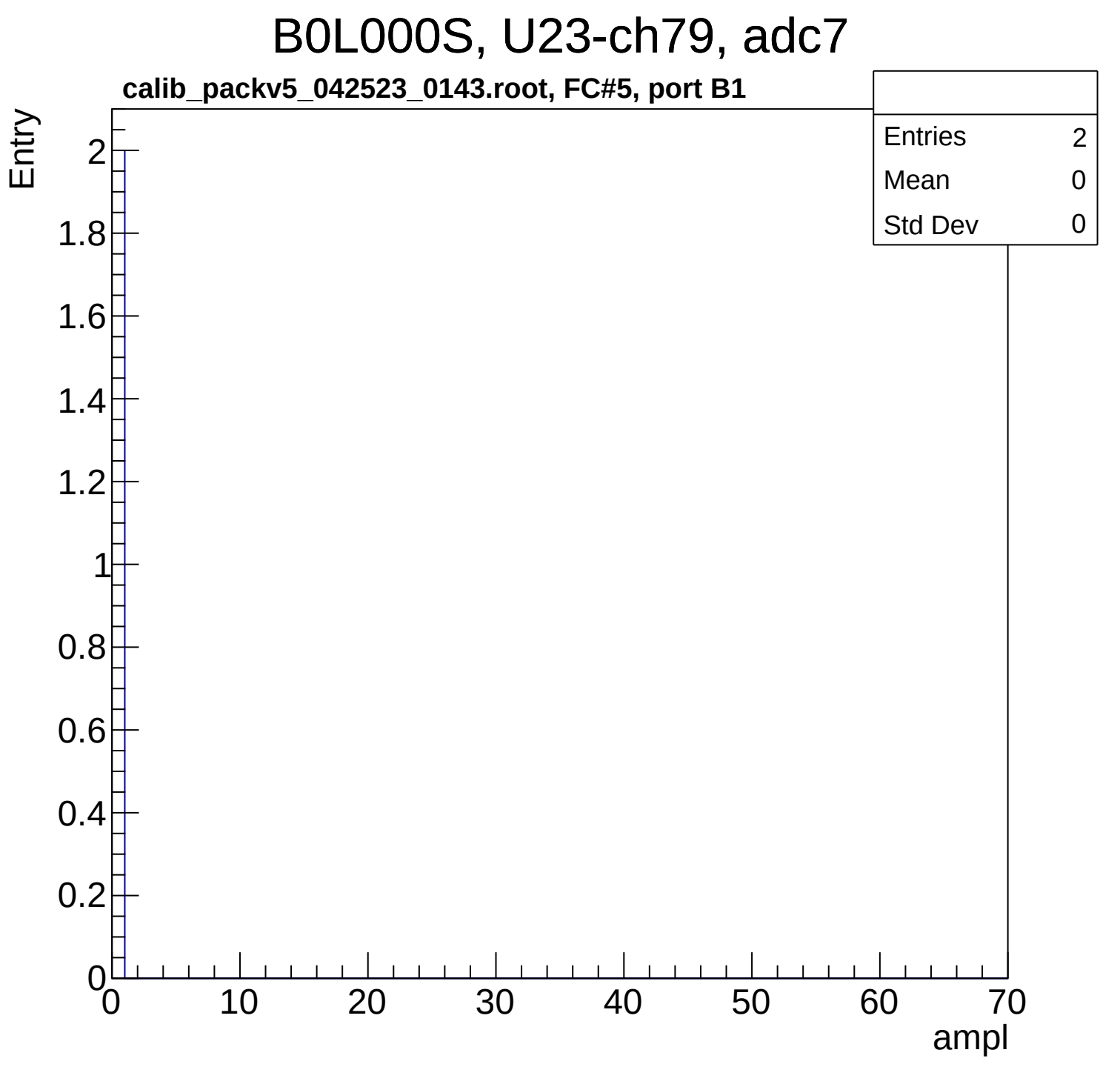

B0L000S, U23-ch79, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 44.52
Std Dev 3.084
Gaus mean : 44.4827
Gaus Width: 2.7213
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

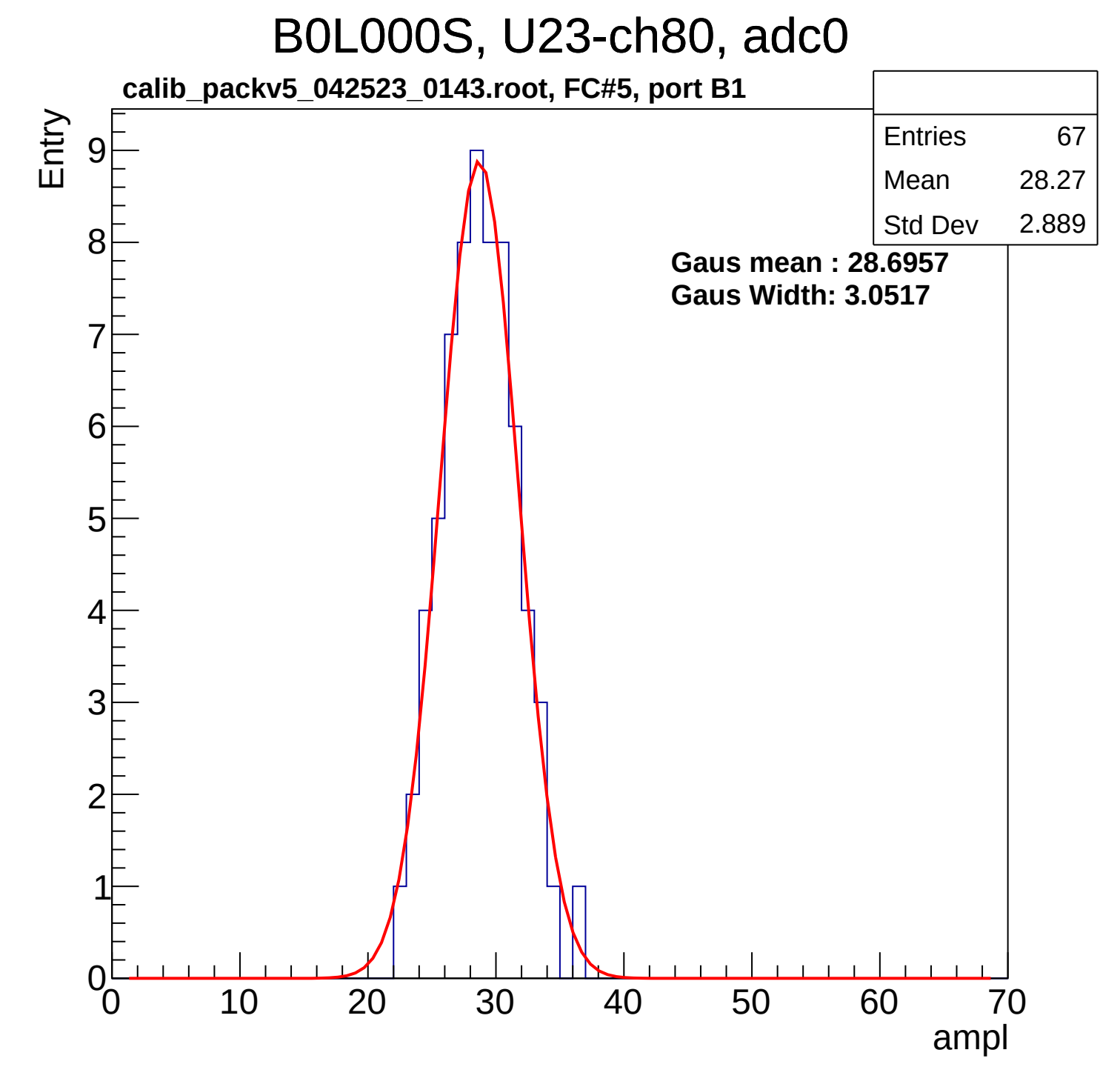

B0L000S, U23-ch79, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.57
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

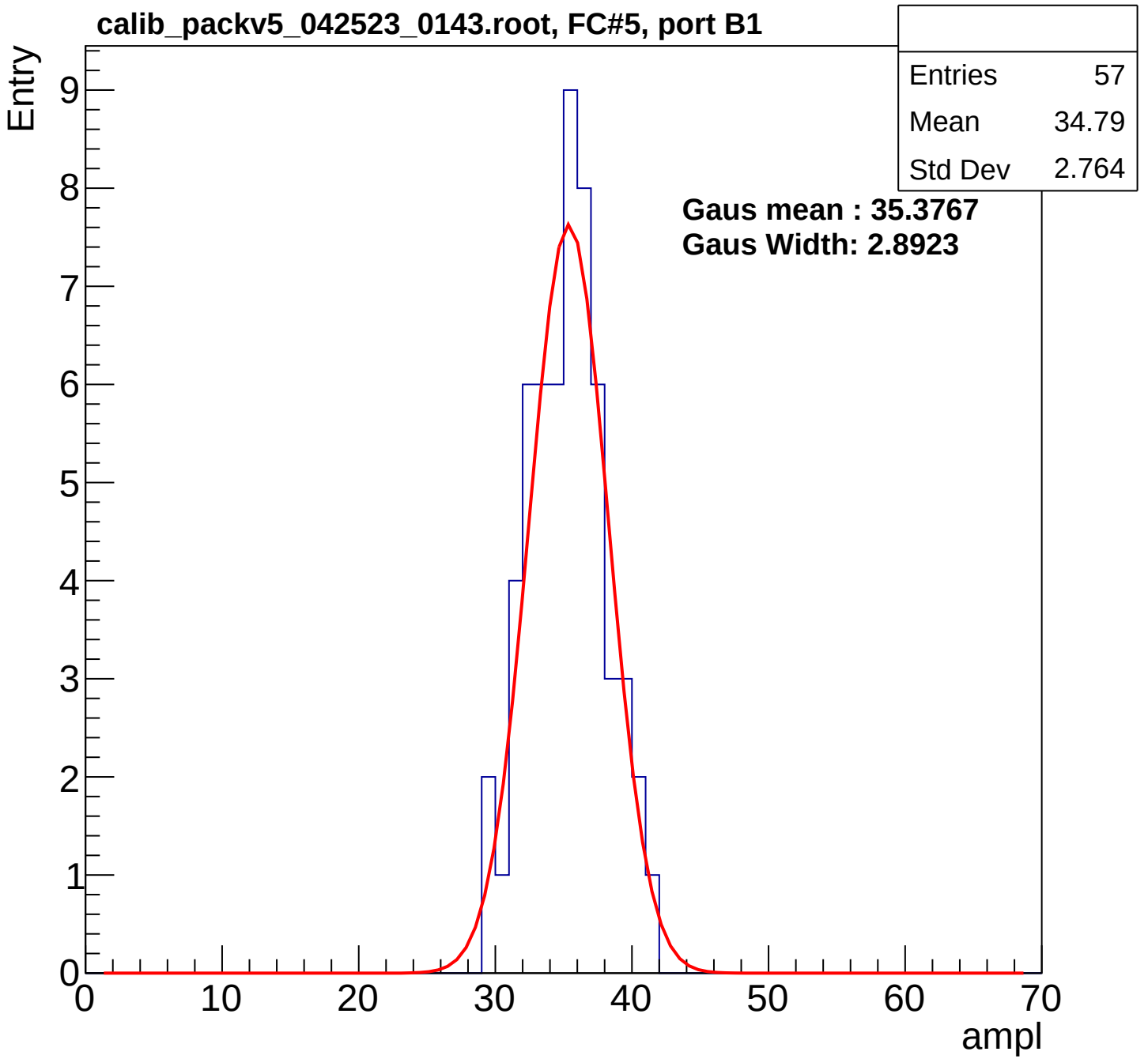

B0L000S, U23-ch79, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 55.75
Std Dev 7.632
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

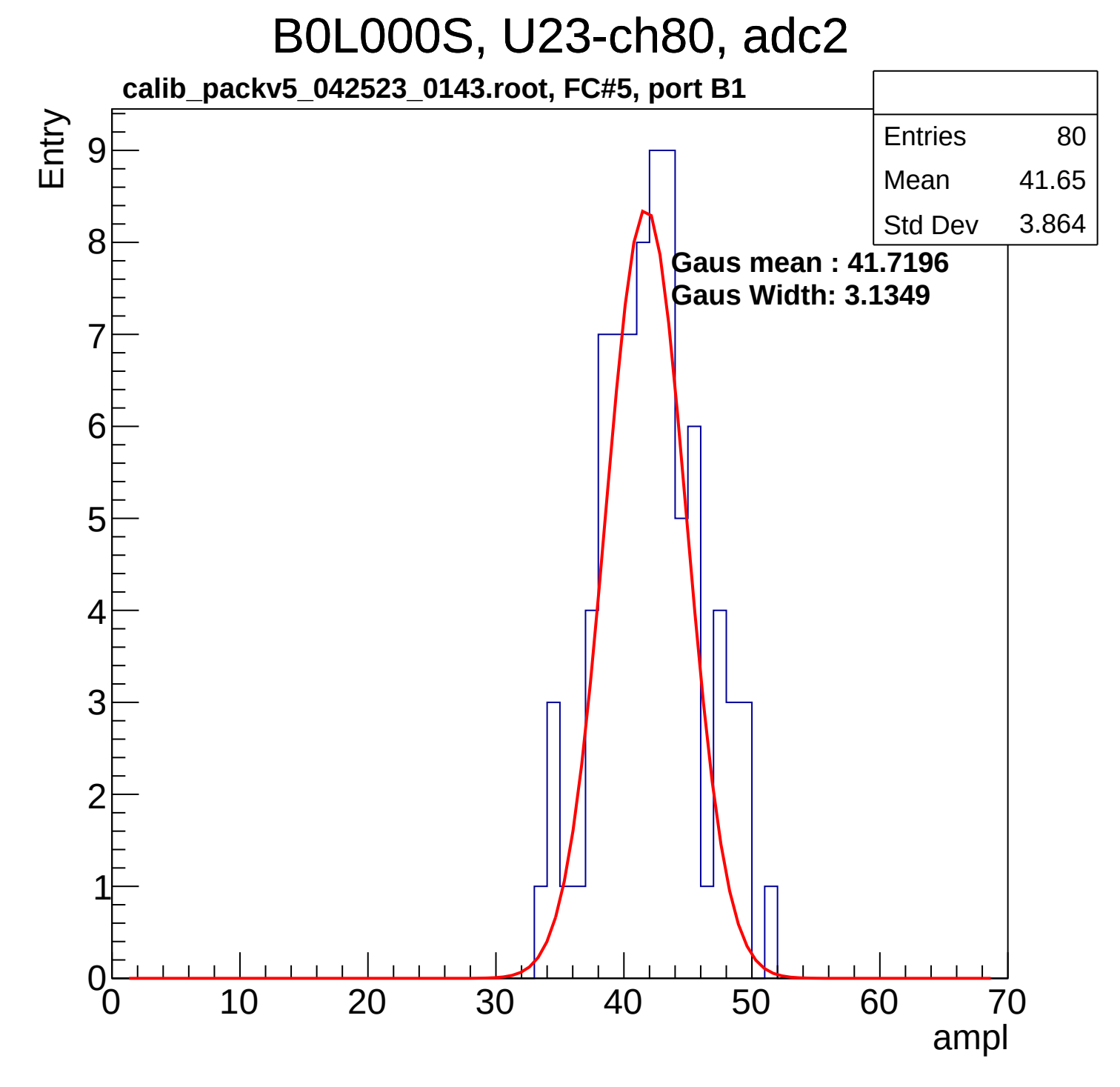

B0L000S, U23-ch79, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 61.03
Std Dev 1.926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

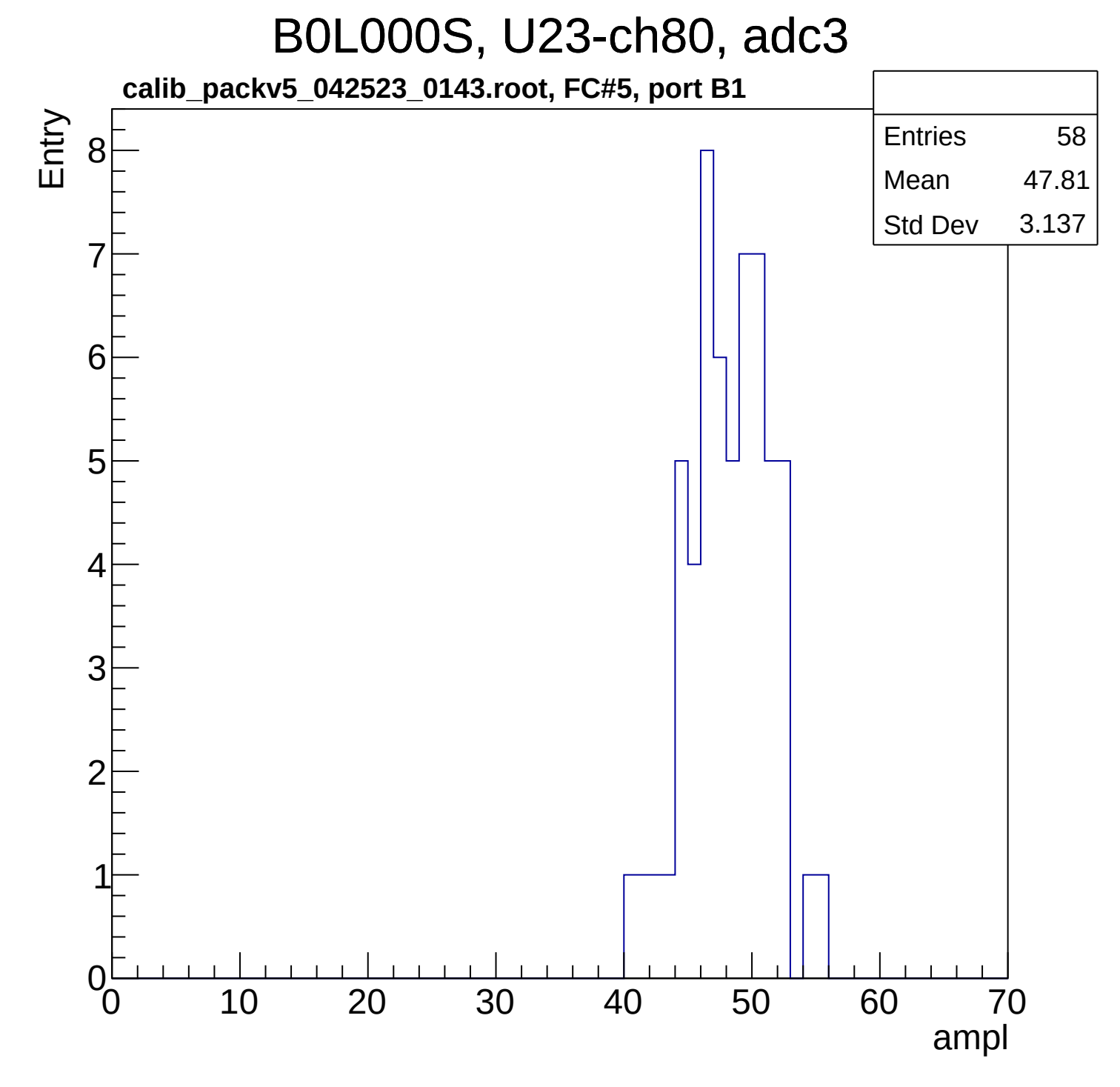

B0L000S, U23-ch79, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.33
Std Dev 2.357
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

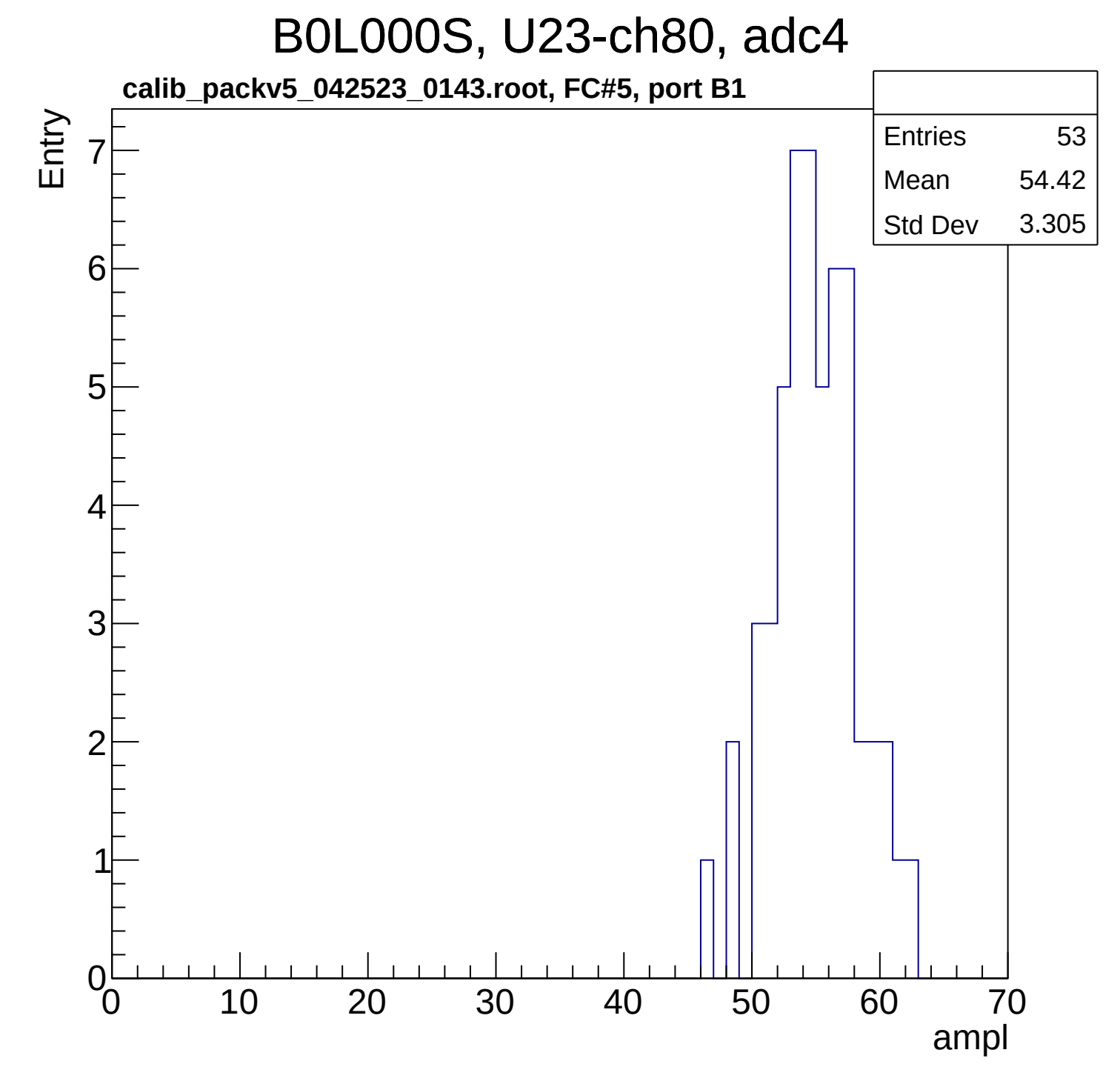

B0L000S, U23-ch79, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

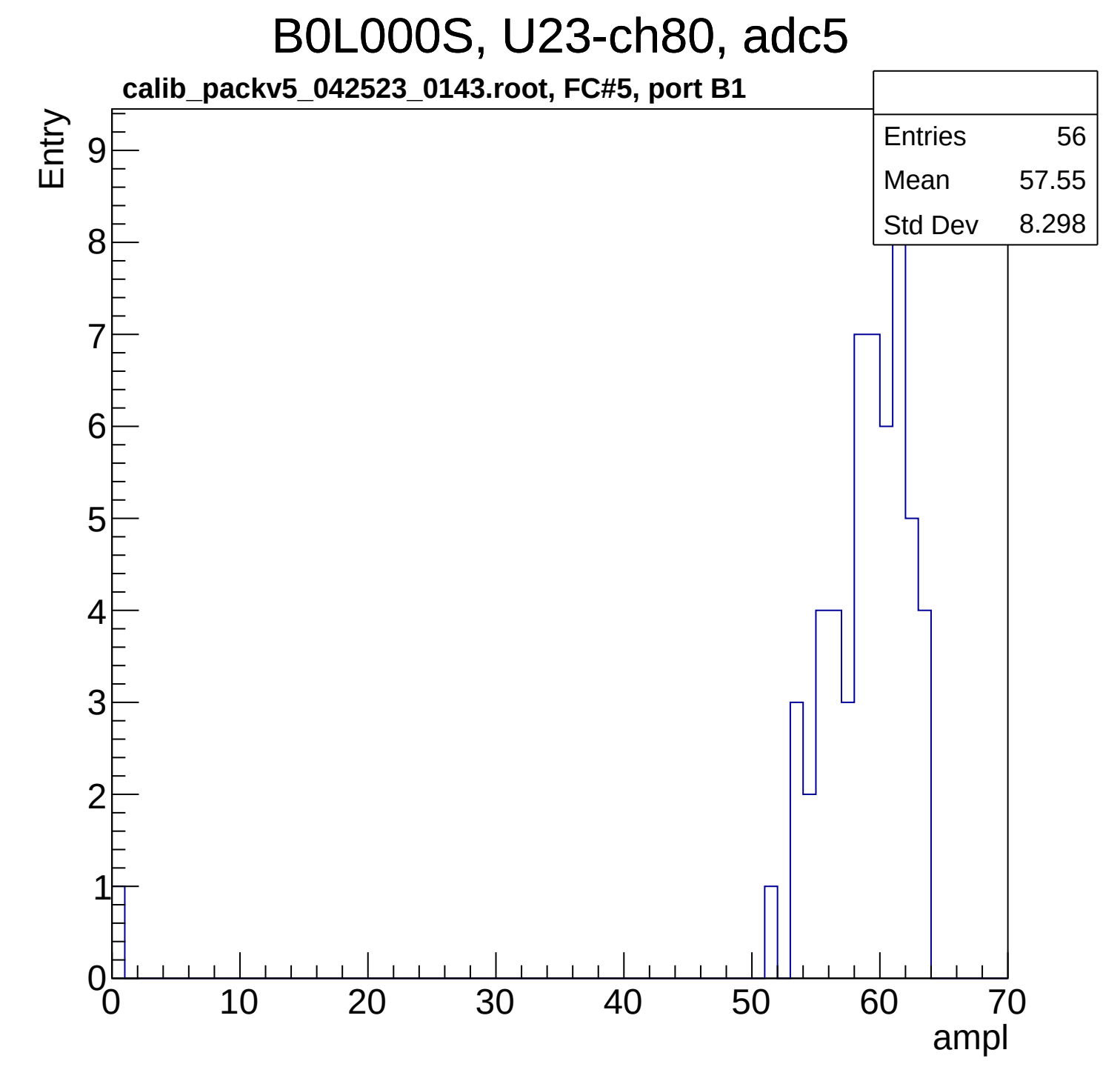

B0L000S, U23-ch80, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 28.27
Std Dev 2.889
Gaus mean : 28.6957
Gaus Width: 3.0517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

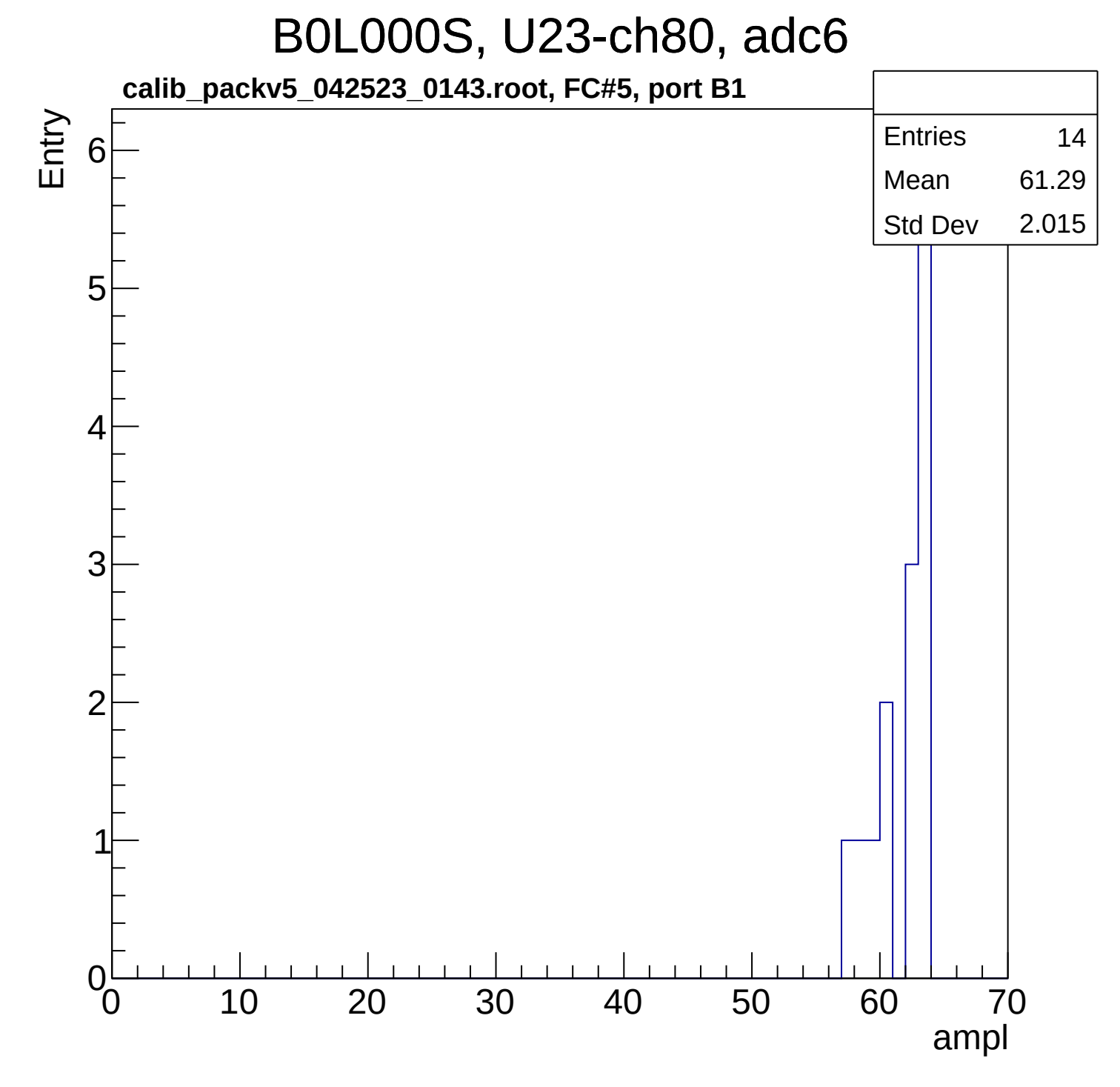

B0L000S, U23-ch80, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 34.79
Std Dev 2.764
Gaus mean : 35.3767
Gaus Width: 2.8923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

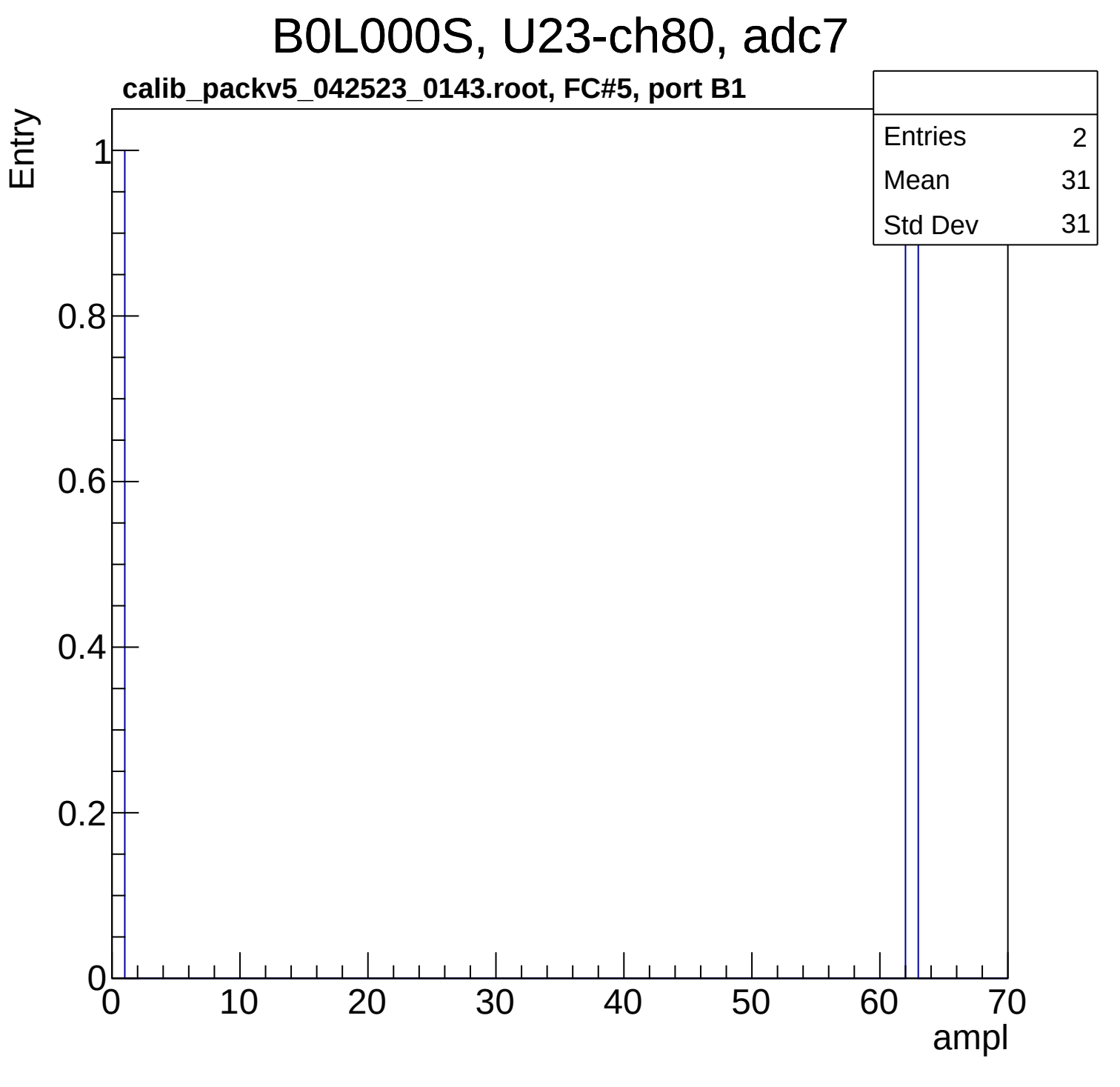

B0L000S, U23-ch80, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 41.65
Std Dev 3.864
Gaus mean : 41.7196
Gaus Width: 3.1349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

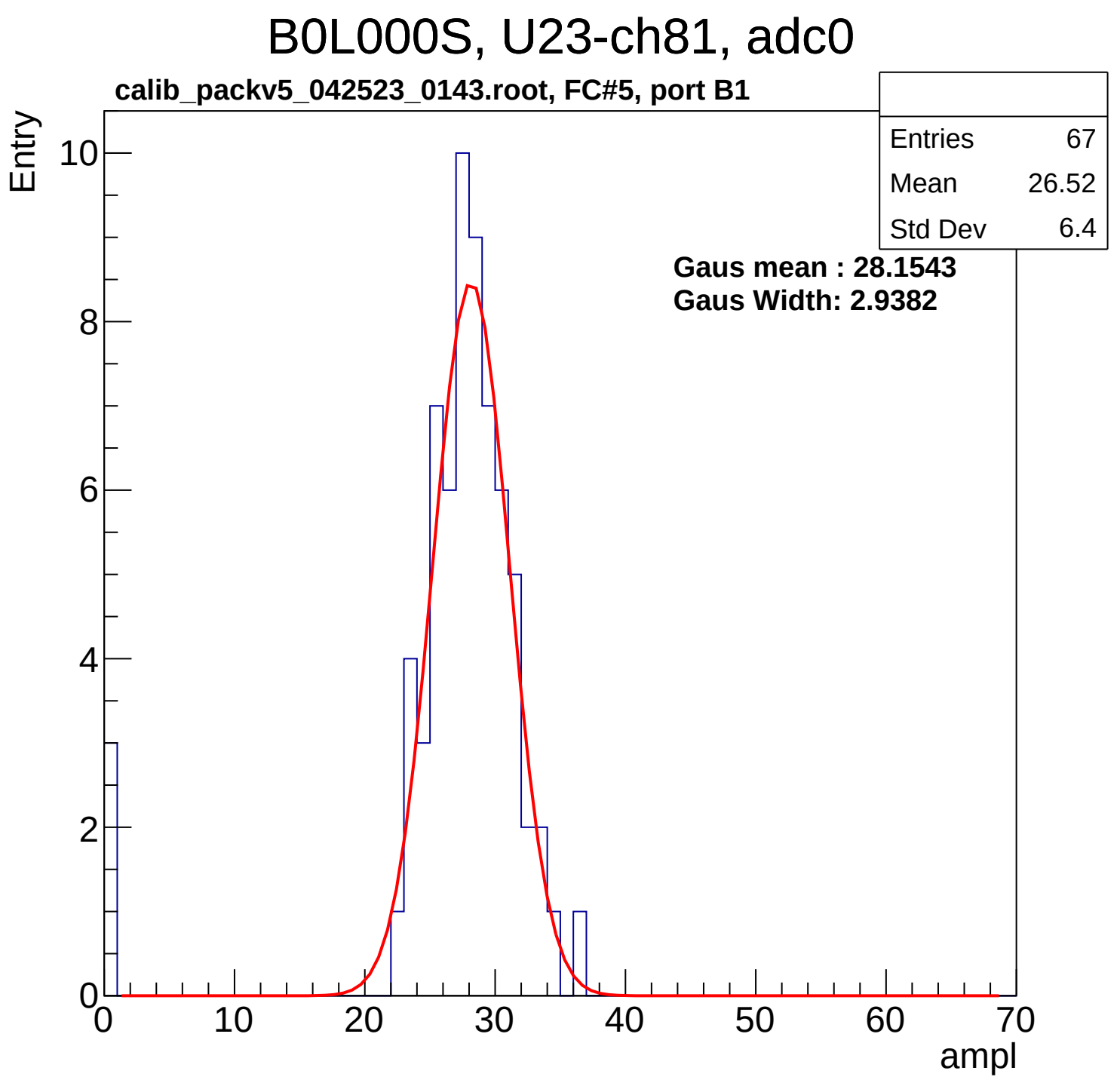

B0L000S, U23-ch80, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 47.81
Std Dev 3.137
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

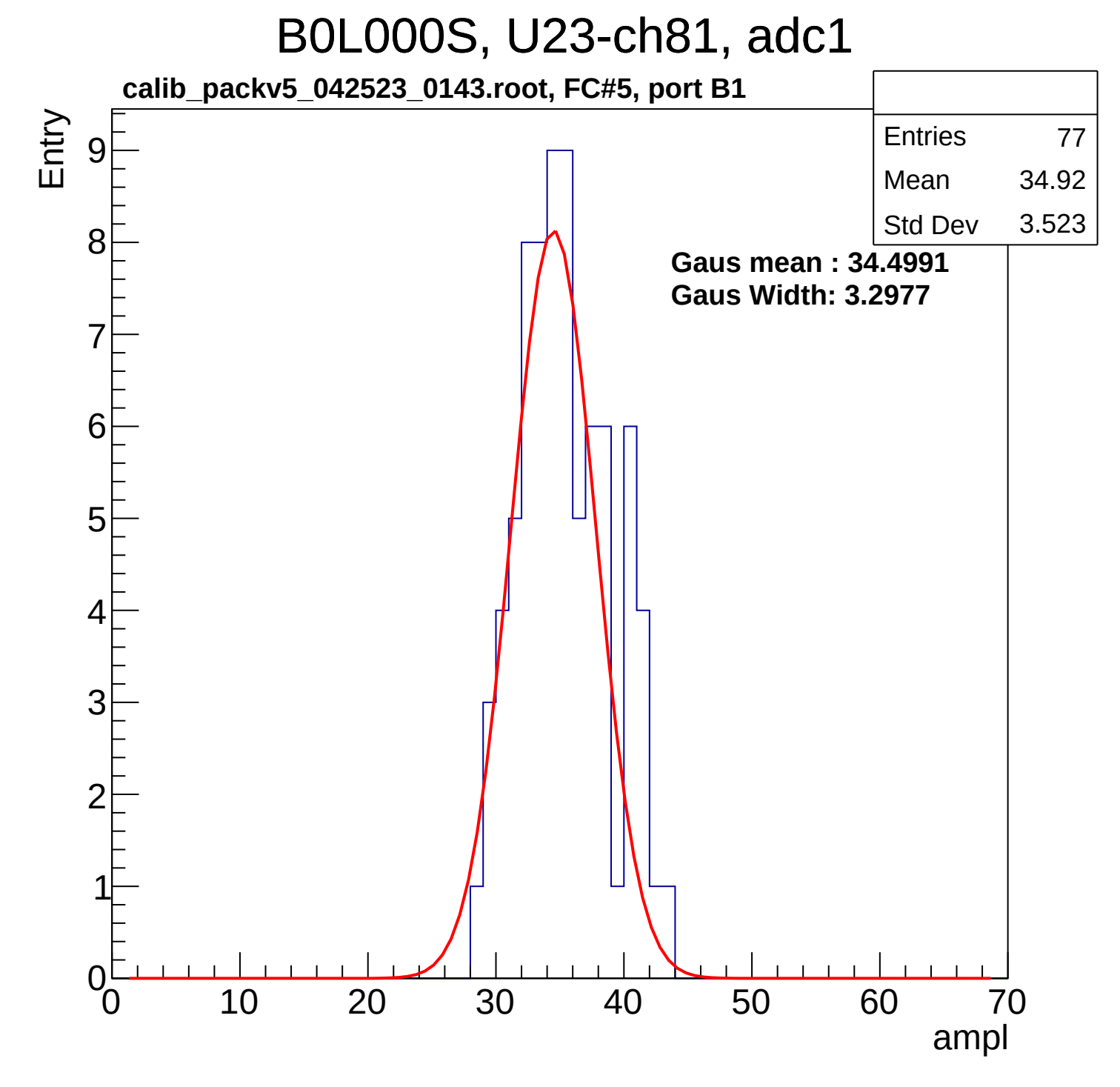

B0L000S, U23-ch80, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.42
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

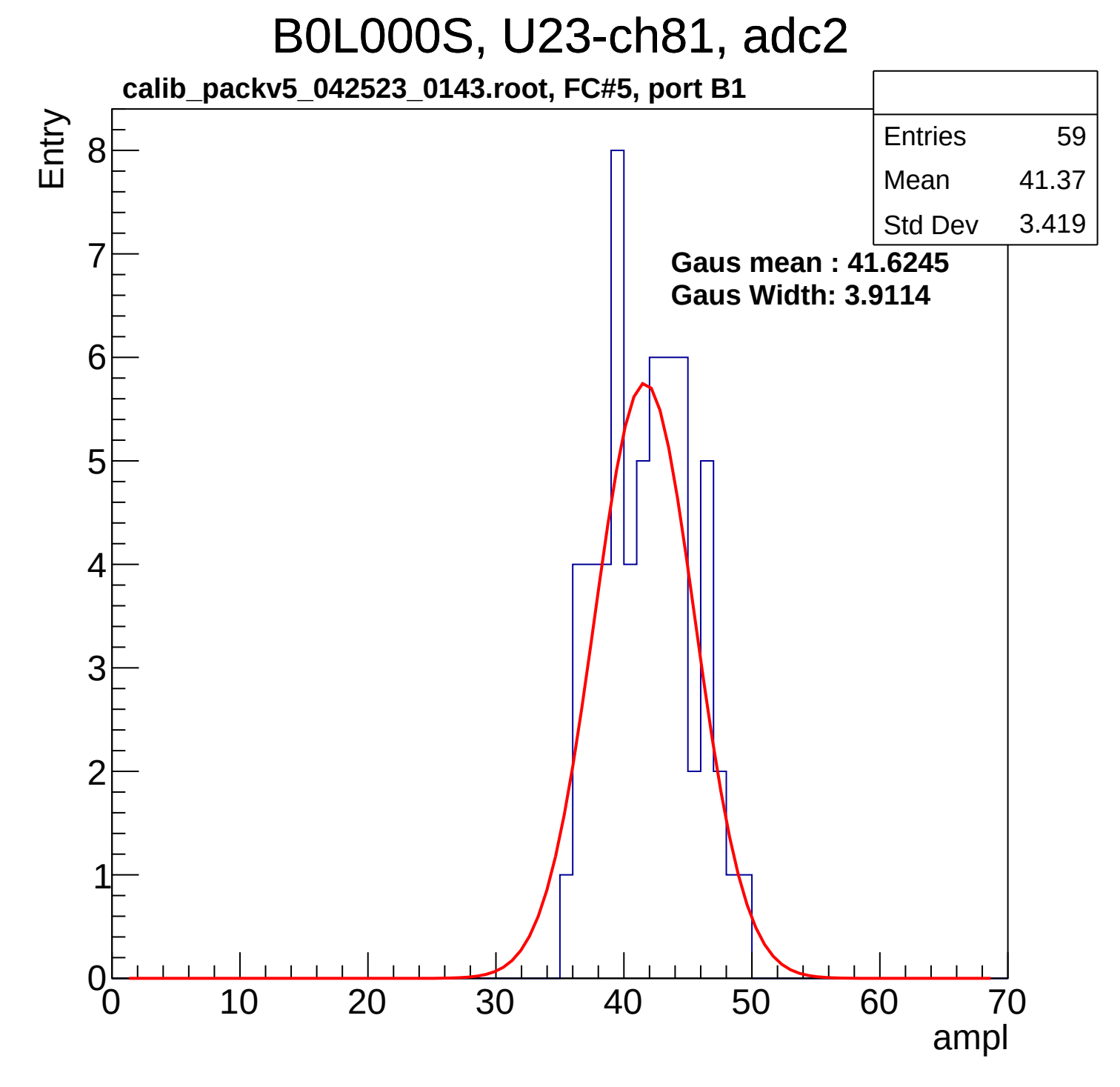

B0L000S, U23-ch80, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.55
Std Dev 8.298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

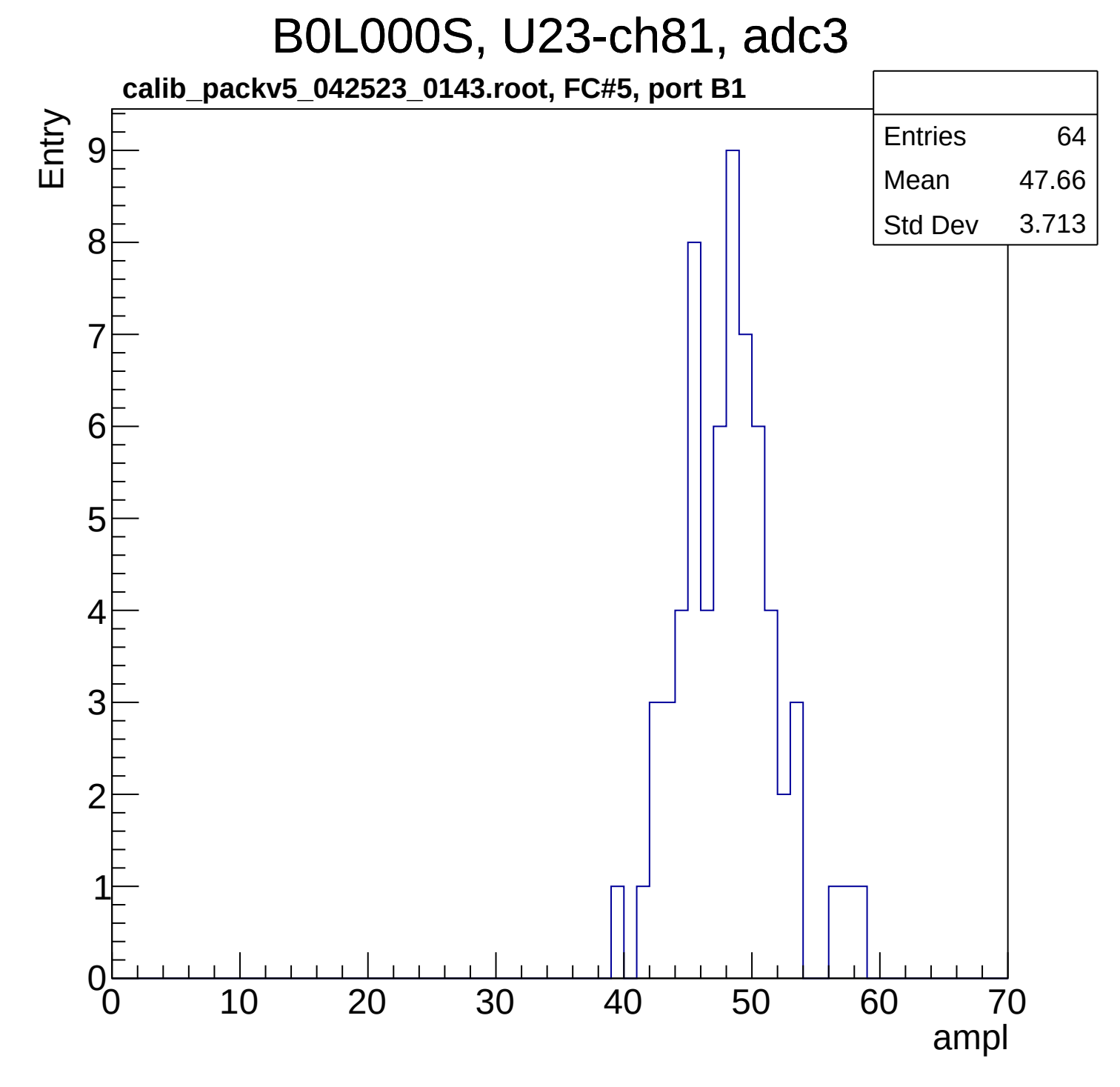

B0L000S, U23-ch80, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.29
Std Dev 2.015
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

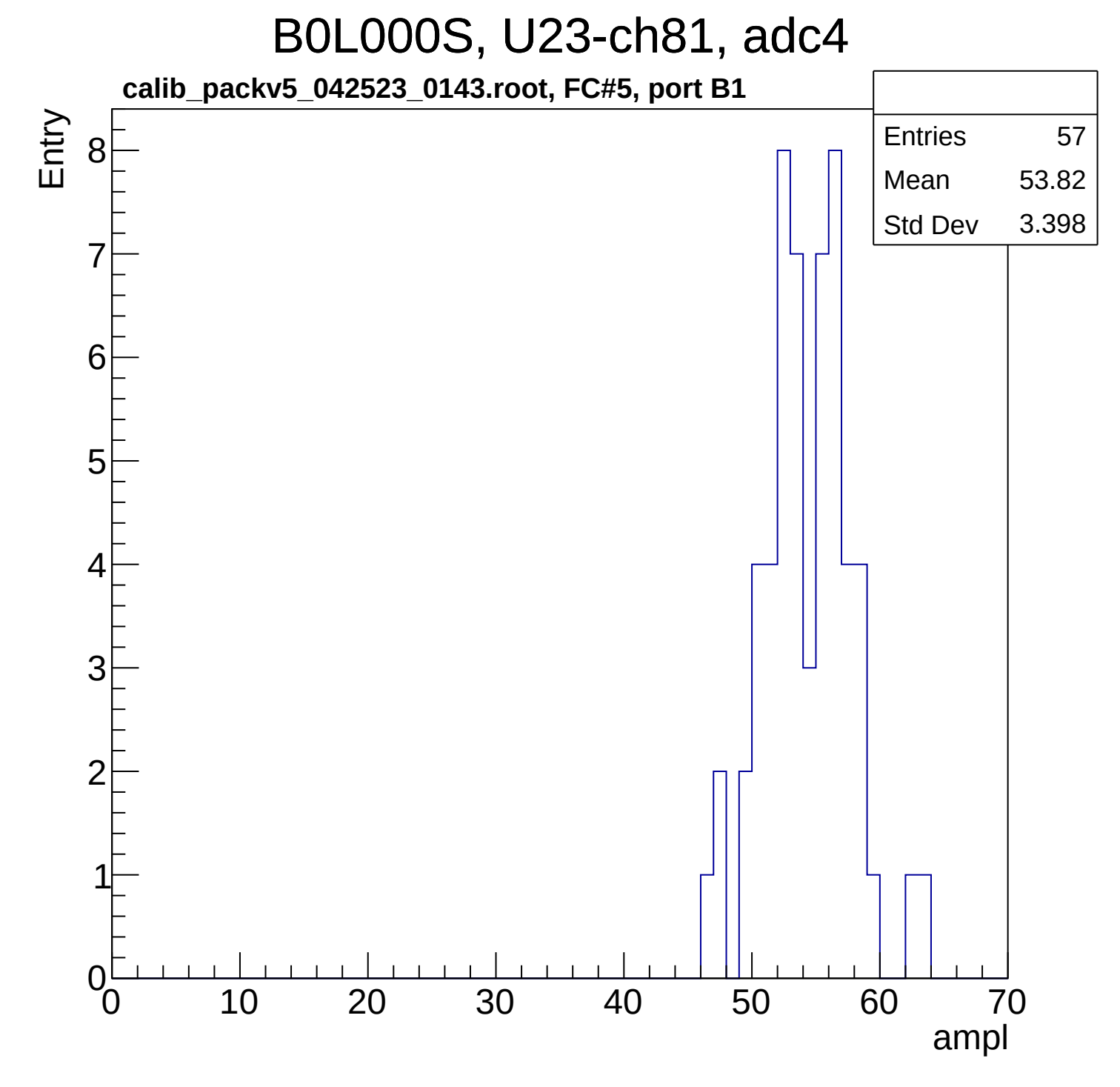

B0L000S, U23-ch80, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

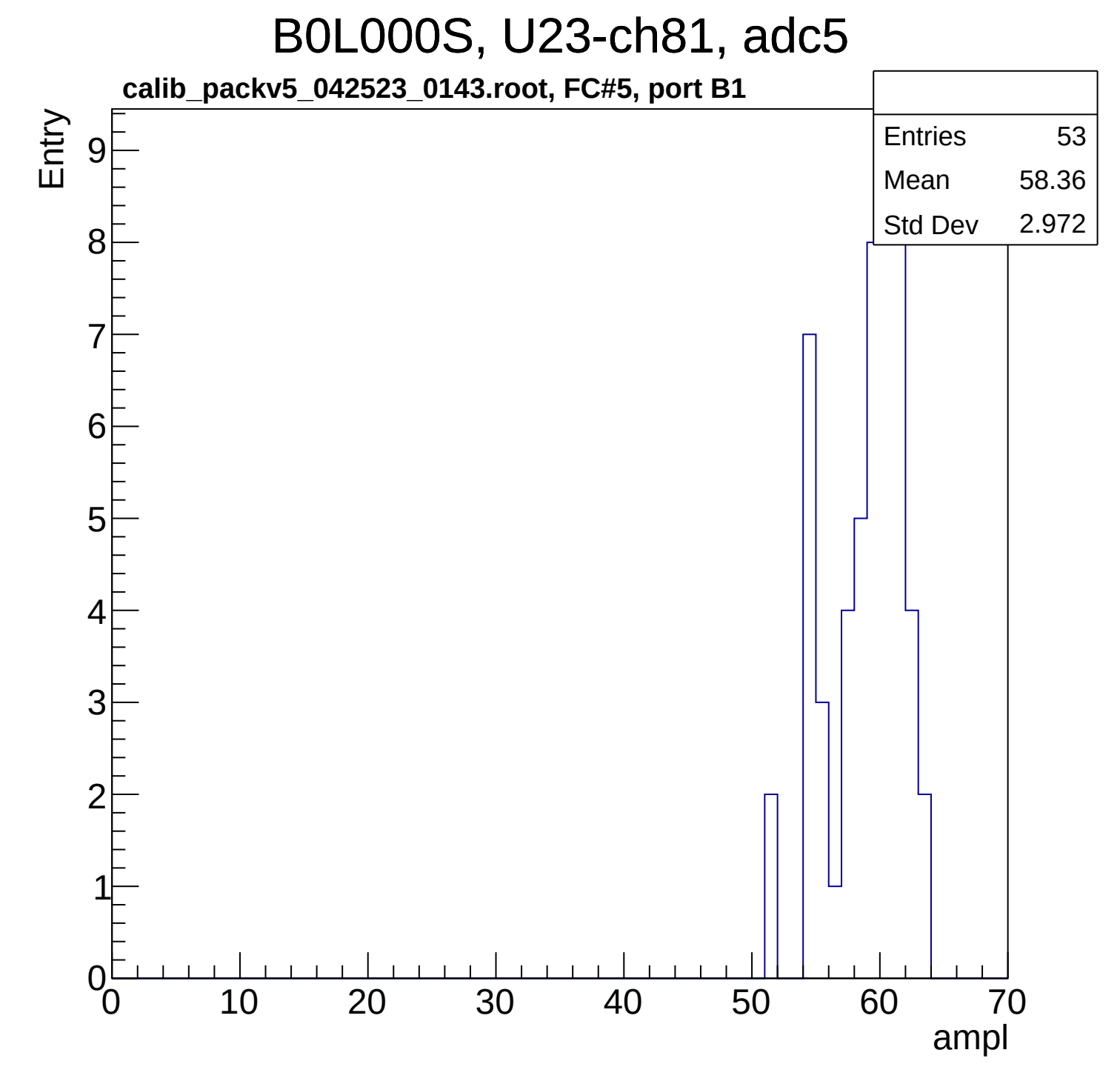

B0L000S, U23-ch81, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 26.52
Std Dev 6.4
Gaus mean : 28.1543
Gaus Width: 2.9382
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

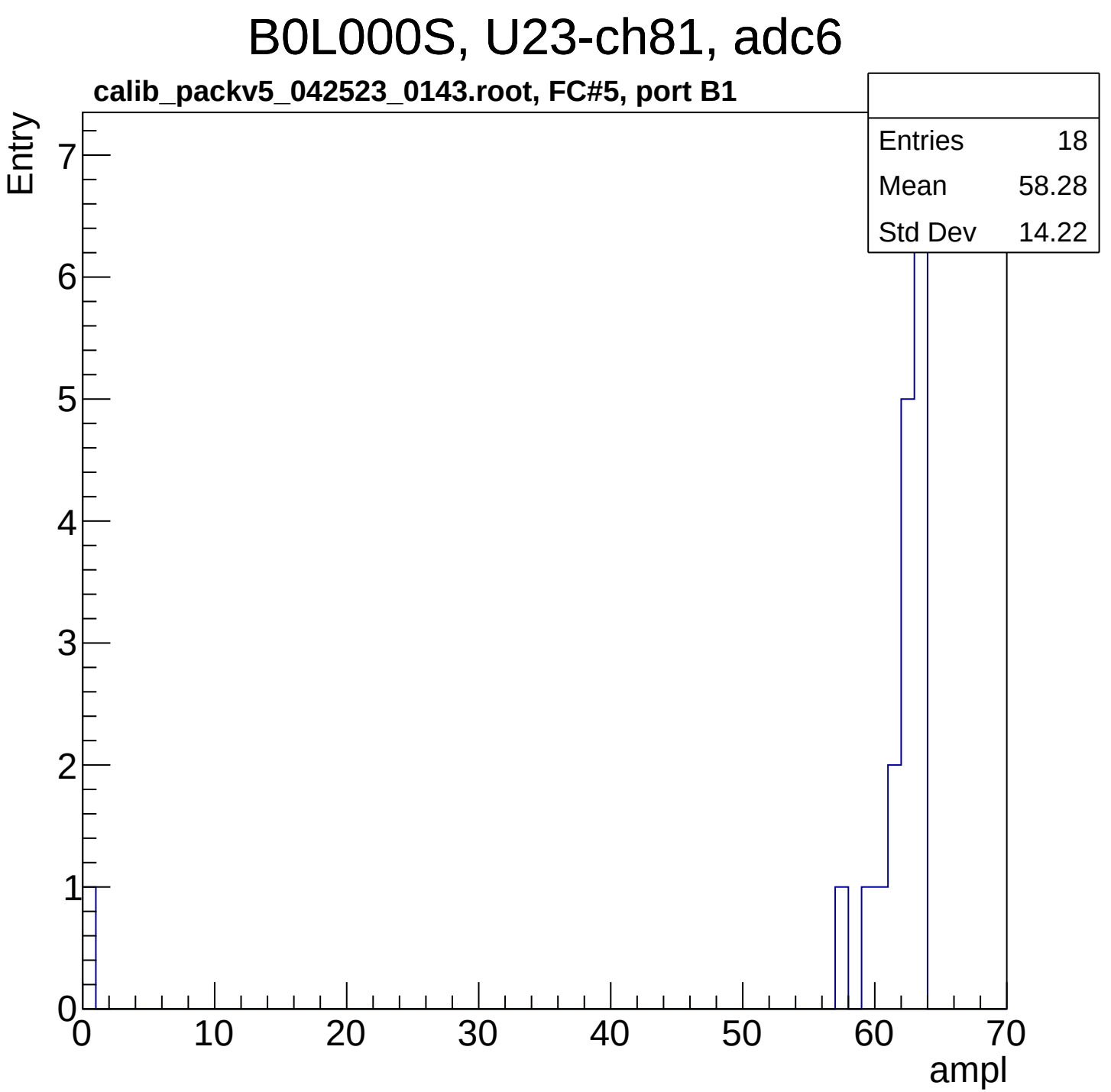

B0L000S, U23-ch81, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 34.92
Std Dev 3.523
Gaus mean : 34.4991
Gaus Width: 3.2977
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

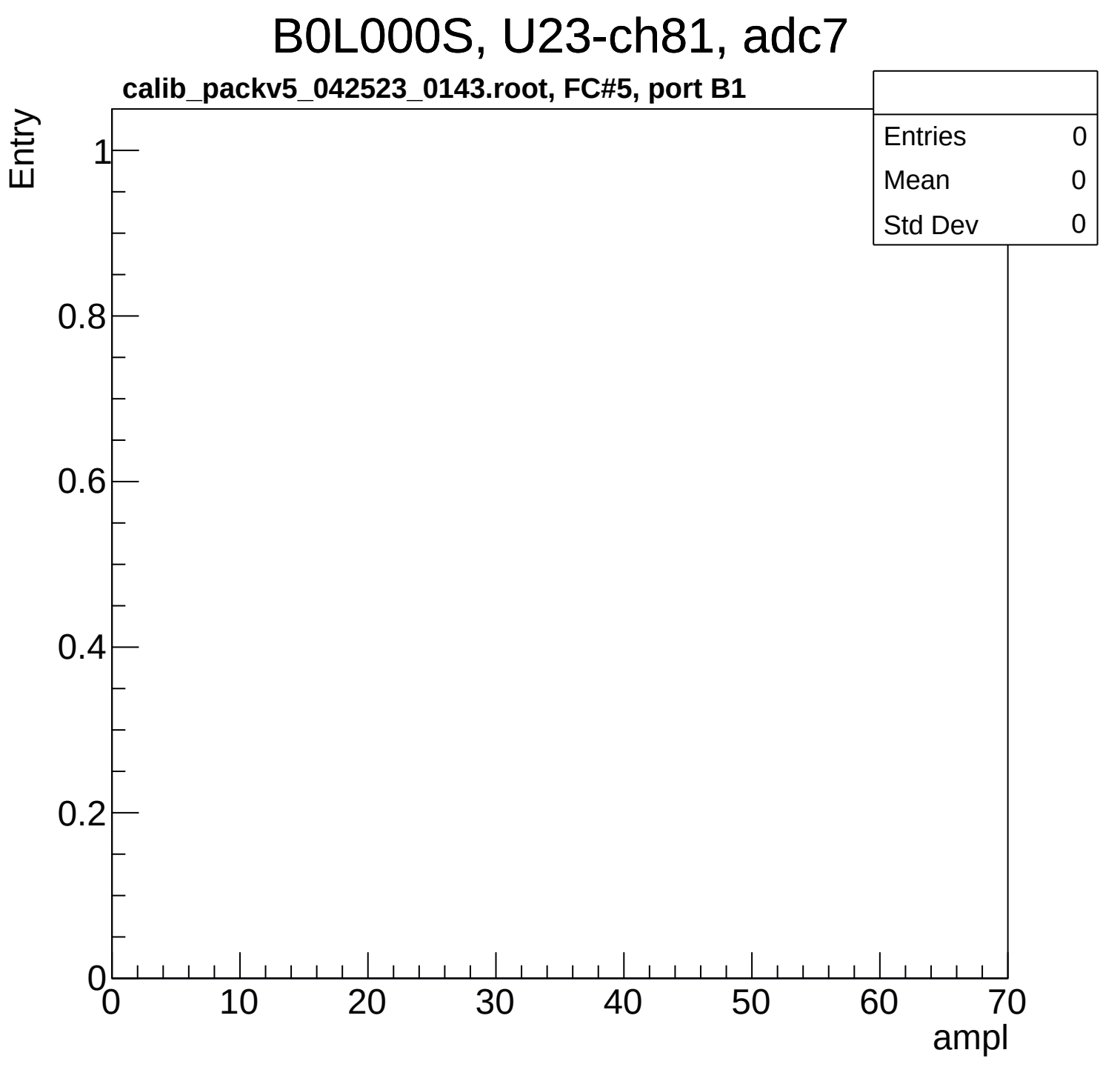

B0L000S, U23-ch81, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 41.37
Std Dev 3.419
Gaus mean : 41.6245
Gaus Width: 3.9114
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

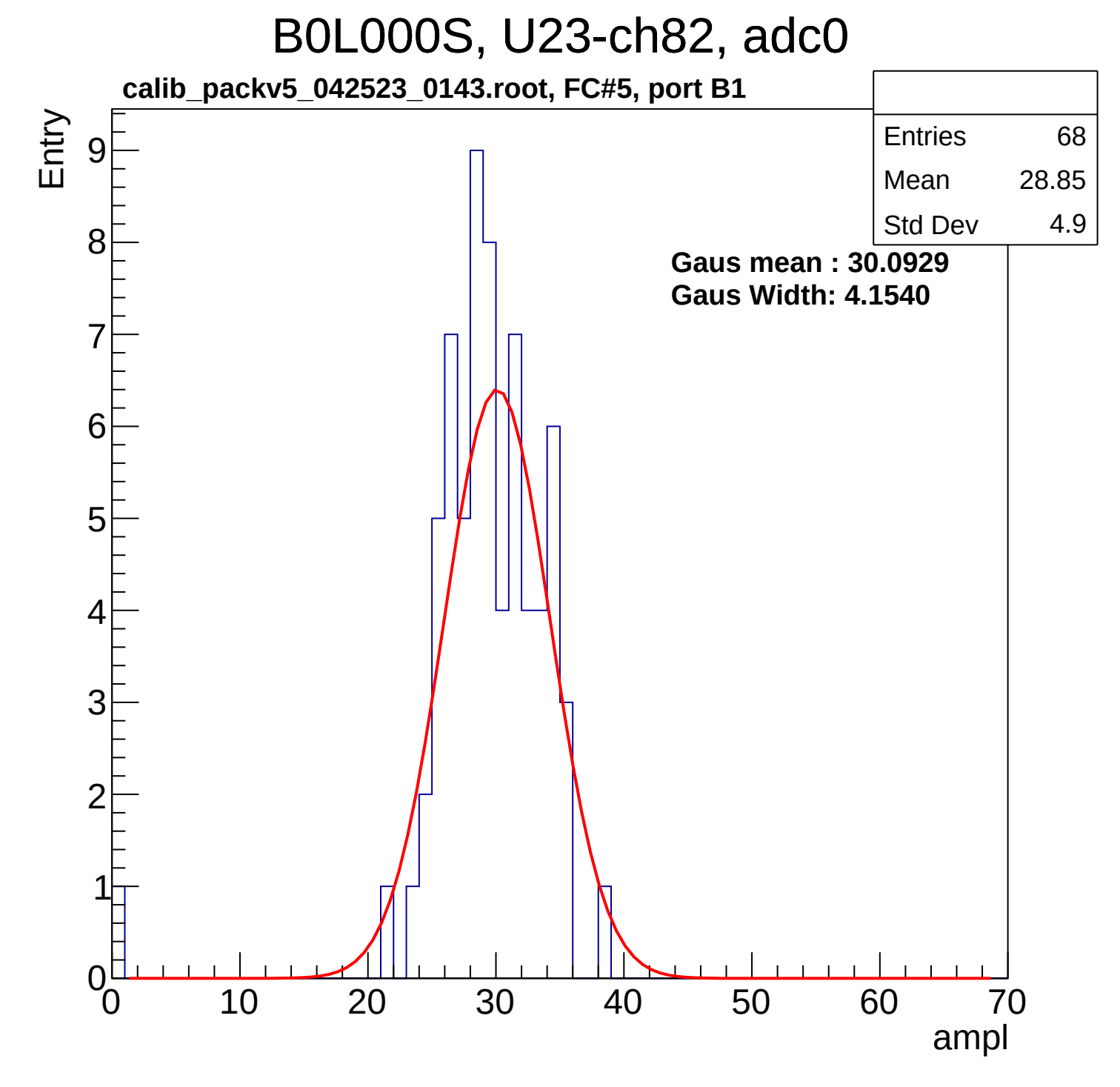

B0L000S, U23-ch81, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.66
Std Dev 3.713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

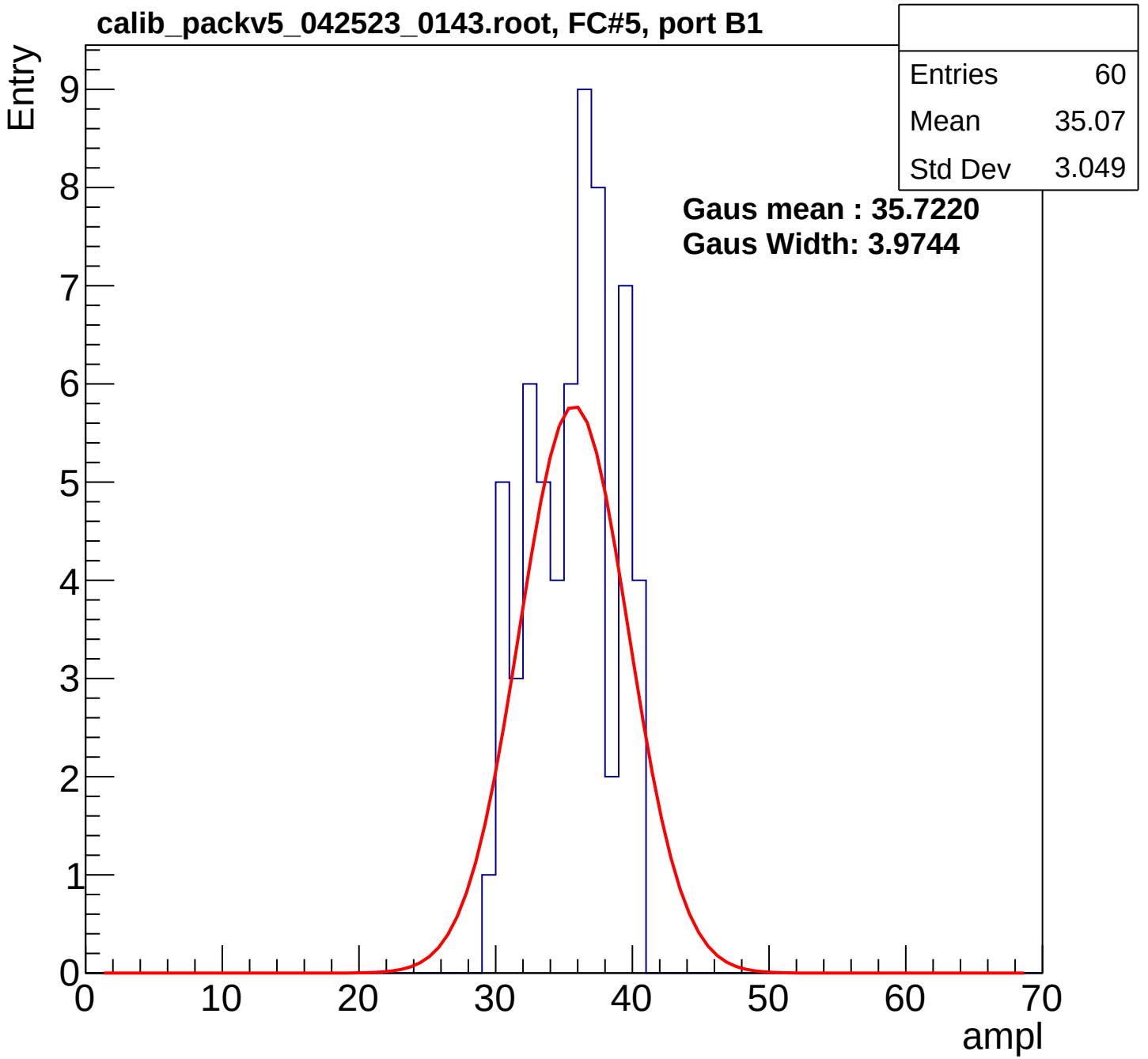

B0L000S, U23-ch81, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 53.82
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

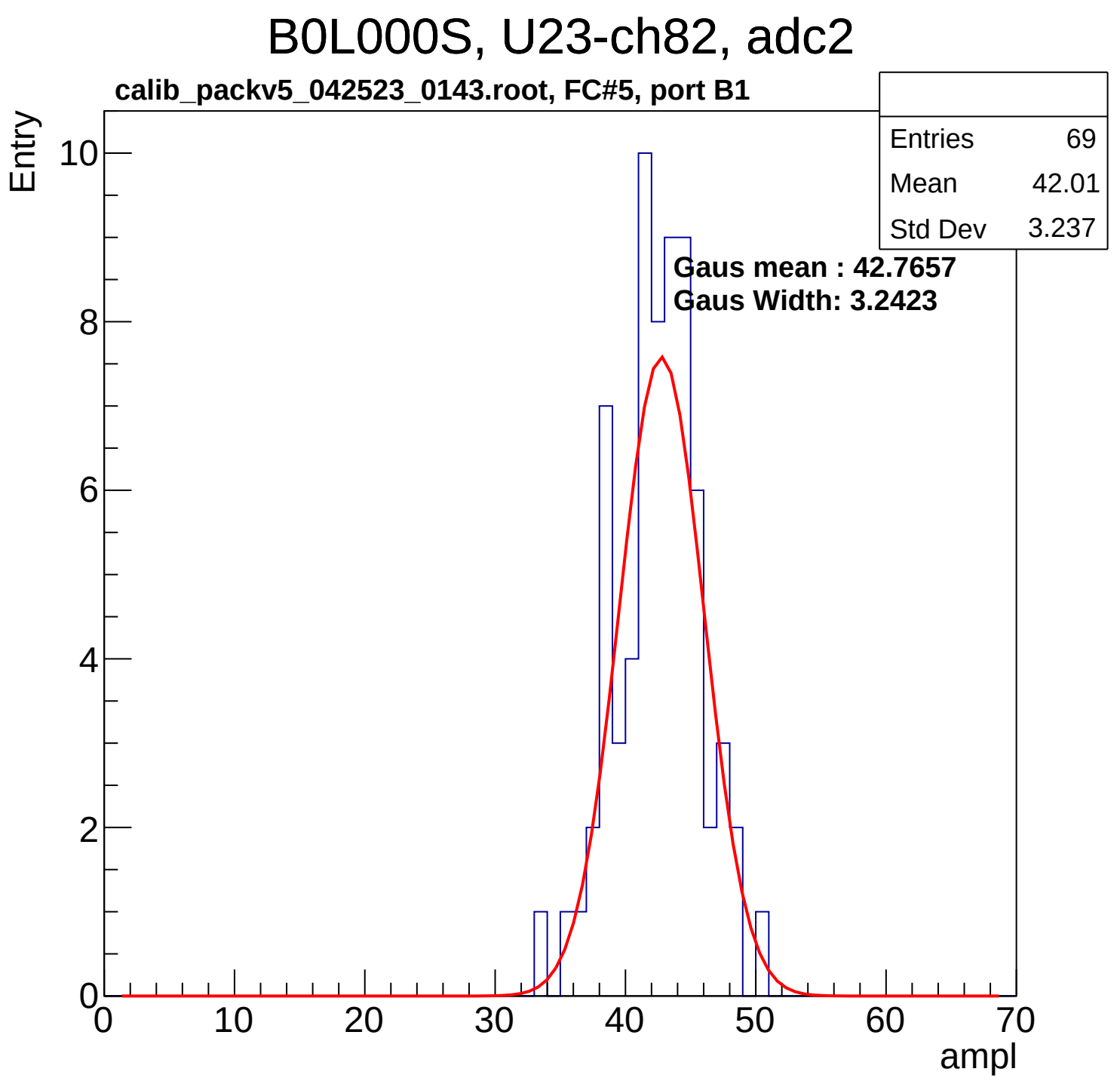

B0L000S, U23-ch81, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.36
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

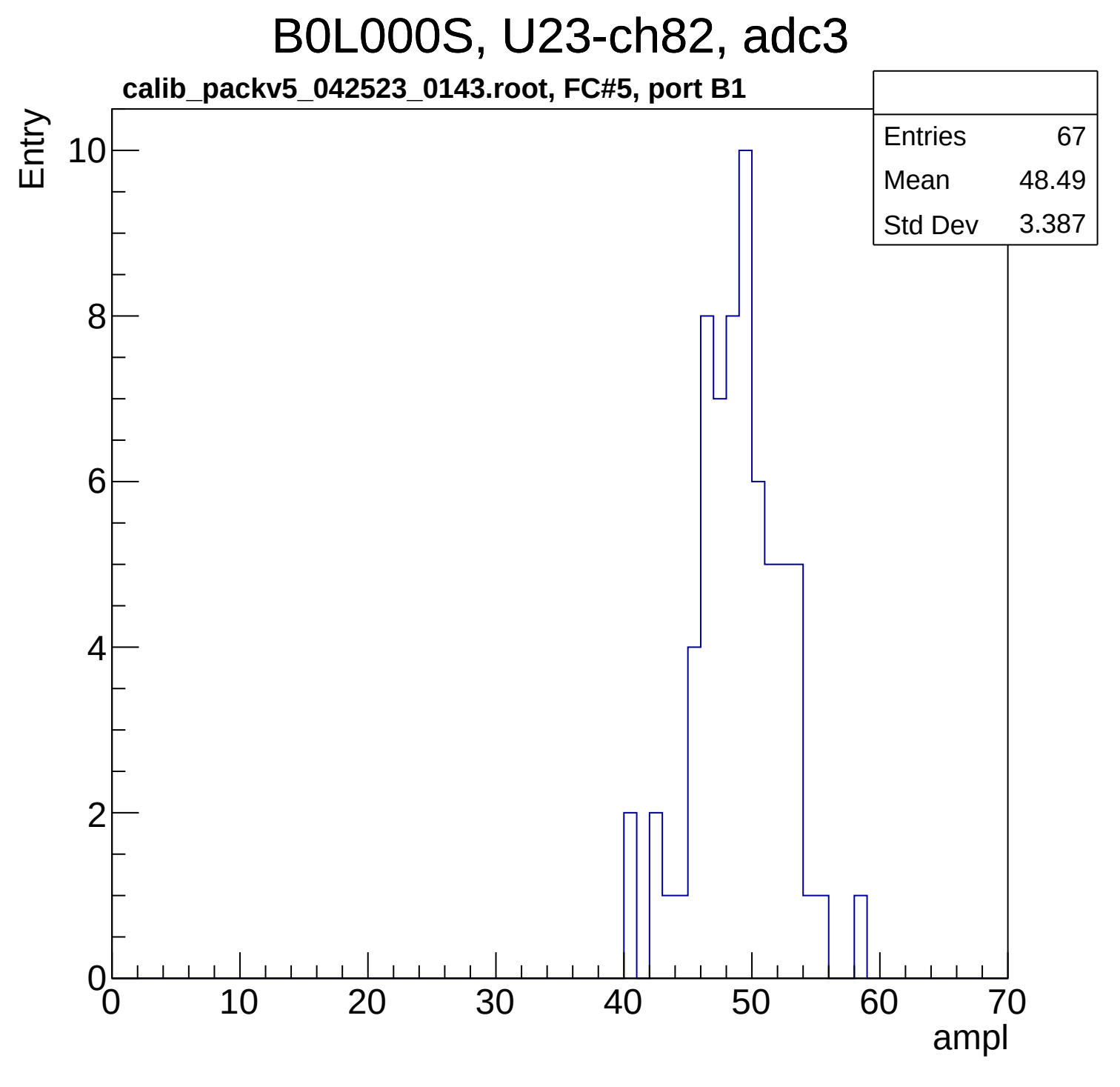

B0L000S, U23-ch81, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 58.28
Std Dev 14.22
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

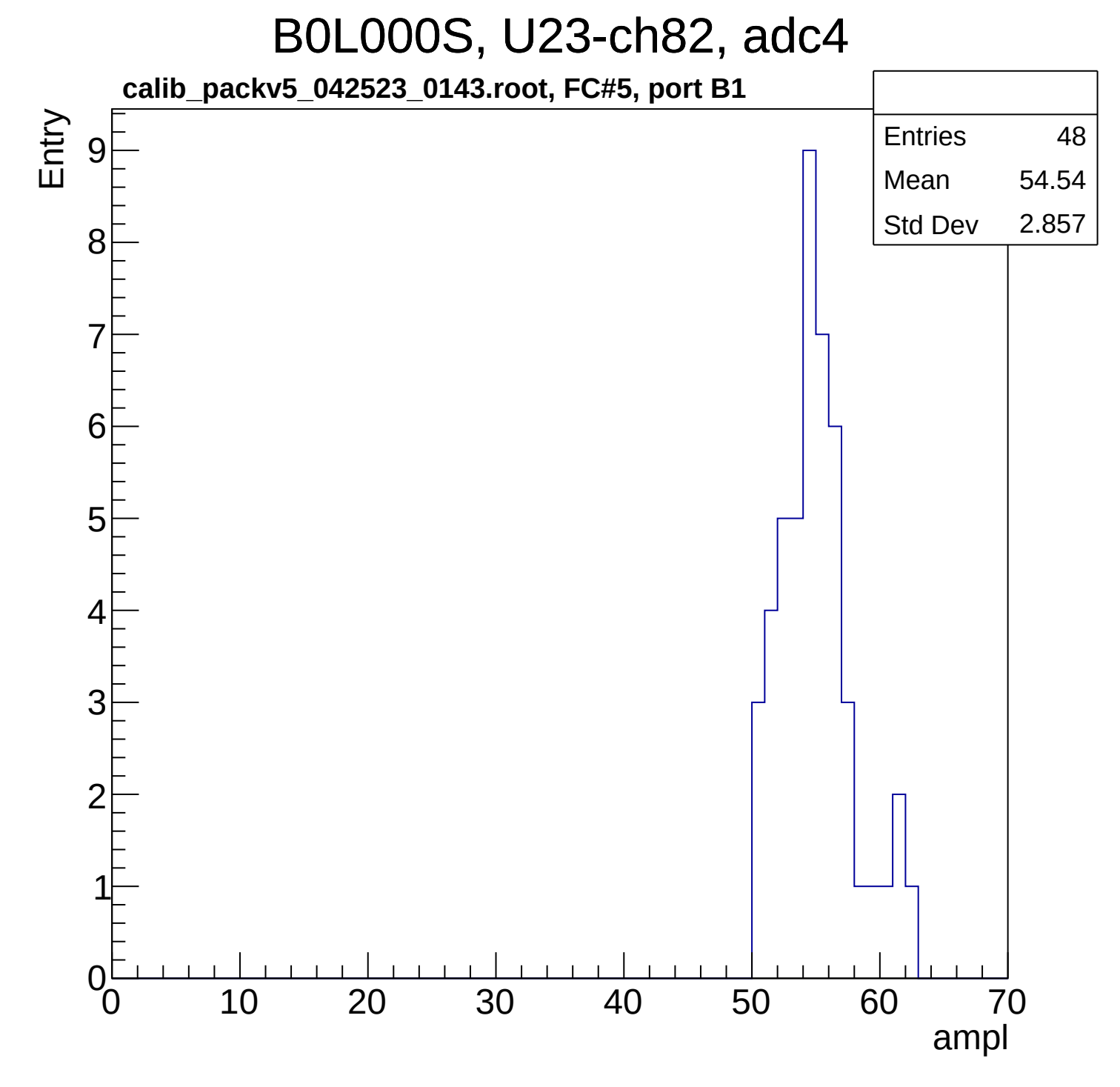

B0L000S, U23-ch81, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

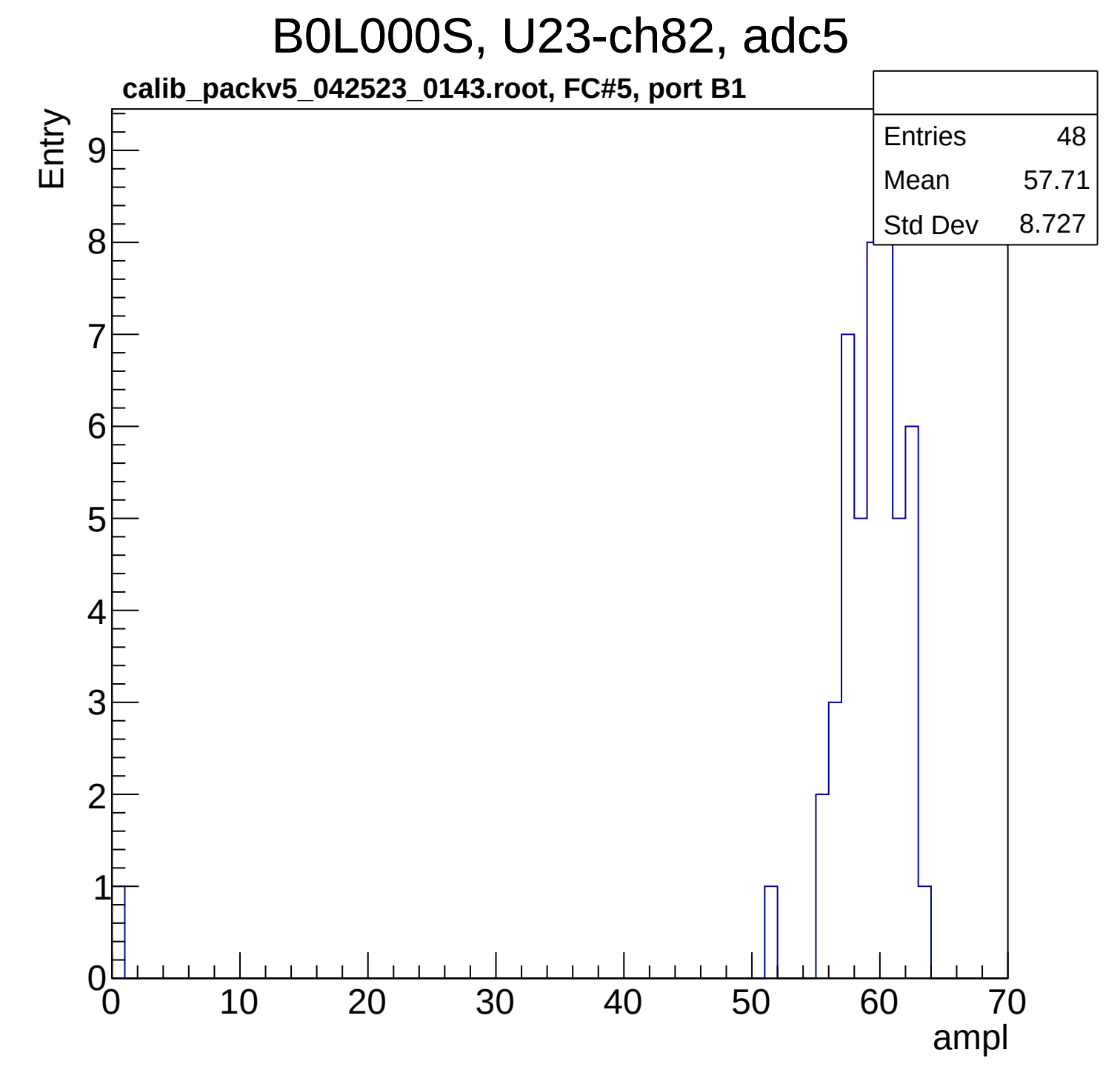

B0L000S, U23-ch82, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.85
Std Dev 4.9
Gaus mean : 30.0929
Gaus Width: 4.1540
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

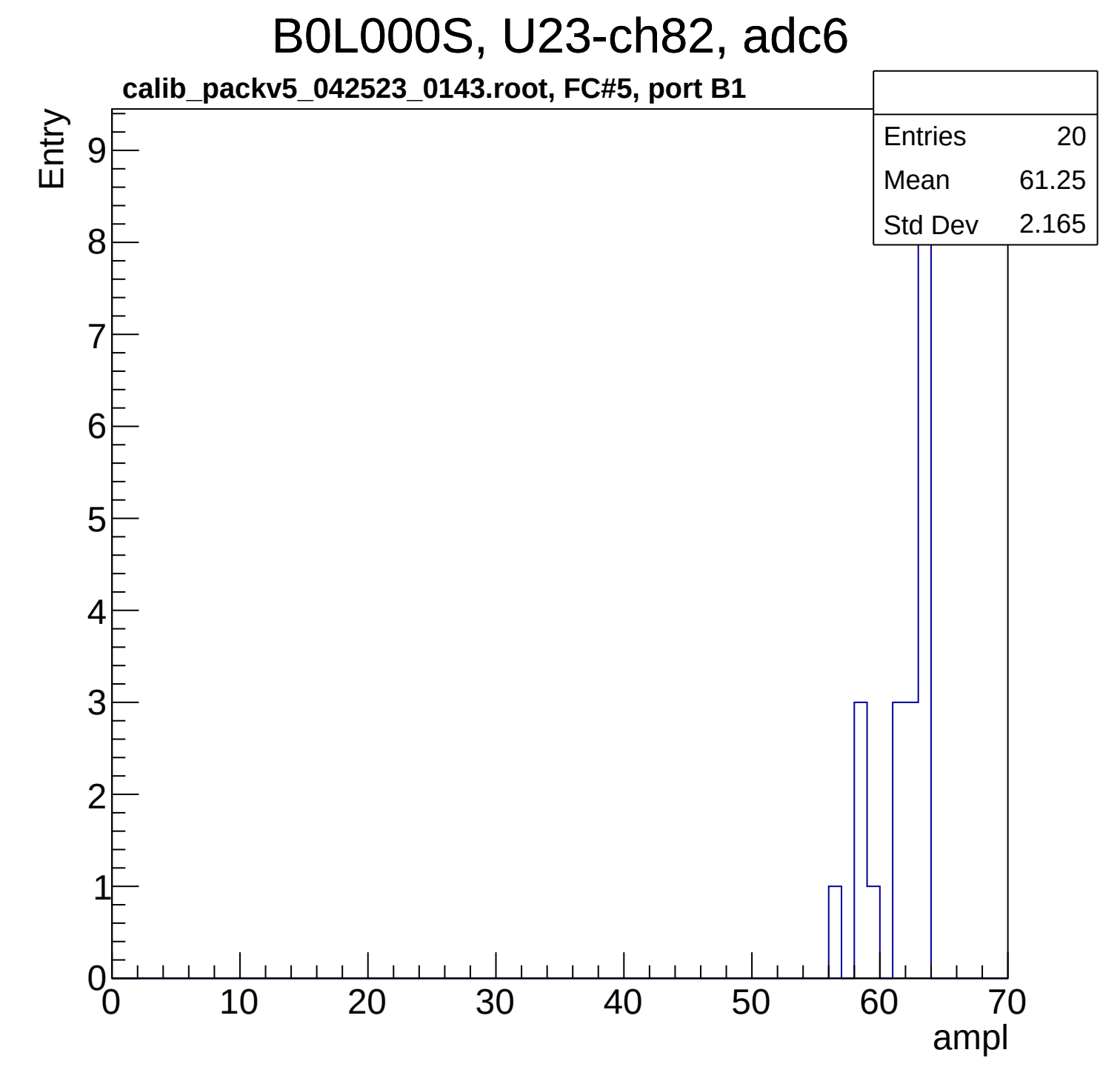

B0L000S, U23-ch82, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 35.07
Std Dev 3.049
Gaus mean : 35.7220
Gaus Width: 3.9744
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

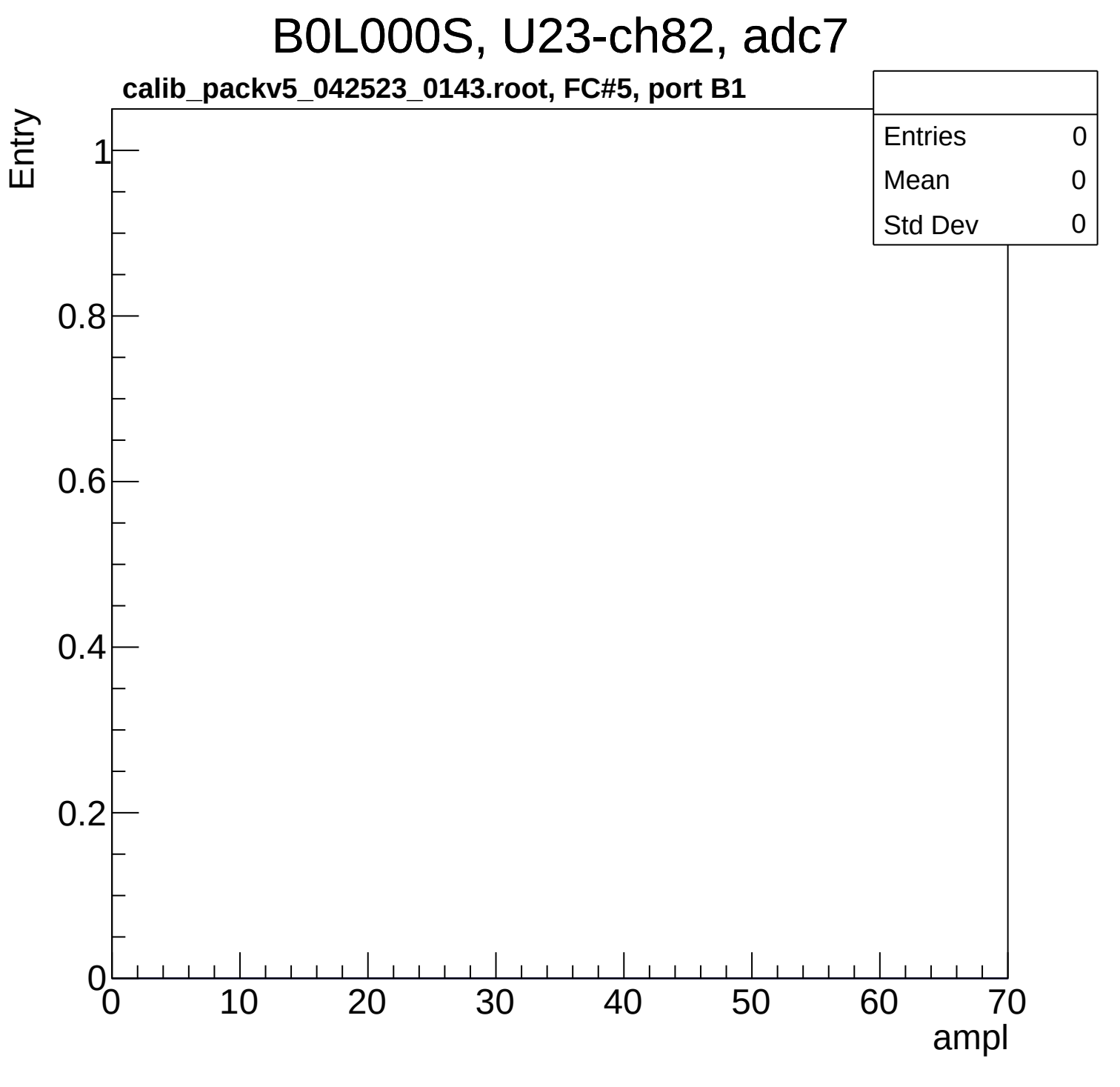

B0L000S, U23-ch82, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 42.01
Std Dev 3.237
Gaus mean : 42.7657
Gaus Width: 3.2423
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

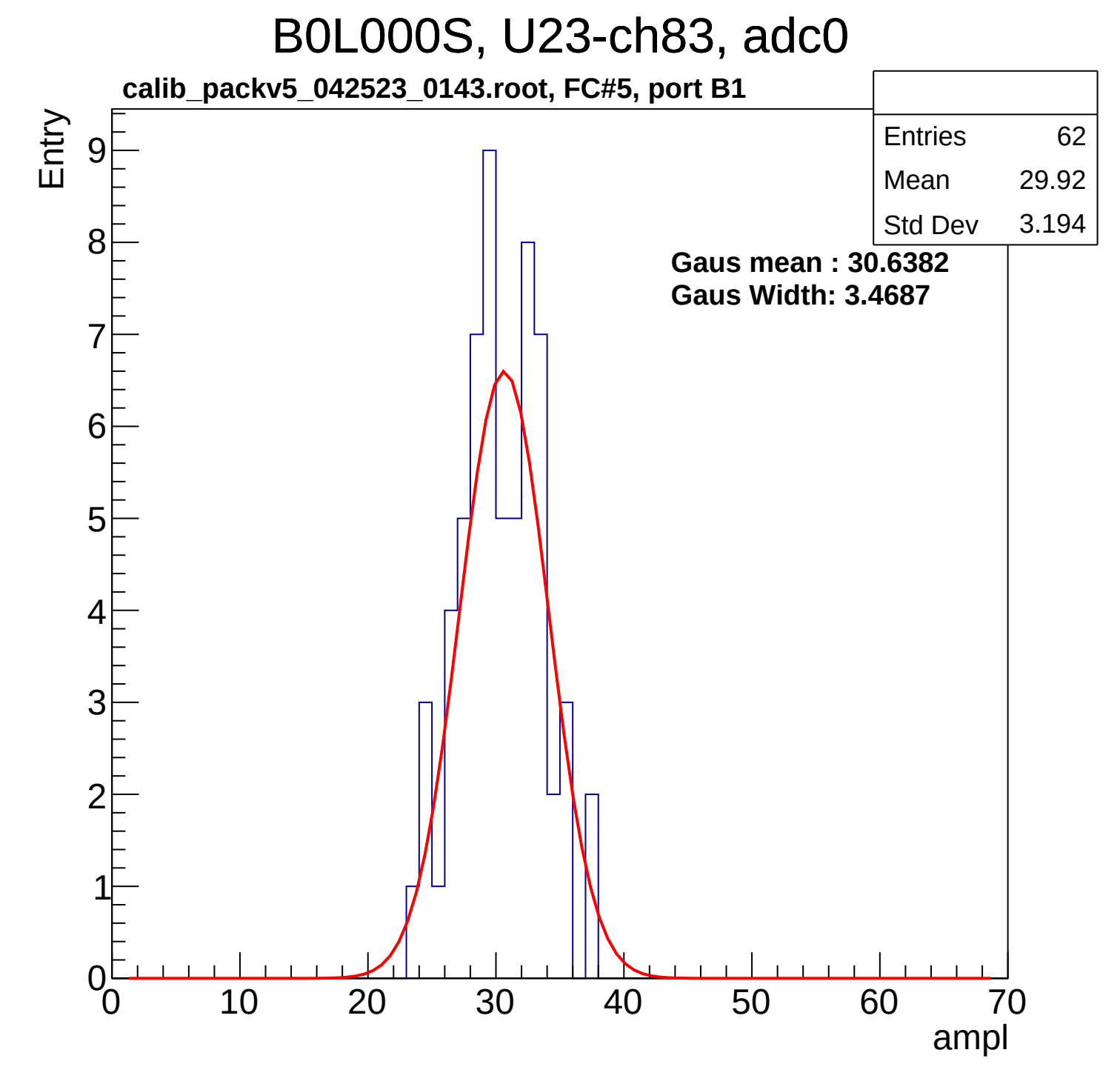

B0L000S, U23-ch82, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.49
Std Dev 3.387
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

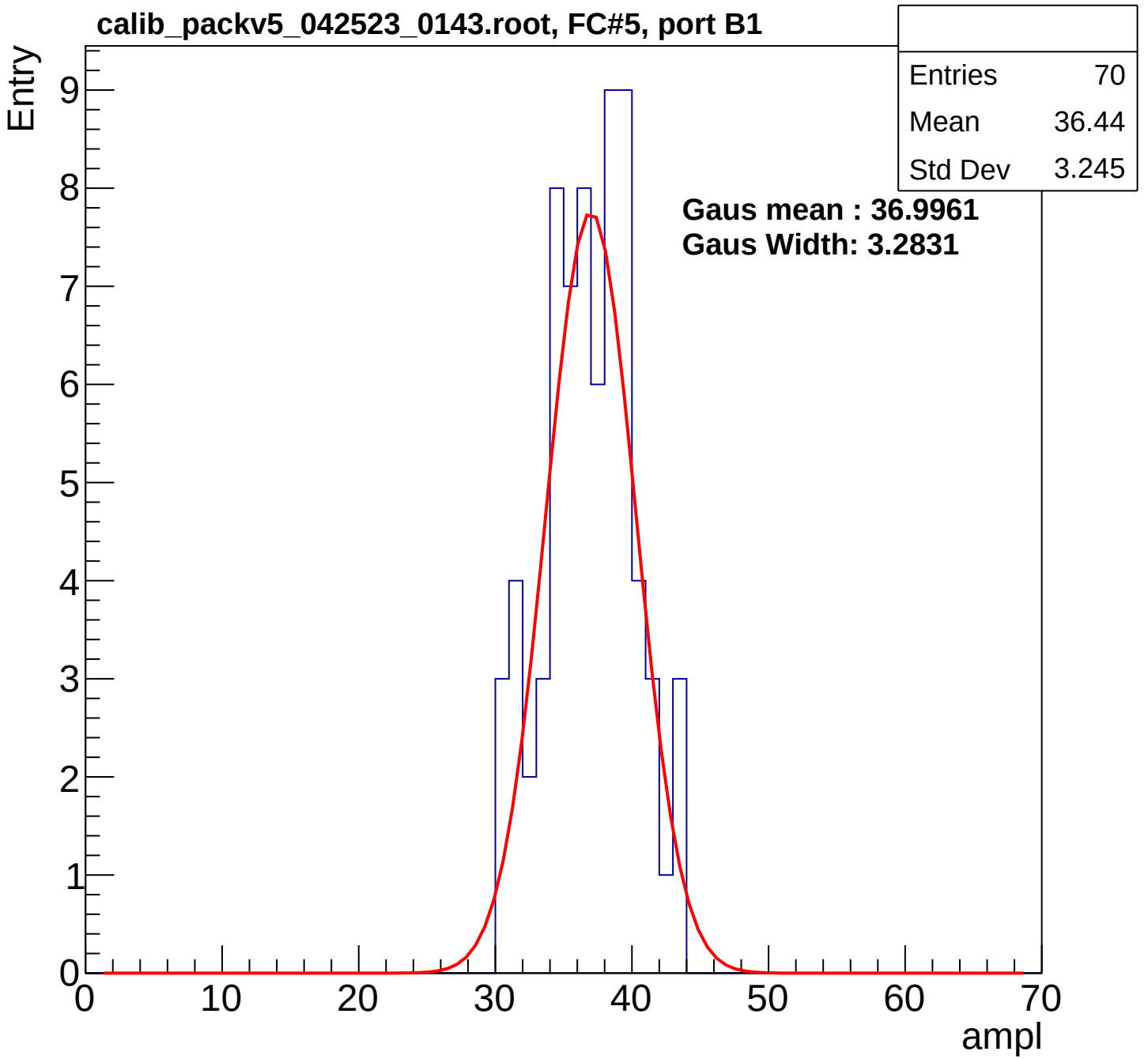

B0L000S, U23-ch82, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.54
Std Dev 2.857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

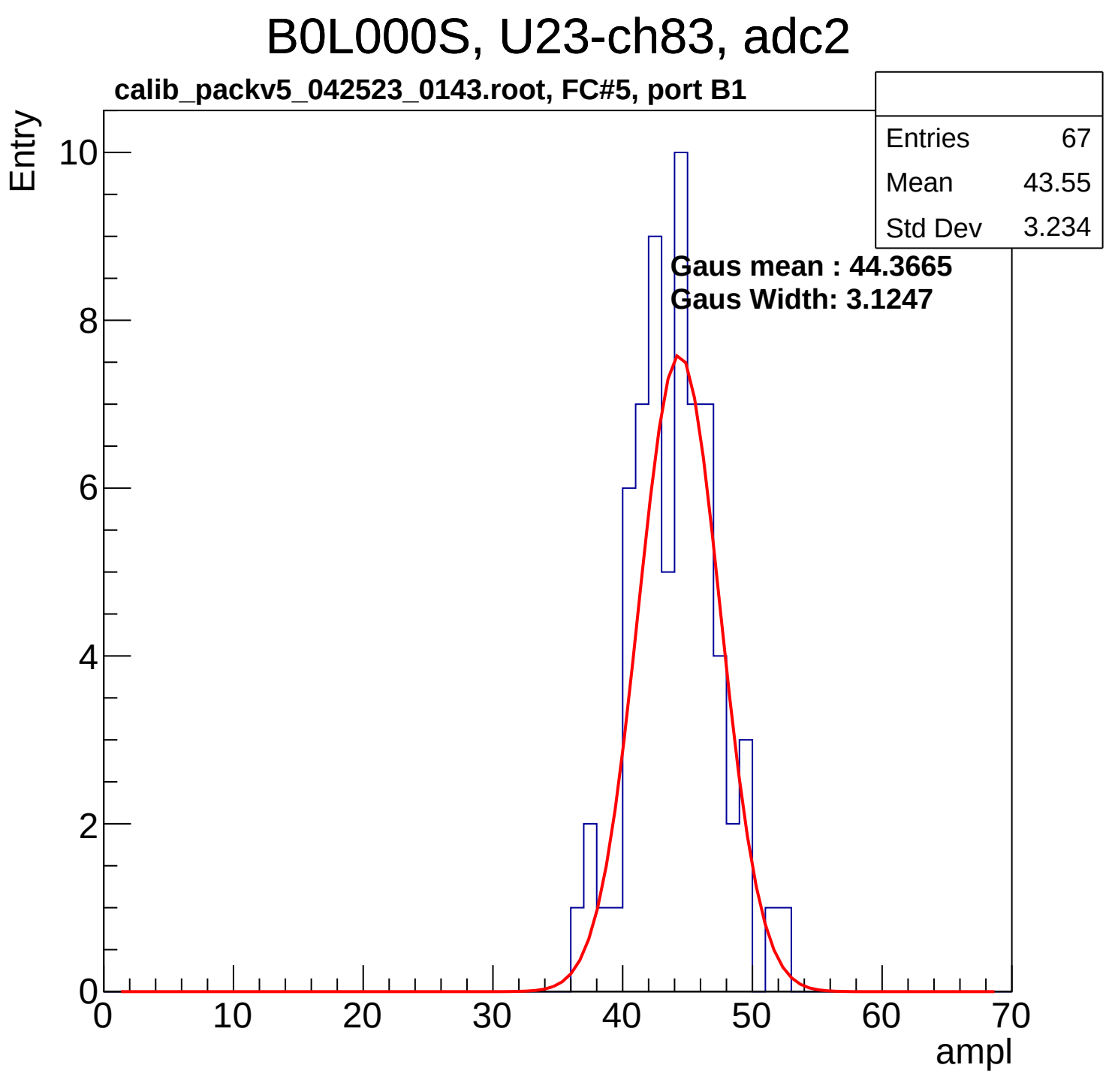

B0L000S, U23-ch82, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 57.71
Std Dev 8.727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

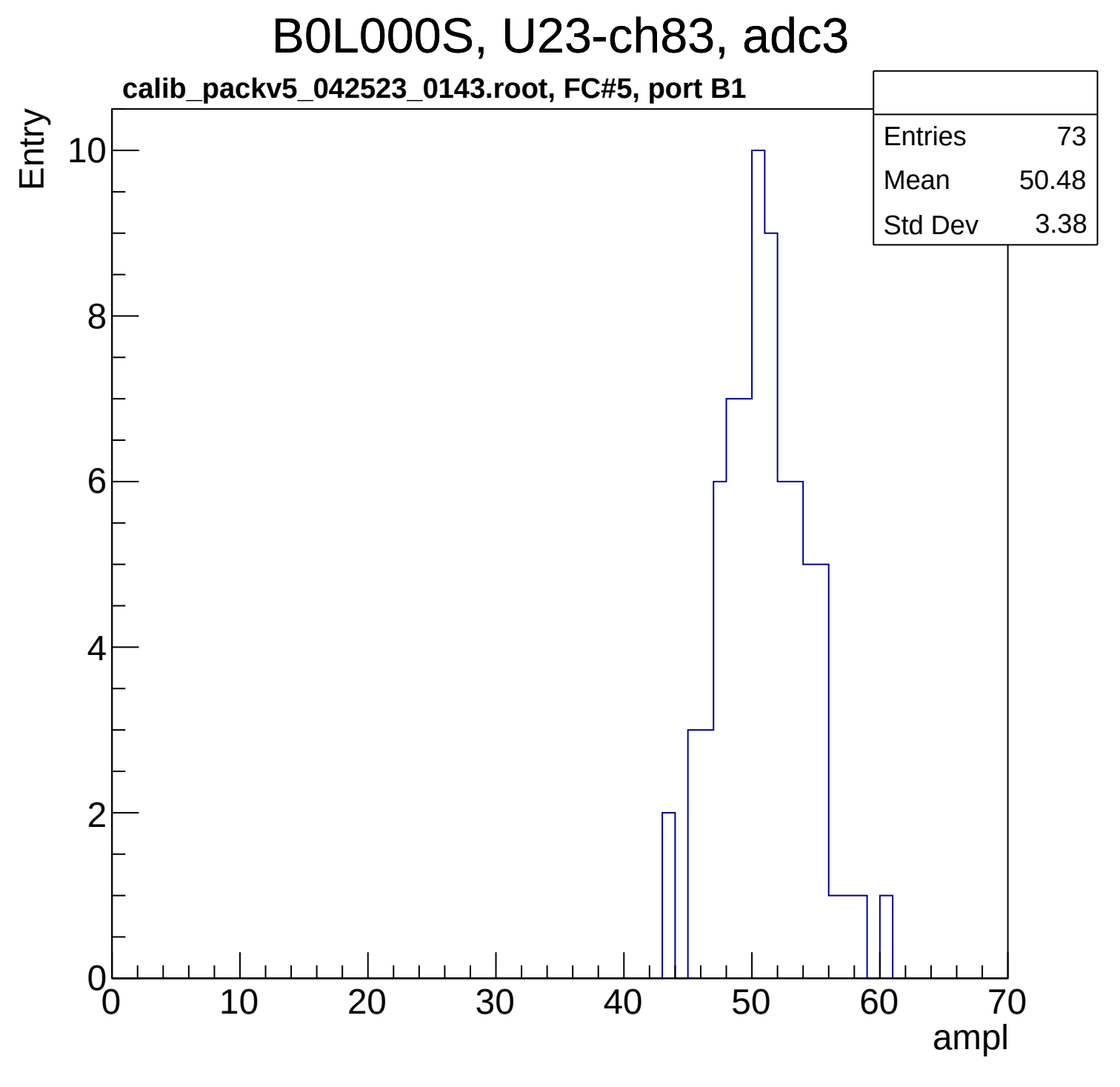

B0L000S, U23-ch82, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 61.25
Std Dev 2.165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

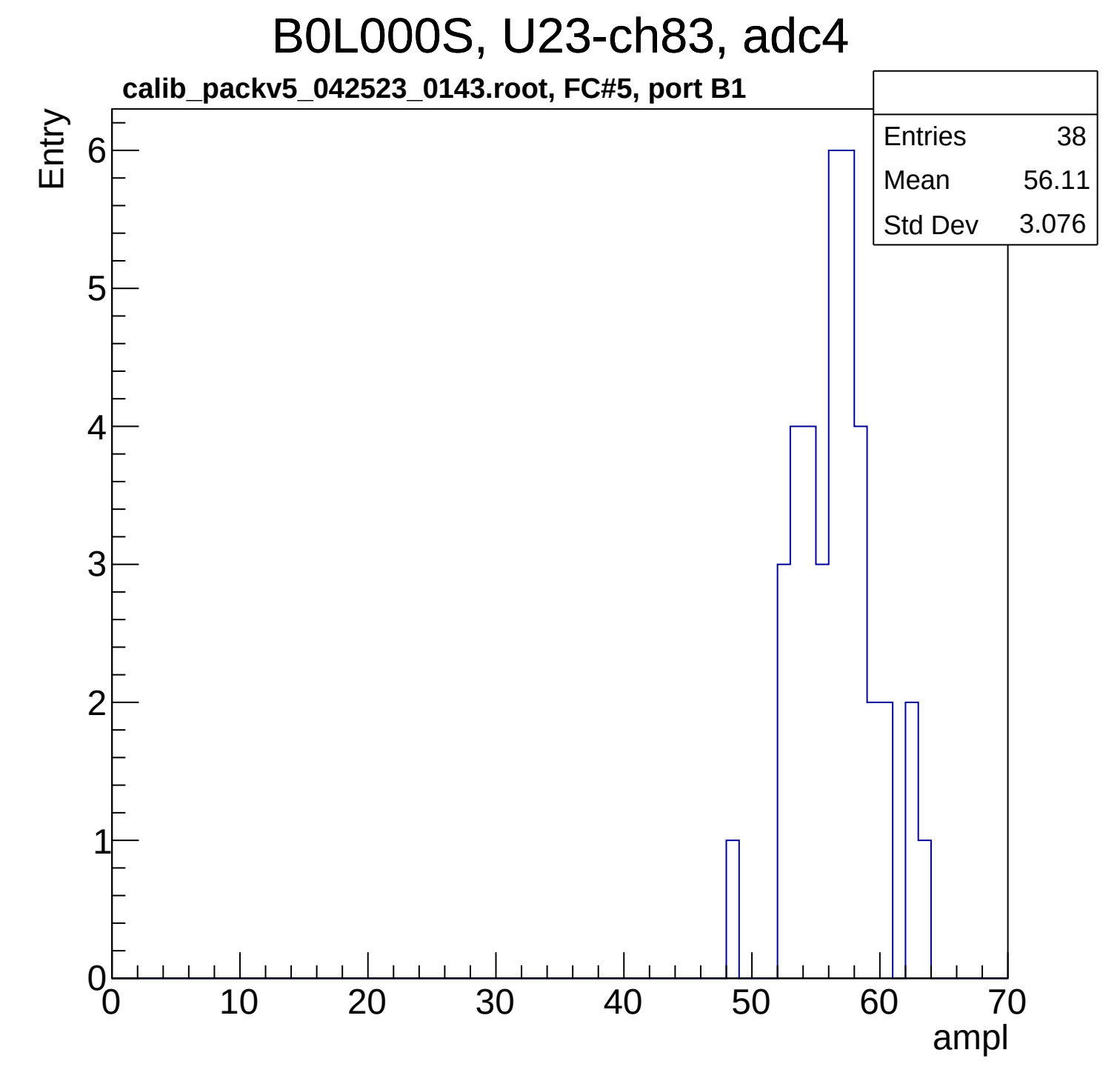

B0L000S, U23-ch82, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

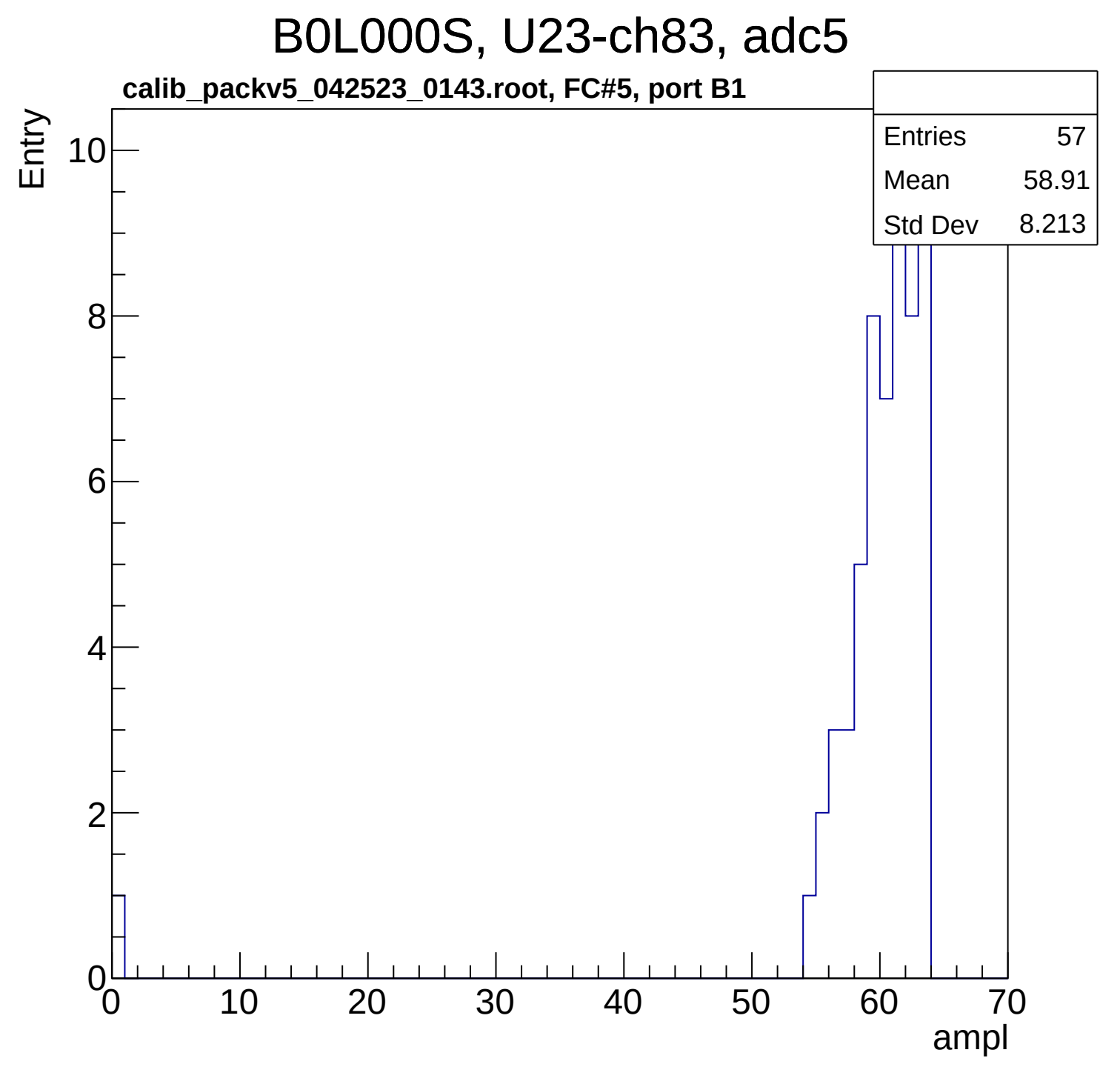

B0L000S, U23-ch83, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.92
Std Dev 3.194
Gaus mean : 30.6382
Gaus Width: 3.4687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

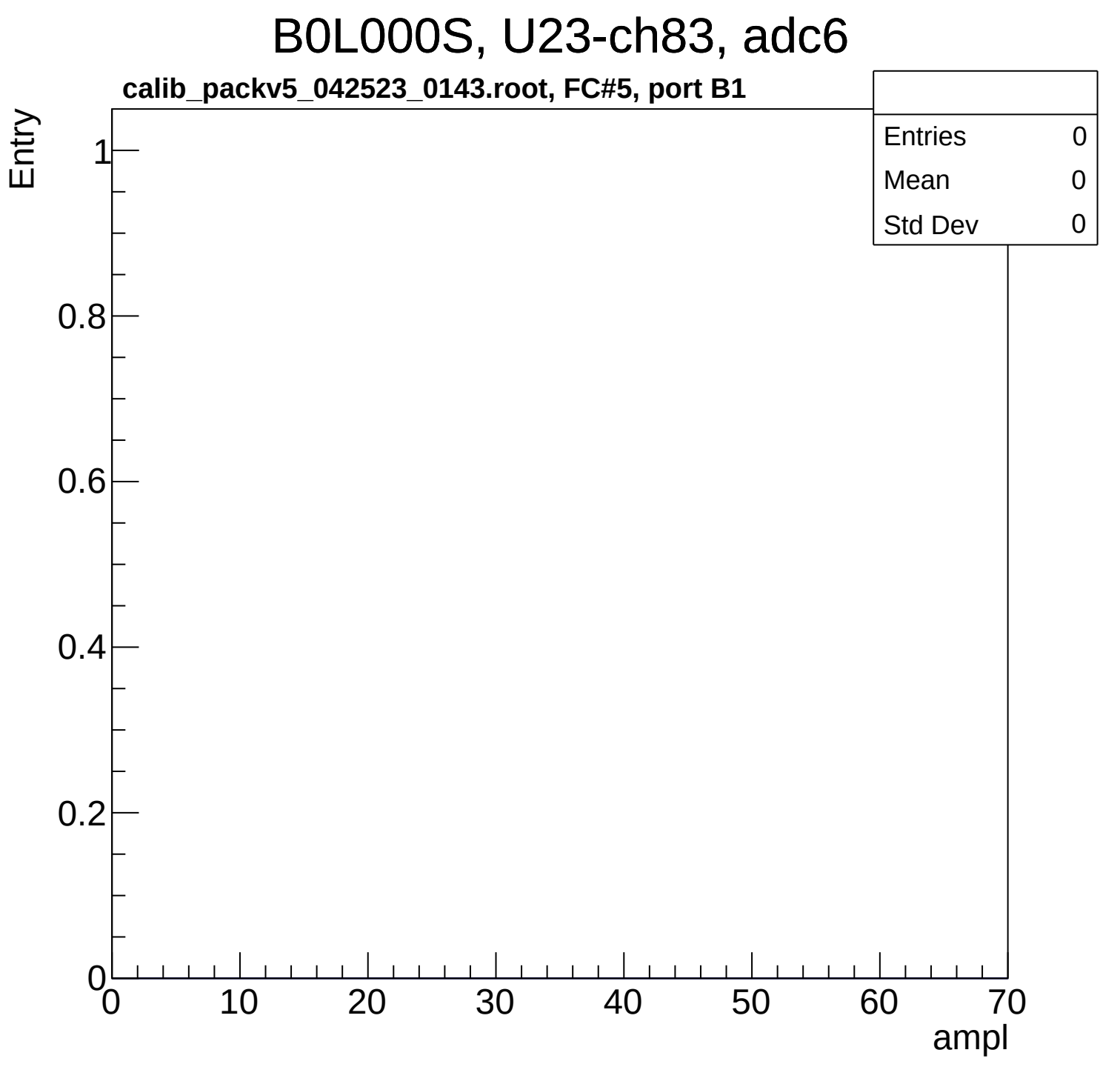

B0L000S, U23-ch83, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.44
Std Dev 3.245
Gaus mean : 36.9961
Gaus Width: 3.2831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

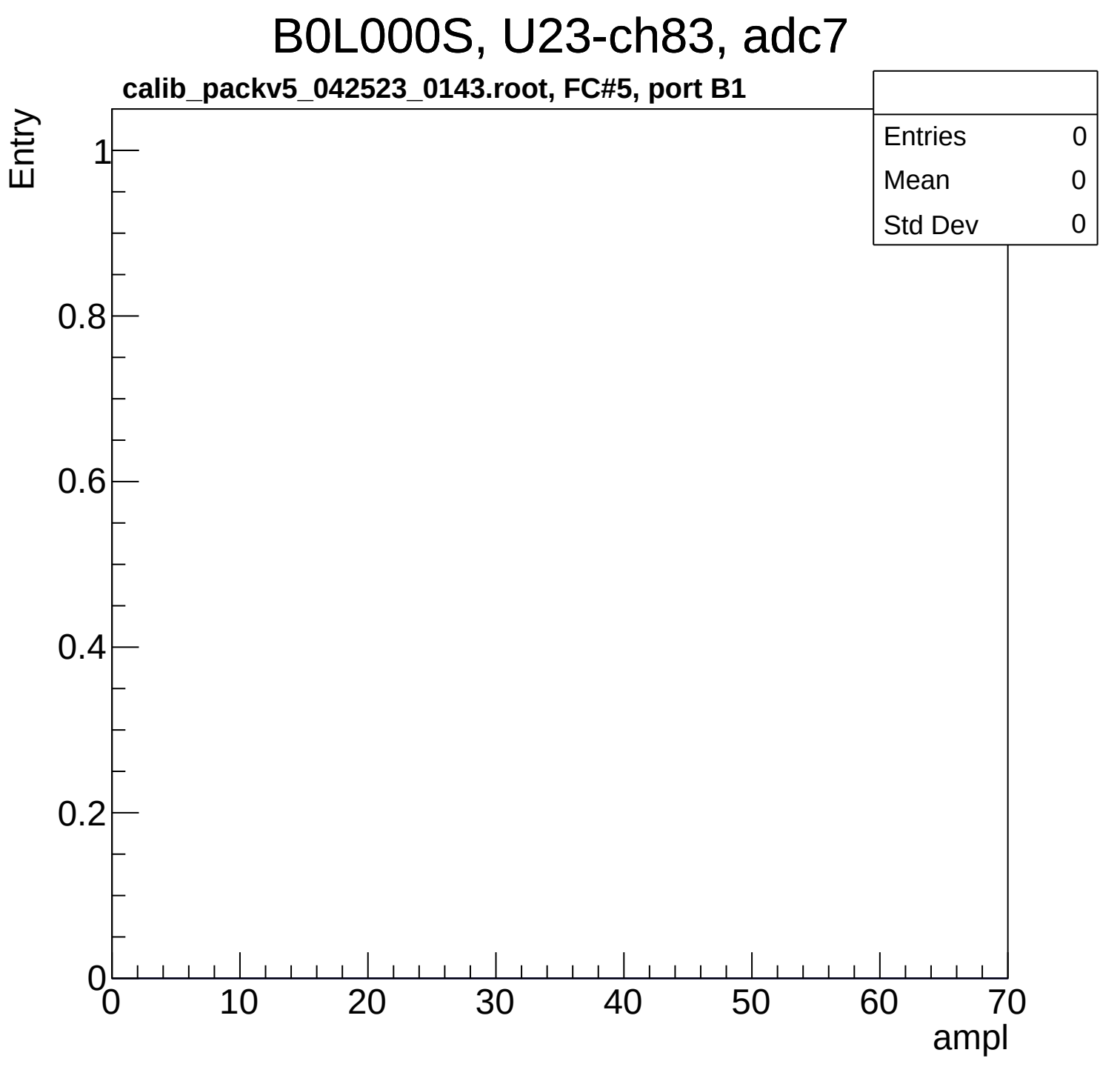

B0L000S, U23-ch83, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.55
Std Dev 3.234
Gaus mean : 44.3665
Gaus Width: 3.1247
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

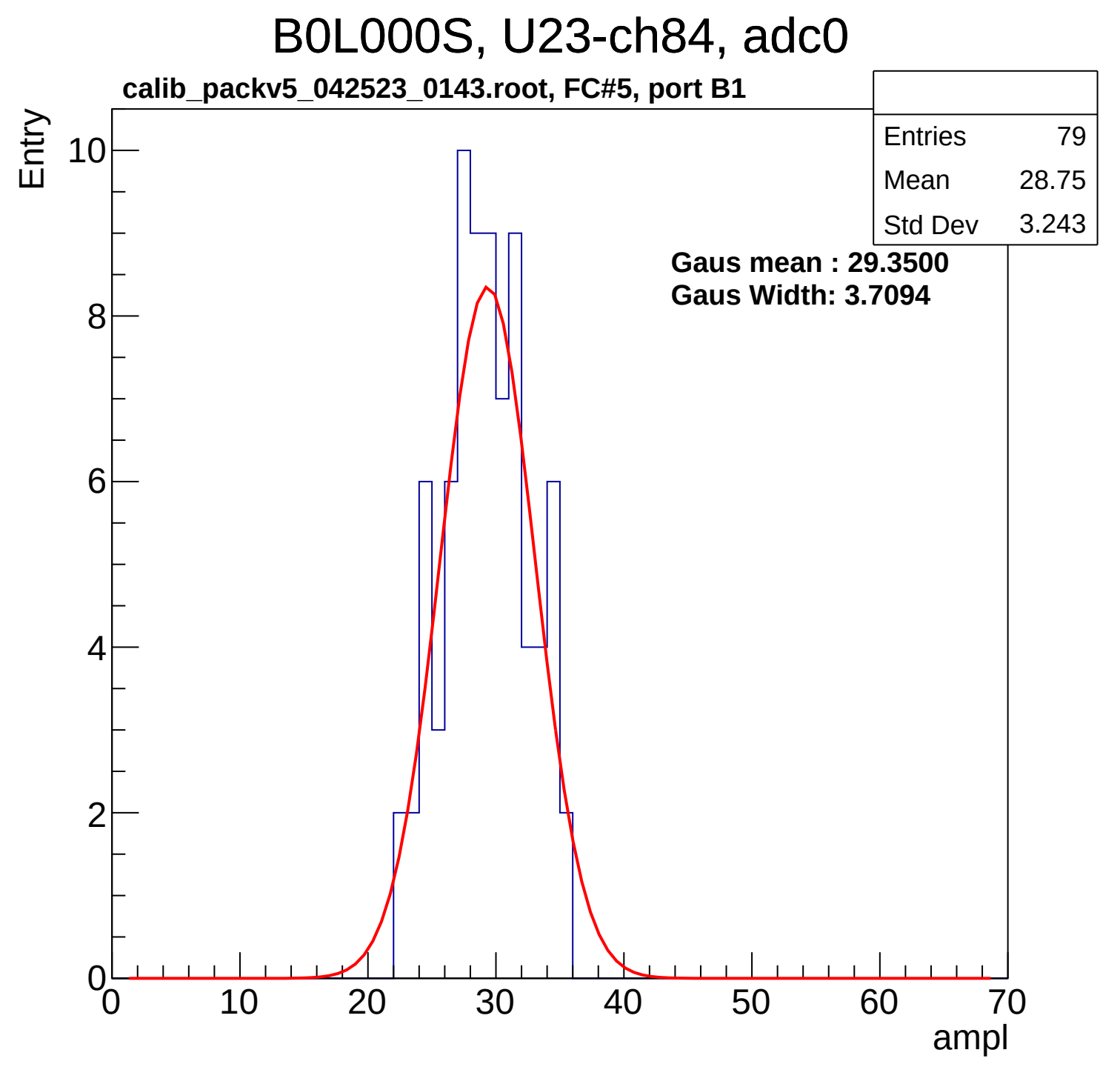

B0L000S, U23-ch83, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 50.48
Std Dev 3.38
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

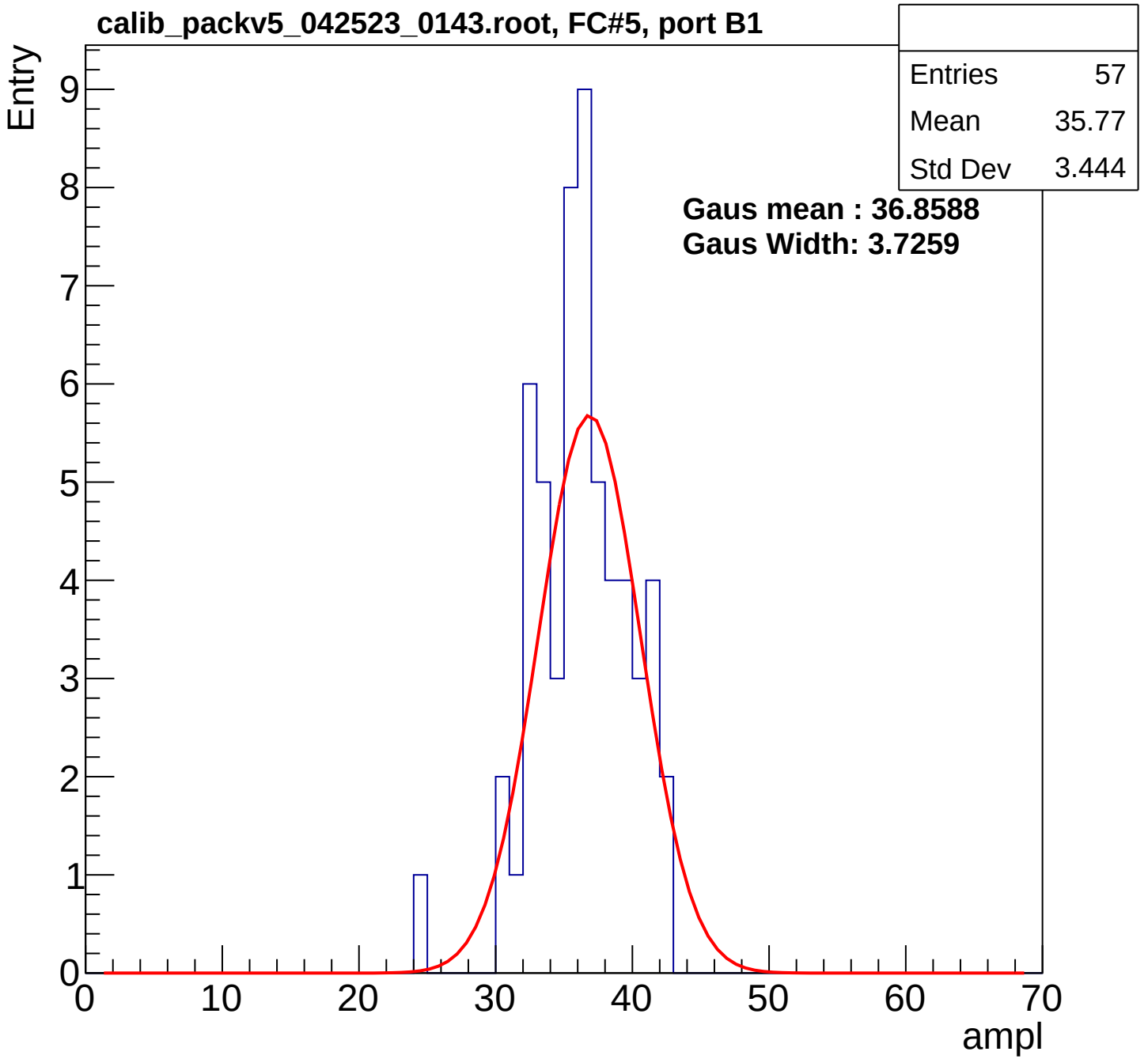

B0L000S, U23-ch83, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 56.11
Std Dev 3.076
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

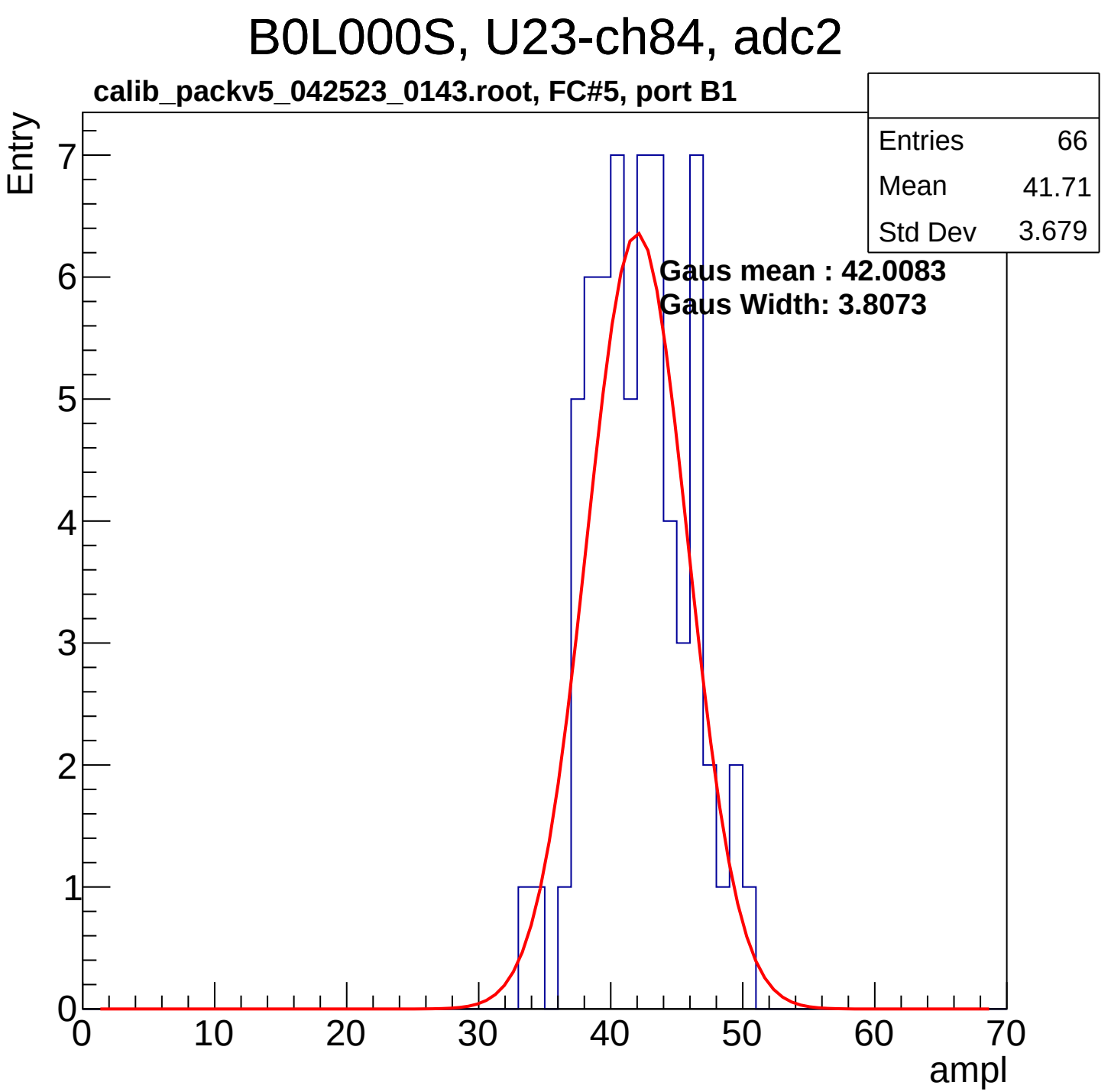

B0L000S, U23-ch83, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.91
Std Dev 8.213
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

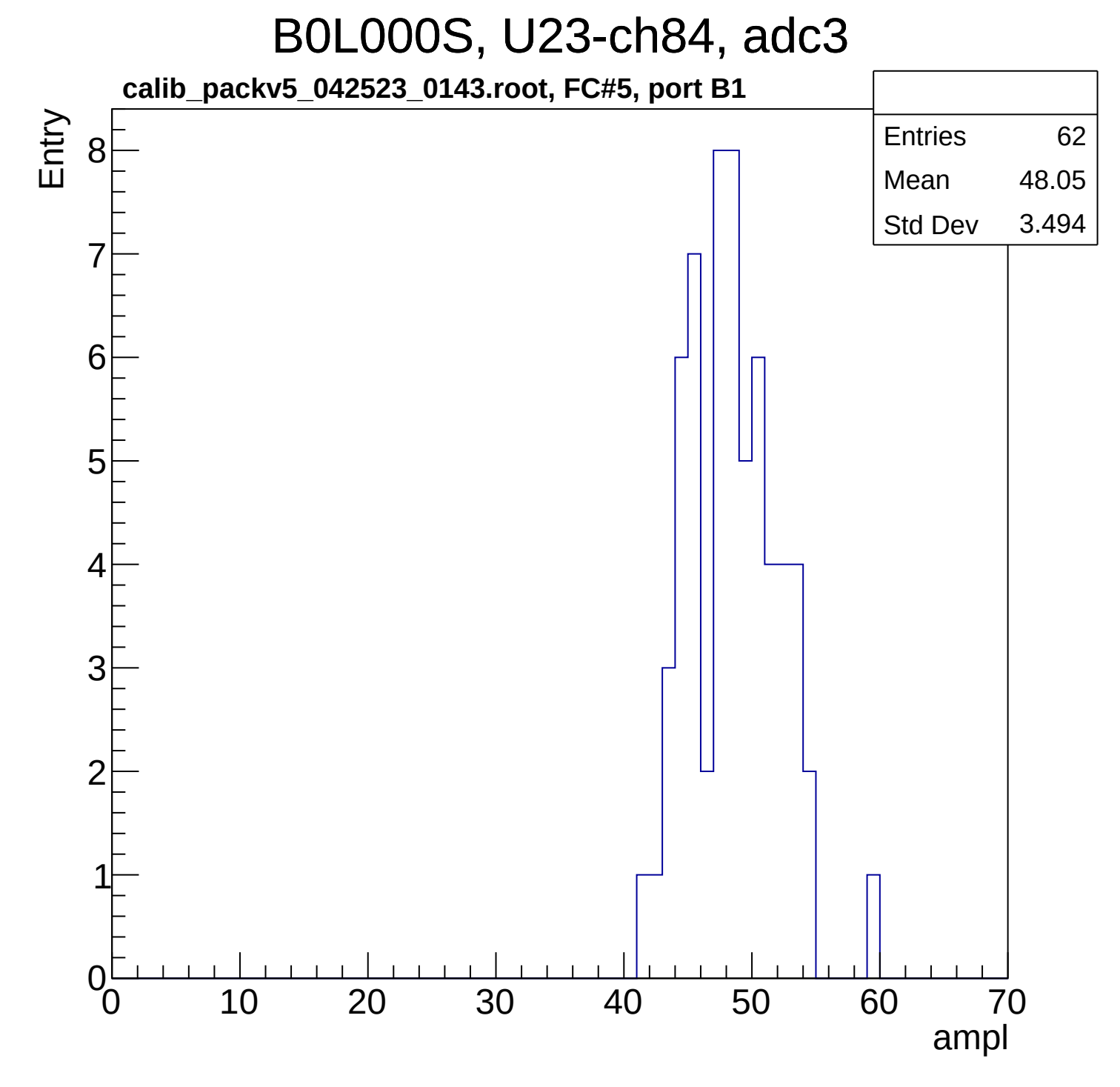

B0L000S, U23-ch83, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

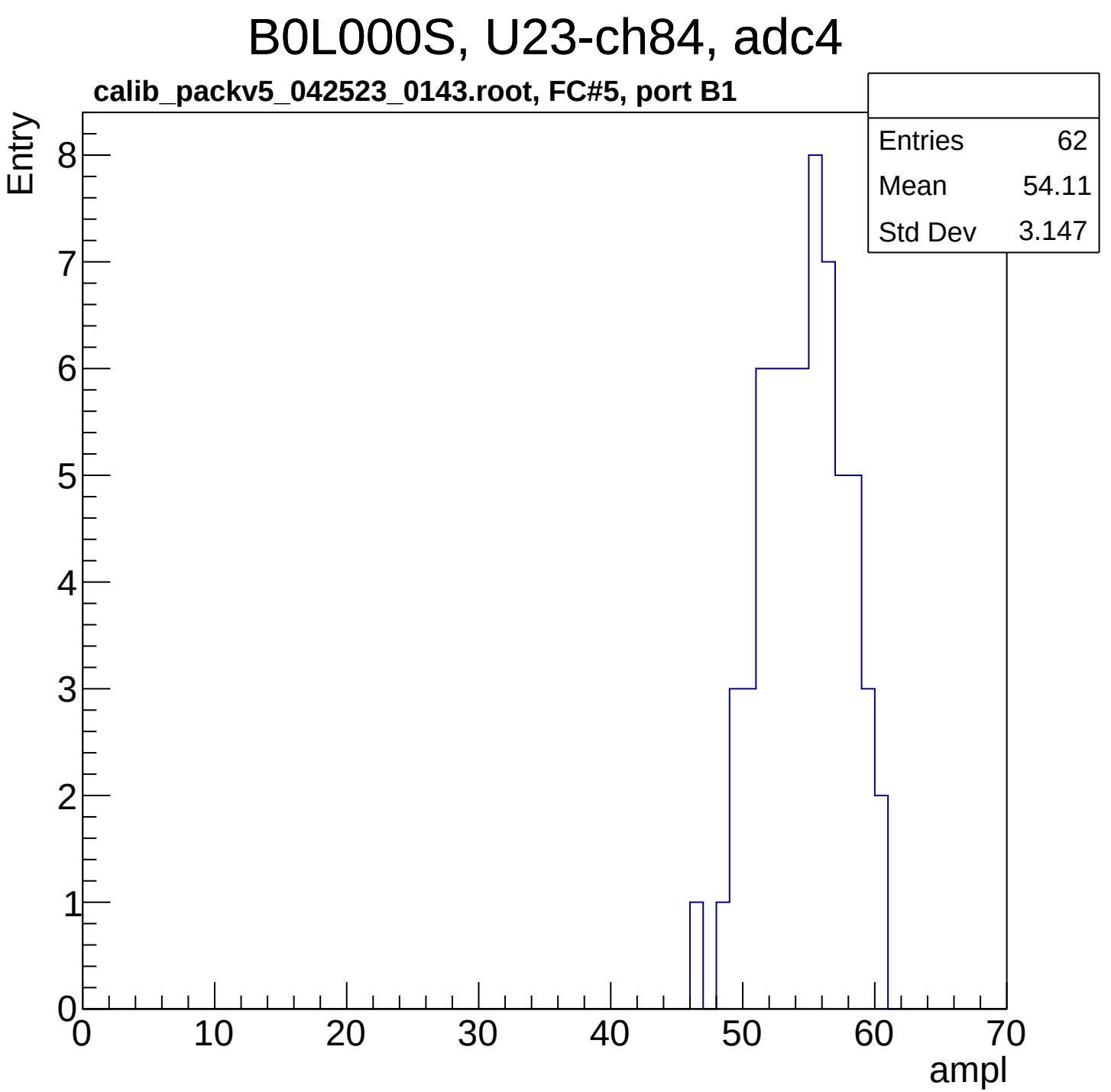

B0L000S, U23-ch83, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

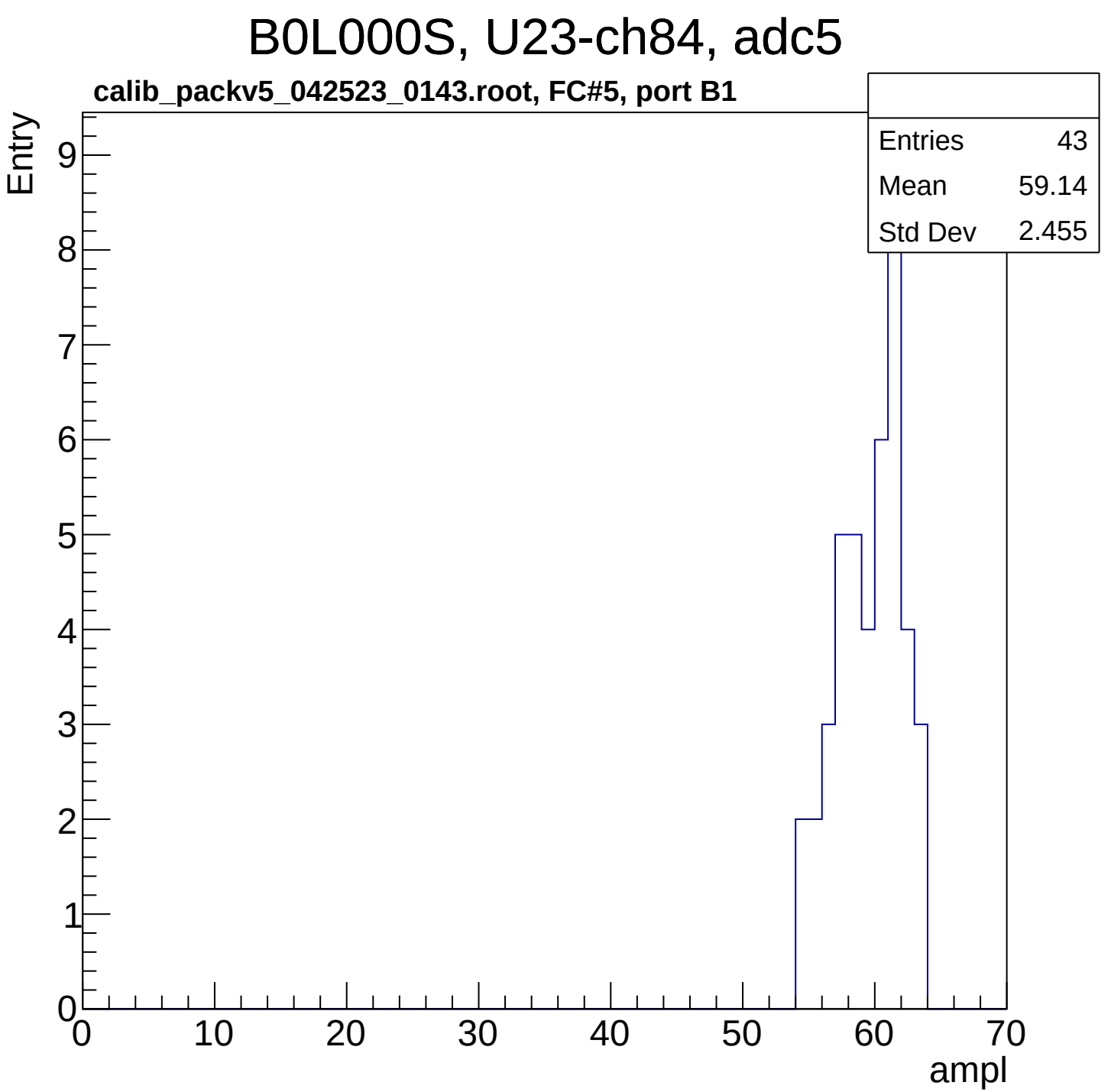

B0L000S, U23-ch84, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 28.75
Std Dev 3.243
Gaus mean : 29.3500
Gaus Width: 3.7094
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

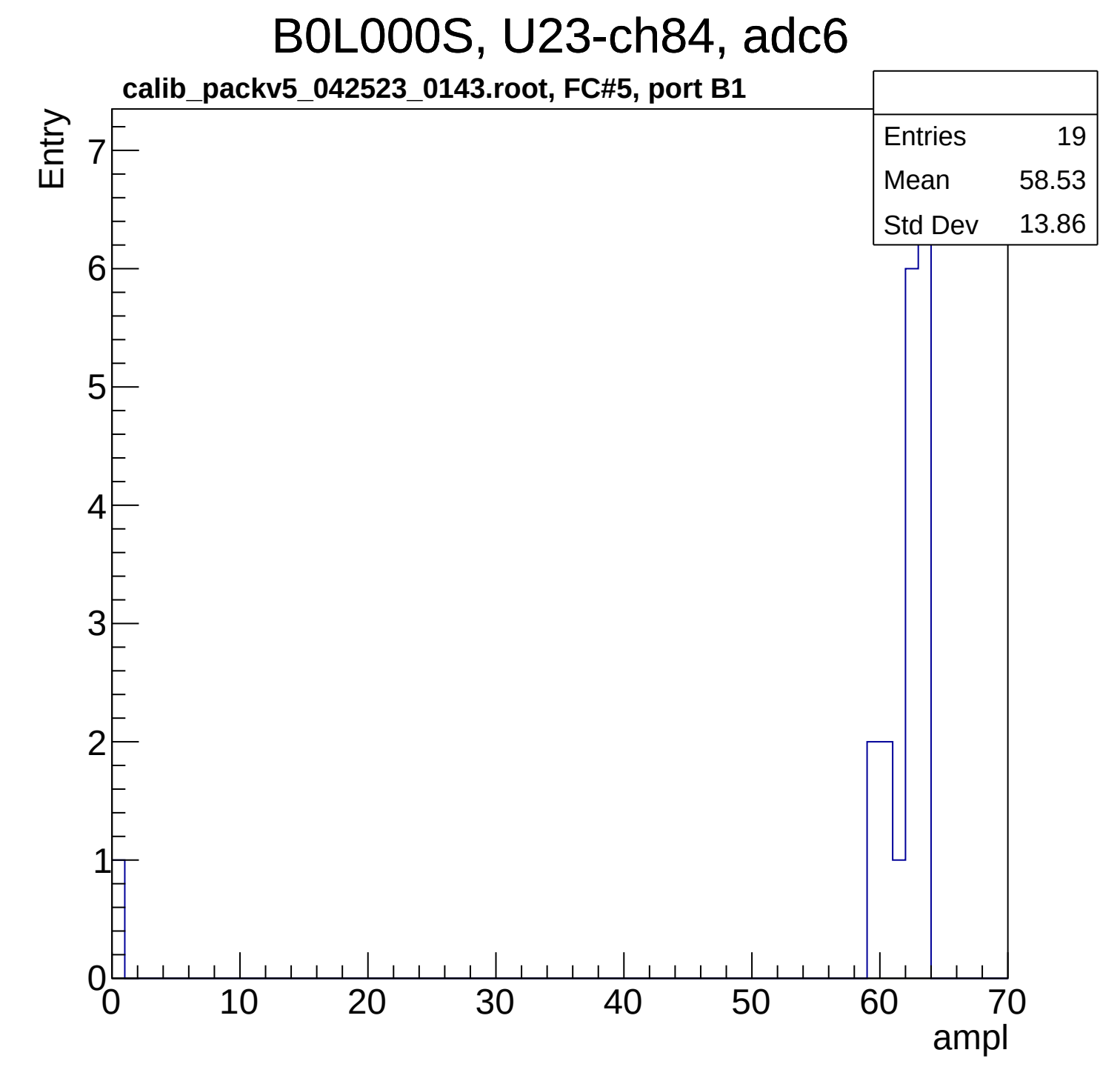

B0L000S, U23-ch84, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.77
Std Dev 3.444
Gaus mean : 36.8588
Gaus Width: 3.7259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

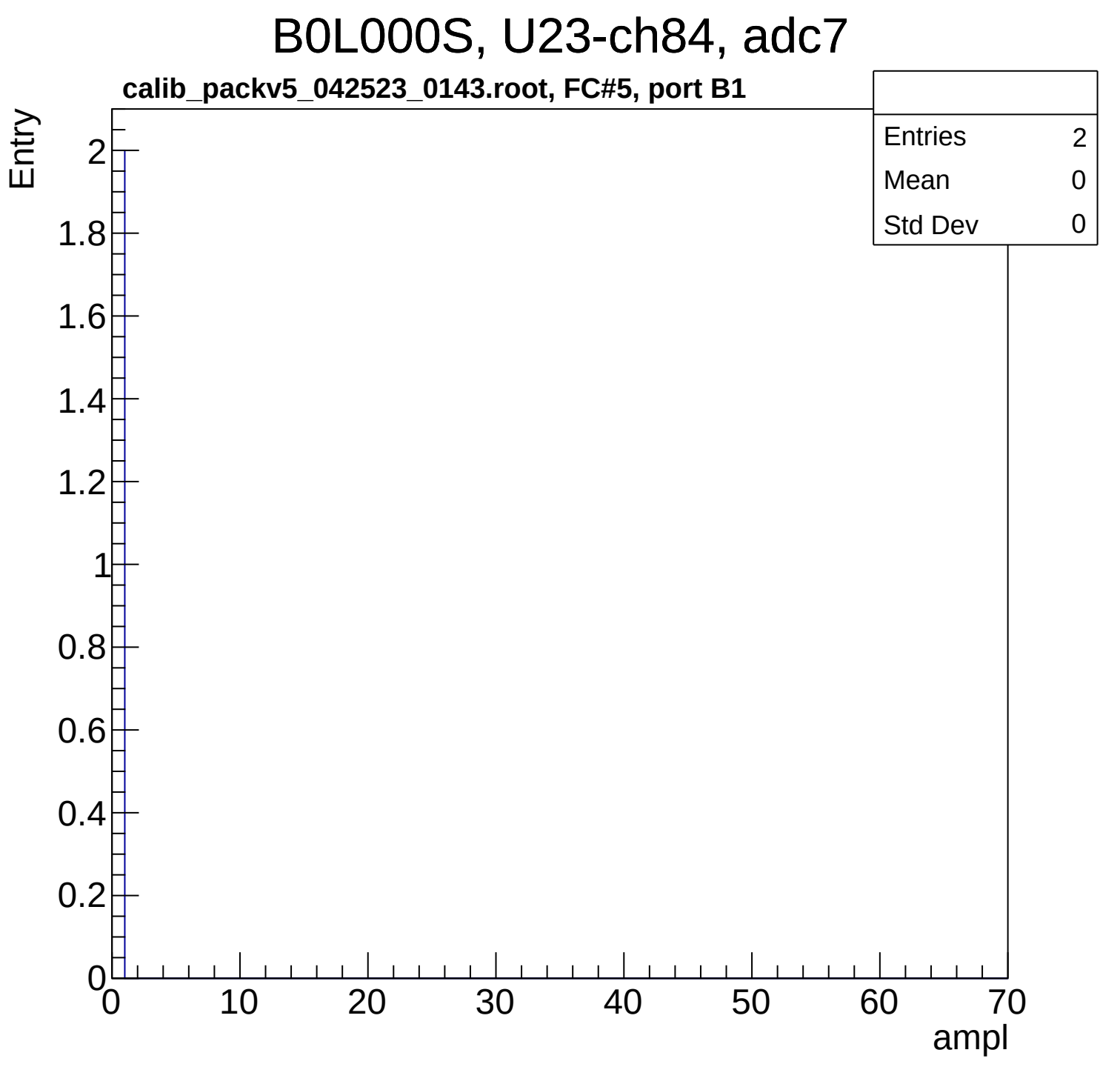

B0L000S, U23-ch84, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 41.71
Std Dev 3.679
Gaus mean : 42.0083
Gaus Width: 3.8073
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

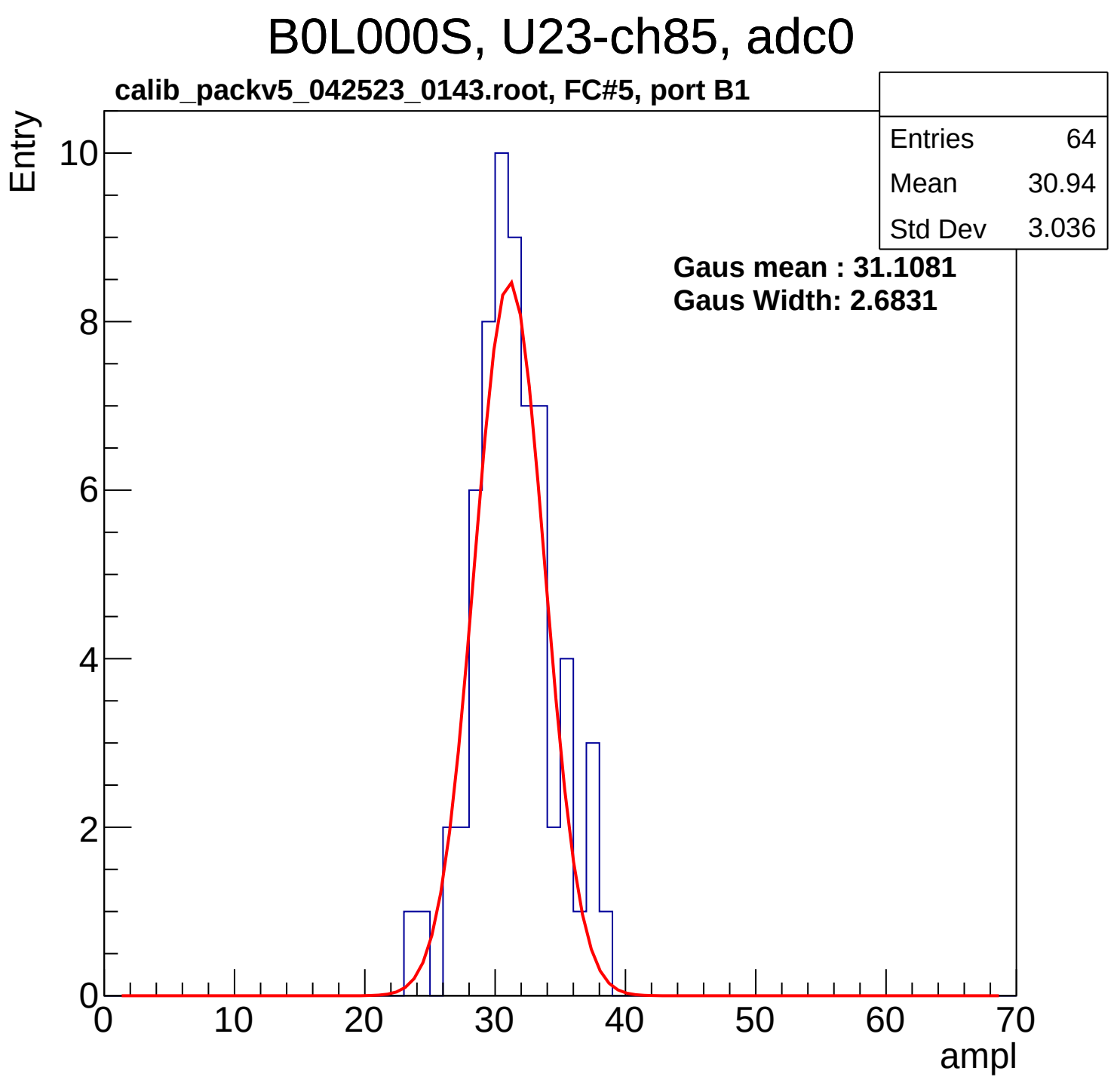

B0L000S, U23-ch84, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.05
Std Dev 3.494
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

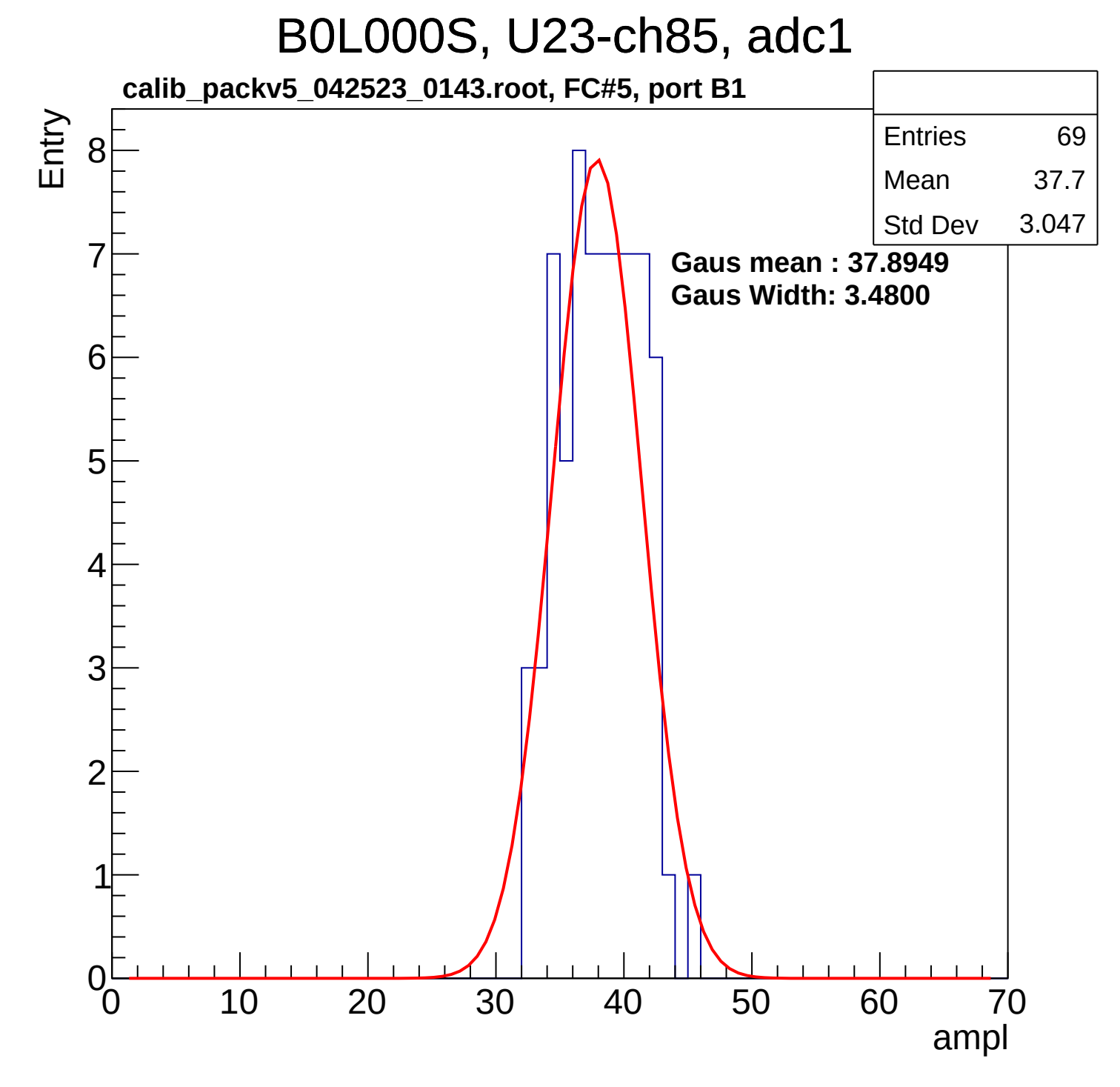

B0L000S, U23-ch84, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 54.11
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

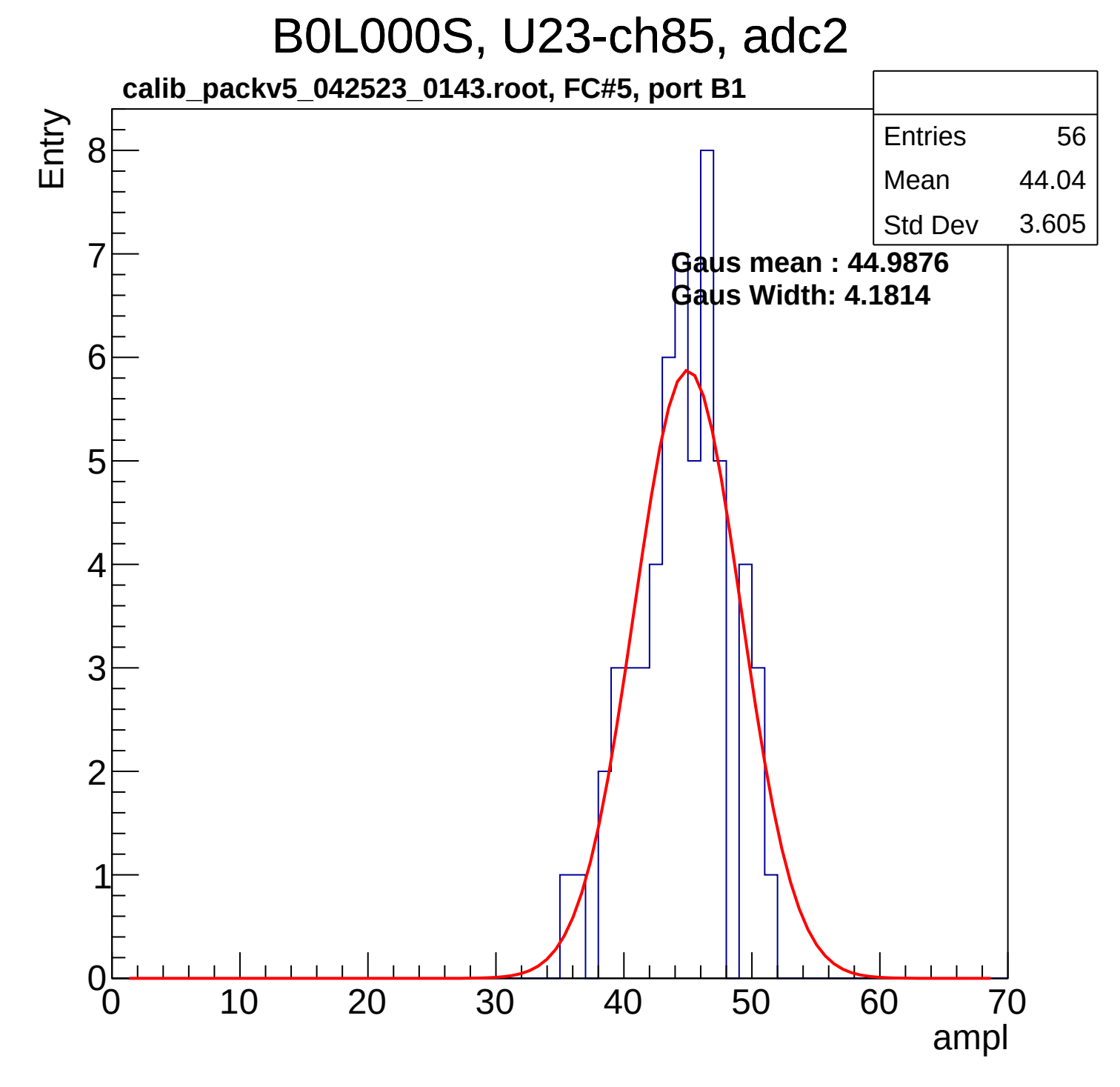

B0L000S, U23-ch84, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.14
Std Dev 2.455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

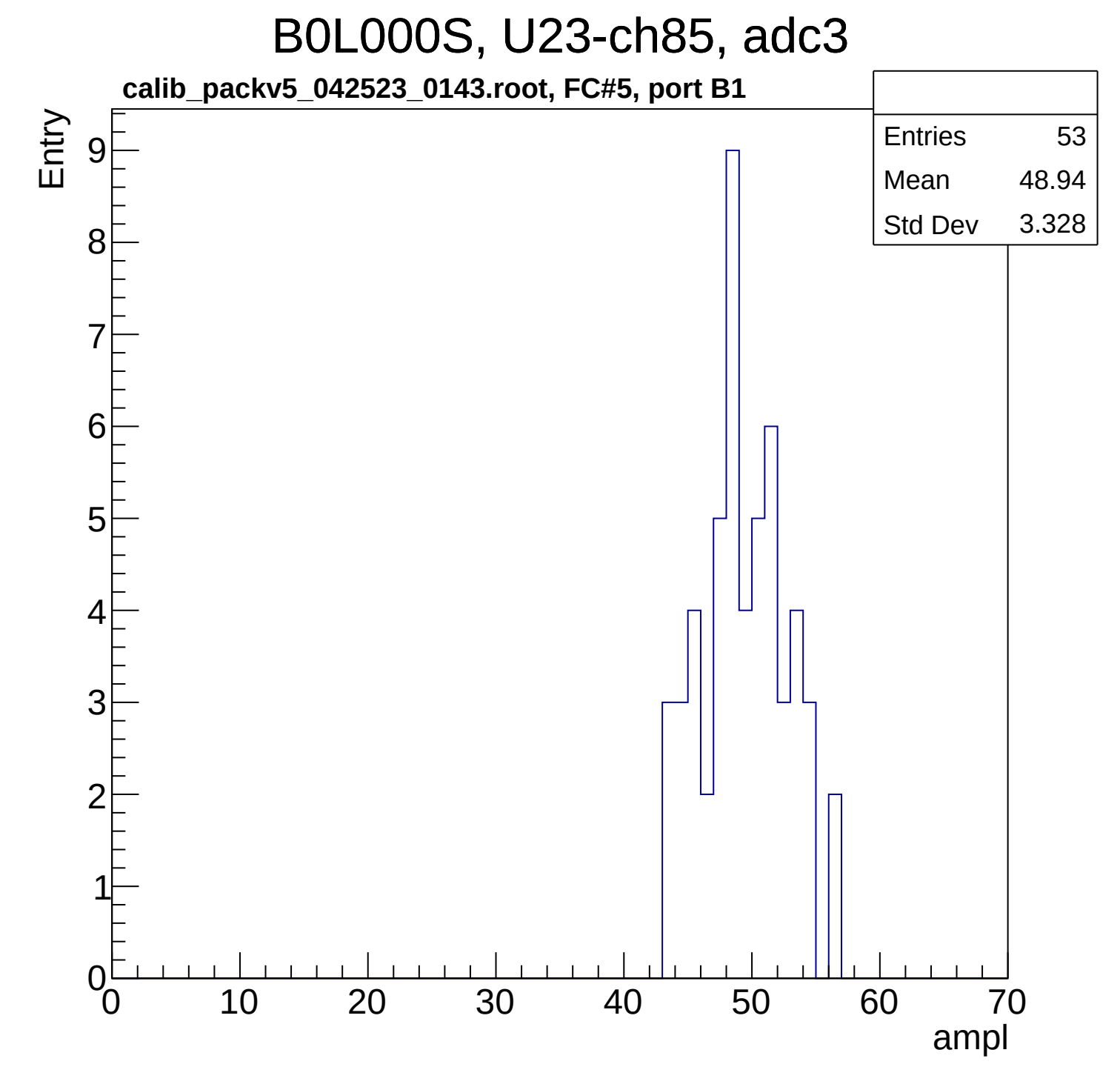

B0L000S, U23-ch84, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.53
Std Dev 13.86
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

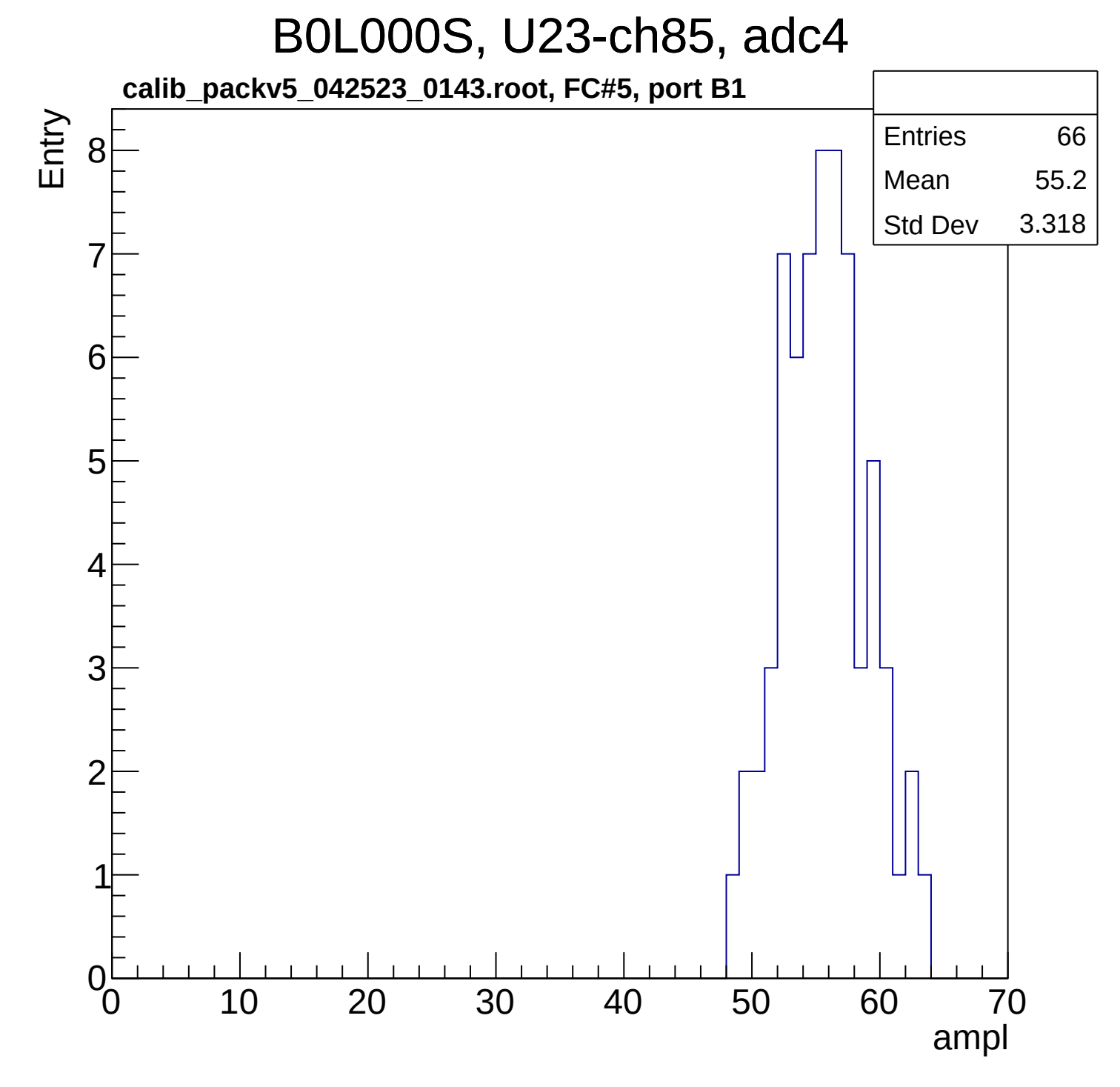

B0L000S, U23-ch84, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

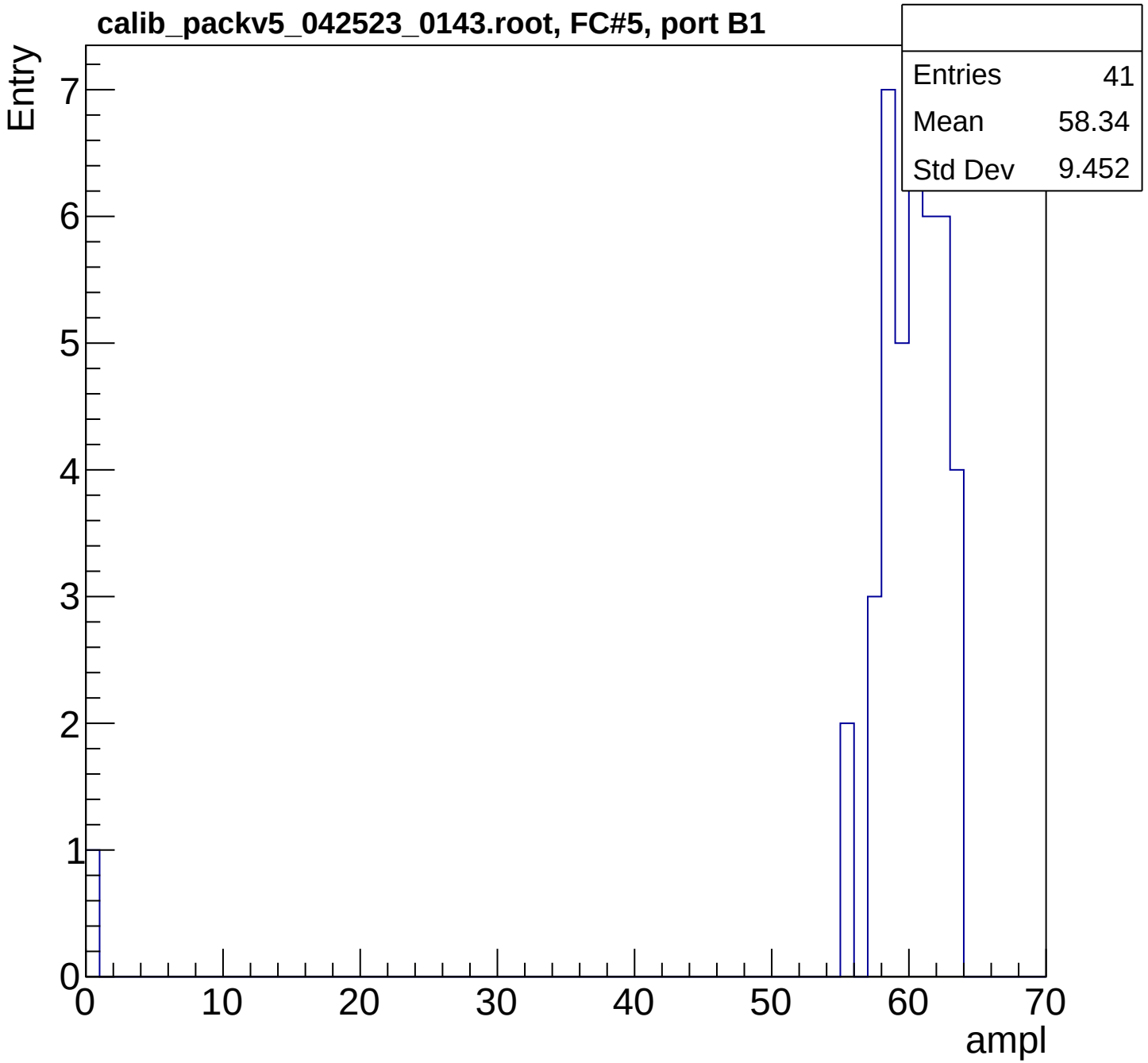

B0L000S, U23-ch85, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 30.94
Std Dev 3.036
Gaus mean : 31.1081
Gaus Width: 2.6831
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

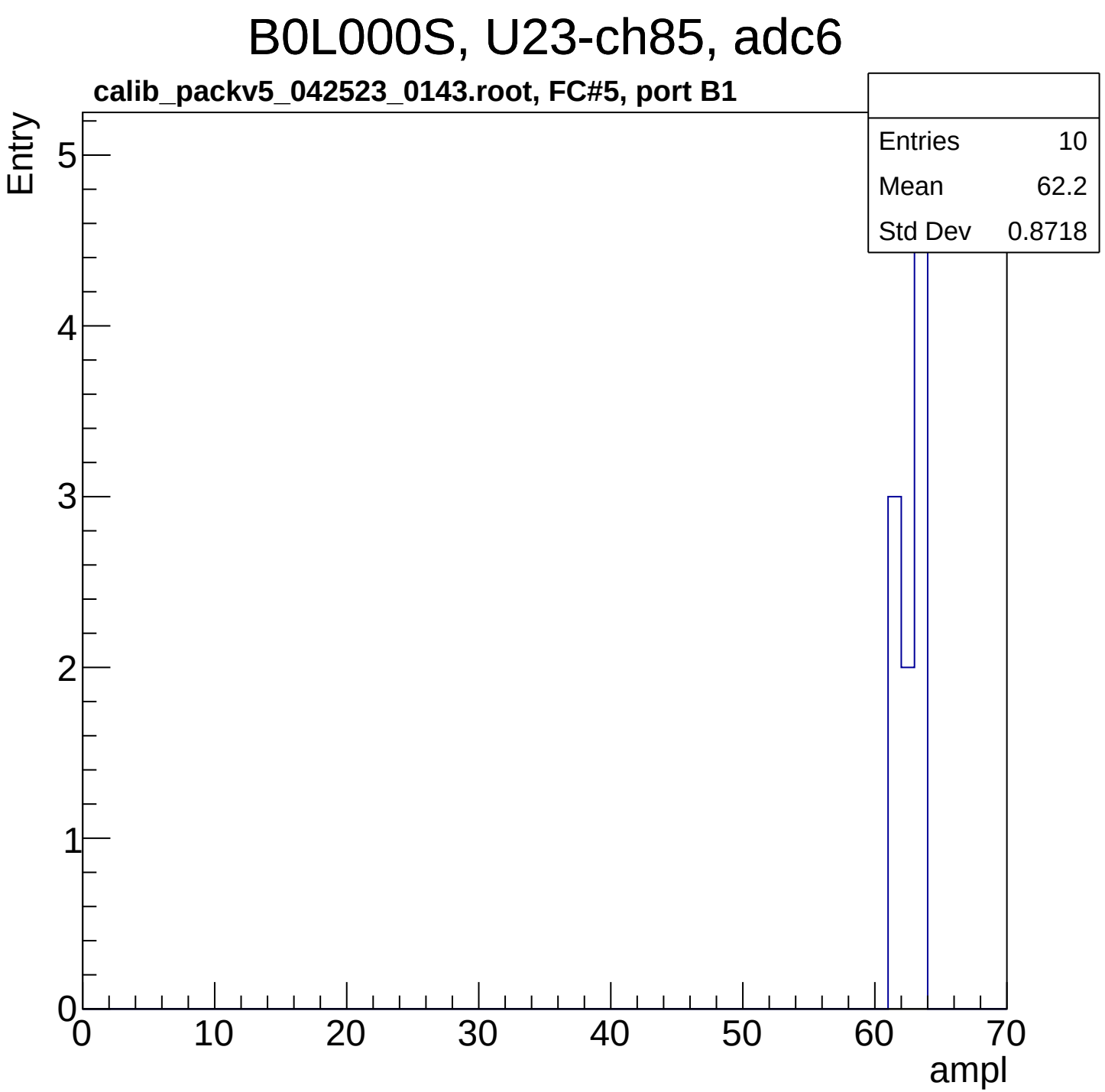

B0L000S, U23-ch85, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 37.7
Std Dev 3.047
Gaus mean : 37.8949
Gaus Width: 3.4800
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

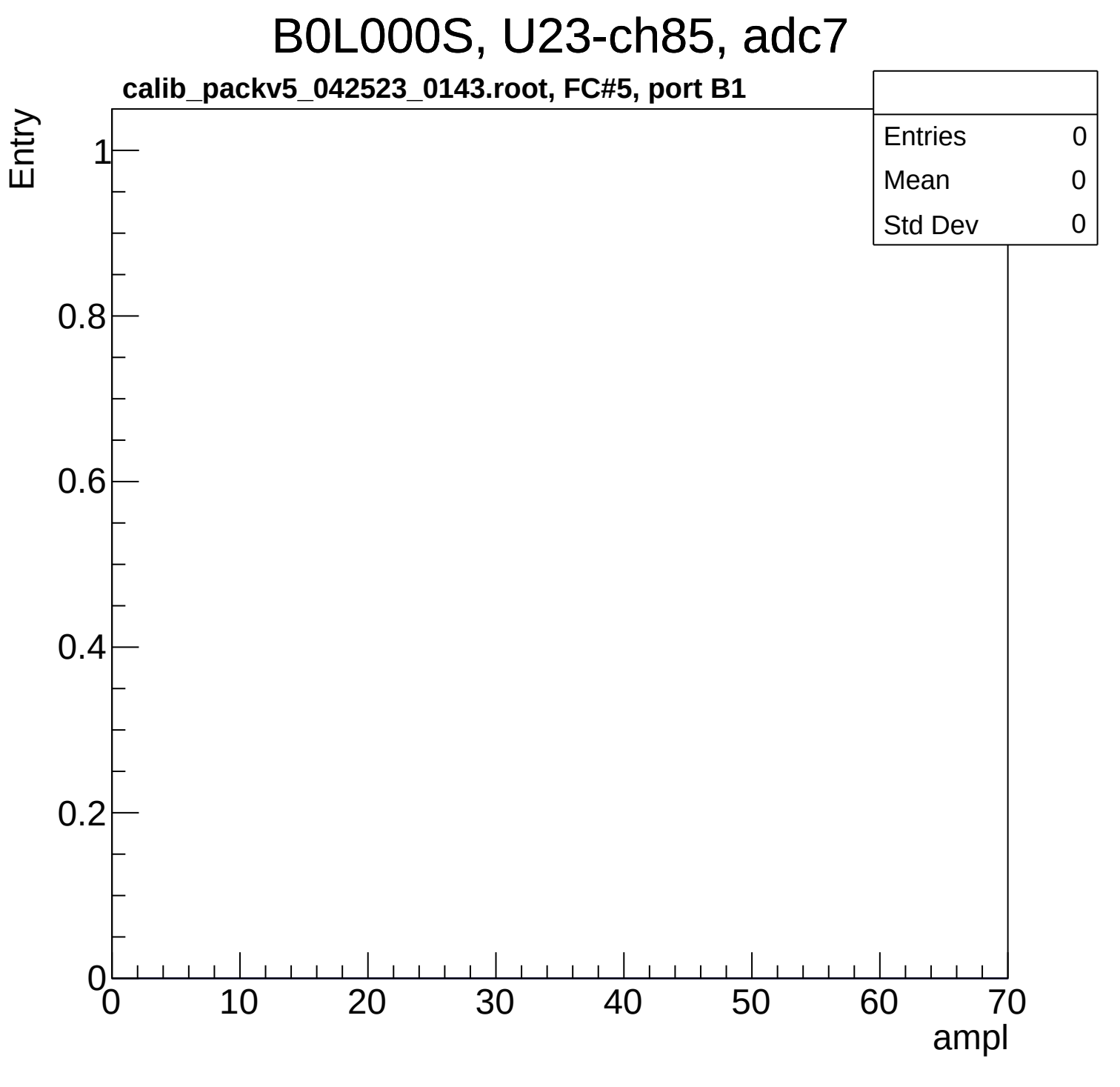

B0L000S, U23-ch85, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.04
Std Dev 3.605
Gaus mean : 44.9876
Gaus Width: 4.1814
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

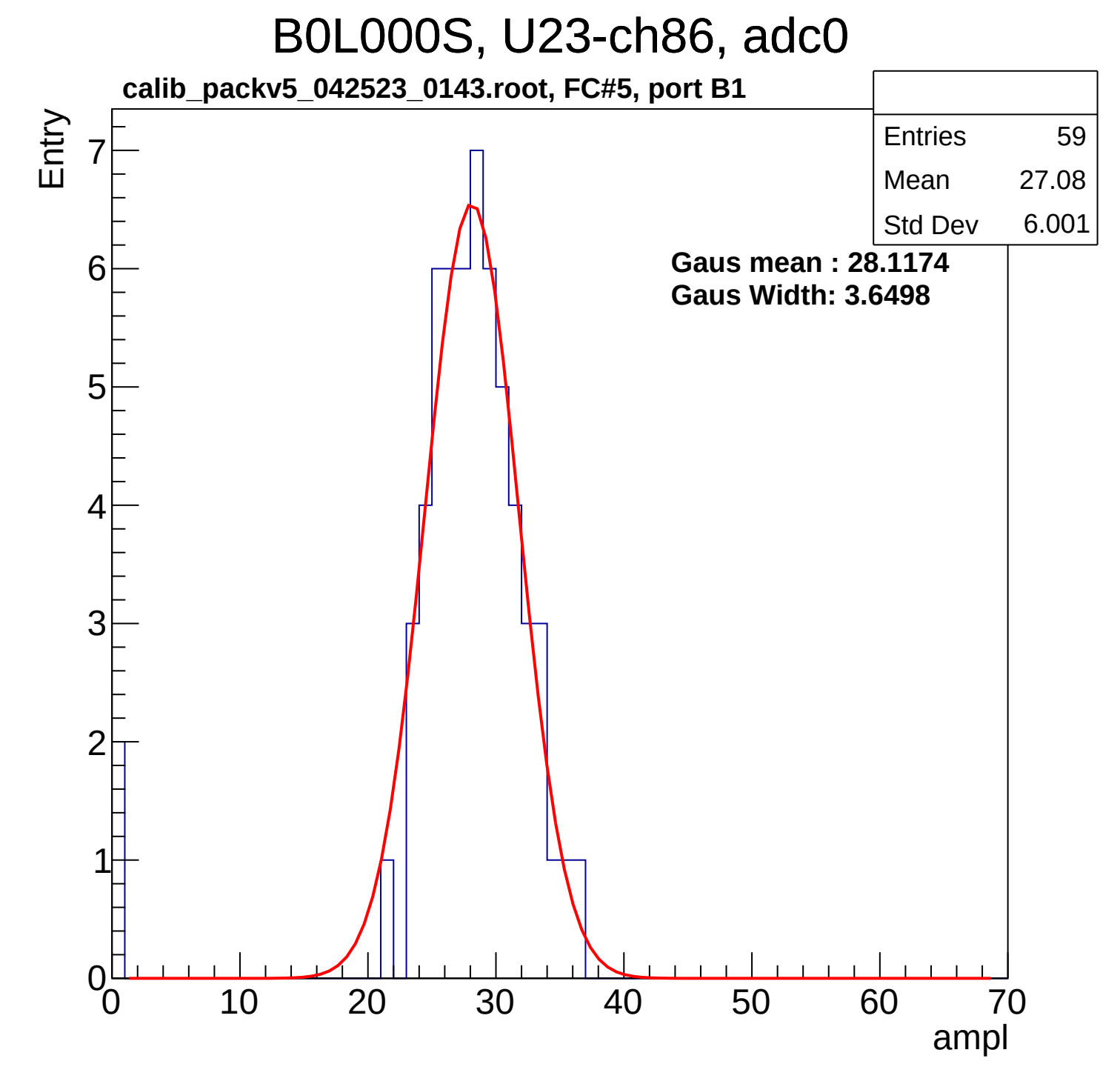

B0L000S, U23-ch85, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 48.94
Std Dev 3.328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

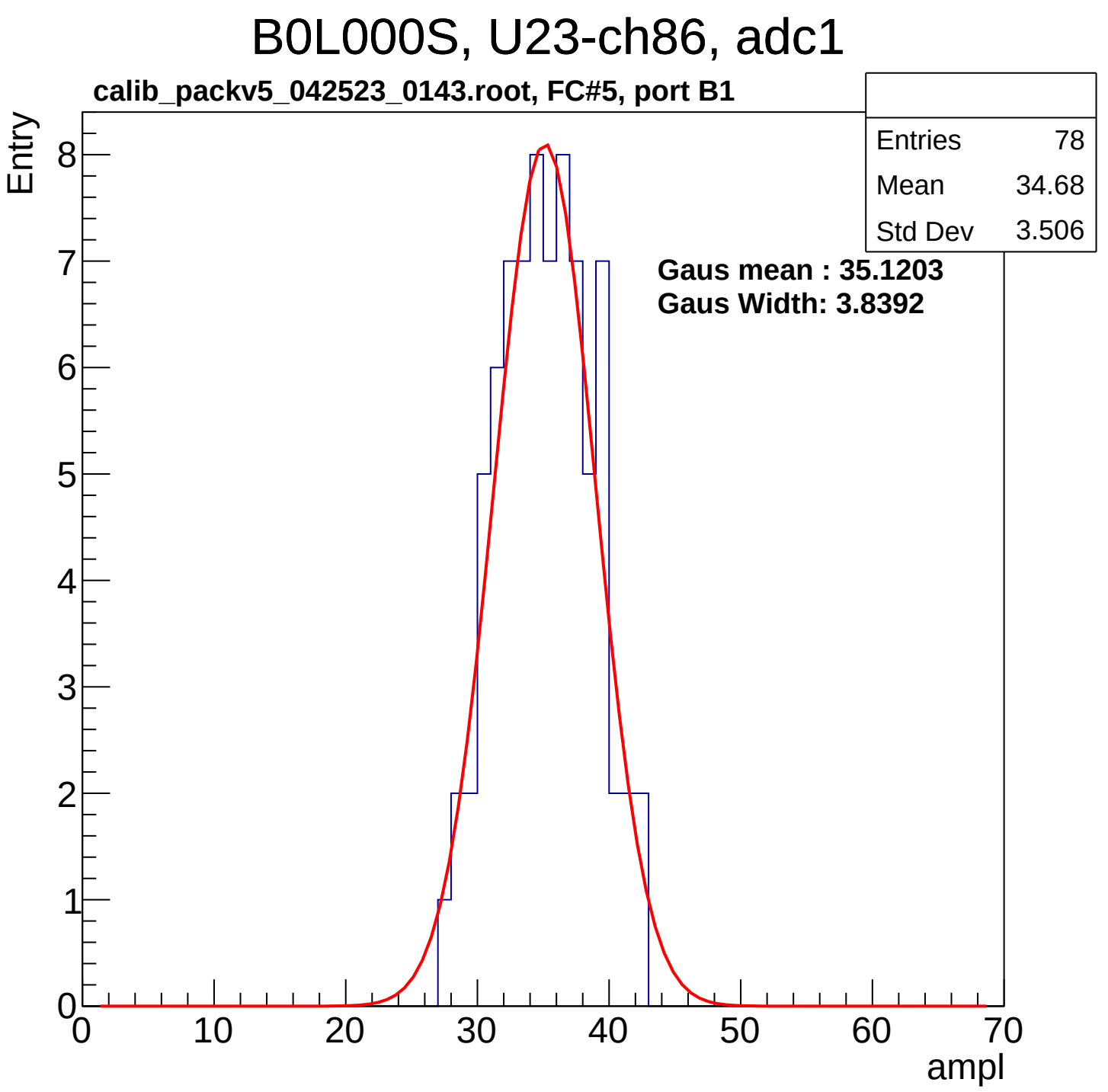

B0L000S, U23-ch85, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 55.2
Std Dev 3.318
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

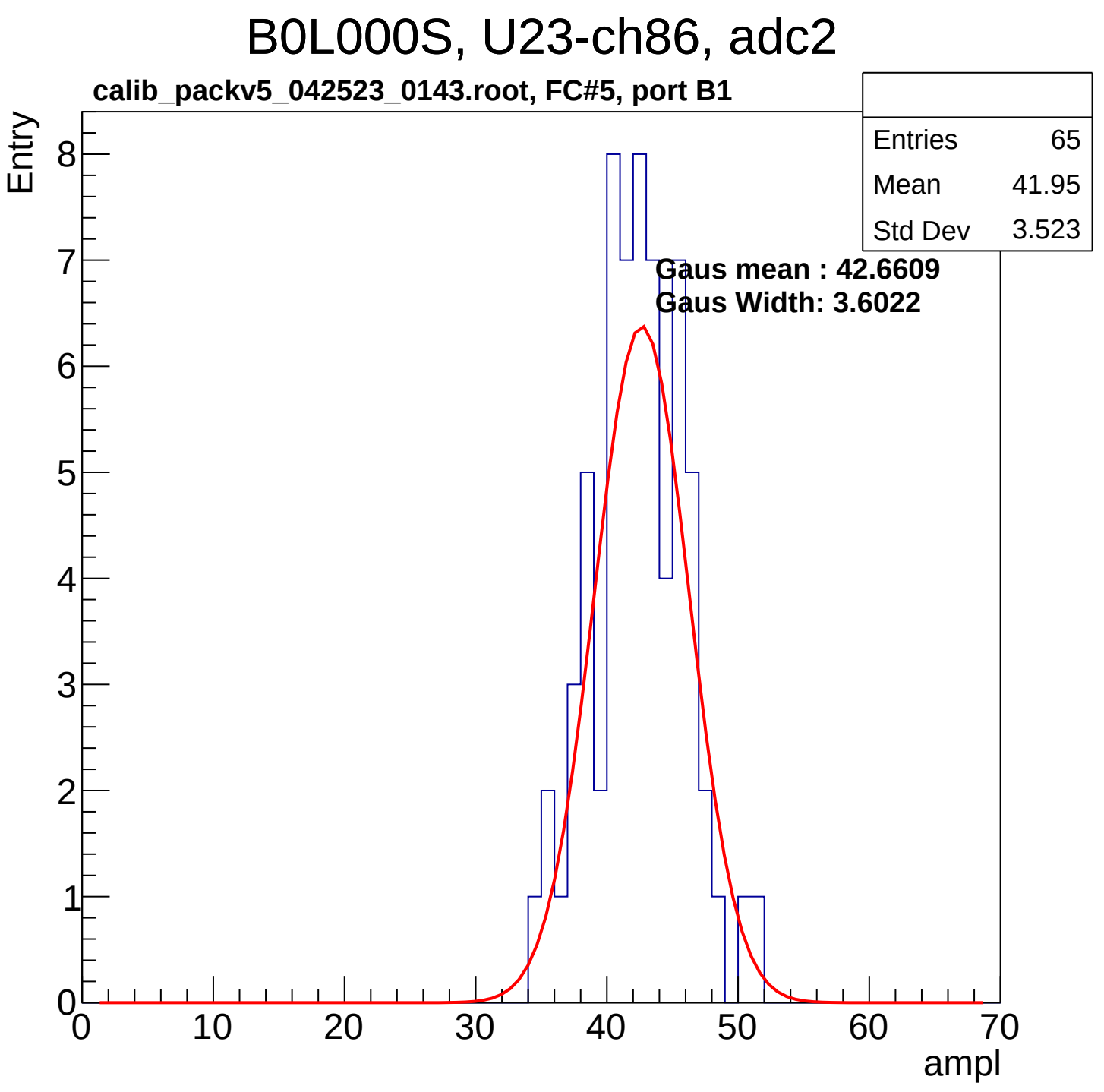

B0L000S, U23-ch85, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.34
Std Dev 9.452
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

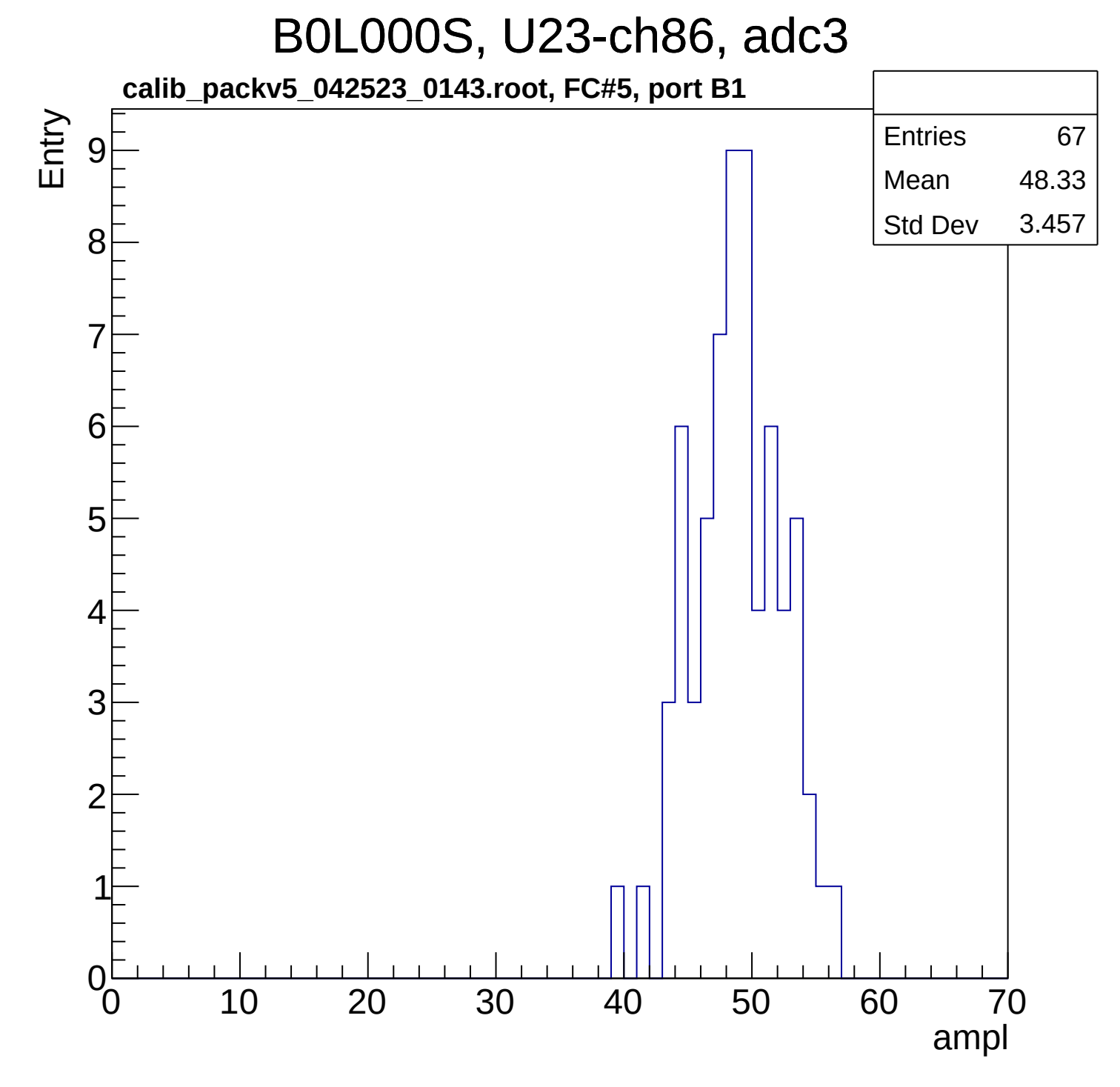

B0L000S, U23-ch85, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 62.2
Std Dev 0.8718
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

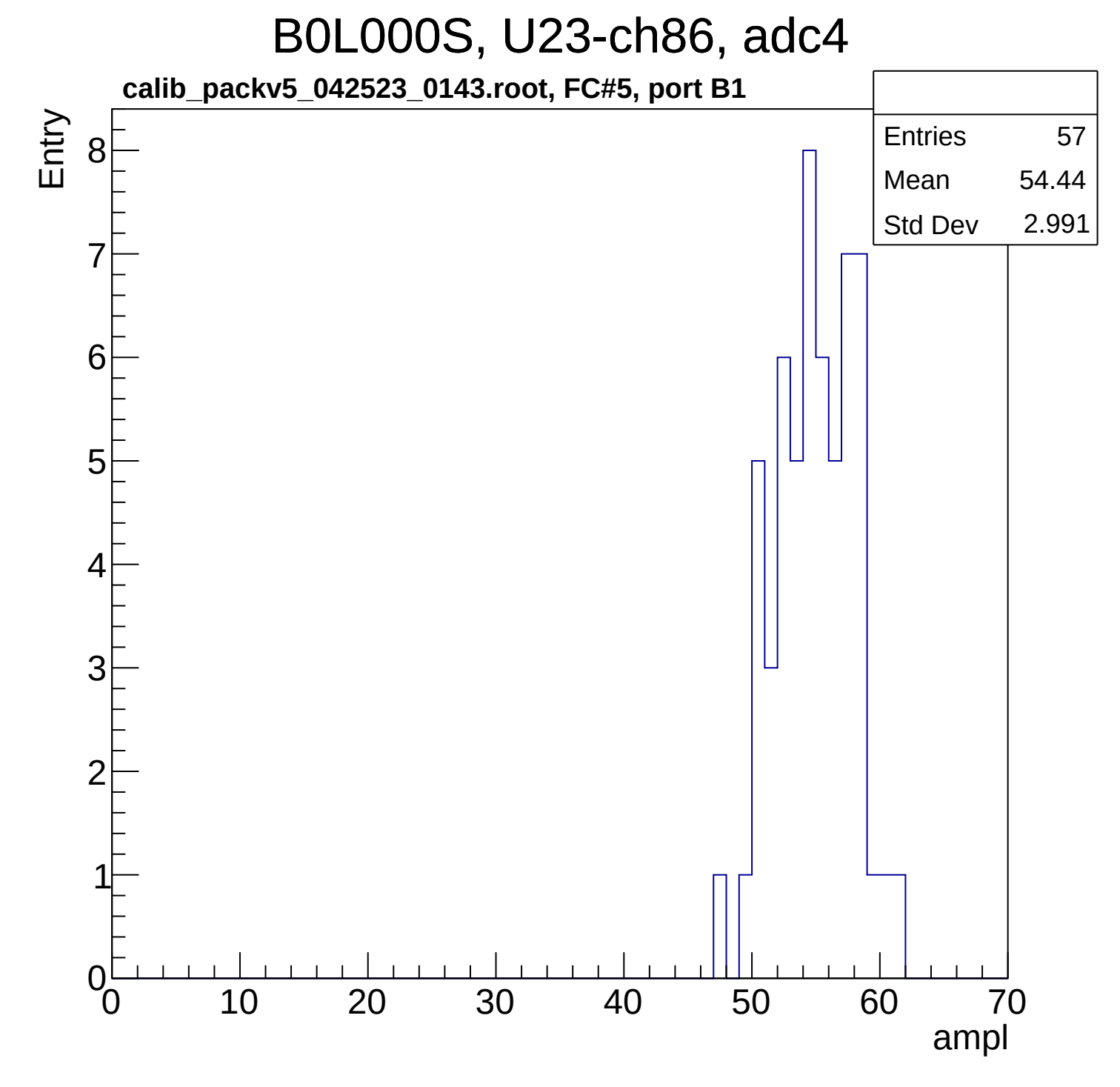

B0L000S, U23-ch85, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

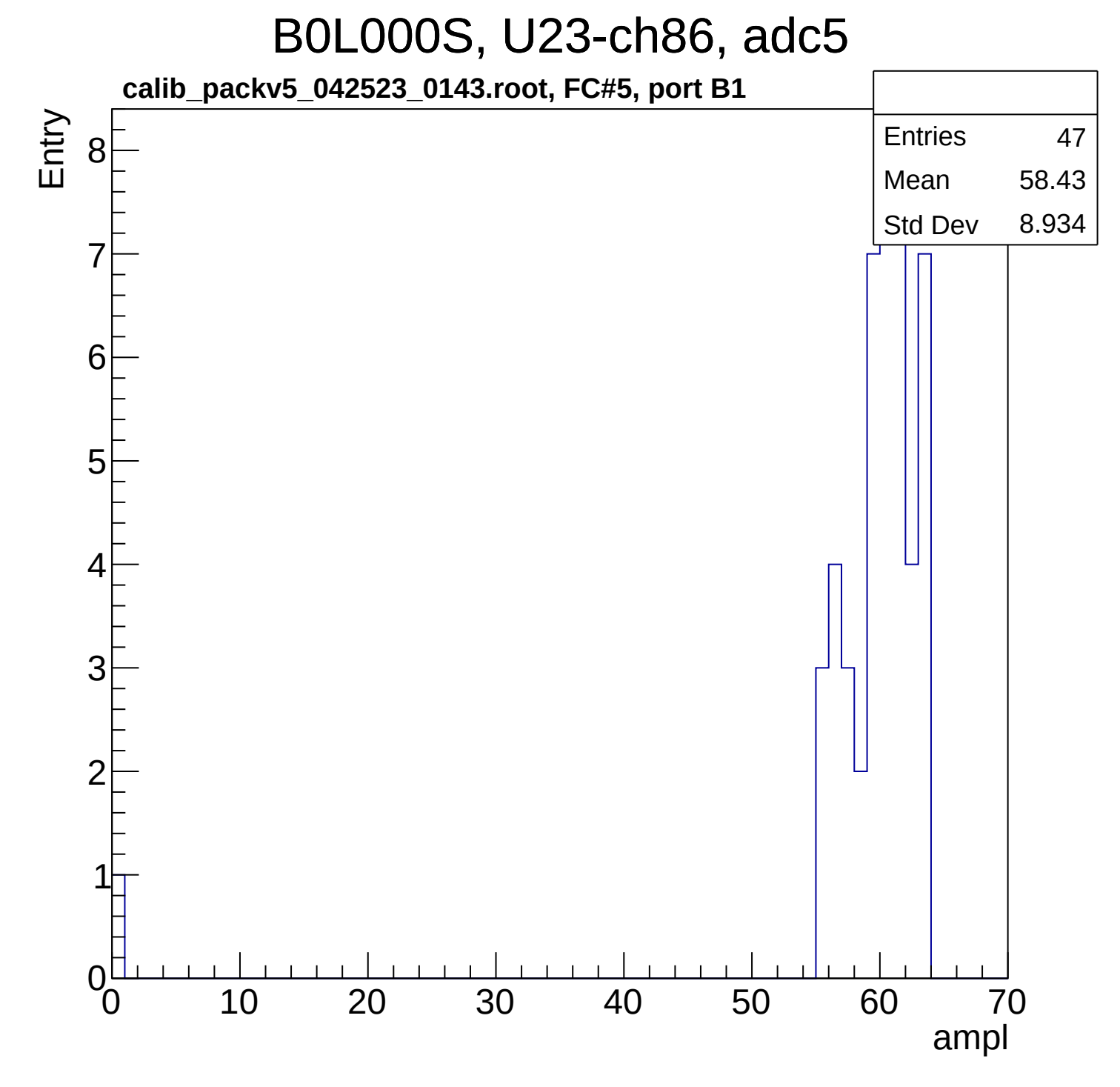

B0L000S, U23-ch86, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.08
Std Dev 6.001
Gaus mean : 28.1174
Gaus Width: 3.6498
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

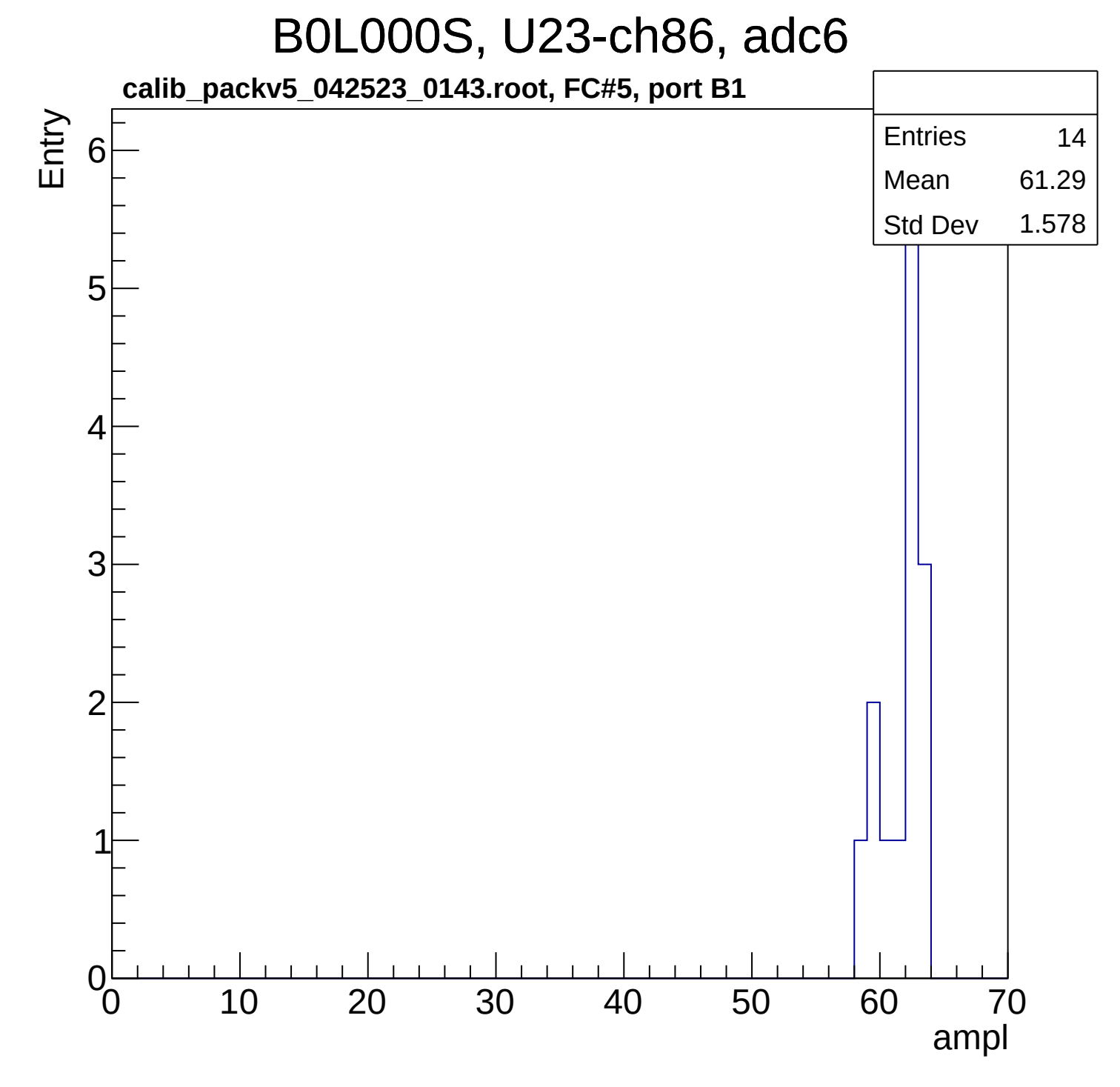

B0L000S, U23-ch86, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 34.68
Std Dev 3.506
Gaus mean : 35.1203
Gaus Width: 3.8392
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

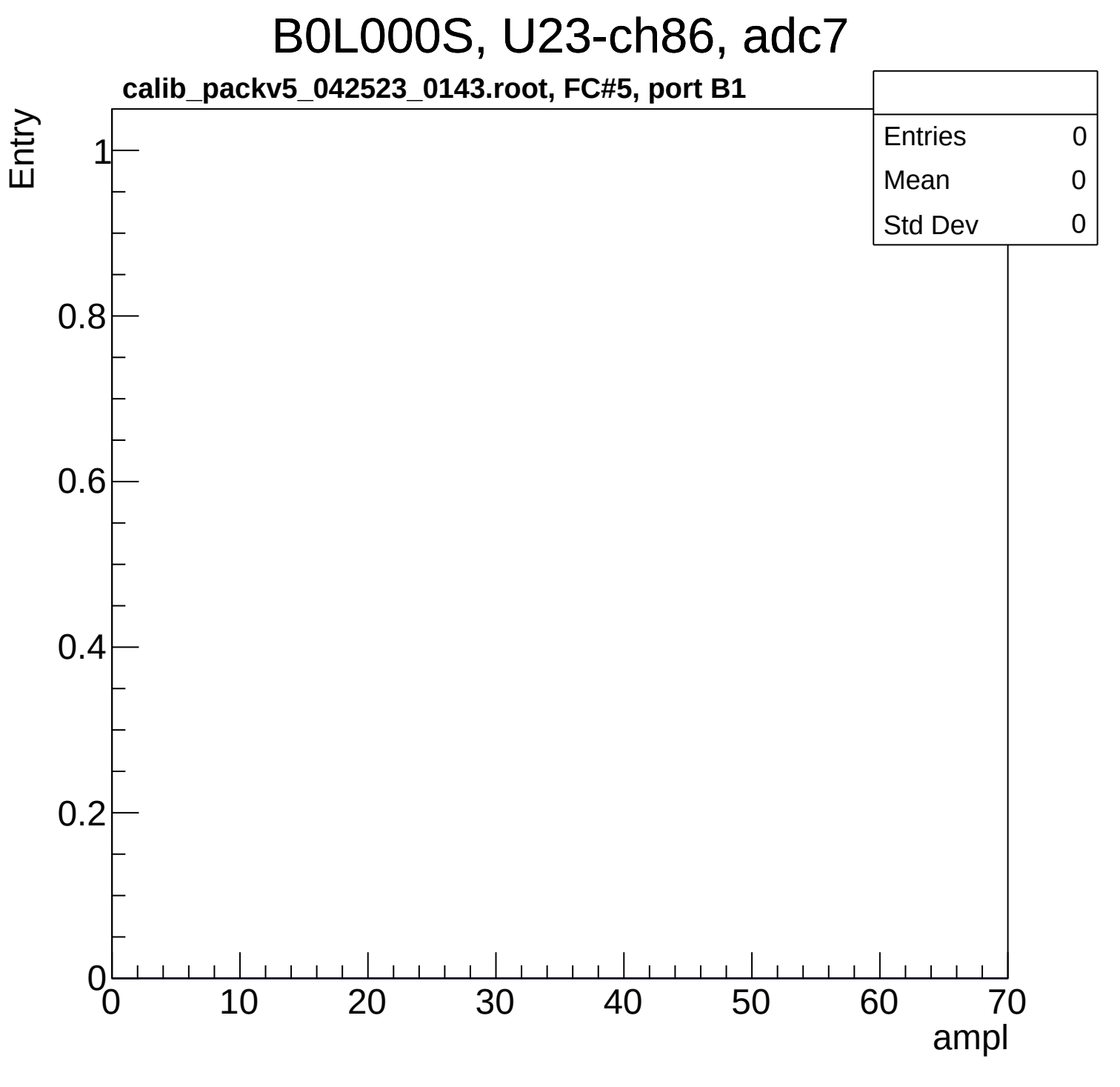

B0L000S, U23-ch86, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.95
Std Dev 3.523
Gaus mean : 42.6609
Gaus Width: 3.6022
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

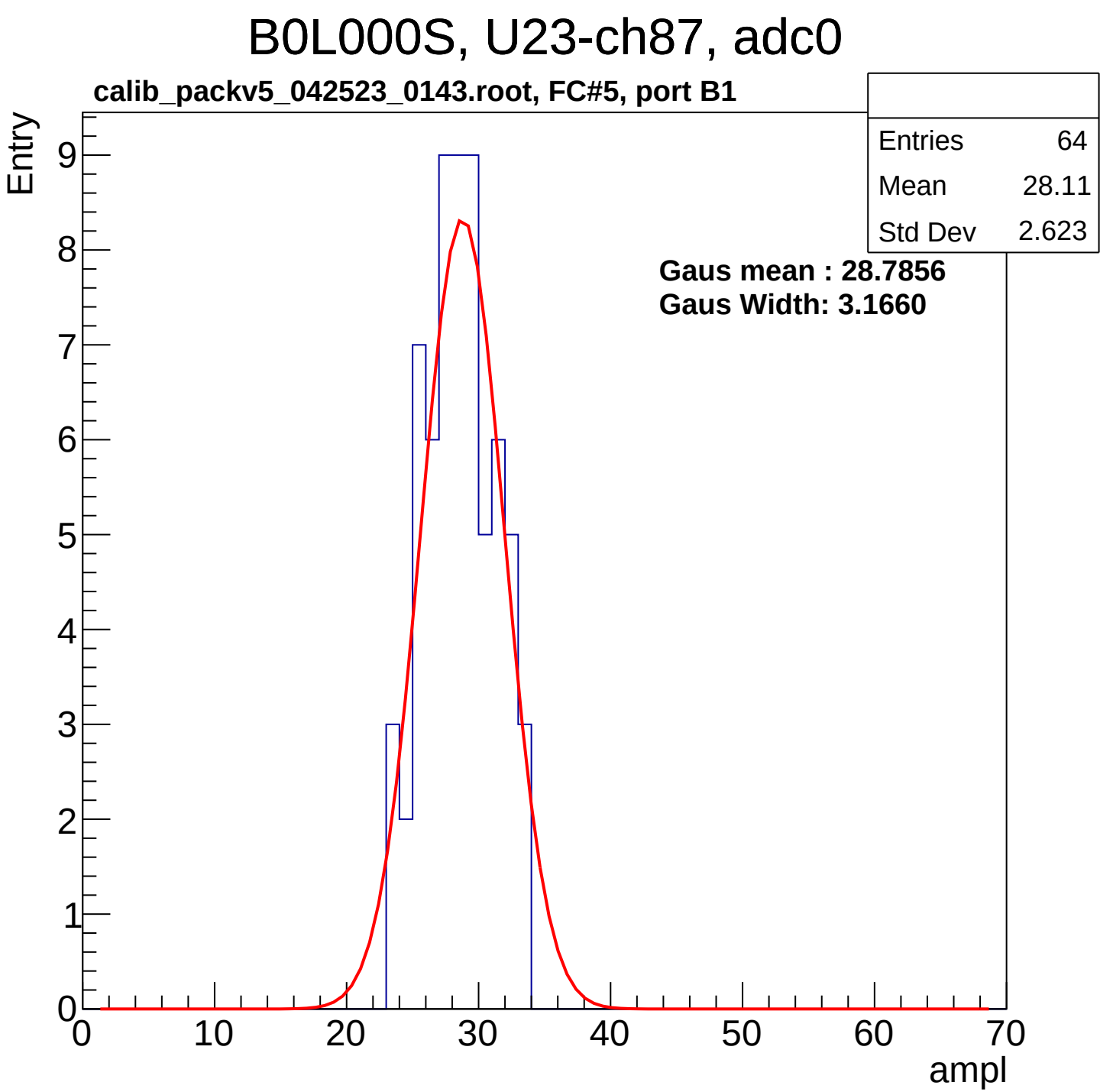

B0L000S, U23-ch86, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.33
Std Dev 3.457
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

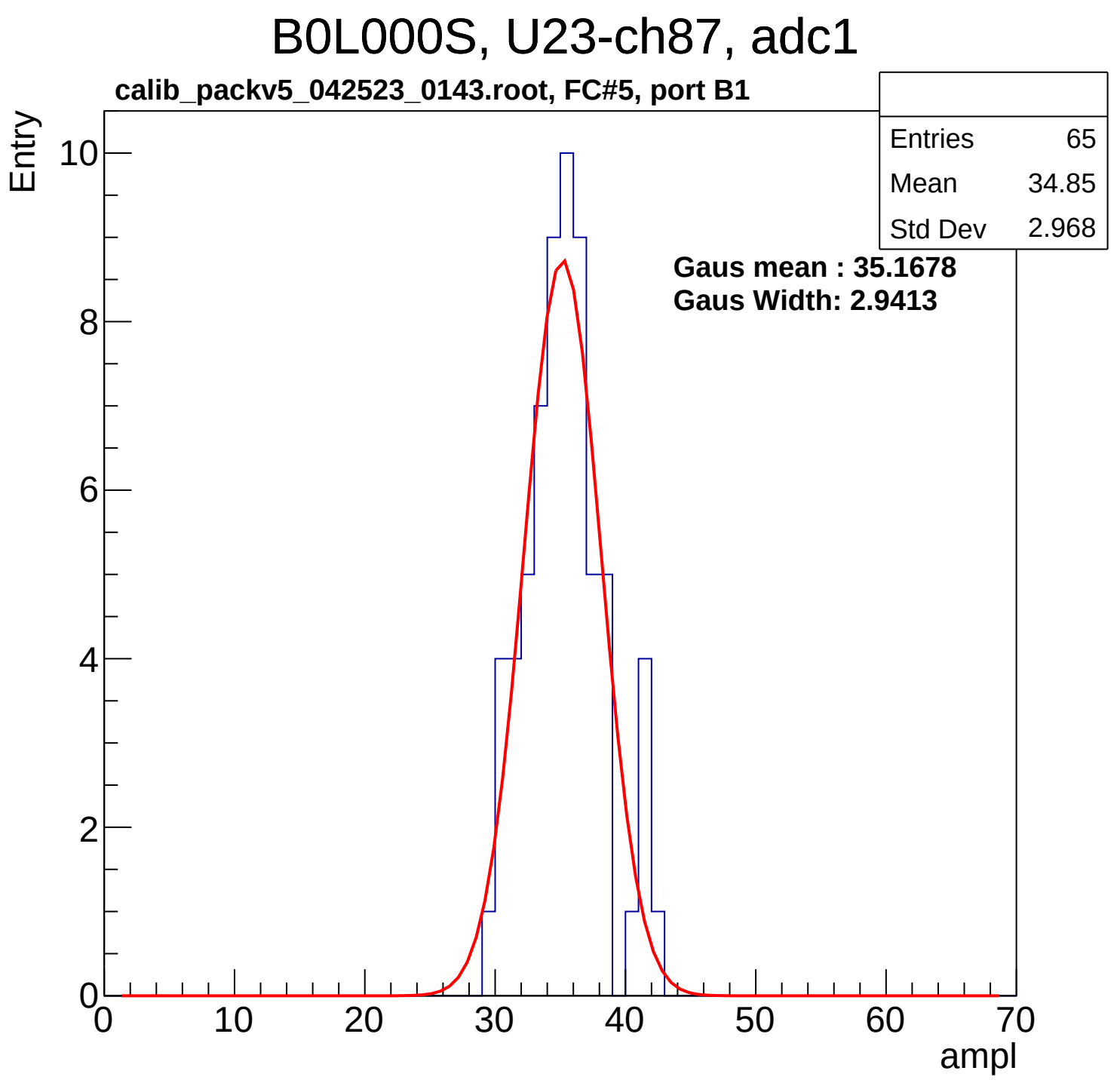

B0L000S, U23-ch86, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 54.44
Std Dev 2.991
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

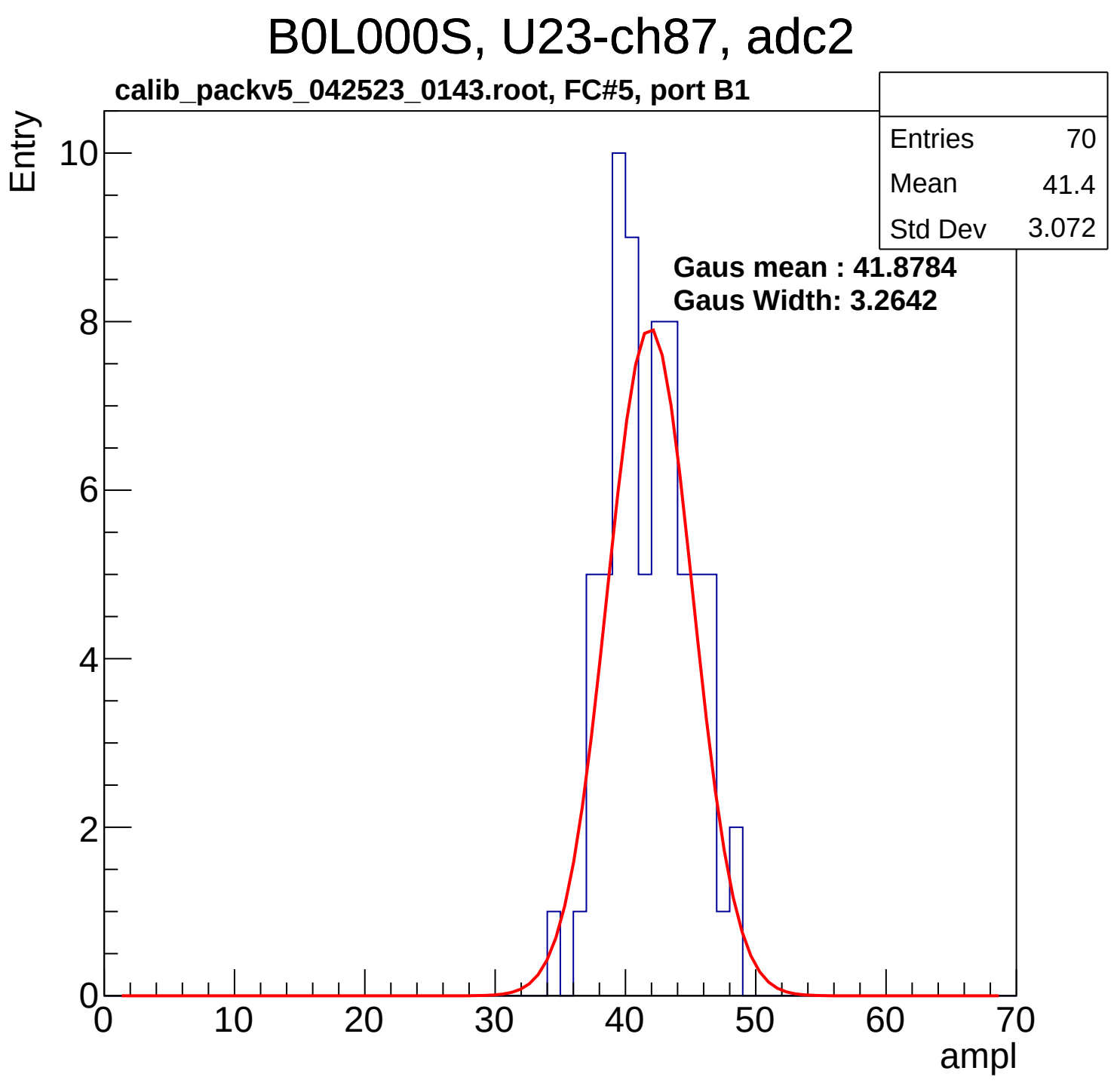

B0L000S, U23-ch86, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.43
Std Dev 8.934
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

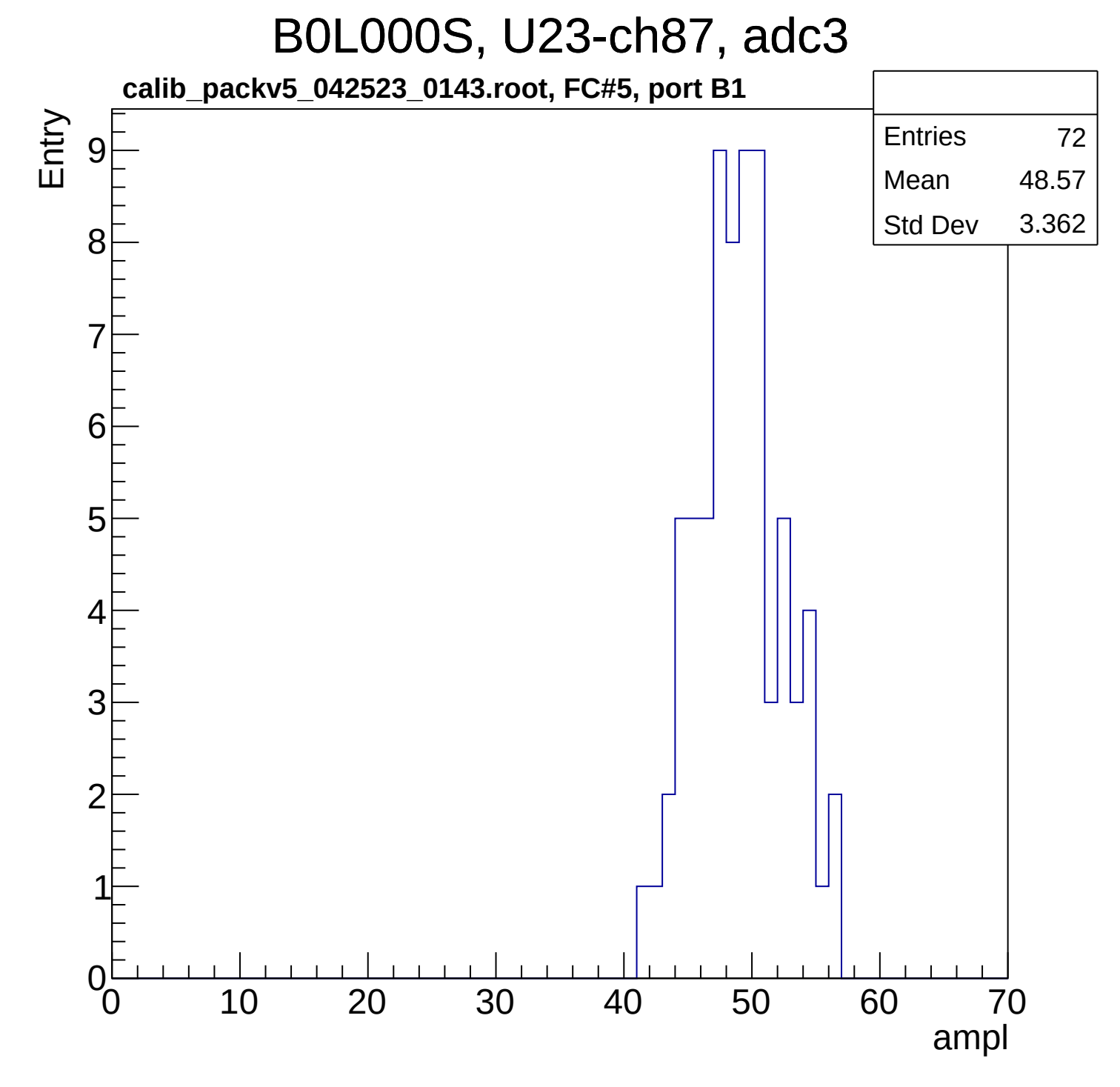

B0L000S, U23-ch86, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.29
Std Dev 1.578
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

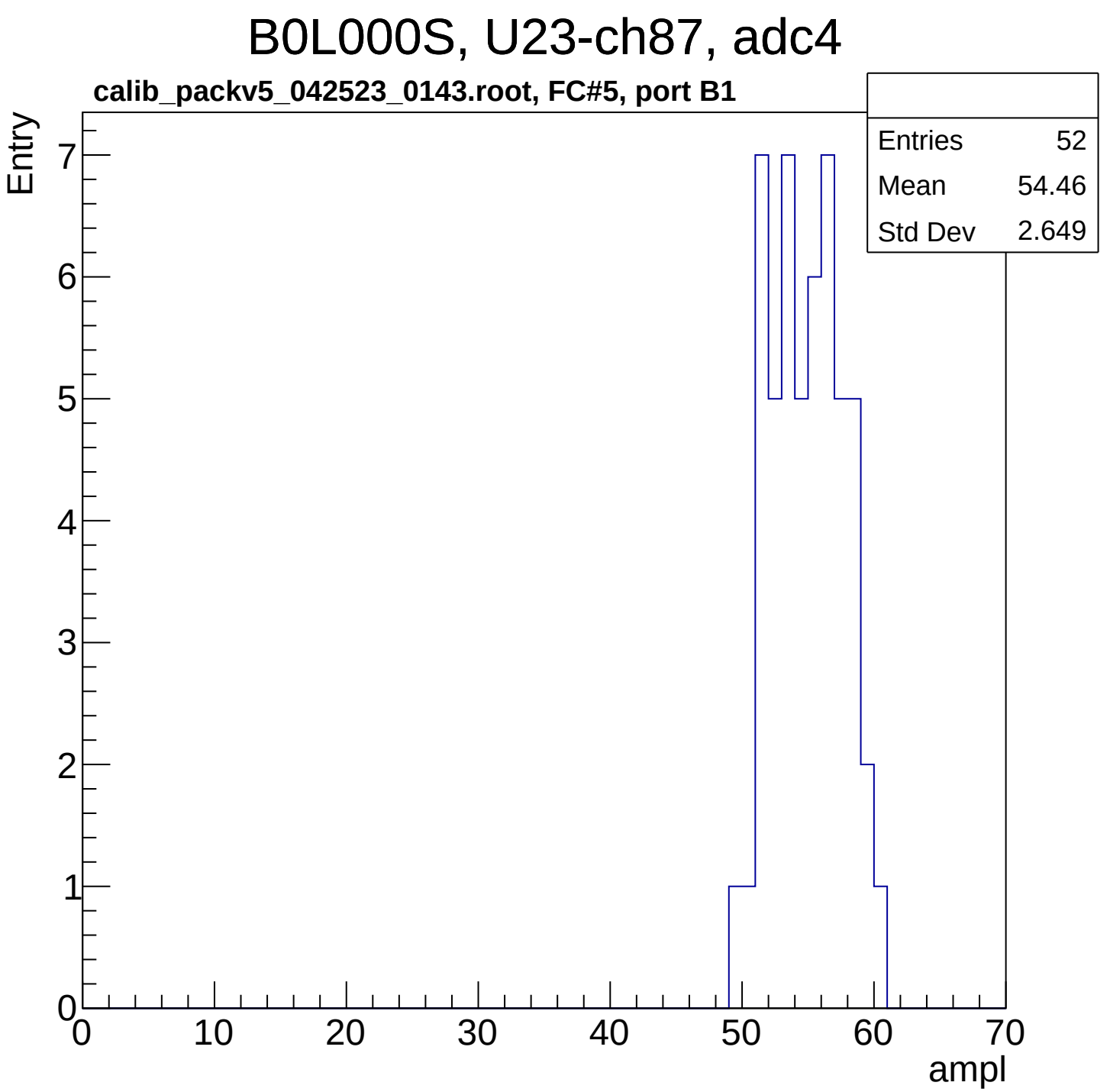

B0L000S, U23-ch86, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

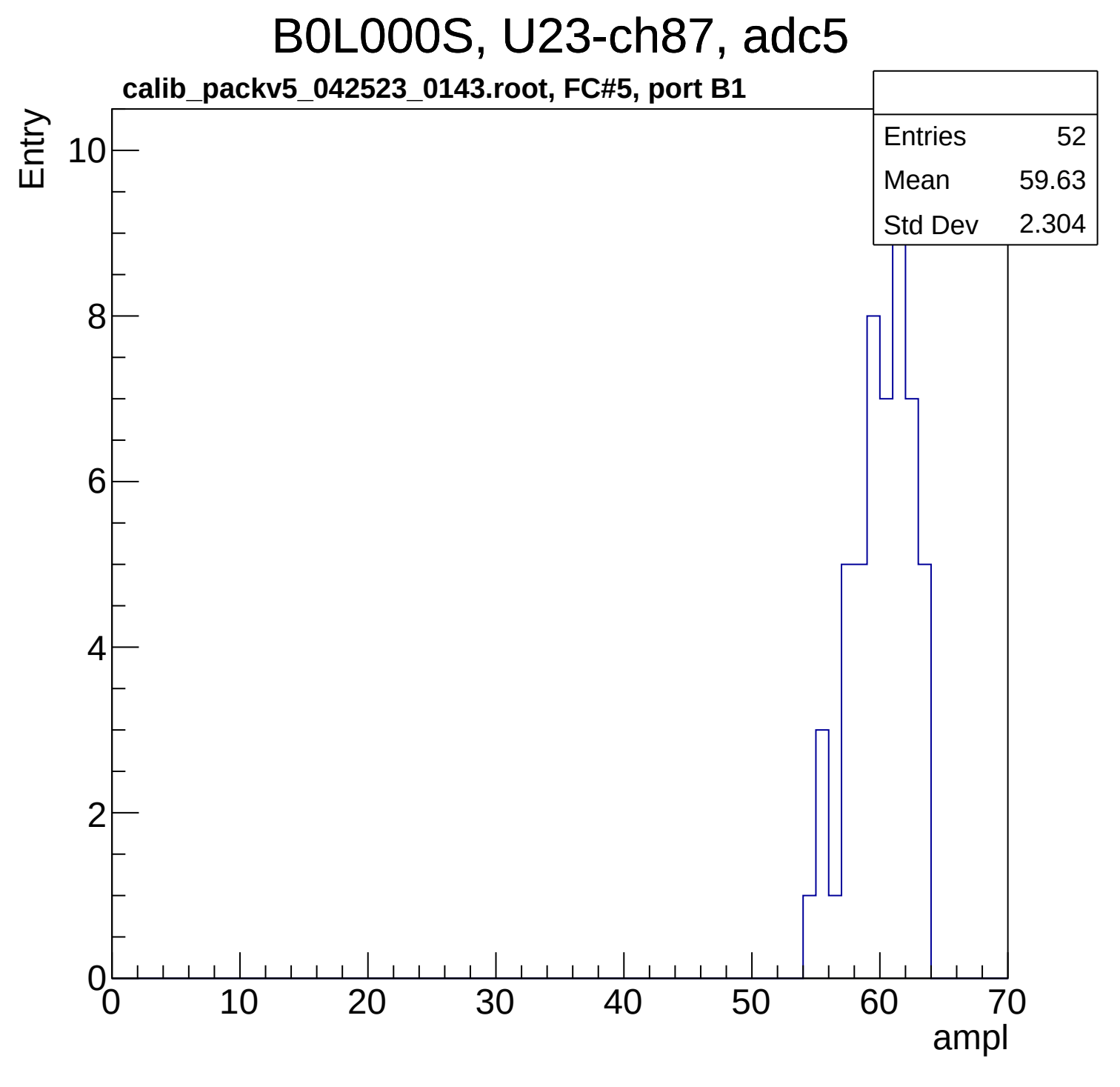

B0L000S, U23-ch87, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 28.11
Std Dev 2.623
Gaus mean : 28.7856
Gaus Width: 3.1660
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

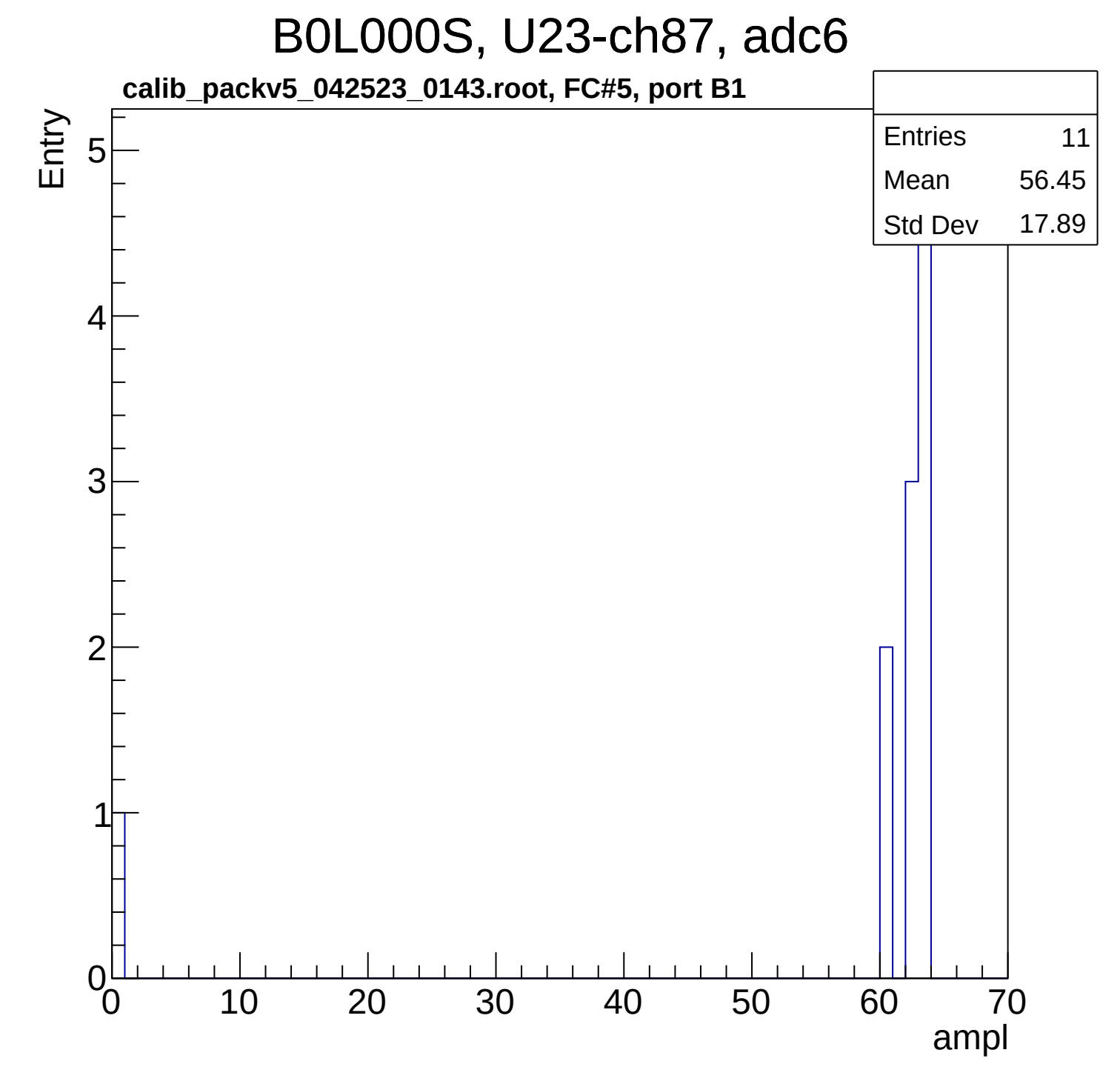

B0L000S, U23-ch87, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 34.85
Std Dev 2.968
Gaus mean : 35.1678
Gaus Width: 2.9413
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

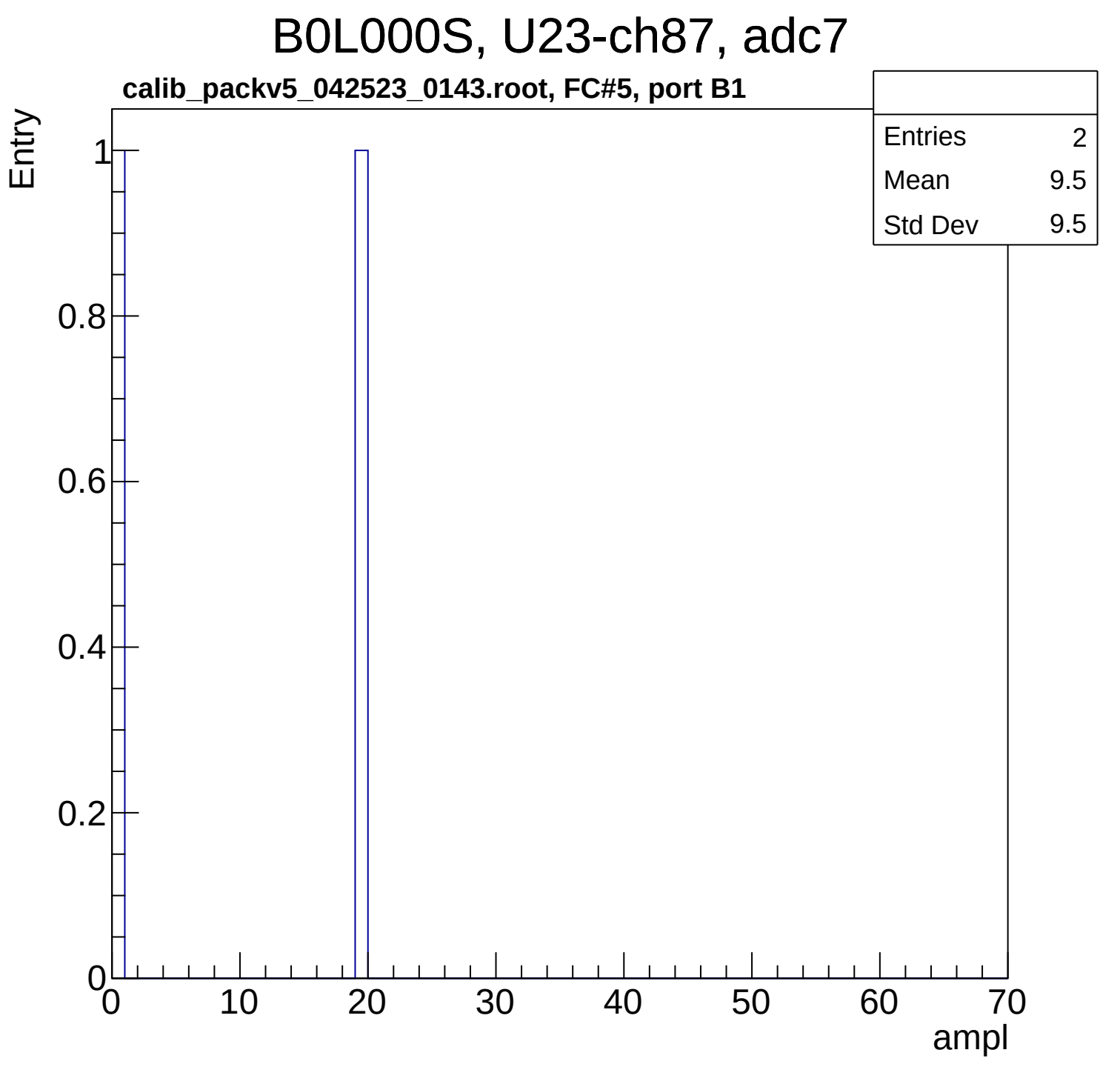

B0L000S, U23-ch87, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 41.4
Std Dev 3.072
Gaus mean : 41.8784
Gaus Width: 3.2642
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

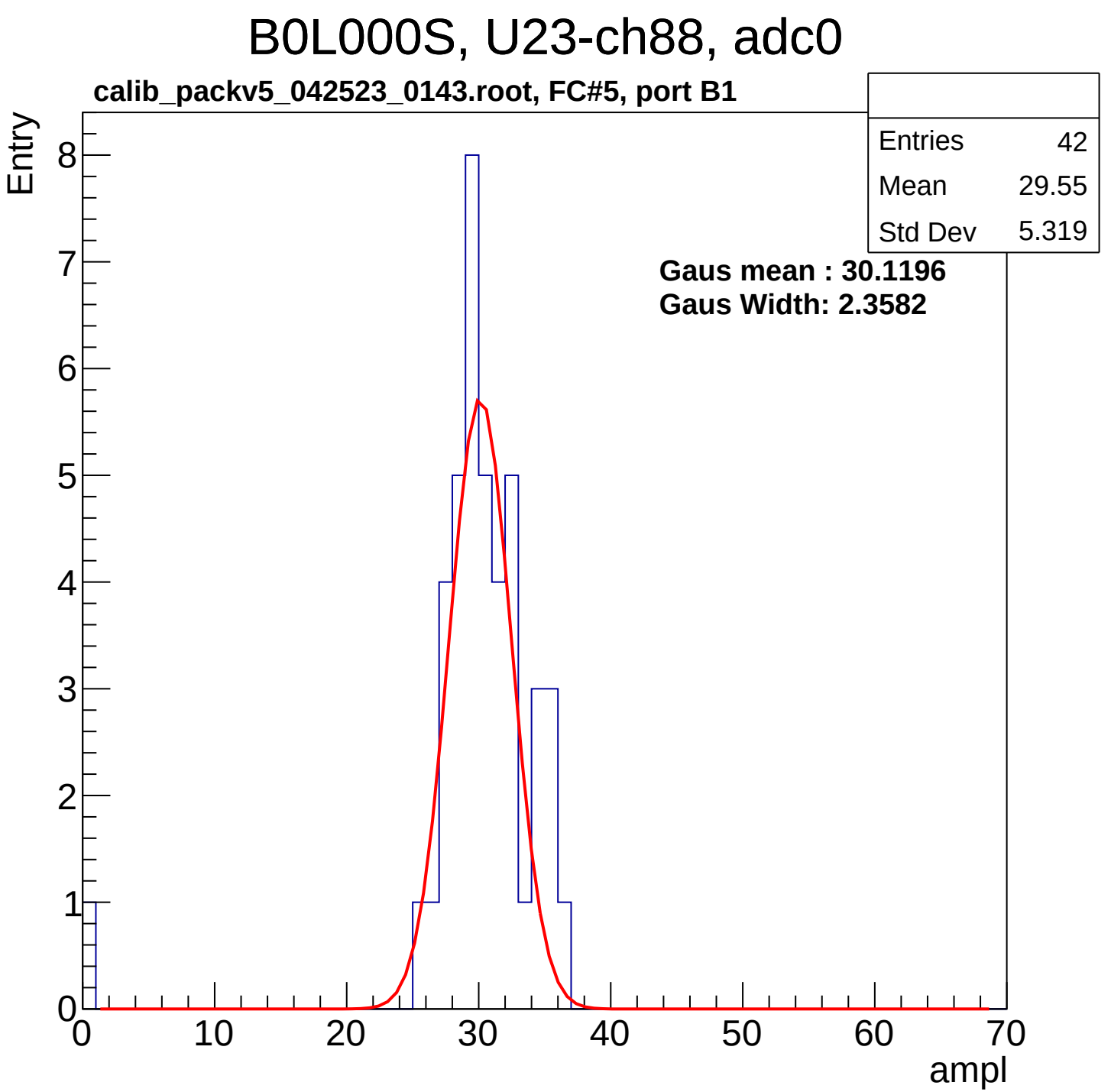

B0L000S, U23-ch87, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.57
Std Dev 3.362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

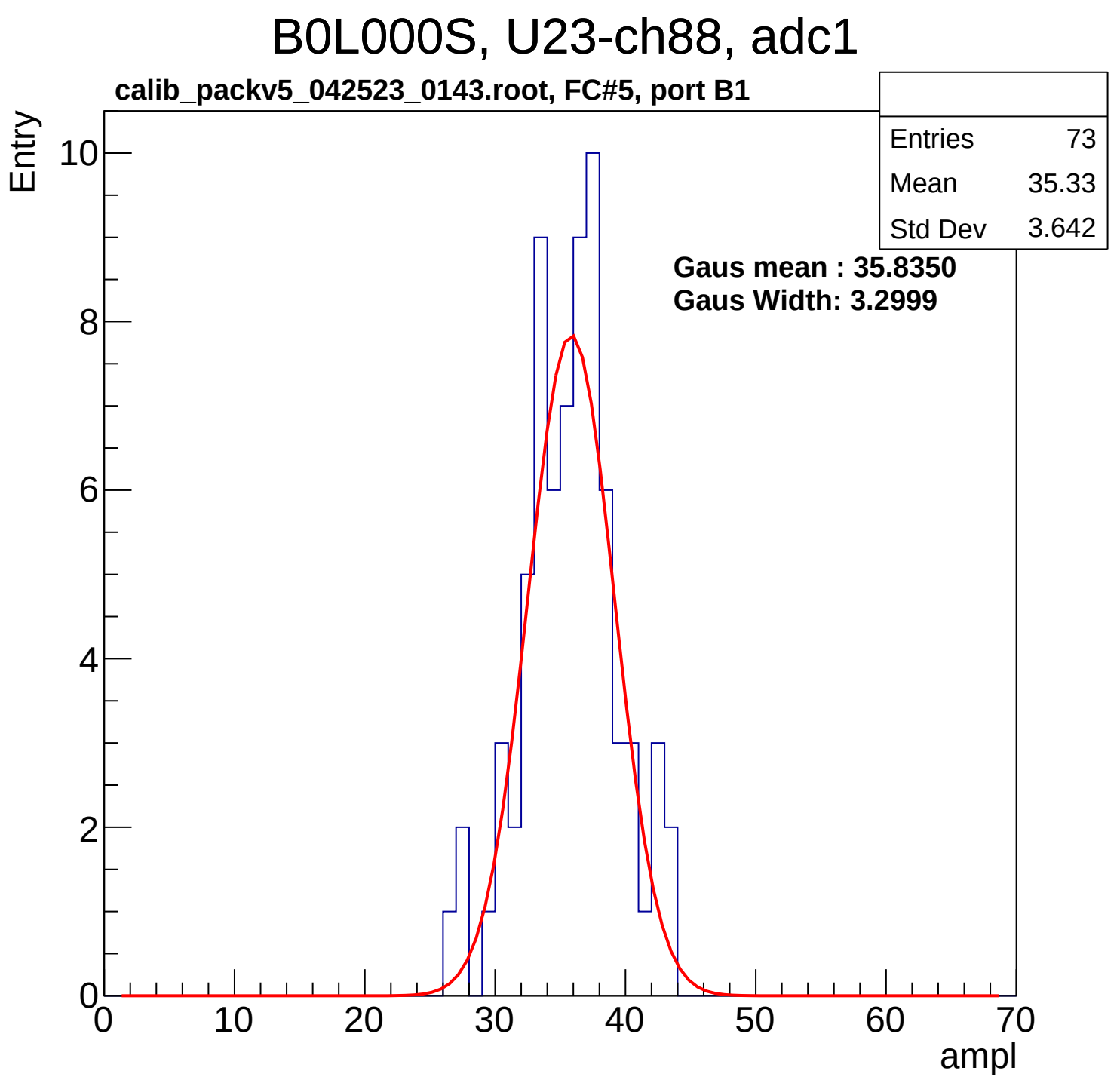

B0L000S, U23-ch87, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.46
Std Dev 2.649
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

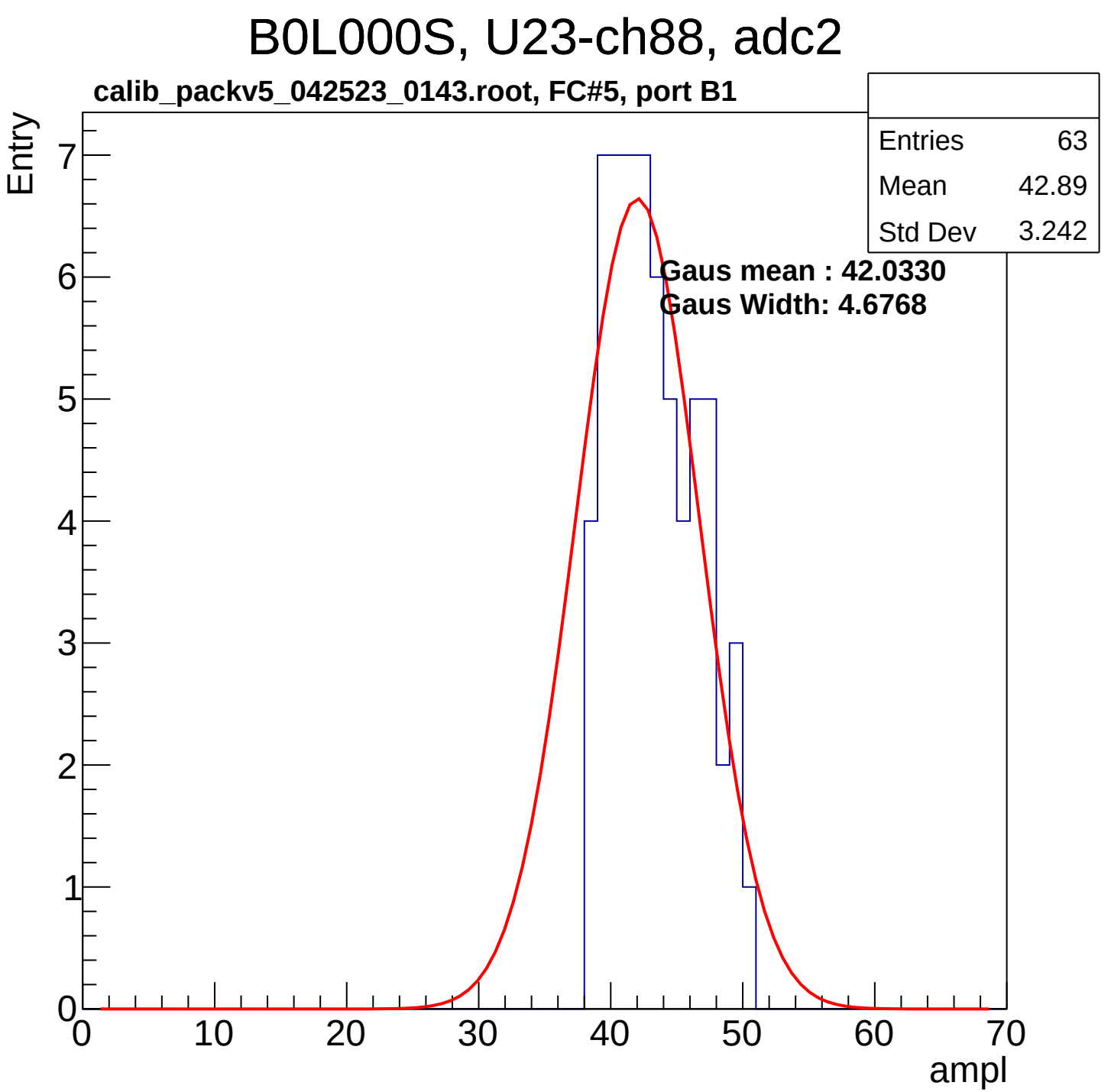

B0L000S, U23-ch87, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.63
Std Dev 2.304
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

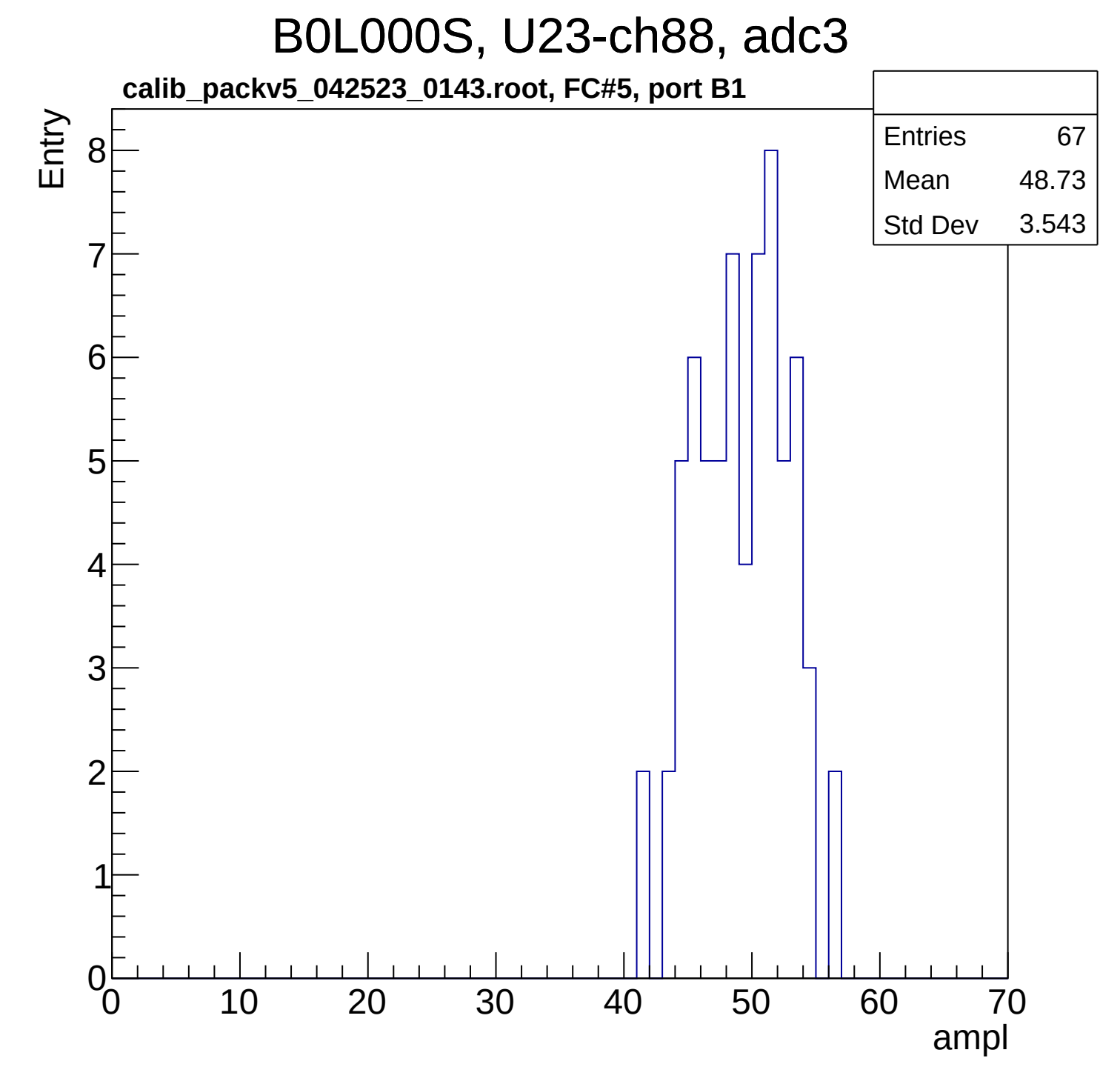

B0L000S, U23-ch87, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 56.45
Std Dev 17.89
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

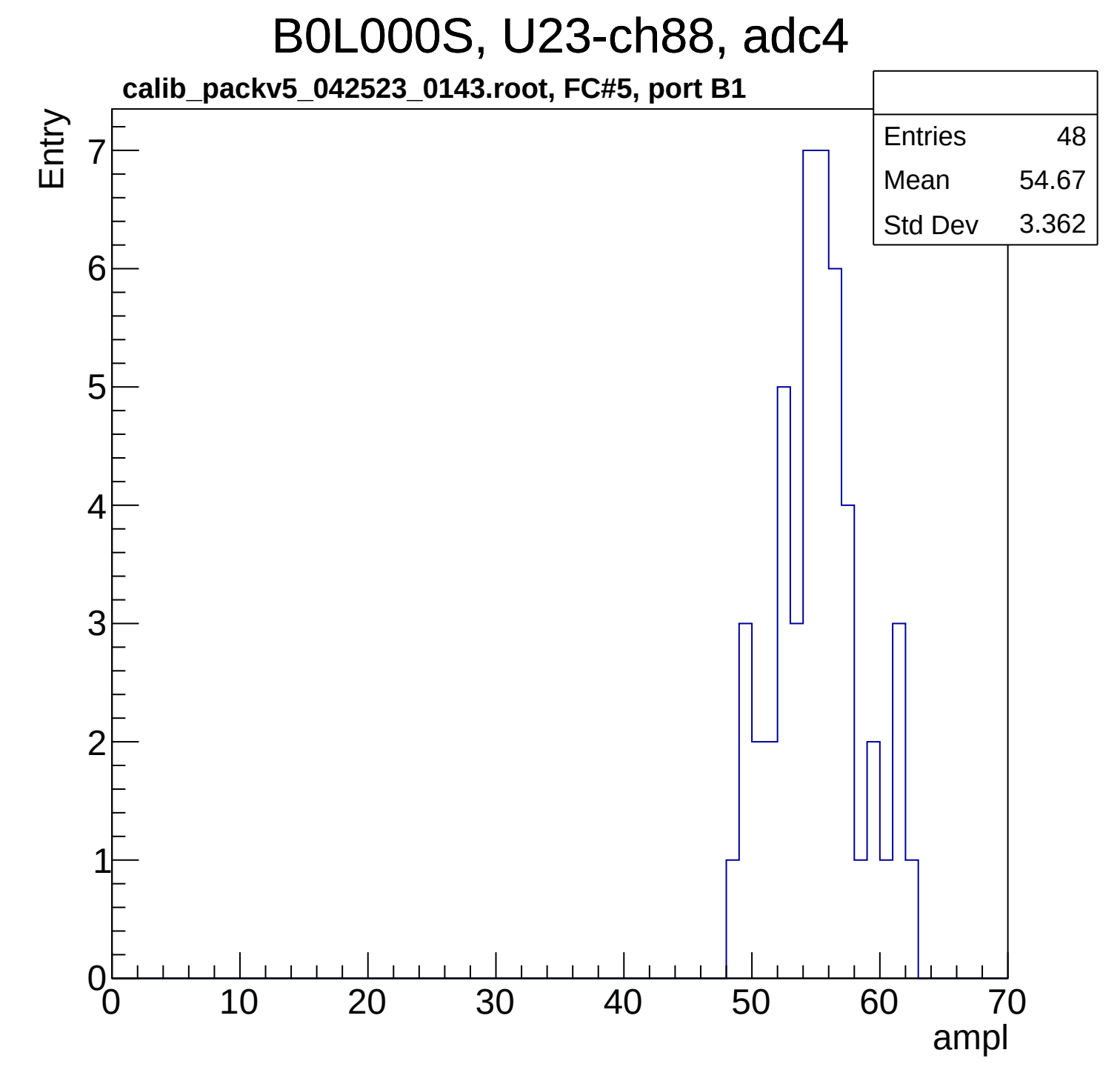

B0L000S, U23-ch87, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

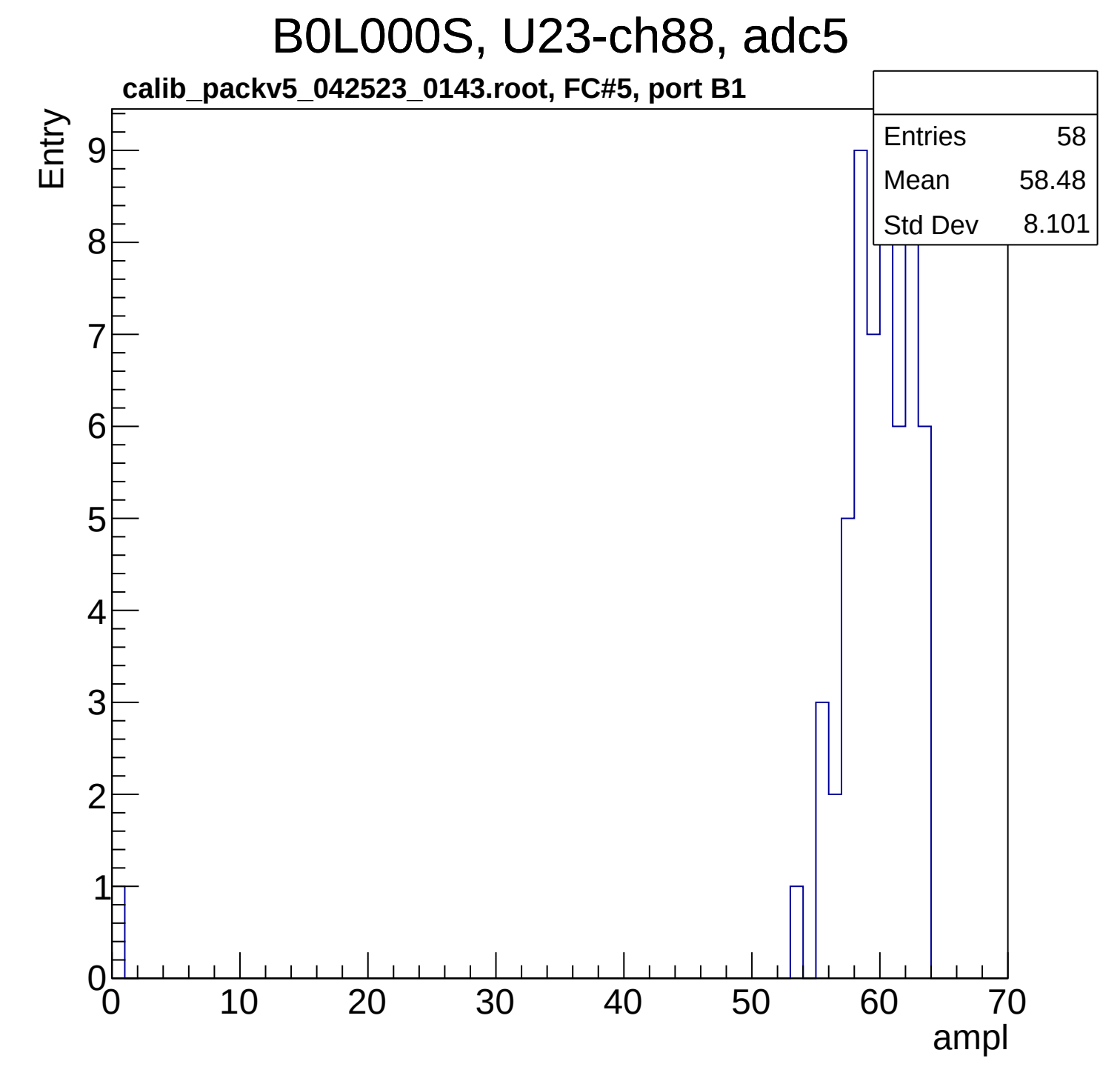

B0L000S, U23-ch88, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 29.55
Std Dev 5.319
Gaus mean : 30.1196
Gaus Width: 2.3582
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

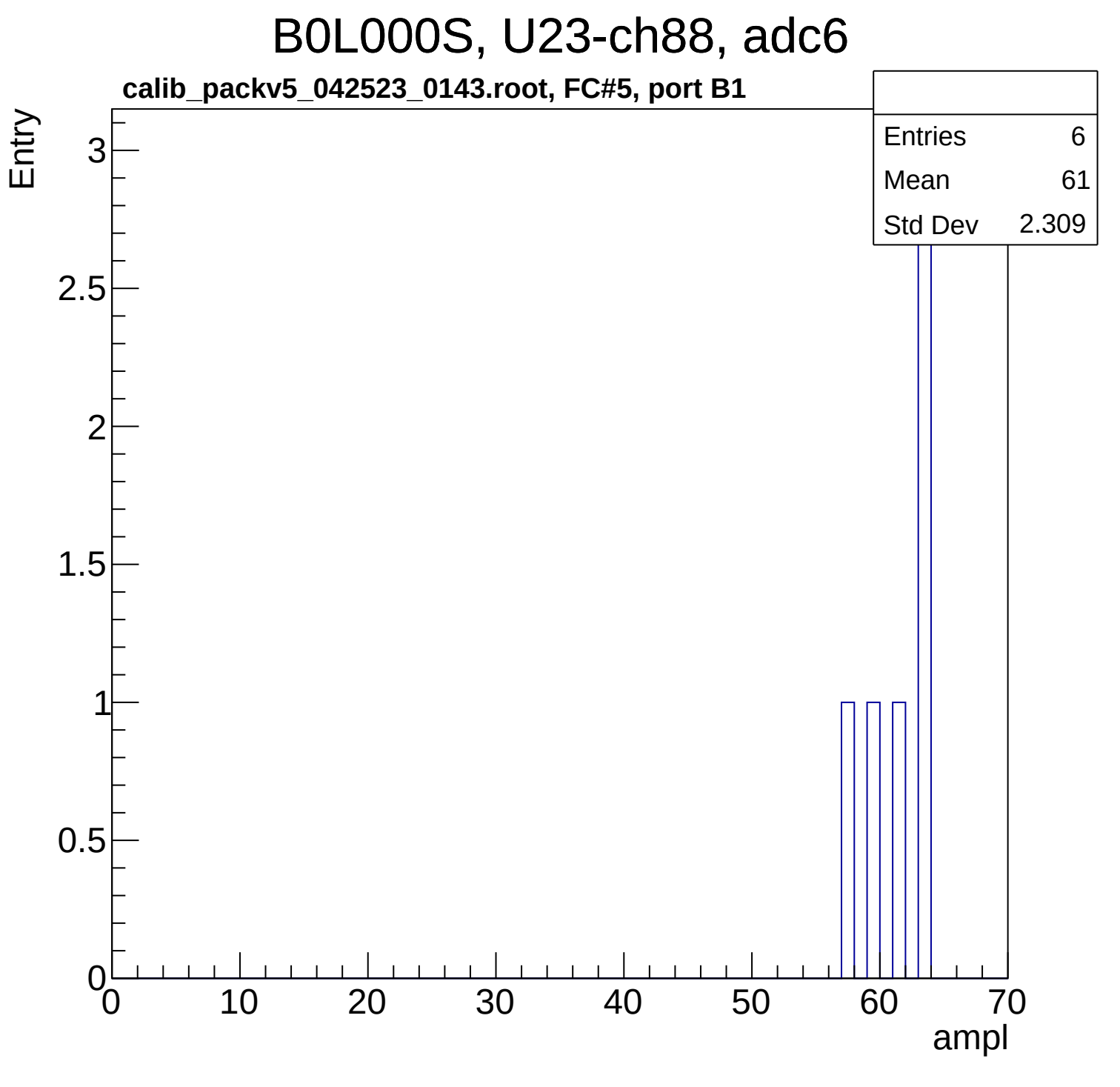

B0L000S, U23-ch88, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.33
Std Dev 3.642
Gaus mean : 35.8350
Gaus Width: 3.2999
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

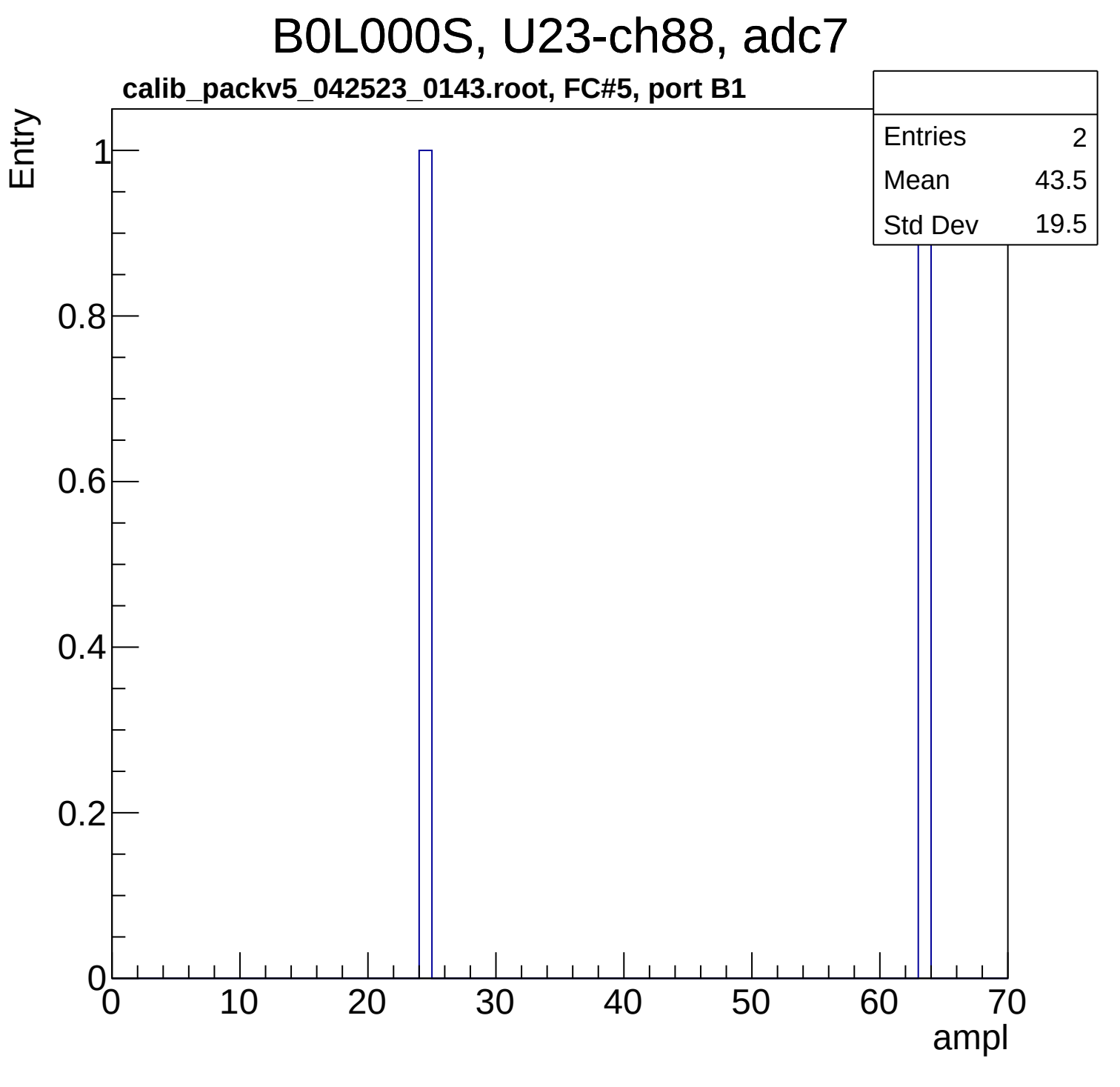

B0L000S, U23-ch88, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.89
Std Dev 3.242
Gaus mean : 42.0330
Gaus Width: 4.6768
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

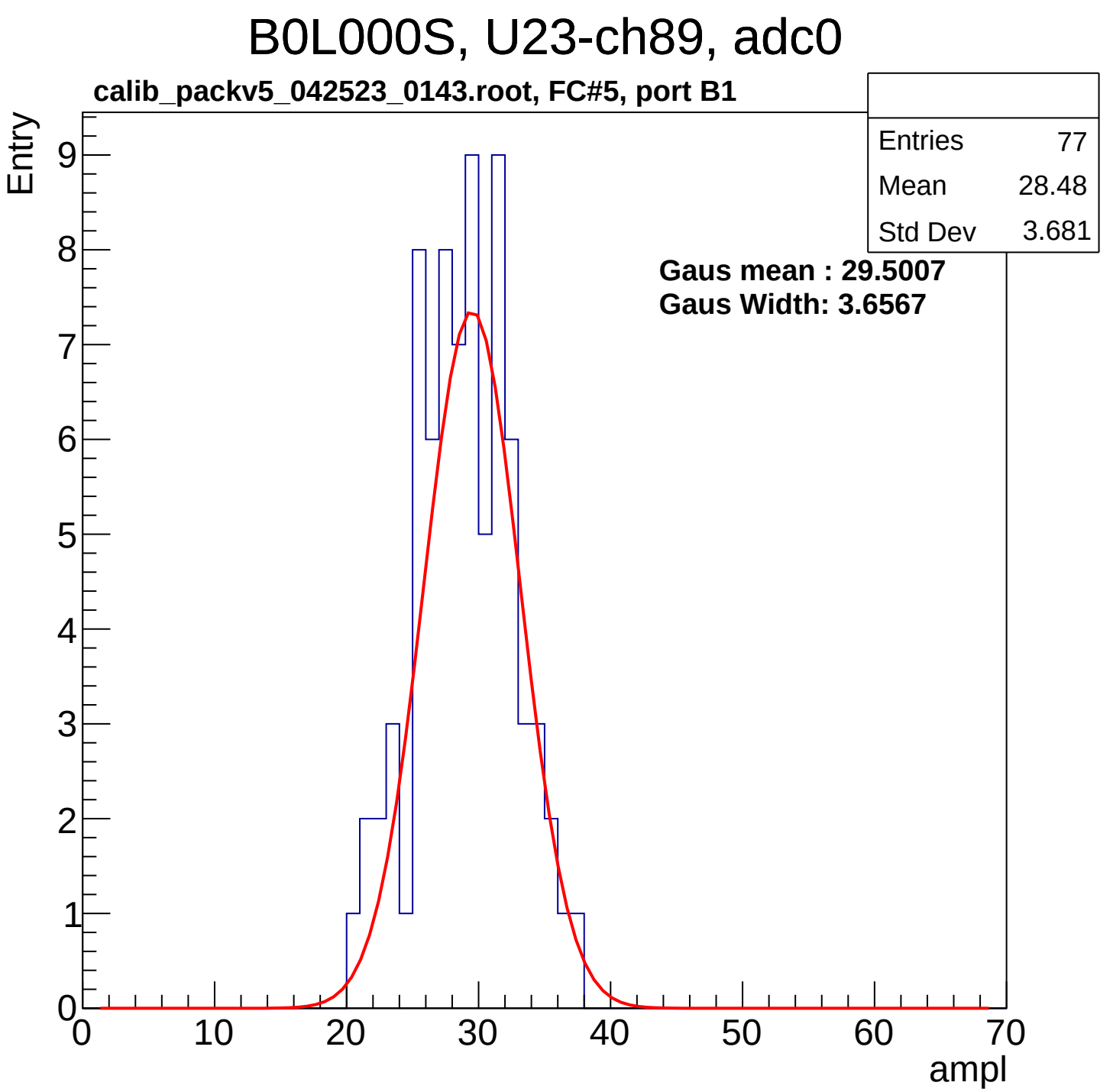

B0L000S, U23-ch88, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.73
Std Dev 3.543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

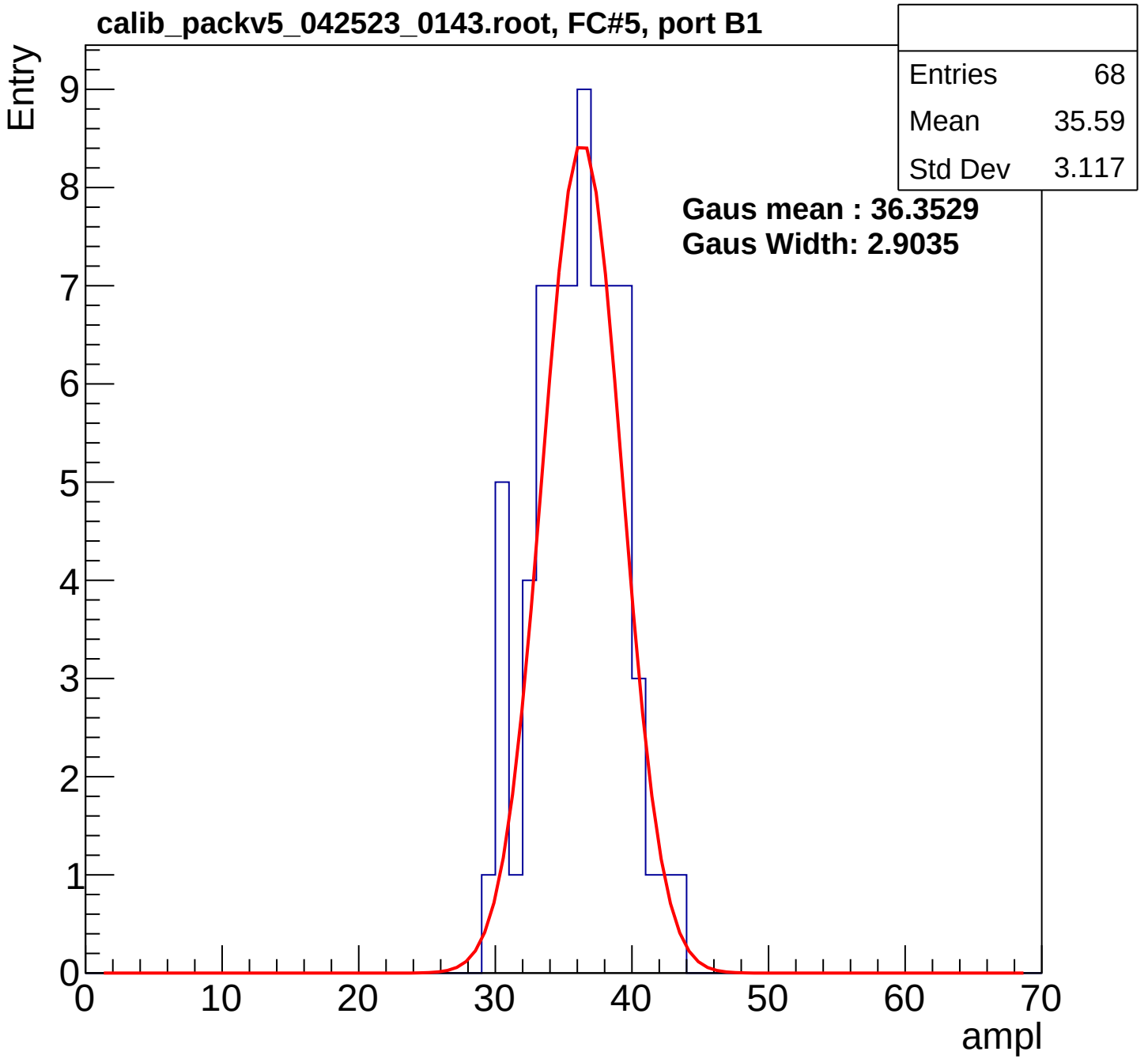

B0L000S, U23-ch88, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.67
Std Dev 3.362
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

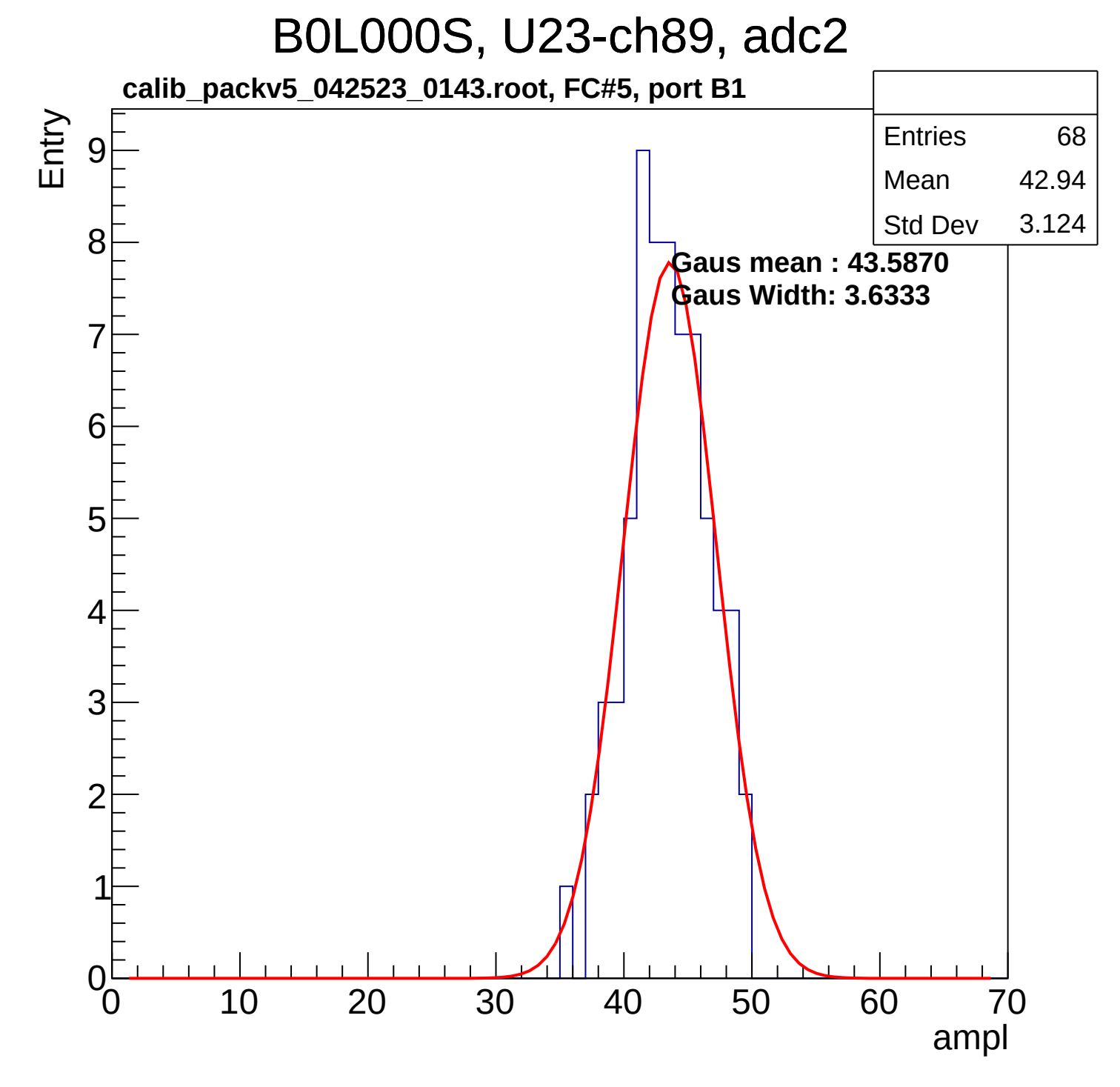

B0L000S, U23-ch88, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.48
Std Dev 8.101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

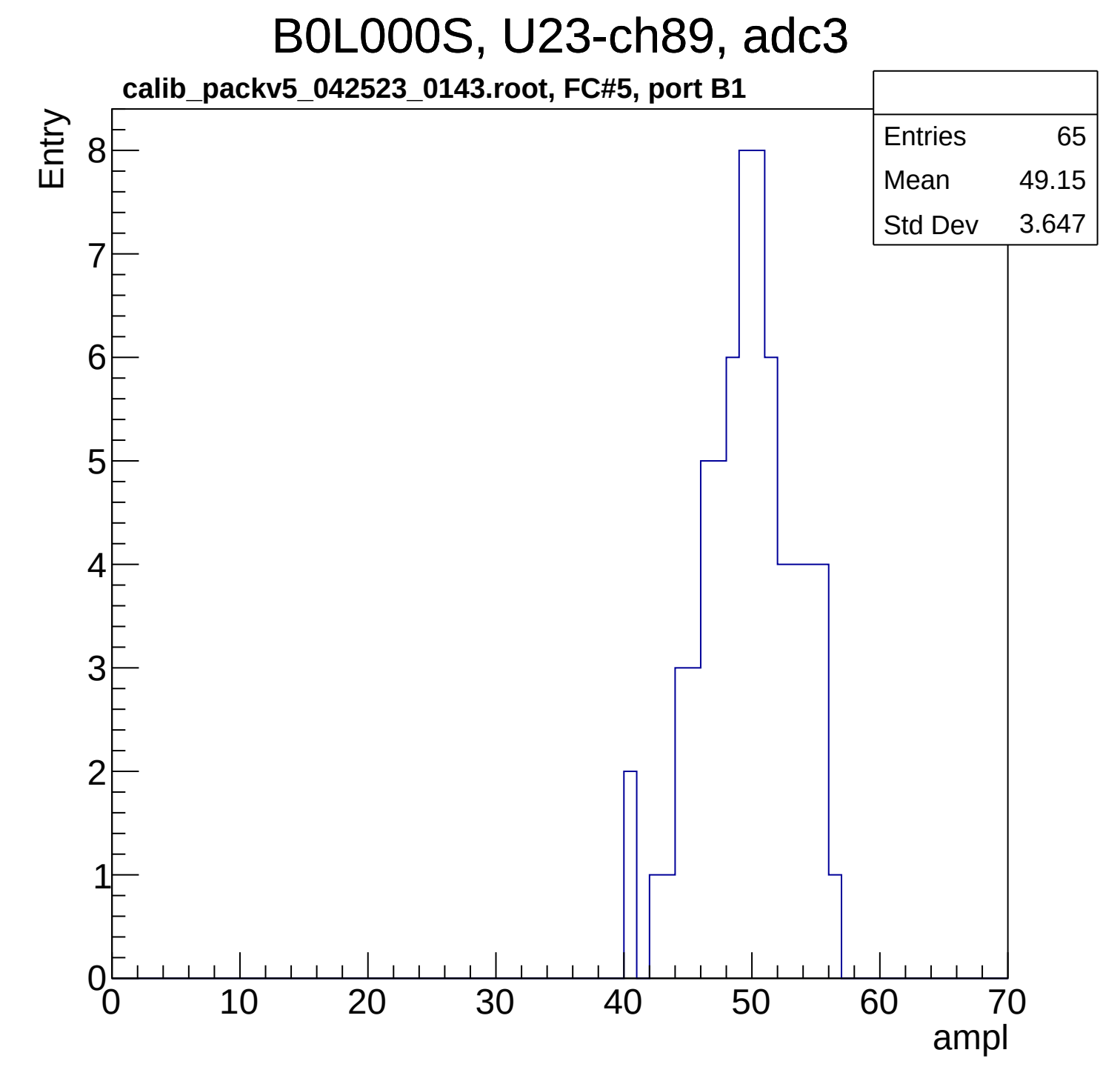

B0L000S, U23-ch88, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61
Std Dev 2.309
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

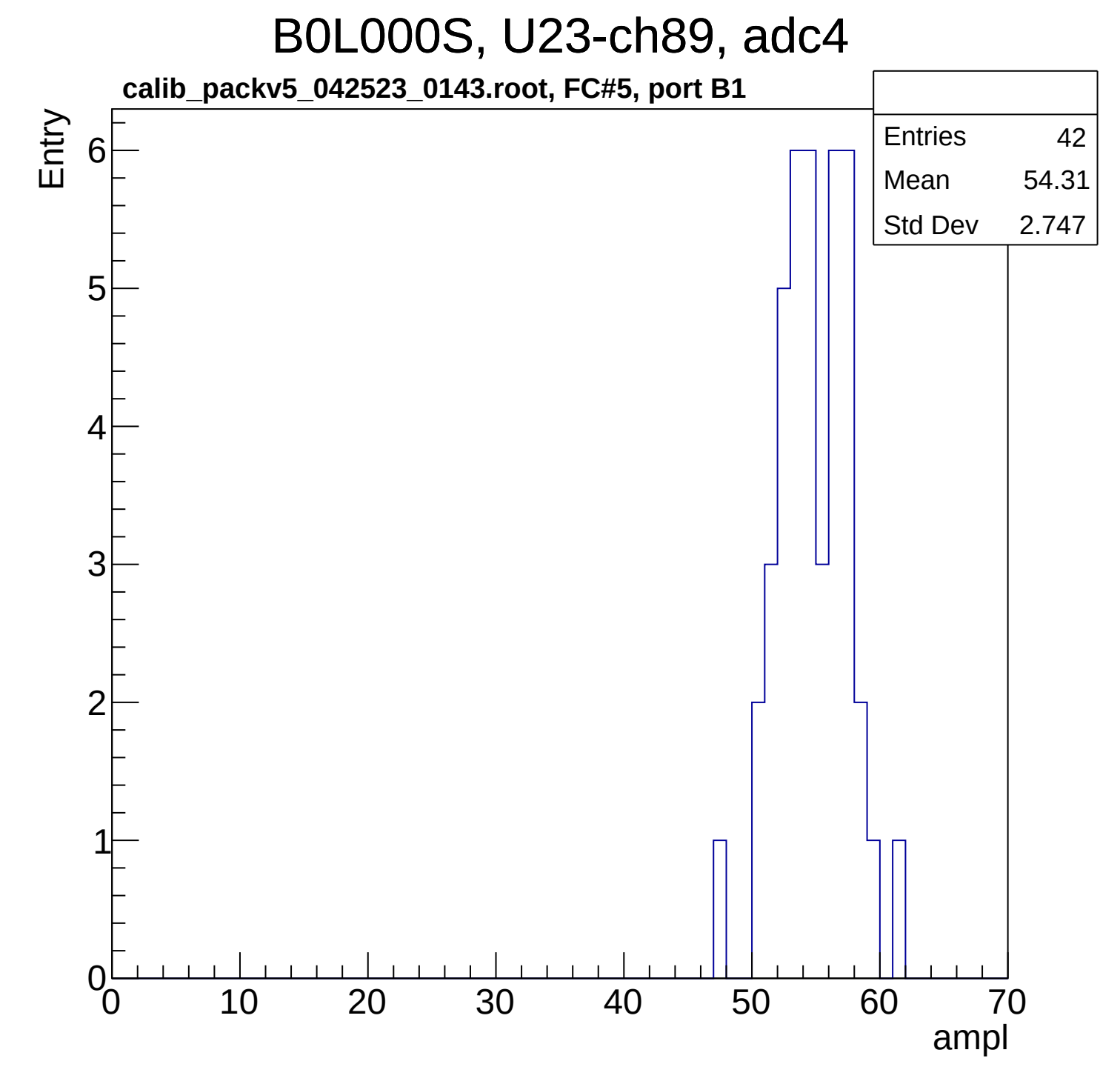

B0L000S, U23-ch88, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 43.5
Std Dev 19.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

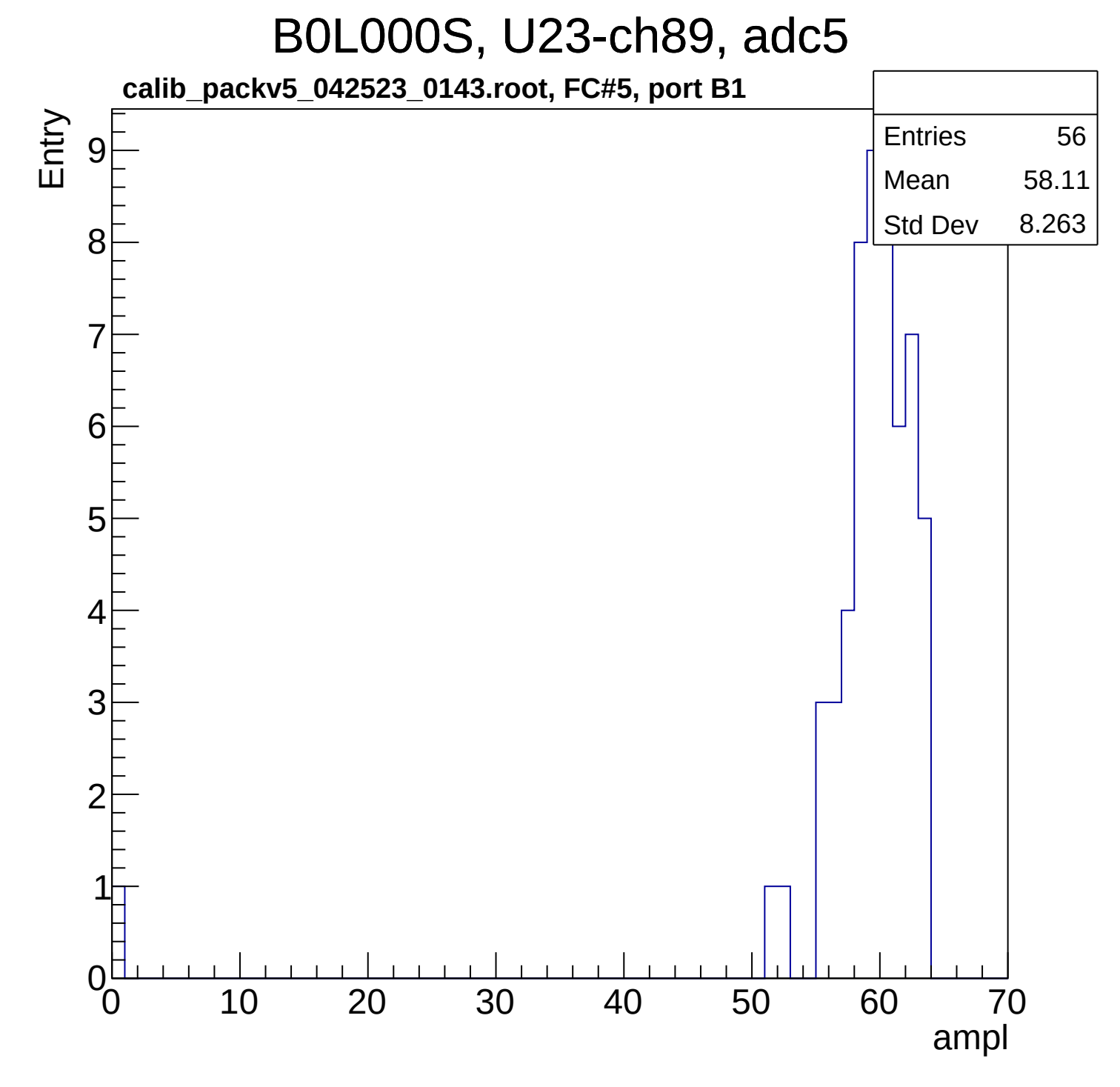

B0L000S, U23-ch89, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.48
Std Dev 3.681
Gaus mean : 29.5007
Gaus Width: 3.6567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

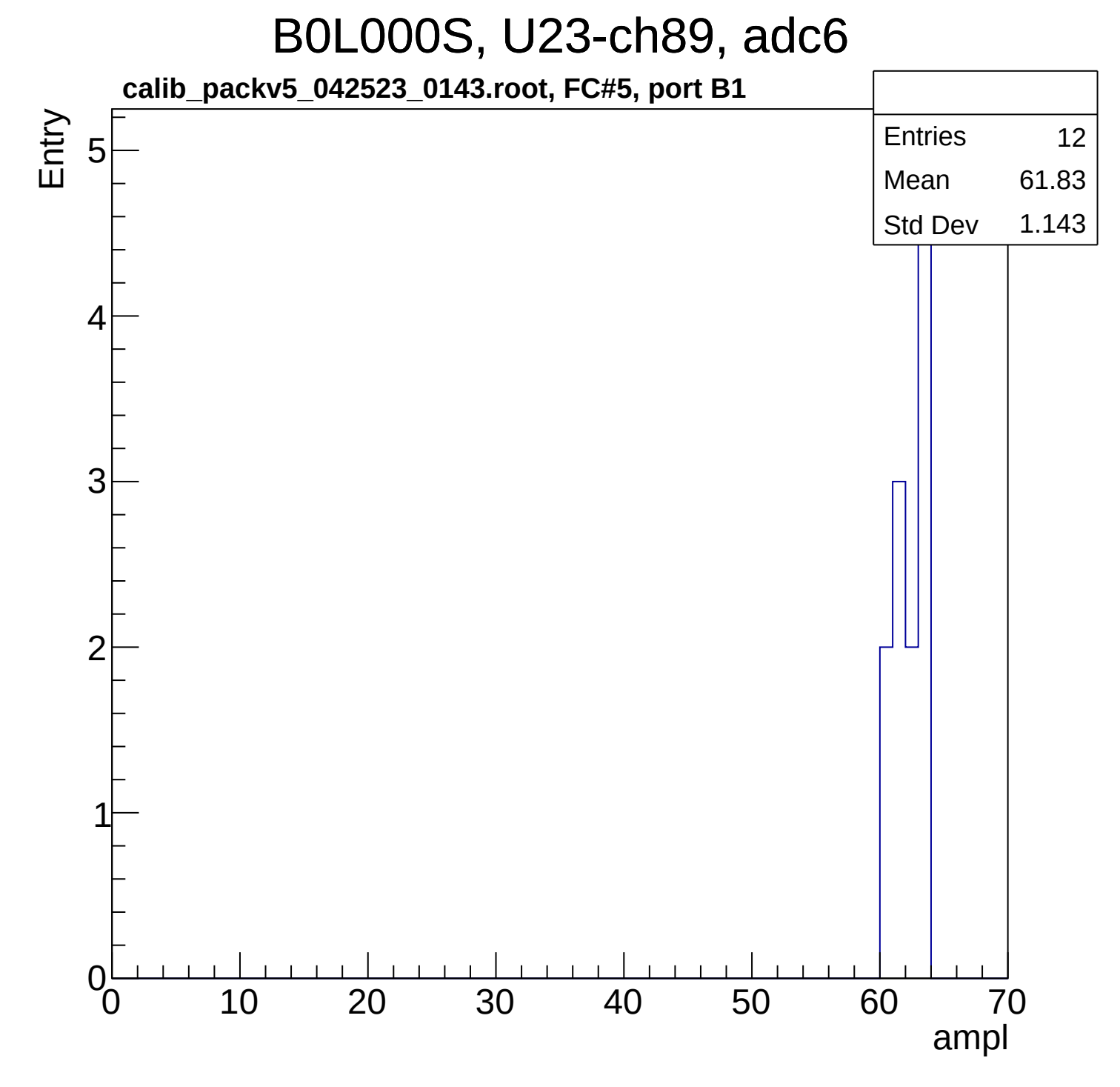

B0L000S, U23-ch89, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 35.59
Std Dev 3.117
Gaus mean : 36.3529
Gaus Width: 2.9035
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

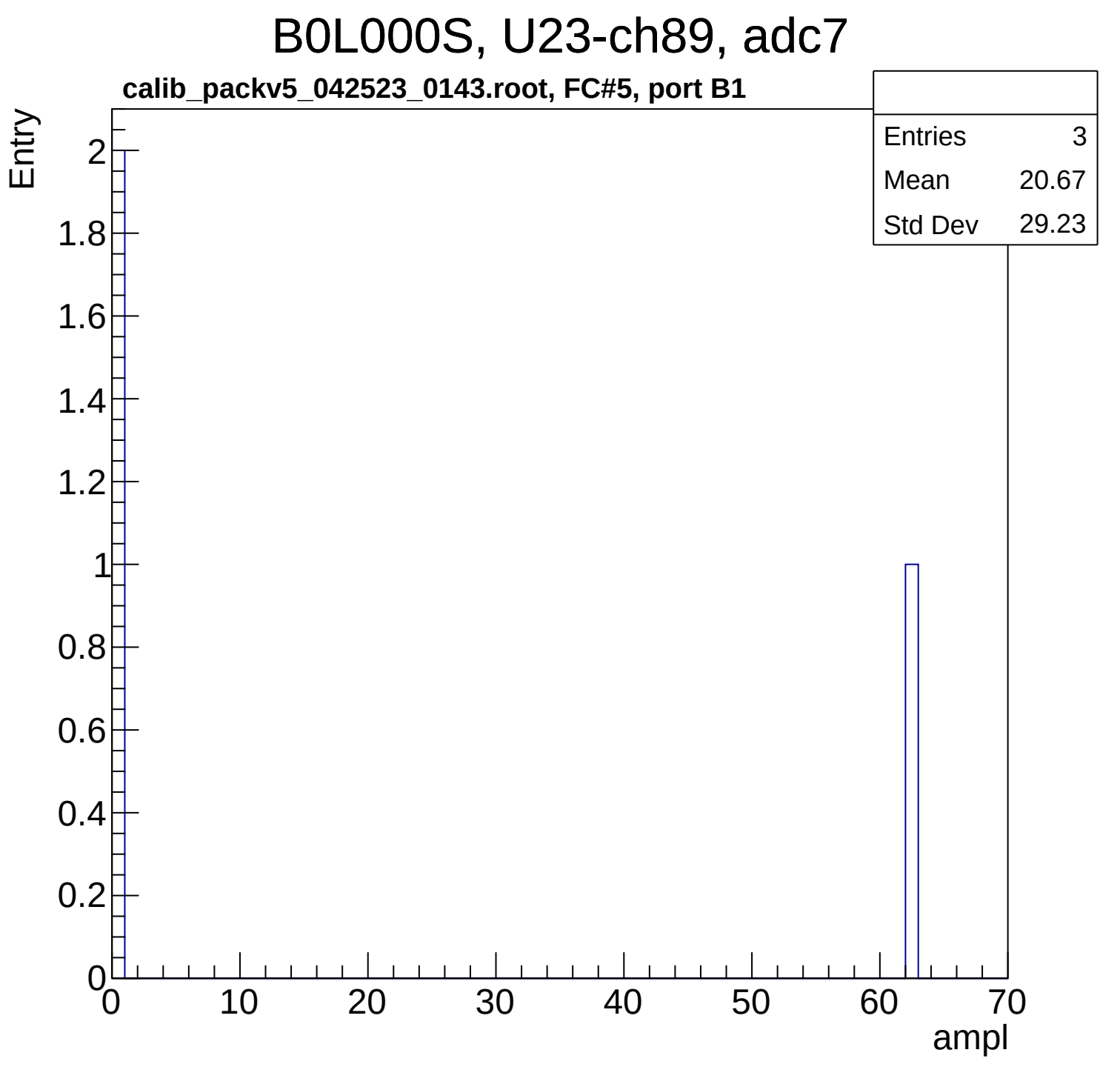

B0L000S, U23-ch89, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.94
Std Dev 3.124
Gaus mean : 43.5870
Gaus Width: 3.6333
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

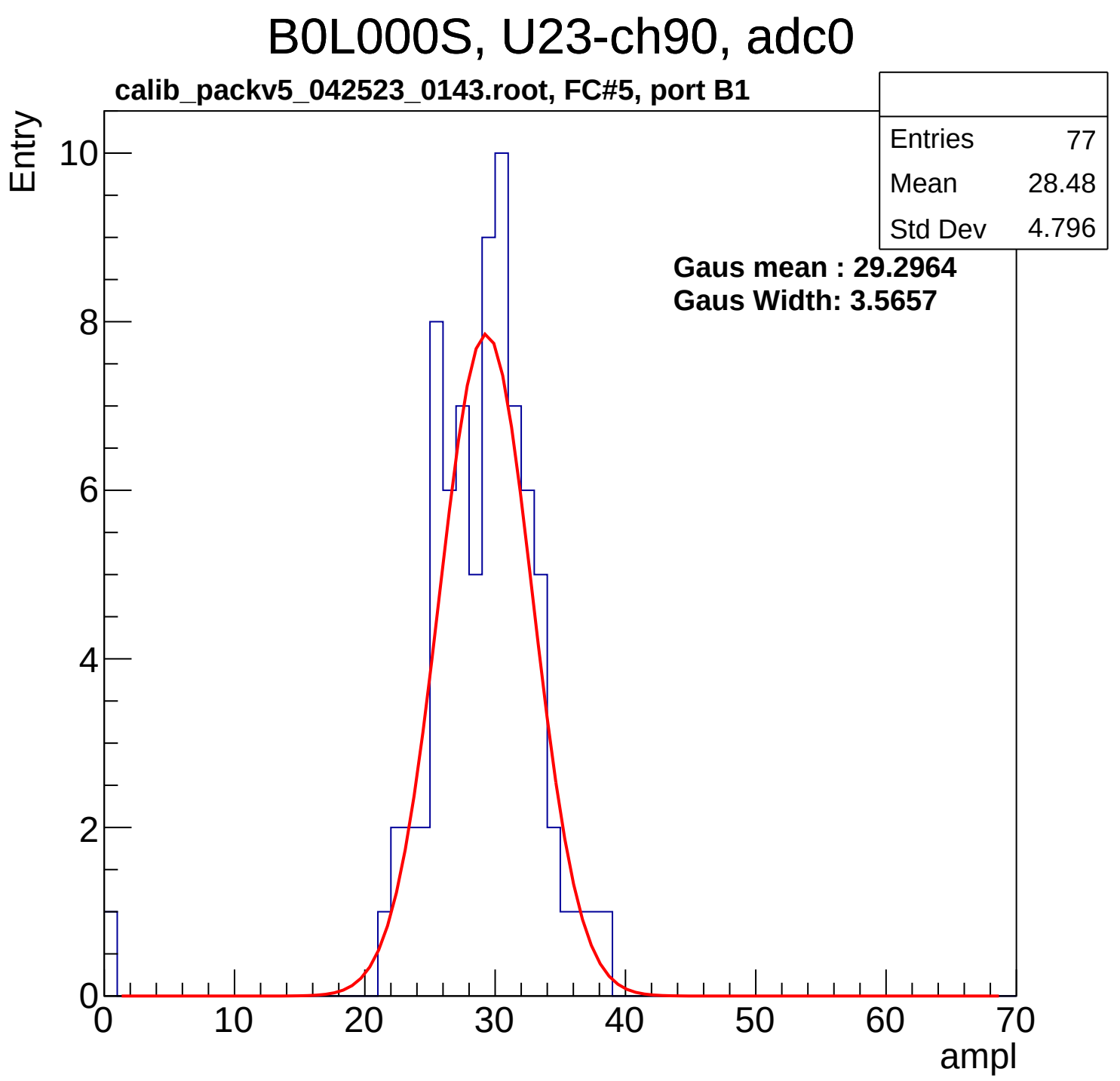

B0L000S, U23-ch89, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.15
Std Dev 3.647
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

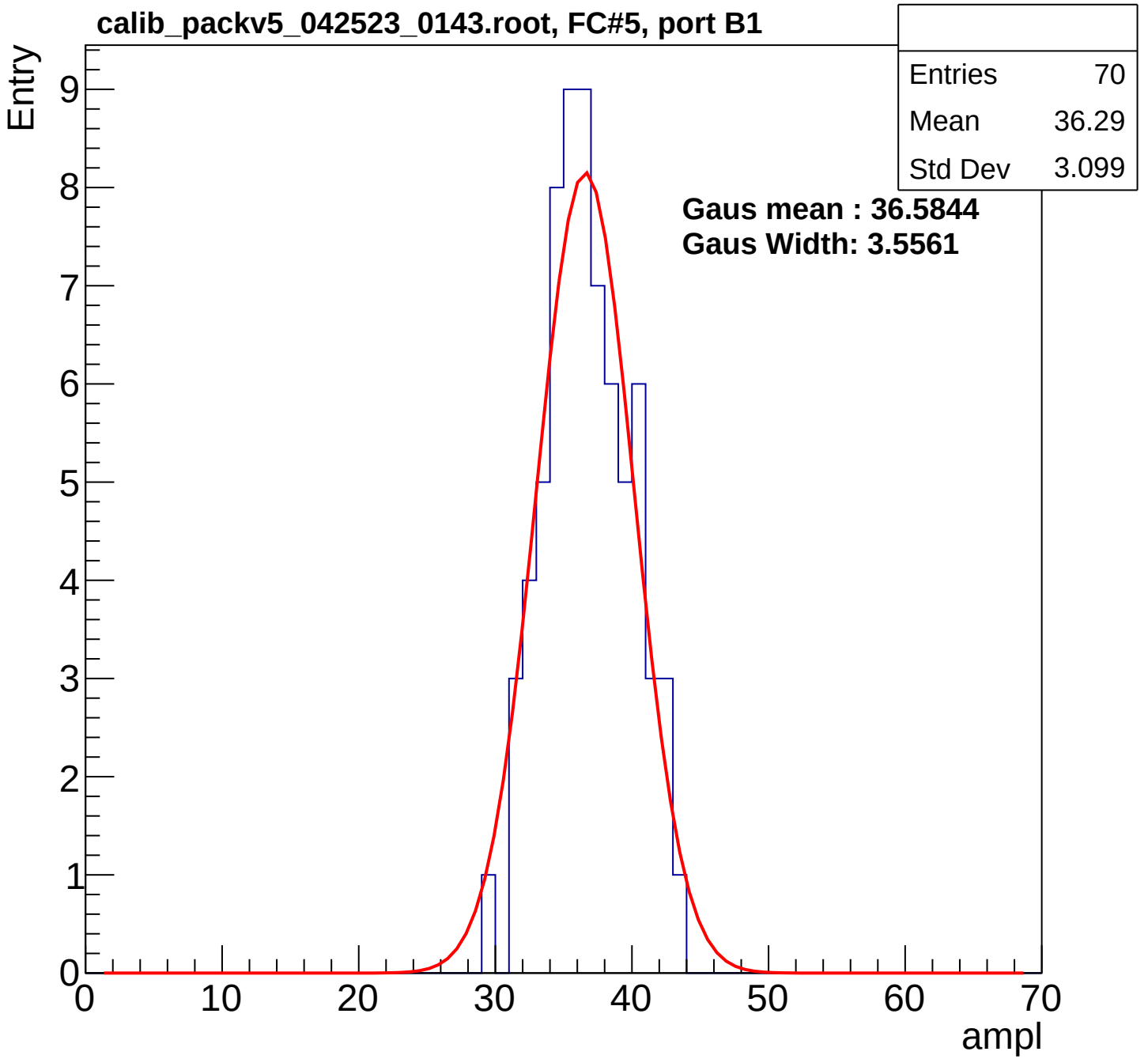

B0L000S, U23-ch89, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 54.31
Std Dev 2.747
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

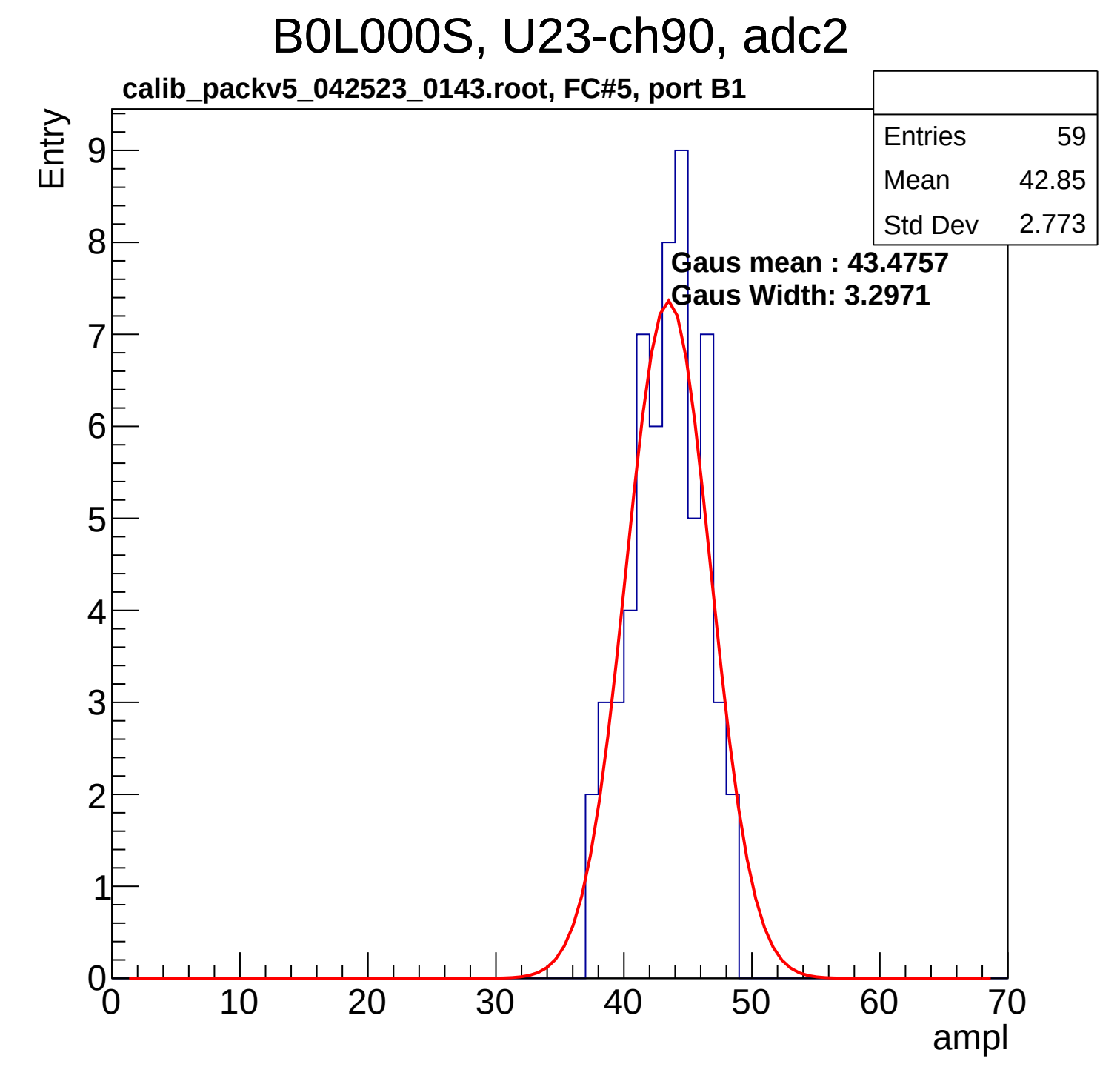

B0L000S, U23-ch89, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.11
Std Dev 8.263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

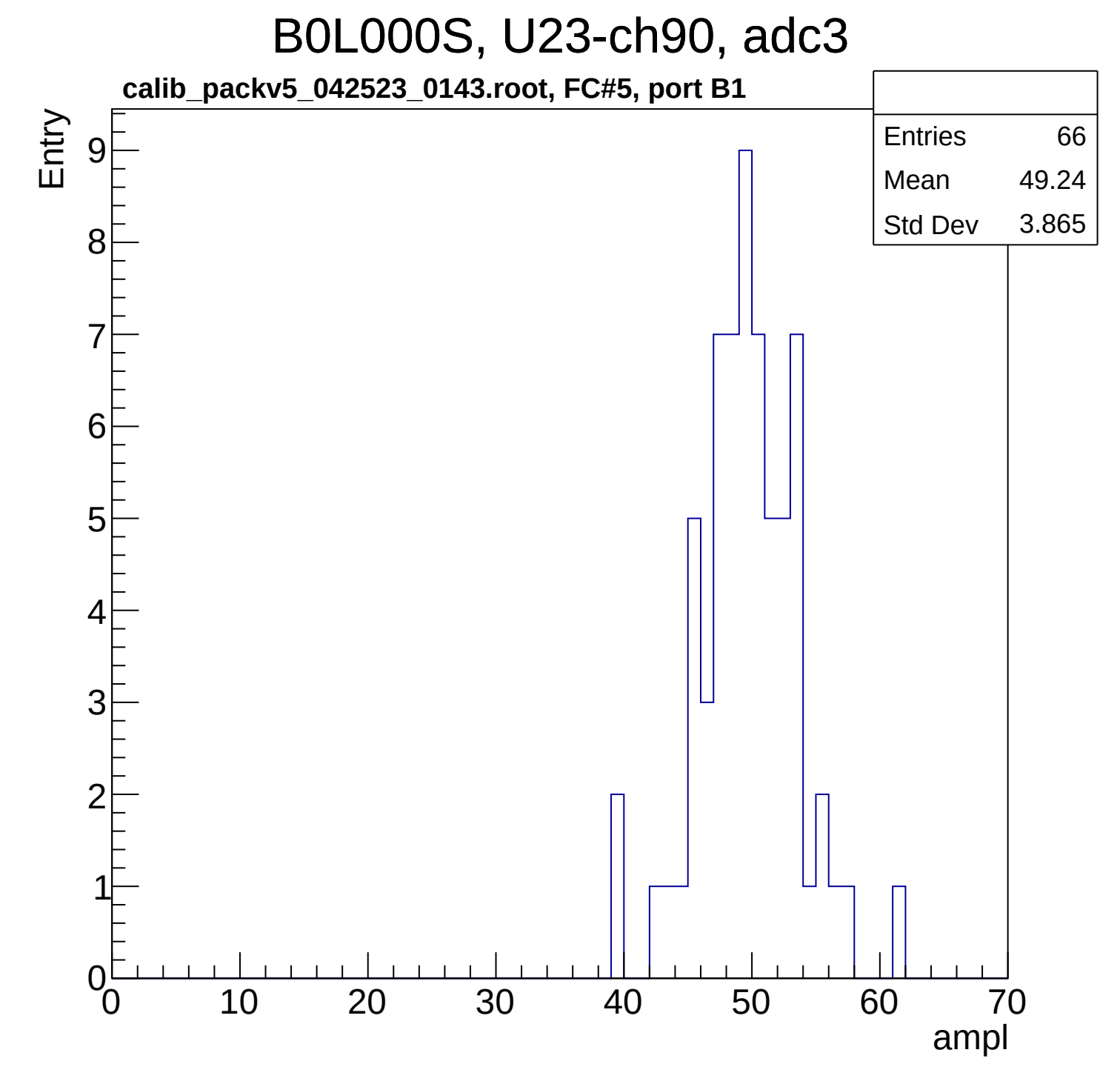

B0L000S, U23-ch89, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.83
Std Dev 1.143
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

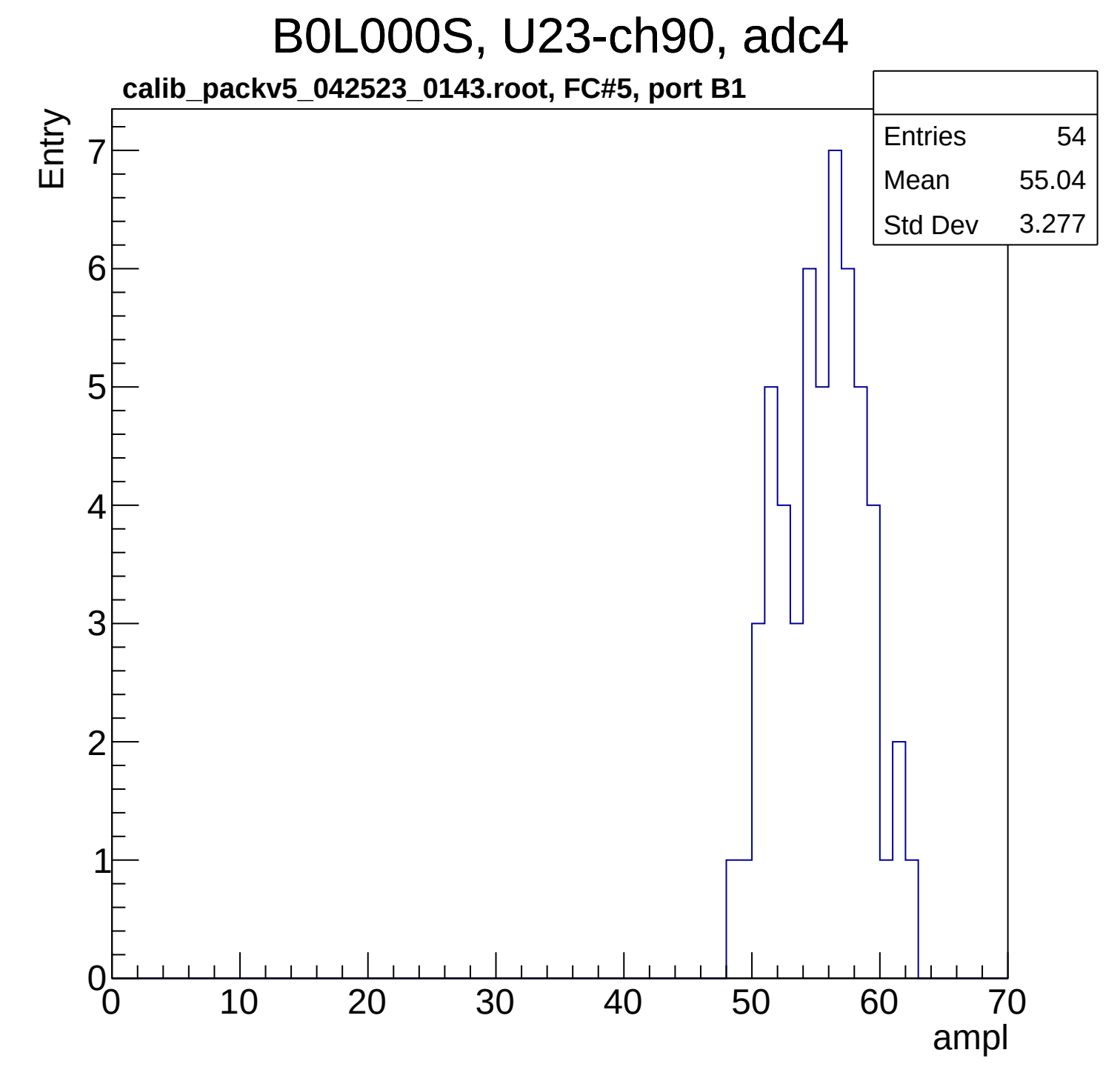

B0L000S, U23-ch89, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

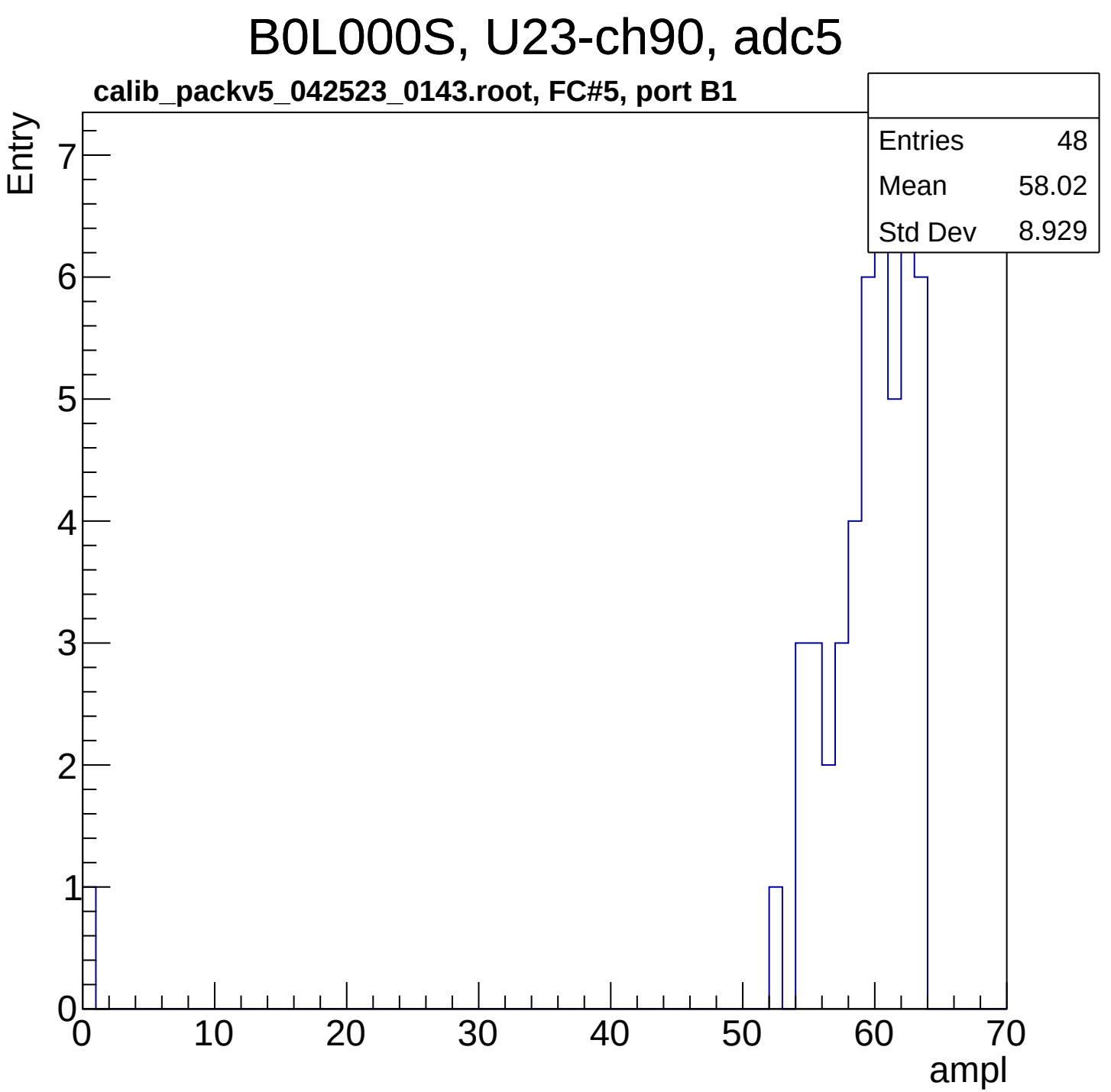

B0L000S, U23-ch90, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.48
Std Dev 4.796
Gaus mean : 29.2964
Gaus Width: 3.5657
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

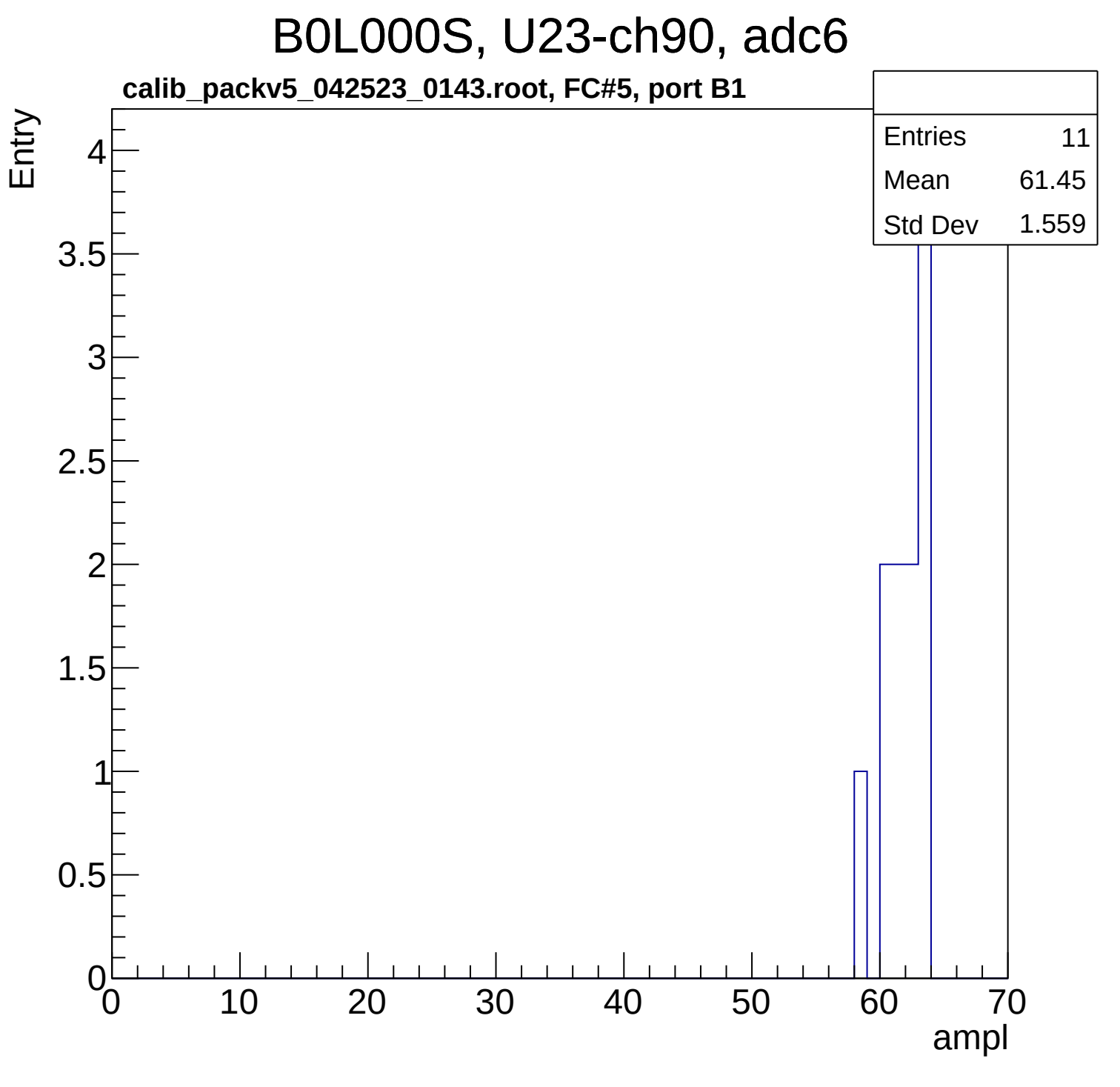

B0L000S, U23-ch90, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.29
Std Dev 3.099
Gaus mean : 36.5844
Gaus Width: 3.5561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

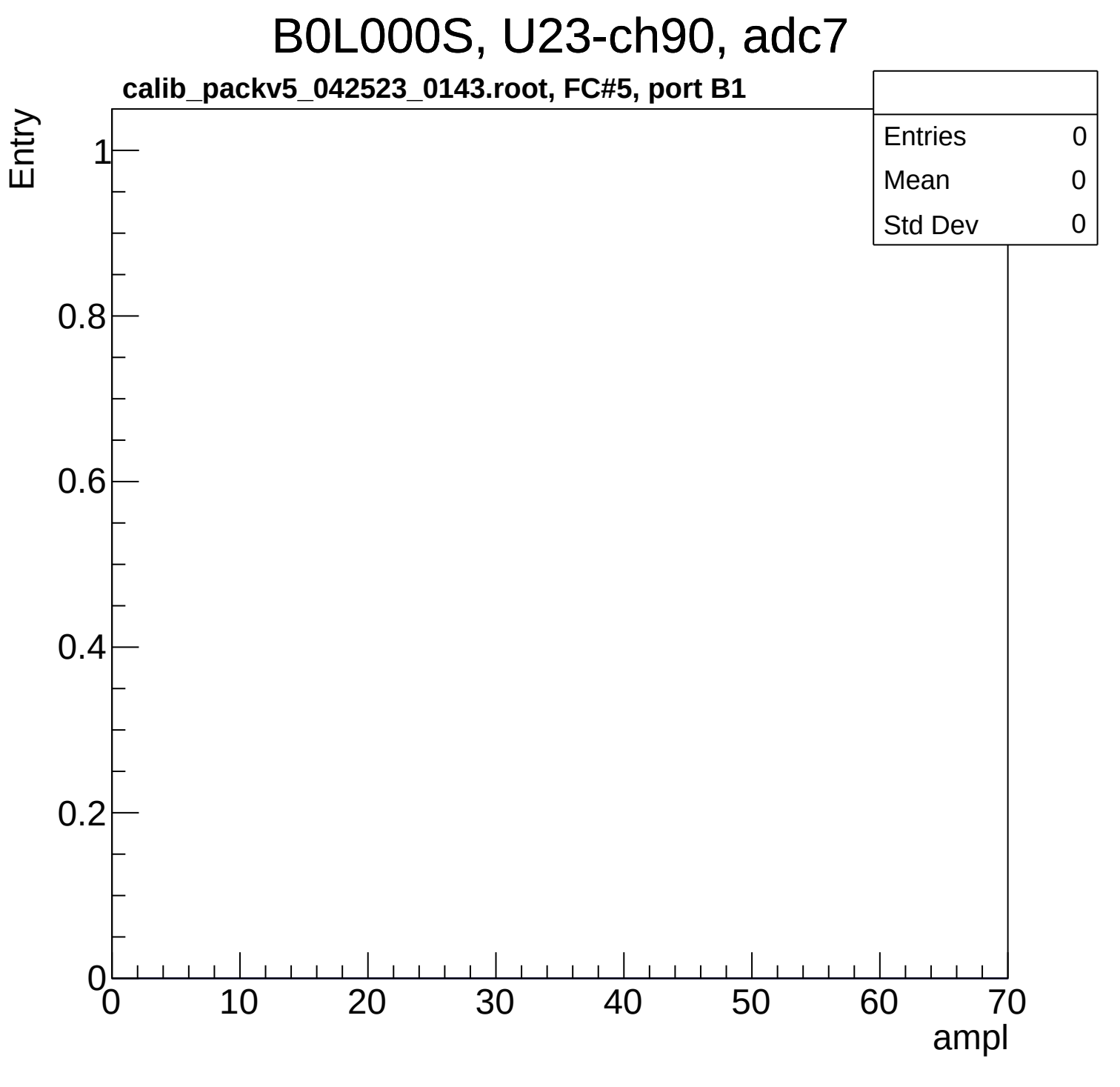

B0L000S, U23-ch90, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.85
Std Dev 2.773
Gaus mean : 43.4757
Gaus Width: 3.2971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

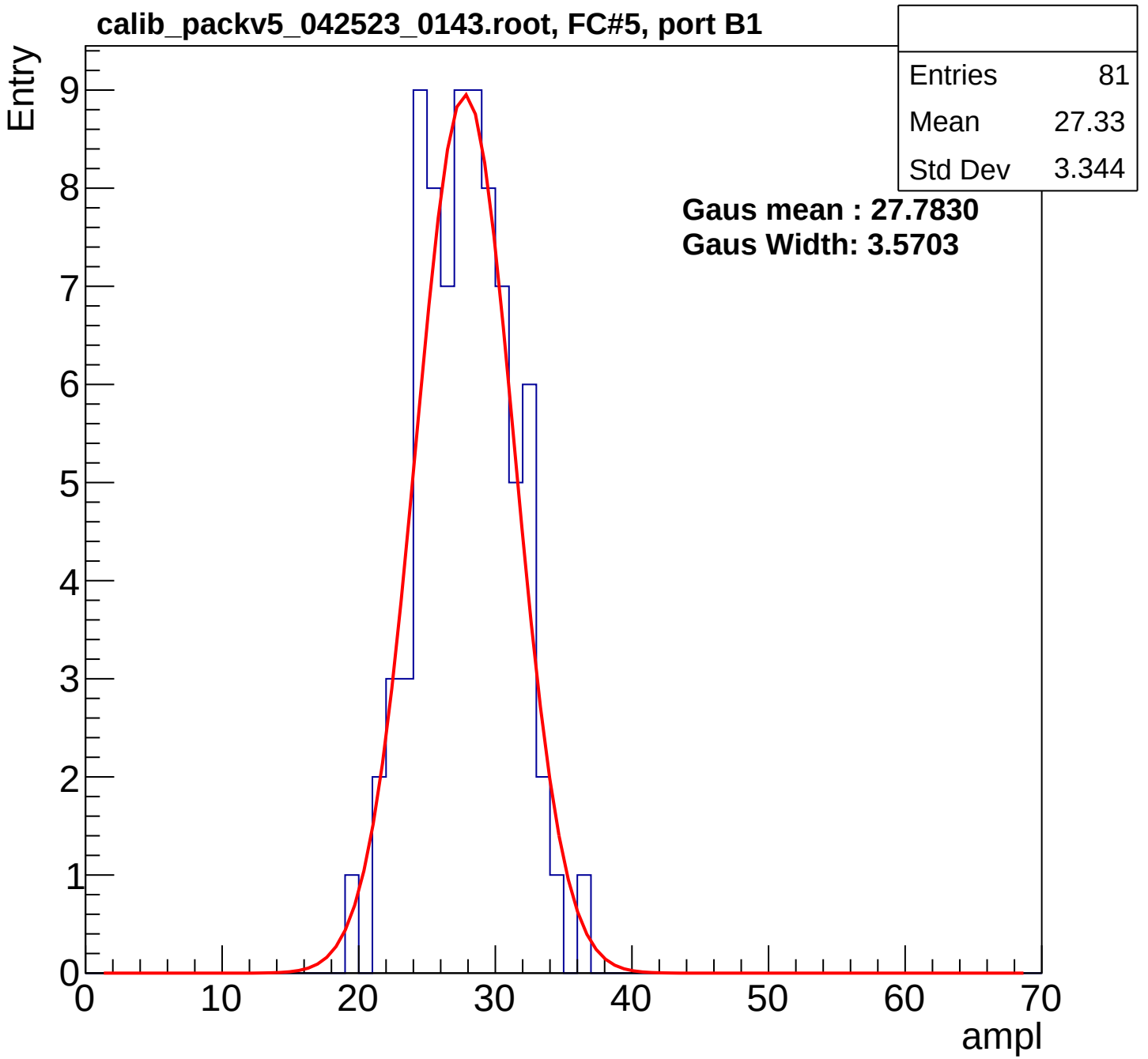

B0L000S, U23-ch90, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.24
Std Dev 3.865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

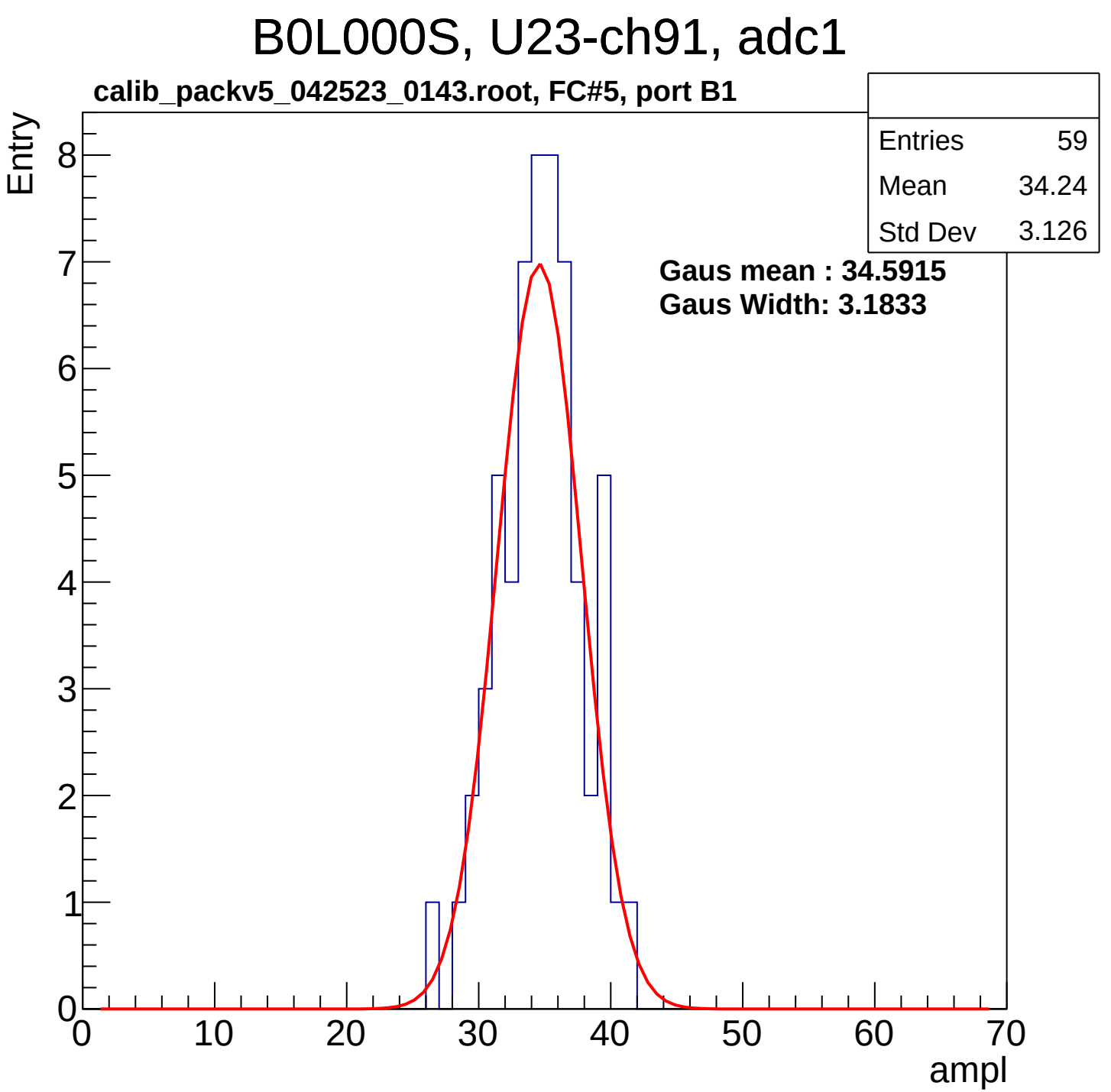

B0L000S, U23-ch90, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.04
Std Dev 3.277
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

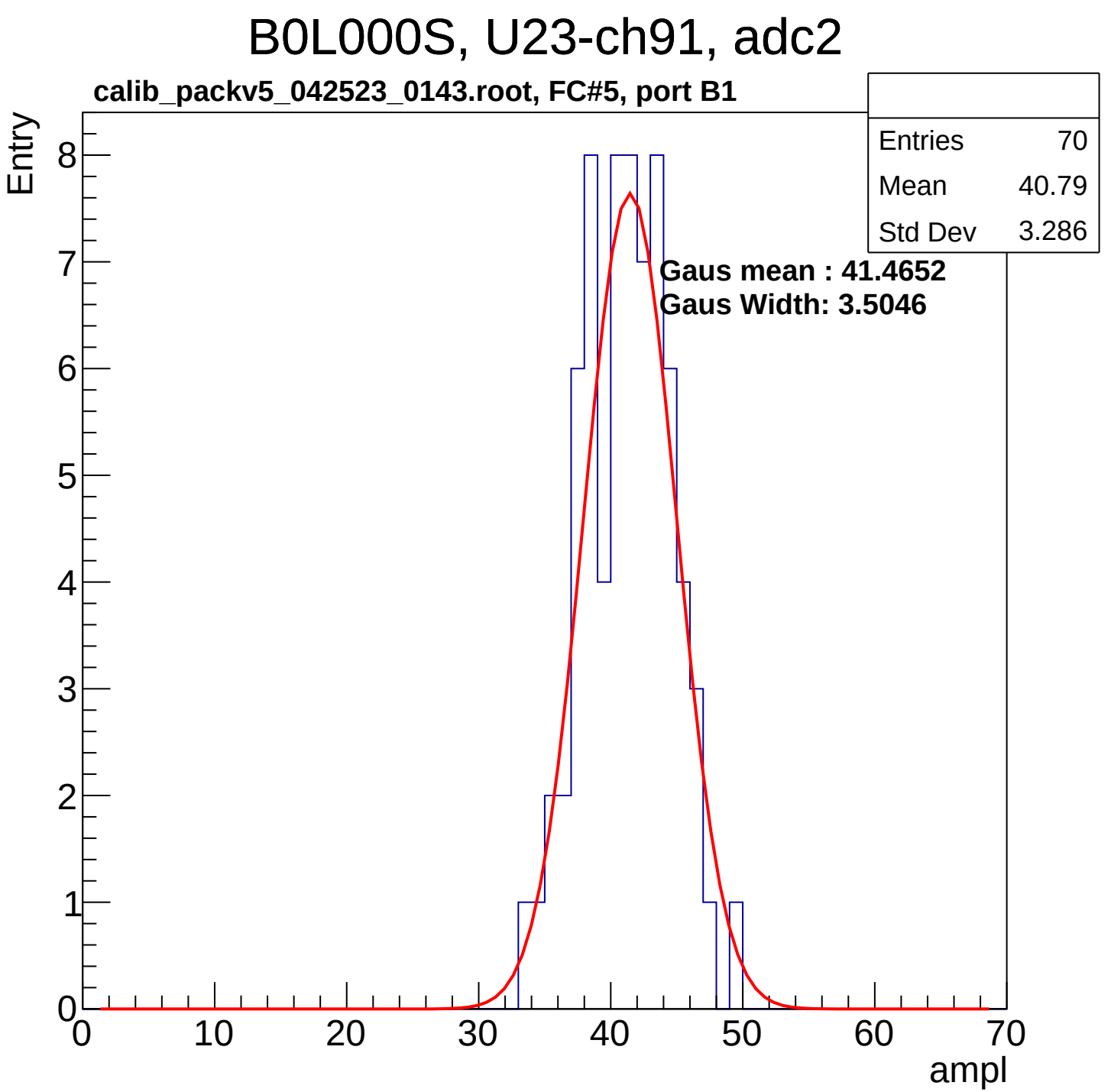

B0L000S, U23-ch90, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 58.02
Std Dev 8.929
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

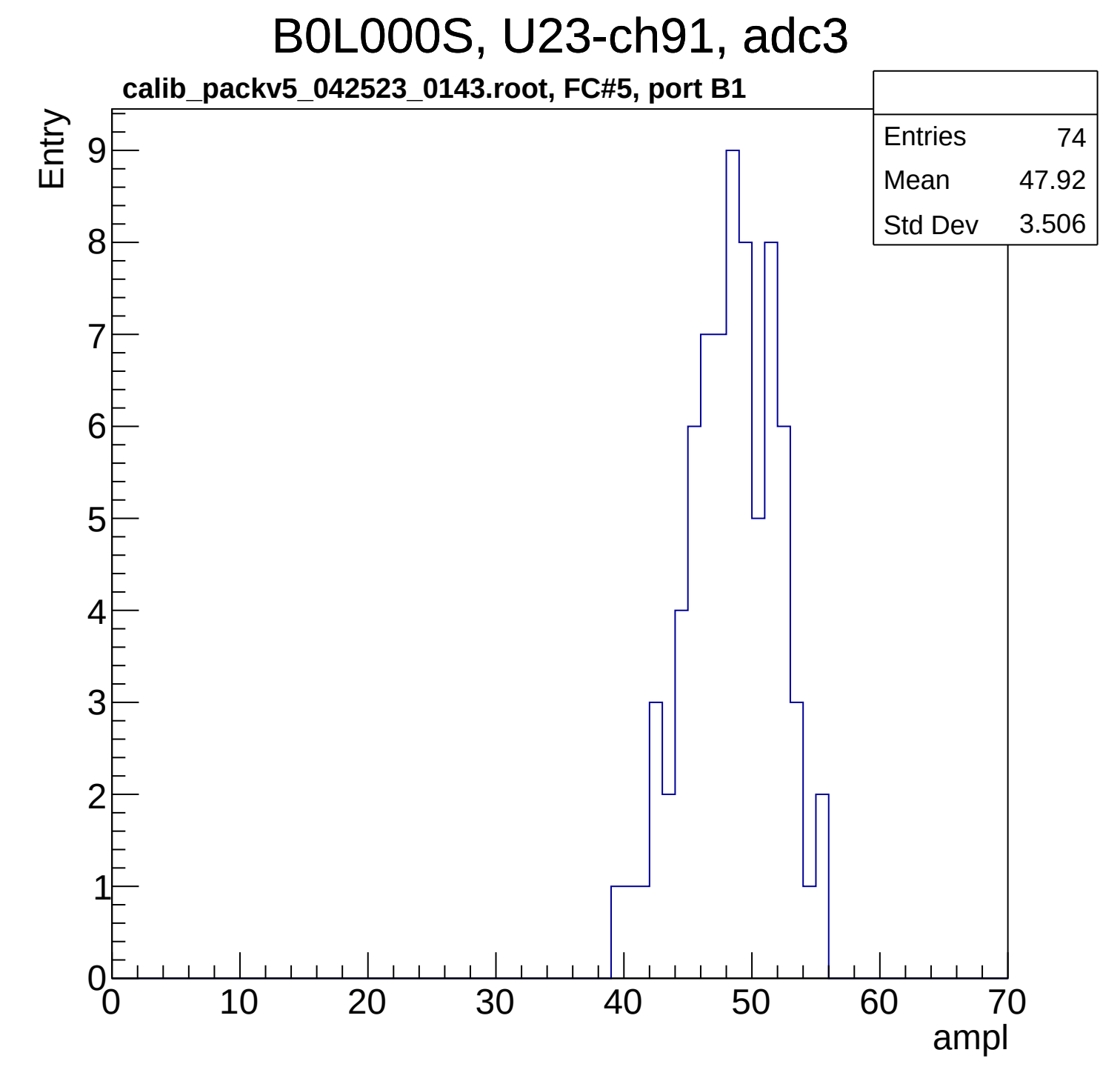

B0L000S, U23-ch90, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.45
Std Dev 1.559
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

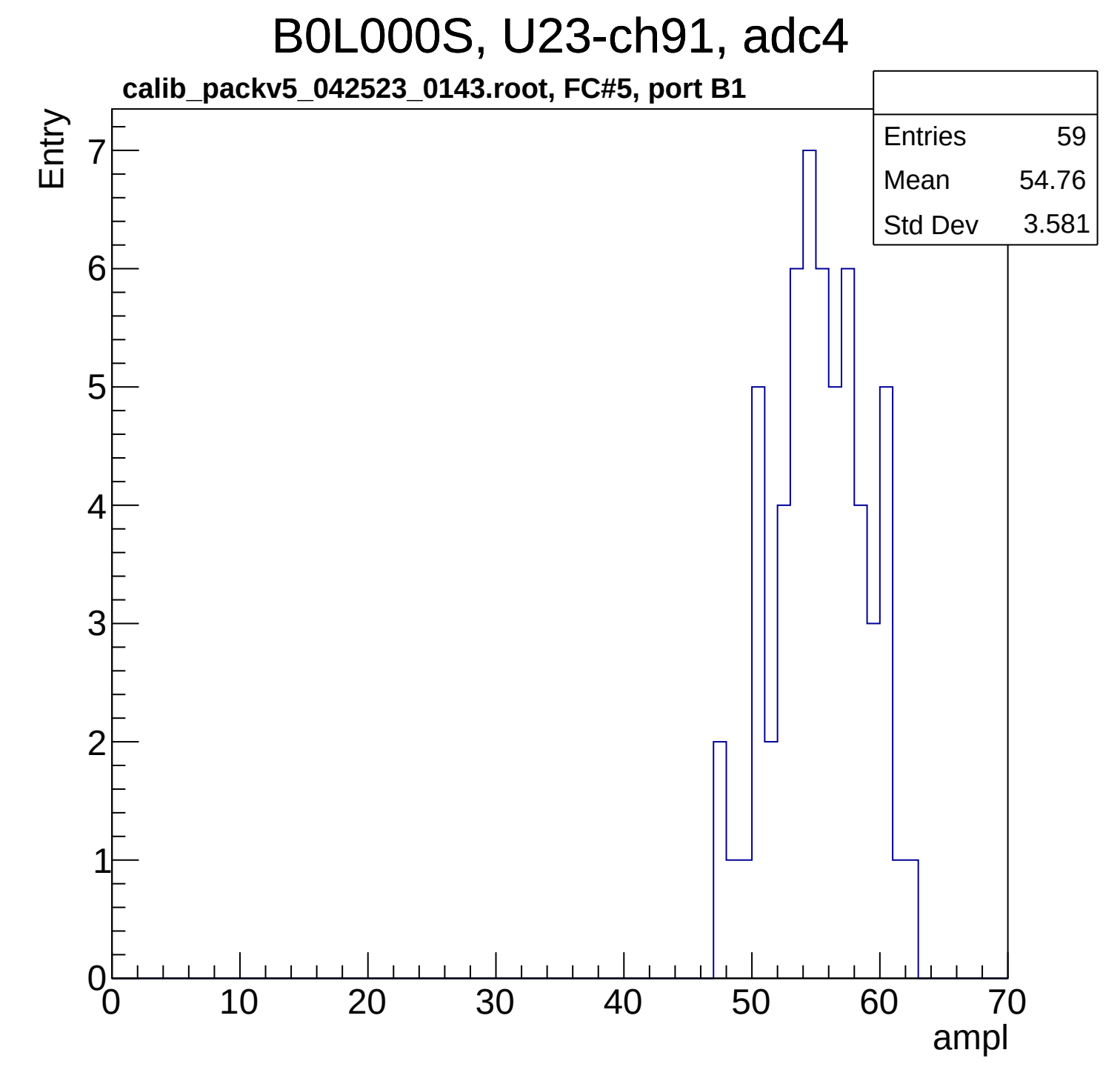

B0L000S, U23-ch90, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

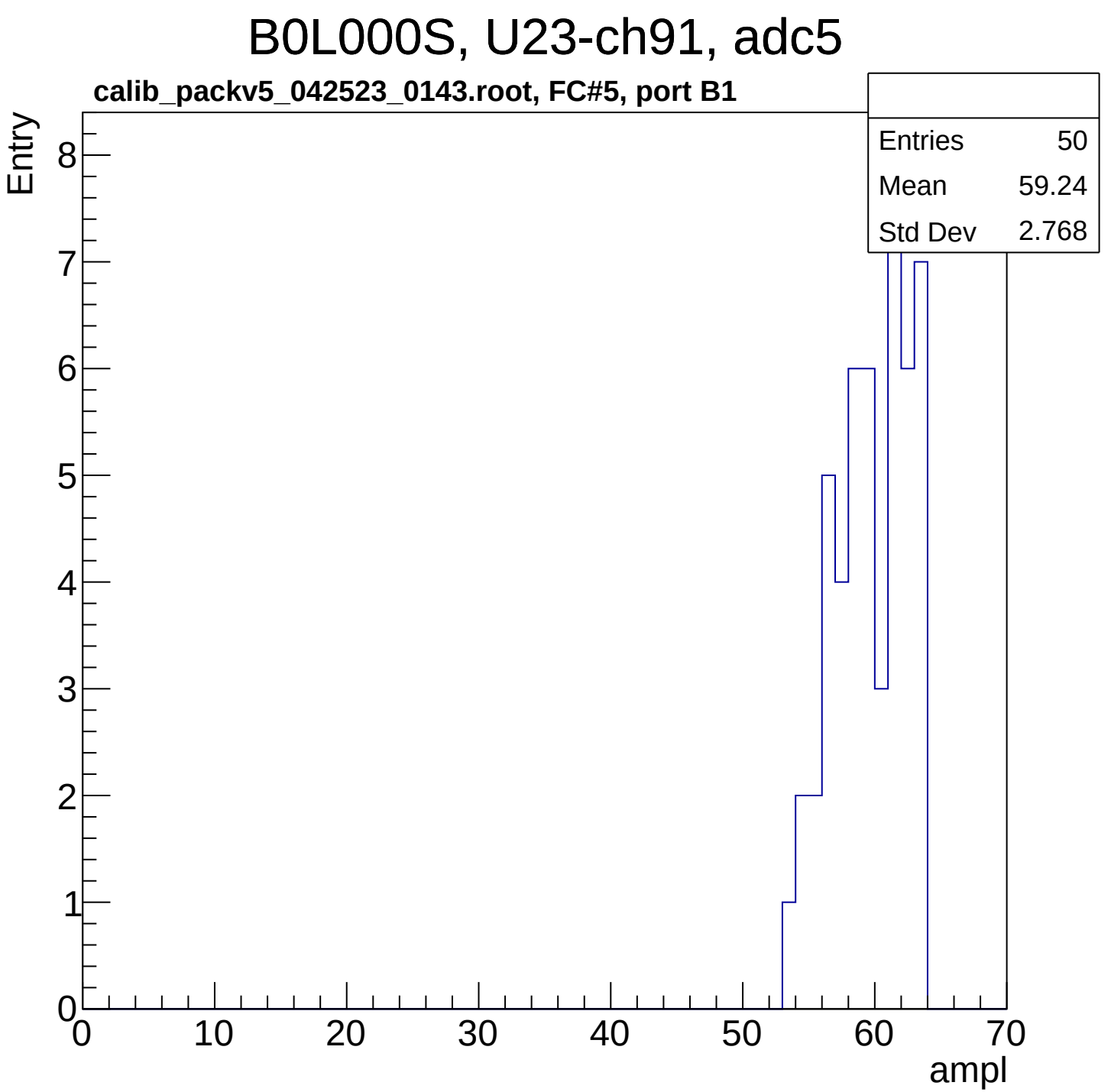

B0L000S, U23-ch91, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 27.33
Std Dev 3.344
Gaus mean : 27.7830
Gaus Width: 3.5703
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

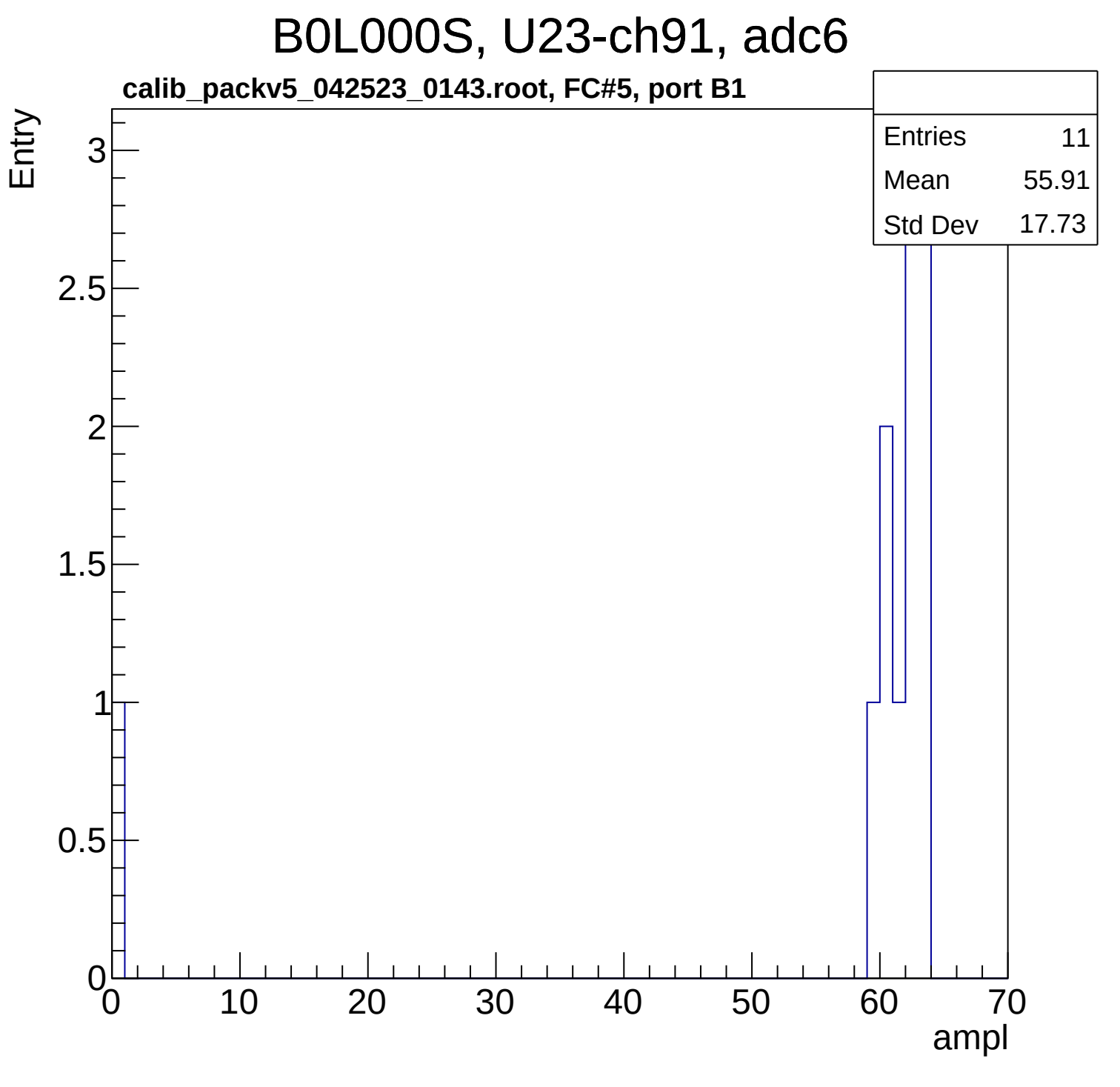

B0L000S, U23-ch91, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 34.24
Std Dev 3.126
Gaus mean : 34.5915
Gaus Width: 3.1833
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

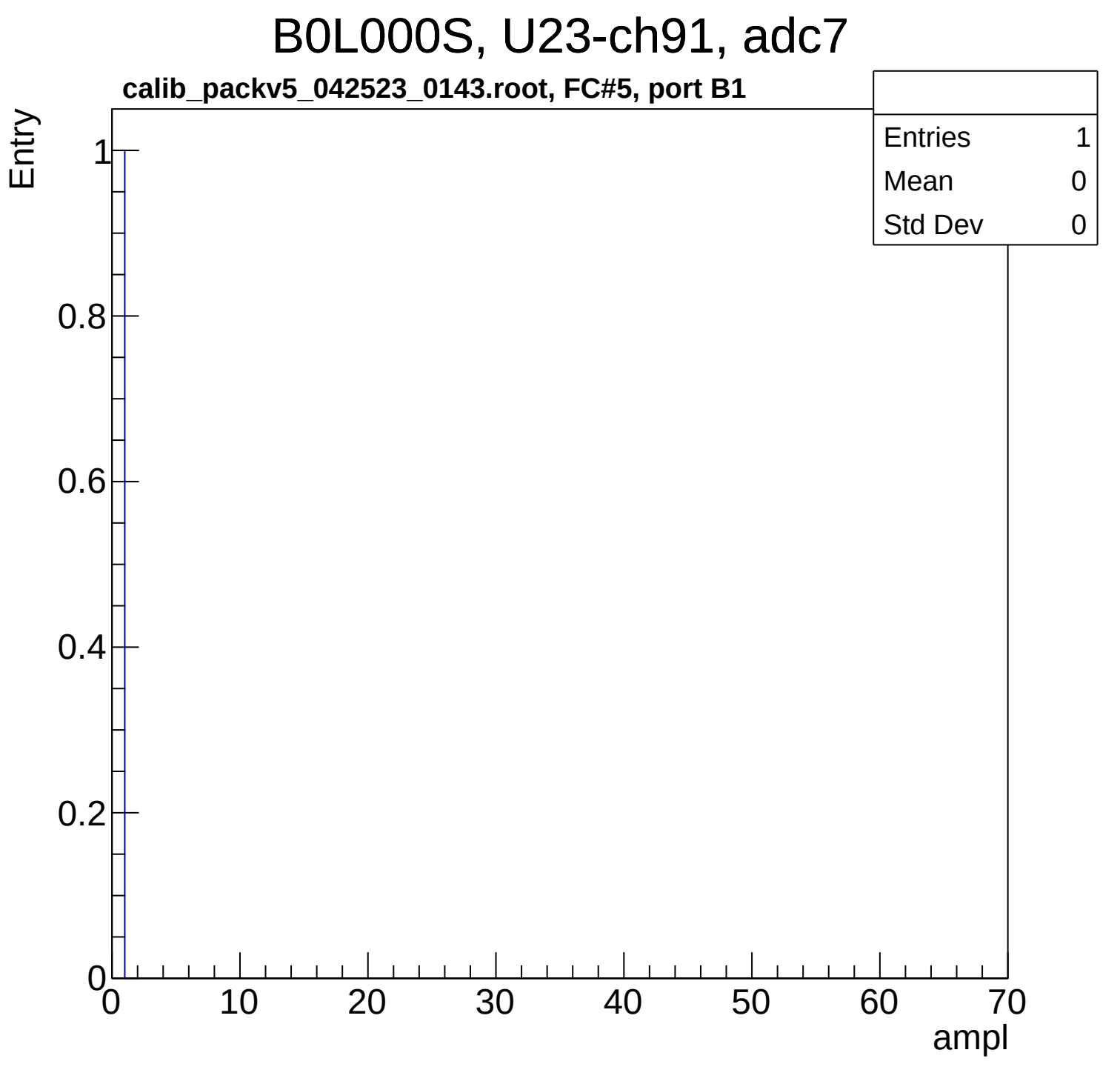

B0L000S, U23-ch91, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 40.79
Std Dev 3.286
Gaus mean : 41.4652
Gaus Width: 3.5046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

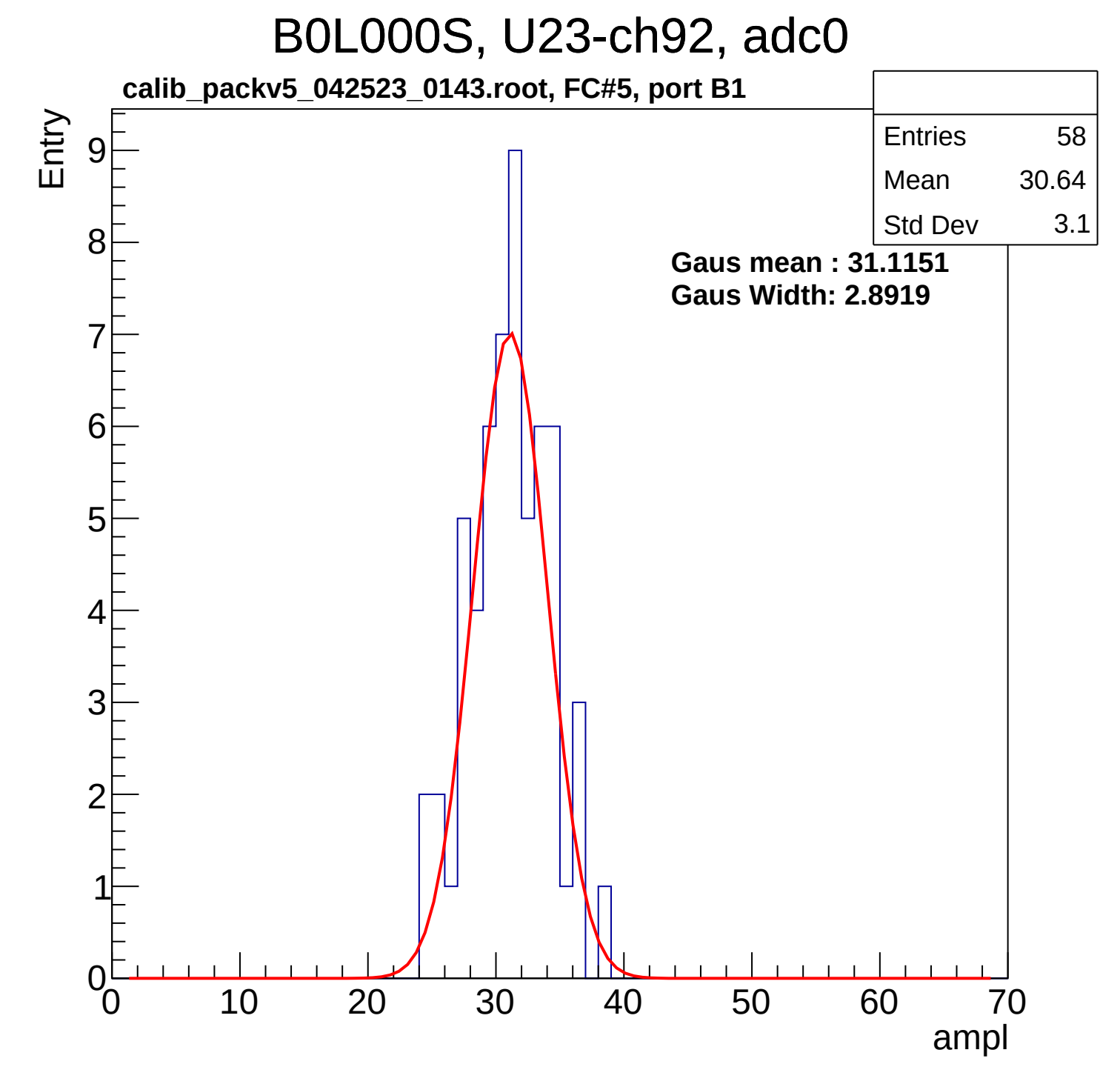

B0L000S, U23-ch91, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 47.92
Std Dev 3.506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

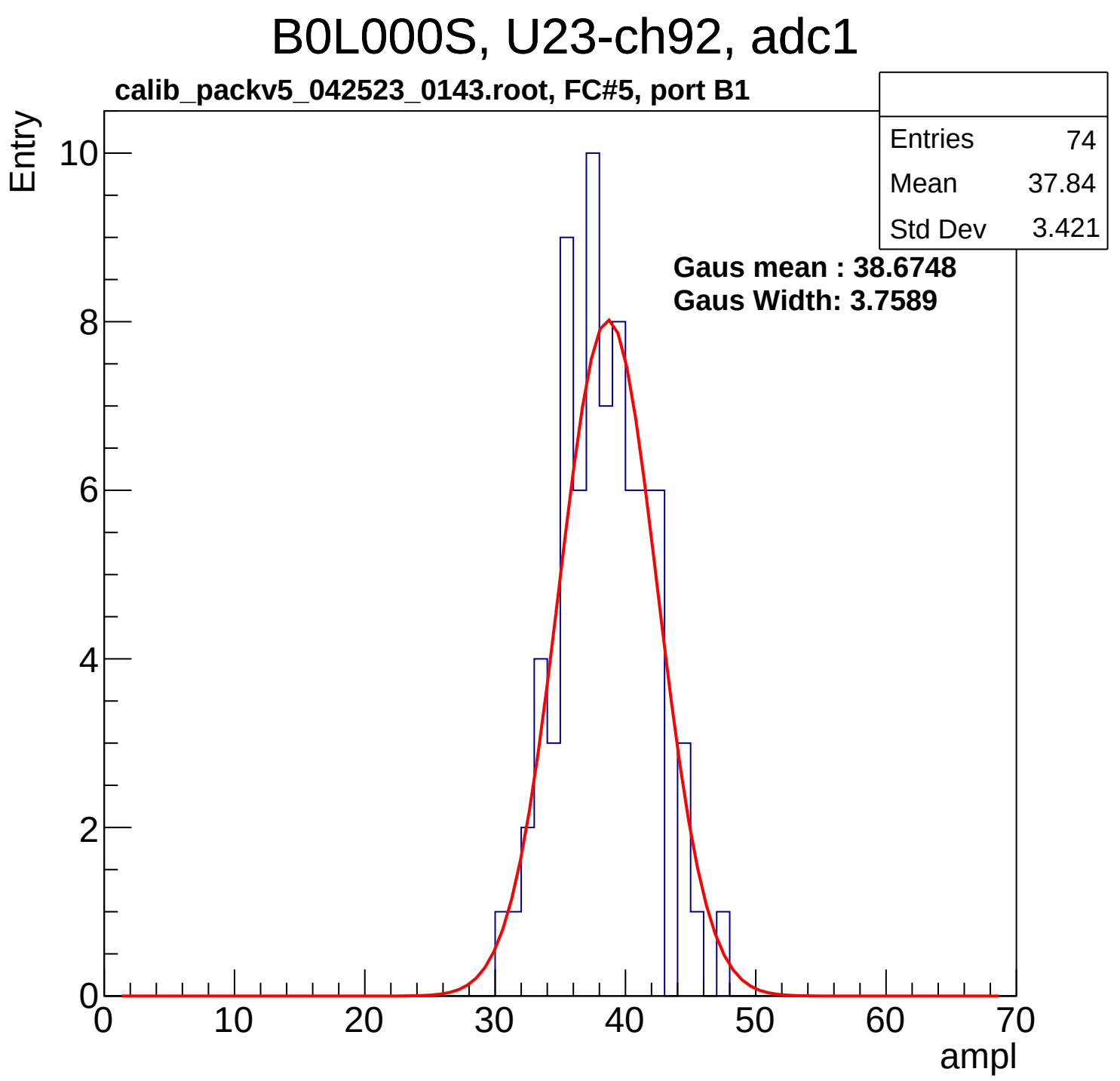

B0L000S, U23-ch91, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.76
Std Dev 3.581
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

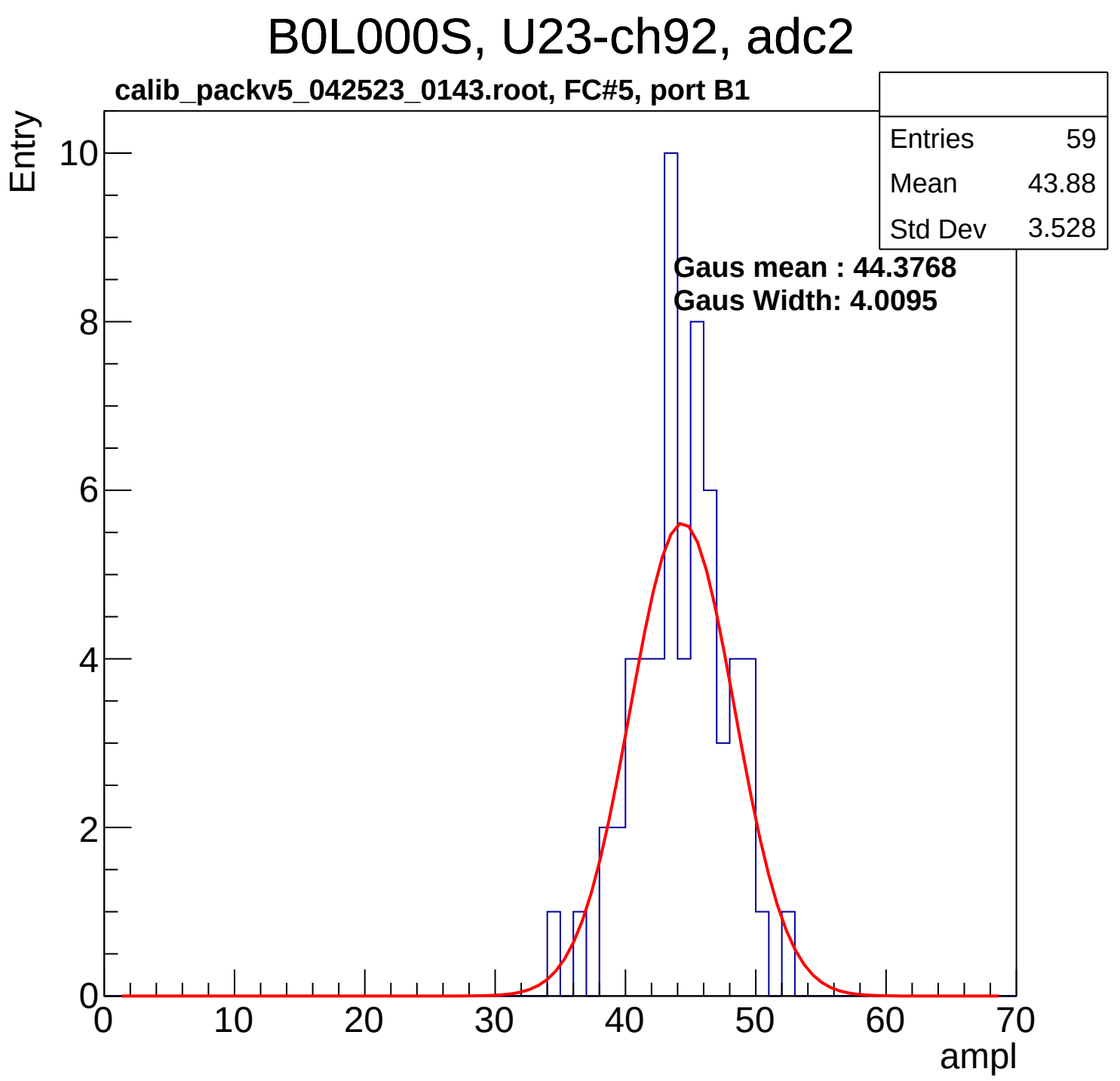

B0L000S, U23-ch91, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.24
Std Dev 2.768
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

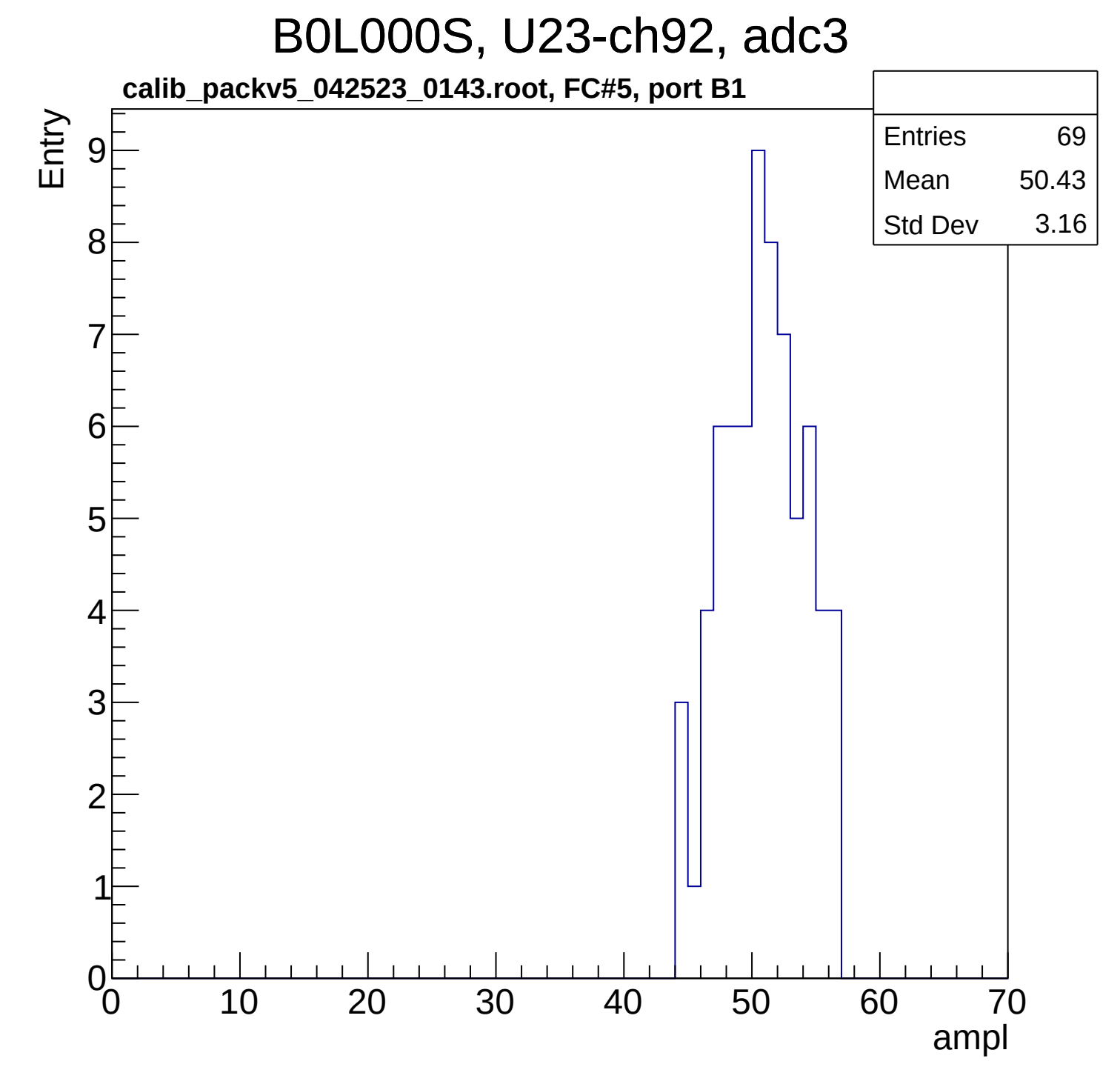

B0L000S, U23-ch91, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 55.91
Std Dev 17.73
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

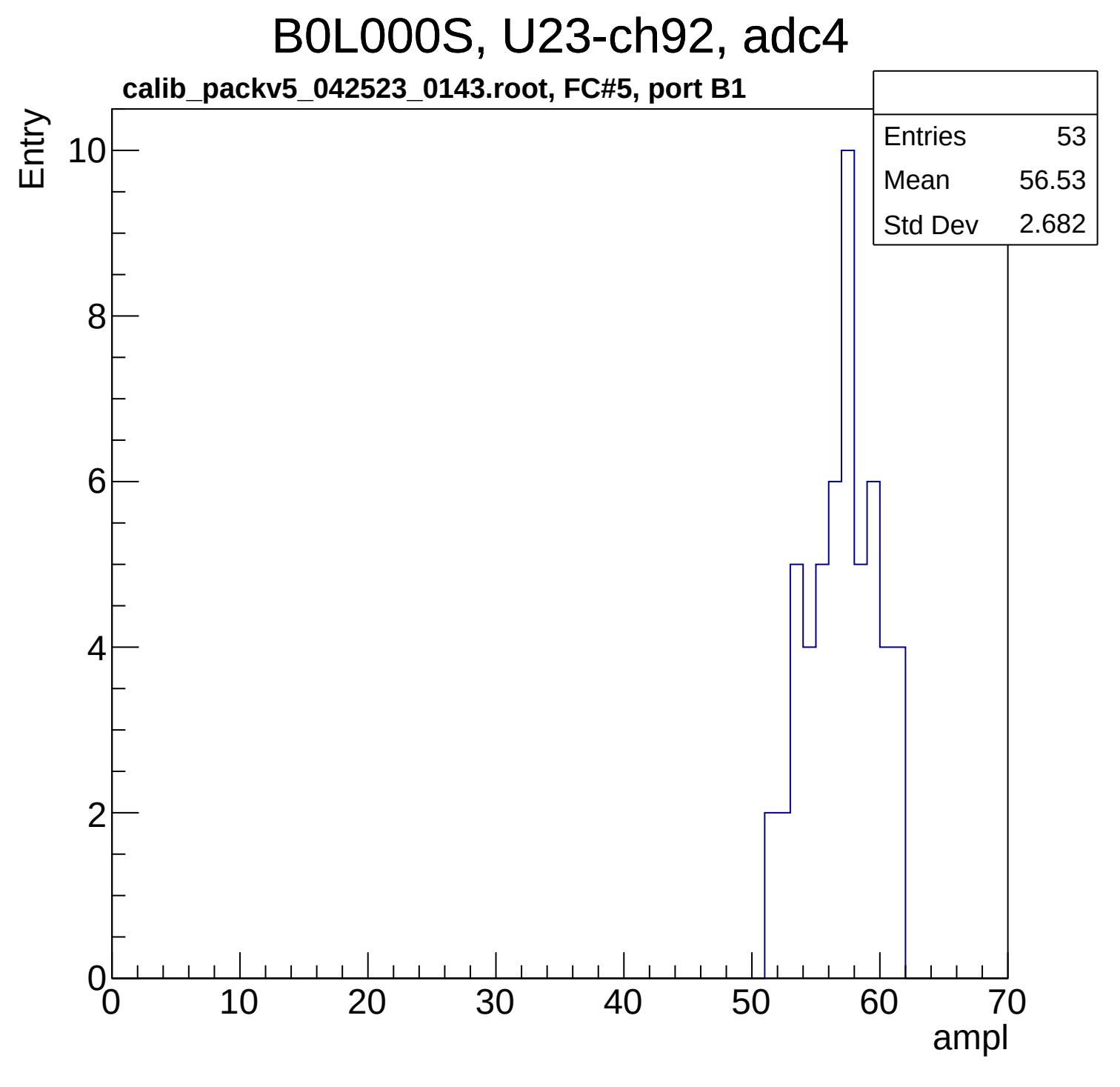

B0L000S, U23-ch91, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

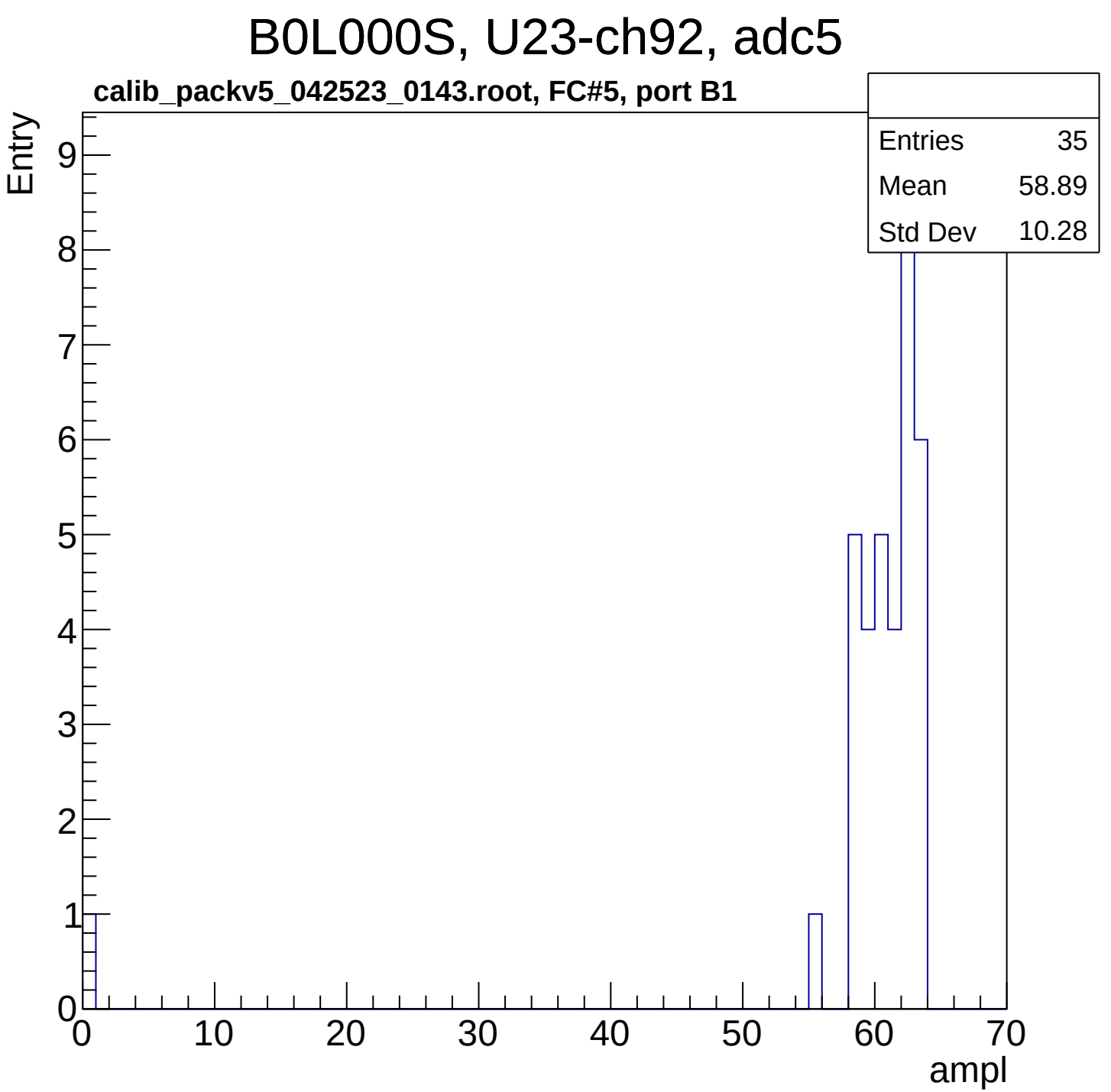

B0L000S, U23-ch92, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 30.64
Std Dev 3.1
Gaus mean : 31.1151
Gaus Width: 2.8919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

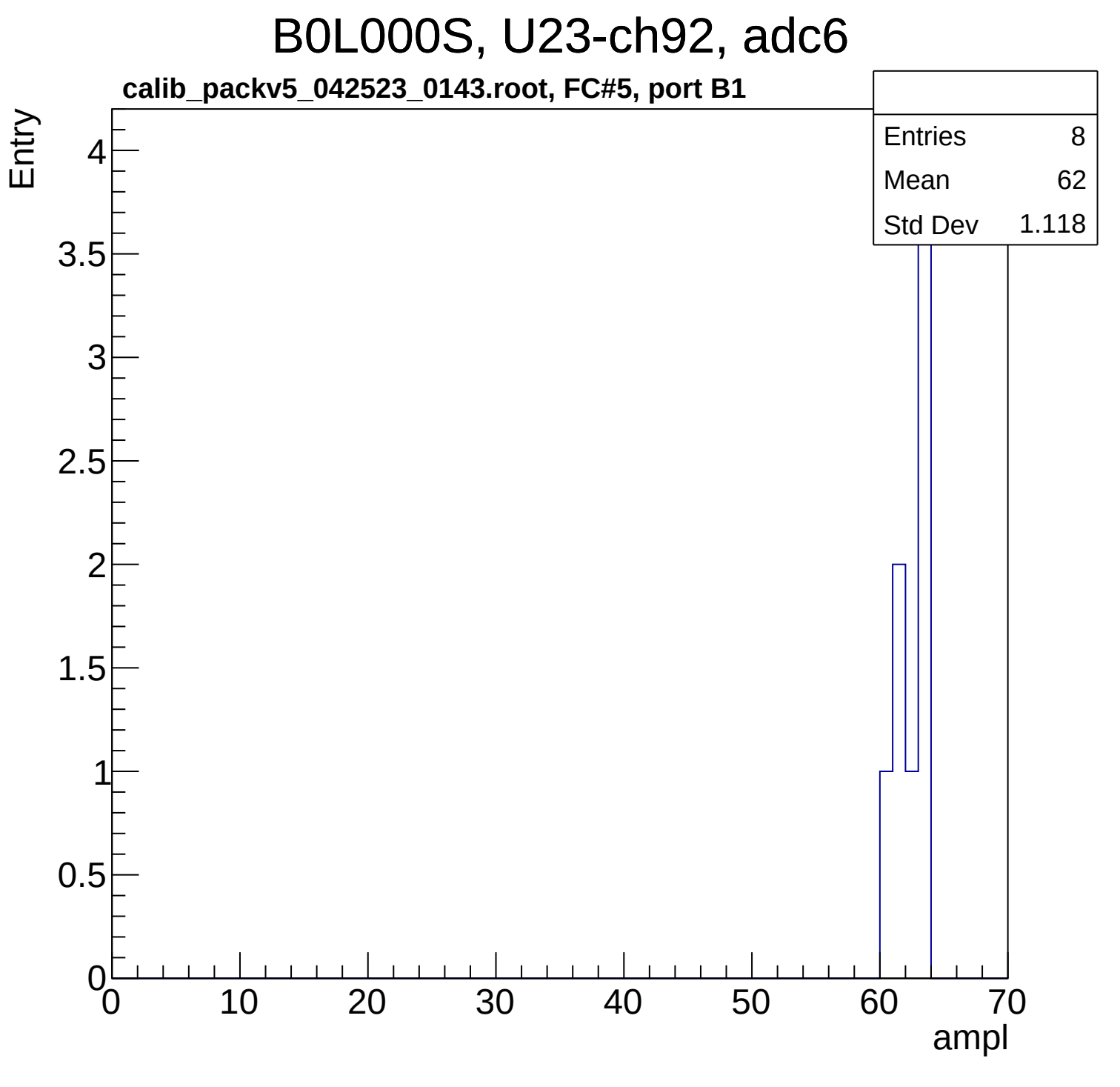

B0L000S, U23-ch92, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 37.84
Std Dev 3.421
Gaus mean : 38.6748
Gaus Width: 3.7589
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

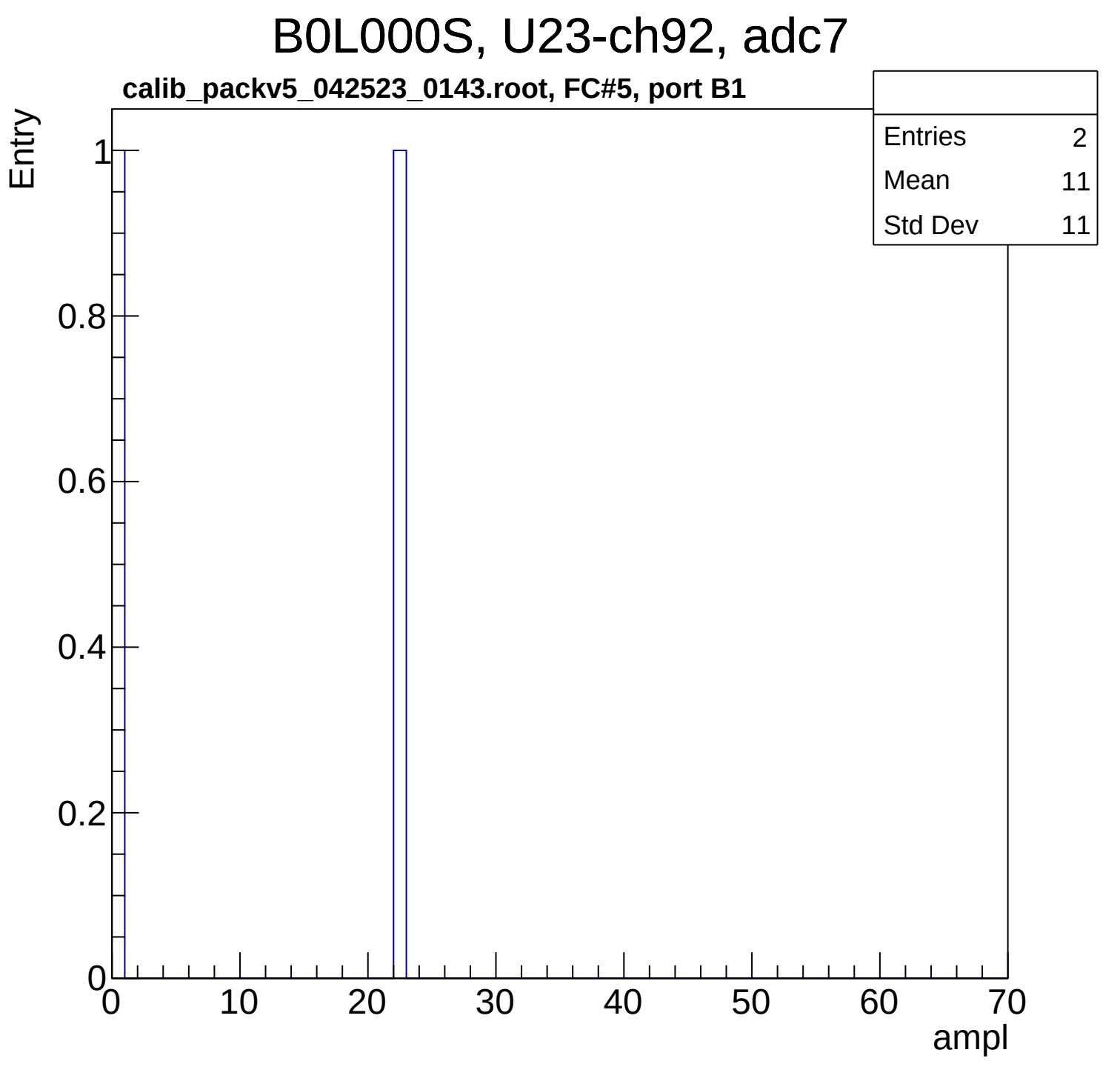

B0L000S, U23-ch92, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.88
Std Dev 3.528
Gaus mean : 44.3768
Gaus Width: 4.0095
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

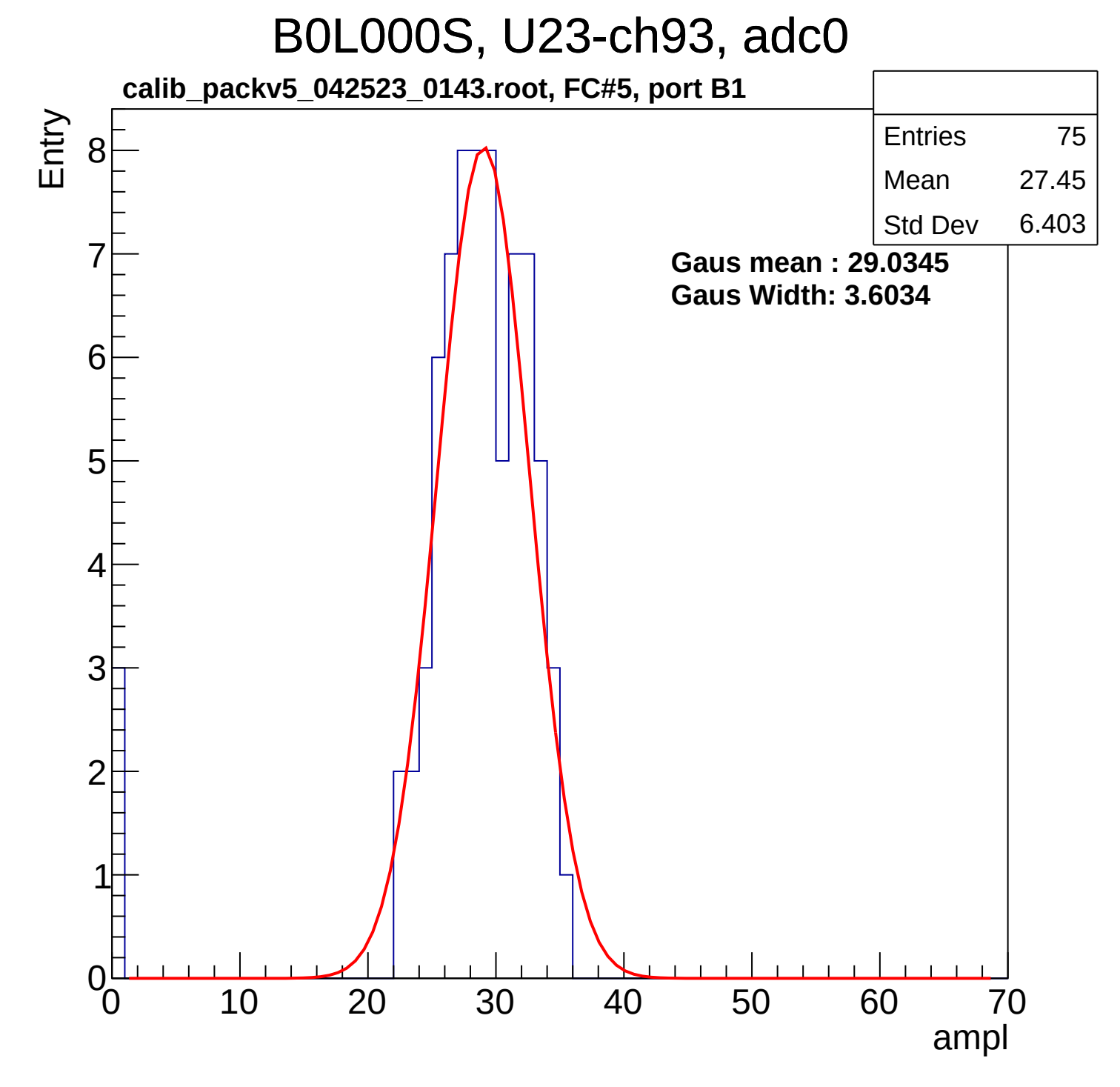

B0L000S, U23-ch92, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.43
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

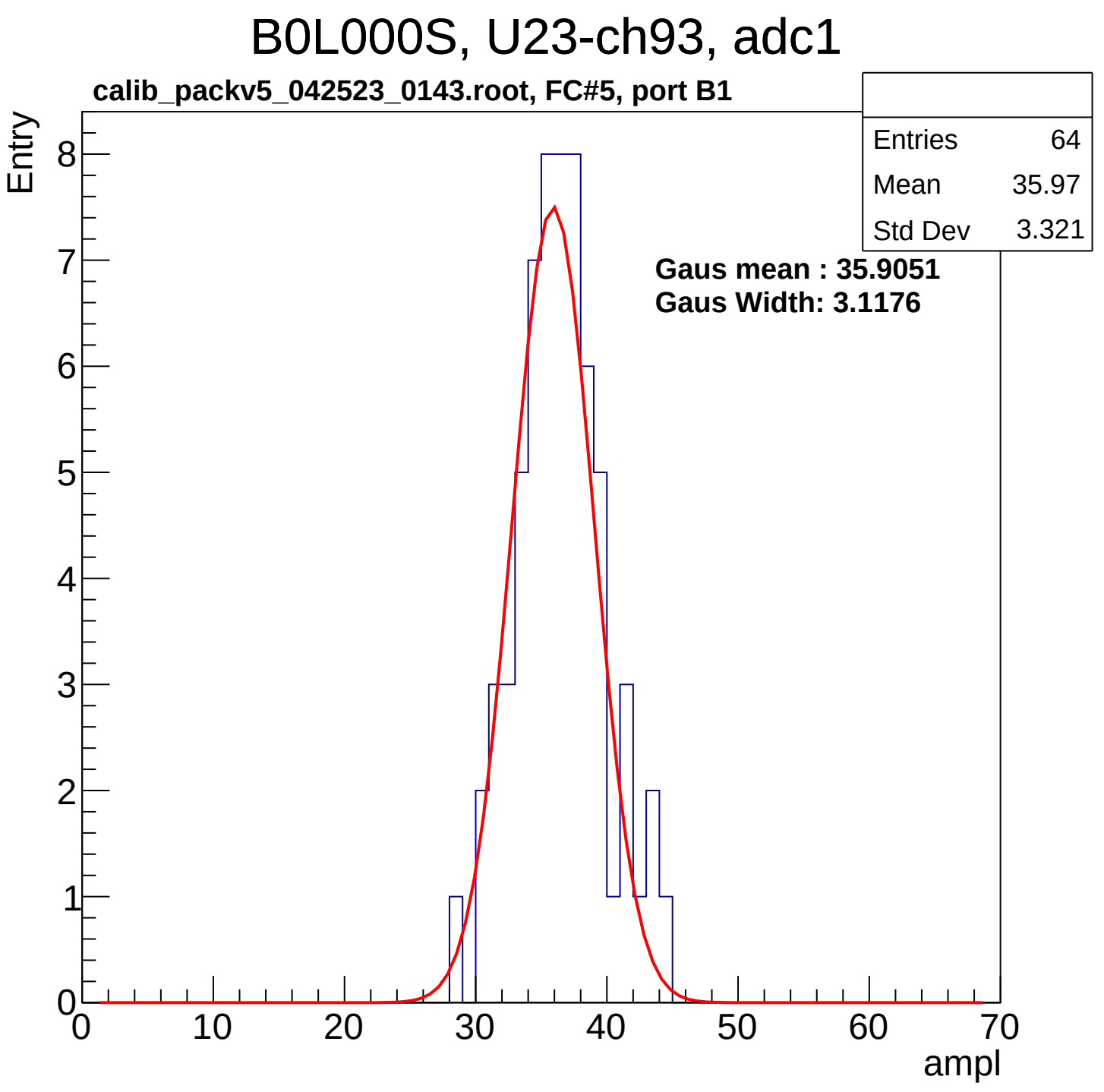

B0L000S, U23-ch92, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.53
Std Dev 2.682
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

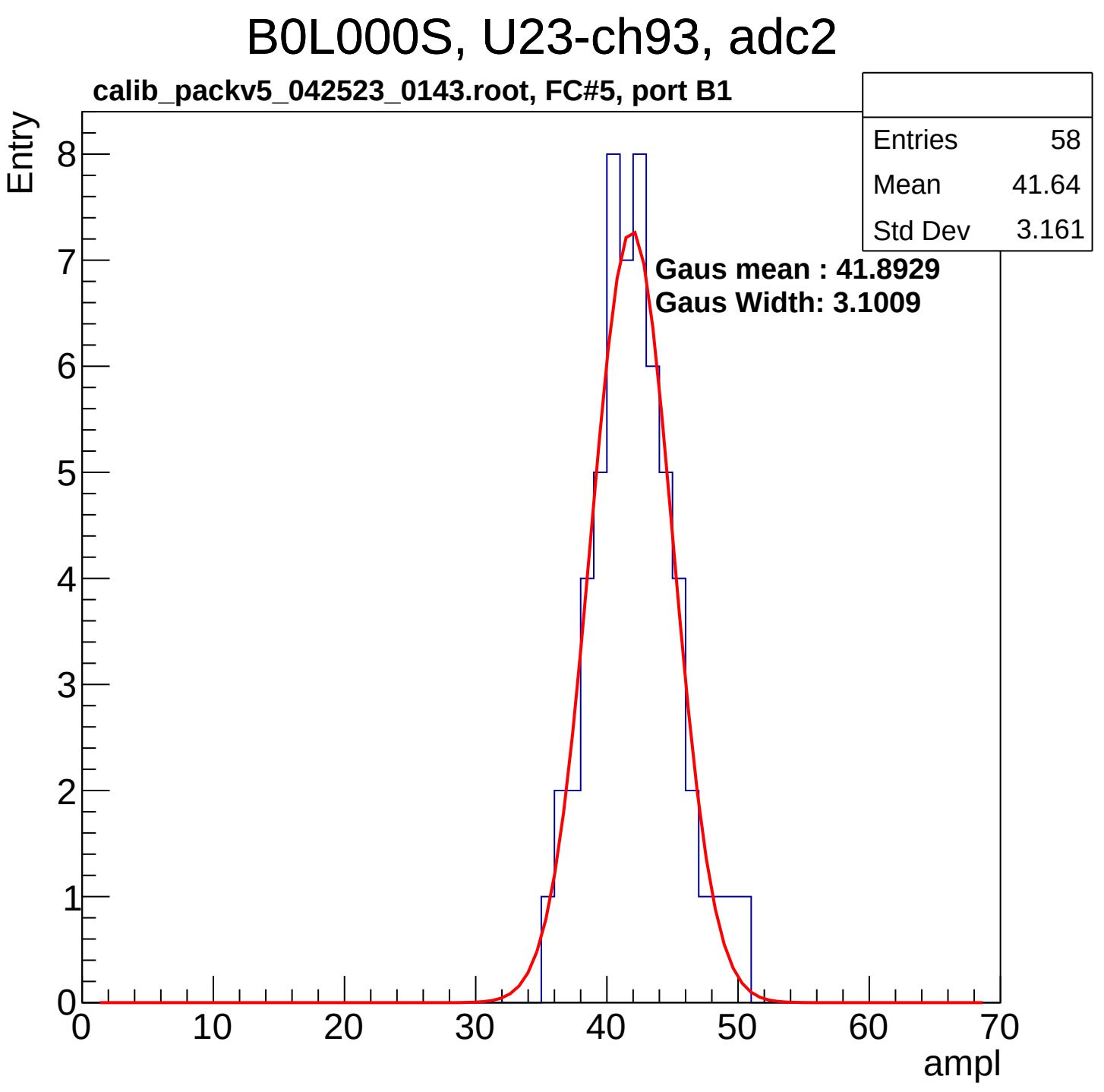

B0L000S, U23-ch92, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.89
Std Dev 10.28
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

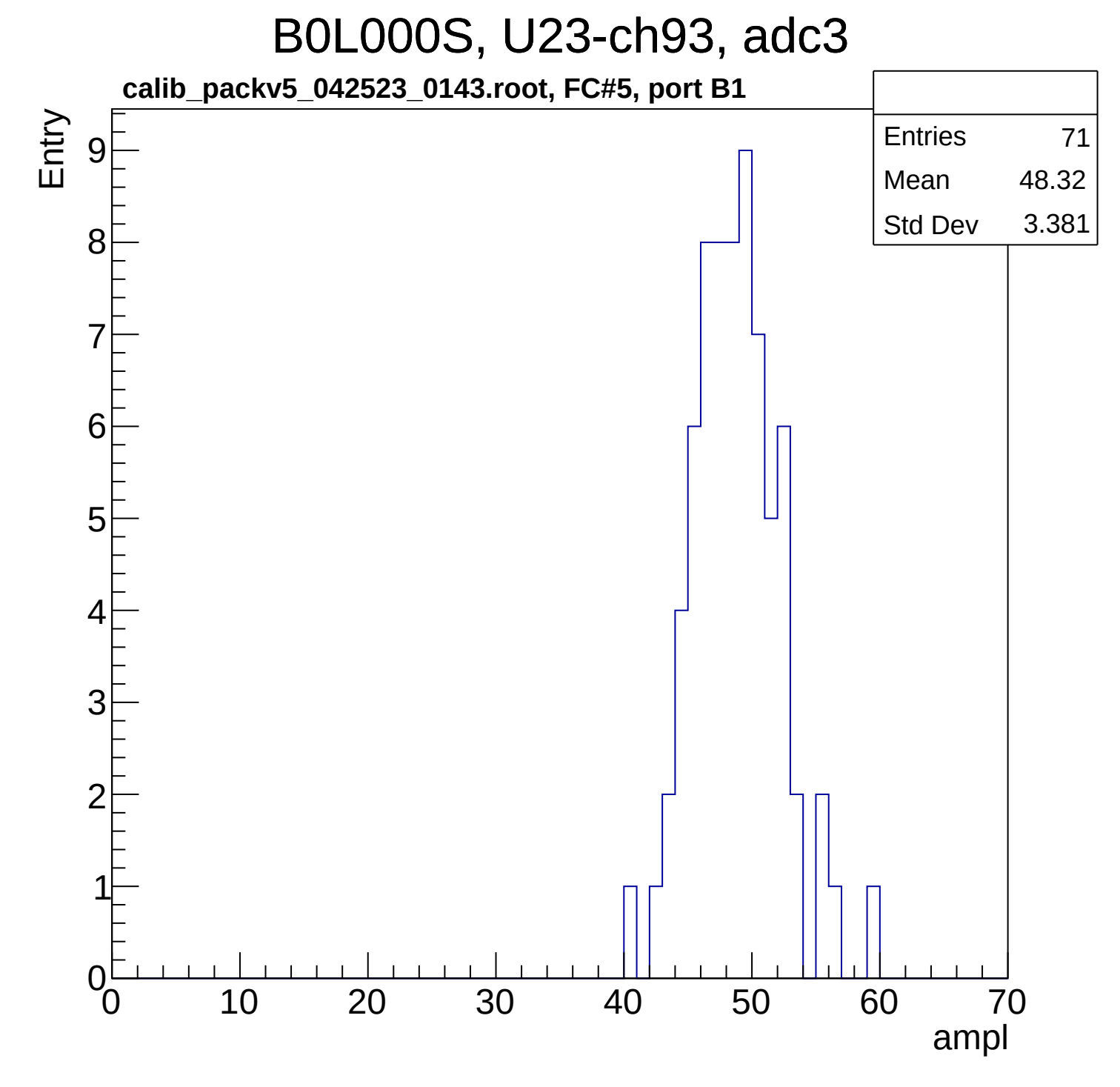

B0L000S, U23-ch92, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62
Std Dev 1.118
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

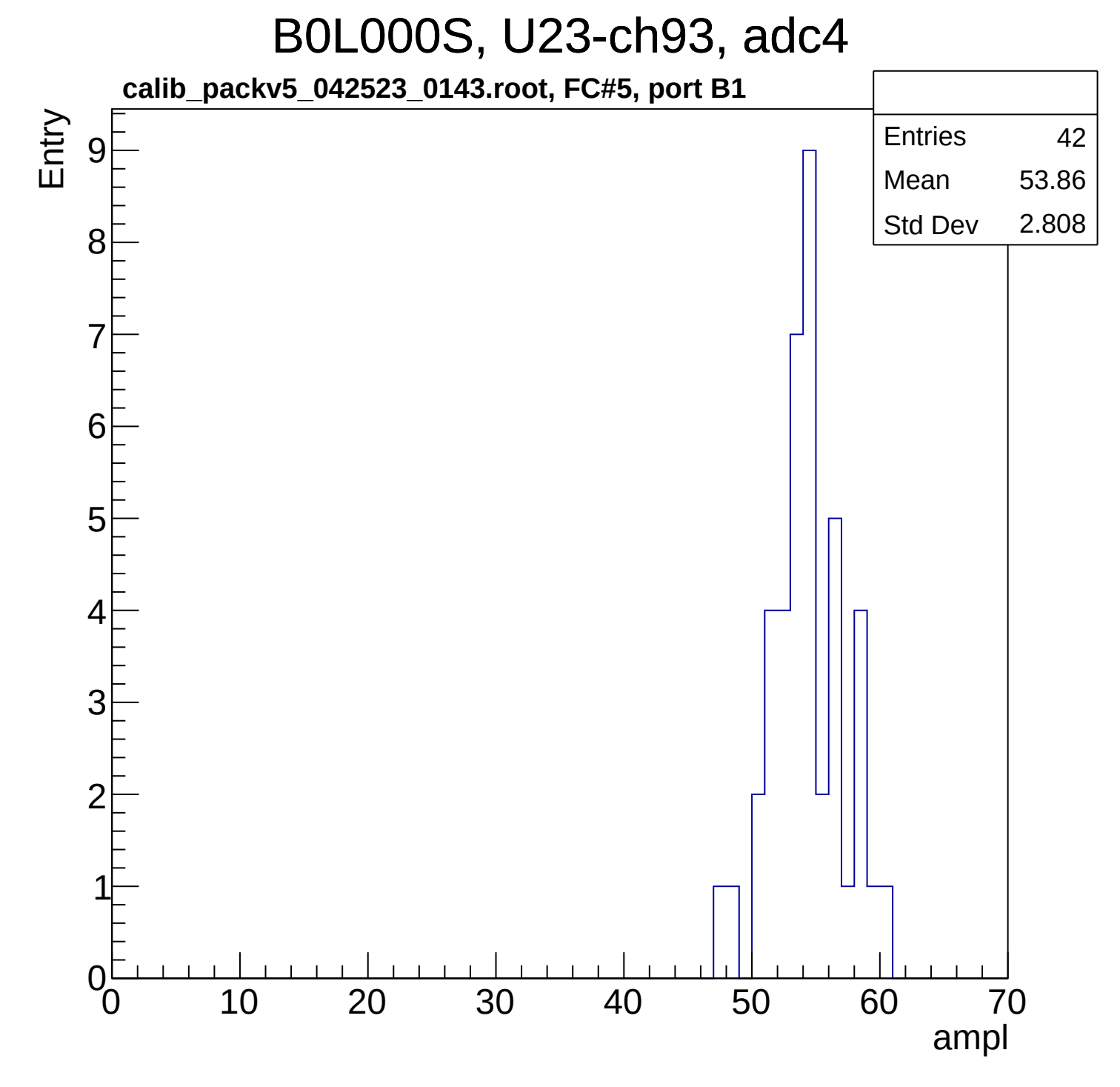

B0L000S, U23-ch92, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

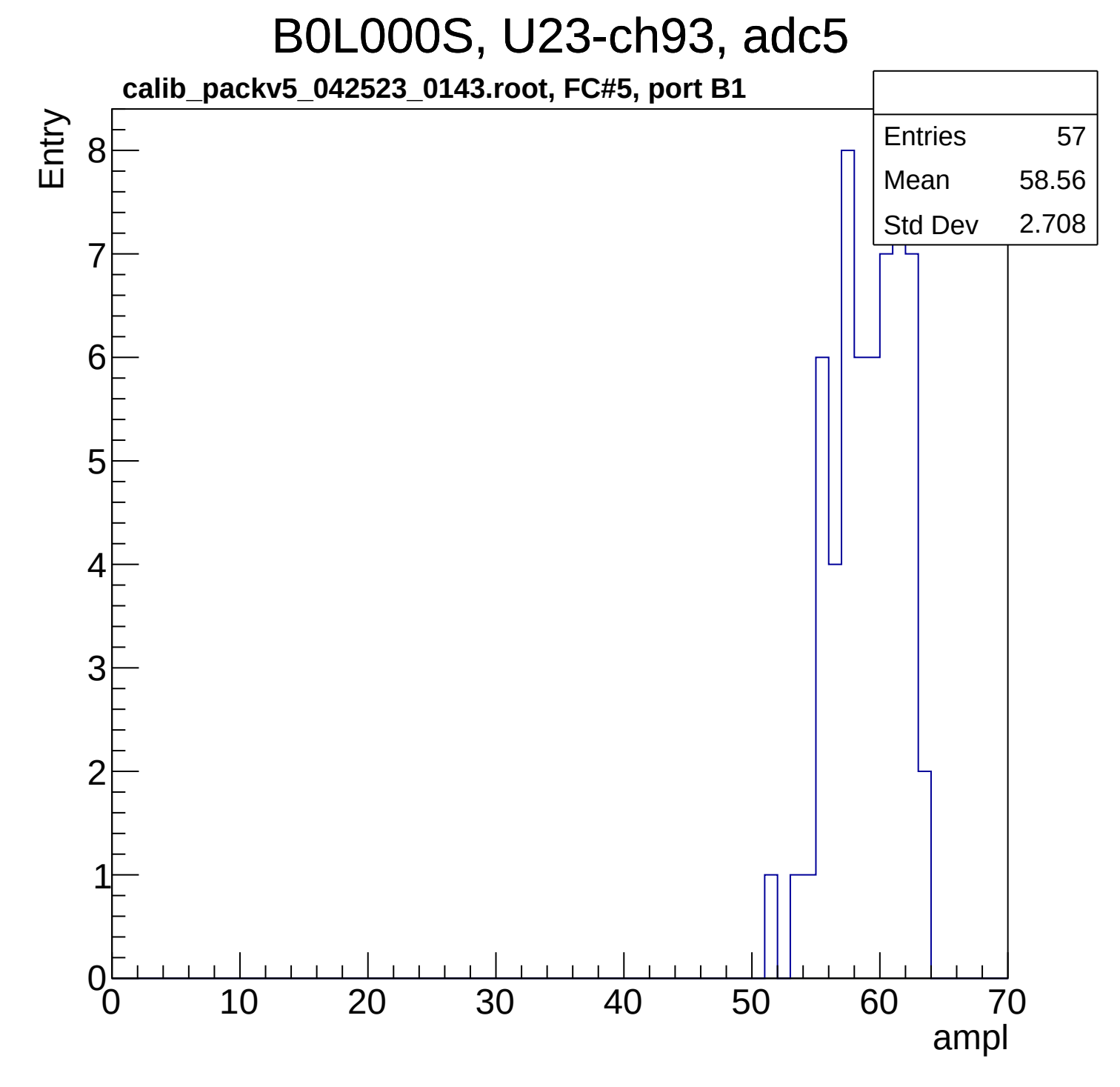

B0L000S, U23-ch93, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 27.45
Std Dev 6.403
Gaus mean : 29.0345
Gaus Width: 3.6034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

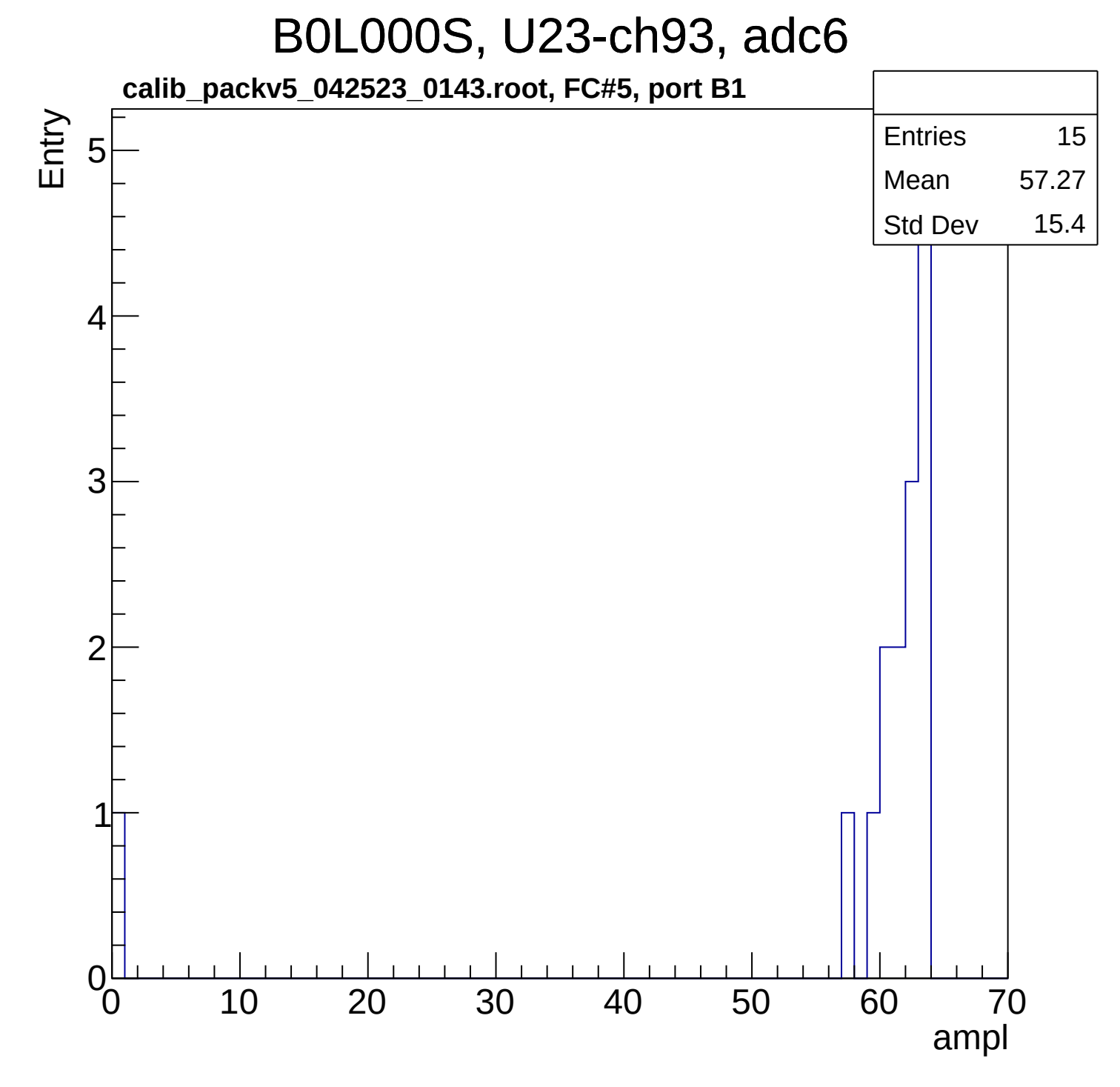

B0L000S, U23-ch93, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.97
Std Dev 3.321
Gaus mean : 35.9051
Gaus Width: 3.1176
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

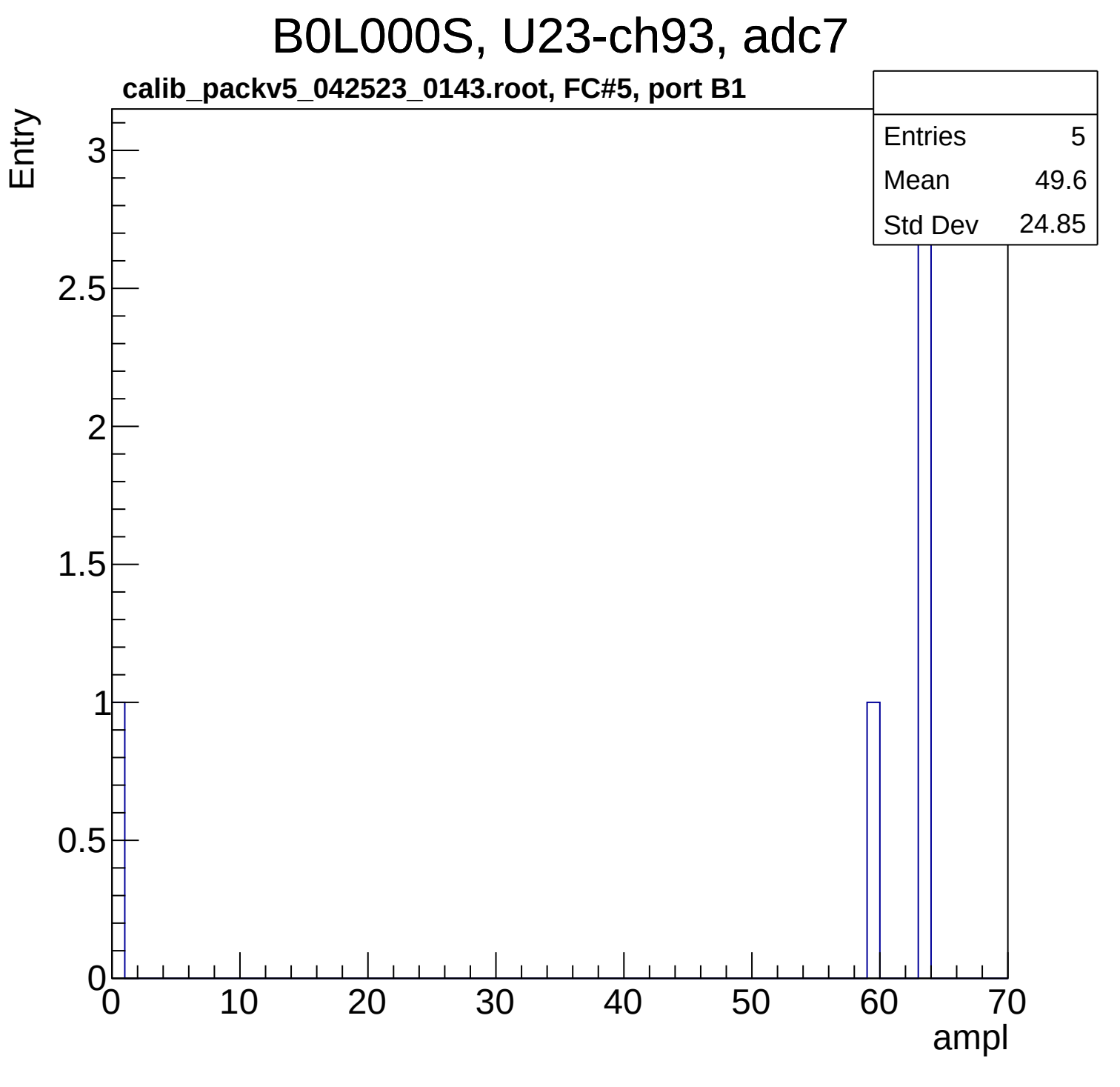

B0L000S, U23-ch93, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 41.64
Std Dev 3.161
Gaus mean : 41.8929
Gaus Width: 3.1009
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

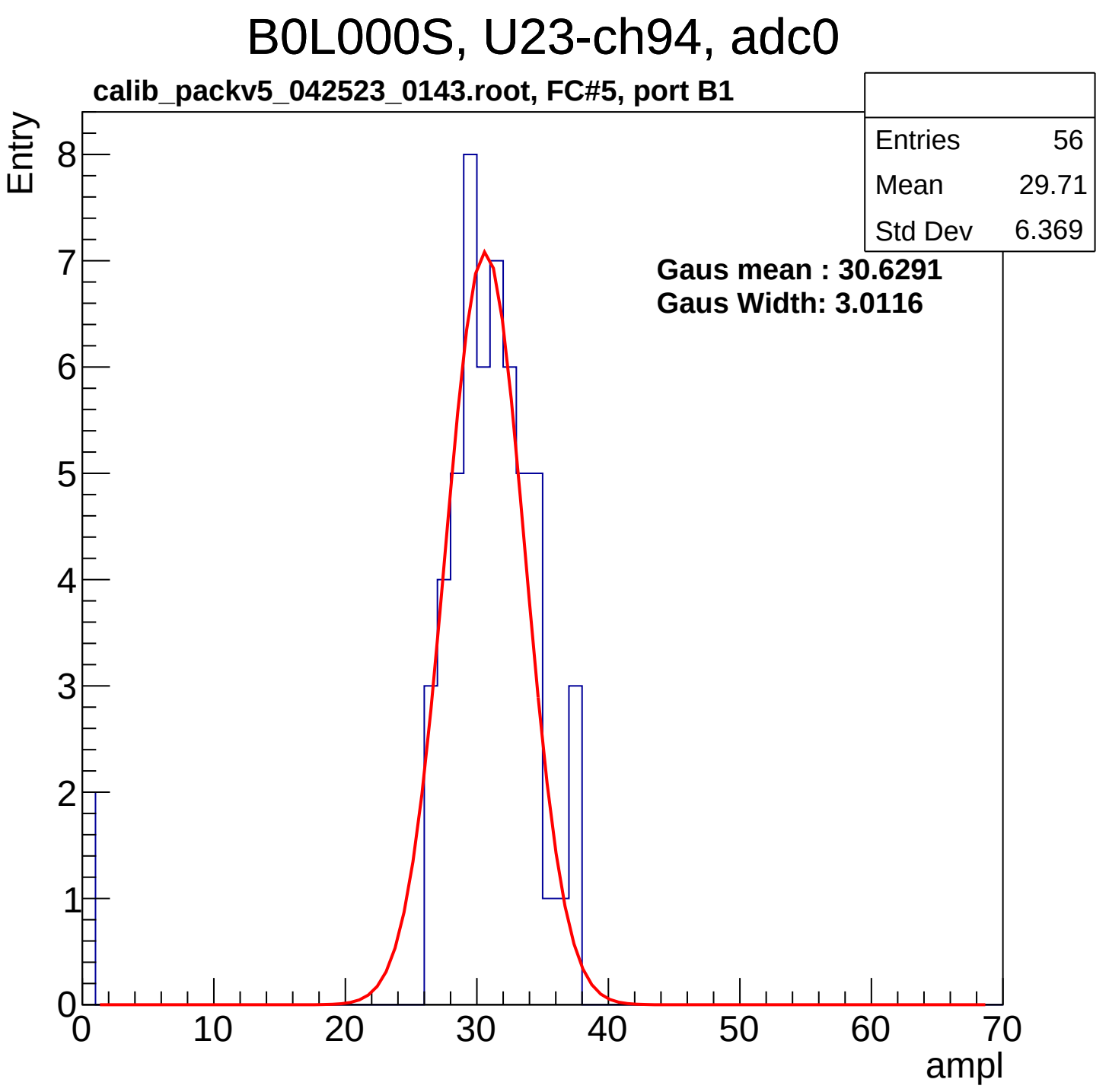

B0L000S, U23-ch93, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 48.32
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

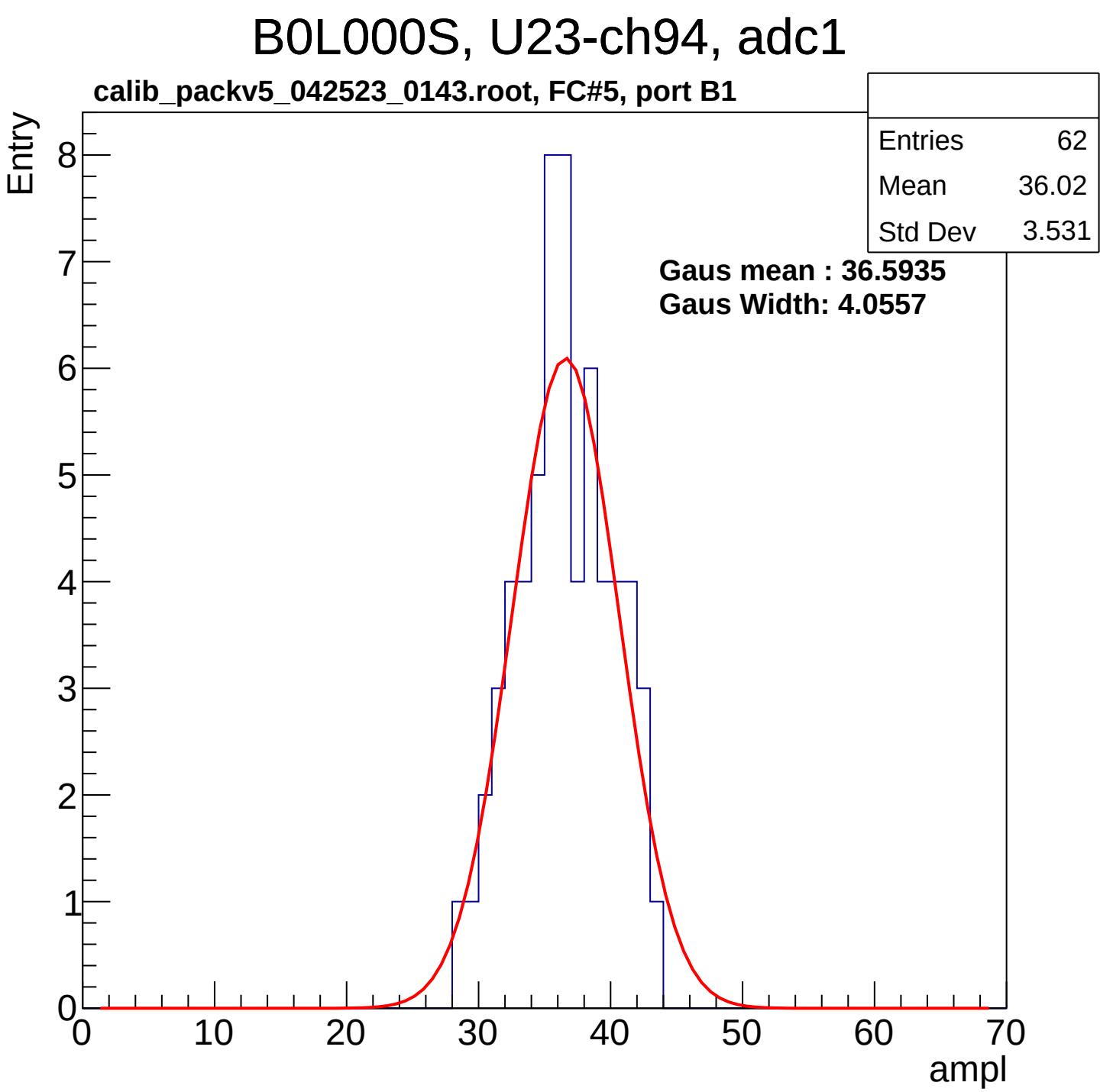

B0L000S, U23-ch93, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 53.86
Std Dev 2.808
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

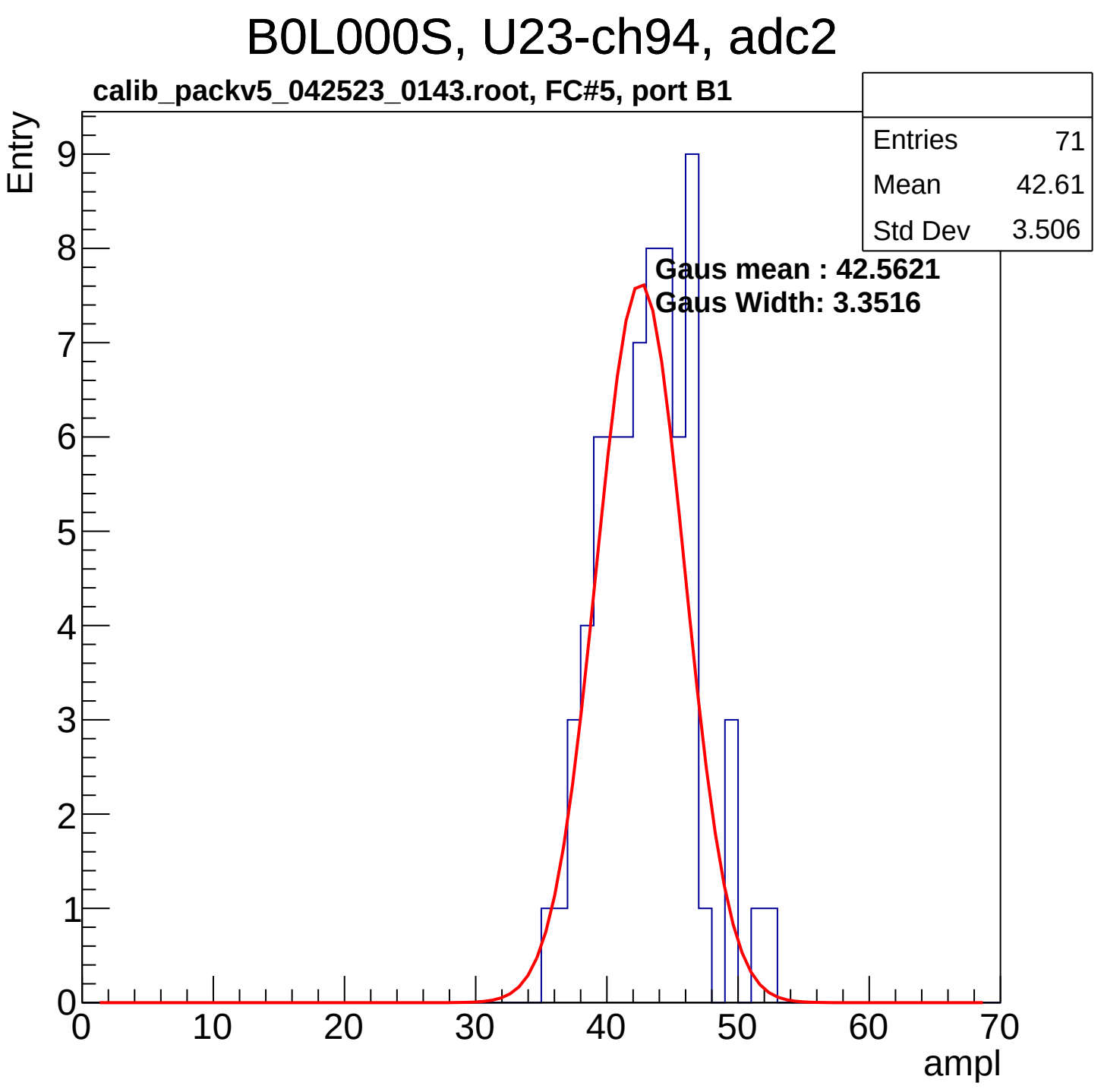

B0L000S, U23-ch93, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.56
Std Dev 2.708
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

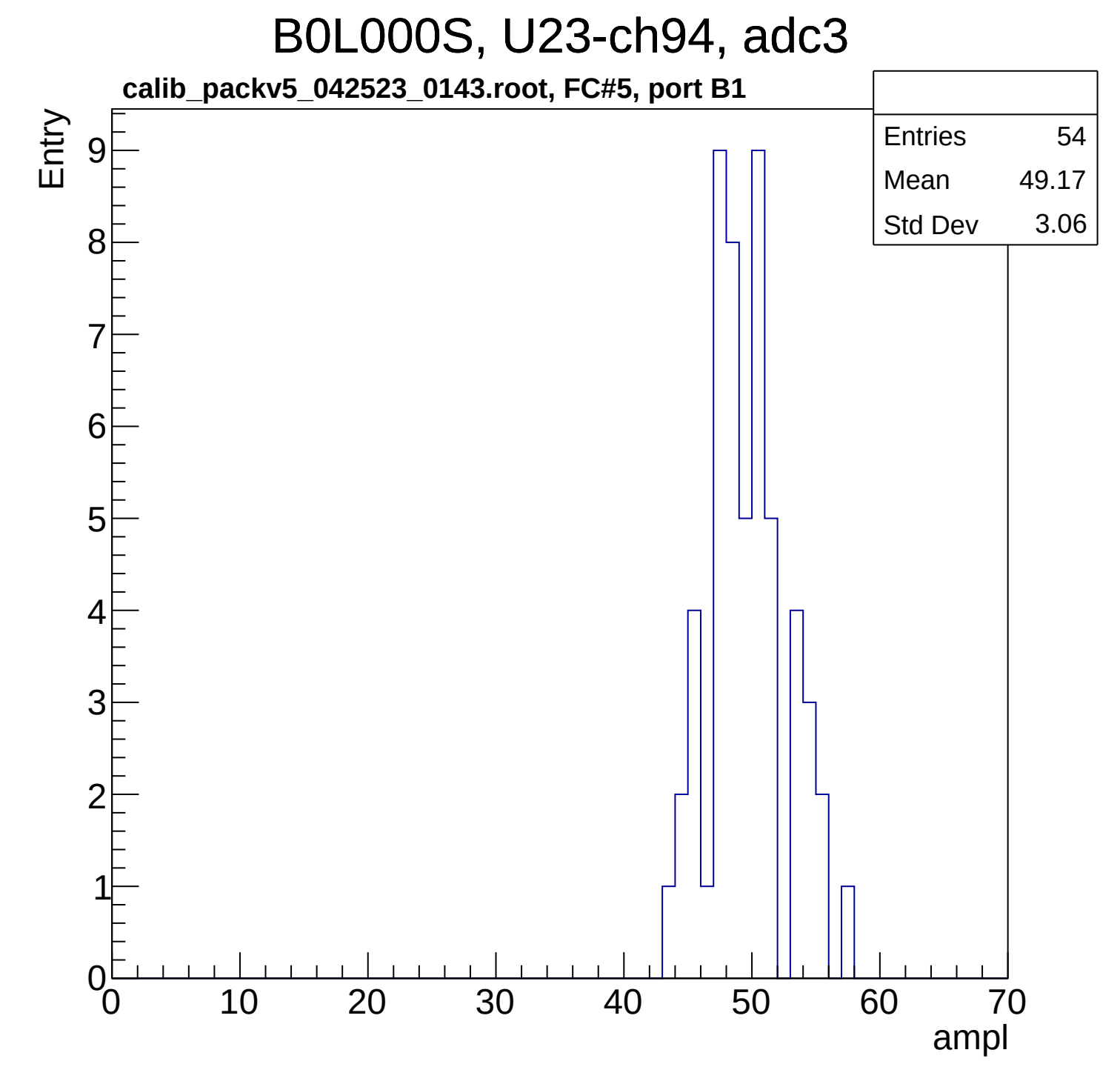

B0L000S, U23-ch93, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.27
Std Dev 15.4
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

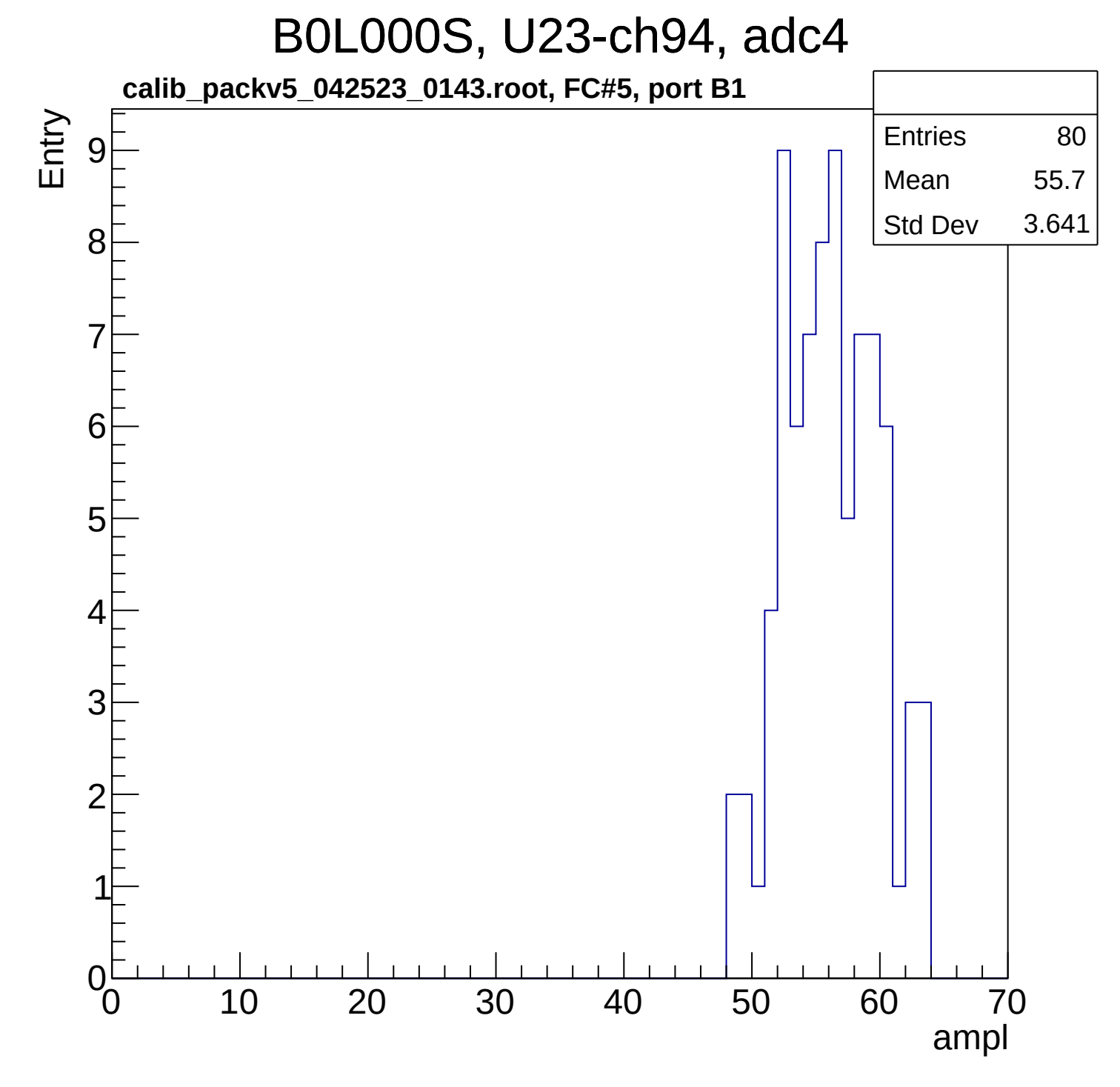

B0L000S, U23-ch93, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.6
Std Dev 24.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

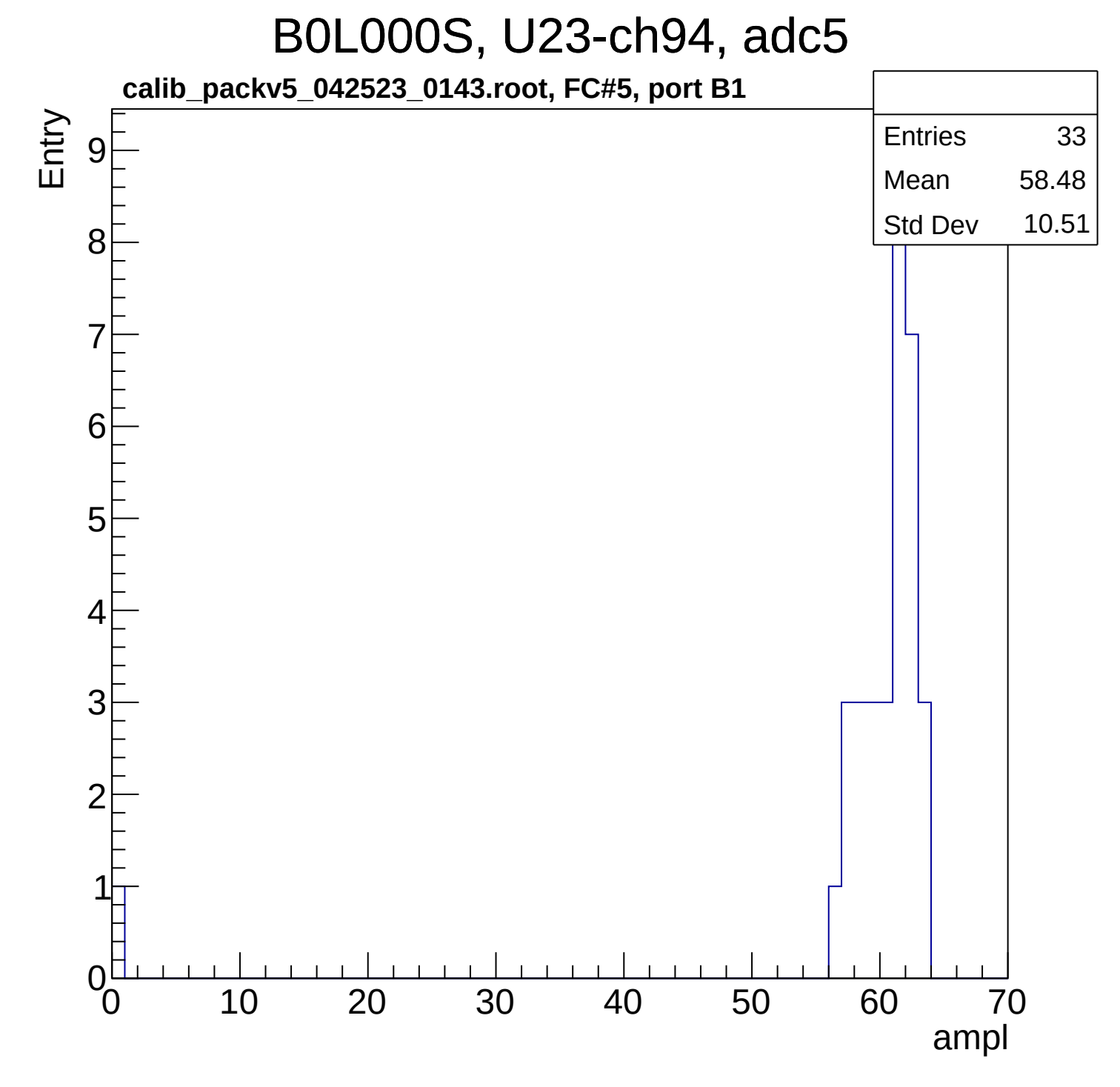

B0L000S, U23-ch94, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 29.71
Std Dev 6.369
Gaus mean : 30.6291
Gaus Width: 3.0116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

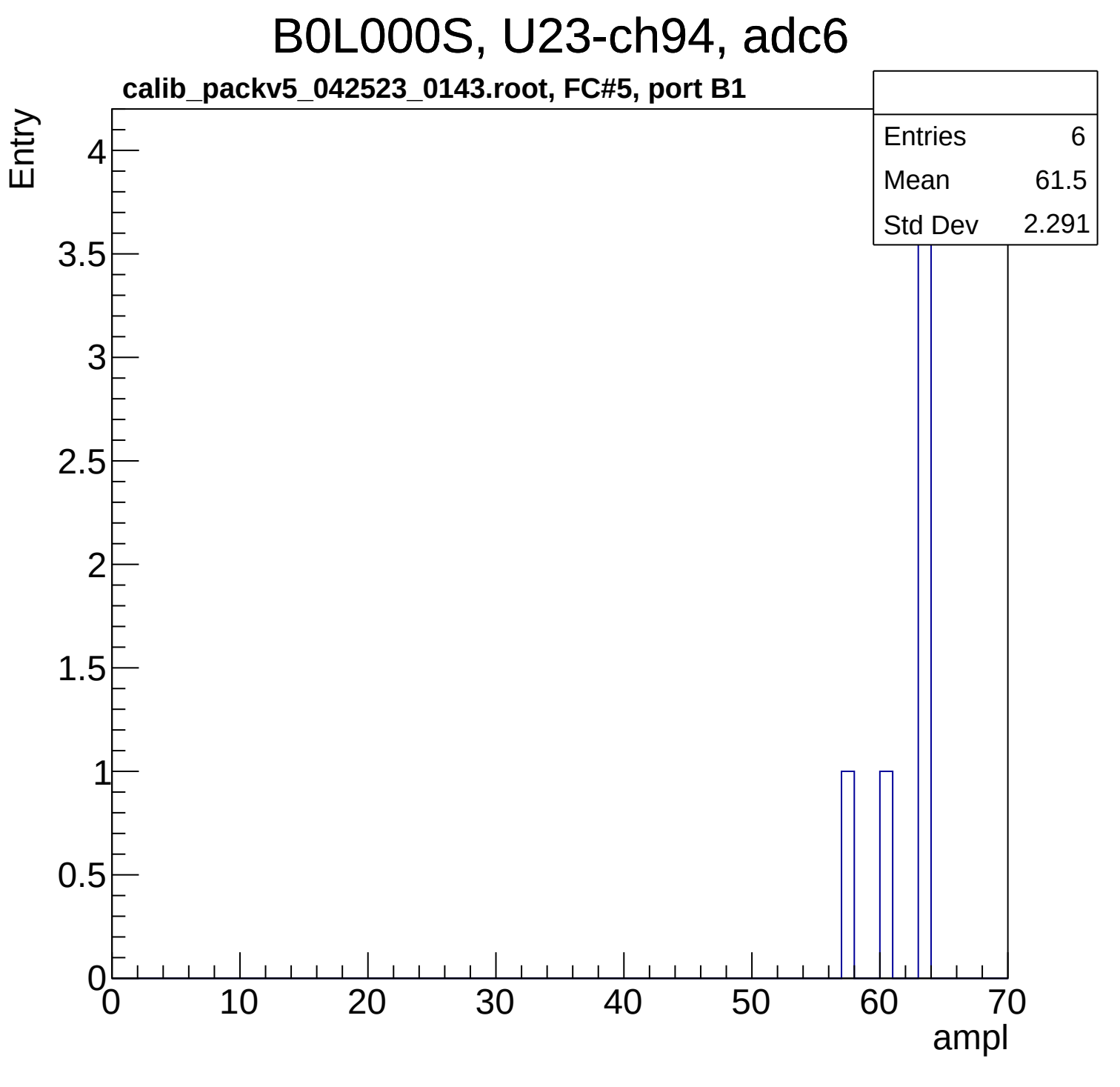

B0L000S, U23-ch94, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.02
Std Dev 3.531
Gaus mean : 36.5935
Gaus Width: 4.0557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

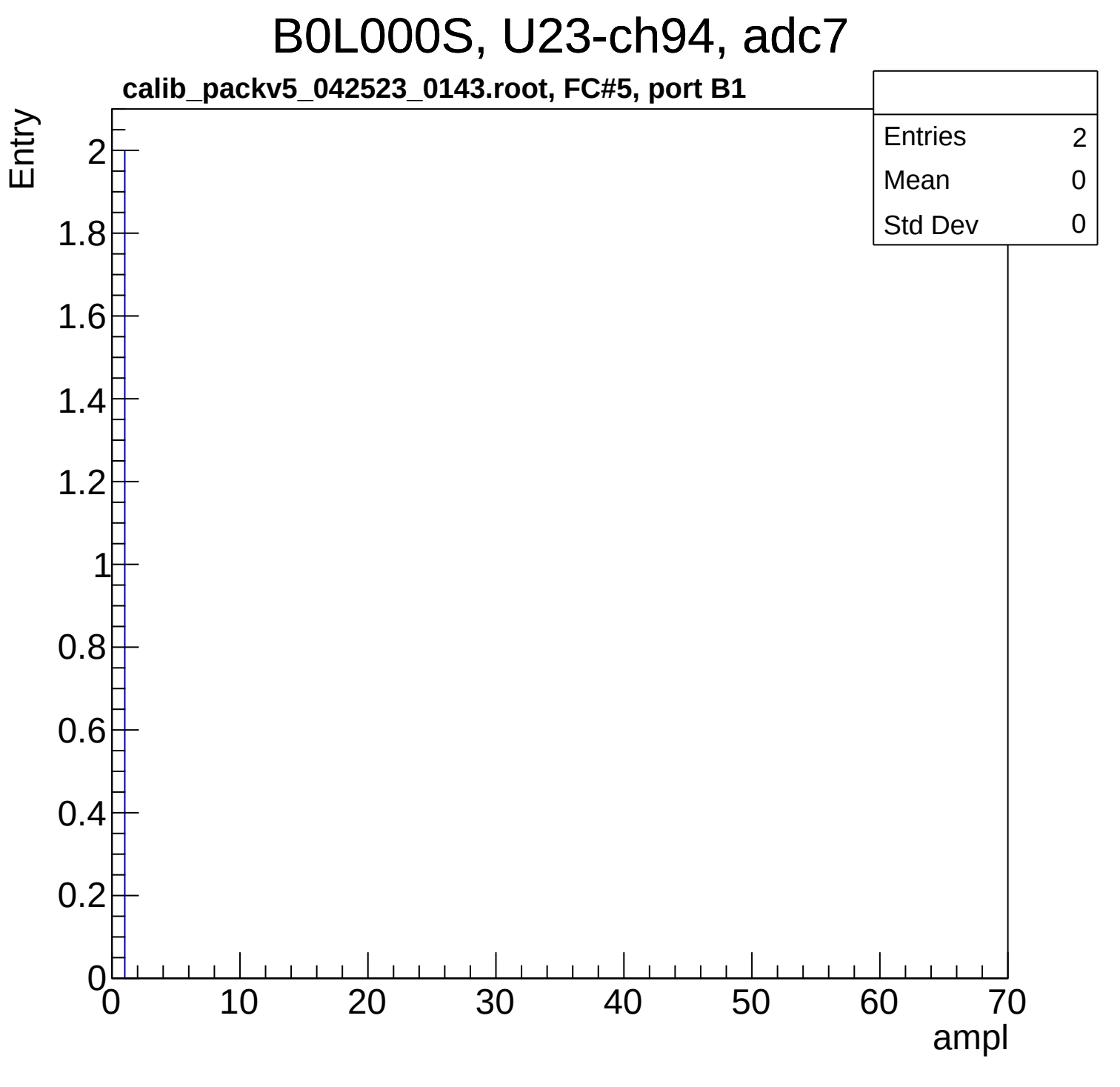

B0L000S, U23-ch94, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.61
Std Dev 3.506
Gaus mean : 42.5621
Gaus Width: 3.3516
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

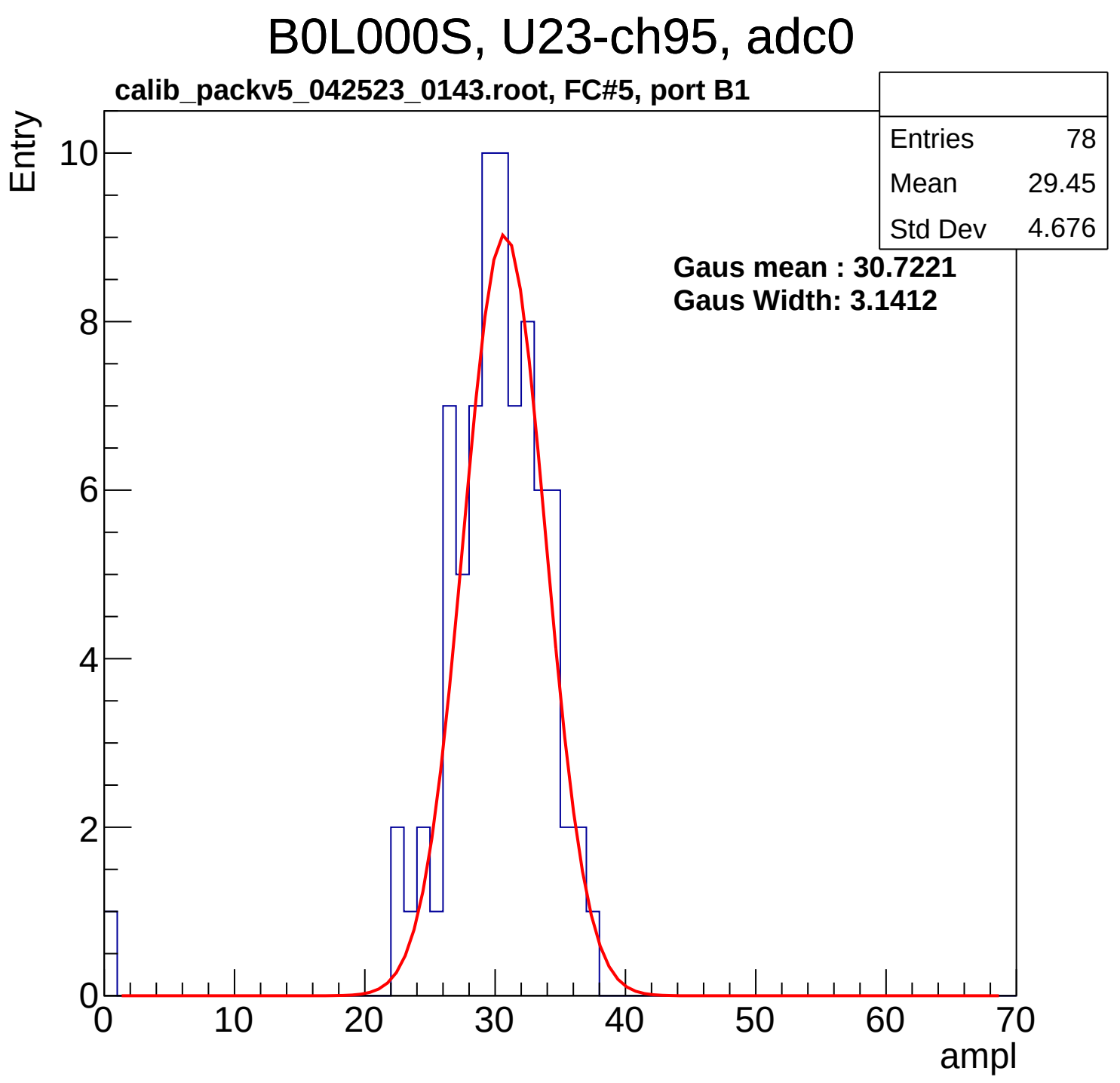

B0L000S, U23-ch94, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 49.17
Std Dev 3.06
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

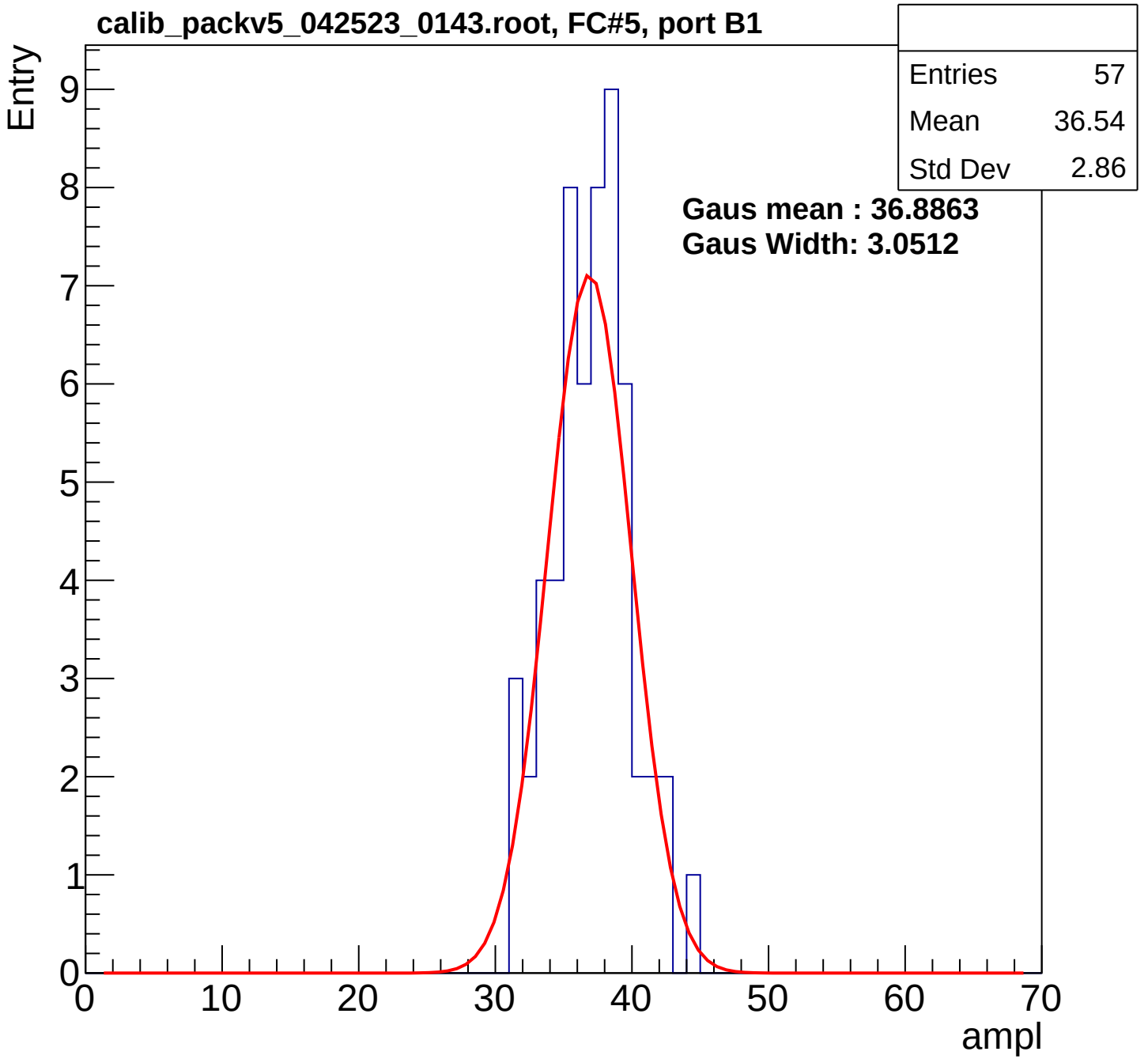

B0L000S, U23-ch94, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 55.7
Std Dev 3.641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

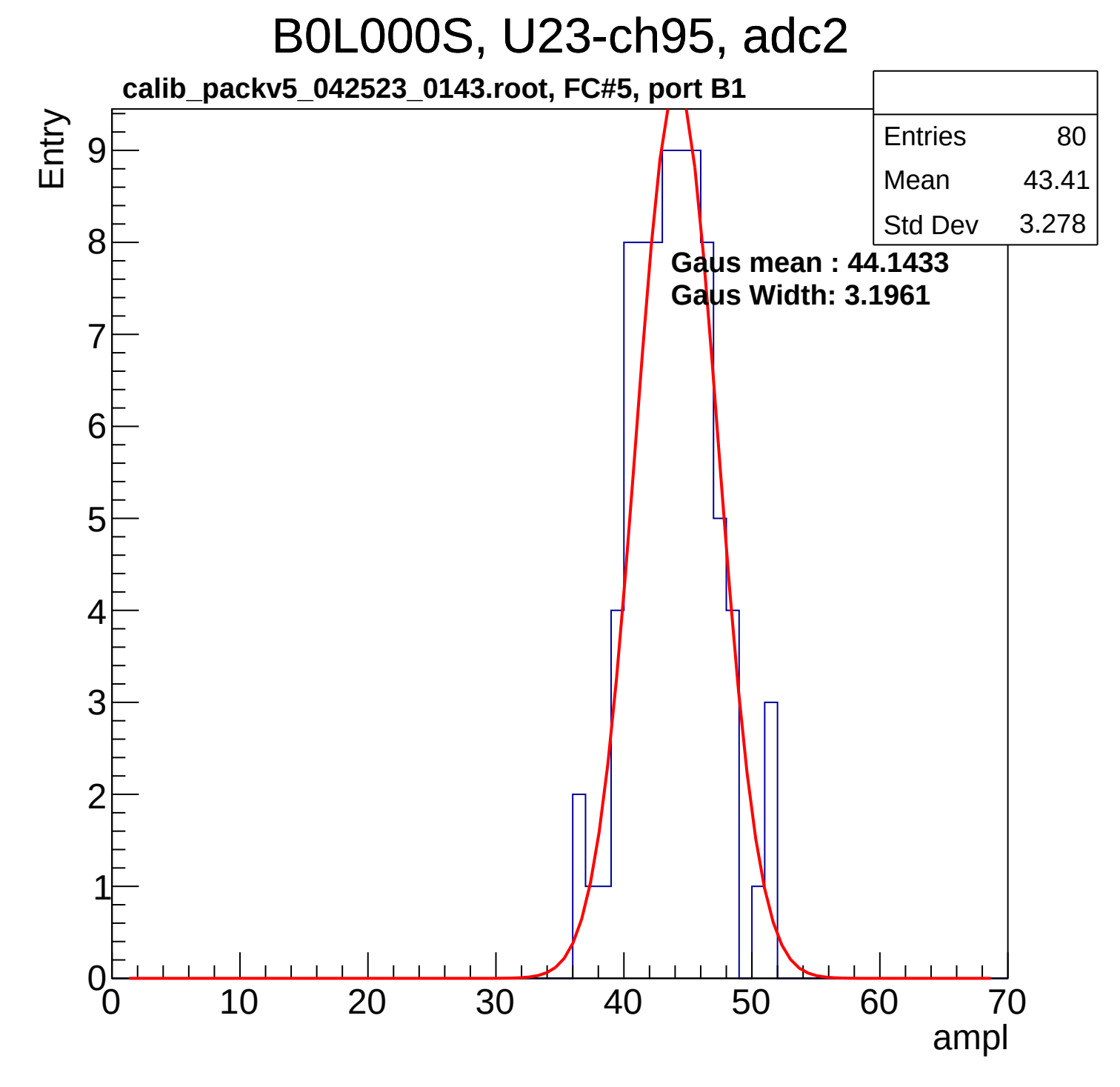

B0L000S, U23-ch94, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 58.48
Std Dev 10.51
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

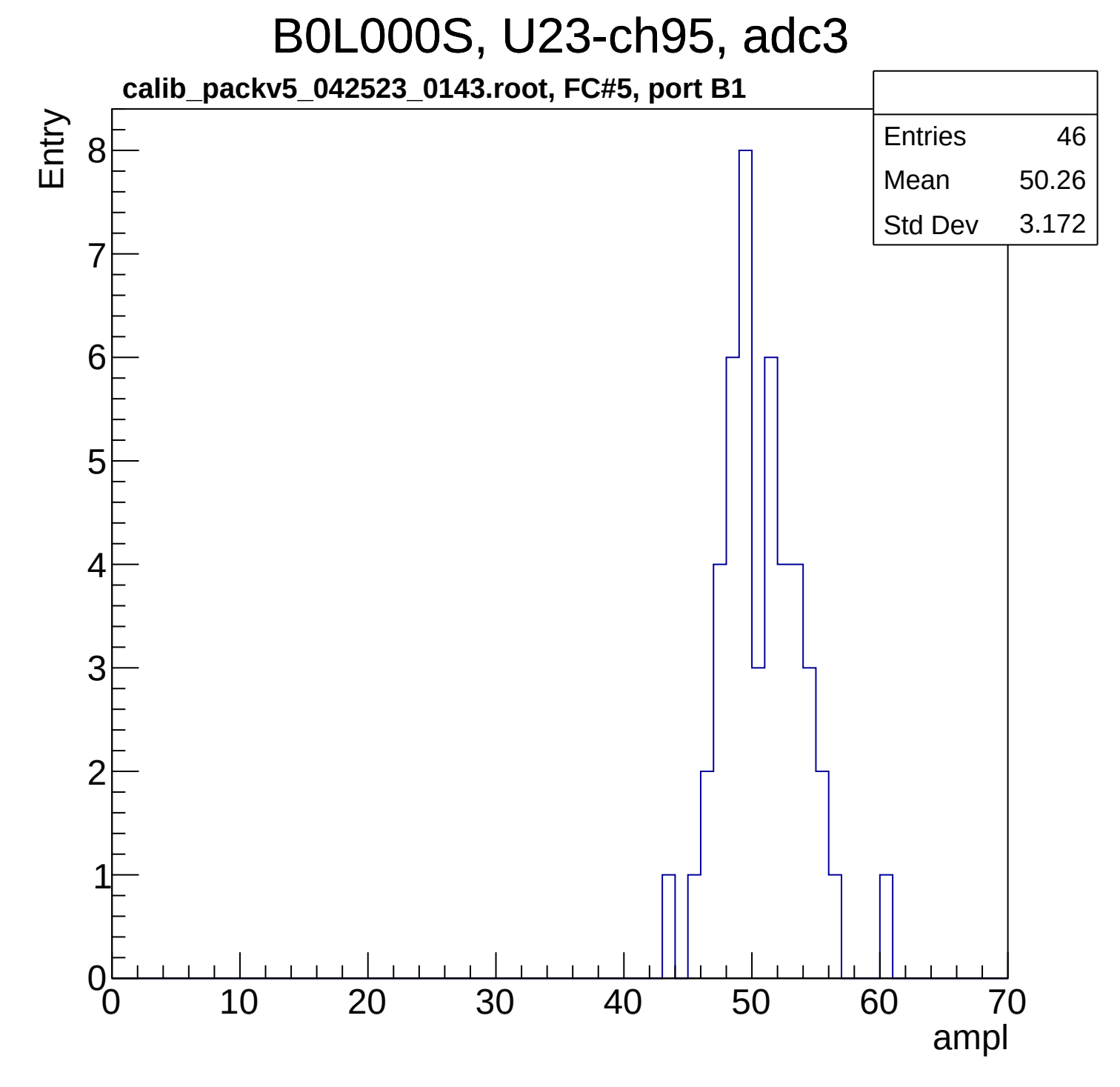

B0L000S, U23-ch94, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.5
Std Dev 2.291
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

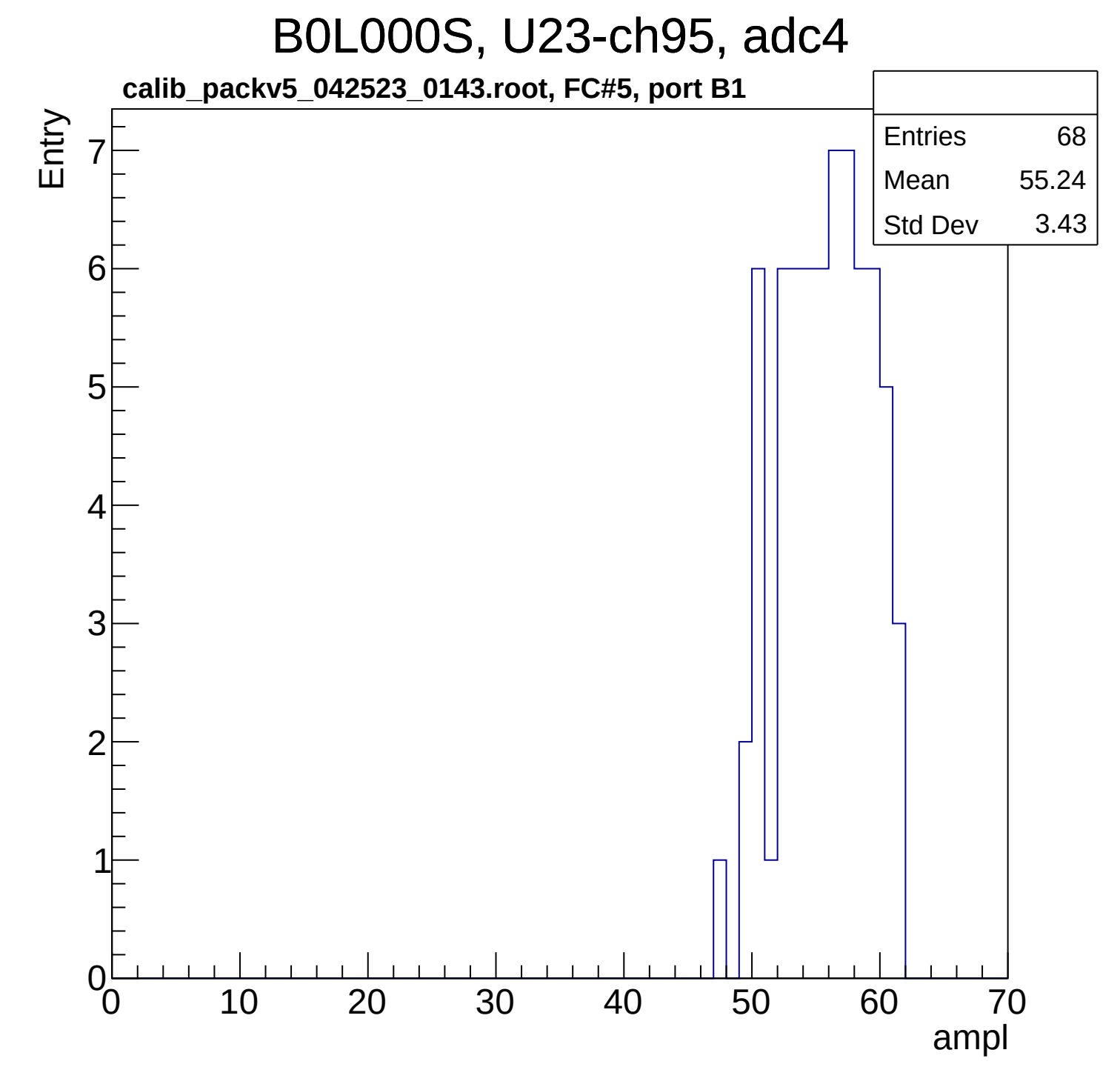

B0L000S, U23-ch94, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

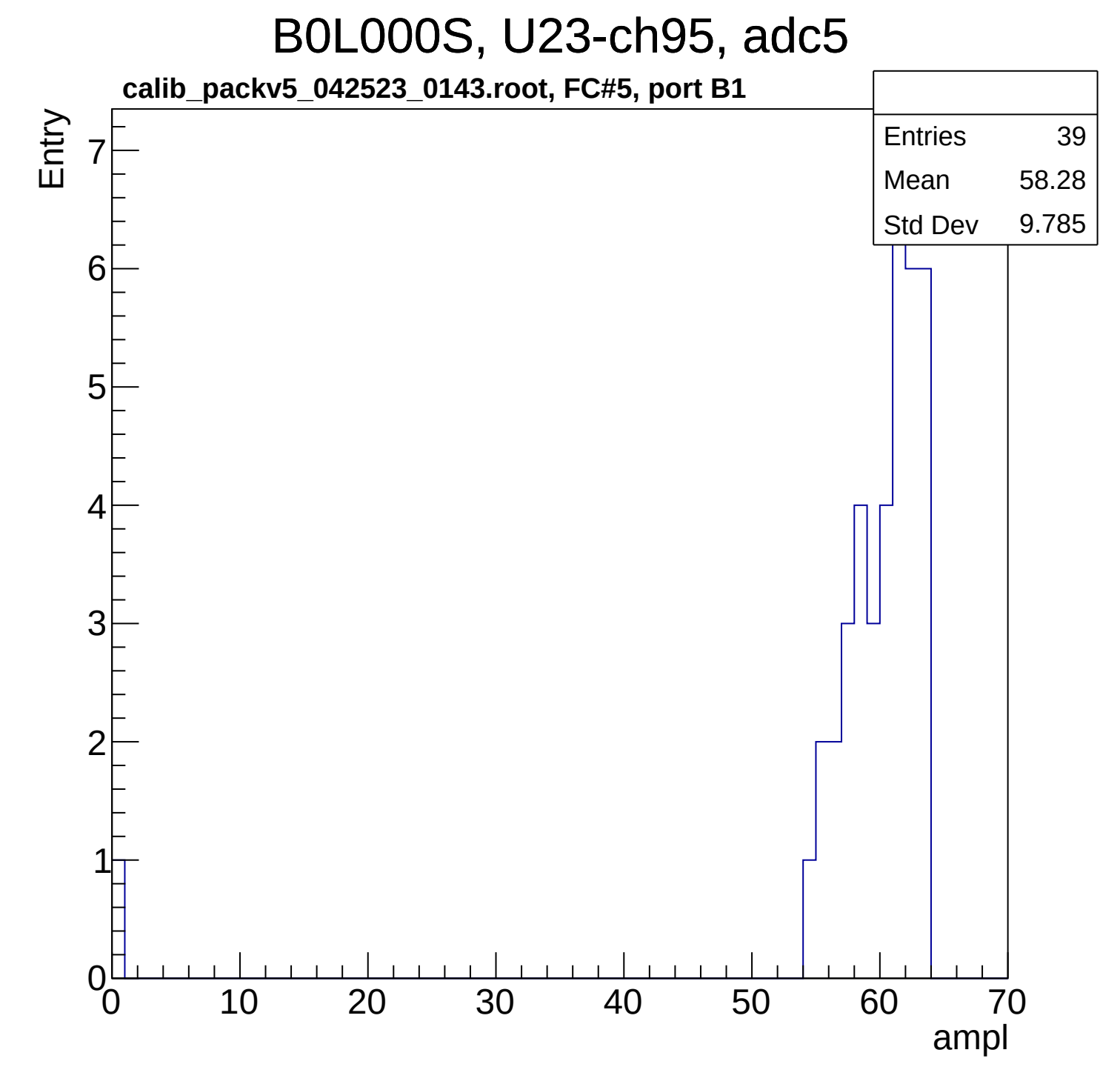

B0L000S, U23-ch95, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 29.45
Std Dev 4.676
Gaus mean : 30.7221
Gaus Width: 3.1412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

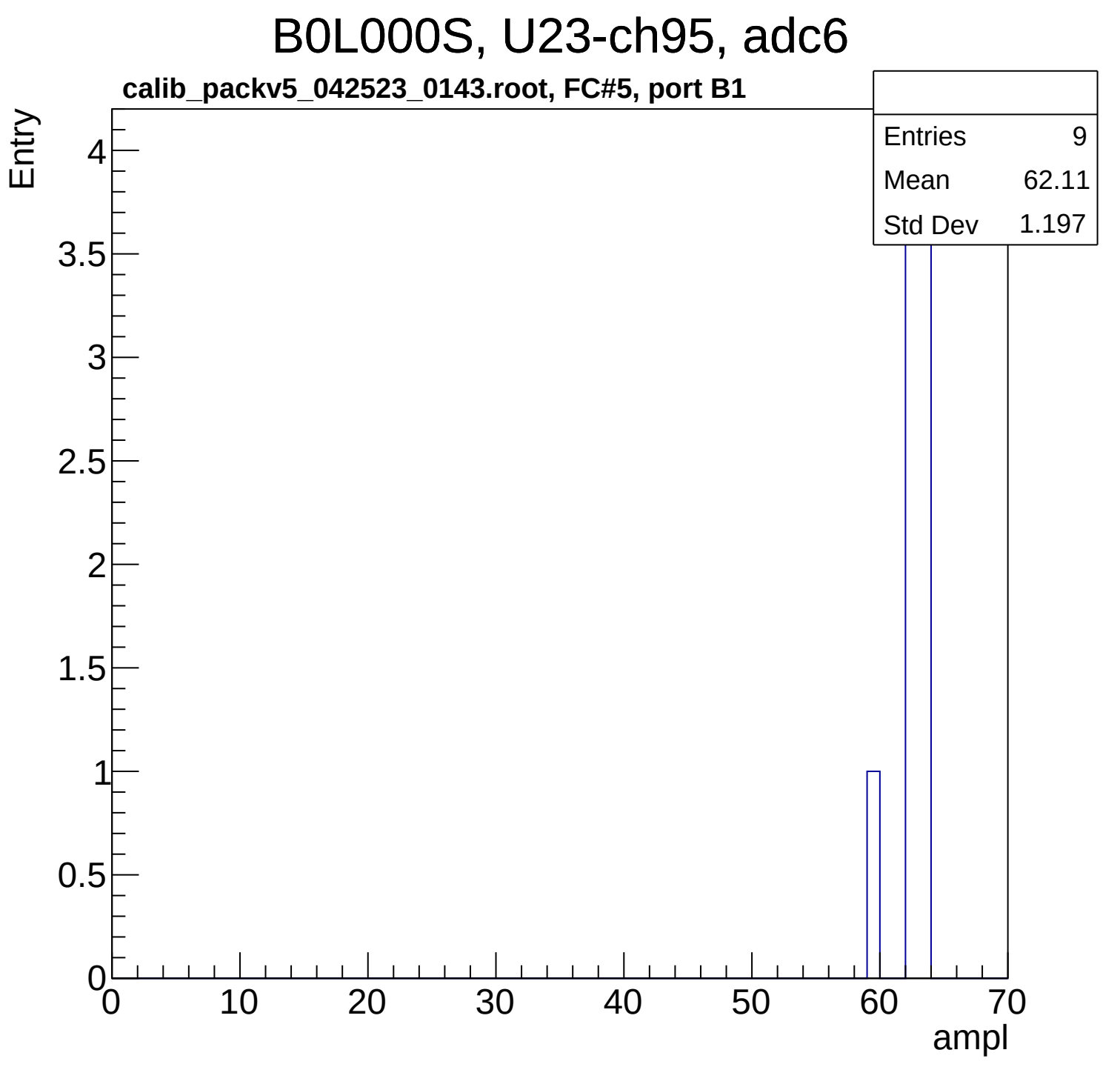

B0L000S, U23-ch95, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.54
Std Dev 2.86
Gaus mean : 36.8863
Gaus Width: 3.0512
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

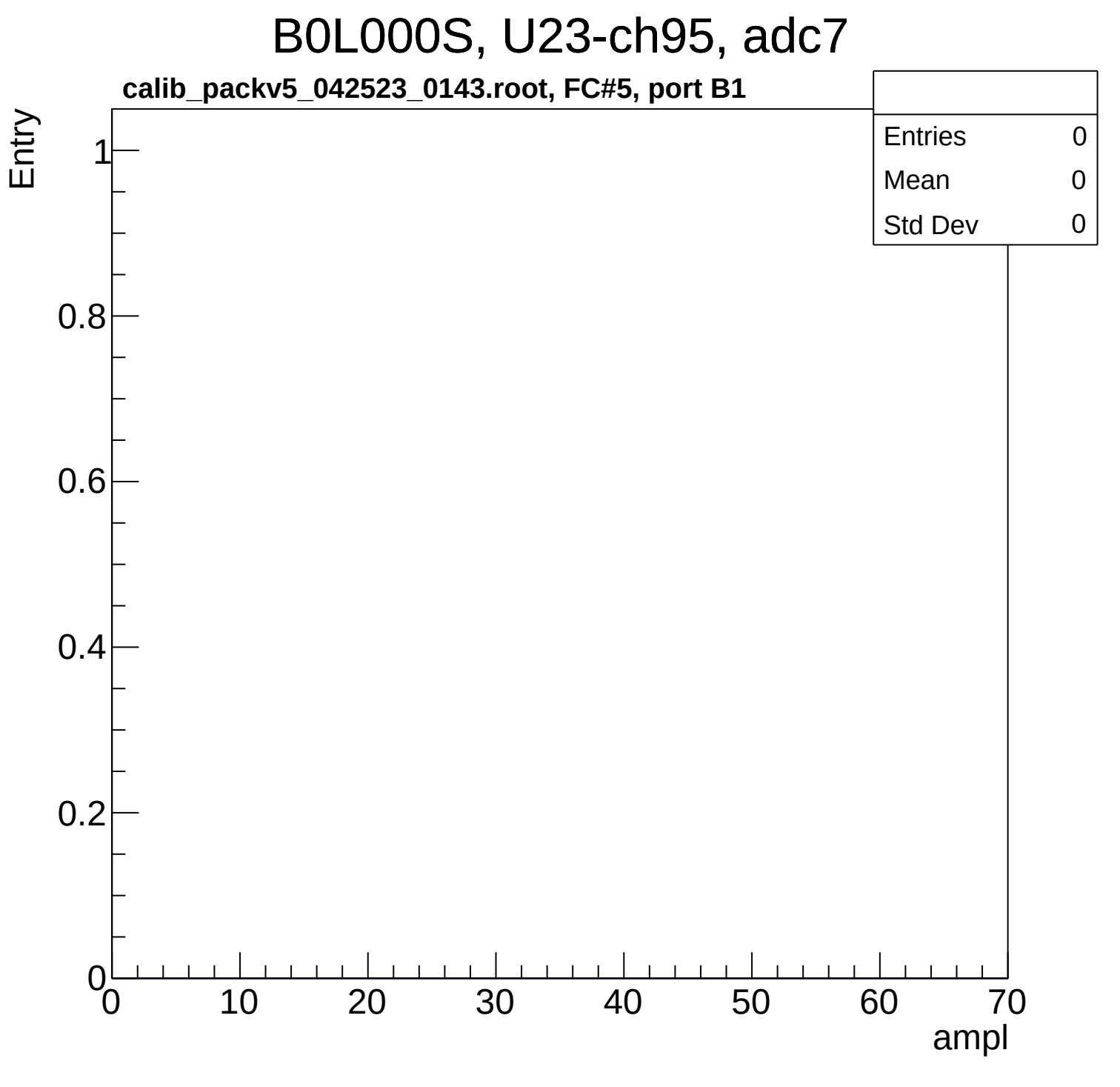

B0L000S, U23-ch95, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 43.41
Std Dev 3.278
Gaus mean : 44.1433
Gaus Width: 3.1961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

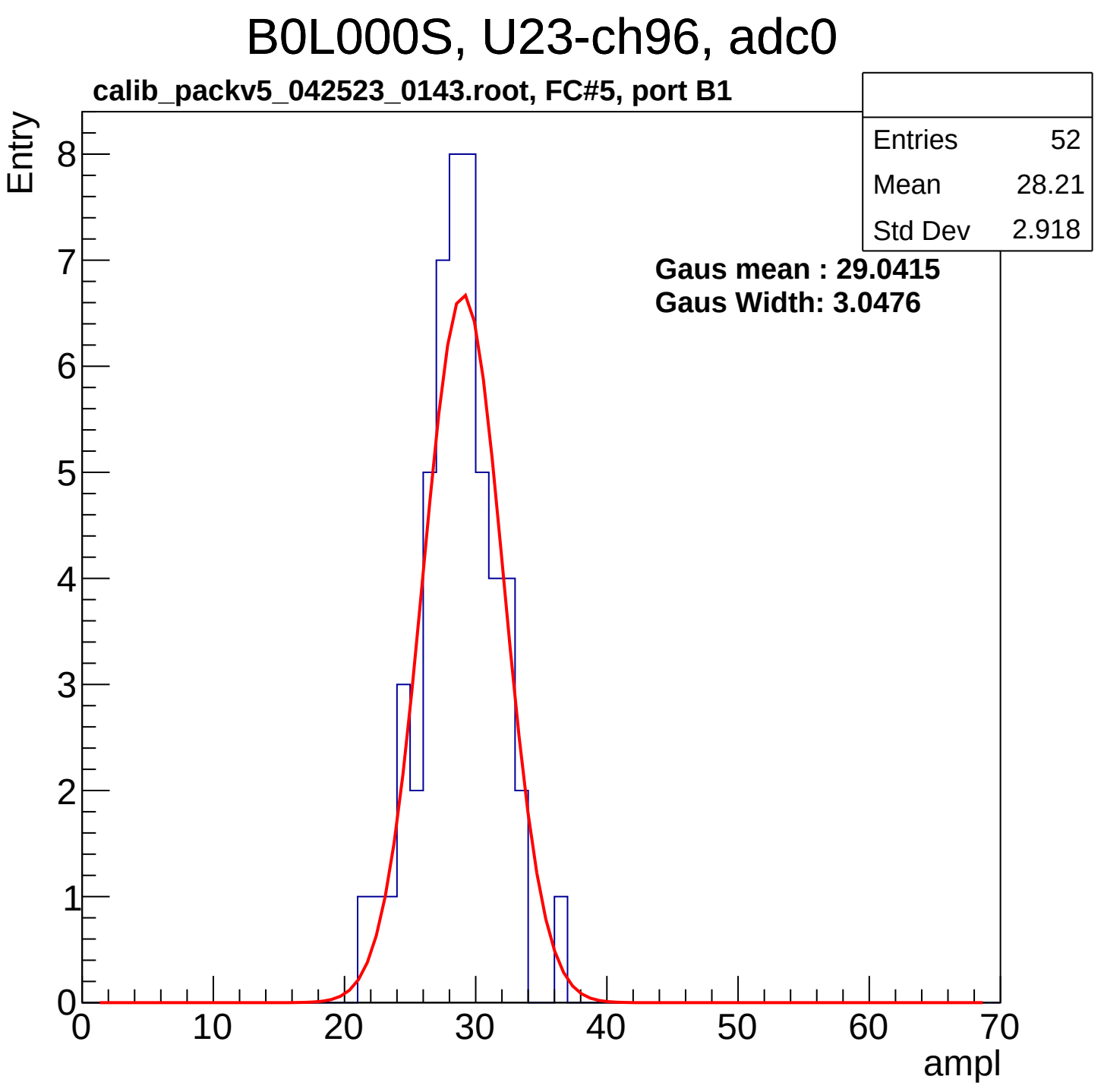

B0L000S, U23-ch95, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 50.26
Std Dev 3.172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

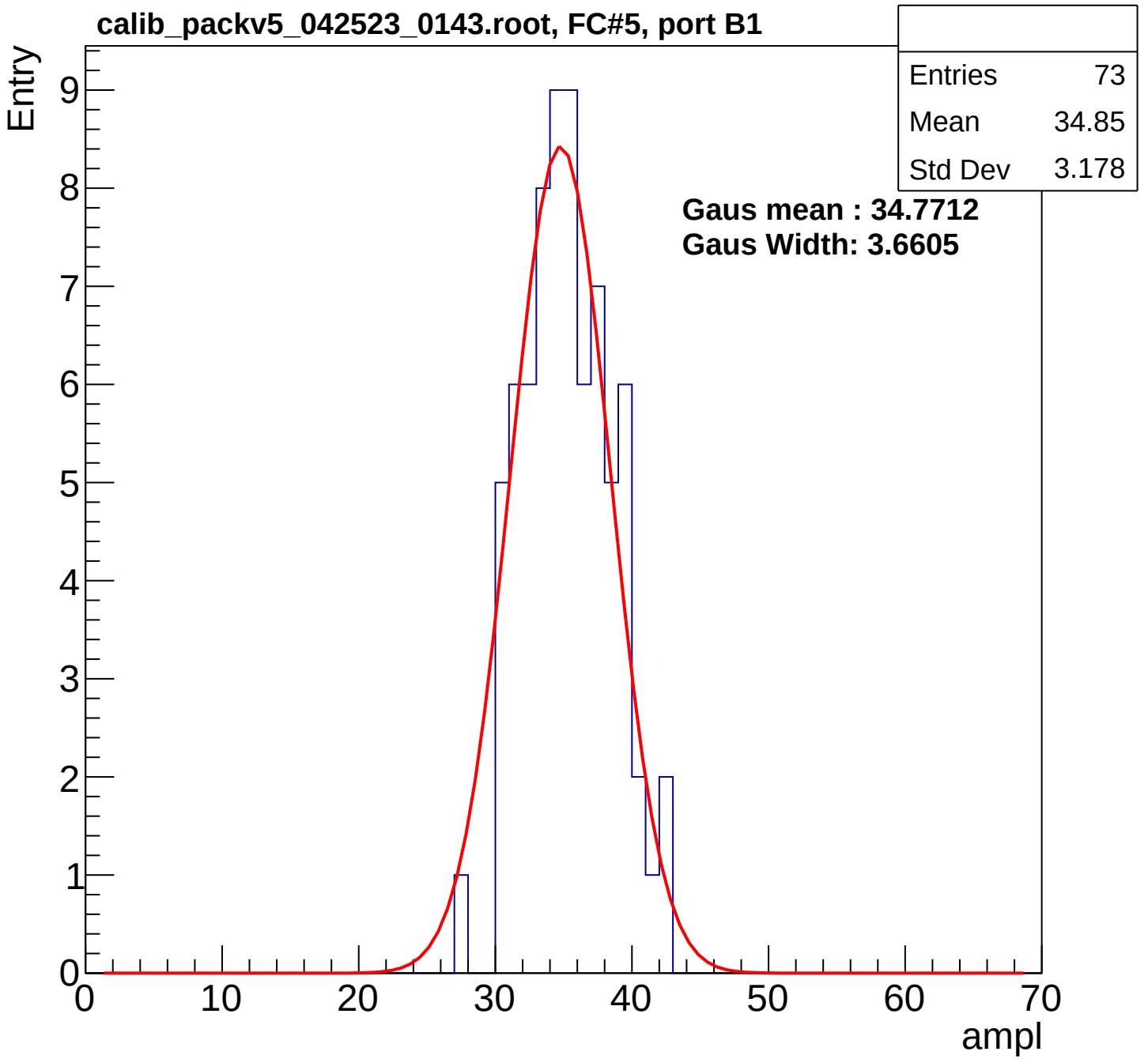

B0L000S, U23-ch95, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 55.24
Std Dev 3.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

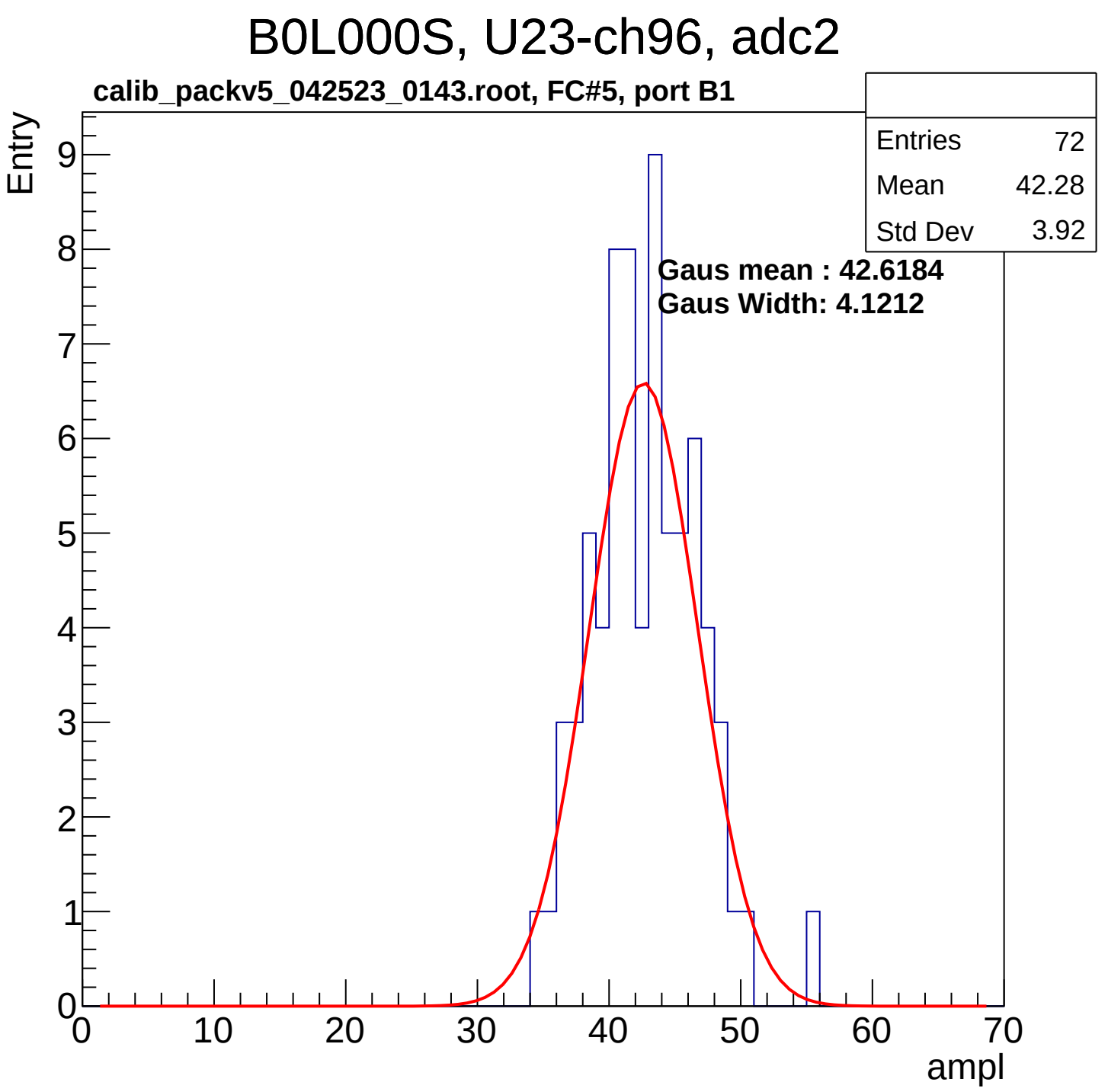

B0L000S, U23-ch95, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.28
Std Dev 9.785
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

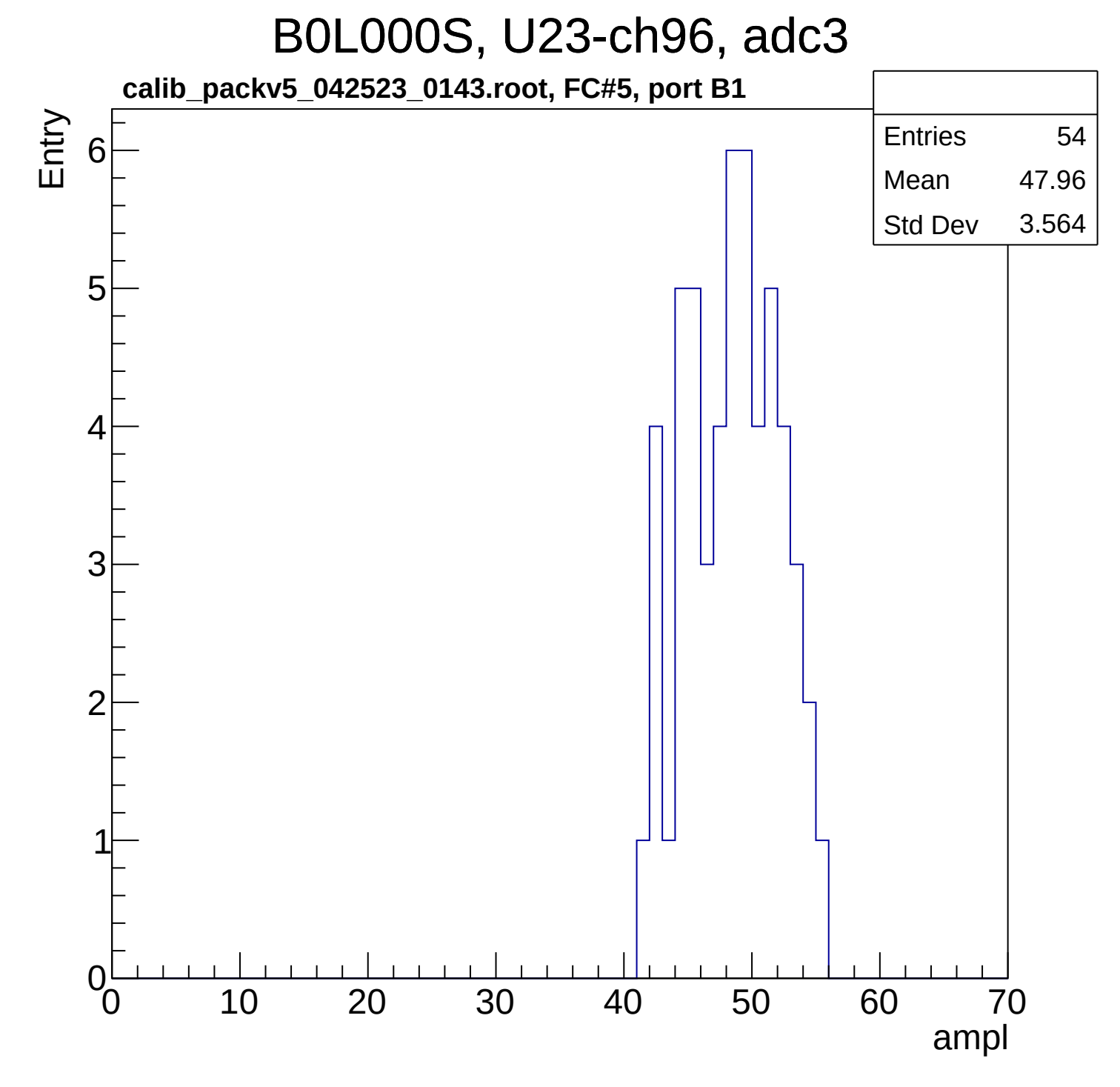

B0L000S, U23-ch95, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62.11
Std Dev 1.197
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

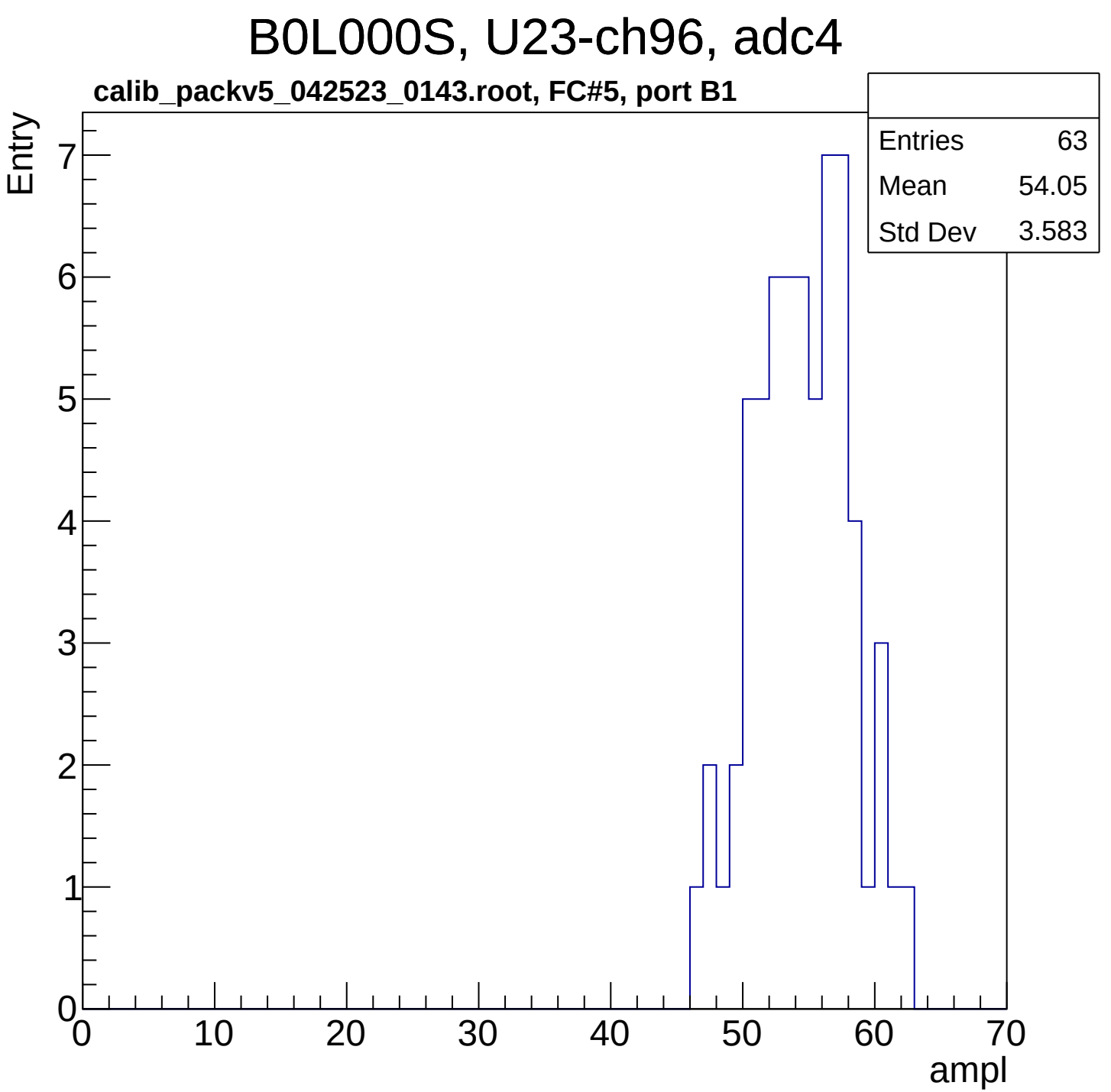

B0L000S, U23-ch95, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

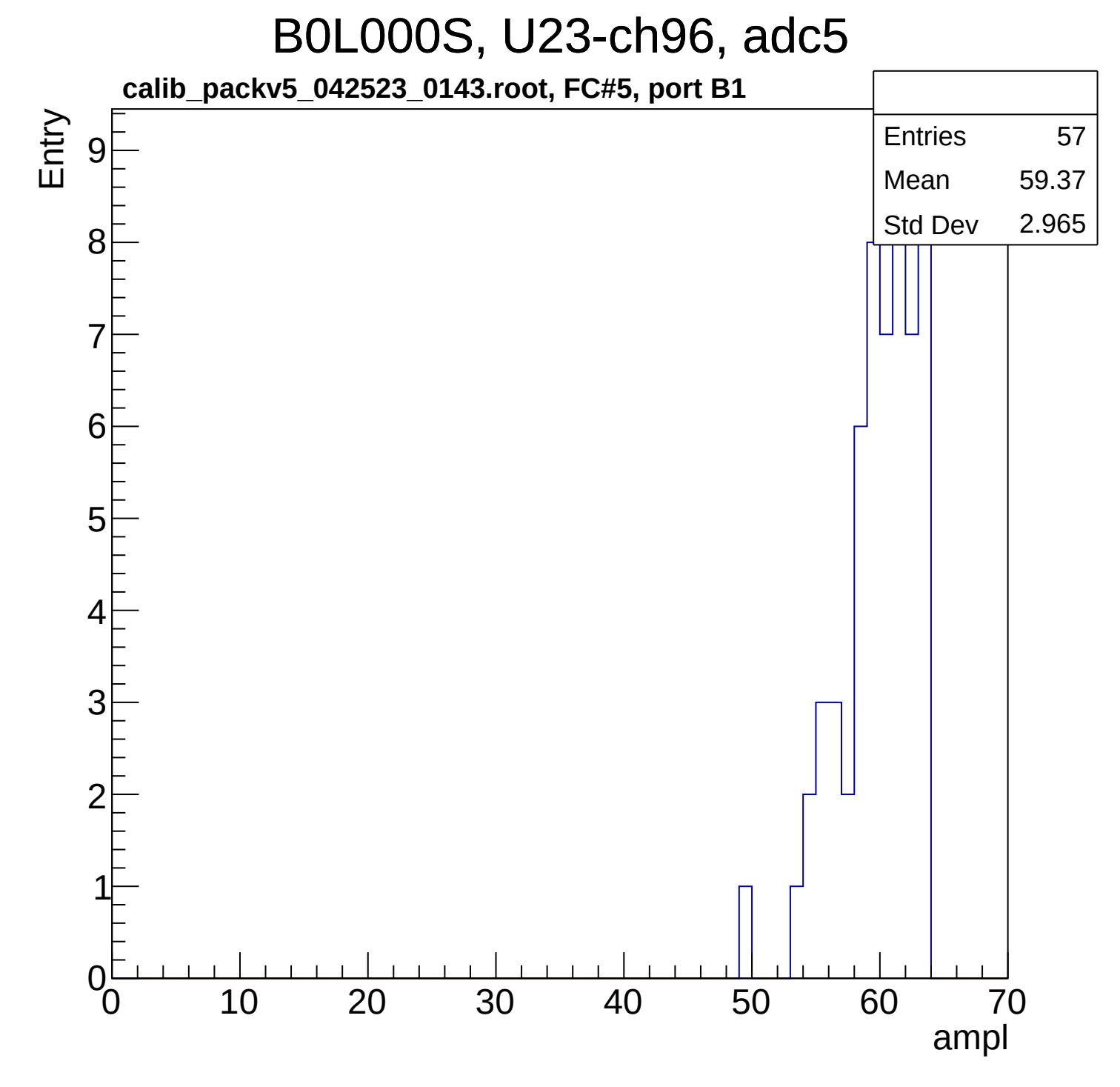

B0L000S, U23-ch96, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 28.21
Std Dev 2.918
Gaus mean : 29.0415
Gaus Width: 3.0476
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

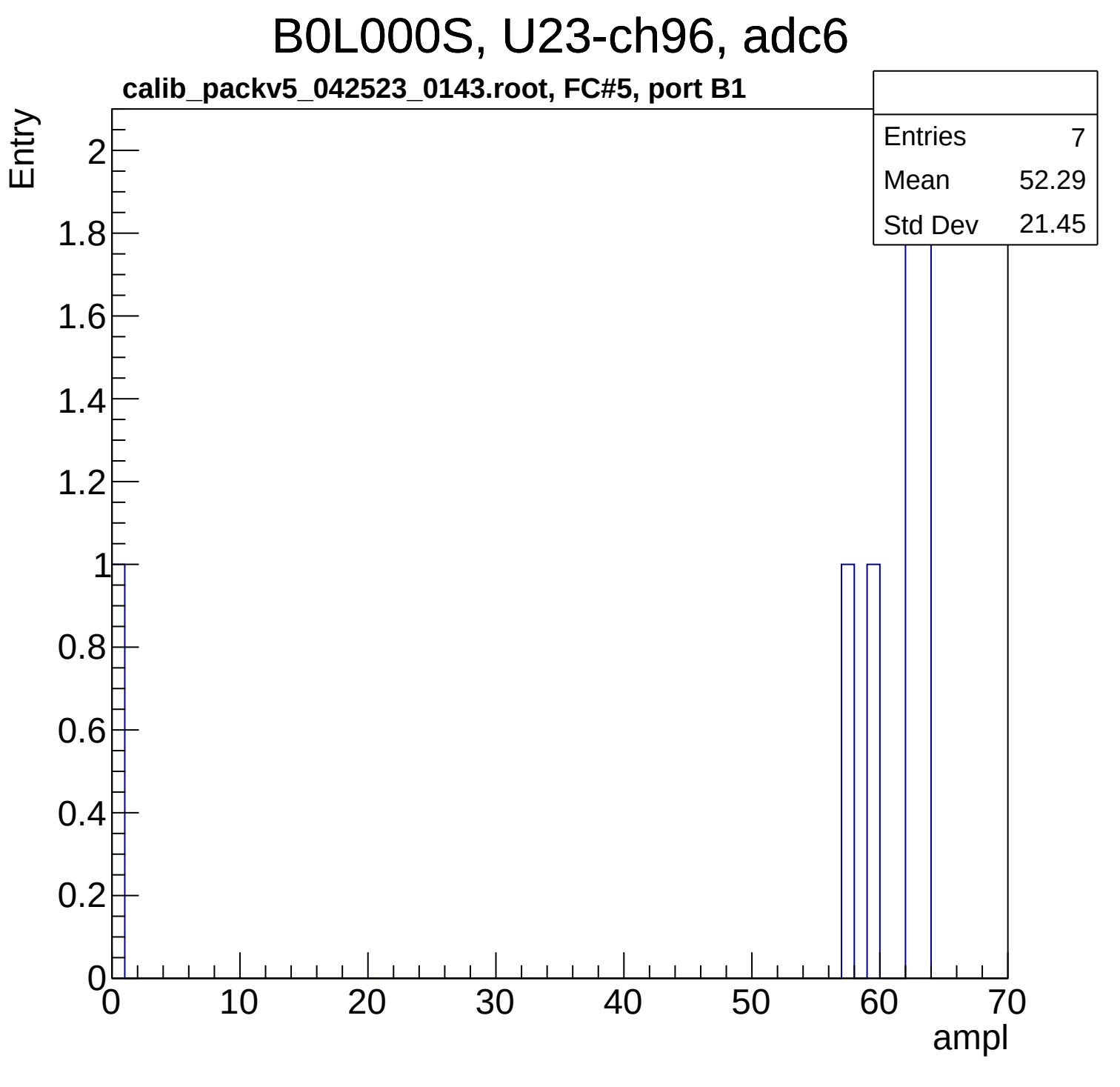

B0L000S, U23-ch96, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 34.85
Std Dev 3.178
Gaus mean : 34.7712
Gaus Width: 3.6605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

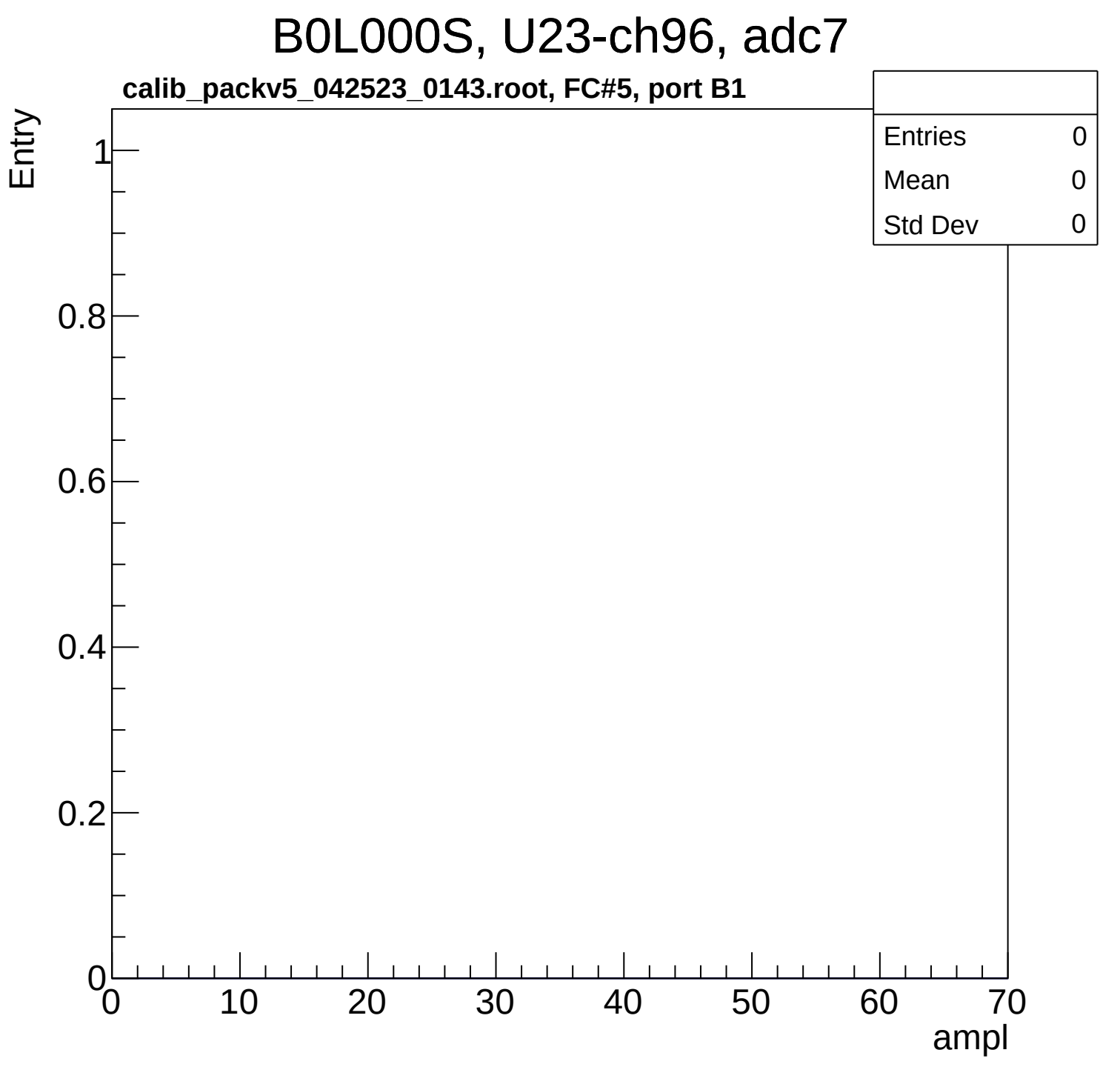

B0L000S, U23-ch96, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 42.28
Std Dev 3.92
Gaus mean : 42.6184
Gaus Width: 4.1212
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

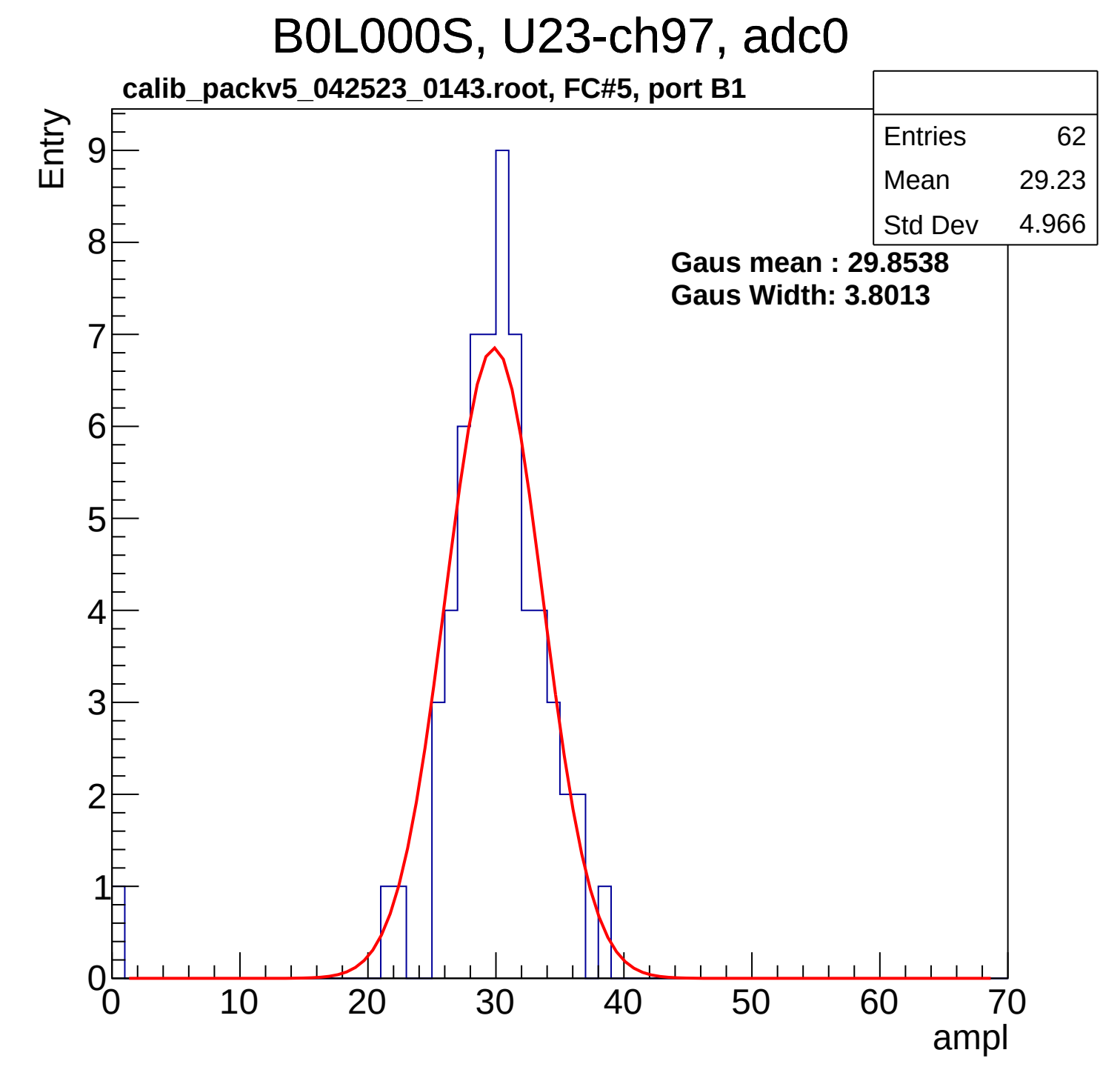

B0L000S, U23-ch96, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 47.96
Std Dev 3.564
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

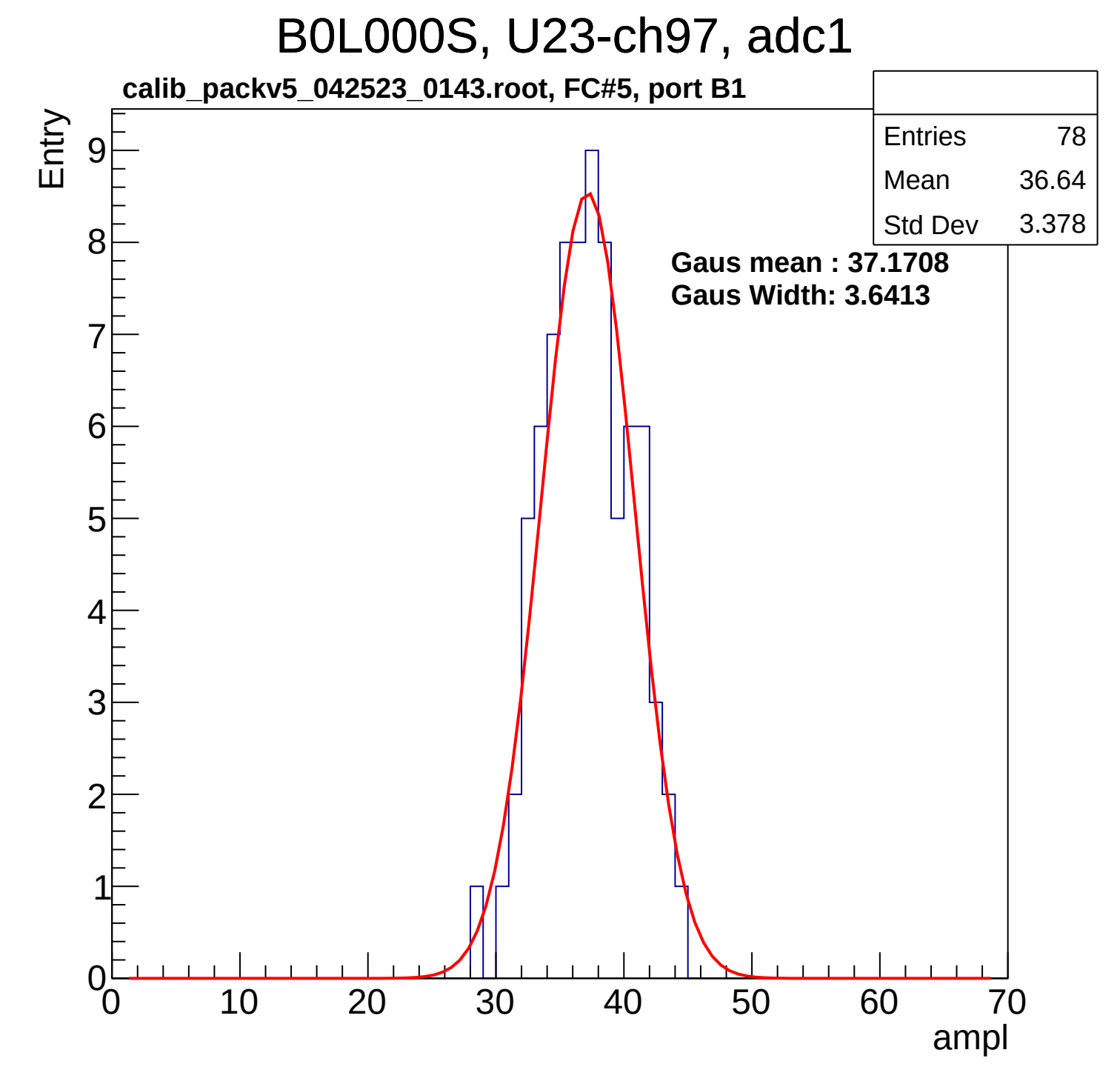

B0L000S, U23-ch96, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.05
Std Dev 3.583
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

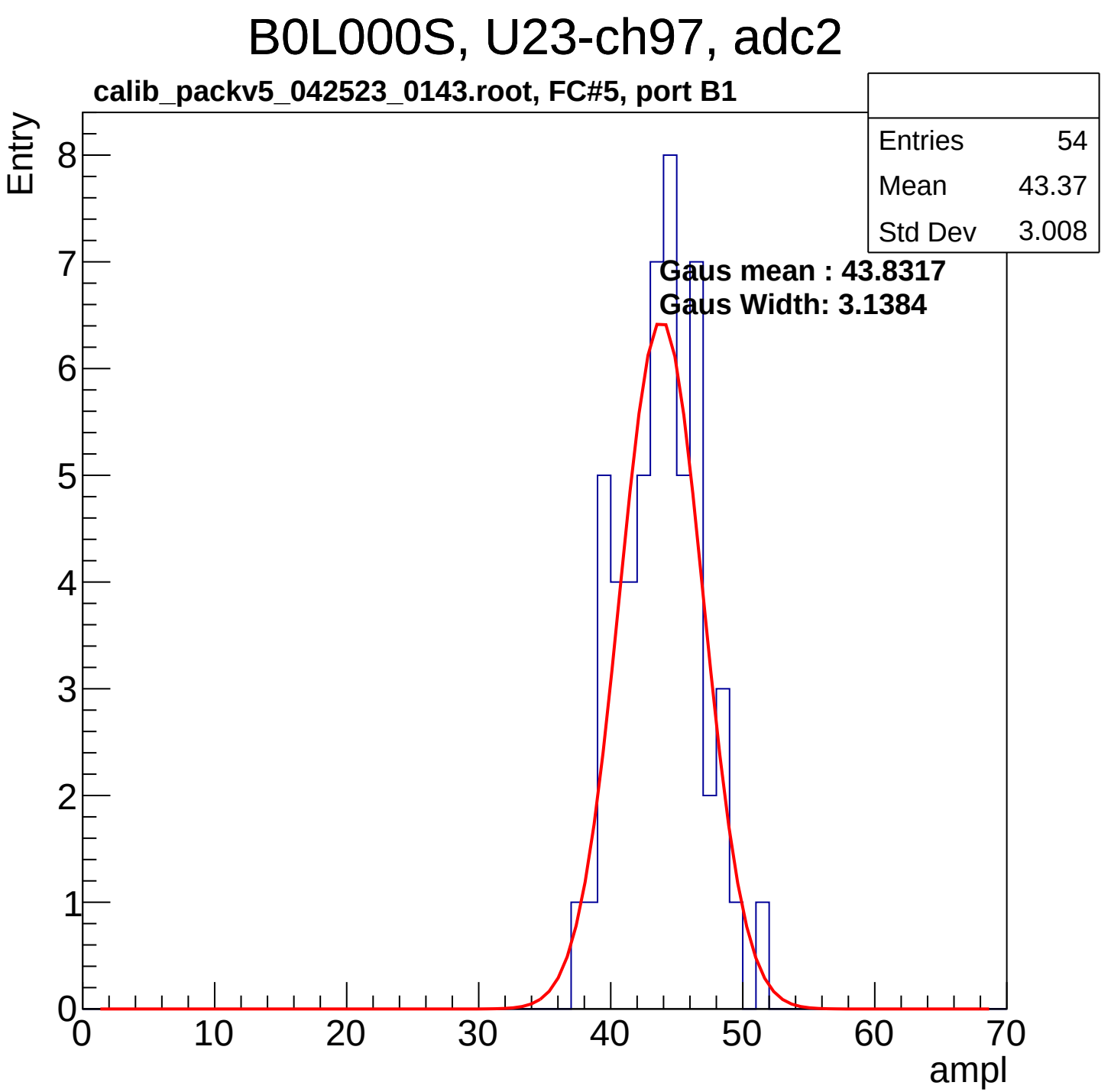

B0L000S, U23-ch96, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 59.37
Std Dev 2.965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

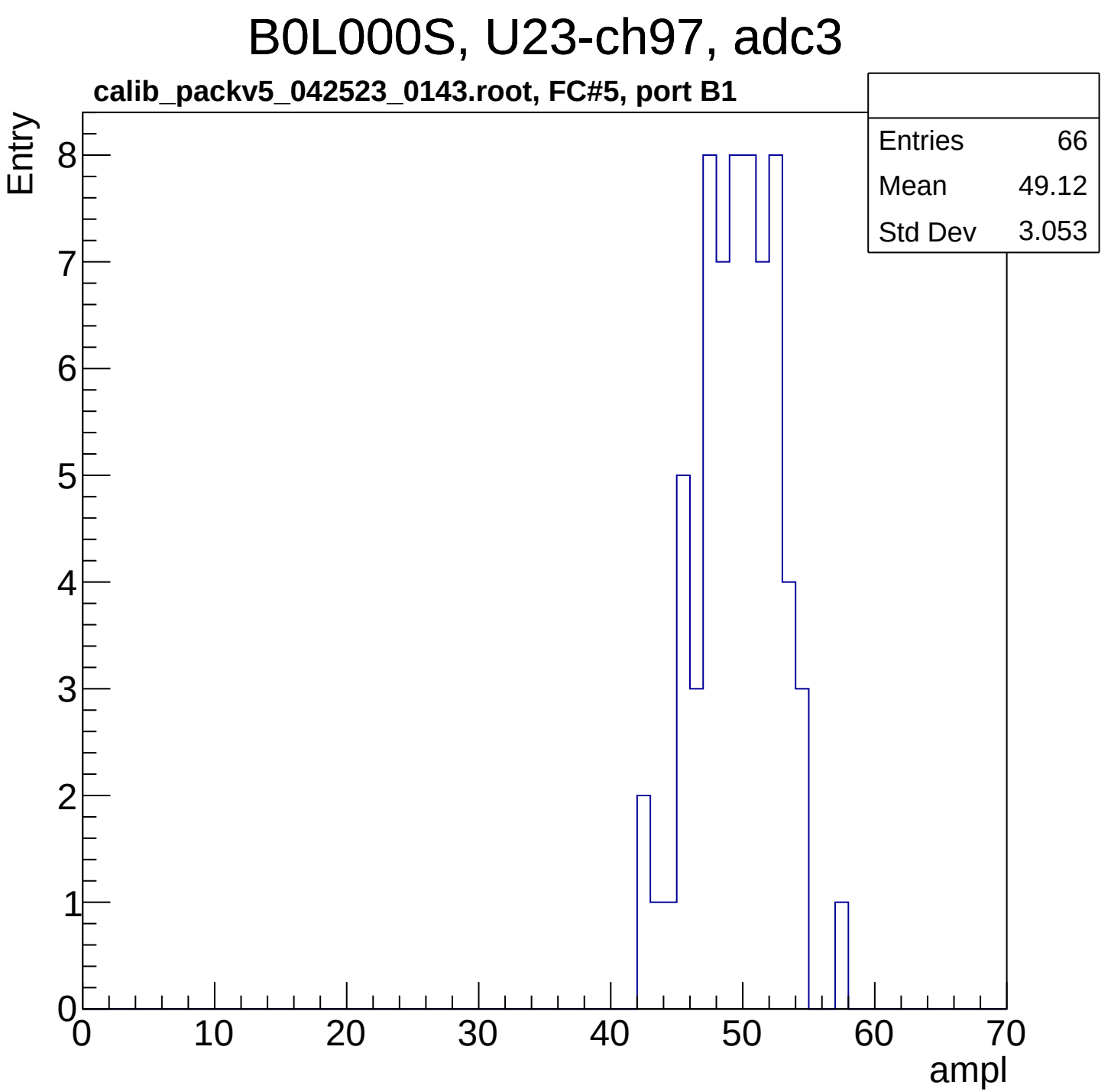

B0L000S, U23-ch96, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 52.29
Std Dev 21.45
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

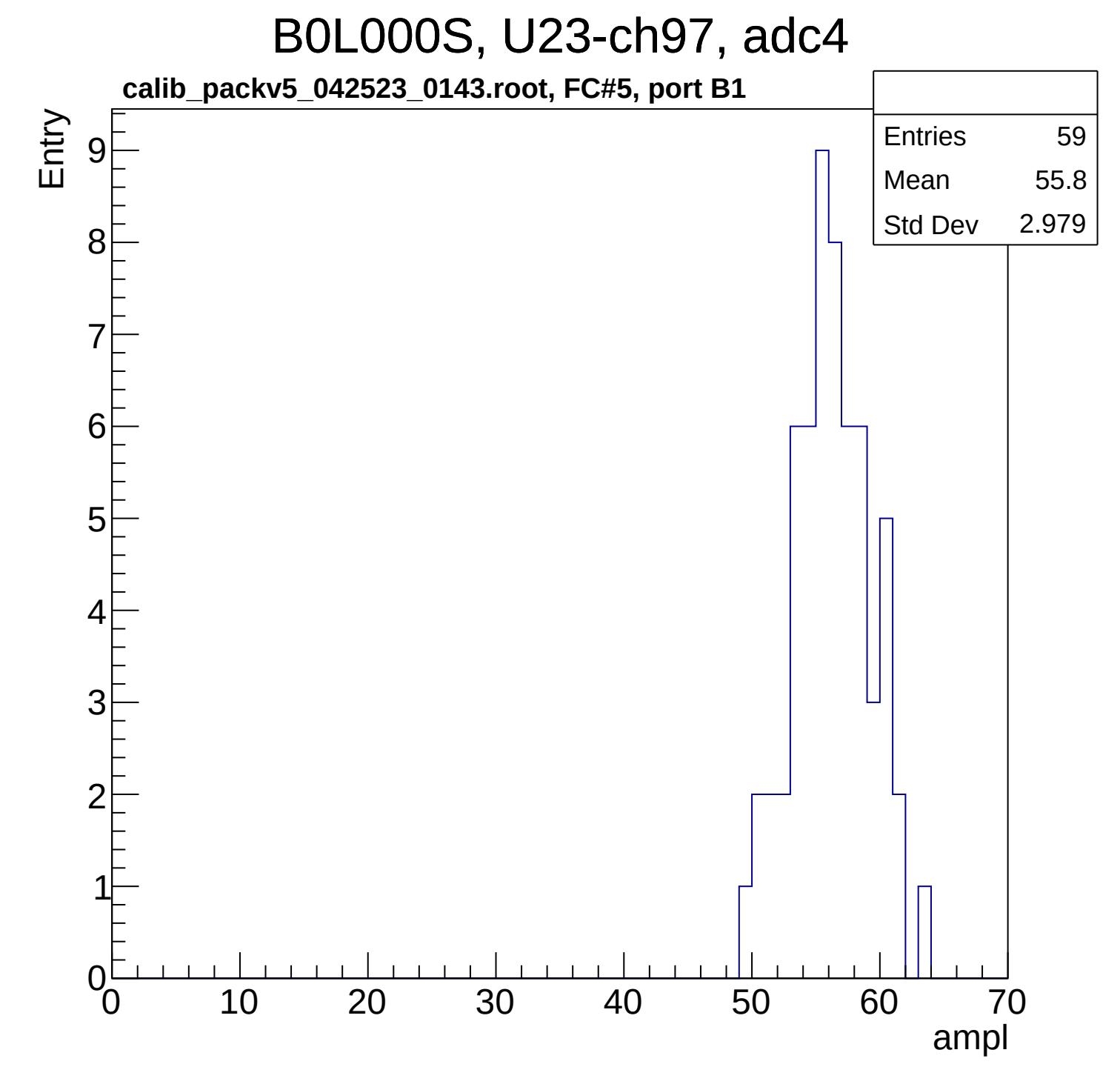

B0L000S, U23-ch96, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

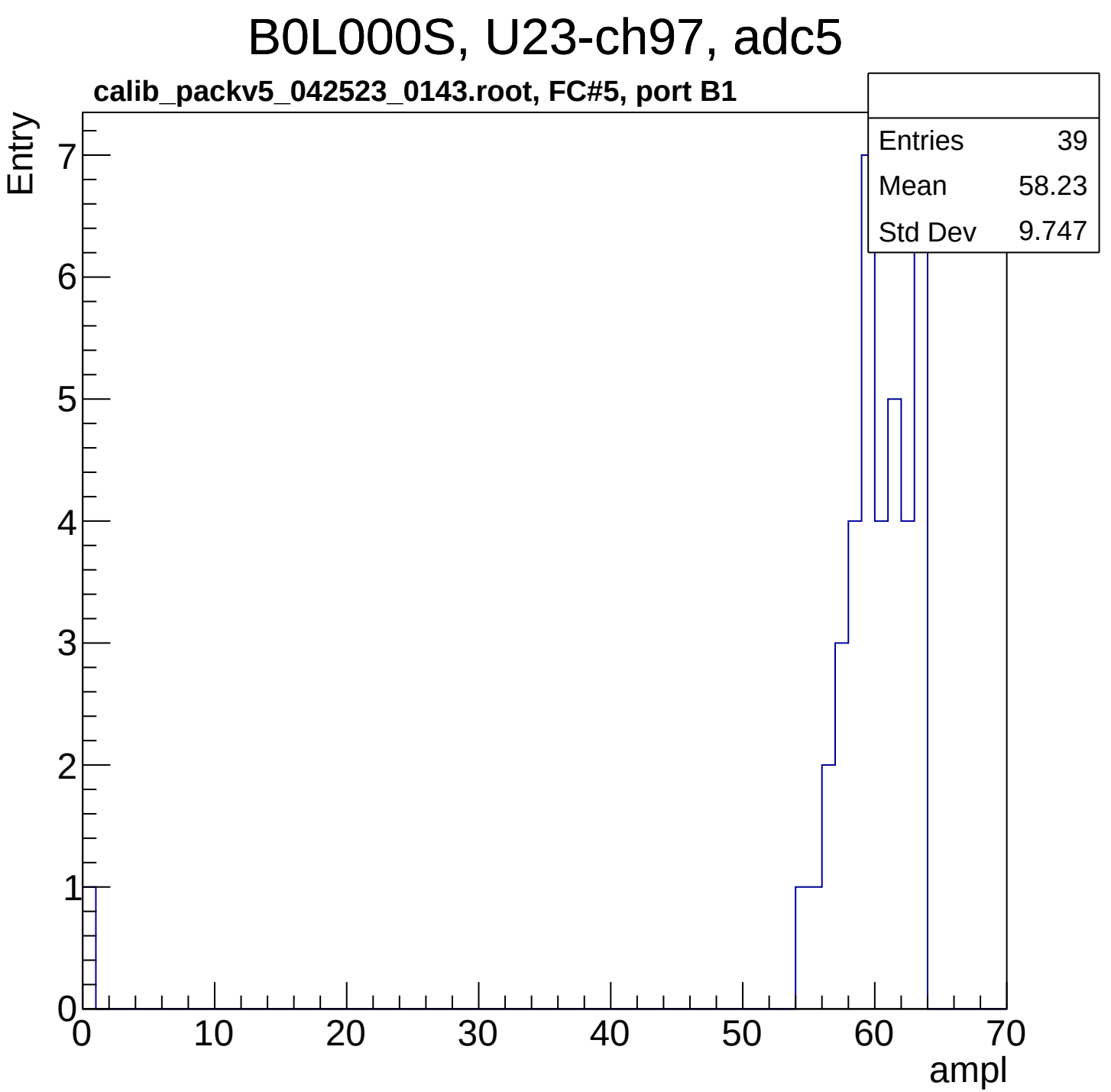

B0L000S, U23-ch97, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.23
Std Dev 4.966
Gaus mean : 29.8538
Gaus Width: 3.8013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

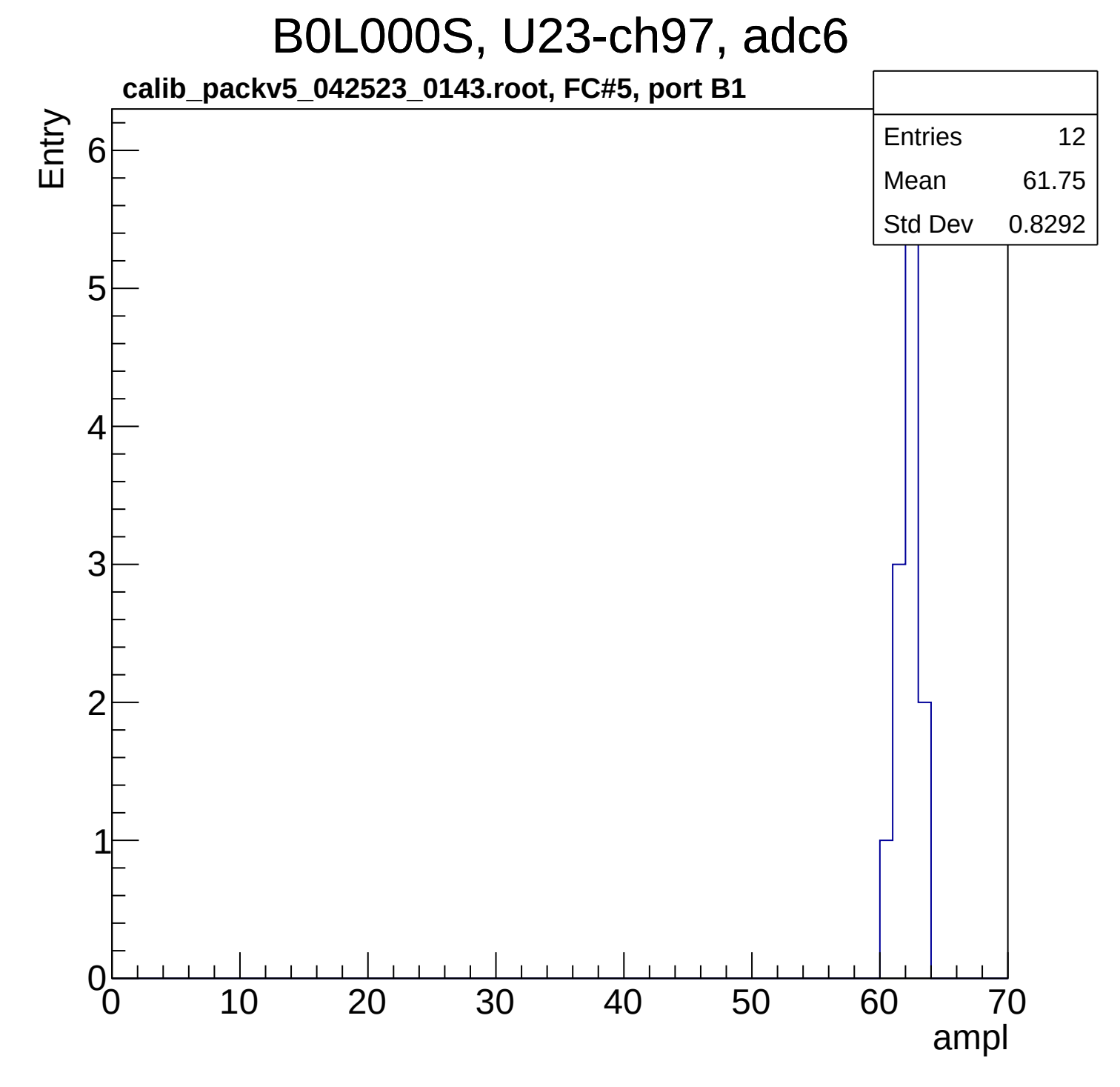

B0L000S, U23-ch97, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 36.64
Std Dev 3.378
Gaus mean : 37.1708
Gaus Width: 3.6413
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

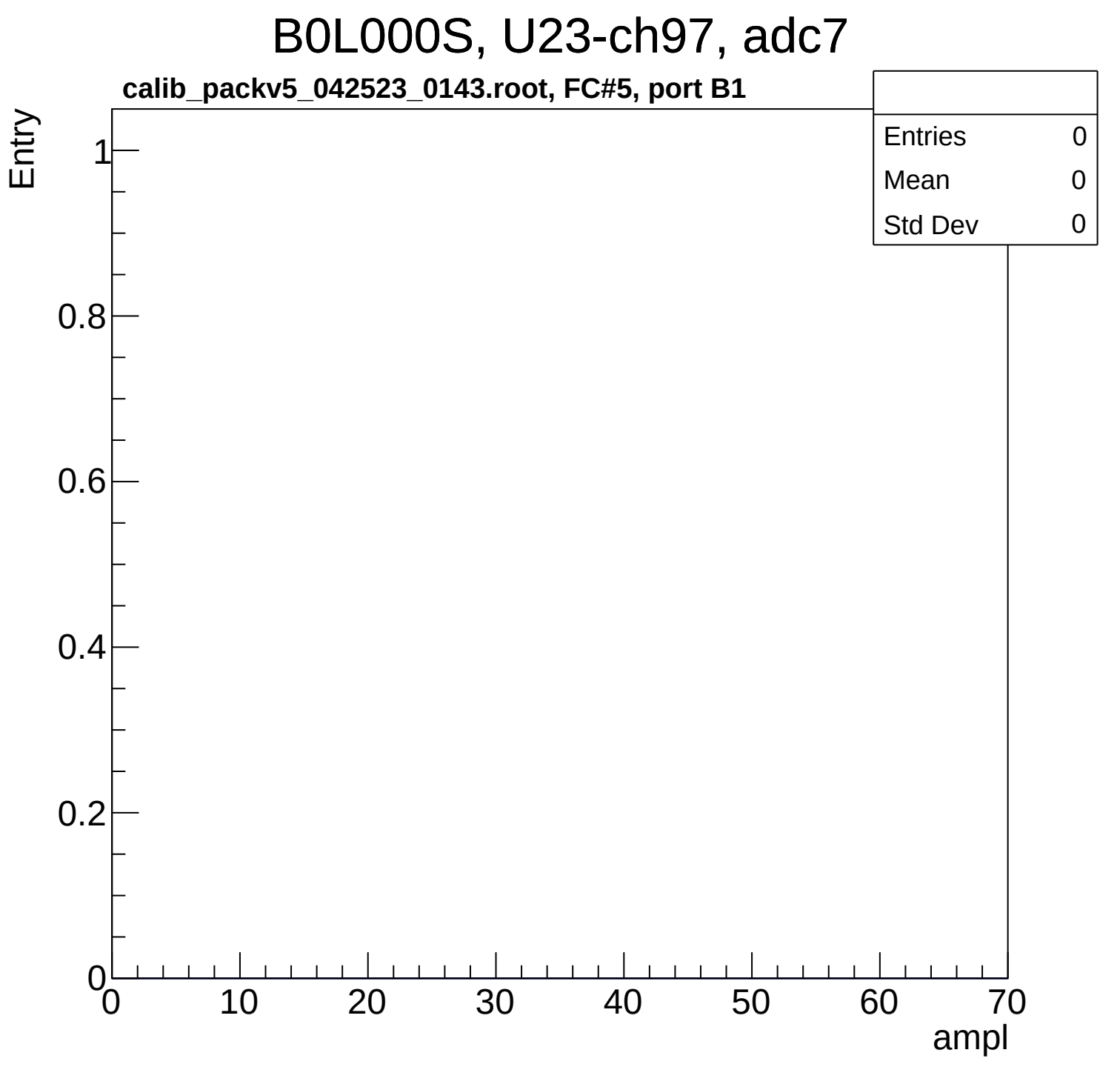

B0L000S, U23-ch97, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 43.37
Std Dev 3.008
Gaus mean : 43.8317
Gaus Width: 3.1384
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

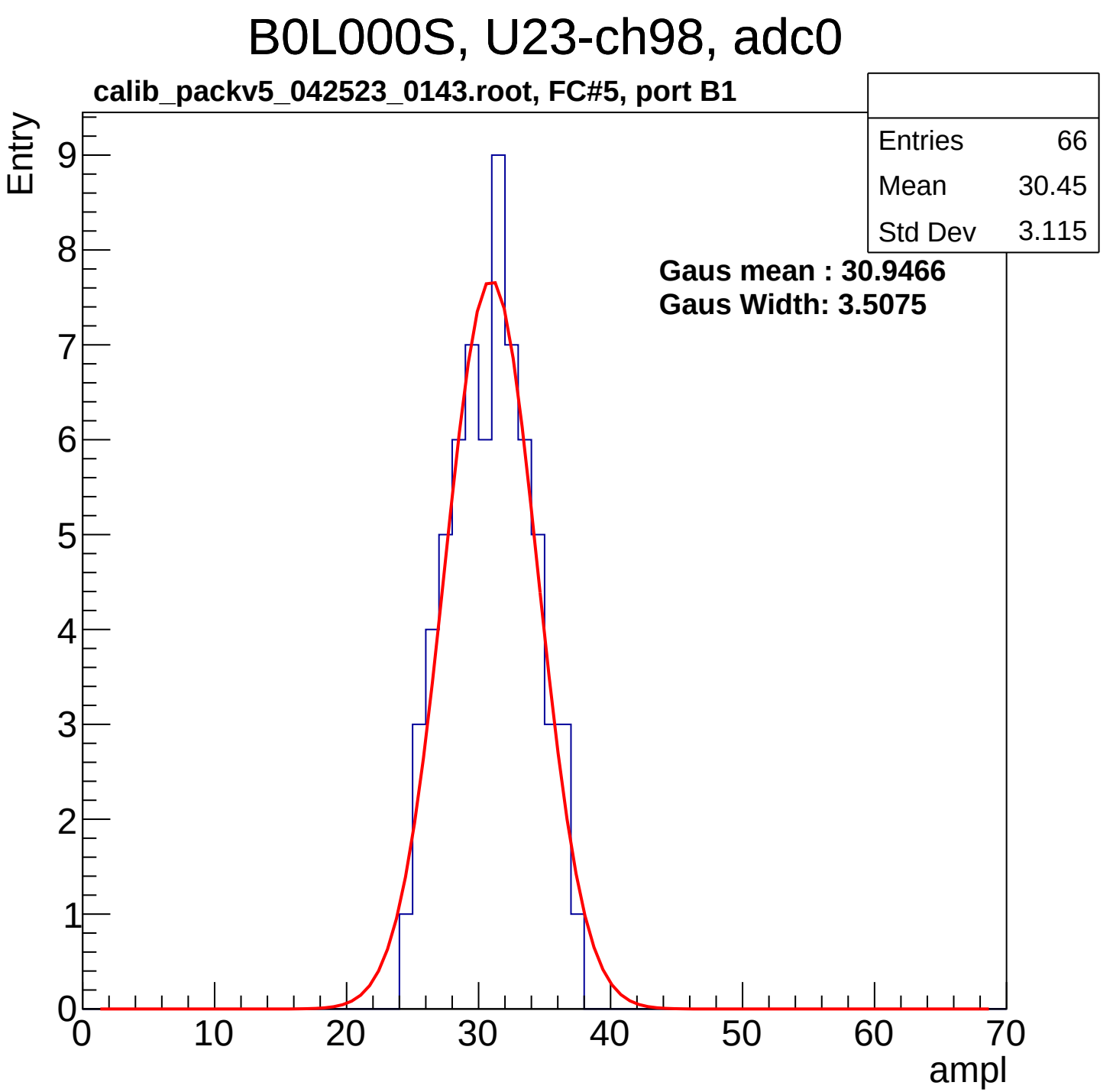

B0L000S, U23-ch97, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.12
Std Dev 3.053
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

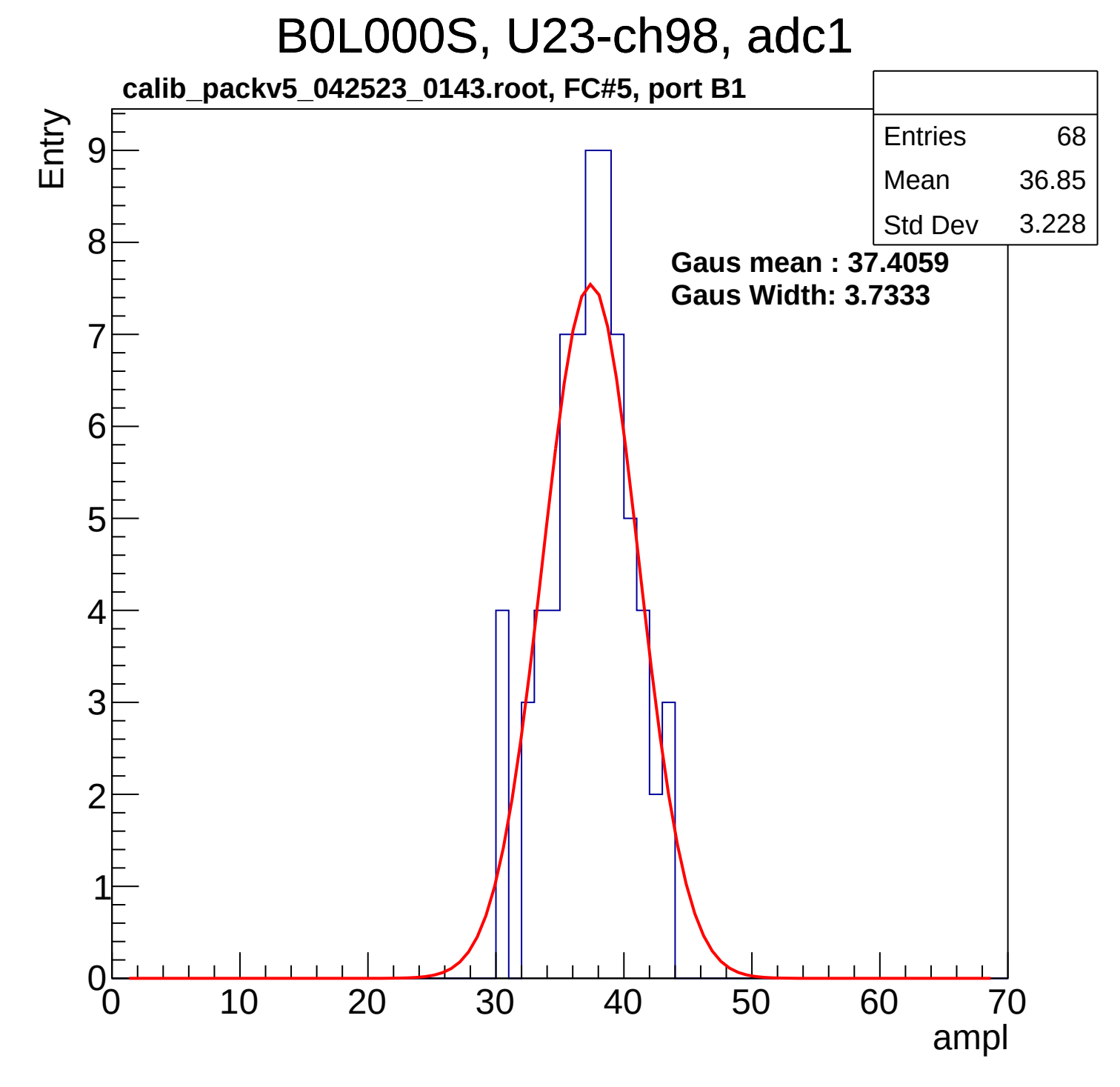

B0L000S, U23-ch97, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.8
Std Dev 2.979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

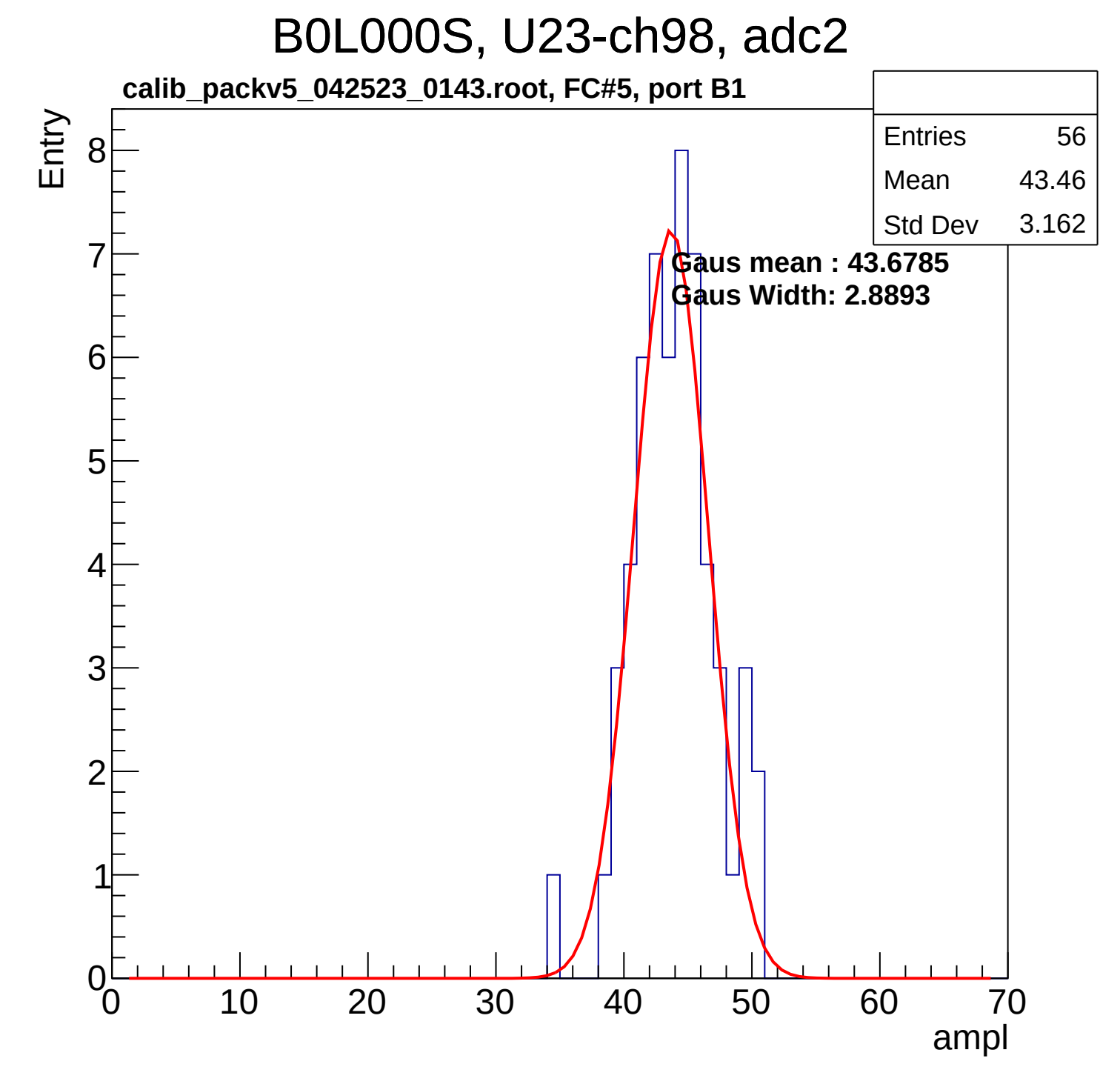

B0L000S, U23-ch97, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.23
Std Dev 9.747
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

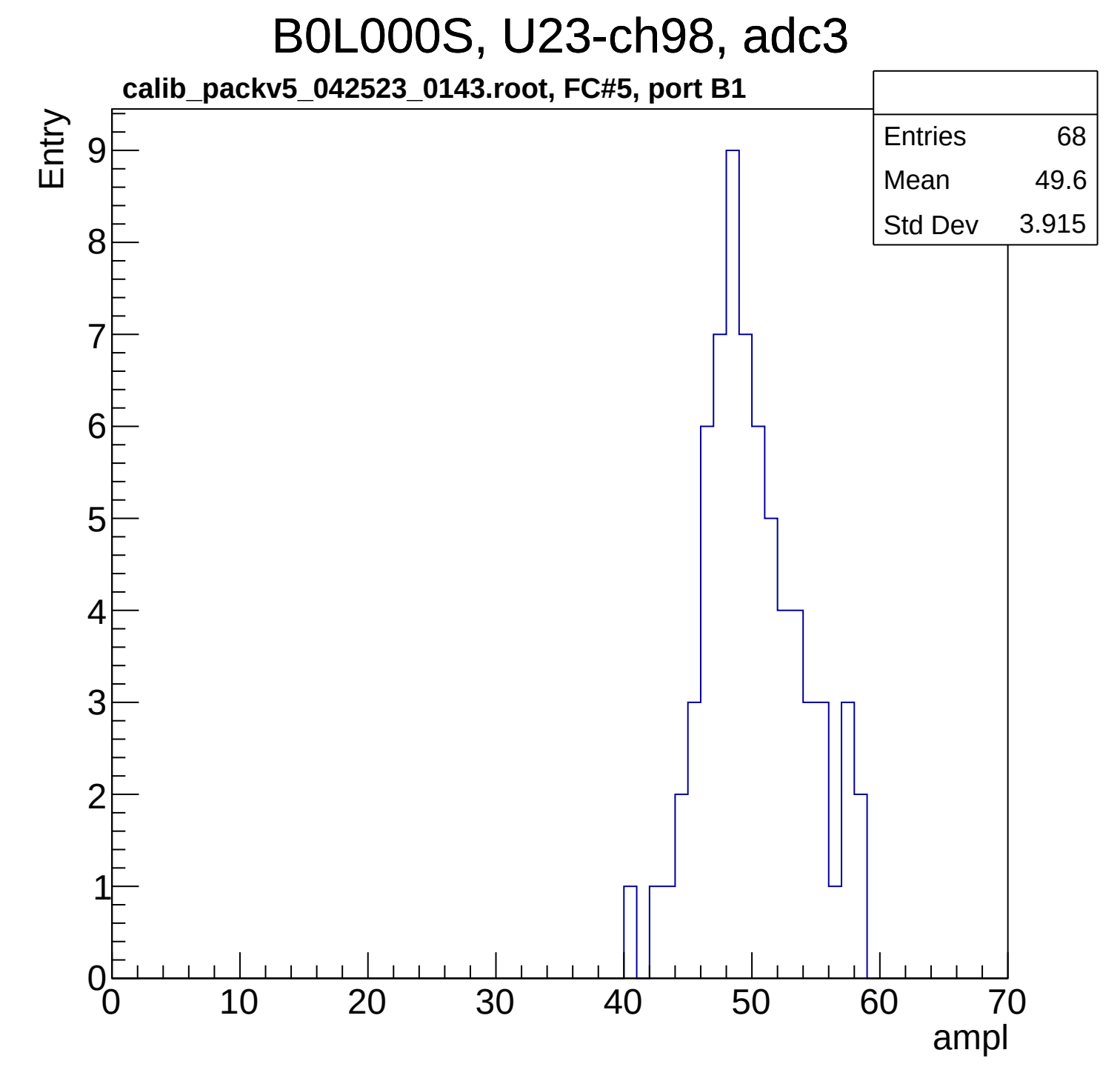

B0L000S, U23-ch97, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.75
Std Dev 0.8292
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

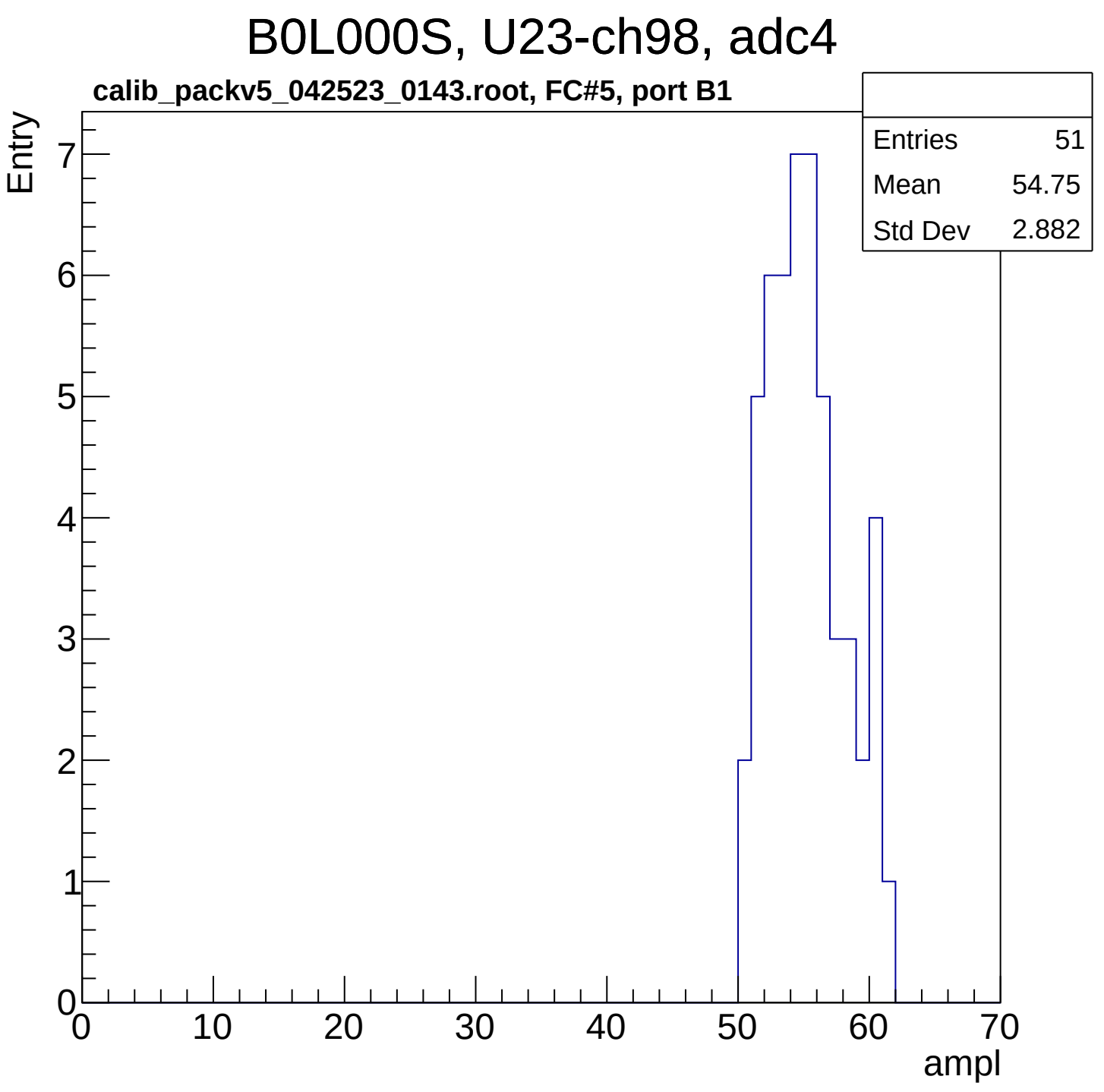

B0L000S, U23-ch97, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

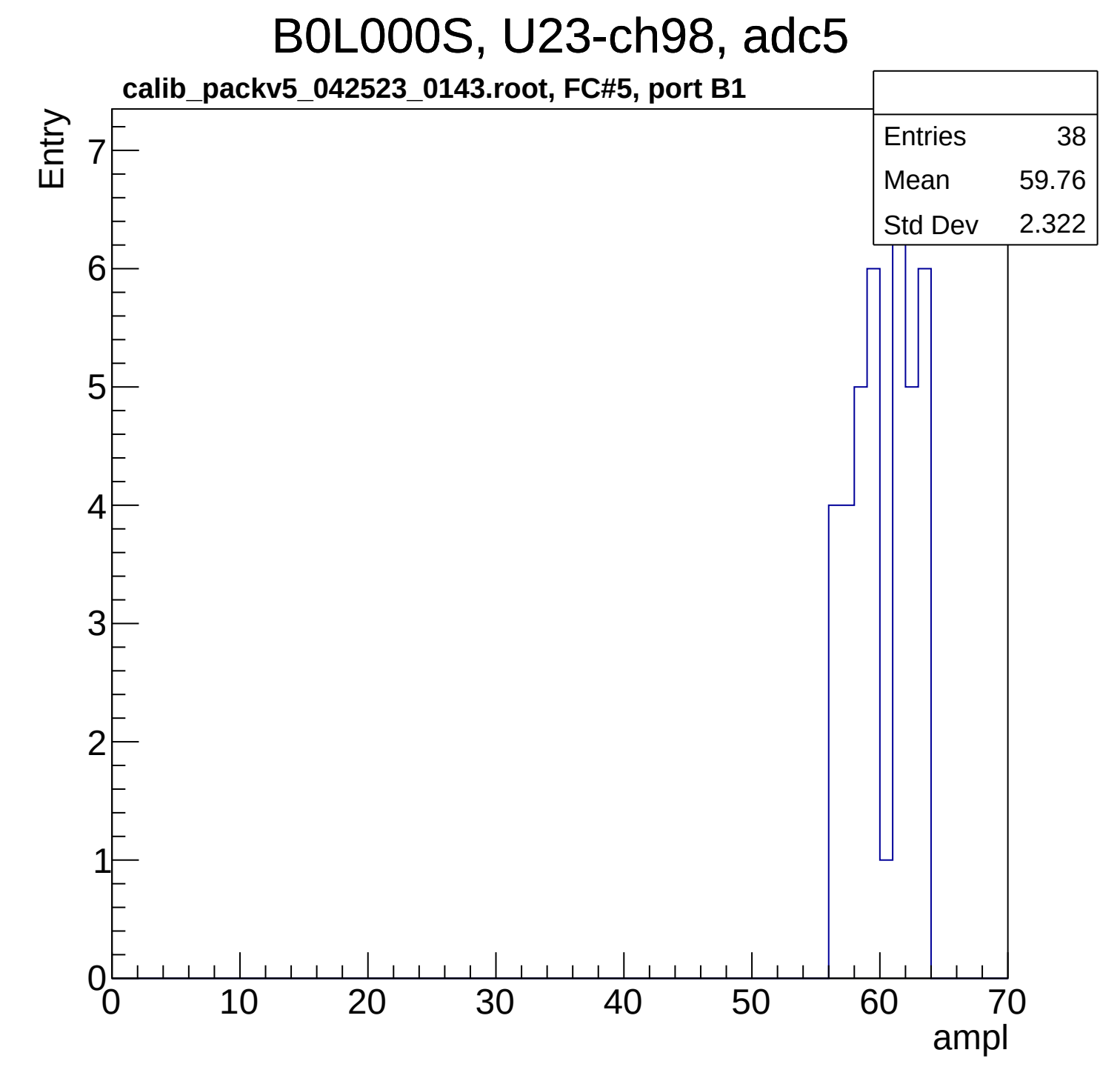

B0L000S, U23-ch98, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 30.45
Std Dev 3.115
Gaus mean : 30.9466
Gaus Width: 3.5075
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

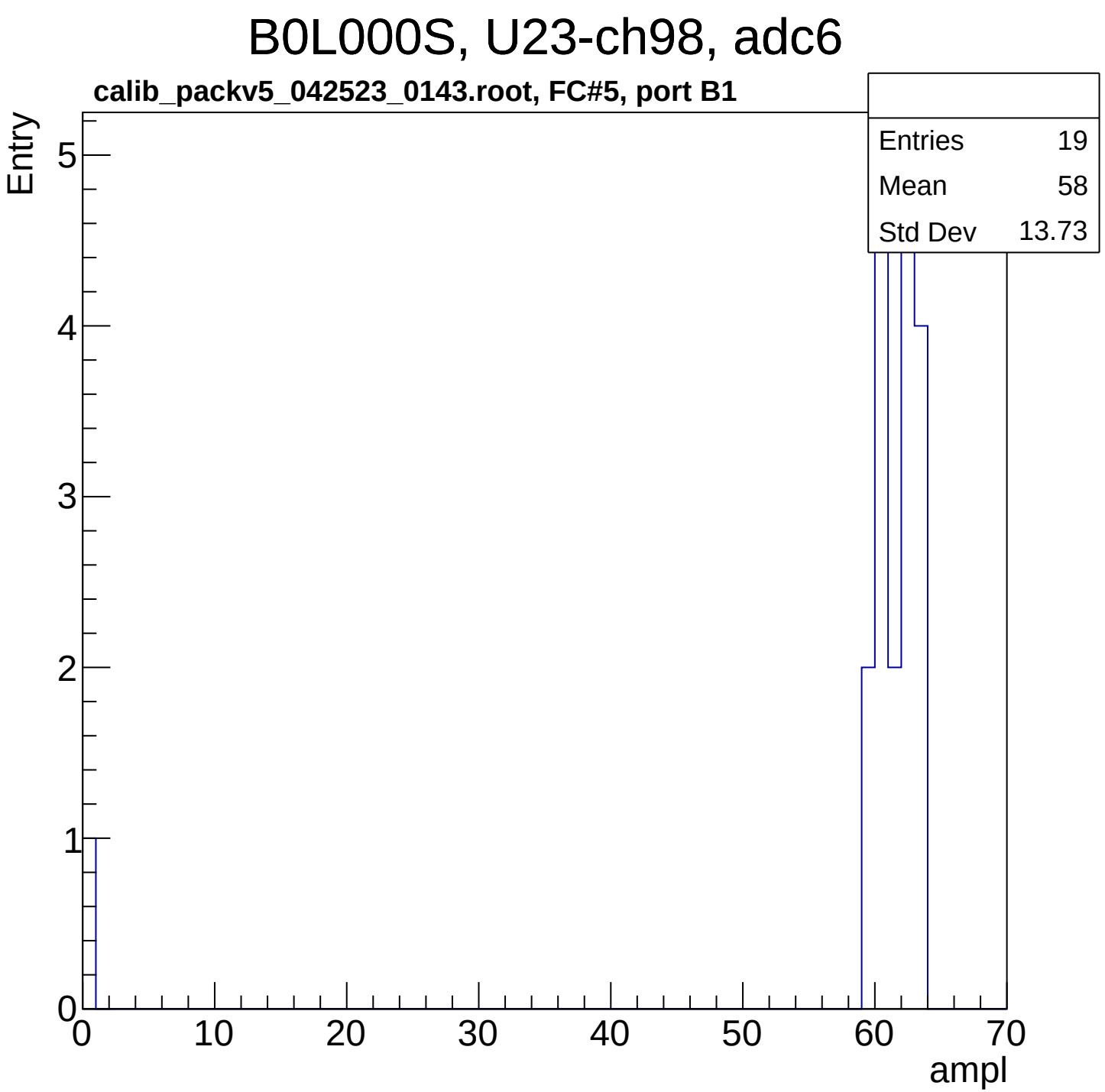

B0L000S, U23-ch98, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.85
Std Dev 3.228
Gaus mean : 37.4059
Gaus Width: 3.7333
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

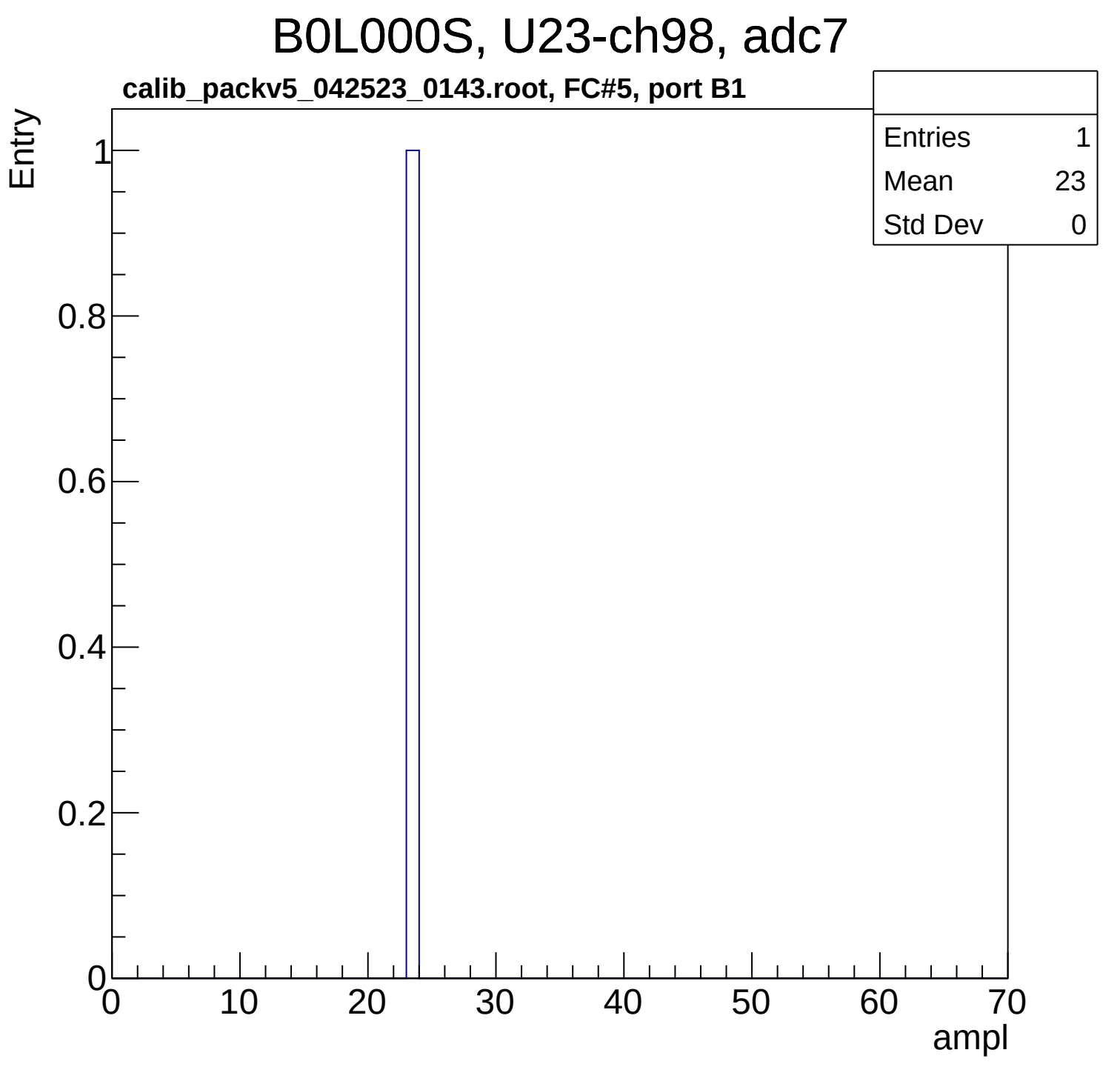

B0L000S, U23-ch98, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.46
Std Dev 3.162
Gaus mean : 43.6785
Gaus Width: 2.8893
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

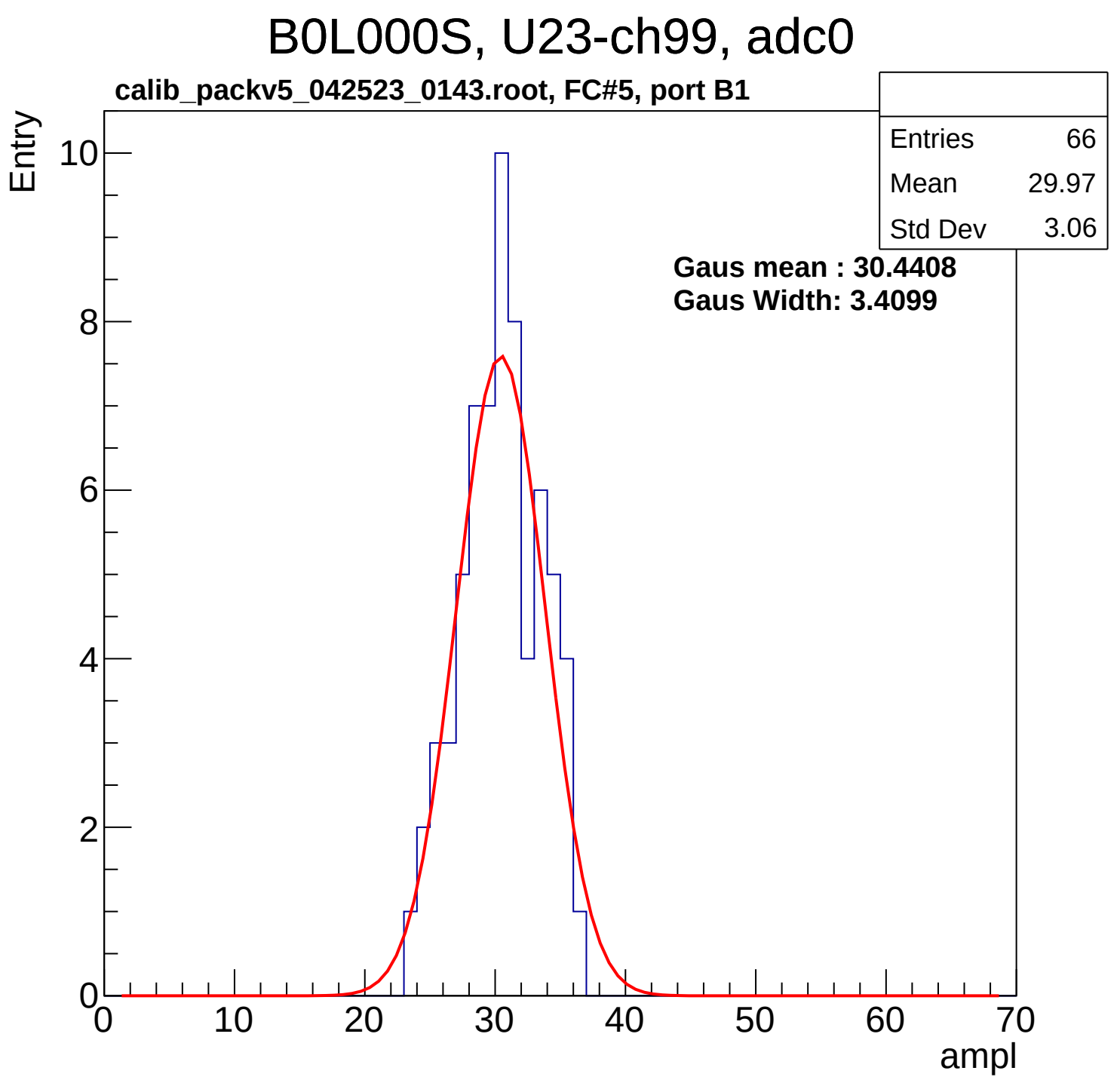

B0L000S, U23-ch98, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 49.6
Std Dev 3.915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

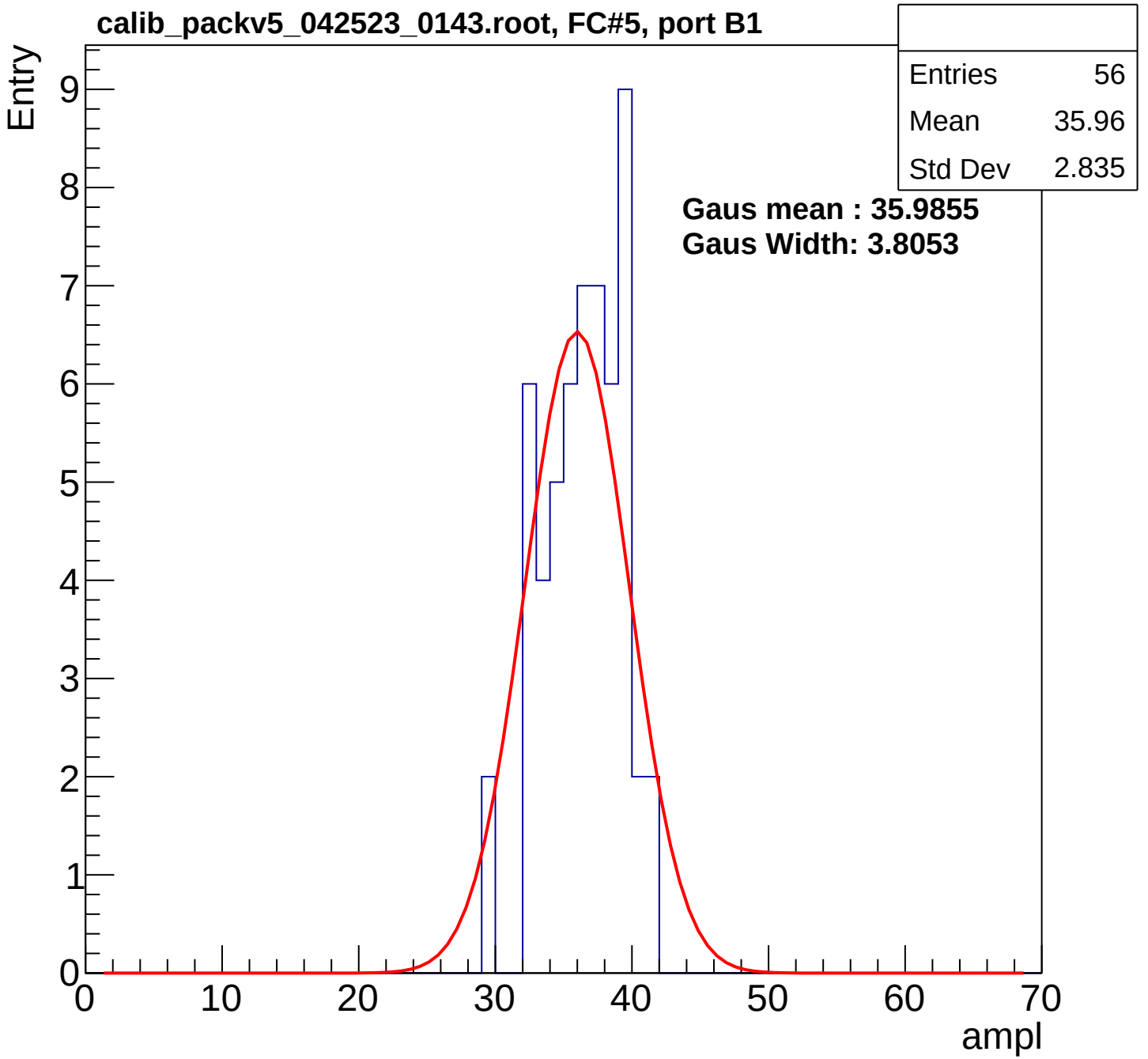

B0L000S, U23-ch98, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.75
Std Dev 2.882
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

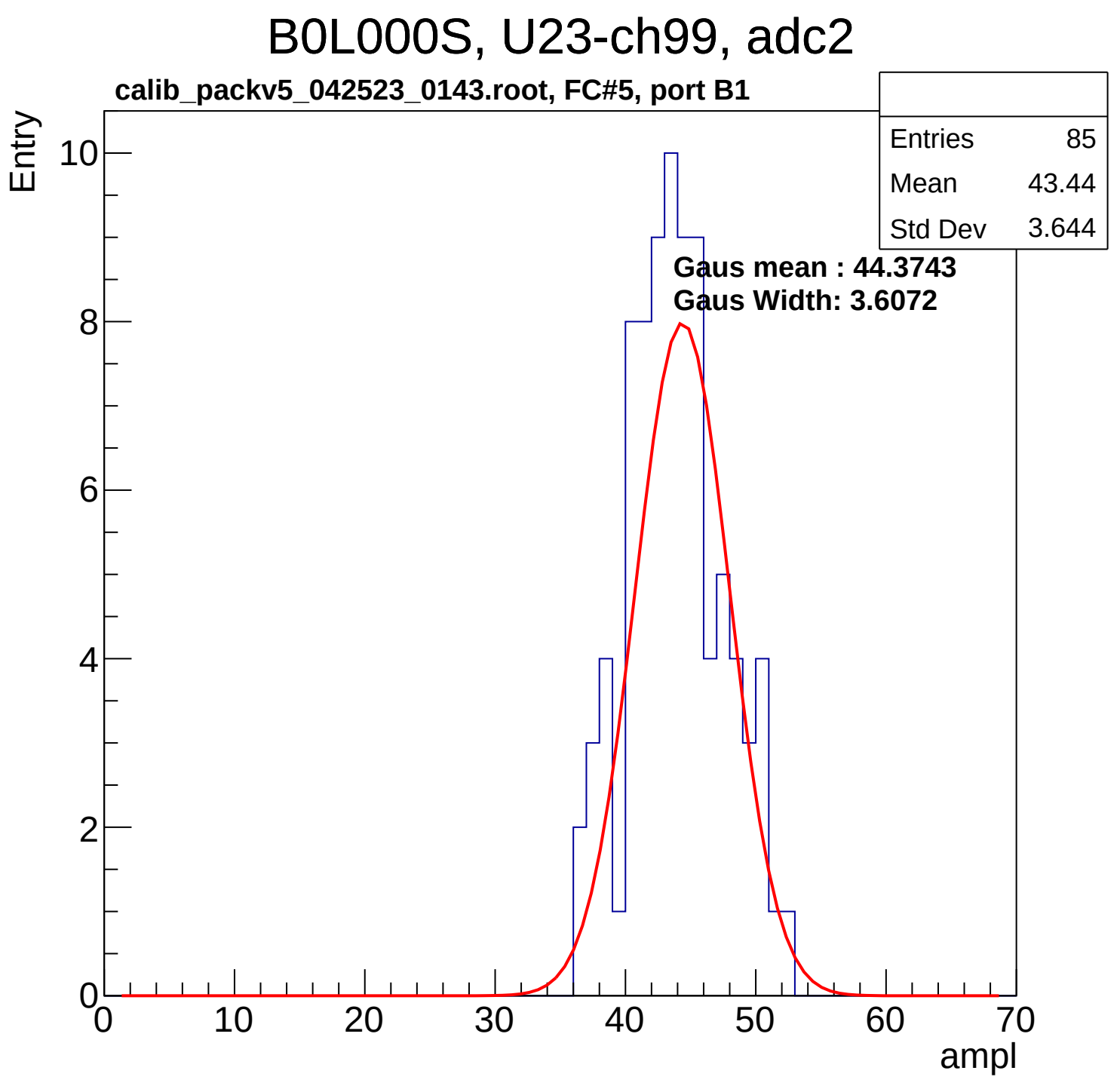

B0L000S, U23-ch98, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 59.76
Std Dev 2.322
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

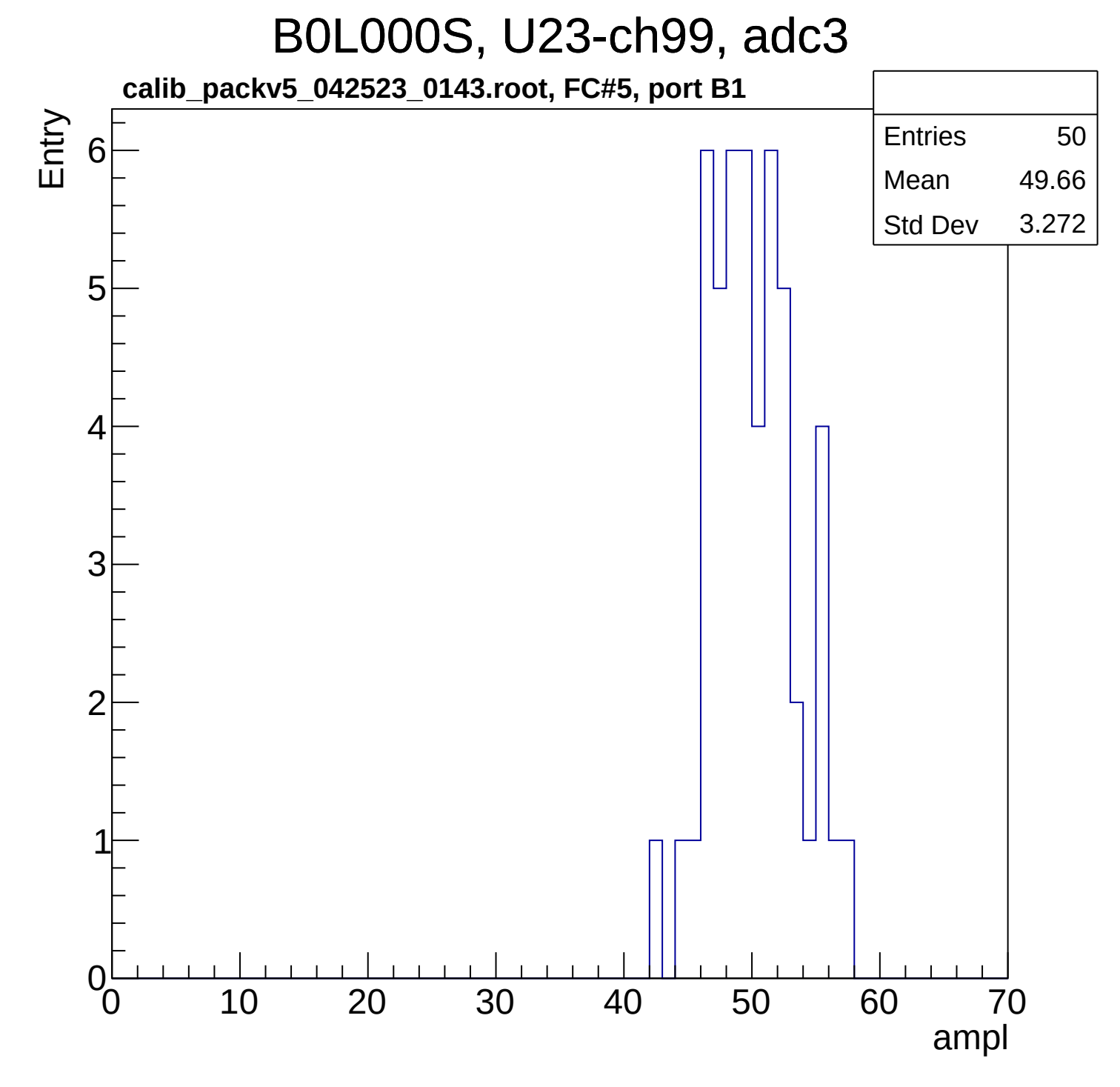

B0L000S, U23-ch98, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58
Std Dev 13.73
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

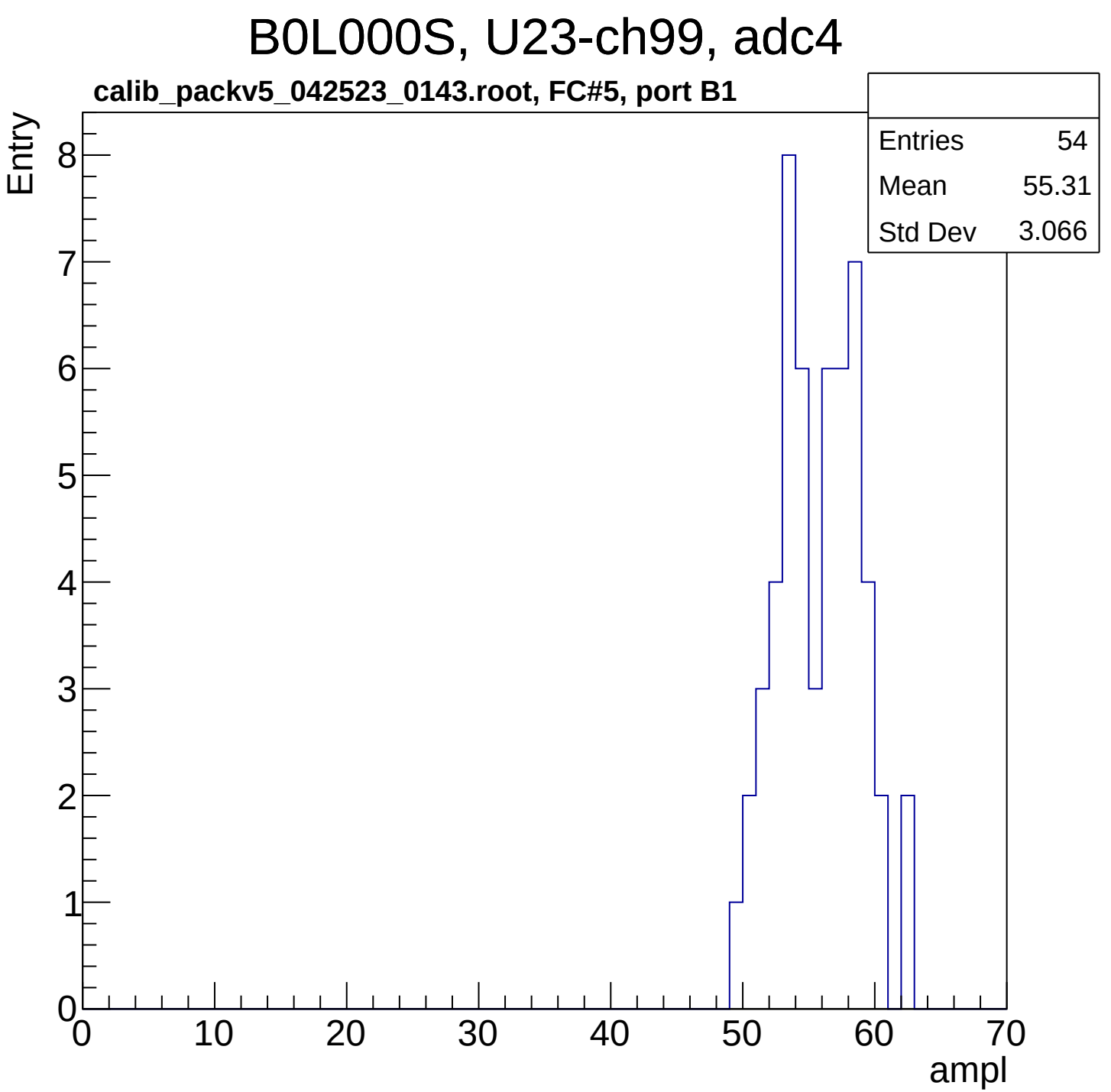

B0L000S, U23-ch98, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

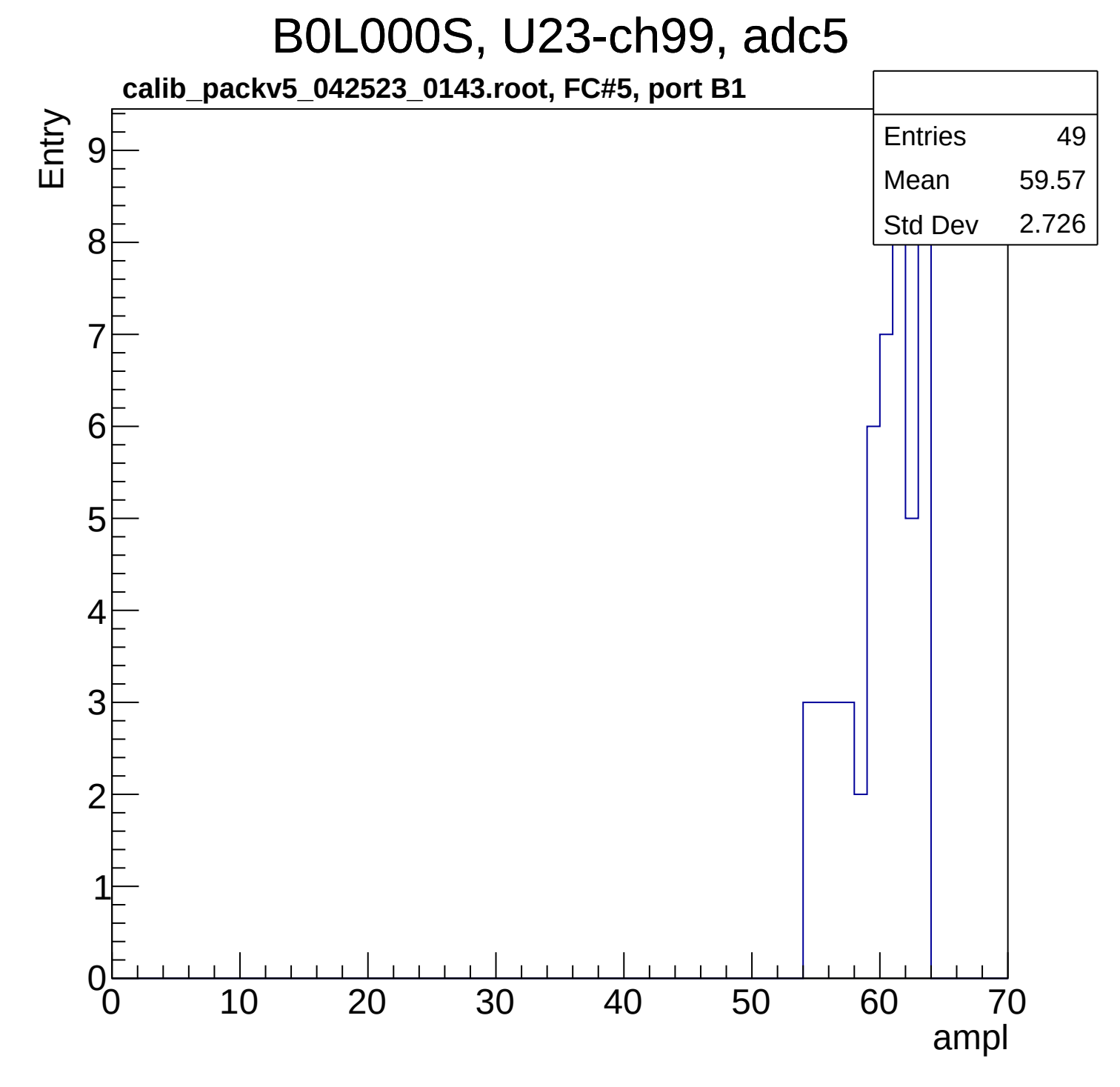

B0L000S, U23-ch99, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.97
Std Dev 3.06
Gaus mean : 30.4408
Gaus Width: 3.4099
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

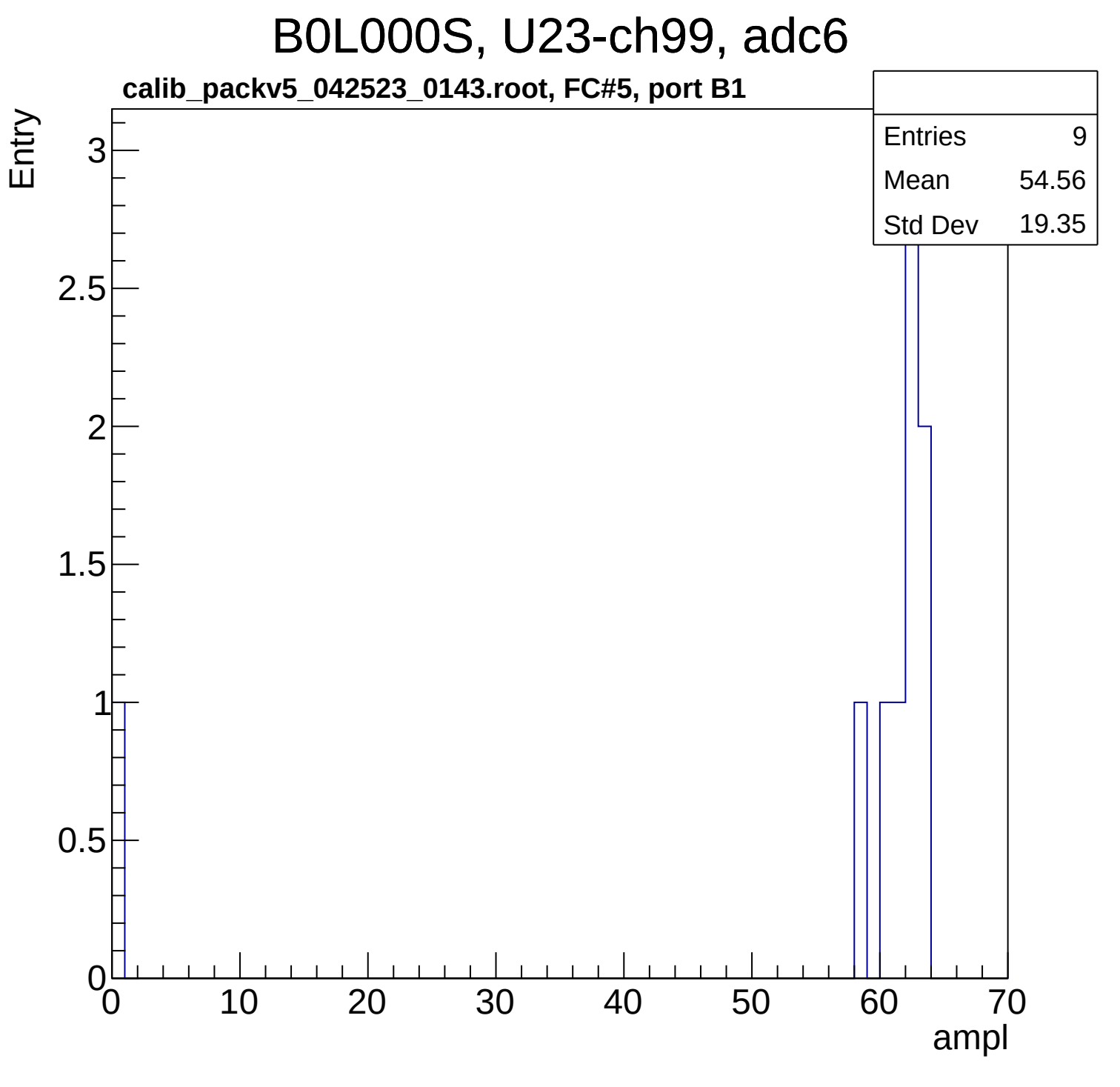

B0L000S, U23-ch99, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 35.96
Std Dev 2.835
Gaus mean : 35.9855
Gaus Width: 3.8053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

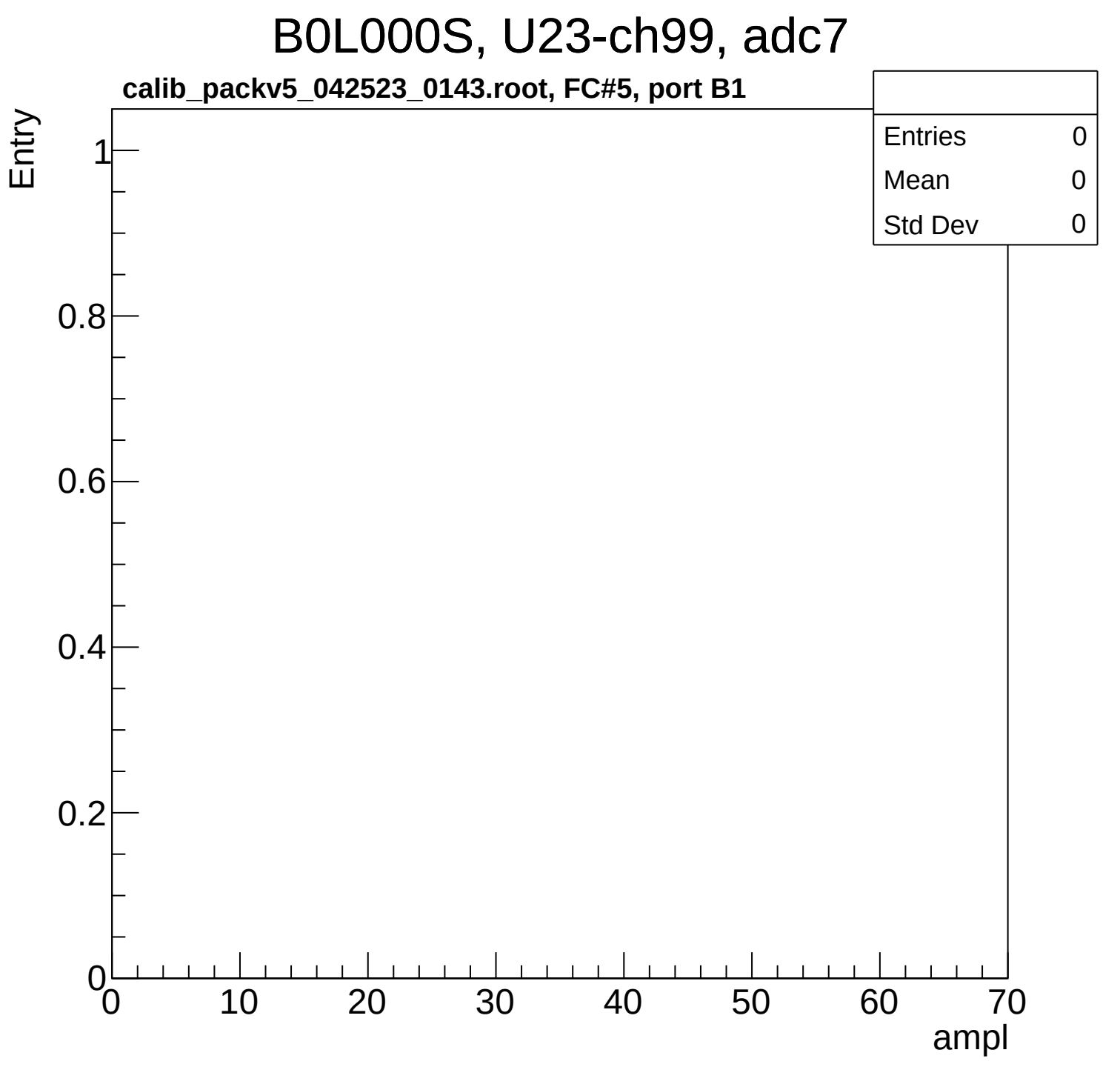

B0L000S, U23-ch99, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 43.44
Std Dev 3.644
Gaus mean : 44.3743
Gaus Width: 3.6072
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

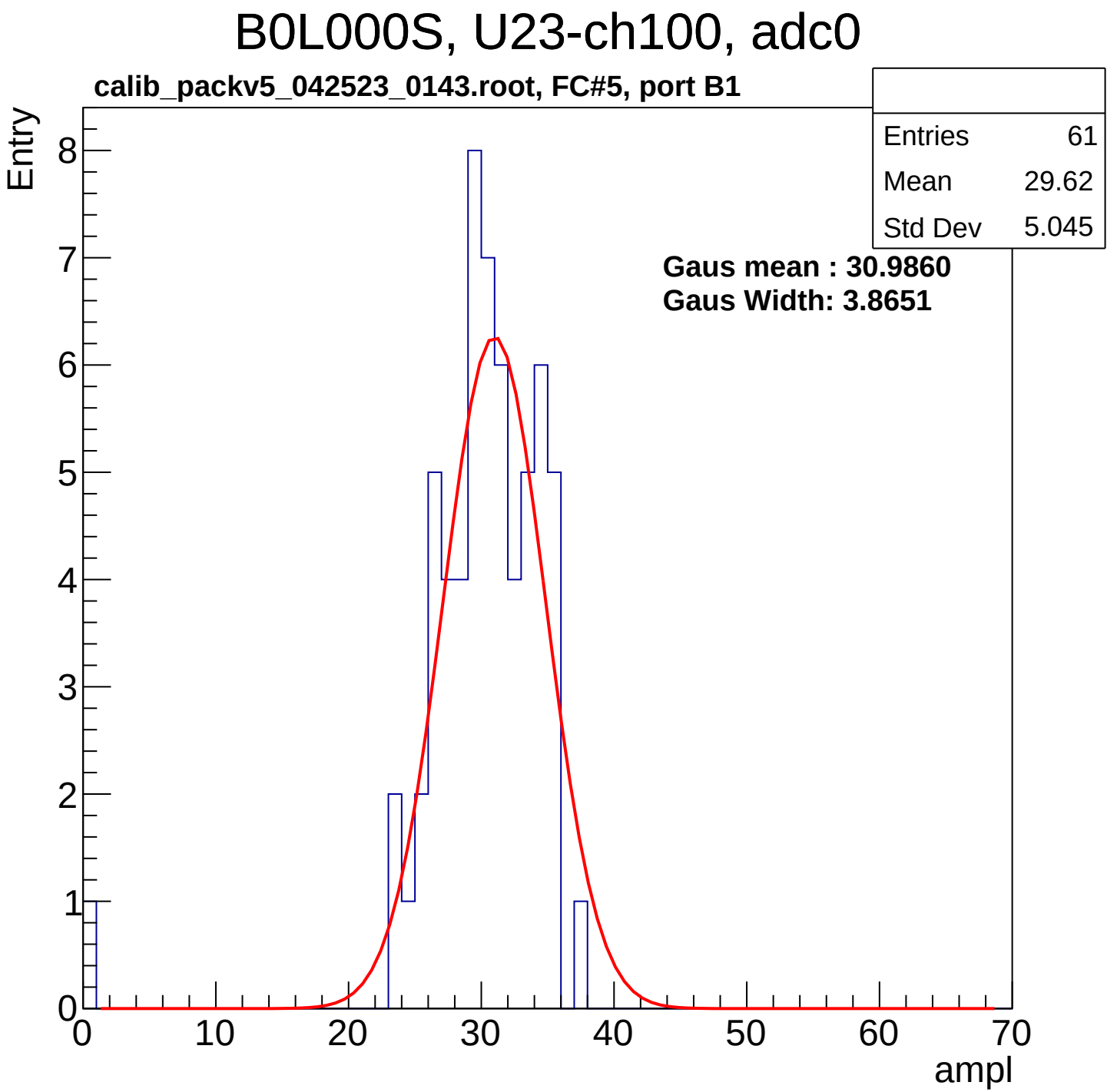

B0L000S, U23-ch99, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 49.66
Std Dev 3.272
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

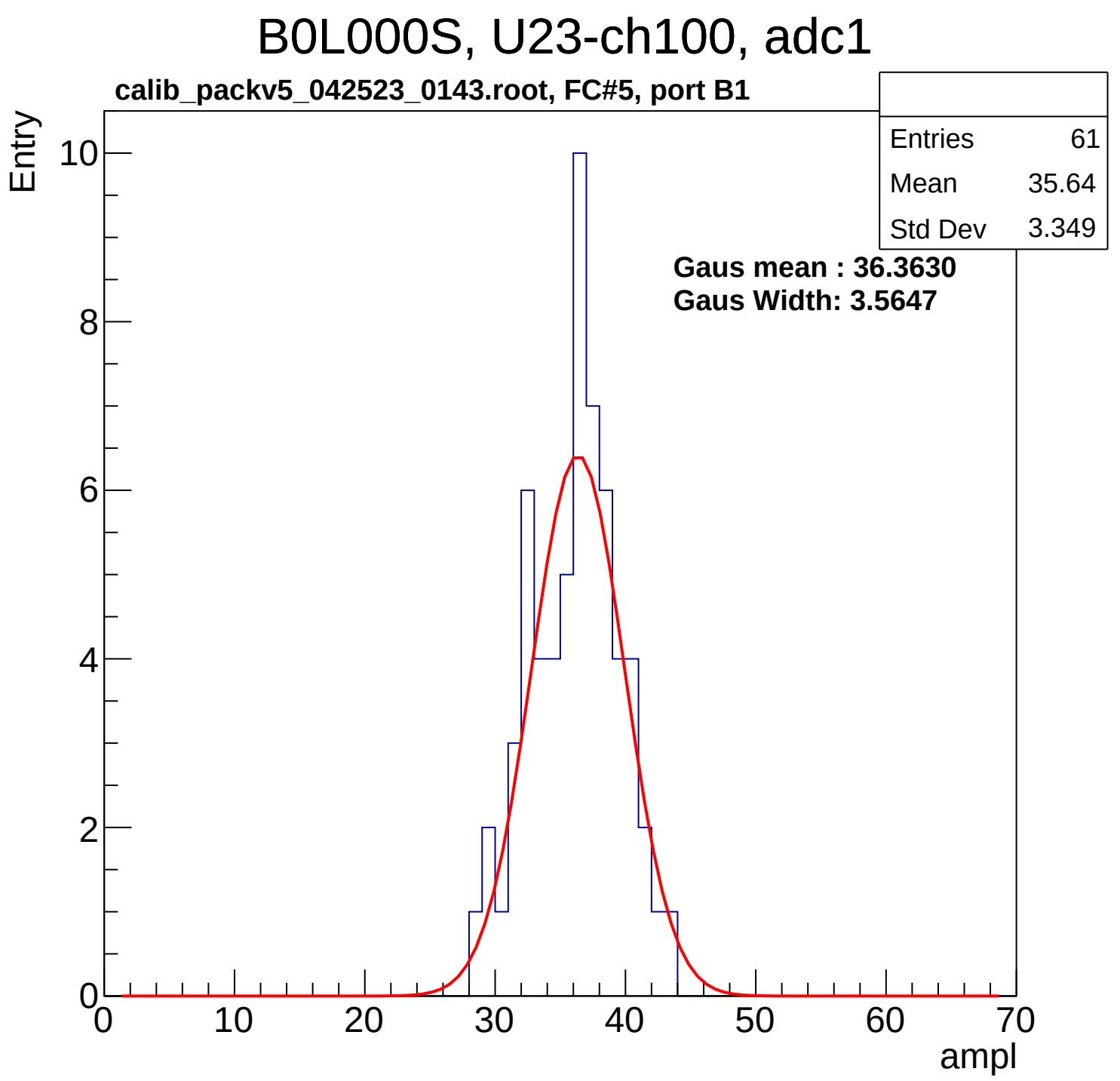

B0L000S, U23-ch99, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.31
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

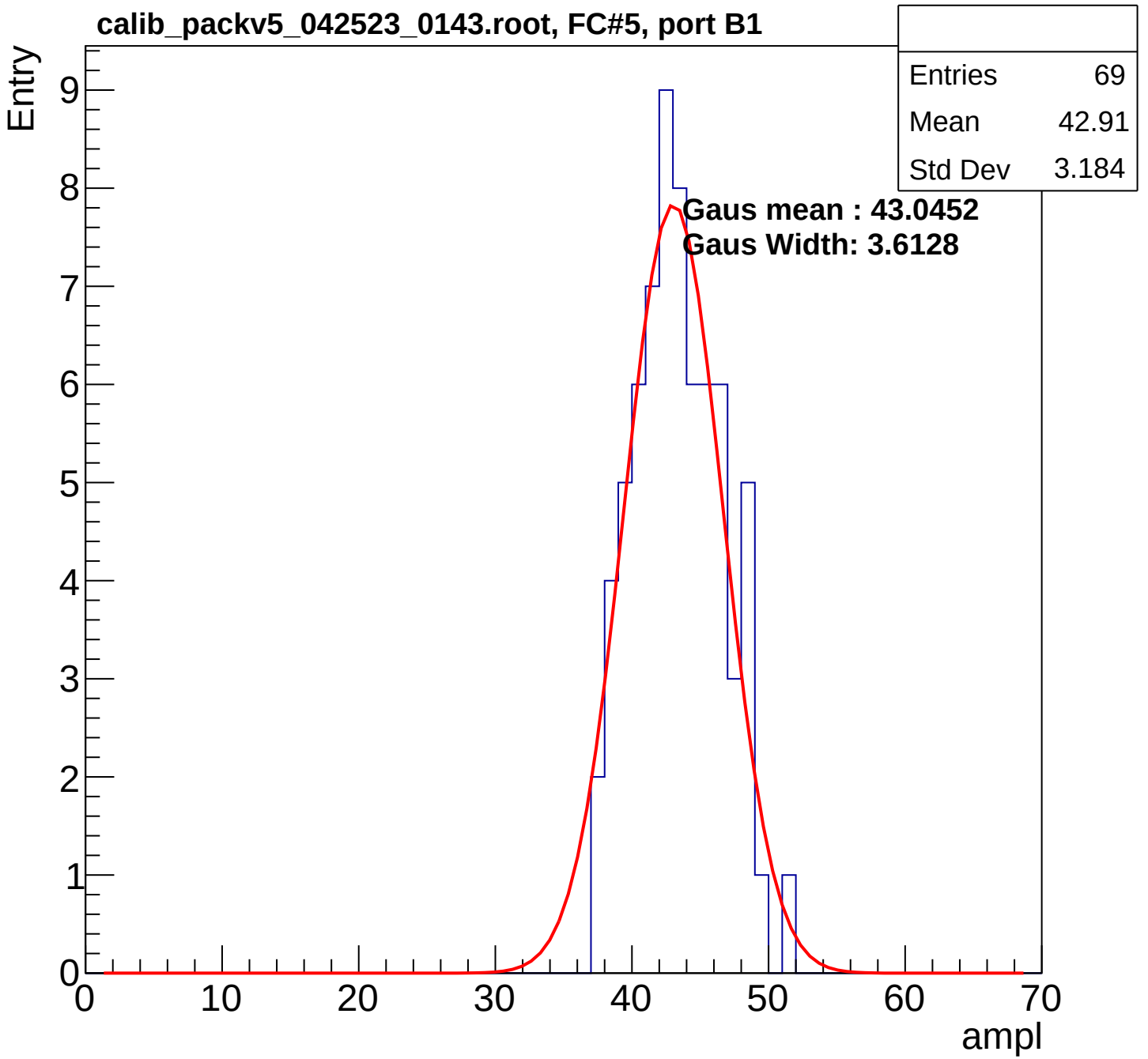

B0L000S, U23-ch99, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 59.57
Std Dev 2.726
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

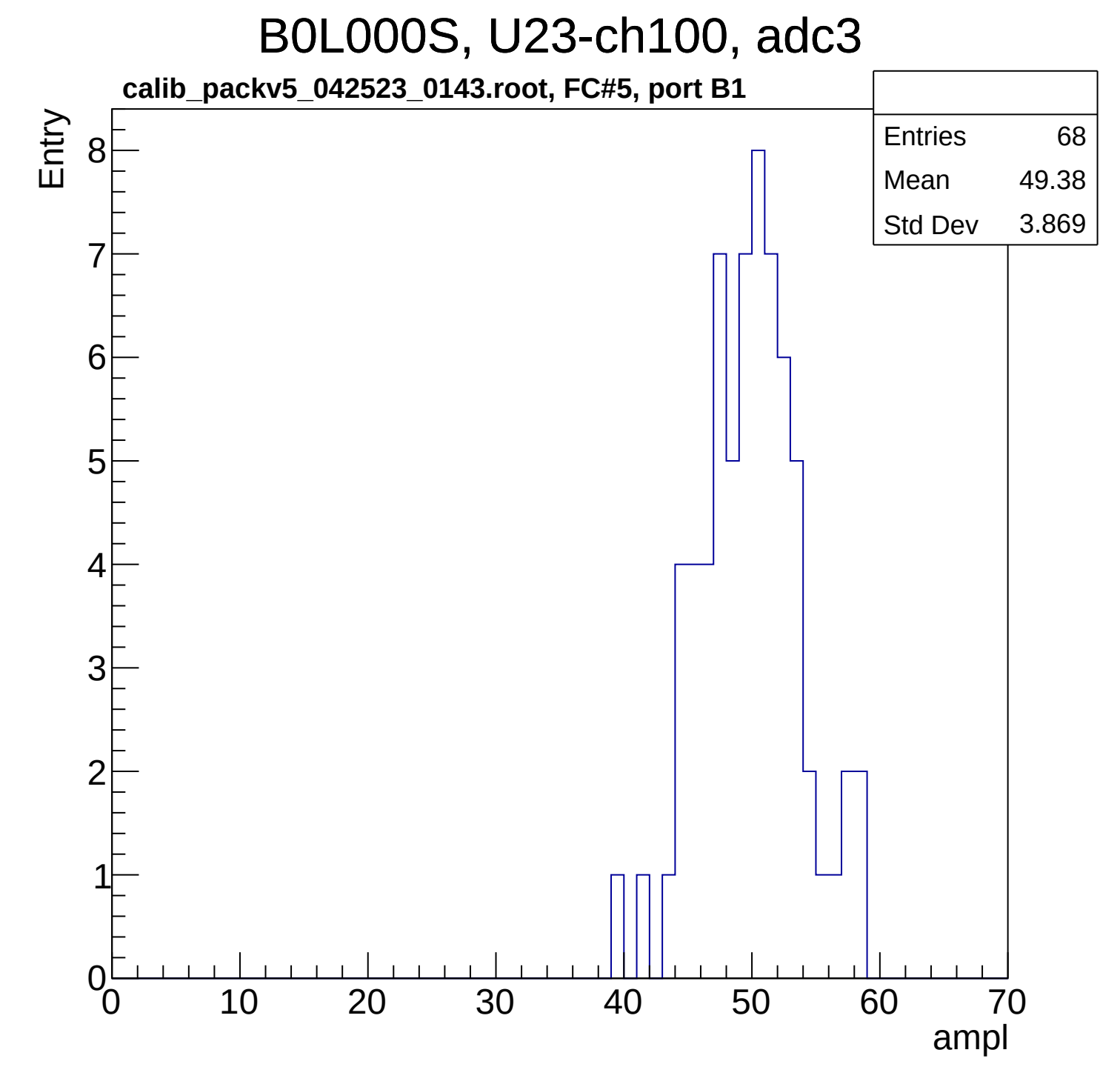

B0L000S, U23-ch99, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 54.56
Std Dev 19.35
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

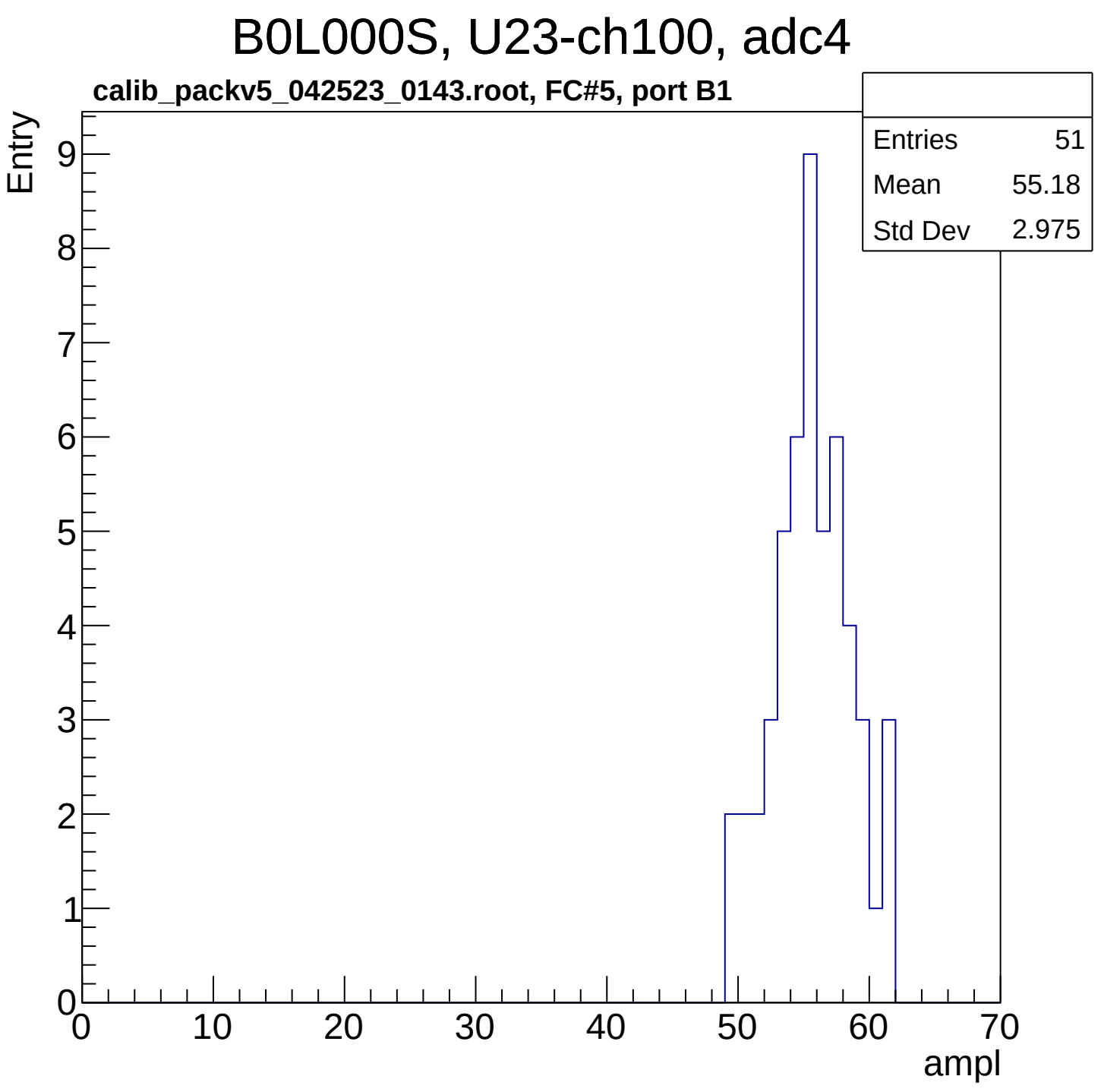

B0L000S, U23-ch99, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

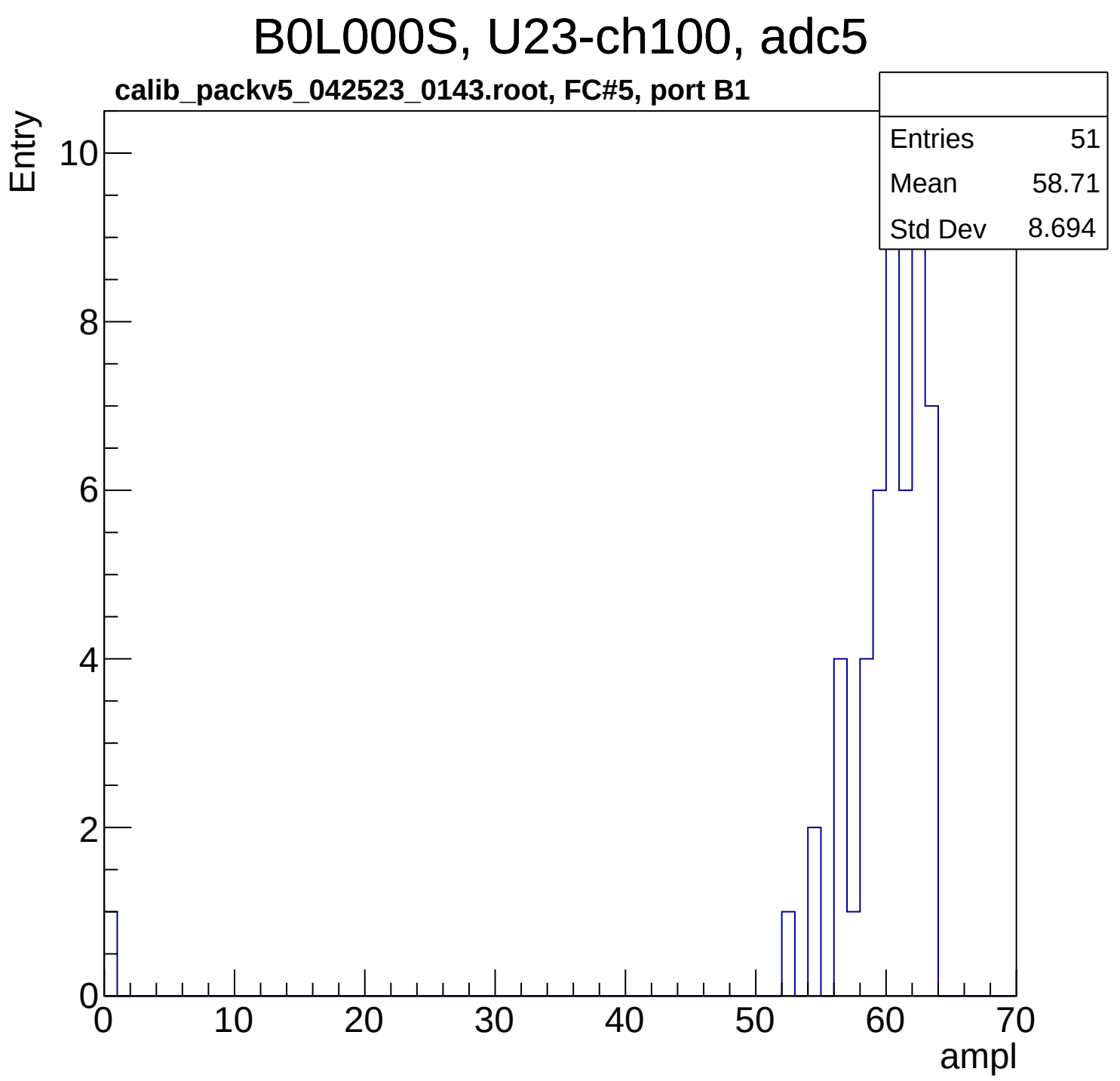

B0L000S, U23-ch100, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.62
Std Dev 5.045
Gaus mean : 30.9860
Gaus Width: 3.8651
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

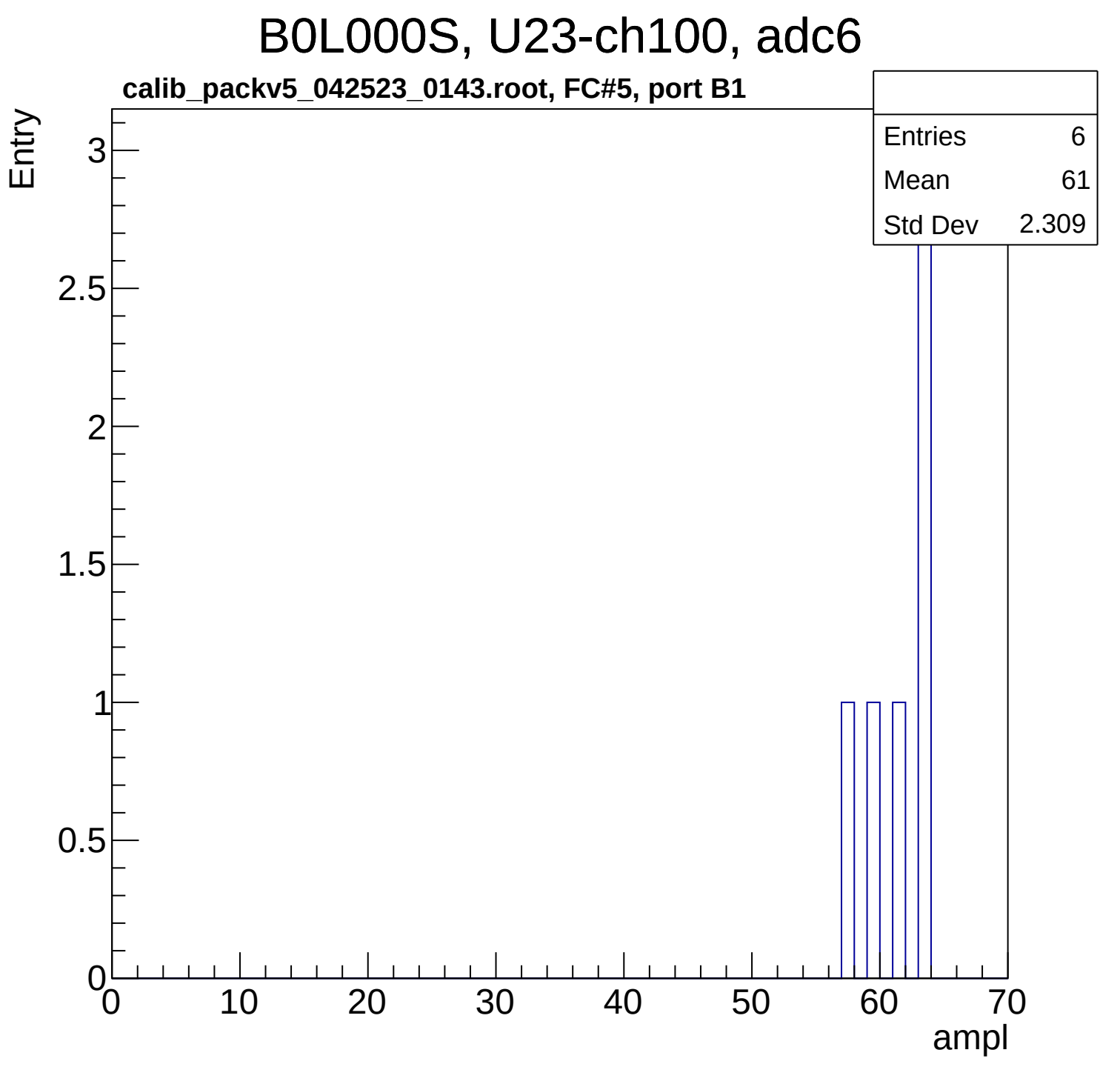

B0L000S, U23-ch100, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.64
Std Dev 3.349
Gaus mean : 36.3630
Gaus Width: 3.5647
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

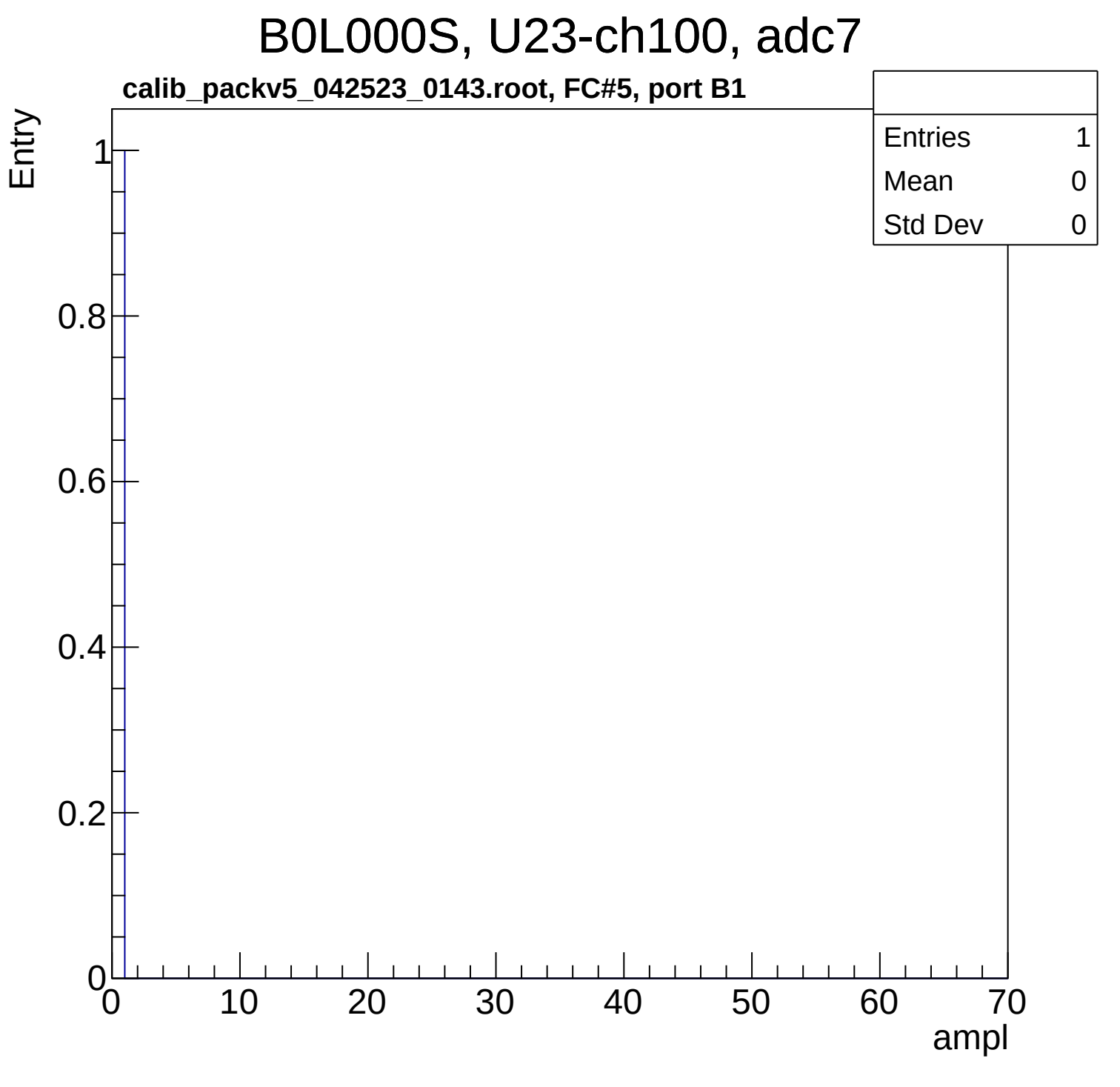

B0L000S, U23-ch100, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 42.91
Std Dev 3.184
Gaus mean : 43.0452
Gaus Width: 3.6128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

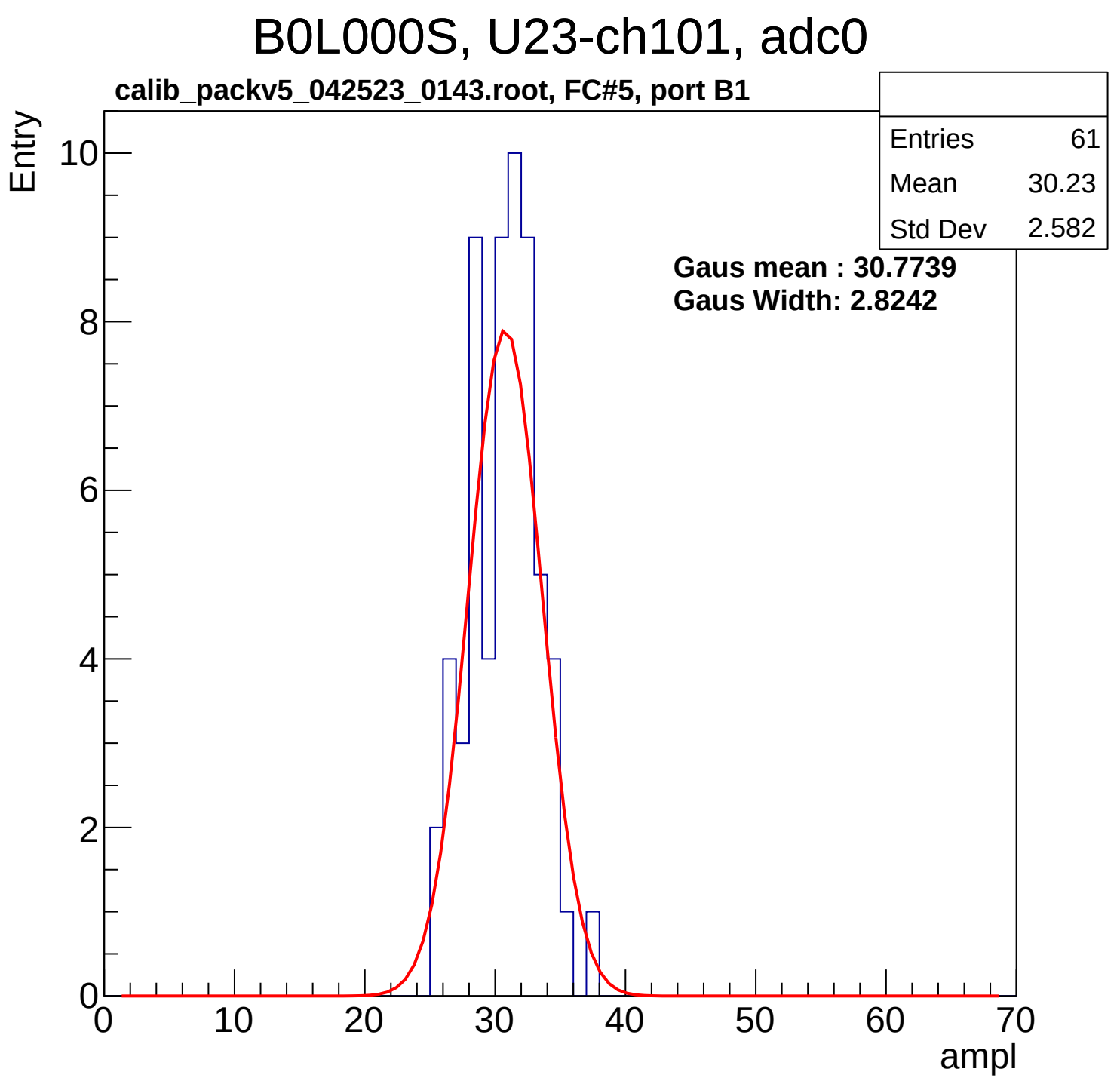

B0L000S, U23-ch100, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 49.38
Std Dev 3.869
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

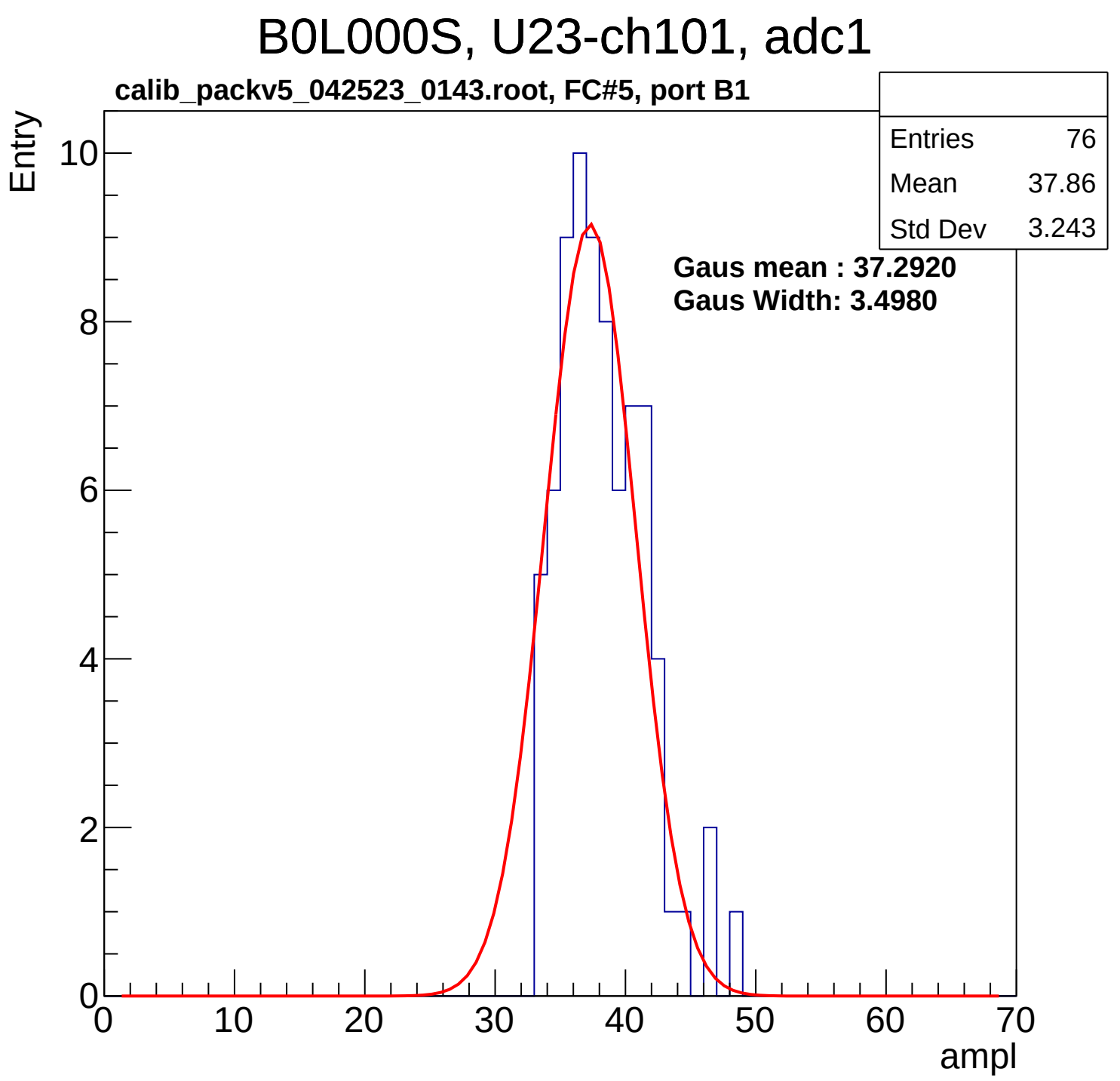

B0L000S, U23-ch100, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 55.18
Std Dev 2.975
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

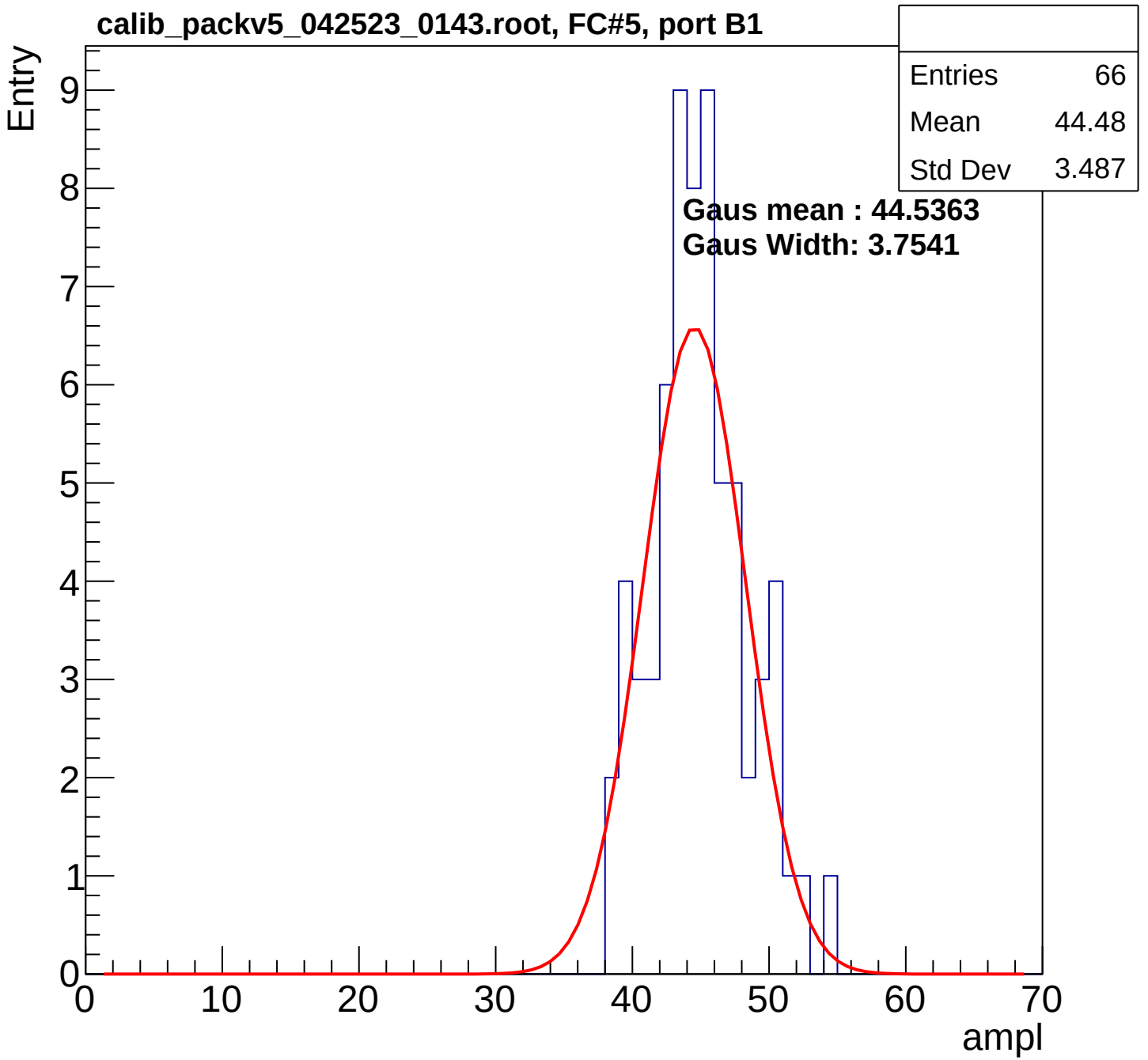

B0L000S, U23-ch100, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.71
Std Dev 8.694
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

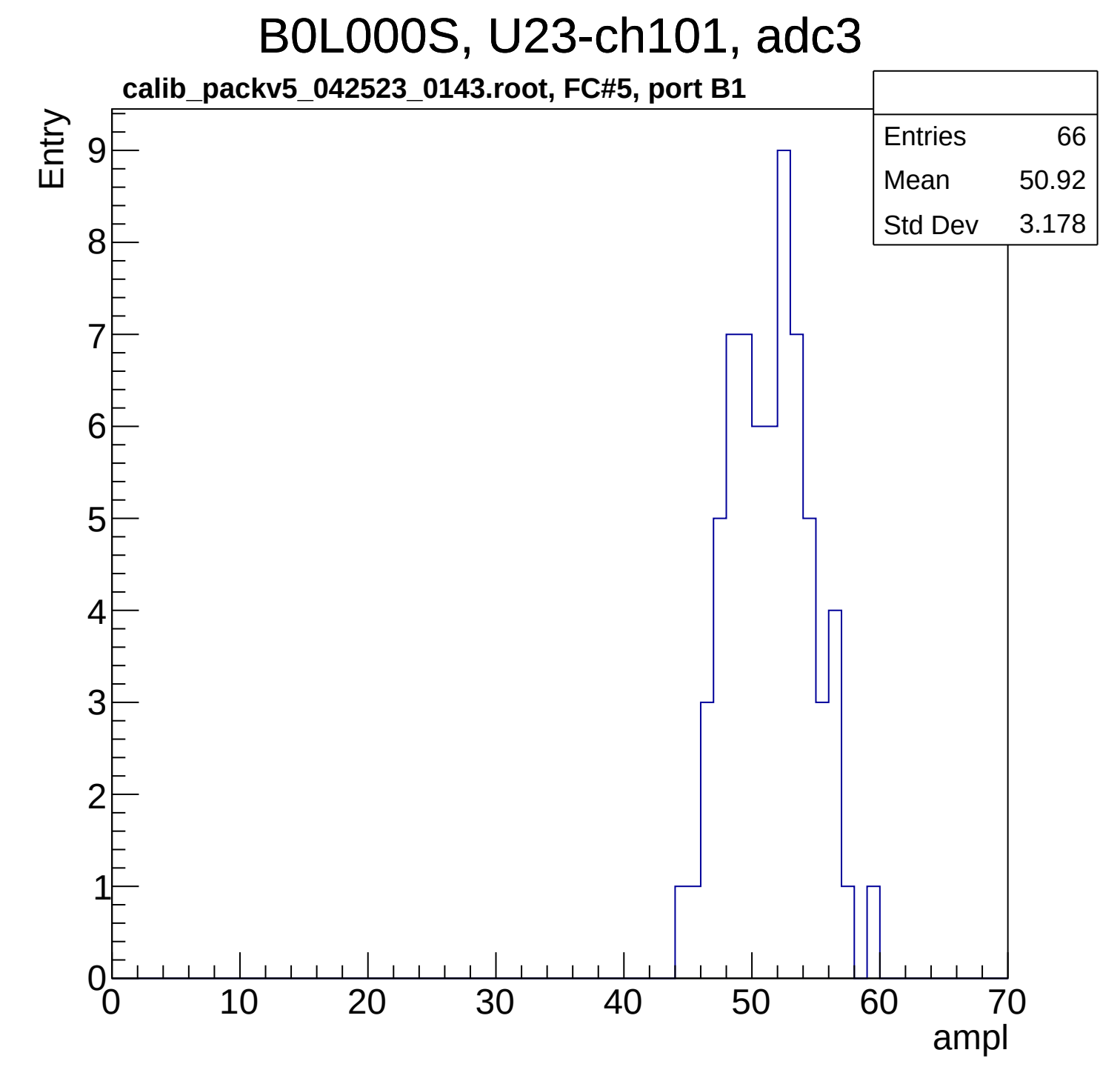

B0L000S, U23-ch100, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61
Std Dev 2.309
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

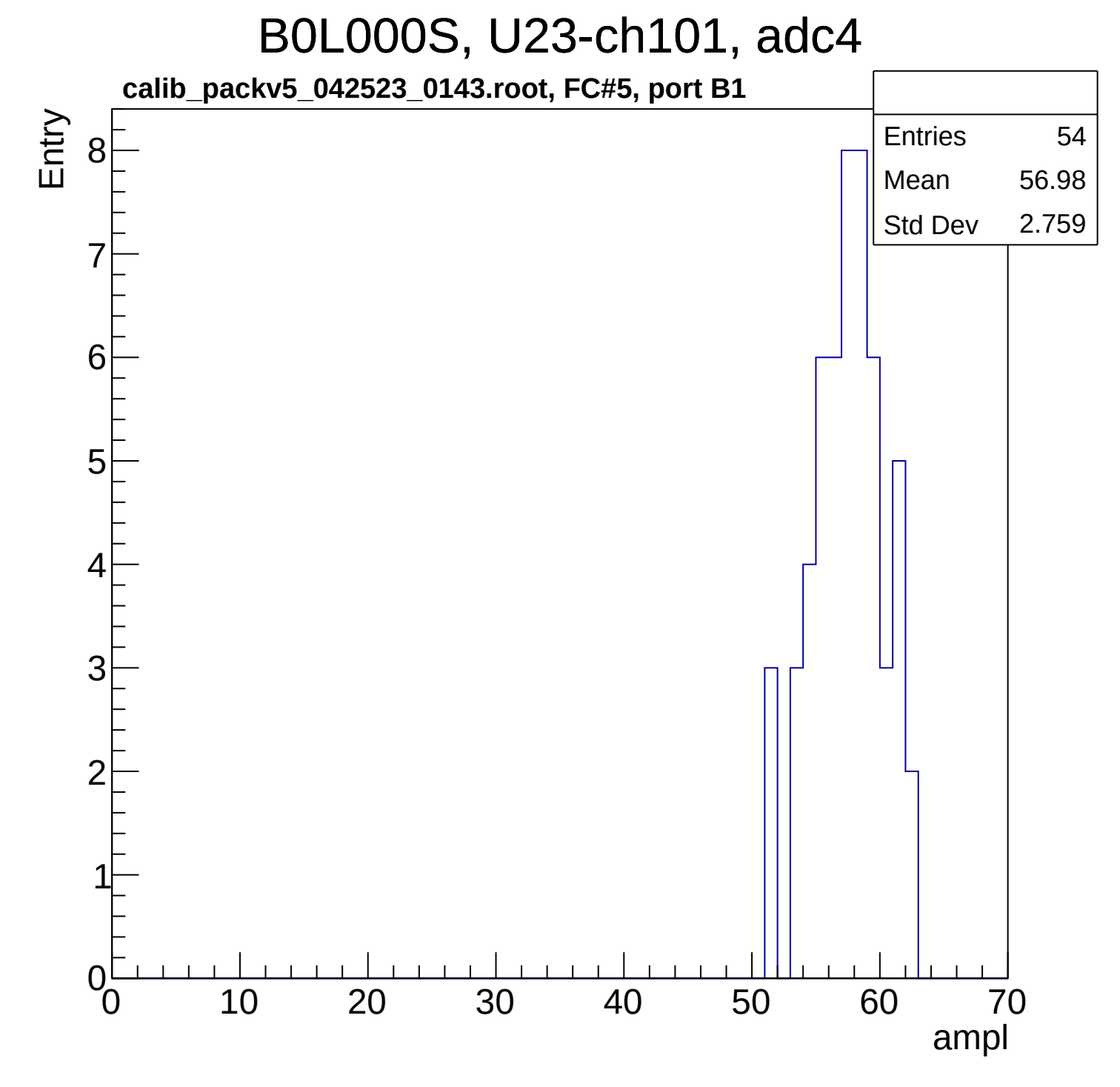

B0L000S, U23-ch100, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

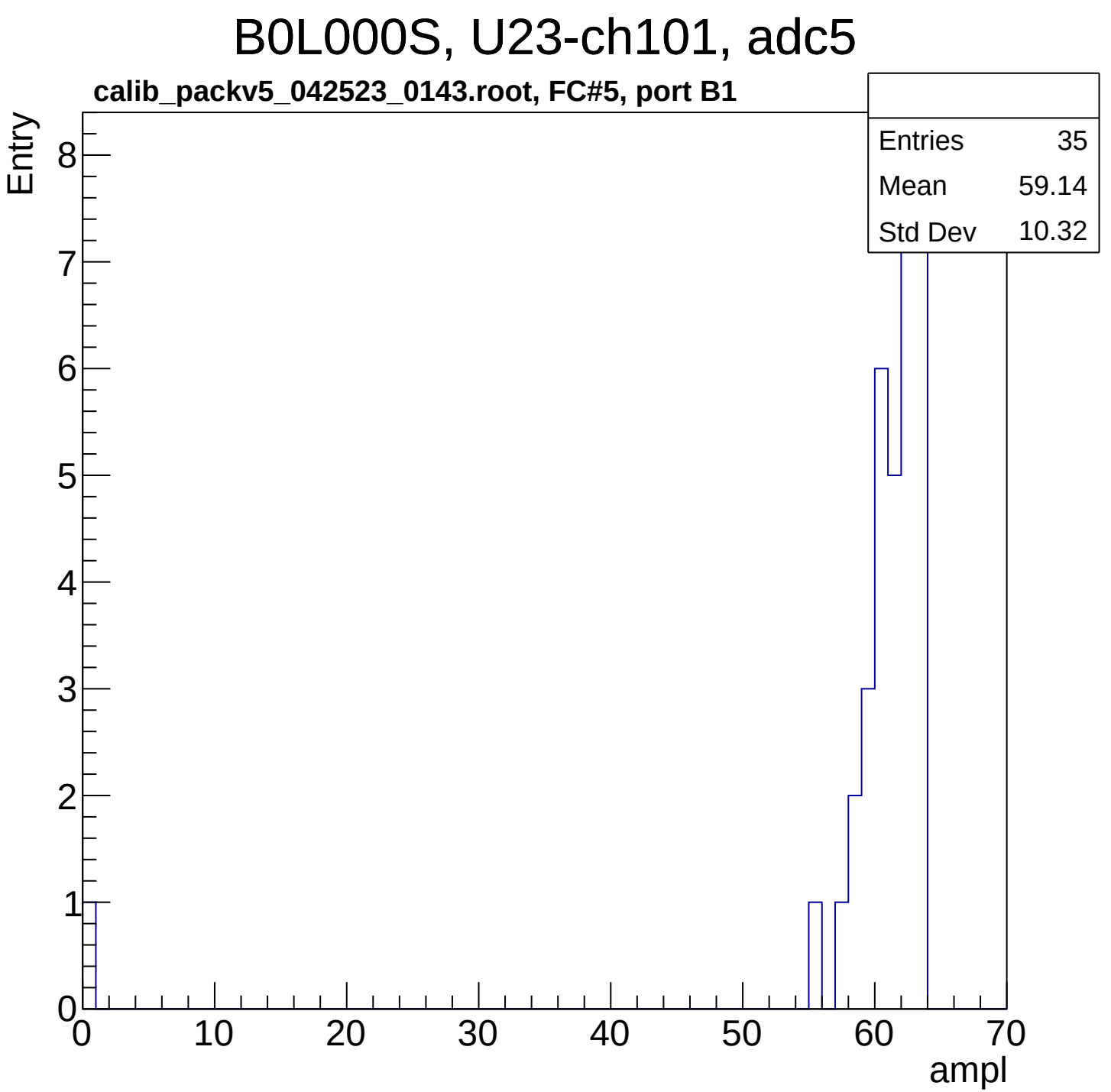

B0L000S, U23-ch101, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 30.23
Std Dev 2.582
Gaus mean : 30.7739
Gaus Width: 2.8242
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

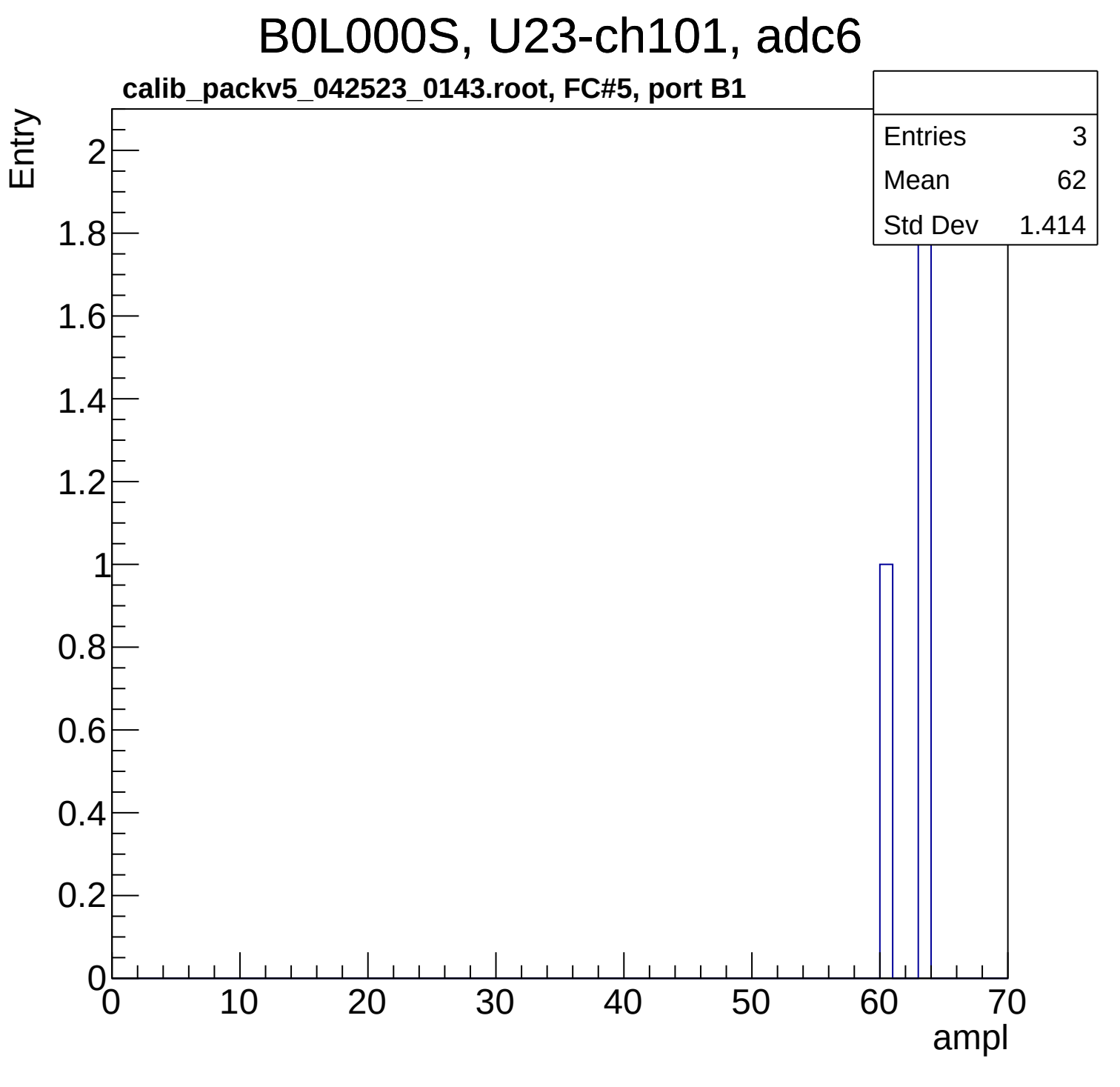

B0L000S, U23-ch101, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 37.86
Std Dev 3.243
Gaus mean : 37.2920
Gaus Width: 3.4980
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

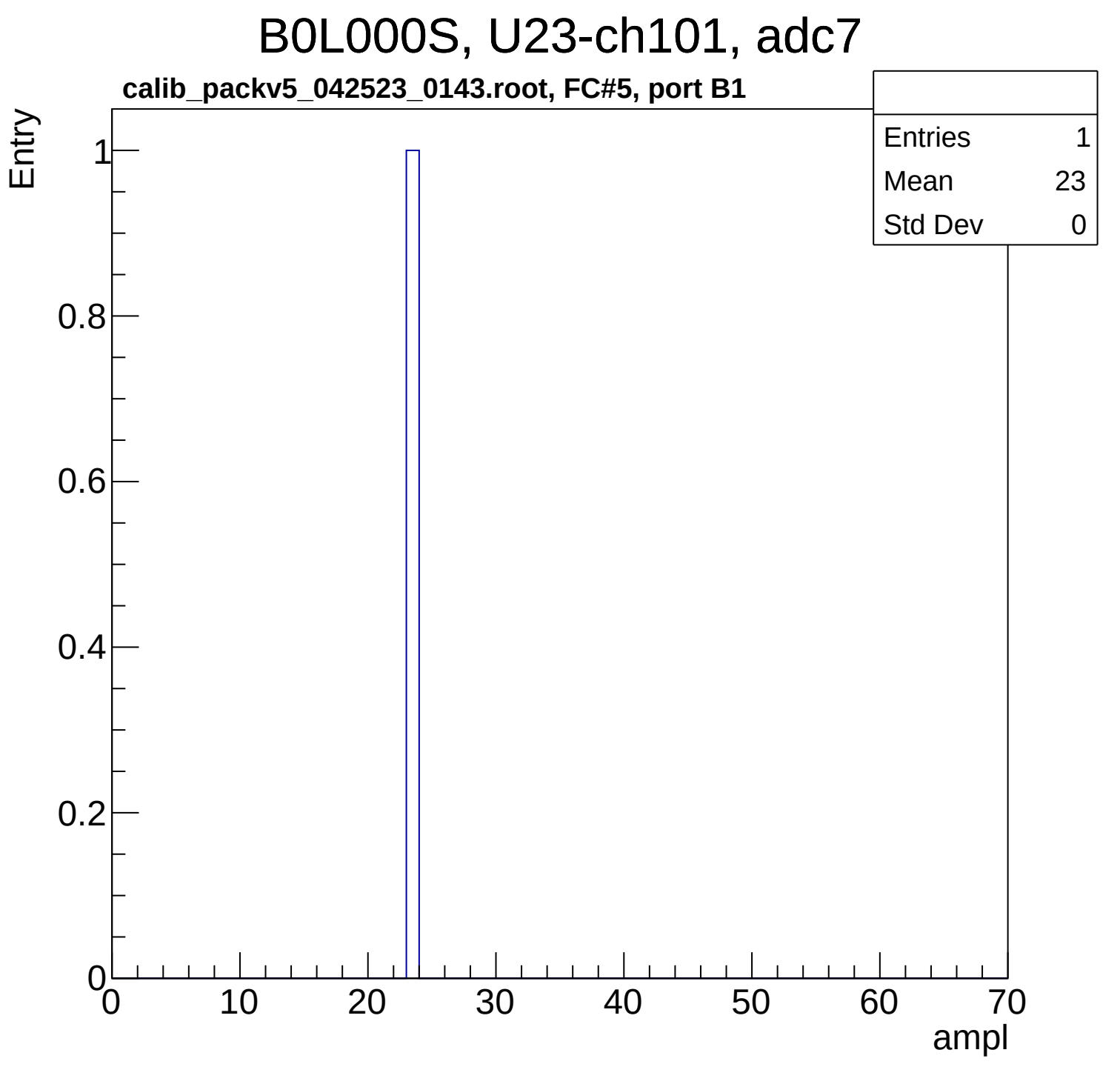

B0L000S, U23-ch101, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 44.48
Std Dev 3.487
Gaus mean : 44.5363
Gaus Width: 3.7541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

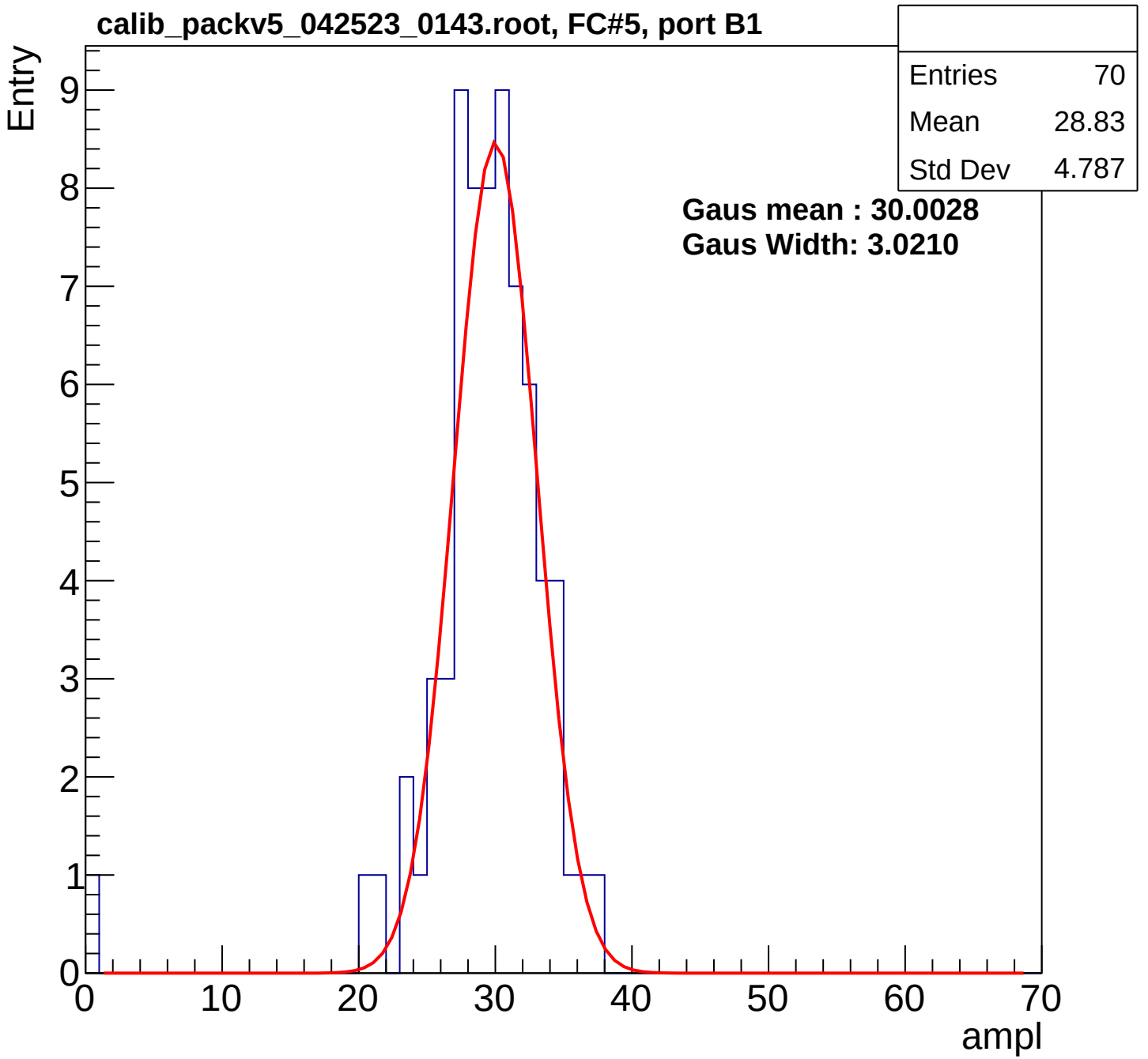

B0L000S, U23-ch101, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.92
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

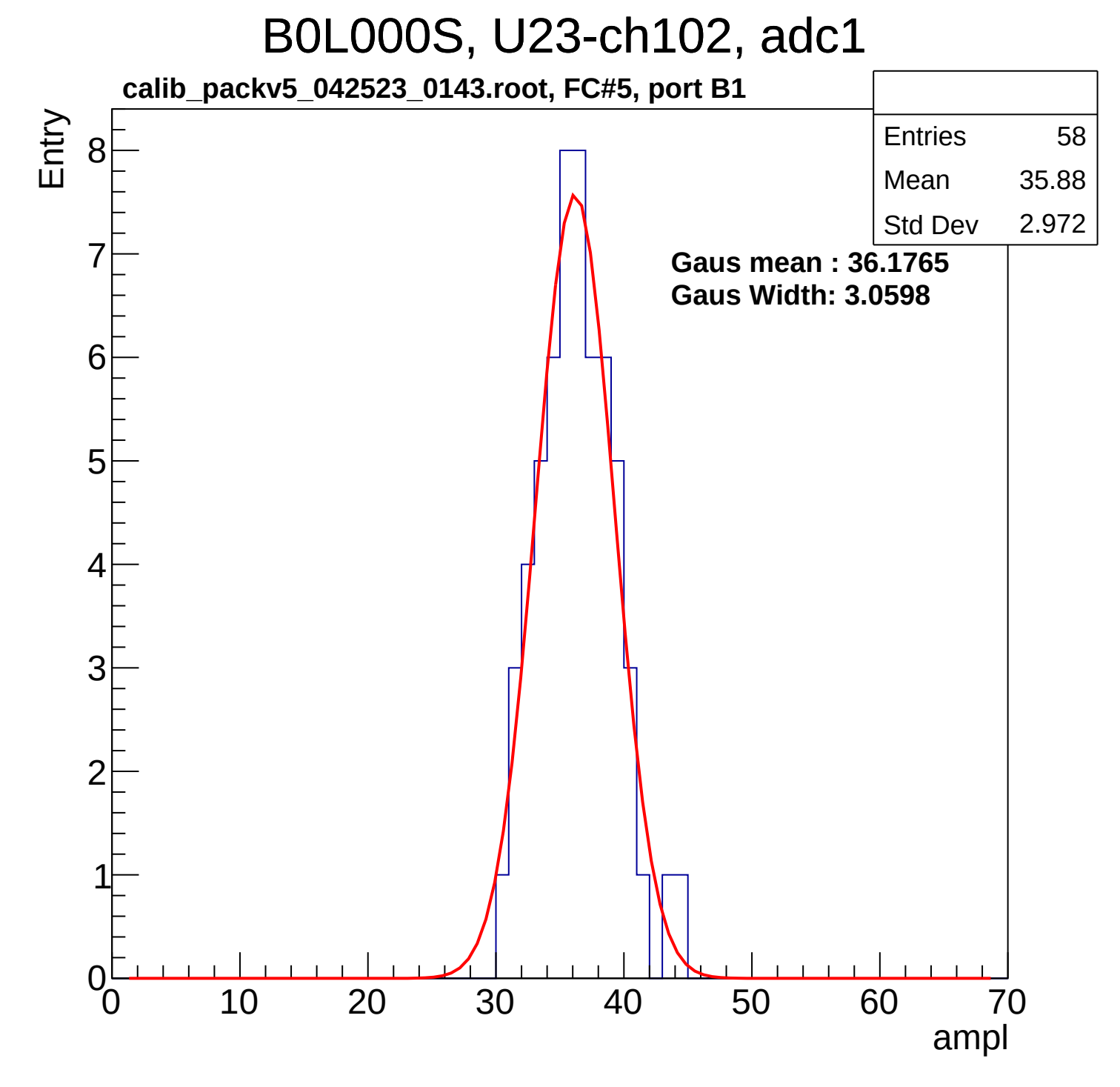

B0L000S, U23-ch101, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.98
Std Dev 2.759
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

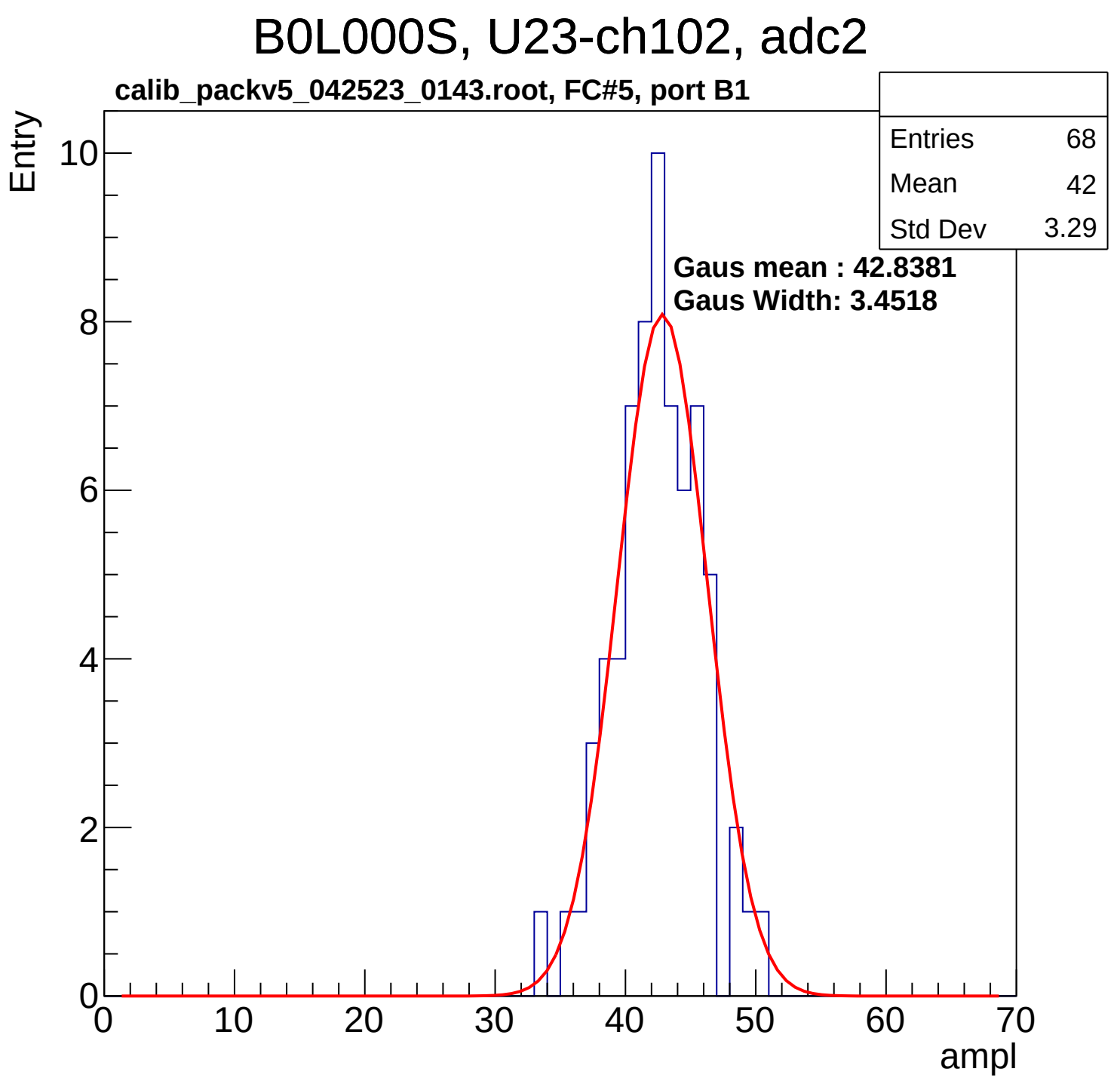

B0L000S, U23-ch101, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 59.14
Std Dev 10.32
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

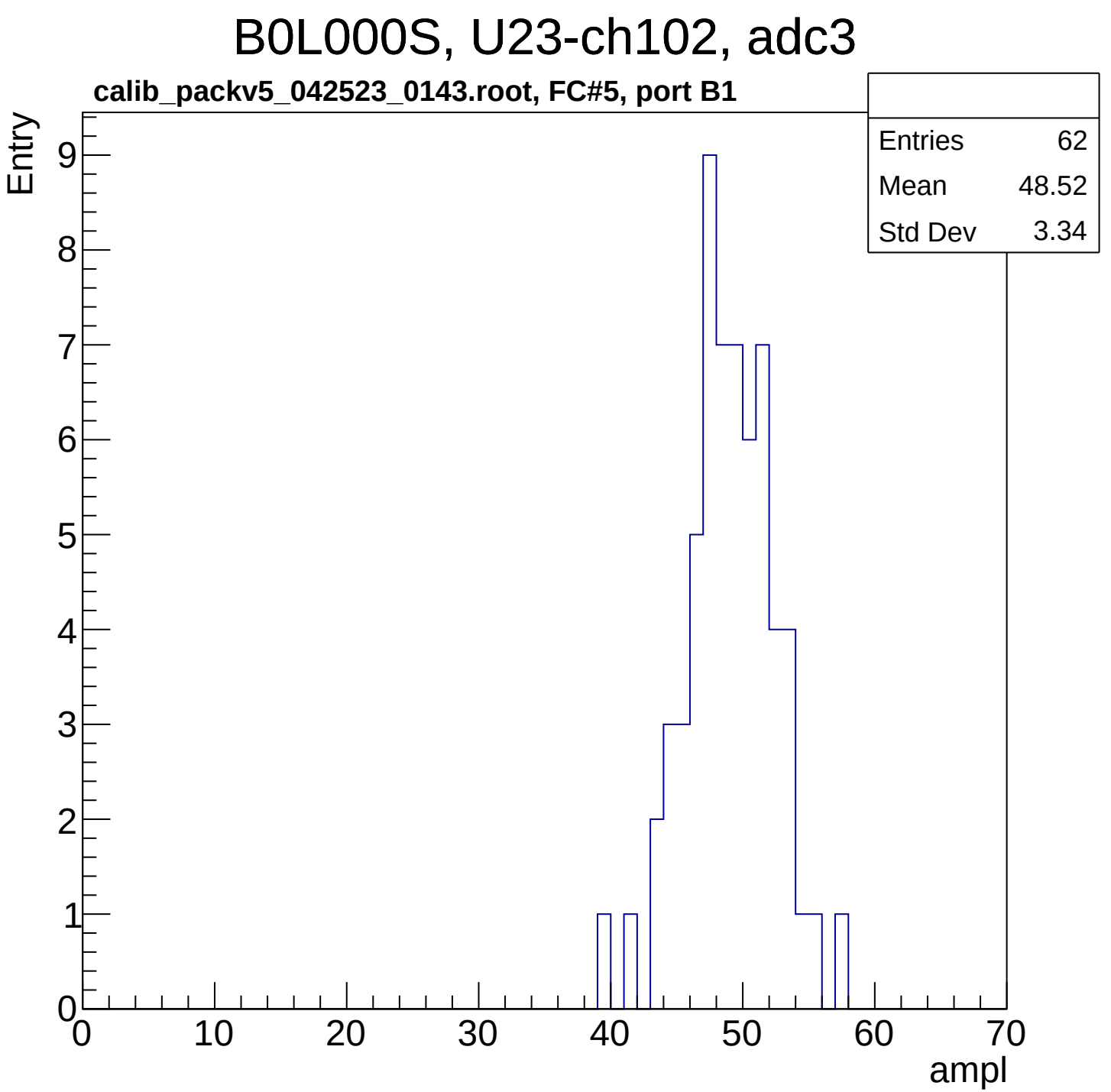

B0L000S, U23-ch101, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

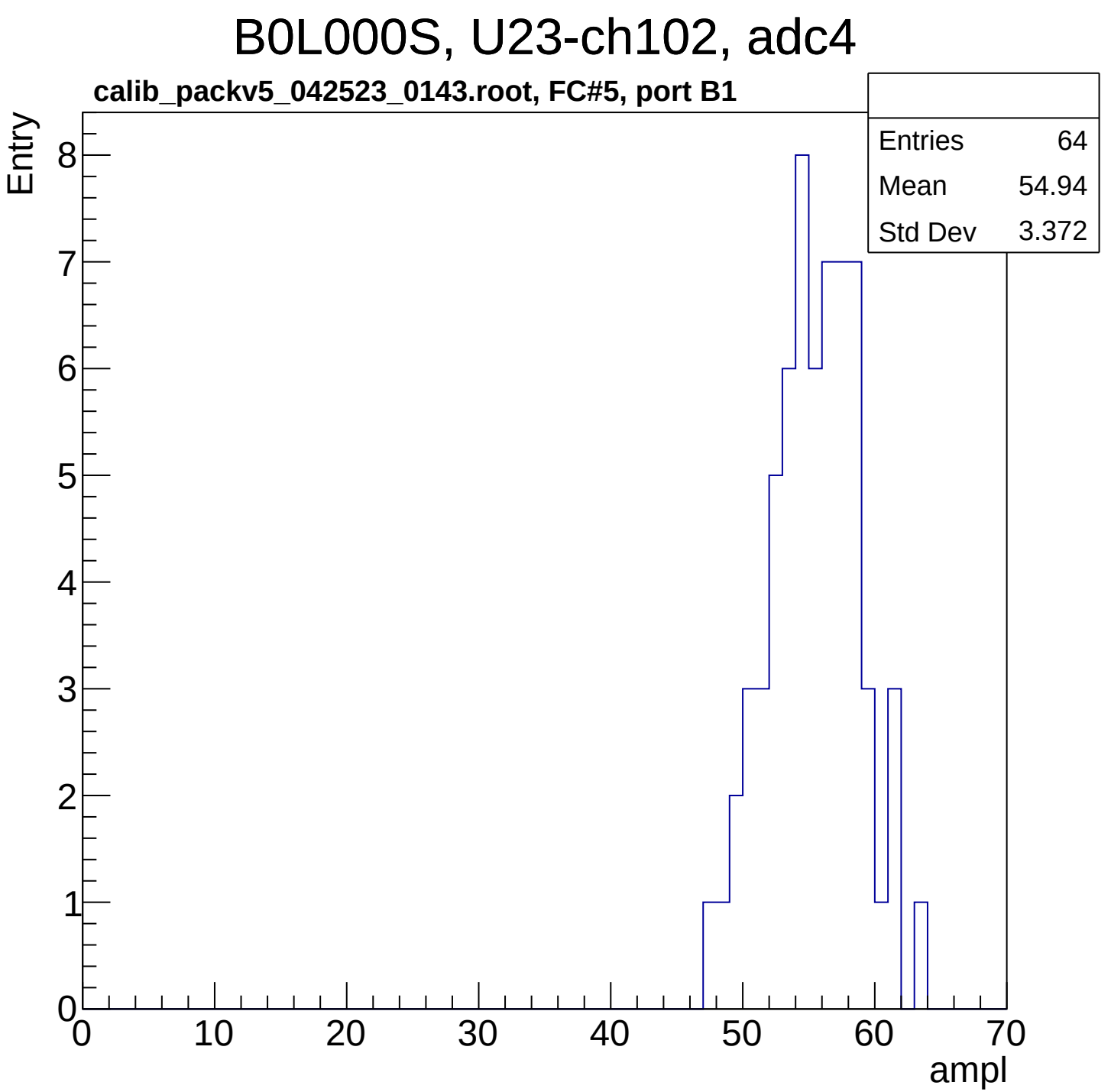

B0L000S, U23-ch101, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

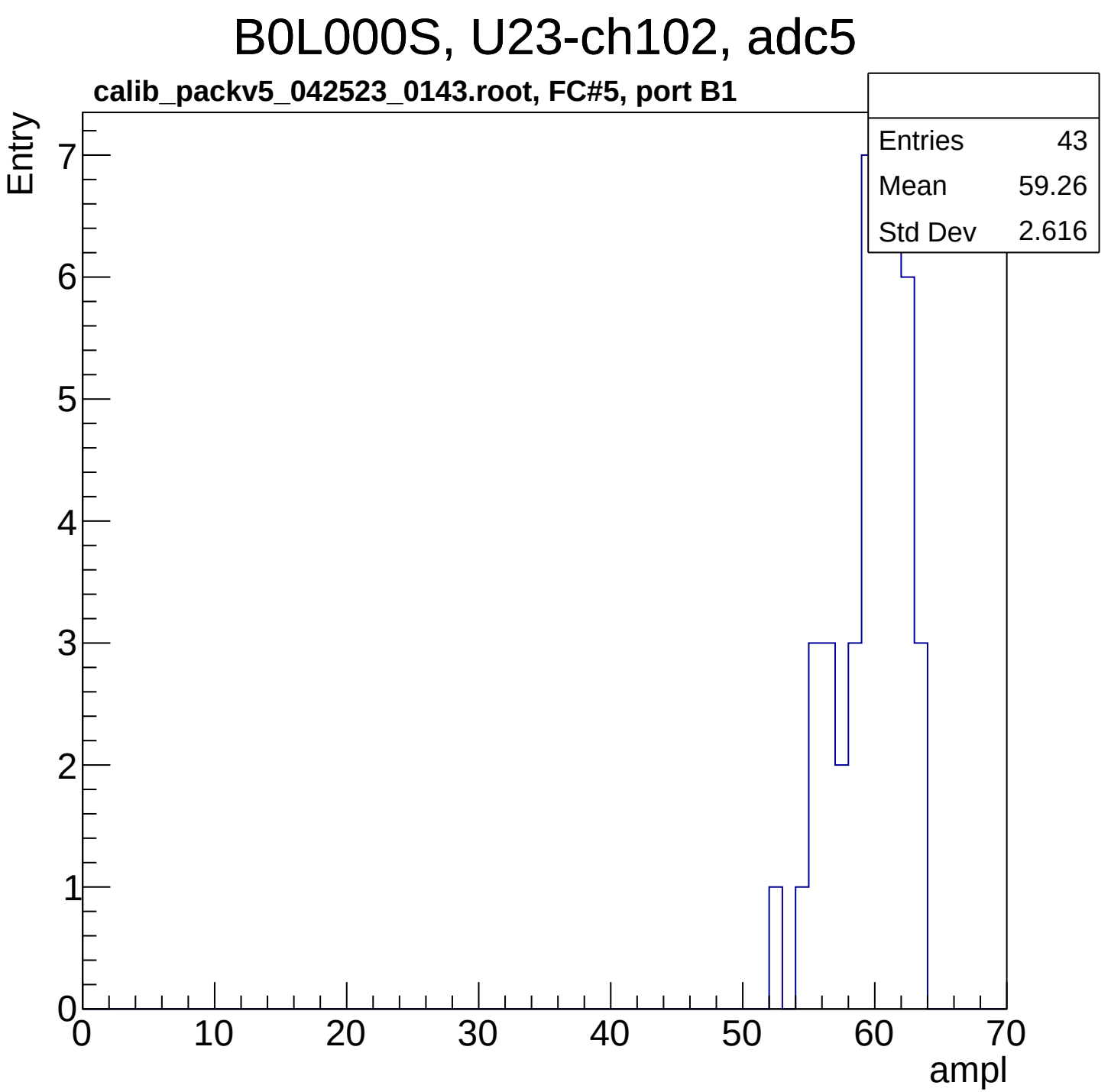

B0L000S, U23-ch102, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.83
Std Dev 4.787
Gaus mean : 30.0028
Gaus Width: 3.0210
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

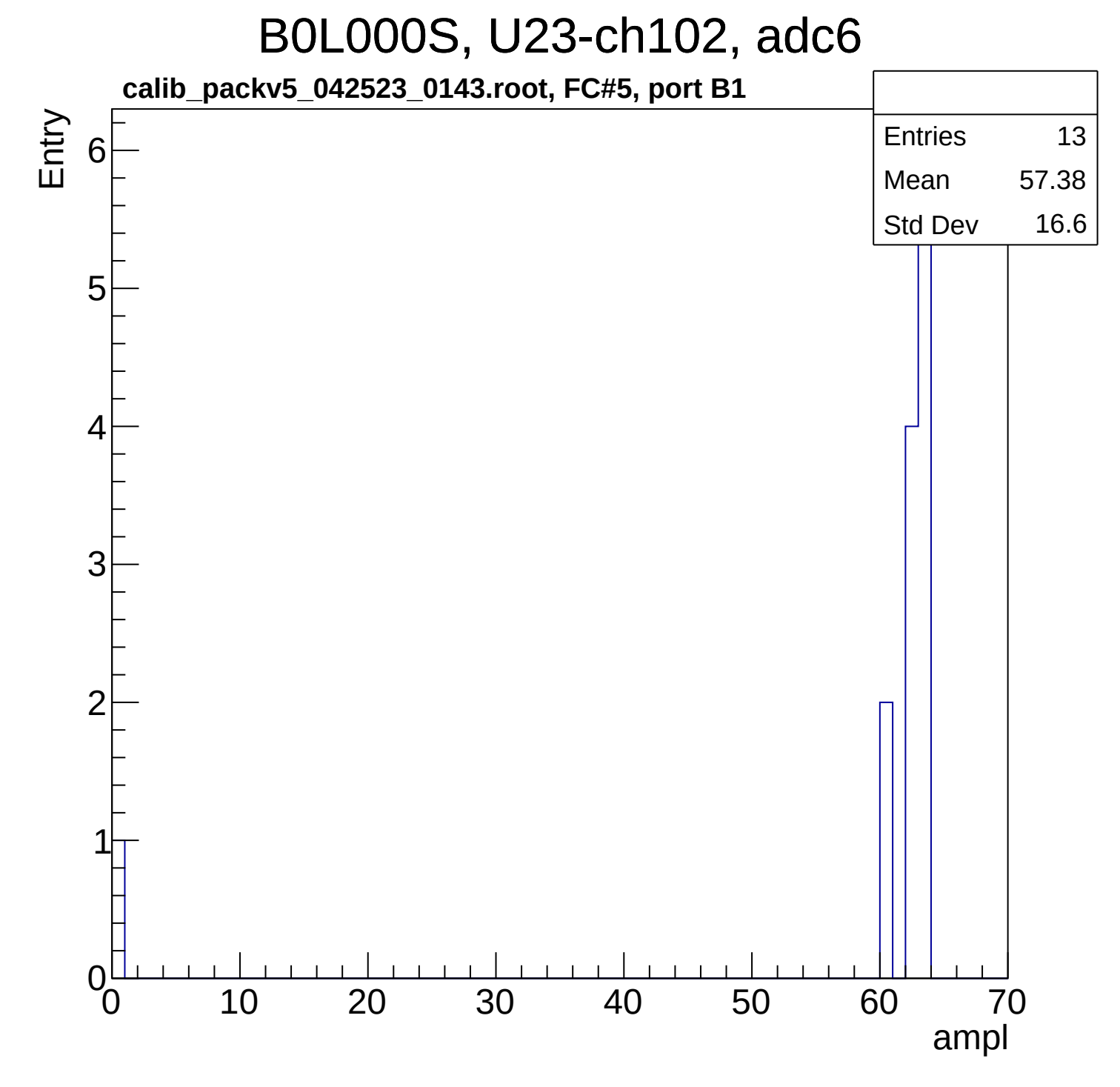

B0L000S, U23-ch102, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.88
Std Dev 2.972
Gaus mean : 36.1765
Gaus Width: 3.0598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

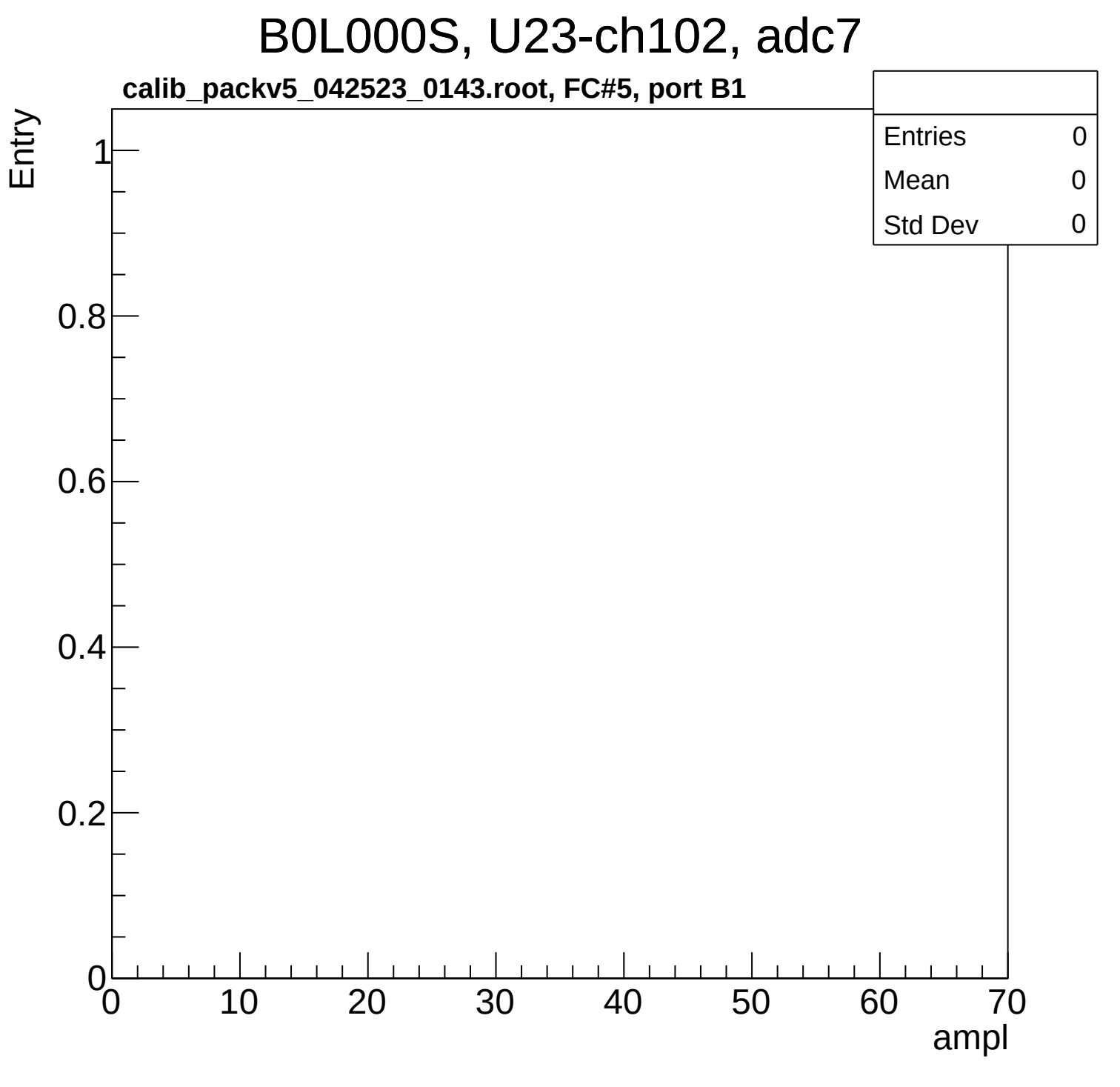

B0L000S, U23-ch102, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42
Std Dev 3.29
Gaus mean : 42.8381
Gaus Width: 3.4518
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

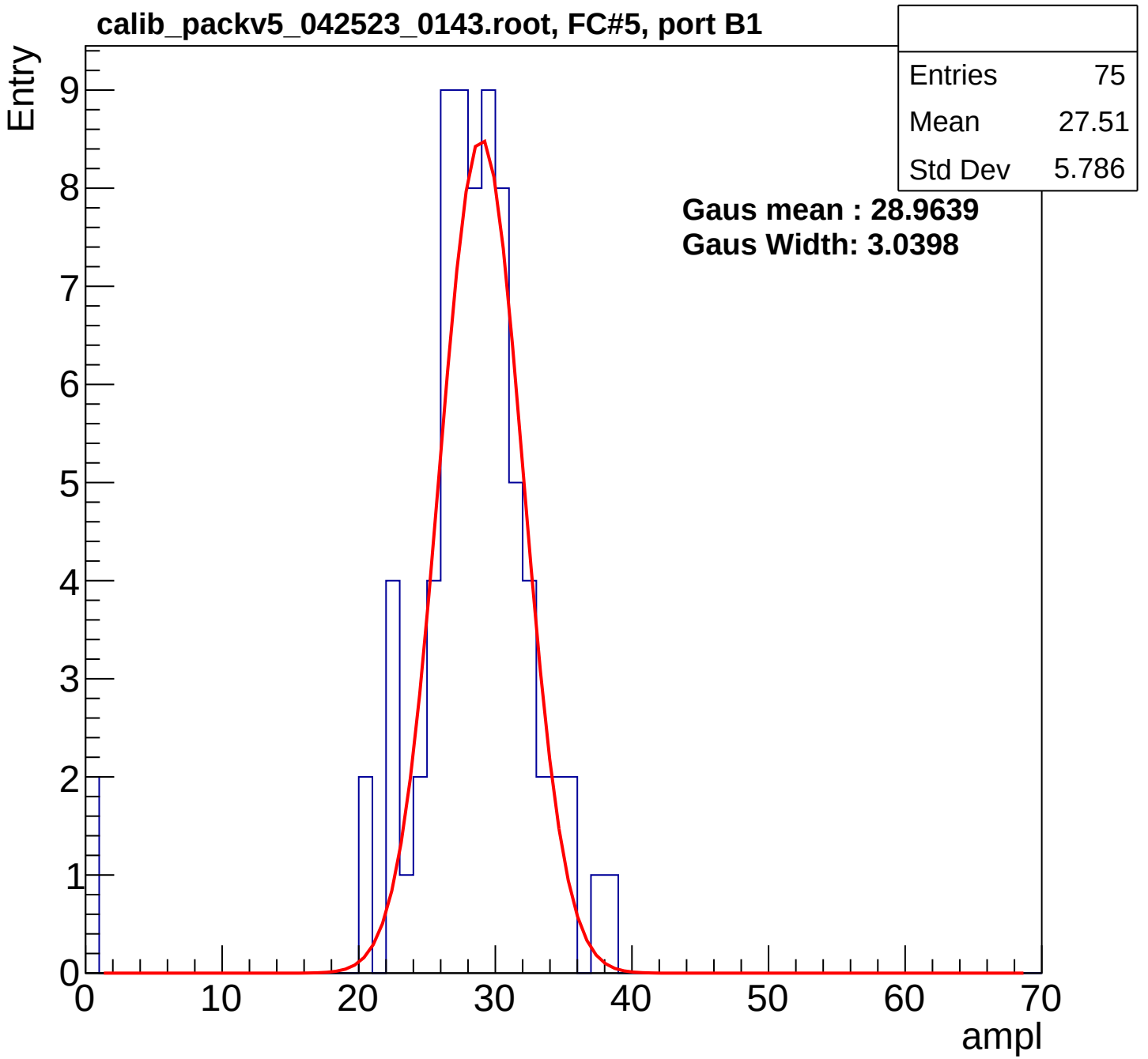

B0L000S, U23-ch102, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.52
Std Dev 3.34
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

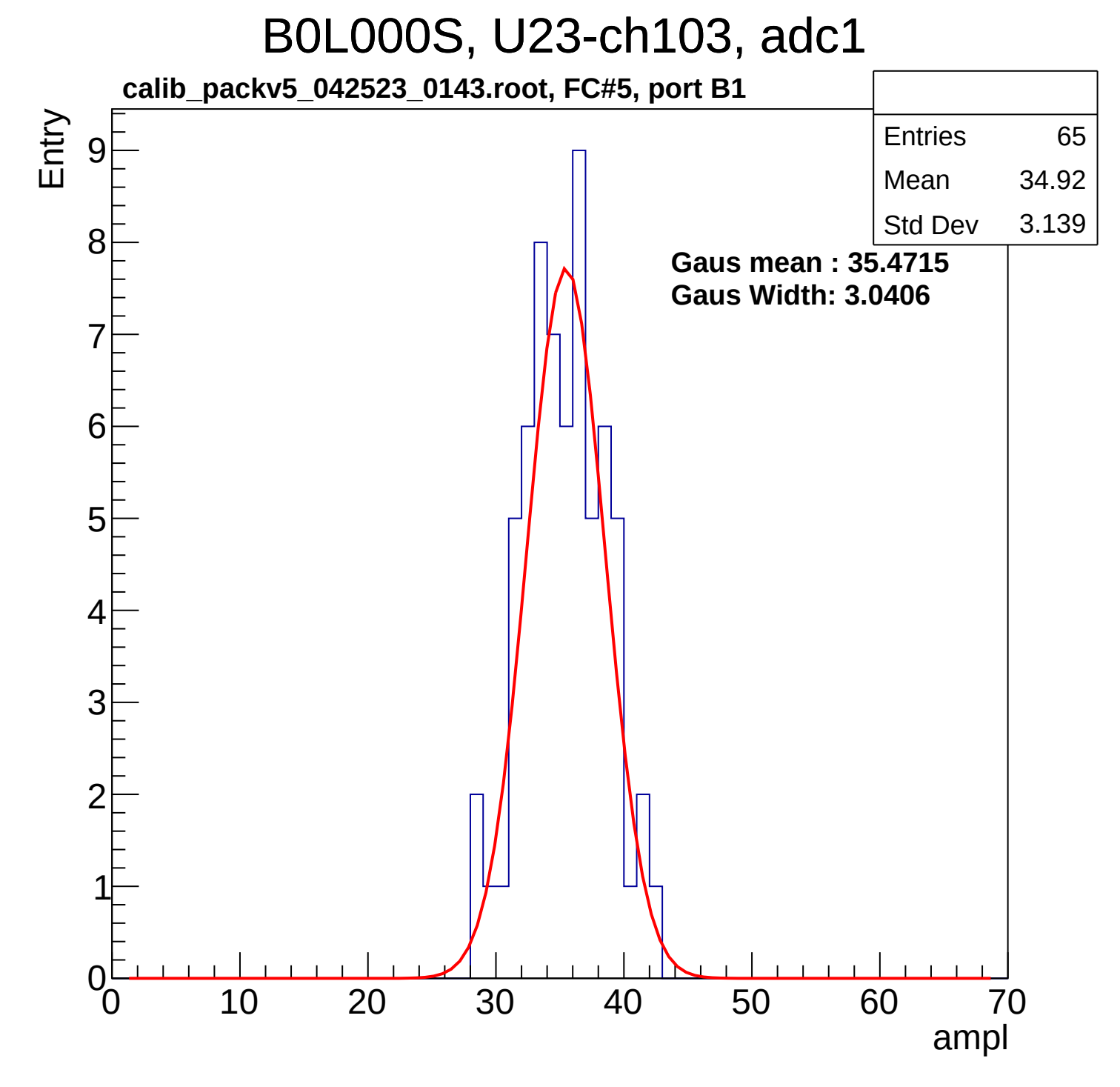

B0L000S, U23-ch102, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 54.94
Std Dev 3.372
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

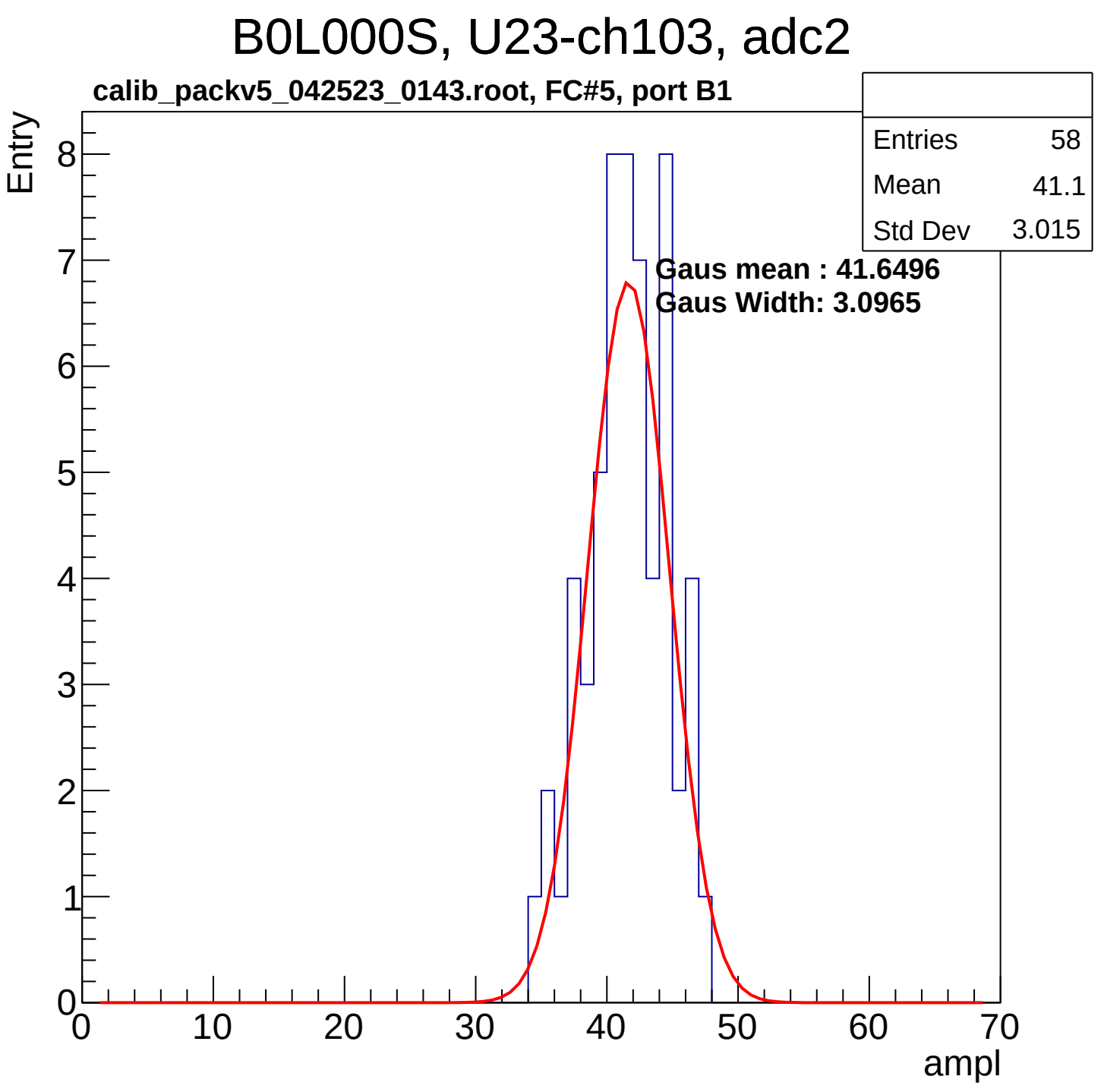

B0L000S, U23-ch102, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.26
Std Dev 2.616
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

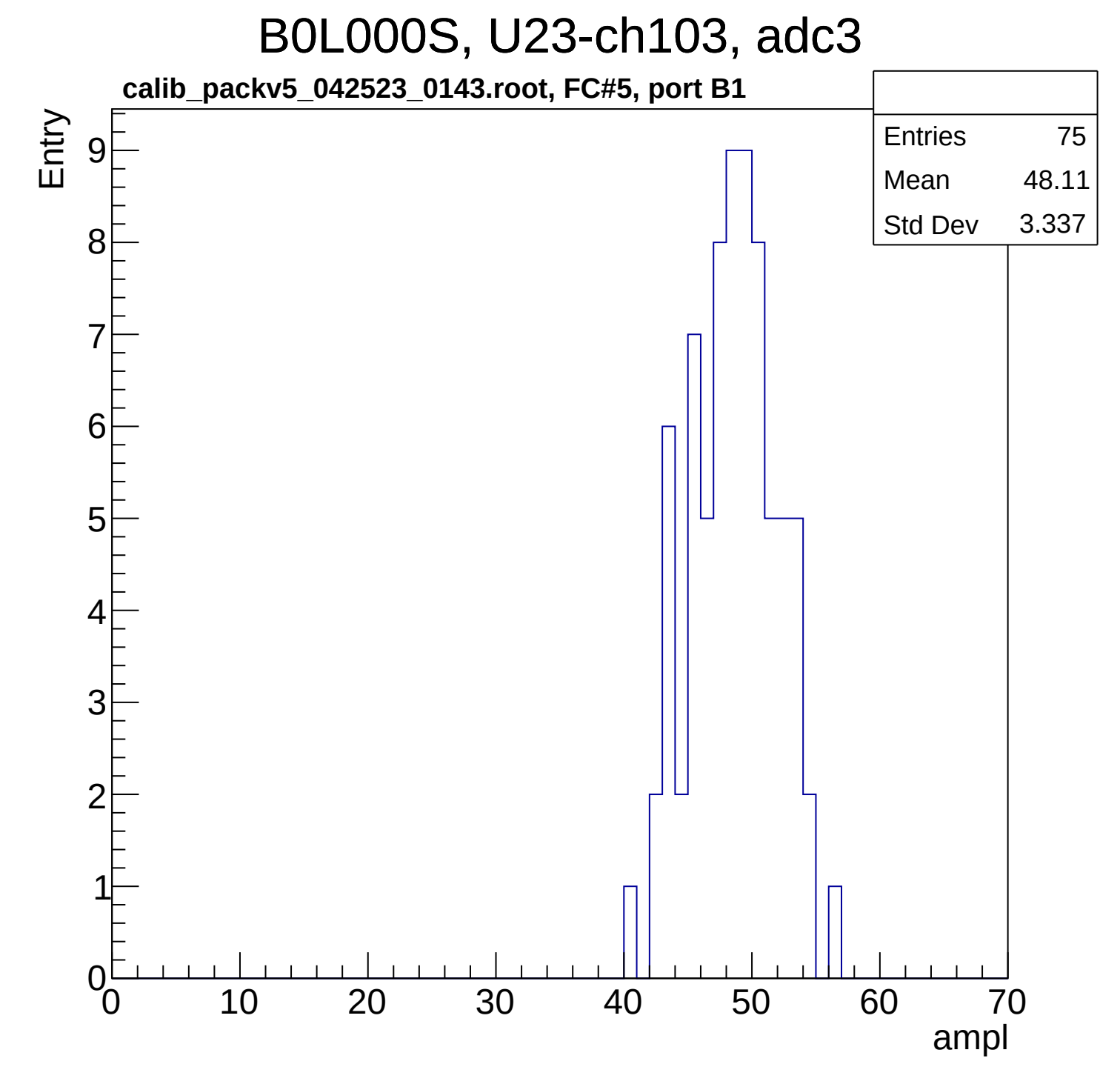

B0L000S, U23-ch102, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.38
Std Dev 16.6
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

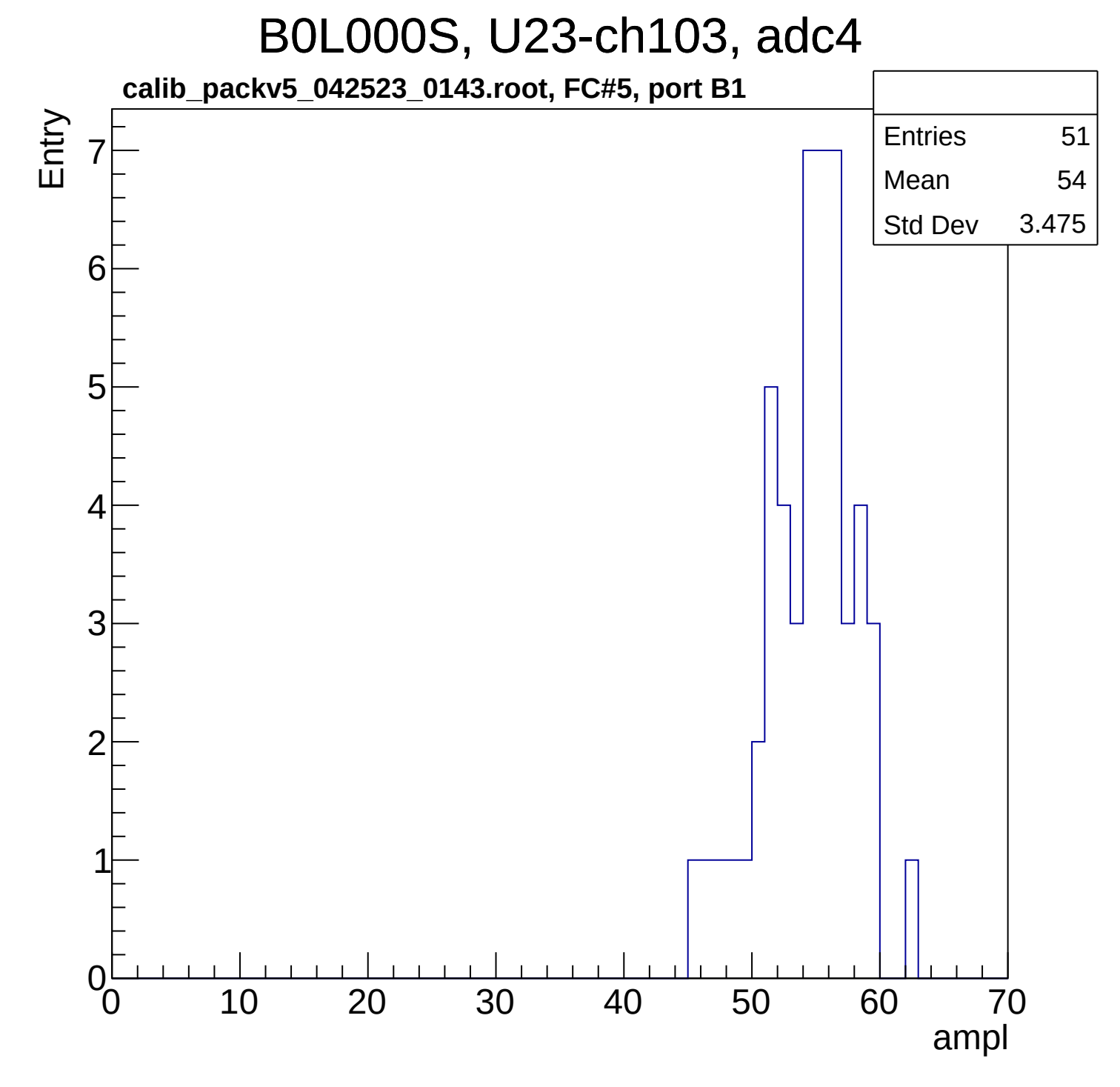

B0L000S, U23-ch102, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

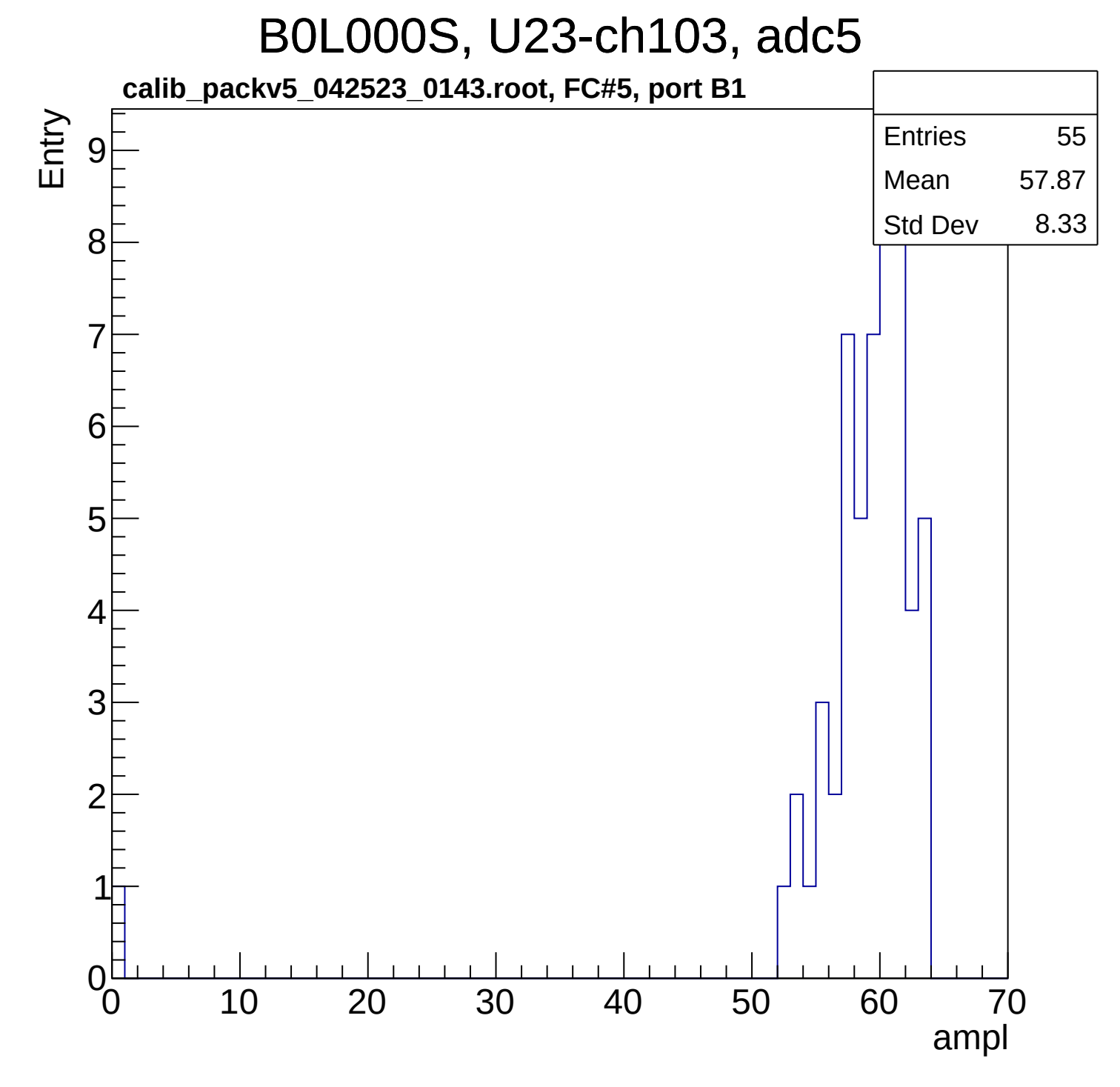

B0L000S, U23-ch103, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 27.51
Std Dev 5.786
Gaus mean : 28.9639
Gaus Width: 3.0398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

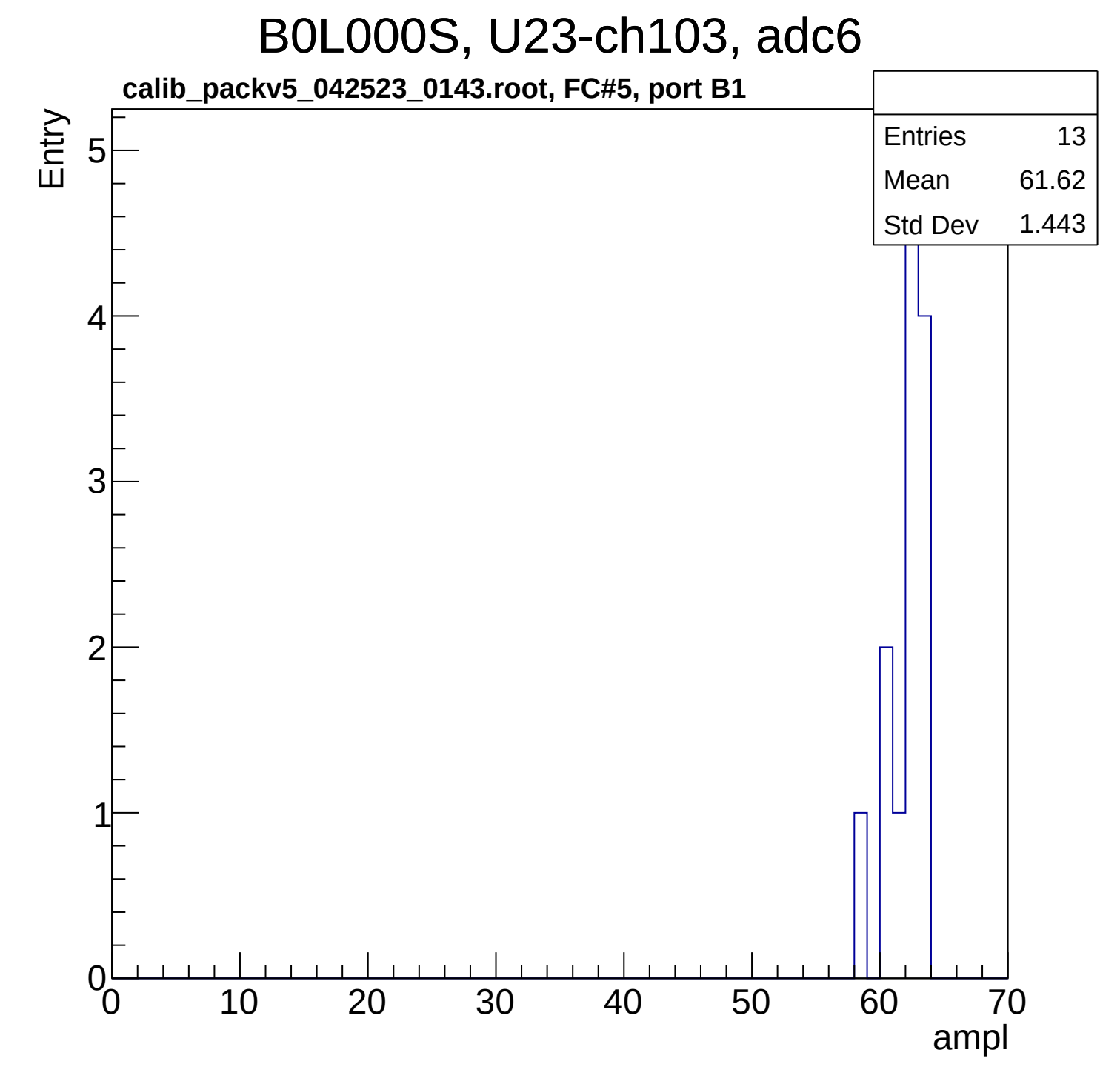

B0L000S, U23-ch103, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 34.92
Std Dev 3.139
Gaus mean : 35.4715
Gaus Width: 3.0406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

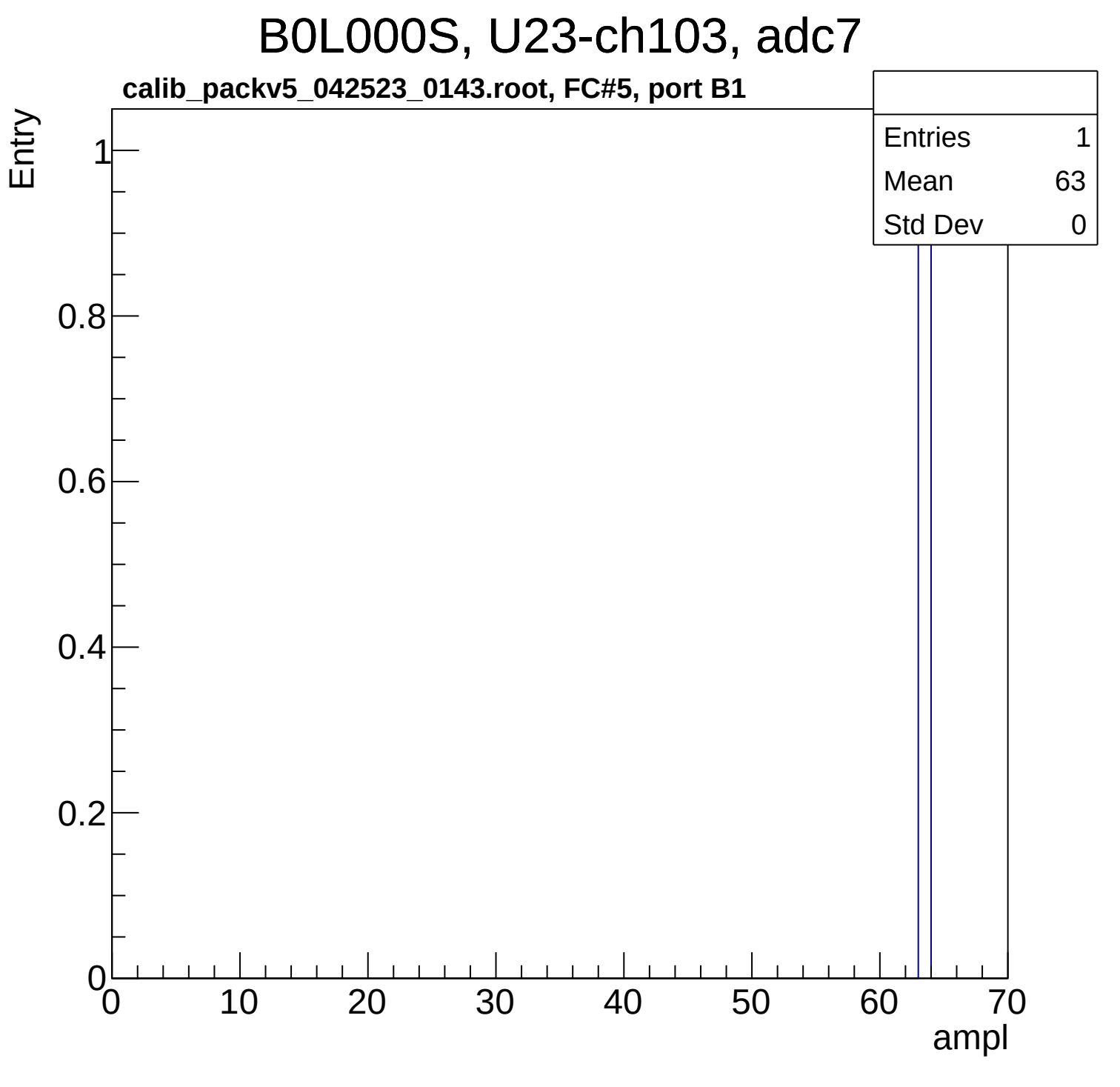

B0L000S, U23-ch103, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 41.1
Std Dev 3.015
Gaus mean : 41.6496
Gaus Width: 3.0965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

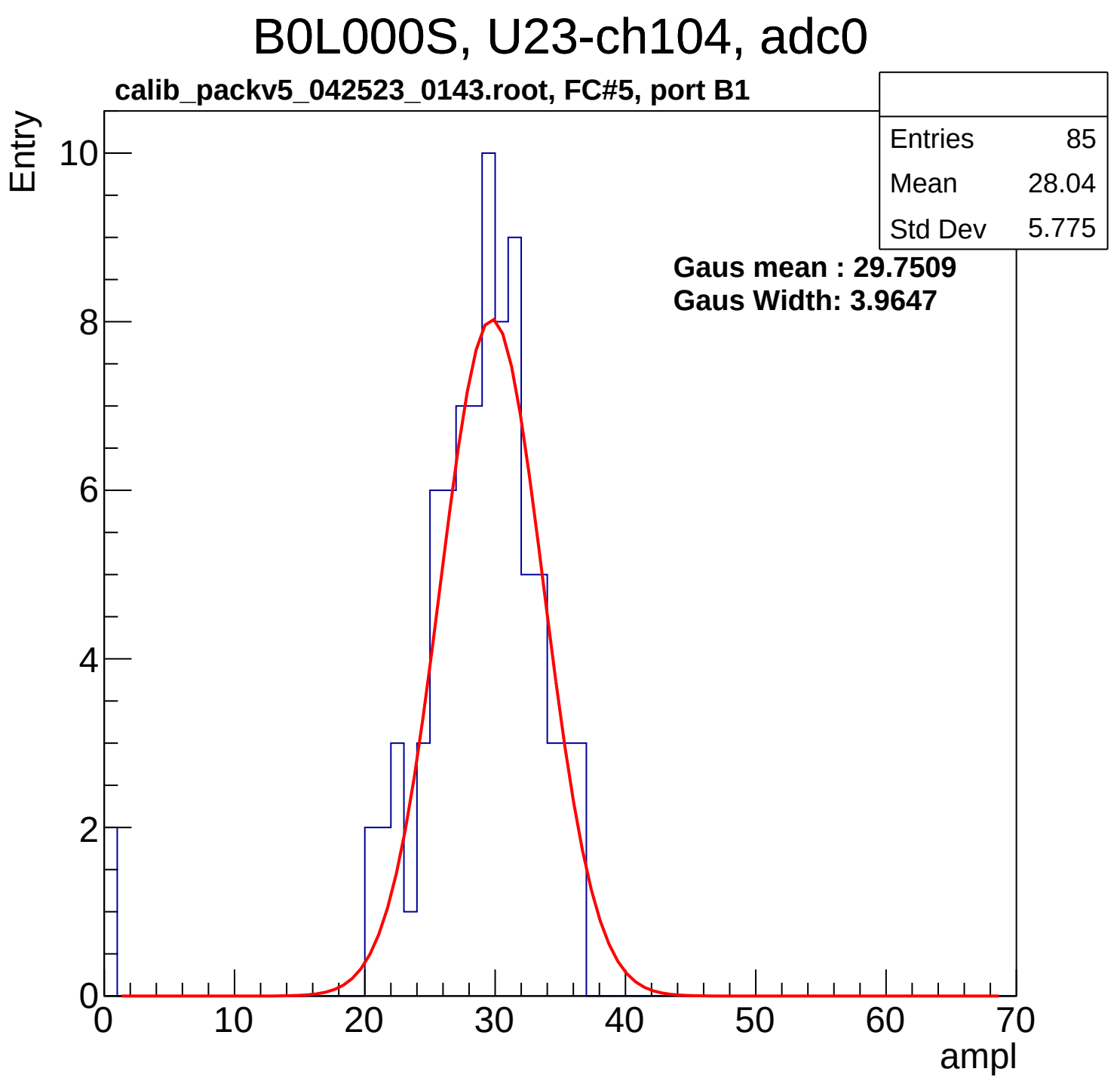

B0L000S, U23-ch103, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.11
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

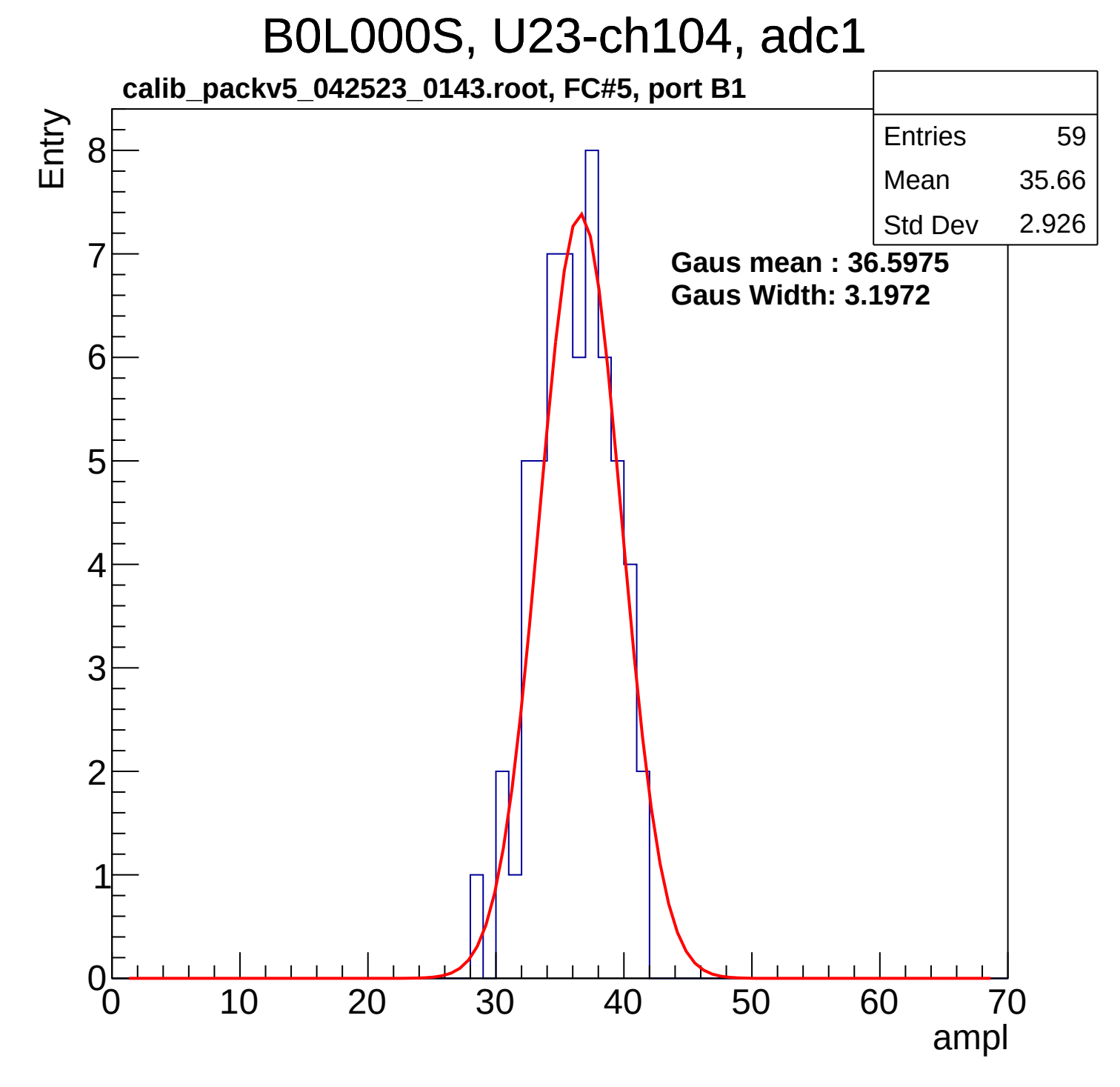

B0L000S, U23-ch103, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54
Std Dev 3.475
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

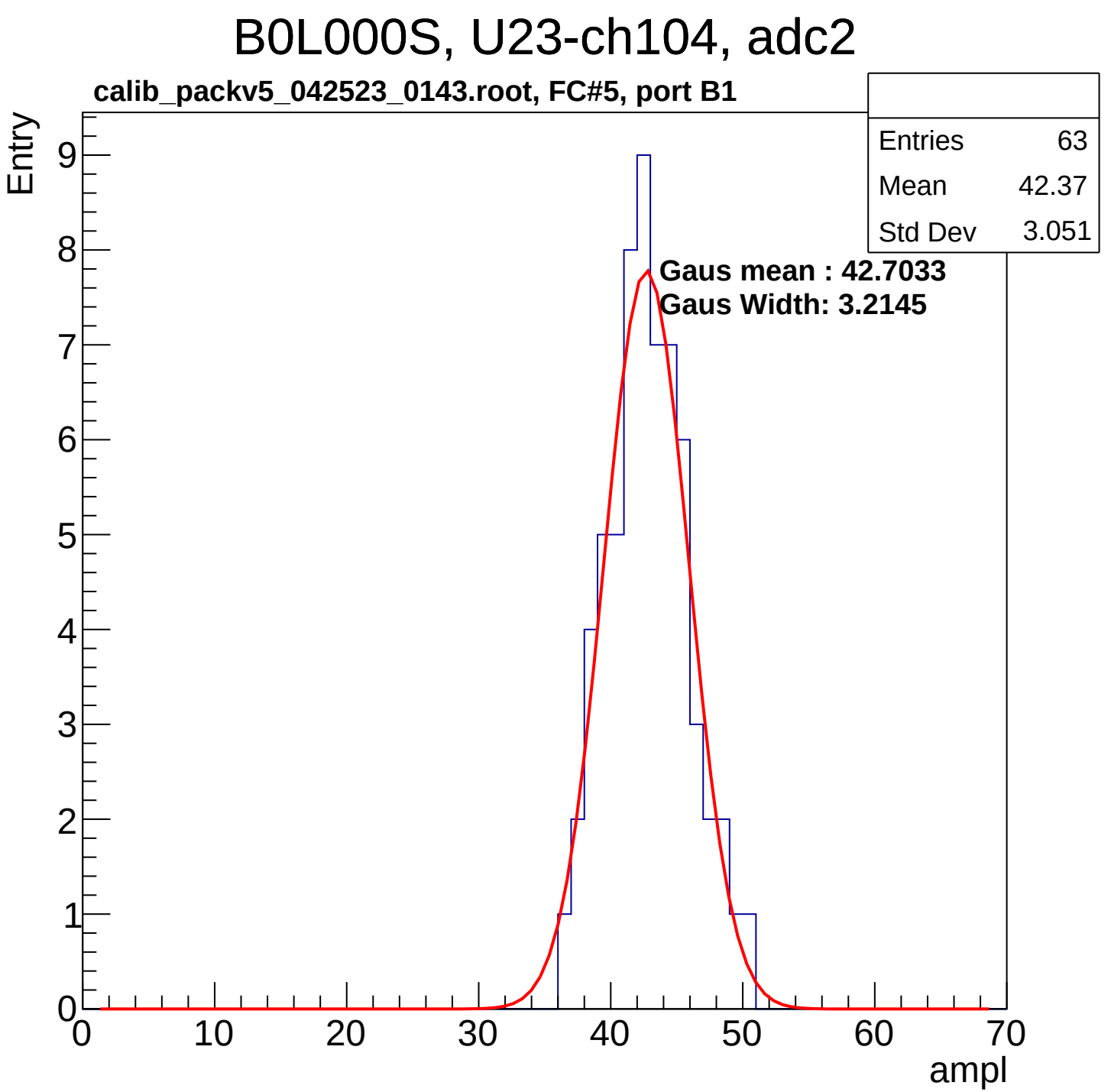

B0L000S, U23-ch103, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.87
Std Dev 8.33
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

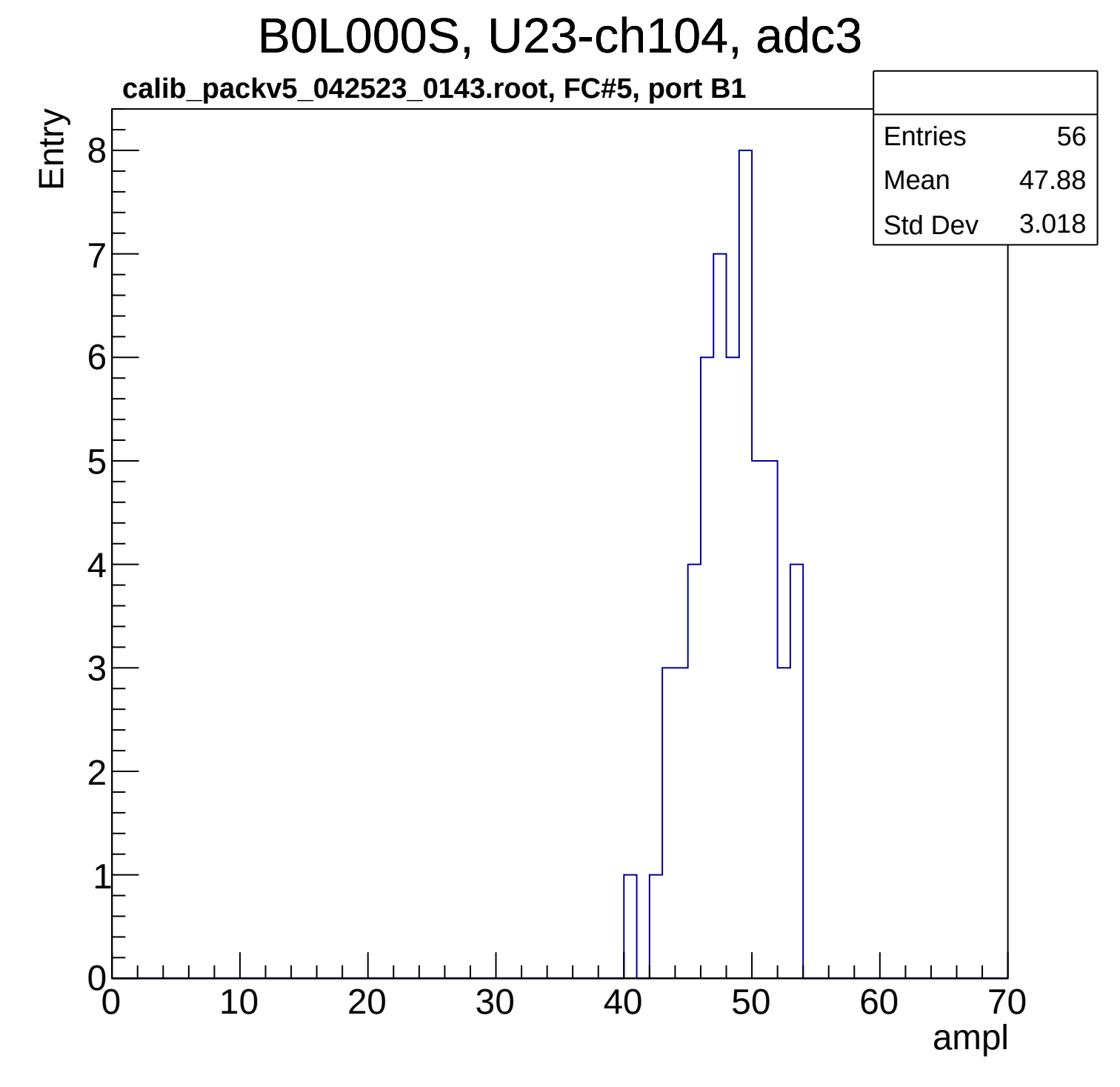

B0L000S, U23-ch103, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.62
Std Dev 1.443
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

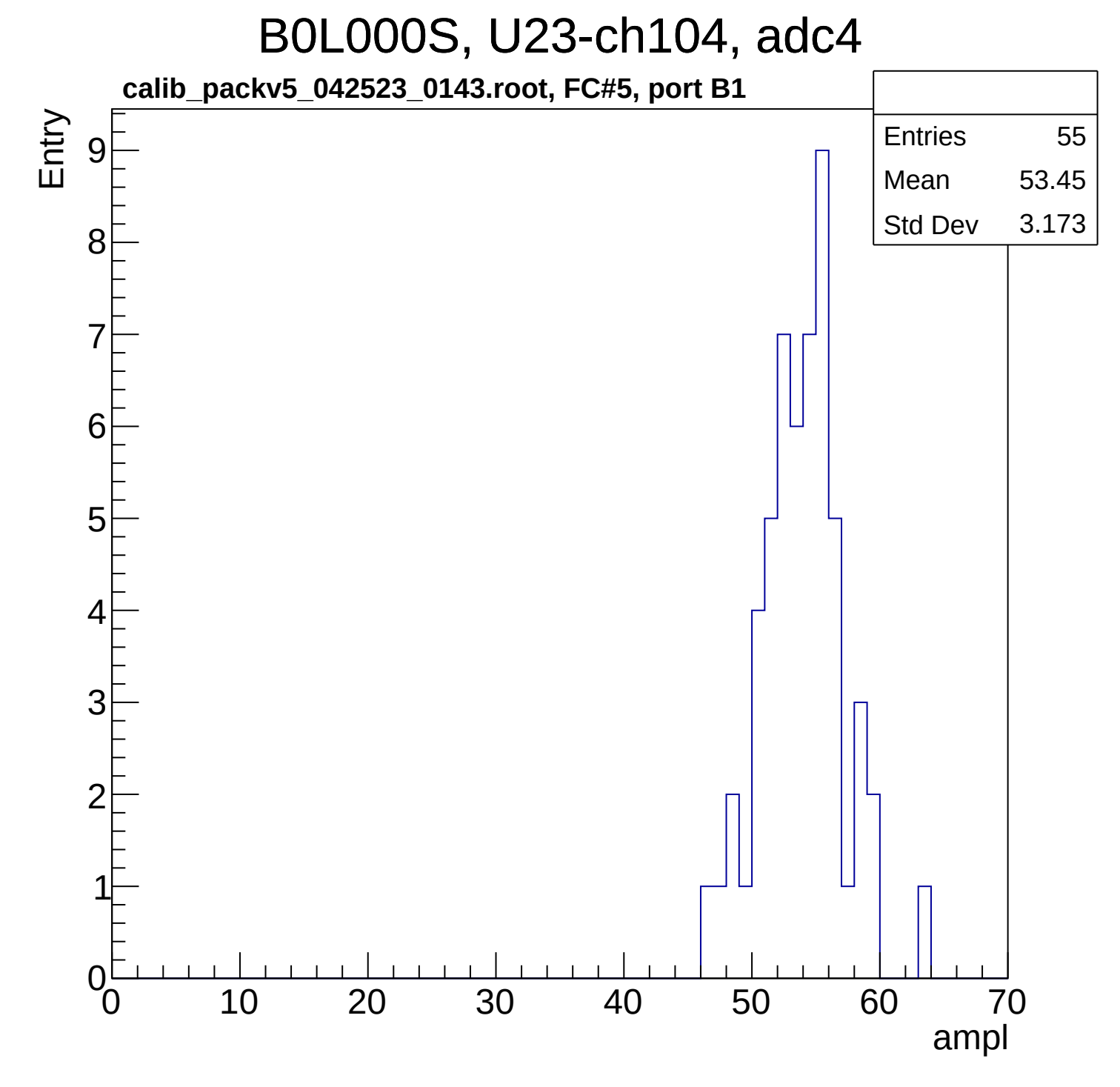

B0L000S, U23-ch103, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

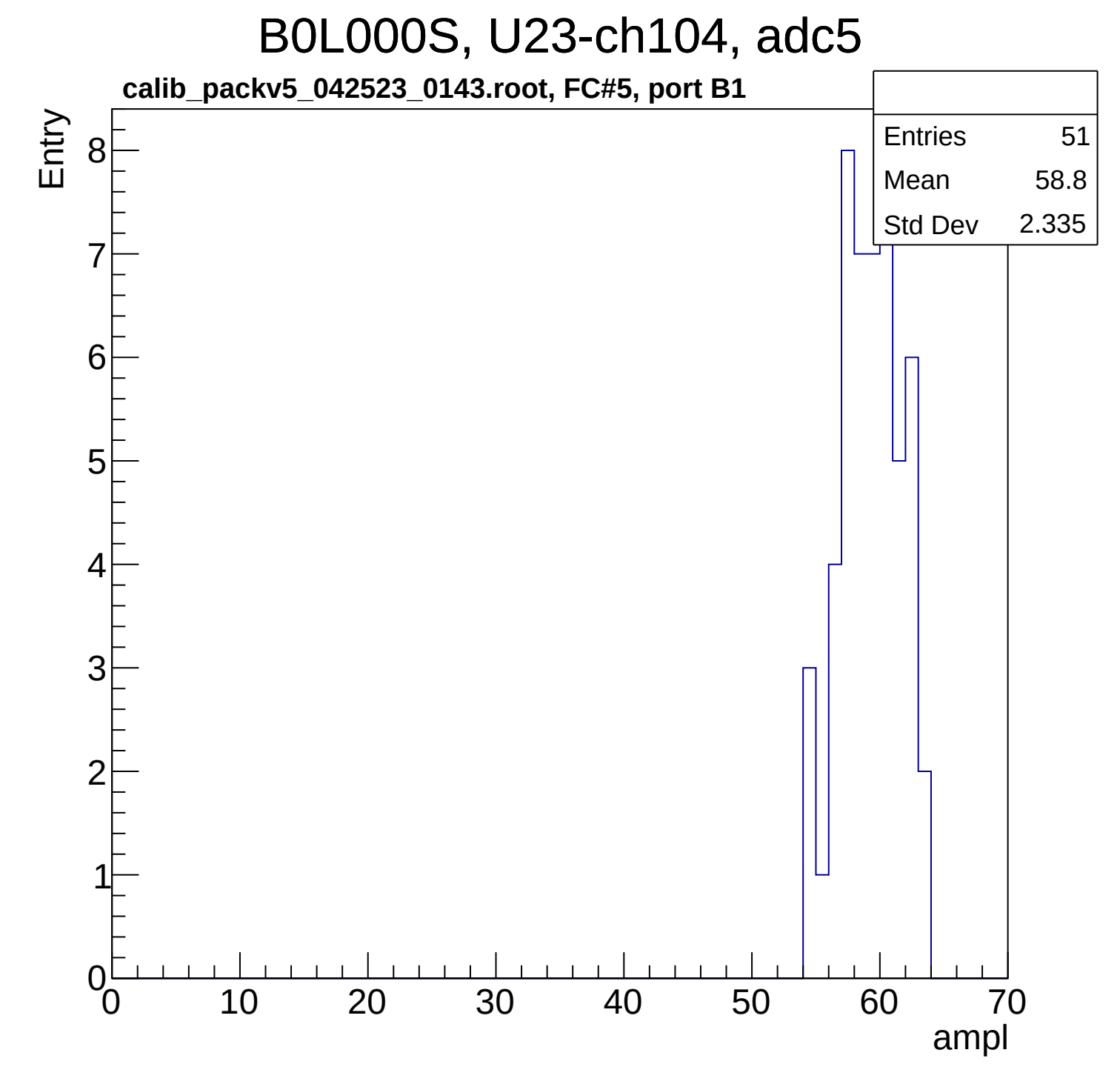

B0L000S, U23-ch104, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 28.04
Std Dev 5.775
Gaus mean : 29.7509
Gaus Width: 3.9647
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

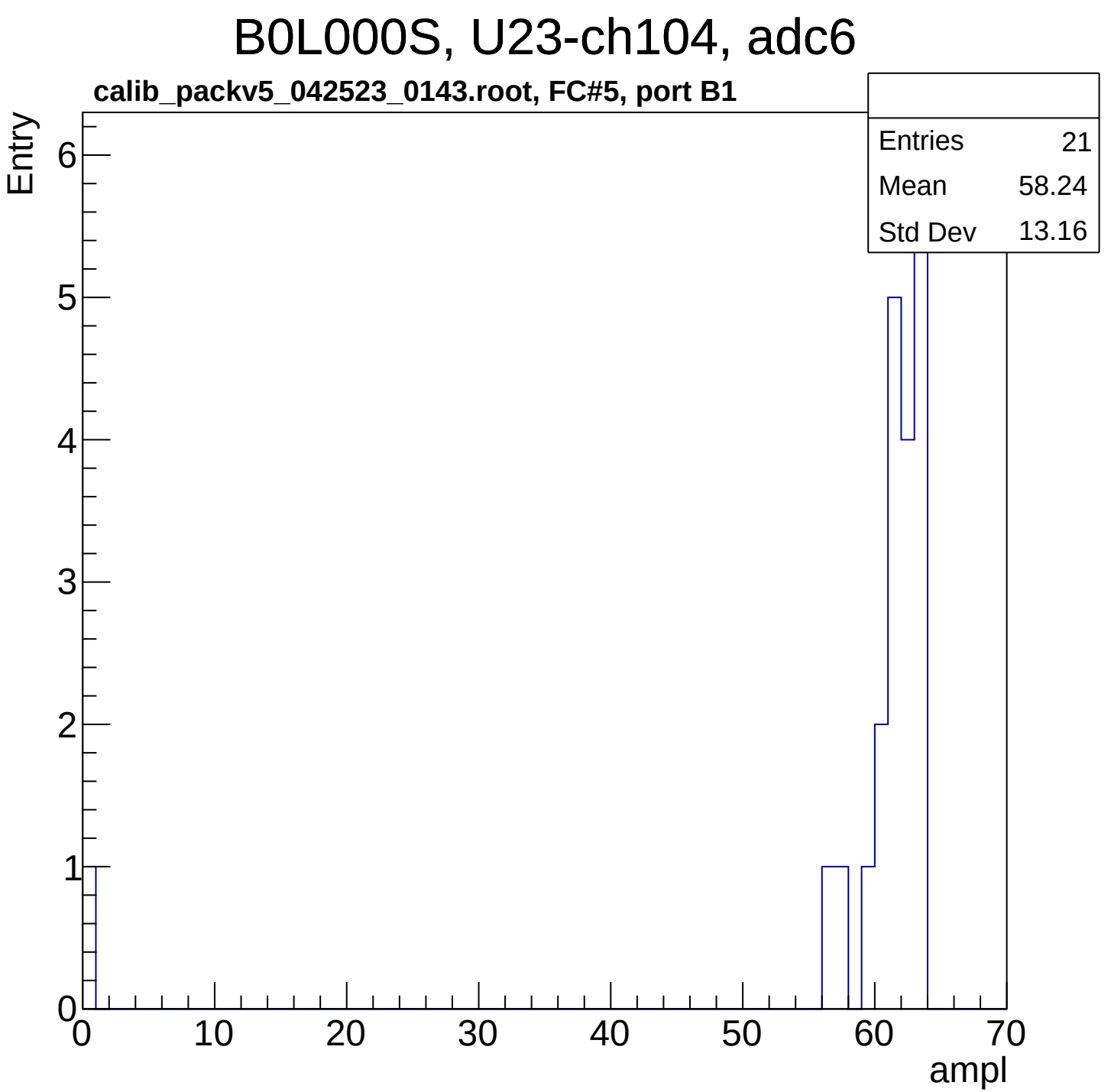

B0L000S, U23-ch104, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.66
Std Dev 2.926
Gaus mean : 36.5975
Gaus Width: 3.1972
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

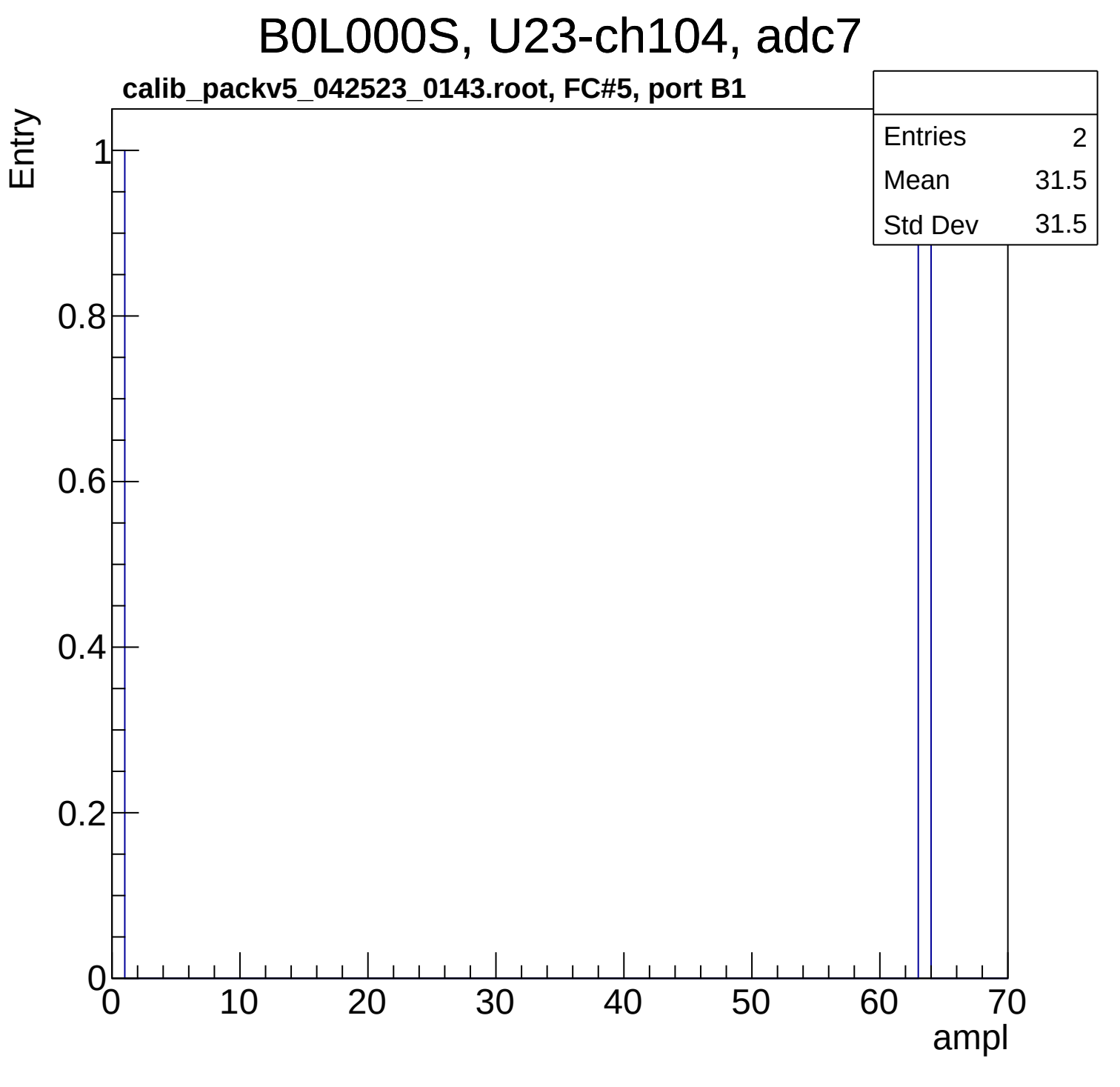

B0L000S, U23-ch104, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.37
Std Dev 3.051
Gaus mean : 42.7033
Gaus Width: 3.2145
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

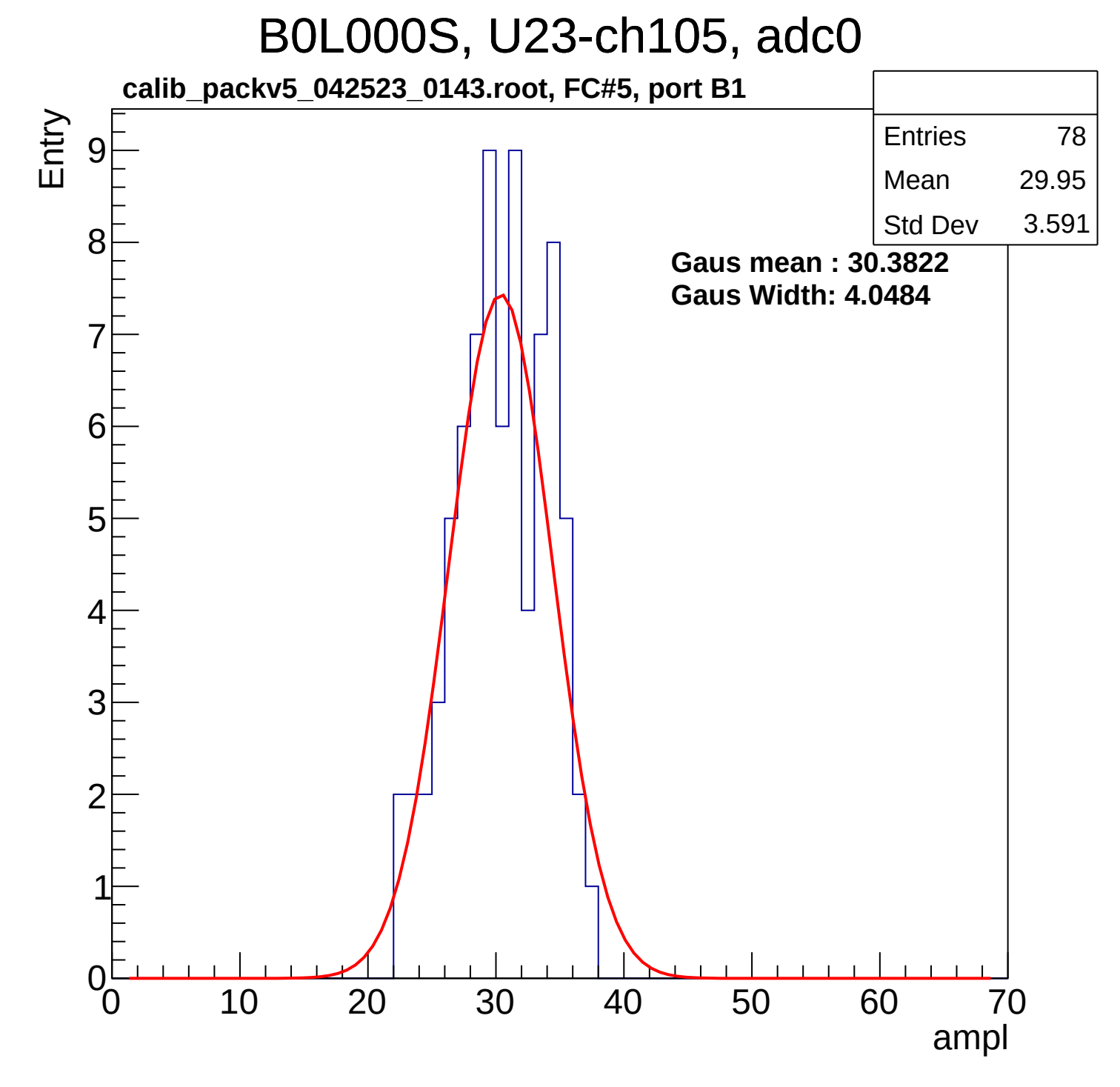

B0L000S, U23-ch104, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 47.88
Std Dev 3.018
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

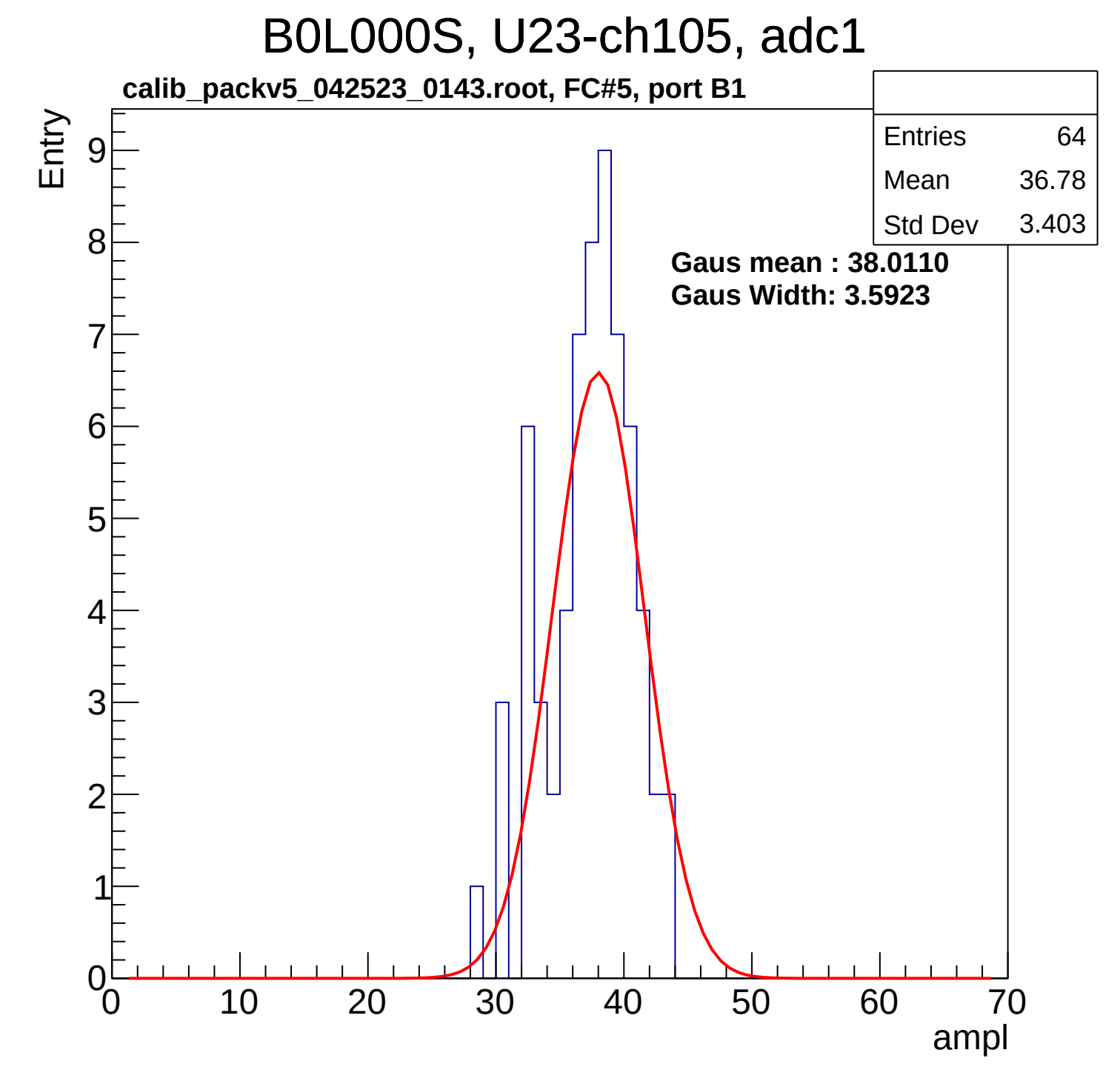

B0L000S, U23-ch104, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 53.45
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

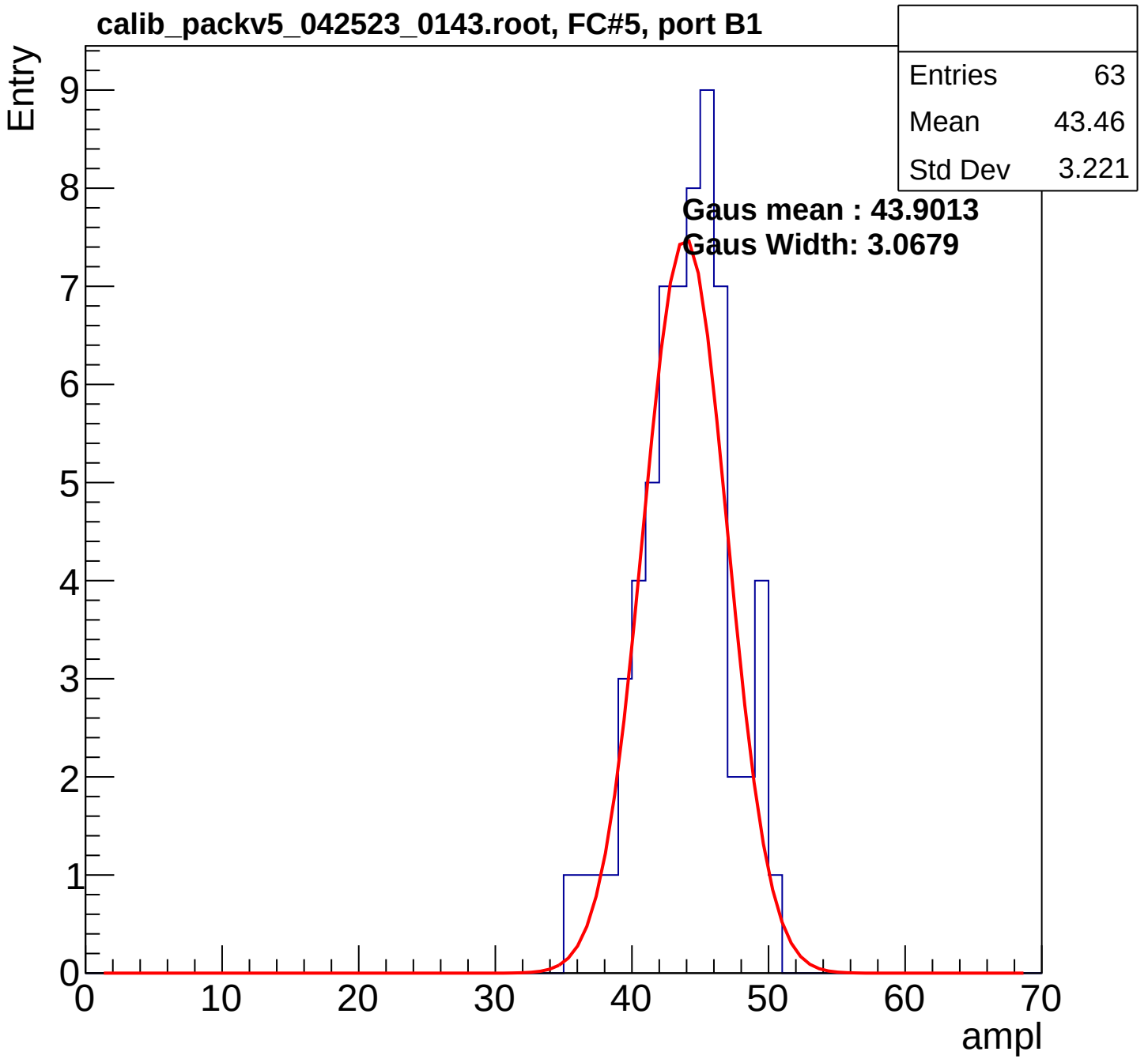

B0L000S, U23-ch104, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.8
Std Dev 2.335
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

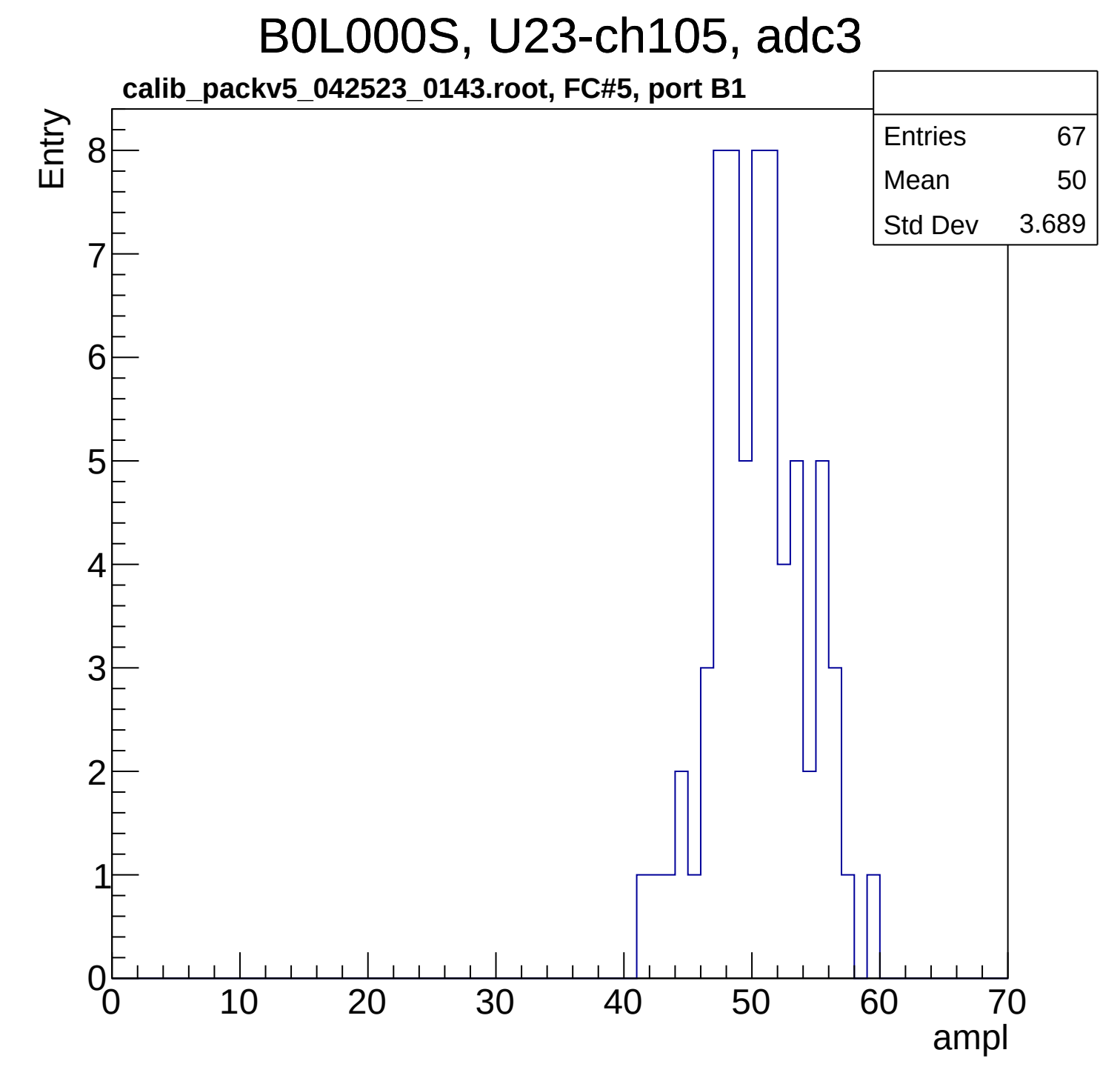

B0L000S, U23-ch104, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.24
Std Dev 13.16
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

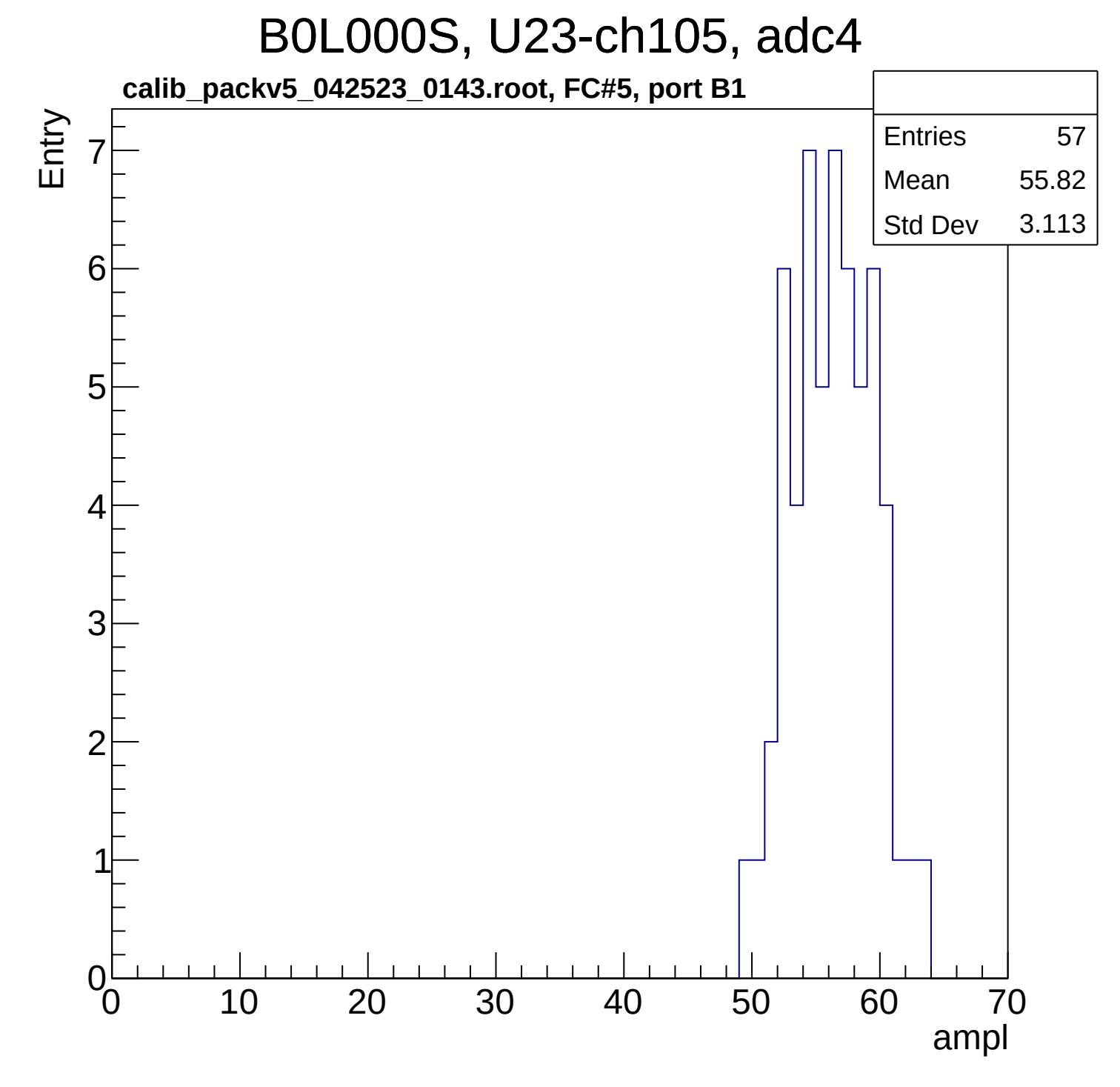

B0L000S, U23-ch104, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

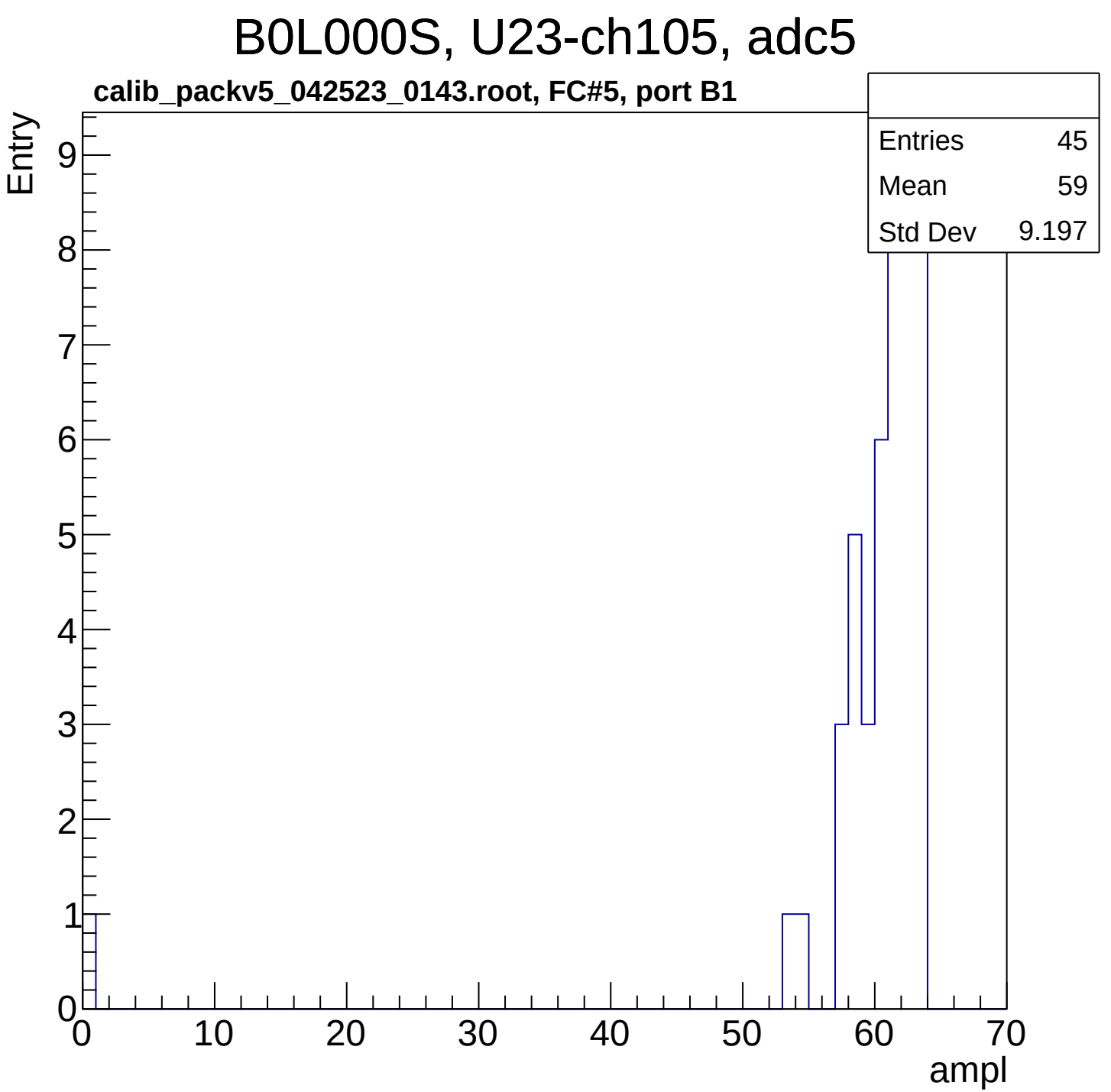

B0L000S, U23-ch105, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 29.95
Std Dev 3.591
Gaus mean : 30.3822
Gaus Width: 4.0484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

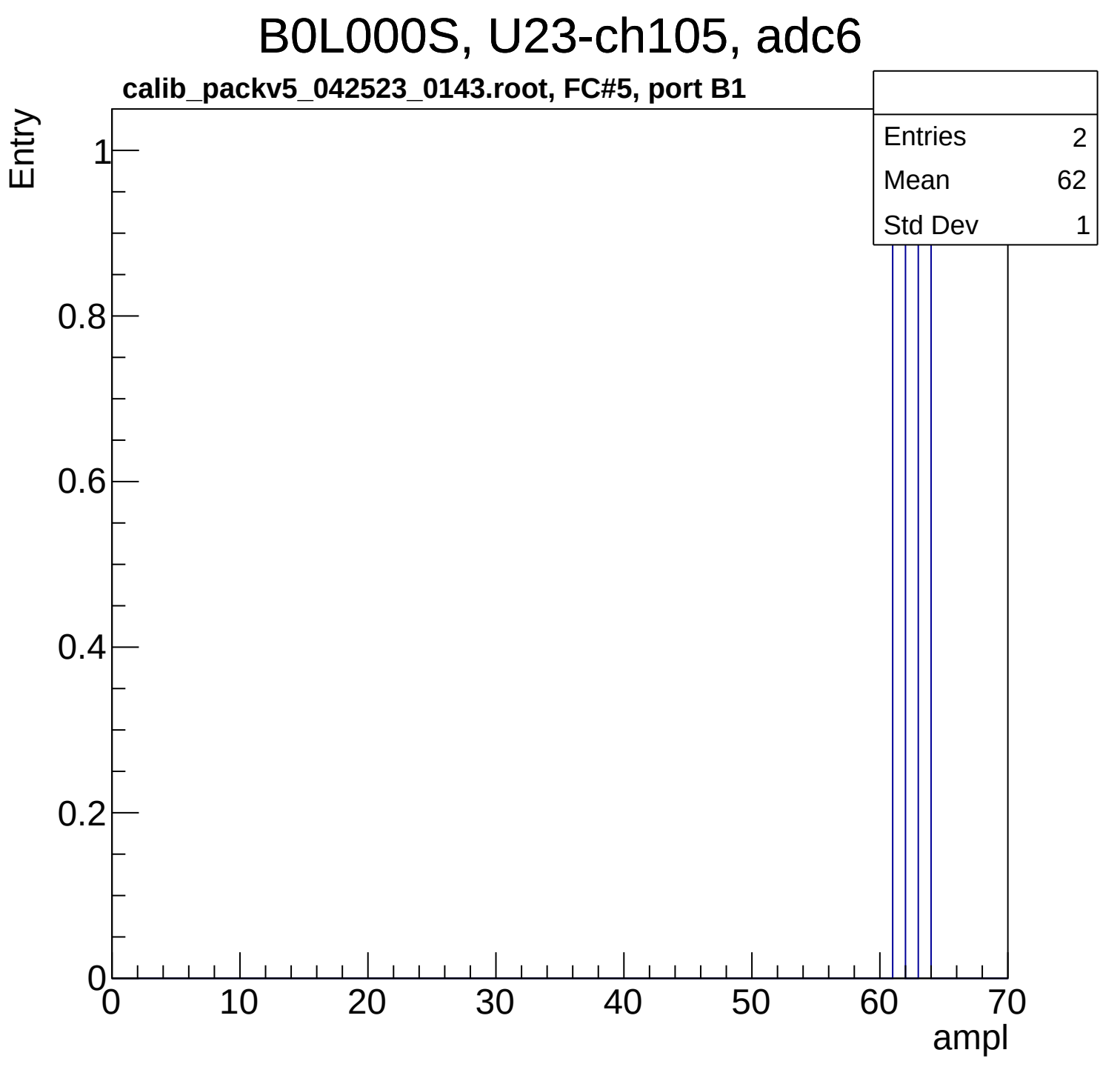

B0L000S, U23-ch105, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 36.78
Std Dev 3.403
Gaus mean : 38.0110
Gaus Width: 3.5923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

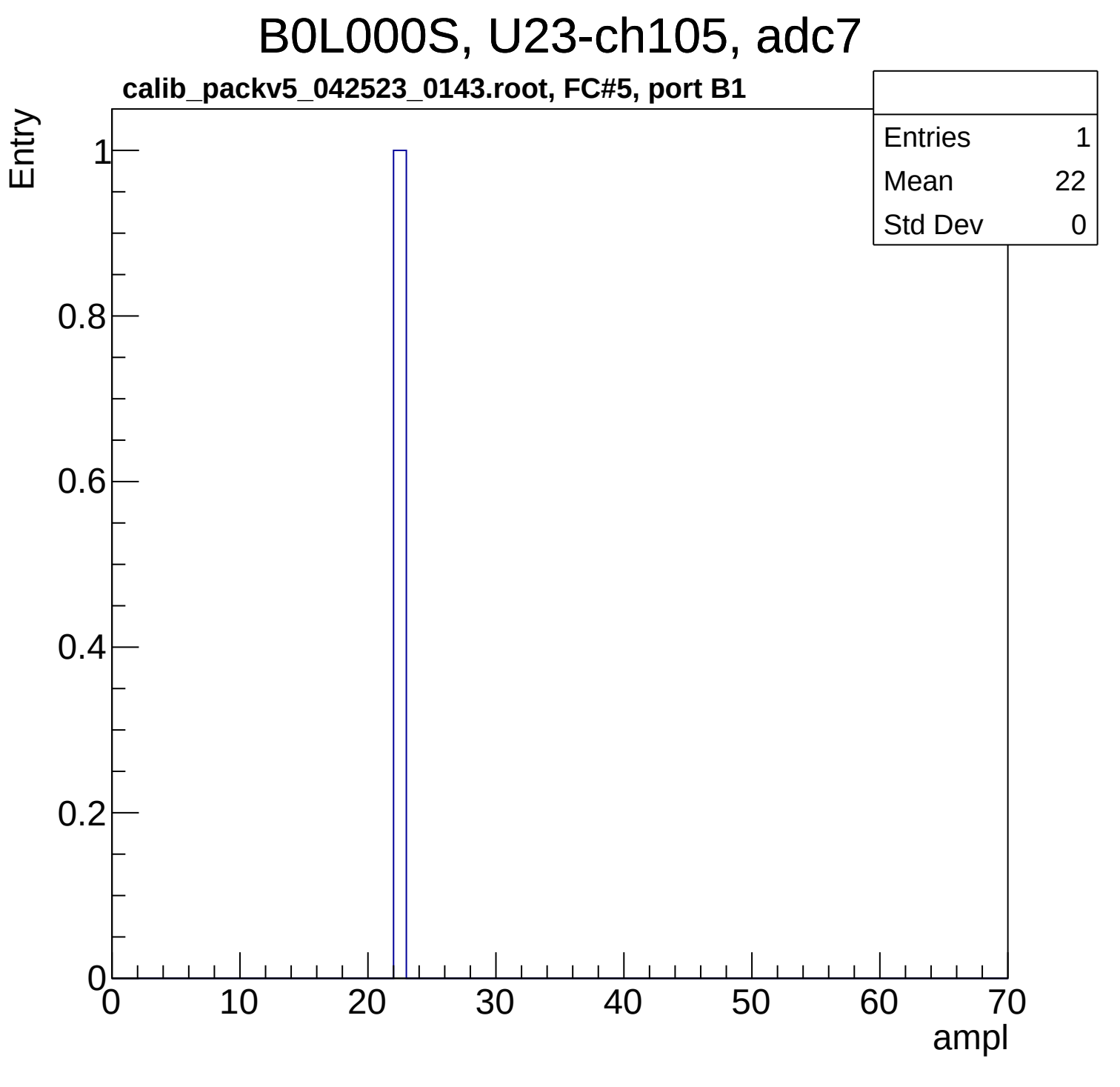

B0L000S, U23-ch105, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.46
Std Dev 3.221
Gaus mean : 43.9013
Gaus Width: 3.0679
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

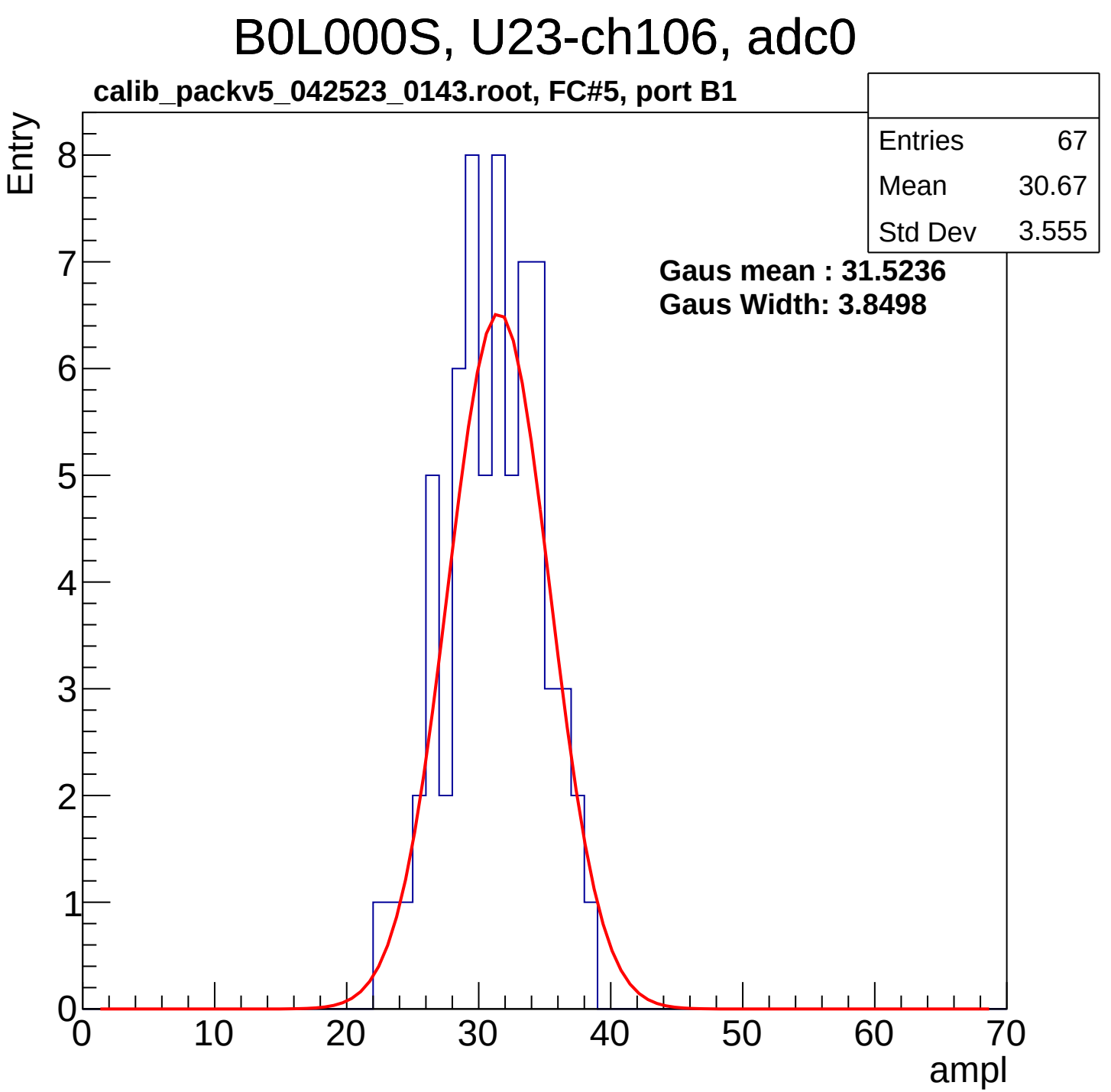

B0L000S, U23-ch105, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50
Std Dev 3.689
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

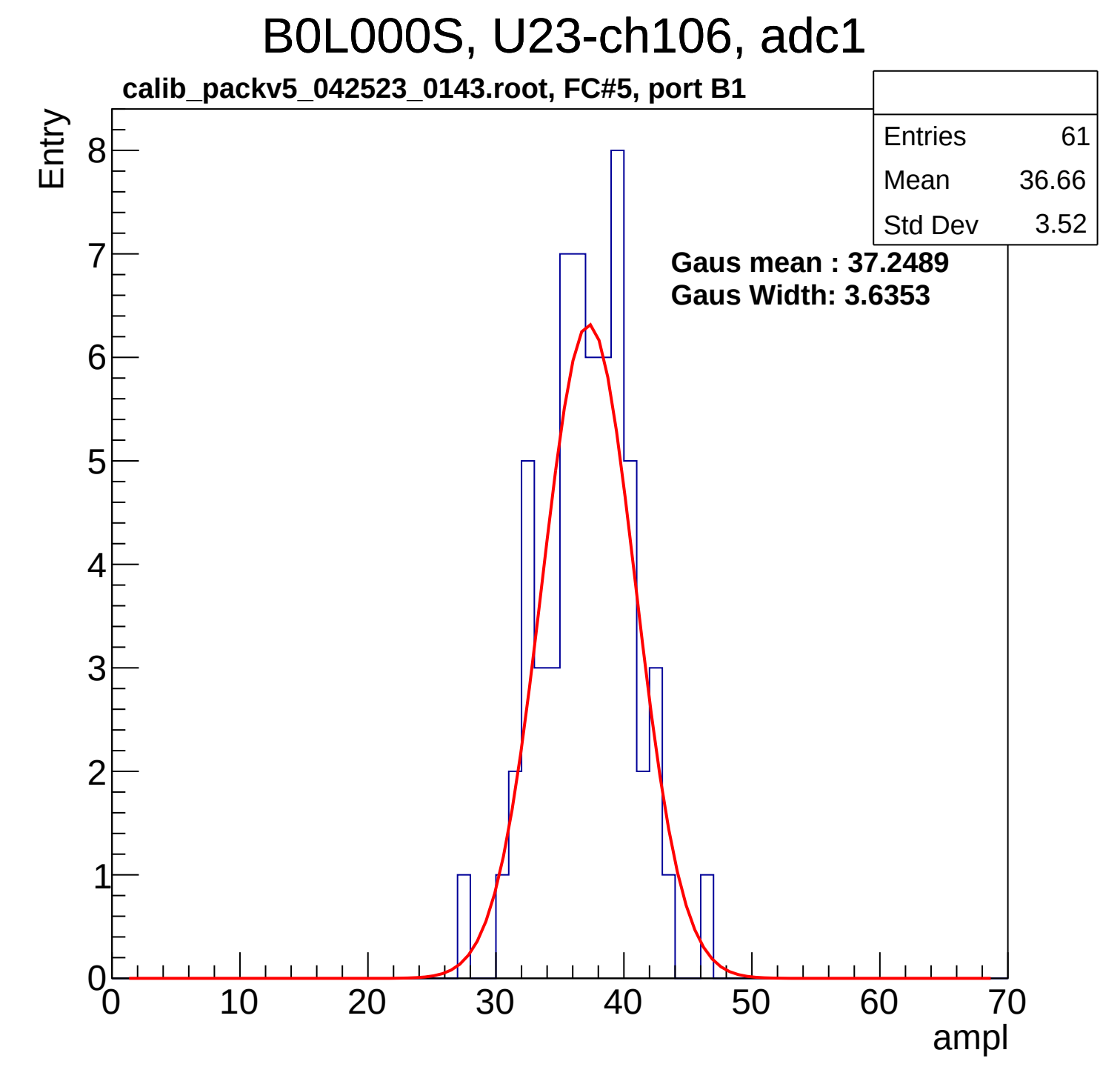

B0L000S, U23-ch105, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.82
Std Dev 3.113
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

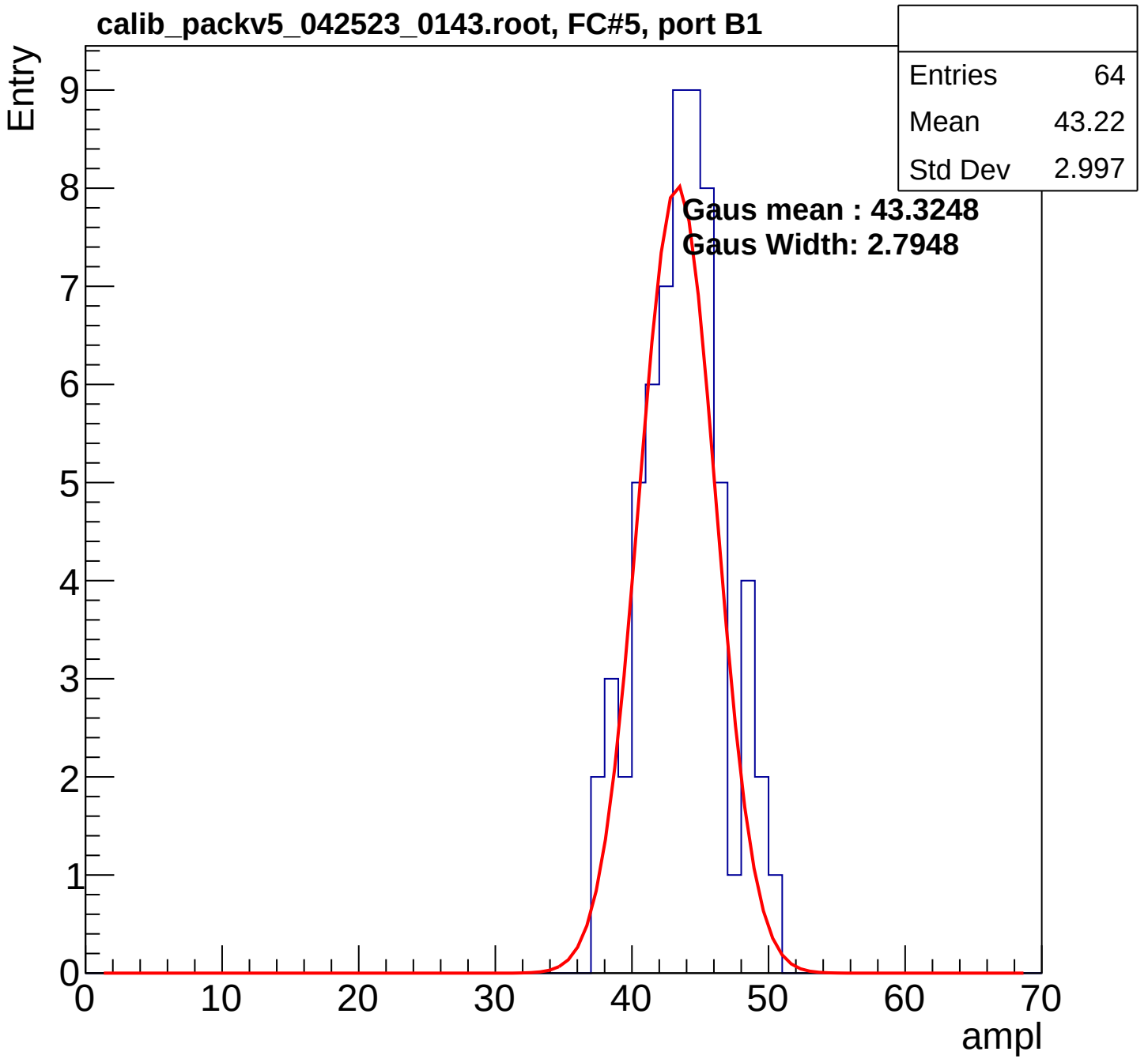

B0L000S, U23-ch105, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59
Std Dev 9.197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

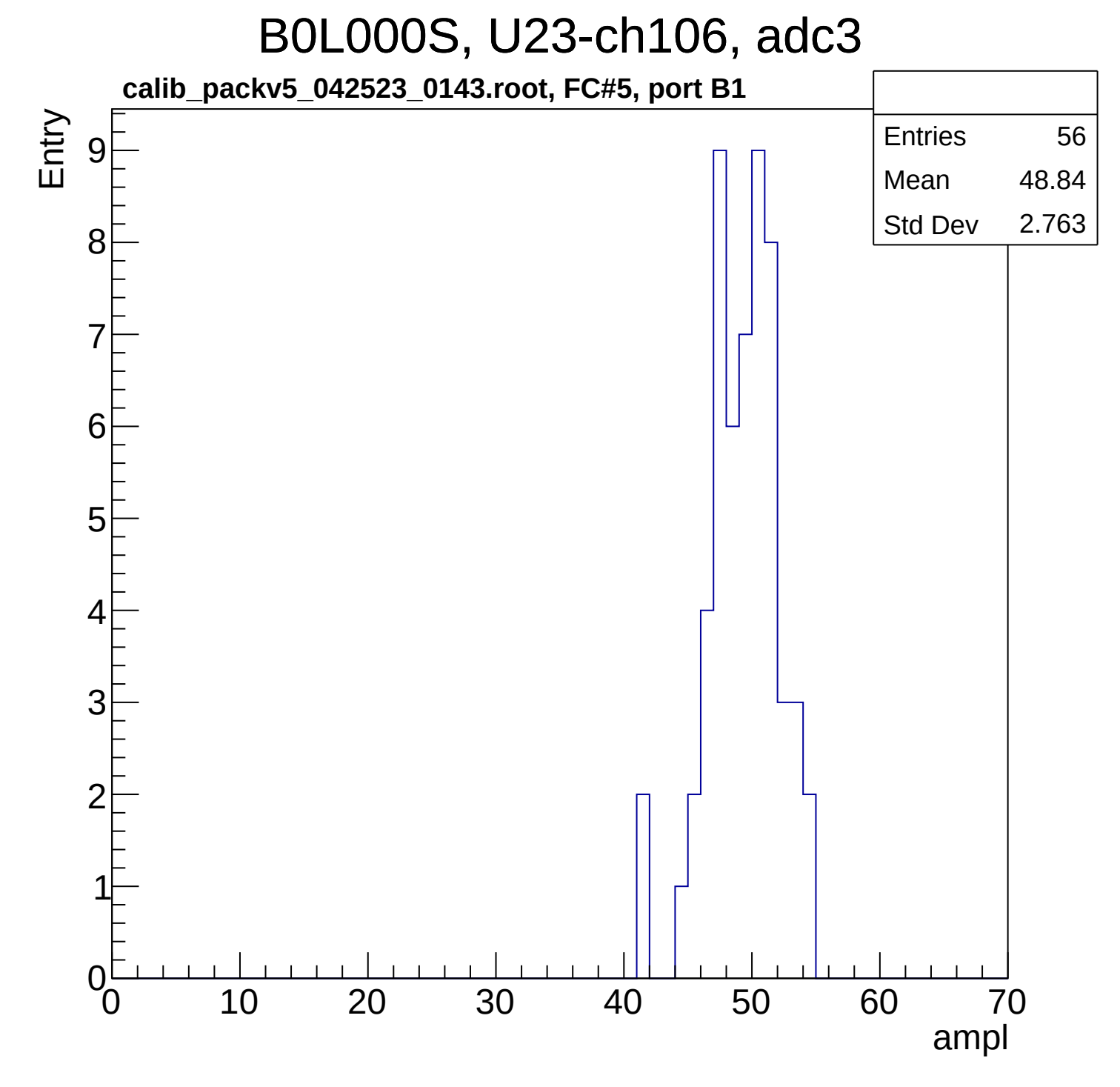

B0L000S, U23-ch105, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

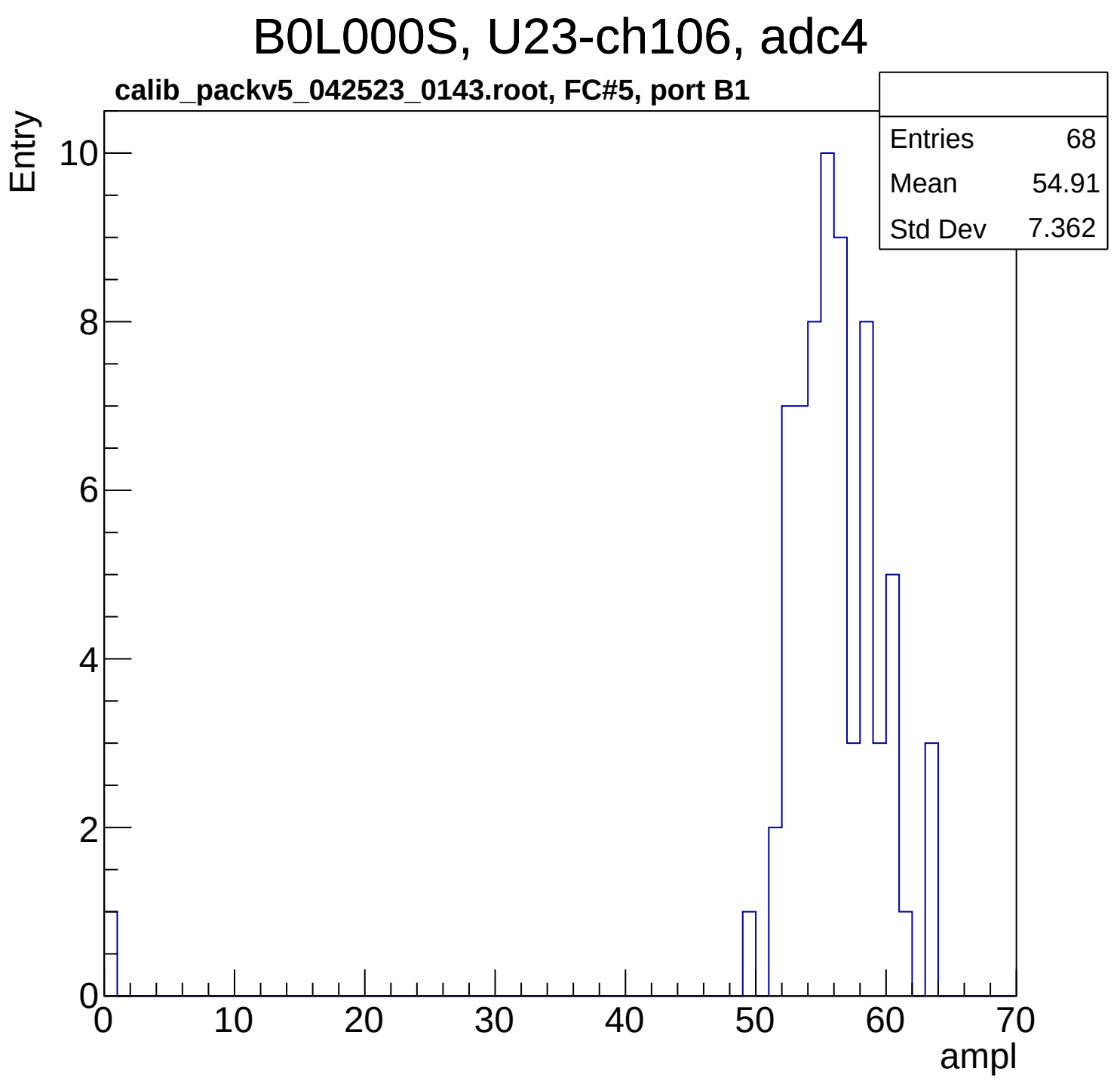

B0L000S, U23-ch105, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

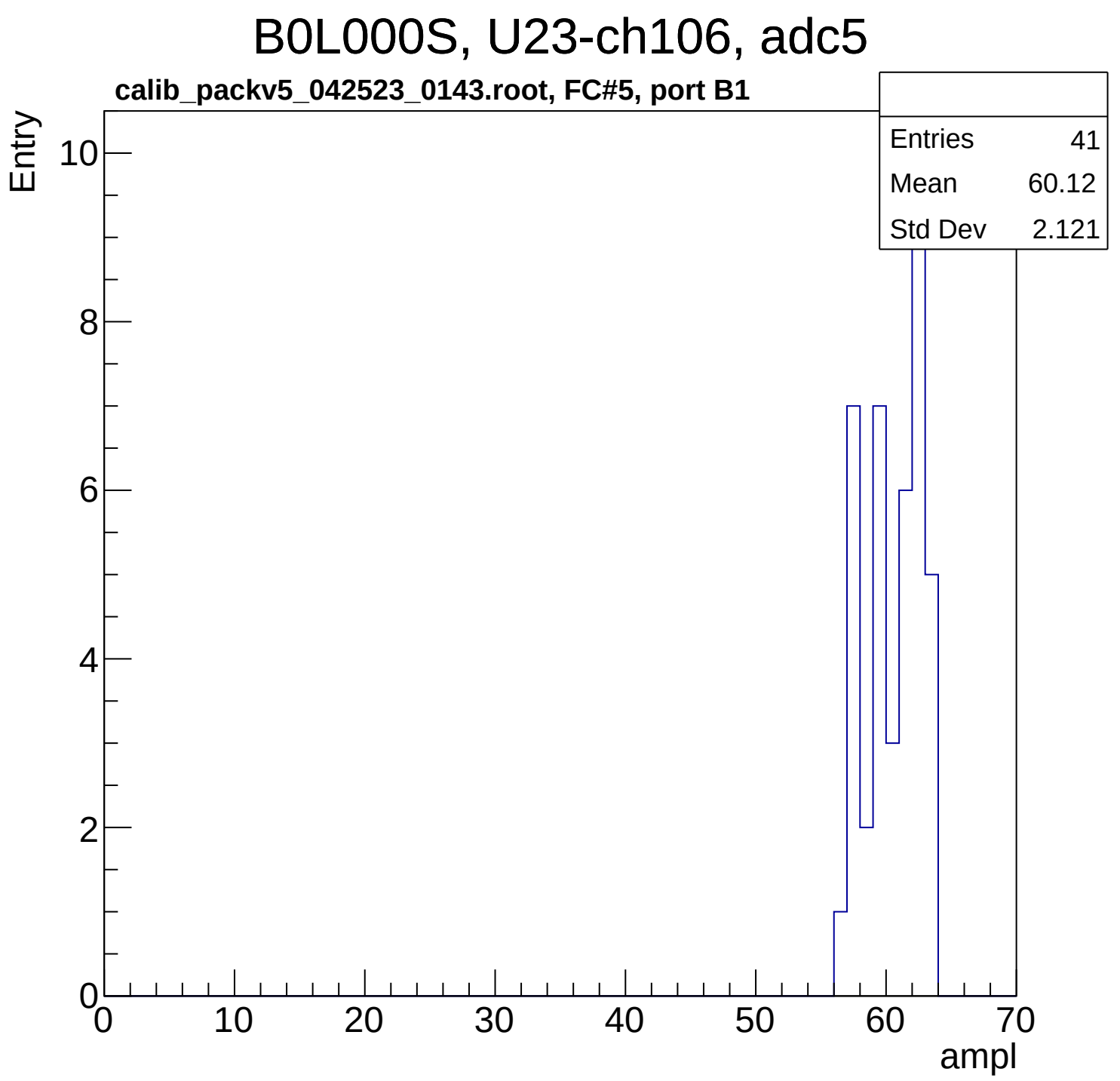

B0L000S, U23-ch106, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.67
Std Dev 3.555
Gaus mean : 31.5236
Gaus Width: 3.8498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

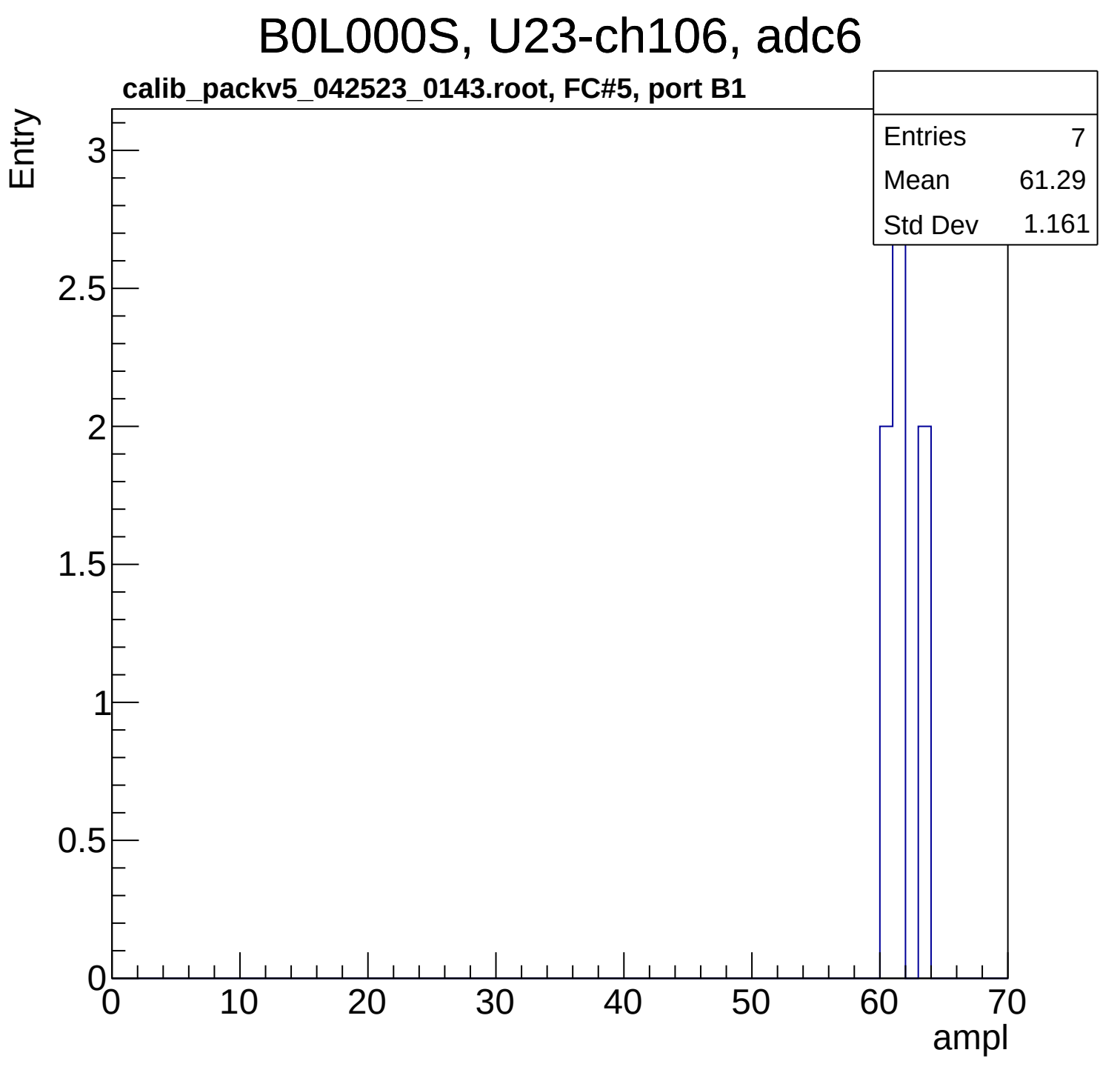

B0L000S, U23-ch106, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.66
Std Dev 3.52
Gaus mean : 37.2489
Gaus Width: 3.6353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

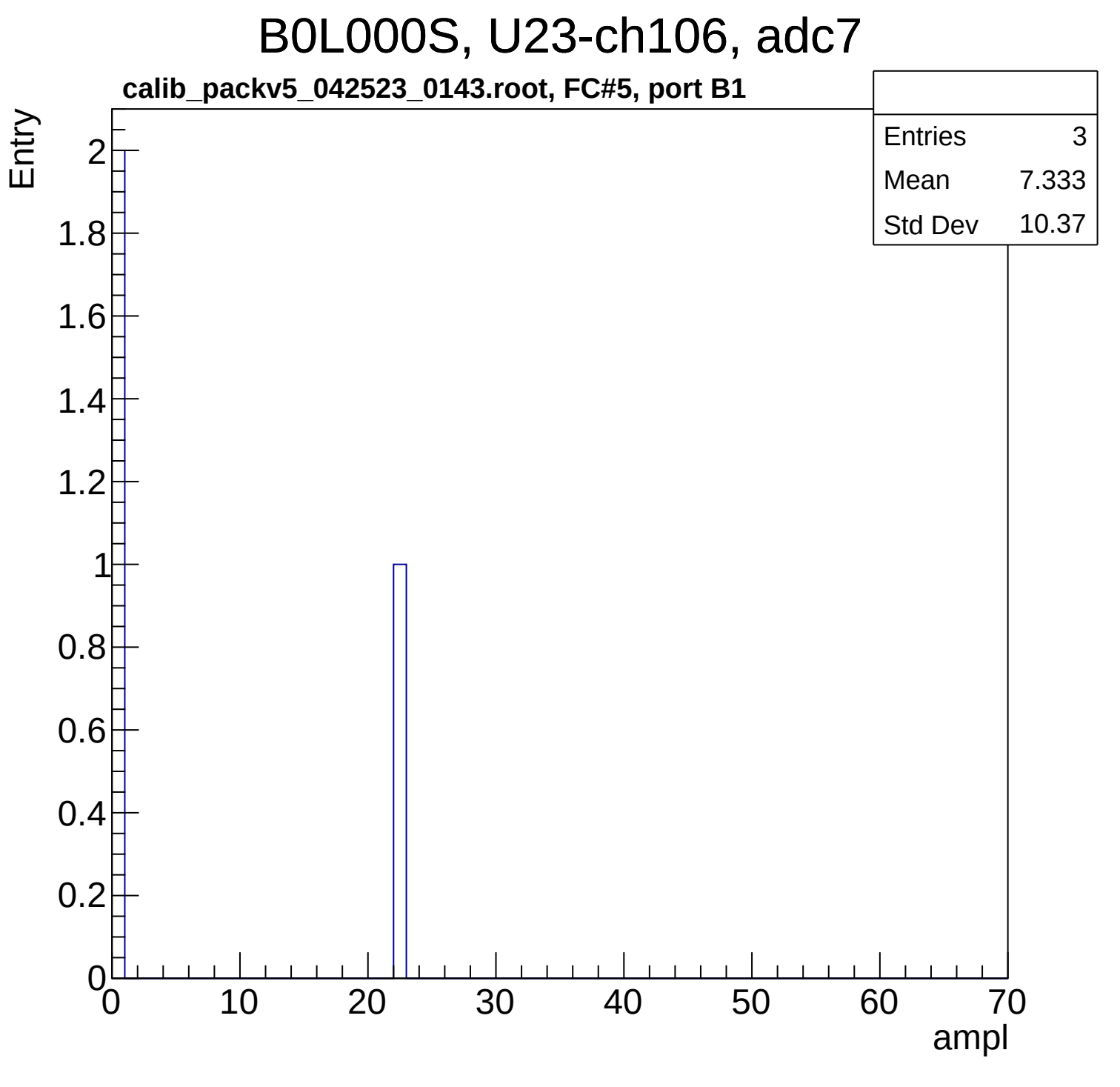

B0L000S, U23-ch106, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.22
Std Dev 2.997
Gaus mean : 43.3248
Gaus Width: 2.7948
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

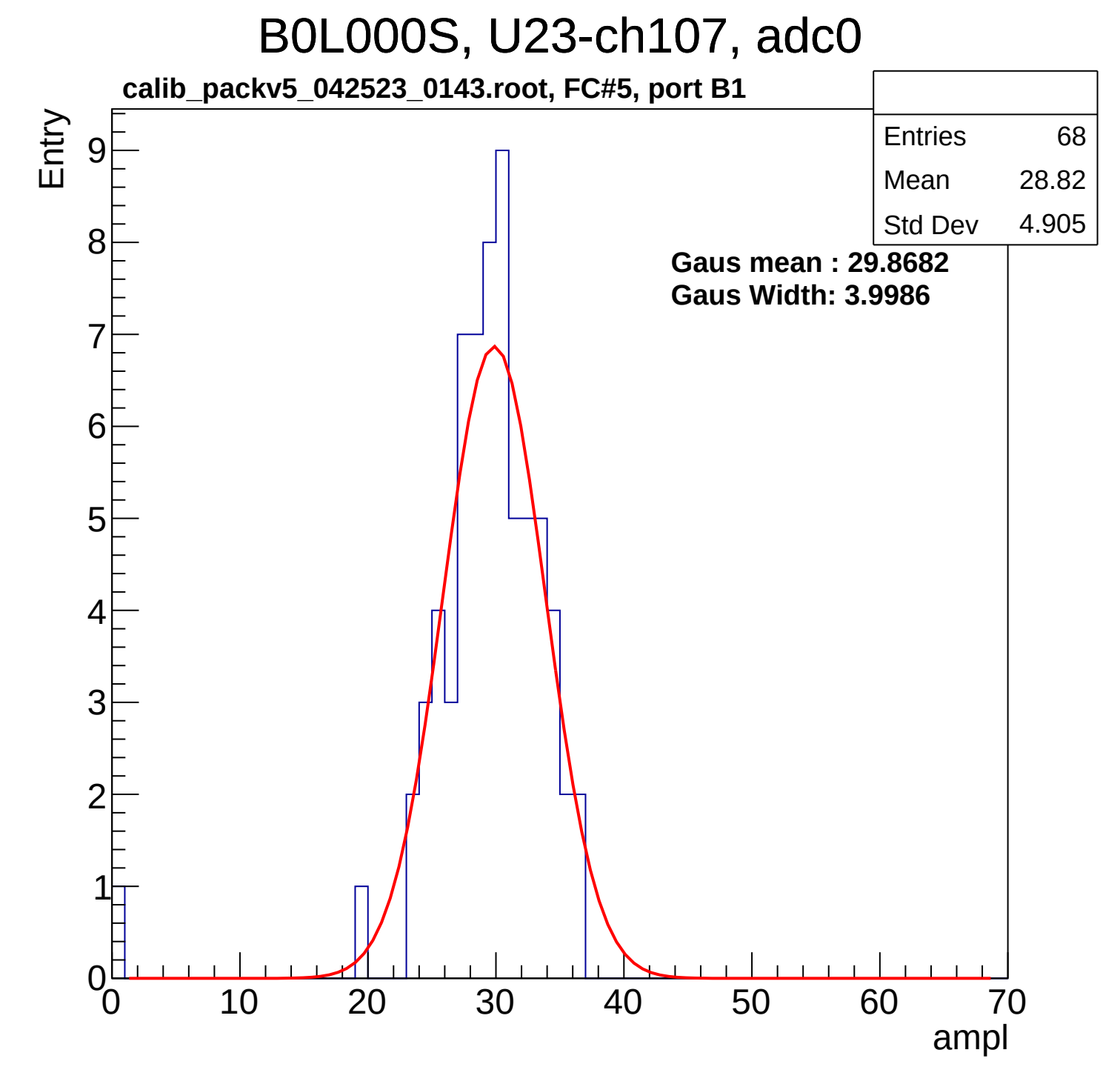

B0L000S, U23-ch106, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 48.84
Std Dev 2.763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

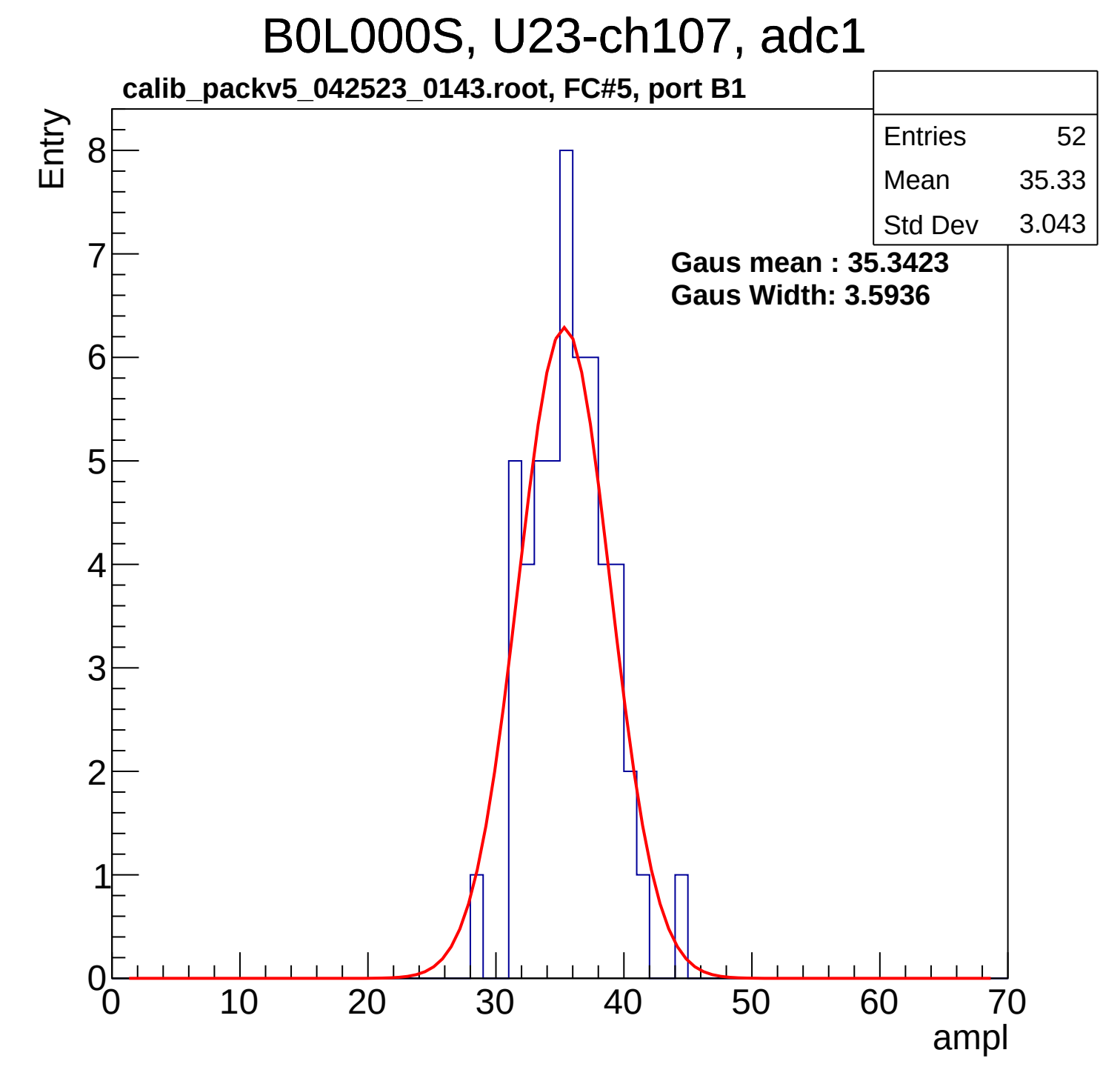

B0L000S, U23-ch106, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 54.91
Std Dev 7.362
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

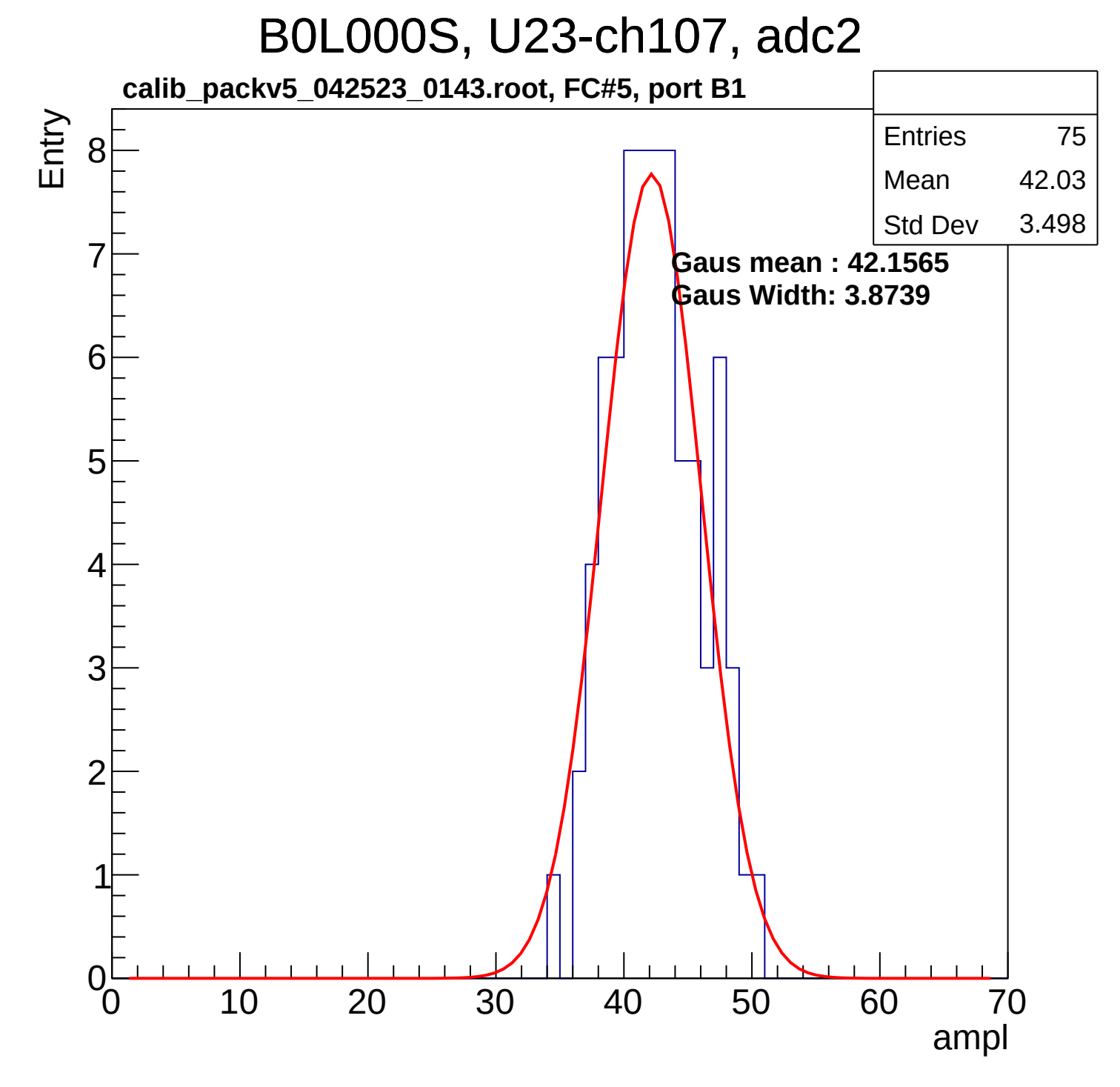

B0L000S, U23-ch106, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 60.12
Std Dev 2.121
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

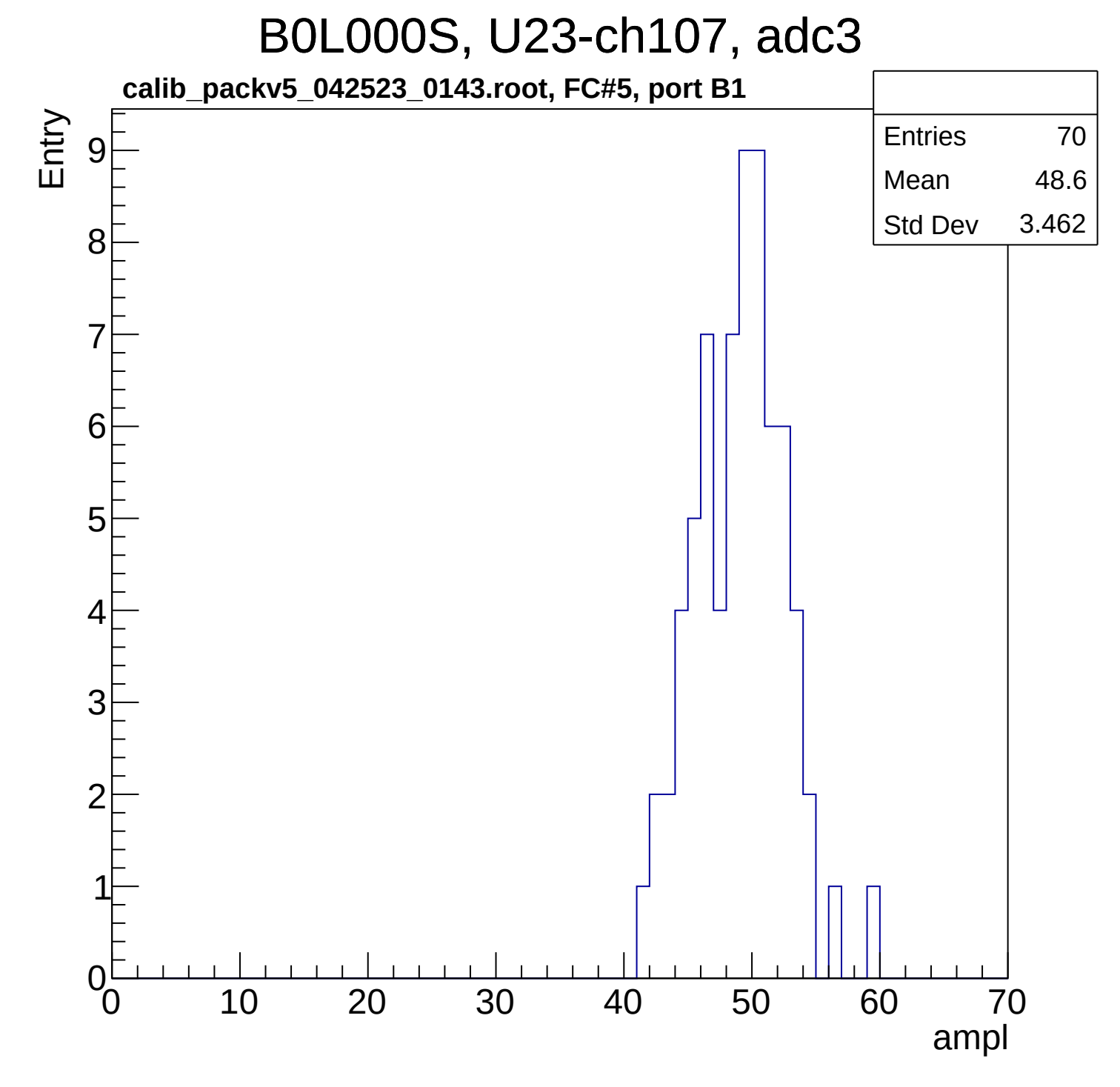

B0L000S, U23-ch106, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.29
Std Dev 1.161
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

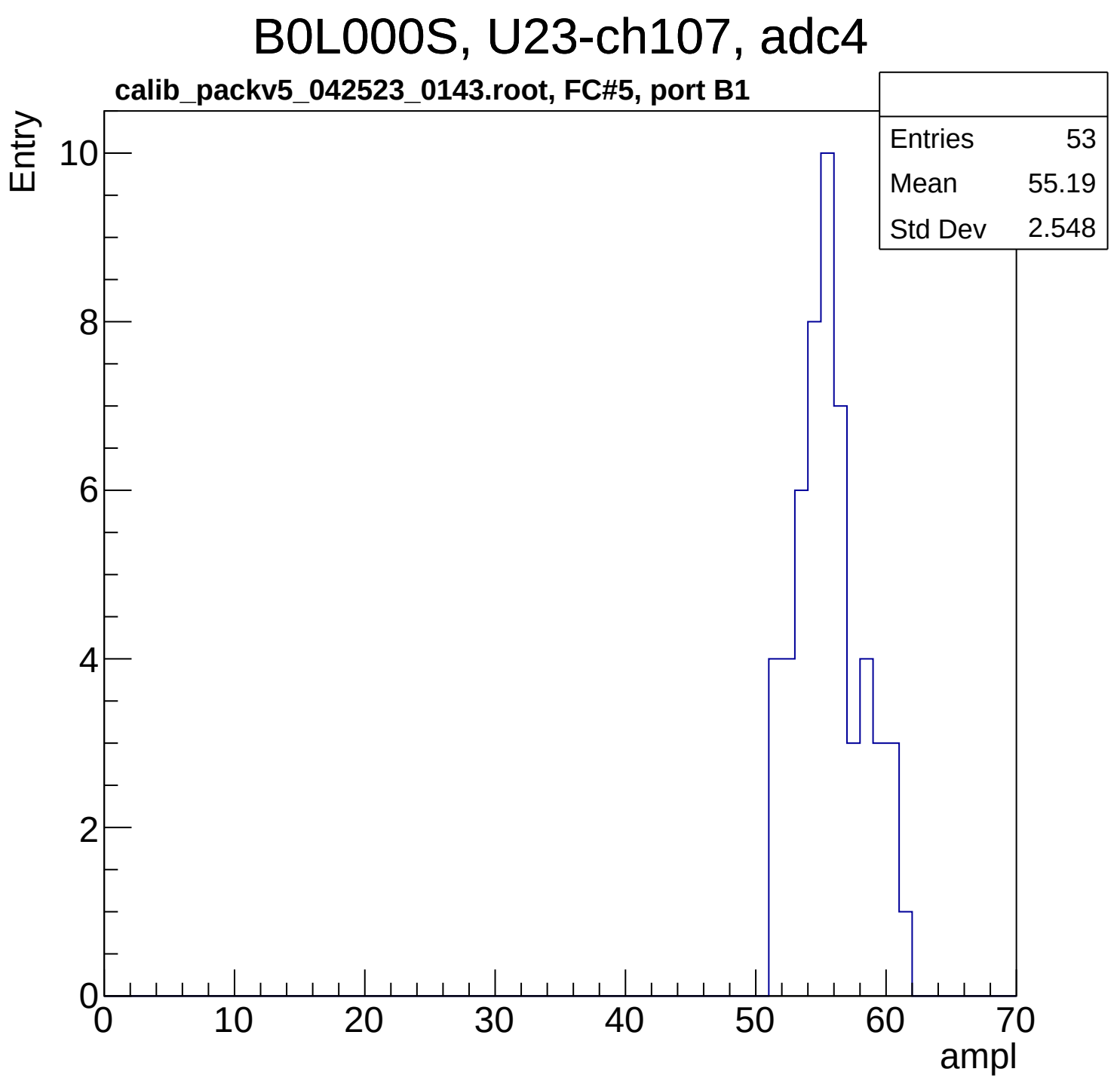

B0L000S, U23-ch106, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

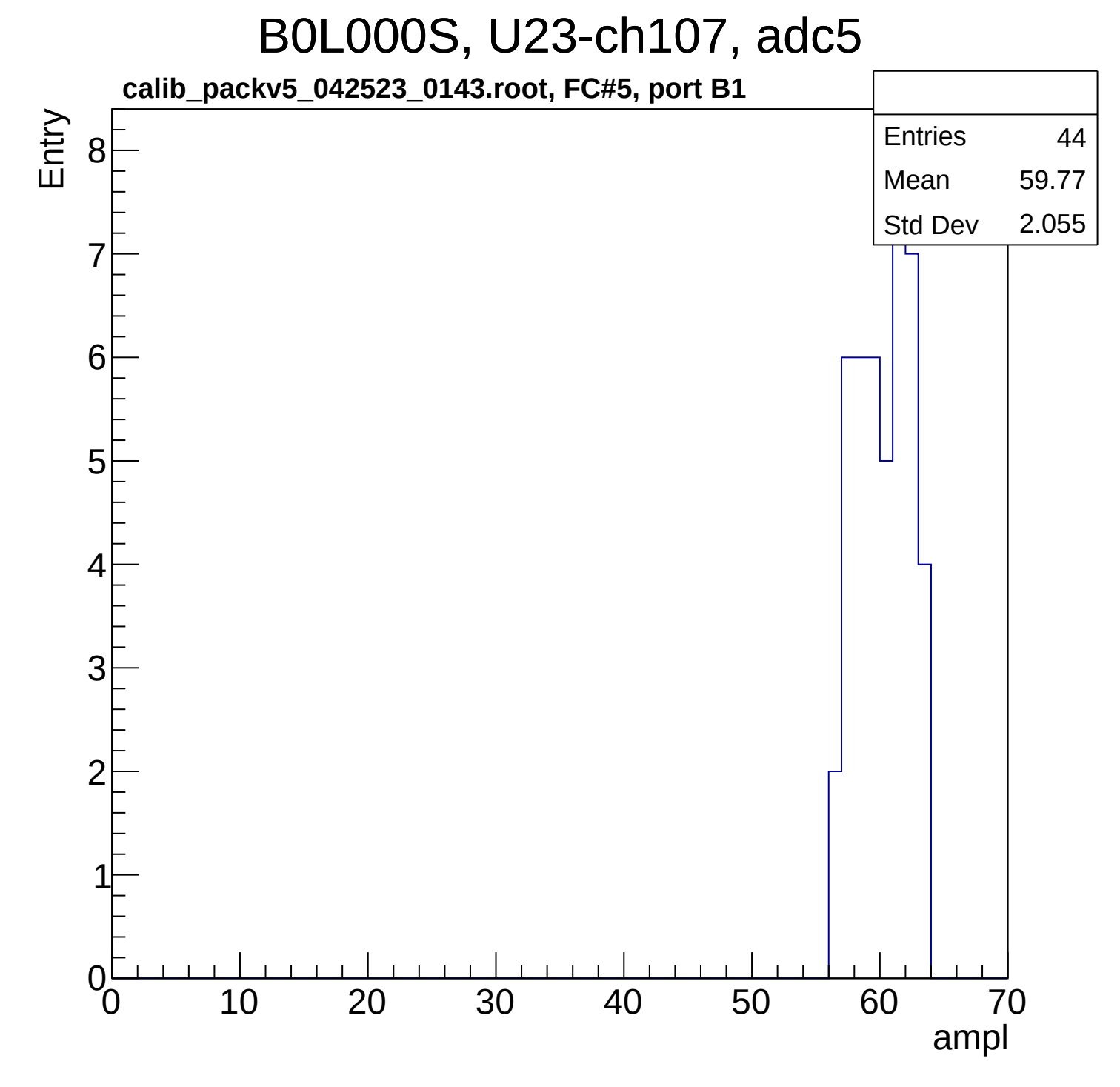

B0L000S, U23-ch107, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.82
Std Dev 4.905
Gaus mean : 29.8682
Gaus Width: 3.9986
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

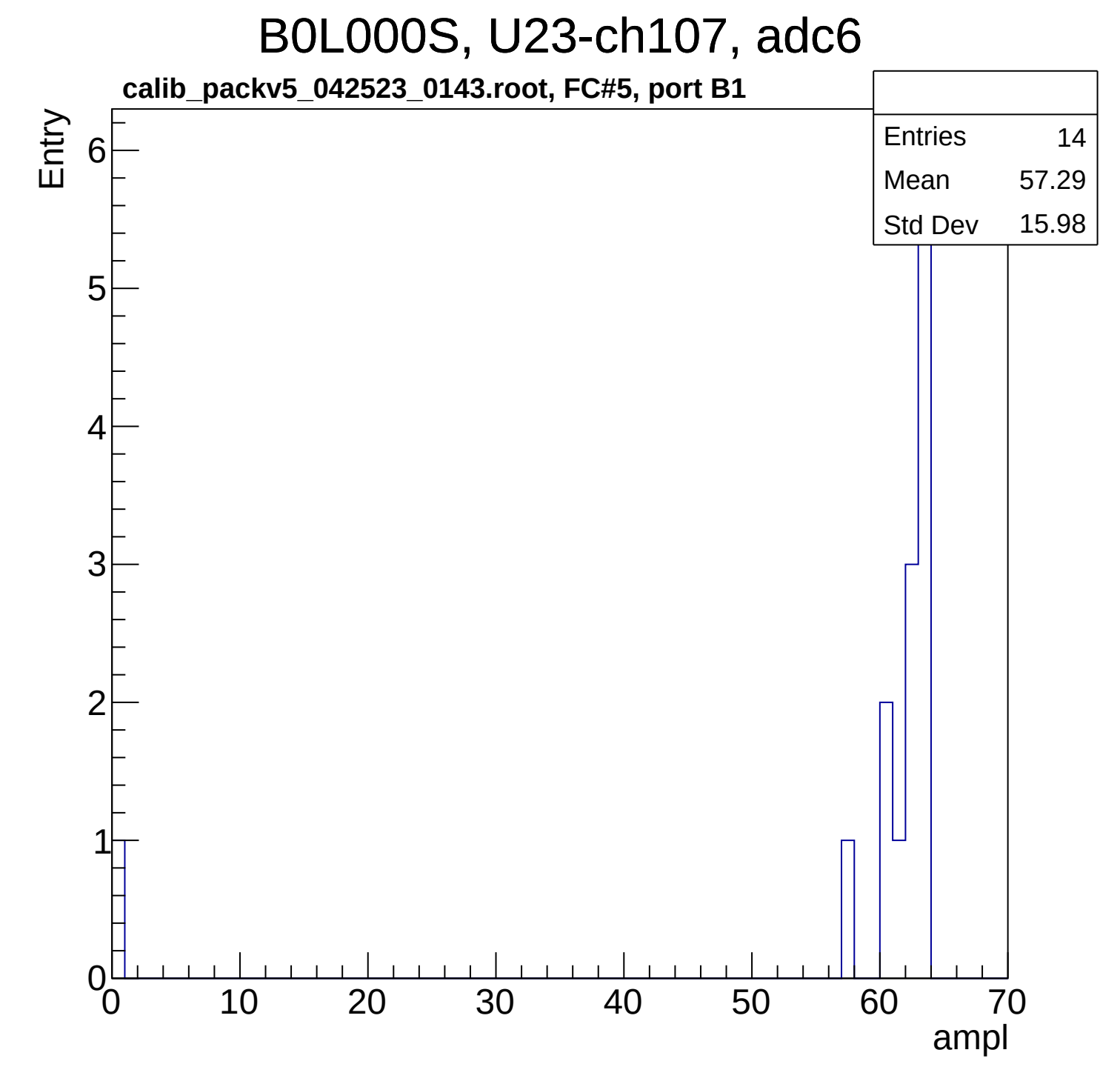

B0L000S, U23-ch107, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 35.33
Std Dev 3.043
Gaus mean : 35.3423
Gaus Width: 3.5936
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

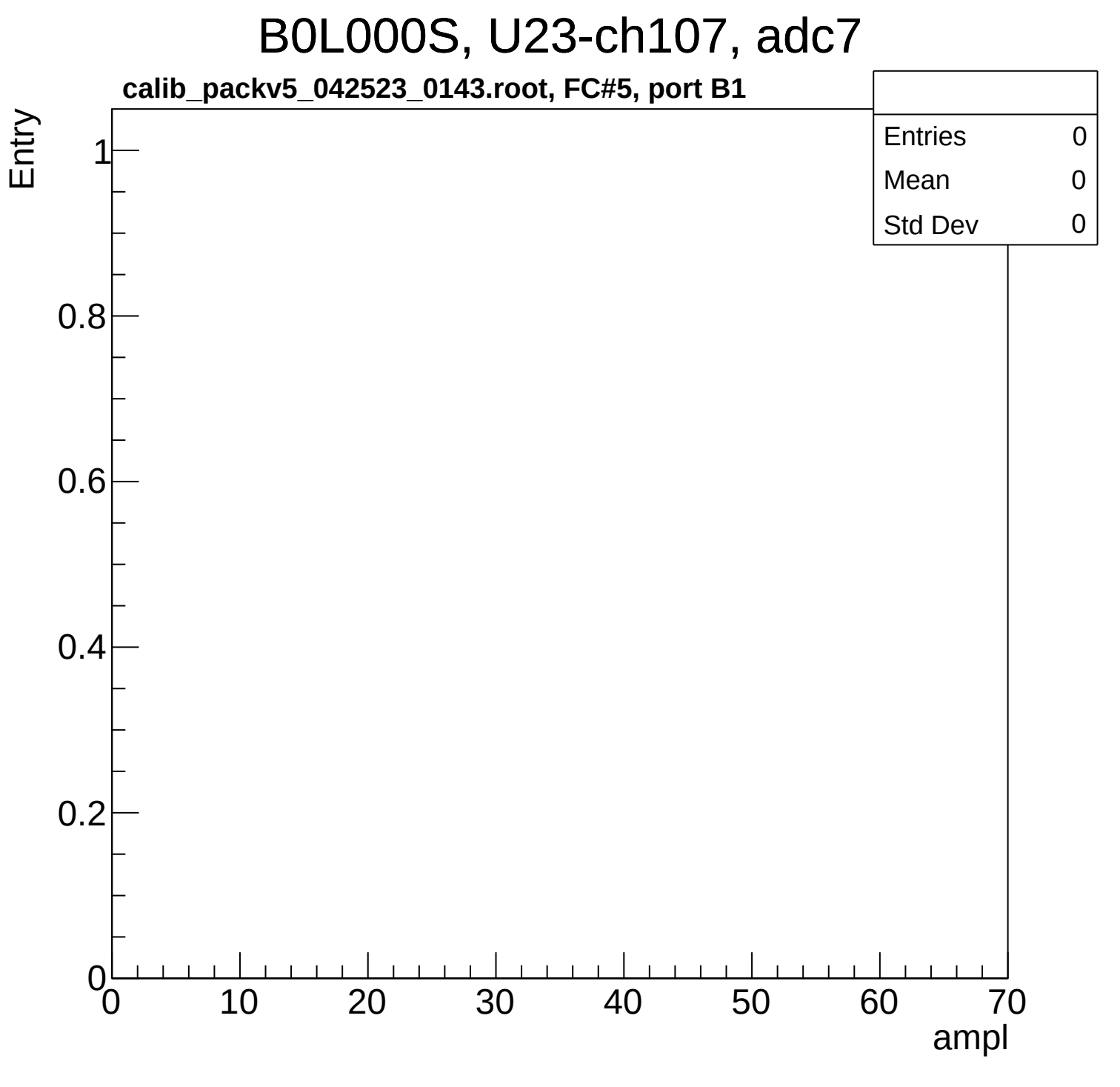

B0L000S, U23-ch107, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 42.03
Std Dev 3.498
Gaus mean : 42.1565
Gaus Width: 3.8739
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

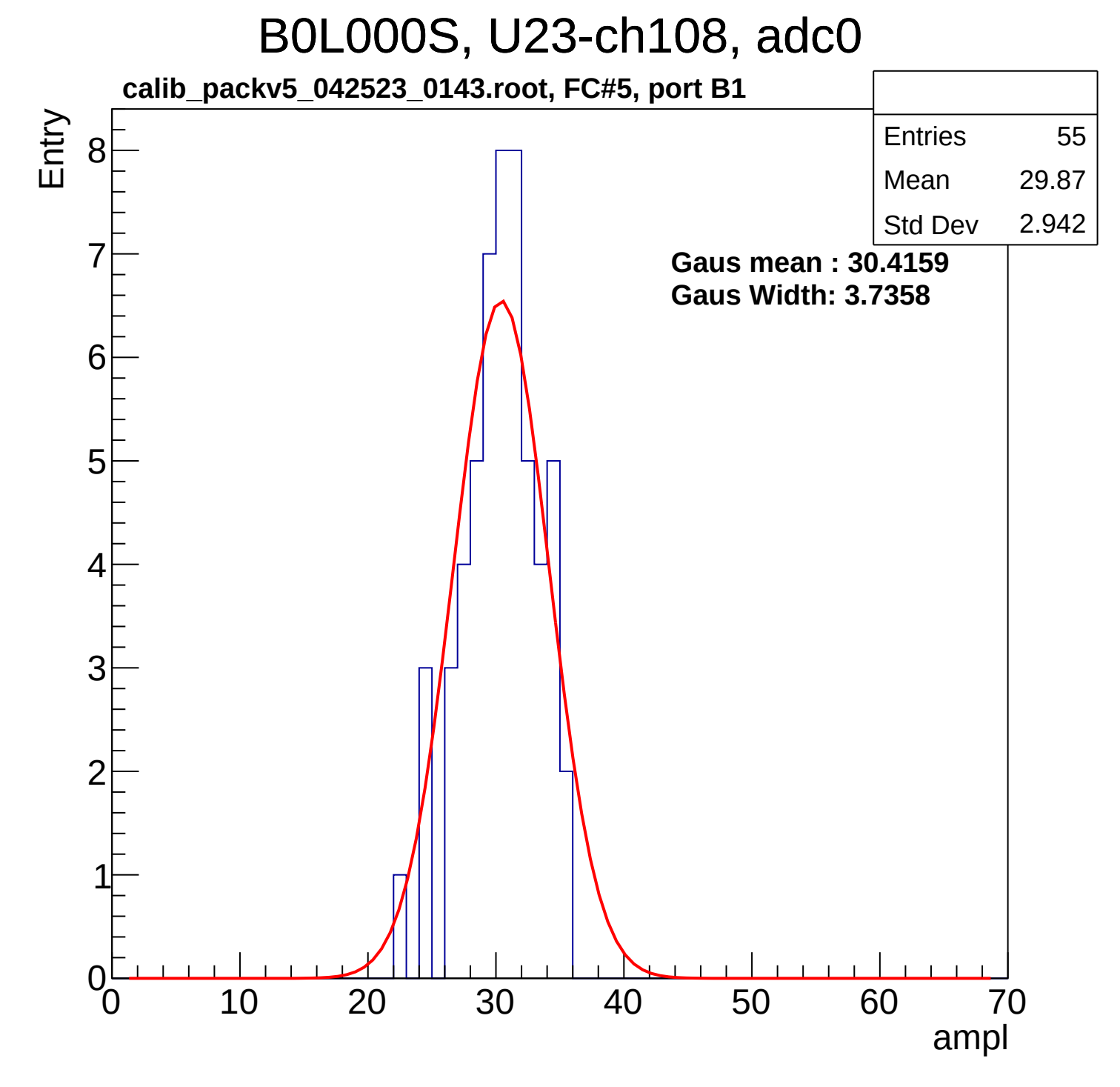

B0L000S, U23-ch107, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.6
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

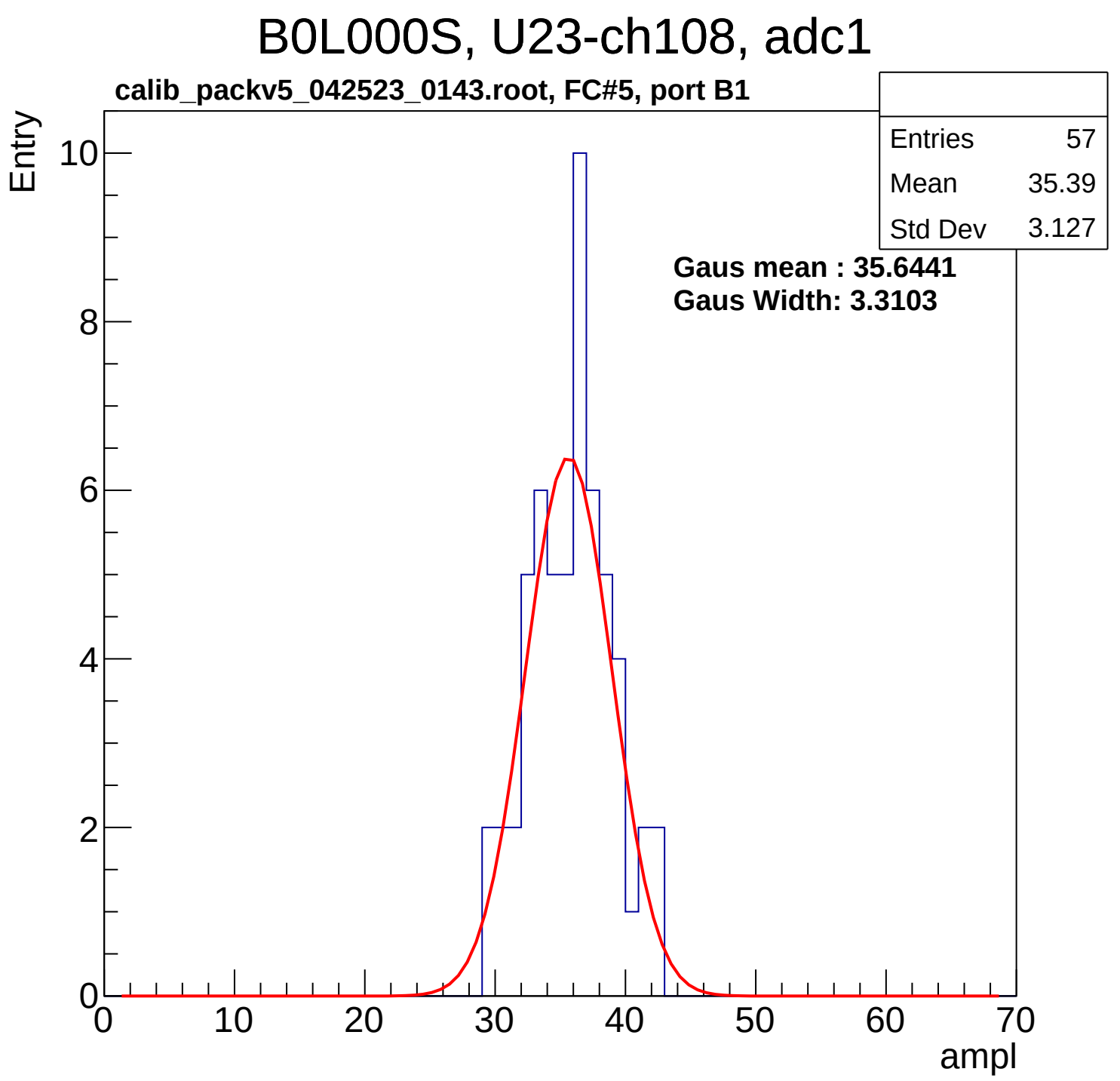

B0L000S, U23-ch107, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.19
Std Dev 2.548
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

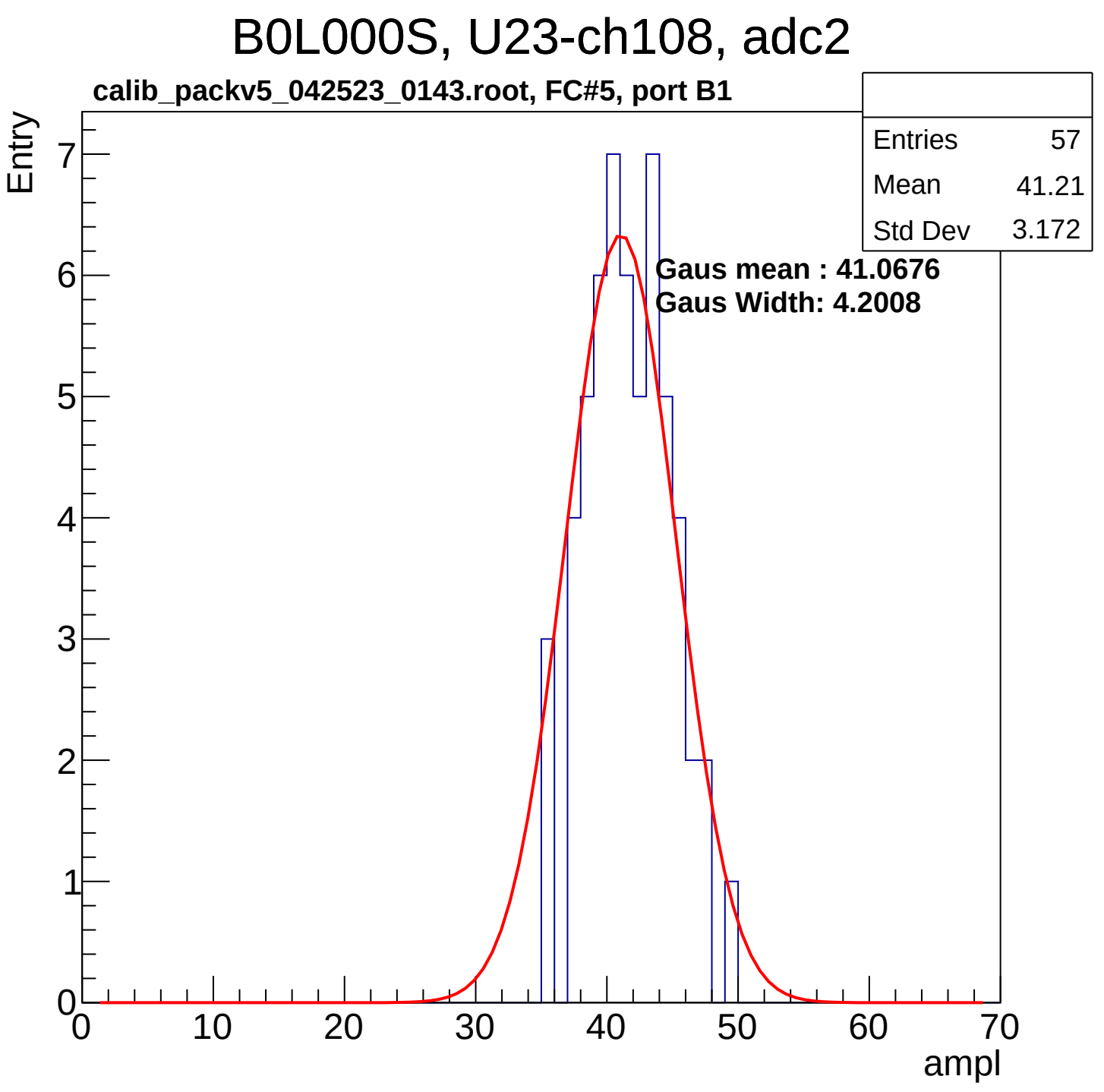

B0L000S, U23-ch107, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.77
Std Dev 2.055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

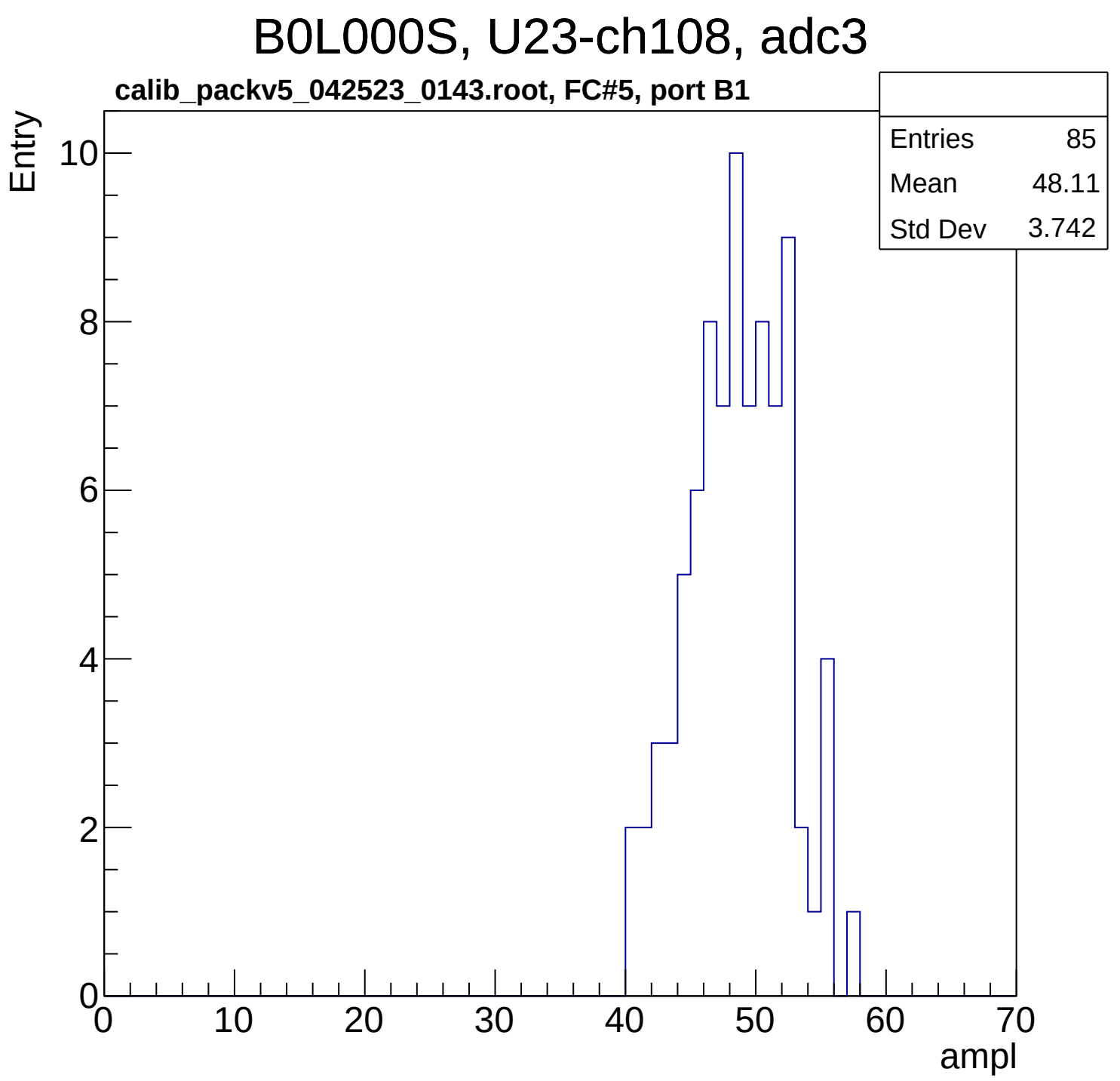

B0L000S, U23-ch107, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.29
Std Dev 15.98
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

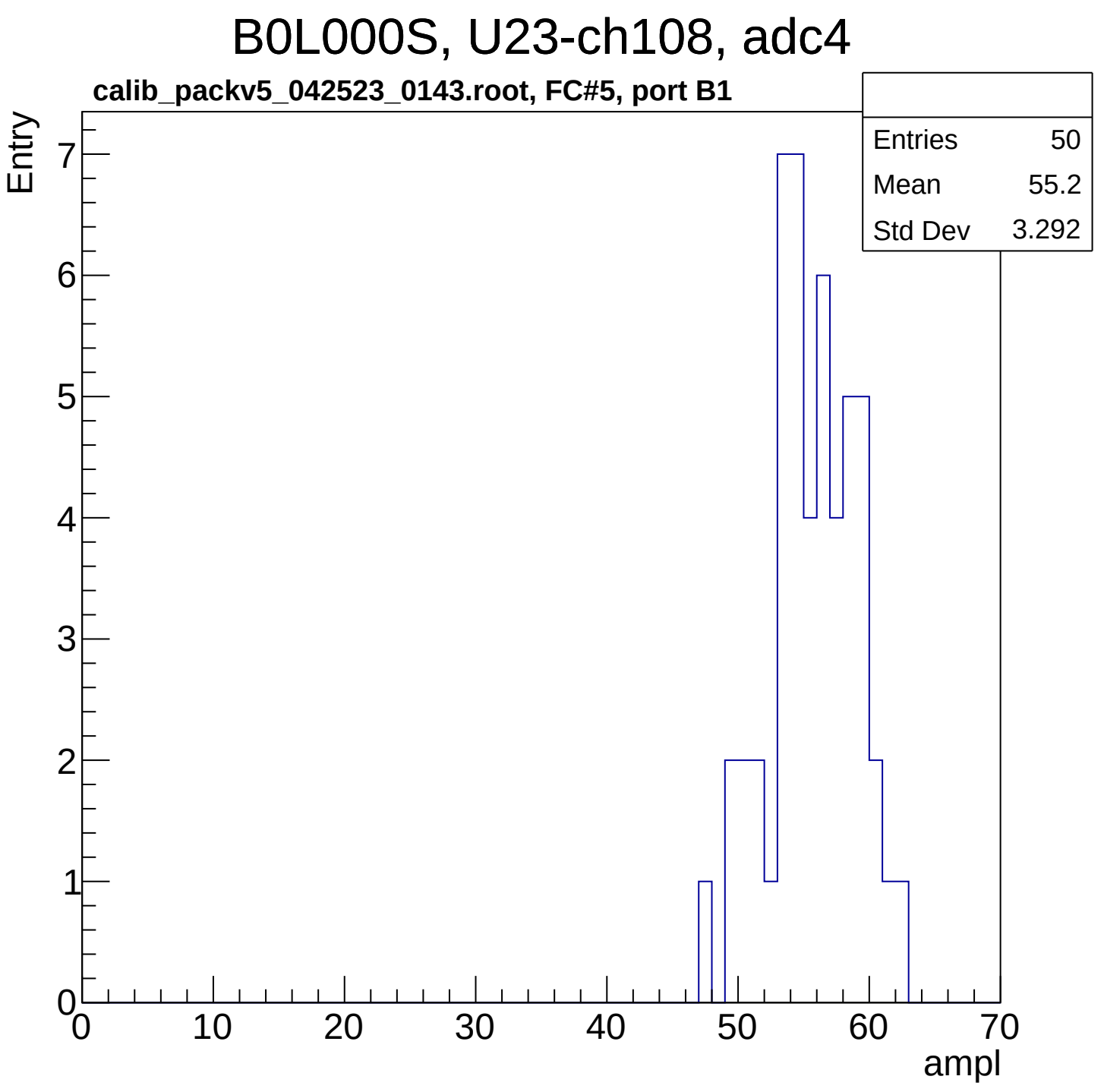

B0L000S, U23-ch107, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

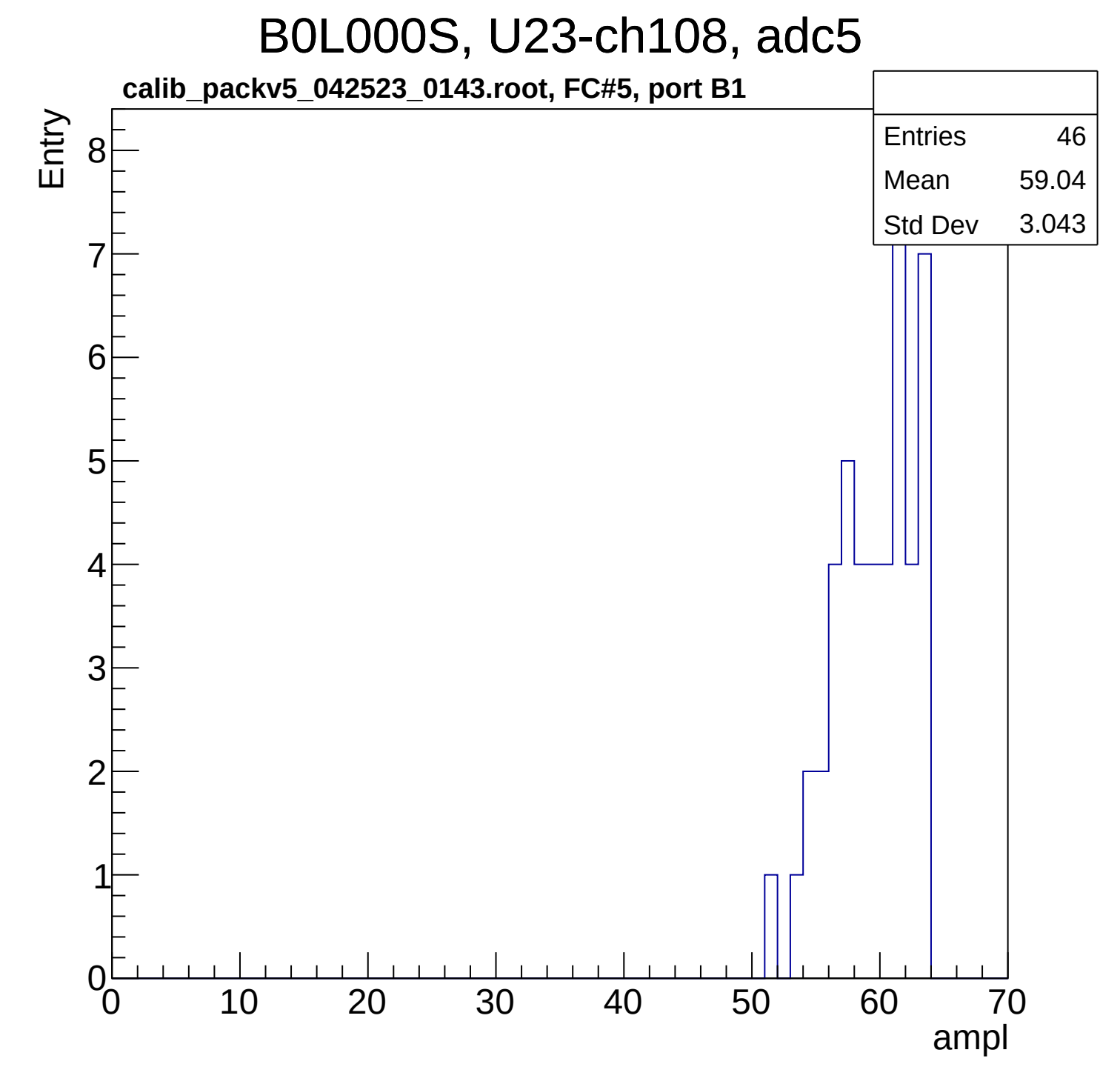

B0L000S, U23-ch108, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.87
Std Dev 2.942
Gaus mean : 30.4159
Gaus Width: 3.7358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

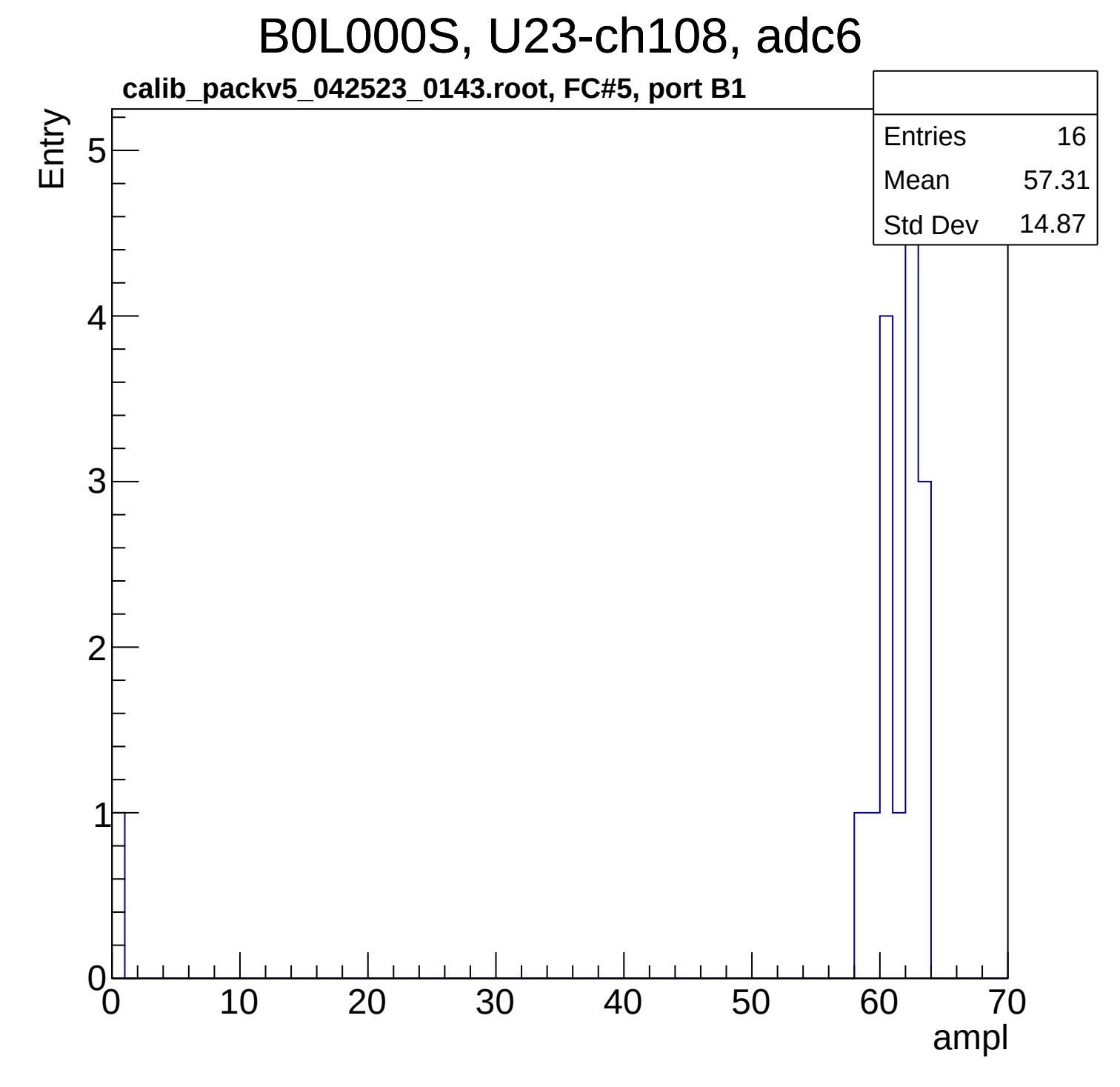

B0L000S, U23-ch108, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.39
Std Dev 3.127
Gaus mean : 35.6441
Gaus Width: 3.3103
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

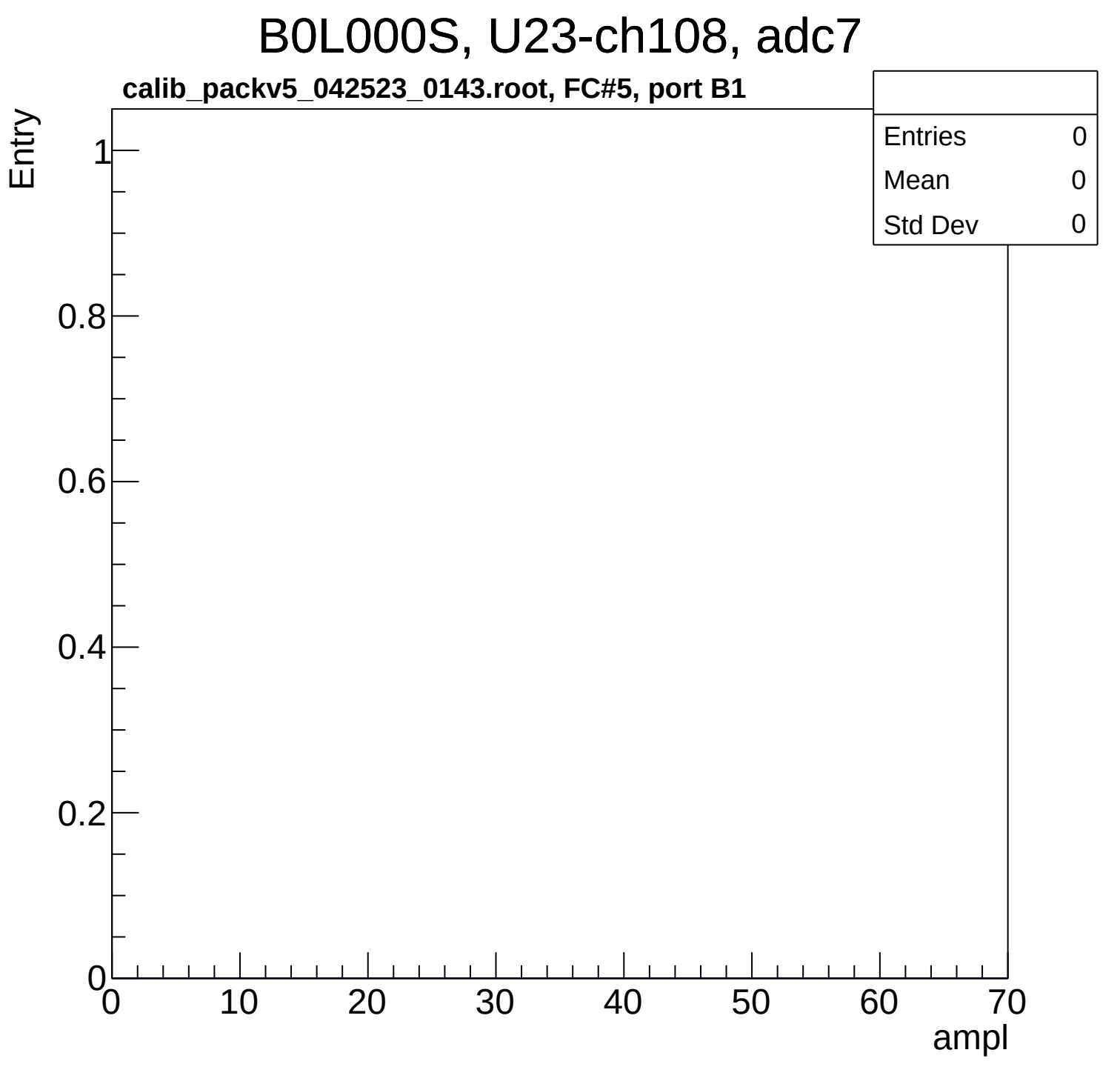

B0L000S, U23-ch108, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 41.21
Std Dev 3.172
Gaus mean : 41.0676
Gaus Width: 4.2008
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

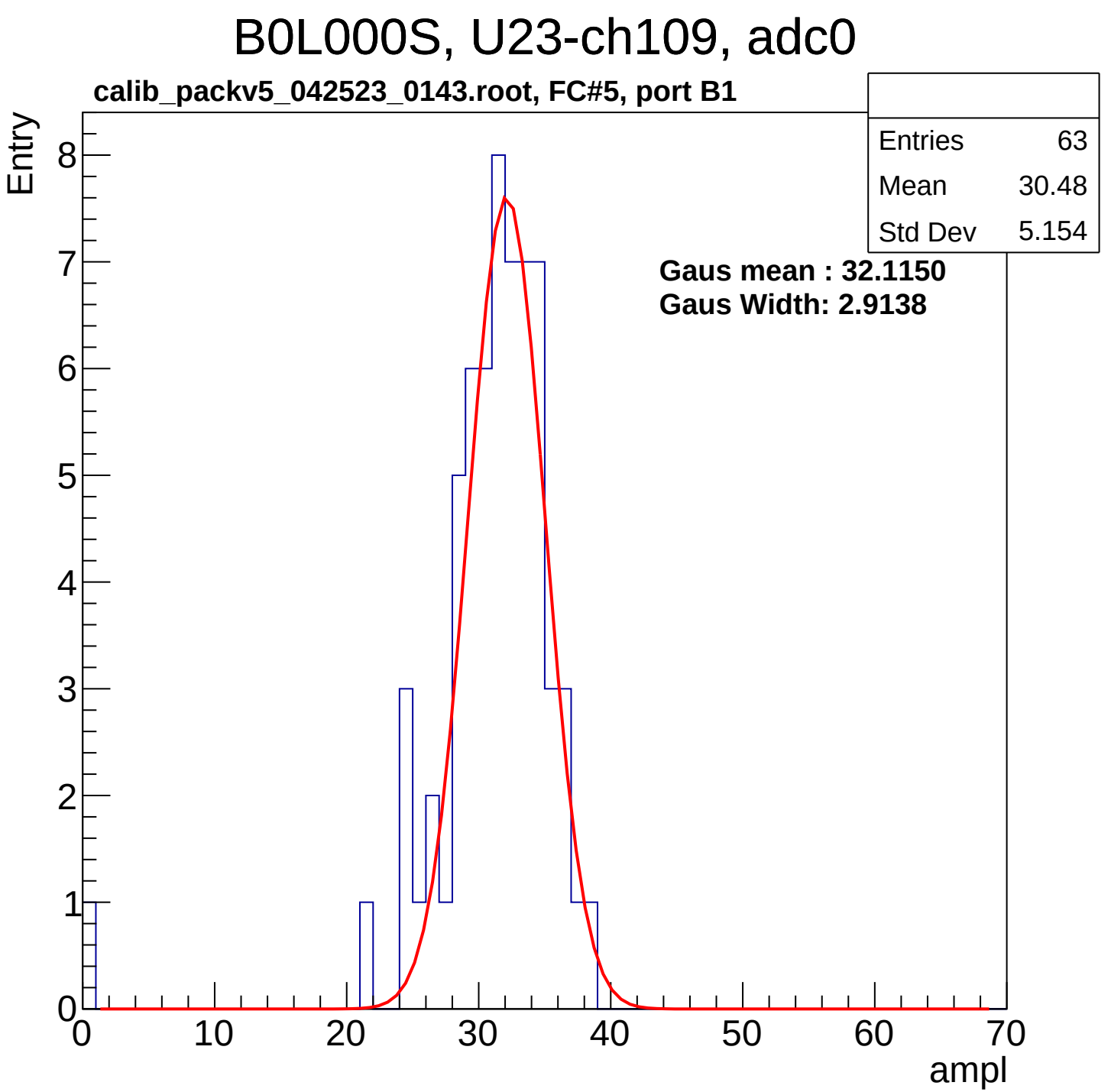

B0L000S, U23-ch108, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 48.11
Std Dev 3.742
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

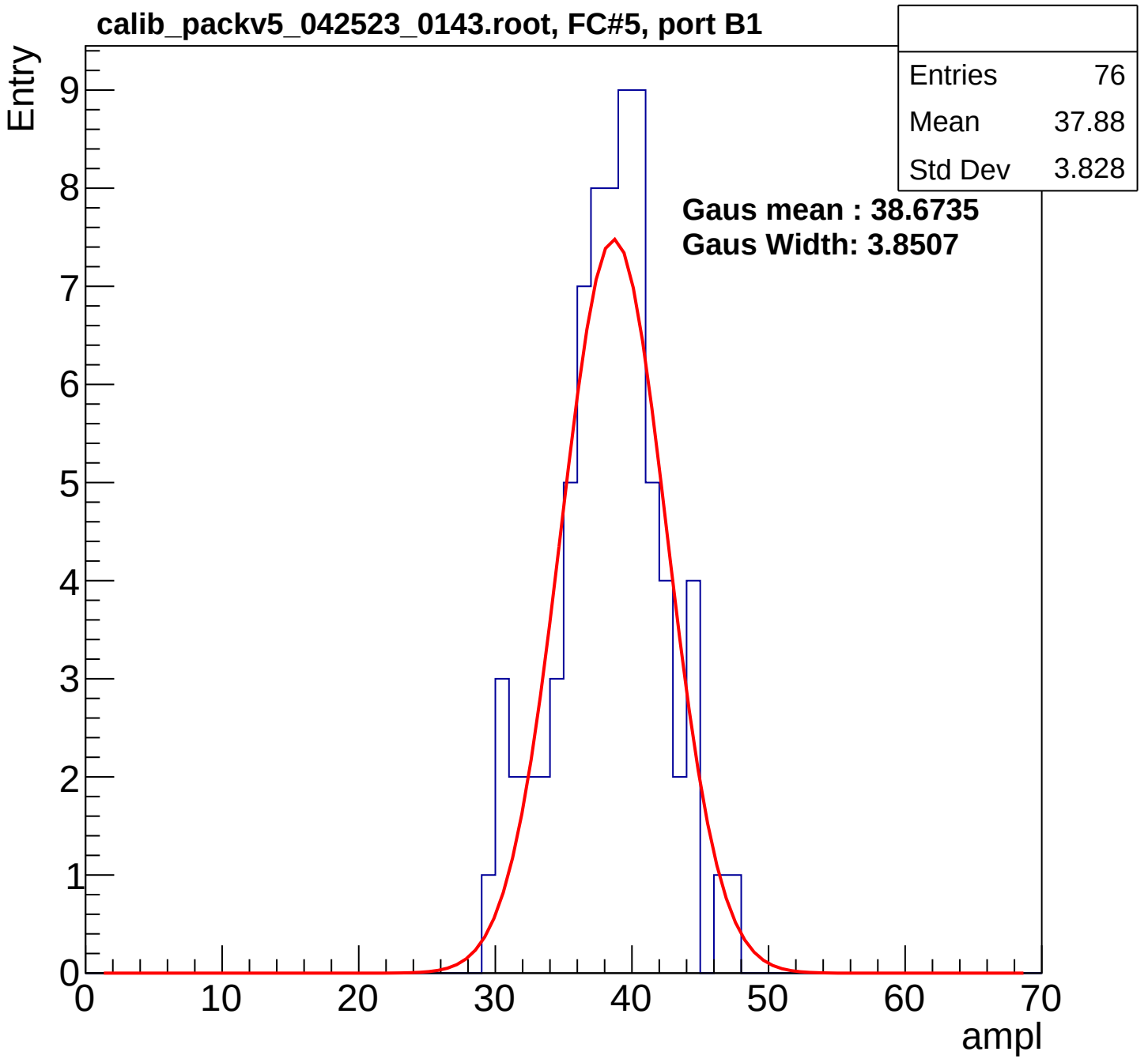

B0L000S, U23-ch108, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.2
Std Dev 3.292
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

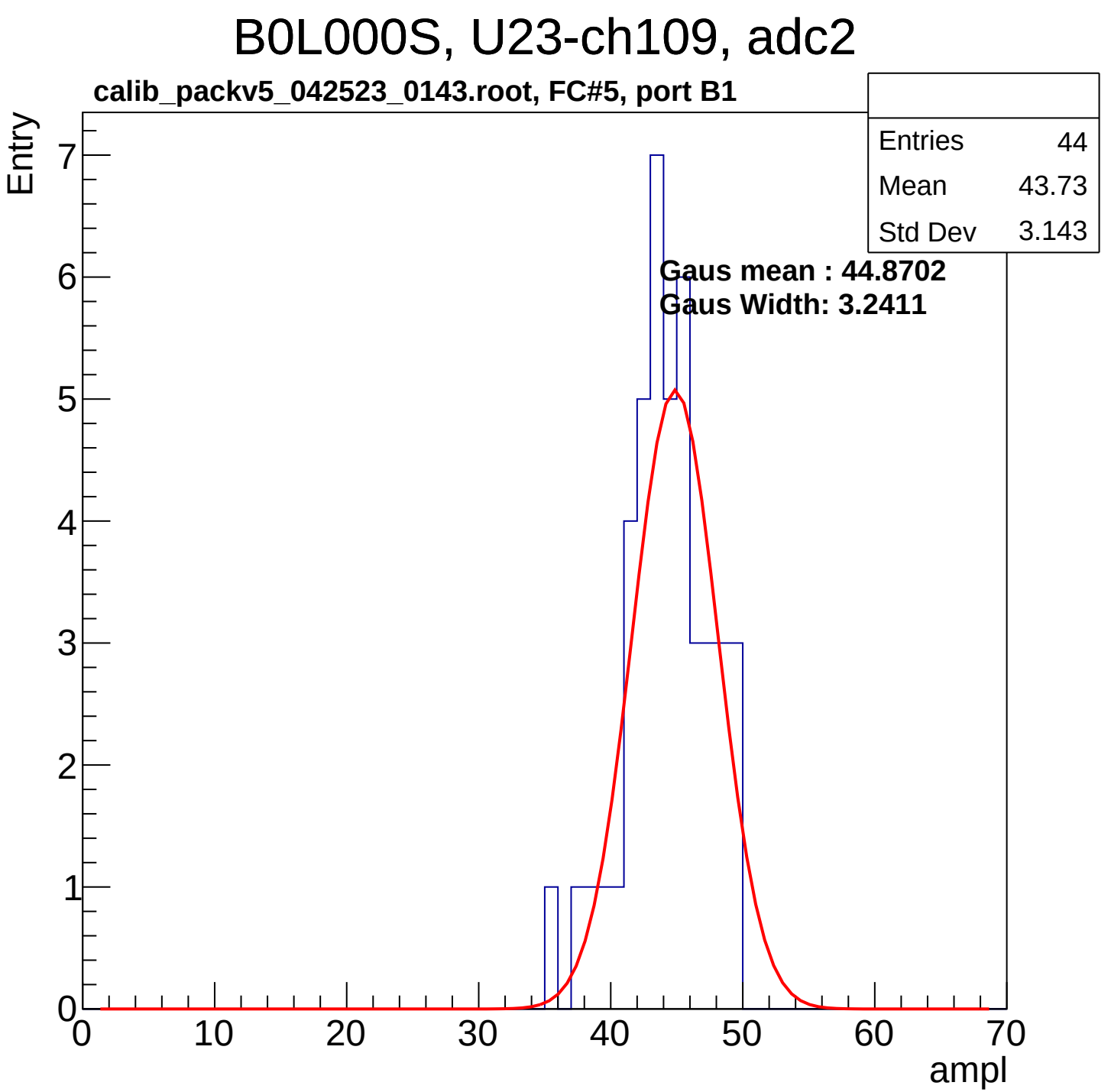

B0L000S, U23-ch108, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 59.04
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

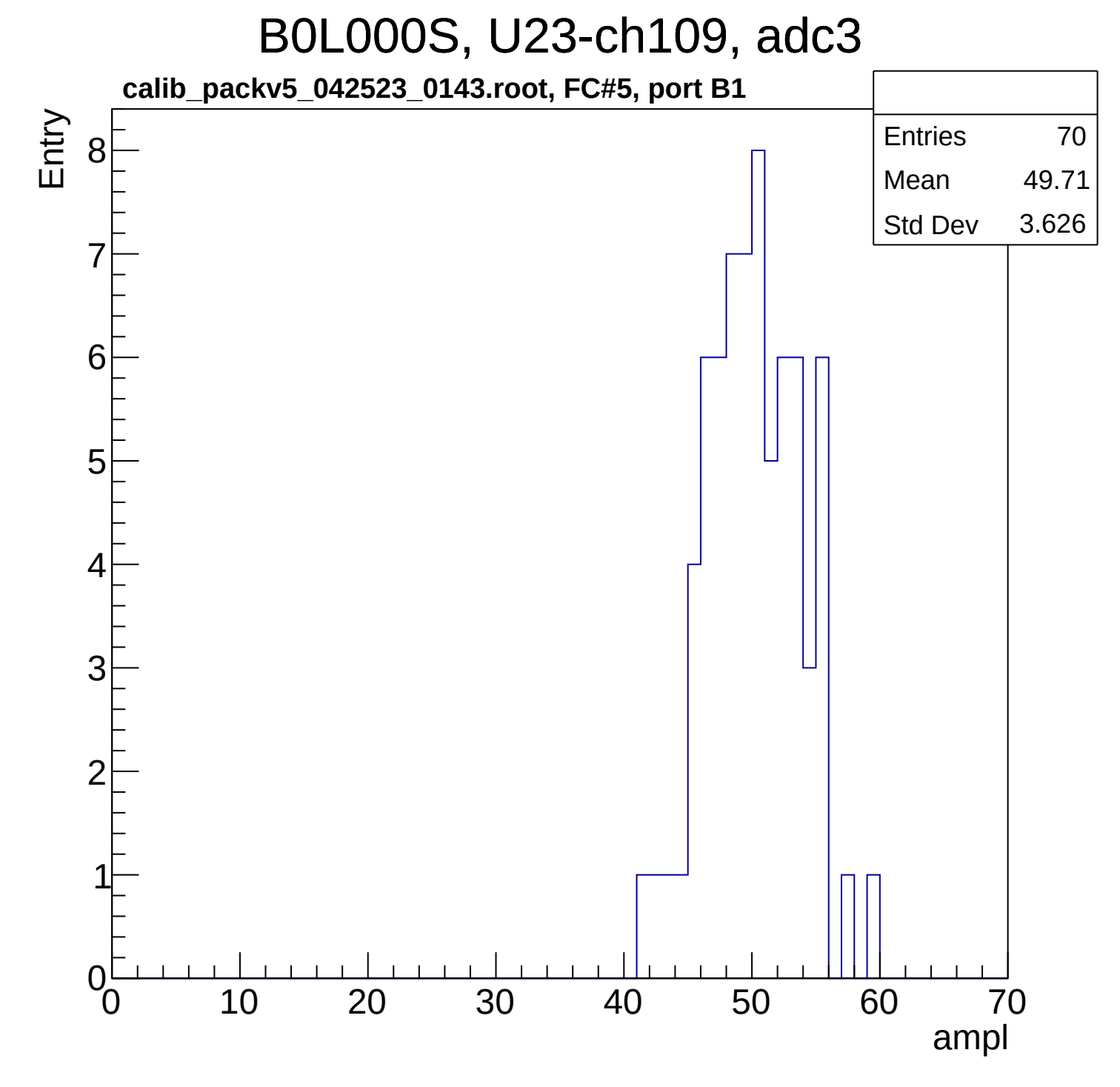

B0L000S, U23-ch108, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 57.31
Std Dev 14.87
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

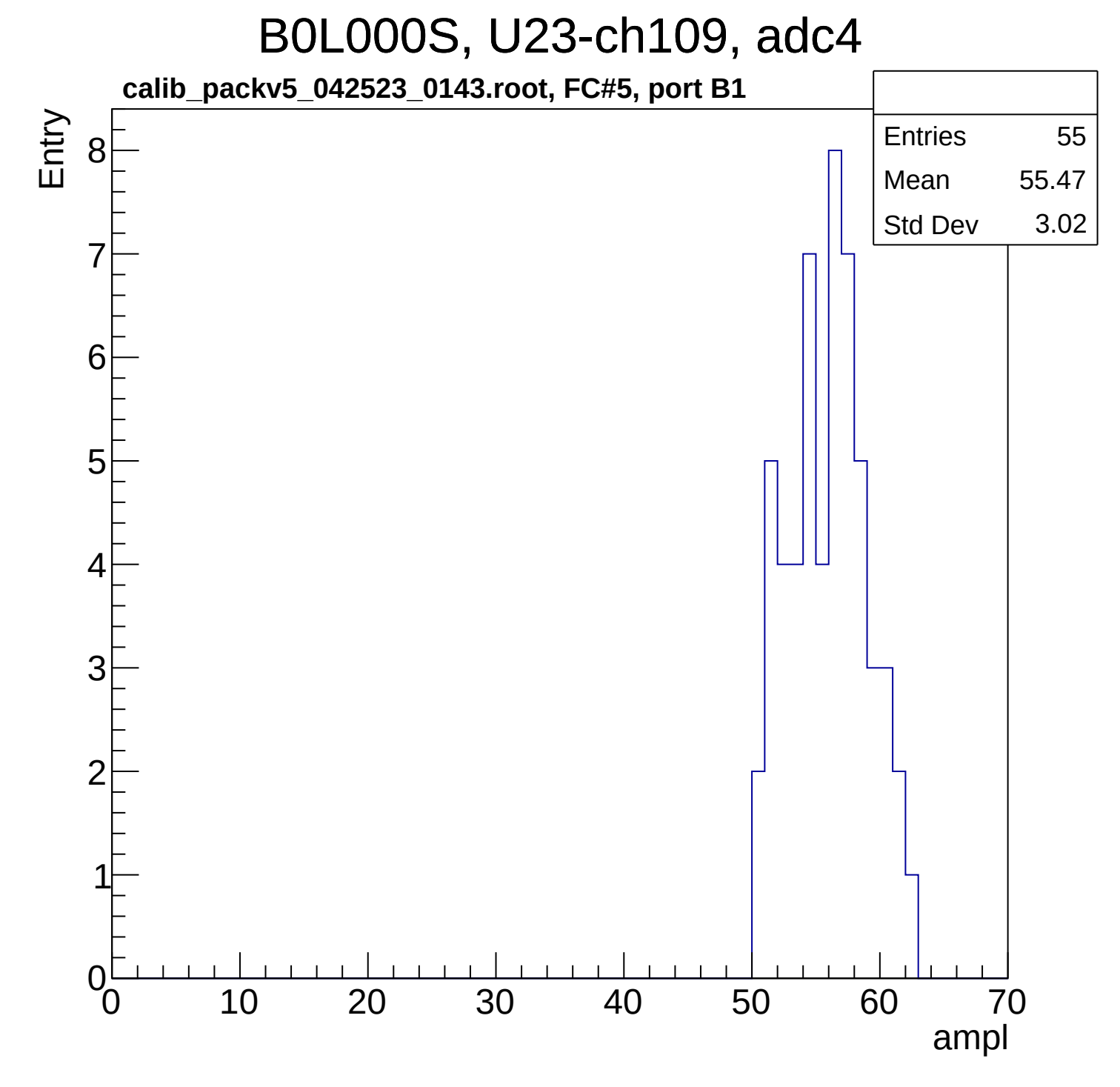

B0L000S, U23-ch108, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

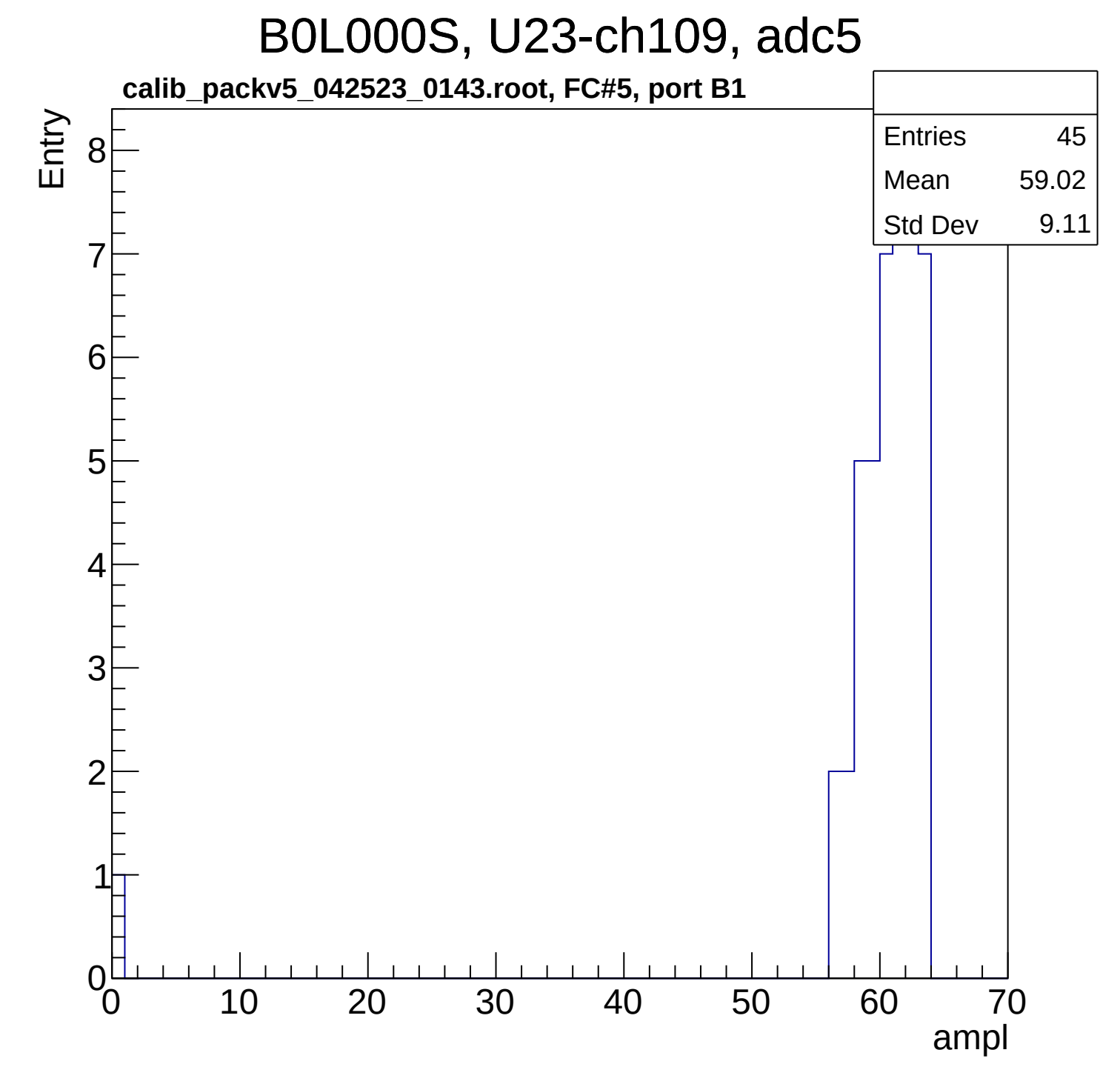

B0L000S, U23-ch109, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30.48
Std Dev 5.154
Gaus mean : 32.1150
Gaus Width: 2.9138
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

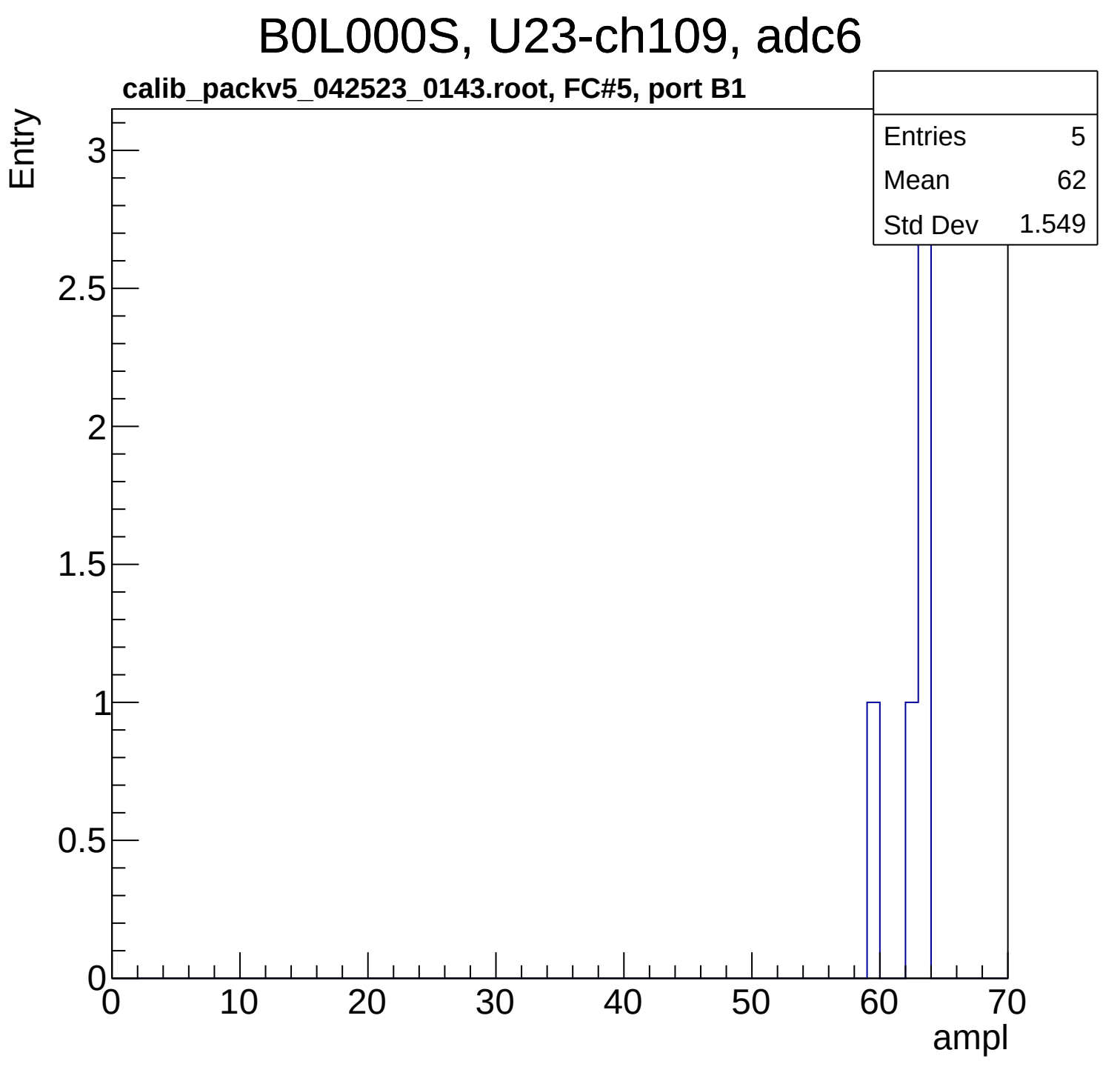

B0L000S, U23-ch109, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 37.88
Std Dev 3.828
Gaus mean : 38.6735
Gaus Width: 3.8507
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

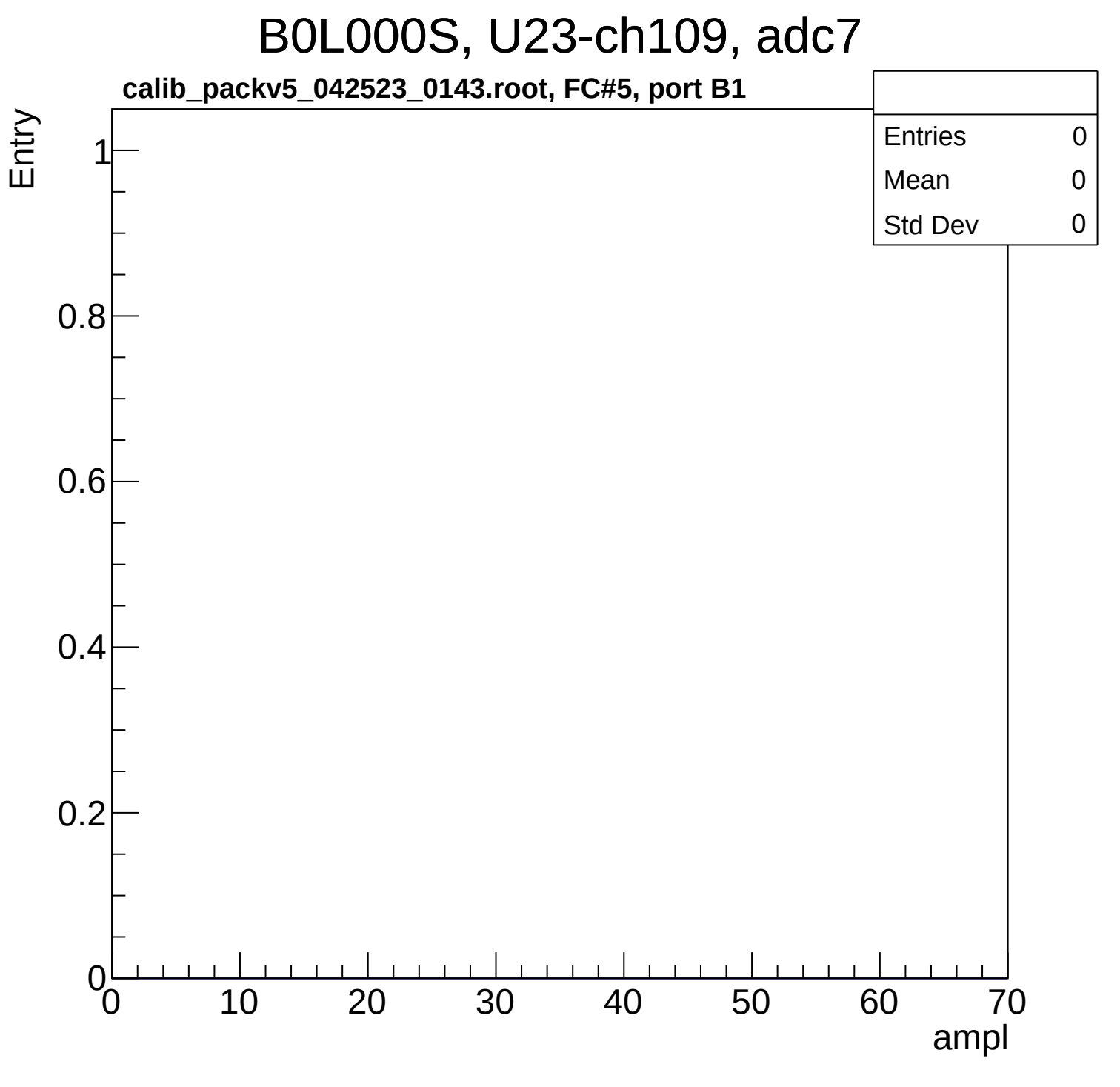

B0L000S, U23-ch109, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 43.73
Std Dev 3.143
Gaus mean : 44.8702
Gaus Width: 3.2411
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

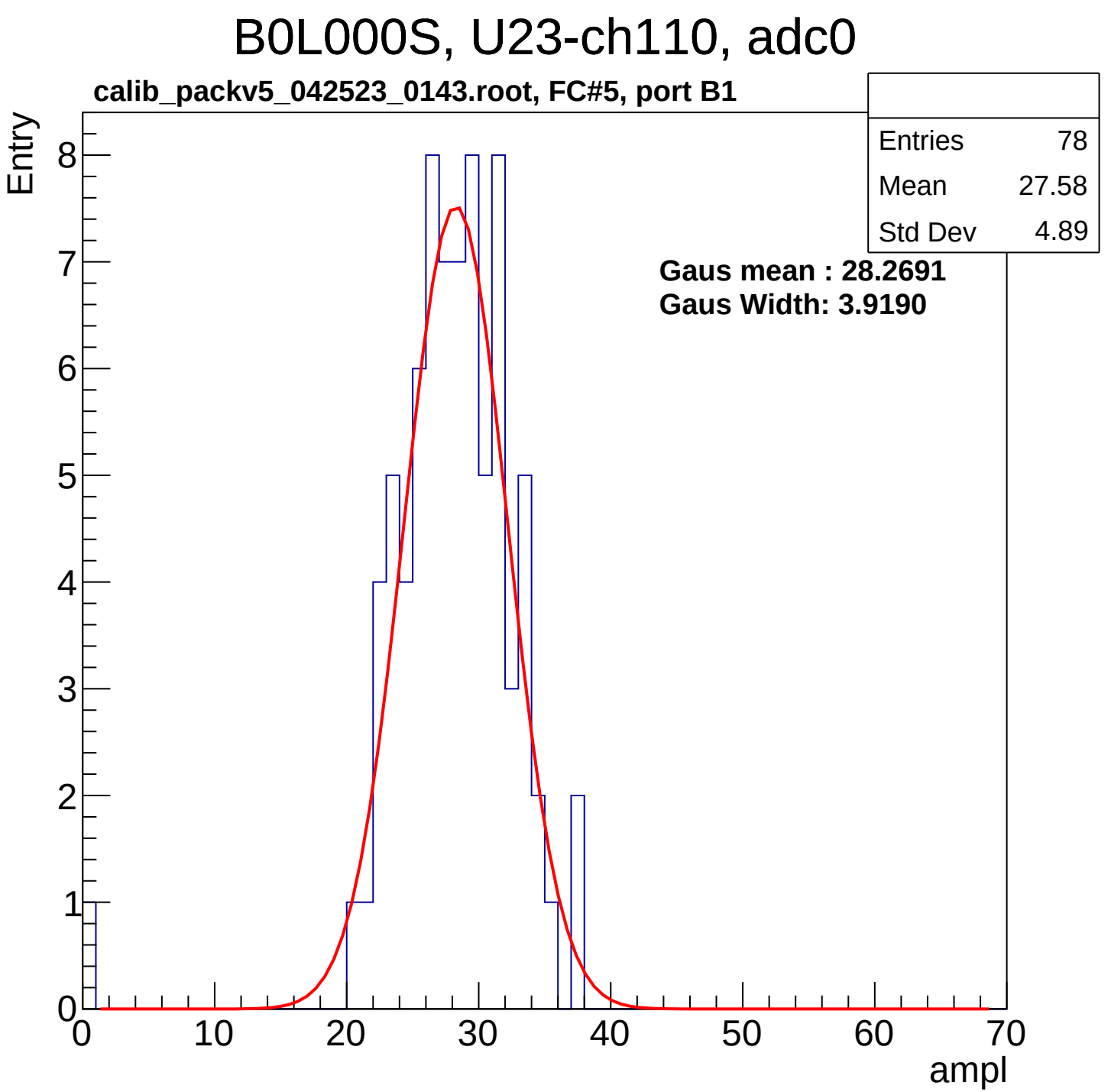

B0L000S, U23-ch109, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.71
Std Dev 3.626
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

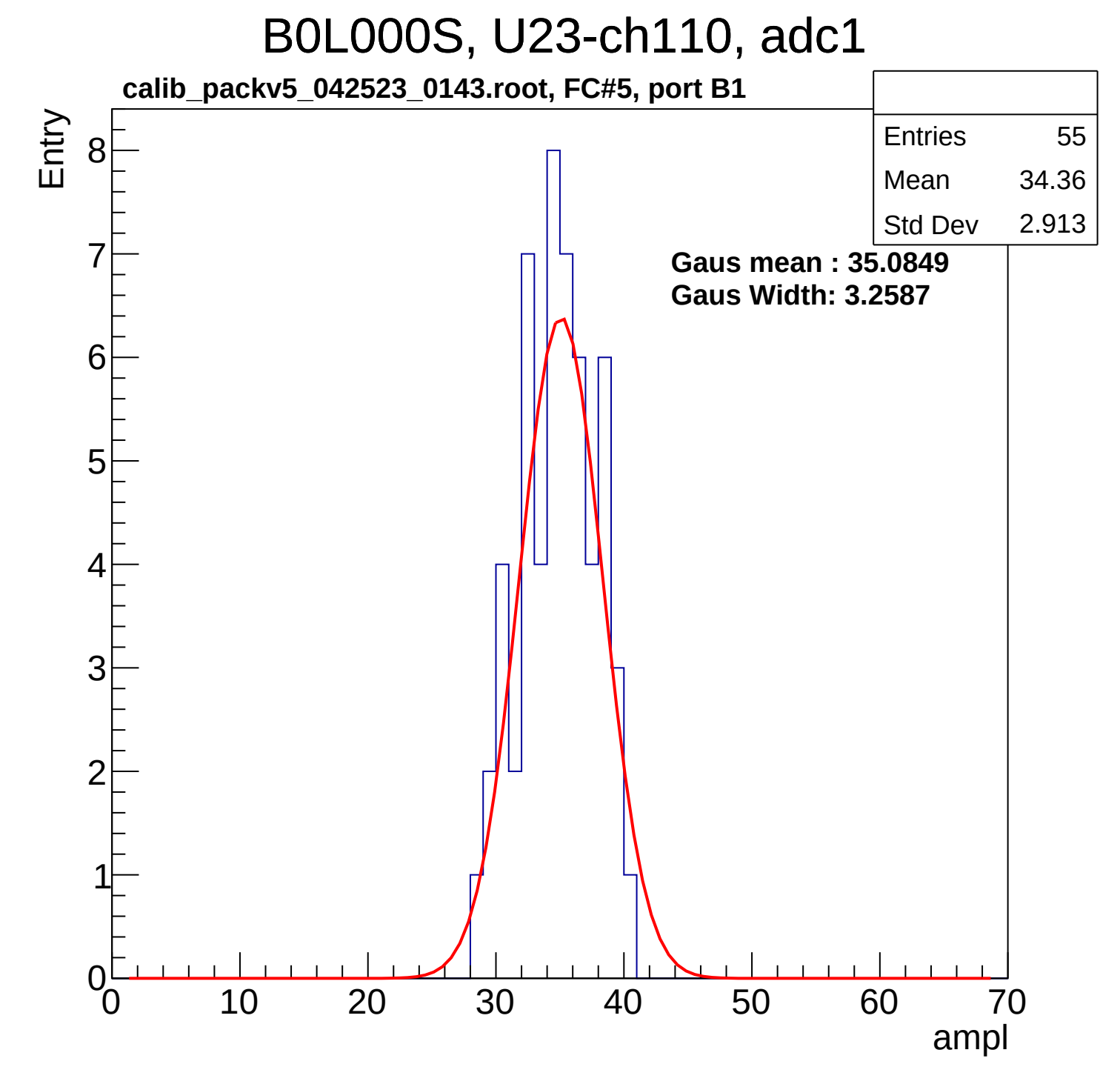

B0L000S, U23-ch109, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.47
Std Dev 3.02
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

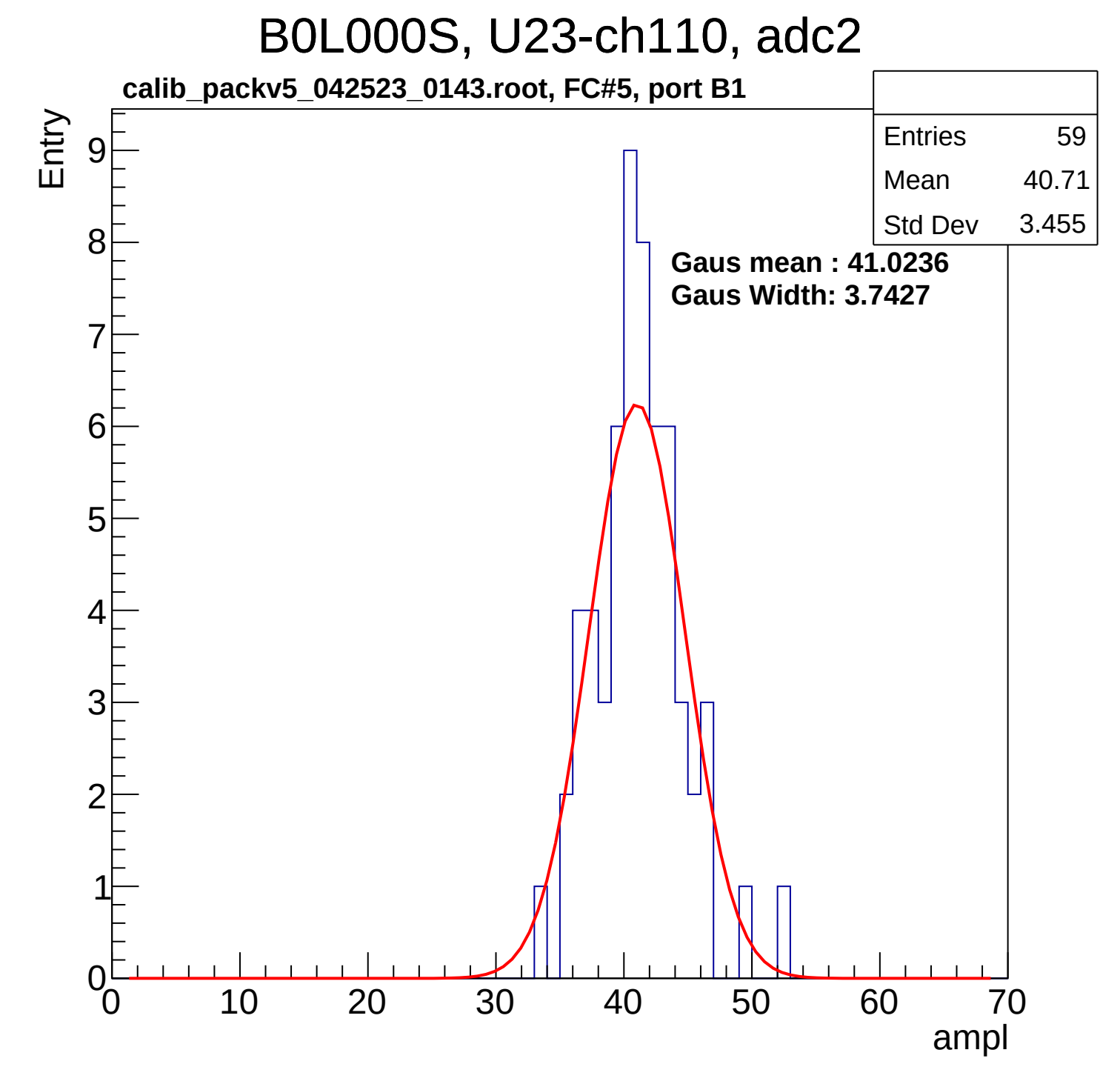

B0L000S, U23-ch109, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.02
Std Dev 9.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

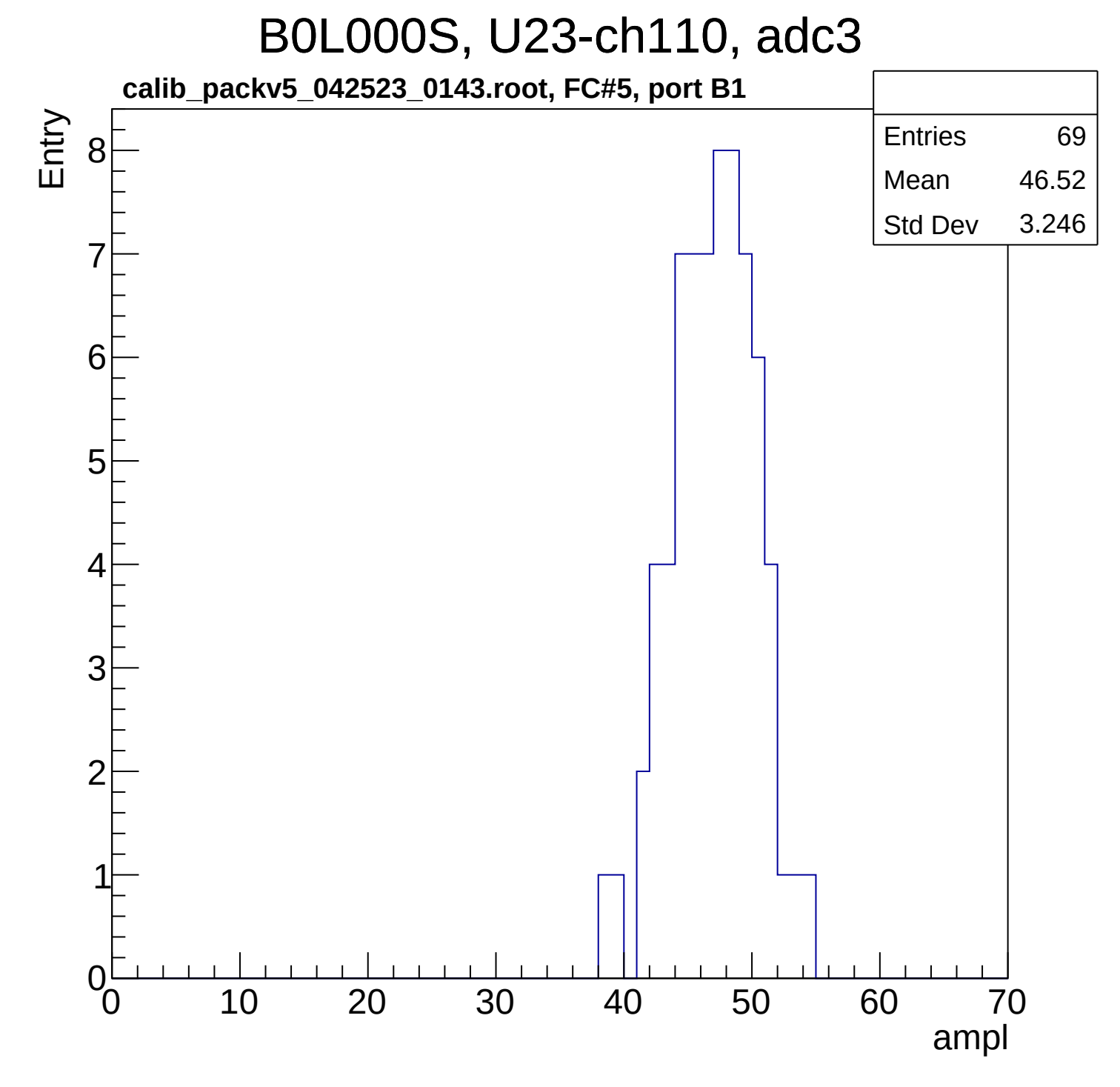

B0L000S, U23-ch109, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 1.549
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

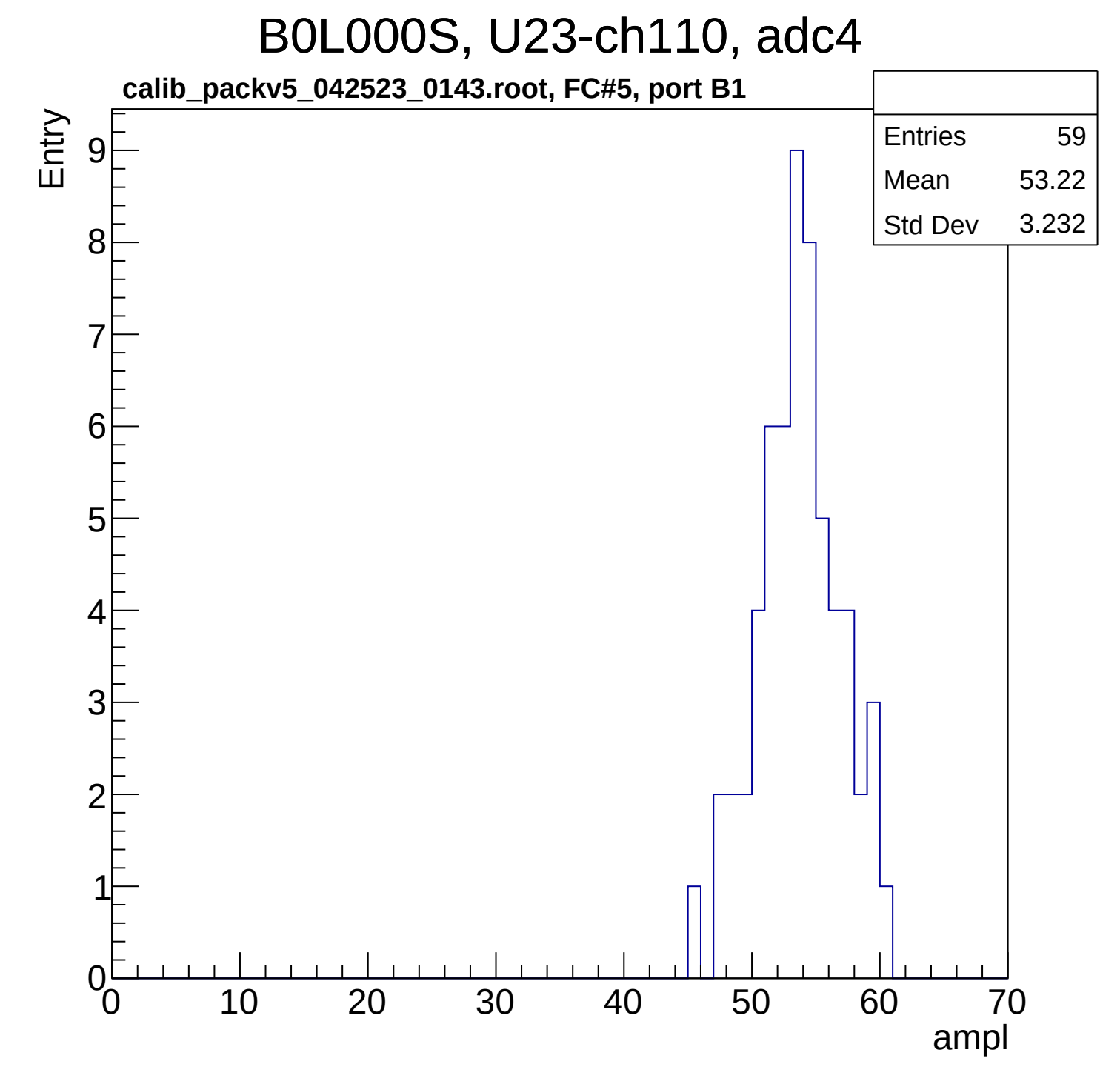

B0L000S, U23-ch109, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

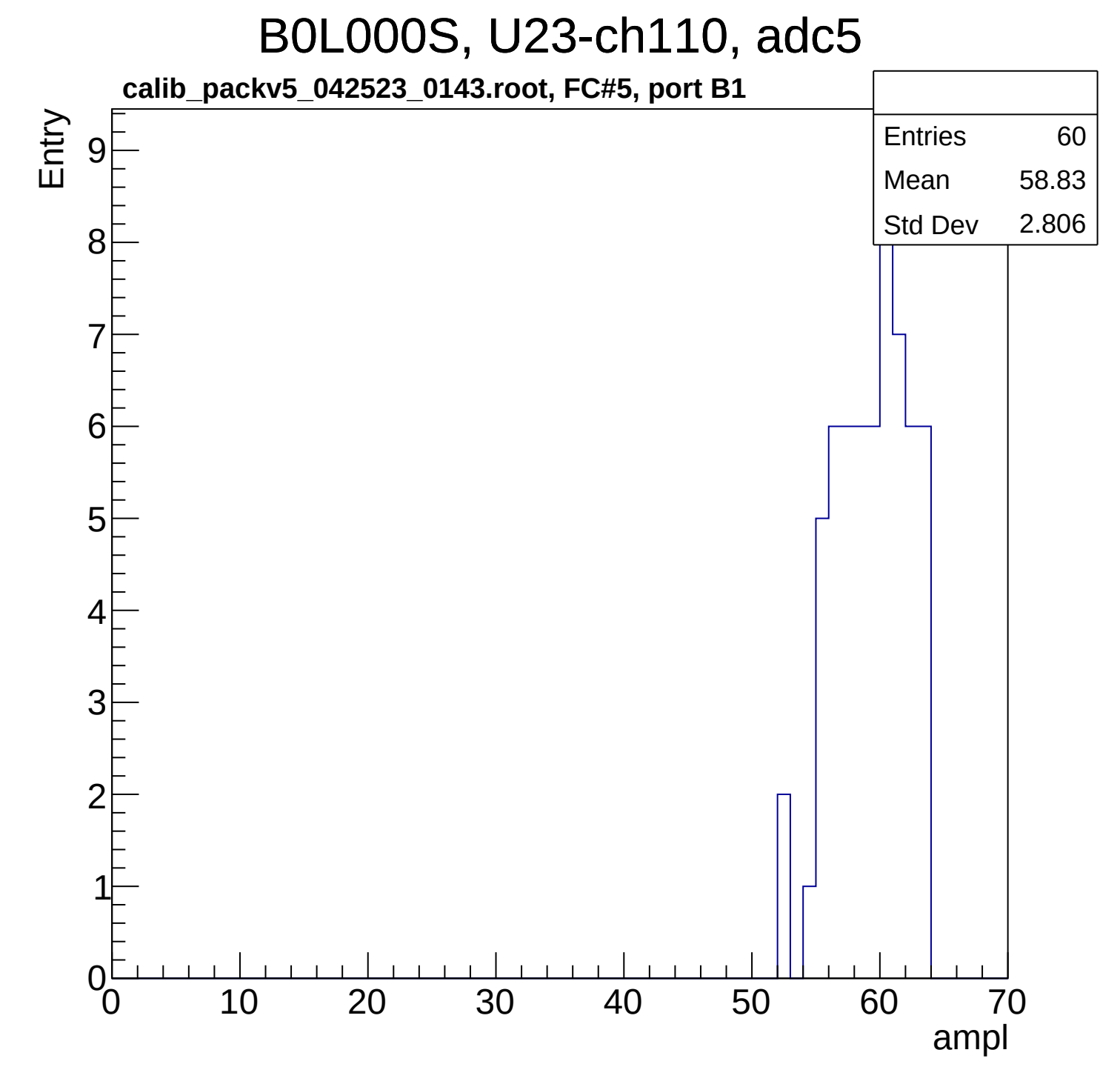

B0L000S, U23-ch110, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 27.58
Std Dev 4.89
Gaus mean : 28.2691
Gaus Width: 3.9190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

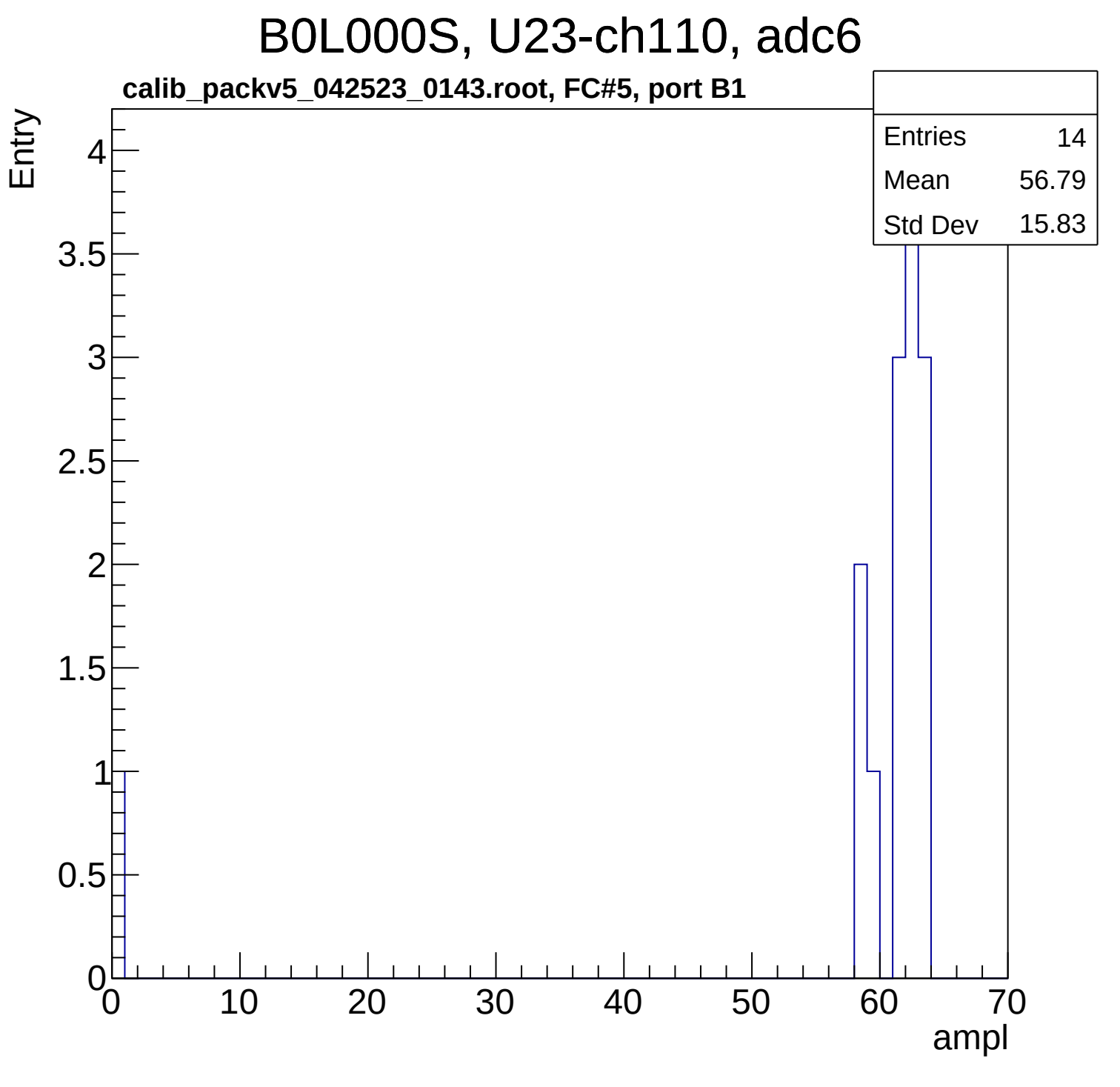

B0L000S, U23-ch110, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 34.36
Std Dev 2.913
Gaus mean : 35.0849
Gaus Width: 3.2587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

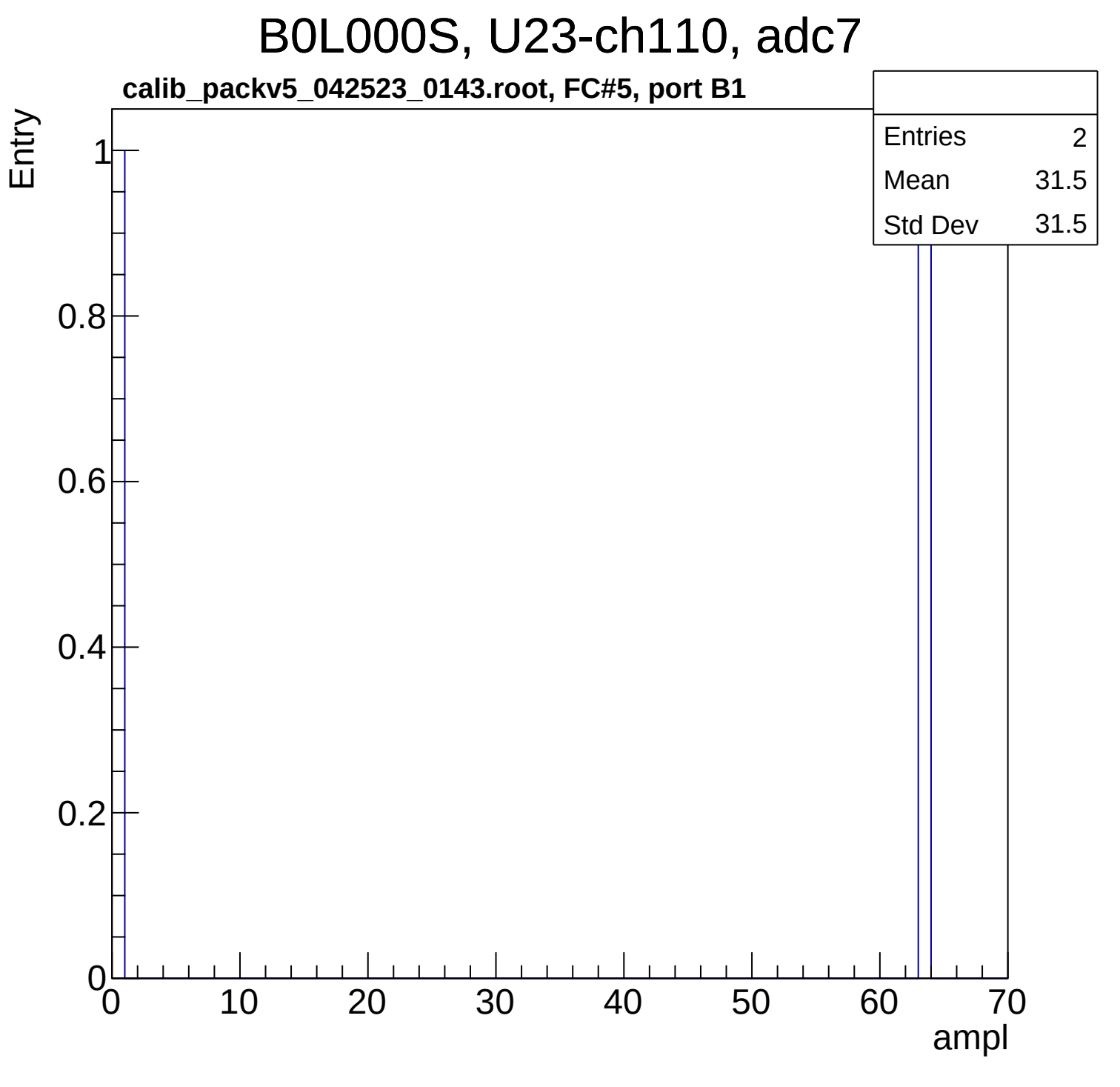

B0L000S, U23-ch110, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 40.71
Std Dev 3.455
Gaus mean : 41.0236
Gaus Width: 3.7427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

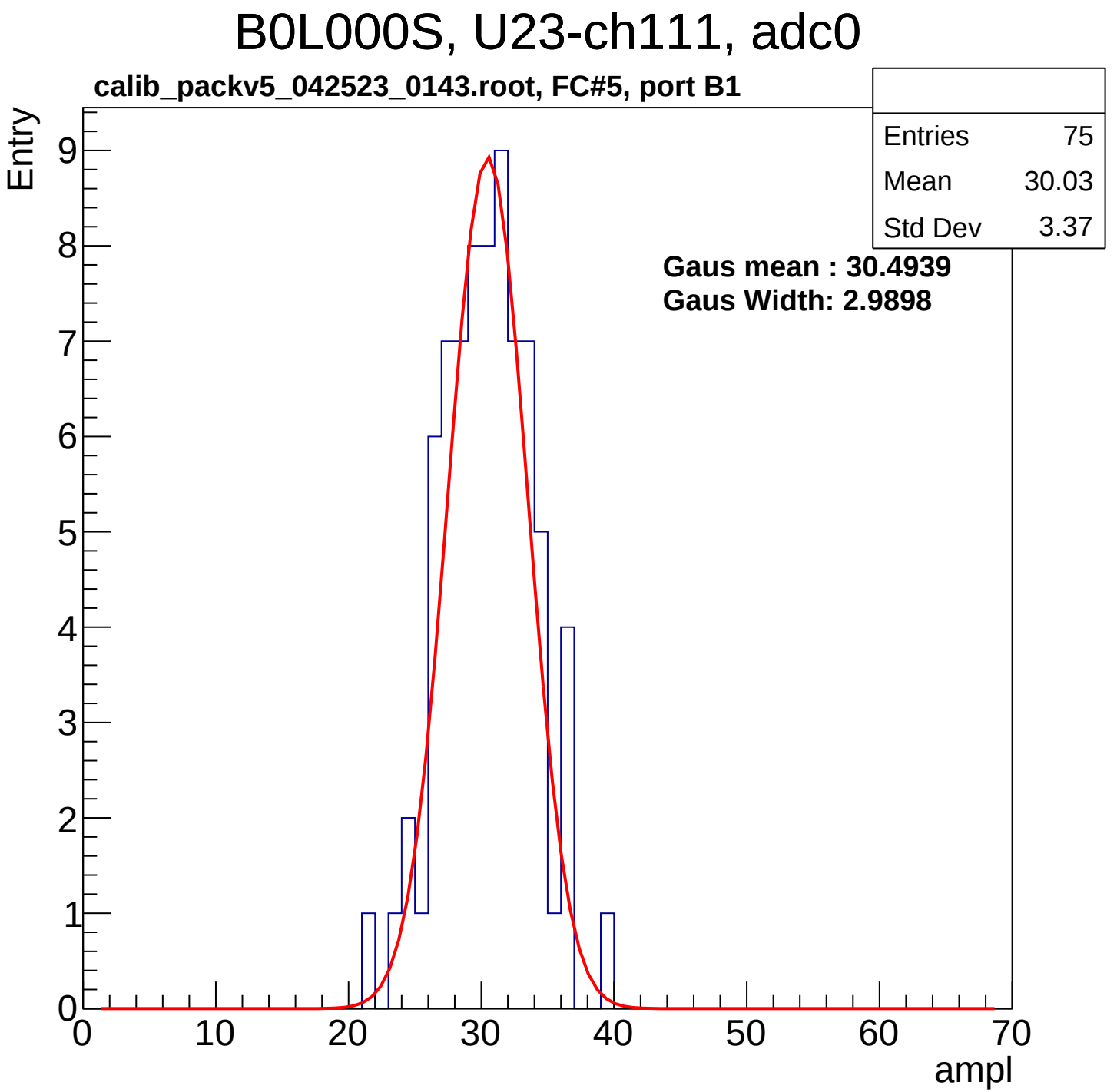

B0L000S, U23-ch110, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 46.52
Std Dev 3.246
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

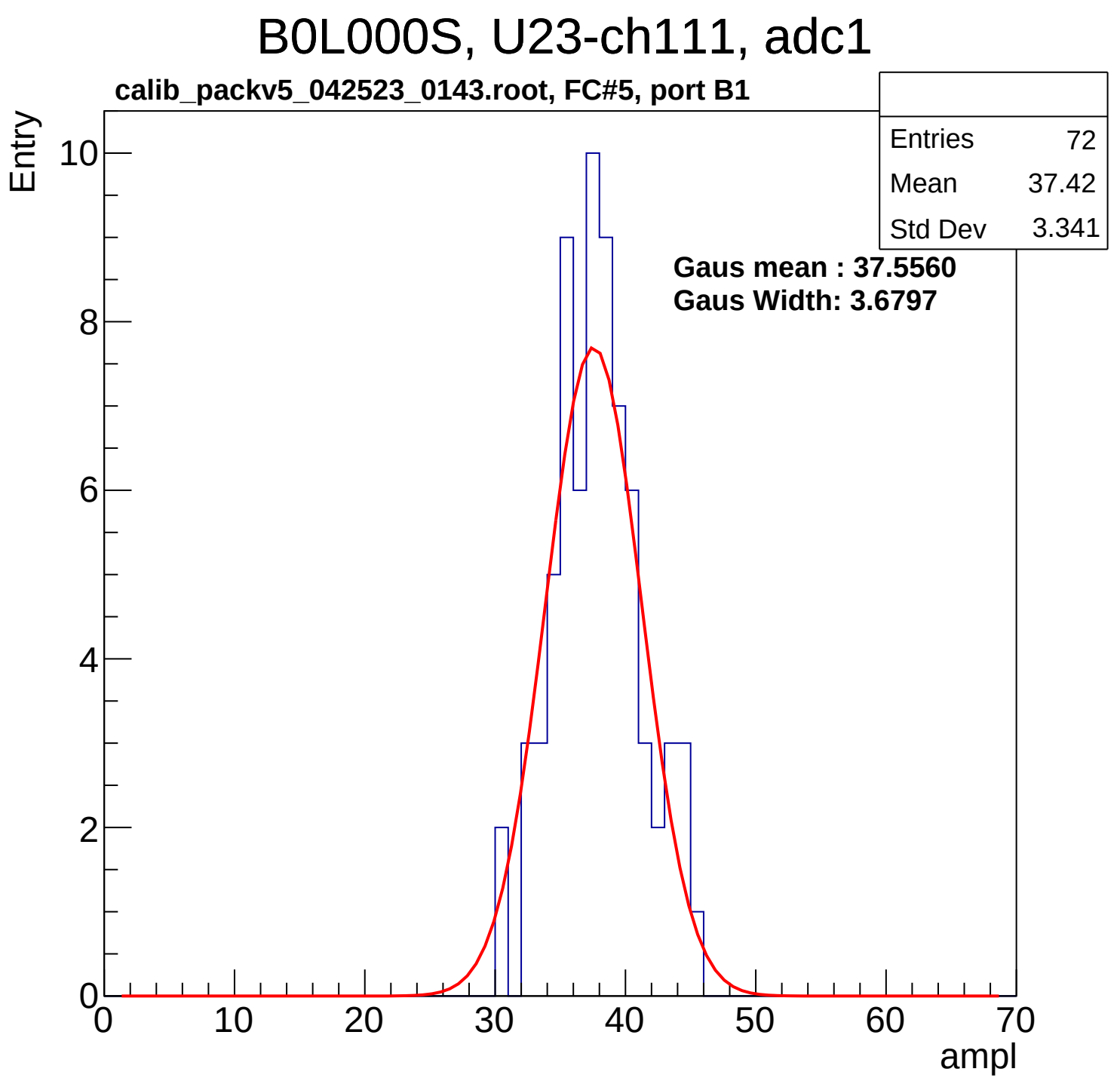

B0L000S, U23-ch110, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 53.22
Std Dev 3.232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

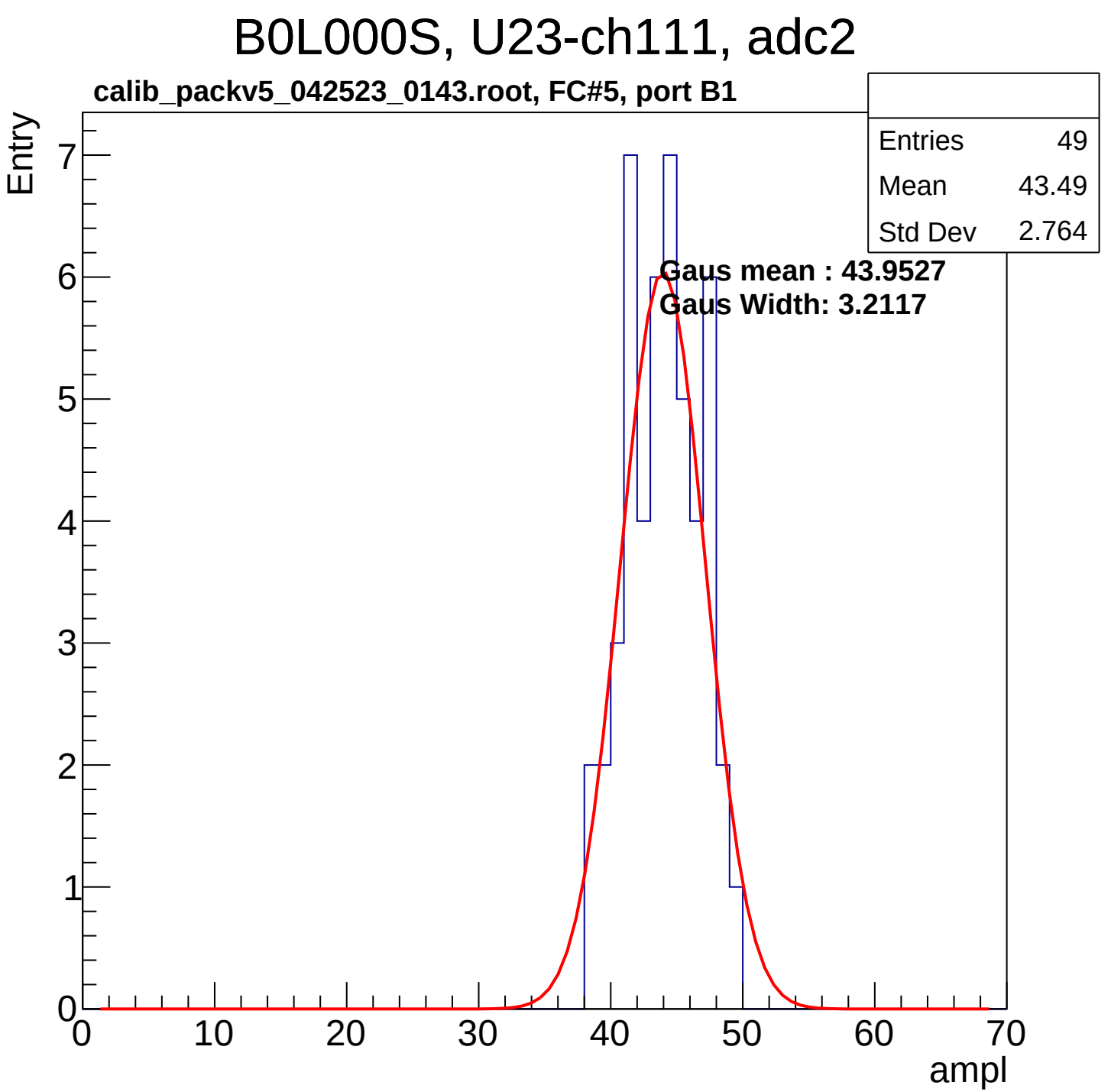

B0L000S, U23-ch110, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58.83
Std Dev 2.806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

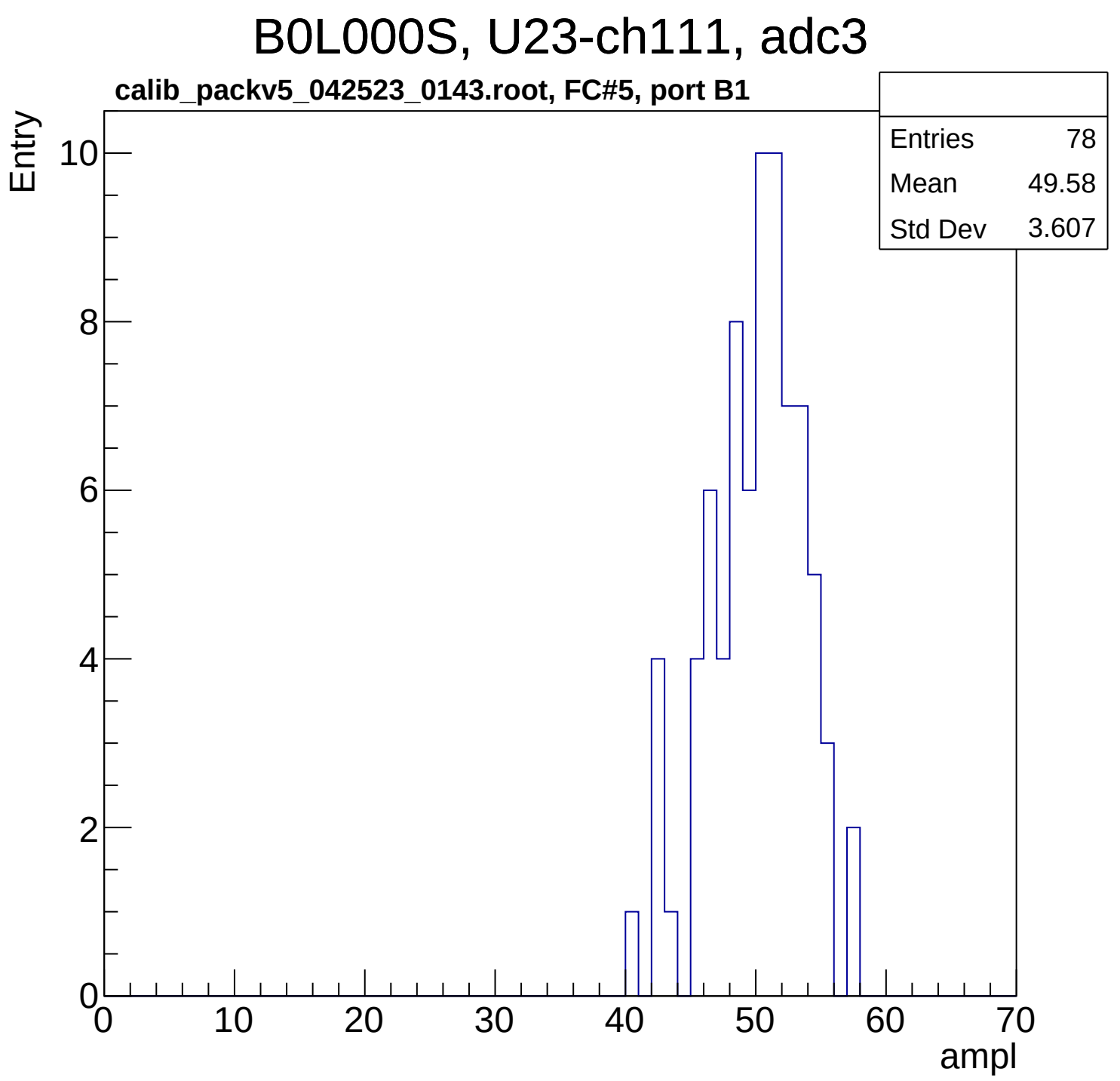

B0L000S, U23-ch110, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 56.79
Std Dev 15.83
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

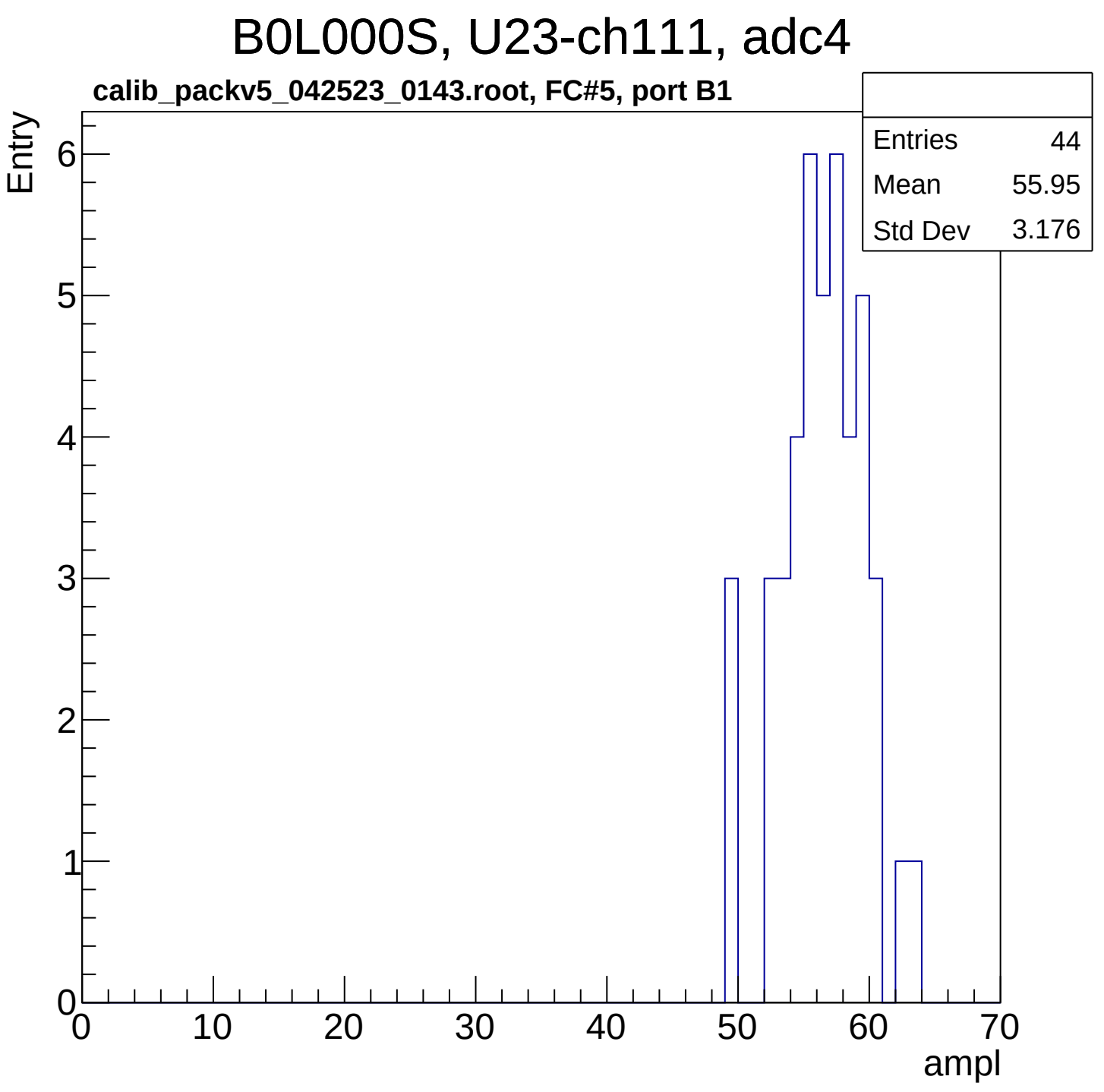

B0L000S, U23-ch110, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

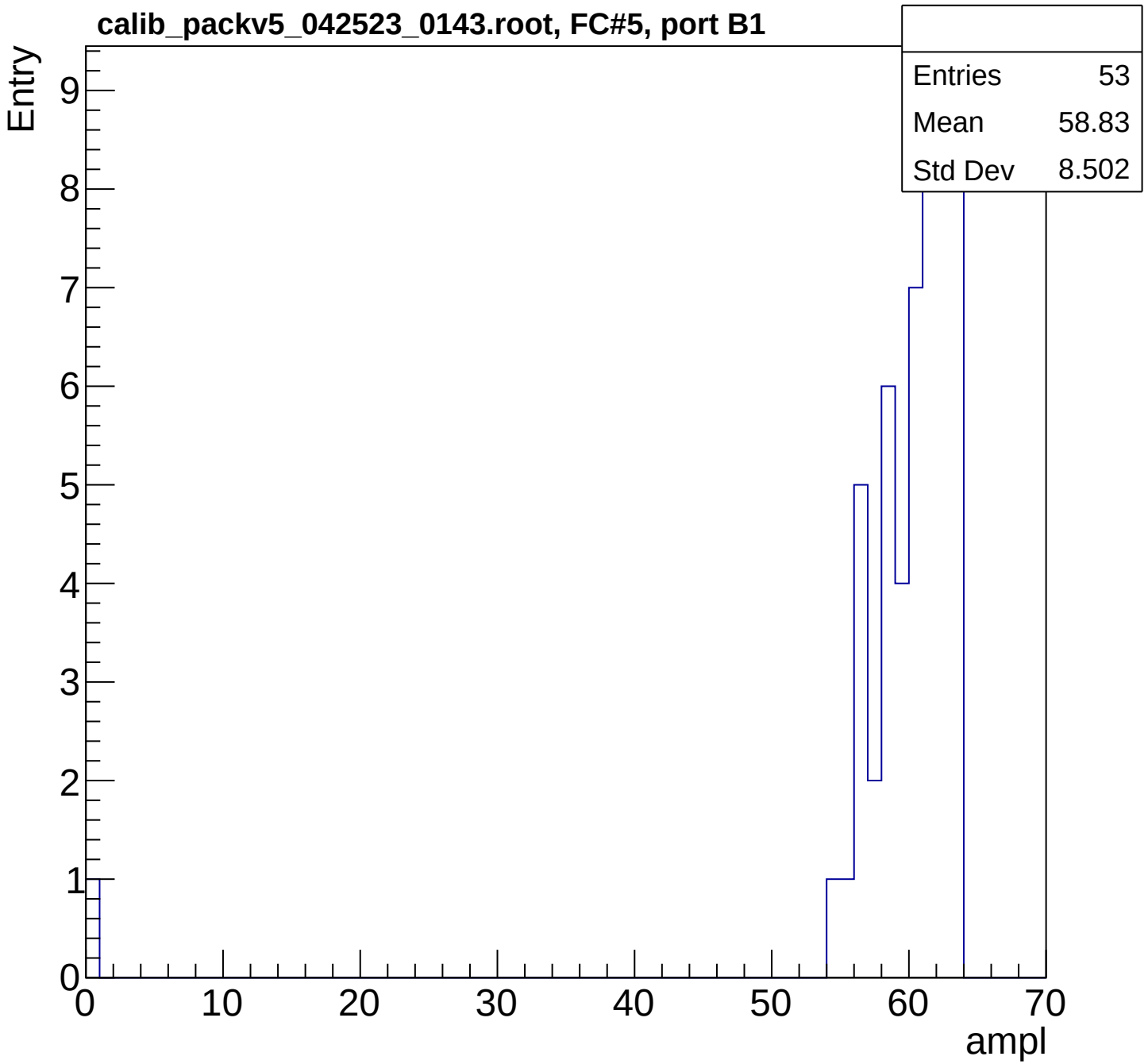

B0L000S, U23-ch111, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 30.03
Std Dev 3.37
Gaus mean : 30.4939
Gaus Width: 2.9898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

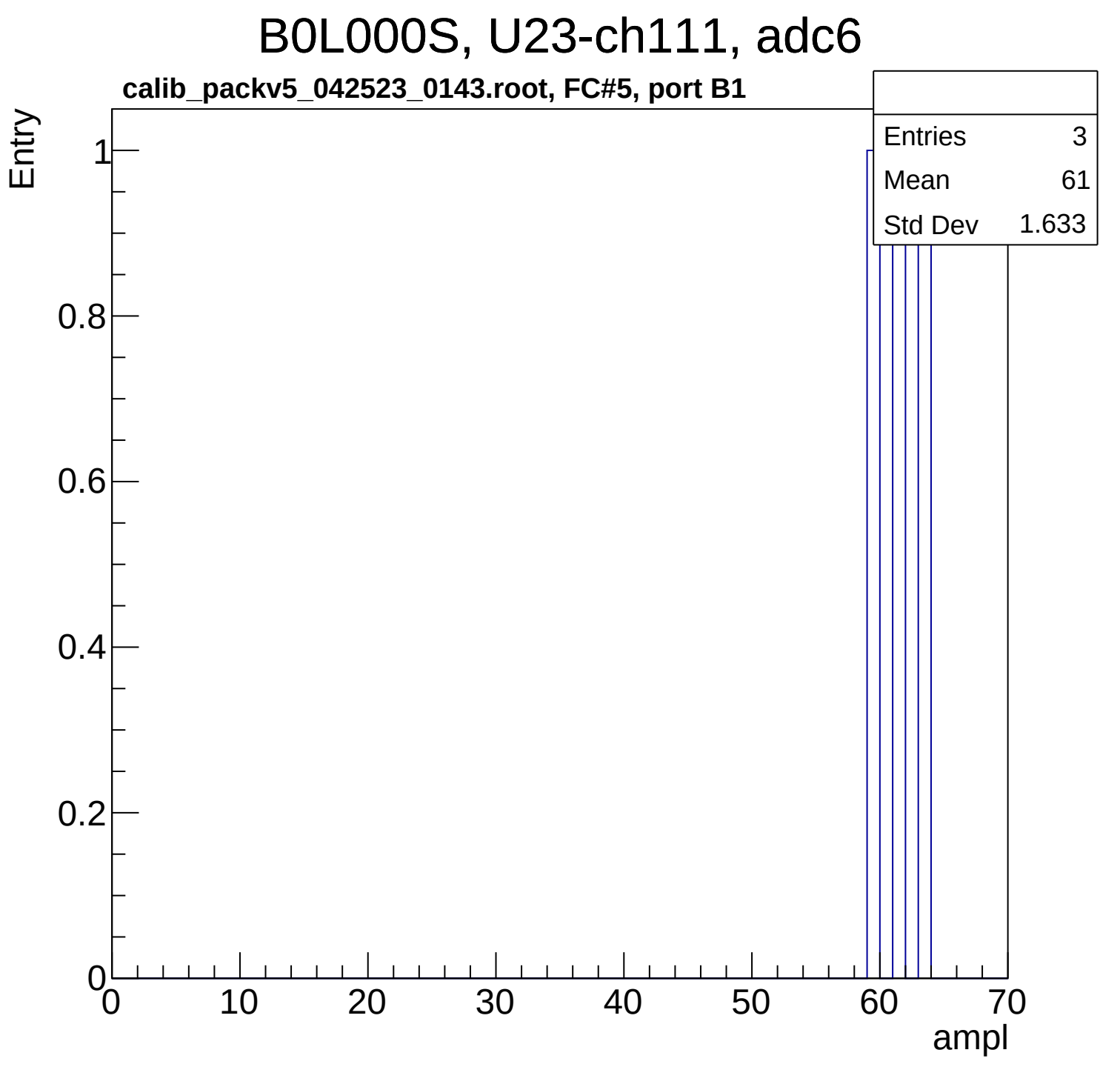

B0L000S, U23-ch111, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.42
Std Dev 3.341
Gaus mean : 37.5560
Gaus Width: 3.6797
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

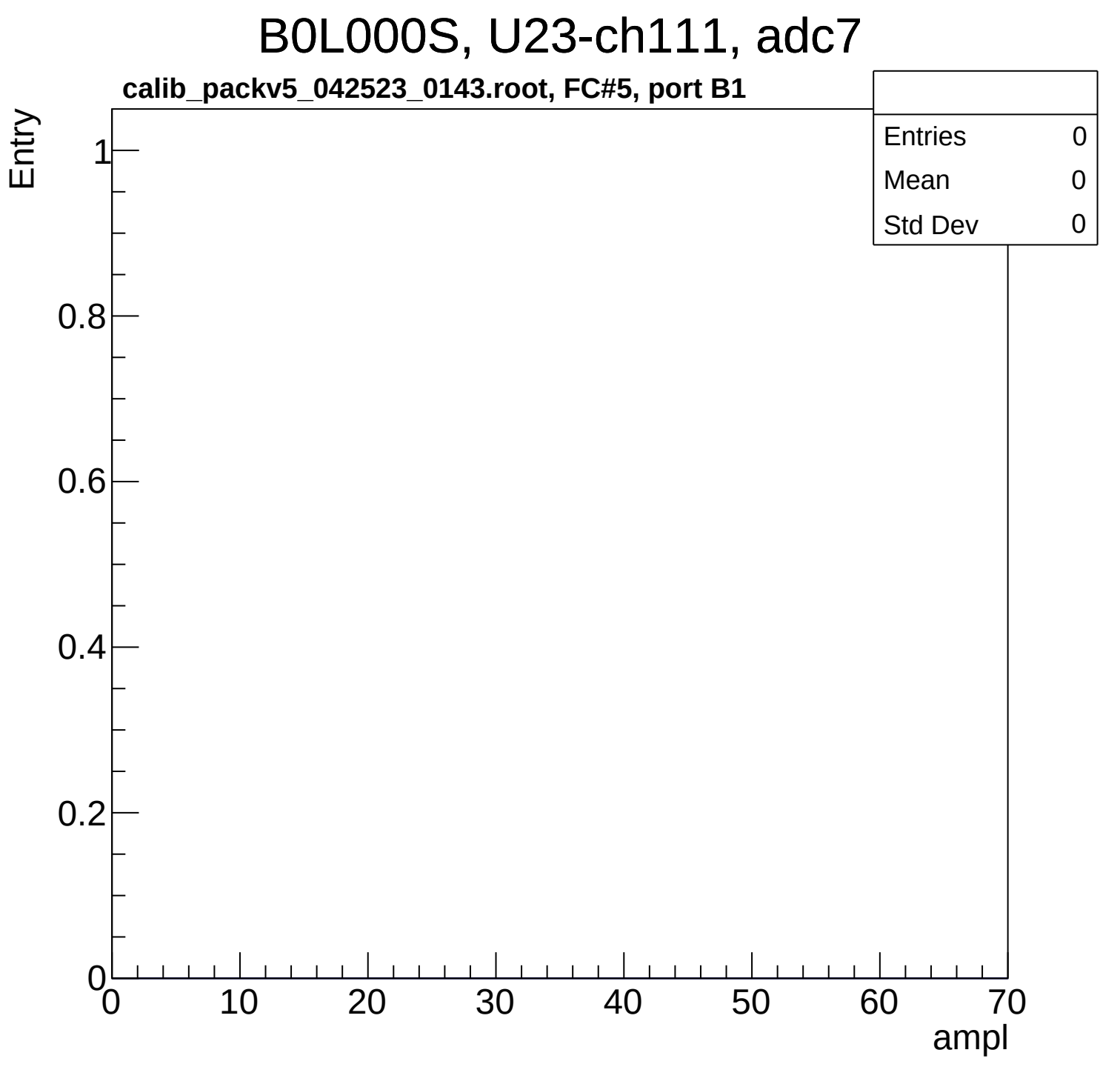

B0L000S, U23-ch111, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 43.49
Std Dev 2.764
Gaus mean : 43.9527
Gaus Width: 3.2117
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

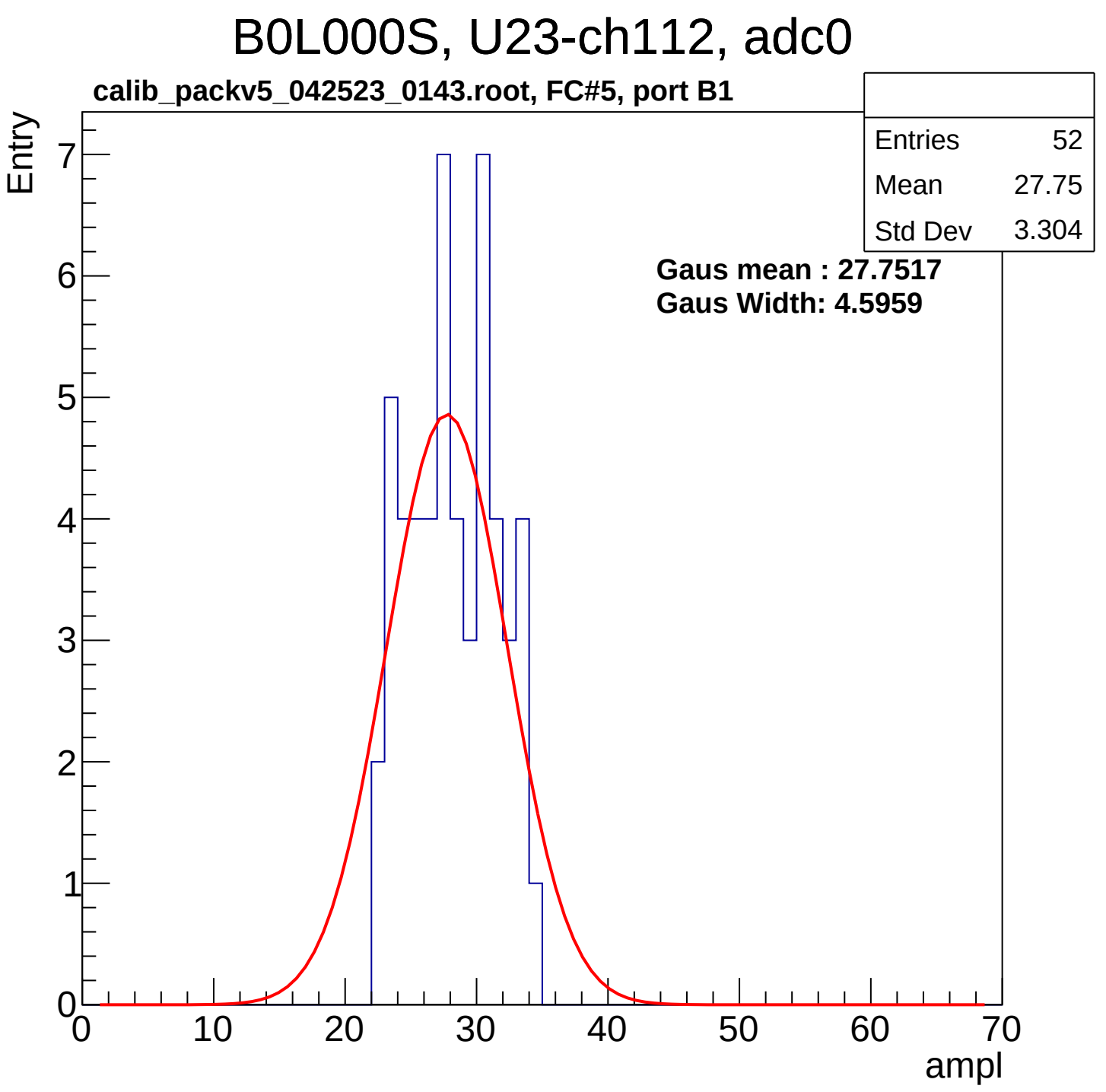

B0L000S, U23-ch111, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 49.58
Std Dev 3.607
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

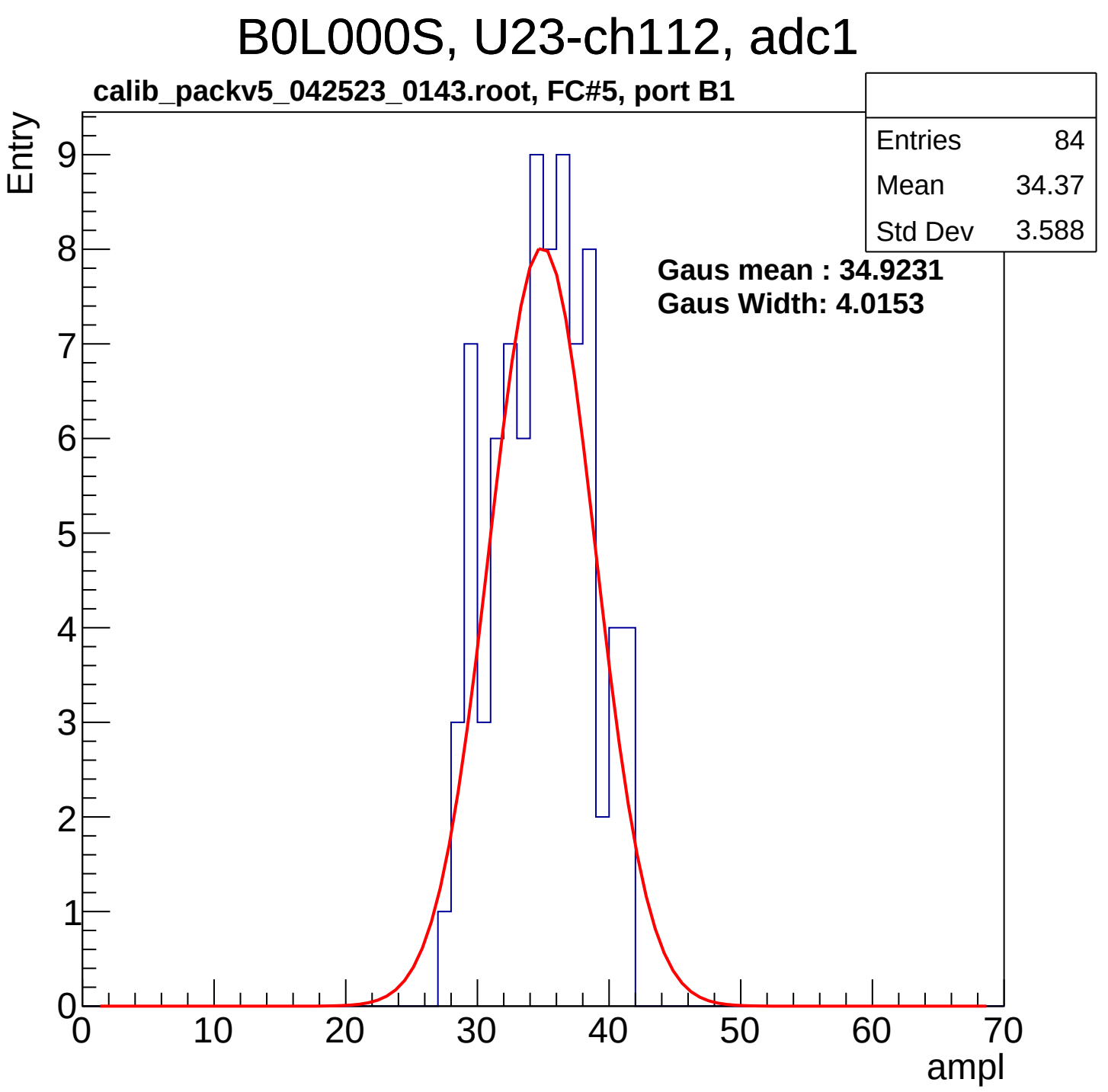

B0L000S, U23-ch111, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 55.95
Std Dev 3.176
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

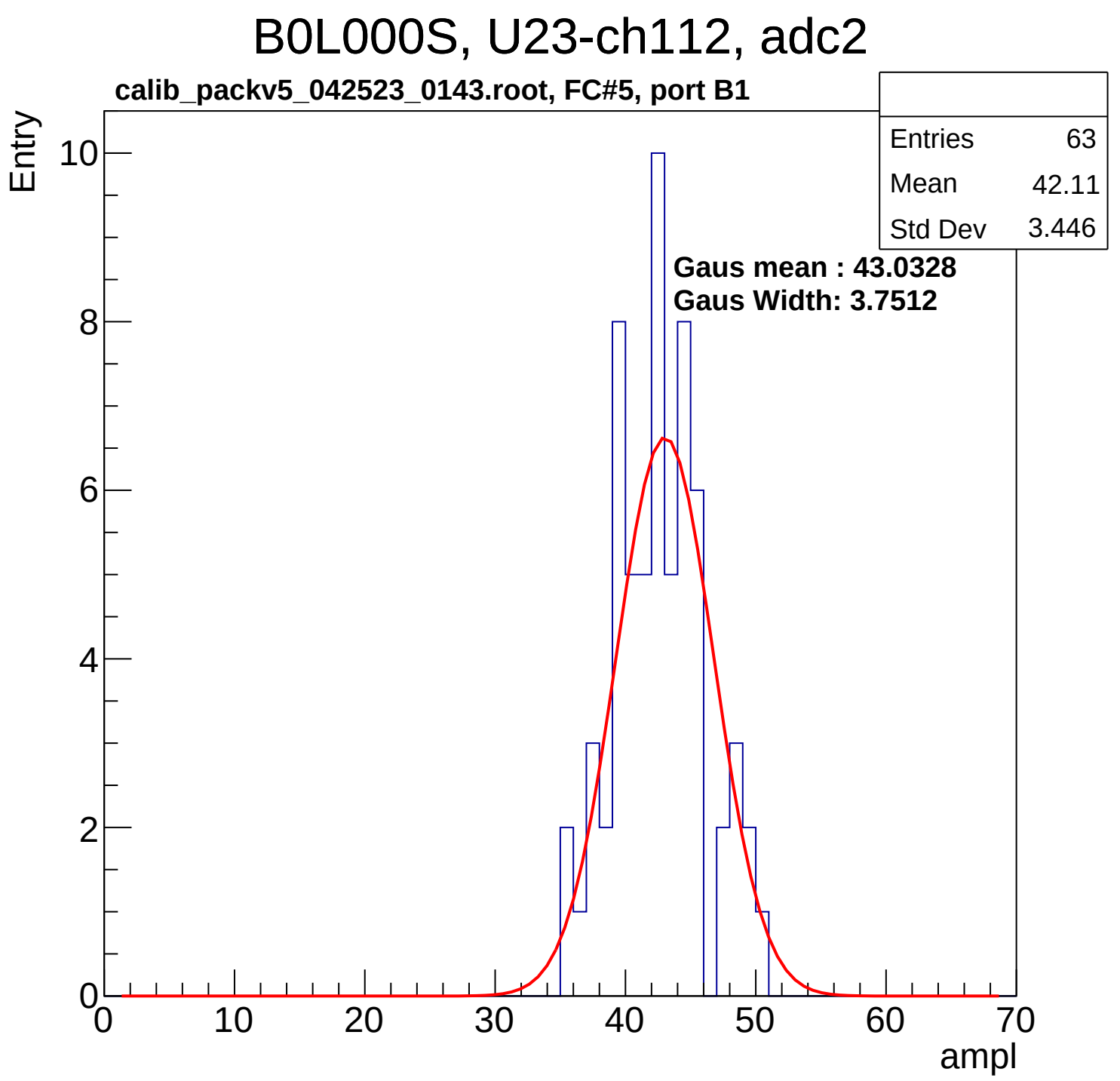

B0L000S, U23-ch111, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.83
Std Dev 8.502
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

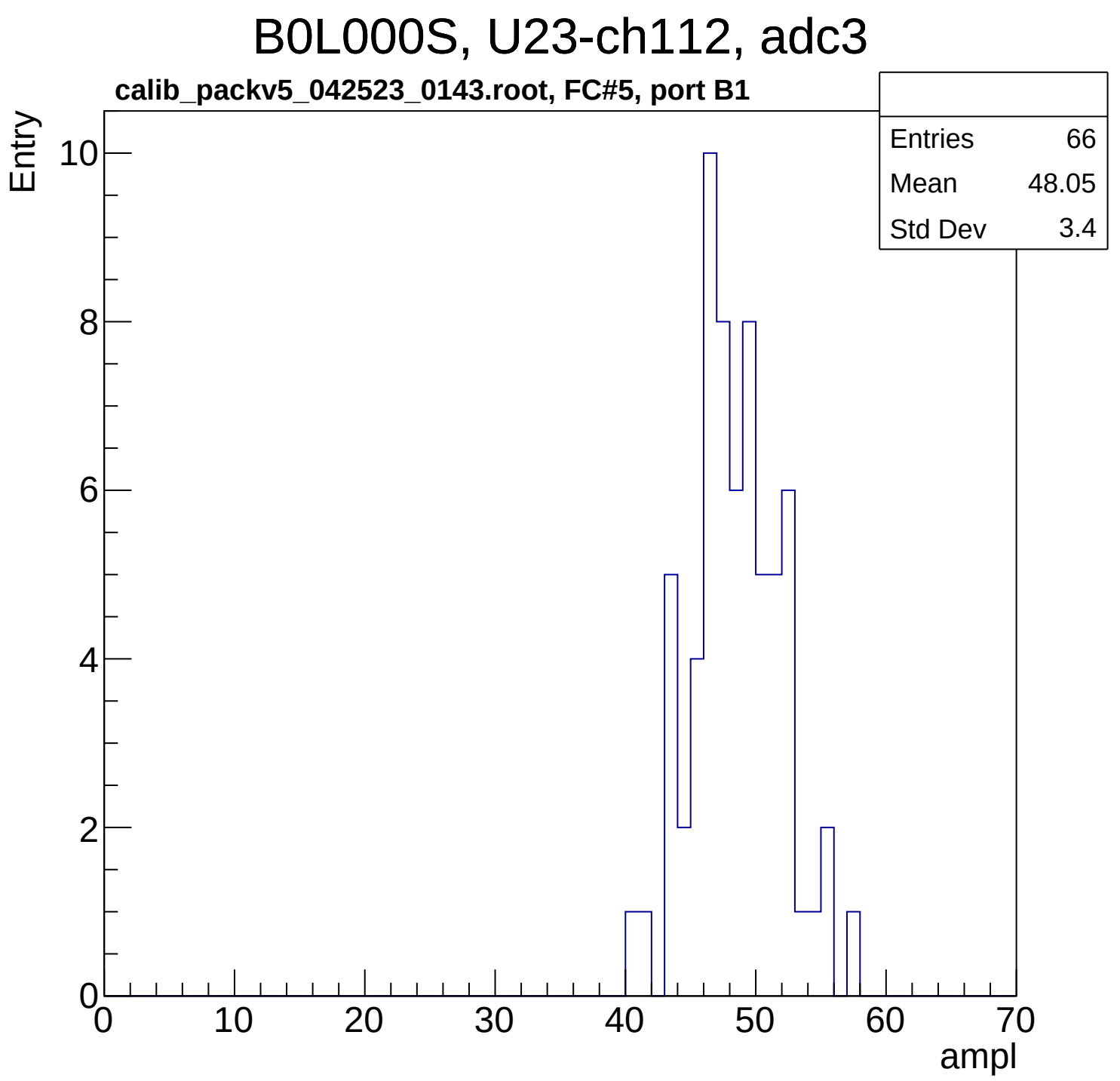

B0L000S, U23-ch111, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61
Std Dev 1.633
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

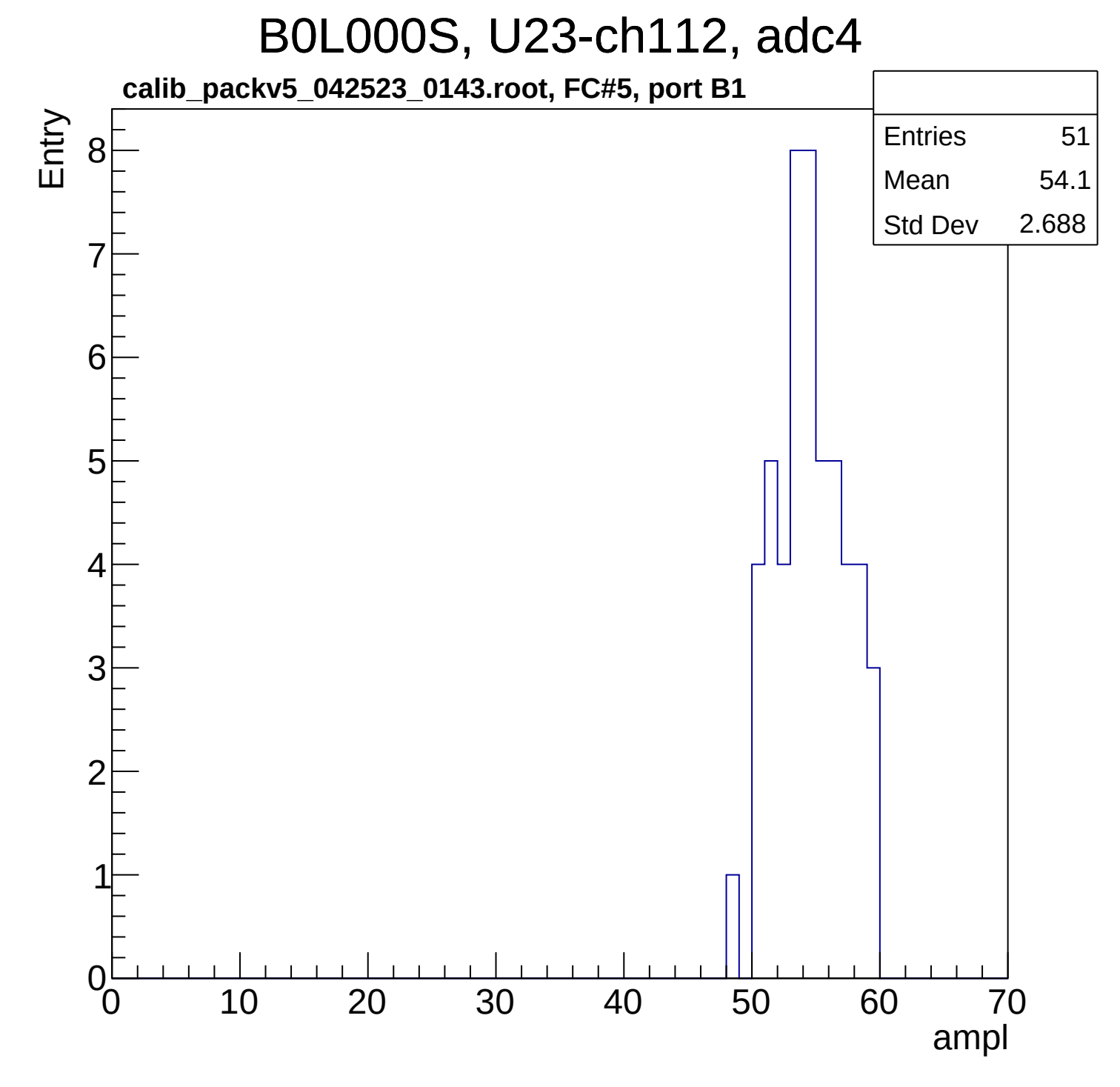

B0L000S, U23-ch111, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

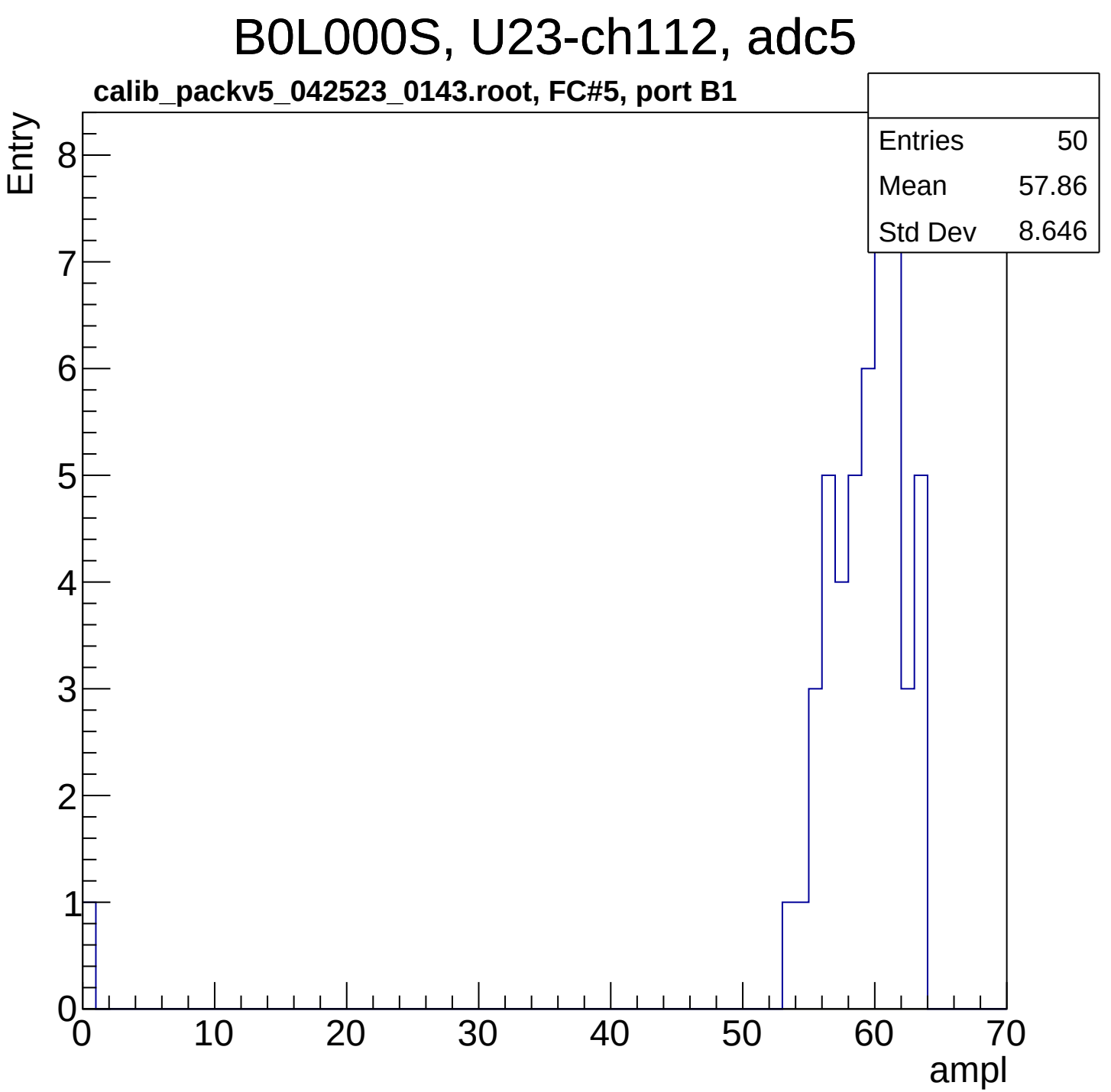

B0L000S, U23-ch112, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 27.75
Std Dev 3.304
Gaus mean : 27.7517
Gaus Width: 4.5959
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

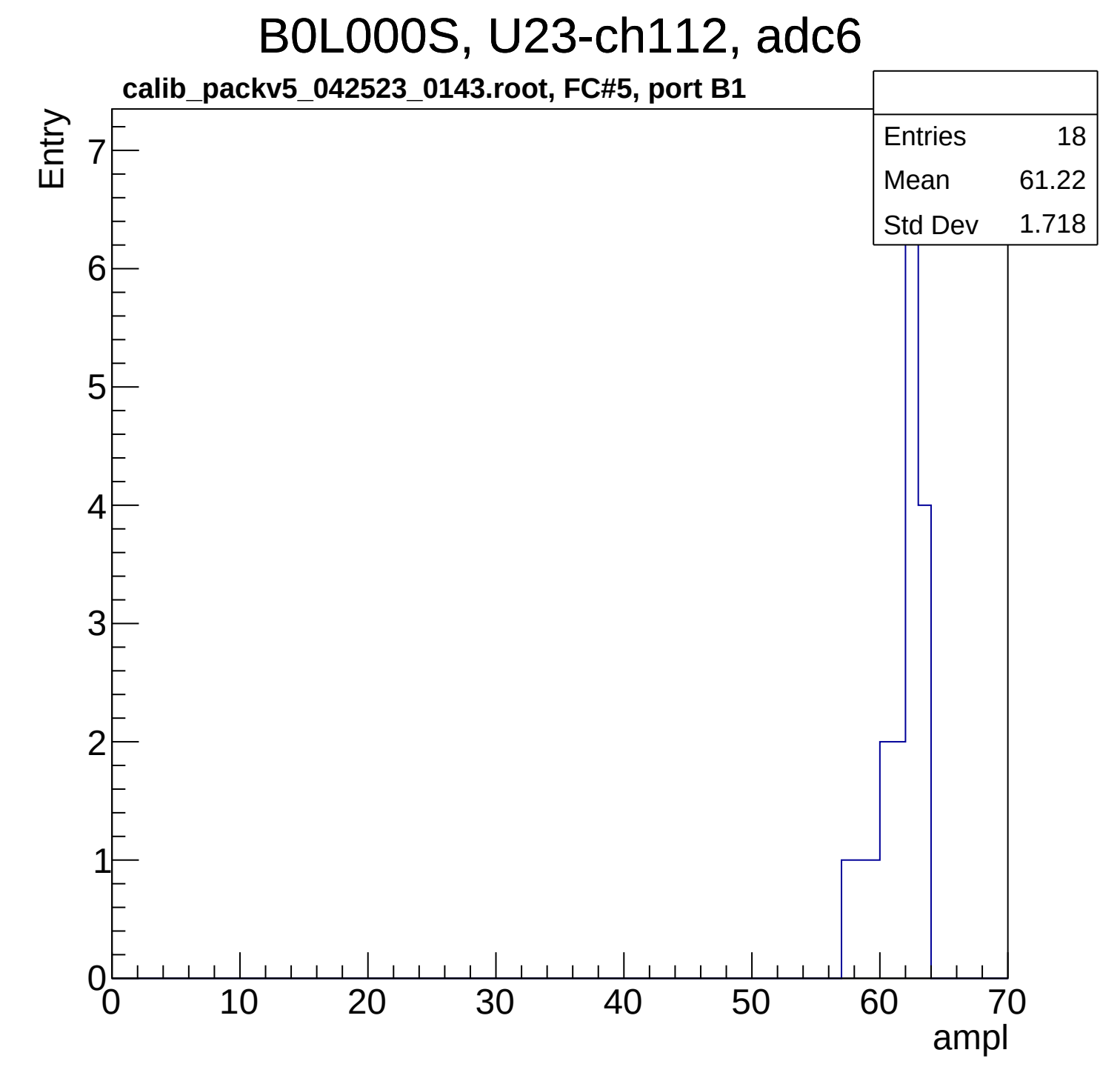

B0L000S, U23-ch112, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 34.37
Std Dev 3.588
Gaus mean : 34.9231
Gaus Width: 4.0153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

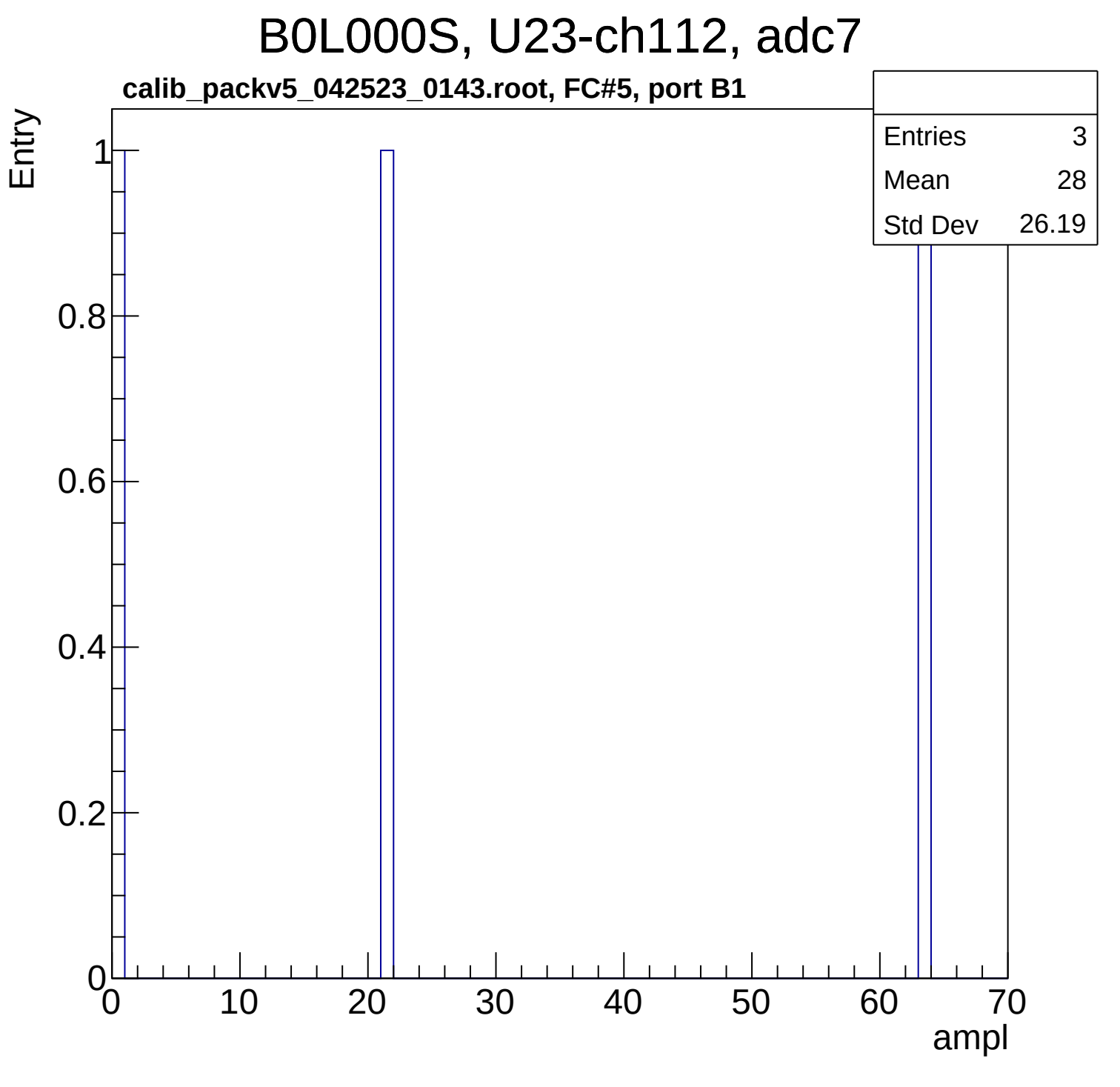

B0L000S, U23-ch112, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.11
Std Dev 3.446
Gaus mean : 43.0328
Gaus Width: 3.7512
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

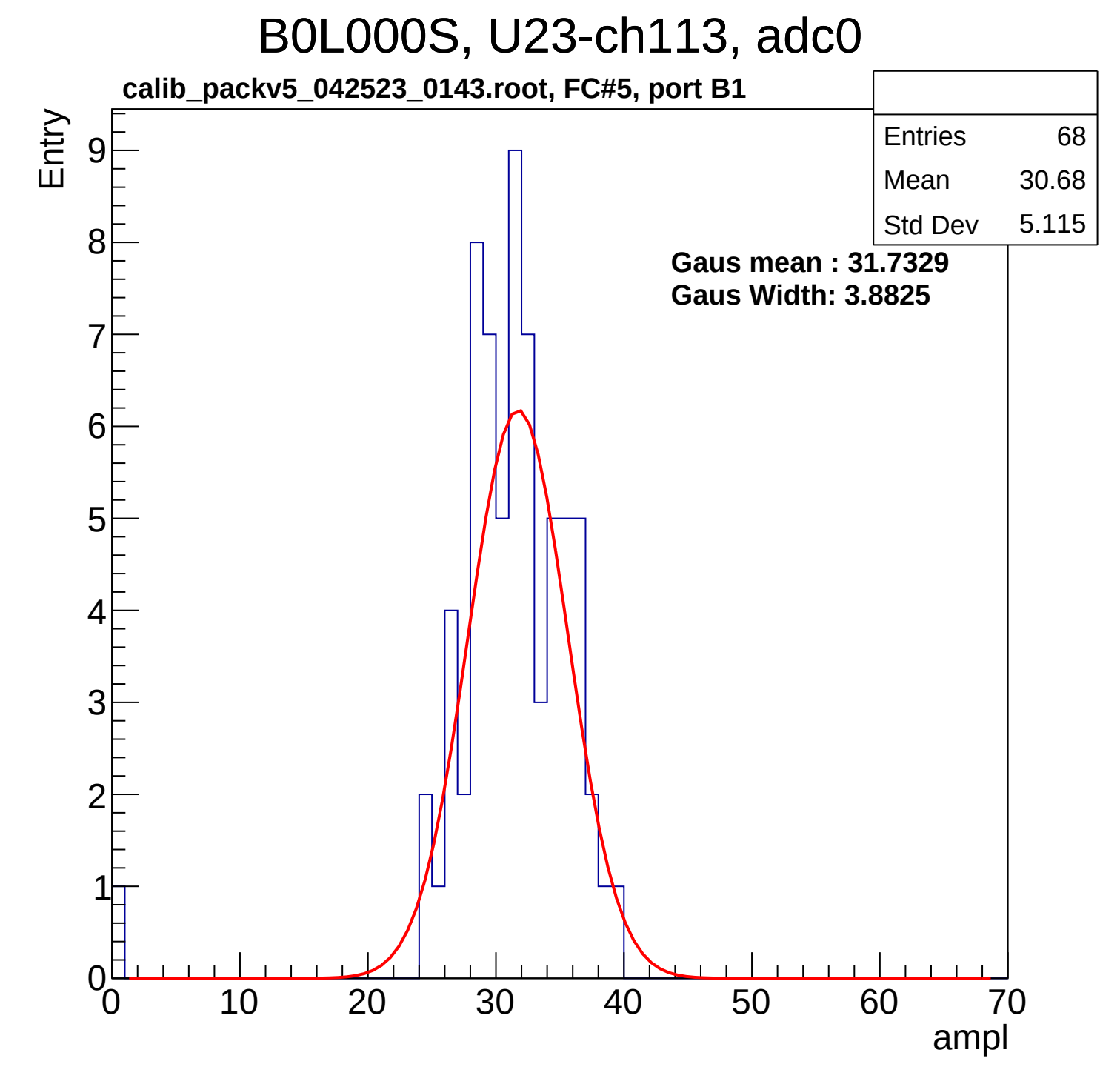

B0L000S, U23-ch112, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 48.05
Std Dev 3.4
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

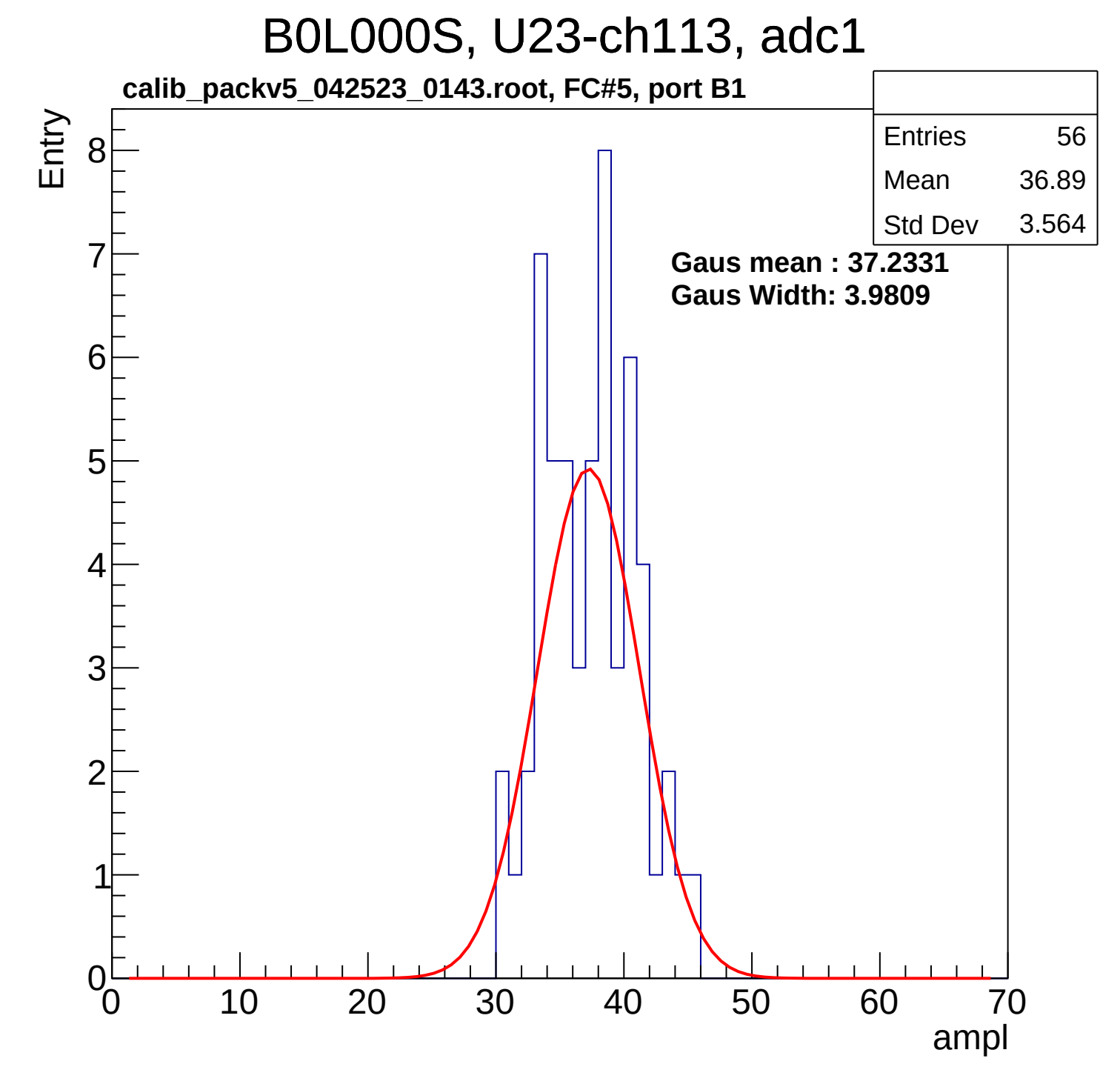

B0L000S, U23-ch112, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.1
Std Dev 2.688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

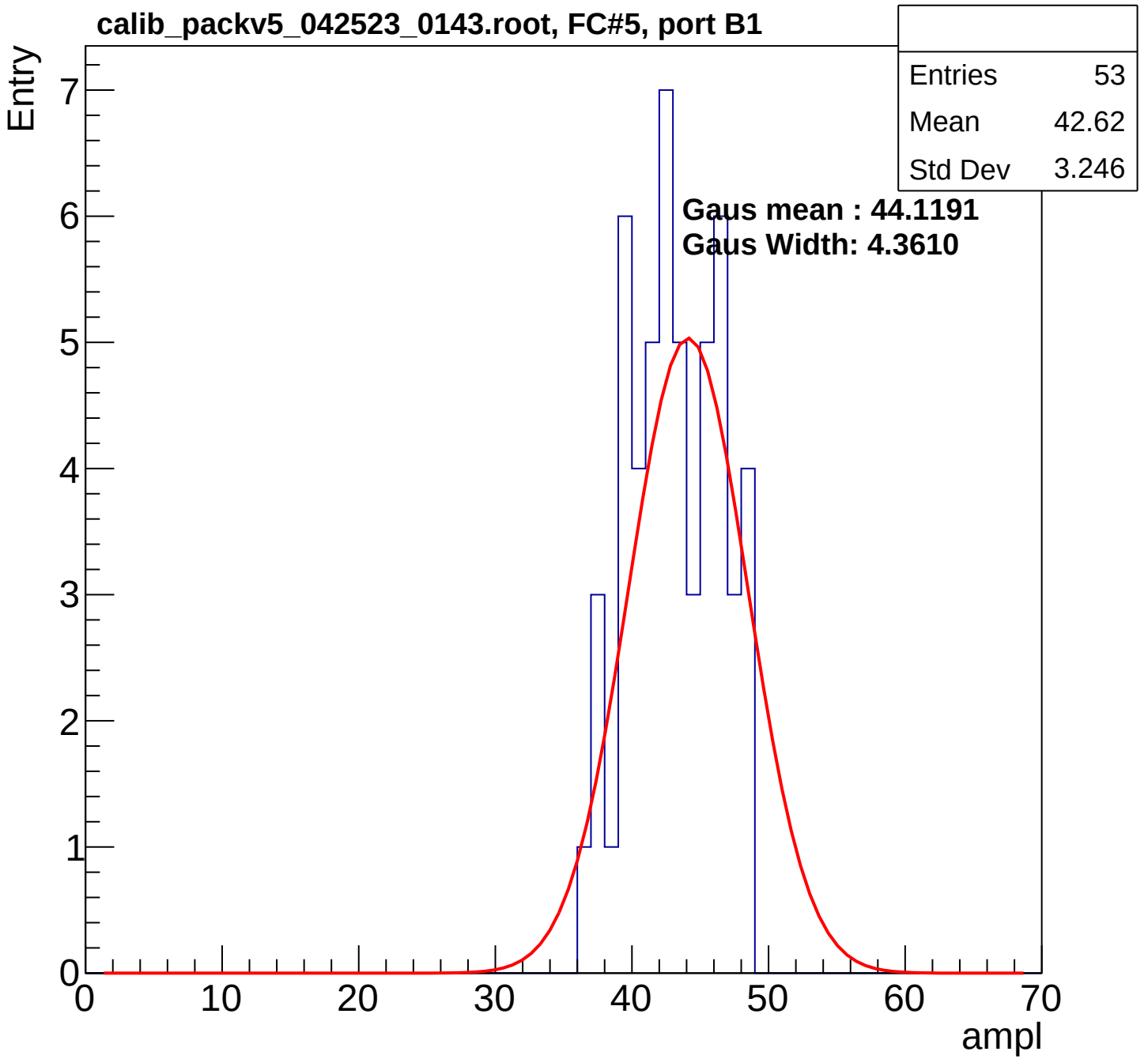

B0L000S, U23-ch112, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.86
Std Dev 8.646
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

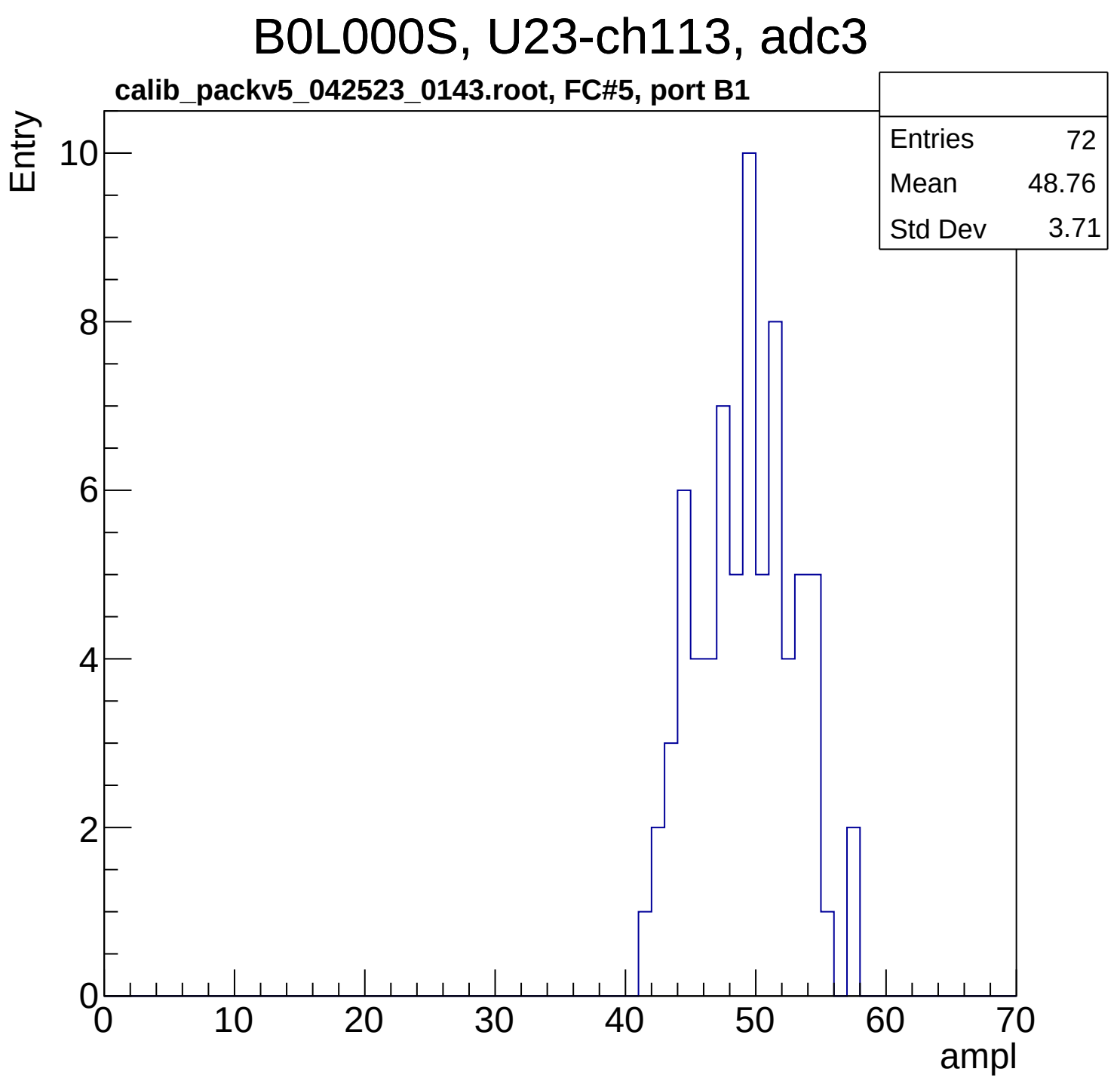

B0L000S, U23-ch112, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 61.22
Std Dev 1.718
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

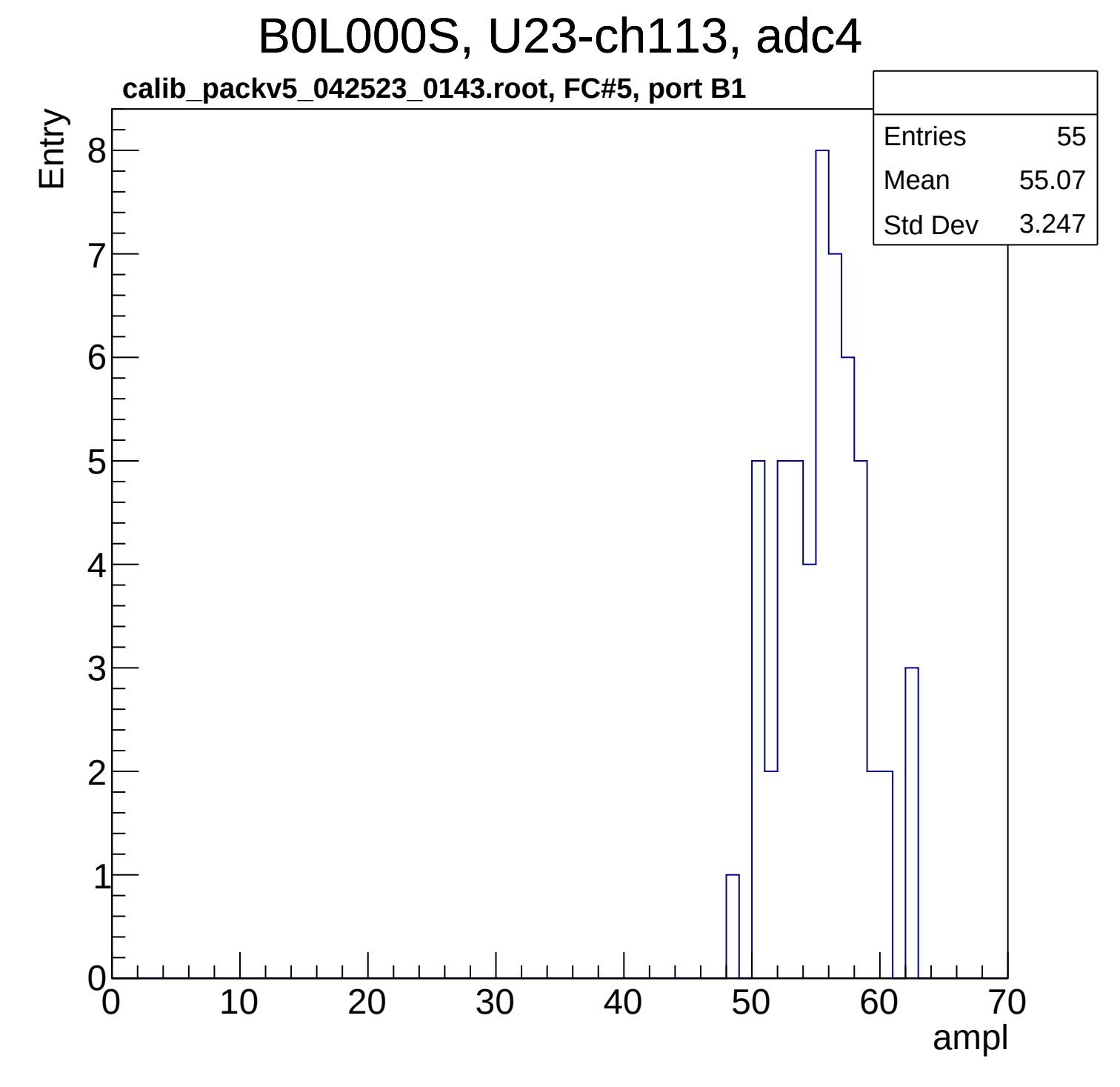

B0L000S, U23-ch112, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 28
Std Dev 26.19
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

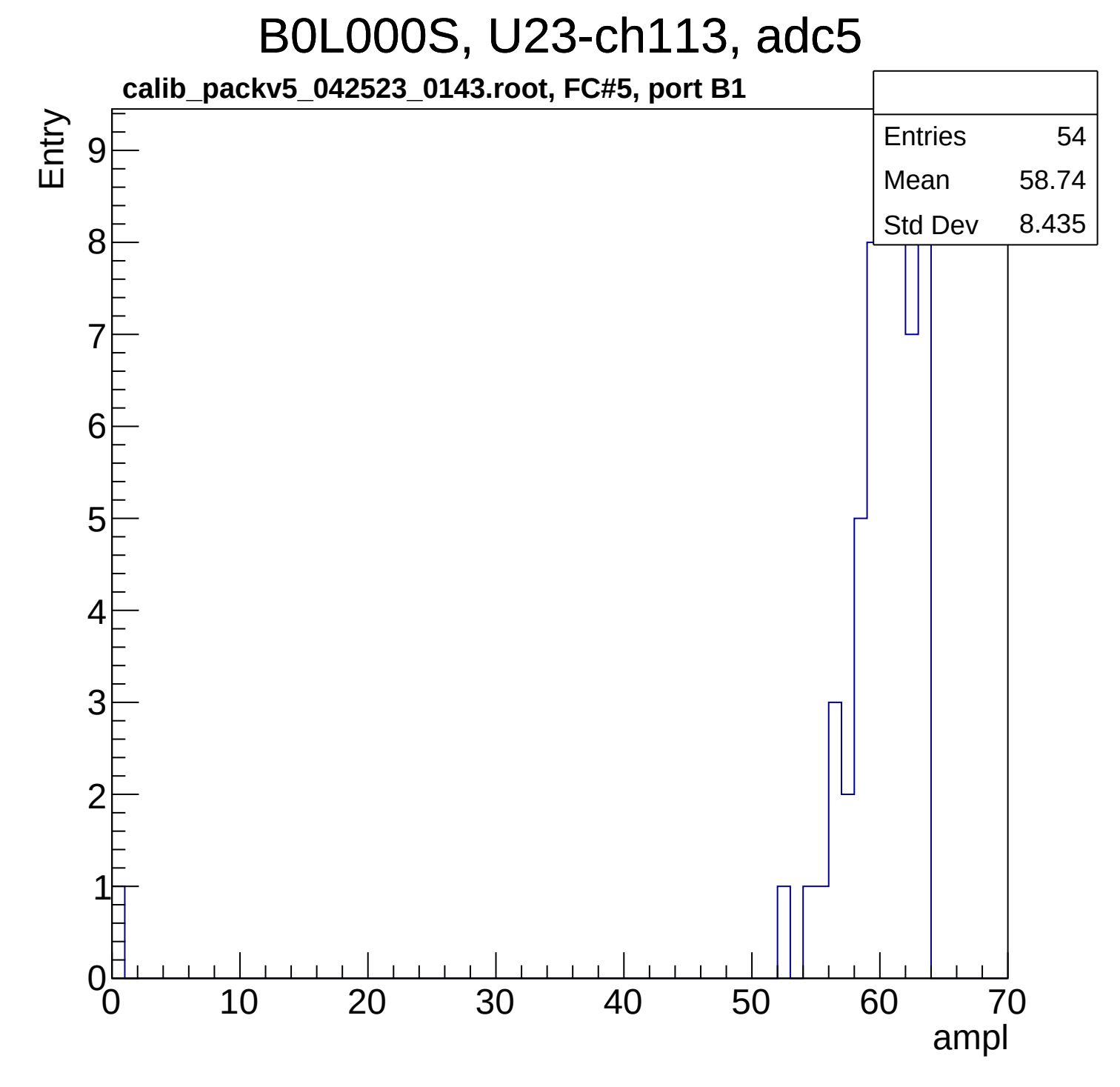

B0L000S, U23-ch113, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.68
Std Dev 5.115
Gaus mean : 31.7329
Gaus Width: 3.8825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

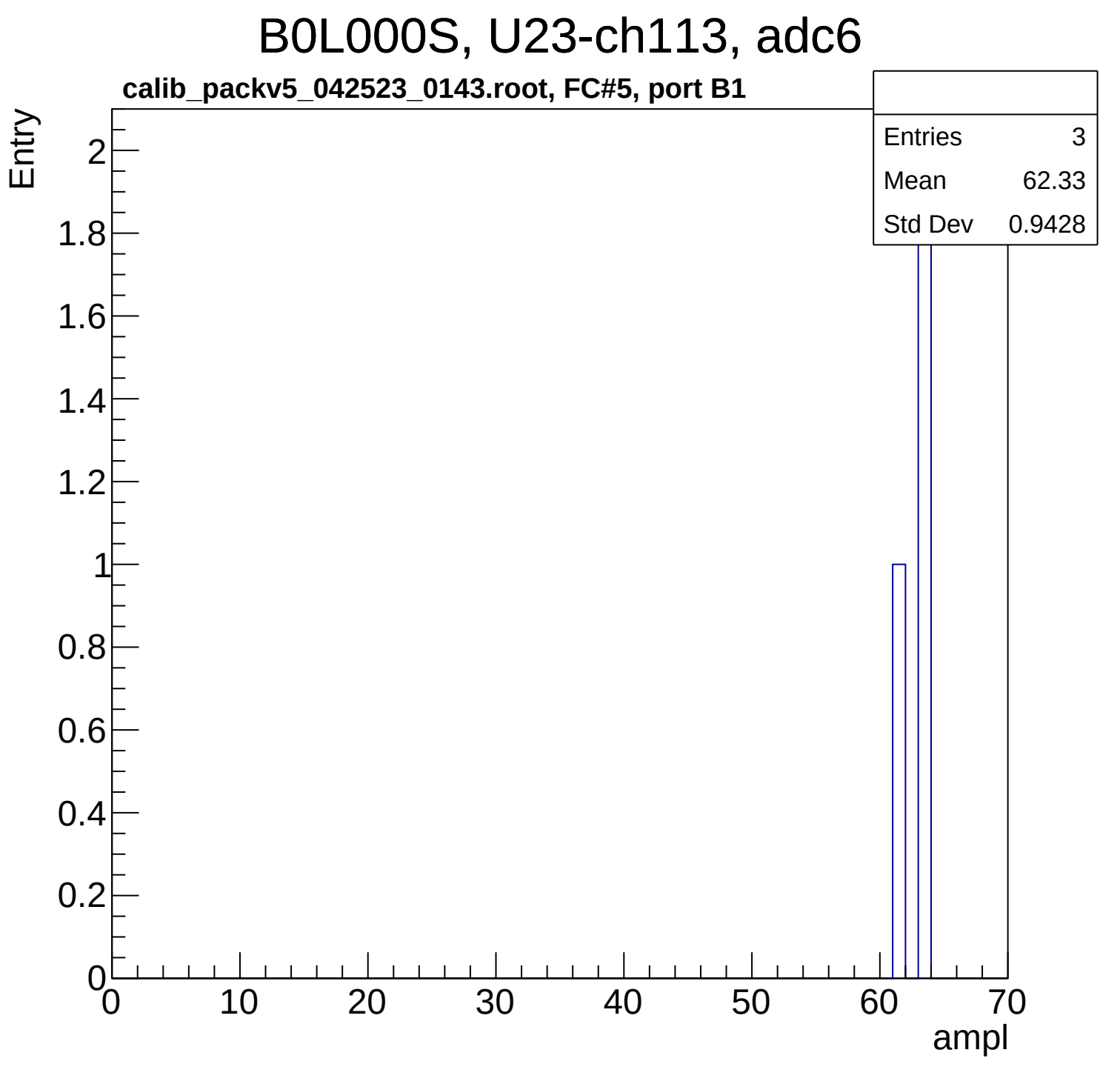

B0L000S, U23-ch113, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 36.89
Std Dev 3.564
Gaus mean : 37.2331
Gaus Width: 3.9809
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

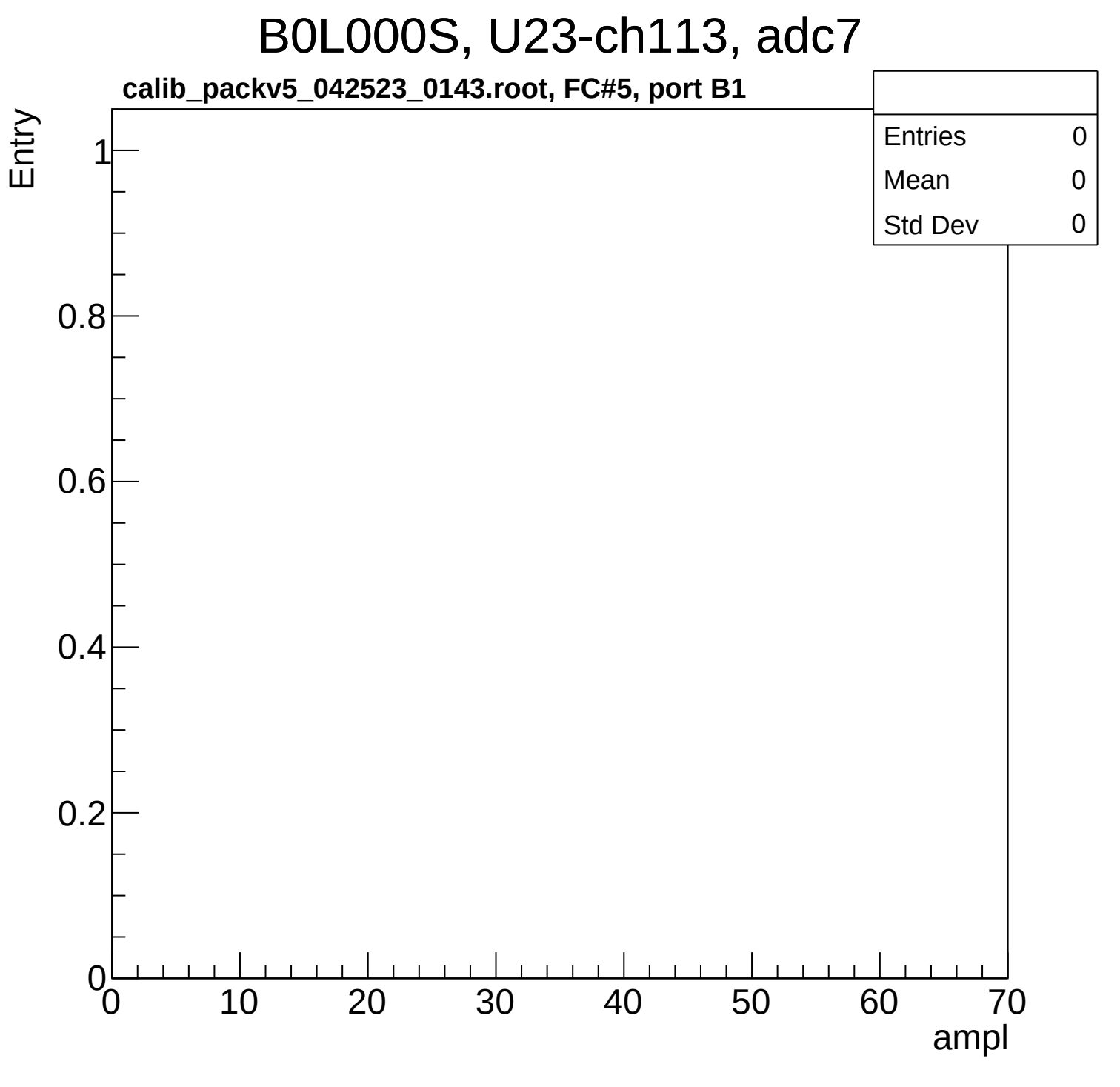

B0L000S, U23-ch113, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 42.62
Std Dev 3.246
Gaus mean : 44.1191
Gaus Width: 4.3610
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

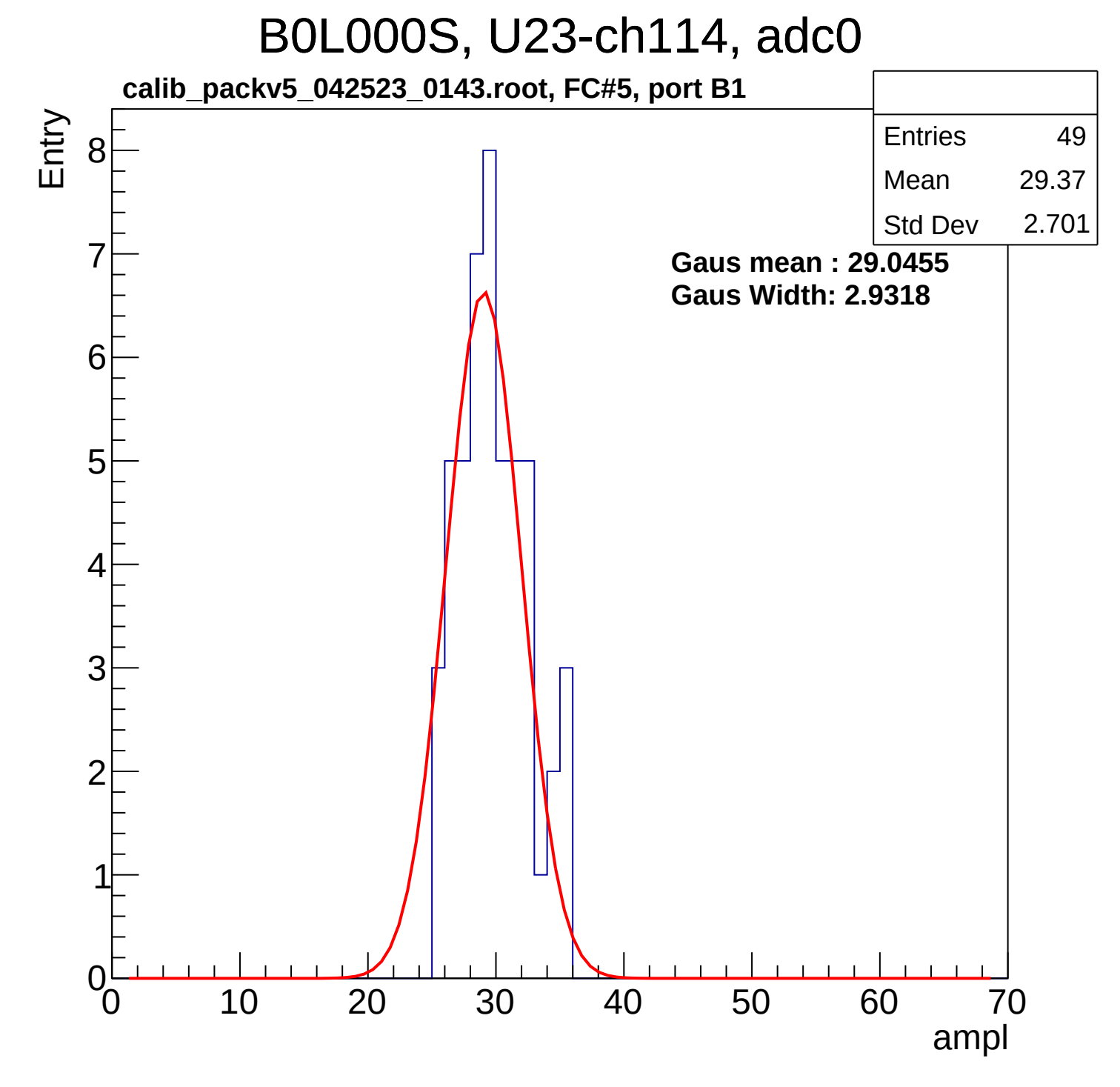

B0L000S, U23-ch113, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.76
Std Dev 3.71
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

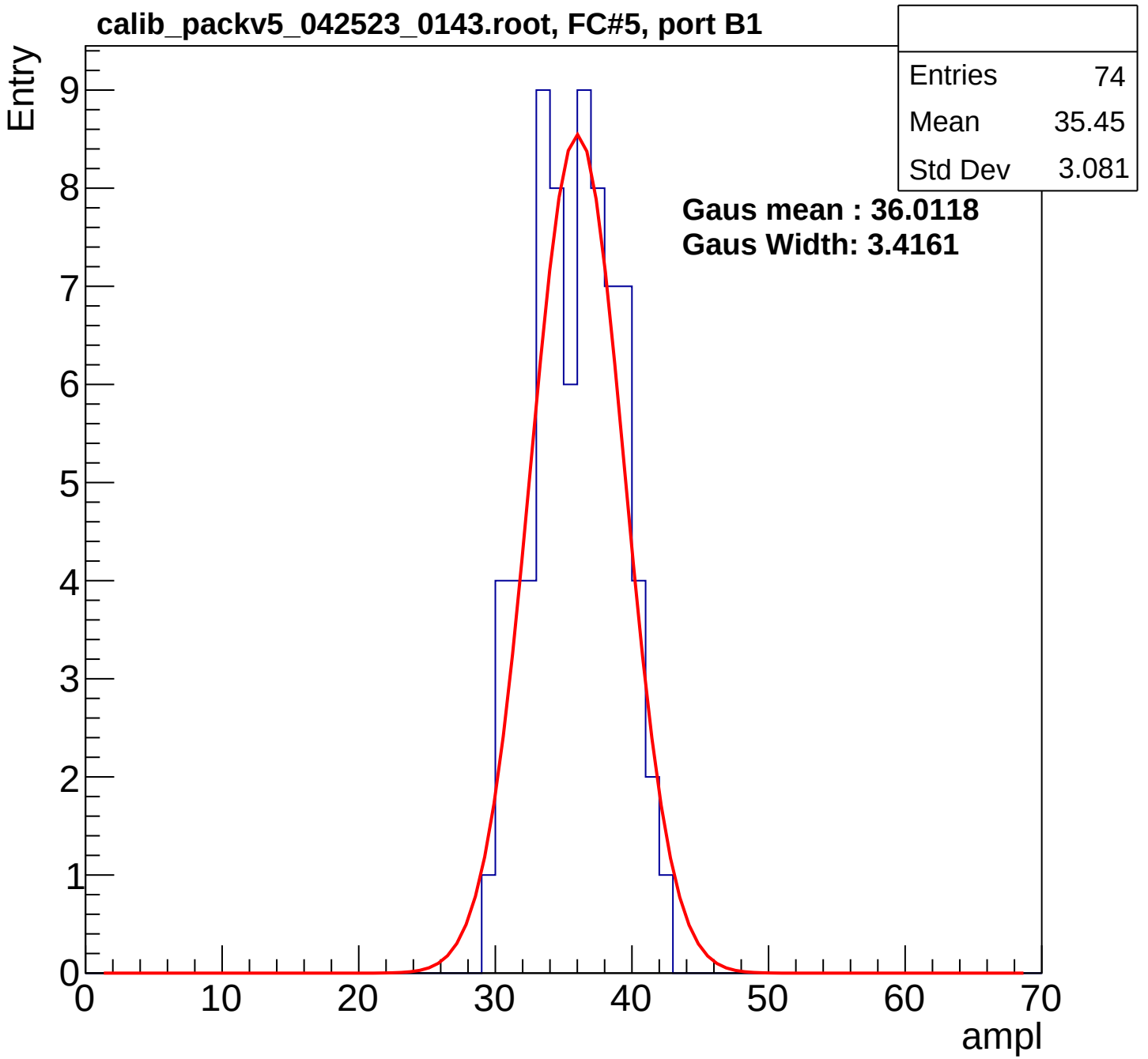

B0L000S, U23-ch113, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.07
Std Dev 3.247
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

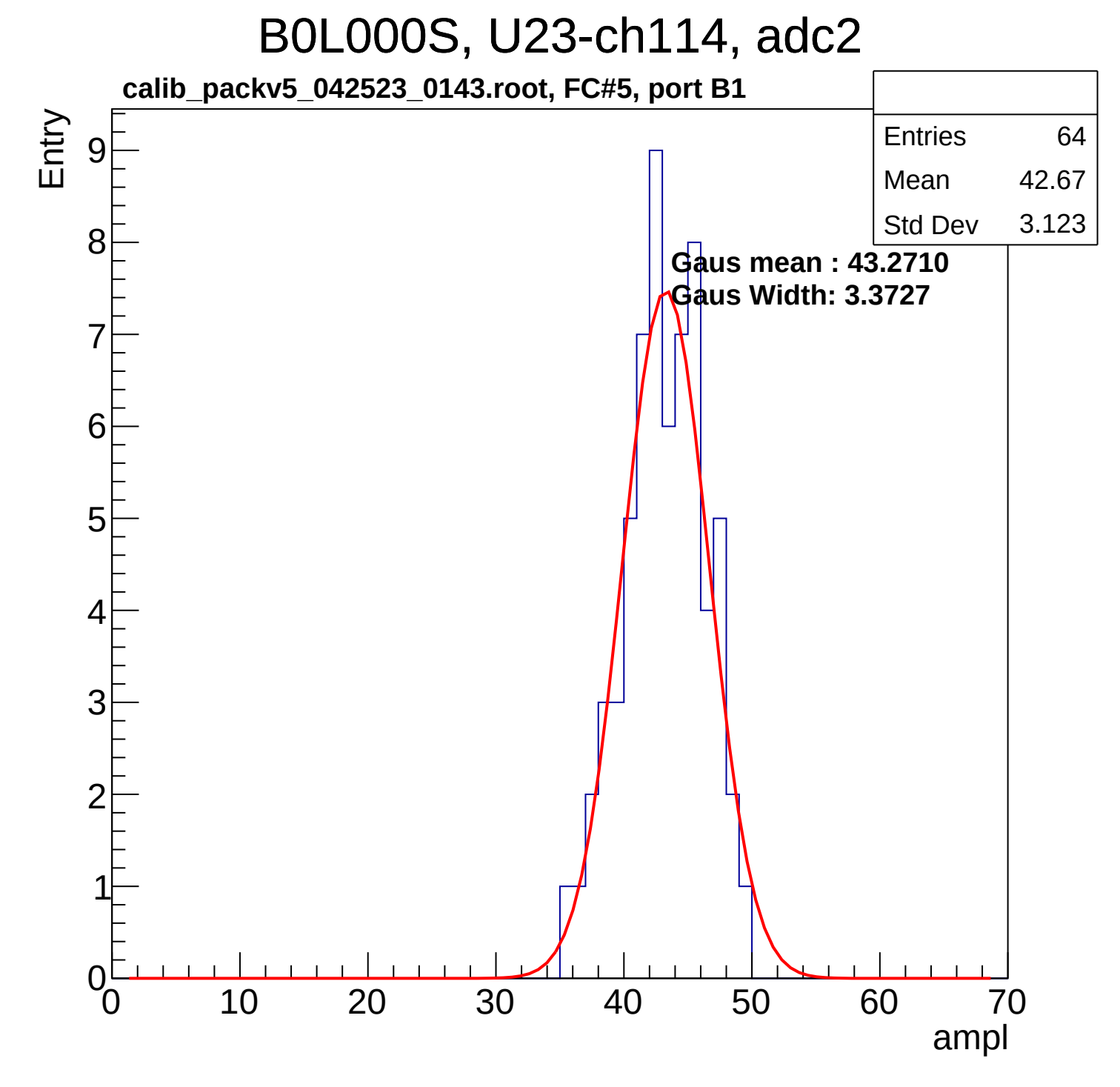

B0L000S, U23-ch113, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.74
Std Dev 8.435
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

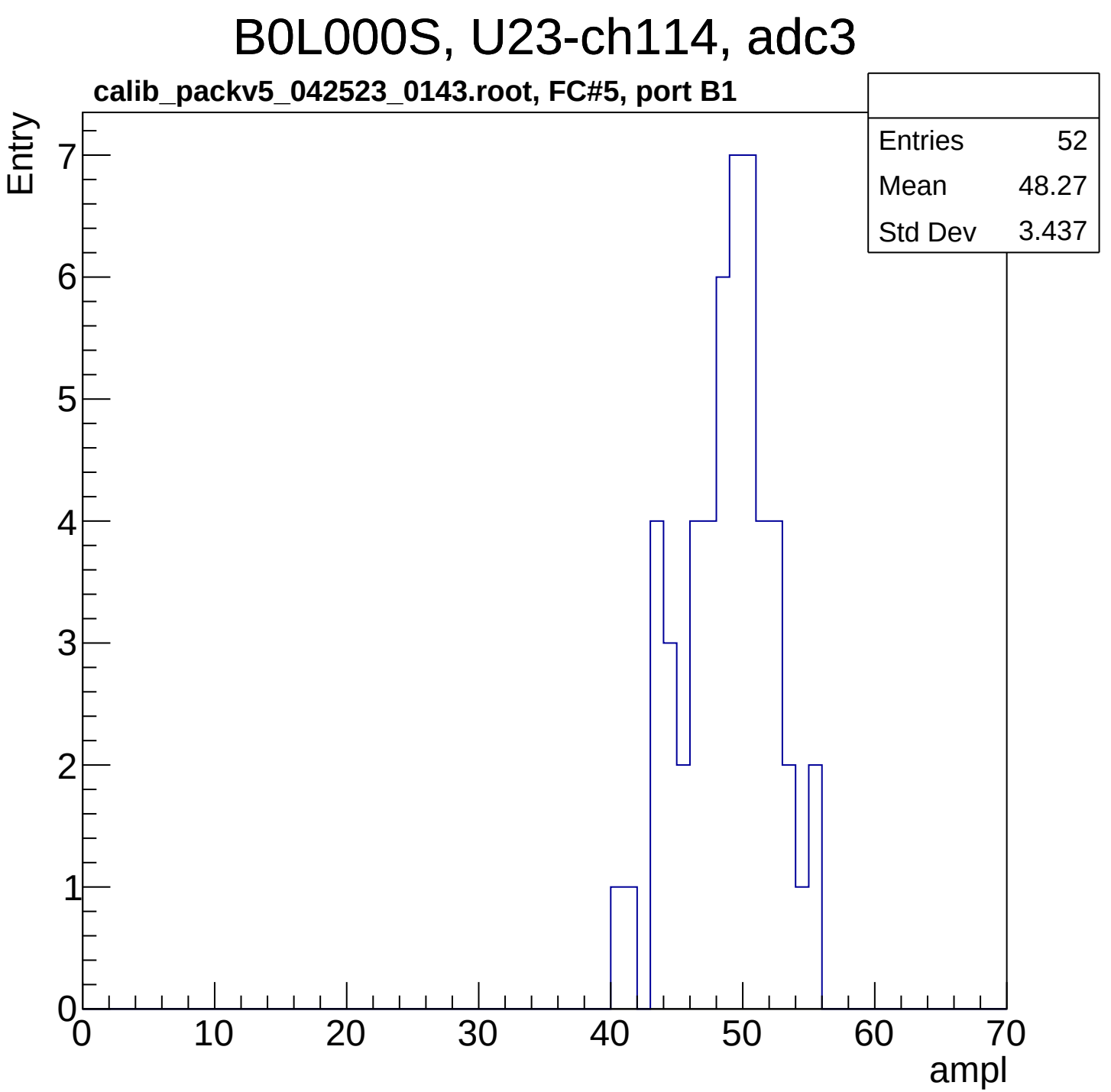

B0L000S, U23-ch113, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

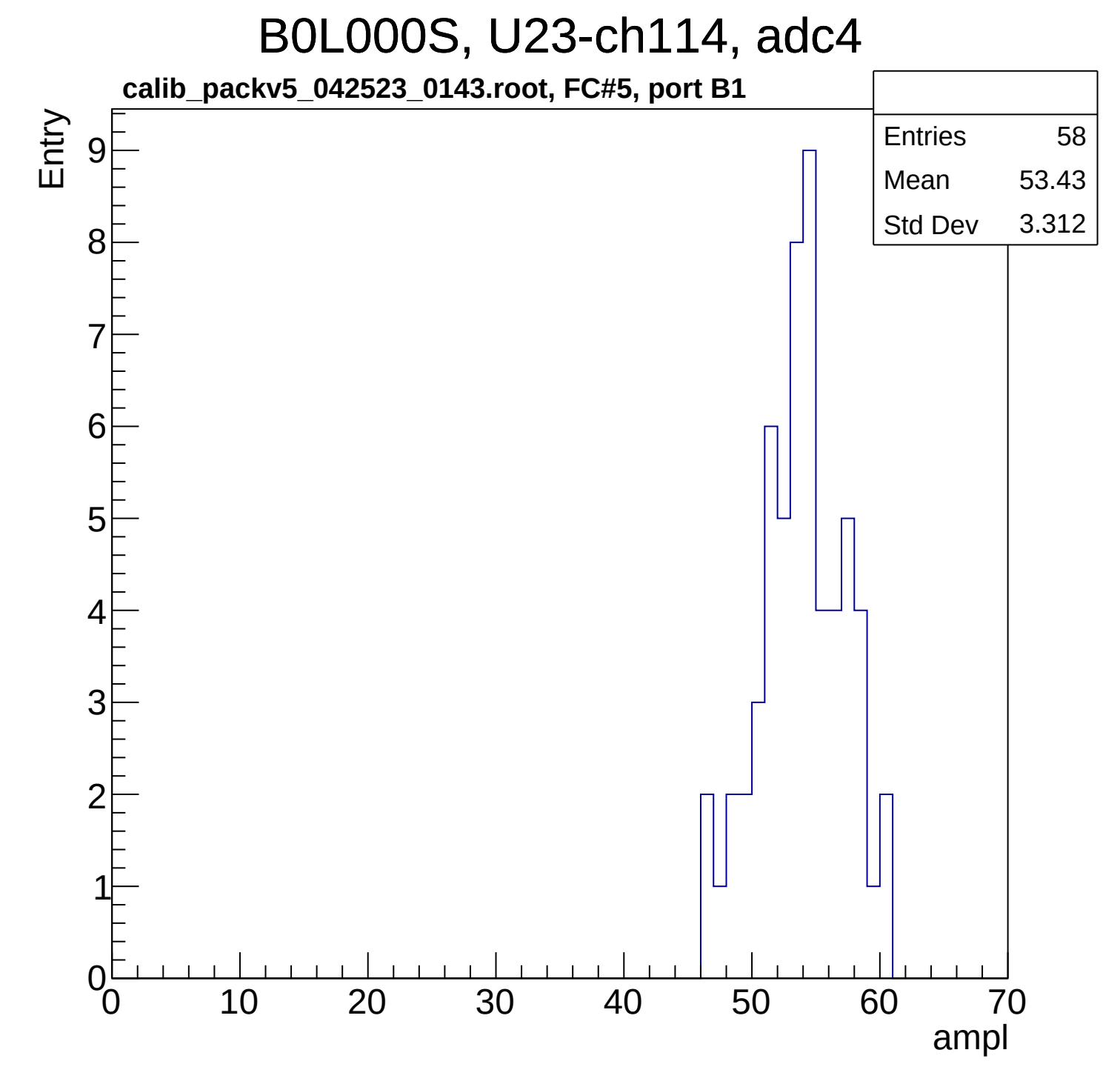

B0L000S, U23-ch113, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

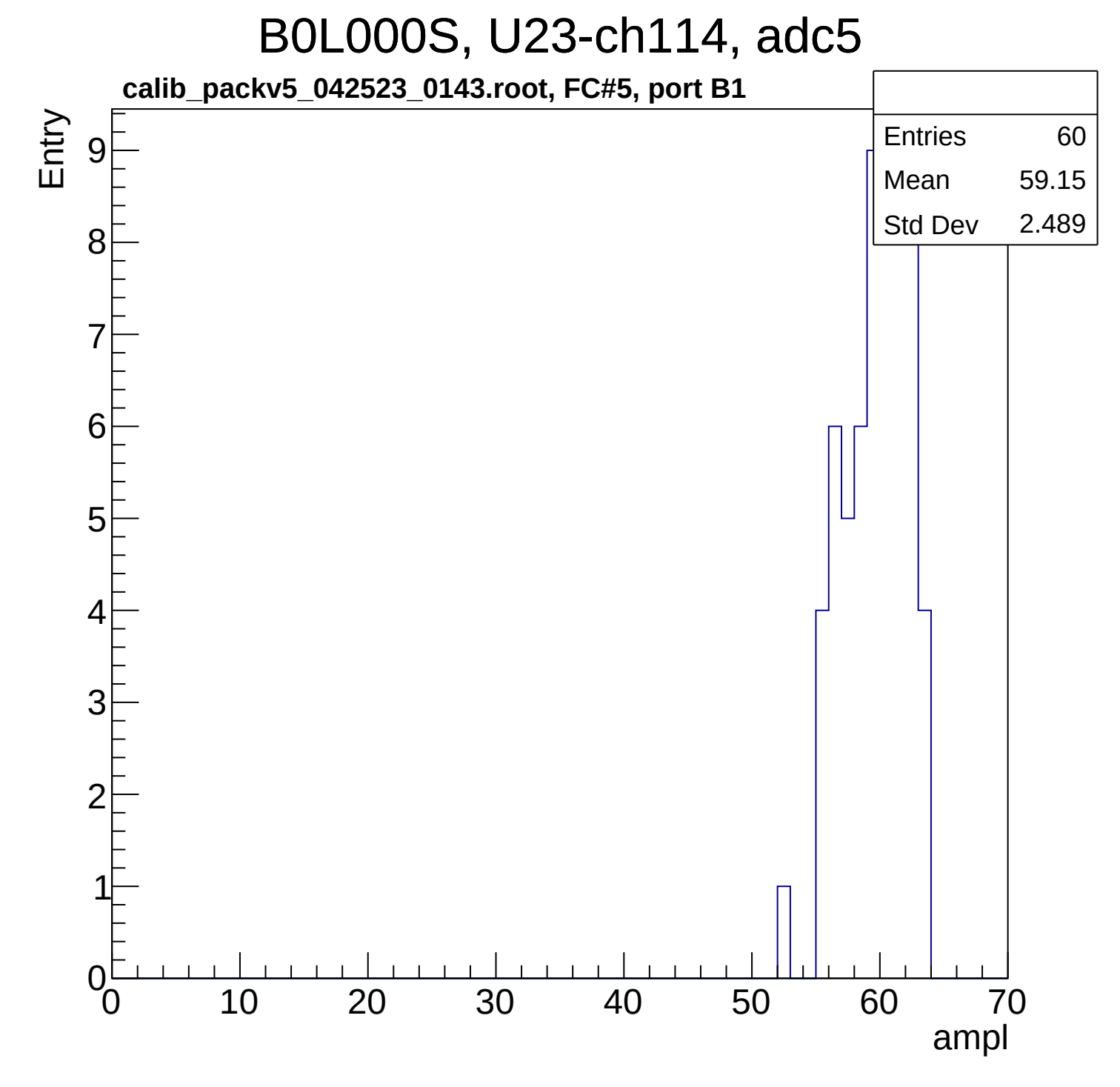

B0L000S, U23-ch114, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 29.37
Std Dev 2.701
Gaus mean : 29.0455
Gaus Width: 2.9318
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

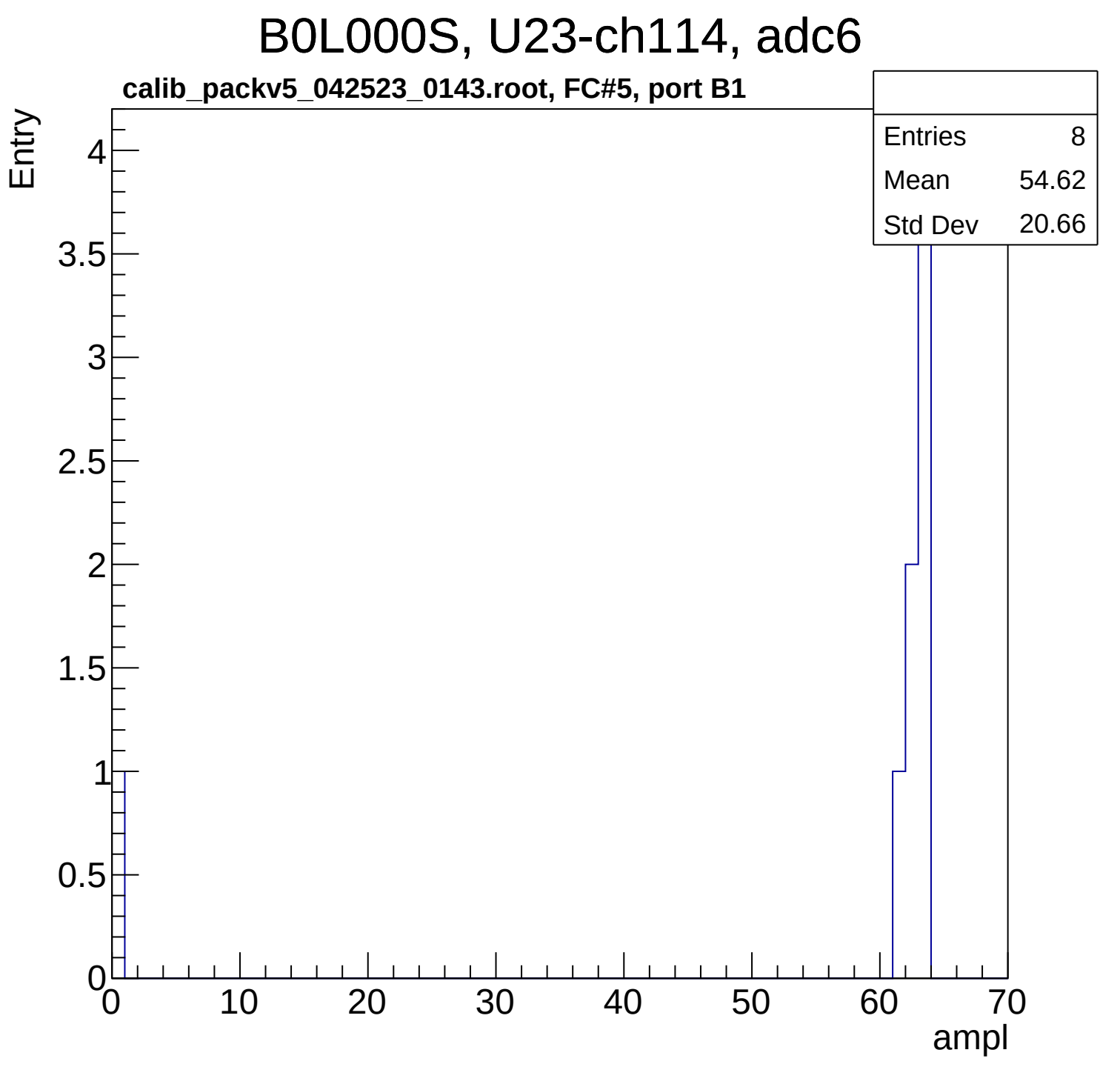

B0L000S, U23-ch114, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 35.45
Std Dev 3.081
Gaus mean : 36.0118
Gaus Width: 3.4161
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

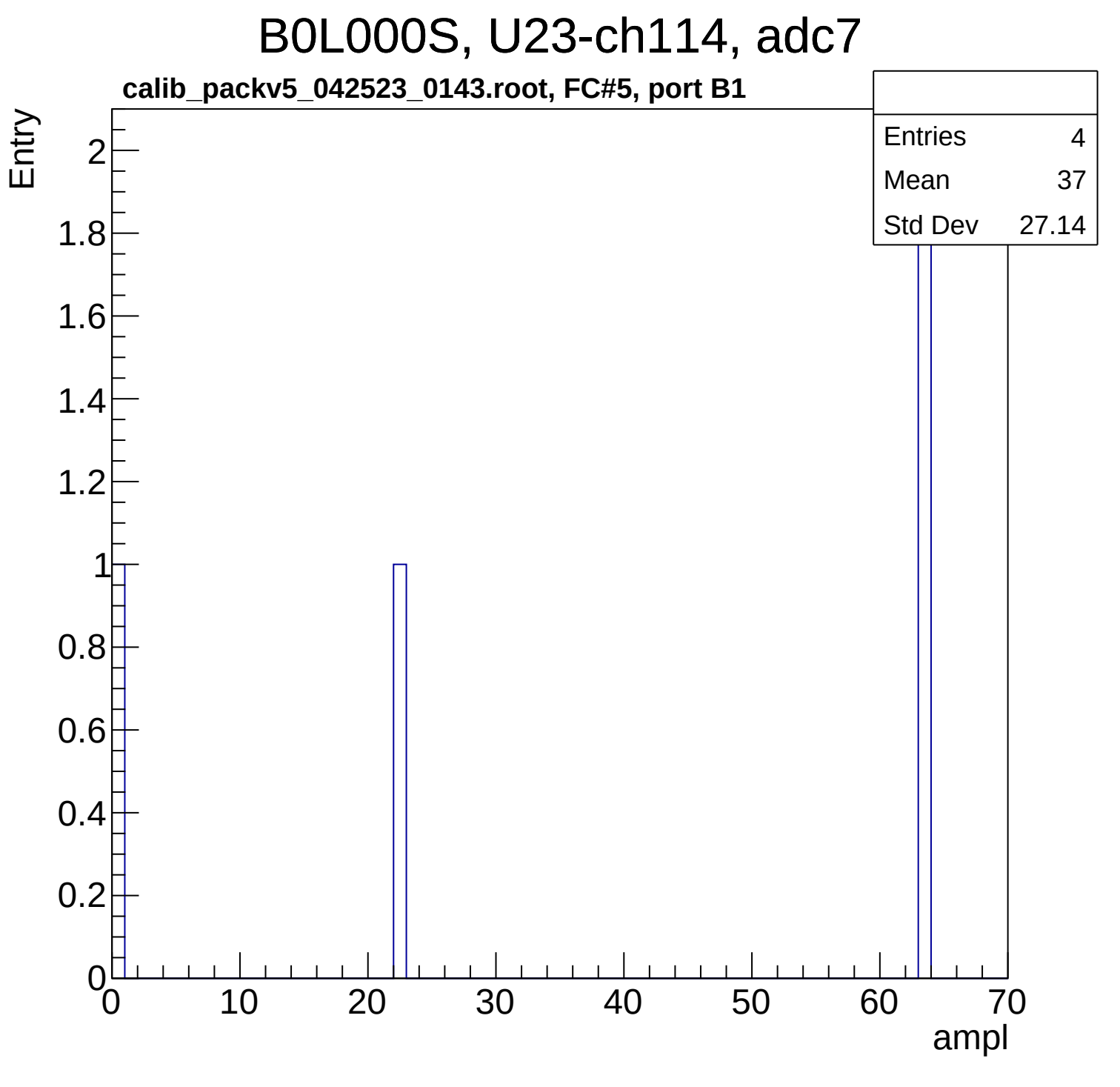

B0L000S, U23-ch114, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.67
Std Dev 3.123
Gaus mean : 43.2710
Gaus Width: 3.3727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

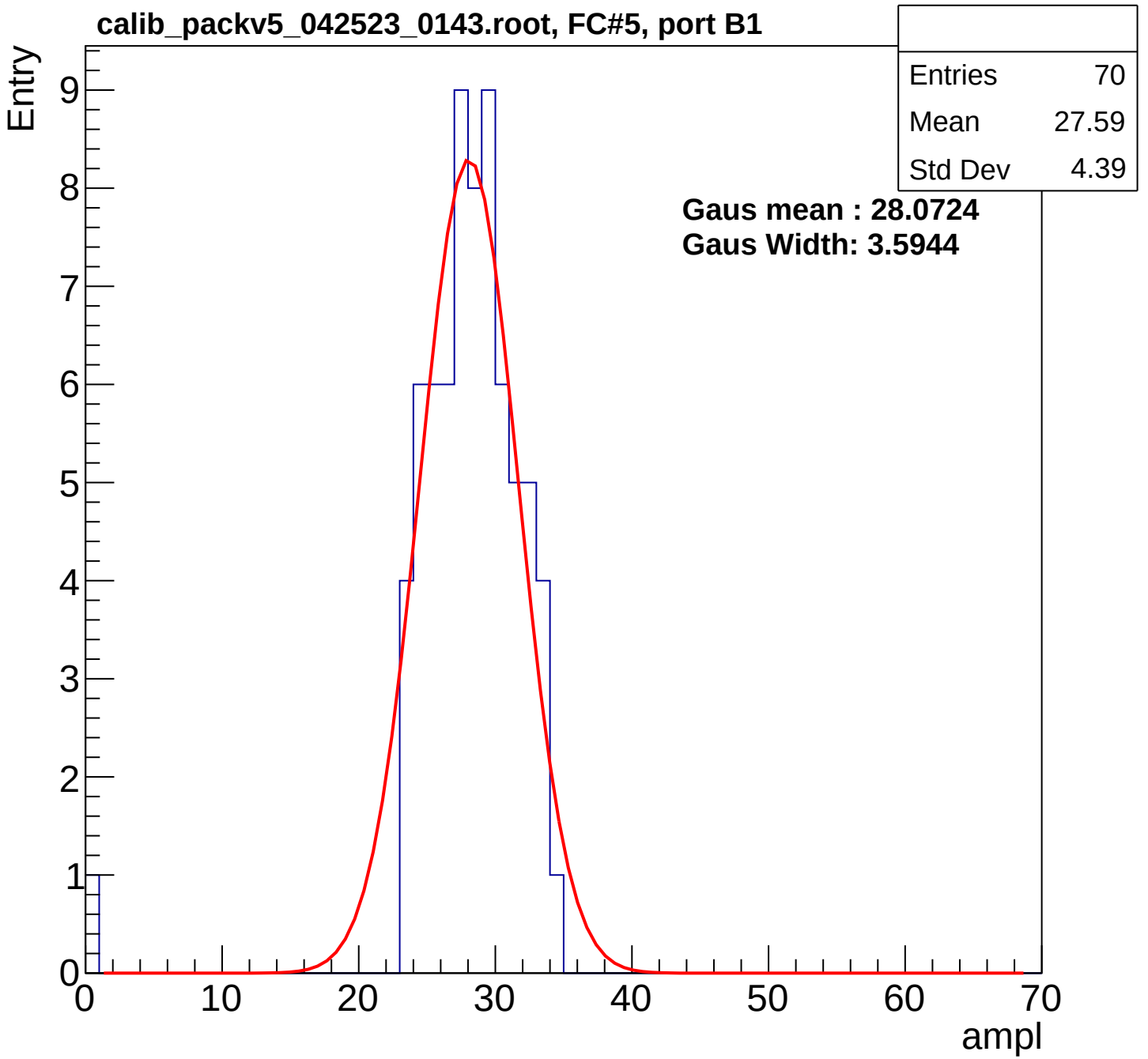

B0L000S, U23-ch114, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 48.27
Std Dev 3.437
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

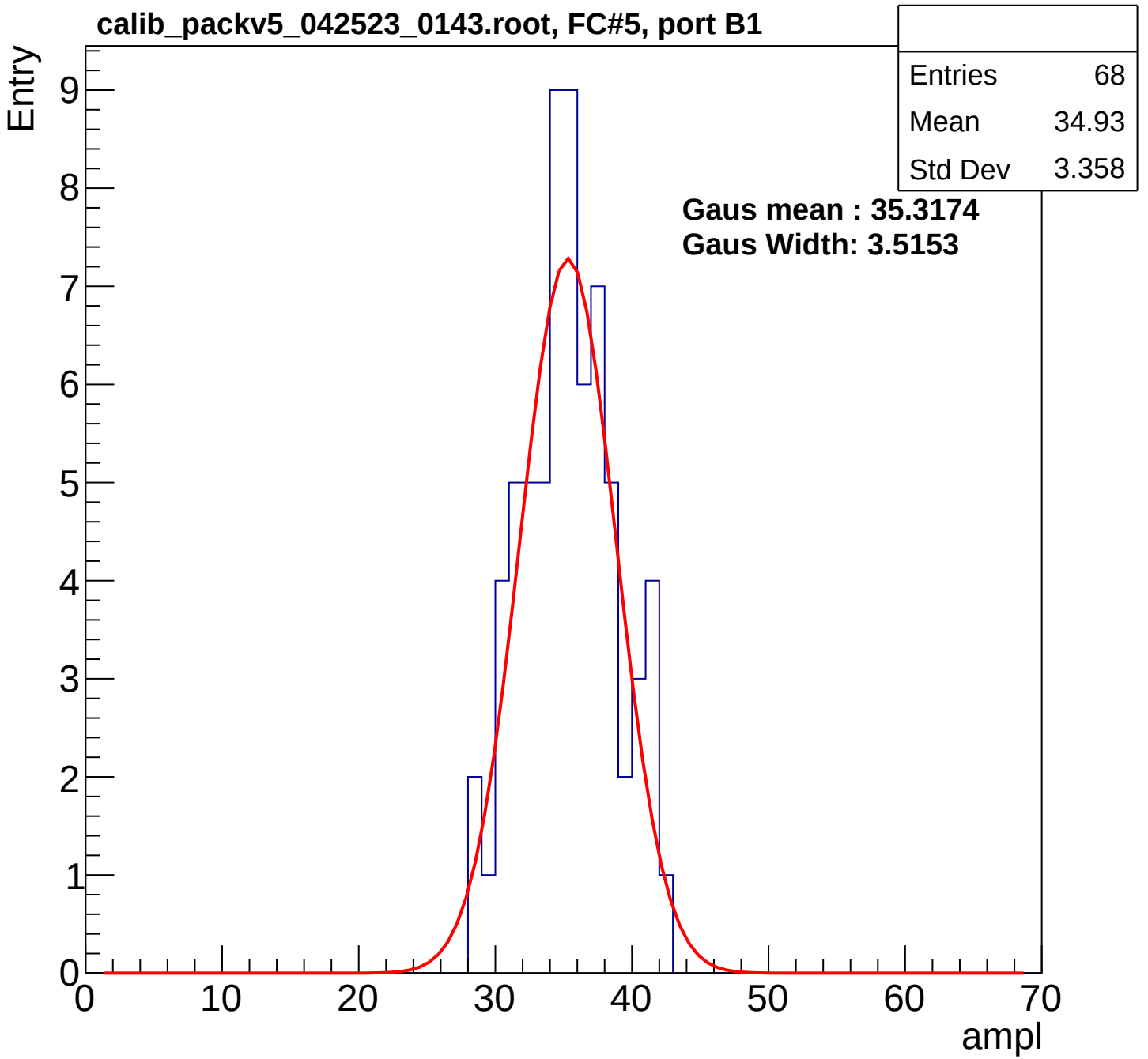

B0L000S, U23-ch114, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.43
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

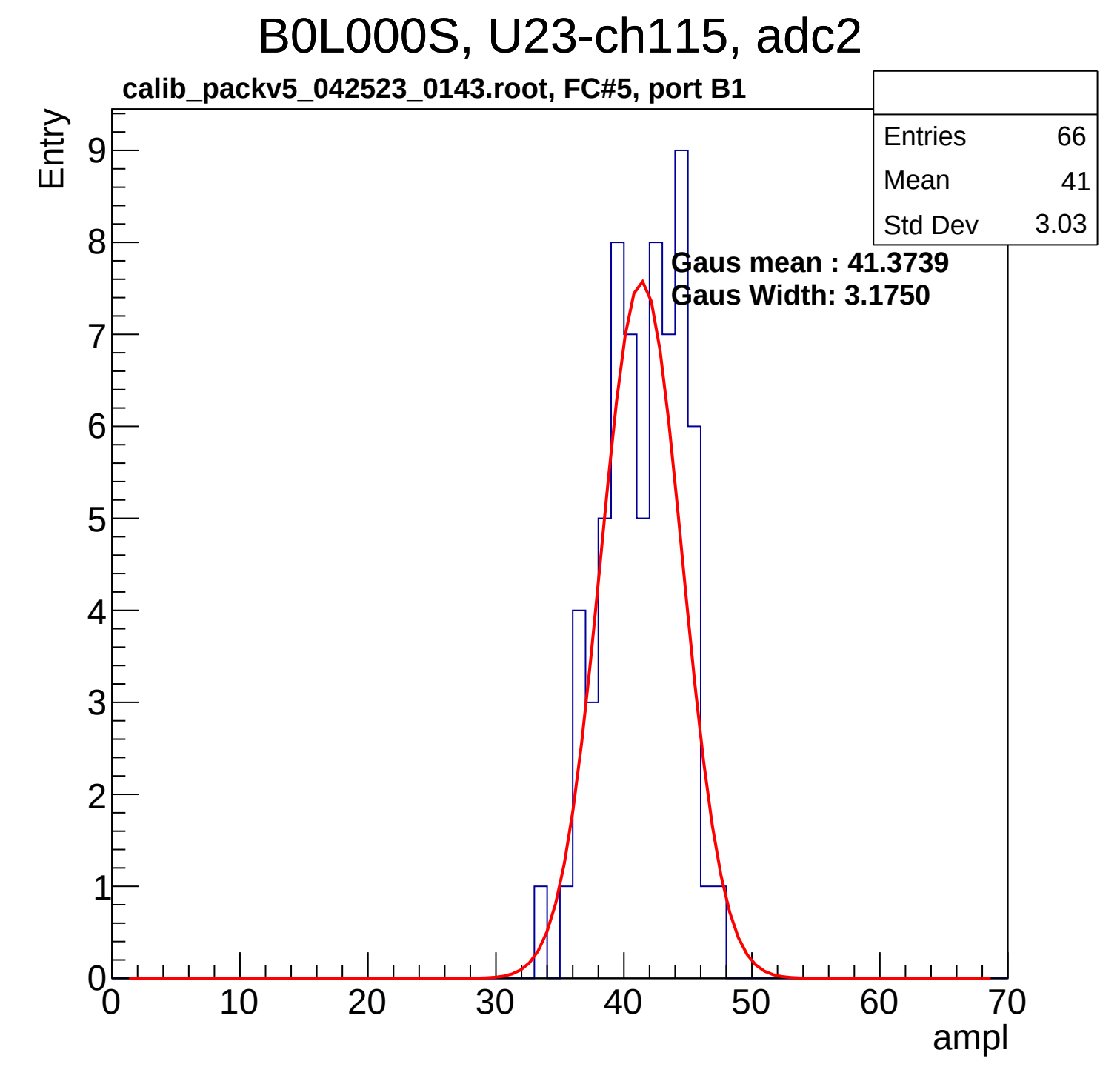

B0L000S, U23-ch114, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 59.15
Std Dev 2.489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

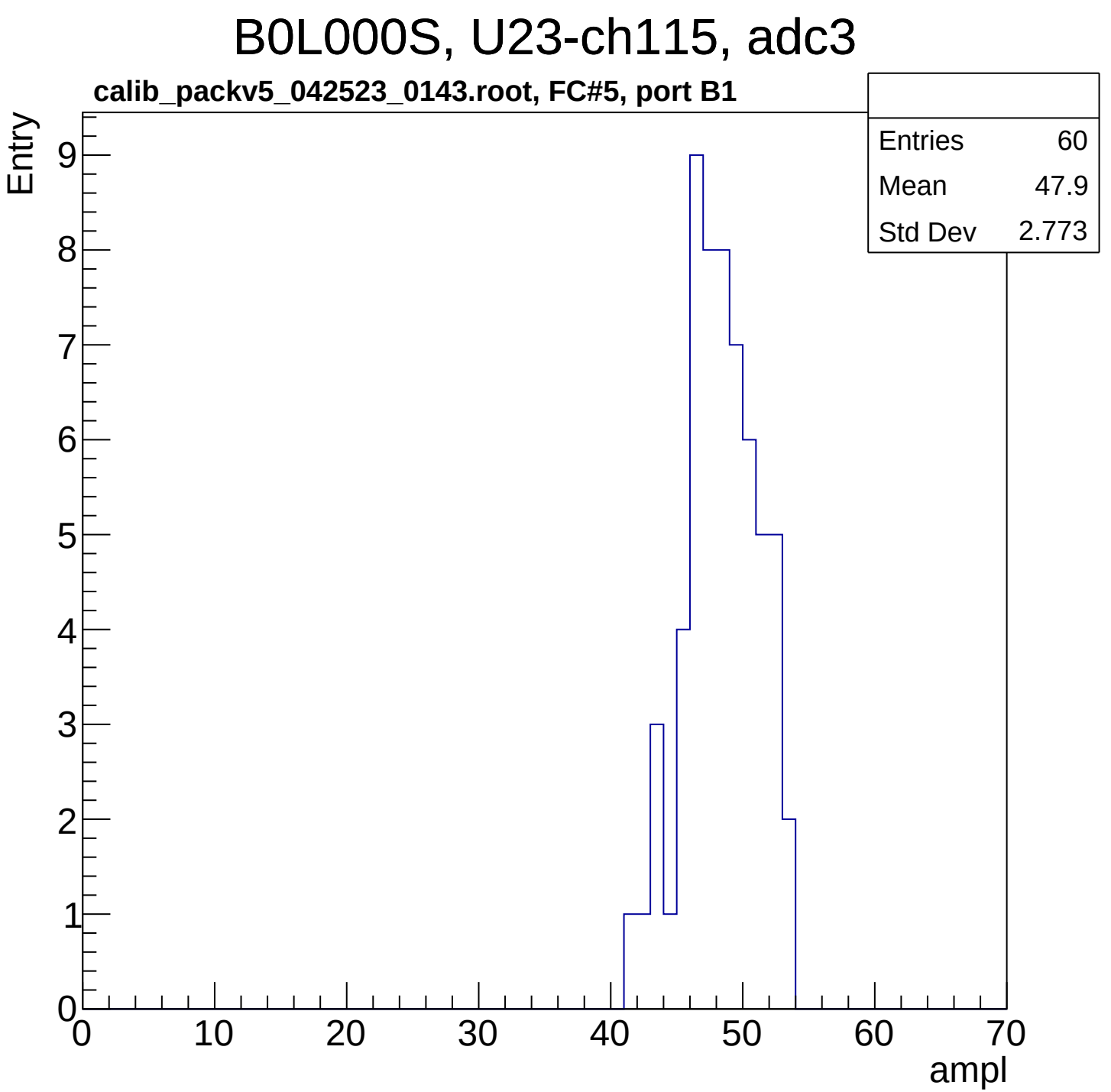

B0L000S, U23-ch114, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.62
Std Dev 20.66
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

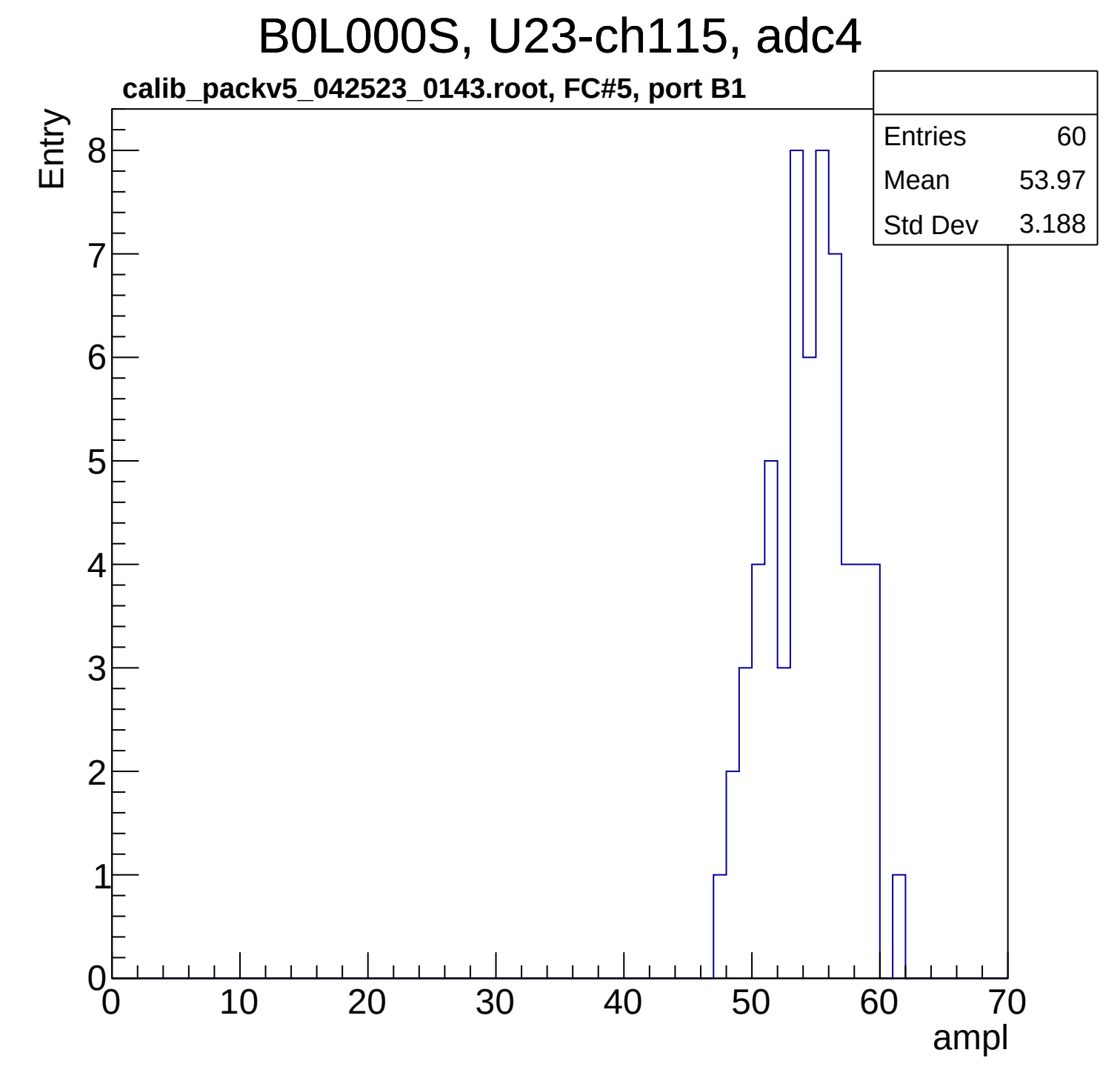

B0L000S, U23-ch114, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 37
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

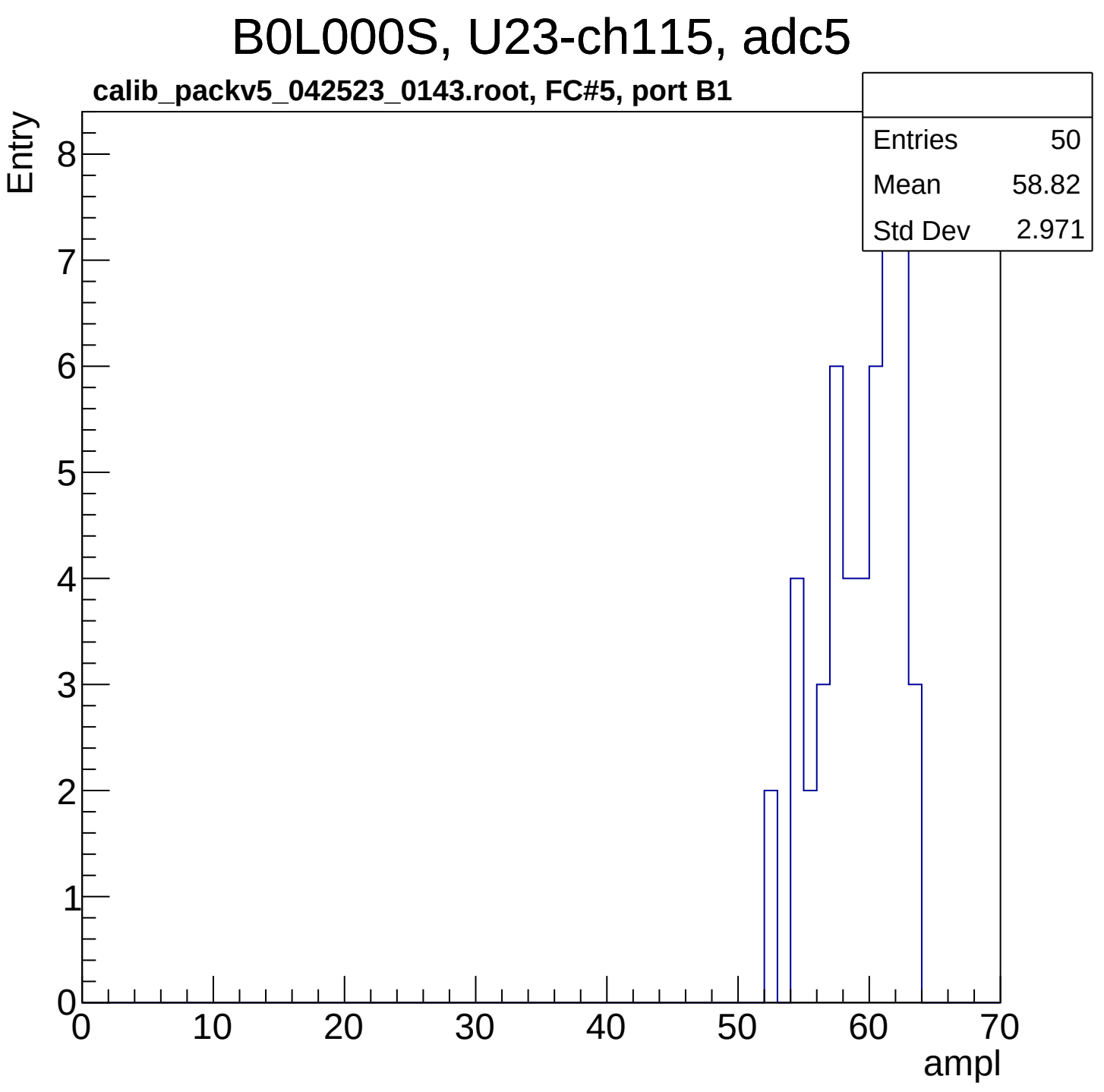

B0L000S, U23-ch115, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 27.59
Std Dev 4.39
Gaus mean : 28.0724
Gaus Width: 3.5944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

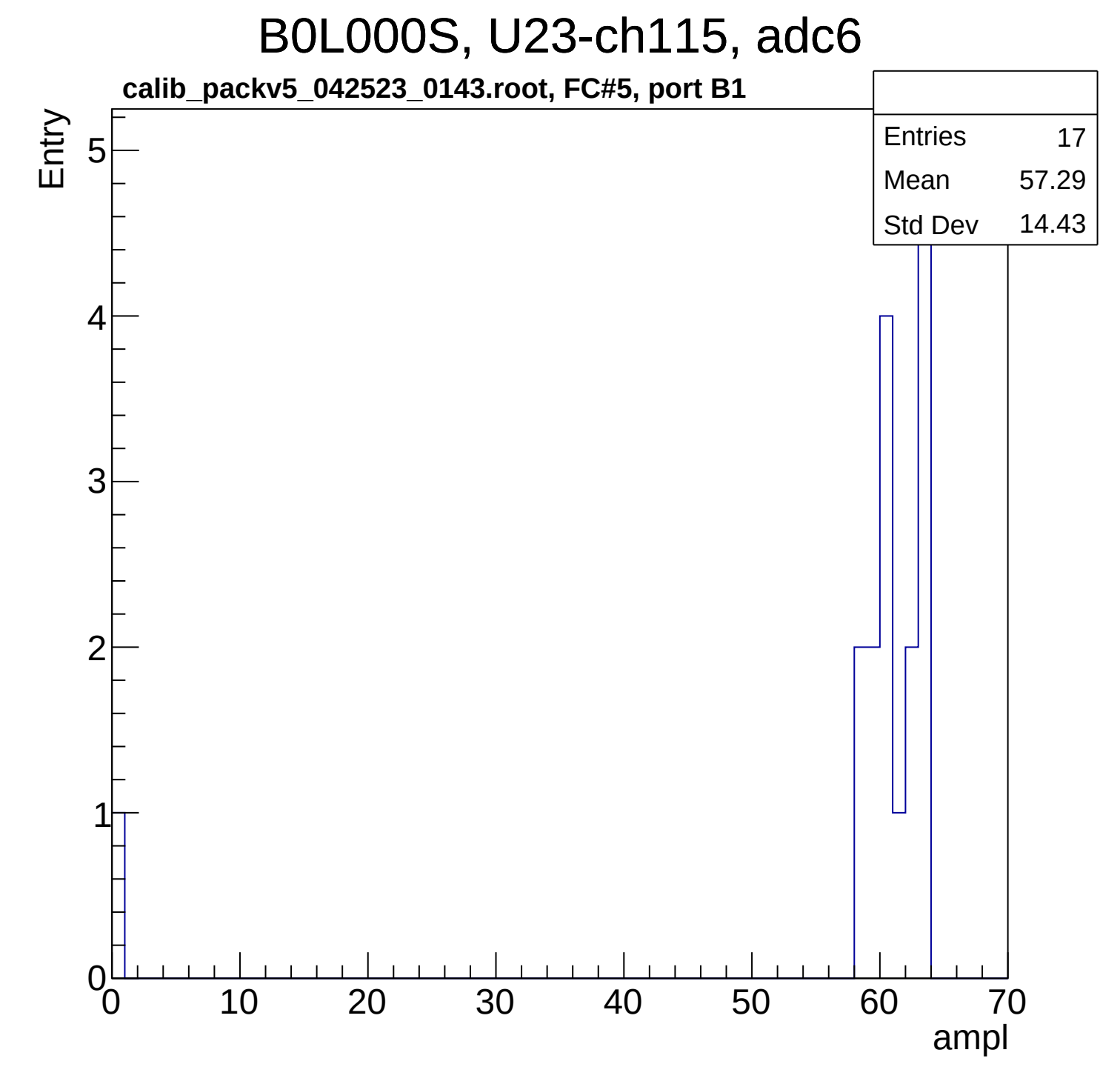

B0L000S, U23-ch115, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 34.93
Std Dev 3.358
Gaus mean : 35.3174
Gaus Width: 3.5153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

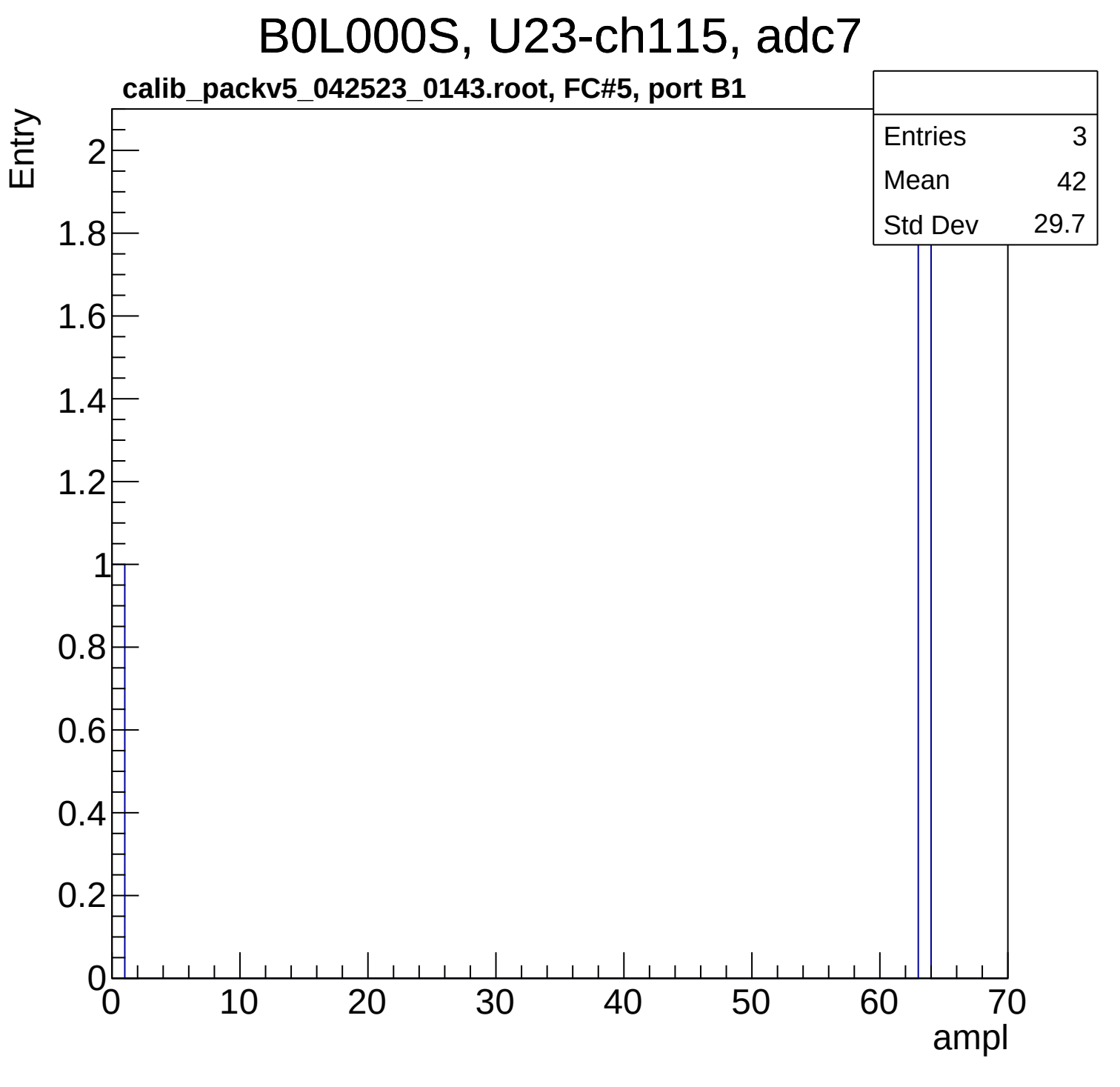

B0L000S, U23-ch115, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 41
Std Dev 3.03
Gaus mean : 41.3739
Gaus Width: 3.1750
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

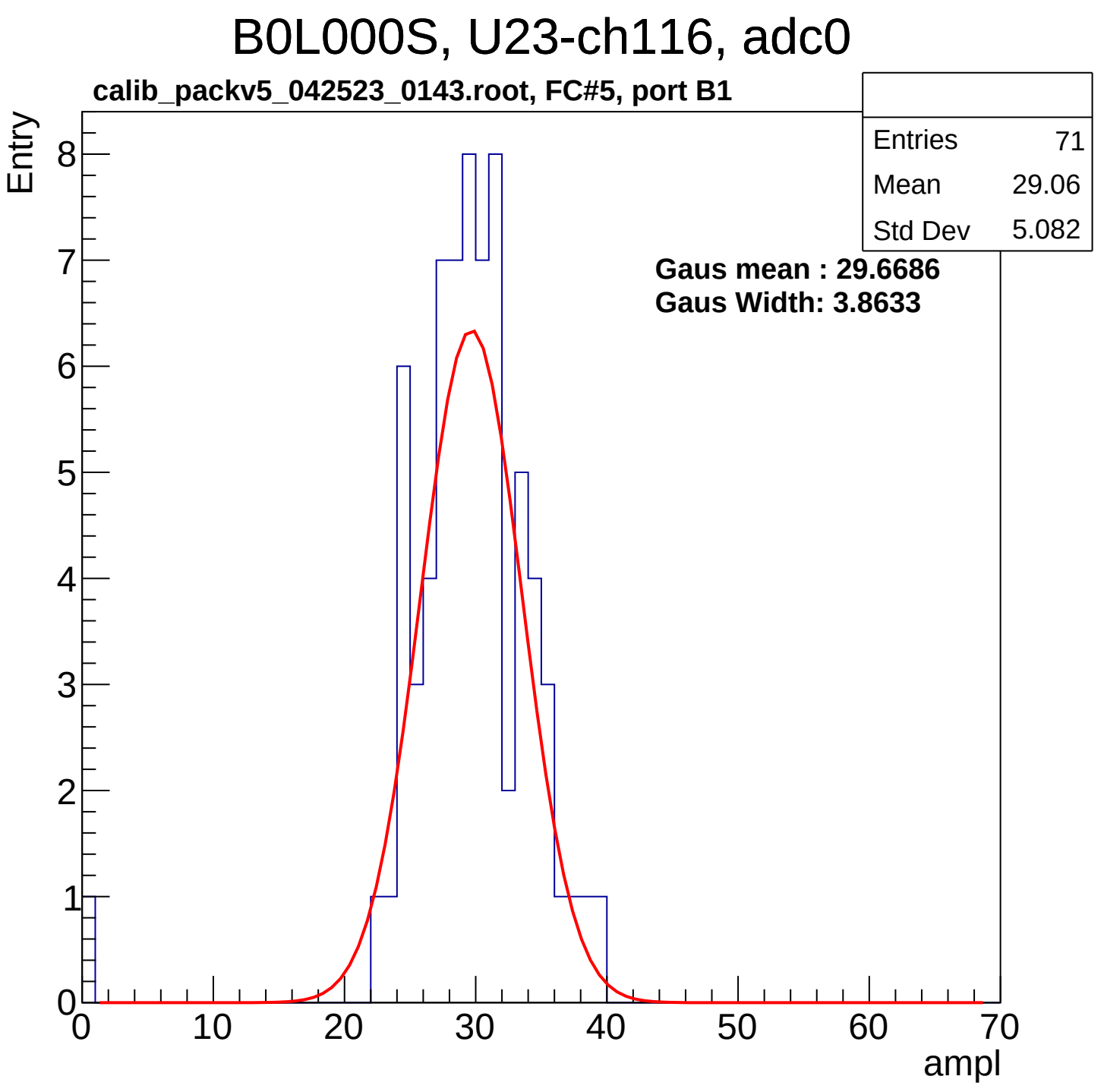

B0L000S, U23-ch115, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 47.9
Std Dev 2.773
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

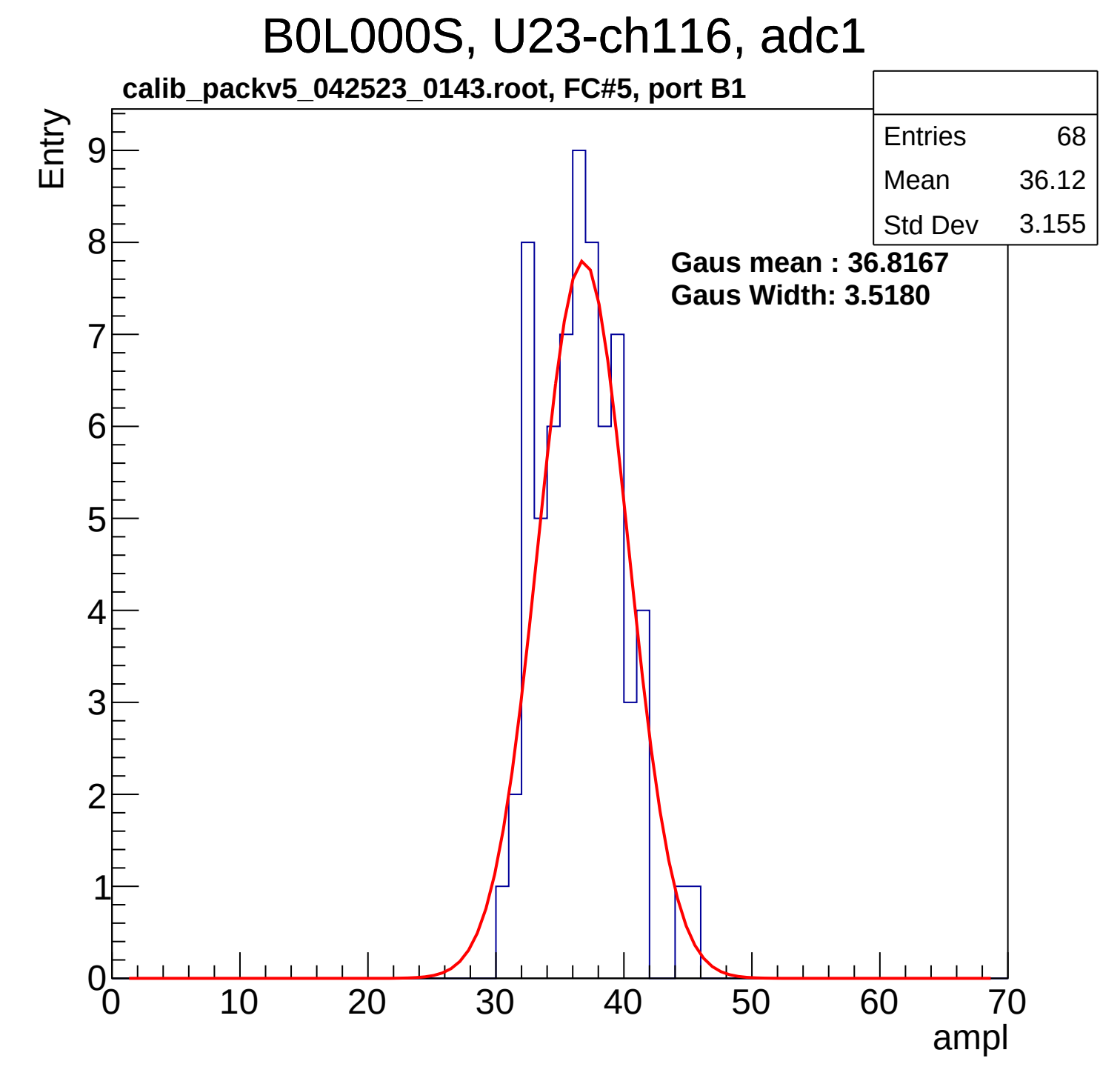

B0L000S, U23-ch115, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 53.97
Std Dev 3.188
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

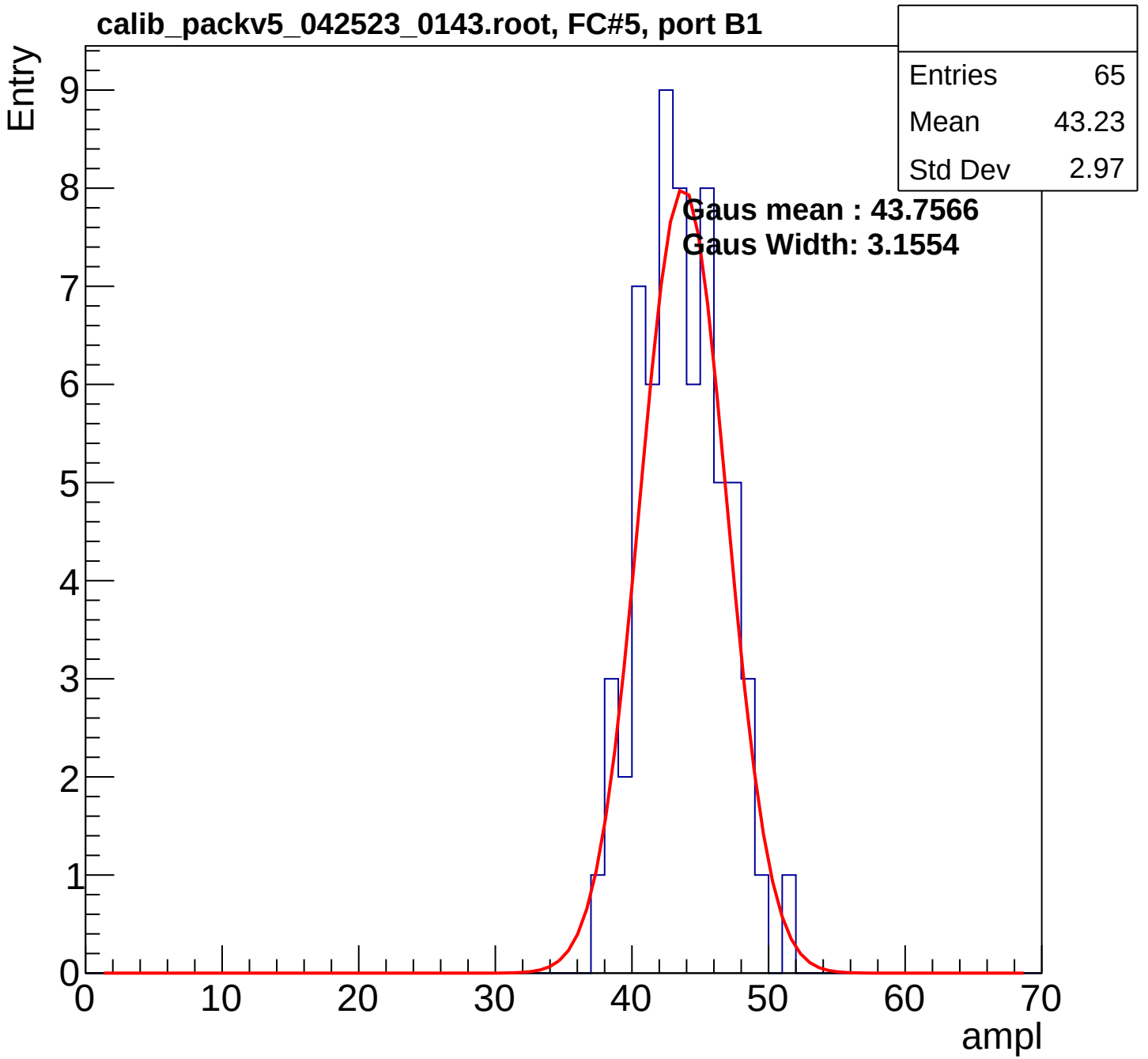

B0L000S, U23-ch115, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.82
Std Dev 2.971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

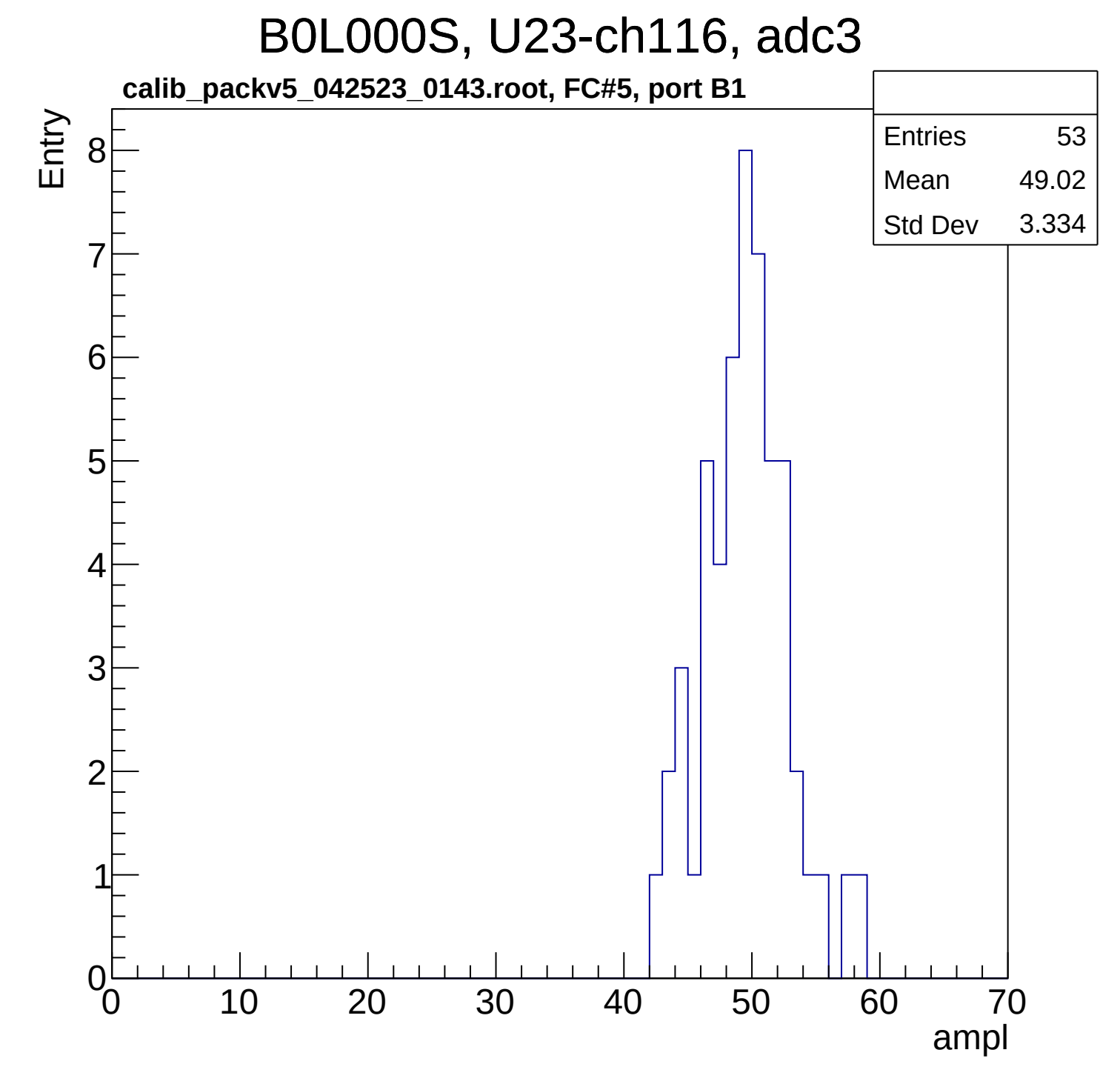

B0L000S, U23-ch115, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 57.29
Std Dev 14.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

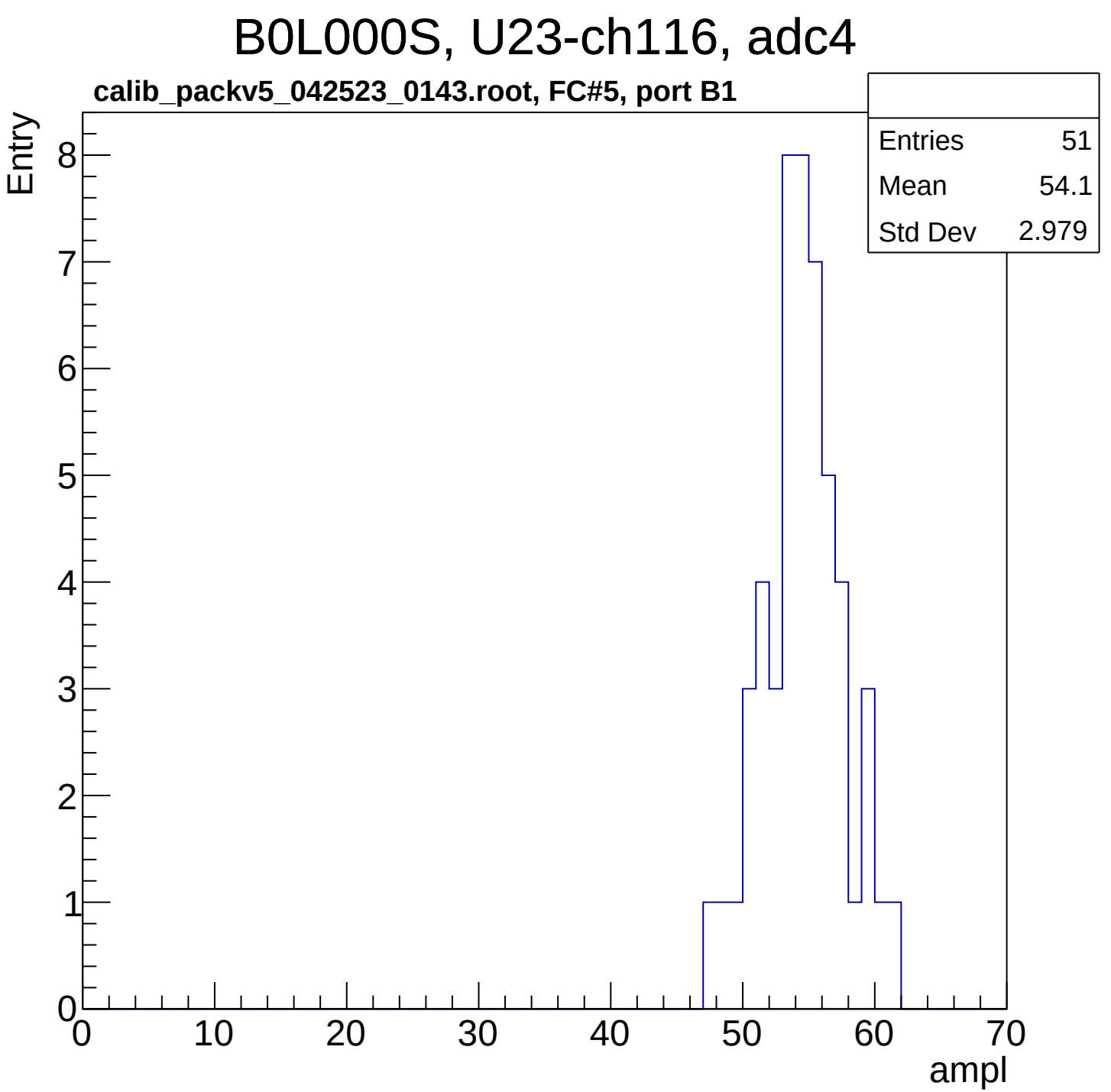

B0L000S, U23-ch115, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

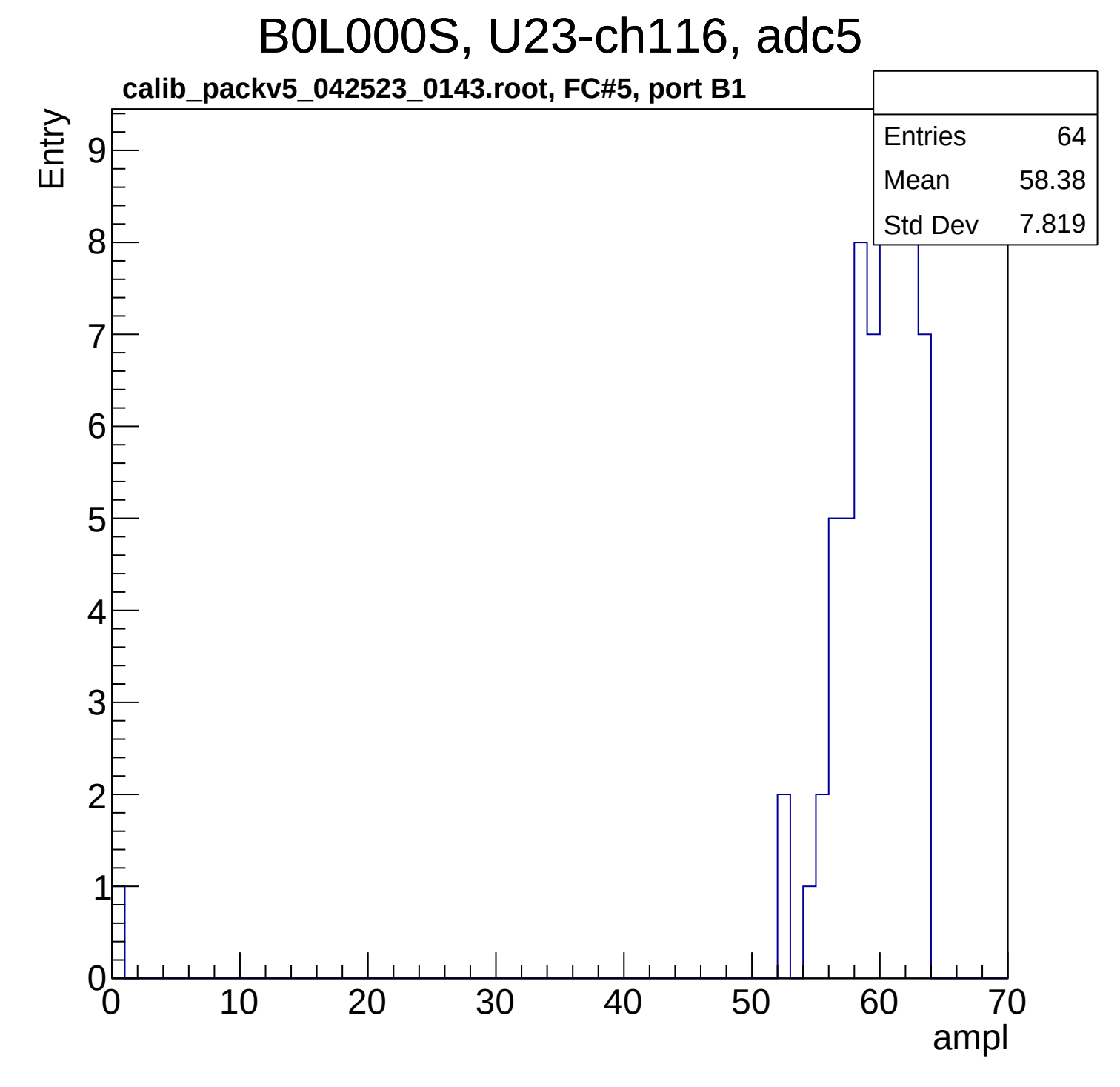

B0L000S, U23-ch116, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.06
Std Dev 5.082
Gaus mean : 29.6686
Gaus Width: 3.8633
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

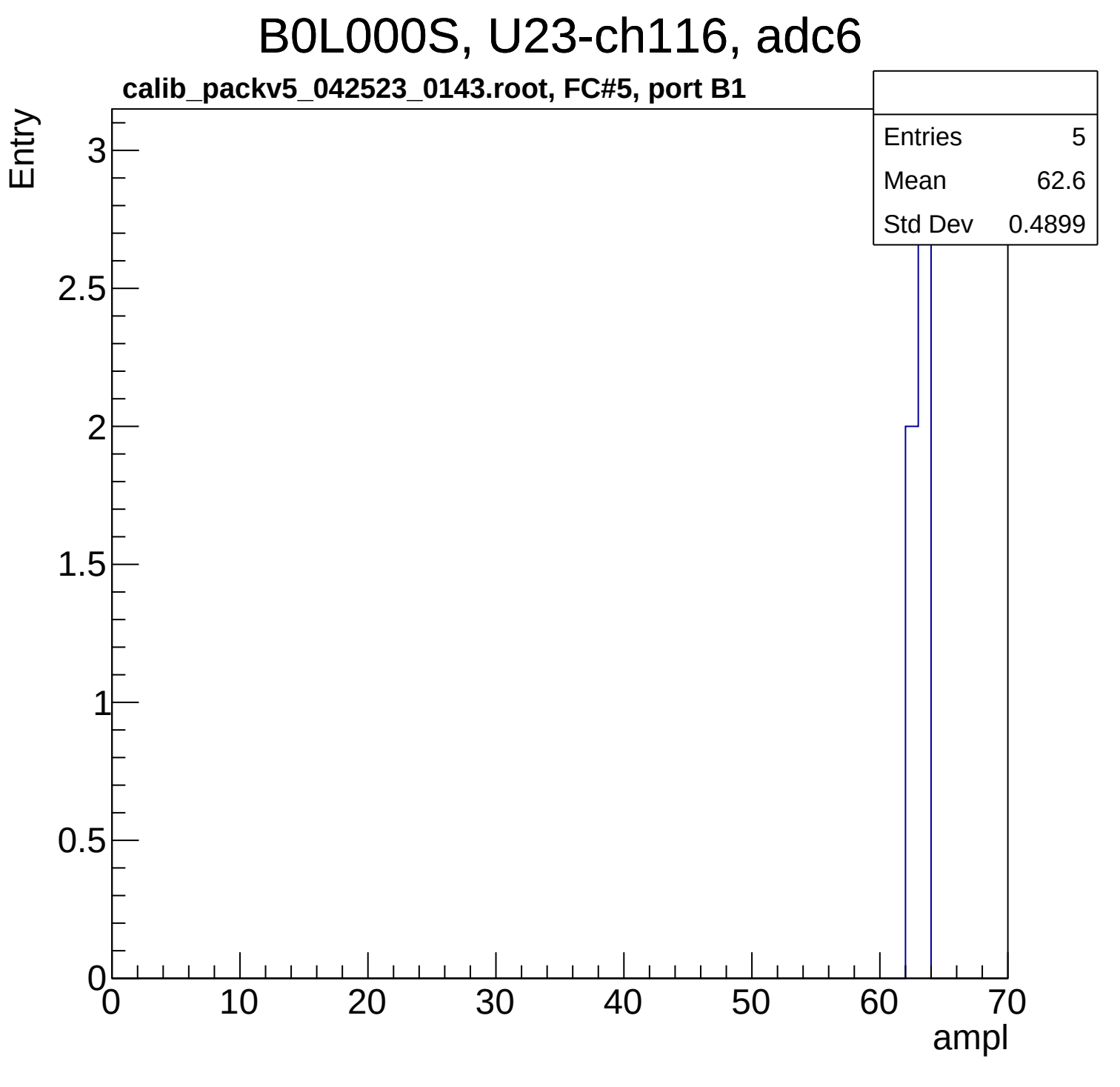

B0L000S, U23-ch116, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.12
Std Dev 3.155
Gaus mean : 36.8167
Gaus Width: 3.5180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

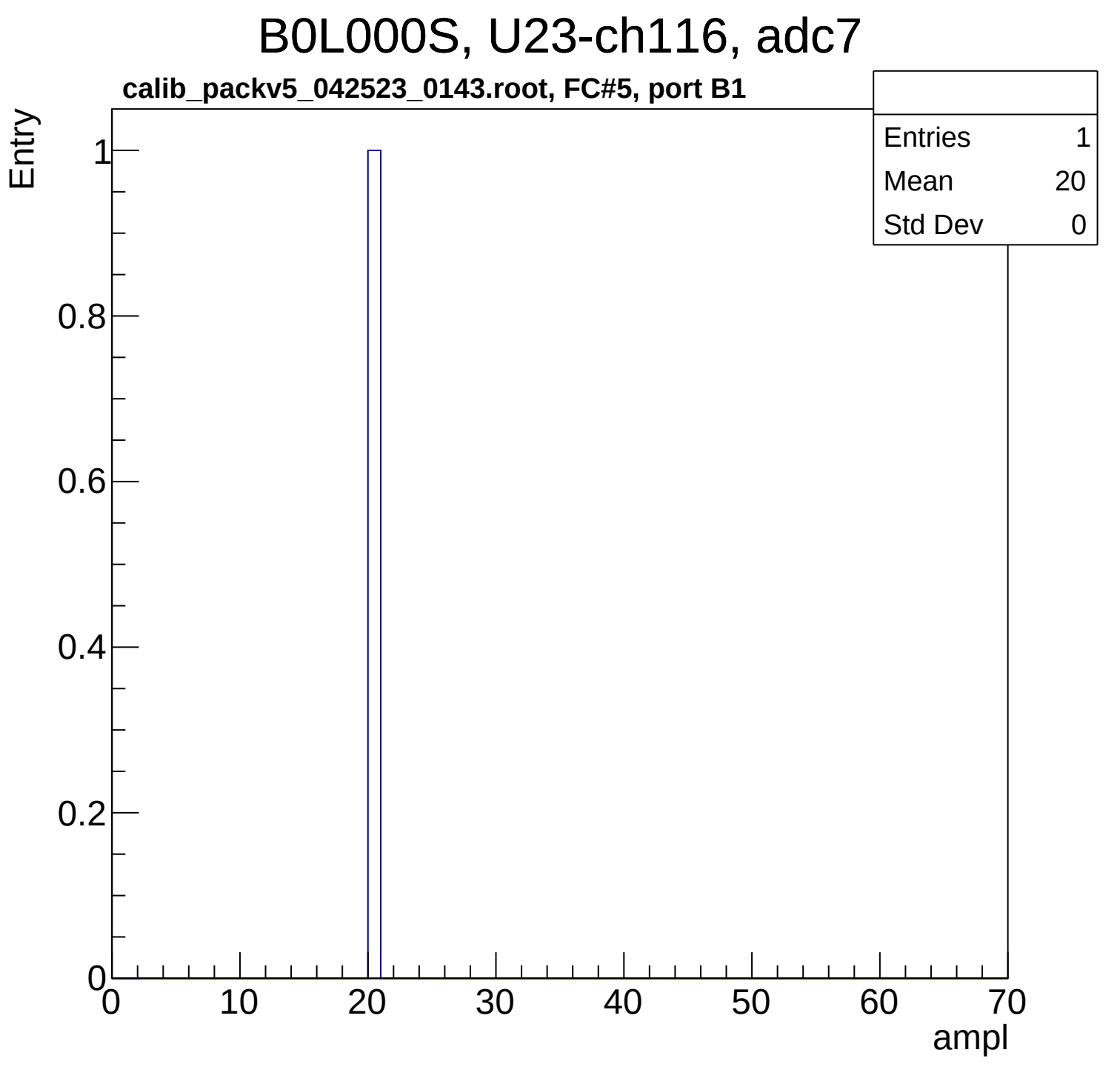

B0L000S, U23-ch116, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.23
Std Dev 2.97
Gaus mean : 43.7566
Gaus Width: 3.1554
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

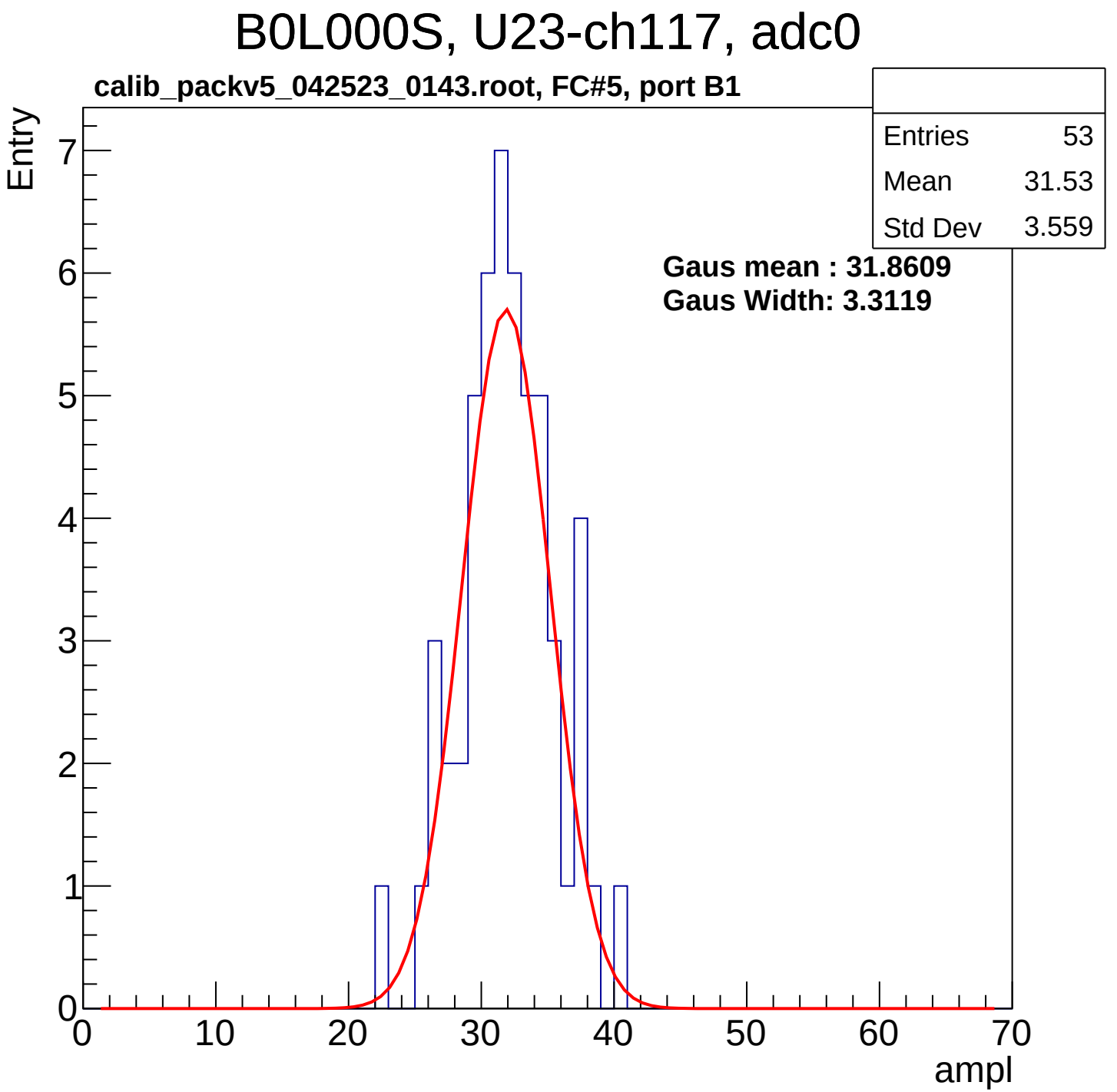

B0L000S, U23-ch116, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 49.02
Std Dev 3.334
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

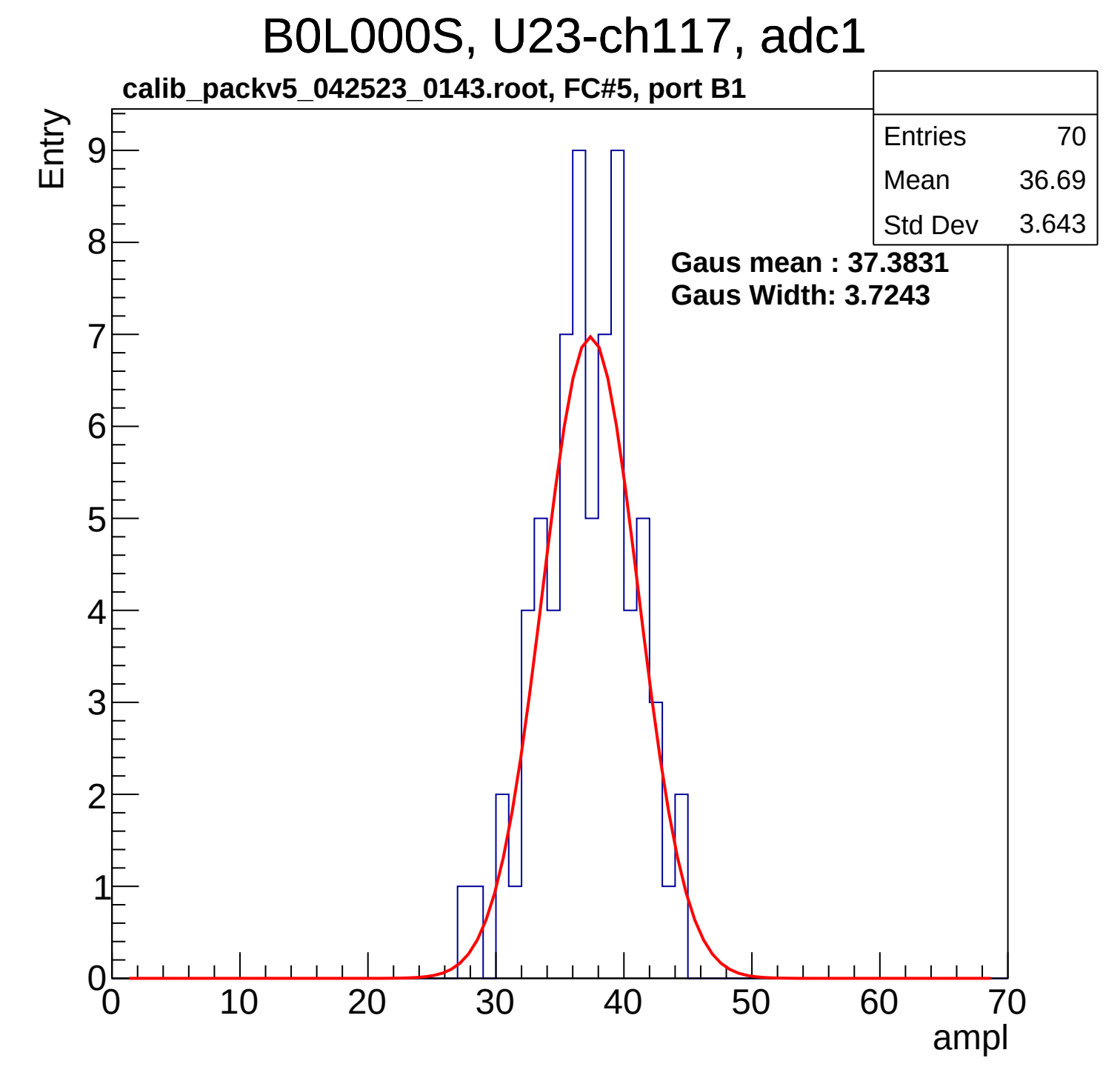

B0L000S, U23-ch116, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.1
Std Dev 2.979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

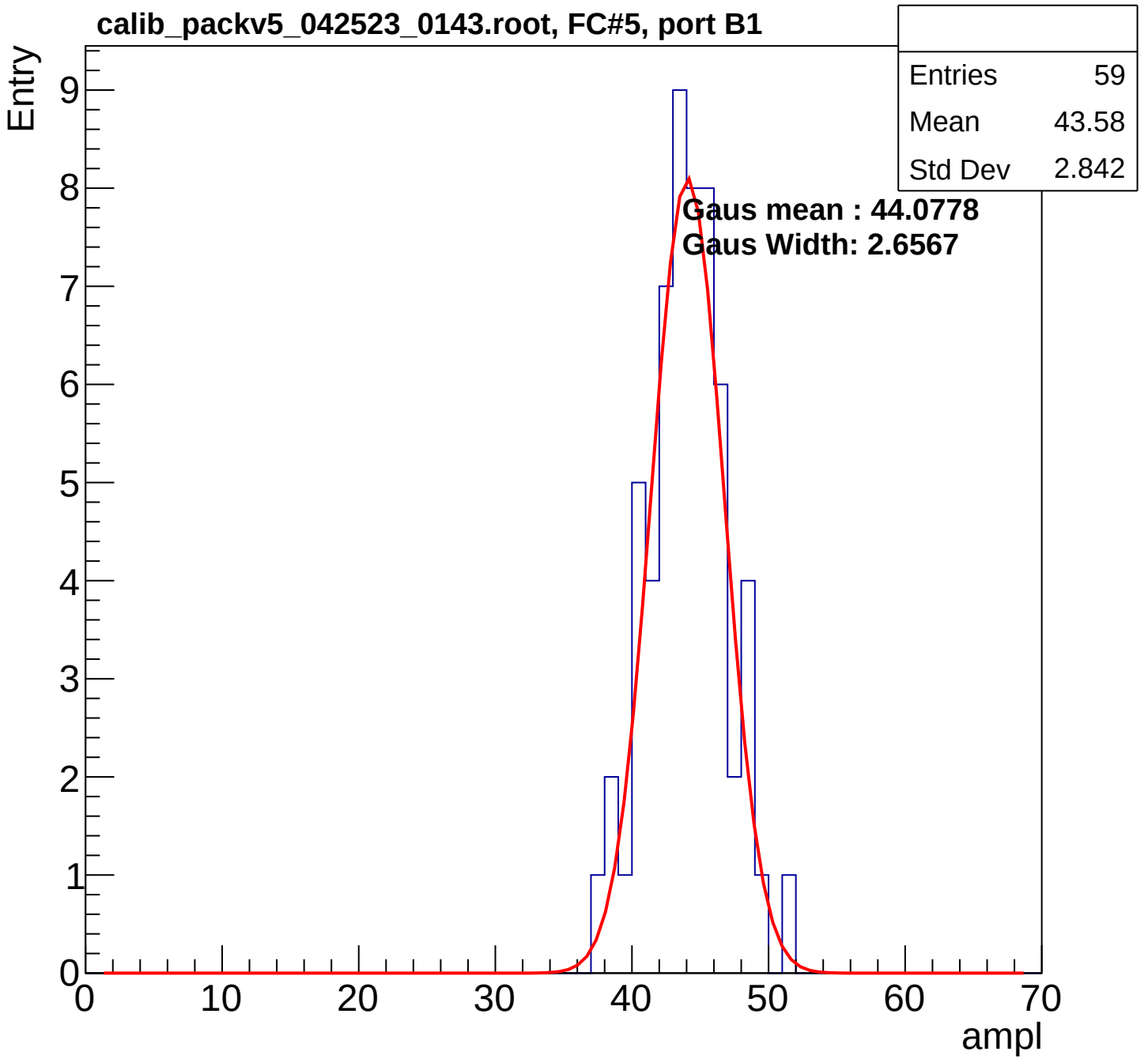

B0L000S, U23-ch116, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 58.38
Std Dev 7.819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

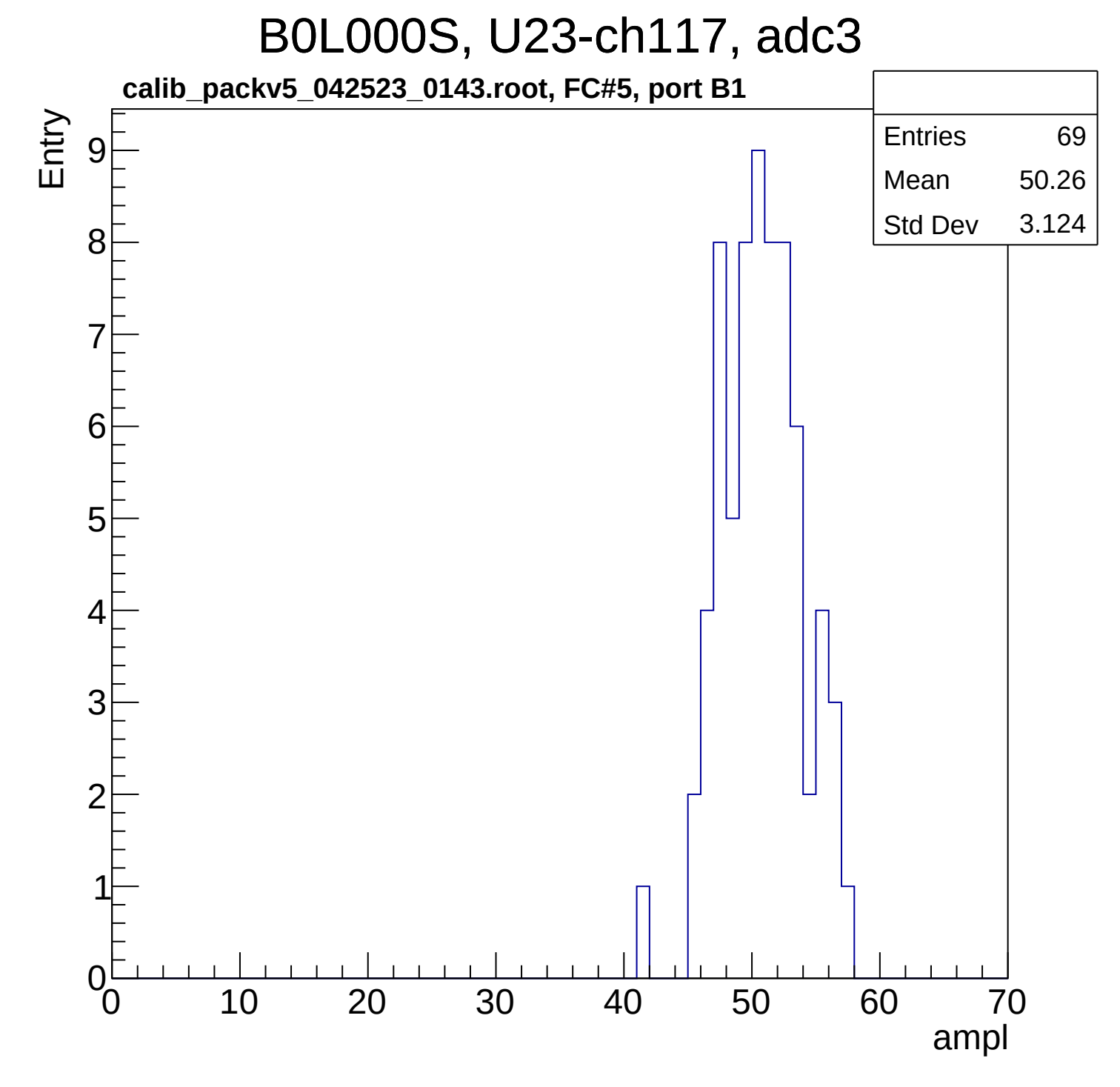

B0L000S, U23-ch116, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

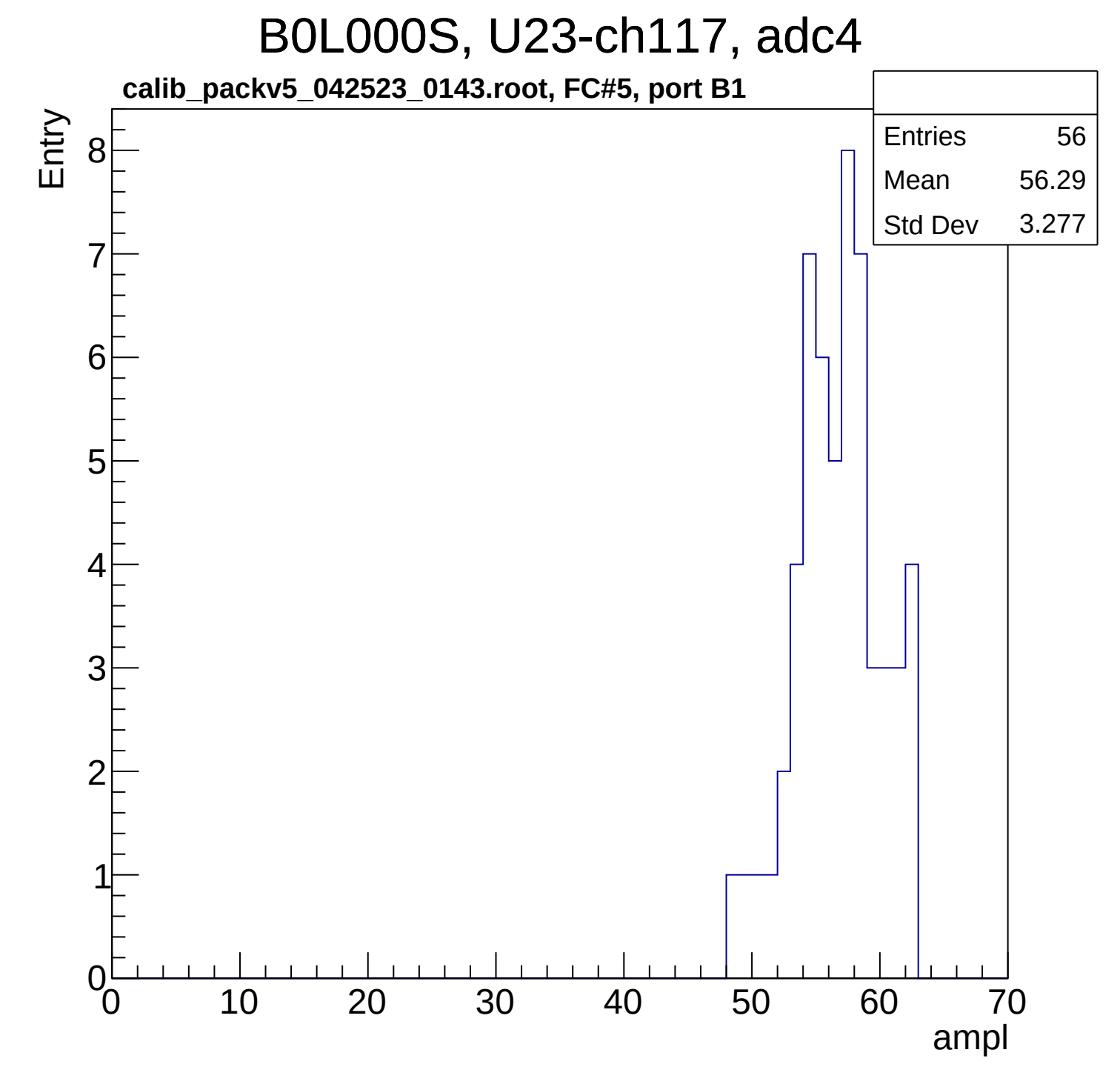

B0L000S, U23-ch116, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

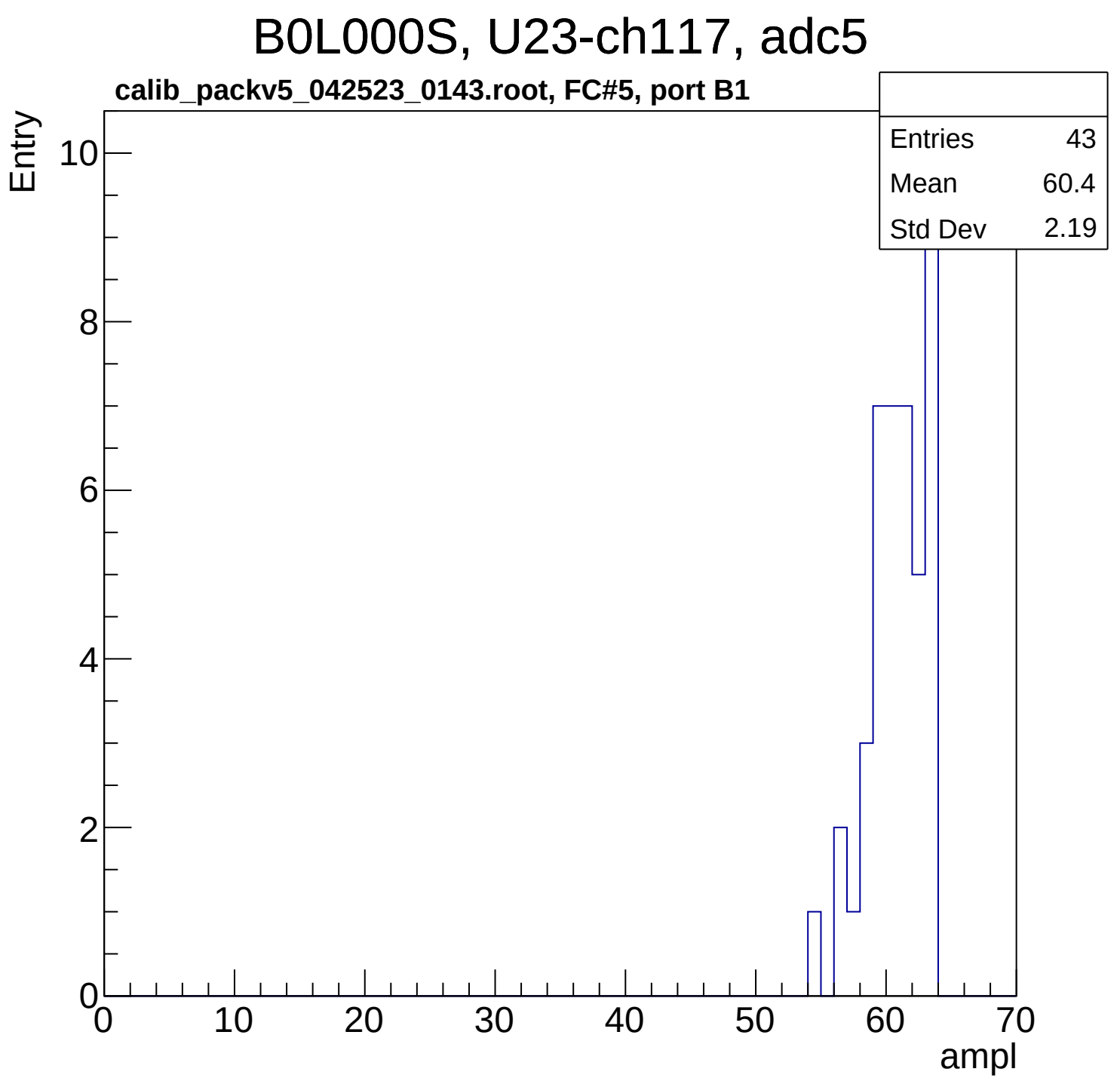

B0L000S, U23-ch117, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 31.53
Std Dev 3.559
Gaus mean : 31.8609
Gaus Width: 3.3119
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

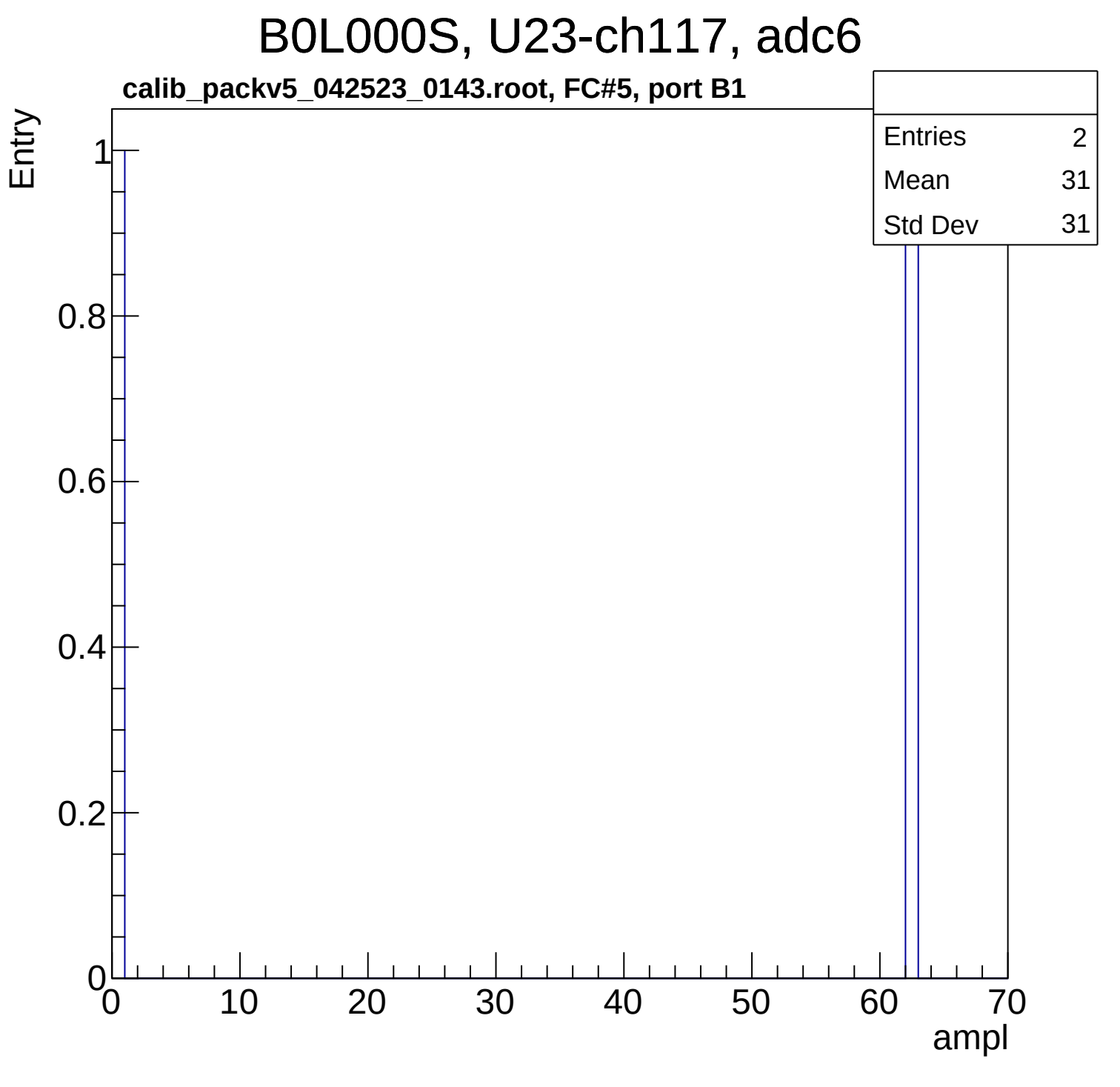

B0L000S, U23-ch117, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.69
Std Dev 3.643
Gaus mean : 37.3831
Gaus Width: 3.7243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

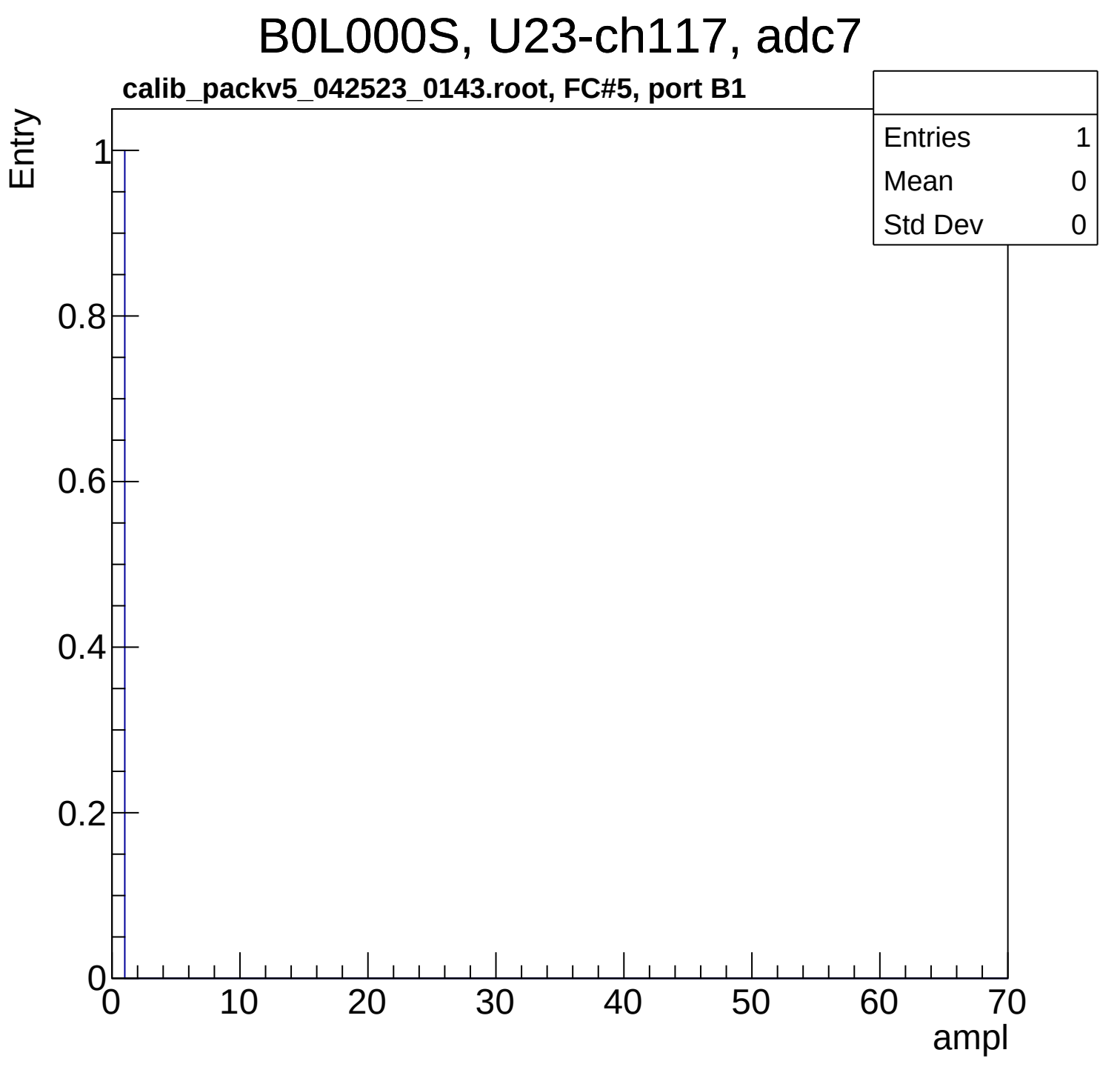

B0L000S, U23-ch117, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.58
Std Dev 2.842
Gaus mean : 44.0778
Gaus Width: 2.6567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

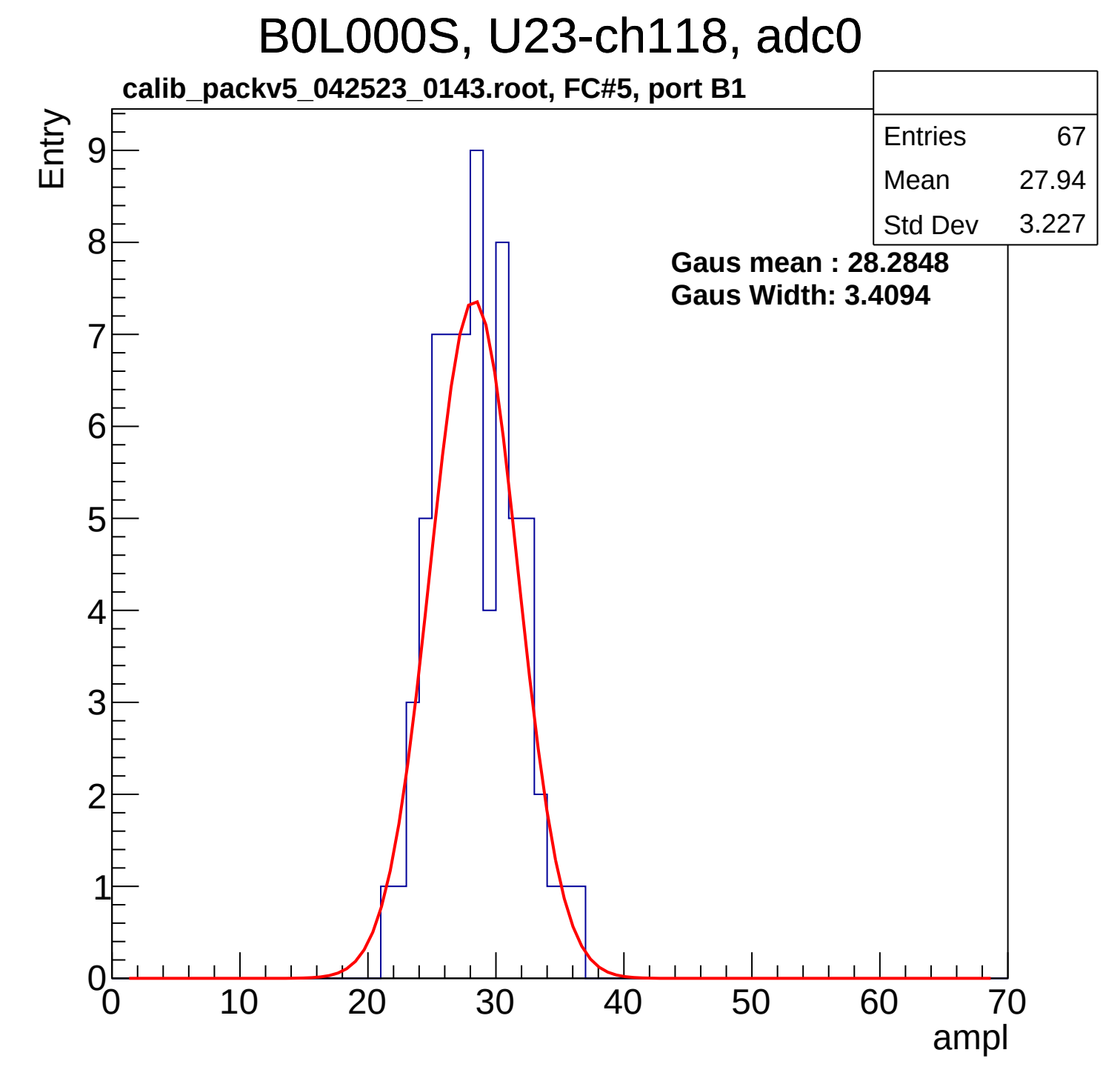

B0L000S, U23-ch117, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.26
Std Dev 3.124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

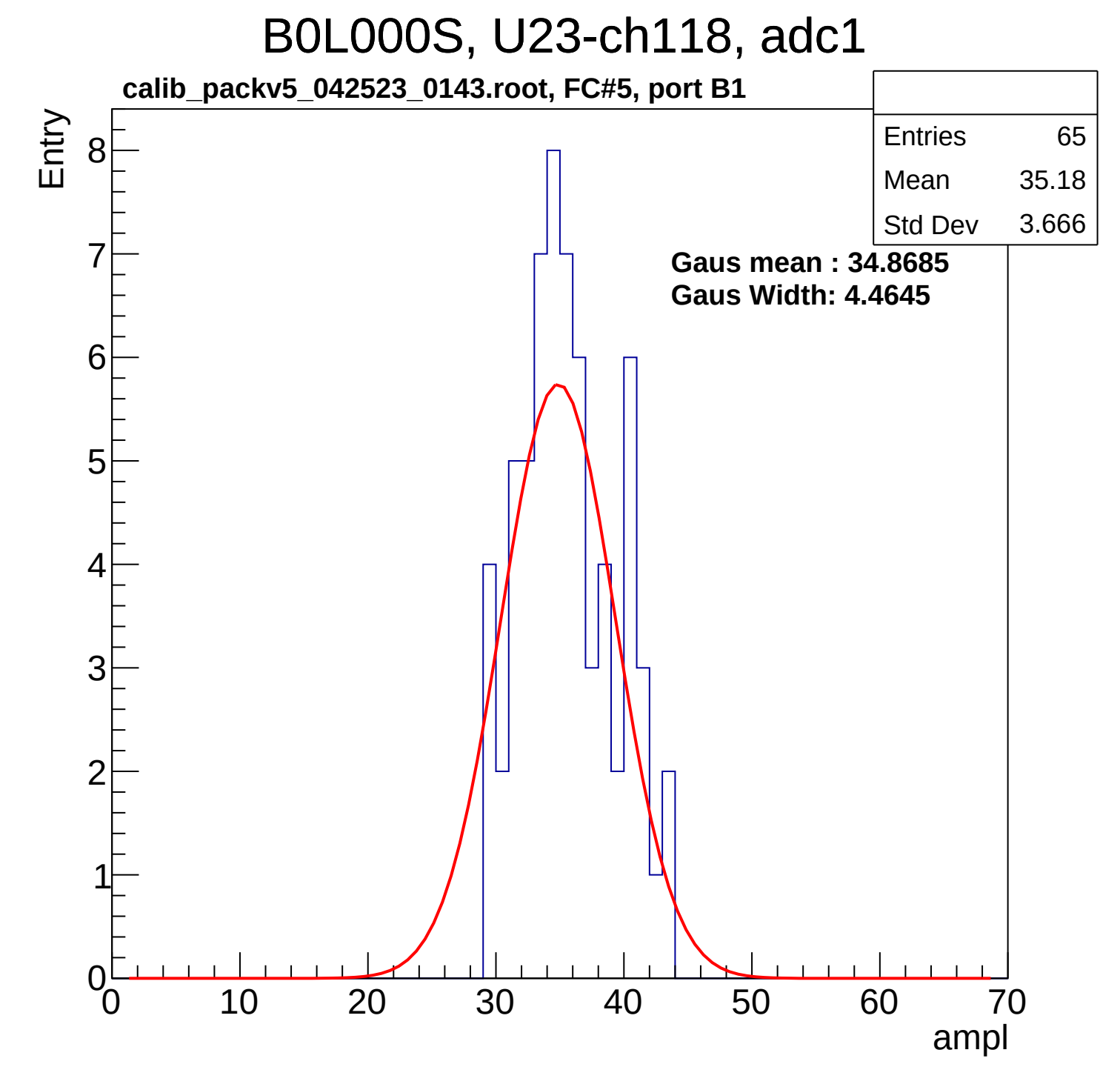

B0L000S, U23-ch117, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.29
Std Dev 3.277
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

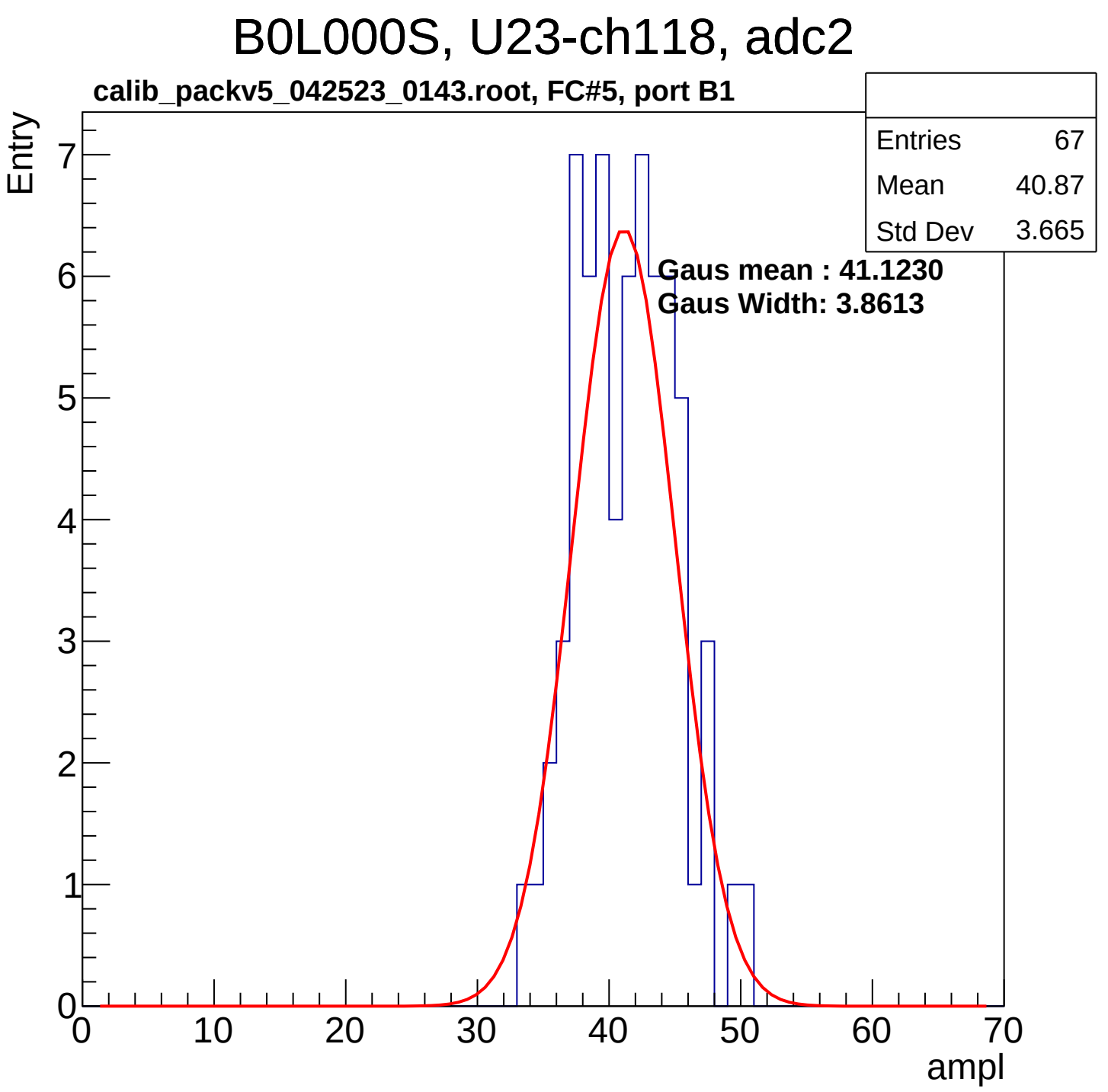

B0L000S, U23-ch117, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.4
Std Dev 2.19
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

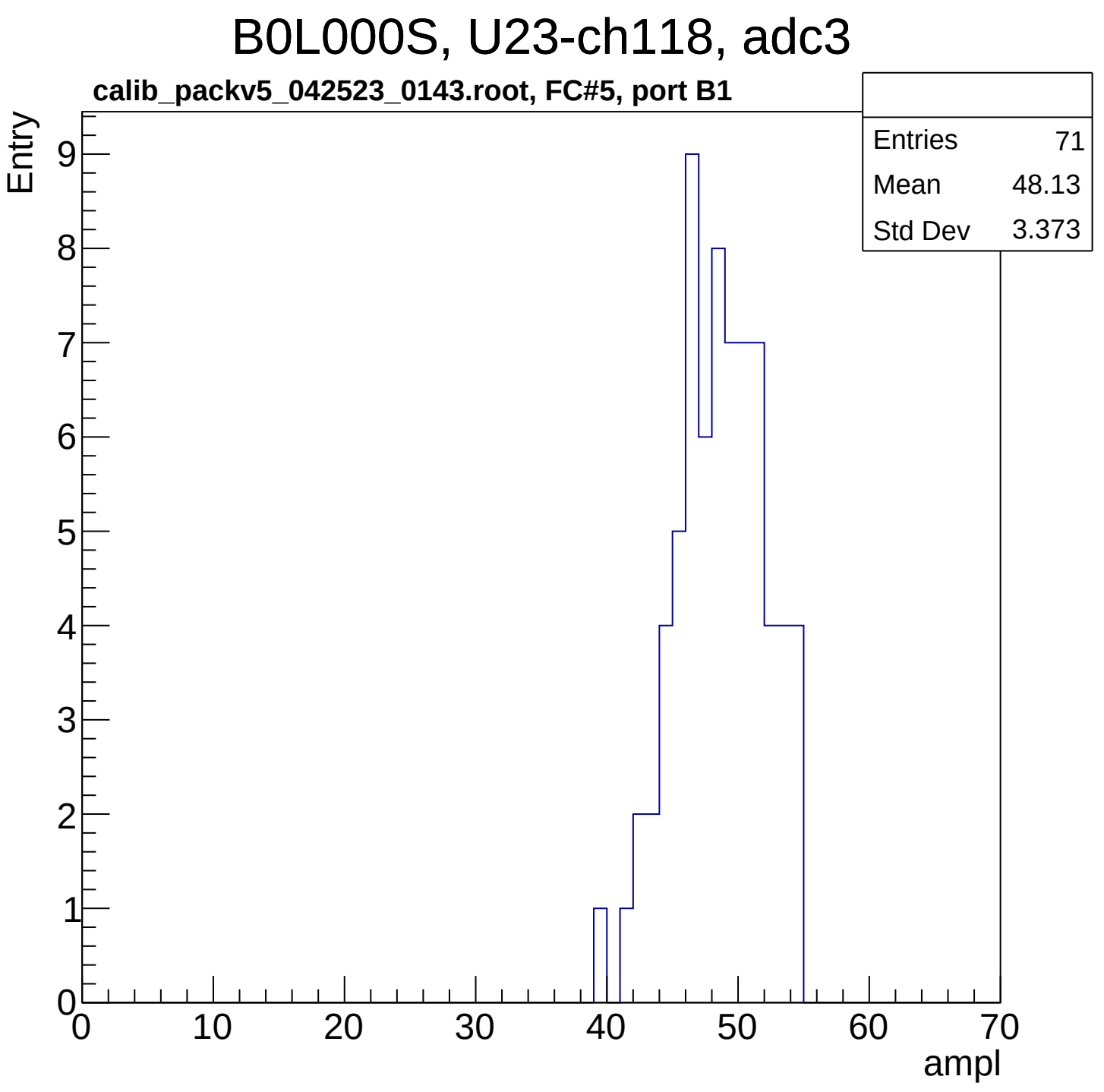

B0L000S, U23-ch117, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

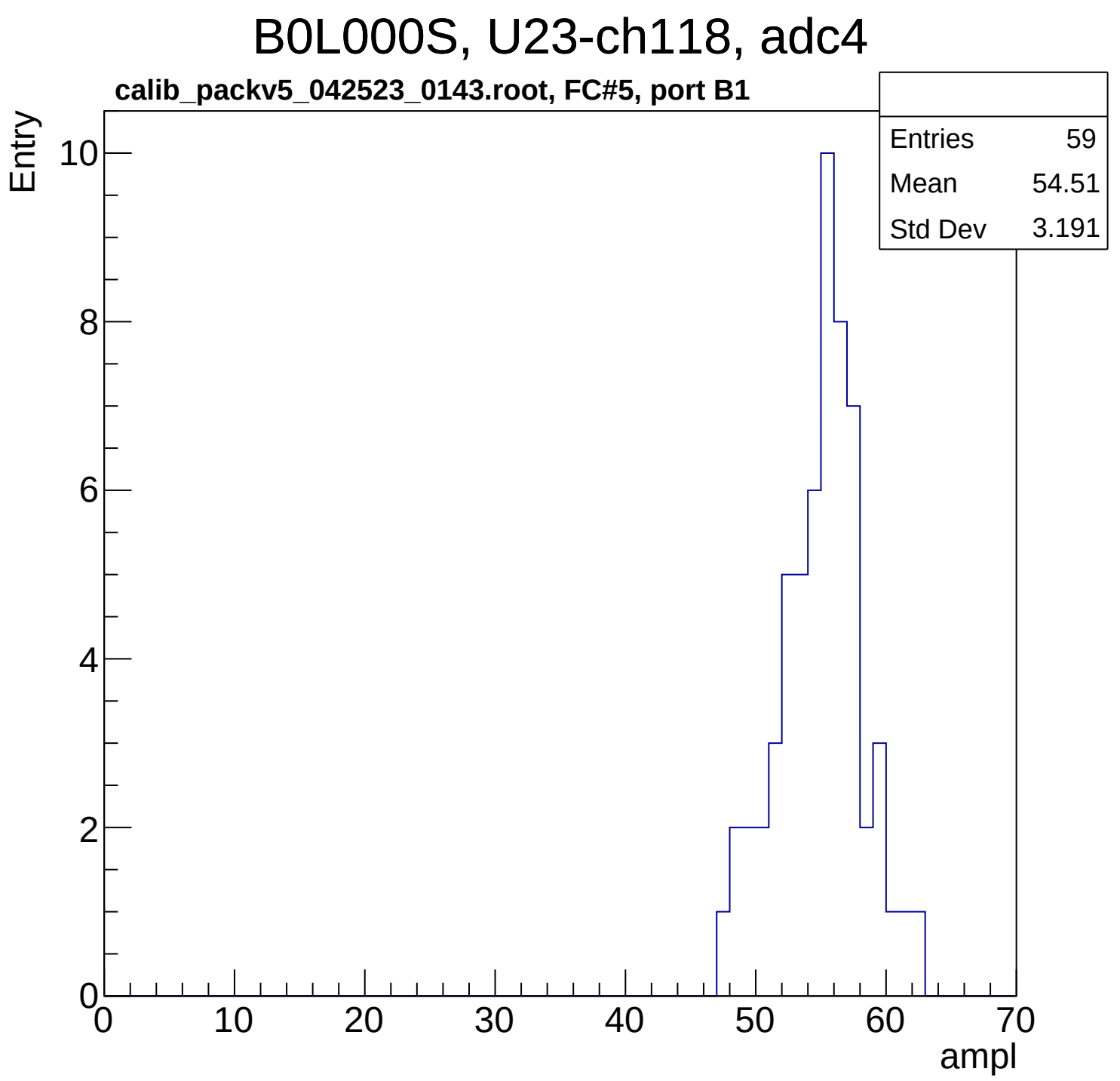

B0L000S, U23-ch117, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

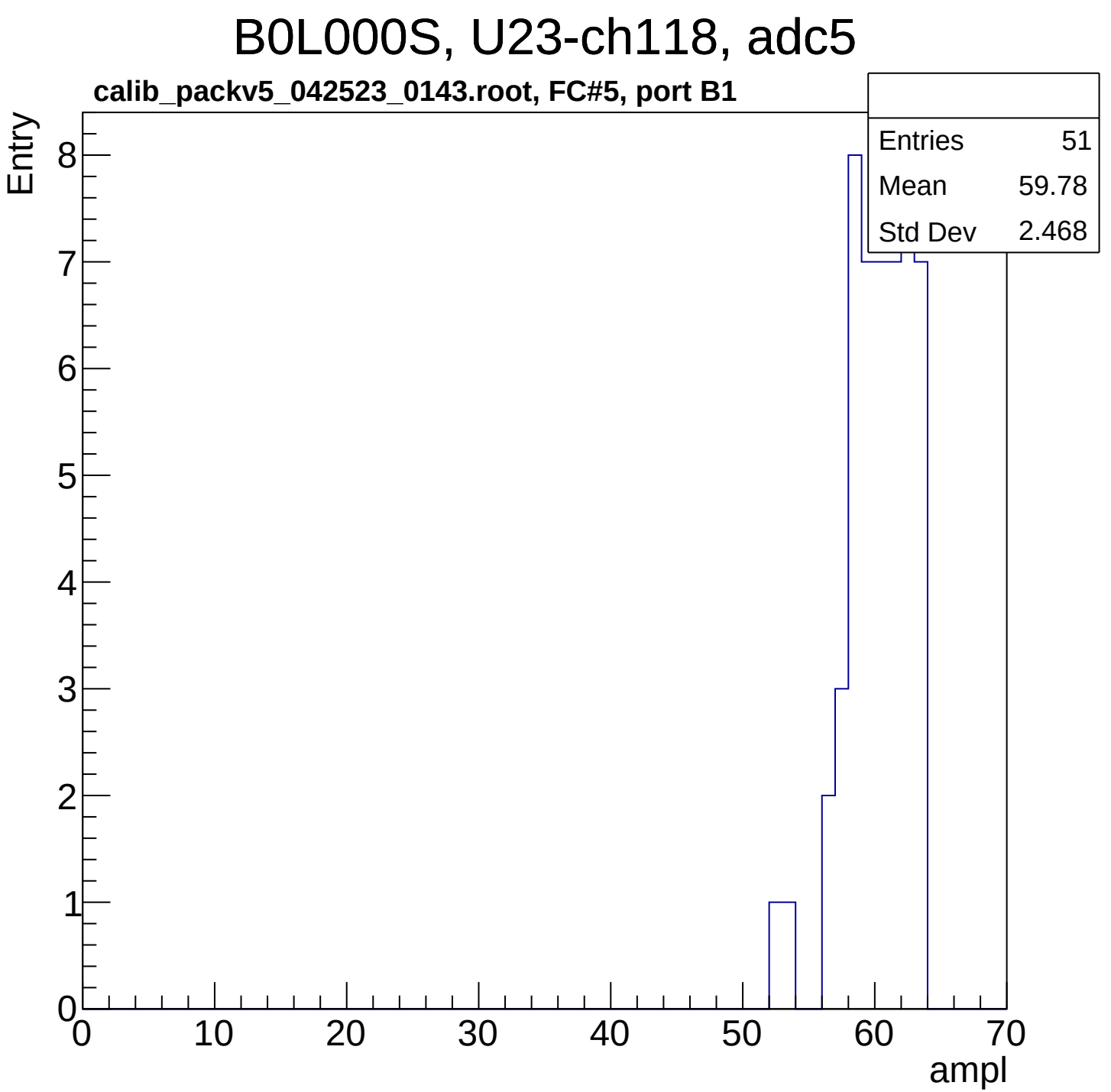

B0L000S, U23-ch118, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 27.94
Std Dev 3.227
Gaus mean : 28.2848
Gaus Width: 3.4094
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

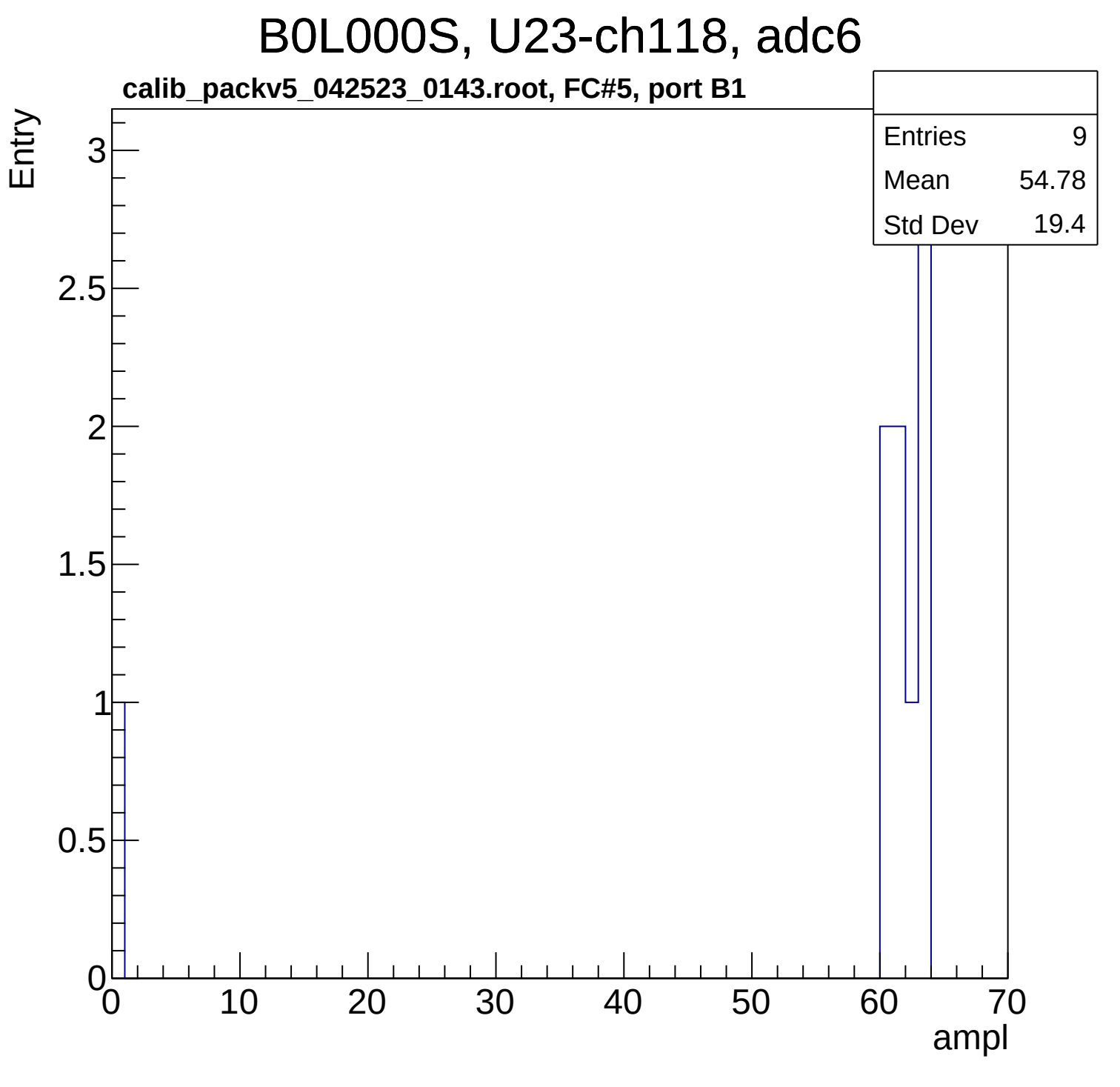

B0L000S, U23-ch118, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 35.18
Std Dev 3.666
Gaus mean : 34.8685
Gaus Width: 4.4645
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

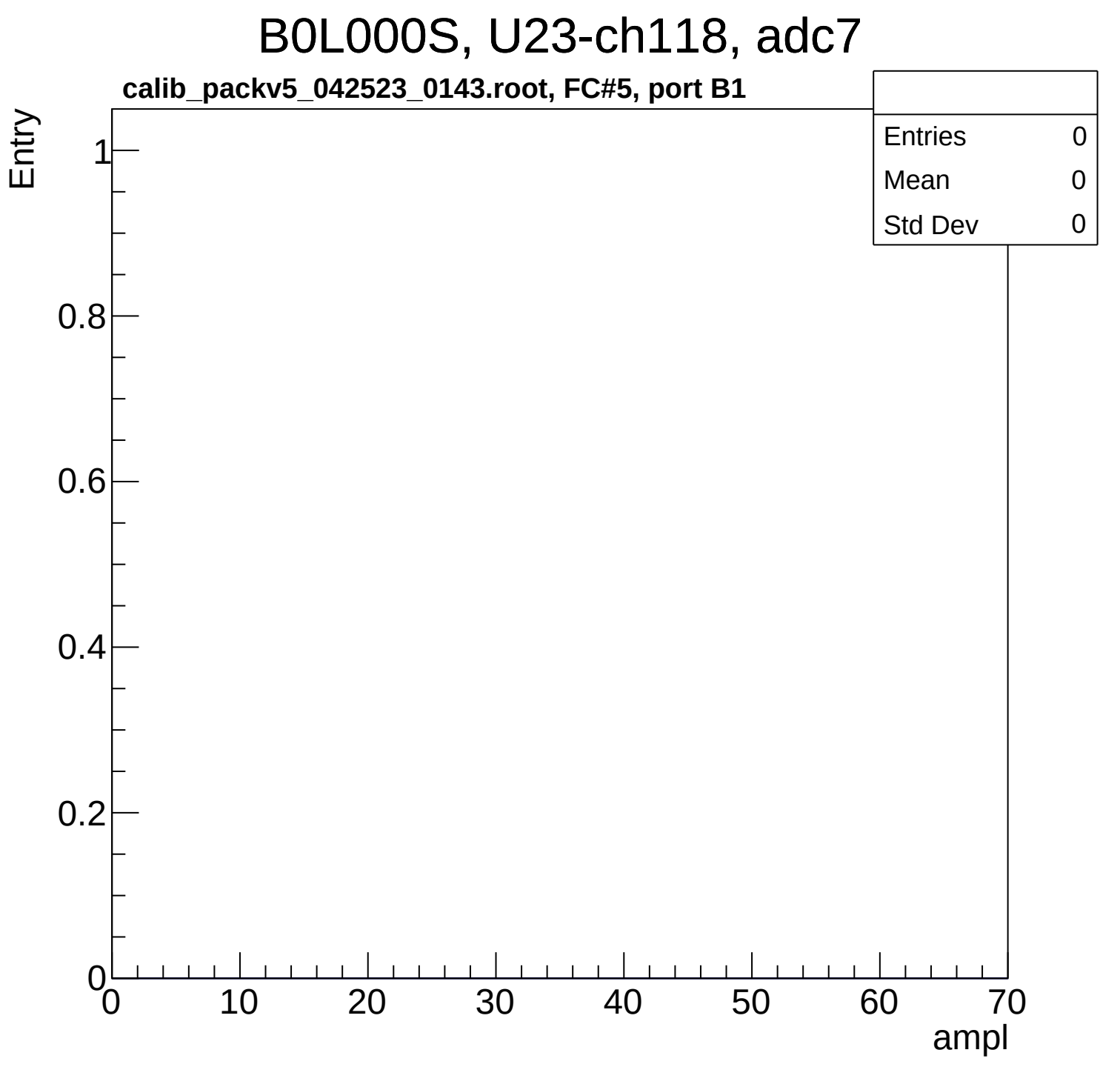

B0L000S, U23-ch118, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 40.87
Std Dev 3.665
Gaus mean : 41.1230
Gaus Width: 3.8613
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

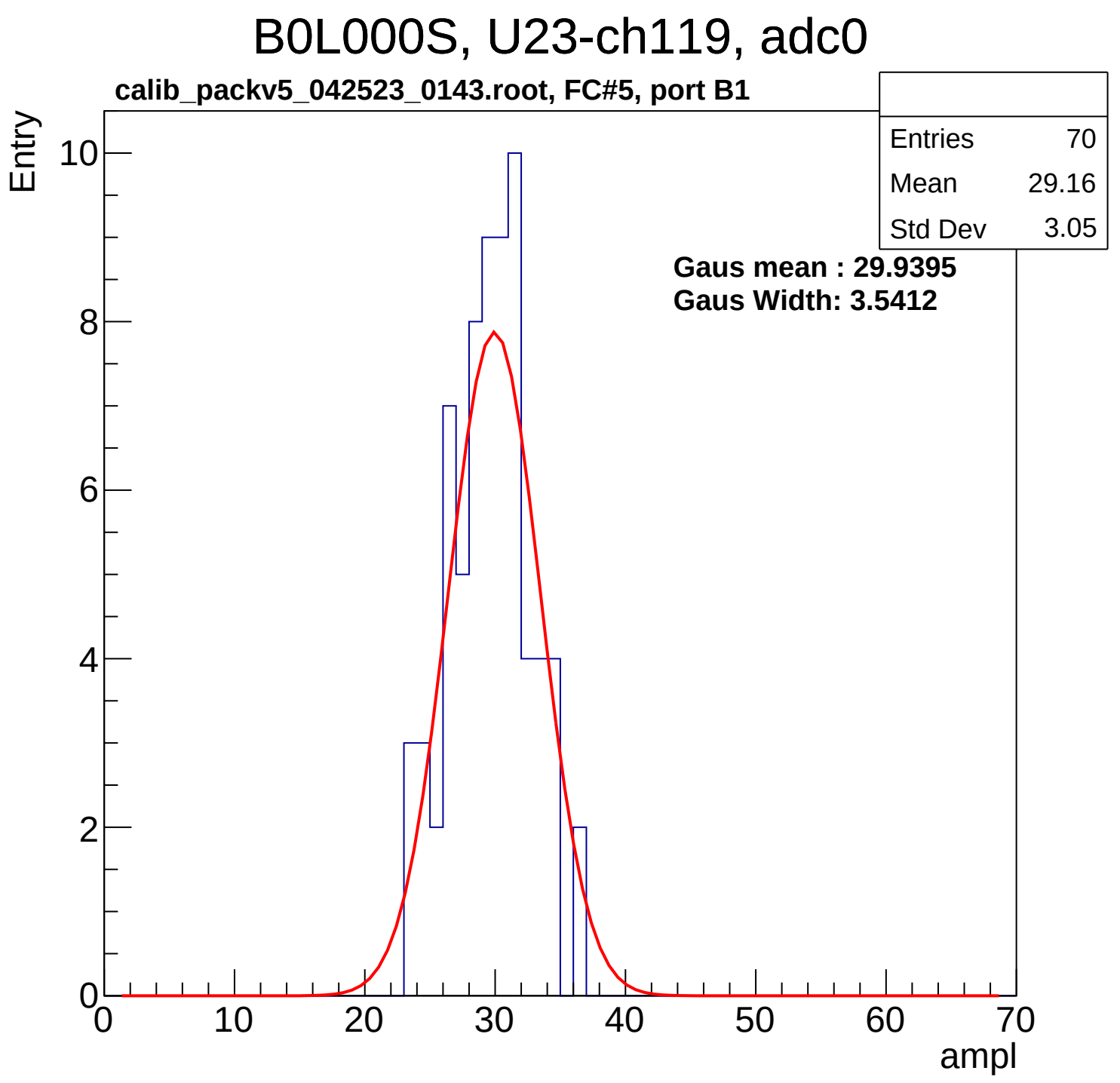

B0L000S, U23-ch118, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 48.13
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

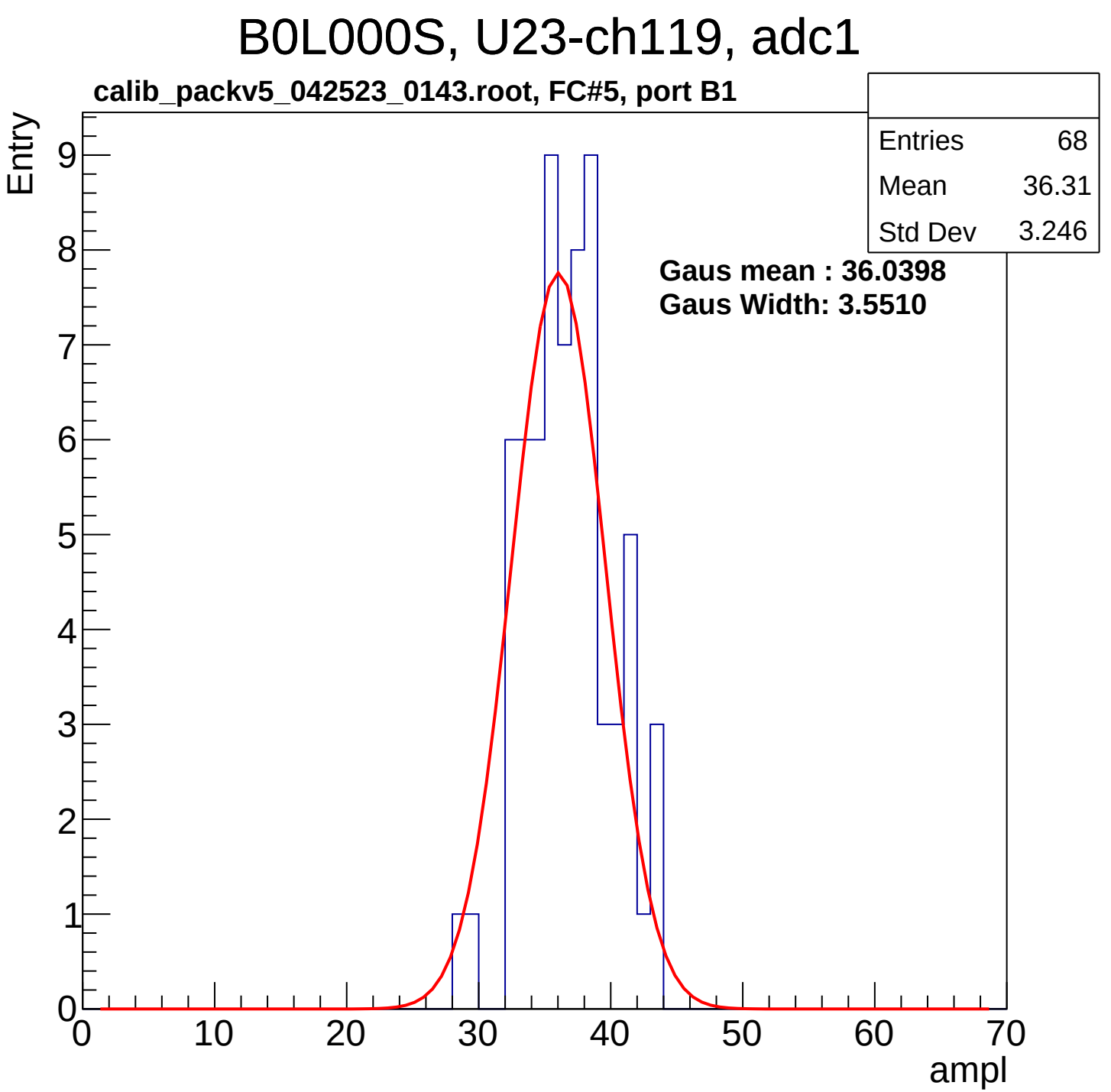

B0L000S, U23-ch118, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.51
Std Dev 3.191
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

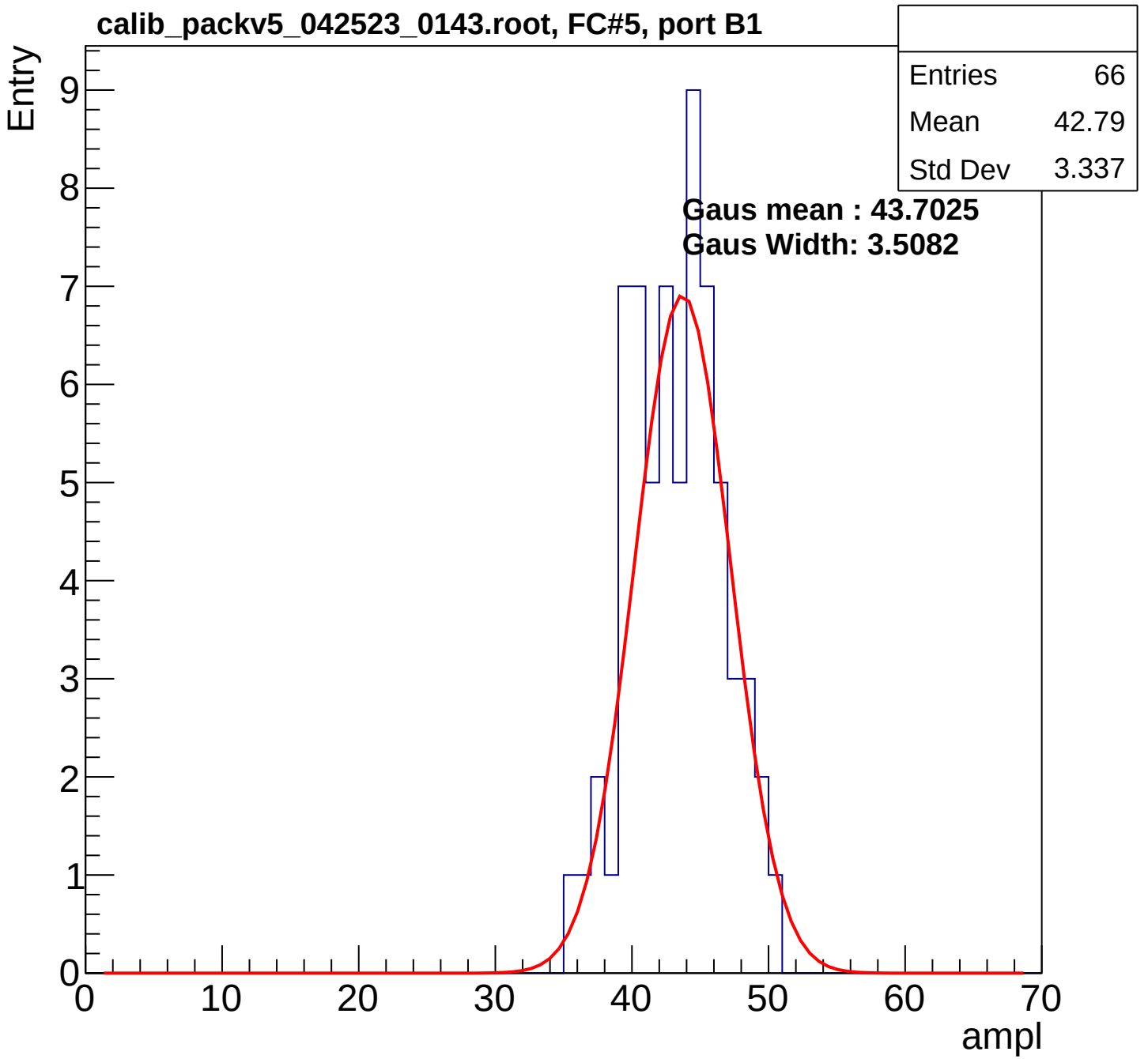

B0L000S, U23-ch118, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.78
Std Dev 2.468
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

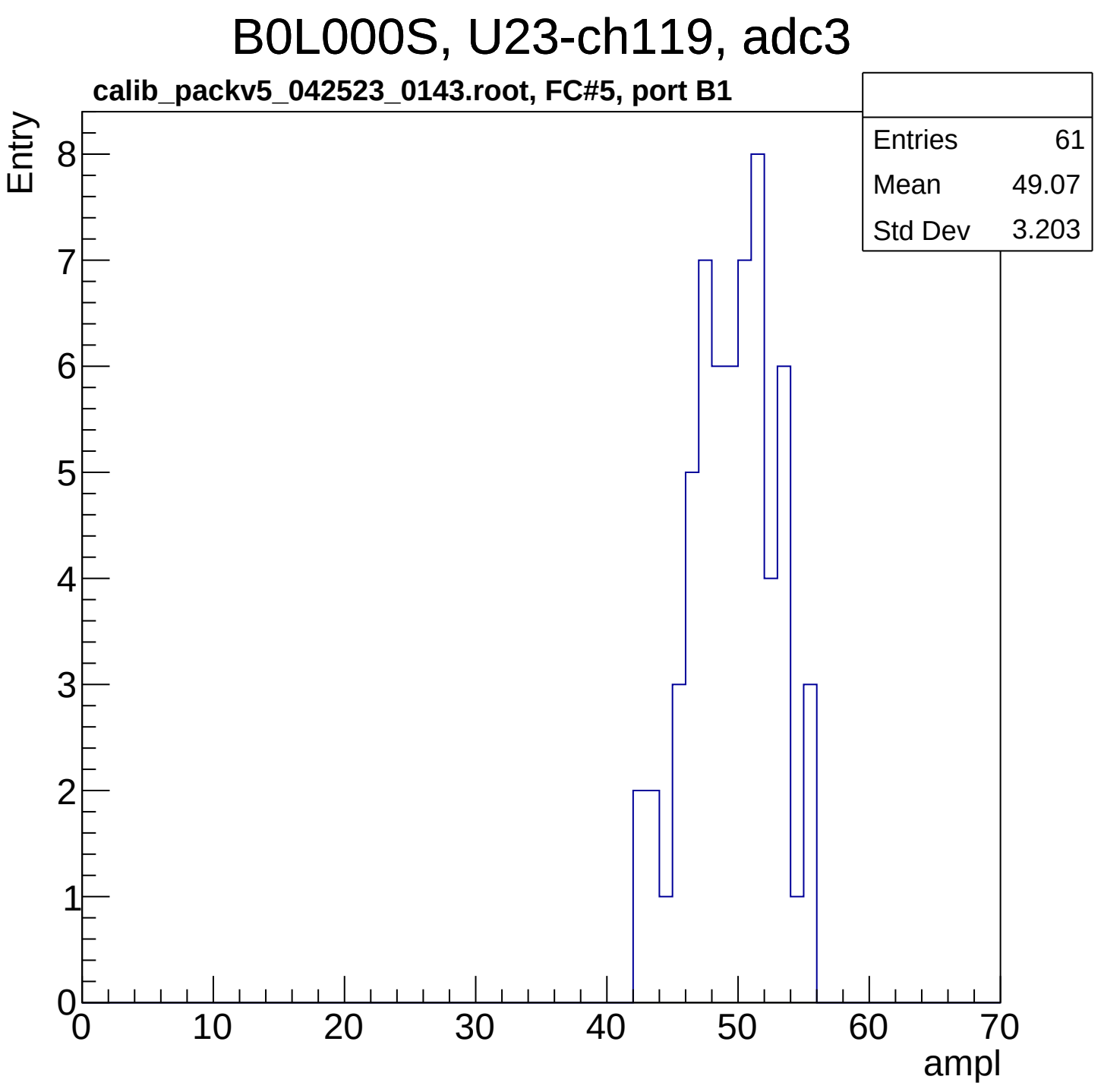

B0L000S, U23-ch118, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 54.78
Std Dev 19.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

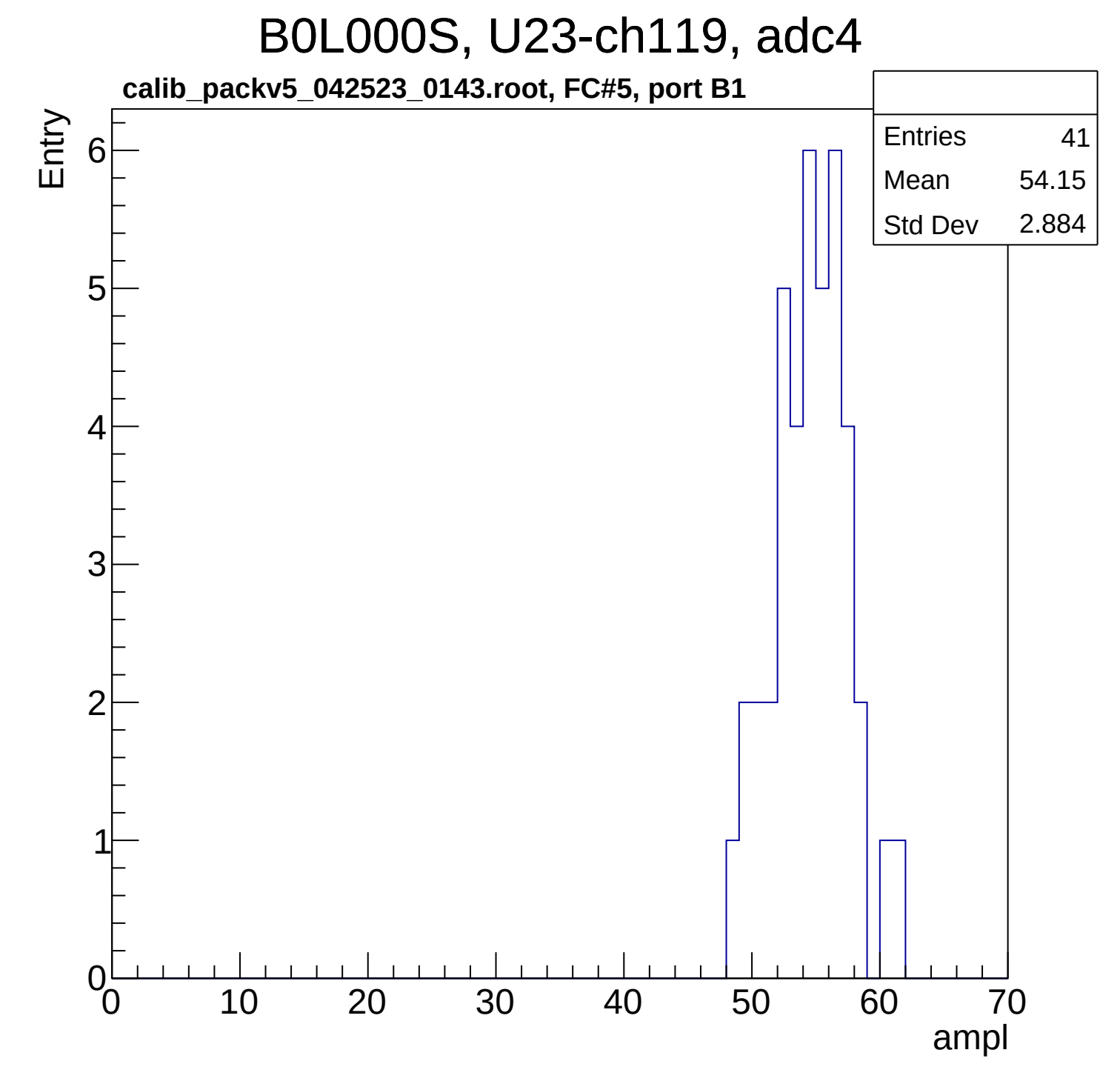

B0L000S, U23-ch118, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

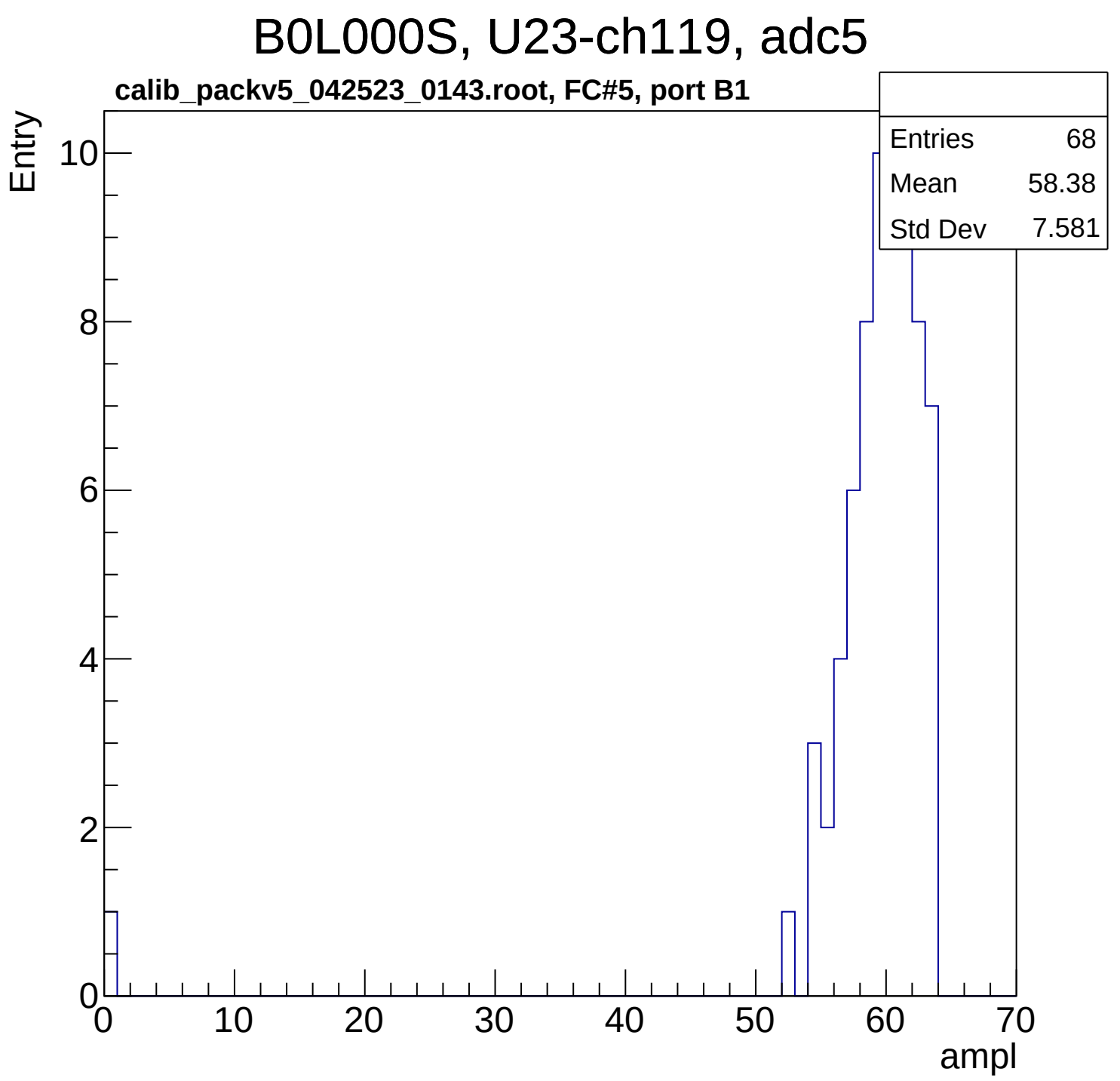

B0L000S, U23-ch119, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.16
Std Dev 3.05
Gaus mean : 29.9395
Gaus Width: 3.5412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

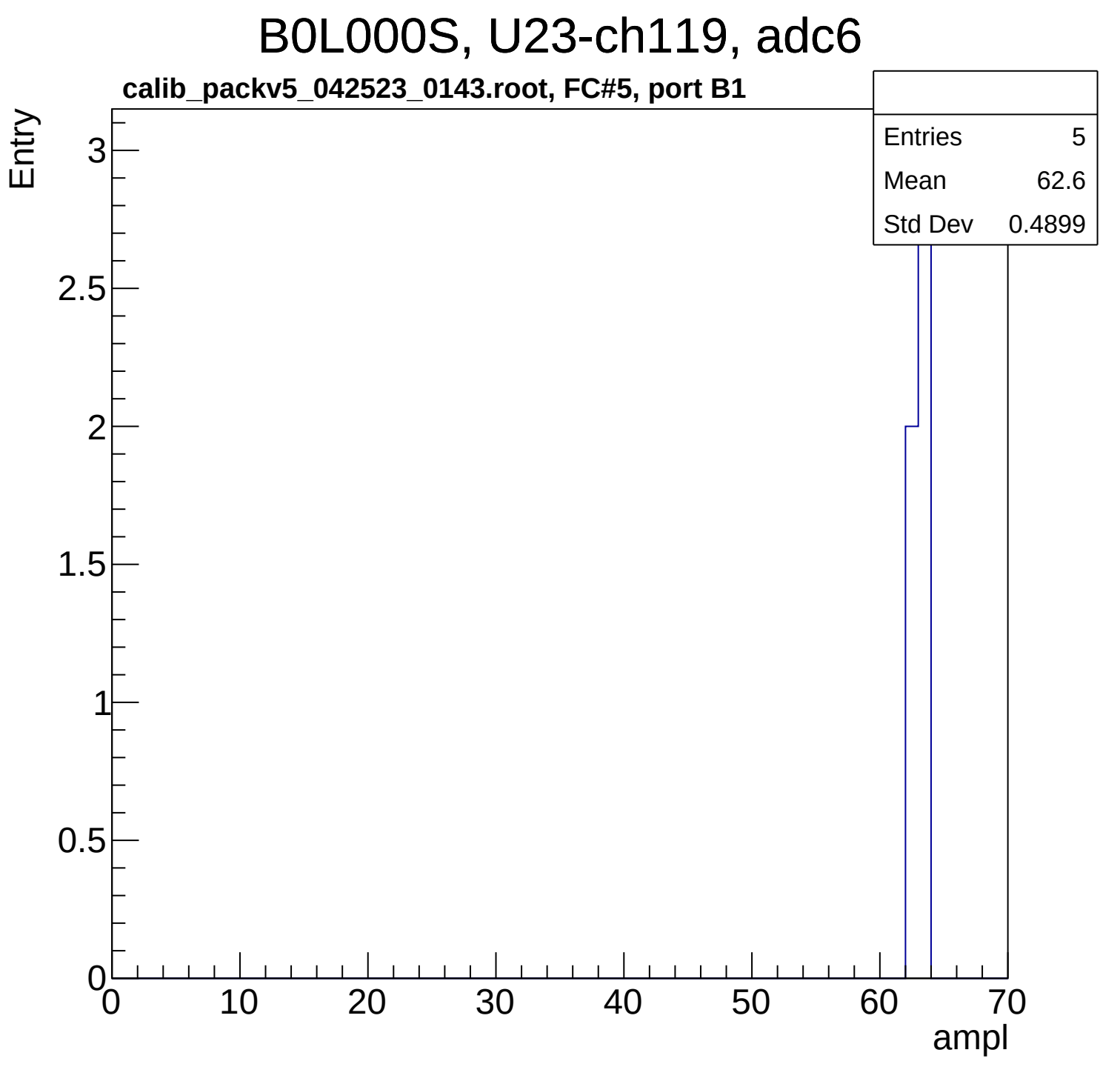

B0L000S, U23-ch119, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.31
Std Dev 3.246
Gaus mean : 36.0398
Gaus Width: 3.5510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

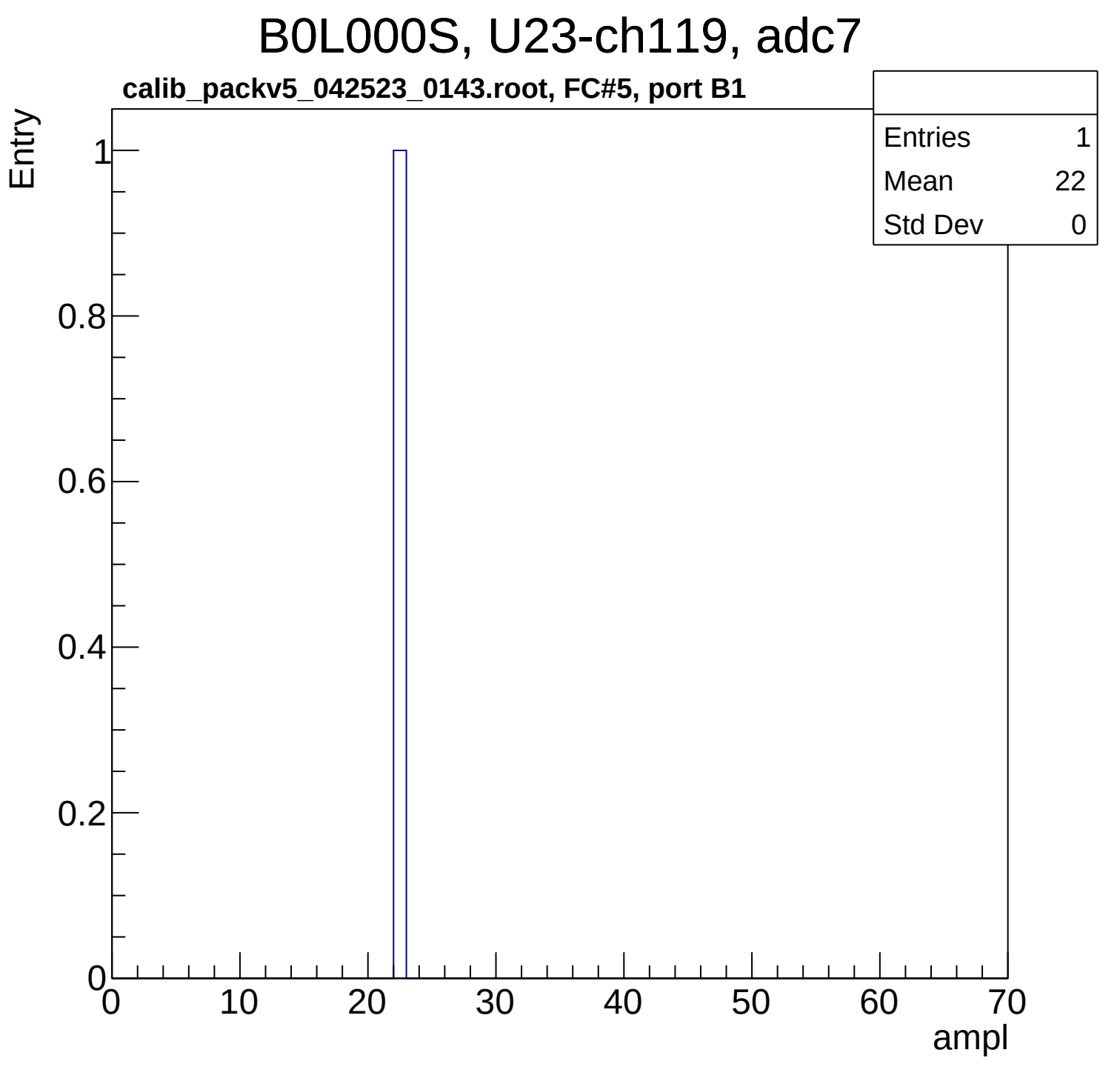

B0L000S, U23-ch119, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 42.79
Std Dev 3.337
Gaus mean : 43.7025
Gaus Width: 3.5082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

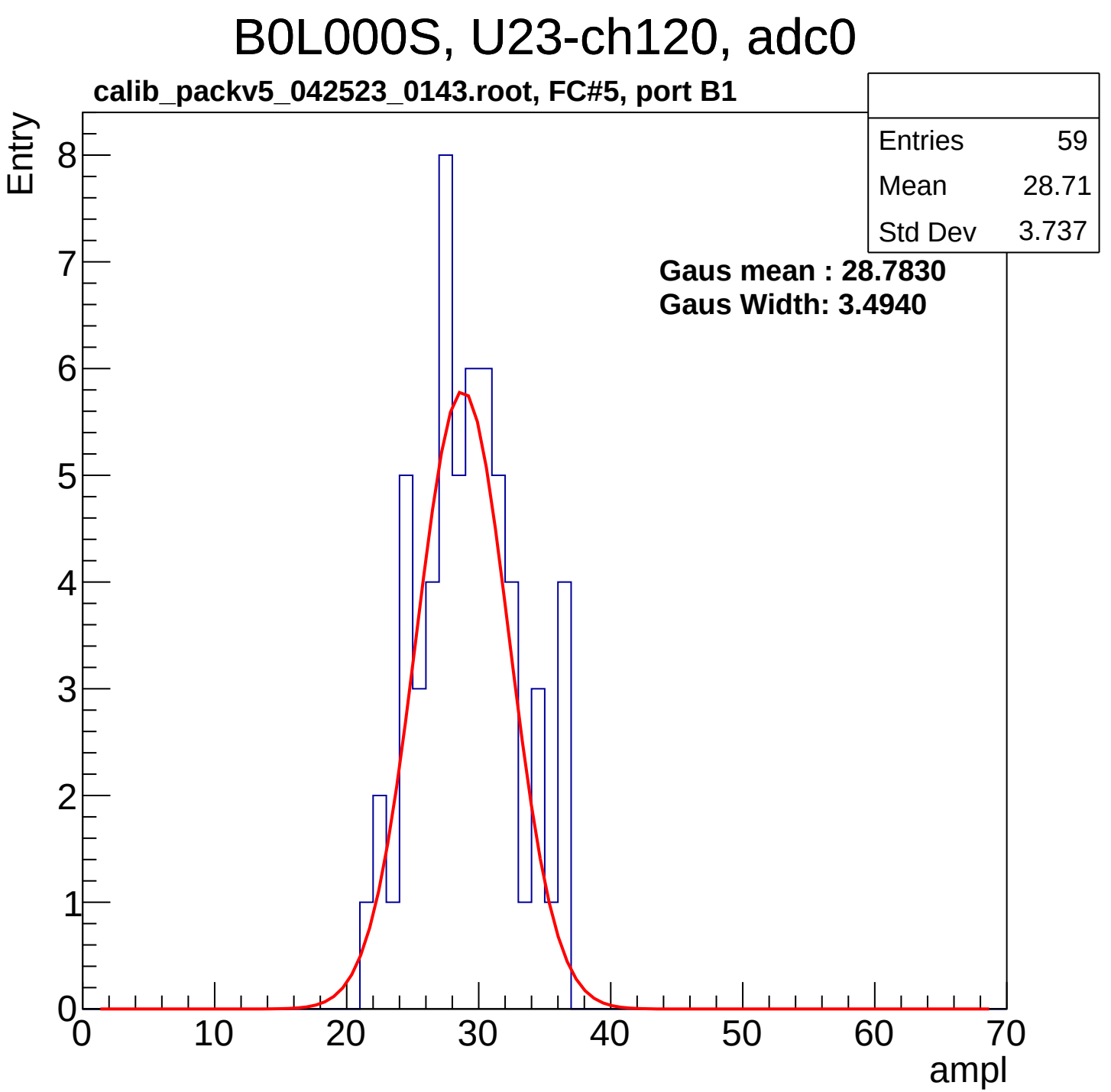

B0L000S, U23-ch119, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 49.07
Std Dev 3.203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

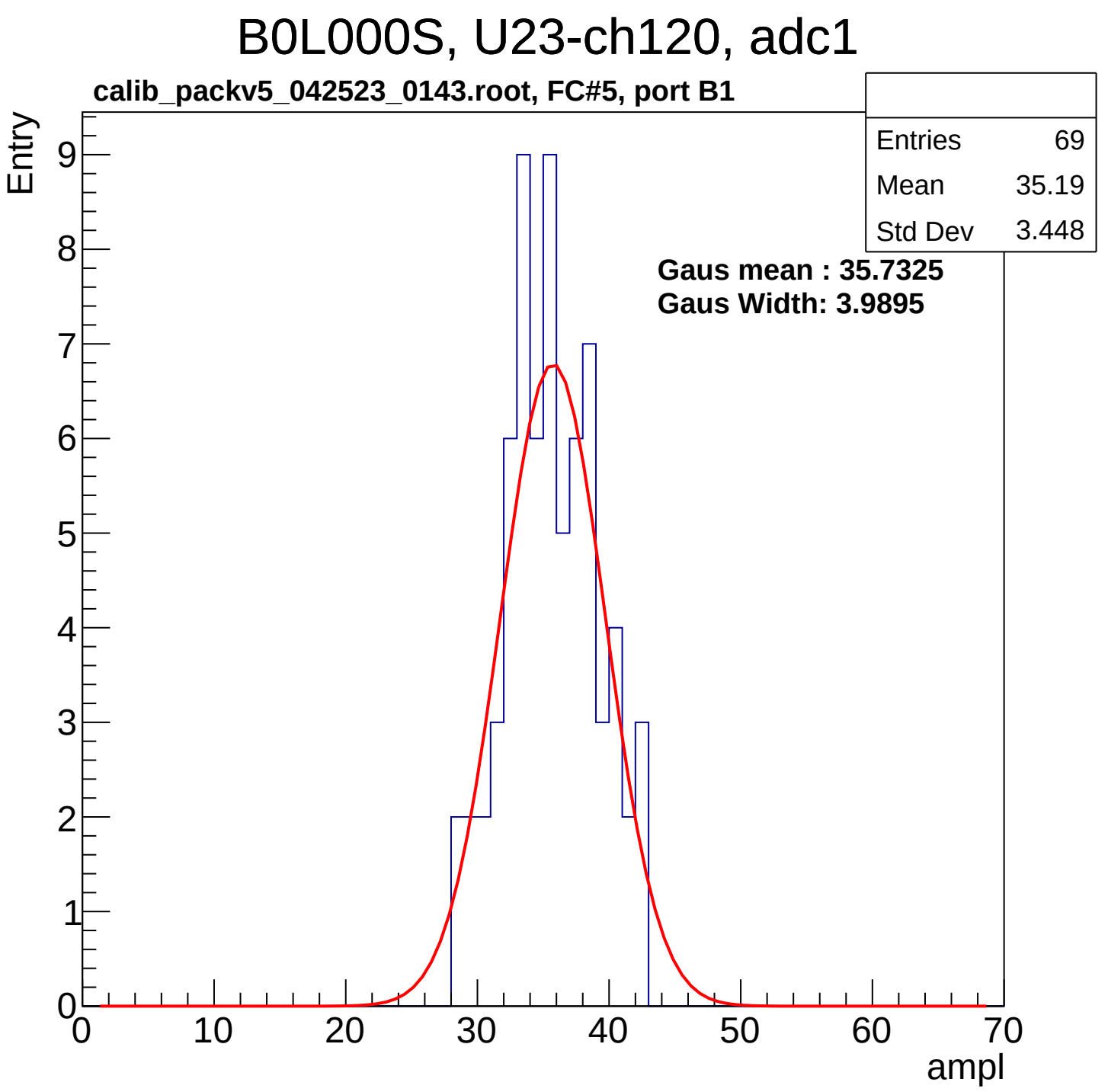

B0L000S, U23-ch119, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 54.15
Std Dev 2.884
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

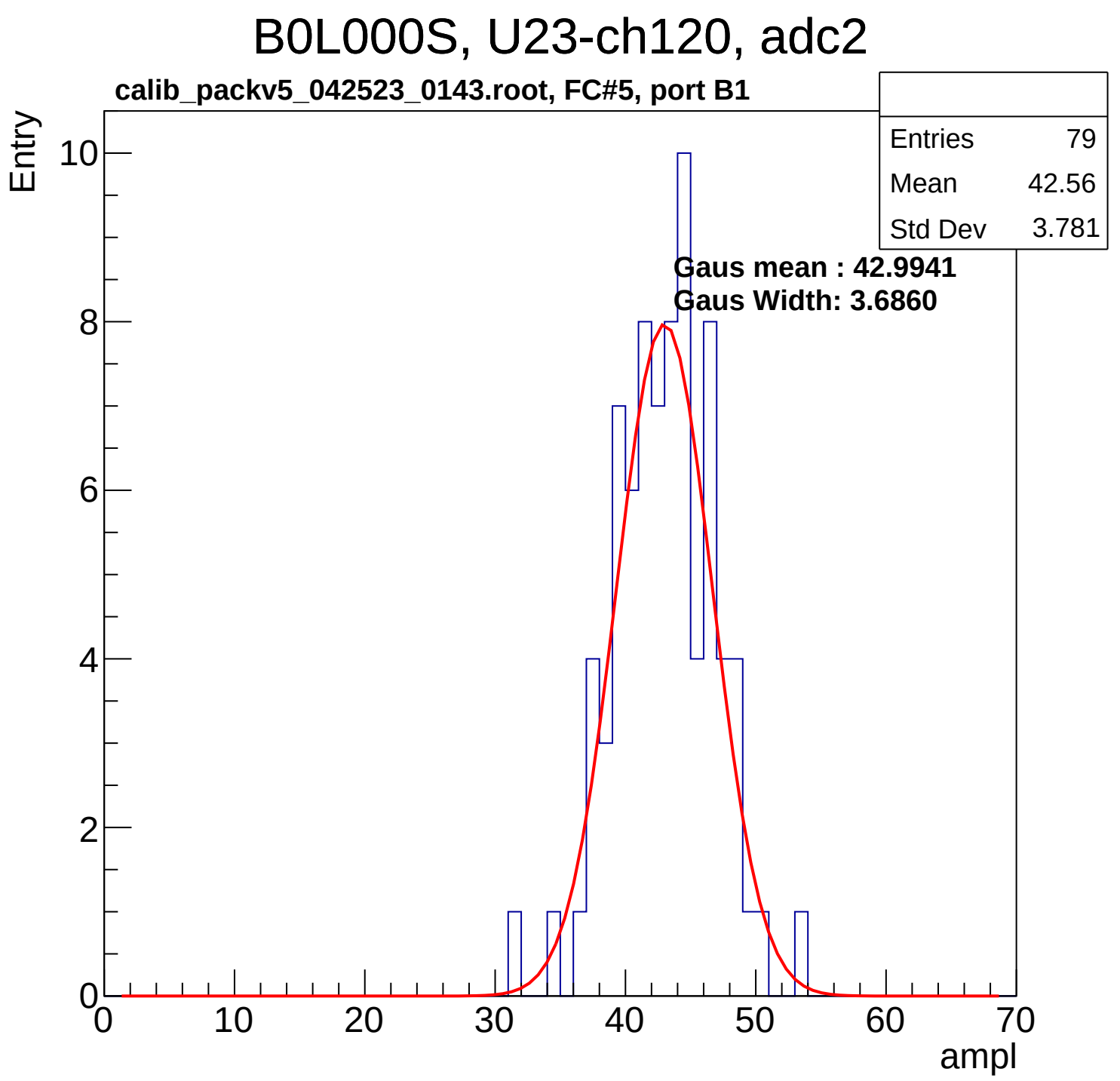

B0L000S, U23-ch119, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 58.38
Std Dev 7.581
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

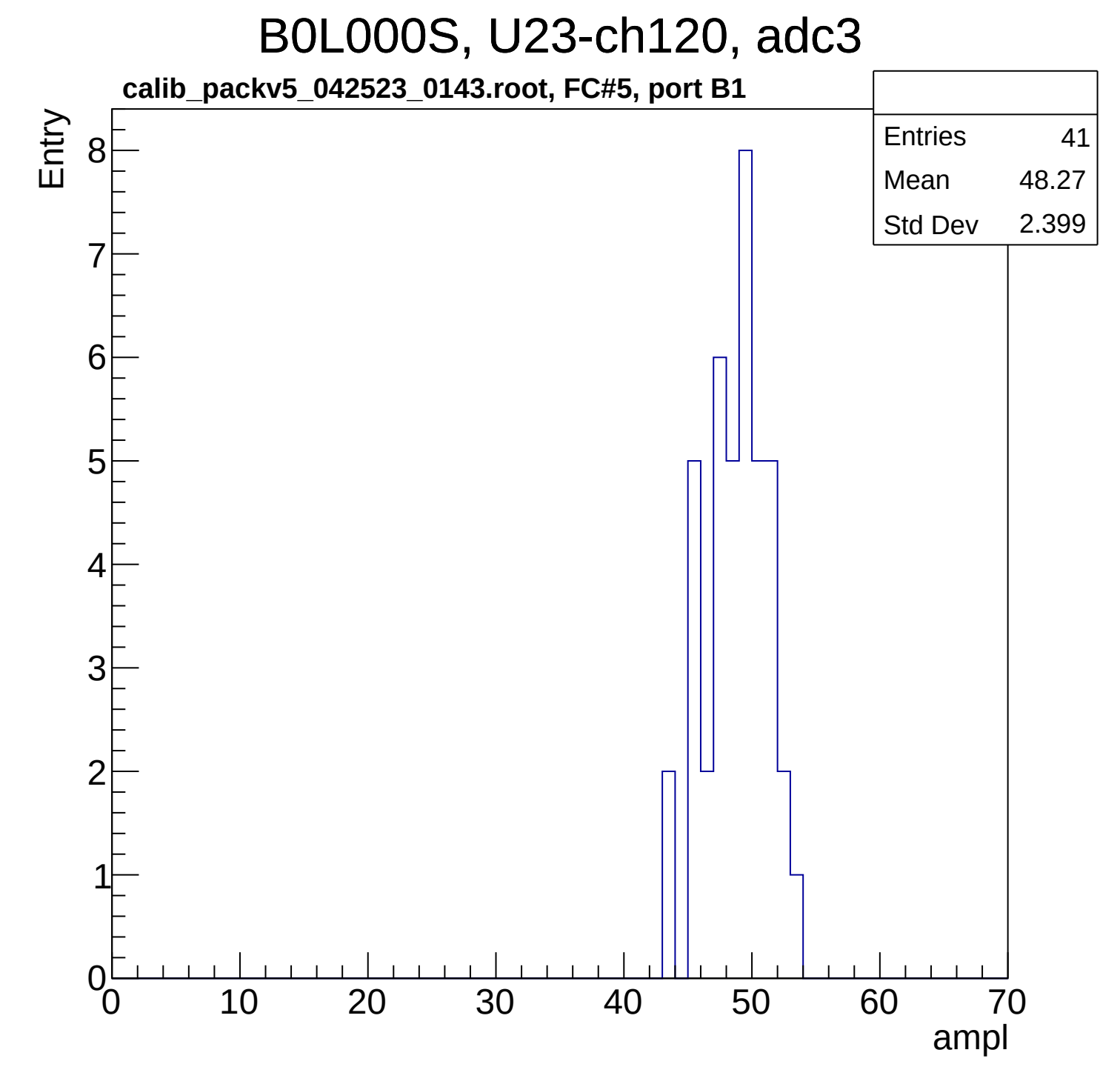

B0L000S, U23-ch119, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

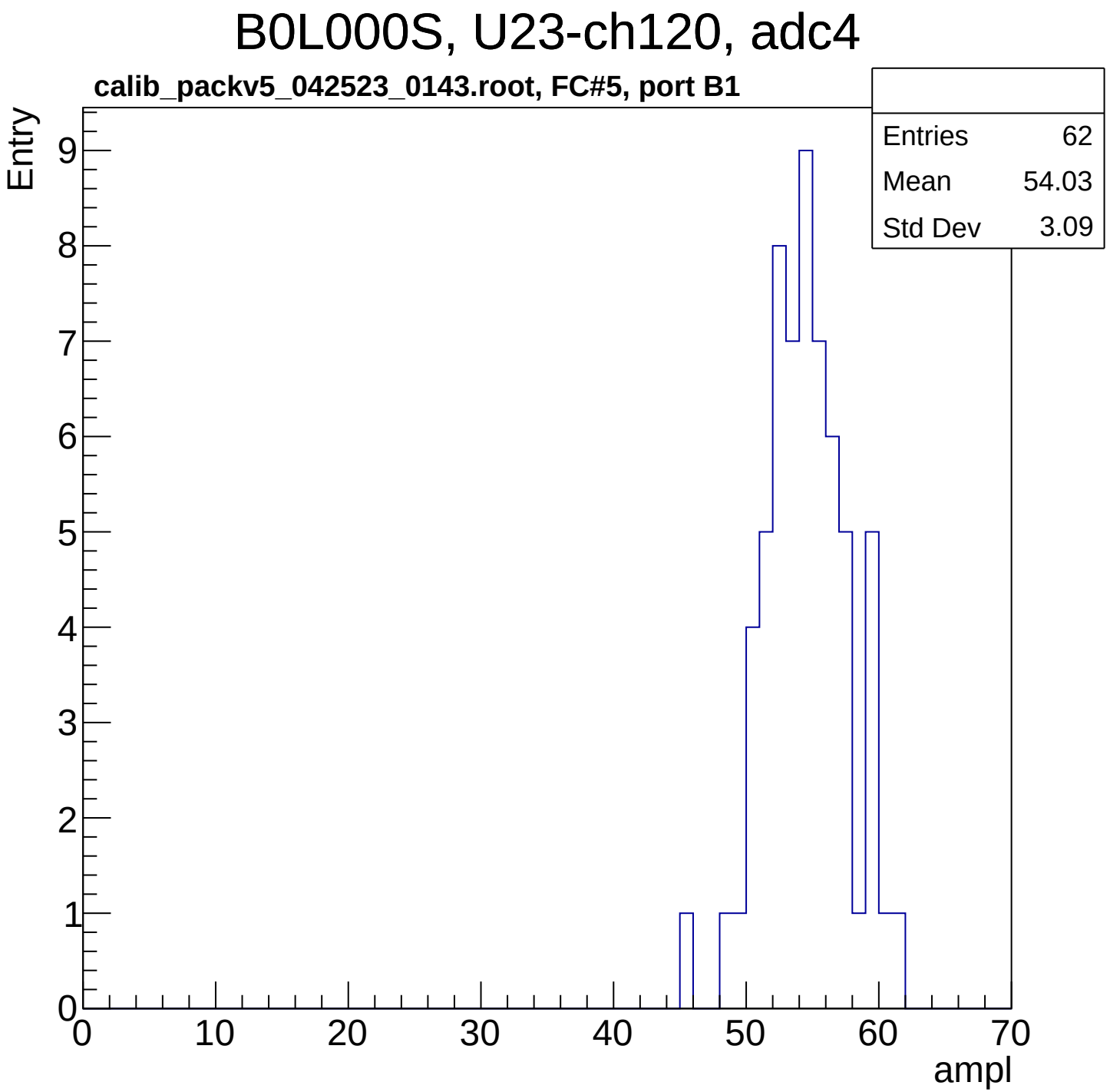

B0L000S, U23-ch119, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

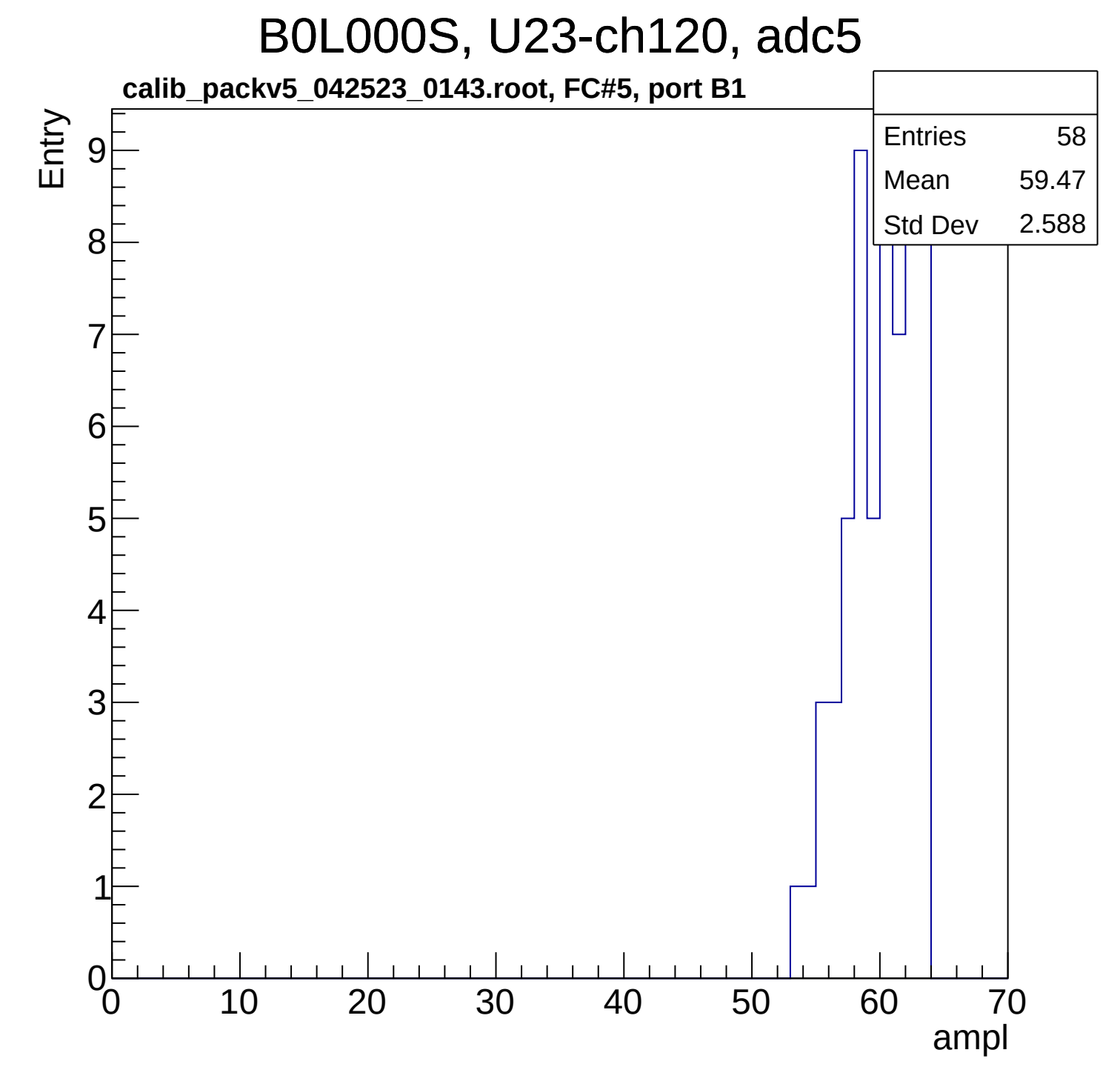

B0L000S, U23-ch120, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 28.71
Std Dev 3.737
Gaus mean : 28.7830
Gaus Width: 3.4940
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

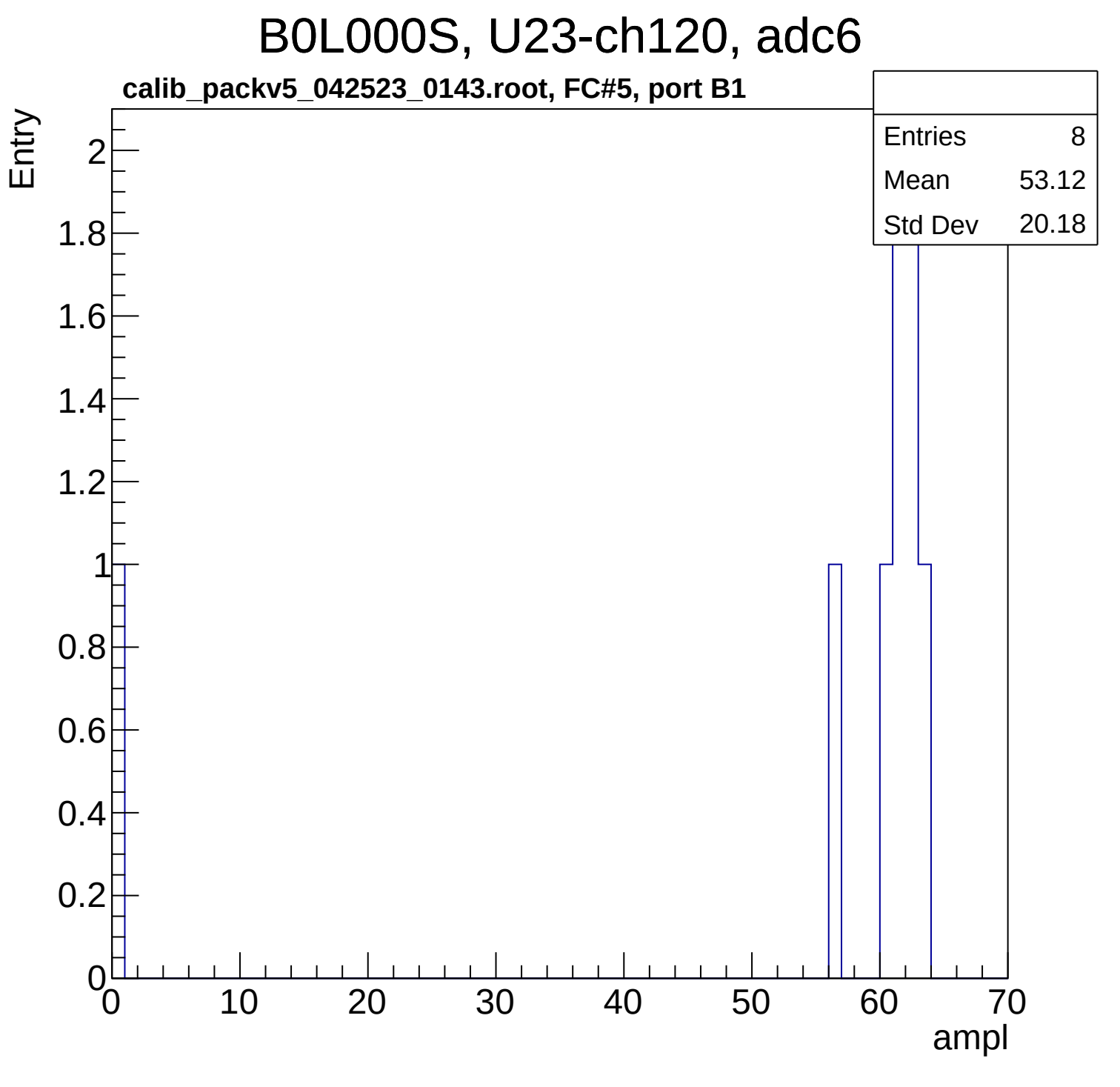

B0L000S, U23-ch120, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.19
Std Dev 3.448
Gaus mean : 35.7325
Gaus Width: 3.9895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

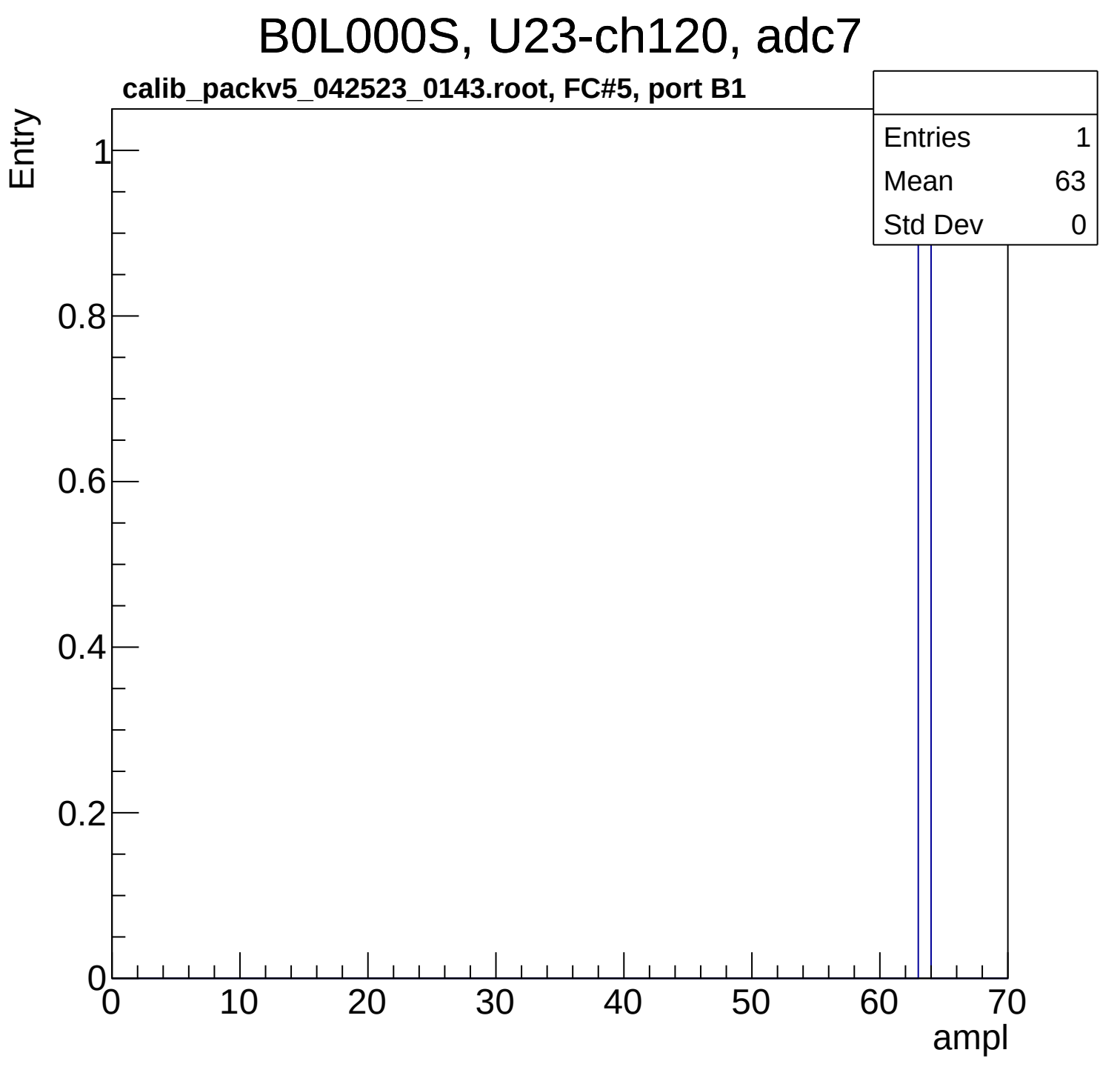

B0L000S, U23-ch120, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 42.56
Std Dev 3.781
Gaus mean : 42.9941
Gaus Width: 3.6860
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

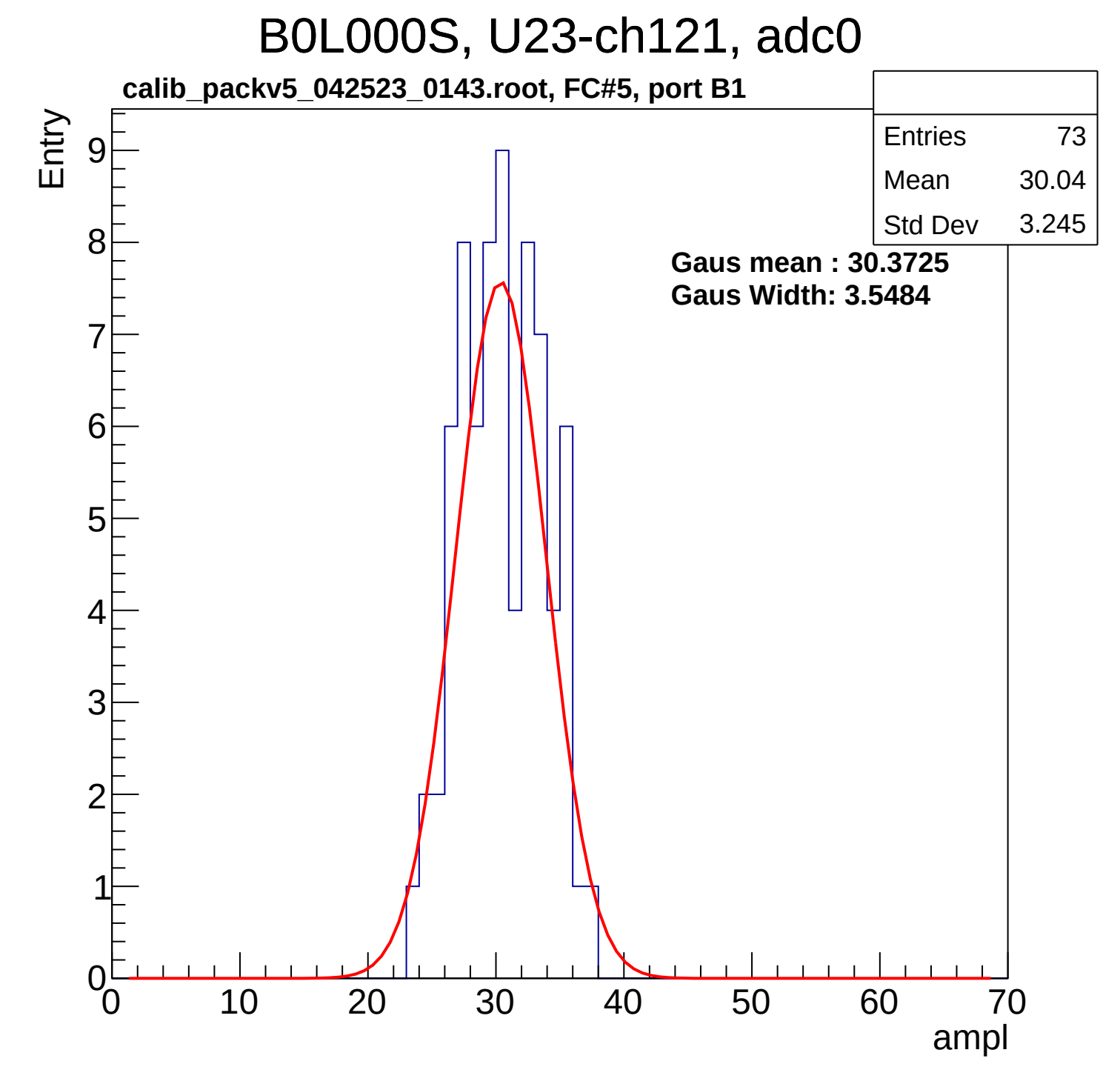

B0L000S, U23-ch120, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 48.27
Std Dev 2.399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

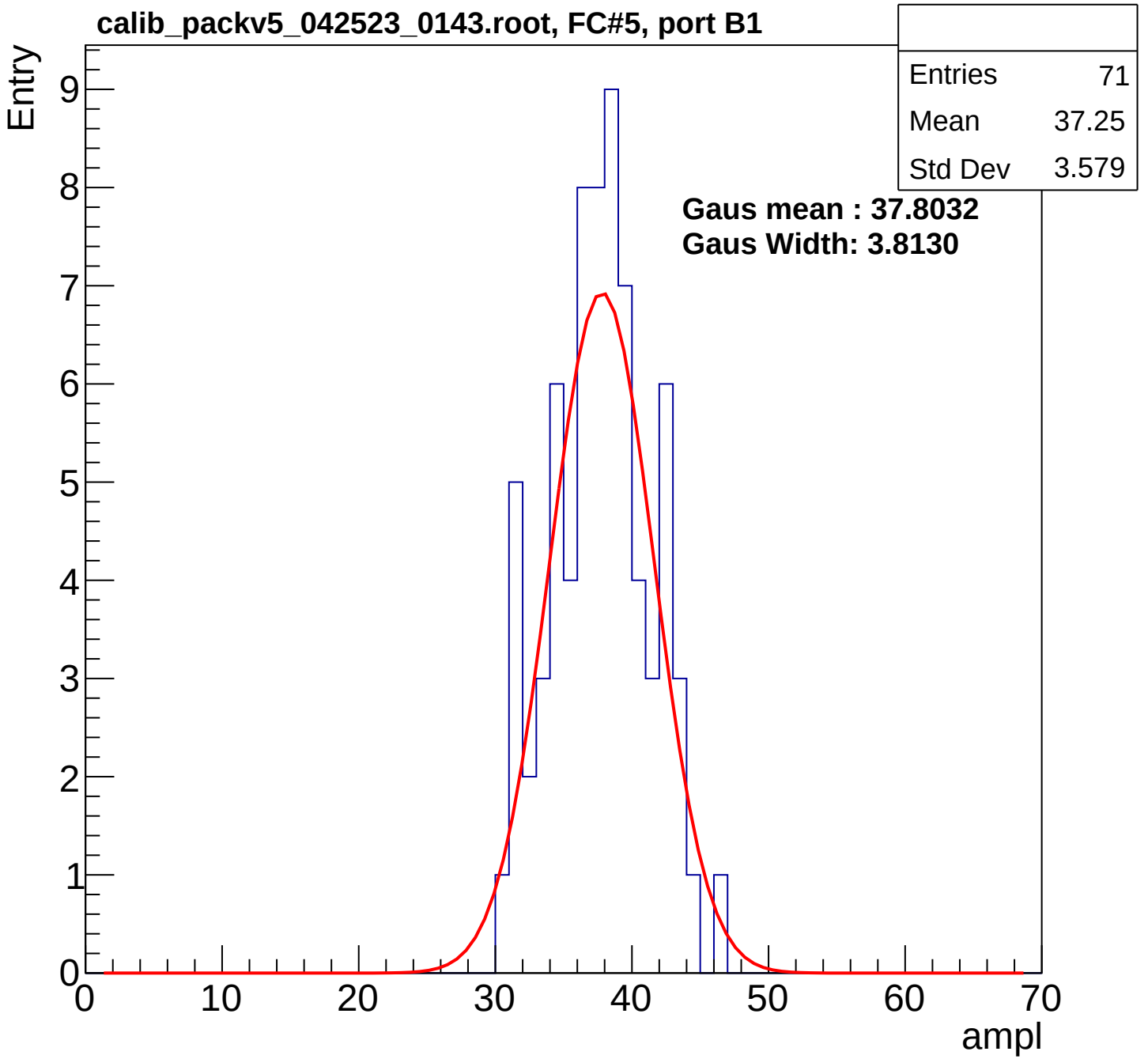

B0L000S, U23-ch120, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 54.03
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

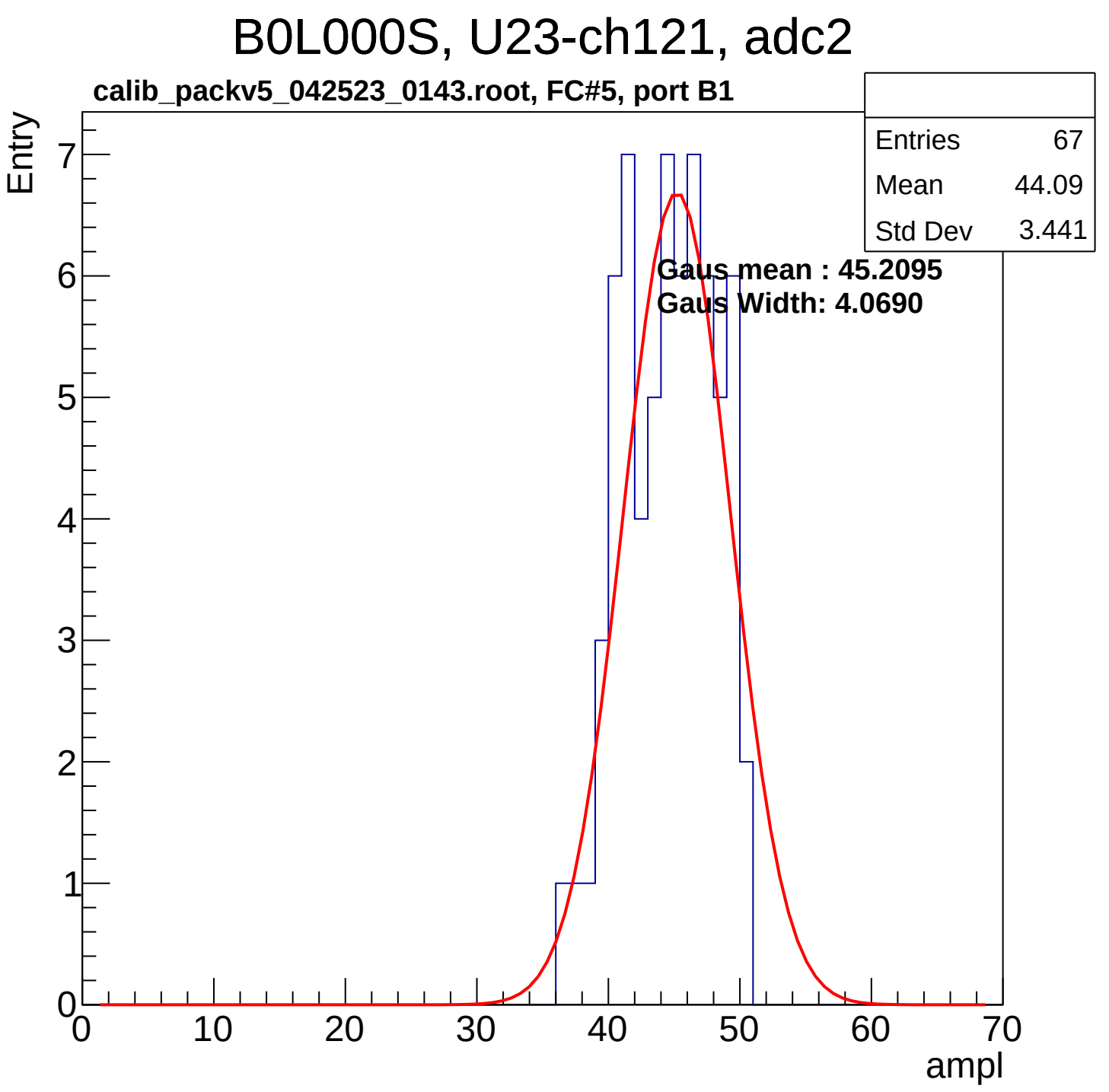

B0L000S, U23-ch120, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 59.47
Std Dev 2.588
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

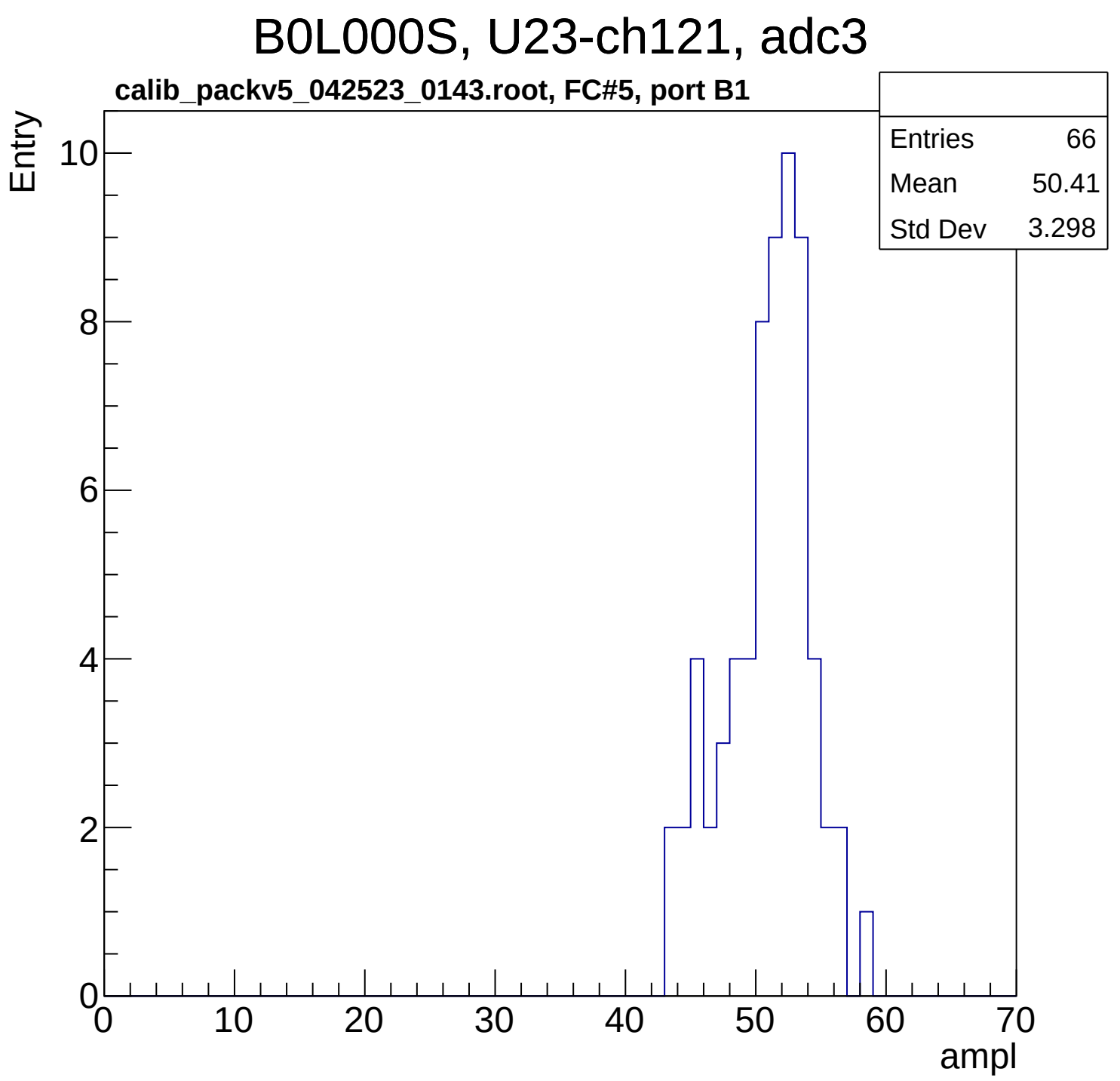

B0L000S, U23-ch120, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 53.12
Std Dev 20.18
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

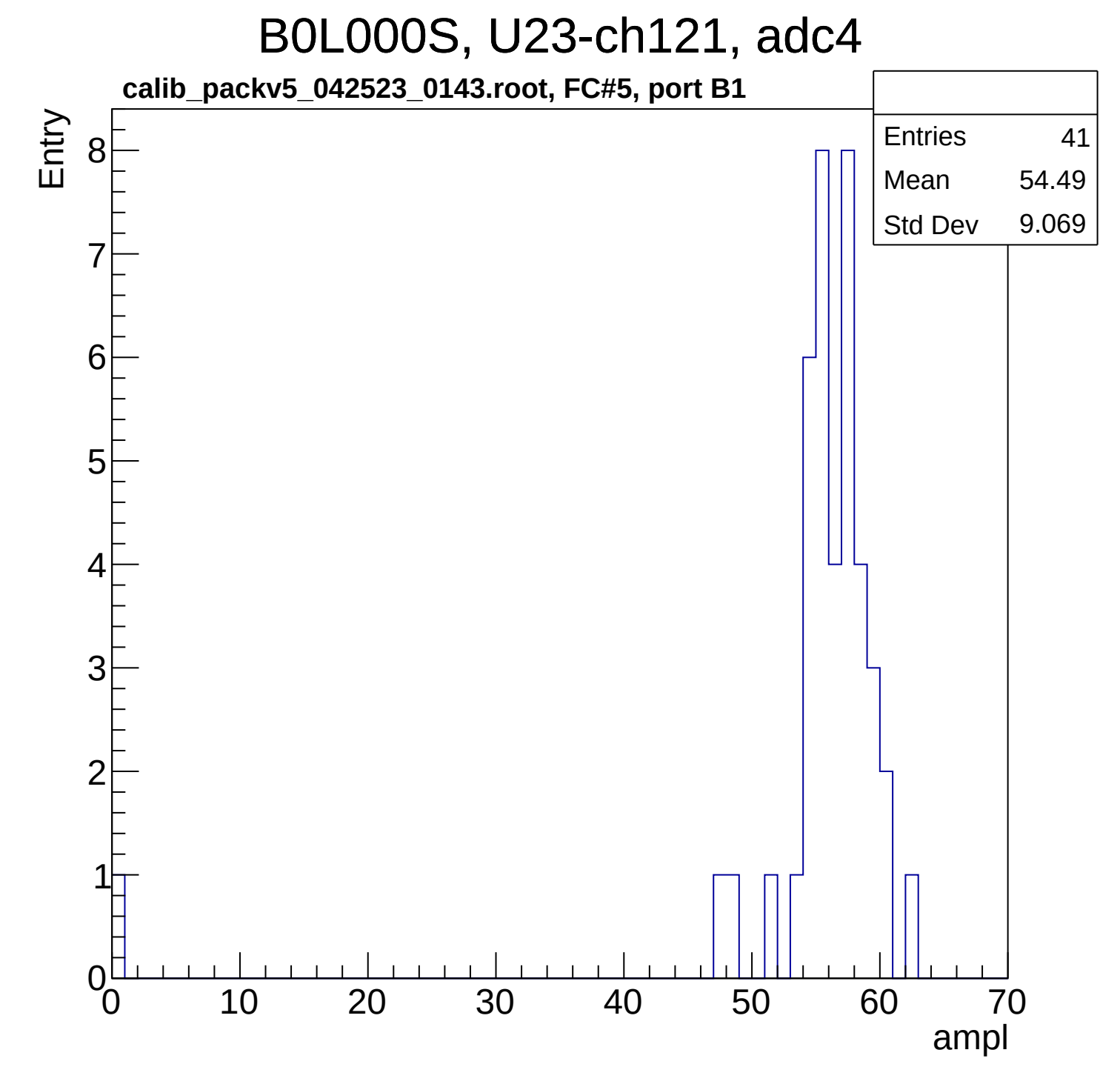

B0L000S, U23-ch120, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

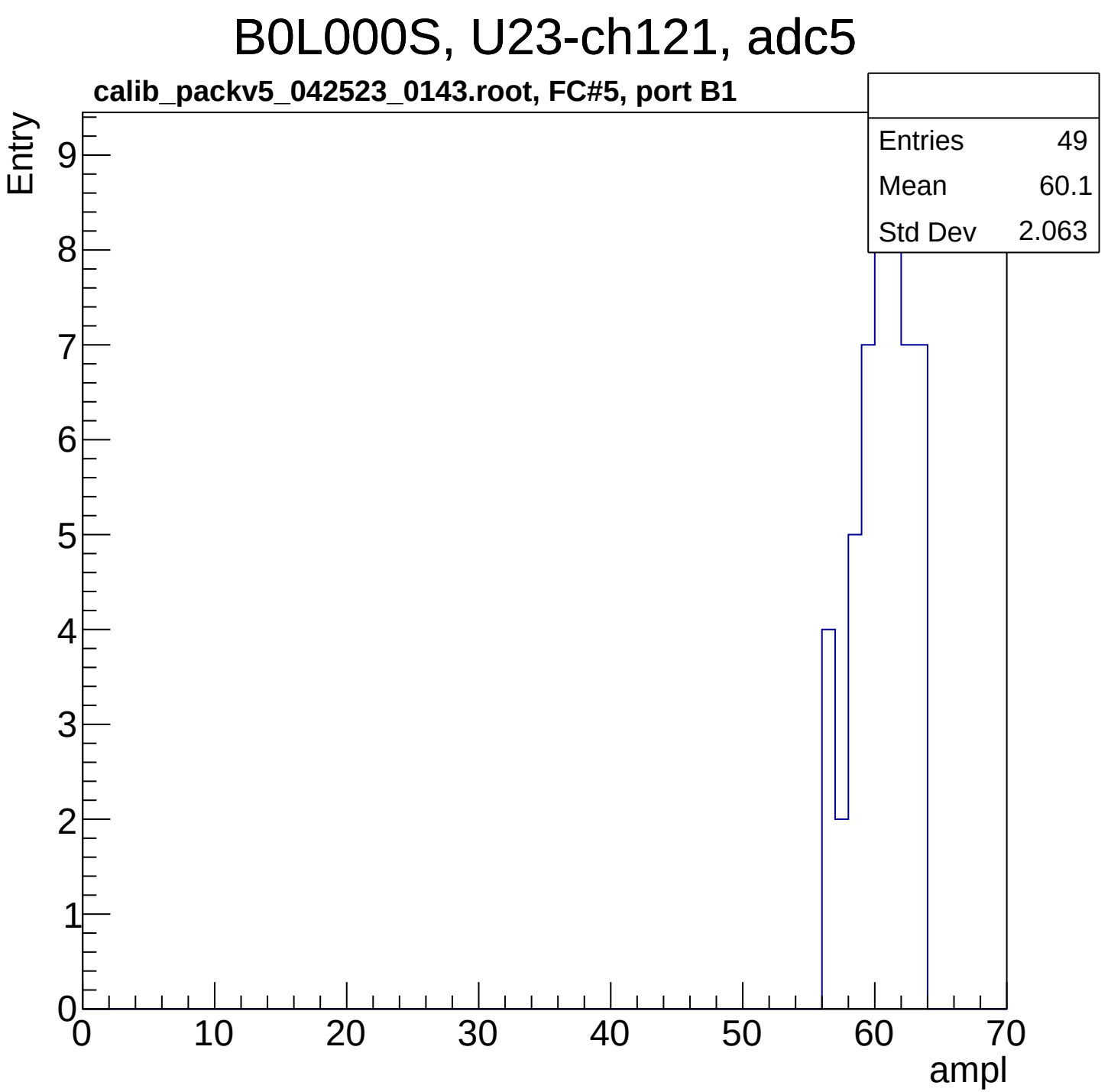

B0L000S, U23-ch121, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.04
Std Dev 3.245
Gaus mean : 30.3725
Gaus Width: 3.5484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

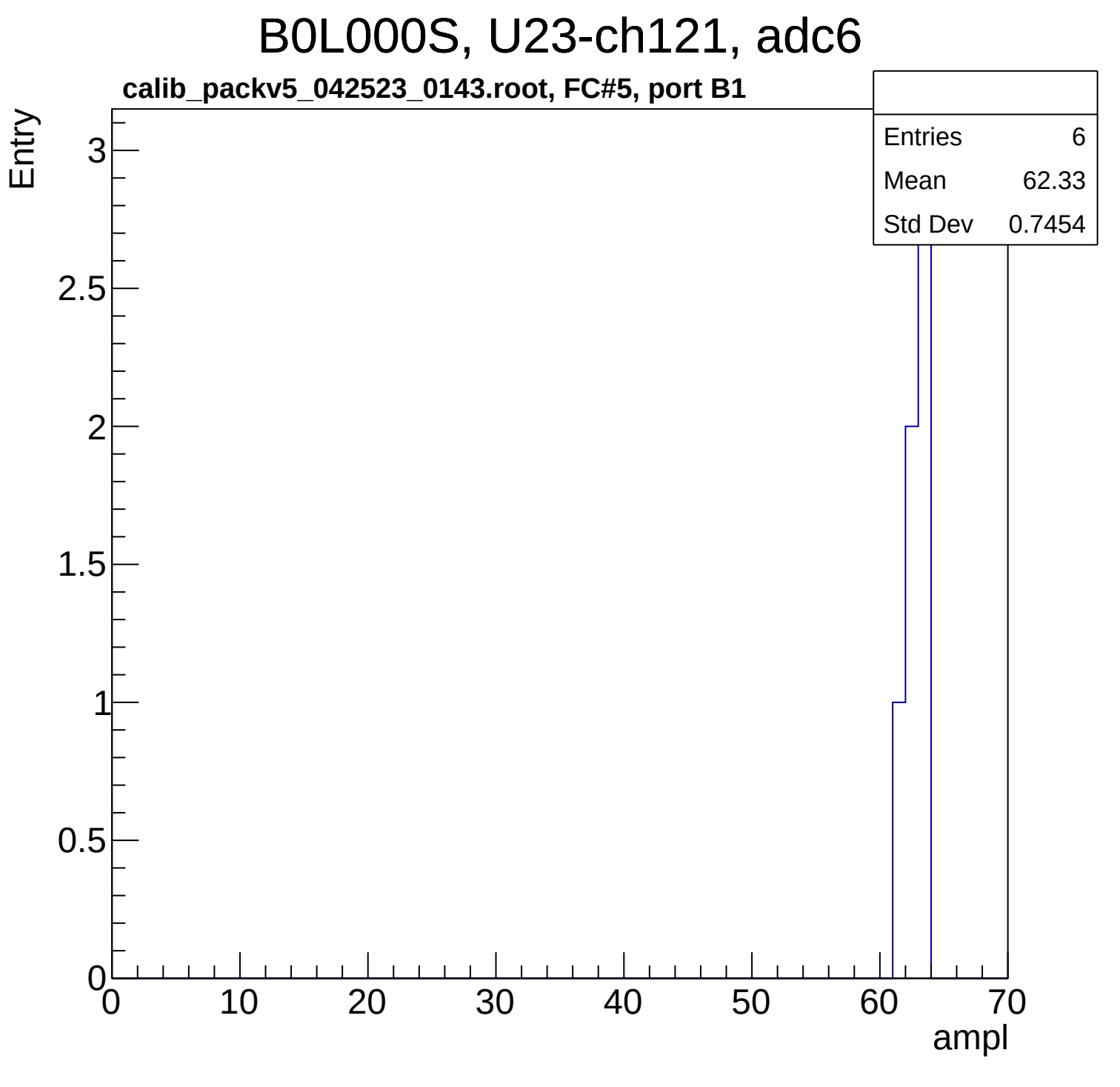

B0L000S, U23-ch121, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 37.25
Std Dev 3.579
Gaus mean : 37.8032
Gaus Width: 3.8130
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

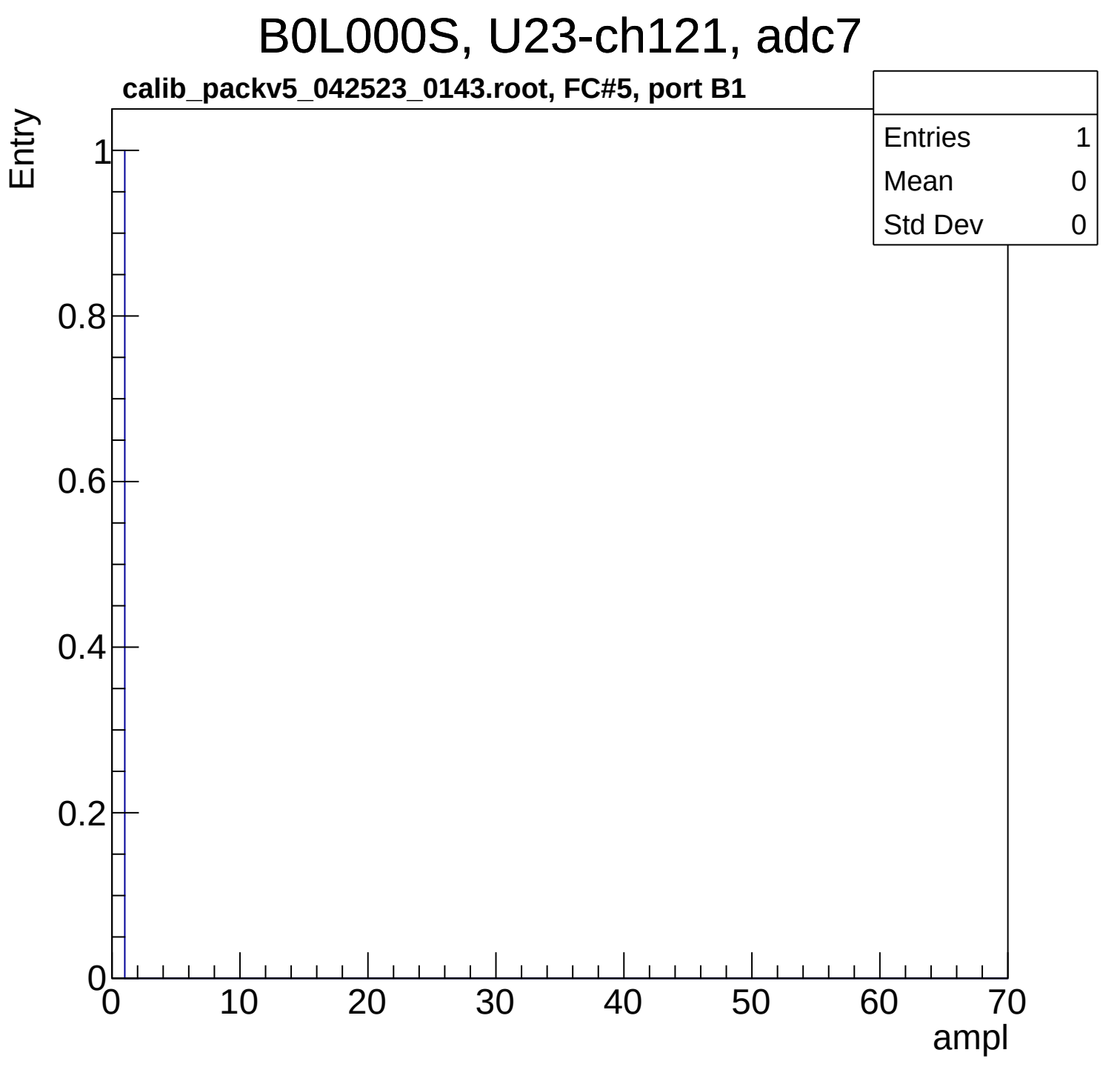

B0L000S, U23-ch121, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.09
Std Dev 3.441
Gaus mean : 45.2095
Gaus Width: 4.0690
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

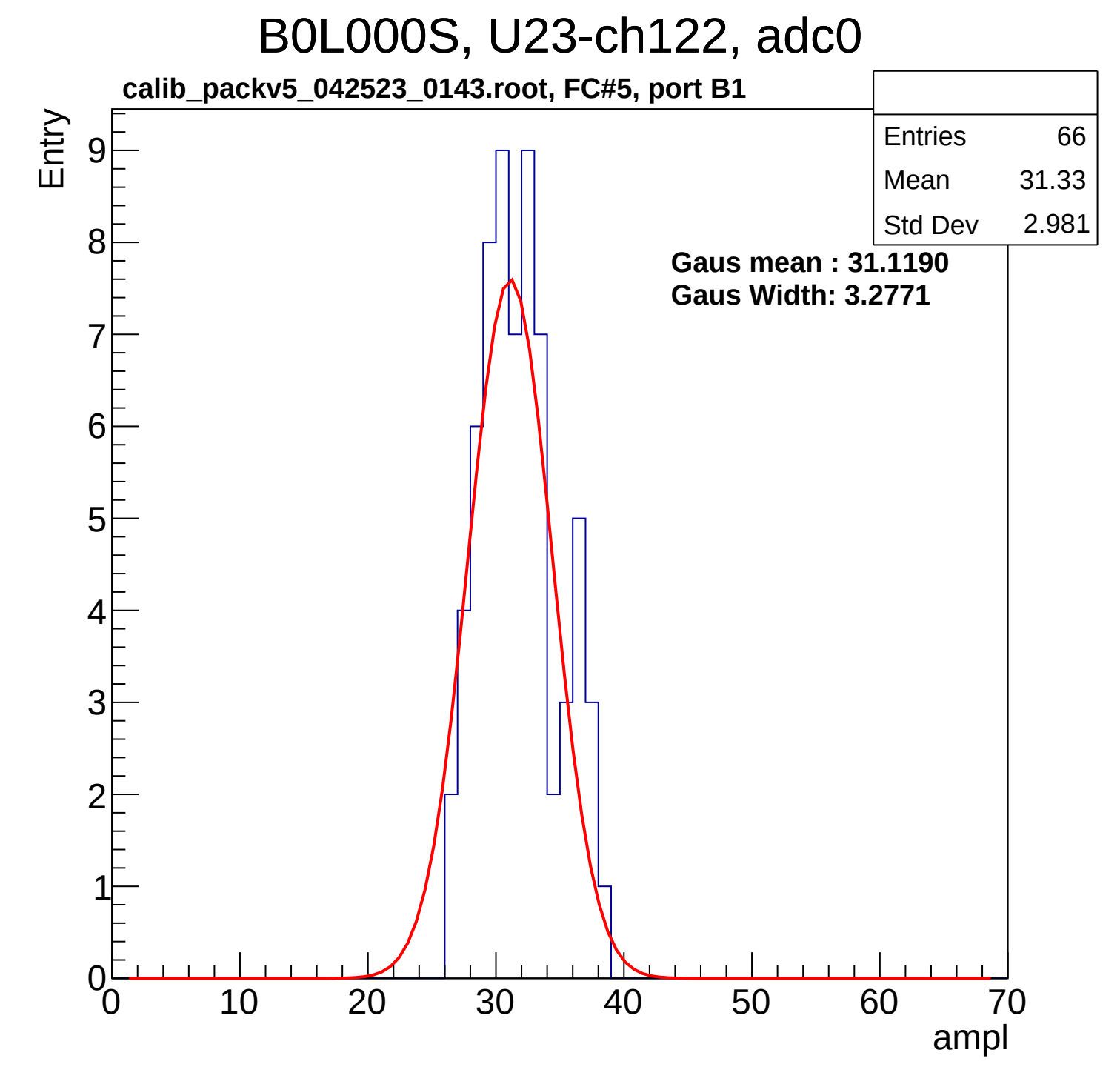

B0L000S, U23-ch121, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.41
Std Dev 3.298
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

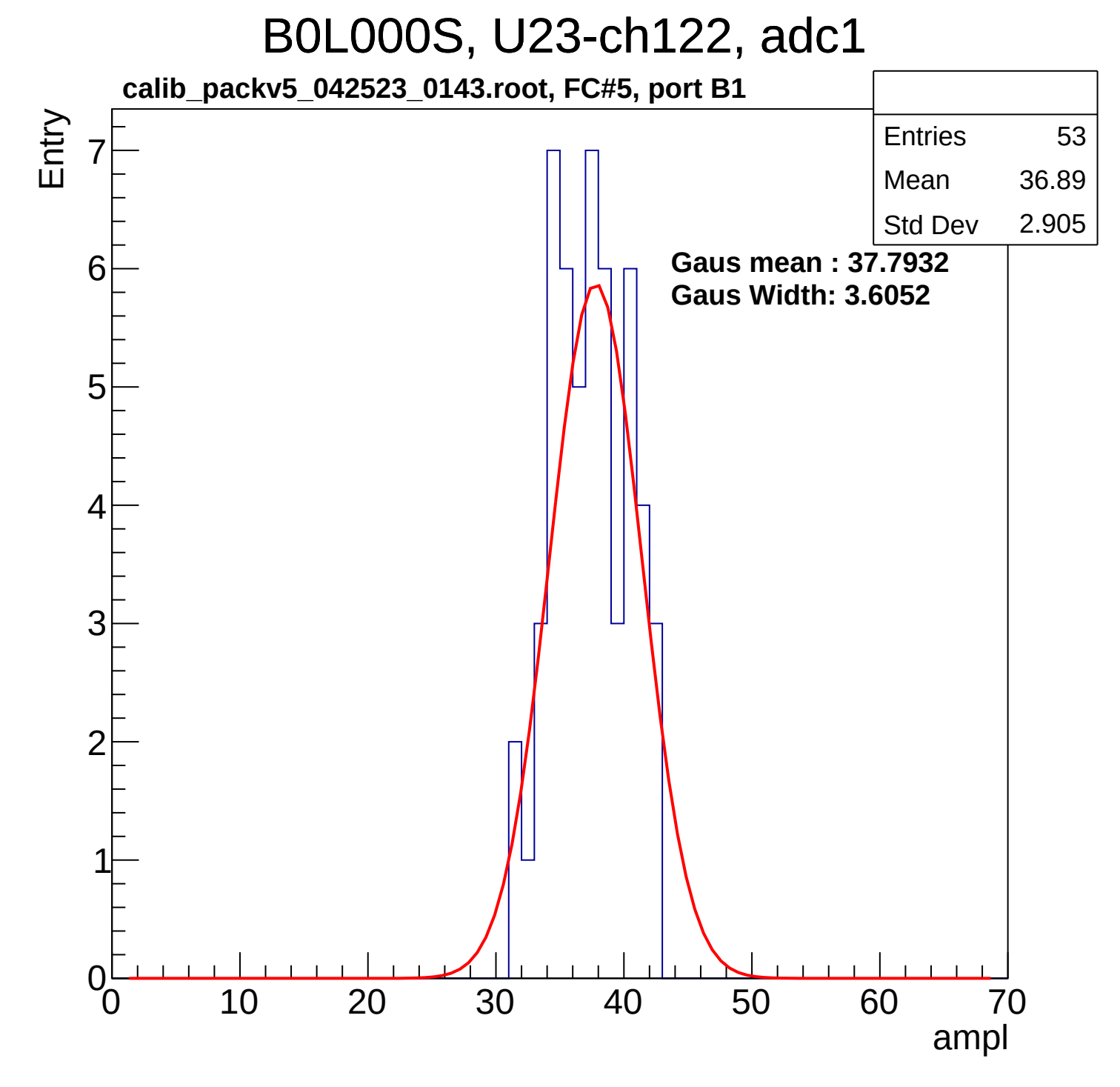

B0L000S, U23-ch121, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 54.49
Std Dev 9.069
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

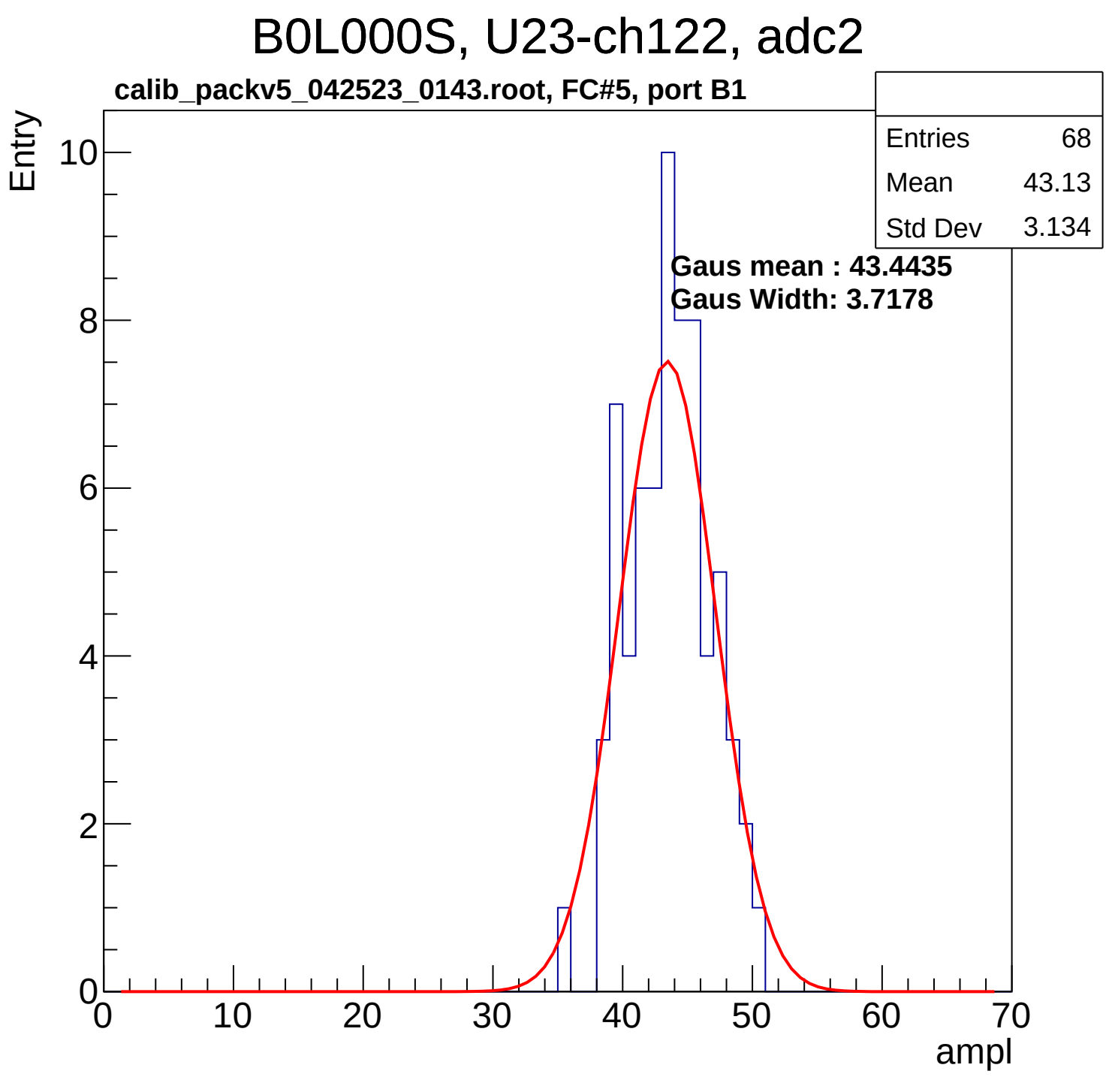

B0L000S, U23-ch121, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 60.1
Std Dev 2.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

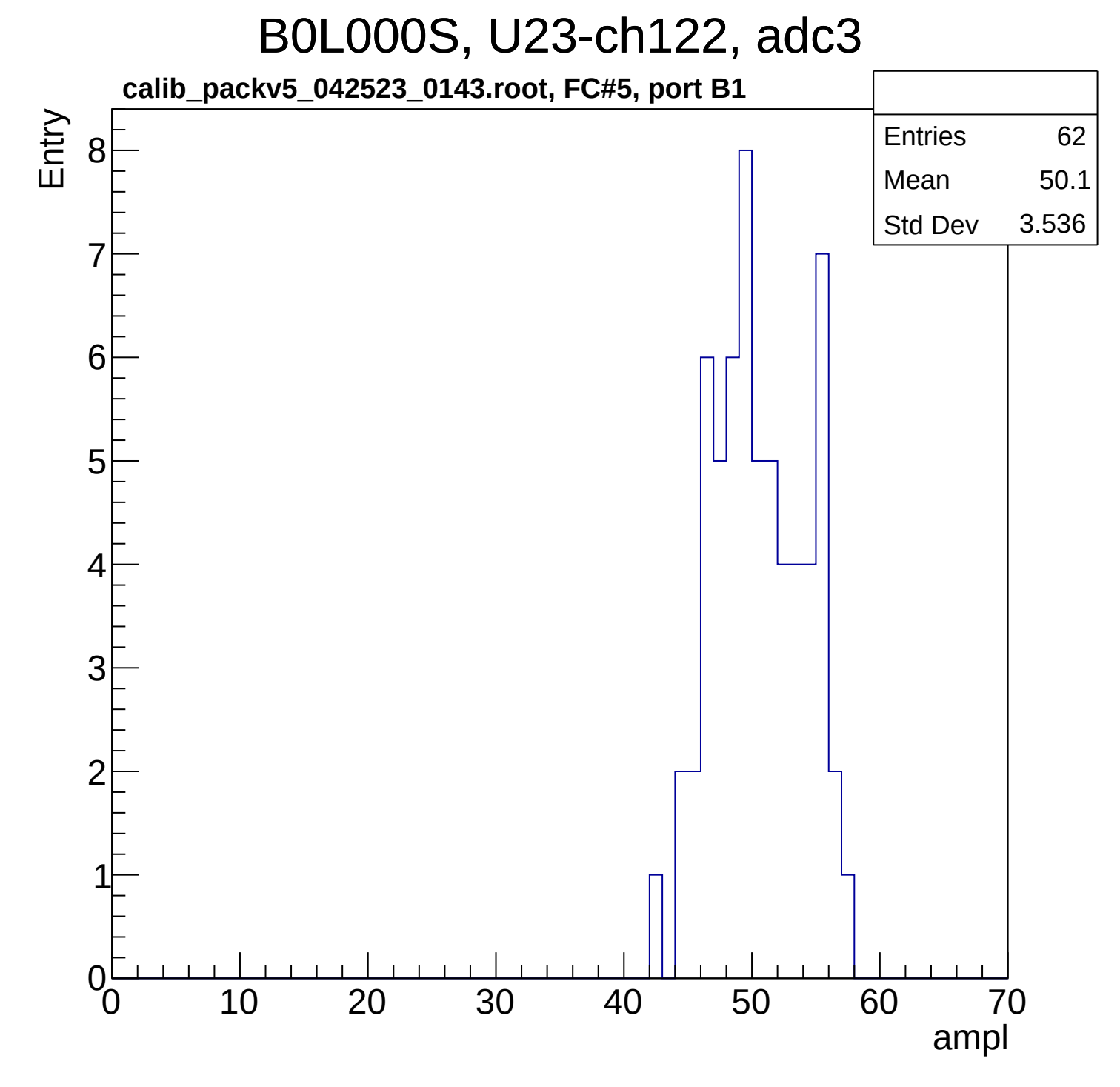

B0L000S, U23-ch121, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

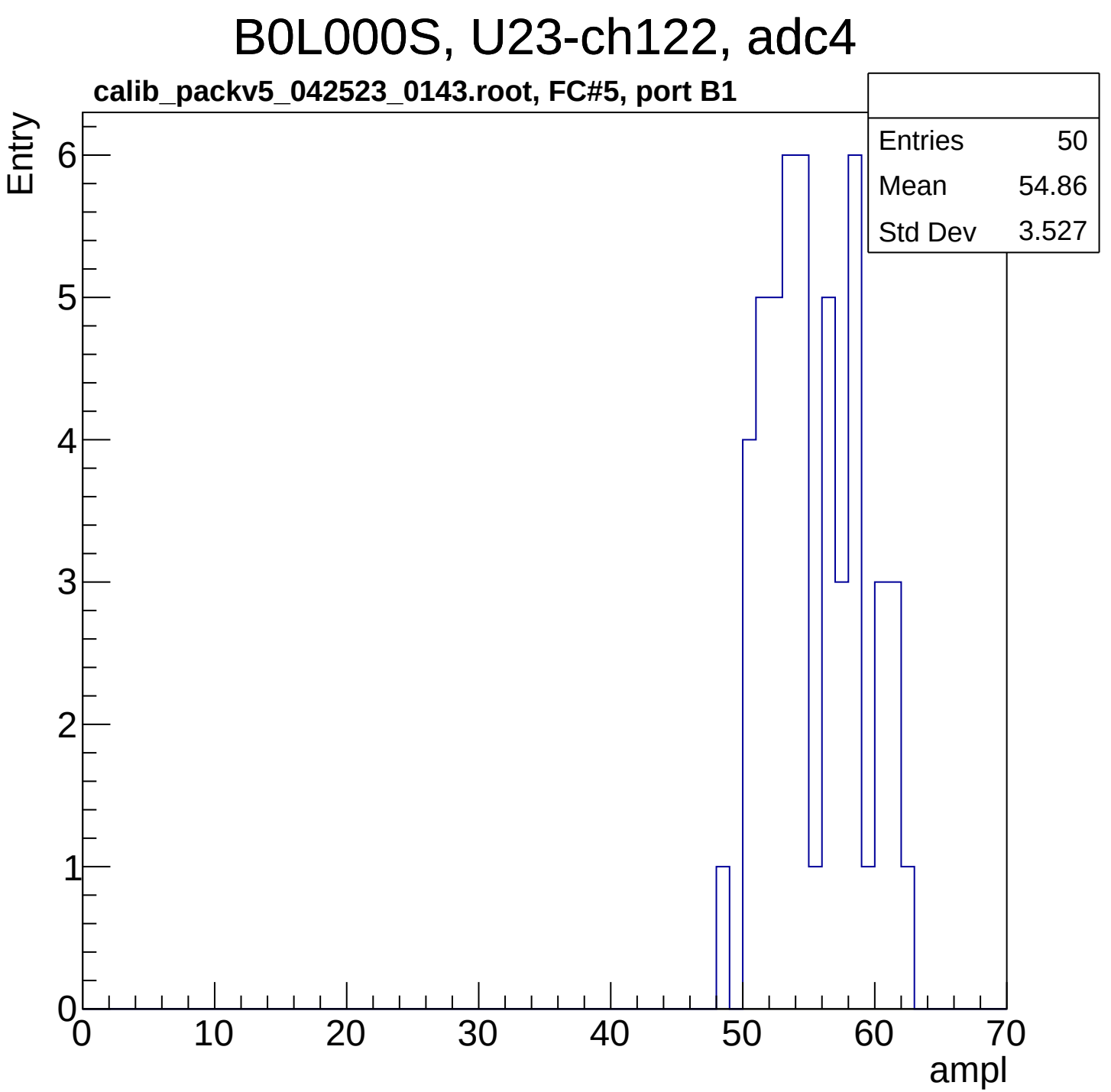

B0L000S, U23-ch121, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

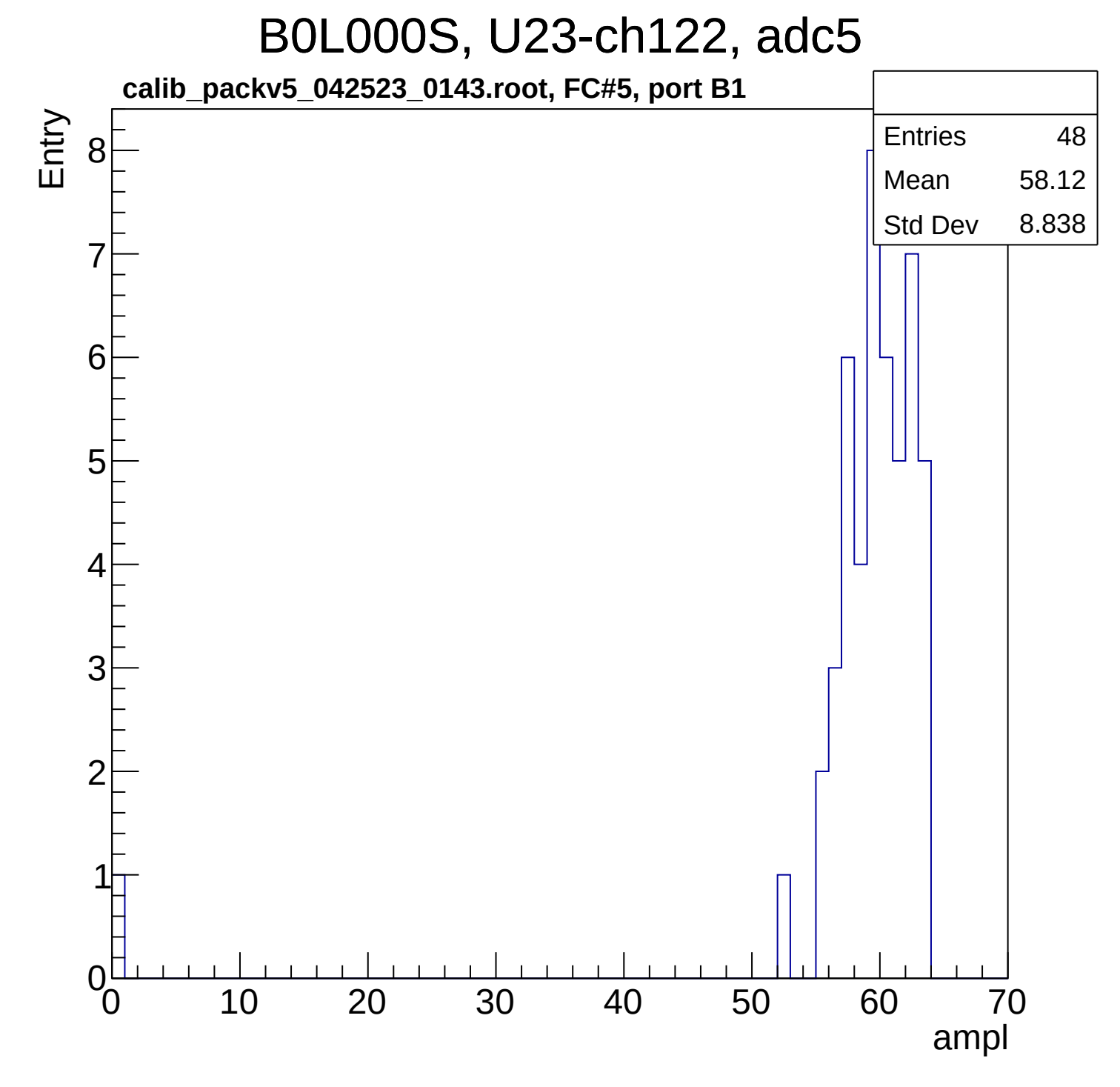

B0L000S, U23-ch122, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 31.33
Std Dev 2.981
Gaus mean : 31.1190
Gaus Width: 3.2771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

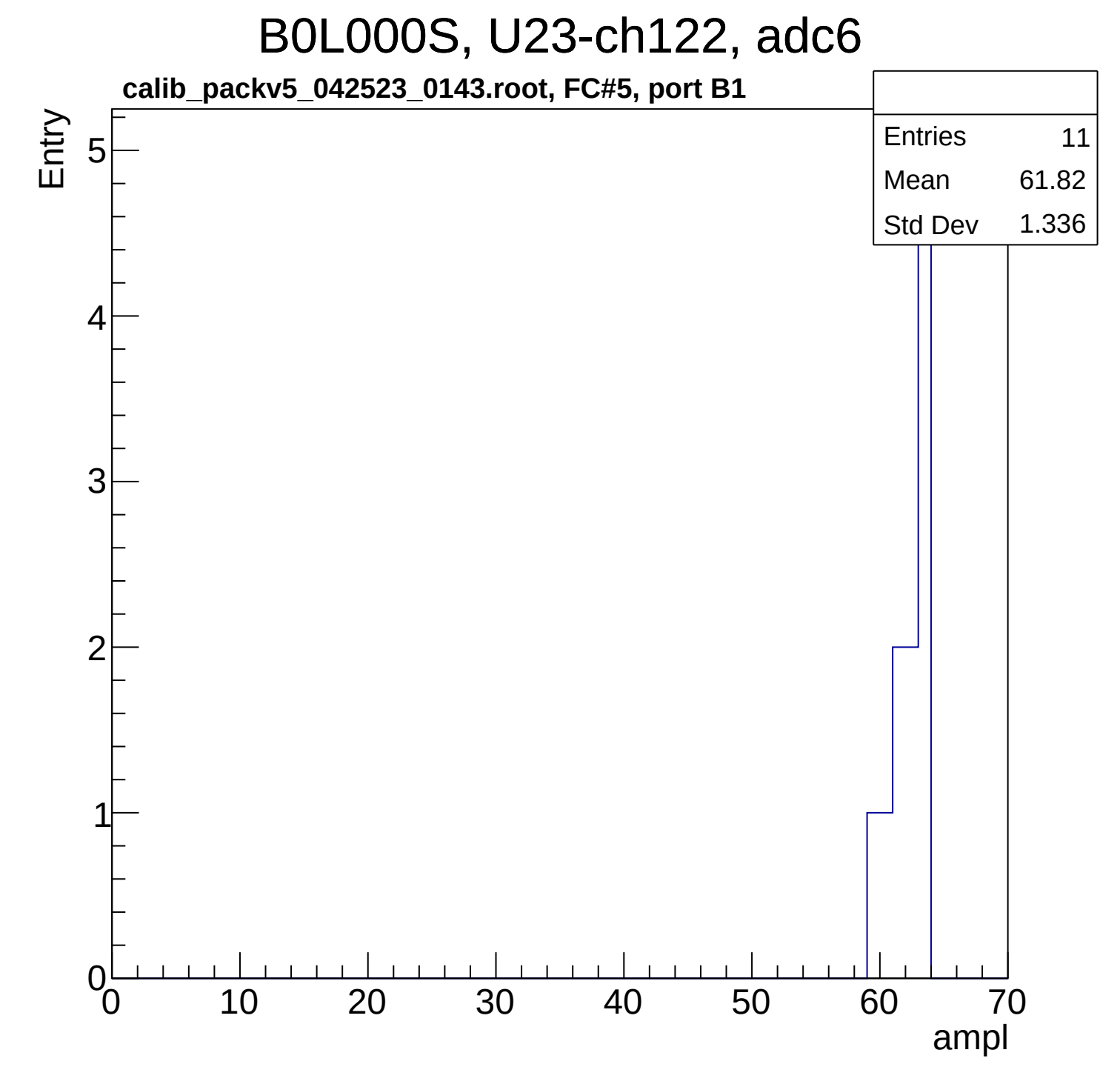

B0L000S, U23-ch122, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 36.89
Std Dev 2.905
Gaus mean : 37.7932
Gaus Width: 3.6052
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

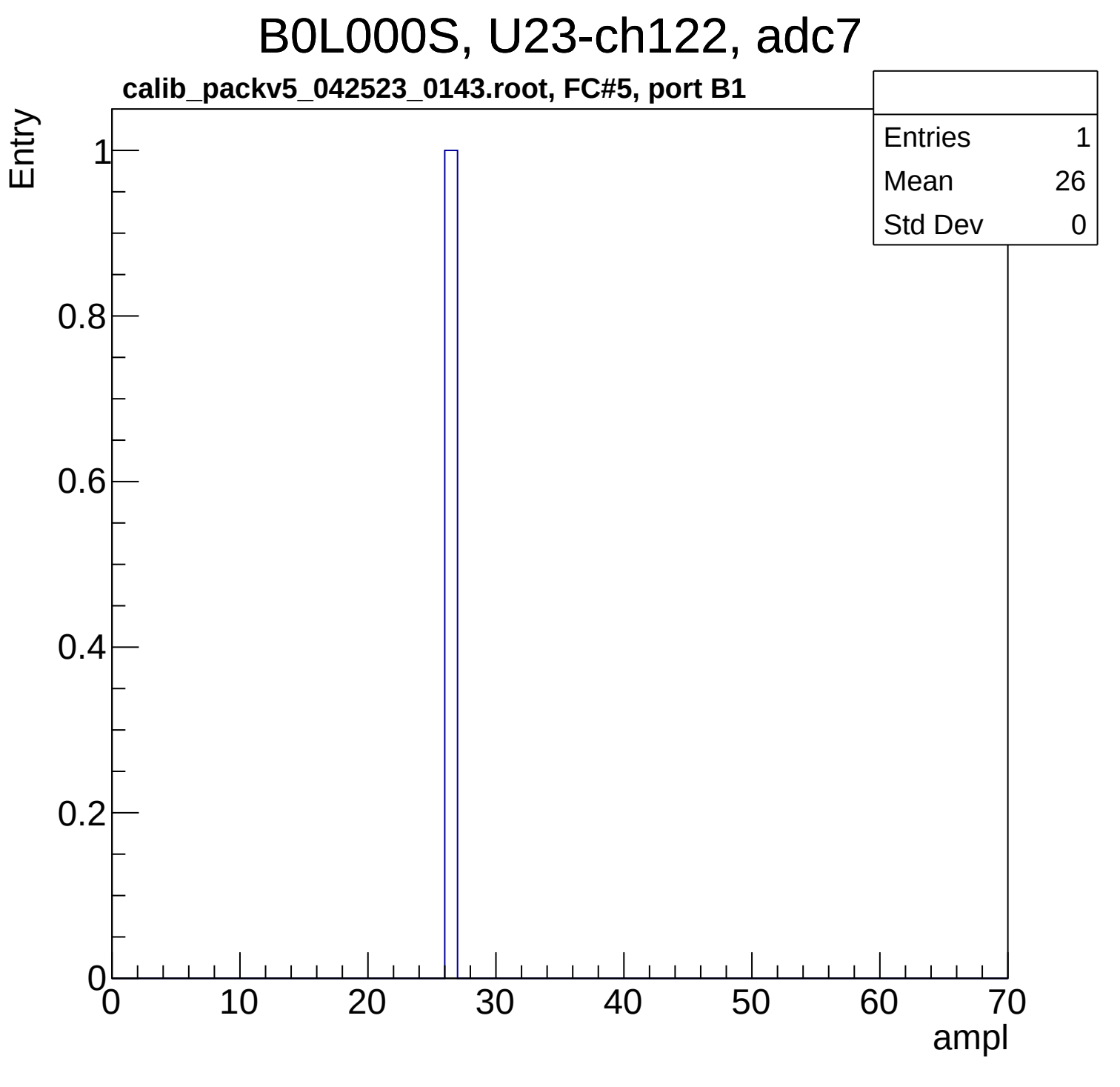

B0L000S, U23-ch122, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 43.13
Std Dev 3.134
Gaus mean : 43.4435
Gaus Width: 3.7178
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

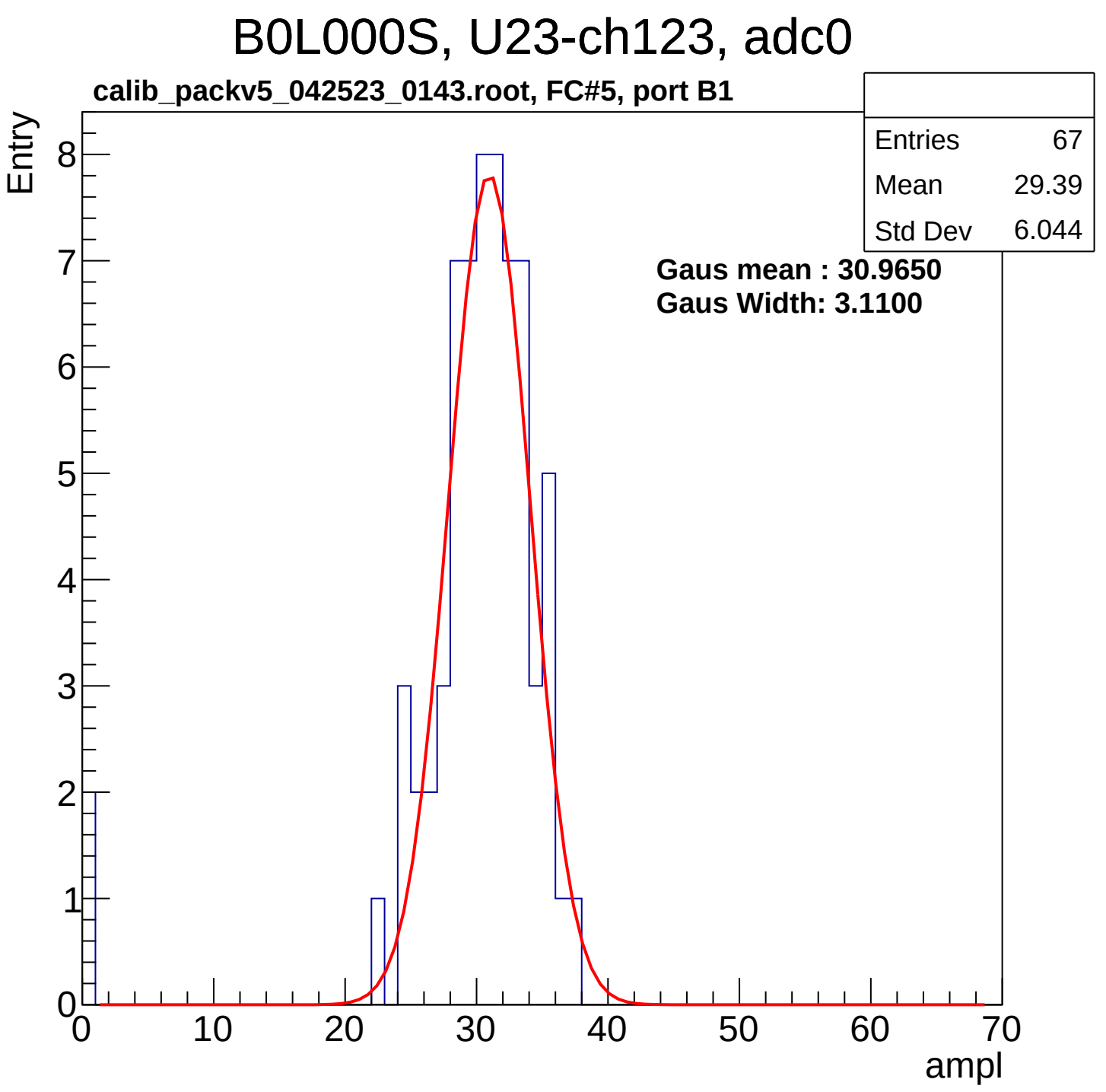

B0L000S, U23-ch122, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.1
Std Dev 3.536
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

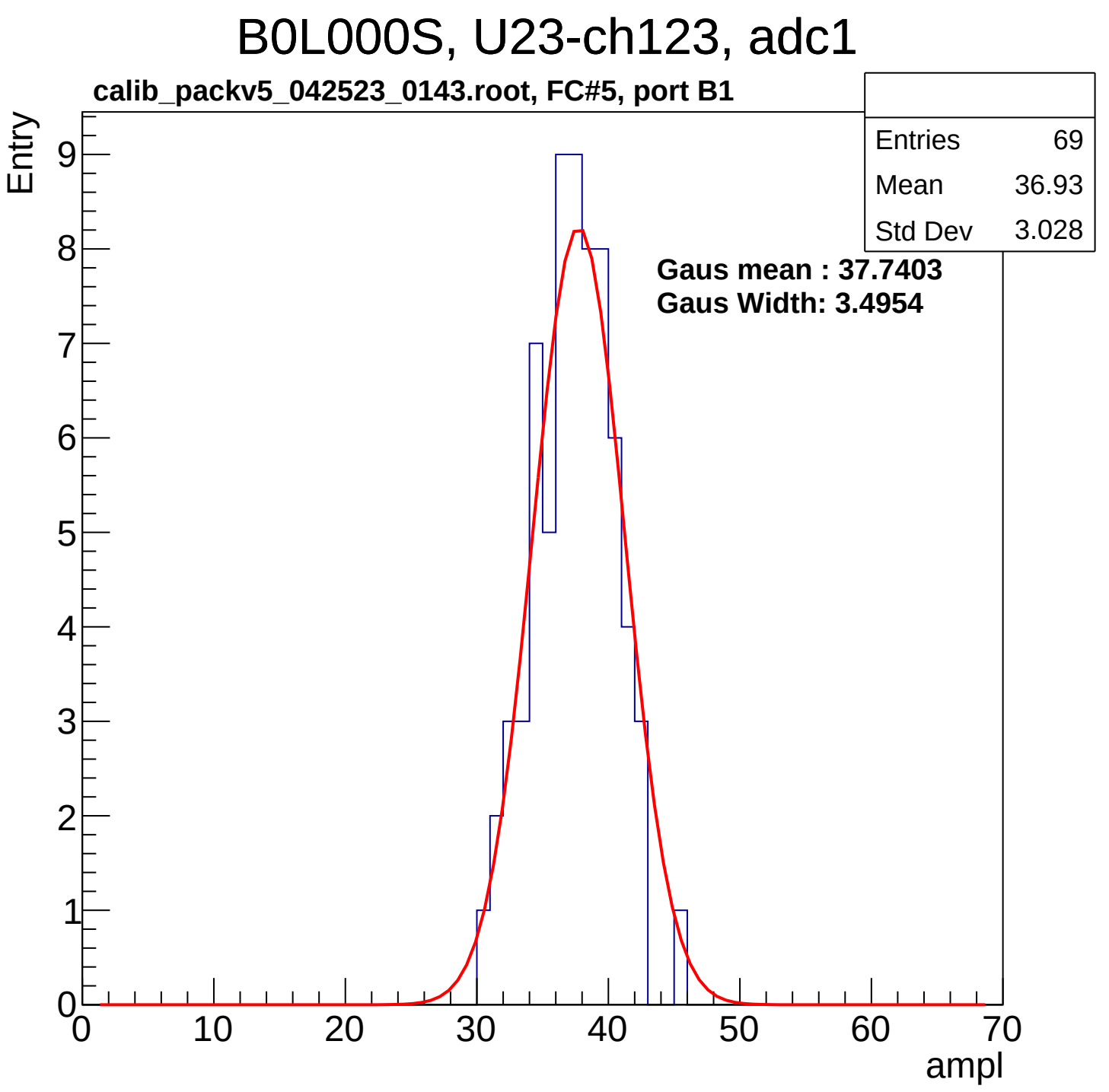

B0L000S, U23-ch122, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54.86
Std Dev 3.527
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

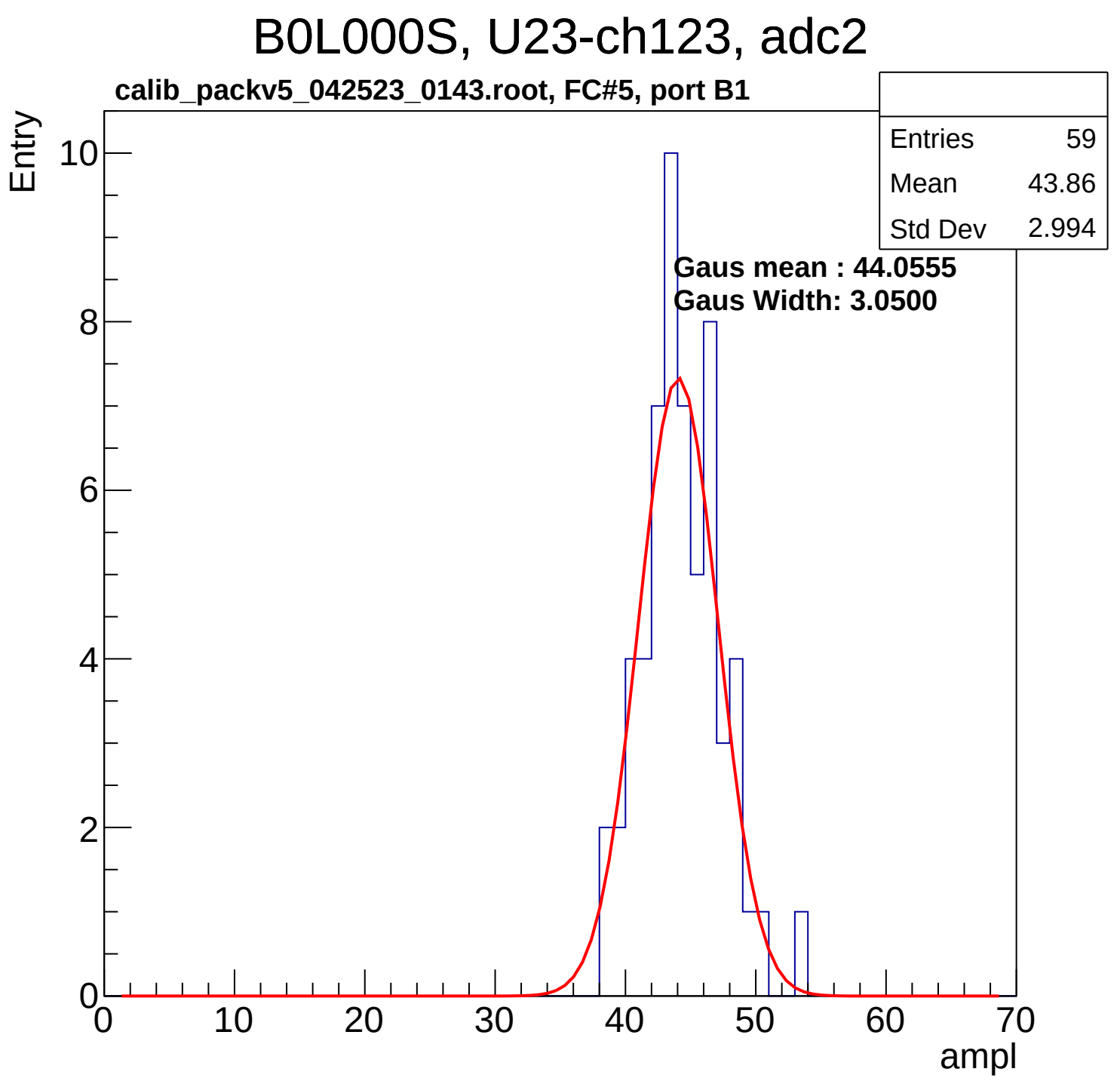

B0L000S, U23-ch122, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 58.12
Std Dev 8.838
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

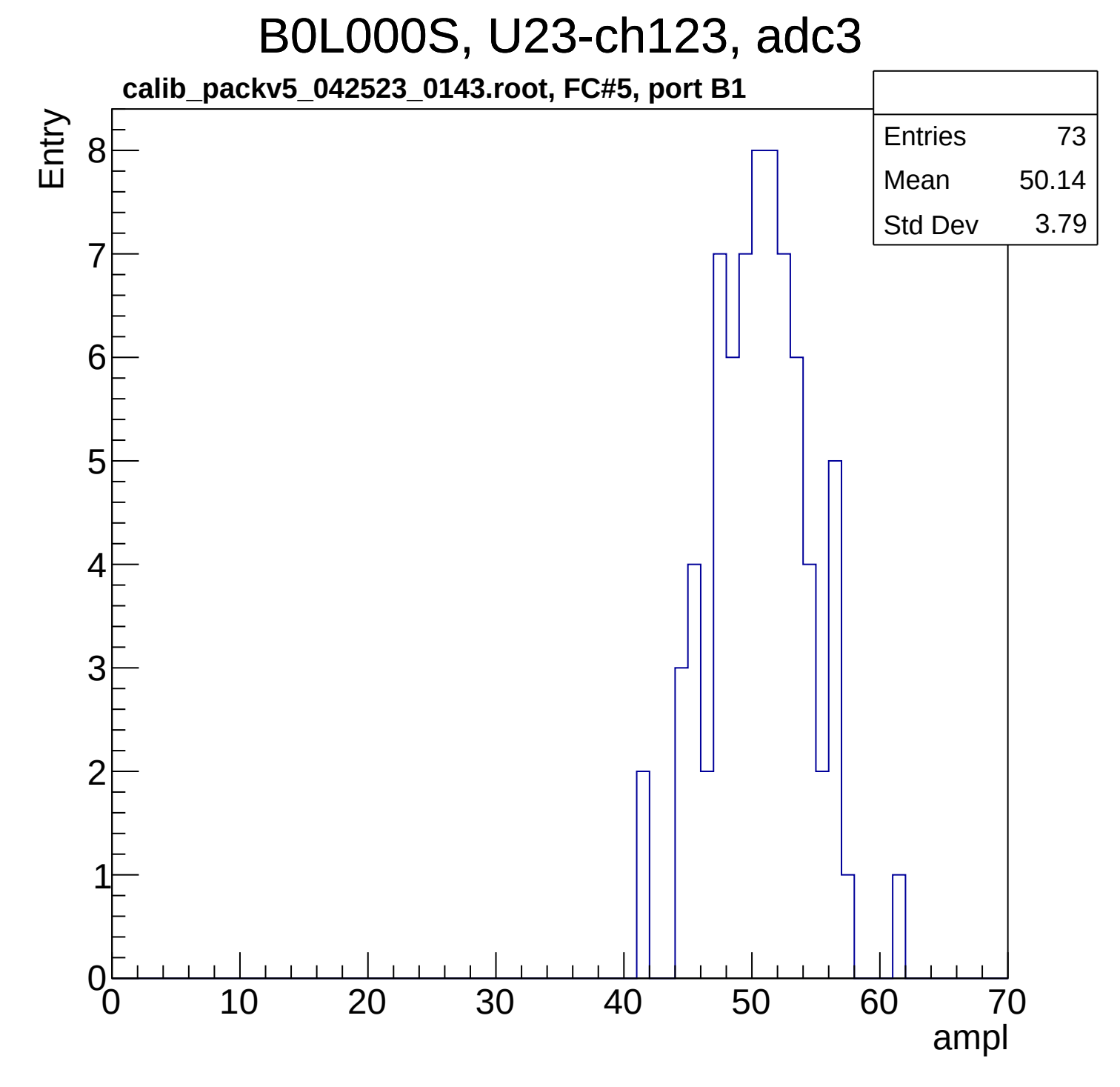

B0L000S, U23-ch122, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.82
Std Dev 1.336
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

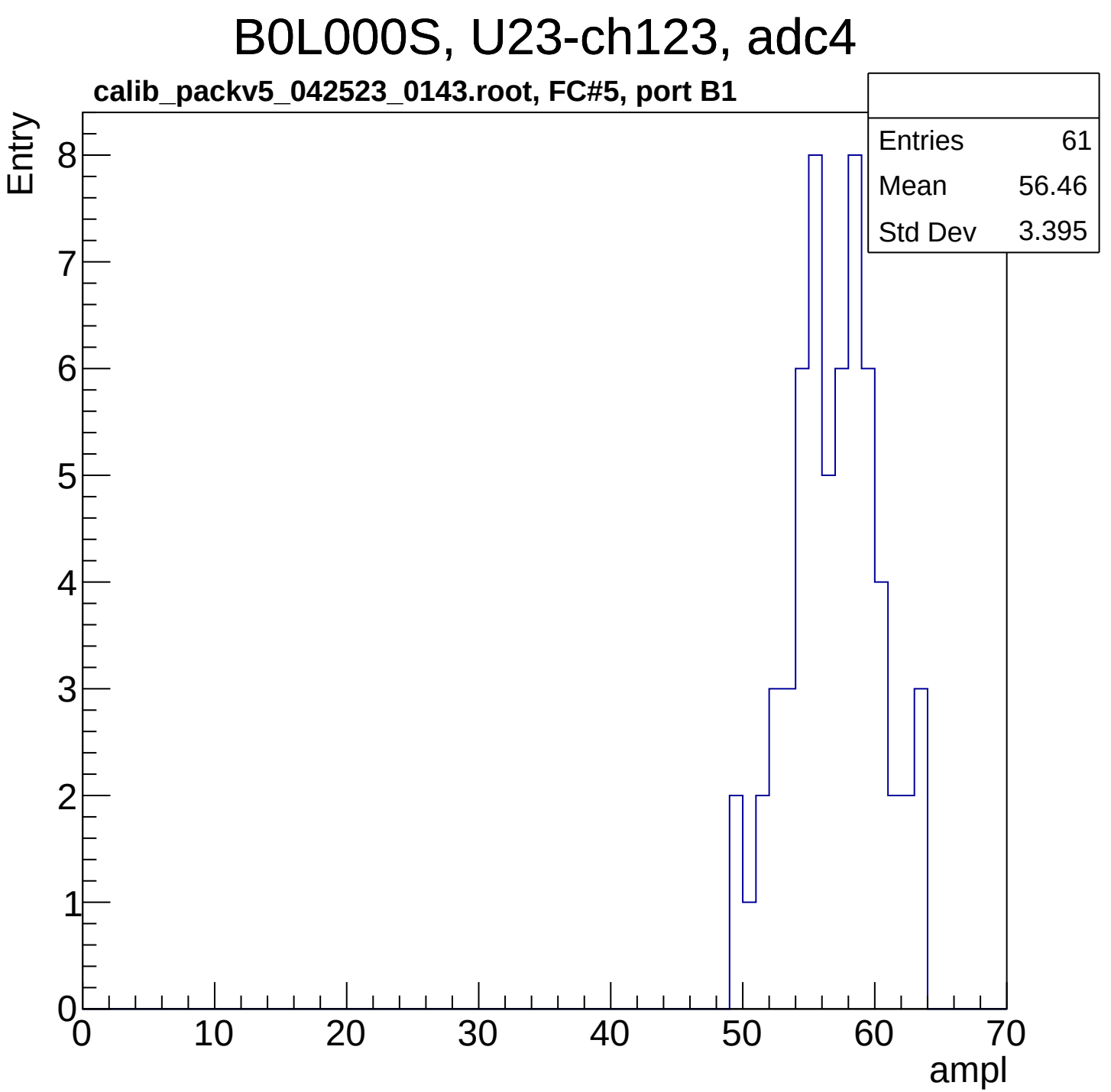

B0L000S, U23-ch122, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 26
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

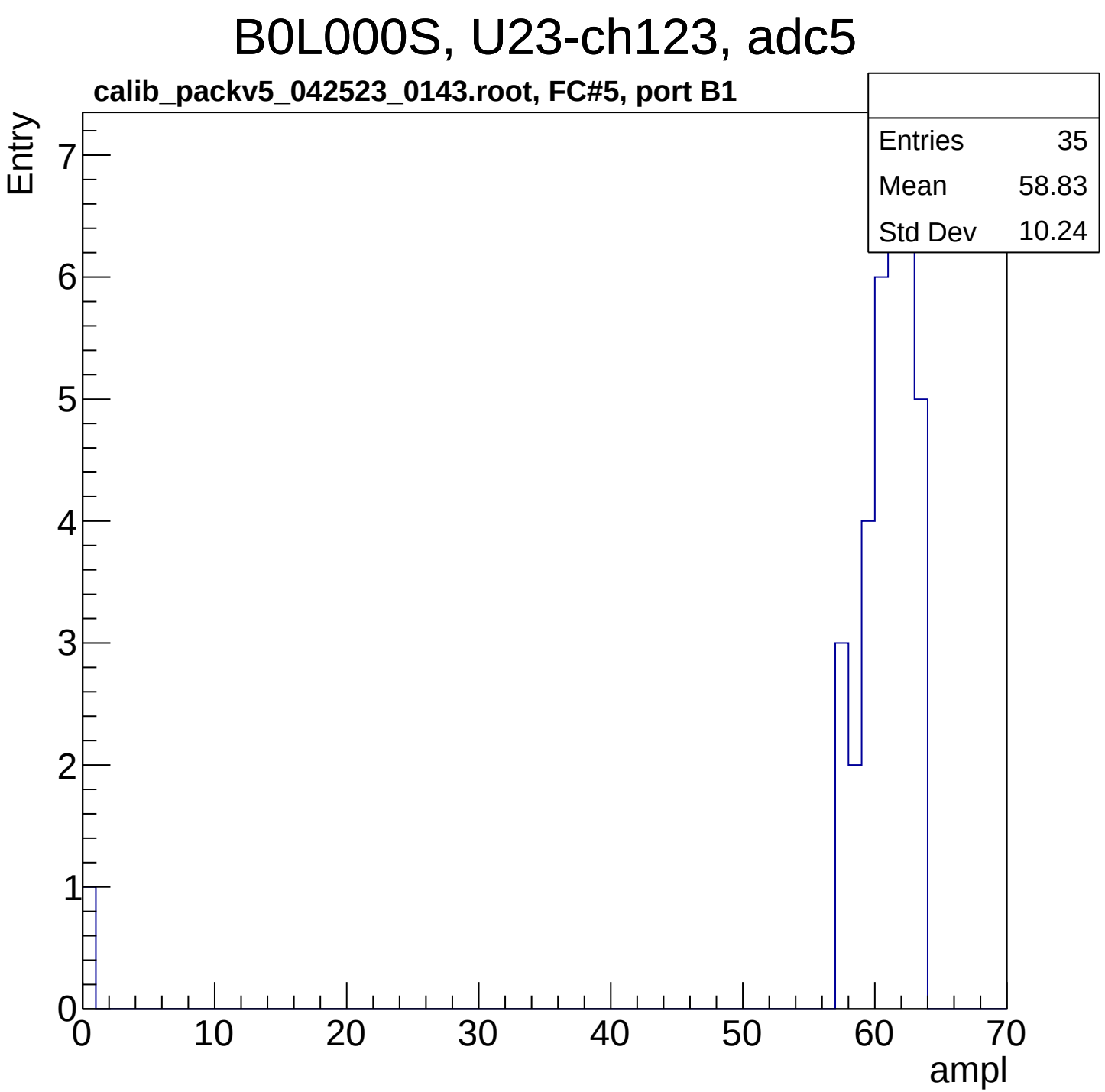

B0L000S, U23-ch123, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 29.39
Std Dev 6.044
Gaus mean : 30.9650
Gaus Width: 3.1100
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

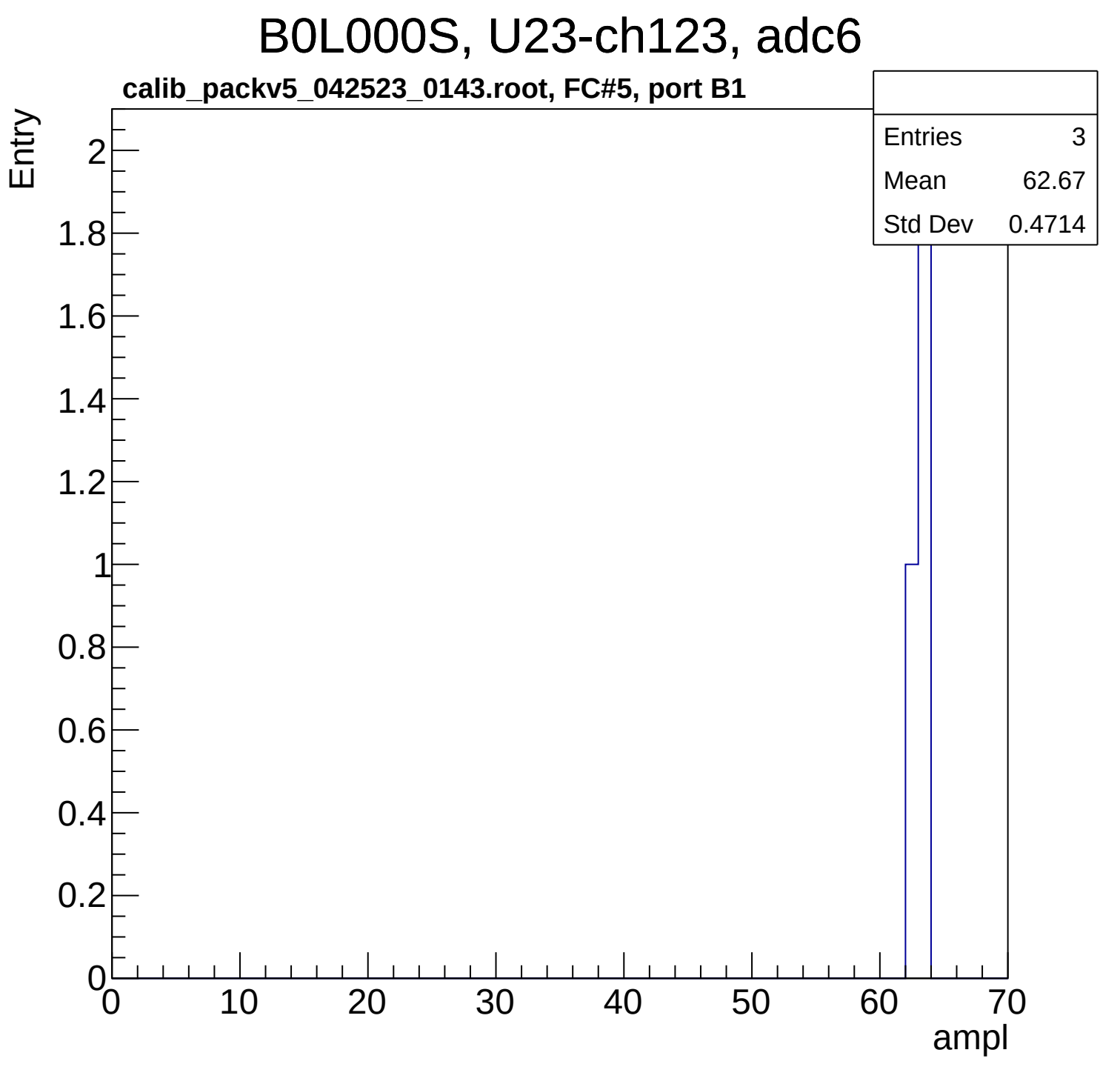

B0L000S, U23-ch123, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.93
Std Dev 3.028
Gaus mean : 37.7403
Gaus Width: 3.4954
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

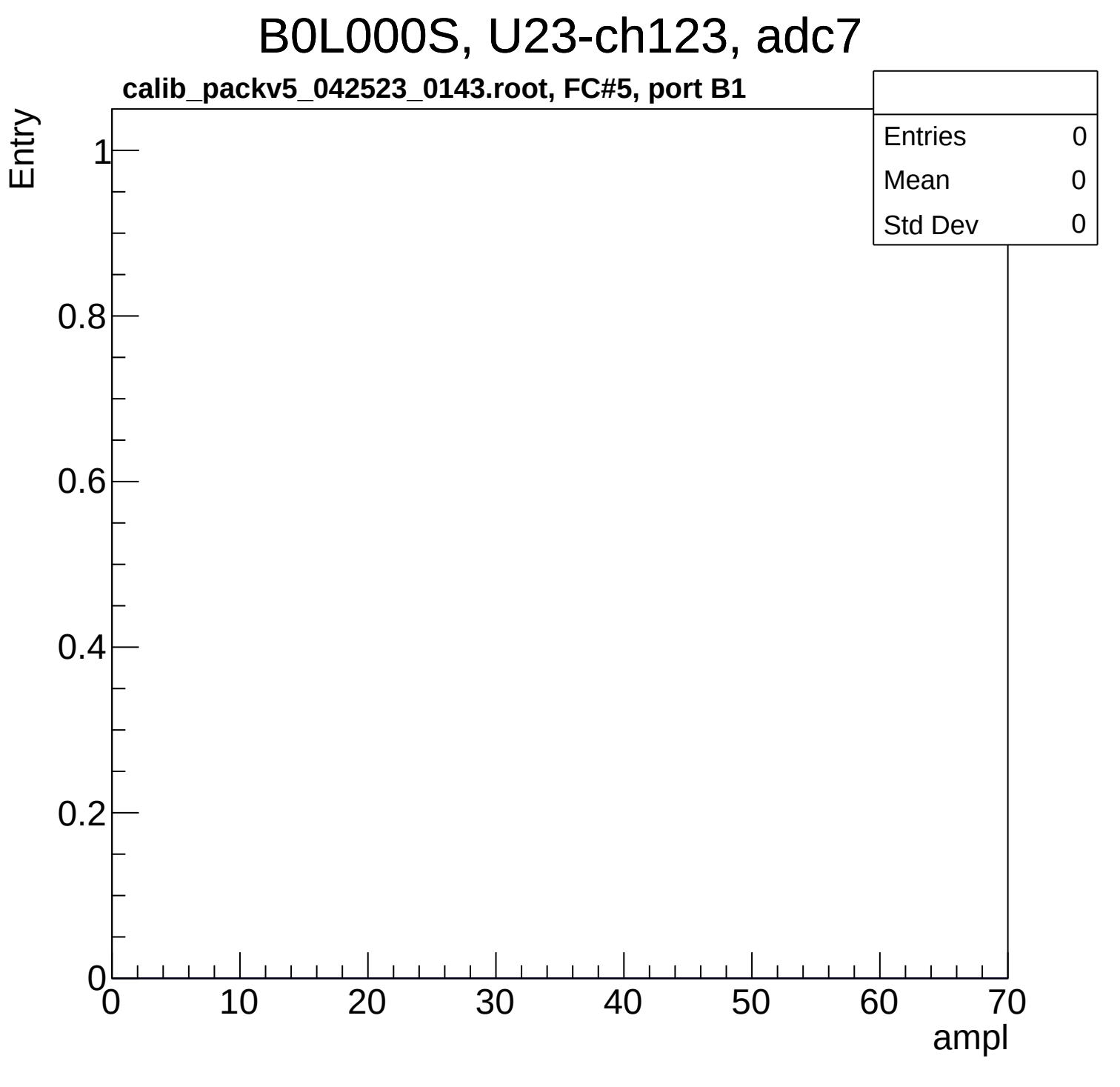

B0L000S, U23-ch123, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.86
Std Dev 2.994
Gaus mean : 44.0555
Gaus Width: 3.0500
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

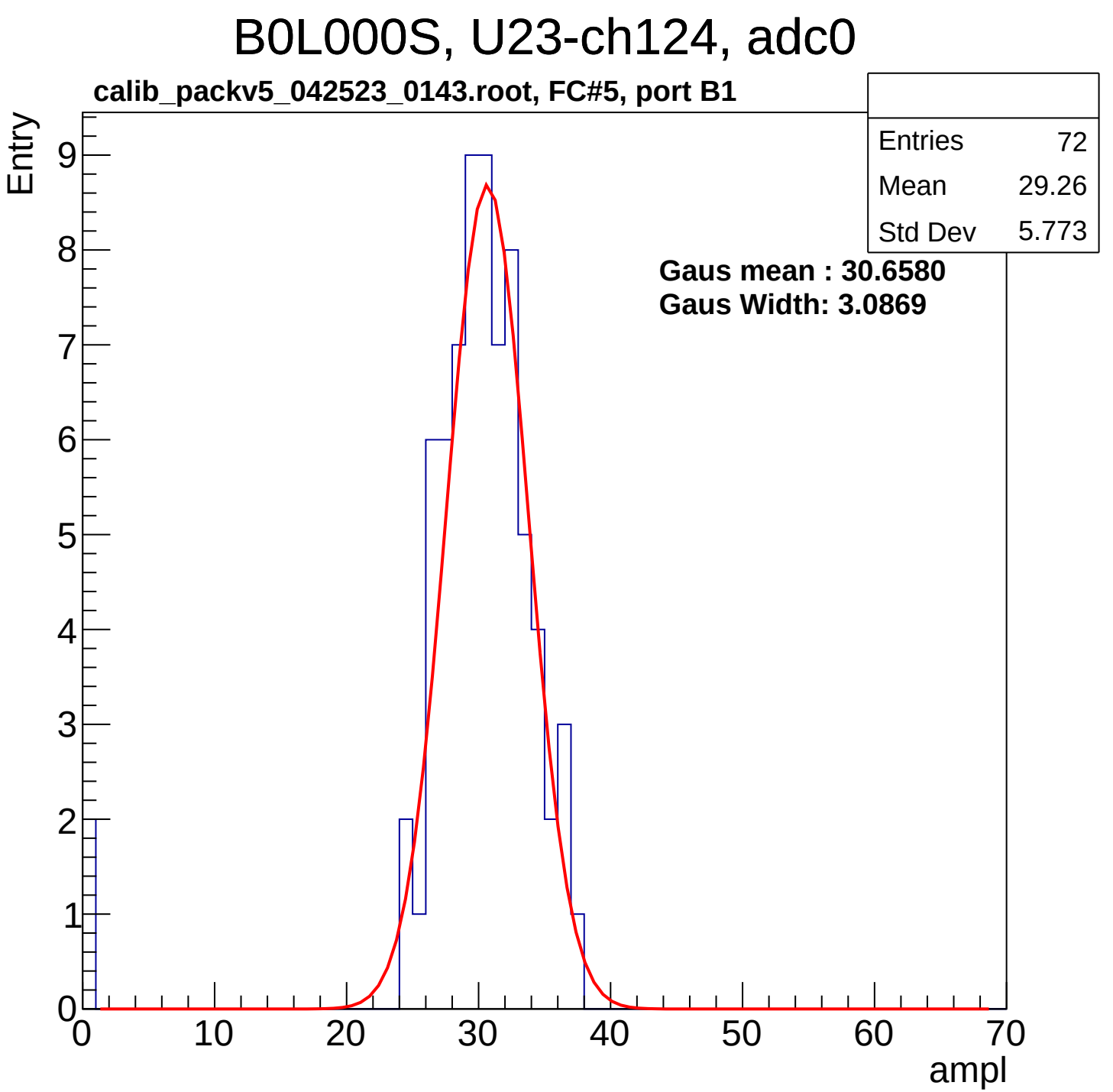

B0L000S, U23-ch123, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 50.14
Std Dev 3.79
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

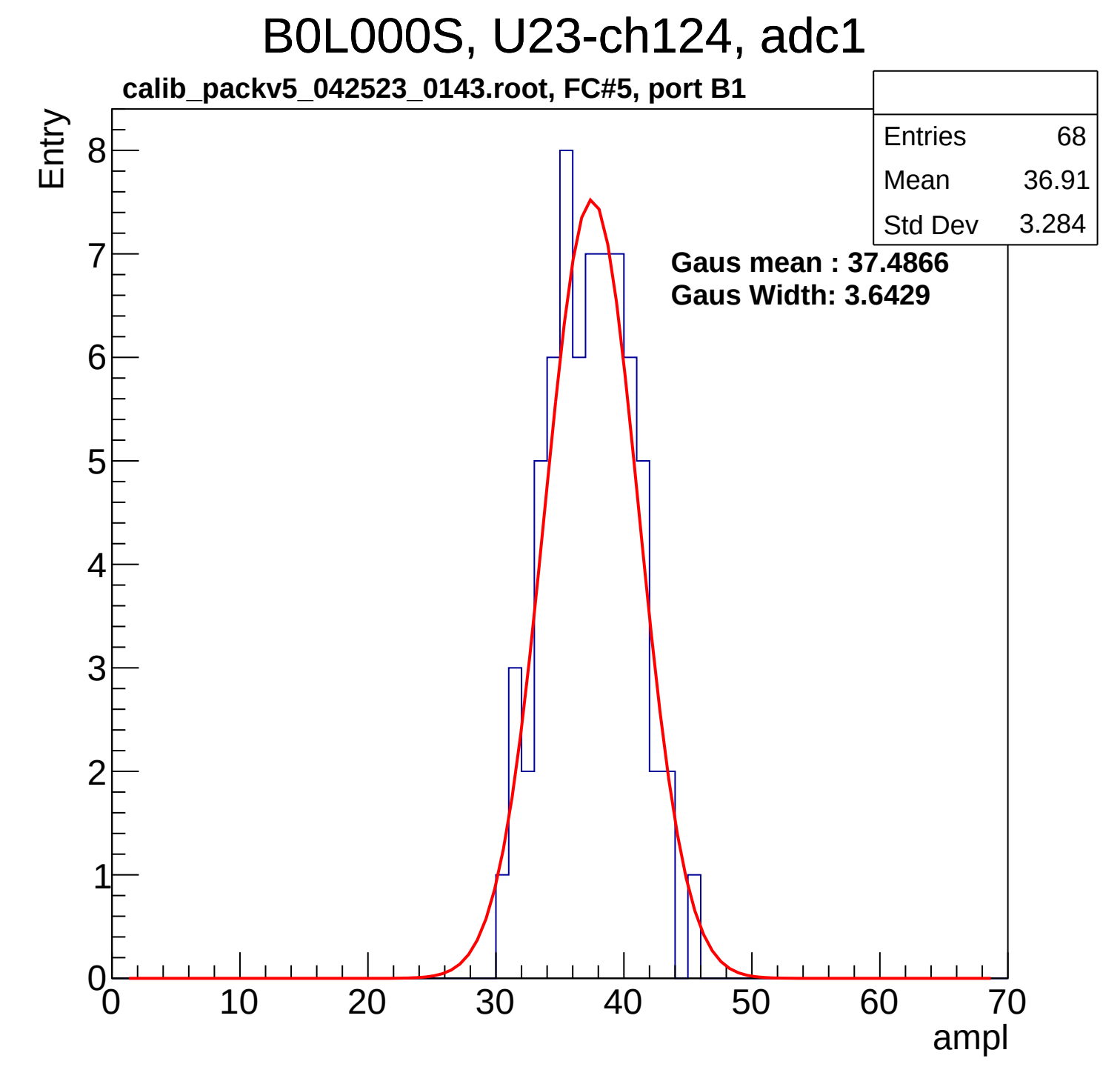

B0L000S, U23-ch123, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.46
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

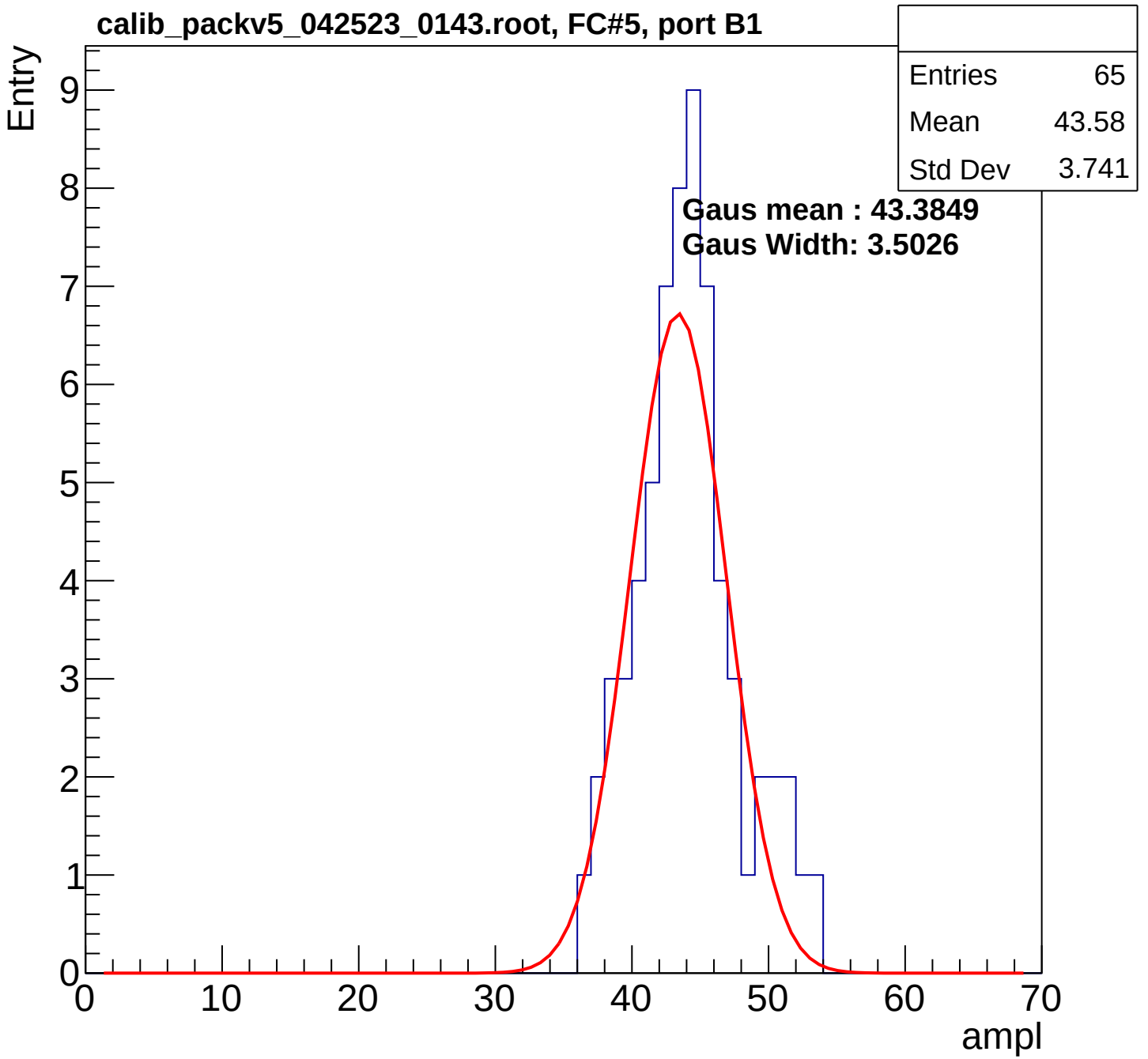

B0L000S, U23-ch123, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.83
Std Dev 10.24
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

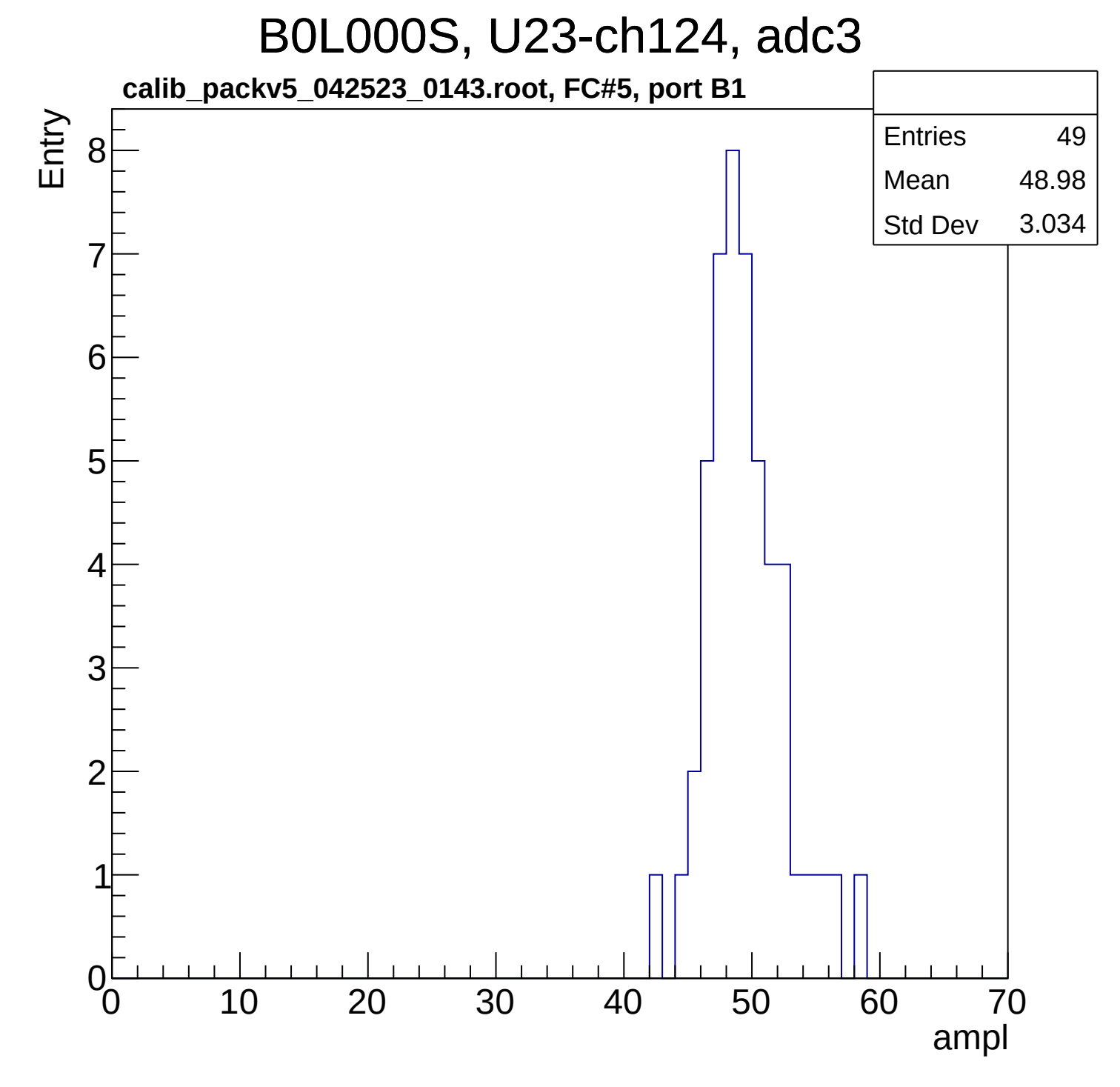

B0L000S, U23-ch123, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

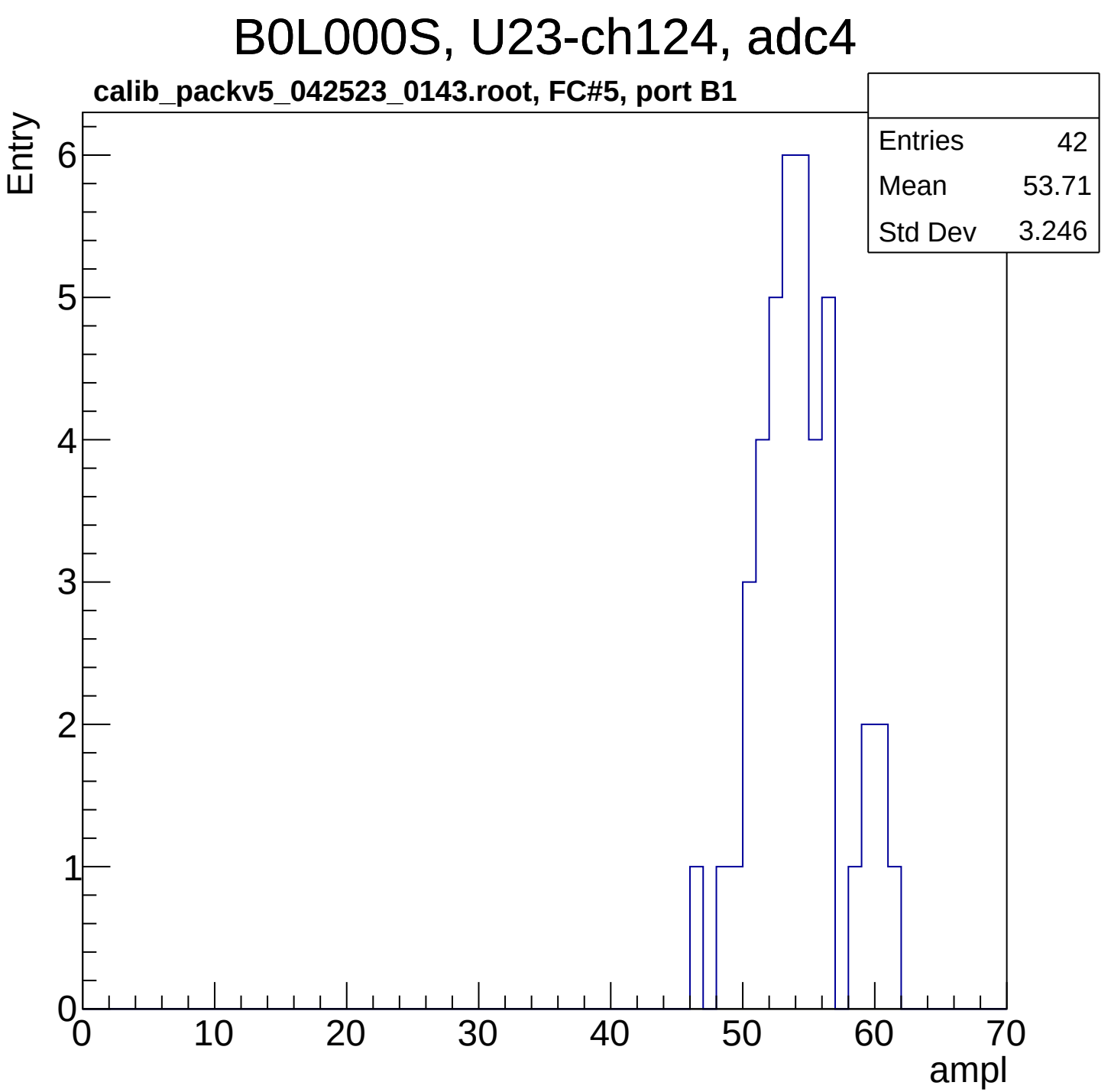

B0L000S, U23-ch123, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

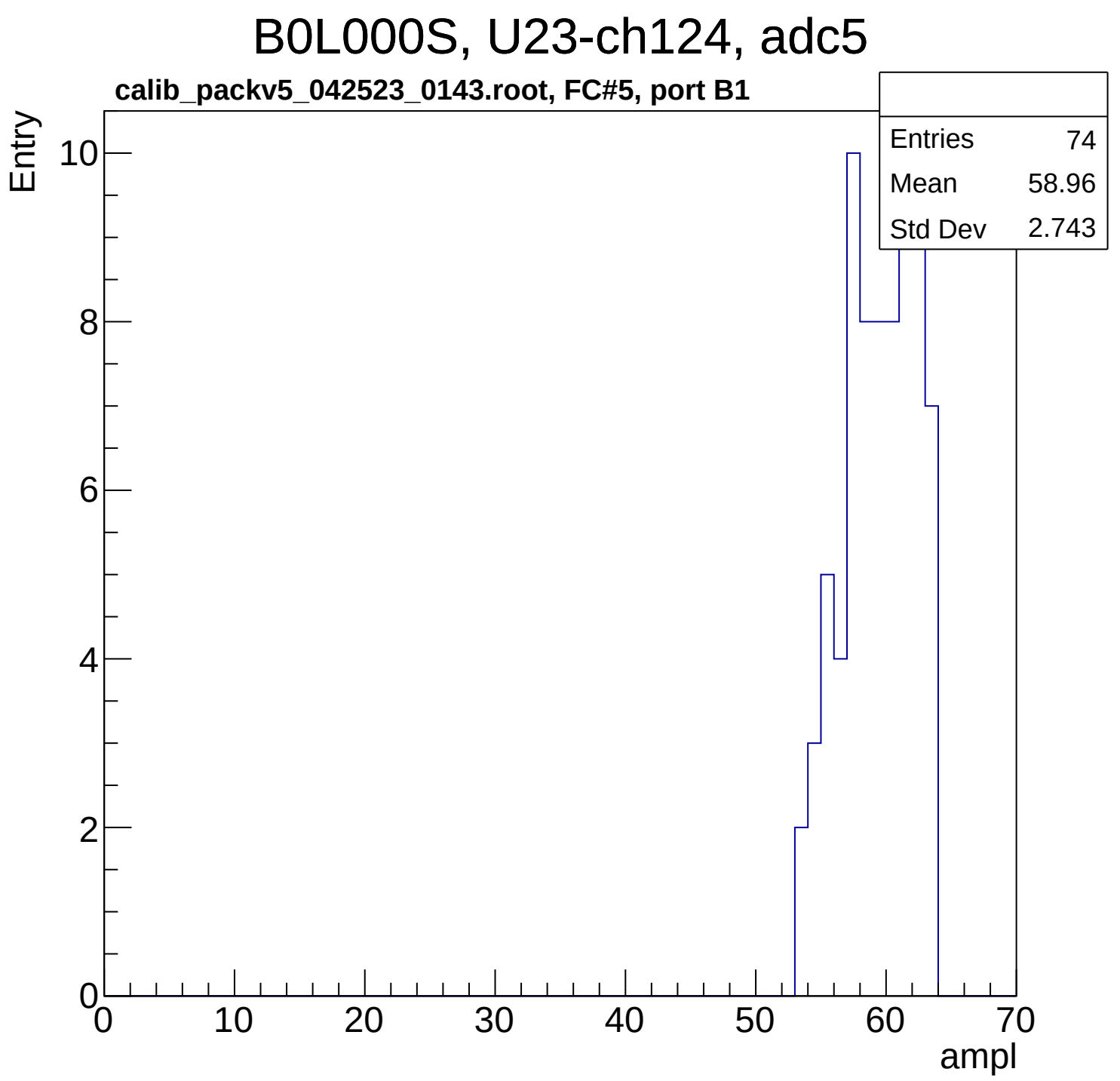

B0L000S, U23-ch124, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.26
Std Dev 5.773
Gaus mean : 30.6580
Gaus Width: 3.0869
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

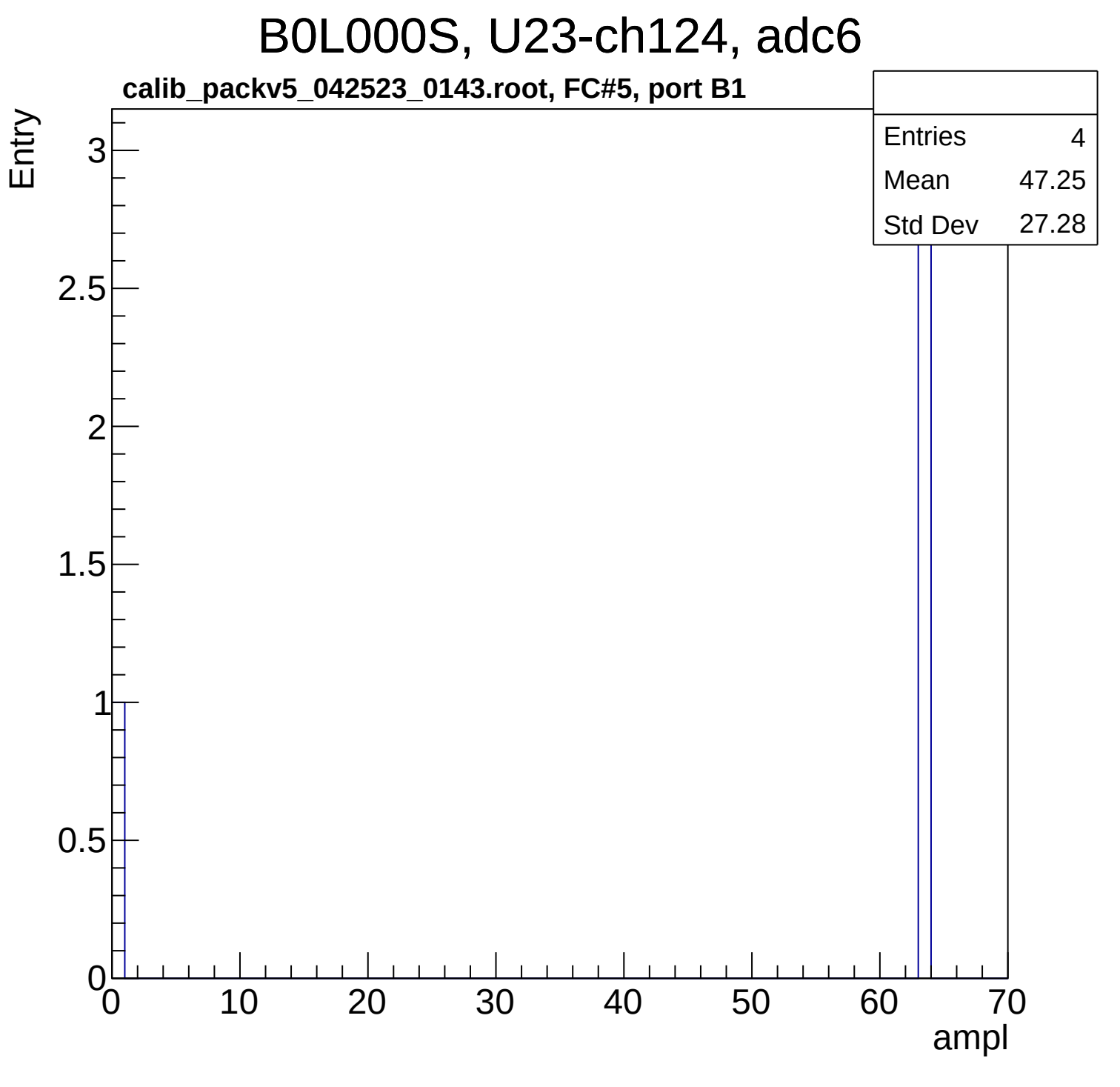

B0L000S, U23-ch124, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.91
Std Dev 3.284
Gaus mean : 37.4866
Gaus Width: 3.6429
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

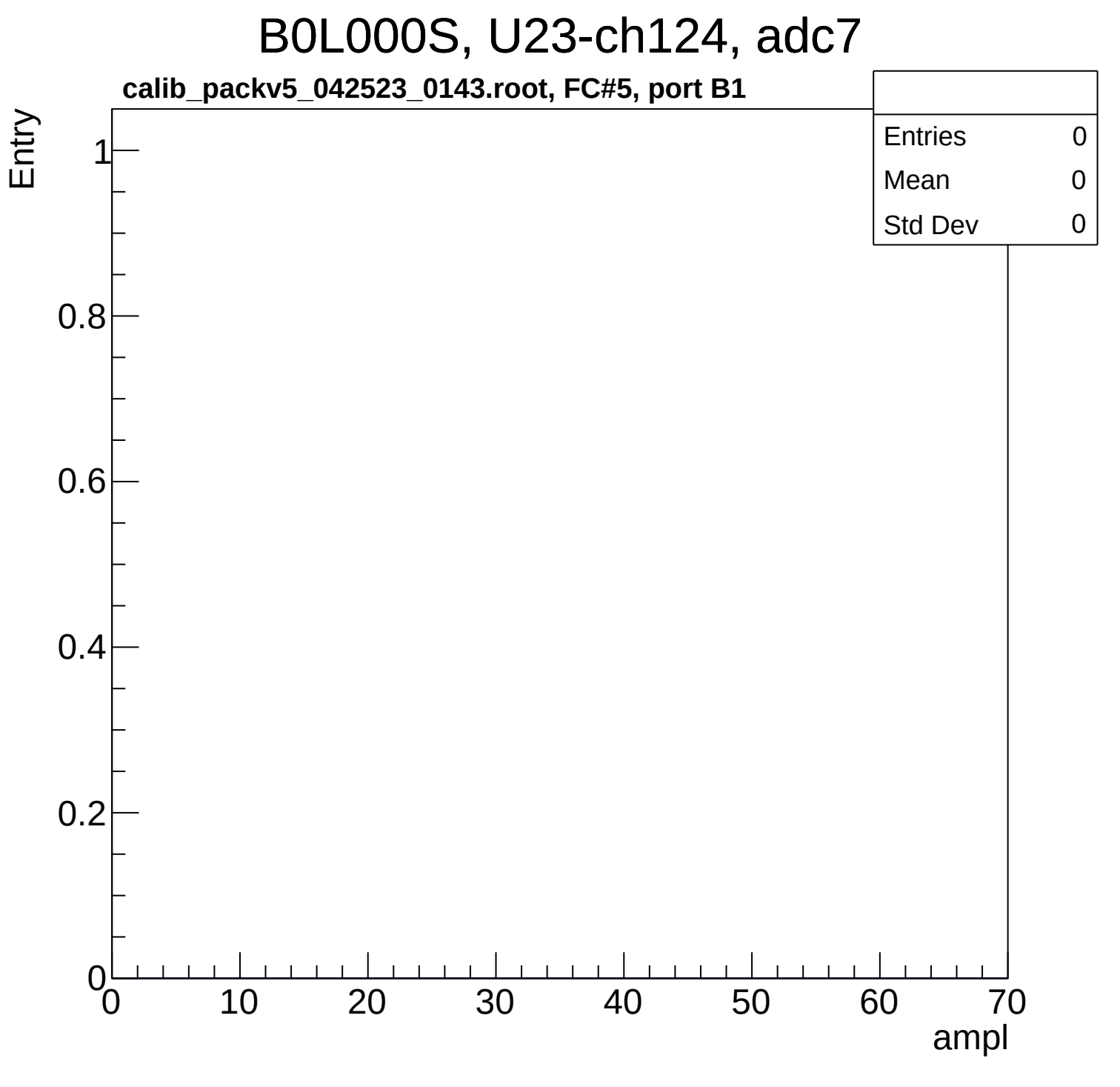

B0L000S, U23-ch124, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.58
Std Dev 3.741
Gaus mean : 43.3849
Gaus Width: 3.5026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

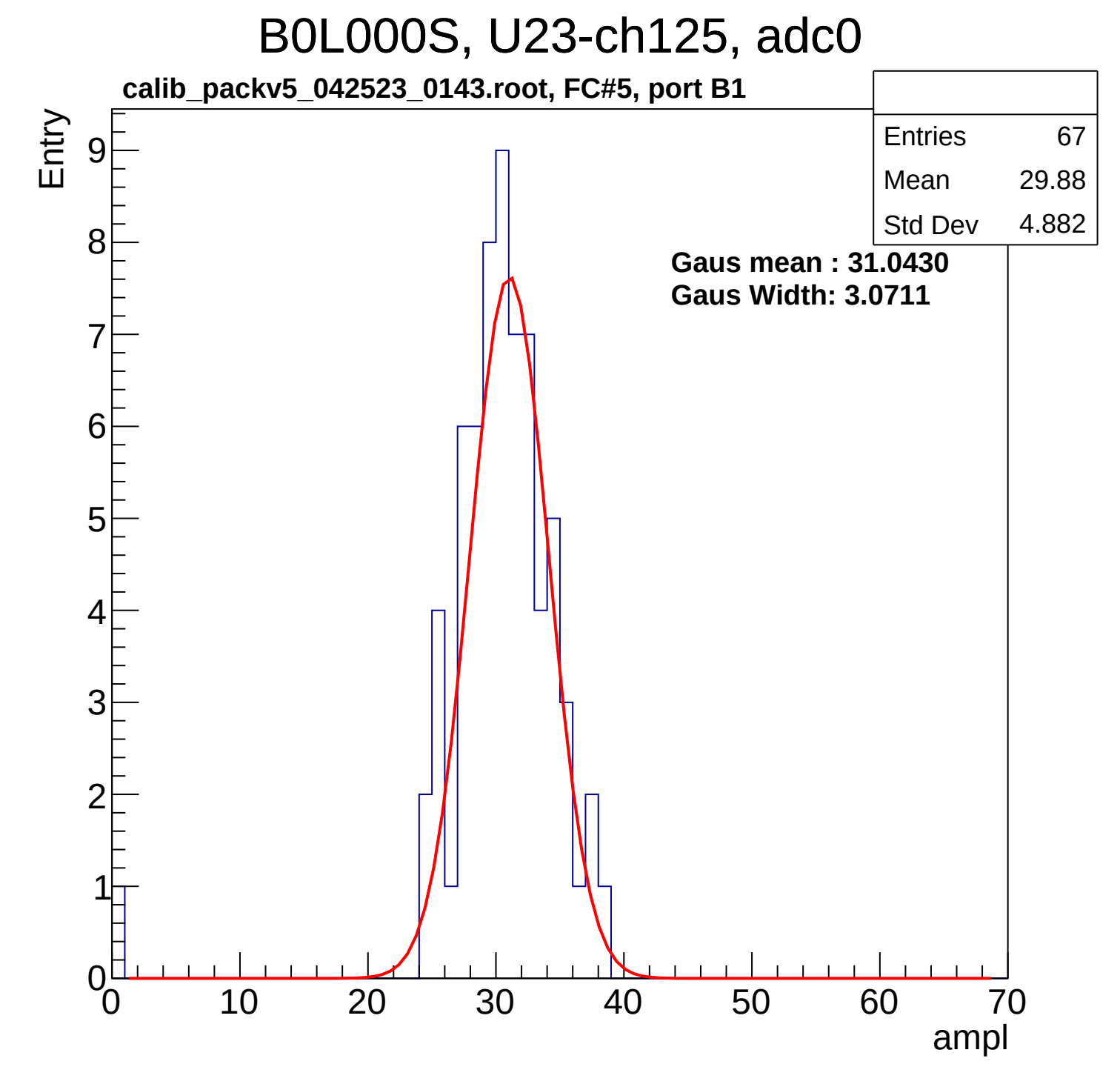

B0L000S, U23-ch124, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 48.98
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

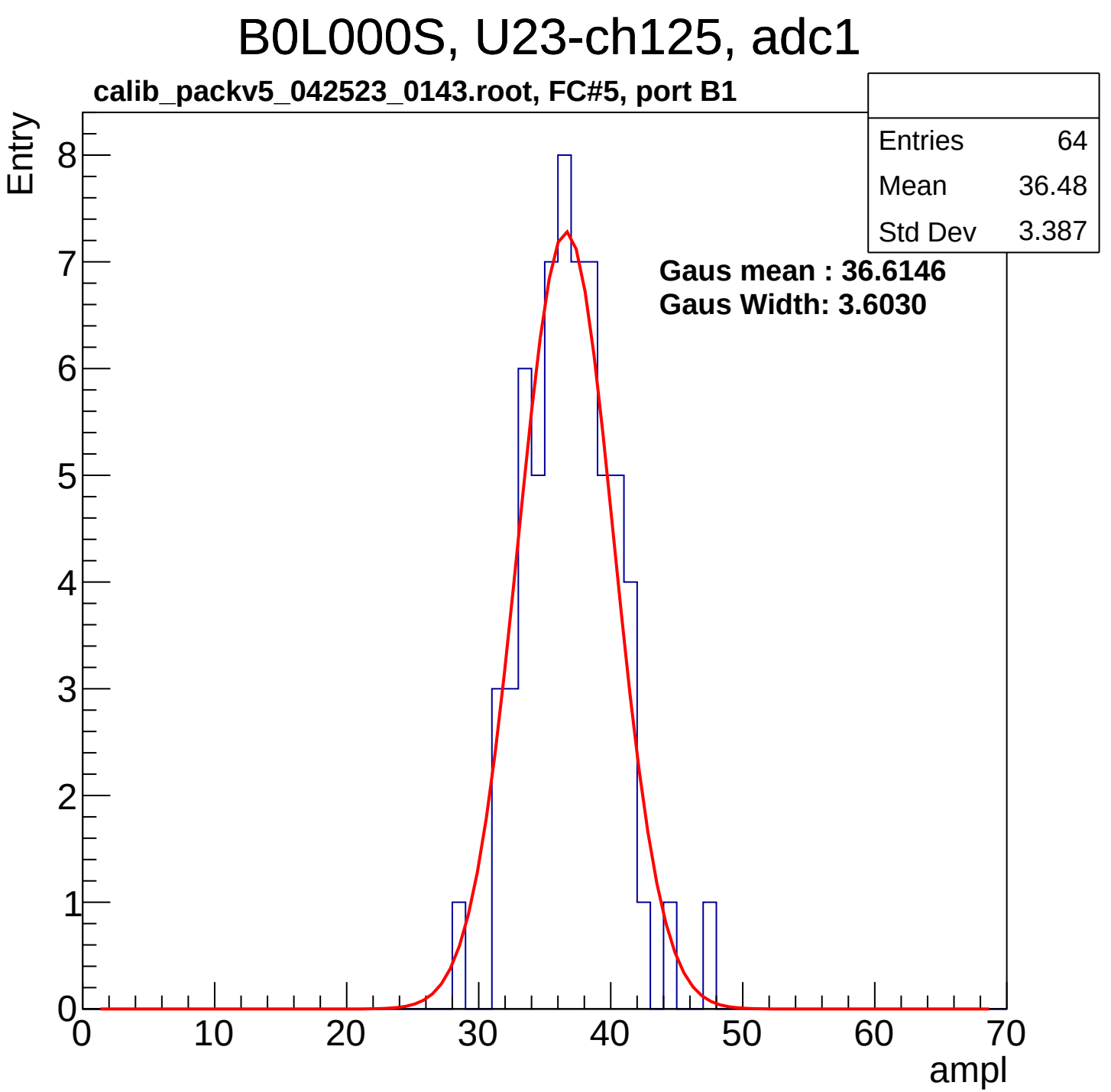

B0L000S, U23-ch124, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 53.71
Std Dev 3.246
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

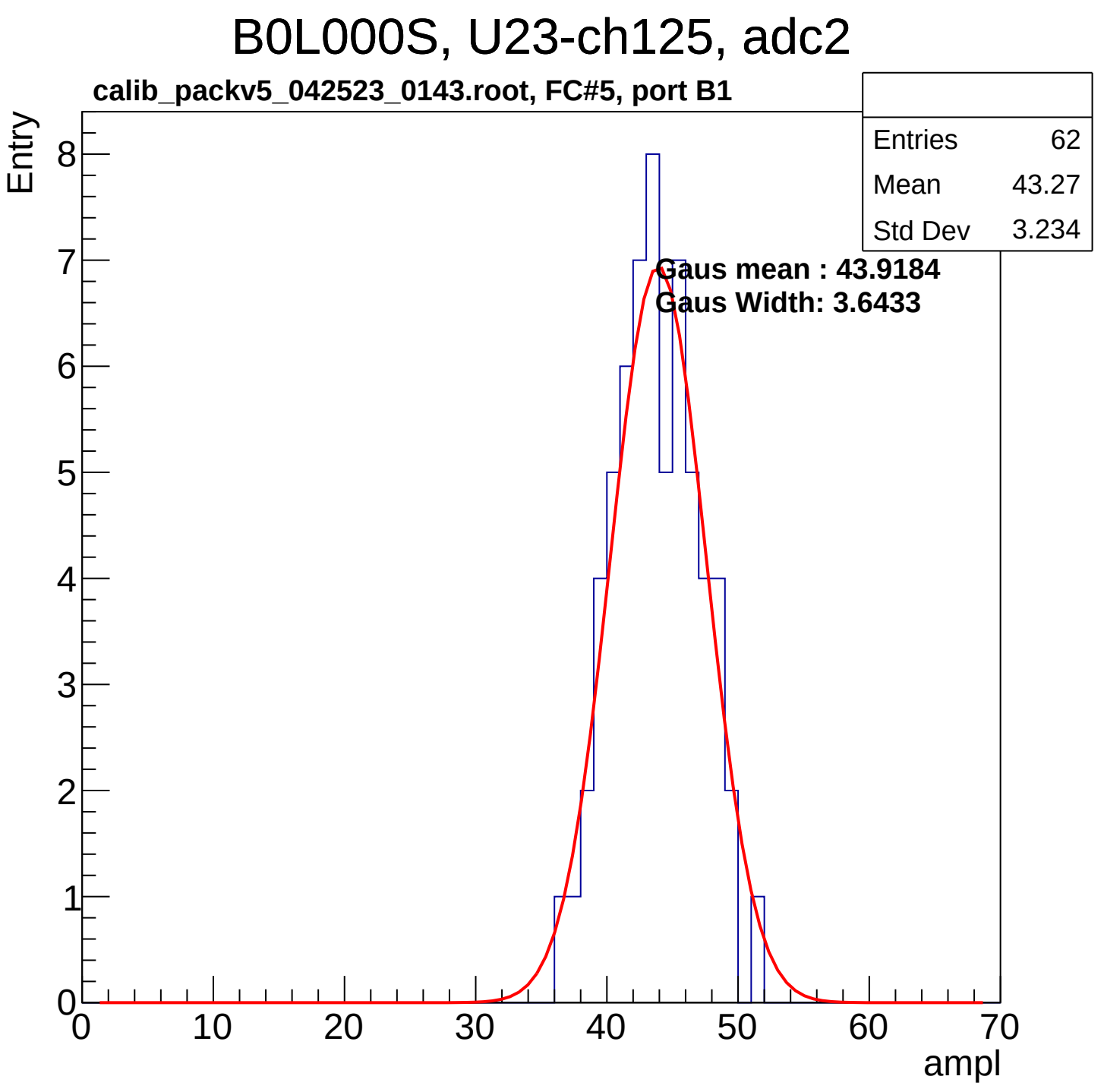

B0L000S, U23-ch124, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 58.96
Std Dev 2.743
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

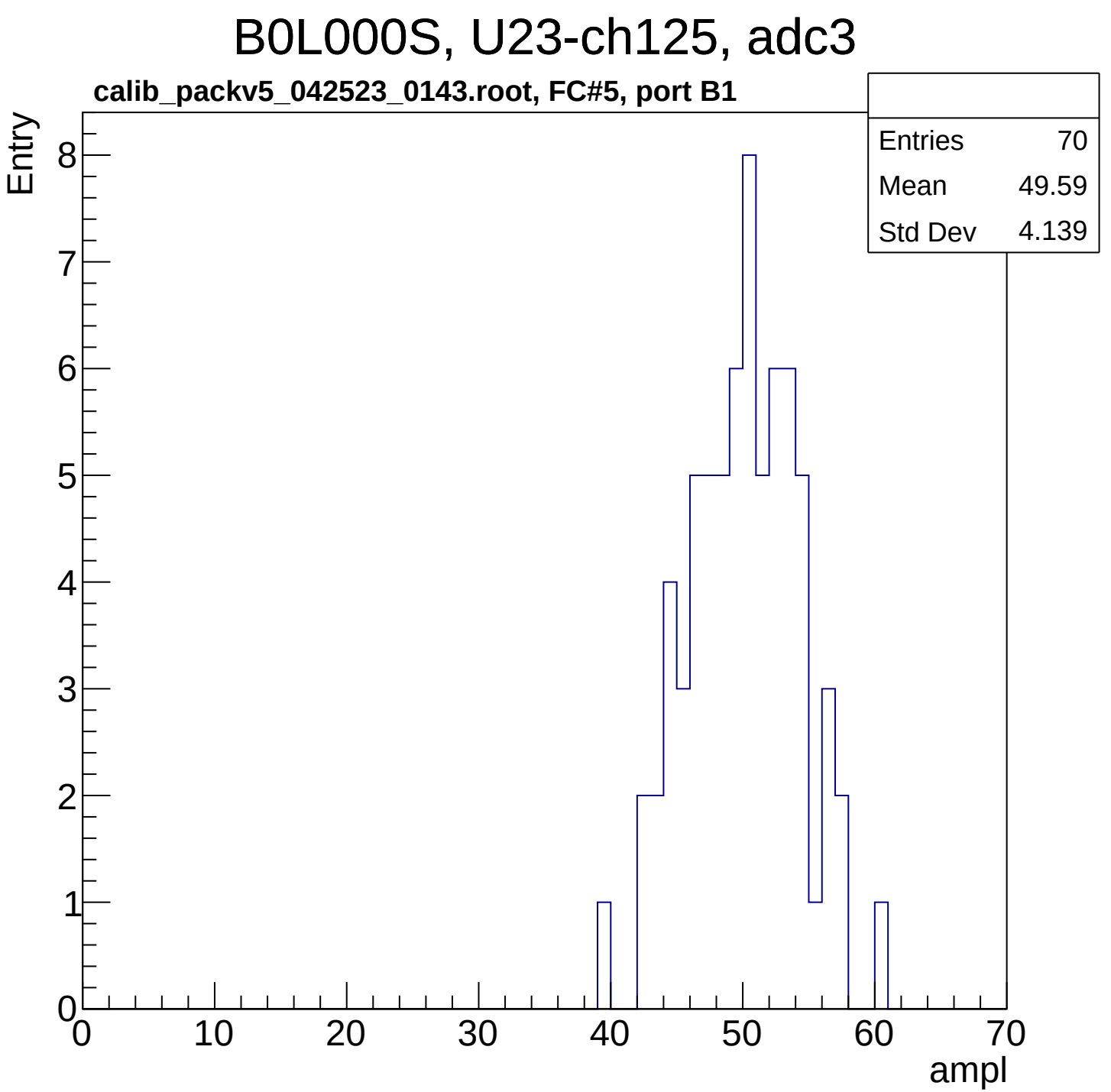

B0L000S, U23-ch124, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

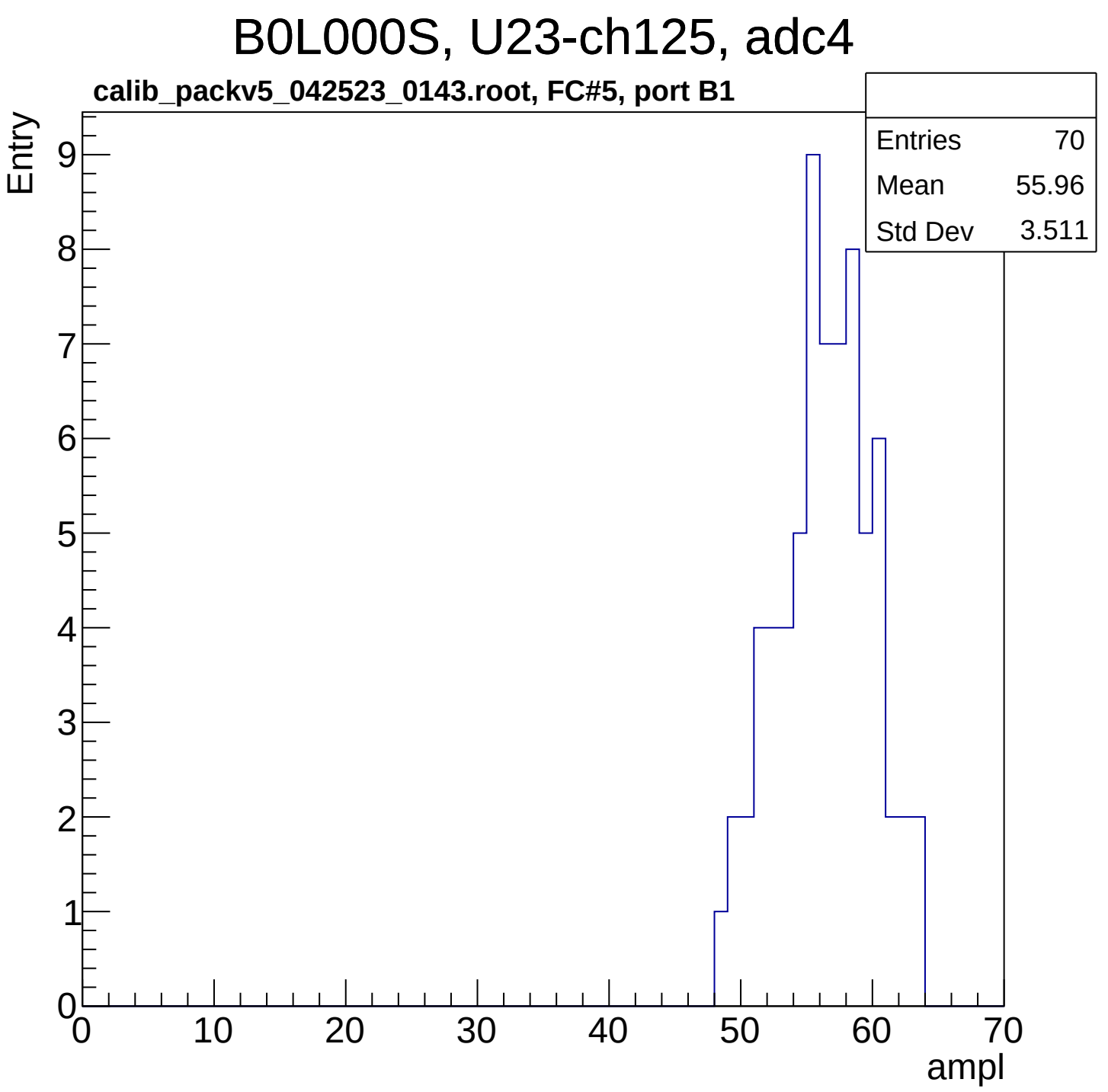

B0L000S, U23-ch124, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

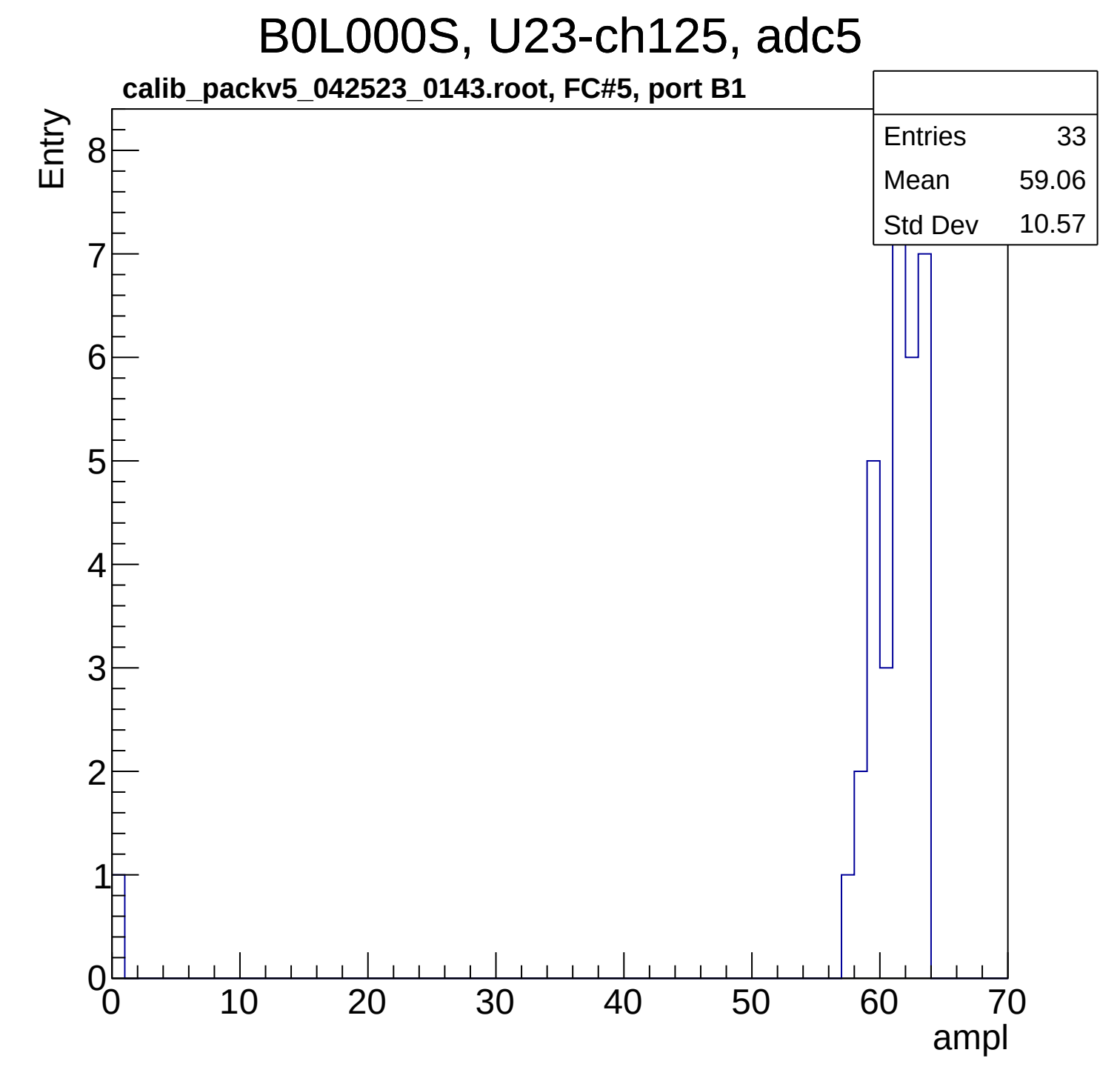

B0L000S, U23-ch125, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 29.88
Std Dev 4.882
Gaus mean : 31.0430
Gaus Width: 3.0711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

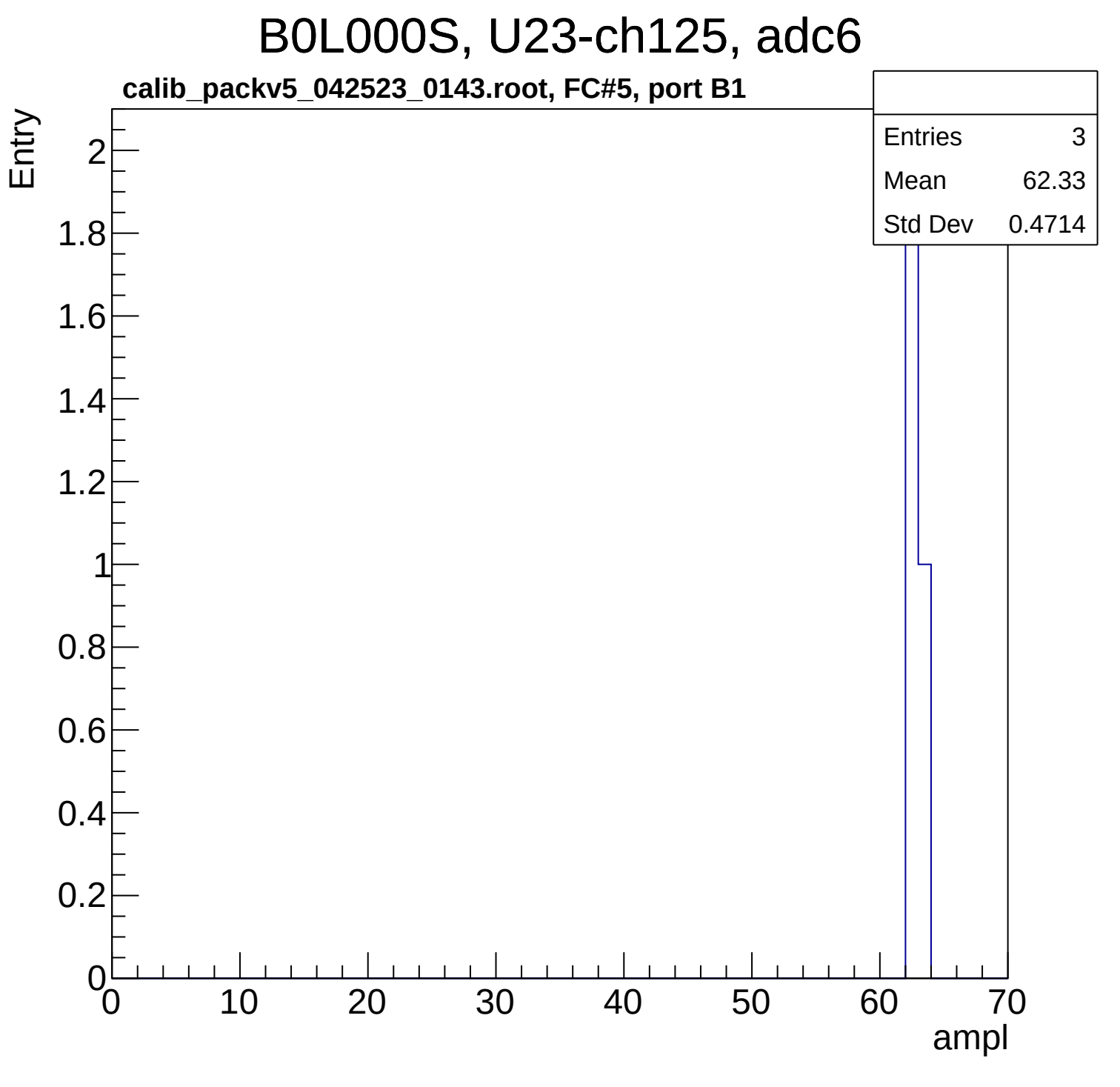

B0L000S, U23-ch125, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 36.48
Std Dev 3.387
Gaus mean : 36.6146
Gaus Width: 3.6030
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

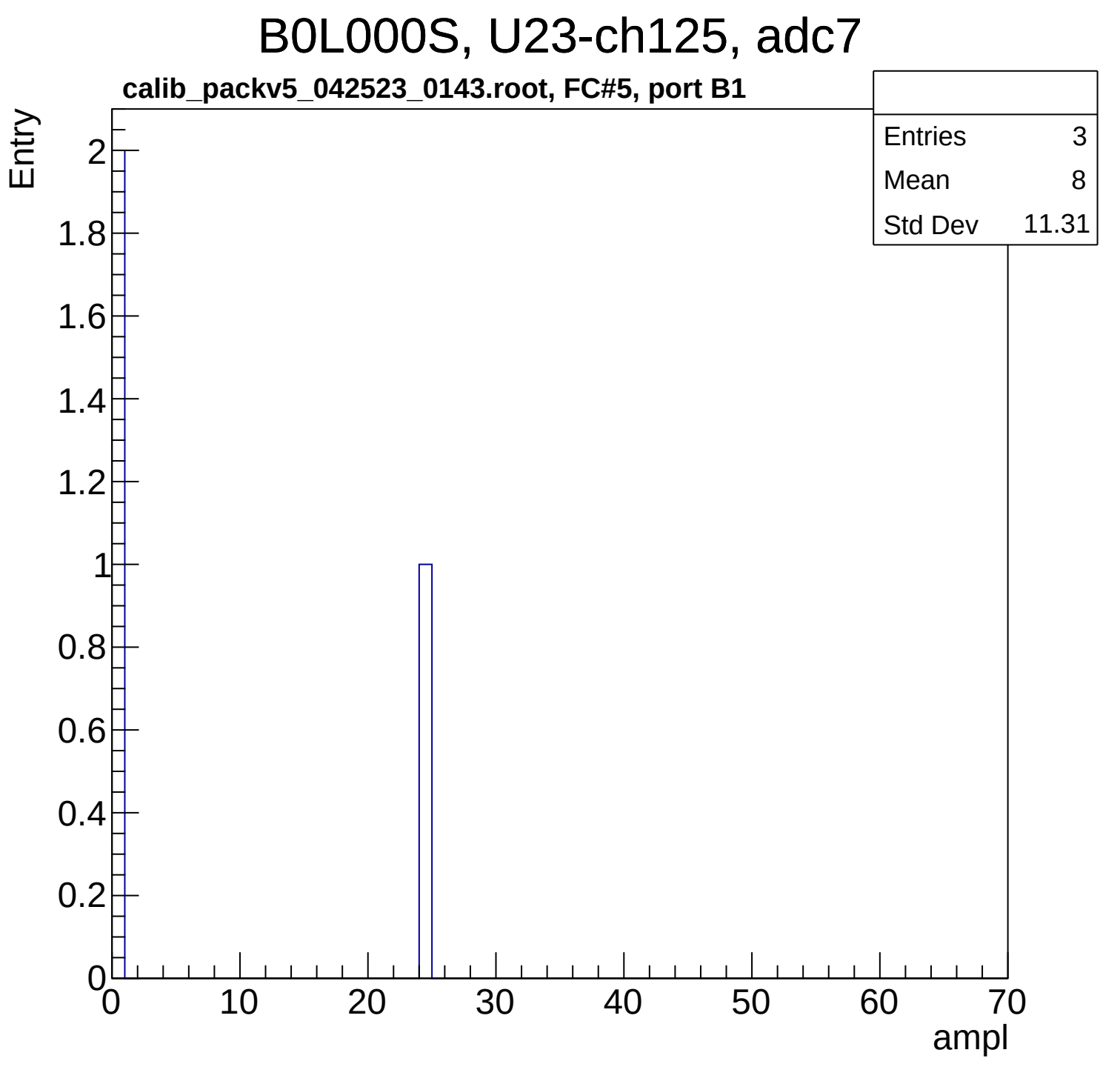

B0L000S, U23-ch125, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.27
Std Dev 3.234
Gaus mean : 43.9184
Gaus Width: 3.6433
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

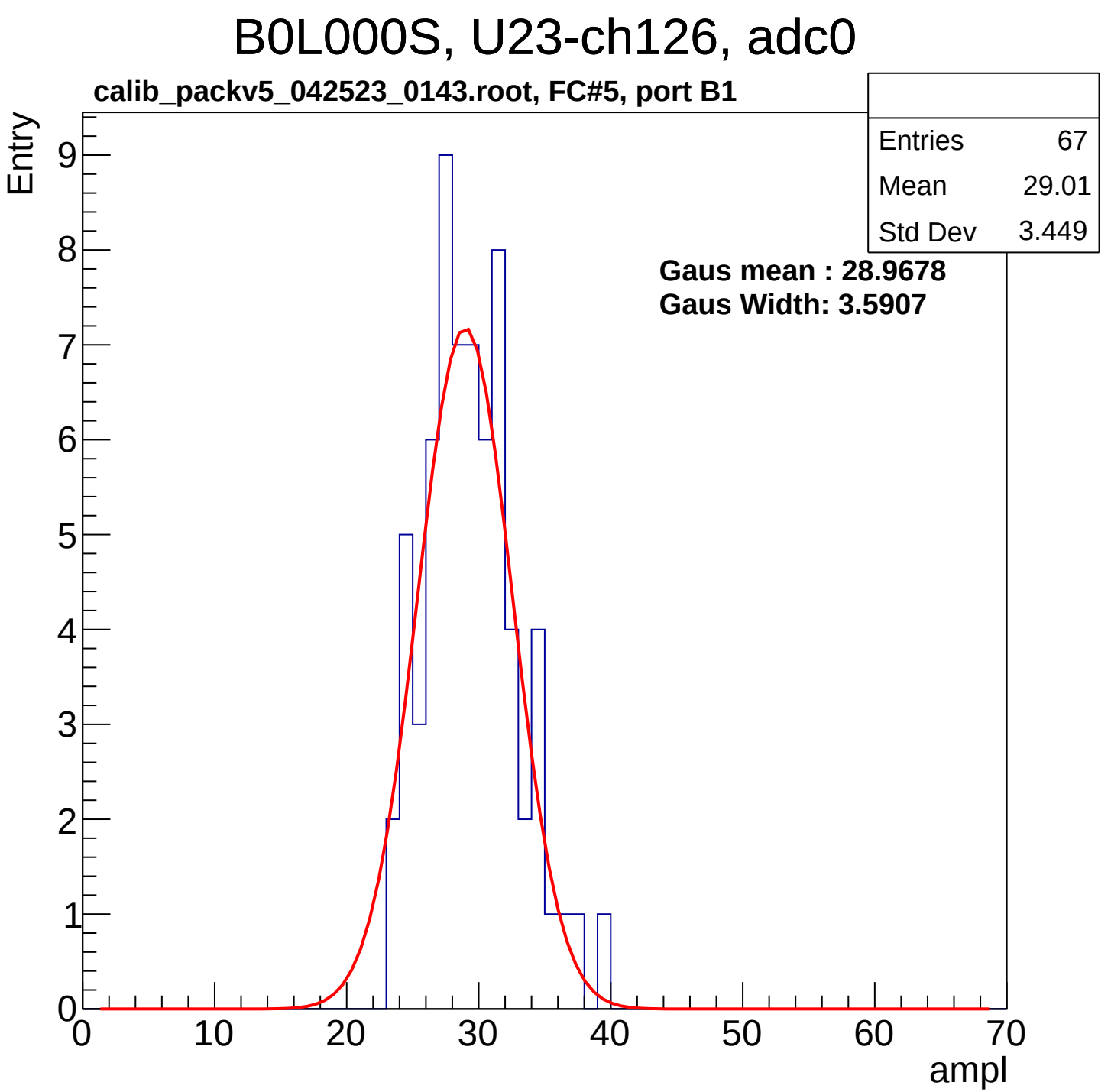

B0L000S, U23-ch125, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.59
Std Dev 4.139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

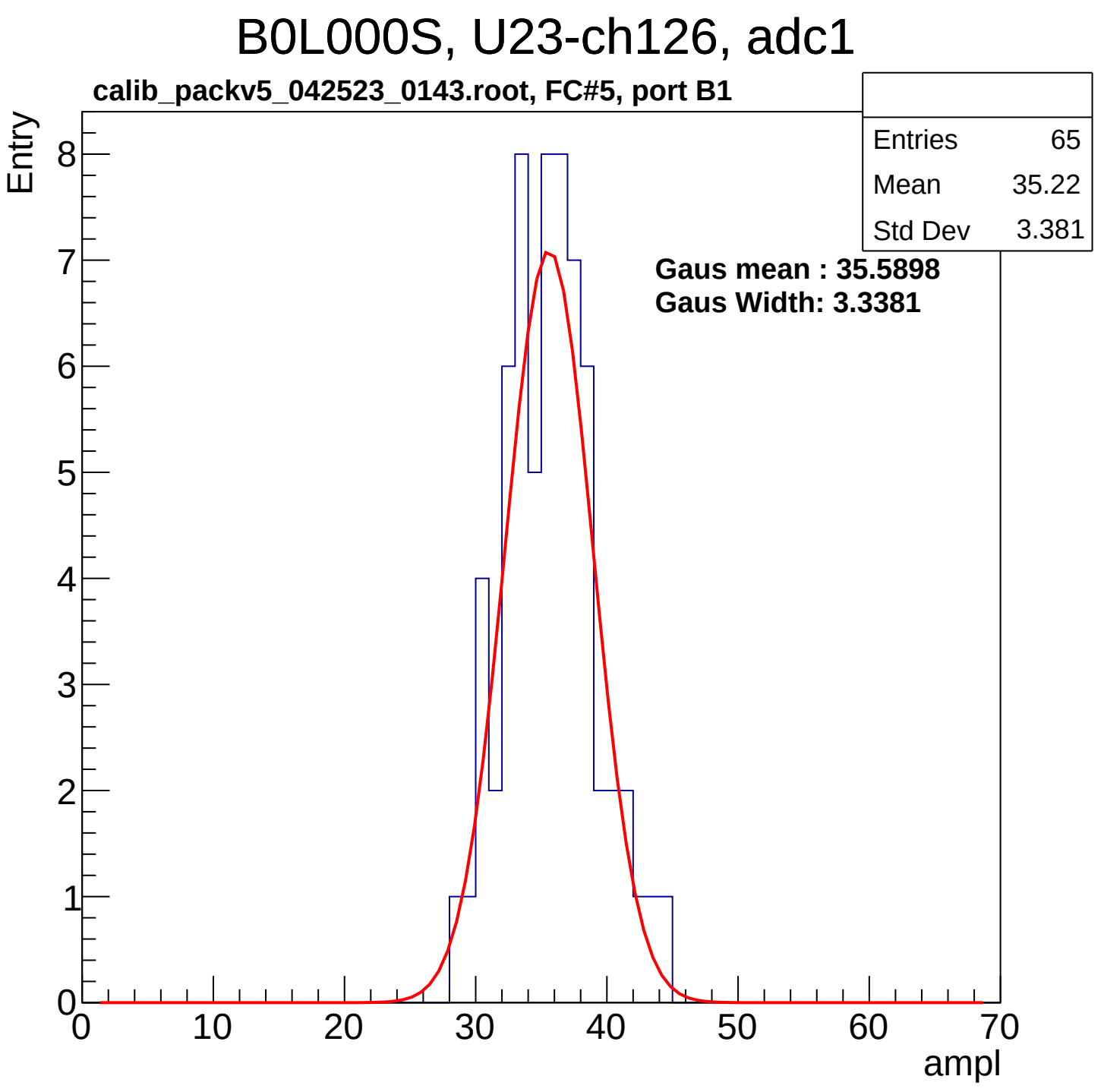

B0L000S, U23-ch125, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 55.96
Std Dev 3.511
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

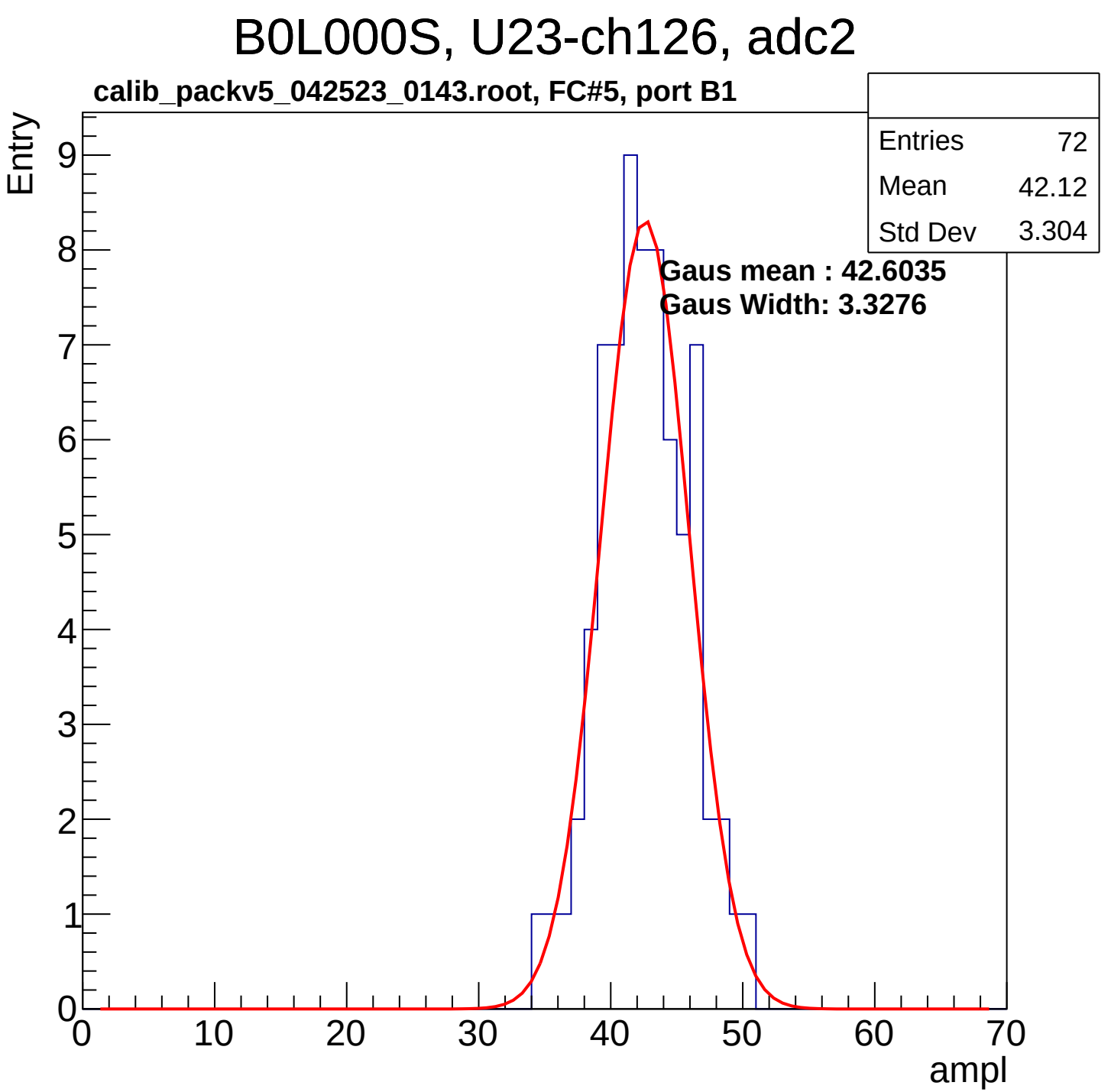

B0L000S, U23-ch125, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 59.06
Std Dev 10.57
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

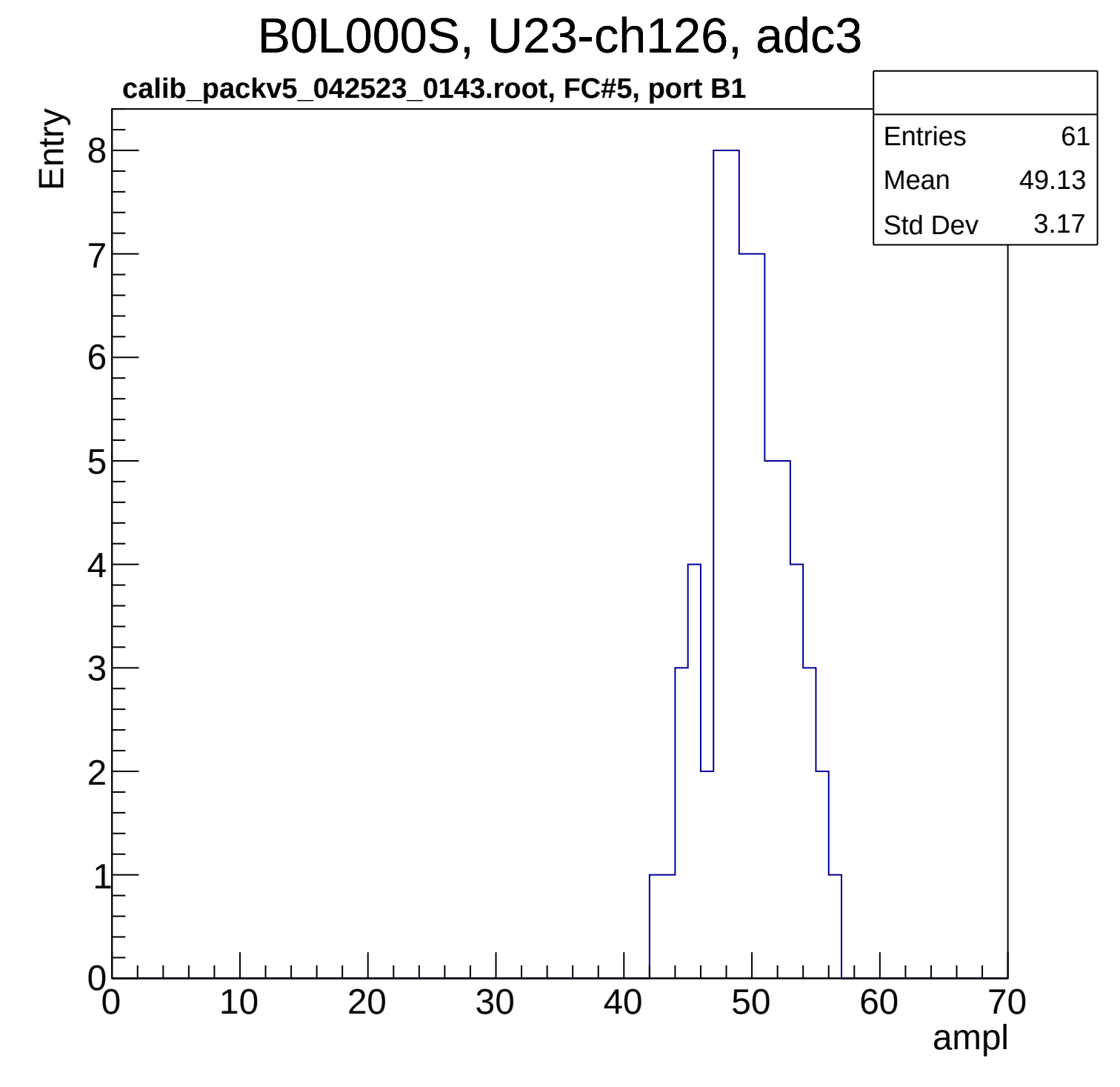

B0L000S, U23-ch125, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

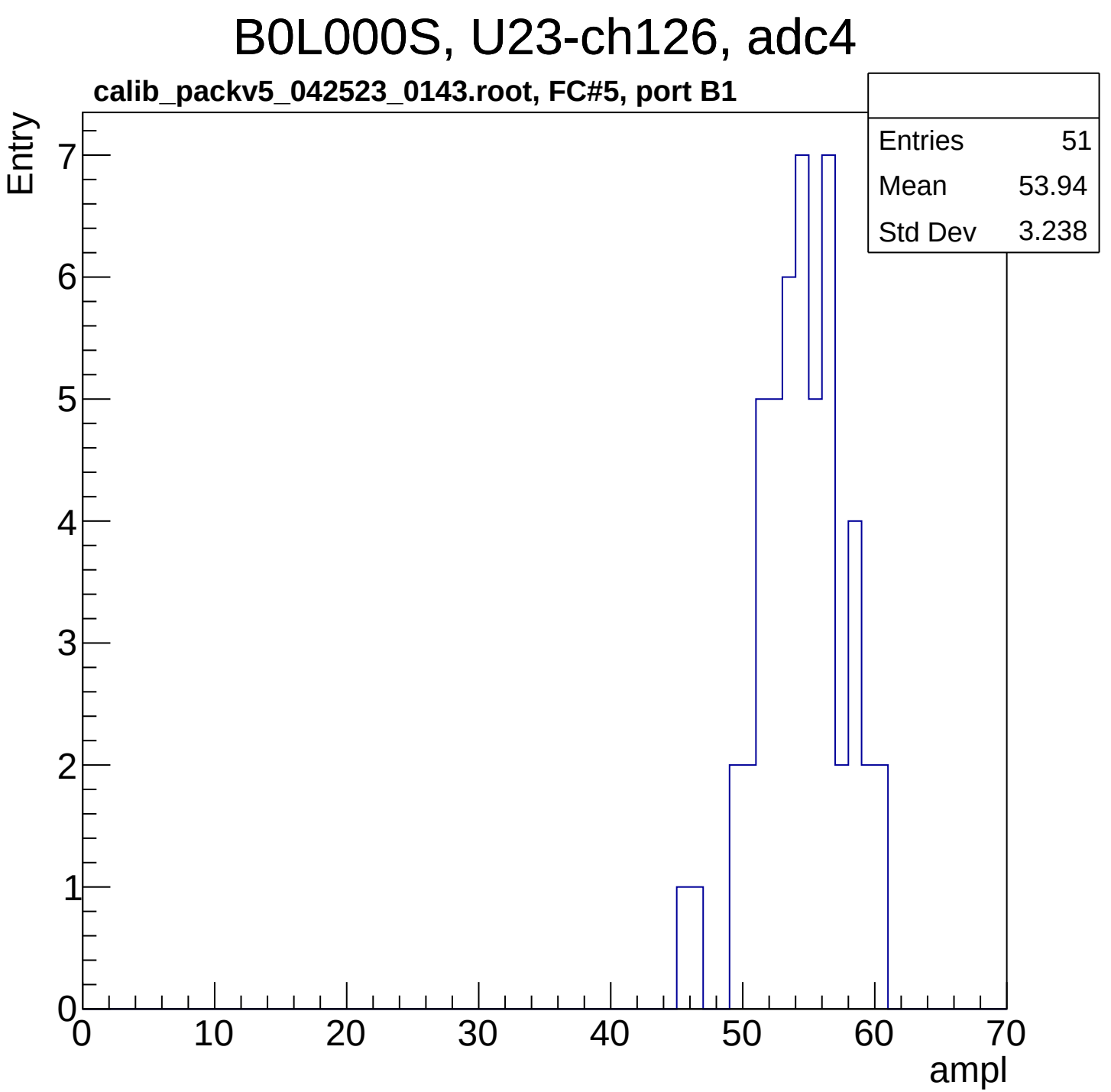

B0L000S, U23-ch125, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

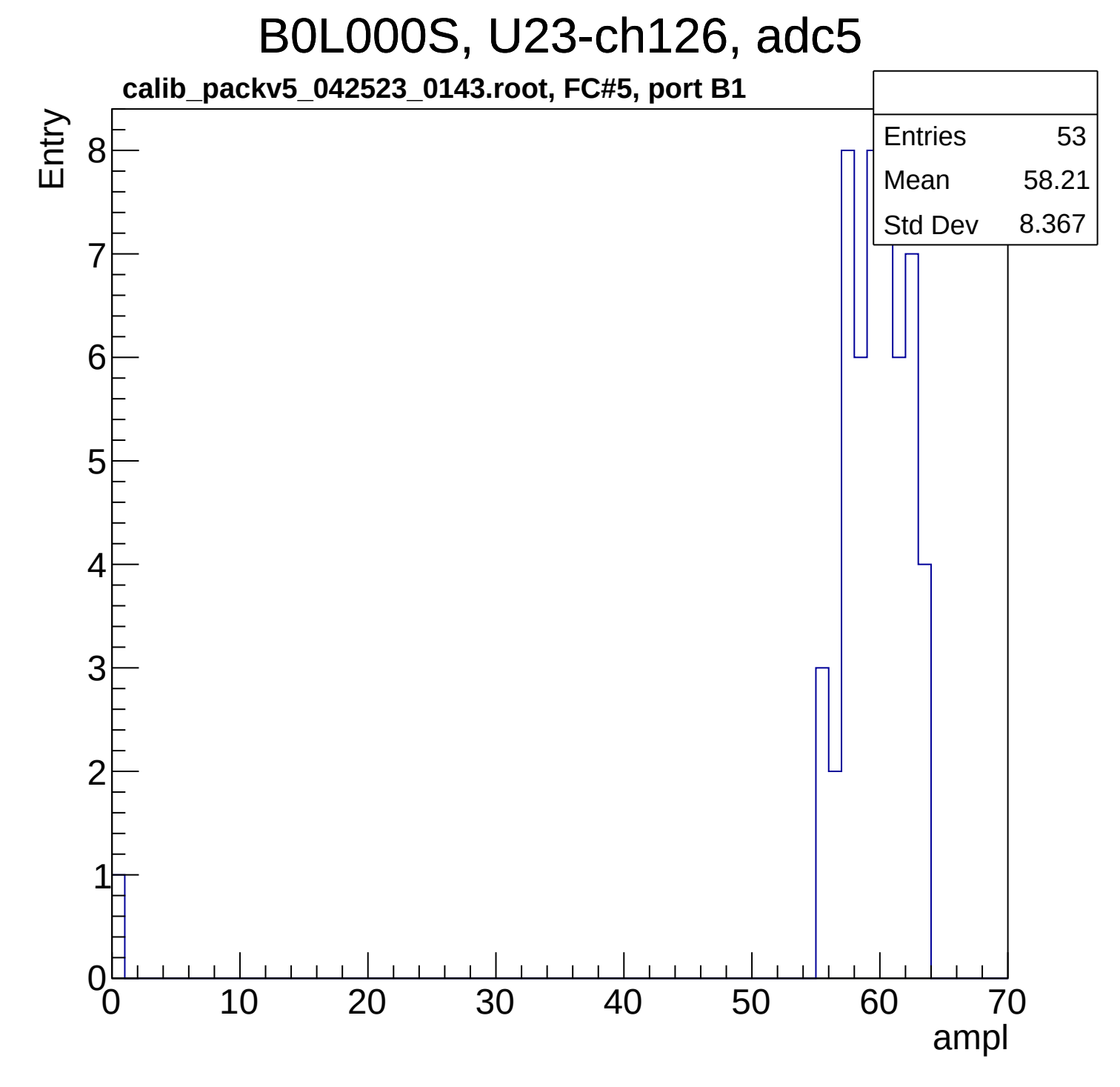

B0L000S, U23-ch126, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 29.01
Std Dev 3.449
Gaus mean : 28.9678
Gaus Width: 3.5907
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

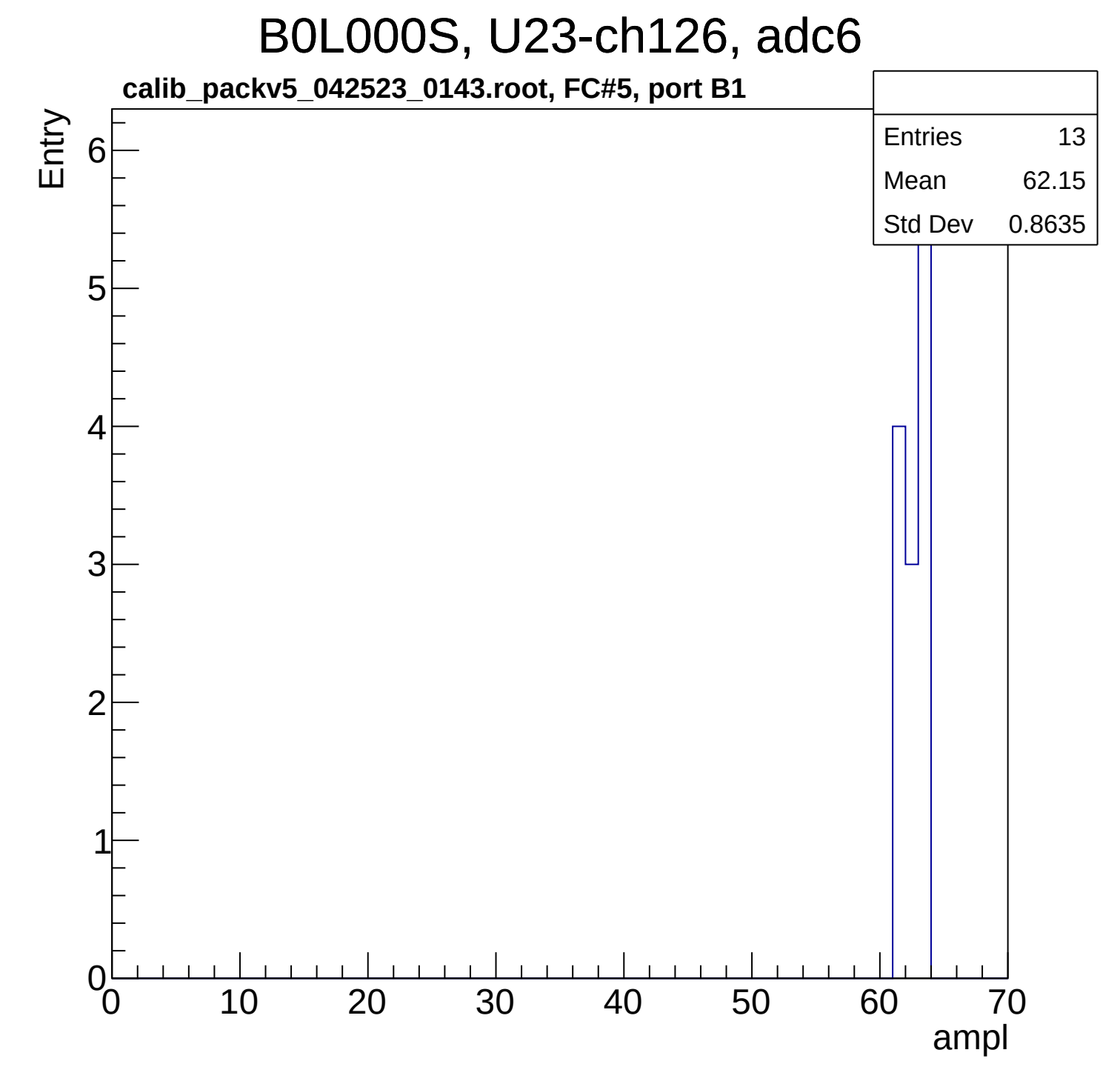

B0L000S, U23-ch126, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 35.22
Std Dev 3.381
Gaus mean : 35.5898
Gaus Width: 3.3381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

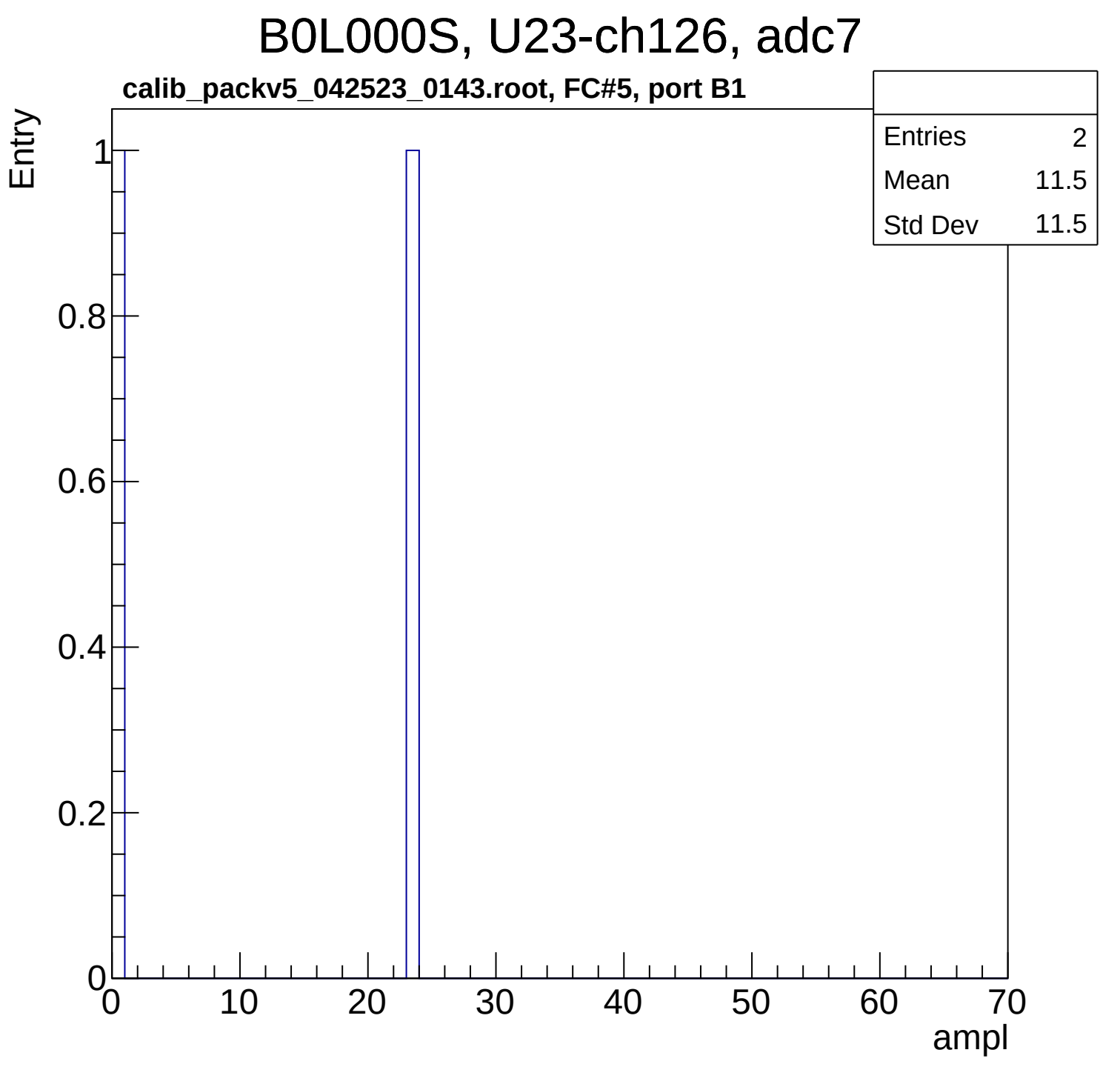

B0L000S, U23-ch126, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 42.12
Std Dev 3.304
Gaus mean : 42.6035
Gaus Width: 3.3276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

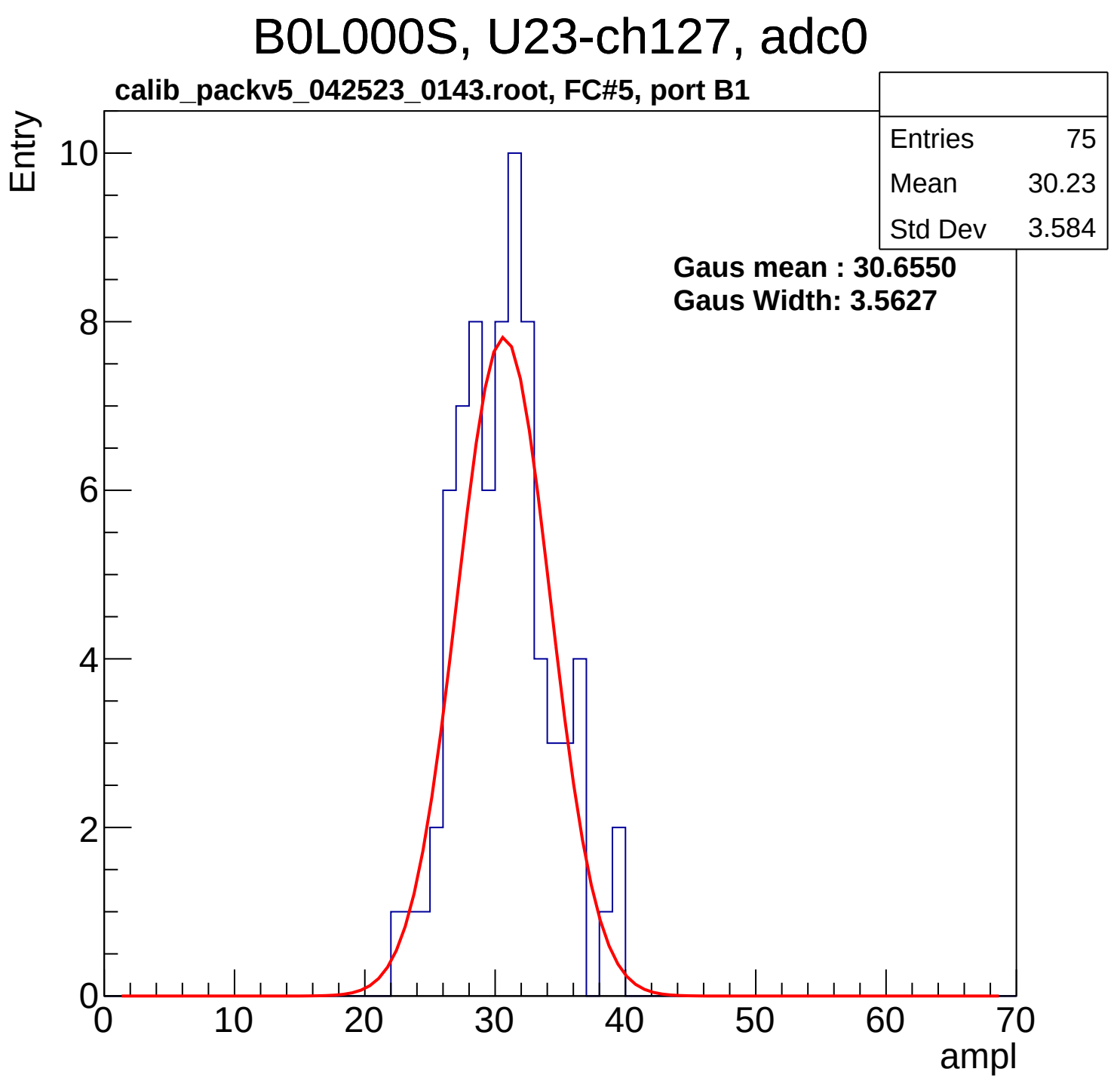

B0L000S, U23-ch126, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 49.13
Std Dev 3.17
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

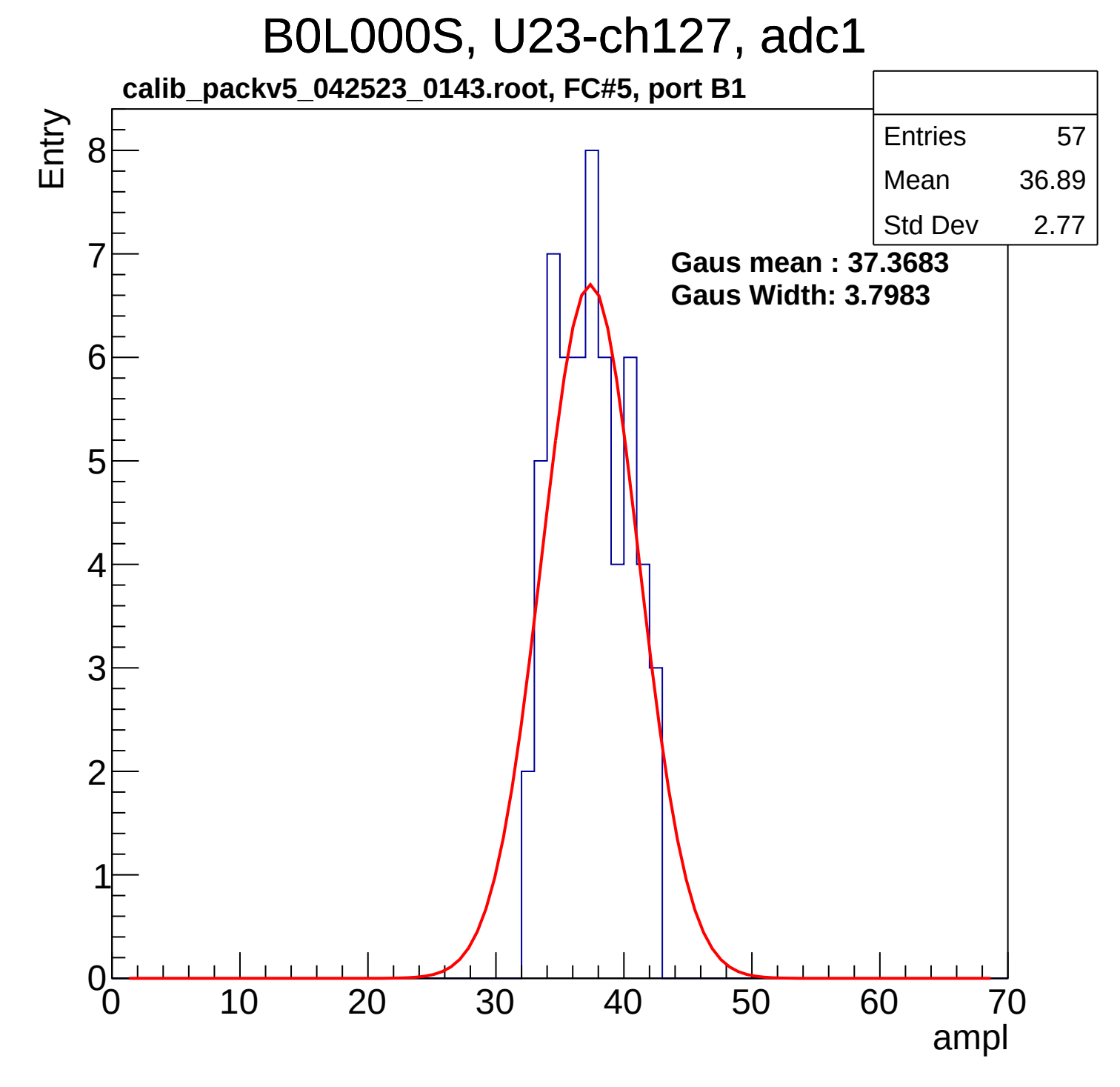

B0L000S, U23-ch126, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 53.94
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

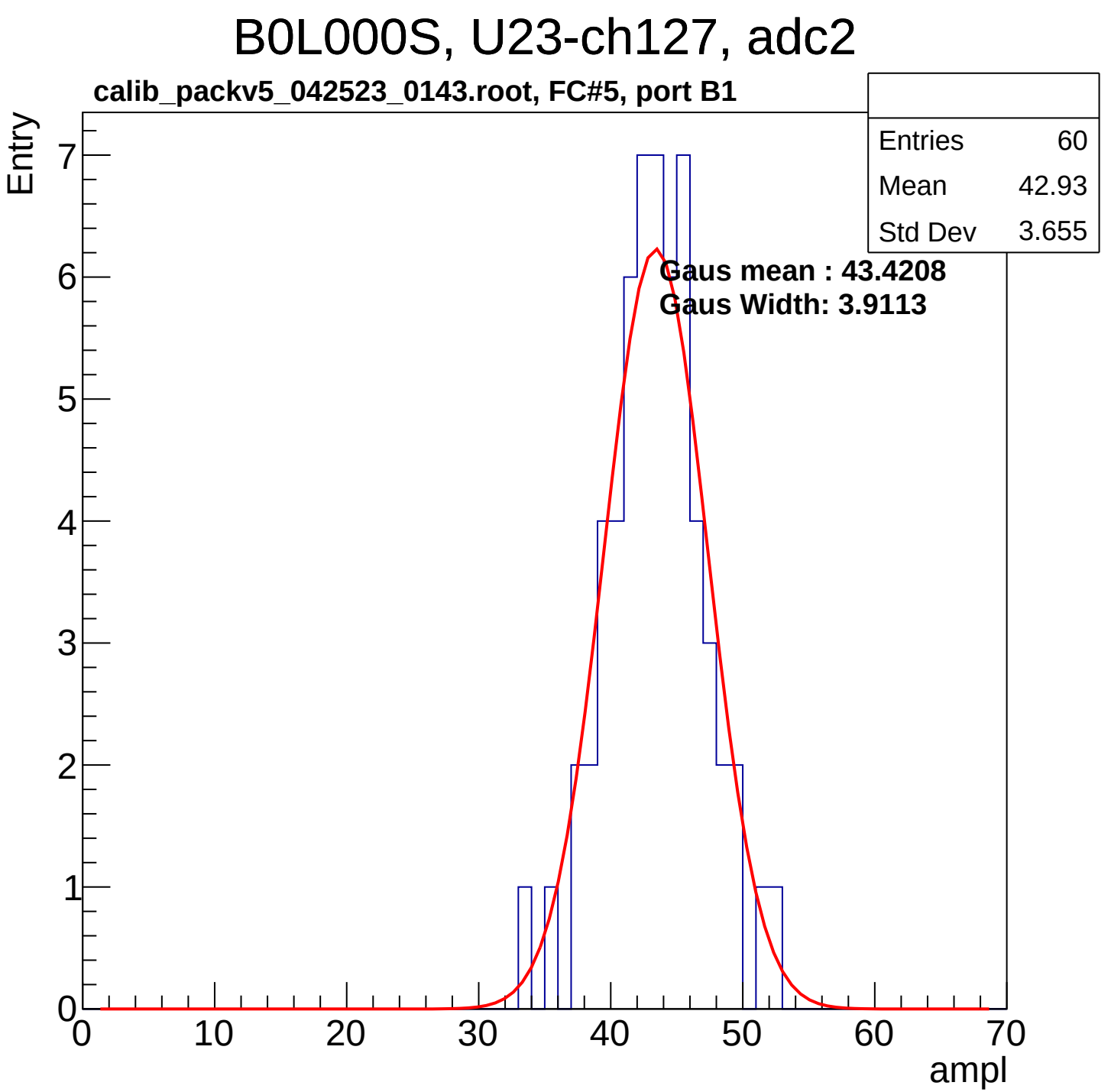

B0L000S, U23-ch126, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.21
Std Dev 8.367
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

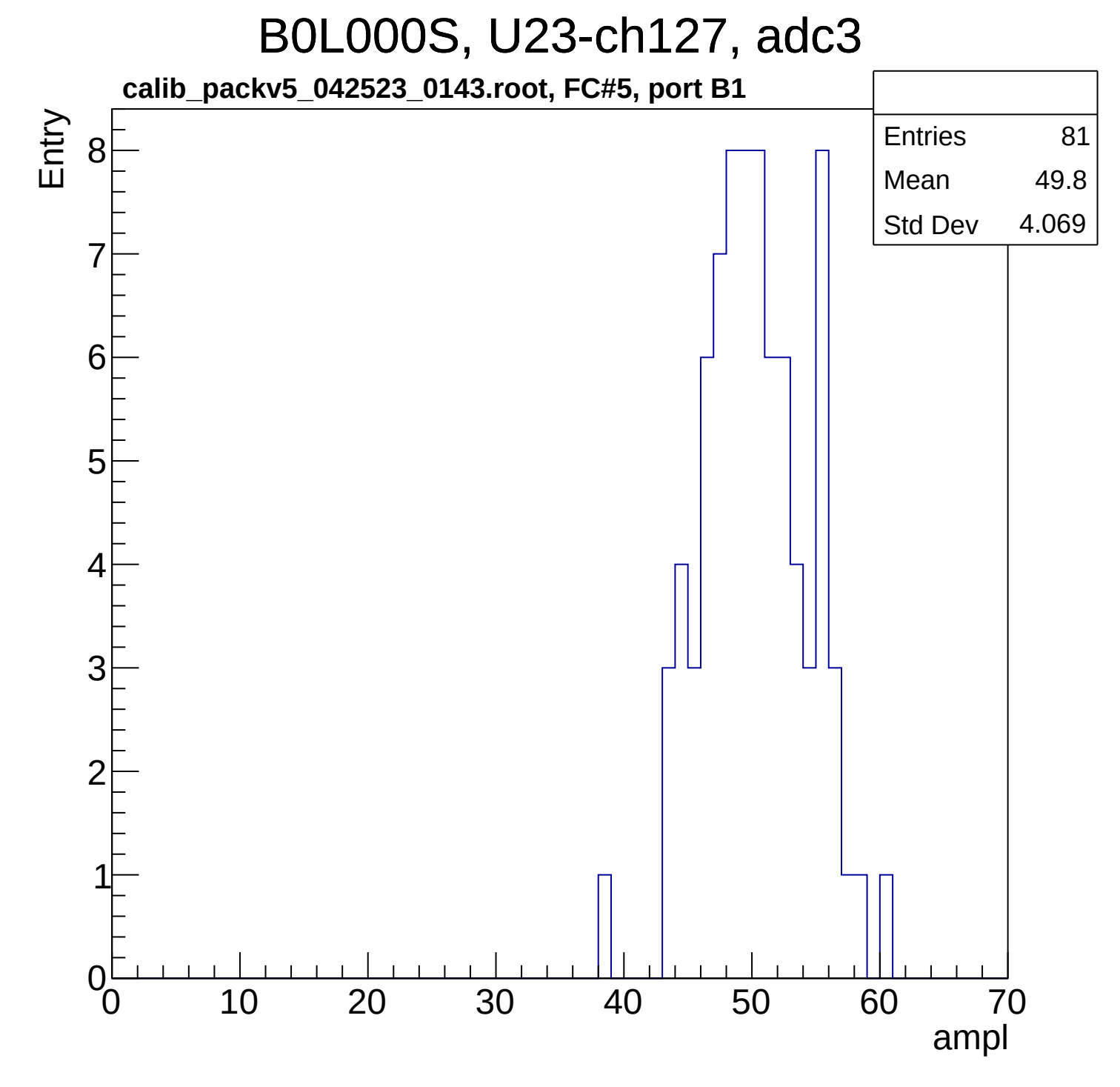

B0L000S, U23-ch126, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 62.15
Std Dev 0.8635
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

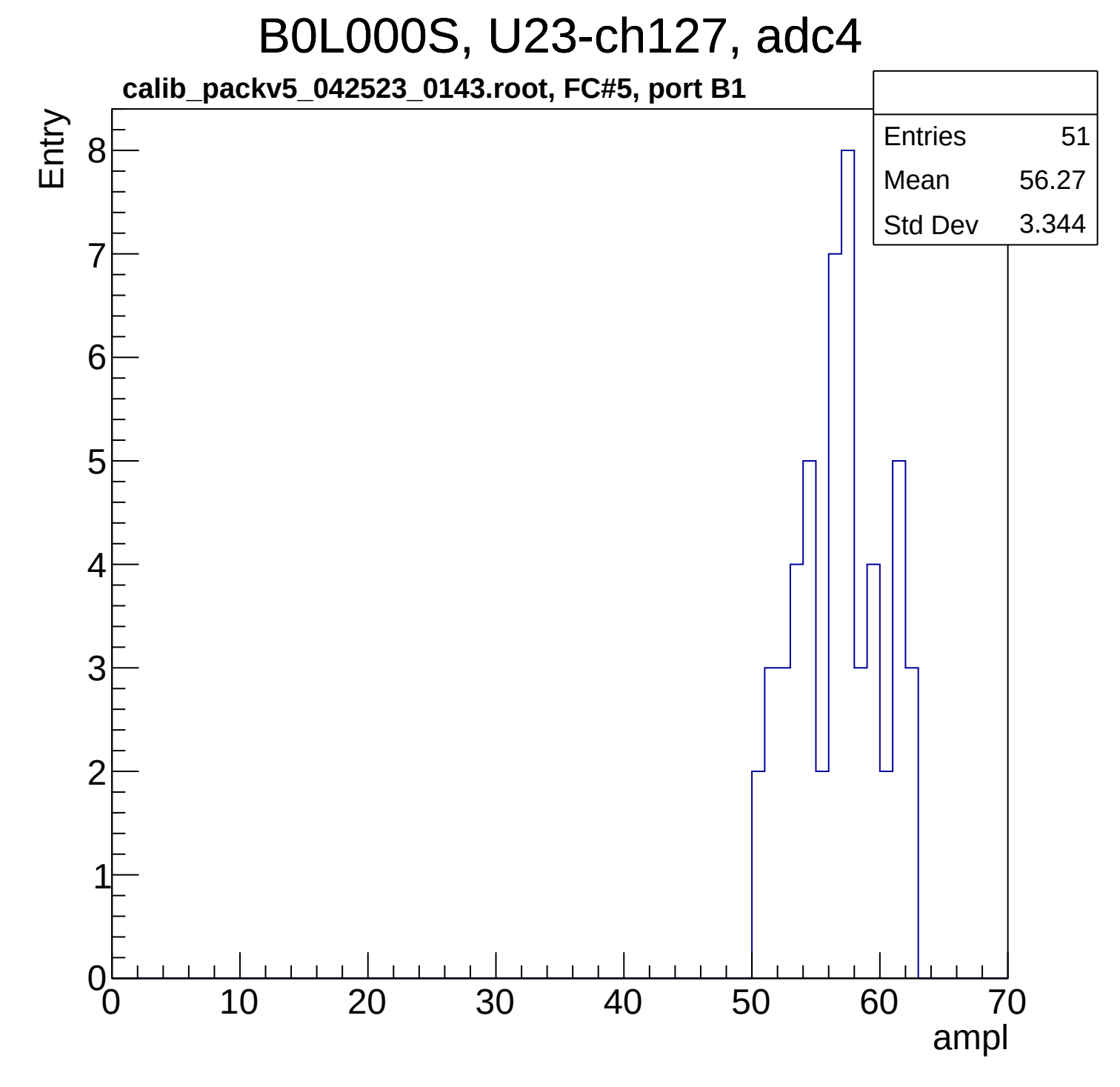

B0L000S, U23-ch126, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

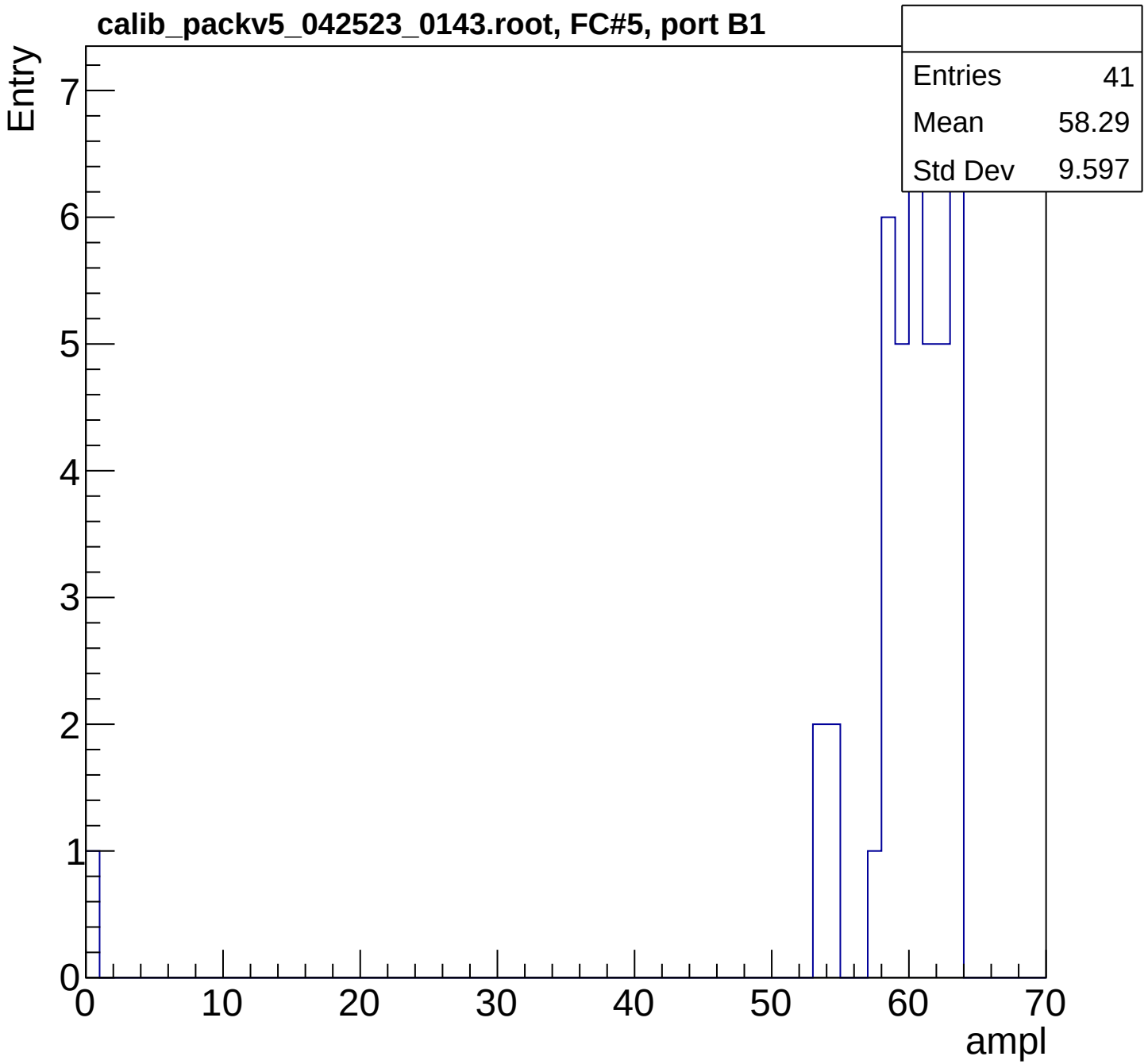

B0L000S, U23-ch127, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 30.23
Std Dev 3.584
Gaus mean : 30.6550
Gaus Width: 3.5627
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

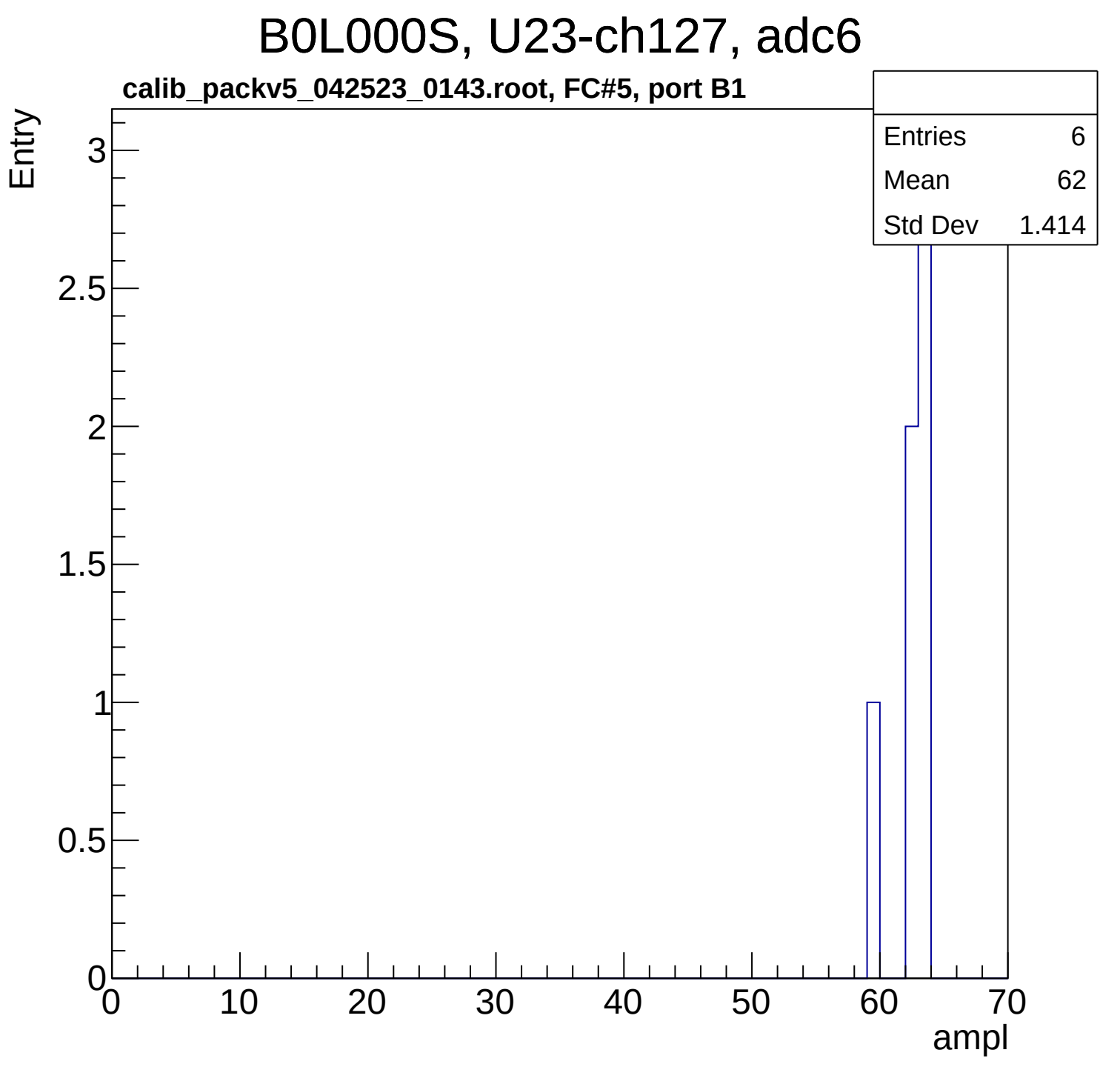

B0L000S, U23-ch127, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.89
Std Dev 2.77
Gaus mean : 37.3683
Gaus Width: 3.7983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

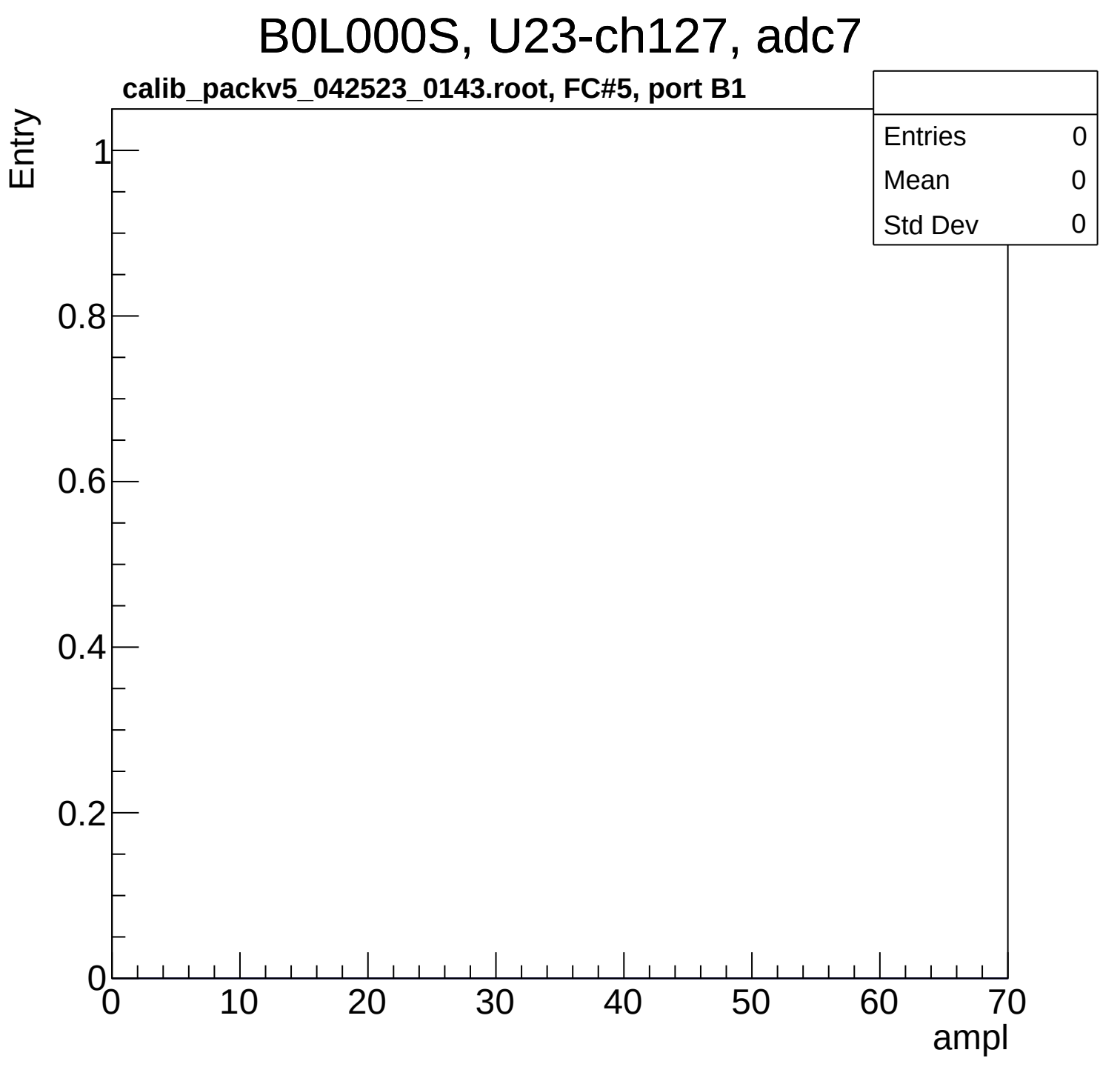

B0L000S, U23-ch127, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 42.93
Std Dev 3.655
Gaus mean : 43.4208
Gaus Width: 3.9113
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

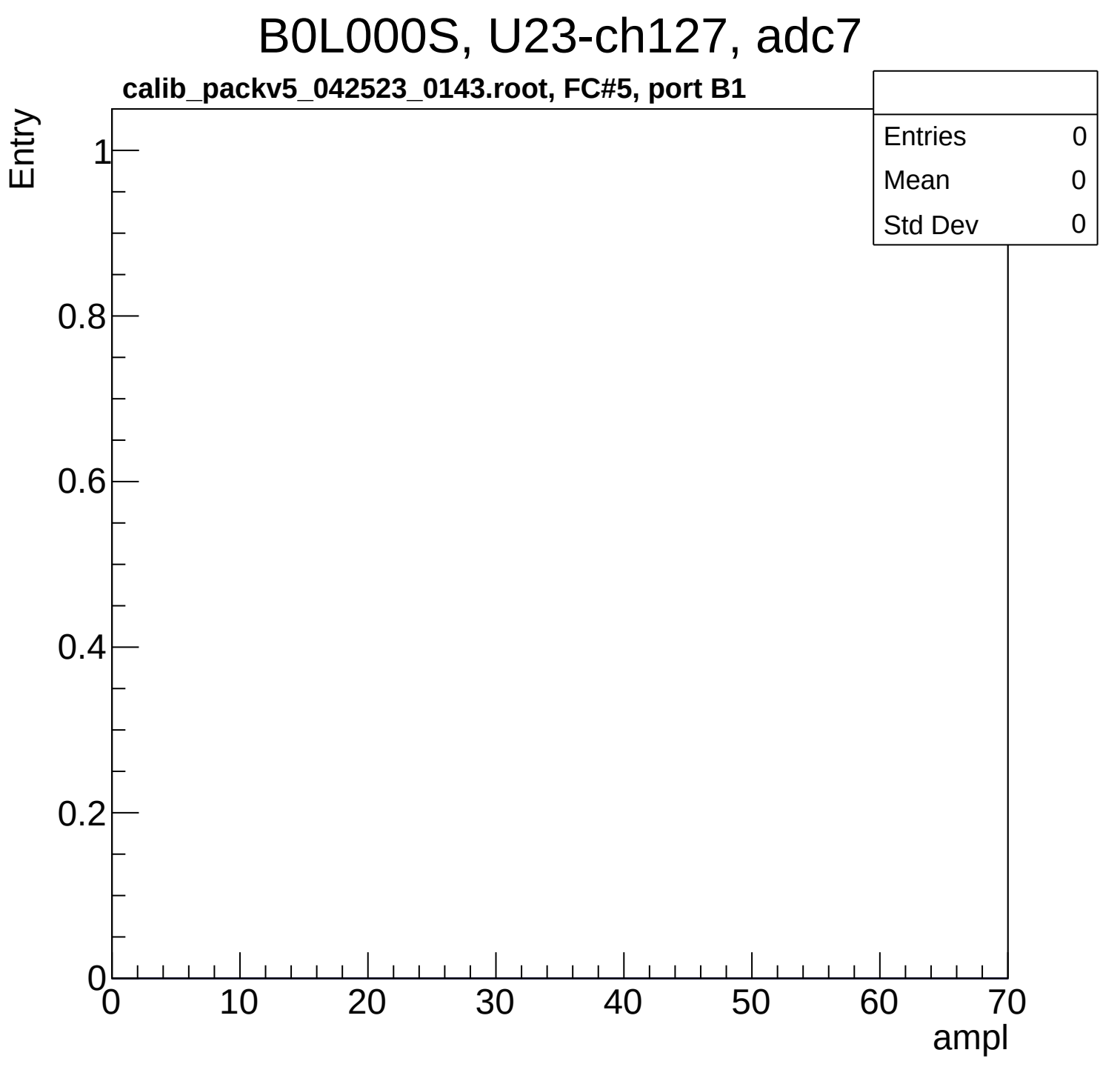

B0L000S, U23-ch127, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 49.8
Std Dev 4.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U23-ch127, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.27
Std Dev 3.344
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U23-ch127, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.29
Std Dev 9.597
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U23-ch127, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62
Std Dev 1.414
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl