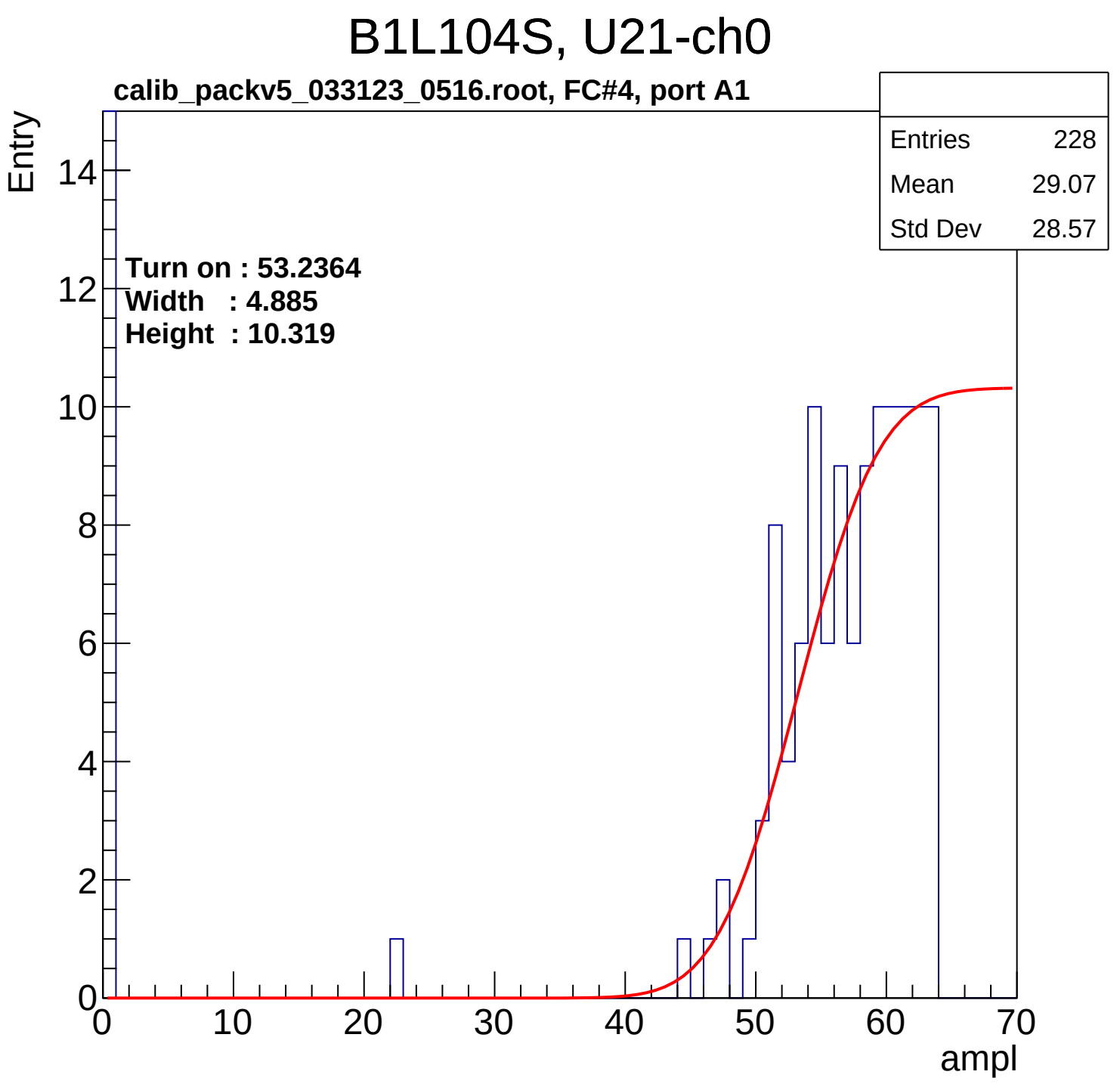
B1L104S, U21-ch0
calib_packv5_033123_0516.root, FC#4, port A1
Entries 228
Mean 29.07
Std Dev 28.57
Turn on : 53.2364
Width : 4.885
Height : 10.319
Entry
ampl
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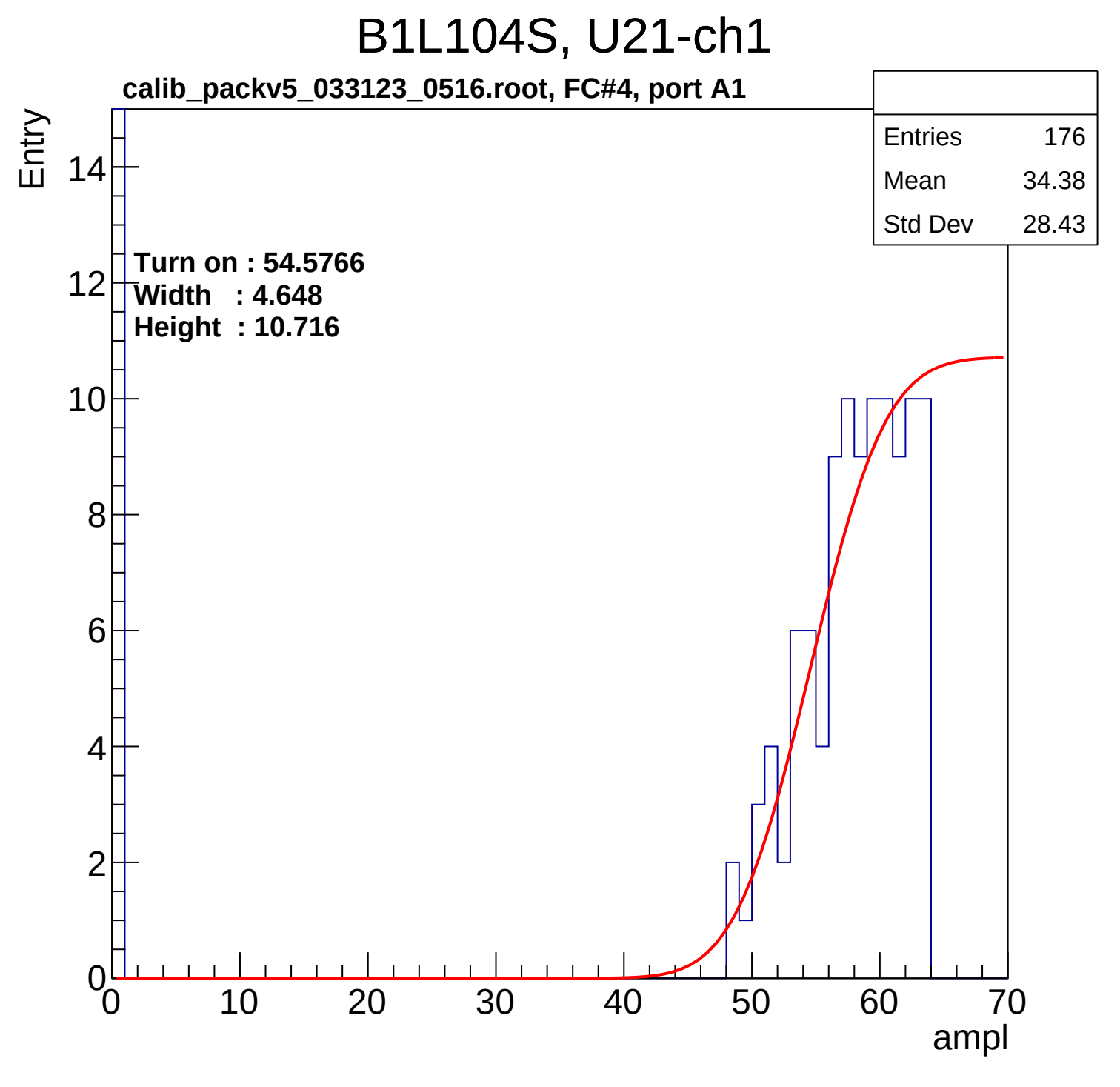
B1L104S, U21-ch1
calib_packv5_033123_0516.root, FC#4, port A1
Entries 176
Mean 34.38
Std Dev 28.43
Turn on : 54.5766
Width : 4.648
Height : 10.716
Entry
ampl
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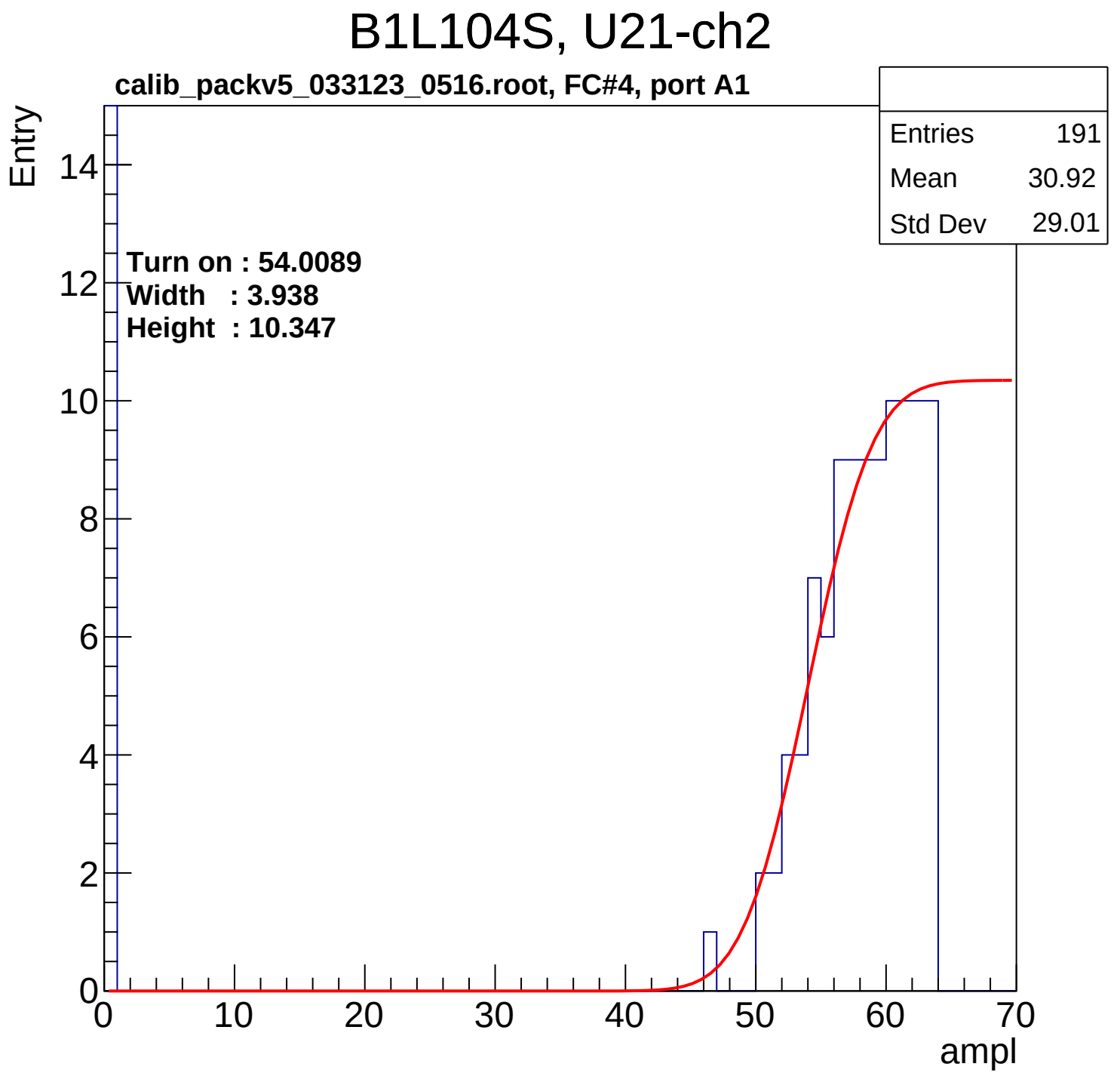
B1L104S, U21-ch2
calib_packv5_033123_0516.root, FC#4, port A1
Entries 191
Mean 30.92
Std Dev 29.01
Turn on : 54.0089
Width : 3.938
Height : 10.347
Entry
ampl
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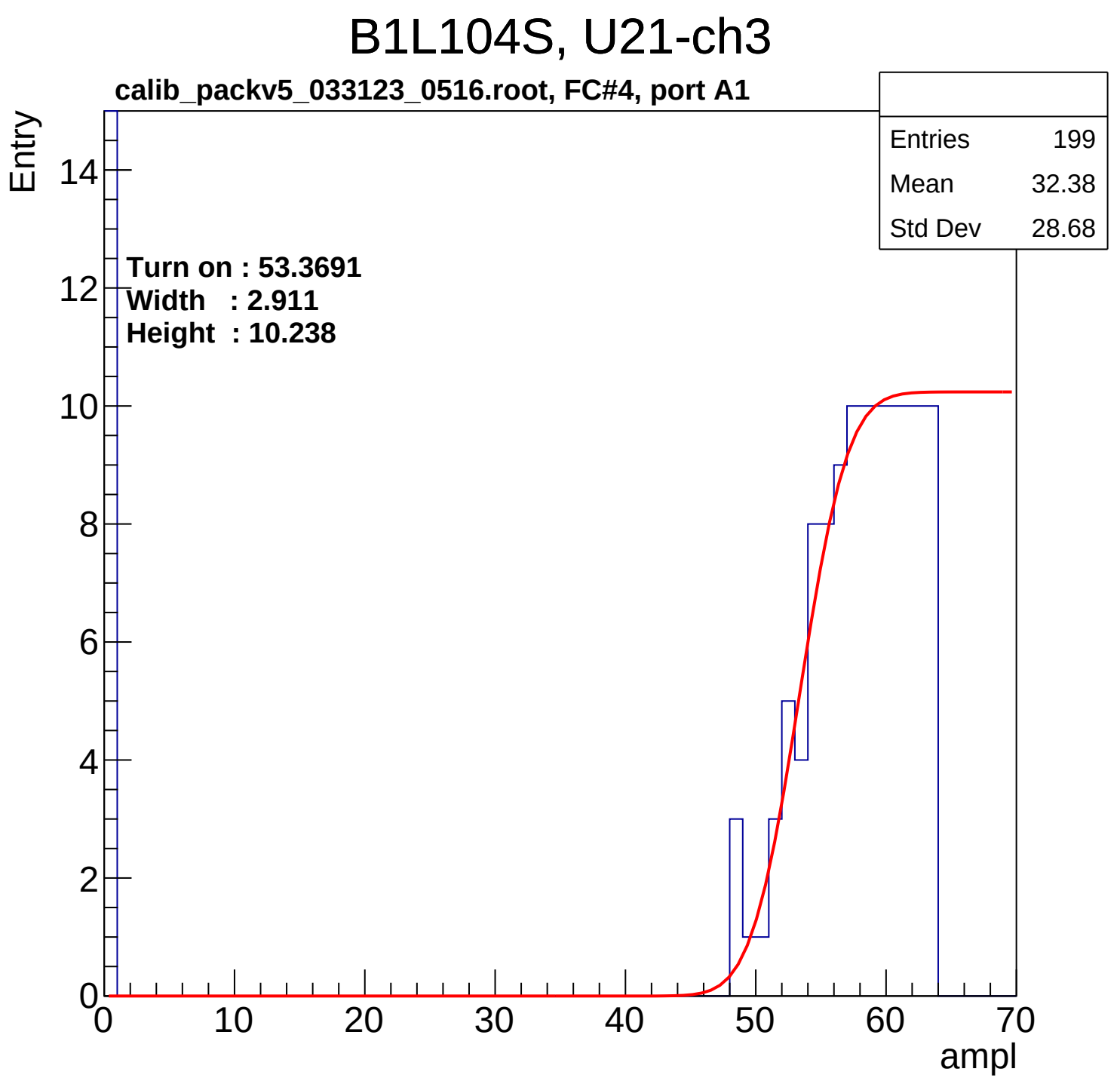
B1L104S, U21-ch3
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 32.38
Std Dev 28.68
Turn on : 53.3691
Width : 2.911
Height : 10.238
Entry
ampl
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2
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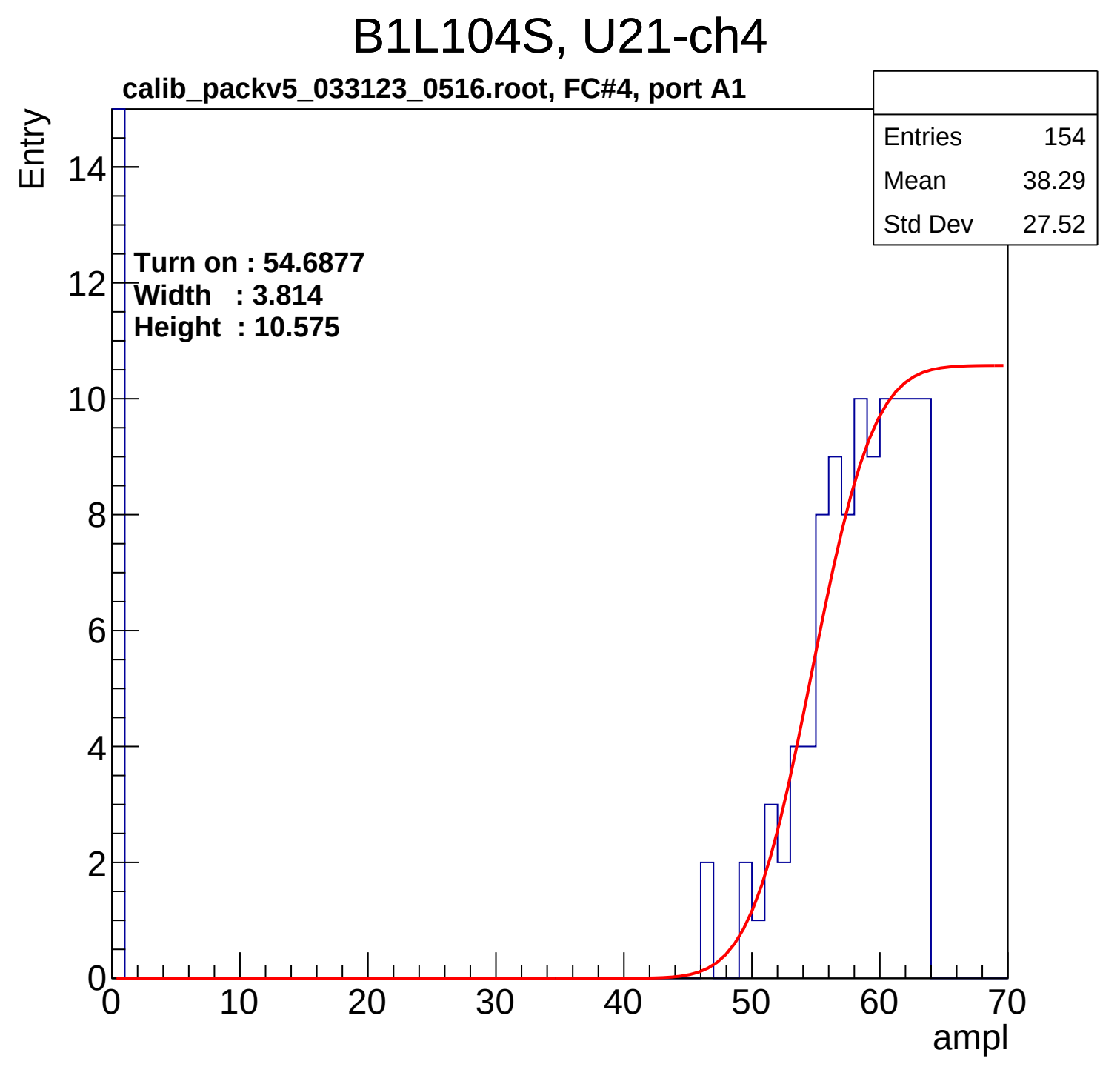
B1L104S, U21-ch4
calib_packv5_033123_0516.root, FC#4, port A1
Entries 154
Mean 38.29
Std Dev 27.52
Turn on : 54.6877
Width : 3.814
Height : 10.575
Entry
ampl
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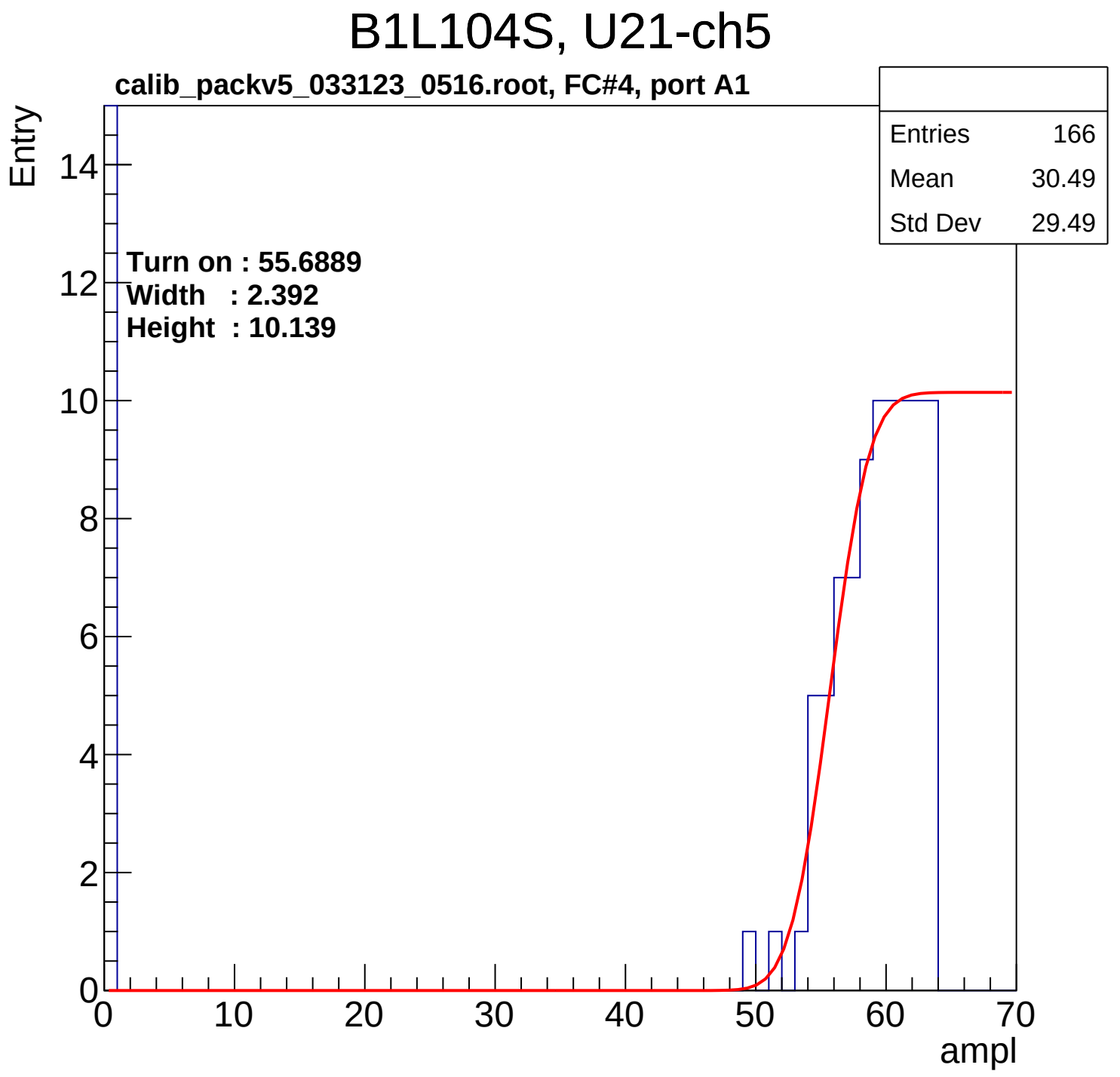
B1L104S, U21-ch5
calib_packv5_033123_0516.root, FC#4, port A1
Entries 166
Mean 30.49
Std Dev 29.49
Turn on : 55.6889
Width : 2.392
Height : 10.139
Entry
ampl
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2
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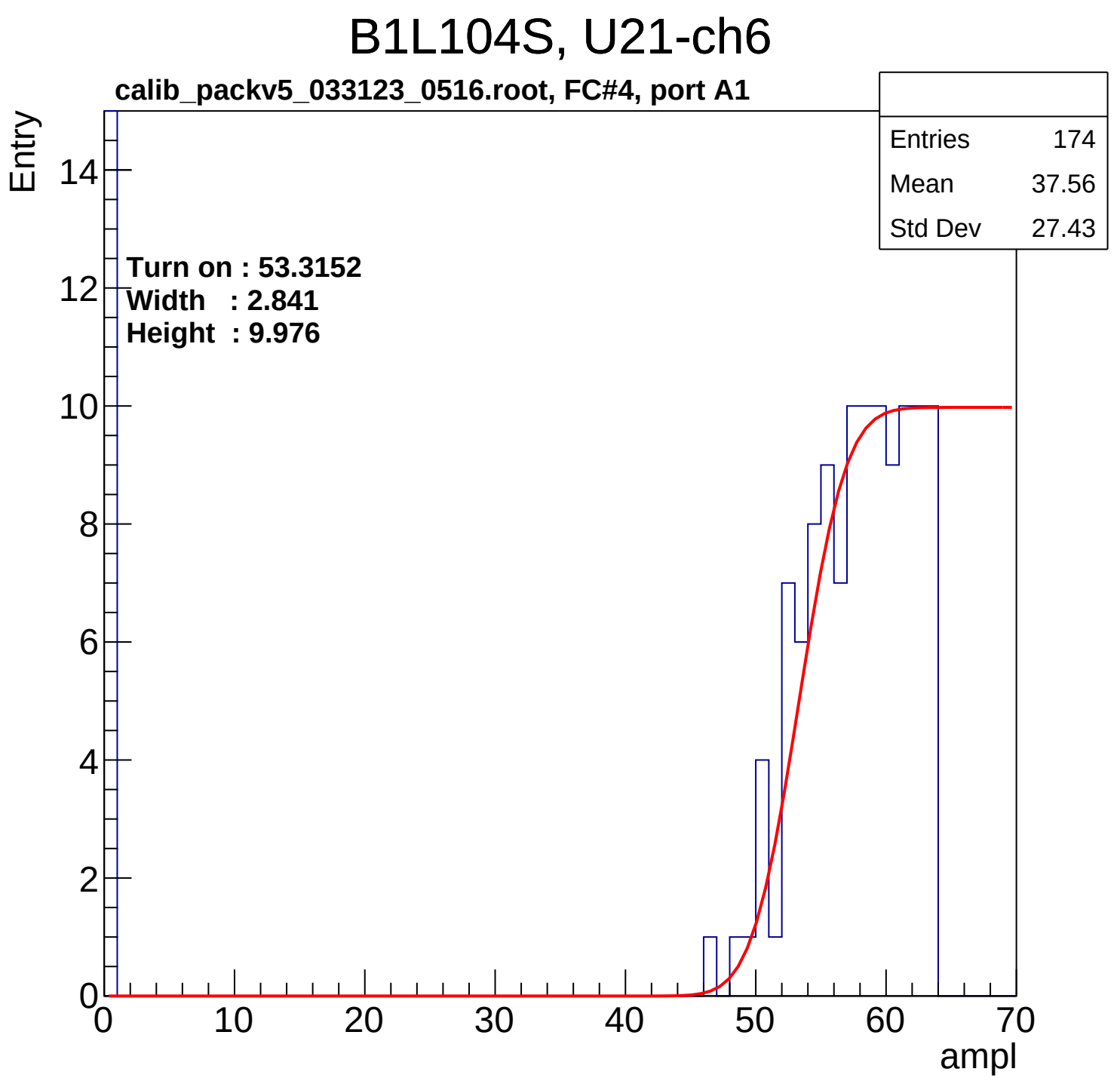
B1L104S, U21-ch6
calib_packv5_033123_0516.root, FC#4, port A1
Entries 174
Mean 37.56
Std Dev 27.43
Turn on : 53.3152
Width : 2.841
Height : 9.976
Entry
ampl
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2
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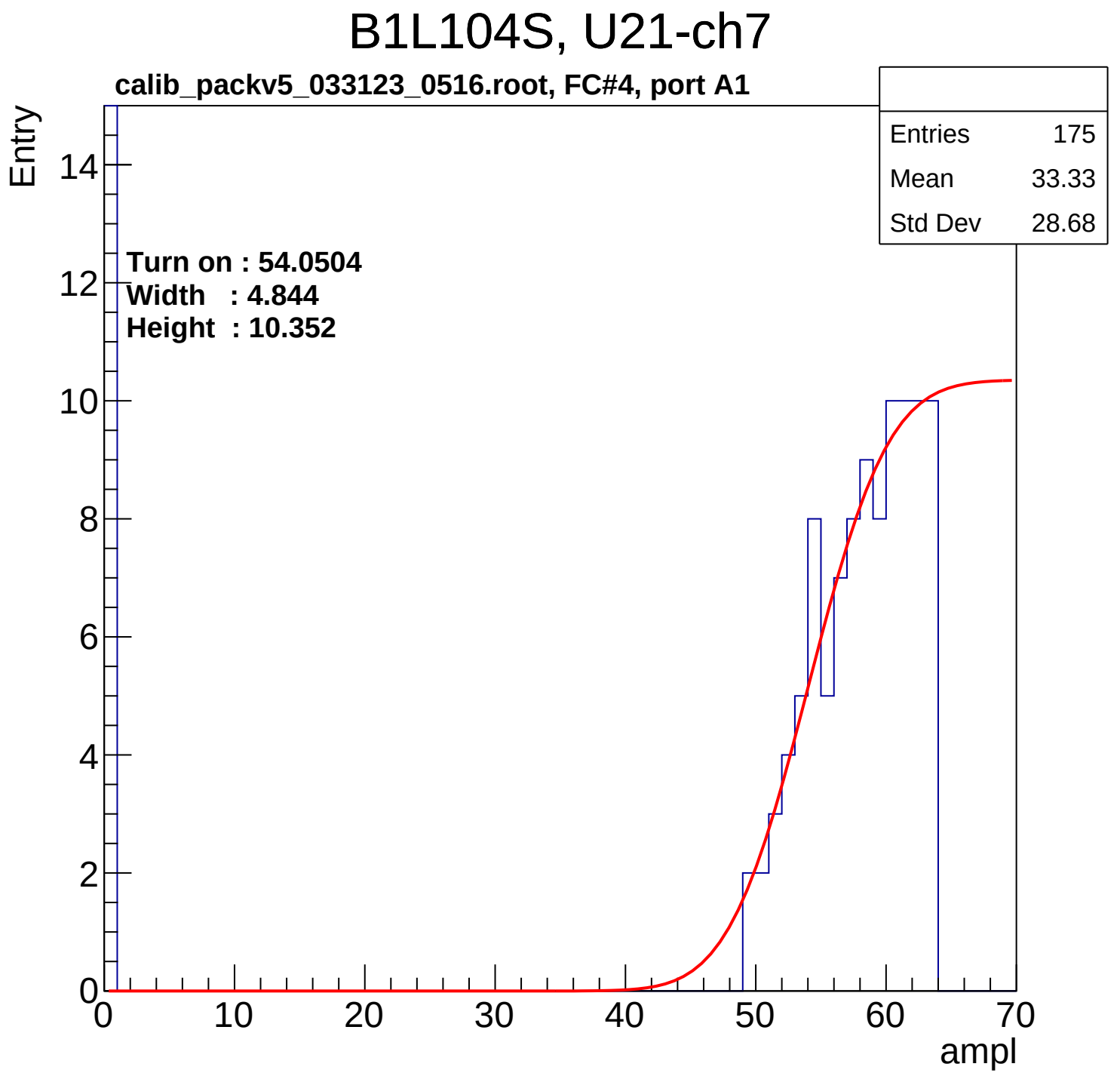
B1L104S, U21-ch7
calib_packv5_033123_0516.root, FC#4, port A1
Entries 175
Mean 33.33
Std Dev 28.68
Turn on : 54.0504
Width : 4.844
Height : 10.352
Entry
ampl
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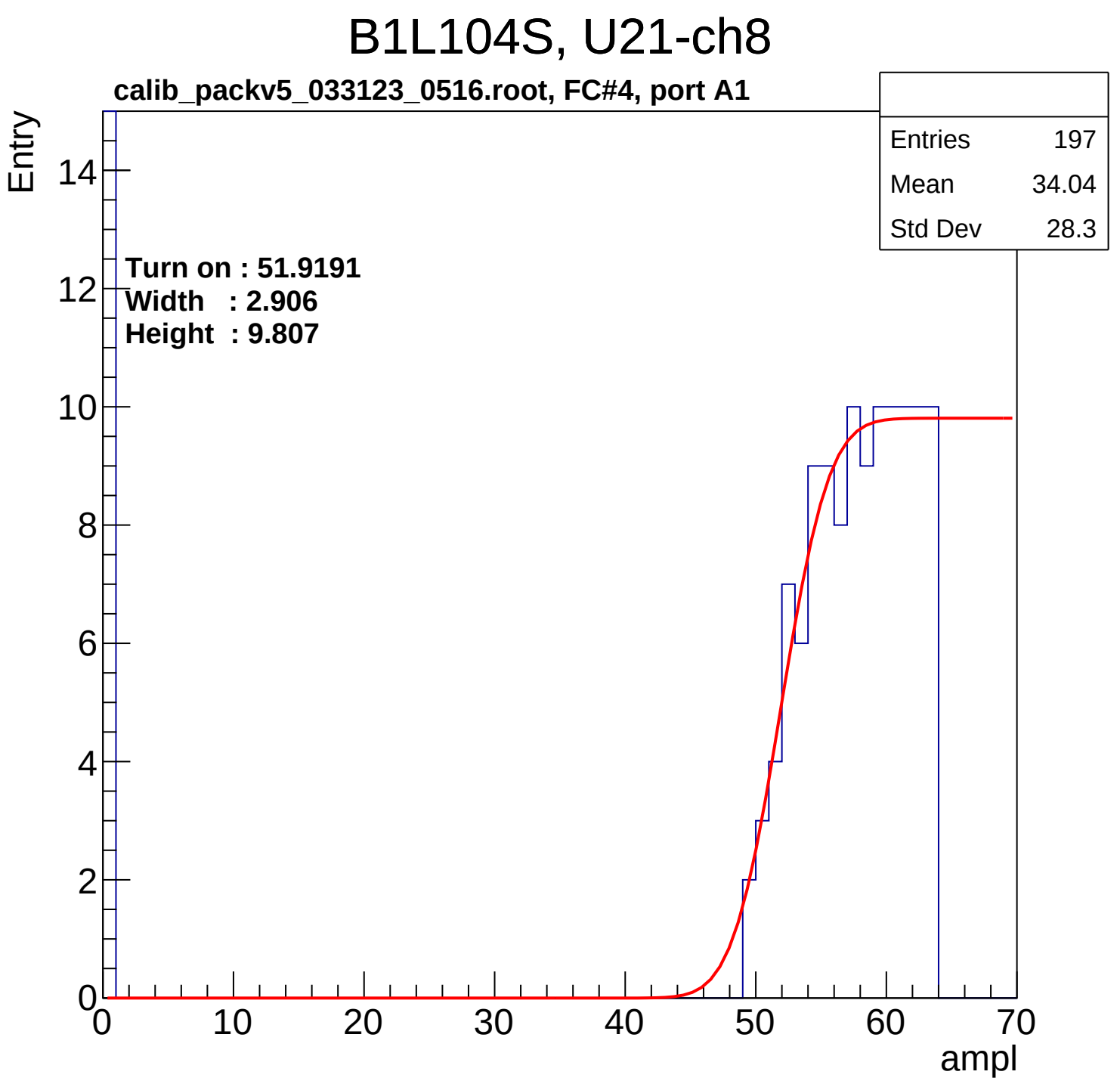
B1L104S, U21-ch8
calib_packv5_033123_0516.root, FC#4, port A1
Entries 197
Mean 34.04
Std Dev 28.3
Turn on : 51.9191
Width : 2.906
Height : 9.807
Entry
ampl
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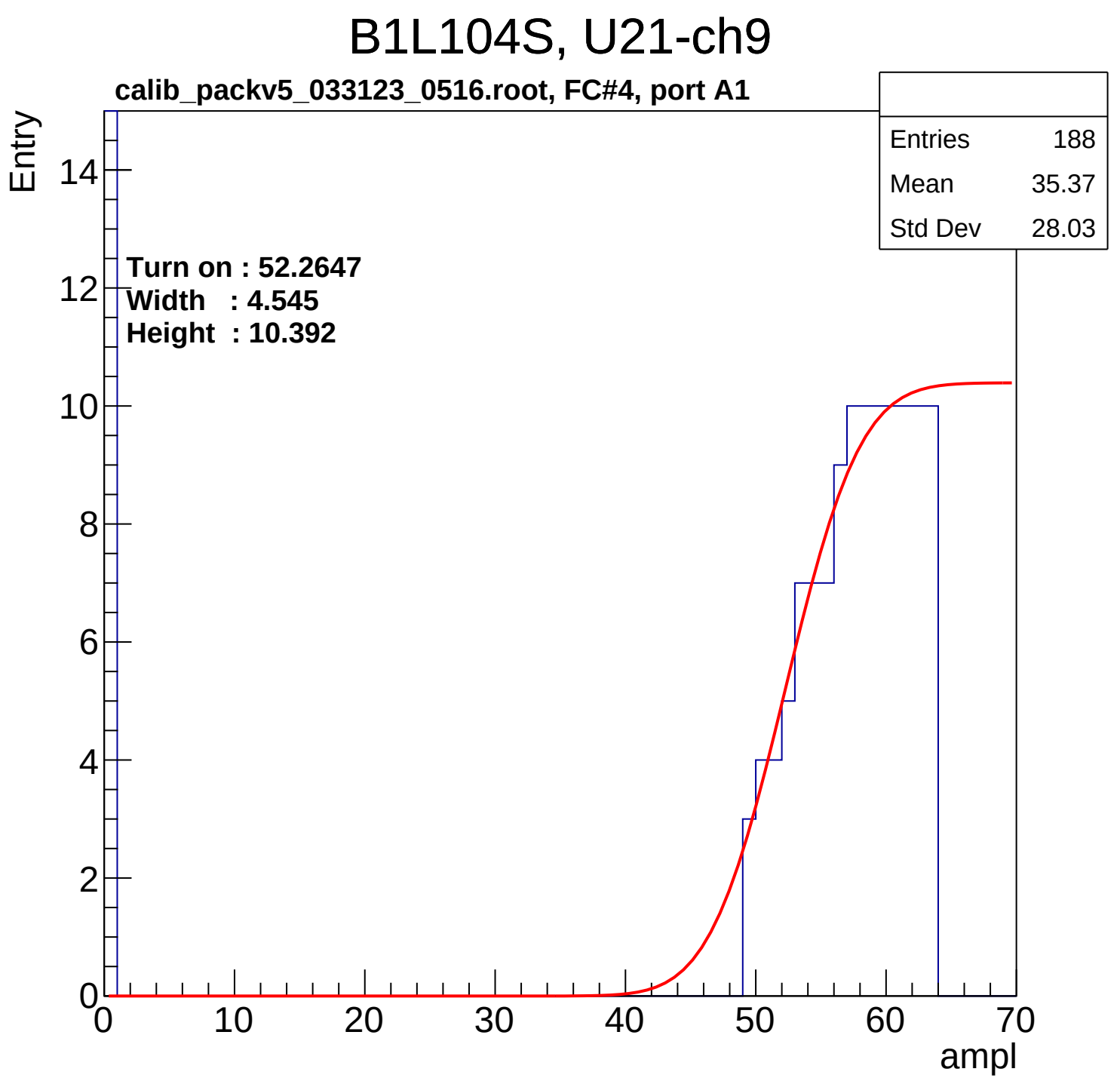
B1L104S, U21-ch9
calib_packv5_033123_0516.root, FC#4, port A1
Entries 188
Mean 35.37
Std Dev 28.03
Turn on : 52.2647
Width : 4.545
Height : 10.392
Entry
ampl
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4
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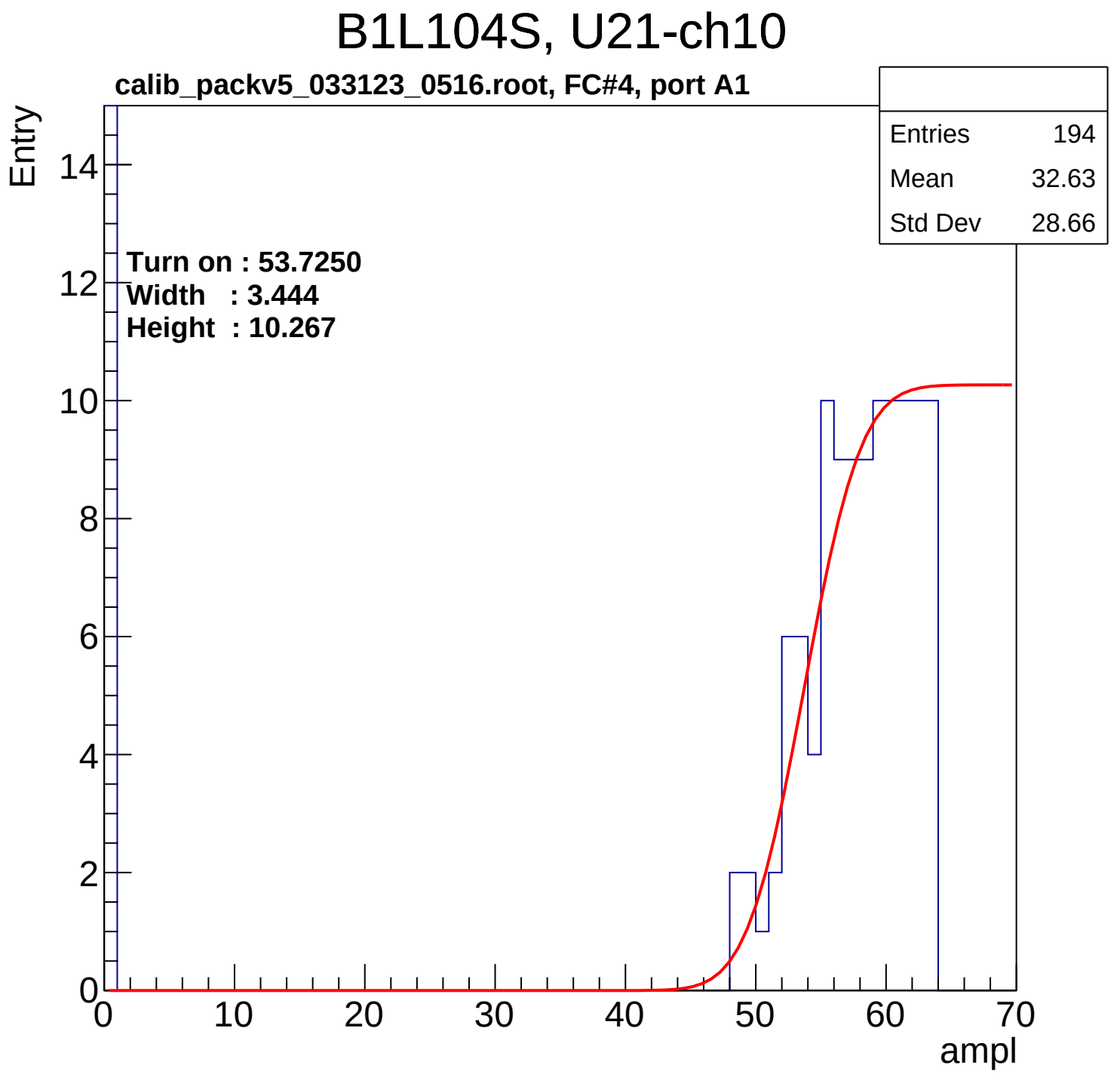
B1L104S, U21-ch10
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 32.63
Std Dev 28.66
Turn on : 53.7250
Width : 3.444
Height : 10.267
Entry
ampl
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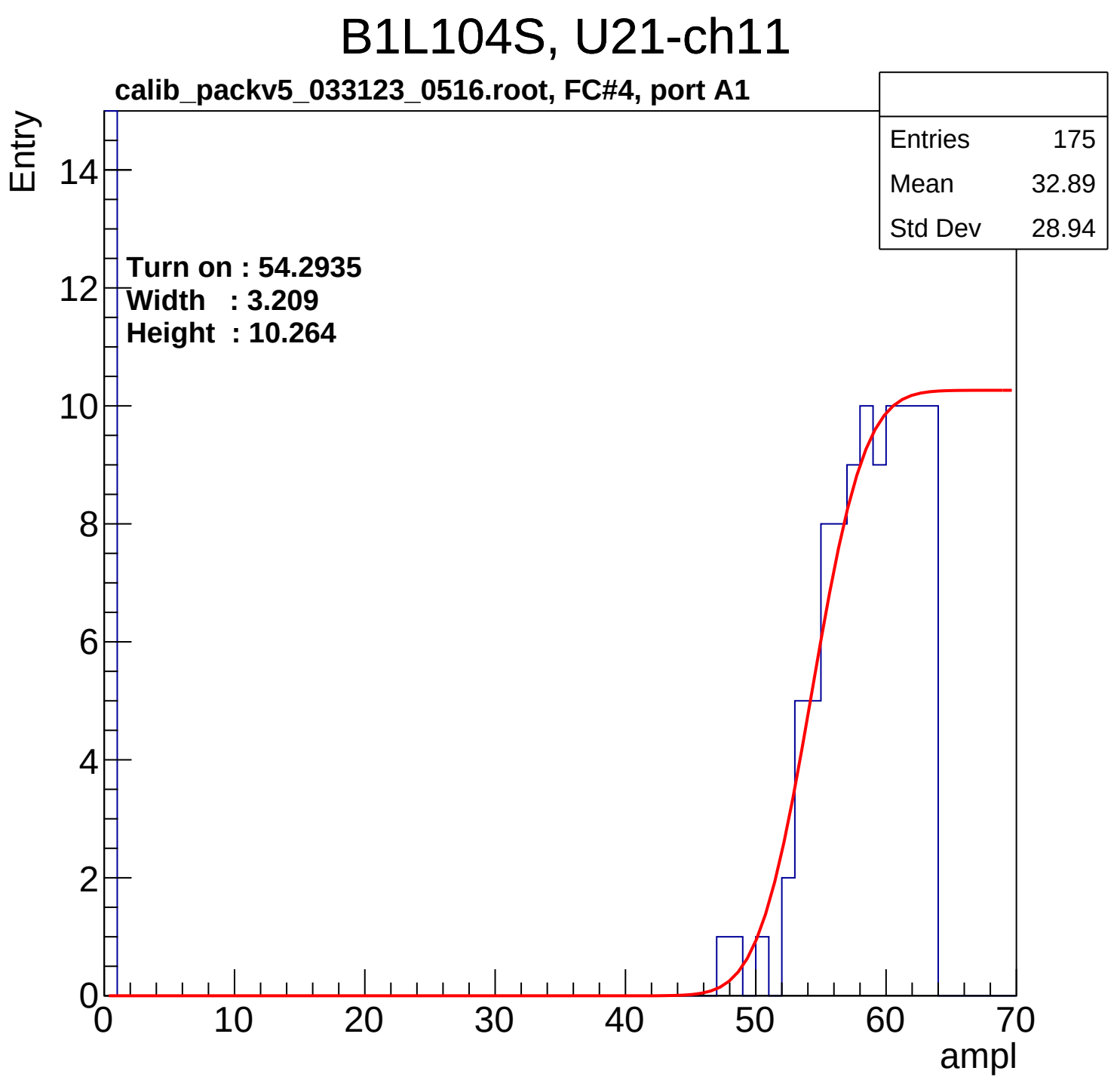
B1L104S, U21-ch11
calib_packv5_033123_0516.root, FC#4, port A1
Entries 175
Mean 32.89
Std Dev 28.94
Turn on : 54.2935
Width : 3.209
Height : 10.264
Entry
ampl
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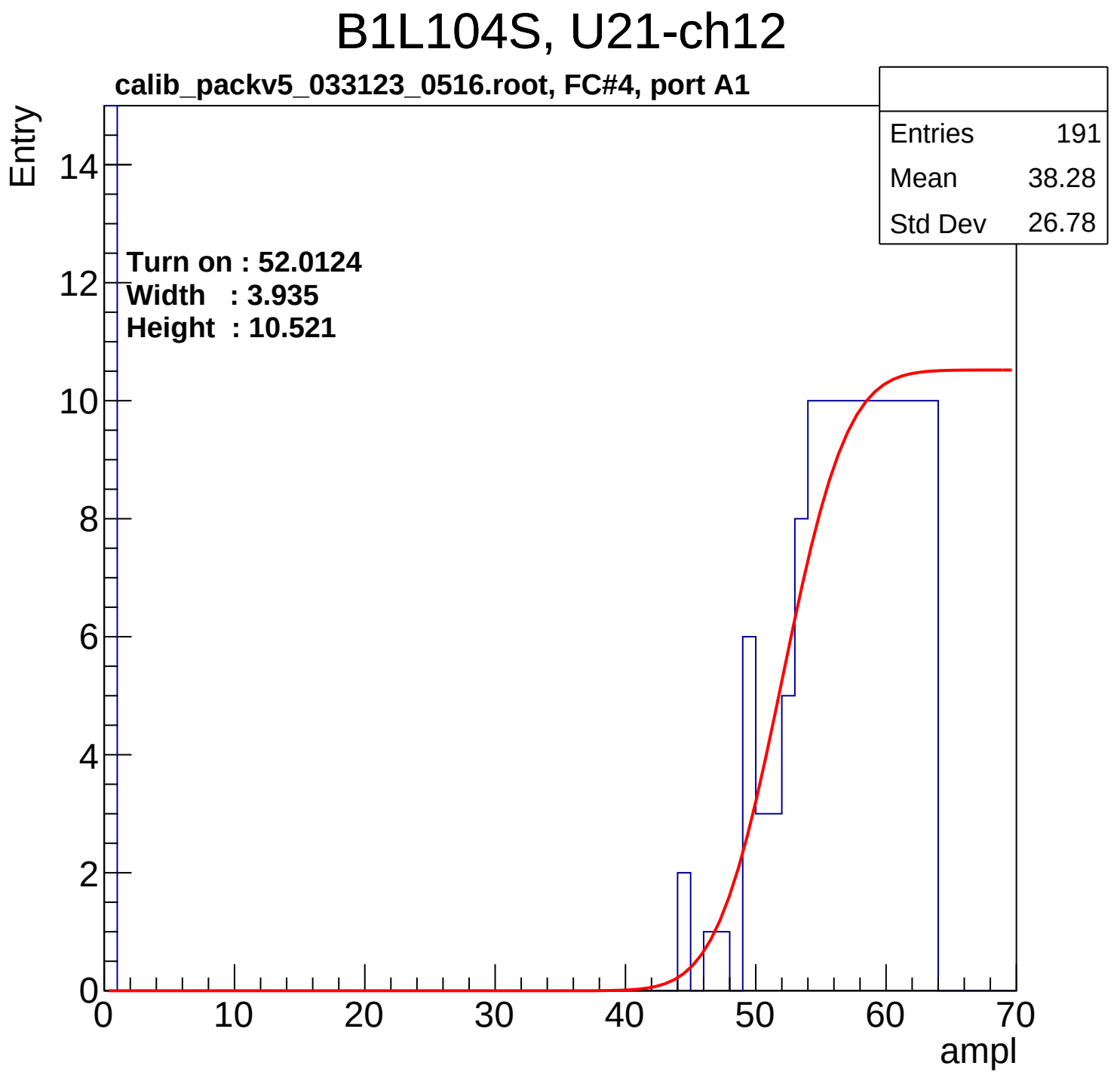
B1L104S, U21-ch12
calib_packv5_033123_0516.root, FC#4, port A1
Entries 191
Mean 38.28
Std Dev 26.78
Turn on : 52.0124
Width : 3.935
Height : 10.521
Entry
ampl
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8
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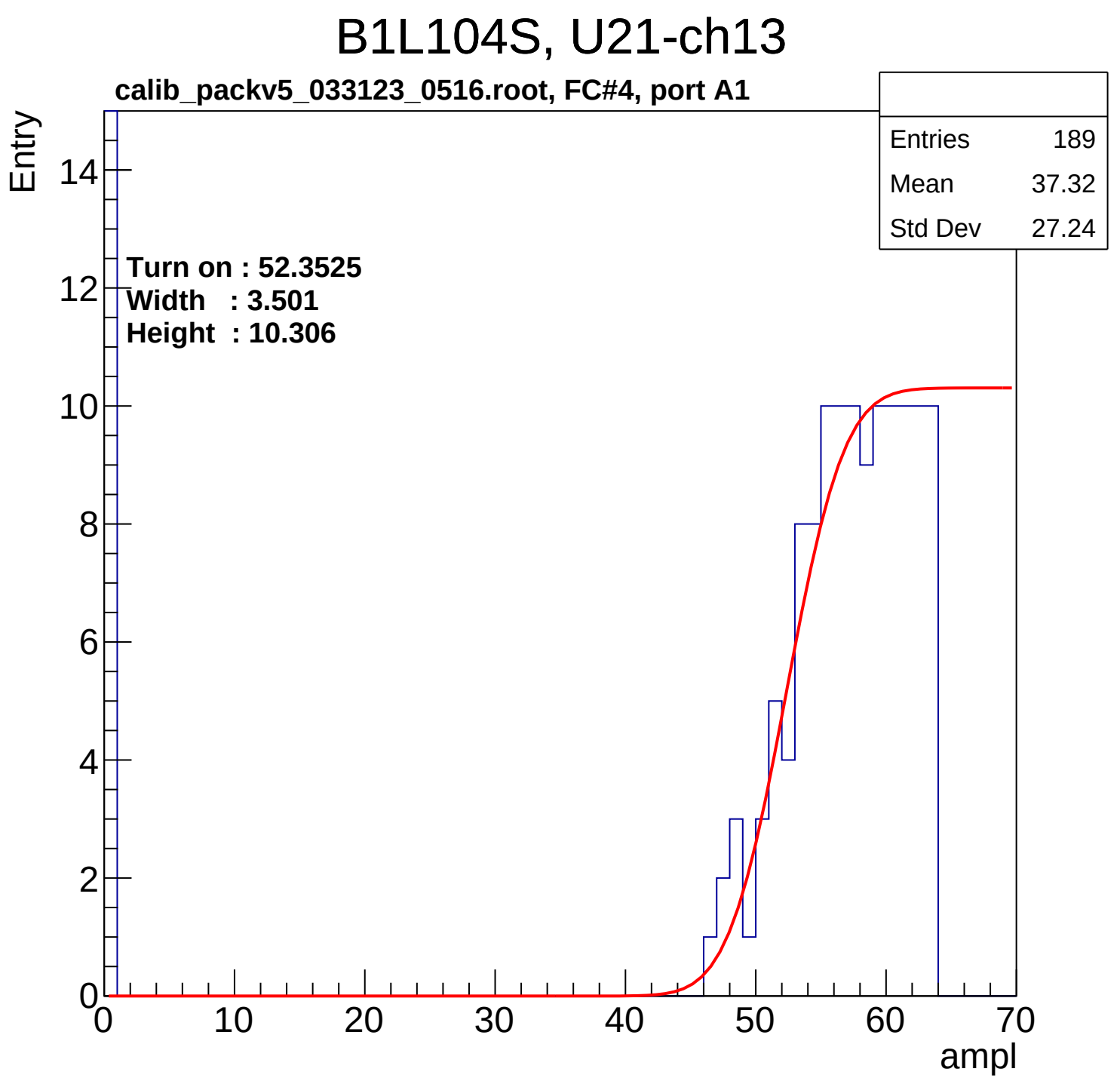
B1L104S, U21-ch13
calib_packv5_033123_0516.root, FC#4, port A1
Entries 189
Mean 37.32
Std Dev 27.24
Turn on : 52.3525
Width : 3.501
Height : 10.306
Entry
ampl
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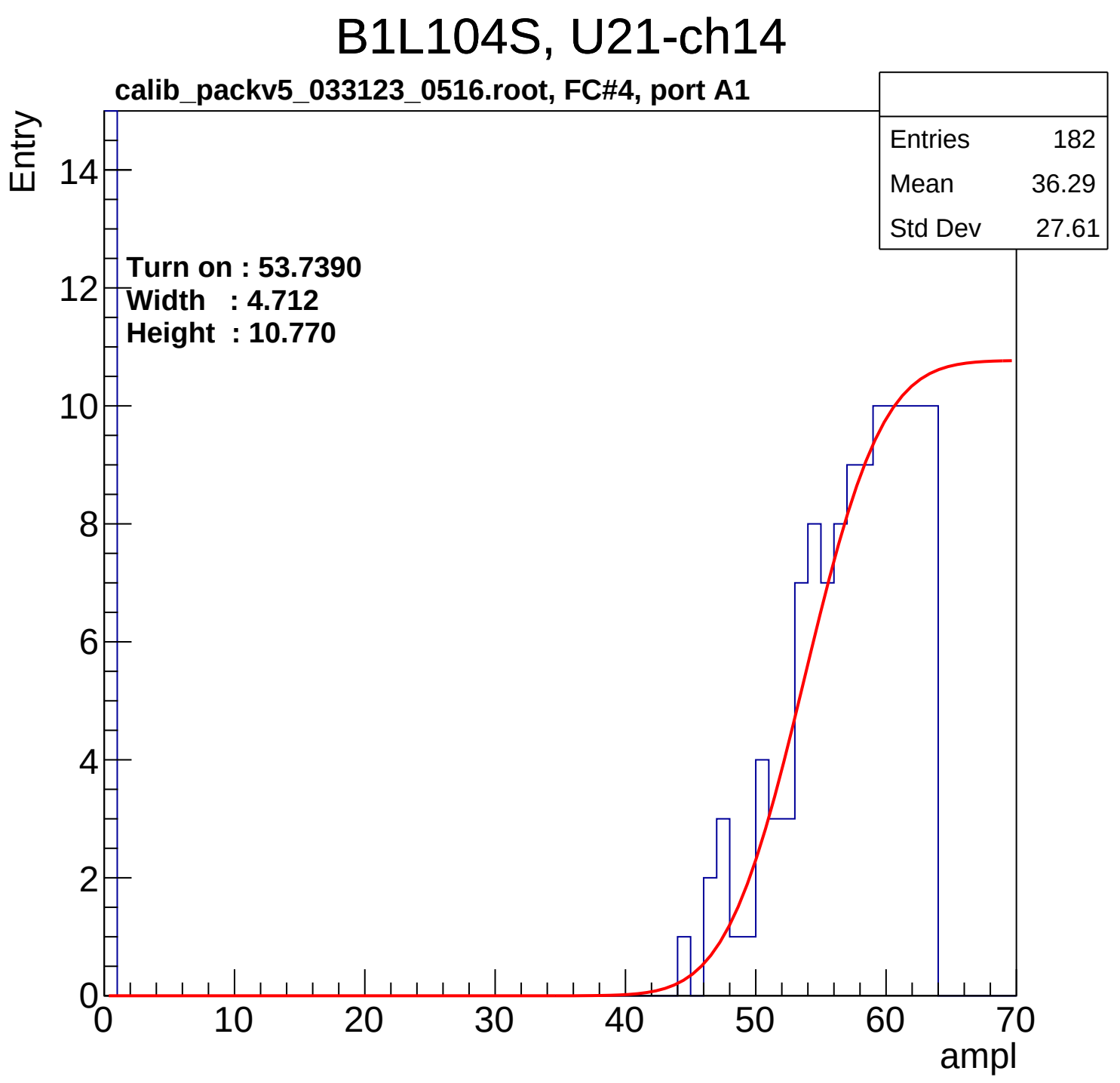
B1L104S, U21-ch14
calib_packv5_033123_0516.root, FC#4, port A1
Entries 182
Mean 36.29
Std Dev 27.61
Turn on : 53.7390
Width : 4.712
Height : 10.770
Entry
ampl
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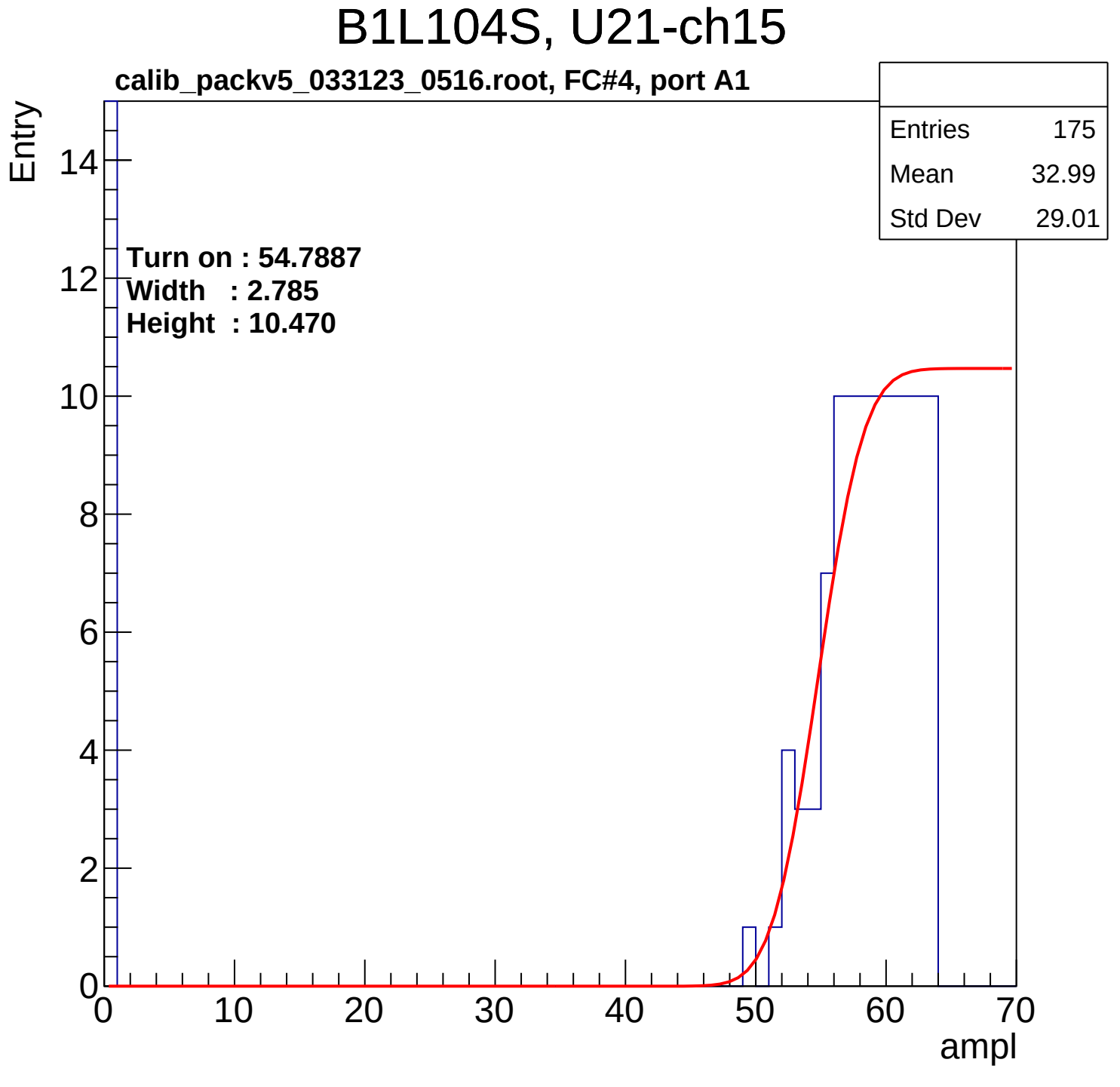
B1L104S, U21-ch15
calib_packv5_033123_0516.root, FC#4, port A1
Entries 175
Mean 32.99
Std Dev 29.01
Turn on : 54.7887
Width : 2.785
Height : 10.470
Entry
ampl
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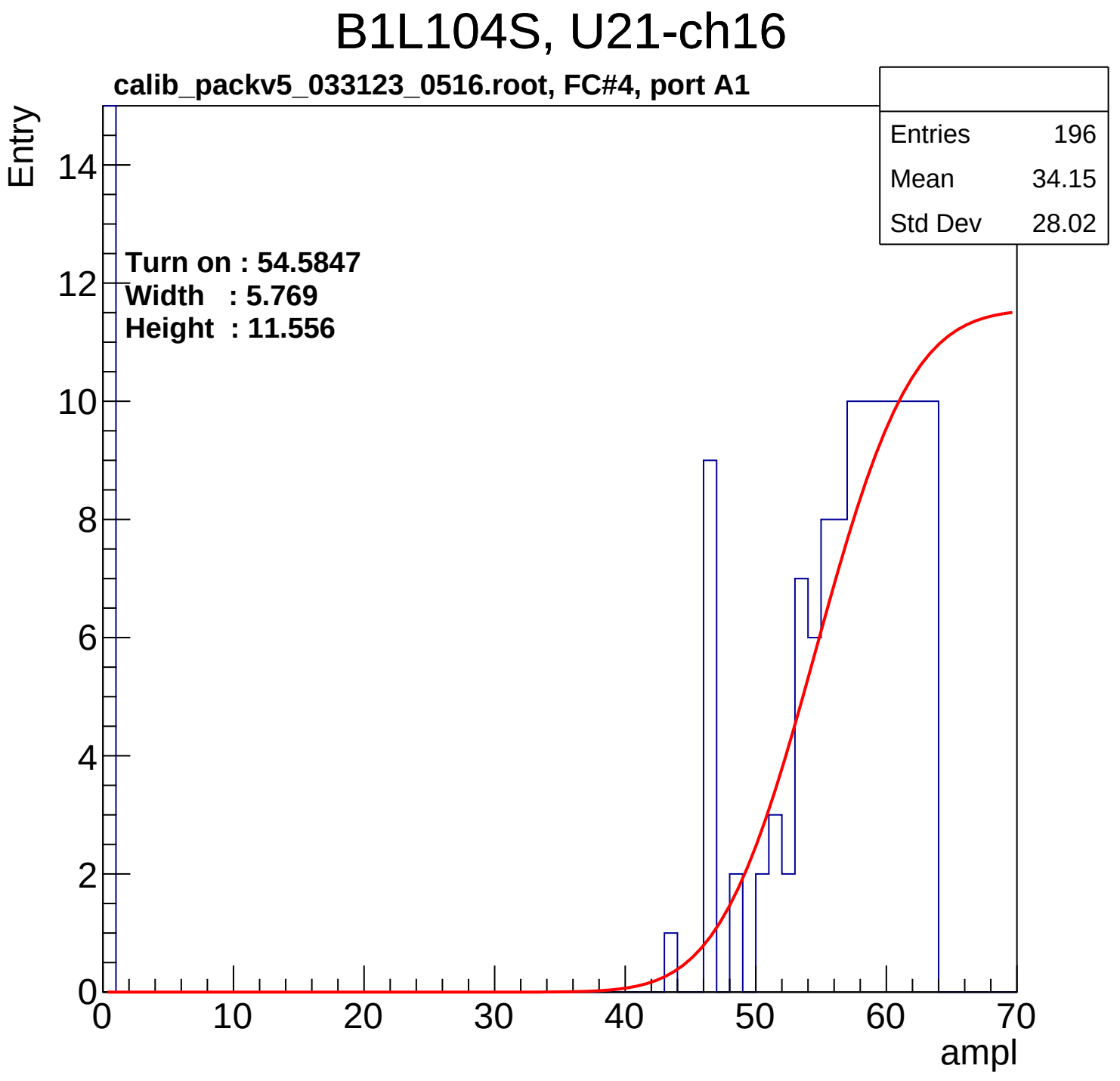
B1L104S, U21-ch16
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 34.15
Std Dev 28.02
Turn on : 54.5847
Width : 5.769
Height : 11.556
Entry
ampl
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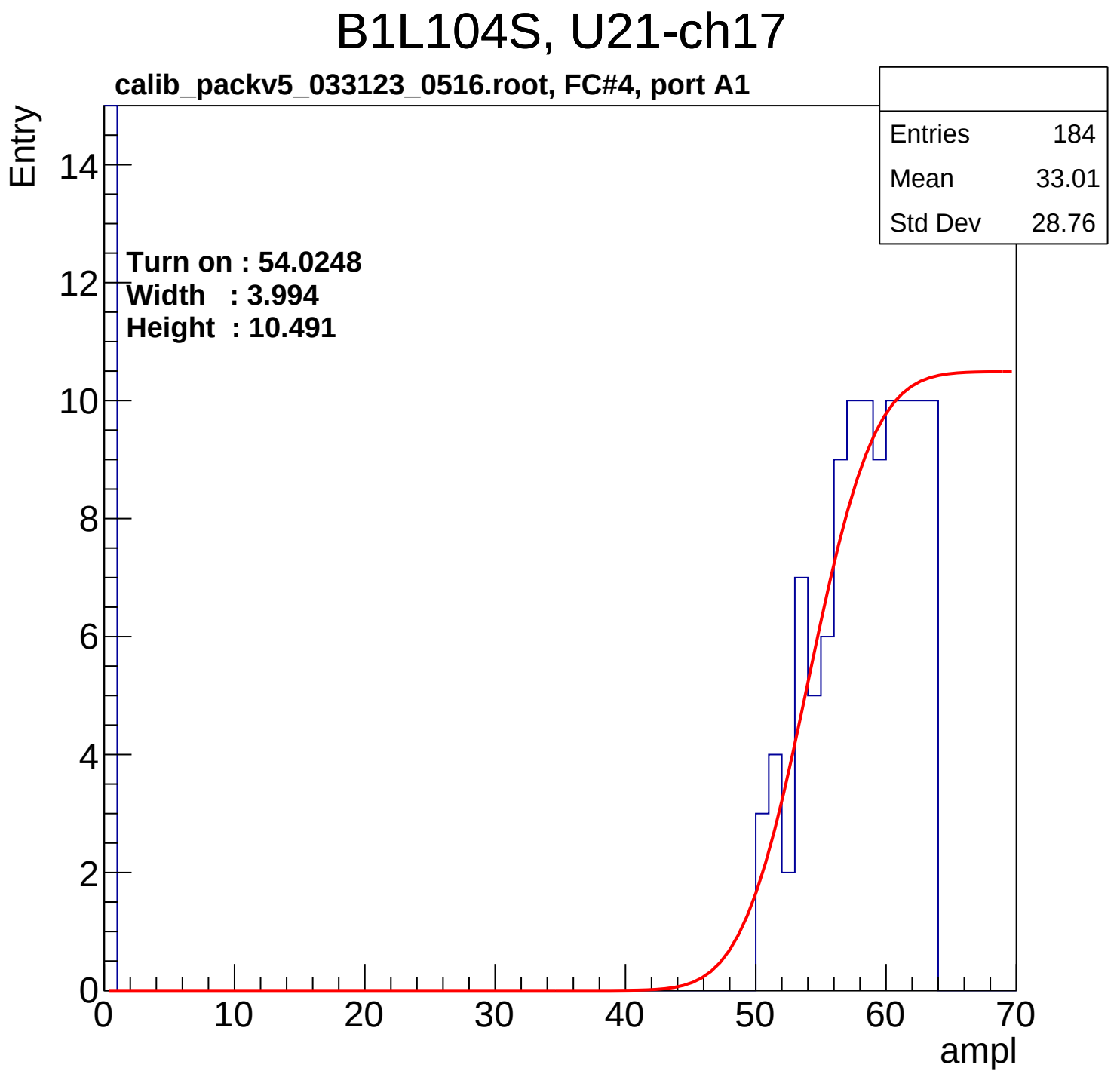
B1L104S, U21-ch17
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 33.01
Std Dev 28.76
Turn on : 54.0248
Width : 3.994
Height : 10.491
Entry
ampl
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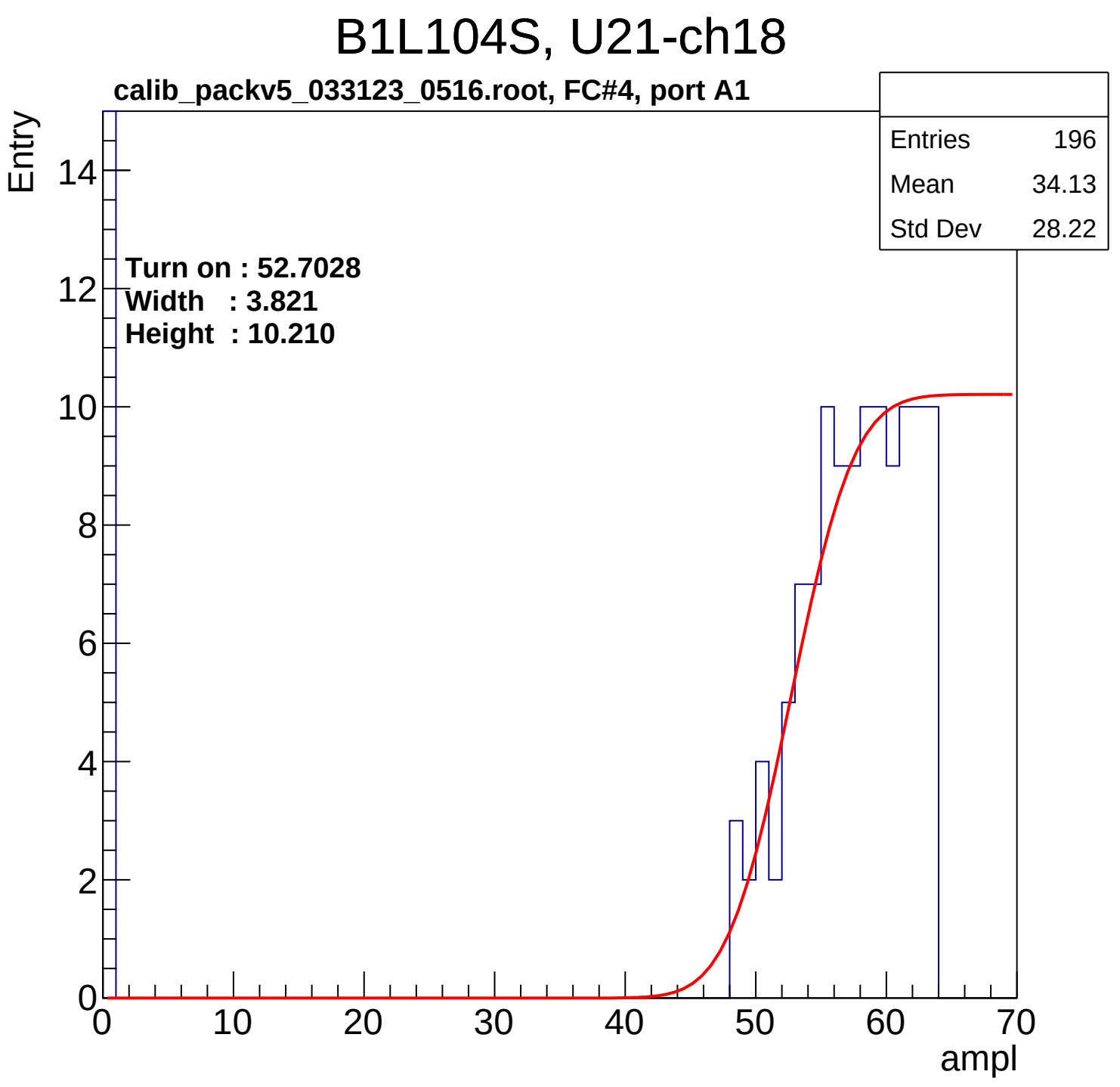
B1L104S, U21-ch18
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 34.13
Std Dev 28.22
Turn on : 52.7028
Width : 3.821
Height : 10.210
Entry
ampl
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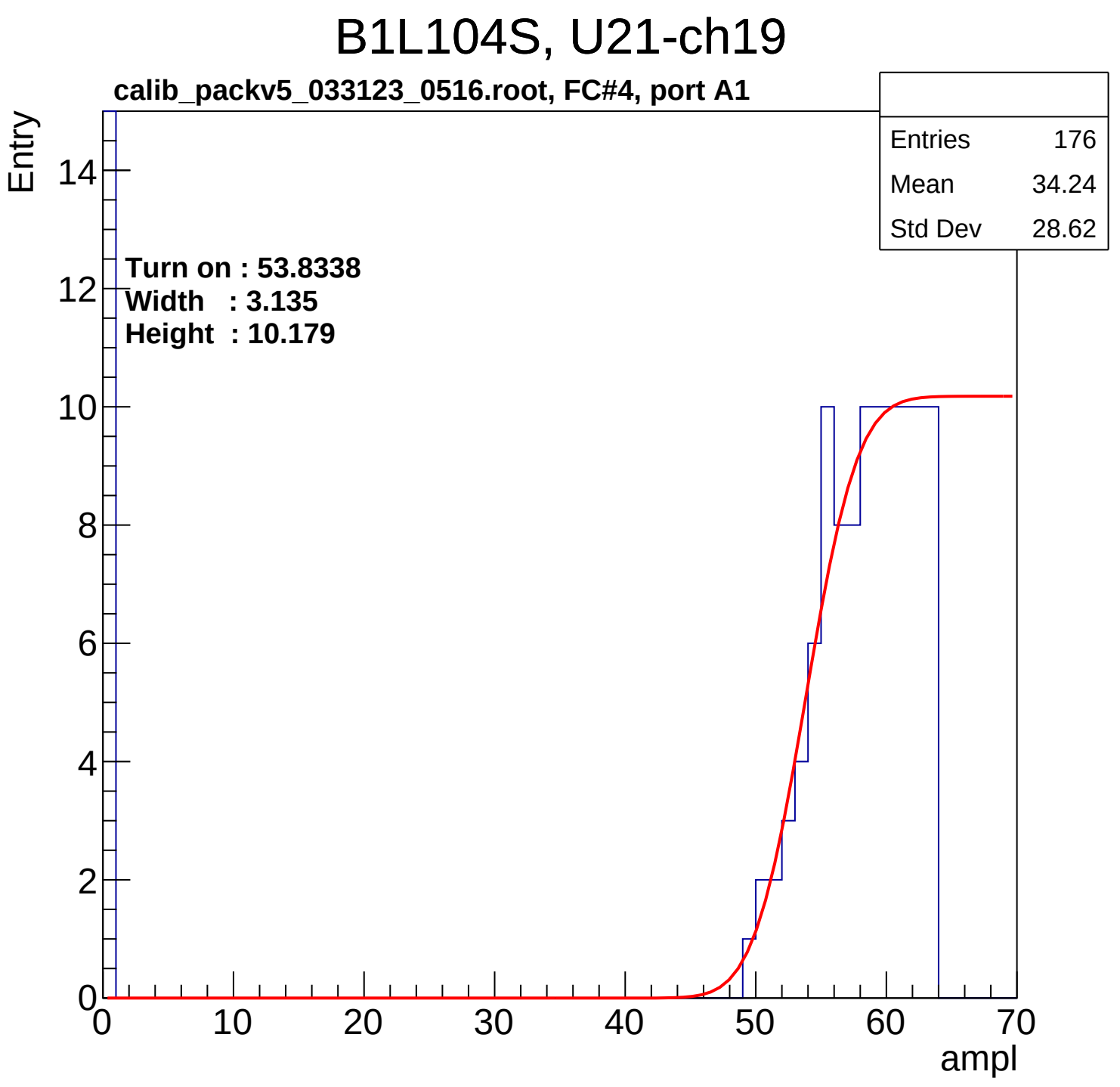
B1L104S, U21-ch19
calib_packv5_033123_0516.root, FC#4, port A1
Entries 176
Mean 34.24
Std Dev 28.62
Turn on : 53.8338
Width : 3.135
Height : 10.179
Entry
ampl
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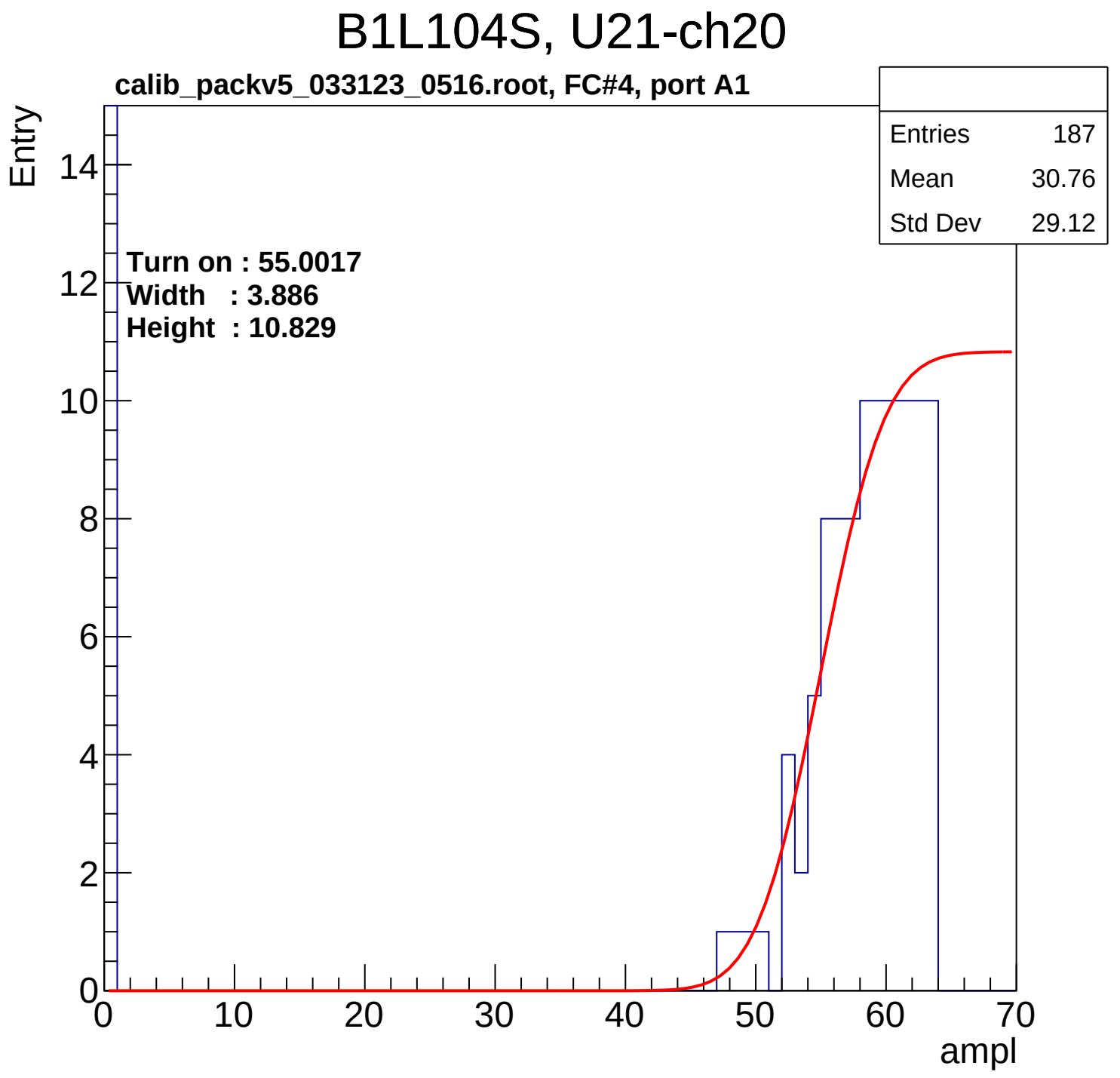
B1L104S, U21-ch20
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 30.76
Std Dev 29.12
Turn on : 55.0017
Width : 3.886
Height : 10.829
Entry
ampl
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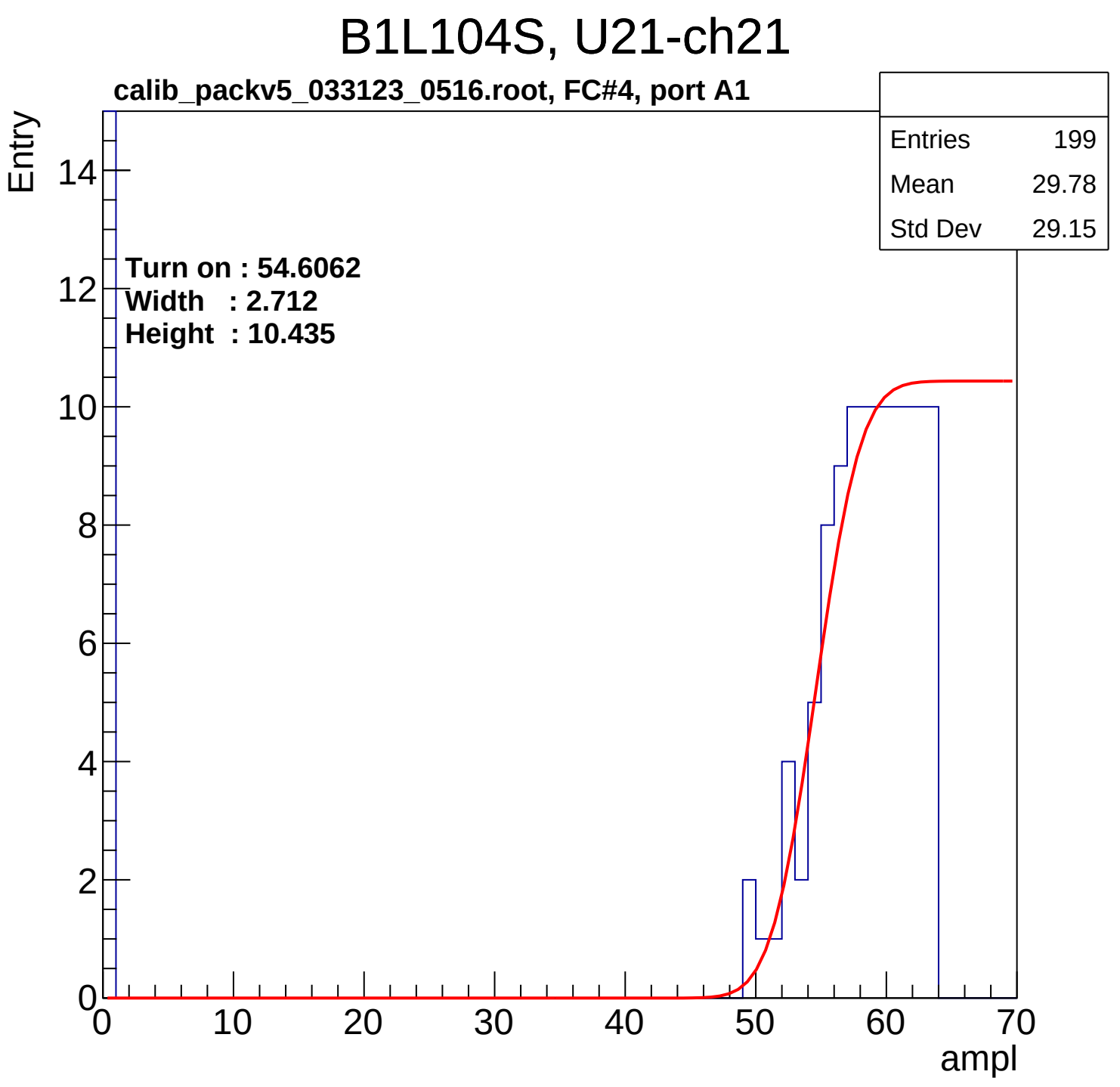
B1L104S, U21-ch21
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 29.78
Std Dev 29.15
Turn on : 54.6062
Width : 2.712
Height : 10.435
Entry
ampl
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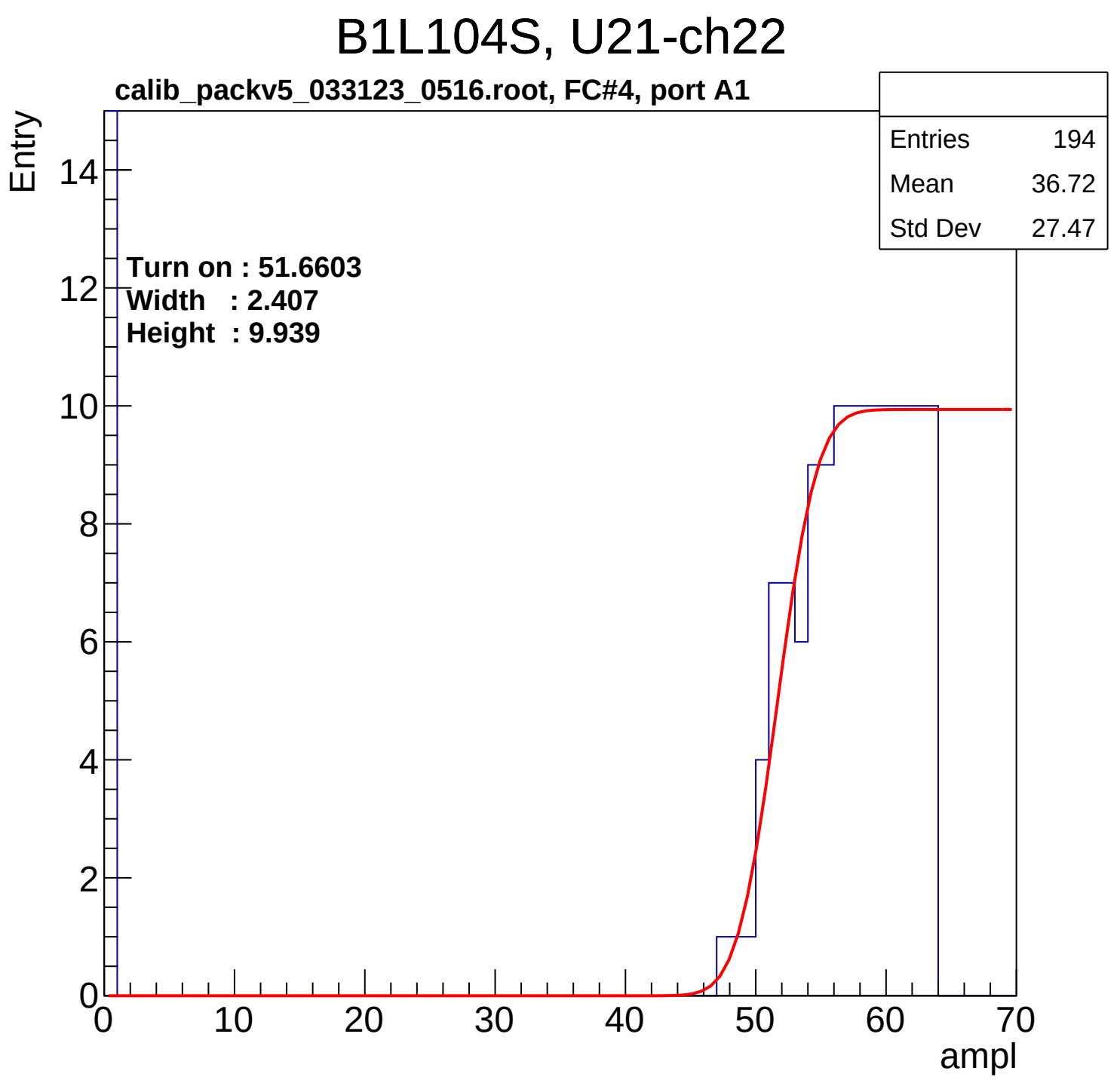
B1L104S, U21-ch22
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 36.72
Std Dev 27.47
Turn on : 51.6603
Width : 2.407
Height : 9.939
Entry
ampl
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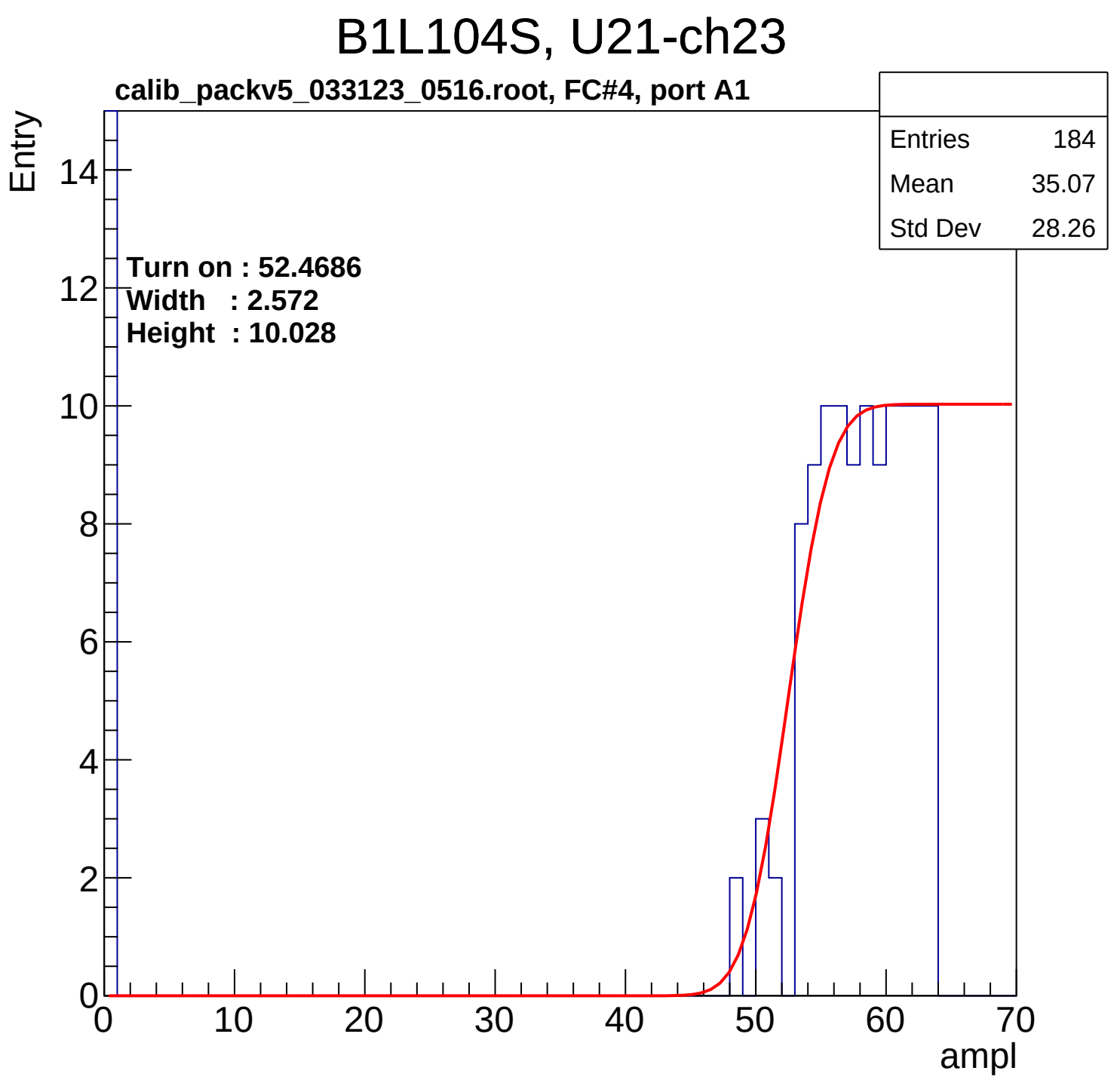
B1L104S, U21-ch23
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 35.07
Std Dev 28.26
Turn on : 52.4686
Width : 2.572
Height : 10.028
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ampl
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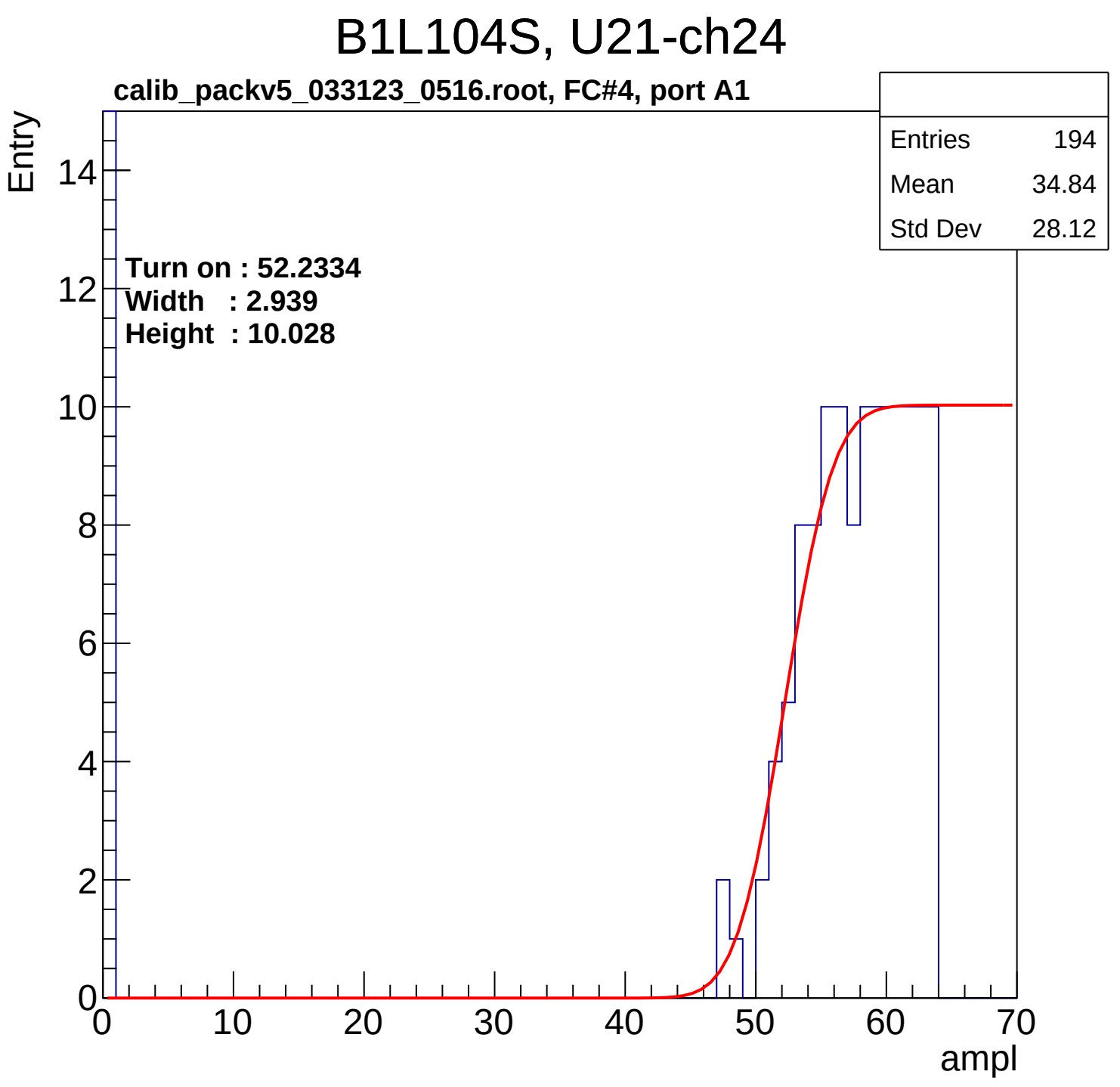
B1L104S, U21-ch24
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 34.84
Std Dev 28.12
Turn on : 52.2334
Width : 2.939
Height : 10.028
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ampl
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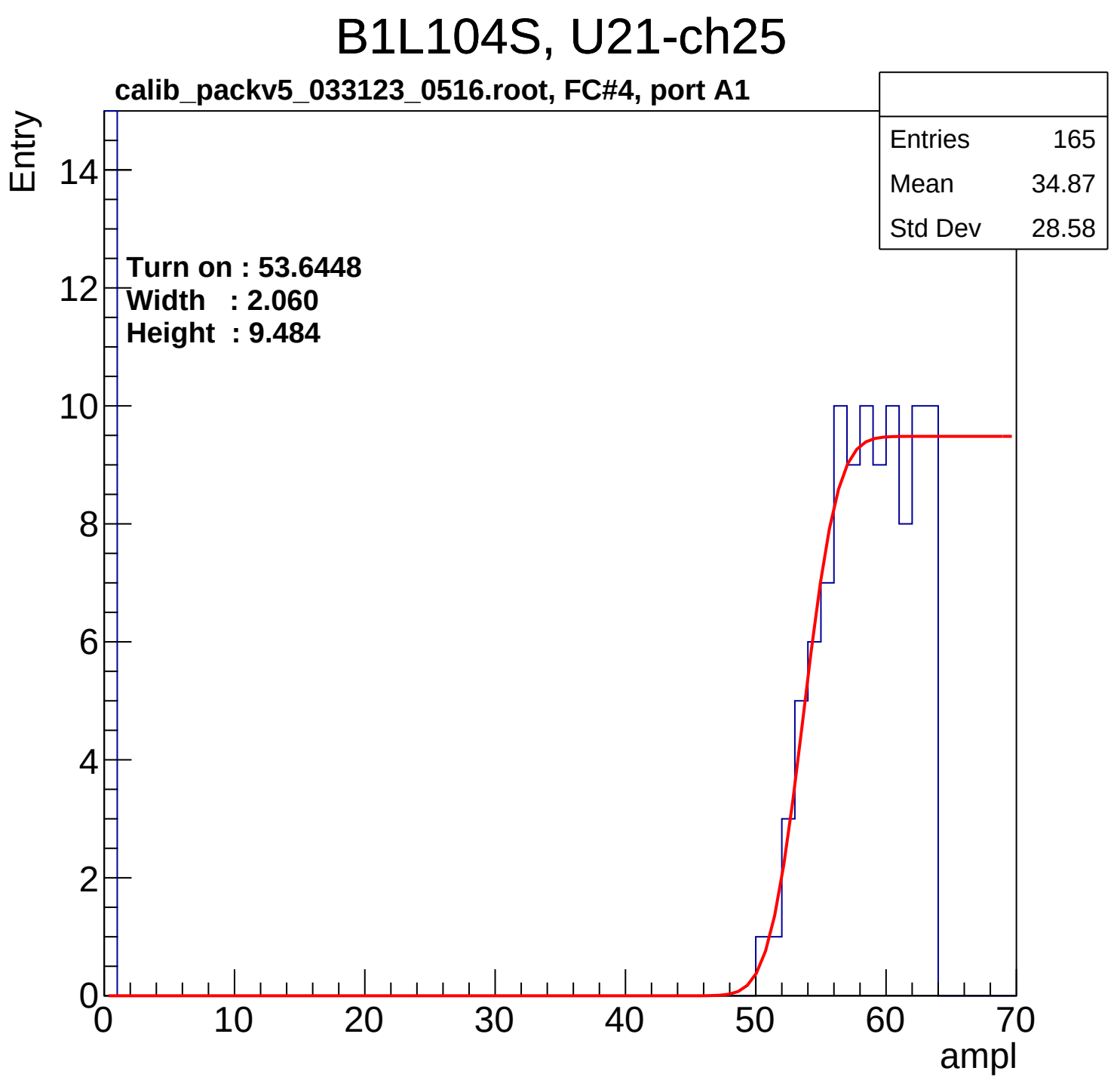
B1L104S, U21-ch25
calib_packv5_033123_0516.root, FC#4, port A1
Entries 165
Mean 34.87
Std Dev 28.58
Turn on : 53.6448
Width : 2.060
Height : 9.484
Entry
ampl
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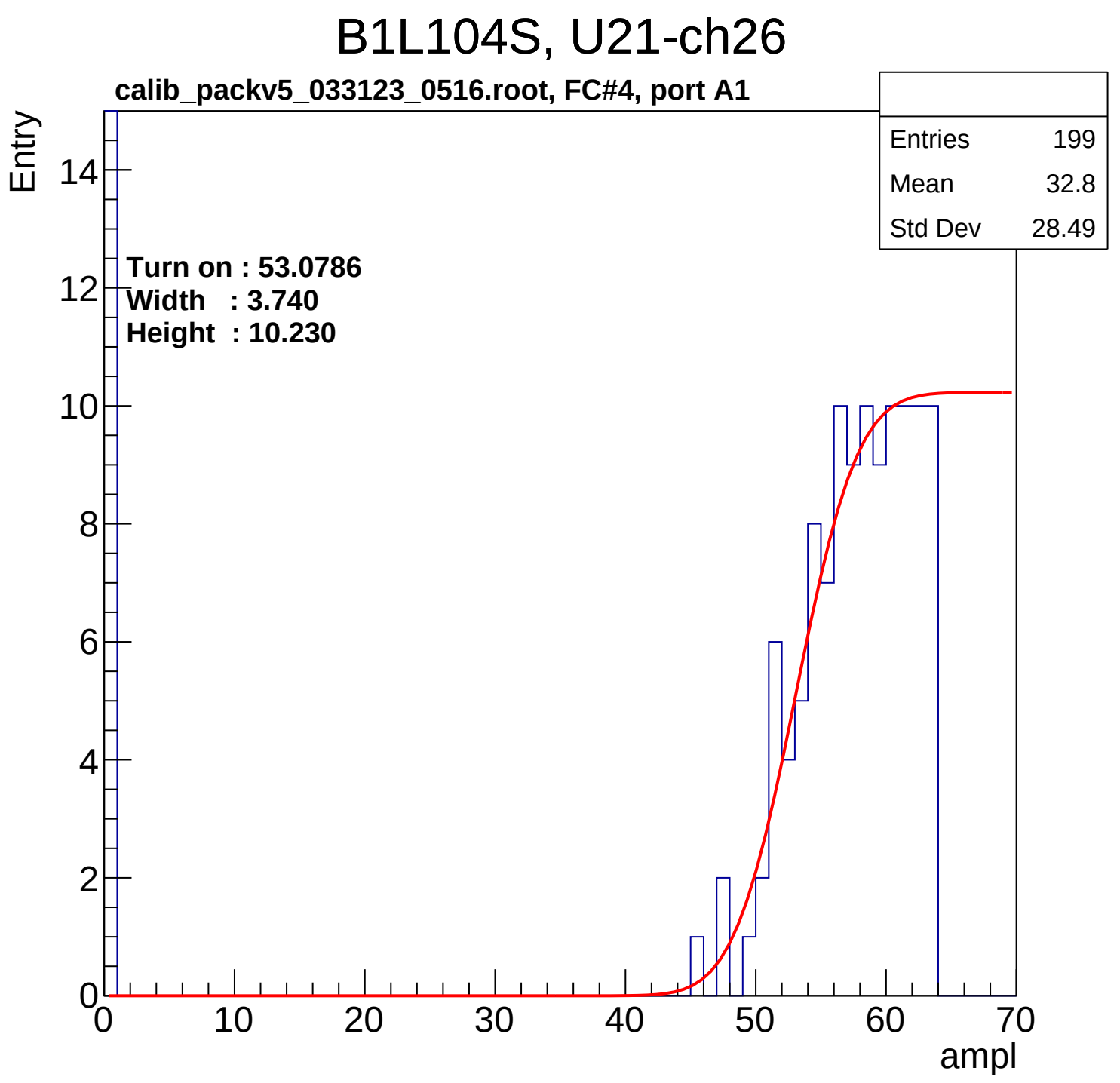
B1L104S, U21-ch26
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 32.8
Std Dev 28.49
Turn on : 53.0786
Width : 3.740
Height : 10.230
Entry
ampl
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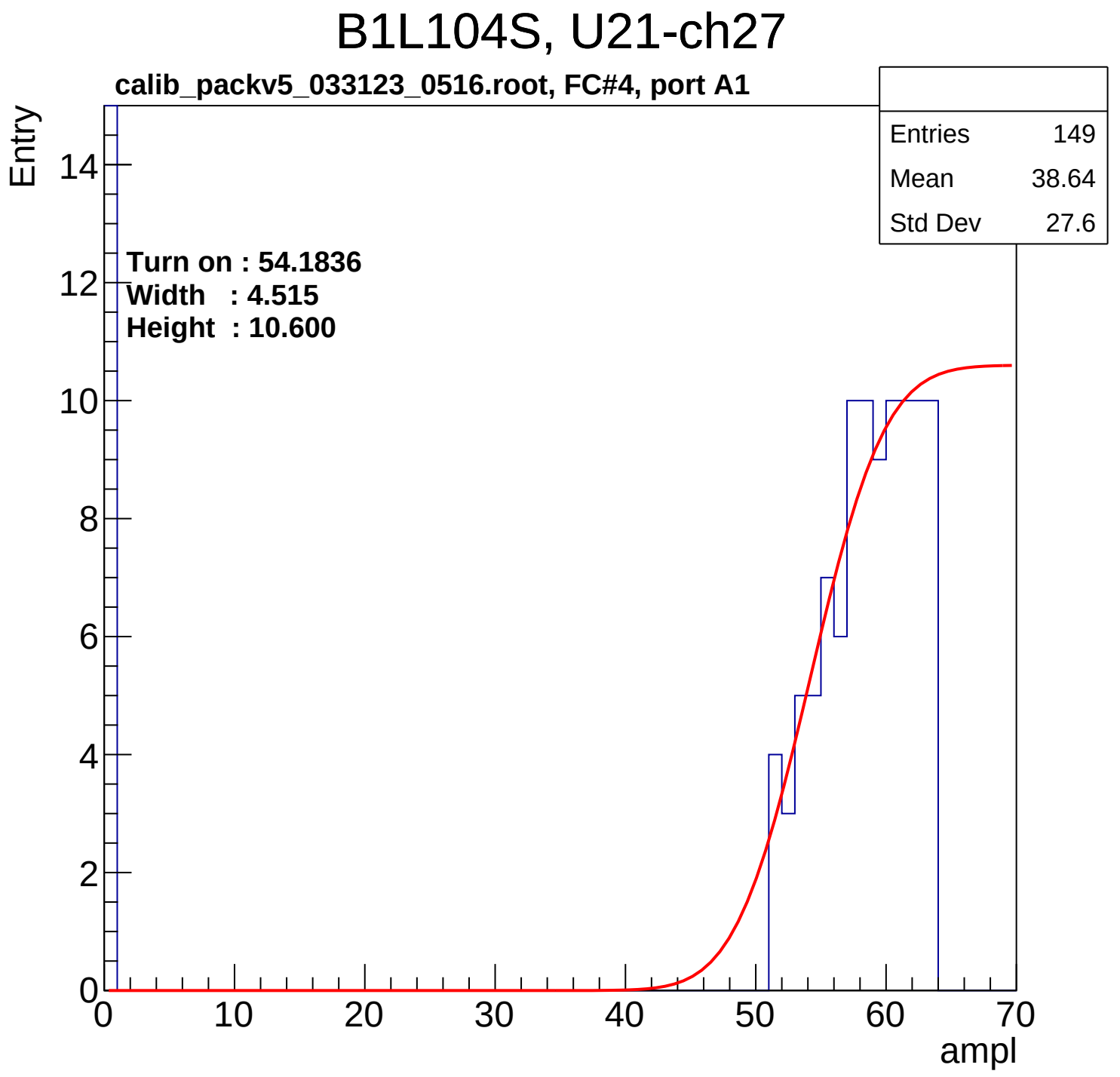
B1L104S, U21-ch27
calib_packv5_033123_0516.root, FC#4, port A1
Entries 149
Mean 38.64
Std Dev 27.6
Turn on : 54.1836
Width : 4.515
Height : 10.600
Entry
ampl
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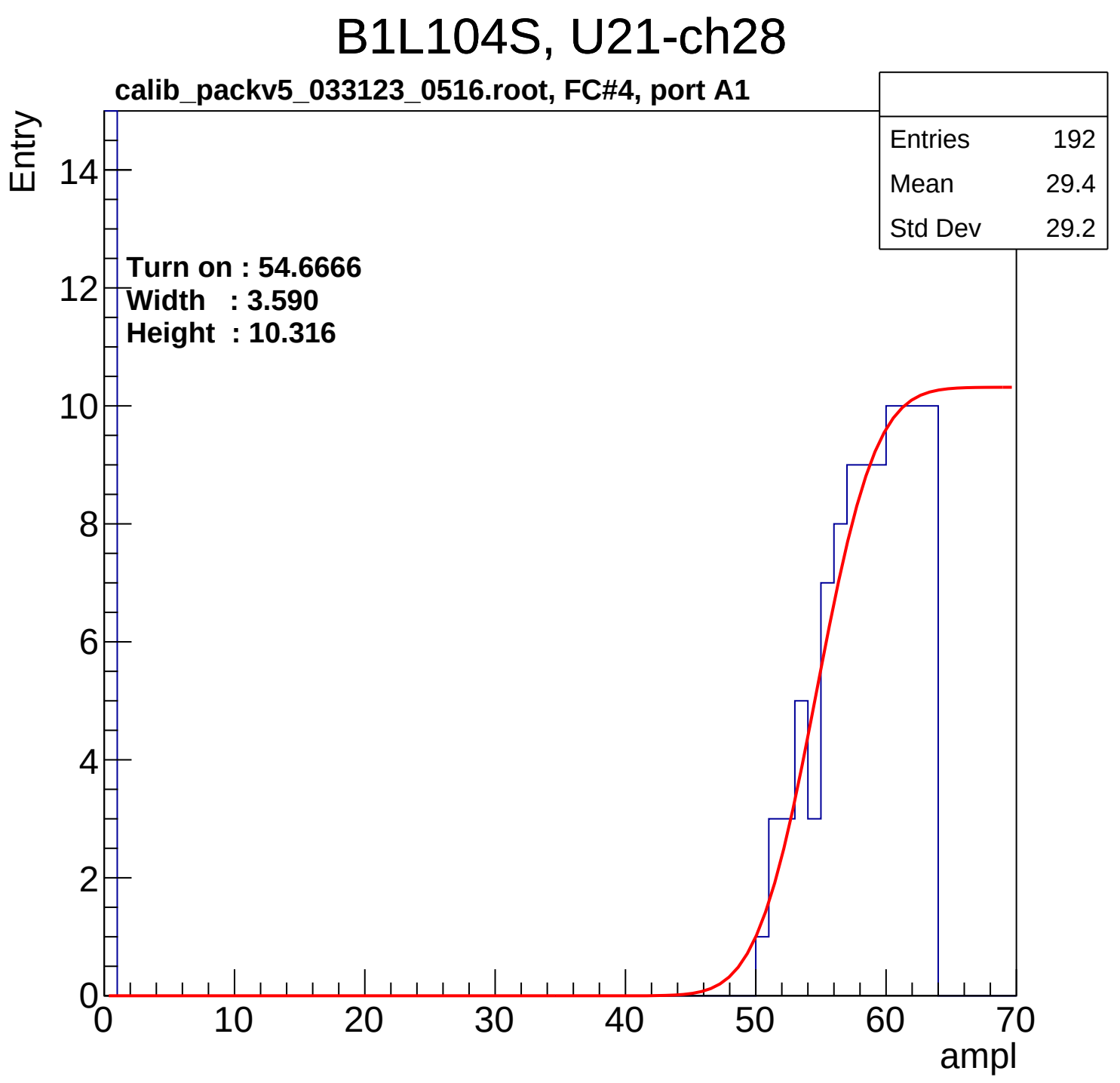
B1L104S, U21-ch28
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 29.4
Std Dev 29.2
Turn on : 54.6666
Width : 3.590
Height : 10.316
Entry
ampl
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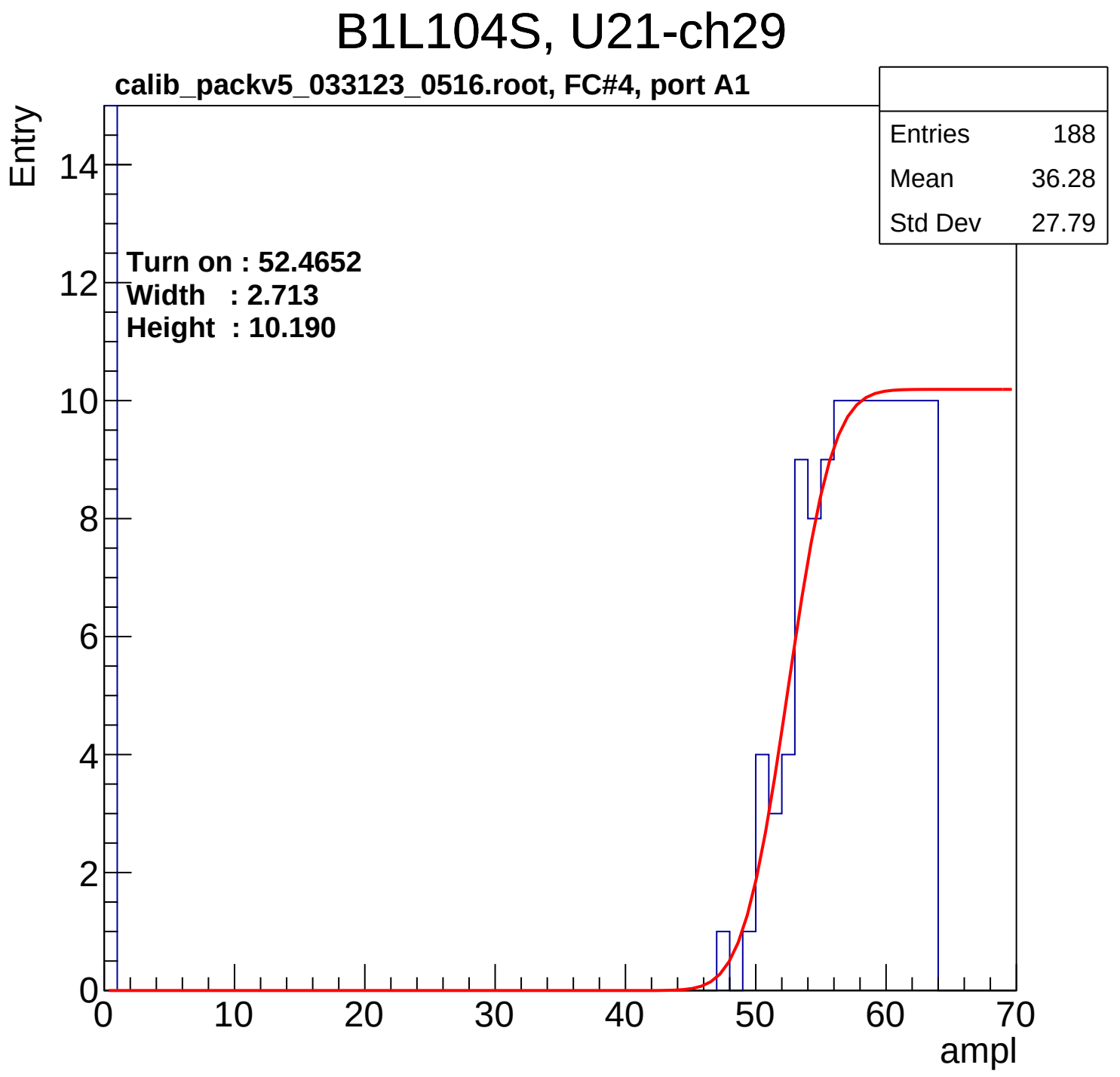
B1L104S, U21-ch29
calib_packv5_033123_0516.root, FC#4, port A1
Entries 188
Mean 36.28
Std Dev 27.79
Turn on : 52.4652
Width : 2.713
Height : 10.190
Entry
ampl
0
2
4
6
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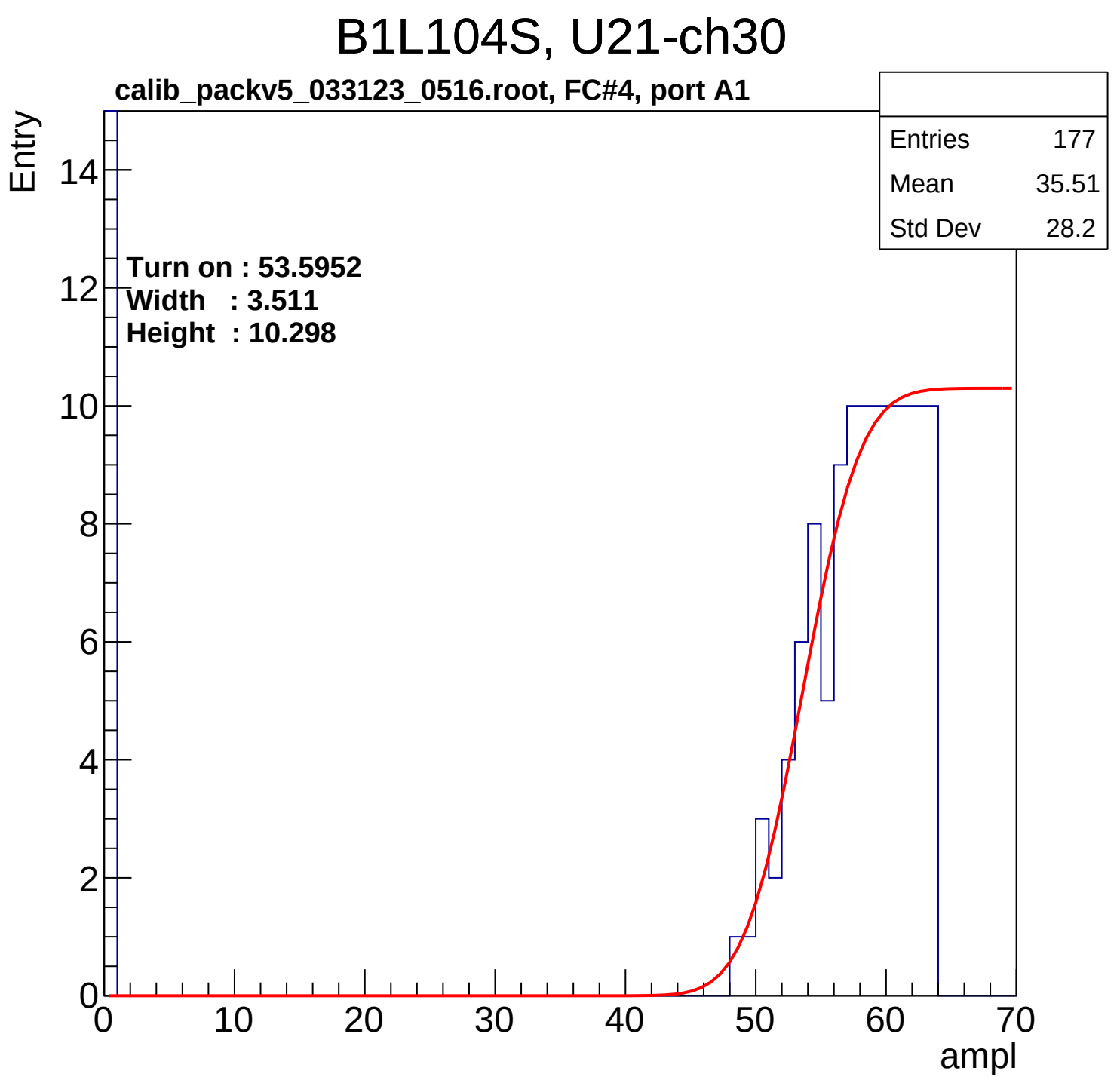
B1L104S, U21-ch30
calib_packv5_033123_0516.root, FC#4, port A1
Entries 177
Mean 35.51
Std Dev 28.2
Turn on : 53.5952
Width : 3.511
Height : 10.298
Entry
ampl
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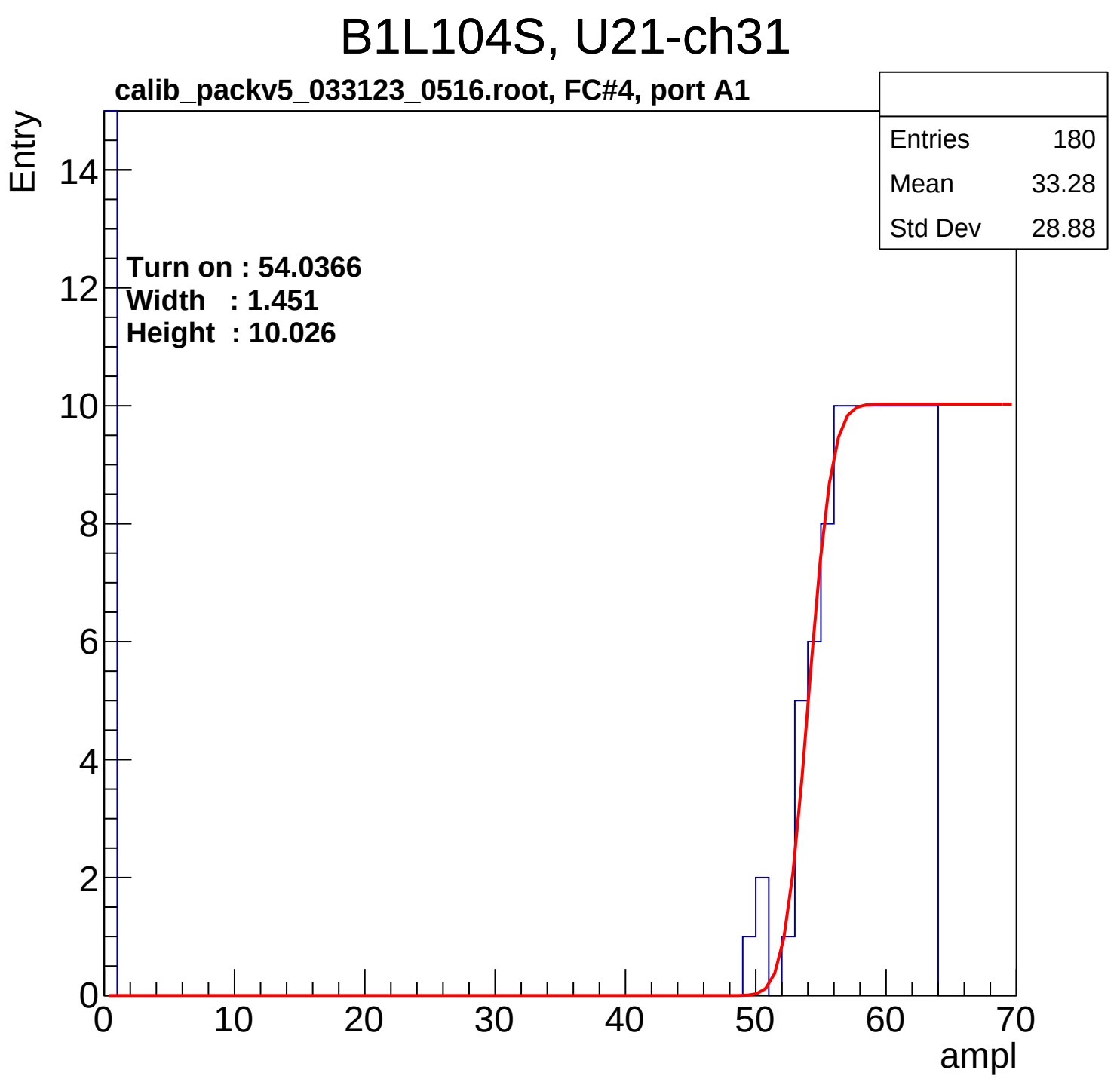
B1L104S, U21-ch31
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 33.28
Std Dev 28.88
Turn on : 54.0366
Width : 1.451
Height : 10.026
Entry
ampl
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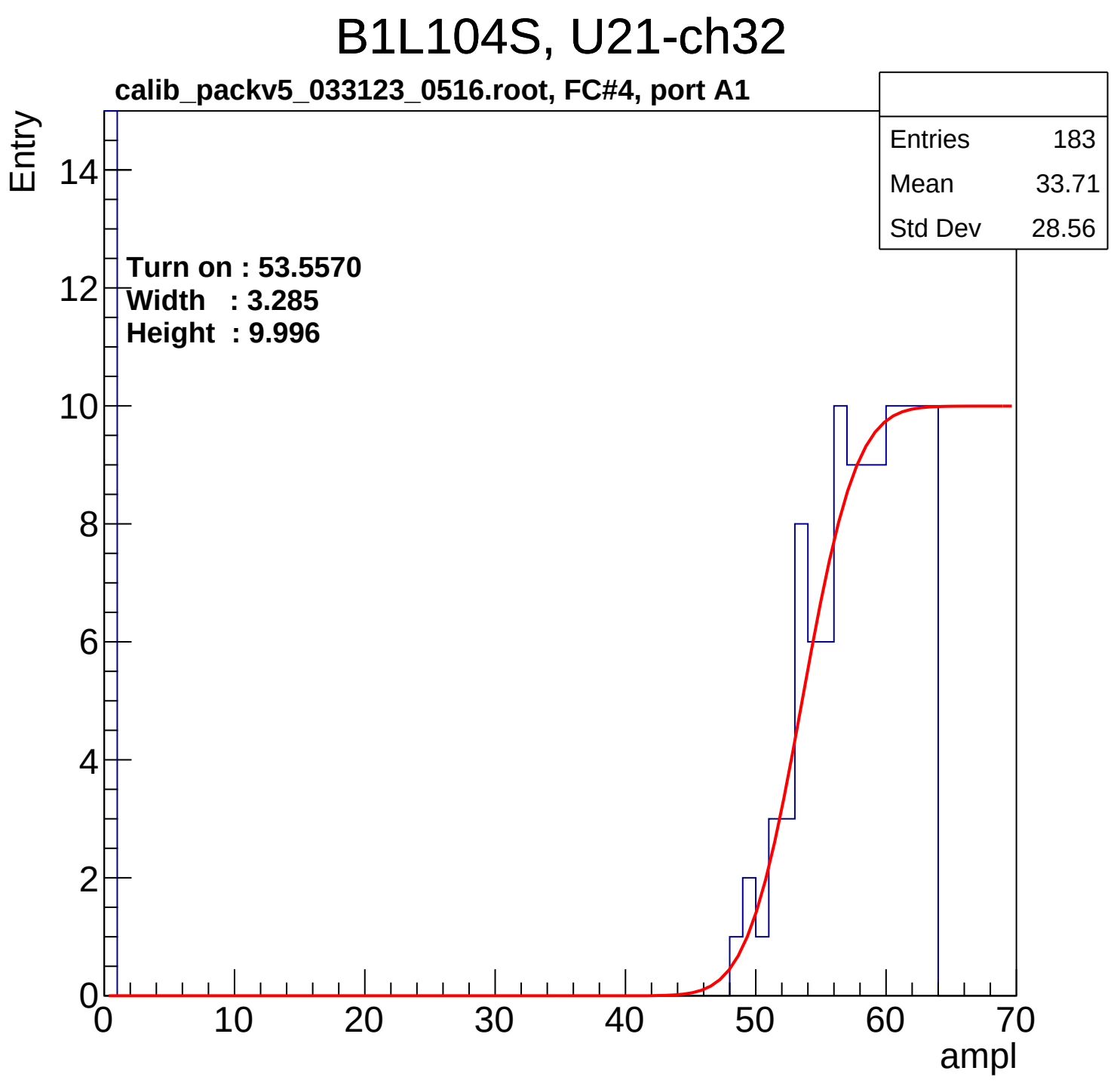
B1L104S, U21-ch32
calib_packv5_033123_0516.root, FC#4, port A1
Entries 183
Mean 33.71
Std Dev 28.56
Turn on : 53.5570
Width : 3.285
Height : 9.996
Entry
ampl
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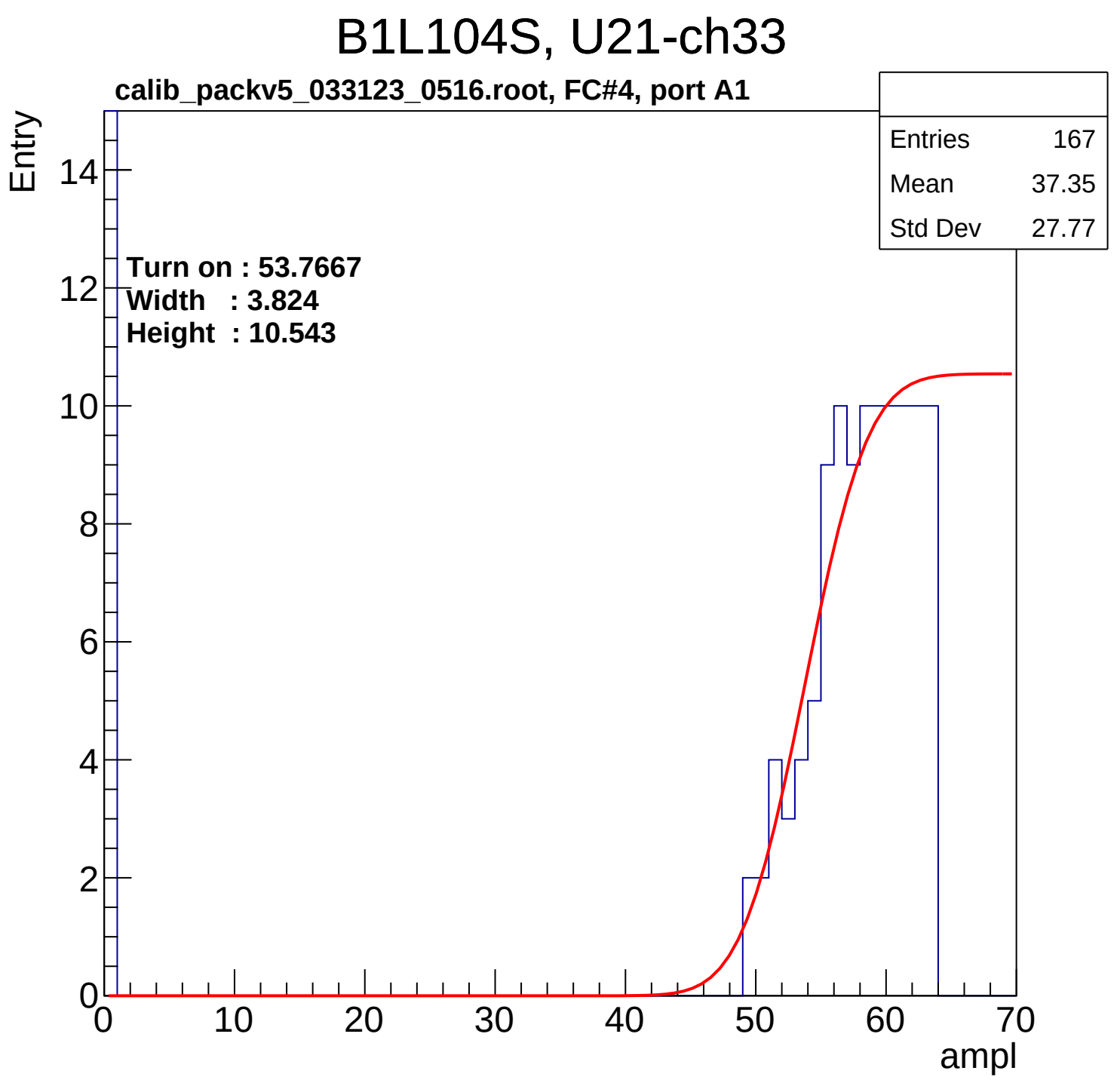
B1L104S, U21-ch33
calib_packv5_033123_0516.root, FC#4, port A1
Entries 167
Mean 37.35
Std Dev 27.77
Turn on : 53.7667
Width : 3.824
Height : 10.543
Entry
ampl
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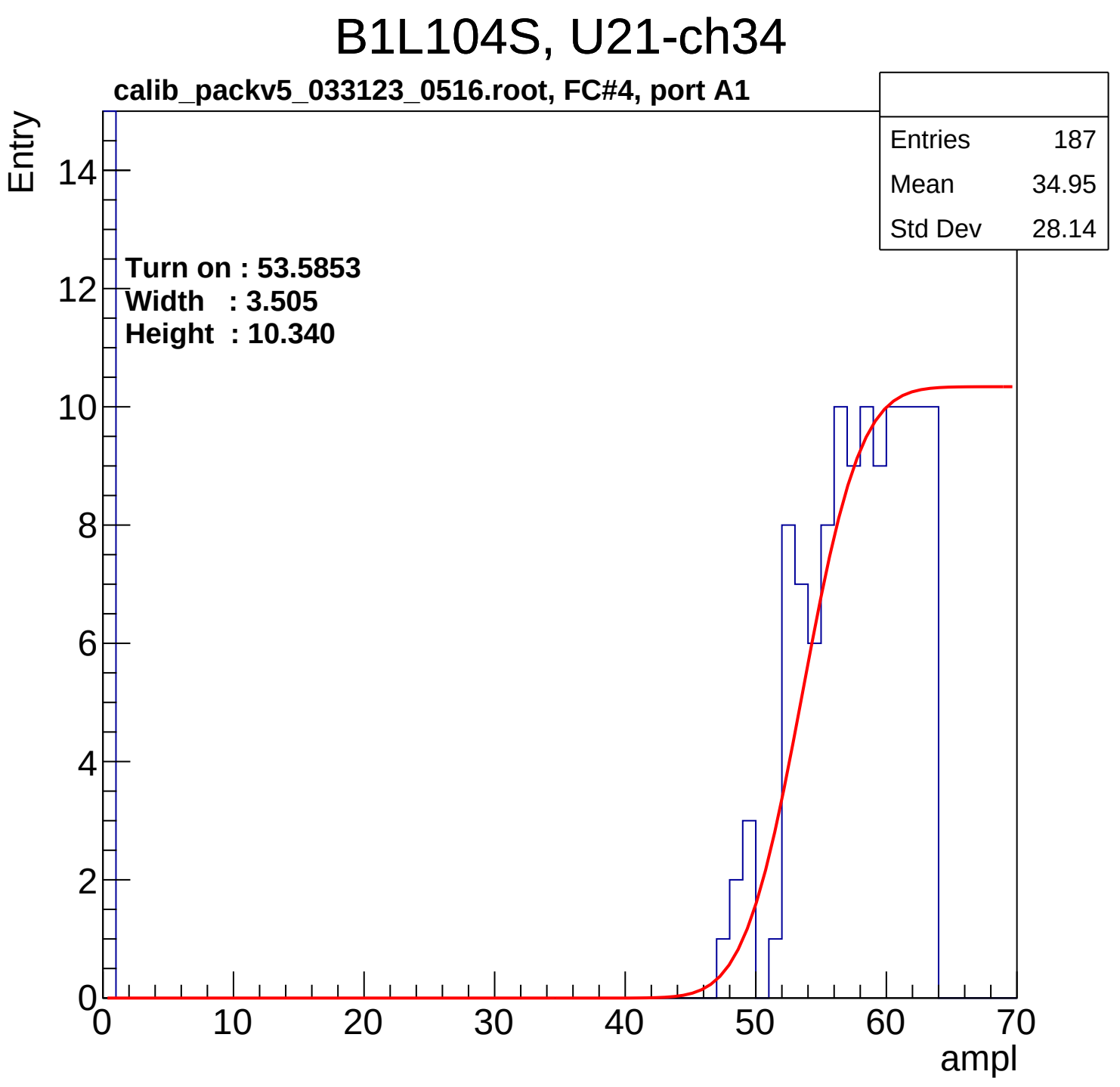
B1L104S, U21-ch34
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 34.95
Std Dev 28.14
Turn on : 53.5853
Width : 3.505
Height : 10.340
Entry
ampl
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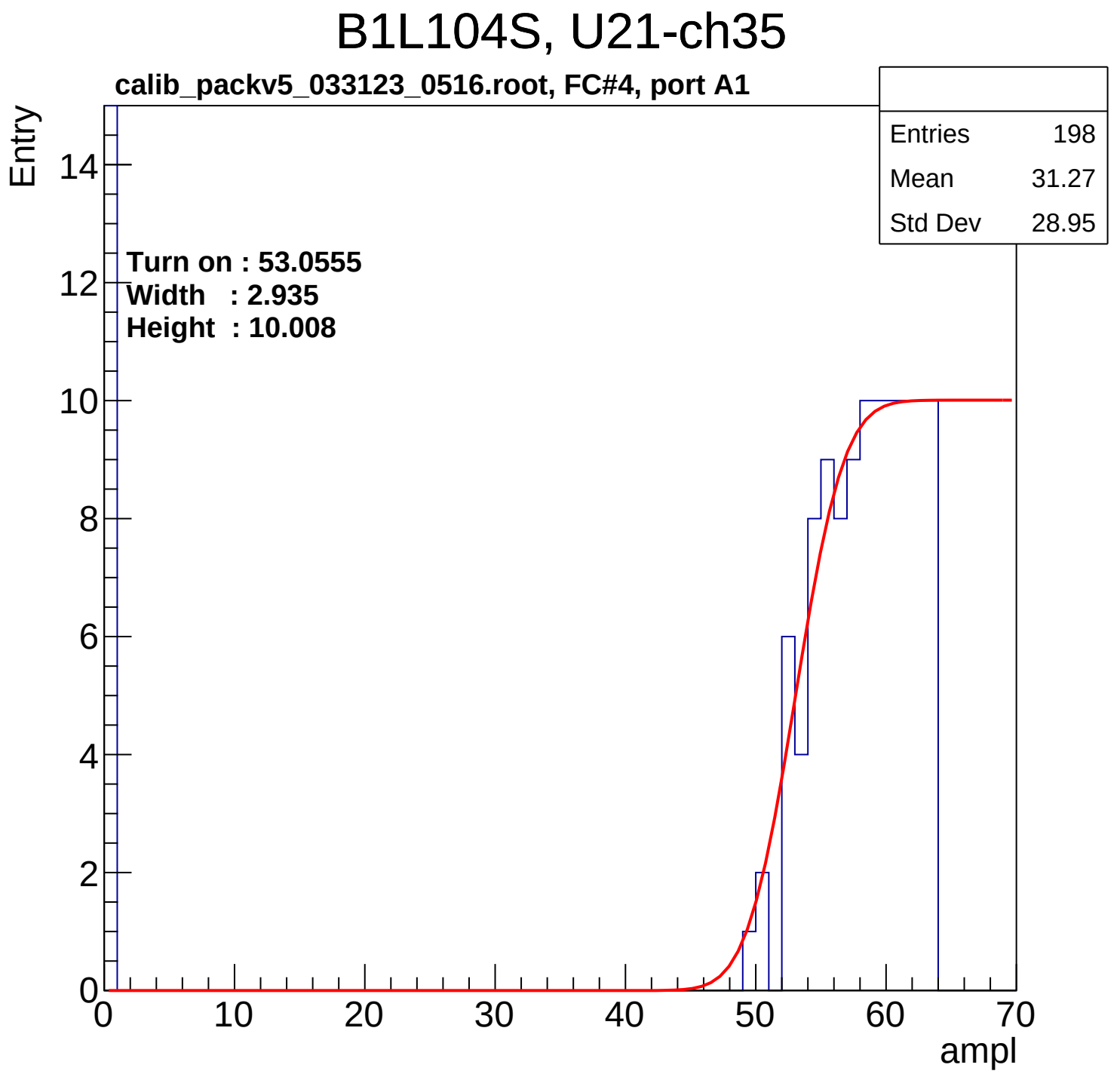
B1L104S, U21-ch35
calib_packv5_033123_0516.root, FC#4, port A1
Entries 198
Mean 31.27
Std Dev 28.95
Turn on : 53.0555
Width : 2.935
Height : 10.008
Entry
ampl
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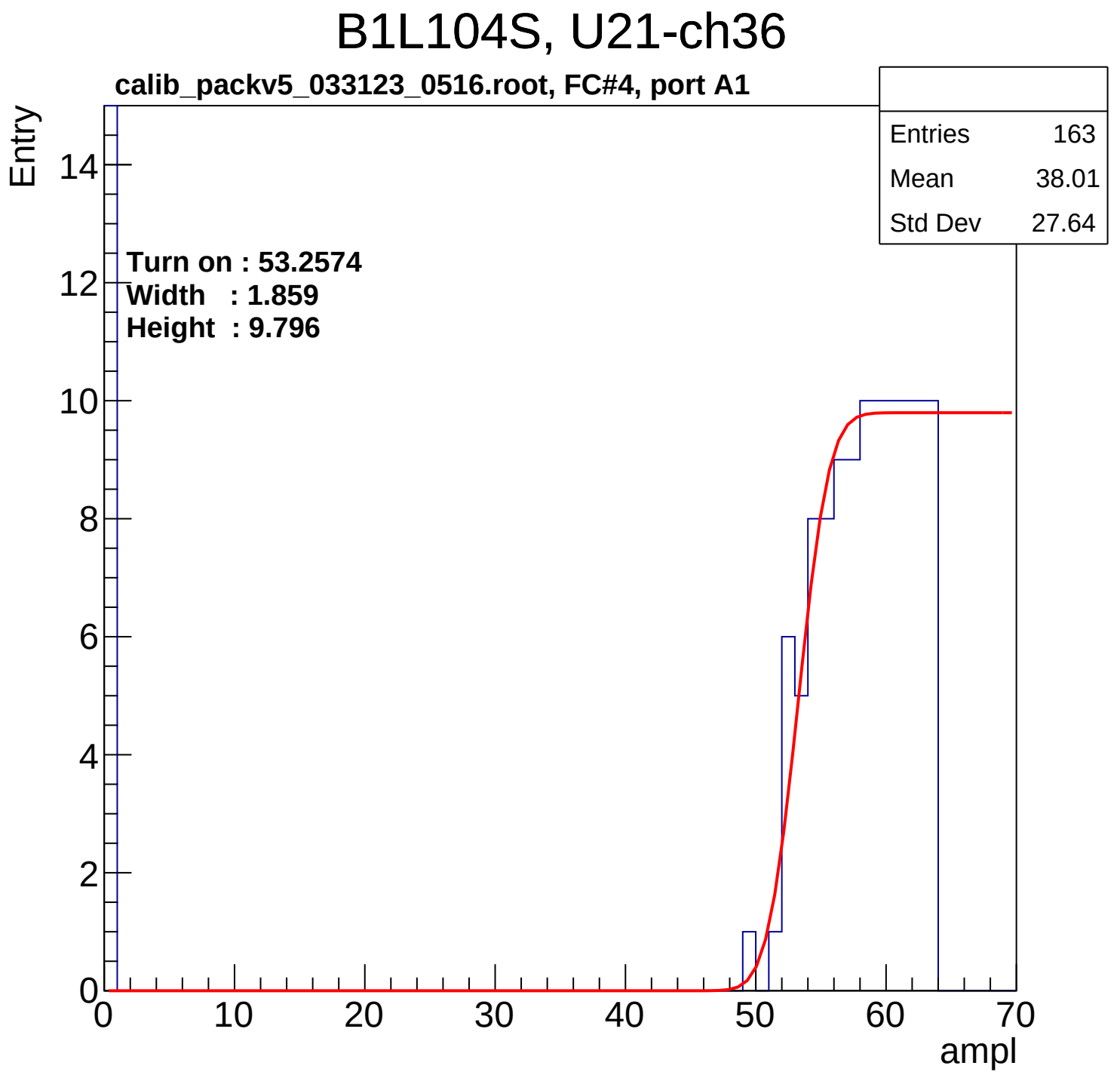
B1L104S, U21-ch36
calib_packv5_033123_0516.root, FC#4, port A1
Entries 163
Mean 38.01
Std Dev 27.64
Turn on : 53.2574
Width : 1.859
Height : 9.796
Entry
ampl
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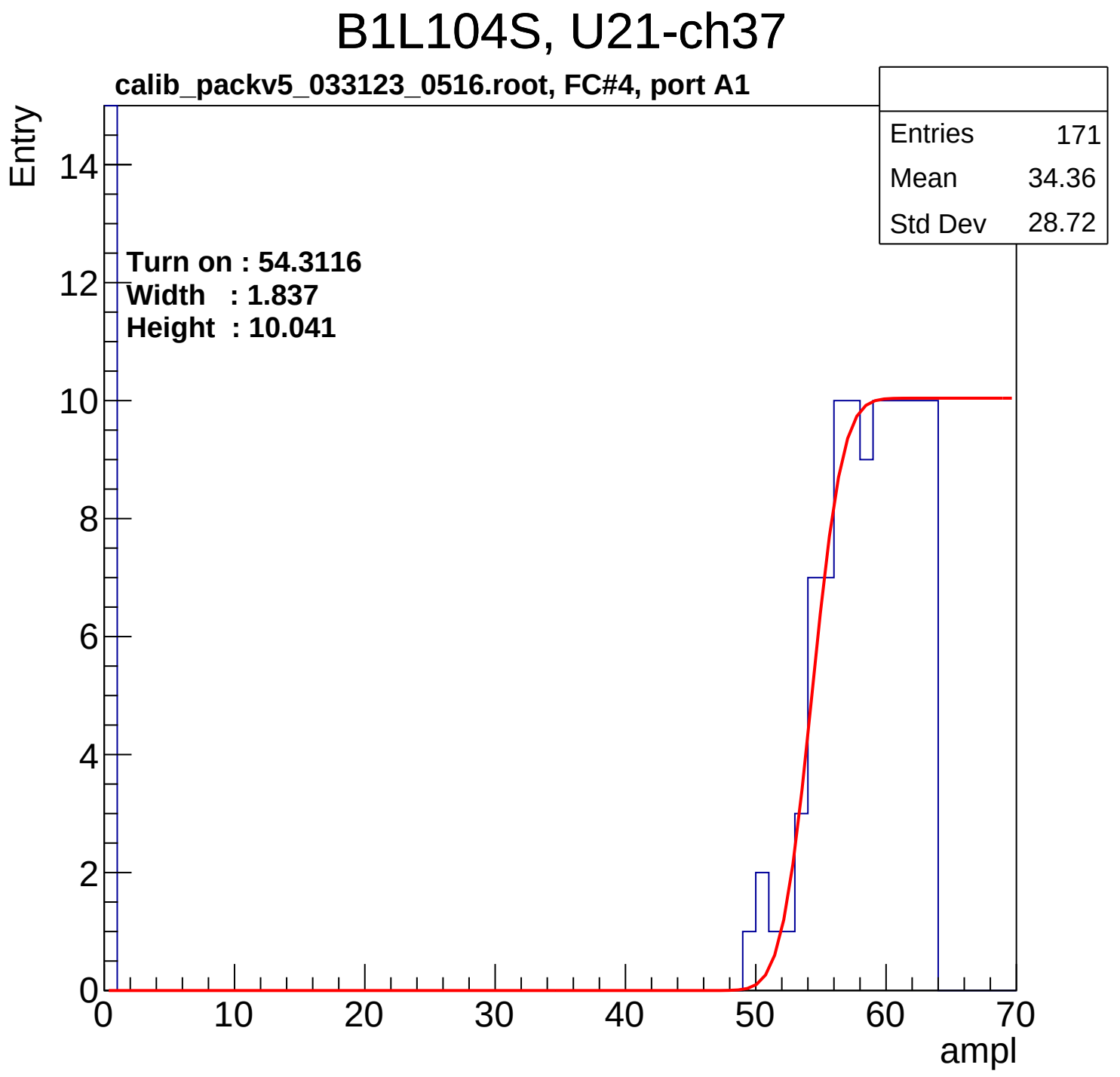
B1L104S, U21-ch37
calib_packv5_033123_0516.root, FC#4, port A1
Entries 171
Mean 34.36
Std Dev 28.72
Turn on : 54.3116
Width : 1.837
Height : 10.041
Entry
ampl
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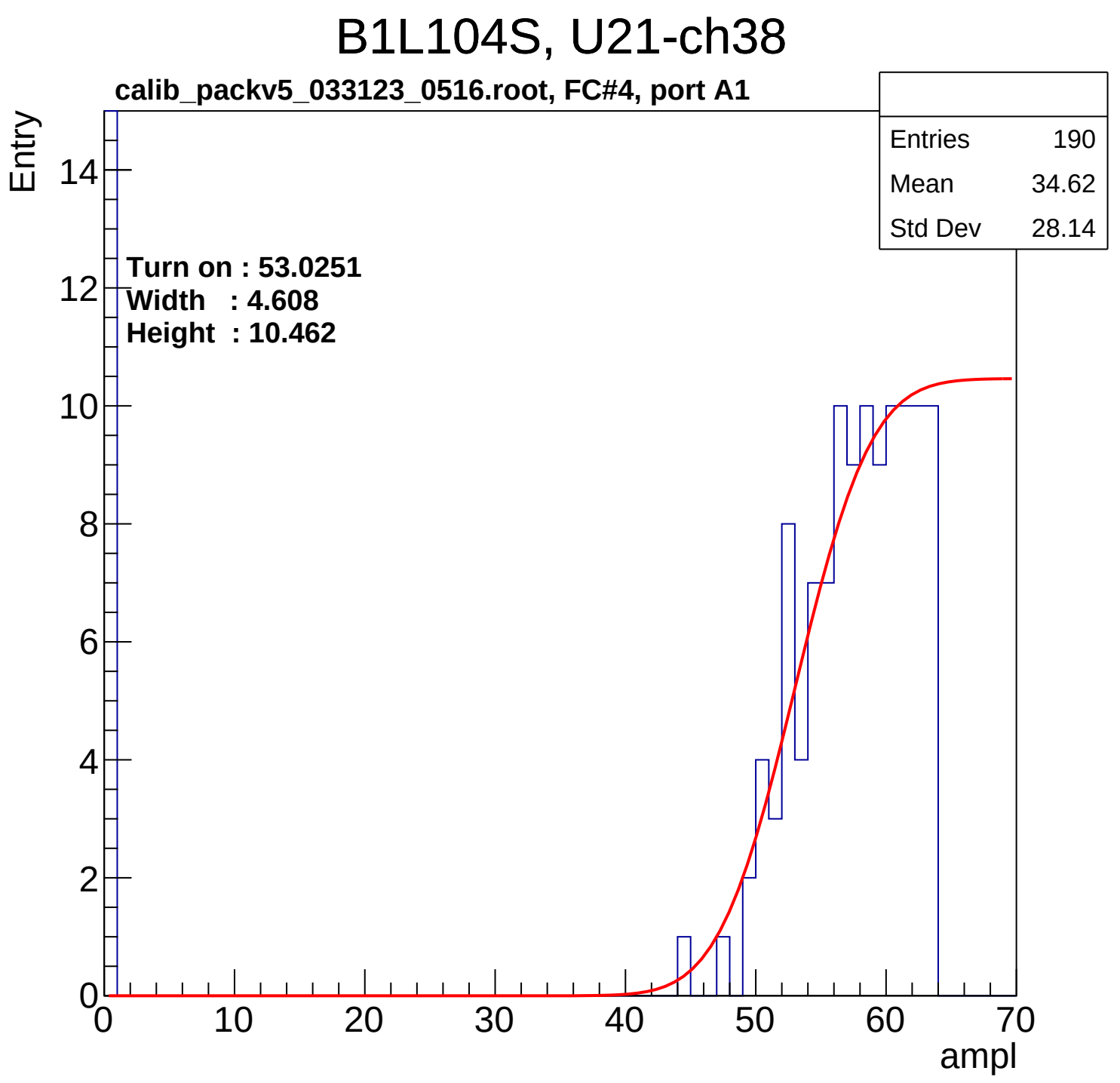
B1L104S, U21-ch38
calib_packv5_033123_0516.root, FC#4, port A1
Entries 190
Mean 34.62
Std Dev 28.14
Turn on : 53.0251
Width : 4.608
Height : 10.462
Entry
ampl
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2
4
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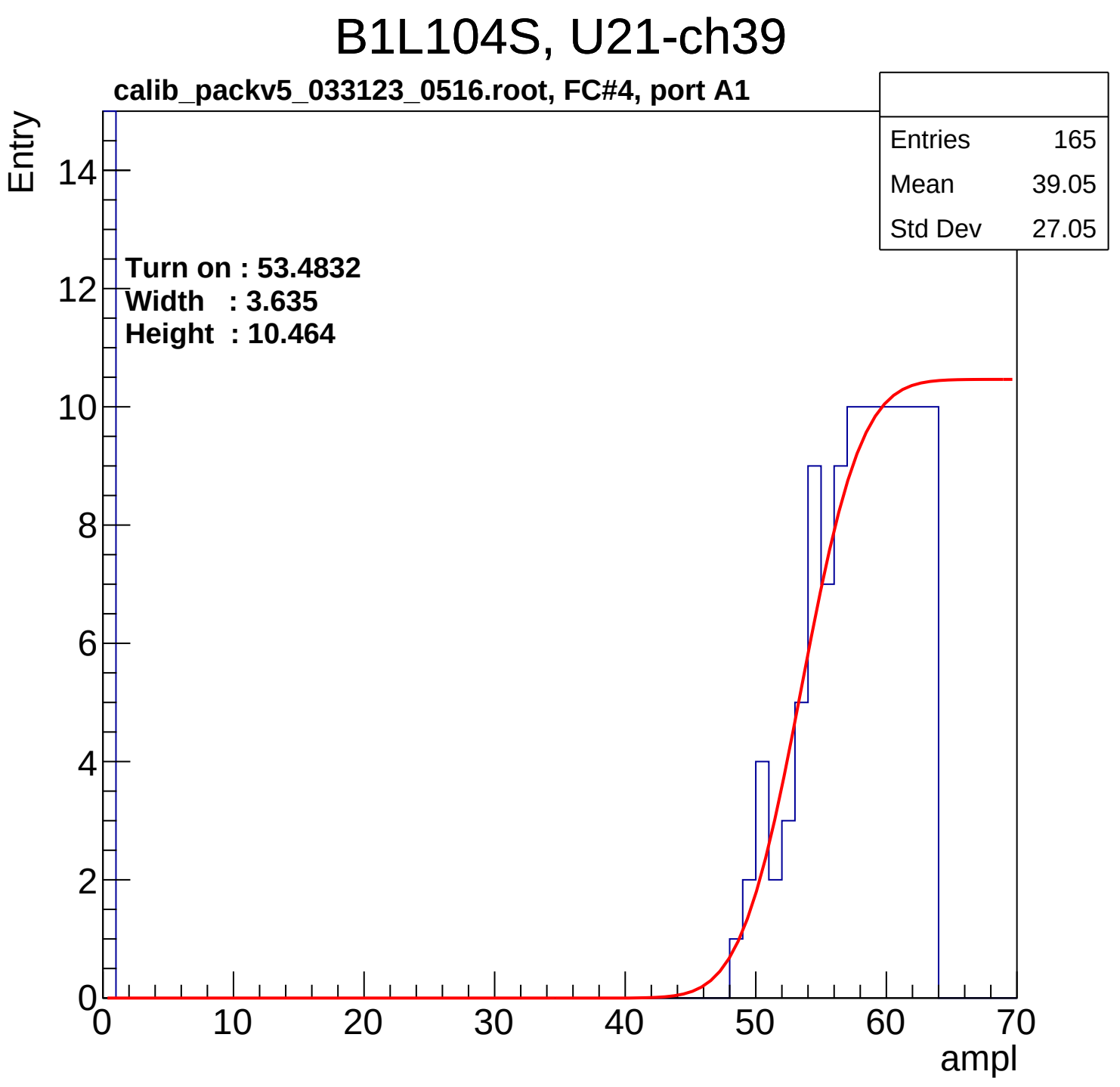
B1L104S, U21-ch39
calib_packv5_033123_0516.root, FC#4, port A1
Entries 165
Mean 39.05
Std Dev 27.05
Turn on : 53.4832
Width : 3.635
Height : 10.464
Entry
ampl
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2
4
6
8
10
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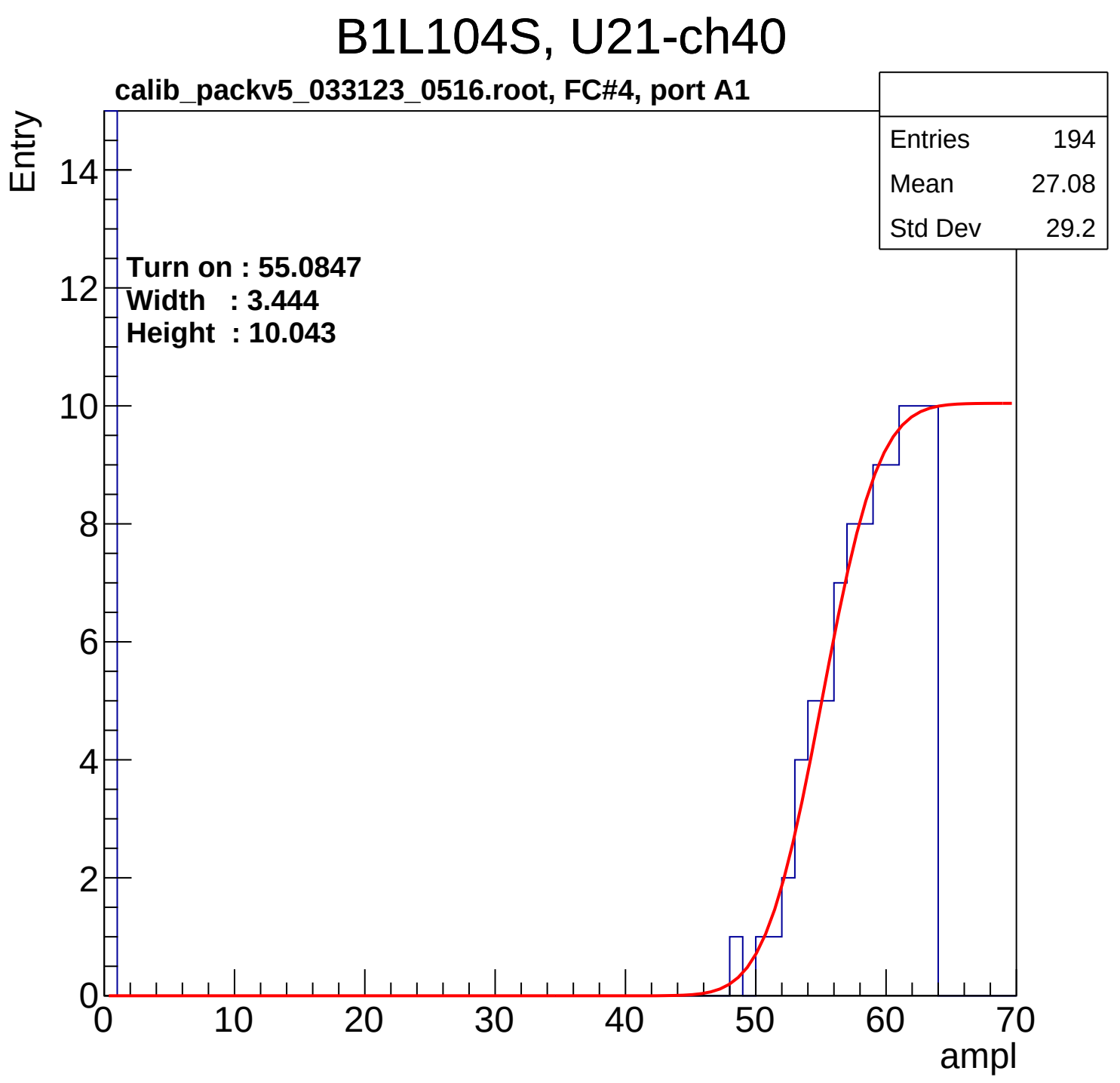
B1L104S, U21-ch40
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 27.08
Std Dev 29.2
Turn on : 55.0847
Width : 3.444
Height : 10.043
Entry
ampl
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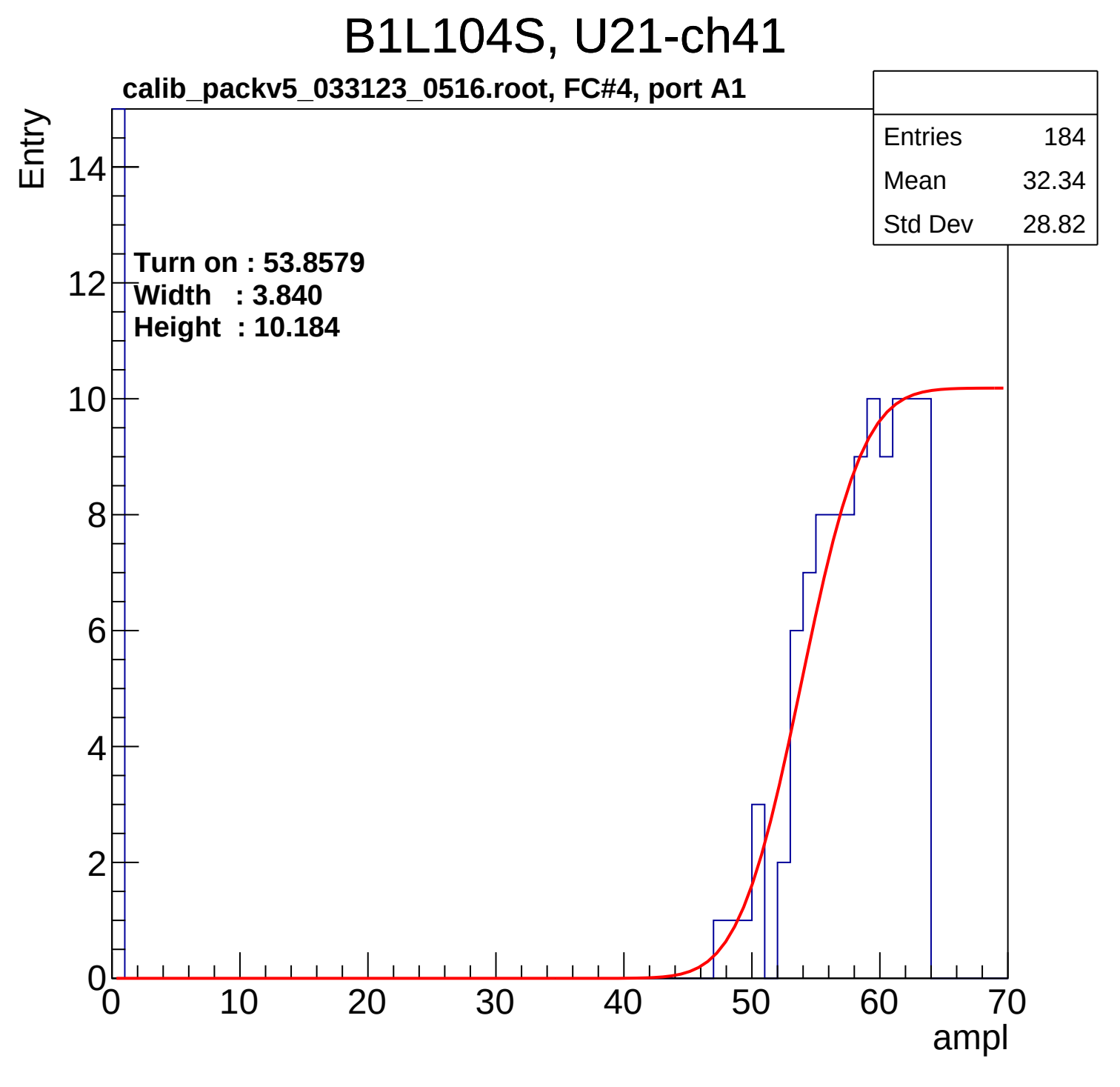
B1L104S, U21-ch41
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 32.34
Std Dev 28.82
Turn on : 53.8579
Width : 3.840
Height : 10.184
Entry
ampl
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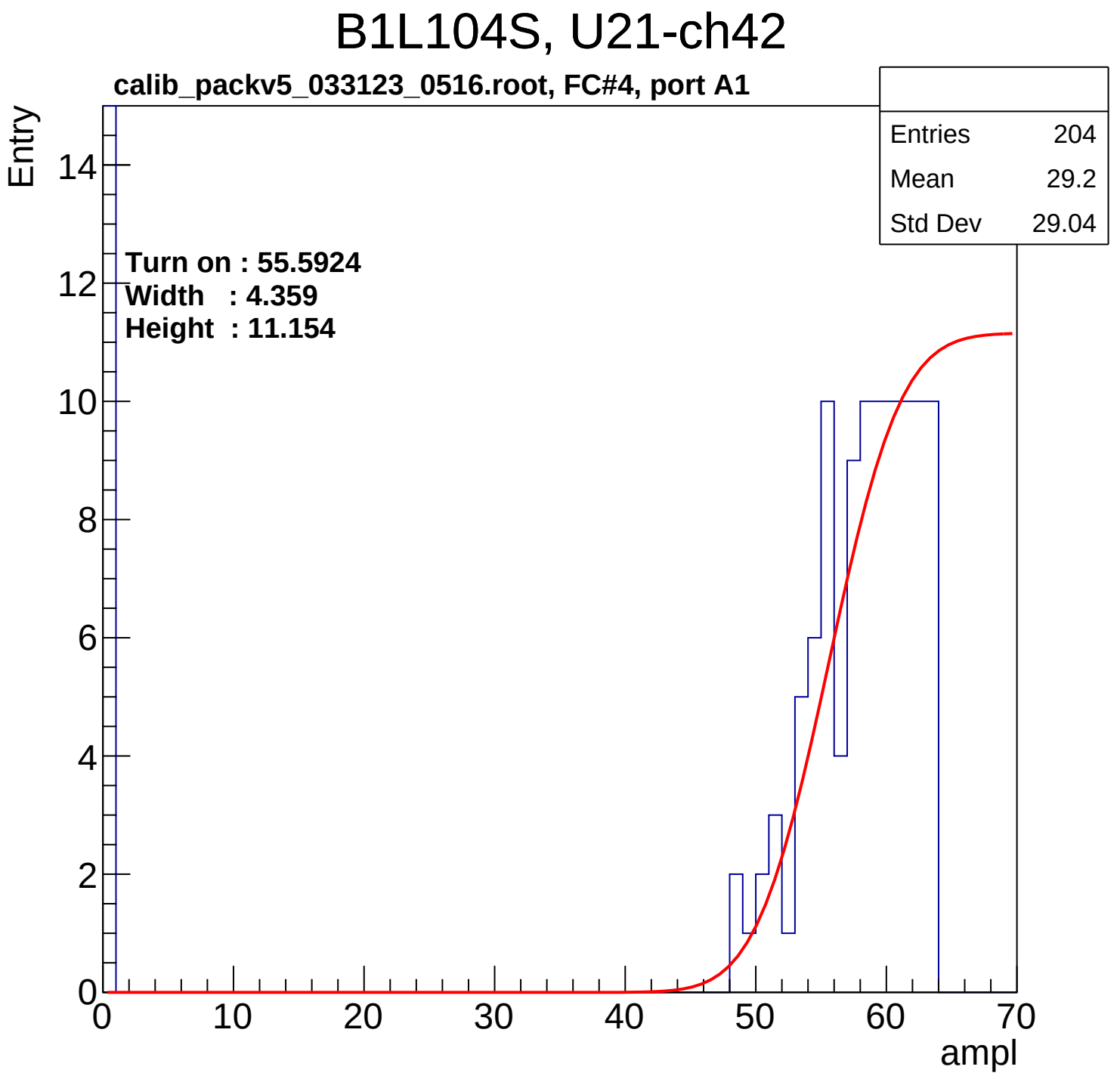
B1L104S, U21-ch42
calib_packv5_033123_0516.root, FC#4, port A1
Entries 204
Mean 29.2
Std Dev 29.04
Turn on : 55.5924
Width : 4.359
Height : 11.154
Entry
ampl
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6
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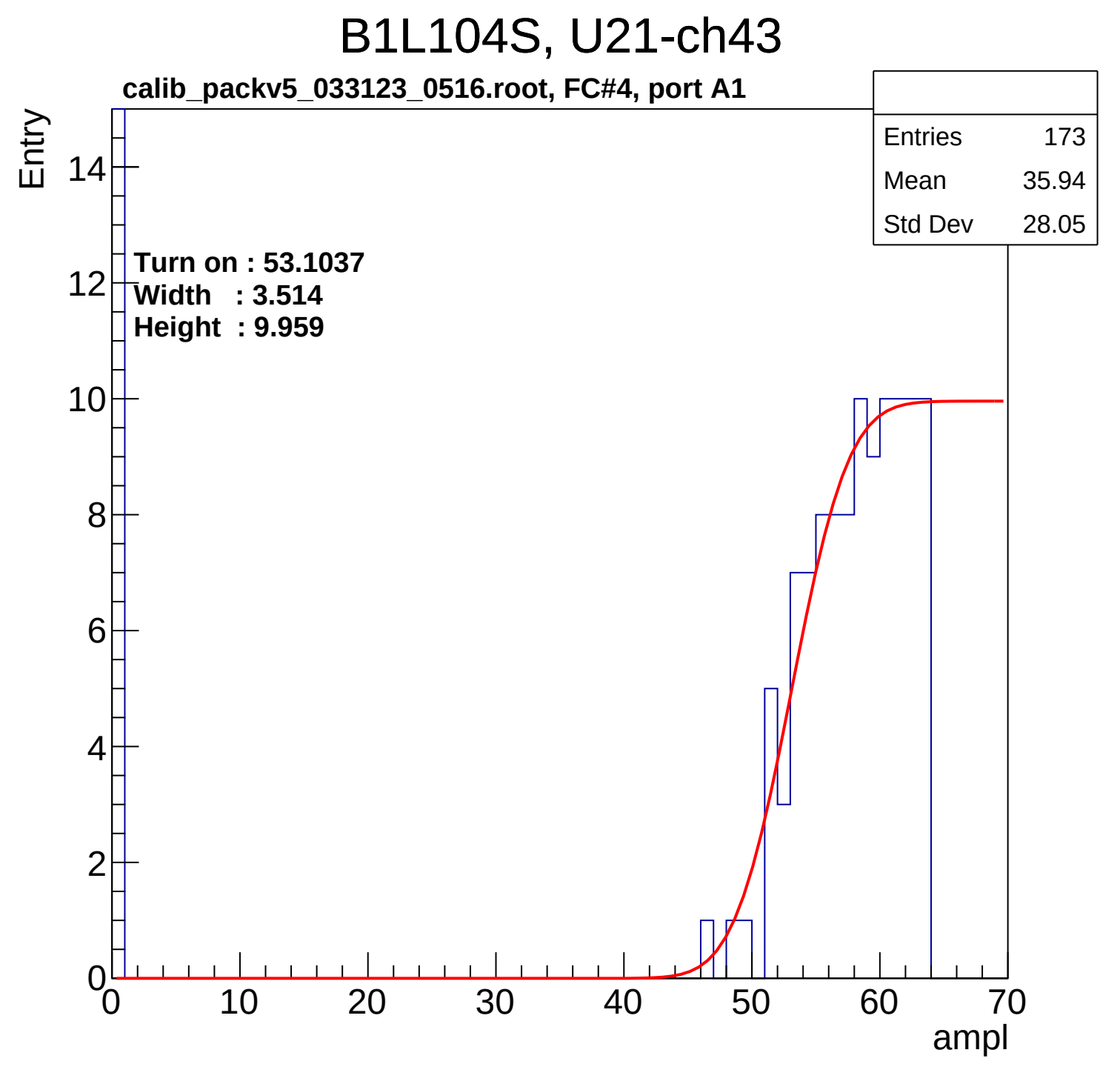
B1L104S, U21-ch43
calib_packv5_033123_0516.root, FC#4, port A1
Entries 173
Mean 35.94
Std Dev 28.05
Turn on : 53.1037
Width : 3.514
Height : 9.959
Entry
ampl
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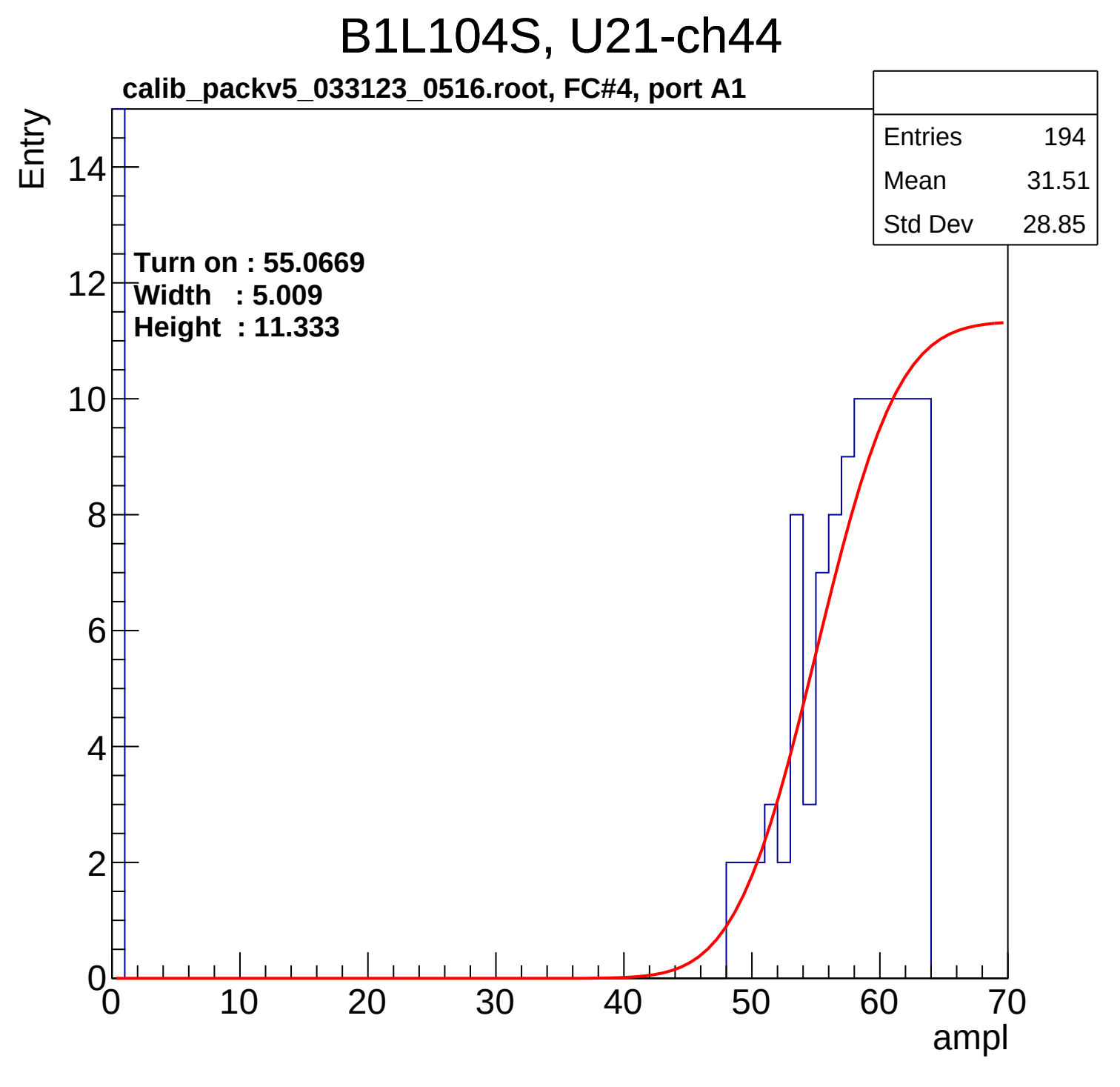
B1L104S, U21-ch44
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 31.51
Std Dev 28.85
Turn on : 55.0669
Width : 5.009
Height : 11.333
Entry
ampl
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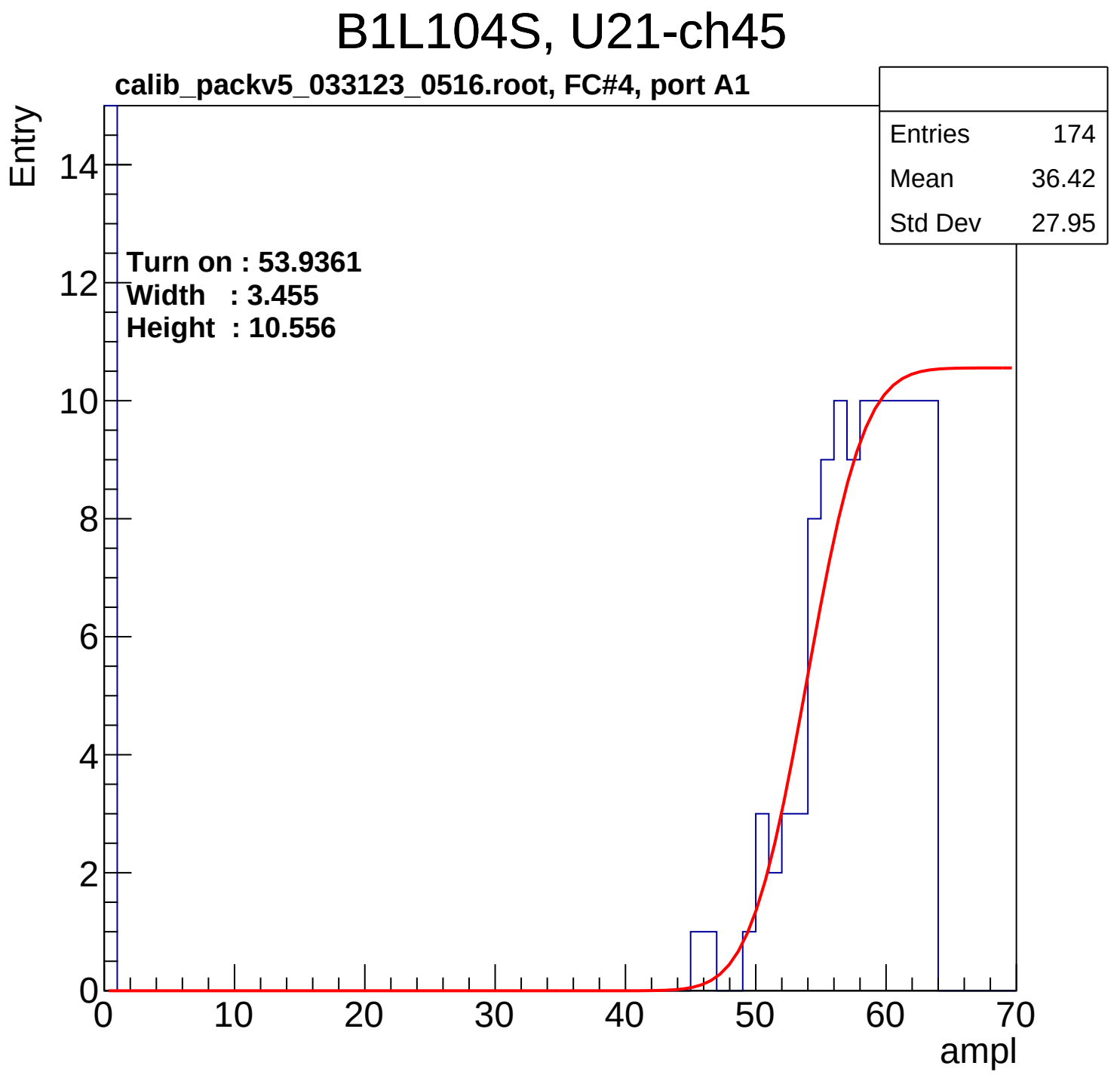
B1L104S, U21-ch45
calib_packv5_033123_0516.root, FC#4, port A1
Entries 174
Mean 36.42
Std Dev 27.95
Turn on : 53.9361
Width : 3.455
Height : 10.556
Entry
ampl
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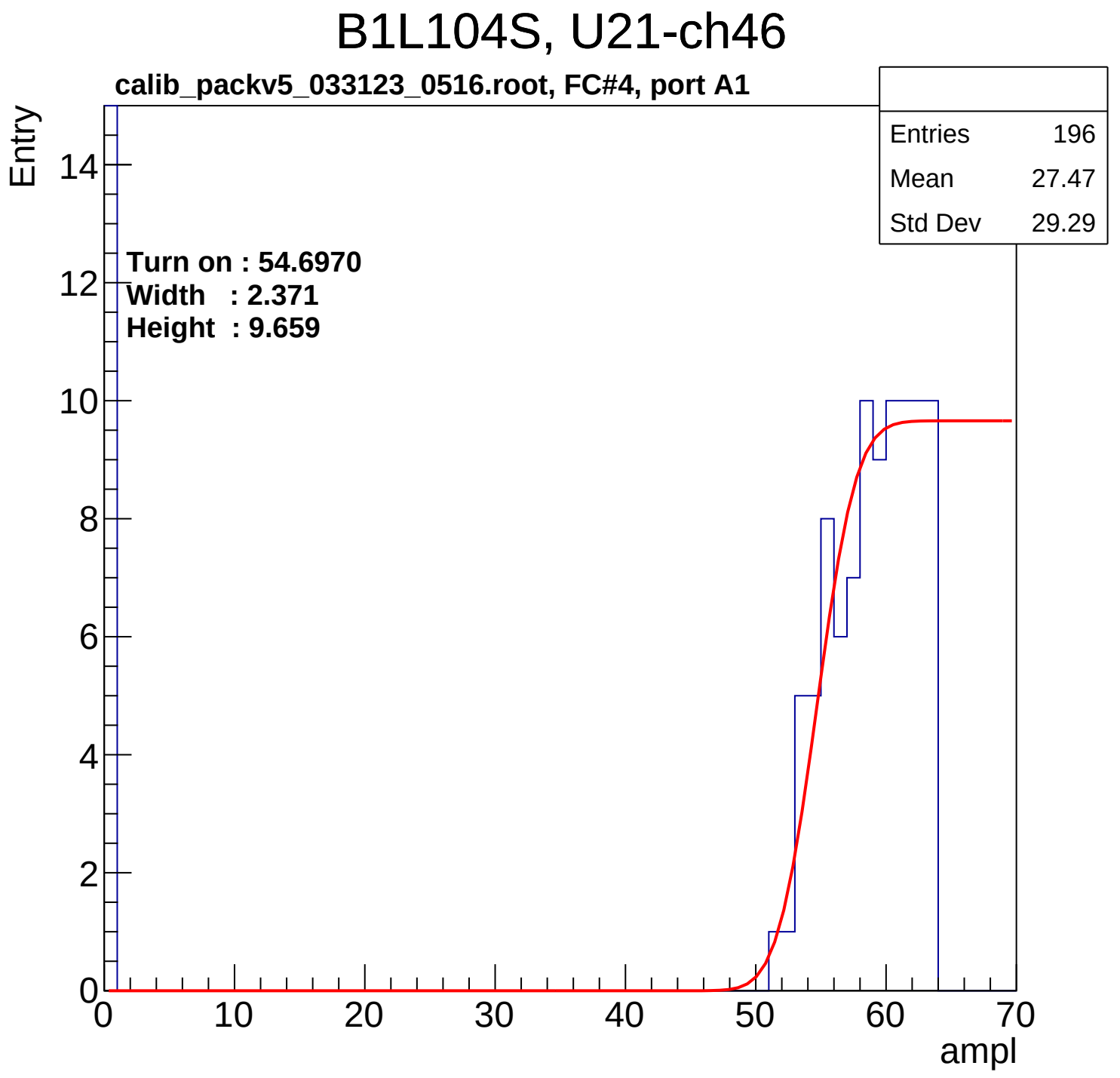
B1L104S, U21-ch46
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 27.47
Std Dev 29.29
Turn on : 54.6970
Width : 2.371
Height : 9.659
Entry
ampl
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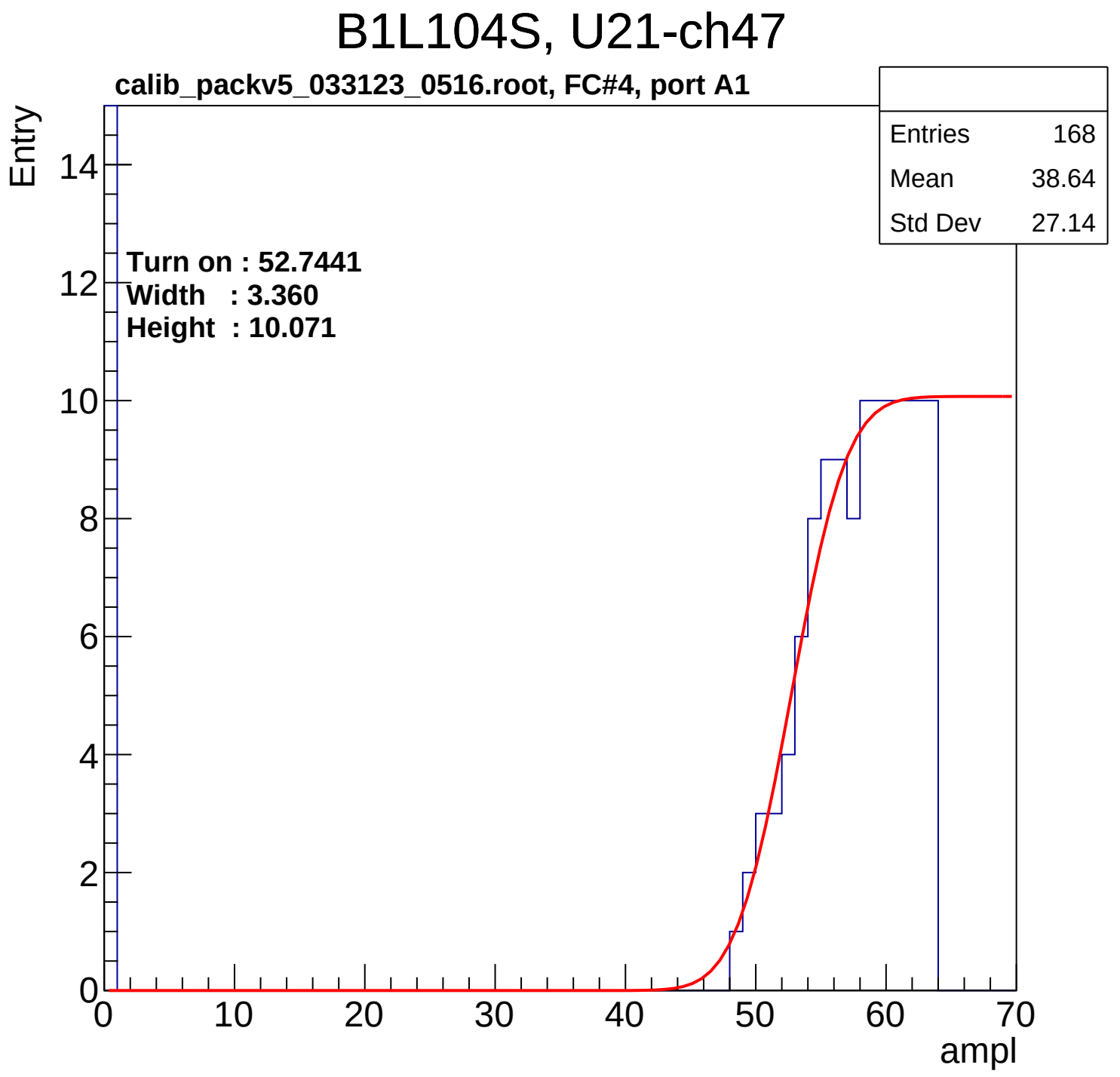
B1L104S, U21-ch47
calib_packv5_033123_0516.root, FC#4, port A1
Entries 168
Mean 38.64
Std Dev 27.14
Turn on : 52.7441
Width : 3.360
Height : 10.071
Entry
ampl
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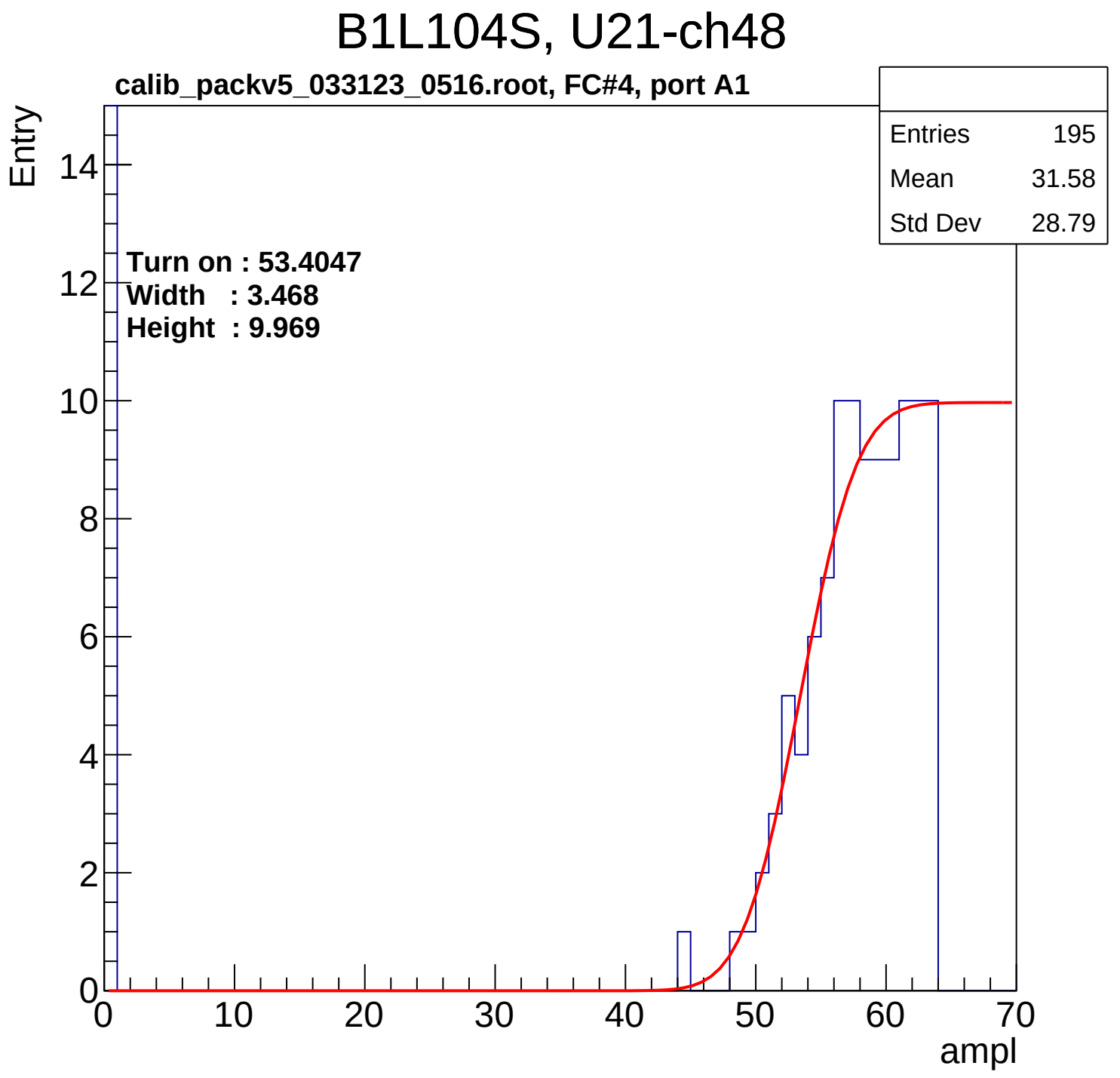
B1L104S, U21-ch48
calib_packv5_033123_0516.root, FC#4, port A1
Entries 195
Mean 31.58
Std Dev 28.79
Turn on : 53.4047
Width : 3.468
Height : 9.969
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ampl
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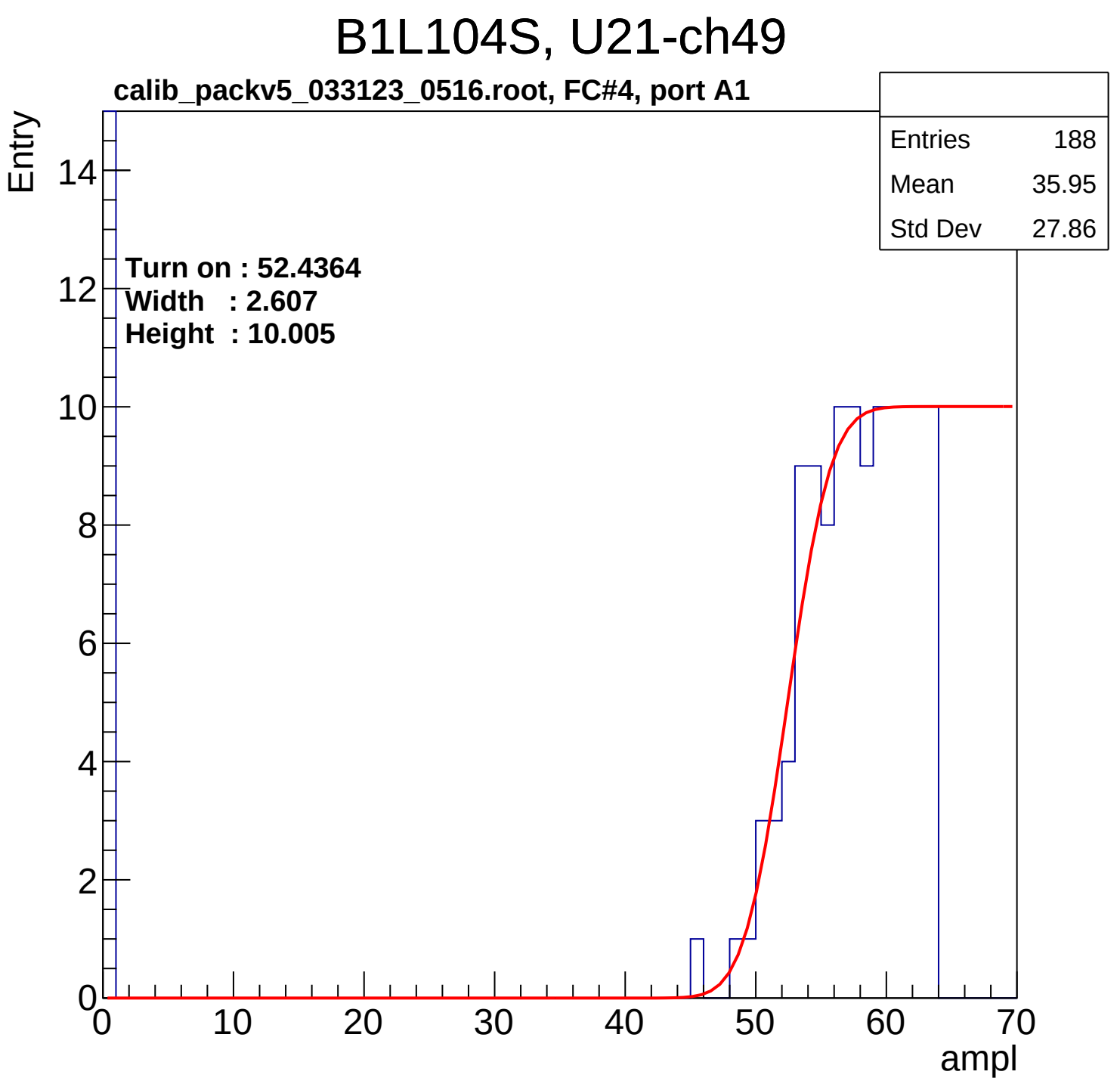
B1L104S, U21-ch49
calib_packv5_033123_0516.root, FC#4, port A1
Entries 188
Mean 35.95
Std Dev 27.86
Turn on : 52.4364
Width : 2.607
Height : 10.005
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ampl
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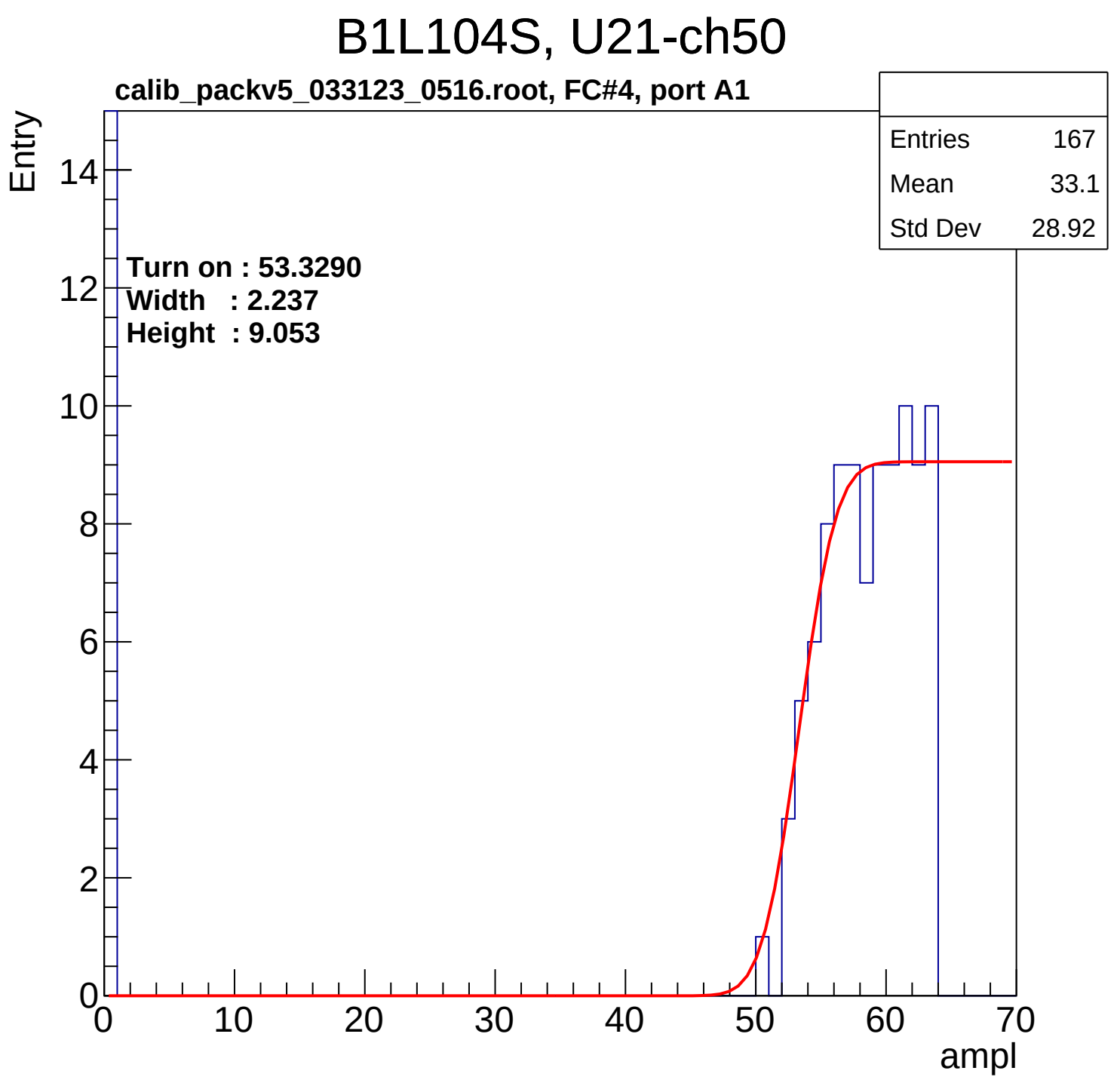
B1L104S, U21-ch50
calib_packv5_033123_0516.root, FC#4, port A1
Entries 167
Mean 33.1
Std Dev 28.92
Turn on : 53.3290
Width : 2.237
Height : 9.053
Entry
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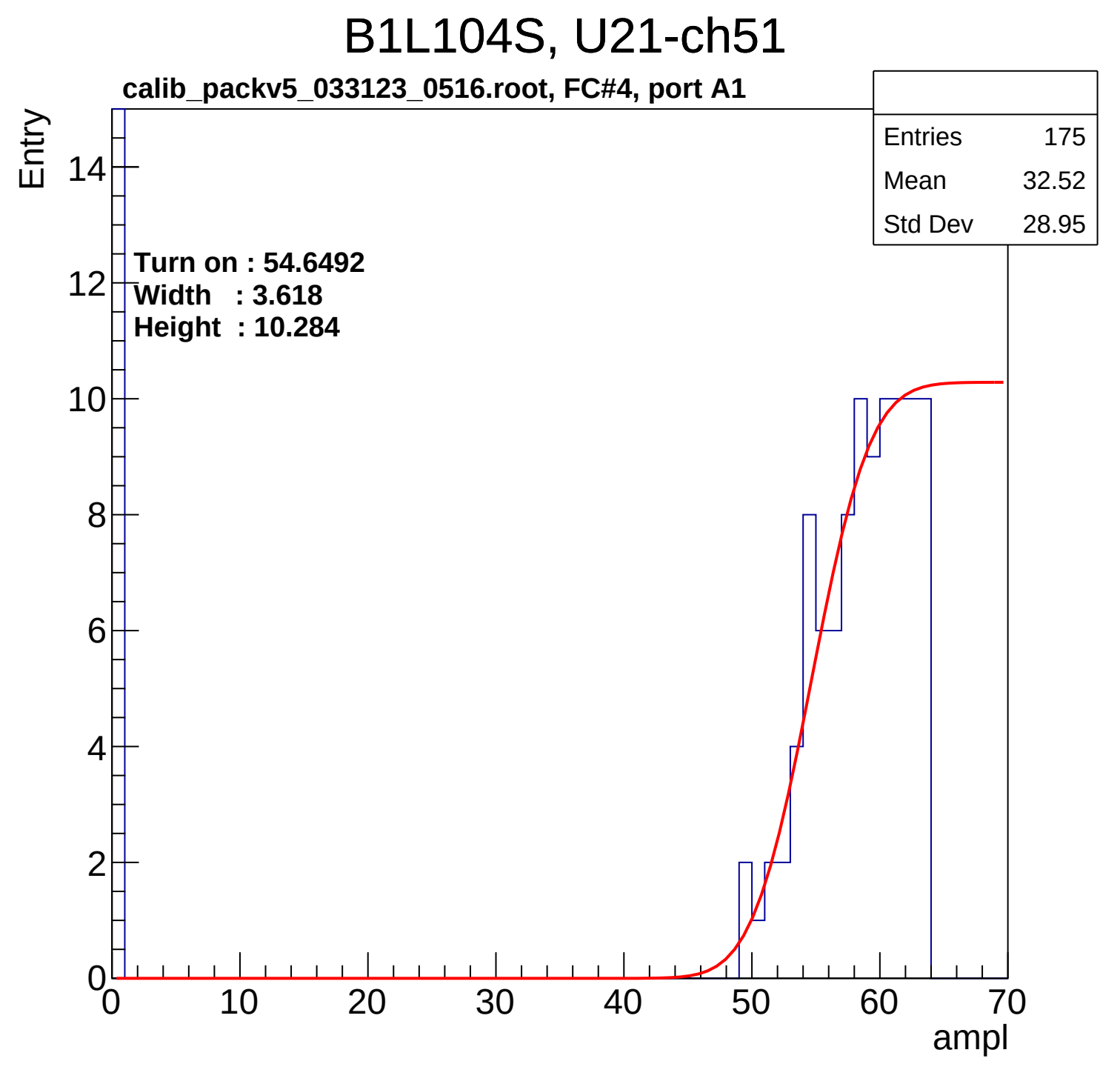
B1L104S, U21-ch51
calib_packv5_033123_0516.root, FC#4, port A1
Entries 175
Mean 32.52
Std Dev 28.95
Turn on : 54.6492
Width : 3.618
Height : 10.284
Entry
ampl
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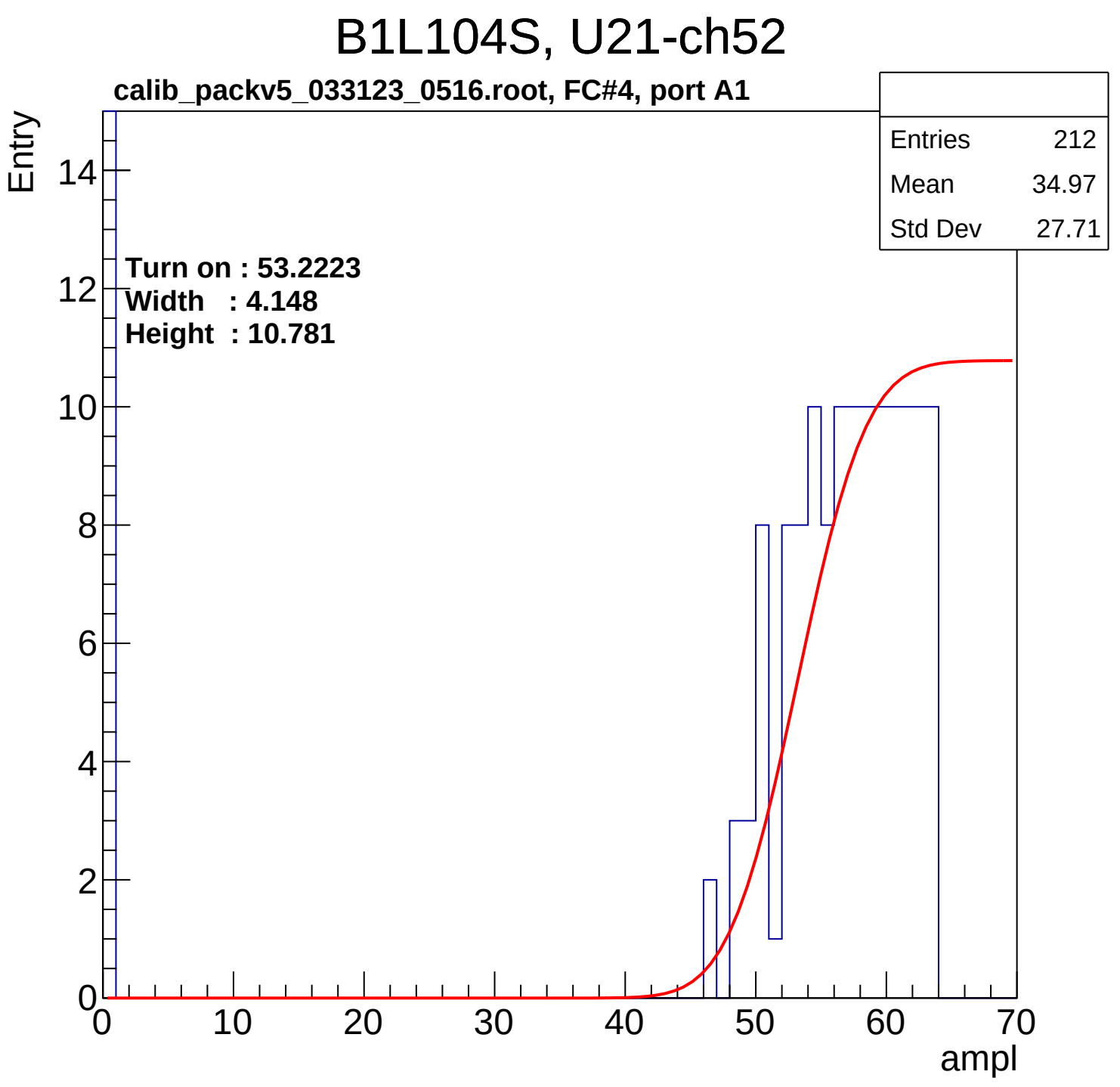
B1L104S, U21-ch52
calib_packv5_033123_0516.root, FC#4, port A1
Entries 212
Mean 34.97
Std Dev 27.71
Turn on : 53.2223
Width : 4.148
Height : 10.781
Entry
ampl
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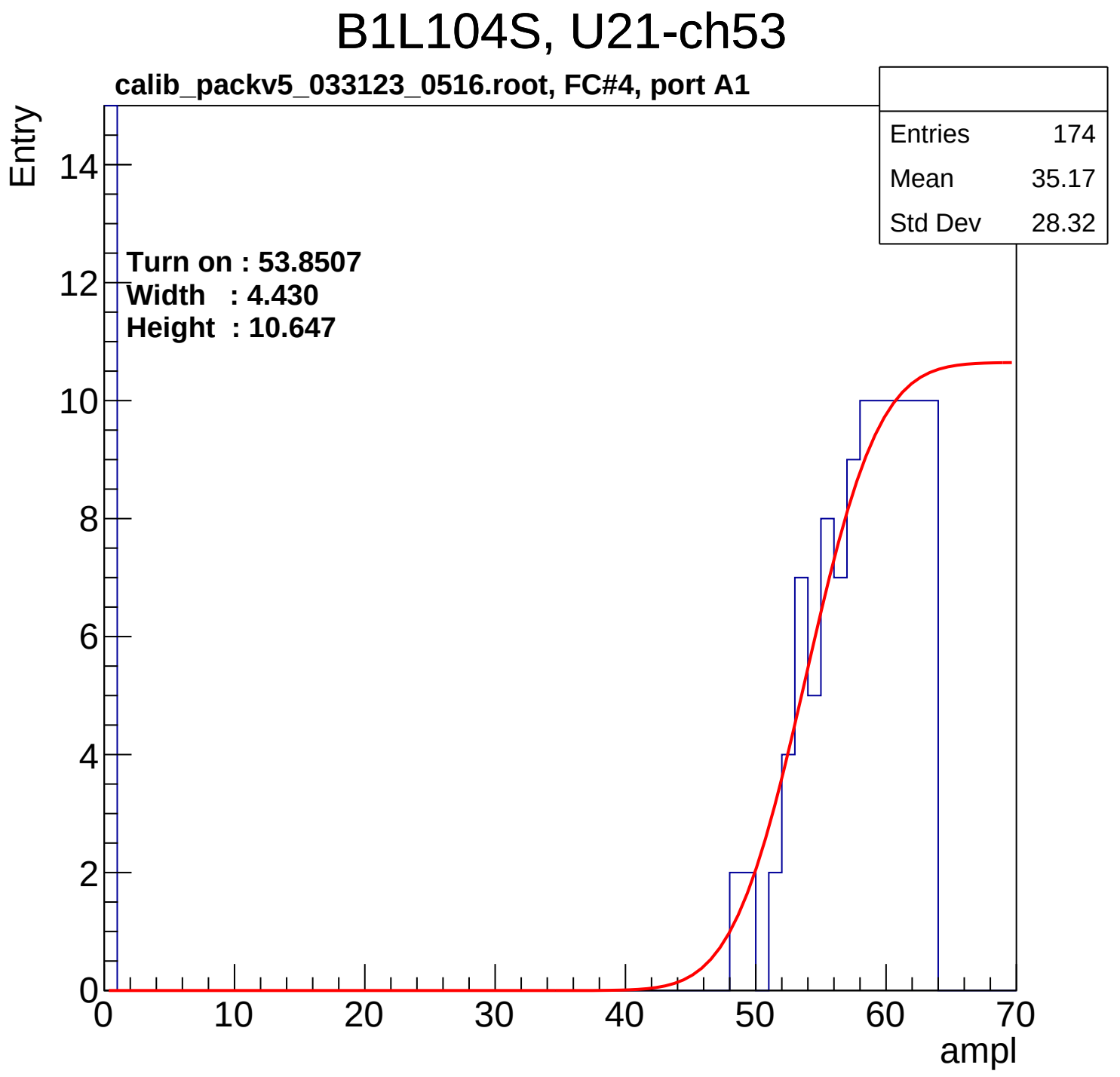
B1L104S, U21-ch53
calib_packv5_033123_0516.root, FC#4, port A1
Entries 174
Mean 35.17
Std Dev 28.32
Turn on : 53.8507
Width : 4.430
Height : 10.647
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ampl
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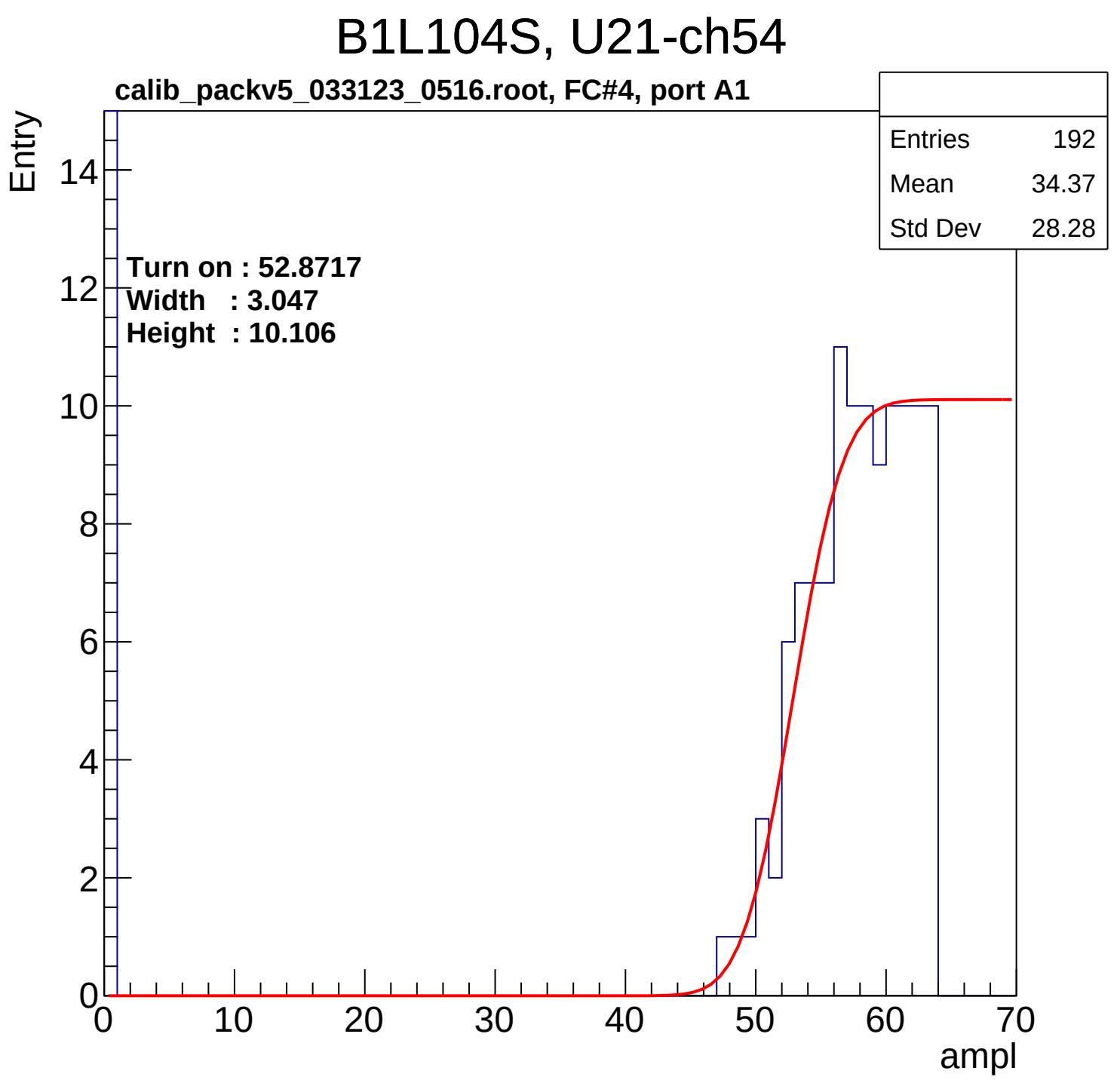
B1L104S, U21-ch54
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 34.37
Std Dev 28.28
Turn on : 52.8717
Width : 3.047
Height : 10.106
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ampl
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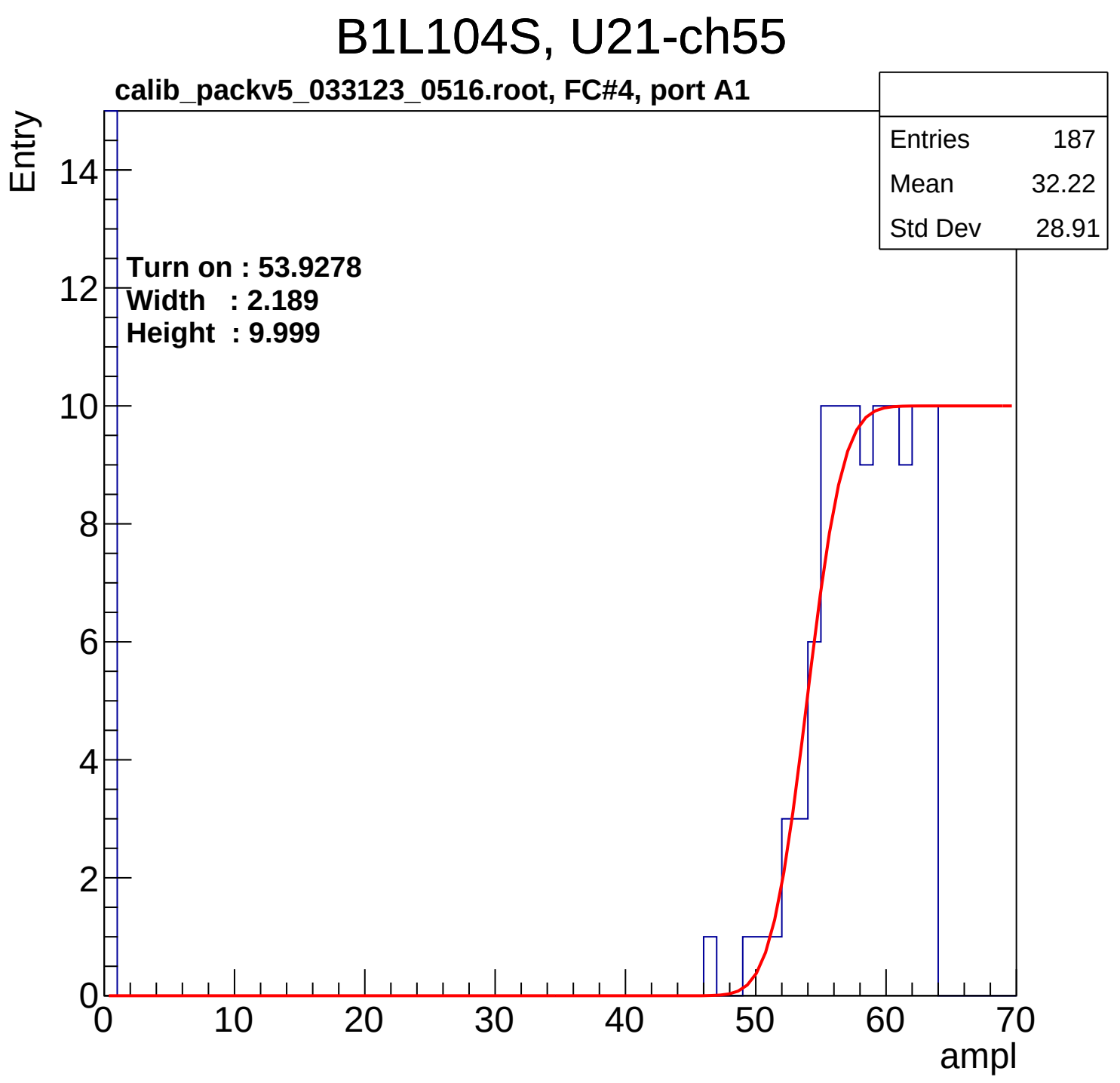
B1L104S, U21-ch55
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 32.22
Std Dev 28.91
Turn on : 53.9278
Width : 2.189
Height : 9.999
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ampl
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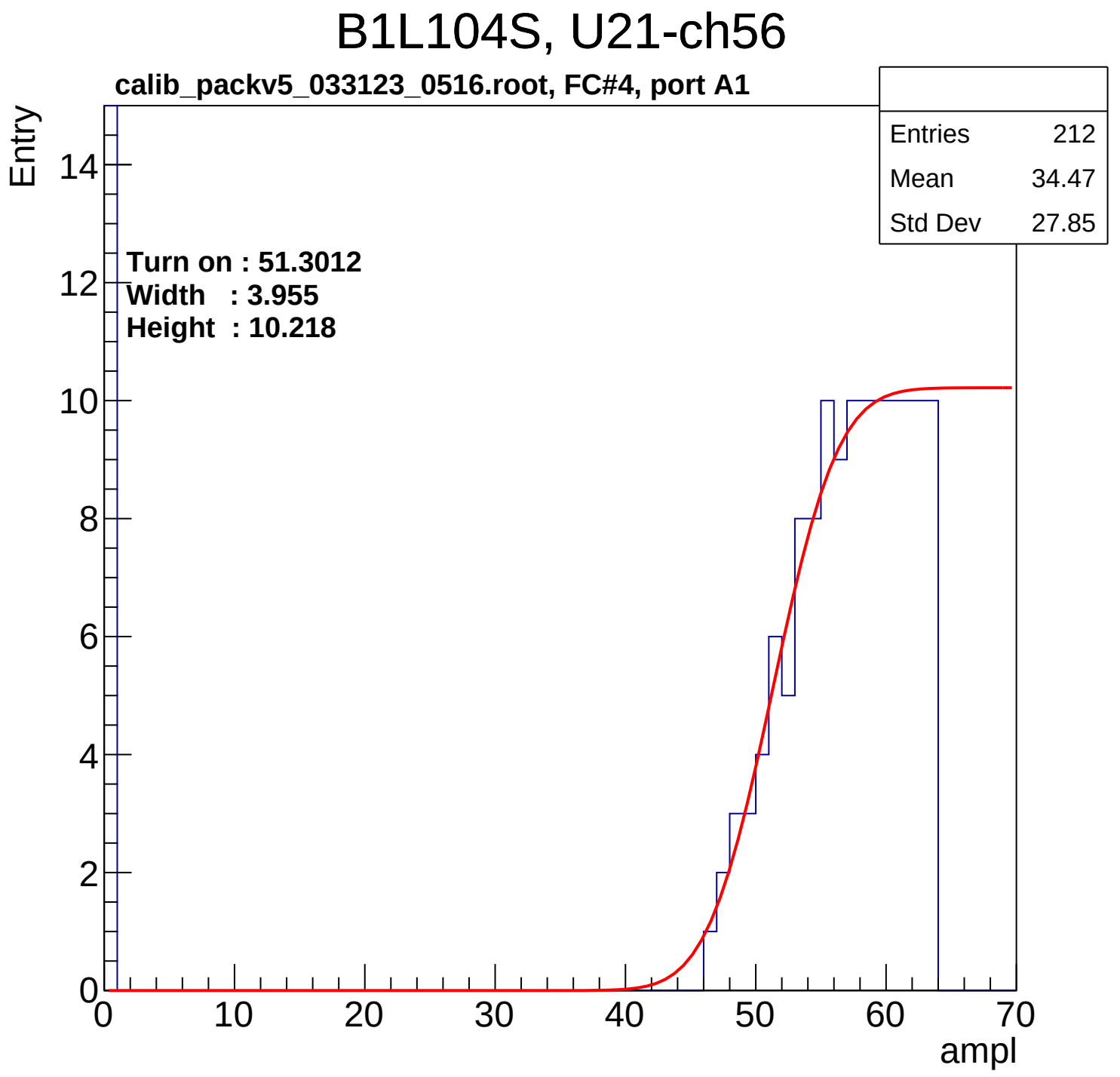
B1L104S, U21-ch56
calib_packv5_033123_0516.root, FC#4, port A1
Entries 212
Mean 34.47
Std Dev 27.85
Turn on : 51.3012
Width : 3.955
Height : 10.218
Entry
ampl
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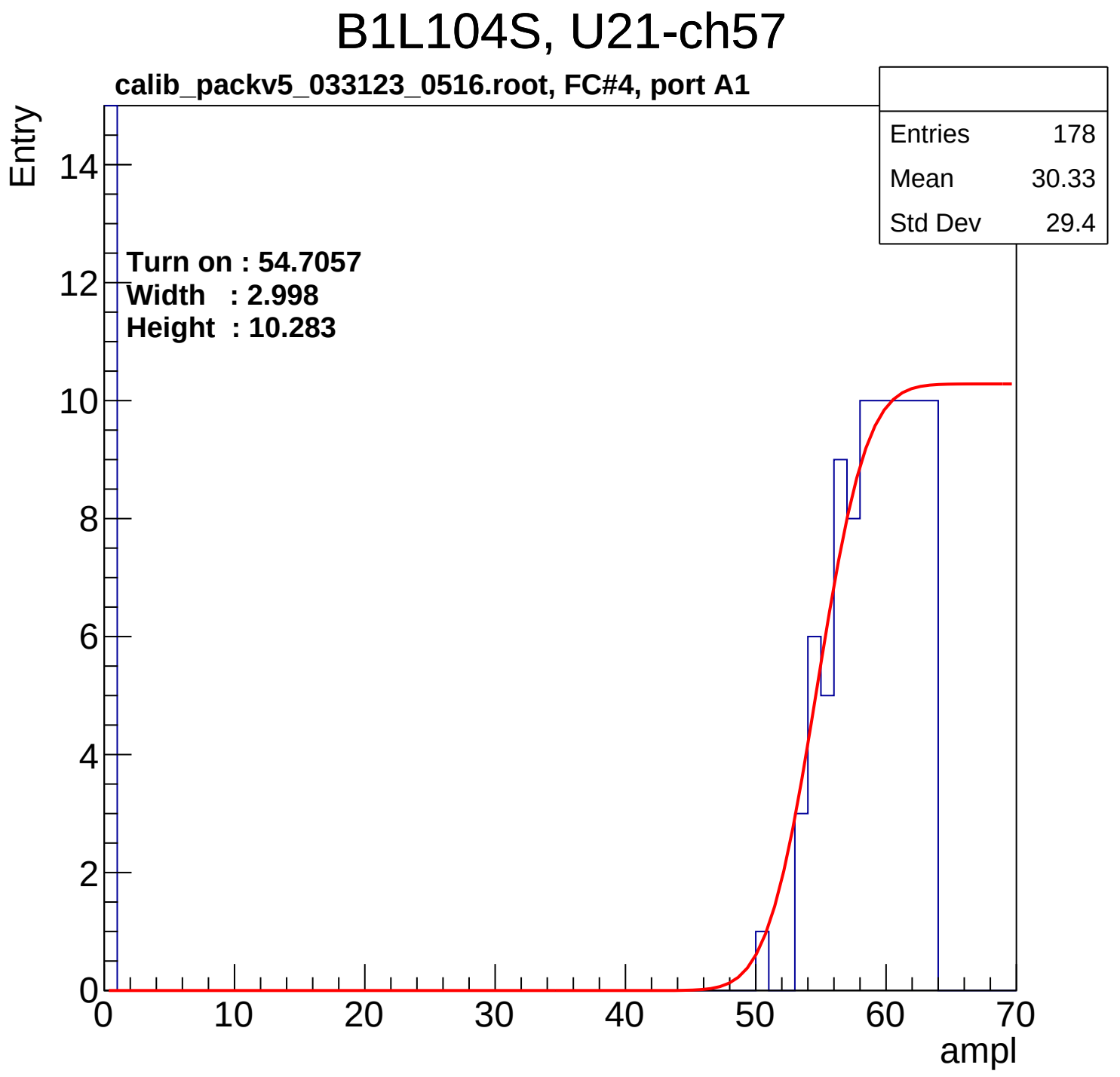
B1L104S, U21-ch57
calib_packv5_033123_0516.root, FC#4, port A1
Entries 178
Mean 30.33
Std Dev 29.4
Turn on : 54.7057
Width : 2.998
Height : 10.283
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ampl
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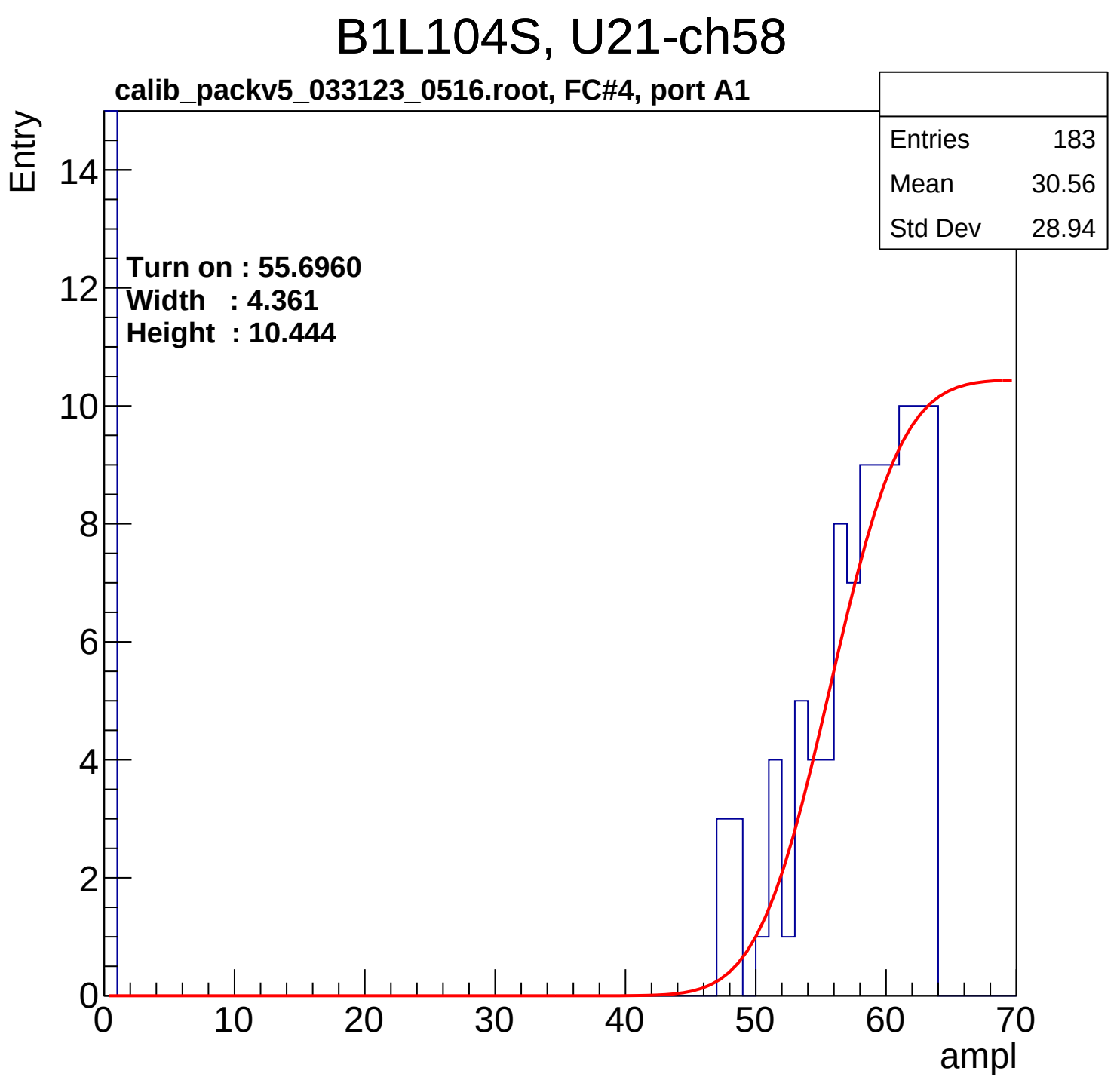
B1L104S, U21-ch58
calib_packv5_033123_0516.root, FC#4, port A1
Entries 183
Mean 30.56
Std Dev 28.94
Turn on : 55.6960
Width : 4.361
Height : 10.444
Entry
ampl
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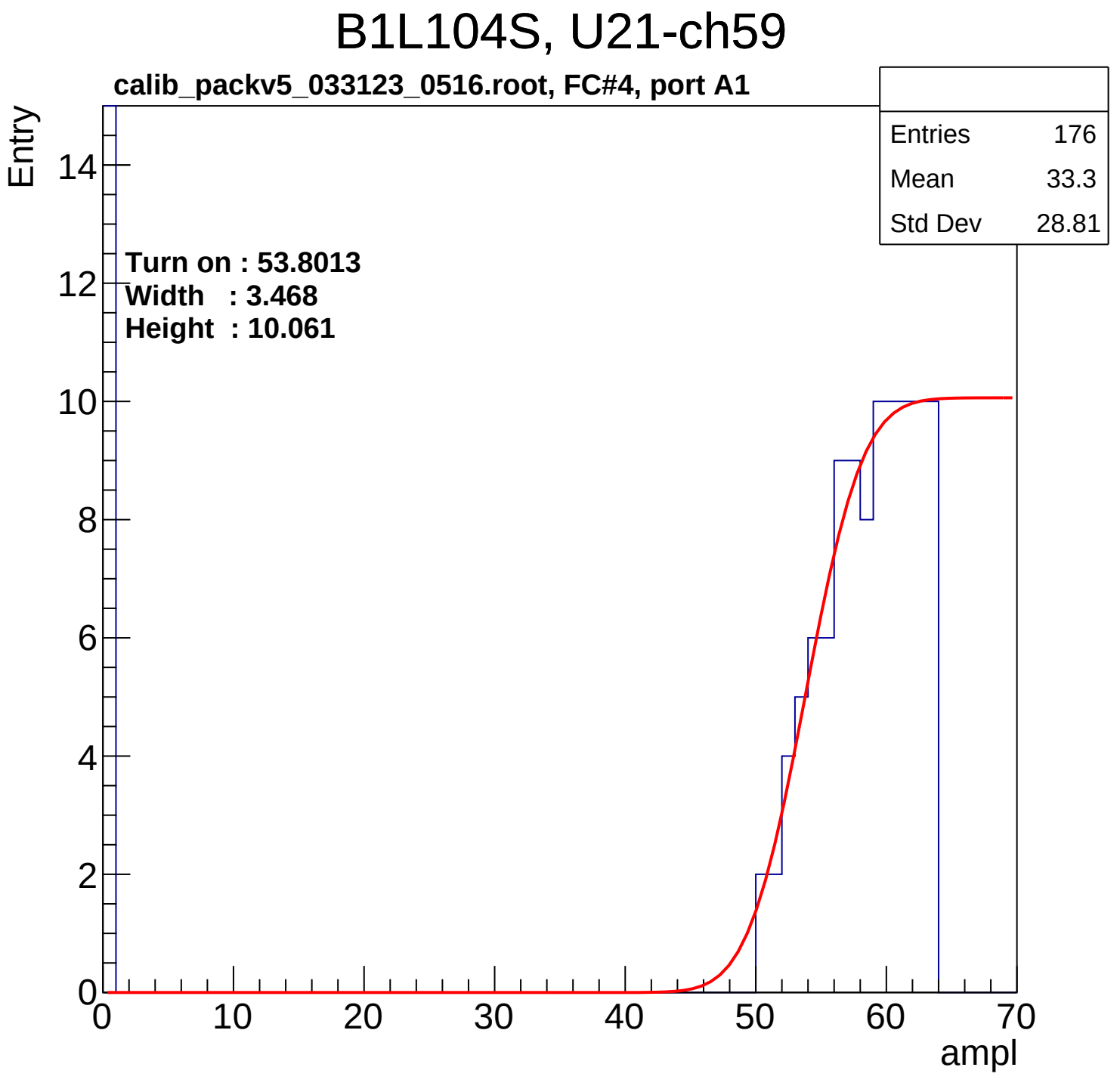
B1L104S, U21-ch59
calib_packv5_033123_0516.root, FC#4, port A1
Entries 176
Mean 33.3
Std Dev 28.81
Turn on : 53.8013
Width : 3.468
Height : 10.061
Entry
ampl
0
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4
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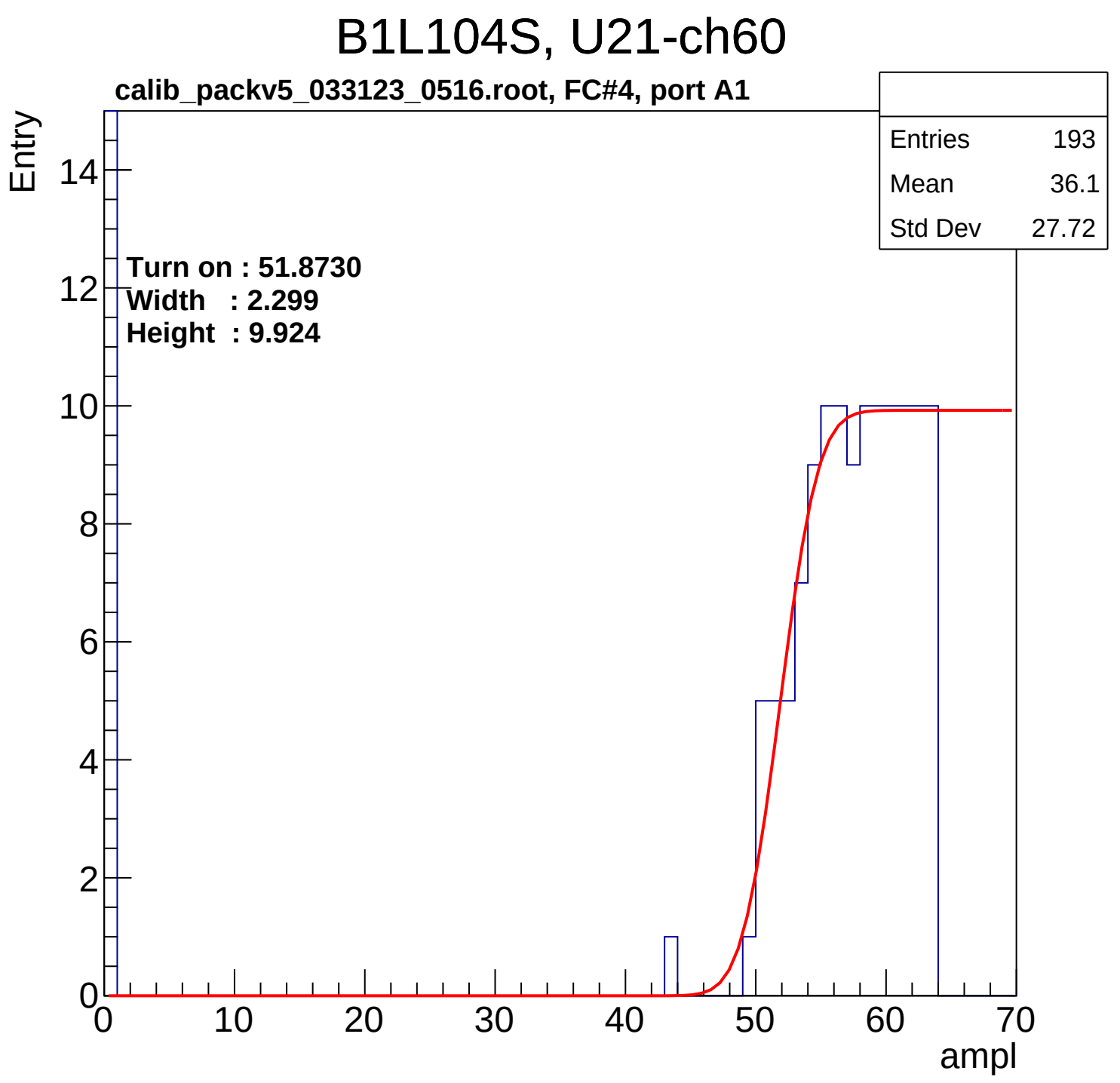
B1L104S, U21-ch60
calib_packv5_033123_0516.root, FC#4, port A1
Entries 193
Mean 36.1
Std Dev 27.72
Turn on : 51.8730
Width : 2.299
Height : 9.924
Entry
ampl
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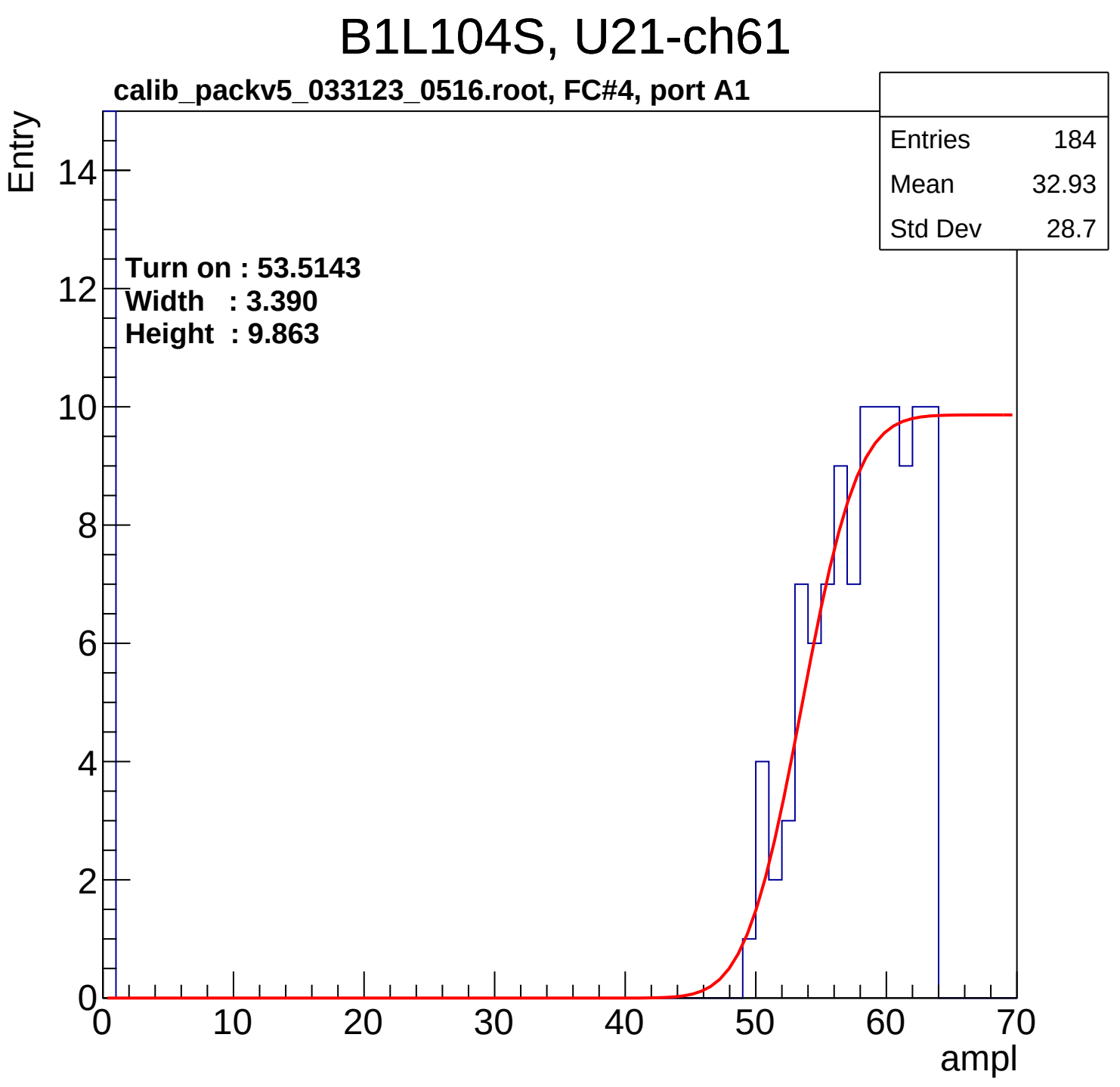
B1L104S, U21-ch61
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 32.93
Std Dev 28.7
Turn on : 53.5143
Width : 3.390
Height : 9.863
Entry
ampl
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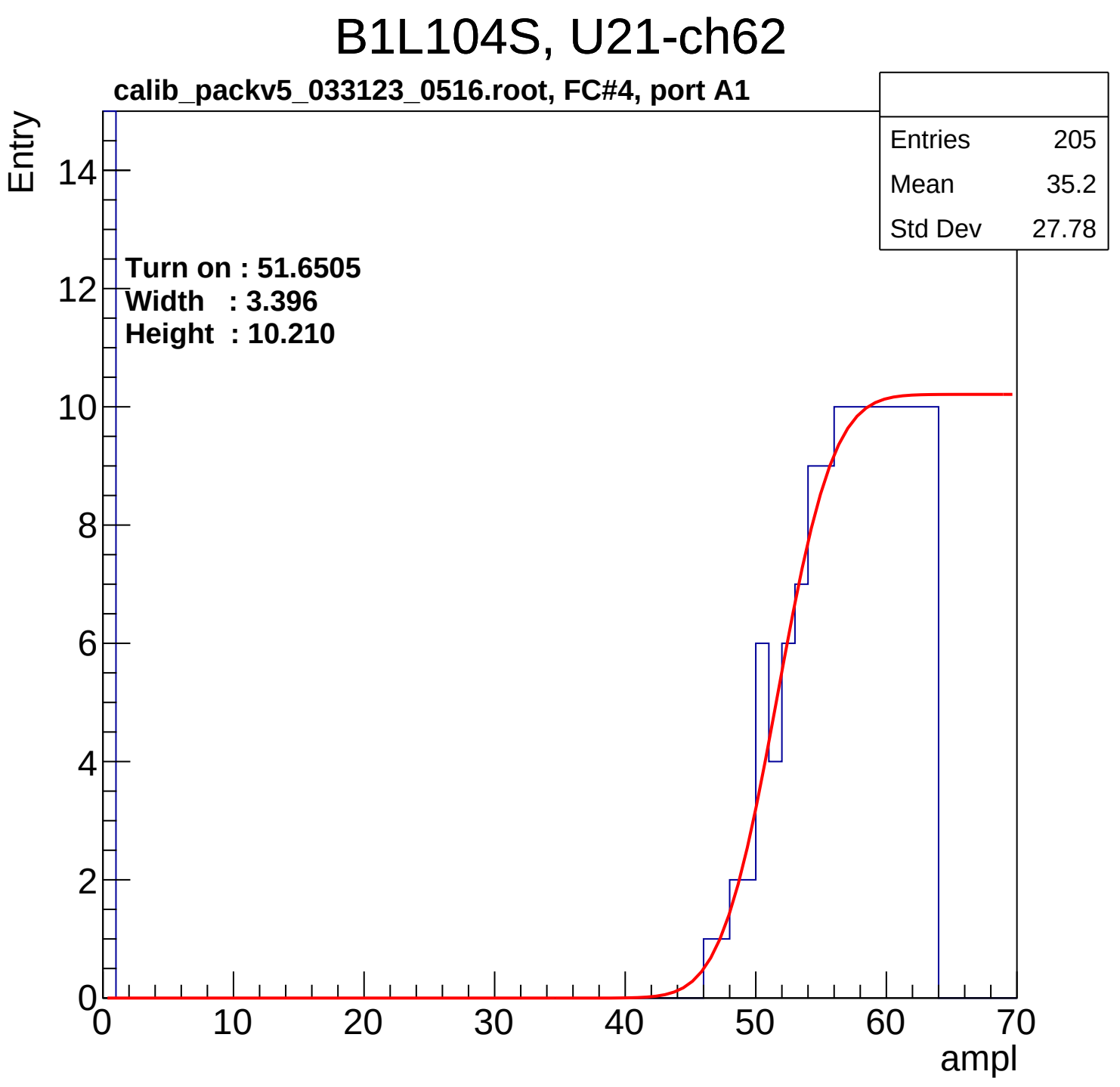
B1L104S, U21-ch62
calib_packv5_033123_0516.root, FC#4, port A1
Entries 205
Mean 35.2
Std Dev 27.78
Turn on : 51.6505
Width : 3.396
Height : 10.210
Entry
ampl
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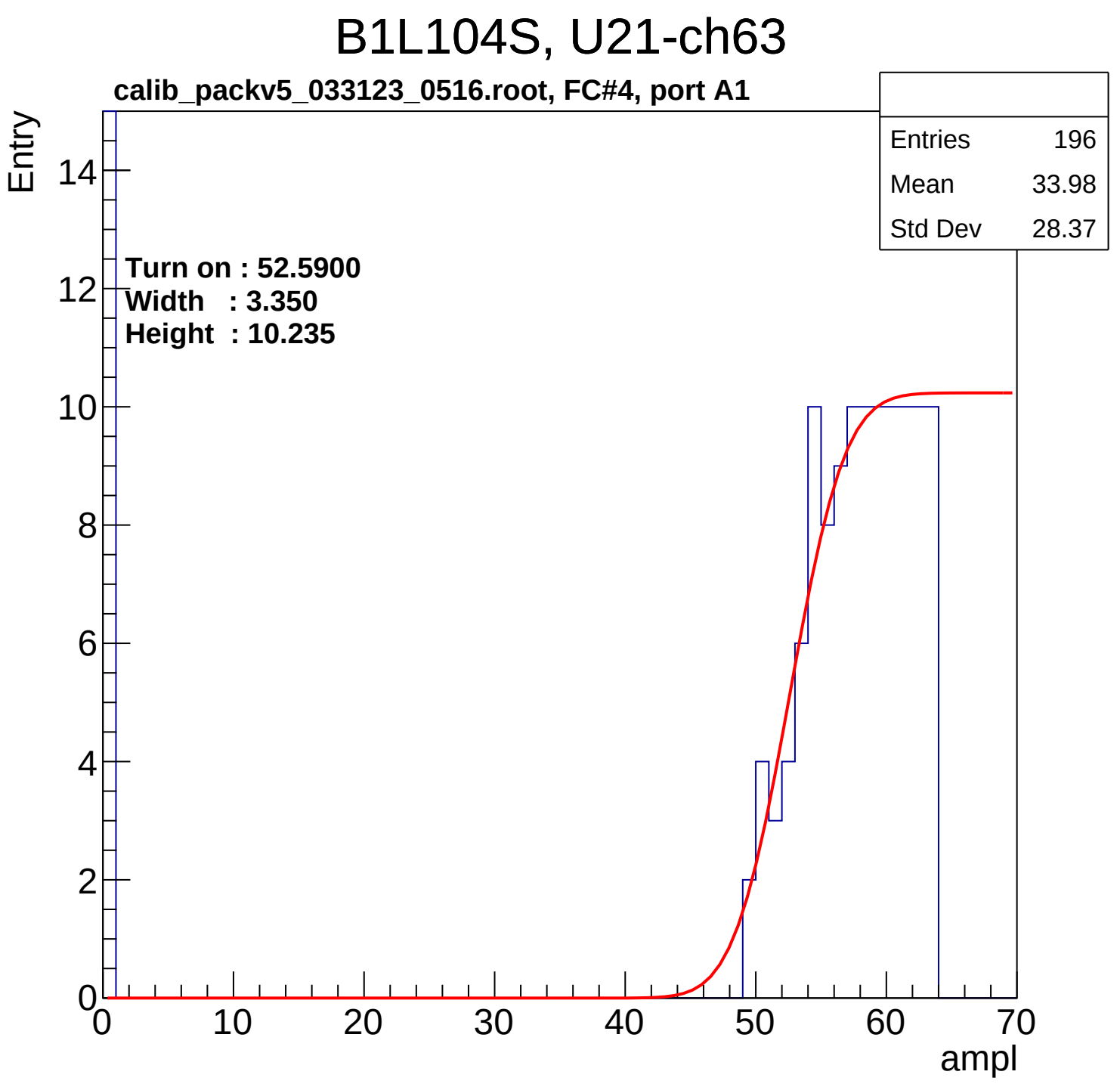
B1L104S, U21-ch63
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 33.98
Std Dev 28.37
Turn on : 52.5900
Width : 3.350
Height : 10.235
Entry
ampl
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6
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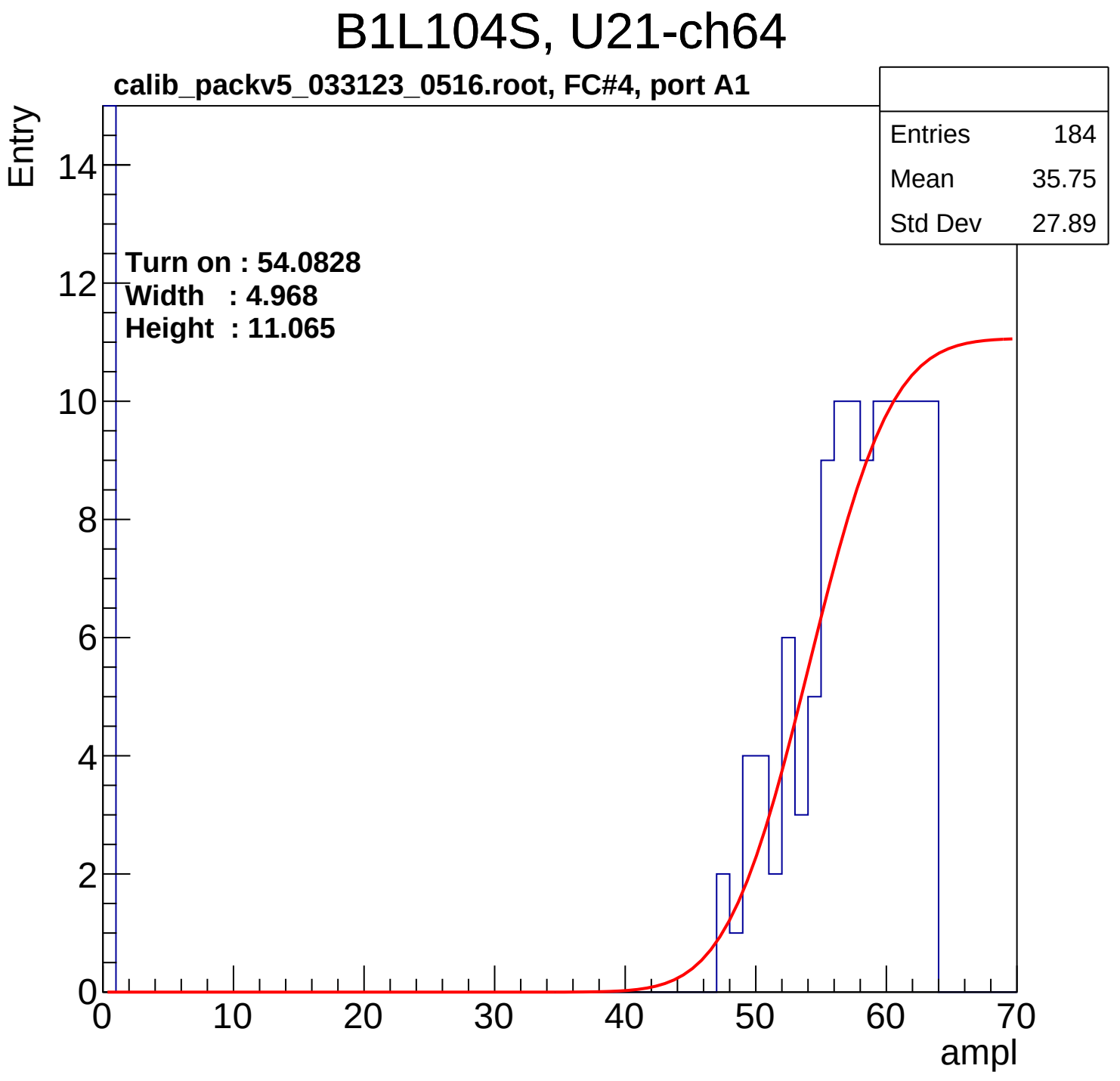
B1L104S, U21-ch64
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 35.75
Std Dev 27.89
Turn on : 54.0828
Width : 4.968
Height : 11.065
Entry
ampl
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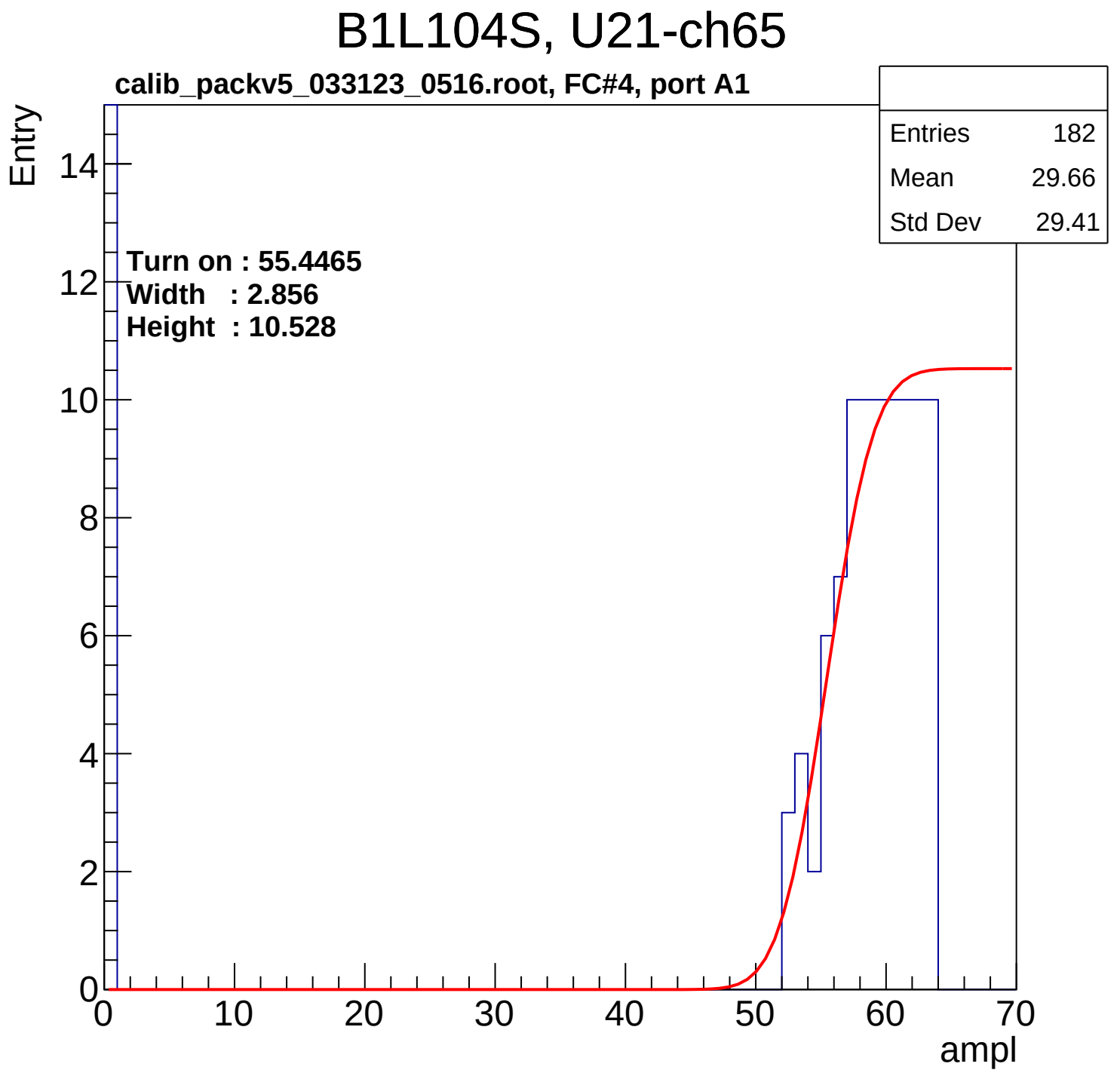
B1L104S, U21-ch65
calib_packv5_033123_0516.root, FC#4, port A1
Entries 182
Mean 29.66
Std Dev 29.41
Turn on : 55.4465
Width : 2.856
Height : 10.528
Entry
ampl
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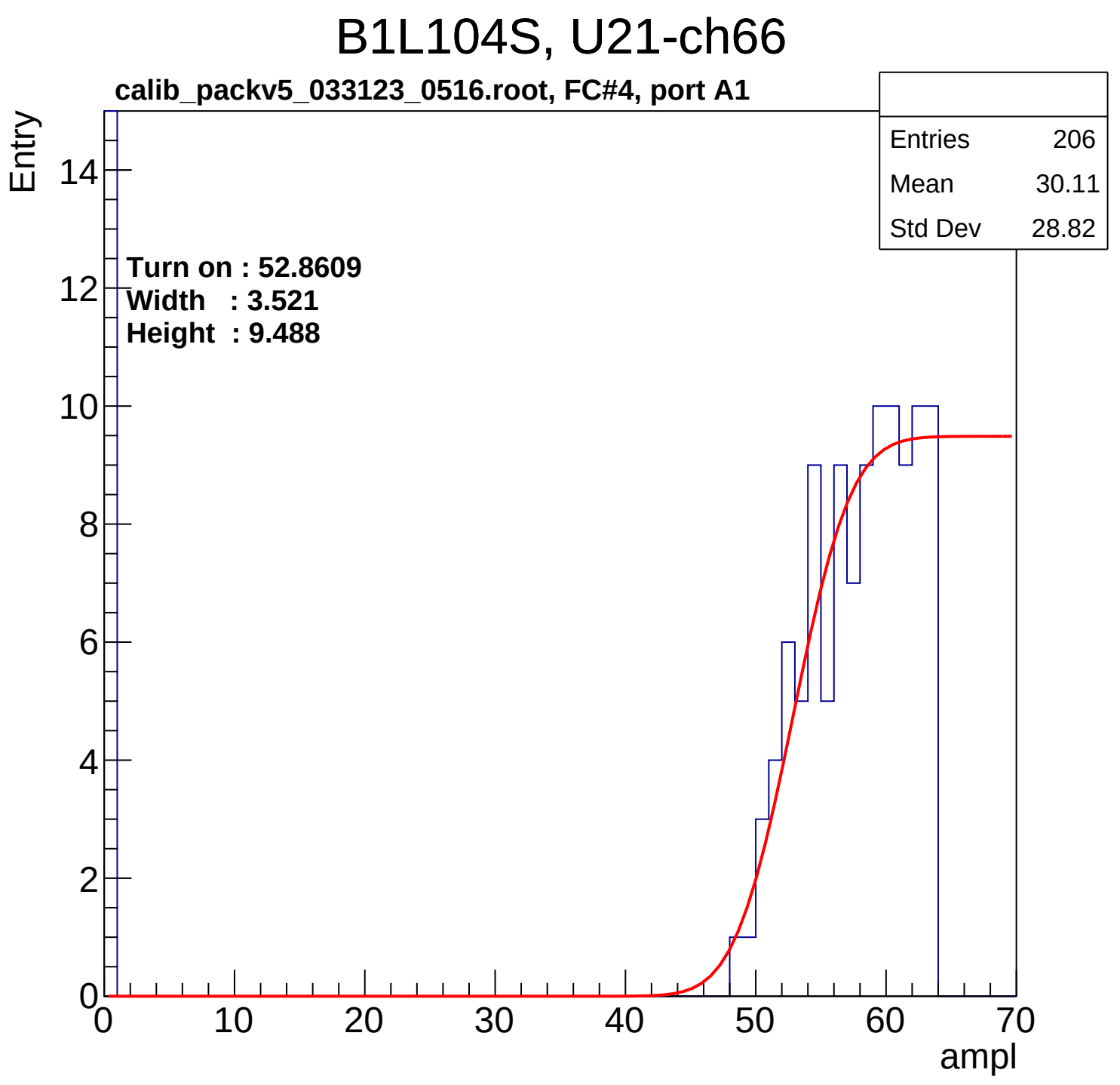
B1L104S, U21-ch66
calib_packv5_033123_0516.root, FC#4, port A1
Entries 206
Mean 30.11
Std Dev 28.82
Turn on : 52.8609
Width : 3.521
Height : 9.488
Entry
ampl
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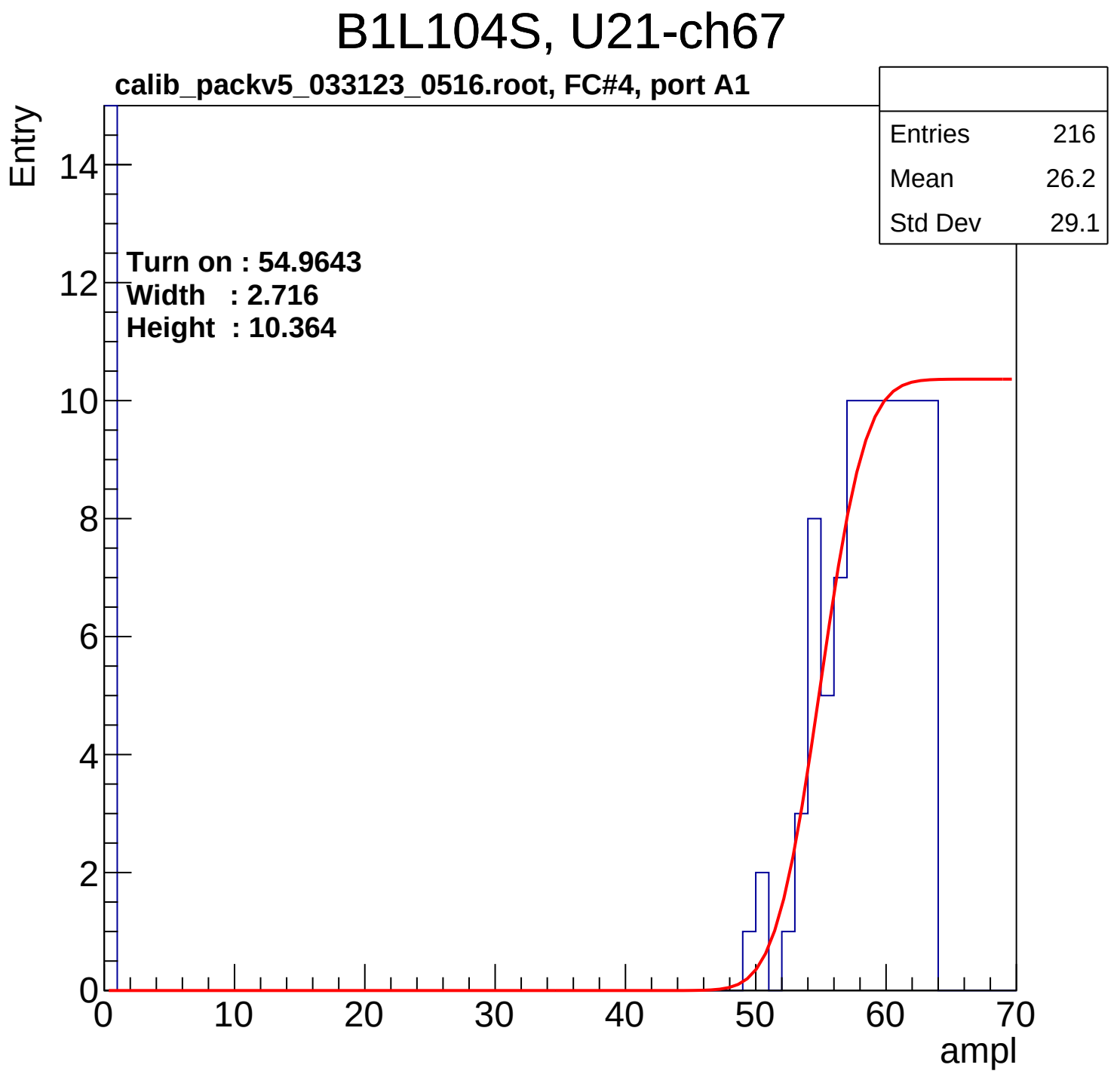
B1L104S, U21-ch67
calib_packv5_033123_0516.root, FC#4, port A1
Entries 216
Mean 26.2
Std Dev 29.1
Turn on : 54.9643
Width : 2.716
Height : 10.364
Entry
ampl
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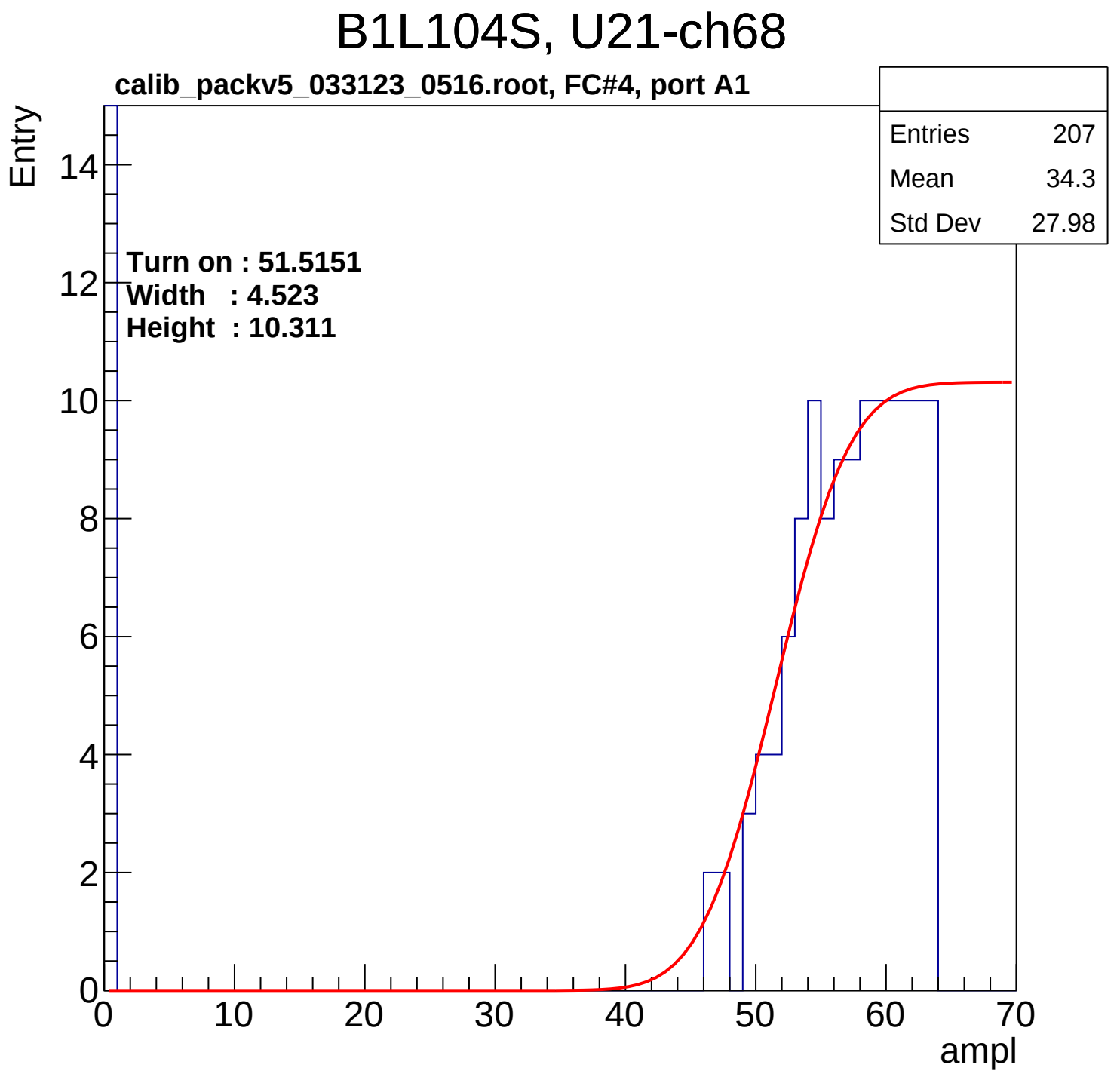
B1L104S, U21-ch68
calib_packv5_033123_0516.root, FC#4, port A1
Entries 207
Mean 34.3
Std Dev 27.98
Turn on : 51.5151
Width : 4.523
Height : 10.311
Entry
ampl
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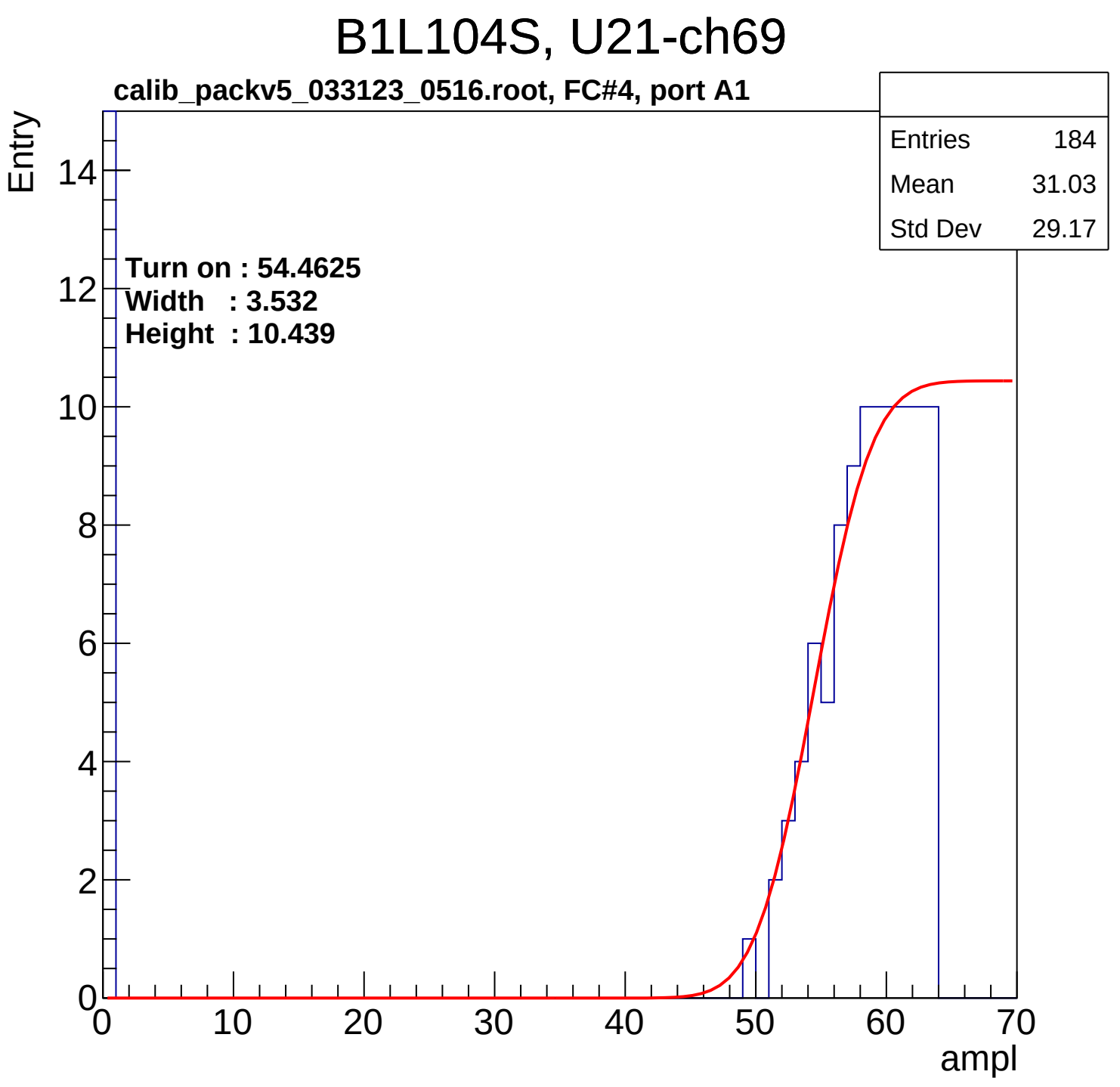
B1L104S, U21-ch69
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 31.03
Std Dev 29.17
Turn on : 54.4625
Width : 3.532
Height : 10.439
Entry
ampl
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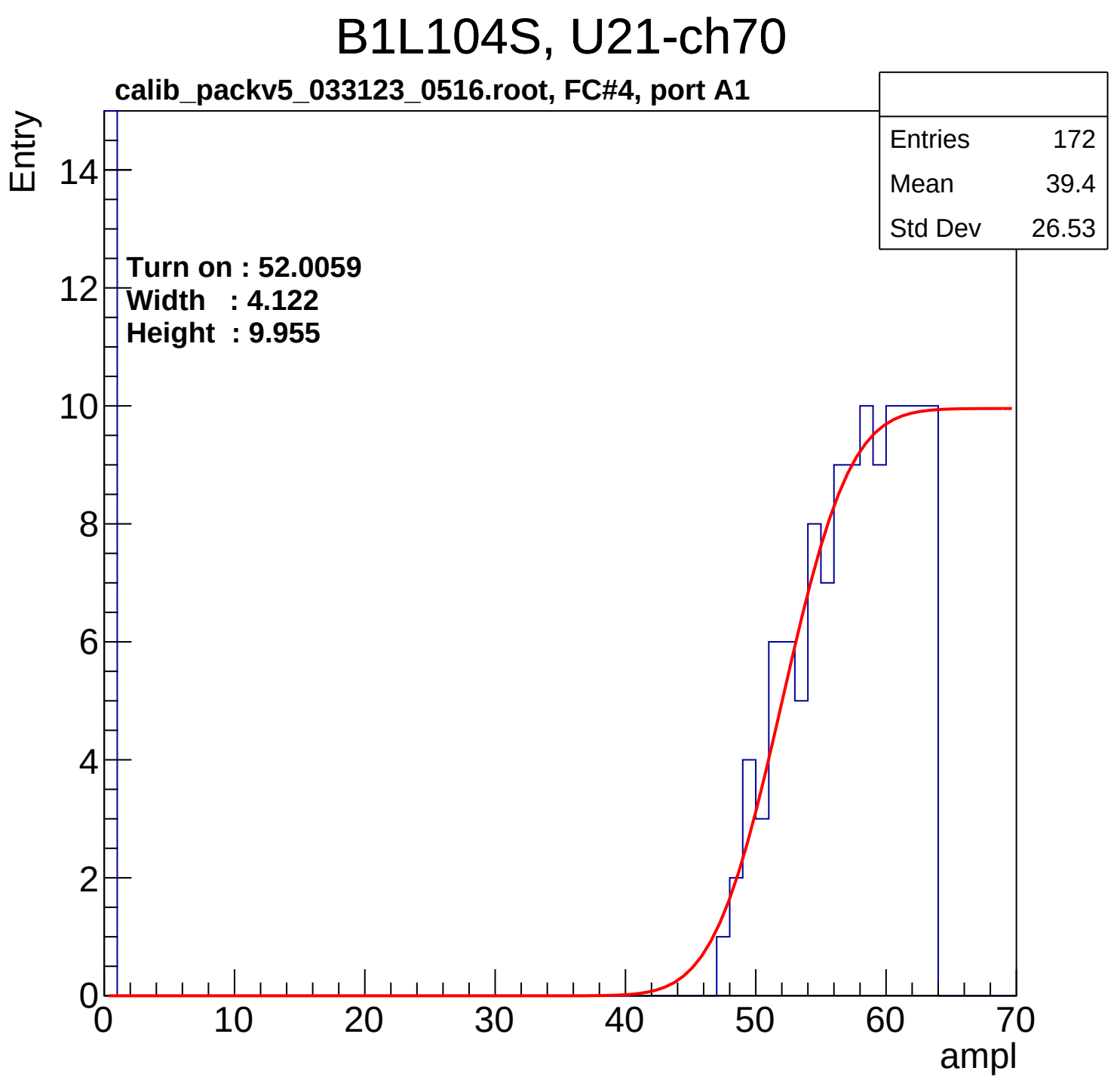
B1L104S, U21-ch70
calib_packv5_033123_0516.root, FC#4, port A1
Entries 172
Mean 39.4
Std Dev 26.53
Turn on : 52.0059
Width : 4.122
Height : 9.955
Entry
ampl
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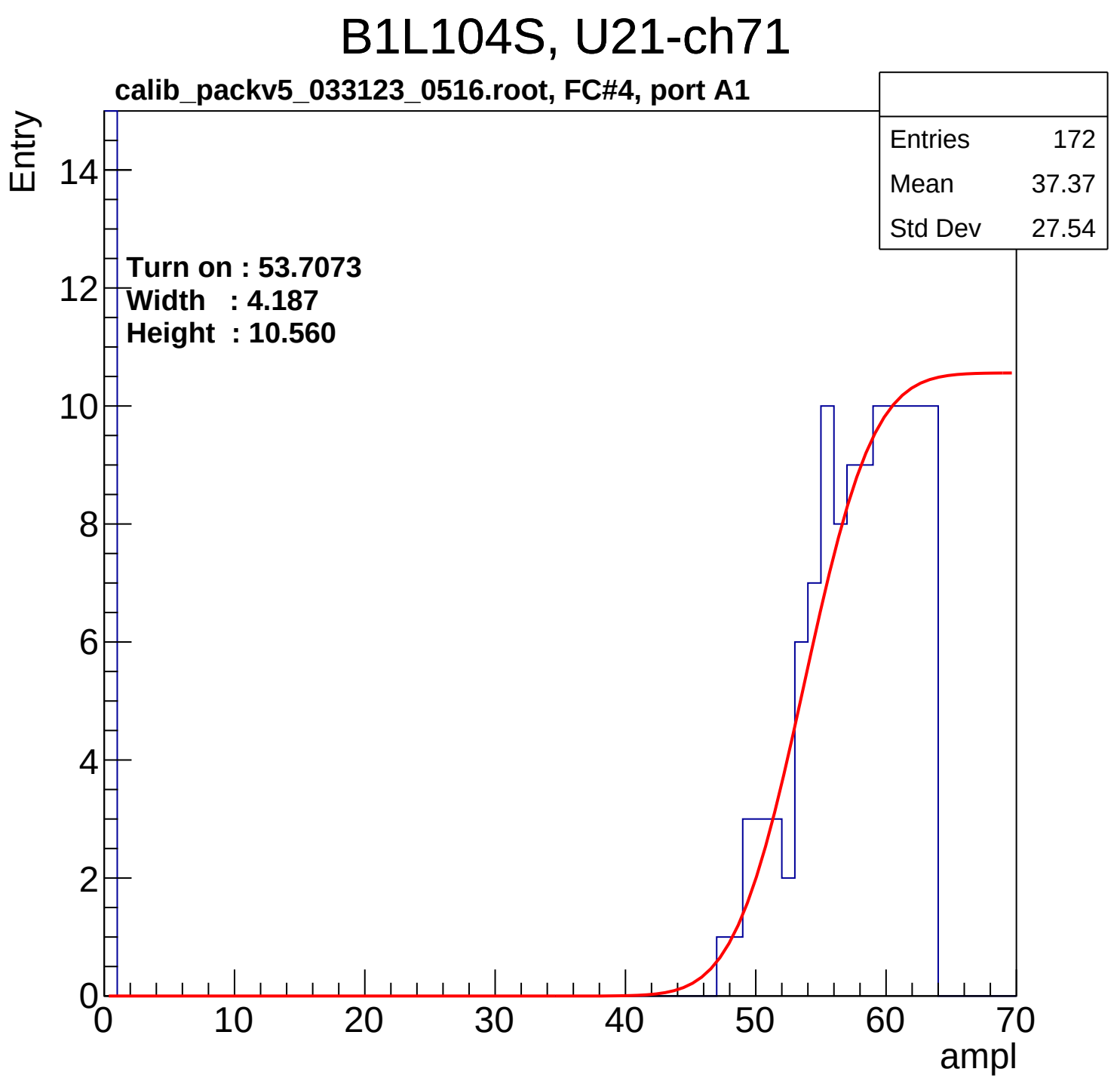
B1L104S, U21-ch71
calib_packv5_033123_0516.root, FC#4, port A1
Entries 172
Mean 37.37
Std Dev 27.54
Turn on : 53.7073
Width : 4.187
Height : 10.560
Entry
ampl
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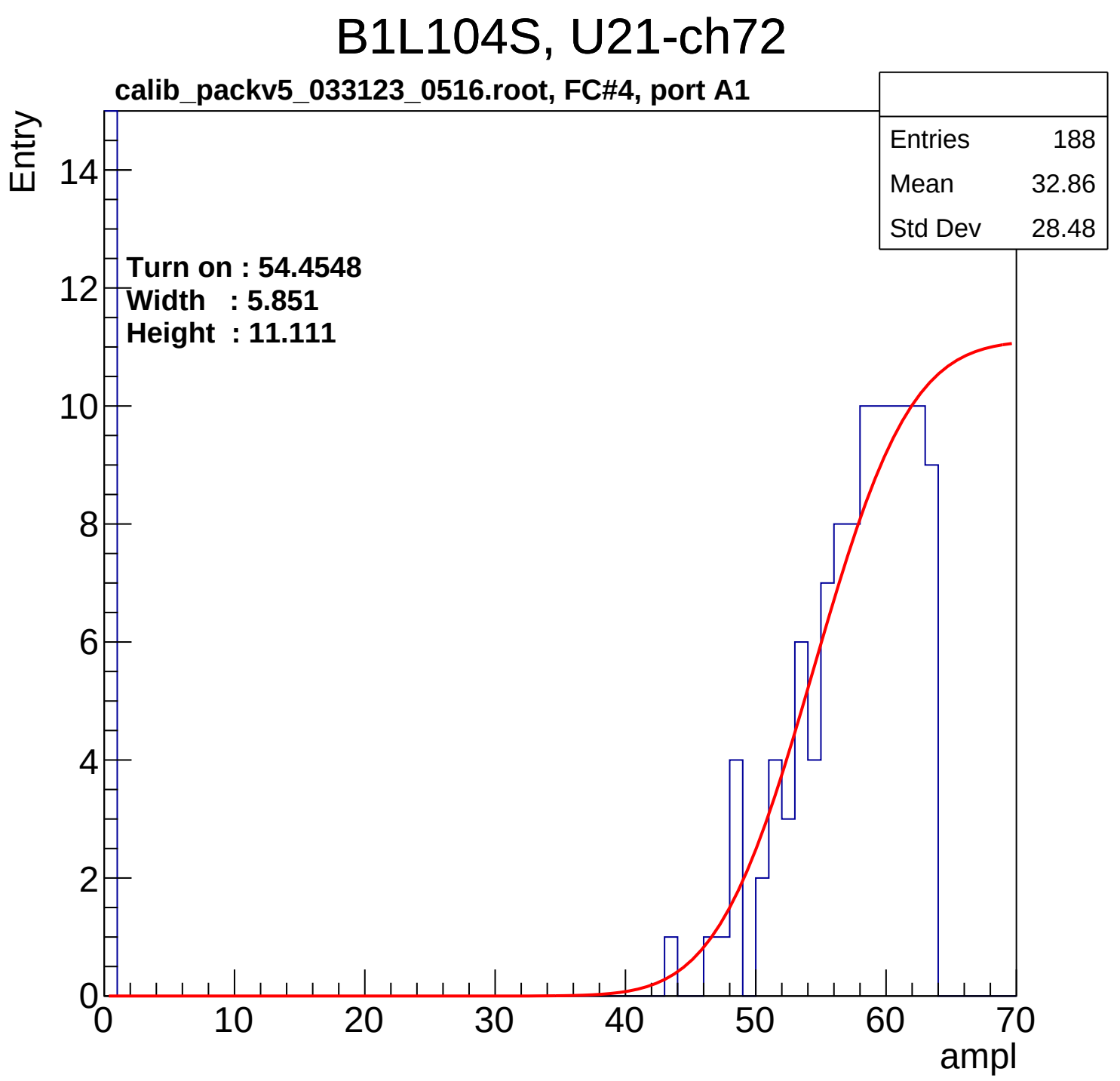
B1L104S, U21-ch72
calib_packv5_033123_0516.root, FC#4, port A1
Entries 188
Mean 32.86
Std Dev 28.48
Turn on : 54.4548
Width : 5.851
Height : 11.111
Entry
ampl
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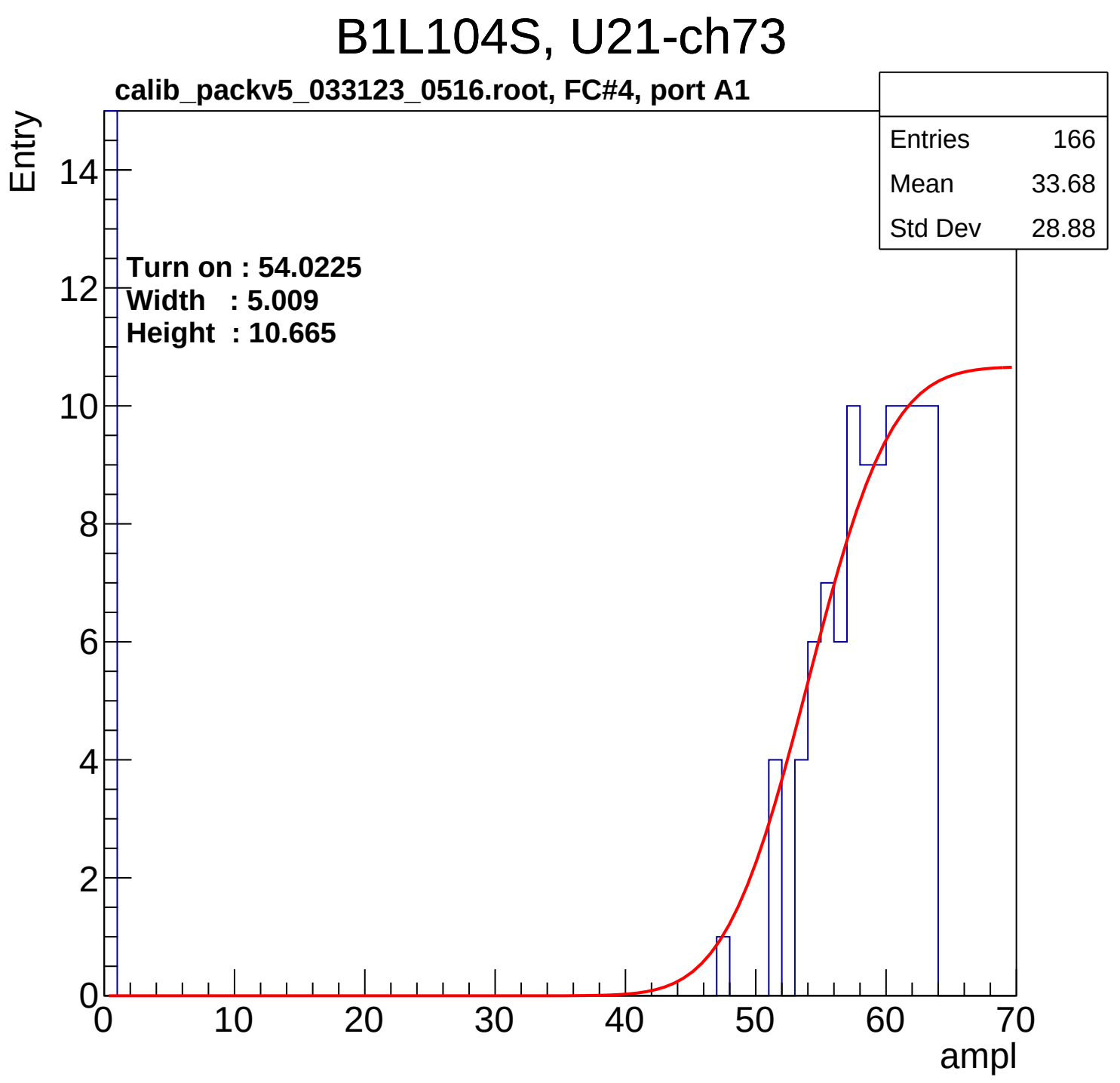
B1L104S, U21-ch73
calib_packv5_033123_0516.root, FC#4, port A1
Entries 166
Mean 33.68
Std Dev 28.88
Turn on : 54.0225
Width : 5.009
Height : 10.665
Entry
ampl
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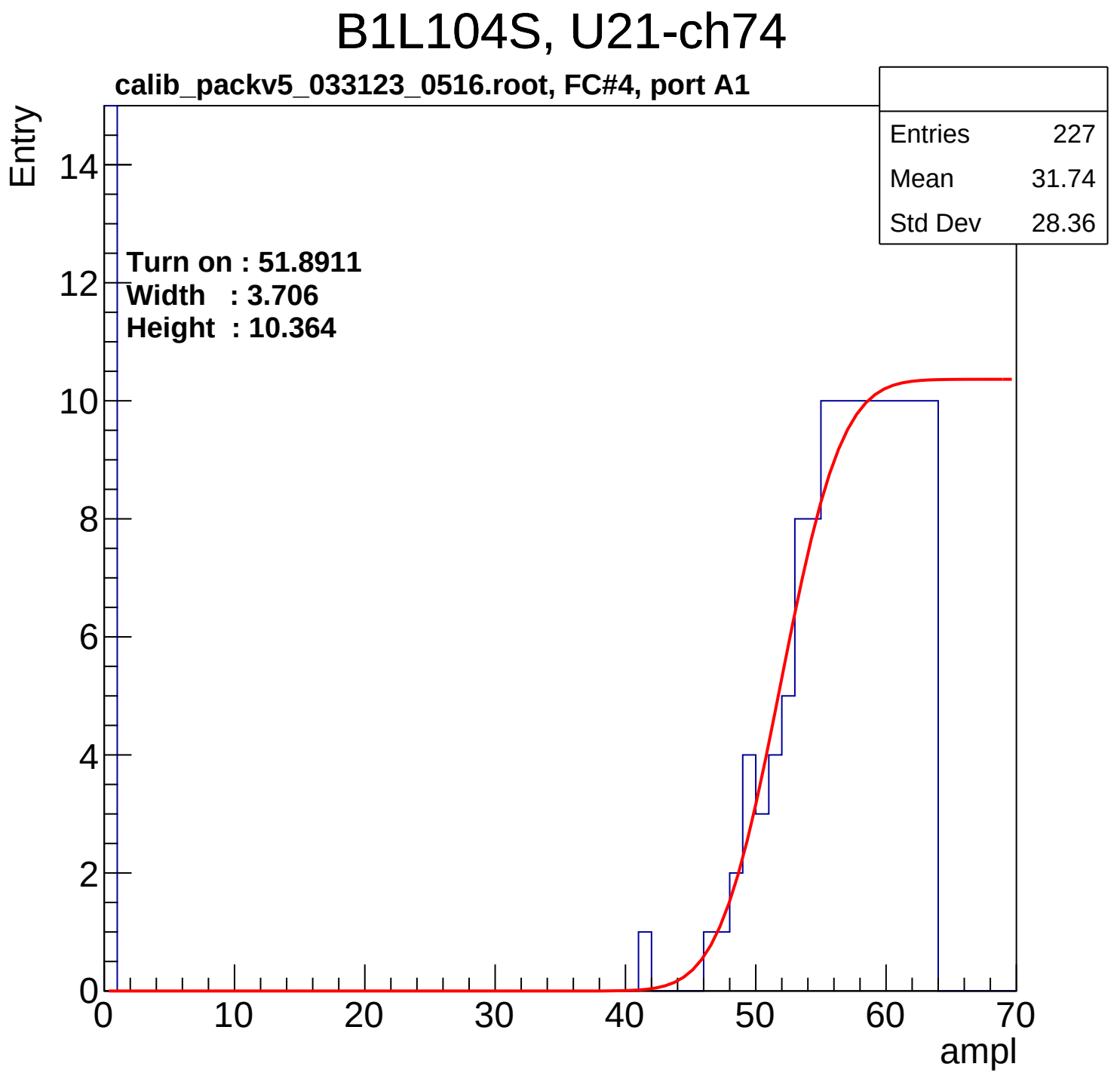
B1L104S, U21-ch74
calib_packv5_033123_0516.root, FC#4, port A1
Entries 227
Mean 31.74
Std Dev 28.36
Turn on : 51.8911
Width : 3.706
Height : 10.364
Entry
ampl
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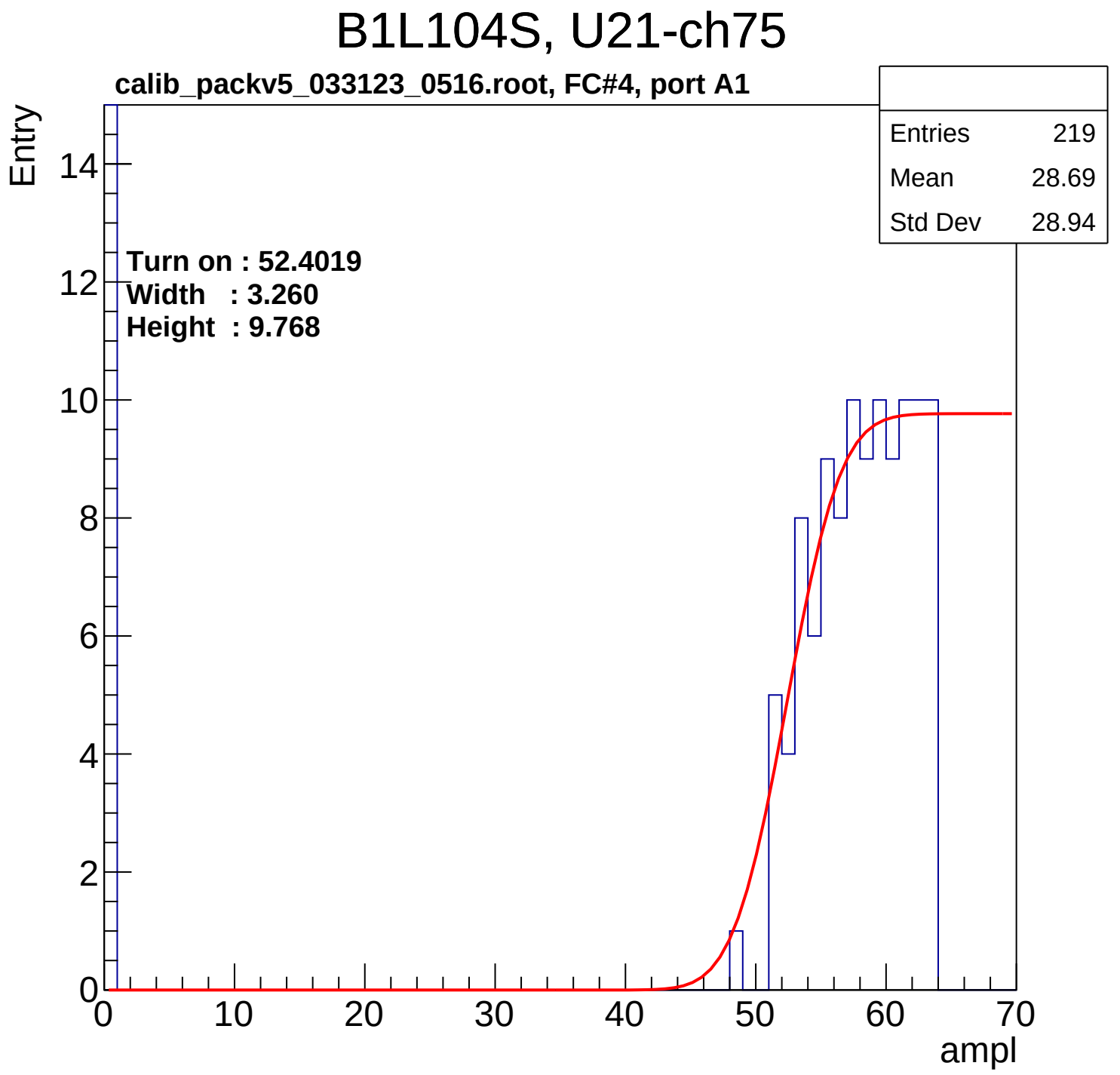
B1L104S, U21-ch75
calib_packv5_033123_0516.root, FC#4, port A1
Entries 219
Mean 28.69
Std Dev 28.94
Turn on : 52.4019
Width : 3.260
Height : 9.768
Entry
ampl
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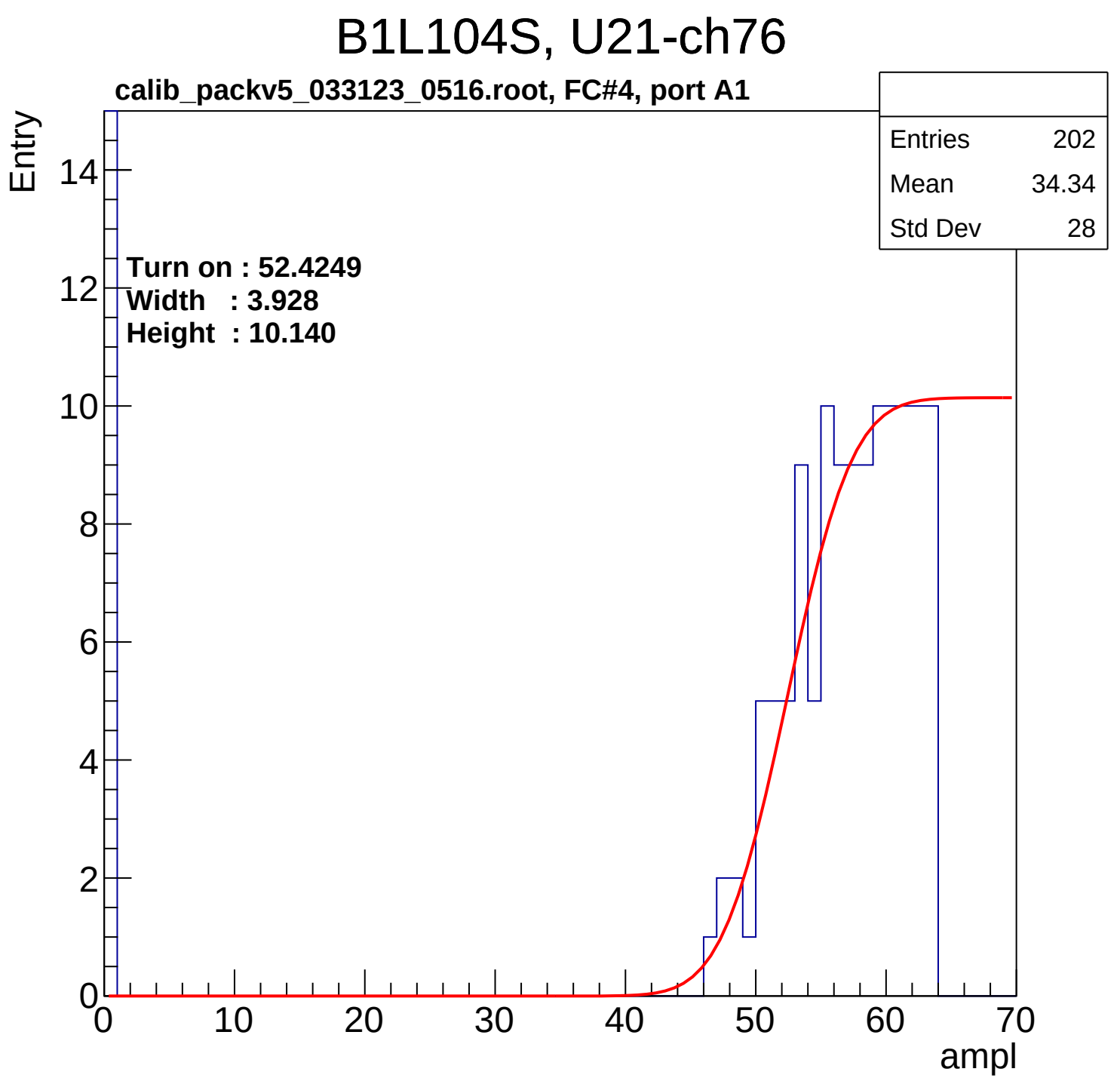
B1L104S, U21-ch76
calib_packv5_033123_0516.root, FC#4, port A1
Entries 202
Mean 34.34
Std Dev 28
Turn on : 52.4249
Width : 3.928
Height : 10.140
Entry
ampl
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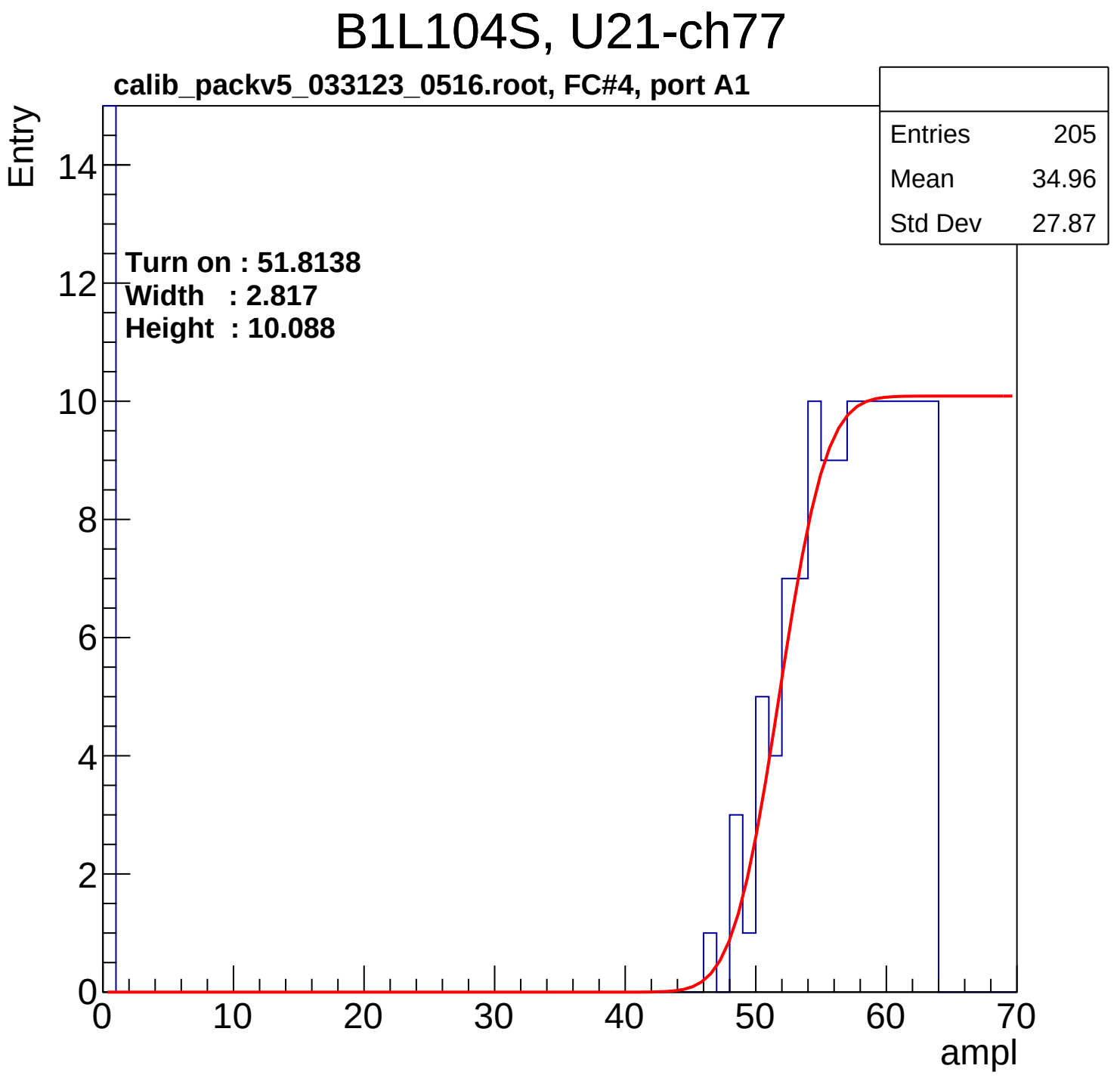
B1L104S, U21-ch77
calib_packv5_033123_0516.root, FC#4, port A1
Entries 205
Mean 34.96
Std Dev 27.87
Turn on : 51.8138
Width : 2.817
Height : 10.088
Entry
ampl
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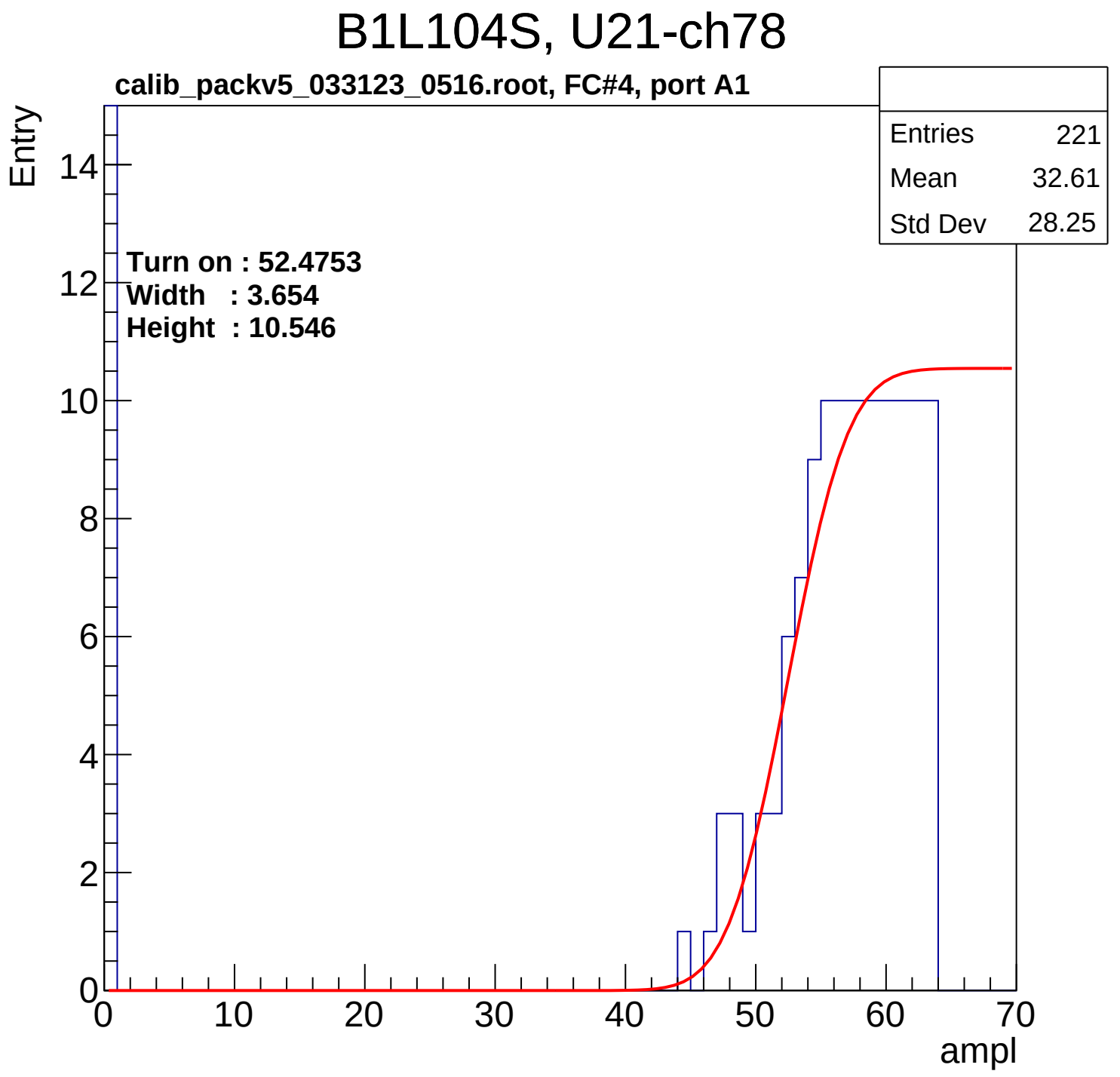
B1L104S, U21-ch78
calib_packv5_033123_0516.root, FC#4, port A1
Entries 221
Mean 32.61
Std Dev 28.25
Turn on : 52.4753
Width : 3.654
Height : 10.546
Entry
ampl
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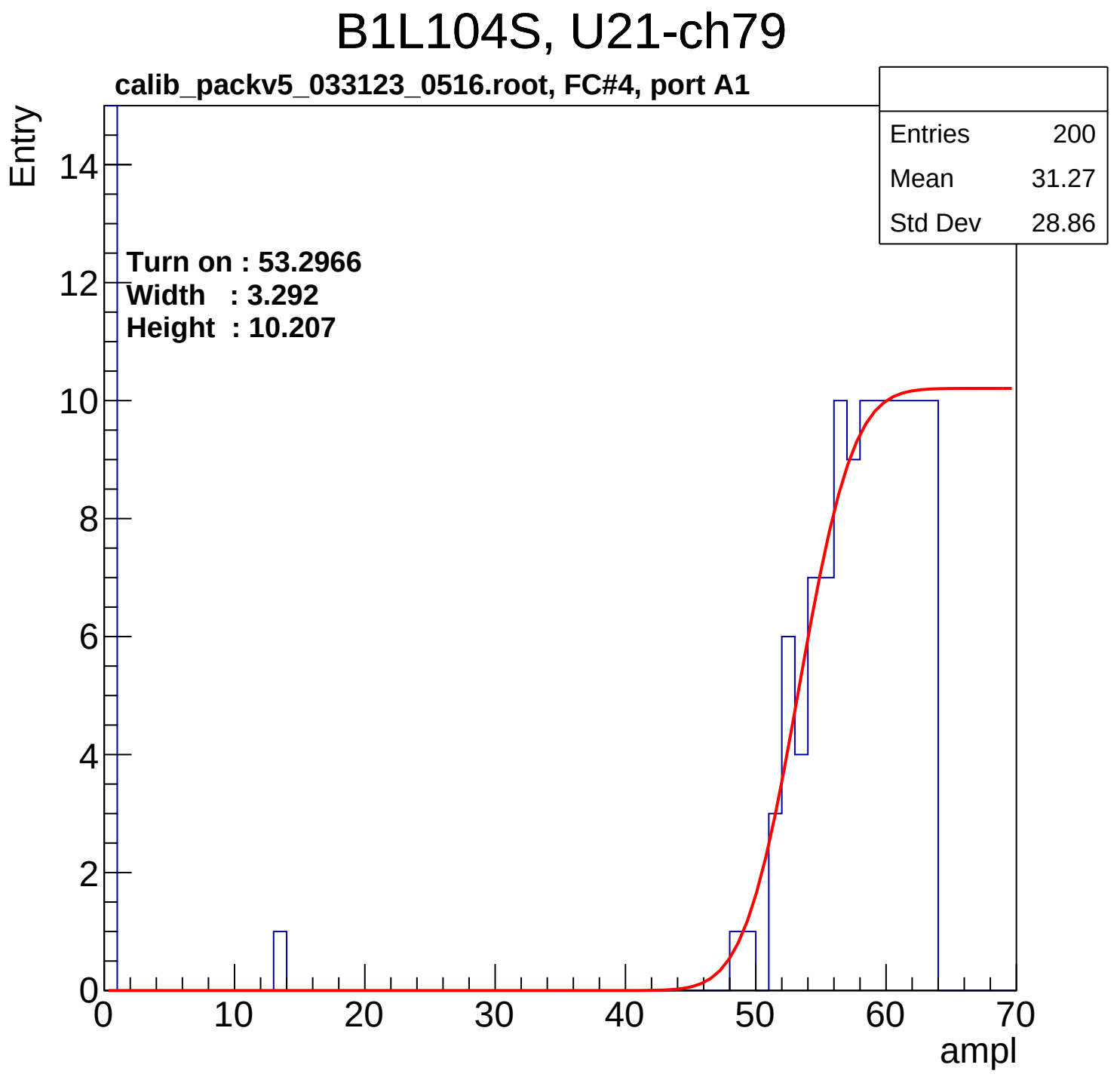
B1L104S, U21-ch79
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 31.27
Std Dev 28.86
Turn on : 53.2966
Width : 3.292
Height : 10.207
Entry
ampl
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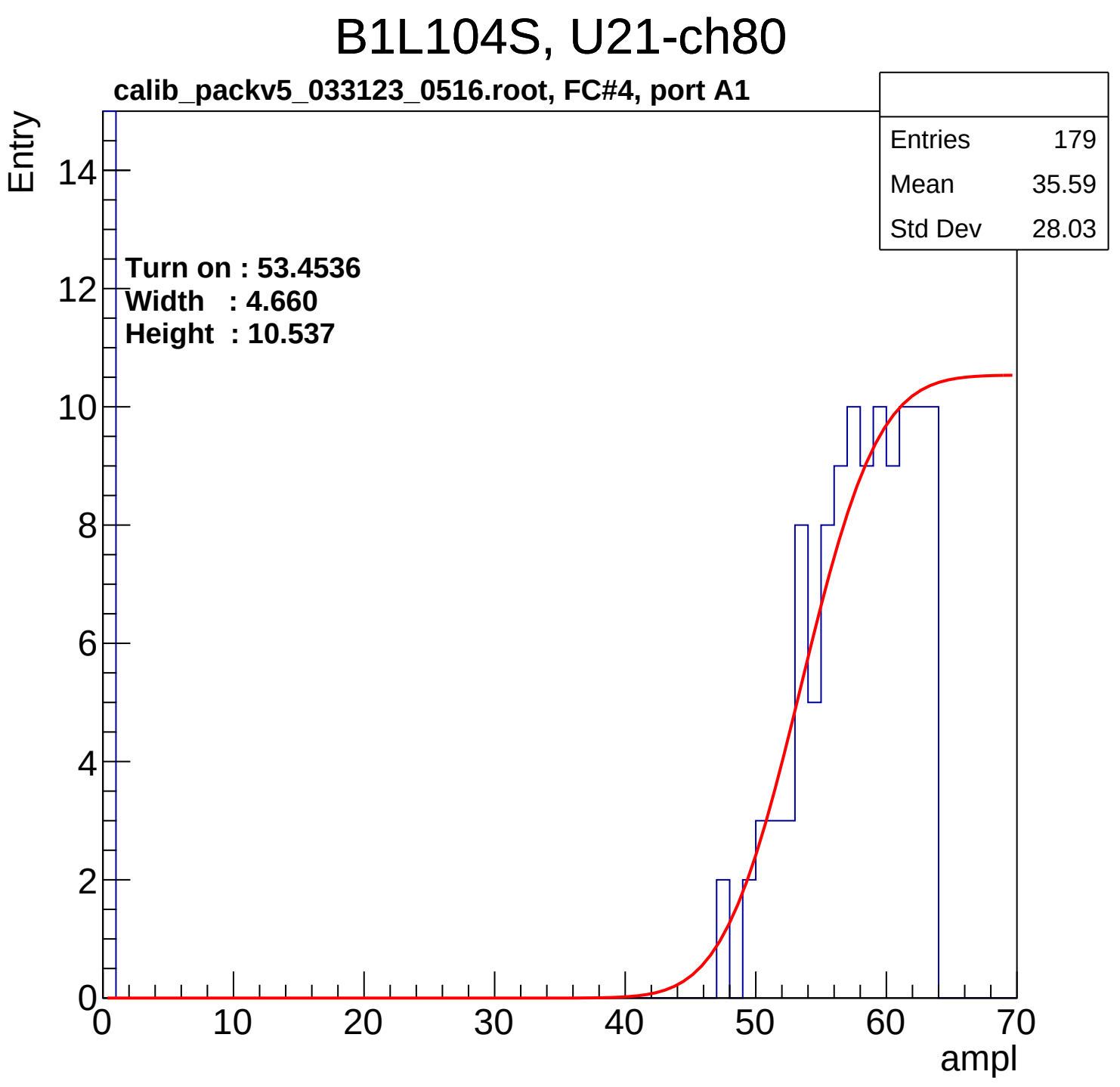
B1L104S, U21-ch80
calib_packv5_033123_0516.root, FC#4, port A1
Entries 179
Mean 35.59
Std Dev 28.03
Turn on : 53.4536
Width : 4.660
Height : 10.537
Entry
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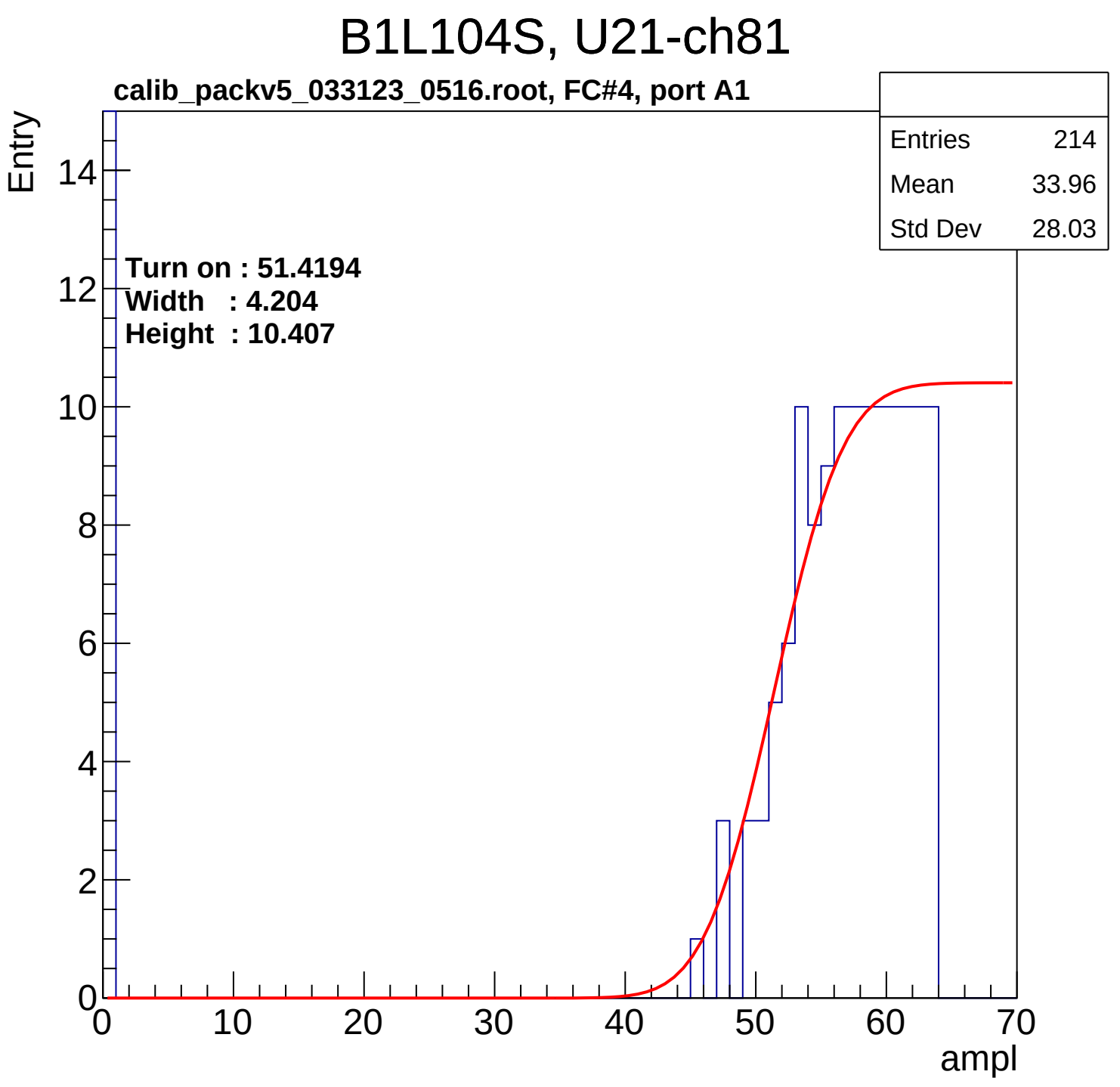
B1L104S, U21-ch81
calib_packv5_033123_0516.root, FC#4, port A1
Entries 214
Mean 33.96
Std Dev 28.03
Turn on : 51.4194
Width : 4.204
Height : 10.407
Entry
ampl
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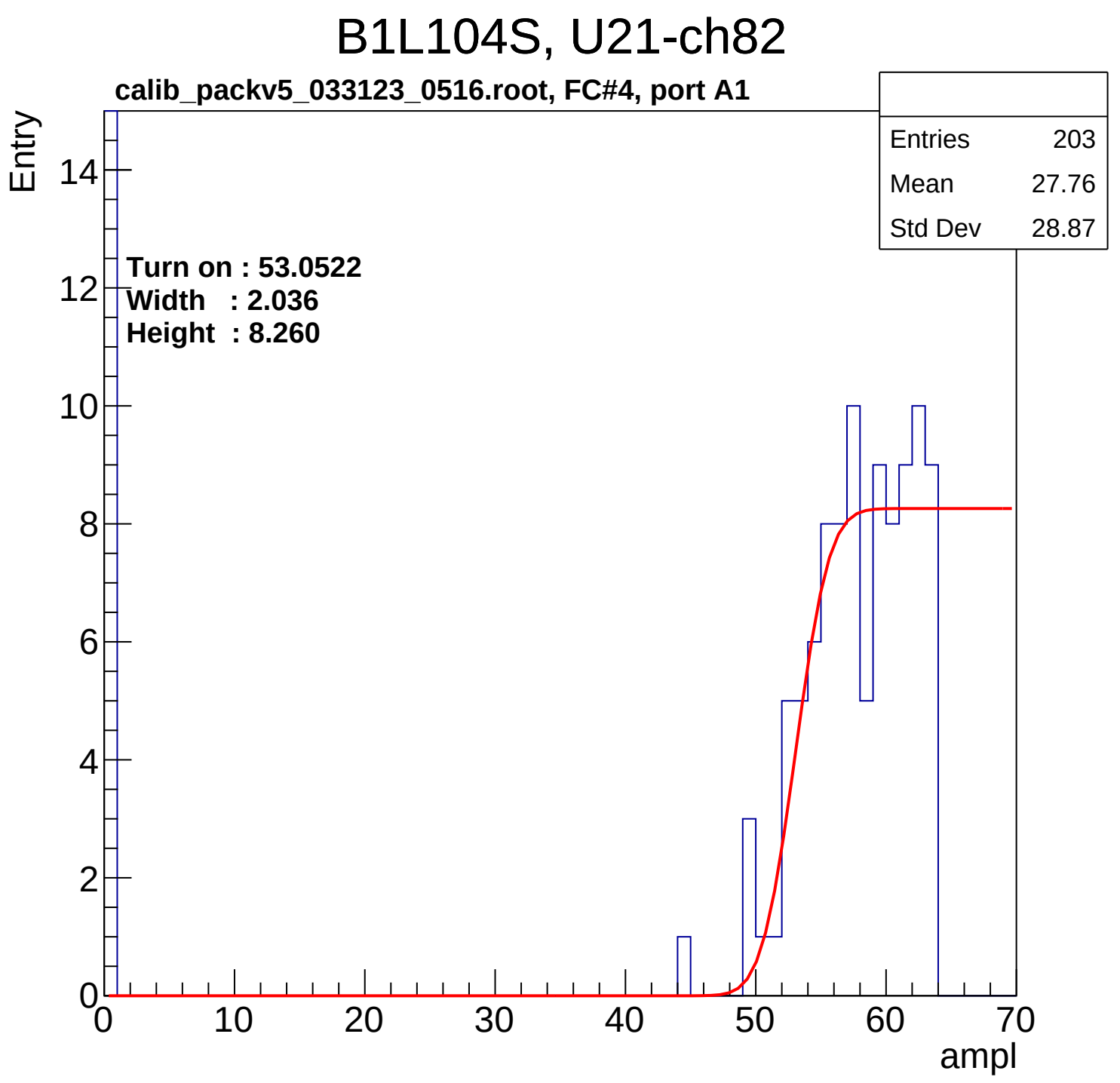
B1L104S, U21-ch82
calib_packv5_033123_0516.root, FC#4, port A1
Entries 203
Mean 27.76
Std Dev 28.87
Turn on : 53.0522
Width : 2.036
Height : 8.260
Entry
ampl
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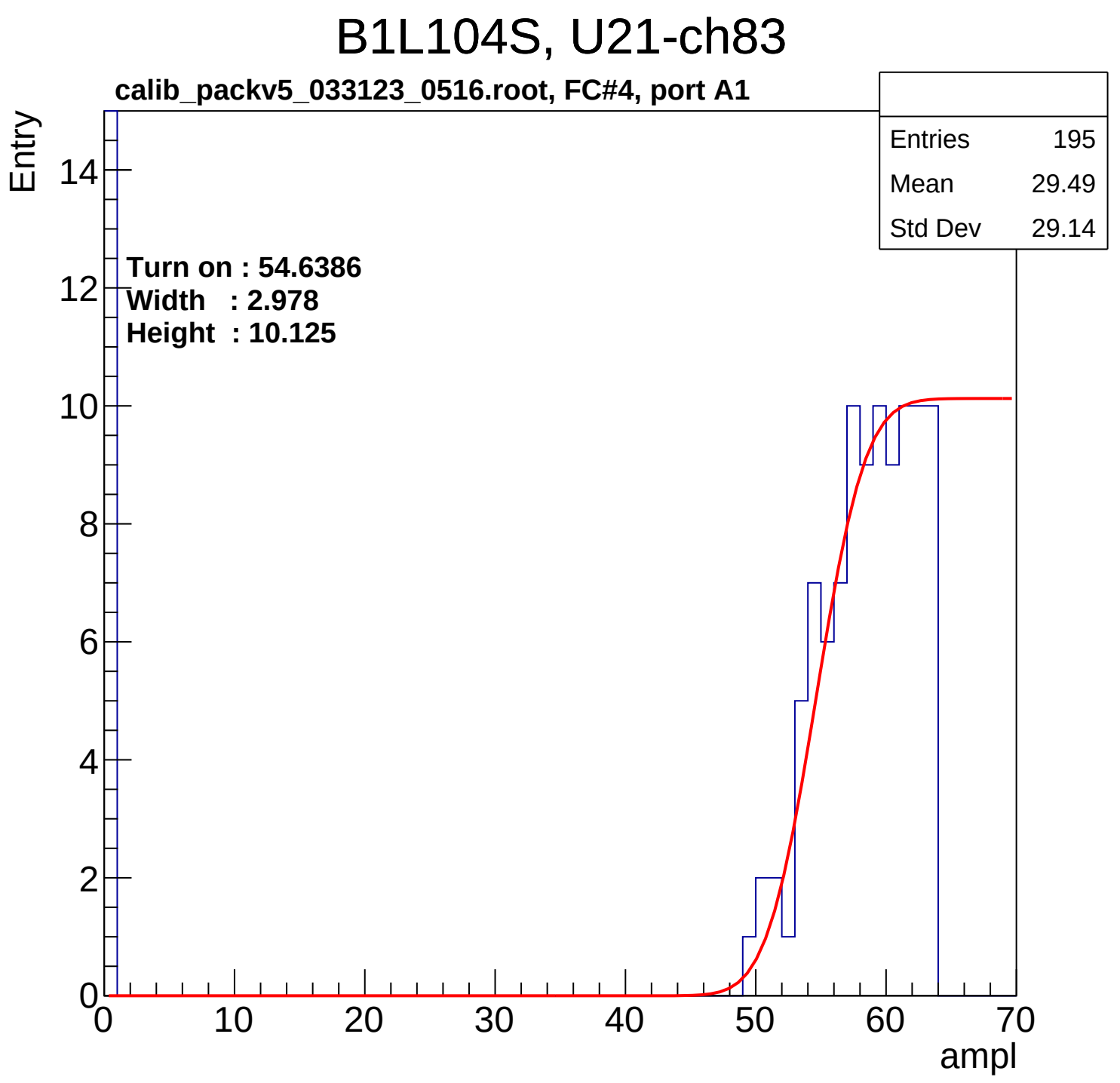
B1L104S, U21-ch83
calib_packv5_033123_0516.root, FC#4, port A1
Entries 195
Mean 29.49
Std Dev 29.14
Turn on : 54.6386
Width : 2.978
Height : 10.125
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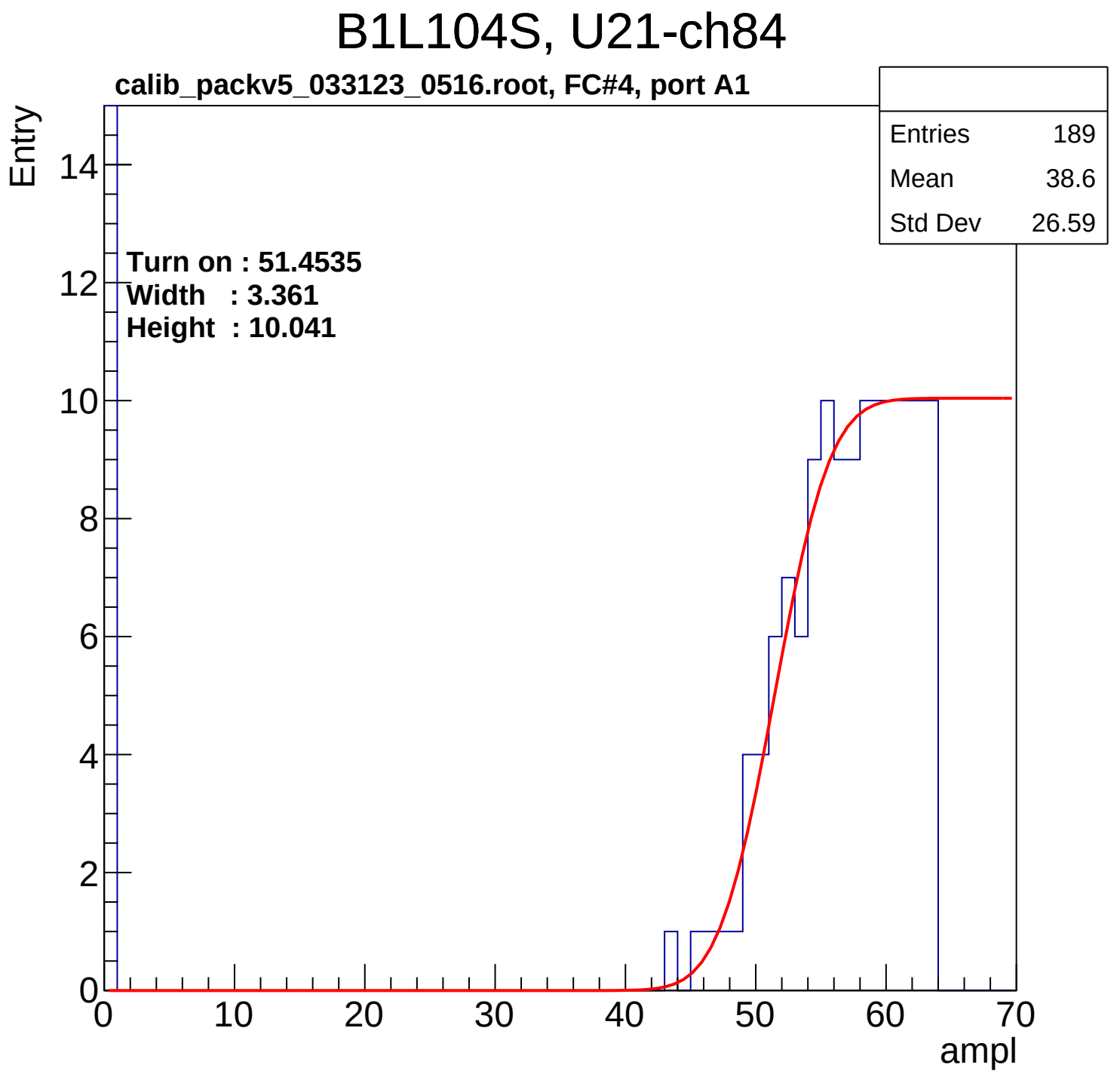
B1L104S, U21-ch84
calib_packv5_033123_0516.root, FC#4, port A1
Entries 189
Mean 38.6
Std Dev 26.59
Turn on : 51.4535
Width : 3.361
Height : 10.041
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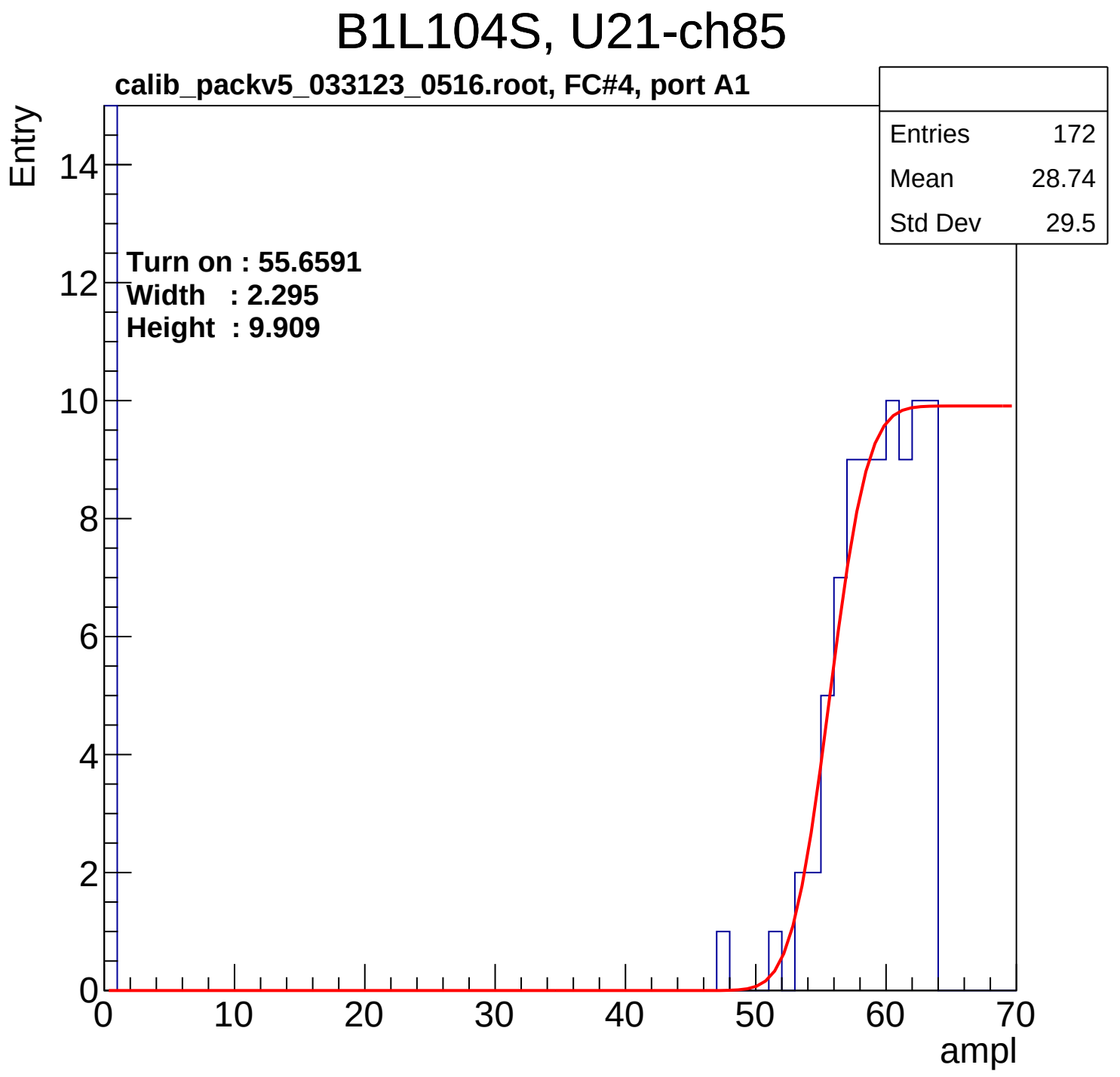
B1L104S, U21-ch85
calib_packv5_033123_0516.root, FC#4, port A1
Entries 172
Mean 28.74
Std Dev 29.5
Turn on : 55.6591
Width : 2.295
Height : 9.909
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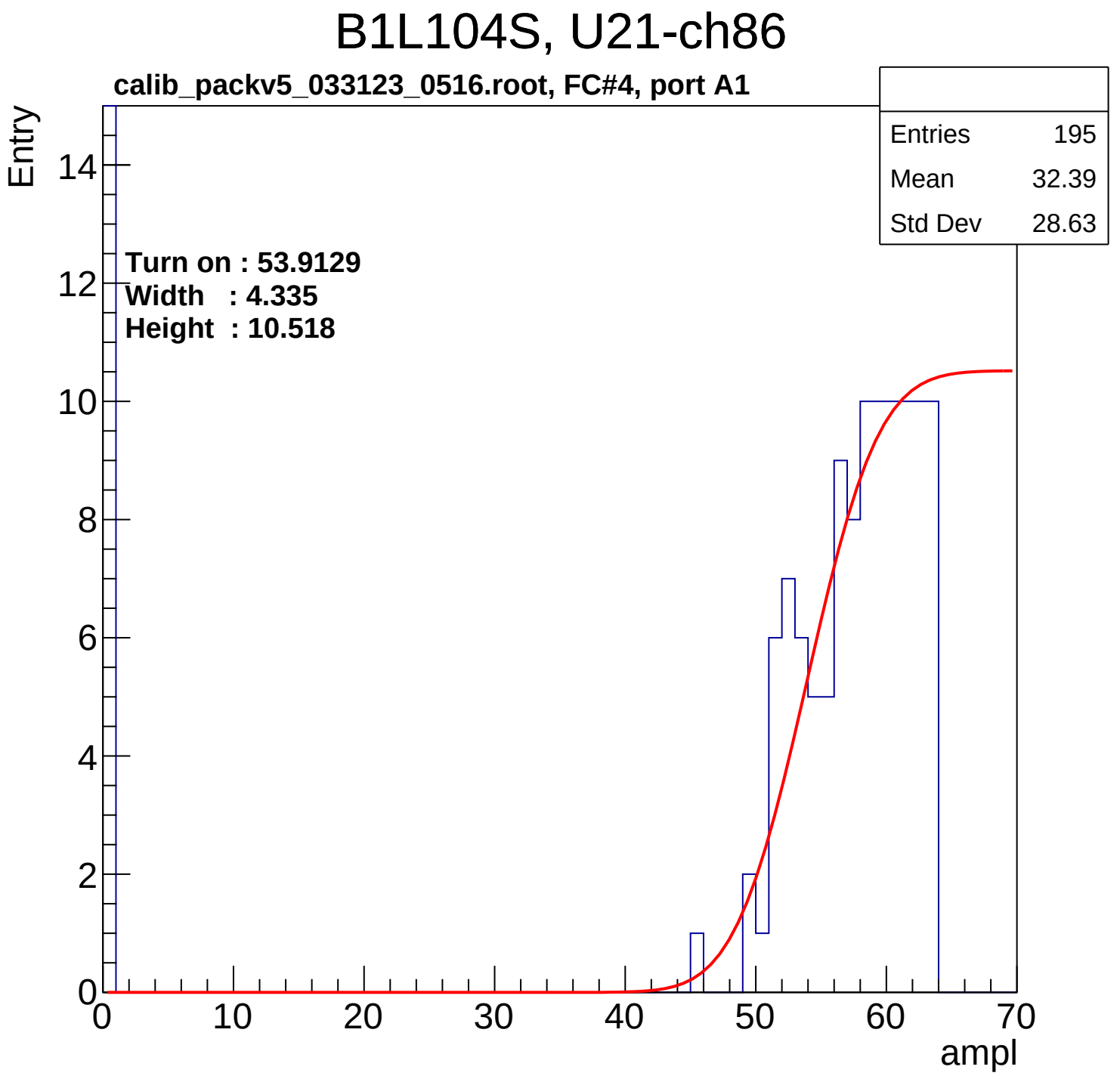
B1L104S, U21-ch86
calib_packv5_033123_0516.root, FC#4, port A1
Entries 195
Mean 32.39
Std Dev 28.63
Turn on : 53.9129
Width : 4.335
Height : 10.518
Entry
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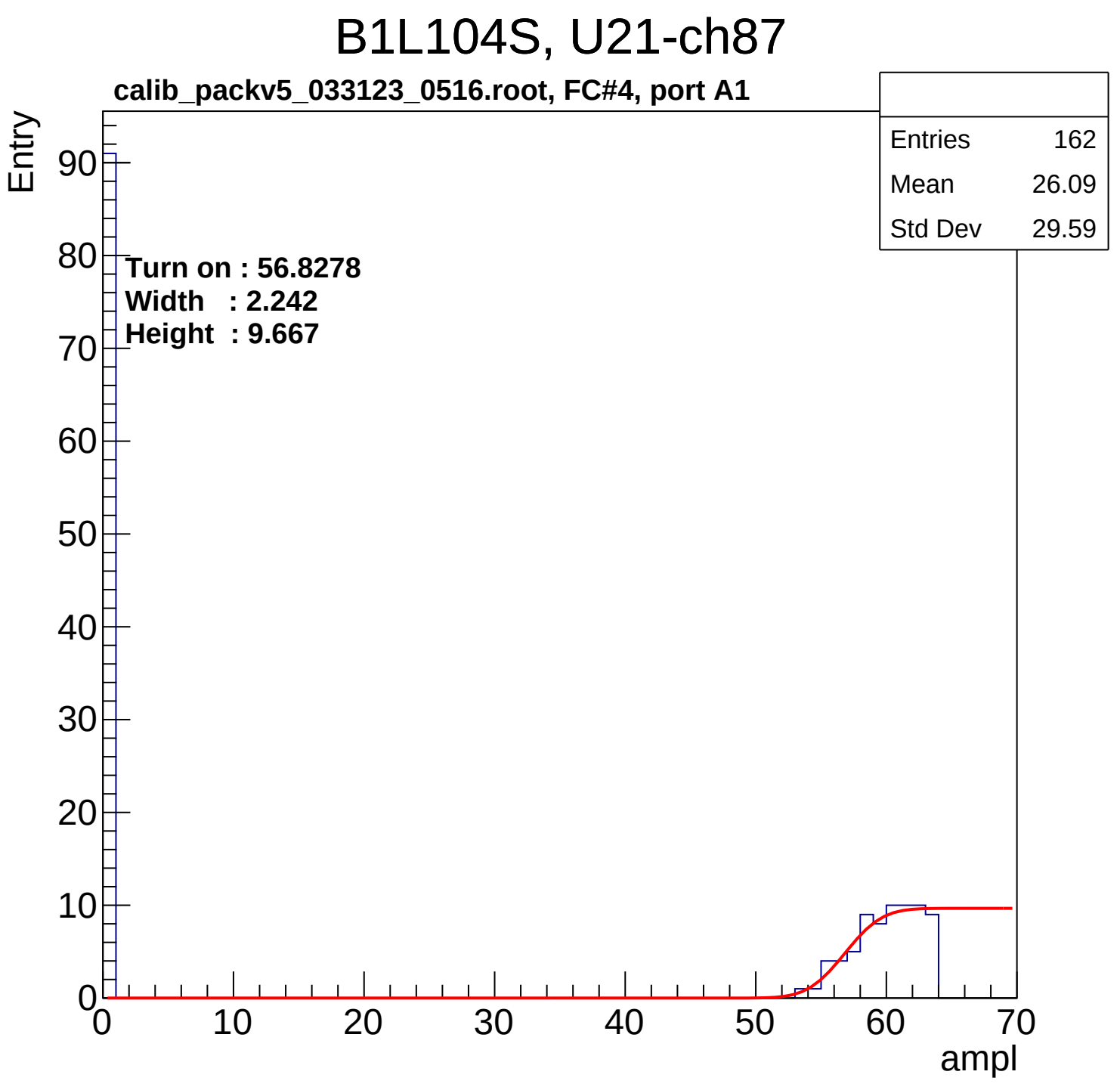
B1L104S, U21-ch87
calib_packv5_033123_0516.root, FC#4, port A1
Entries 162
Mean 26.09
Std Dev 29.59
Turn on : 56.8278
Width : 2.242
Height : 9.667
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ampl
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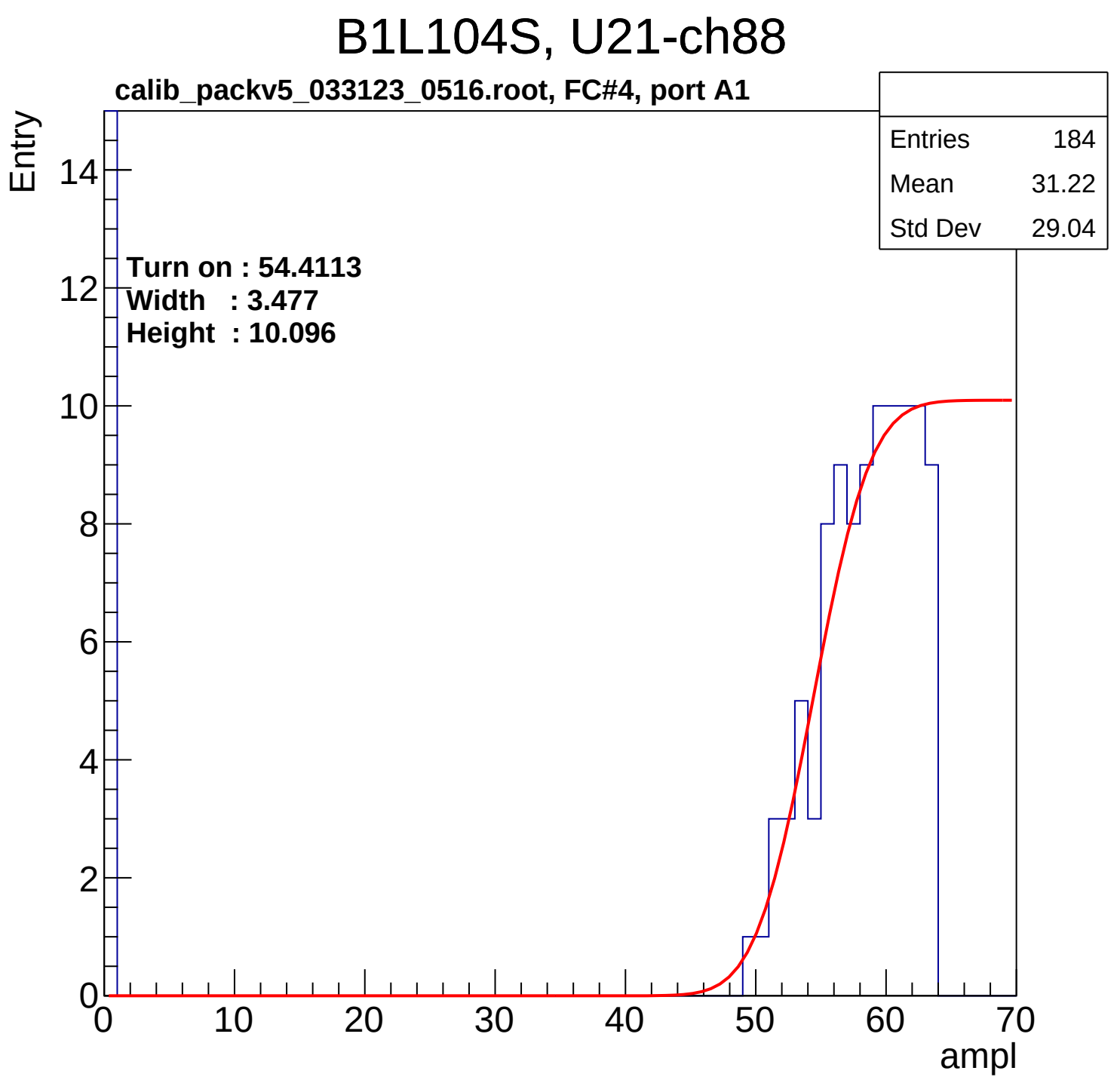
B1L104S, U21-ch88
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 31.22
Std Dev 29.04
Turn on : 54.4113
Width : 3.477
Height : 10.096
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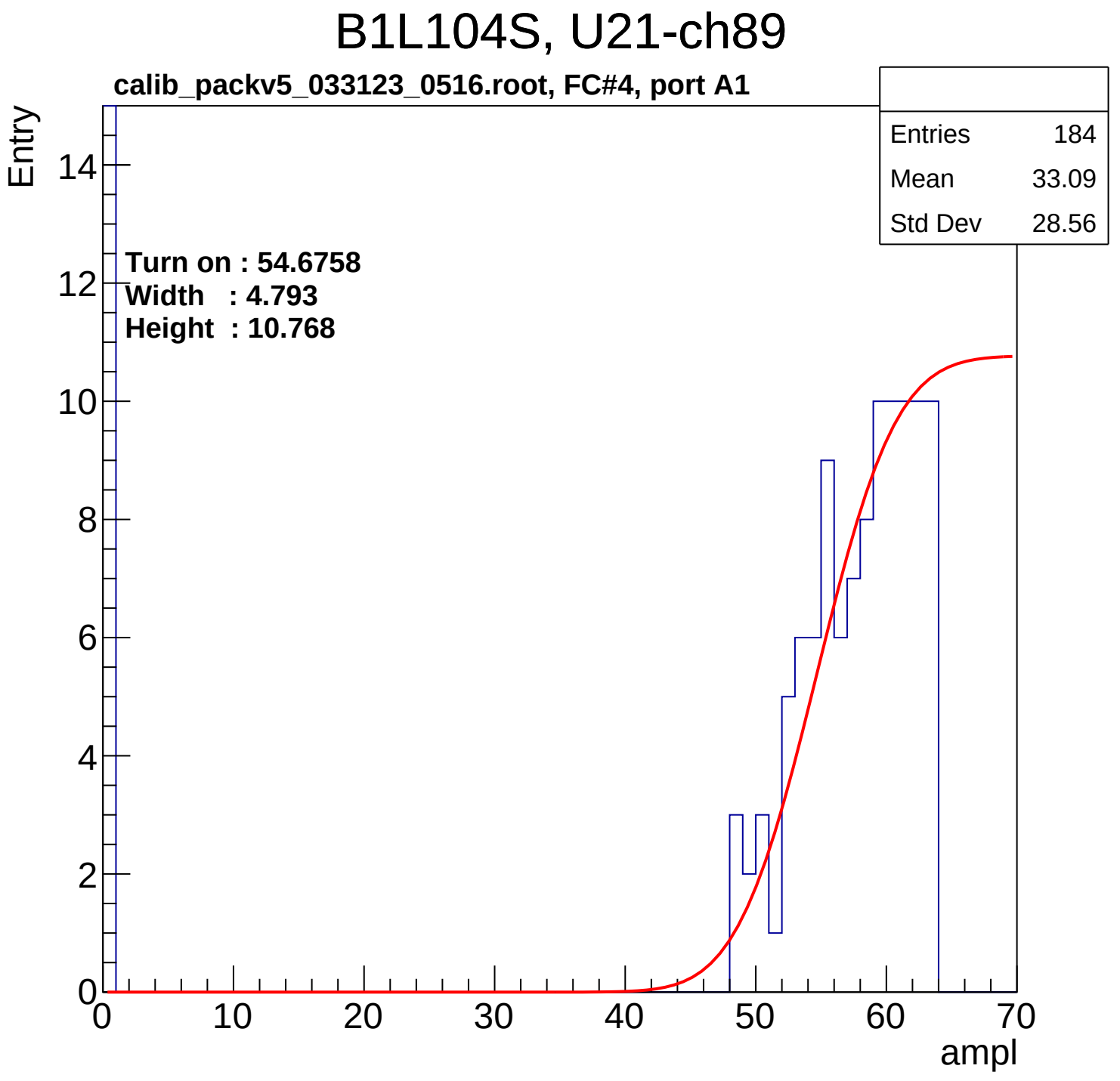
B1L104S, U21-ch89
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 33.09
Std Dev 28.56
Turn on : 54.6758
Width : 4.793
Height : 10.768
Entry
ampl
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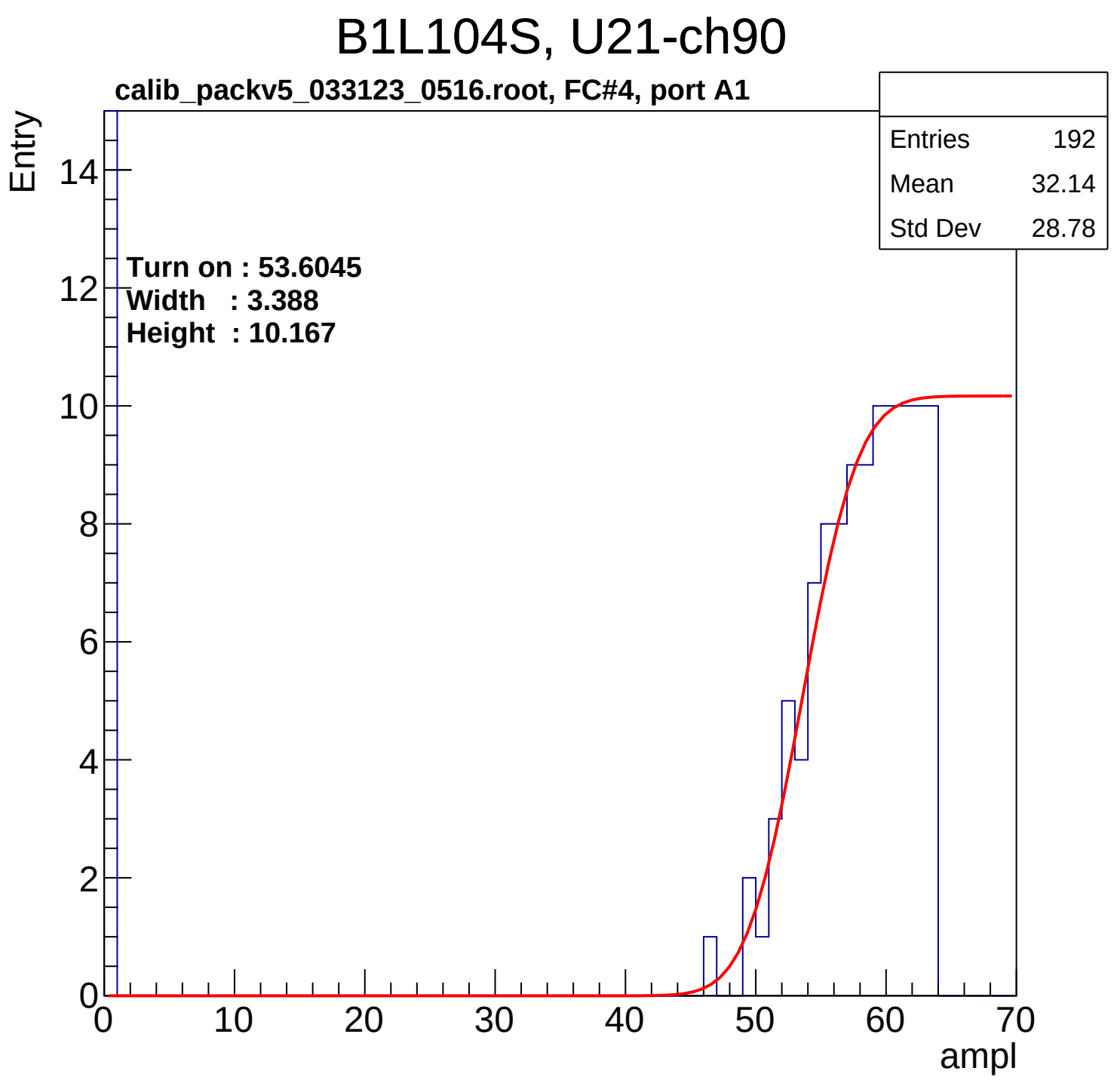
B1L104S, U21-ch90
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 32.14
Std Dev 28.78
Turn on : 53.6045
Width : 3.388
Height : 10.167
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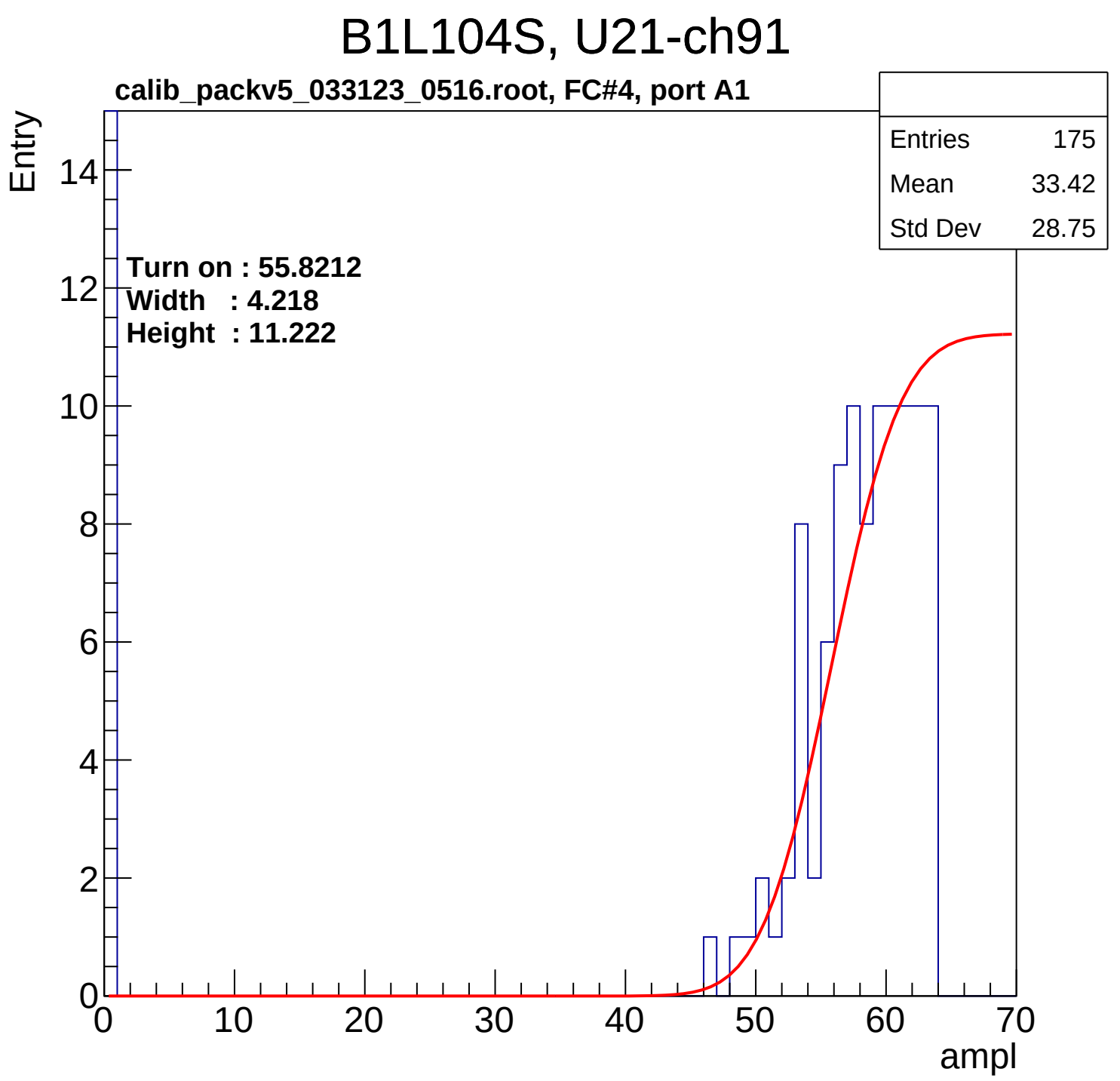
B1L104S, U21-ch91
calib_packv5_033123_0516.root, FC#4, port A1
Entries 175
Mean 33.42
Std Dev 28.75
Turn on : 55.8212
Width : 4.218
Height : 11.222
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ampl
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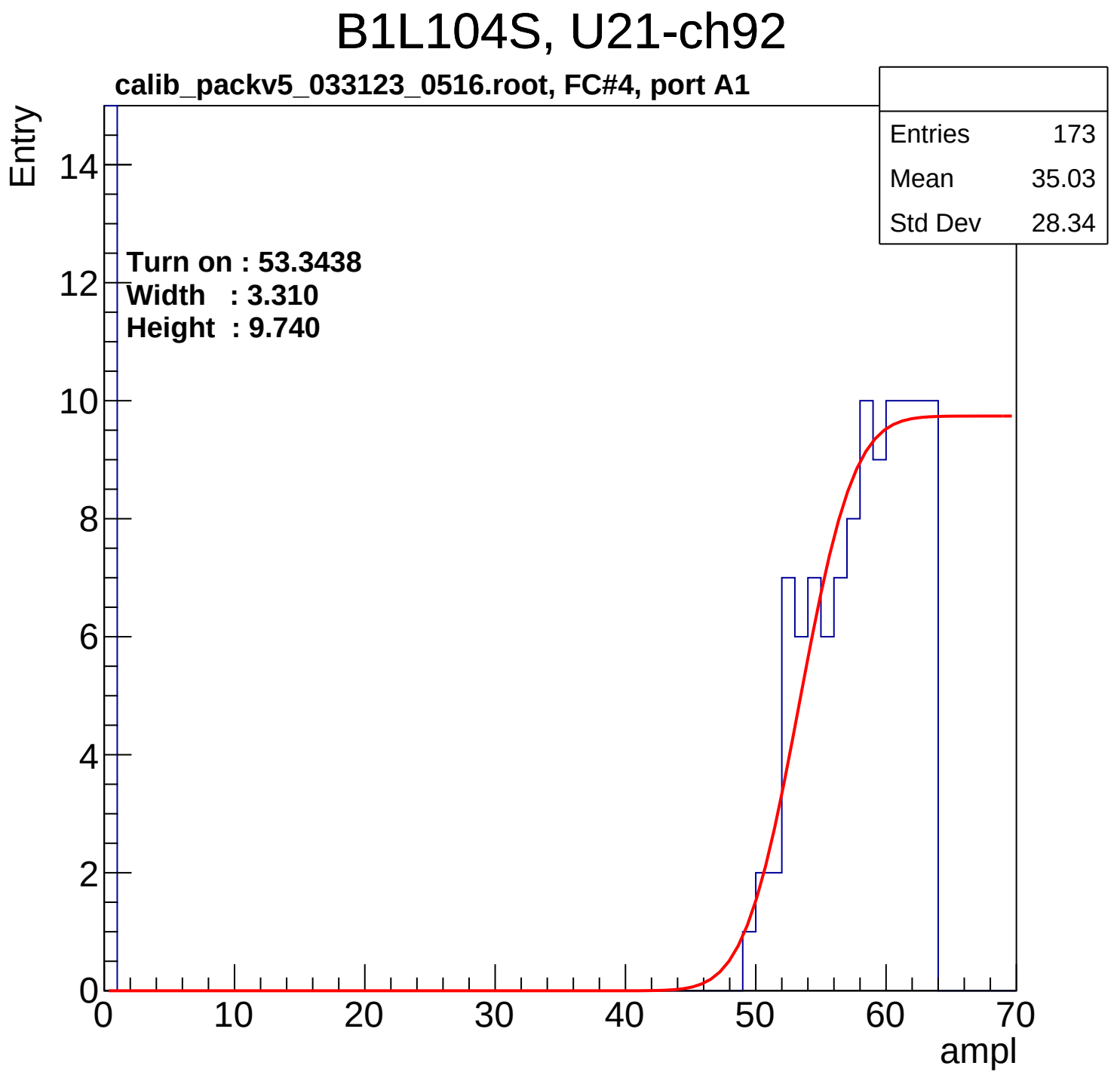
B1L104S, U21-ch92
calib_packv5_033123_0516.root, FC#4, port A1
Entries 173
Mean 35.03
Std Dev 28.34
Turn on : 53.3438
Width : 3.310
Height : 9.740
Entry
ampl
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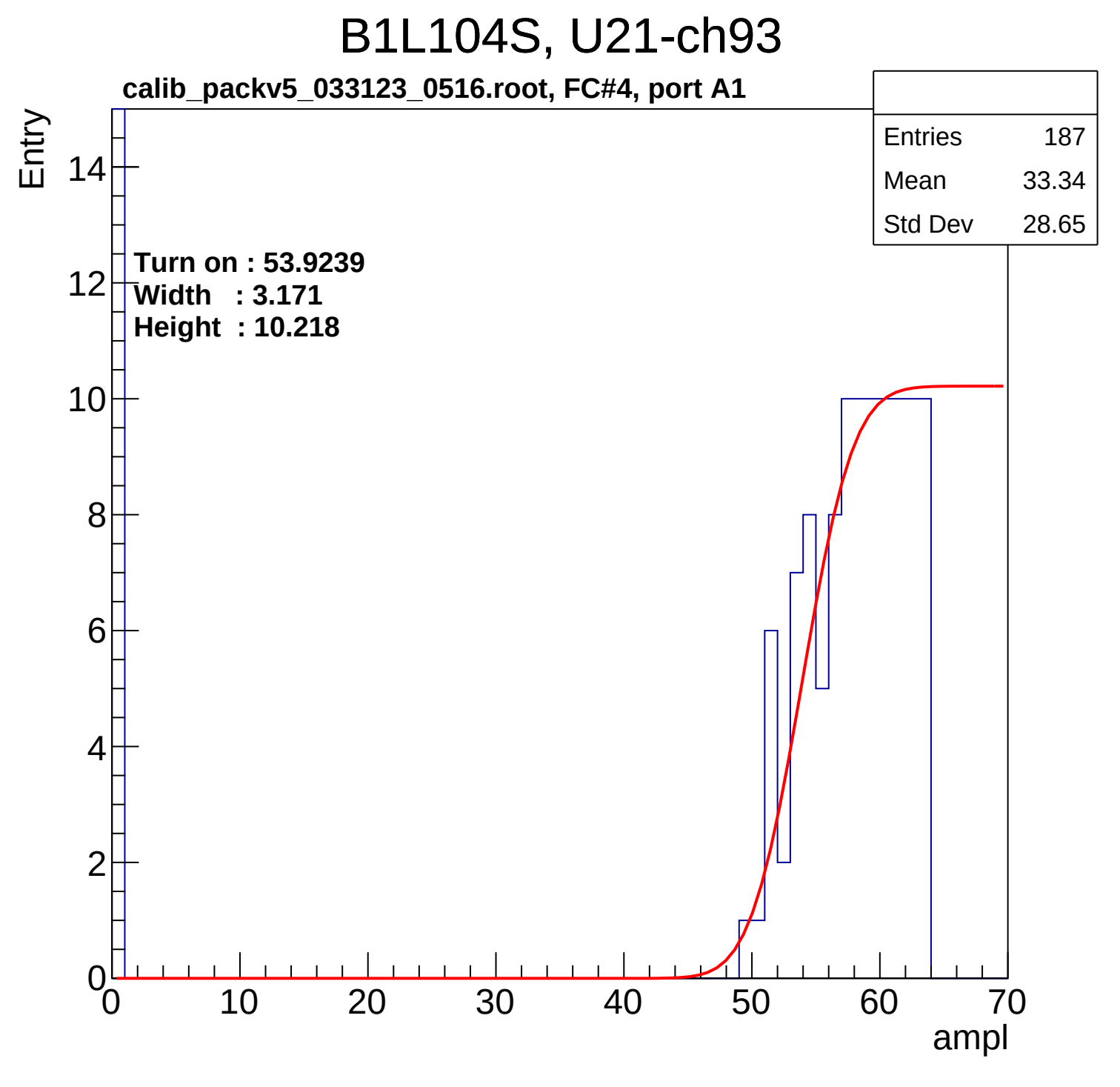
B1L104S, U21-ch93
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 33.34
Std Dev 28.65
Turn on : 53.9239
Width : 3.171
Height : 10.218
Entry
ampl
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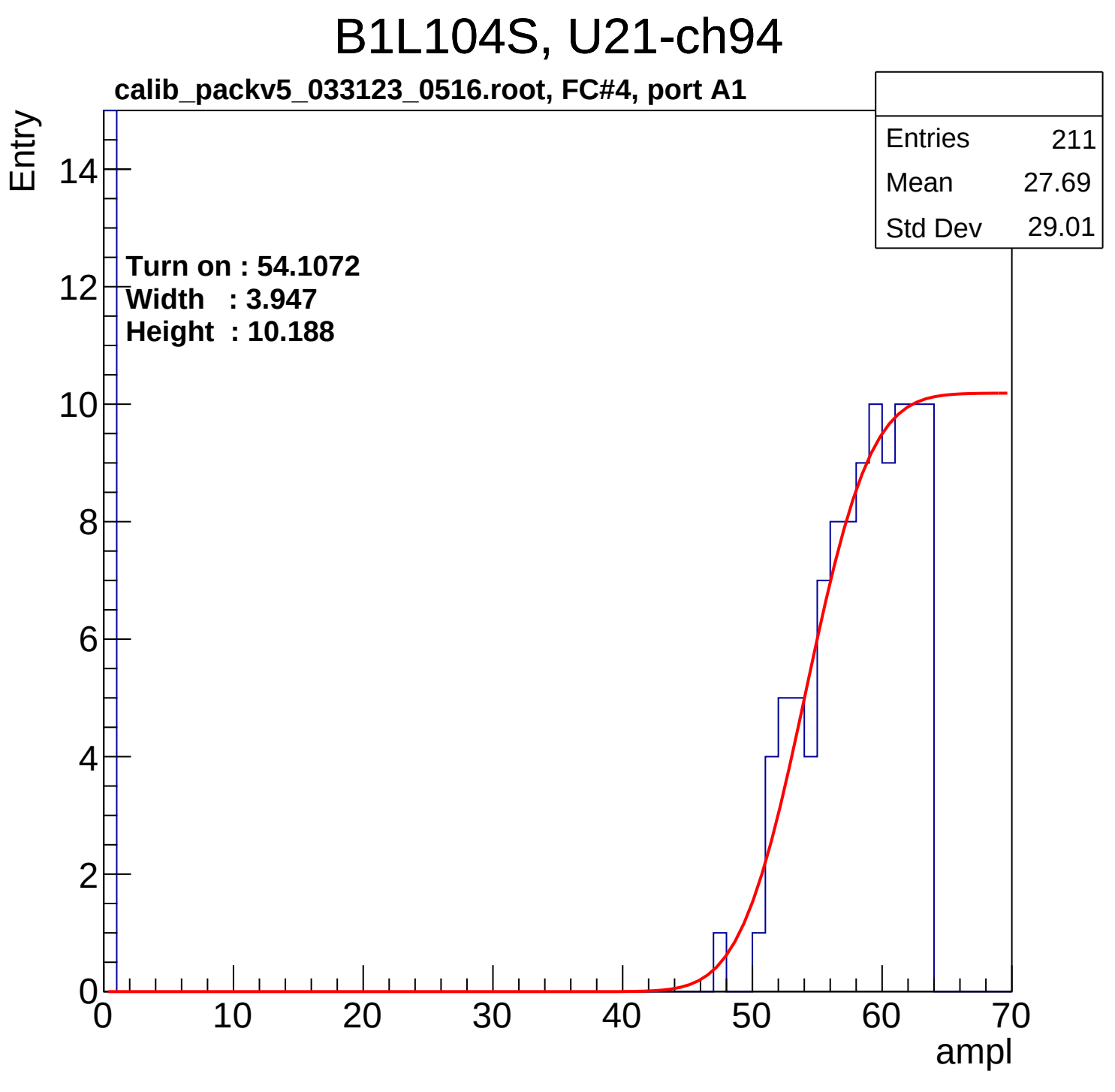
B1L104S, U21-ch94
calib_packv5_033123_0516.root, FC#4, port A1
Entries 211
Mean 27.69
Std Dev 29.01
Turn on : 54.1072
Width : 3.947
Height : 10.188
Entry
ampl
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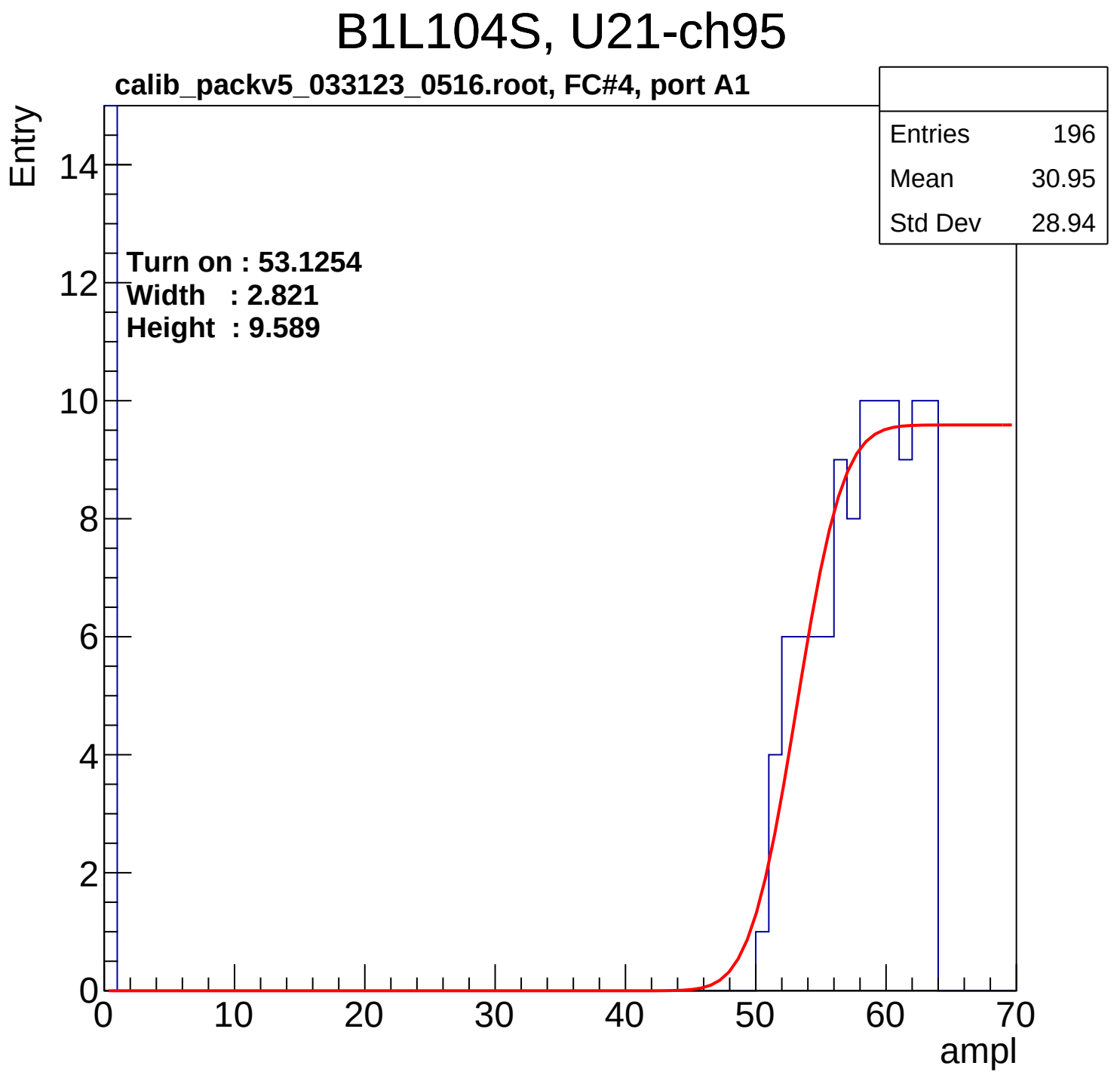
B1L104S, U21-ch95
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 30.95
Std Dev 28.94
Turn on : 53.1254
Width : 2.821
Height : 9.589
Entry
ampl
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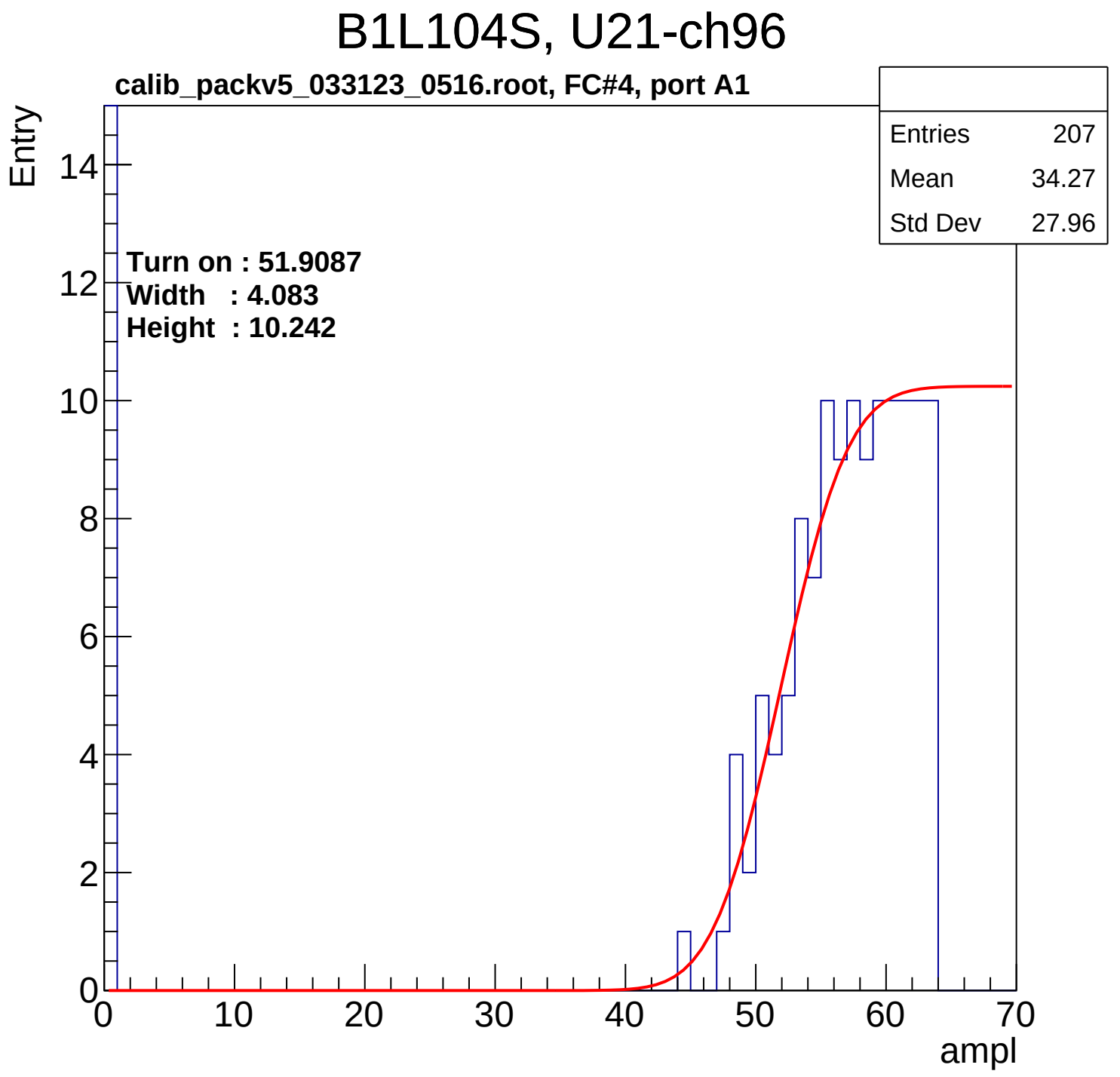
B1L104S, U21-ch96
calib_packv5_033123_0516.root, FC#4, port A1
Entries 207
Mean 34.27
Std Dev 27.96
Turn on : 51.9087
Width : 4.083
Height : 10.242
Entry
ampl
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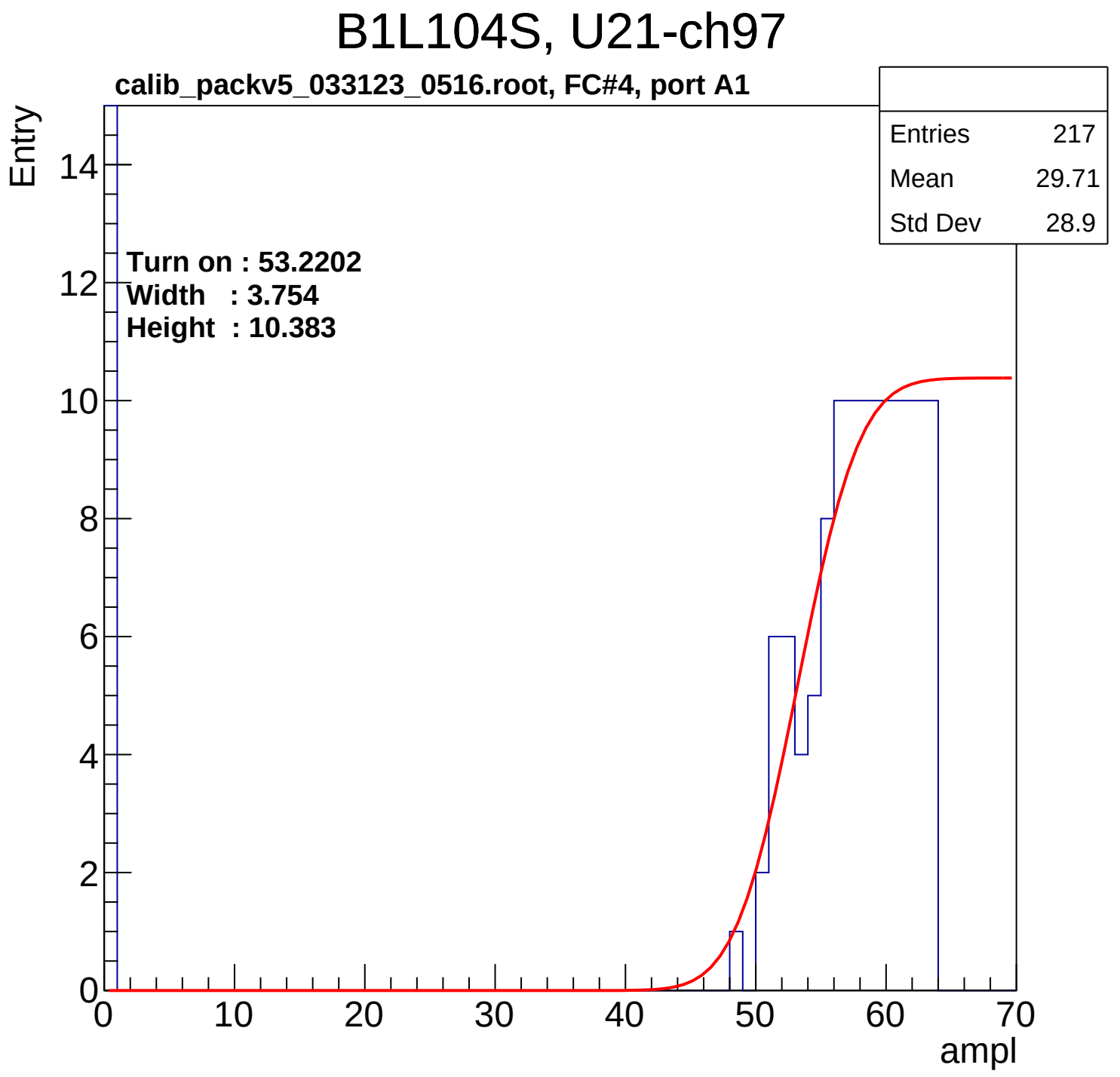
B1L104S, U21-ch97
calib_packv5_033123_0516.root, FC#4, port A1
Entries 217
Mean 29.71
Std Dev 28.9
Turn on : 53.2202
Width : 3.754
Height : 10.383
Entry
ampl
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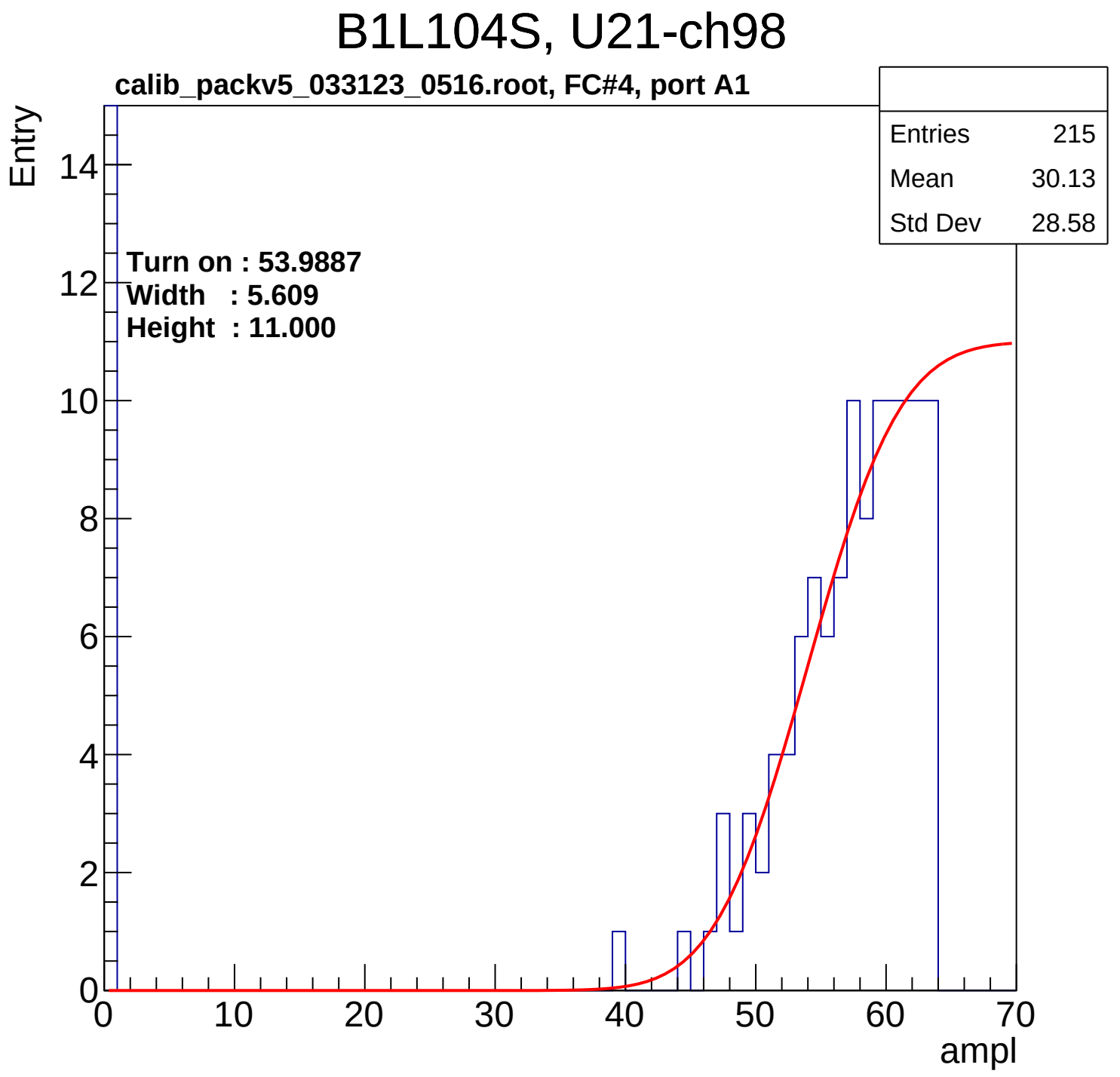
B1L104S, U21-ch98
calib_packv5_033123_0516.root, FC#4, port A1
Entries 215
Mean 30.13
Std Dev 28.58
Turn on : 53.9887
Width : 5.609
Height : 11.000
Entry
ampl
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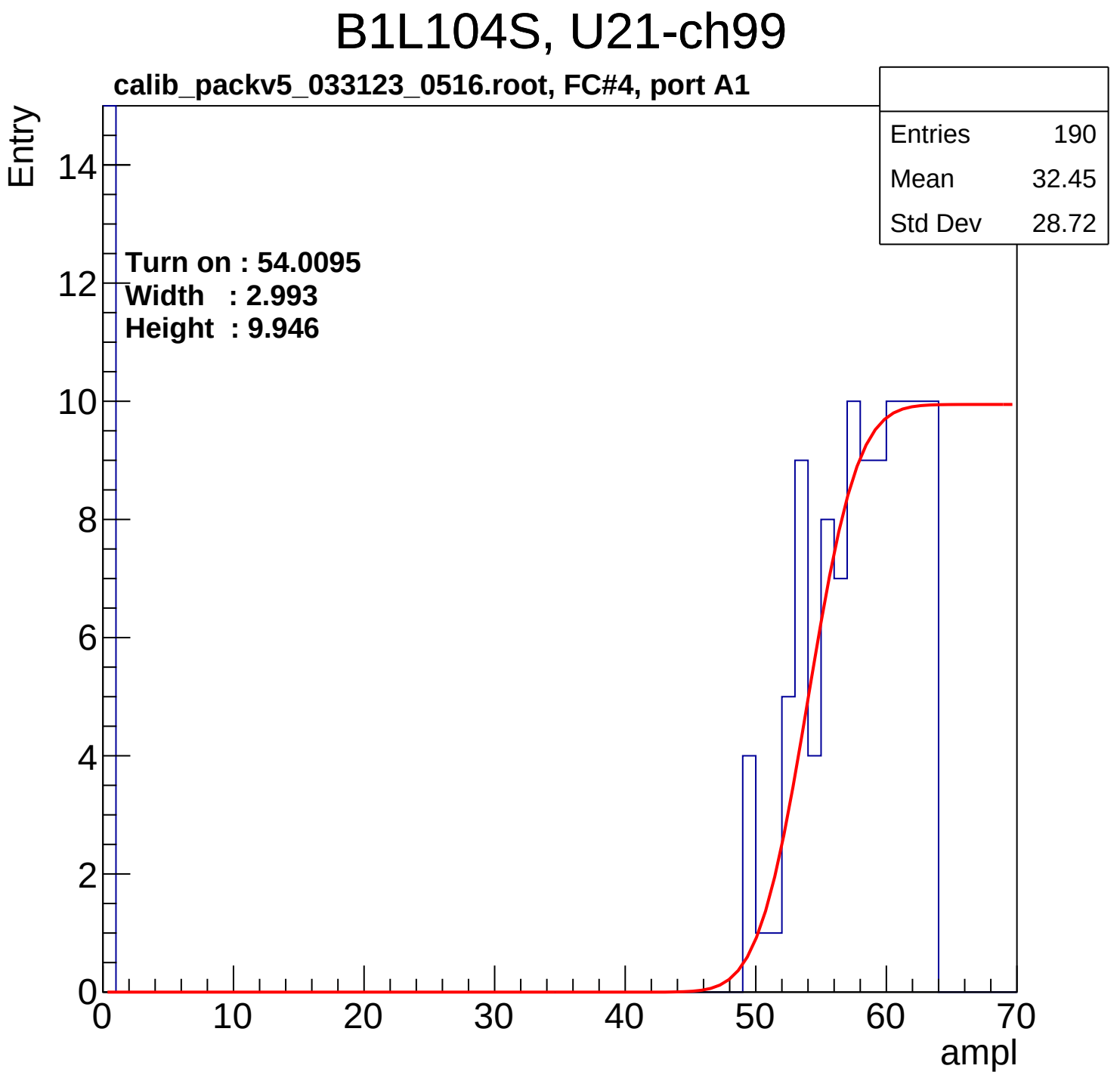
B1L104S, U21-ch99
calib_packv5_033123_0516.root, FC#4, port A1
Entries 190
Mean 32.45
Std Dev 28.72
Turn on : 54.0095
Width : 2.993
Height : 9.946
Entry
ampl
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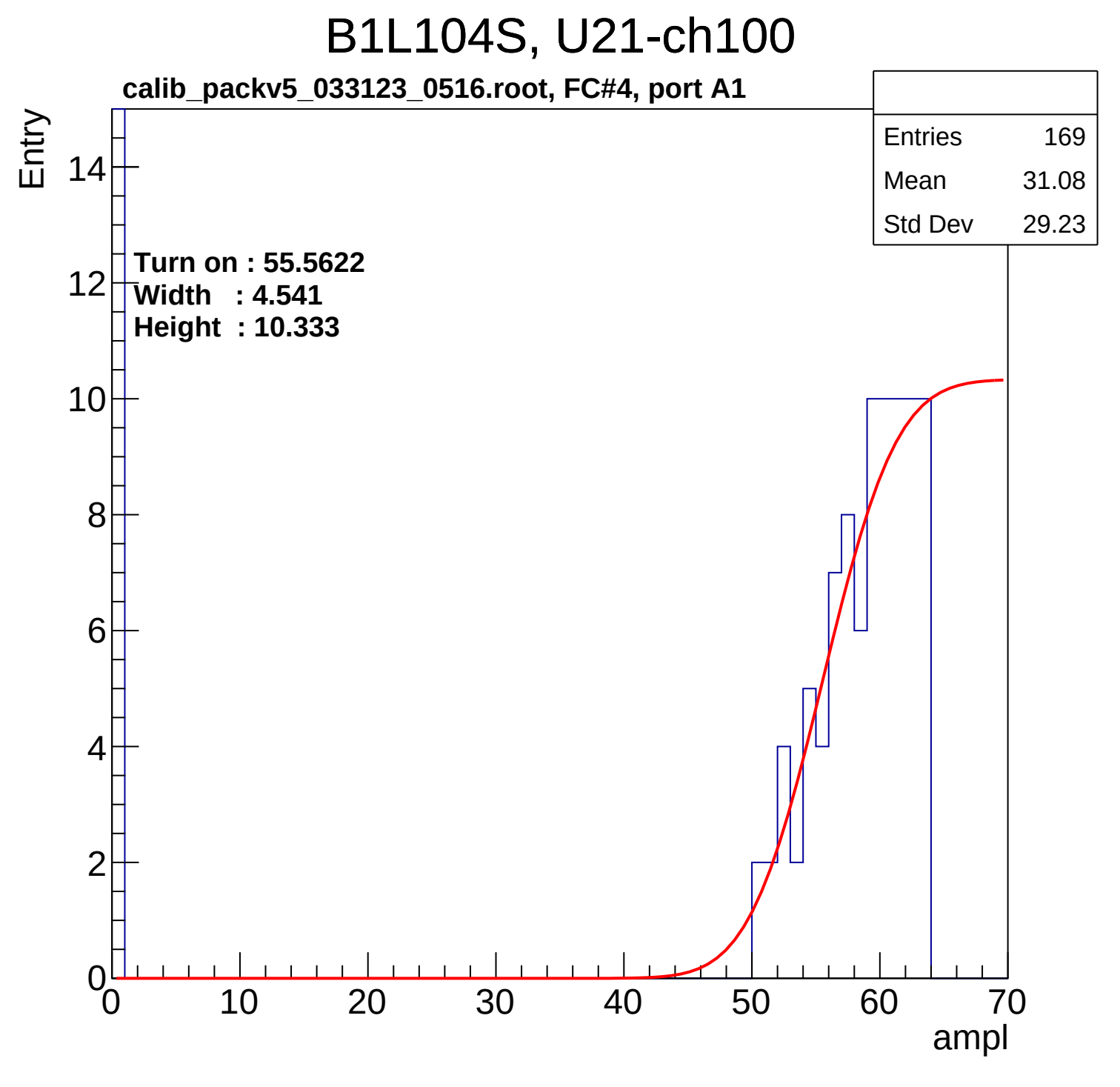
B1L104S, U21-ch100
calib_packv5_033123_0516.root, FC#4, port A1
Entries 169
Mean 31.08
Std Dev 29.23
Turn on : 55.5622
Width : 4.541
Height : 10.333
Entry
ampl
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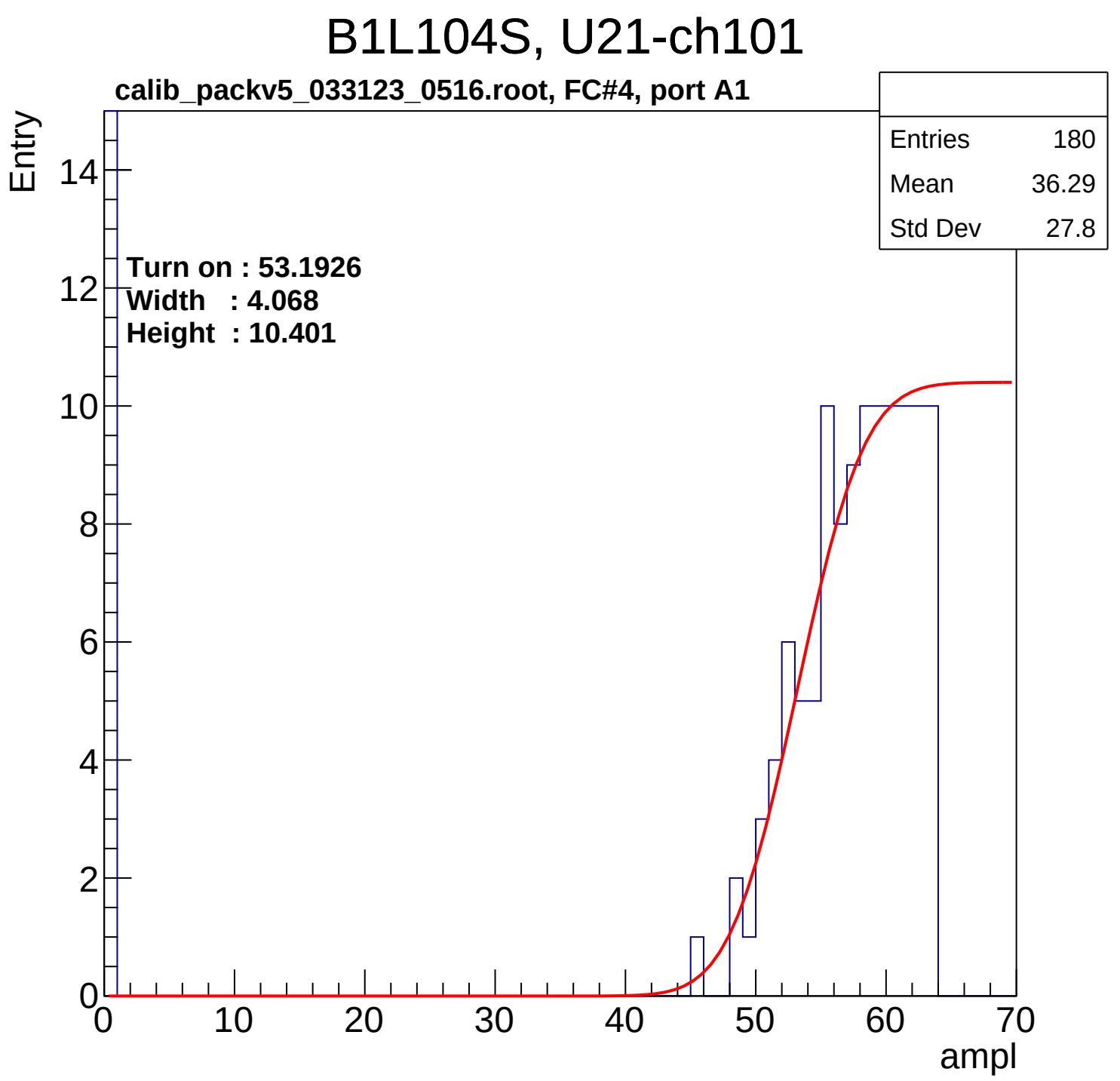
B1L104S, U21-ch101
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 36.29
Std Dev 27.8
Turn on : 53.1926
Width : 4.068
Height : 10.401
Entry
ampl
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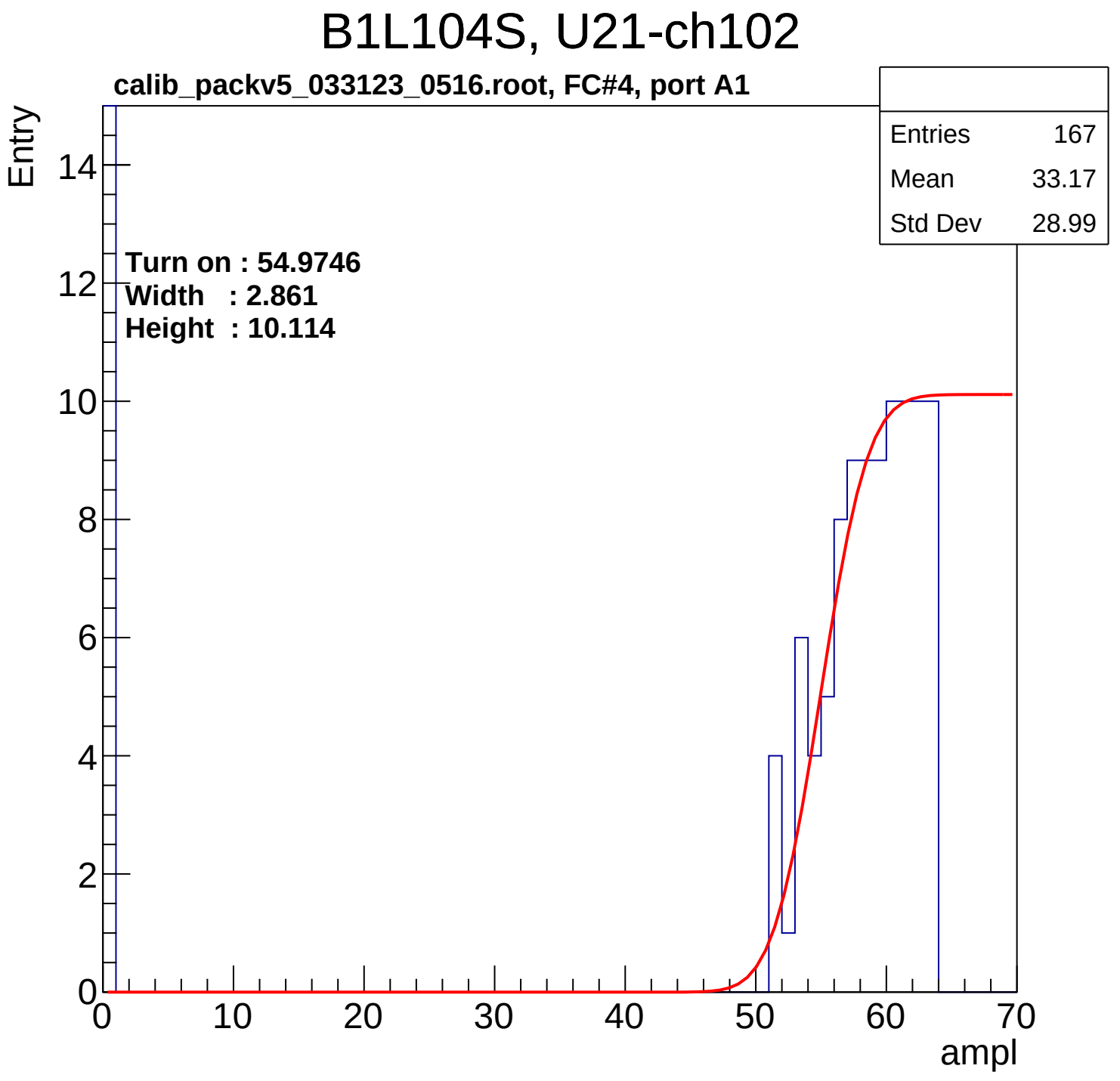
B1L104S, U21-ch102
calib_packv5_033123_0516.root, FC#4, port A1
Entries 167
Mean 33.17
Std Dev 28.99
Turn on : 54.9746
Width : 2.861
Height : 10.114
Entry
ampl
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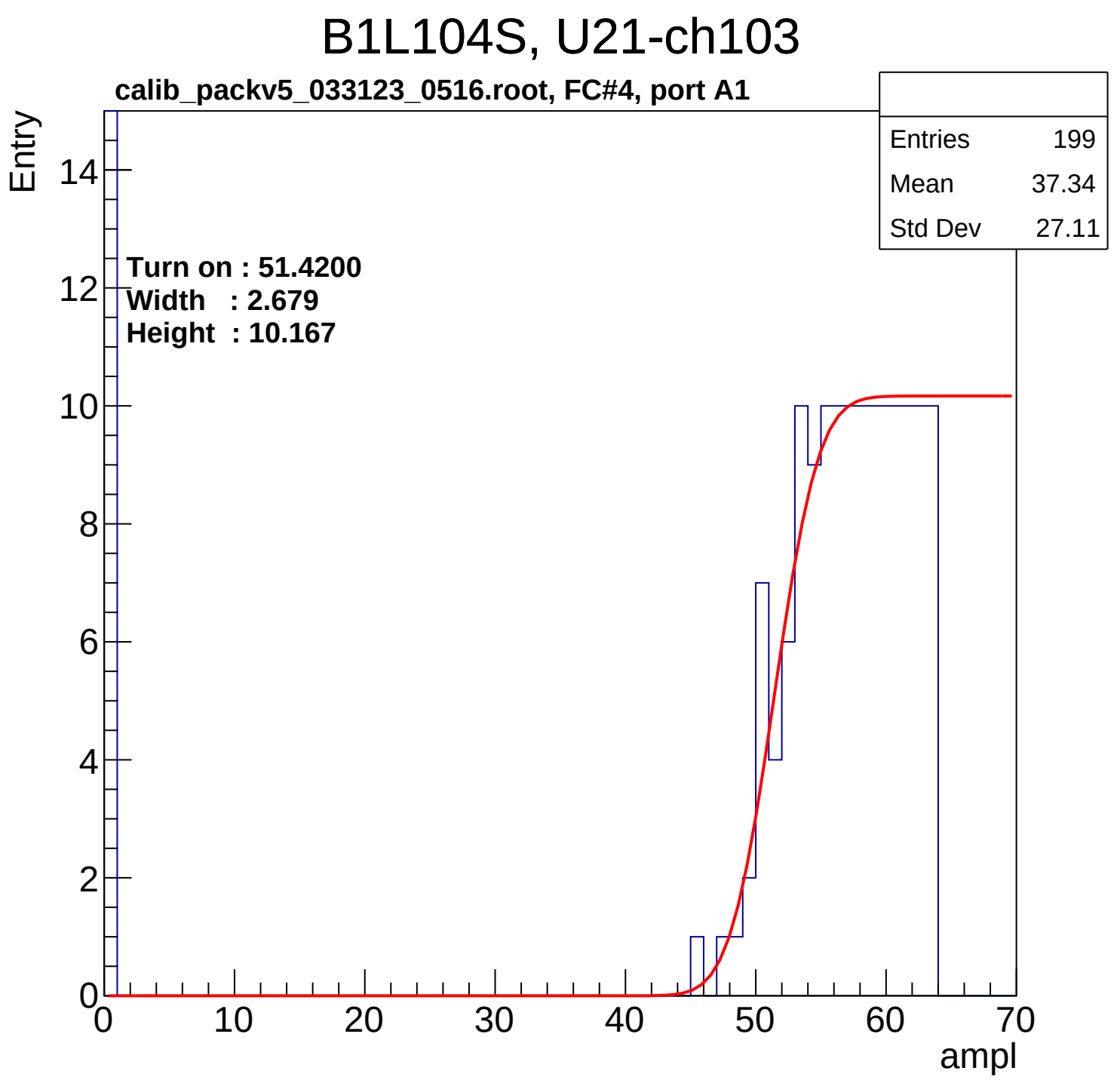
B1L104S, U21-ch103
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 37.34
Std Dev 27.11
Turn on : 51.4200
Width : 2.679
Height : 10.167
Entry
ampl
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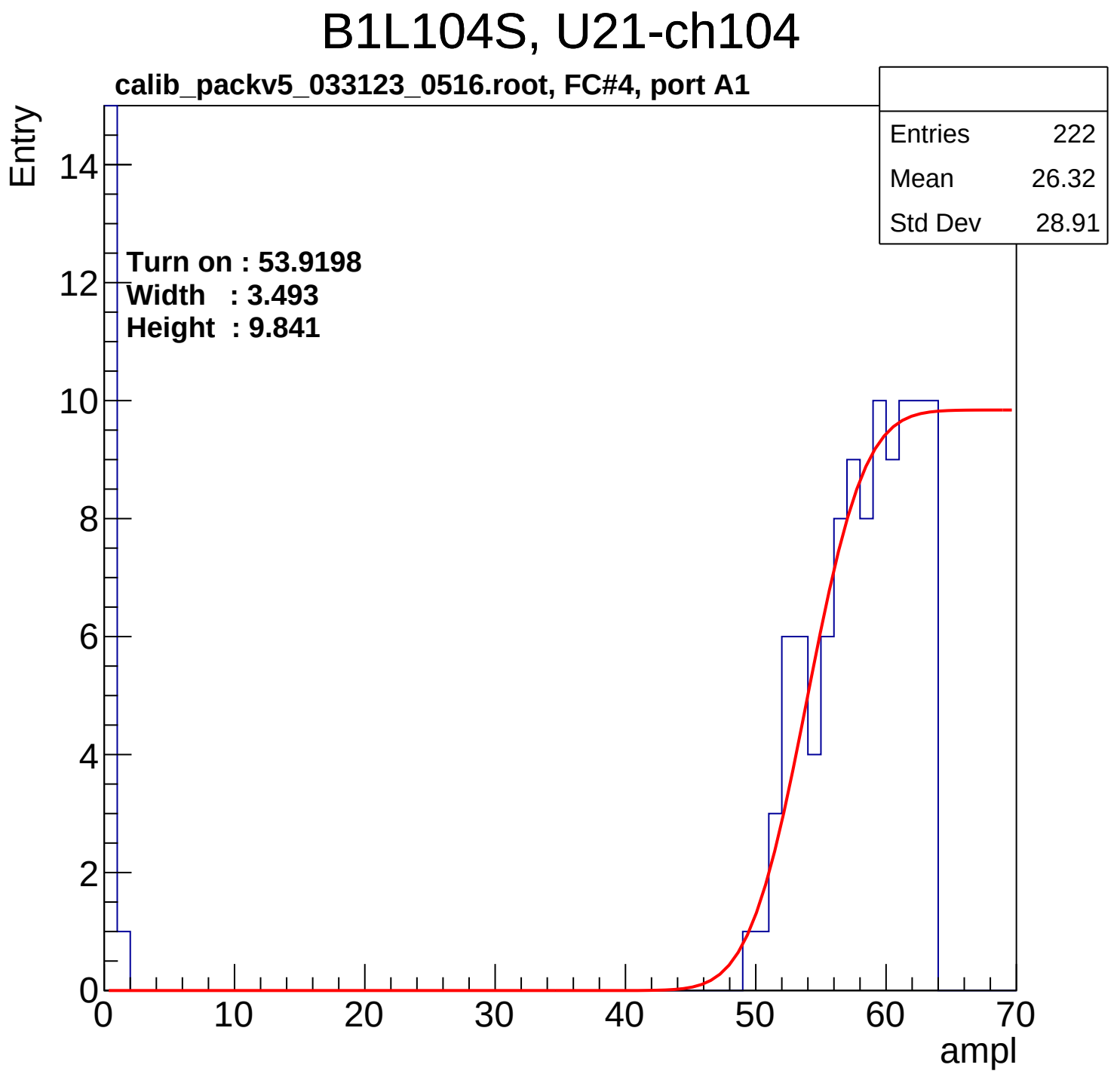
B1L104S, U21-ch104
calib_packv5_033123_0516.root, FC#4, port A1
Entries 222
Mean 26.32
Std Dev 28.91
Turn on : 53.9198
Width : 3.493
Height : 9.841
Entry
ampl
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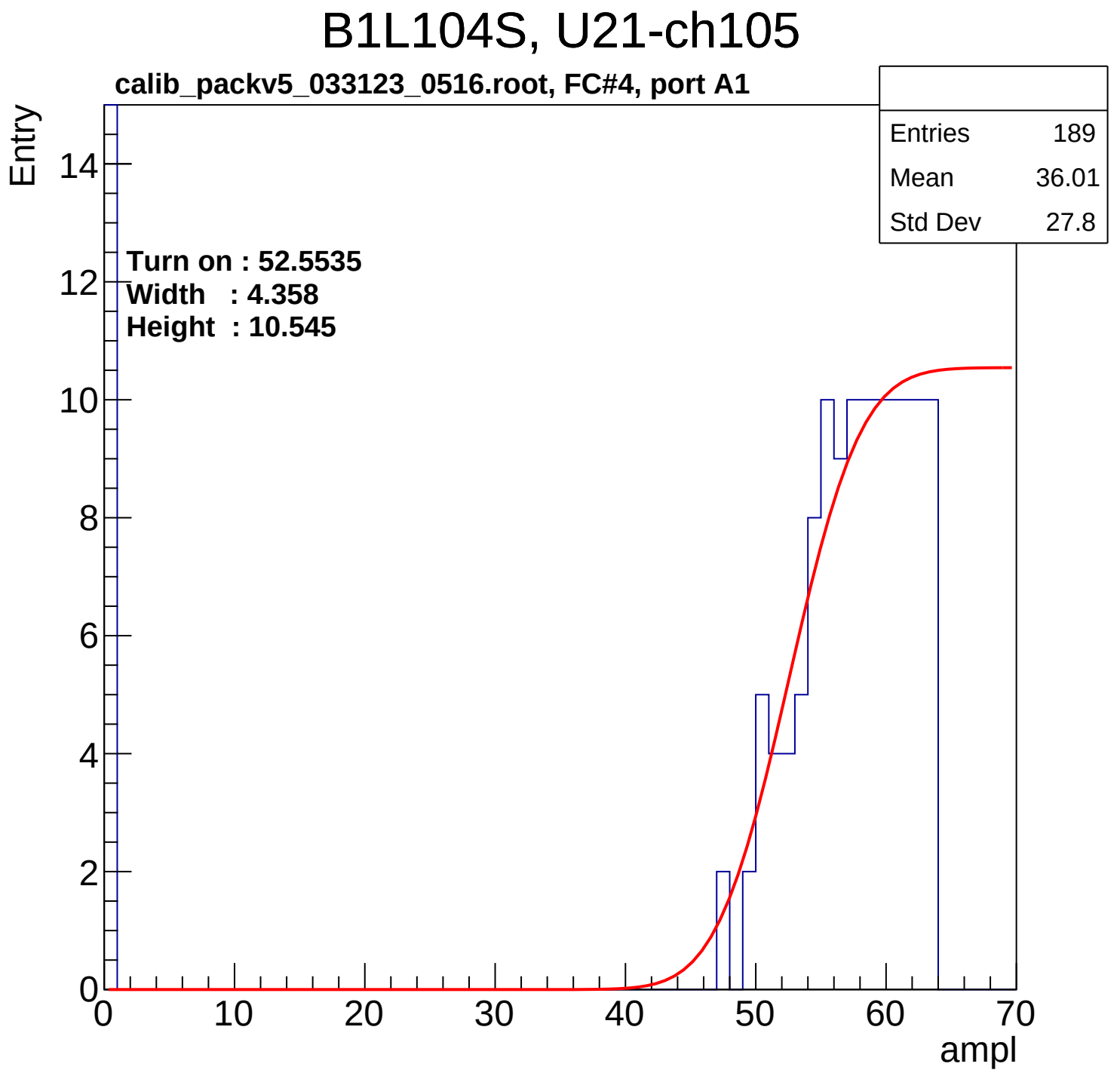
B1L104S, U21-ch105
calib_packv5_033123_0516.root, FC#4, port A1
Entries 189
Mean 36.01
Std Dev 27.8
Turn on : 52.5535
Width : 4.358
Height : 10.545
Entry
ampl
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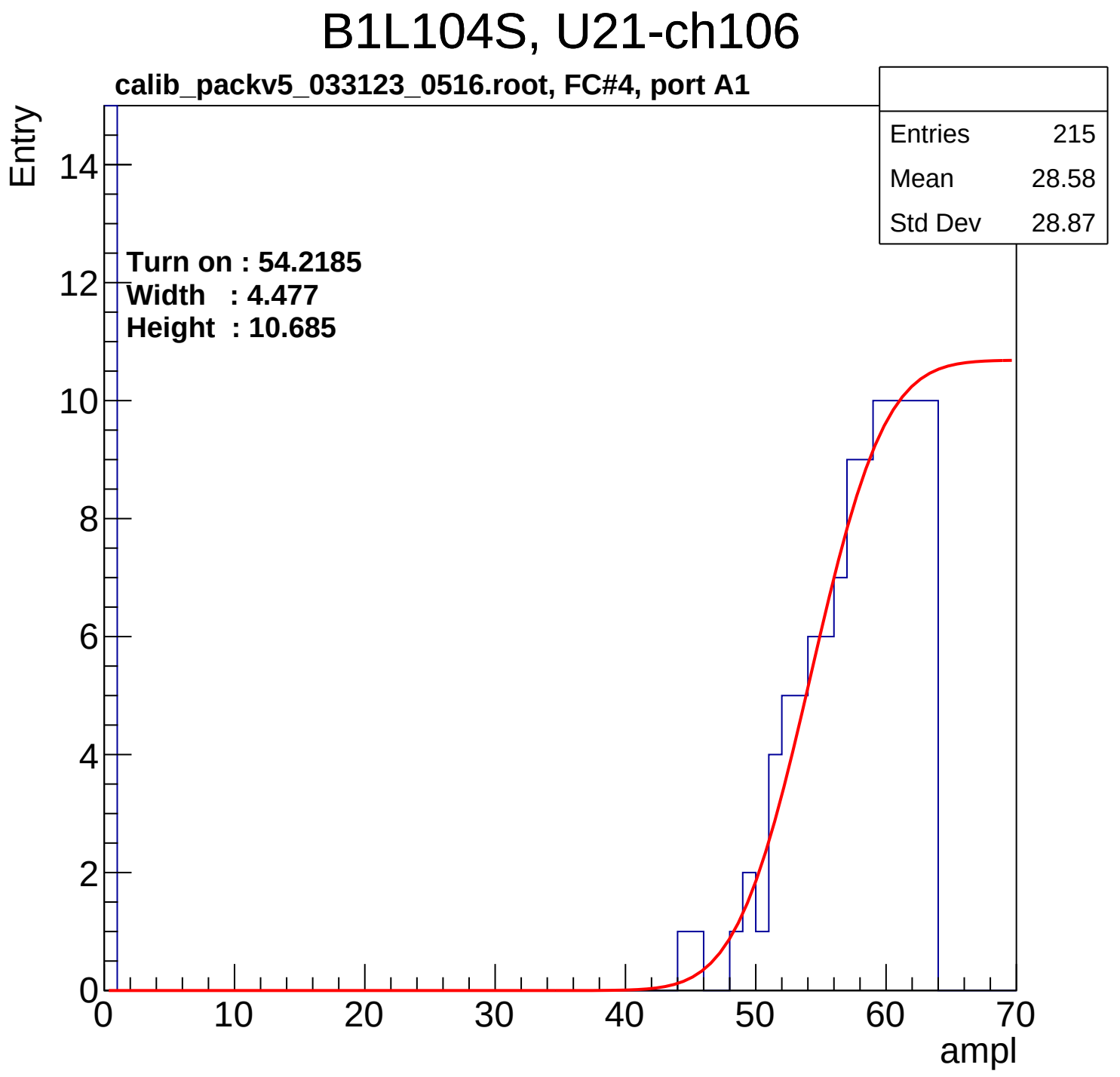
B1L104S, U21-ch106
calib_packv5_033123_0516.root, FC#4, port A1
Entries 215
Mean 28.58
Std Dev 28.87
Turn on : 54.2185
Width : 4.477
Height : 10.685
Entry
ampl
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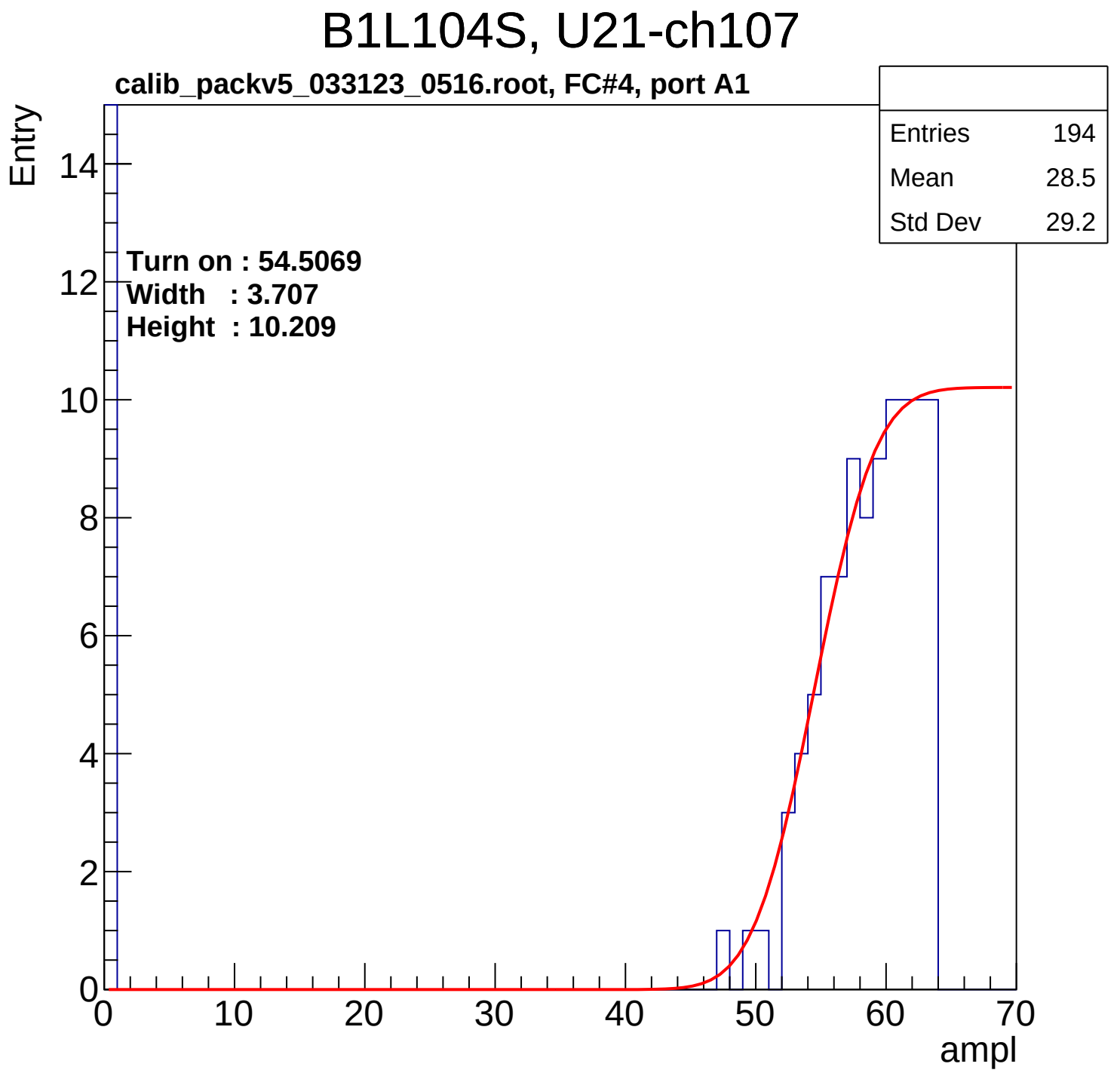
B1L104S, U21-ch107
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 28.5
Std Dev 29.2
Turn on : 54.5069
Width : 3.707
Height : 10.209
Entry
ampl
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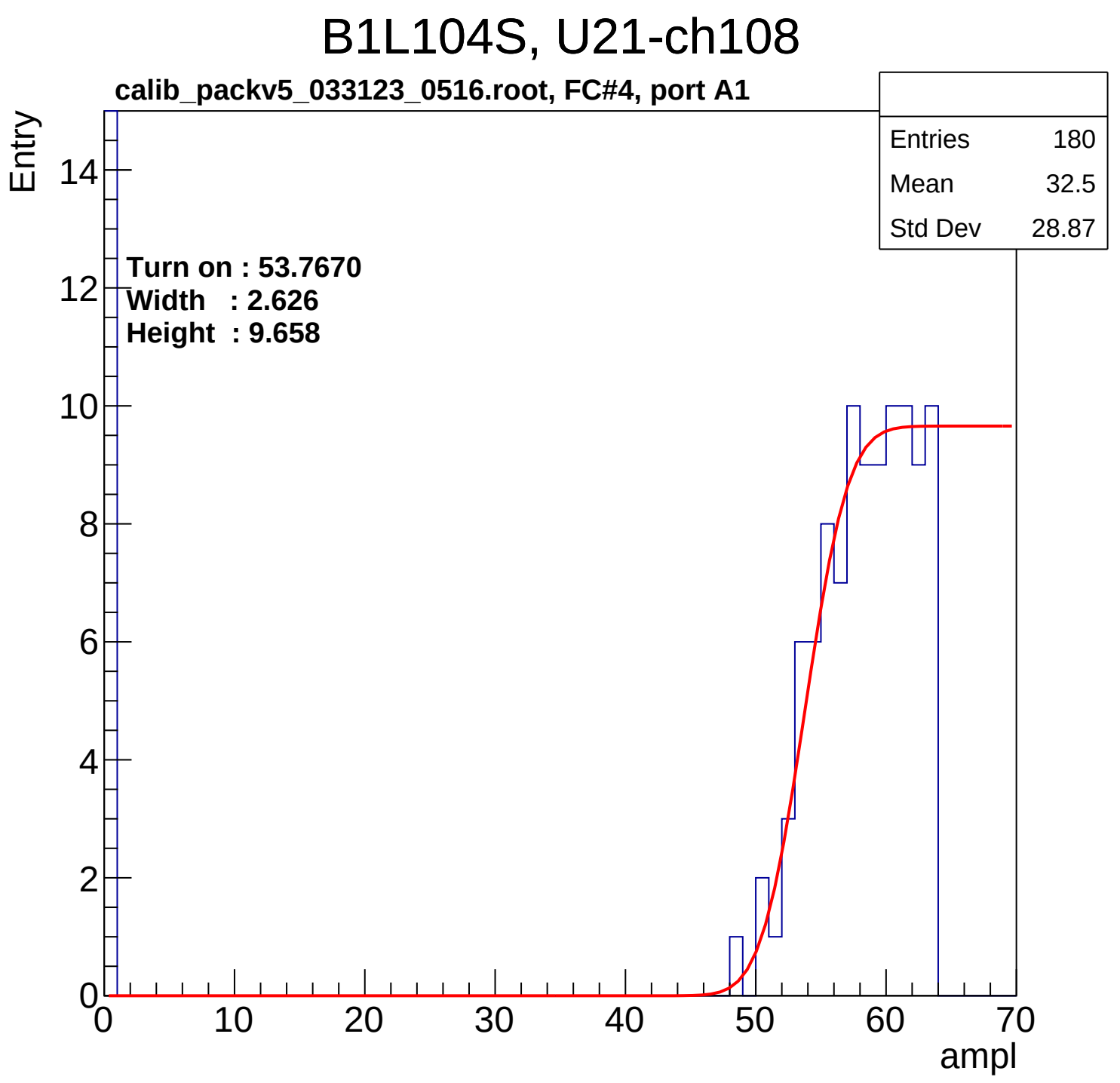
B1L104S, U21-ch108
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 32.5
Std Dev 28.87
Turn on : 53.7670
Width : 2.626
Height : 9.658
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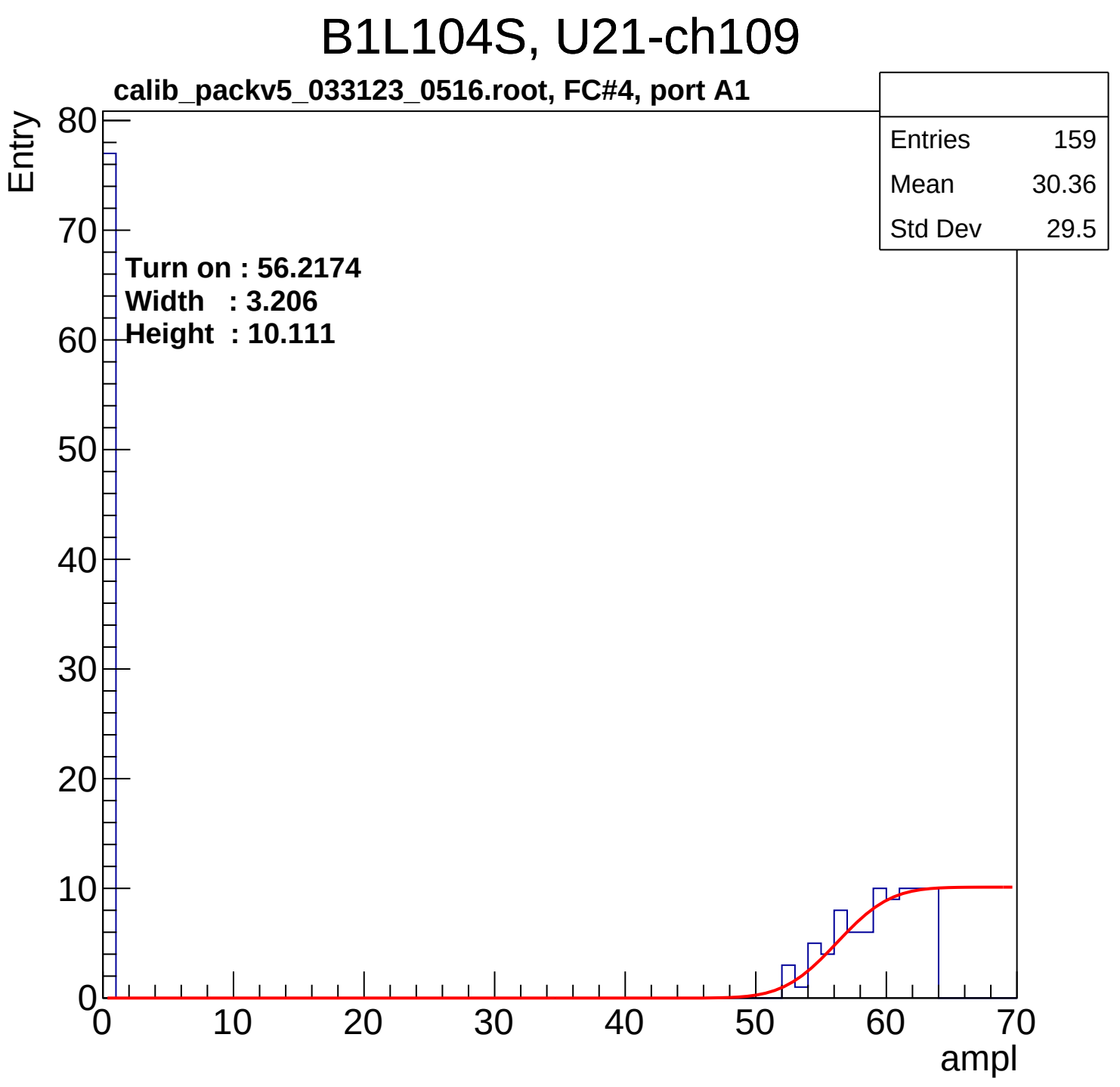
B1L104S, U21-ch109
calib_packv5_033123_0516.root, FC#4, port A1
Entries 159
Mean 30.36
Std Dev 29.5
Turn on : 56.2174
Width : 3.206
Height : 10.111
Entry
ampl
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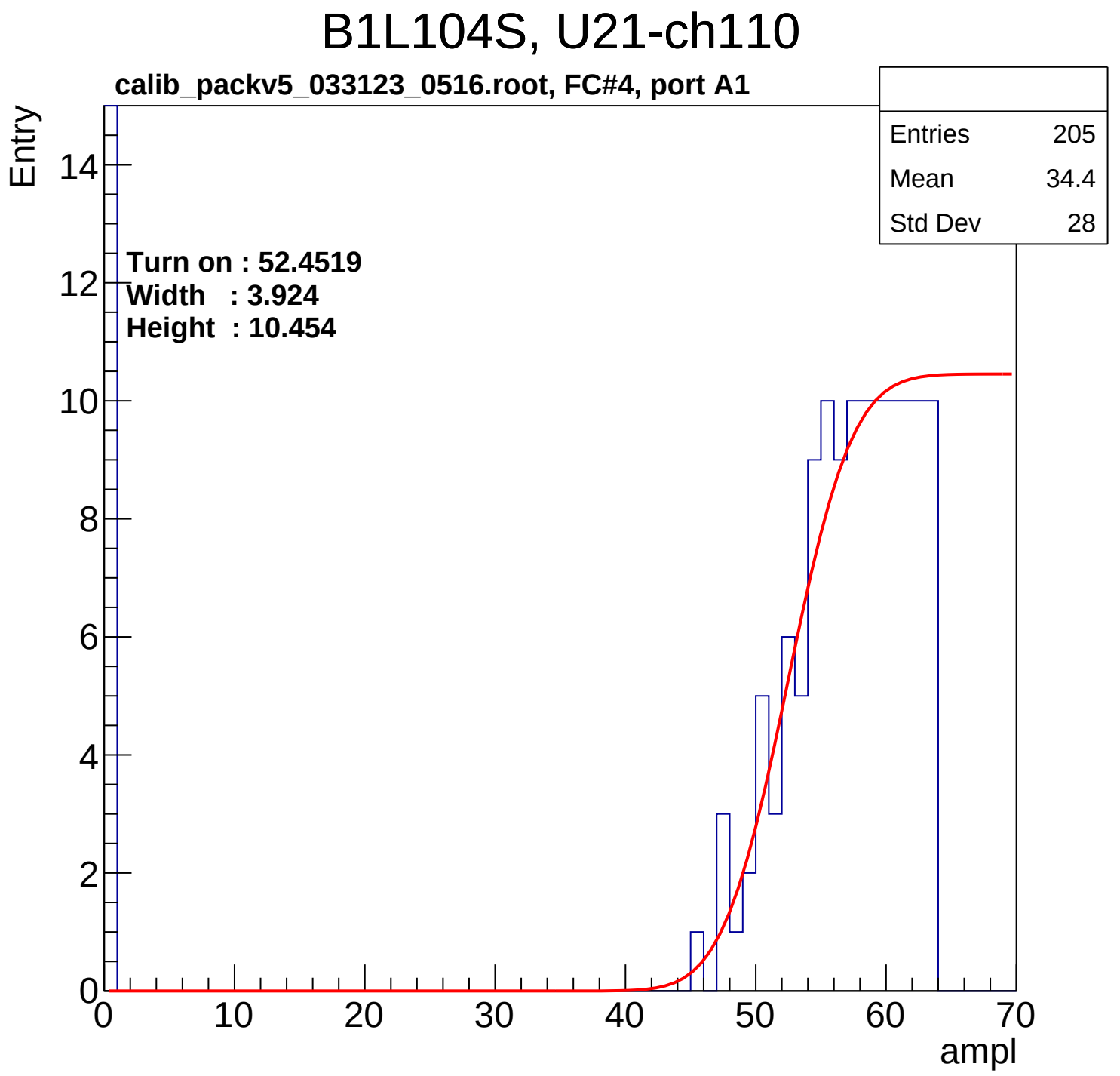
B1L104S, U21-ch110
calib_packv5_033123_0516.root, FC#4, port A1
Entries 205
Mean 34.4
Std Dev 28
Turn on : 52.4519
Width : 3.924
Height : 10.454
Entry
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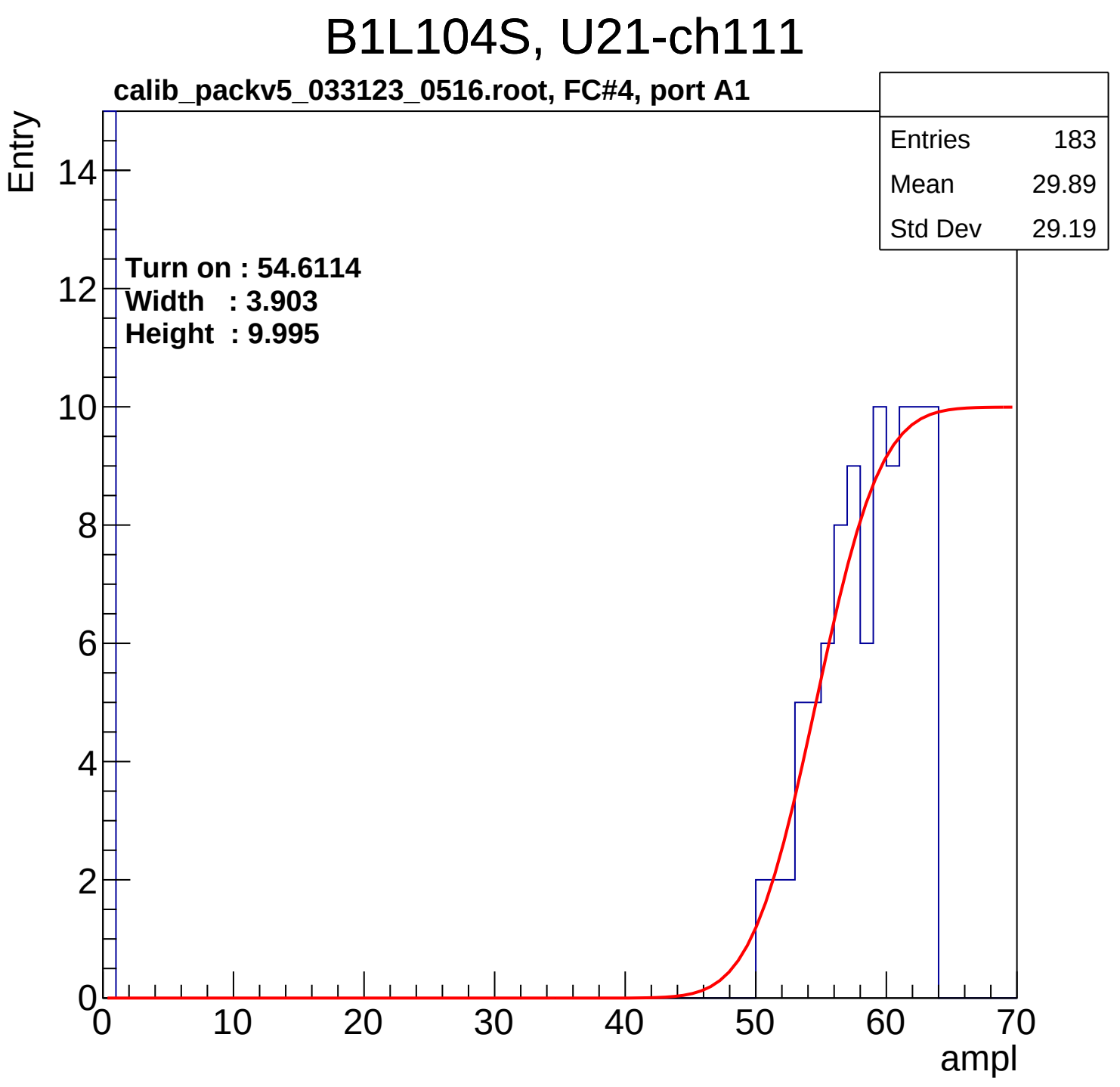
B1L104S, U21-ch111
calib_packv5_033123_0516.root, FC#4, port A1
Entries 183
Mean 29.89
Std Dev 29.19
Turn on : 54.6114
Width : 3.903
Height : 9.995
Entry
ampl
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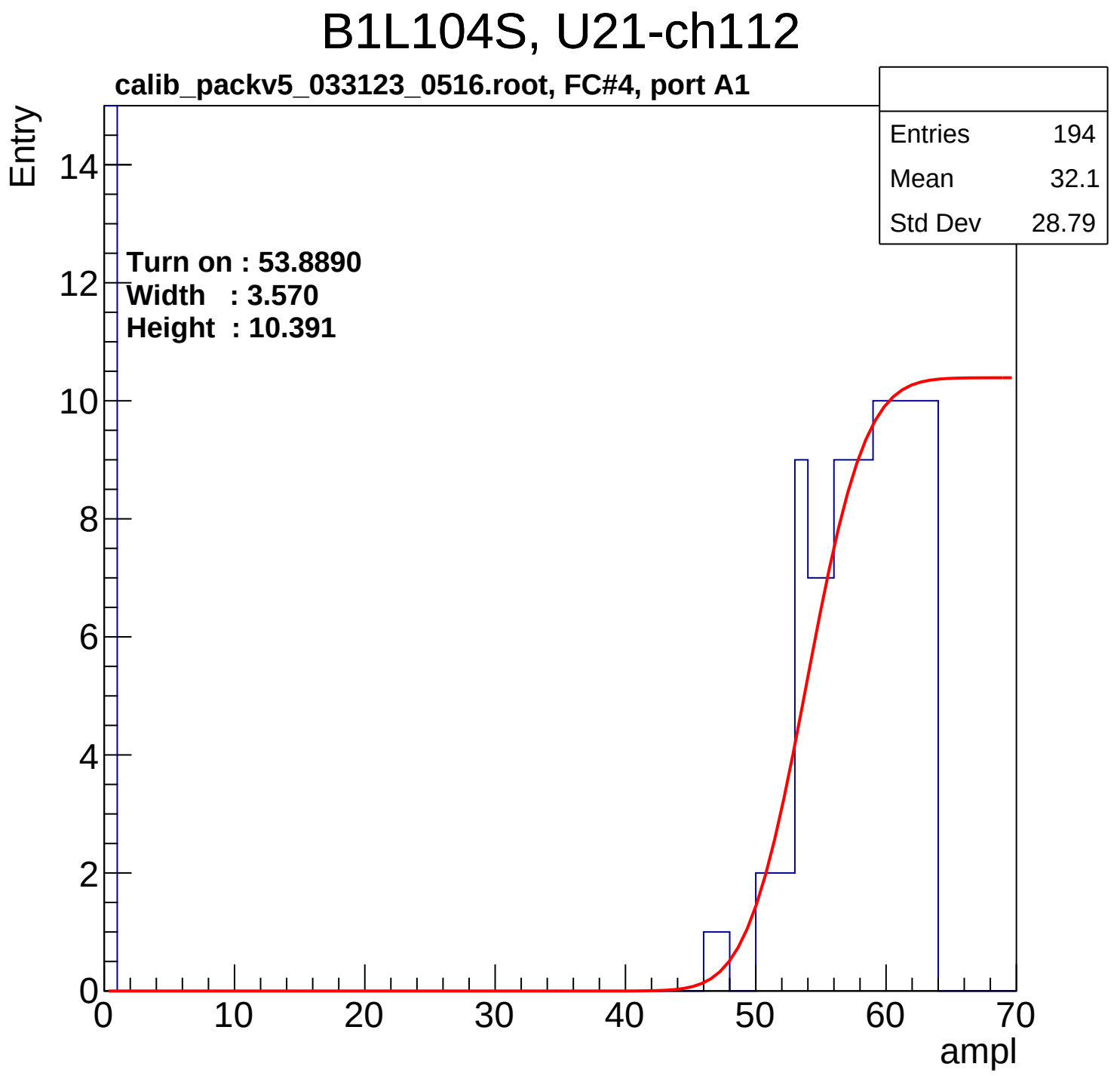
B1L104S, U21-ch112
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 32.1
Std Dev 28.79
Turn on : 53.8890
Width : 3.570
Height : 10.391
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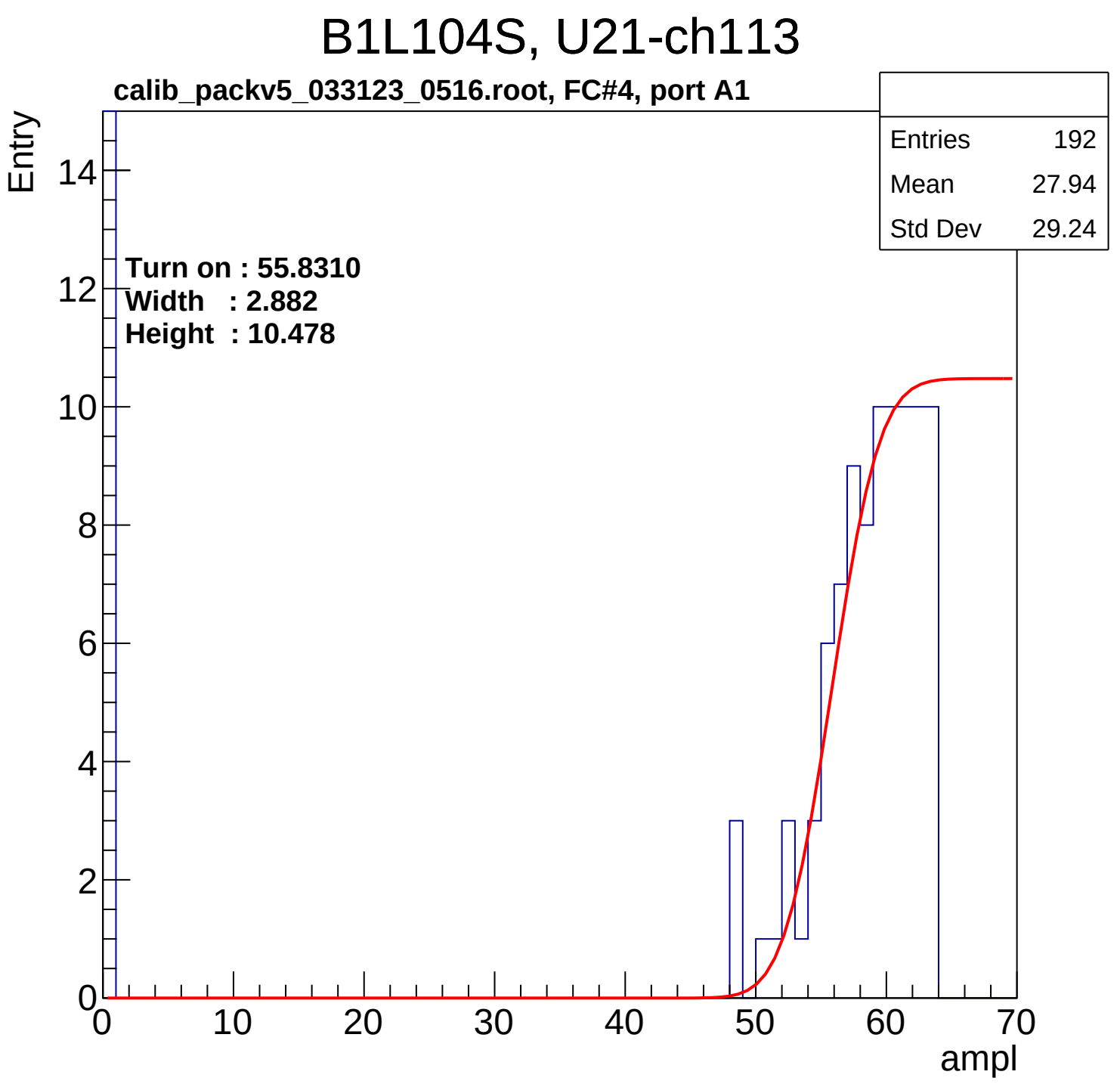
B1L104S, U21-ch113
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 27.94
Std Dev 29.24
Turn on : 55.8310
Width : 2.882
Height : 10.478
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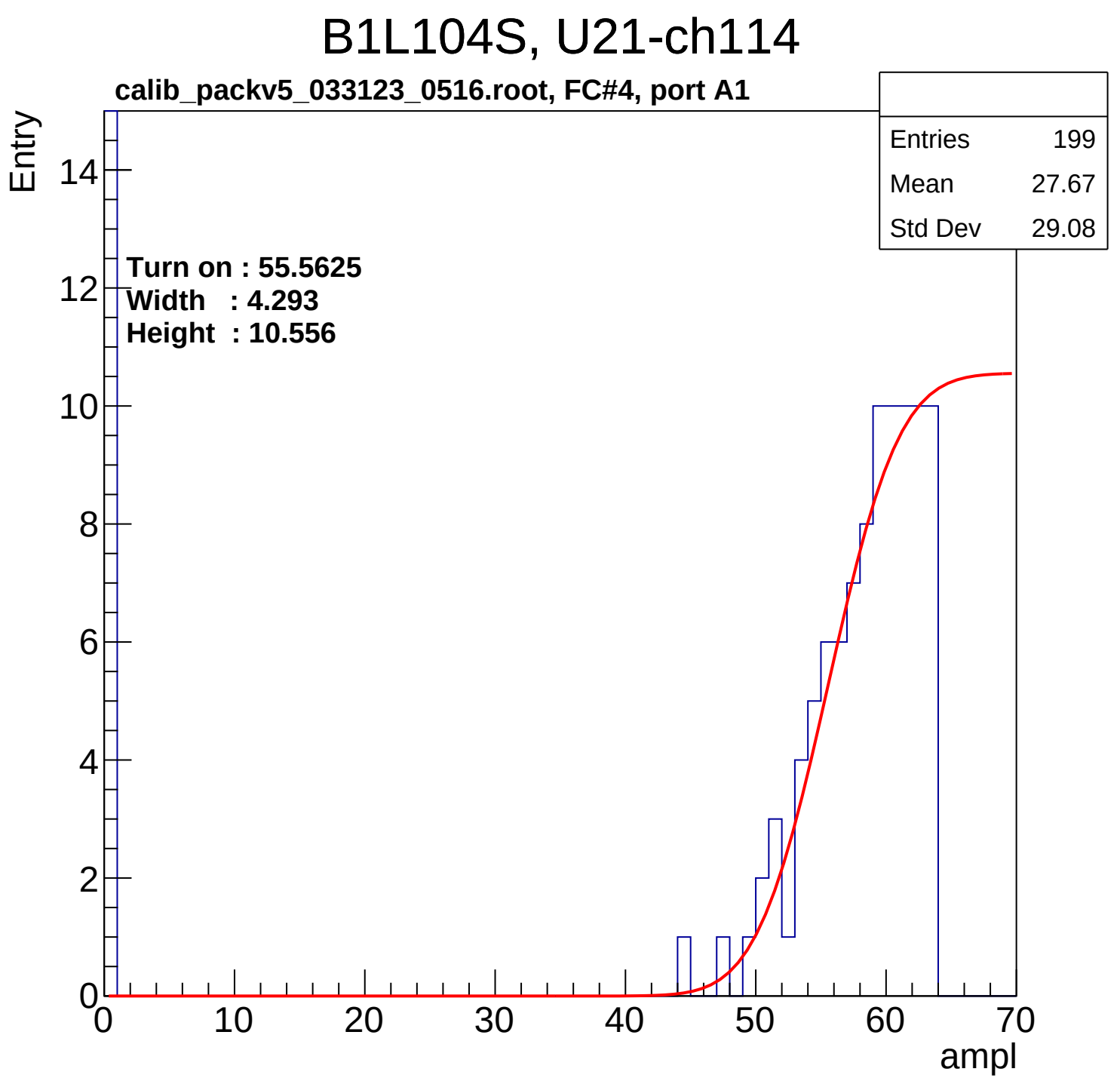
B1L104S, U21-ch114
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 27.67
Std Dev 29.08
Turn on : 55.5625
Width : 4.293
Height : 10.556
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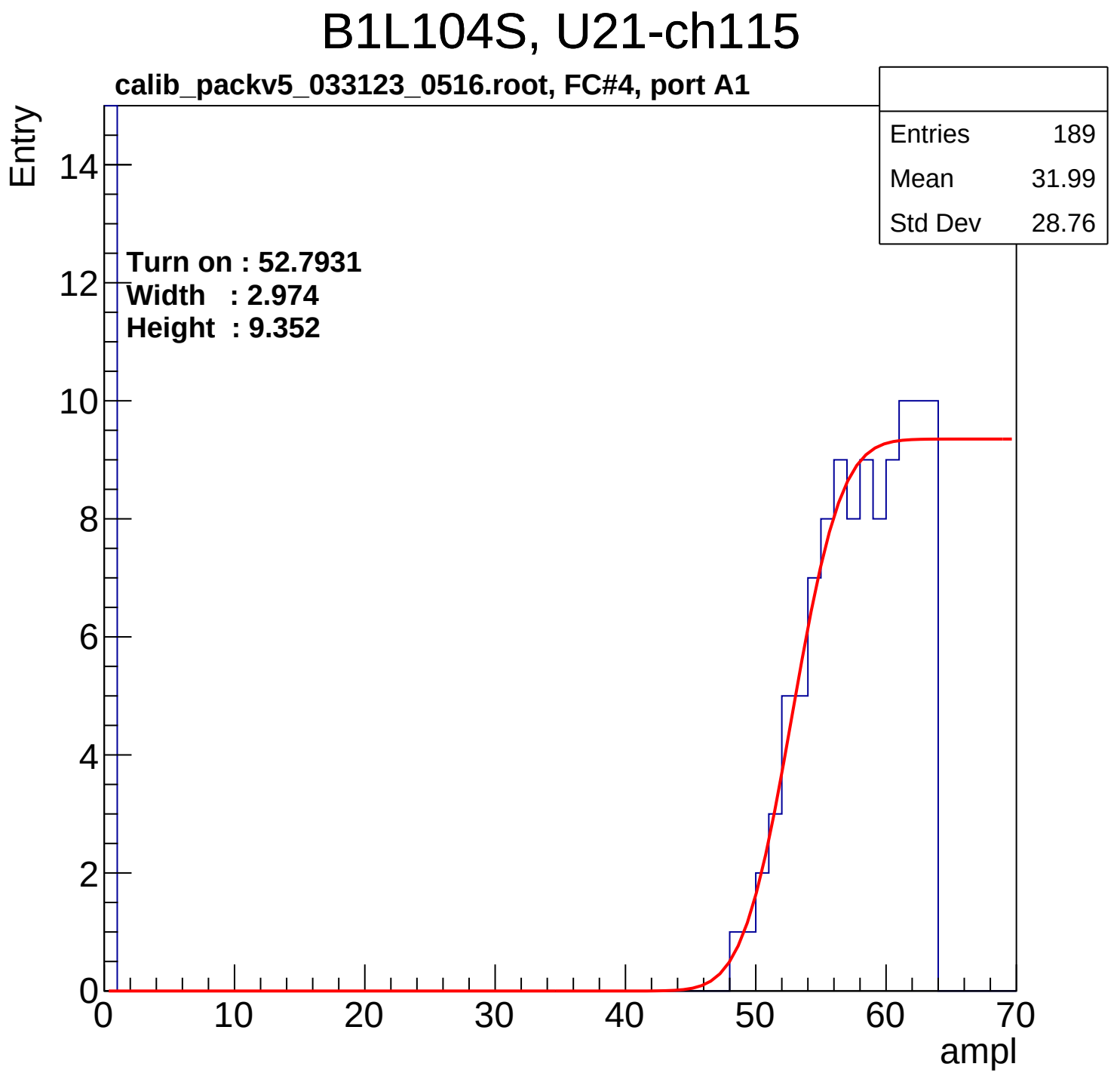
B1L104S, U21-ch115
calib_packv5_033123_0516.root, FC#4, port A1
Entries 189
Mean 31.99
Std Dev 28.76
Turn on : 52.7931
Width : 2.974
Height : 9.352
Entry
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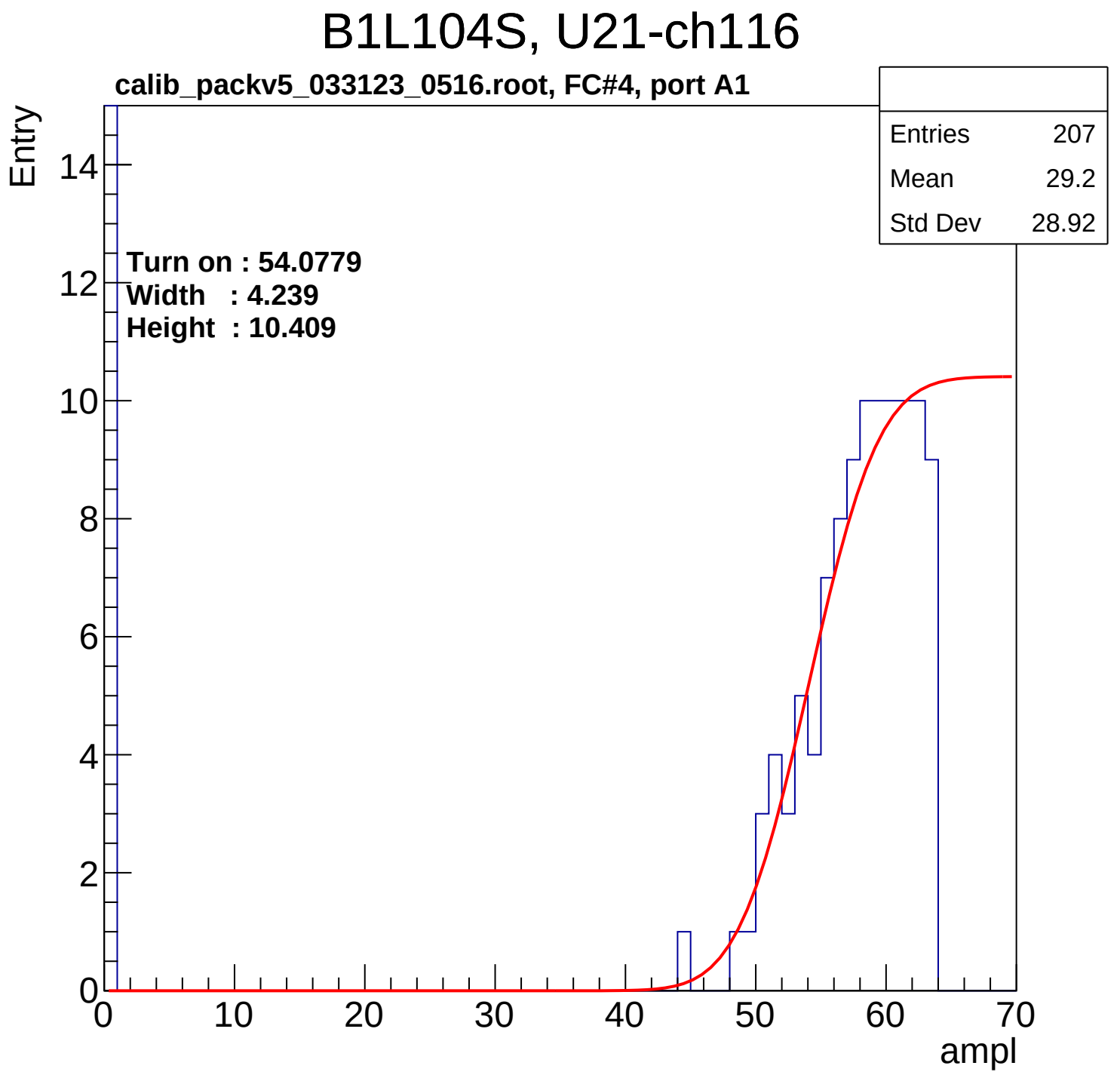
B1L104S, U21-ch116
calib_packv5_033123_0516.root, FC#4, port A1
Entries 207
Mean 29.2
Std Dev 28.92
Turn on : 54.0779
Width : 4.239
Height : 10.409
Entry
ampl
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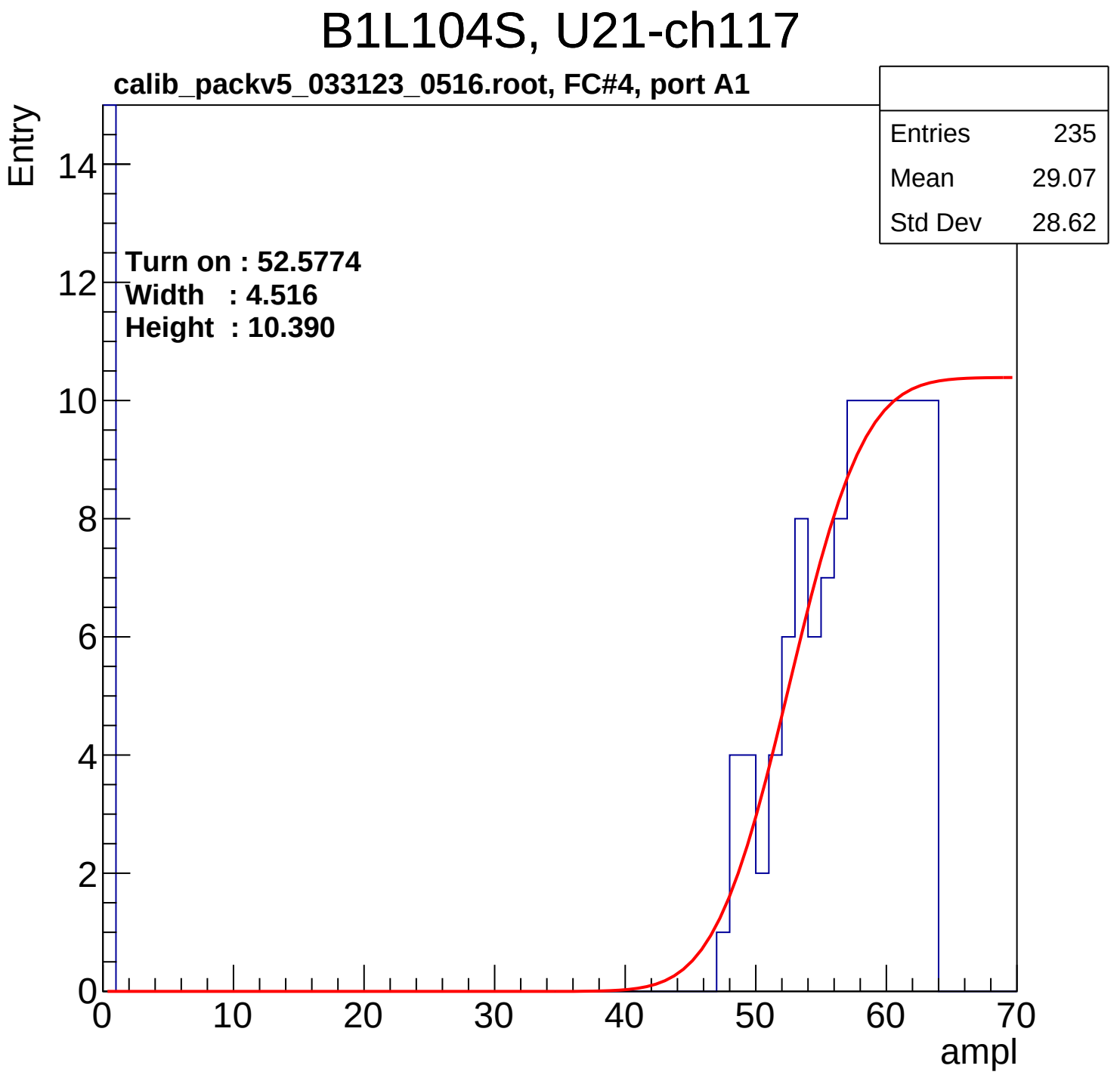
B1L104S, U21-ch117
calib_packv5_033123_0516.root, FC#4, port A1
Entries 235
Mean 29.07
Std Dev 28.62
Turn on : 52.5774
Width : 4.516
Height : 10.390
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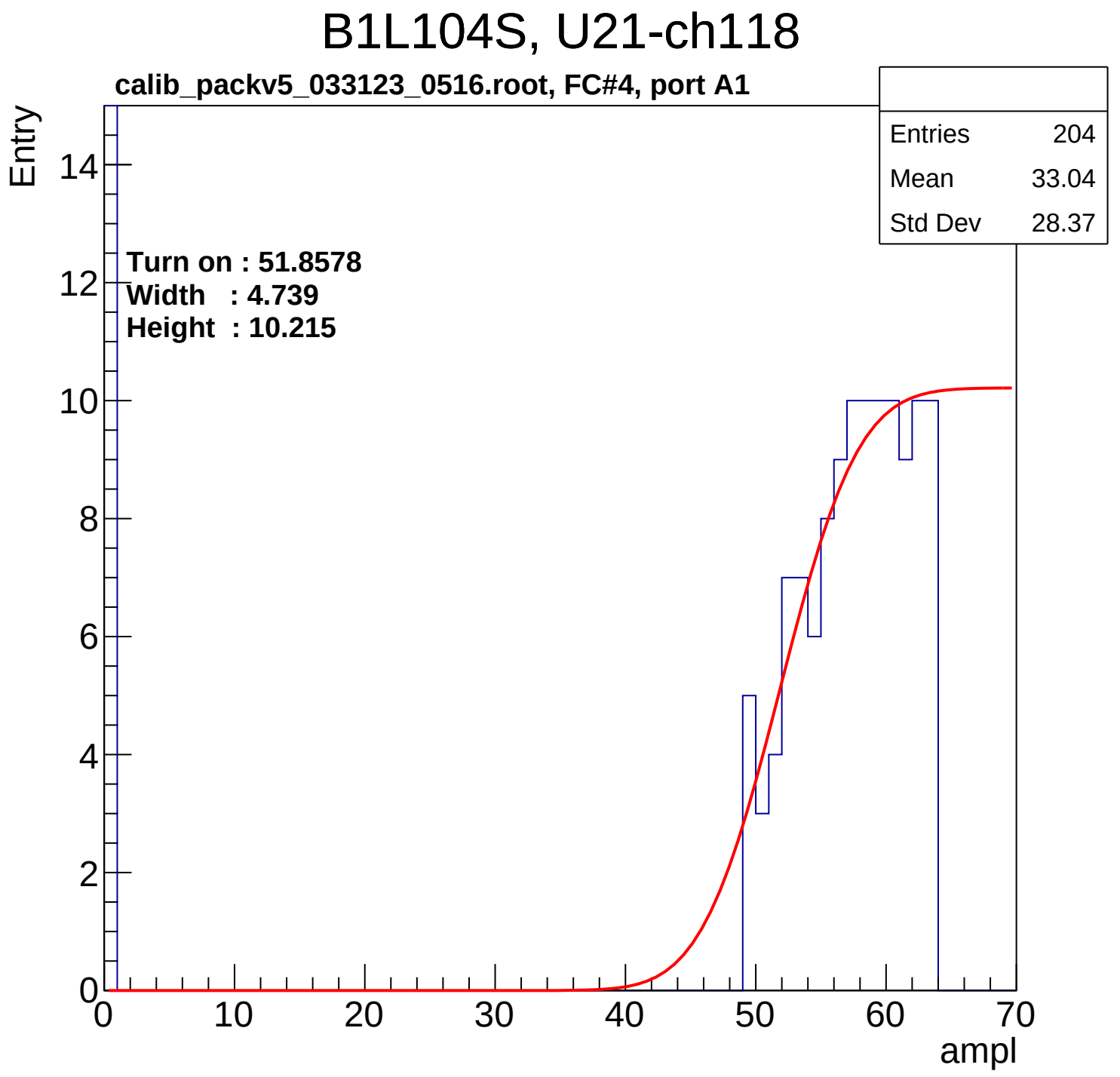
B1L104S, U21-ch118
calib_packv5_033123_0516.root, FC#4, port A1
Entries 204
Mean 33.04
Std Dev 28.37
Turn on : 51.8578
Width : 4.739
Height : 10.215
Entry
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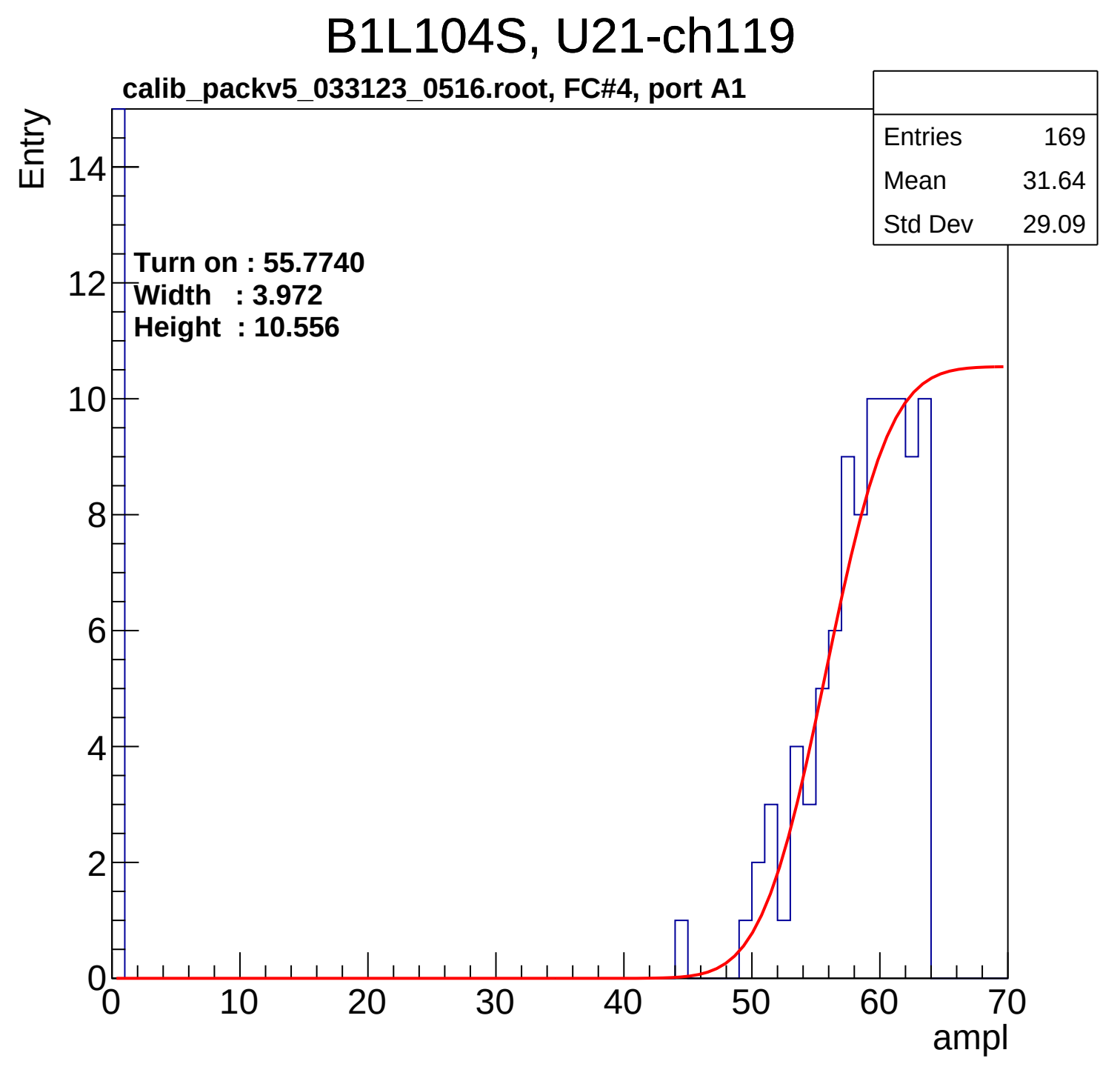
B1L104S, U21-ch119
calib_packv5_033123_0516.root, FC#4, port A1
Entries 169
Mean 31.64
Std Dev 29.09
Turn on : 55.7740
Width : 3.972
Height : 10.556
Entry
ampl
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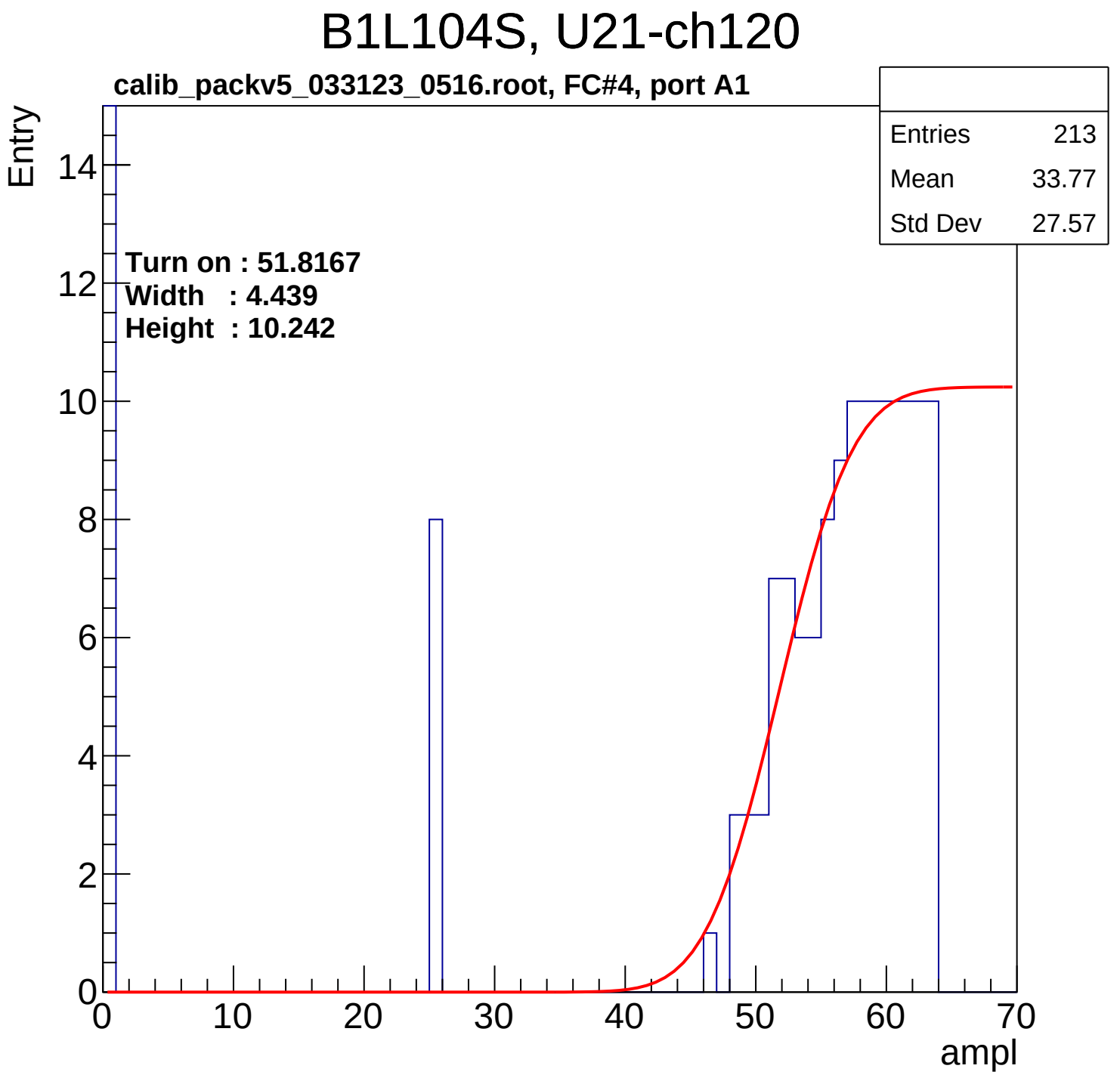
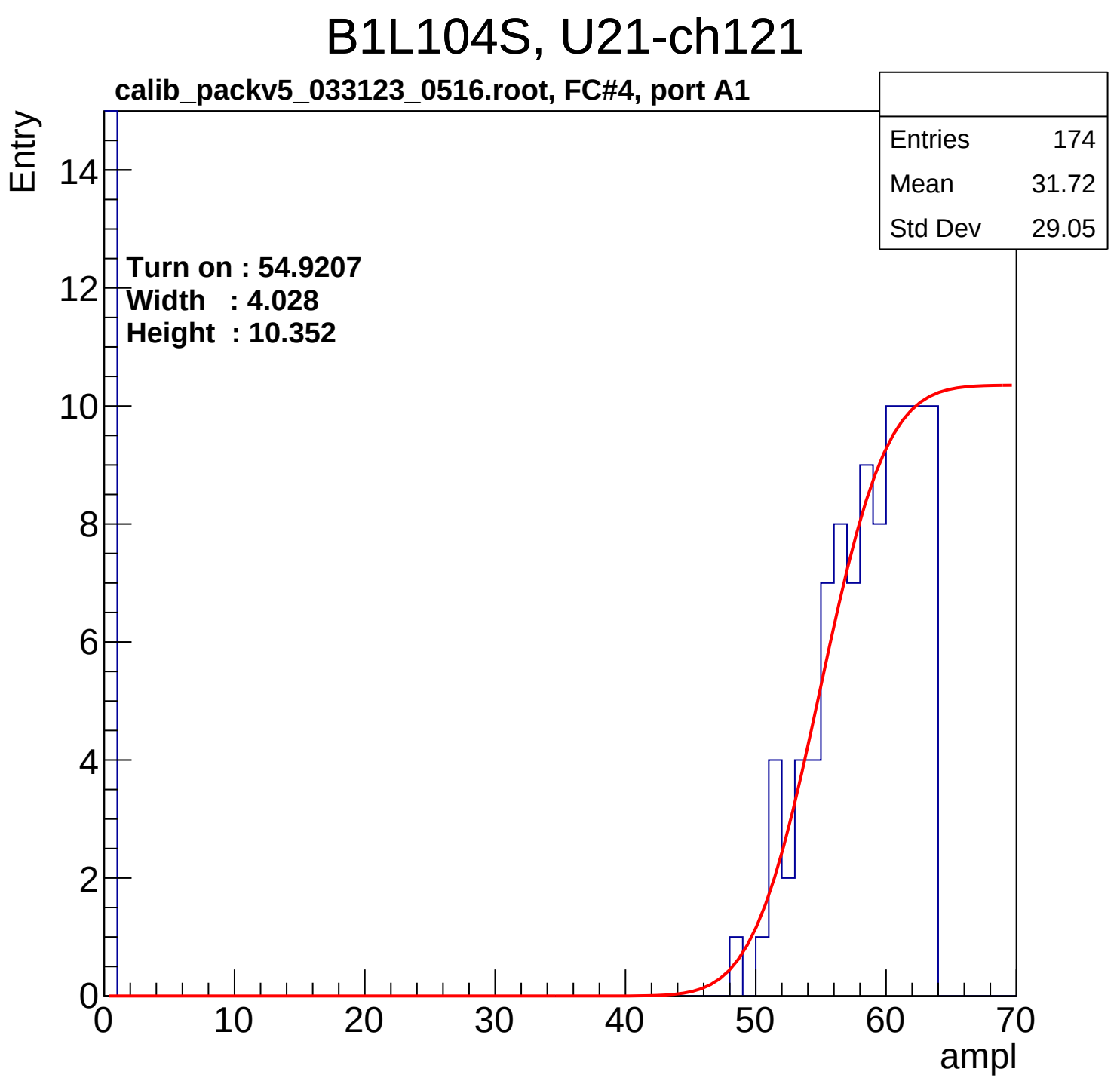
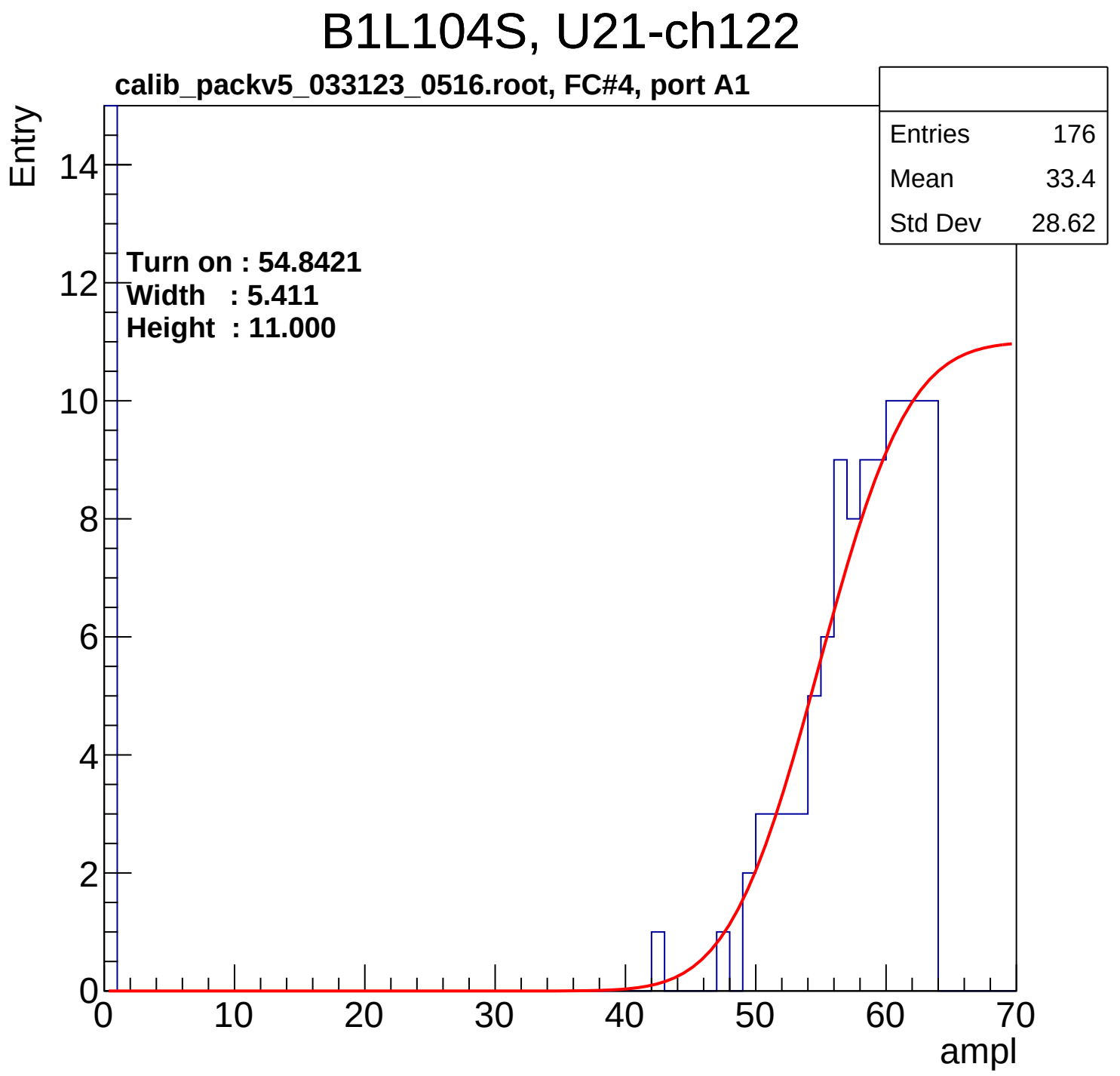
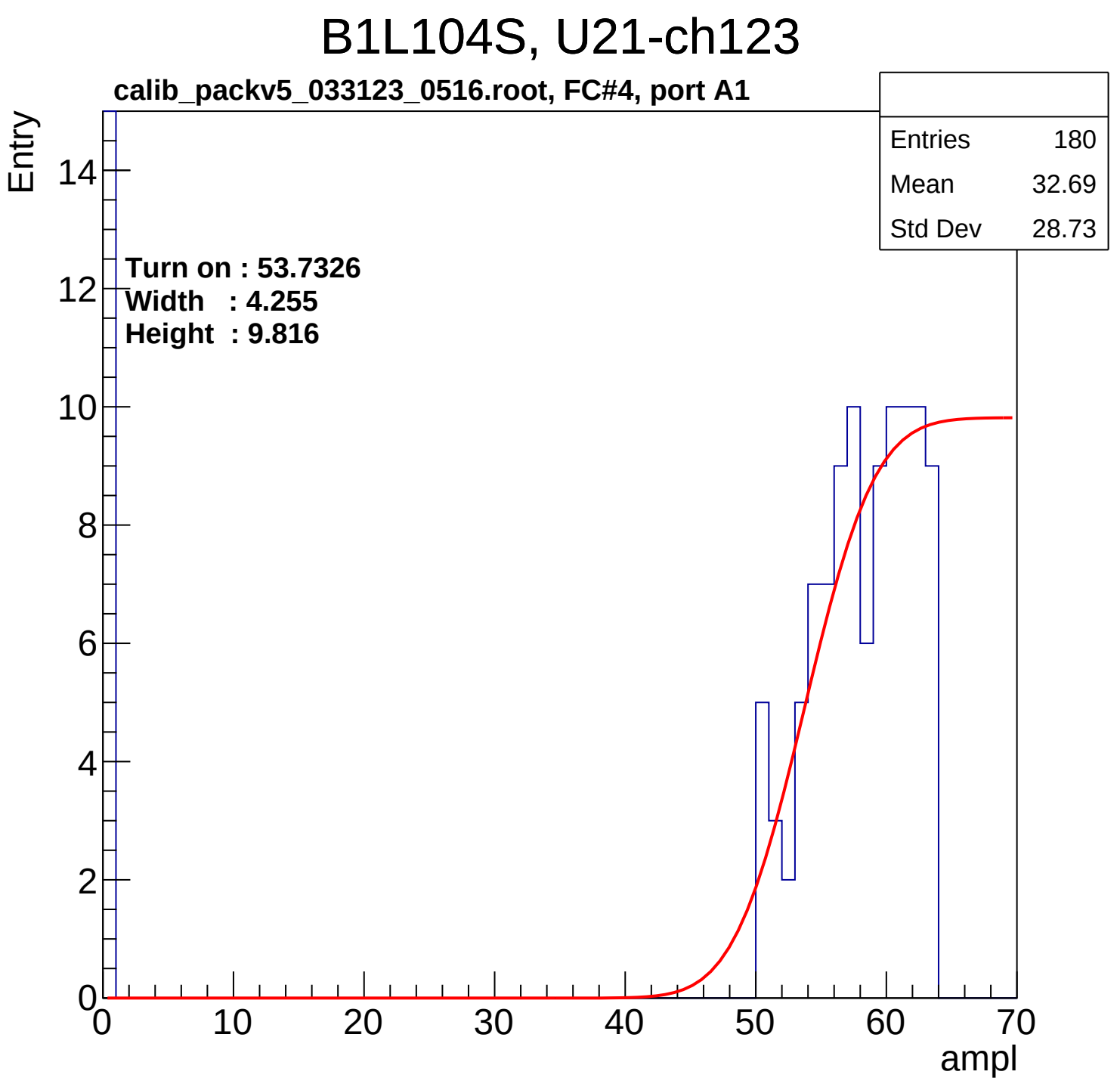
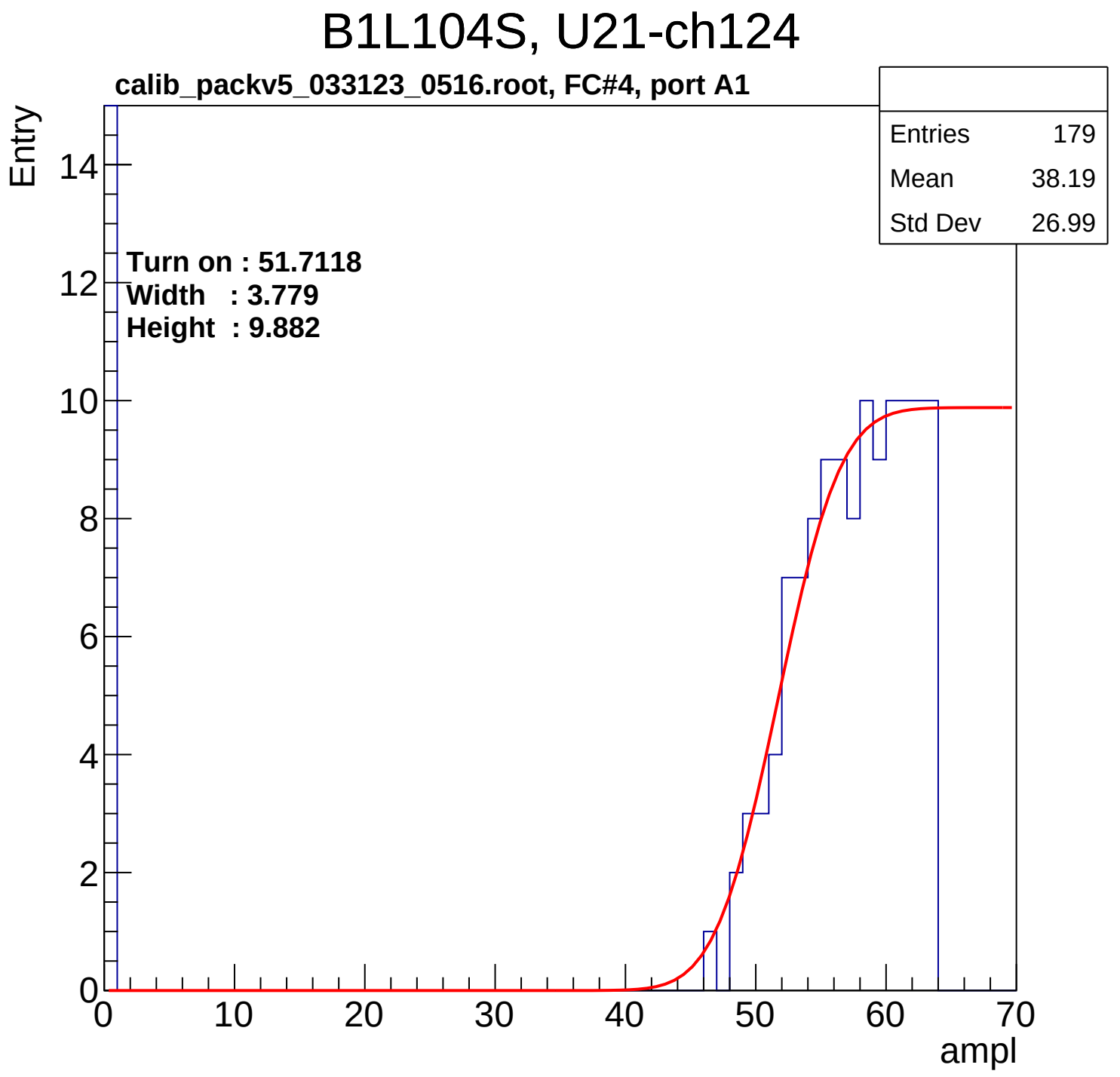

B1L104S, U21-ch120
calib_packv5_033123_0516.root, FC#4, port A1
Entries 213
Mean 33.77
Std Dev 27.57
Turn on : 51.8167
Width : 4.439
Height : 10.242
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U21-ch121
calib_packv5_033123_0516.root, FC#4, port A1
Entries 174
Mean 31.72
Std Dev 29.05
Turn on : 54.9207
Width : 4.028
Height : 10.352
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U21-ch122
calib_packv5_033123_0516.root, FC#4, port A1
Entries 176
Mean 33.4
Std Dev 28.62
Turn on : 54.8421
Width : 5.411
Height : 11.000
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U21-ch123
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 32.69
Std Dev 28.73
Turn on : 53.7326
Width : 4.255
Height : 9.816
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U21-ch124
calib_packv5_033123_0516.root, FC#4, port A1
Entries 179
Mean 38.19
Std Dev 26.99
Turn on : 51.7118
Width : 3.779
Height : 9.882
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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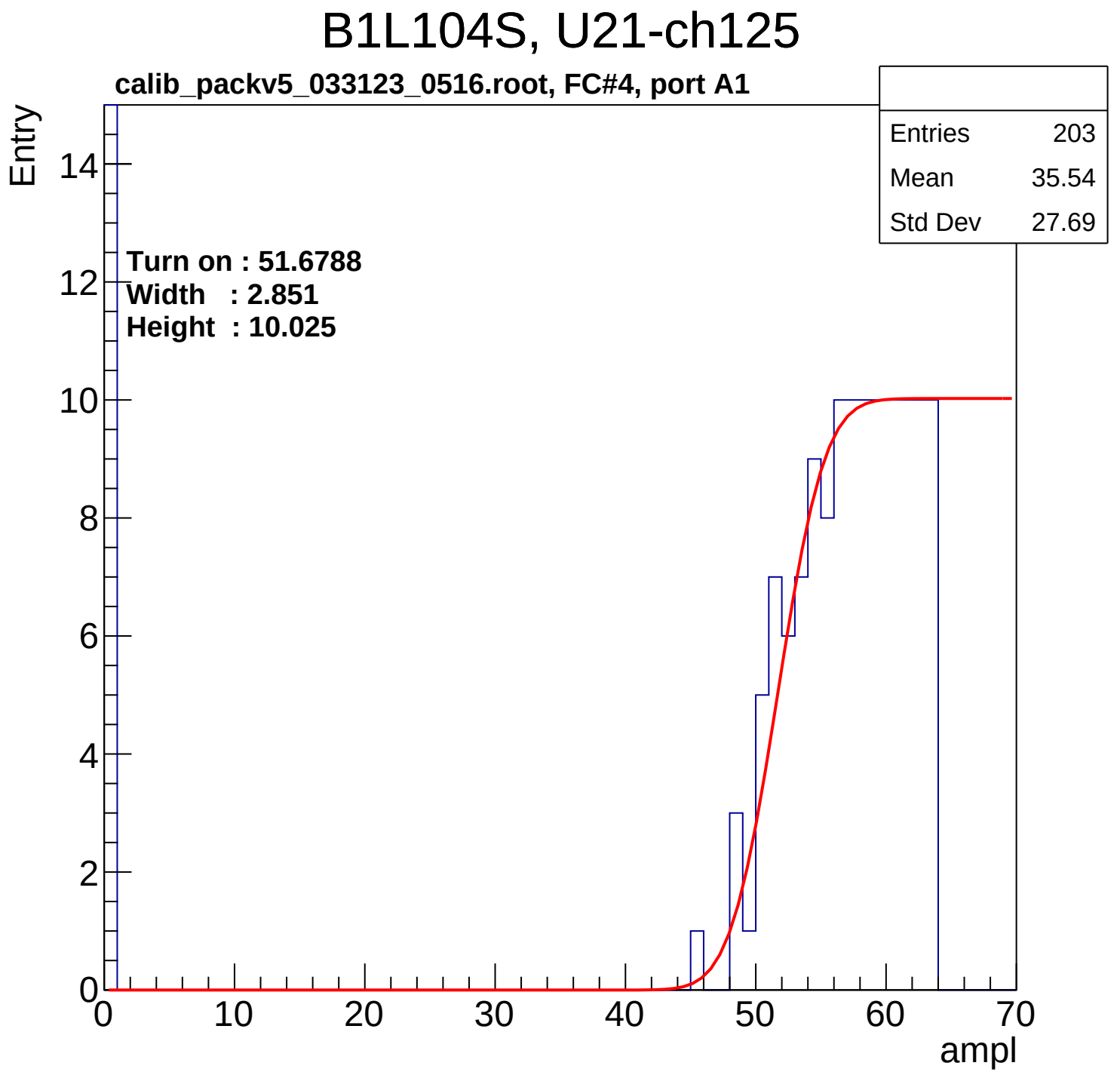
B1L104S, U21-ch125
calib_packv5_033123_0516.root, FC#4, port A1
Entries 203
Mean 35.54
Std Dev 27.69
Turn on : 51.6788
Width : 2.851
Height : 10.025
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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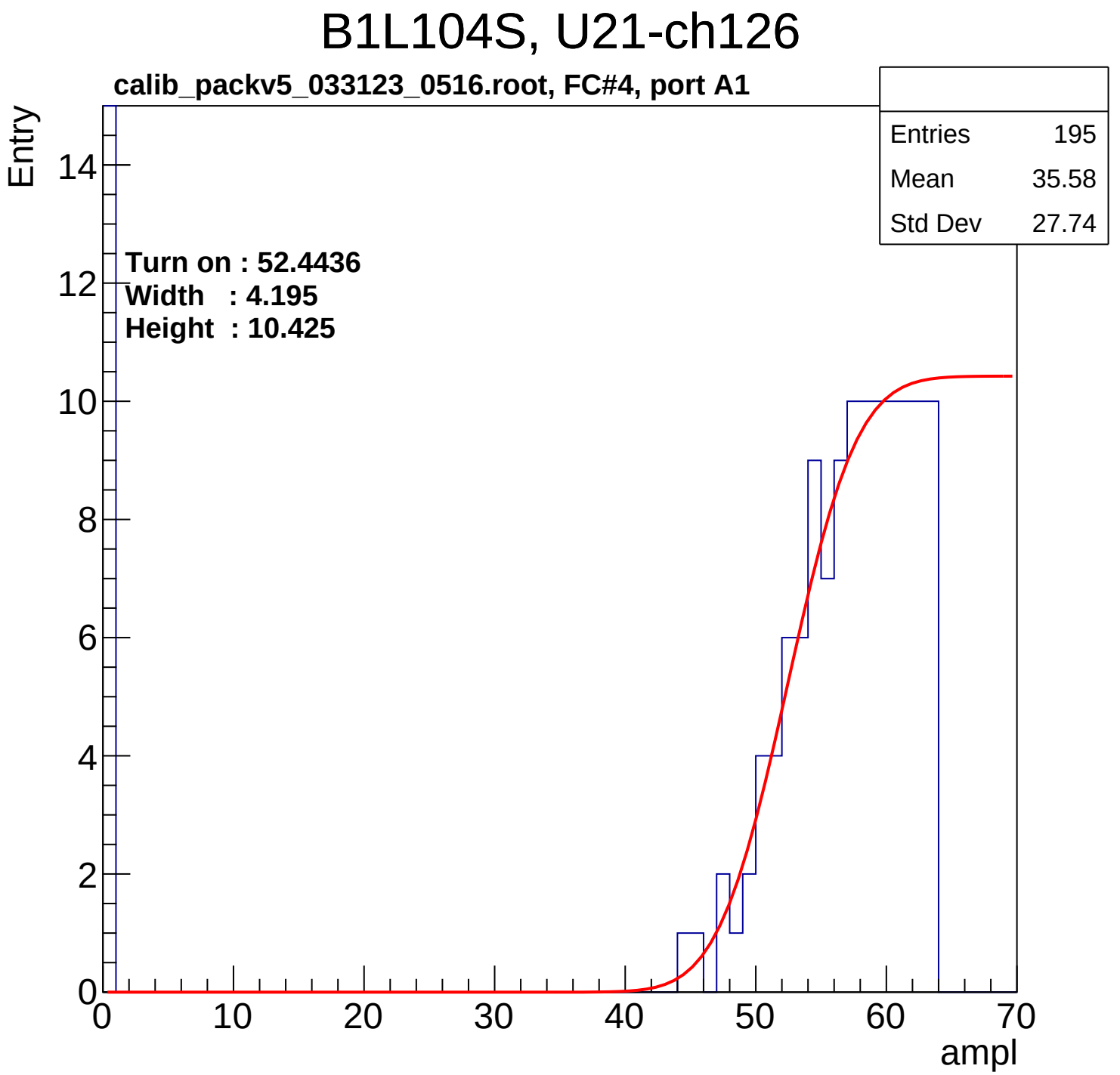
B1L104S, U21-ch126
calib_packv5_033123_0516.root, FC#4, port A1
Entries 195
Mean 35.58
Std Dev 27.74
Turn on : 52.4436
Width : 4.195
Height : 10.425
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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40
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60
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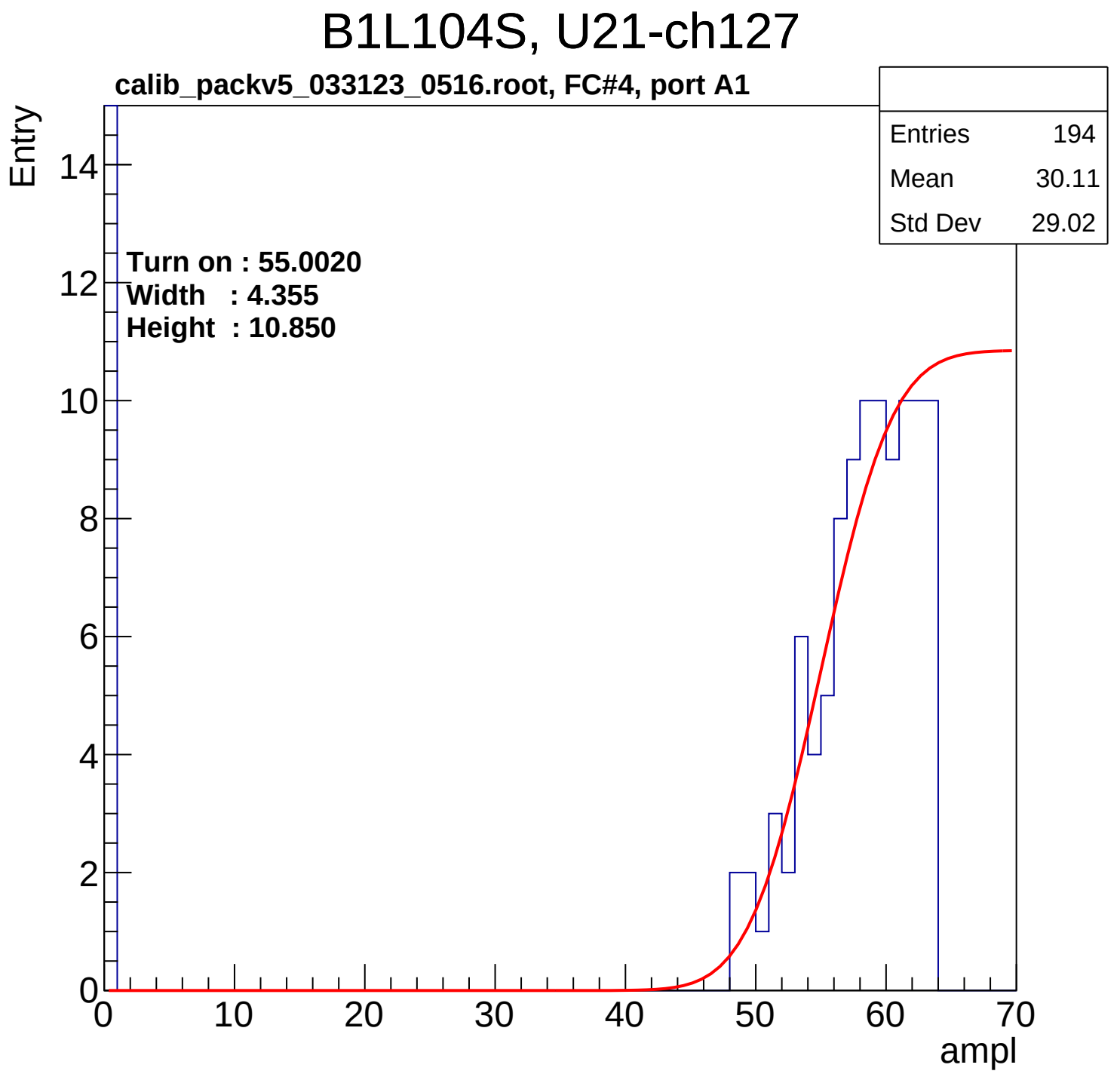
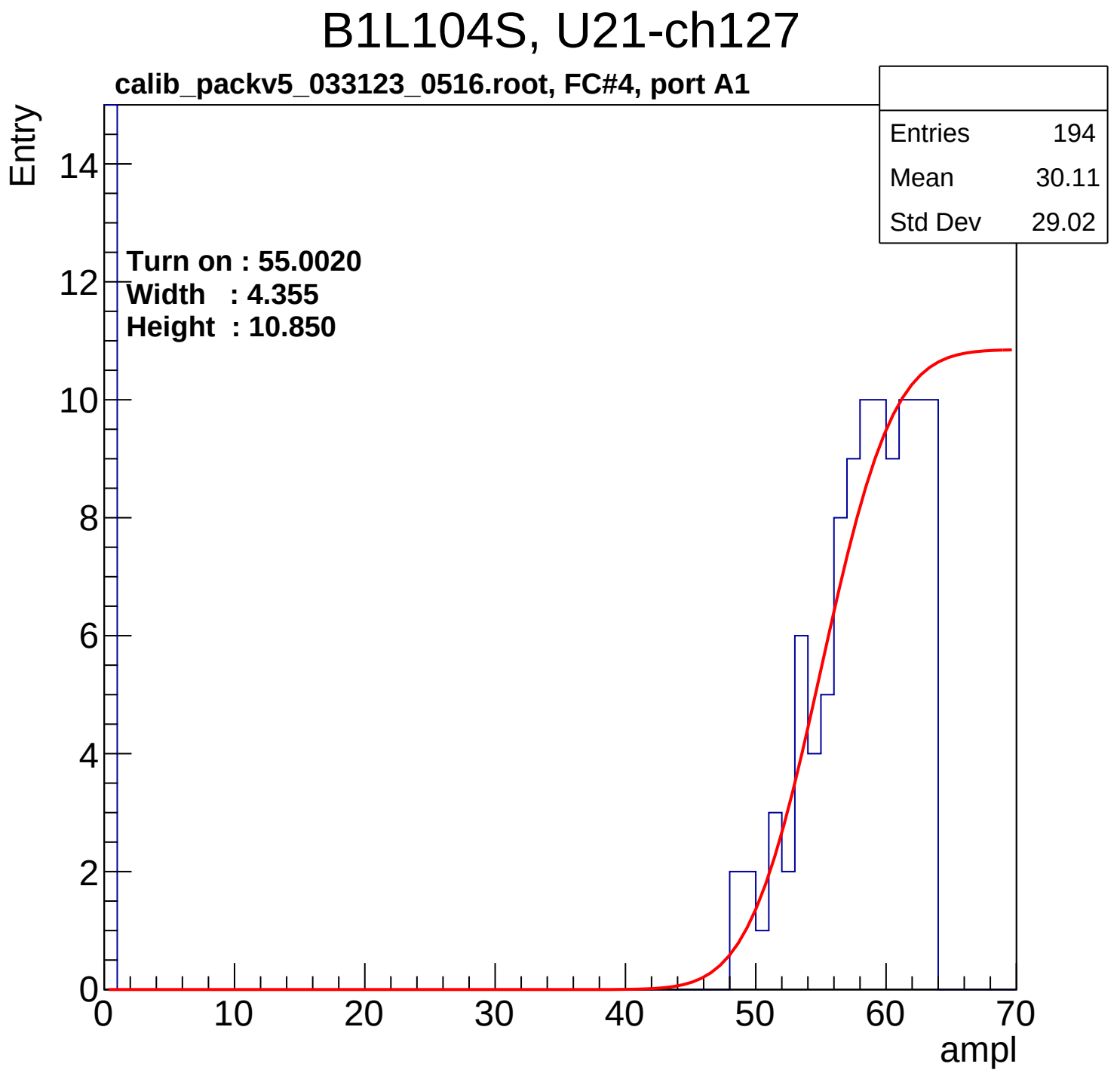

B1L104S, U21-ch127
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 30.11
Std Dev 29.02
Turn on : 55.0020
Width : 4.355
Height : 10.850
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U21-ch127
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 30.11
Std Dev 29.02
Turn on : 55.0020
Width : 4.355
Height : 10.850
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70