

B1L000S, U14-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.97
Std Dev	11.66

Turn on : 26.2162

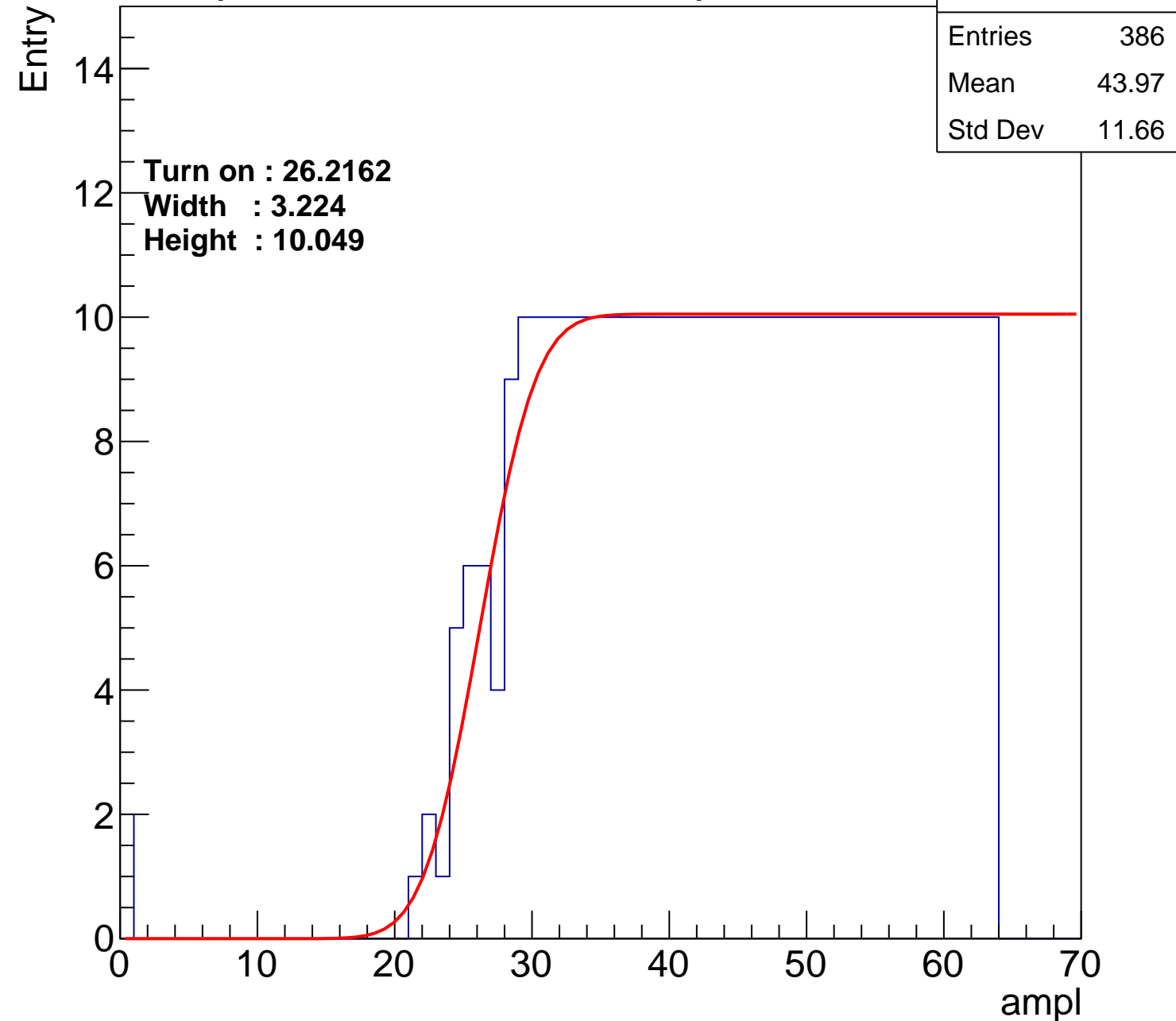
Width : 3.224

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch1

calib_packv5_042523_0143.root, FC#3, port B3

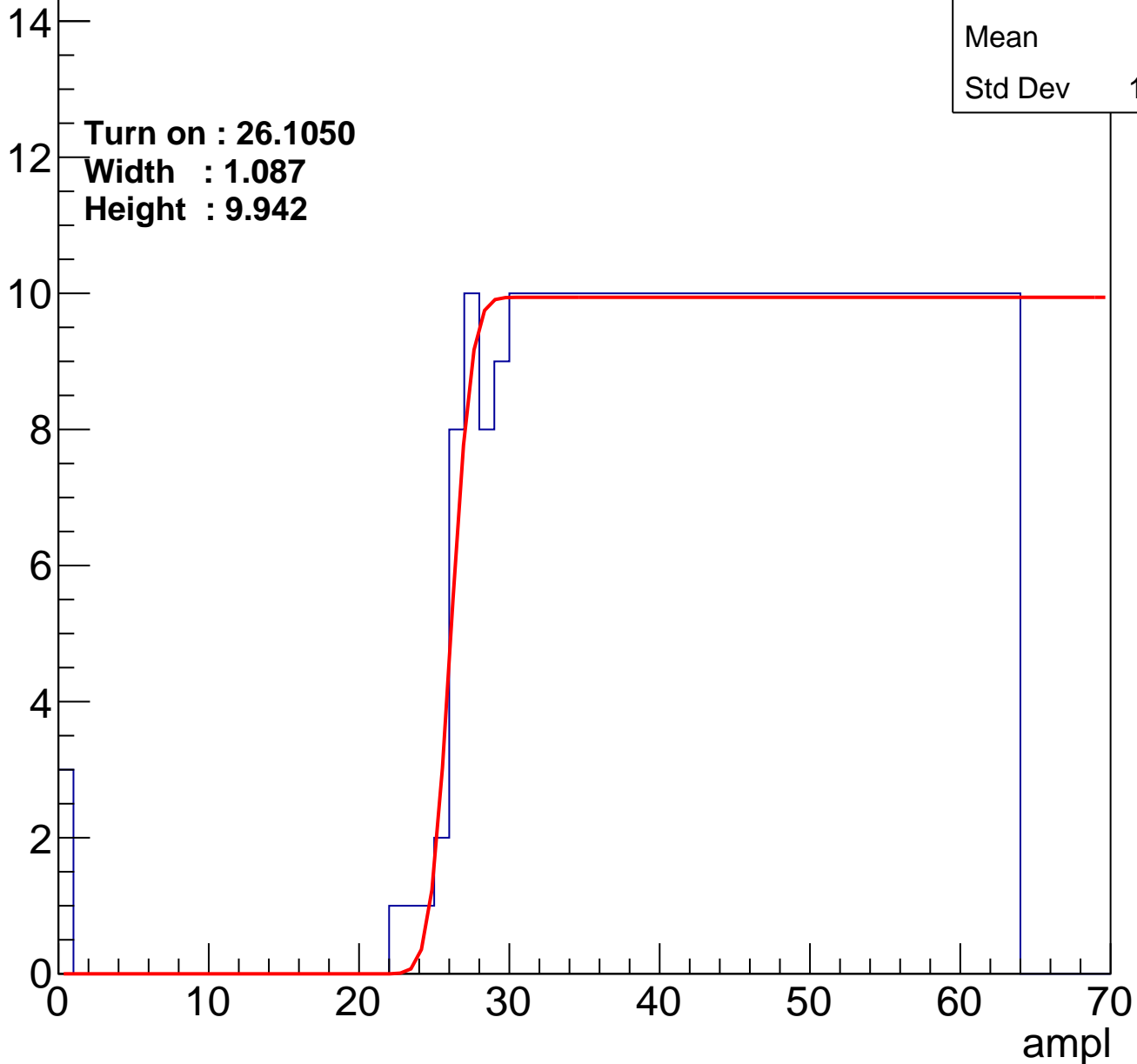
Entries	383
Mean	44.1
Std Dev	11.68

Turn on : 26.1050

Width : 1.087

Height : 9.942

Entry



B1L000S, U14-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.64
Std Dev	11.7

Turn on : 24.4623

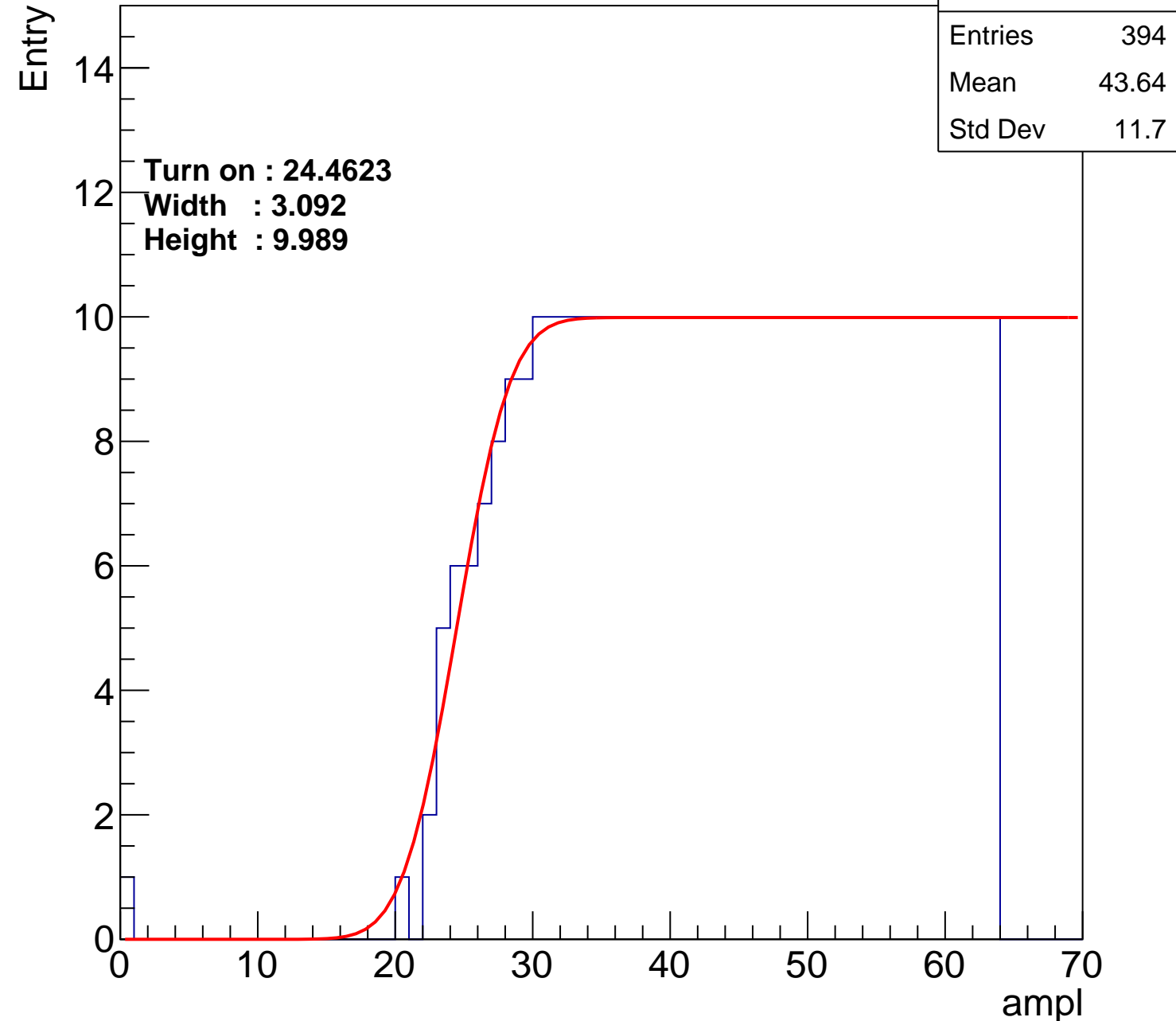
Width : 3.092

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 26.6760

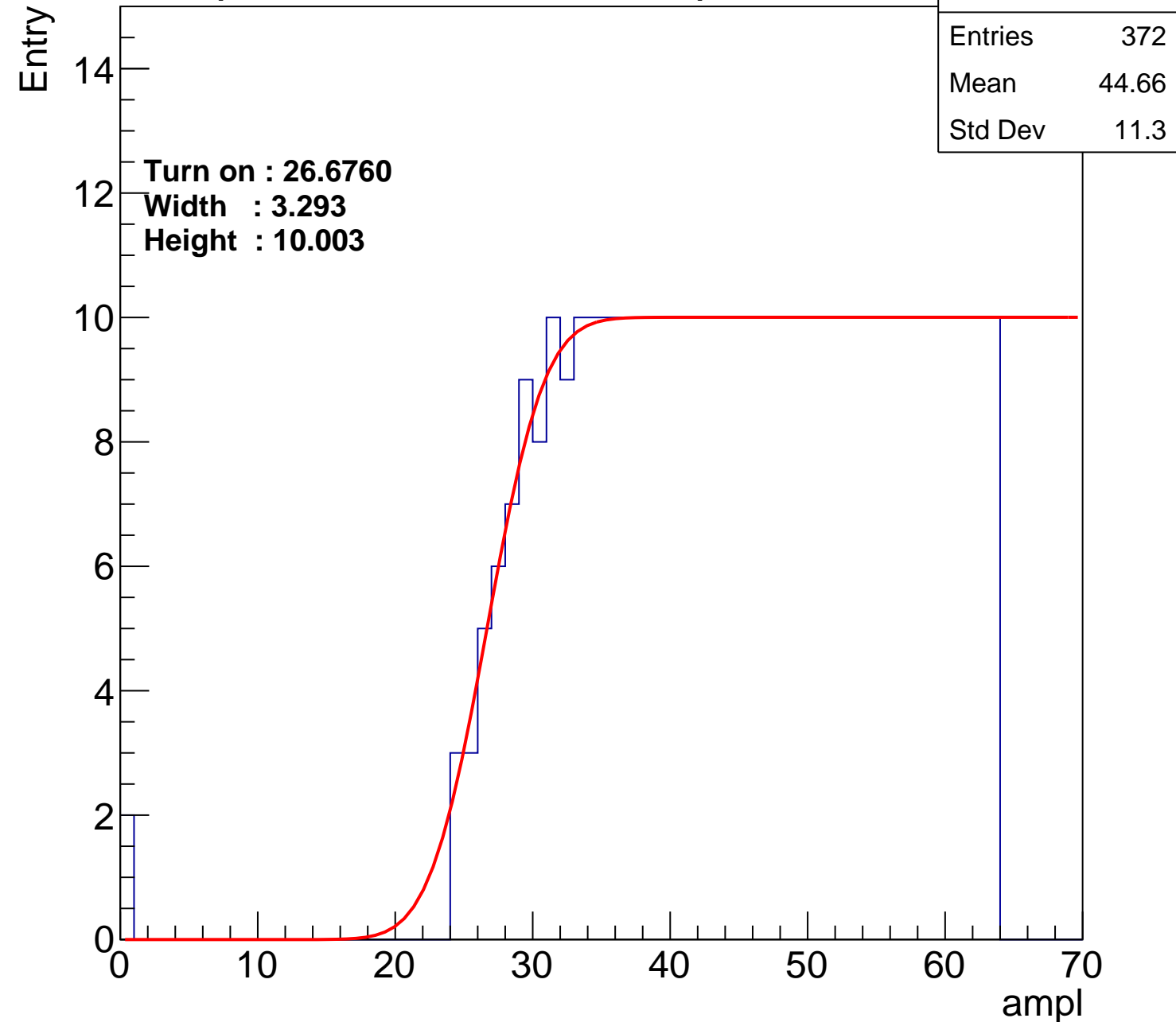
Width : 3.293

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.07
Std Dev	11.43

Turn on : 25.4361

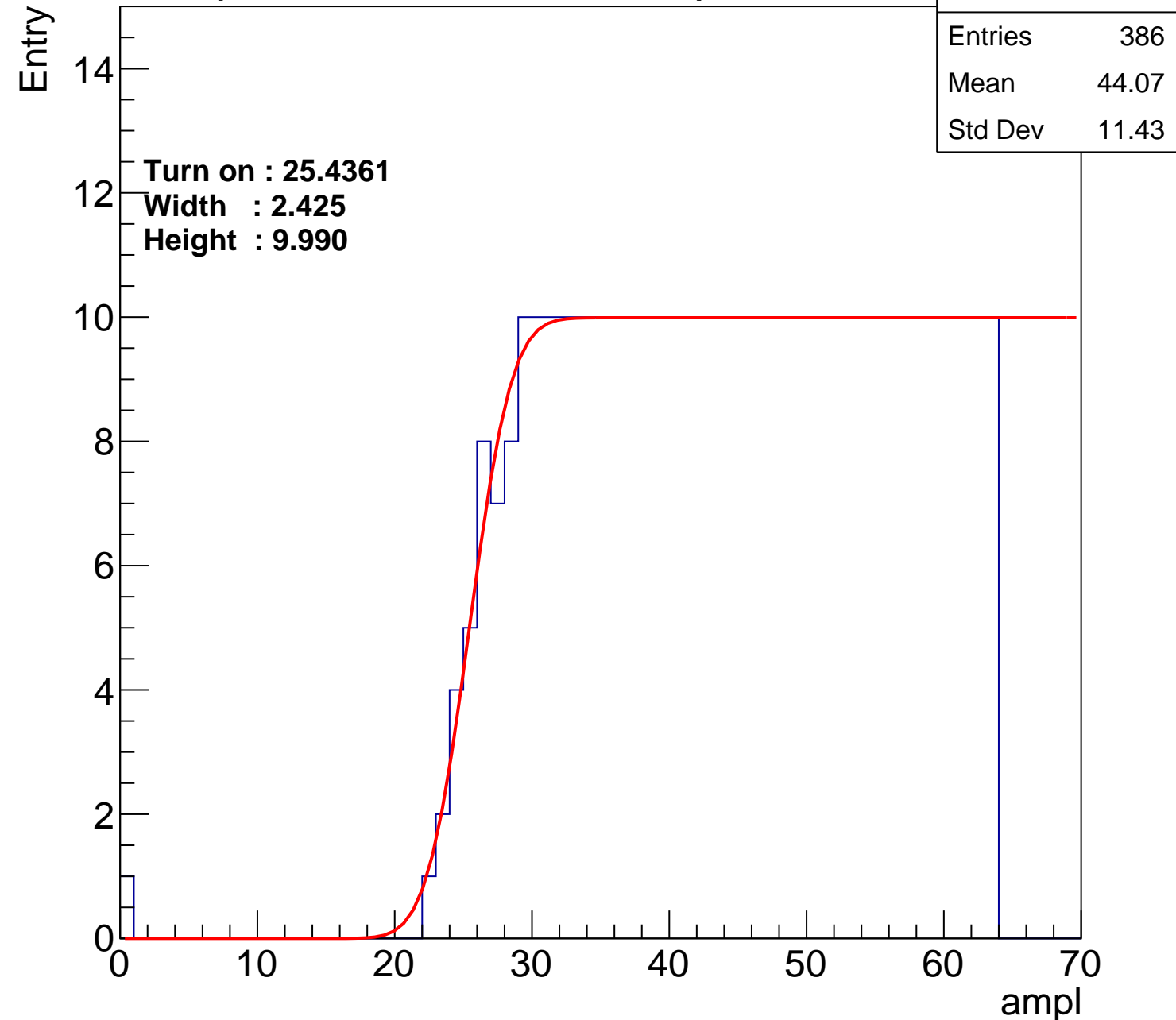
Width : 2.425

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.95
Std Dev	11.53

Turn on : 25.7182

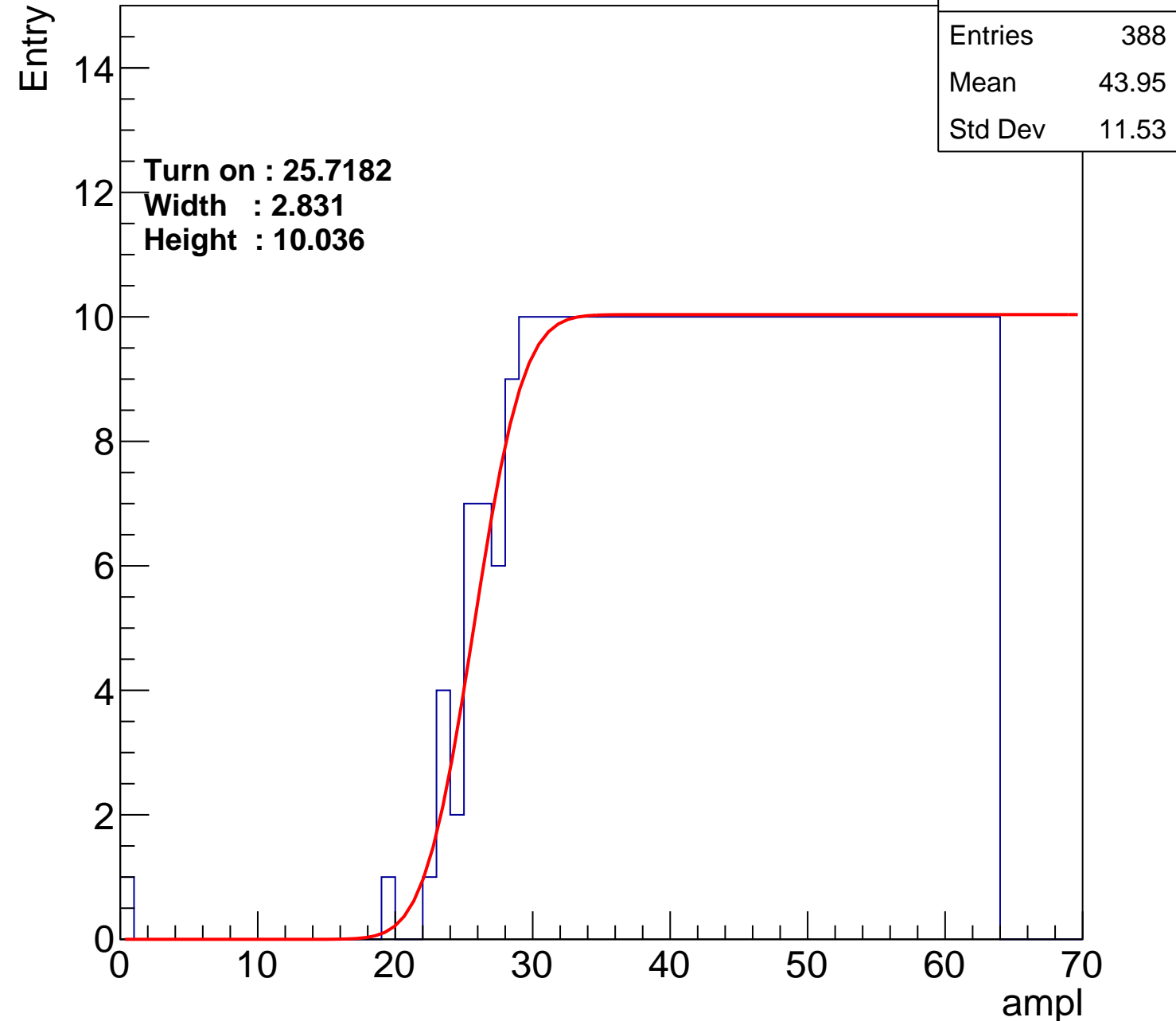
Width : 2.831

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.16
Std Dev	11.45

Turn on : 25.8644

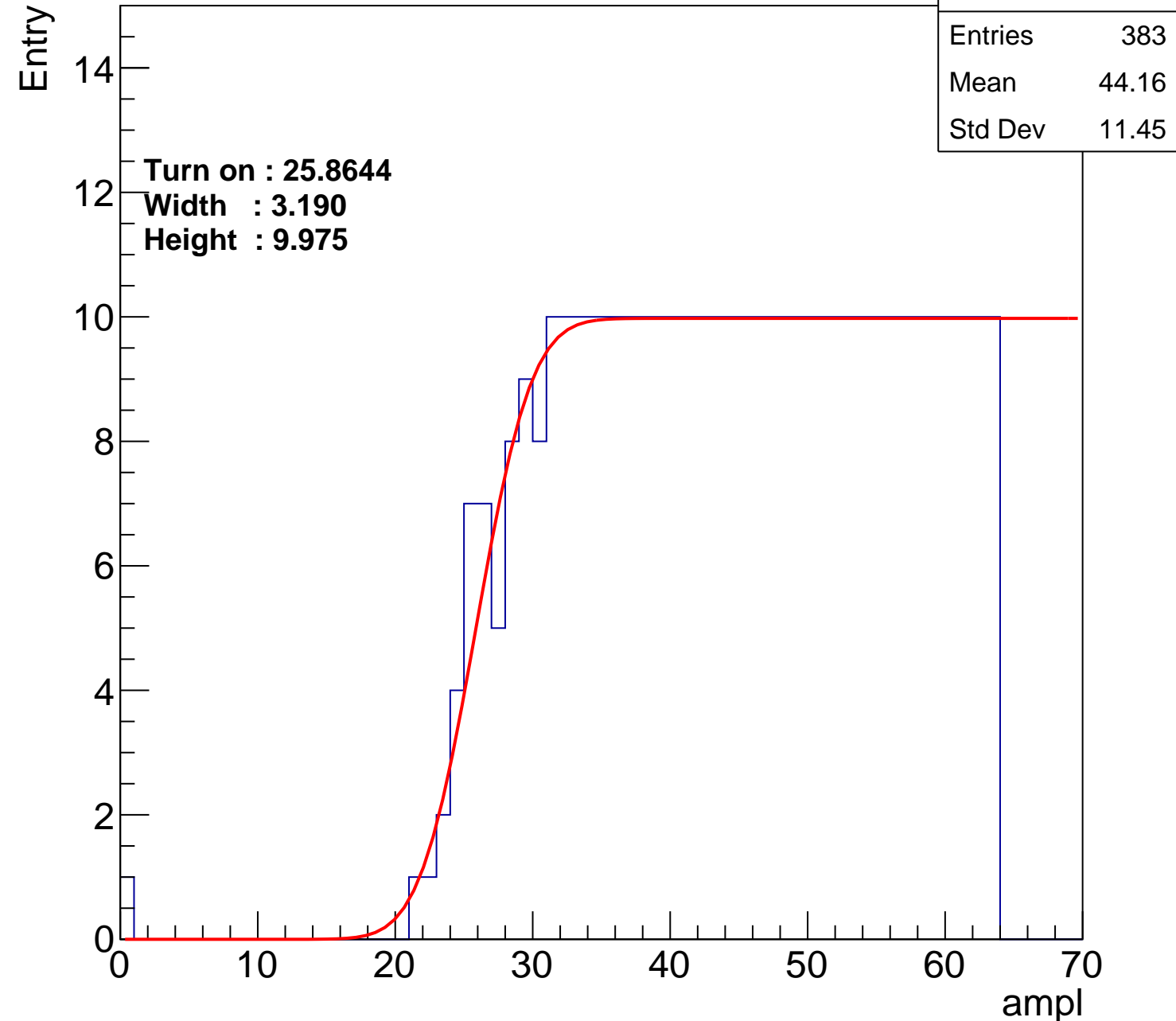
Width : 3.190

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.7
Std Dev	11.1

Turn on : 26.6812

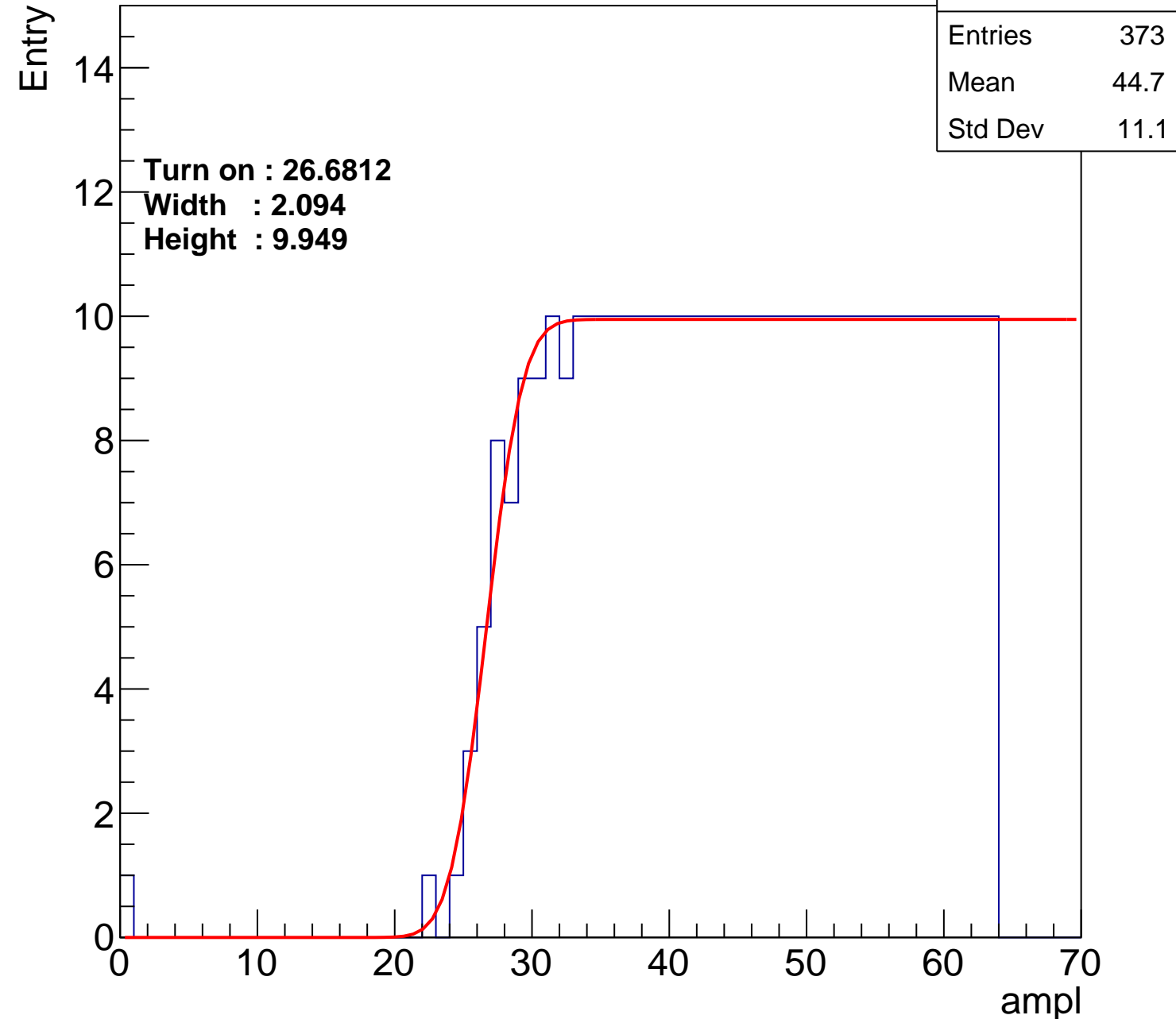
Width : 2.094

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch8

calib_packv5_042523_0143.root, FC#3, port B3

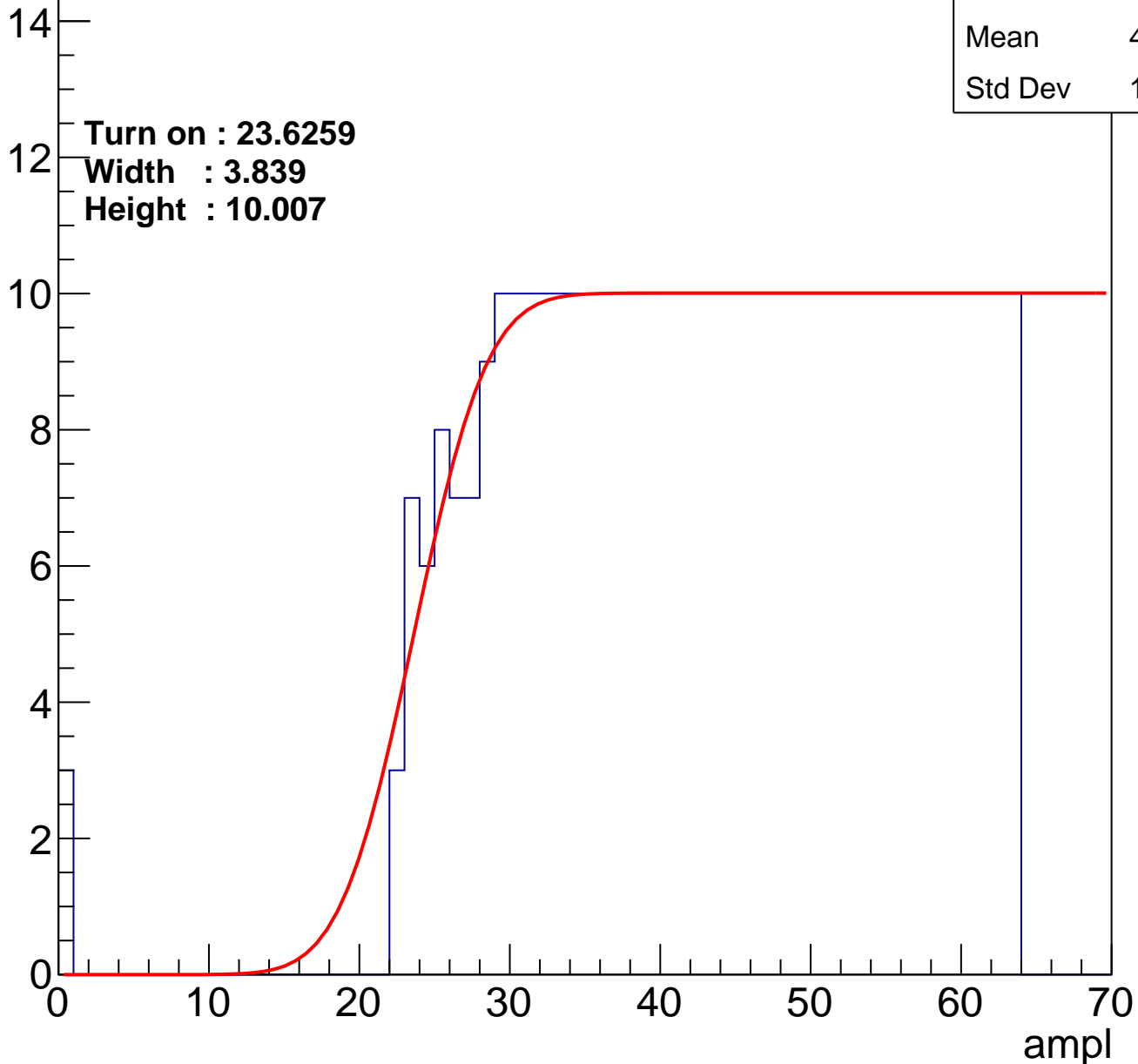
Entries	400
Mean	43.23
Std Dev	12.15

Turn on : 23.6259

Width : 3.839

Height : 10.007

Entry



B1L000S, U14-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.29
Std Dev	12.12

Turn on : 24.7963

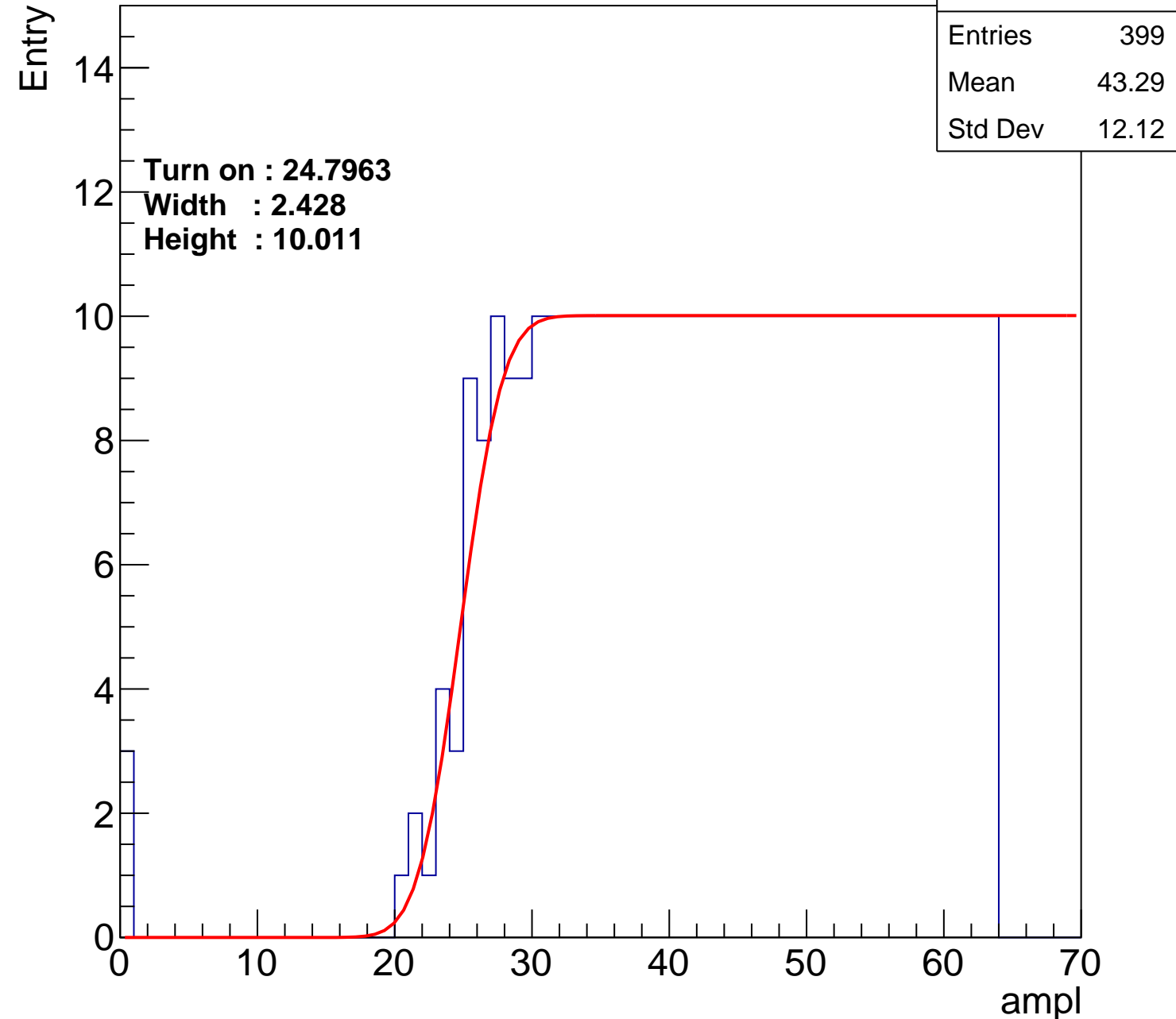
Width : 2.428

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.7465

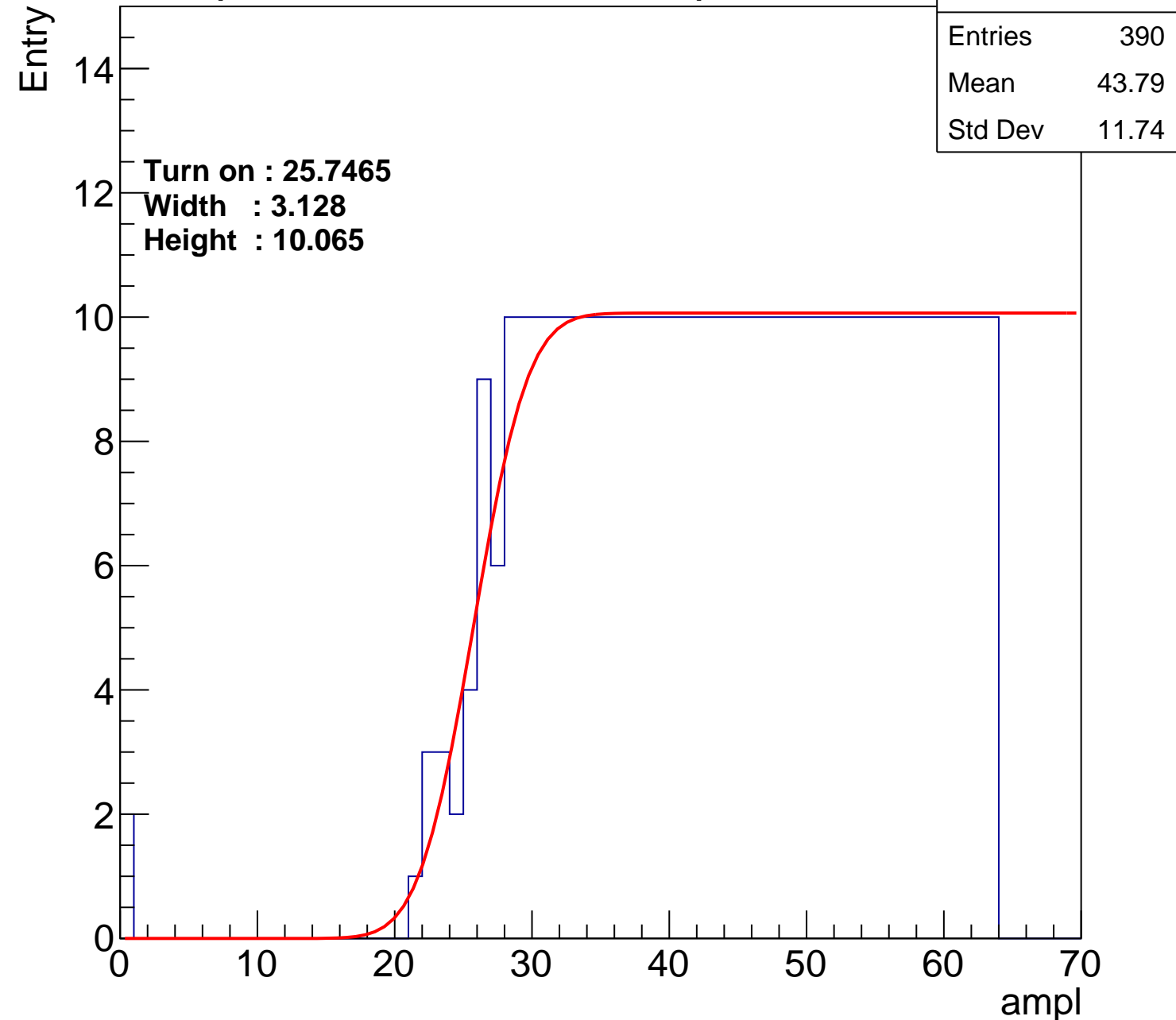
Width : 3.128

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch11

calib_packv5_042523_0143.root, FC#3, port B3

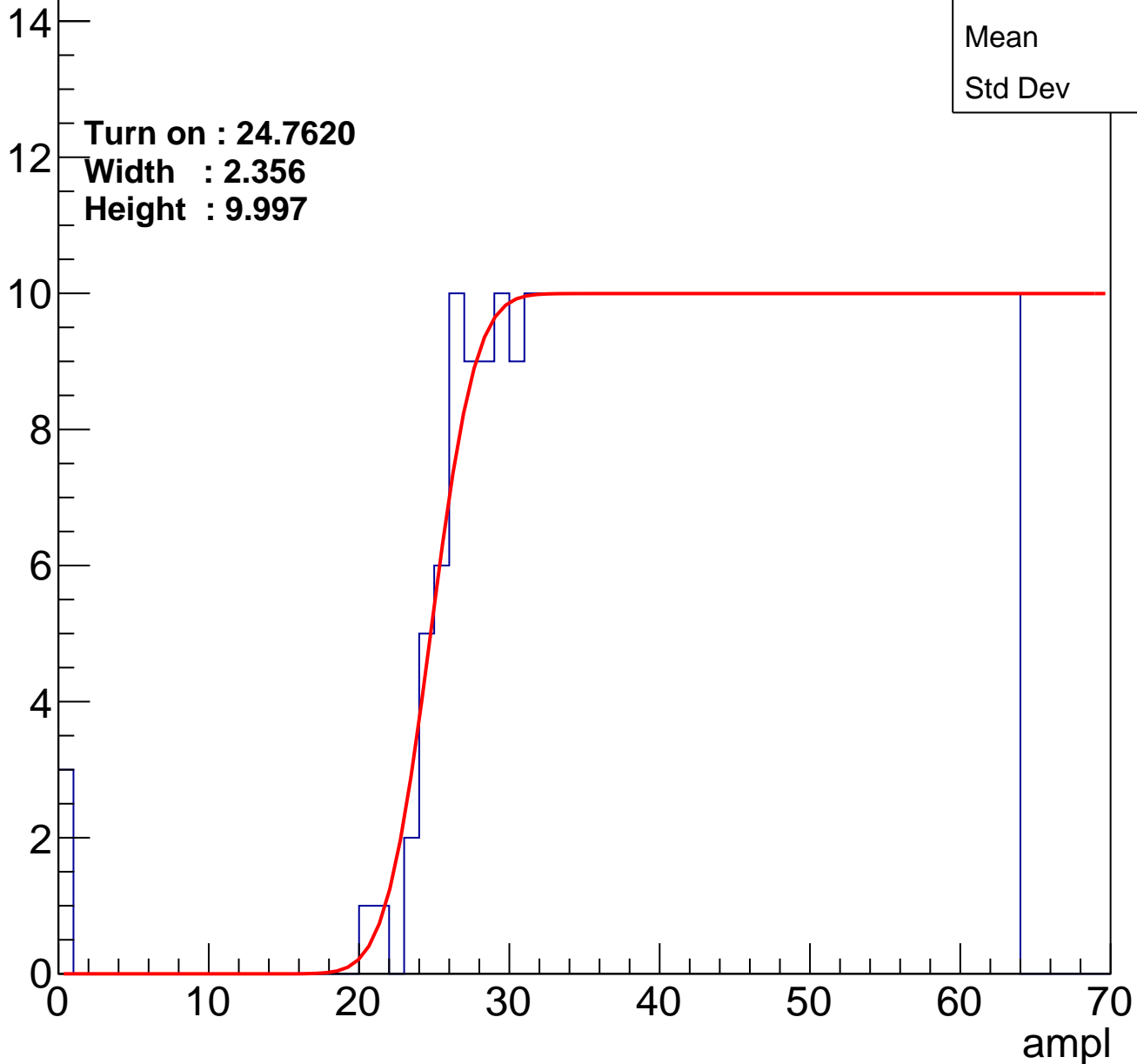
Entries	395
Mean	43.5
Std Dev	12

Turn on : 24.7620

Width : 2.356

Height : 9.997

Entry



B1L000S, U14-ch12

calib_packv5_042523_0143.root, FC#3, port B3

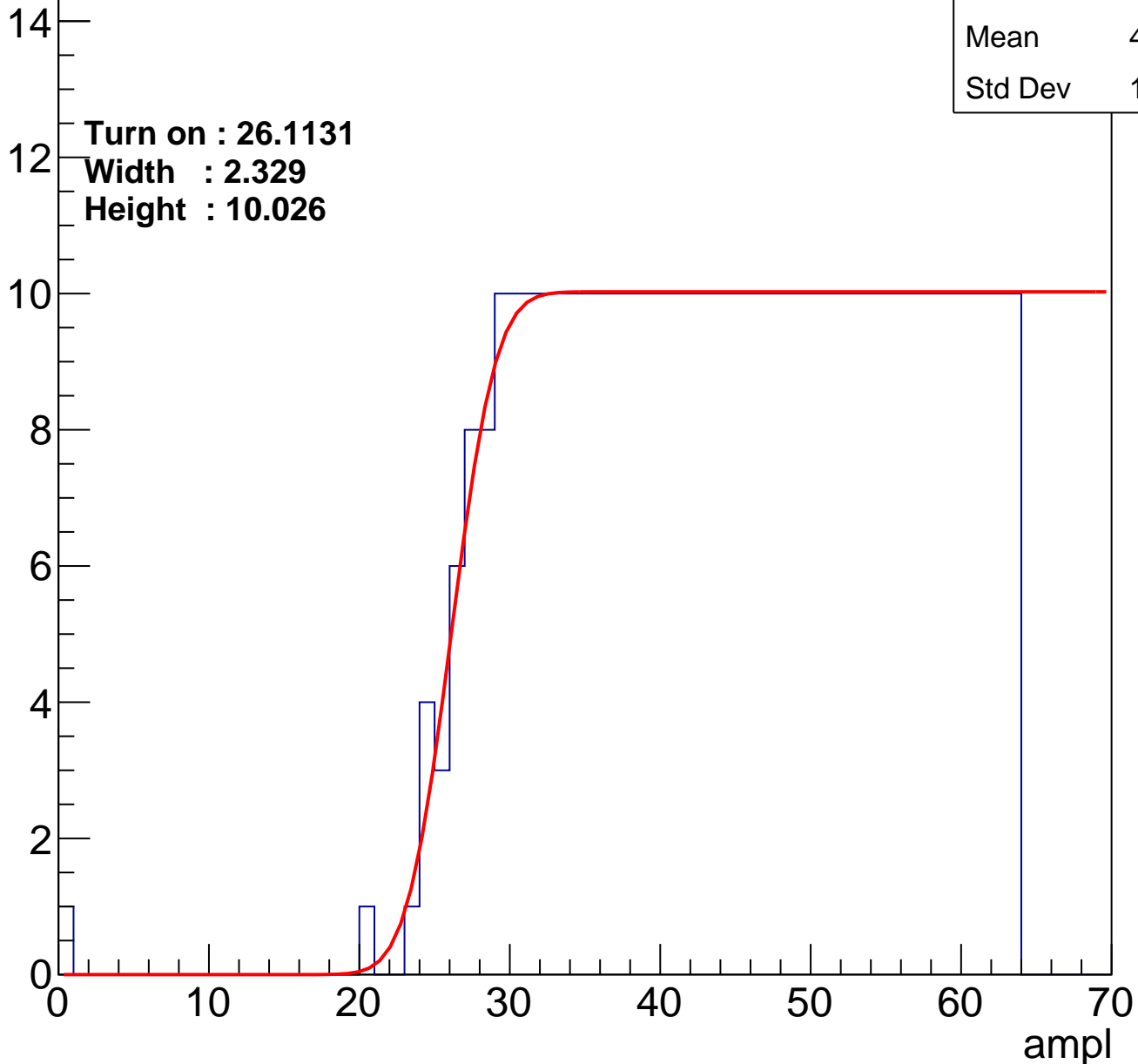
Entries	382
Mean	44.27
Std Dev	11.33

Turn on : 26.1131

Width : 2.329

Height : 10.026

Entry



B1L000S, U14-ch13

calib_packv5_042523_0143.root, FC#3, port B3

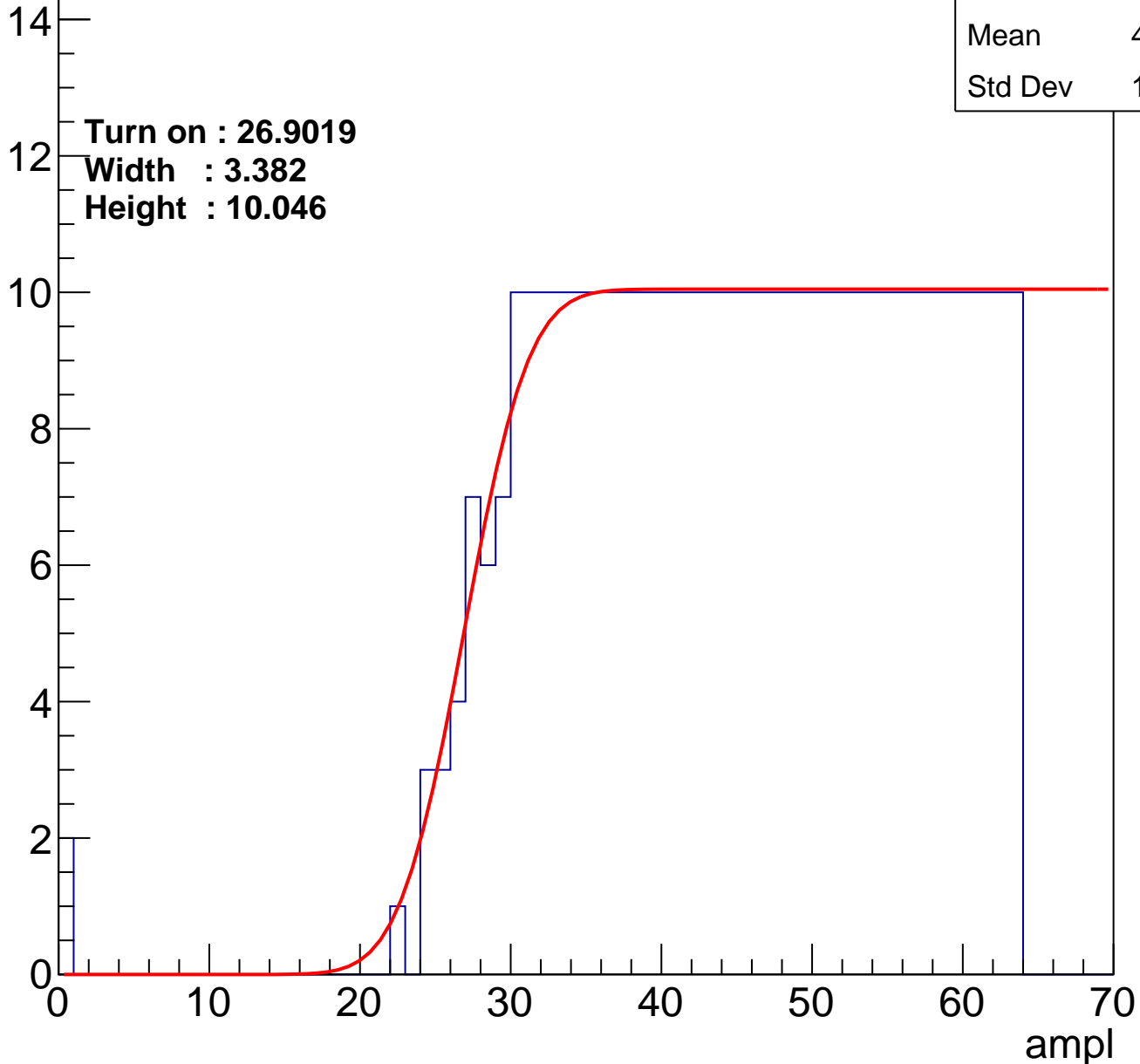
Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 26.9019

Width : 3.382

Height : 10.046

Entry



B1L000S, U14-ch14

calib_packv5_042523_0143.root, FC#3, port B3

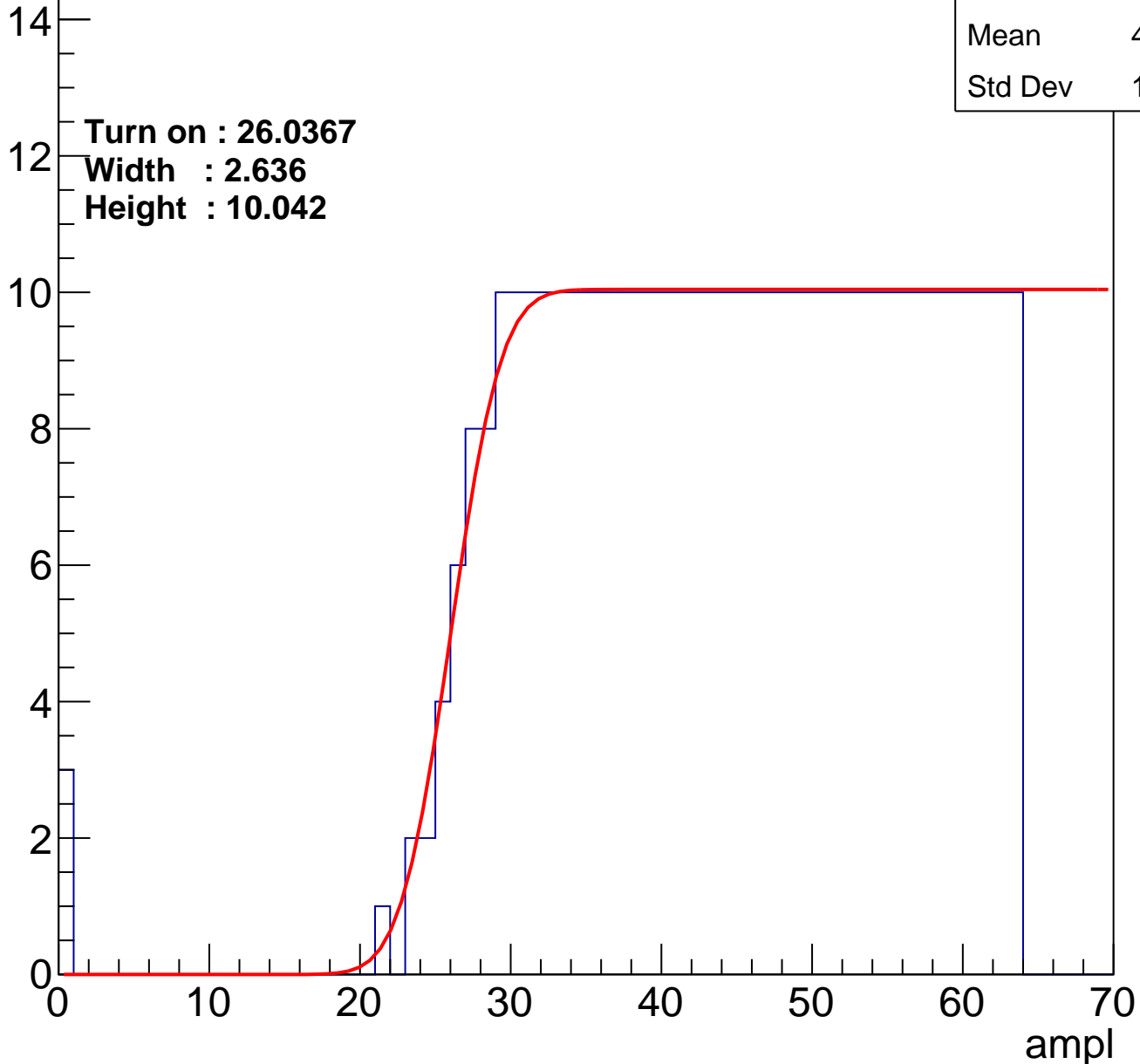
Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 26.0367

Width : 2.636

Height : 10.042

Entry



B1L000S, U14-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.03
Std Dev	11.09

Turn on : 27.4189

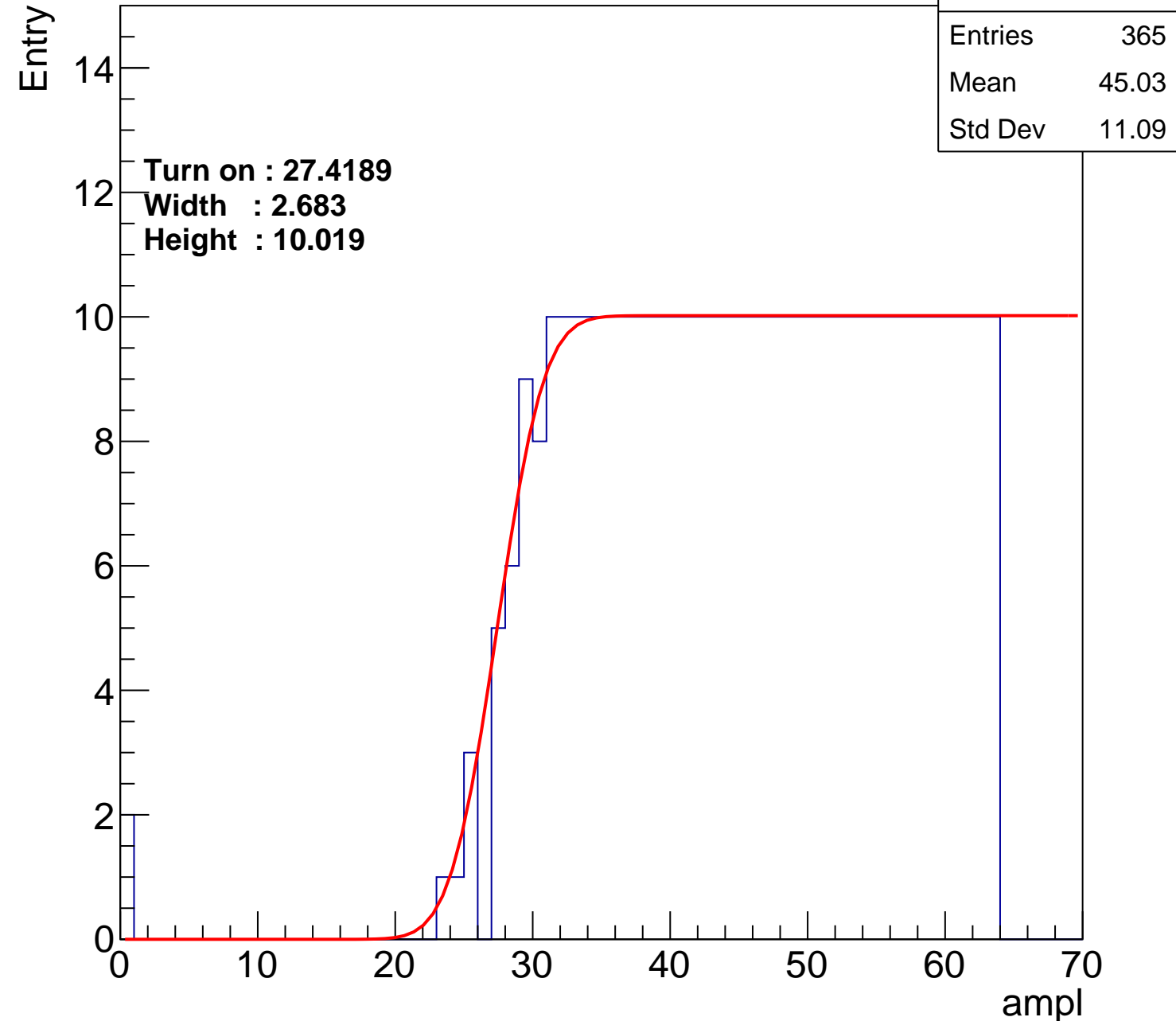
Width : 2.683

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch16

calib_packv5_042523_0143.root, FC#3, port B3

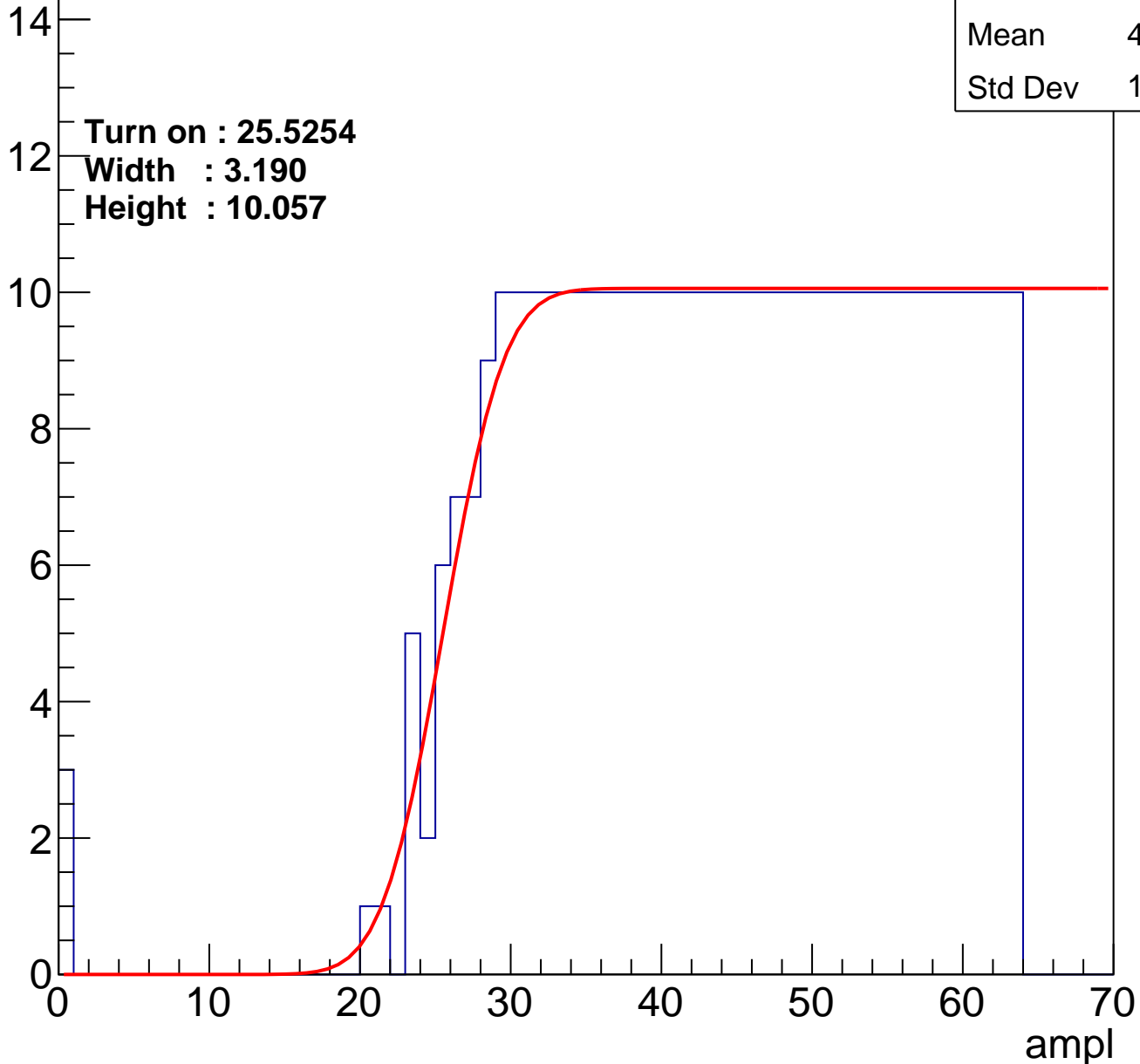
Entries	391
Mean	43.68
Std Dev	11.94

Turn on : 25.5254

Width : 3.190

Height : 10.057

Entry



B1L000S, U14-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.98
Std Dev	10.91

Turn on : 27.1715

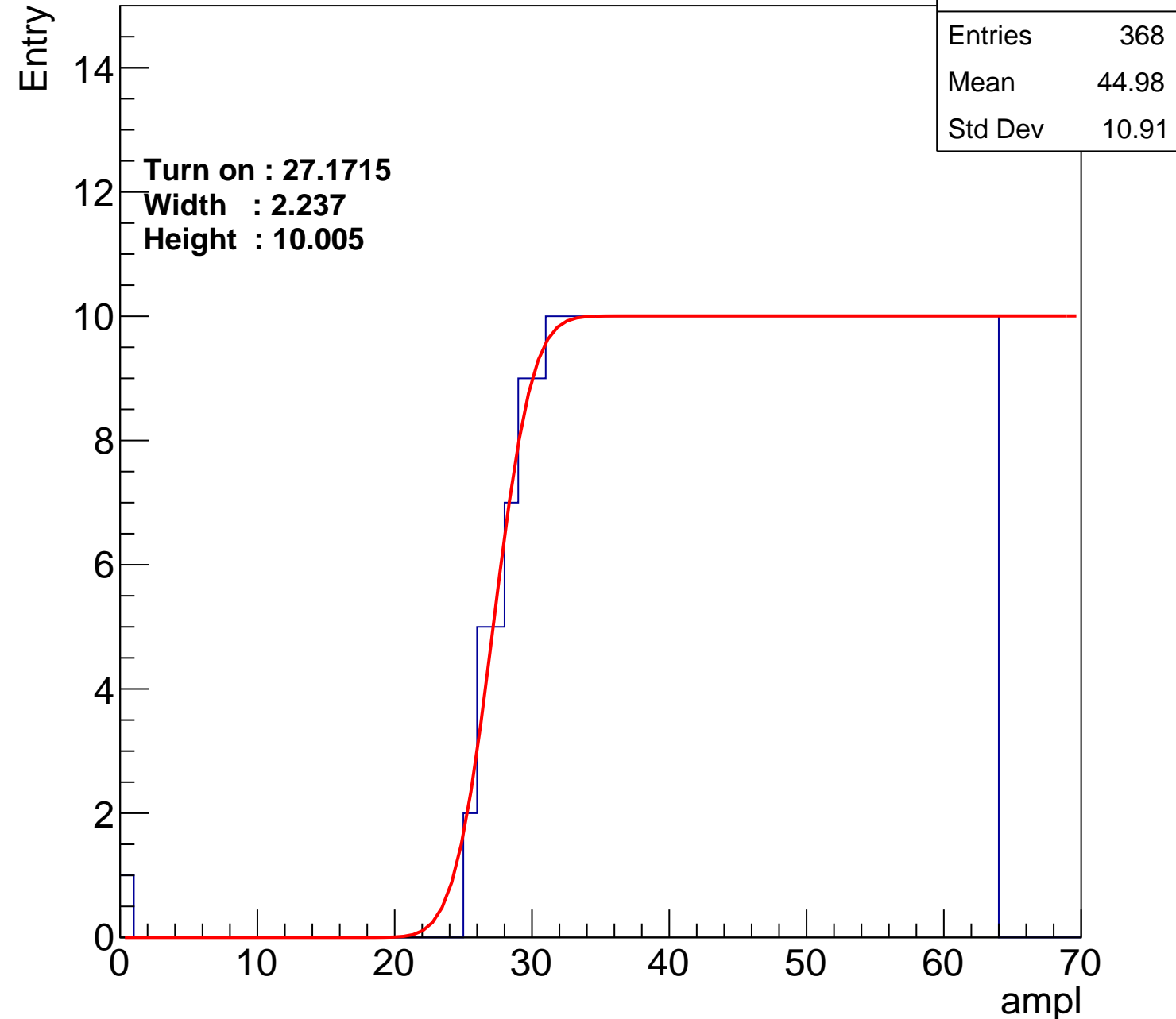
Width : 2.237

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch18

calib_packv5_042523_0143.root, FC#3, port B3

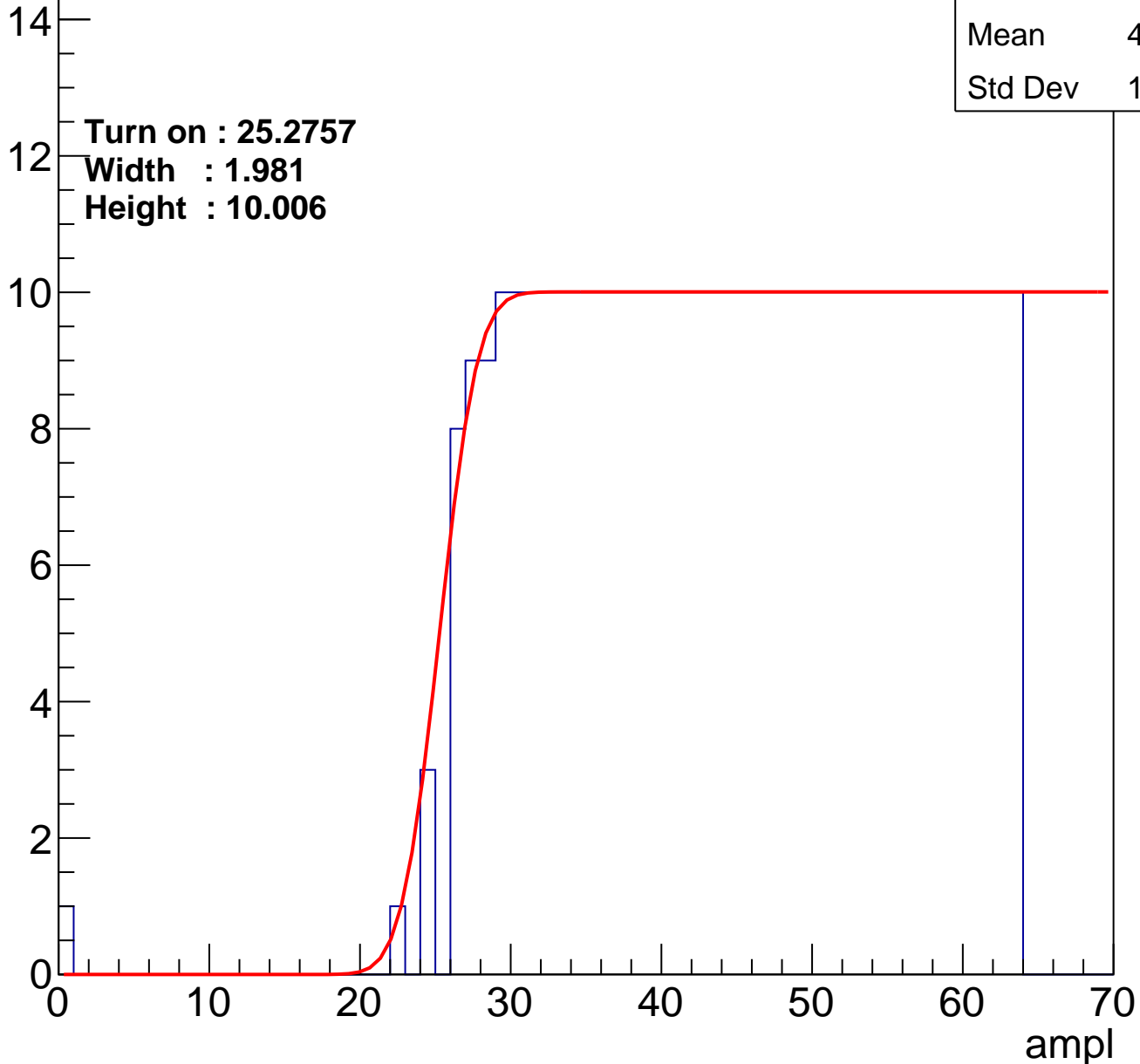
Entries	381
Mean	44.35
Std Dev	11.24

Turn on : 25.2757

Width : 1.981

Height : 10.006

Entry



B1L000S, U14-ch19

calib_packv5_042523_0143.root, FC#3, port B3

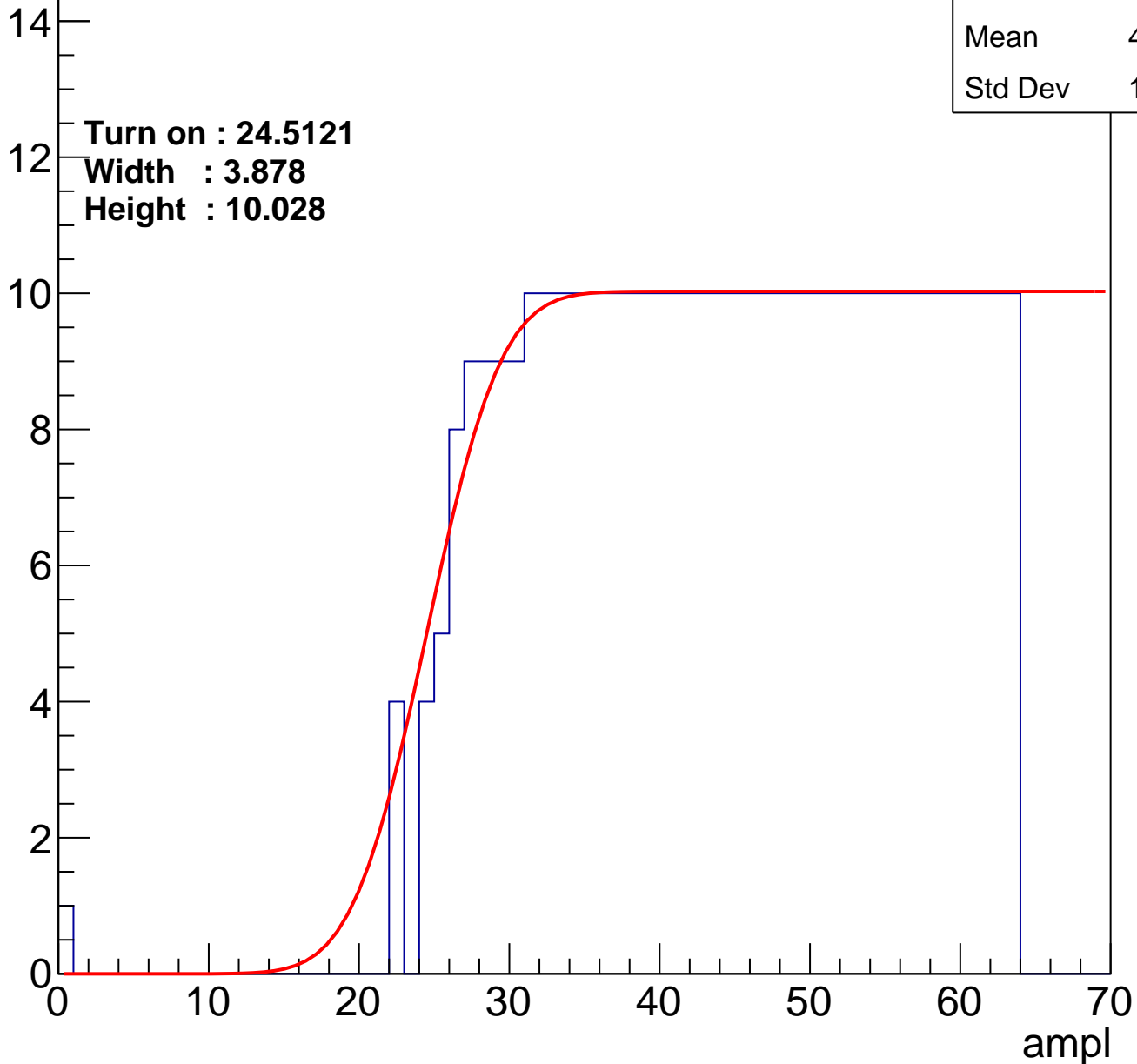
Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 24.5121

Width : 3.878

Height : 10.028

Entry



B1L000S, U14-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	410
Mean	42.89
Std Dev	12.07

Turn on : 23.2936

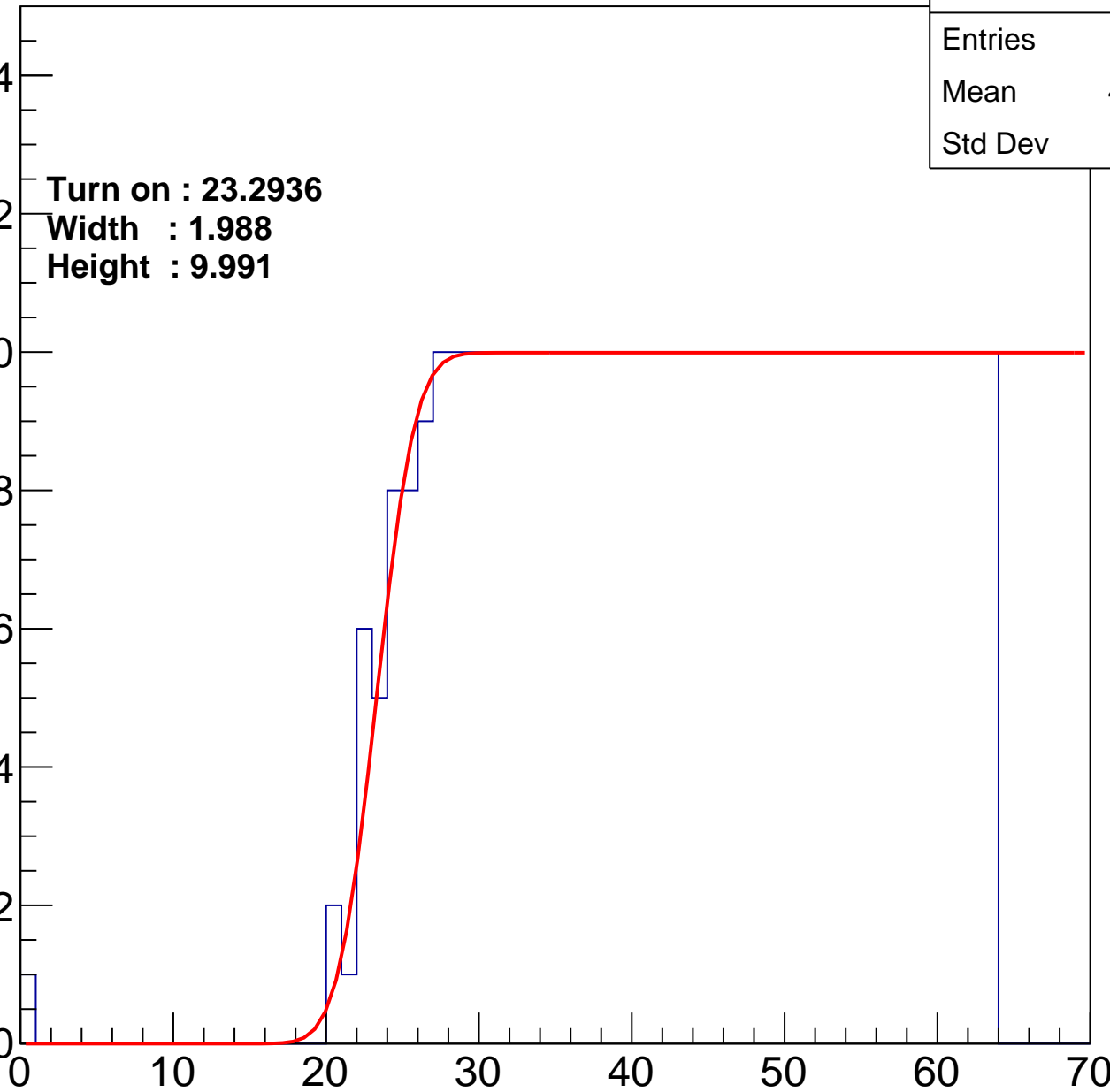
Width : 1.988

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch21

calib_packv5_042523_0143.root, FC#3, port B3

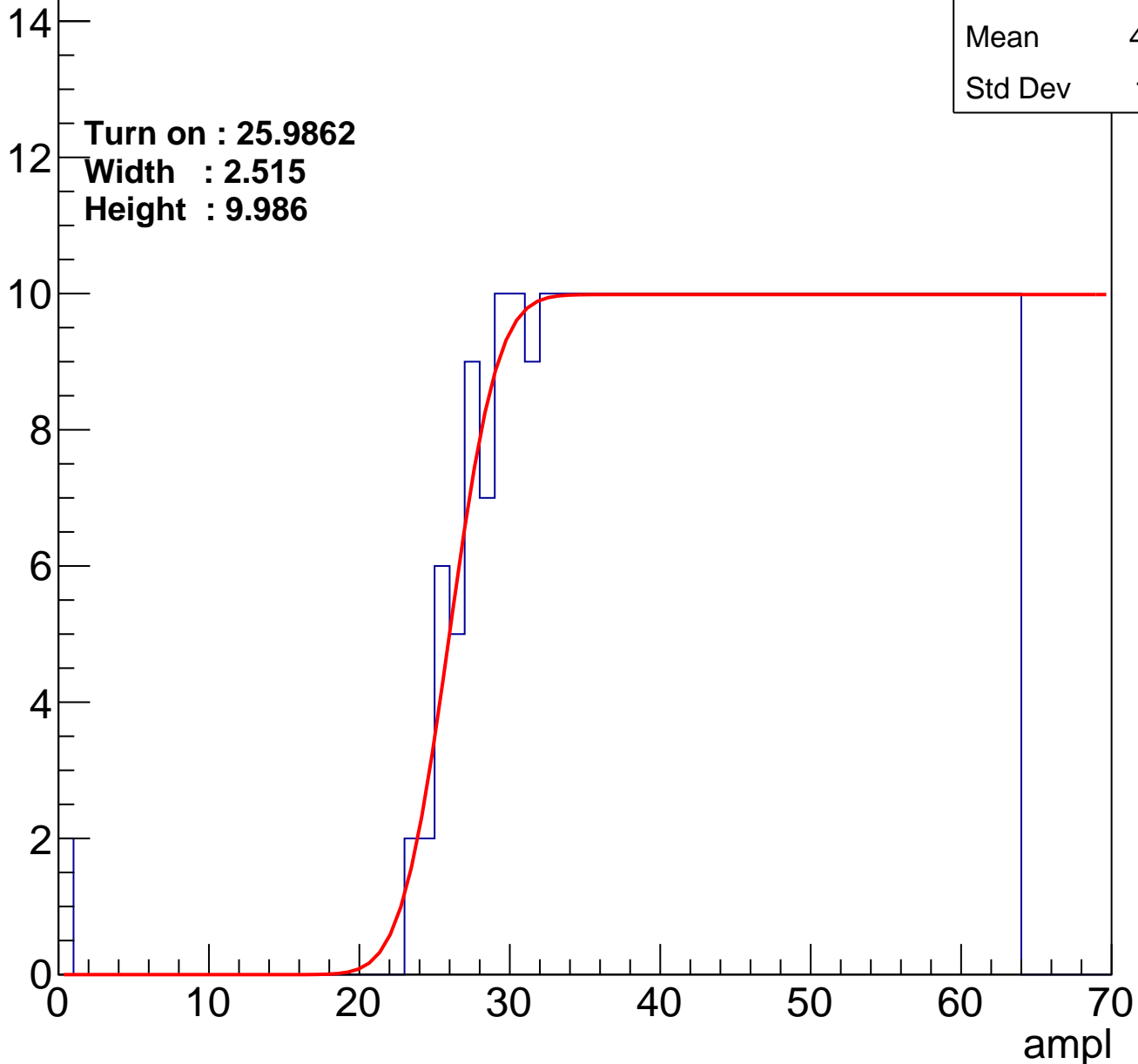
Entries	382
Mean	44.19
Std Dev	11.51

Turn on : 25.9862

Width : 2.515

Height : 9.986

Entry



B1L000S, U14-ch22

calib_packv5_042523_0143.root, FC#3, port B3

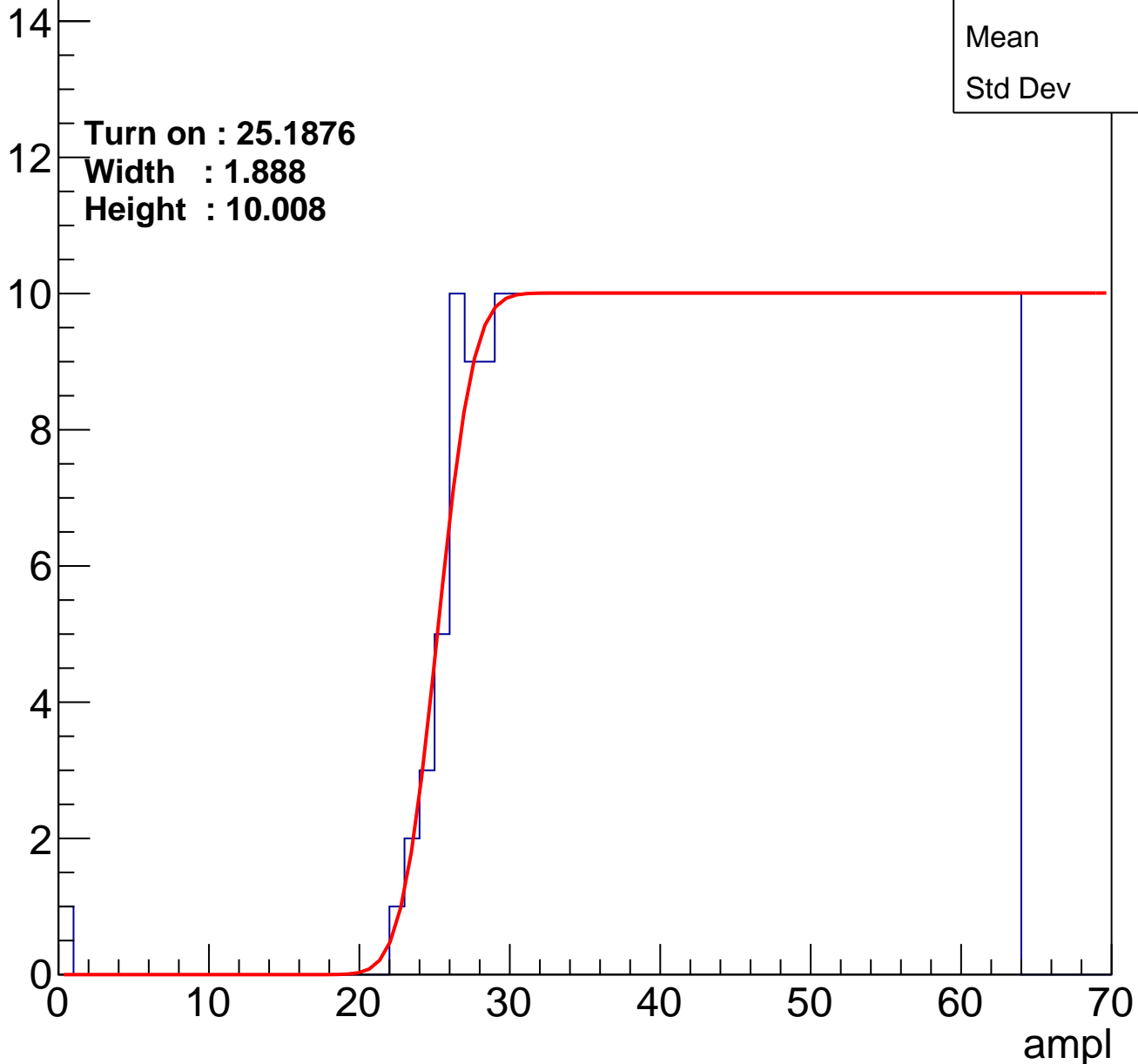
Entries	390
Mean	43.9
Std Dev	11.5

Turn on : 25.1876

Width : 1.888

Height : 10.008

Entry



B1L000S, U14-ch23

calib_packv5_042523_0143.root, FC#3, port B3

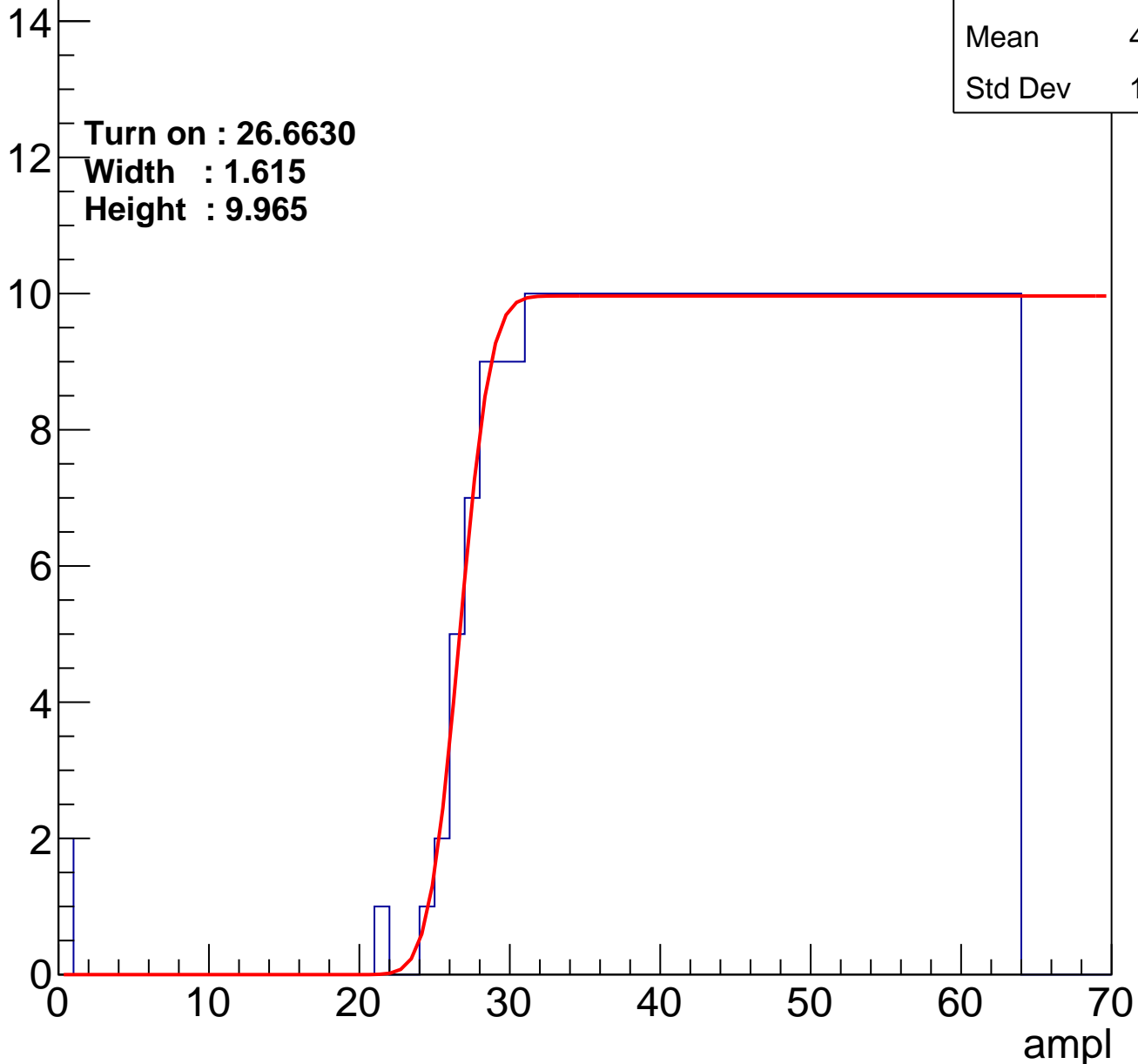
Entries	375
Mean	44.55
Std Dev	11.32

Turn on : 26.6630

Width : 1.615

Height : 9.965

Entry



B1L000S, U14-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.69
Std Dev	12.08

Turn on : 25.8661

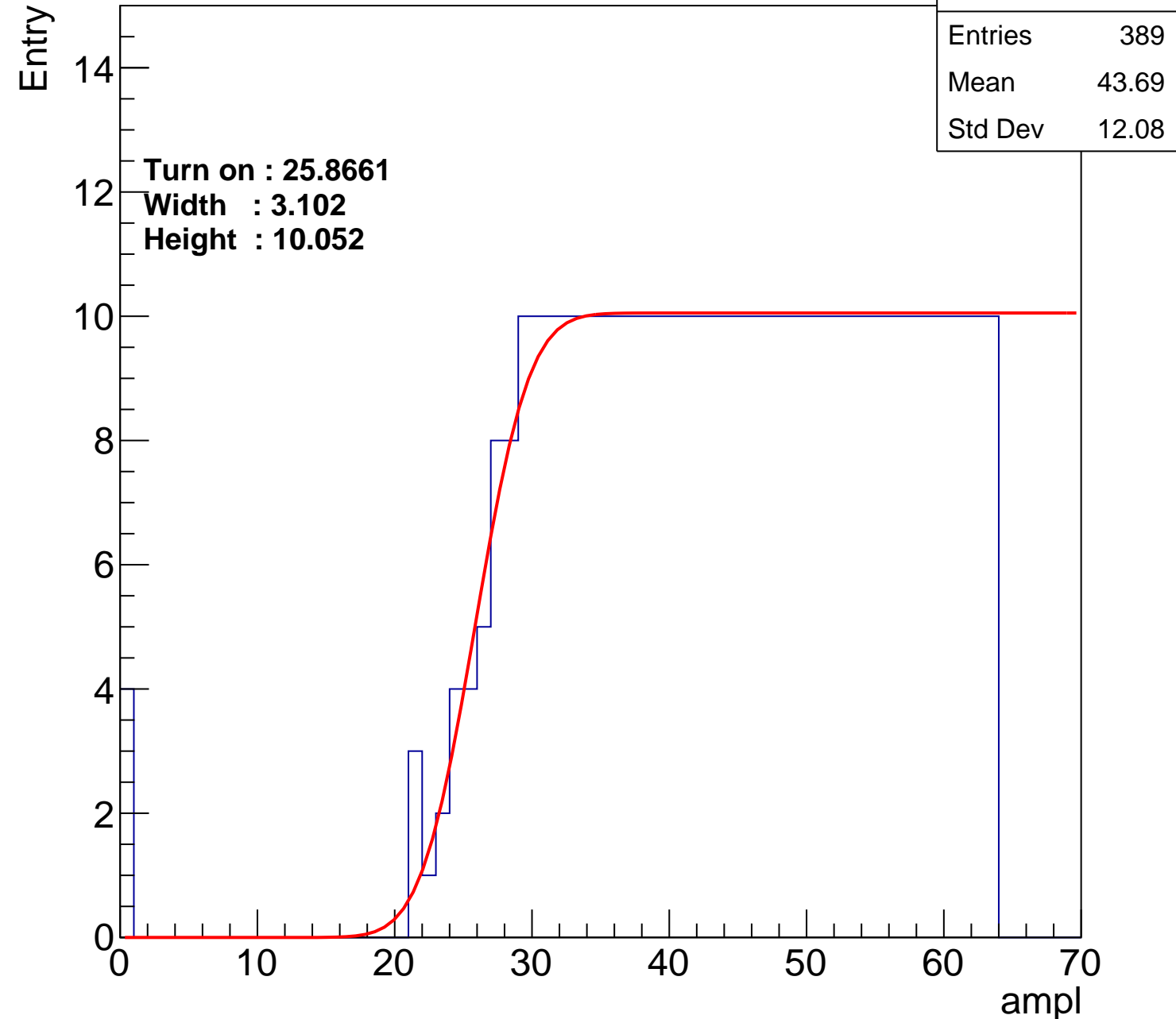
Width : 3.102

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch25

calib_packv5_042523_0143.root, FC#3, port B3

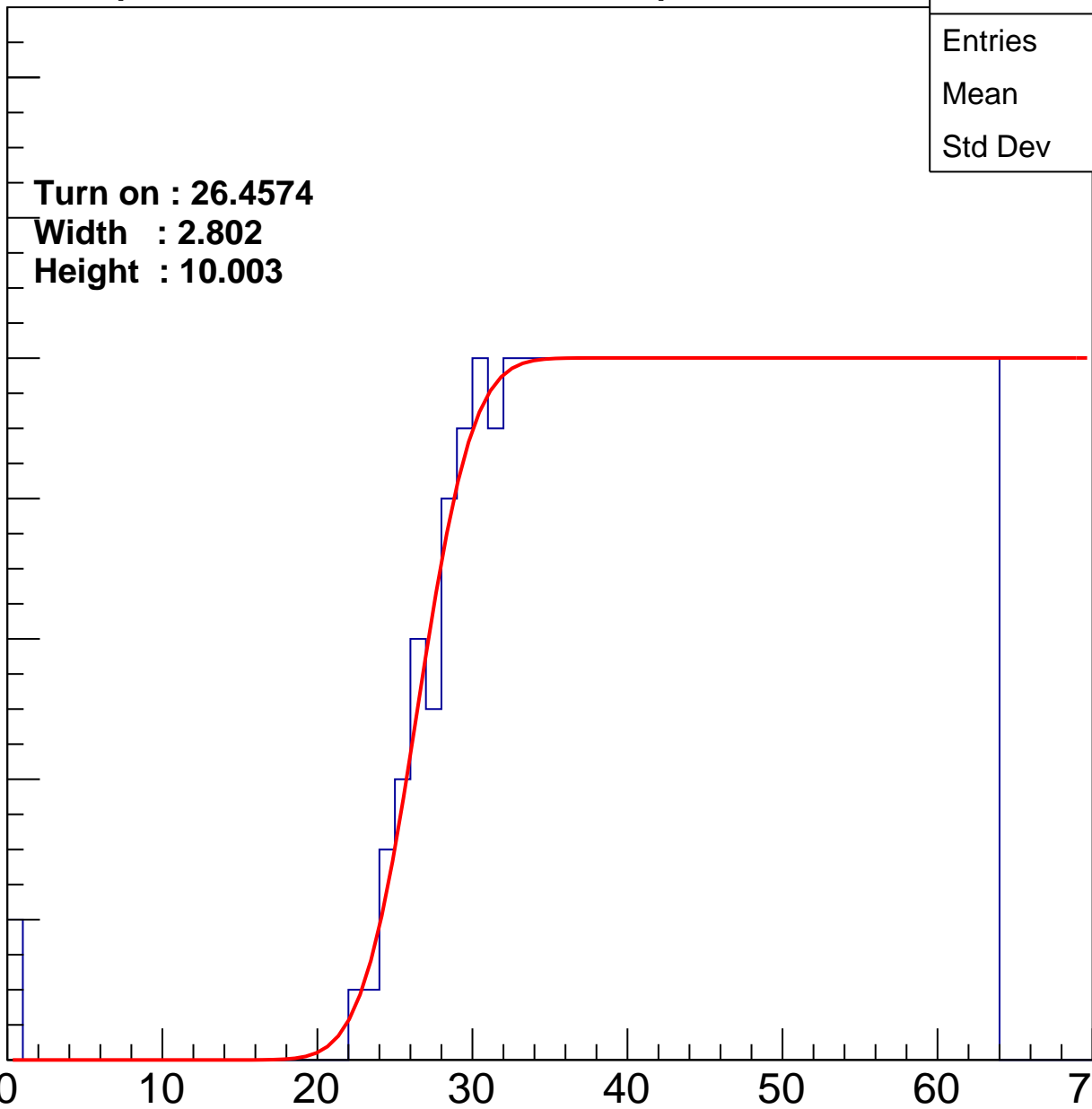
Entry

14
12
10
8
6
4
2
0

Turn on : 26.4574
Width : 2.802
Height : 10.003

Entries	378
Mean	44.37
Std Dev	11.45

ampl



B1L000S, U14-ch26

calib_packv5_042523_0143.root, FC#3, port B3

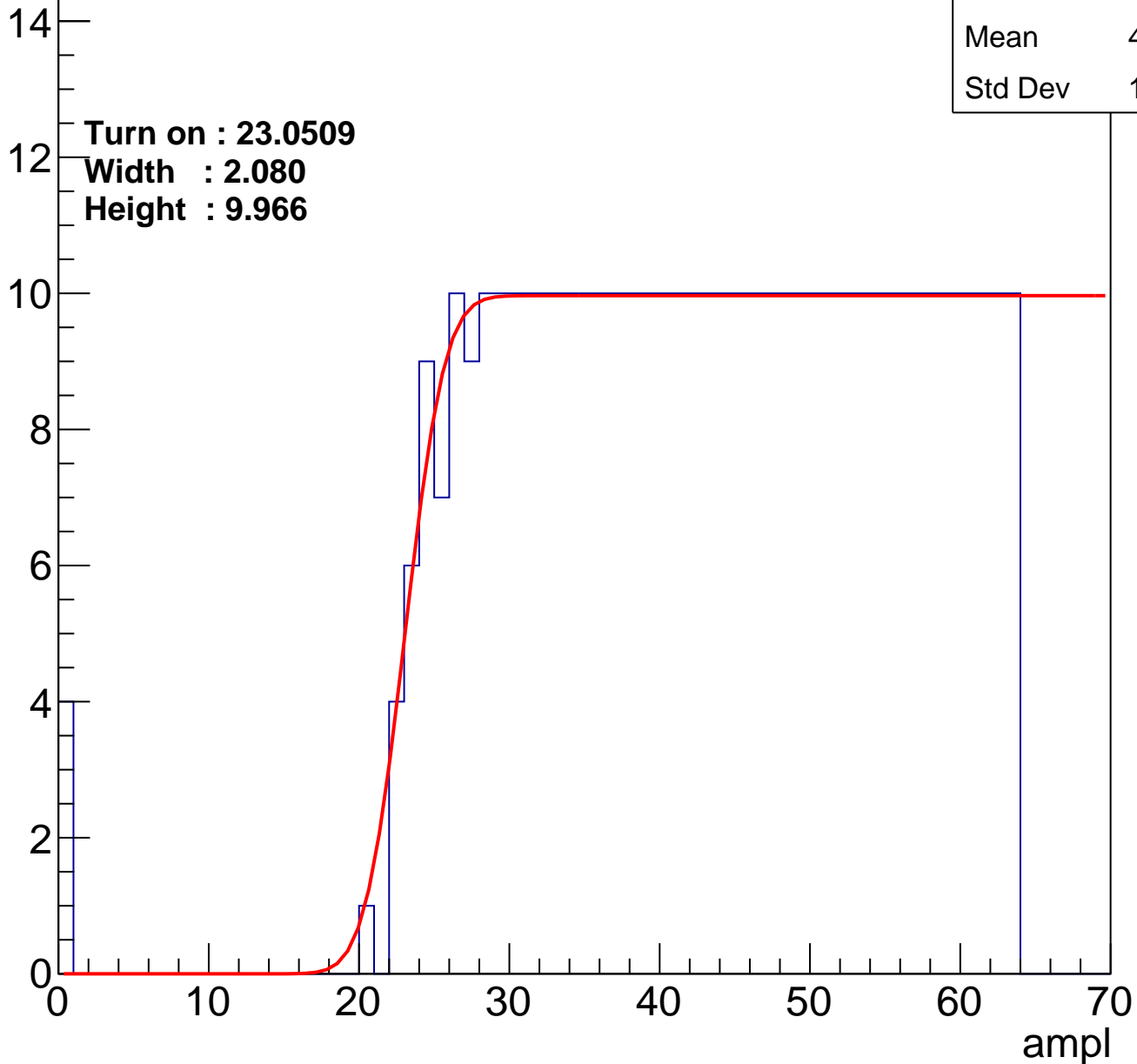
Entries	410
Mean	42.73
Std Dev	12.48

Turn on : 23.0509

Width : 2.080

Height : 9.966

Entry



B1L000S, U14-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	416
Mean	42.38
Std Dev	12.77

Turn on : 23.1029

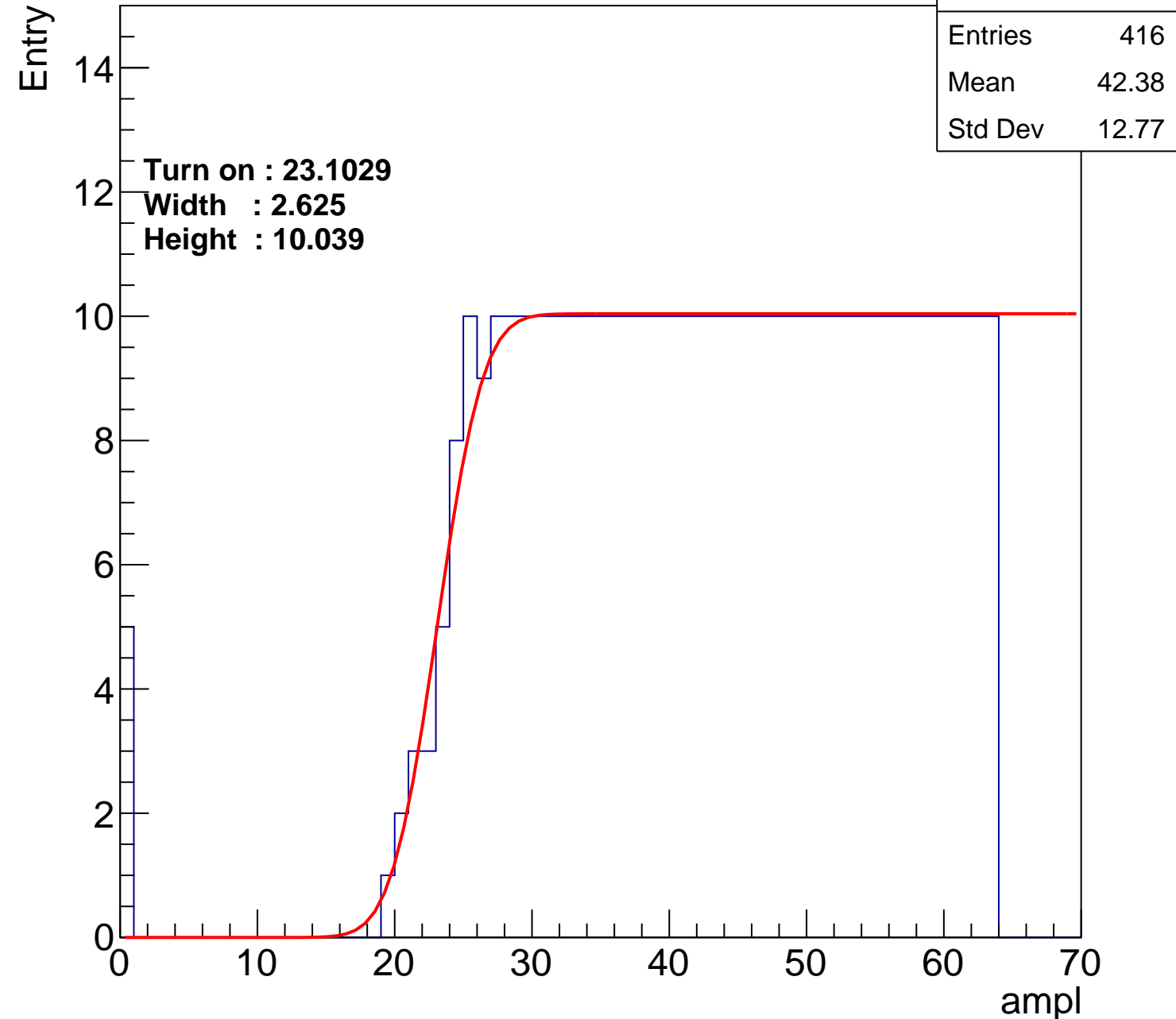
Width : 2.625

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch28

calib_packv5_042523_0143.root, FC#3, port B3

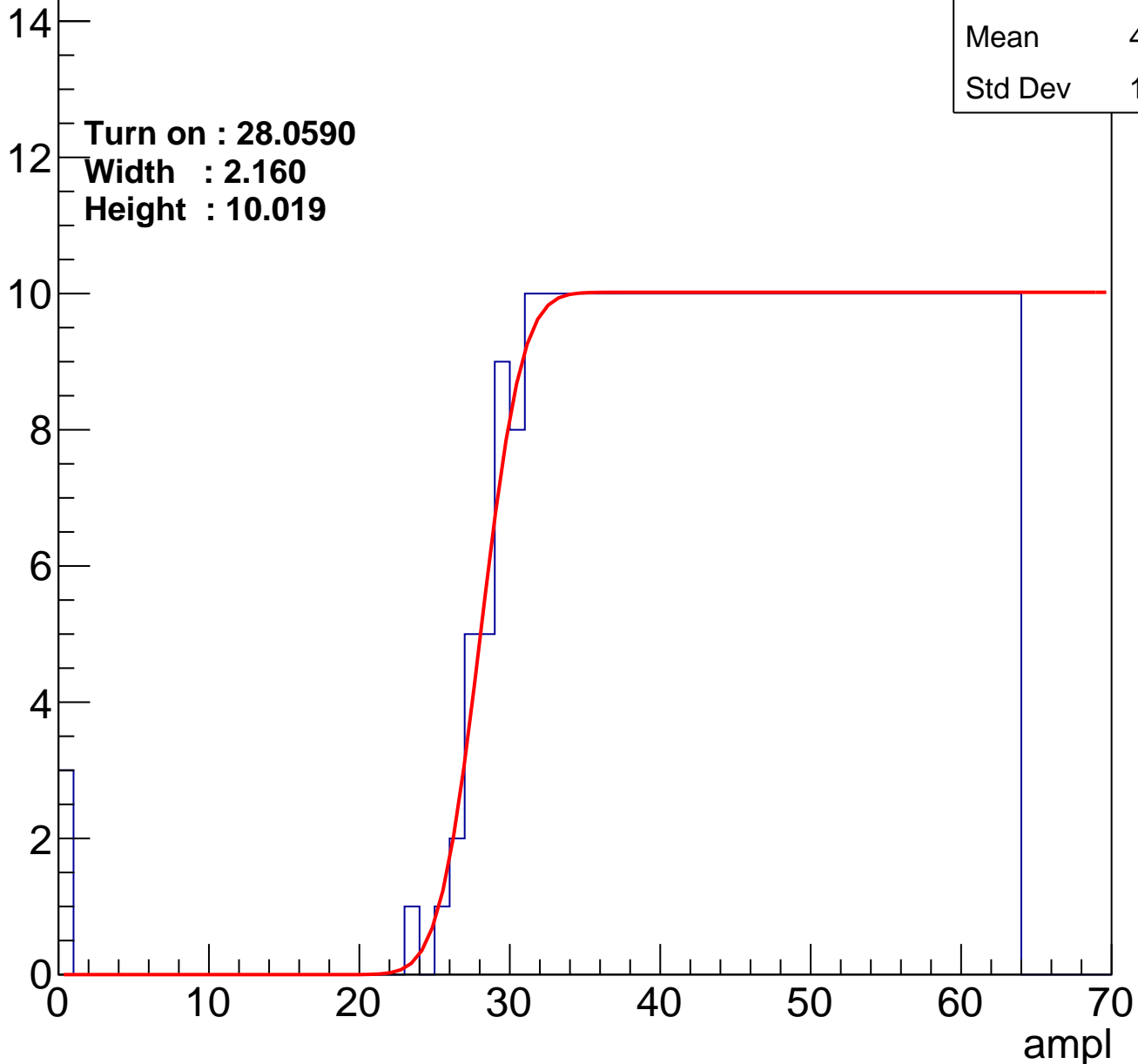
Entries	364
Mean	45.02
Std Dev	11.25

Turn on : 28.0590

Width : 2.160

Height : 10.019

Entry



B1L000S, U14-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.69
Std Dev	11.31

Turn on : 27.5866

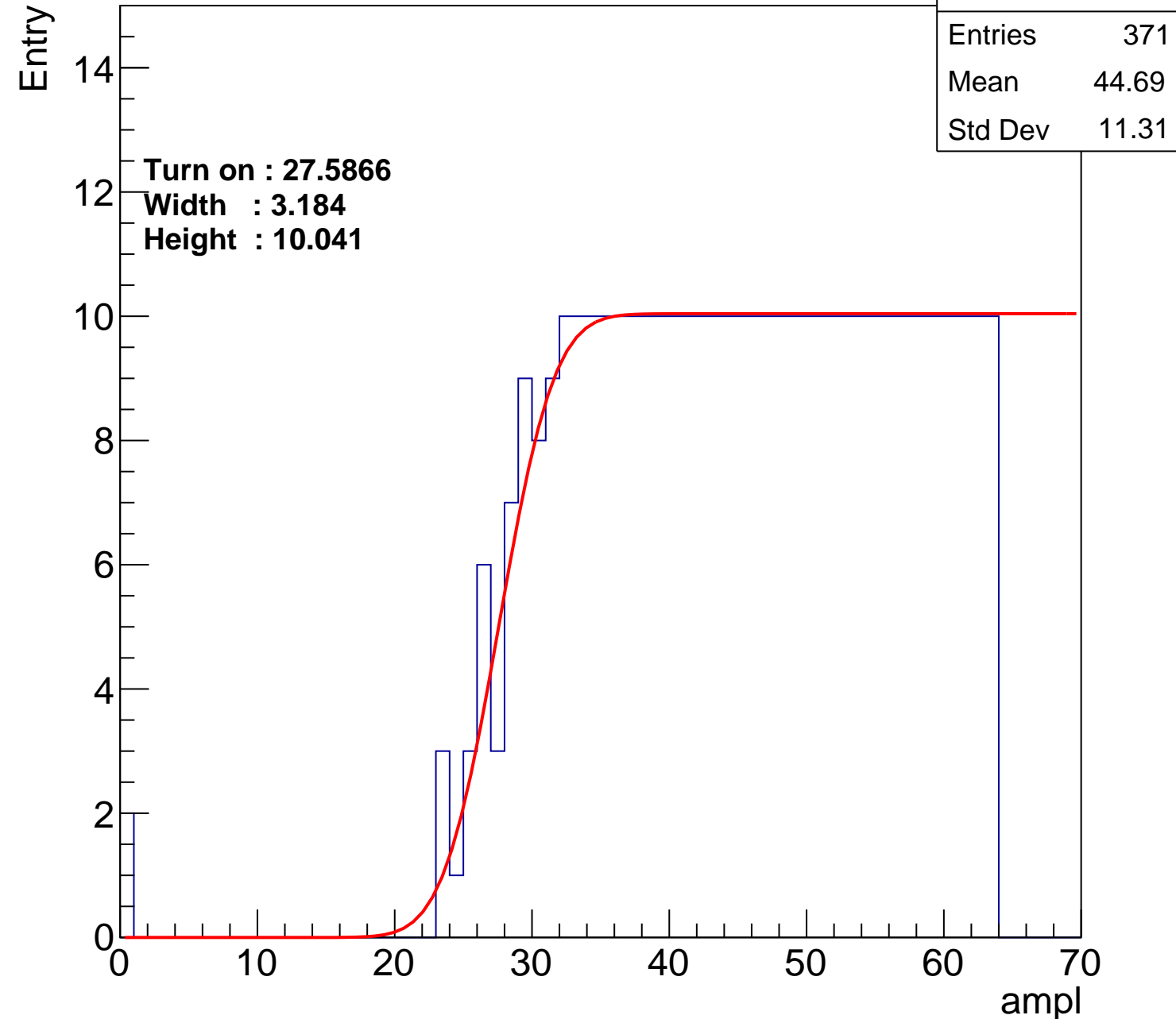
Width : 3.184

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.46
Std Dev	11.91

Turn on : 25.1634

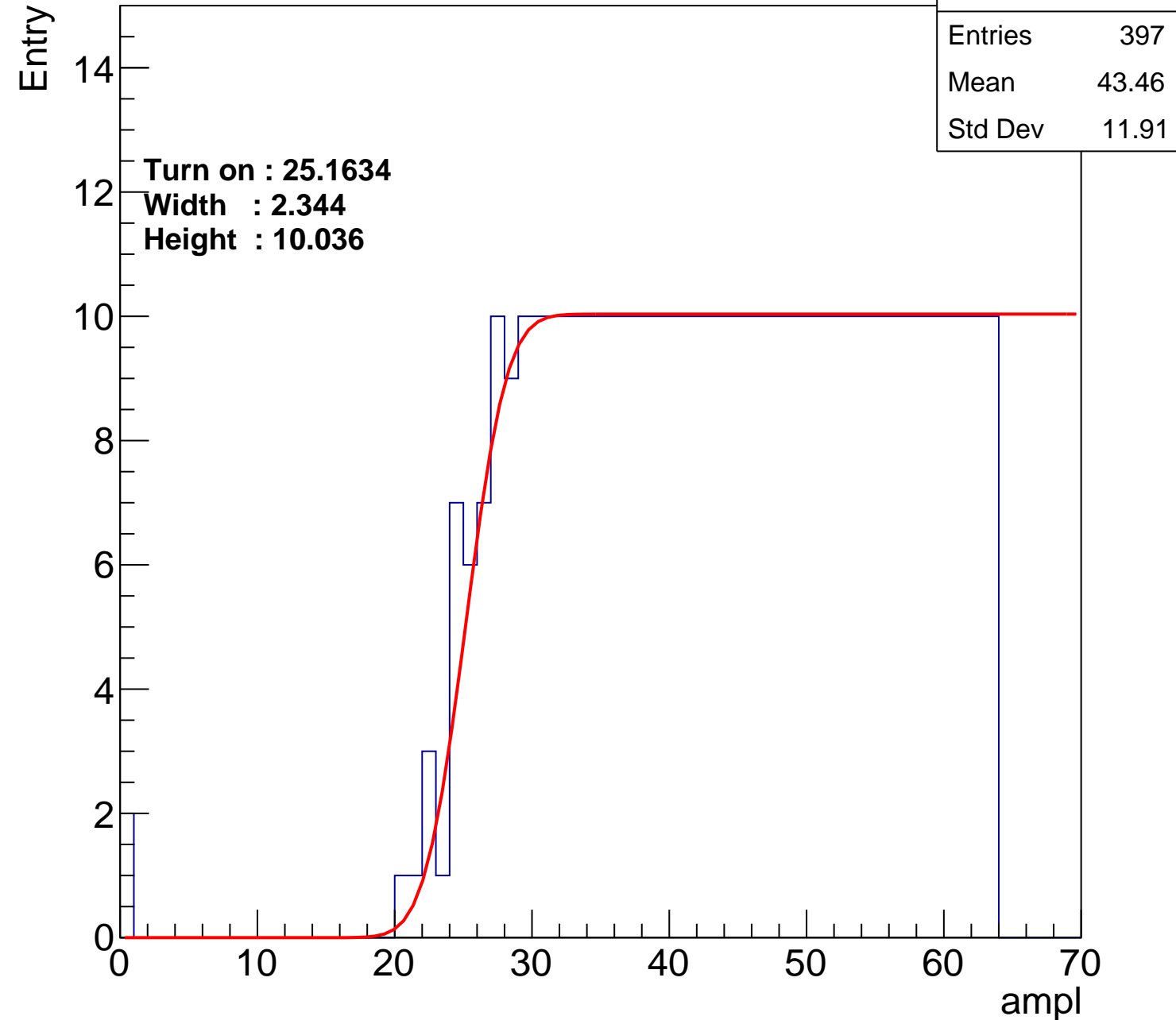
Width : 2.344

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch31

calib_packv5_042523_0143.root, FC#3, port B3

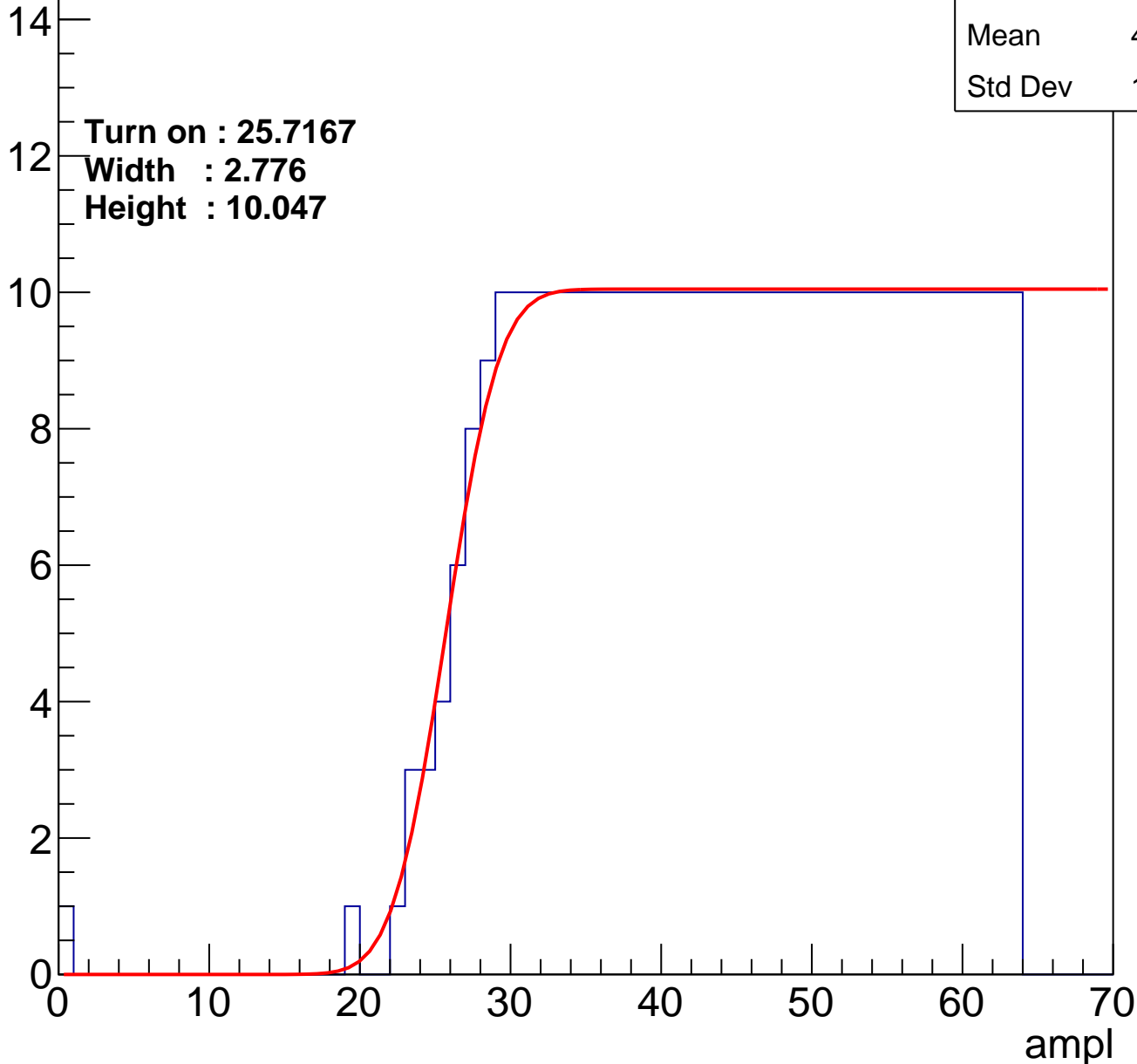
Entries	386
Mean	44.06
Std Dev	11.46

Turn on : 25.7167

Width : 2.776

Height : 10.047

Entry



B1L000S, U14-ch32

calib_packv5_042523_0143.root, FC#3, port B3

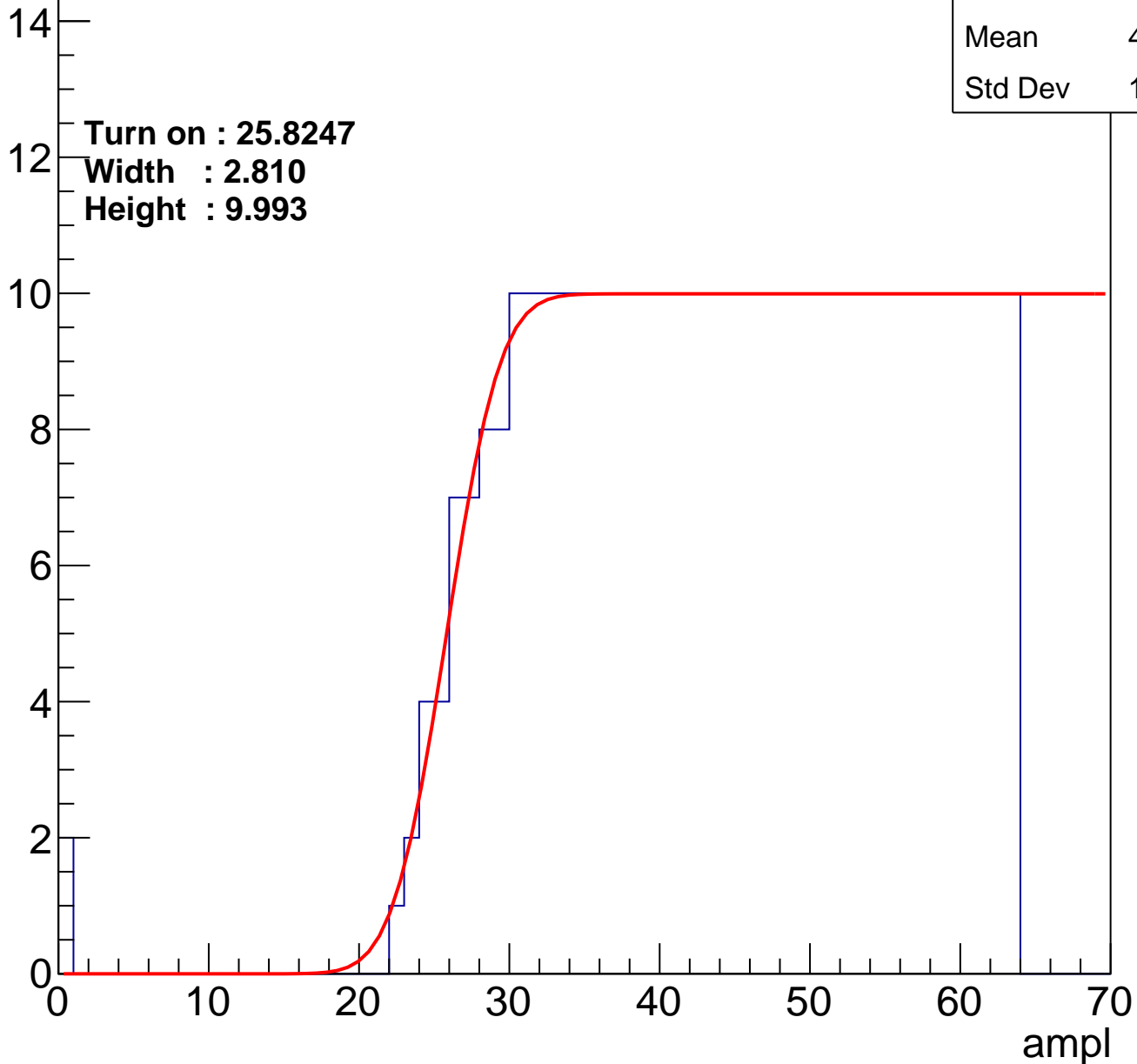
Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.8247

Width : 2.810

Height : 9.993

Entry



B1L000S, U14-ch33

calib_packv5_042523_0143.root, FC#3, port B3

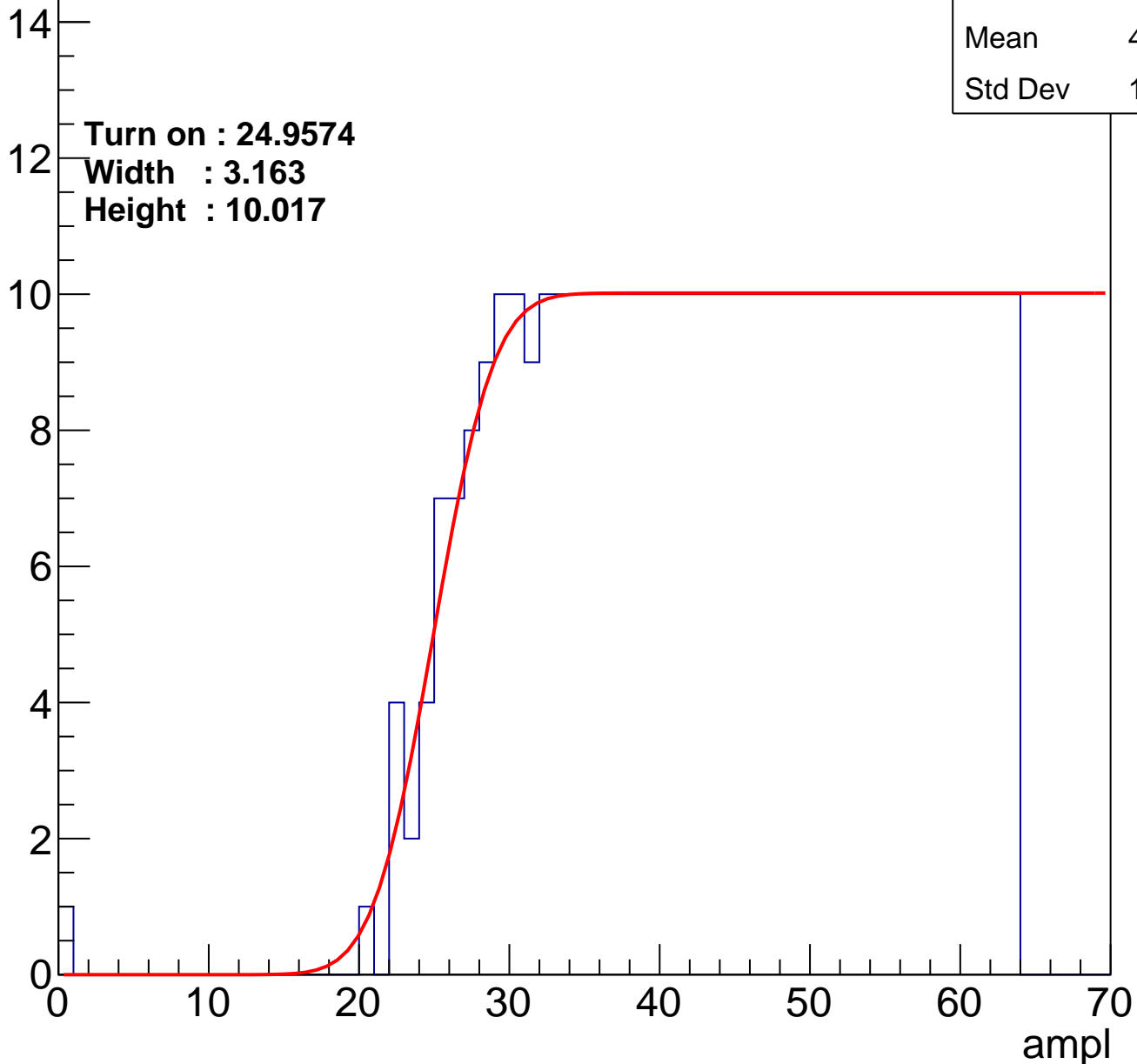
Entries	392
Mean	43.73
Std Dev	11.65

Turn on : 24.9574

Width : 3.163

Height : 10.017

Entry



B1L000S, U14-ch34

calib_packv5_042523_0143.root, FC#3, port B3

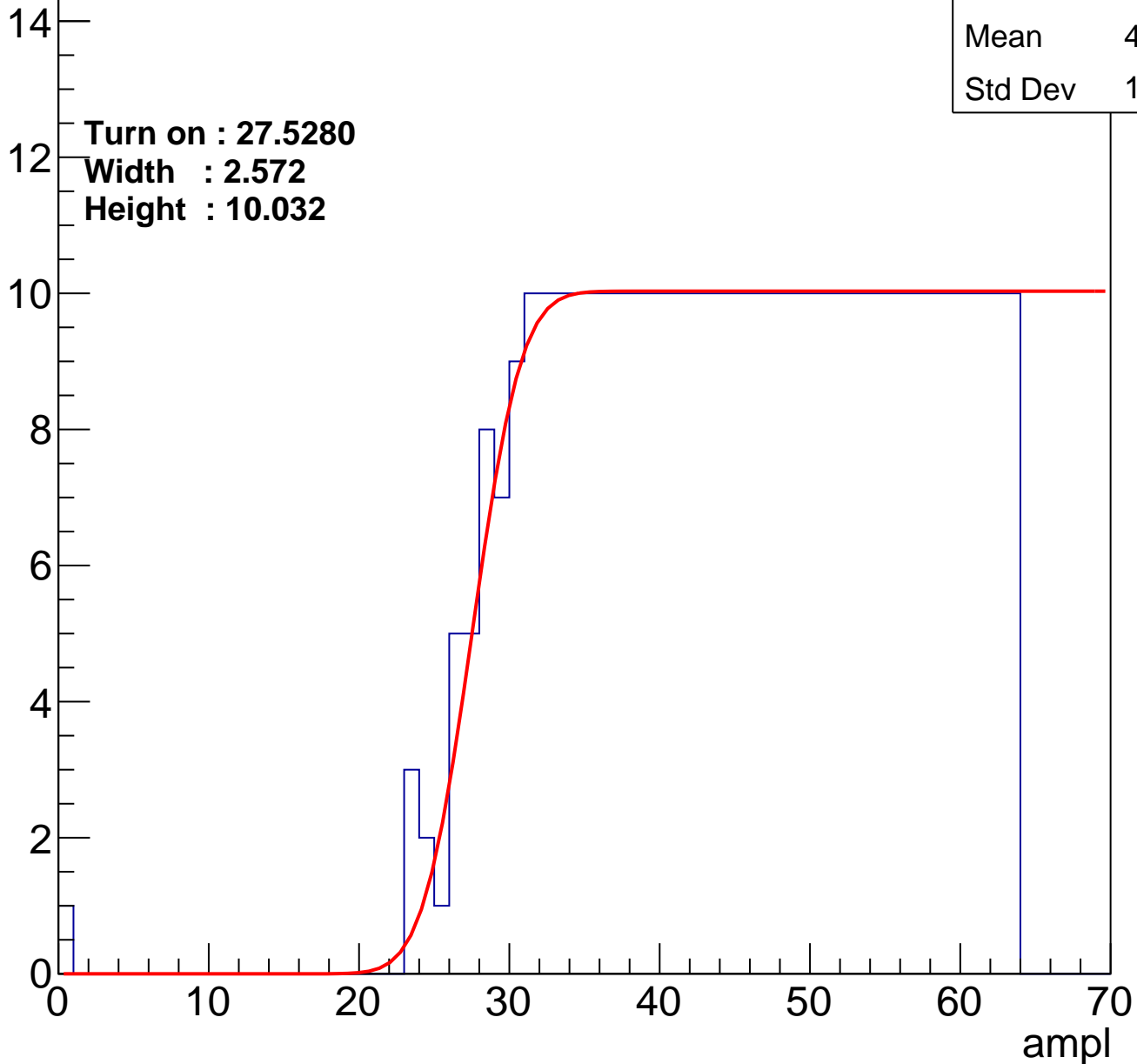
Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.5280

Width : 2.572

Height : 10.032

Entry



B1L000S, U14-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.39
Std Dev	11.96

Turn on : 24.4050

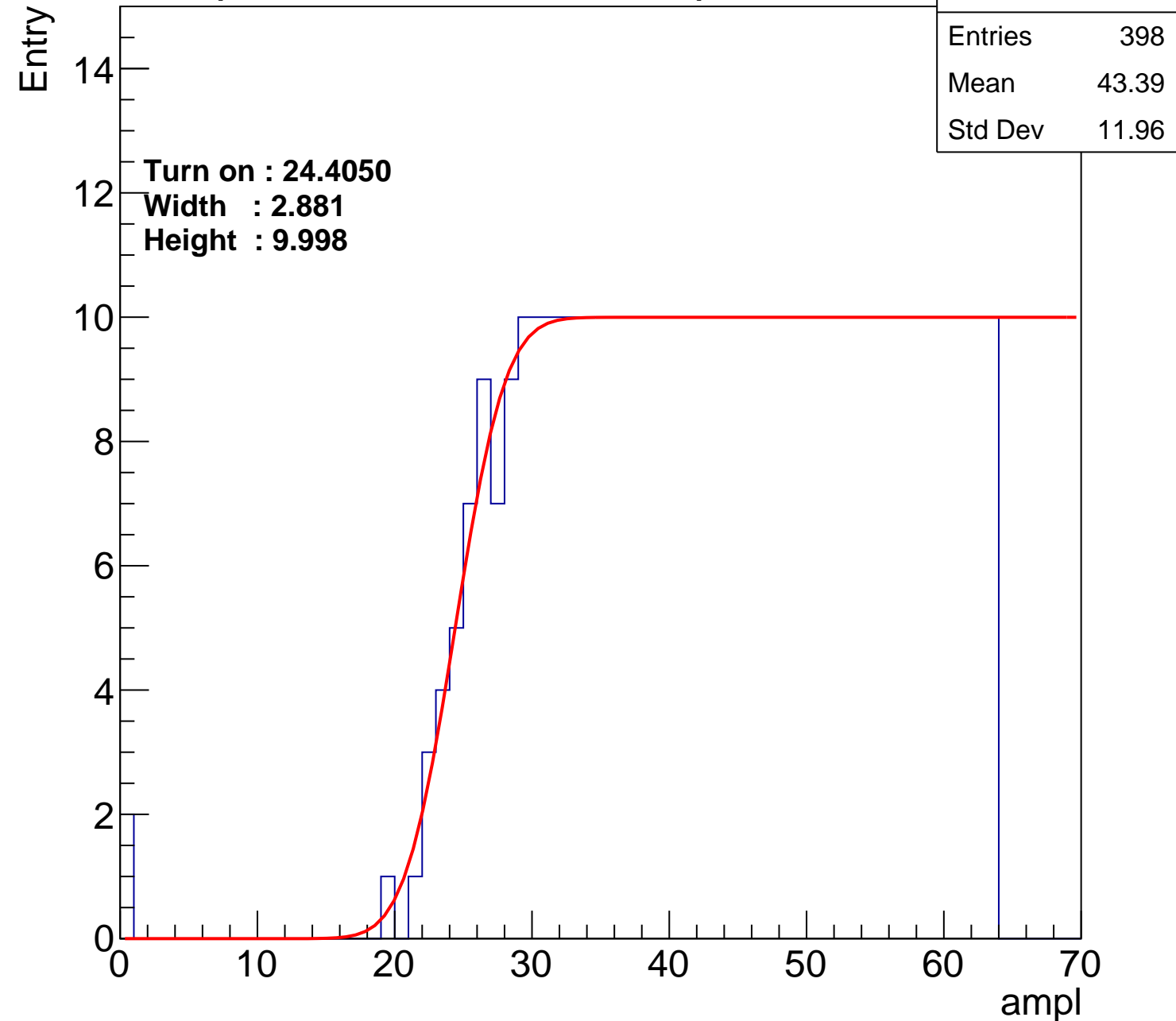
Width : 2.881

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 25.4257

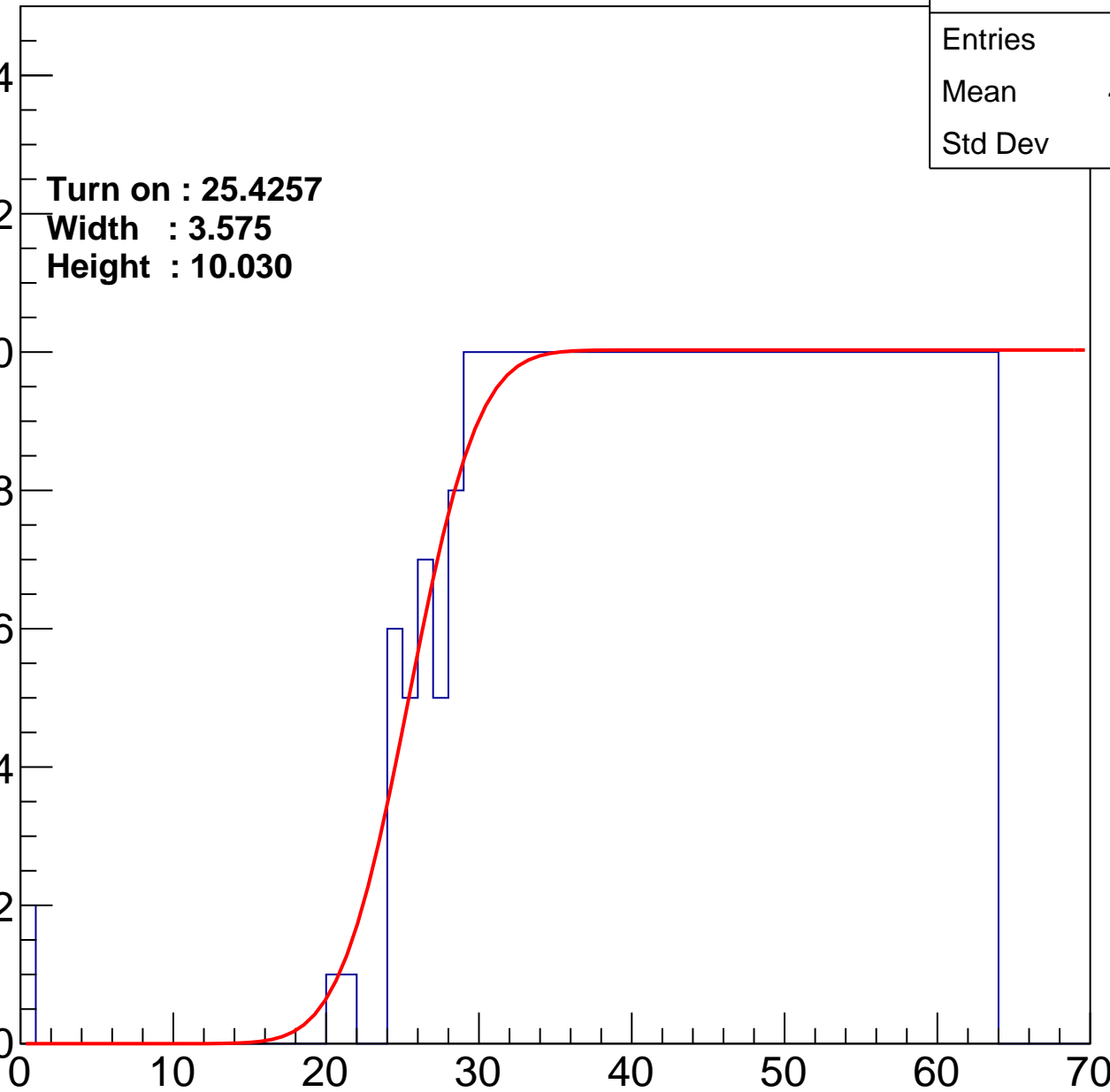
Width : 3.575

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch37

calib_packv5_042523_0143.root, FC#3, port B3

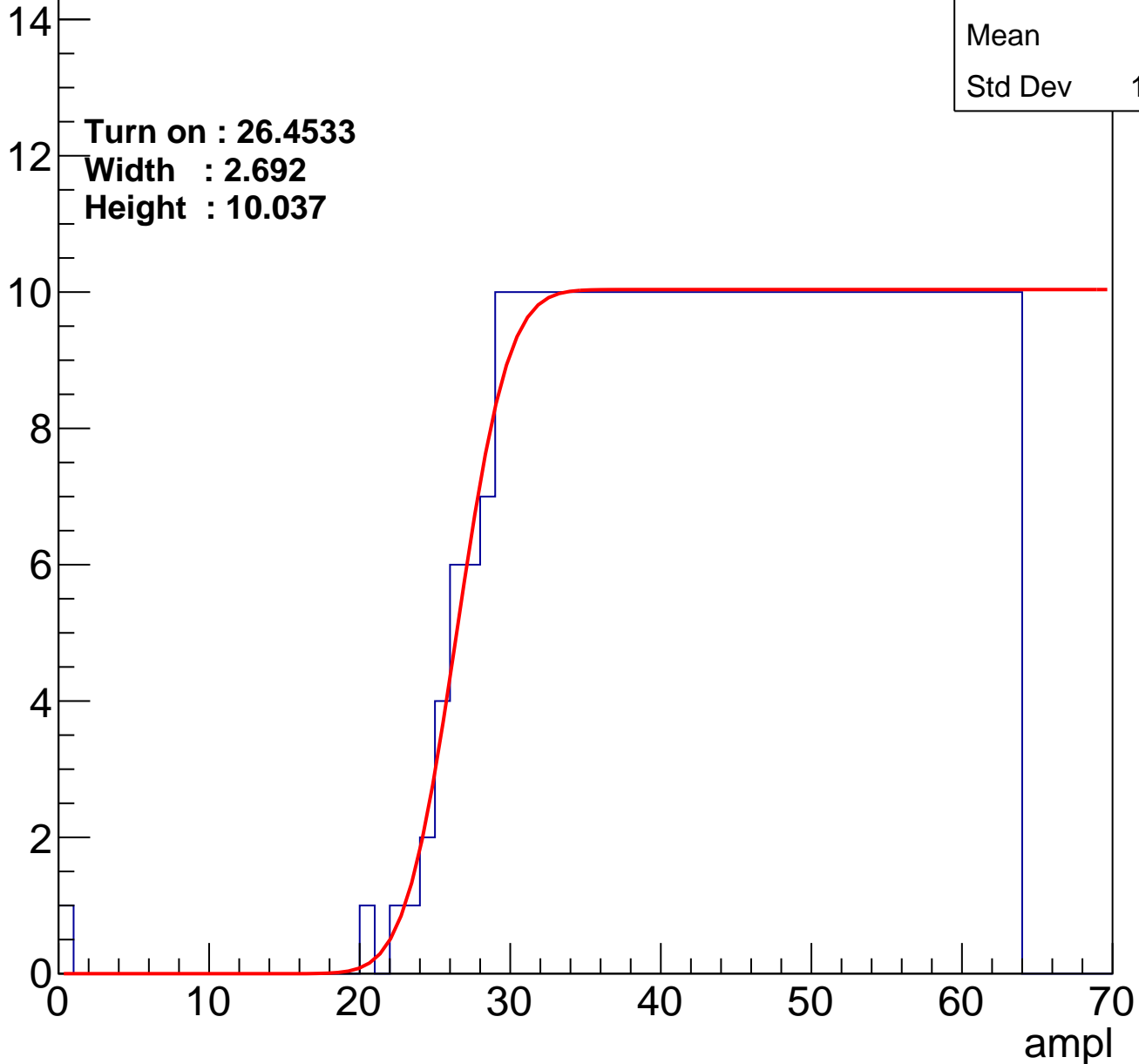
Entries	379
Mean	44.4
Std Dev	11.28

Turn on : 26.4533

Width : 2.692

Height : 10.037

Entry



B1L000S, U14-ch38

calib_packv5_042523_0143.root, FC#3, port B3

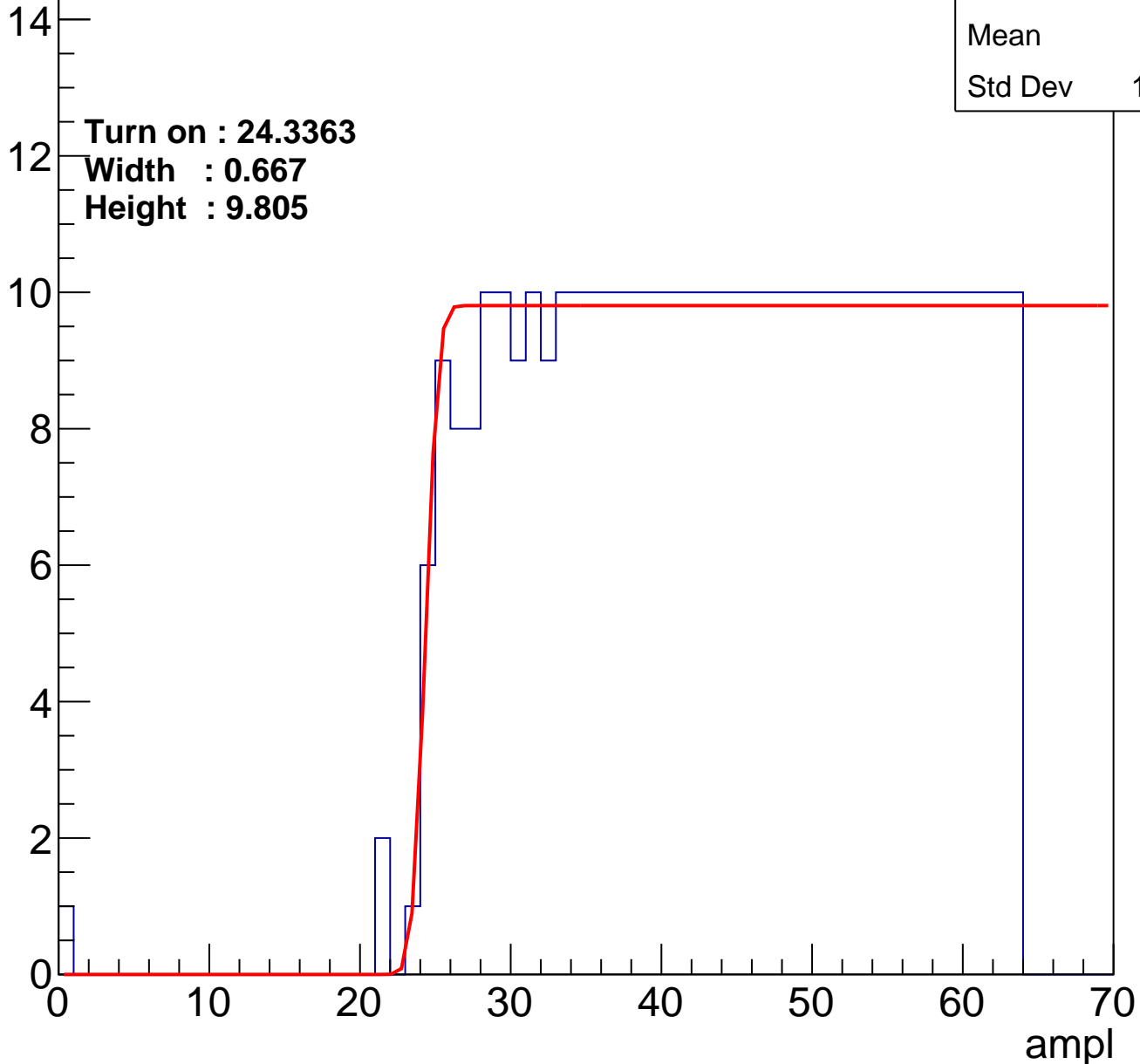
Entries	393
Mean	43.7
Std Dev	11.64

Turn on : 24.3363

Width : 0.667

Height : 9.805

Entry



B1L000S, U14-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.07
Std Dev	11.87

Turn on : 26.3080

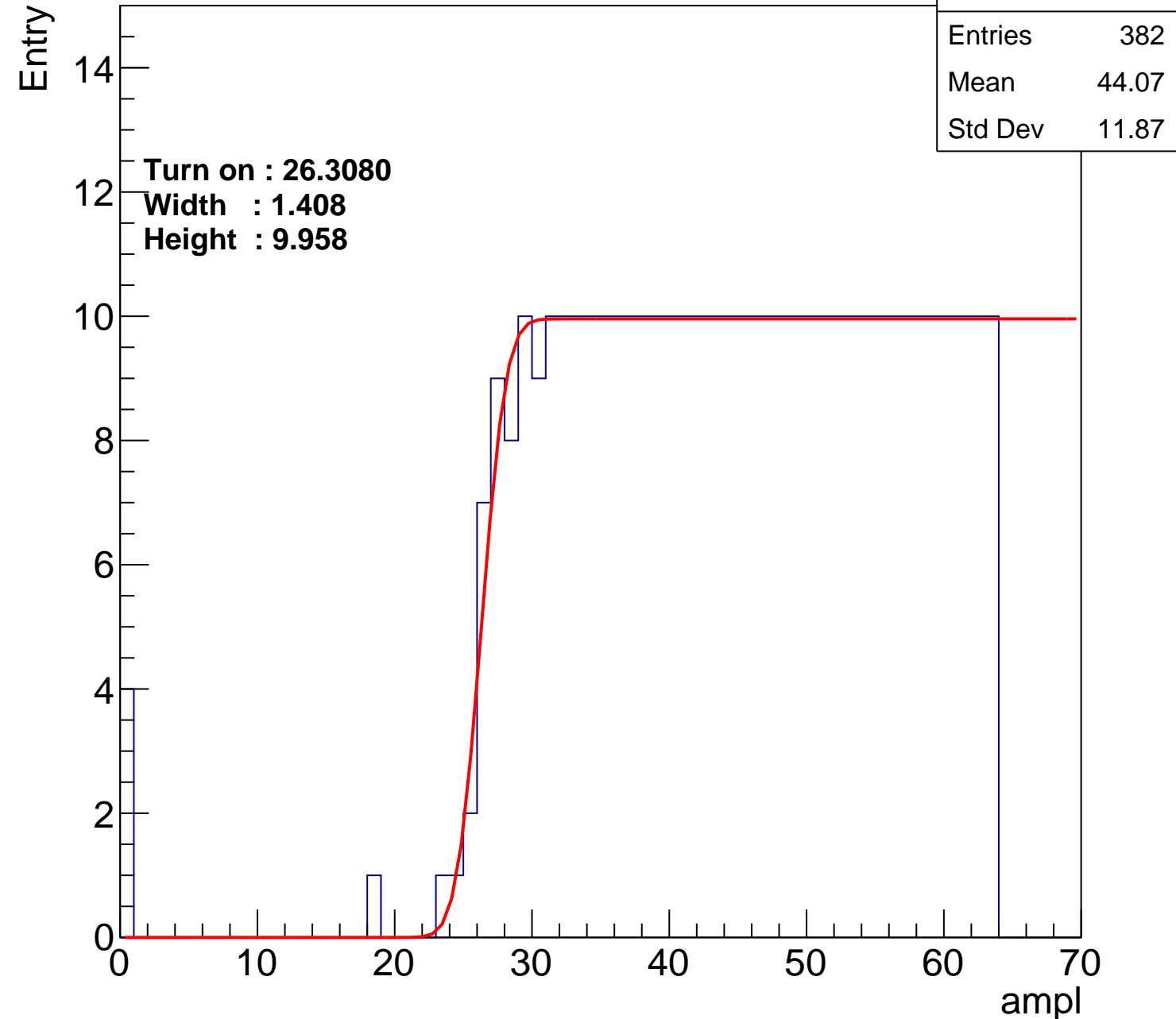
Width : 1.408

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch40

calib_packv5_042523_0143.root, FC#3, port B3

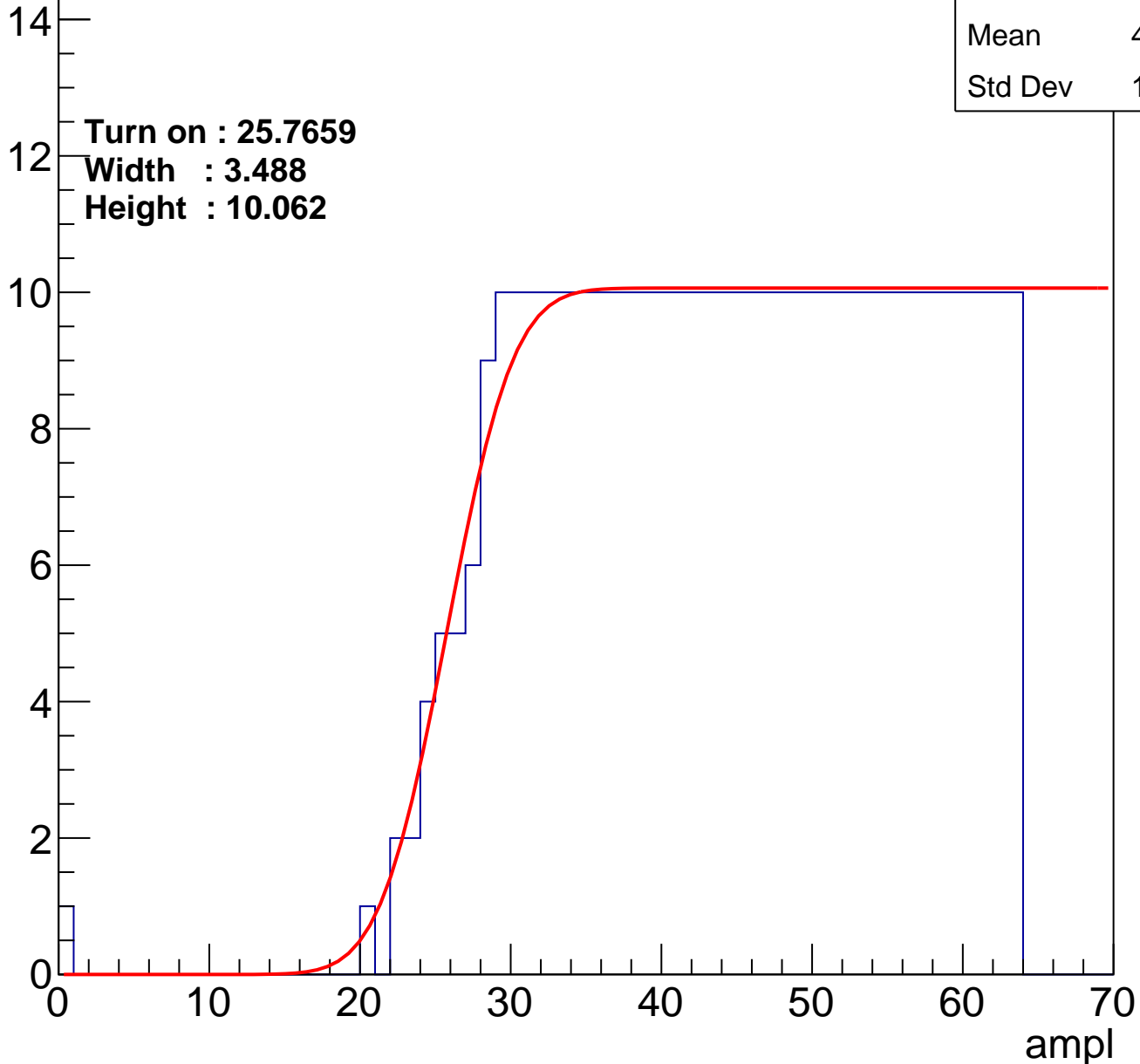
Entries	385
Mean	44.09
Std Dev	11.46

Turn on : 25.7659

Width : 3.488

Height : 10.062

Entry



B1L000S, U14-ch41

calib_packv5_042523_0143.root, FC#3, port B3

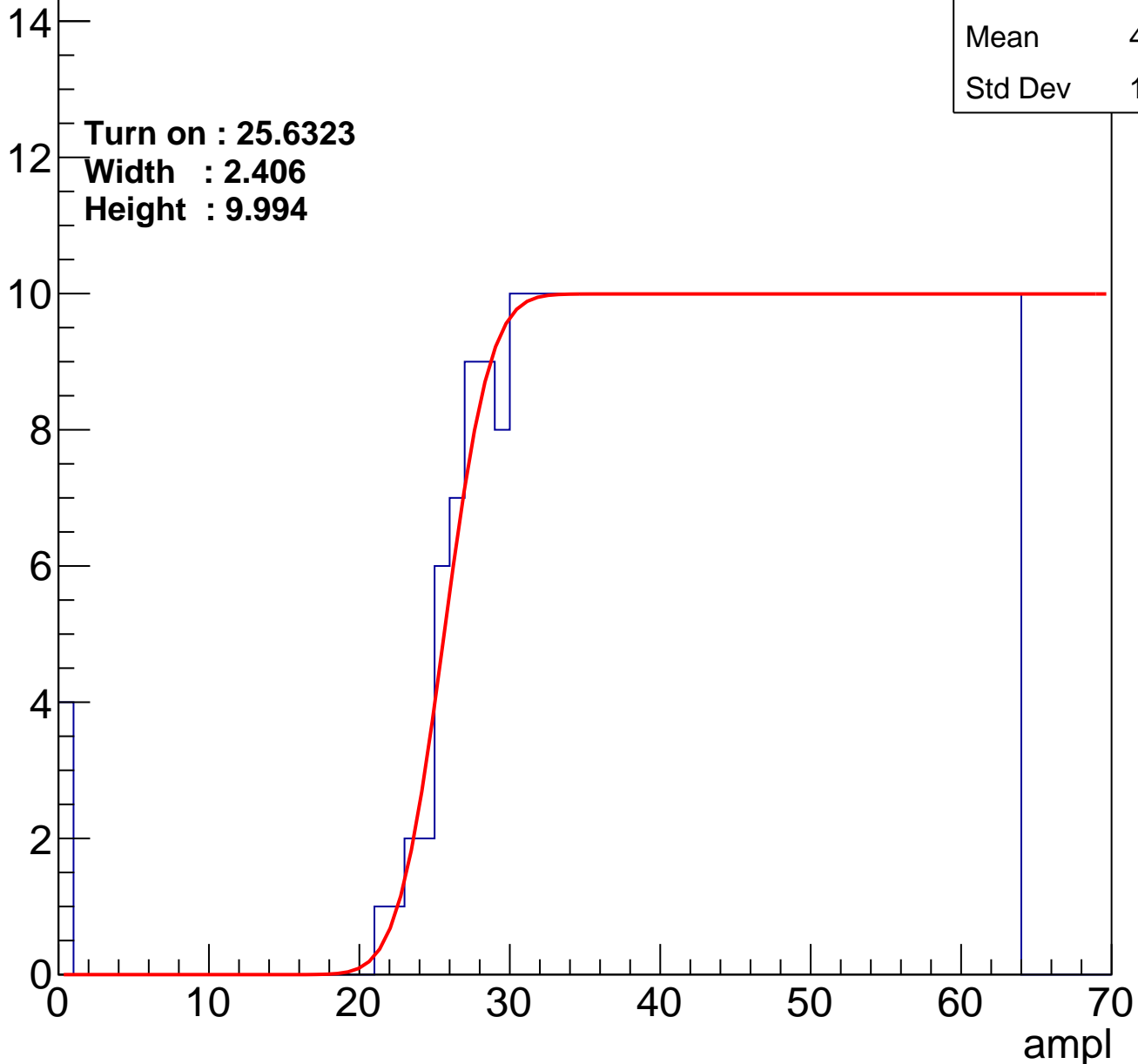
Entries	389
Mean	43.72
Std Dev	12.04

Turn on : 25.6323

Width : 2.406

Height : 9.994

Entry



B1L000S, U14-ch42

calib_packv5_042523_0143.root, FC#3, port B3

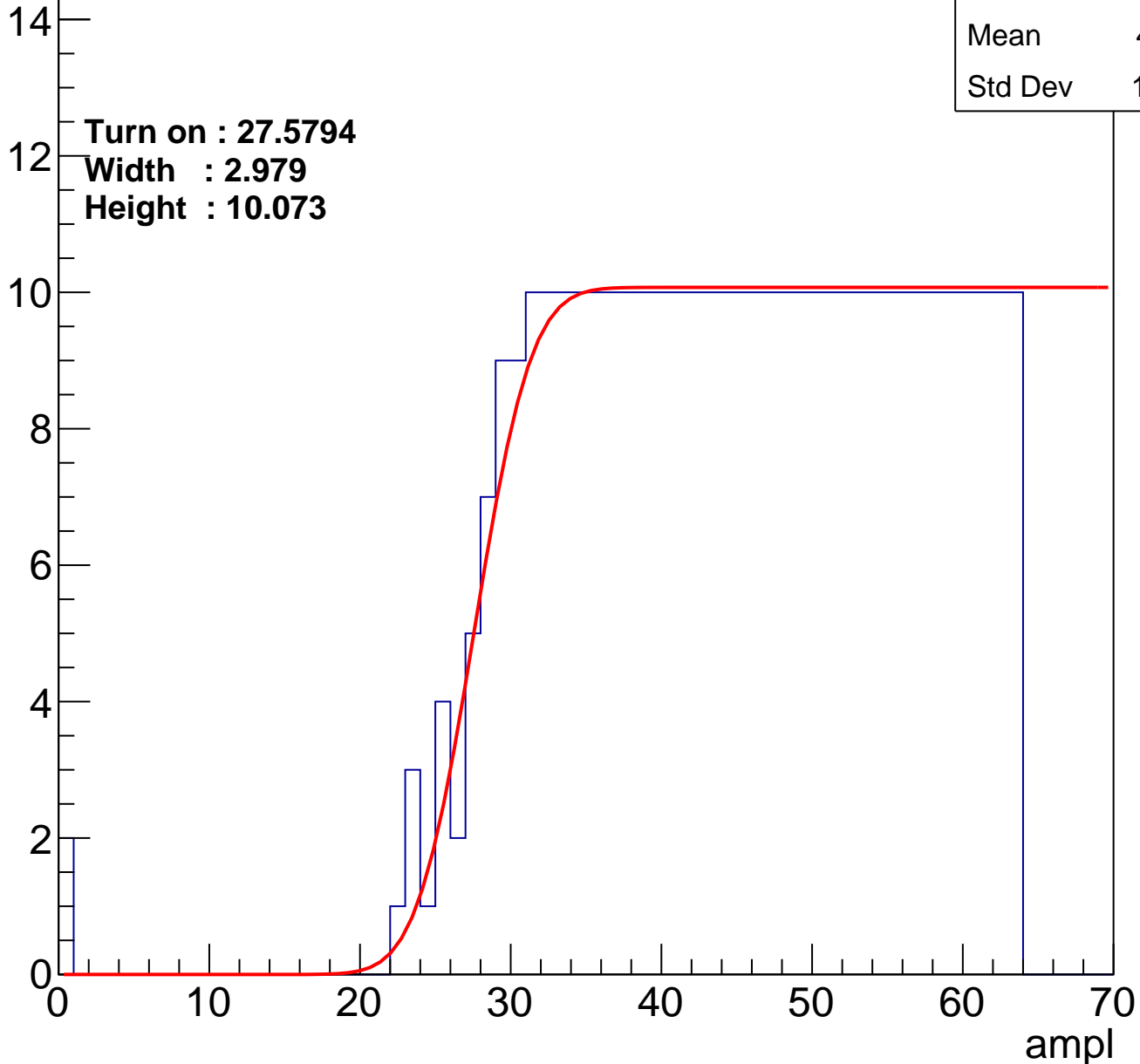
Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.5794

Width : 2.979

Height : 10.073

Entry



B1L000S, U14-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.35
Std Dev	11.35

Turn on : 26.2218

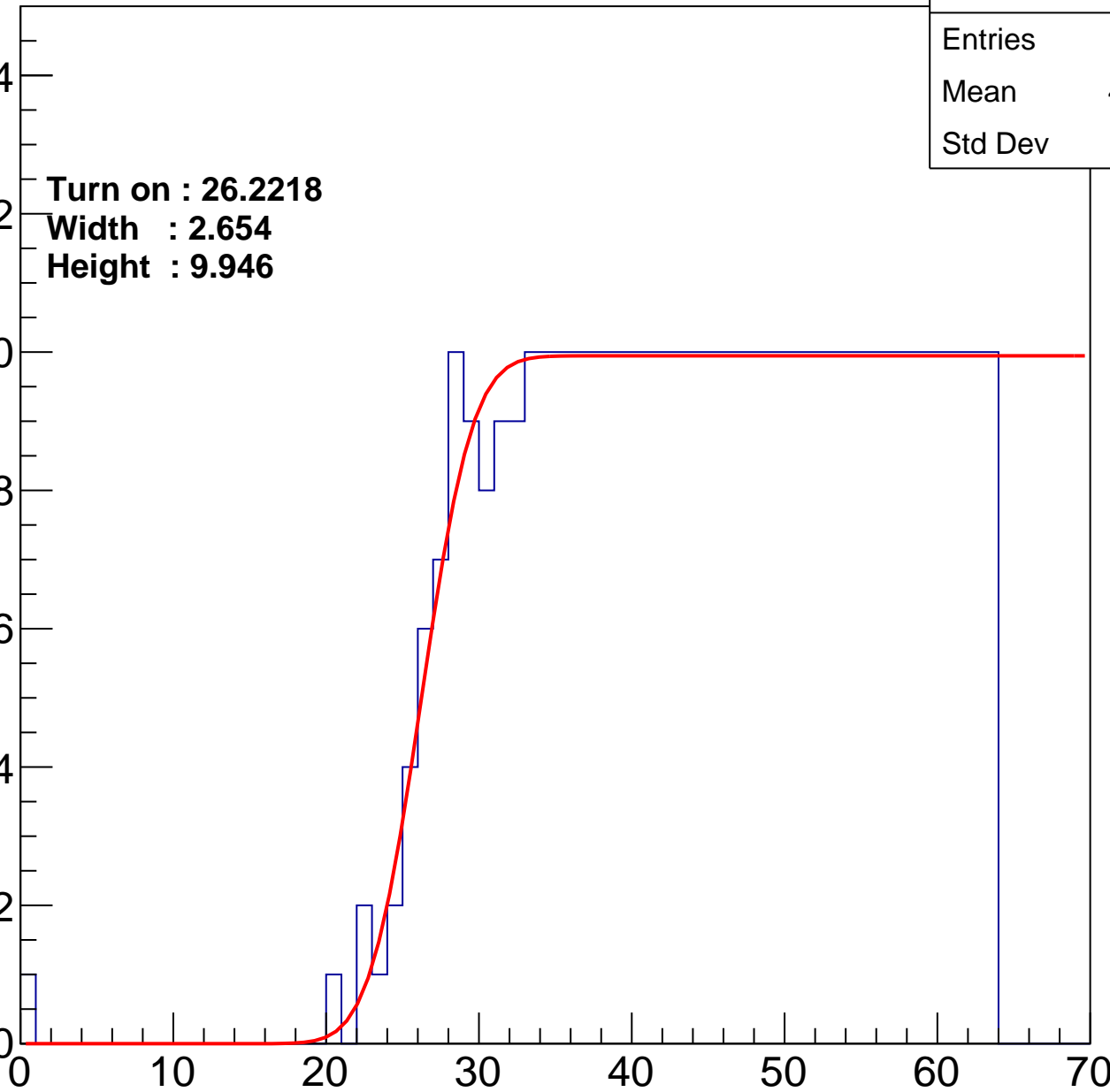
Width : 2.654

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch44

calib_packv5_042523_0143.root, FC#3, port B3

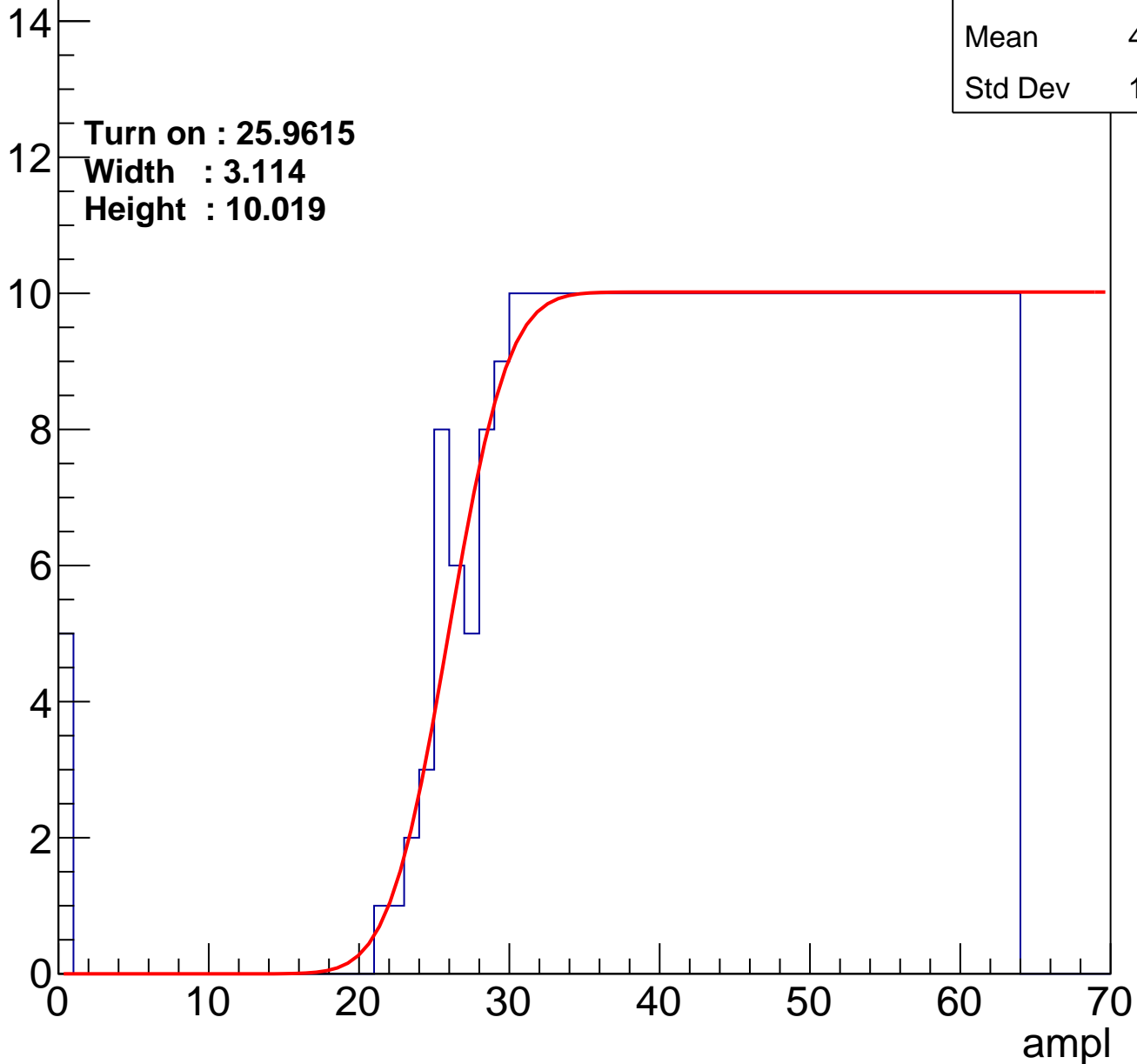
Entries	388
Mean	43.68
Std Dev	12.22

Turn on : 25.9615

Width : 3.114

Height : 10.019

Entry



B1L000S, U14-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.87
Std Dev	11.67

Turn on : 25.9306

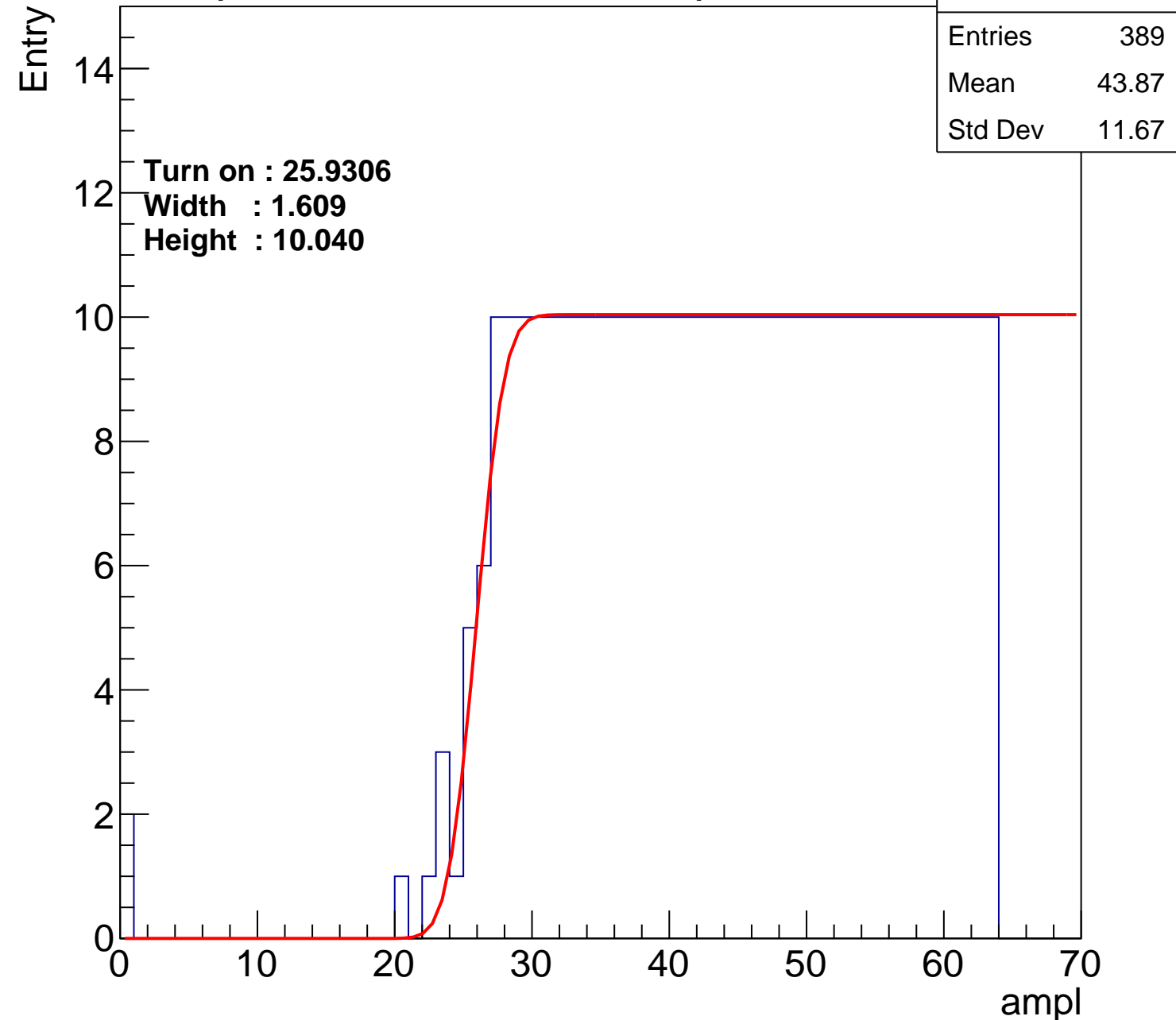
Width : 1.609

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch46

calib_packv5_042523_0143.root, FC#3, port B3

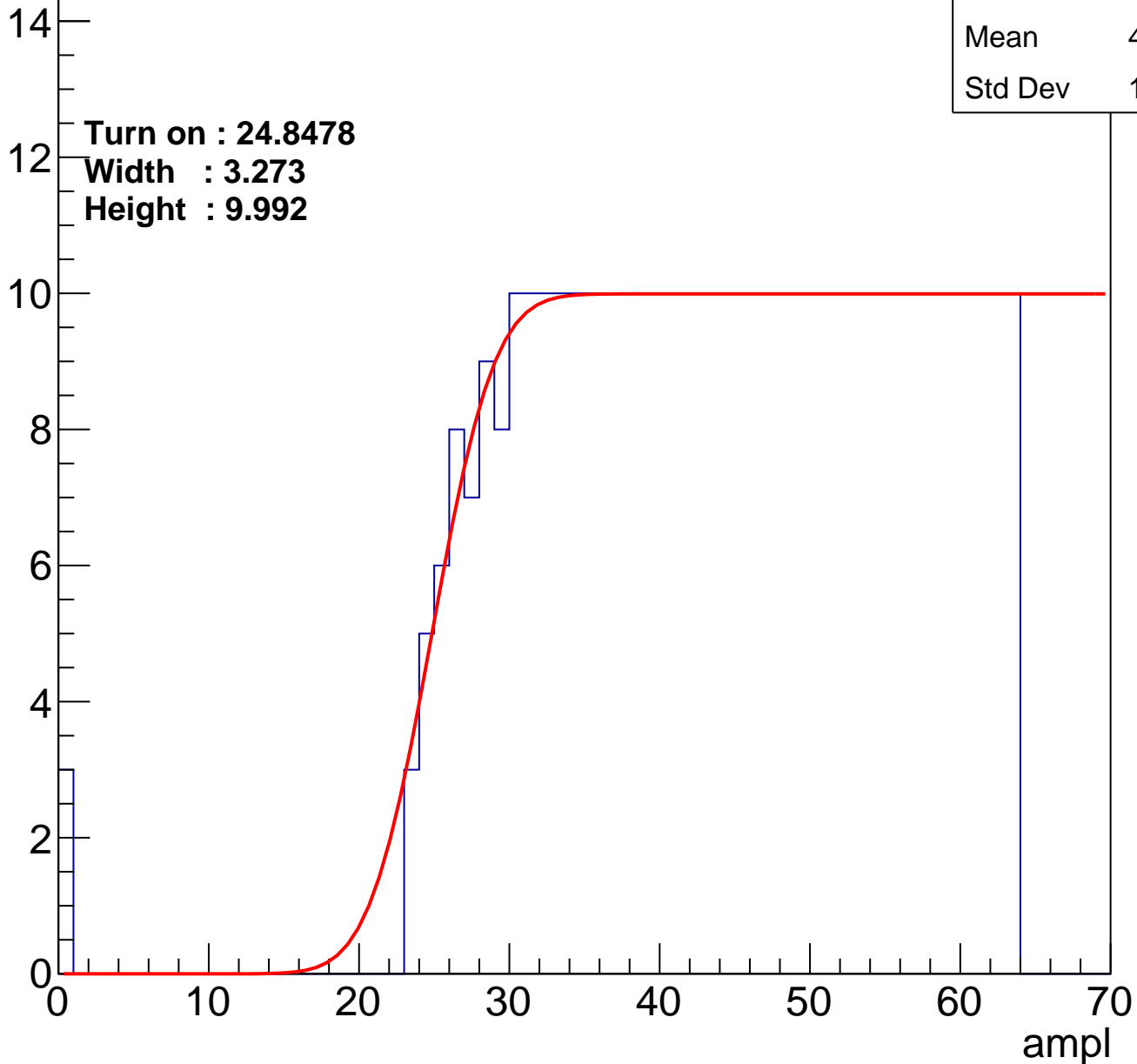
Entries	389
Mean	43.78
Std Dev	11.87

Turn on : 24.8478

Width : 3.273

Height : 9.992

Entry



B1L000S, U14-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.78
Std Dev	11.23

Turn on : 26.8555

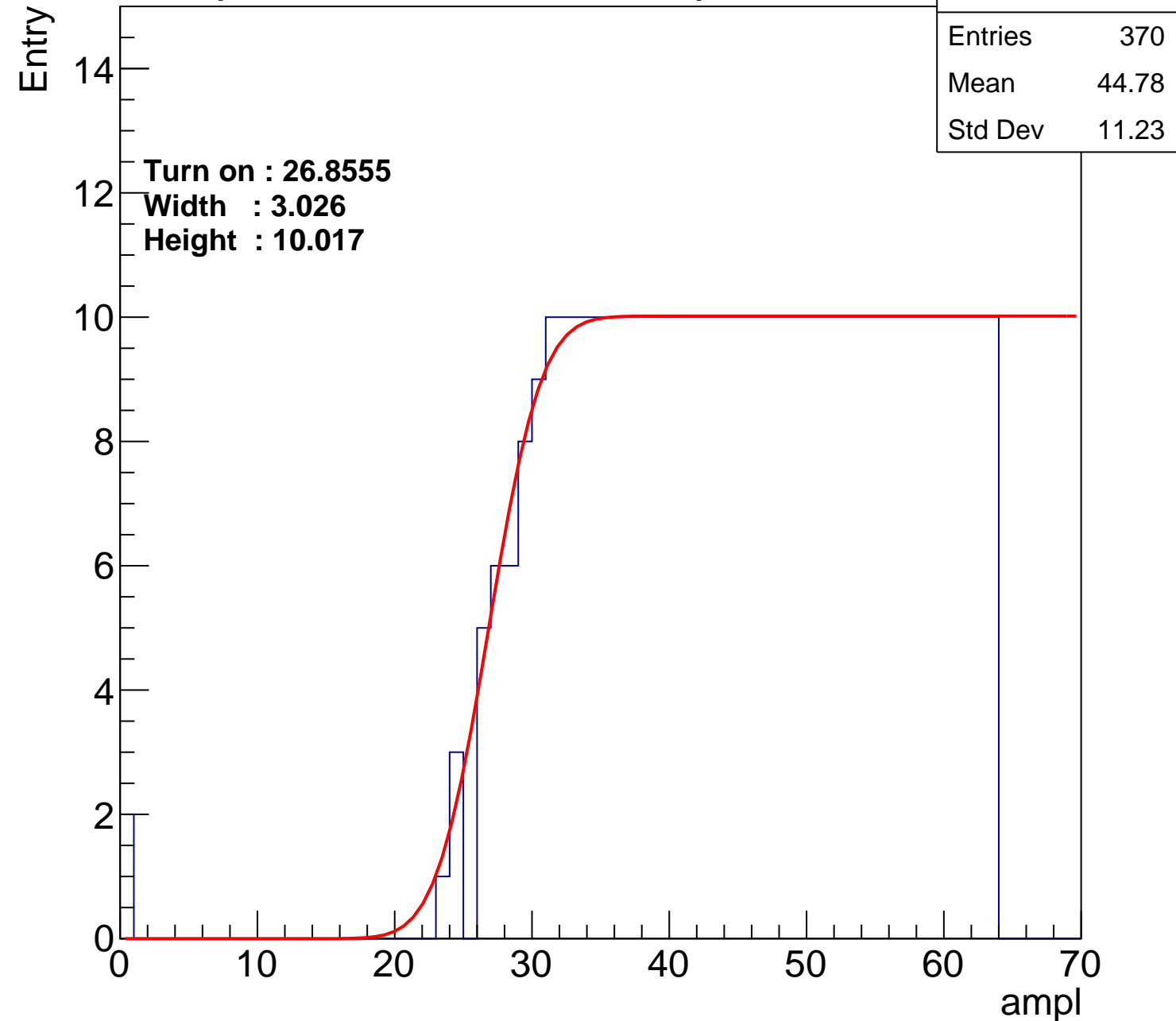
Width : 3.026

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.13
Std Dev	12.06

Turn on : 24.0155

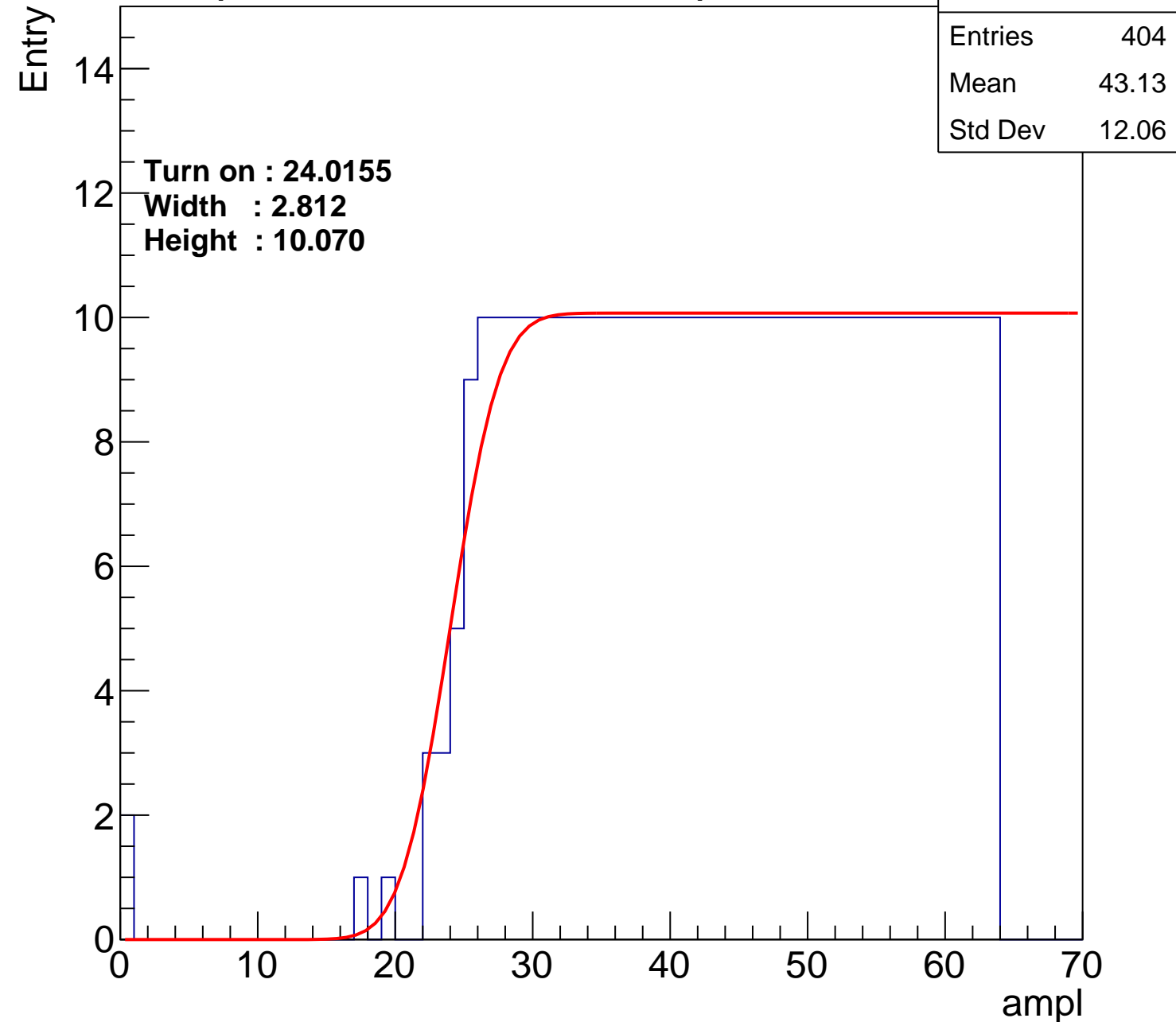
Width : 2.812

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch49

calib_packv5_042523_0143.root, FC#3, port B3

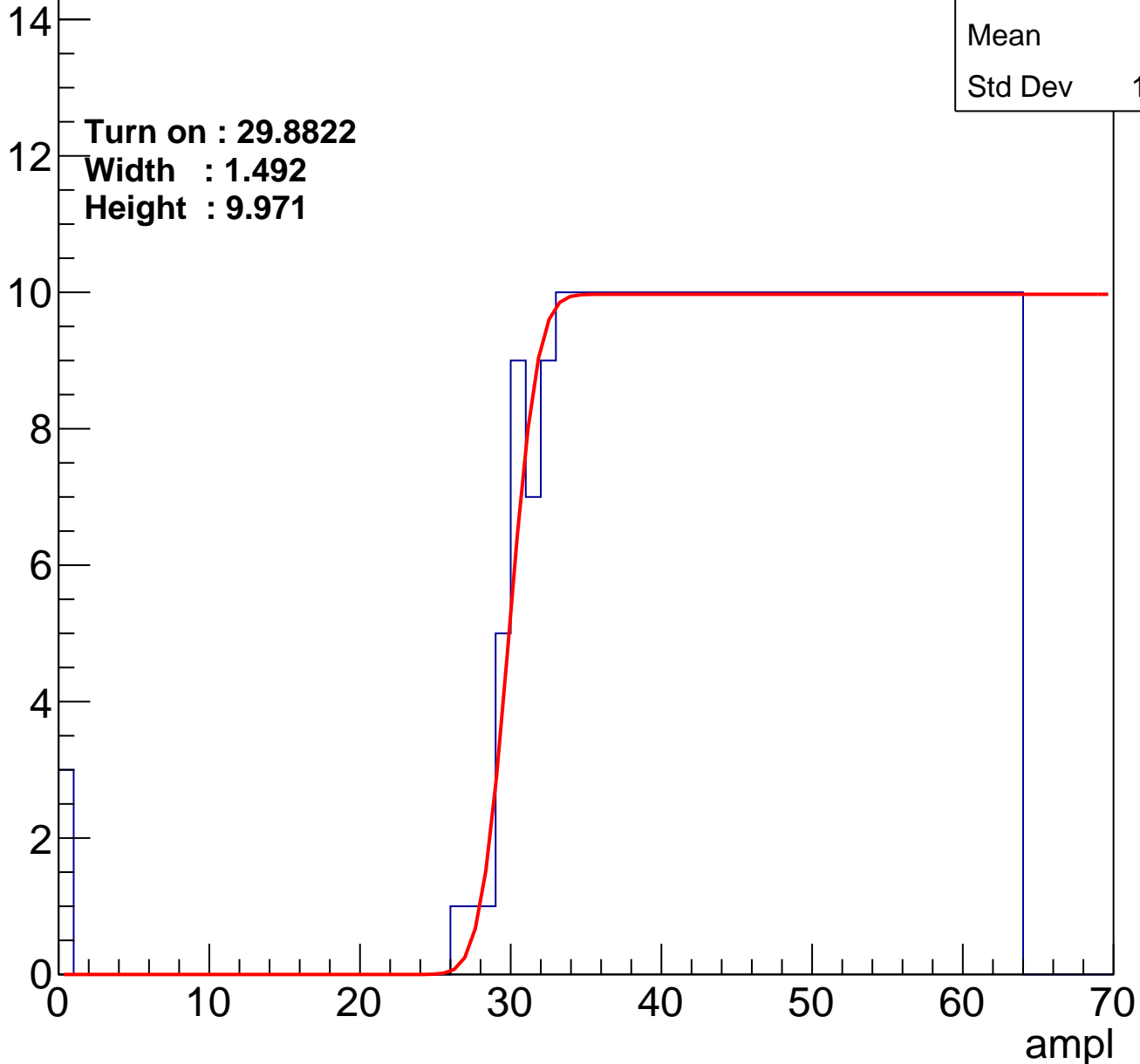
Entries	346
Mean	45.9
Std Dev	10.83

Turn on : 29.8822

Width : 1.492

Height : 9.971

Entry



B1L000S, U14-ch50

calib_packv5_042523_0143.root, FC#3, port B3

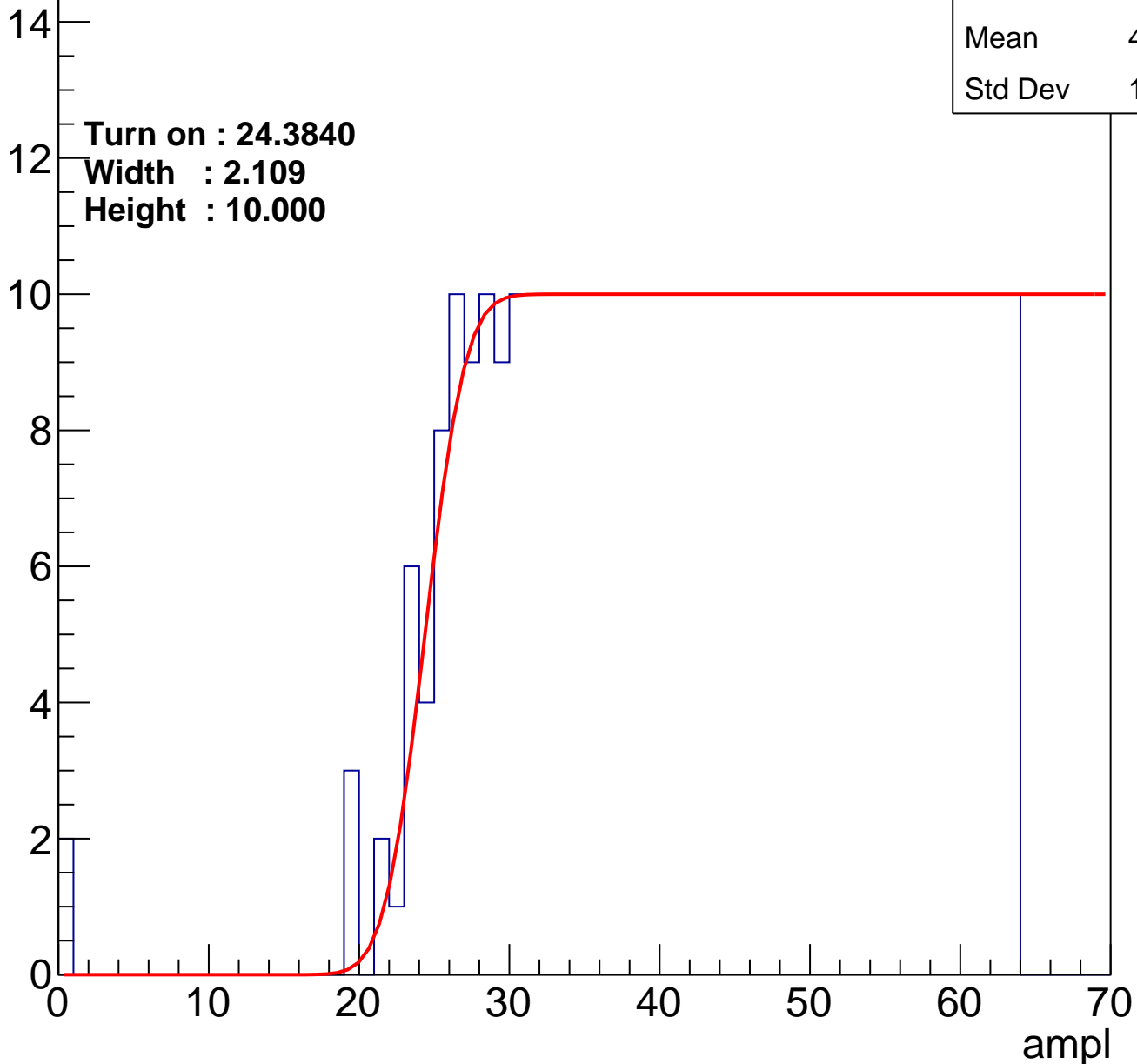
Entries	404
Mean	43.09
Std Dev	12.12

Turn on : 24.3840

Width : 2.109

Height : 10.000

Entry



B1L000S, U14-ch51

calib_packv5_042523_0143.root, FC#3, port B3

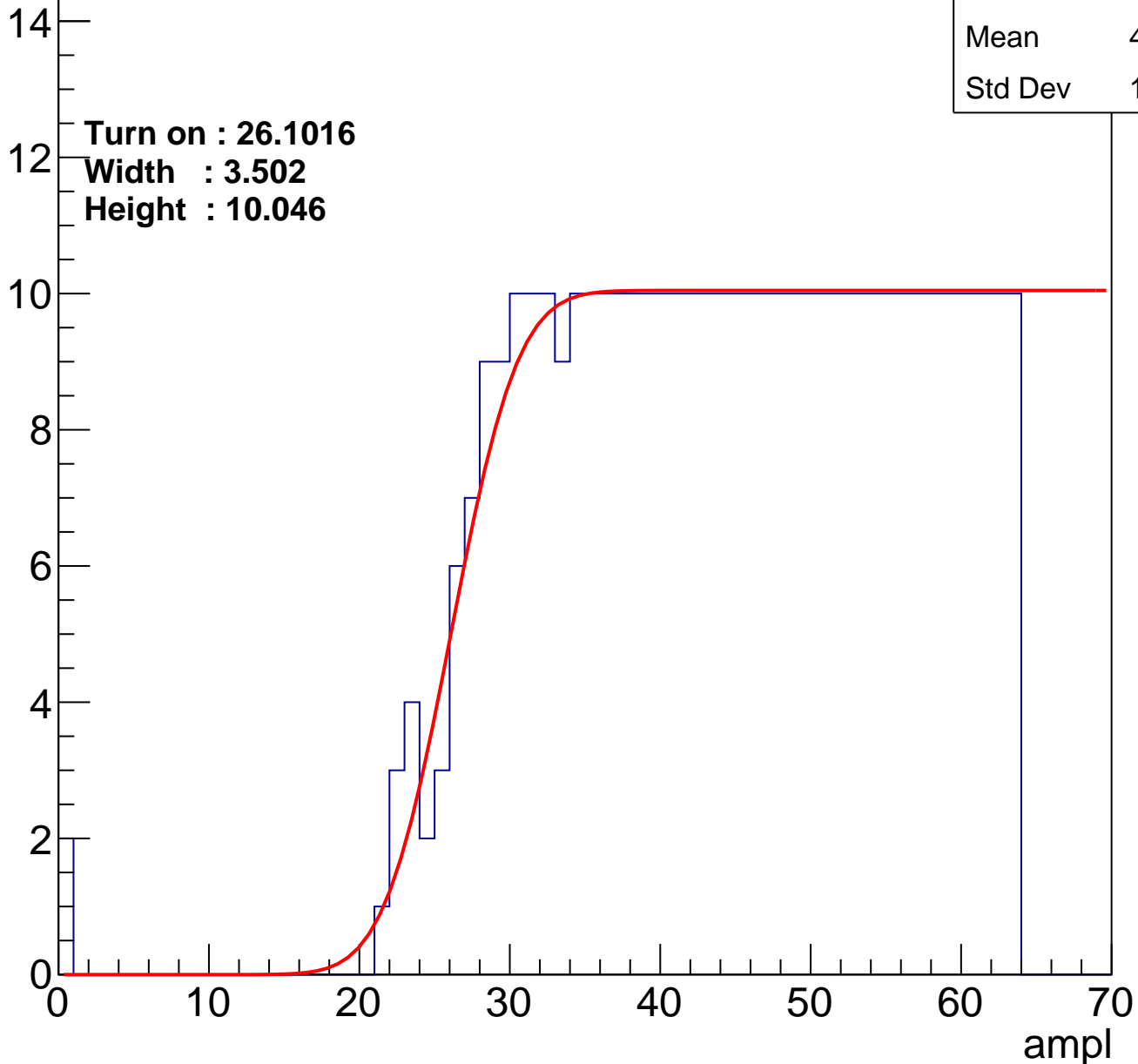
Entries	385
Mean	43.99
Std Dev	11.68

Turn on : 26.1016

Width : 3.502

Height : 10.046

Entry



B1L000S, U14-ch52

calib_packv5_042523_0143.root, FC#3, port B3

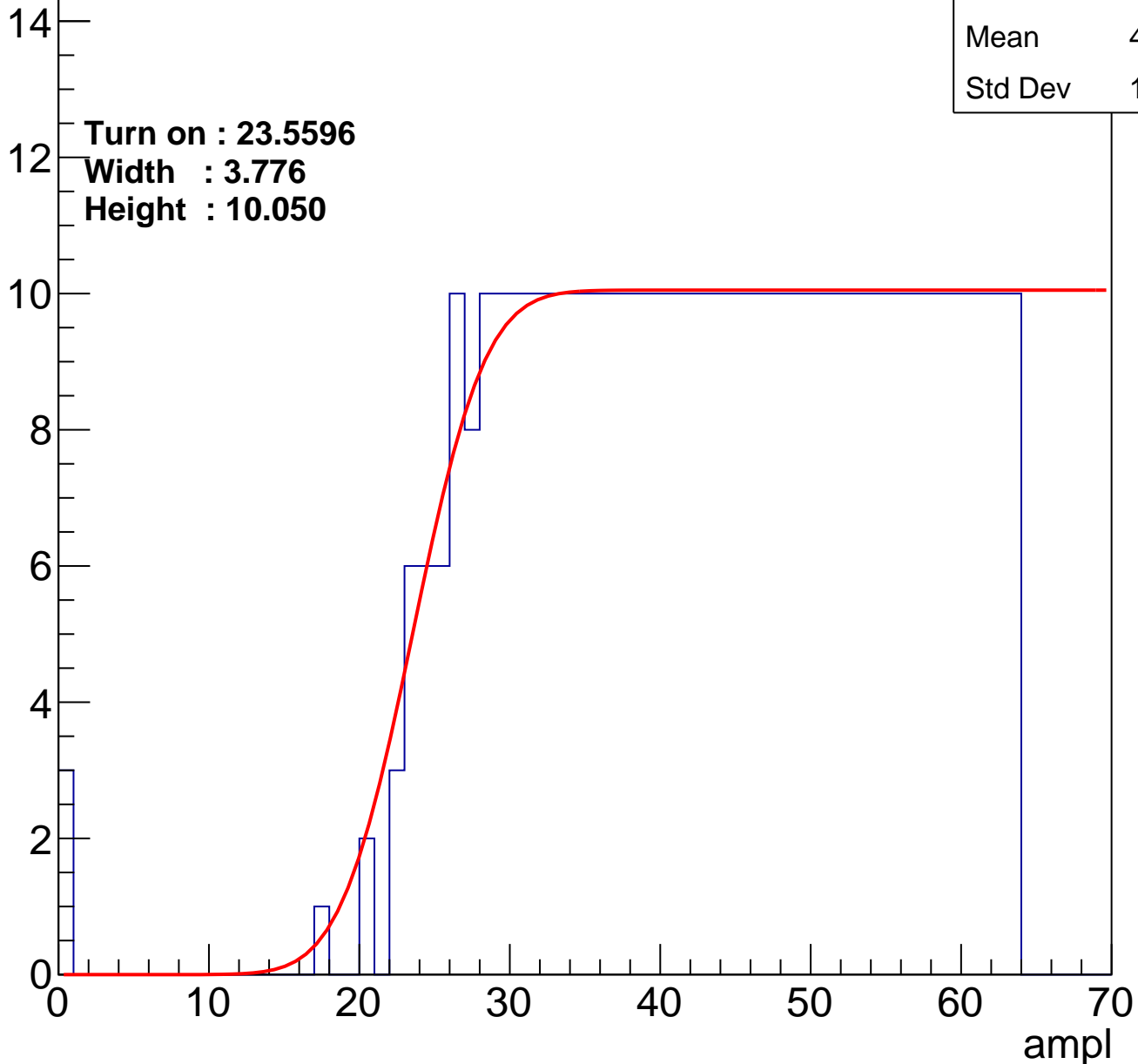
Entries	405
Mean	42.99
Std Dev	12.29

Turn on : 23.5596

Width : 3.776

Height : 10.050

Entry



B1L000S, U14-ch53

calib_packv5_042523_0143.root, FC#3, port B3

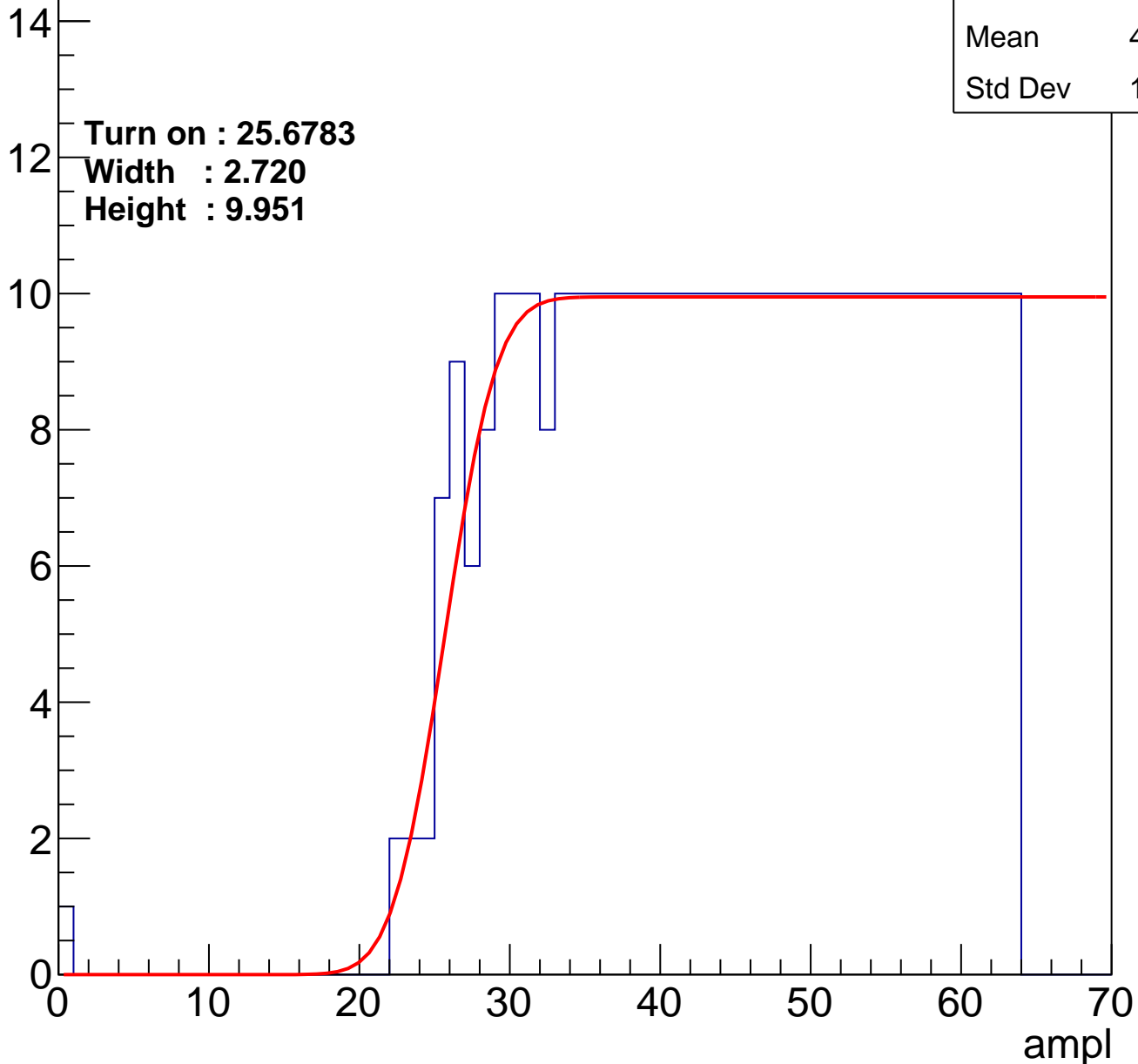
Entries	385
Mean	44.08
Std Dev	11.47

Turn on : 25.6783

Width : 2.720

Height : 9.951

Entry



B1L000S, U14-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.93
Std Dev	11.67

Turn on : 25.4905

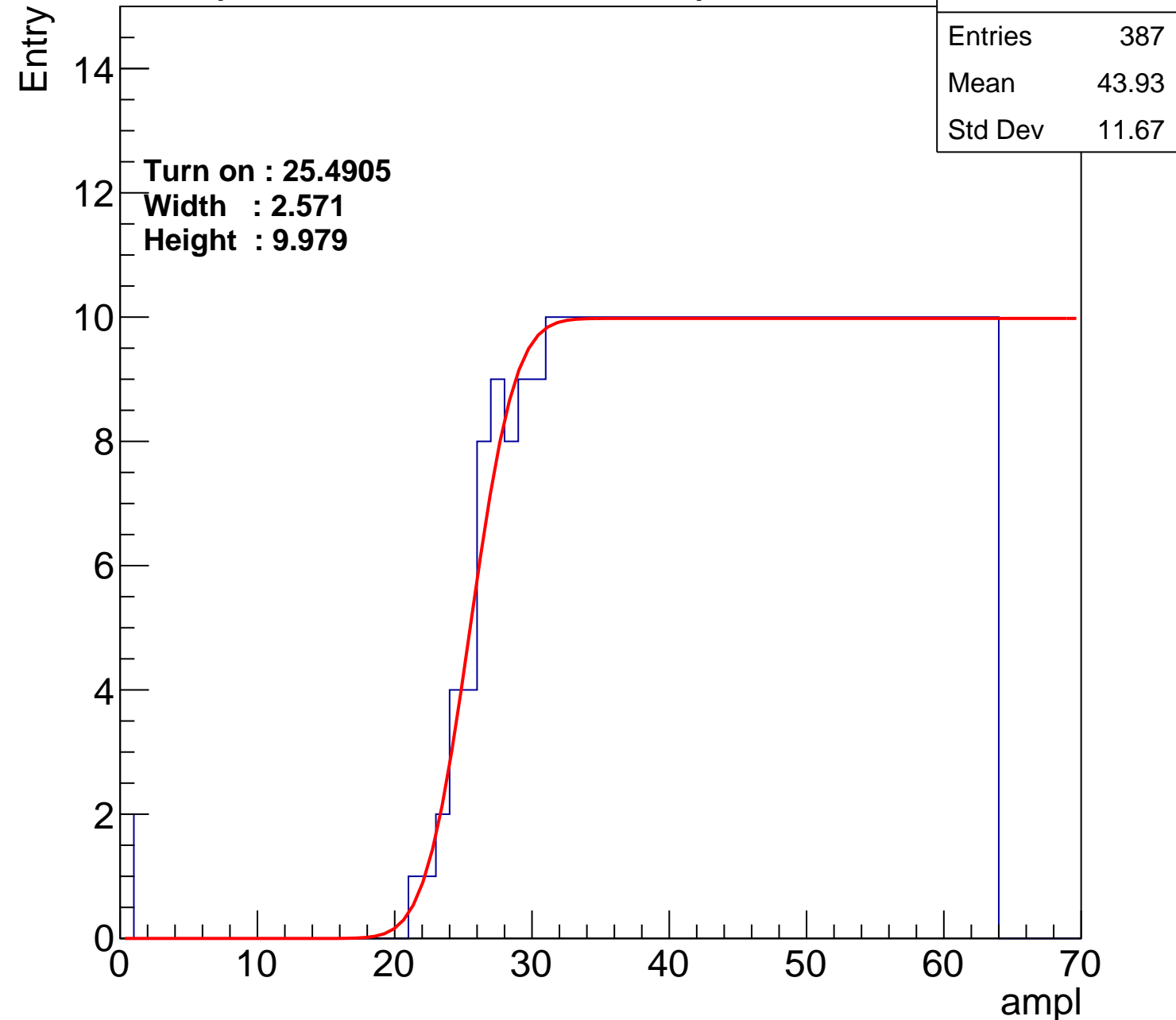
Width : 2.571

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.09
Std Dev	11.4

Turn on : 28.2097

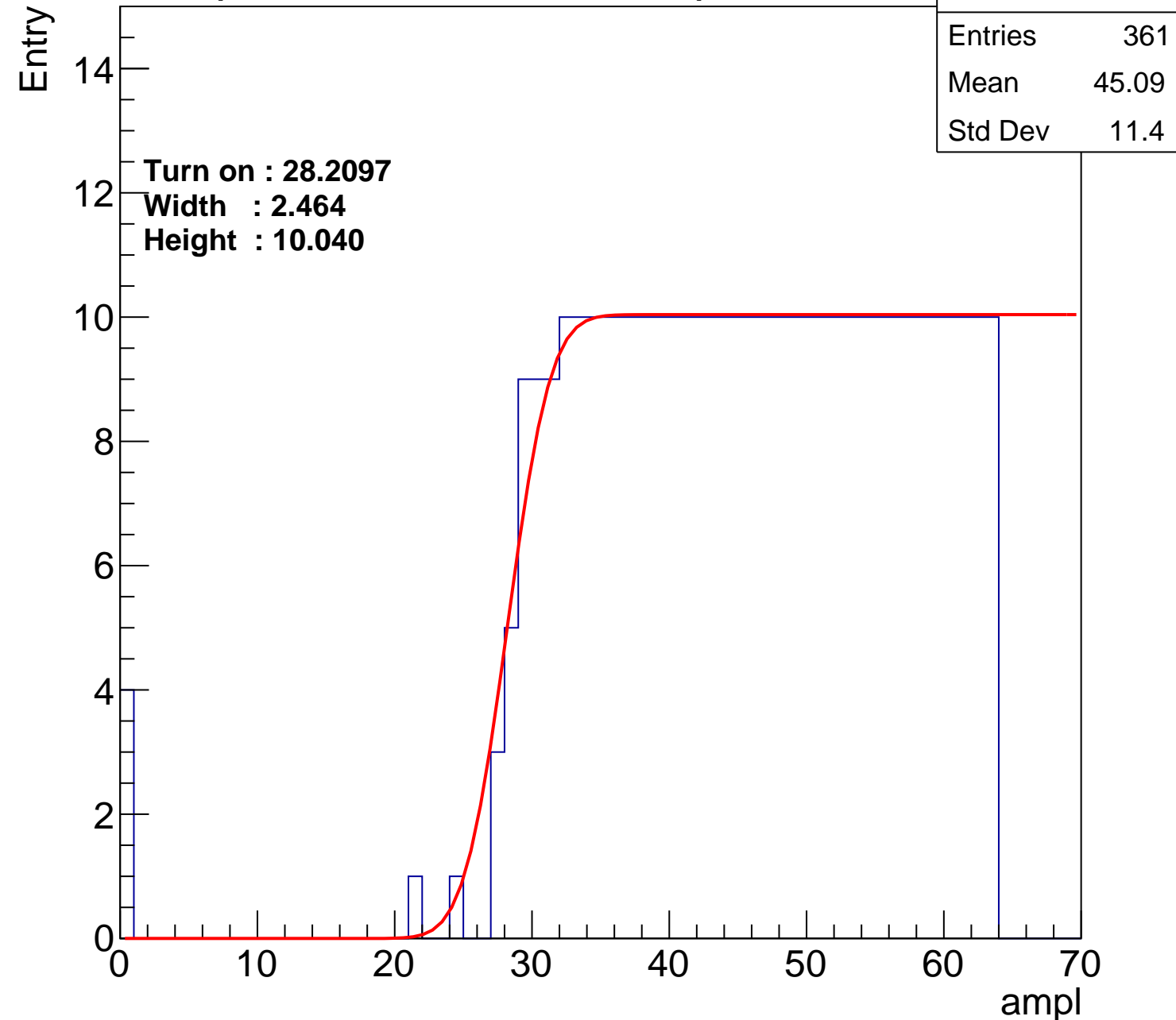
Width : 2.464

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.01
Std Dev	12.08

Turn on : 26.8040

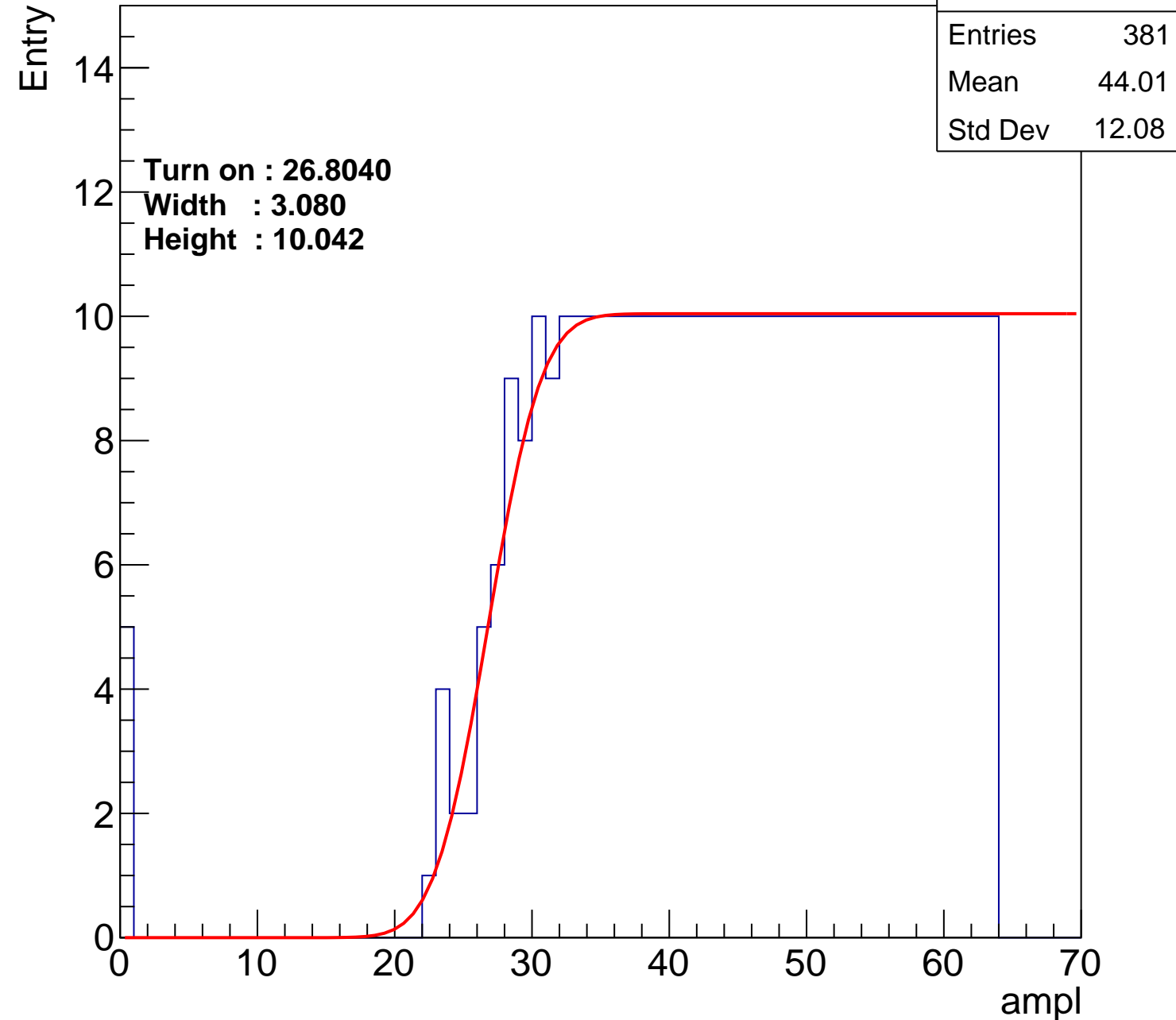
Width : 3.080

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch57

calib_packv5_042523_0143.root, FC#3, port B3

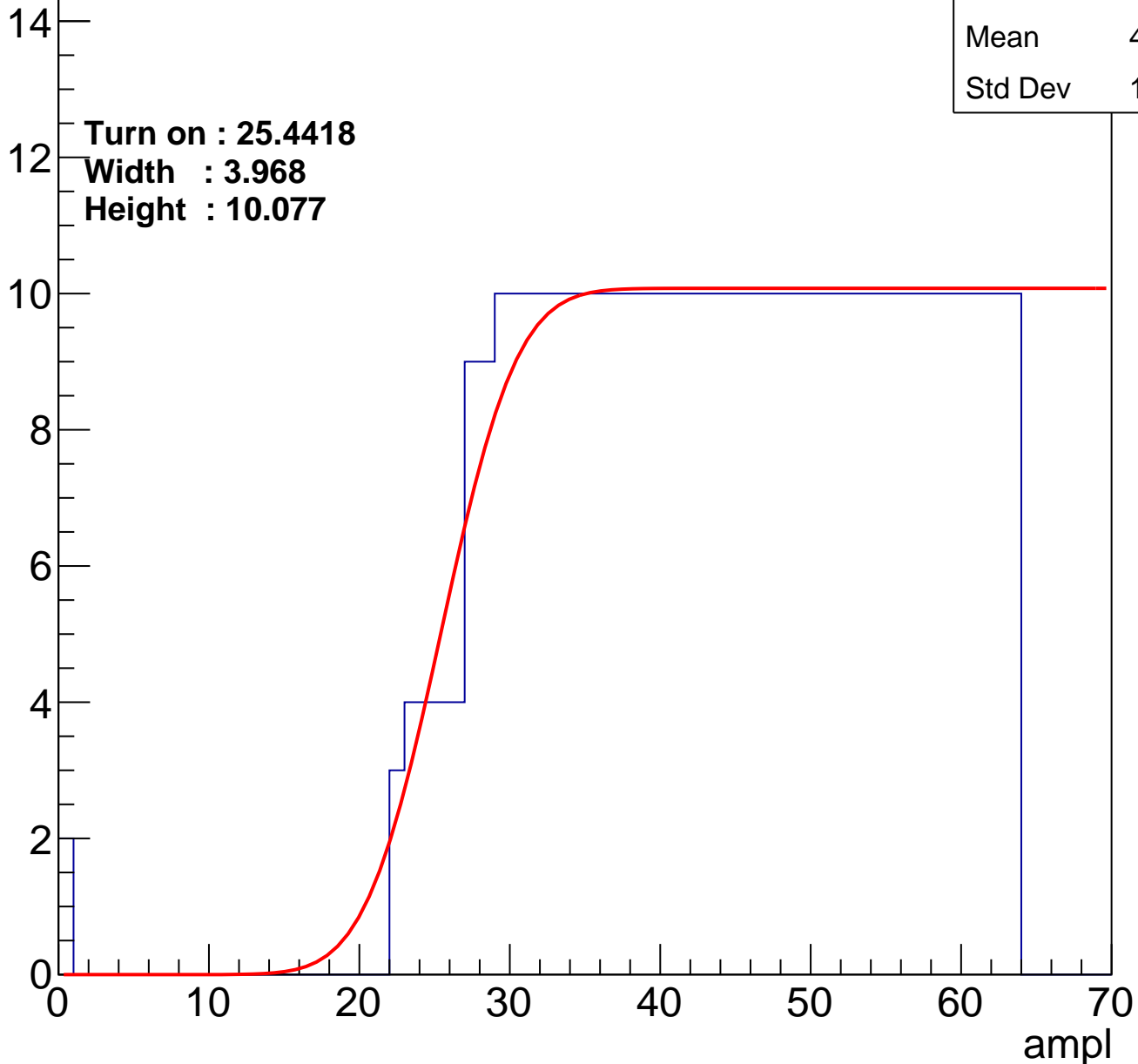
Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.4418

Width : 3.968

Height : 10.077

Entry



B1L000S, U14-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.14
Std Dev	11.72

Turn on : 25.7408

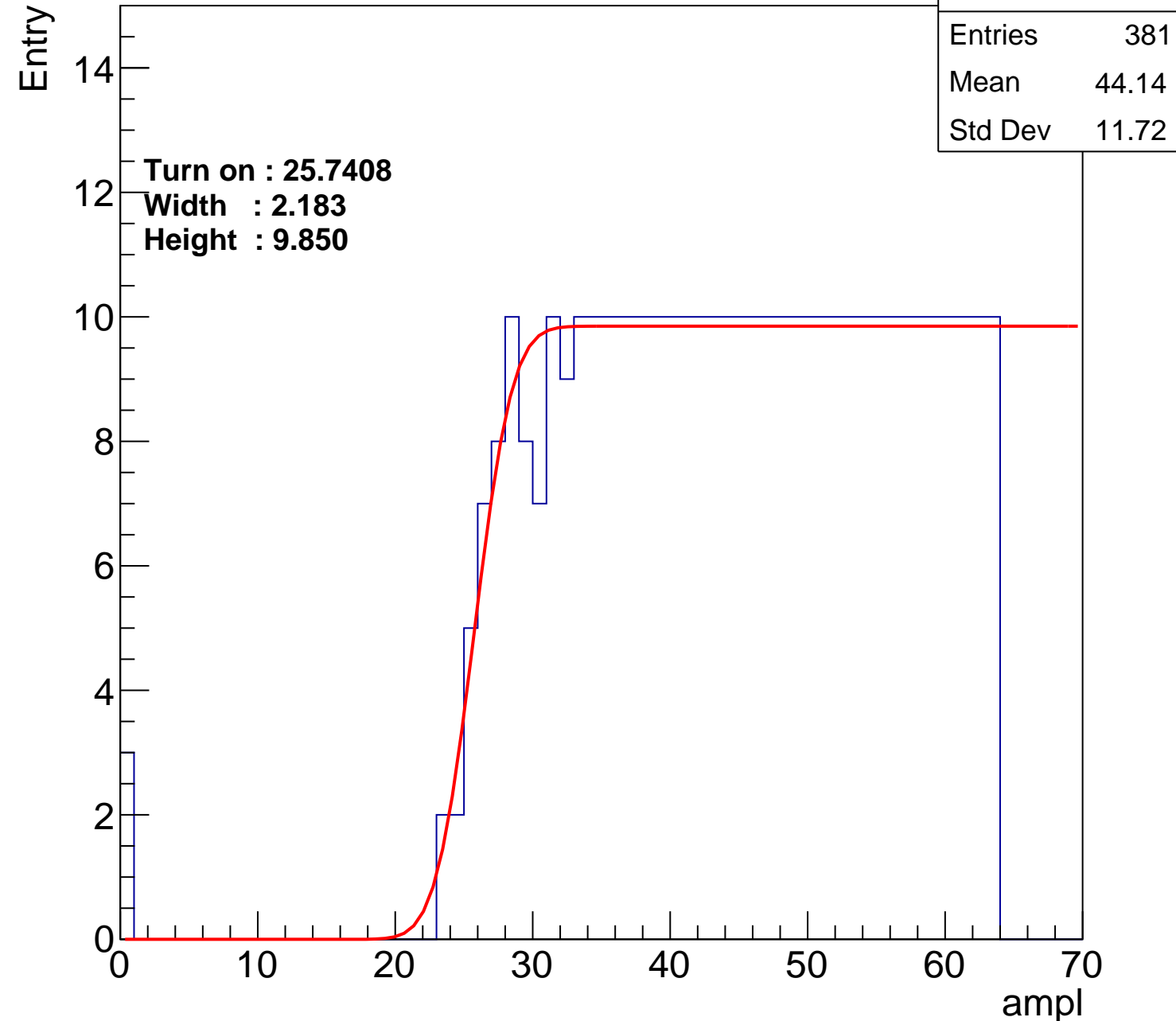
Width : 2.183

Height : 9.850

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.78
Std Dev	11.75

Turn on : 25.7100

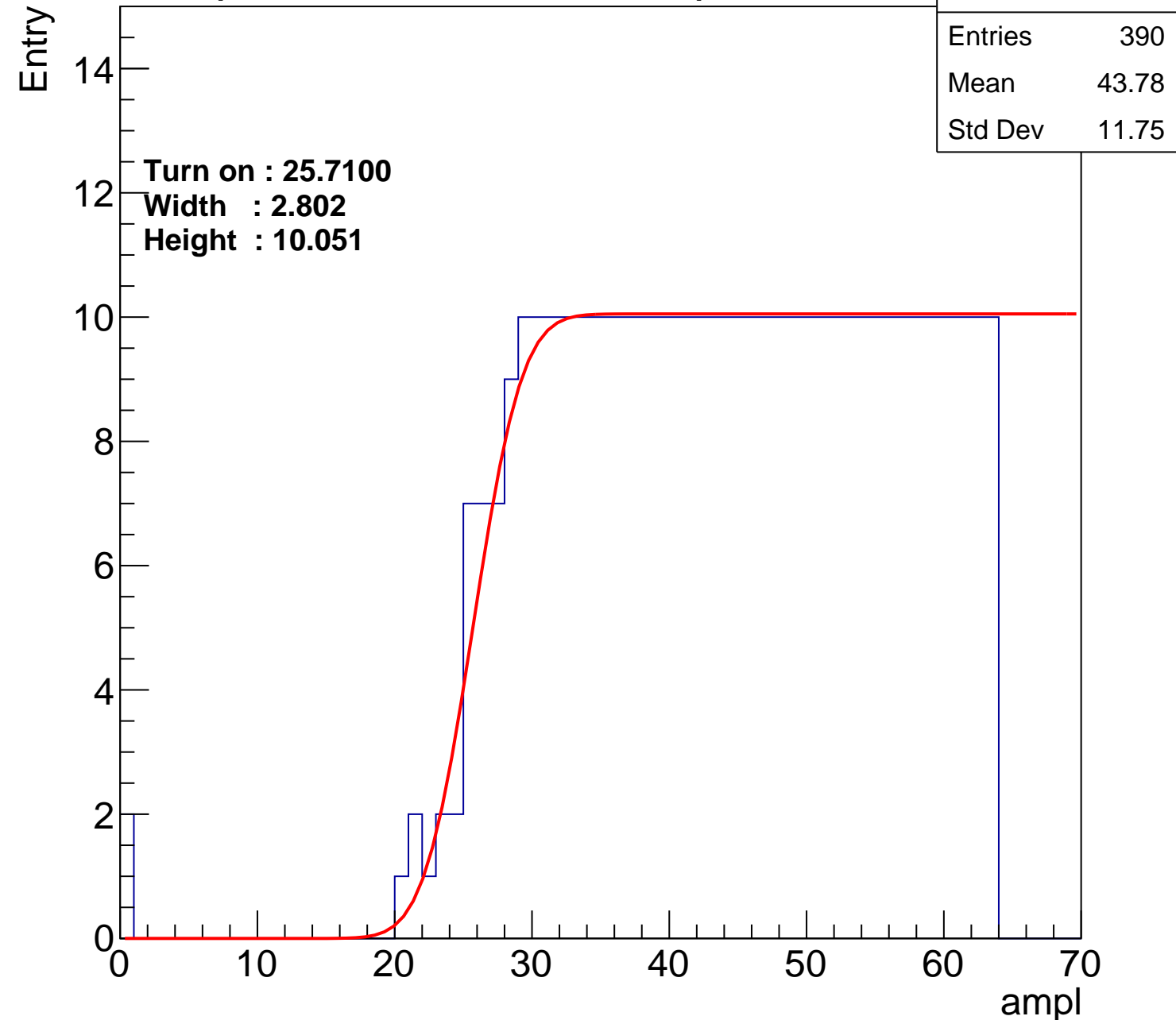
Width : 2.802

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch60

calib_packv5_042523_0143.root, FC#3, port B3

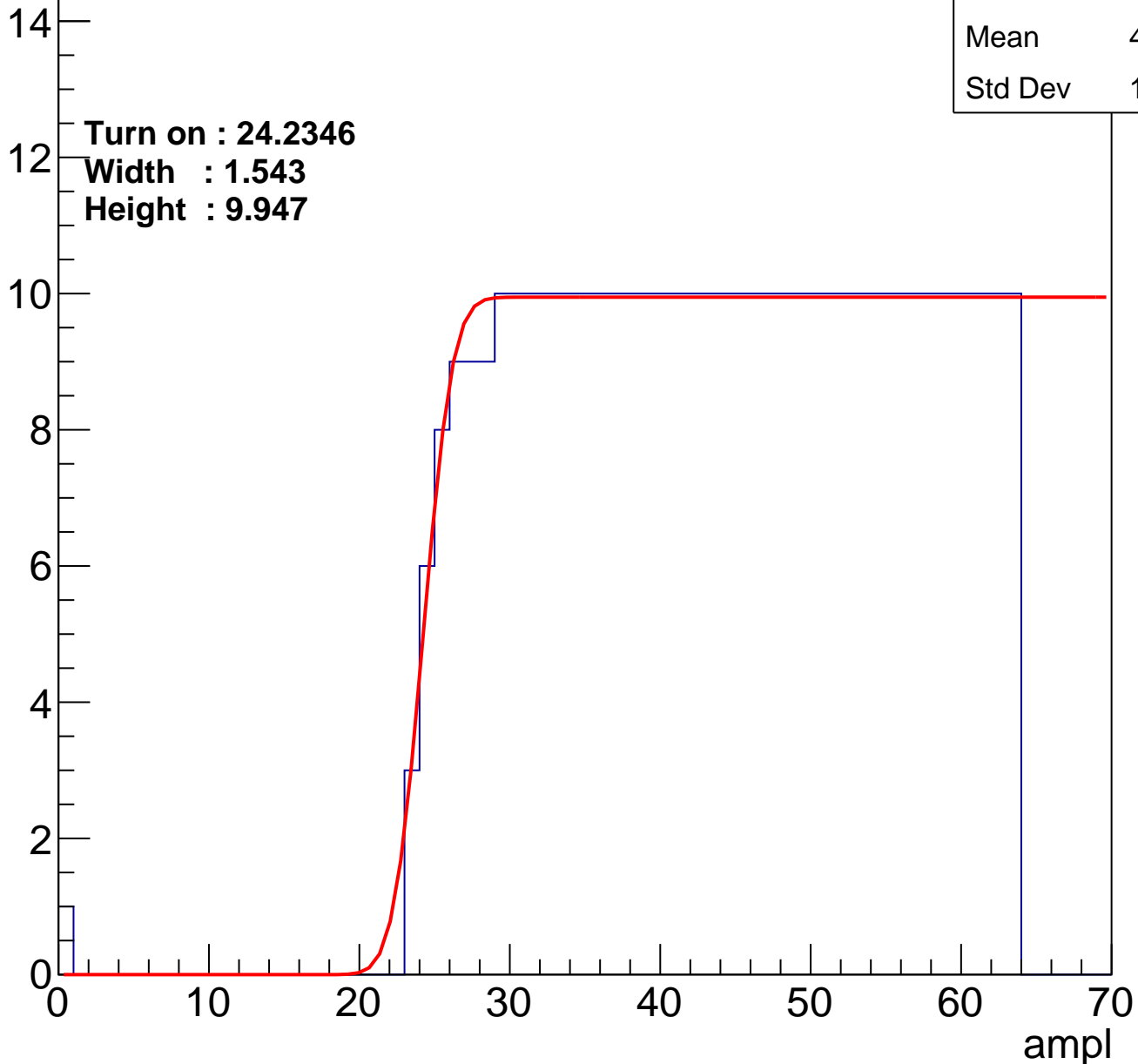
Entries	395
Mean	43.65
Std Dev	11.63

Turn on : 24.2346

Width : 1.543

Height : 9.947

Entry



B1L000S, U14-ch61

calib_packv5_042523_0143.root, FC#3, port B3

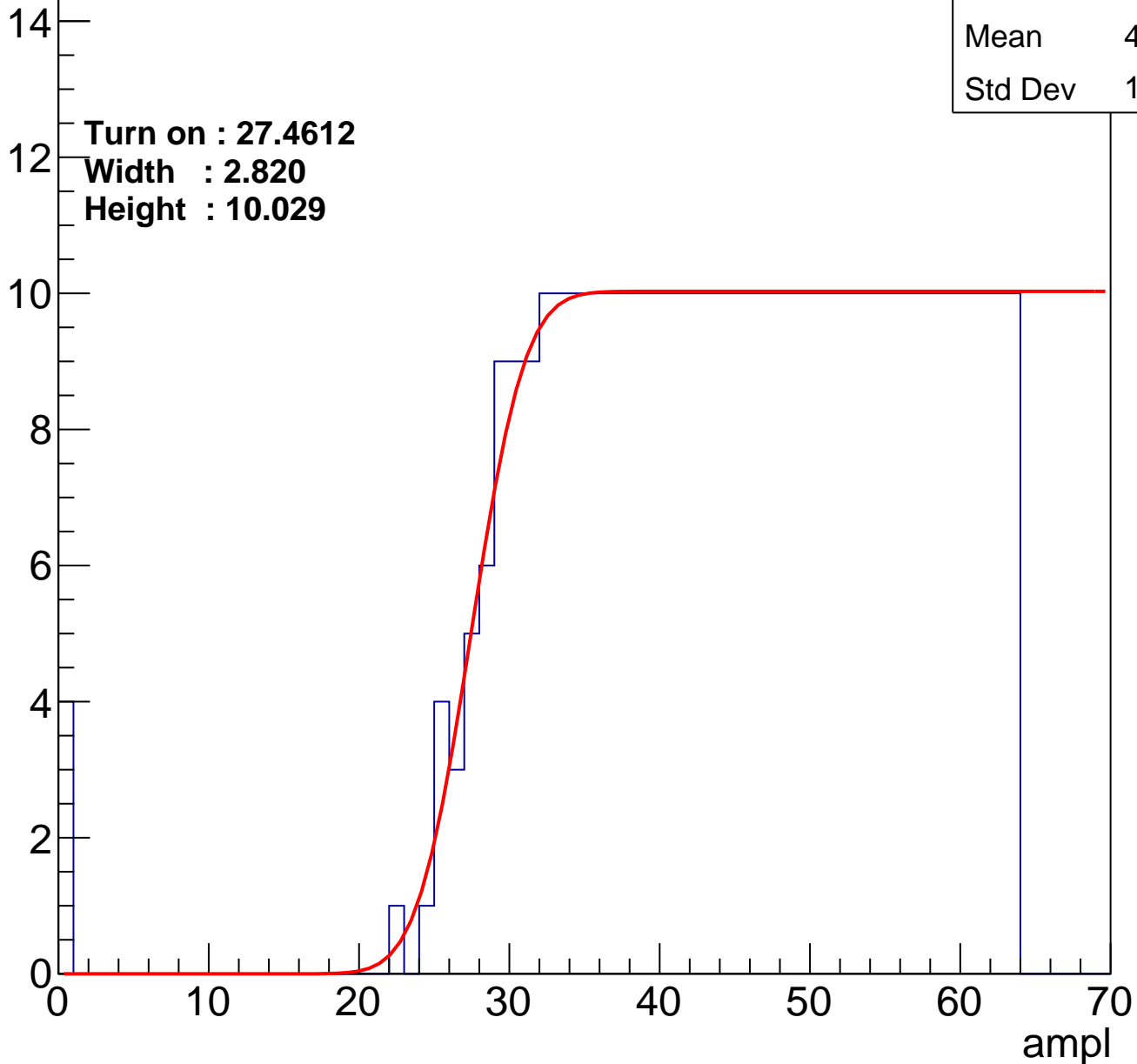
Entries	371
Mean	44.57
Std Dev	11.66

Turn on : 27.4612

Width : 2.820

Height : 10.029

Entry



B1L000S, U14-ch62

calib_packv5_042523_0143.root, FC#3, port B3

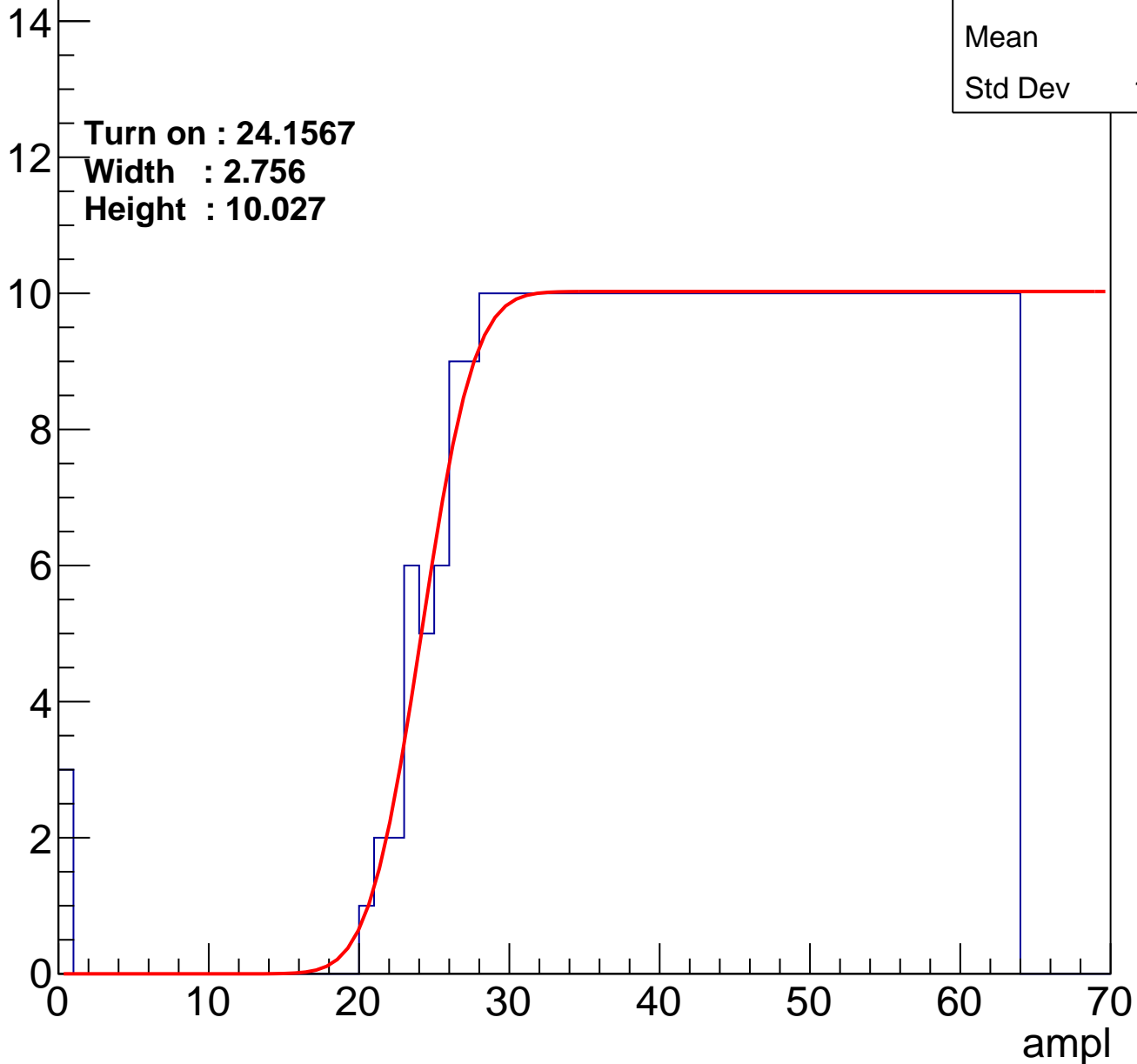
Entries	403
Mean	43.1
Std Dev	12.21

Turn on : 24.1567

Width : 2.756

Height : 10.027

Entry



B1L000S, U14-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.03
Std Dev	12.38

Turn on : 24.3360

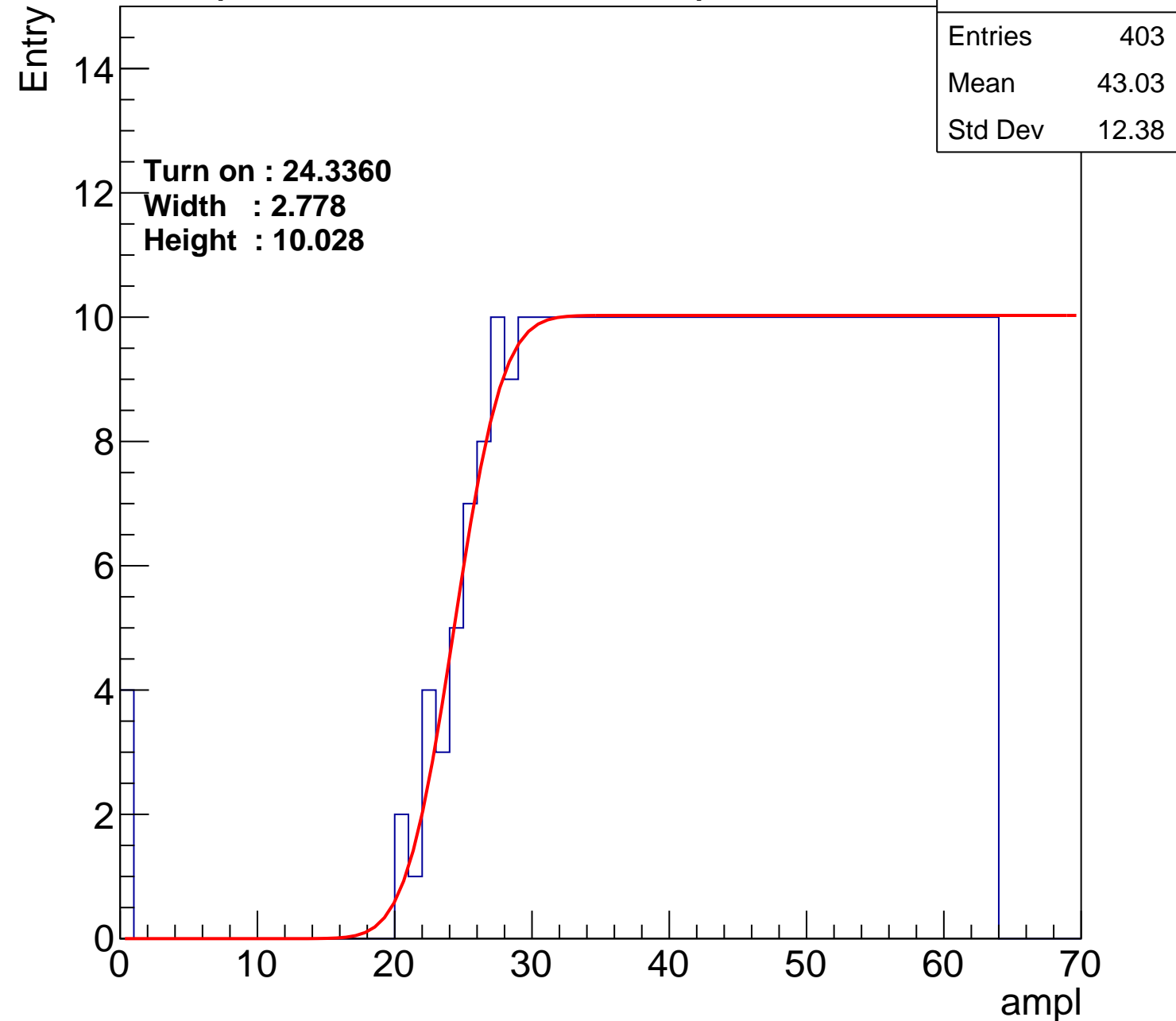
Width : 2.778

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.27
Std Dev	12.17

Turn on : 24.8592

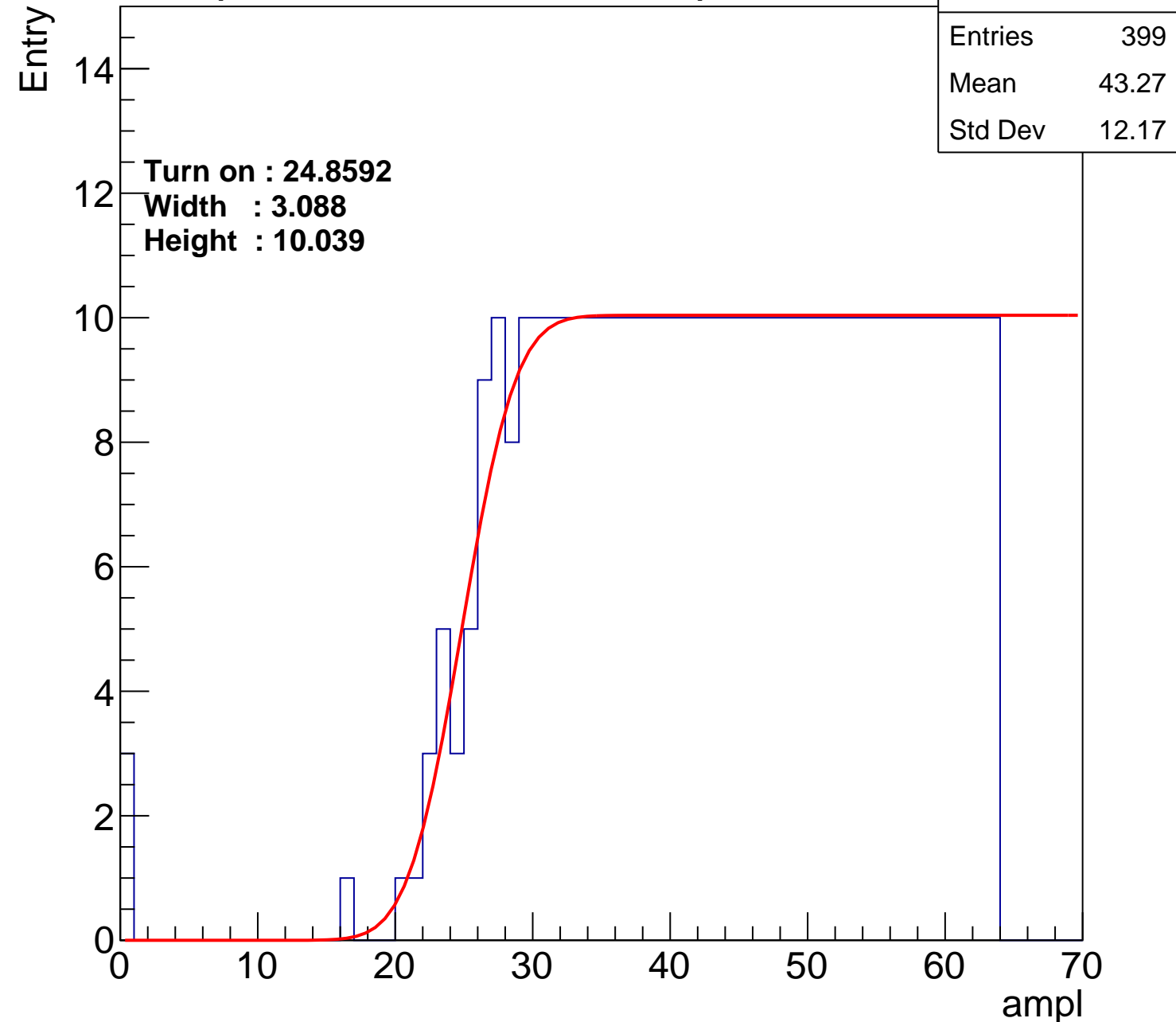
Width : 3.088

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 26.0948

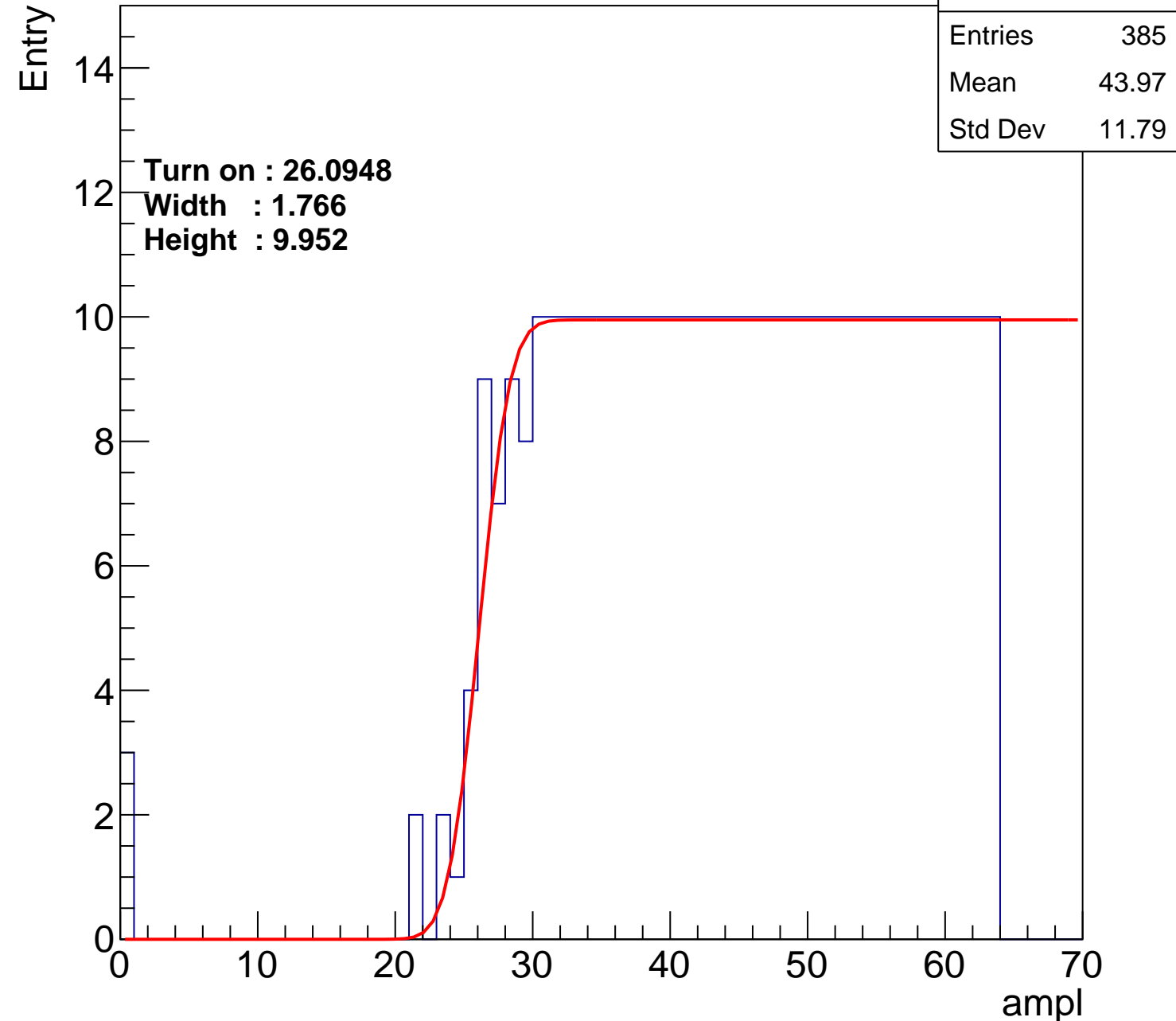
Width : 1.766

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.31
Std Dev	12.25

Turn on : 24.1514

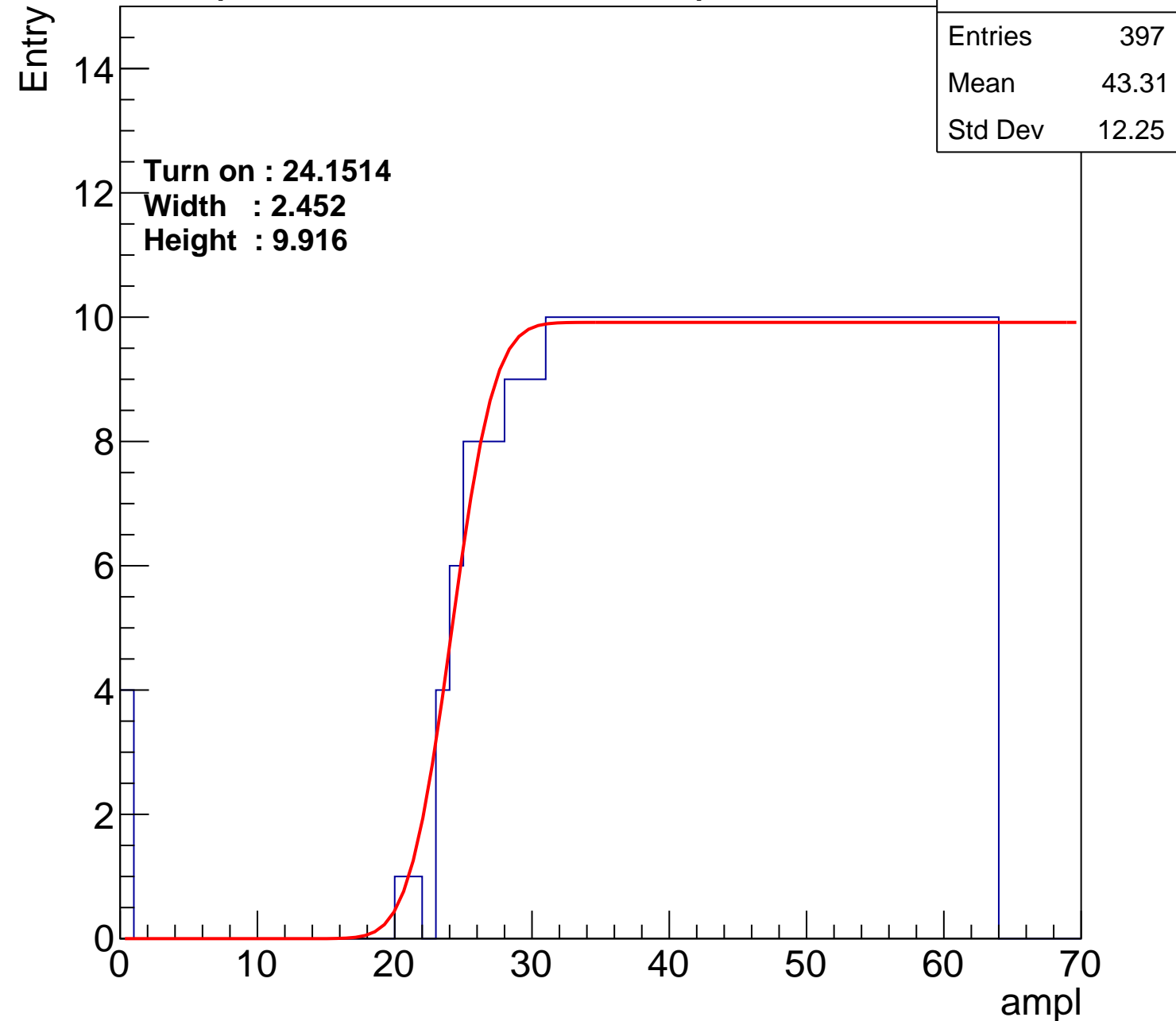
Width : 2.452

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch67

calib_packv5_042523_0143.root, FC#3, port B3

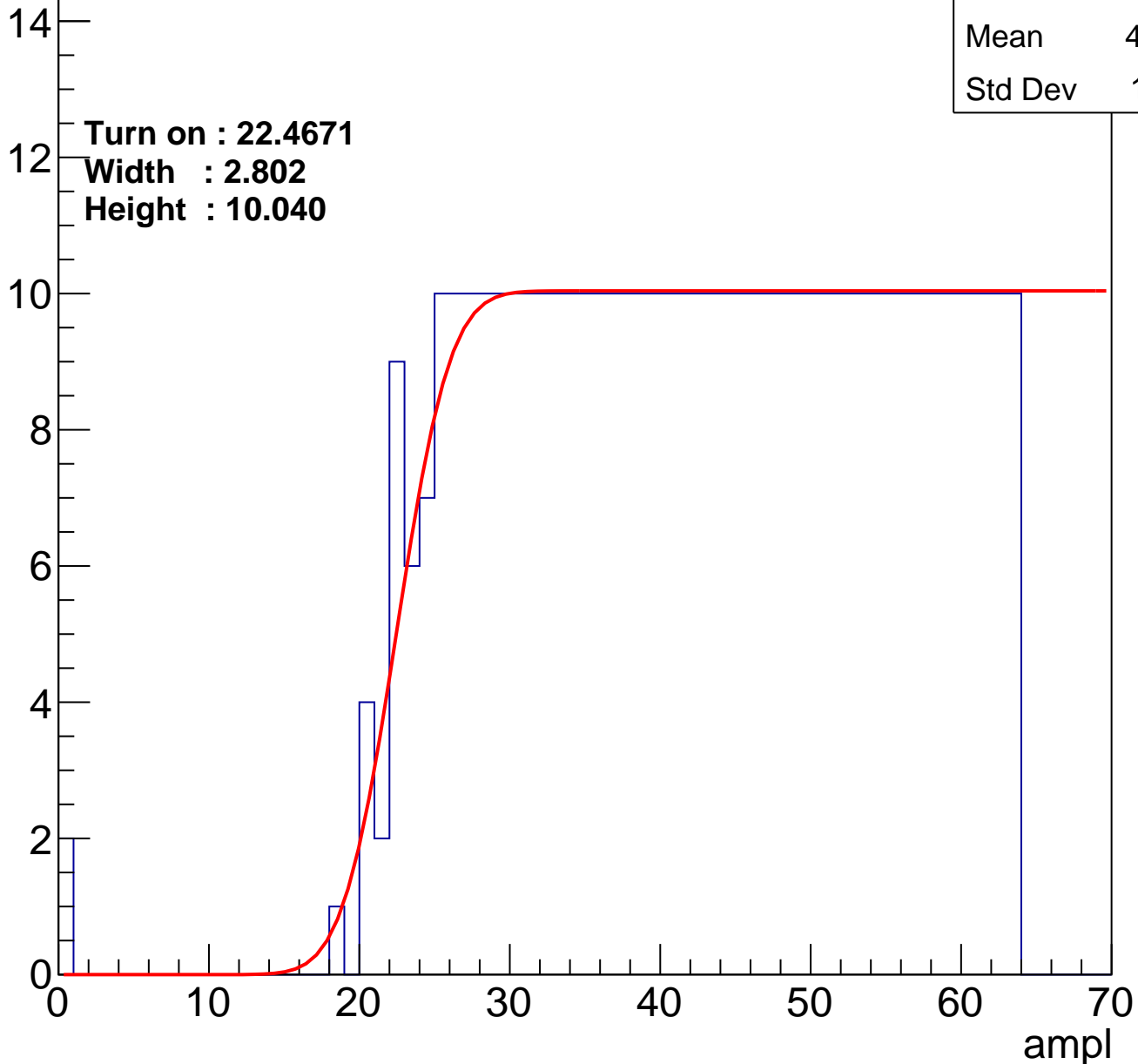
Entries	421
Mean	42.29
Std Dev	12.51

Turn on : 22.4671

Width : 2.802

Height : 10.040

Entry



B1L000S, U14-ch68

calib_packv5_042523_0143.root, FC#3, port B3

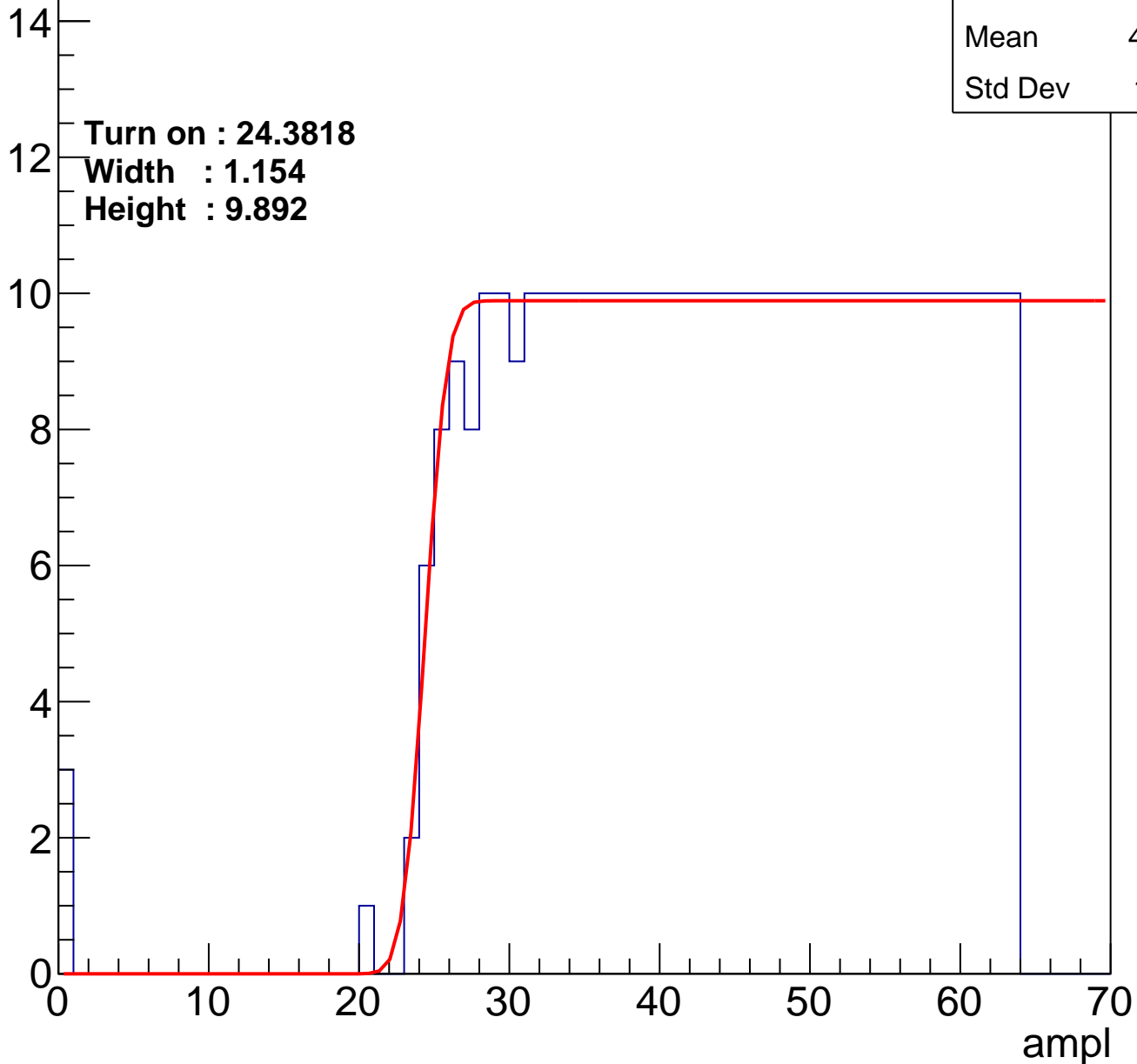
Entries	396
Mean	43.46
Std Dev	12.01

Turn on : 24.3818

Width : 1.154

Height : 9.892

Entry



B1L000S, U14-ch69

calib_packv5_042523_0143.root, FC#3, port B3

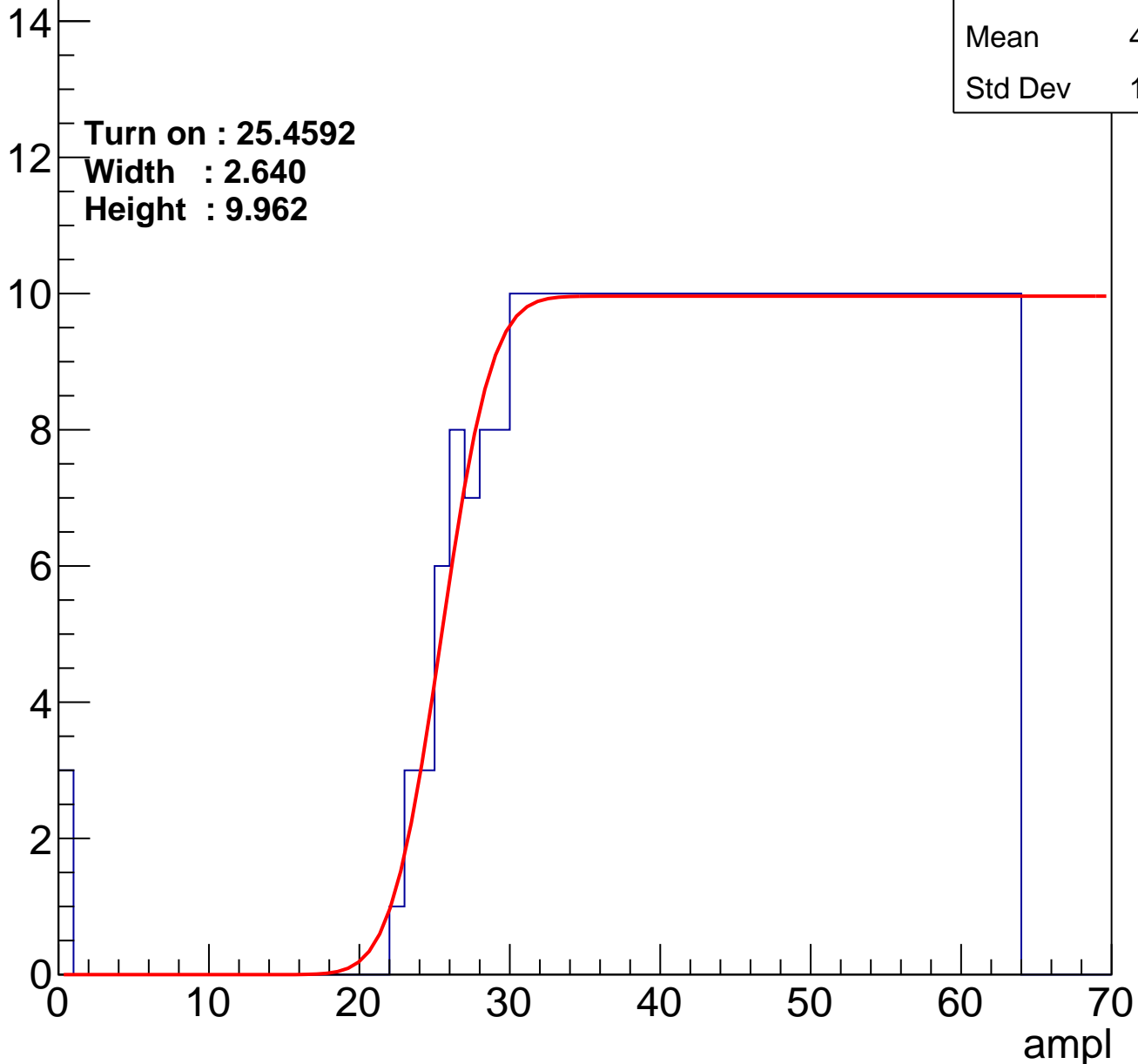
Entries	387
Mean	43.87
Std Dev	11.84

Turn on : 25.4592

Width : 2.640

Height : 9.962

Entry



B1L000S, U14-ch70

calib_packv5_042523_0143.root, FC#3, port B3

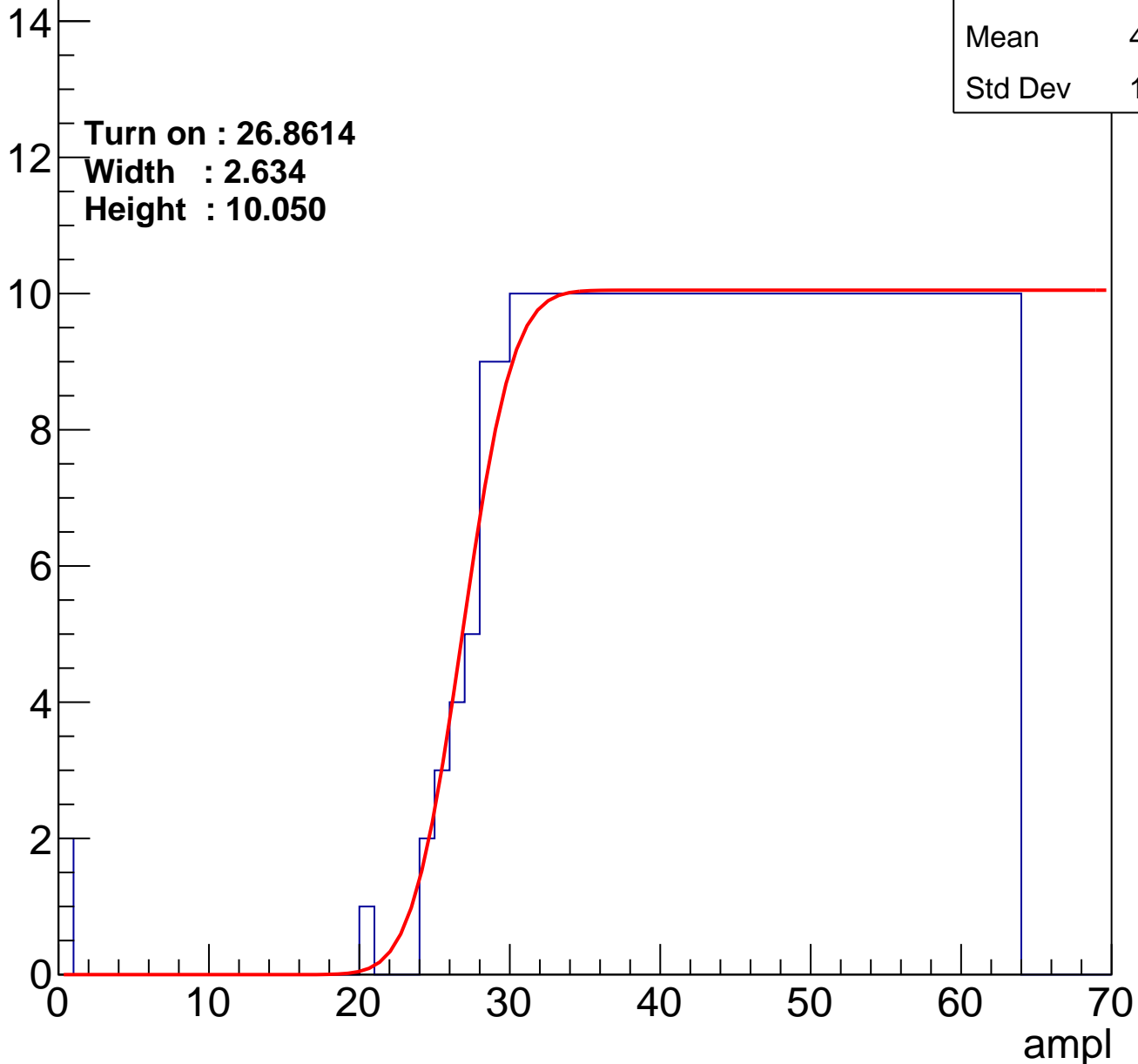
Entries	375
Mean	44.55
Std Dev	11.33

Turn on : 26.8614

Width : 2.634

Height : 10.050

Entry



B1L000S, U14-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.67
Std Dev	11.83

Turn on : 25.1840

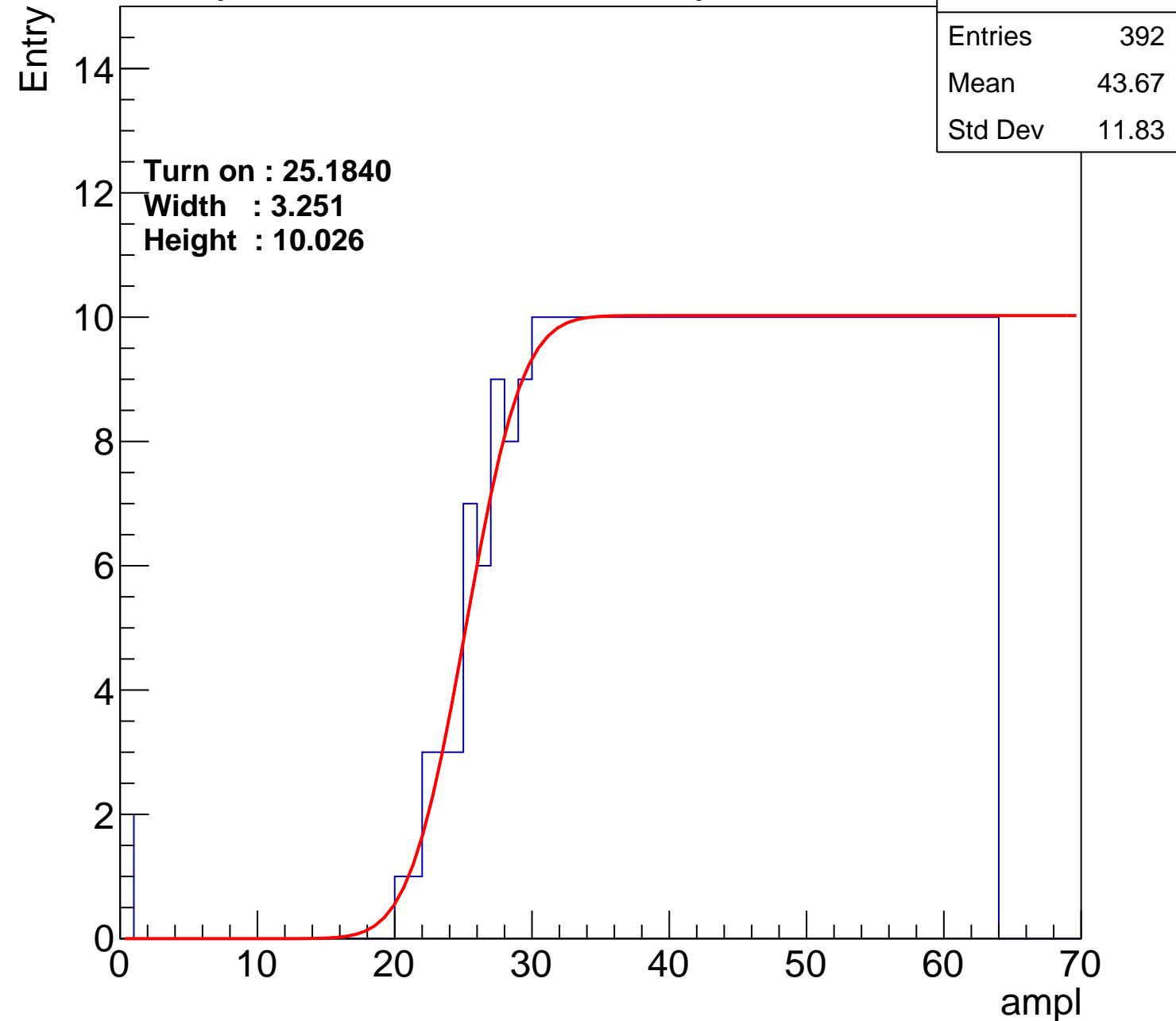
Width : 3.251

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch72

calib_packv5_042523_0143.root, FC#3, port B3

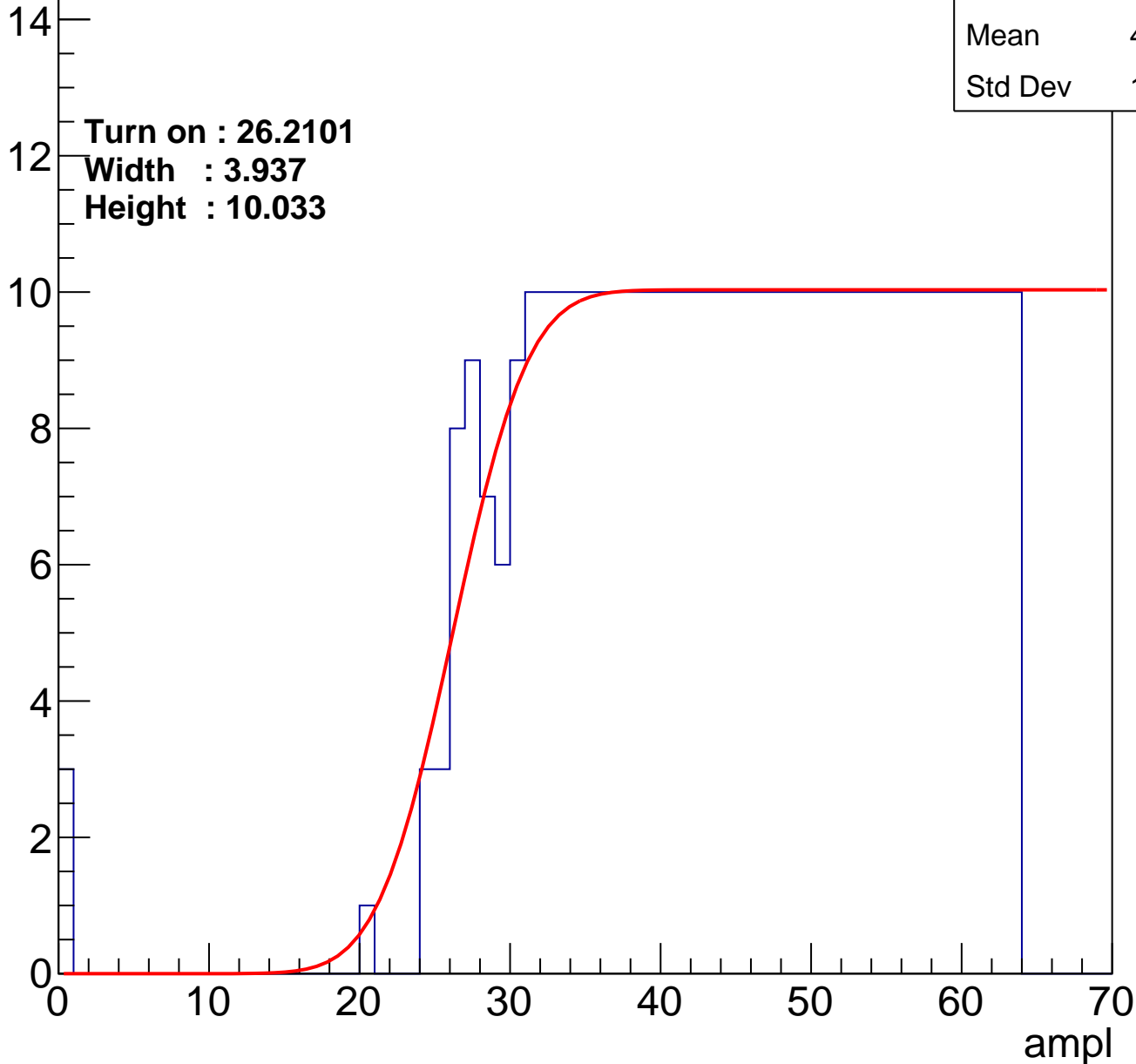
Entries	379
Mean	44.24
Std Dev	11.67

Turn on : 26.2101

Width : 3.937

Height : 10.033

Entry



B1L000S, U14-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.28
Std Dev	12.25

Turn on : 24.7519

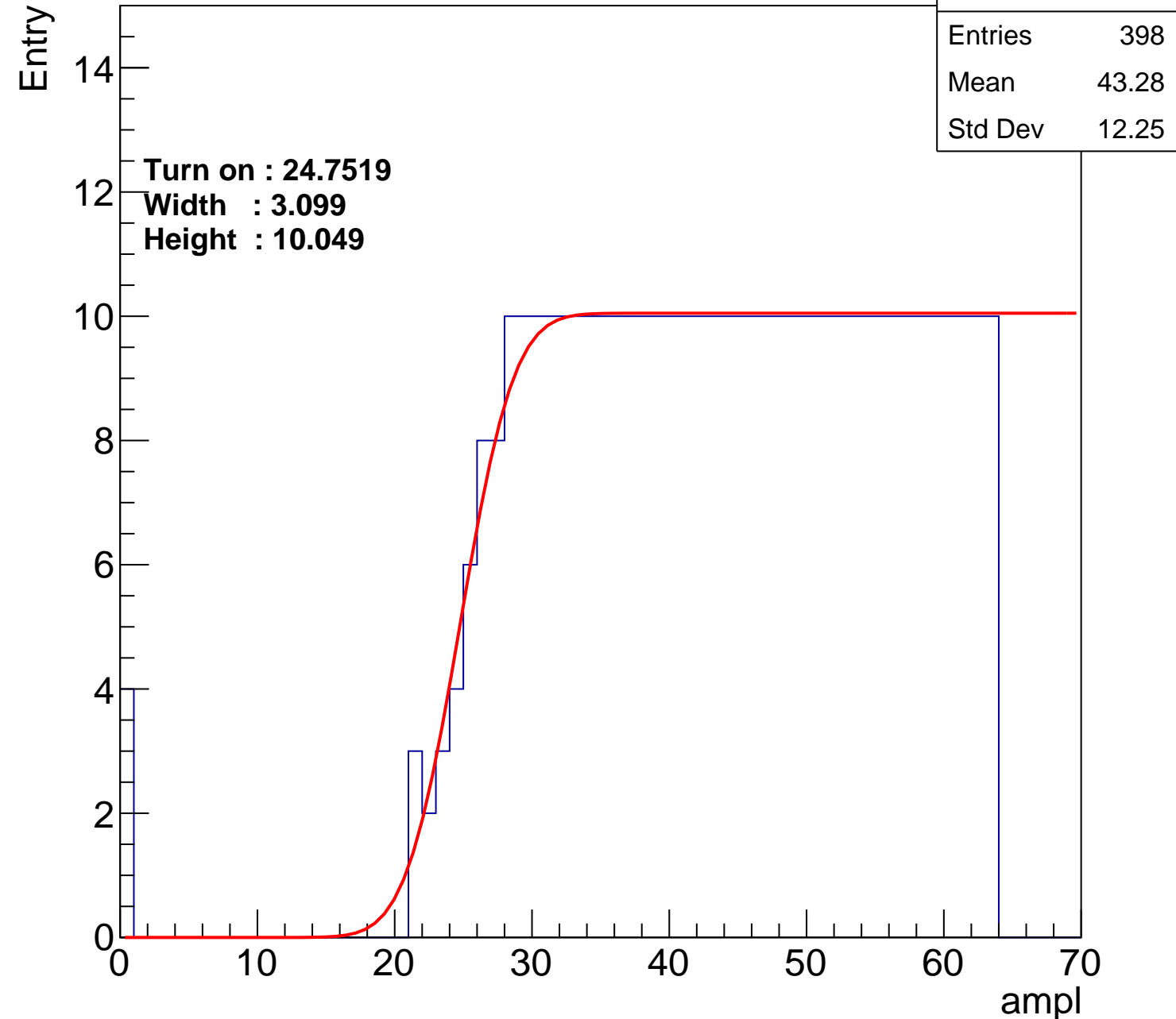
Width : 3.099

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.53
Std Dev	11.72

Turn on : 24.6727

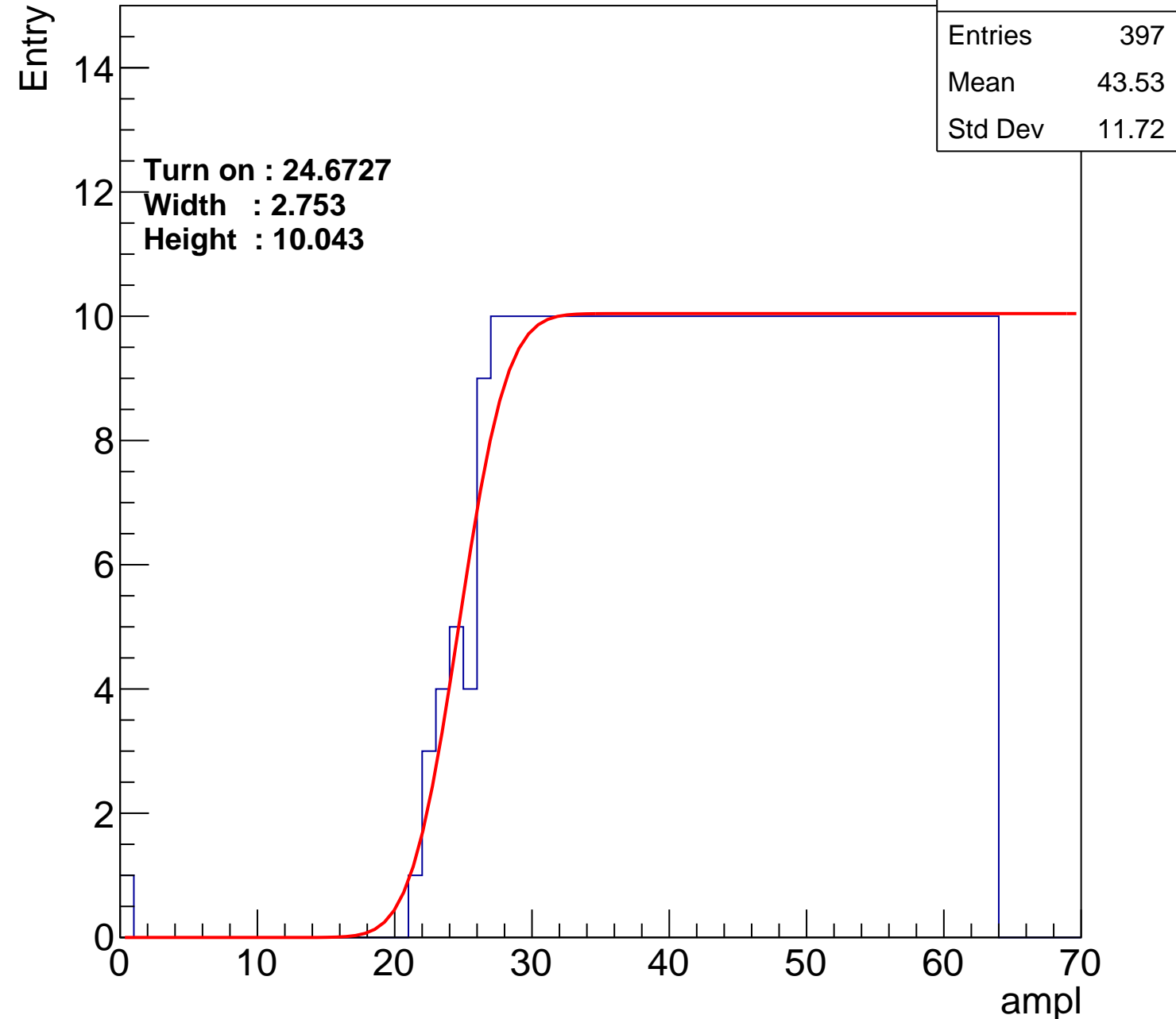
Width : 2.753

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.77
Std Dev	12.02

Turn on : 25.2429

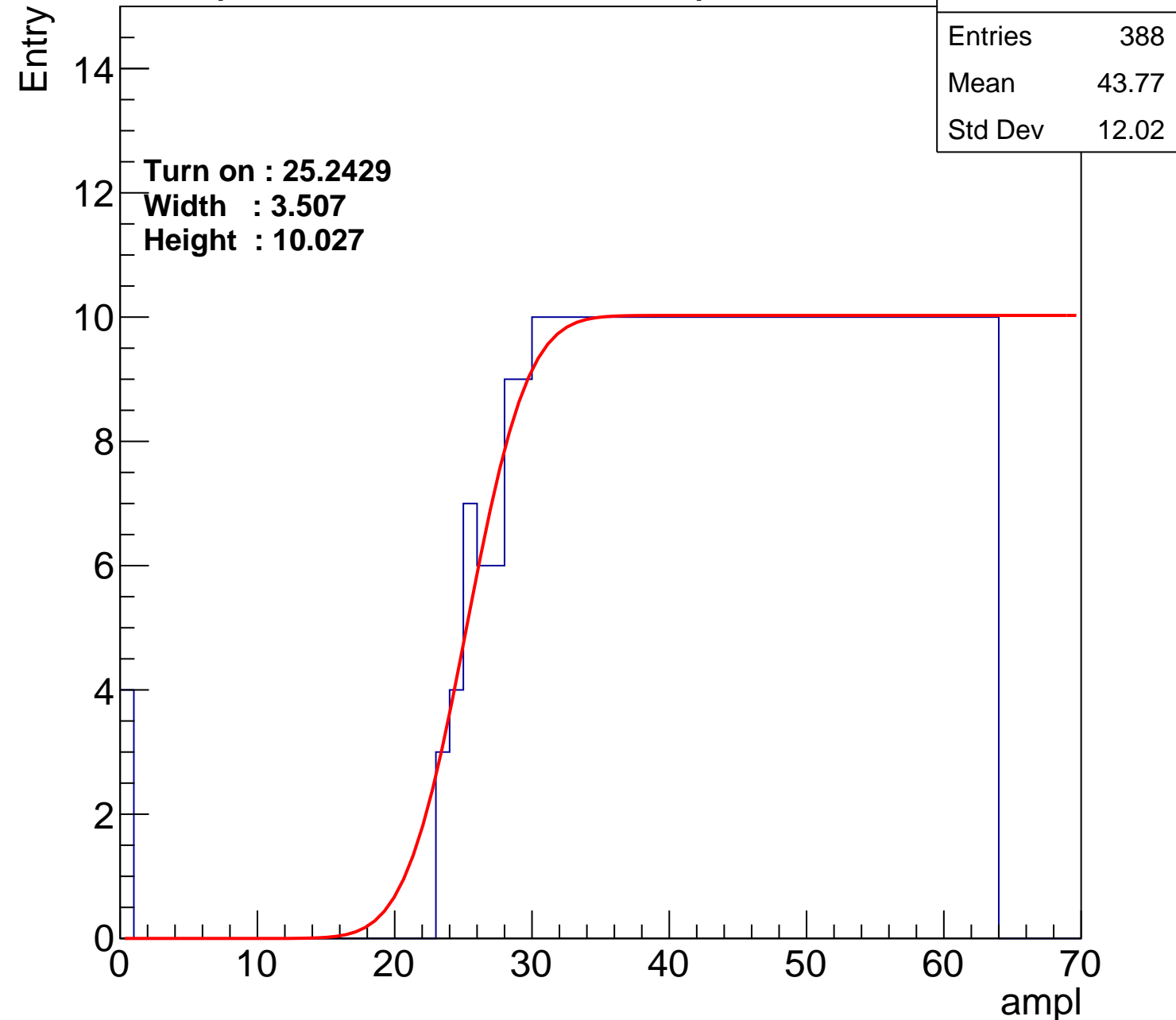
Width : 3.507

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch76

calib_packv5_042523_0143.root, FC#3, port B3

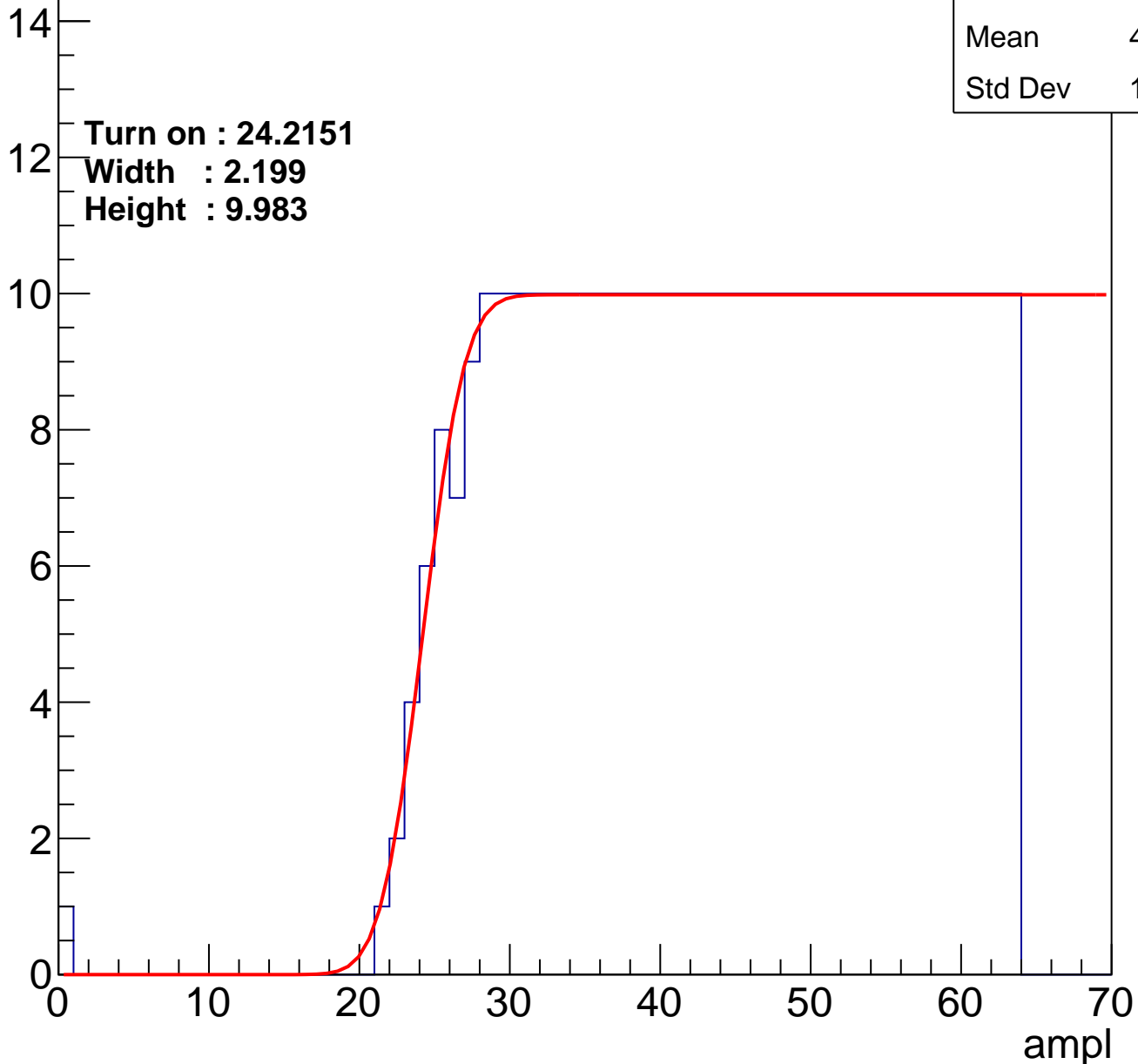
Entries	398
Mean	43.48
Std Dev	11.75

Turn on : 24.2151

Width : 2.199

Height : 9.983

Entry



B1L000S, U14-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.89
Std Dev	11.73

Turn on : 25.7401

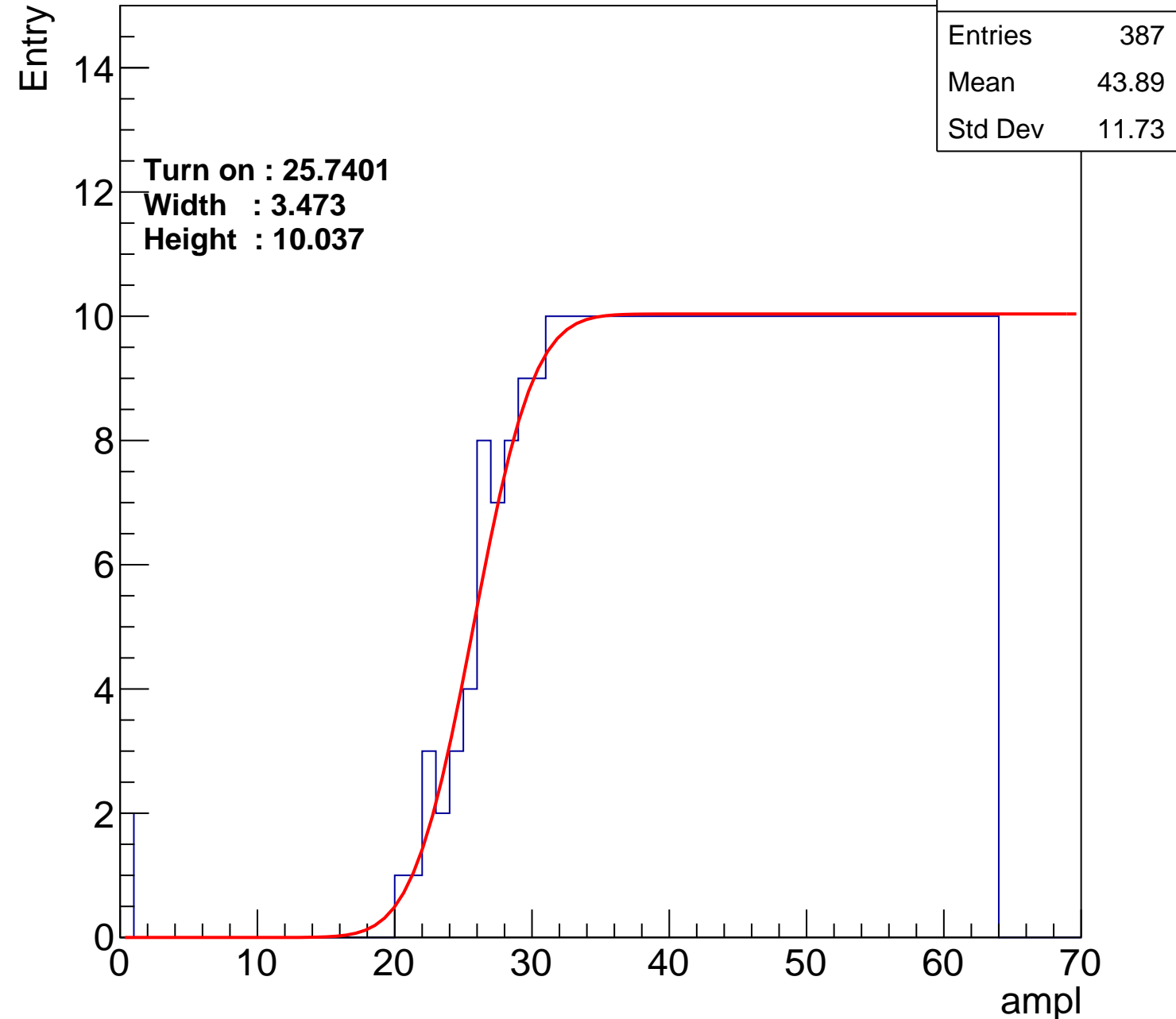
Width : 3.473

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch78

calib_packv5_042523_0143.root, FC#3, port B3

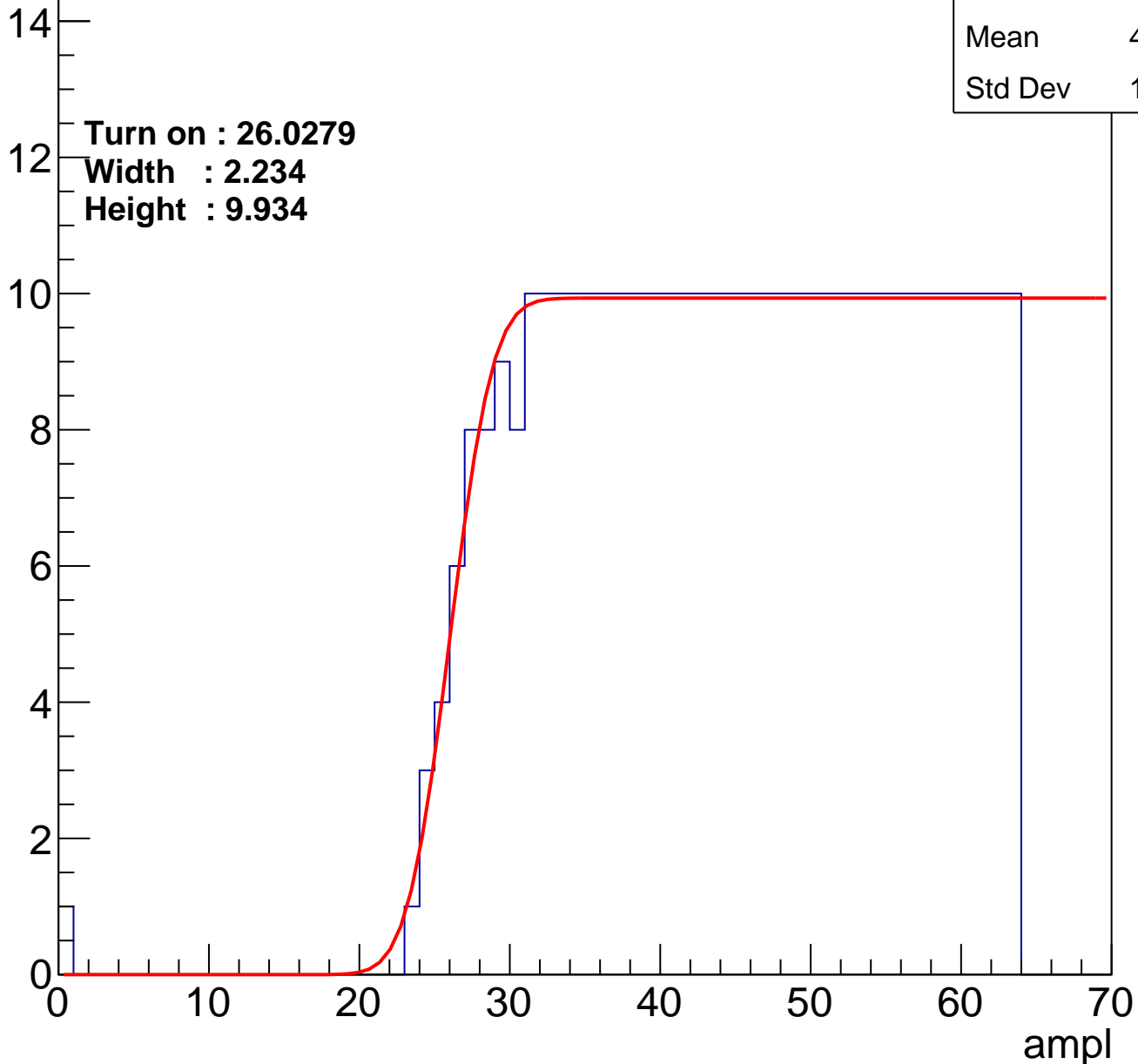
Entries	378
Mean	44.45
Std Dev	11.24

Turn on : 26.0279

Width : 2.234

Height : 9.934

Entry



B1L000S, U14-ch79

calib_packv5_042523_0143.root, FC#3, port B3

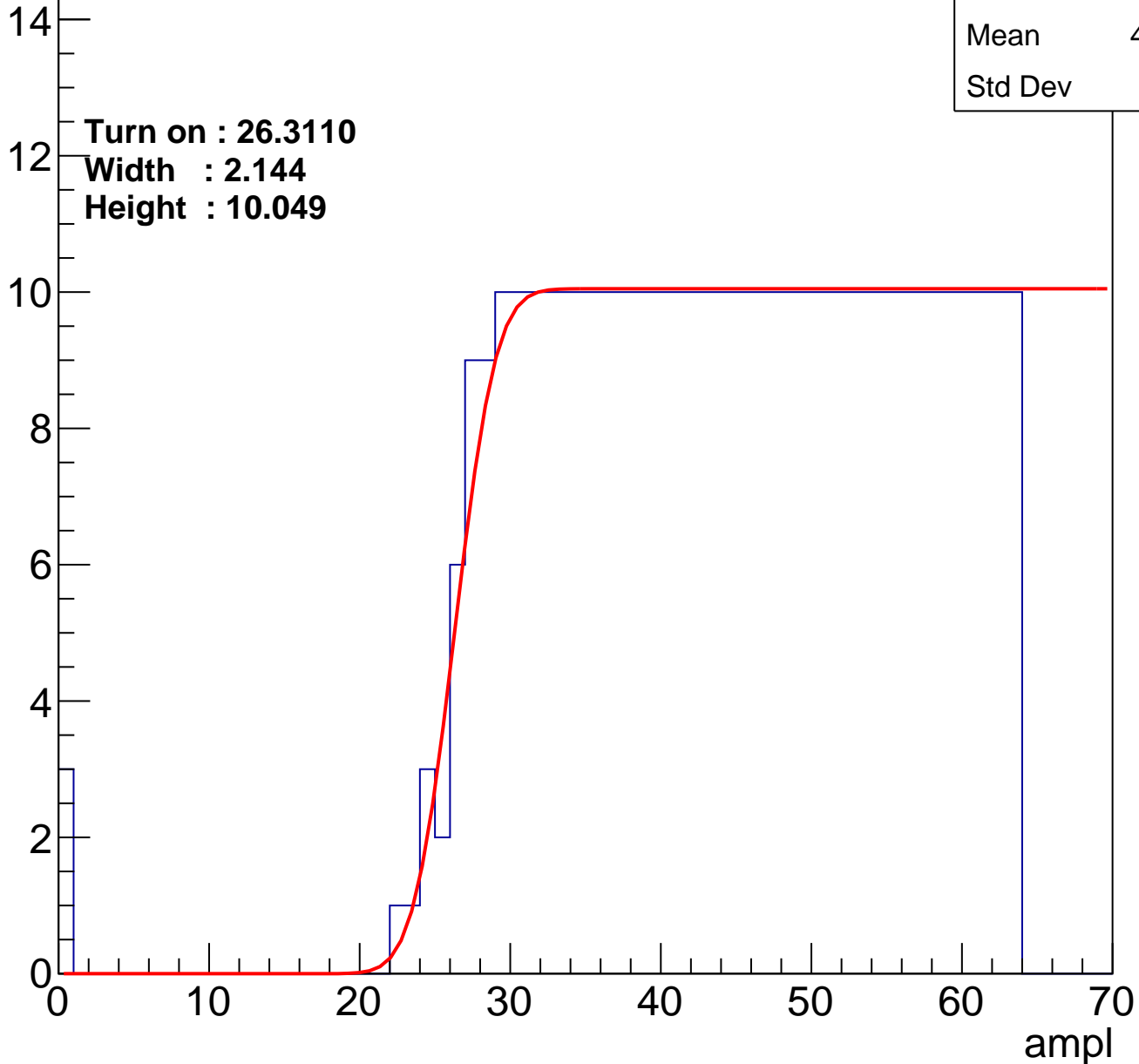
Entries	384
Mean	44.06
Std Dev	11.7

Turn on : 26.3110

Width : 2.144

Height : 10.049

Entry



B1L000S, U14-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.42
Std Dev	11.48

Turn on : 26.8535

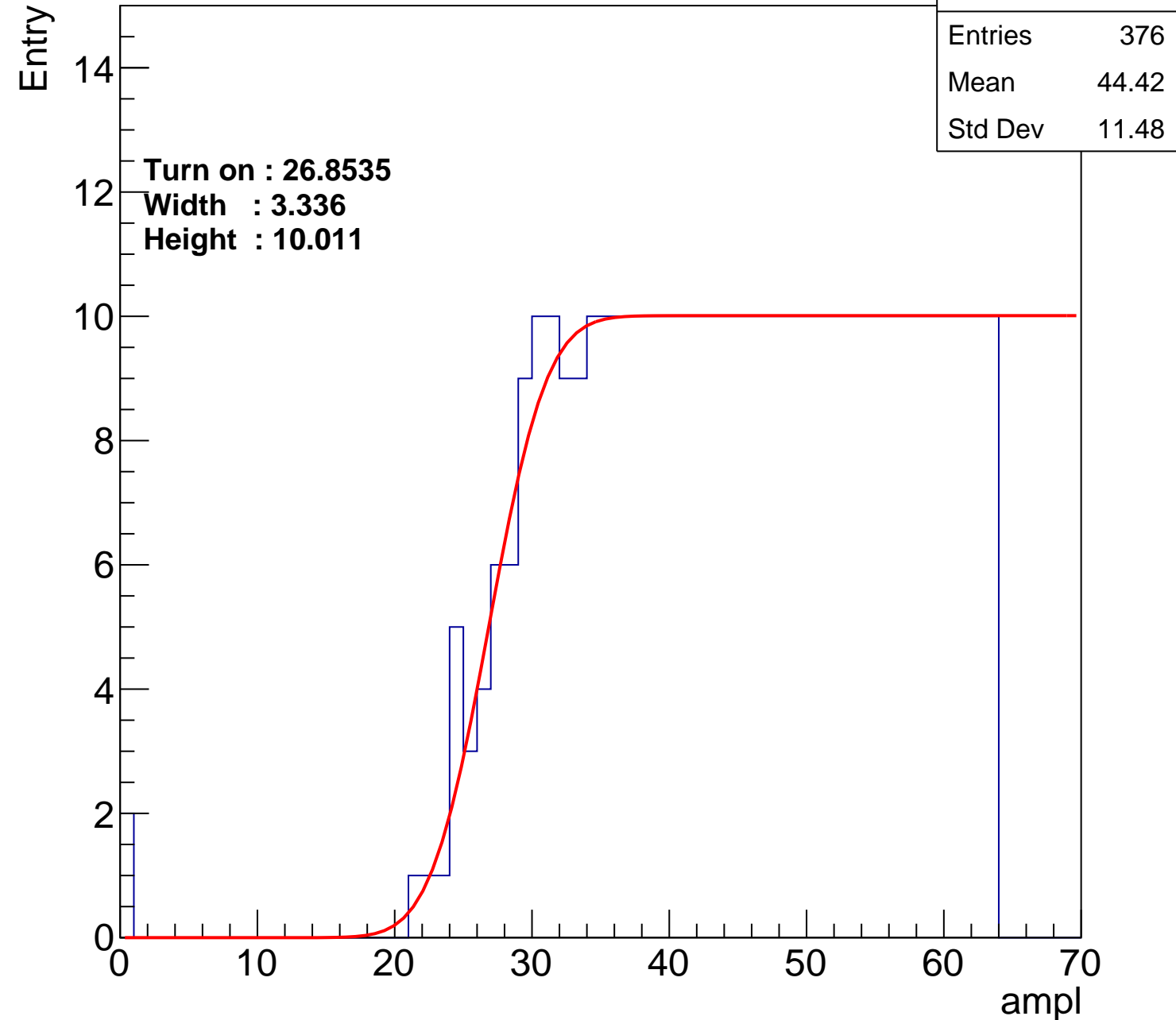
Width : 3.336

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch81

calib_packv5_042523_0143.root, FC#3, port B3

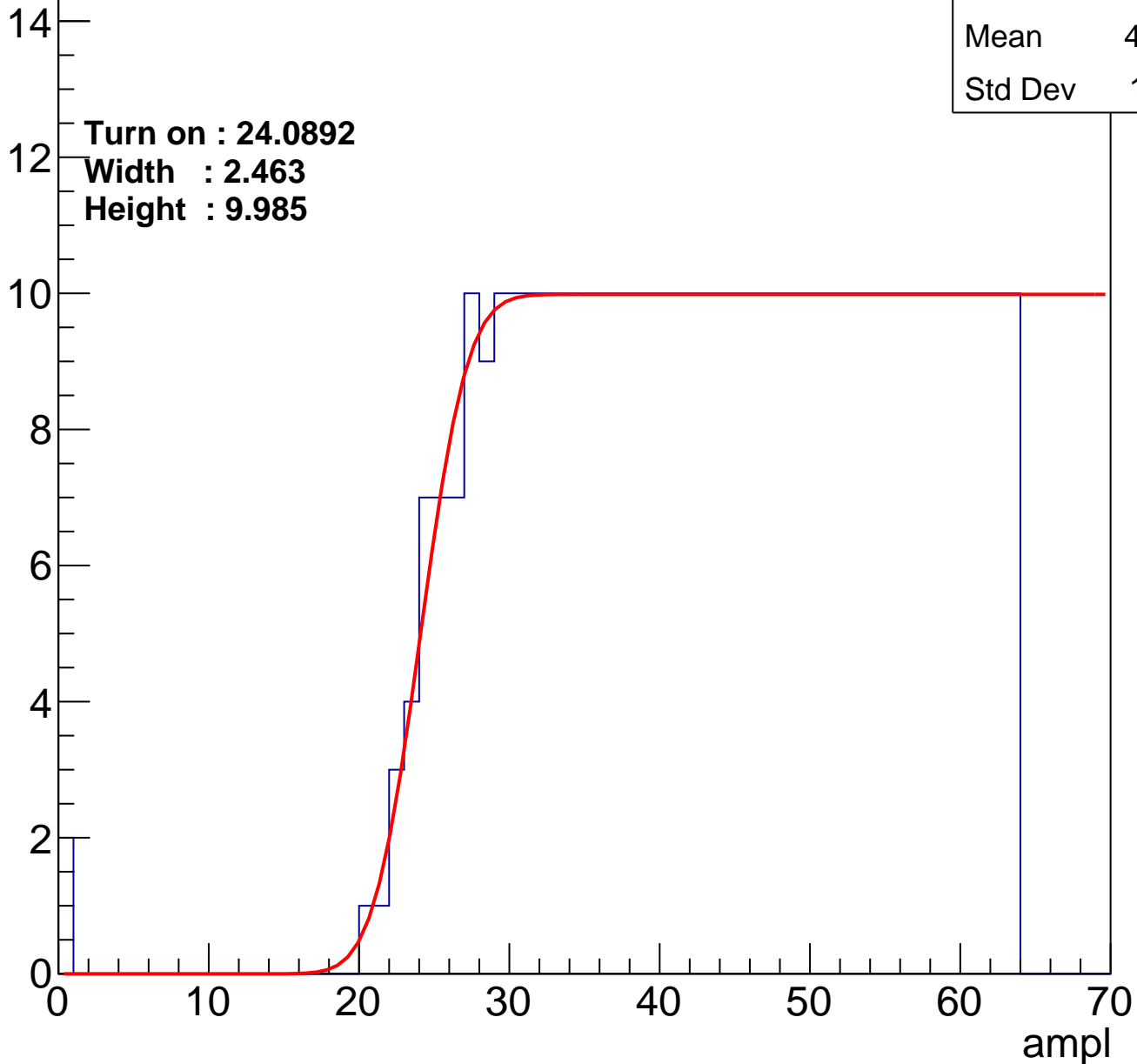
Entries	401
Mean	43.26
Std Dev	12.01

Turn on : 24.0892

Width : 2.463

Height : 9.985

Entry



B1L000S, U14-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.39
Std Dev	11.84

Turn on : 24.6626

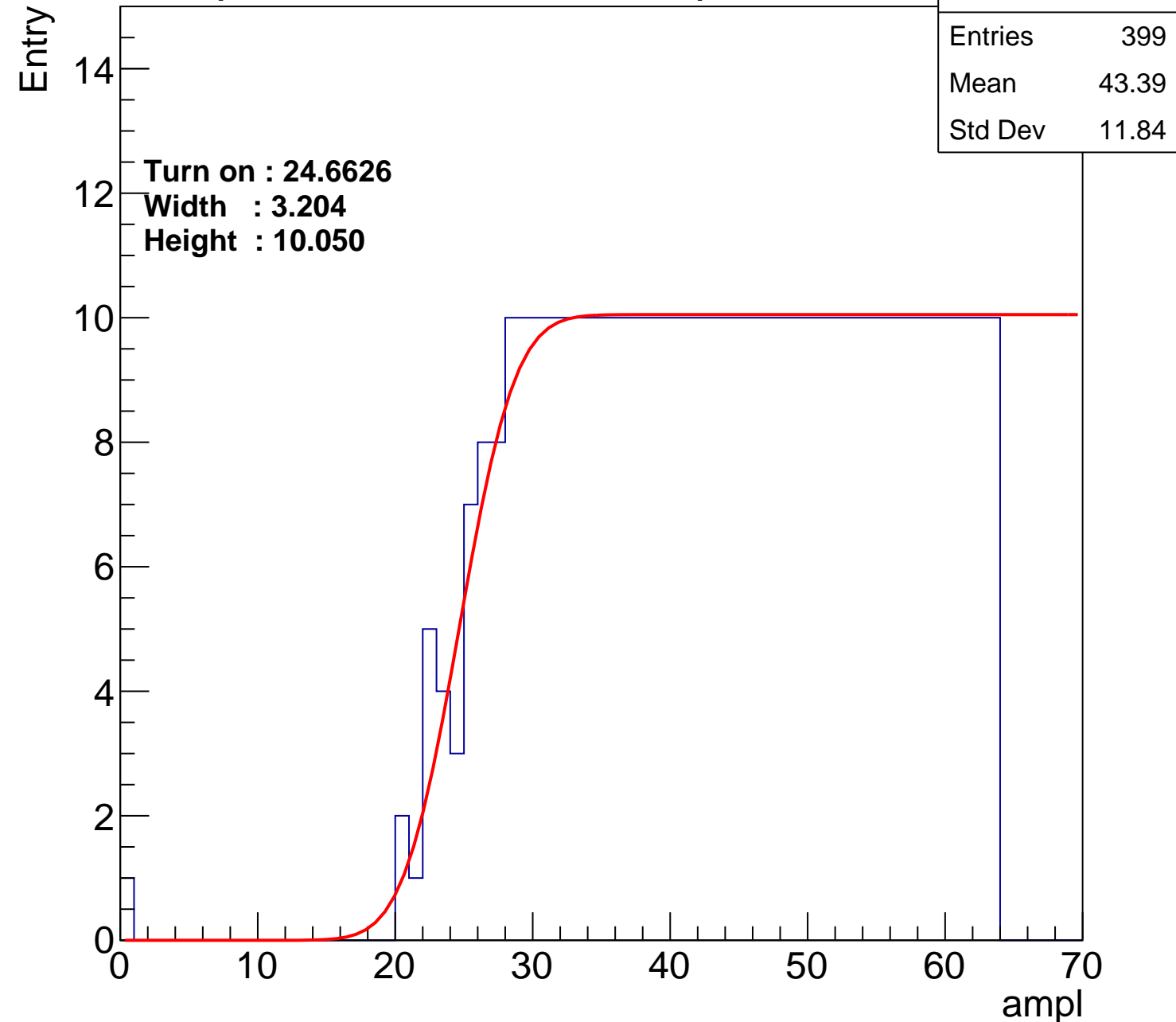
Width : 3.204

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch83

calib_packv5_042523_0143.root, FC#3, port B3

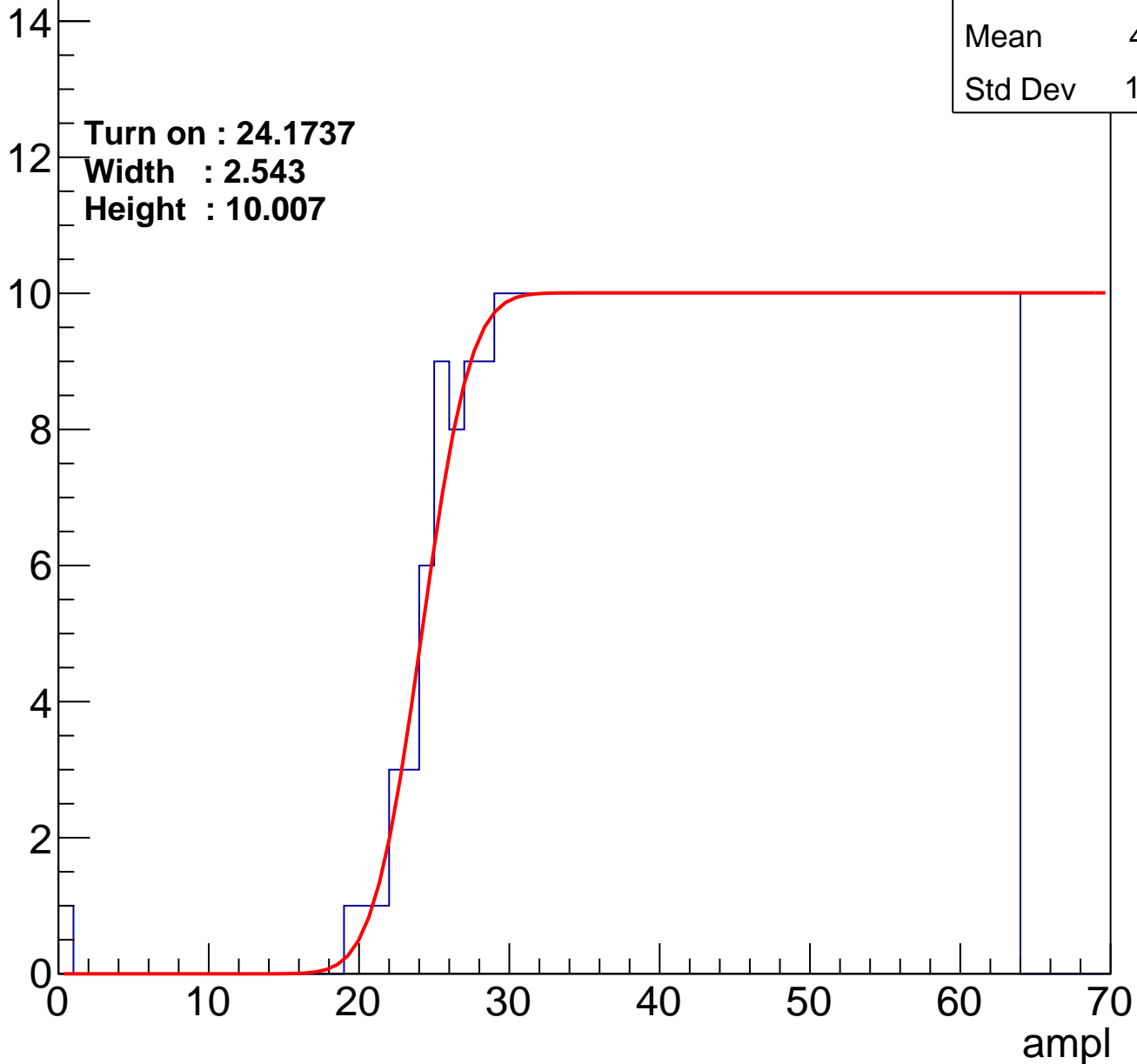
Entries	401
Mean	43.31
Std Dev	11.87

Turn on : 24.1737

Width : 2.543

Height : 10.007

Entry



B1L000S, U14-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.6
Std Dev	12.24

Turn on : 25.8417

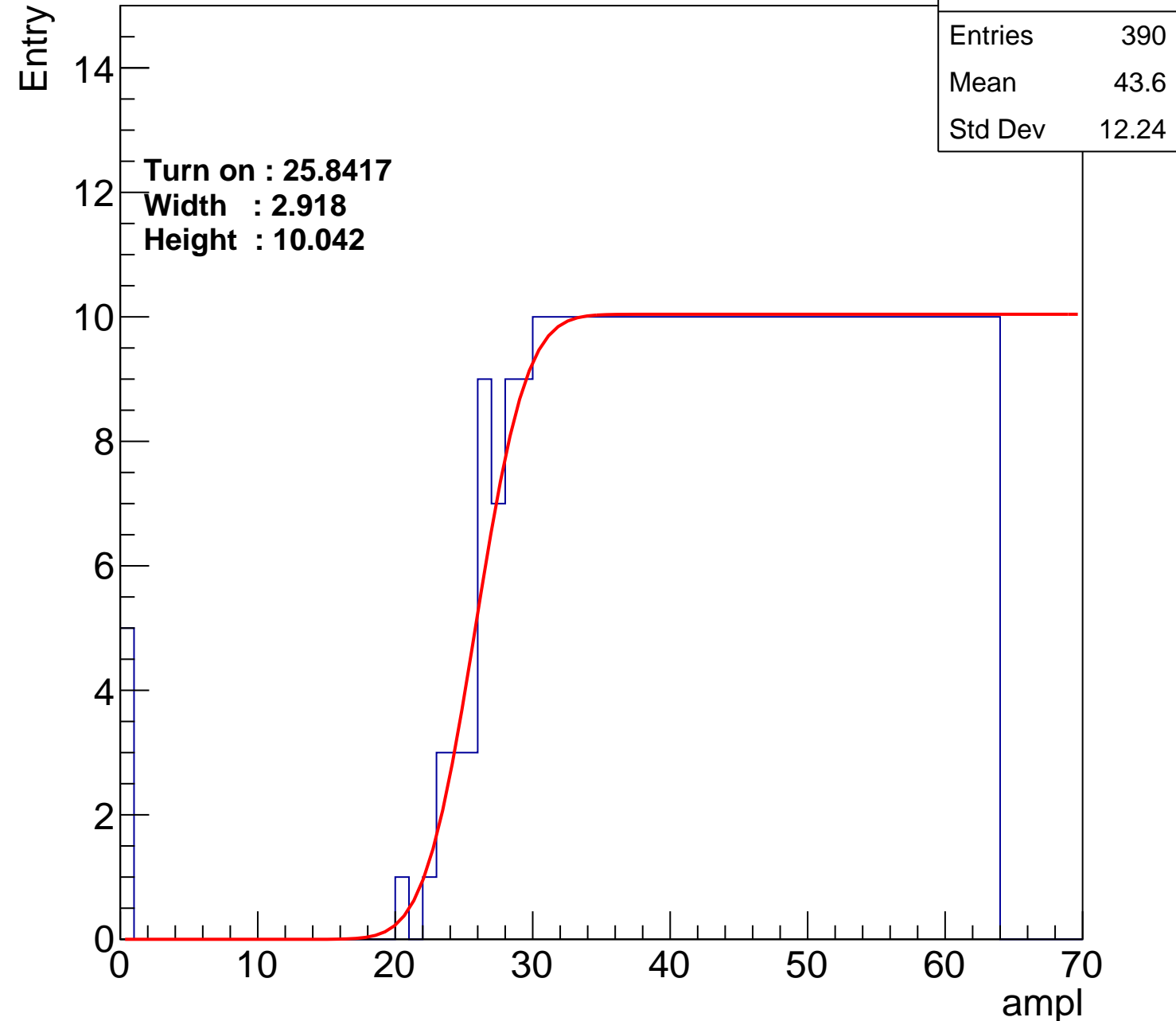
Width : 2.918

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.55
Std Dev	11.85

Turn on : 24.8034

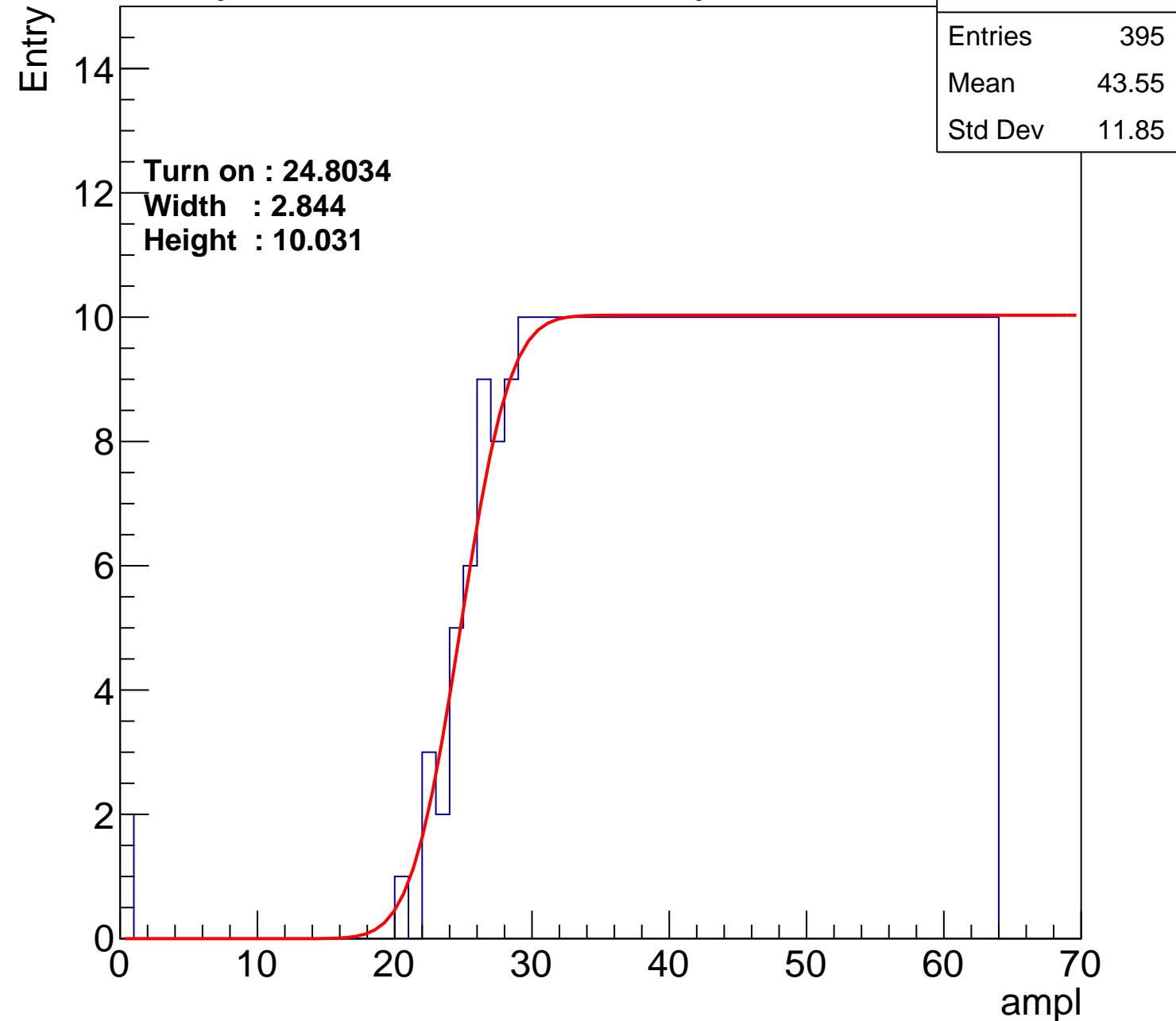
Width : 2.844

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch86

calib_packv5_042523_0143.root, FC#3, port B3

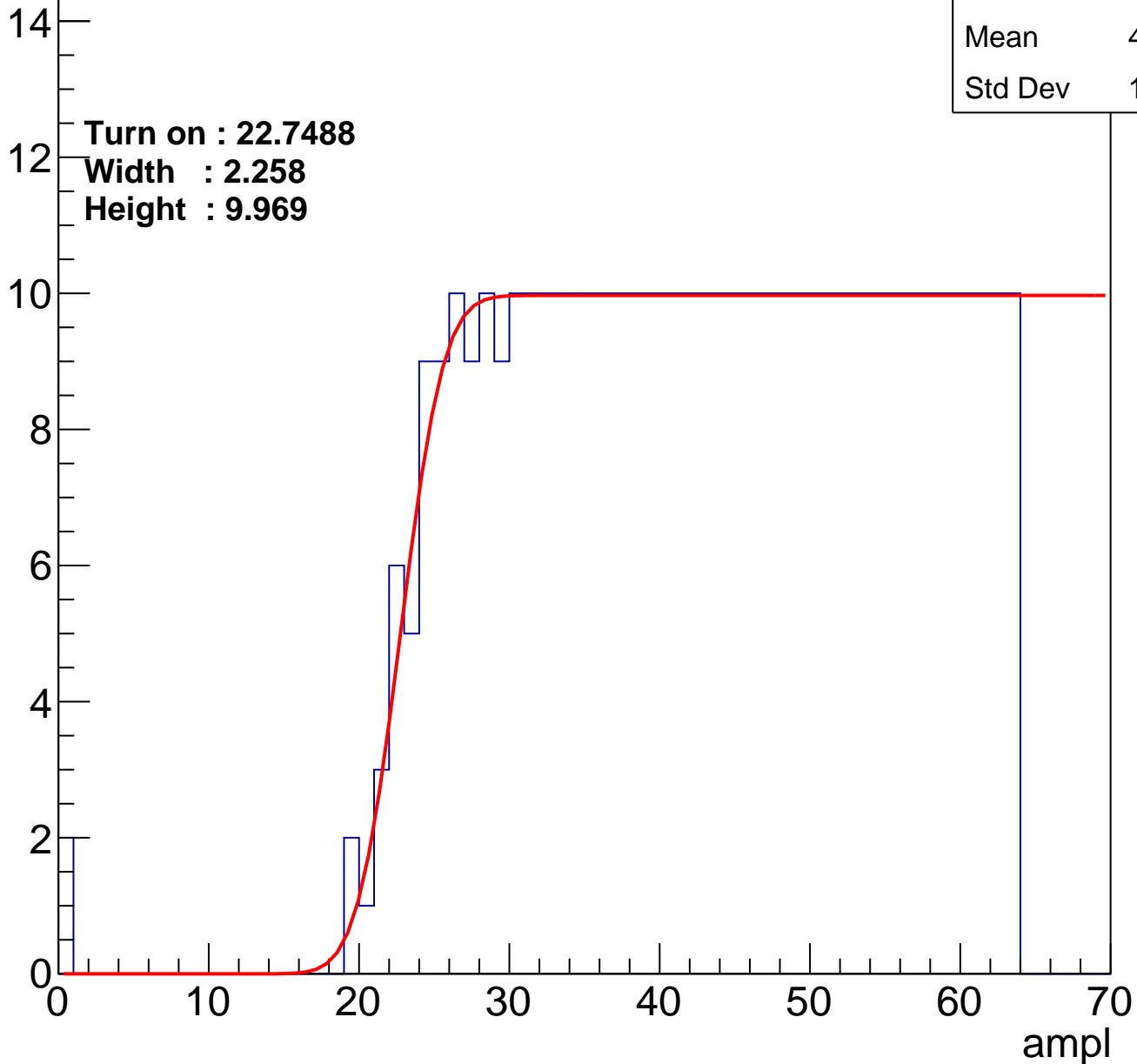
Entries	415
Mean	42.56
Std Dev	12.39

Turn on : 22.7488

Width : 2.258

Height : 9.969

Entry



B1L000S, U14-ch87

calib_packv5_042523_0143.root, FC#3, port B3

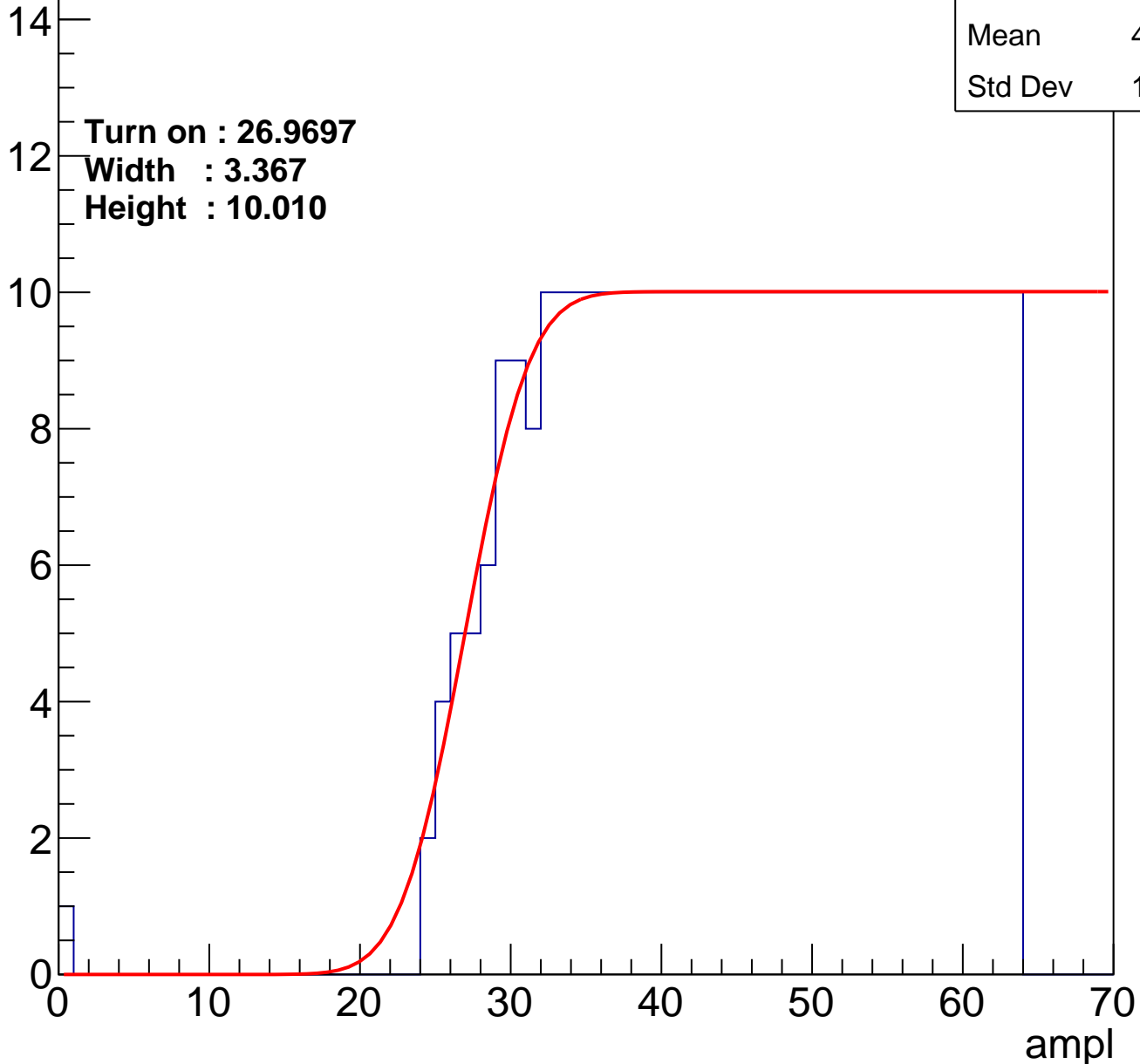
Entries	369
Mean	44.88
Std Dev	11.02

Turn on : 26.9697

Width : 3.367

Height : 10.010

Entry



B1L000S, U14-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.31
Std Dev	12.25

Turn on : 25.2782

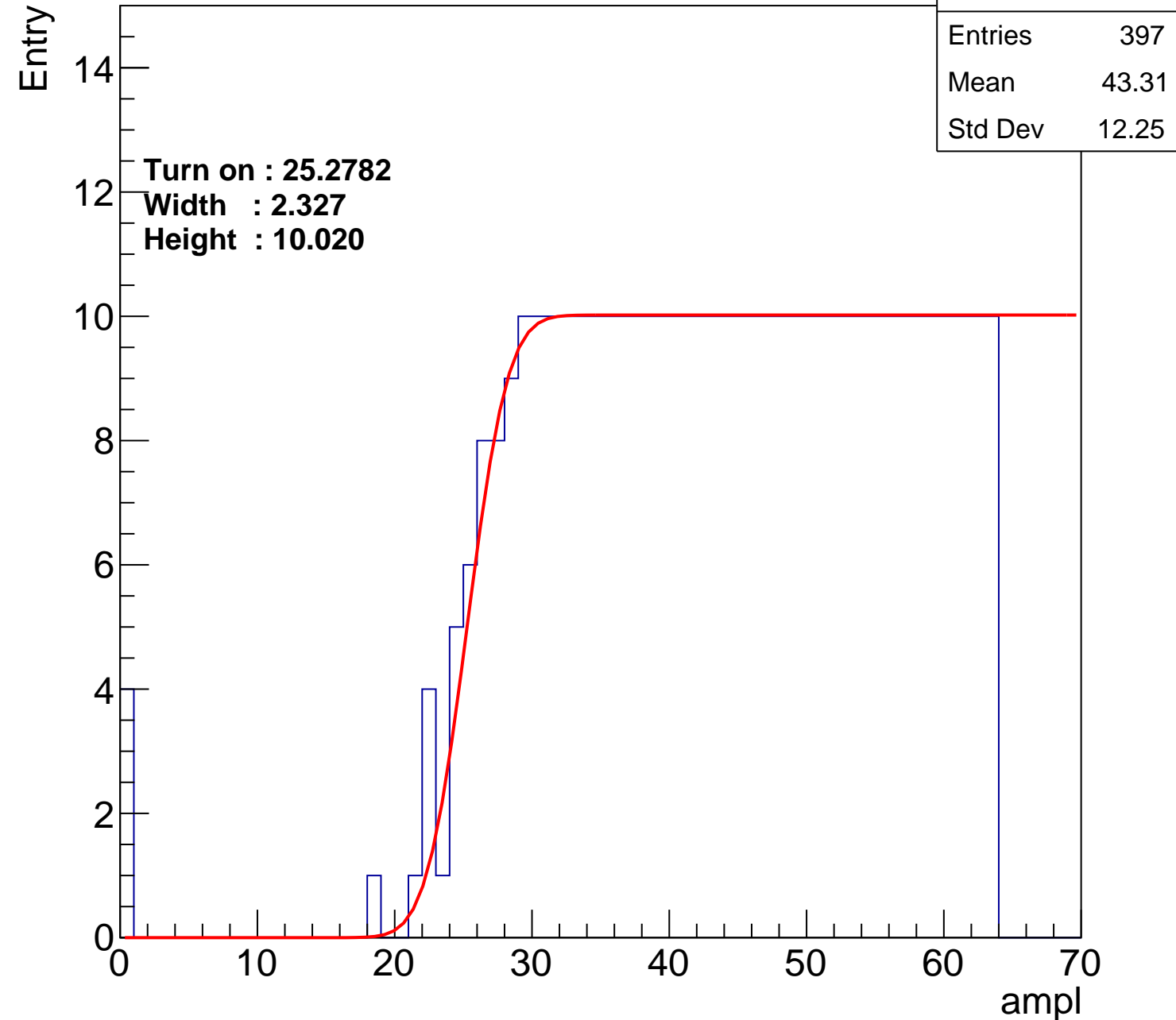
Width : 2.327

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.67
Std Dev	11.1

Turn on : 26.4155

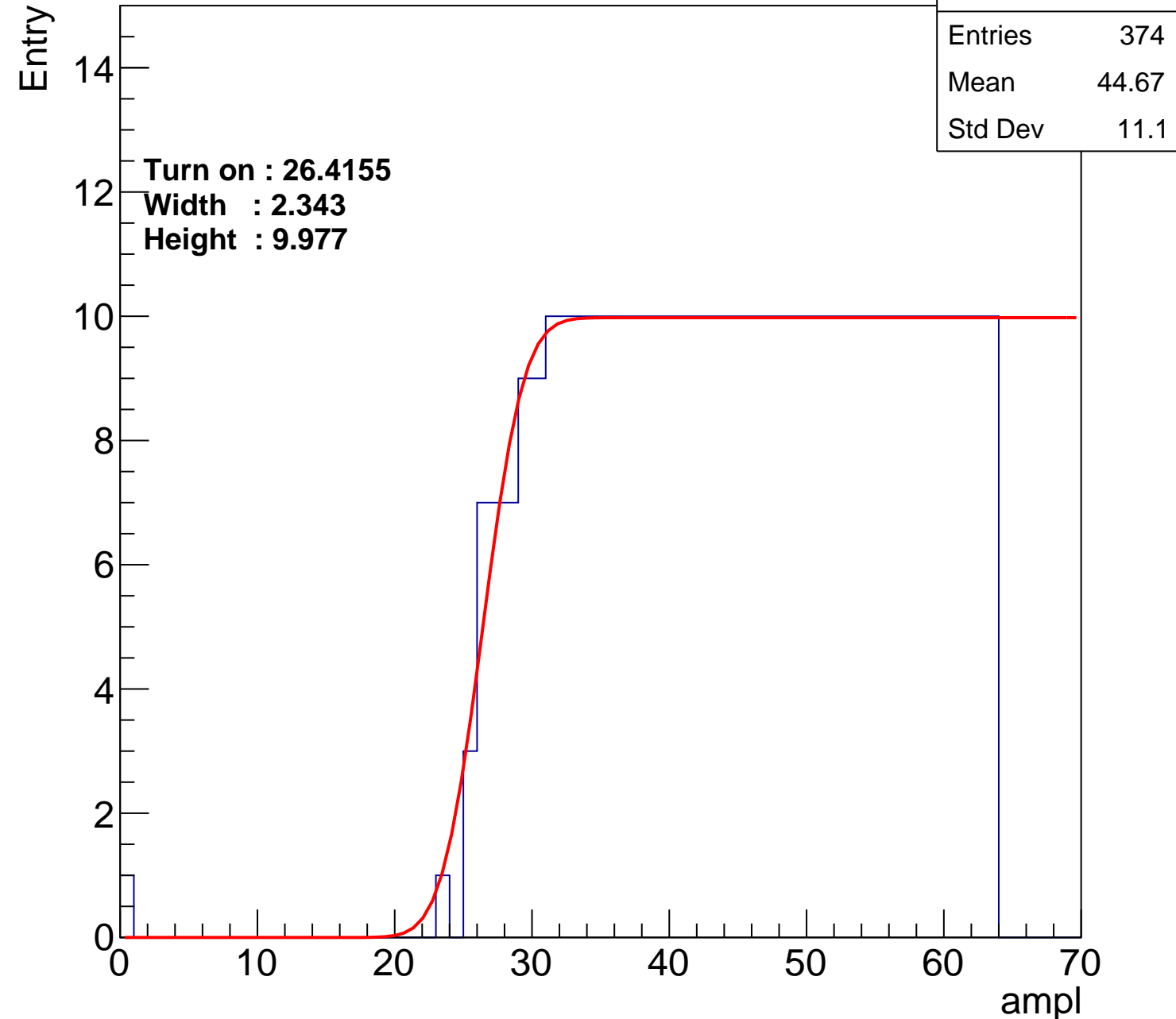
Width : 2.343

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.98
Std Dev	11.78

Turn on : 26.4081

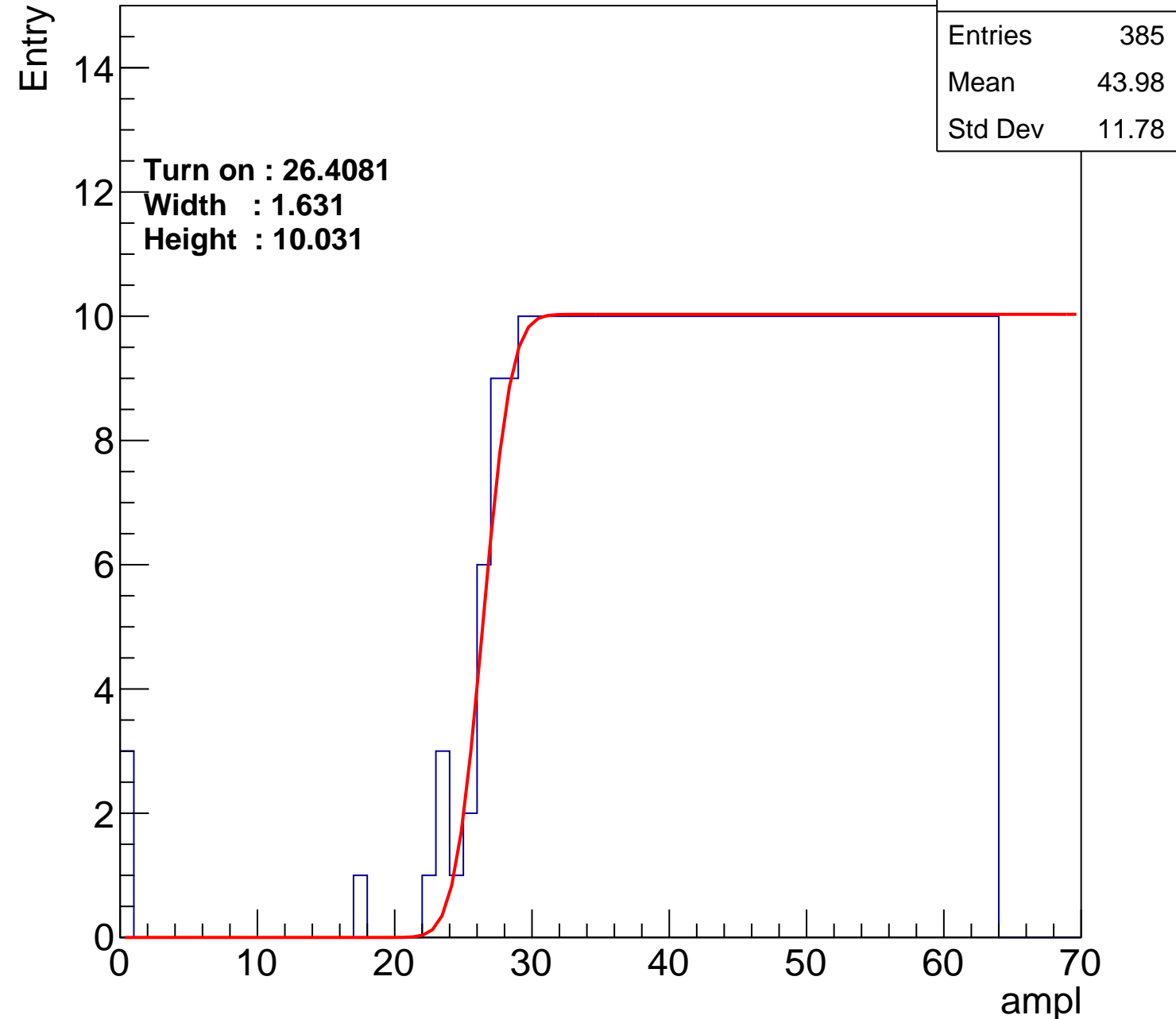
Width : 1.631

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.35
Std Dev	12.24

Turn on : 24.5504

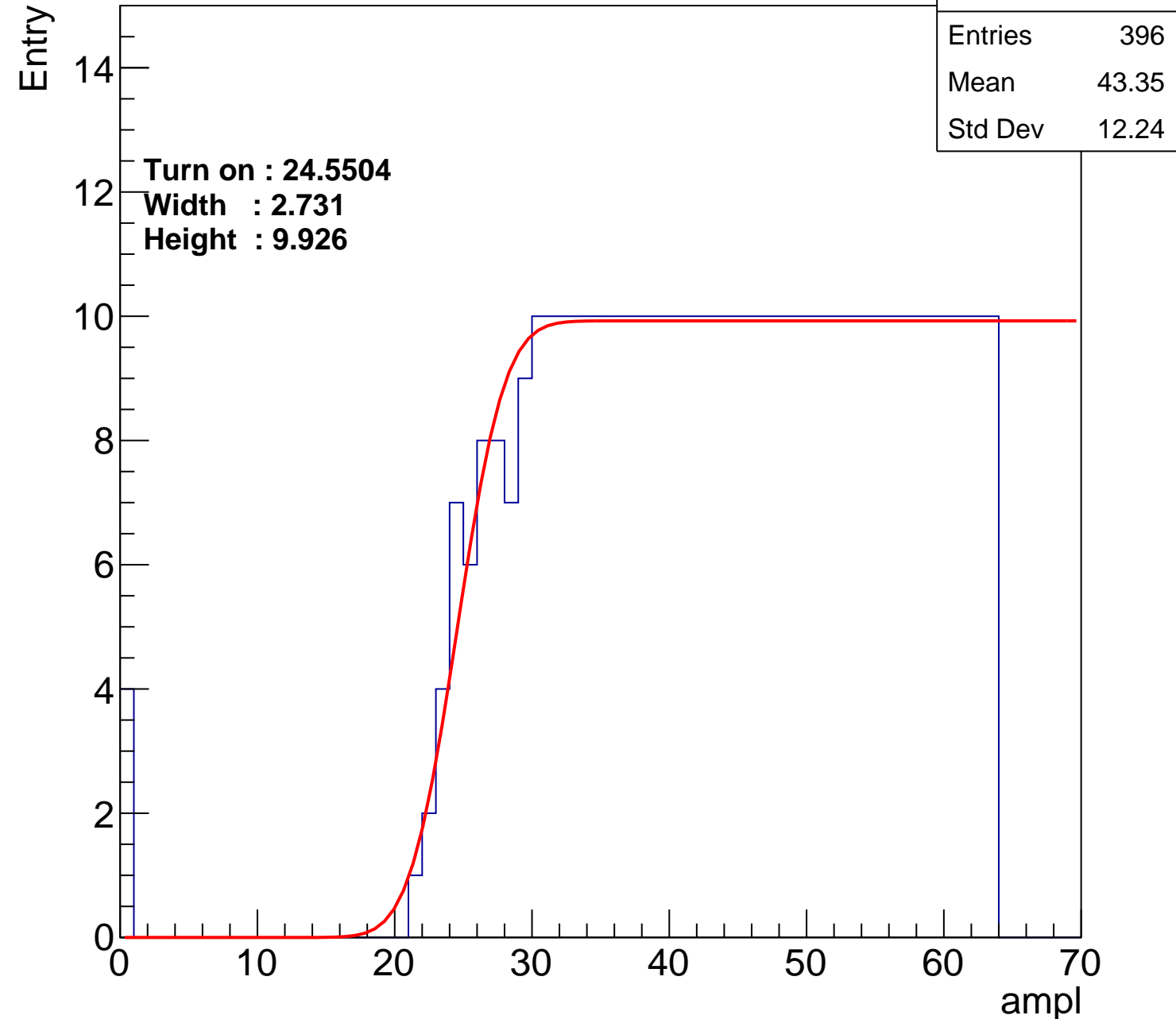
Width : 2.731

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.67
Std Dev	12.07

Turn on : 25.5752

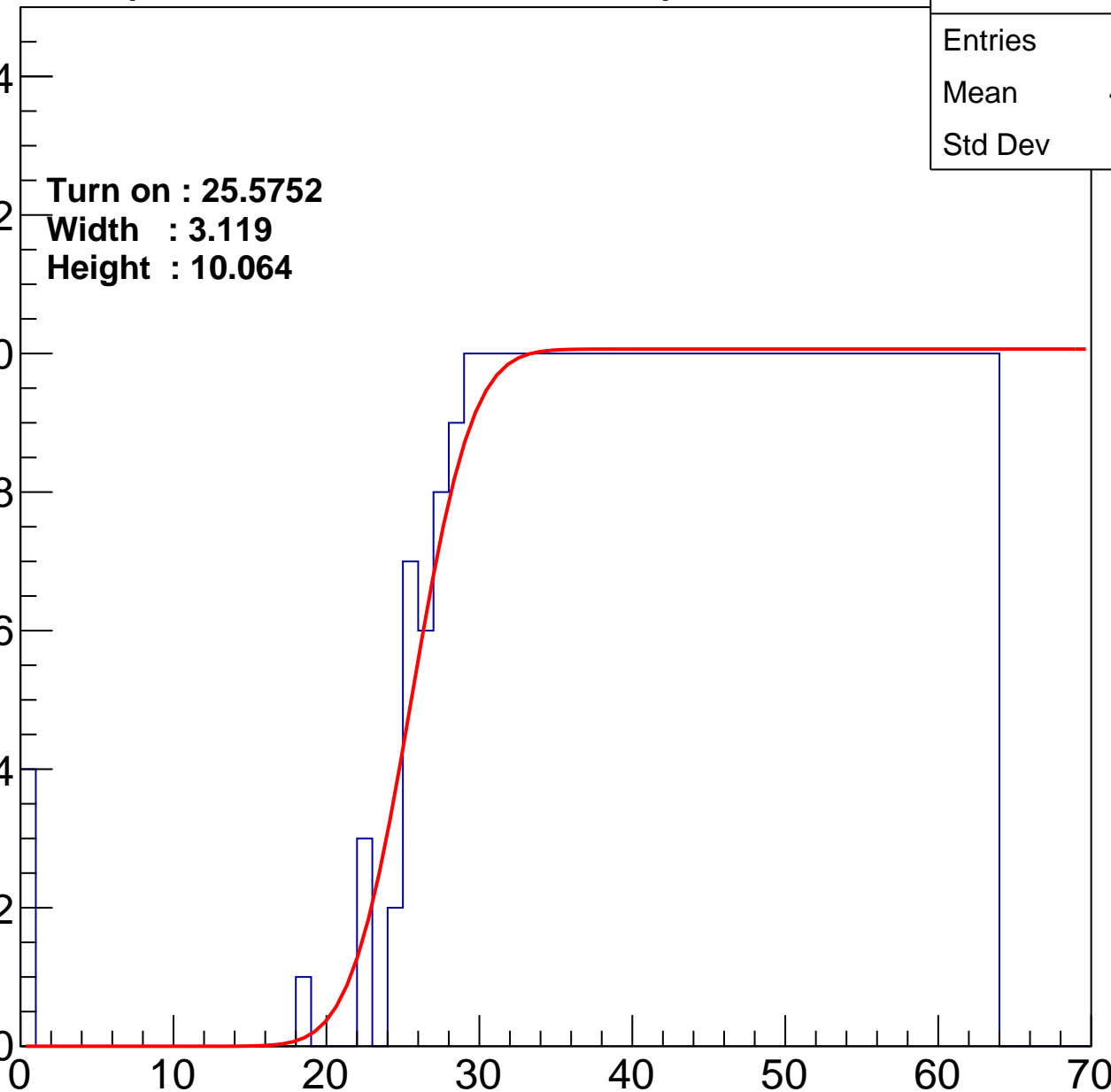
Width : 3.119

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.0431

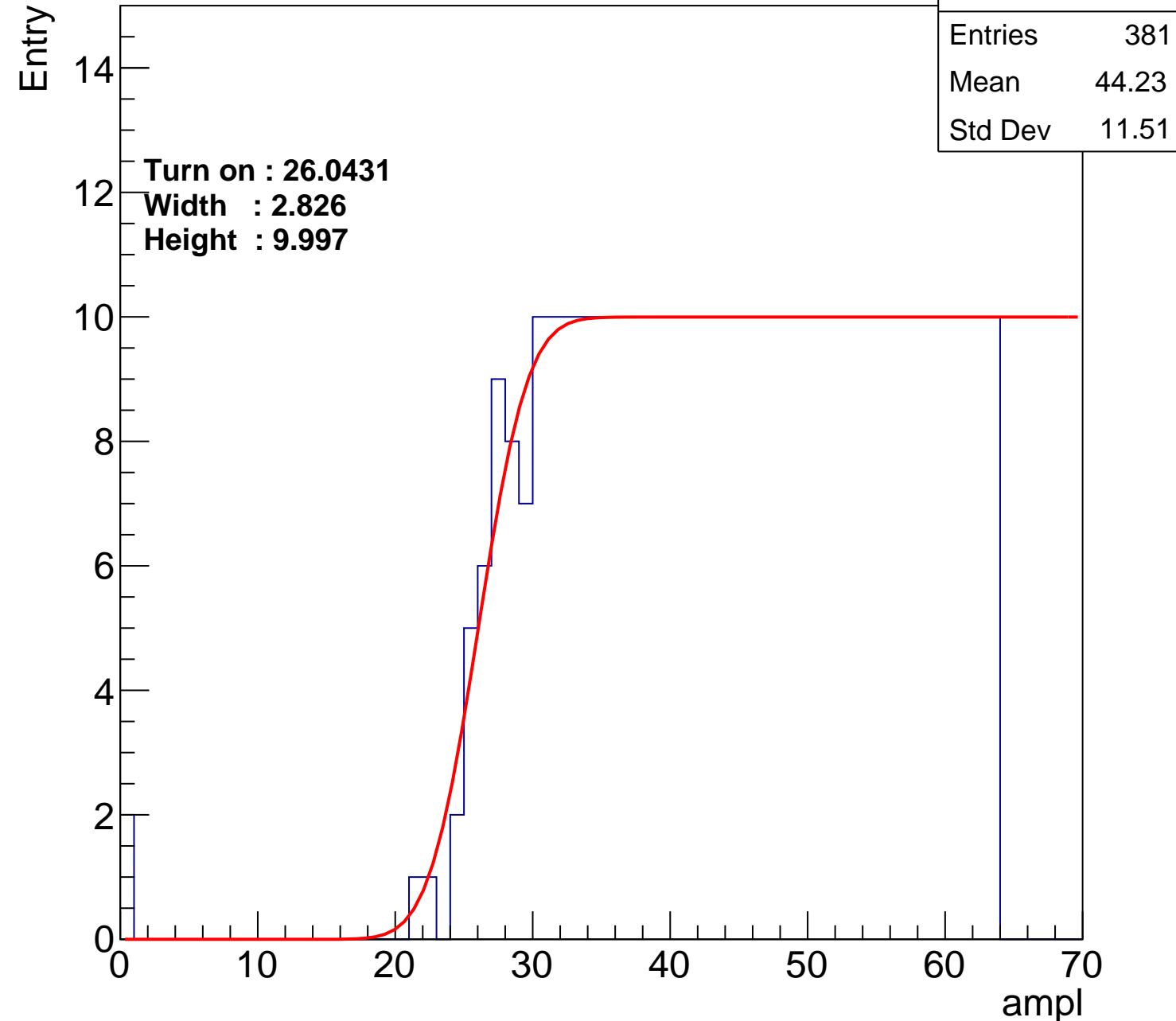
Width : 2.826

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch94

calib_packv5_042523_0143.root, FC#3, port B3

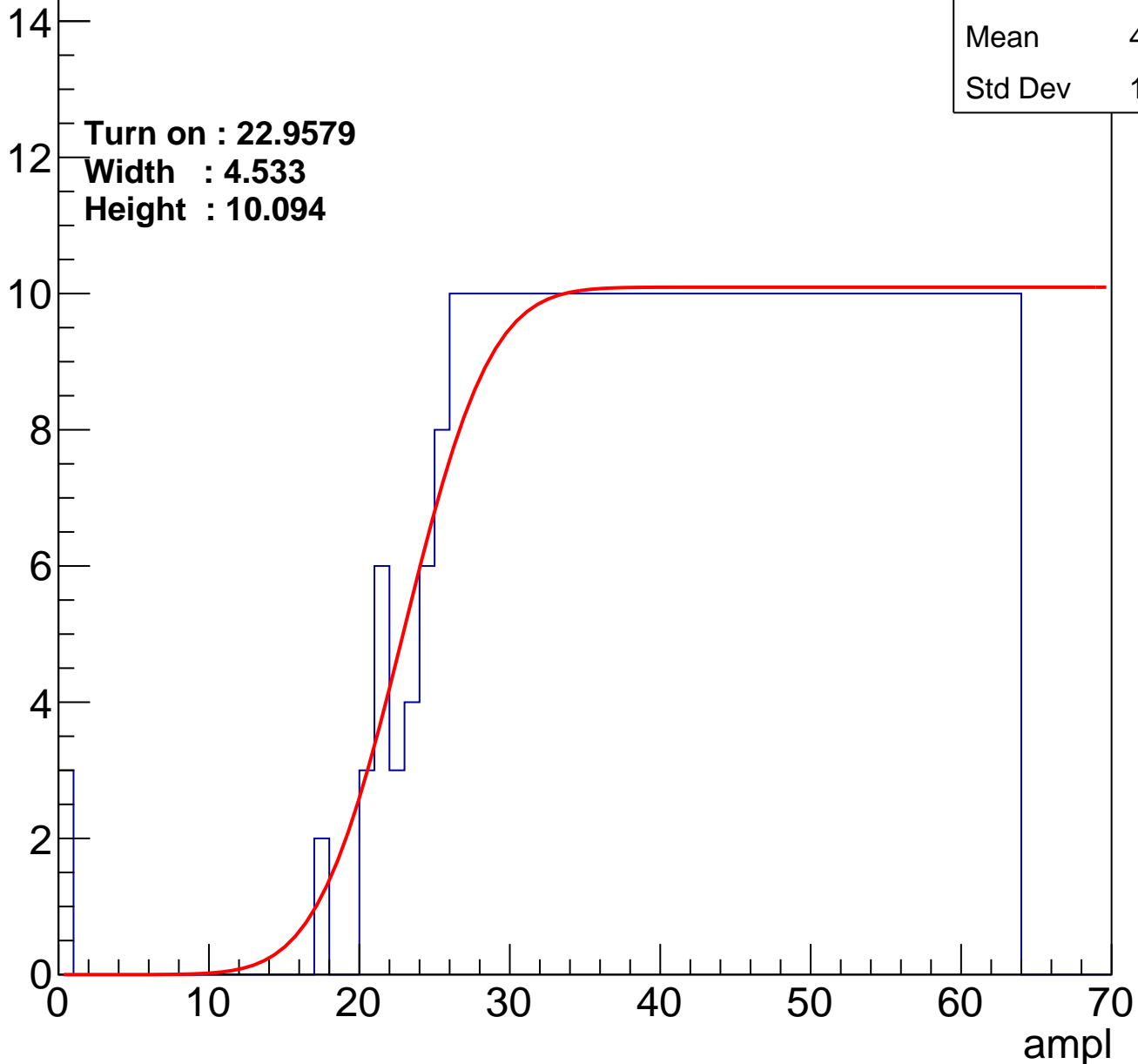
Entries	415
Mean	42.49
Std Dev	12.56

Turn on : 22.9579

Width : 4.533

Height : 10.094

Entry



B1L000S, U14-ch95

calib_packv5_042523_0143.root, FC#3, port B3

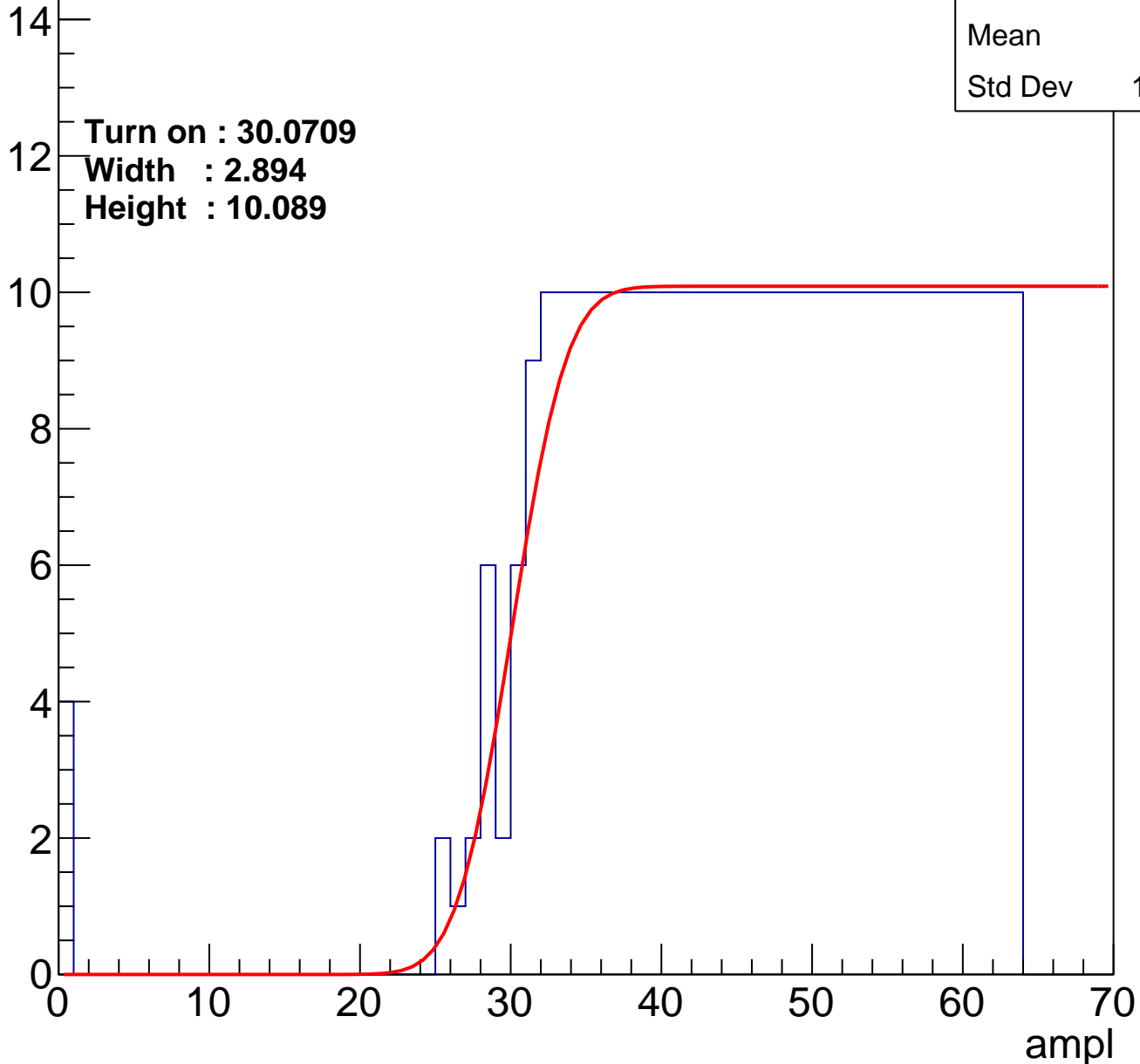
Entries	352
Mean	45.5
Std Dev	11.24

Turn on : 30.0709

Width : 2.894

Height : 10.089

Entry



B1L000S, U14-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.68
Std Dev	12.05

Turn on : 25.7320

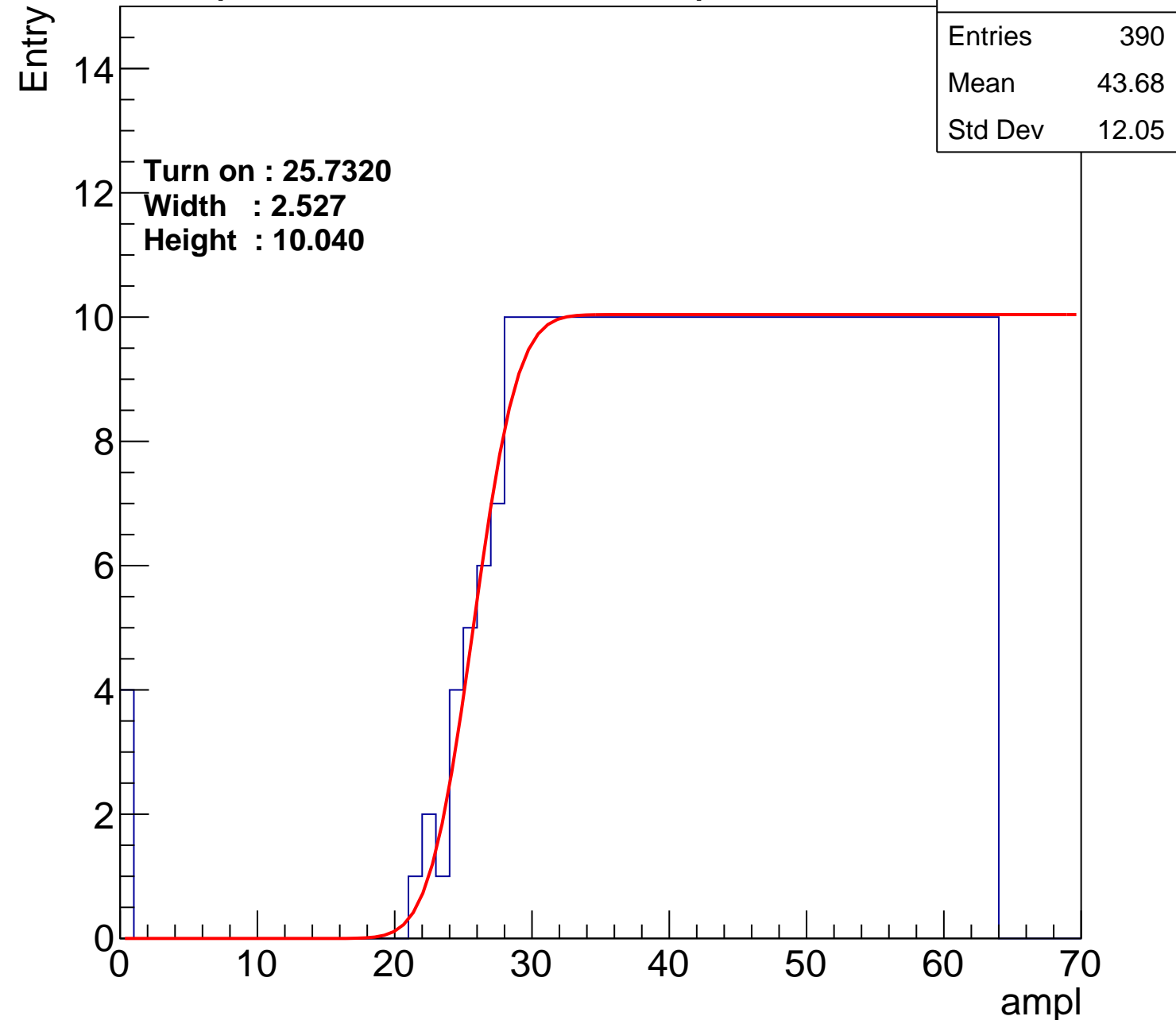
Width : 2.527

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch97

calib_packv5_042523_0143.root, FC#3, port B3

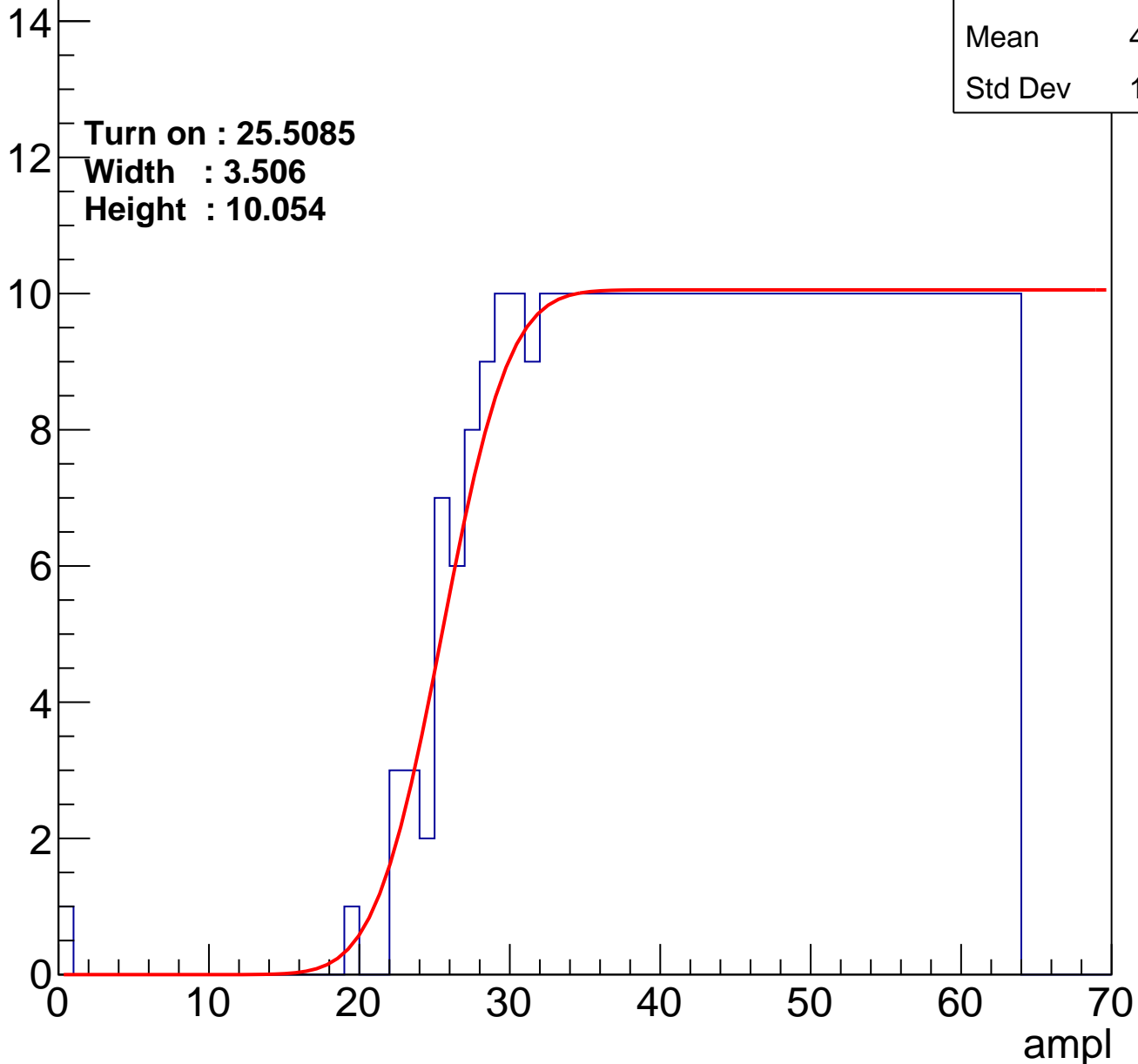
Entries	389
Mean	43.88
Std Dev	11.58

Turn on : 25.5085

Width : 3.506

Height : 10.054

Entry



B1L000S, U14-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.67
Std Dev	11.78

Turn on : 25.1912

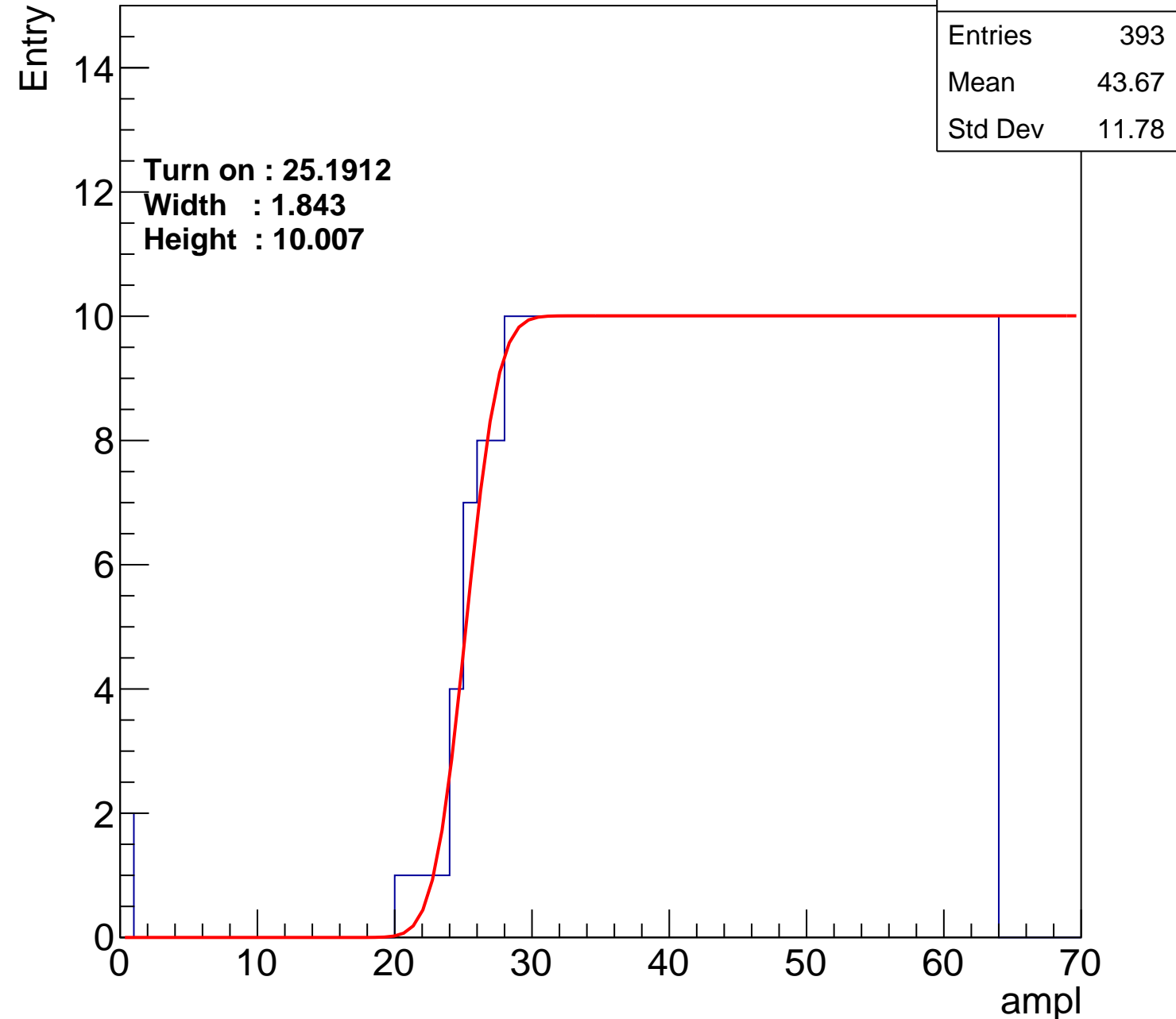
Width : 1.843

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.07
Std Dev	11.48

Turn on : 25.9961

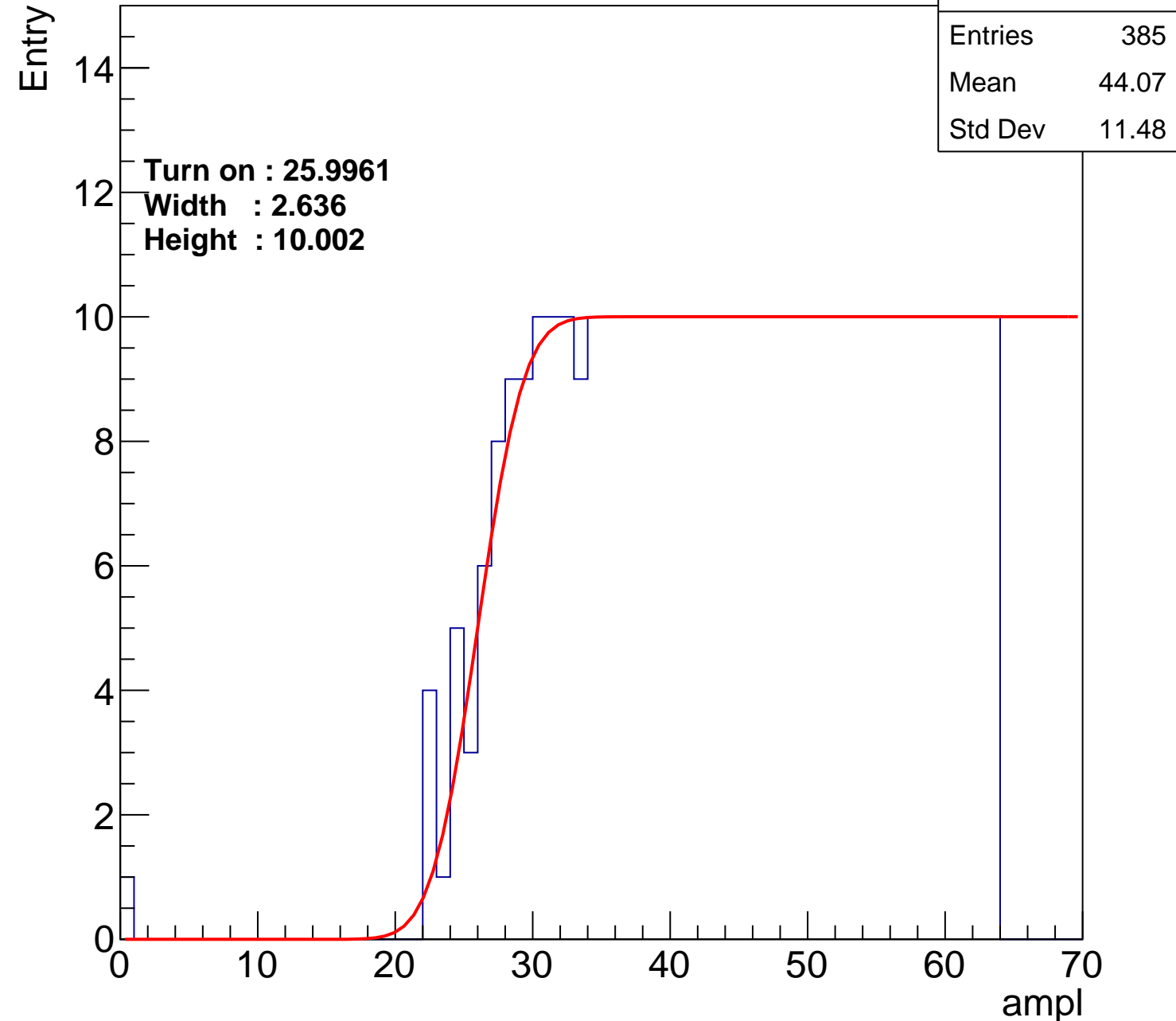
Width : 2.636

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.04
Std Dev	11.61

Turn on : 25.9684

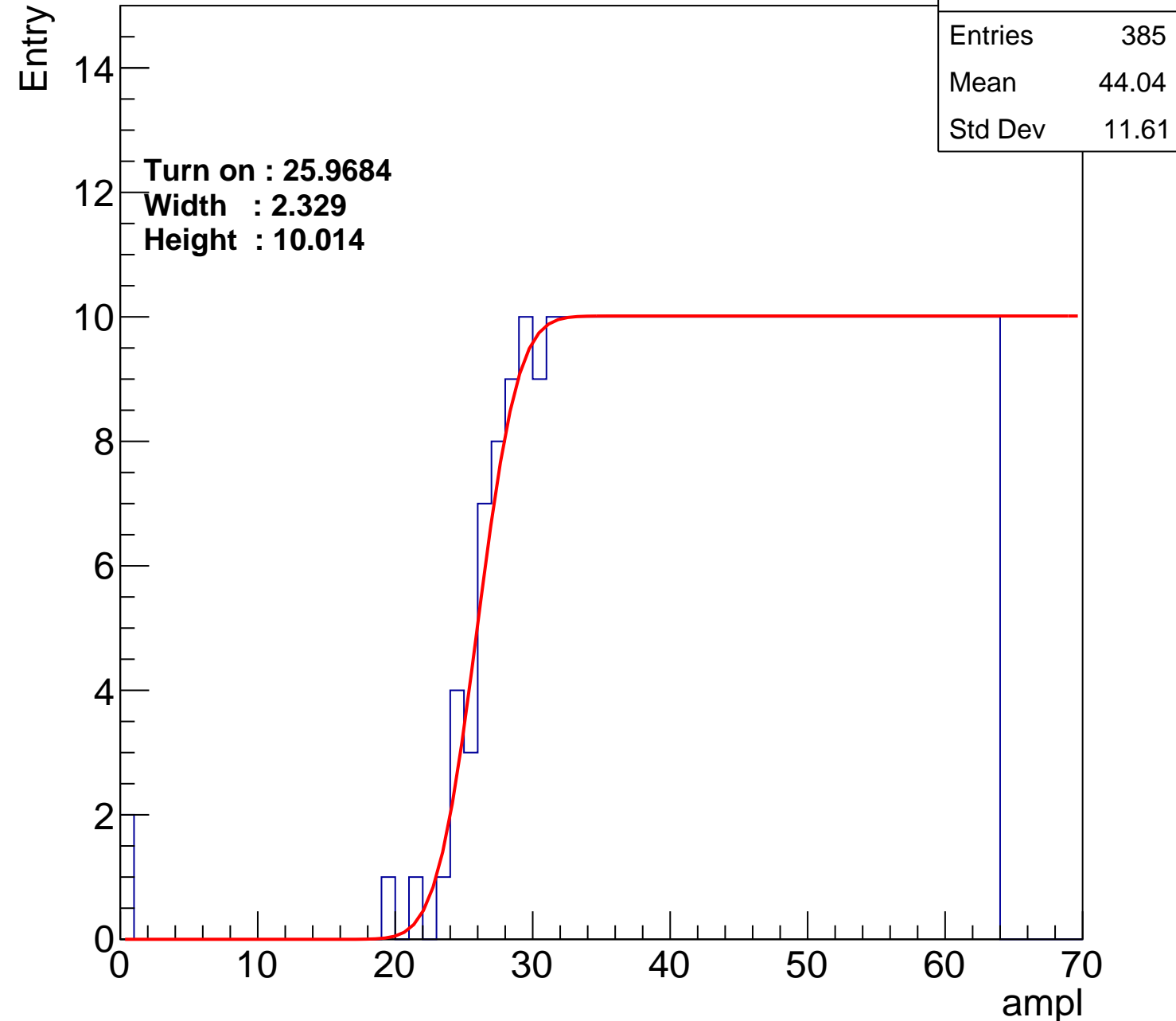
Width : 2.329

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.66
Std Dev	11.67

Turn on : 24.8302

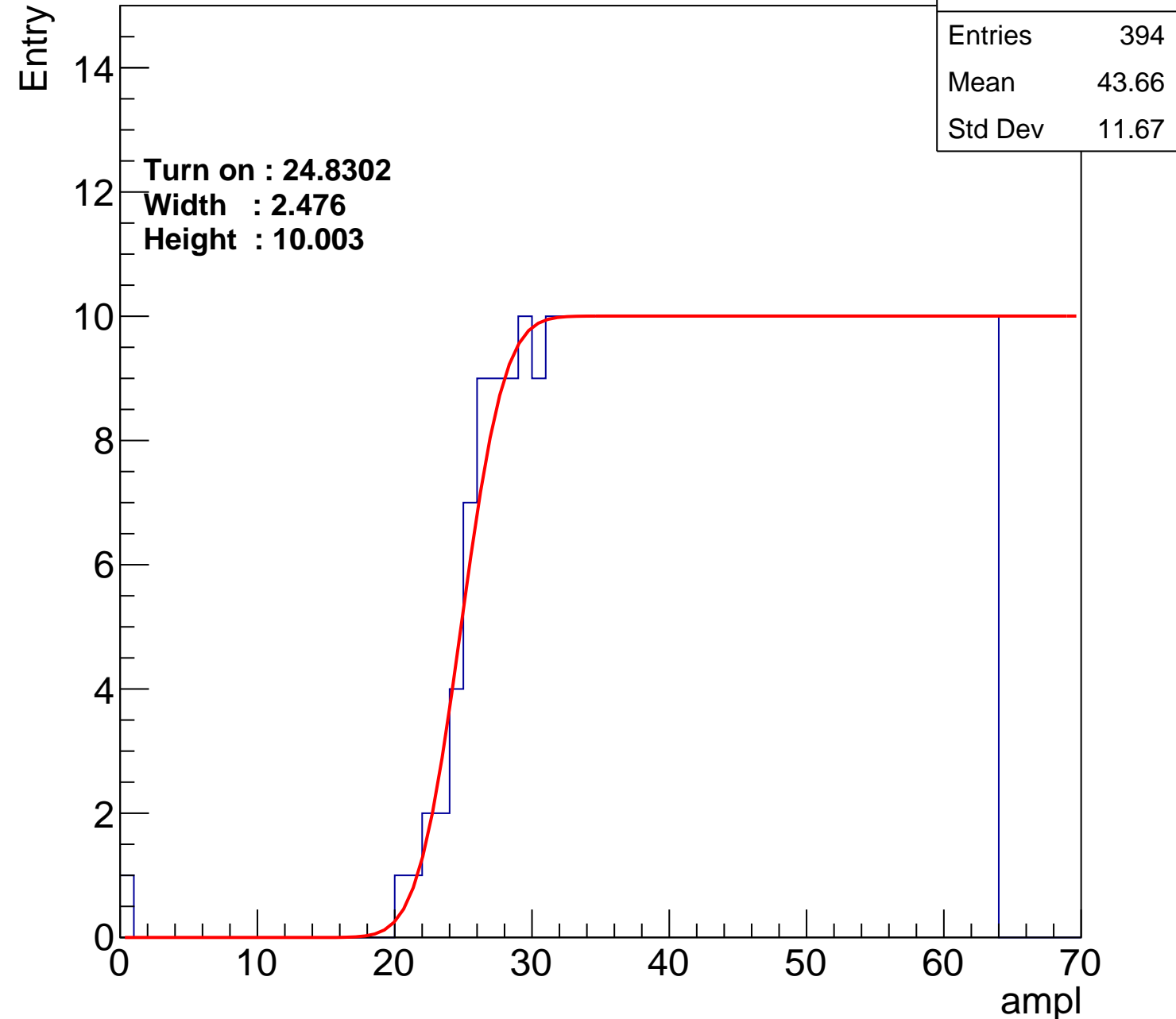
Width : 2.476

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.15
Std Dev	11.97

Turn on : 23.7737

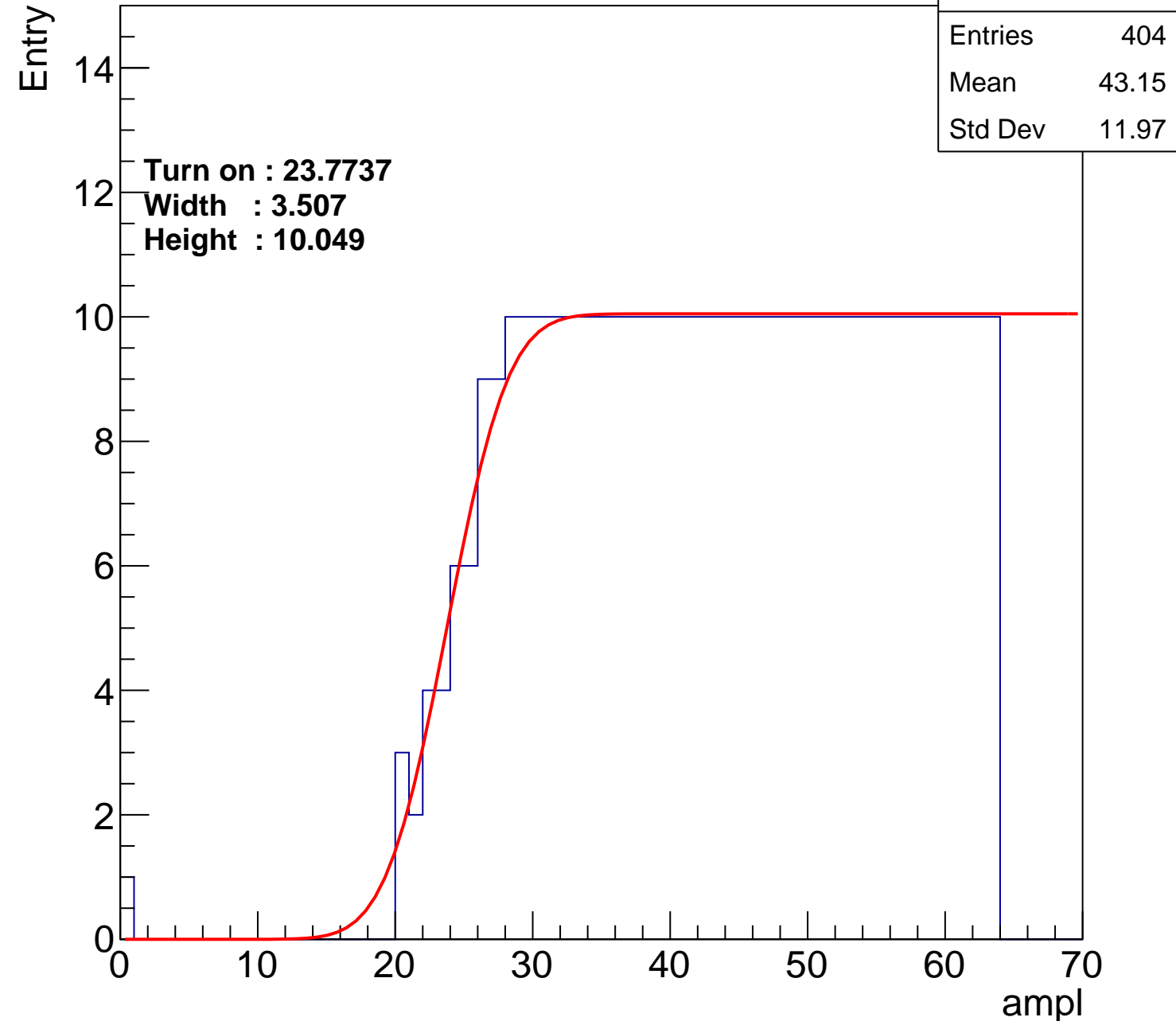
Width : 3.507

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch103

calib_packv5_042523_0143.root, FC#3, port B3

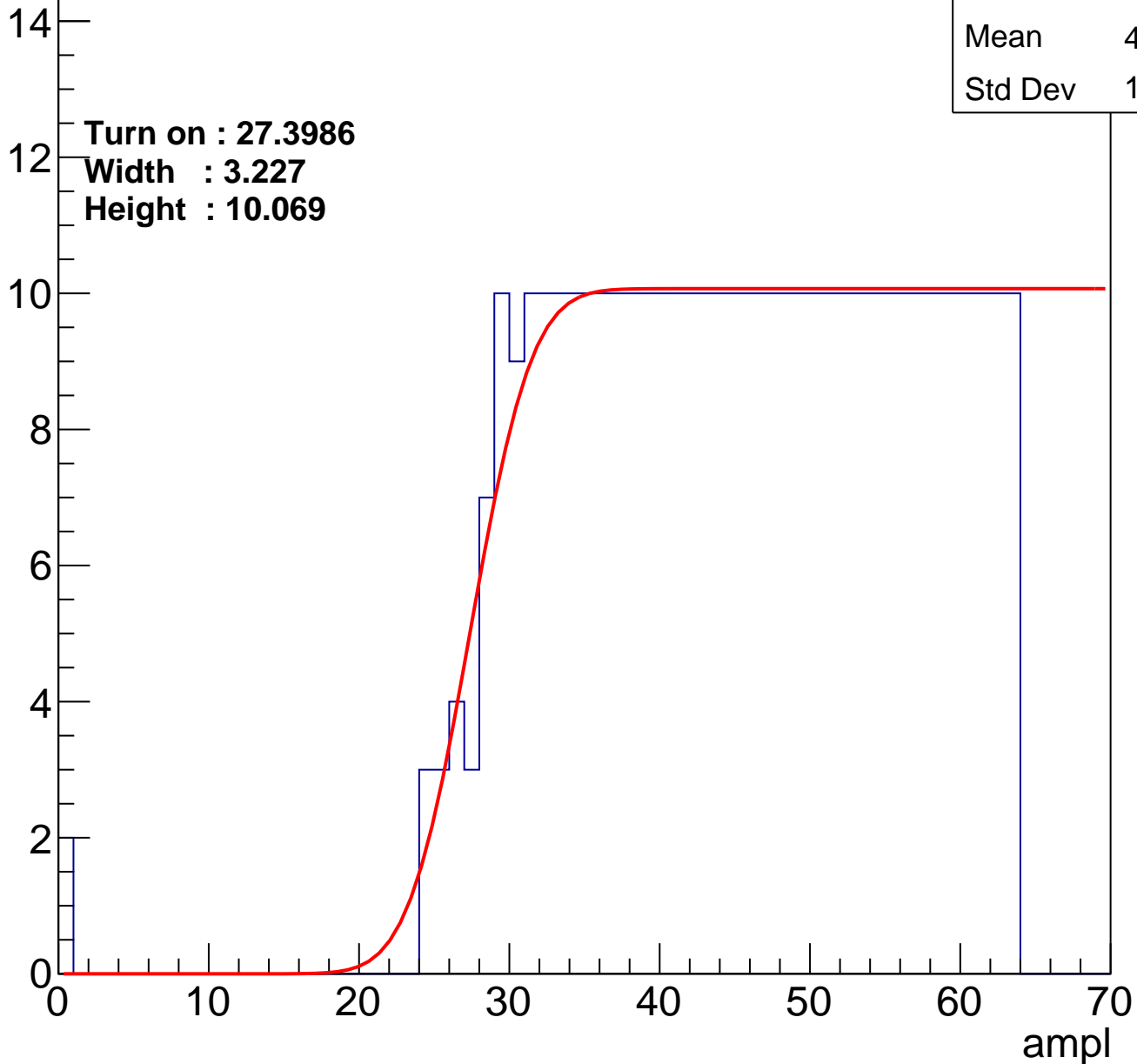
Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.3986

Width : 3.227

Height : 10.069

Entry



B1L000S, U14-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.27
Std Dev	11.63

Turn on : 26.4760

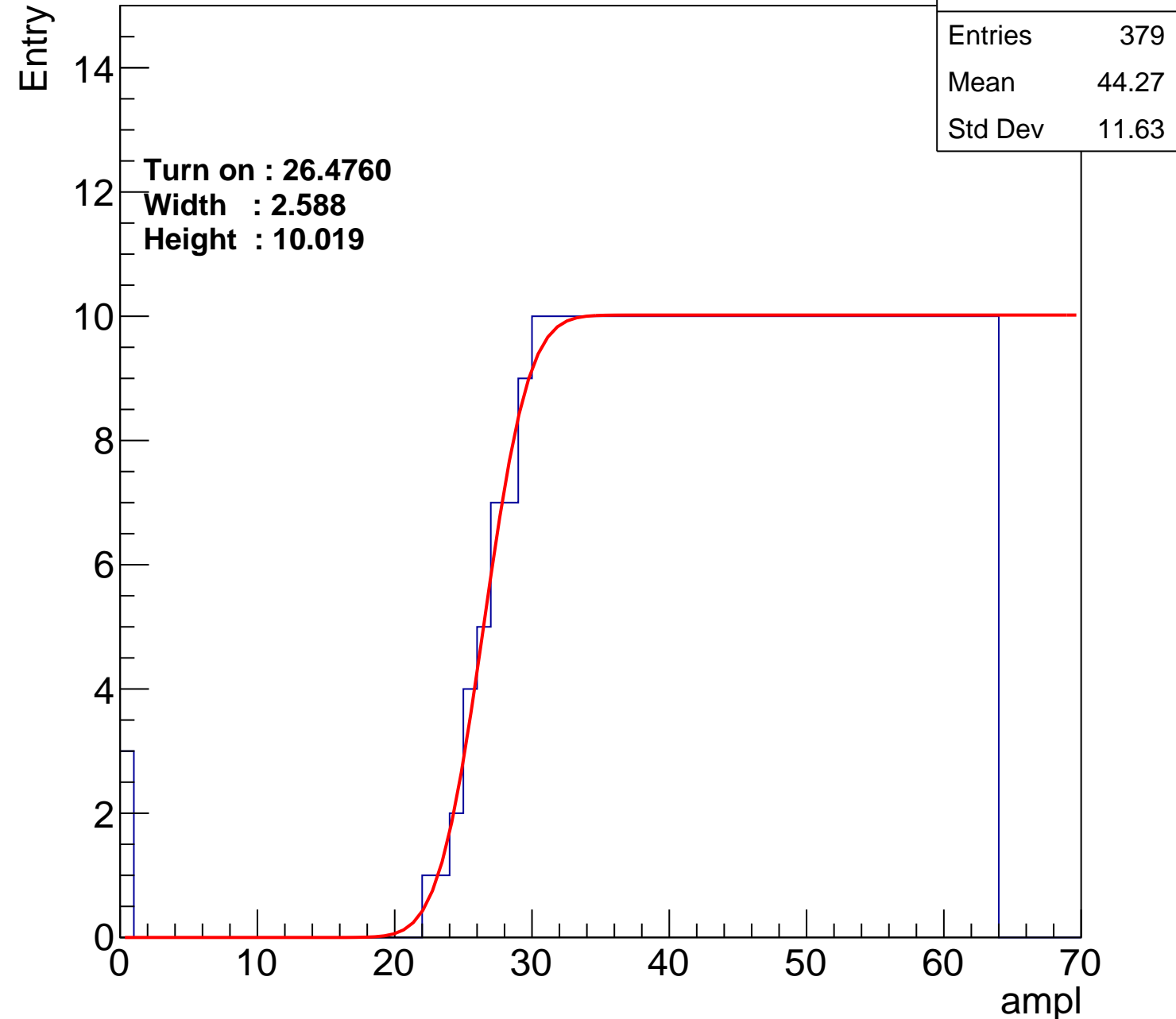
Width : 2.588

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.58
Std Dev	11.82

Turn on : 25.1534

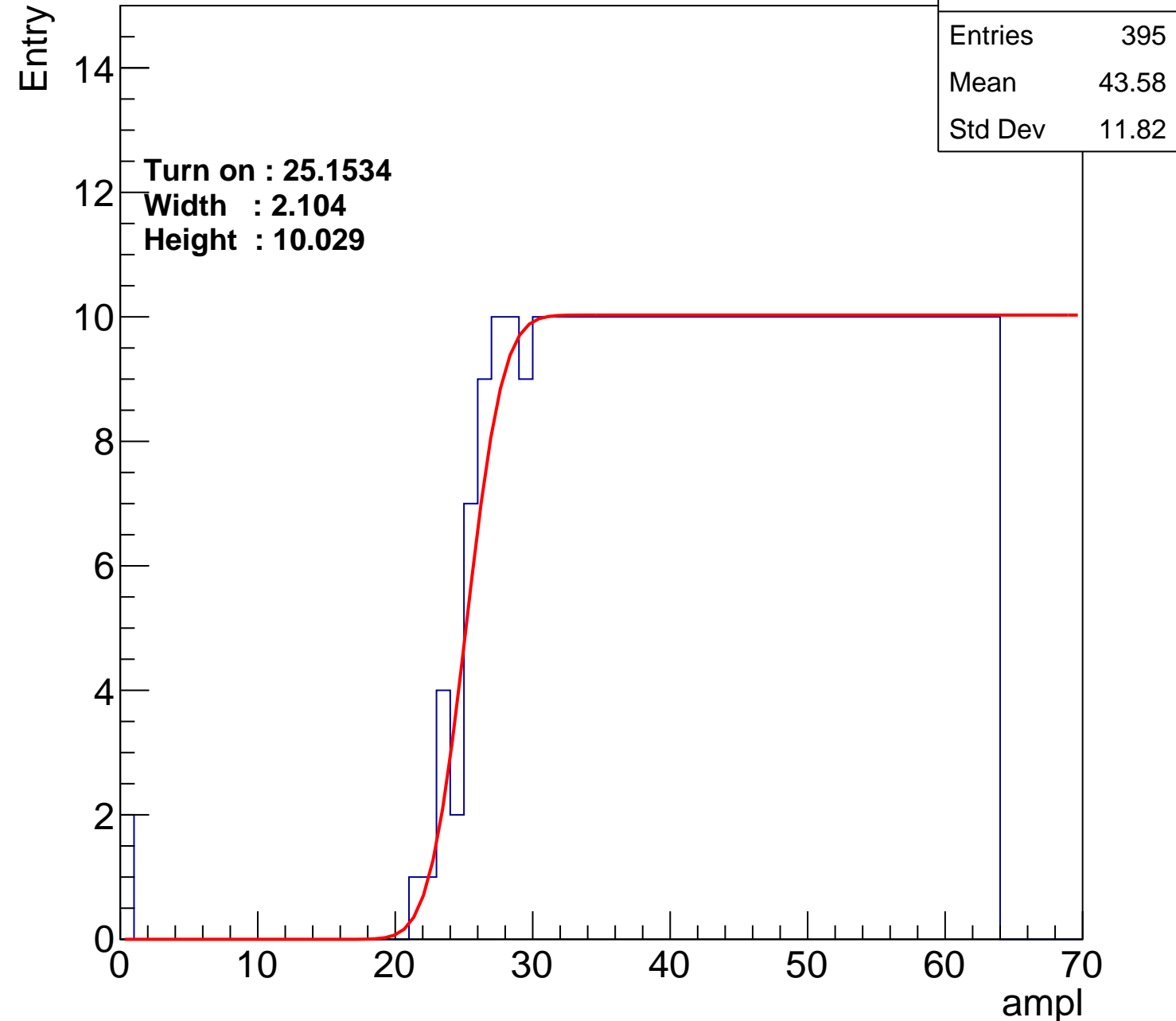
Width : 2.104

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.05
Std Dev	11.58

Turn on : 26.0055

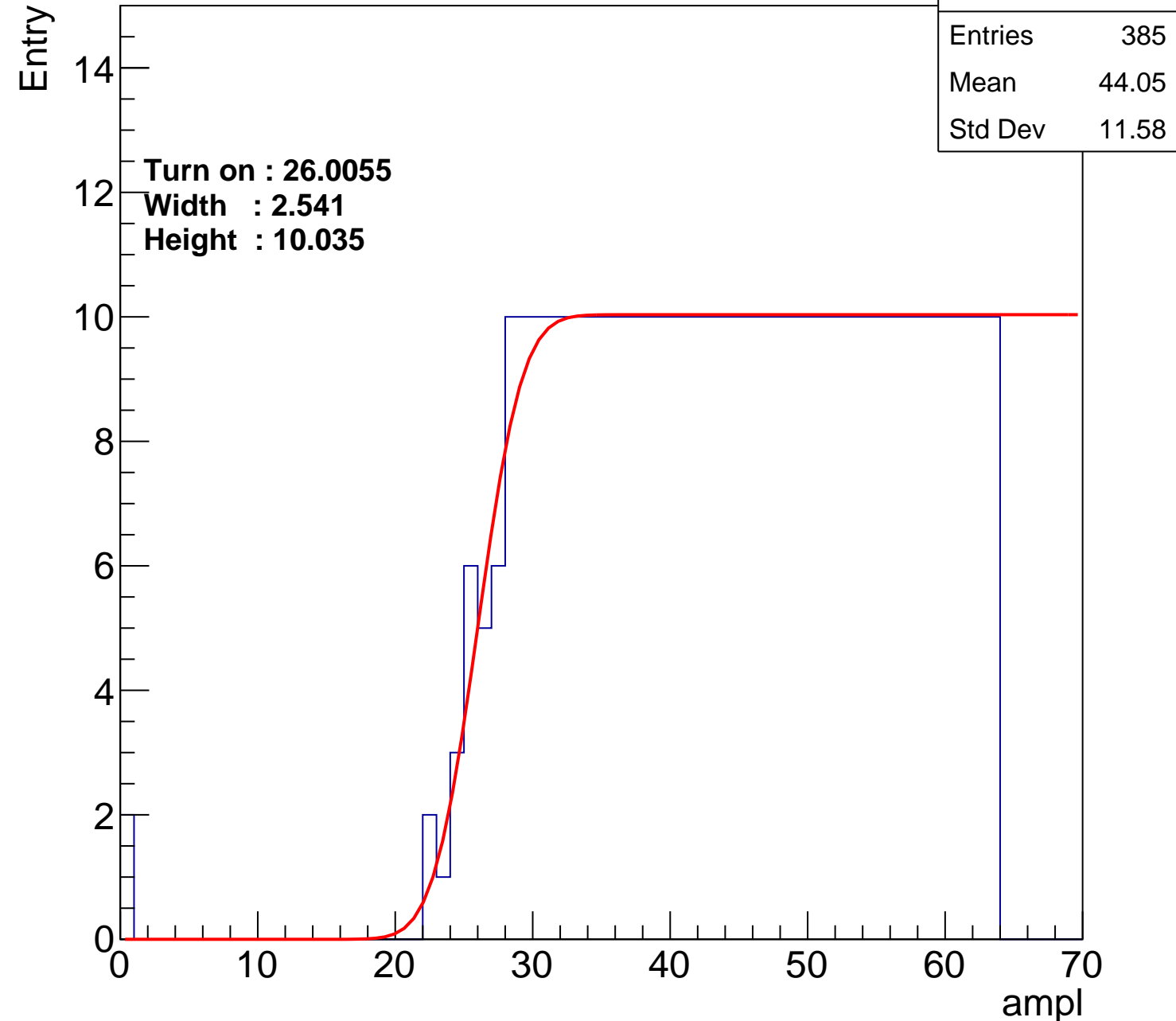
Width : 2.541

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.84
Std Dev	12.12

Turn on : 26.3644

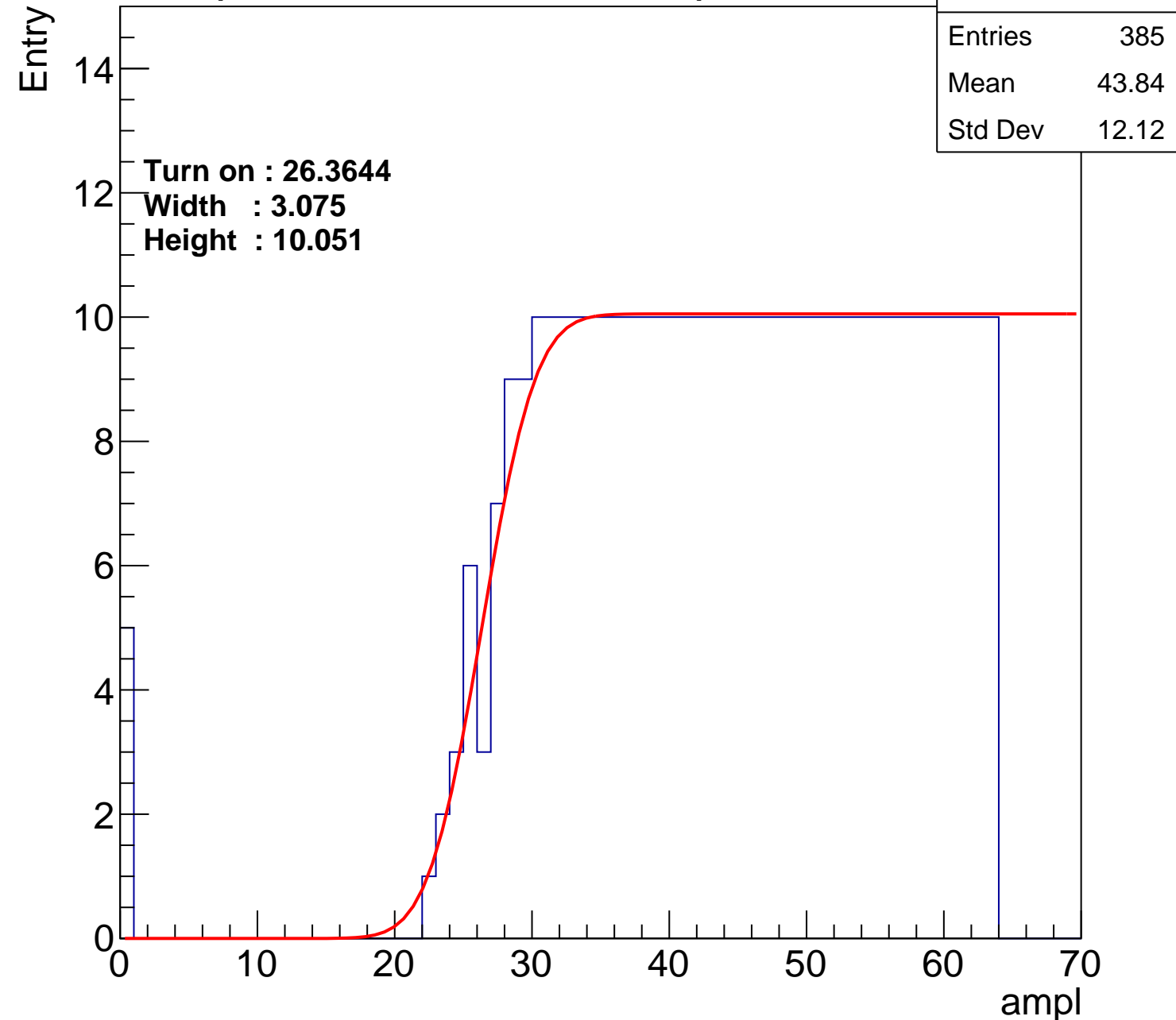
Width : 3.075

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.44
Std Dev	11.82

Turn on : 24.2180

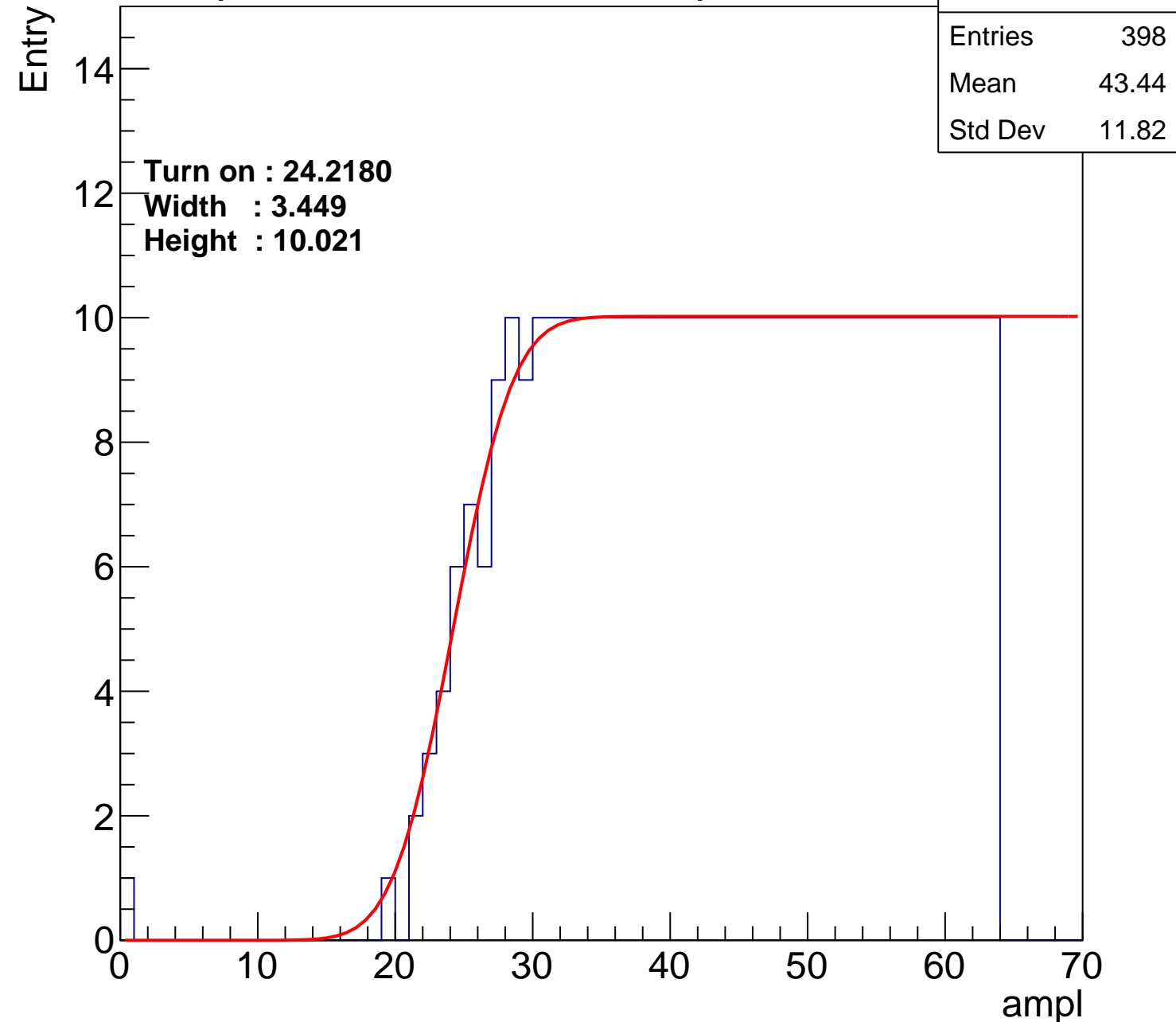
Width : 3.449

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.94
Std Dev	11.78

Turn on : 24.3349

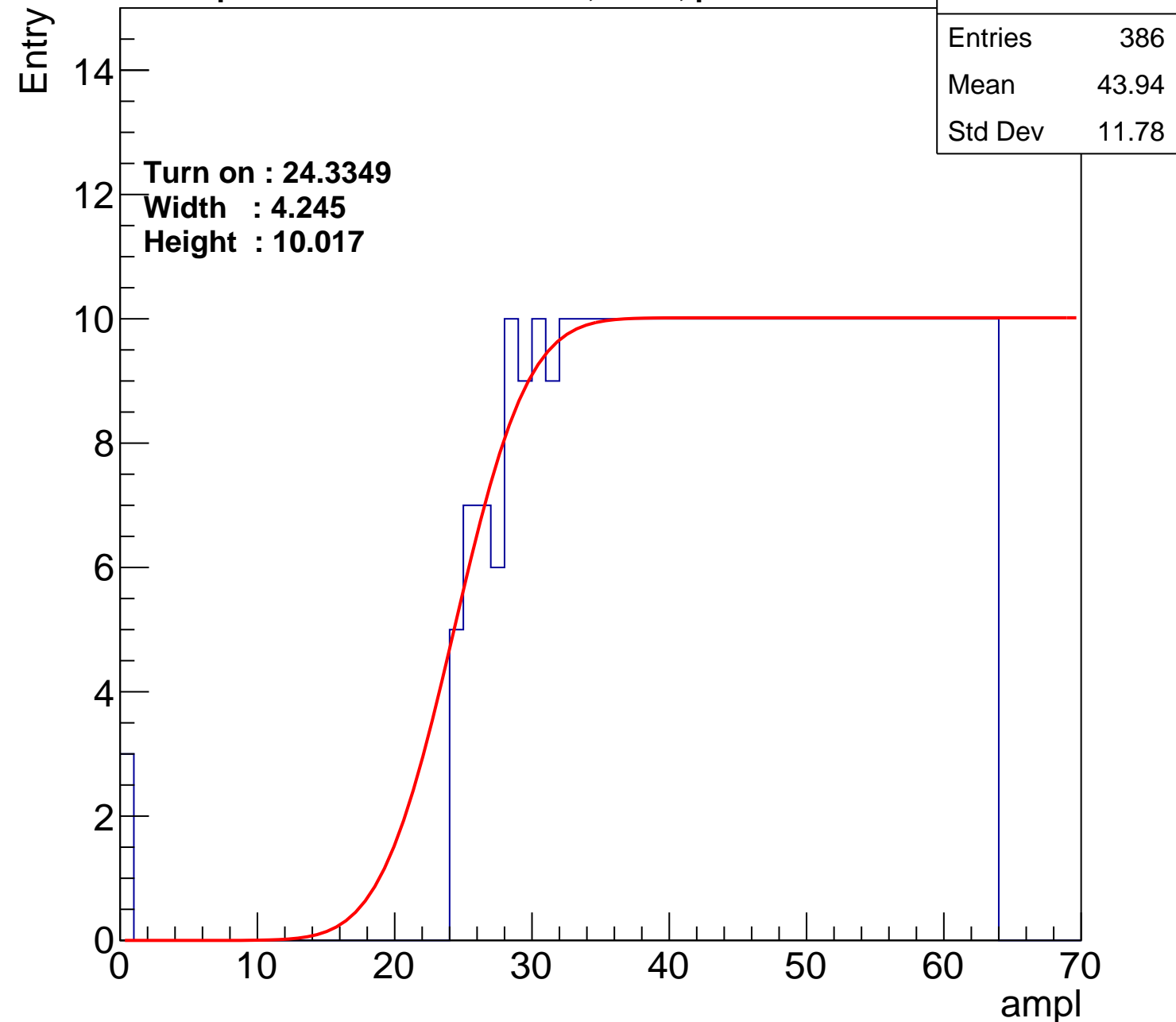
Width : 4.245

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.57
Std Dev	11.47

Turn on : 27.6843

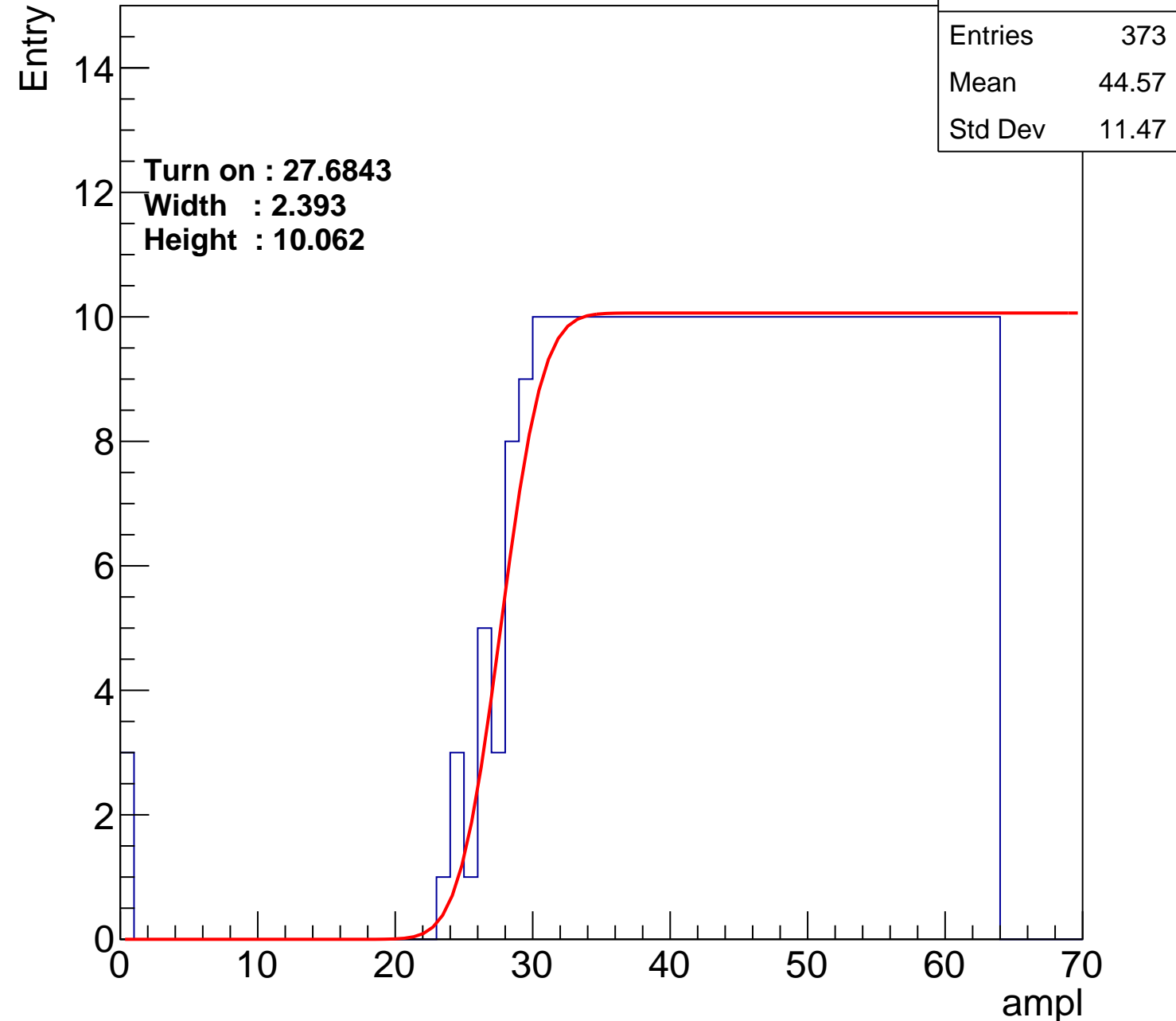
Width : 2.393

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.24
Std Dev	11.36

Turn on : 26.3938

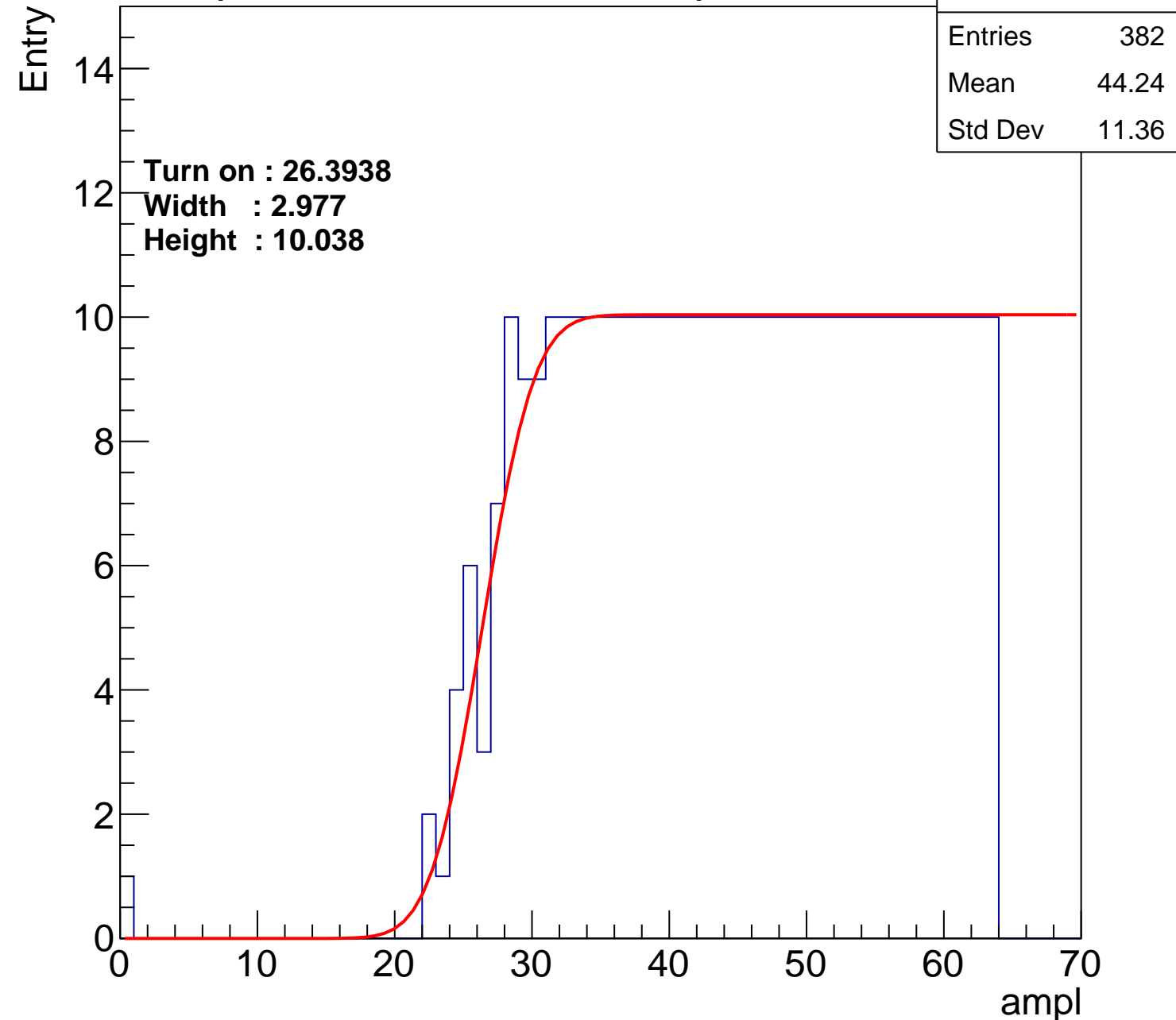
Width : 2.977

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.79
Std Dev	11.56

Turn on : 25.3406

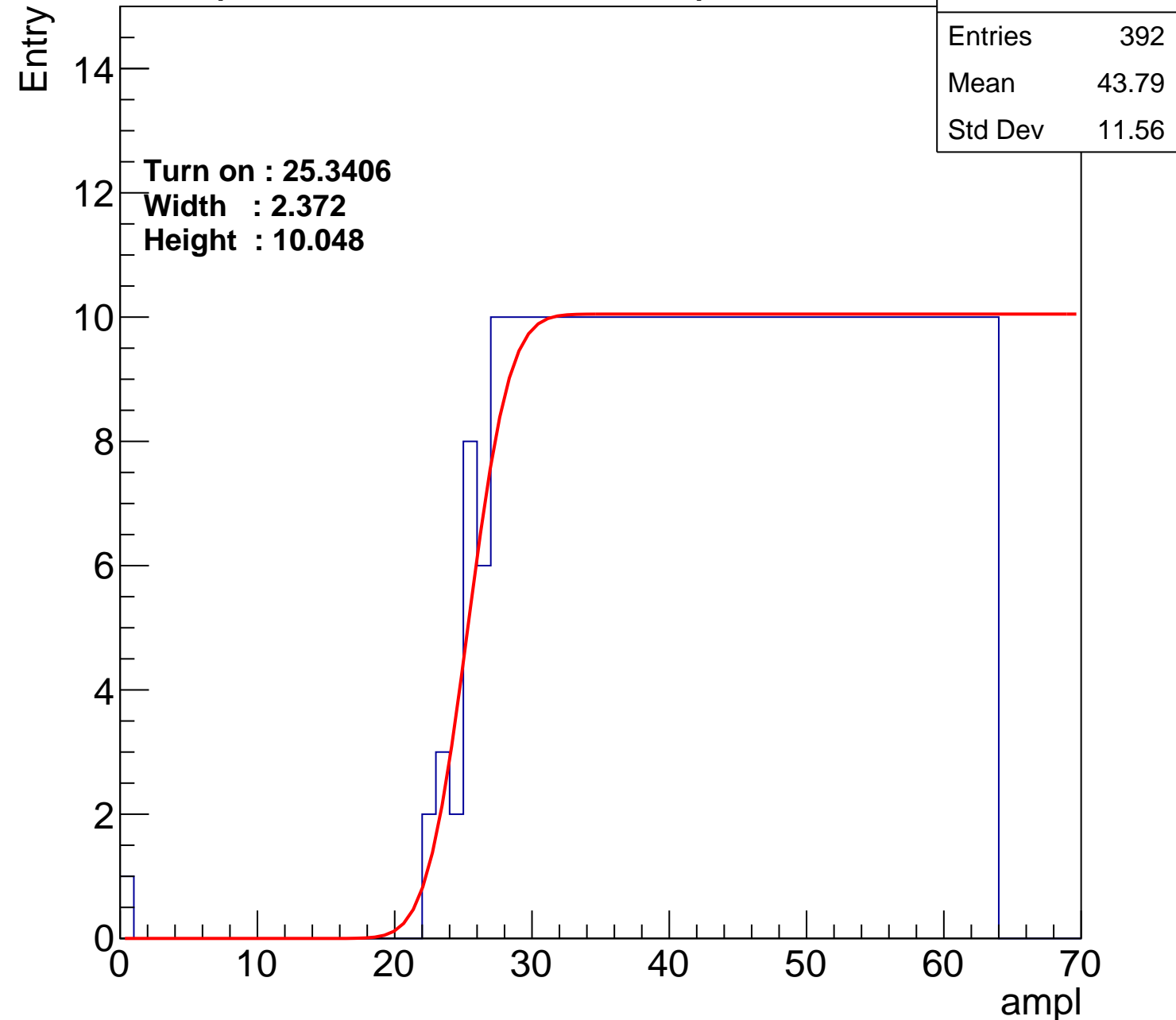
Width : 2.372

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 24.8392

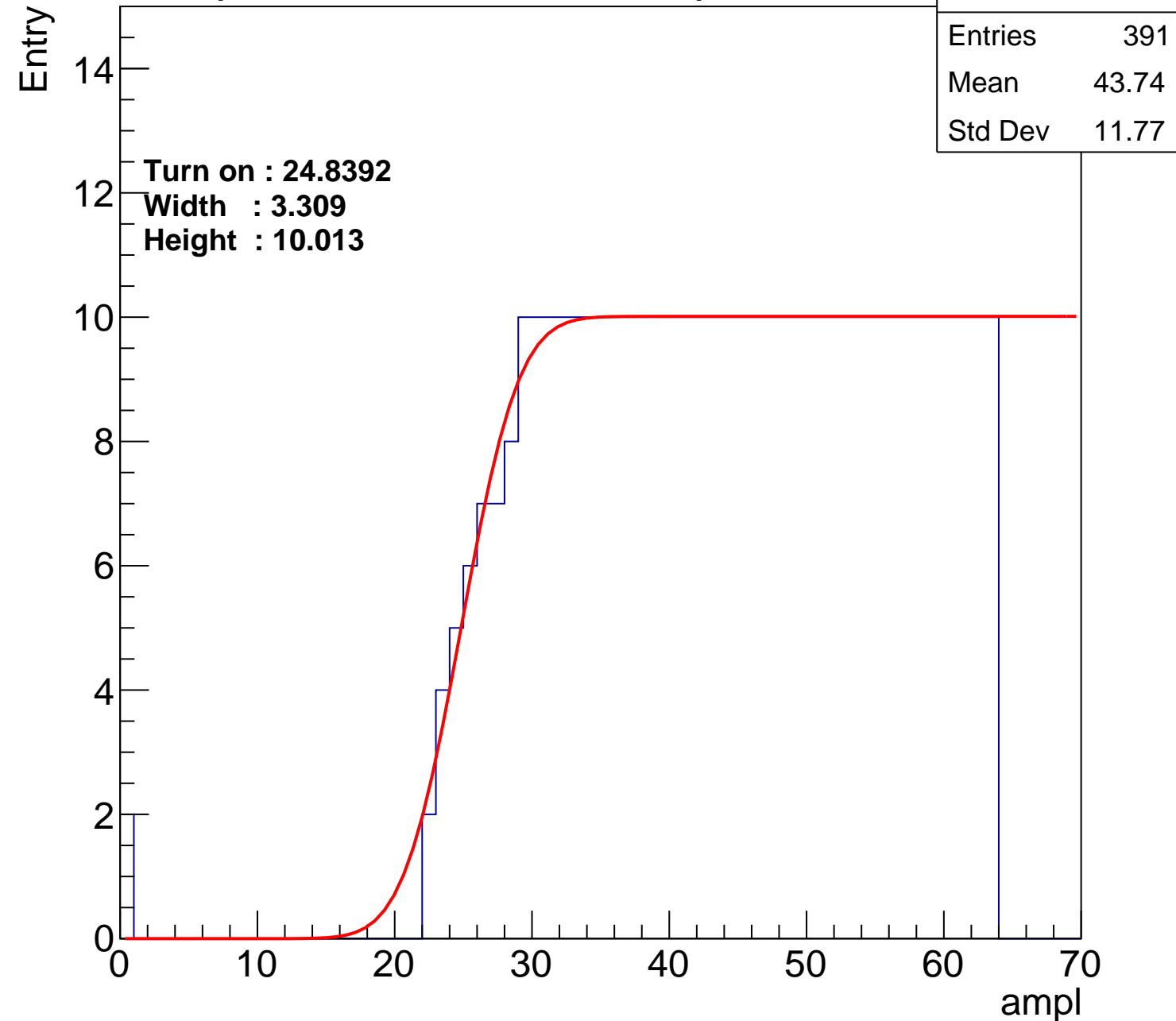
Width : 3.309

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.44
Std Dev	12.05

Turn on : 24.9867

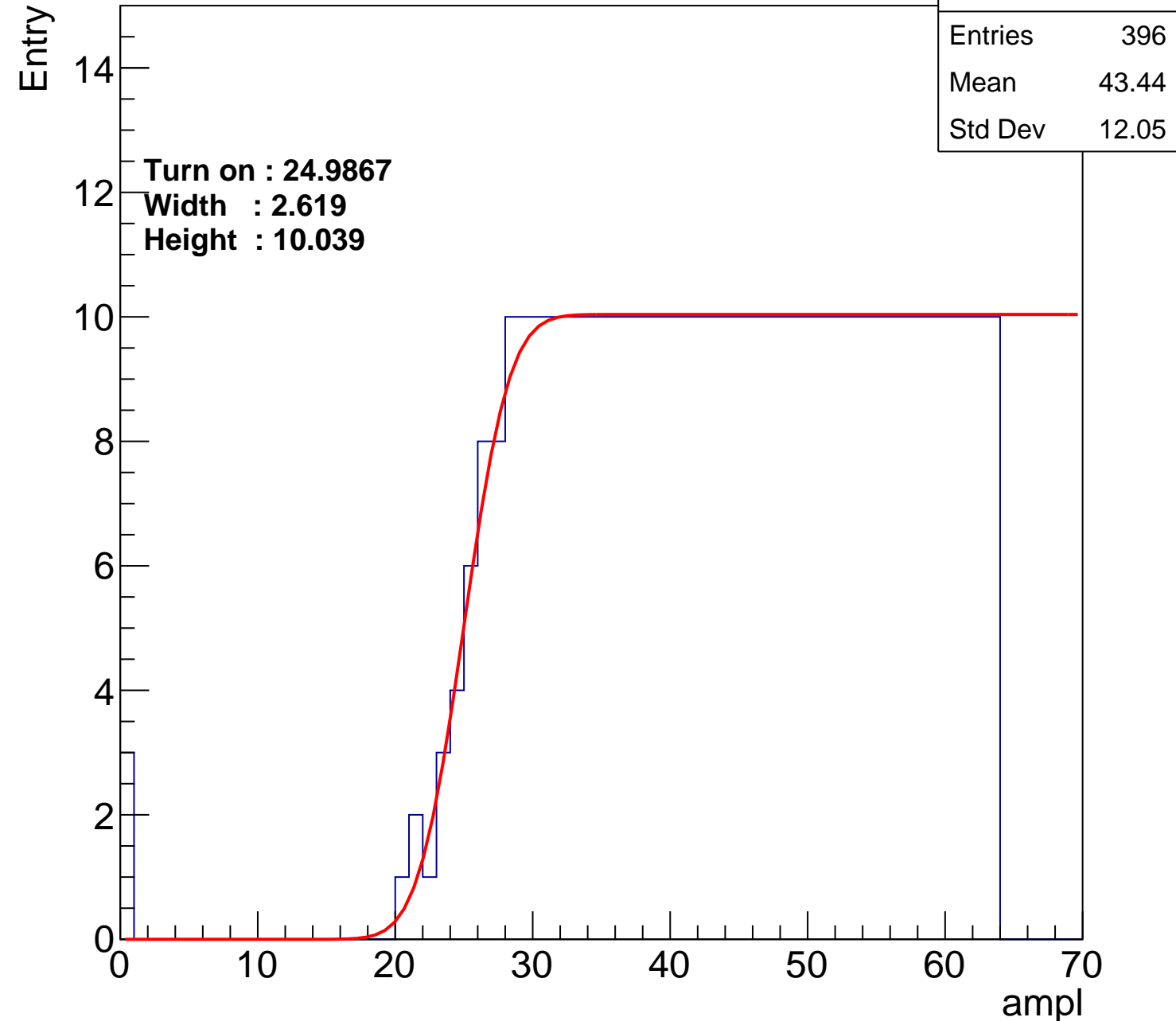
Width : 2.619

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.46
Std Dev	11.23

Turn on : 26.2773

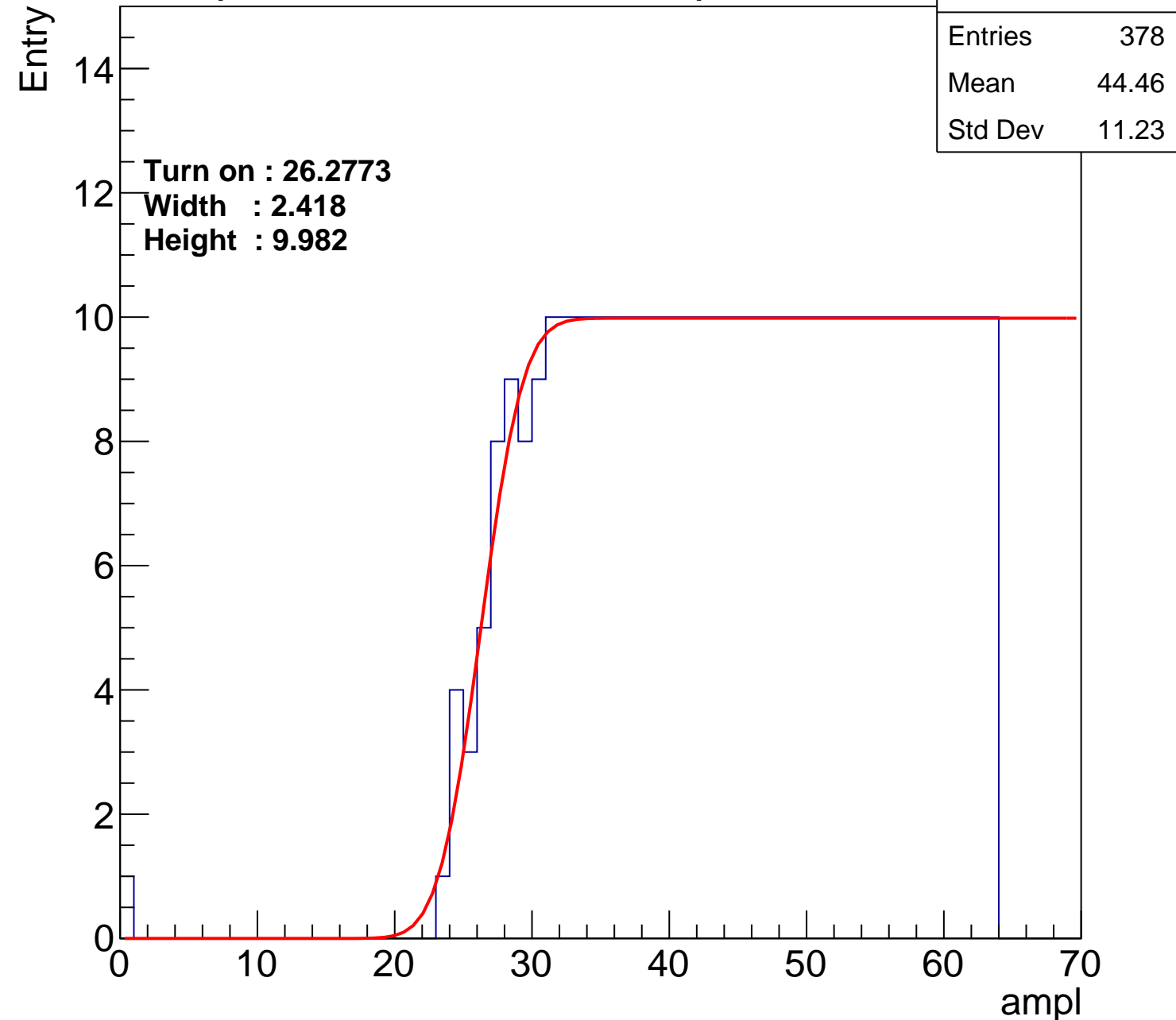
Width : 2.418

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.38
Std Dev	11.49

Turn on : 26.7760

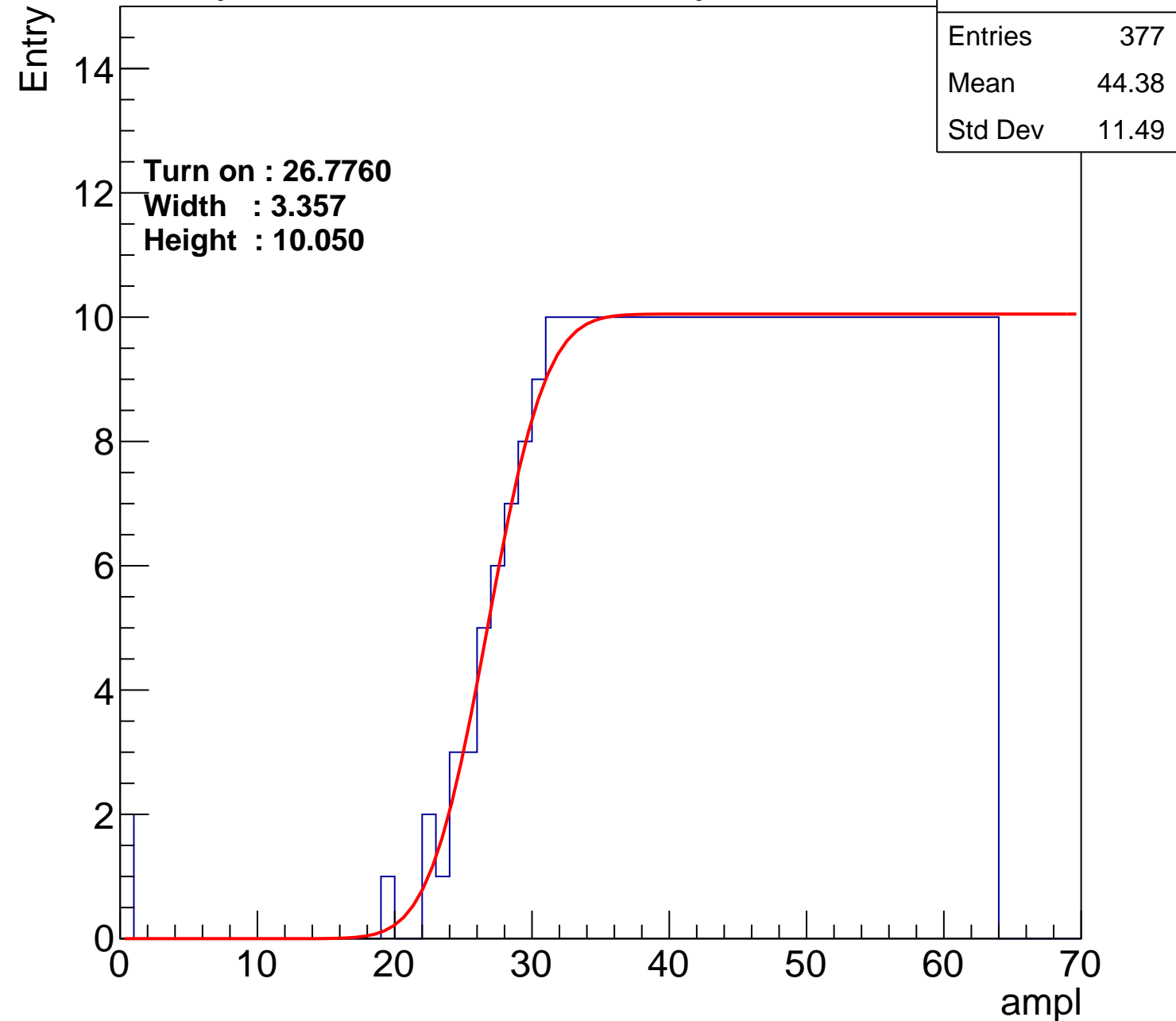
Width : 3.357

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.07
Std Dev	12.04

Turn on : 26.8561

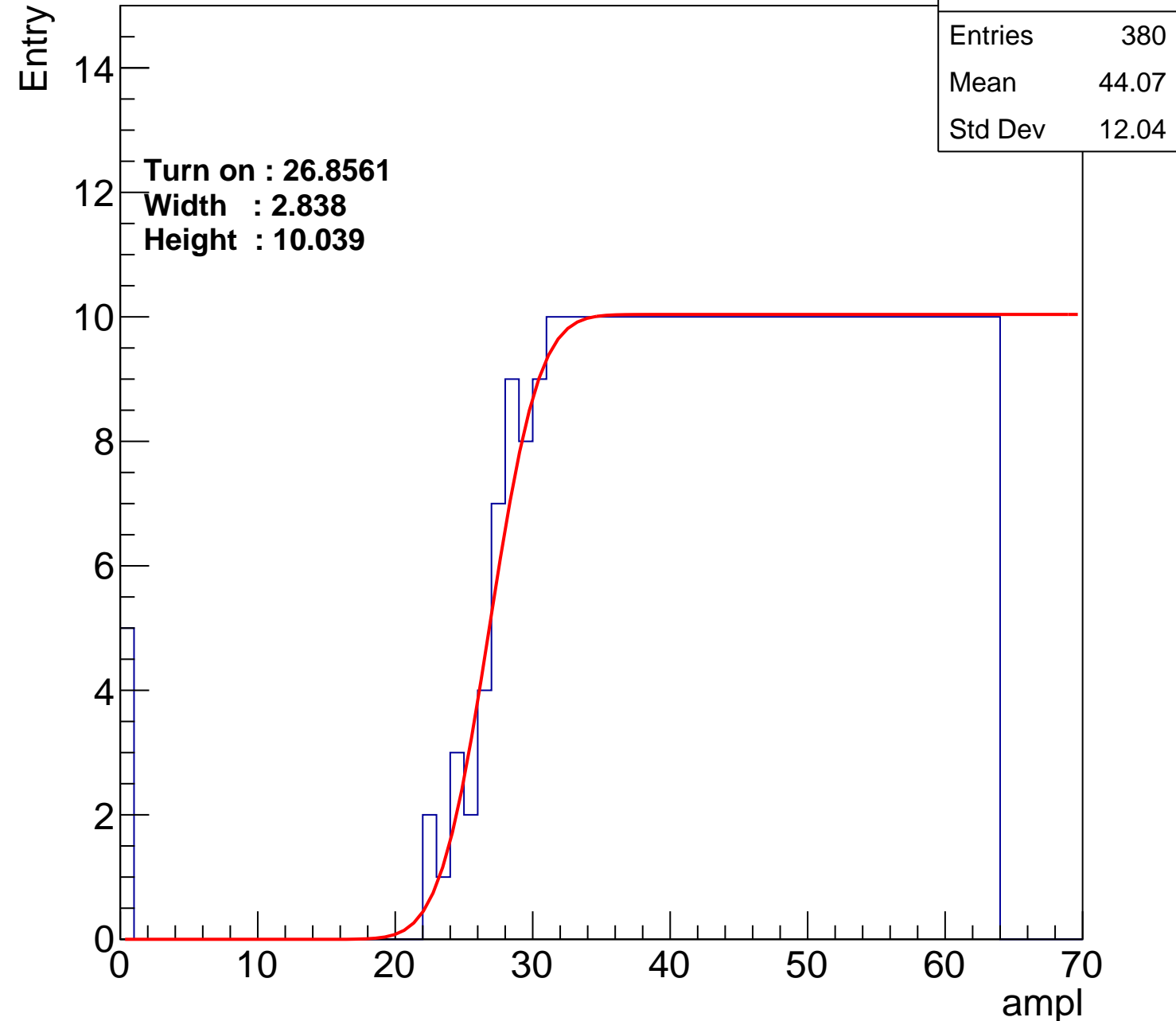
Width : 2.838

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.43
Std Dev	11.77

Turn on : 23.8541

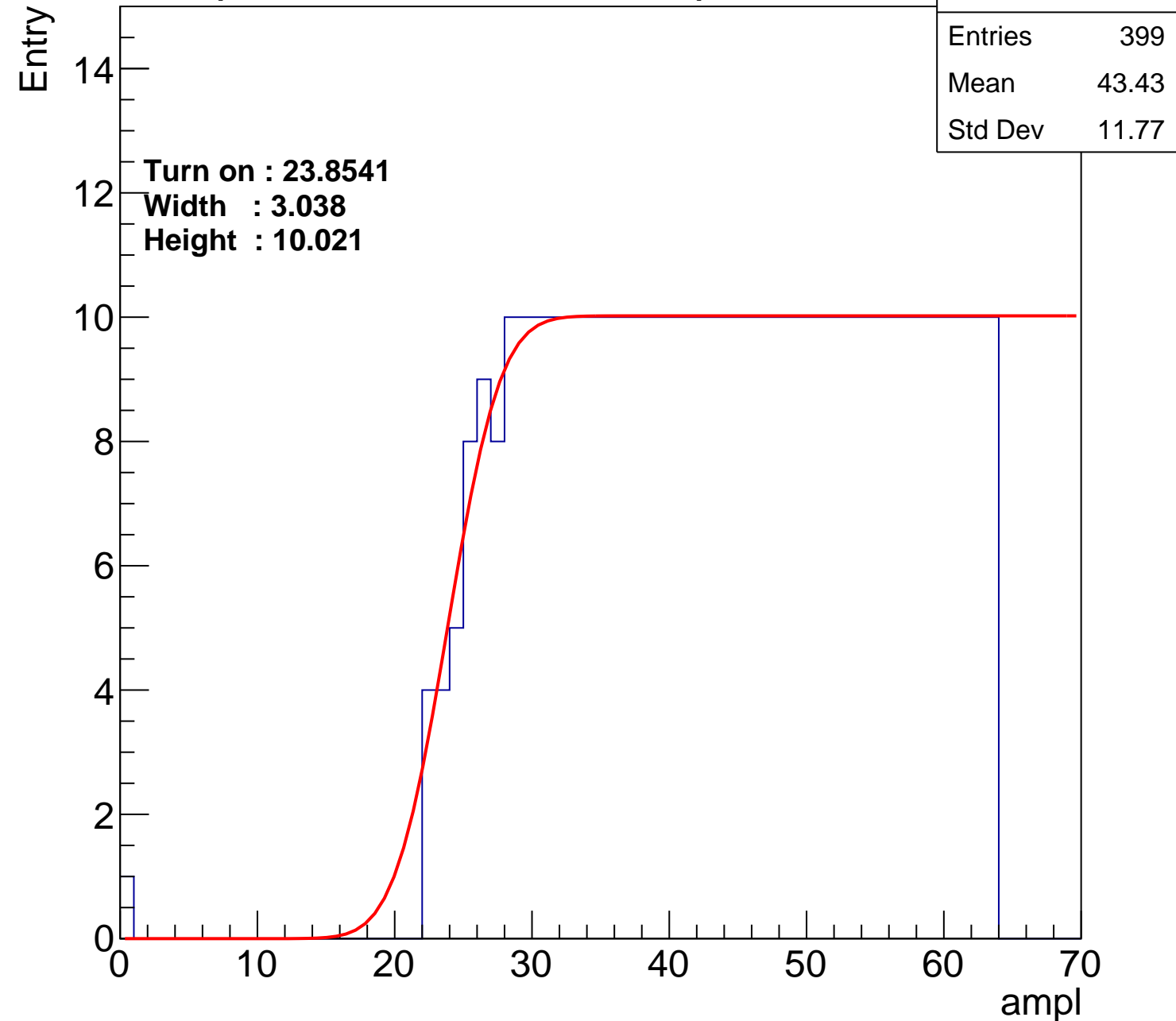
Width : 3.038

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.76
Std Dev	11.78

Turn on : 26.3941

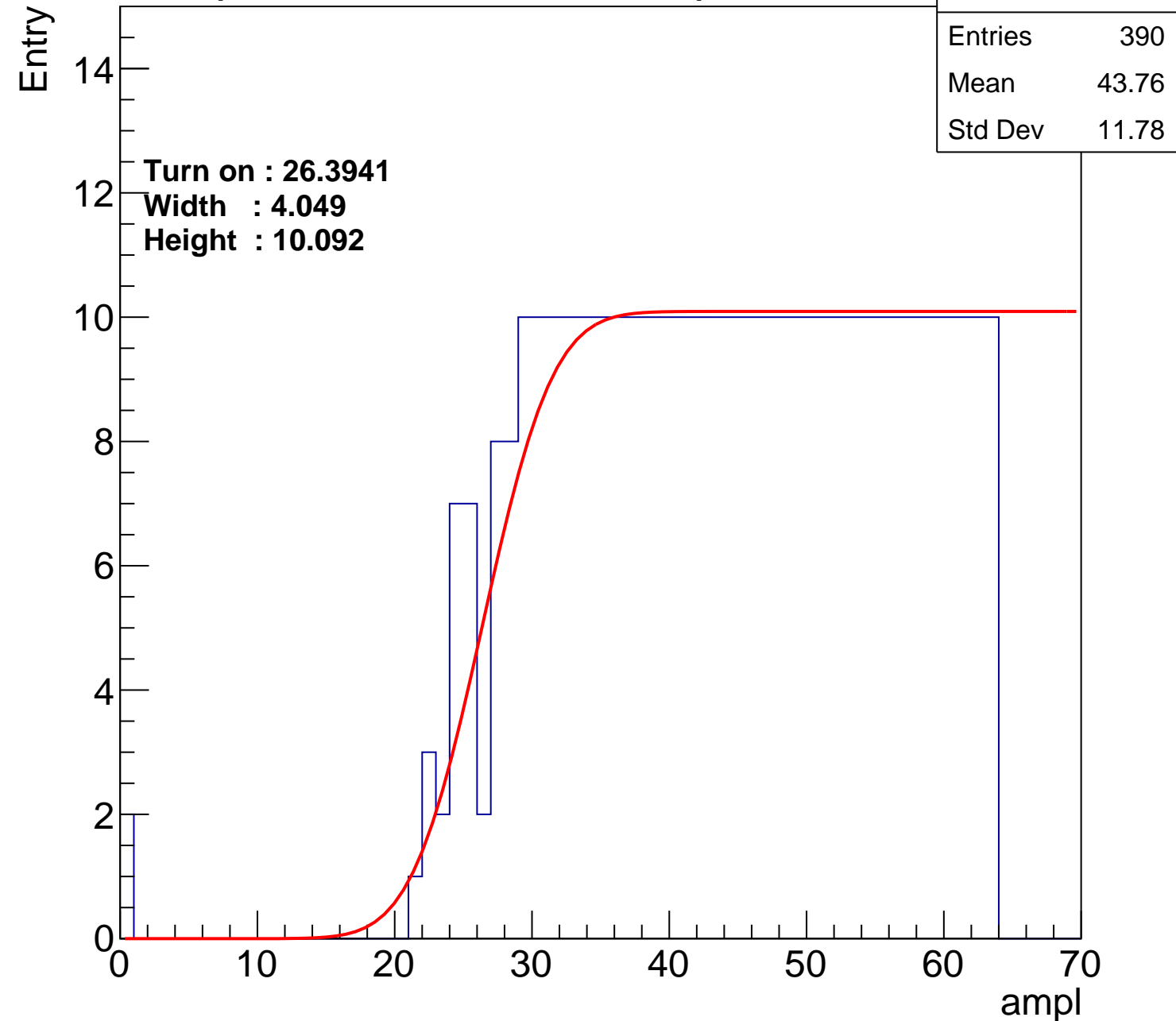
Width : 4.049

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch120

calib_packv5_042523_0143.root, FC#3, port B3

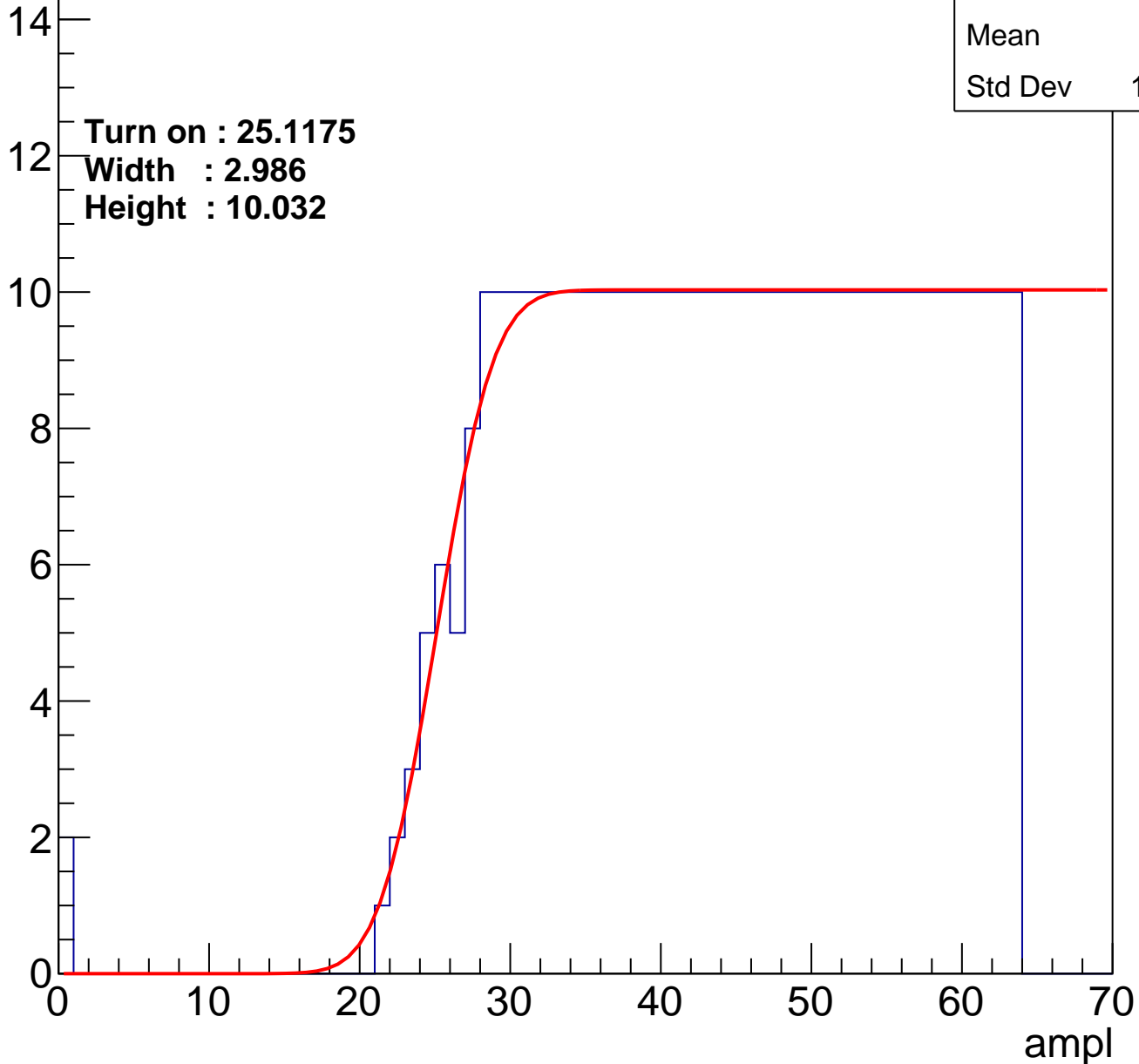
Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.1175

Width : 2.986

Height : 10.032

Entry



B1L000S, U14-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.2
Std Dev	11.56

Turn on : 26.2818

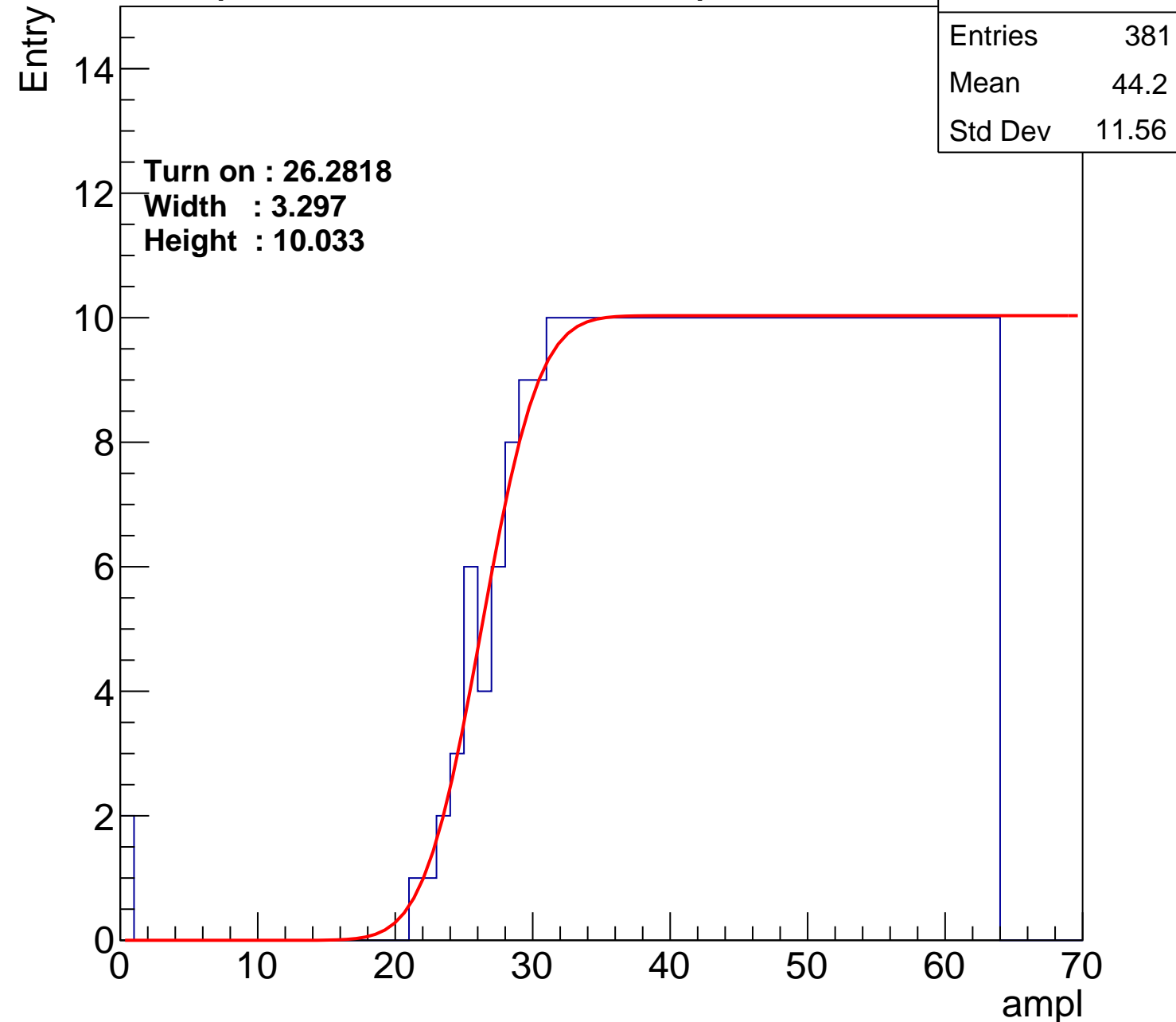
Width : 3.297

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch122

calib_packv5_042523_0143.root, FC#3, port B3

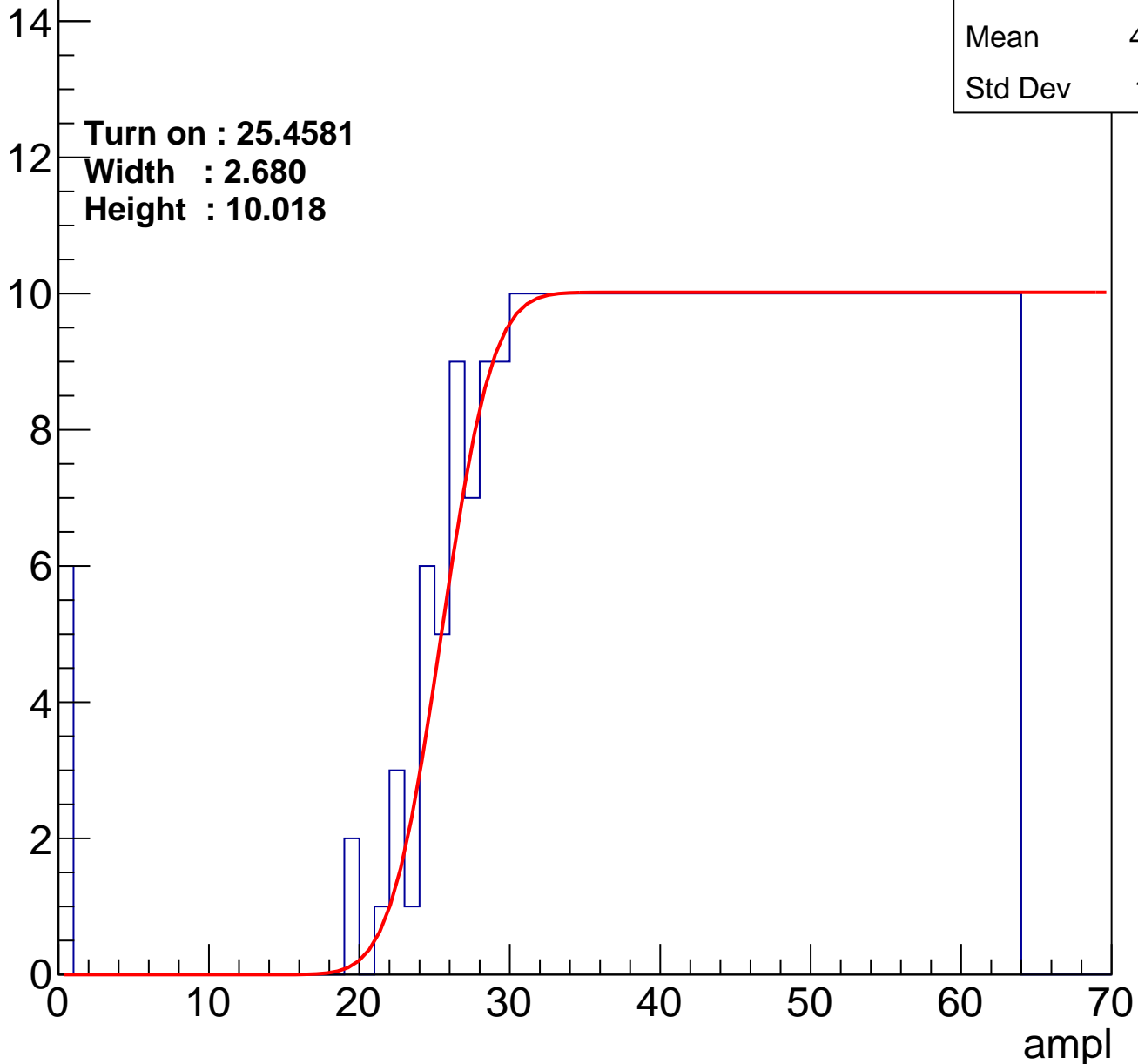
Entries	398
Mean	43.12
Std Dev	12.61

Turn on : 25.4581

Width : 2.680

Height : 10.018

Entry



B1L000S, U14-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.93
Std Dev	11.97

Turn on : 26.2606

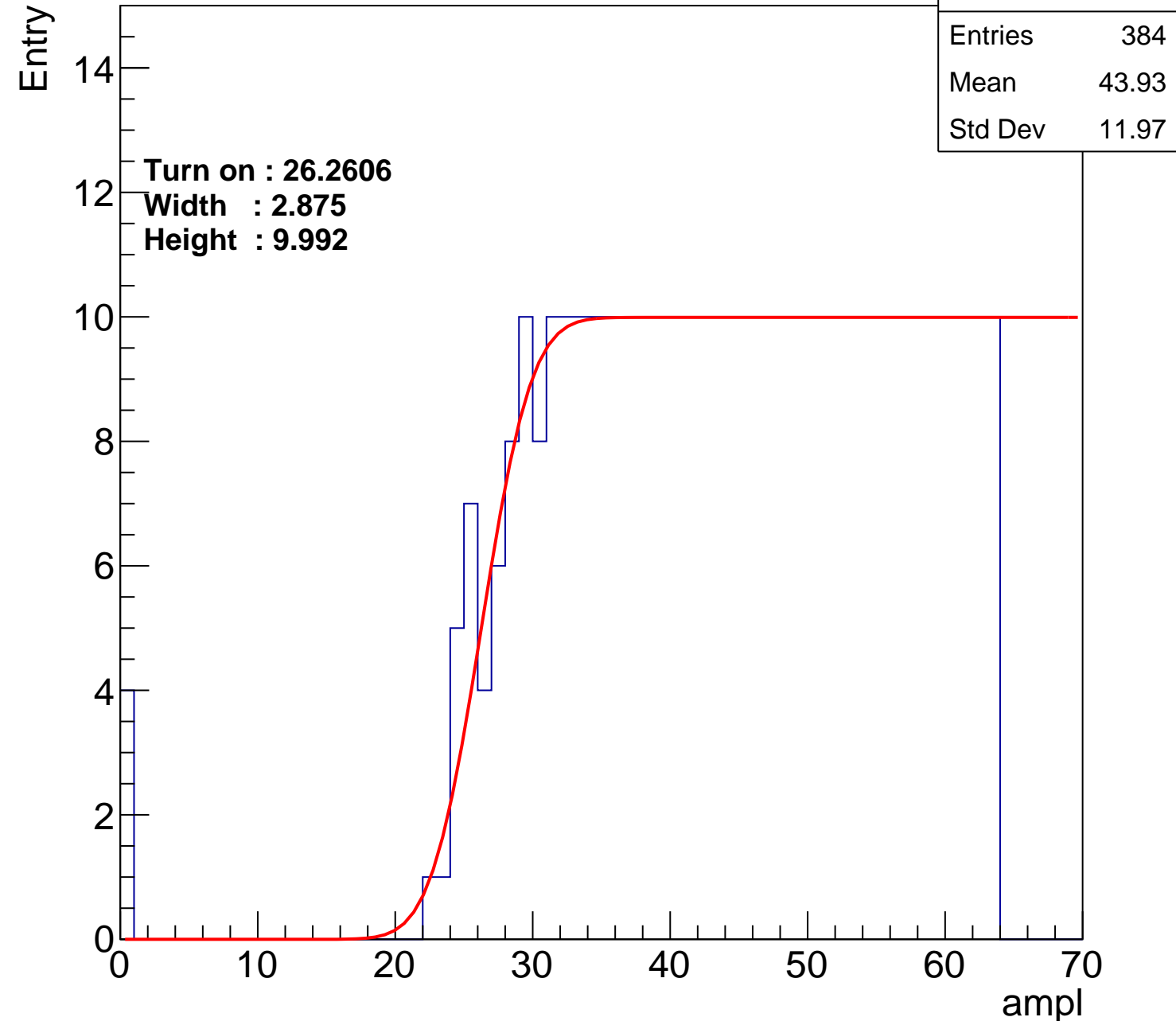
Width : 2.875

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.17
Std Dev	12.32

Turn on : 25.3782

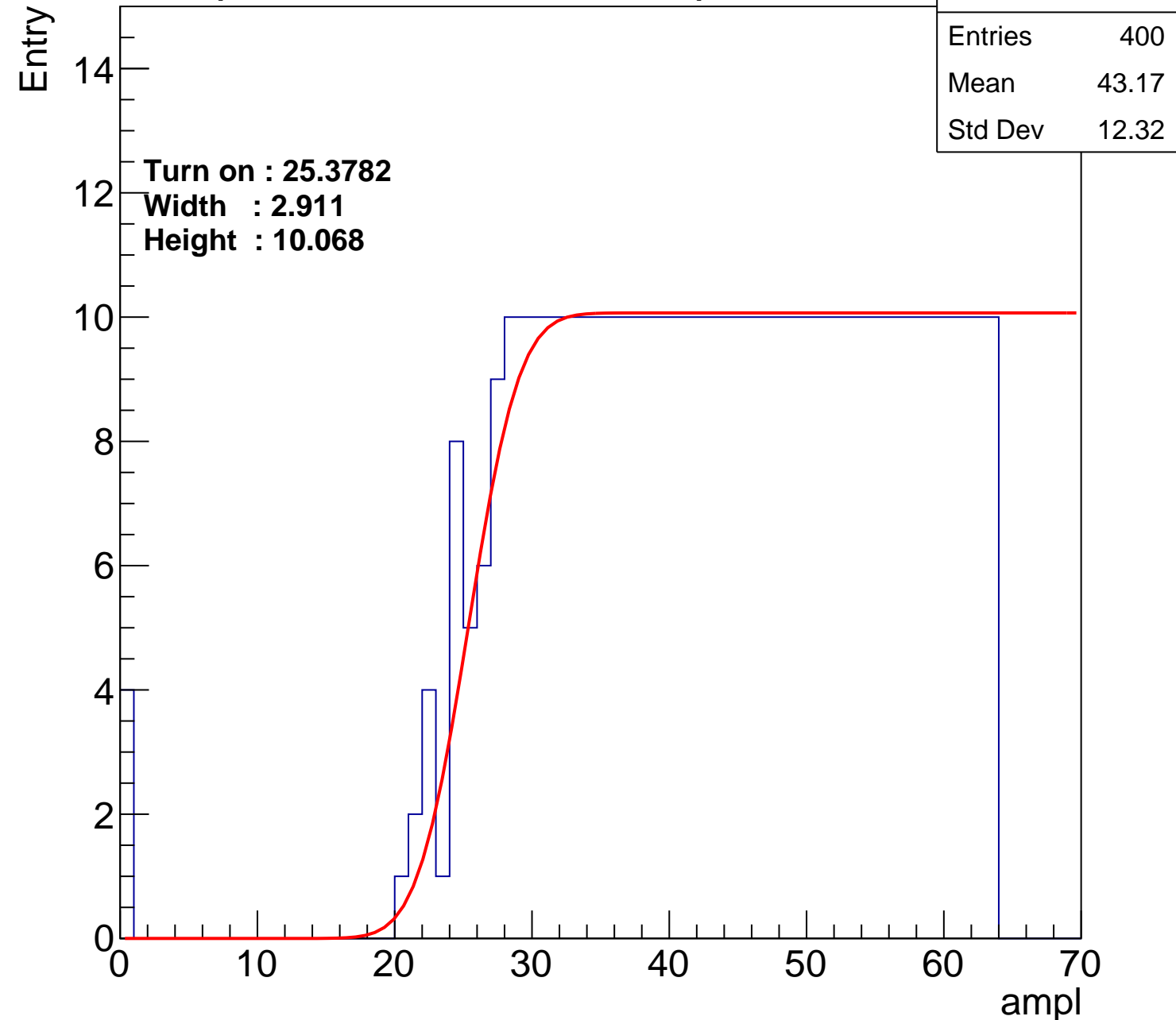
Width : 2.911

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.5665

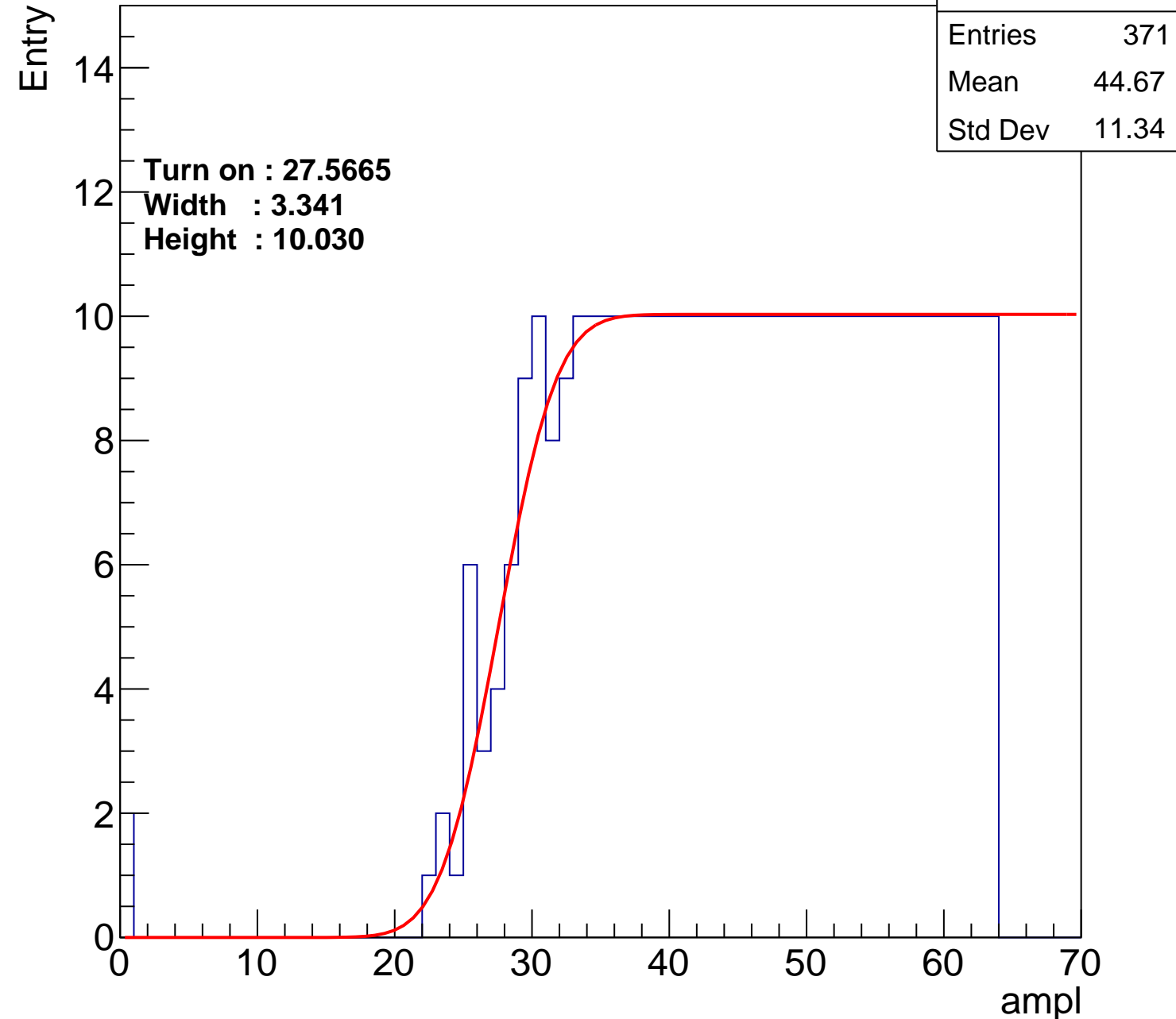
Width : 3.341

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U14-ch126

calib_packv5_042523_0143.root, FC#3, port B3

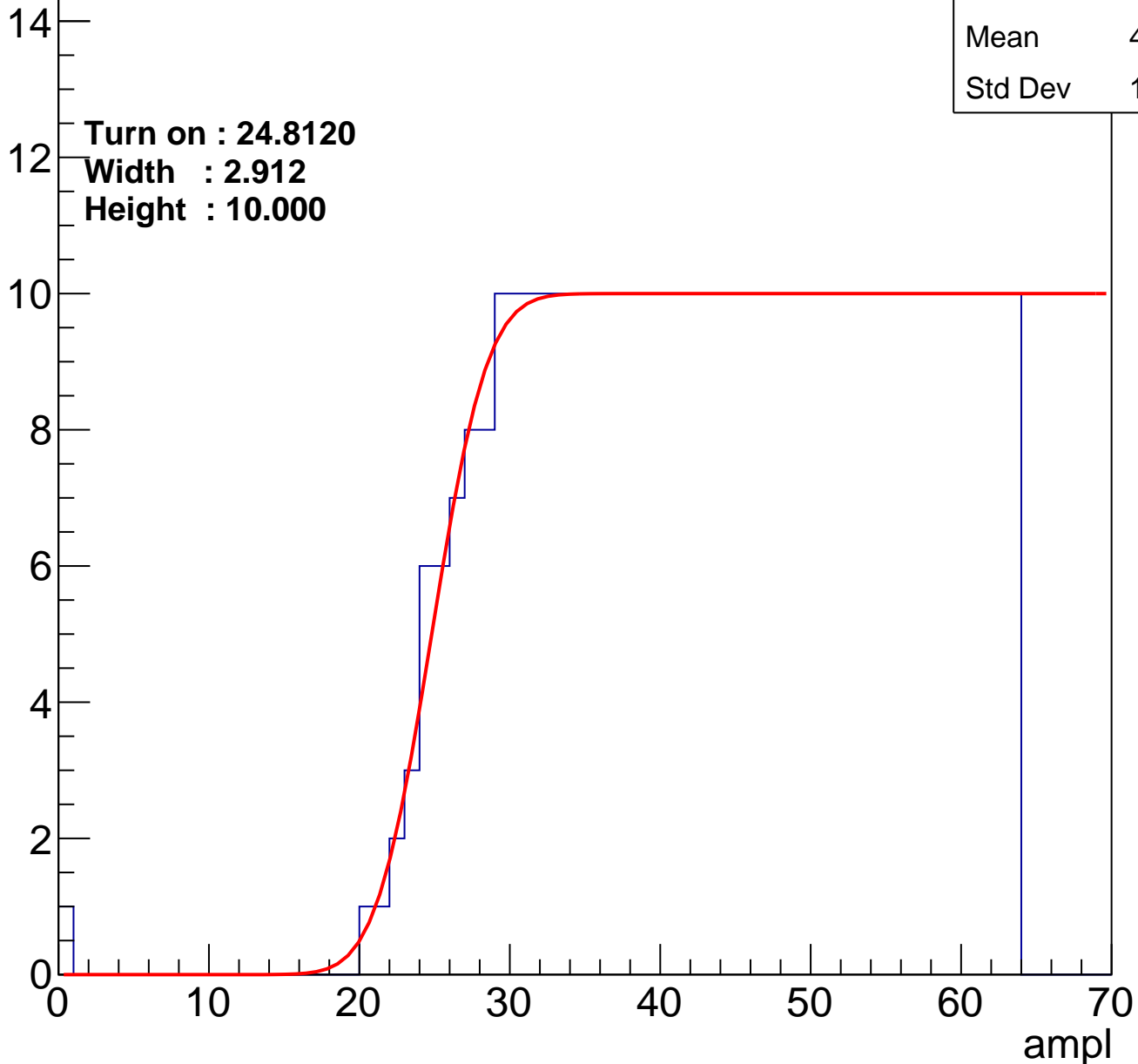
Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 24.8120

Width : 2.912

Height : 10.000

Entry



B1L000S, U14-ch127

calib_packv5_042523_0143.root, FC#3, port B3

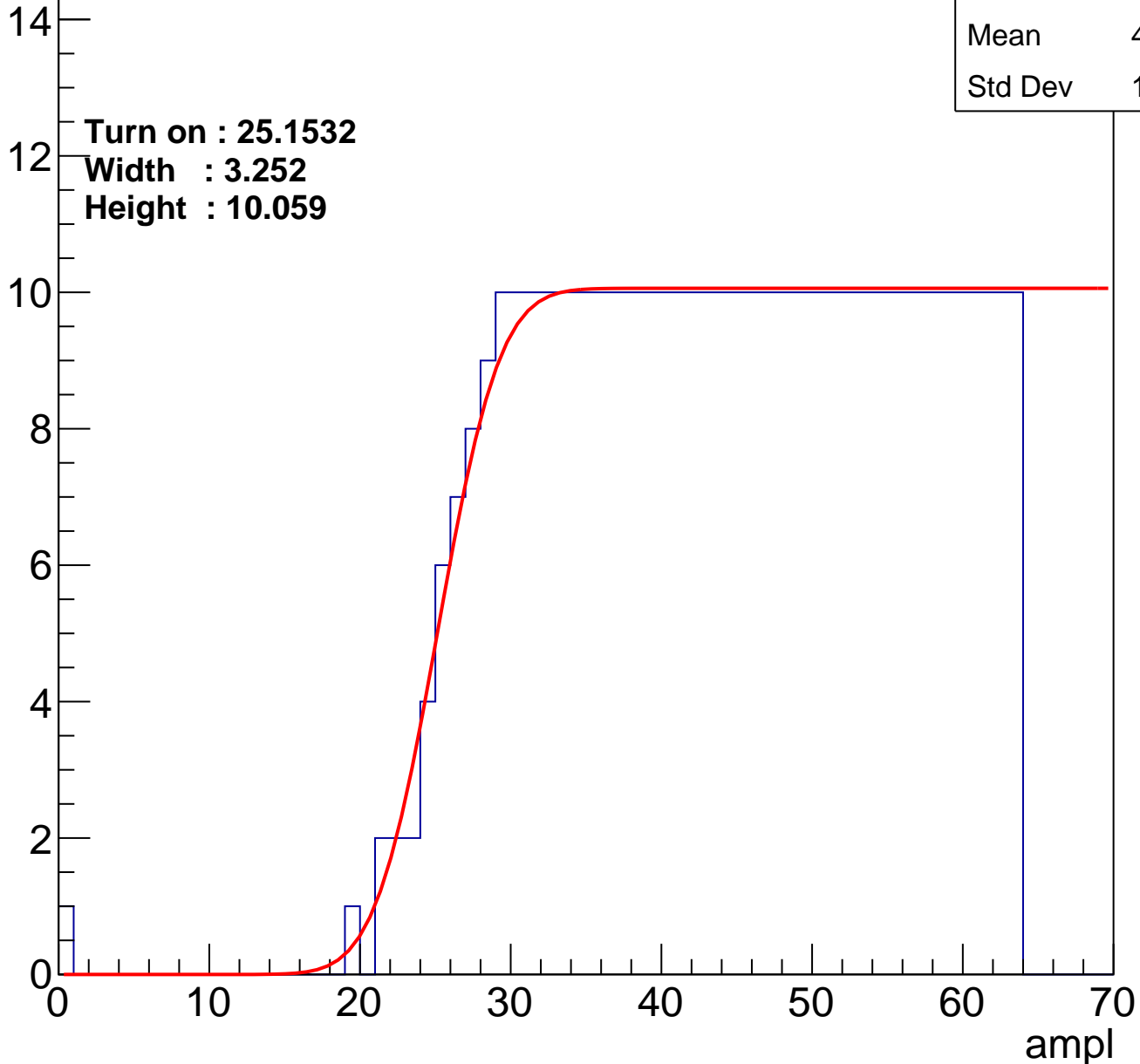
Entries	392
Mean	43.74
Std Dev	11.65

Turn on : 25.1532

Width : 3.252

Height : 10.059

Entry



B1L000S, U14-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.74
Std Dev	11.65

Turn on : 25.1532

Width : 3.252

Height : 10.059

Entry

