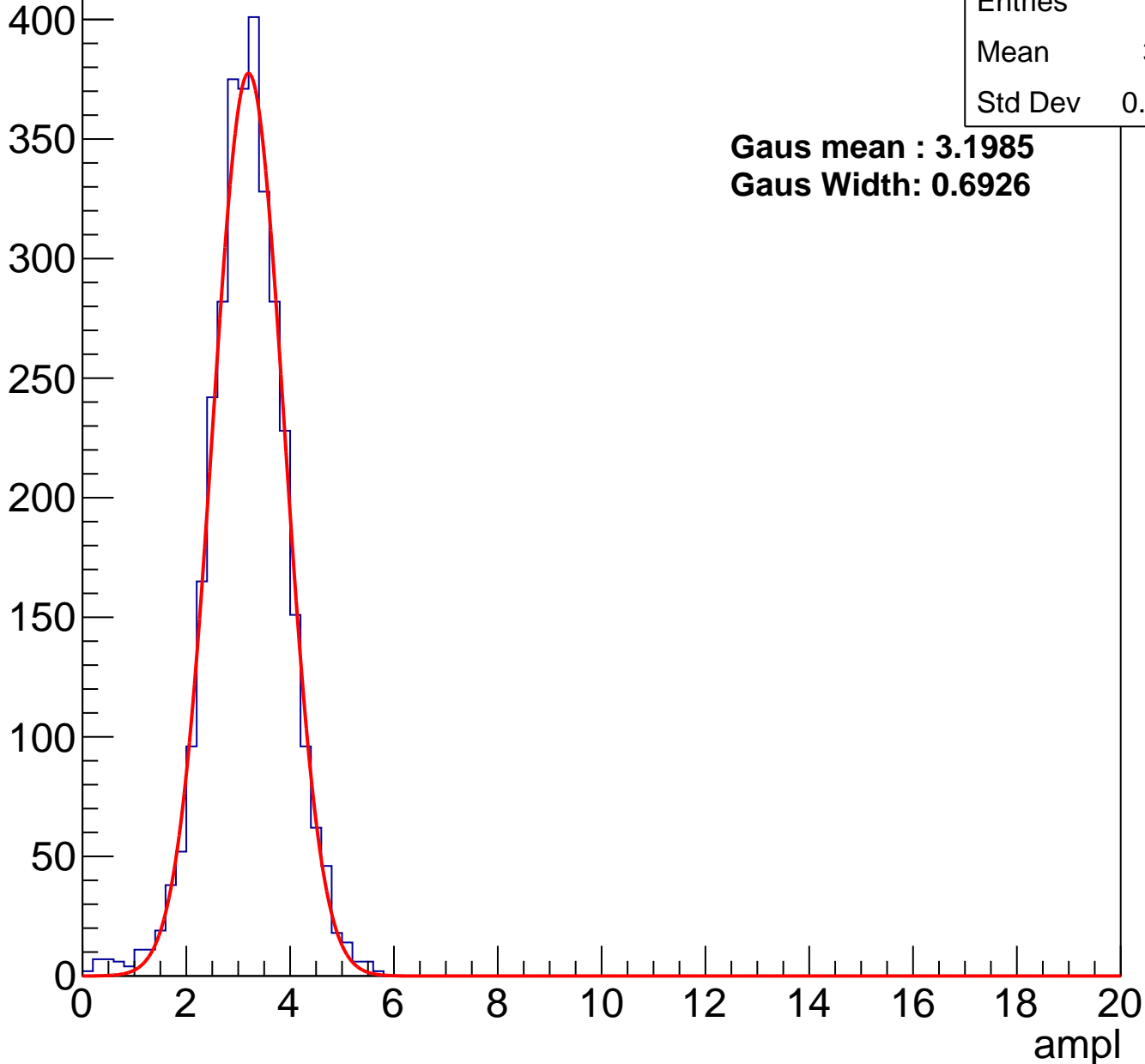


B1L101S, Turn ON width

calib_packv5_042523_0143.root, FC#0, port D2

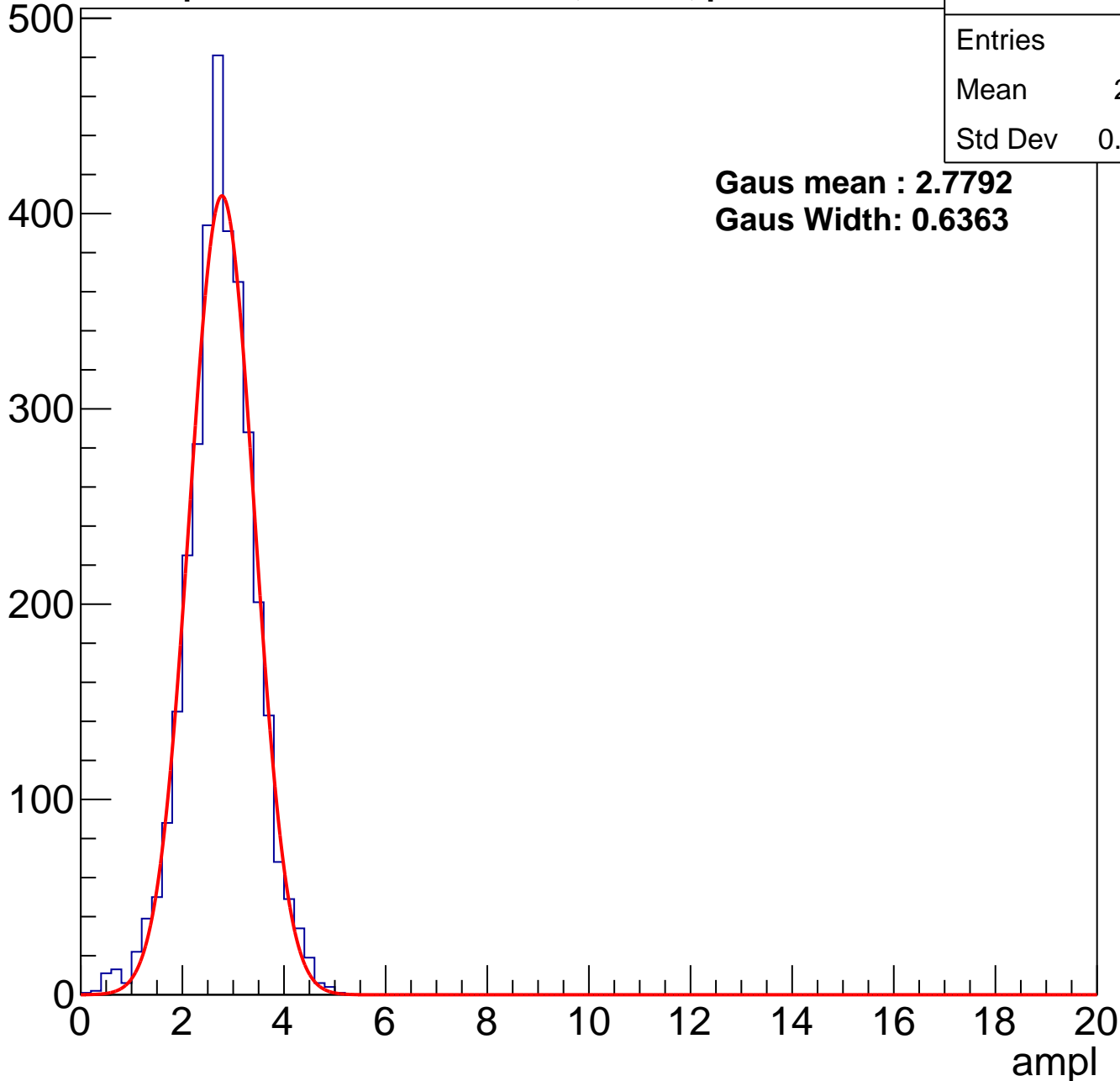
Events



B1L002S, Turn ON width

calib_packv5_042523_0143.root, FC#10, port B3

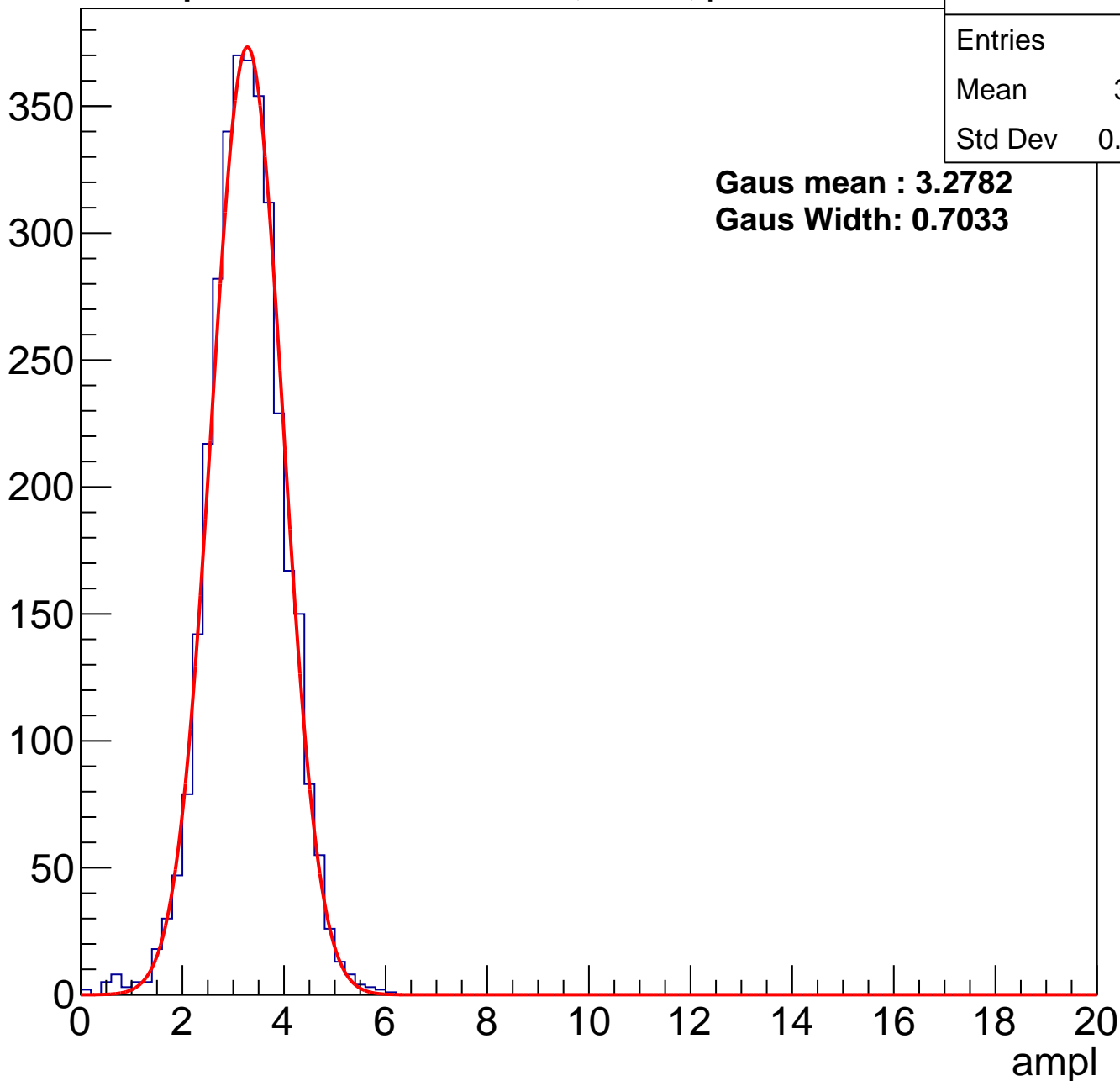
Events



B1L102S, Turn ON width

calib_packv5_042523_0143.root, FC#11, port A2

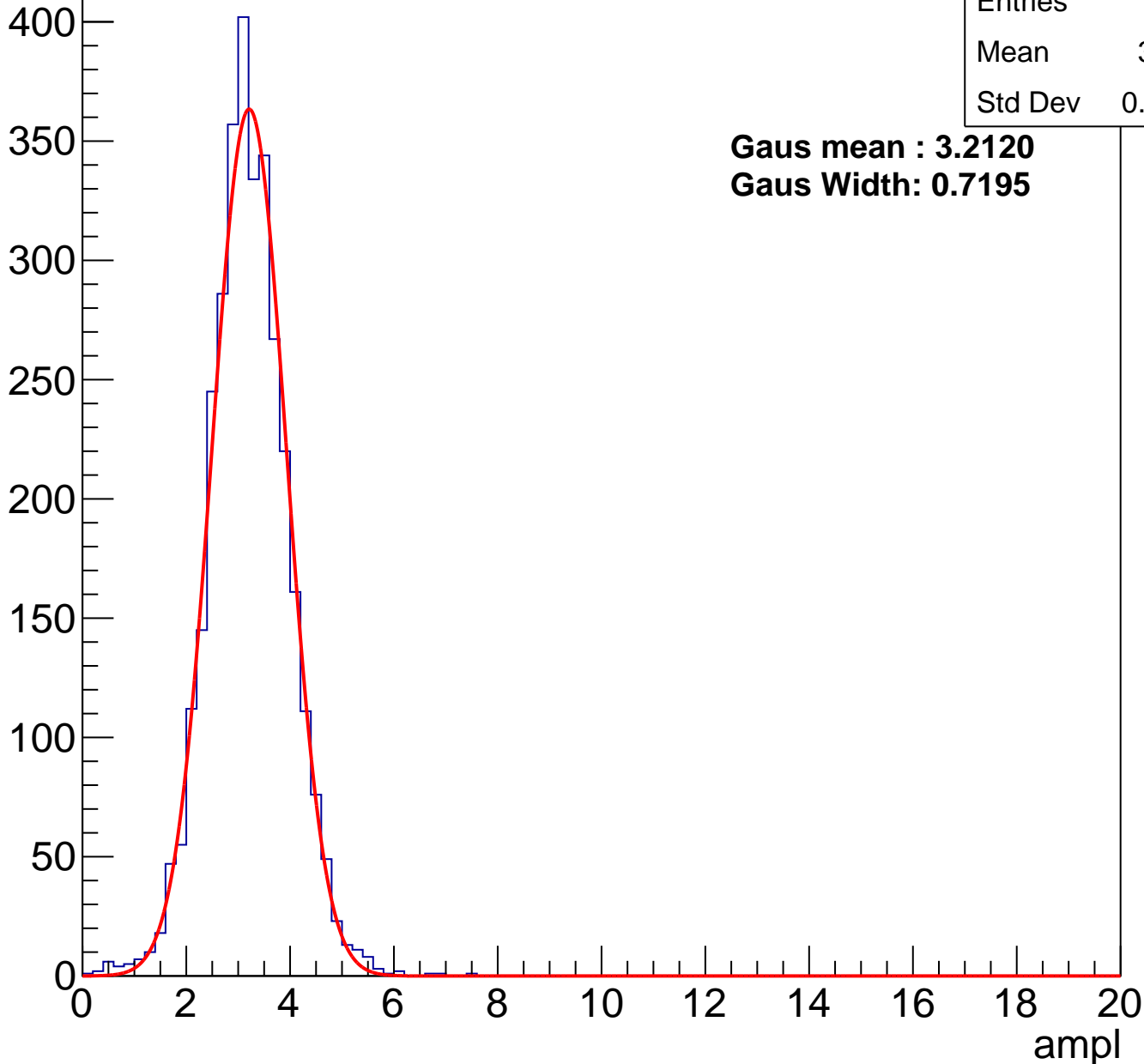
Events



B0L102S, Turn ON width

calib_packv5_042523_0143.root, FC#12, port B1

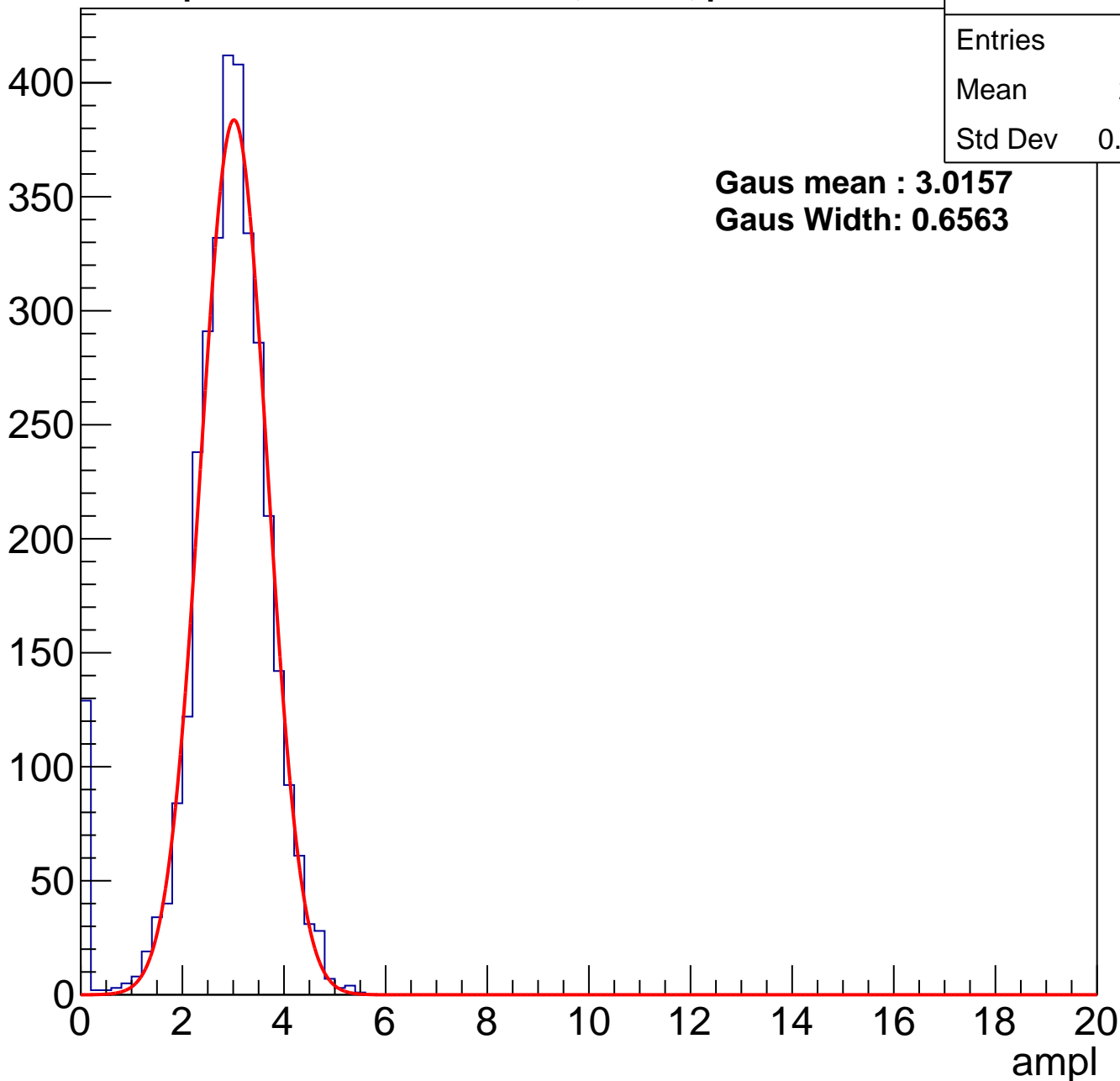
Events



B1L003S, Turn ON width

calib_packv5_042523_0143.root, FC#13, port D2

Events



B0L101S, Turn ON width

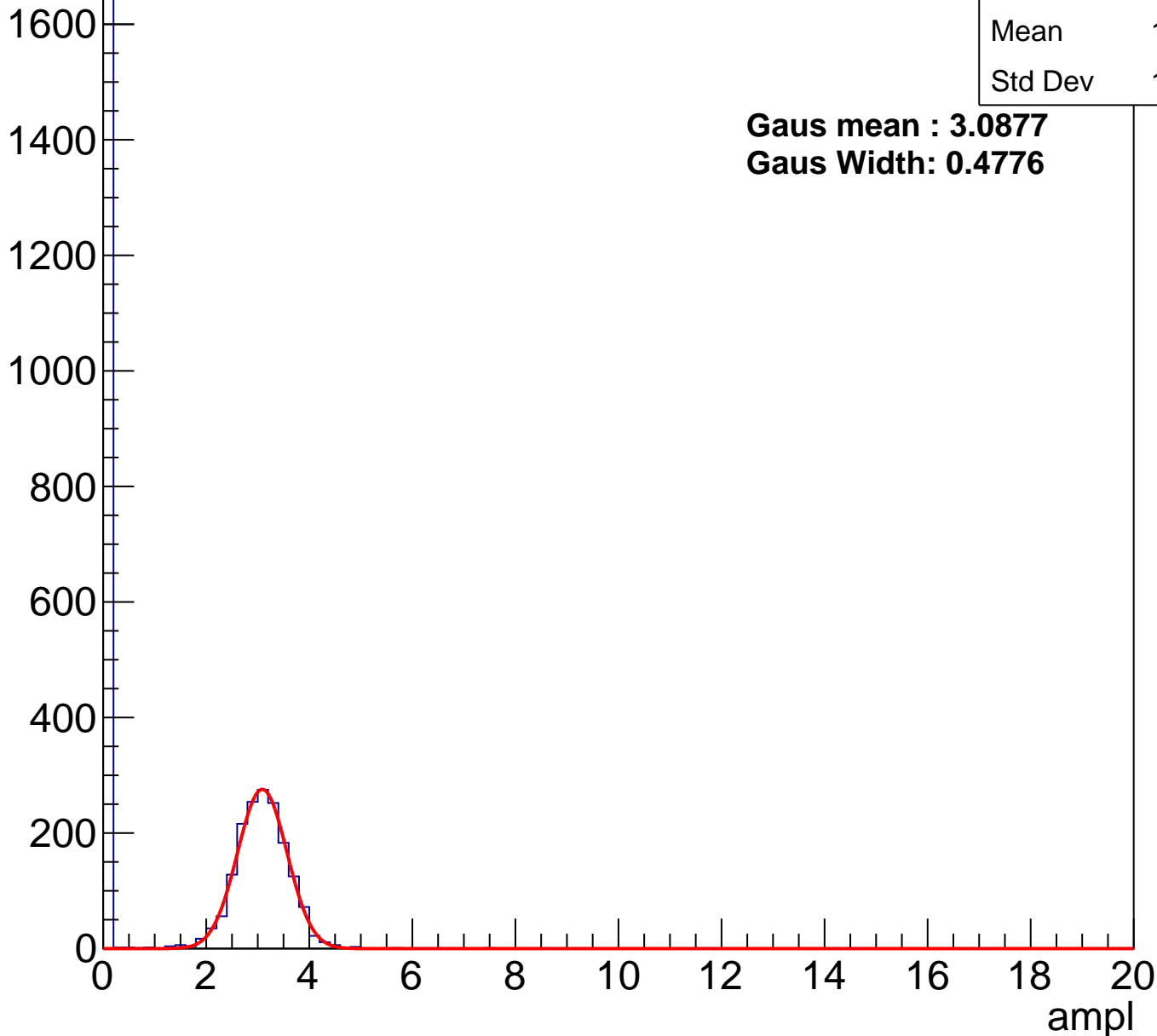
calib_packv5_042523_0143.root, FC#1, port C1

LVC0113

| | |
|---------|-------|
| Entries | 3328 |
| Mean | 1.553 |
| Std Dev | 1.584 |

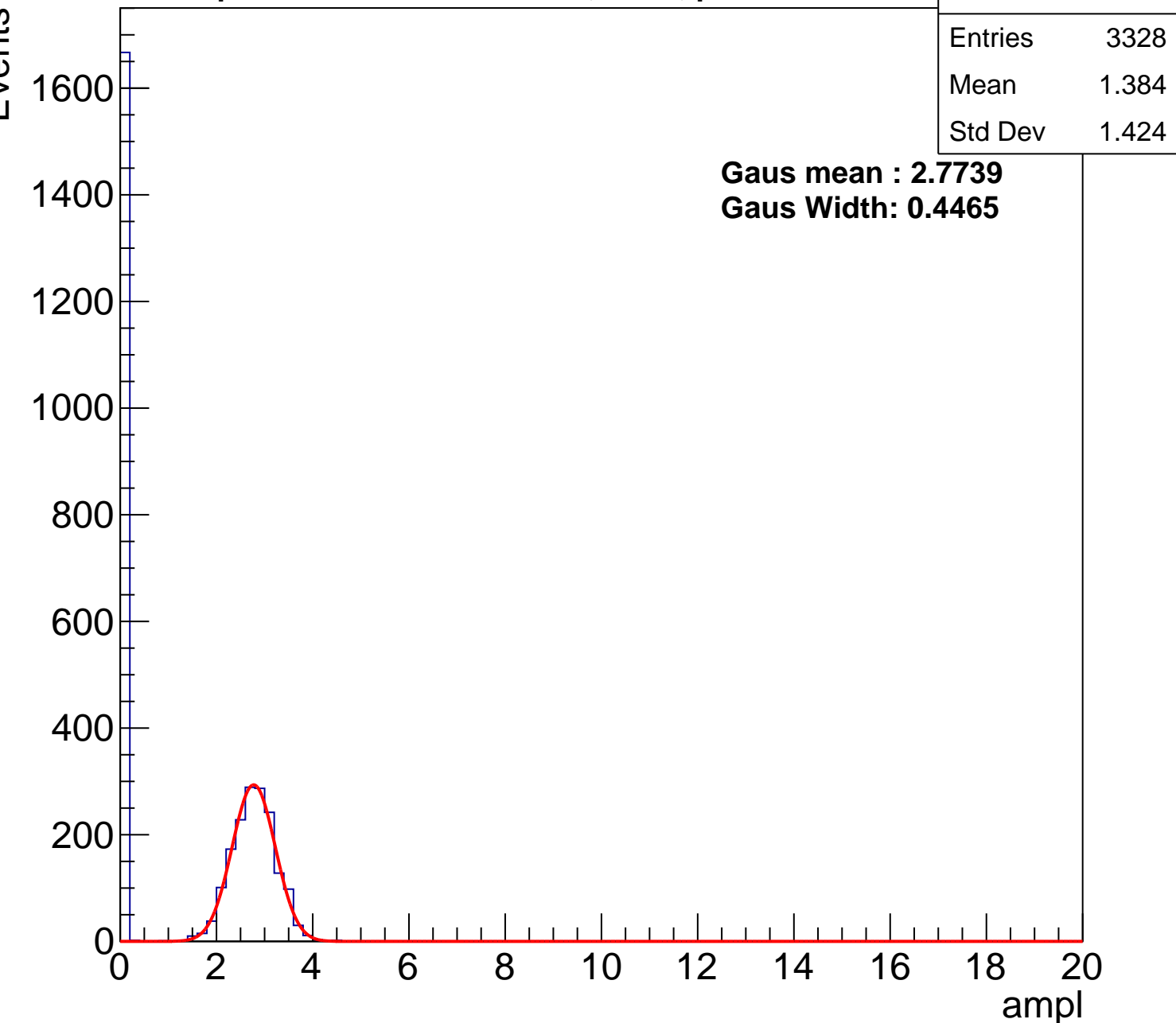
Gaus mean : 3.0877

Gaus Width: 0.4776



B1L001S, Turn ON width

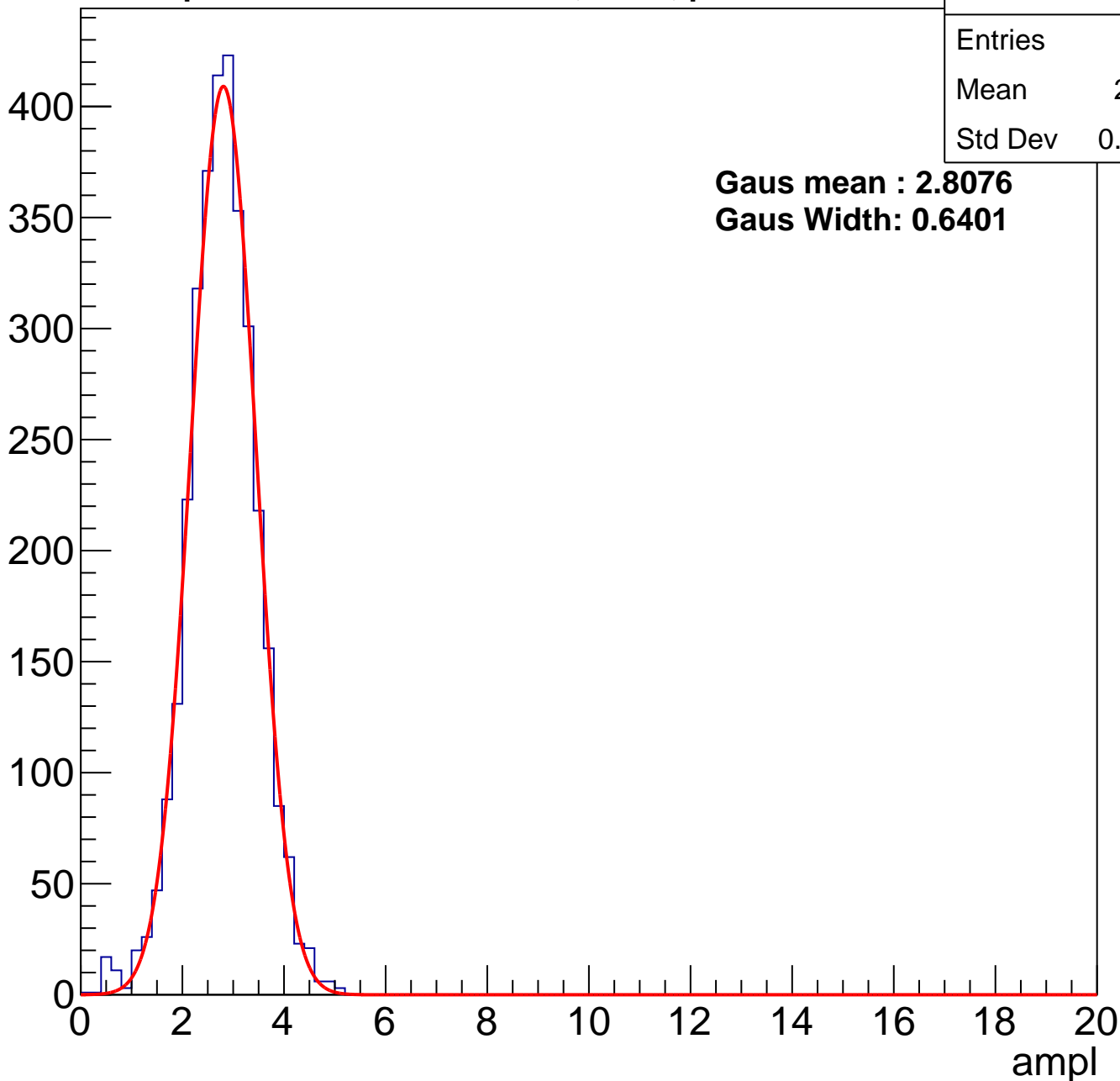
calib_packv5_042523_0143.root, FC#2, port C2



B1L000S, Turn ON width

calib_packv5_042523_0143.root, FC#3, port B3

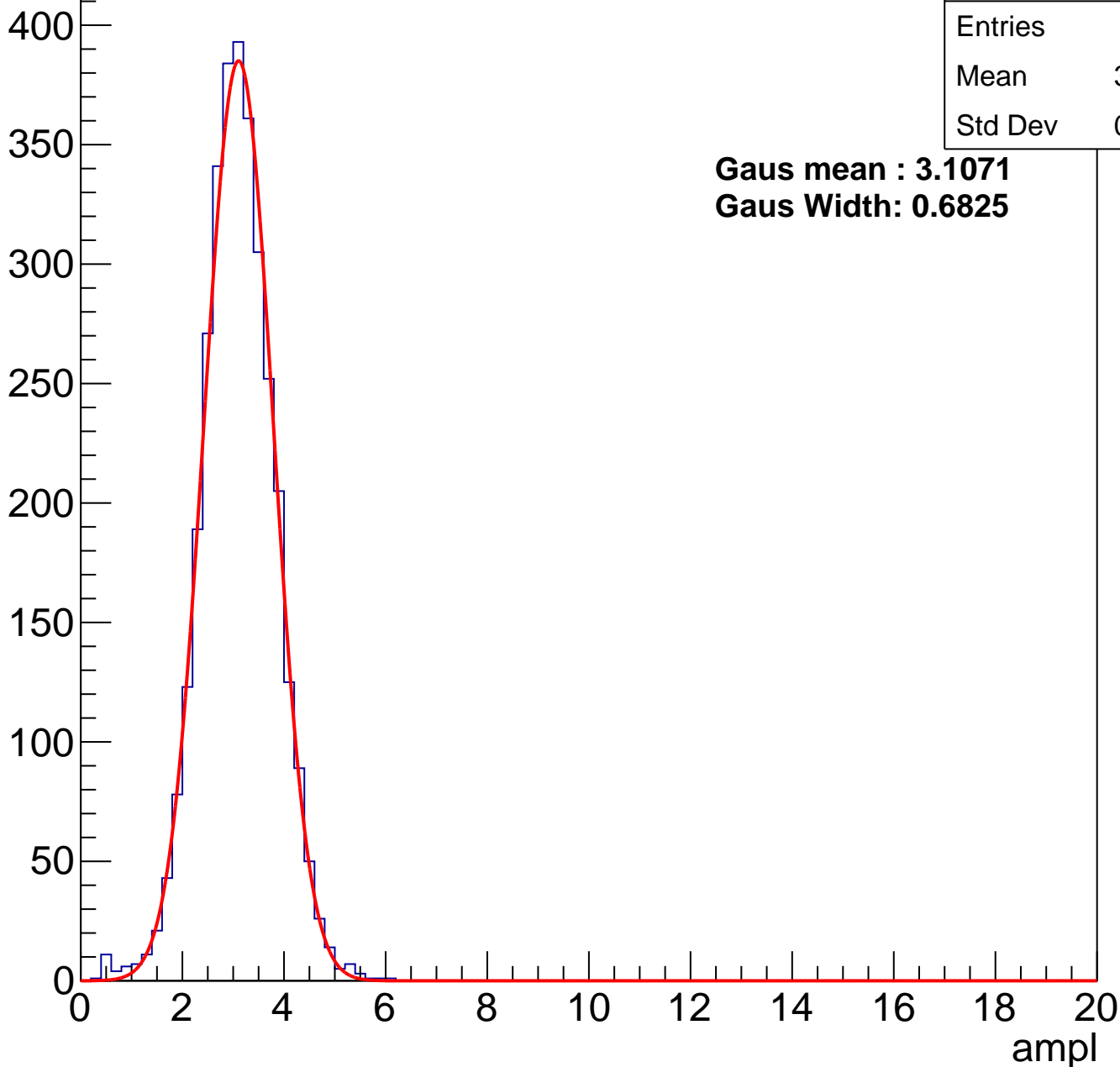
Events



B1L100S, Turn ON width

calib_packv5_042523_0143.root, FC#4, port A2

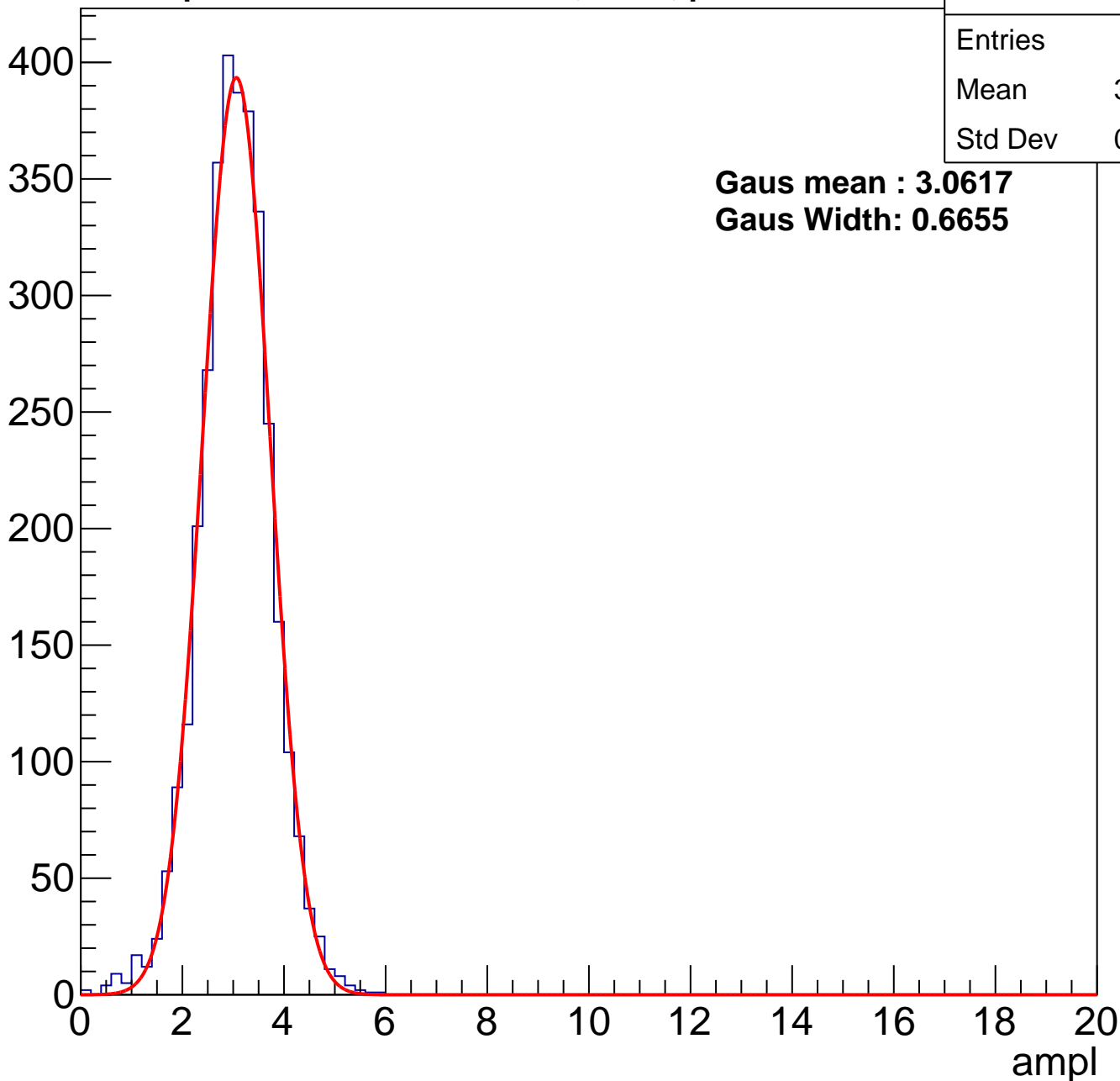
Events



B0L000S, Turn ON width

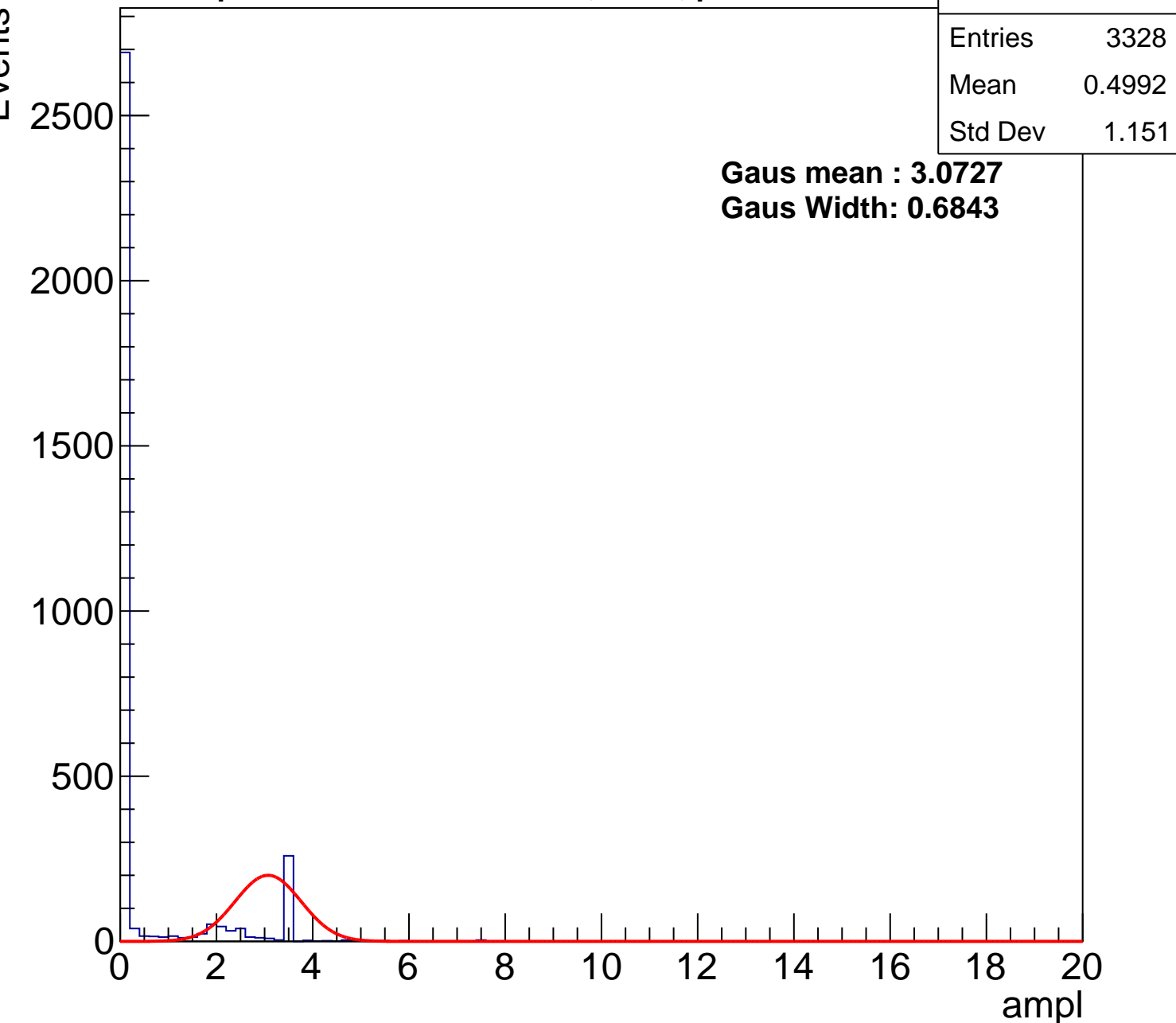
calib_packv5_042523_0143.root, FC#5, port B1

Events



B0L100S, Turn ON width

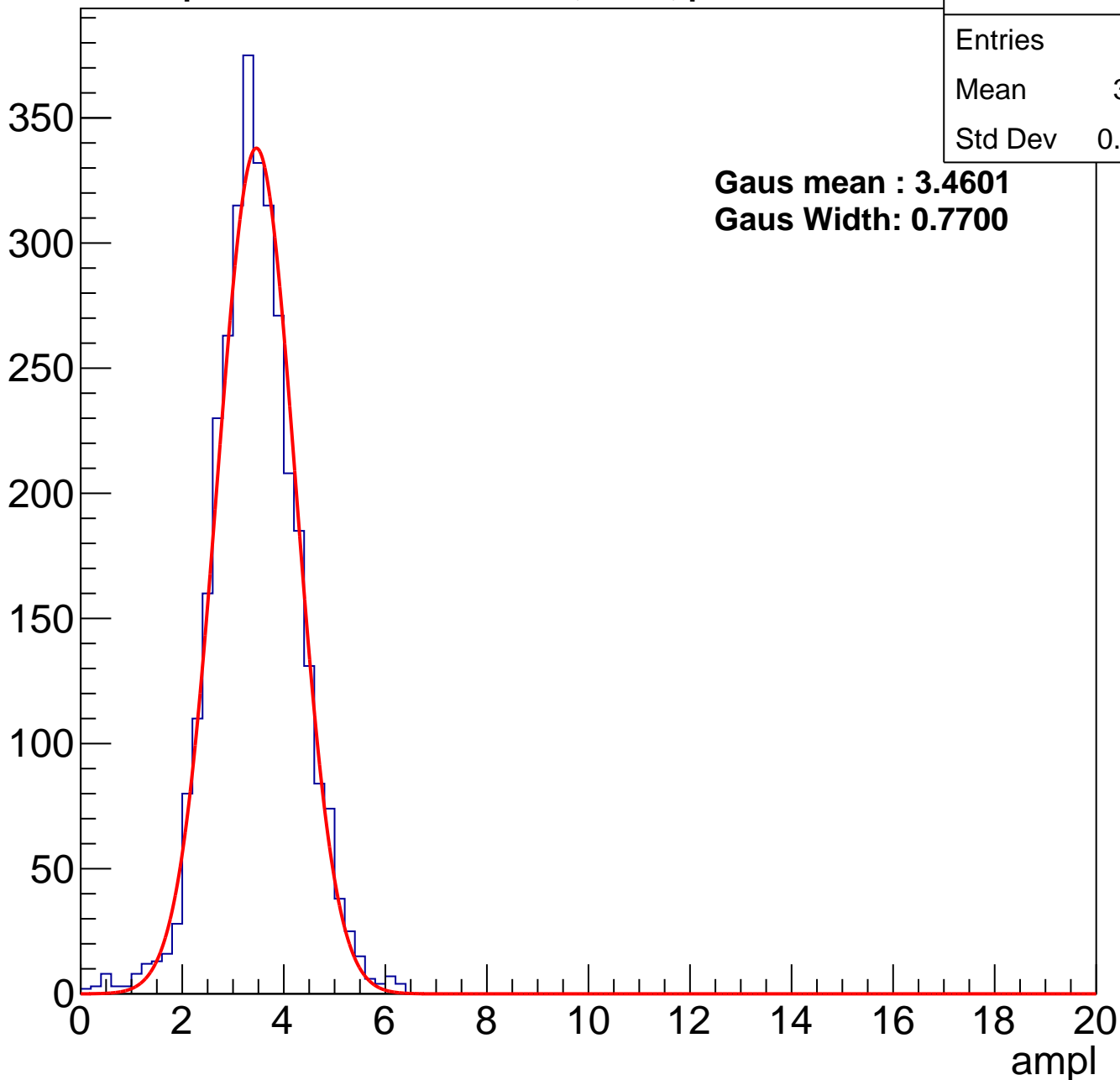
calib_packv5_042523_0143.root, FC#6, port A1



B1L103S, Turn ON width

calib_packv5_042523_0143.root, FC#7, port C2

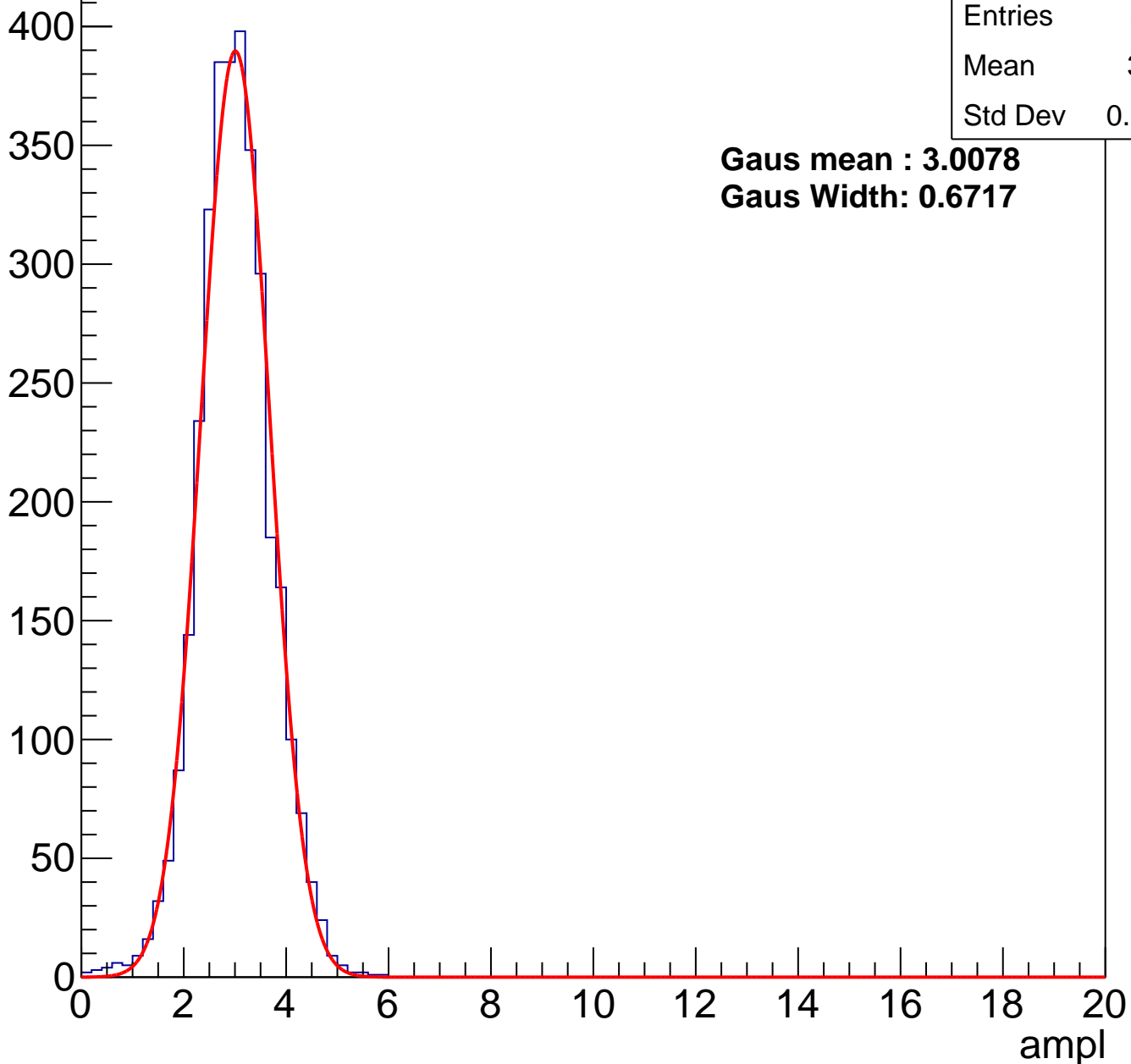
Events



B0L002S, Turn ON width

calib_packv5_042523_0143.root, FC#8, port C1

Events



B0L001S, Turn ON width

calib_packv5_042523_0143.root, FC#9, port A1

Events

