

B1L104S, U15-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	226
Mean	32.61
Std Dev	28.06

Turn on : 51.0399

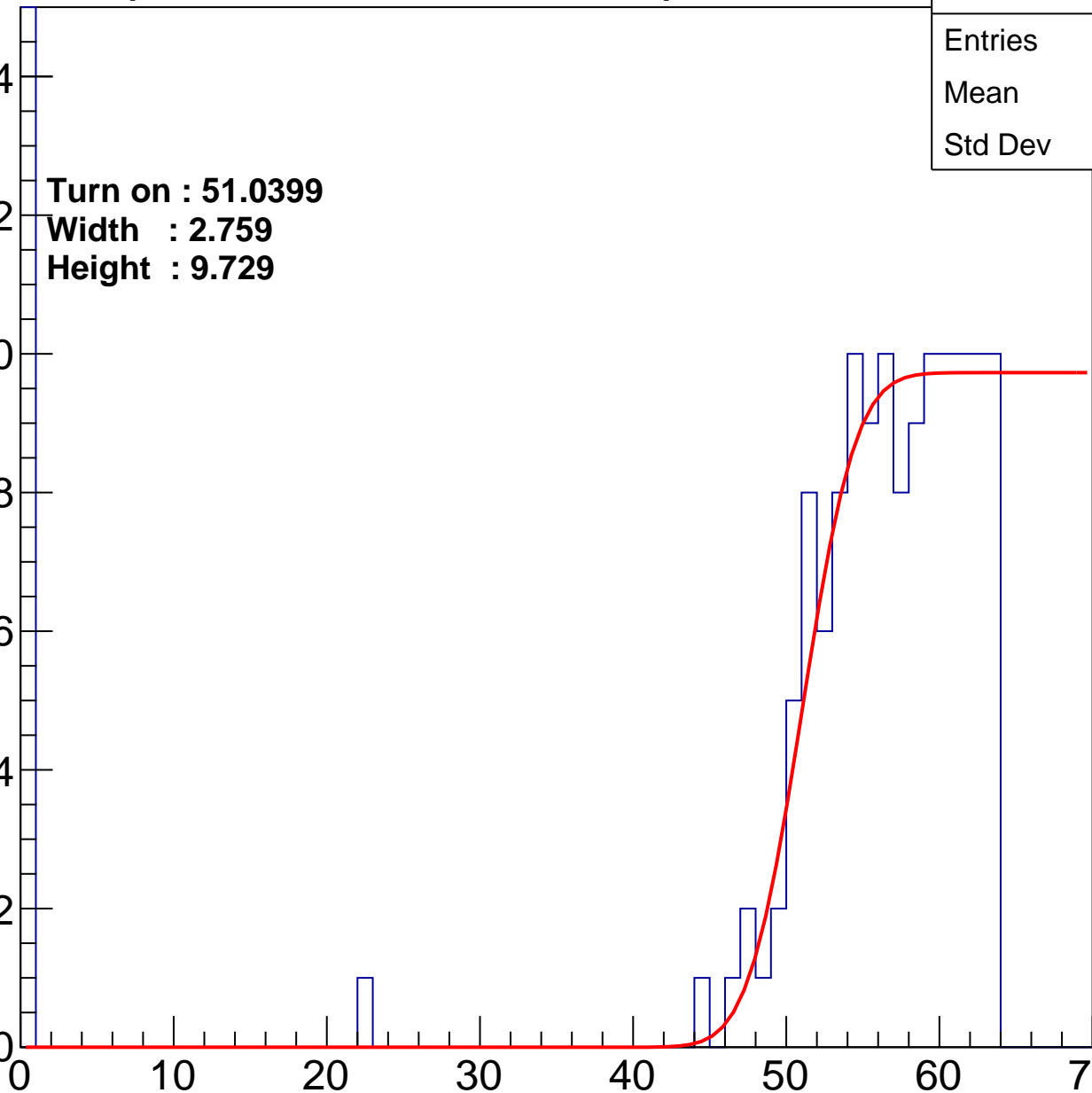
Width : 2.759

Height : 9.729

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	35.35
Std Dev	27.65

Turn on : 50.7431

Width : 2.311

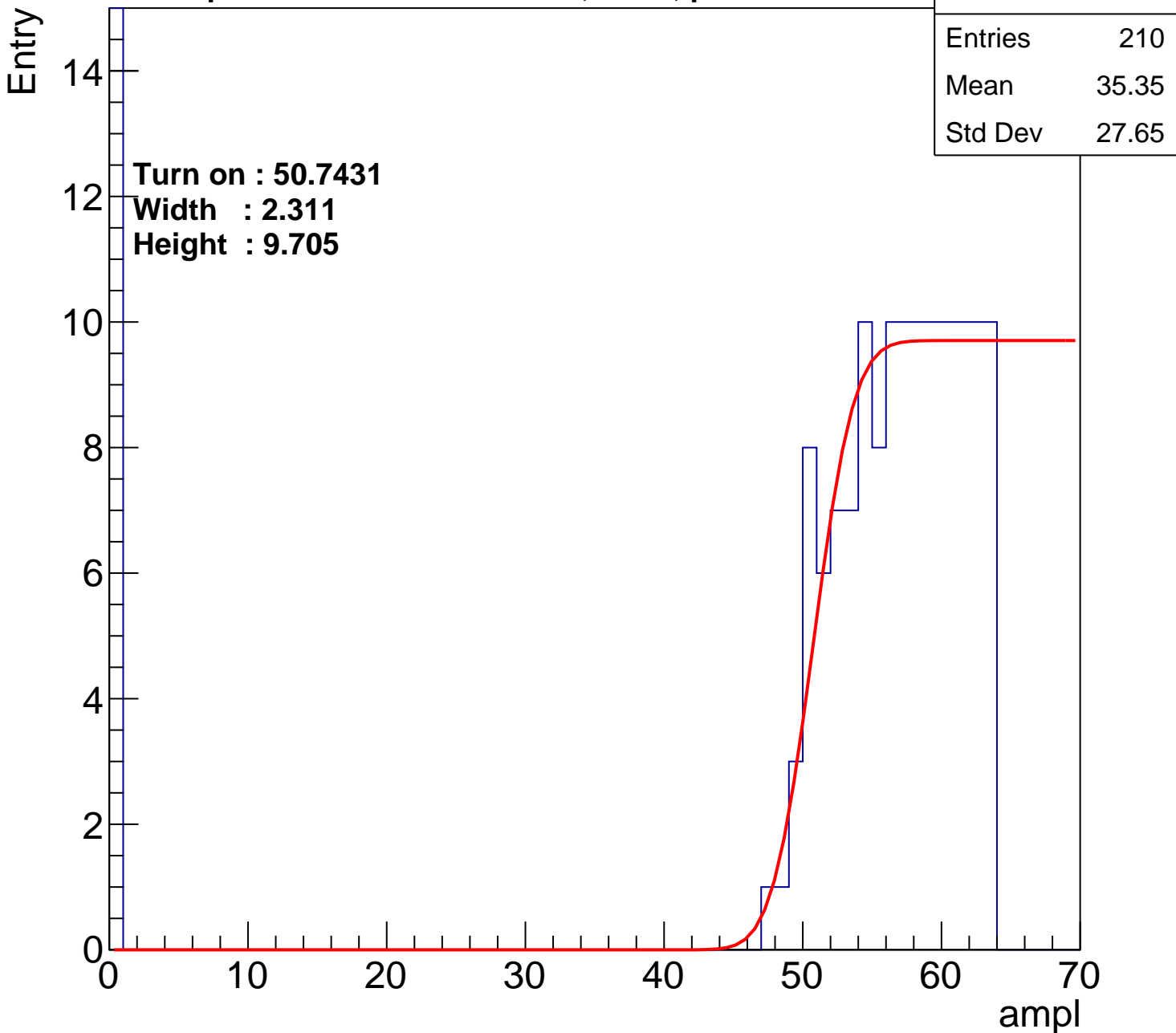
Height : 9.705

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U15-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	35.83
Std Dev	27.38

Turn on : 51.8091

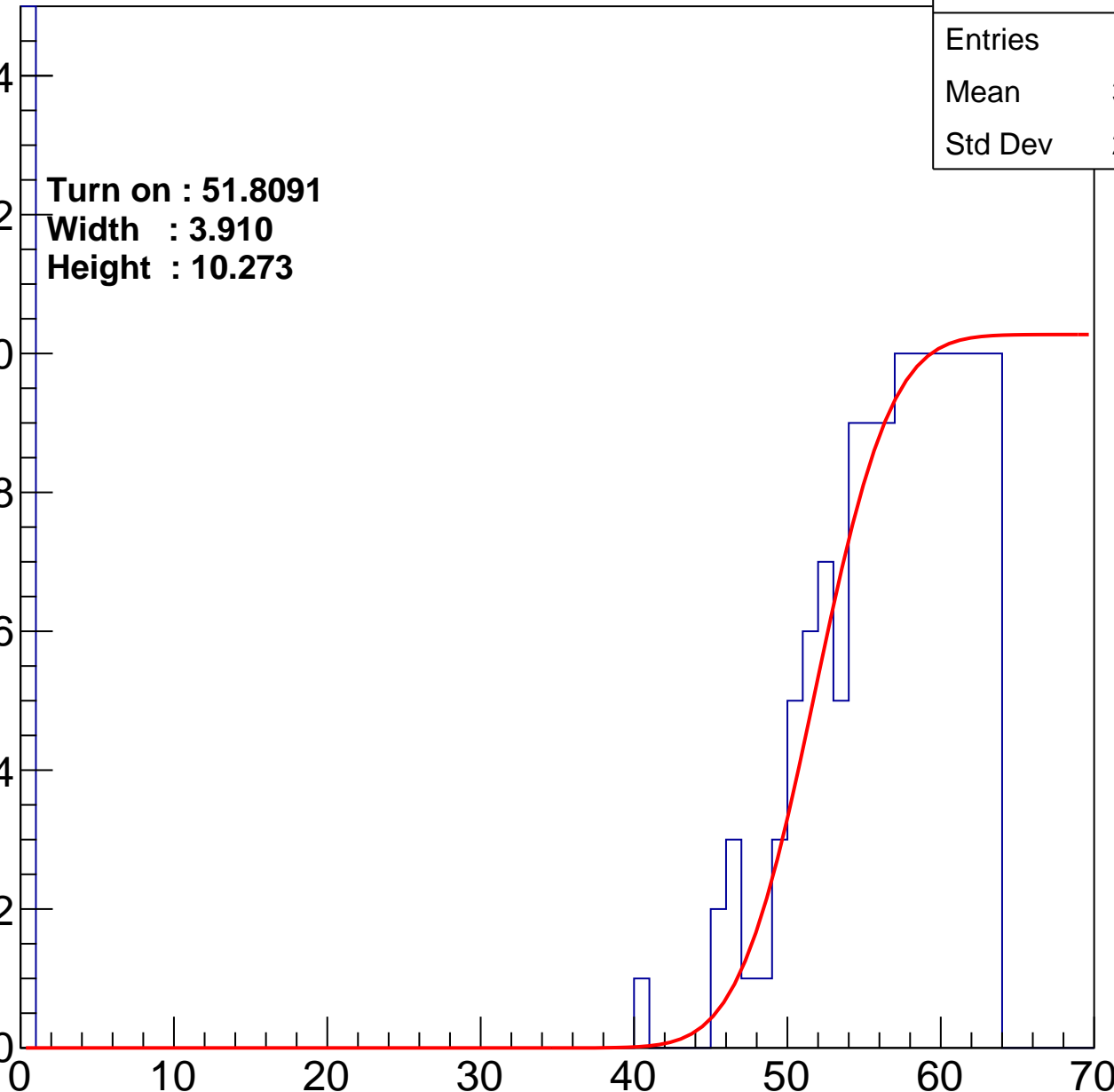
Width : 3.910

Height : 10.273

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	34.6
Std Dev	28.19

Turn on : 53.8676

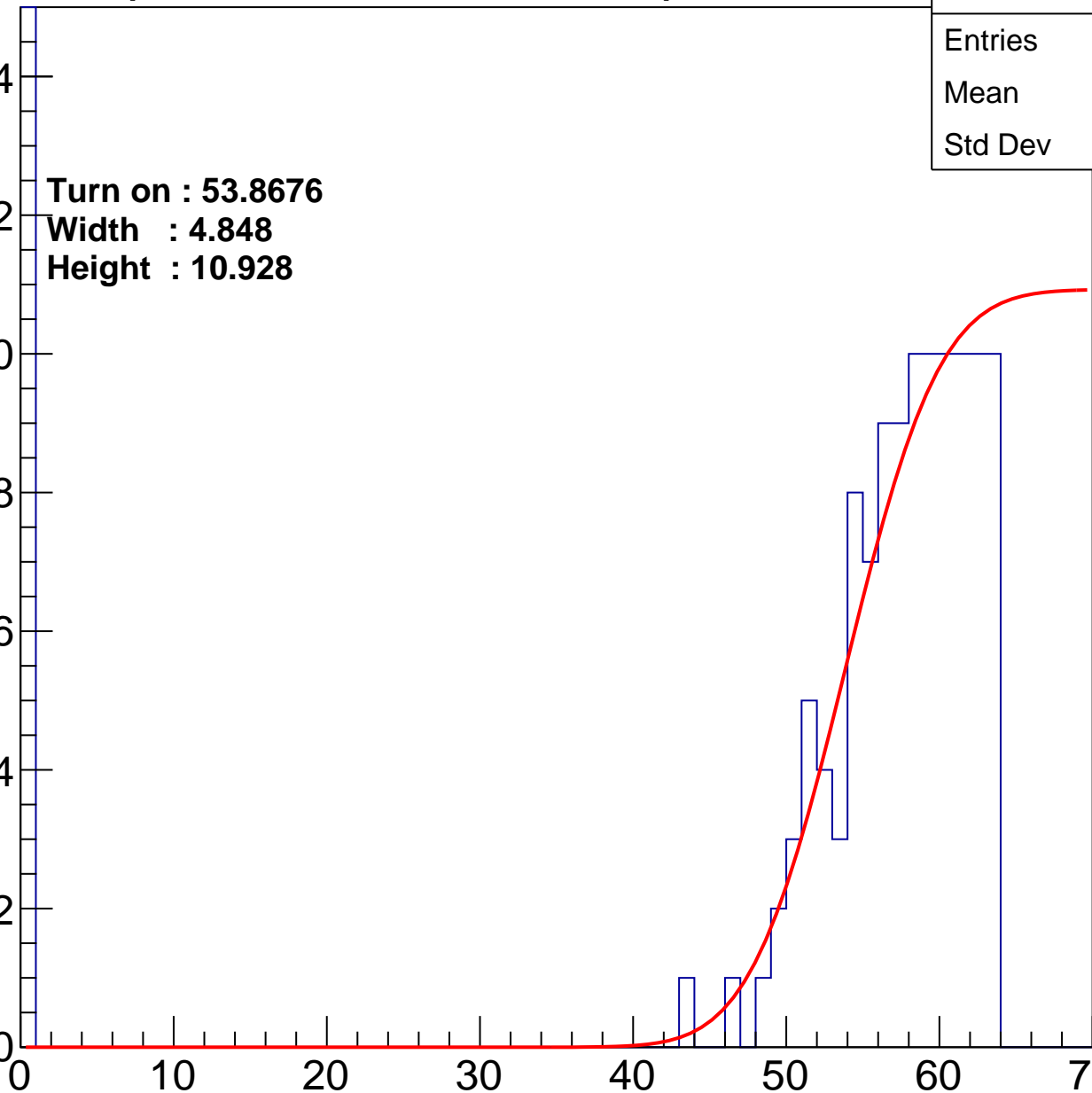
Width : 4.848

Height : 10.928

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	35.1
Std Dev	27.65

Turn on : 51.4730

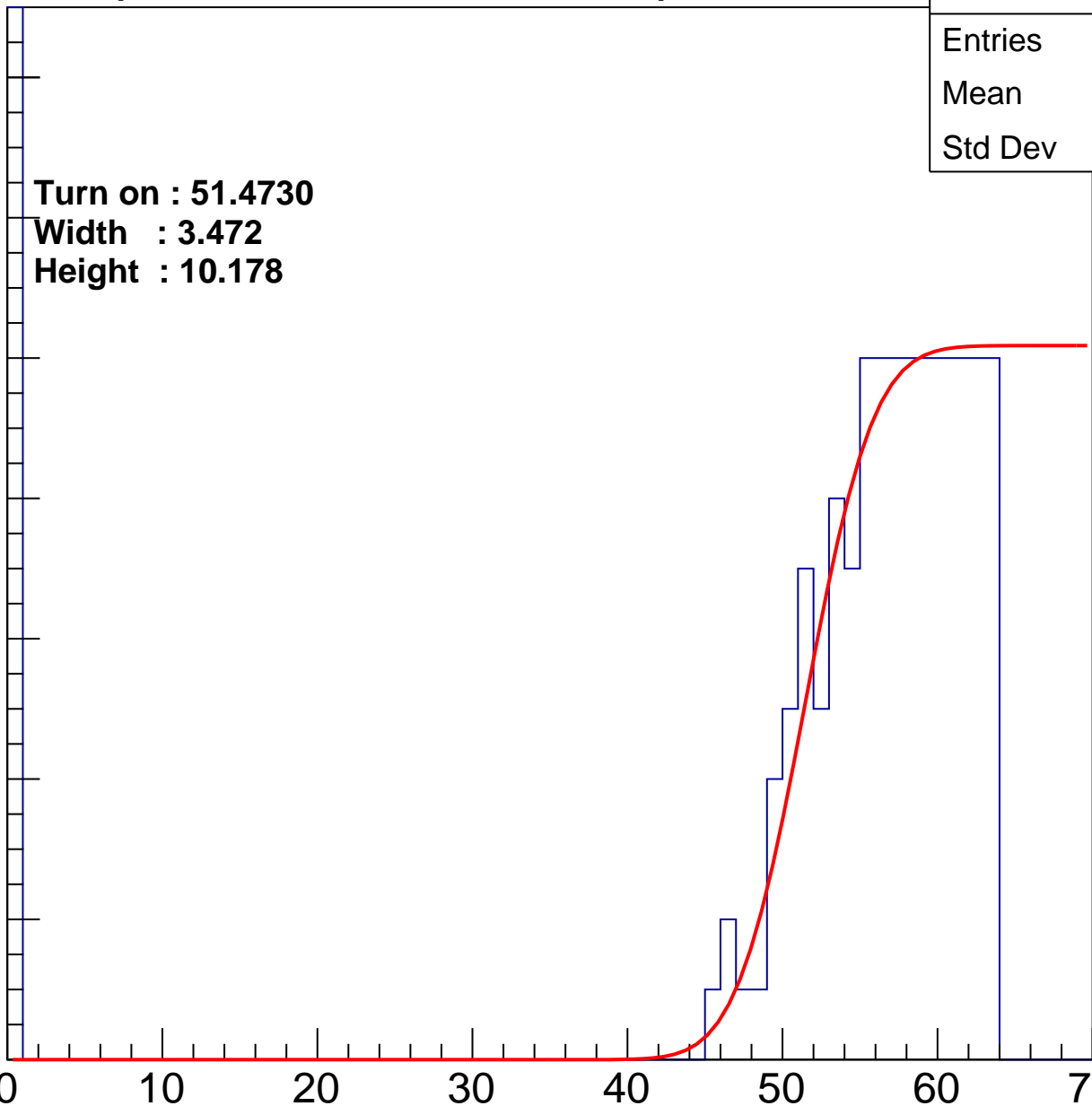
Width : 3.472

Height : 10.178

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	37.66
Std Dev	27.85

Turn on : 54.7020

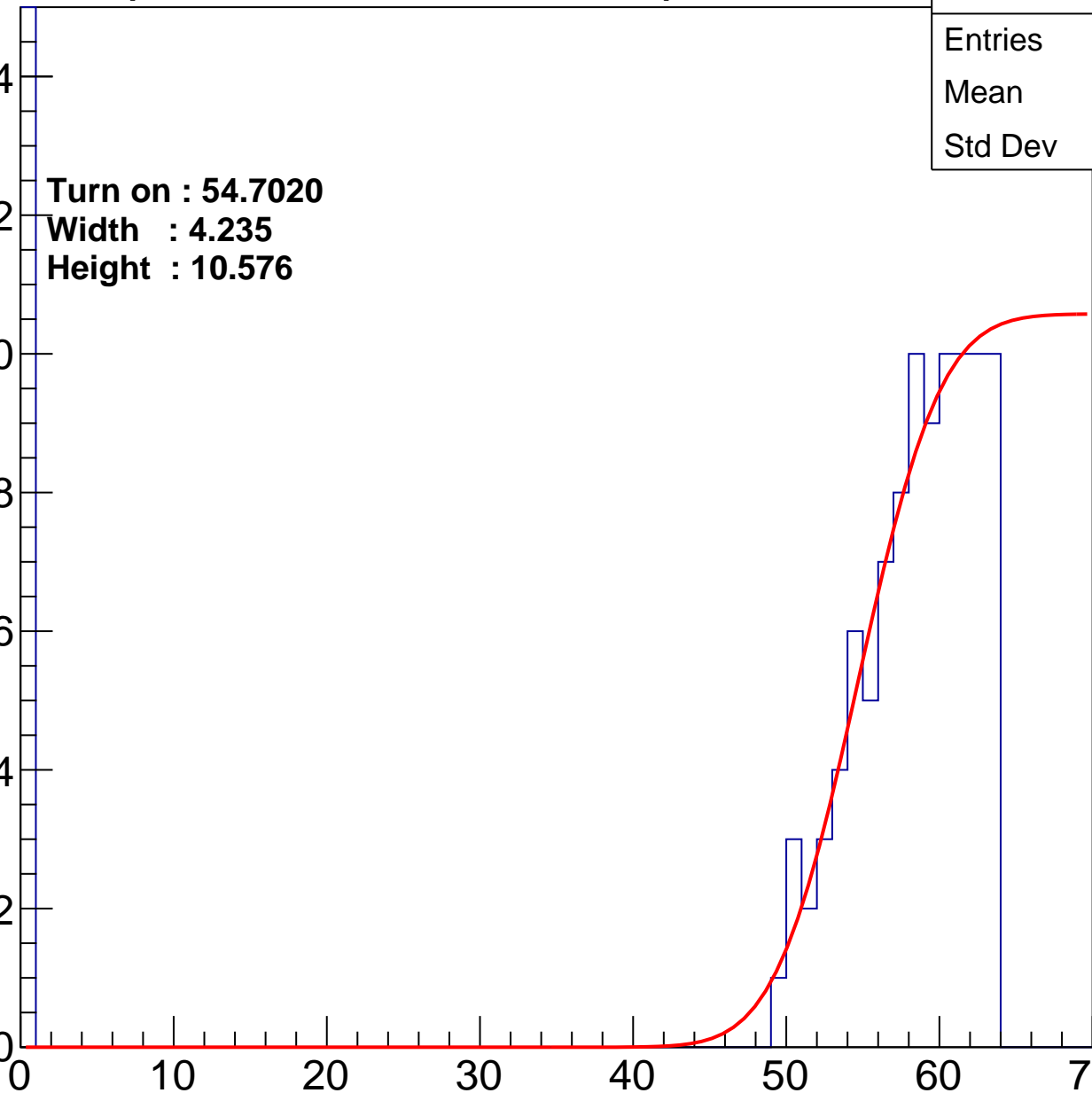
Width : 4.235

Height : 10.576

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	36.52
Std Dev	27.43

Turn on : 51.5508

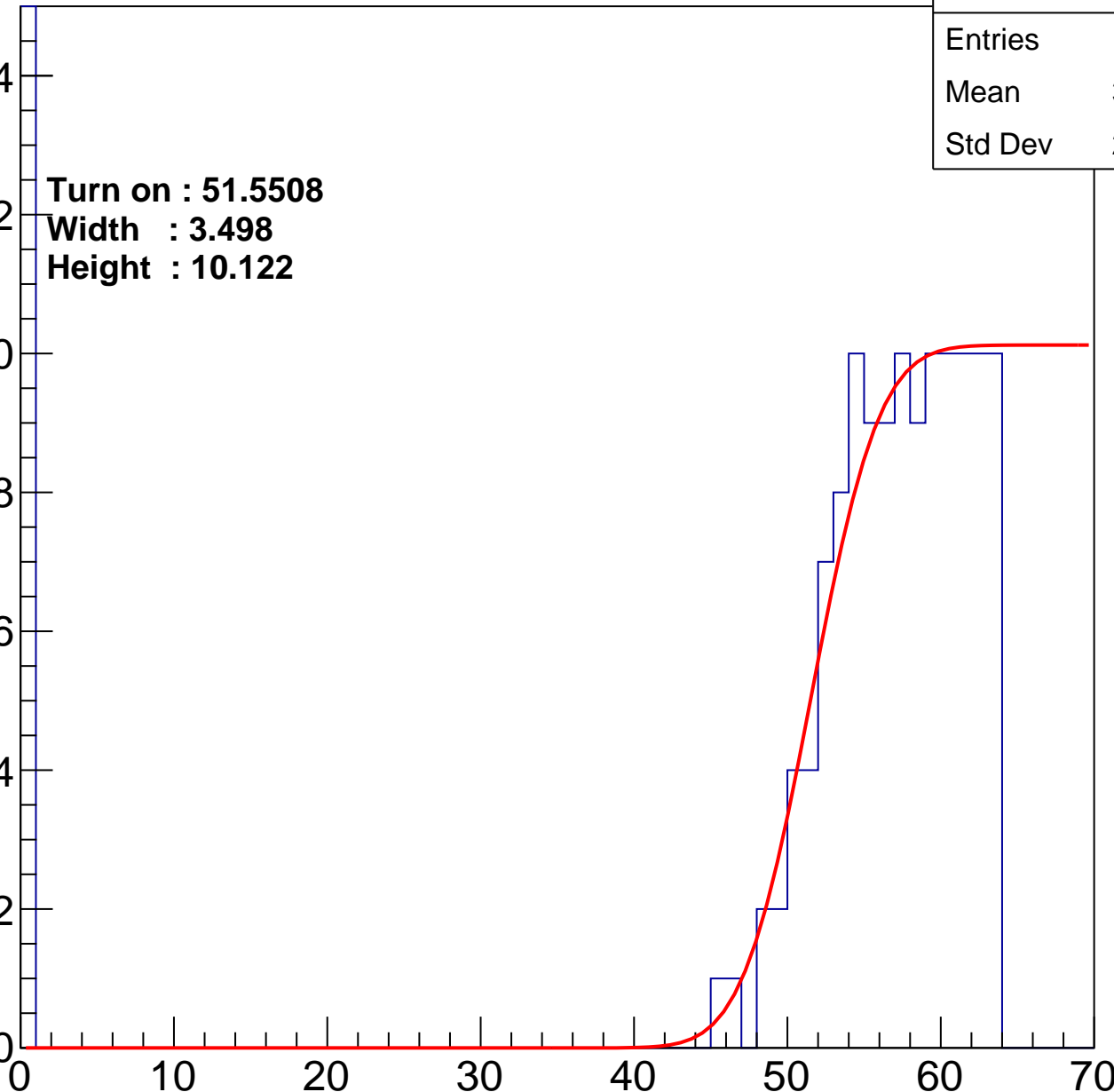
Width : 3.498

Height : 10.122

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	35.04
Std Dev	28.59

Turn on : 55.0433

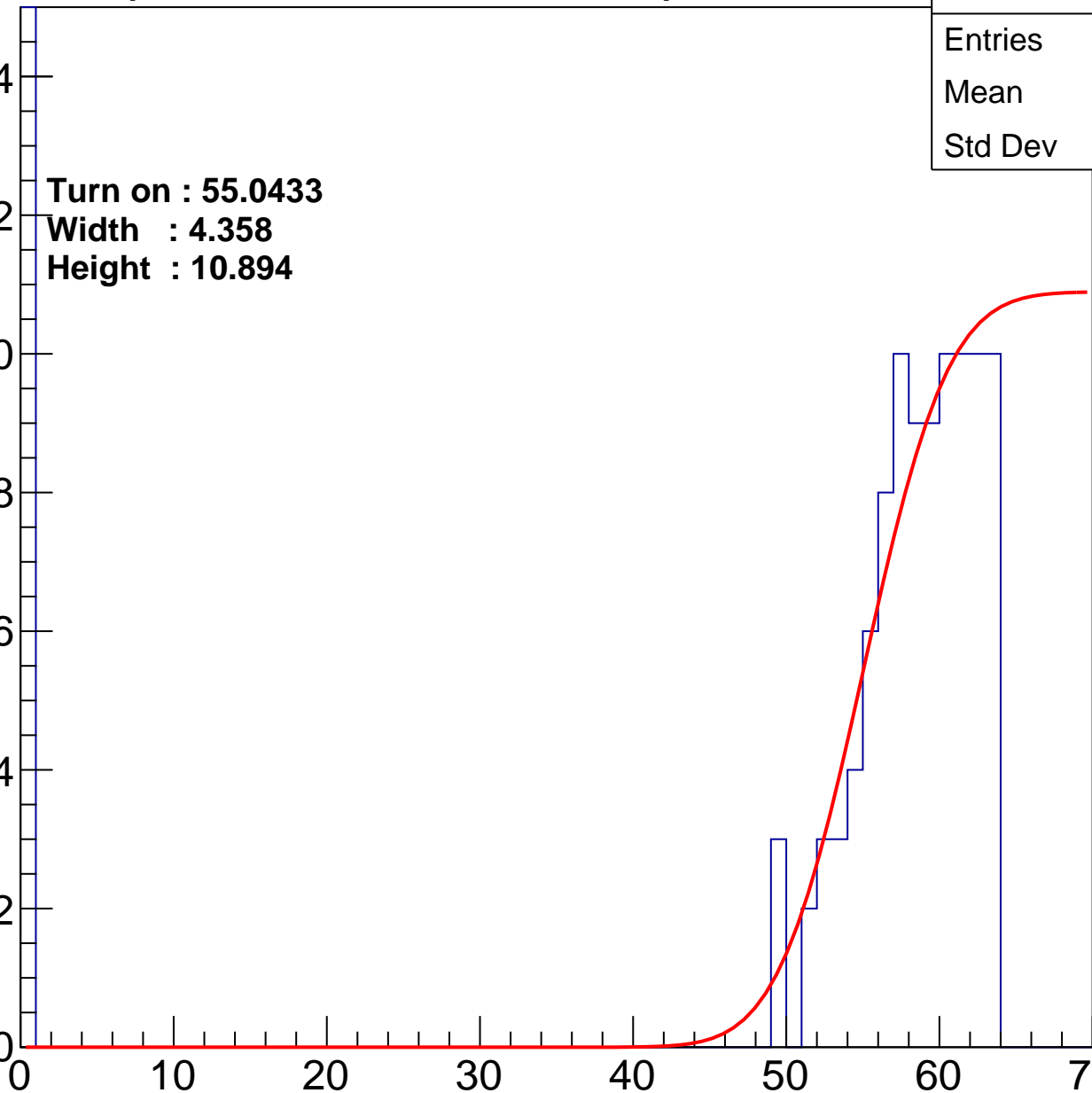
Width : 4.358

Height : 10.894

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch8

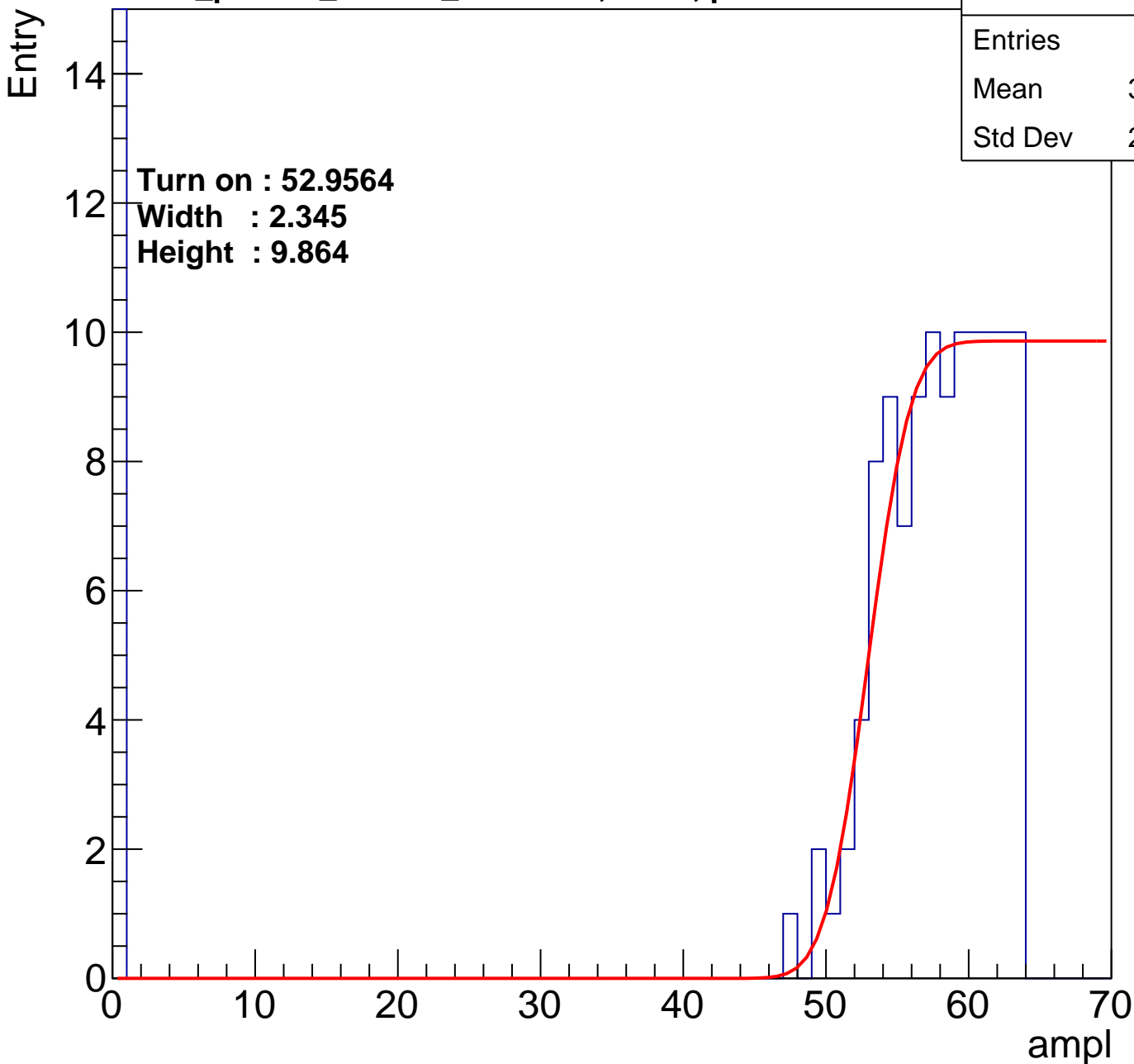
calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	33.57
Std Dev	28.52

Turn on : 52.9564

Width : 2.345

Height : 9.864



B1L104S, U15-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	34.14
Std Dev	28.34

Turn on : 52.8539

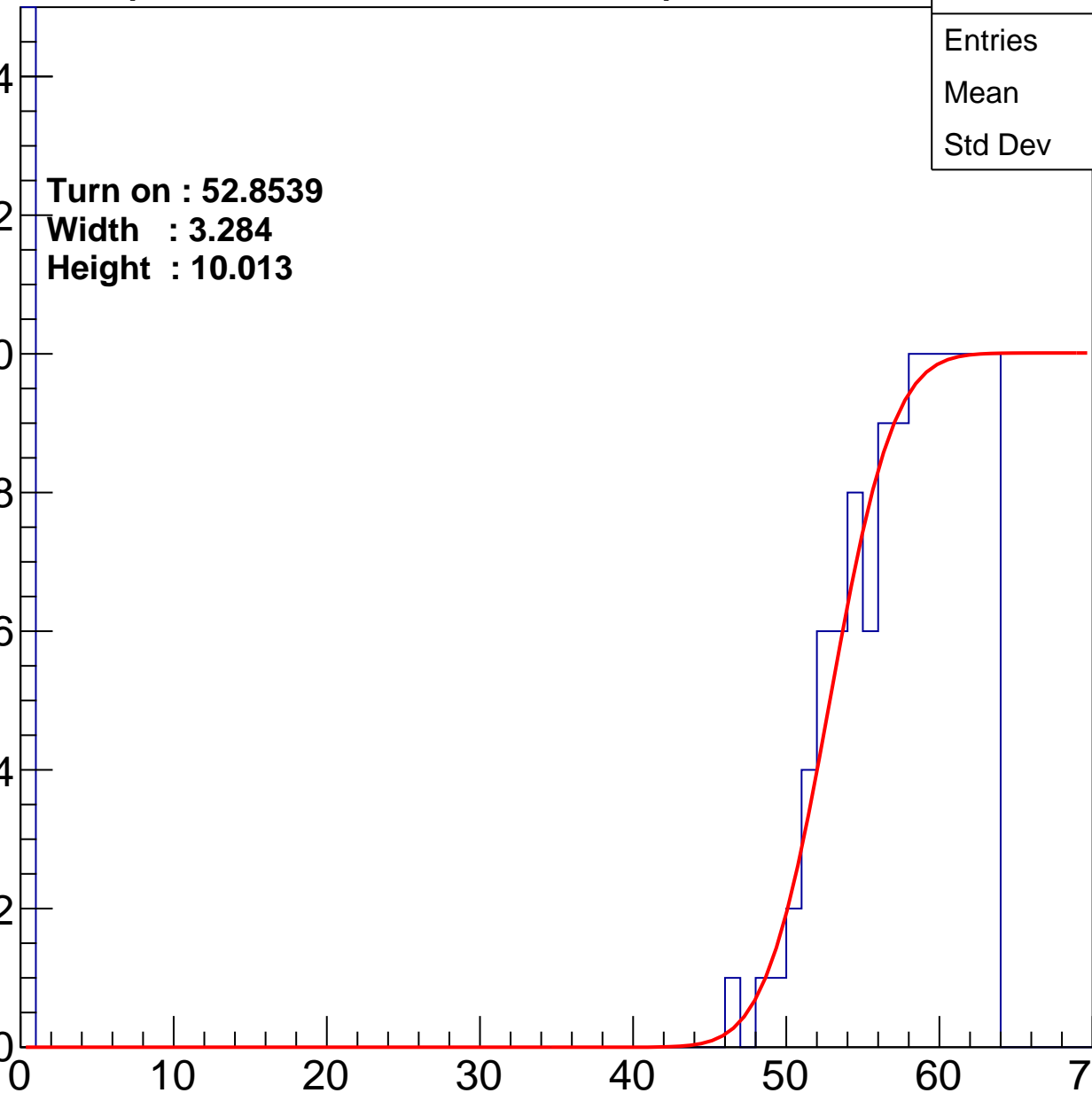
Width : 3.284

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	33.59
Std Dev	28.31

Turn on : 52.1066

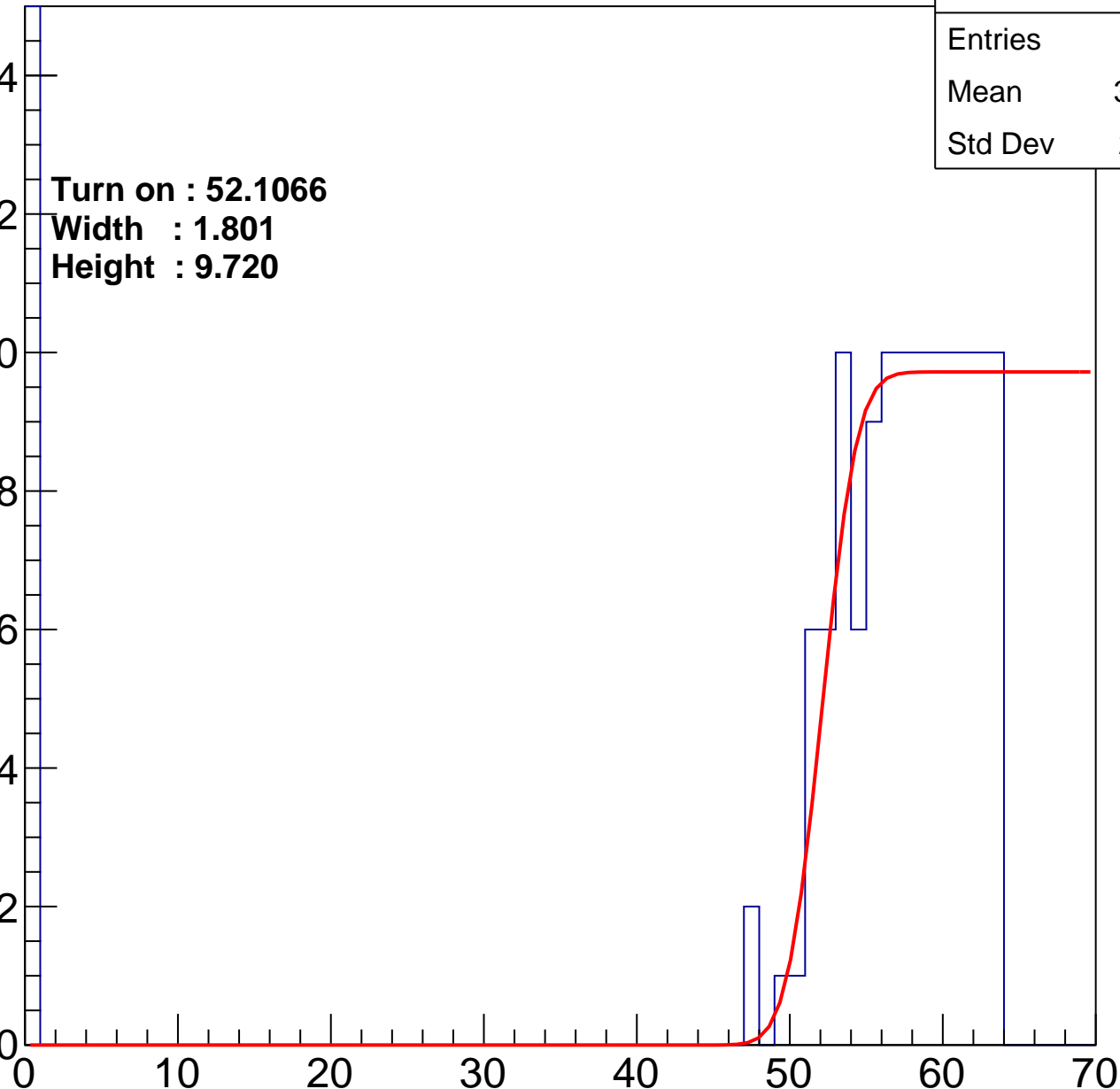
Width : 1.801

Height : 9.720

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	36.45
Std Dev	27.45

Turn on : 51.3676

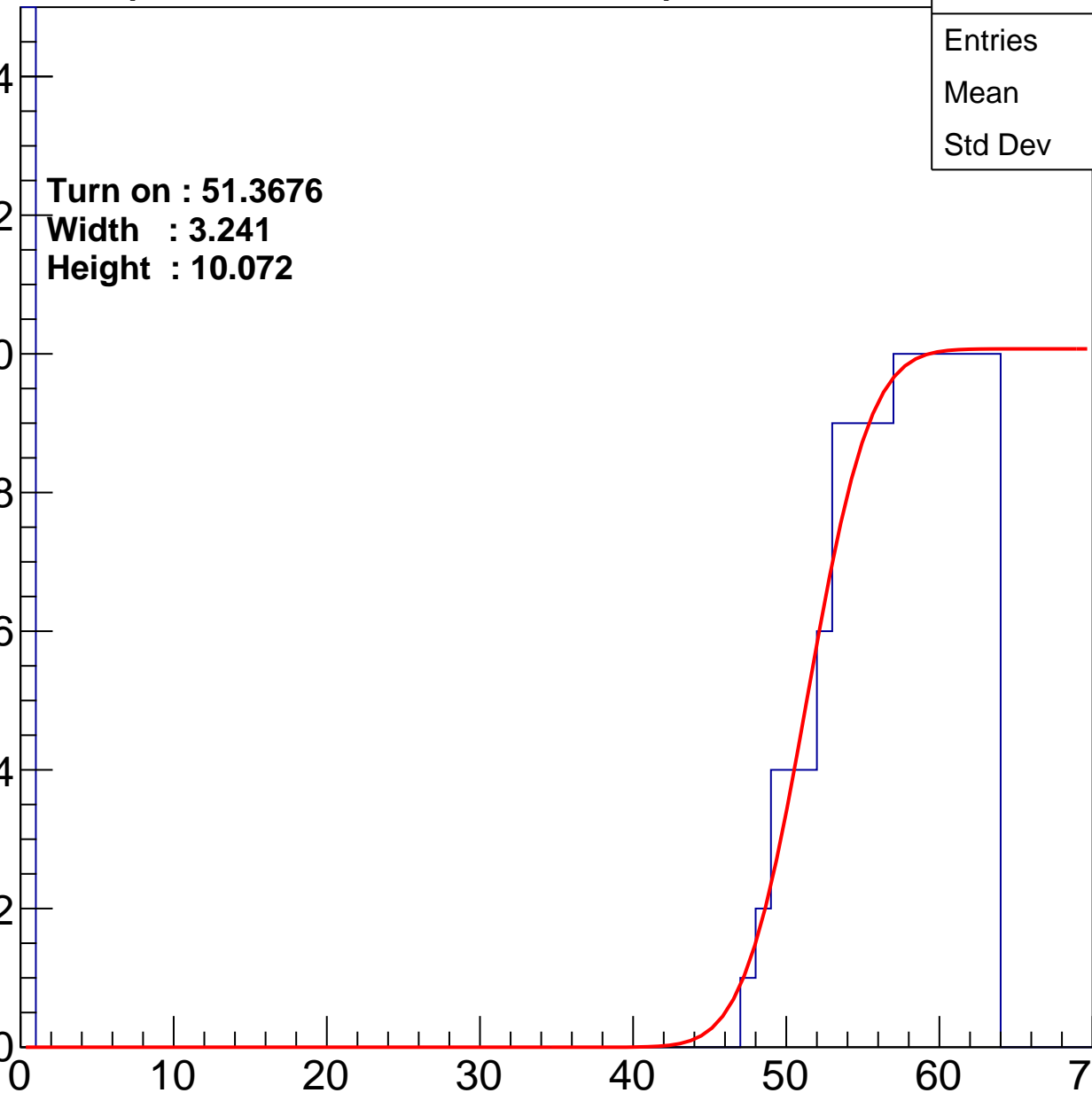
Width : 3.241

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	31.37
Std Dev	28.81

Turn on : 53.5376

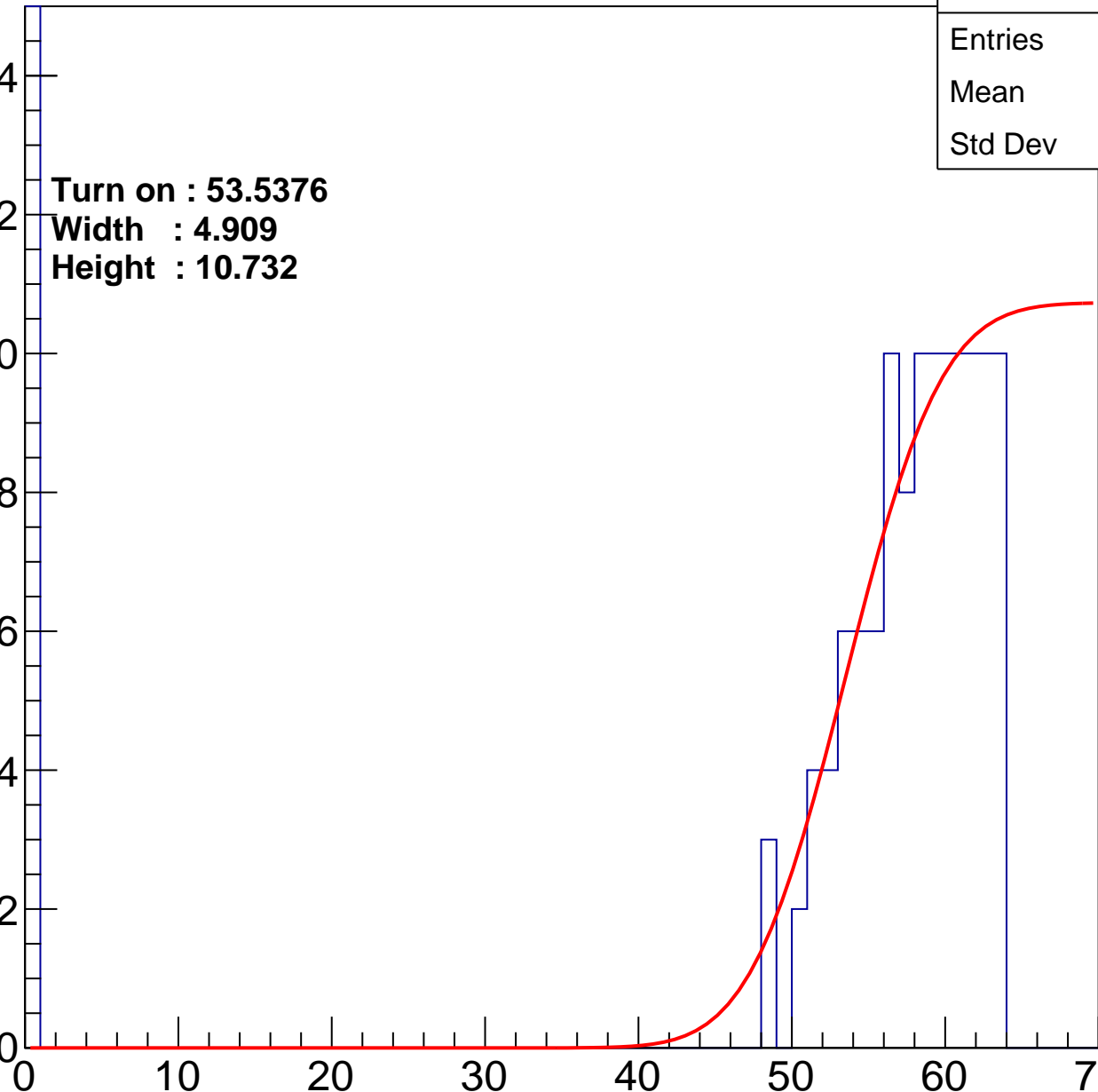
Width : 4.909

Height : 10.732

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	29.55
Std Dev	28.87

Turn on : 52.9093

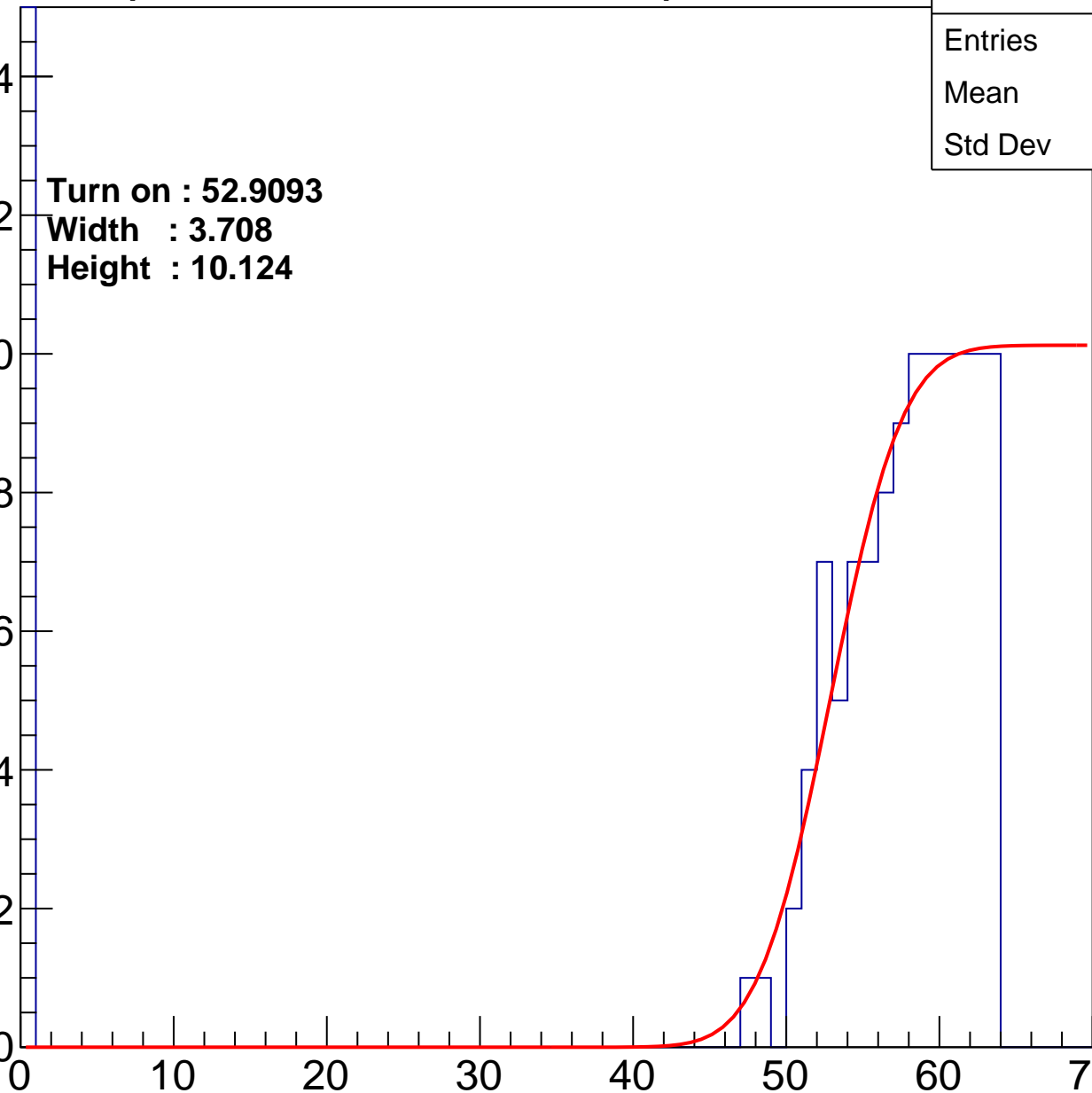
Width : 3.708

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	36.57
Std Dev	27.33

Turn on : 51.8524

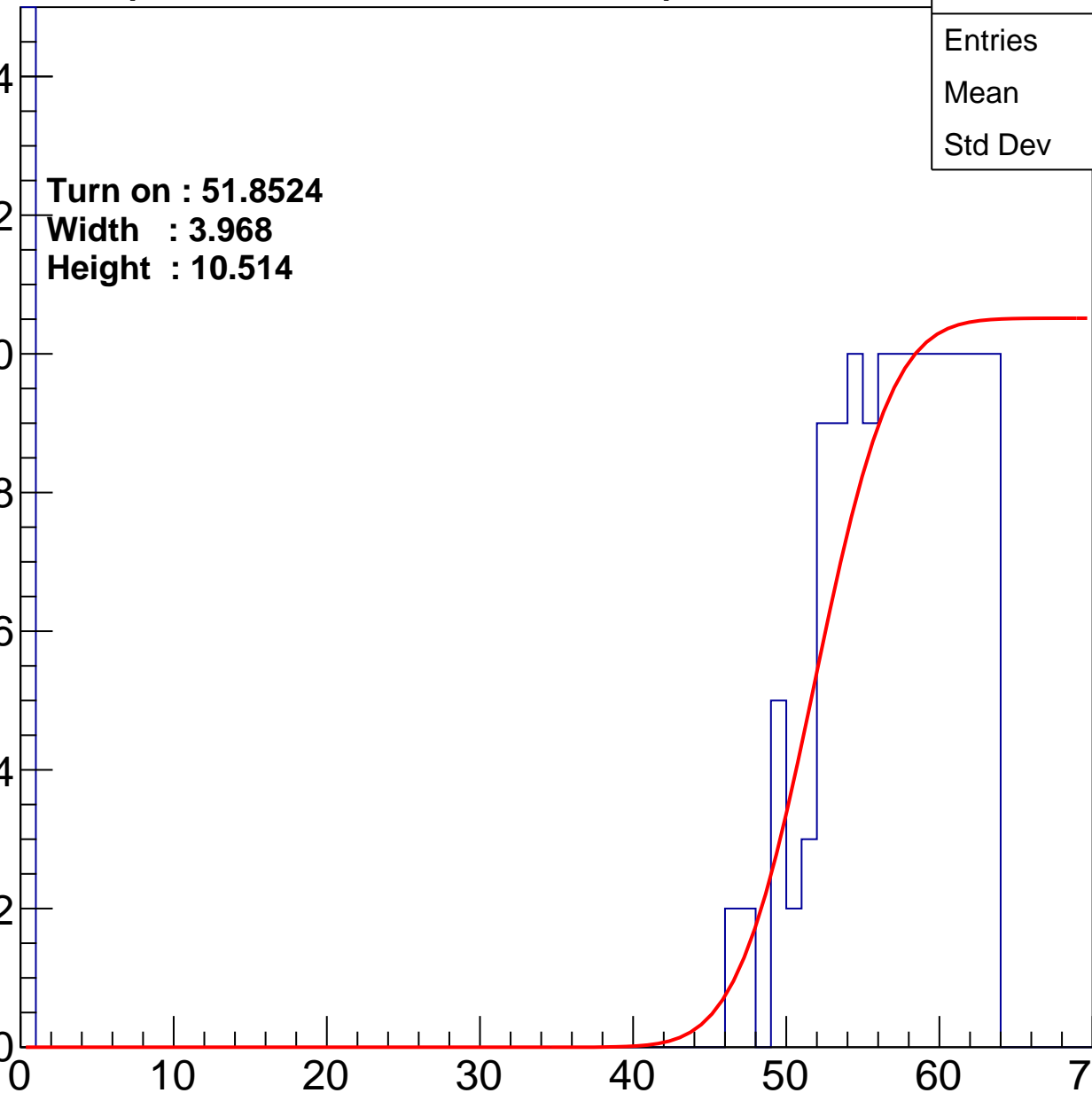
Width : 3.968

Height : 10.514

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	230
Mean	31.6
Std Dev	28.38

Turn on : 51.3840

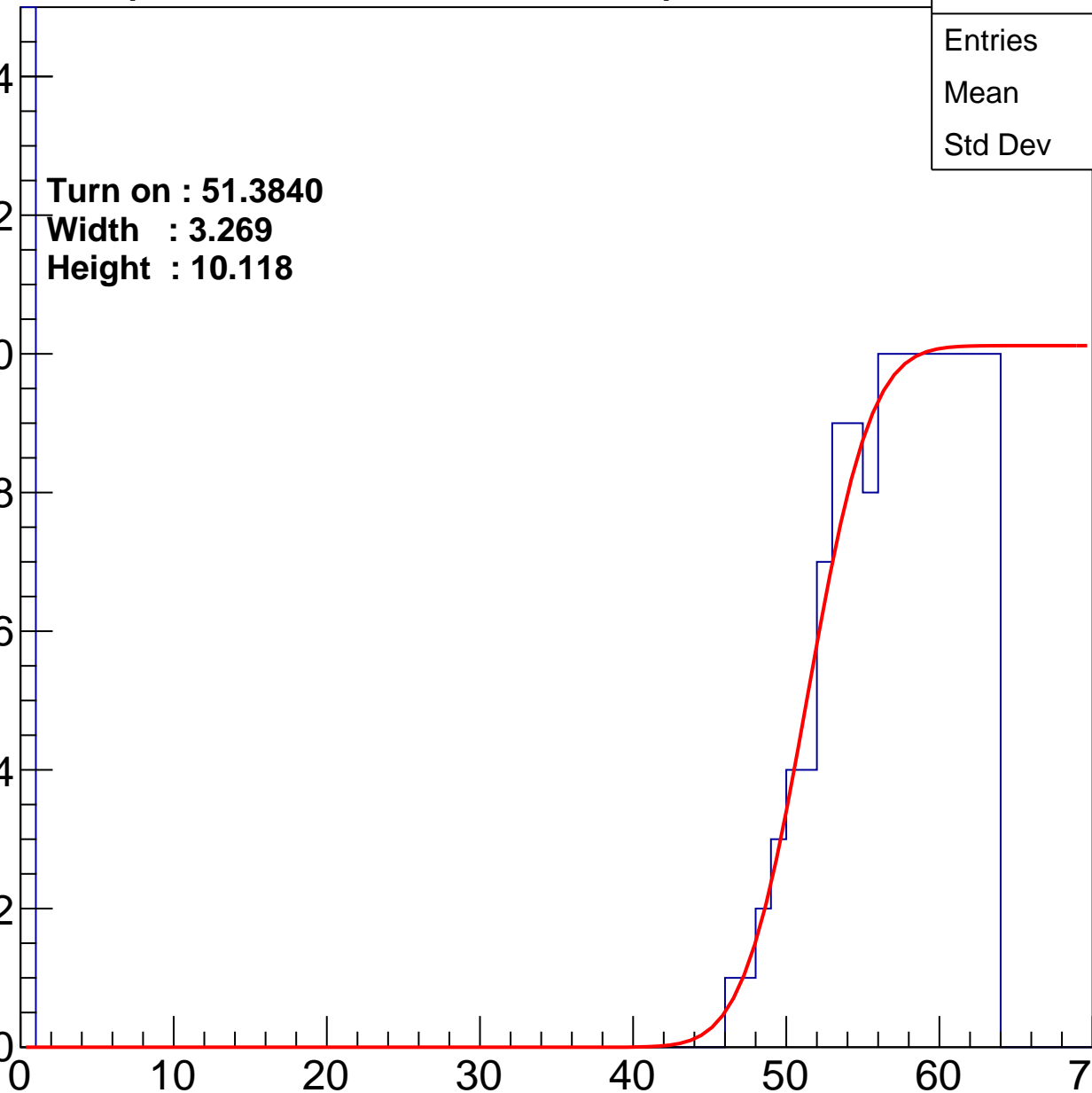
Width : 3.269

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	36.95
Std Dev	26.7

Turn on : 50.6215

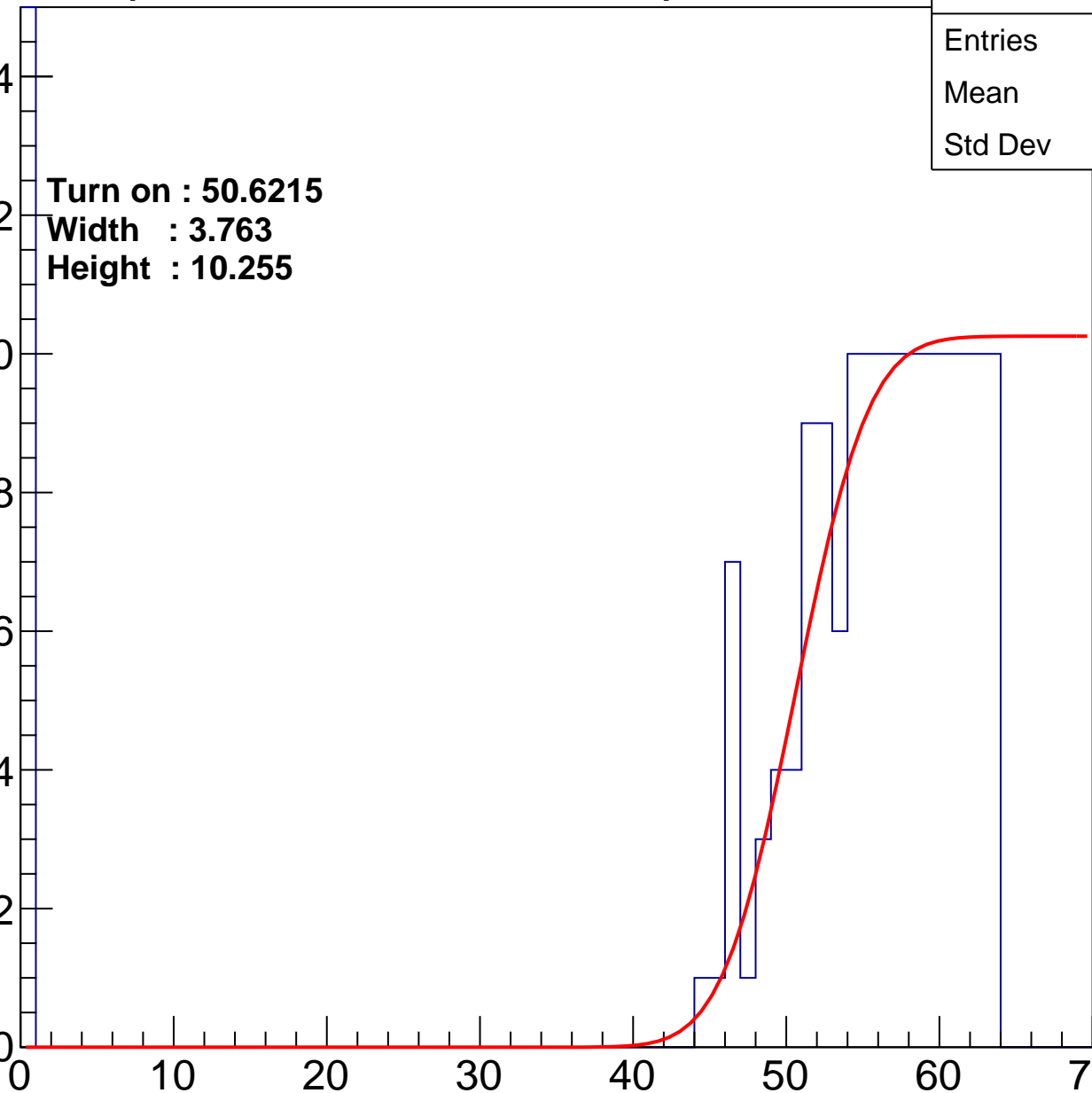
Width : 3.763

Height : 10.255

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	35.06
Std Dev	28.2

Turn on : 52.1720

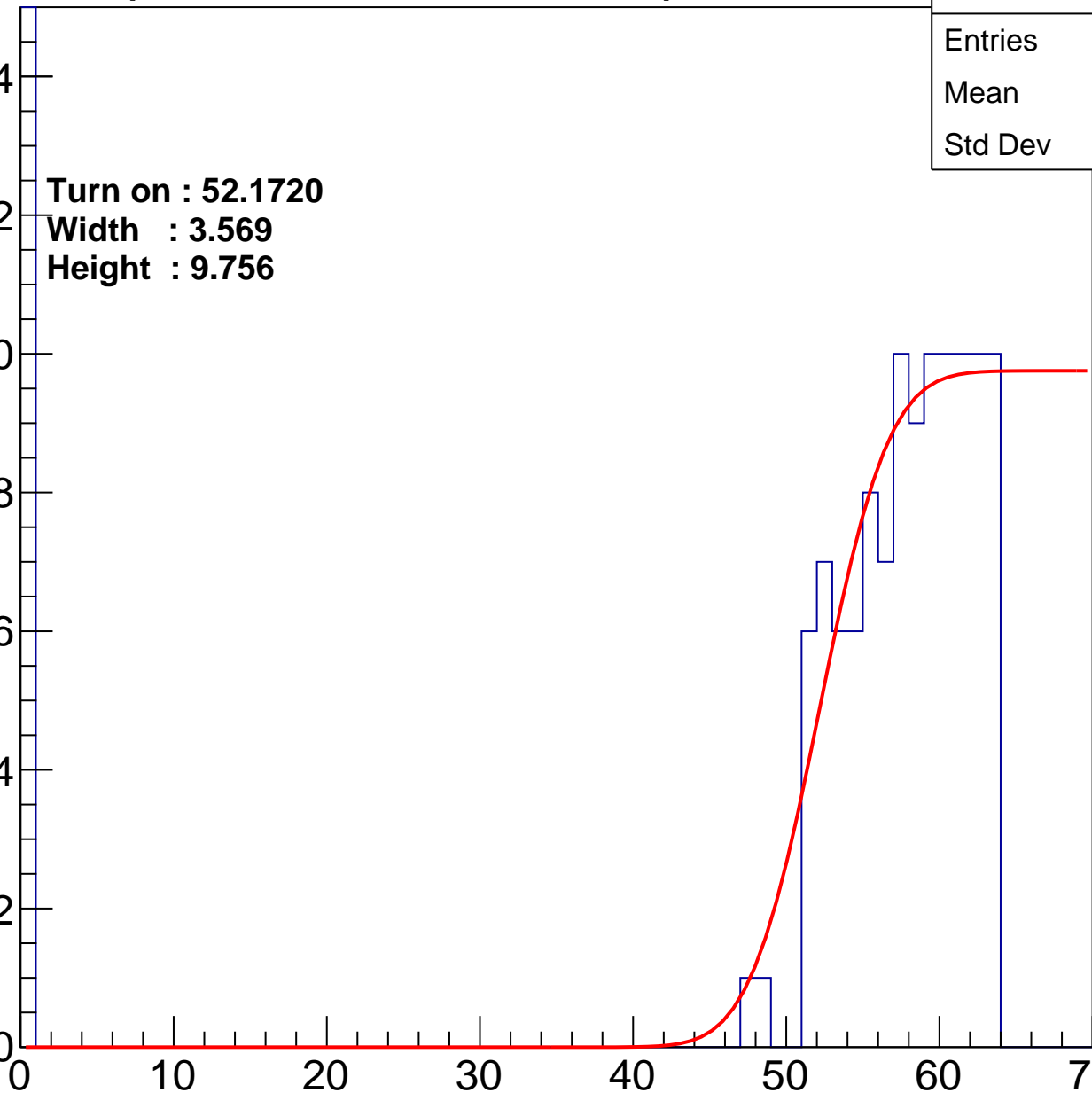
Width : 3.569

Height : 9.756

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	36.35
Std Dev	28.12

Turn on : 53.4724

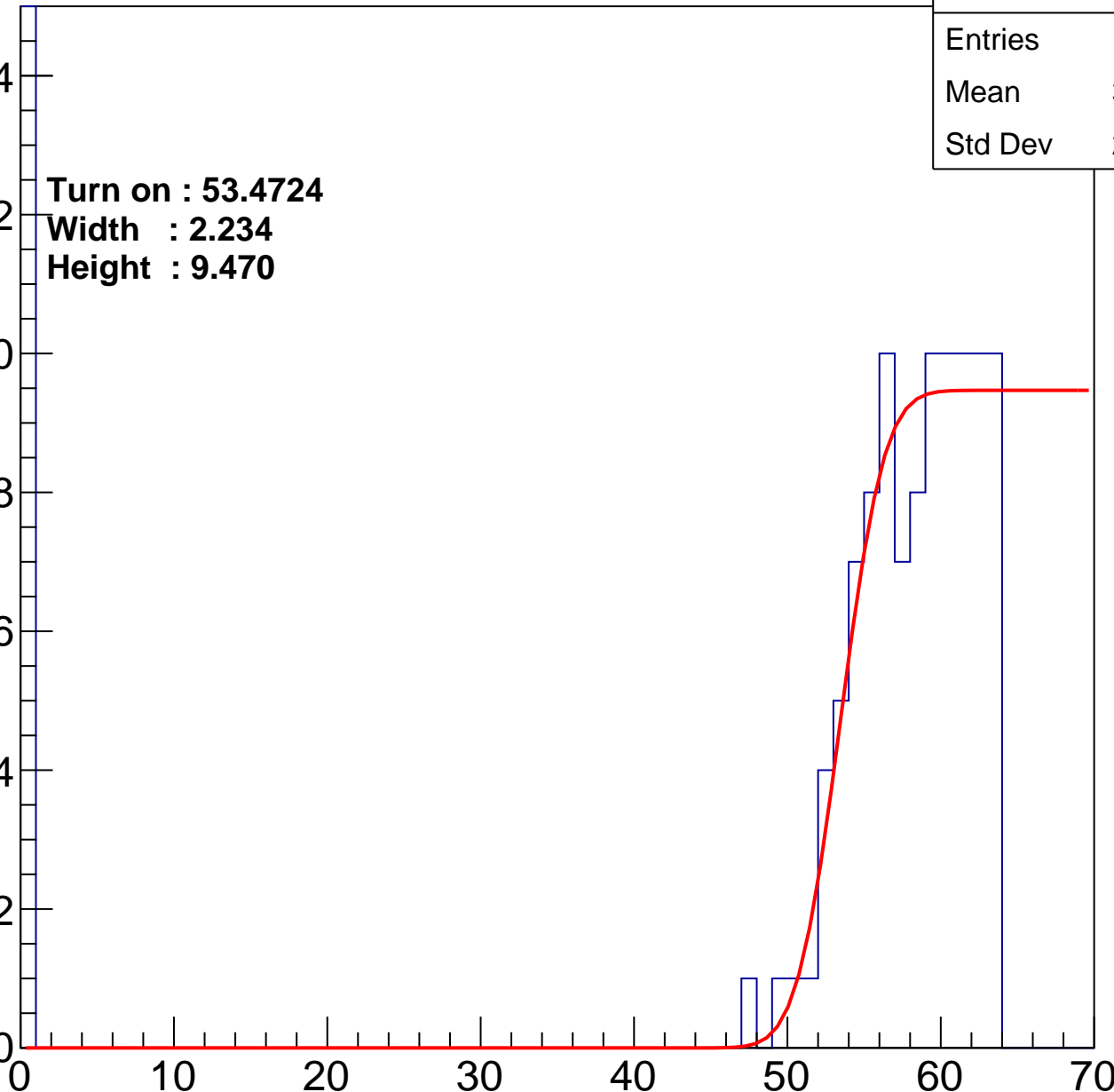
Width : 2.234

Height : 9.470

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	31.02
Std Dev	29.26

Turn on : 54.9362

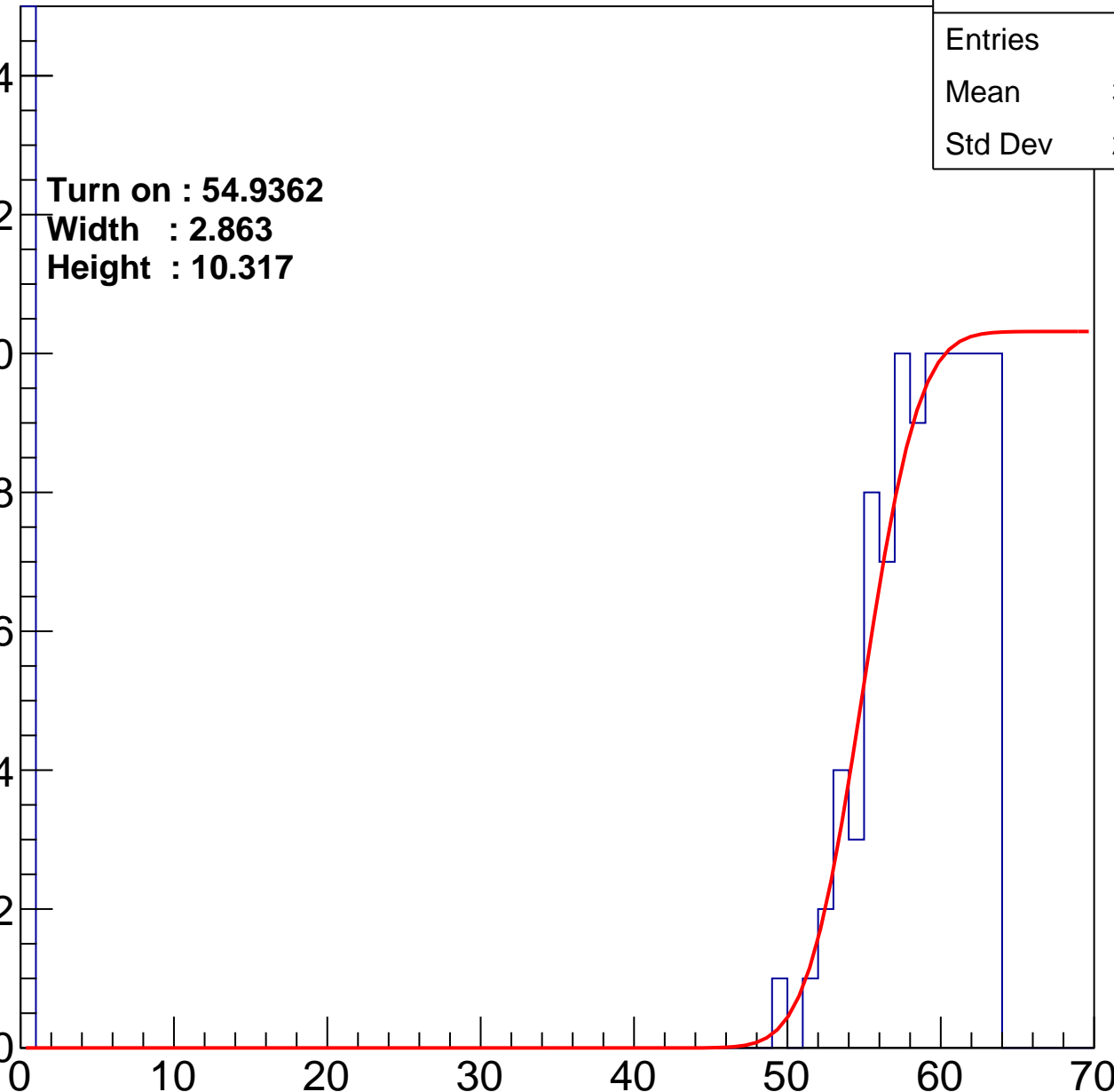
Width : 2.863

Height : 10.317

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	29.22
Std Dev	28.94

Turn on : 53.8348

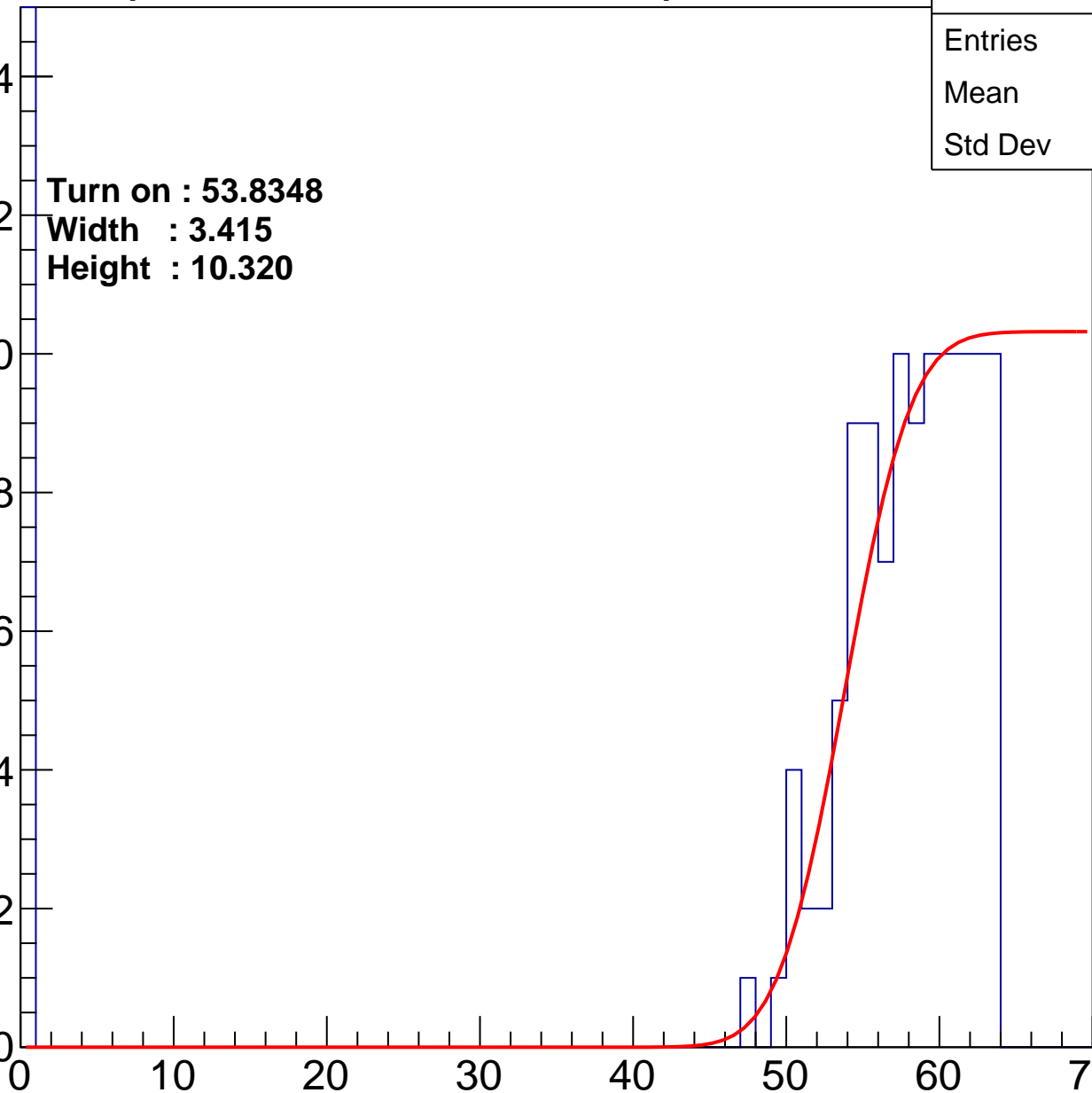
Width : 3.415

Height : 10.320

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	29.33
Std Dev	29.13

Turn on : 53.6664

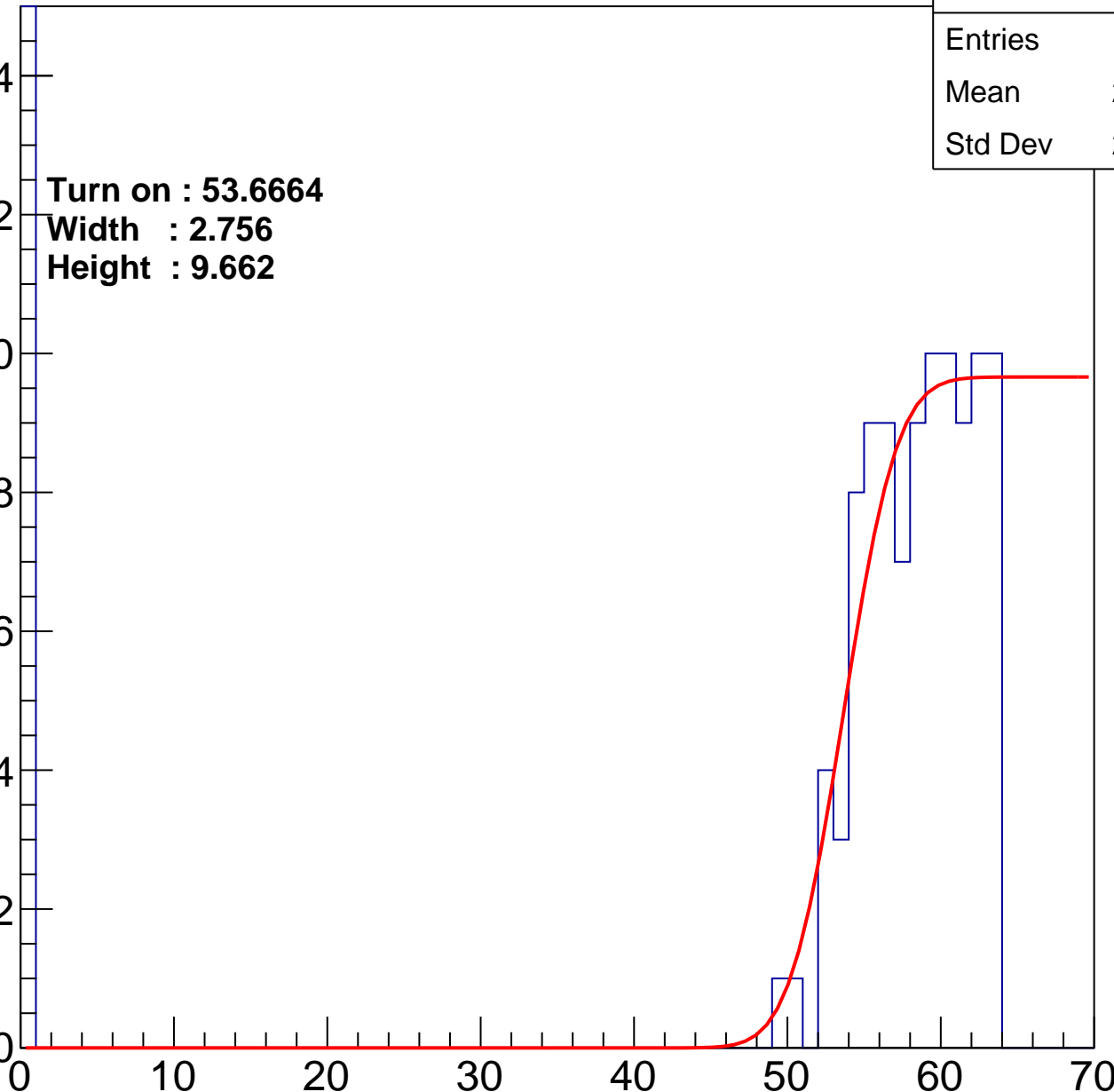
Width : 2.756

Height : 9.662

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	37.4
Std Dev	27.42

Turn on : 52.3562

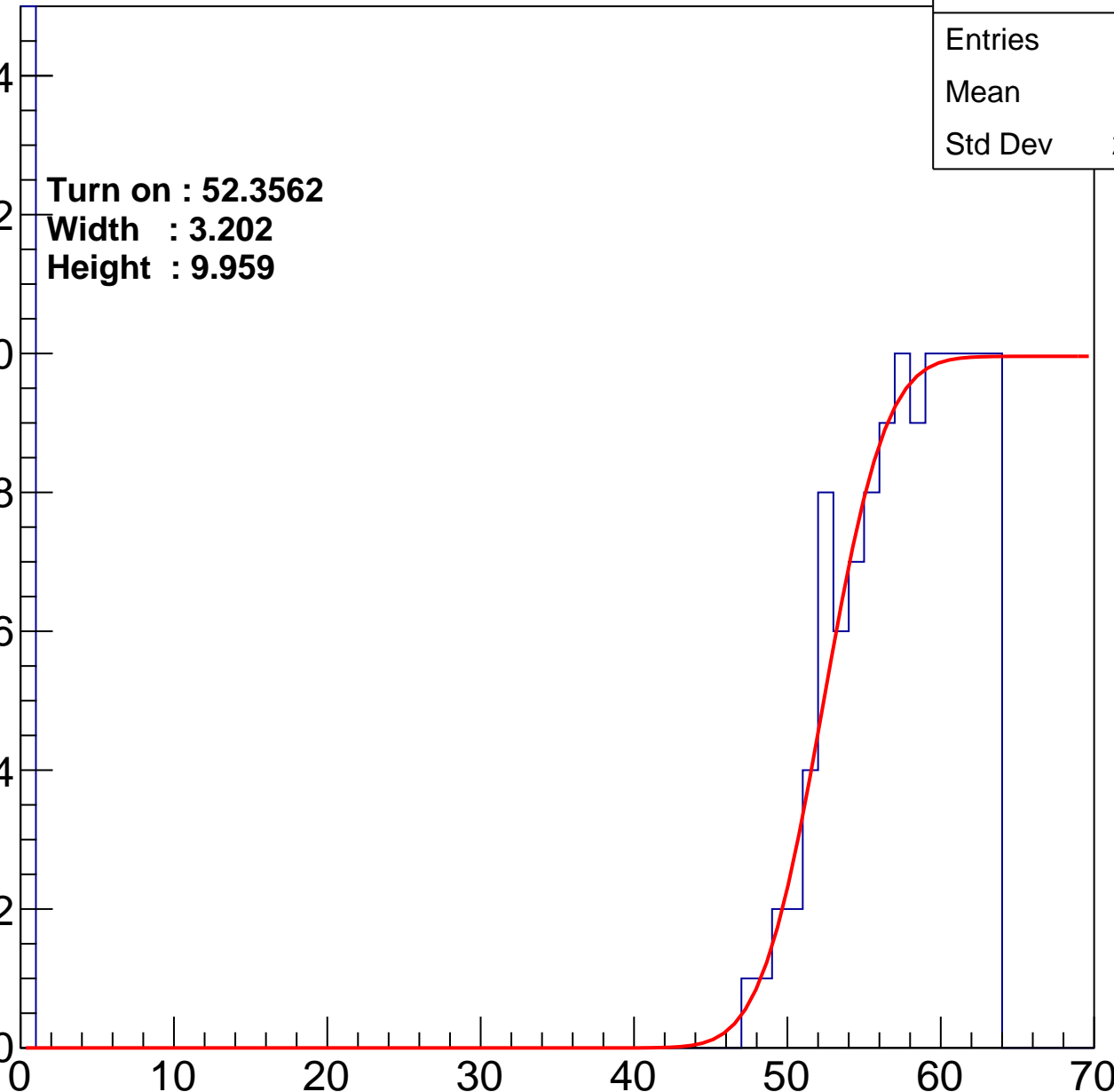
Width : 3.202

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	39.31
Std Dev	26.87

Turn on : 53.0118

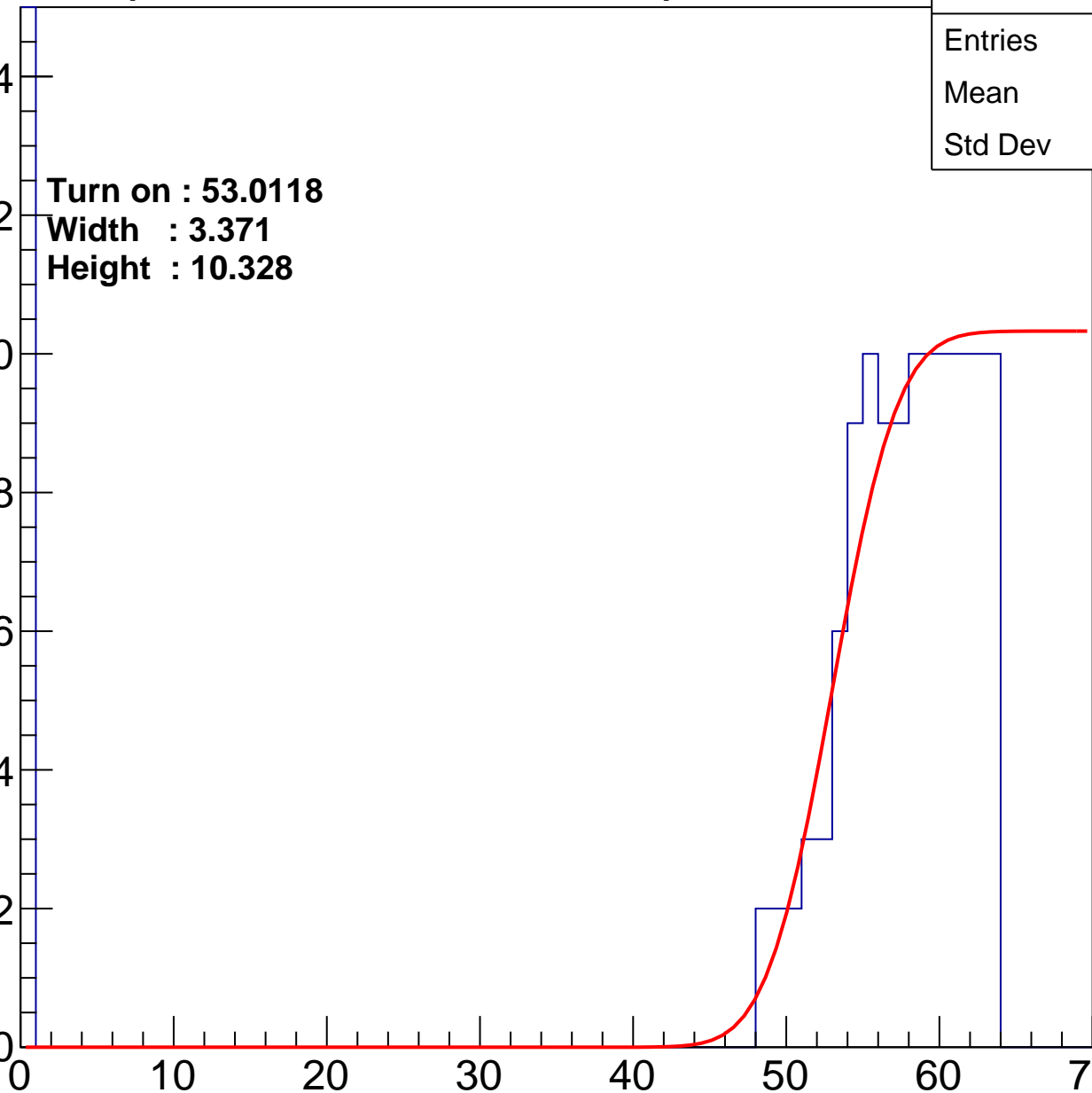
Width : 3.371

Height : 10.328

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch24

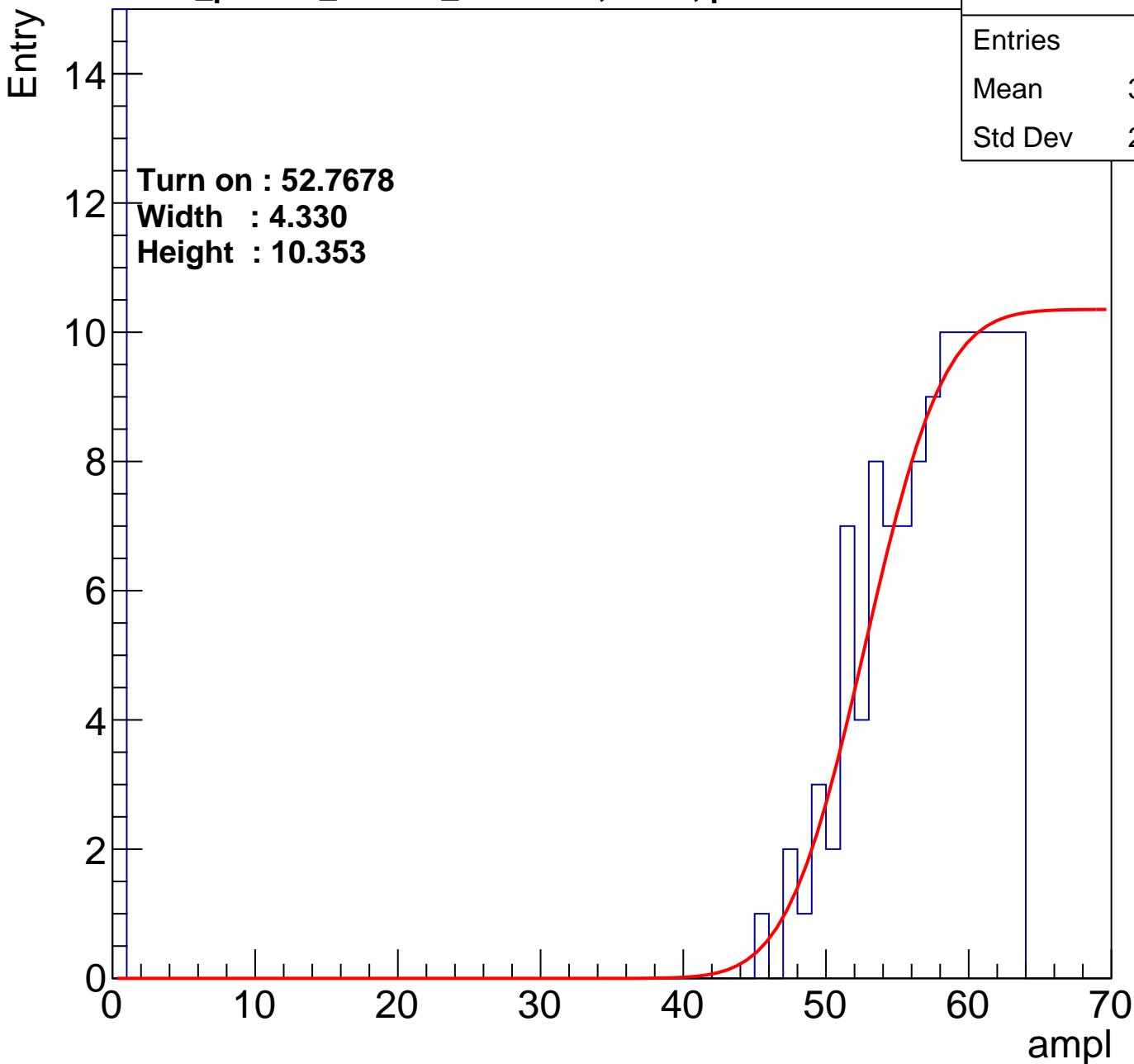
calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	31.97
Std Dev	28.44

Turn on : 52.7678

Width : 4.330

Height : 10.353



B1L104S, U15-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	37.18
Std Dev	27.26

Turn on : 52.3819

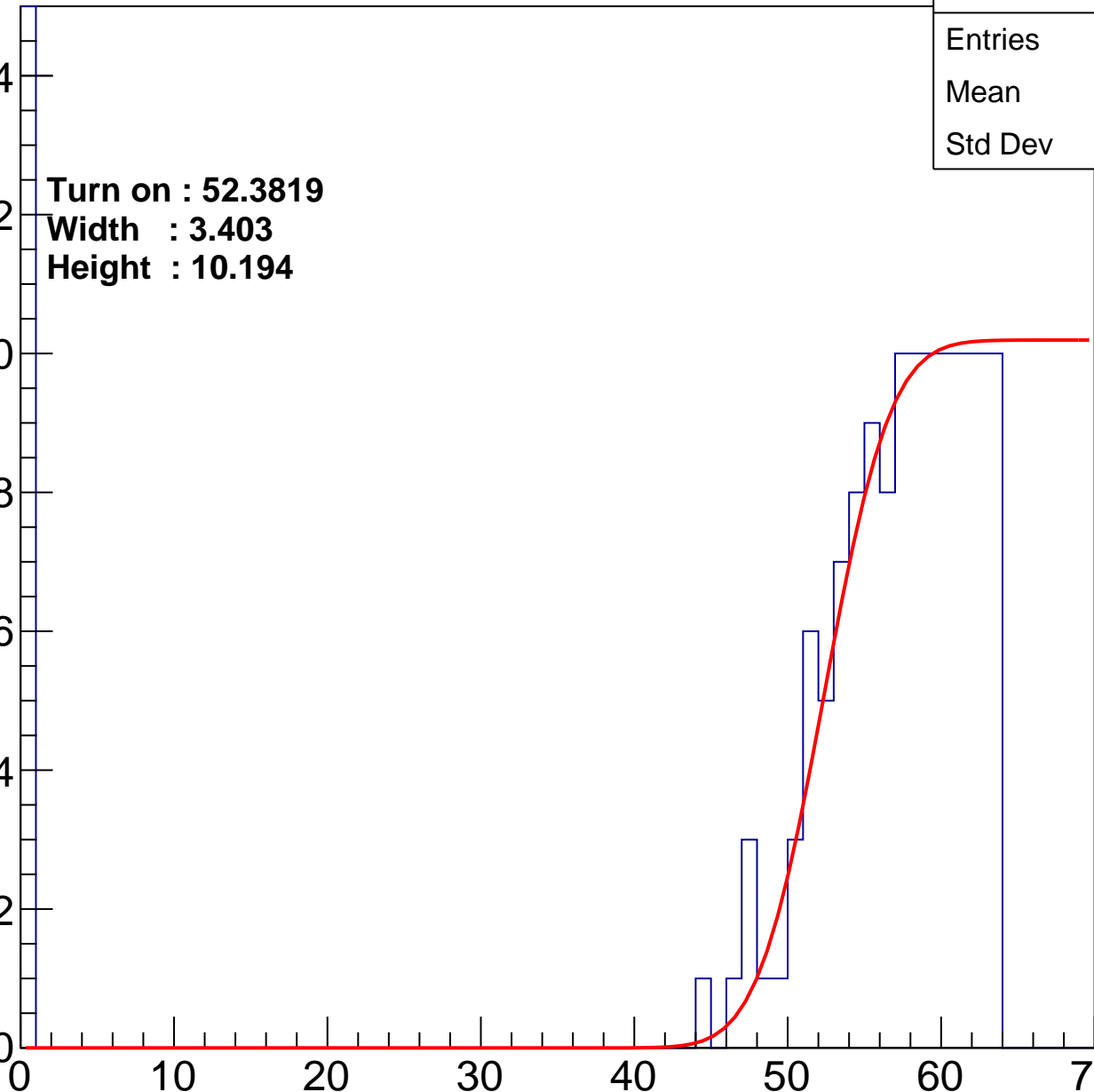
Width : 3.403

Height : 10.194

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch26

calib_packv5_033123_0516.root, FC#4, port A1

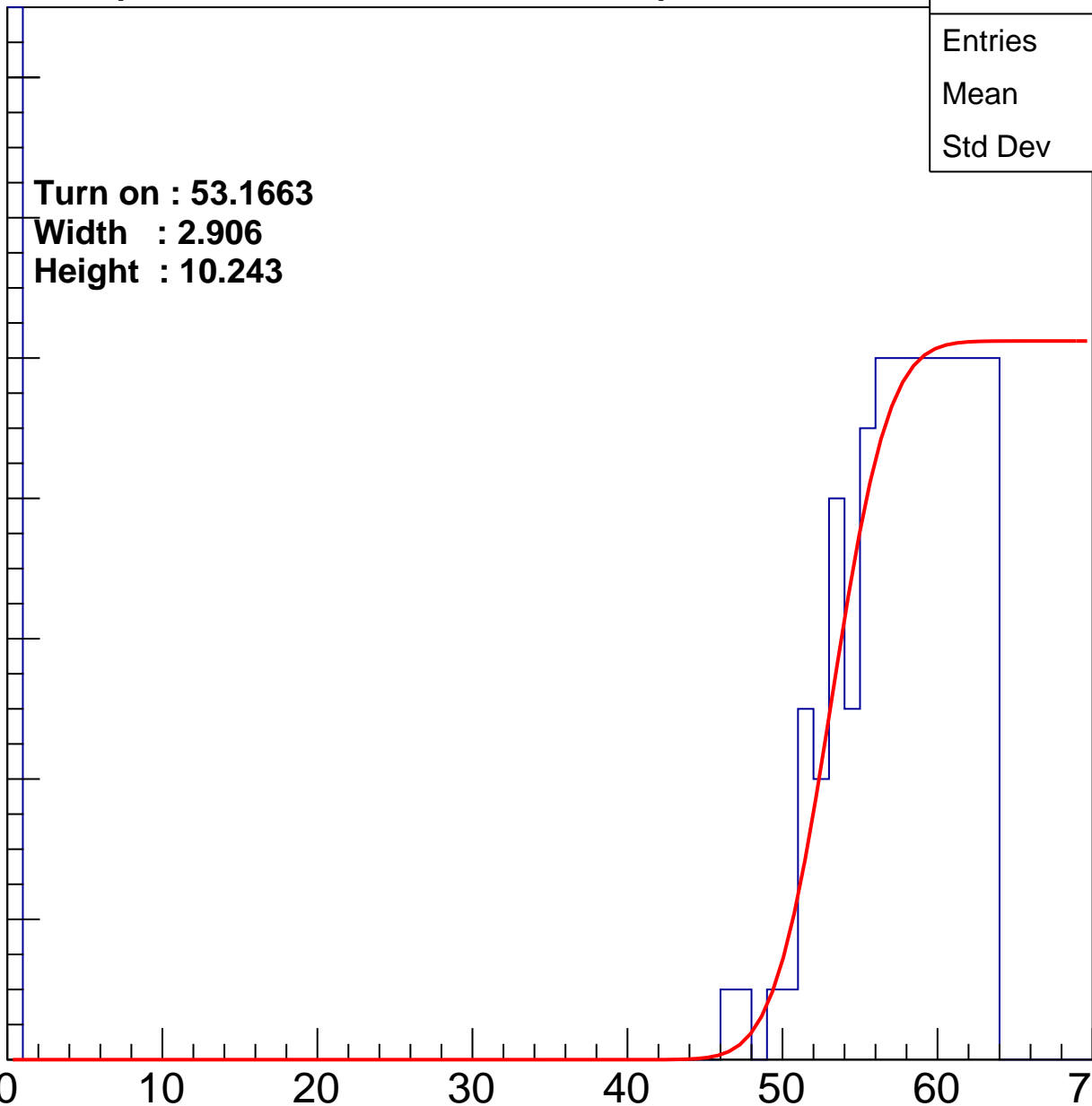
Entry

14
12
10
8
6
4
2
0

Turn on : 53.1663
Width : 2.906
Height : 10.243

Entries	192
Mean	34.4
Std Dev	28.3

ampl



B1L104S, U15-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	32.35
Std Dev	28.53

Turn on : 53.6812

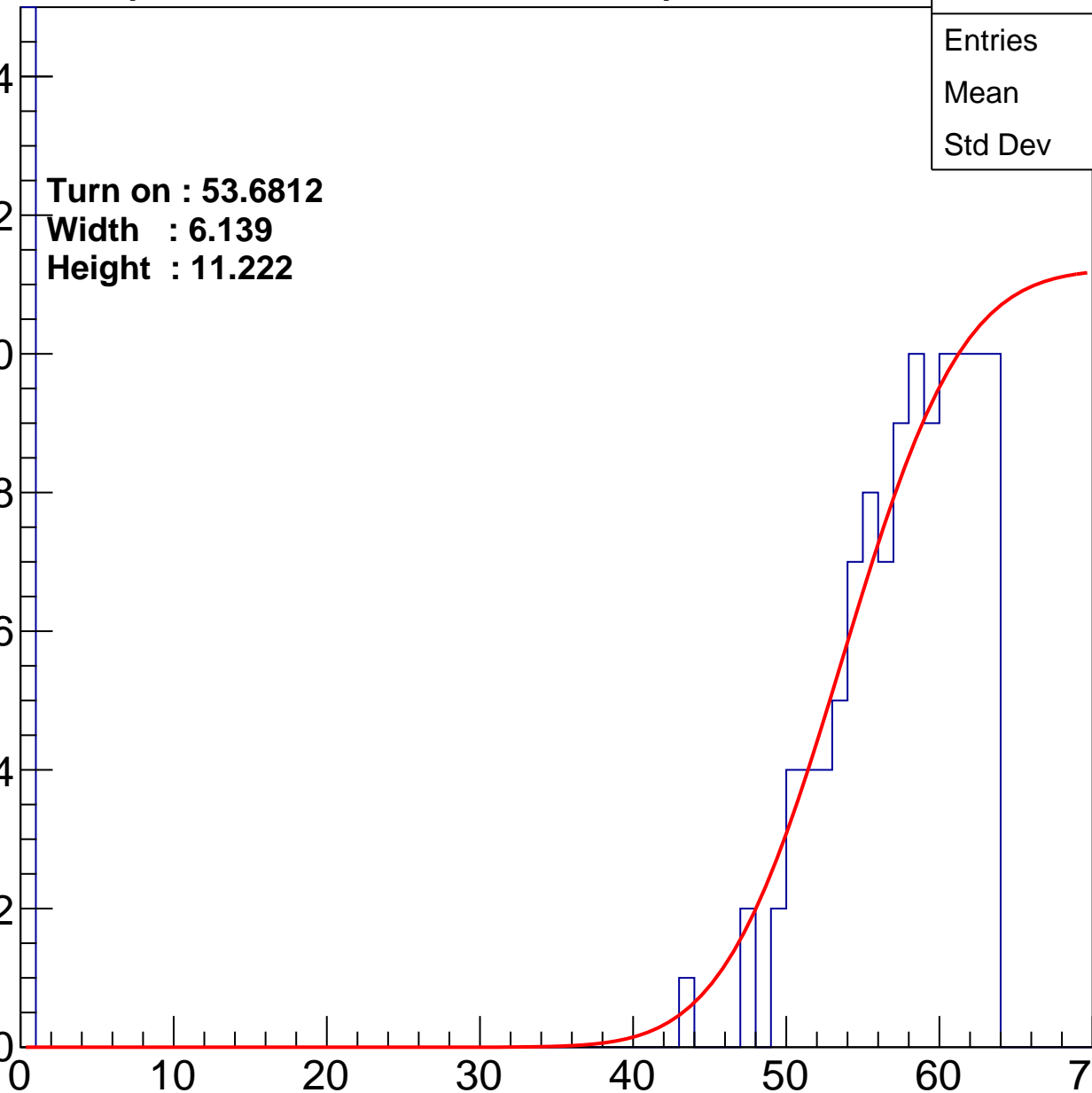
Width : 6.139

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	36.97
Std Dev	27.41

Turn on : 52.8928

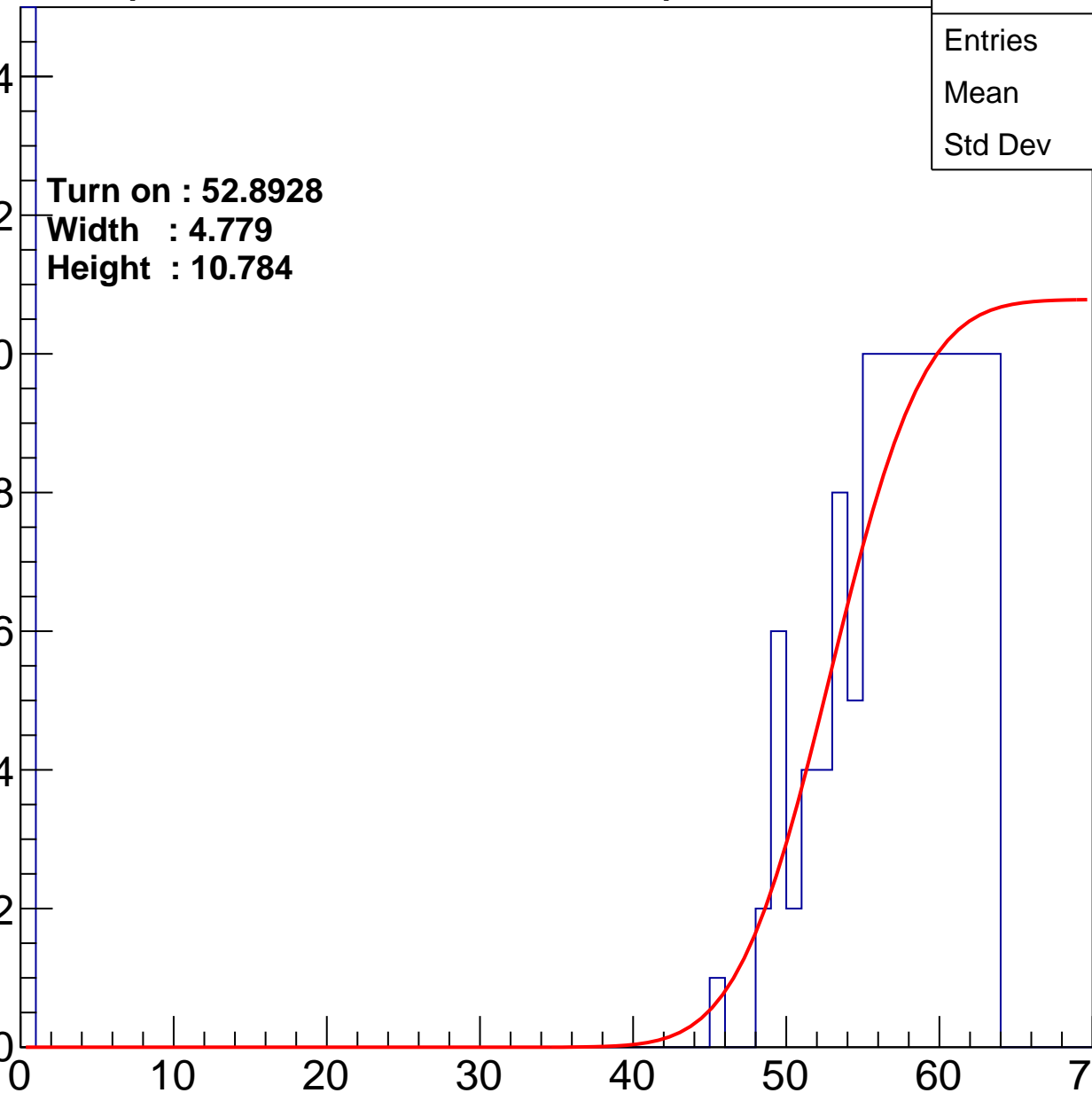
Width : 4.779

Height : 10.784

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	33.56
Std Dev	28.48

Turn on : 52.2664

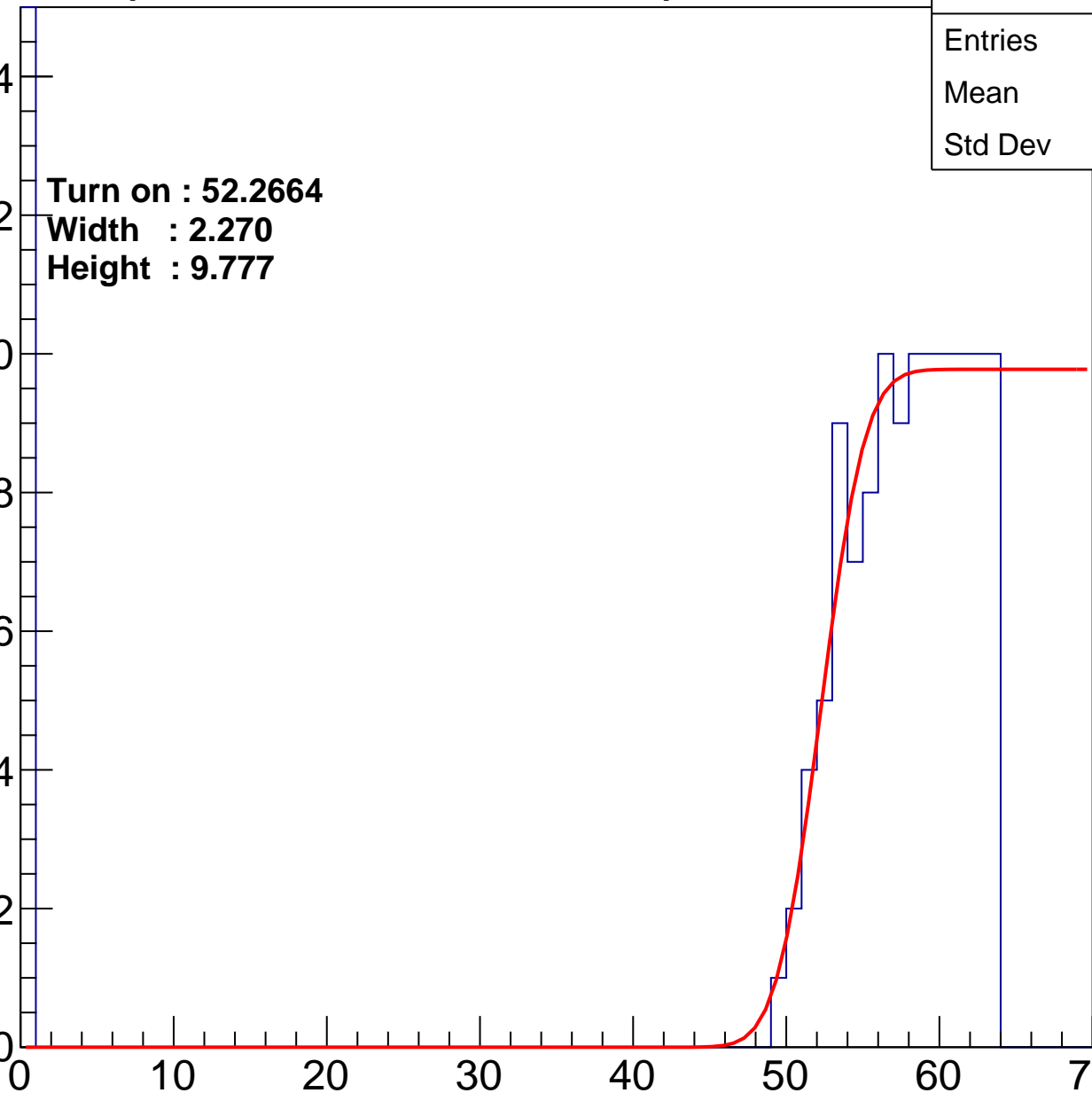
Width : 2.270

Height : 9.777

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	27.78
Std Dev	29.01

Turn on : 54.9213

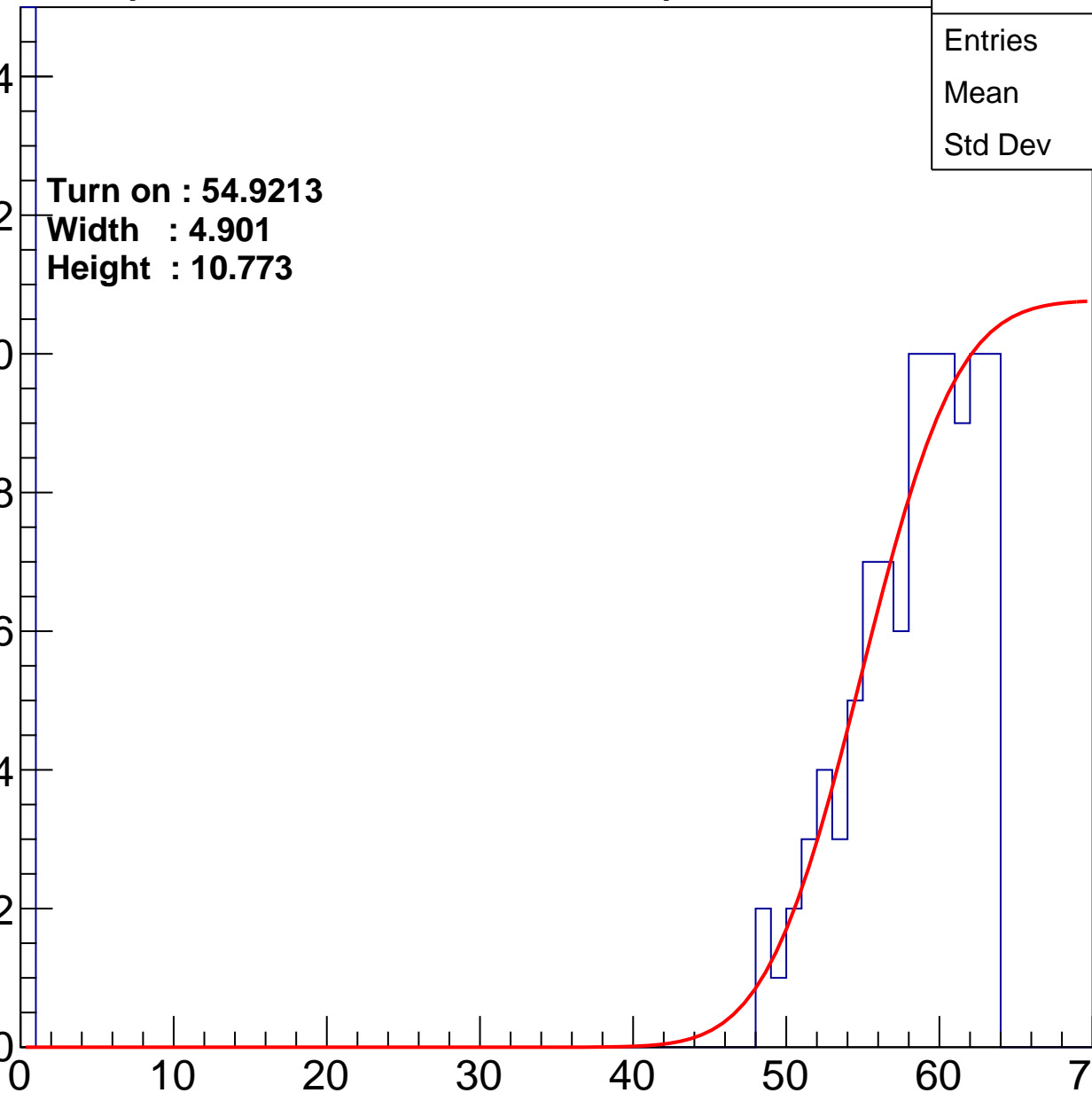
Width : 4.901

Height : 10.773

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	39.6
Std Dev	26.54

Turn on : 52.3981

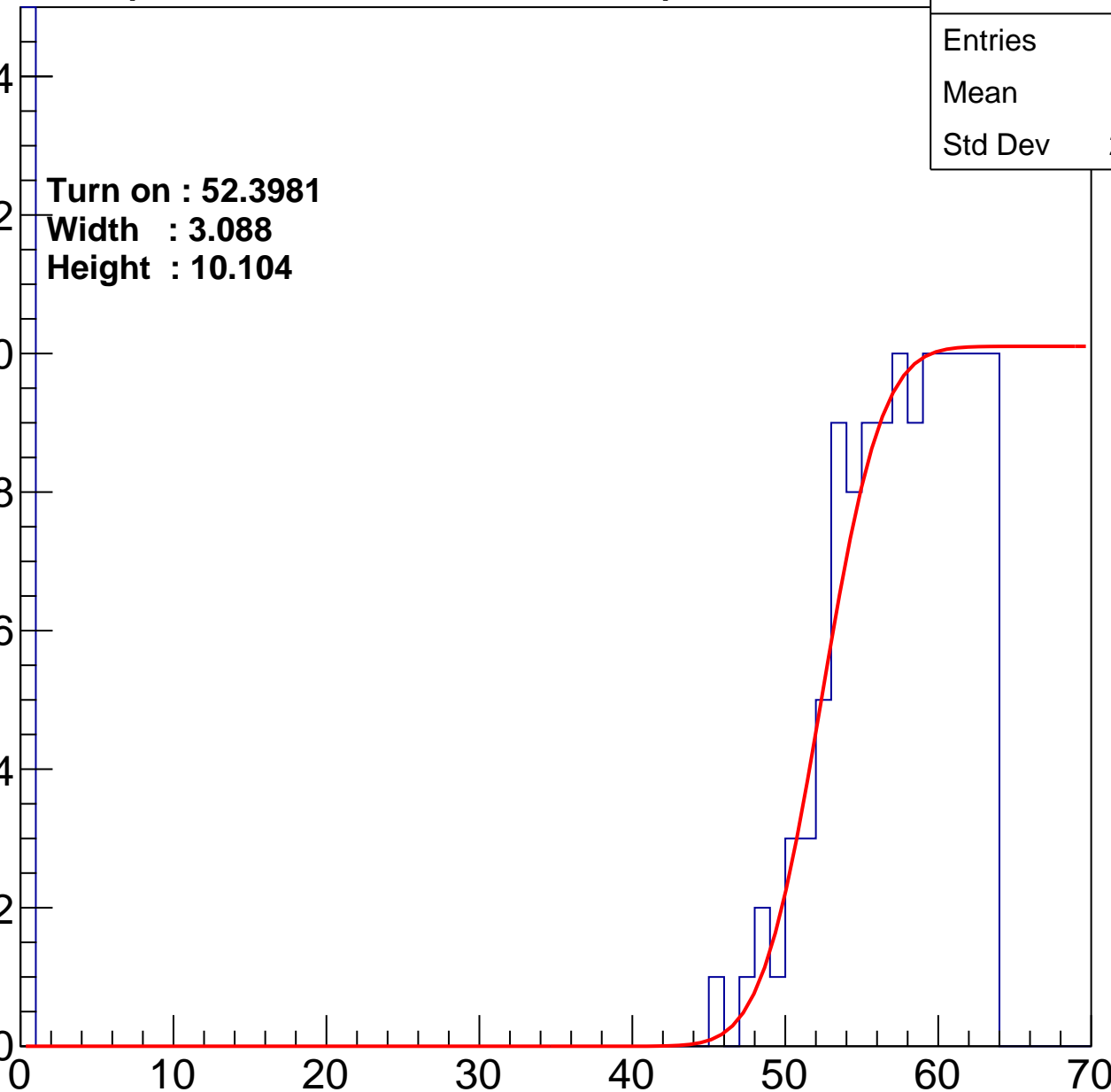
Width : 3.088

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	35.5
Std Dev	27.91

Turn on : 52.7434

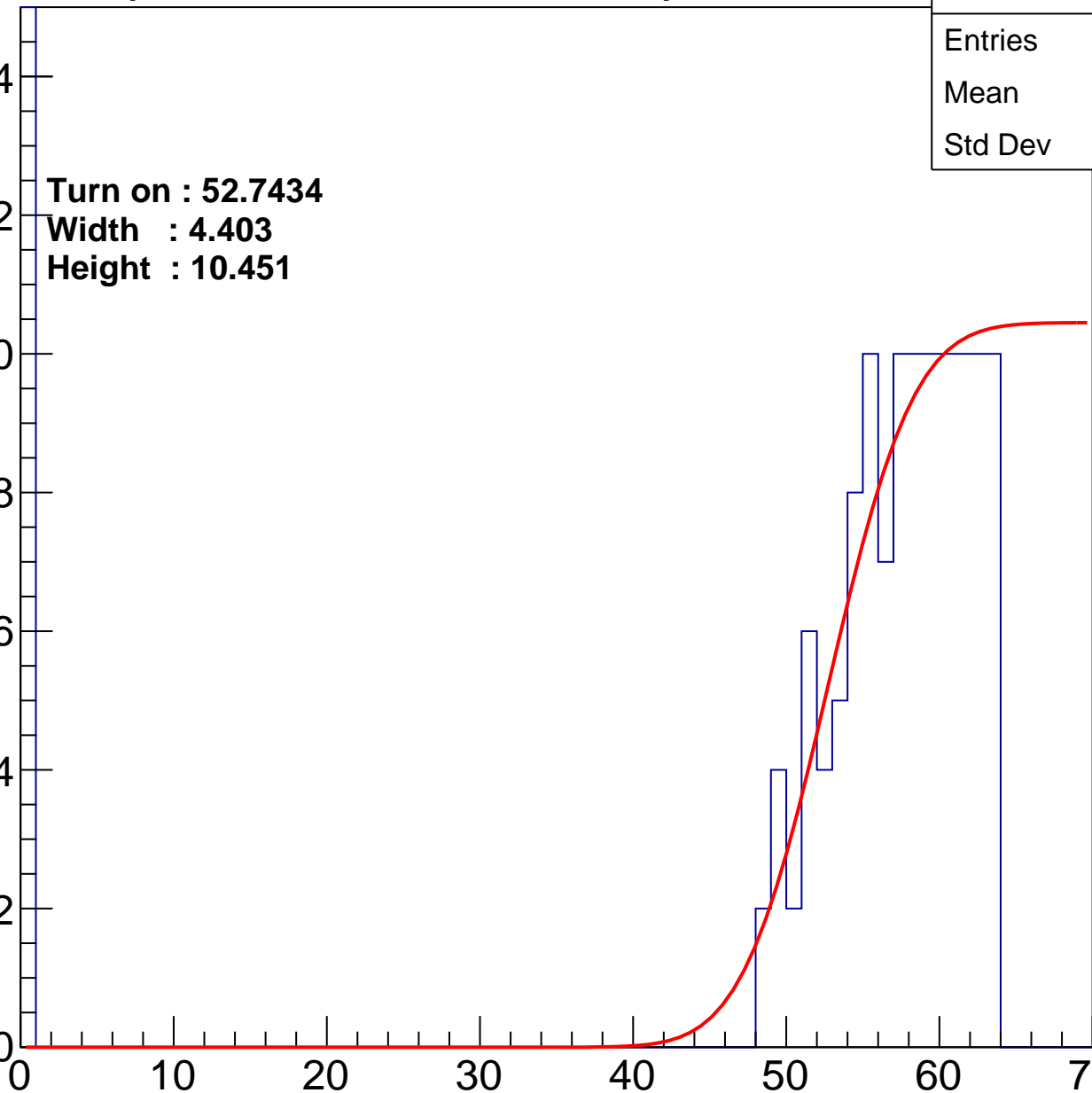
Width : 4.403

Height : 10.451

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	34.63
Std Dev	28.31

Turn on : 54.2898

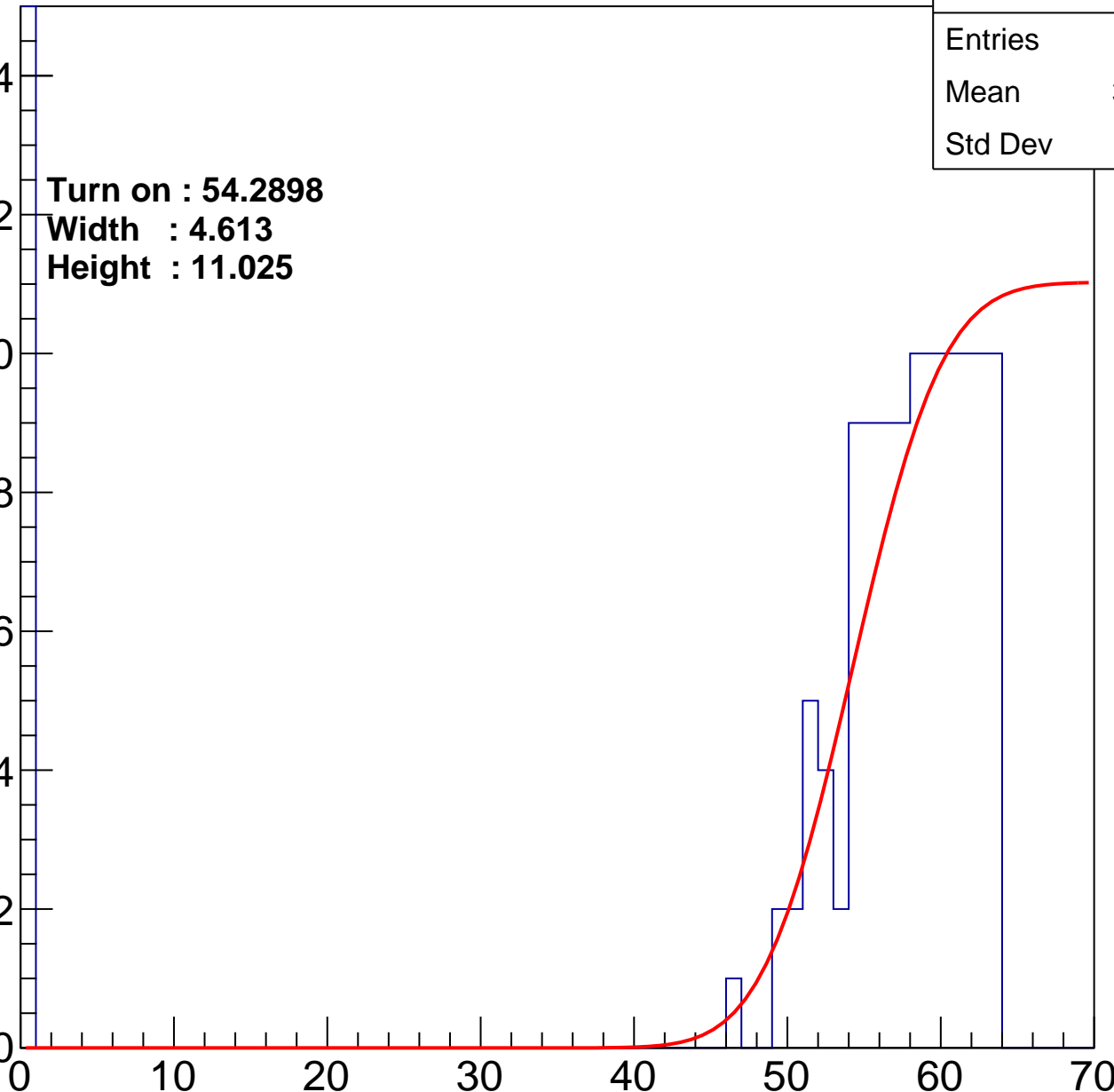
Width : 4.613

Height : 11.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	34.06
Std Dev	28.26

Turn on : 52.1952

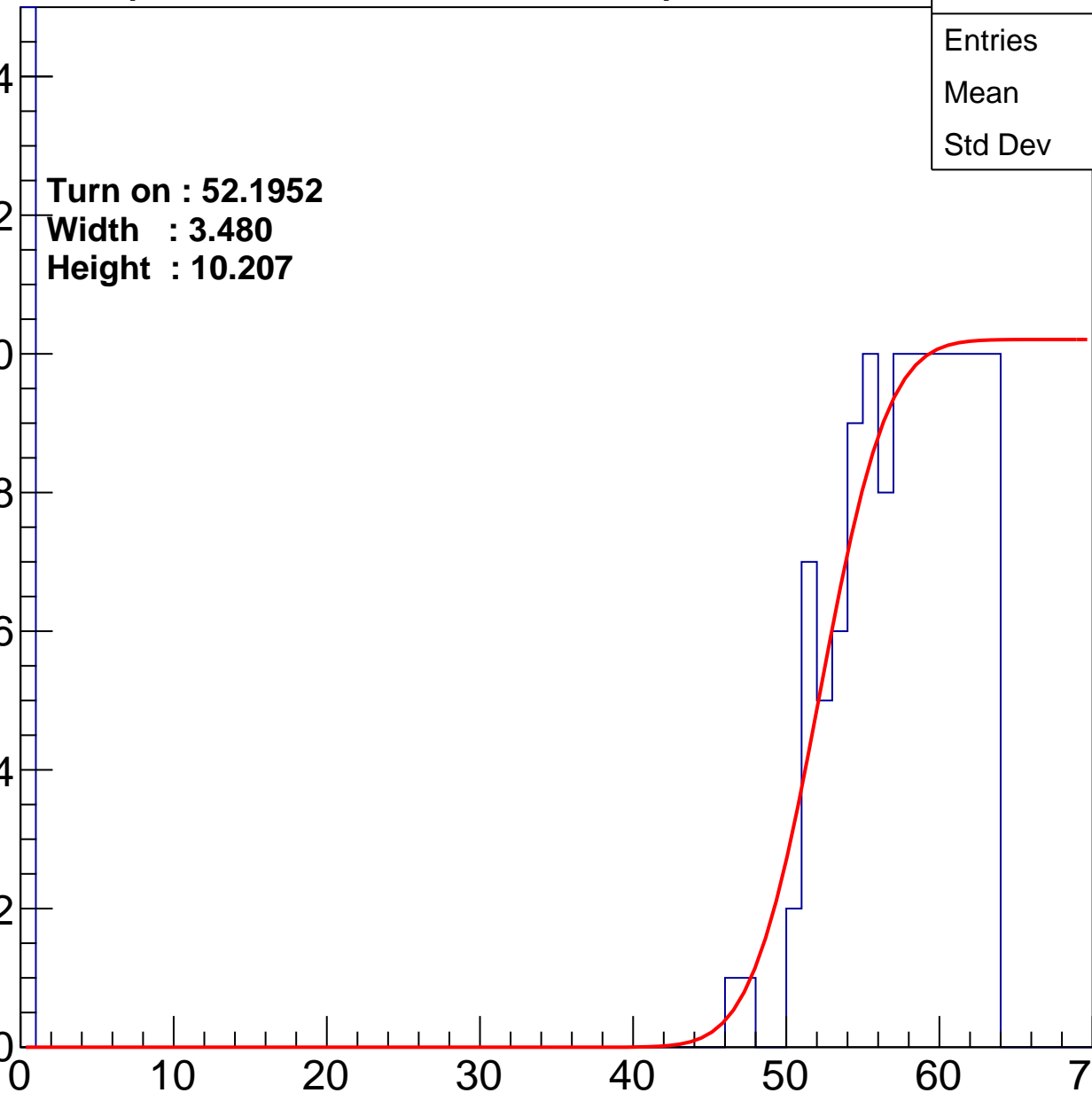
Width : 3.480

Height : 10.207

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	36.45
Std Dev	28.11

Turn on : 54.0090

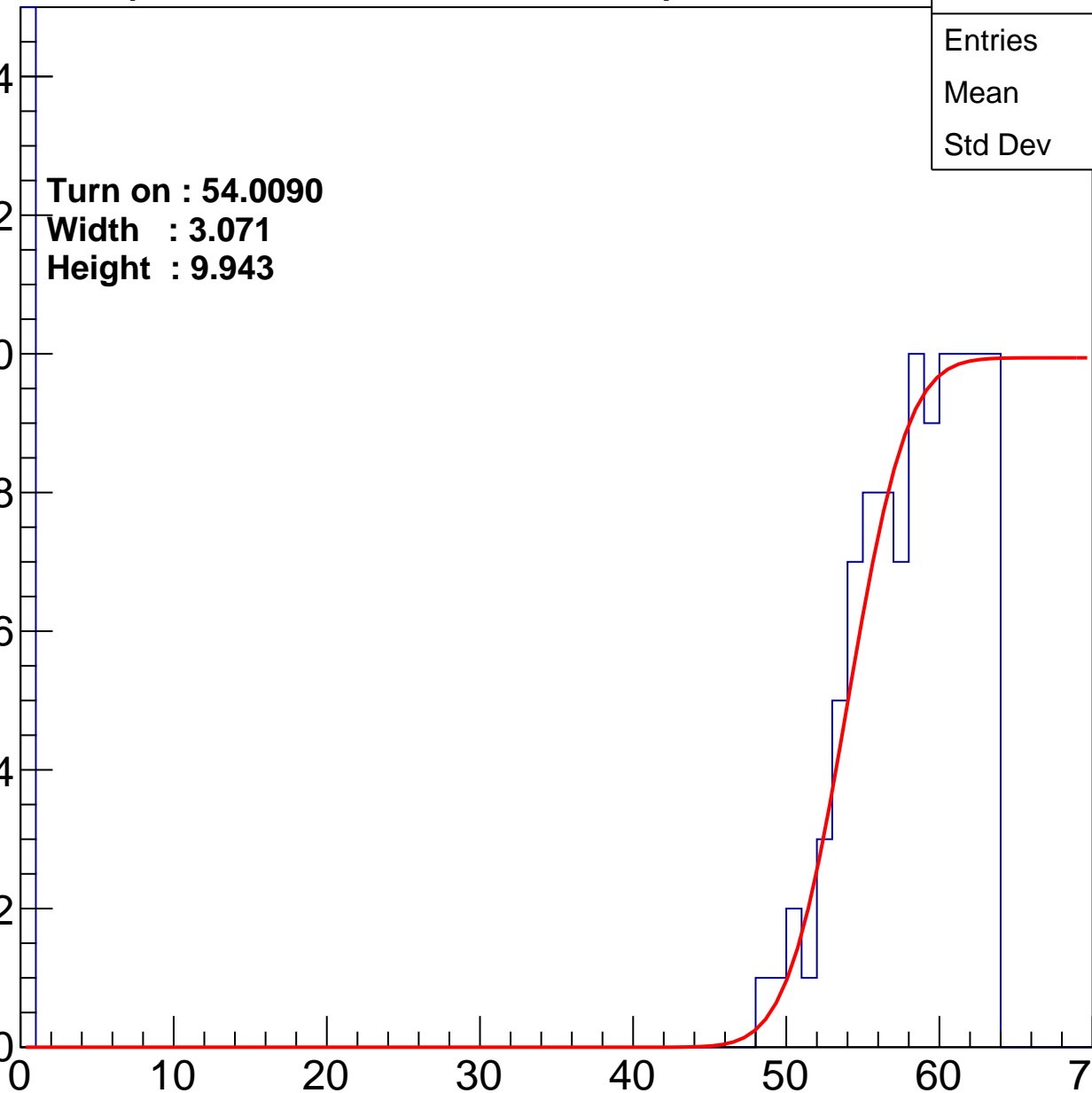
Width : 3.071

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	30.57
Std Dev	28.87

Turn on : 53.0533

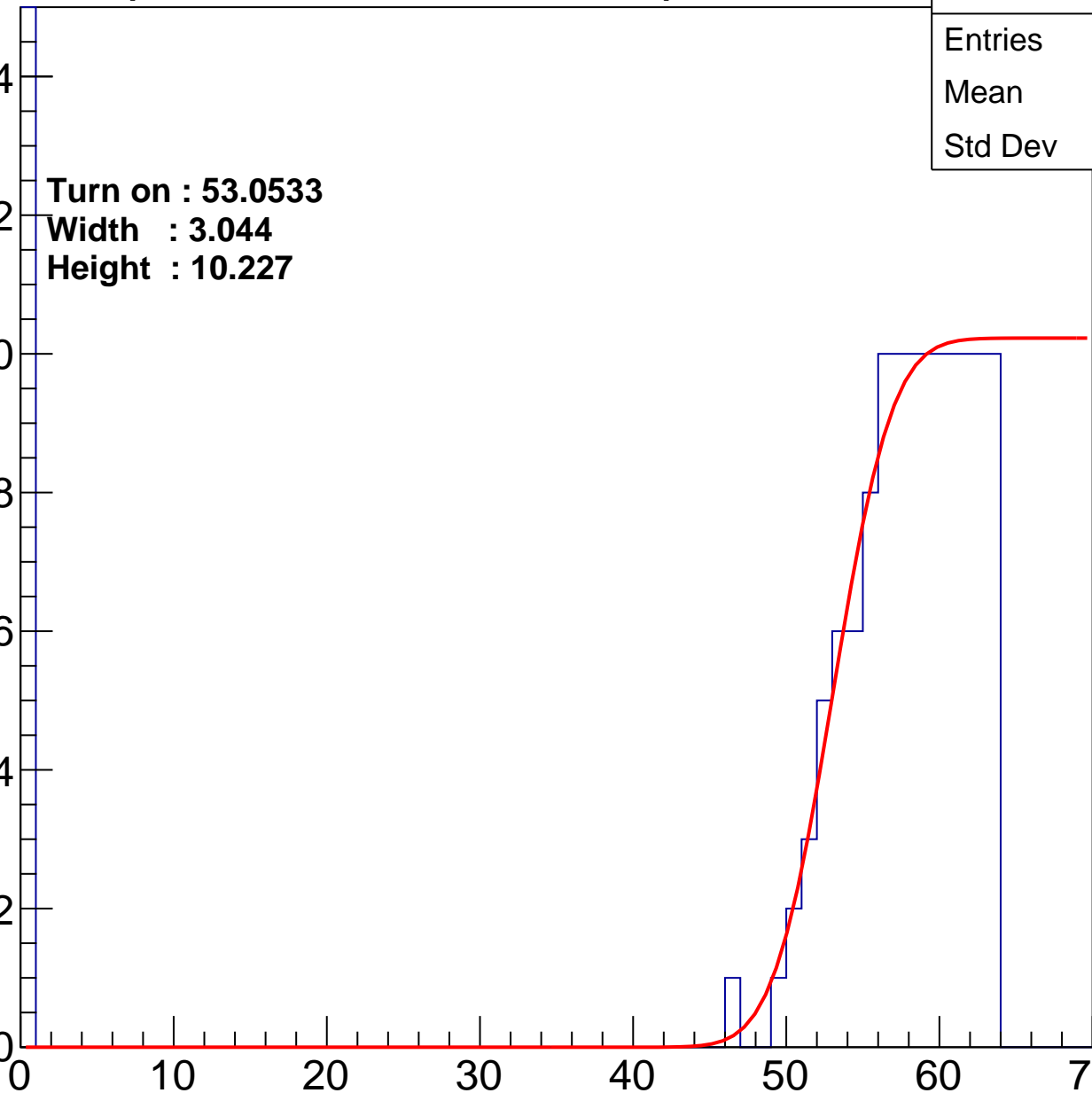
Width : 3.044

Height : 10.227

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	35.24
Std Dev	28.54

Turn on : 54.5171

Width : 3.041

Height : 10.282

Entry

14

12

10

8

6

4

2

0

0

10

20

30

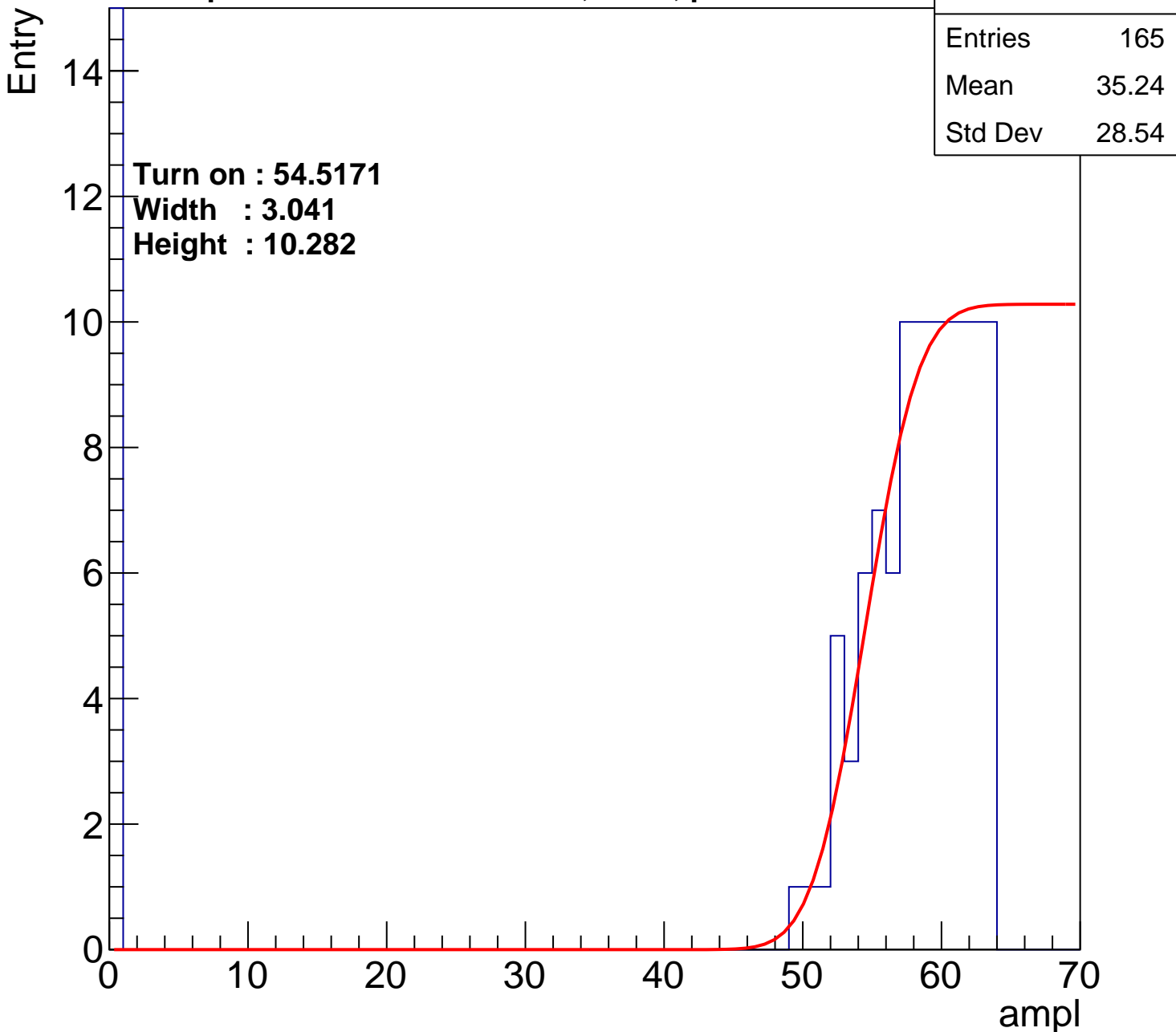
40

50

60

70

ampl



B1L104S, U15-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	218
Mean	34.18
Std Dev	27.8

Turn on : 50.9016

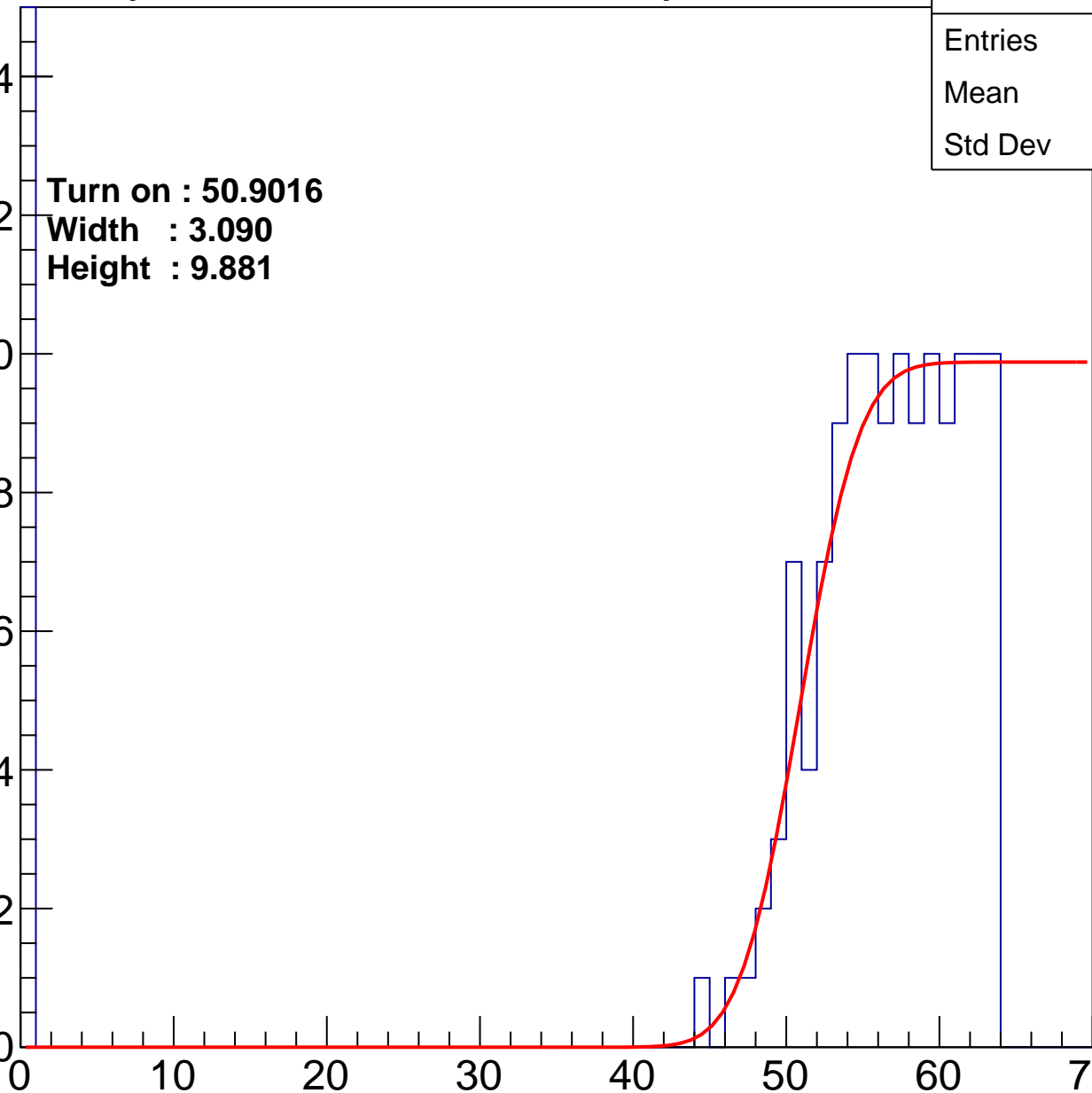
Width : 3.090

Height : 9.881

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	36.85
Std Dev	27.67

Turn on : 52.2049

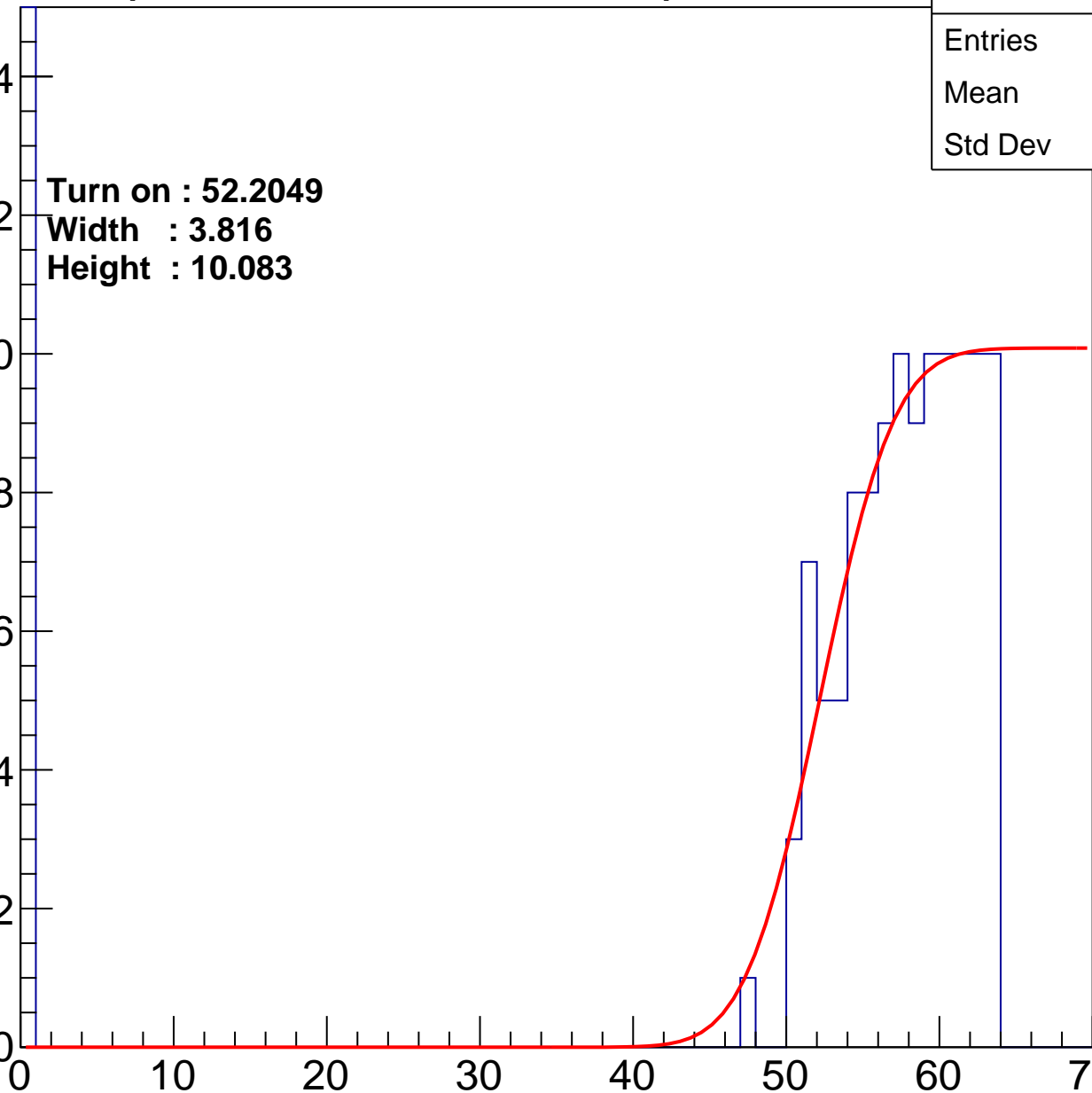
Width : 3.816

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	32
Std Dev	28.6

Turn on : 53.2155

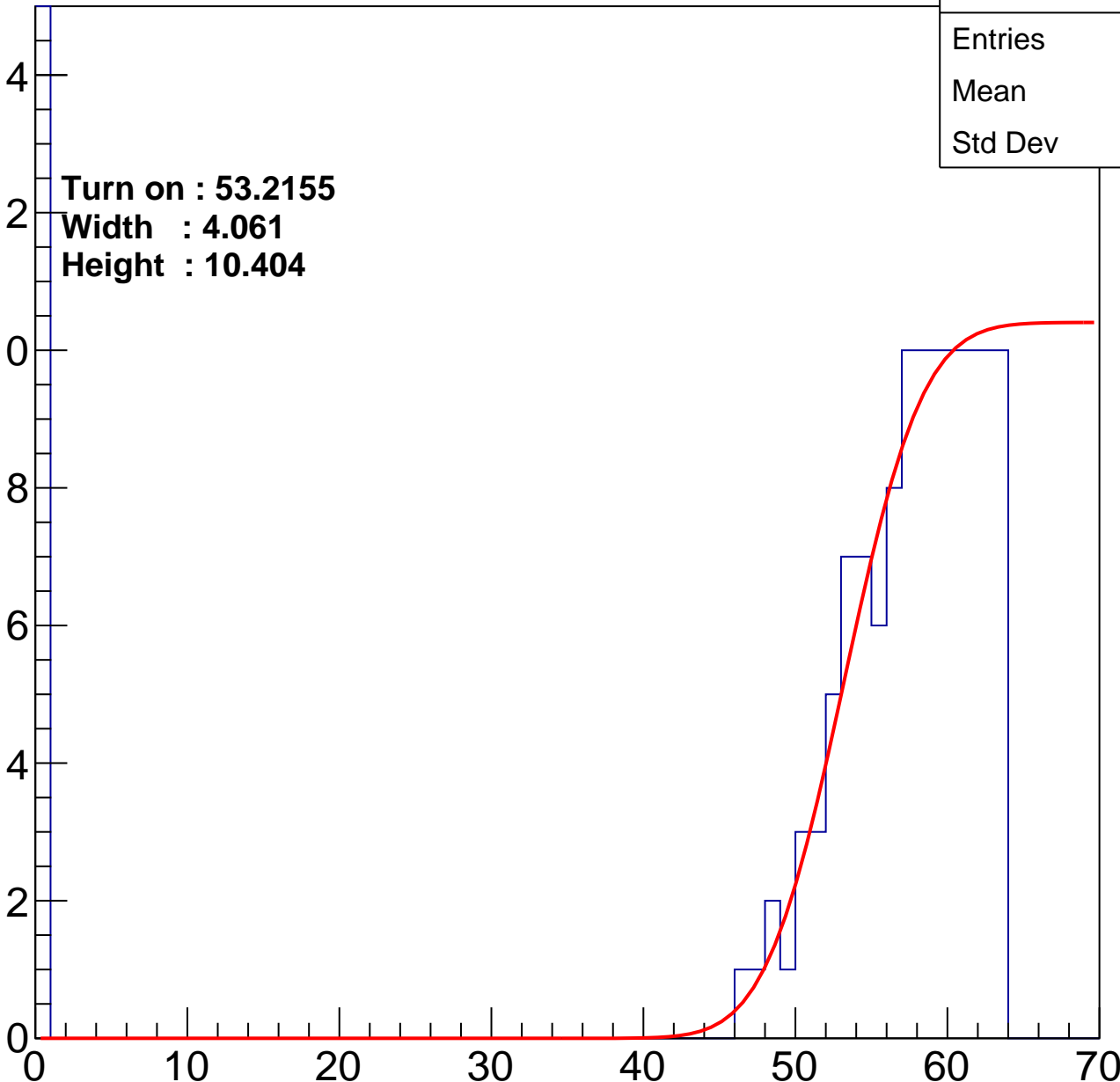
Width : 4.061

Height : 10.404

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	37.67
Std Dev	27.64

Turn on : 53.5652

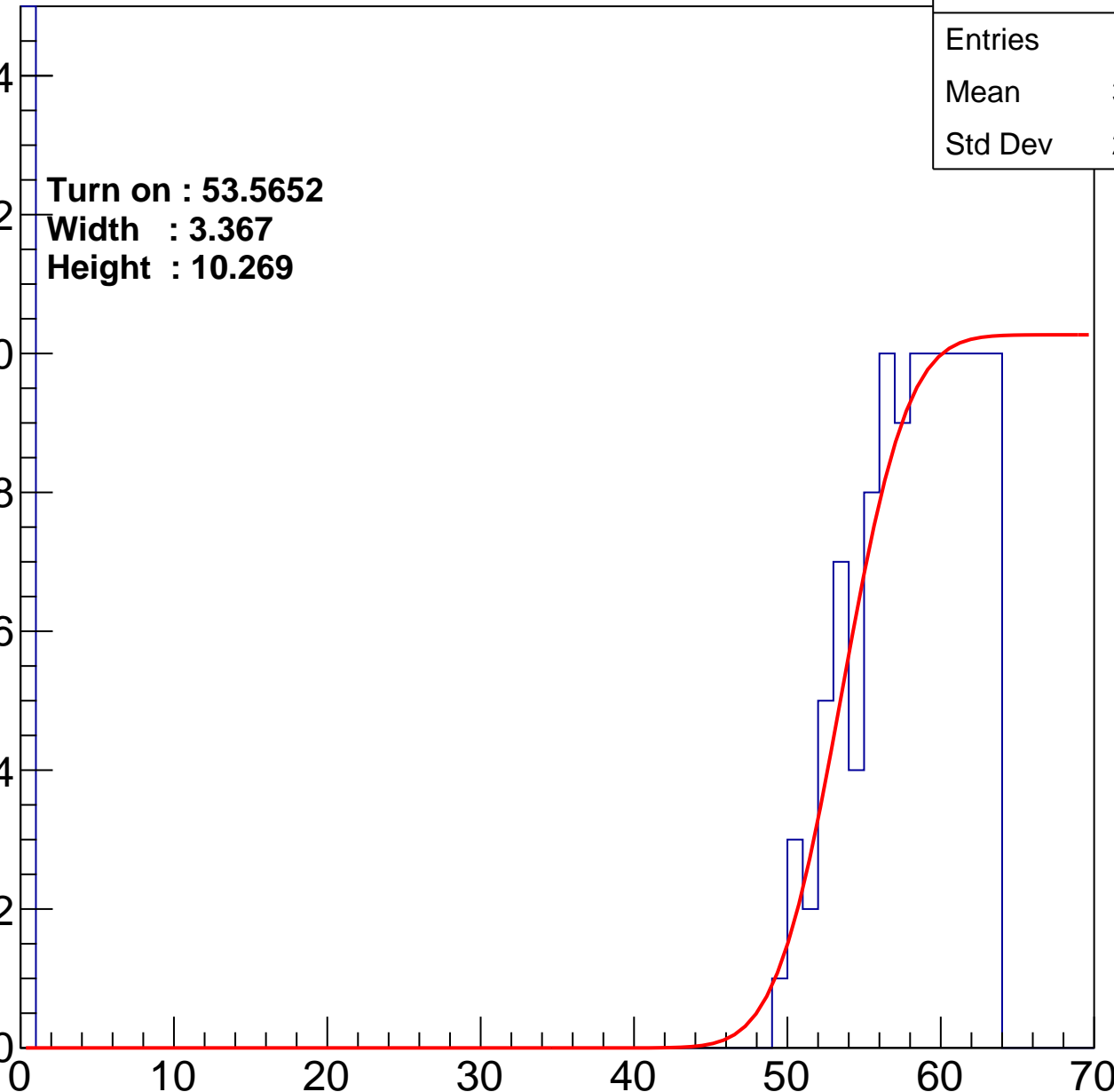
Width : 3.367

Height : 10.269

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	31.71
Std Dev	28.38

Turn on : 51.8896

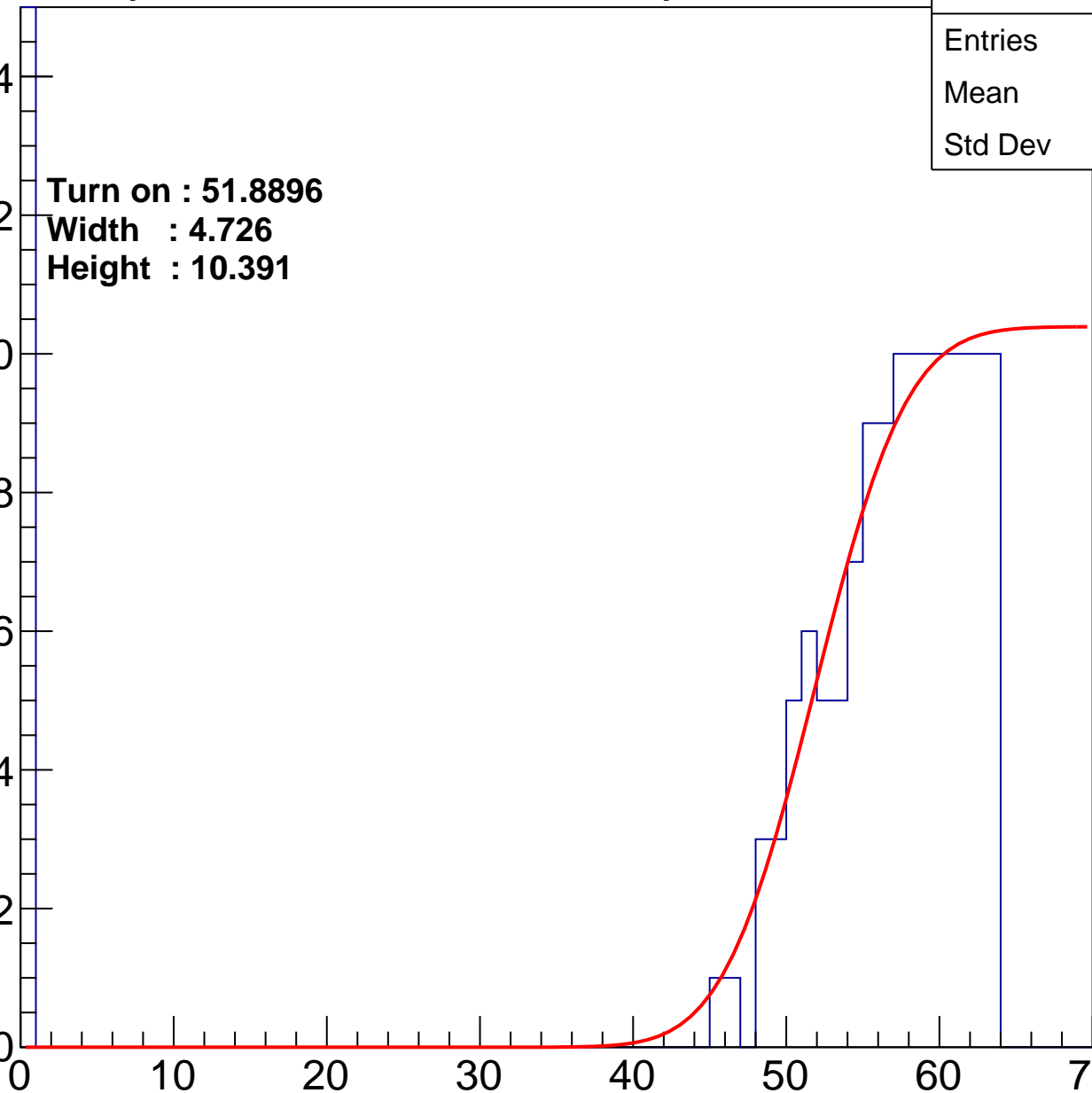
Width : 4.726

Height : 10.391

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch43

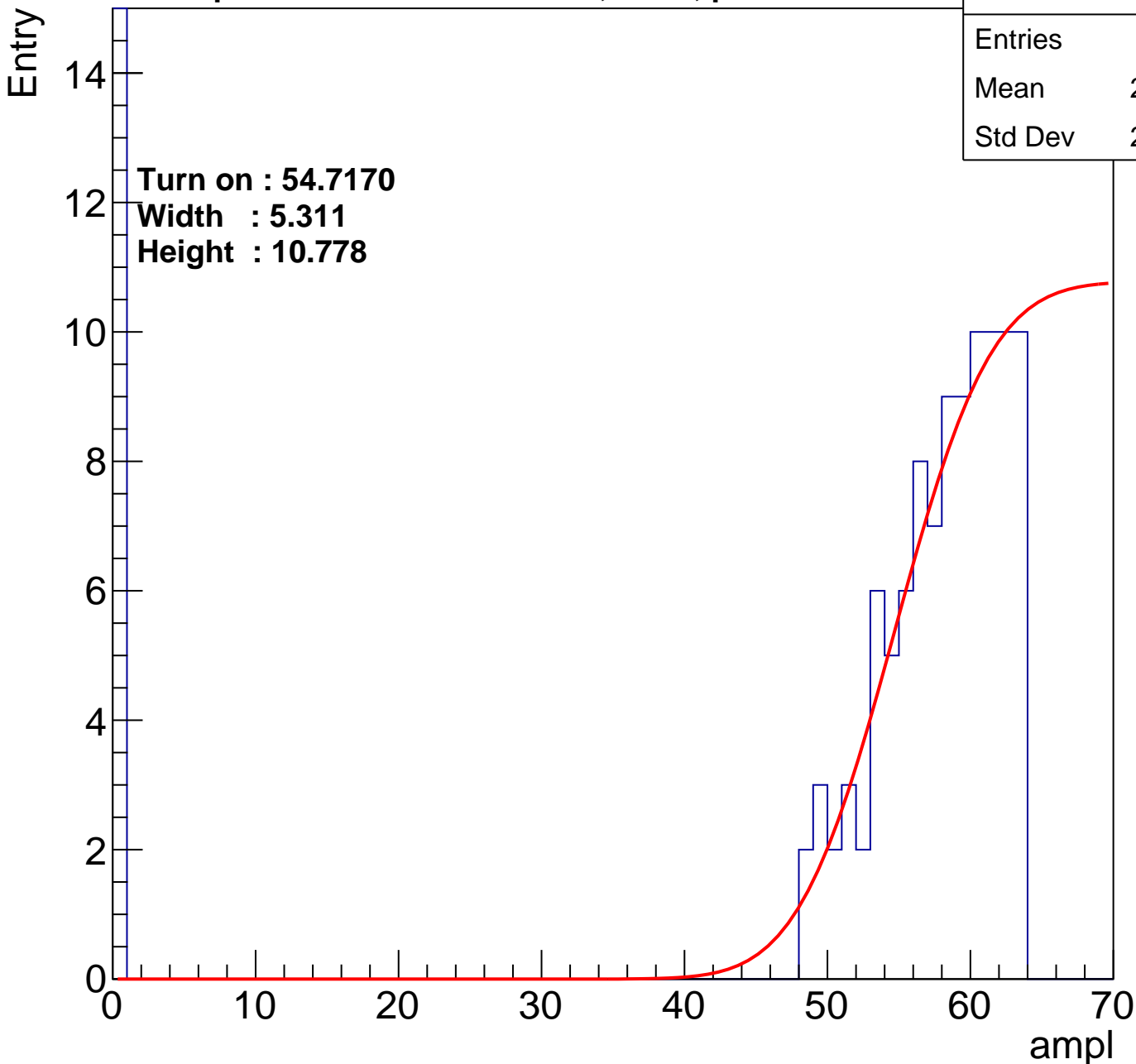
calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	29.84
Std Dev	28.94

Turn on : 54.7170

Width : 5.311

Height : 10.778



B1L104S, U15-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	34.75
Std Dev	27.71

Turn on : 50.9695

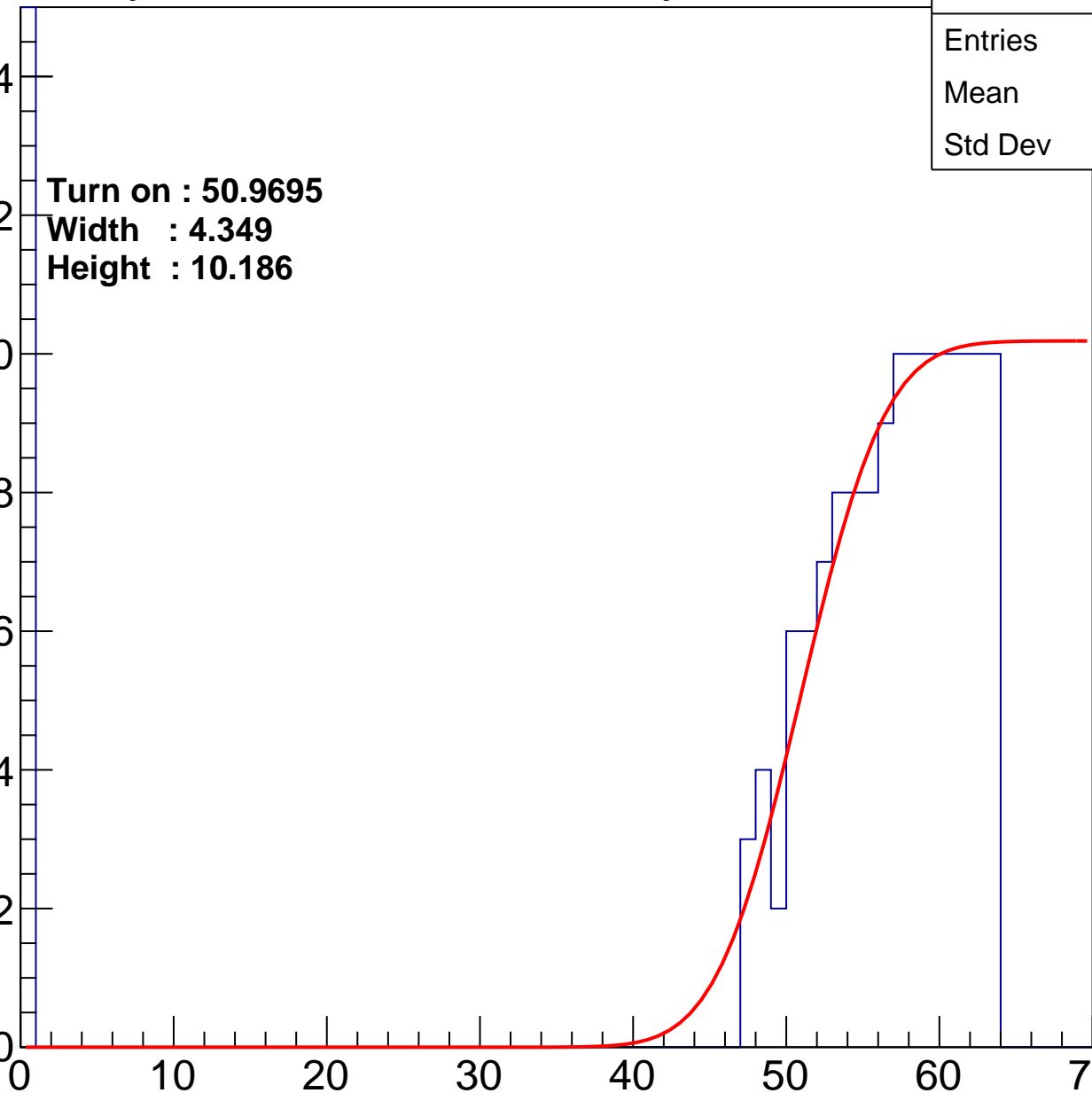
Width : 4.349

Height : 10.186

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	32.12
Std Dev	29.02

Turn on : 54.3401

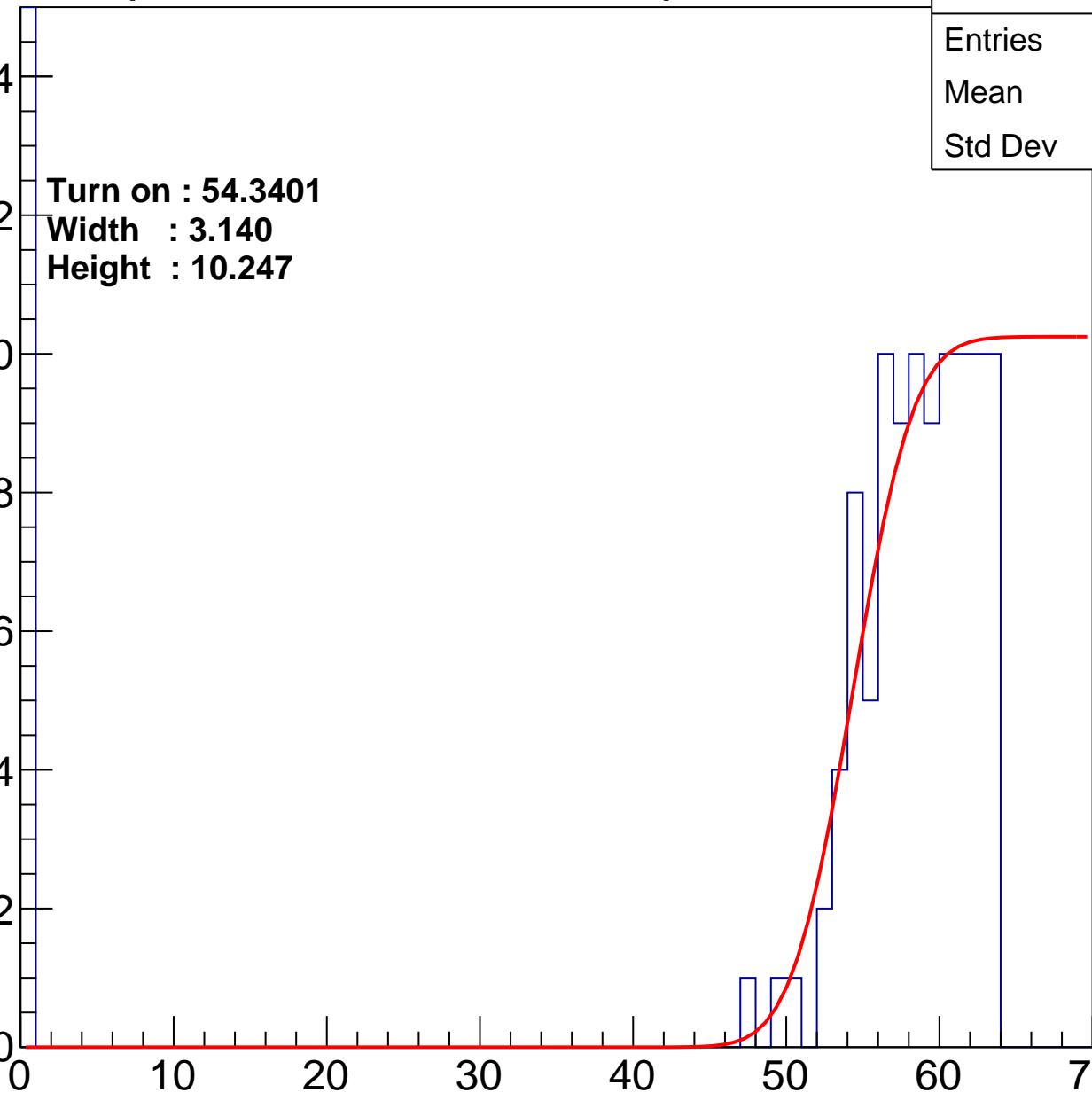
Width : 3.140

Height : 10.247

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch46

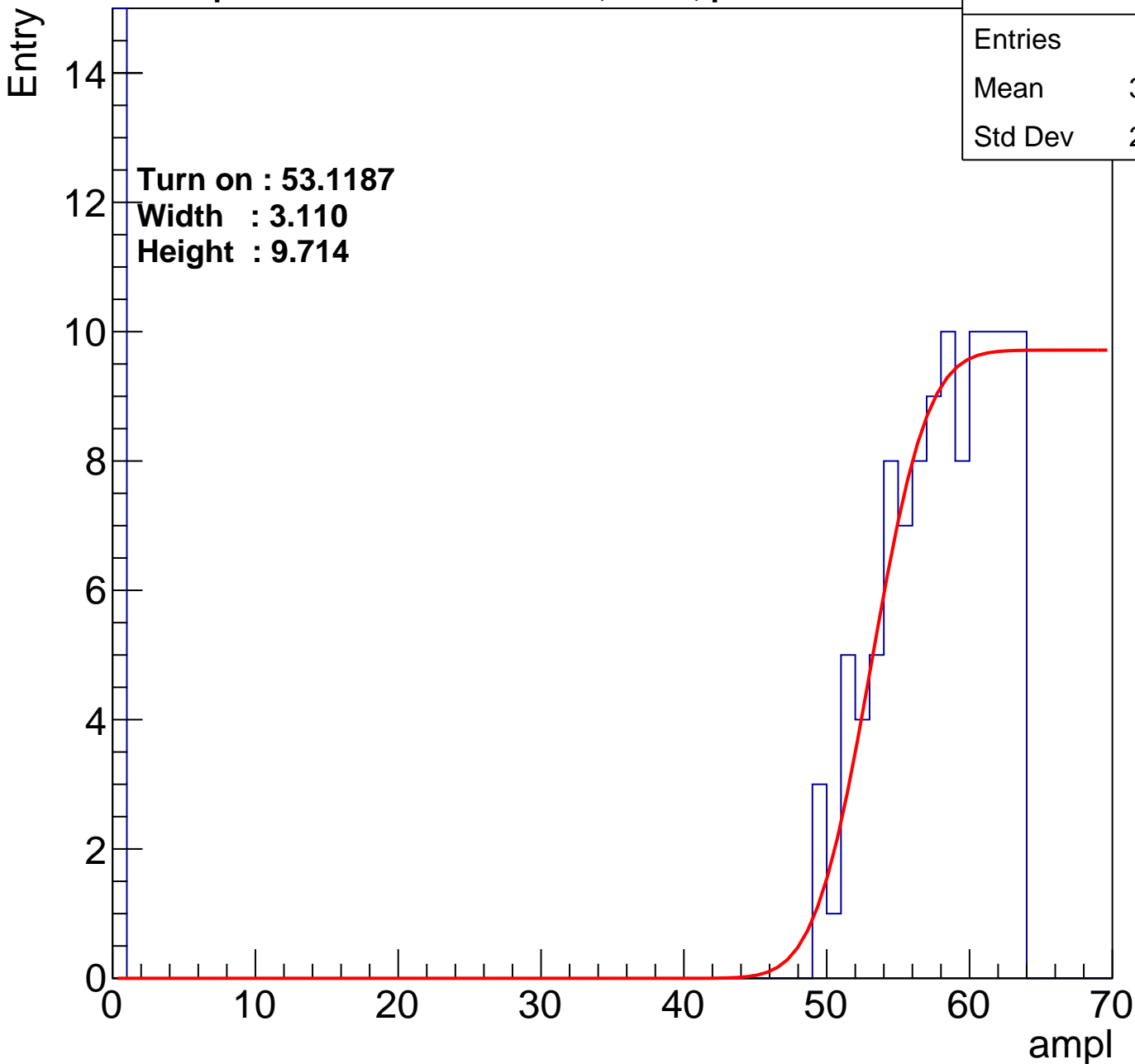
calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	33.24
Std Dev	28.57

Turn on : 53.1187

Width : 3.110

Height : 9.714



B1L104S, U15-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	37.39
Std Dev	27.4

Turn on : 52.3624

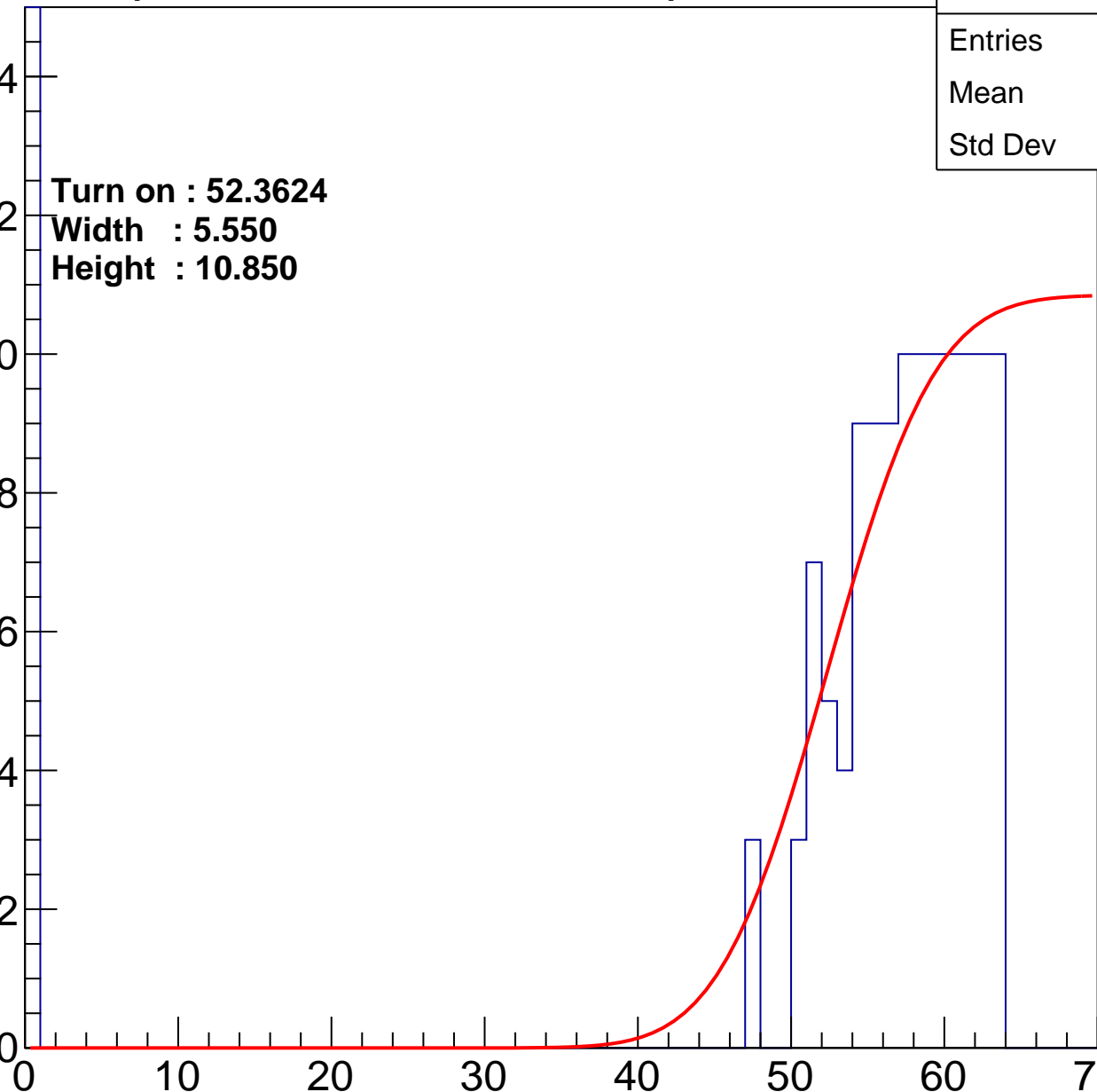
Width : 5.550

Height : 10.850

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	34.26
Std Dev	28.32

Turn on : 52.7753

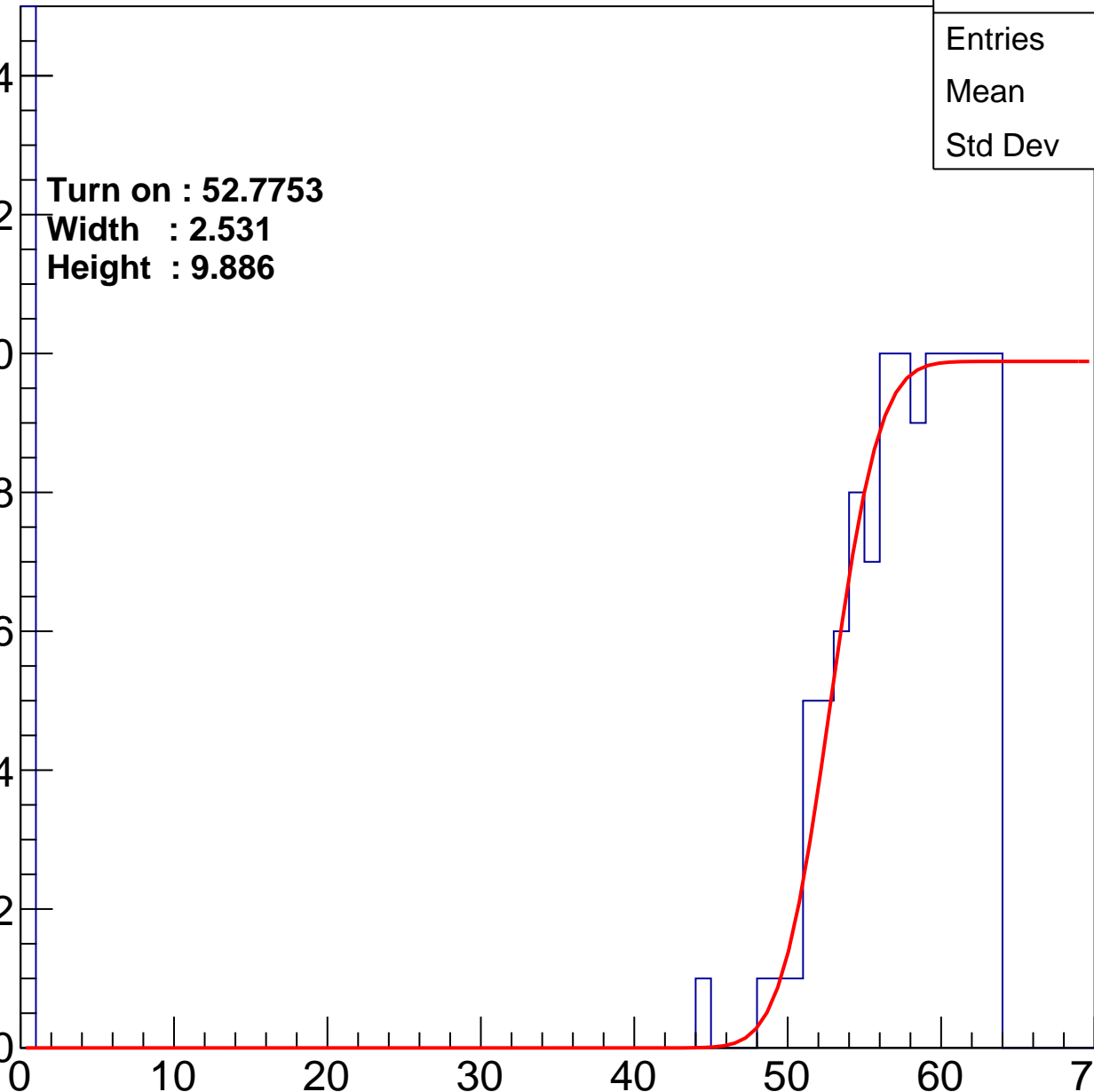
Width : 2.531

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	35.76
Std Dev	27.77

Turn on : 51.6372

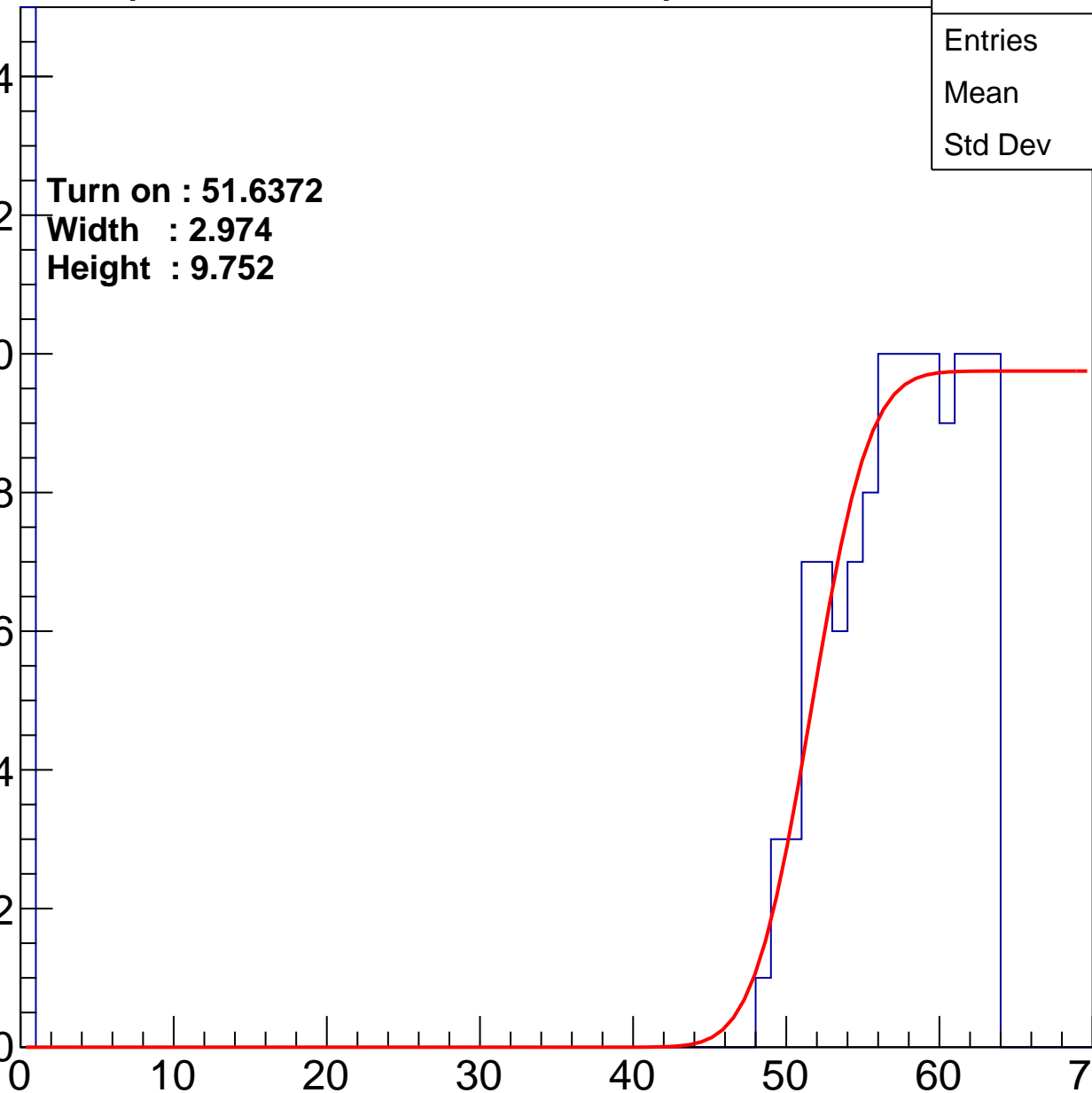
Width : 2.974

Height : 9.752

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	36
Std Dev	27.16

Turn on : 51.0630

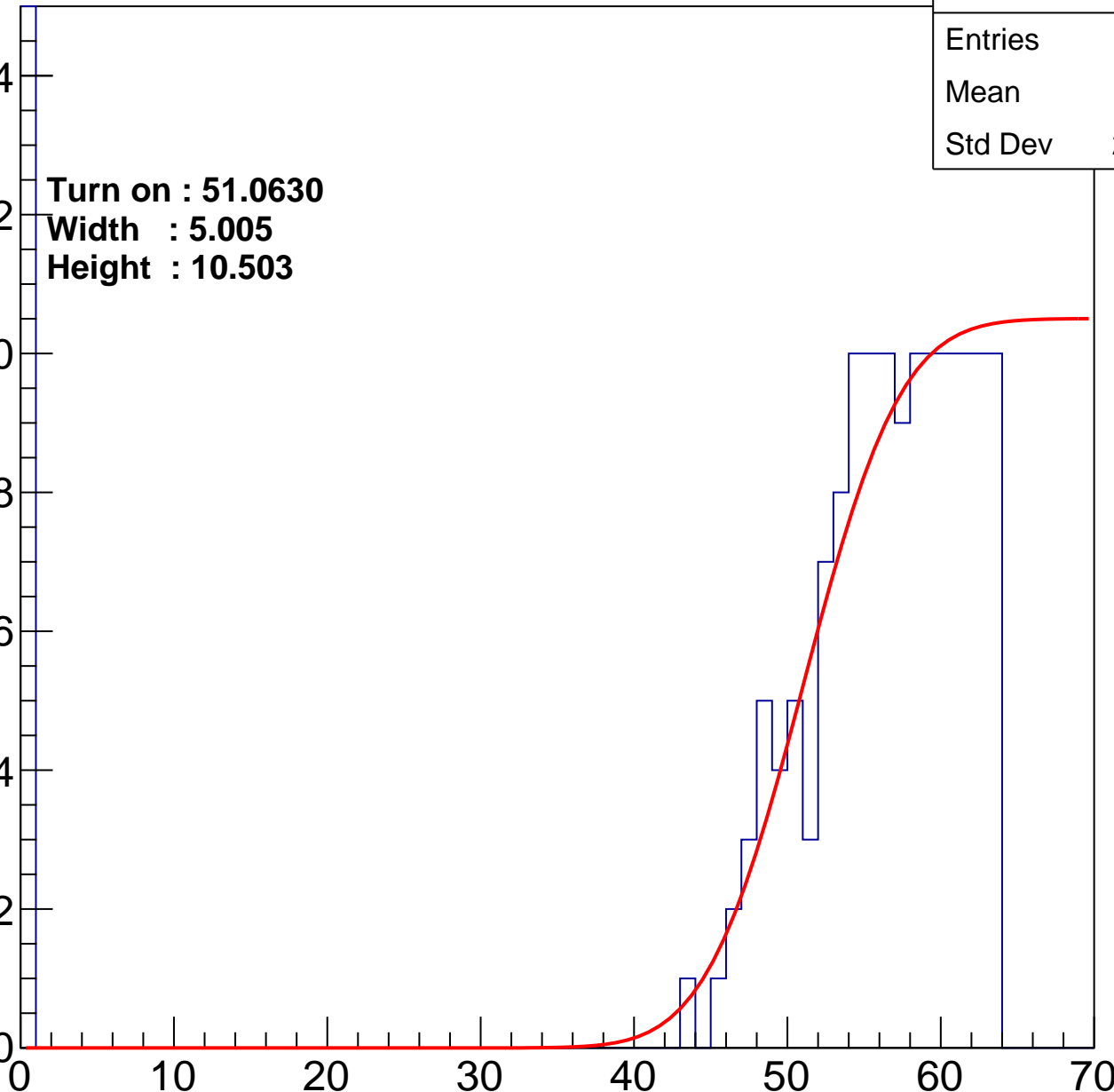
Width : 5.005

Height : 10.503

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch51

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	37.5
Std Dev	27.52

Turn on : 53.0713

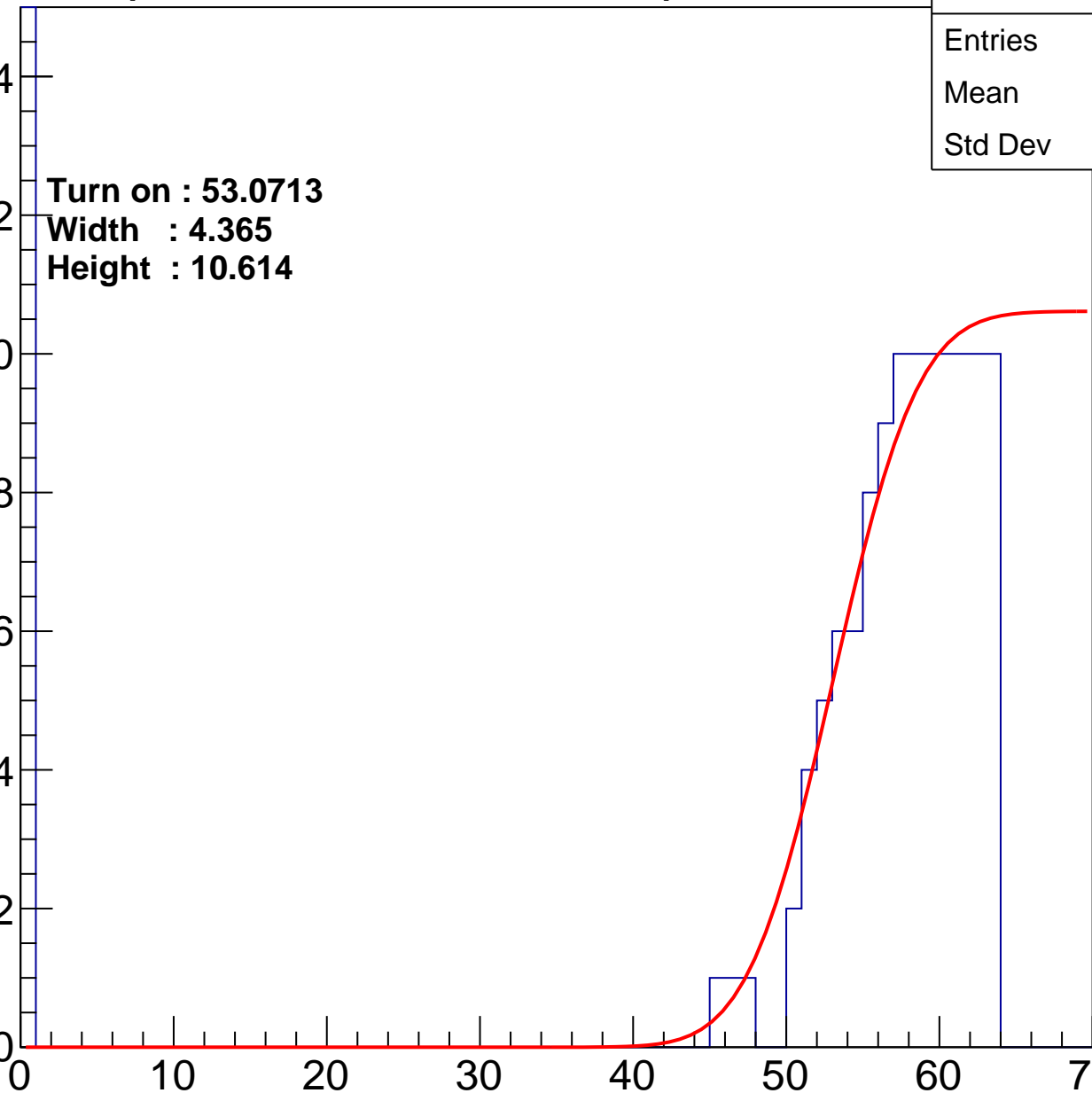
Width : 4.365

Height : 10.614

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	36.69
Std Dev	27.65

Turn on : 52.7822

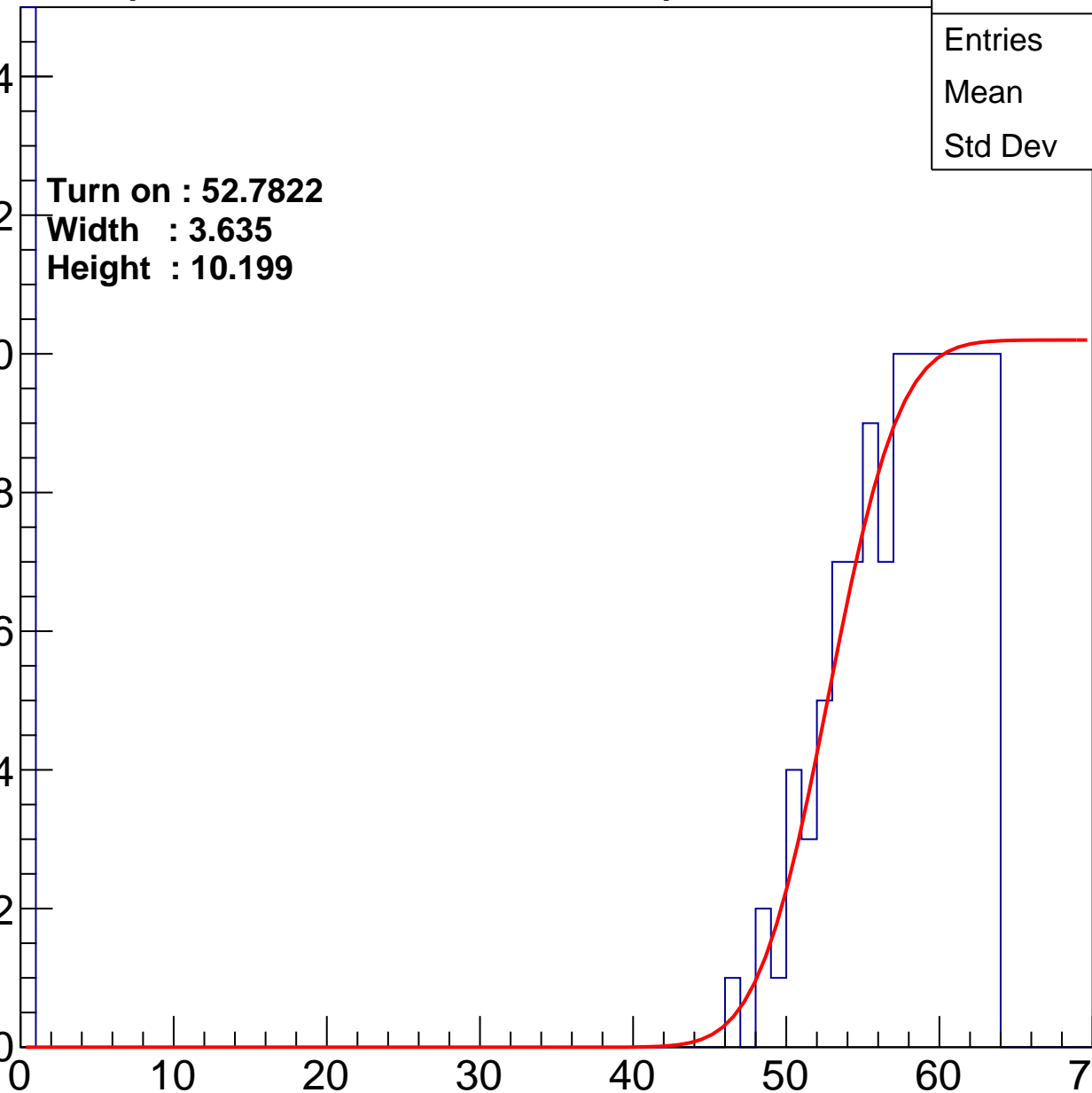
Width : 3.635

Height : 10.199

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	223
Mean	35.87
Std Dev	27.12

Turn on : 51.0074

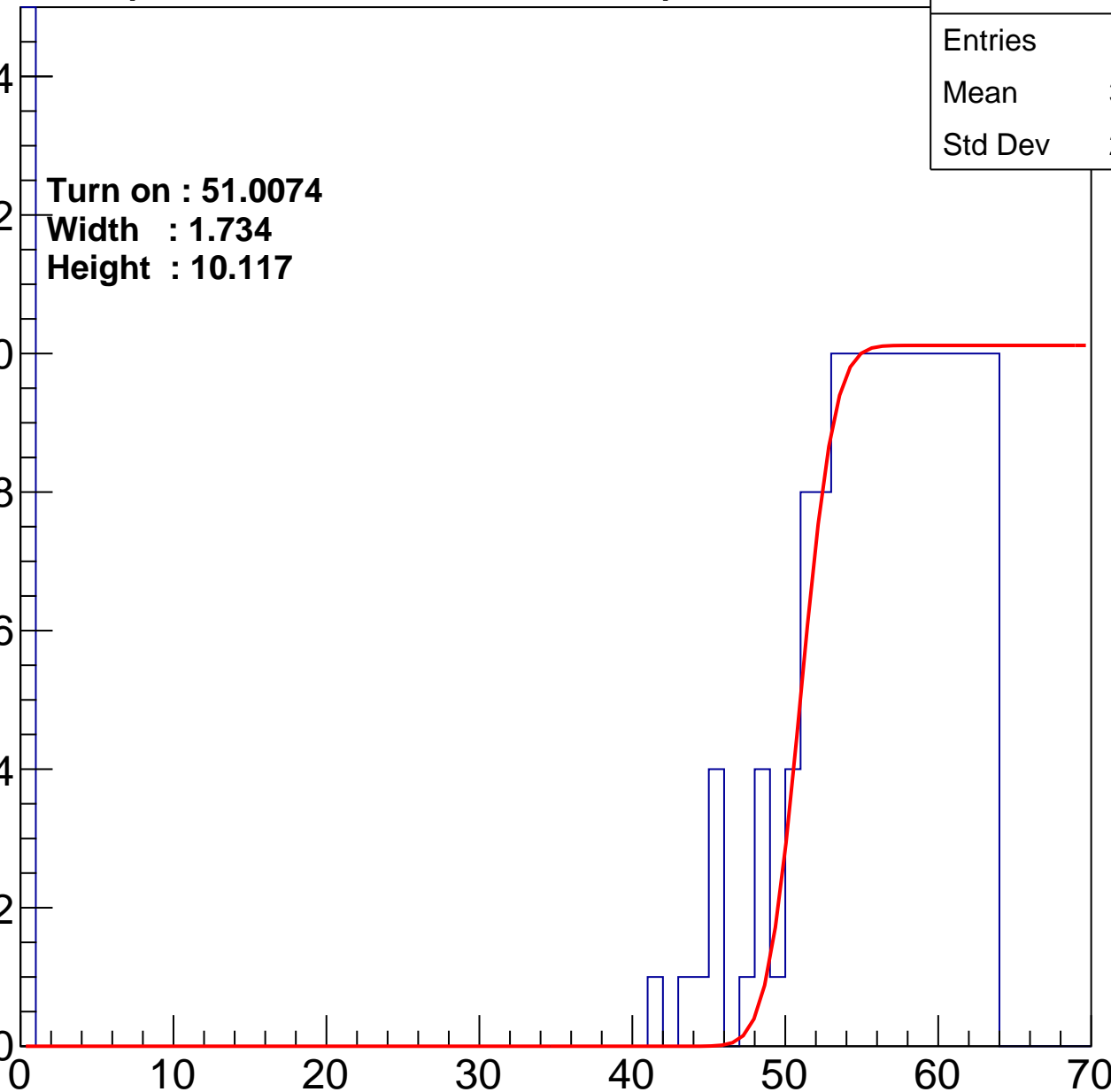
Width : 1.734

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch54

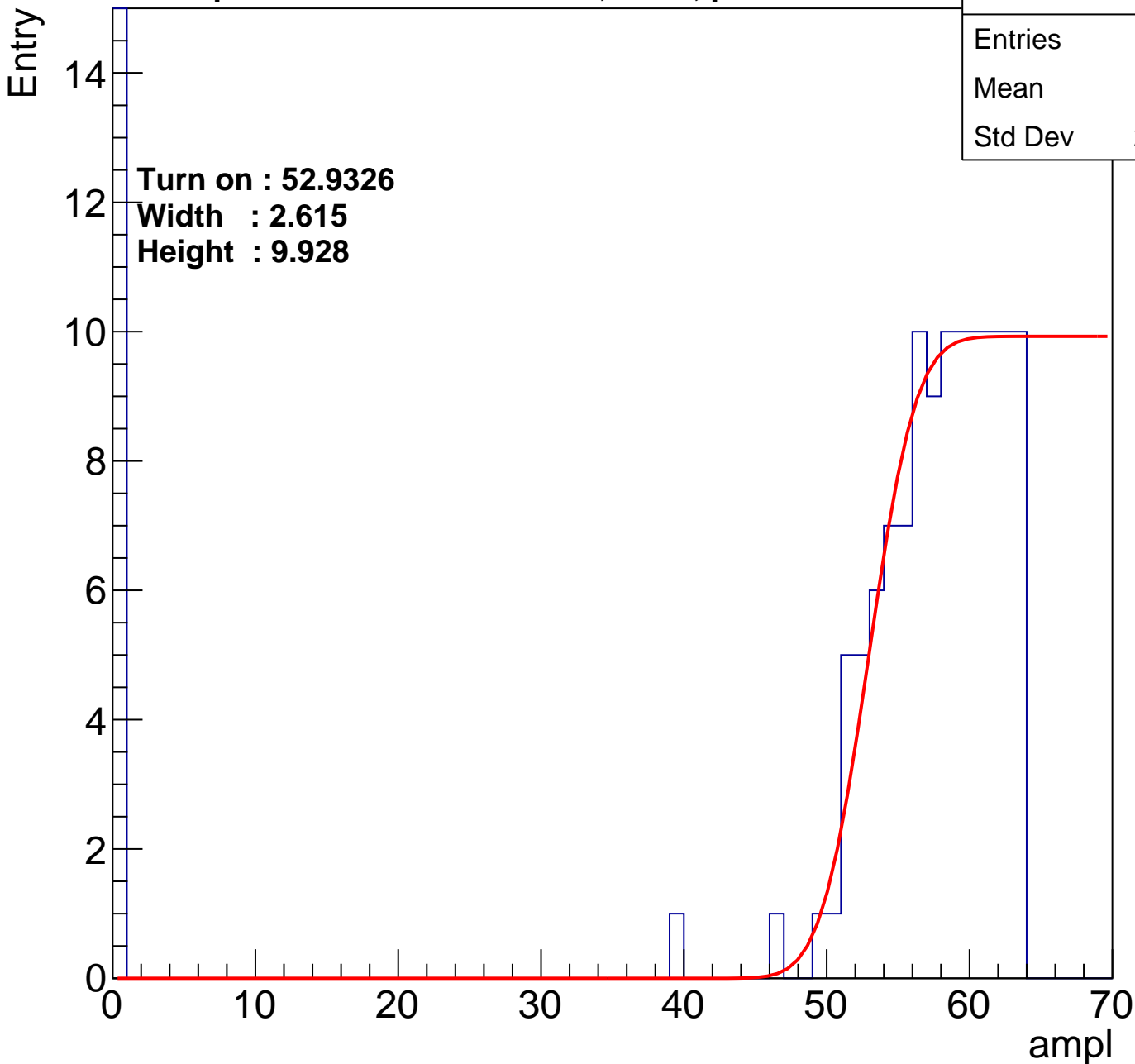
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	34.3
Std Dev	28.31

Turn on : 52.9326

Width : 2.615

Height : 9.928



B1L104S, U15-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	37.15
Std Dev	27.55

Turn on : 52.4172

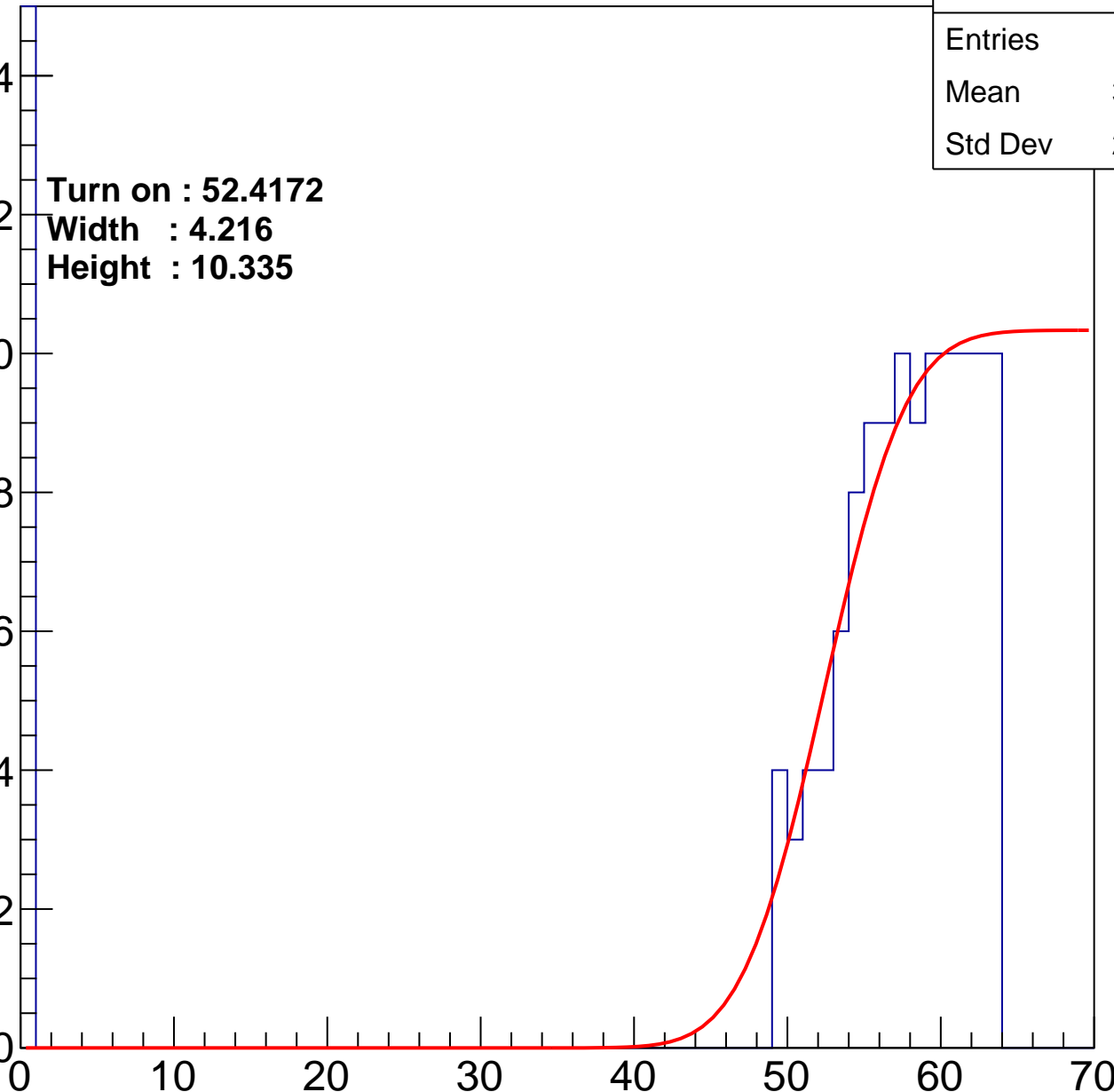
Width : 4.216

Height : 10.335

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	32.44
Std Dev	28.86

Turn on : 53.8380

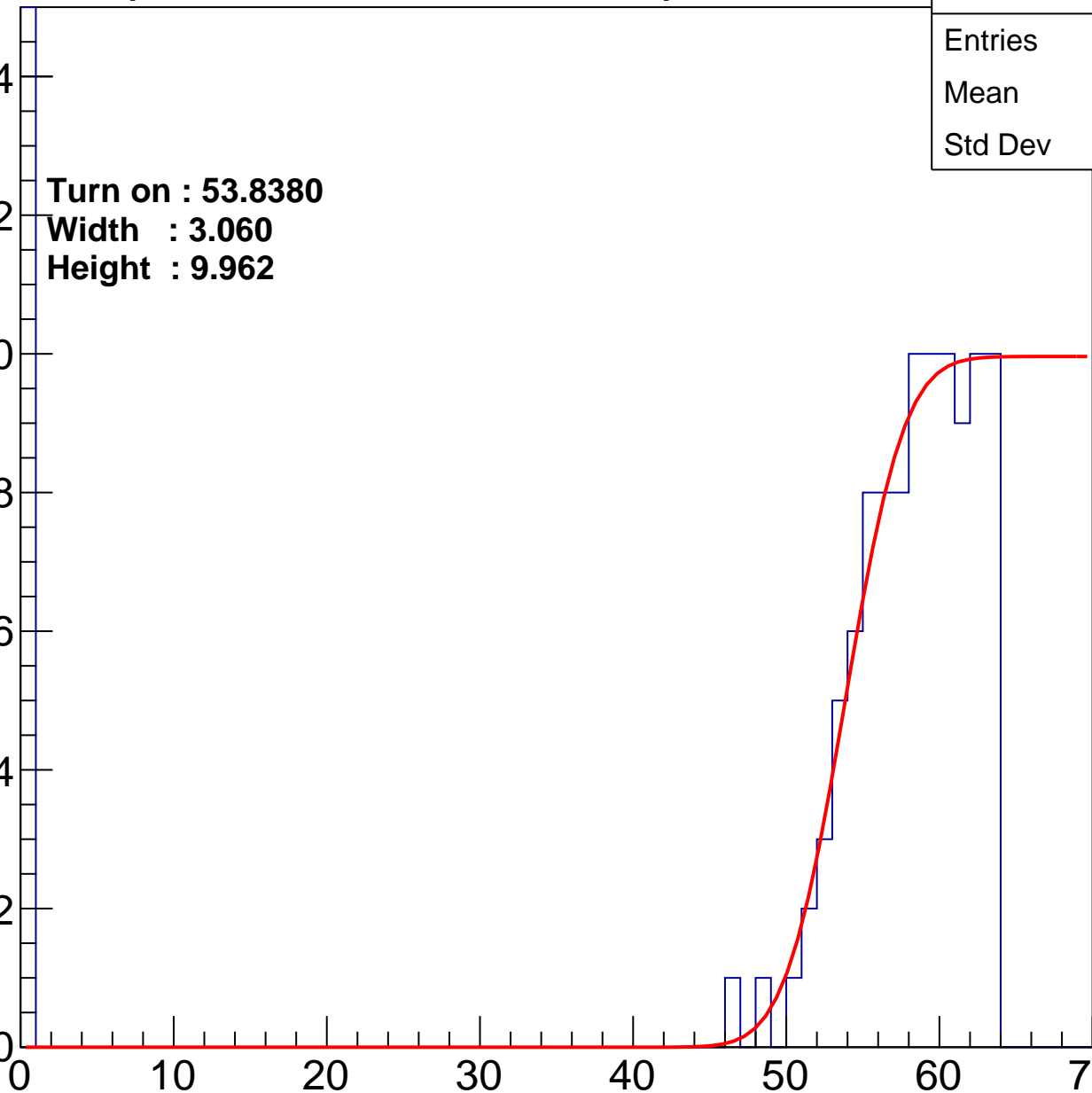
Width : 3.060

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch57

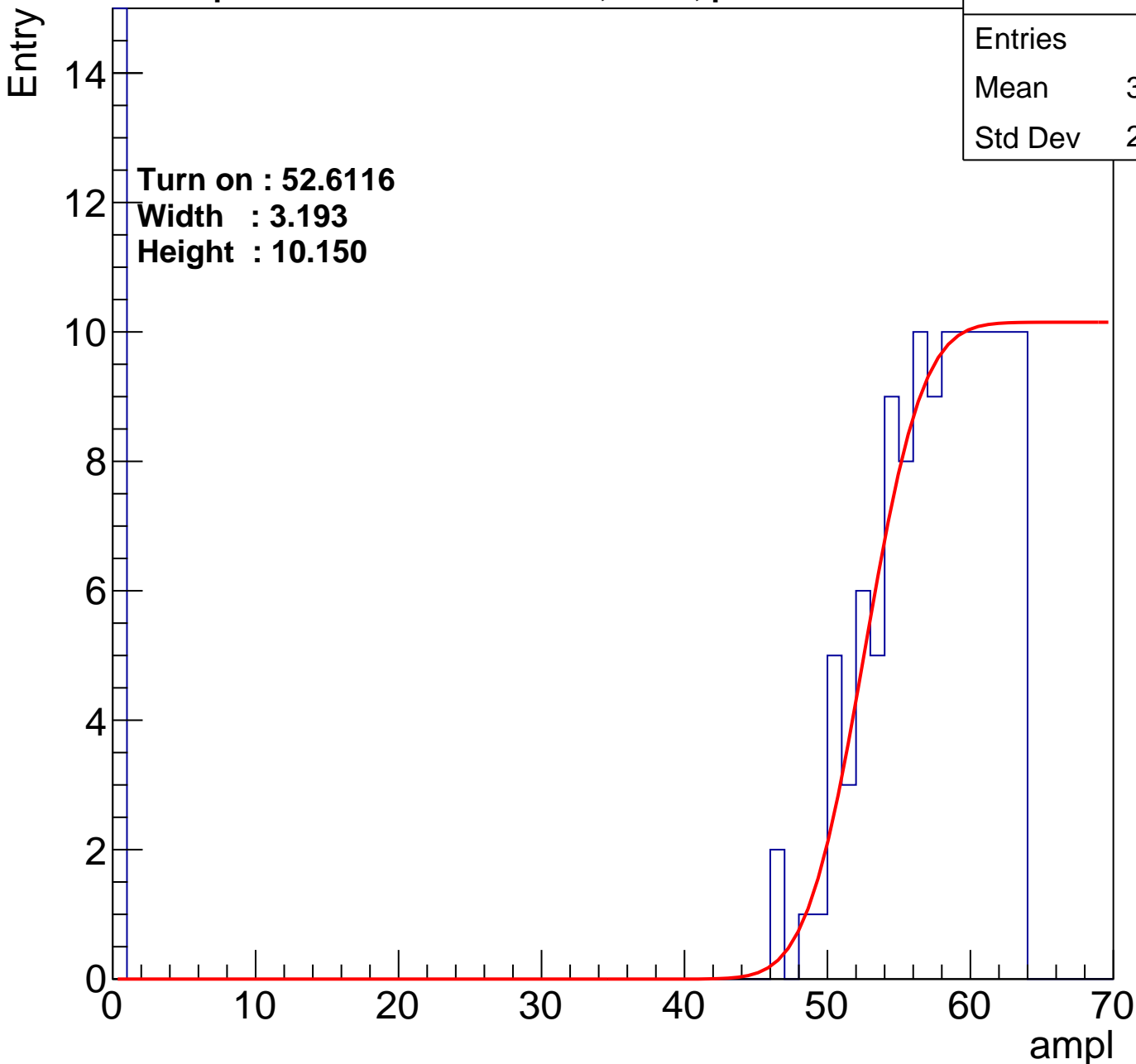
calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	33.82
Std Dev	28.25

Turn on : 52.6116

Width : 3.193

Height : 10.150



B1L104S, U15-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.15
Std Dev	28.71

Turn on : 55.2529

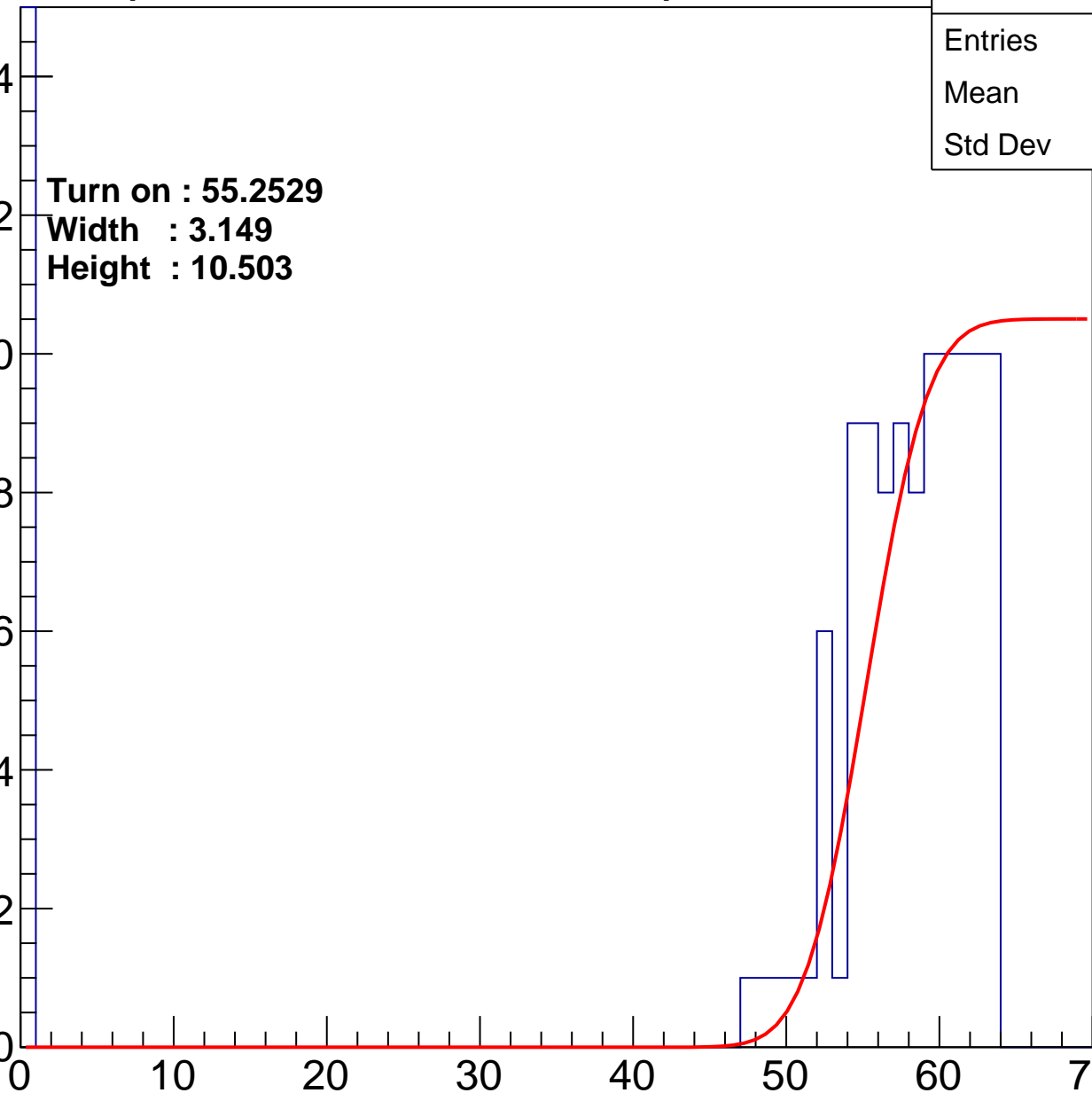
Width : 3.149

Height : 10.503

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	221
Mean	28.66
Std Dev	28.91

Turn on : 53.0663

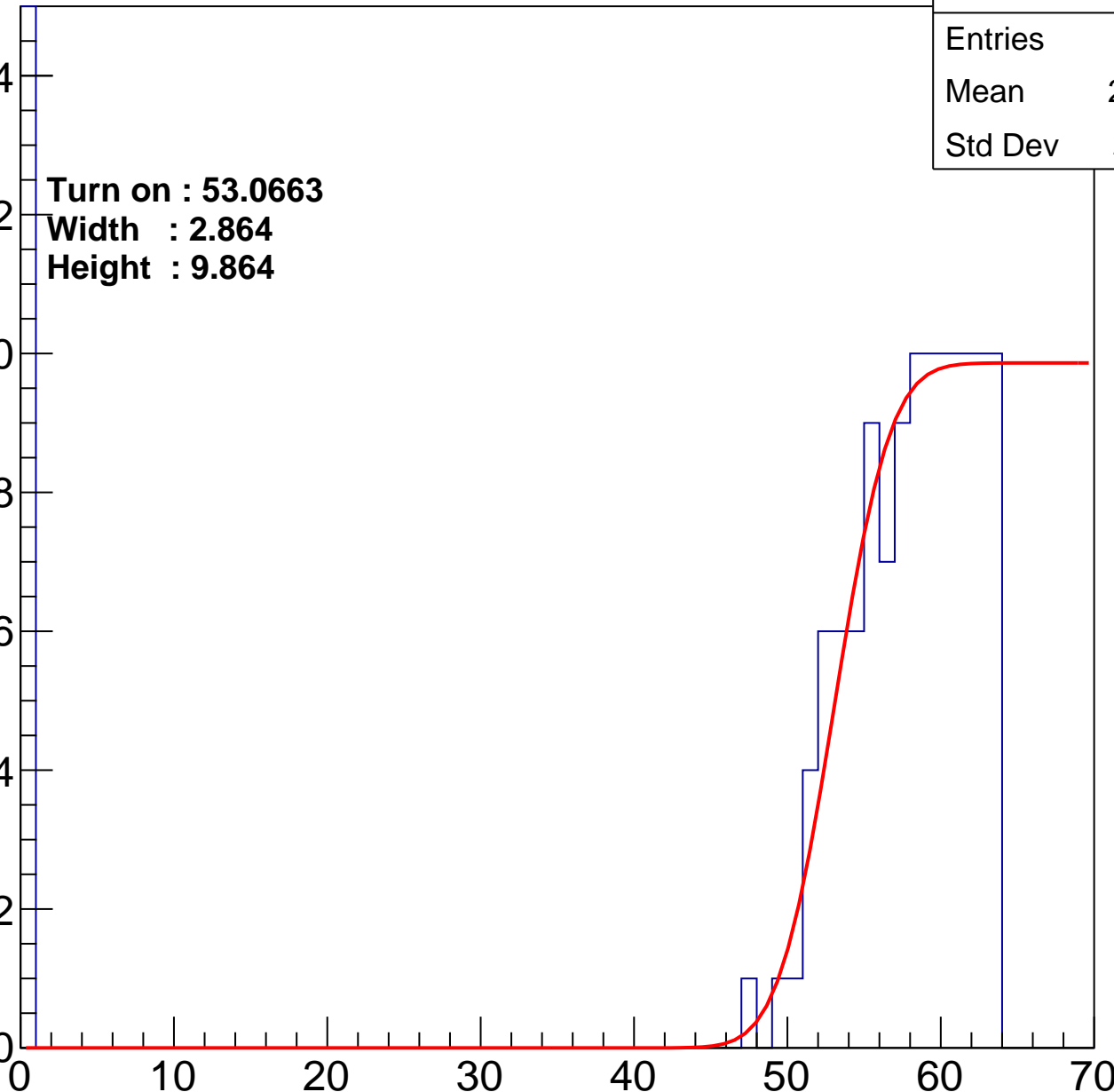
Width : 2.864

Height : 9.864

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	36.46
Std Dev	27.38

Turn on : 49.5005

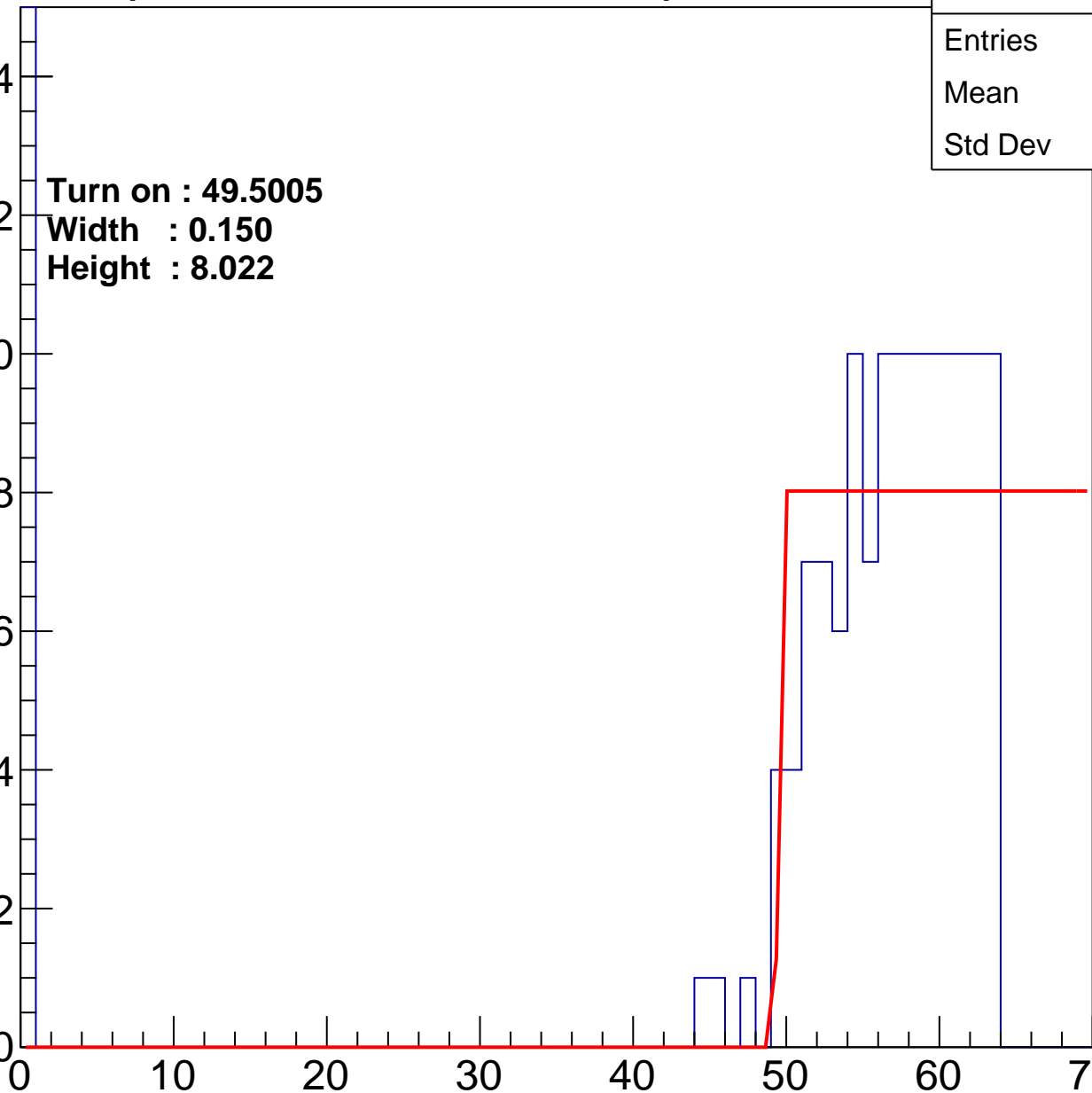
Width : 0.150

Height : 8.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	39.28
Std Dev	27.15

Turn on : 53.6389

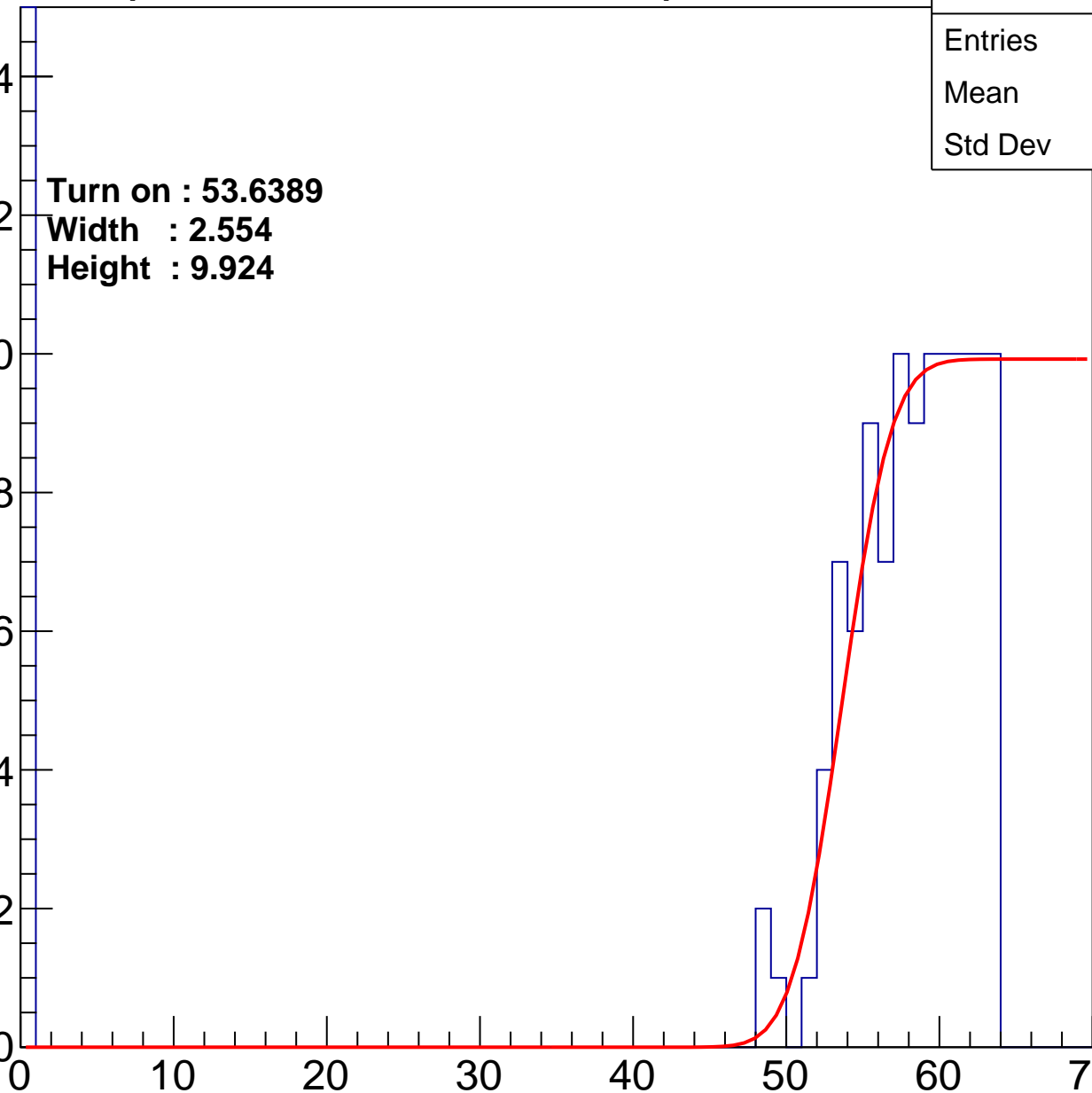
Width : 2.554

Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	35.27
Std Dev	27.89

Turn on : 52.8767

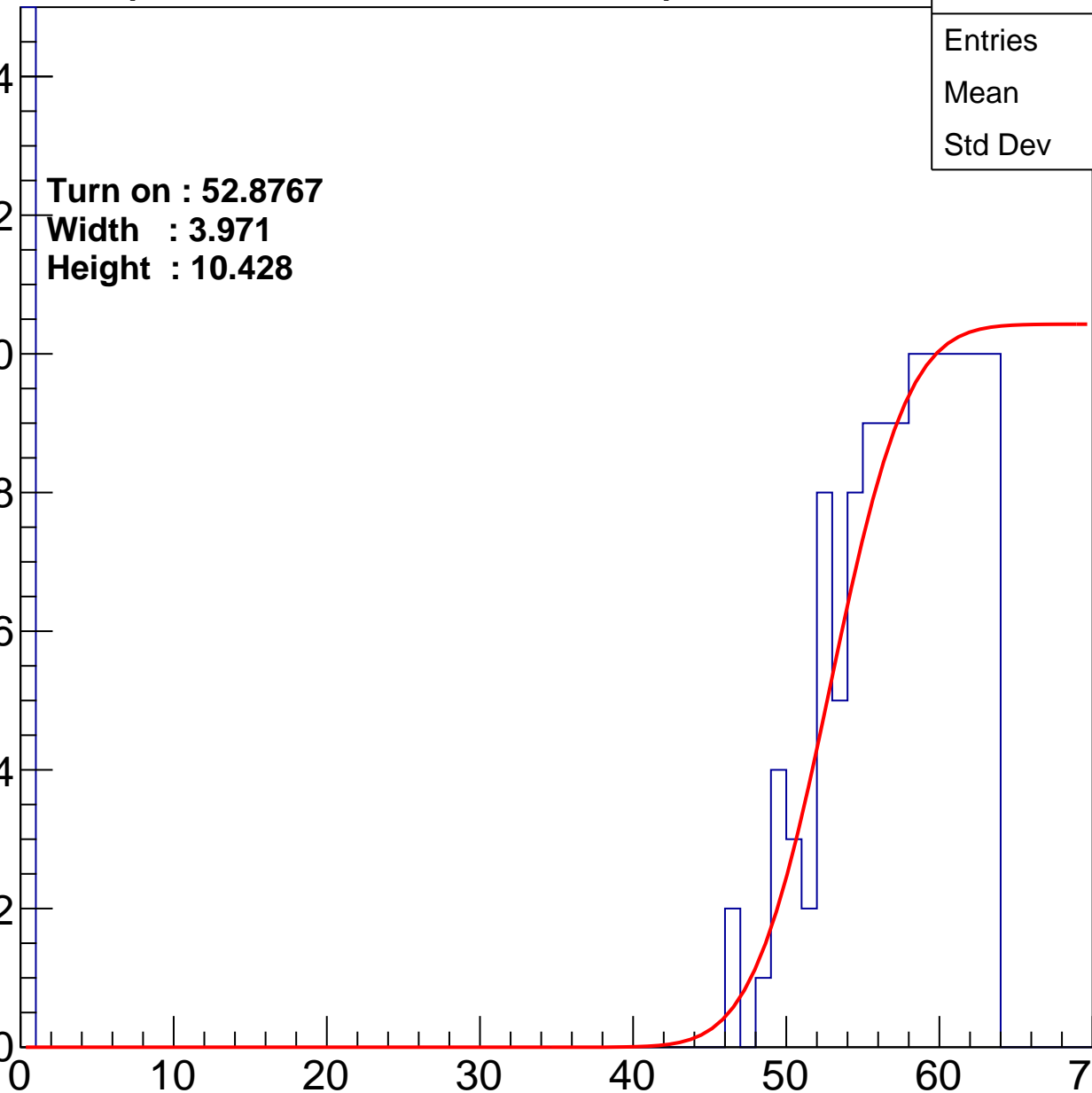
Width : 3.971

Height : 10.428

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	35.06
Std Dev	28.11

Turn on : 52.5454

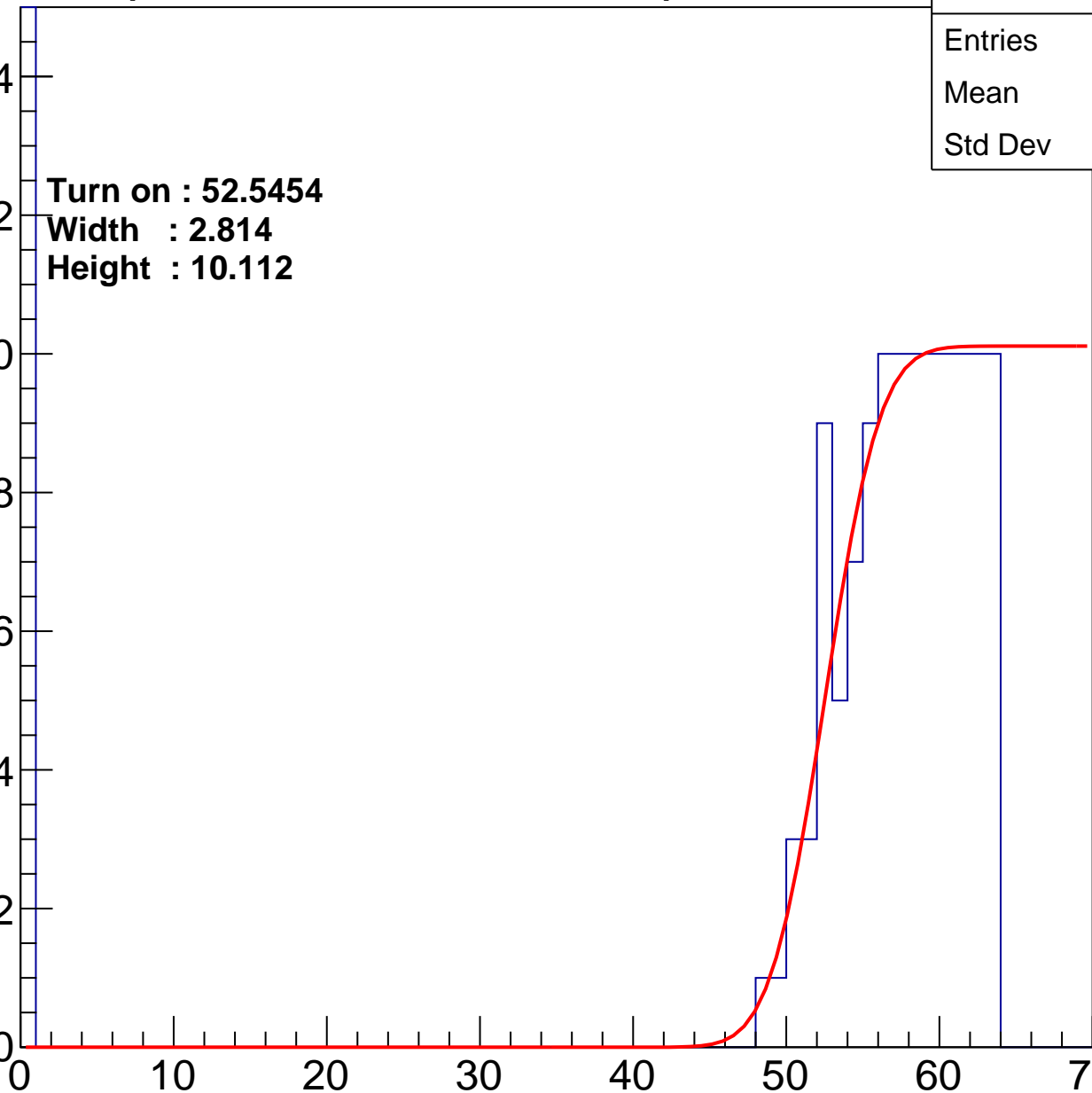
Width : 2.814

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	34.73
Std Dev	27.87

Turn on : 51.6008

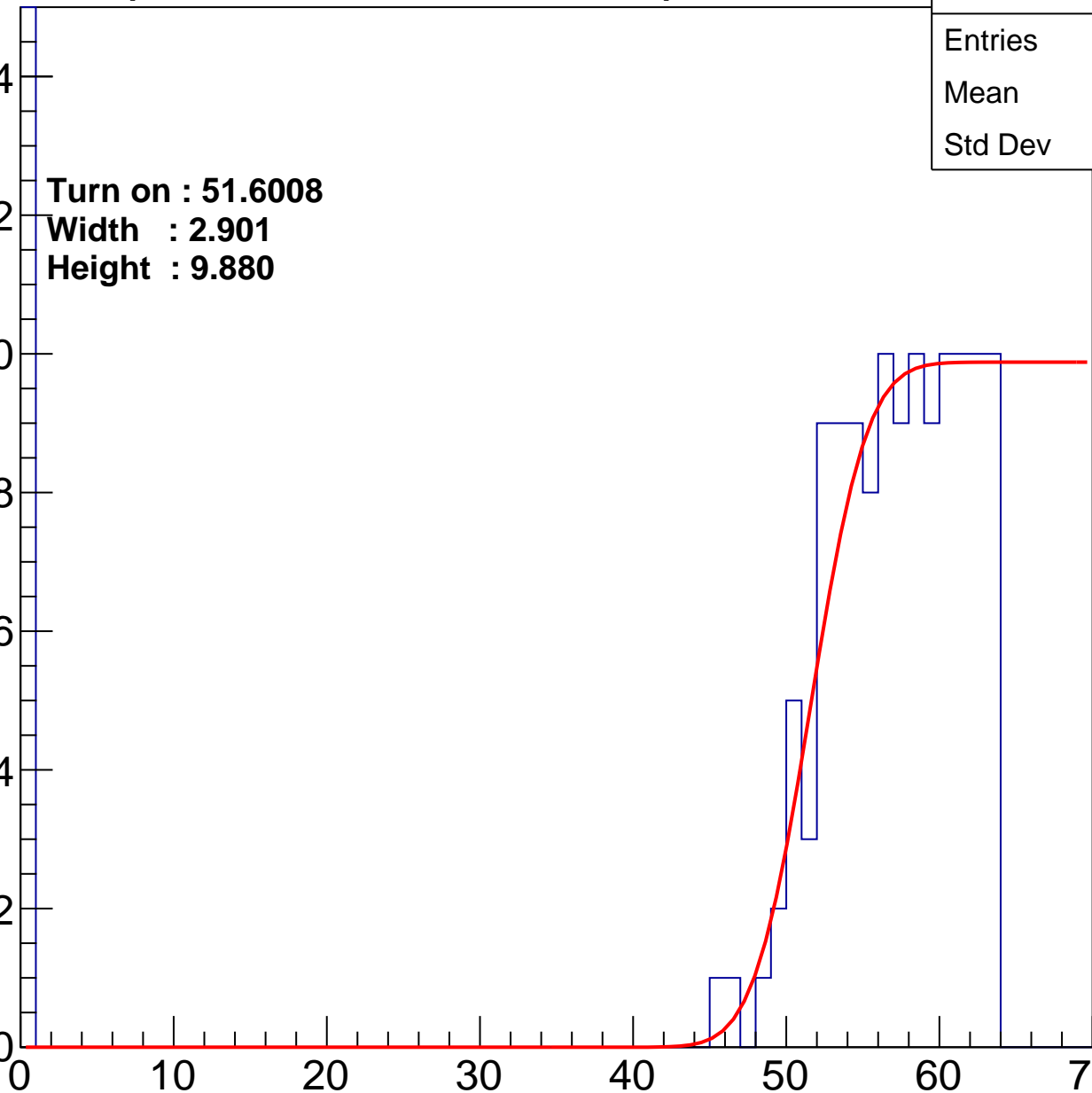
Width : 2.901

Height : 9.880

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	32.73
Std Dev	28.8

Turn on : 54.1849

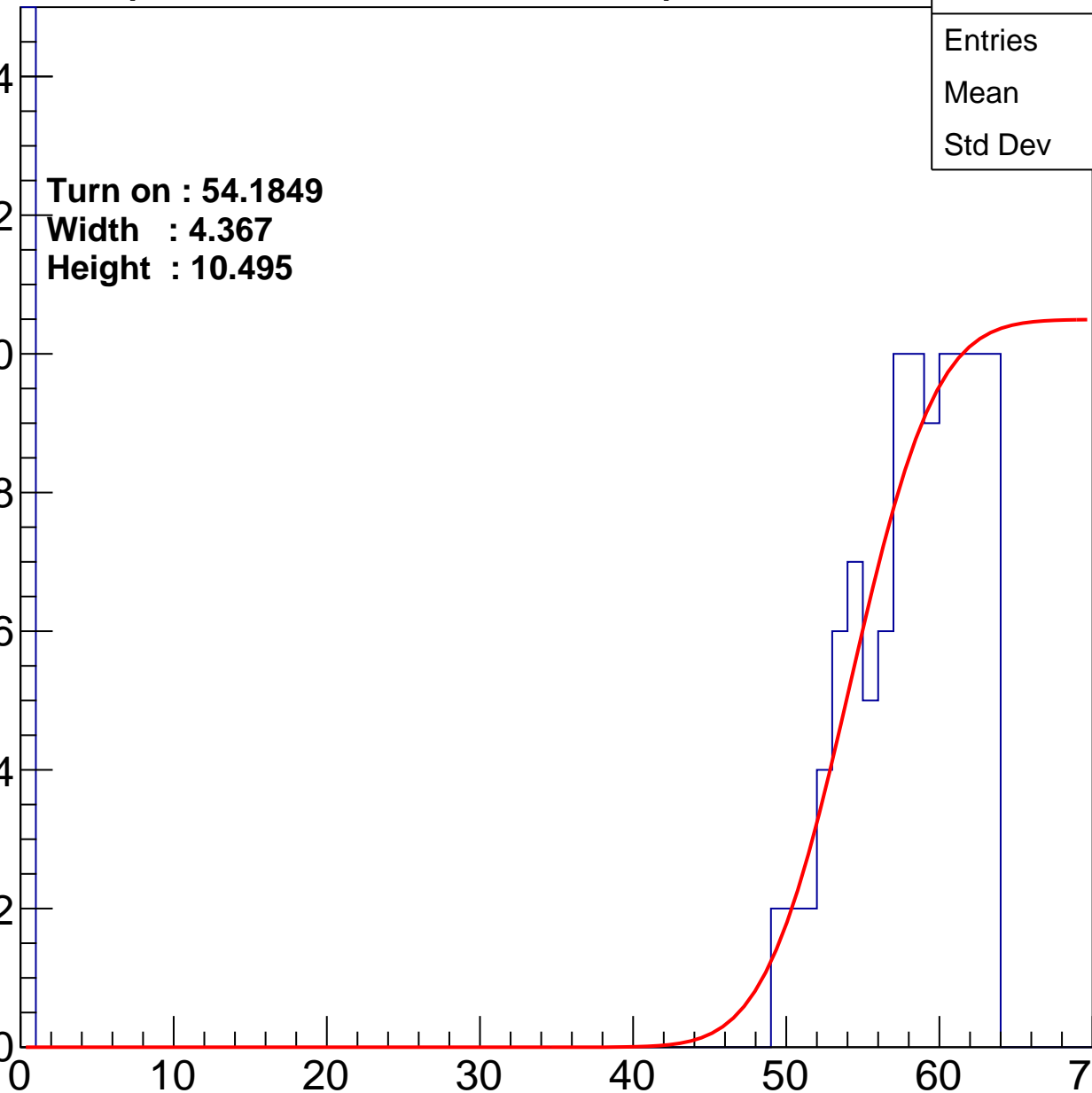
Width : 4.367

Height : 10.495

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.28
Std Dev	28.43

Turn on : 52.6360

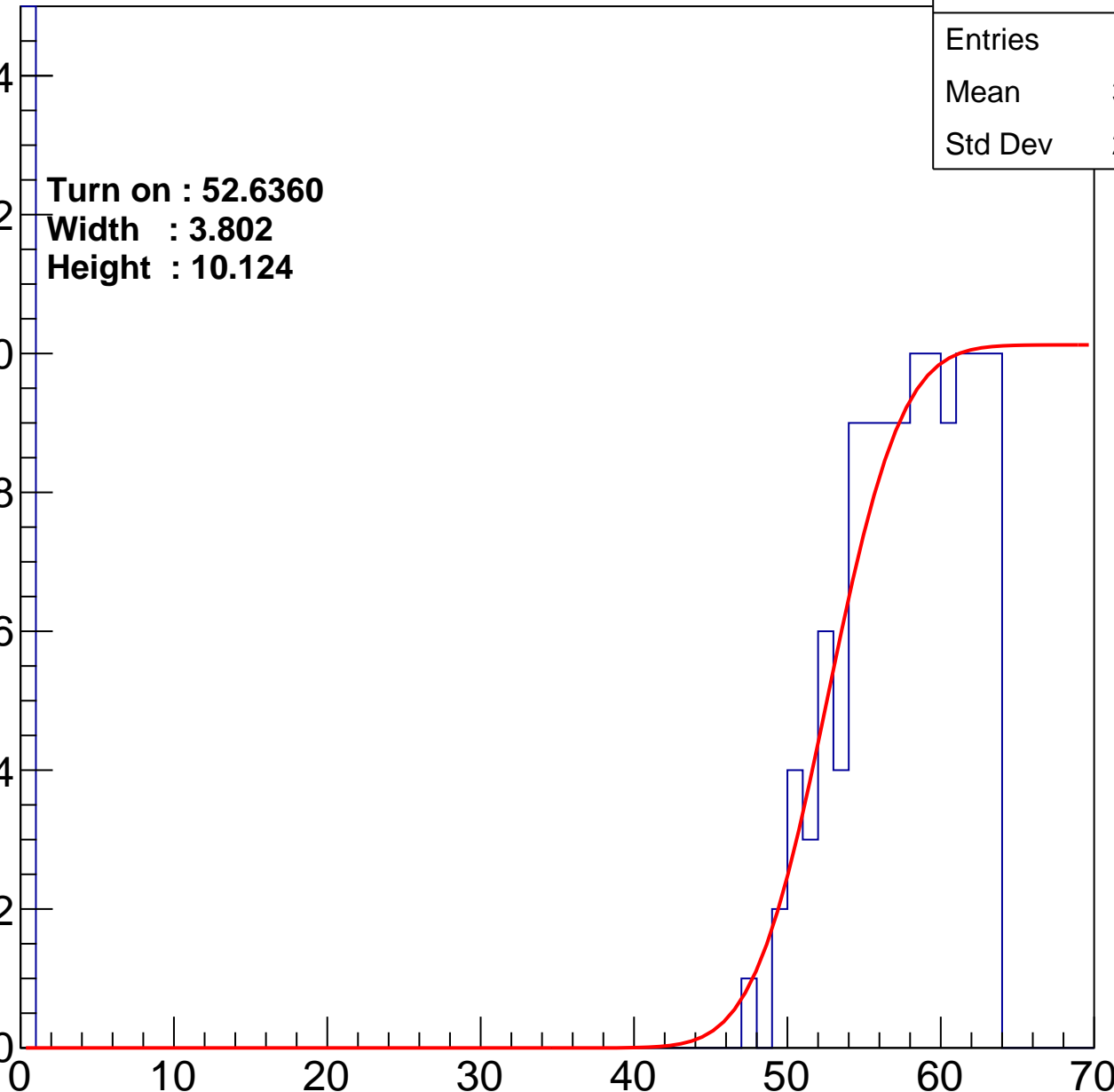
Width : 3.802

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	37.92
Std Dev	27.59

Turn on : 52.9273

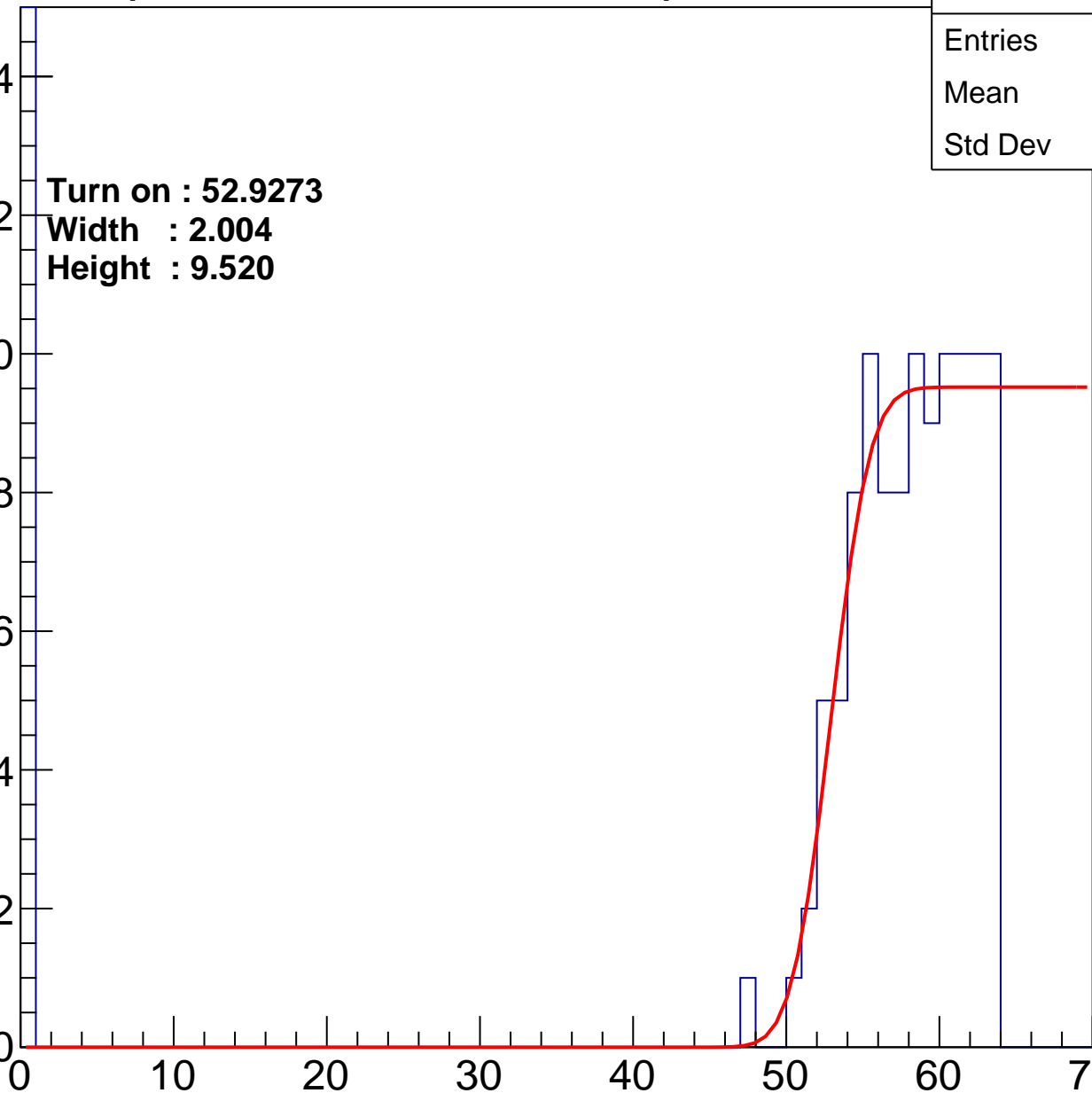
Width : 2.004

Height : 9.520

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	33.42
Std Dev	28.28

Turn on : 51.7258

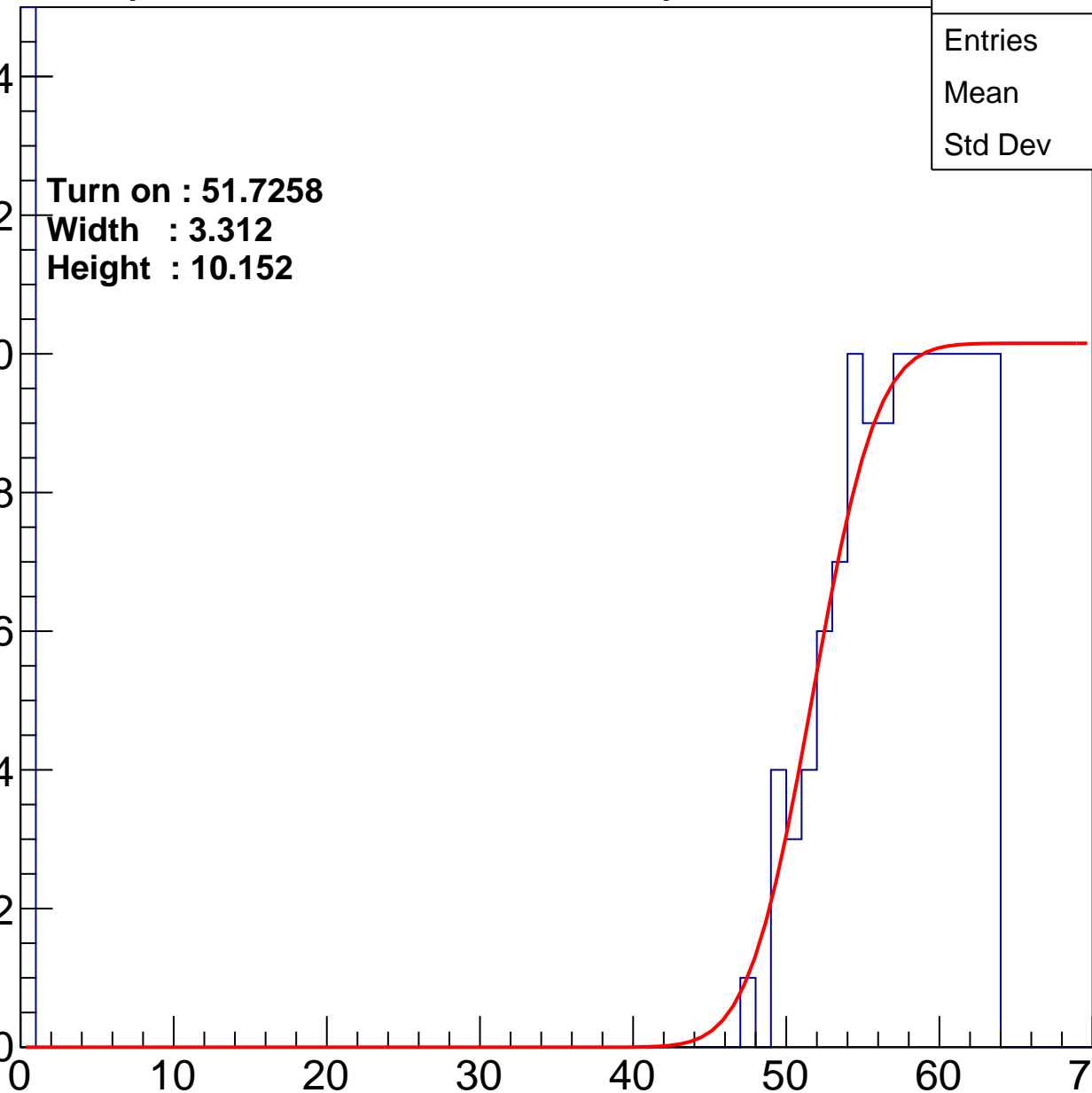
Width : 3.312

Height : 10.152

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	35.08
Std Dev	27.85

Turn on : 51.0178

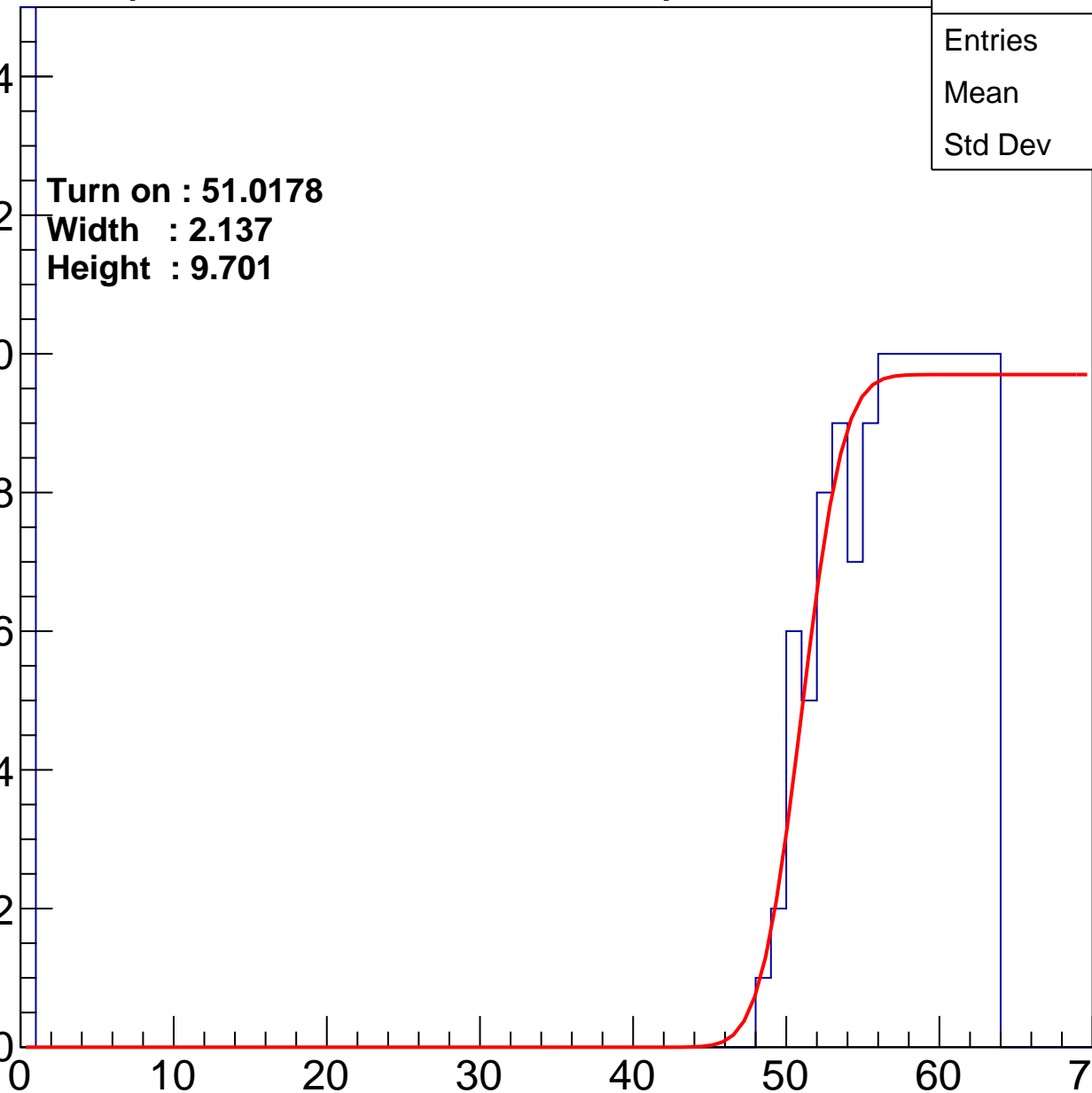
Width : 2.137

Height : 9.701

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	34.18
Std Dev	28.15

Turn on : 52.4254

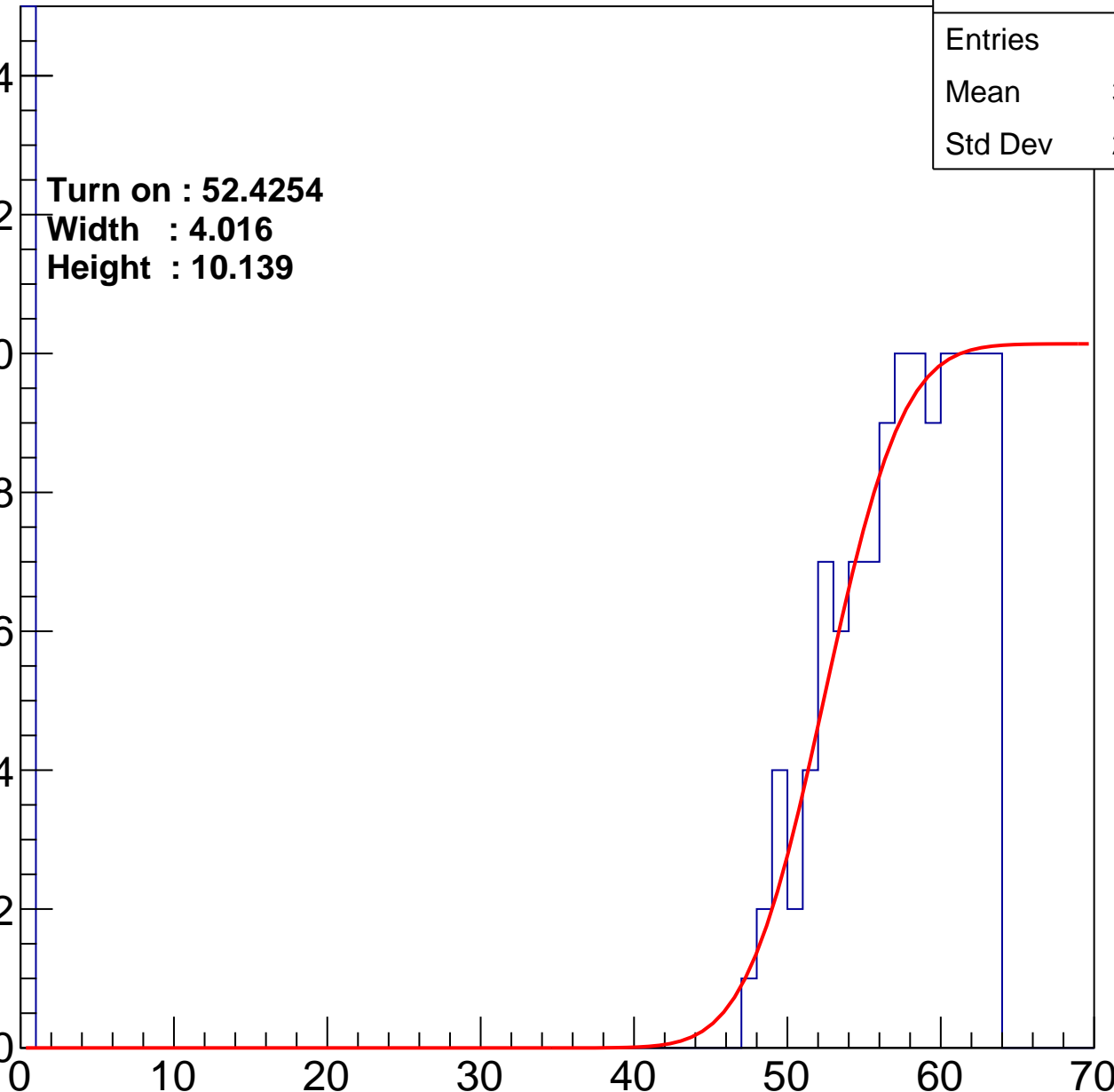
Width : 4.016

Height : 10.139

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch71

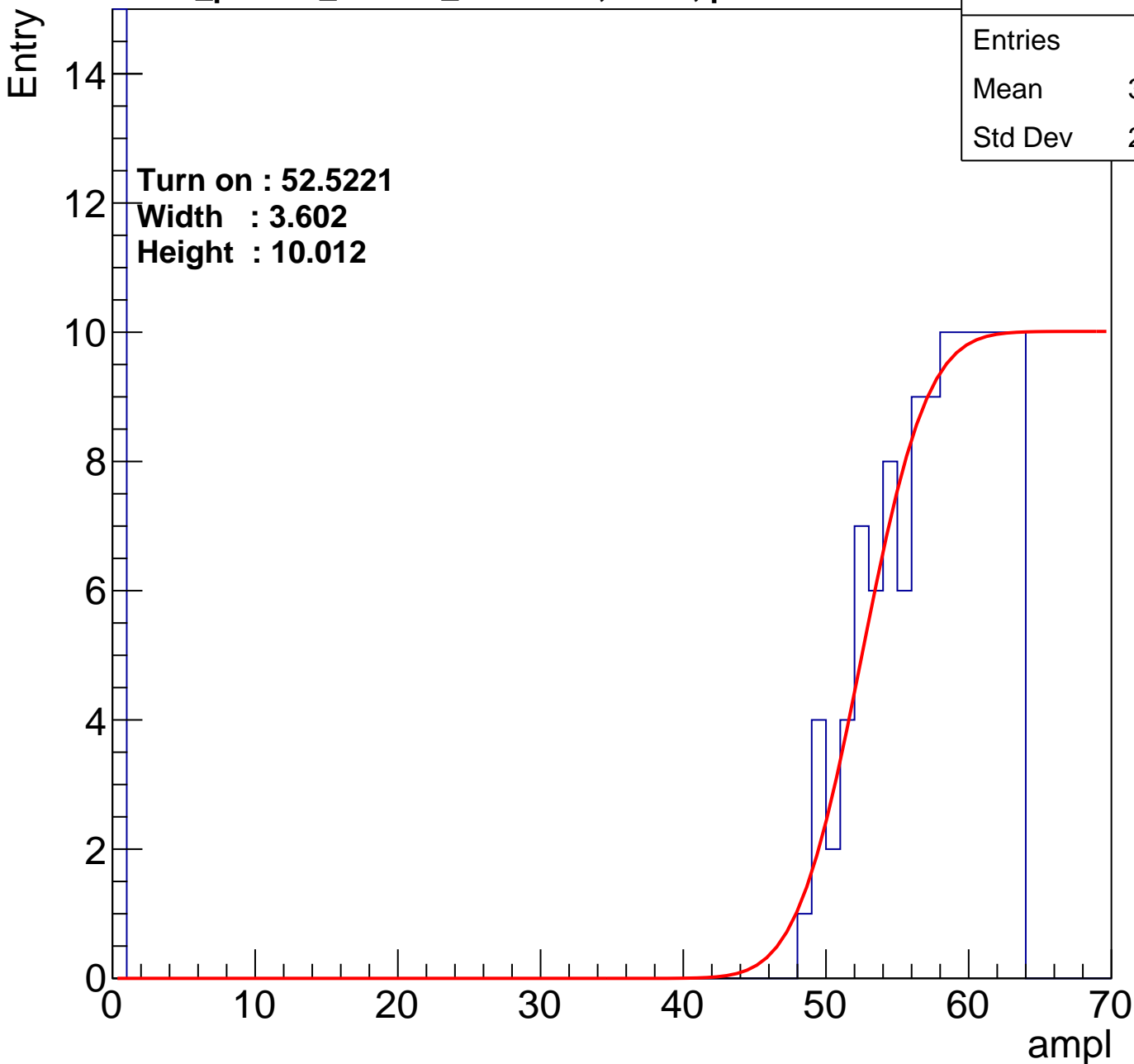
calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	34.58
Std Dev	28.16

Turn on : 52.5221

Width : 3.602

Height : 10.012



B1L104S, U15-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	32.92
Std Dev	28.75

Turn on : 54.3482

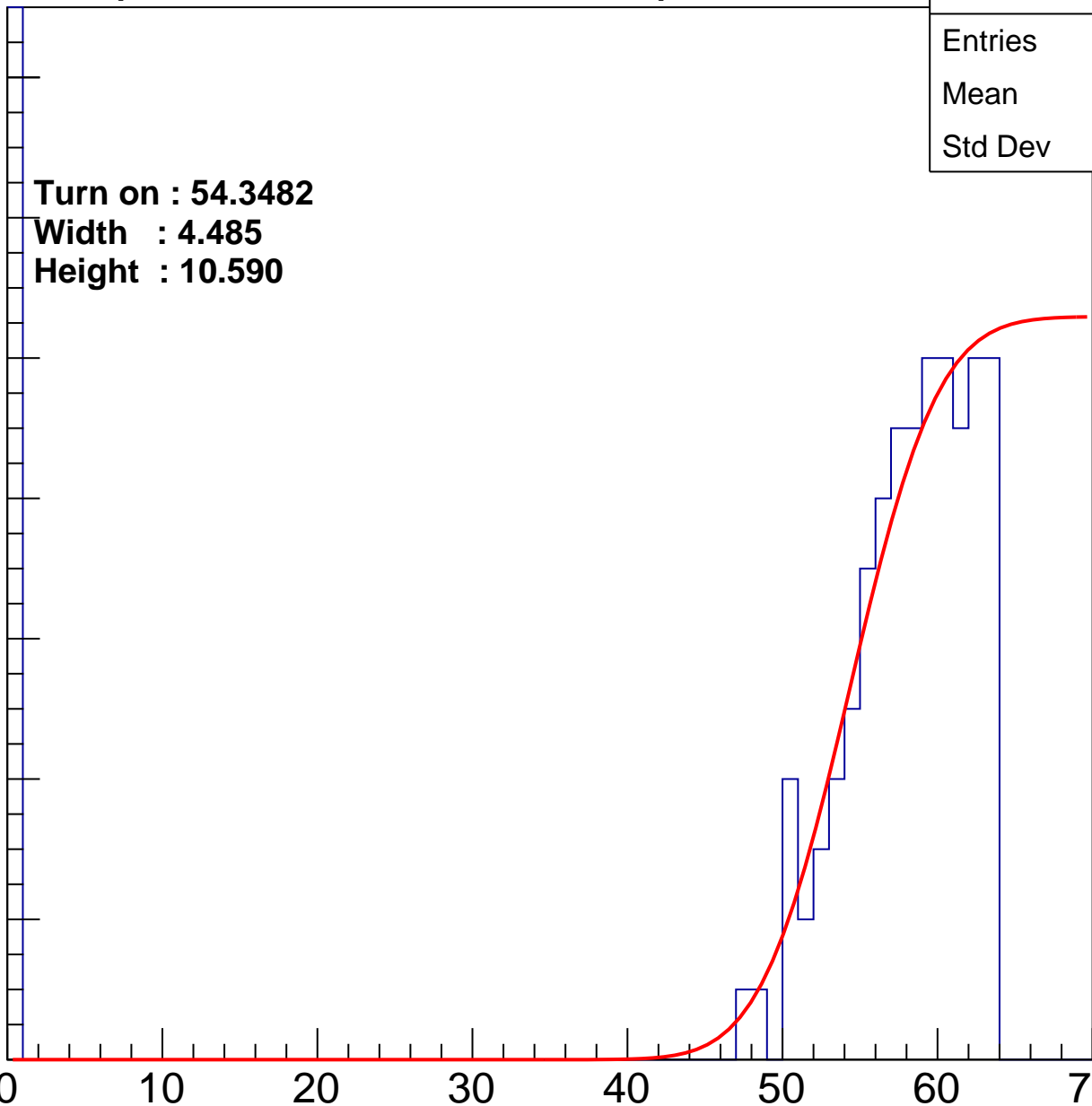
Width : 4.485

Height : 10.590

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.71
Std Dev	28.6

Turn on : 52.8872

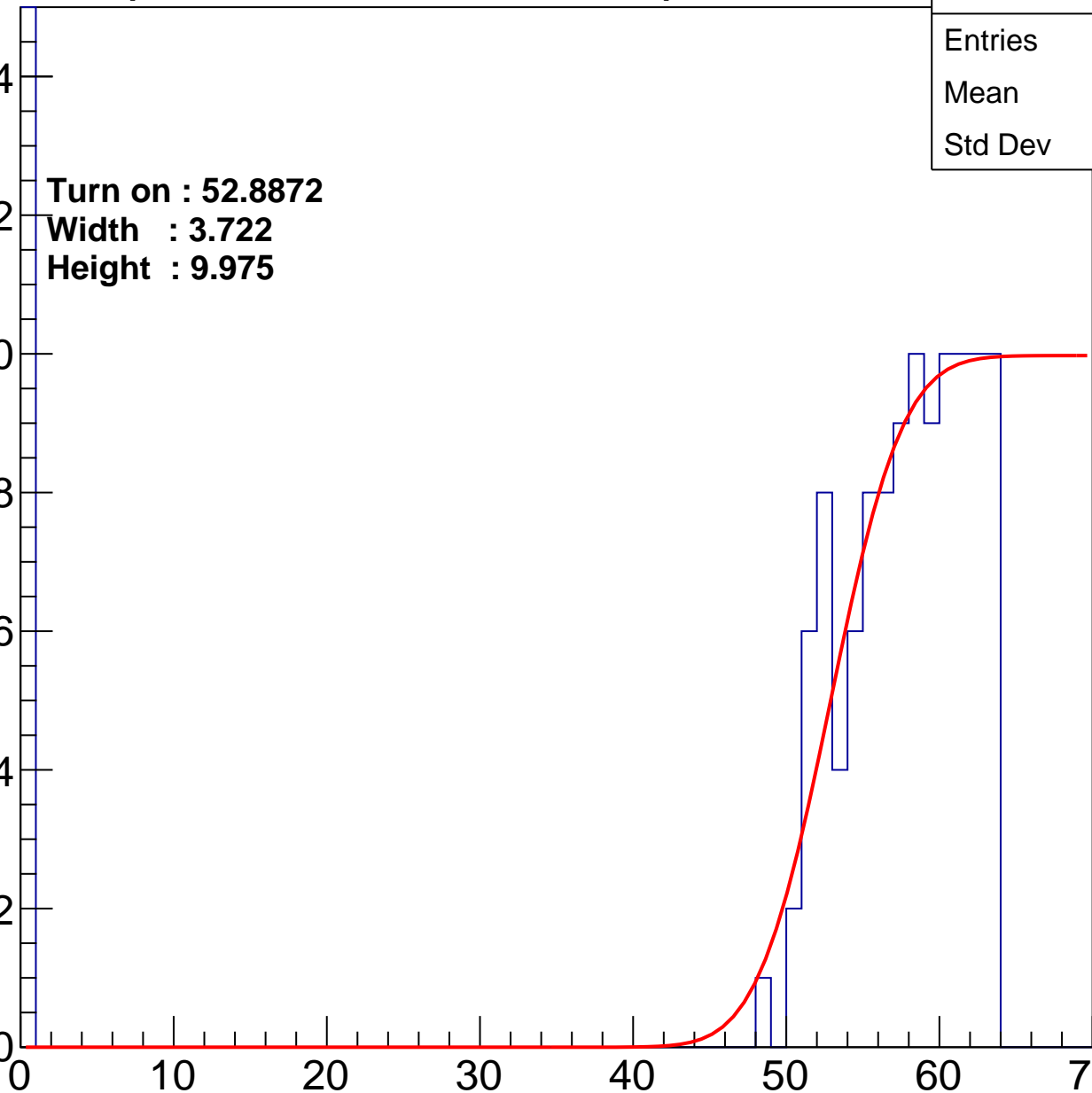
Width : 3.722

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch74

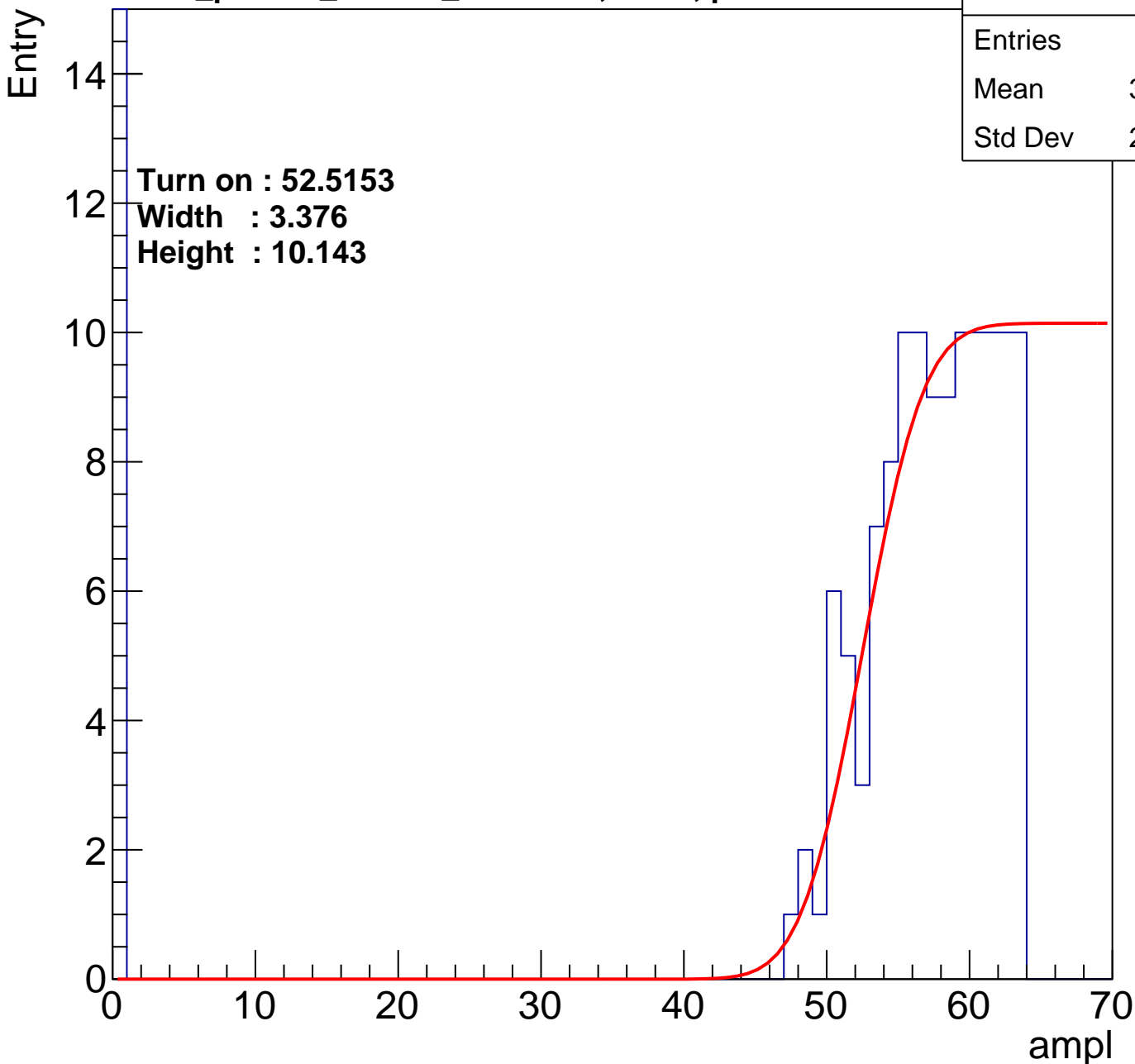
calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	33.02
Std Dev	28.33

Turn on : 52.5153

Width : 3.376

Height : 10.143



B1L104S, U15-ch75

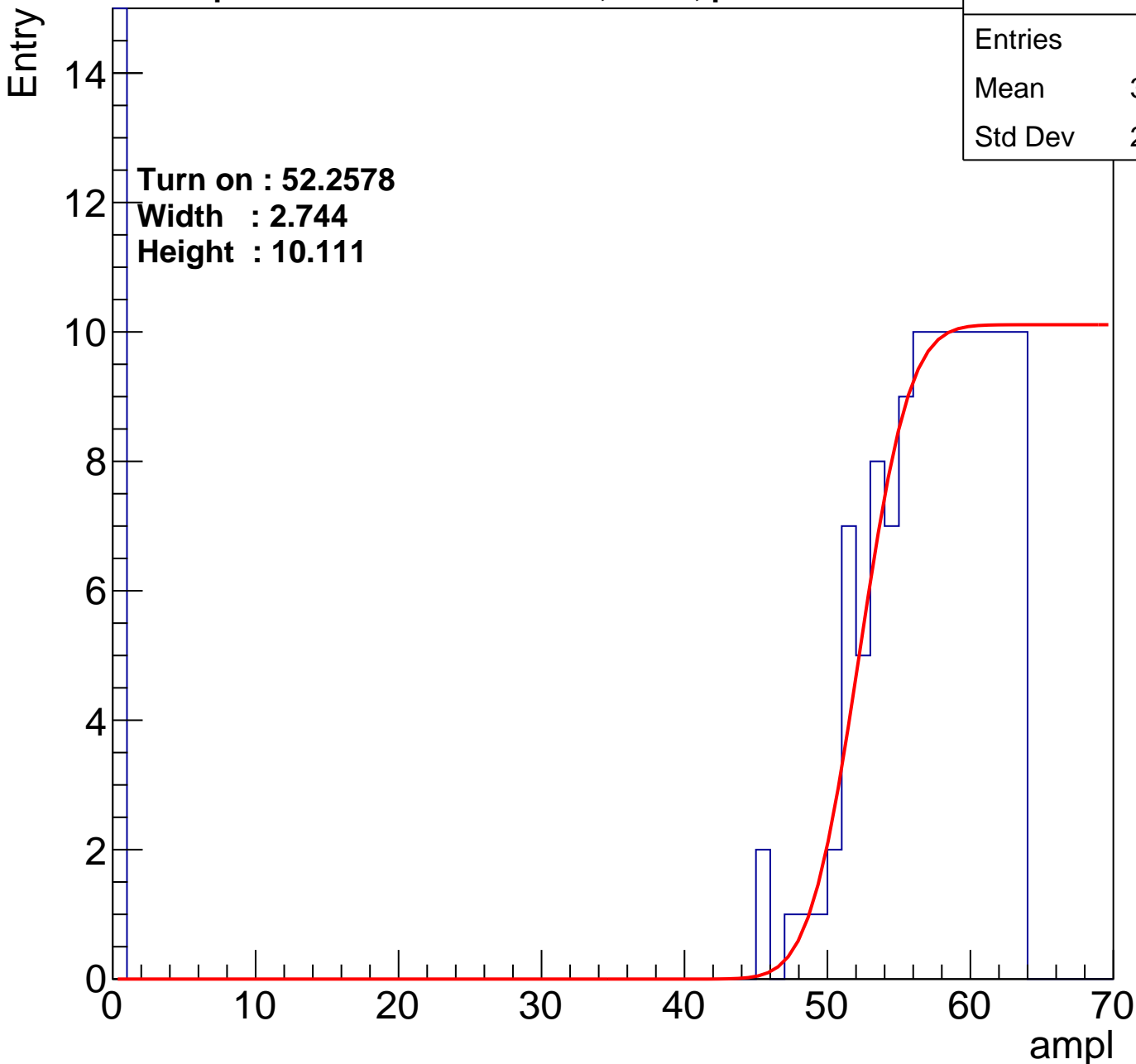
calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	35.57
Std Dev	27.79

Turn on : 52.2578

Width : 2.744

Height : 10.111



B1L104S, U15-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	35.32
Std Dev	27.95

Turn on : 51.9657

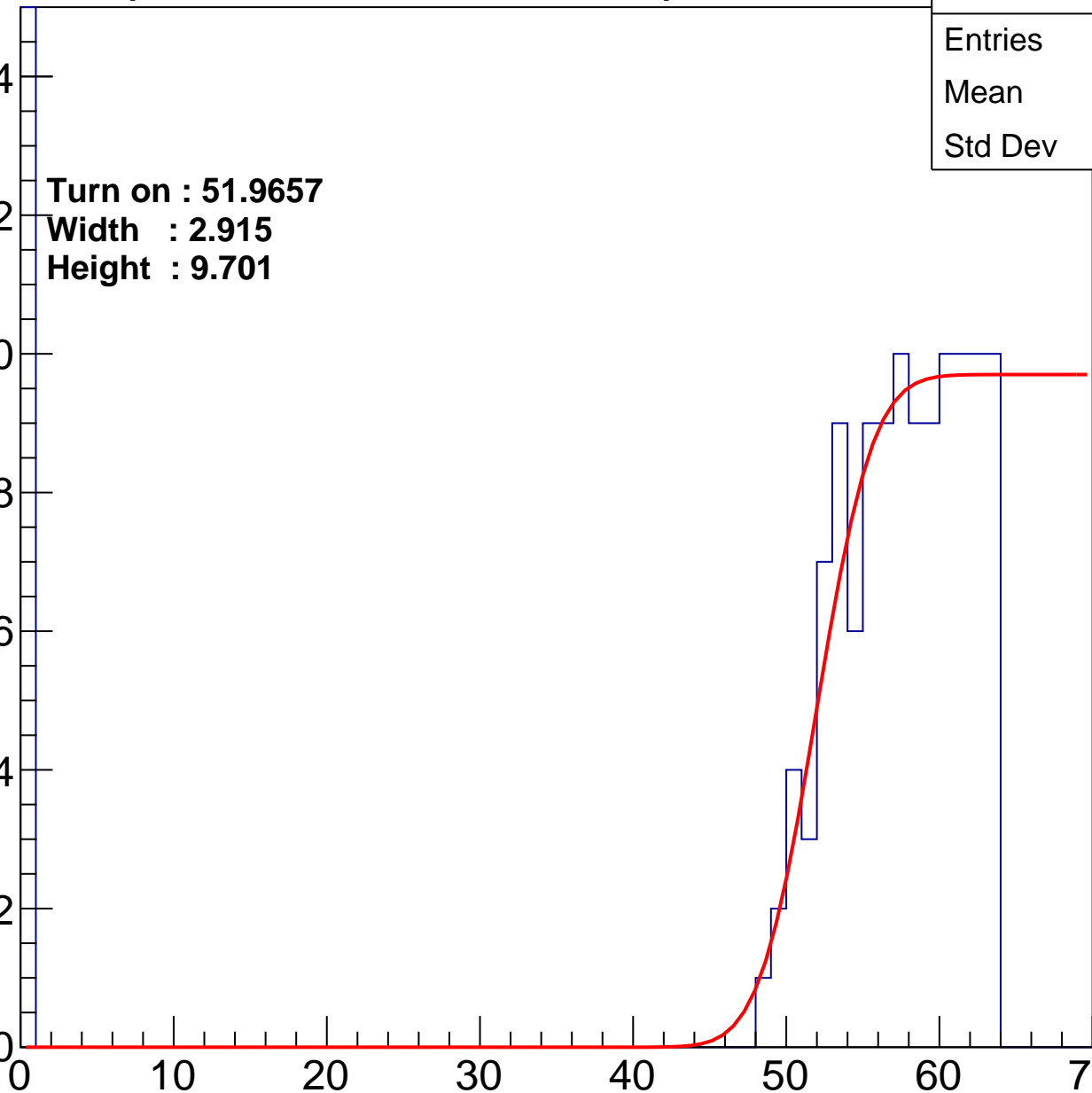
Width : 2.915

Height : 9.701

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch77

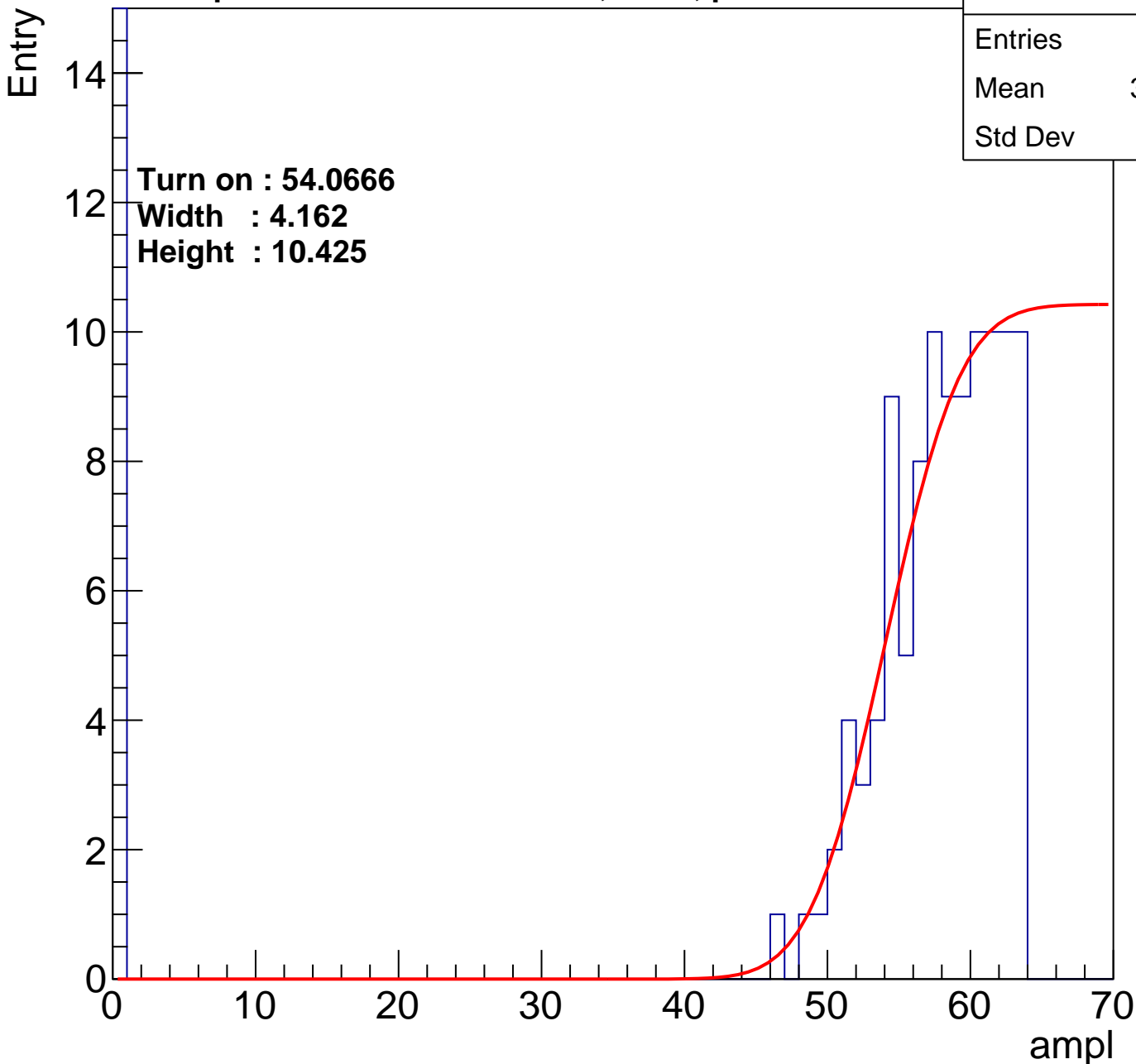
calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	30.54
Std Dev	28.9

Turn on : 54.0666

Width : 4.162

Height : 10.425



B1L104S, U15-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	35.57
Std Dev	28.02

Turn on : 54.3622

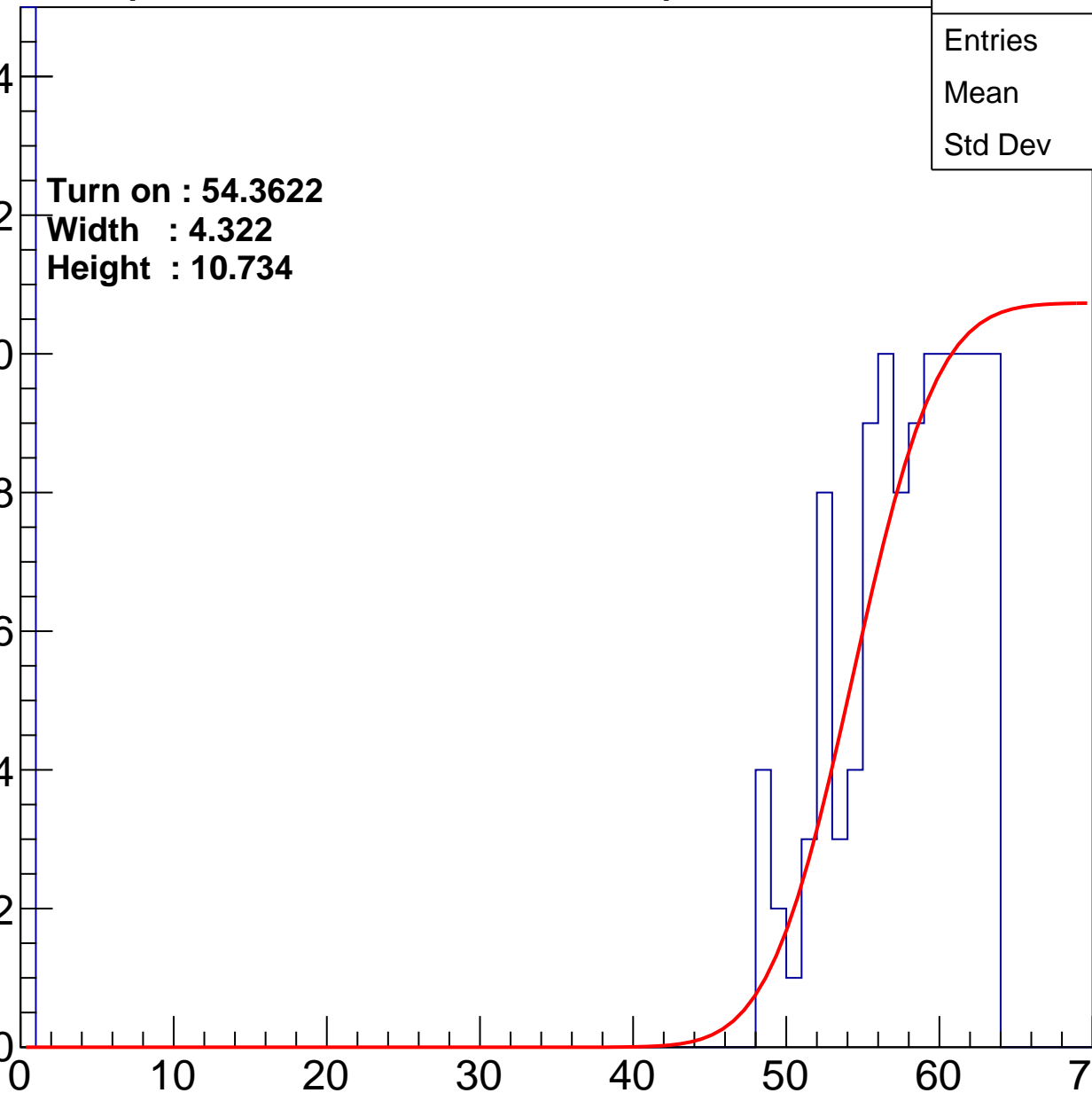
Width : 4.322

Height : 10.734

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch79

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	37.8
Std Dev	27.27

Turn on : 53.2184

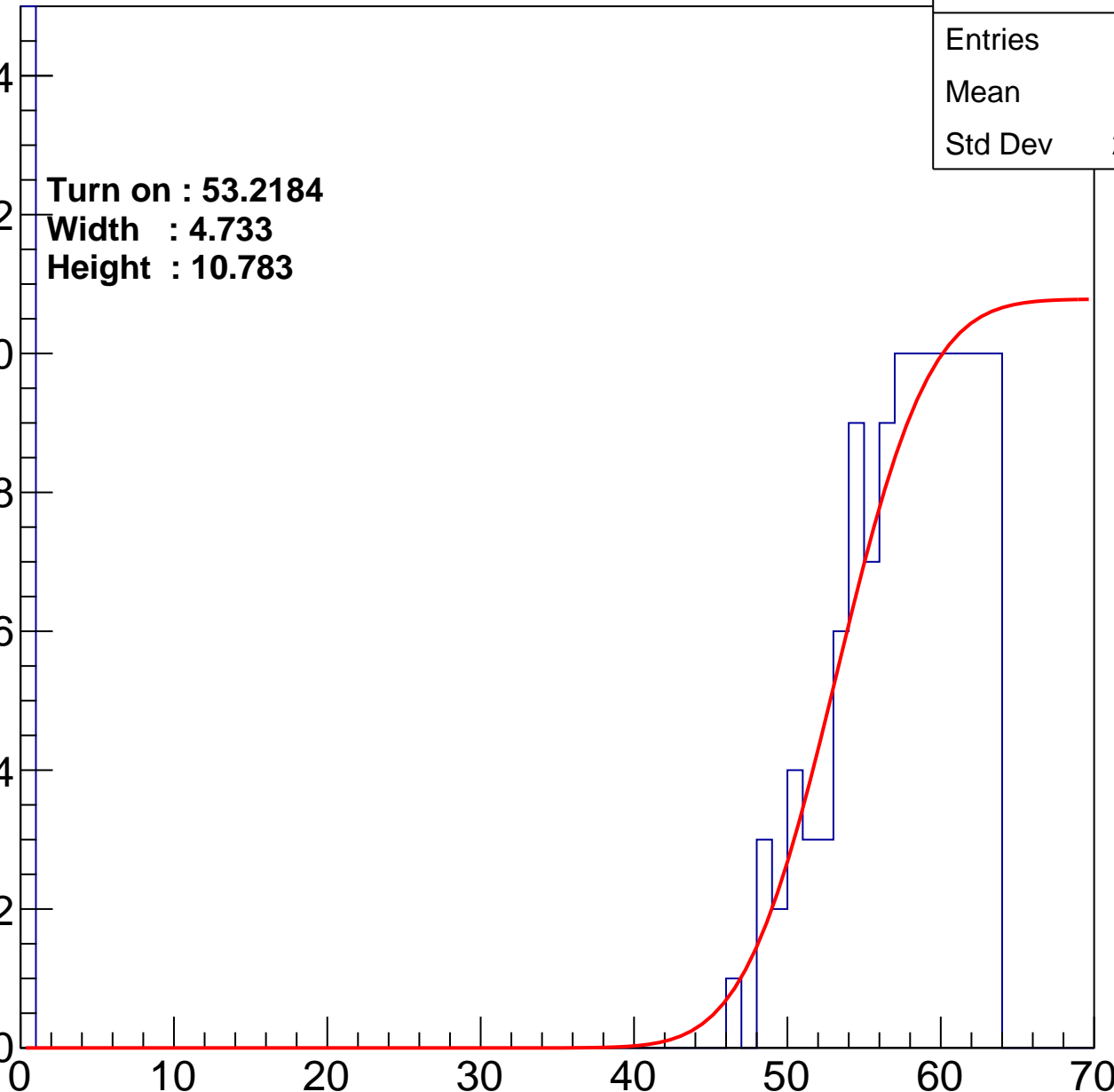
Width : 4.733

Height : 10.783

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch80

calib_packv5_033123_0516.root, FC#4, port A1

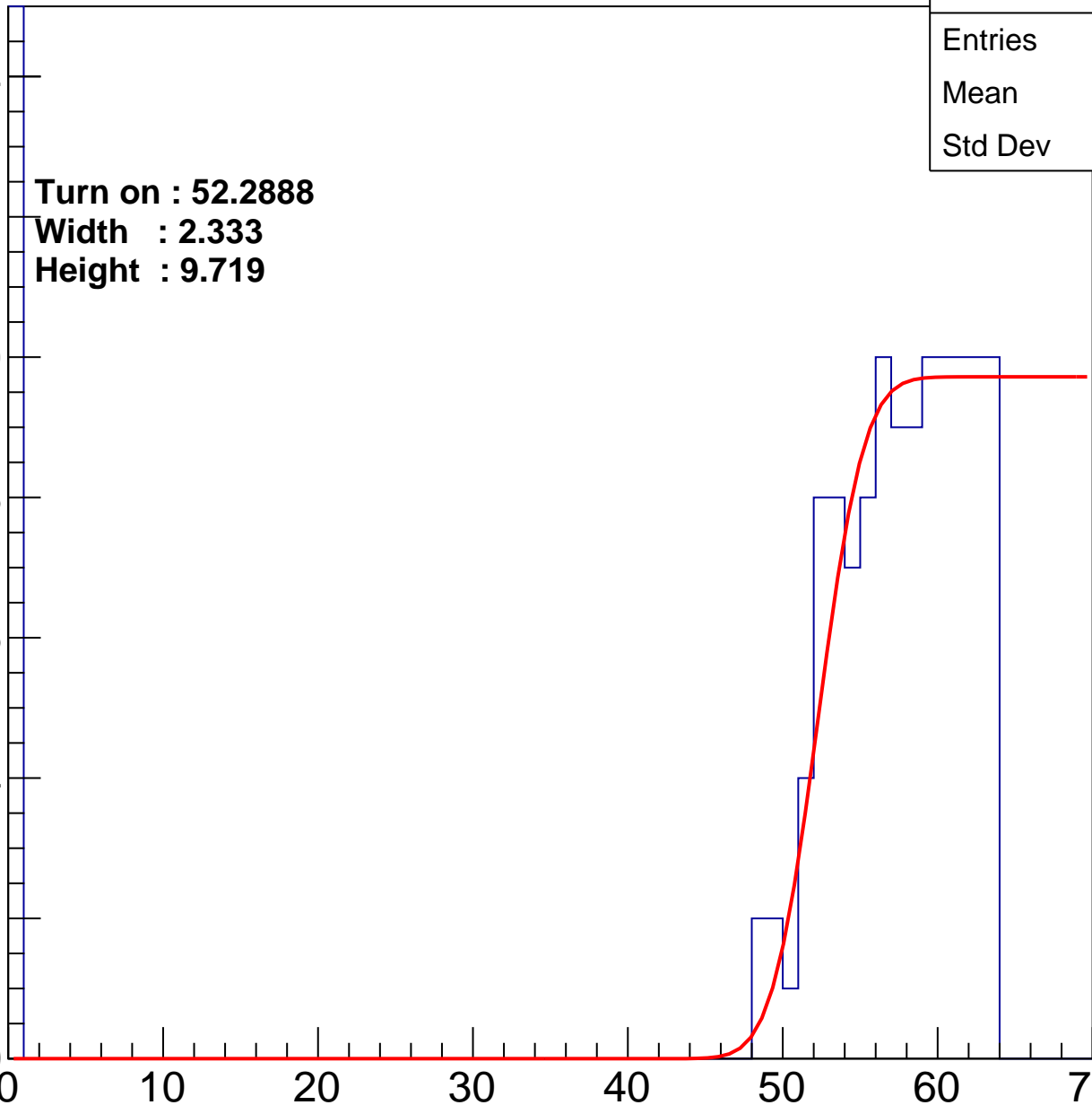
Entry

14
12
10
8
6
4
2
0

Turn on : 52.2888
Width : 2.333
Height : 9.719

Entries	199
Mean	33.92
Std Dev	28.27

ampl



B1L104S, U15-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	32.37
Std Dev	28.78

Turn on : 53.5900

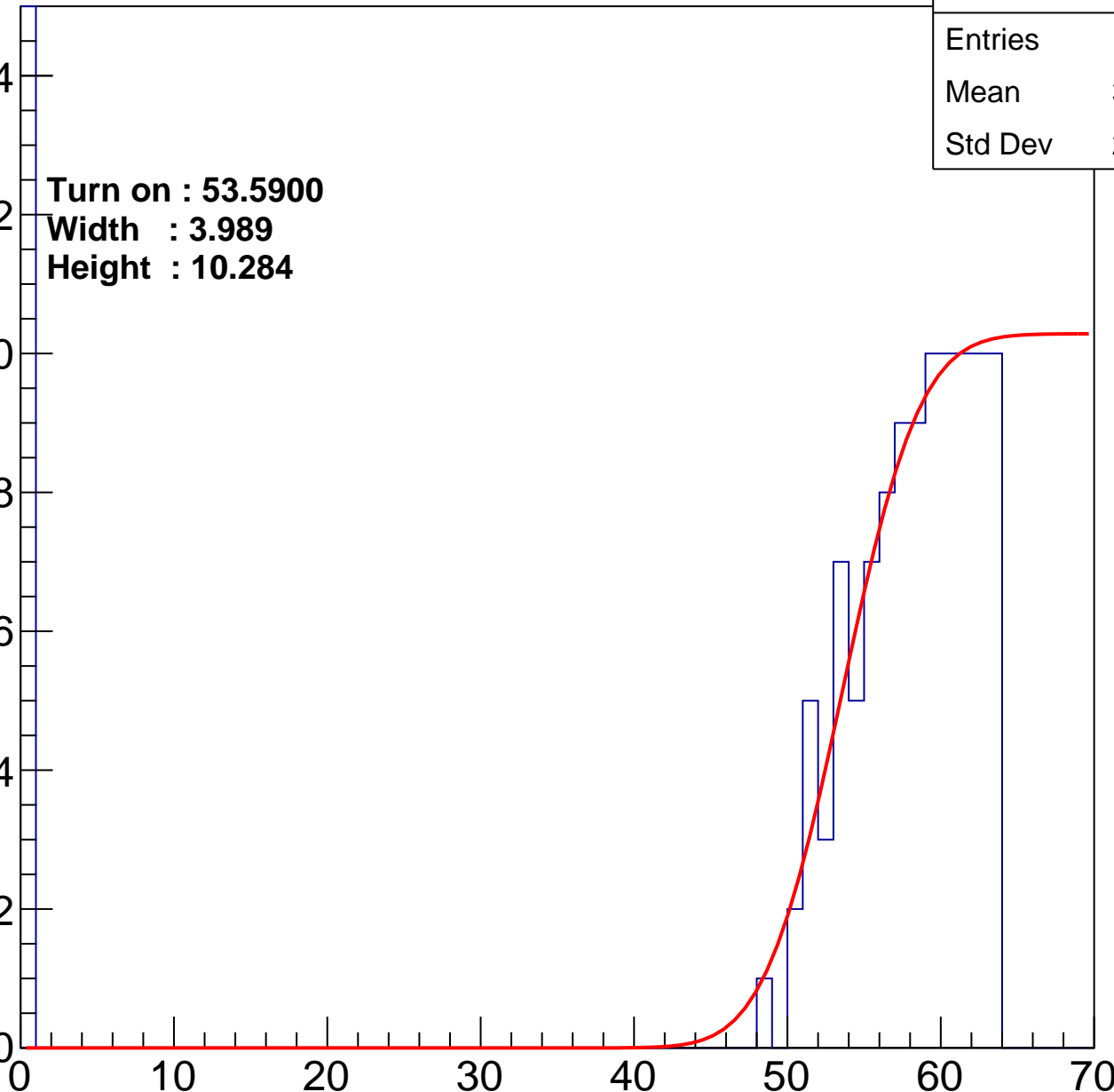
Width : 3.989

Height : 10.284

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	33.23
Std Dev	28.51

Turn on : 52.5375

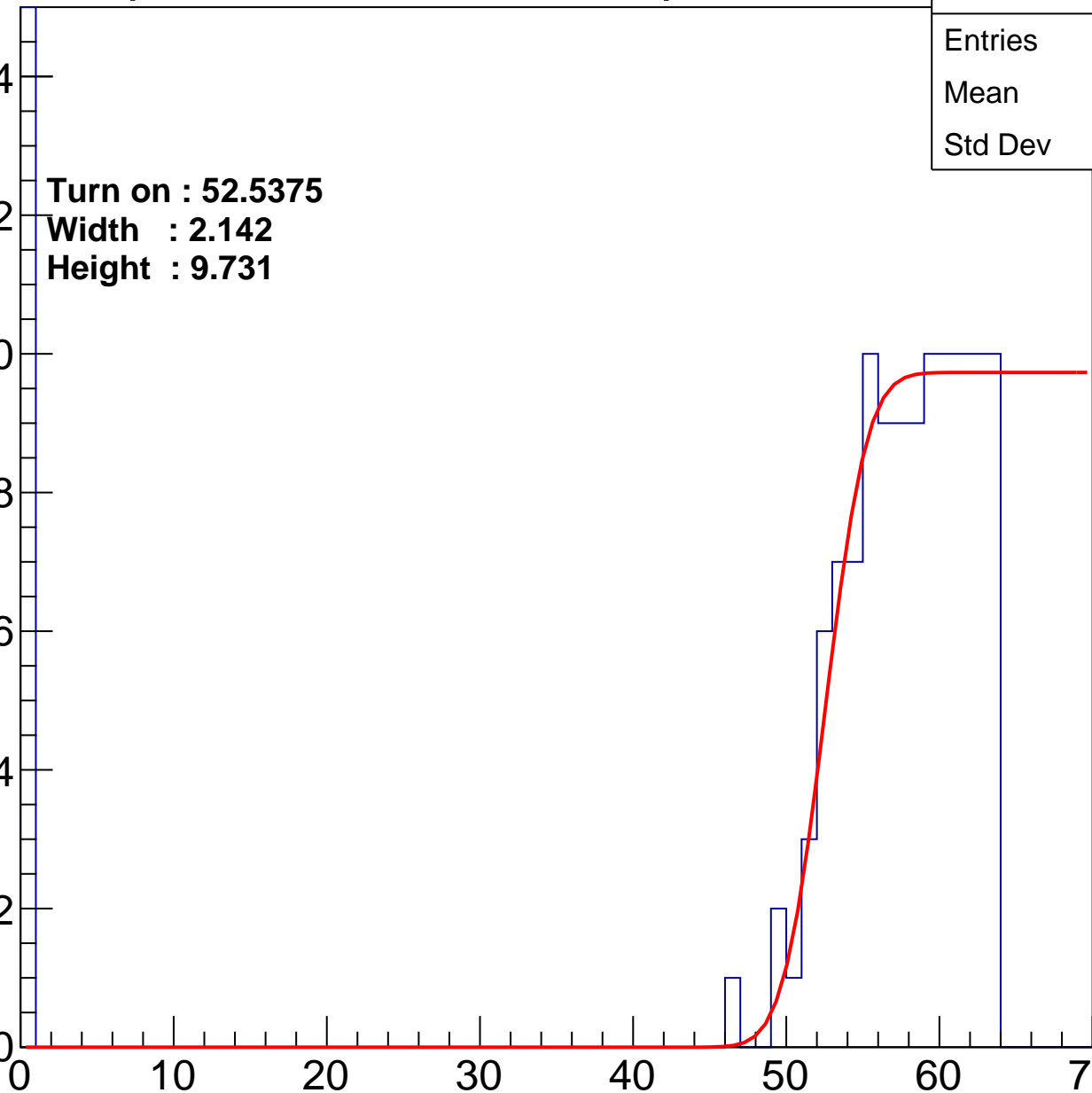
Width : 2.142

Height : 9.731

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	32.38
Std Dev	28.45

Turn on : 52.1086

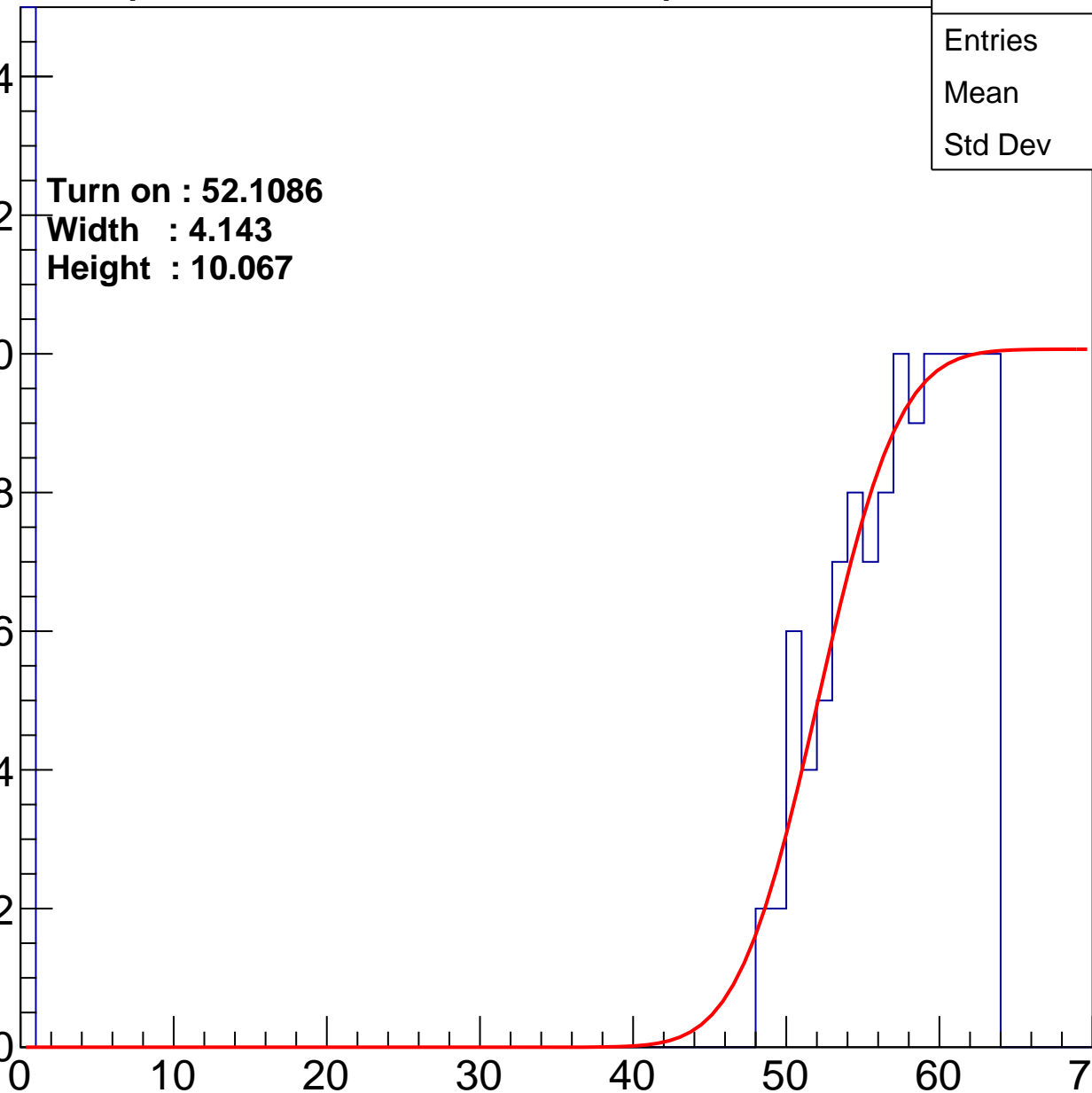
Width : 4.143

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.88
Std Dev	28.37

Turn on : 52.9703

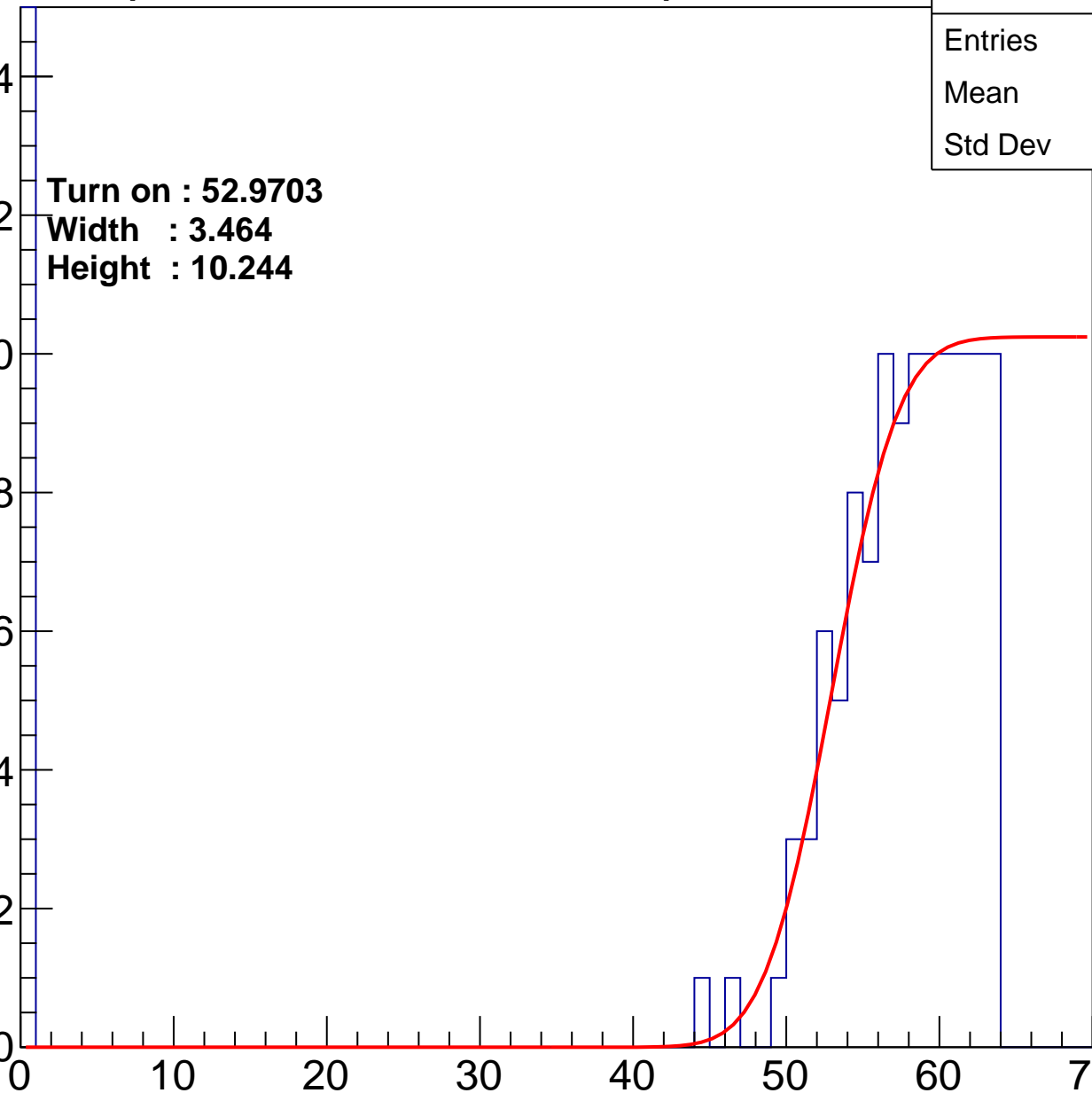
Width : 3.464

Height : 10.244

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	36.28
Std Dev	27.63

Turn on : 52.1210

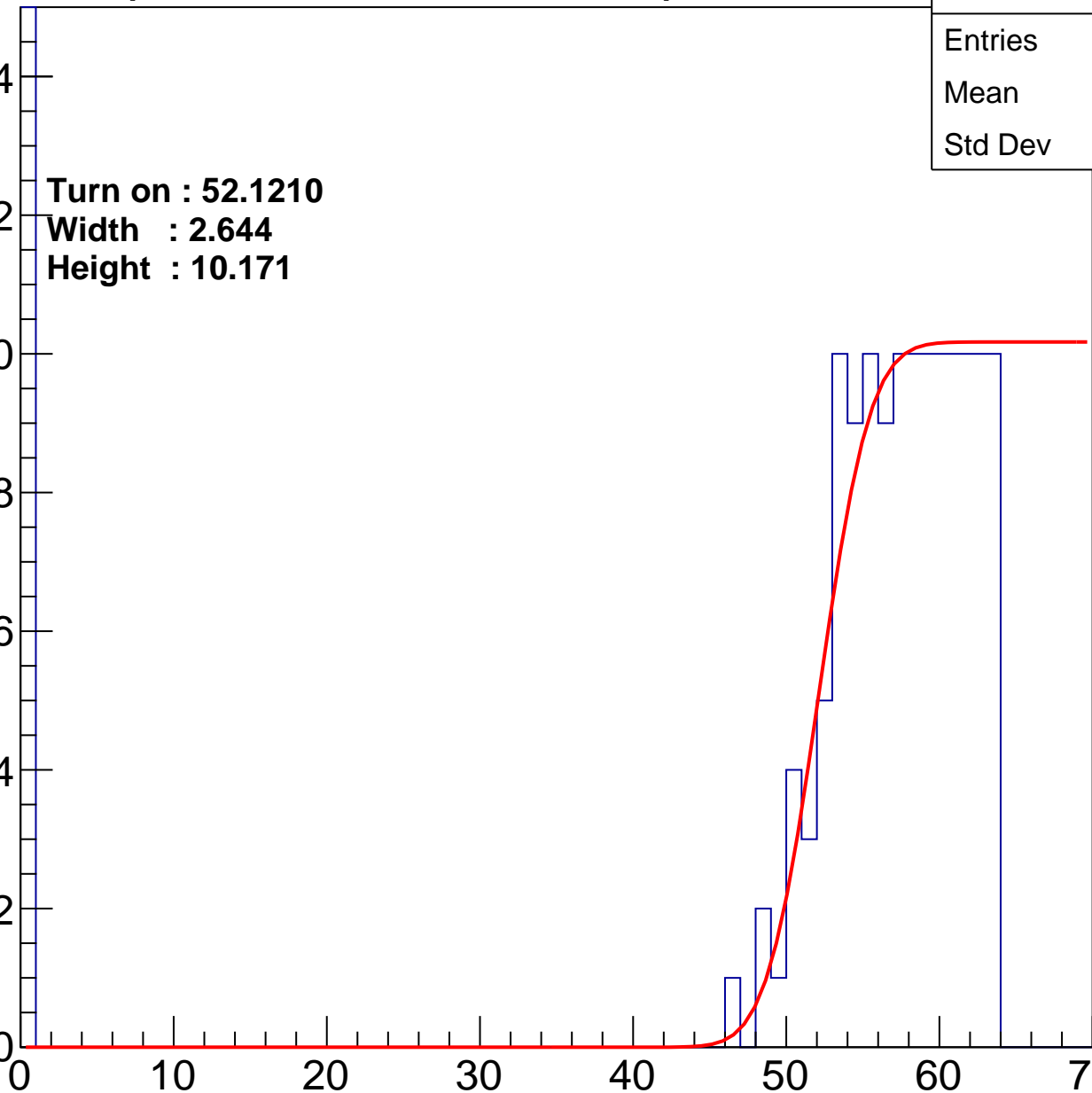
Width : 2.644

Height : 10.171

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	35.6
Std Dev	27.38

Turn on : 50.6617

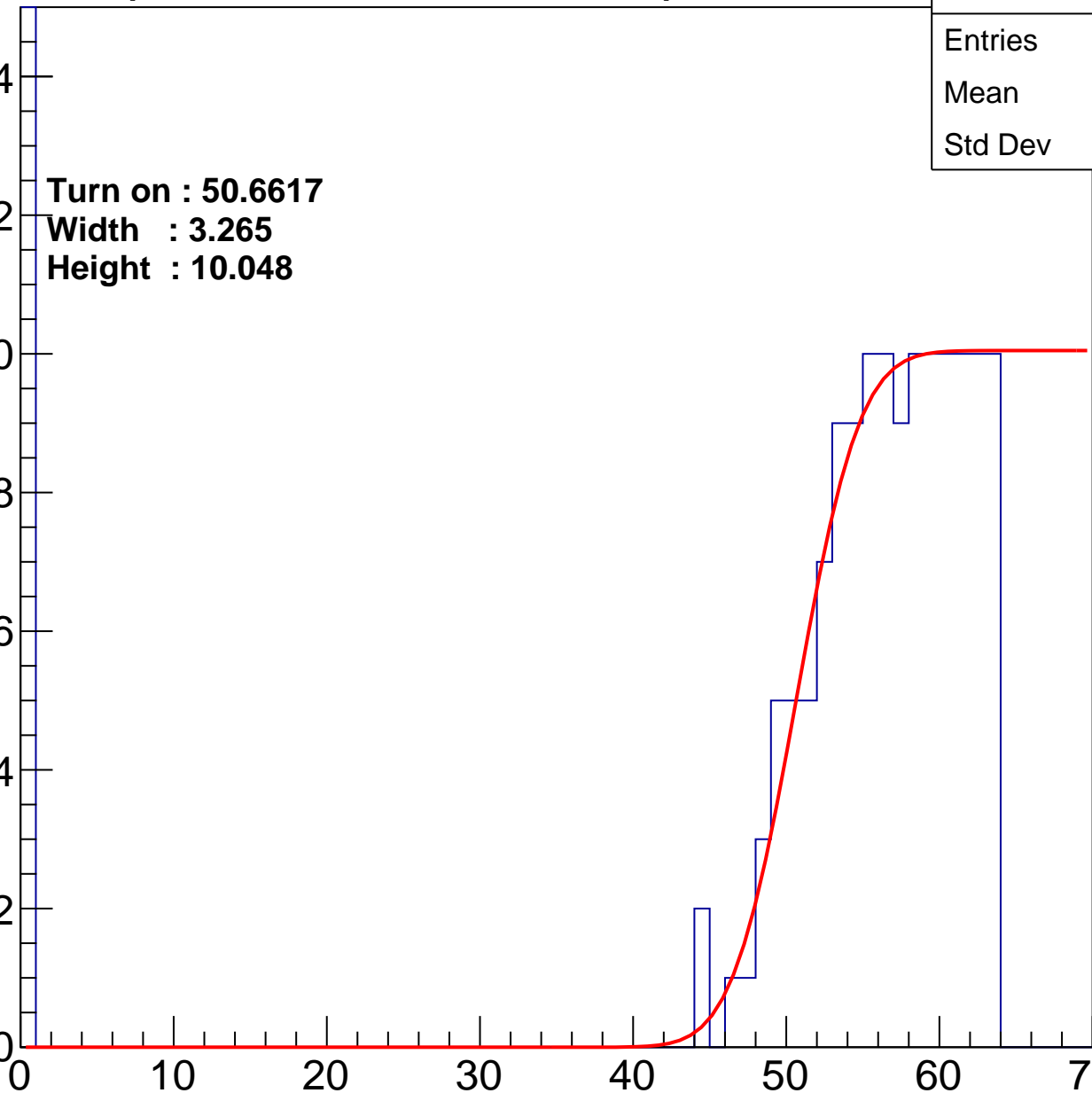
Width : 3.265

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	33.16
Std Dev	28.67

Turn on : 53.5514

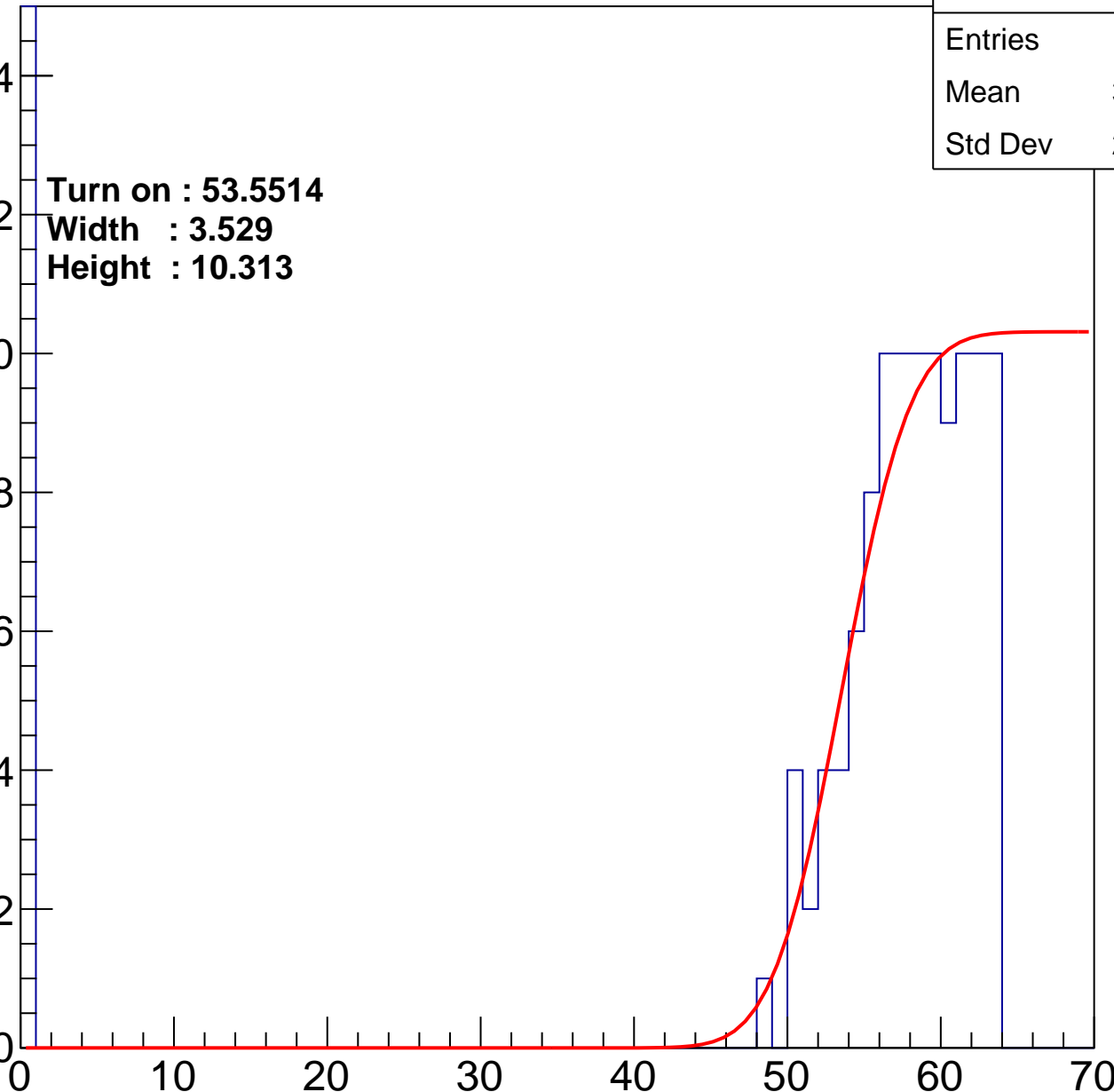
Width : 3.529

Height : 10.313

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch88

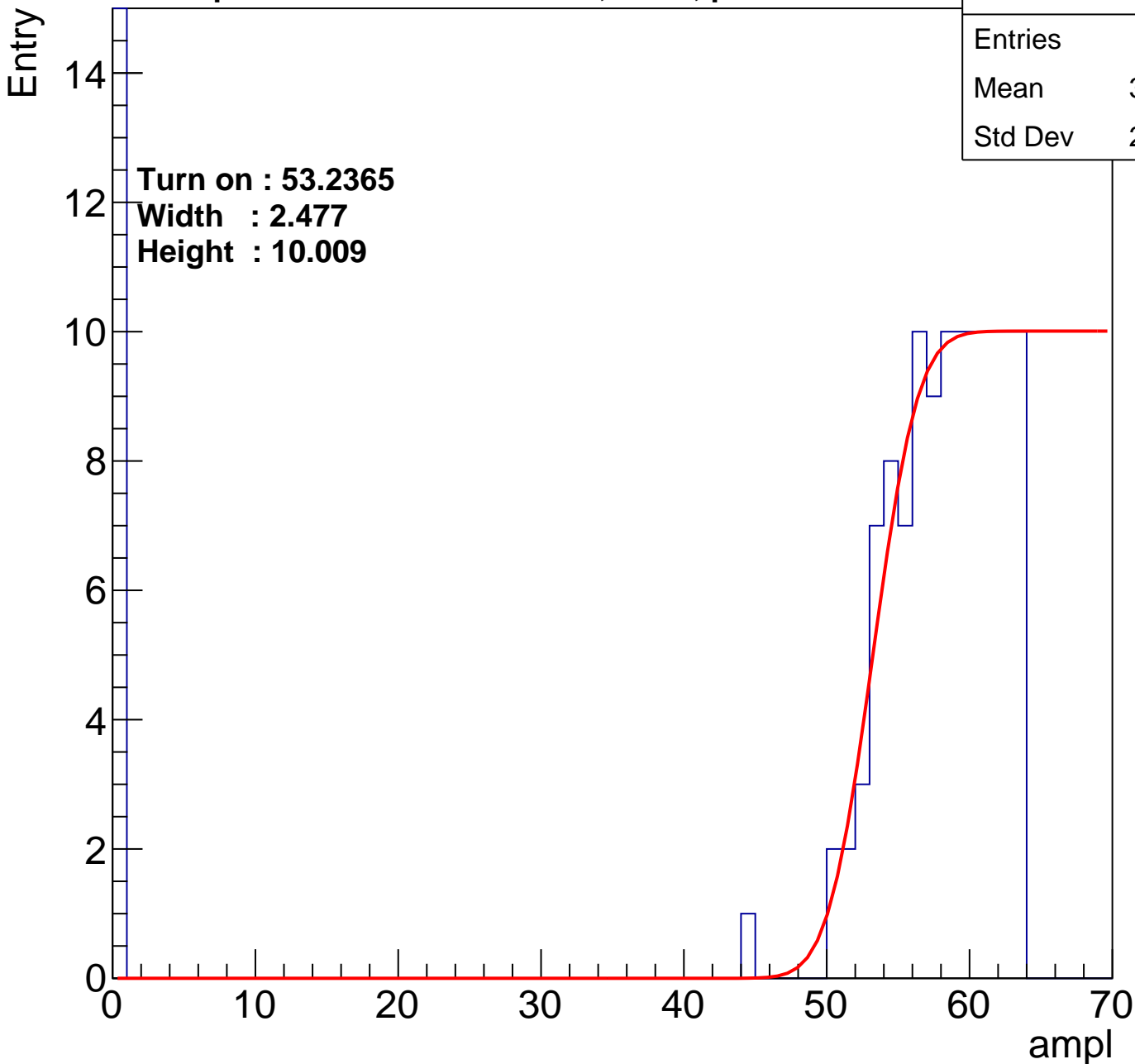
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	31.78
Std Dev	28.85

Turn on : 53.2365

Width : 2.477

Height : 10.009



B1L104S, U15-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	33.74
Std Dev	28.36

Turn on : 52.6244

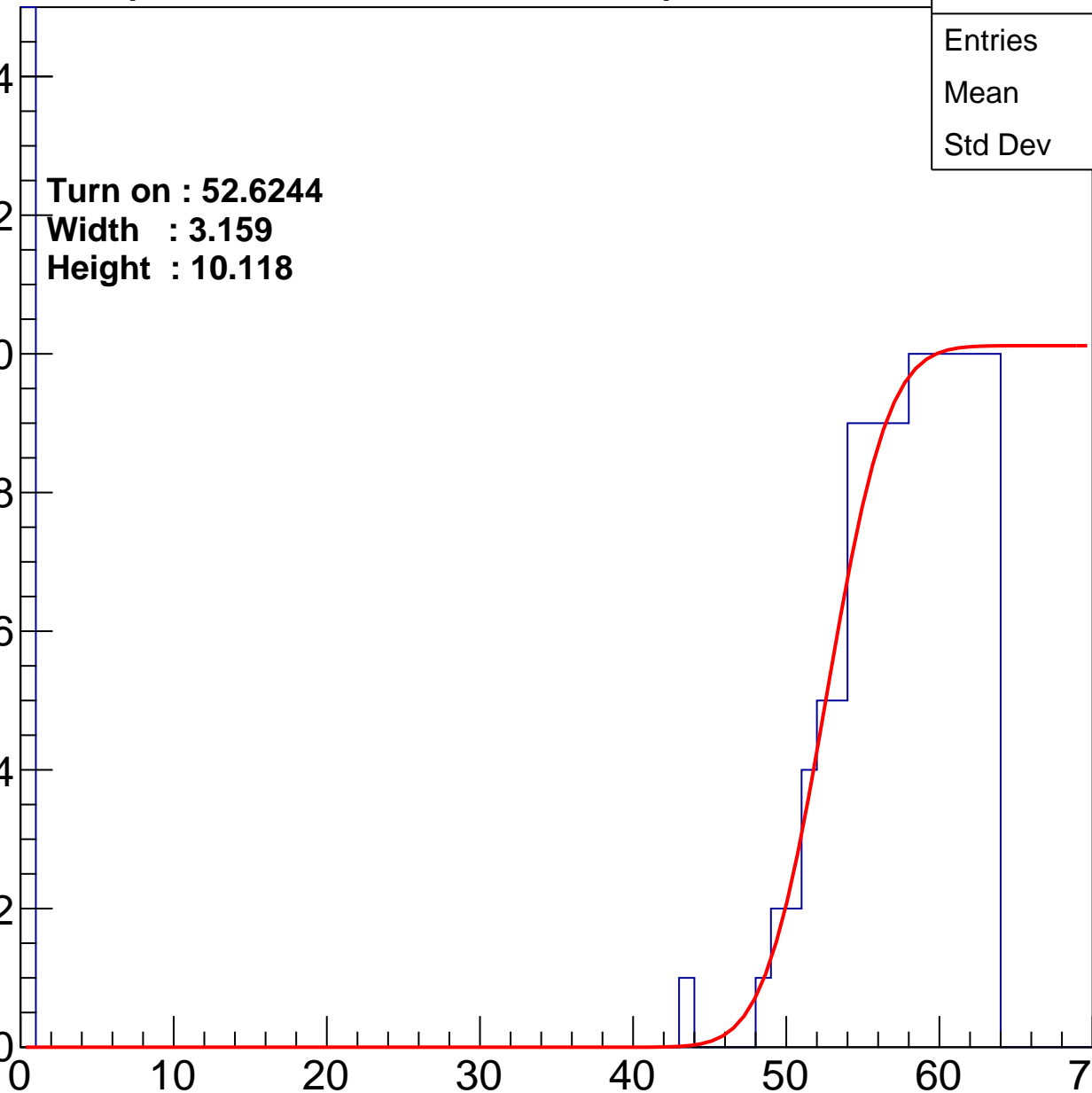
Width : 3.159

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	34.3
Std Dev	28.23

Turn on : 52.4605

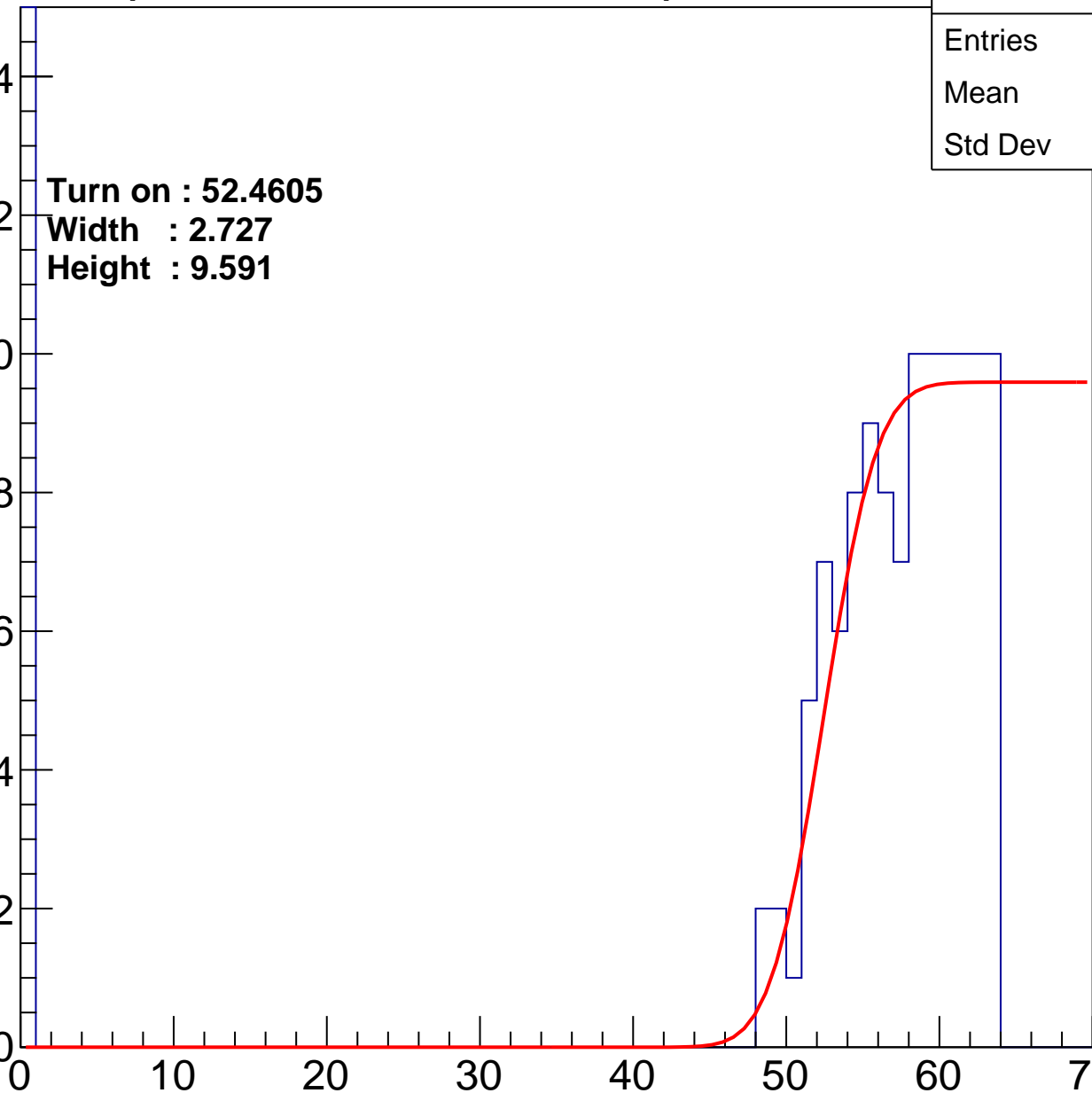
Width : 2.727

Height : 9.591

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	33.71
Std Dev	28.53

Turn on : 54.4085

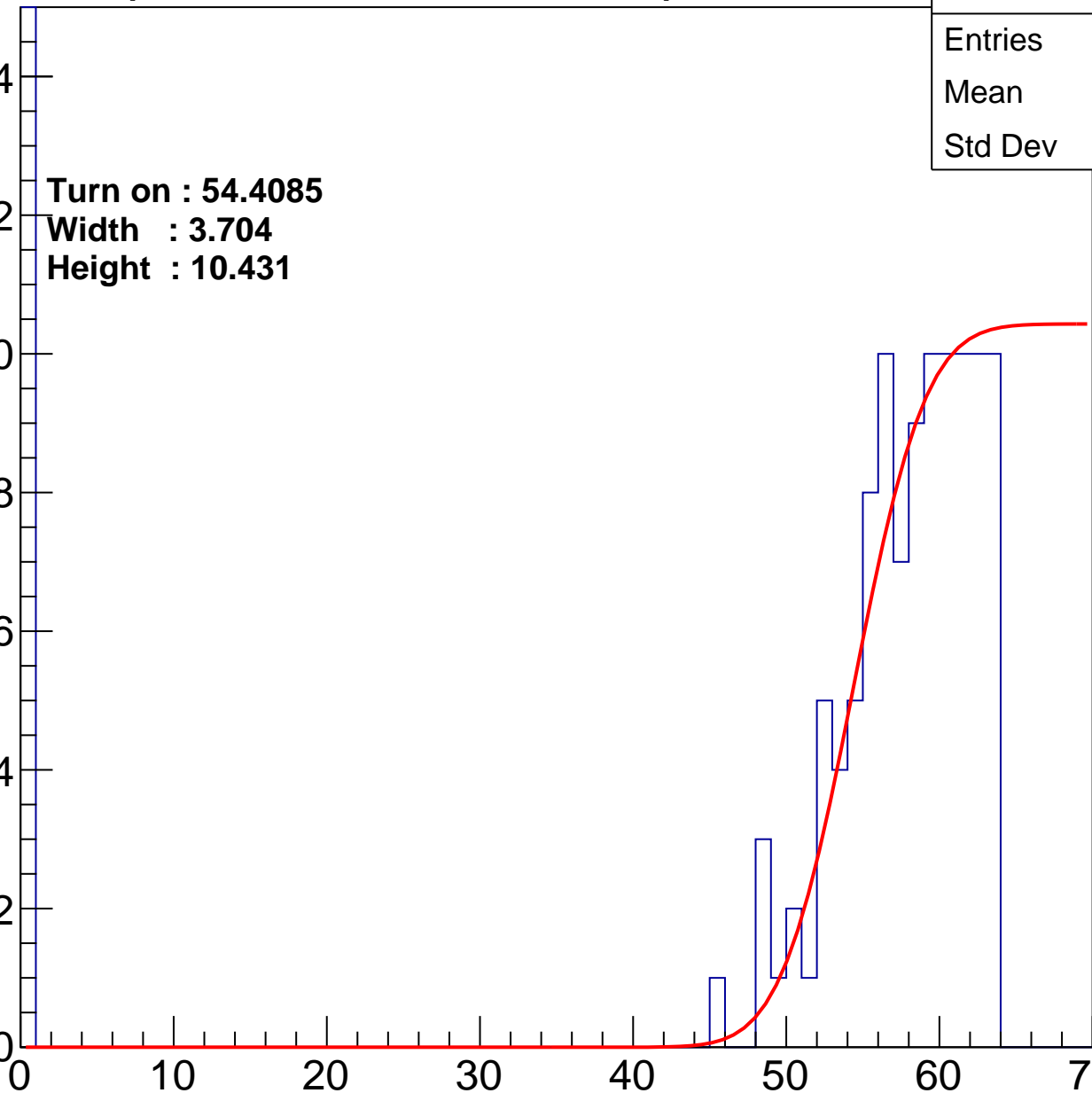
Width : 3.704

Height : 10.431

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	34.7
Std Dev	28.36

Turn on : 53.7341

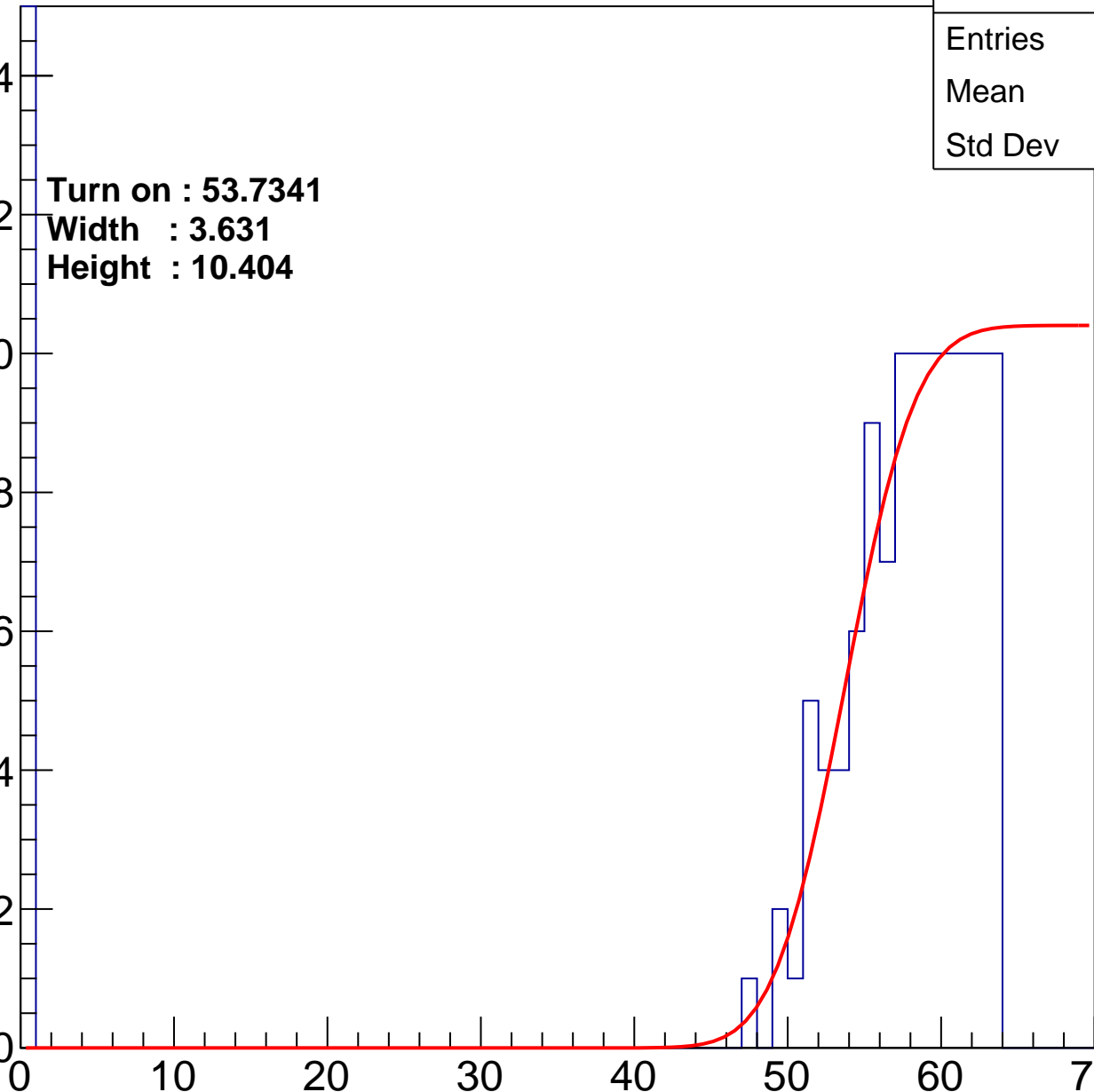
Width : 3.631

Height : 10.404

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	35.84
Std Dev	28.17

Turn on : 52.9045

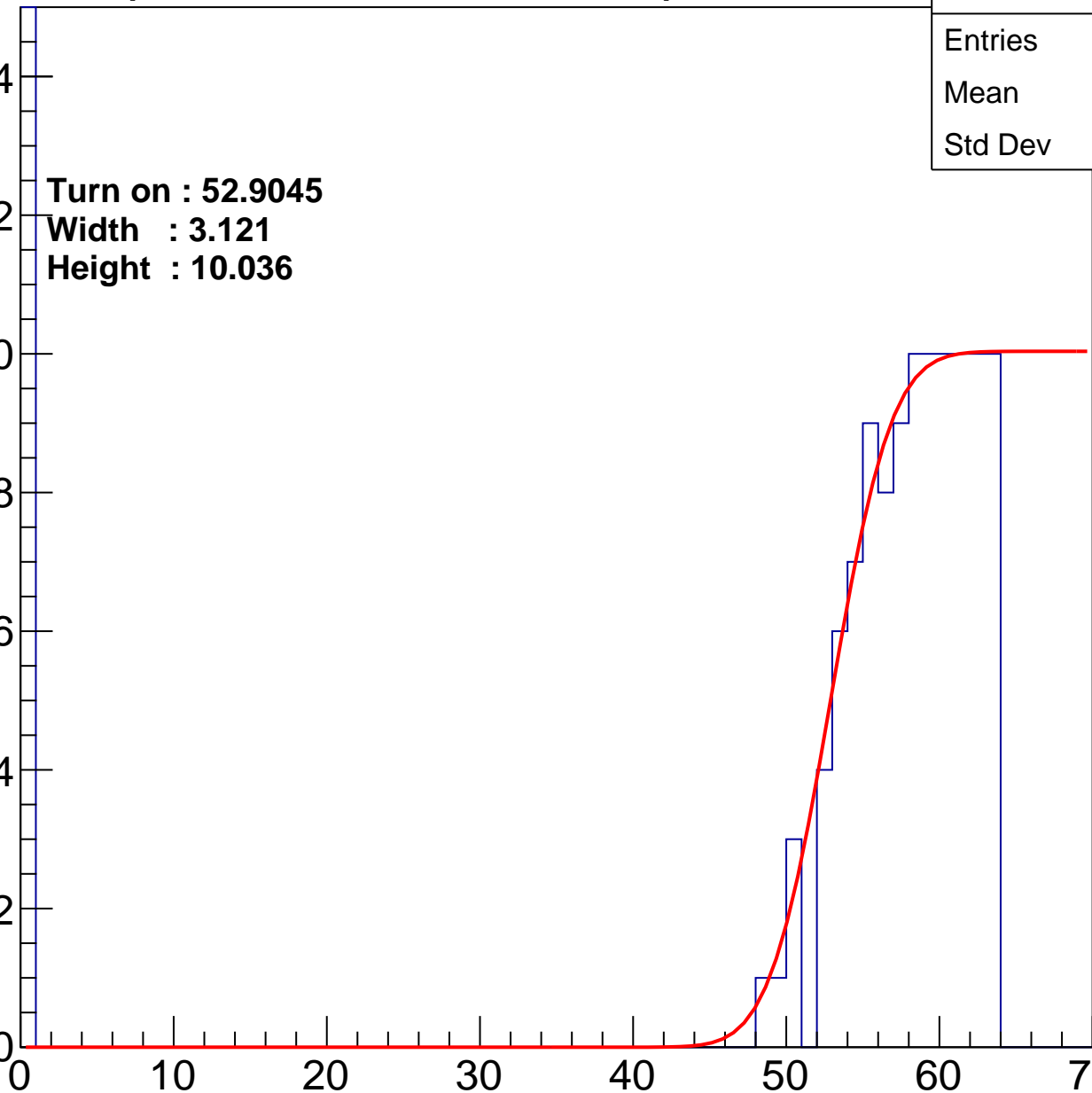
Width : 3.121

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch94

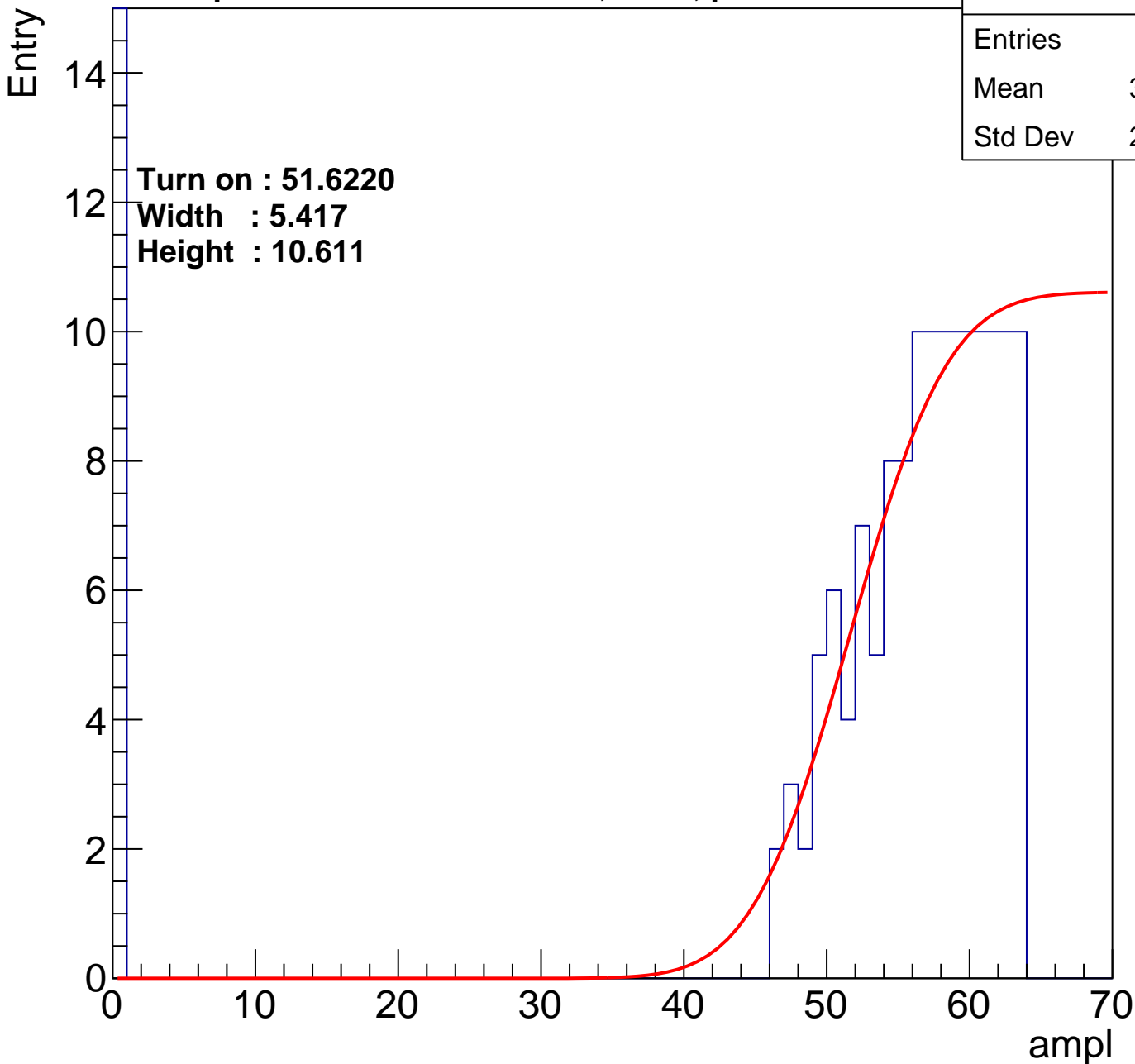
calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	33.06
Std Dev	28.03

Turn on : 51.6220

Width : 5.417

Height : 10.611



B1L104S, U15-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	39.02
Std Dev	27.03

Turn on : 53.7979

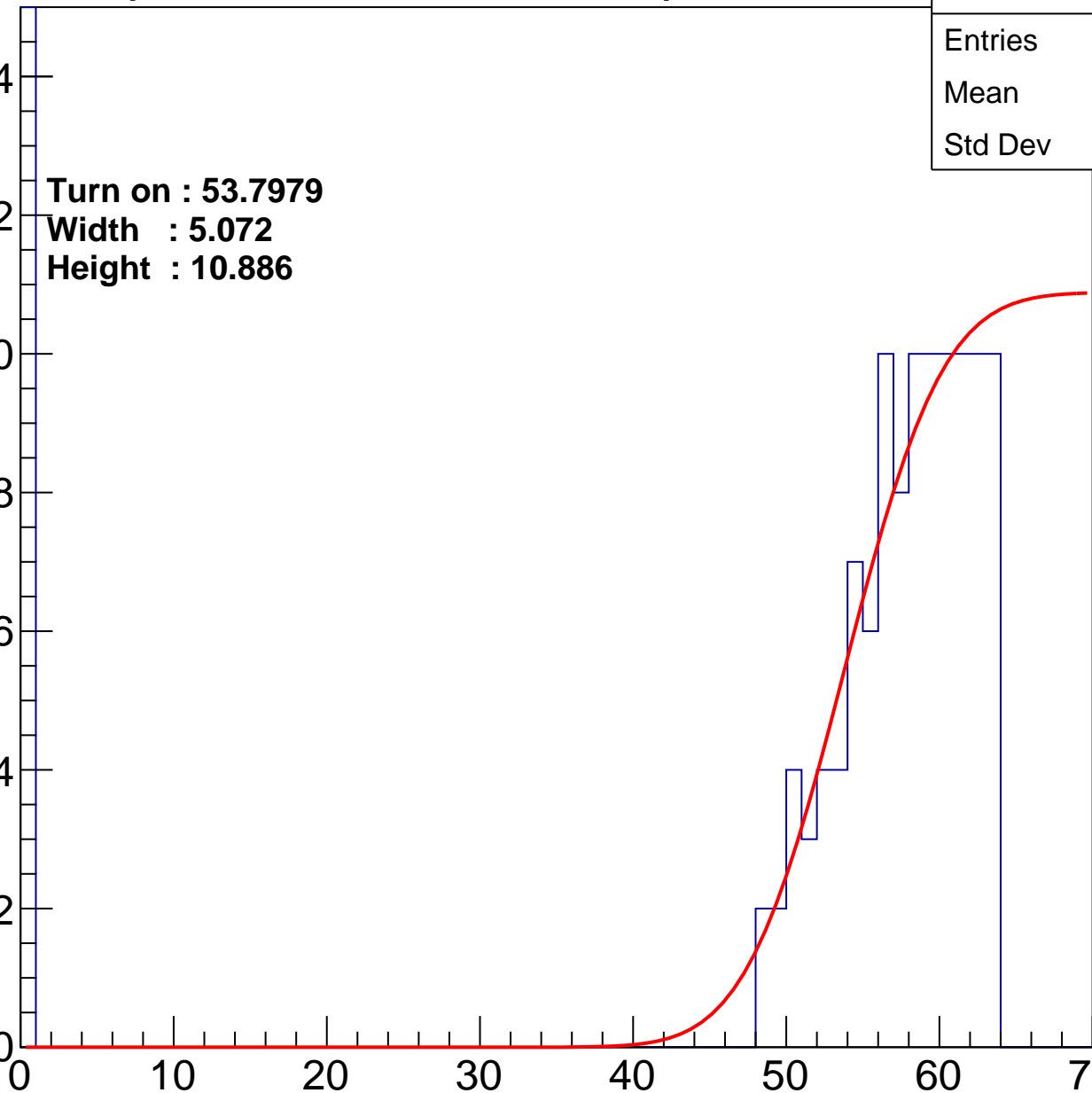
Width : 5.072

Height : 10.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	34.76
Std Dev	28.3

Turn on : 53.6700

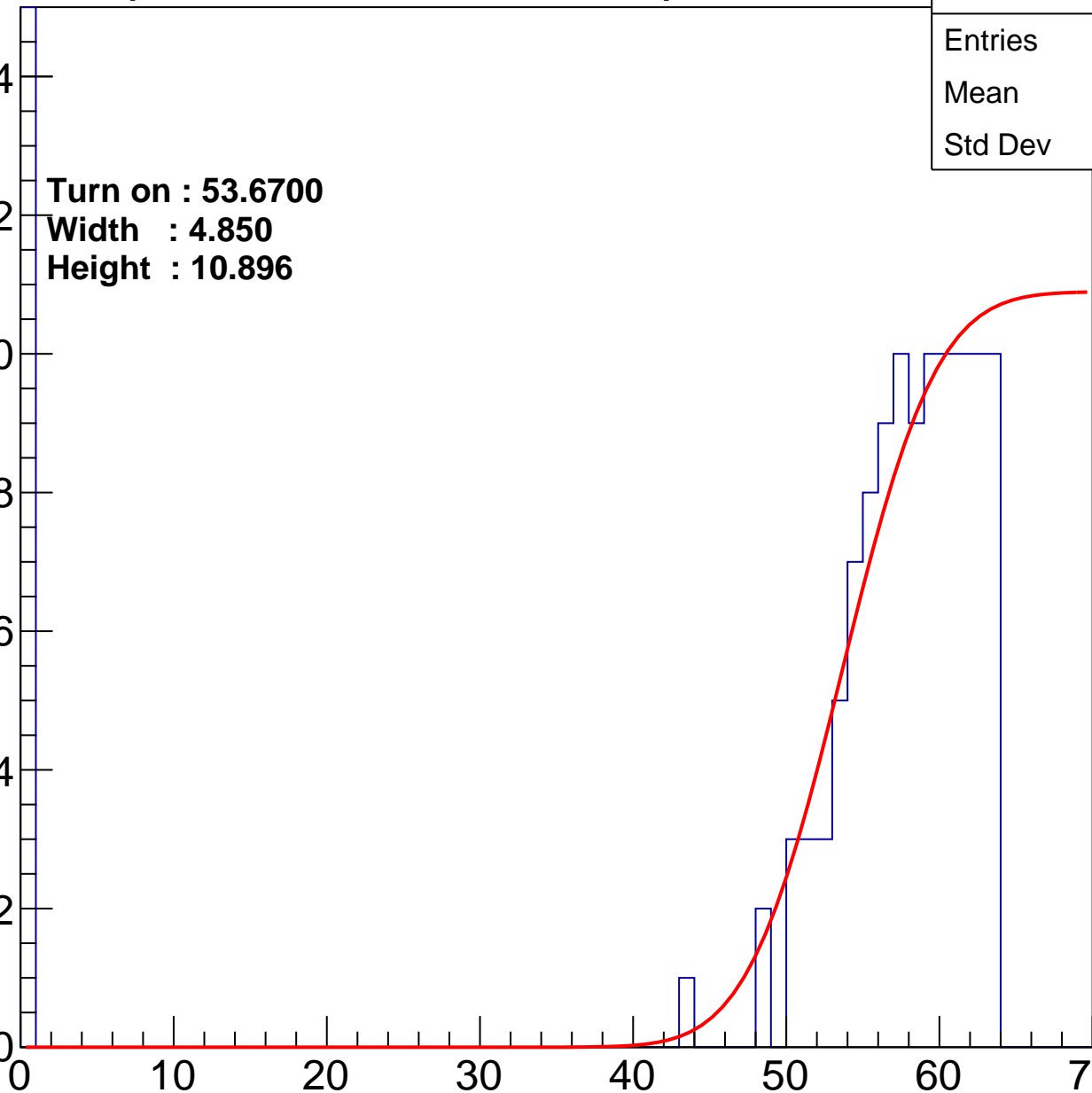
Width : 4.850

Height : 10.896

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch97

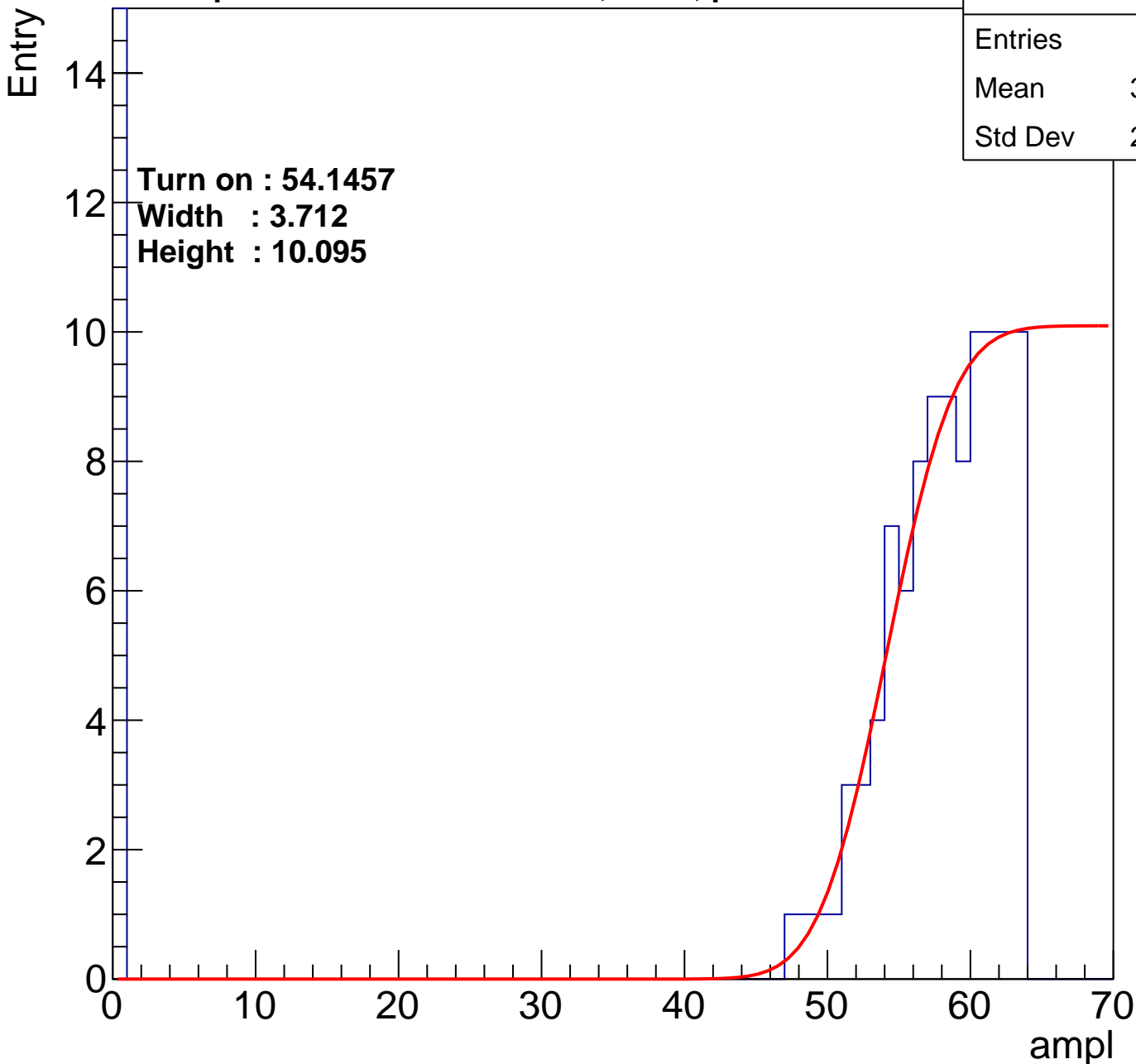
calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	31.22
Std Dev	28.94

Turn on : 54.1457

Width : 3.712

Height : 10.095



B1L104S, U15-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	237
Mean	33.85
Std Dev	27.48

Turn on : 50.7324

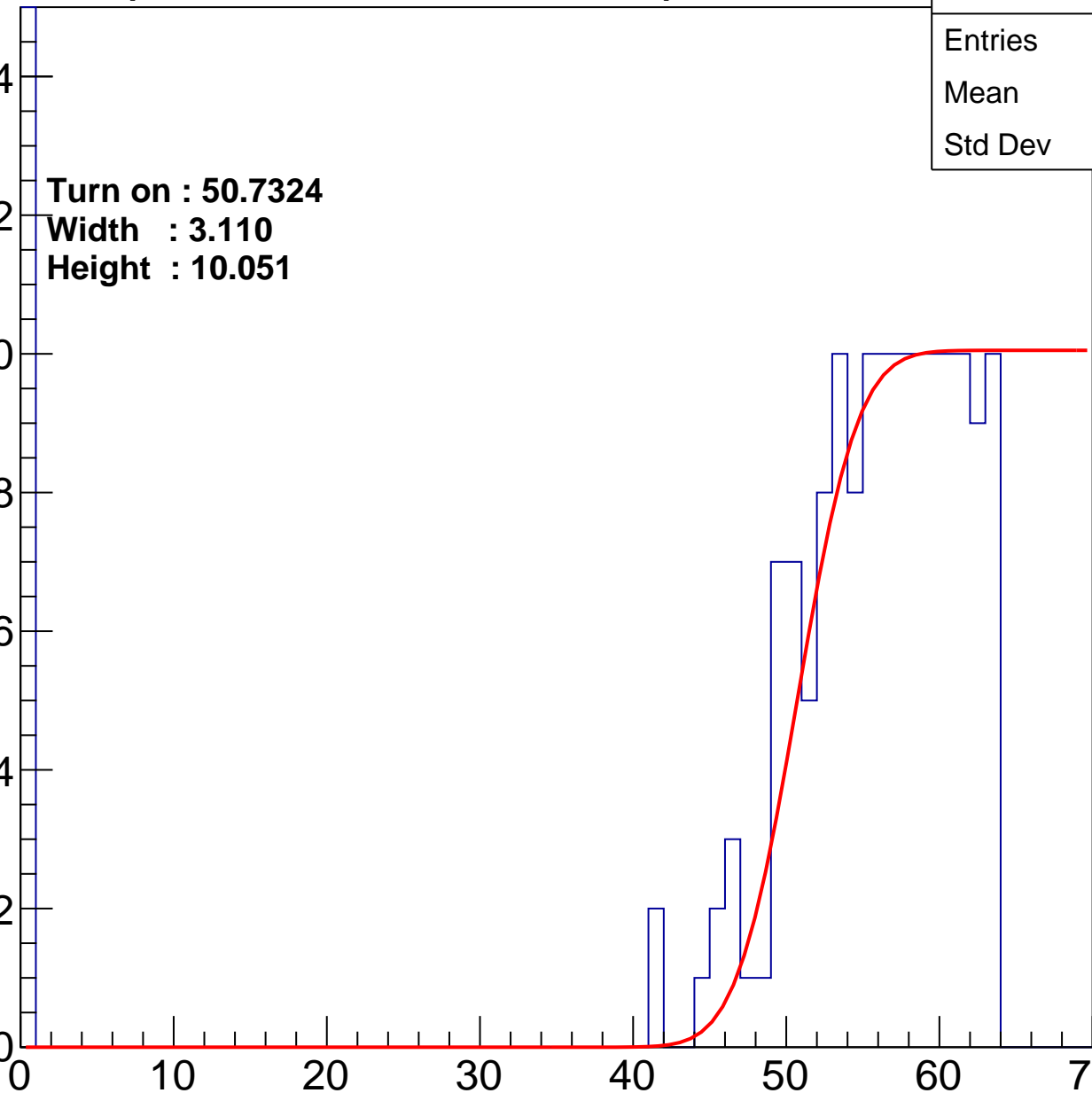
Width : 3.110

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	31.08
Std Dev	29.11

Turn on : 55.7073

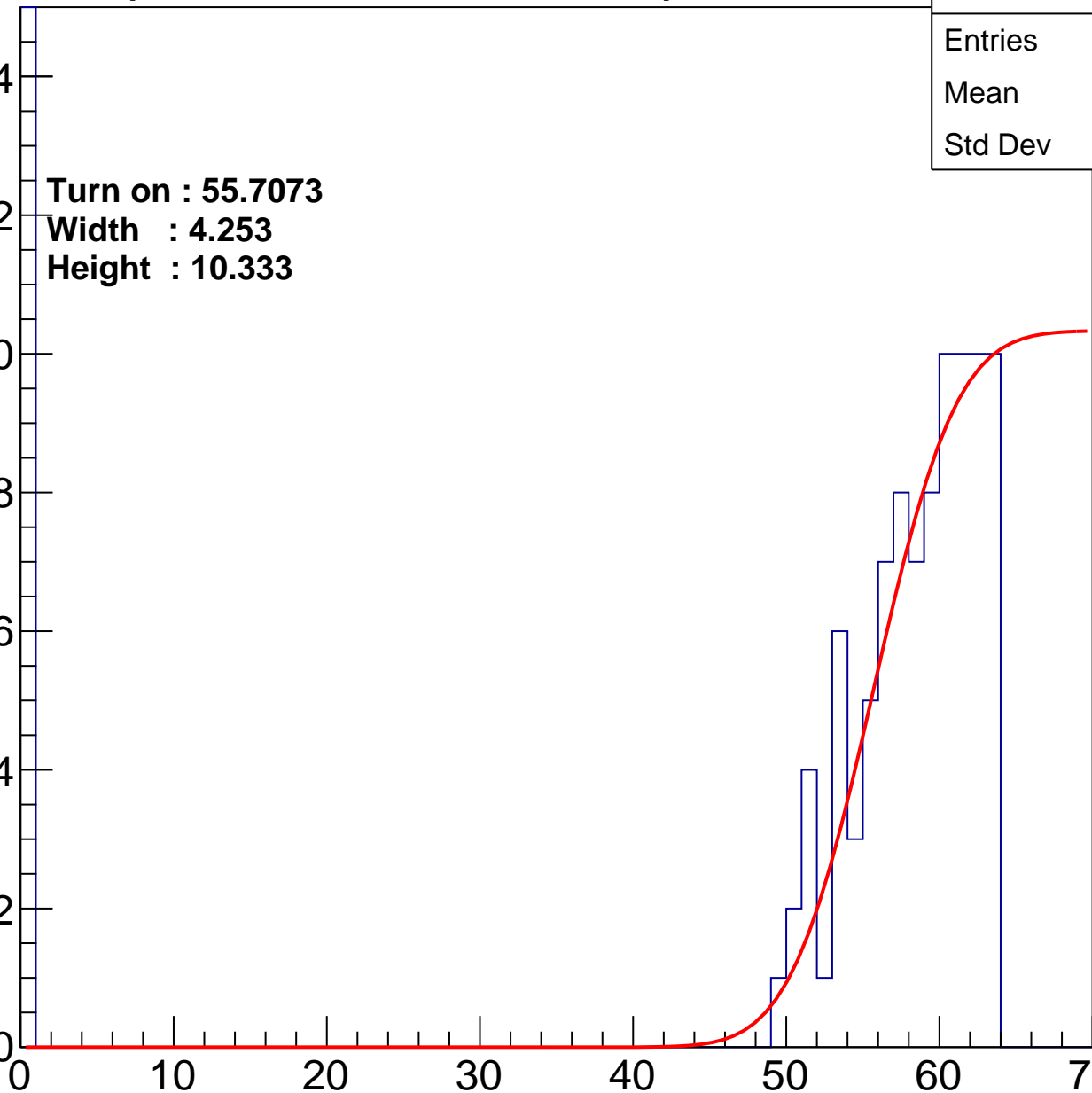
Width : 4.253

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch100

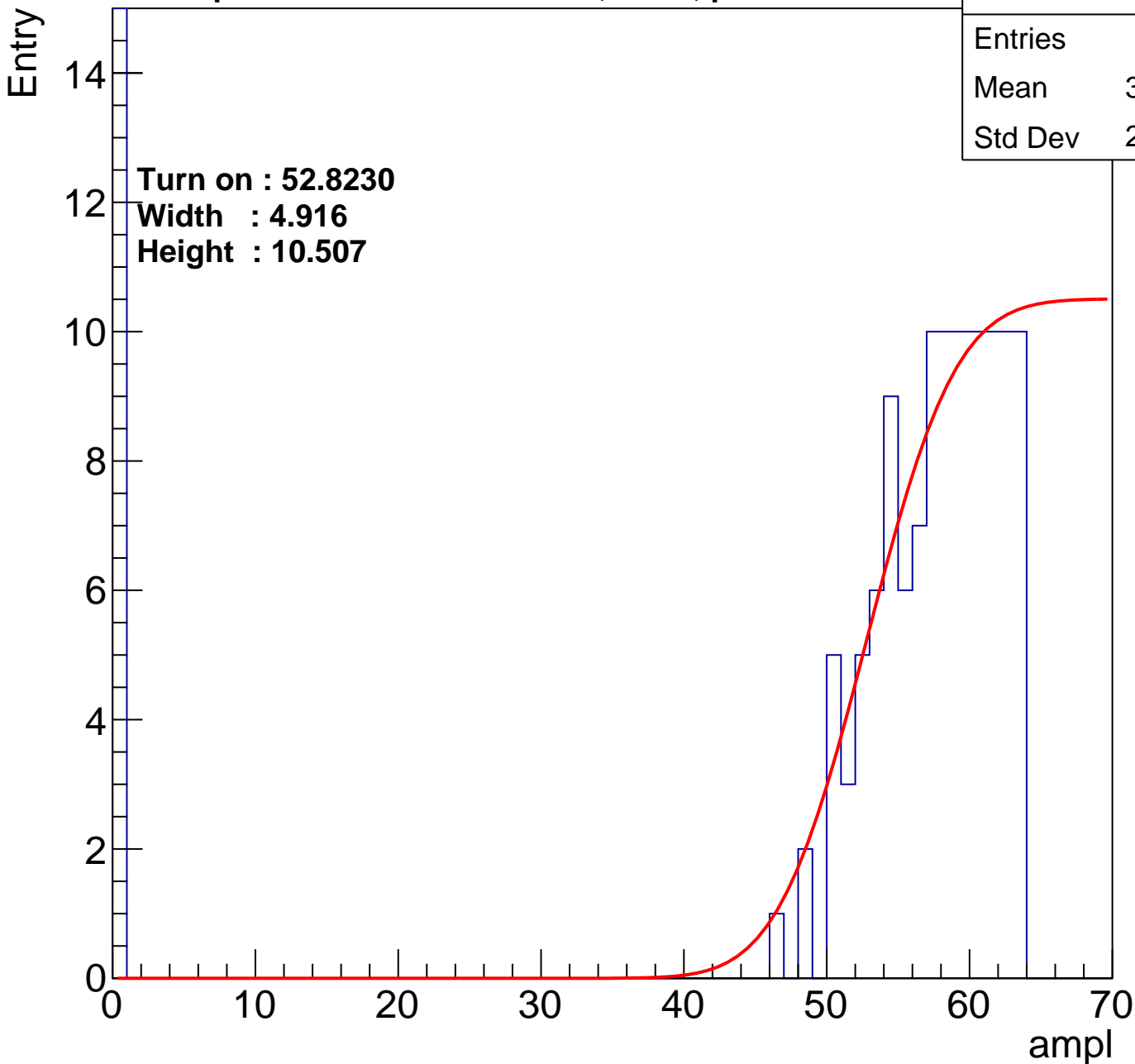
calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	34.19
Std Dev	28.28

Turn on : 52.8230

Width : 4.916

Height : 10.507



B1L104S, U15-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	32.57
Std Dev	28.9

Turn on : 54.5380

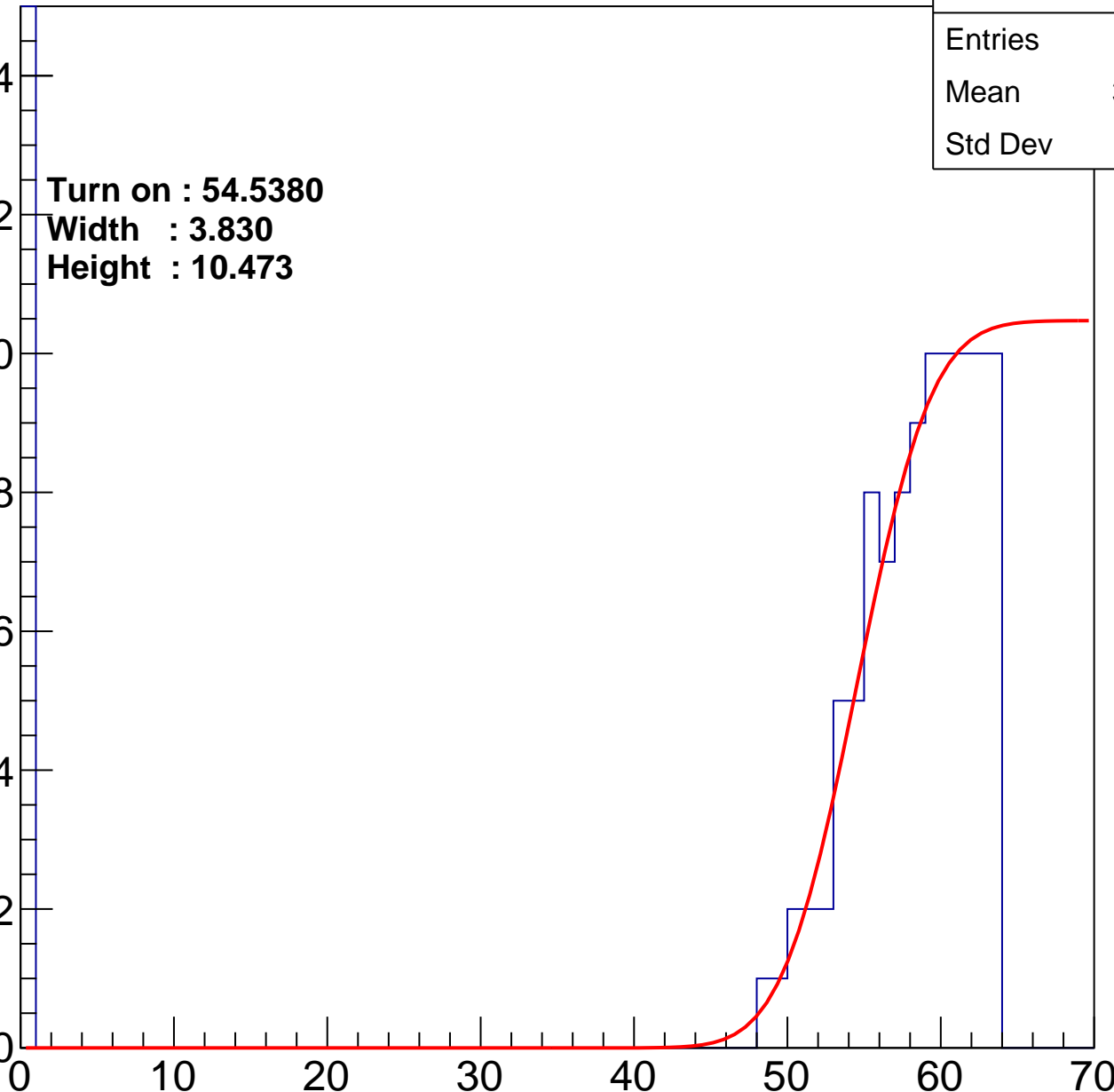
Width : 3.830

Height : 10.473

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	29.96
Std Dev	28.83

Turn on : 54.0433

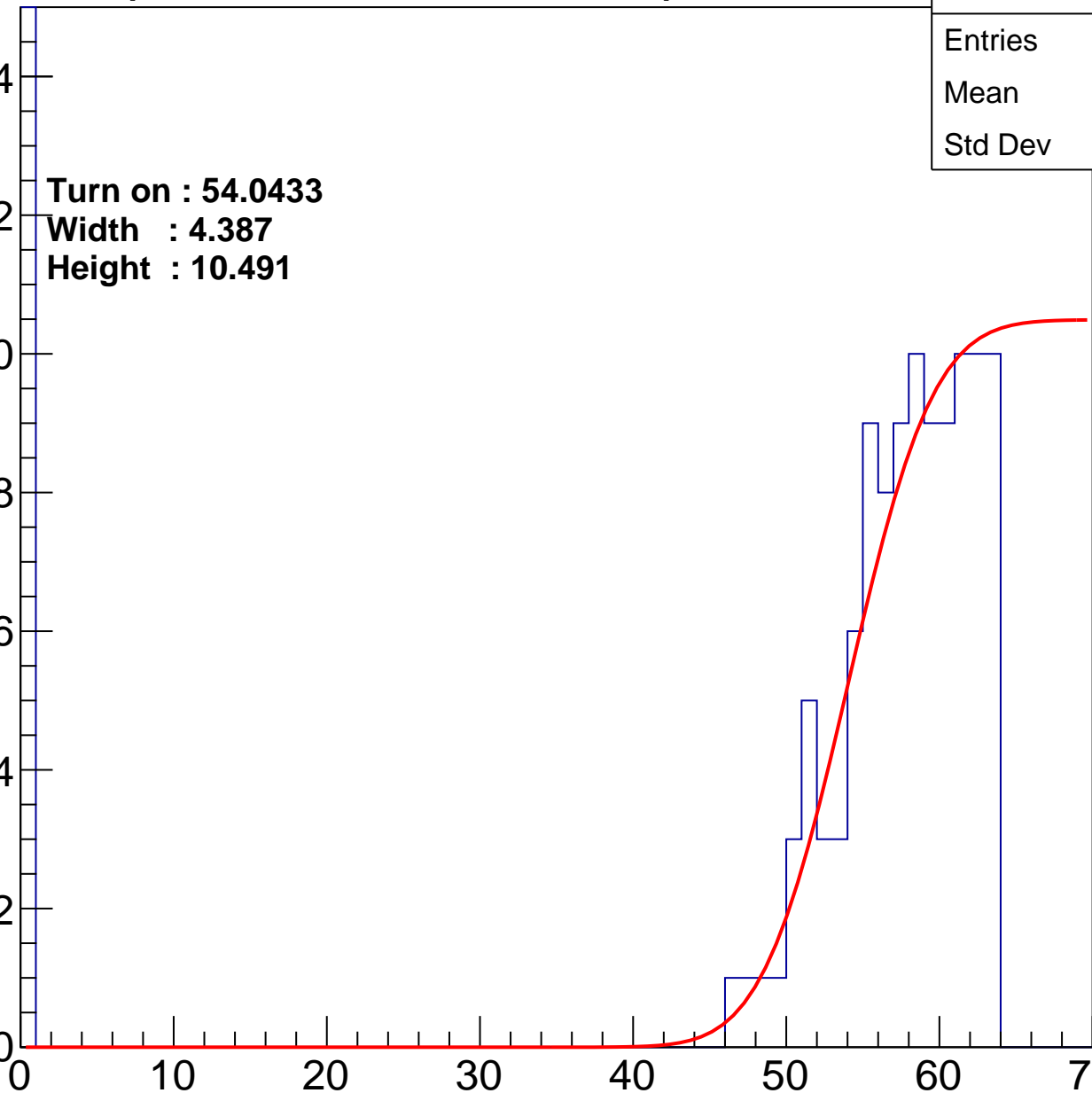
Width : 4.387

Height : 10.491

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	32.74
Std Dev	28.99

Turn on : 53.9833

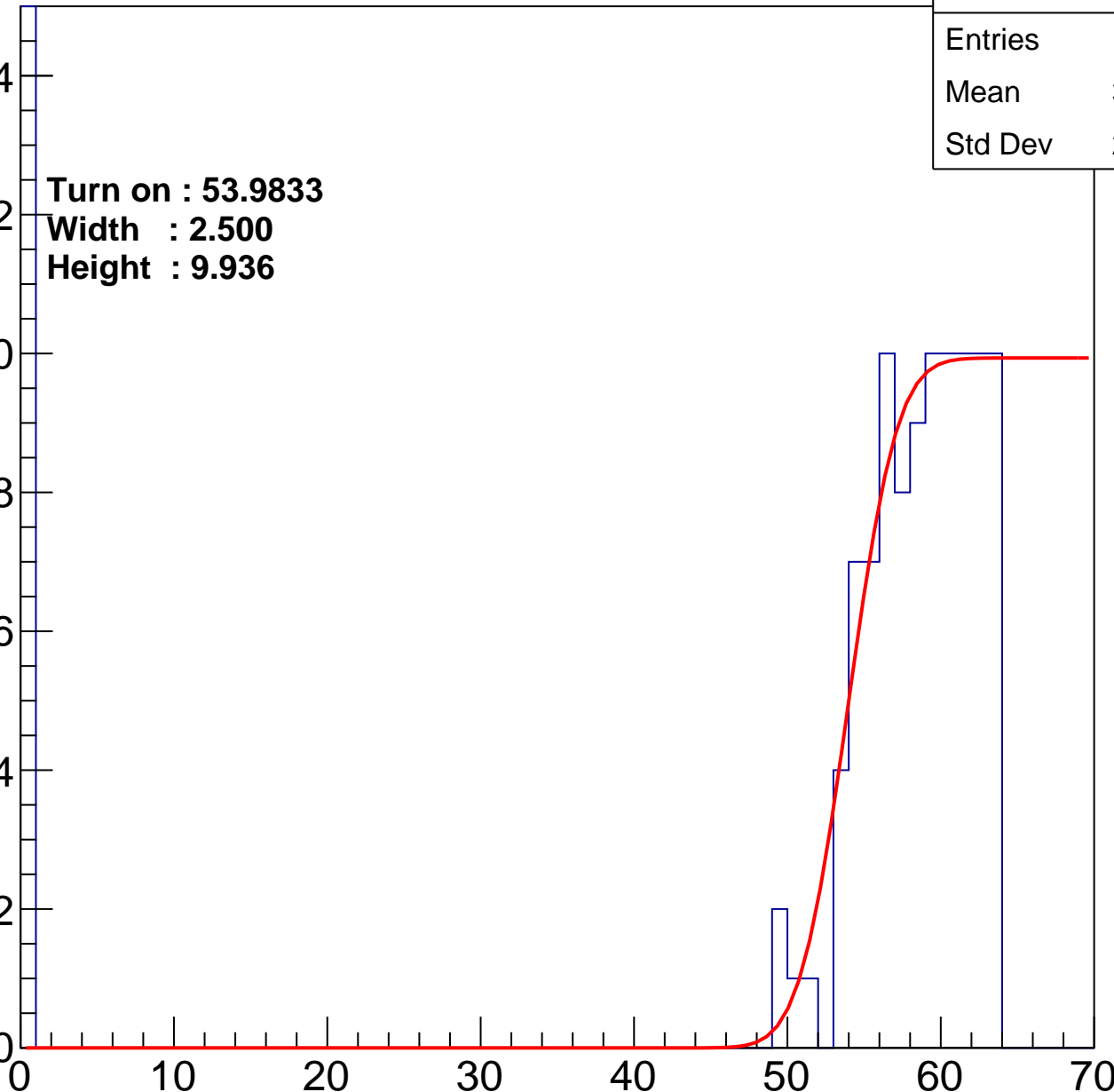
Width : 2.500

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch104

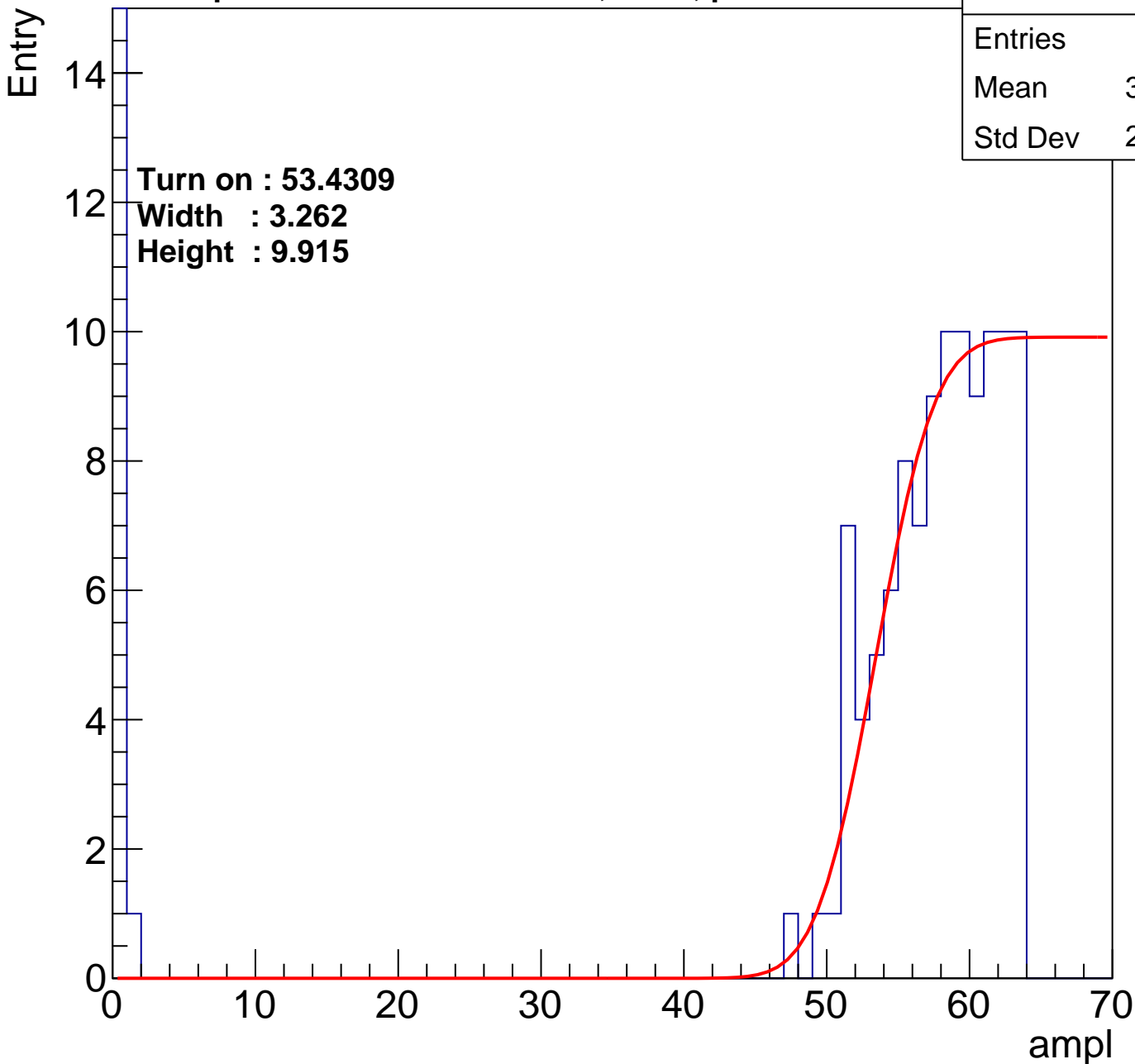
calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	34.34
Std Dev	28.38

Turn on : 53.4309

Width : 3.262

Height : 9.915



B1L104S, U15-ch105

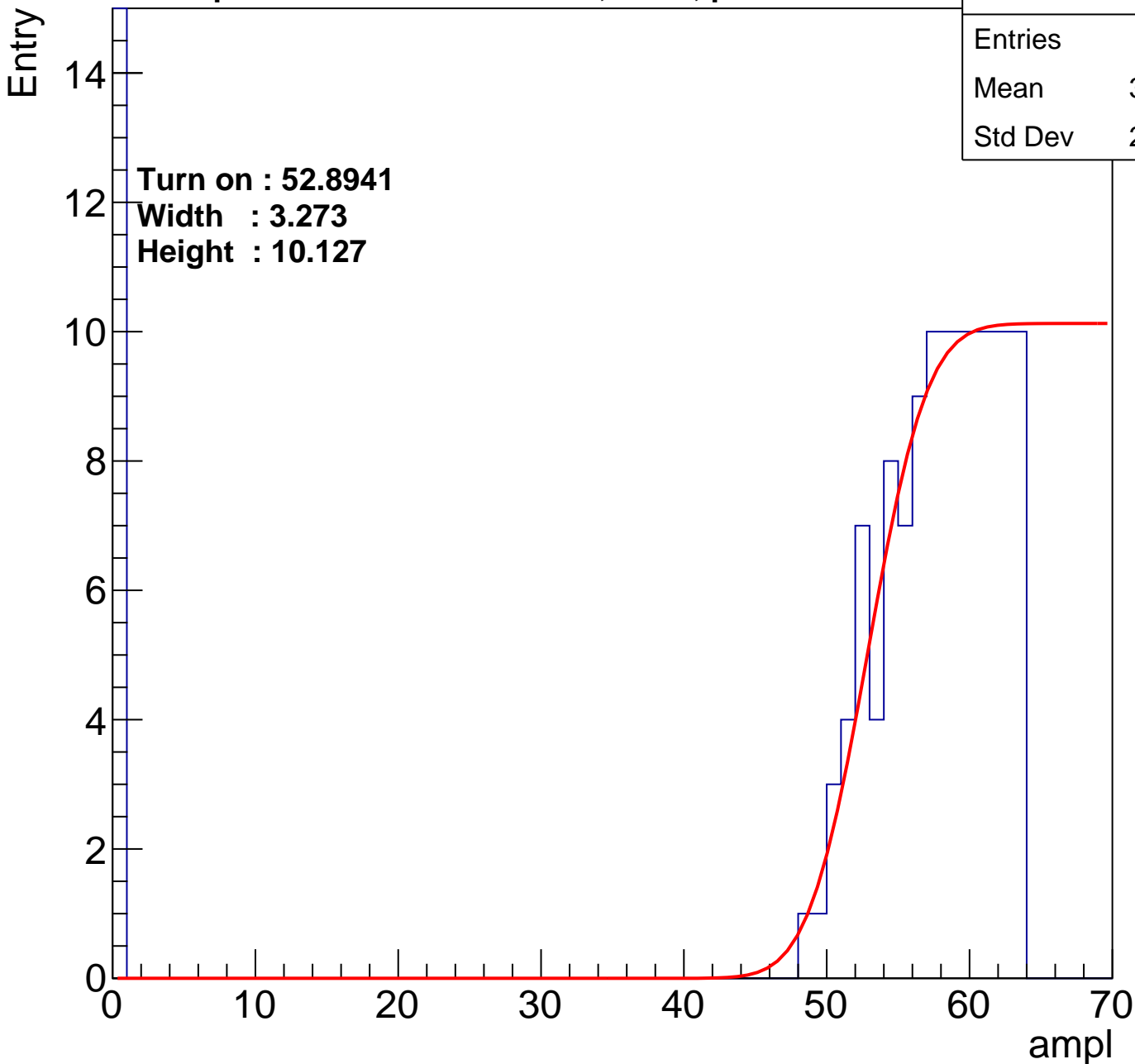
calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	33.58
Std Dev	28.46

Turn on : 52.8941

Width : 3.273

Height : 10.127



B1L104S, U15-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	33.5
Std Dev	28.14

Turn on : 52.0790

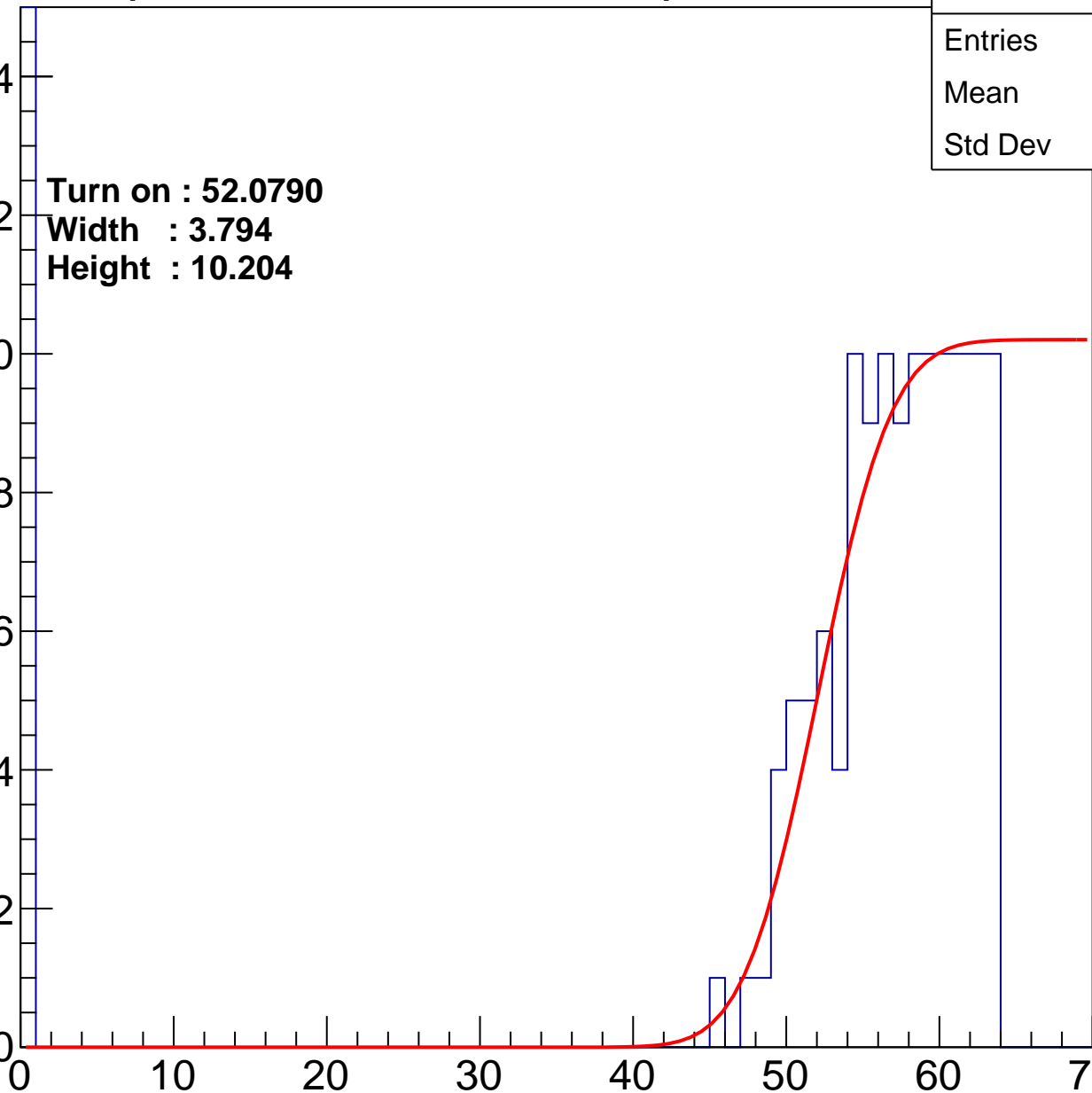
Width : 3.794

Height : 10.204

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	39.25
Std Dev	26.92

Turn on : 52.5049

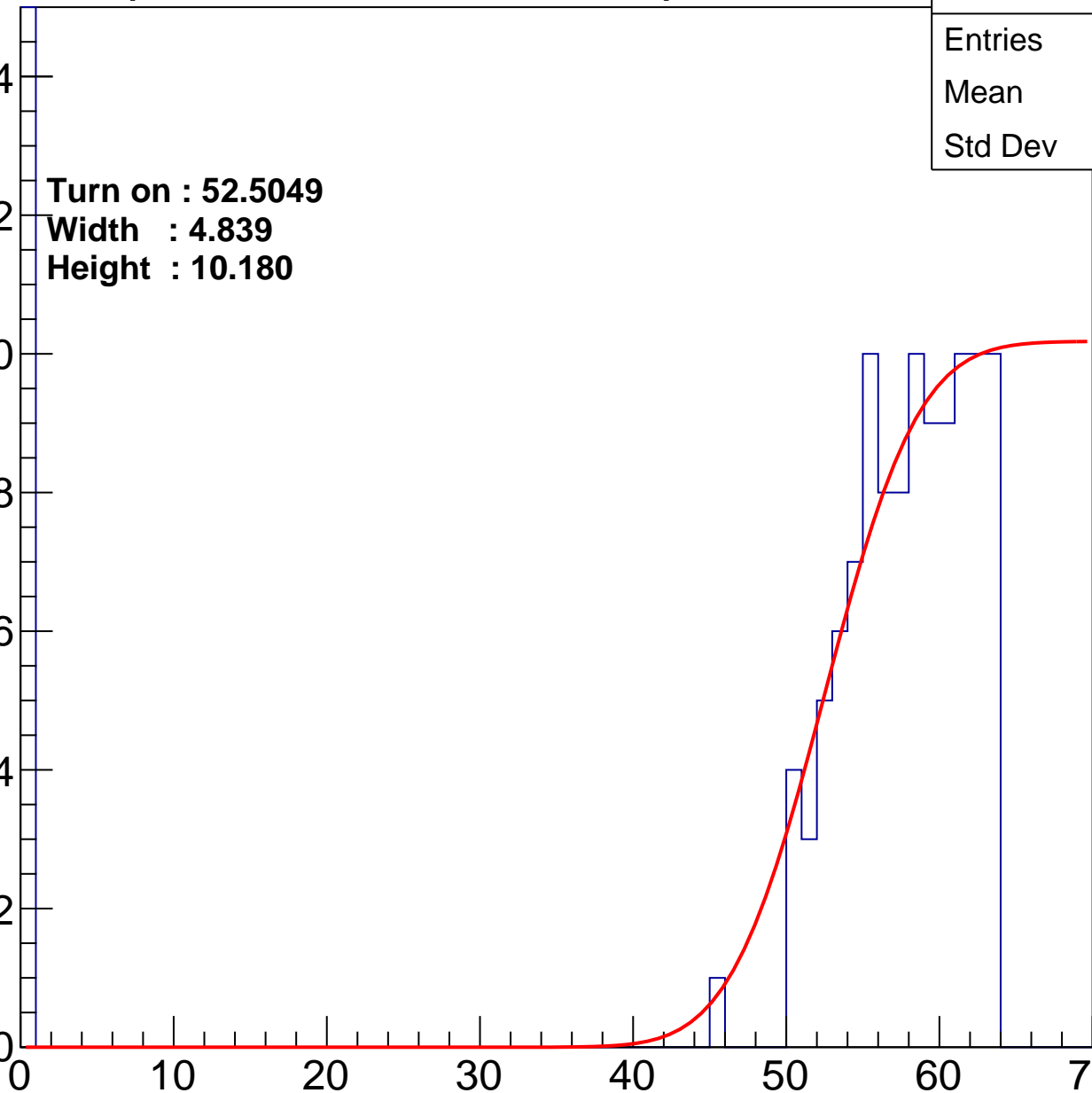
Width : 4.839

Height : 10.180

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	37.71
Std Dev	27.32

Turn on : 52.8671

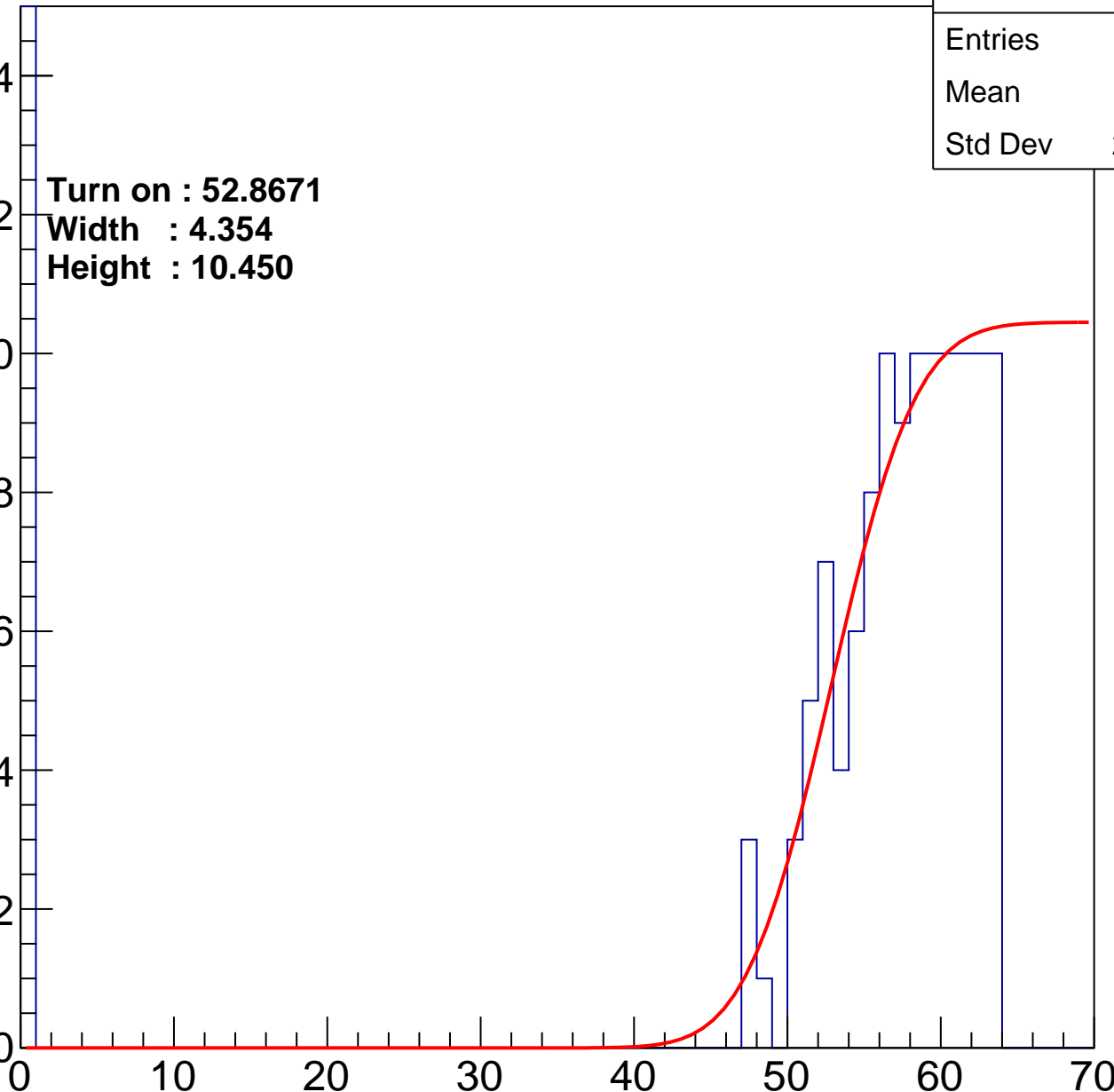
Width : 4.354

Height : 10.450

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch109

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	39.42
Std Dev	26.48

Turn on : 52.4121

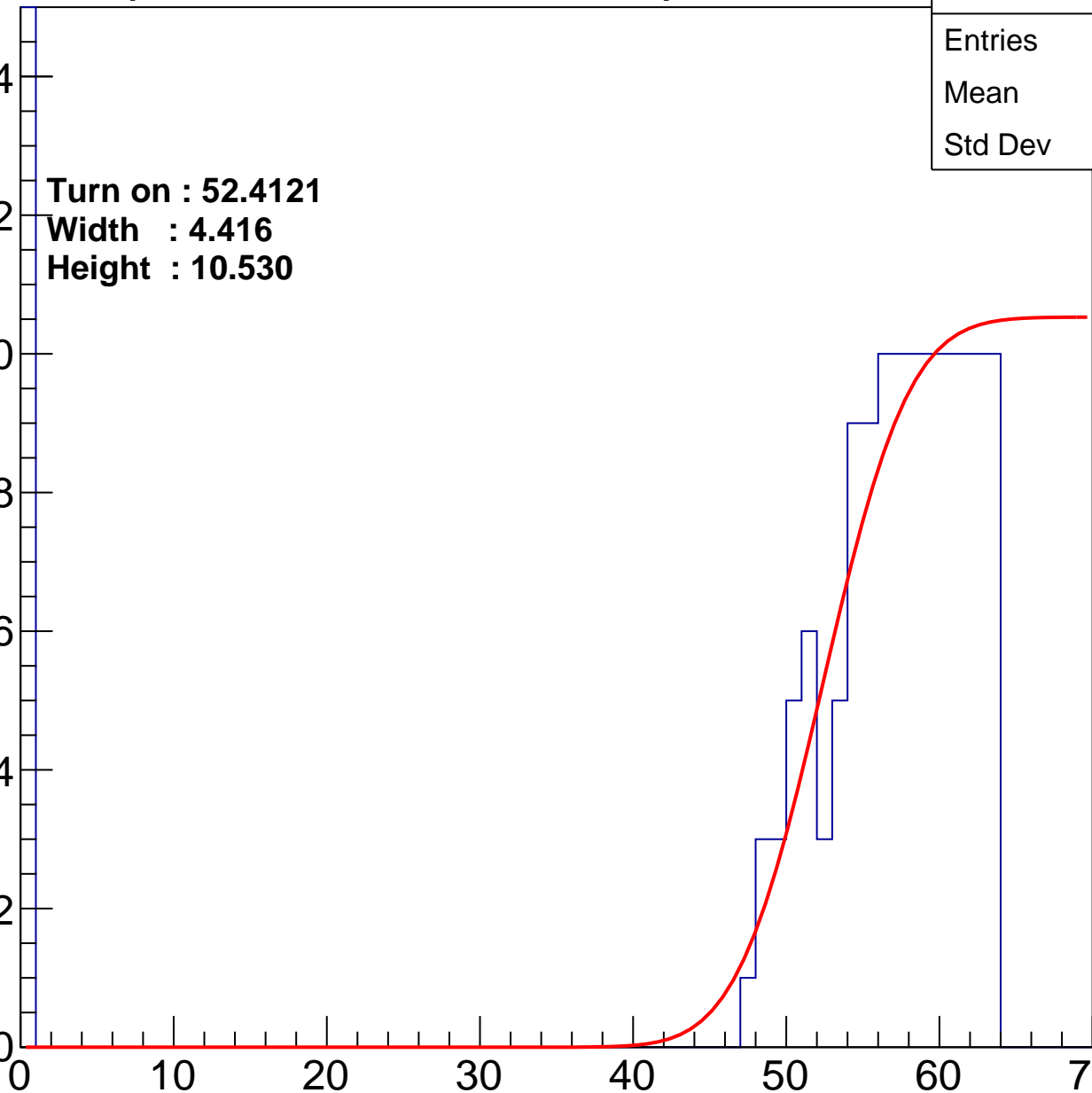
Width : 4.416

Height : 10.530

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	35.21
Std Dev	28.52

Turn on : 54.5916

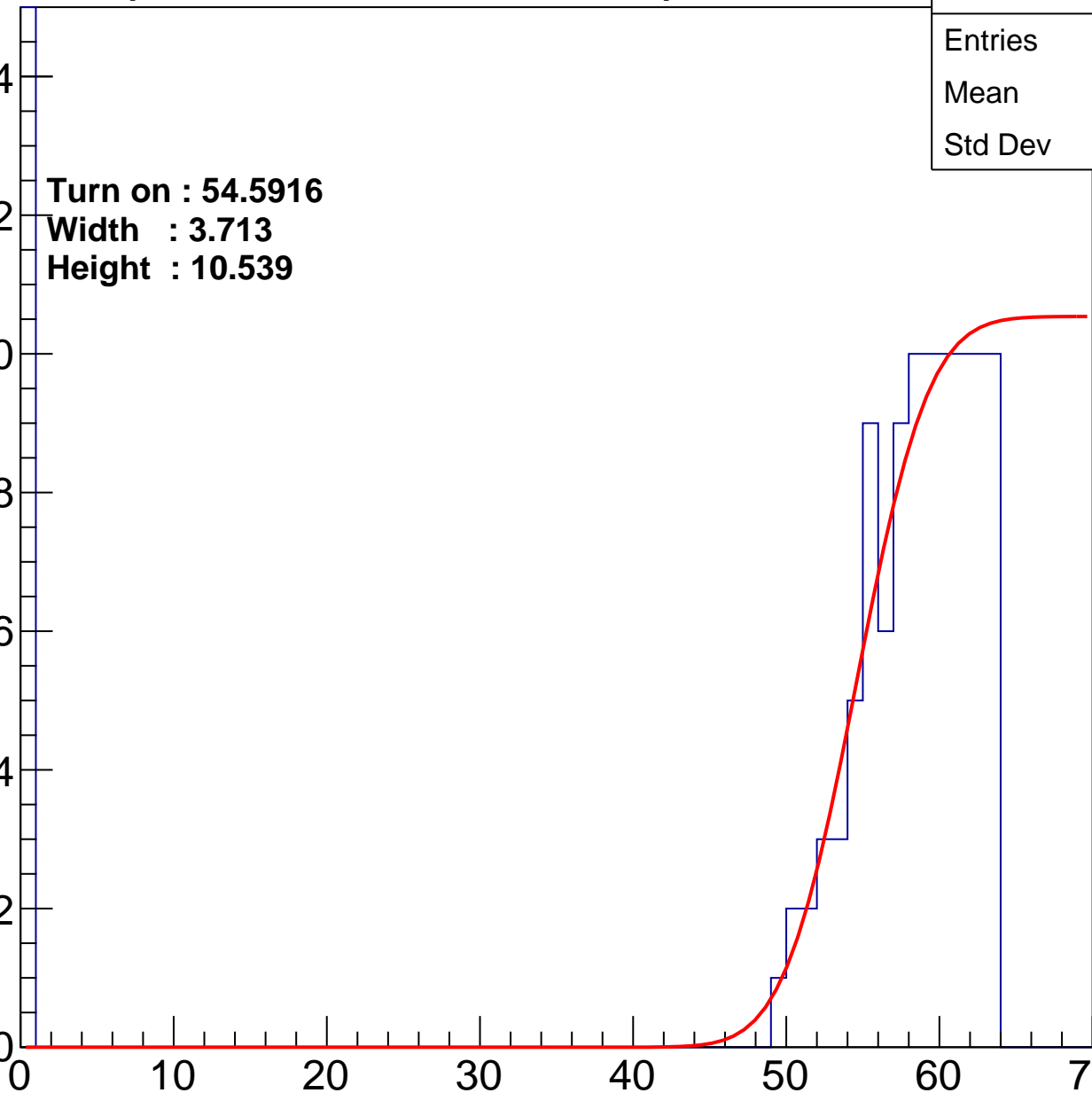
Width : 3.713

Height : 10.539

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	27.2
Std Dev	29.26

Turn on : 56.2047

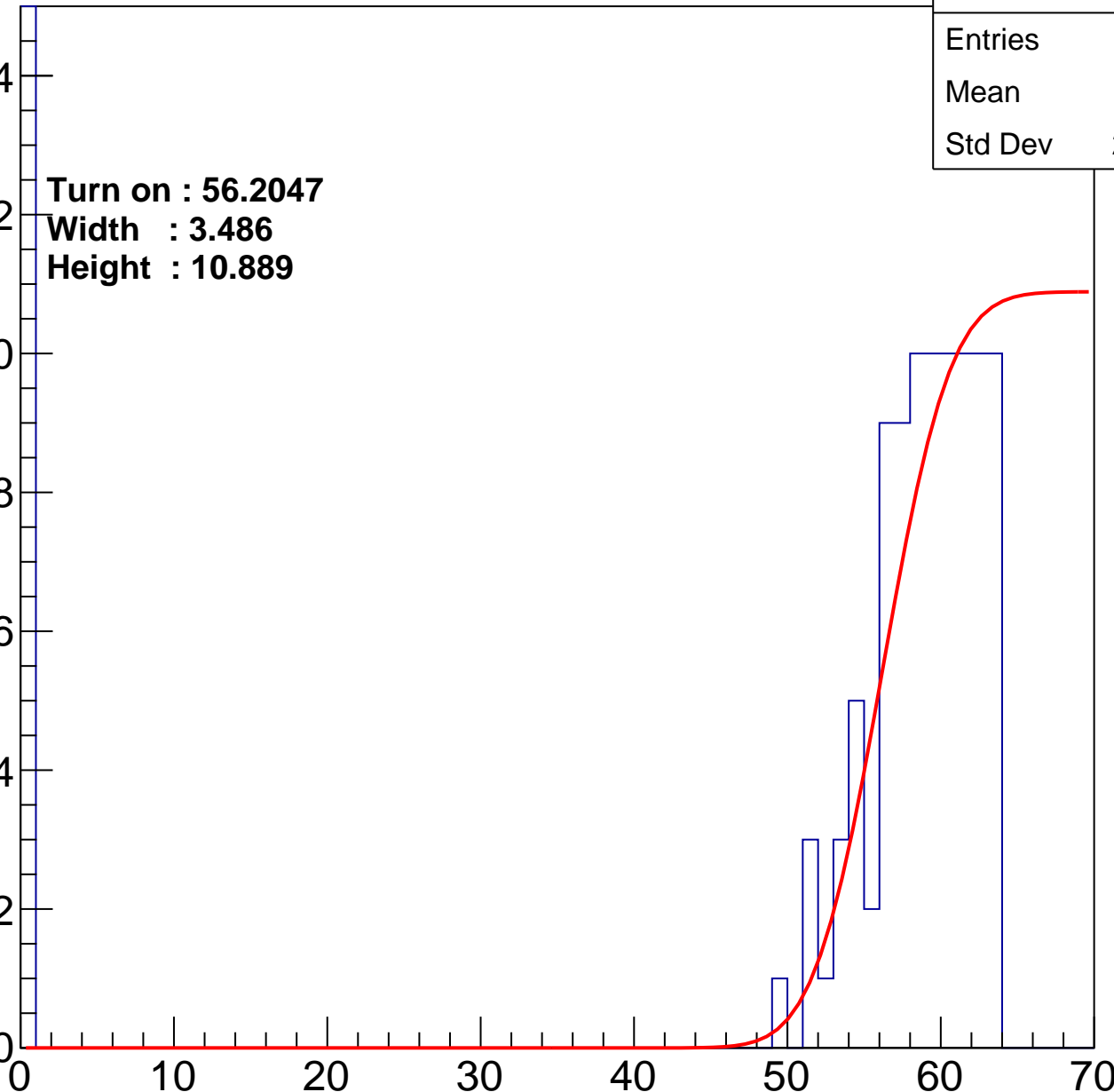
Width : 3.486

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	32.47
Std Dev	28.58

Turn on : 54.0284

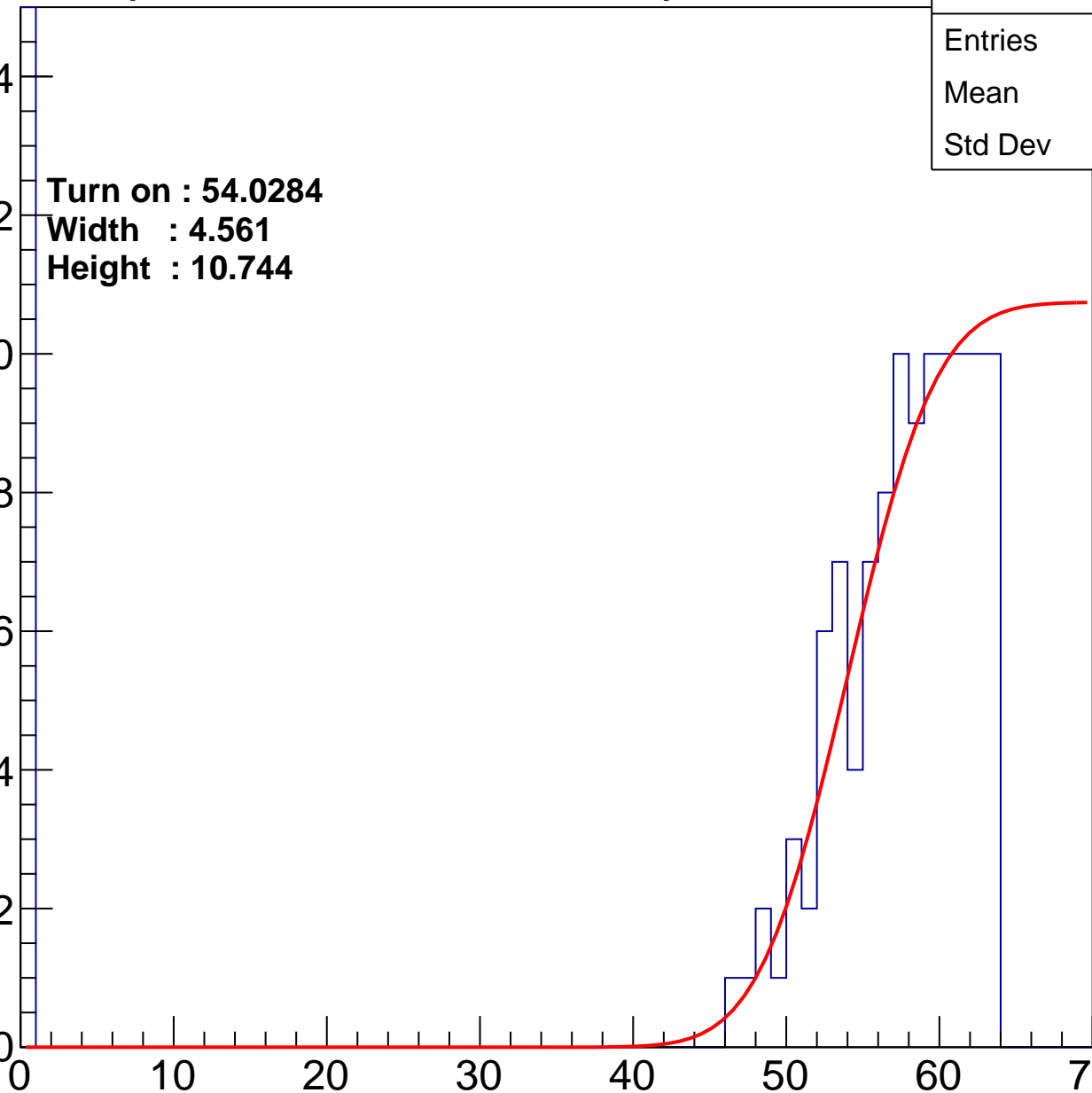
Width : 4.561

Height : 10.744

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	36.38
Std Dev	27.92

Turn on : 53.6723

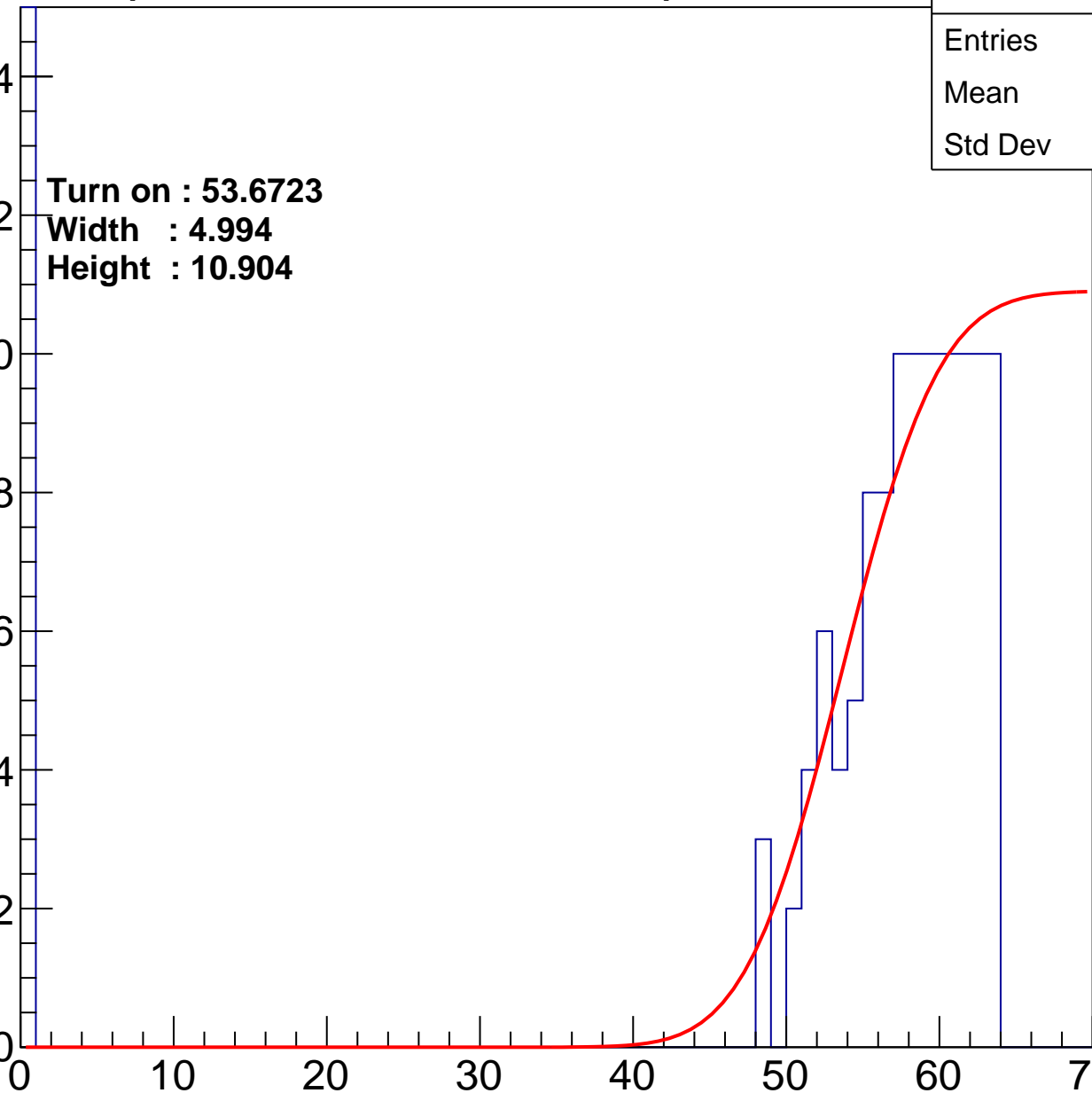
Width : 4.994

Height : 10.904

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	33.46
Std Dev	28.06

Turn on : 51.7110

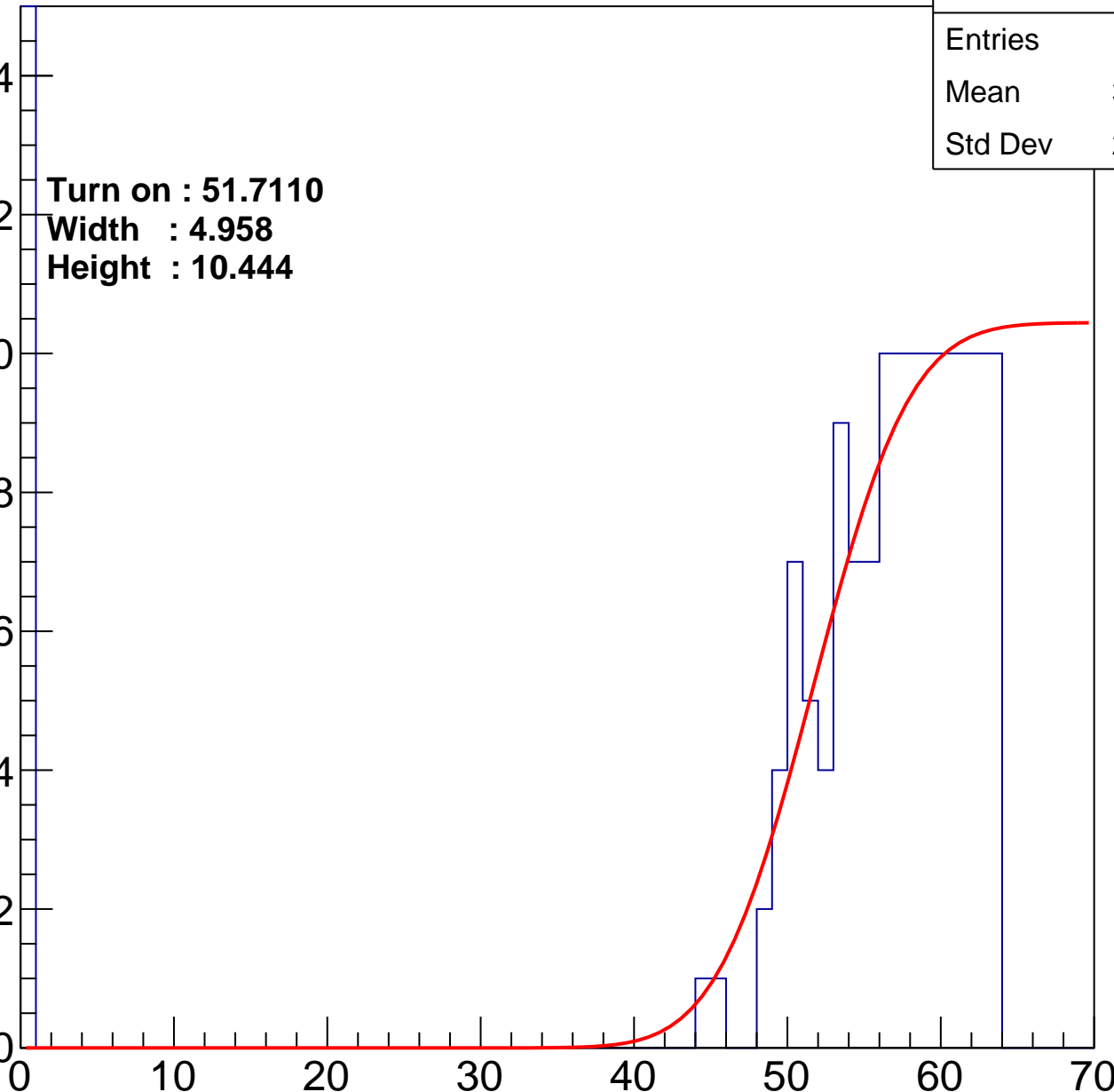
Width : 4.958

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	32.43
Std Dev	28.61

Turn on : 53.6951

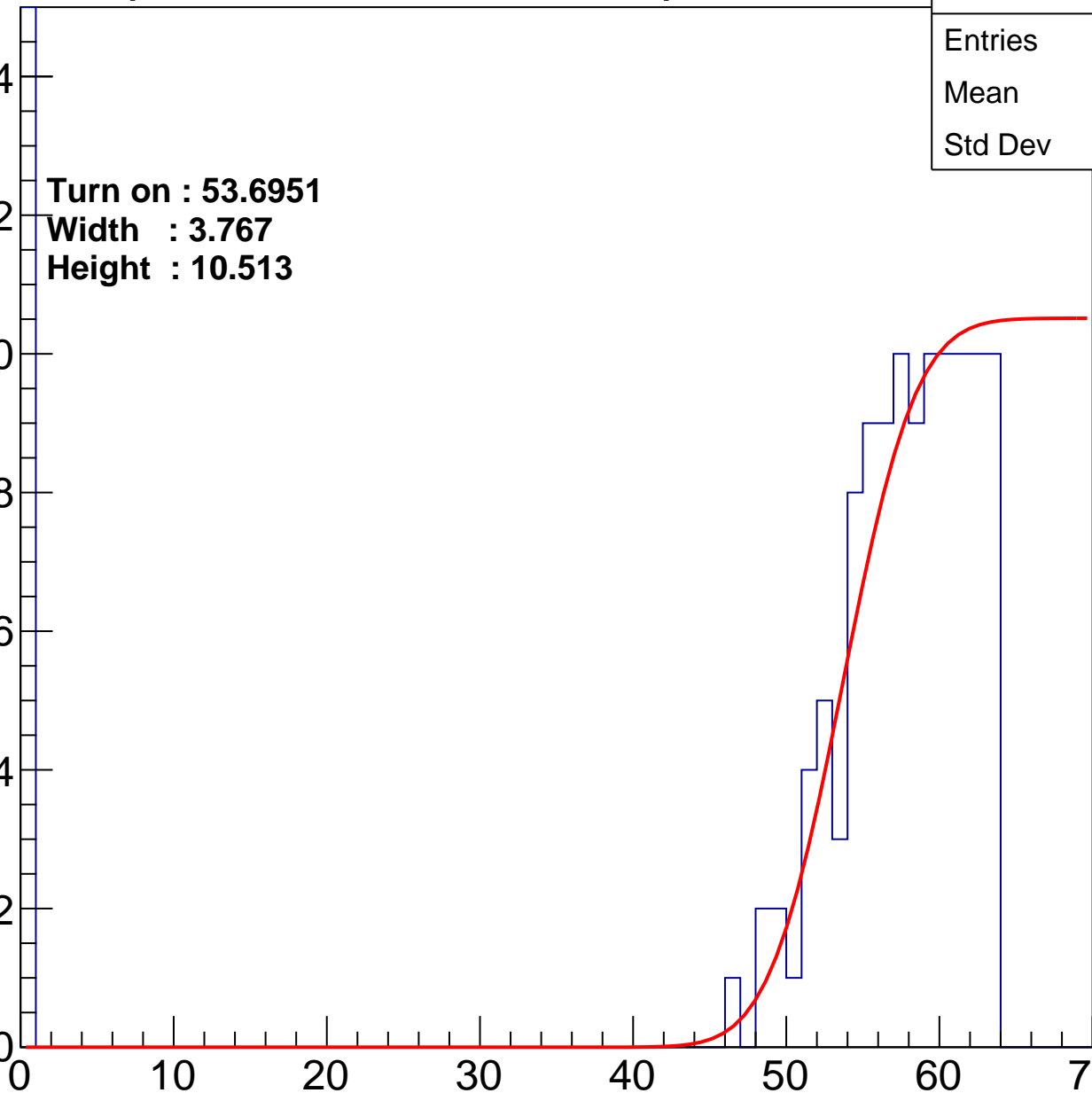
Width : 3.767

Height : 10.513

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	29.89
Std Dev	28.8

Turn on : 54.0864

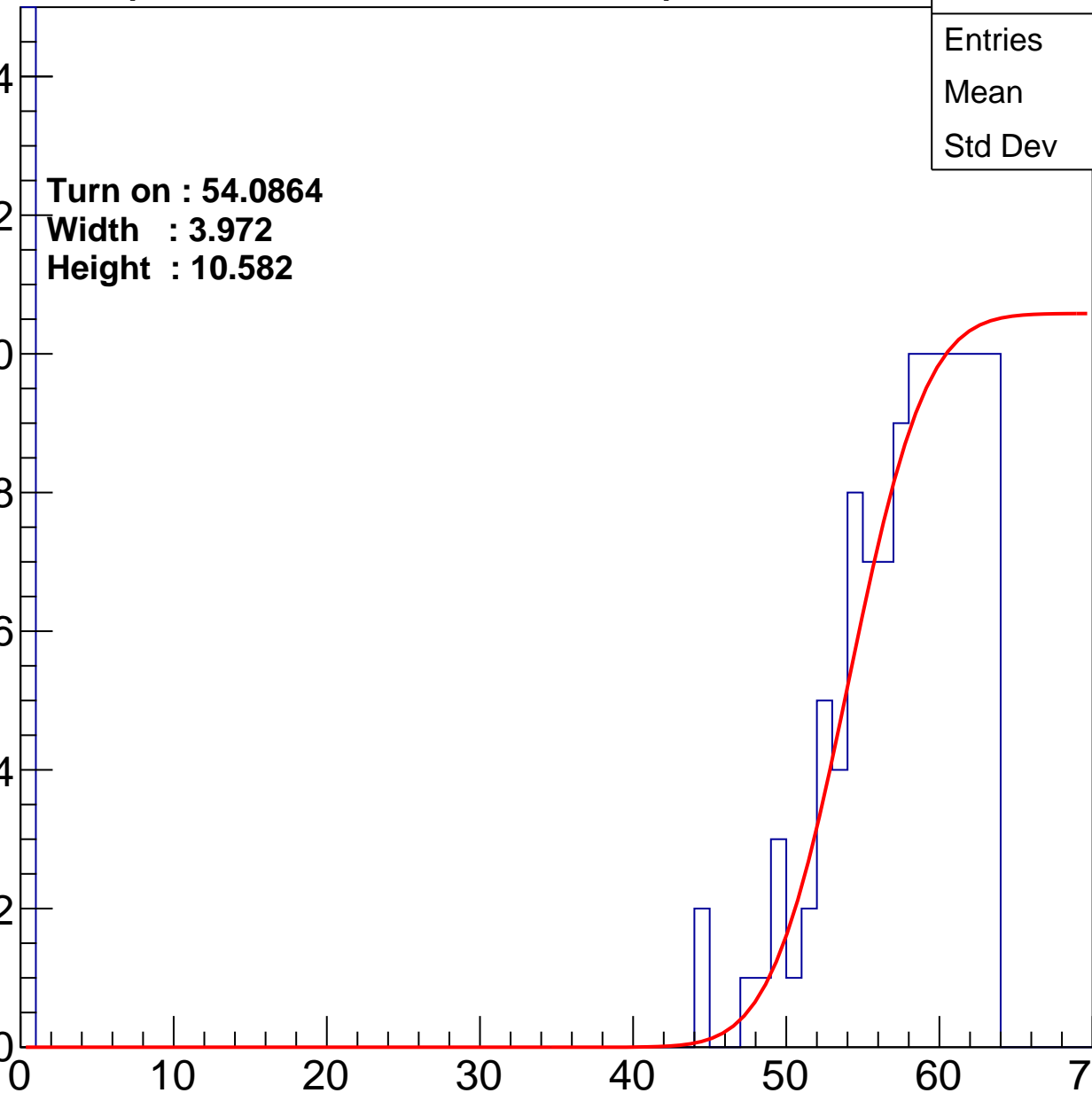
Width : 3.972

Height : 10.582

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	27.83
Std Dev	29.04

Turn on : 54.2220

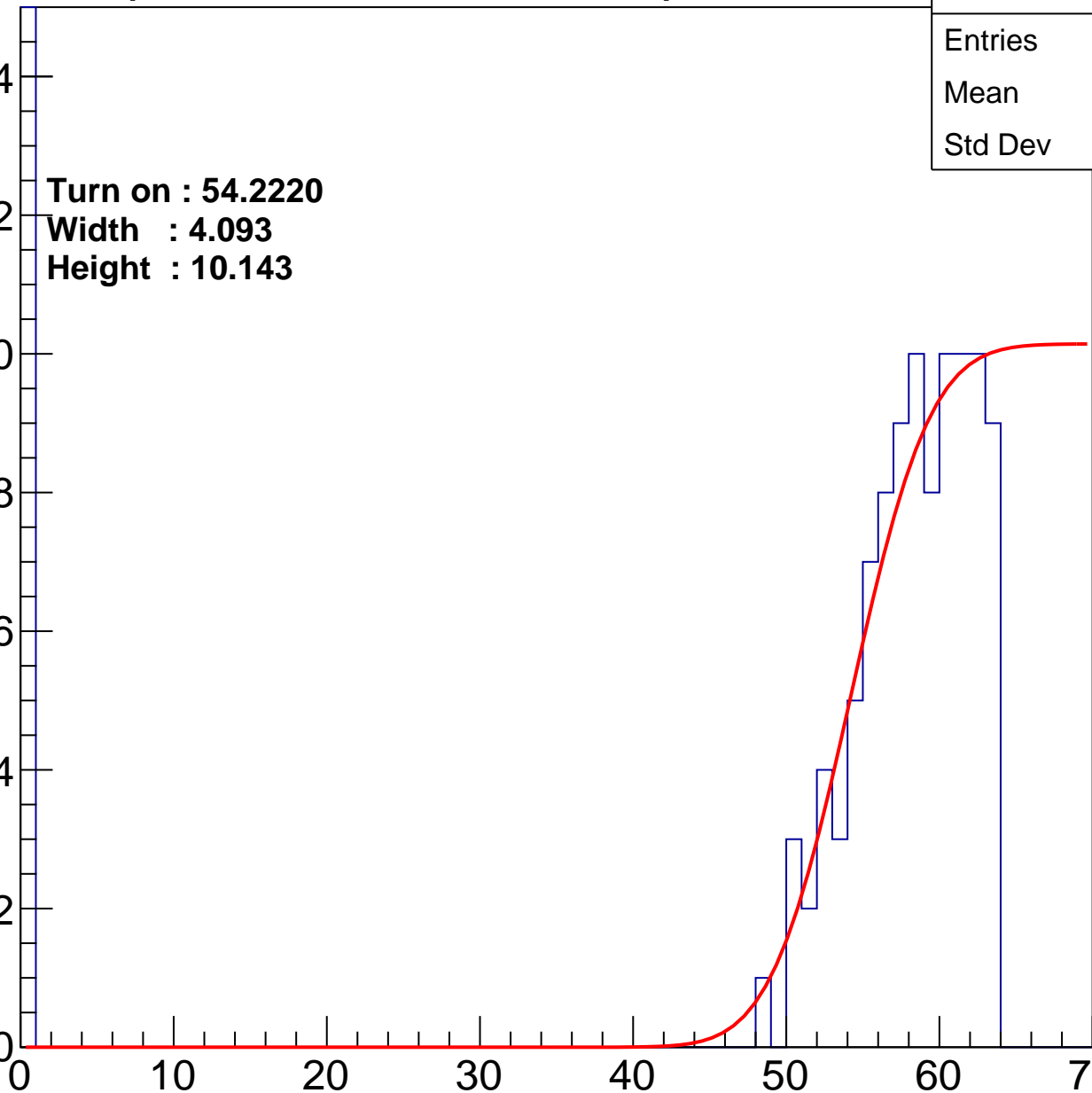
Width : 4.093

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	32.9
Std Dev	28.39

Turn on : 53.8770

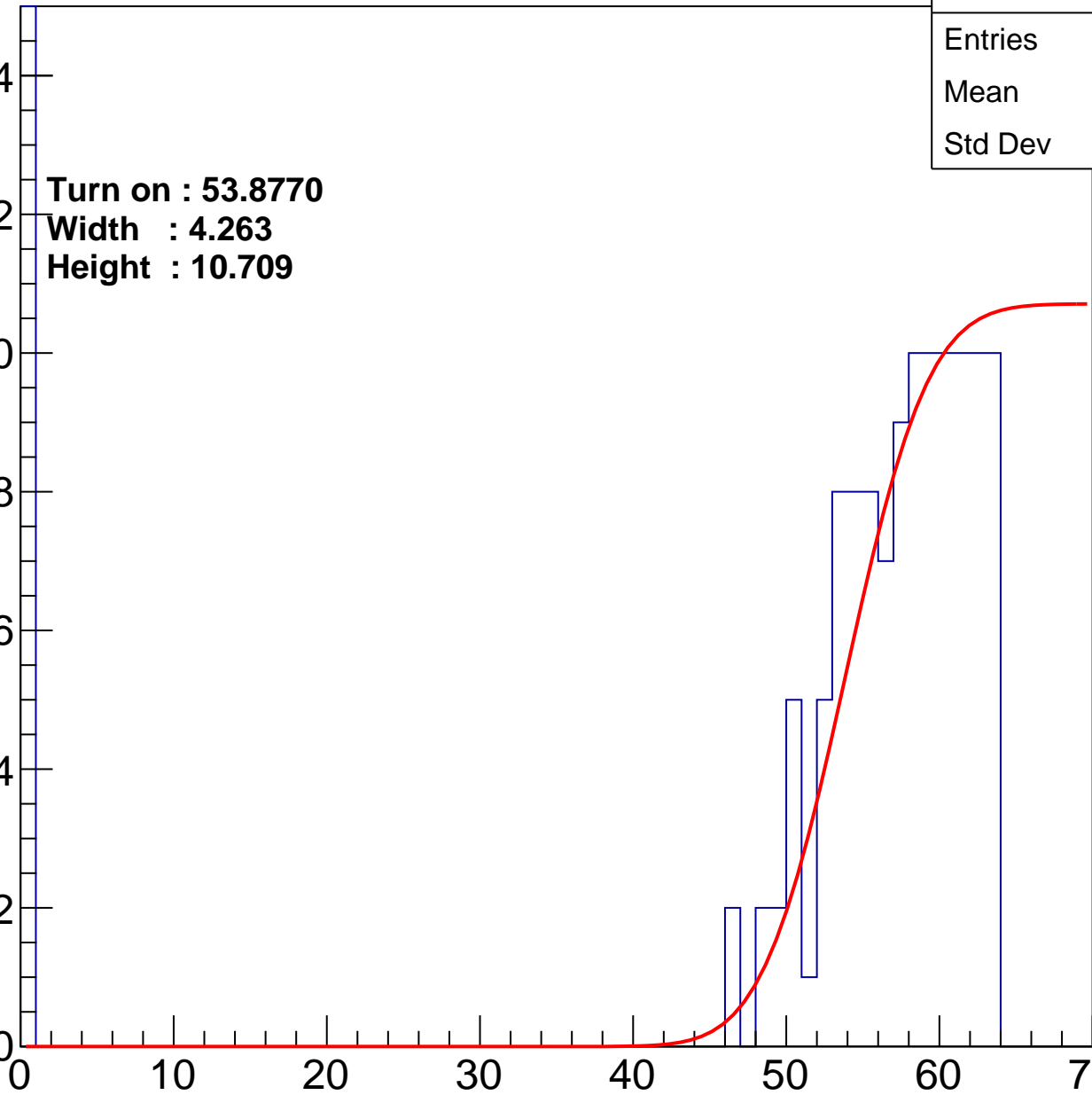
Width : 4.263

Height : 10.709

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	30.56
Std Dev	28.74

Turn on : 53.4662

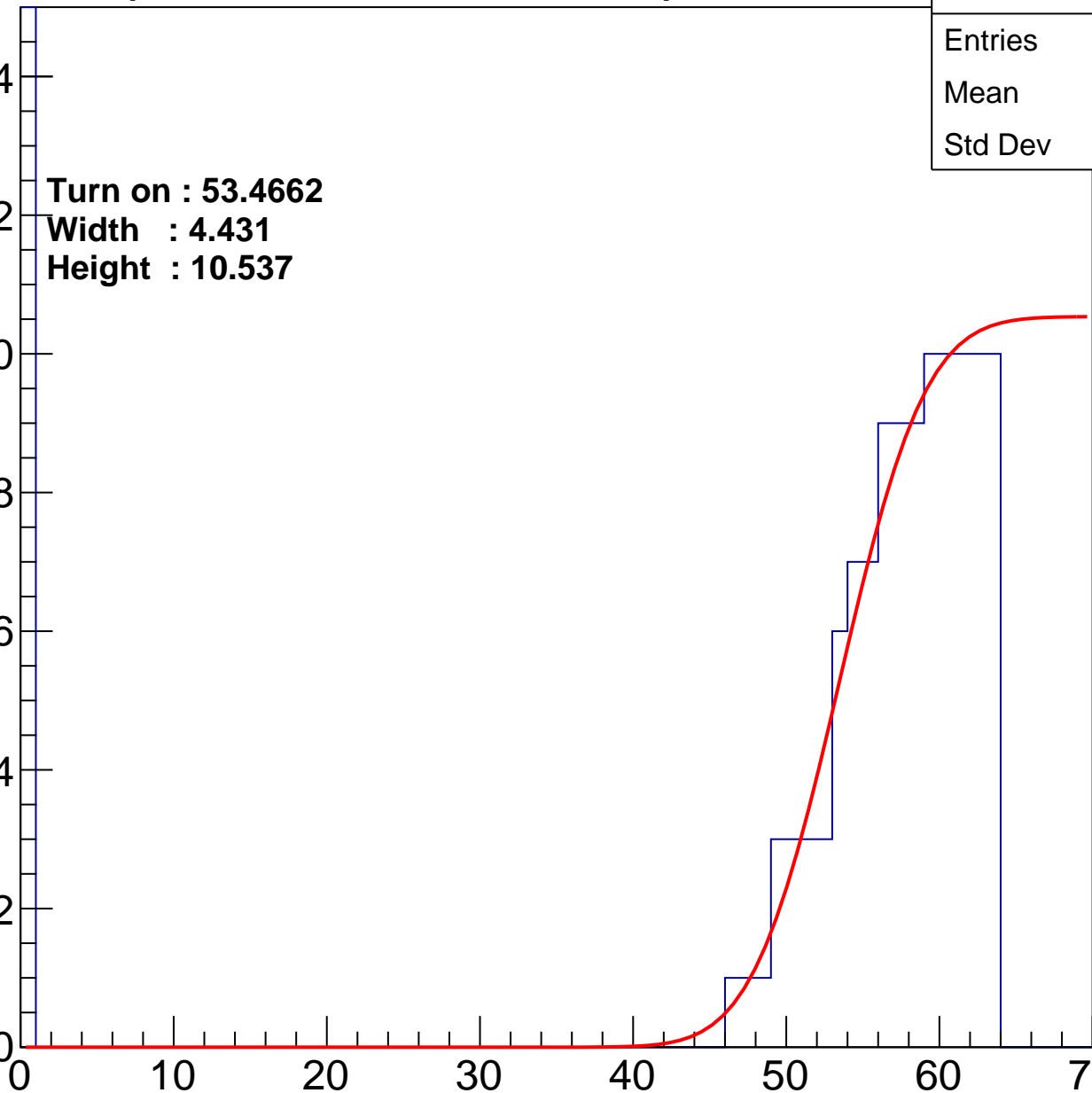
Width : 4.431

Height : 10.537

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	34.91
Std Dev	27.91

Turn on : 53.8699

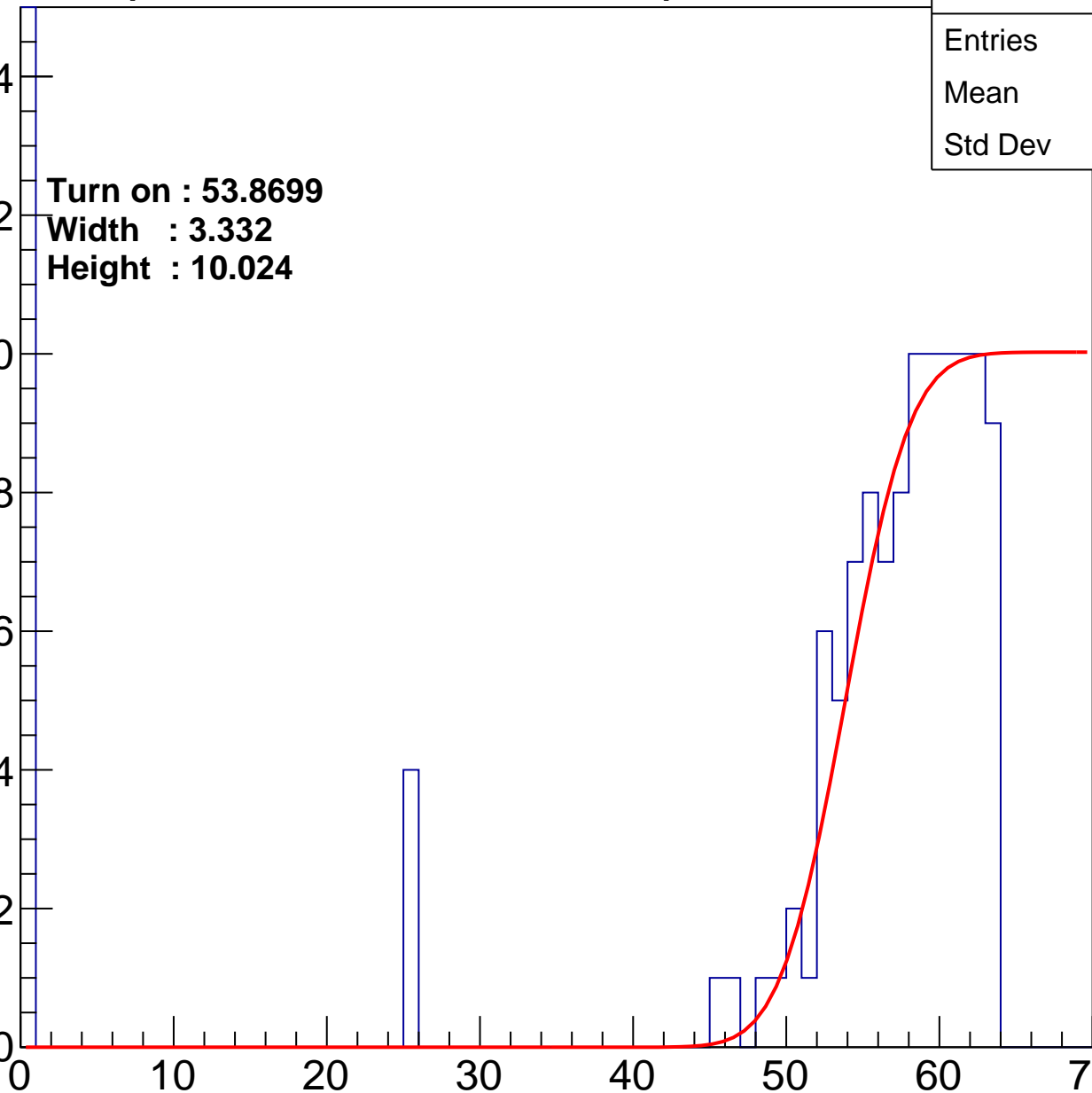
Width : 3.332

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	27.66
Std Dev	29.22

Turn on : 55.6283

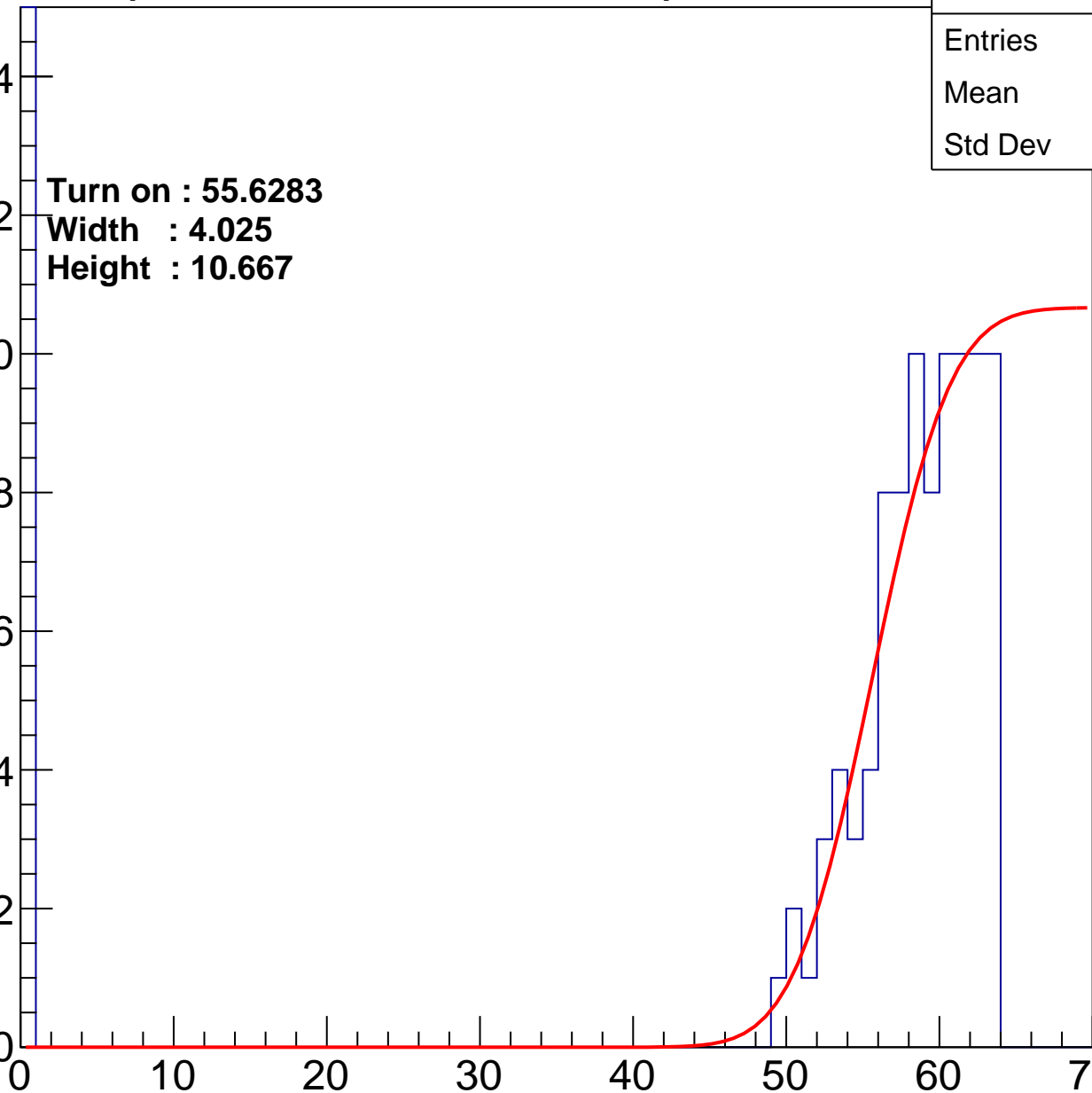
Width : 4.025

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	30.06
Std Dev	28.8

Turn on : 53.0255

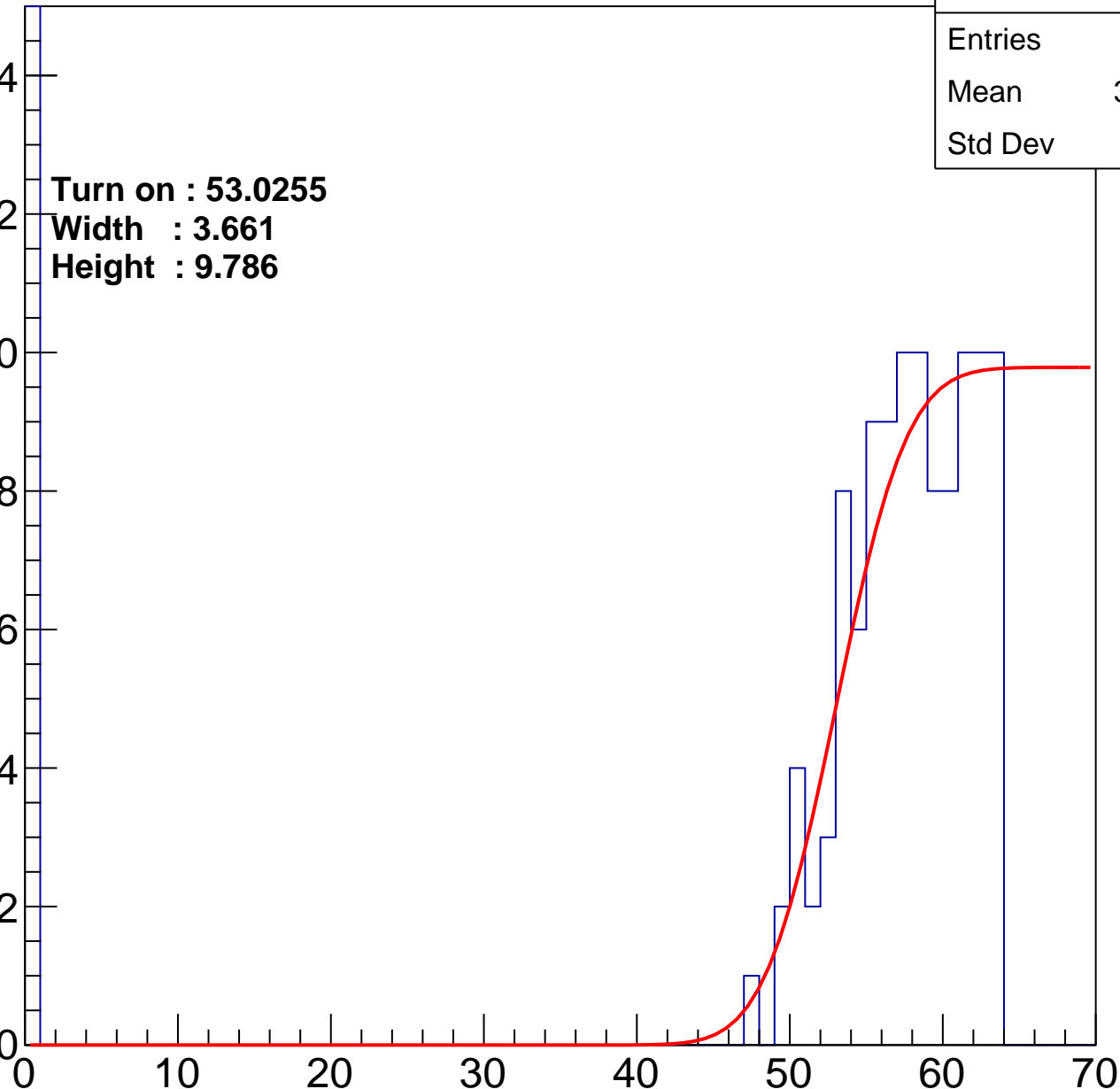
Width : 3.661

Height : 9.786

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	38.51
Std Dev	26.97

Turn on : 52.2922

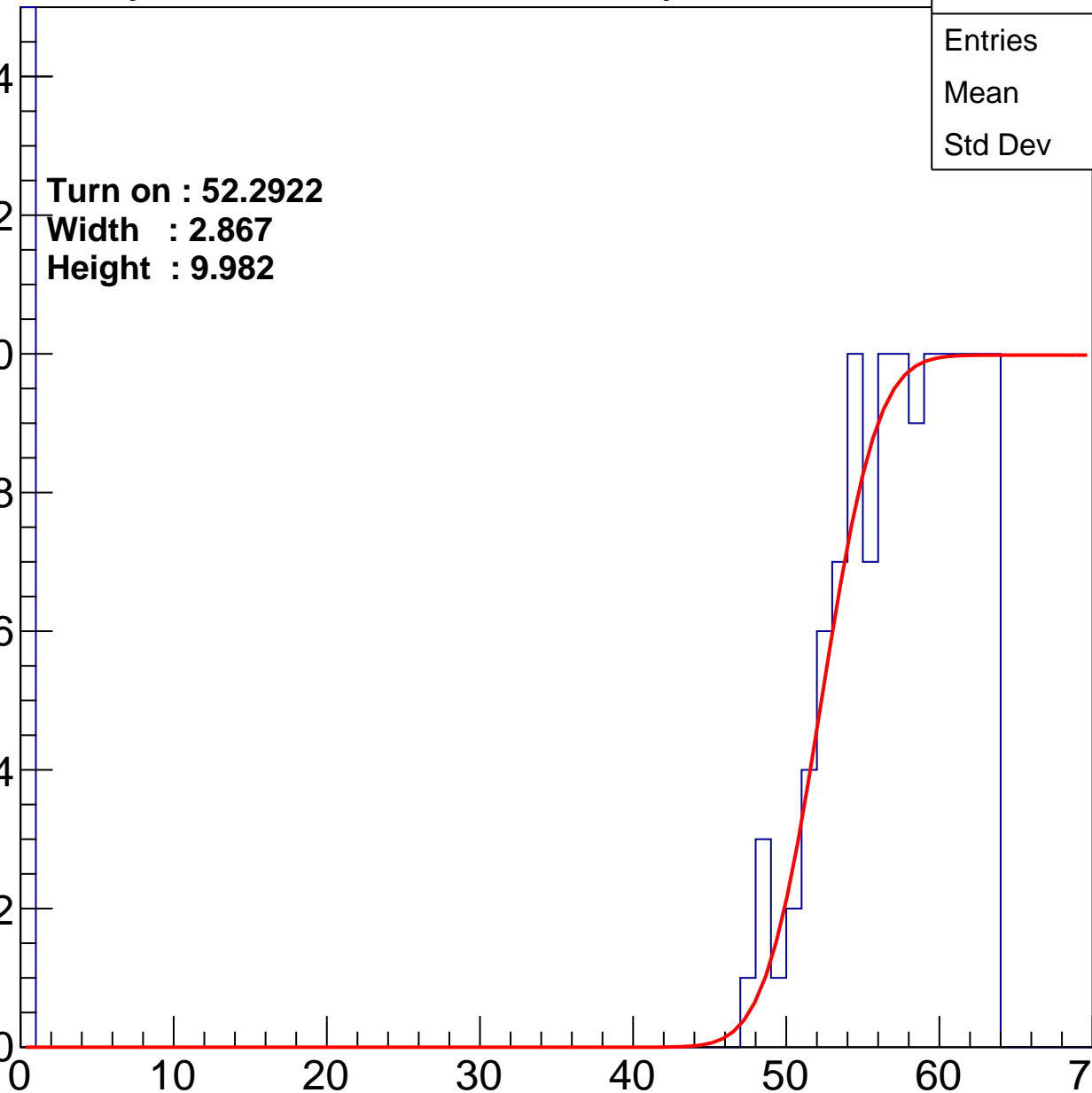
Width : 2.867

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	34.24
Std Dev	28.25

Turn on : 52.8309

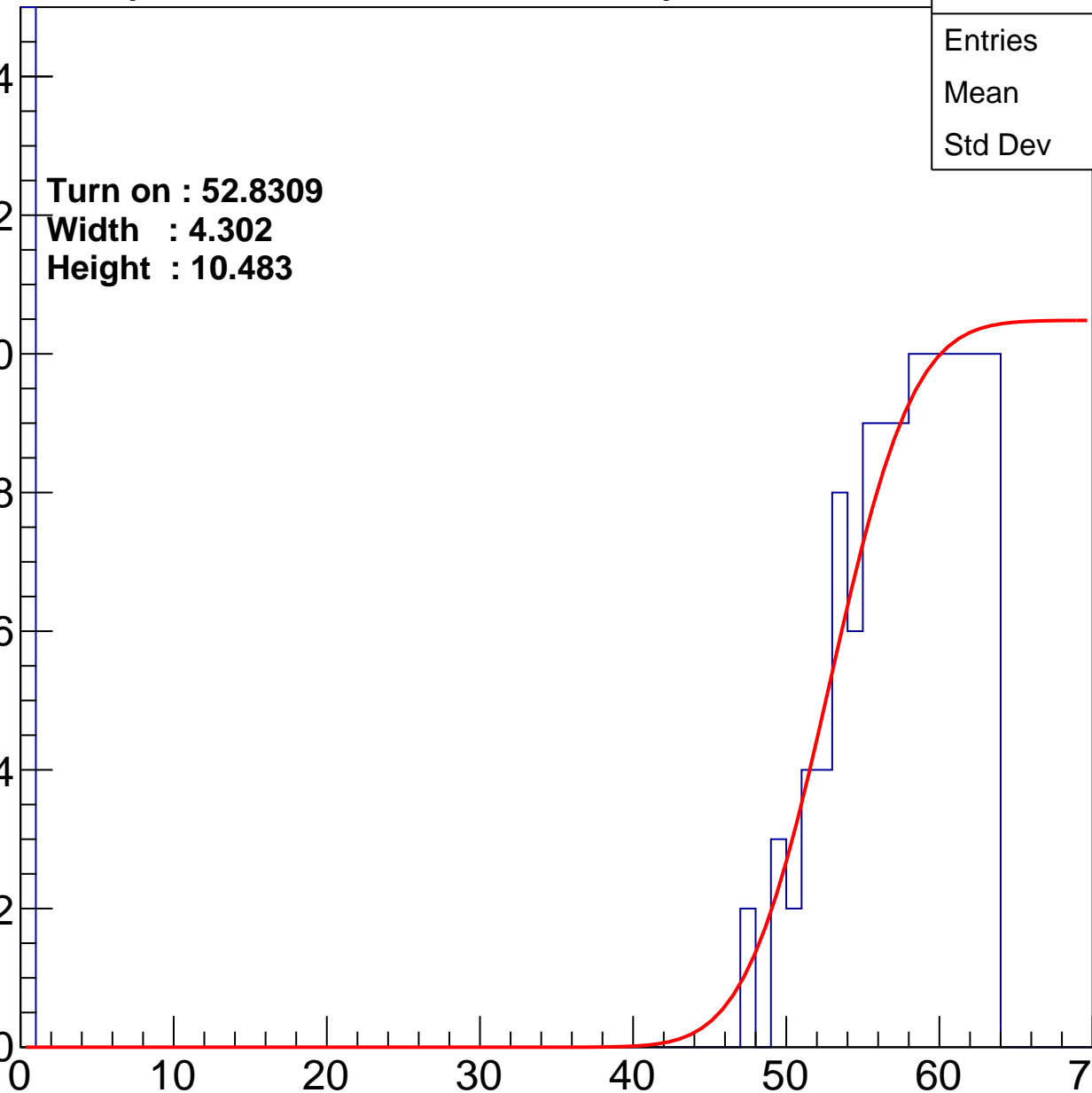
Width : 4.302

Height : 10.483

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	31.23
Std Dev	28.74

Turn on : 53.3131

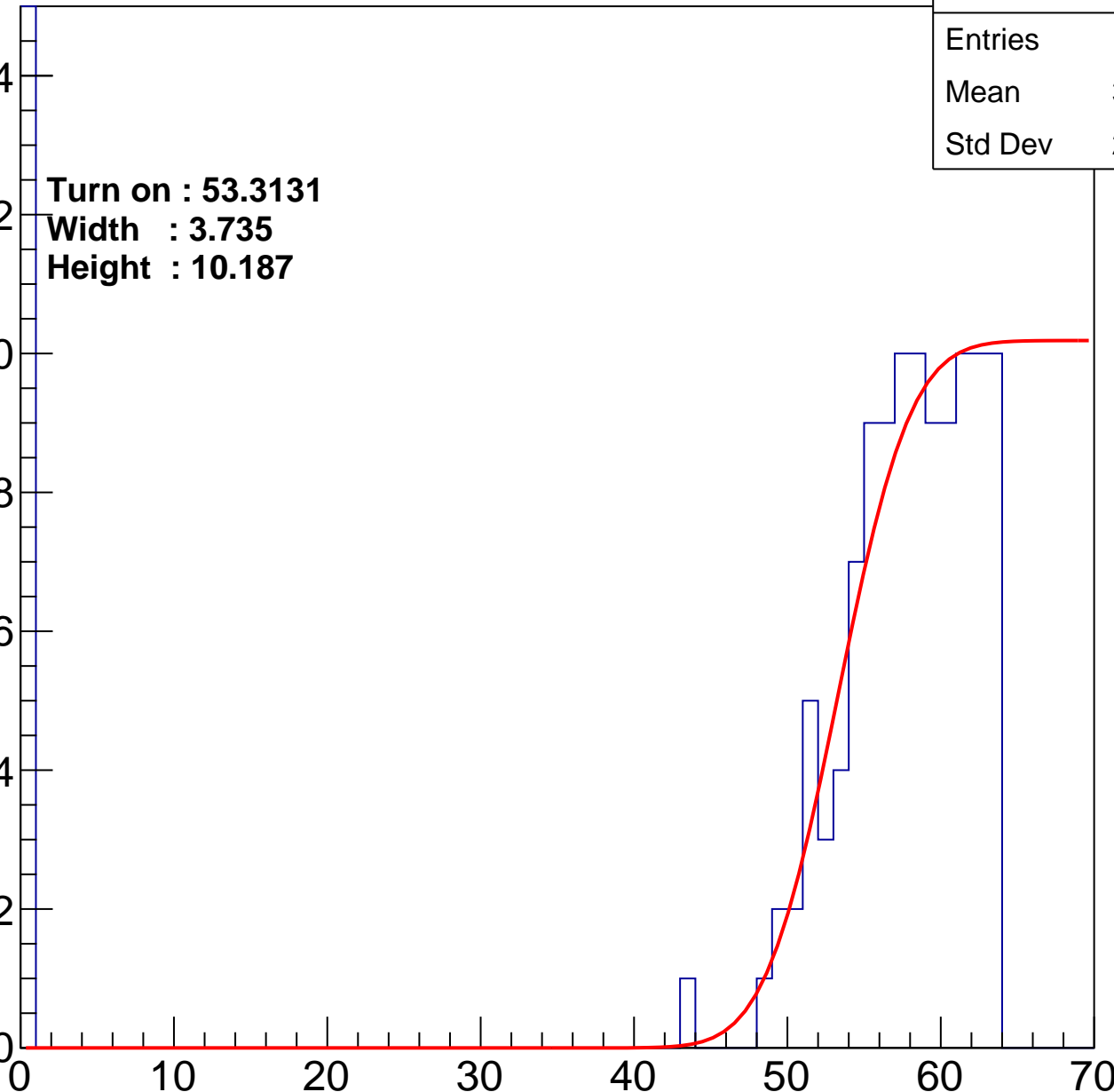
Width : 3.735

Height : 10.187

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	30.95
Std Dev	28.85

Turn on : 53.9472

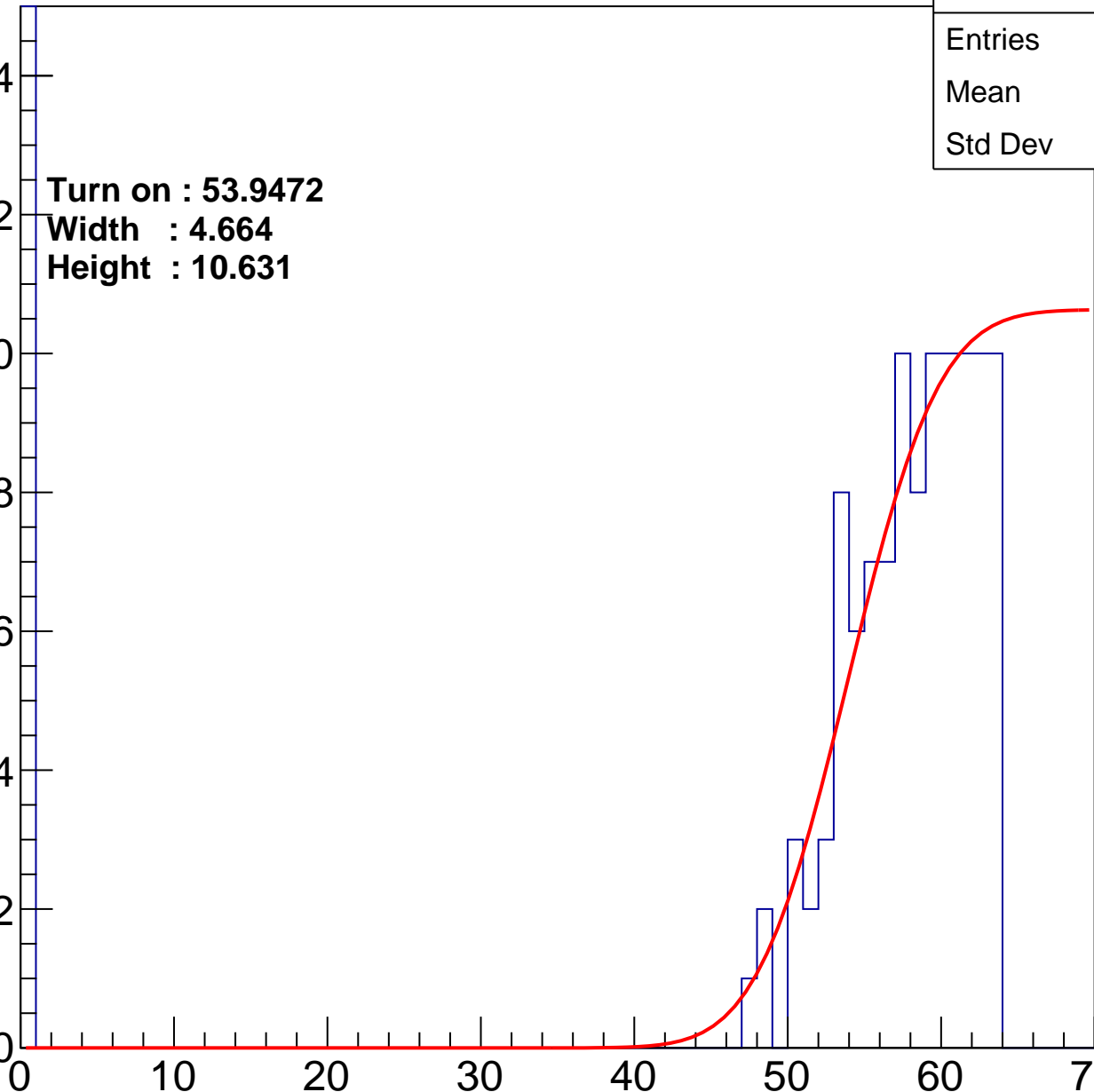
Width : 4.664

Height : 10.631

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	35.41
Std Dev	27.92

Turn on : 52.3759

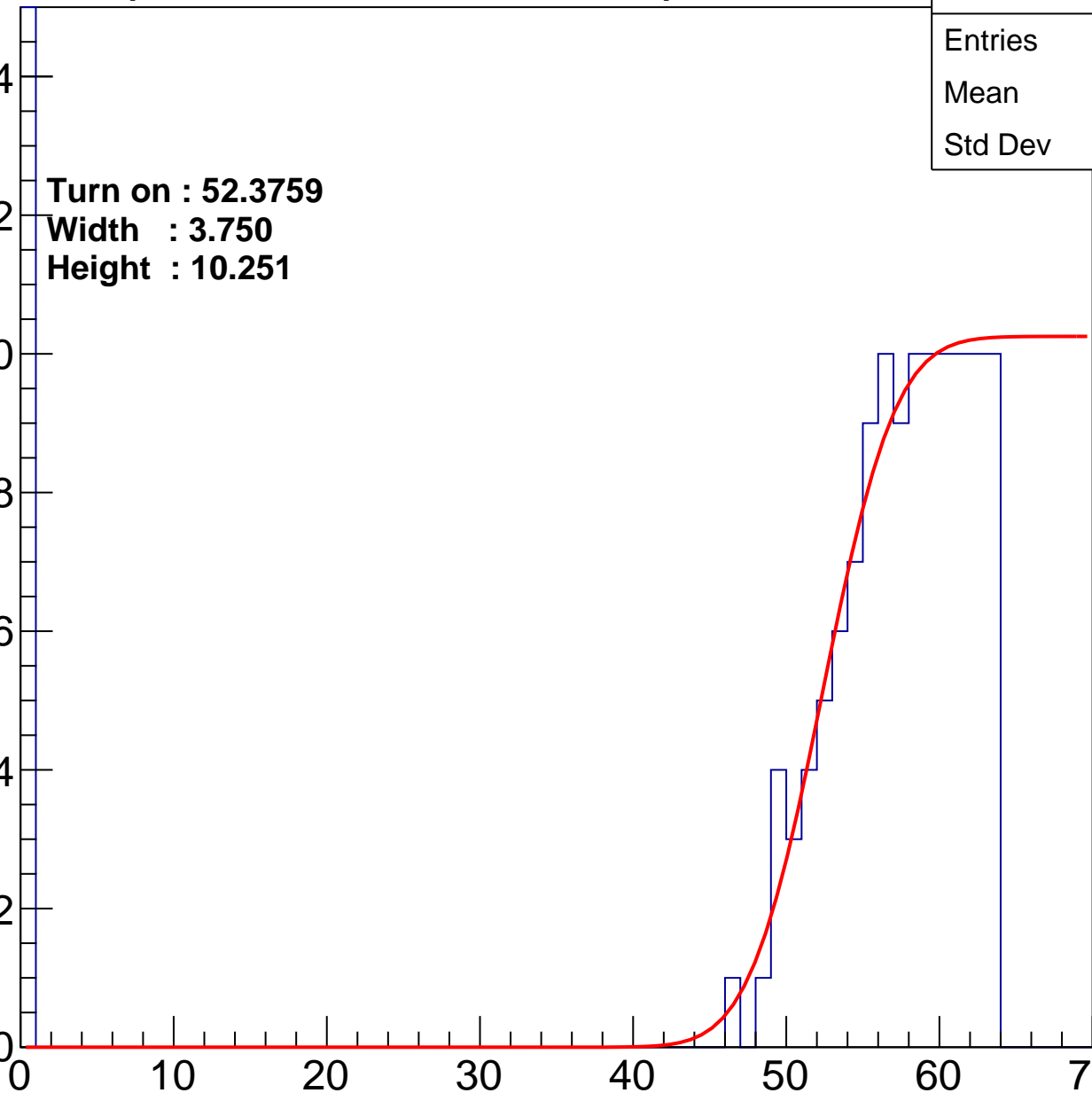
Width : 3.750

Height : 10.251

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U15-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	35.41
Std Dev	27.92

Turn on : 52.3759

Width : 3.750

Height : 10.251

Entry

14
12
10
8
6
4
2
0

ampl

