

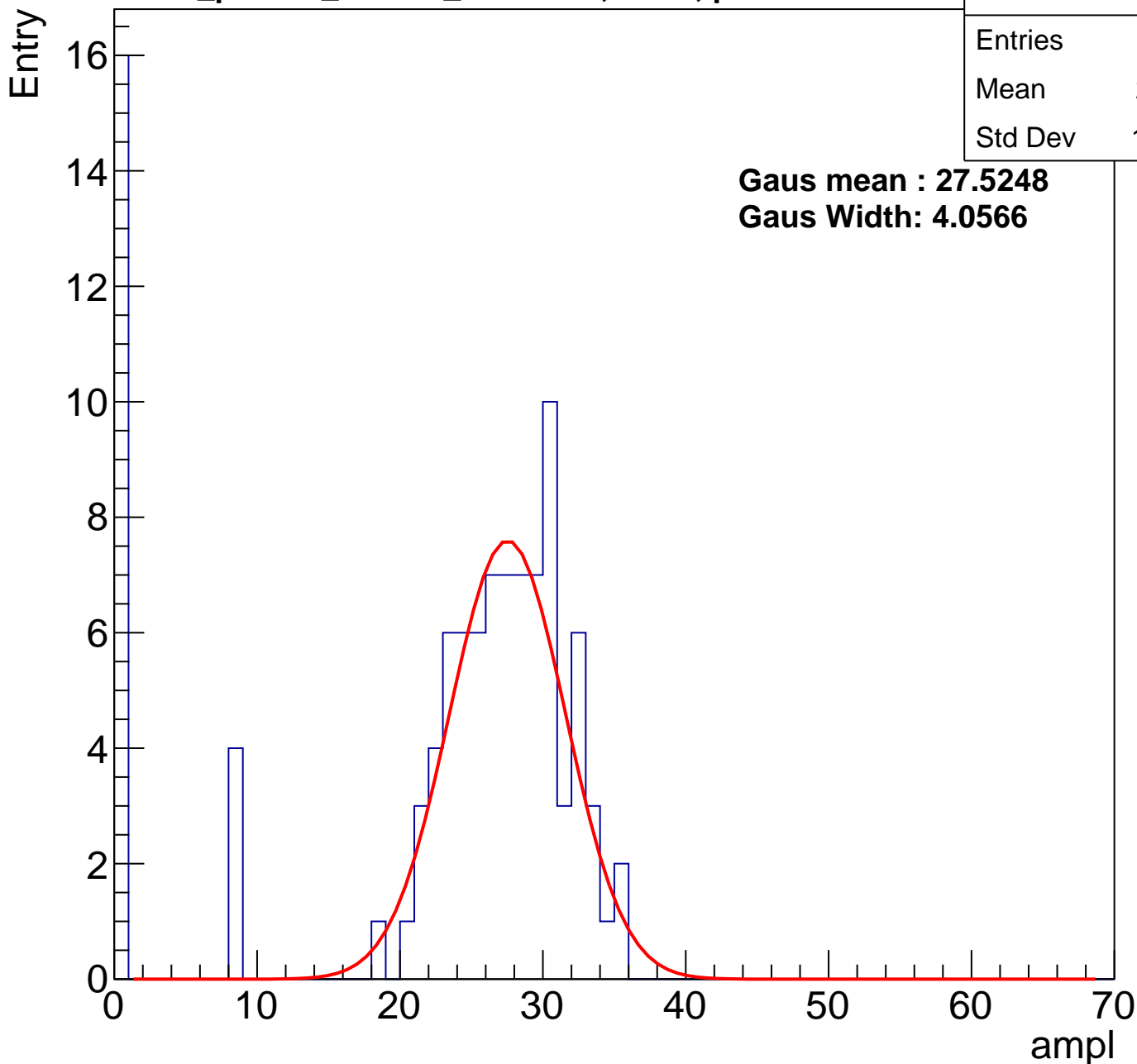
B1L103S, U8-ch0, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	22.11
Std Dev	10.88

Gaus mean : 27.5248

Gaus Width: 4.0566



B1L103S, U8-ch0, adc1

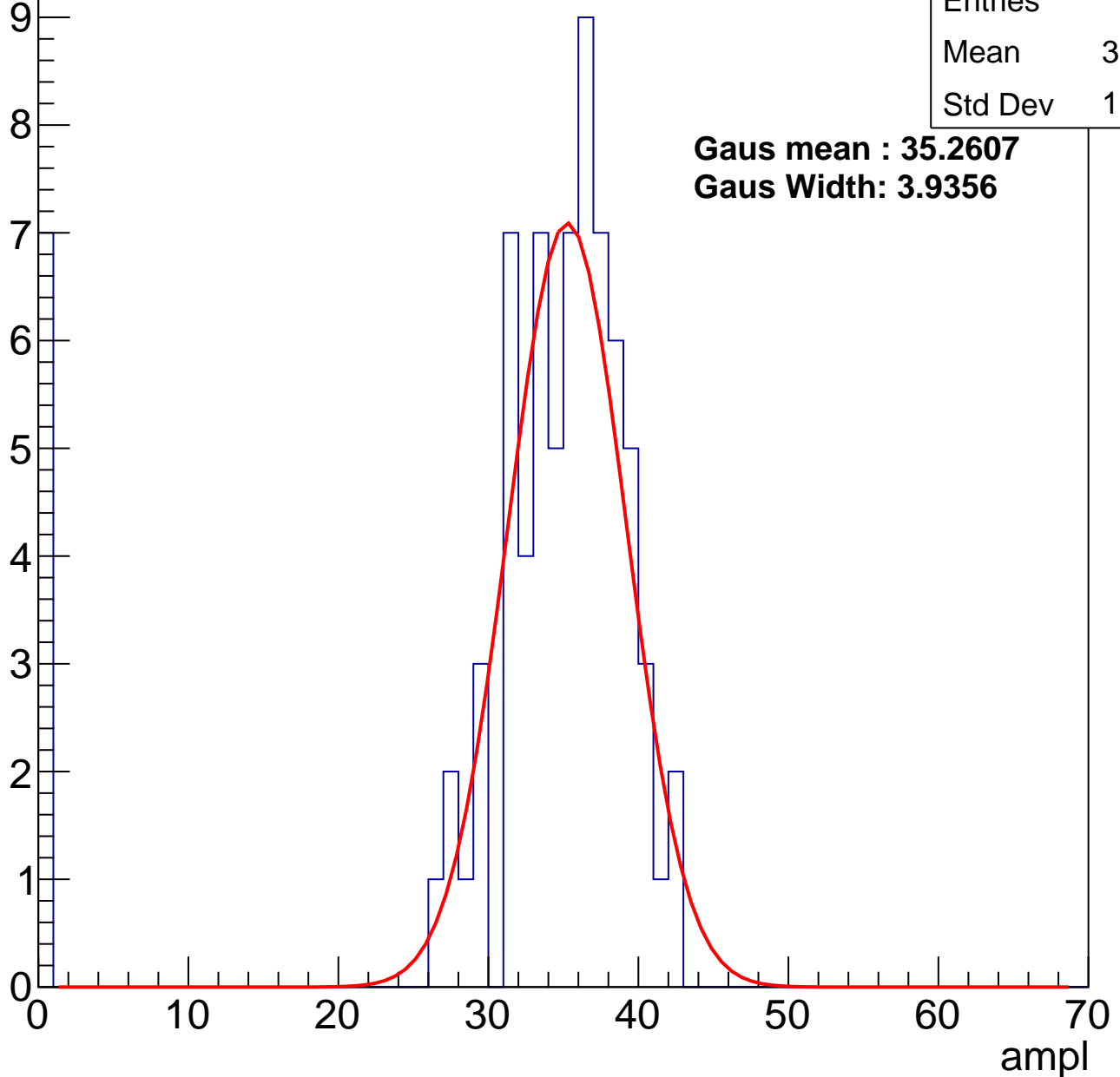
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	31.65
Std Dev	10.59

Gaus mean : 35.2607

Gaus Width: 3.9356



B1L103S, U8-ch0, adc2

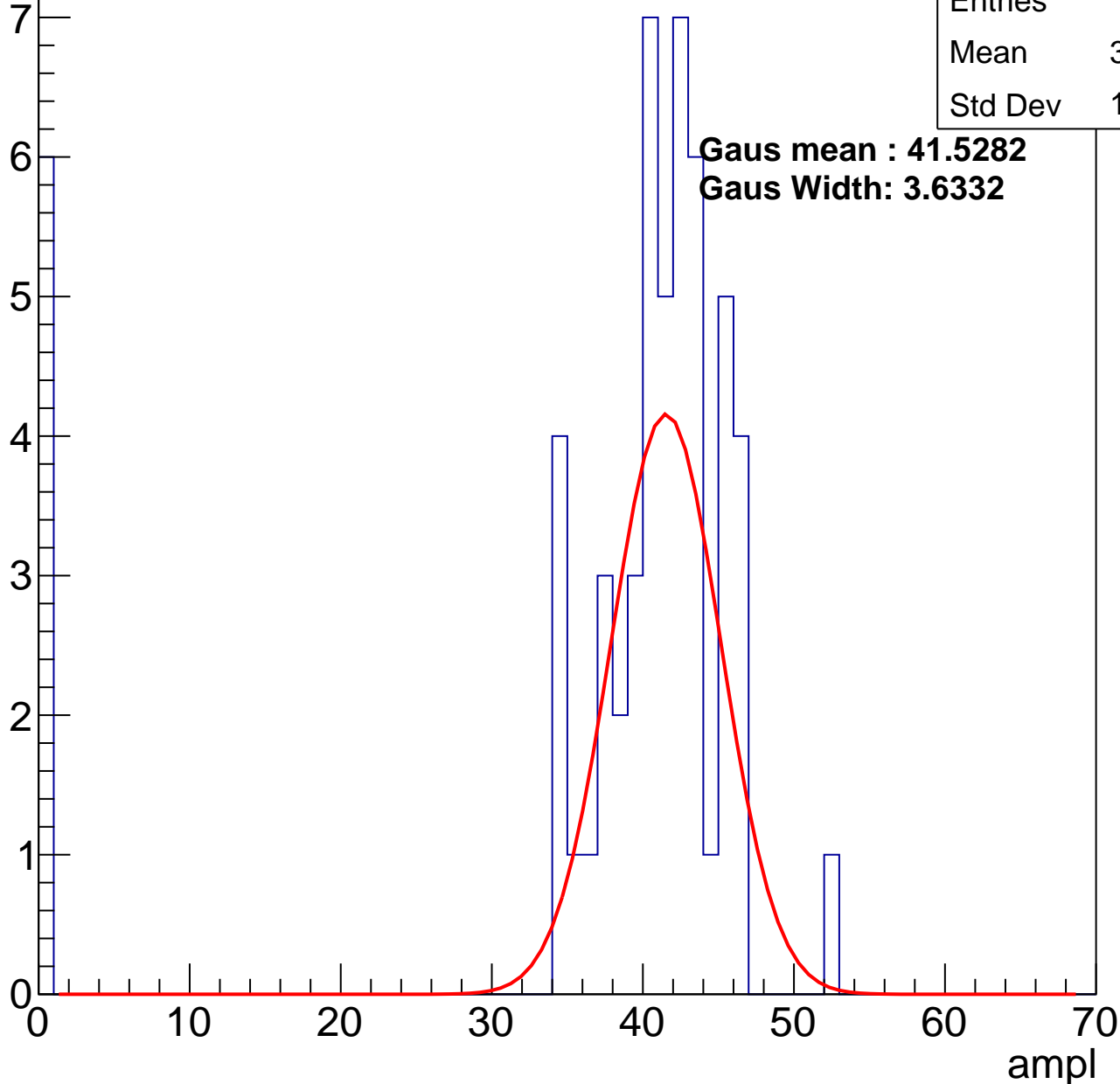
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	36.66
Std Dev	13.17

Gaus mean : 41.5282

Gaus Width: 3.6332

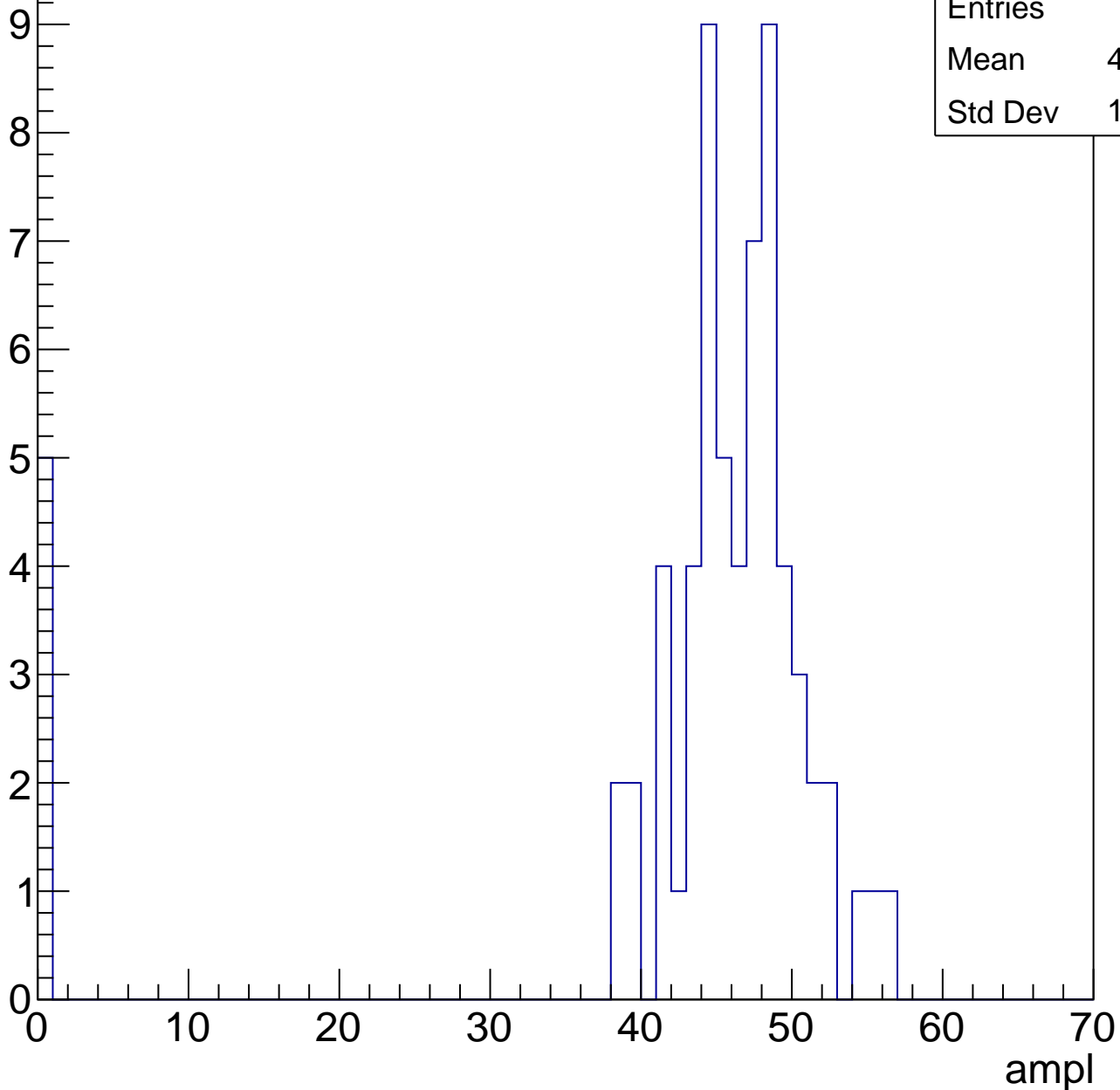


B1L103S, U8-ch0, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.65
Std Dev	12.76

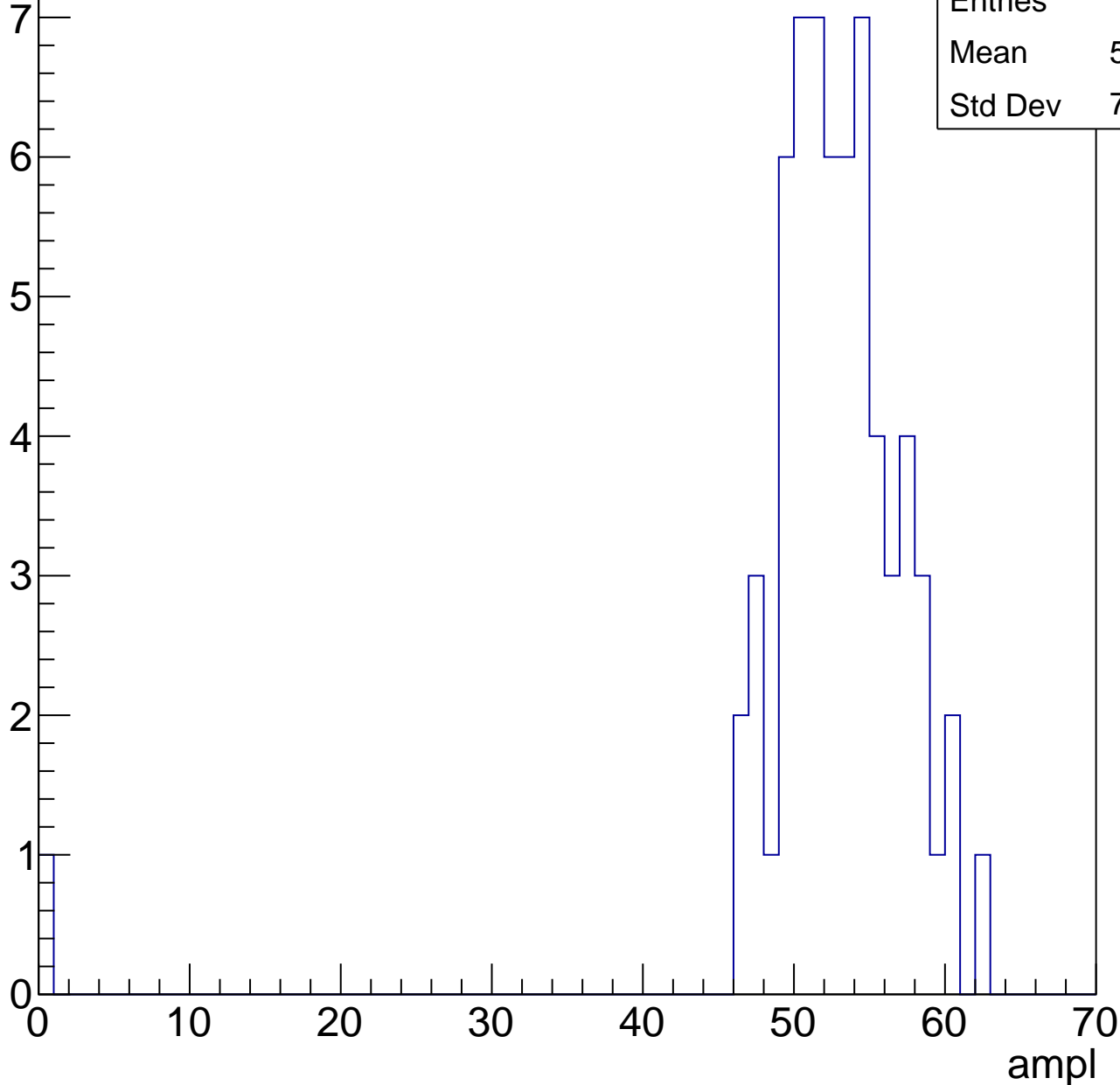


B1L103S, U8-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	51.89
Std Dev	7.456

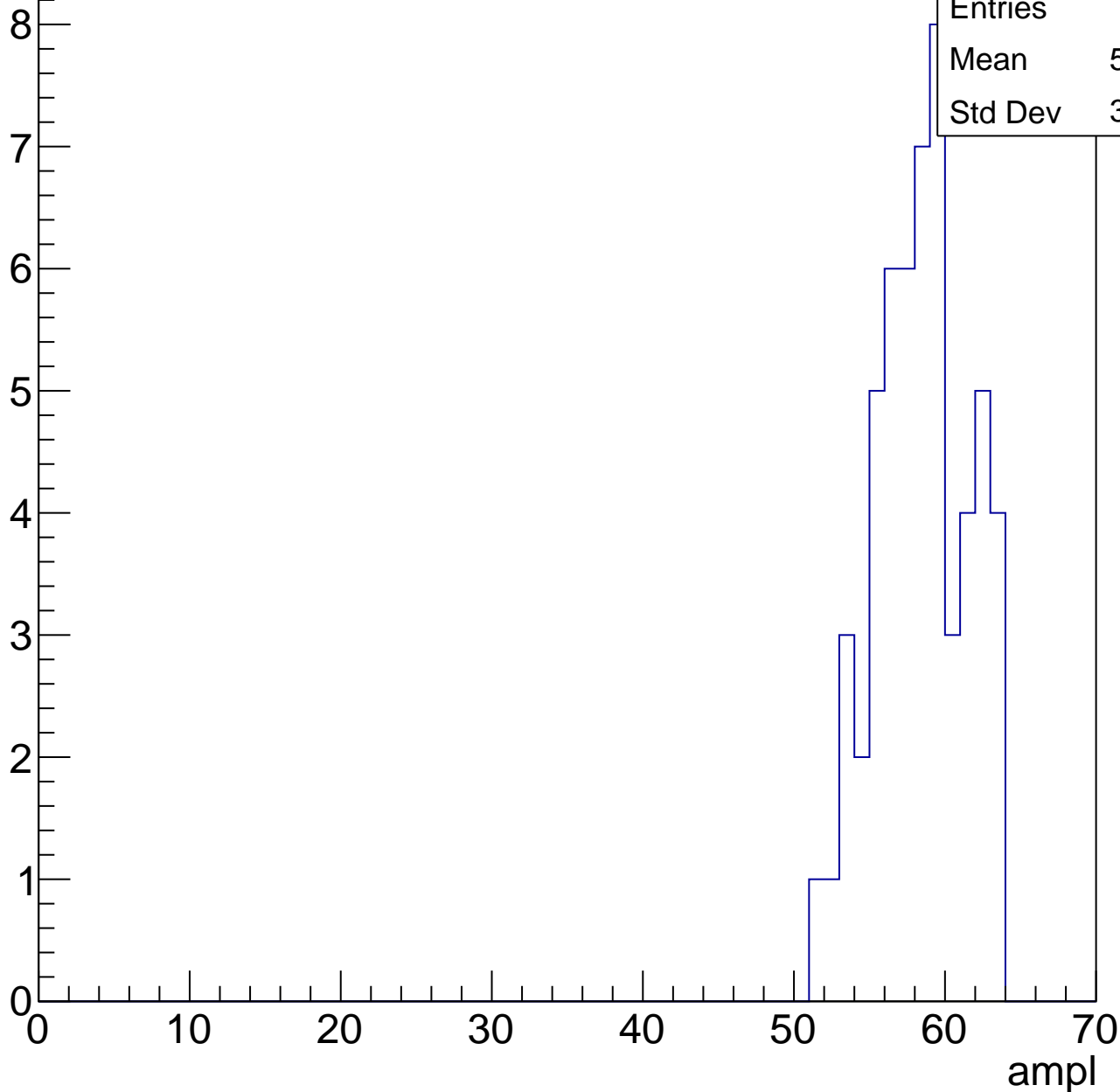


B1L103S, U8-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.95
Std Dev	3.024

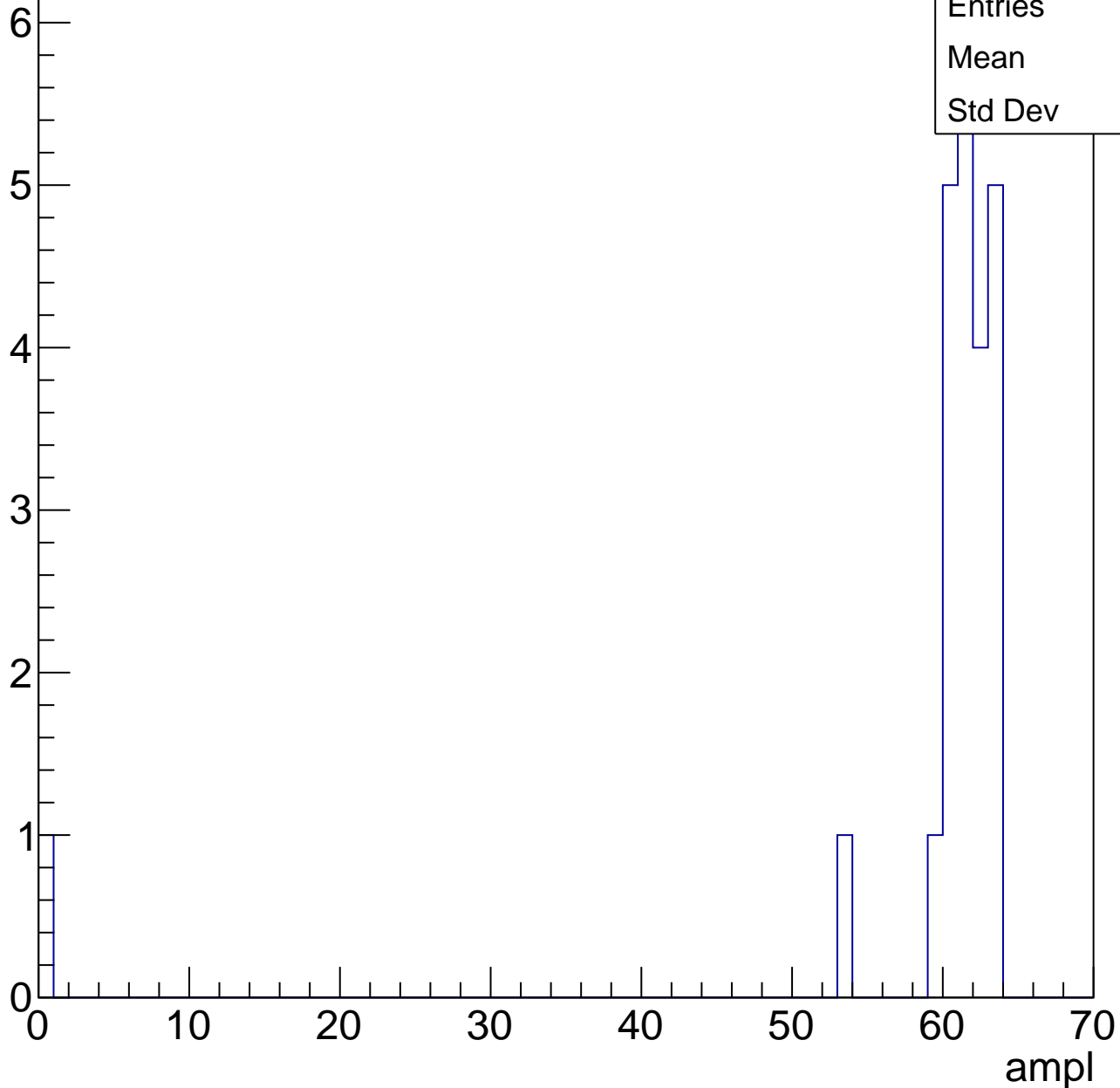


B1L103S, U8-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.3
Std Dev	12.6



B1L103S, U8-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U8-ch1, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	27.32
Std Dev	9.111

Gaus mean : 30.7078

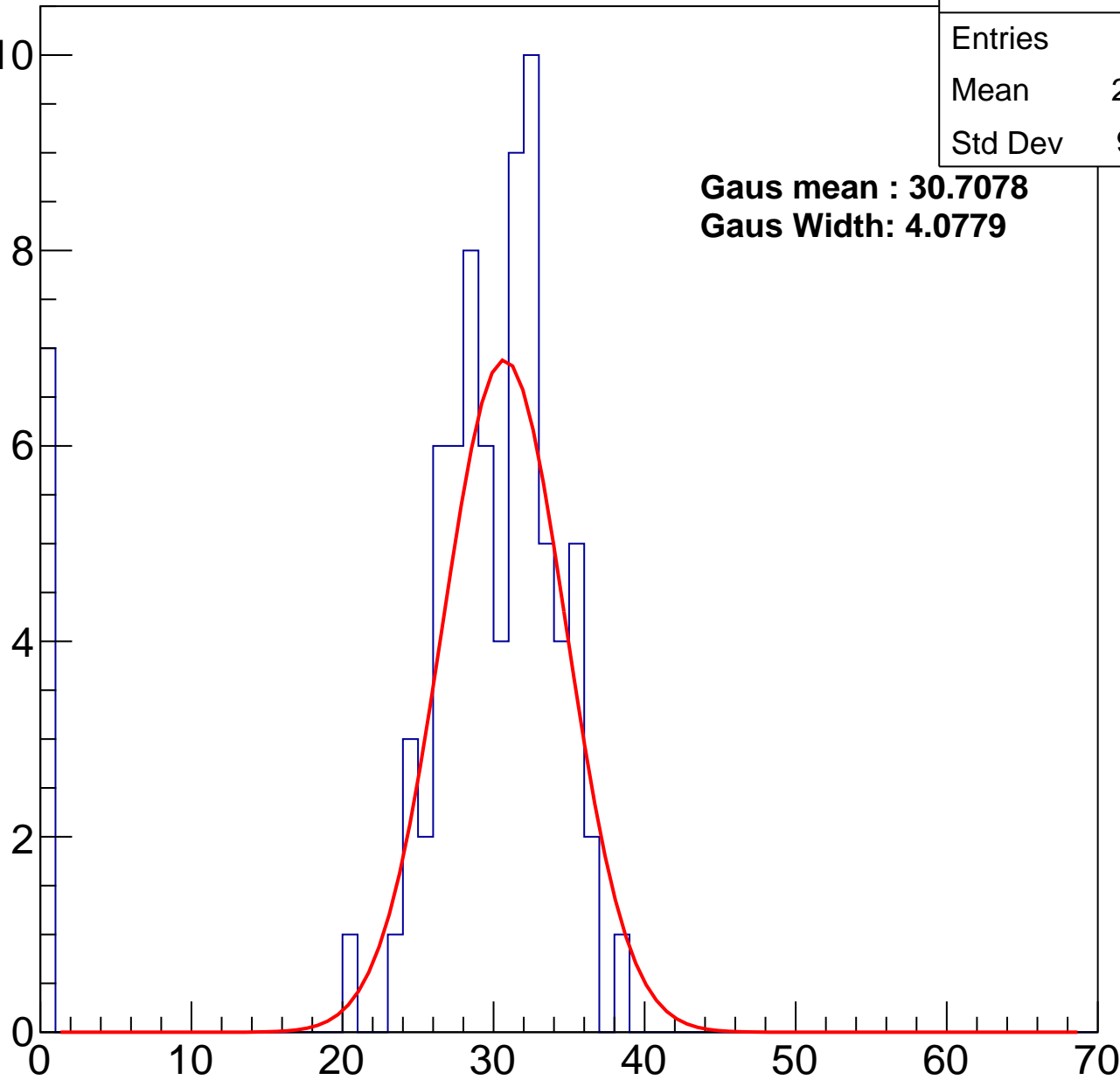
Gaus Width: 4.0779

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch1, adc1

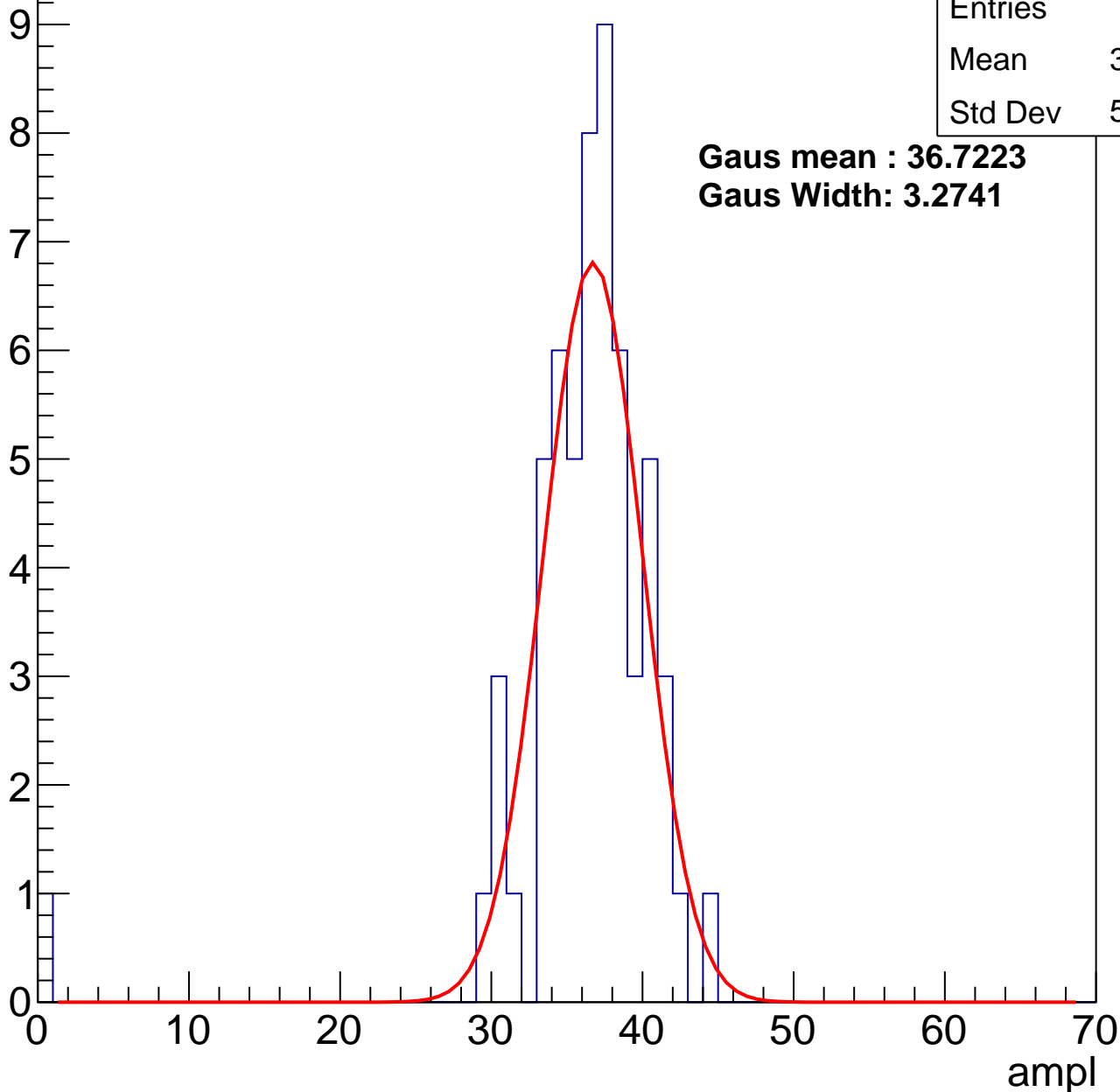
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	35.67
Std Dev	5.664

Gaus mean : 36.7223

Gaus Width: 3.2741



B1L103S, U8-ch1, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	38.26
Std Dev	13.82

Gaus mean : 43.4081

Gaus Width: 3.2096

Entry

10

8

6

4

2

0

0

10

20

30

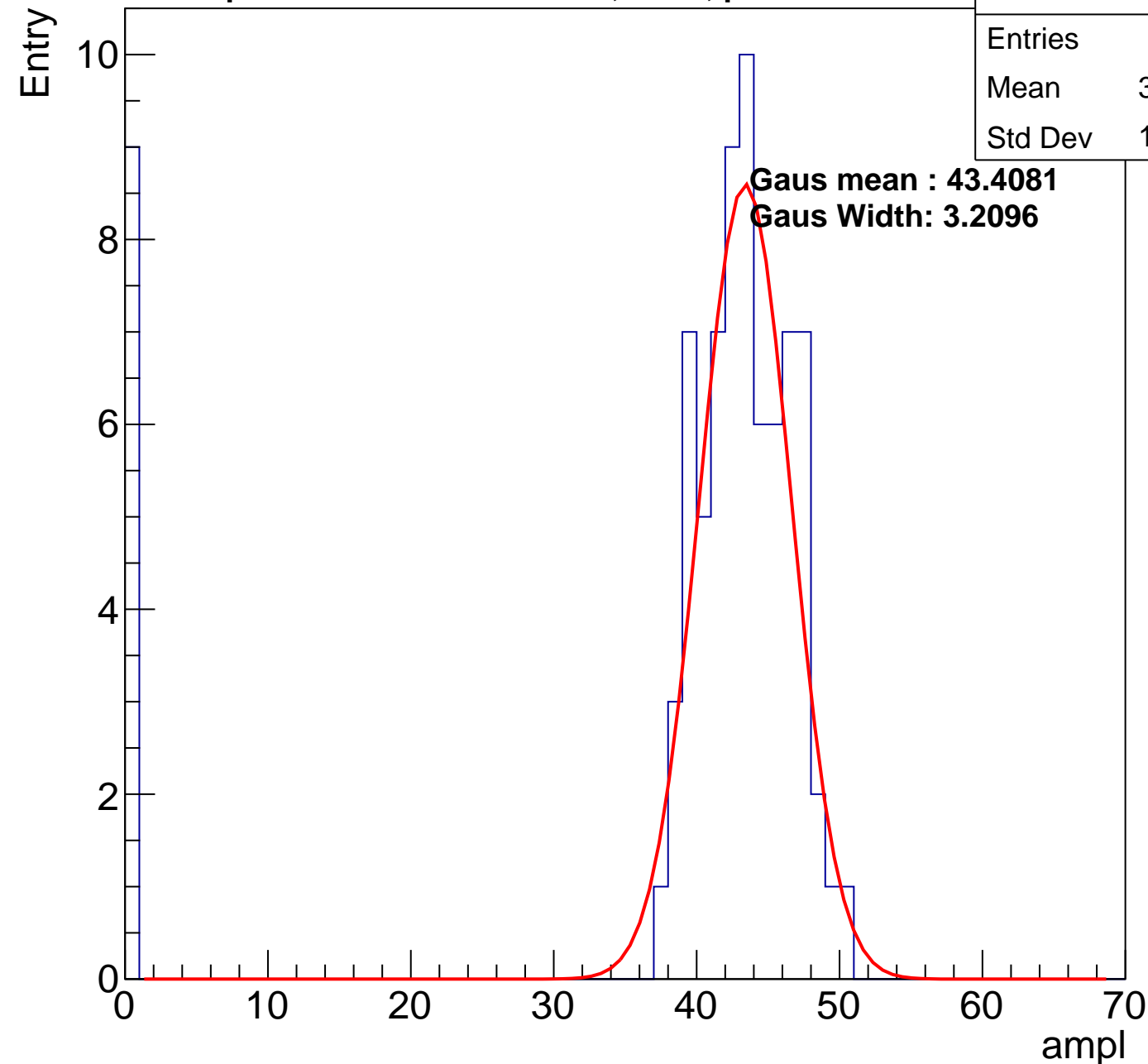
40

50

60

70

ampl

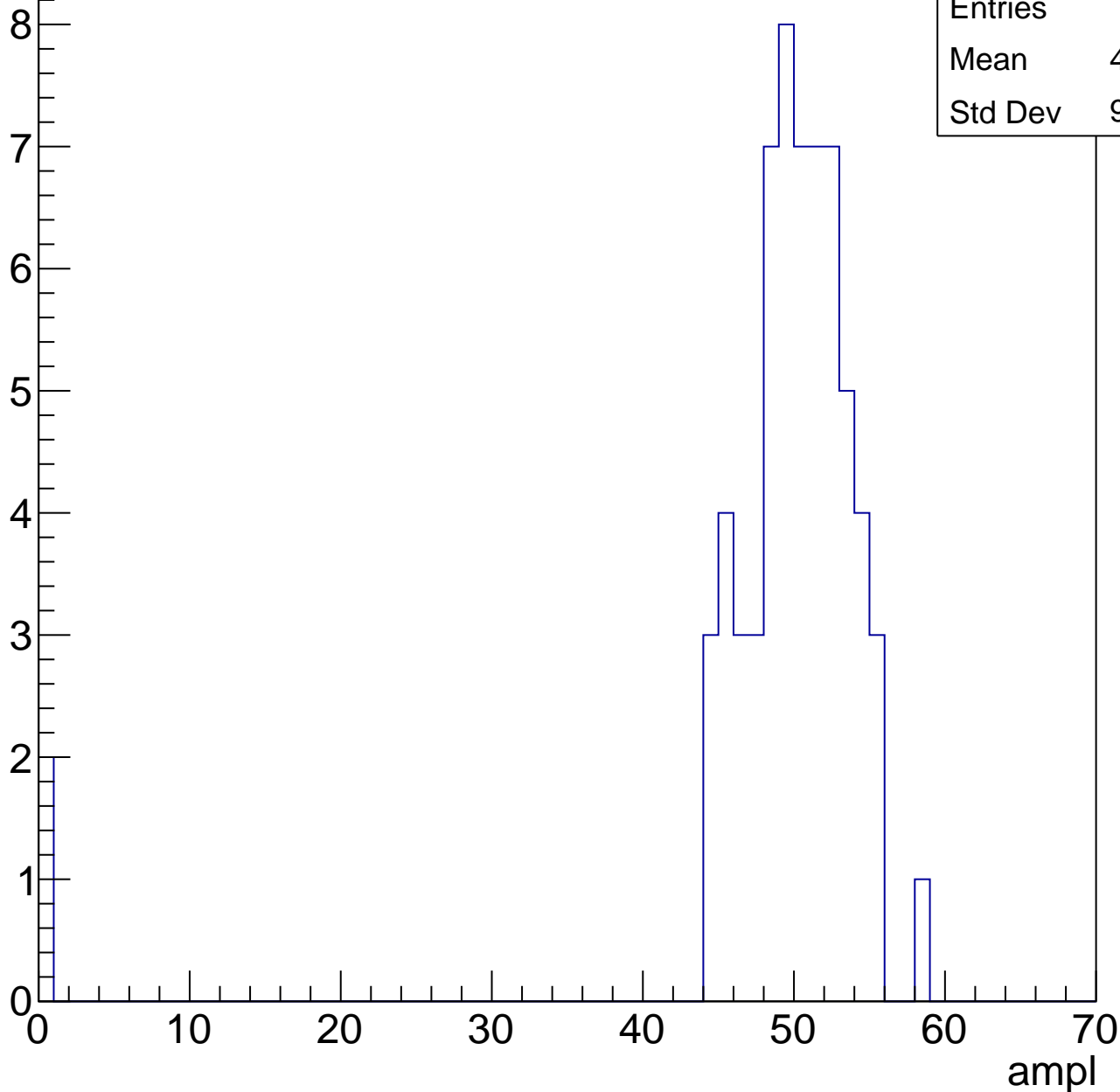


B1L103S, U8-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

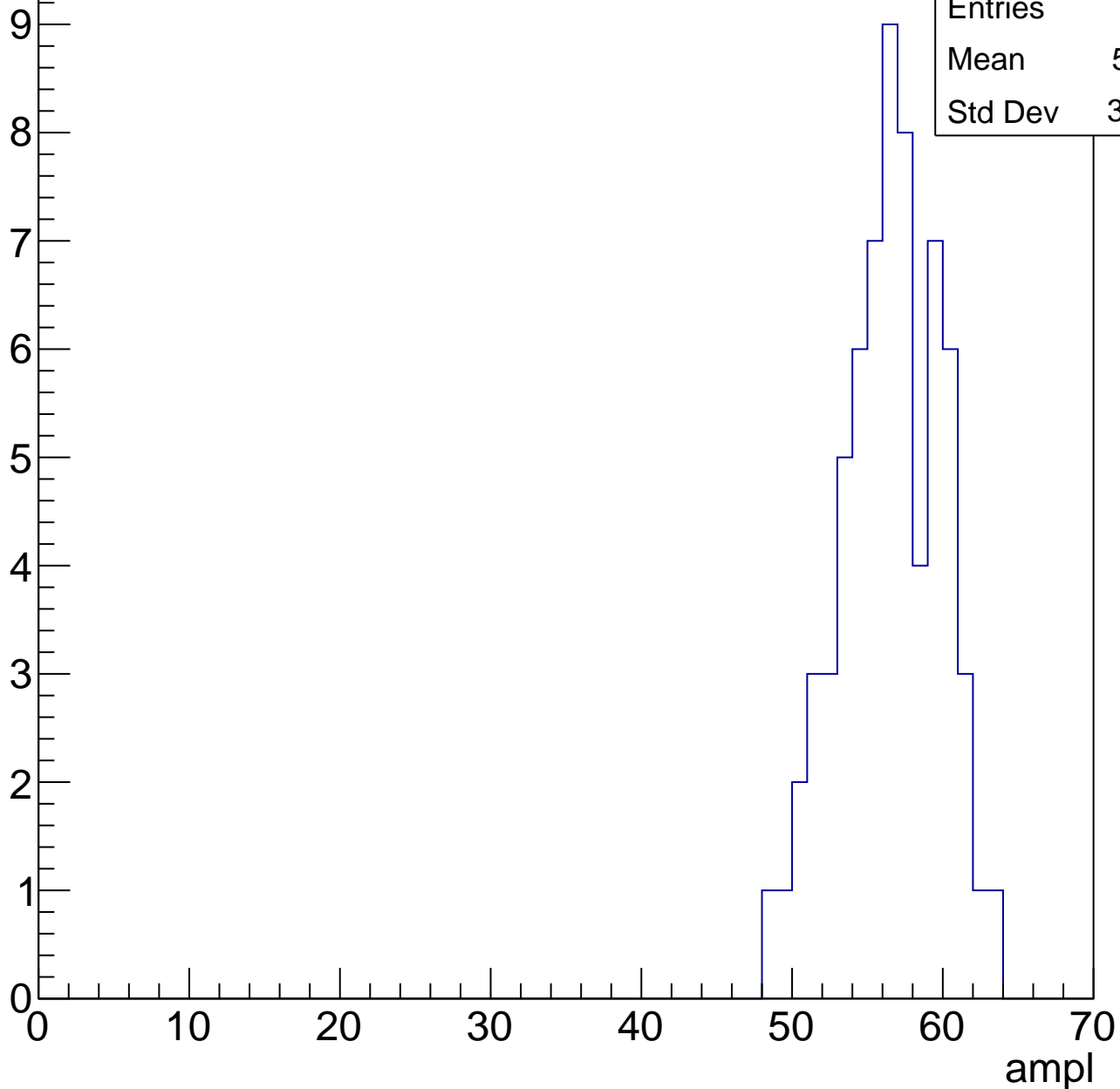
Entries	64
Mean	48.34
Std Dev	9.208



B1L103S, U8-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

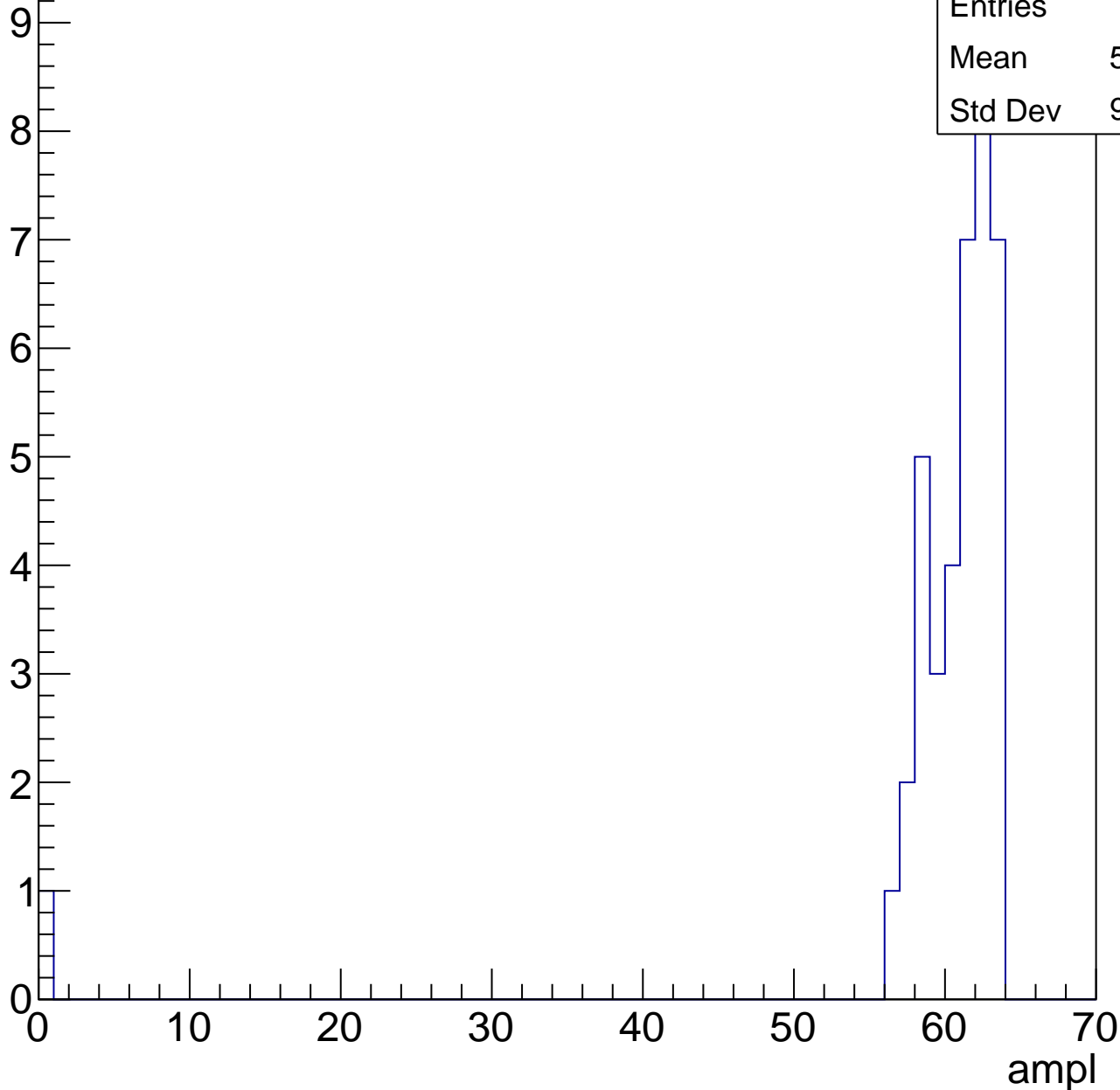


B1L103S, U8-ch1, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	59.05
Std Dev	9.777



B1L103S, U8-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U8-ch2, adc0

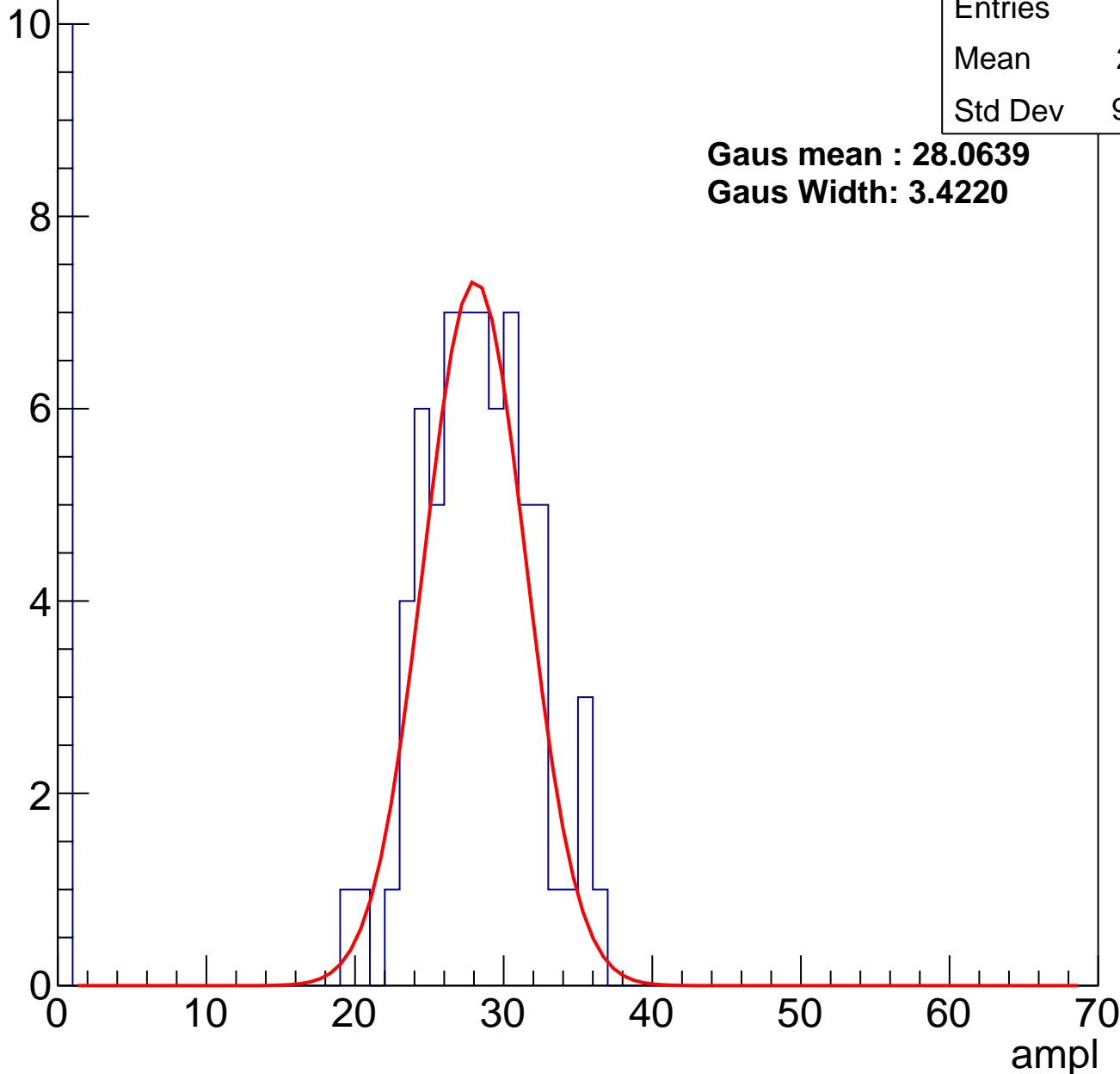
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	24.31
Std Dev	9.913

Gaus mean : 28.0639

Gaus Width: 3.4220

Entry



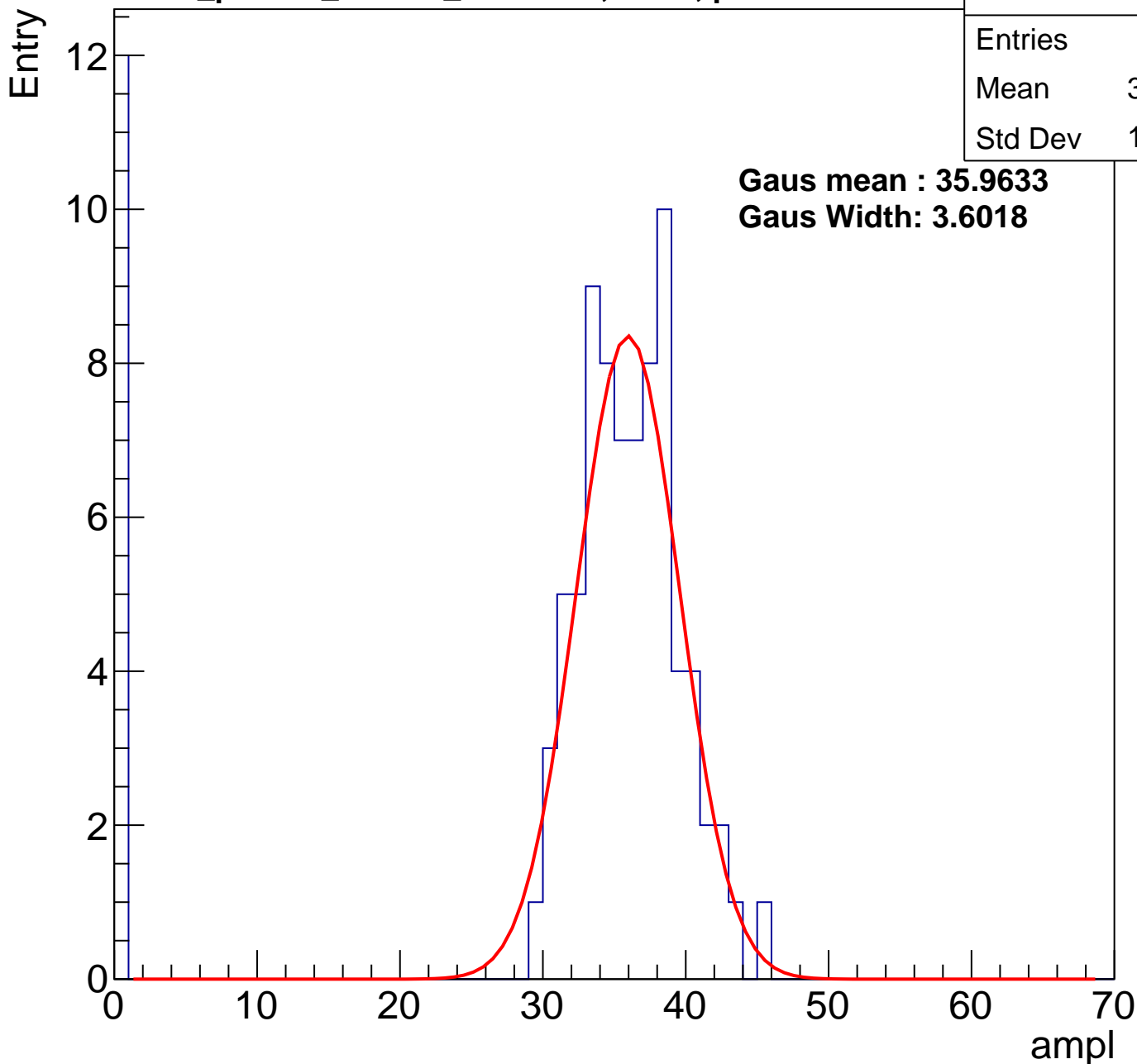
B1L103S, U8-ch2, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	30.85
Std Dev	12.58

Gaus mean : 35.9633

Gaus Width: 3.6018



B1L103S, U8-ch2, adc2

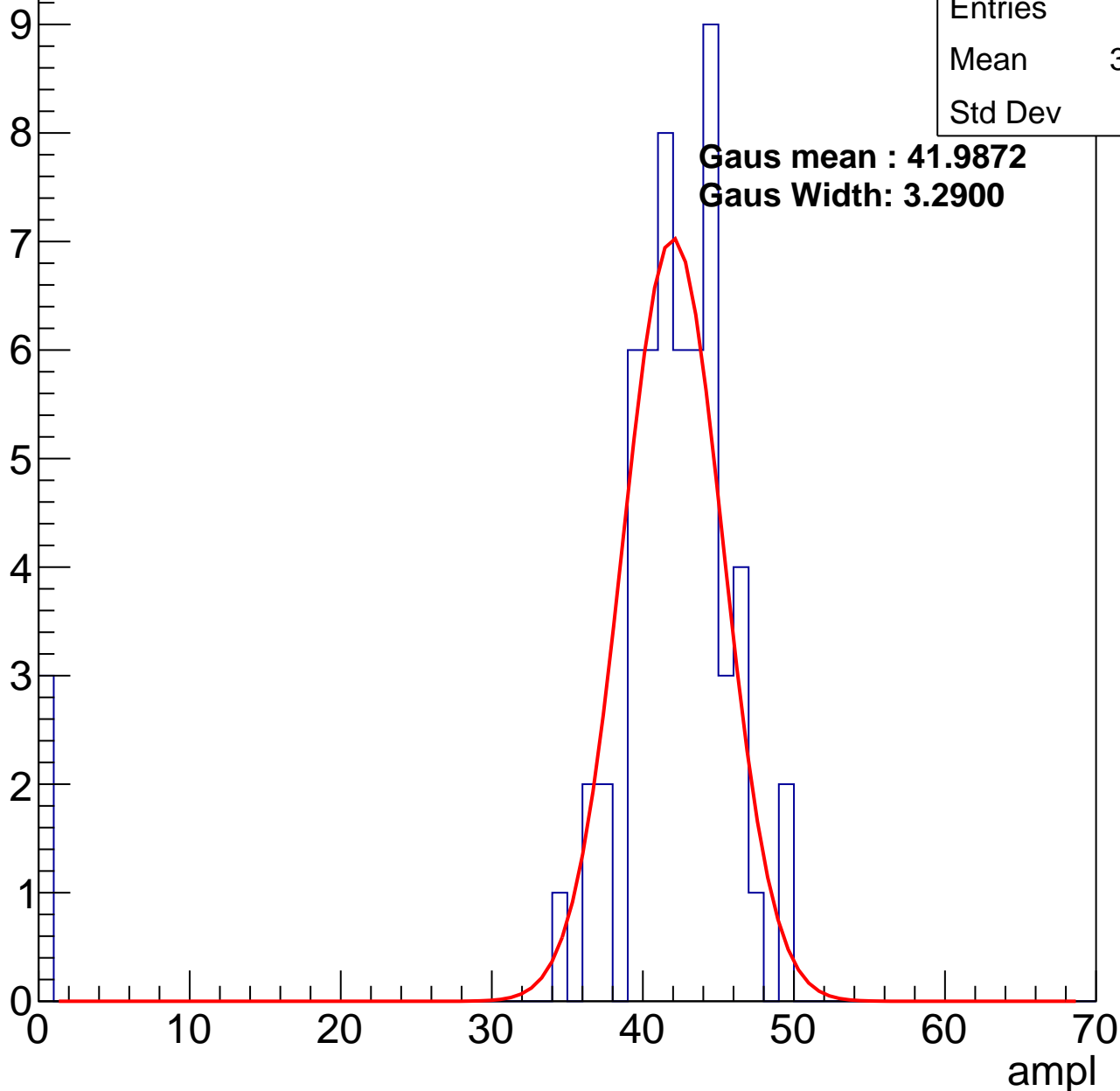
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	39.86
Std Dev	9.7

Gaus mean : 41.9872

Gaus Width: 3.2900

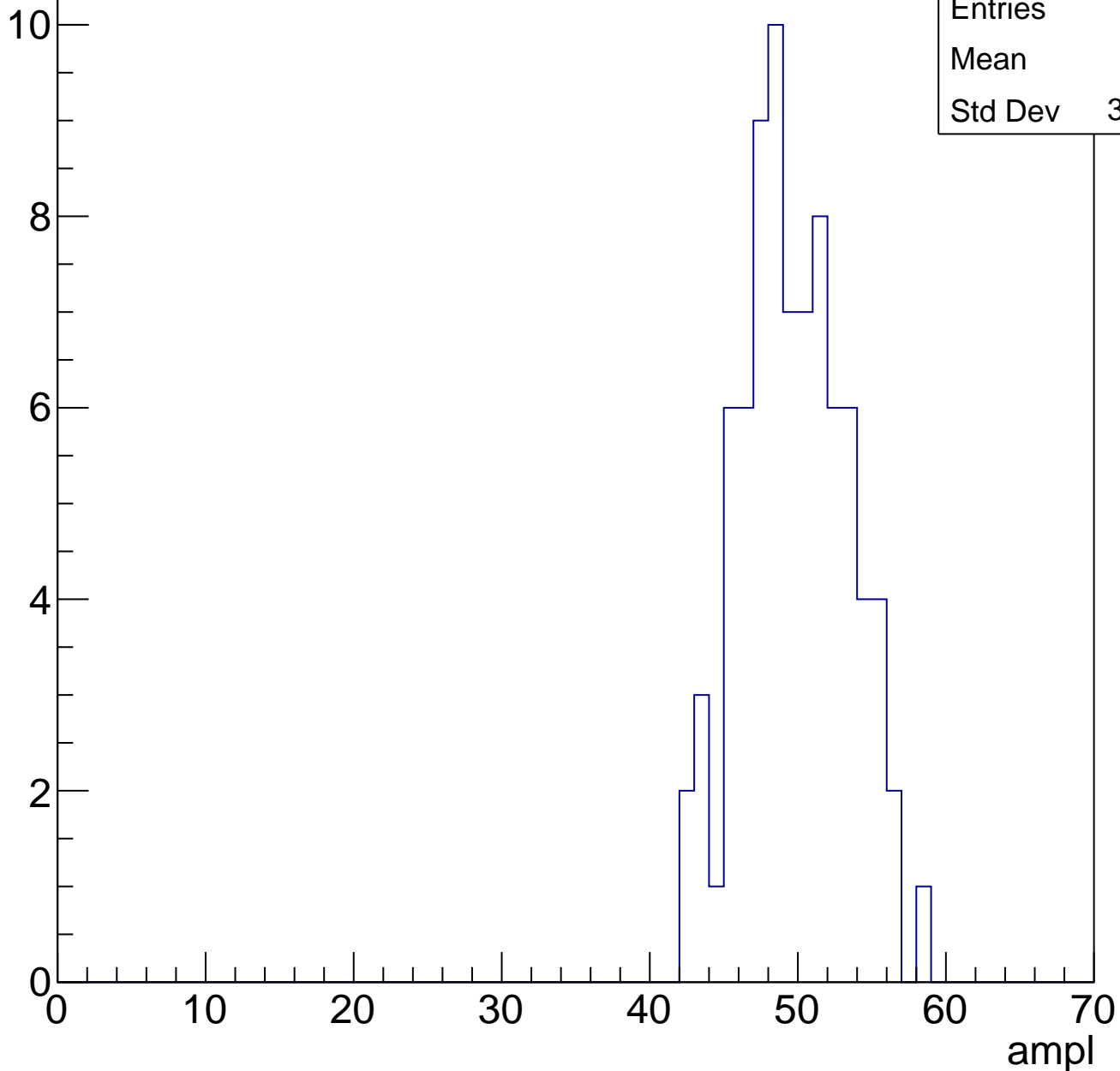


B1L103S, U8-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	49.3
Std Dev	3.547

Entry

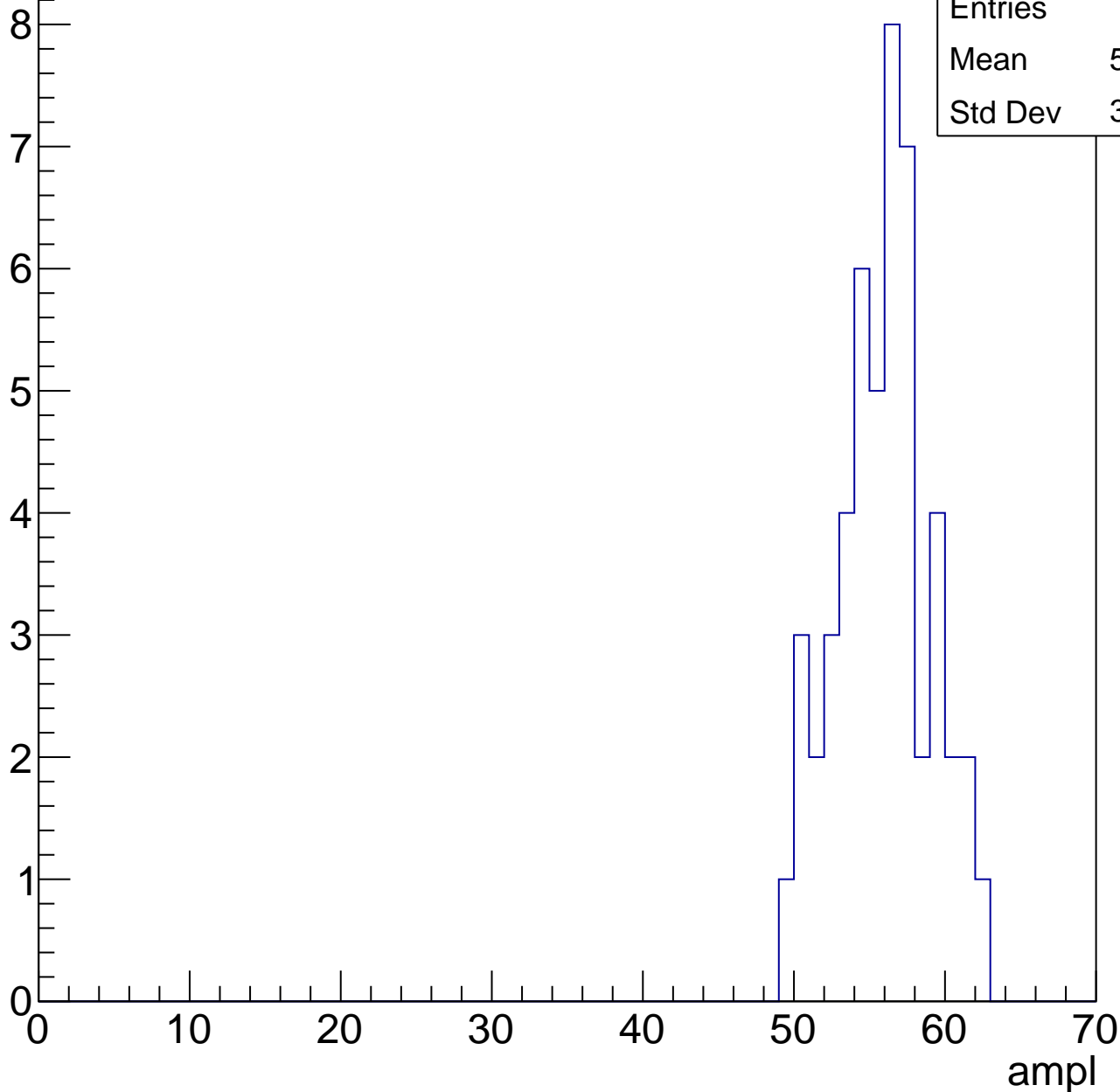


B1L103S, U8-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.42
Std Dev	3.073



B1L103S, U8-ch2, adc5

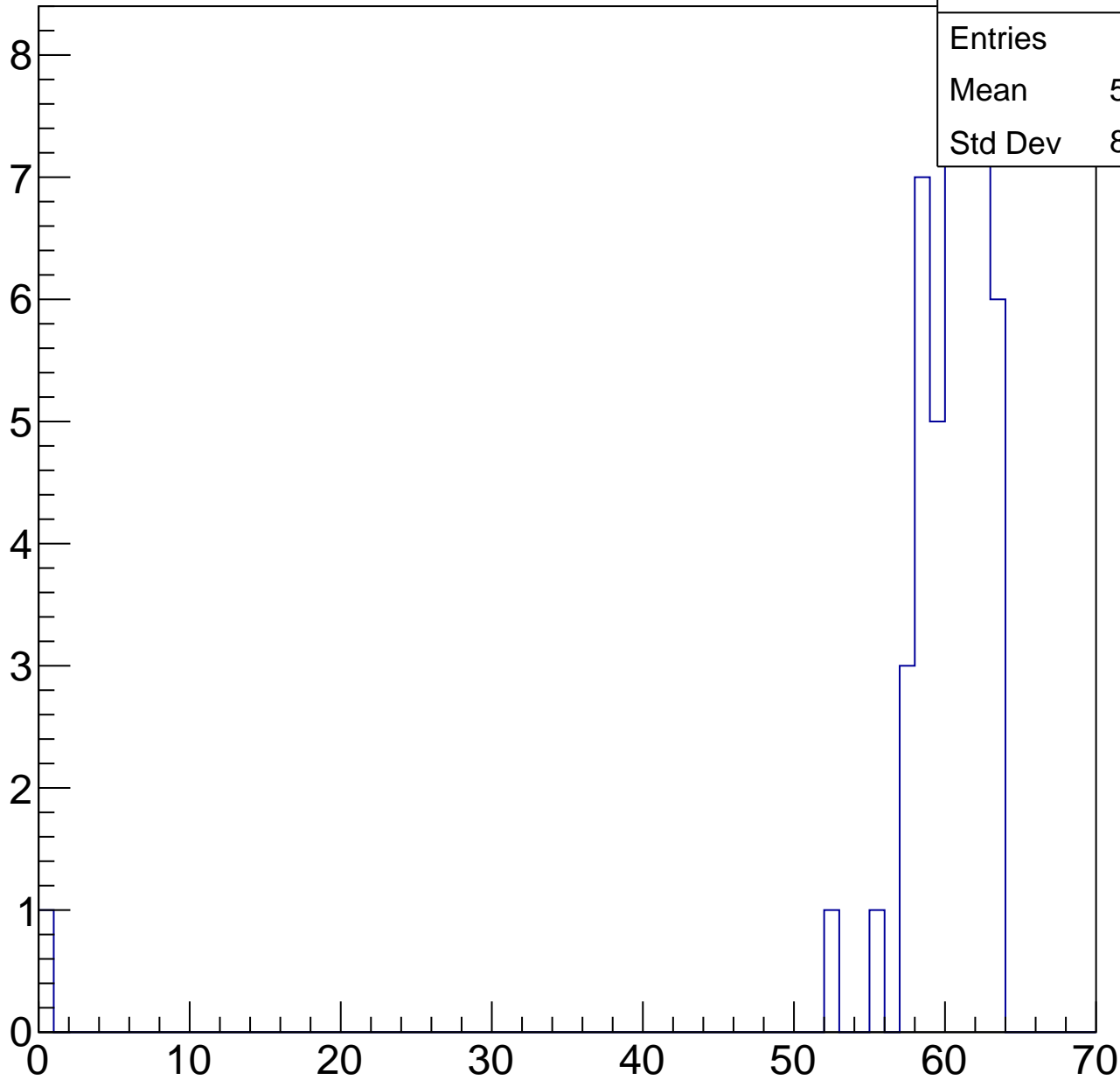
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.77
Std Dev	8.863

ampl



B1L103S, U8-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U8-ch3, adc0

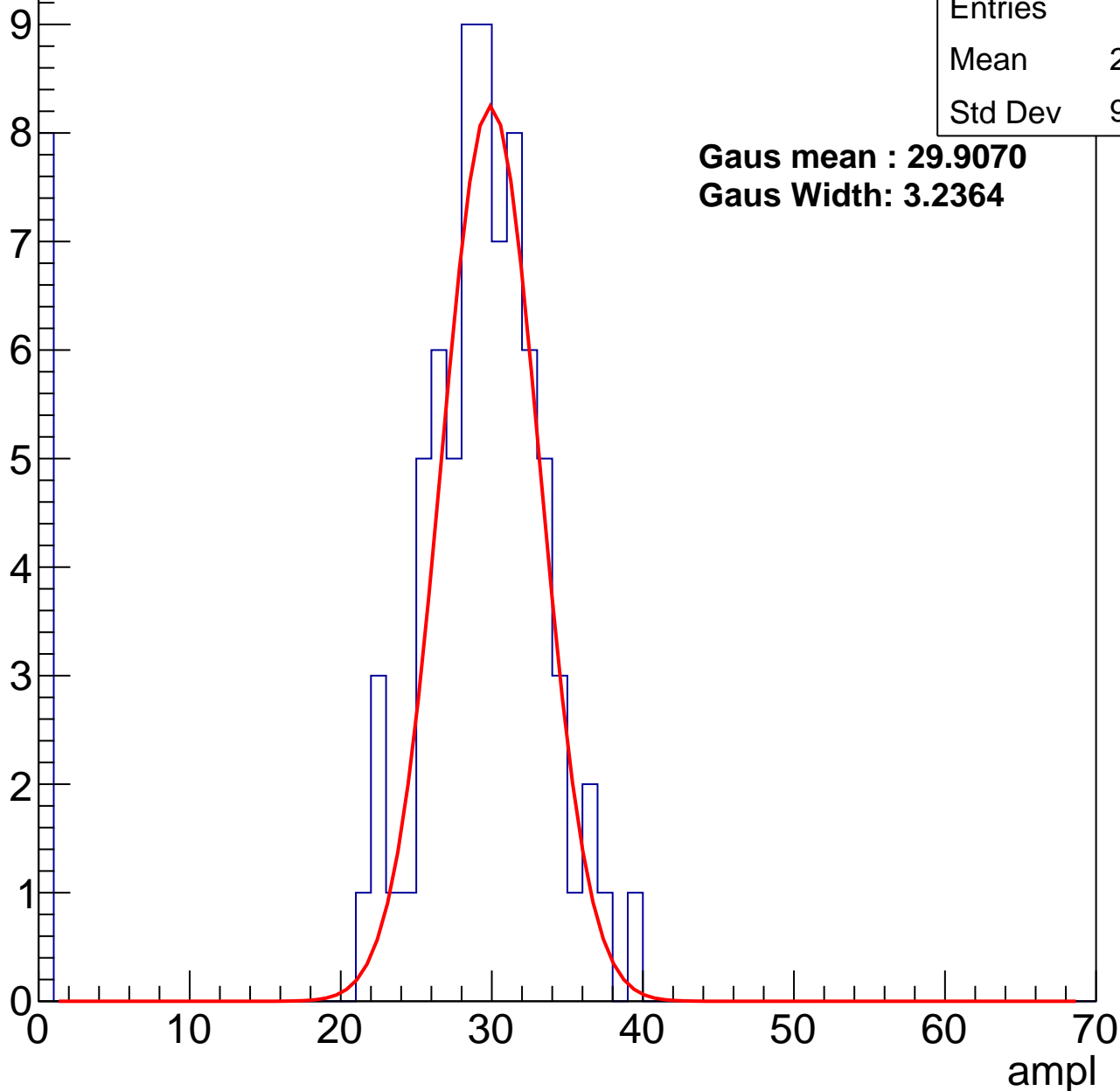
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	26.38
Std Dev	9.335

Gaus mean : 29.9070

Gaus Width: 3.2364



B1L103S, U8-ch3, adc1

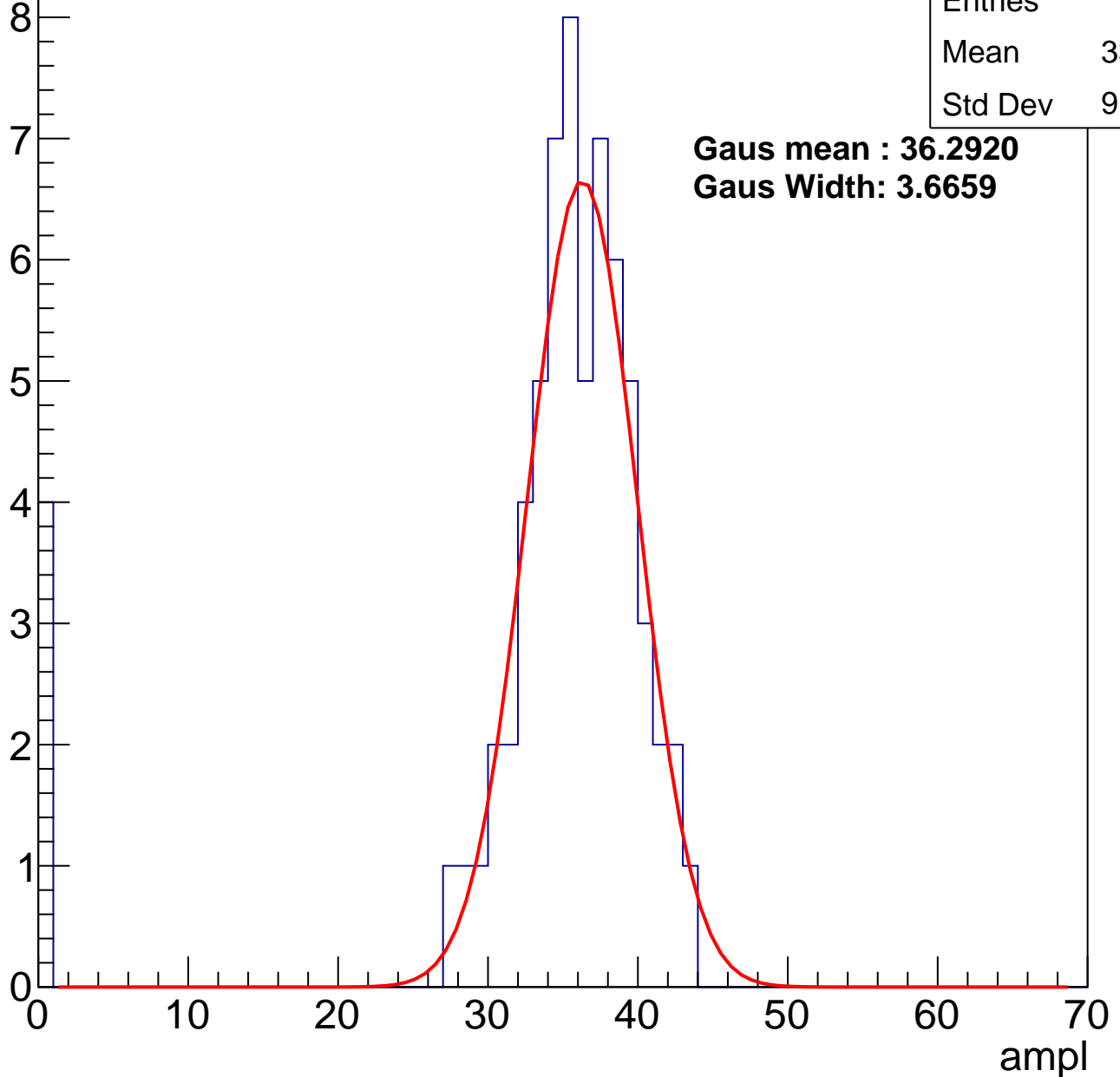
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	33.45
Std Dev	9.139

Gaus mean : 36.2920

Gaus Width: 3.6659



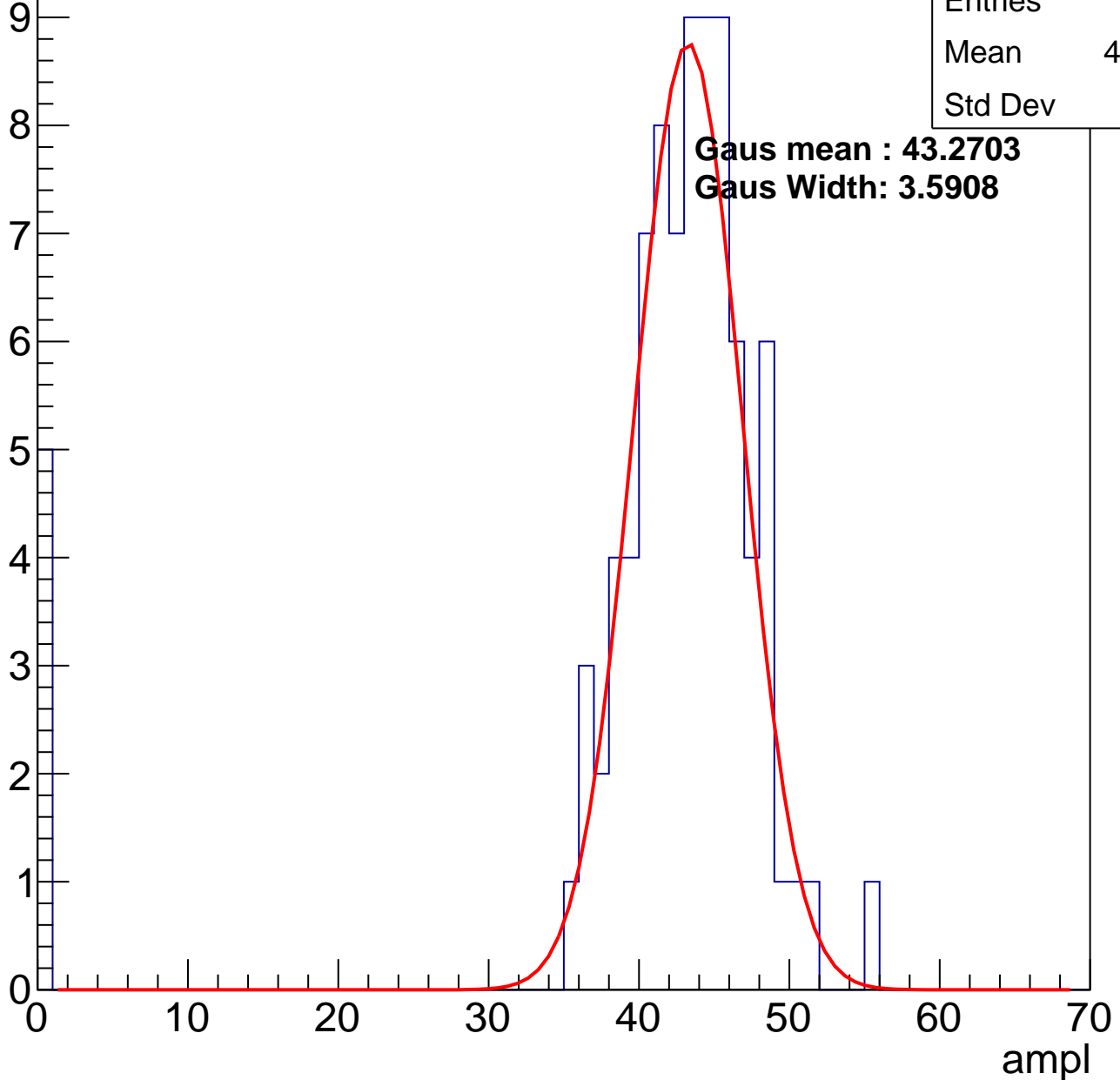
B1L103S, U8-ch3, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	40.59
Std Dev	10.6

Gaus mean : 43.2703
Gaus Width: 3.5908

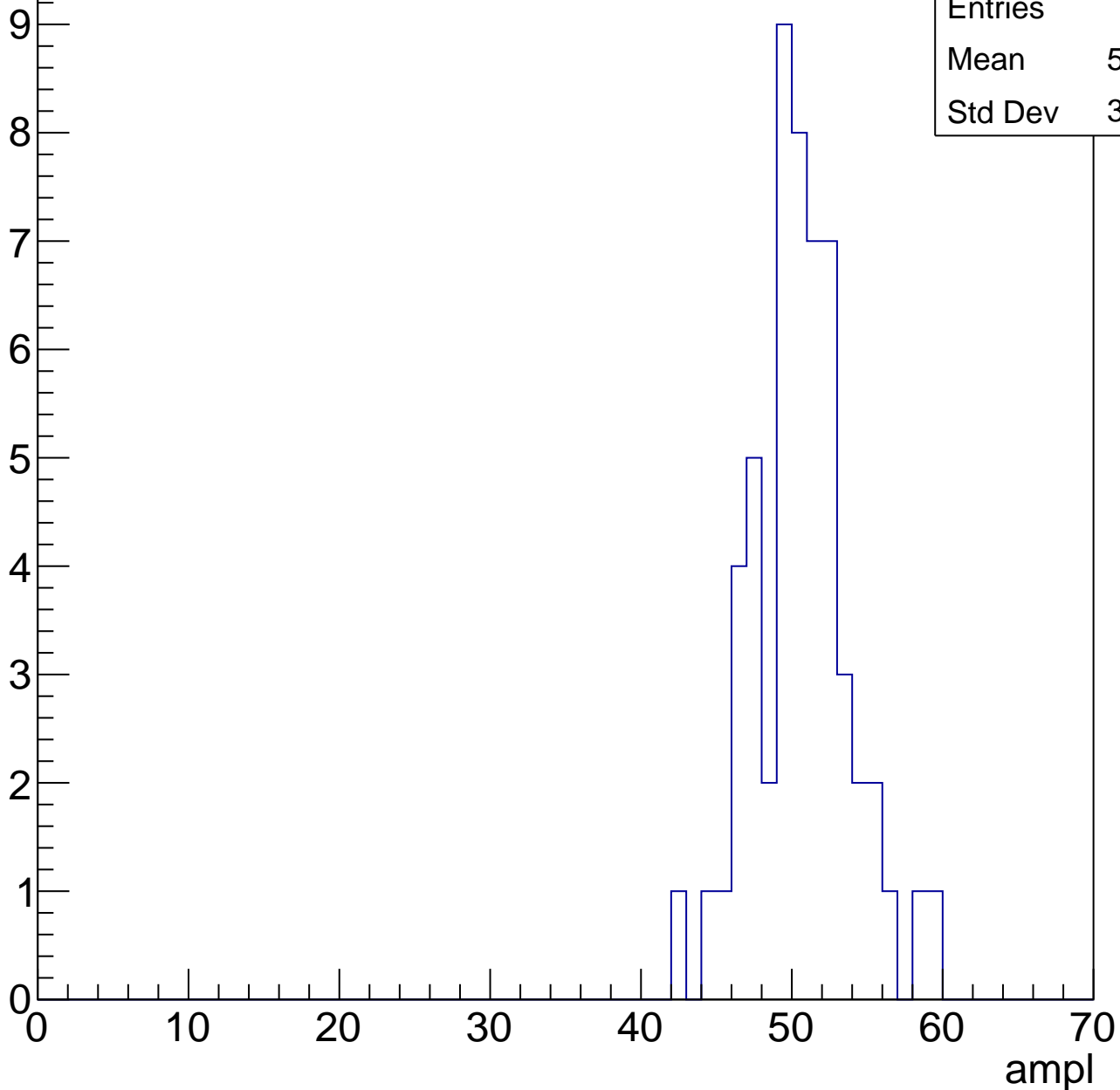


B1L103S, U8-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	50.15
Std Dev	3.233

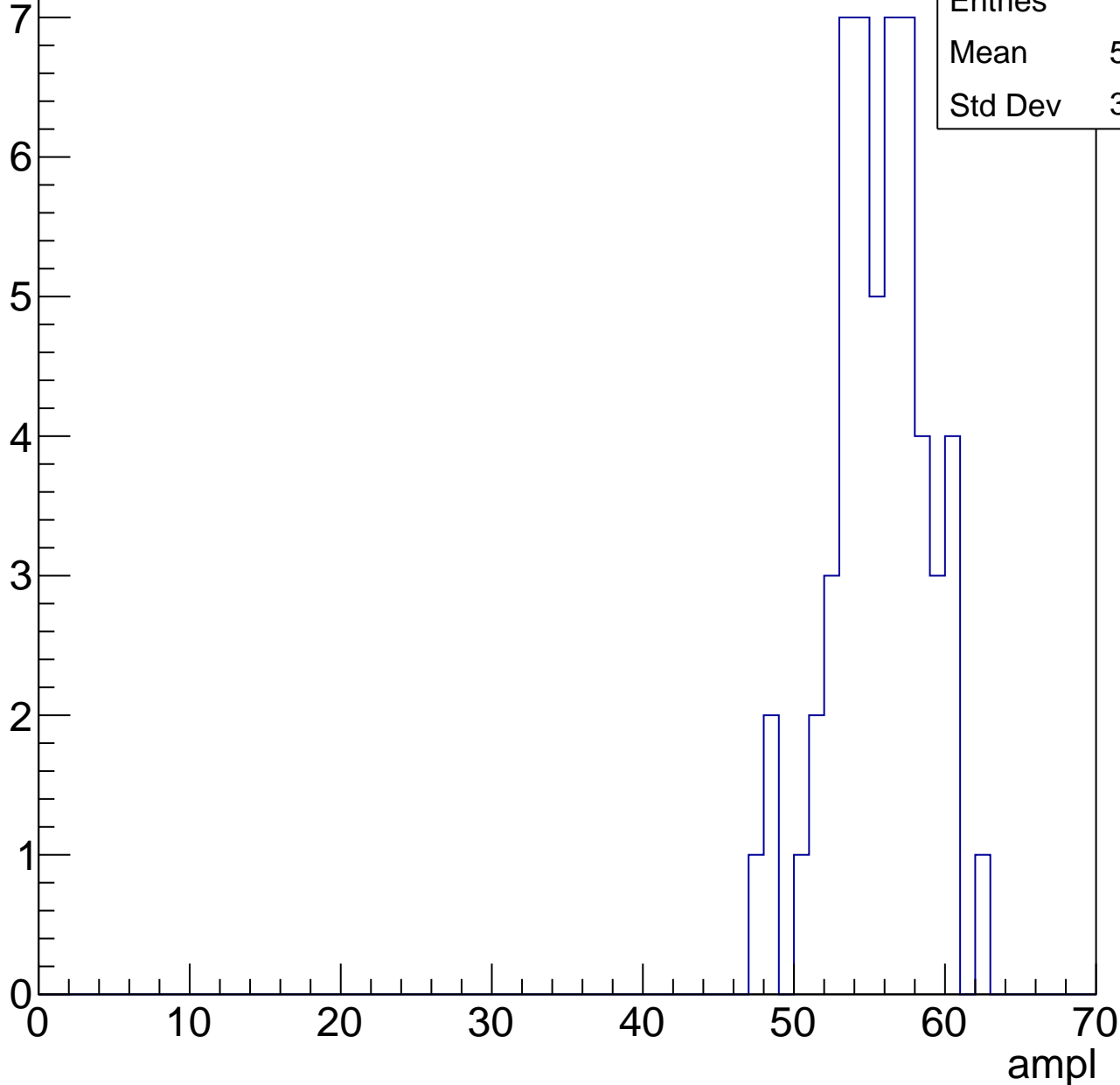


B1L103S, U8-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	55.13
Std Dev	3.186



B1L103S, U8-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	51
Mean	58.8
Std Dev	8.641

Entry

10

8

6

4

2

0

0

10

20

30

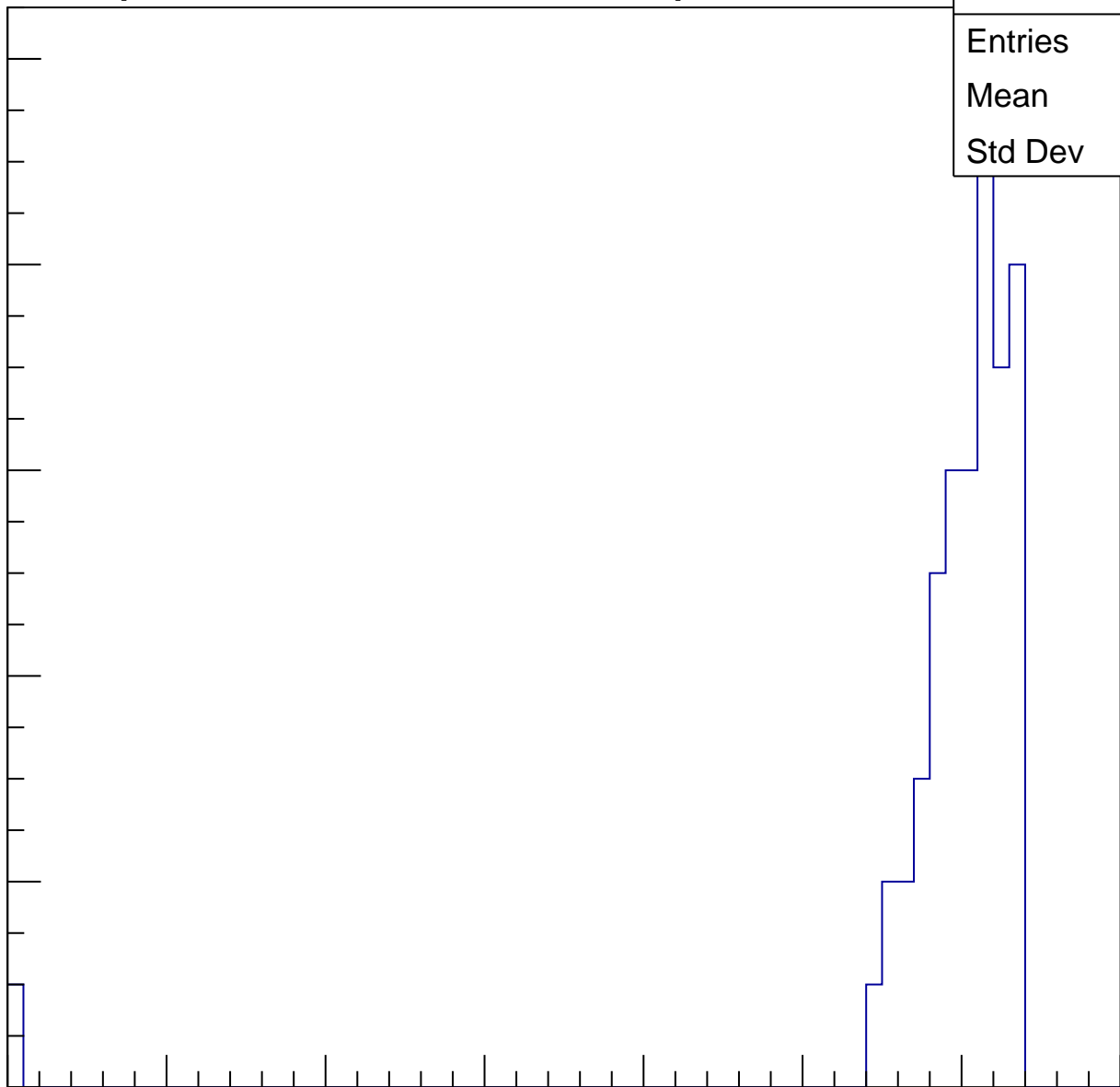
40

50

60

70

ampl



B1L103S, U8-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U8-ch4, adc0

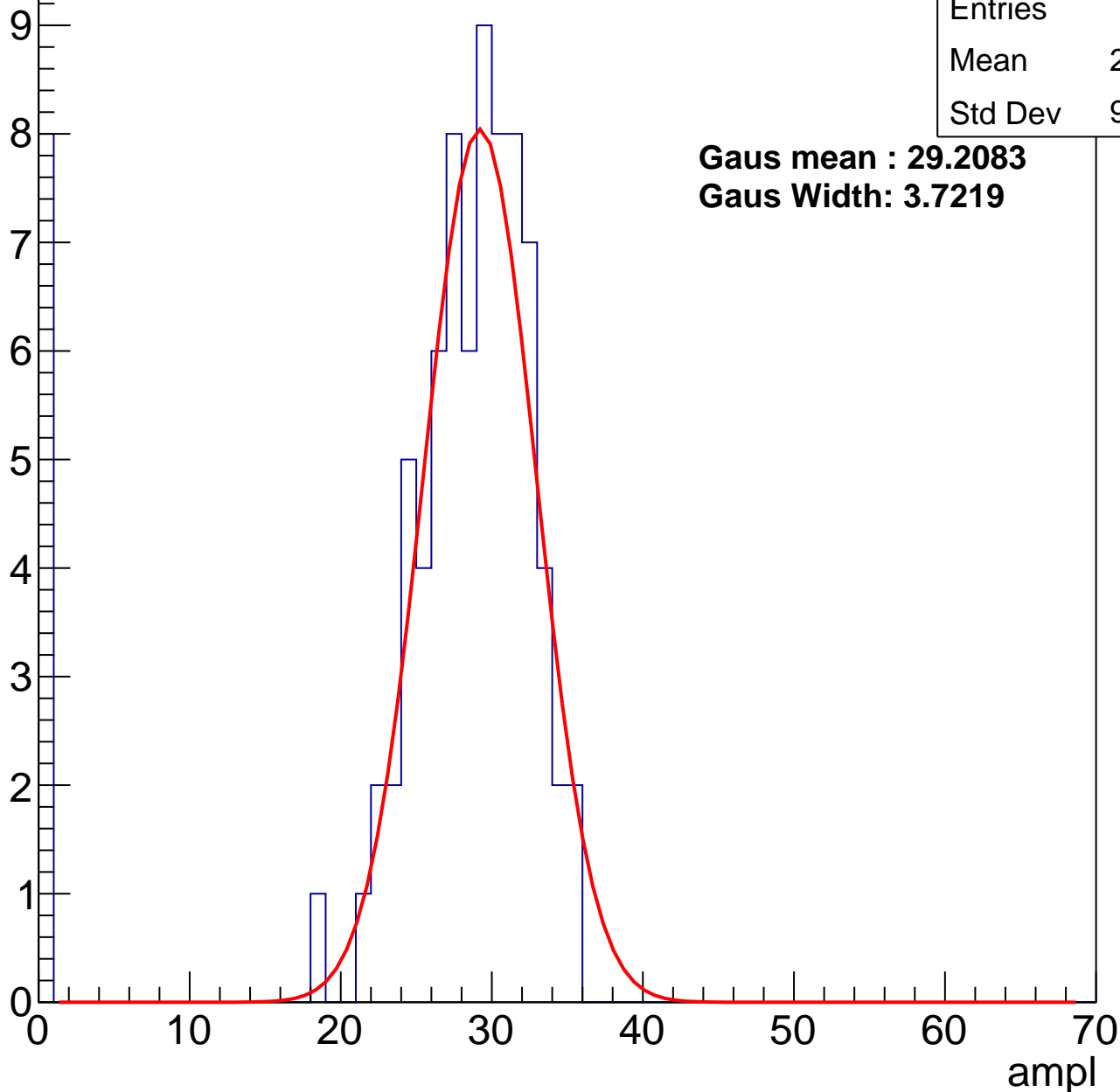
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	25.69
Std Dev	9.013

Gaus mean : 29.2083

Gaus Width: 3.7219



B1L103S, U8-ch4, adc1

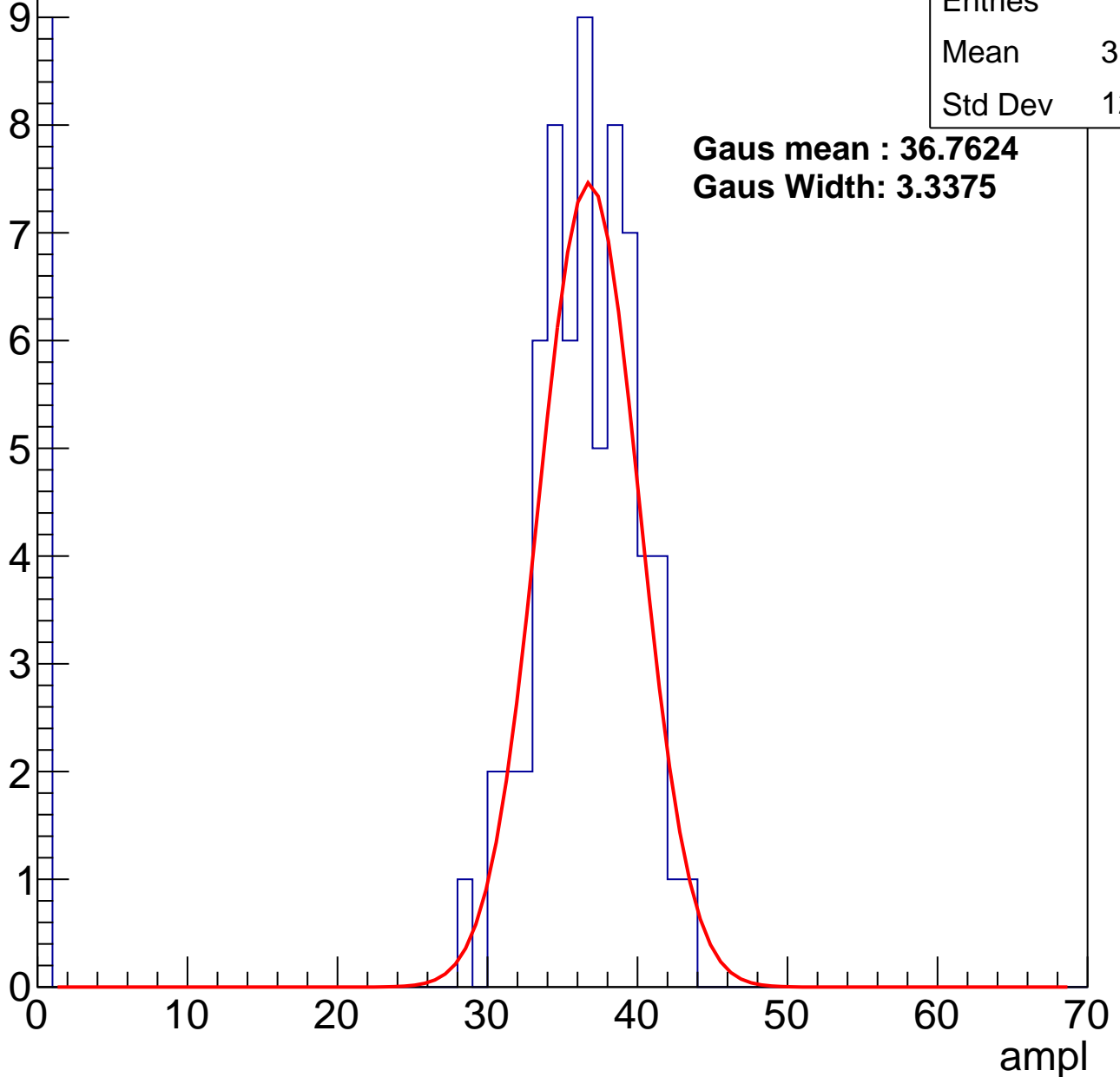
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31.85
Std Dev	12.13

Gaus mean : 36.7624

Gaus Width: 3.3375



B1L103S, U8-ch4, adc2

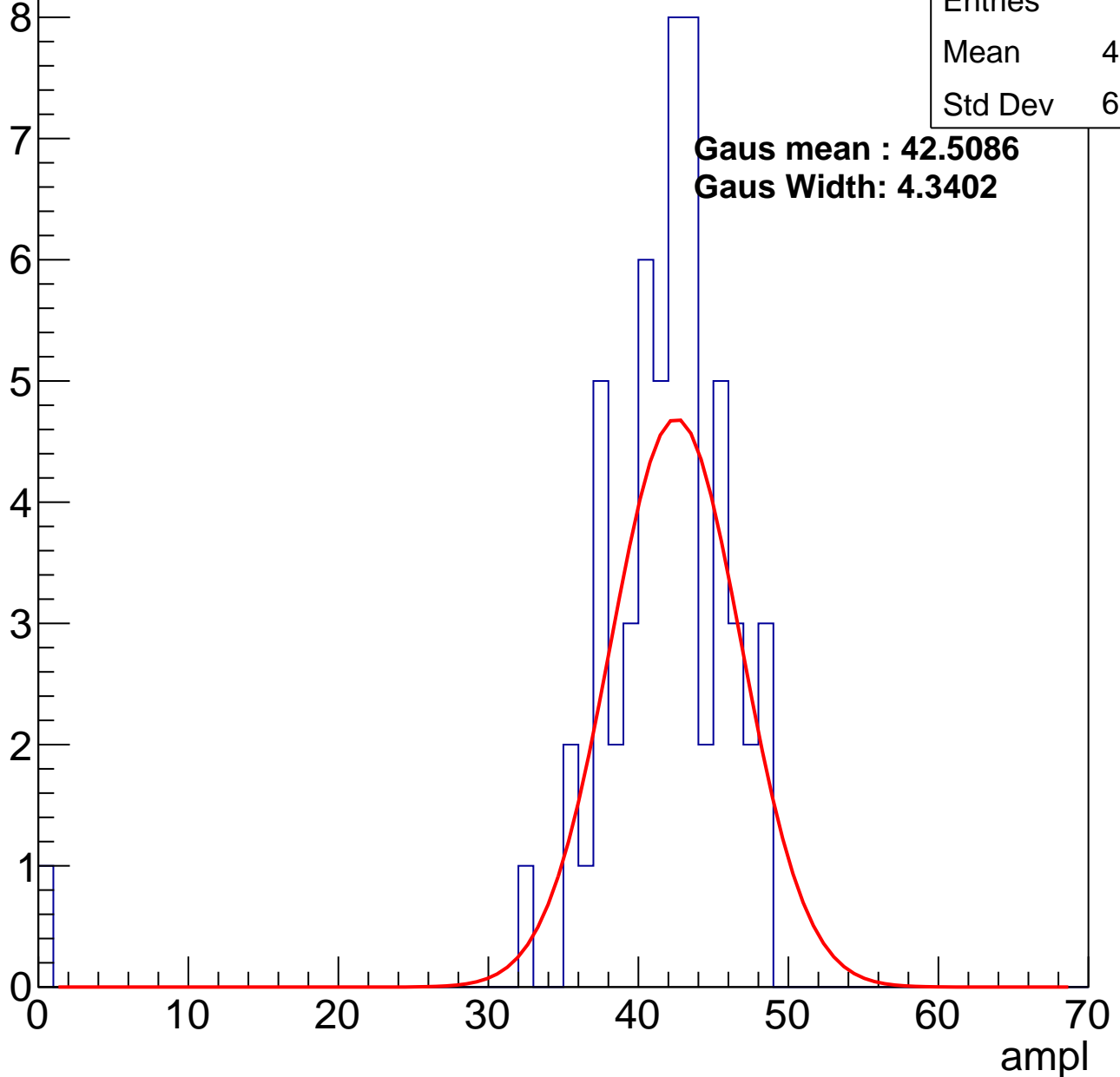
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	40.88
Std Dev	6.497

Gaus mean : 42.5086

Gaus Width: 4.3402

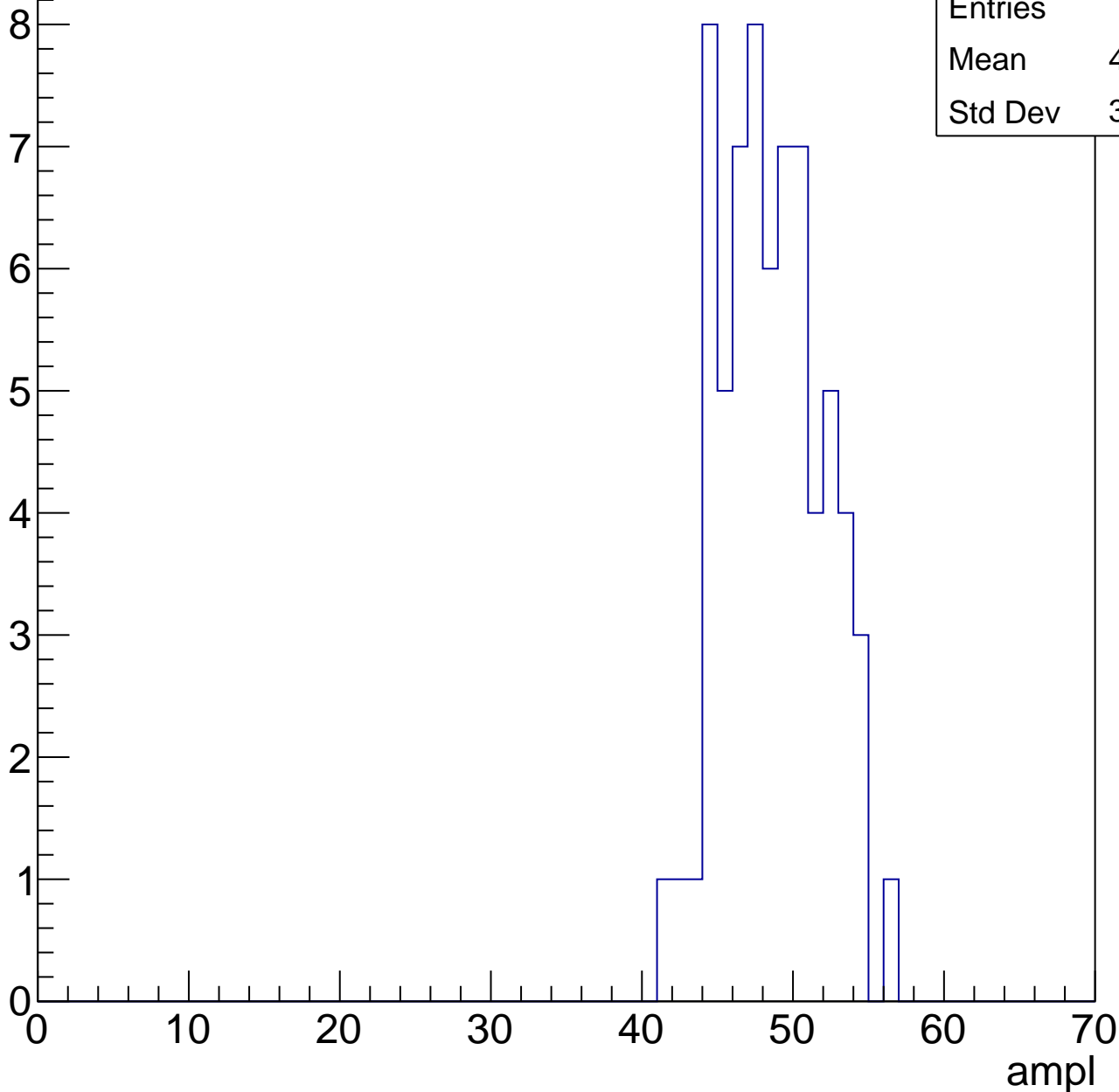


B1L103S, U8-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	48.18
Std Dev	3.299

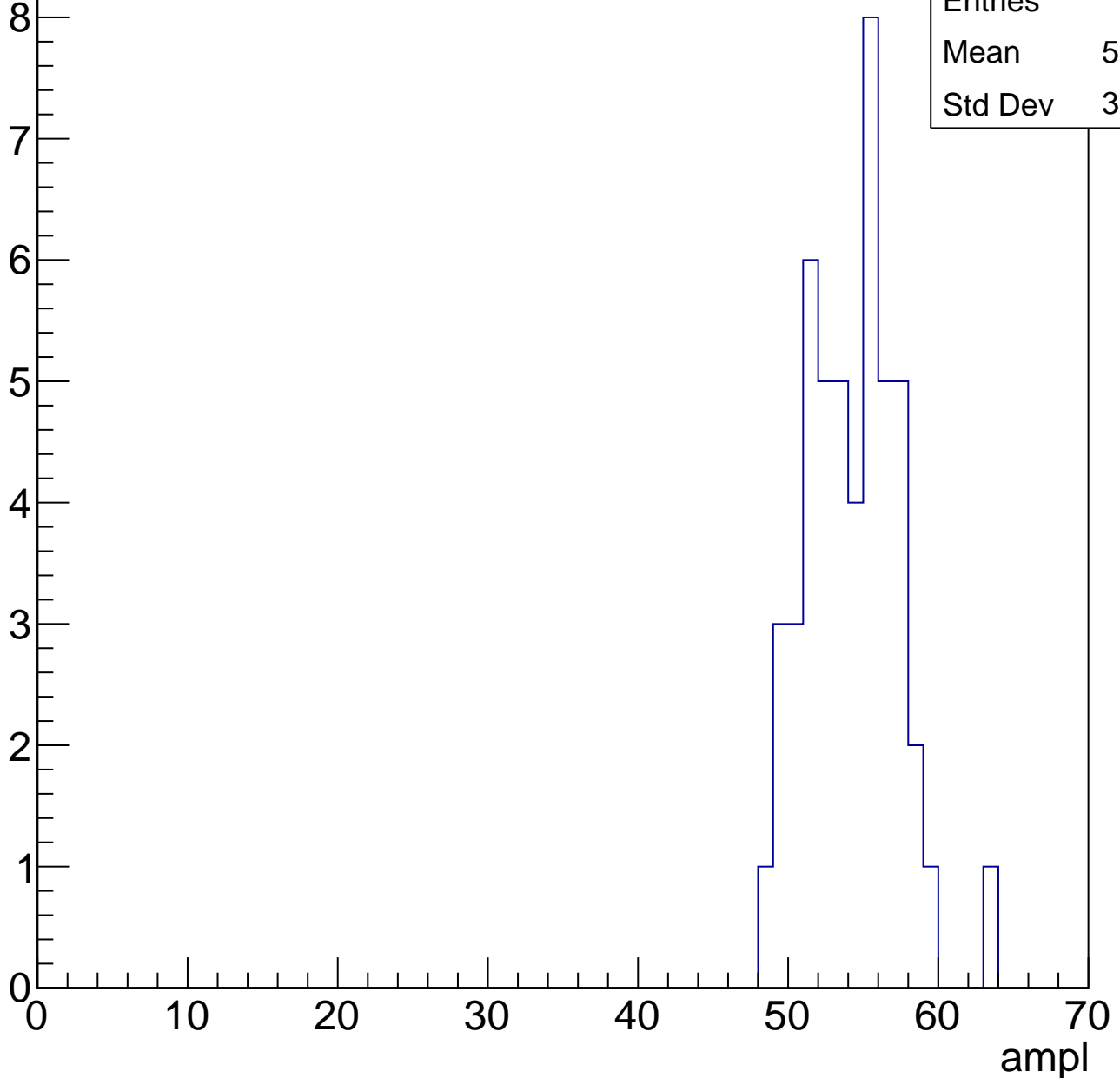


B1L103S, U8-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	53.78
Std Dev	3.019



B1L103S, U8-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

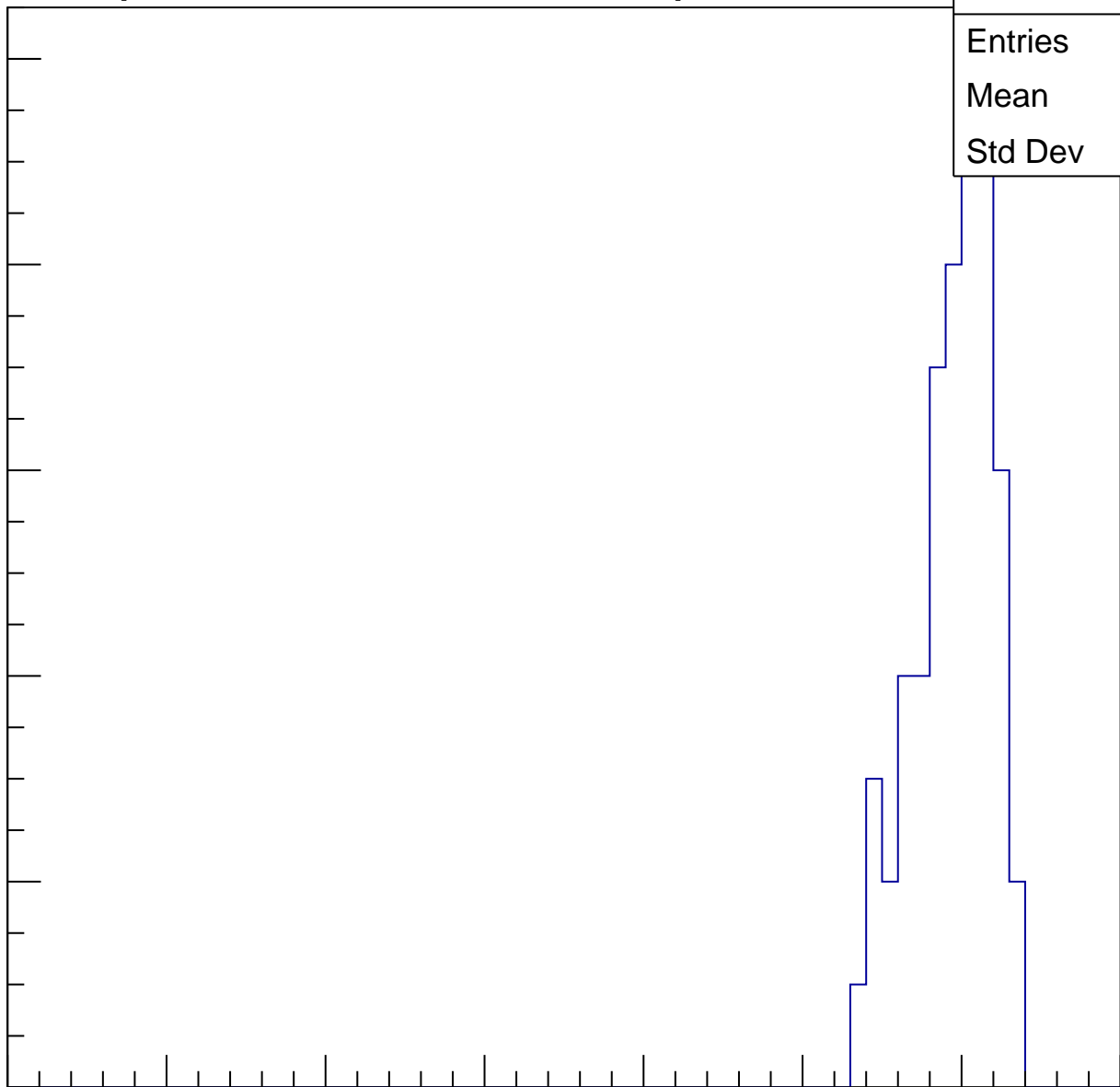
50

60

70

ampl

Entries	56
Mean	58.98
Std Dev	2.453

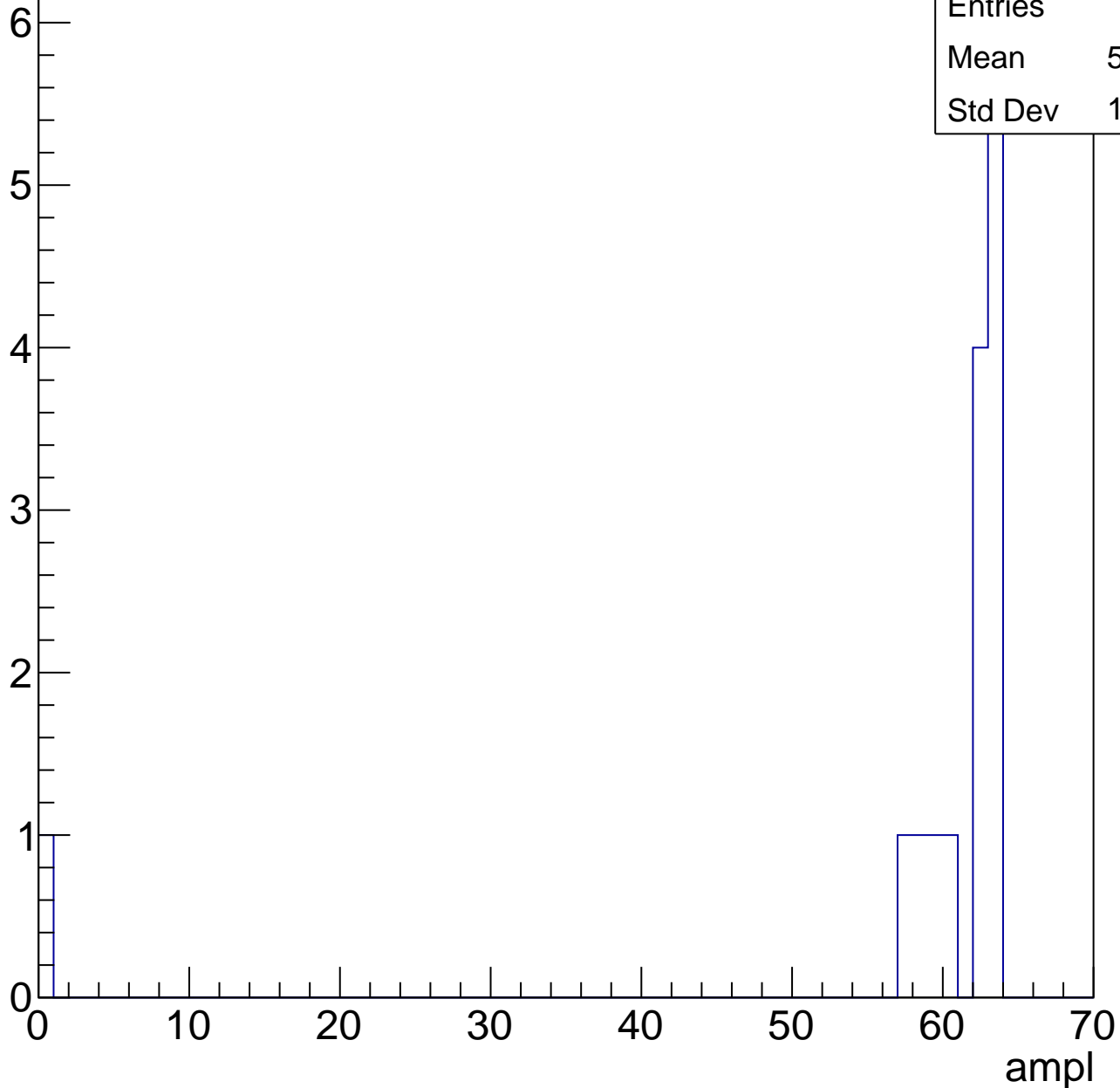


B1L103S, U8-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.33
Std Dev	15.44



B1L103S, U8-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U8-ch5, adc0

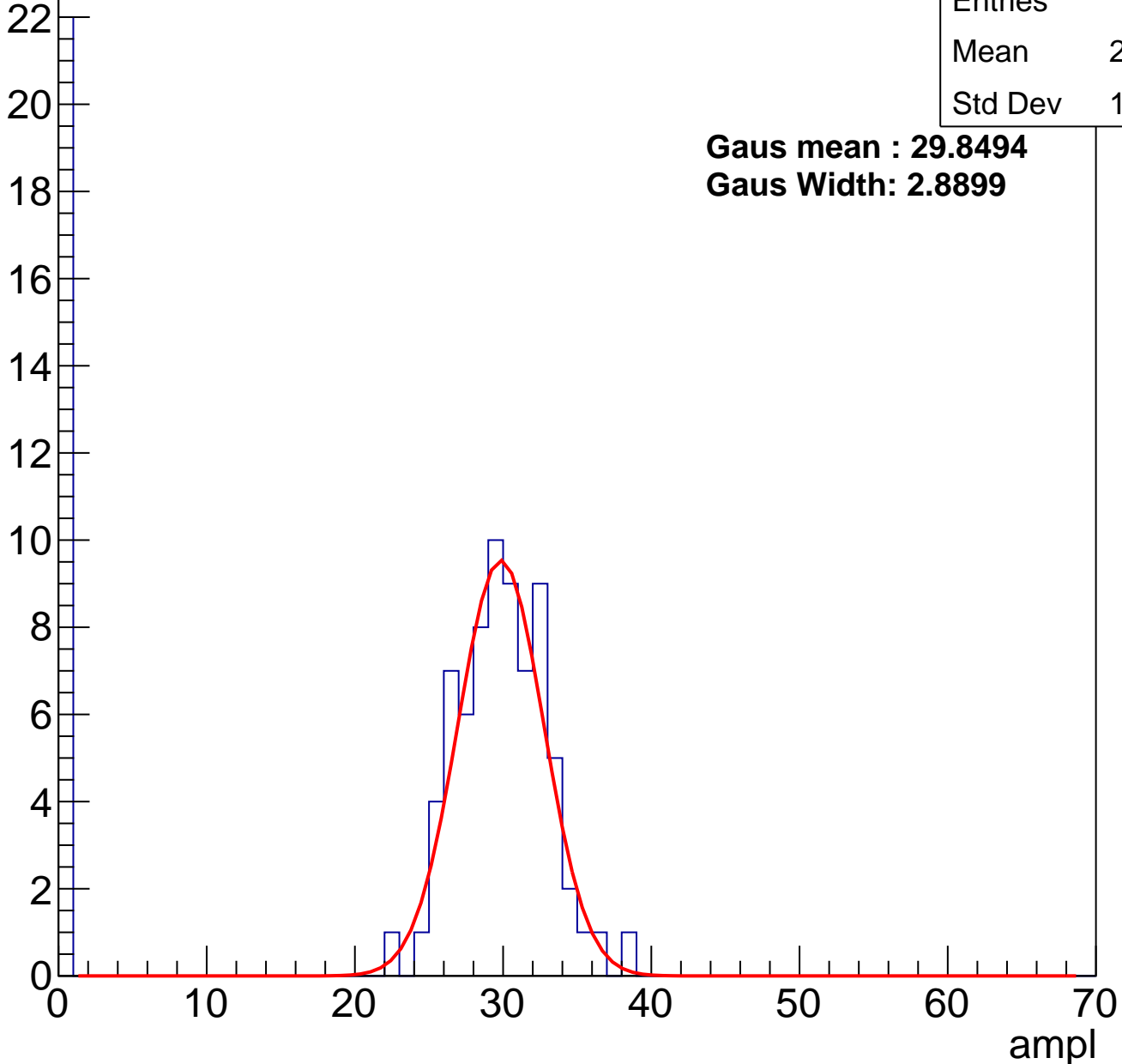
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	22.56
Std Dev	12.74

Gaus mean : 29.8494

Gaus Width: 2.8899

Entry



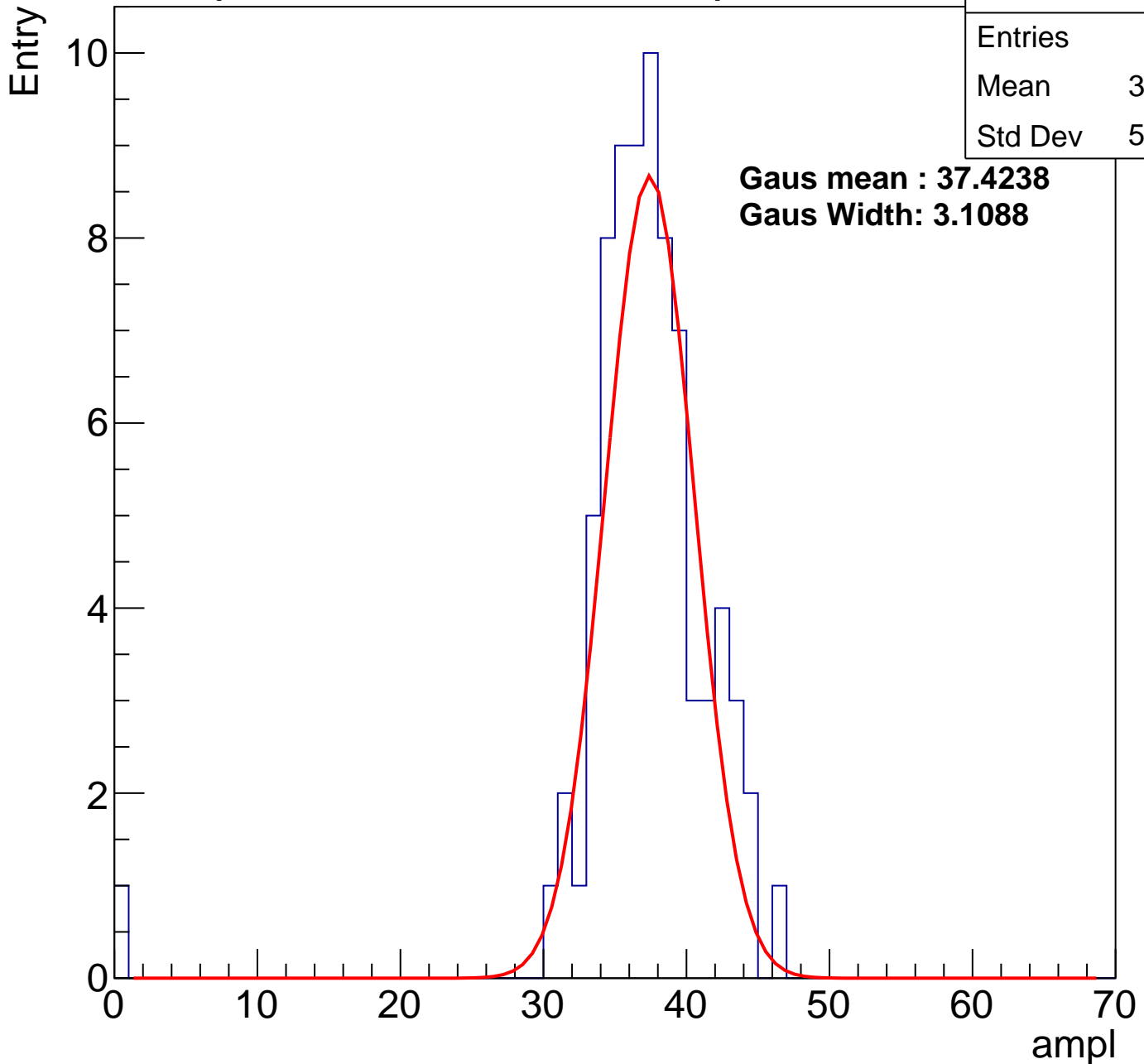
B1L103S, U8-ch5, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	36.64
Std Dev	5.345

Gaus mean : 37.4238

Gaus Width: 3.1088



B1L103S, U8-ch5, adc2

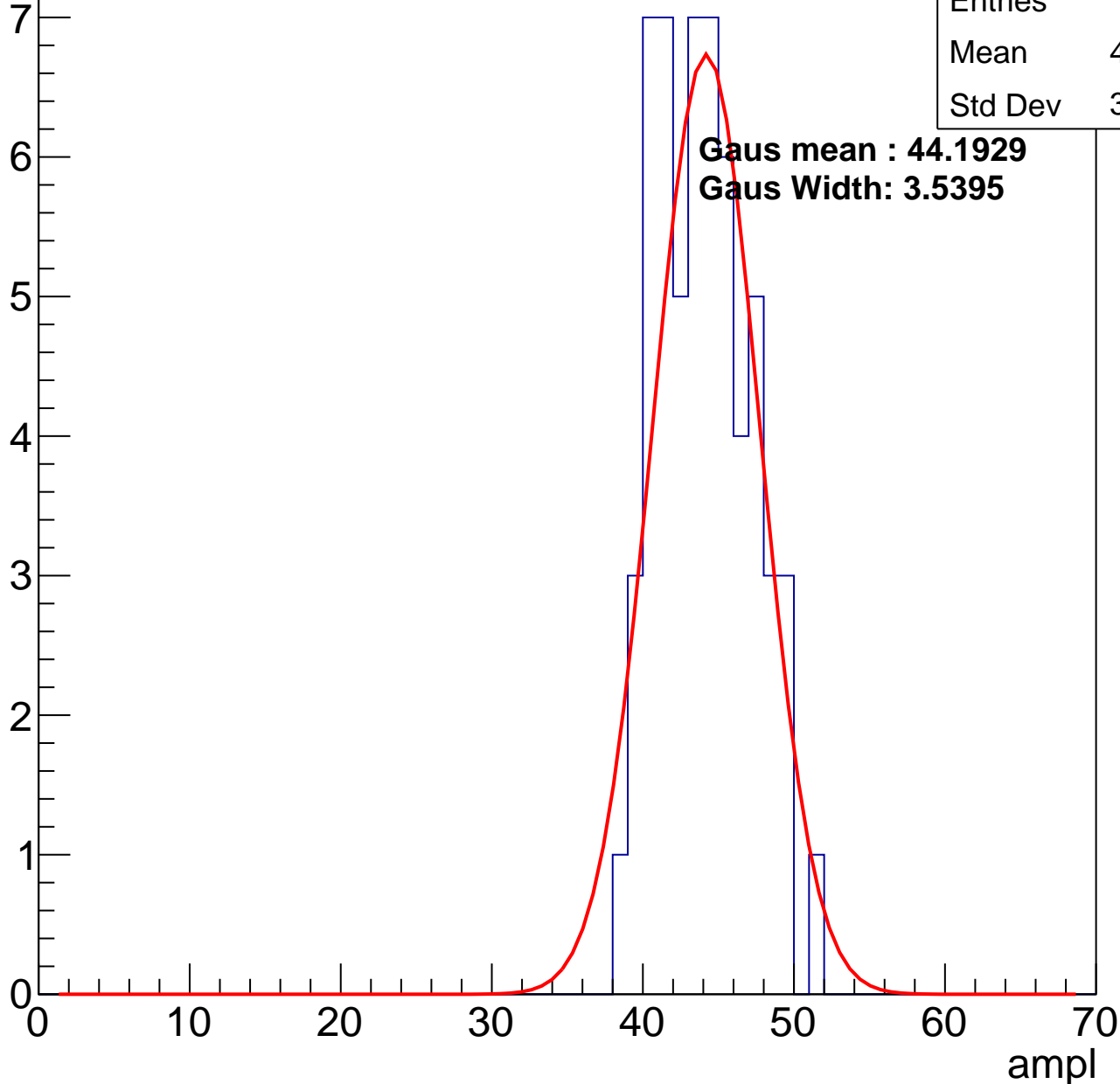
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	43.59
Std Dev	3.032

Gaus mean : 44.1929

Gaus Width: 3.5395

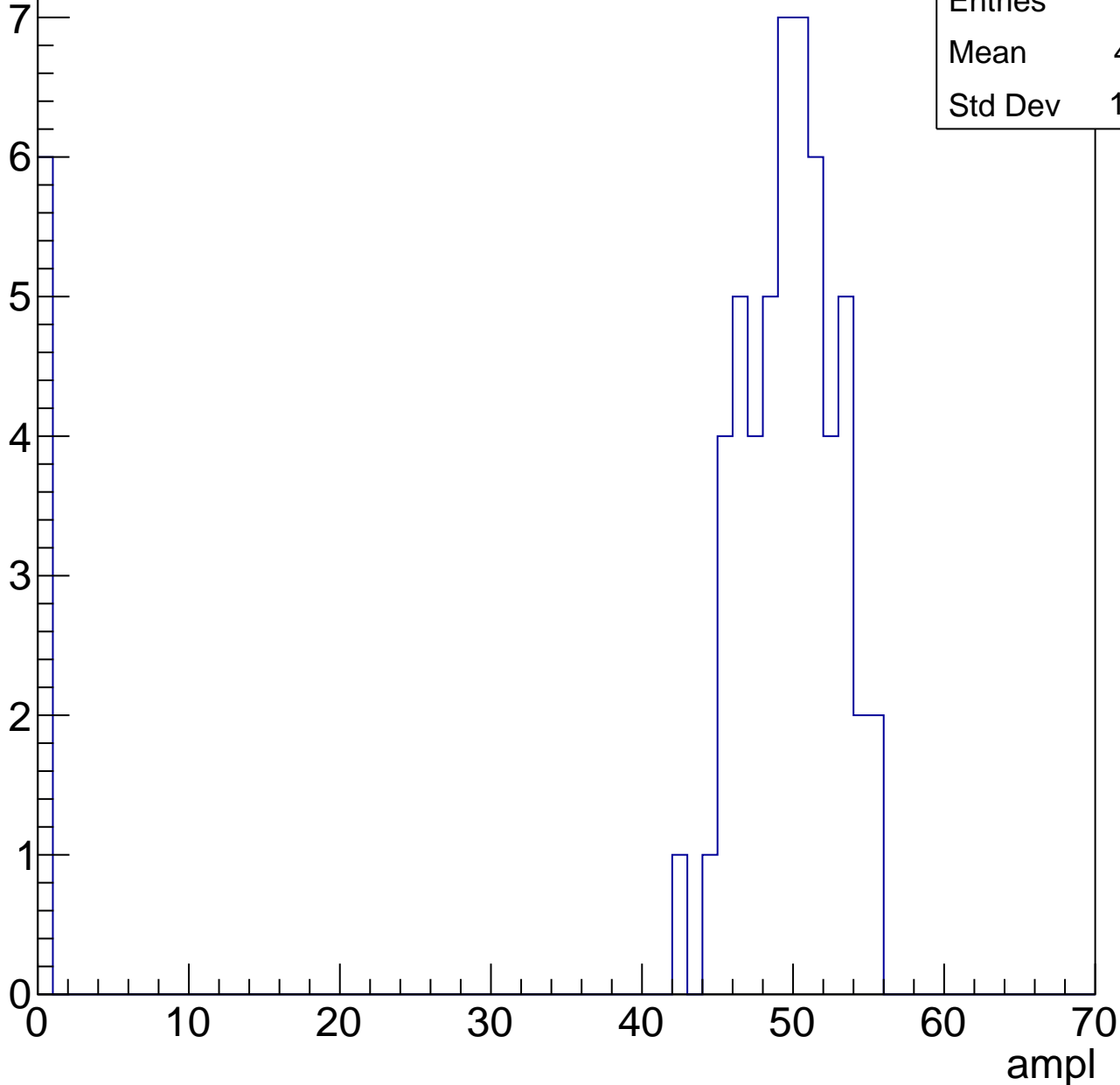


B1L103S, U8-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	44.31
Std Dev	15.17

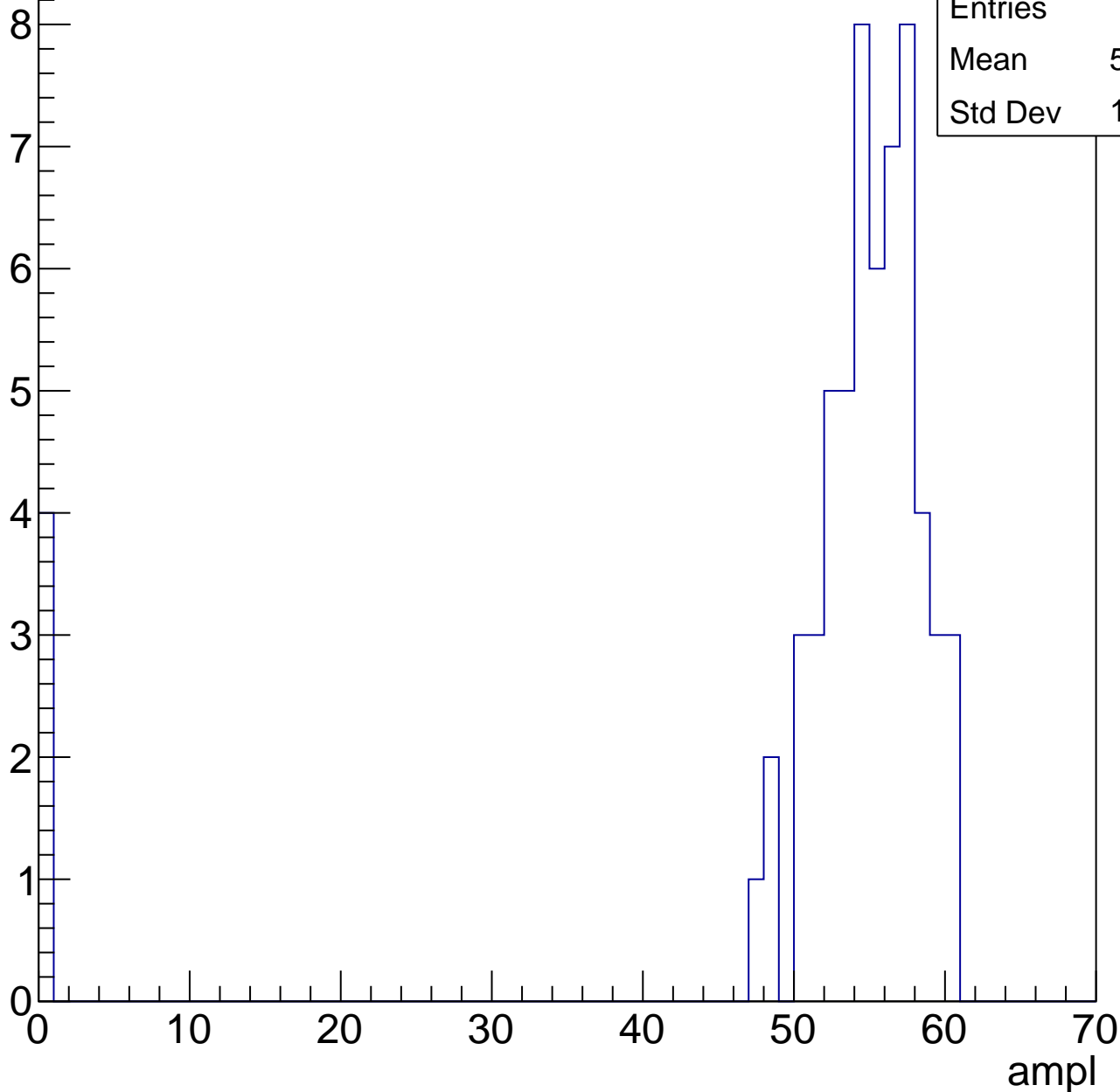


B1L103S, U8-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	51.13
Std Dev	13.75



B1L103S, U8-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	54
Mean	58.72
Std Dev	8.414

Entry

10

8

6

4

2

0

0

10

20

30

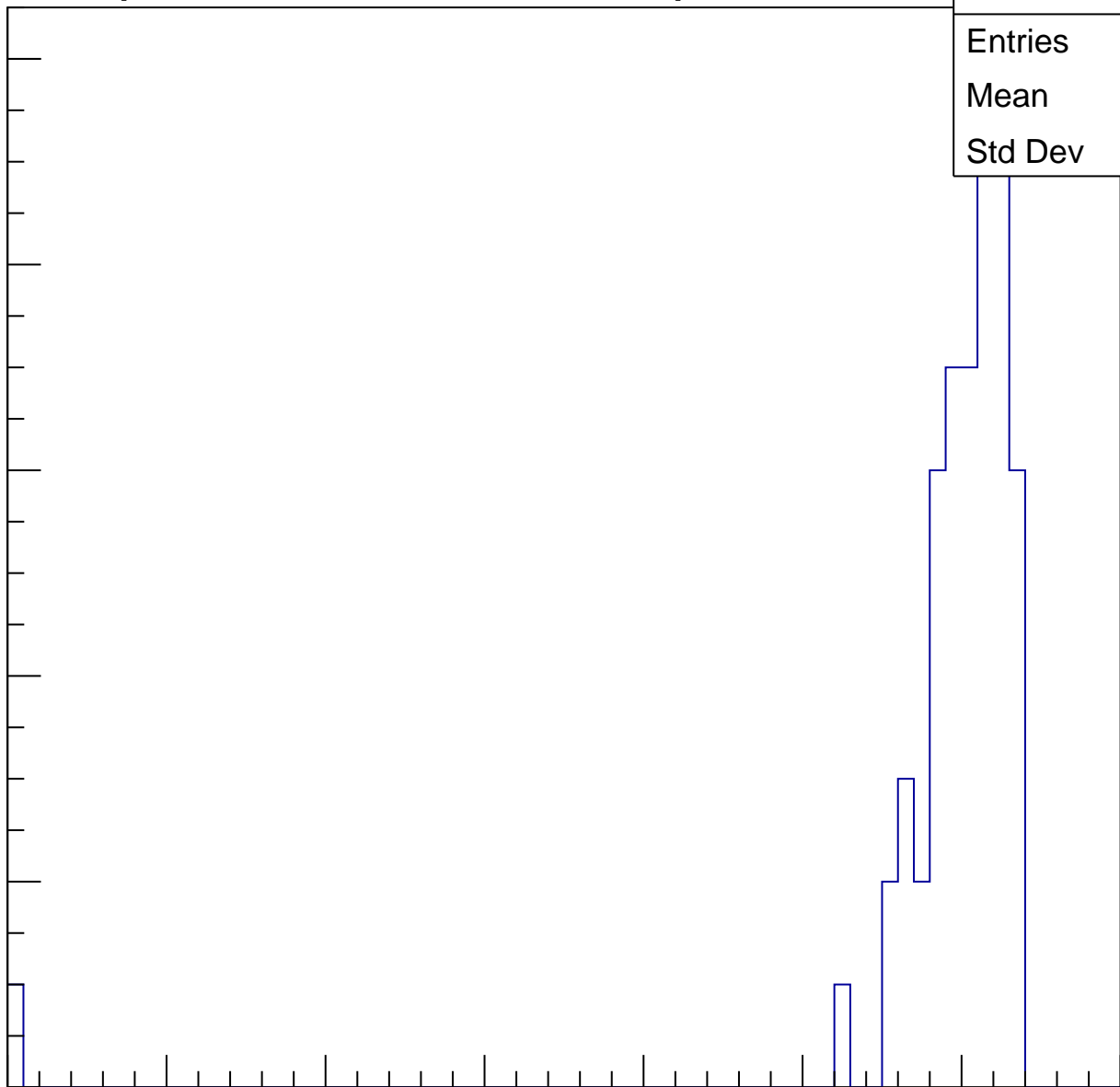
40

50

60

70

ampl



B1L103S, U8-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

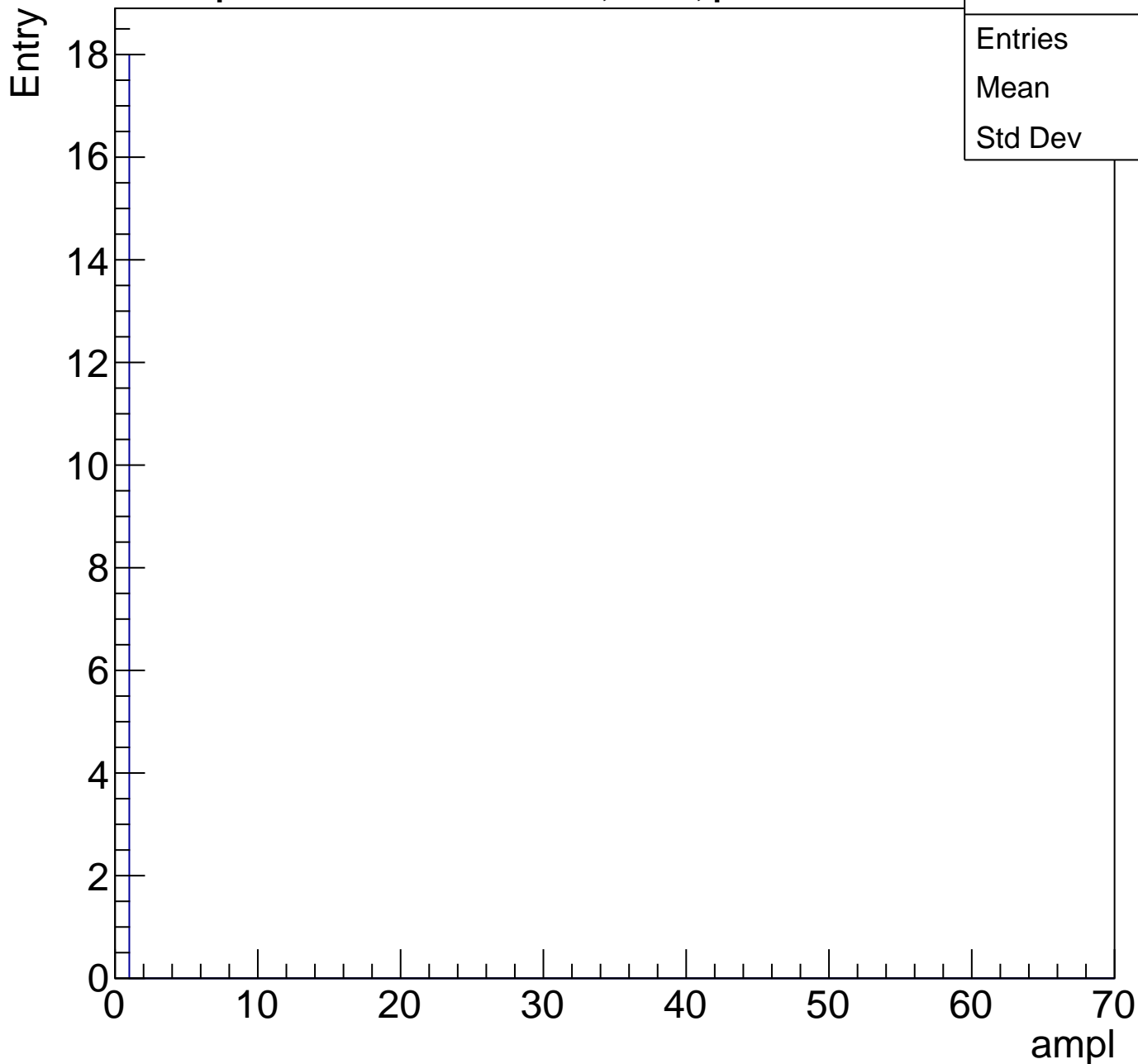


Entries	5
Mean	62.8
Std Dev	0.4

B1L103S, U8-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0



B1L103S, U8-ch6, adc0

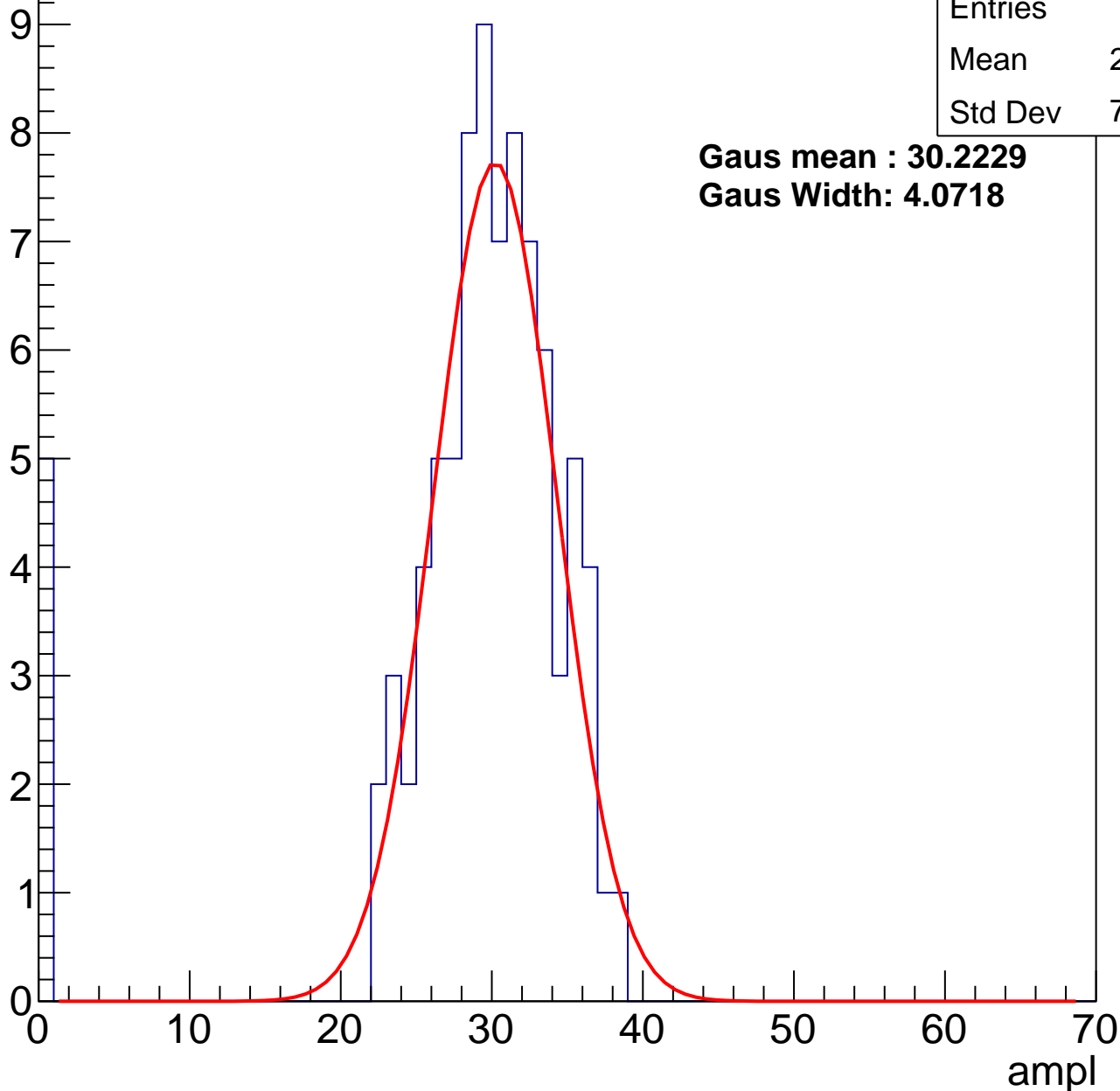
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	28.08
Std Dev	7.912

Gaus mean : 30.2229

Gaus Width: 4.0718



B1L103S, U8-ch6, adc1

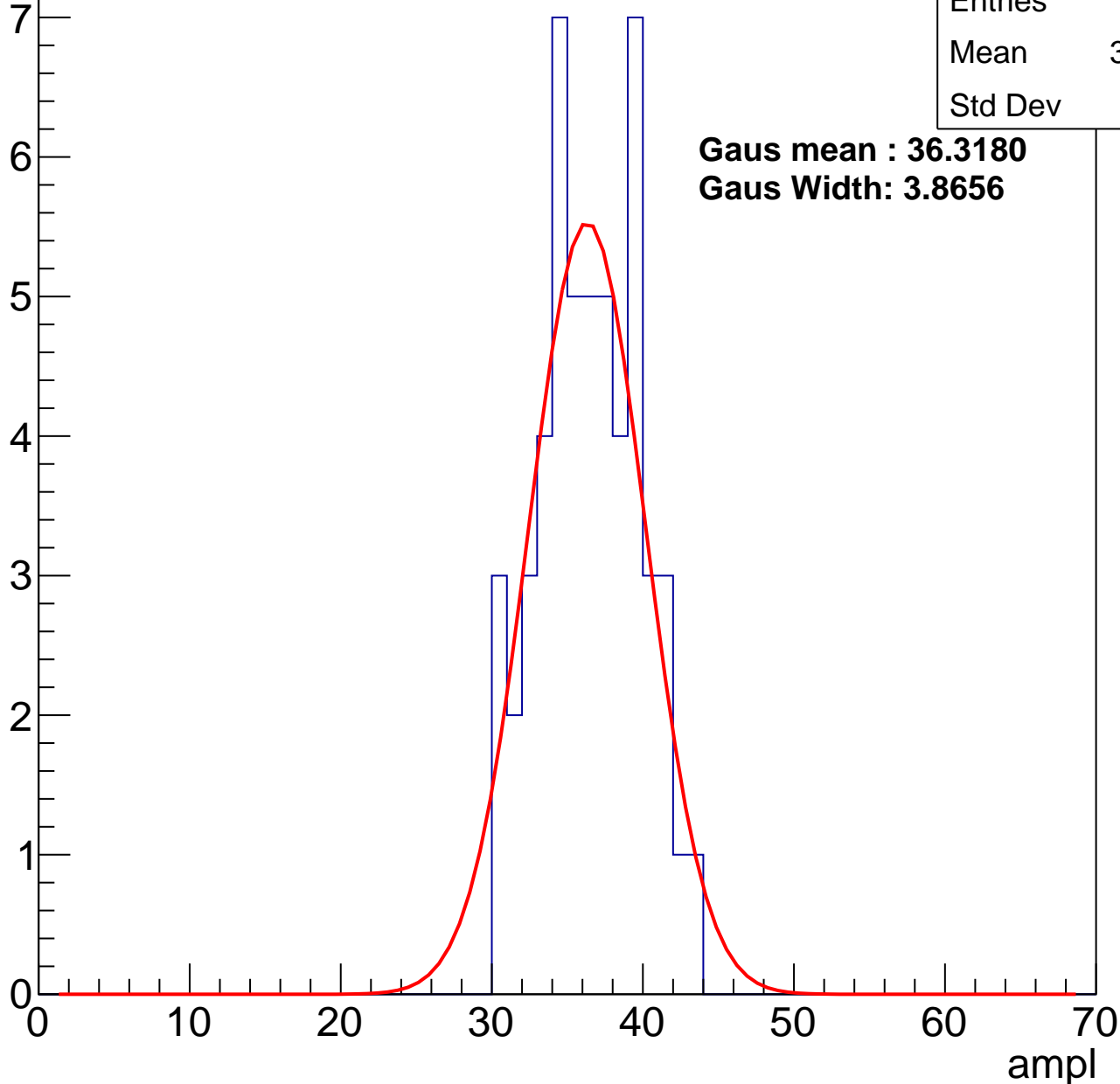
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	36.06
Std Dev	3.27

Gaus mean : 36.3180

Gaus Width: 3.8656



B1L103S, U8-ch6, adc2

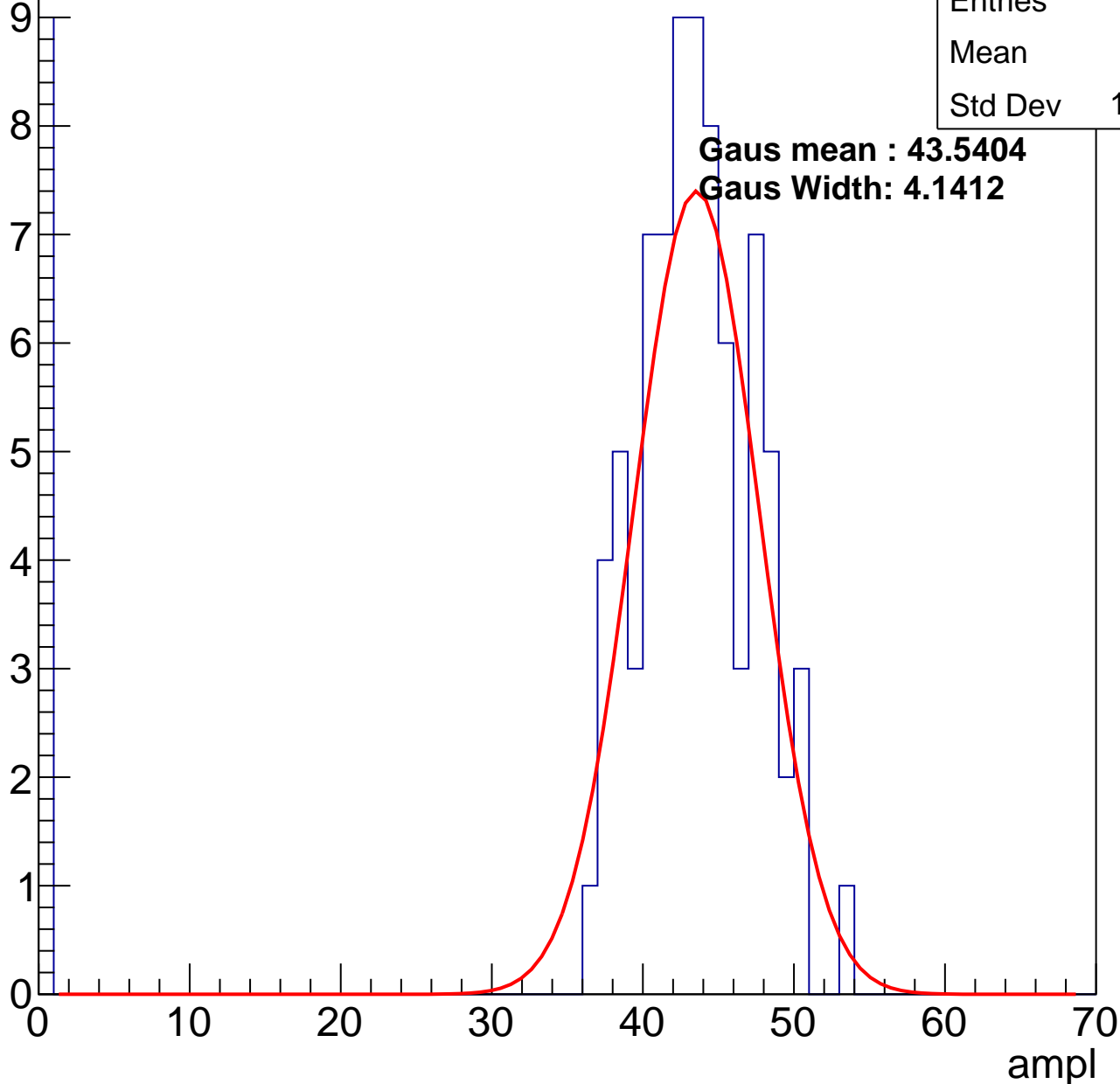
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	38.8
Std Dev	13.47

Gaus mean : 43.5404

Gaus Width: 4.1412

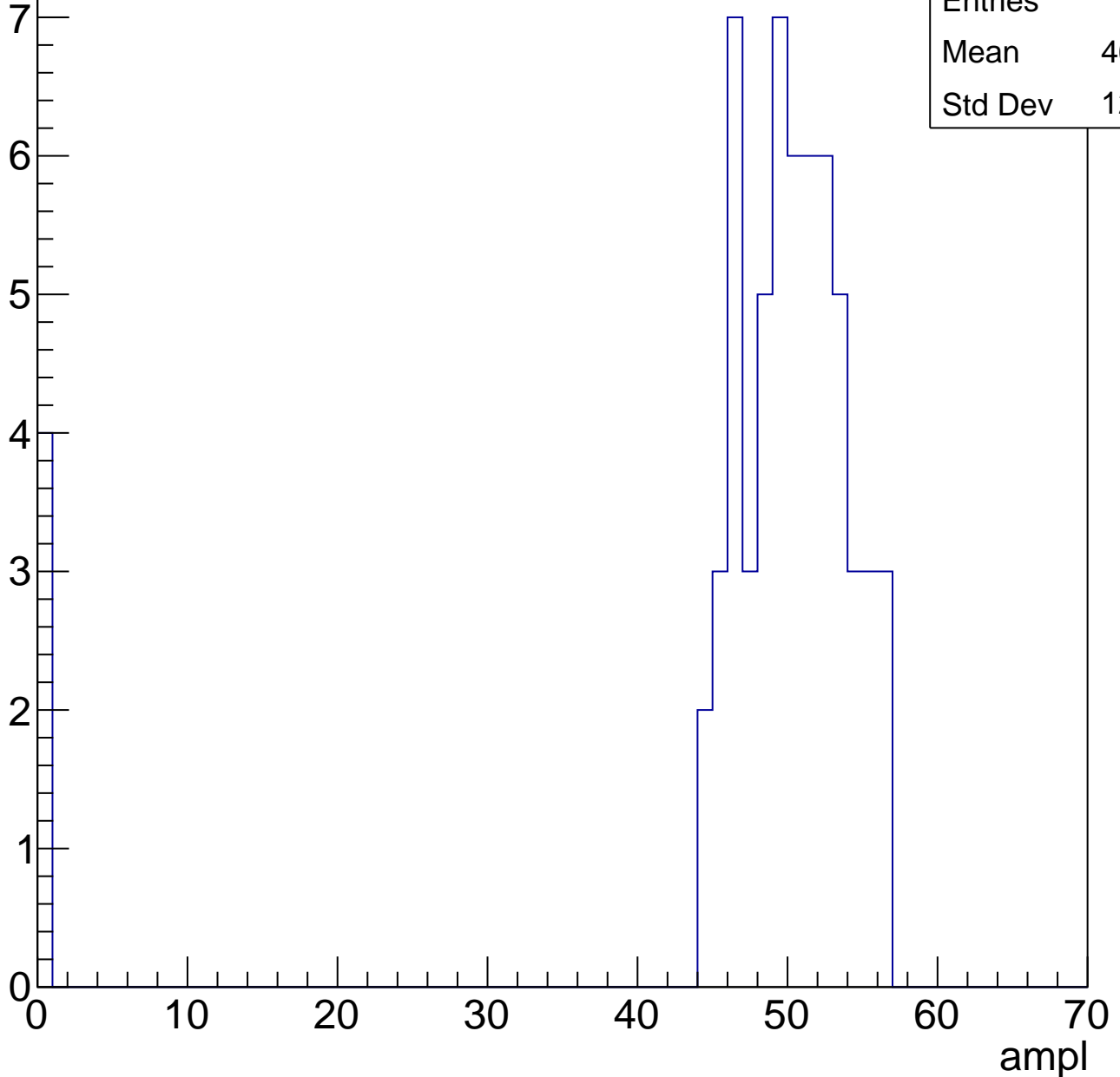


B1L103S, U8-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	46.78
Std Dev	12.58



B1L103S, U8-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	55.57
Std Dev	3.322

Entry

10

8

6

4

2

0

0

10

20

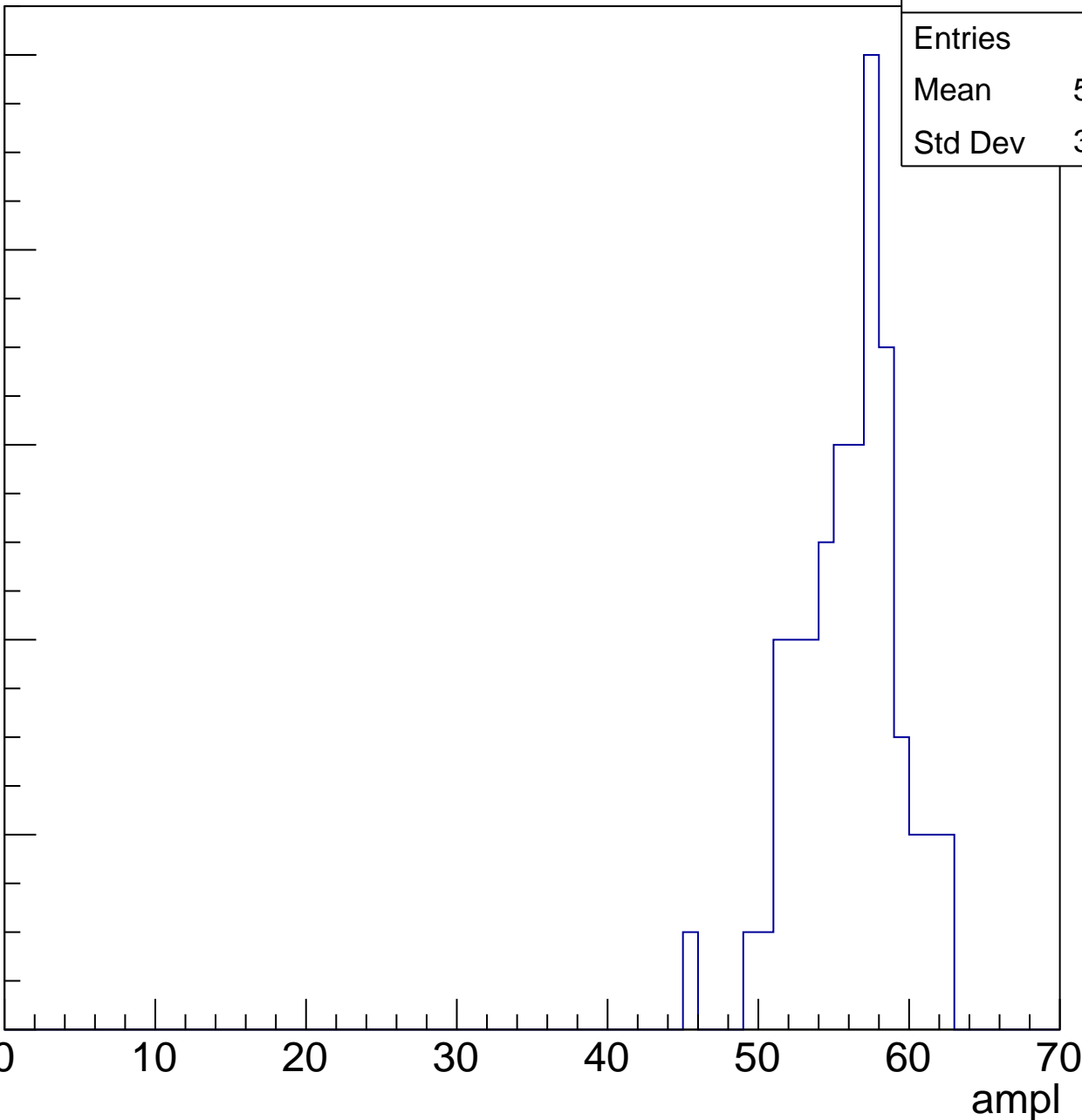
30

40

50

60

ampl



B1L103S, U8-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

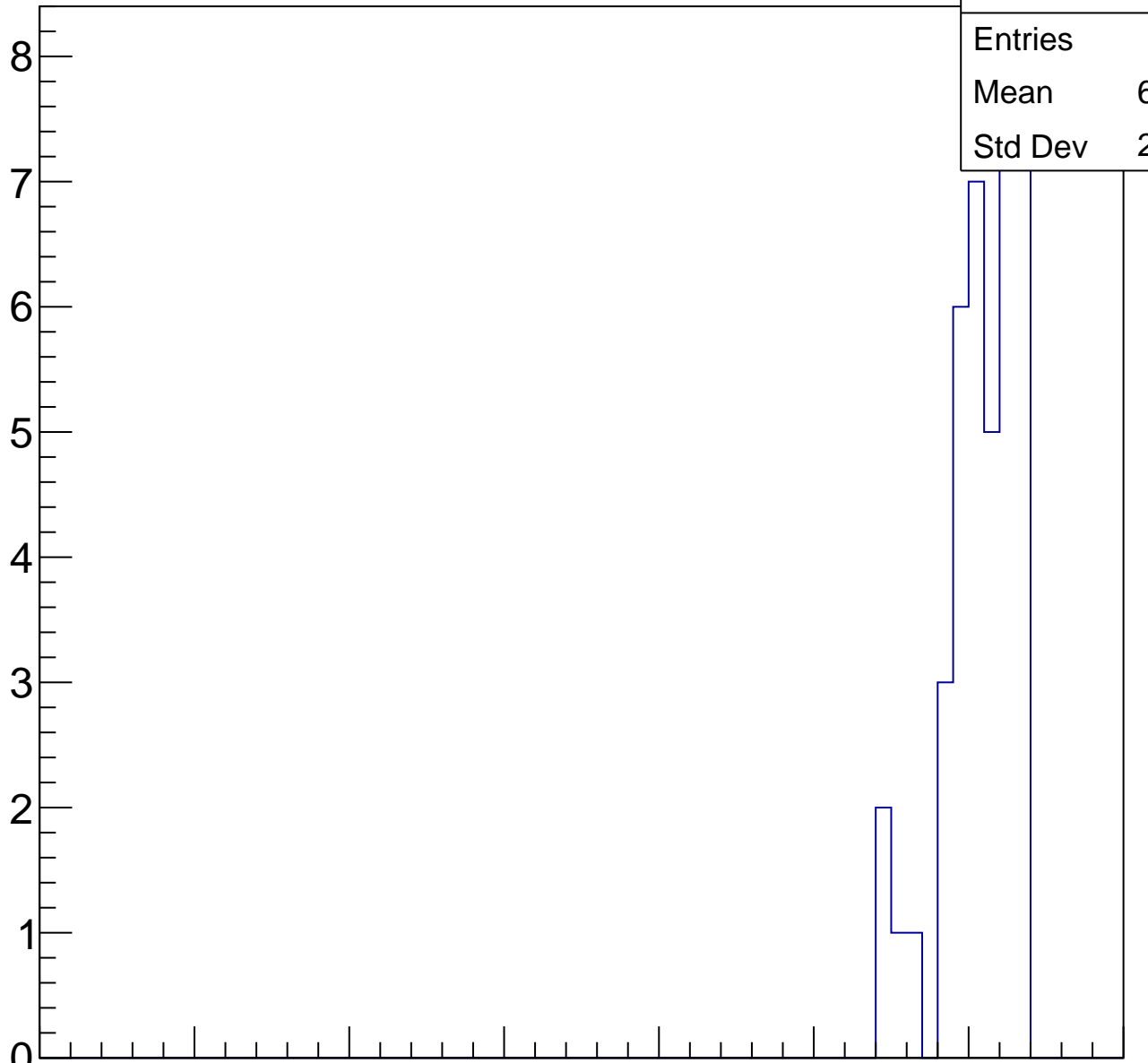
Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.29
Std Dev	2.402

ampl

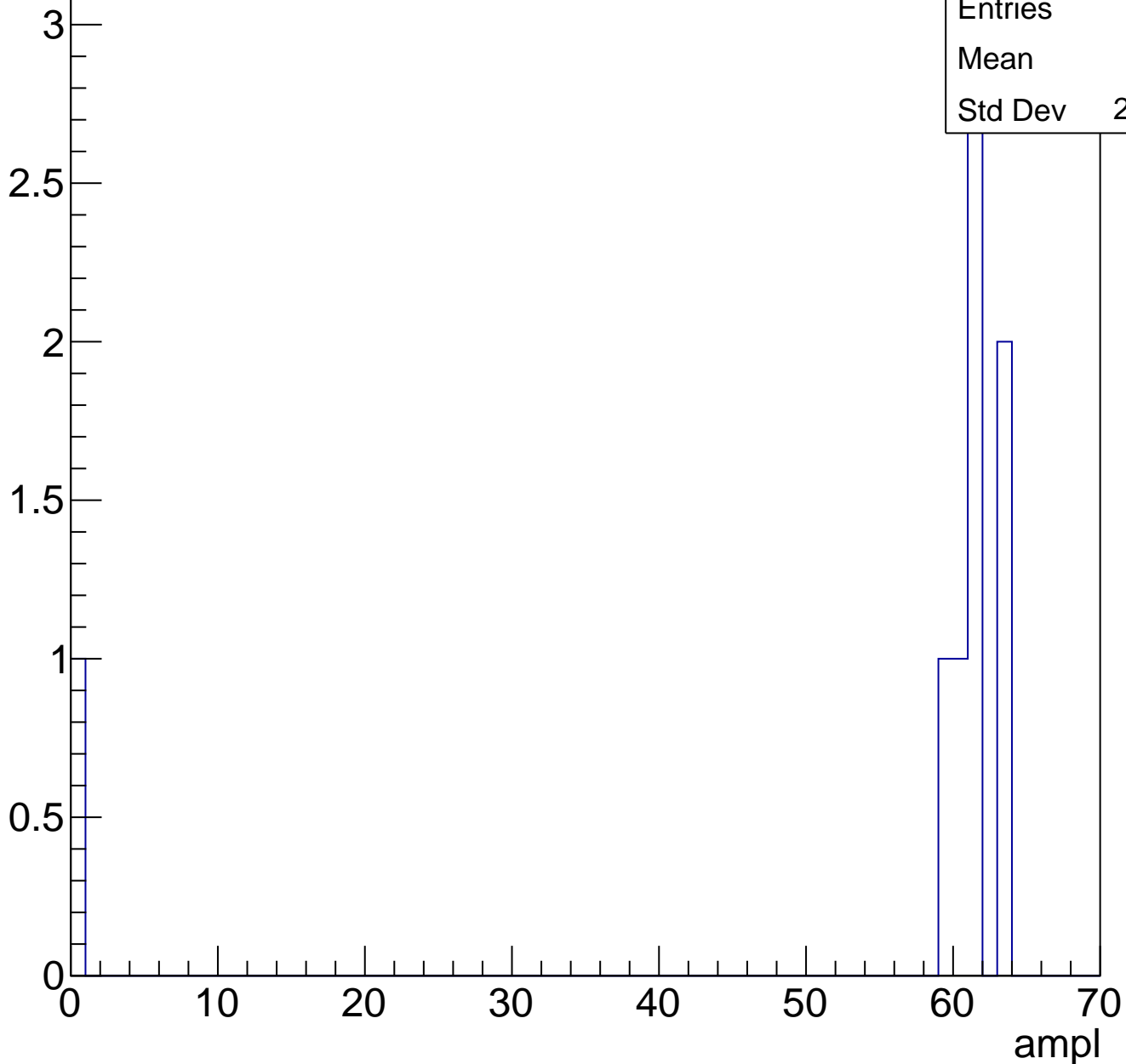
0 10 20 30 40 50 60 70



B1L103S, U8-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

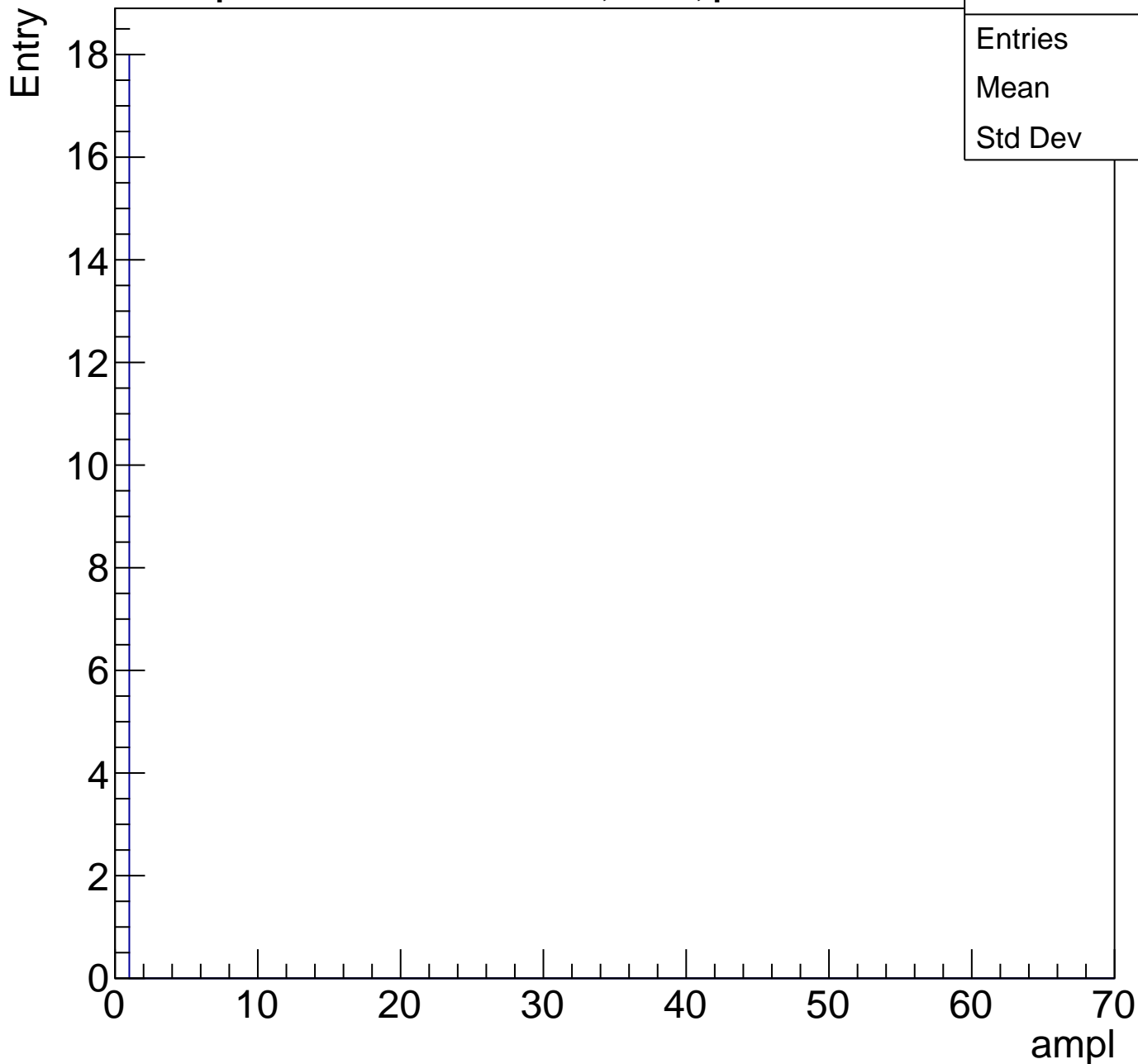
Entry



B1L103S, U8-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0



B1L103S, U8-ch7, adc0

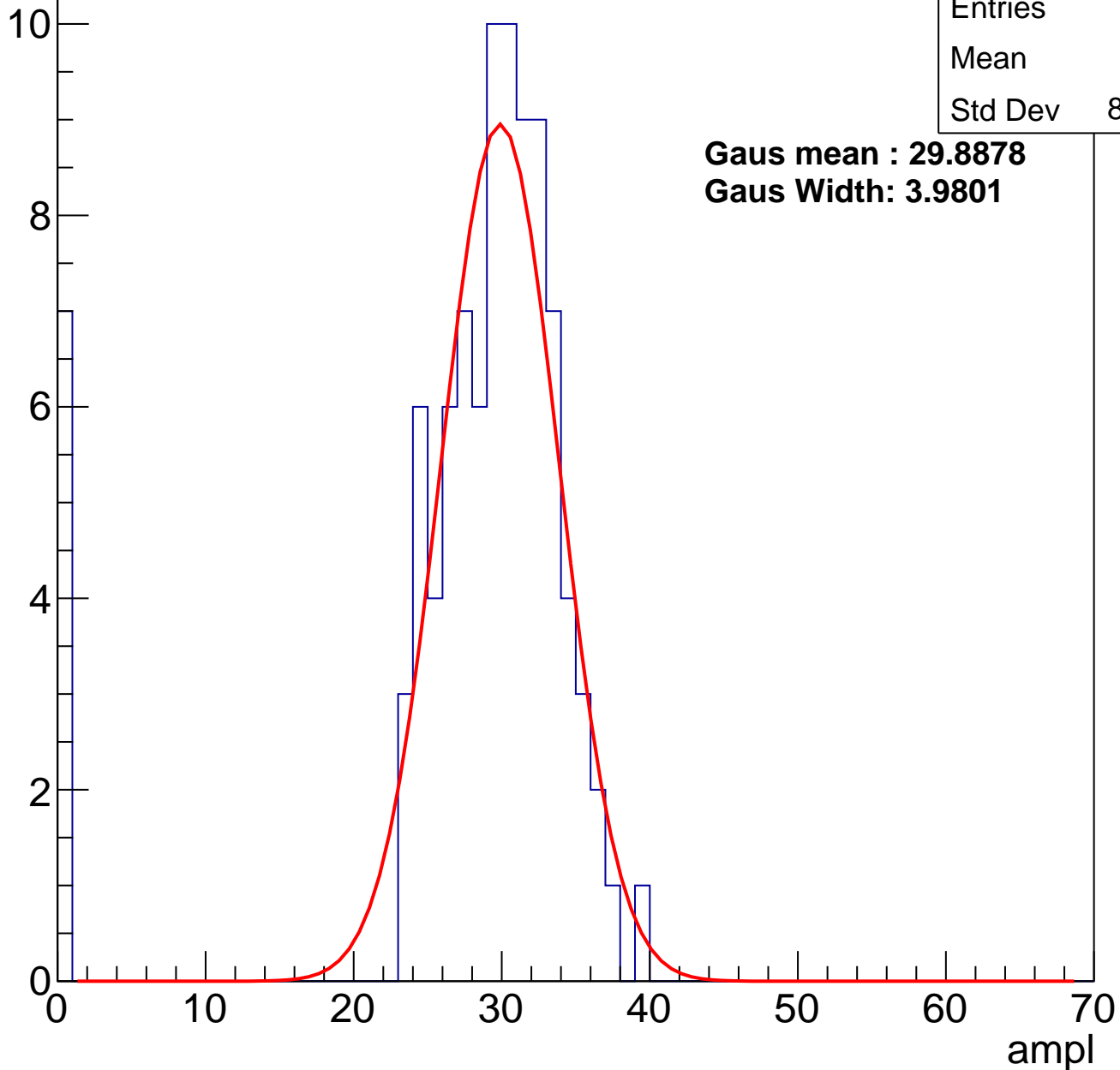
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	27.4
Std Dev	8.437

Gaus mean : 29.8878

Gaus Width: 3.9801

Entry



B1L103S, U8-ch7, adc1

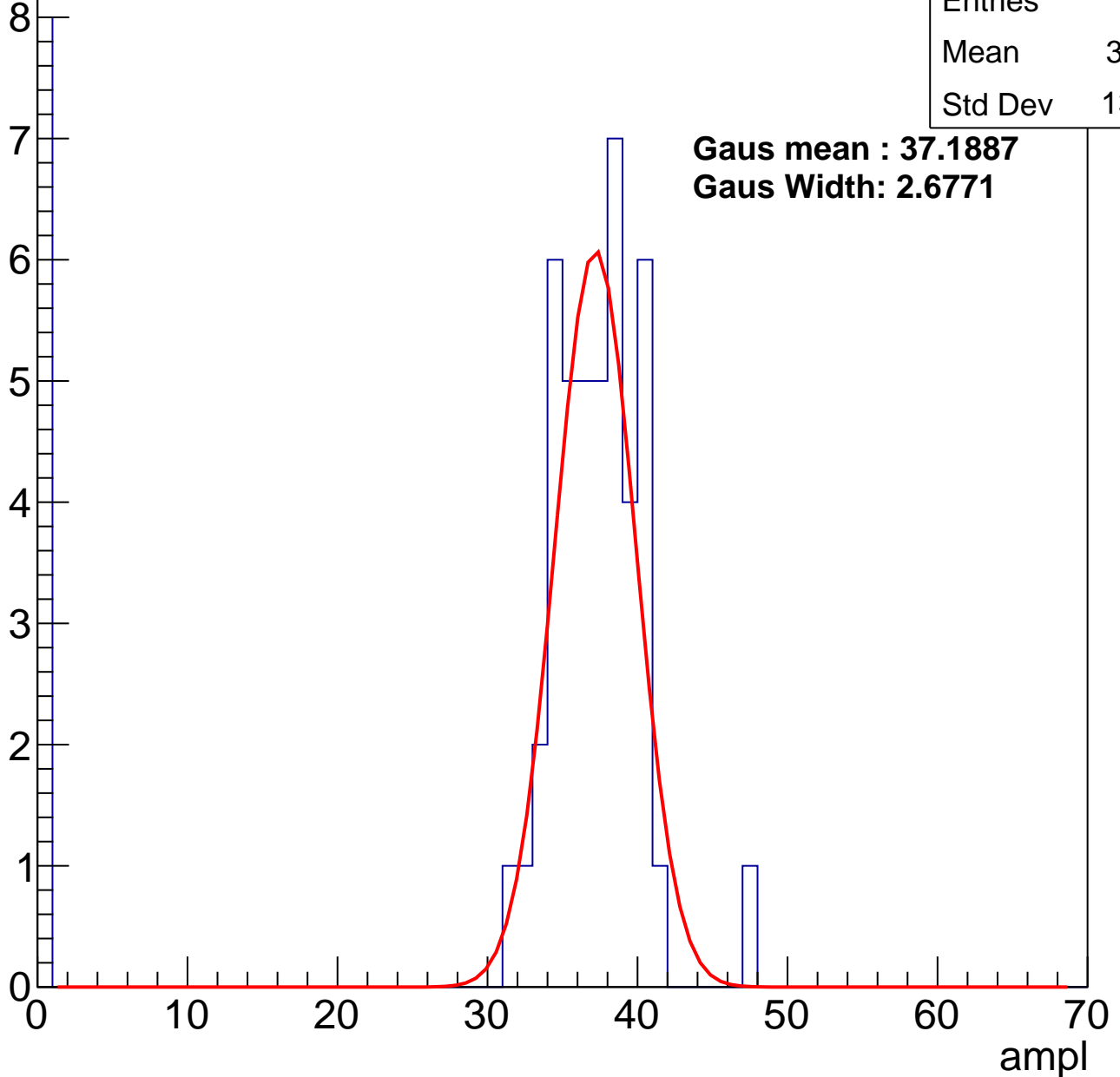
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	31.21
Std Dev	13.57

Gaus mean : 37.1887

Gaus Width: 2.6771



B1L103S, U8-ch7, adc2

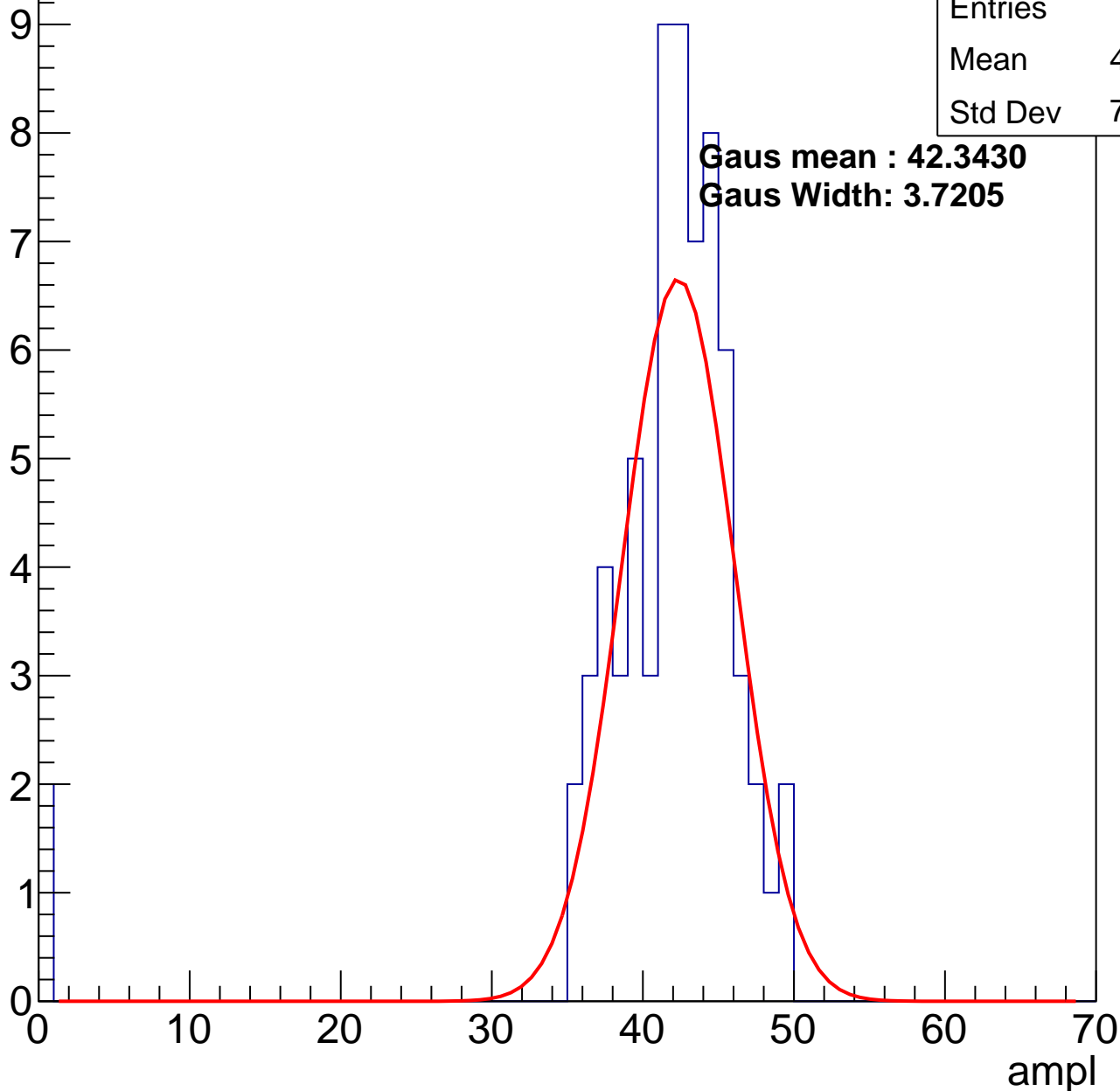
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.62
Std Dev	7.748

Gaus mean : 42.3430

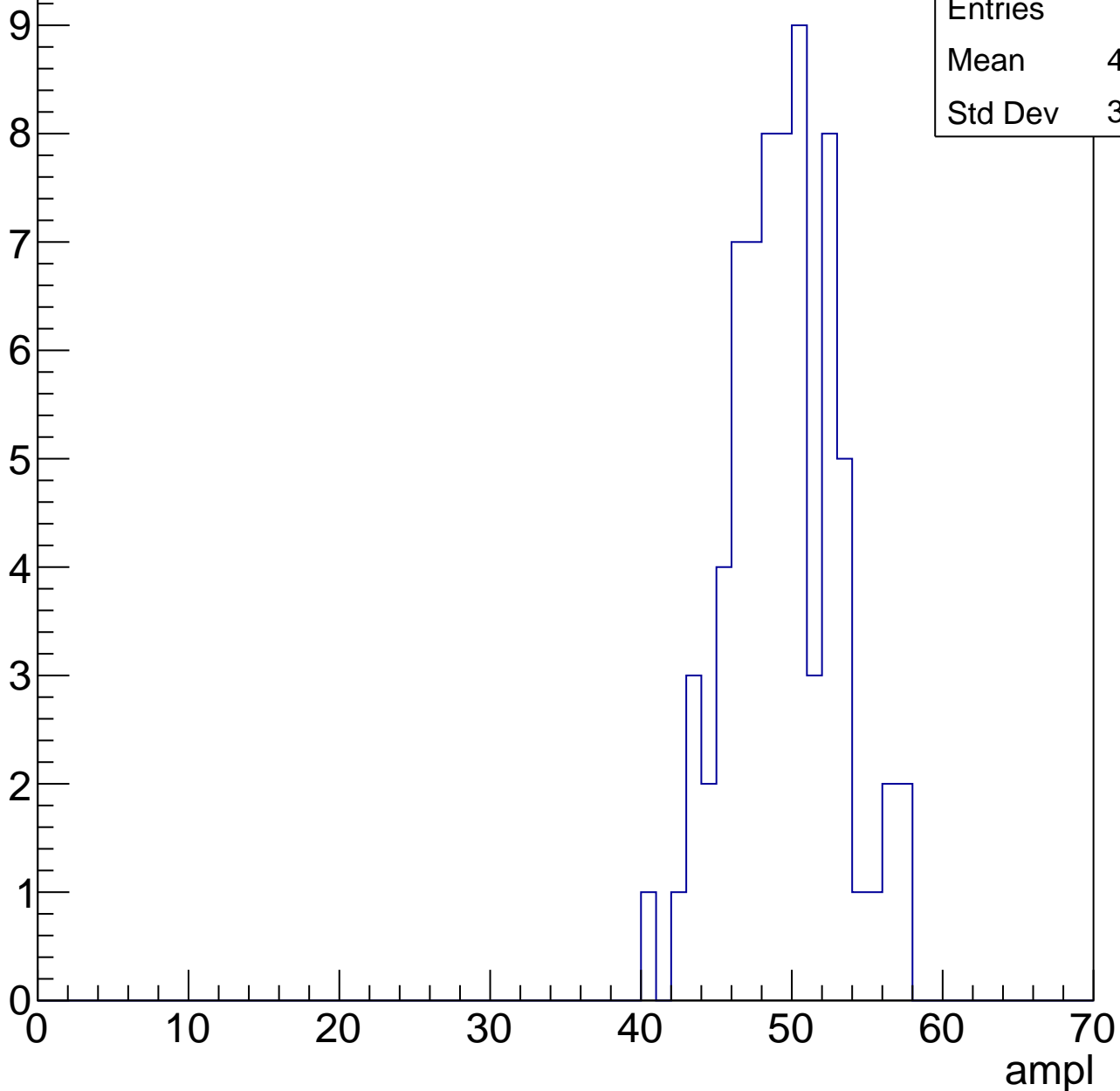
Gaus Width: 3.7205



B1L103S, U8-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

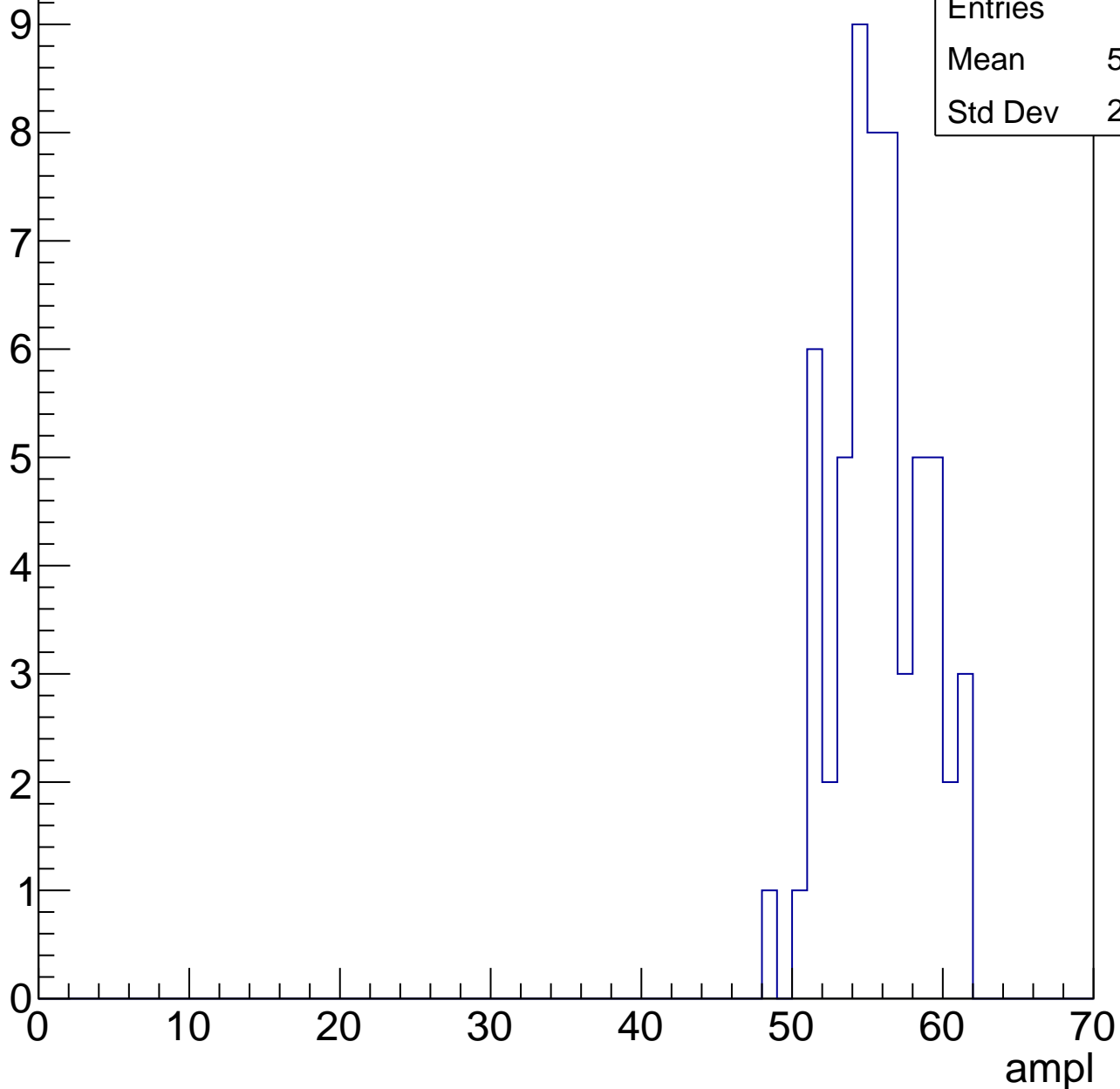


B1L103S, U8-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	55.28
Std Dev	2.982

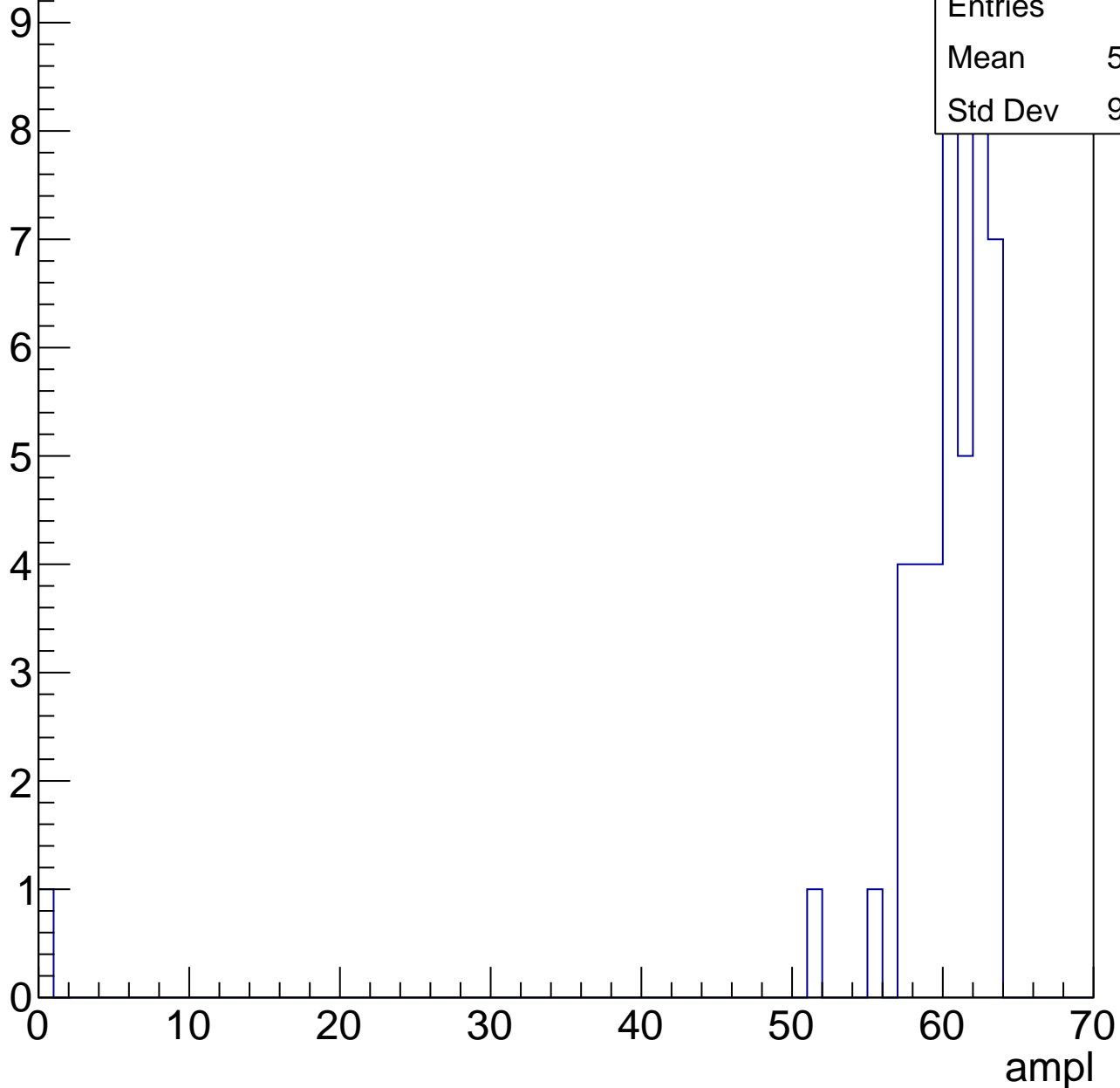


B1L103S, U8-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

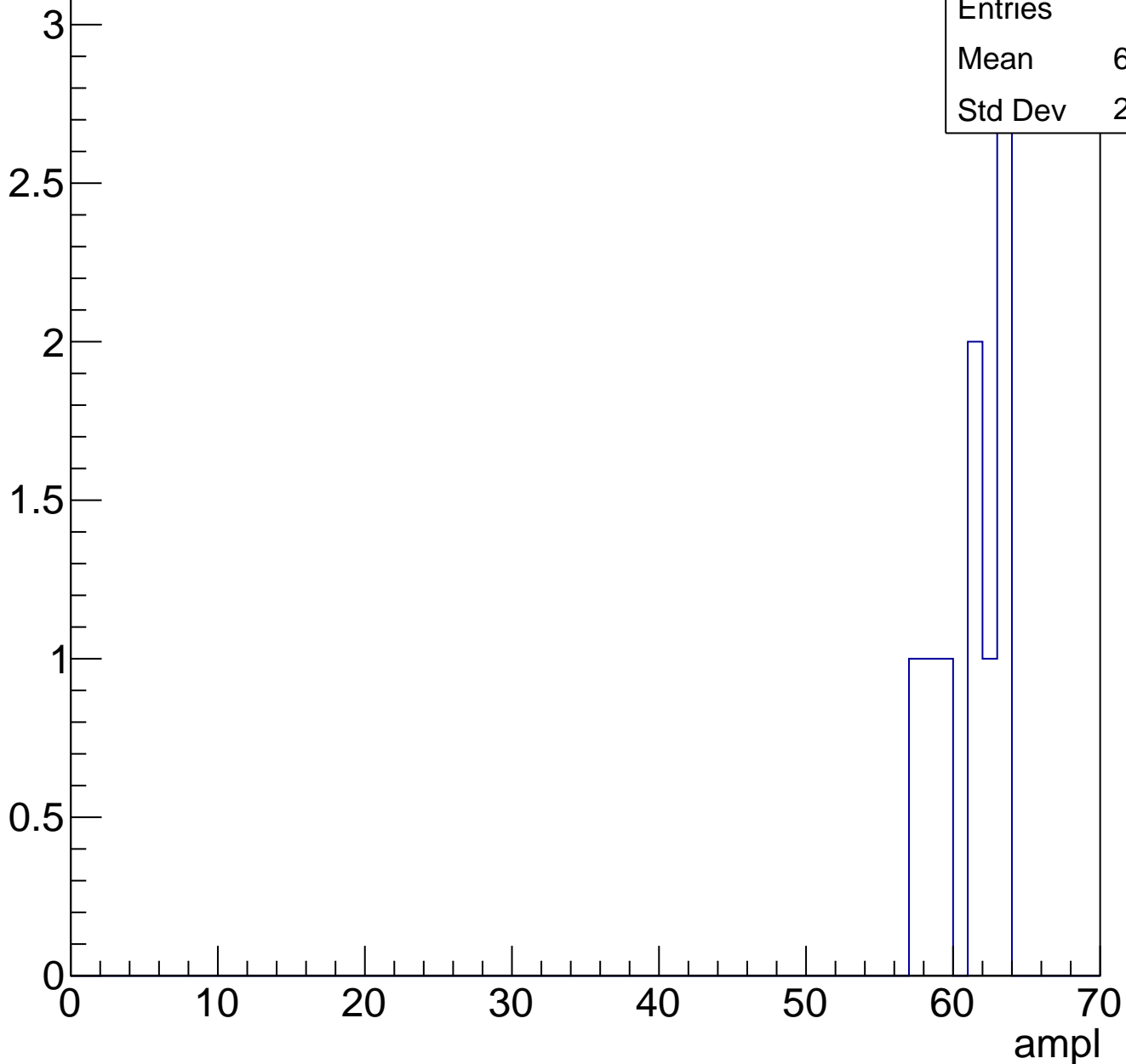
Entries	44
Mean	58.77
Std Dev	9.293



B1L103S, U8-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

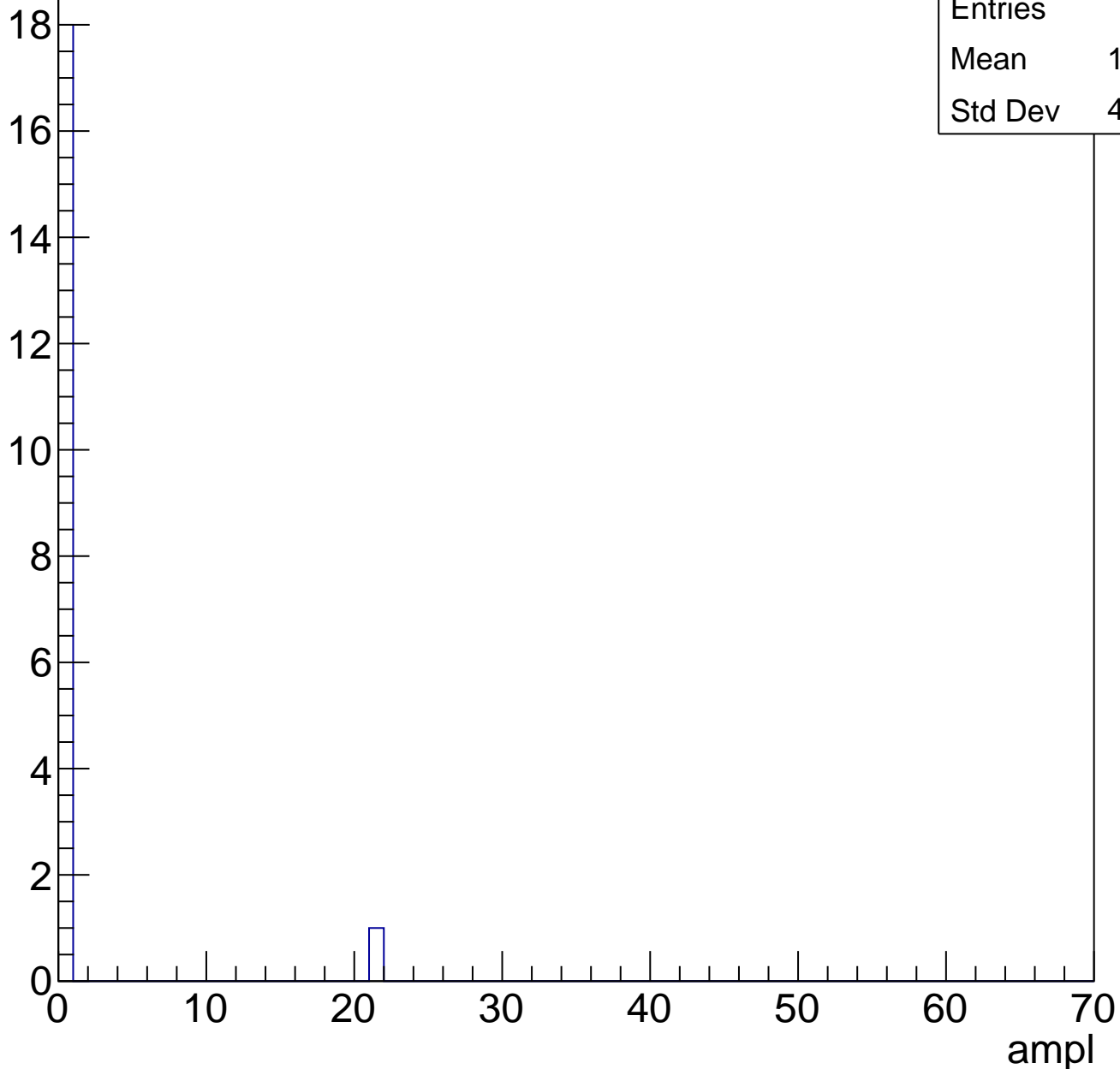


B1L103S, U8-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U8-ch8, adc0

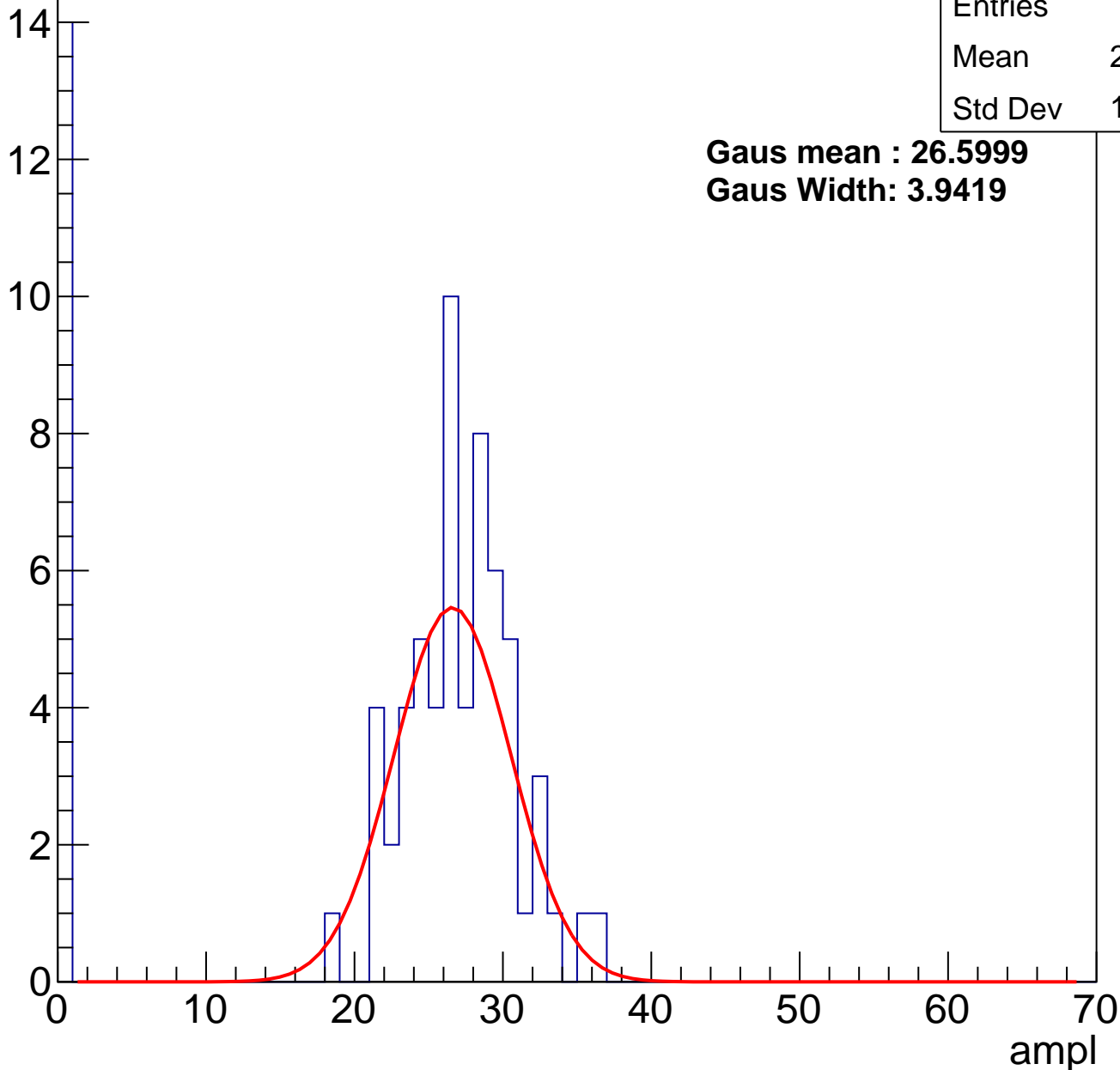
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	21.69
Std Dev	10.95

Gaus mean : 26.5999

Gaus Width: 3.9419

Entry



B1L103S, U8-ch8, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	30.61
Std Dev	11.26

Gaus mean : 35.0691

Gaus Width: 3.8817

Entry

10

8

6

4

2

0

0

10

20

30

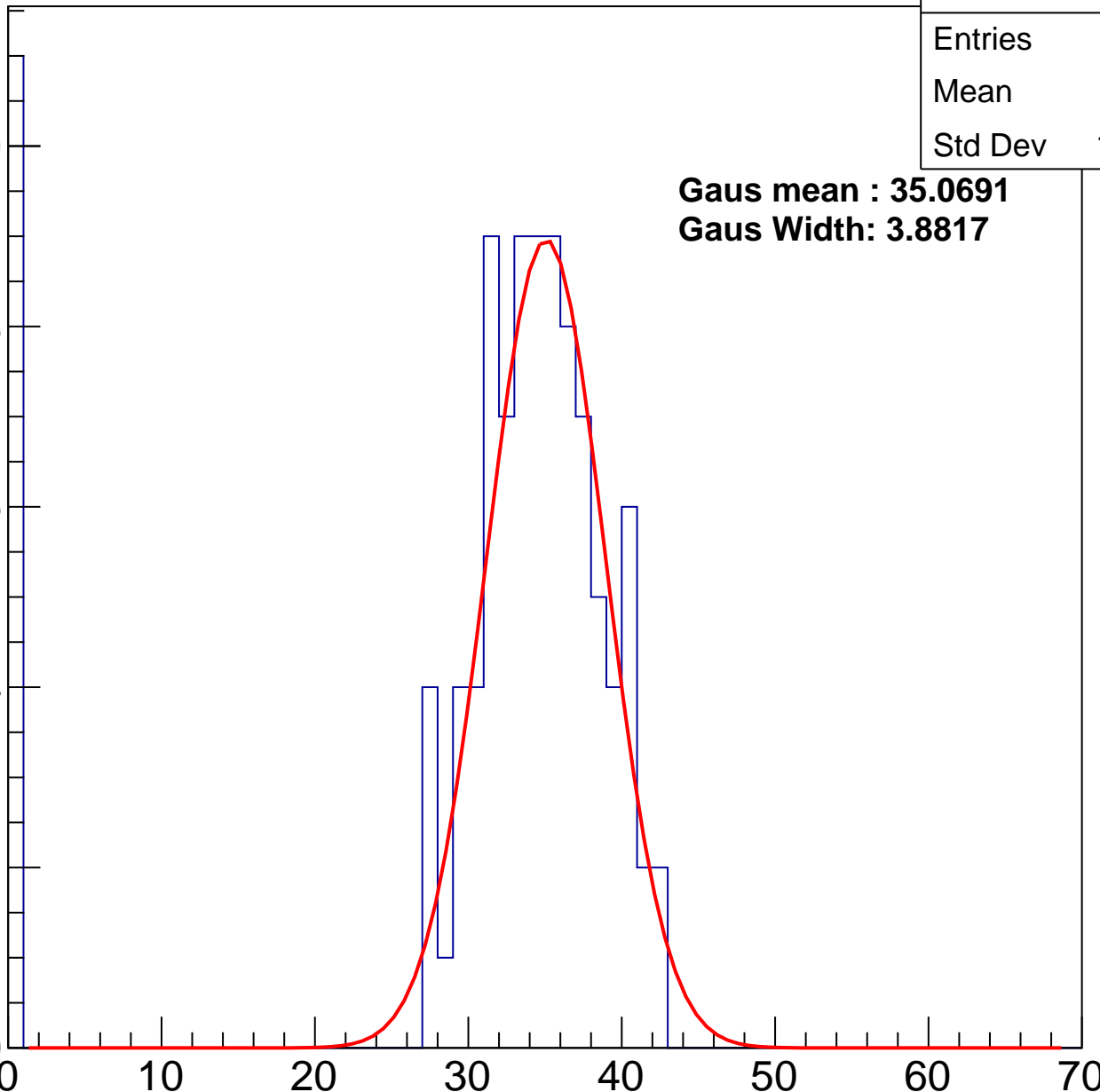
40

50

60

70

ampl



B1L103S, U8-ch8, adc2

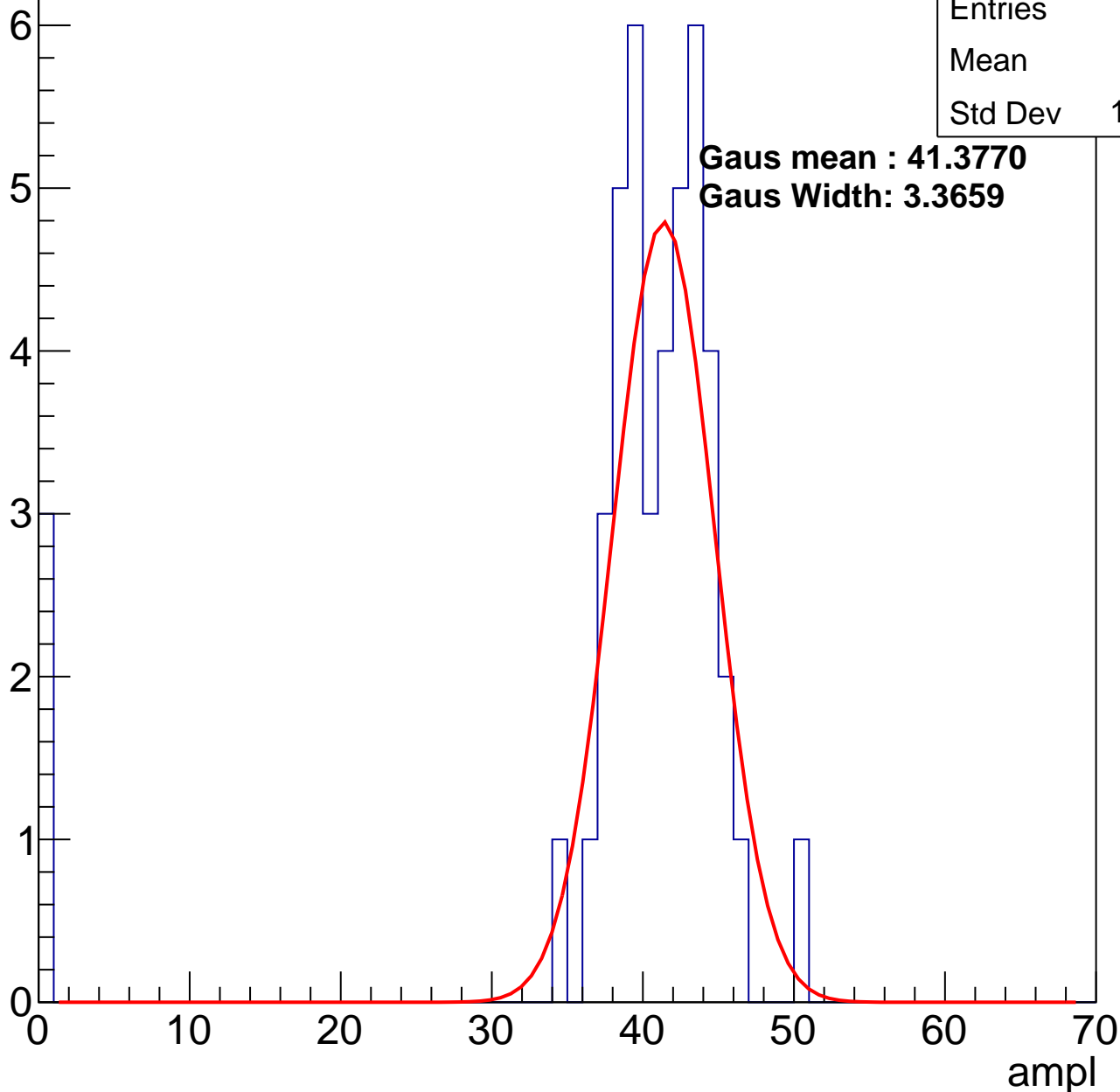
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	38.2
Std Dev	10.63

Gaus mean : 41.3770

Gaus Width: 3.3659



B1L103S, U8-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	47.18
Std Dev	3.569

Entry

10

8

6

4

2

0

0

10

20

30

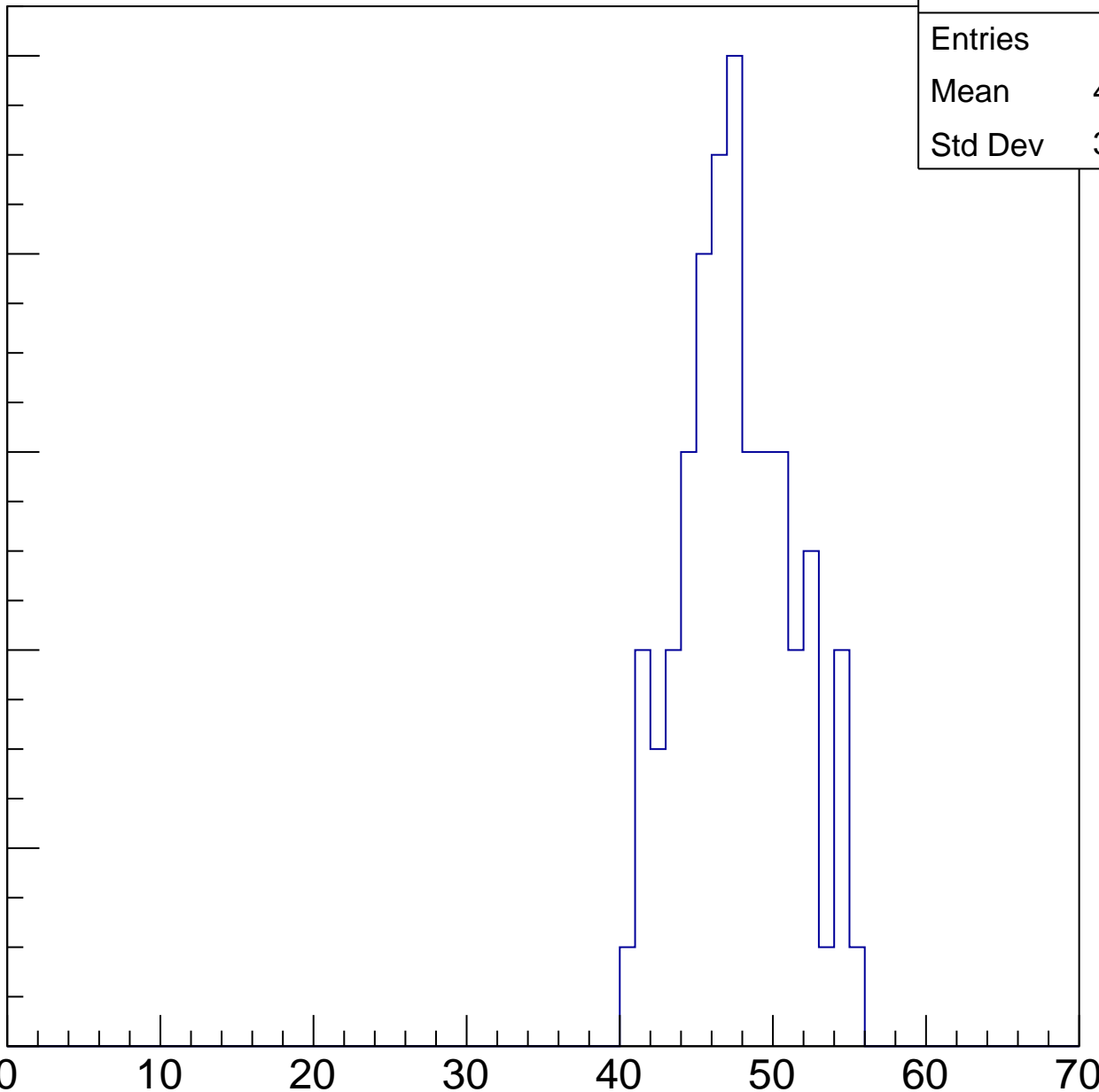
40

50

60

70

ampl

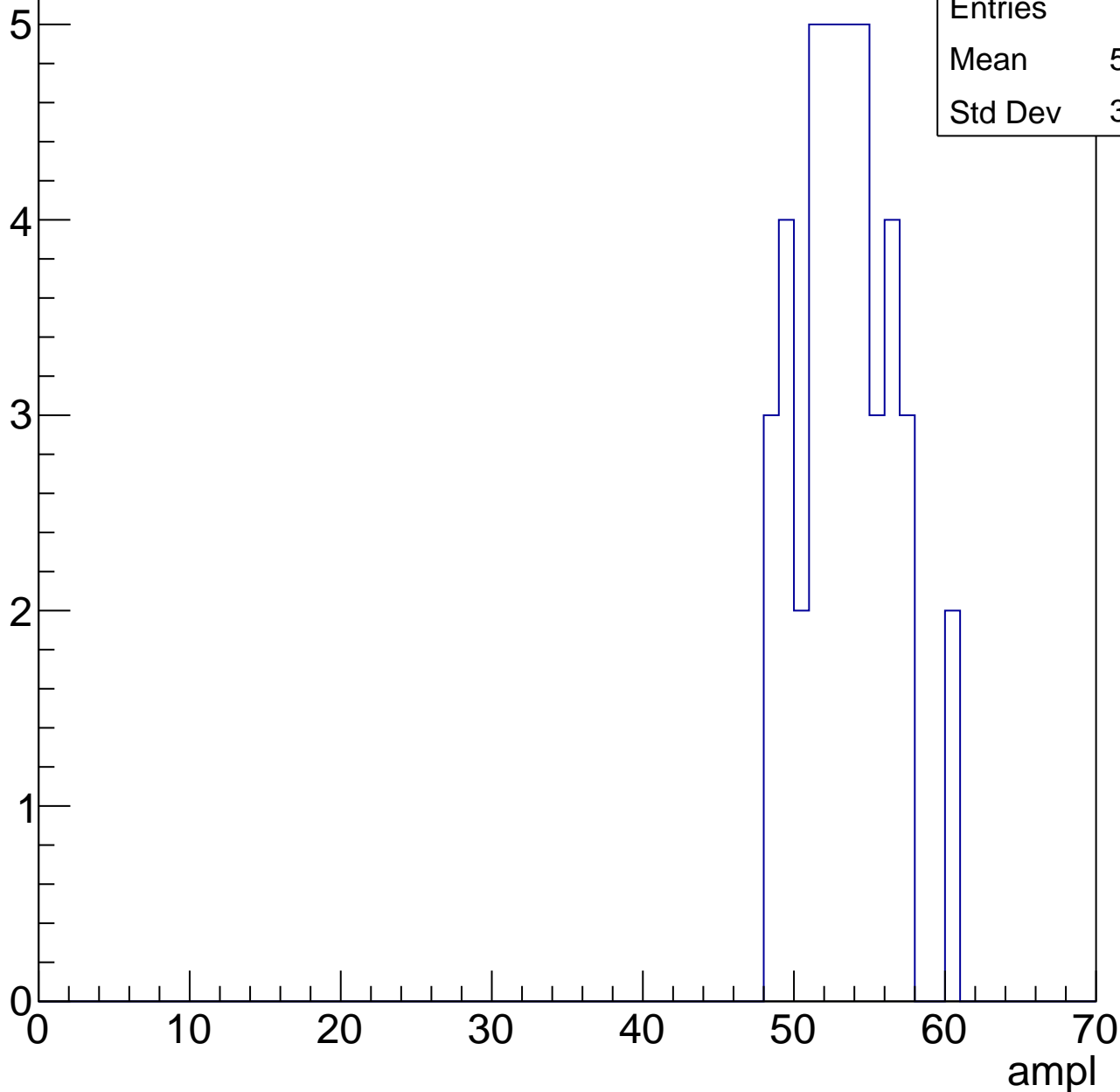


B1L103S, U8-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	52.93
Std Dev	3.048



B1L103S, U8-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	58
Std Dev	3.307

Entry

10

8

6

4

2

0

0

10

20

30

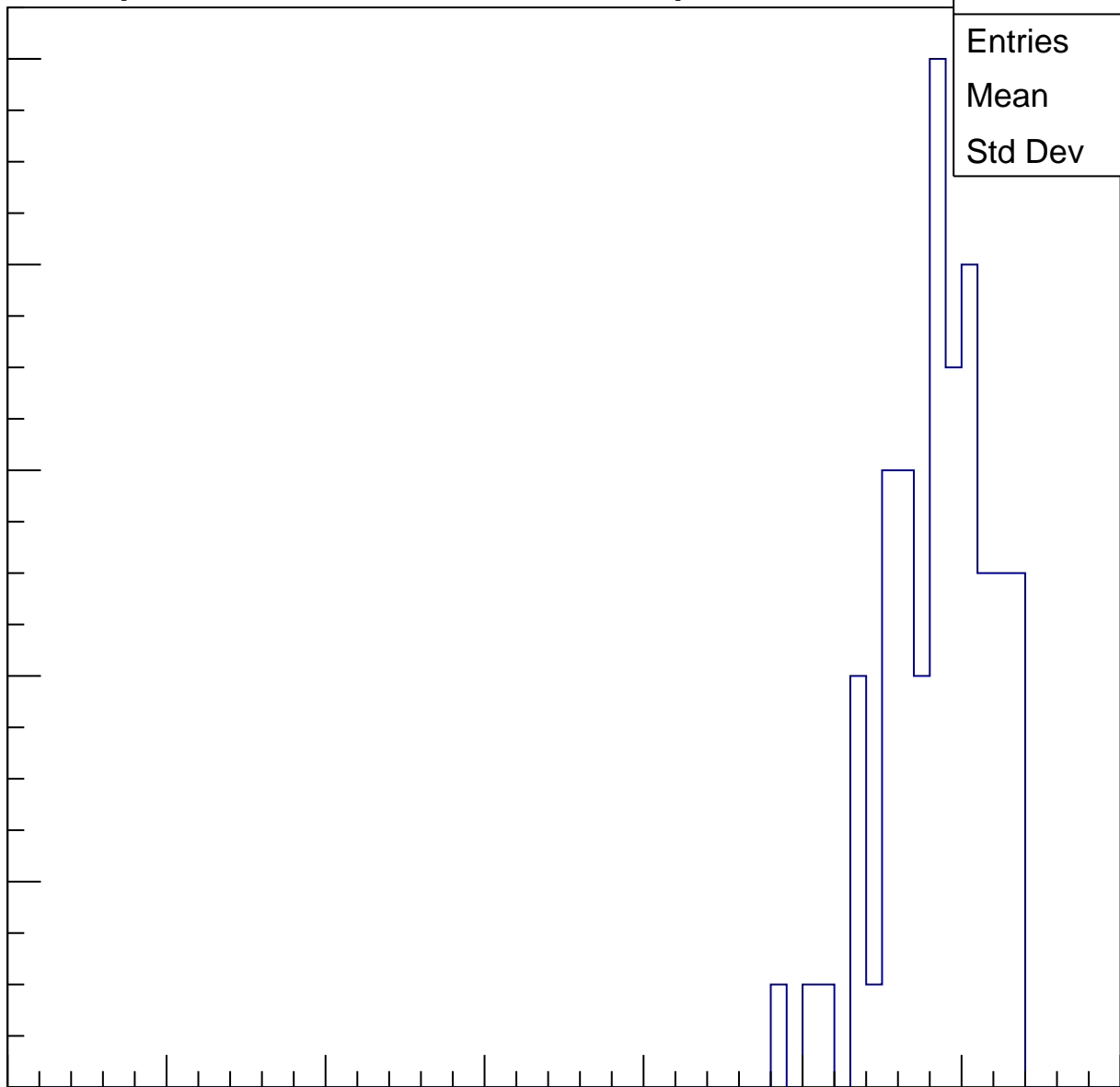
40

50

60

70

ampl

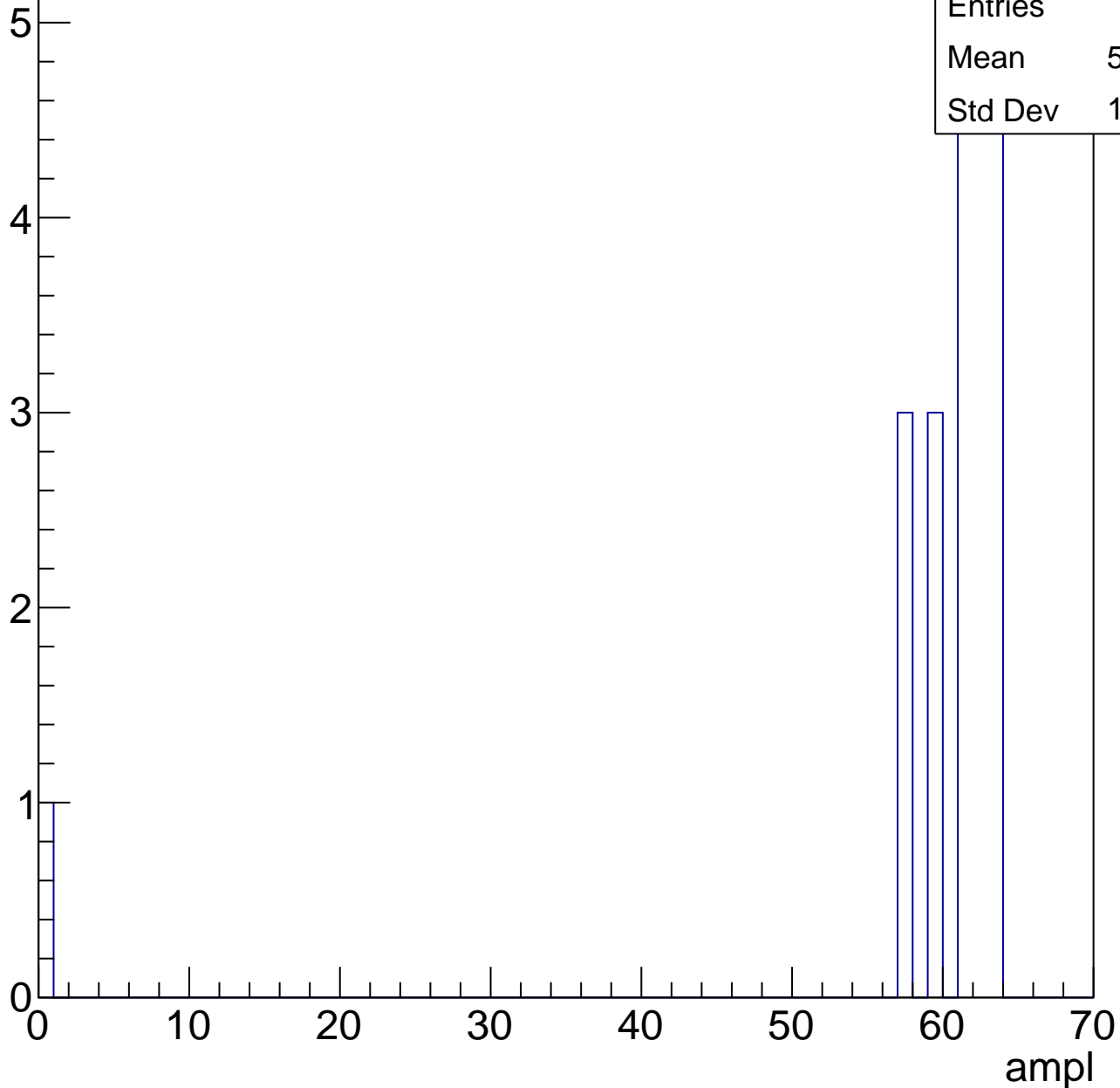


B1L103S, U8-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.09
Std Dev	12.83



B1L103S, U8-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U8-ch9, adc0

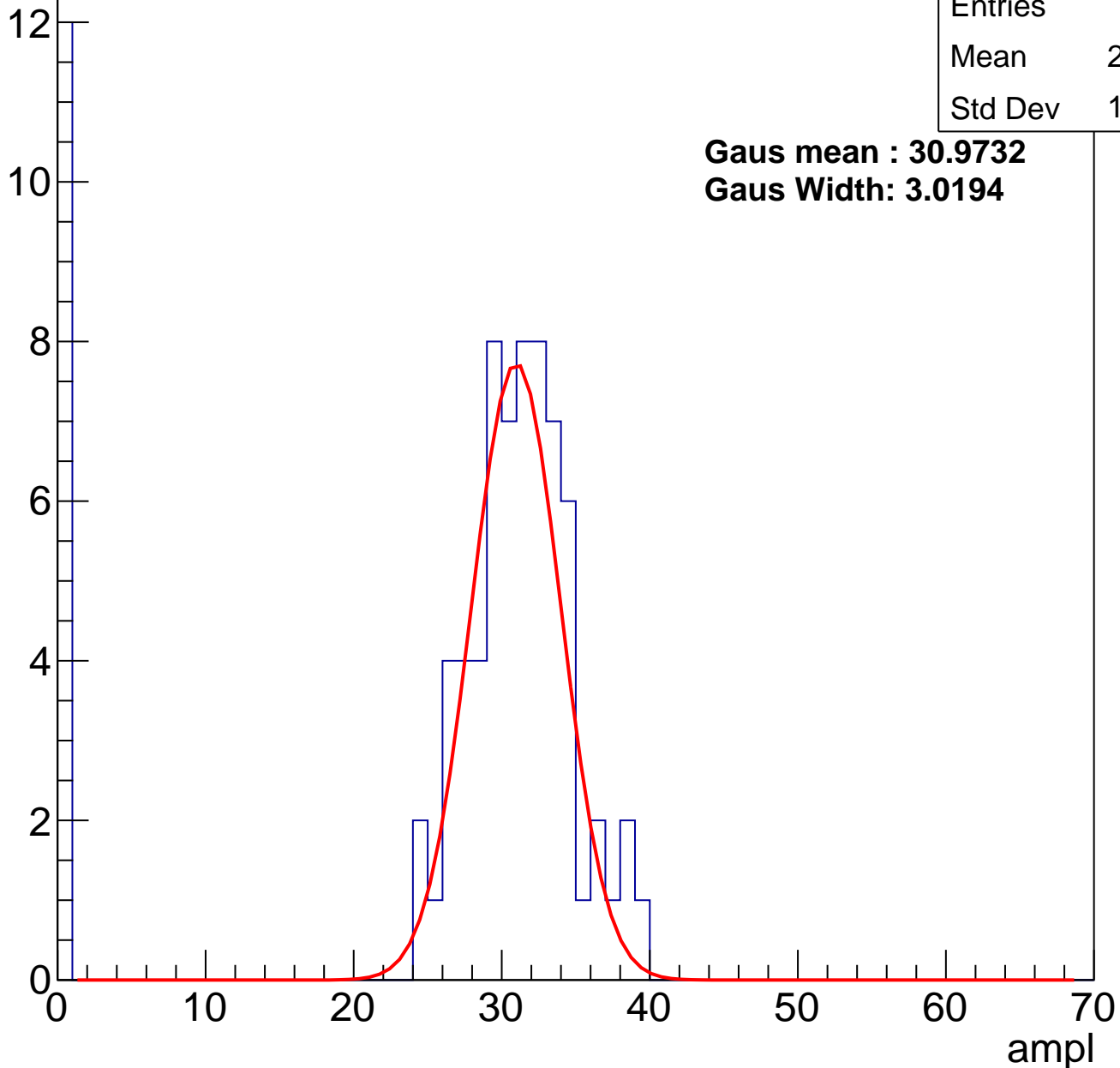
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	26.12
Std Dev	11.55

Gaus mean : 30.9732

Gaus Width: 3.0194

Entry



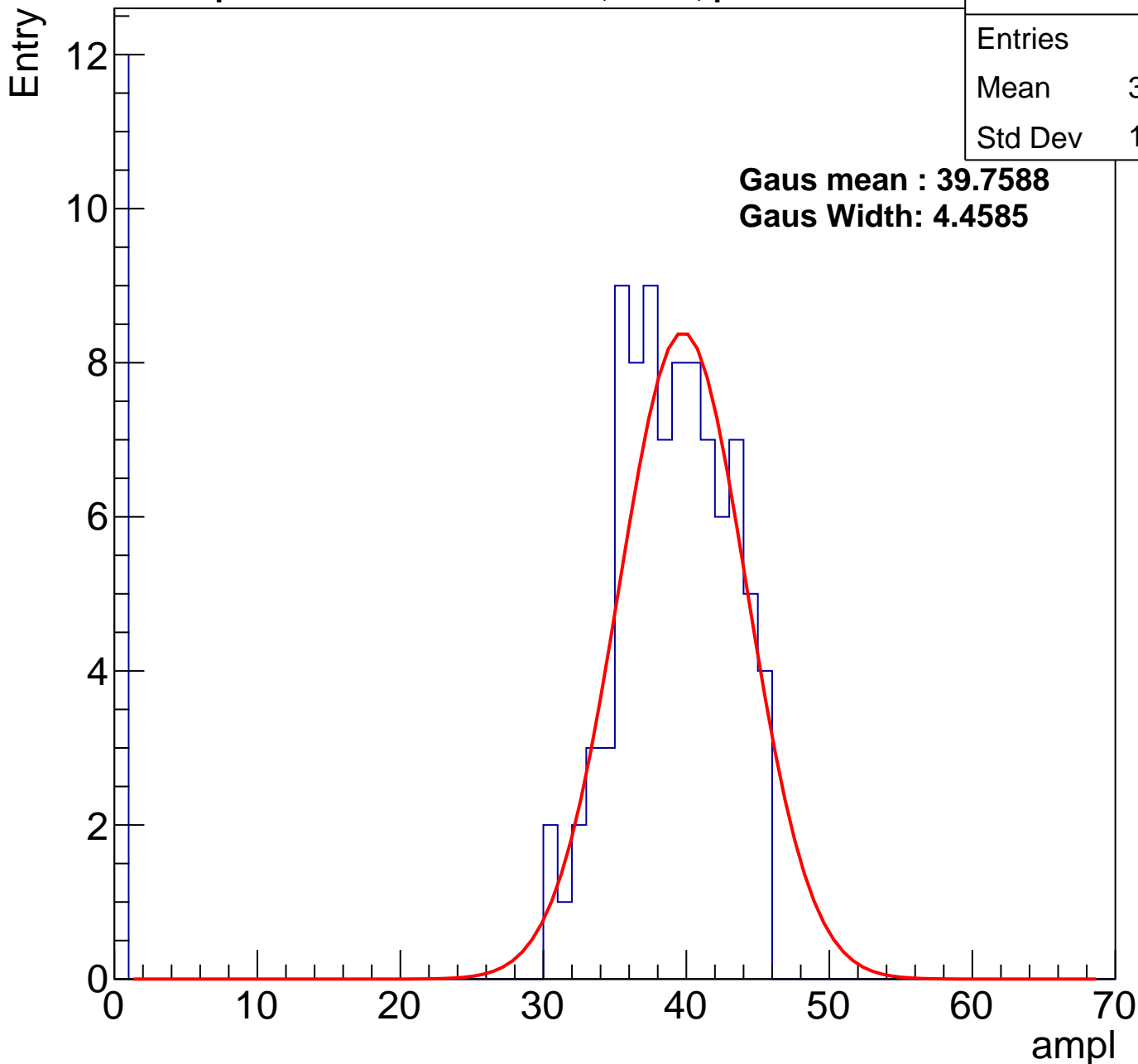
B1L103S, U8-ch9, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	33.96
Std Dev	12.94

Gaus mean : 39.7588

Gaus Width: 4.4585



B1L103S, U8-ch9, adc2

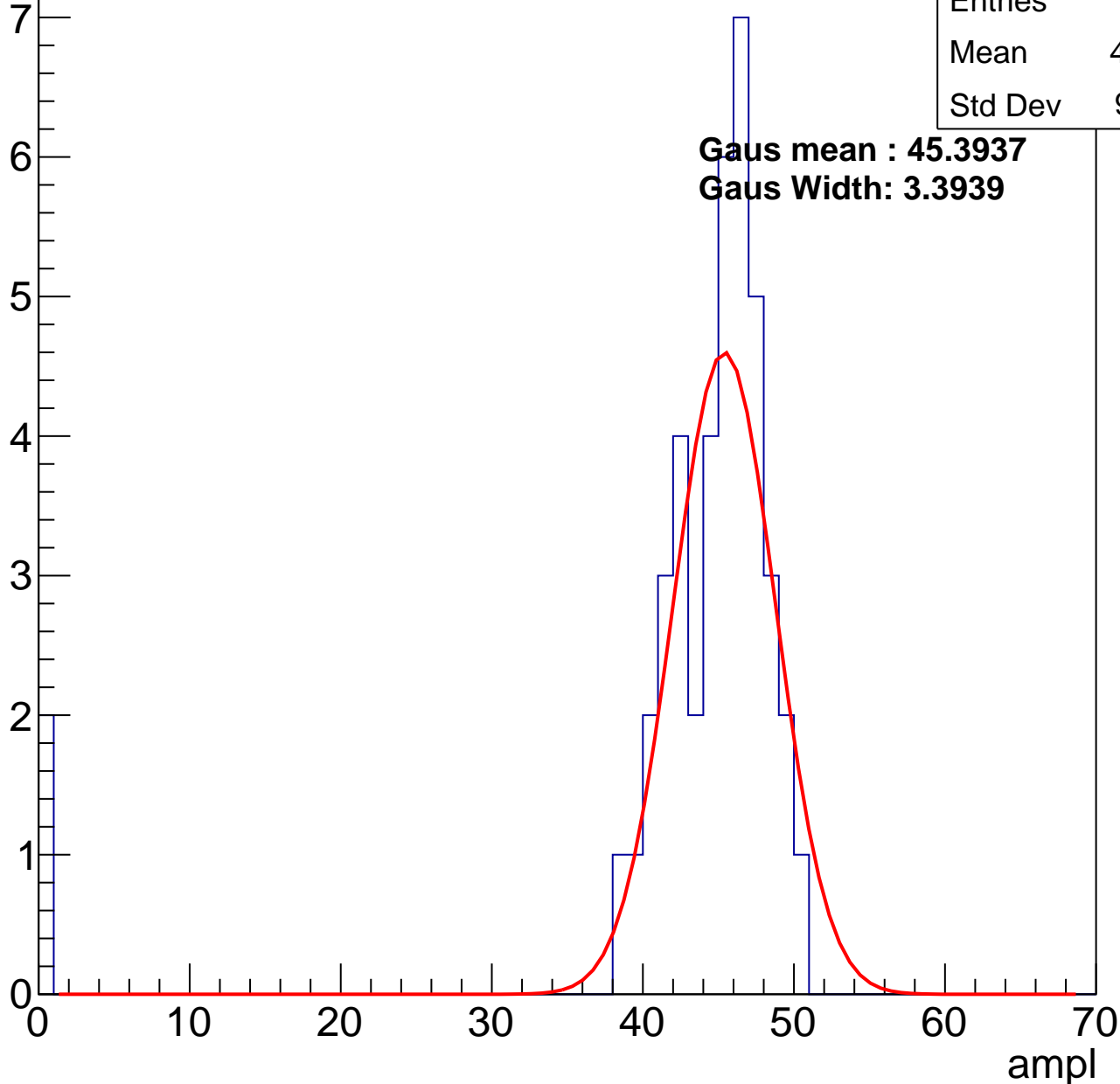
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	42.53
Std Dev	9.801

Gaus mean : 45.3937

Gaus Width: 3.3939

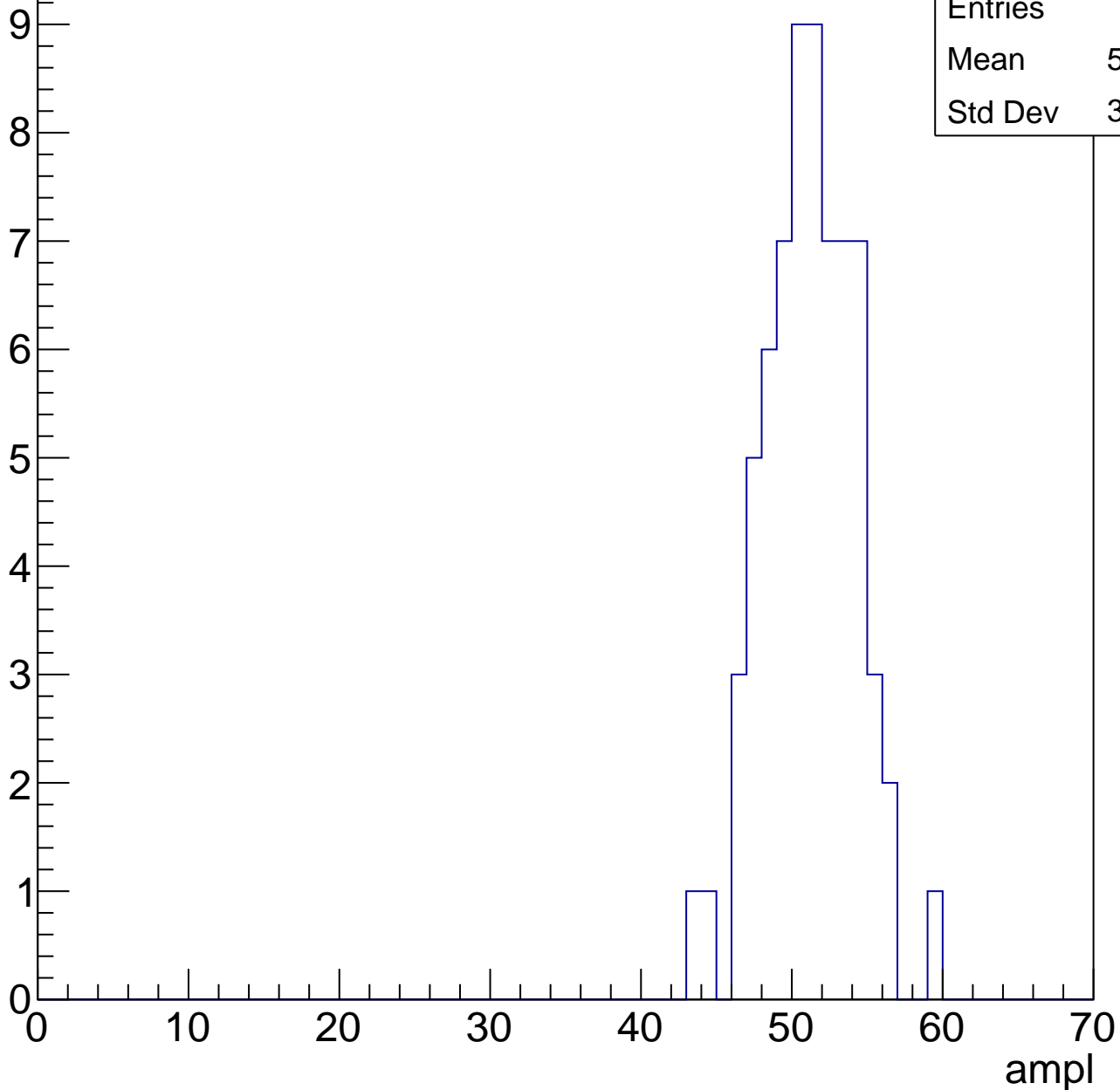


B1L103S, U8-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	50.72
Std Dev	3.004

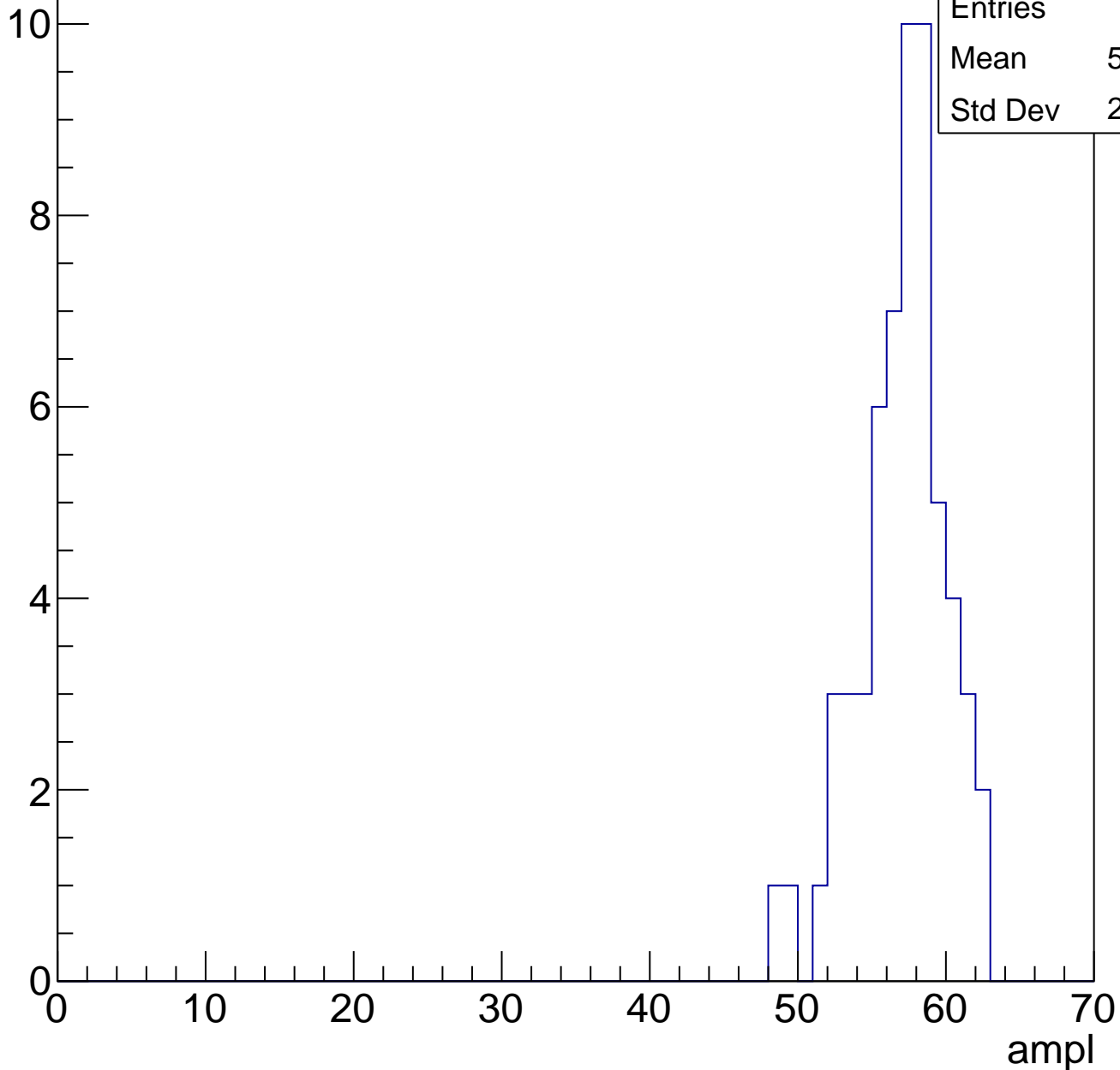


B1L103S, U8-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	56.59
Std Dev	2.958

Entry



B1L103S, U8-ch9, adc5

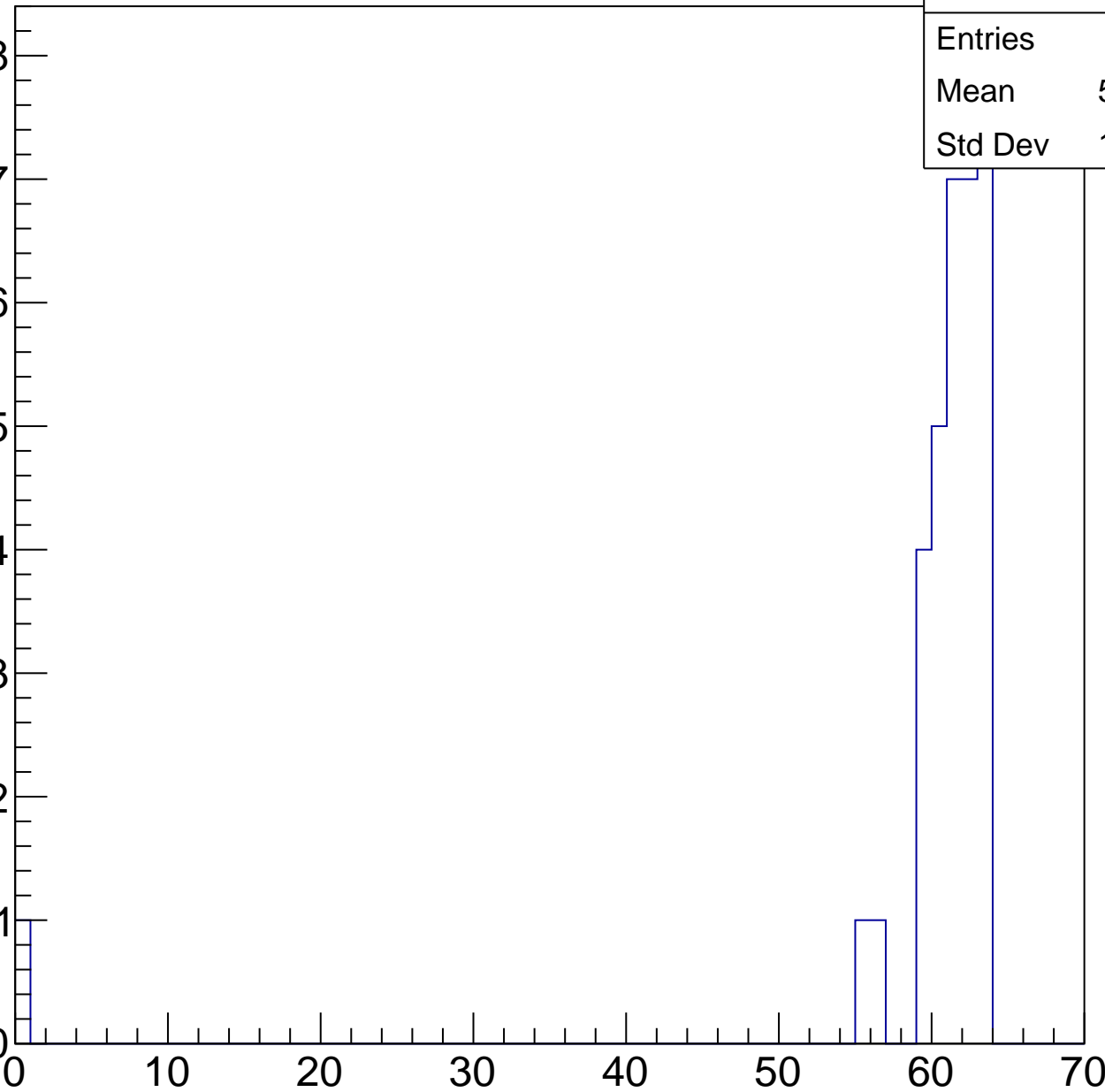
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	34
Mean	59.18
Std Dev	10.47

ampl



B1L103S, U8-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

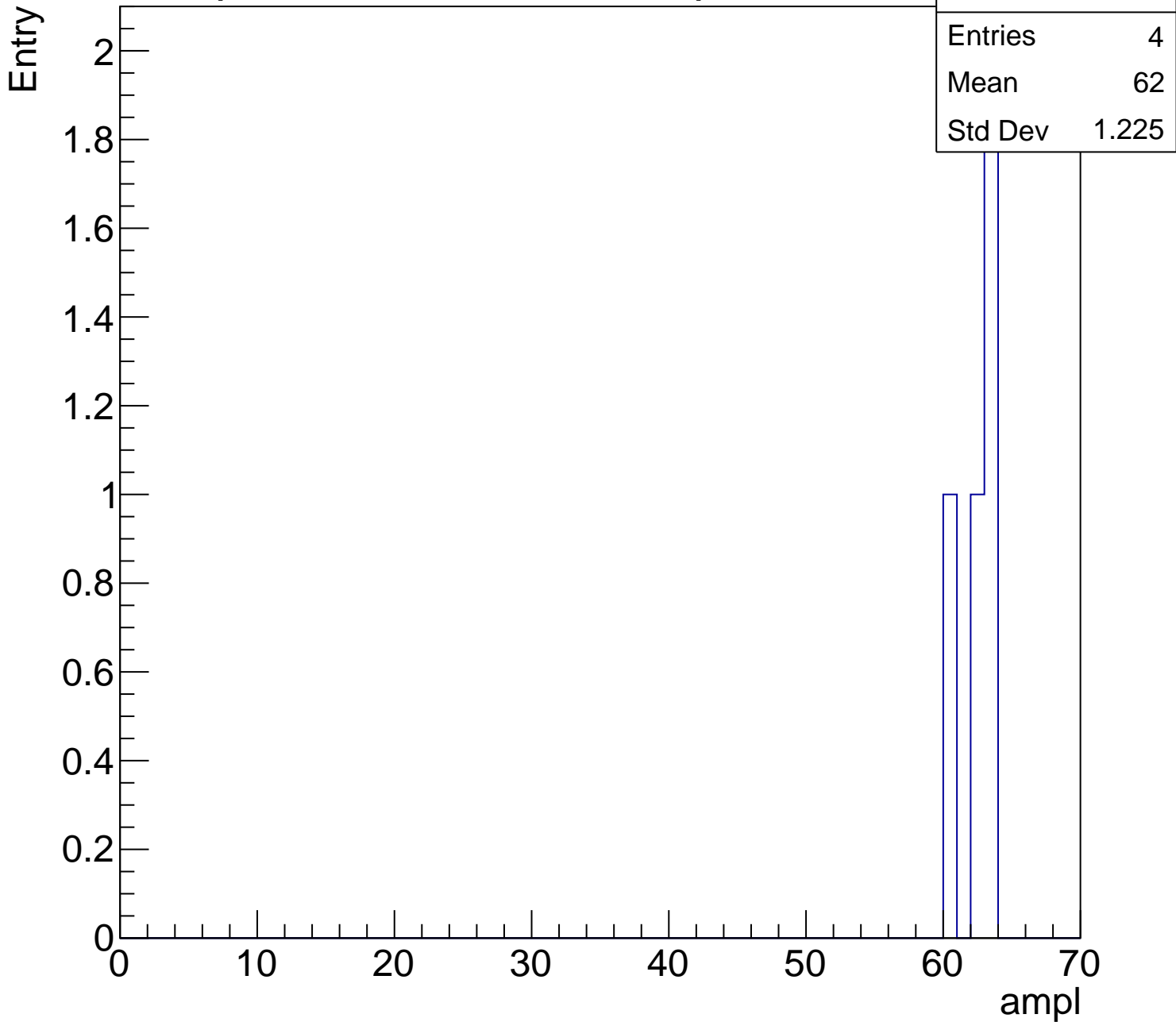
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

ampl

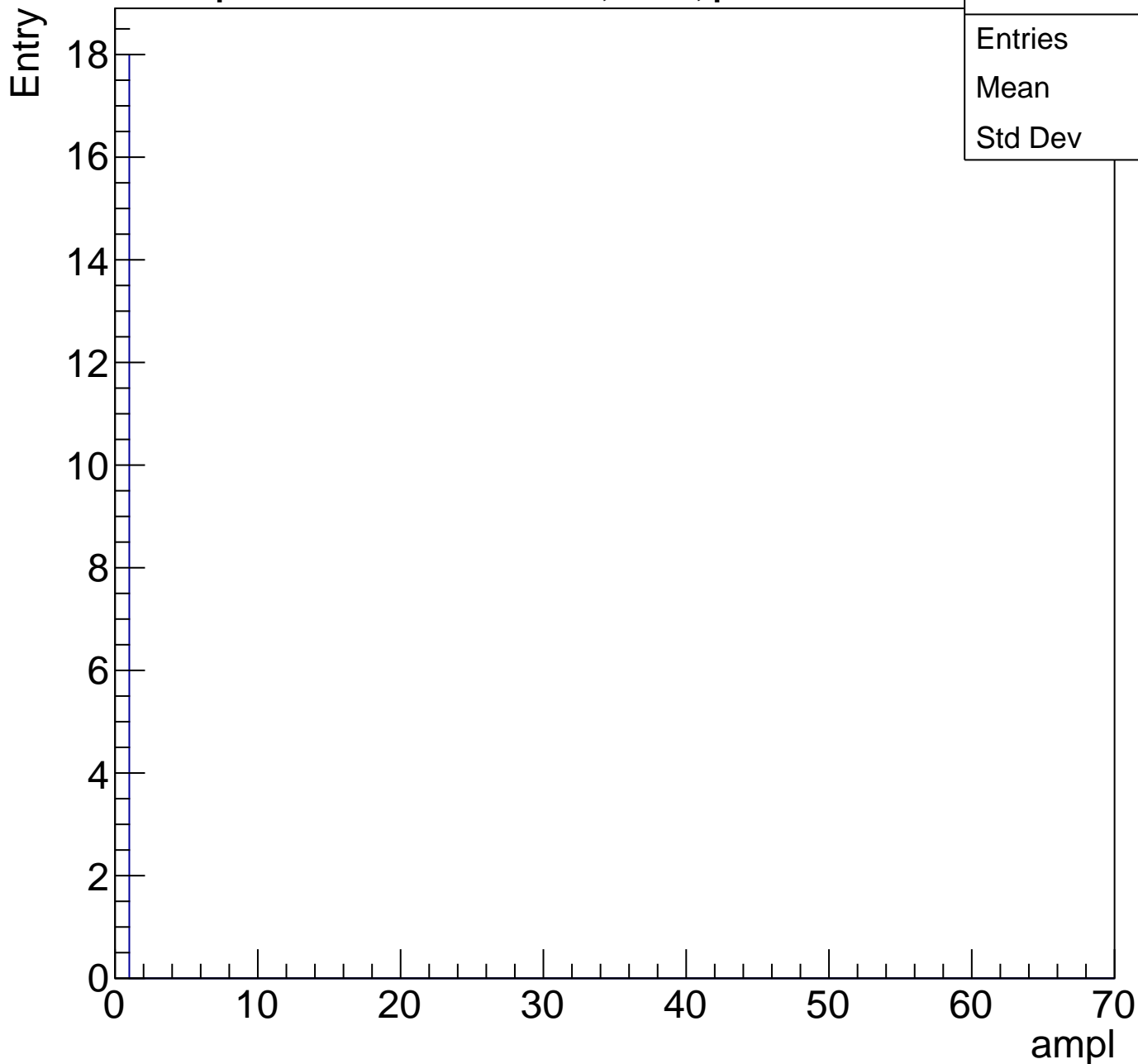
0 10 20 30 40 50 60 70



B1L103S, U8-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0



B1L103S, U8-ch10, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	24.68
Std Dev	10.92

Gaus mean : 29.2427

Gaus Width: 3.3448

Entry

10

8

6

4

2

0

0

10

20

30

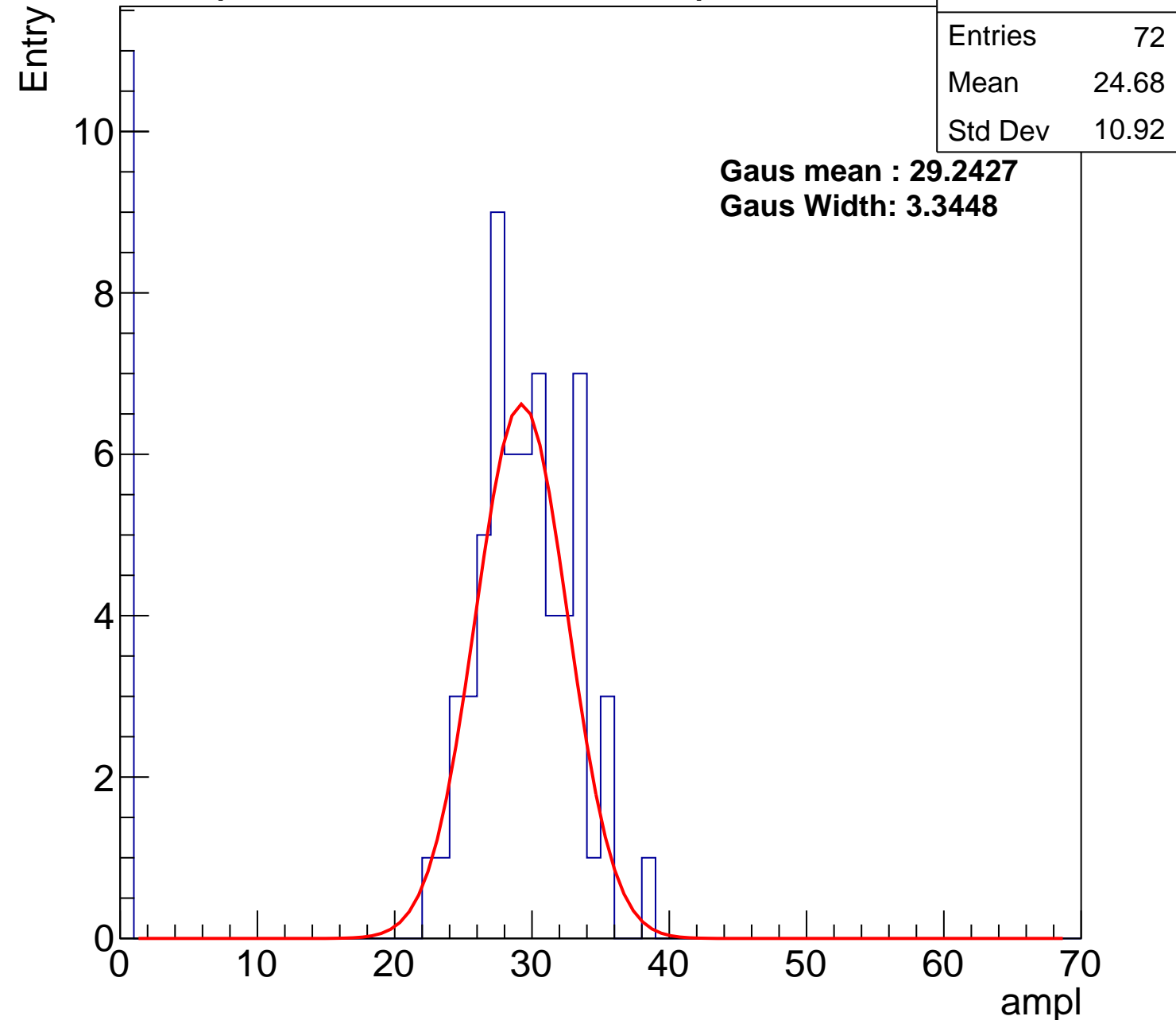
40

50

60

70

ampl



B1L103S, U8-ch10, adc1

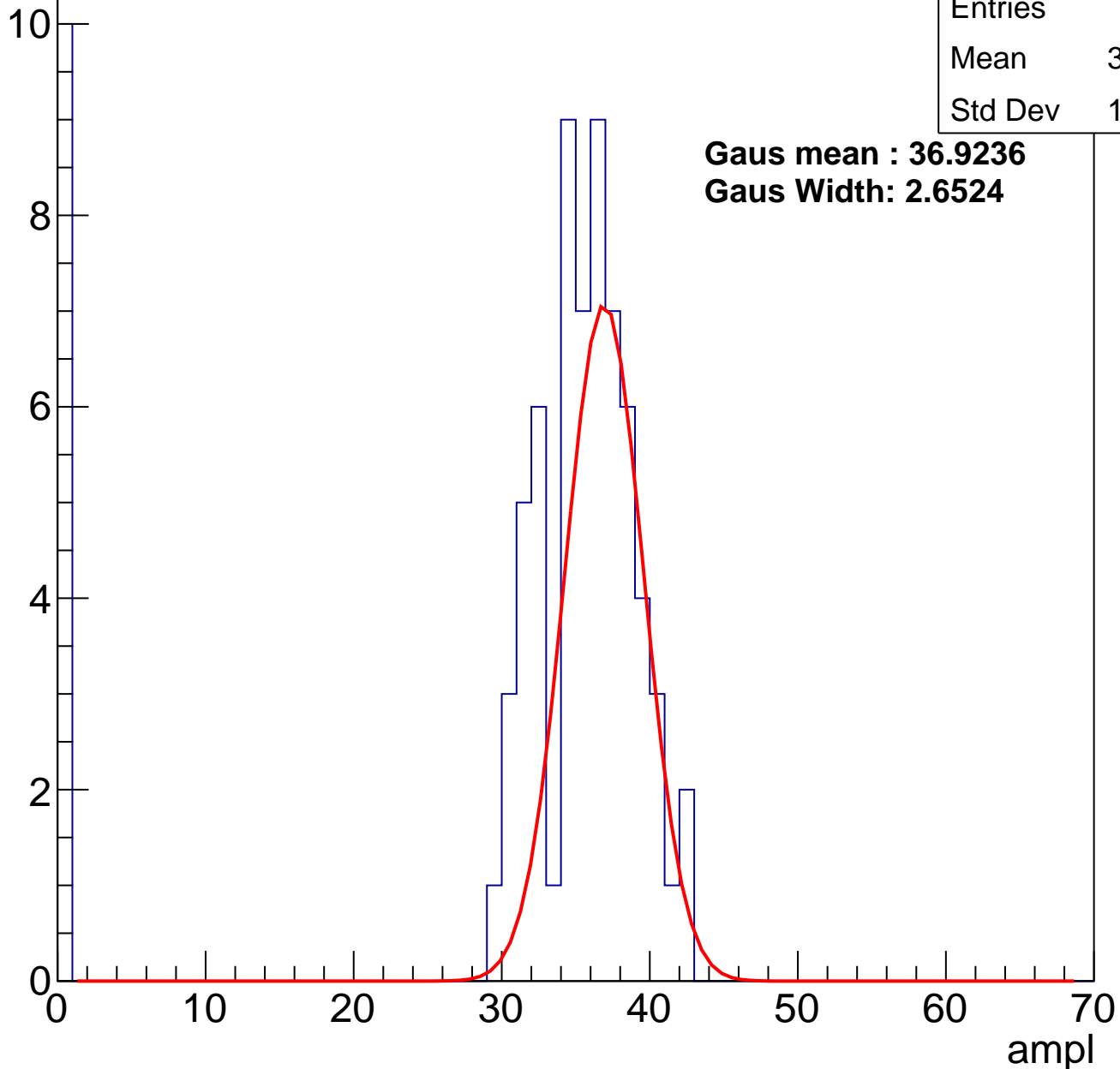
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	30.57
Std Dev	12.42

Gaus mean : 36.9236

Gaus Width: 2.6524

Entry



B1L103S, U8-ch10, adc2

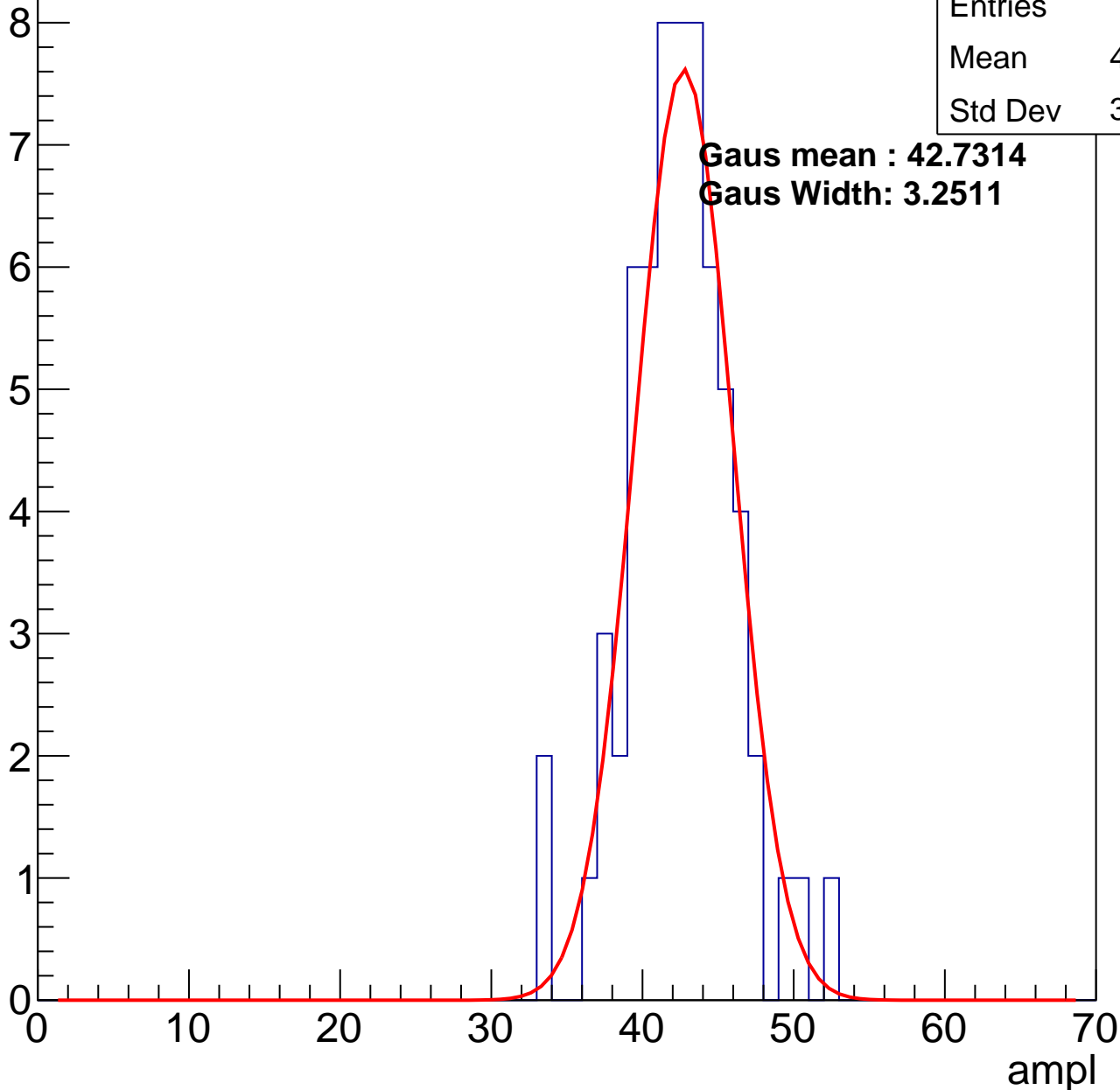
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	42.02
Std Dev	3.524

Gaus mean : 42.7314

Gaus Width: 3.2511

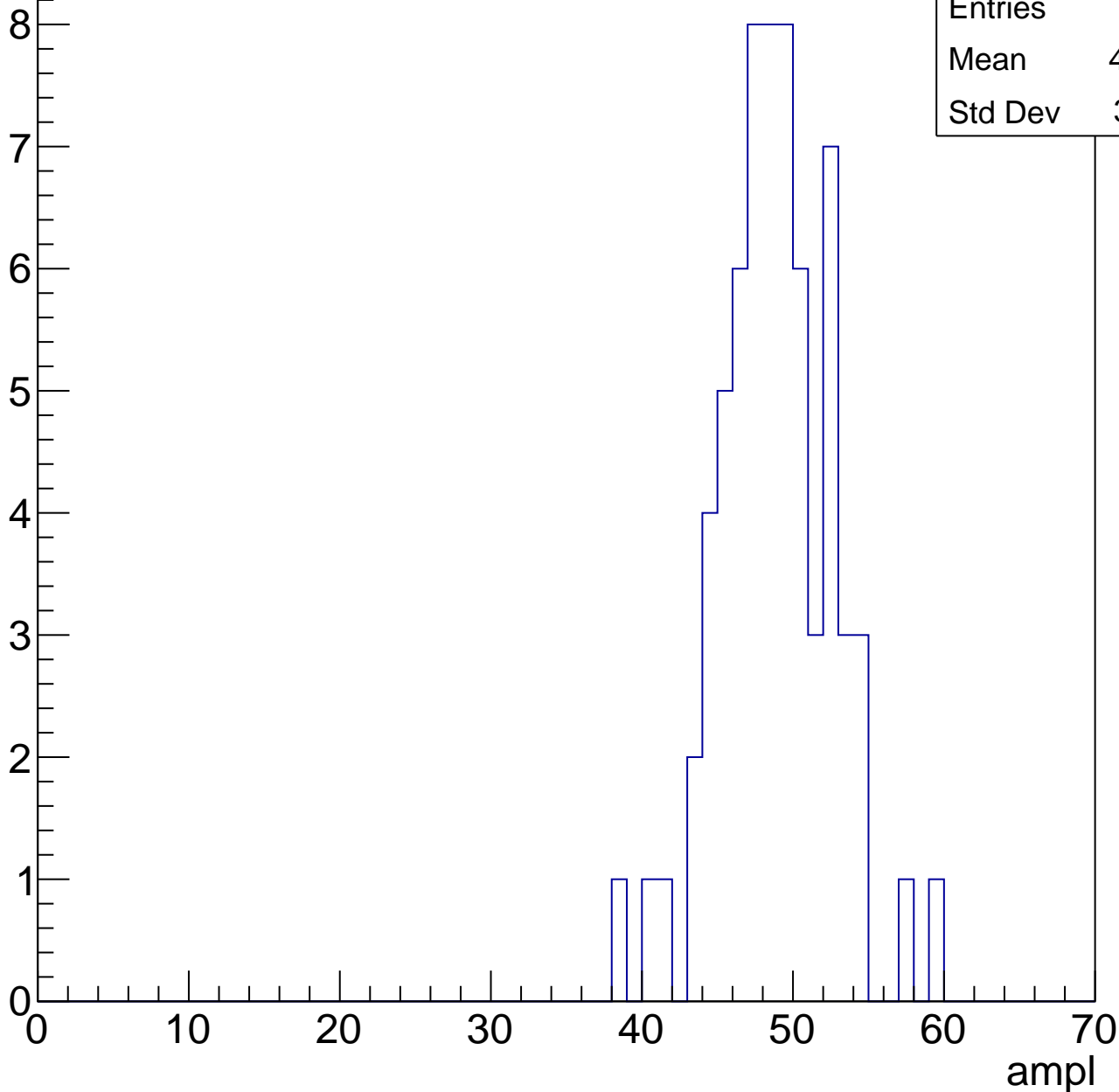


B1L103S, U8-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	48.35
Std Dev	3.741

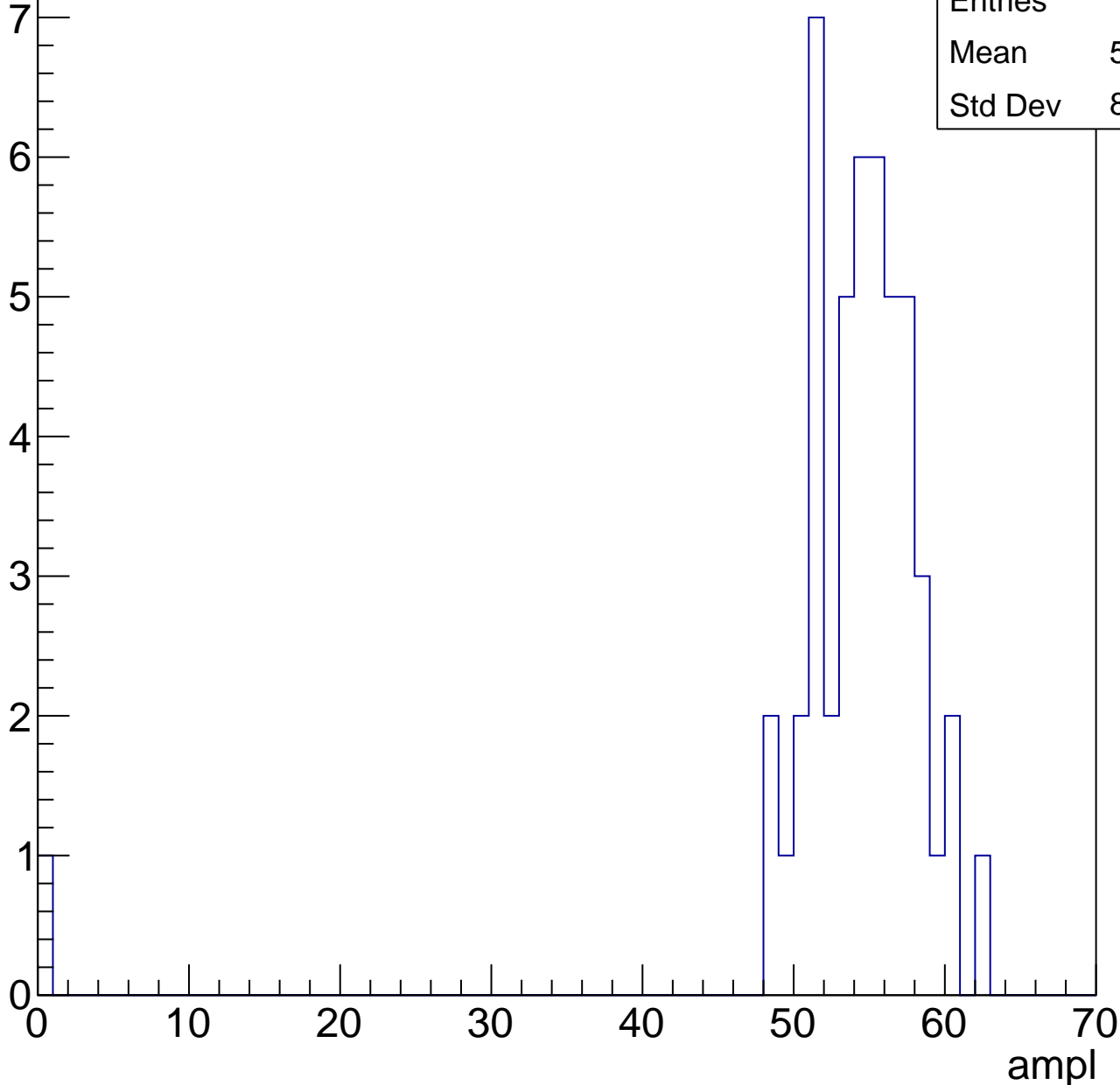


B1L103S, U8-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	53.16
Std Dev	8.289

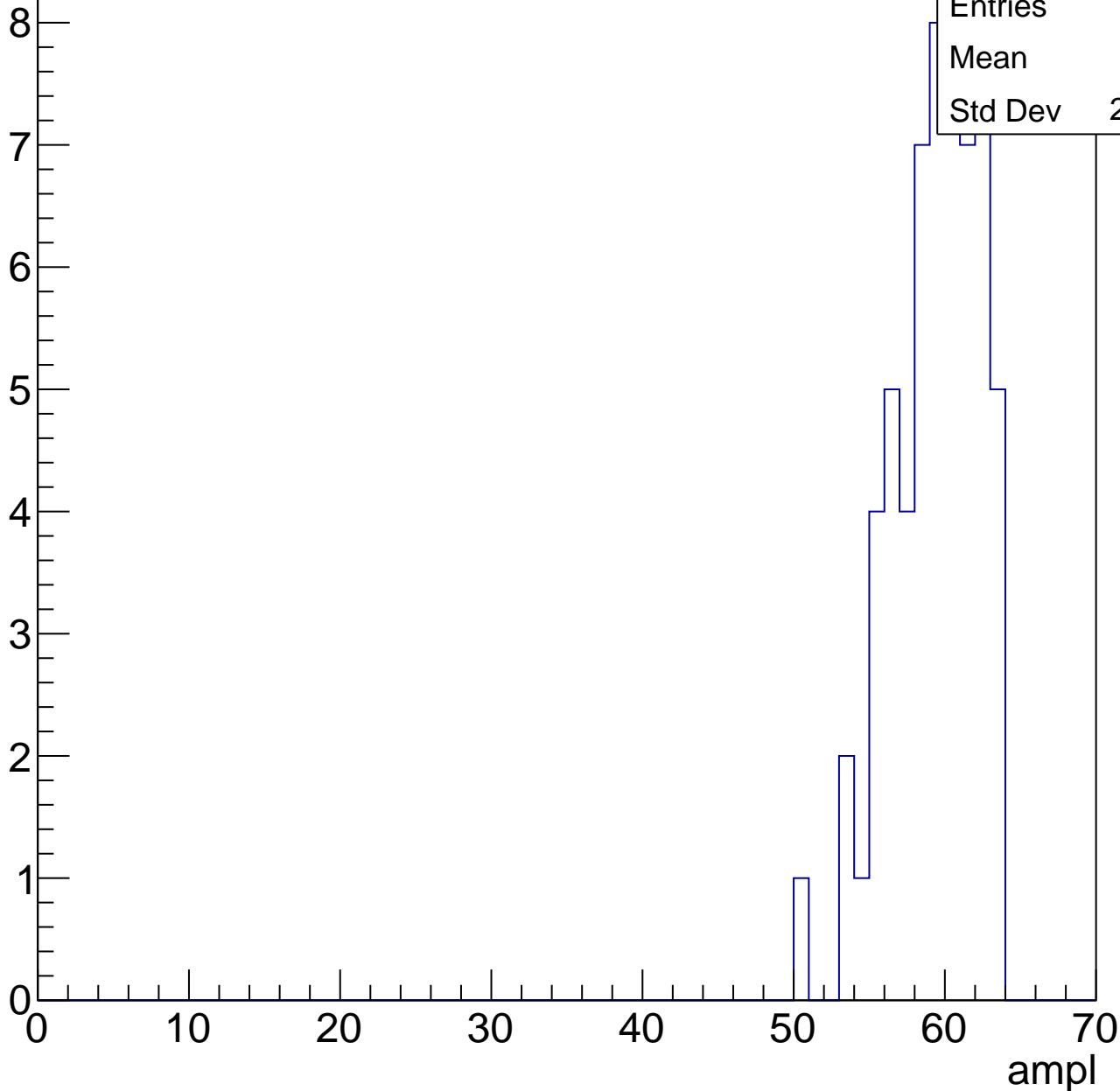


B1L103S, U8-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	58.9
Std Dev	2.879

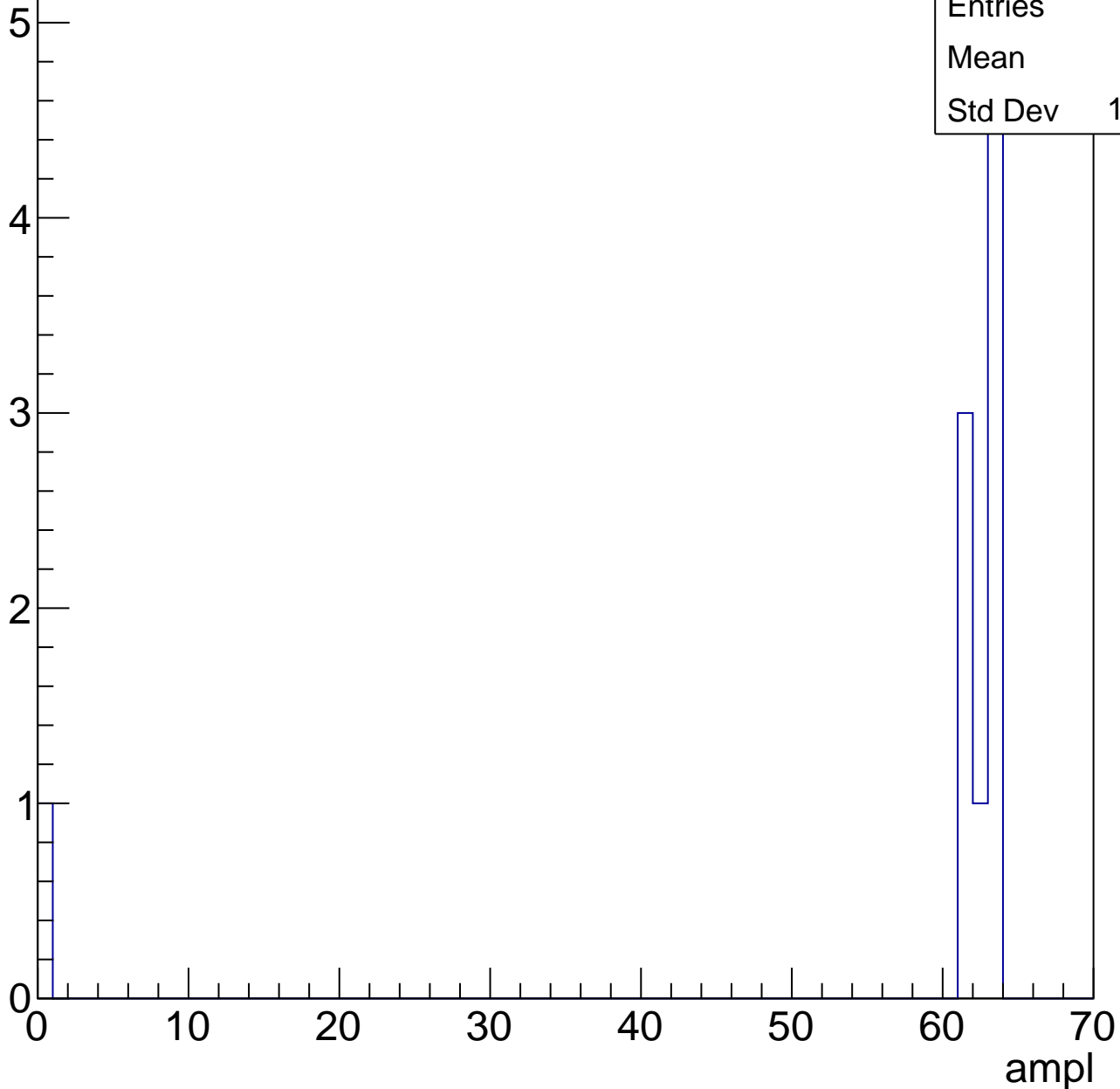


B1L103S, U8-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	56
Std Dev	18.69

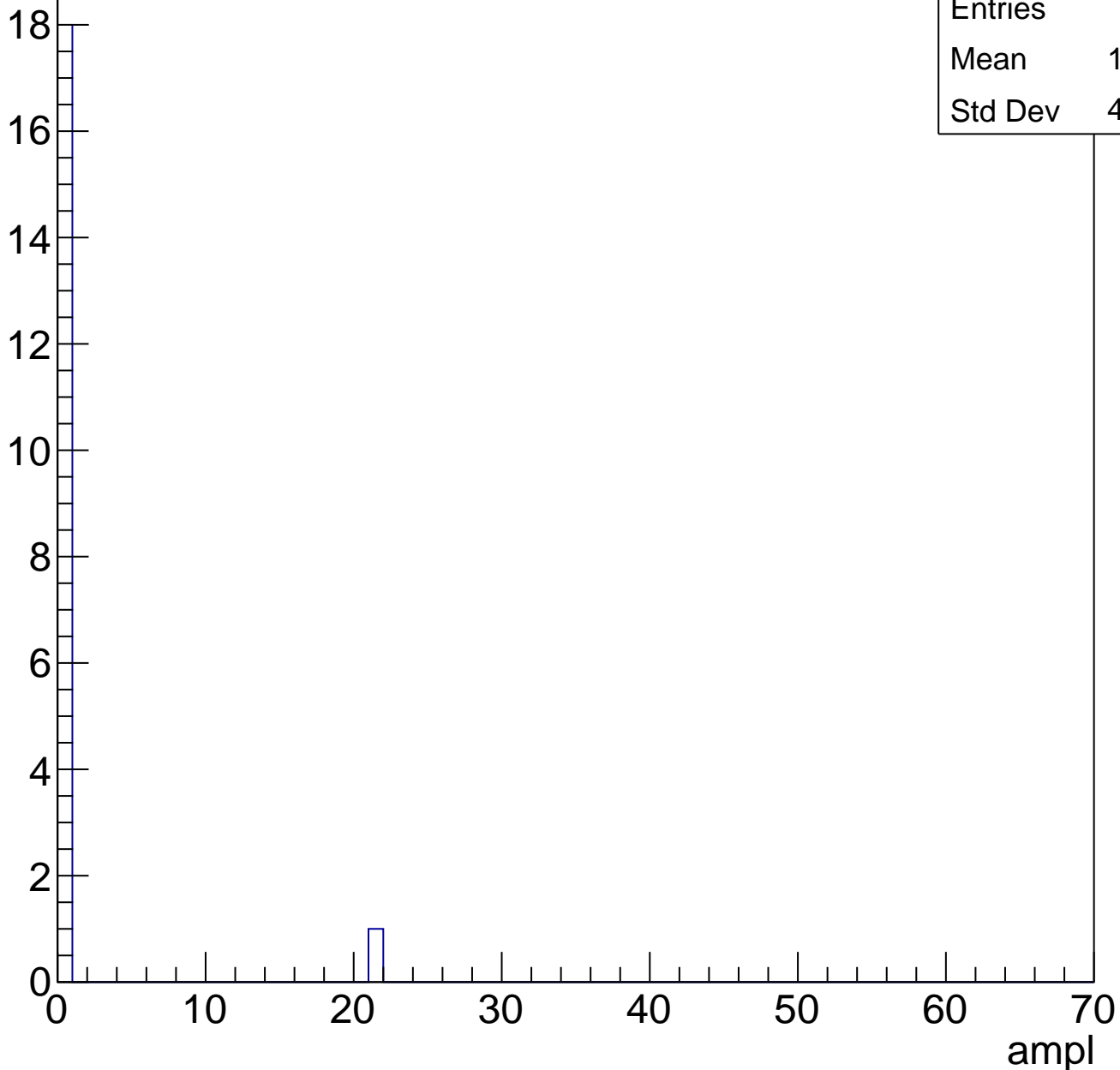


B1L103S, U8-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U8-ch11, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	29.57
Std Dev	7.583

Gaus mean : 31.6904

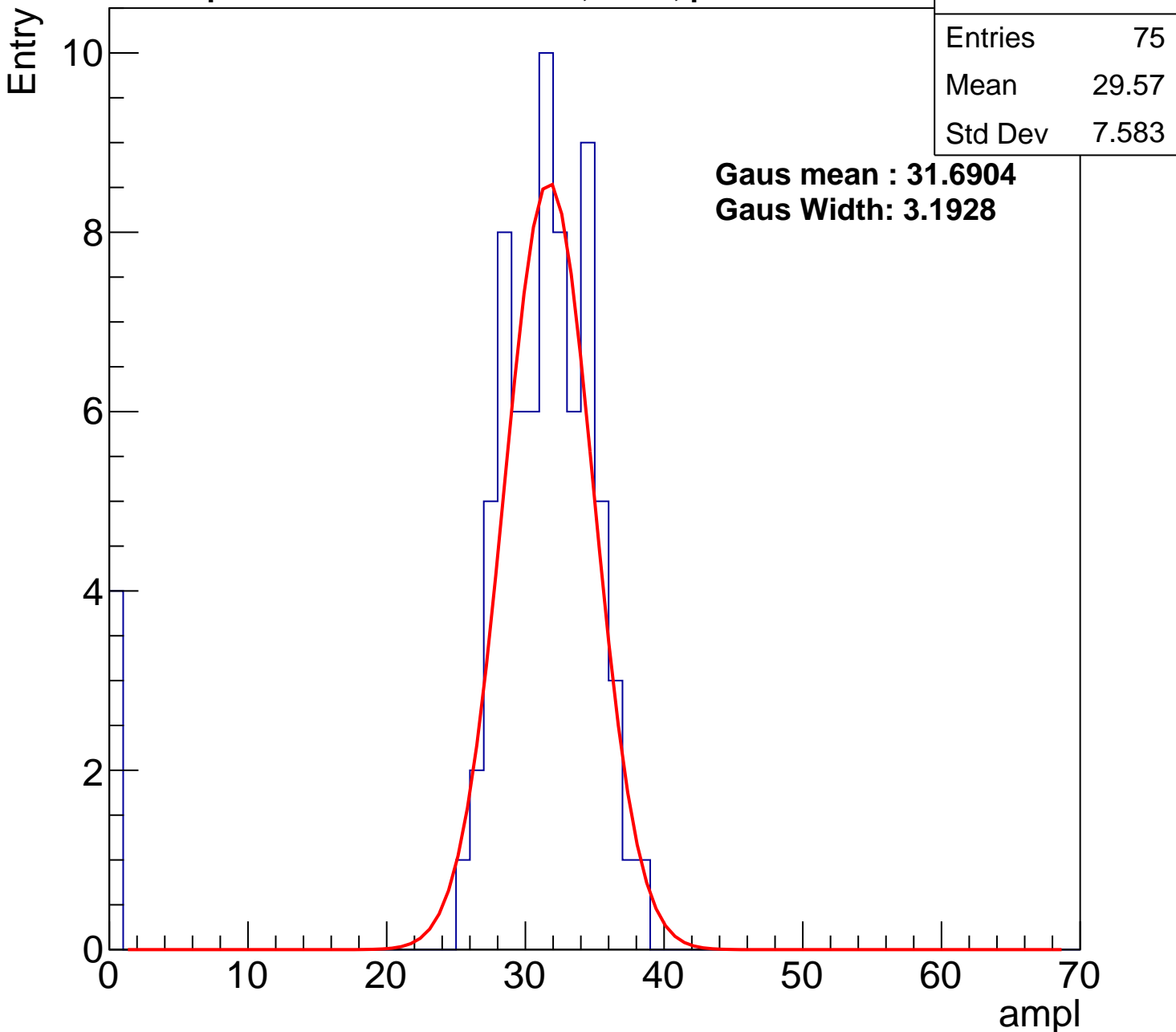
Gaus Width: 3.1928

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch11, adc1

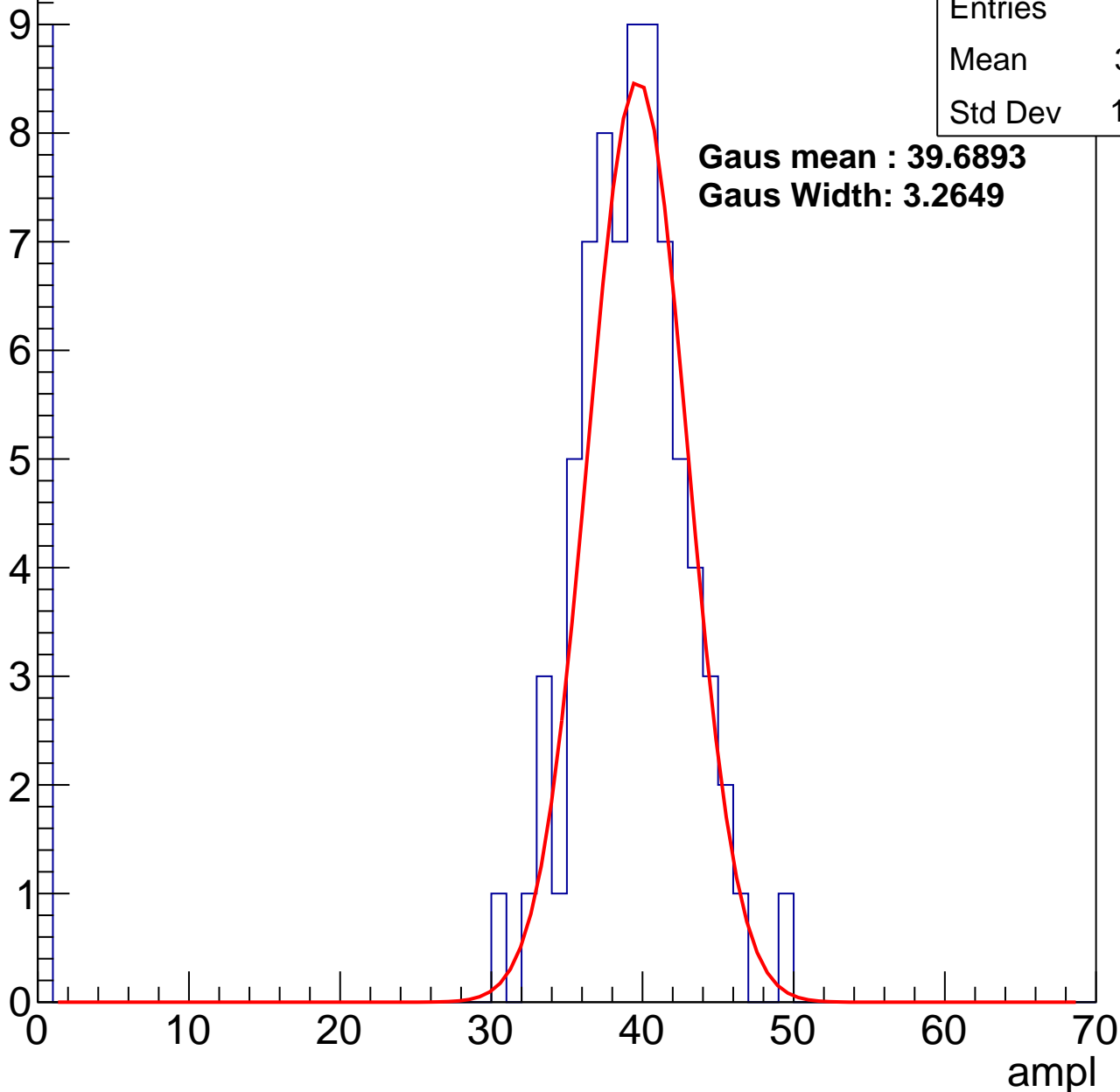
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	34.71
Std Dev	12.54

Gaus mean : 39.6893

Gaus Width: 3.2649



B1L103S, U8-ch11, adc2

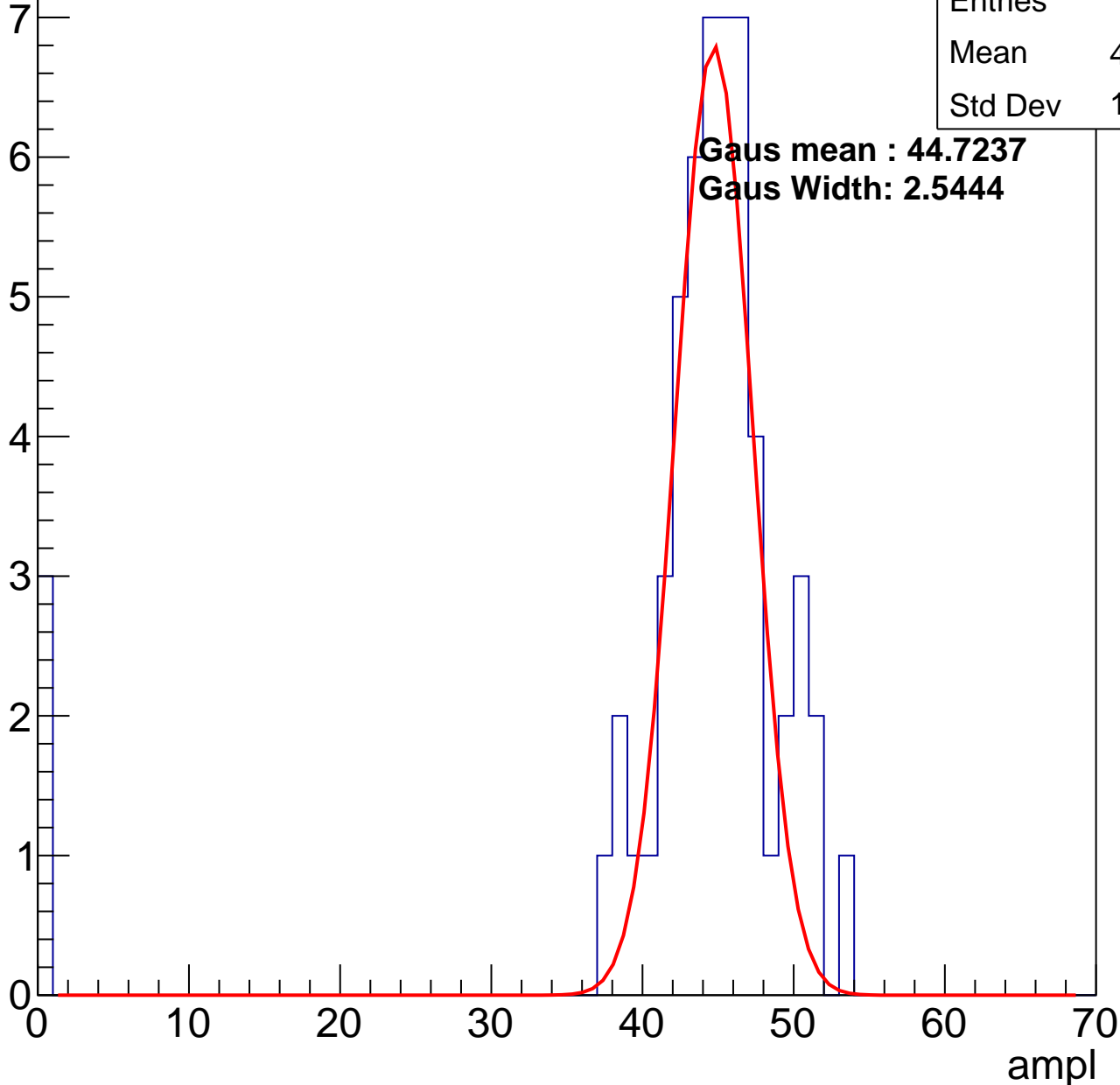
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	42.27
Std Dev	10.59

Gaus mean : 44.7237

Gaus Width: 2.5444

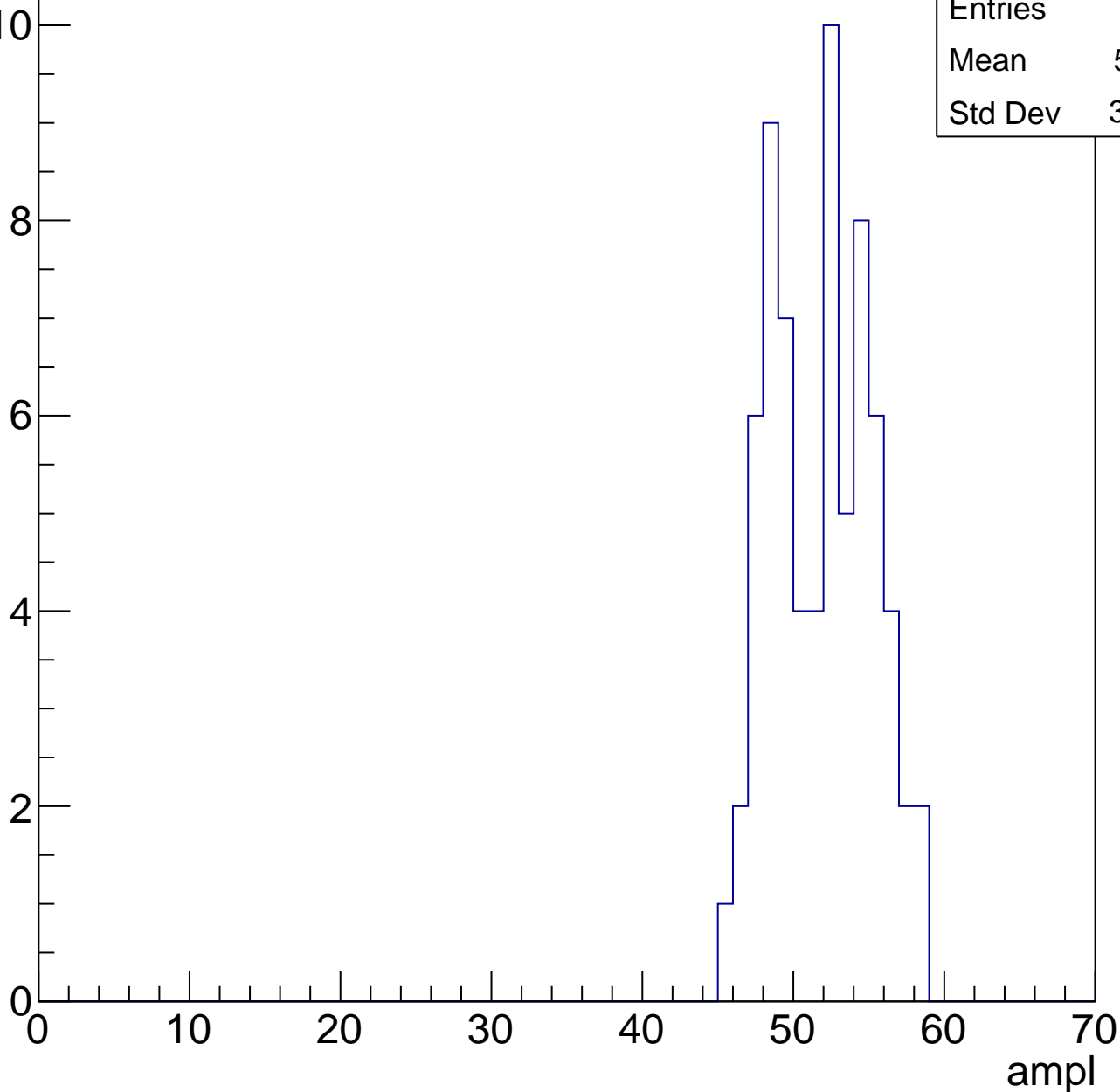


B1L103S, U8-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	51.41
Std Dev	3.275

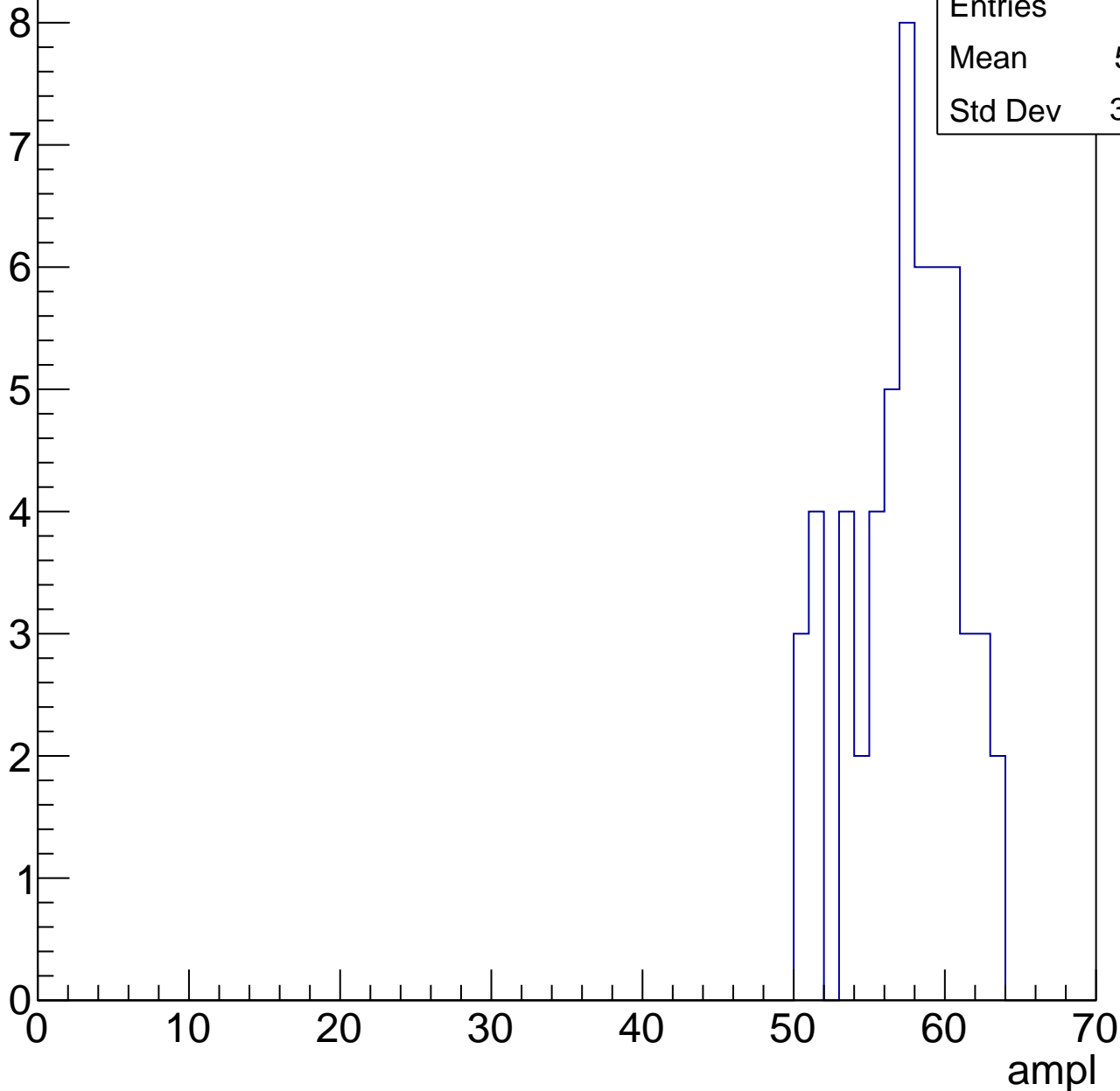


B1L103S, U8-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	56.91
Std Dev	3.466

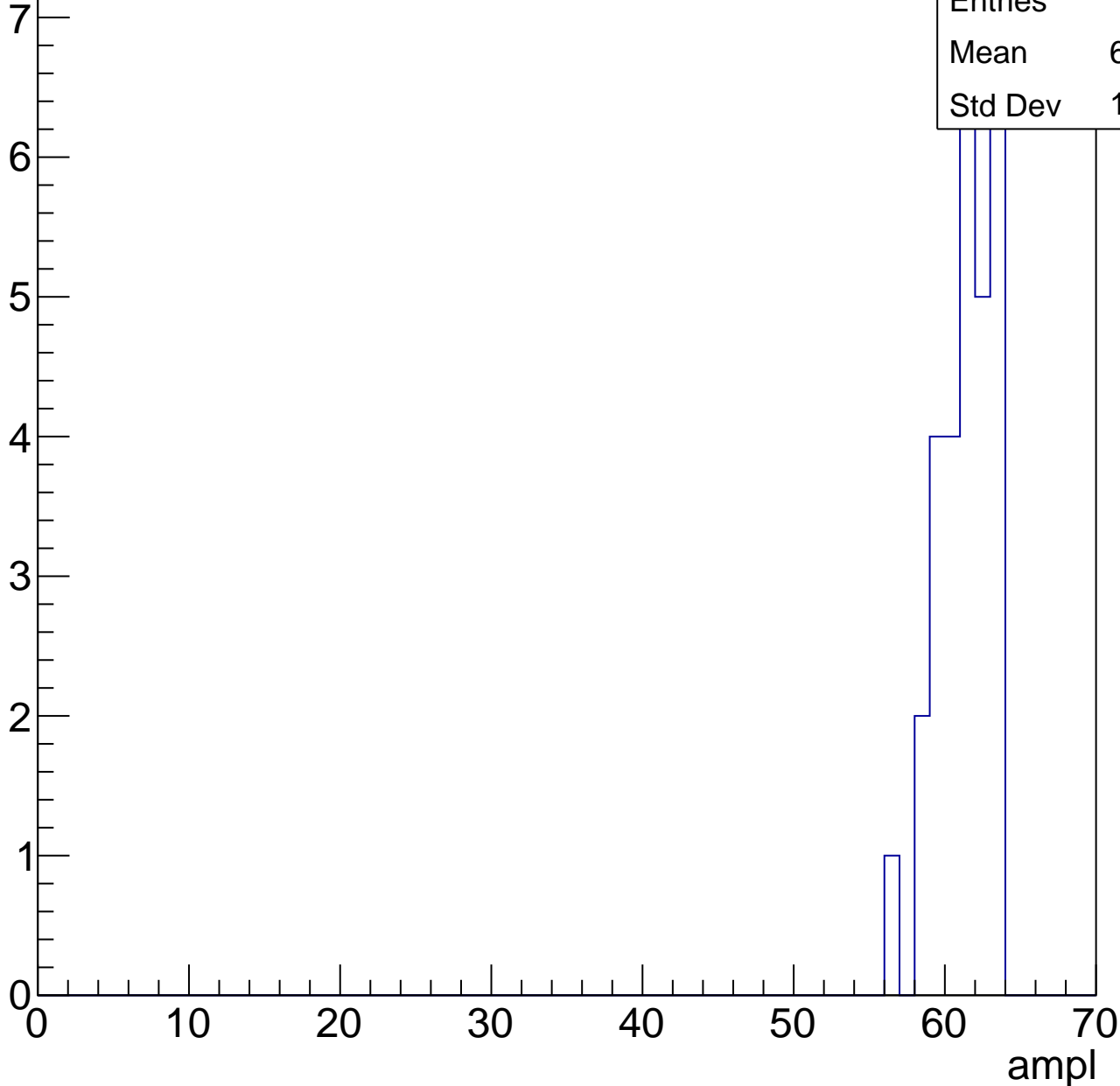


B1L103S, U8-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	60.87
Std Dev	1.784



B1L103S, U8-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	4
Mean	46.75
Std Dev	26.99

B1L103S, U8-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



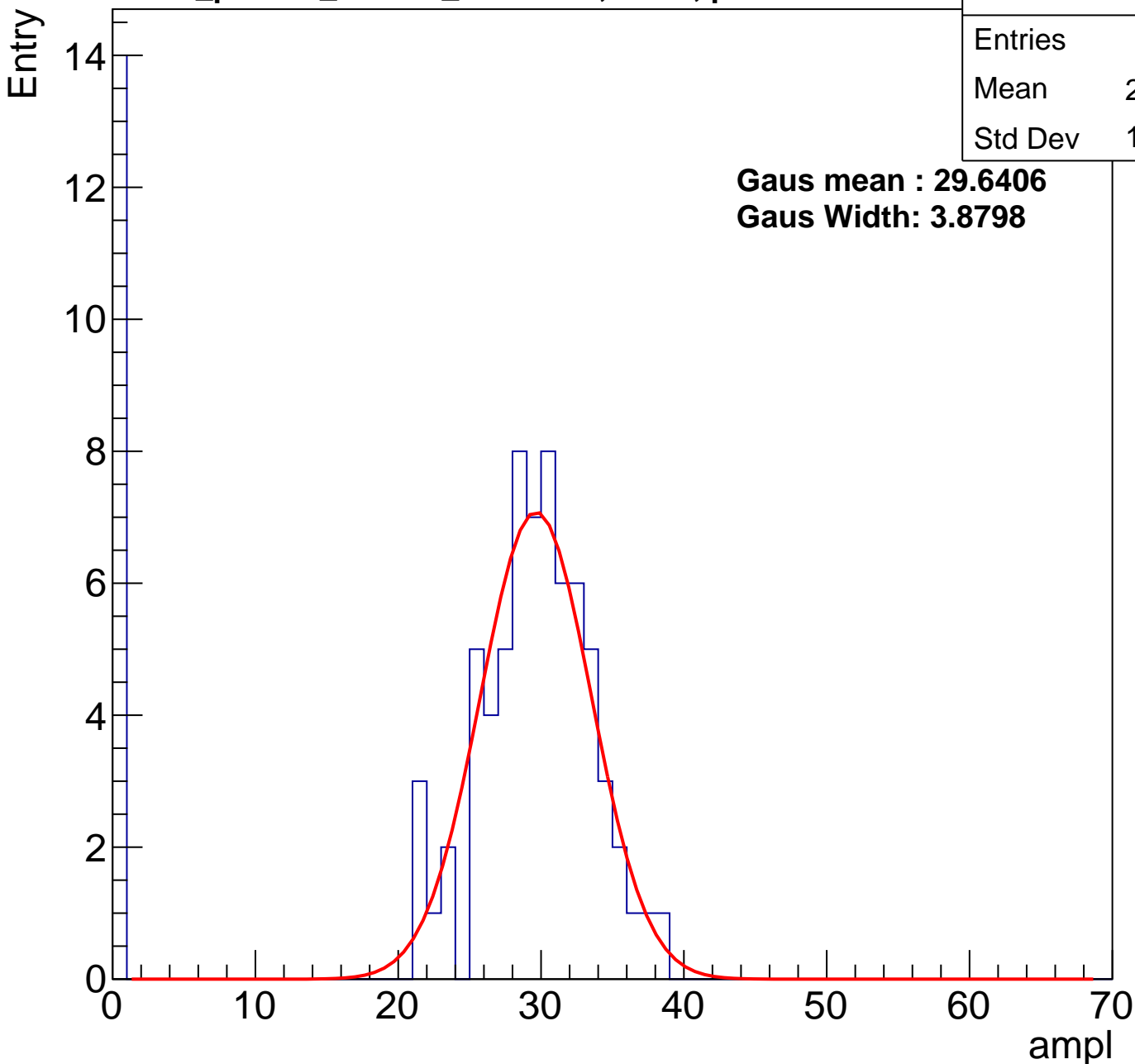
B1L103S, U8-ch12, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	24.24
Std Dev	11.52

Gaus mean : 29.6406

Gaus Width: 3.8798



B1L103S, U8-ch12, adc1

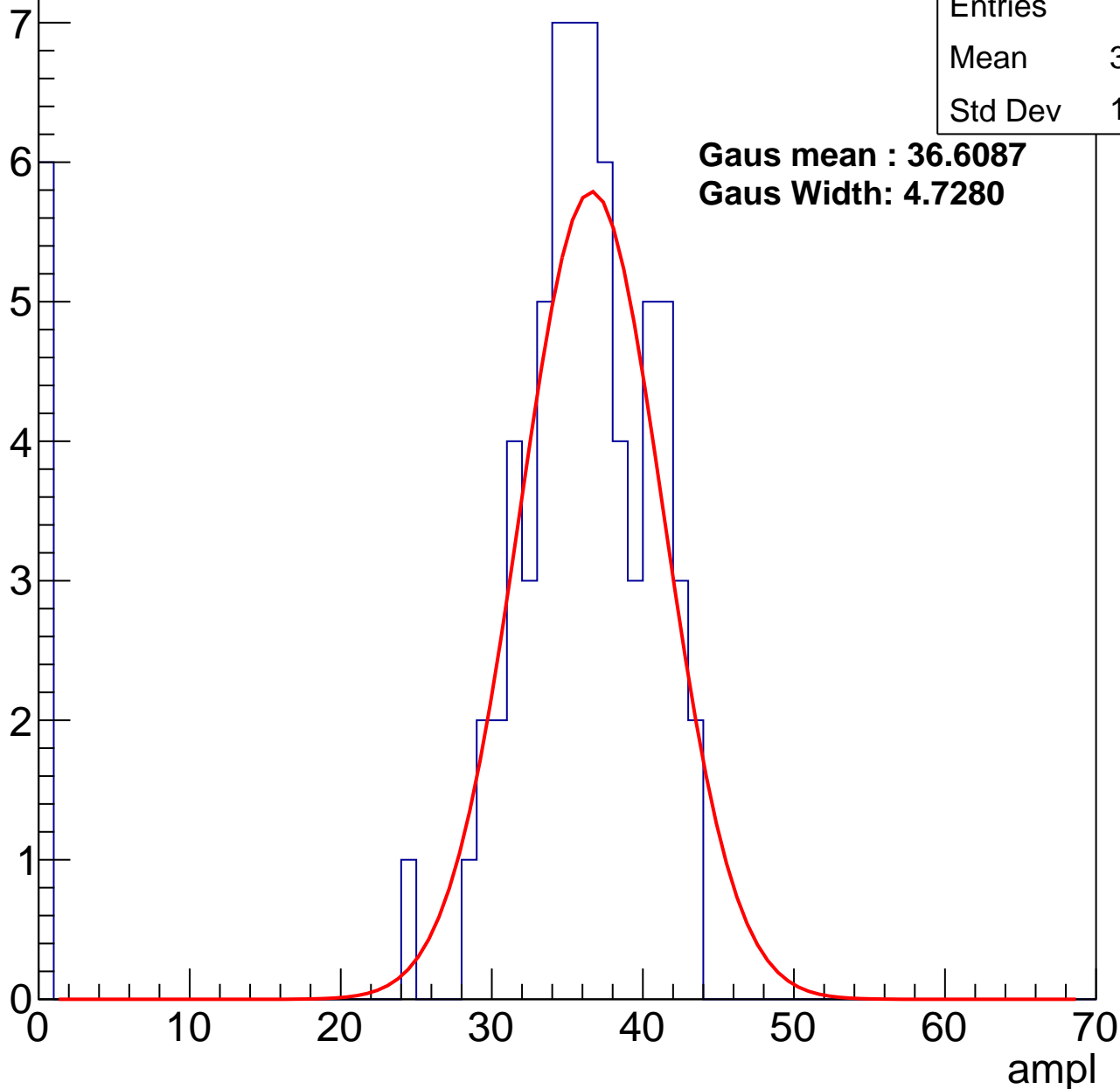
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.85
Std Dev	10.54

Gaus mean : 36.6087

Gaus Width: 4.7280



B1L103S, U8-ch12, adc2

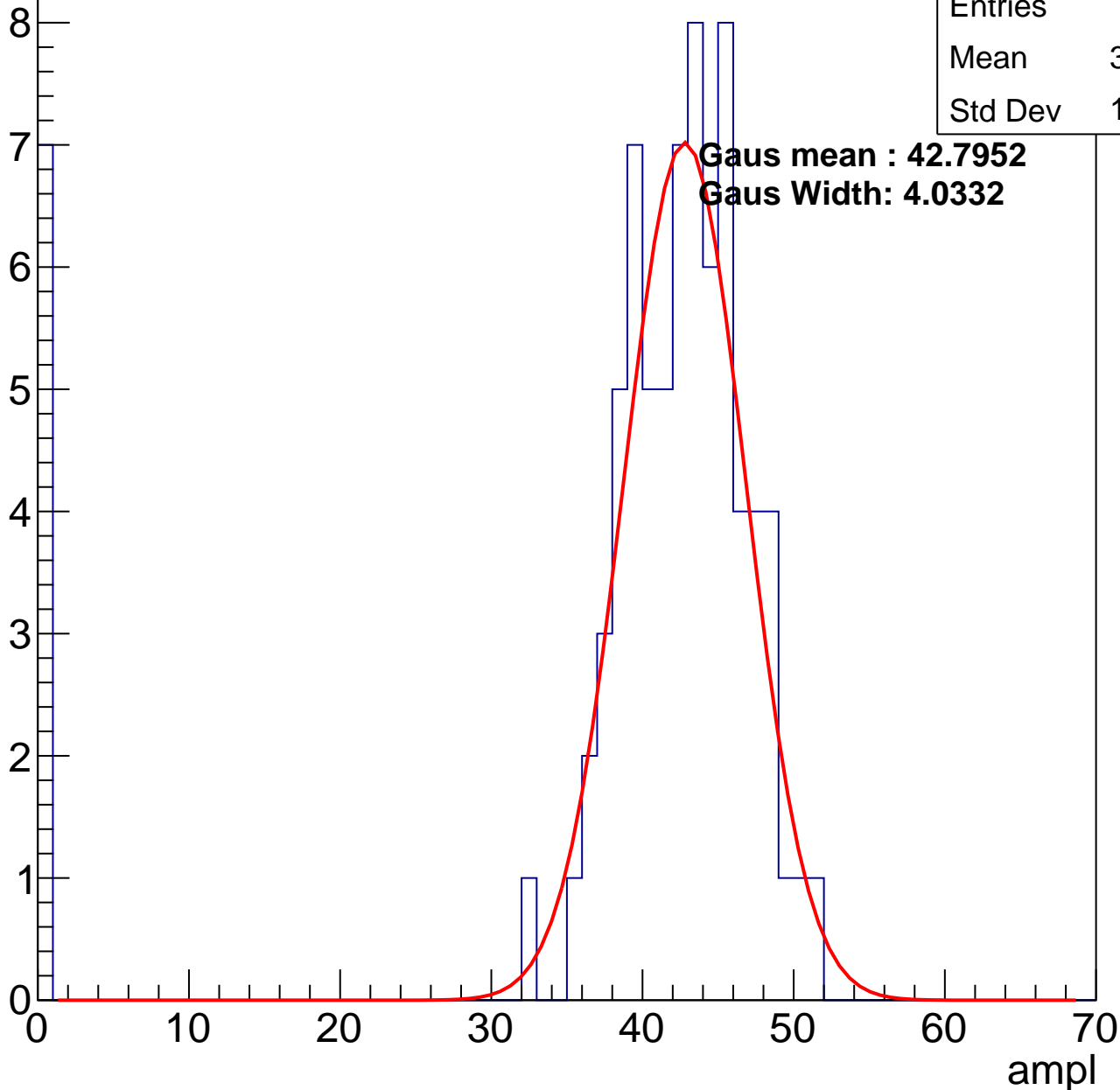
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	38.67
Std Dev	12.52

Gaus mean : 42.7952

Gaus Width: 4.0332

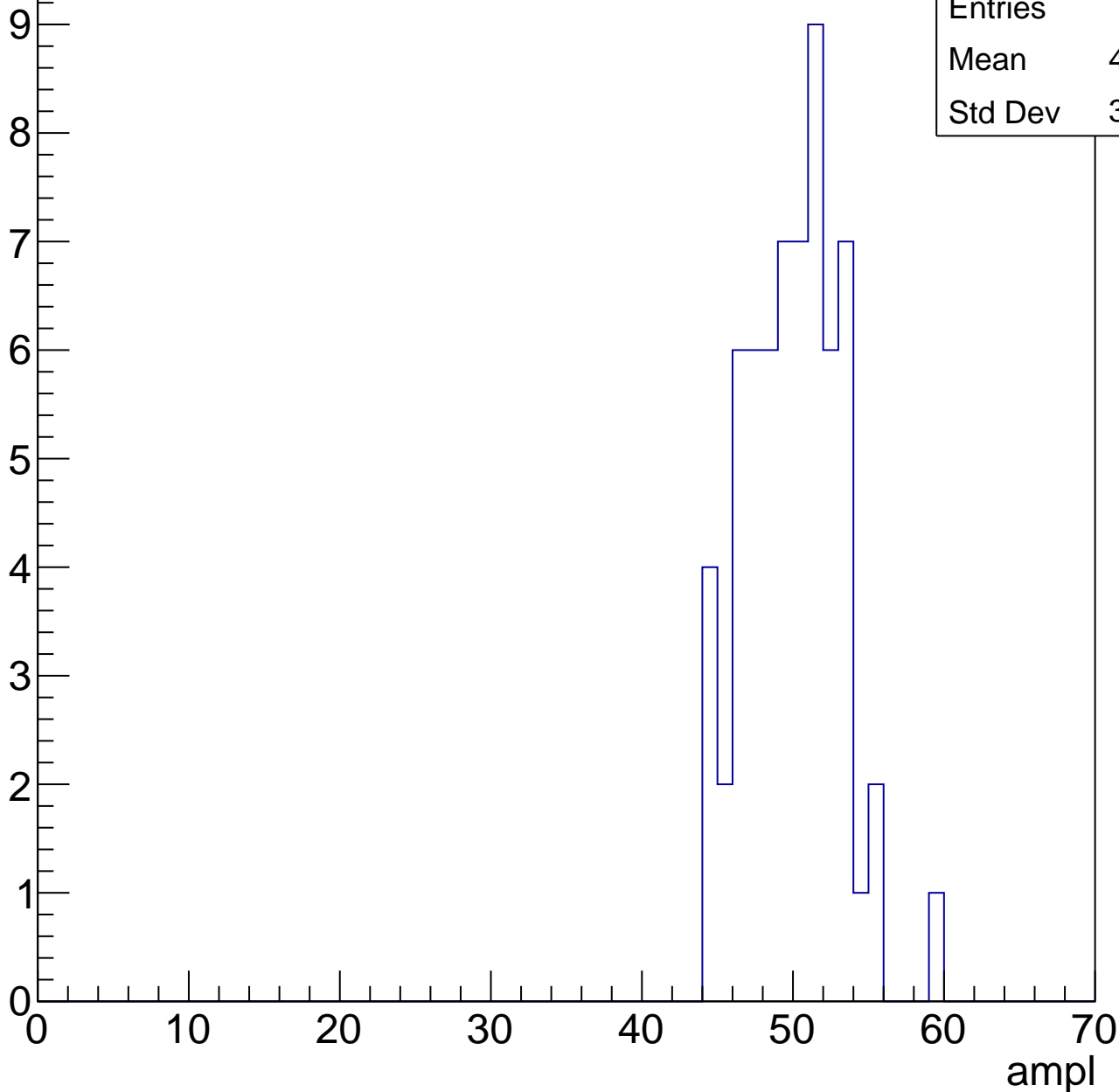


B1L103S, U8-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.53
Std Dev	3.072

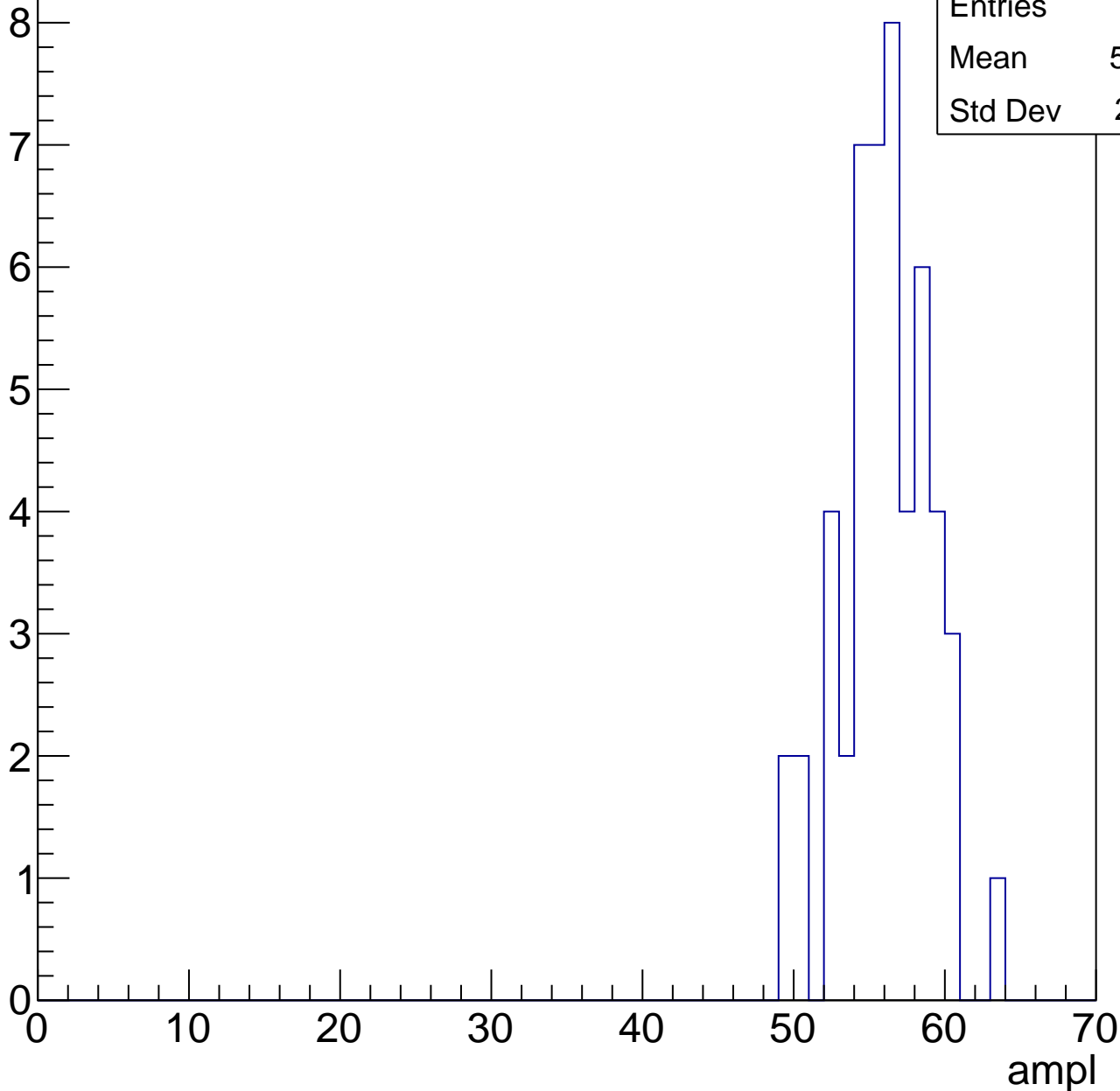


B1L103S, U8-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.56
Std Dev	2.961



B1L103S, U8-ch12, adc5

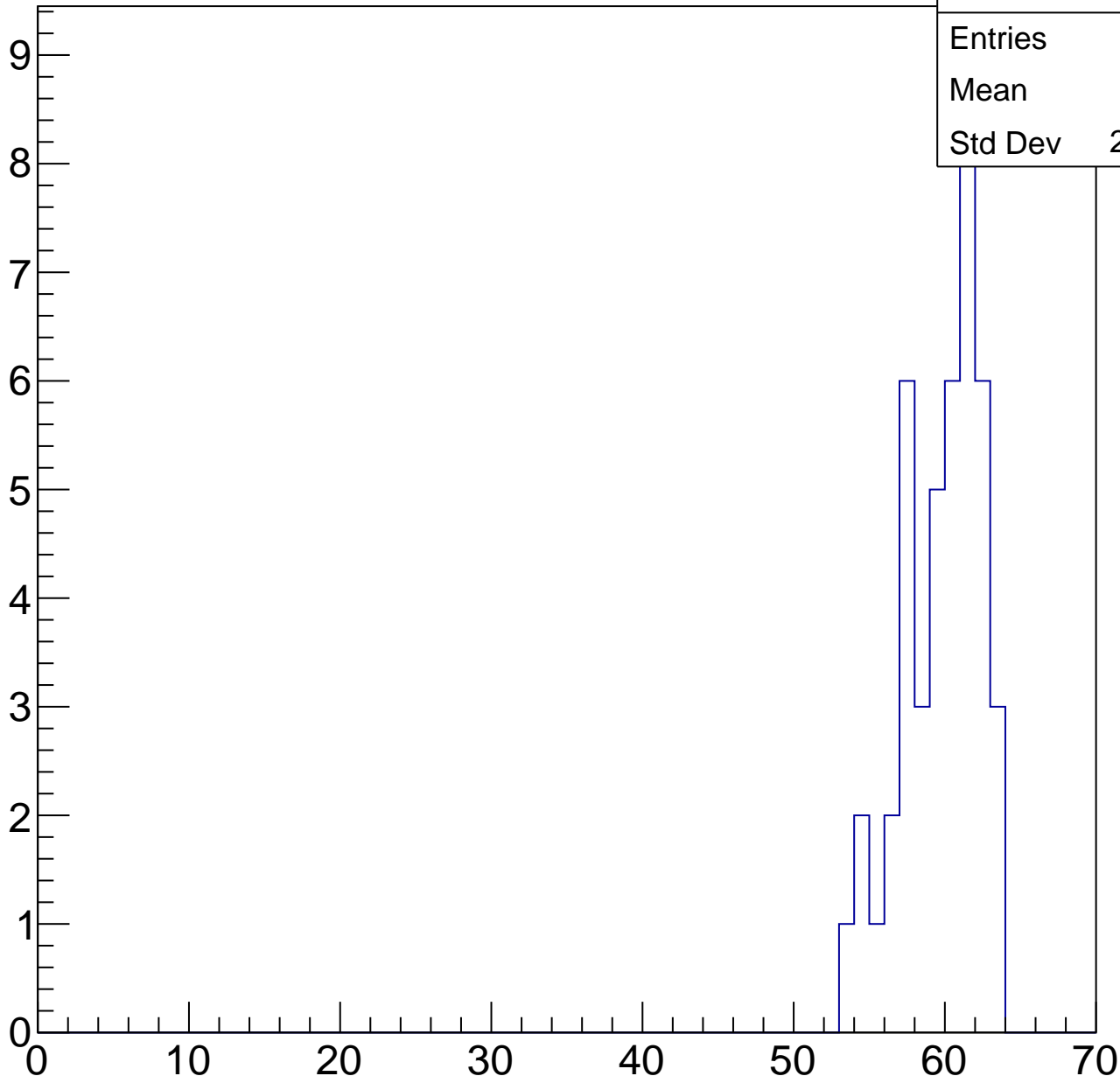
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	59.3
Std Dev	2.555

ampl

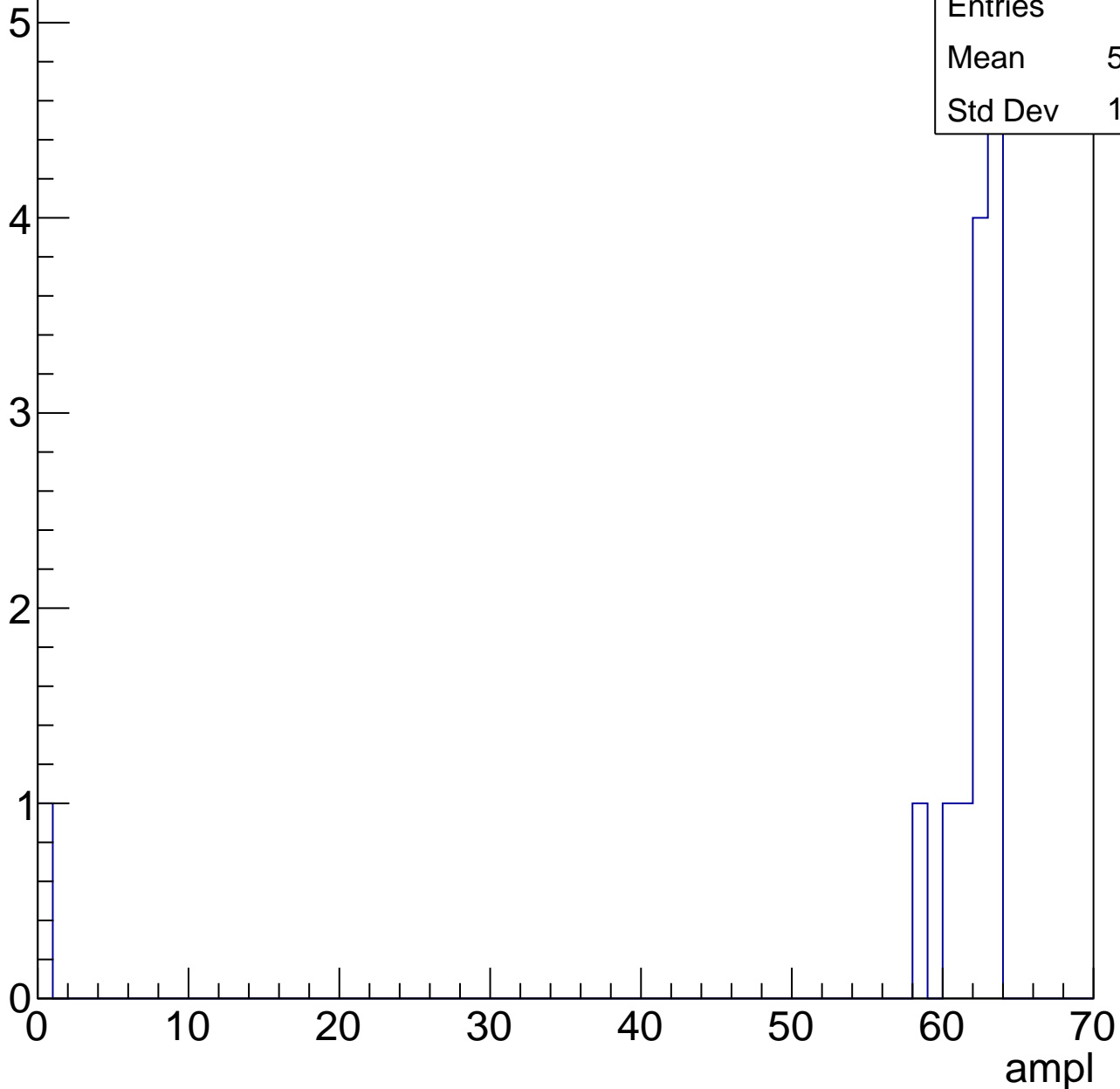


B1L103S, U8-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.08
Std Dev	16.54



B1L103S, U8-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch13, adc0

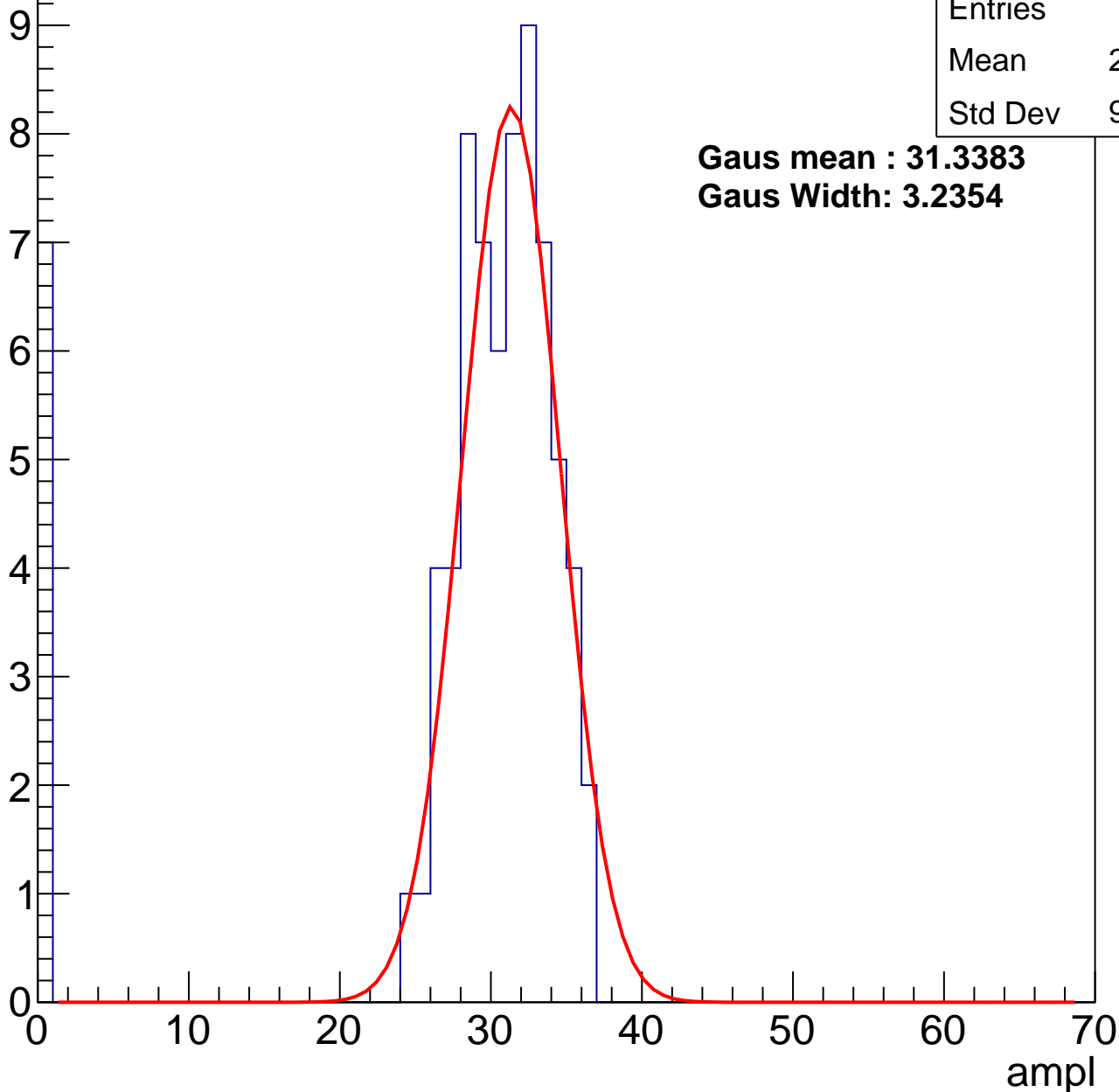
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	27.63
Std Dev	9.399

Gaus mean : 31.3383

Gaus Width: 3.2354



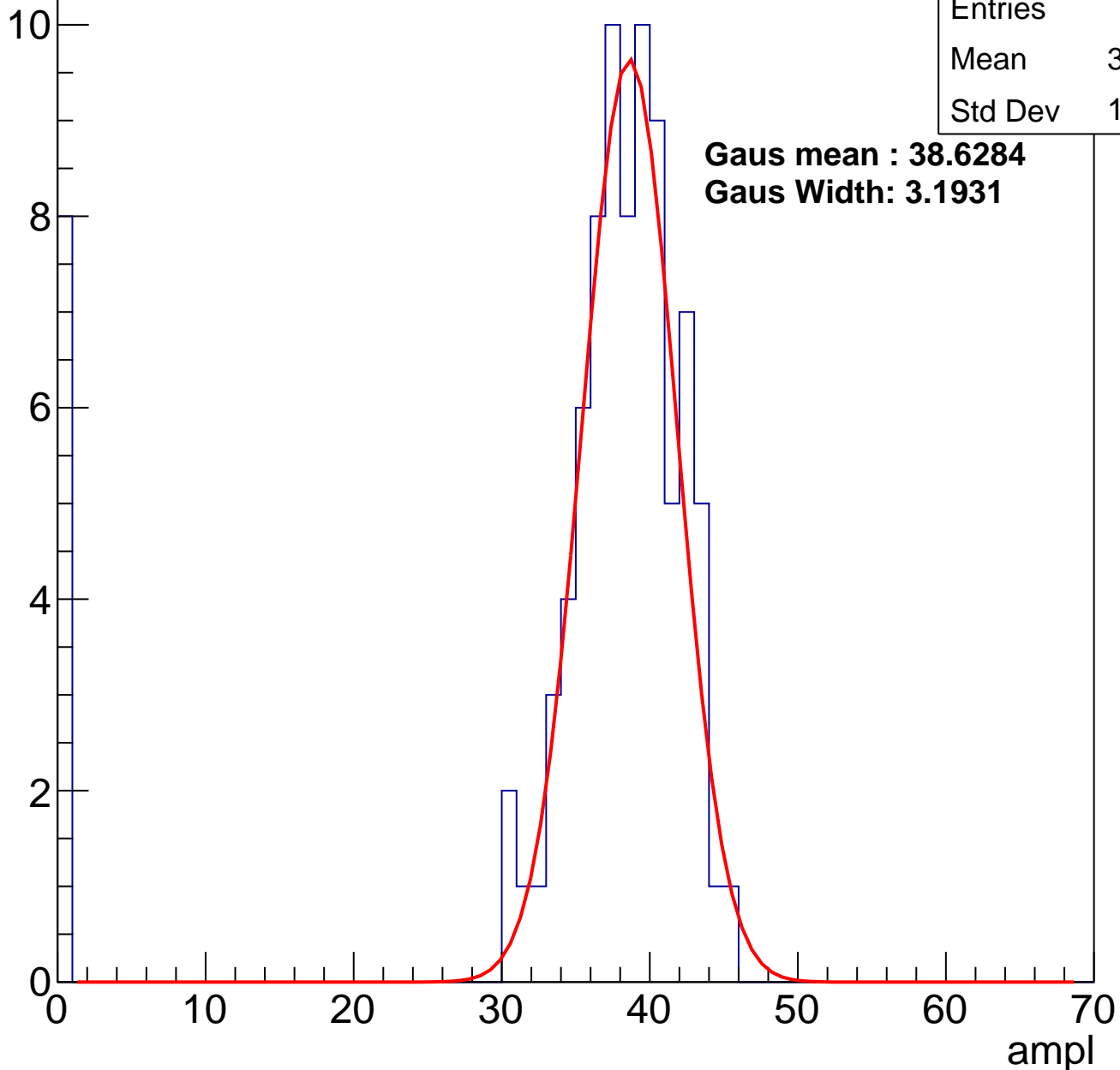
B1L103S, U8-ch13, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	34.64
Std Dev	11.32

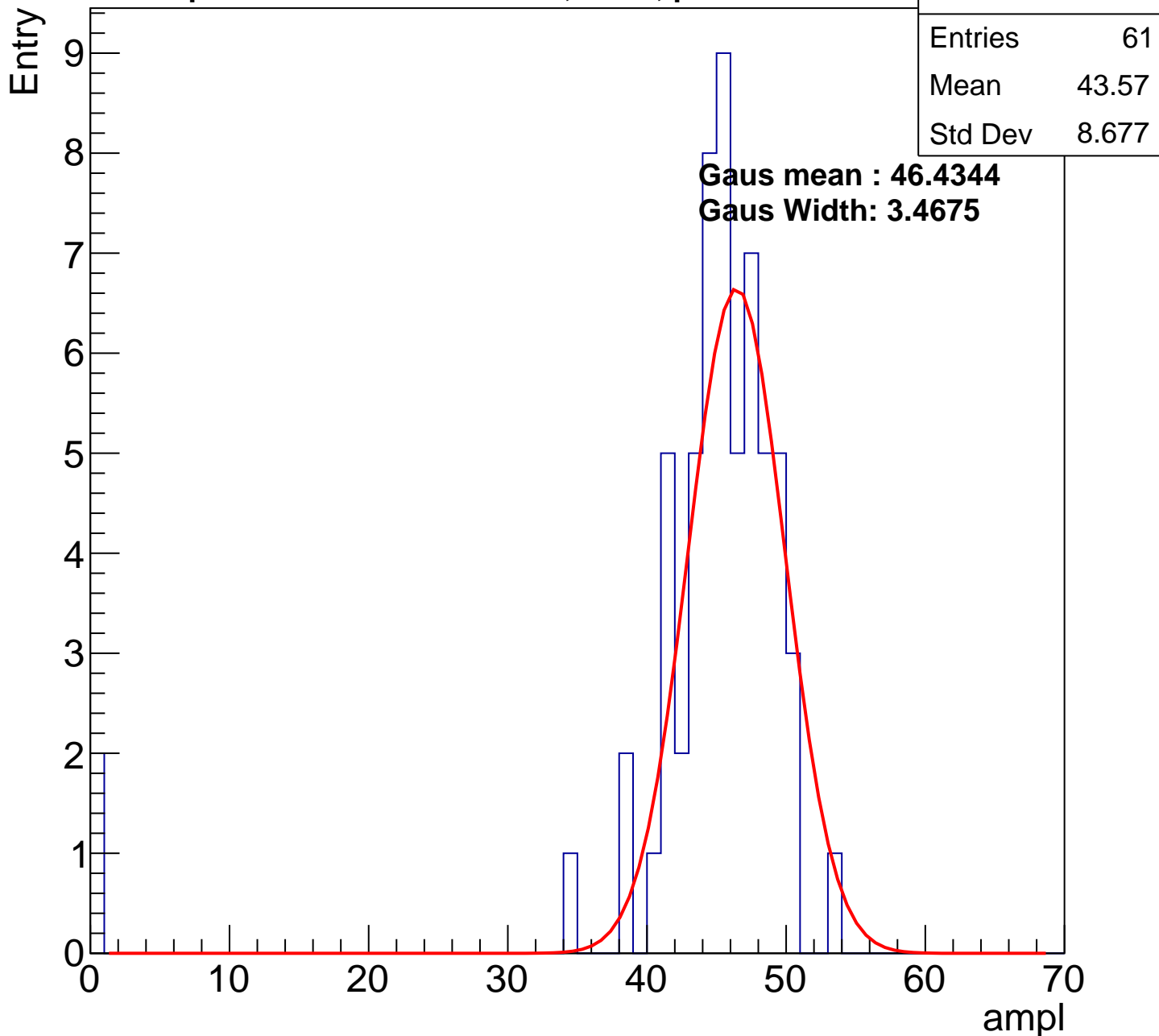
Gaus mean : 38.6284
Gaus Width: 3.1931

Entry



B1L103S, U8-ch13, adc2

calib_packv5_041523_1651.root, FC#0, port C2

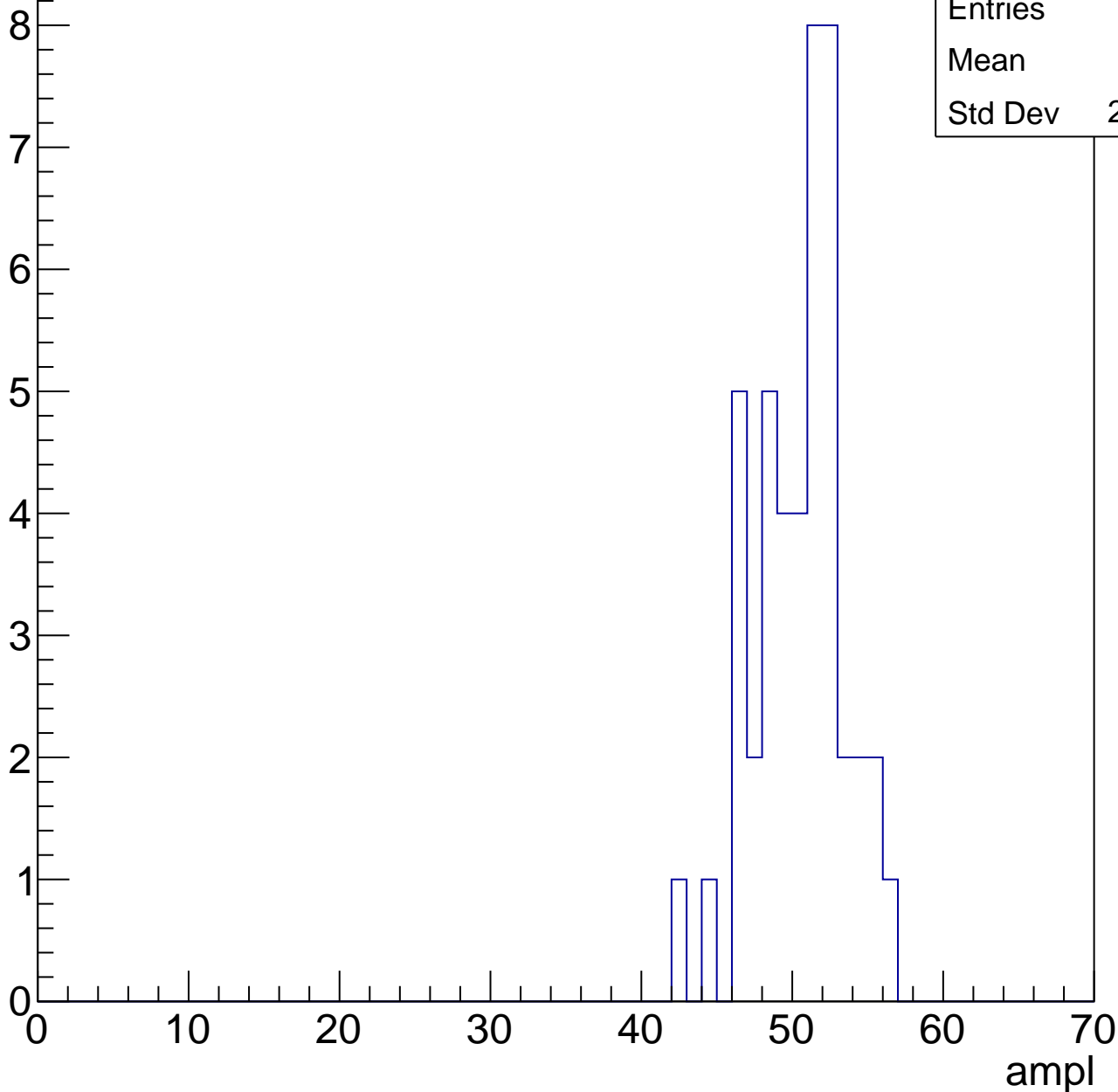


B1L103S, U8-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	50
Std Dev	2.974

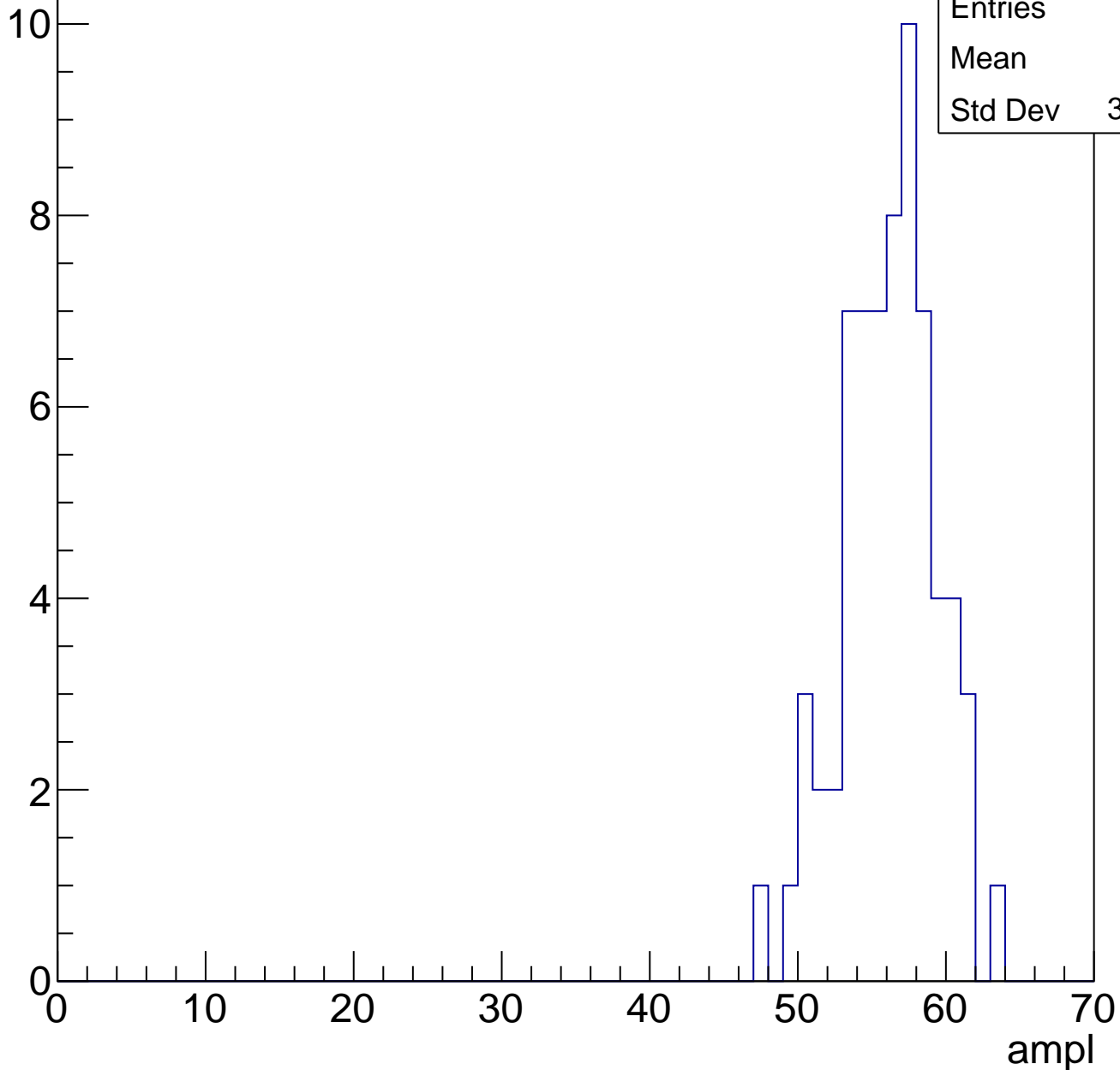


B1L103S, U8-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	55.7
Std Dev	3.172

Entry

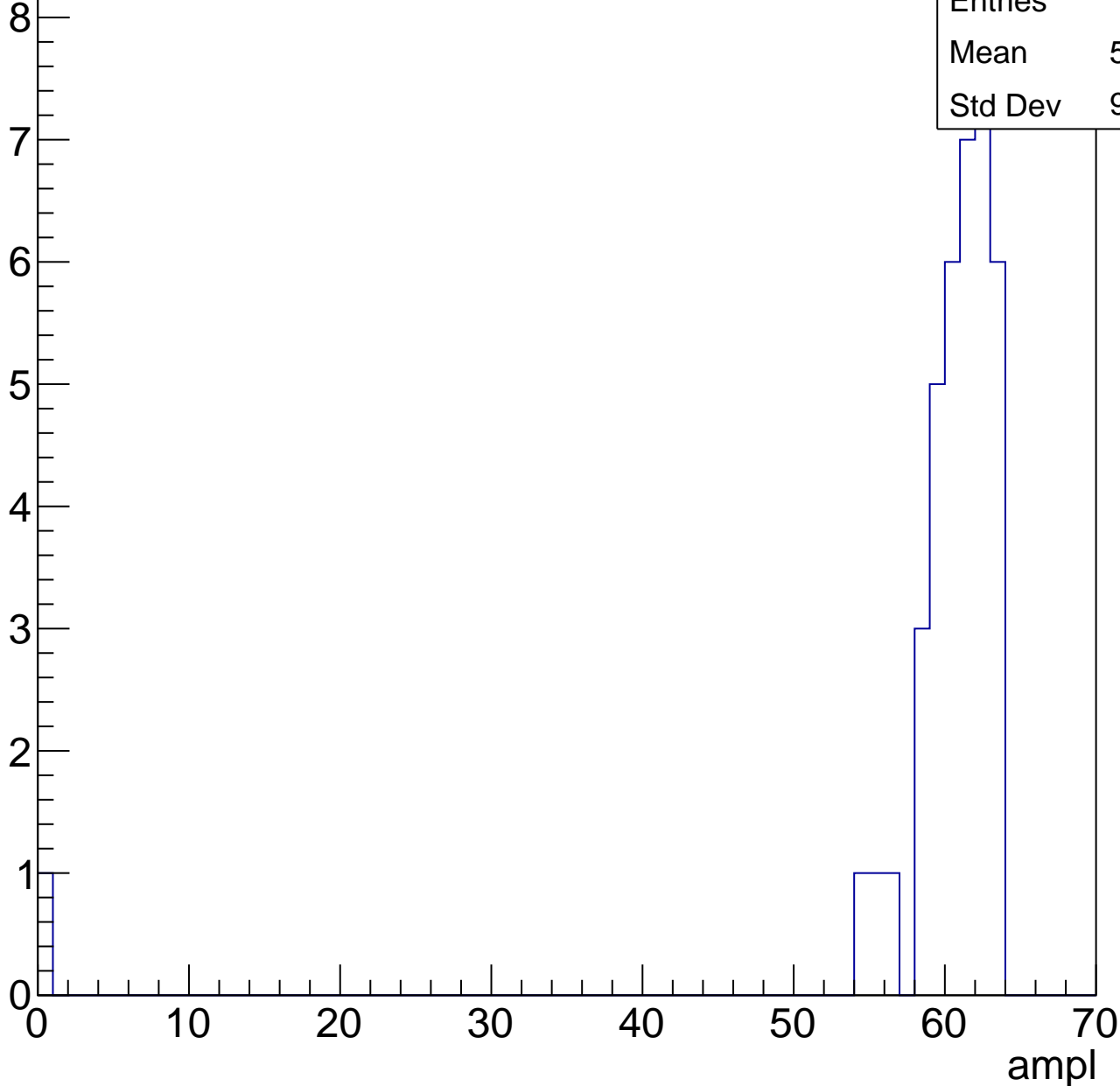


B1L103S, U8-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

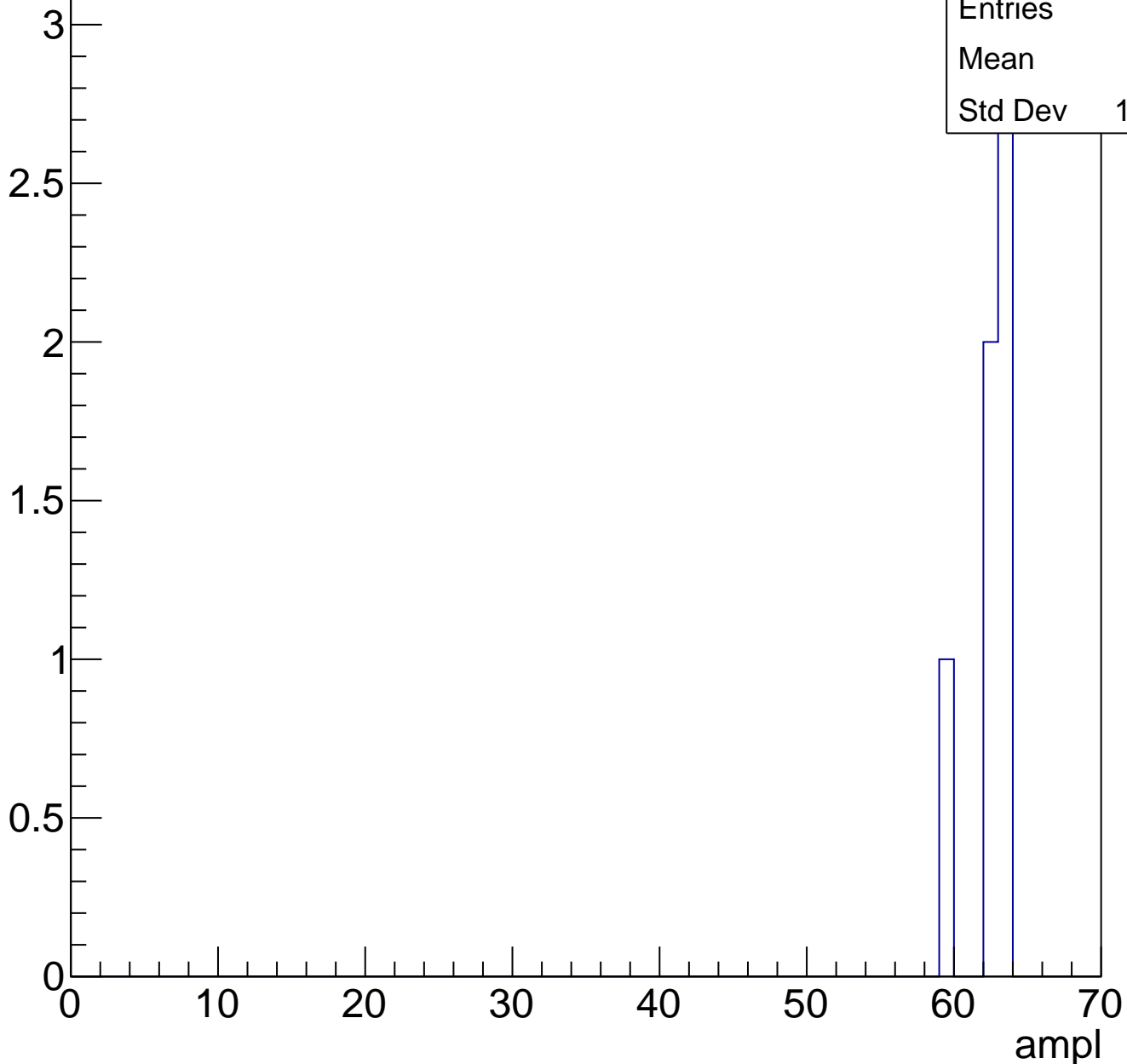
Entries	39
Mean	58.85
Std Dev	9.786



B1L103S, U8-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch14, adc0

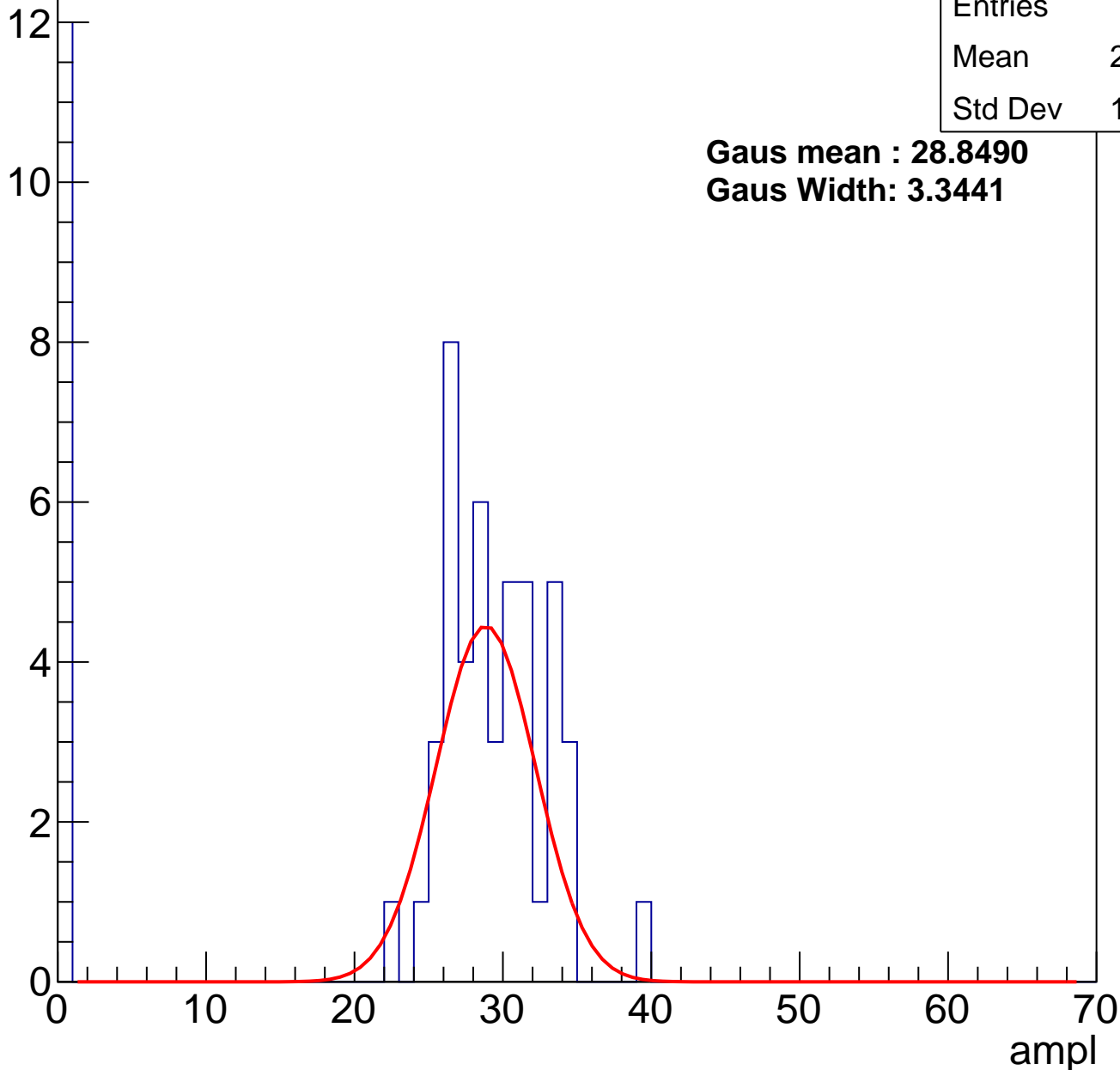
calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	23.02
Std Dev	12.12

Gaus mean : 28.8490

Gaus Width: 3.3441

Entry



B1L103S, U8-ch14, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	30.21
Std Dev	12.6

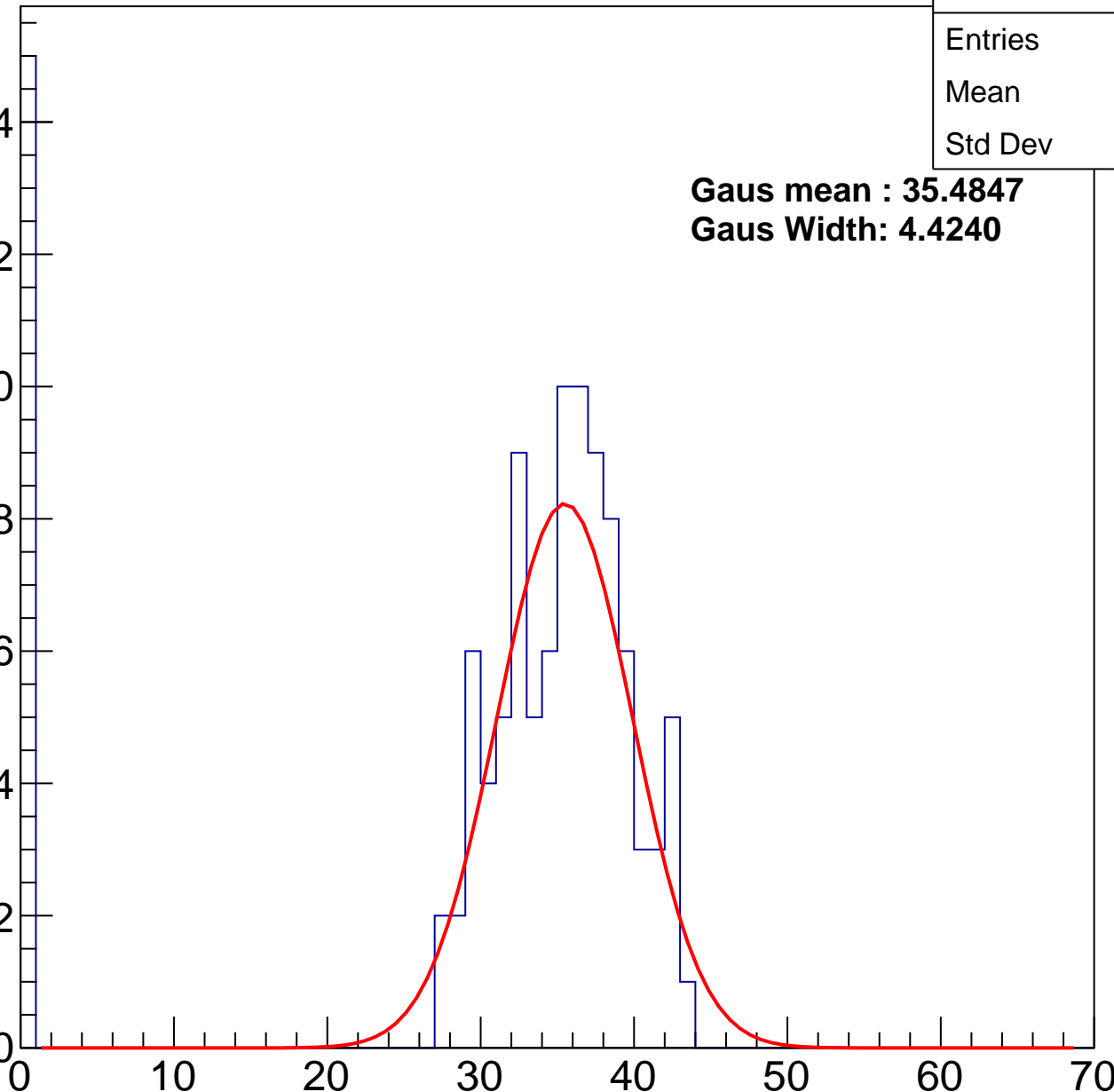
Gaus mean : 35.4847

Gaus Width: 4.4240

Entry

14
12
10
8
6
4
2
0

ampl

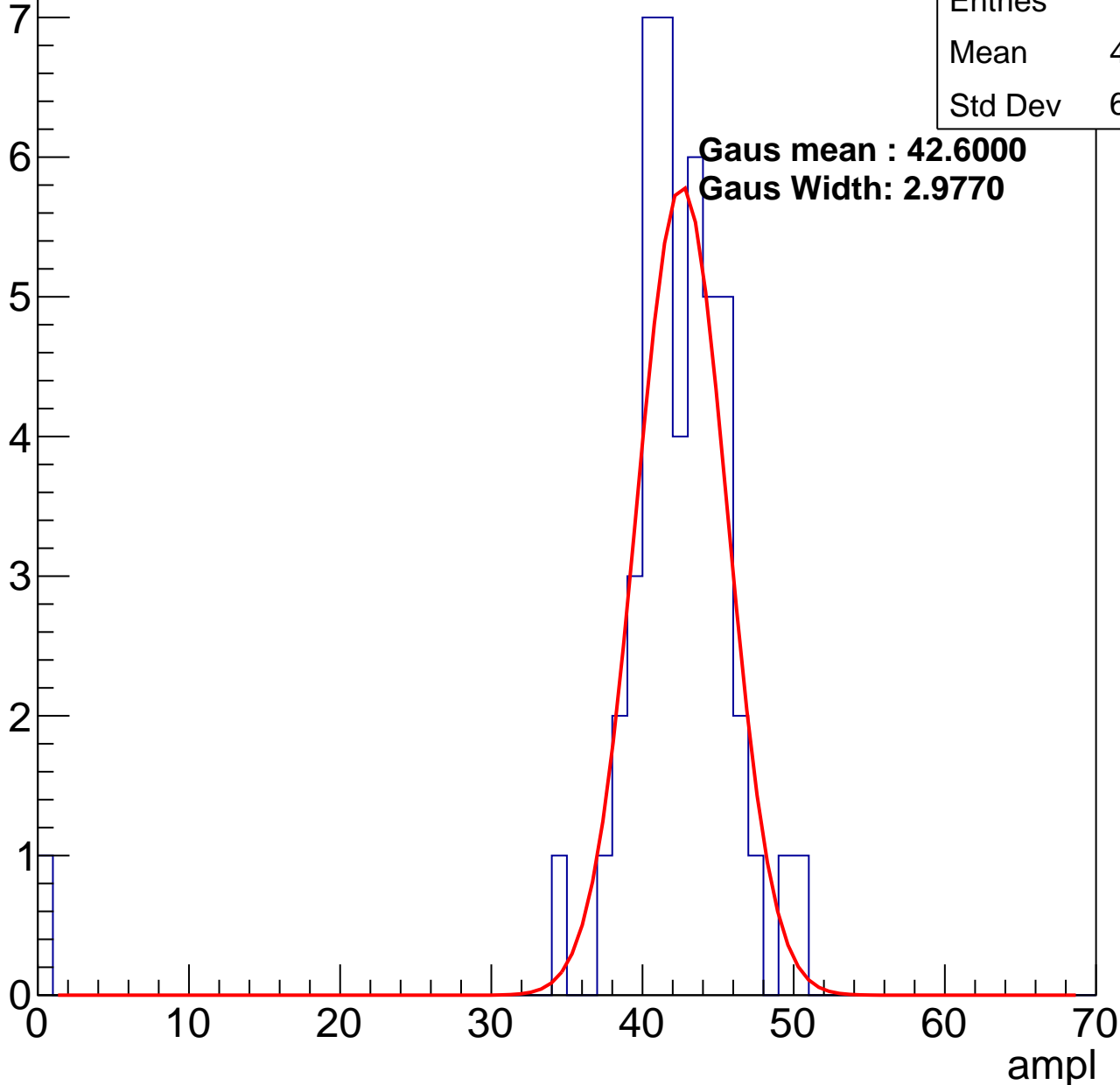


B1L103S, U8-ch14, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	41.28
Std Dev	6.785

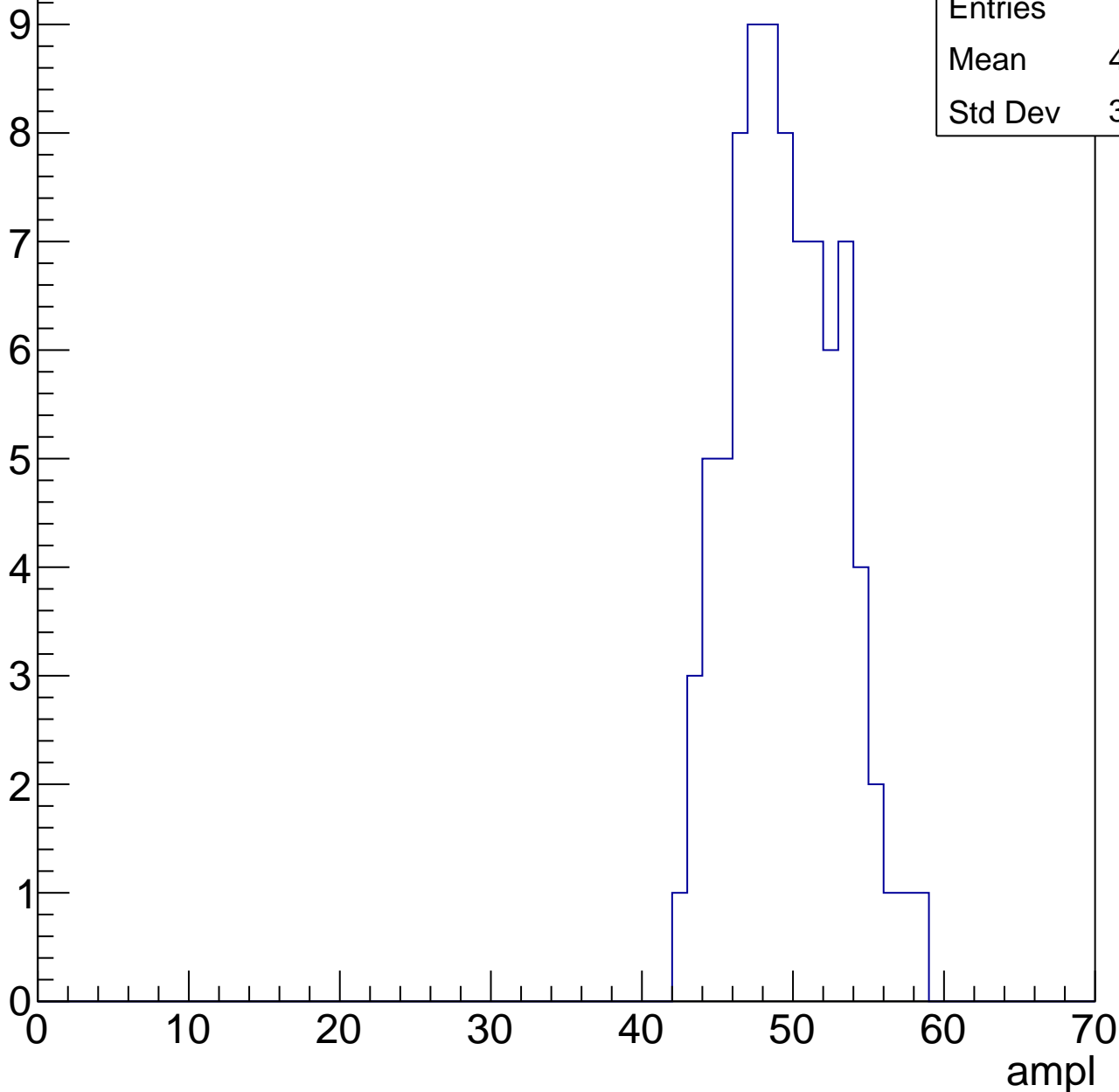


B1L103S, U8-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

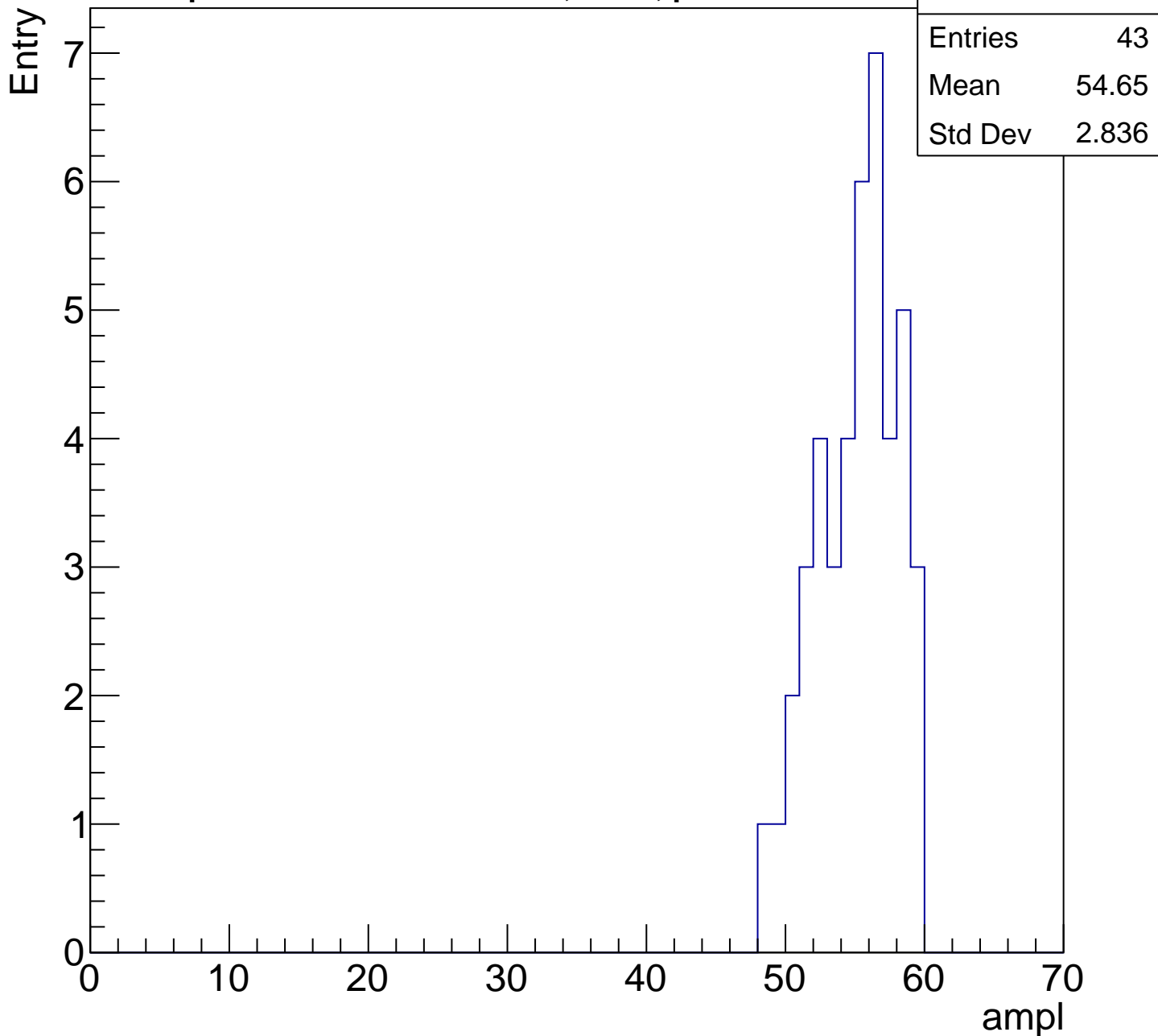
Entry

Entries	84
Mean	49.02
Std Dev	3.529



B1L103S, U8-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

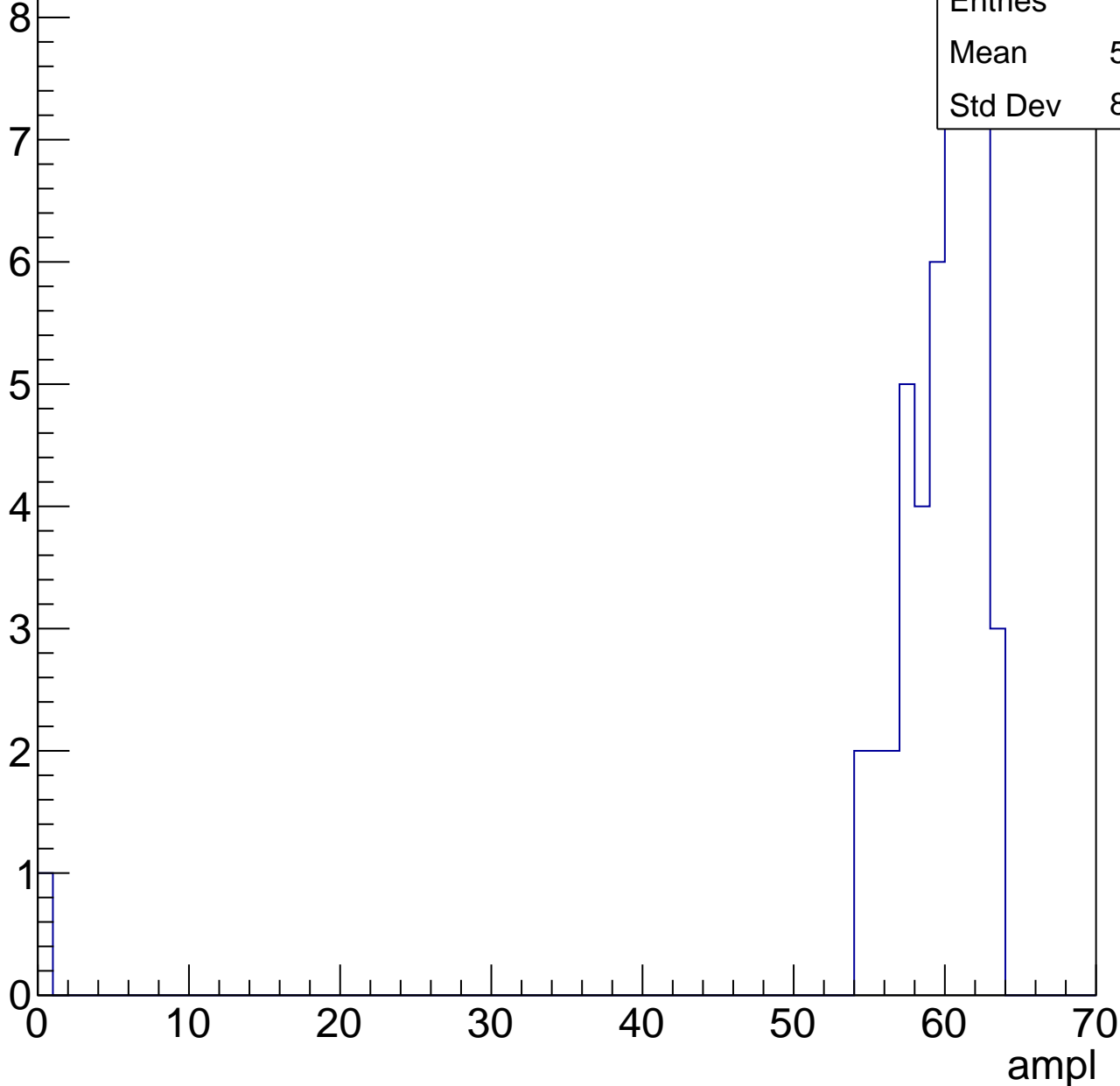


B1L103S, U8-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.24
Std Dev	8.733

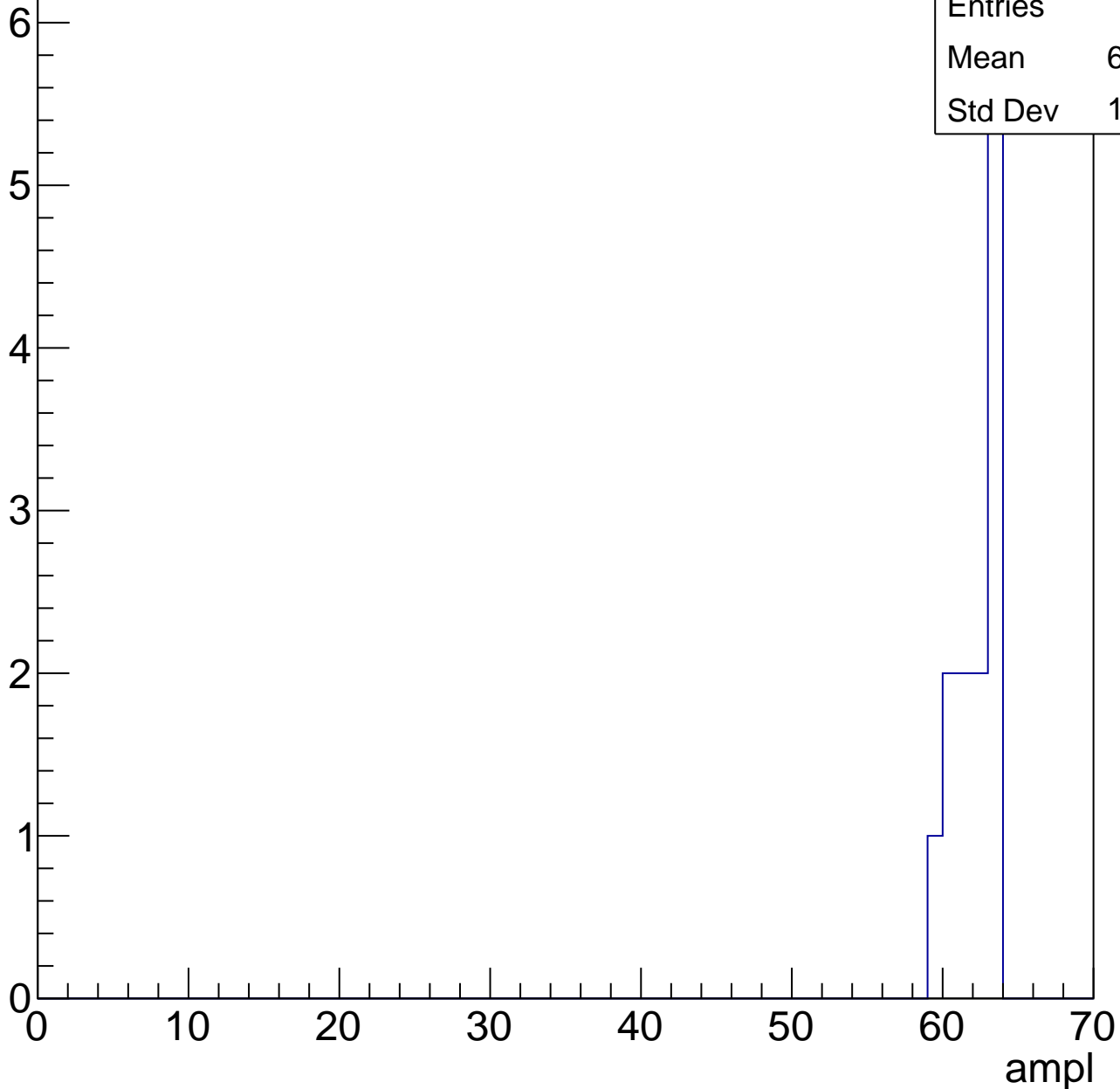


B1L103S, U8-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.77
Std Dev	1.367

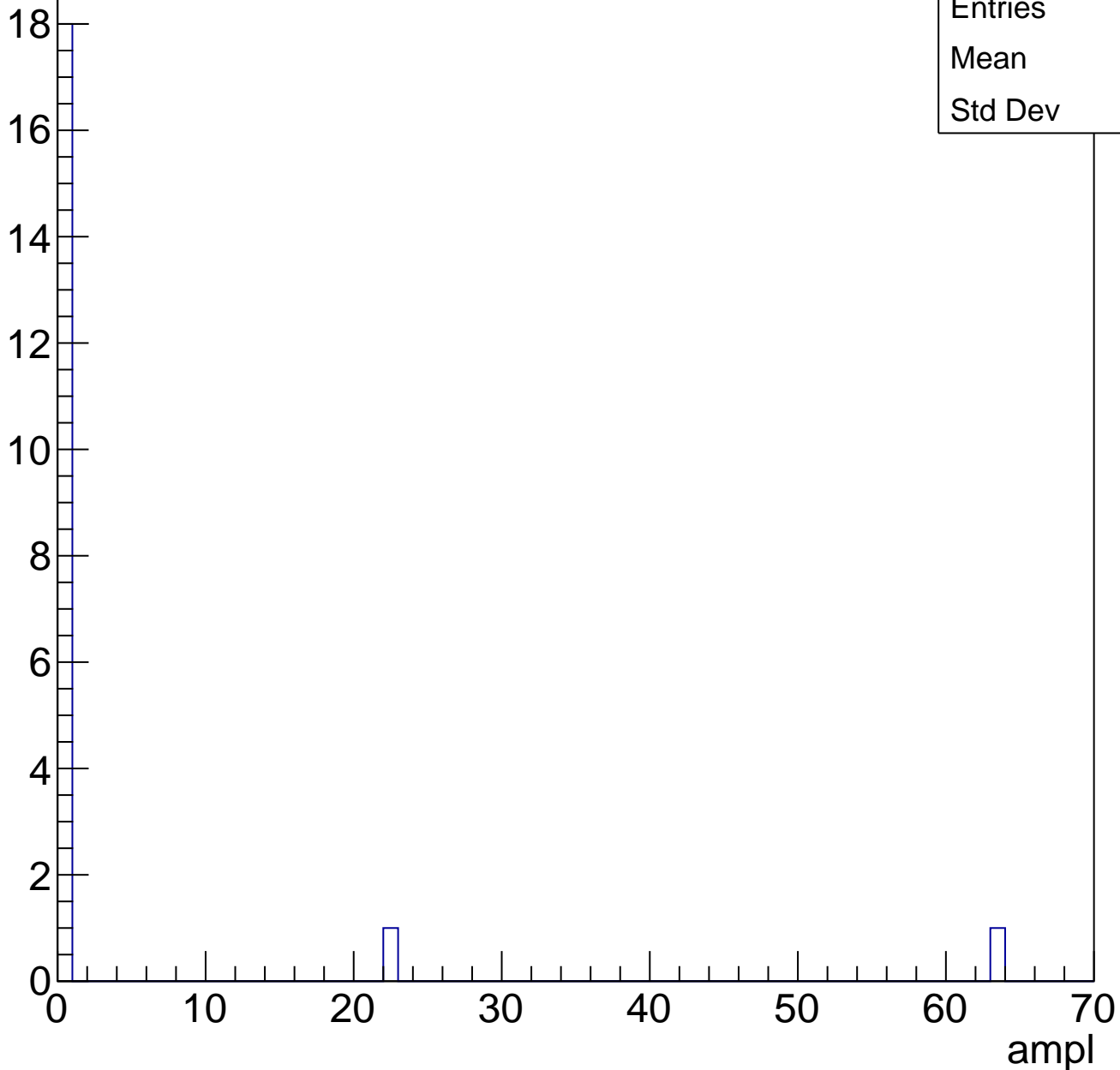


B1L103S, U8-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.25
Std Dev	14.3

Entry



B1L103S, U8-ch15, adc0

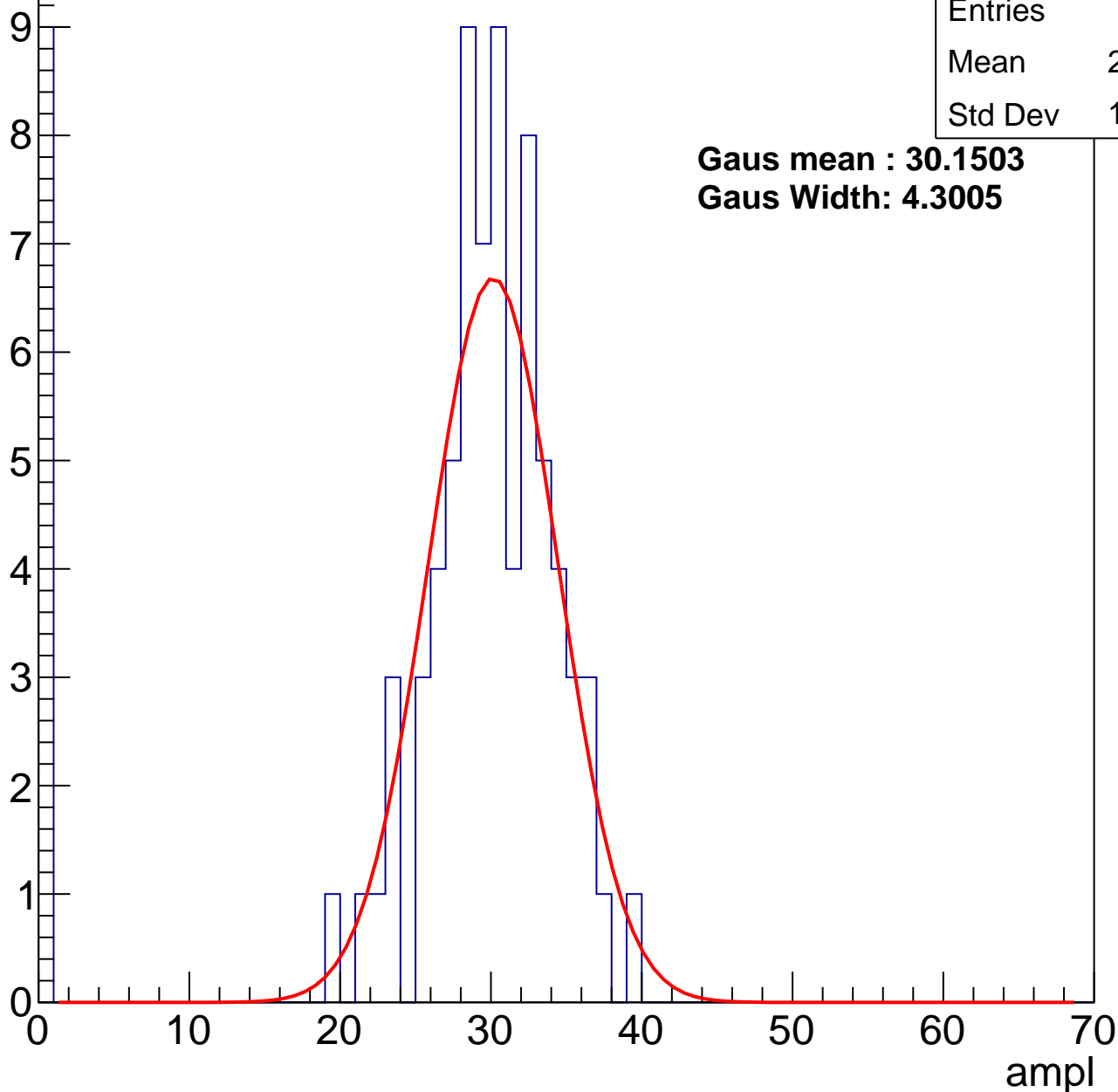
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	26.42
Std Dev	10.04

Gaus mean : 30.1503

Gaus Width: 4.3005



B1L103S, U8-ch15, adc1

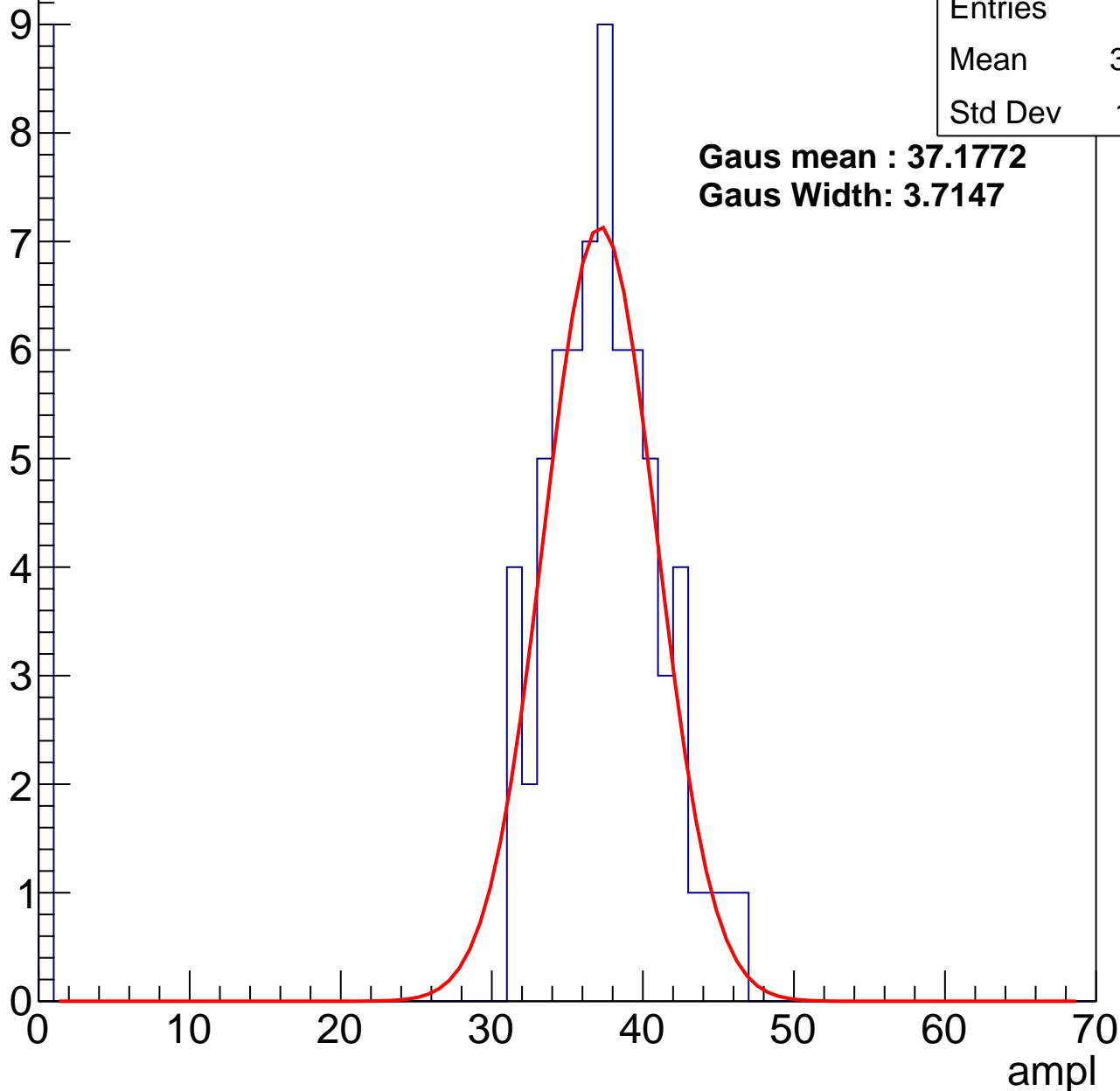
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	32.67
Std Dev	12.41

Gaus mean : 37.1772

Gaus Width: 3.7147



B1L103S, U8-ch15, adc2

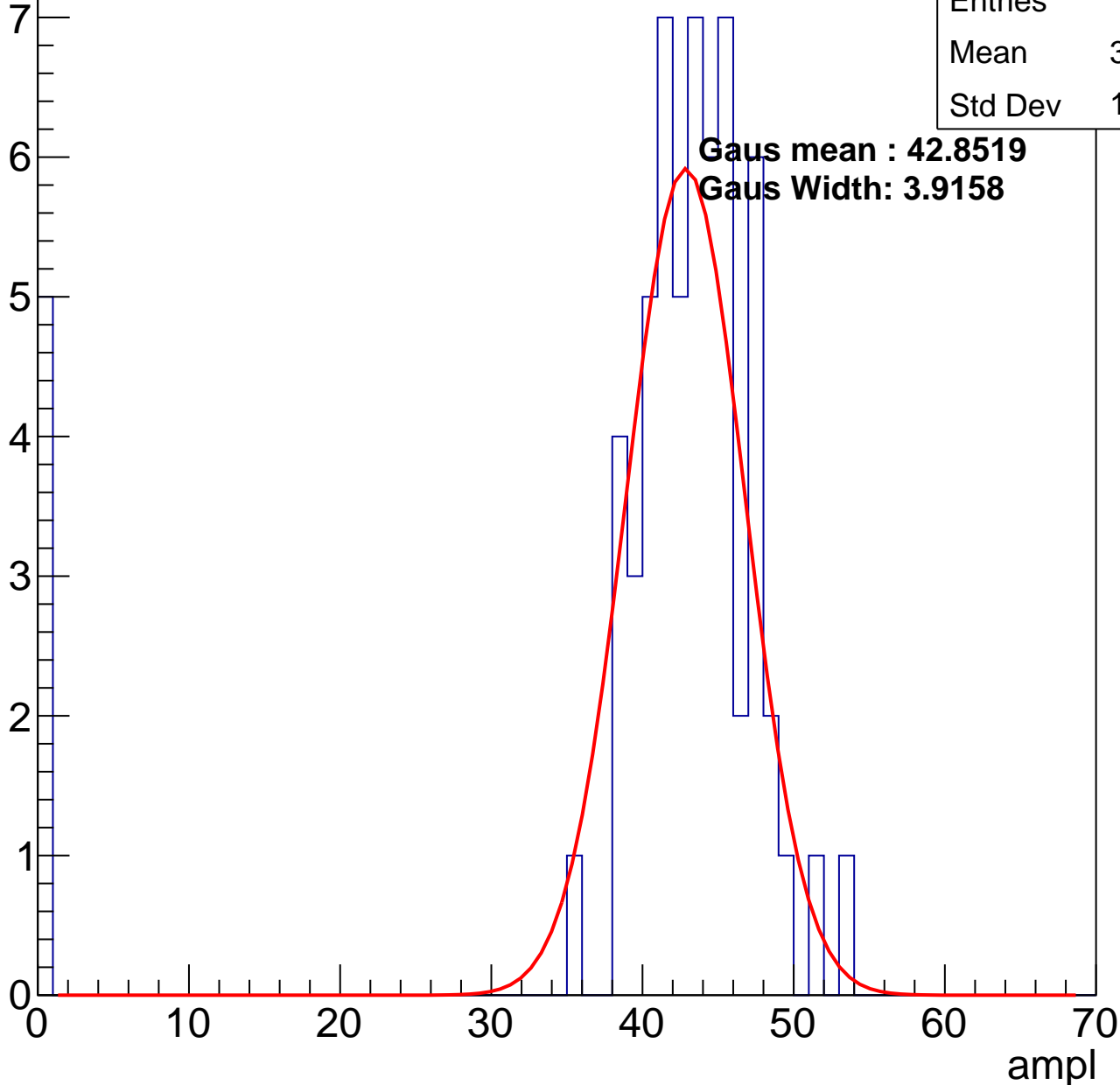
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	39.75
Std Dev	12.13

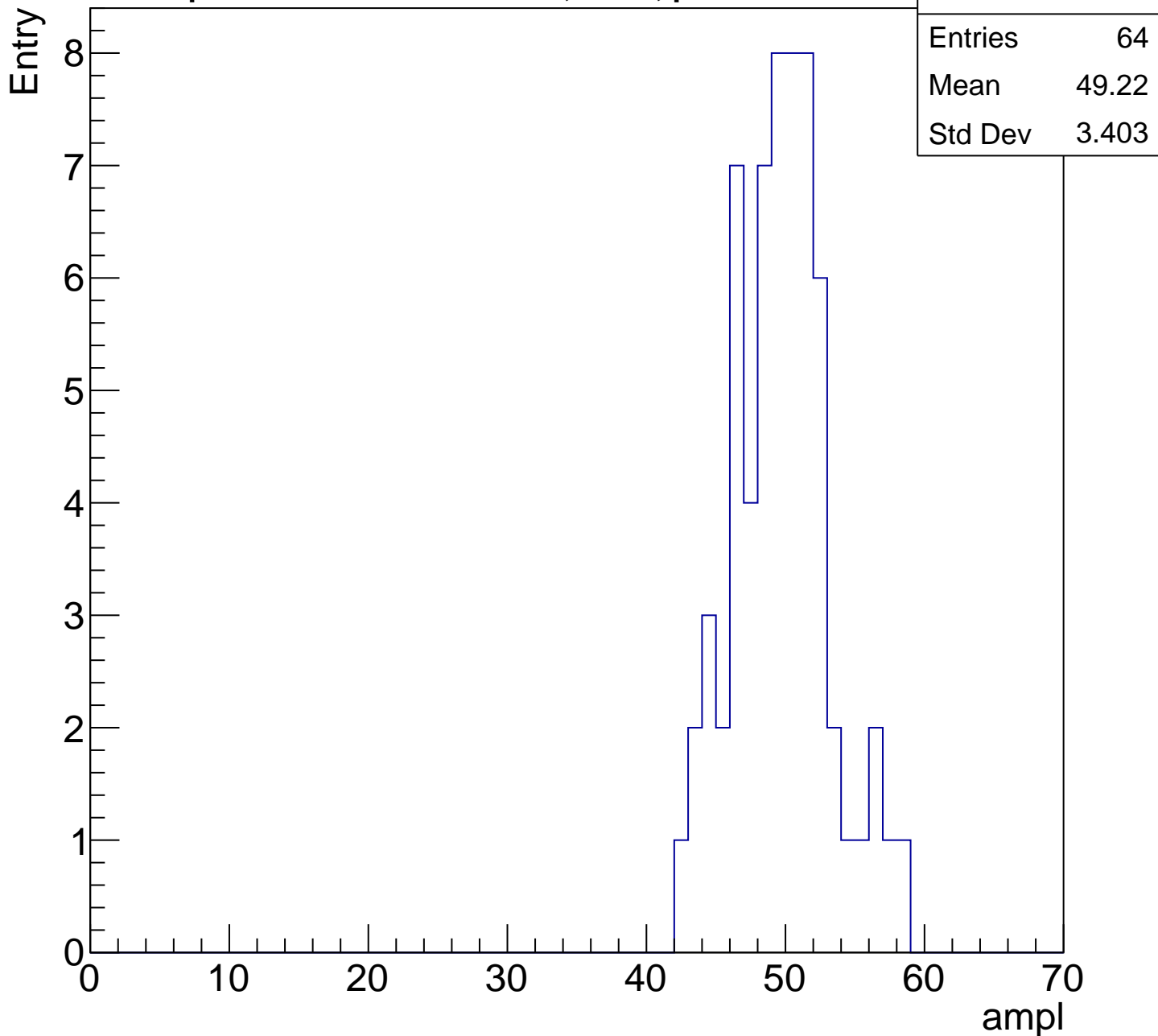
Gaus mean : 42.8519

Gaus Width: 3.9158



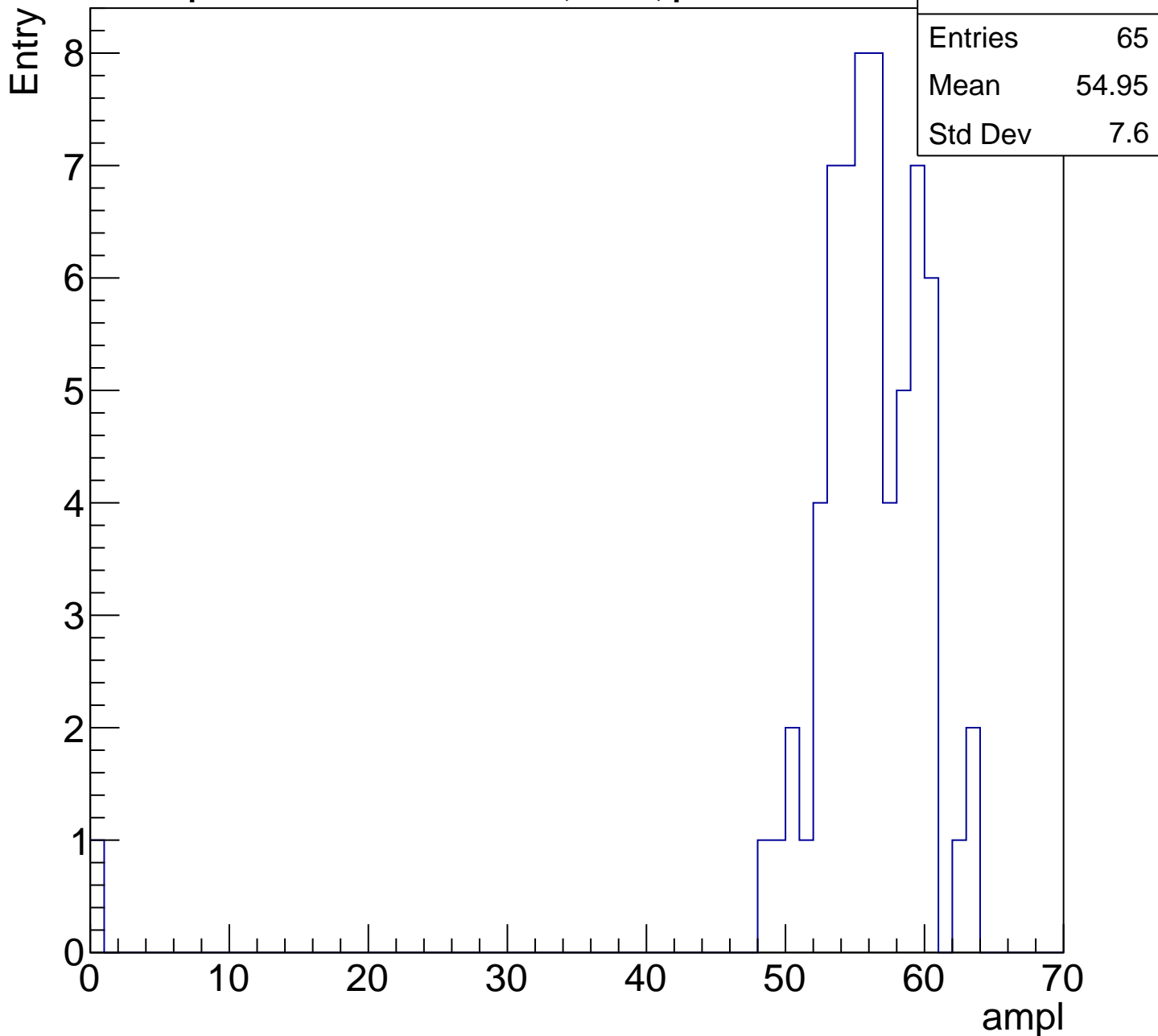
B1L103S, U8-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

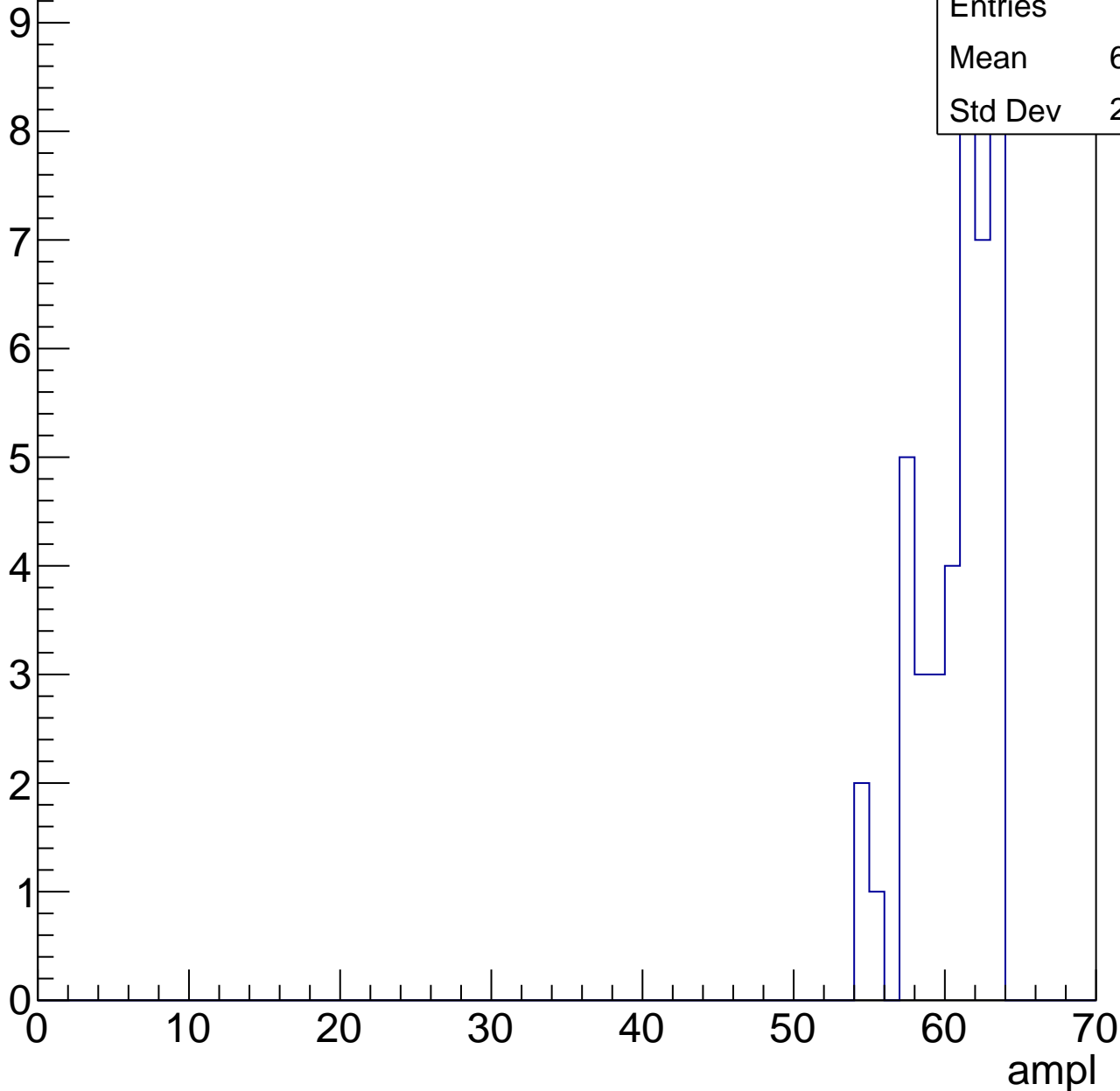


B1L103S, U8-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

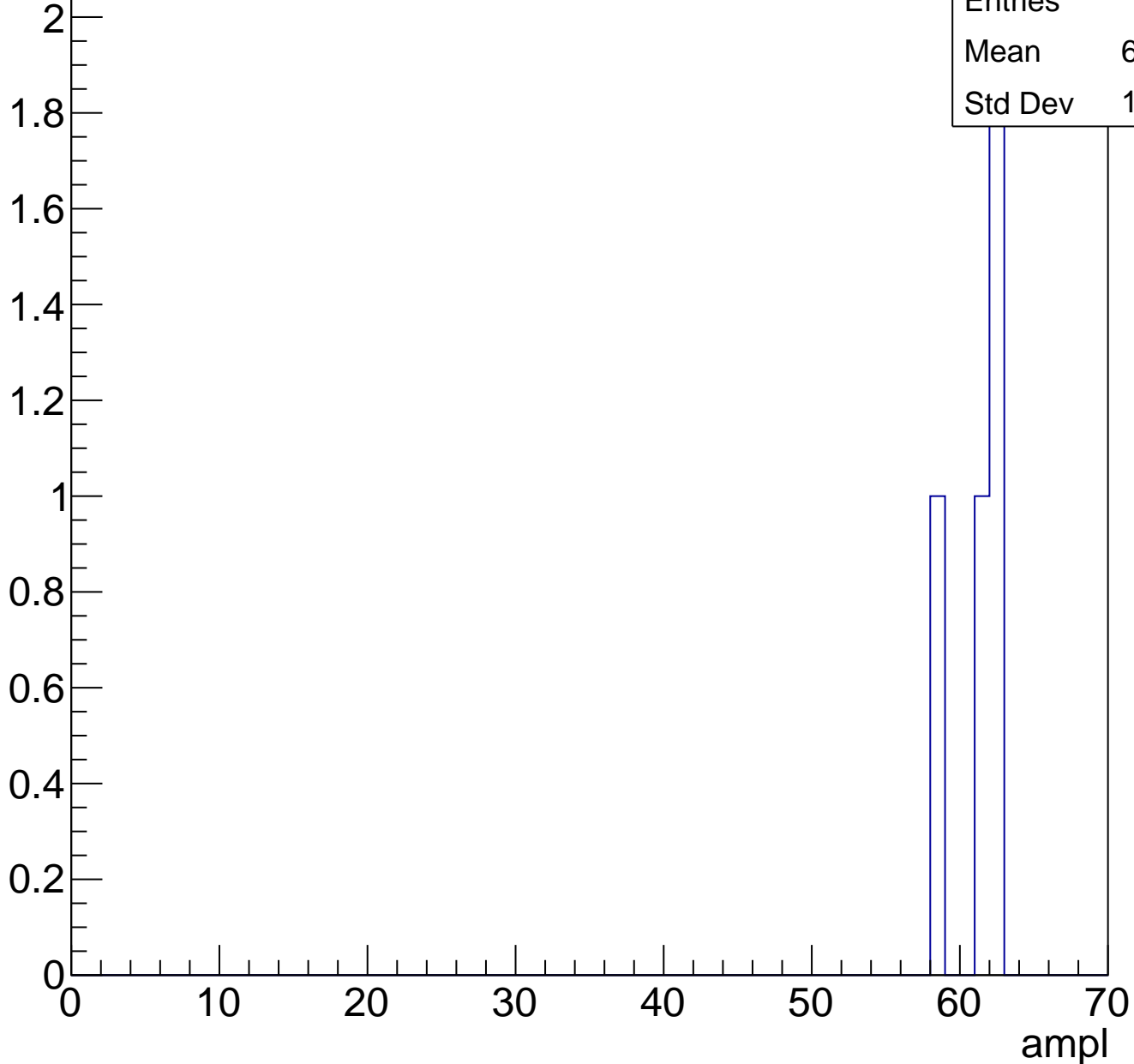
Entries	42
Mean	60.14
Std Dev	2.513



B1L103S, U8-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch16, adc0

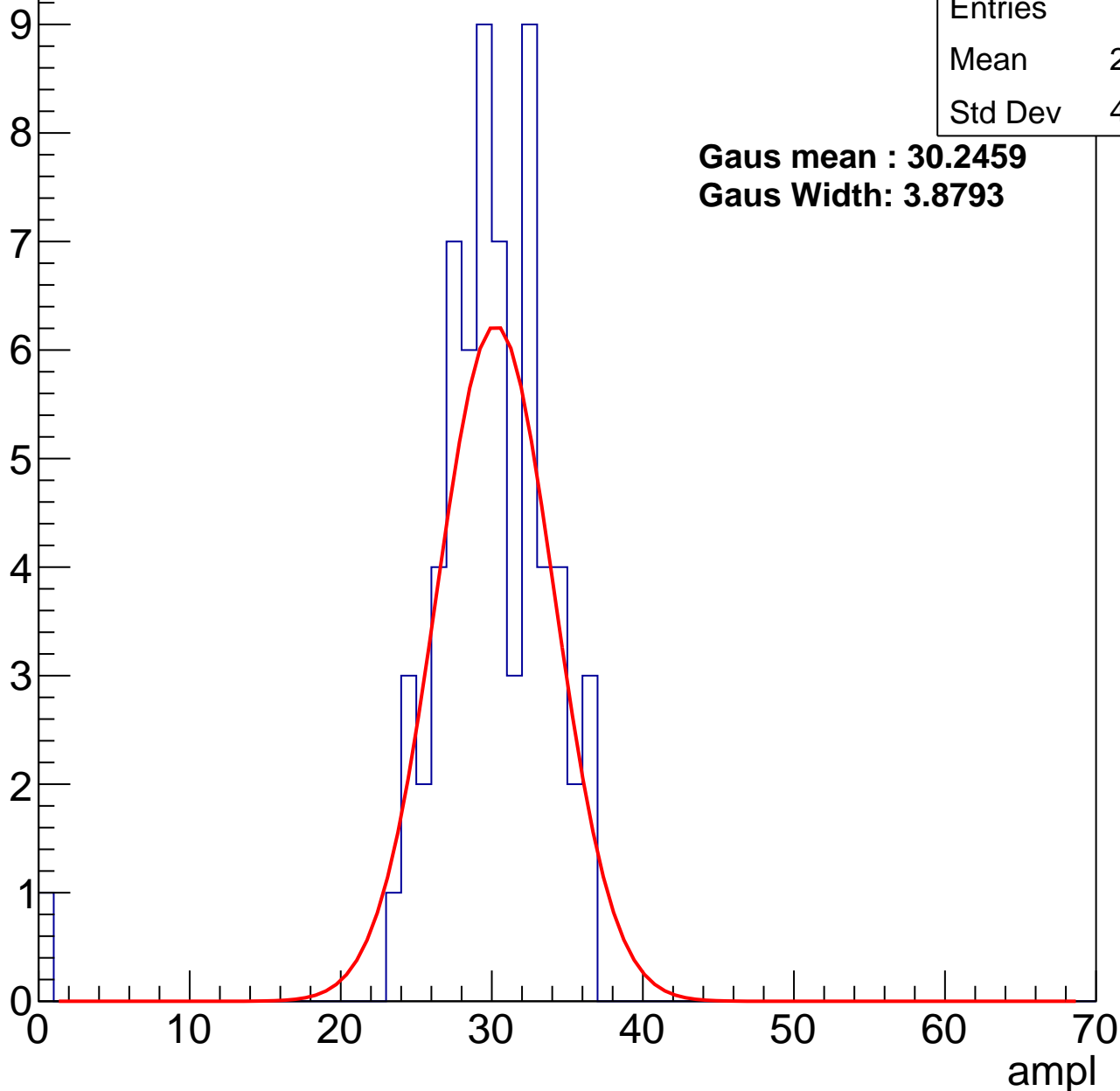
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	29.29
Std Dev	4.854

Gaus mean : 30.2459

Gaus Width: 3.8793



B1L103S, U8-ch16, adc1

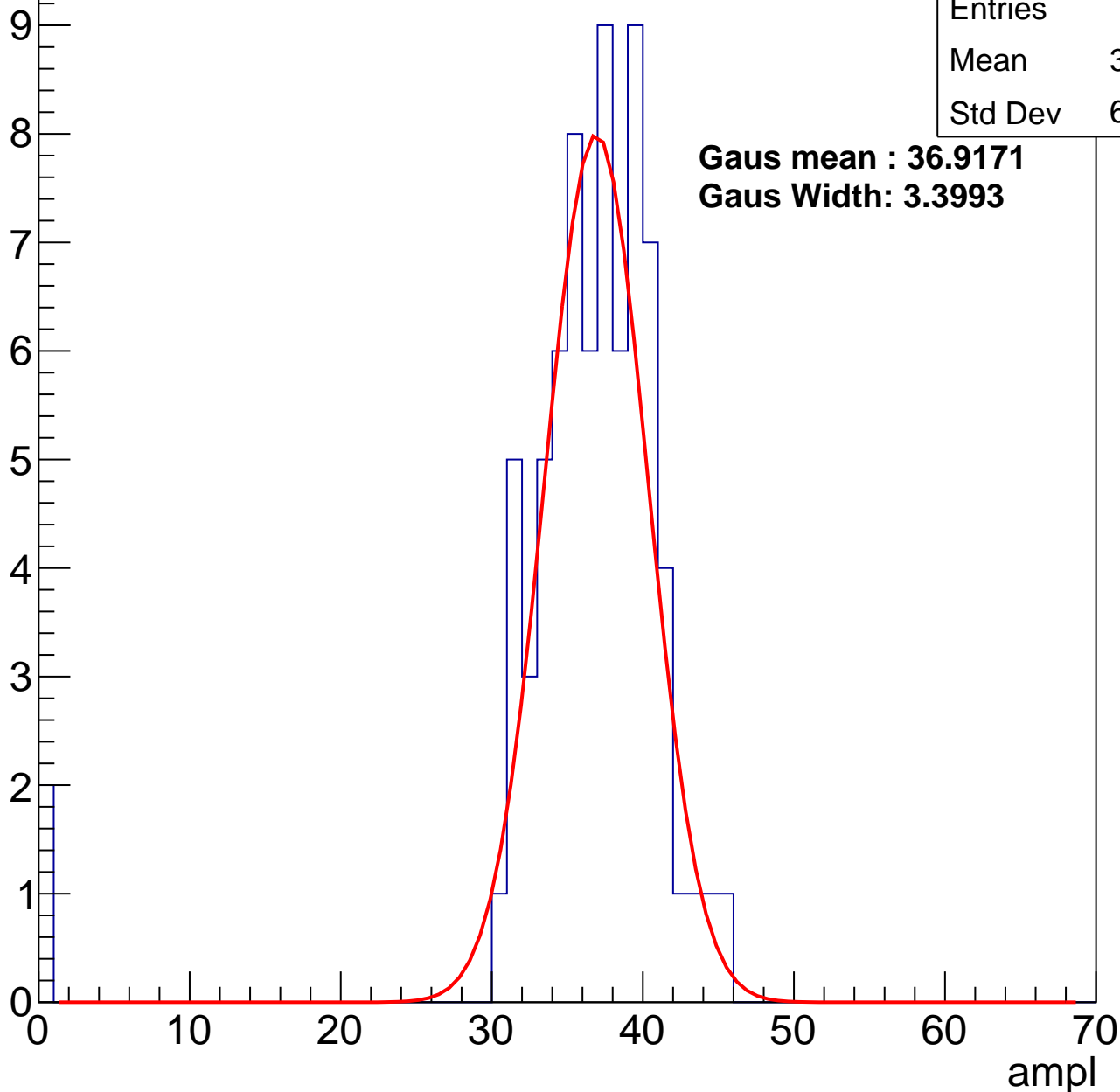
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	35.68
Std Dev	6.759

Gaus mean : 36.9171

Gaus Width: 3.3993



B1L103S, U8-ch16, adc2

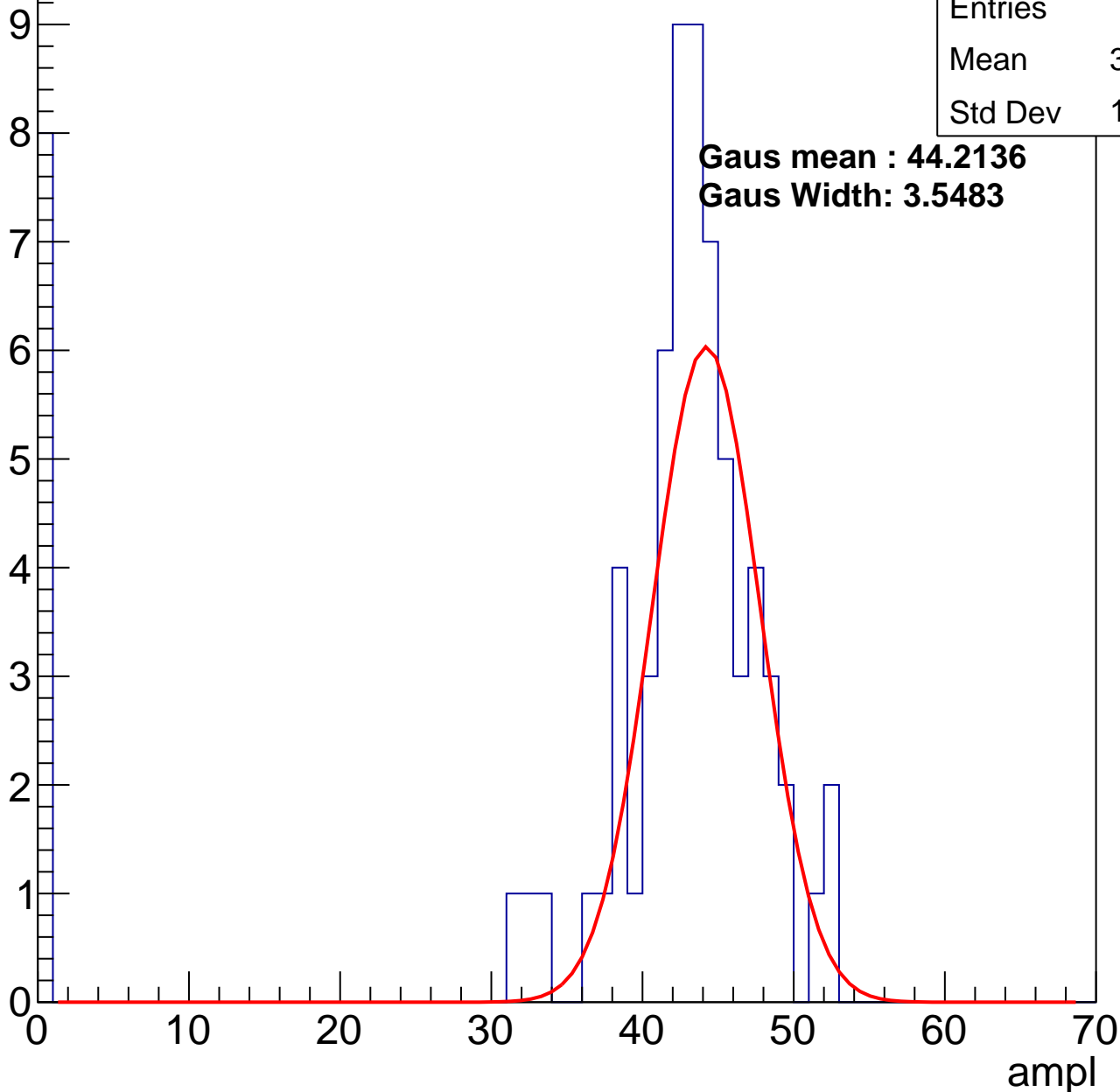
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	38.15
Std Dev	14.05

Gaus mean : 44.2136

Gaus Width: 3.5483

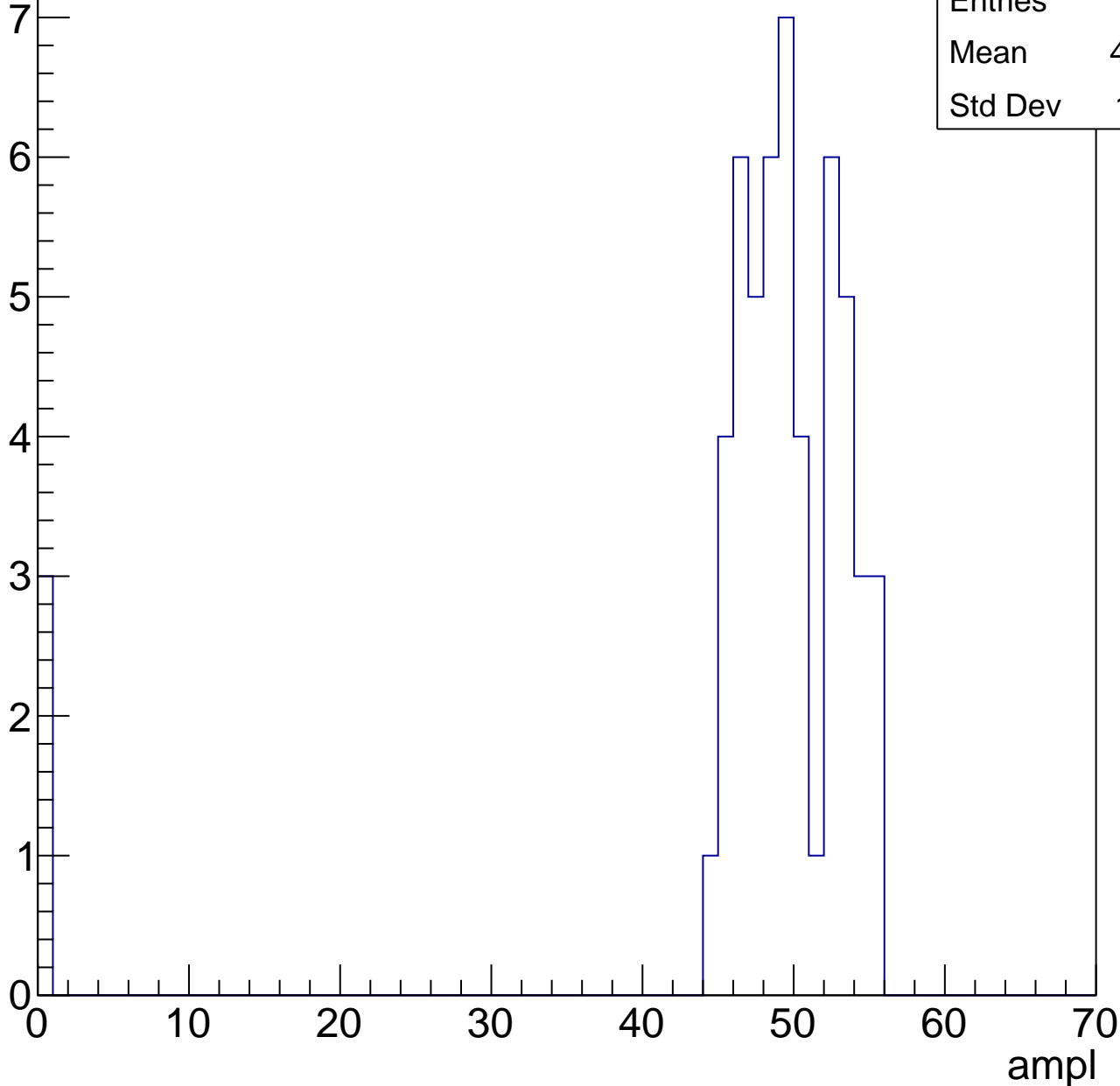


B1L103S, U8-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	46.69
Std Dev	11.71

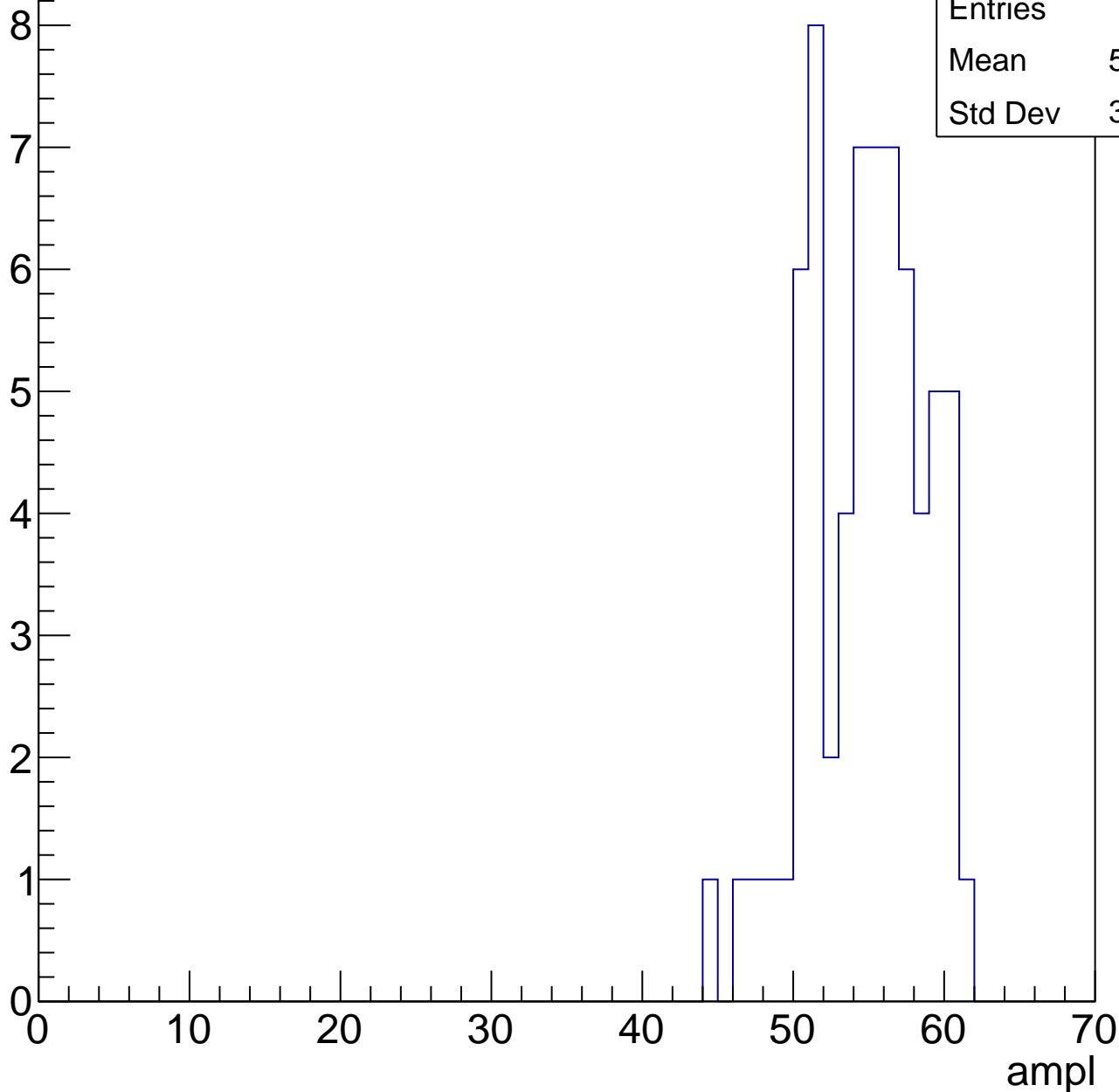


B1L103S, U8-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	54.37
Std Dev	3.769

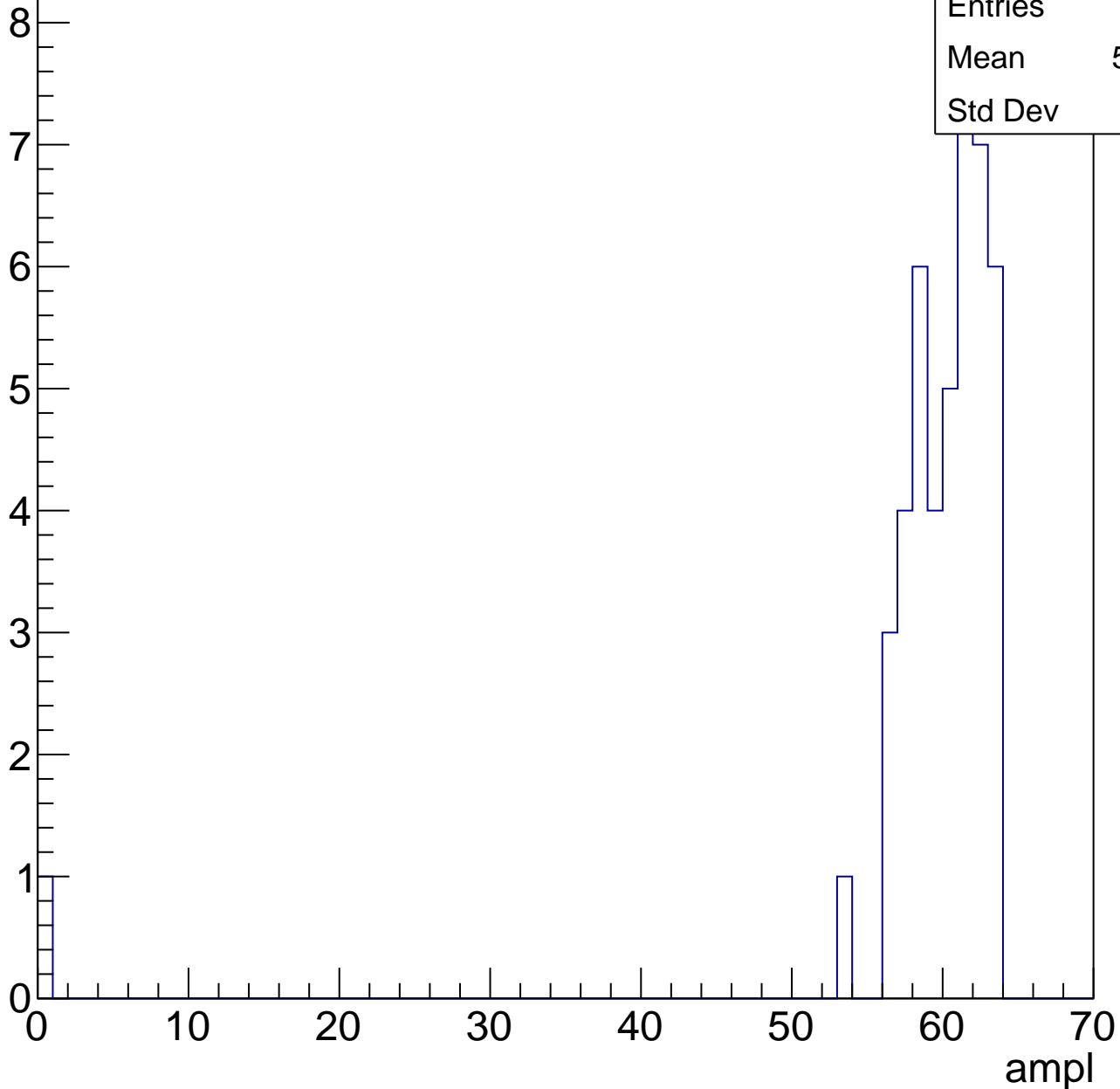


B1L103S, U8-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

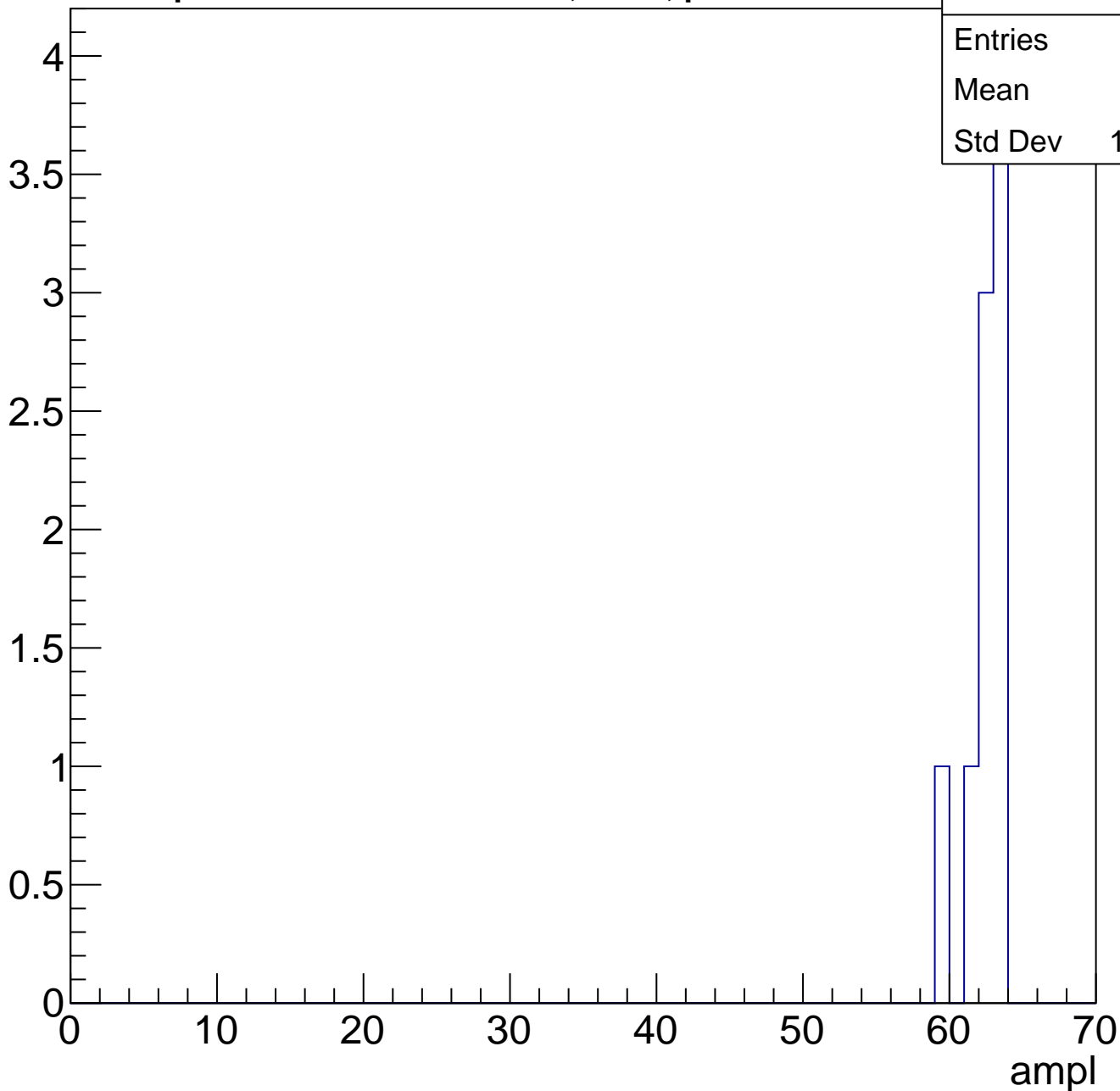
Entries	45
Mean	58.51
Std Dev	9.13



B1L103S, U8-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

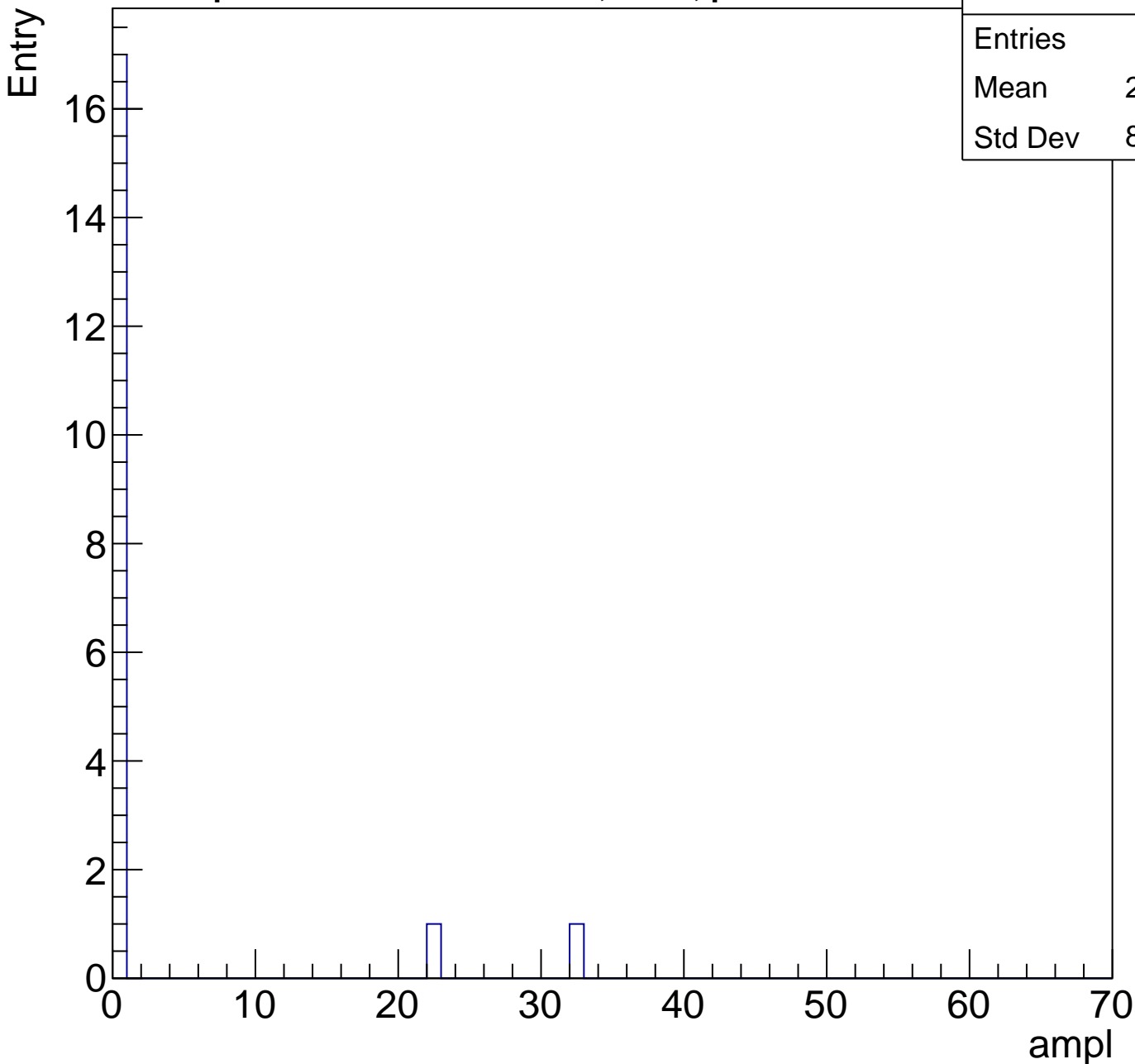
Entry



B1L103S, U8-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	2.842
Std Dev	8.443



B1L103S, U8-ch17, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	24.22
Std Dev	12.27

Gaus mean : 30.4650

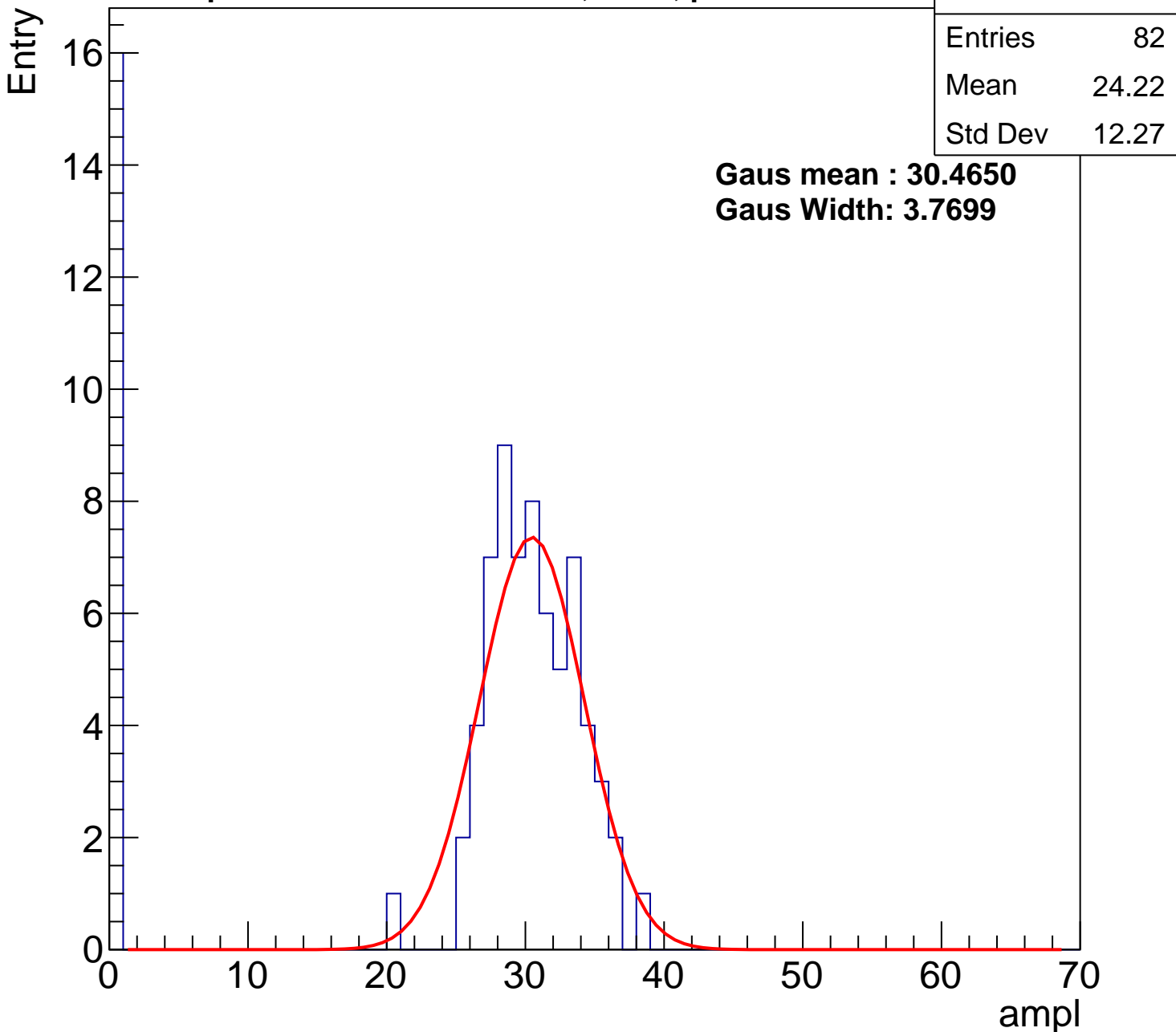
Gaus Width: 3.7699

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch17, adc1

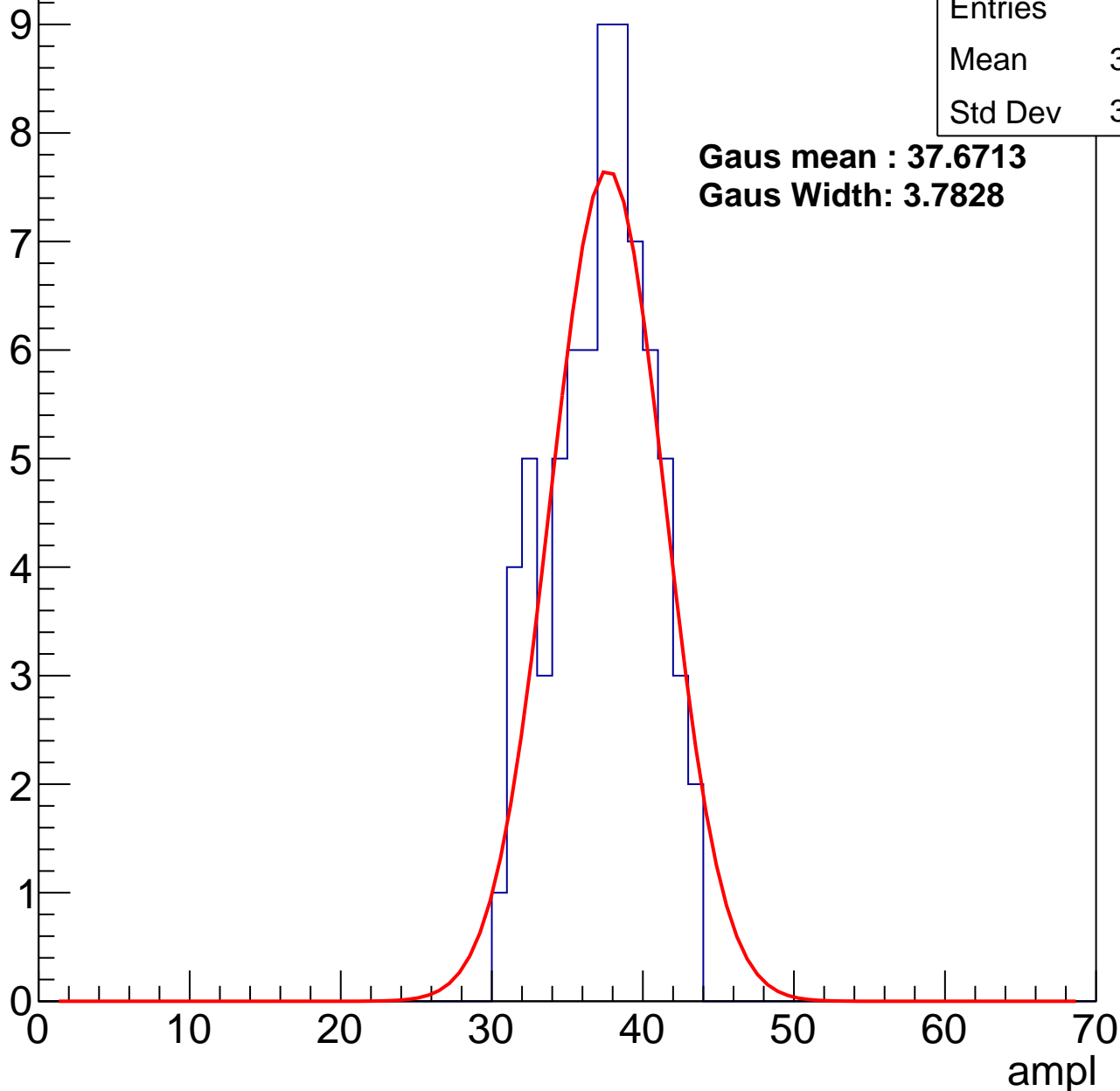
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	36.82
Std Dev	3.264

Gaus mean : 37.6713

Gaus Width: 3.7828



B1L103S, U8-ch17, adc2

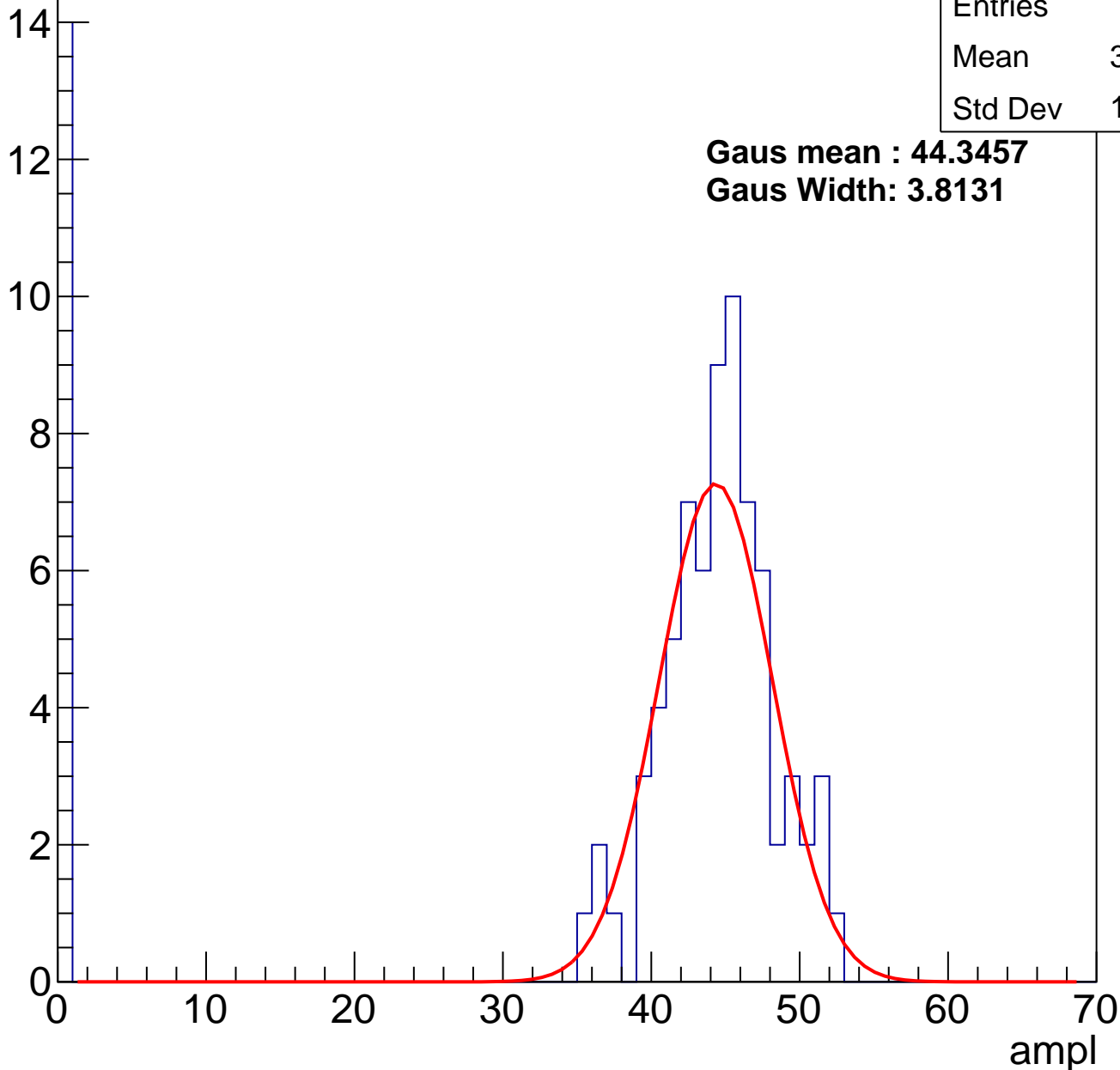
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	36.93
Std Dev	16.62

Gaus mean : 44.3457

Gaus Width: 3.8131

Entry

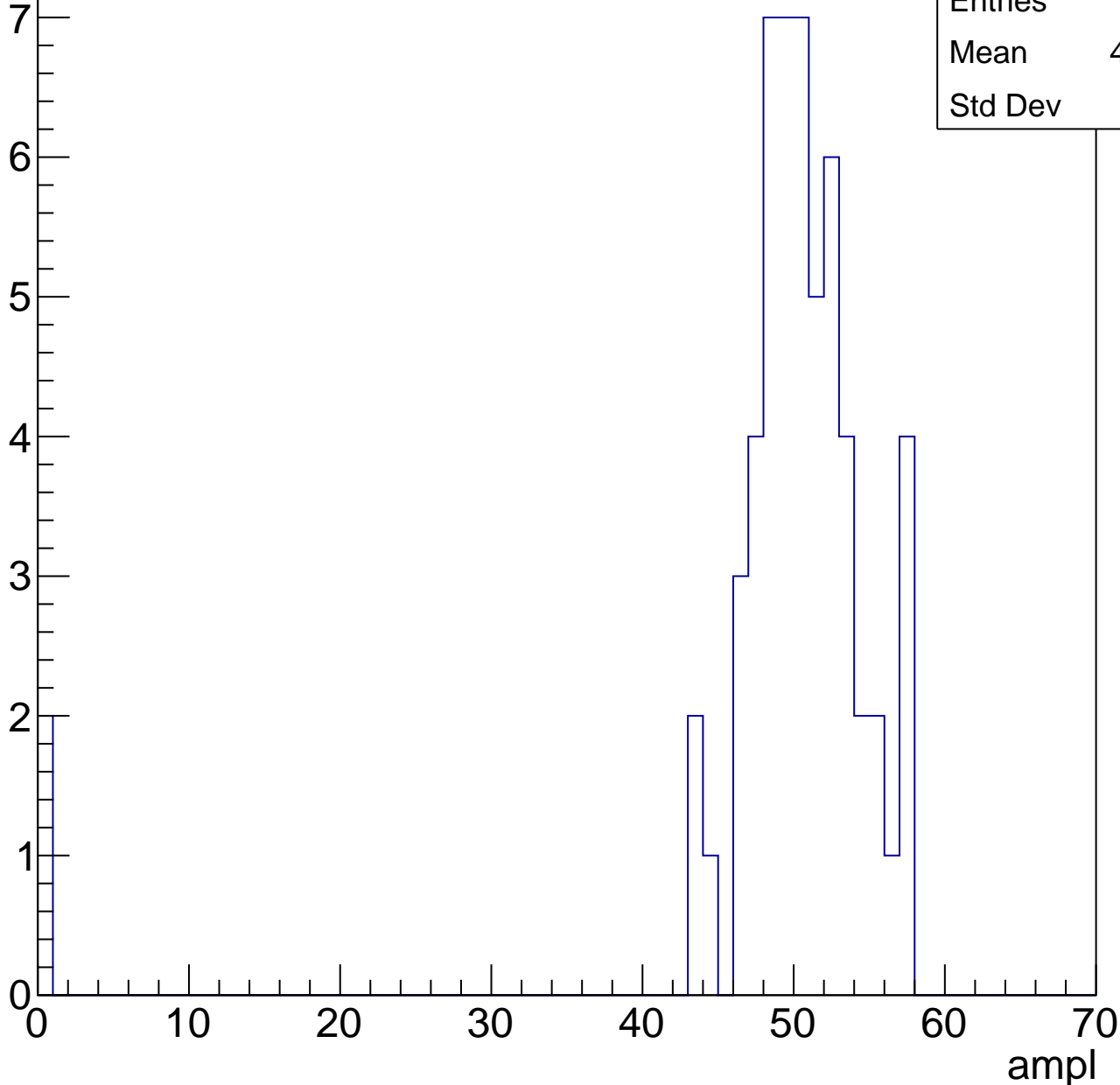


B1L103S, U8-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	48.53
Std Dev	9.83

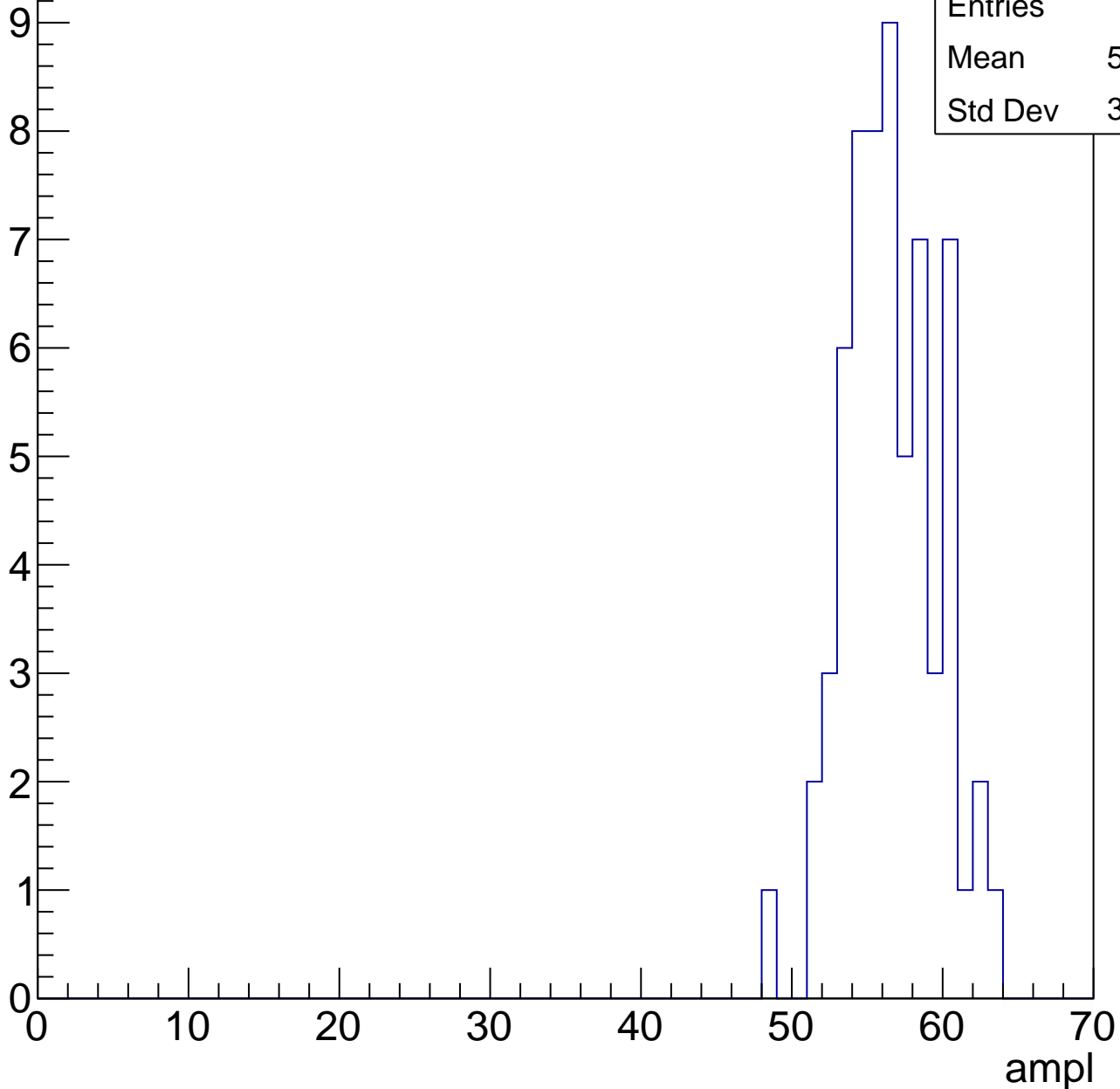


B1L103S, U8-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	56.13
Std Dev	3.016

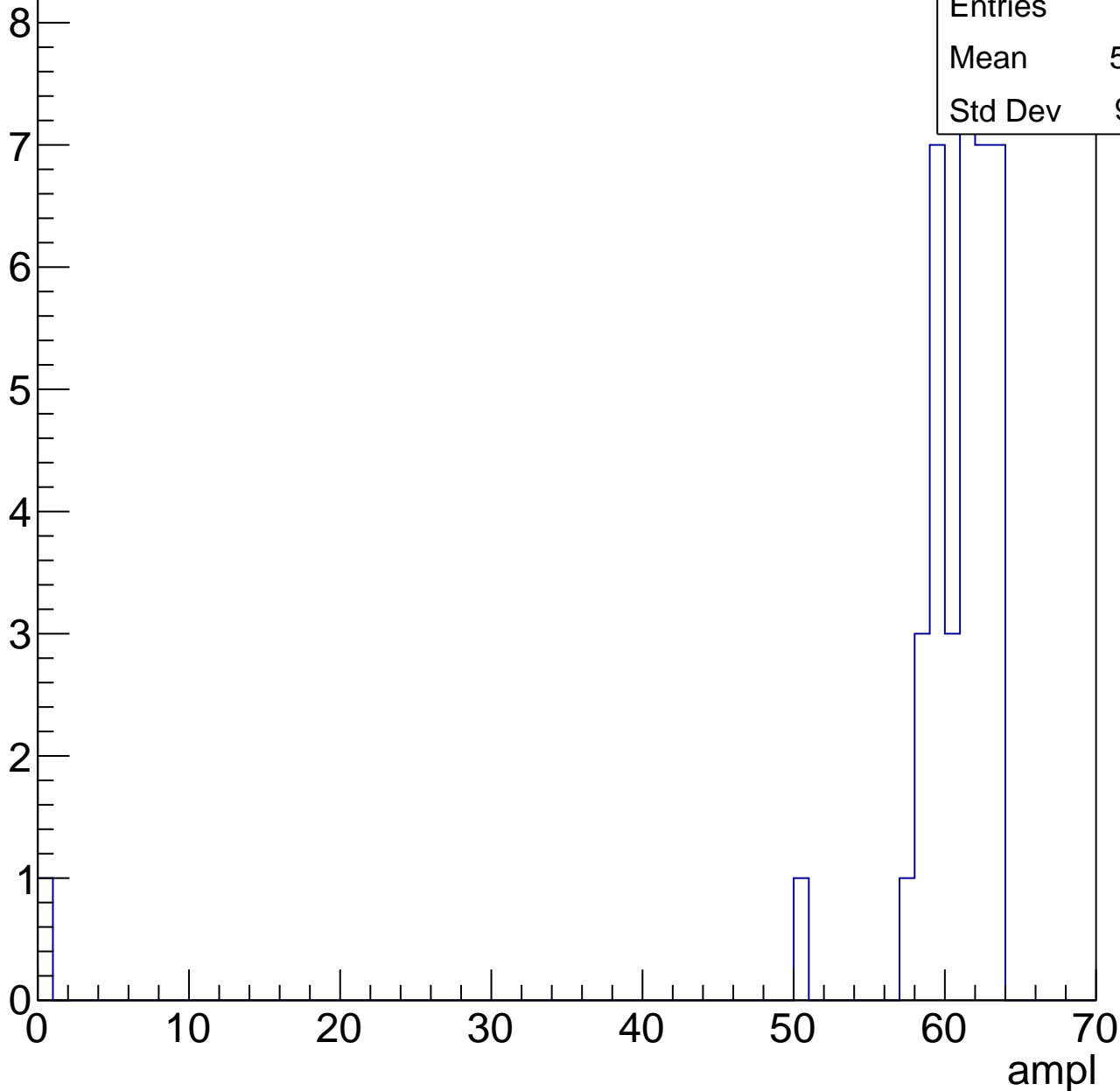


B1L103S, U8-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.87
Std Dev	9.971



B1L103S, U8-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L103S, U8-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

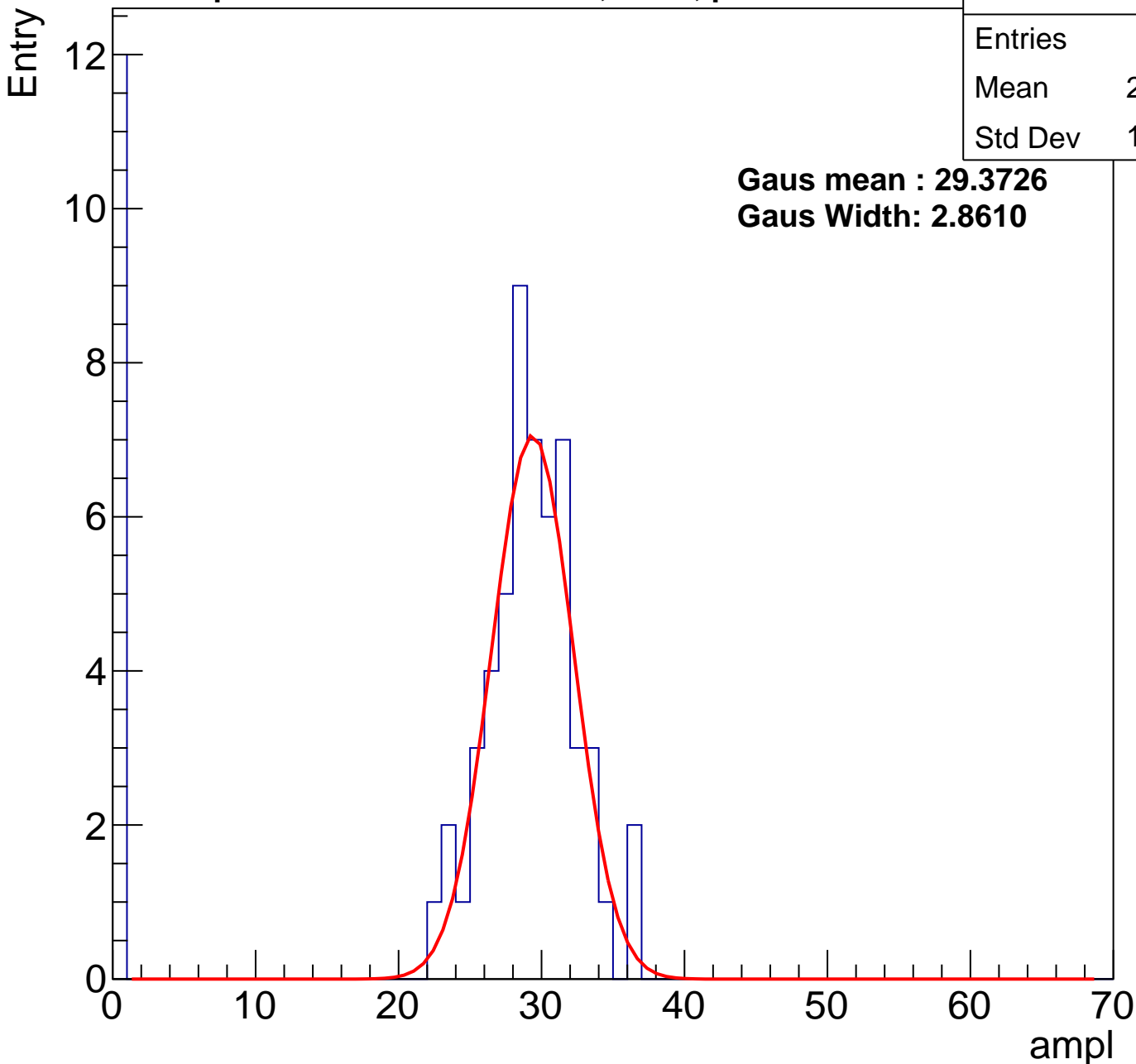
B1L103S, U8-ch18, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	23.62
Std Dev	11.46

Gaus mean : 29.3726

Gaus Width: 2.8610



B1L103S, U8-ch18, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	32.55
Std Dev	9.617

Gaus mean : 35.4632

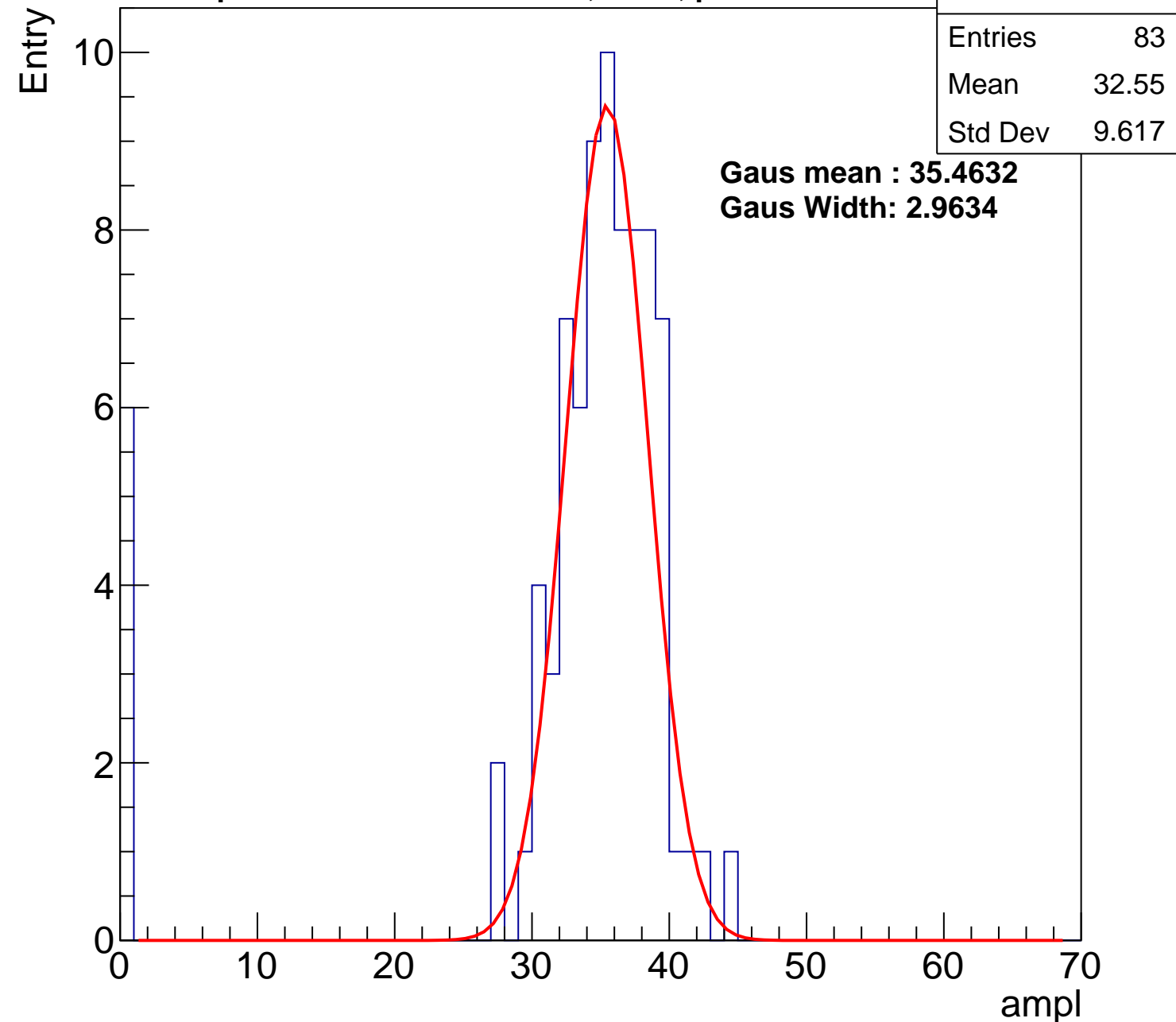
Gaus Width: 2.9634

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch18, adc2

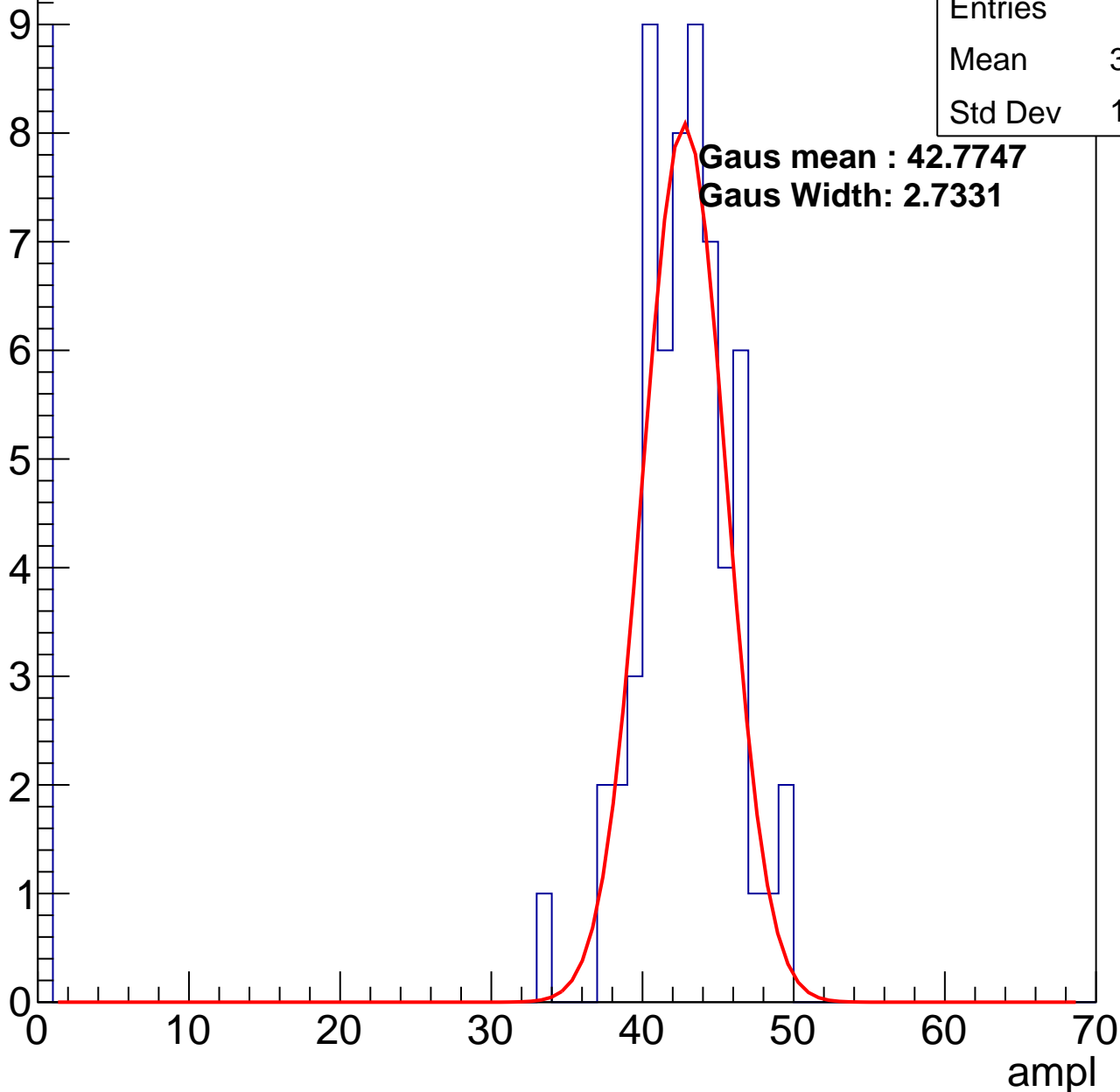
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.94
Std Dev	14.46

Gaus mean : 42.7747

Gaus Width: 2.7331

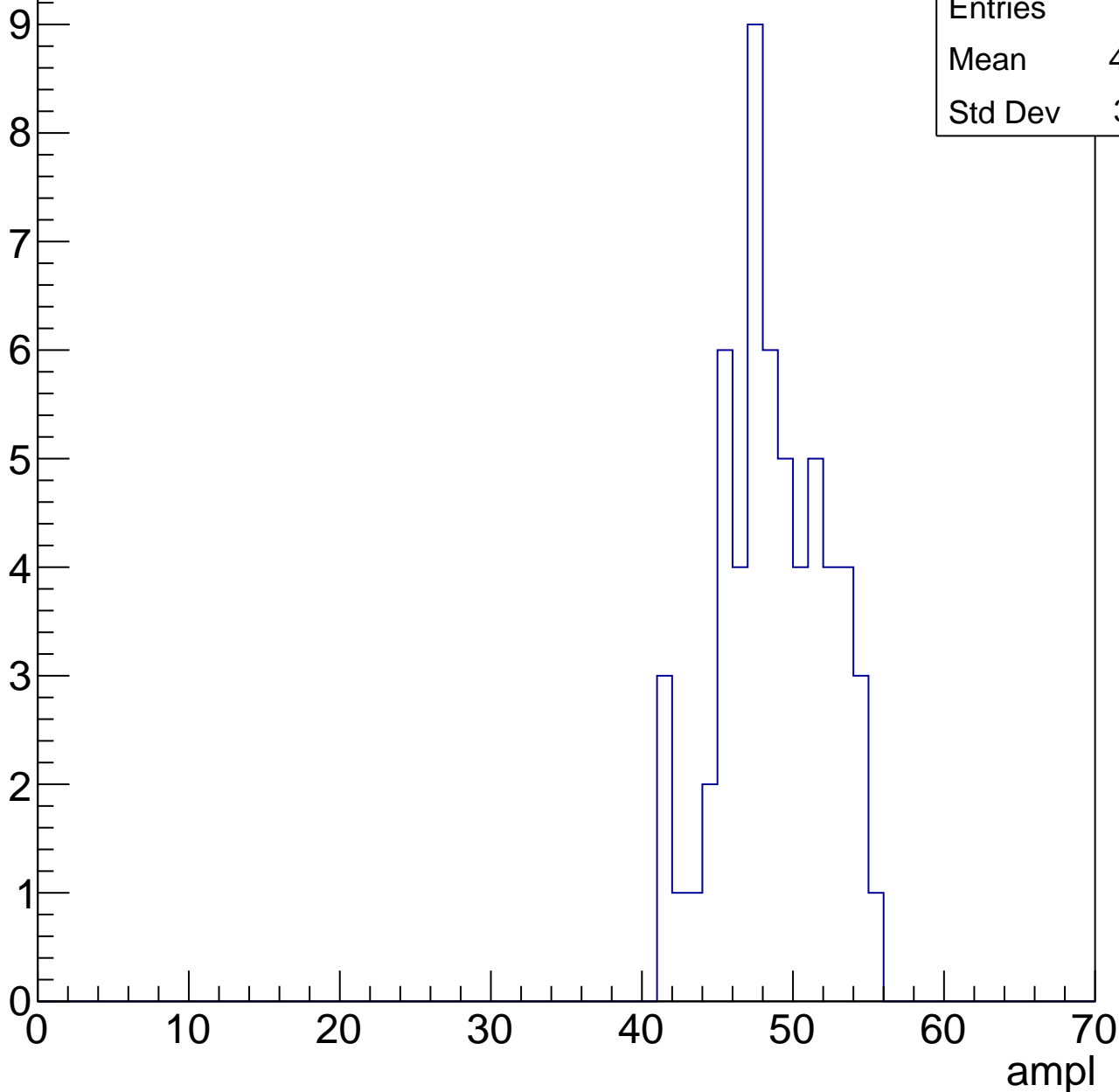


B1L103S, U8-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	48.24
Std Dev	3.481

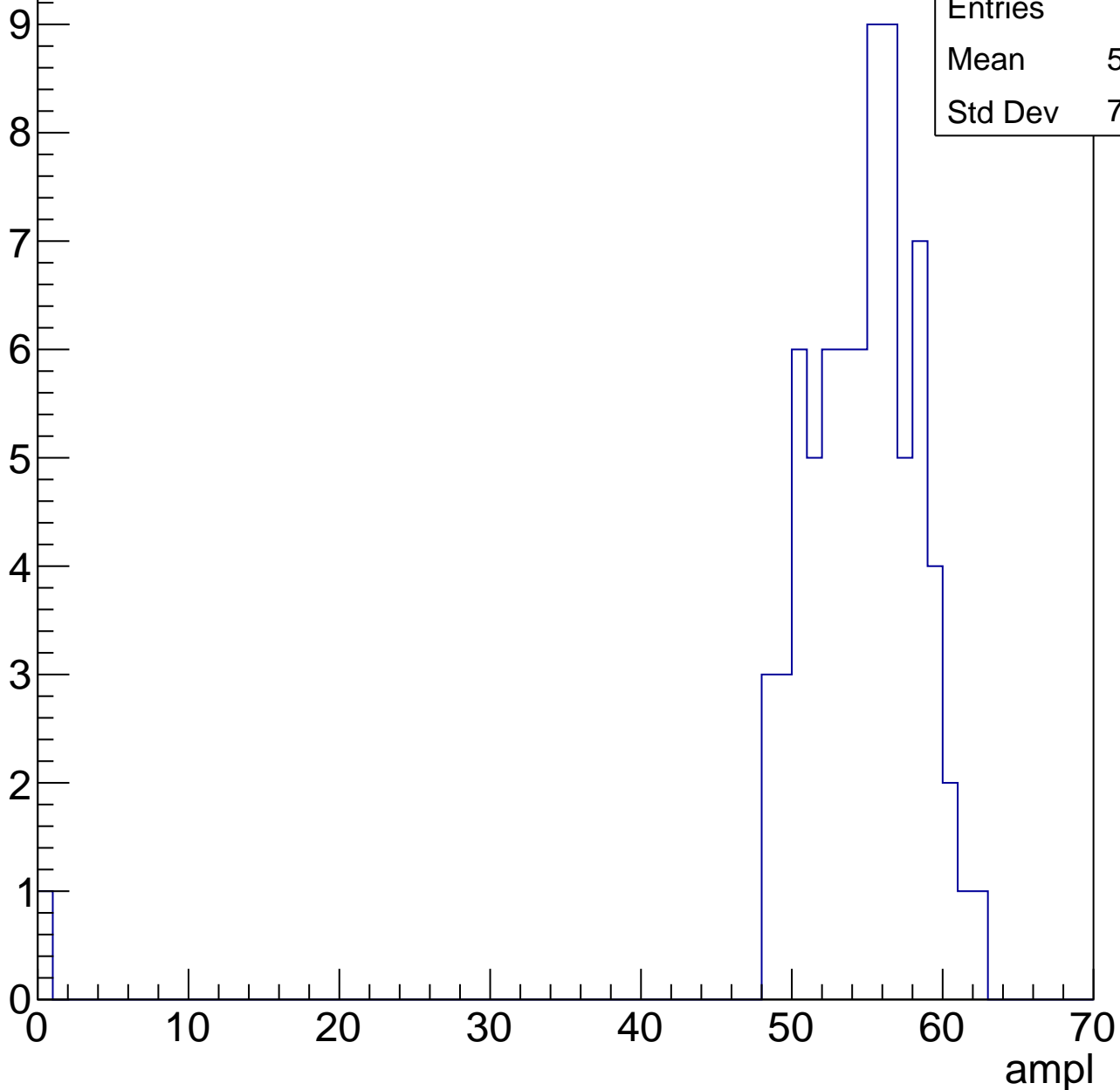


B1L103S, U8-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	53.64
Std Dev	7.116

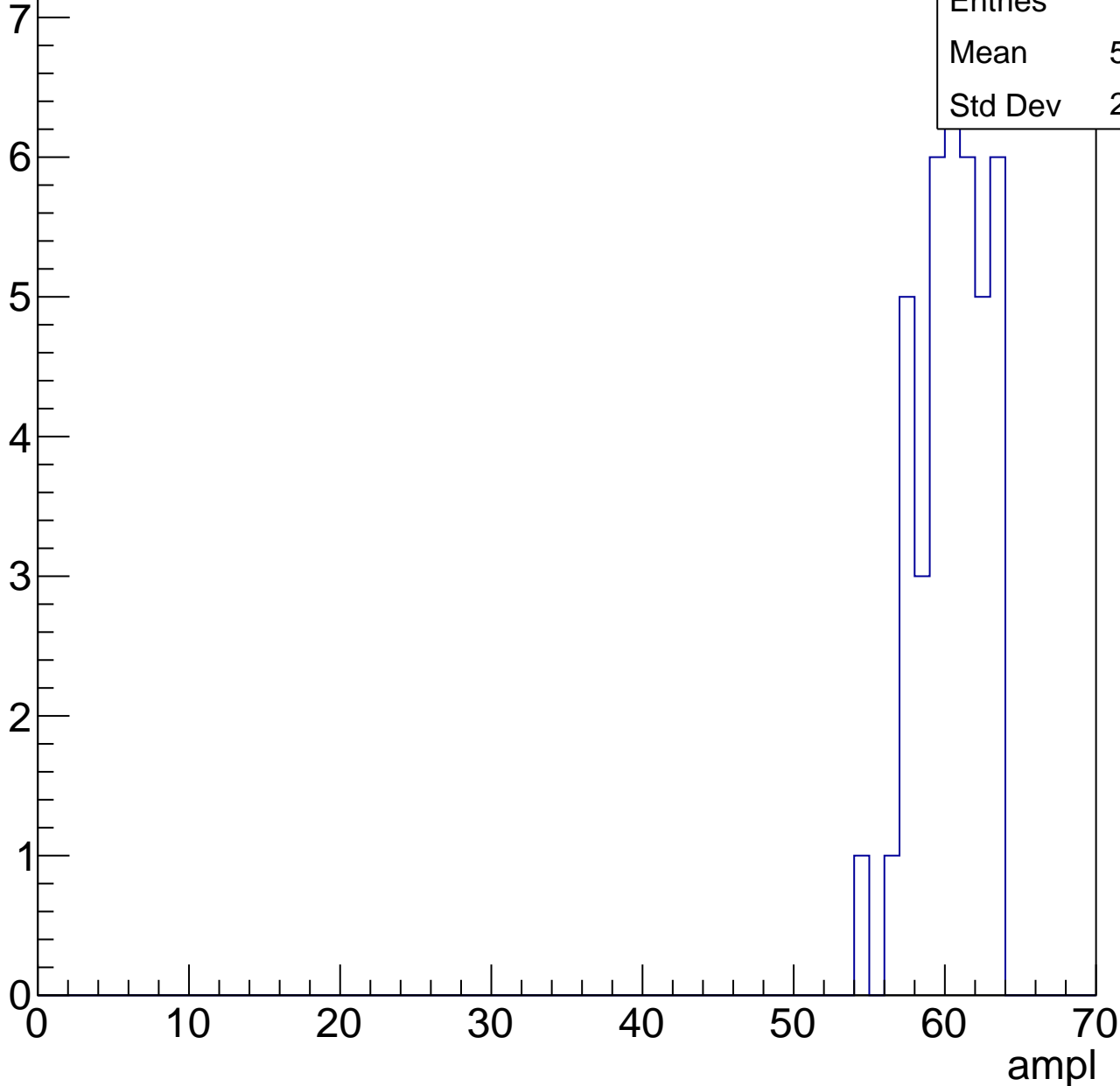


B1L103S, U8-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

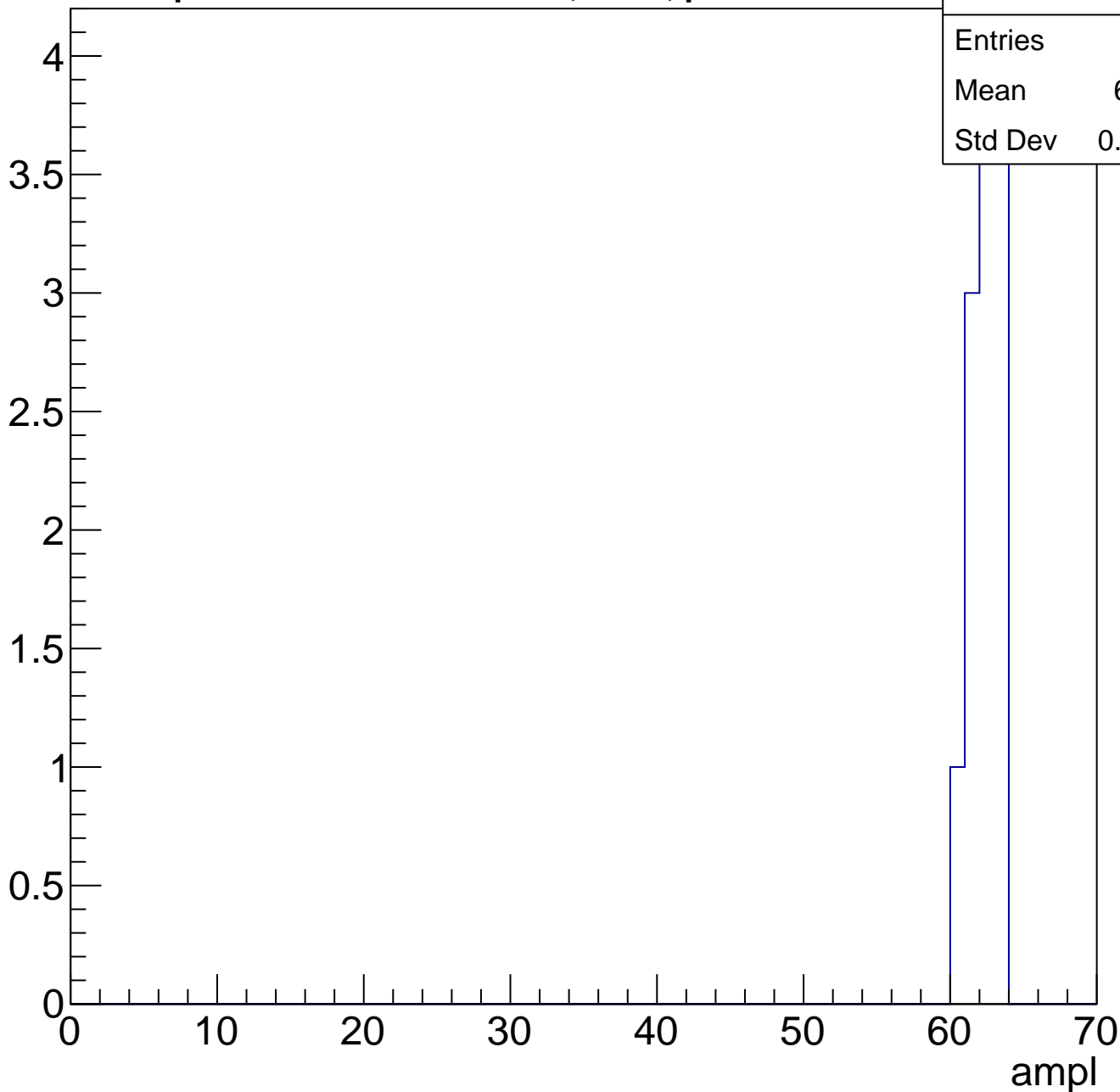
Entries	40
Mean	59.92
Std Dev	2.207



B1L103S, U8-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

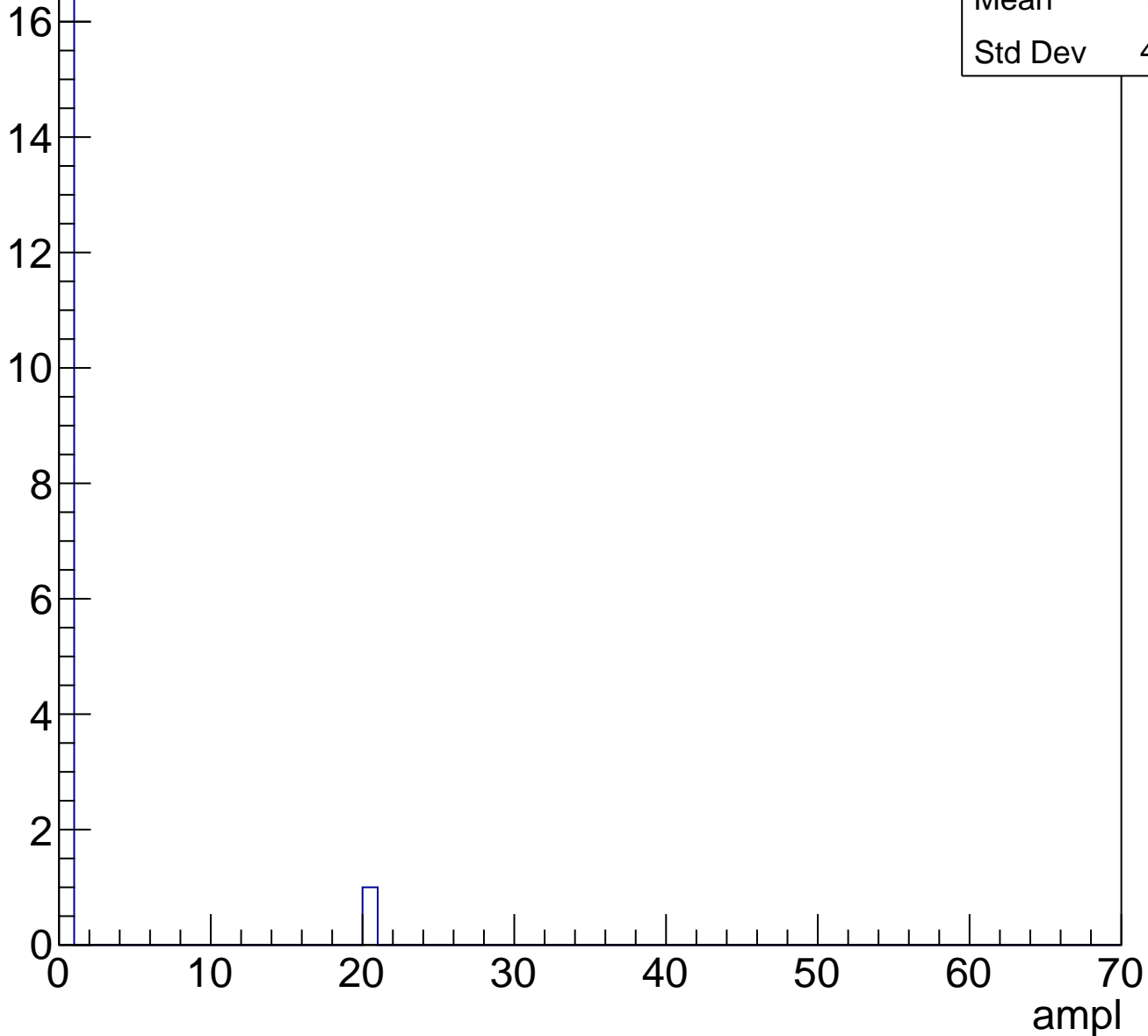
Entry



B1L103S, U8-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	1.111
Std Dev	4.581

B1L103S, U8-ch19, adc0

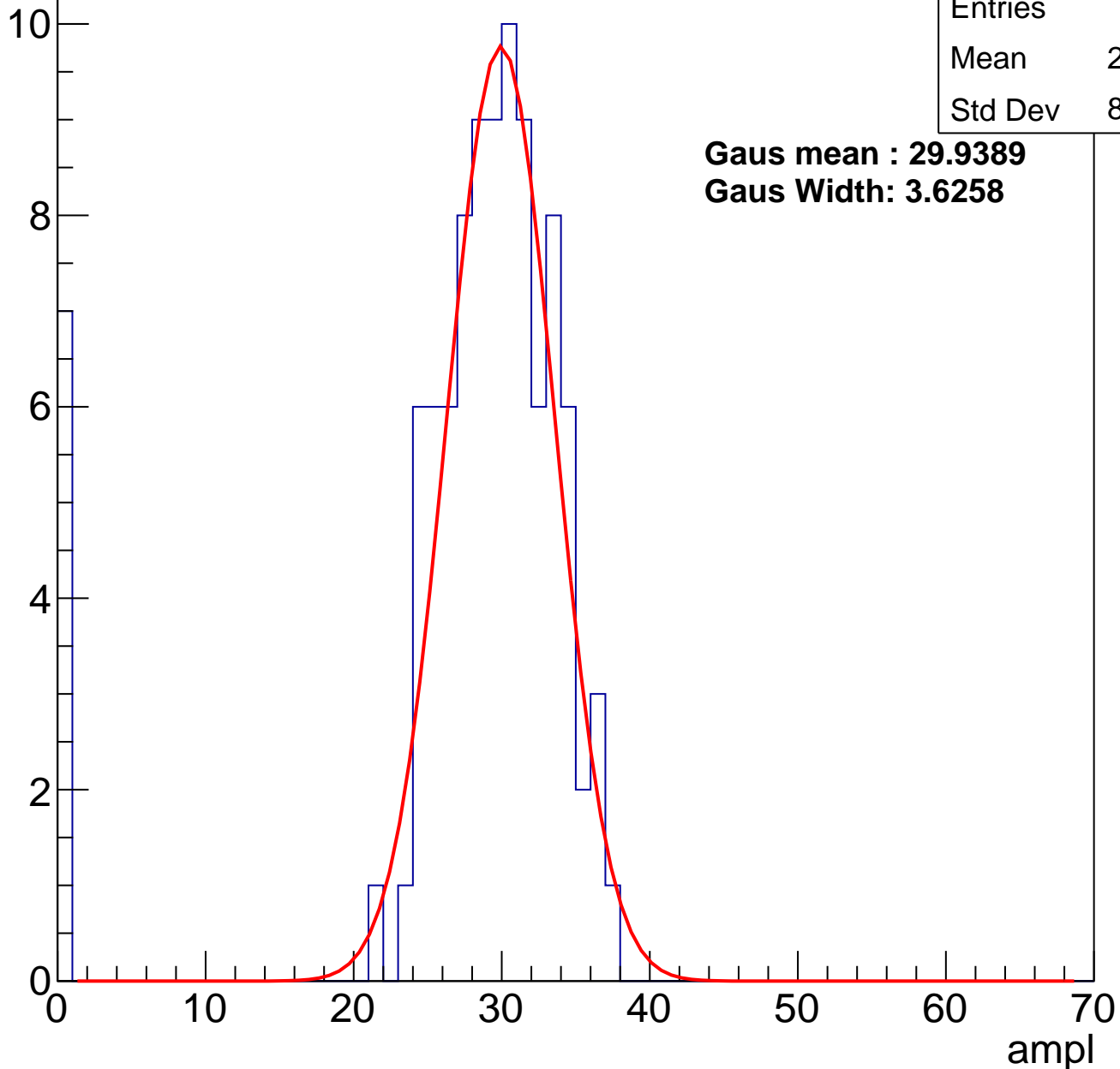
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	27.32
Std Dev	8.275

Gaus mean : 29.9389

Gaus Width: 3.6258

Entry



B1L103S, U8-ch19, adc1

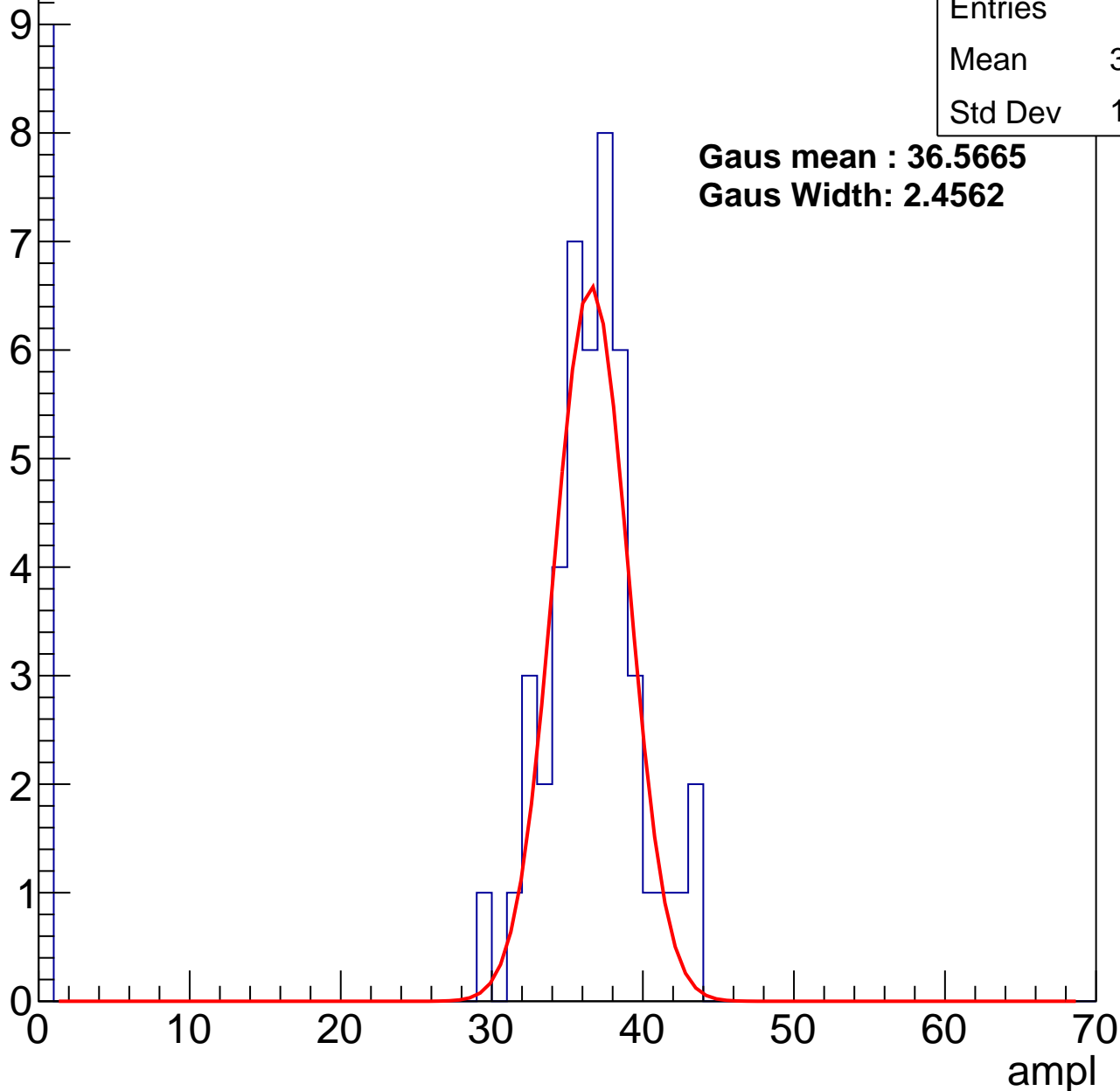
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	30.35
Std Dev	13.69

Gaus mean : 36.5665

Gaus Width: 2.4562



B1L103S, U8-ch19, adc2

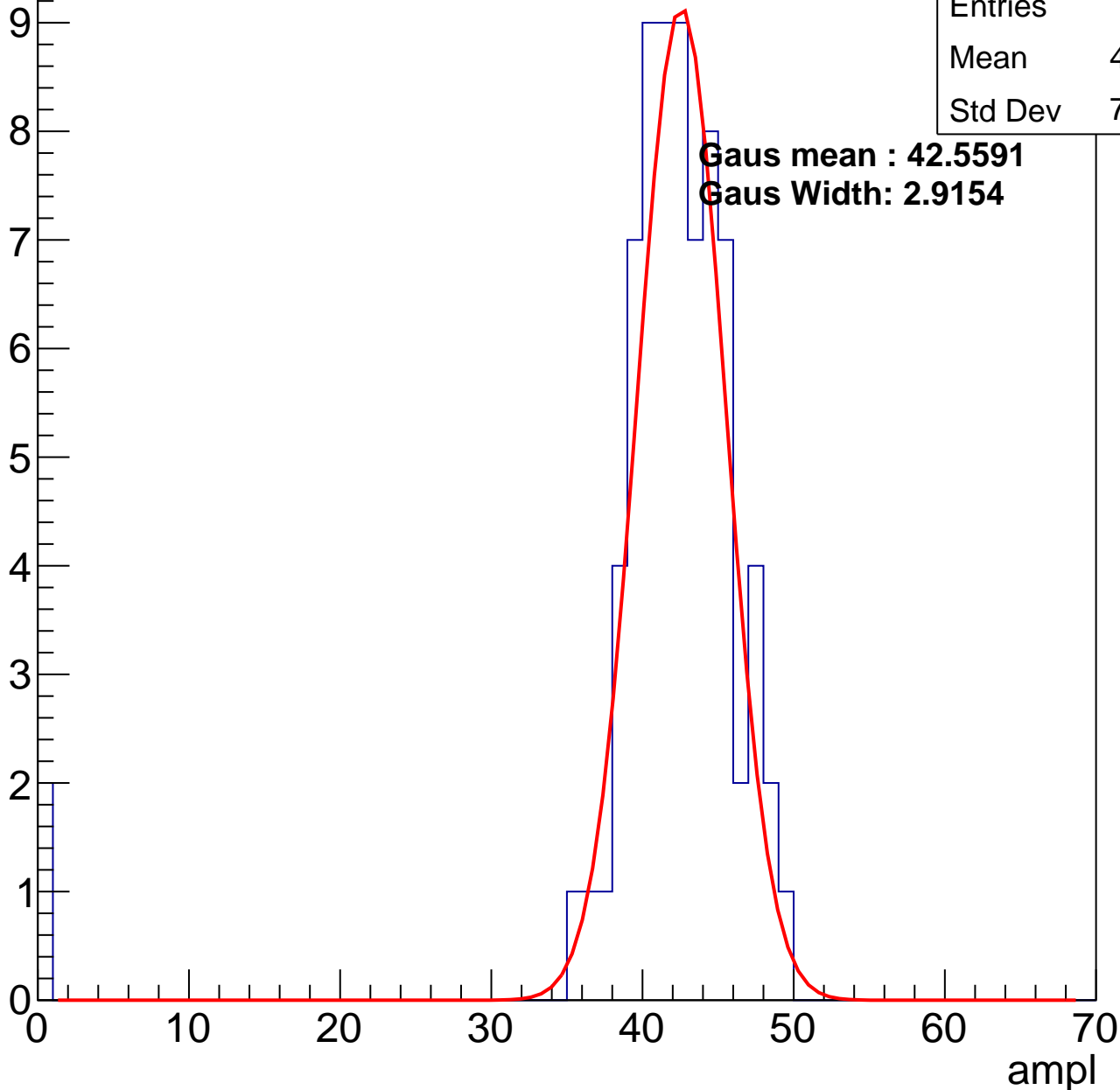
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	40.99
Std Dev	7.433

Gaus mean : 42.5591

Gaus Width: 2.9154

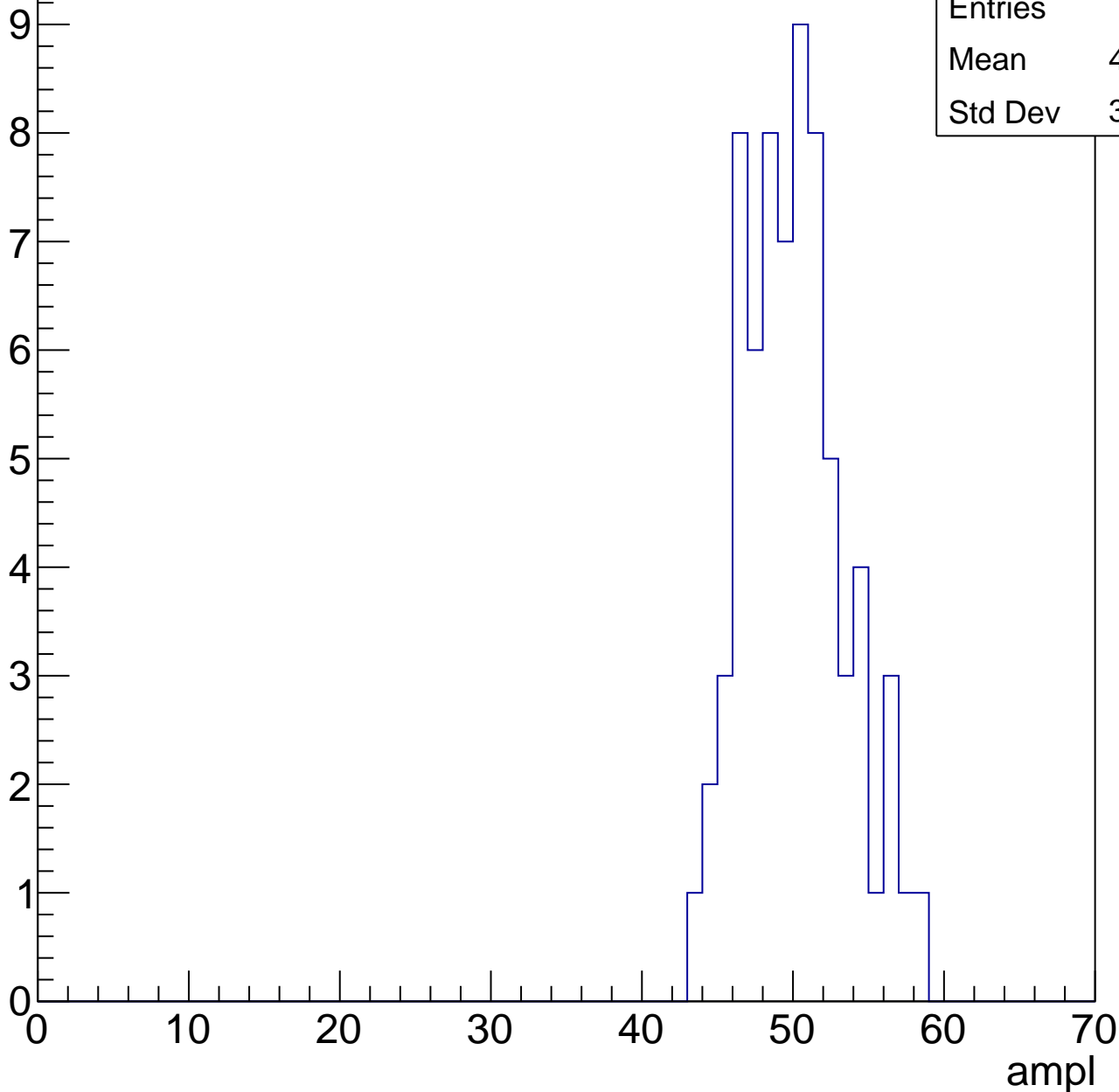


B1L103S, U8-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

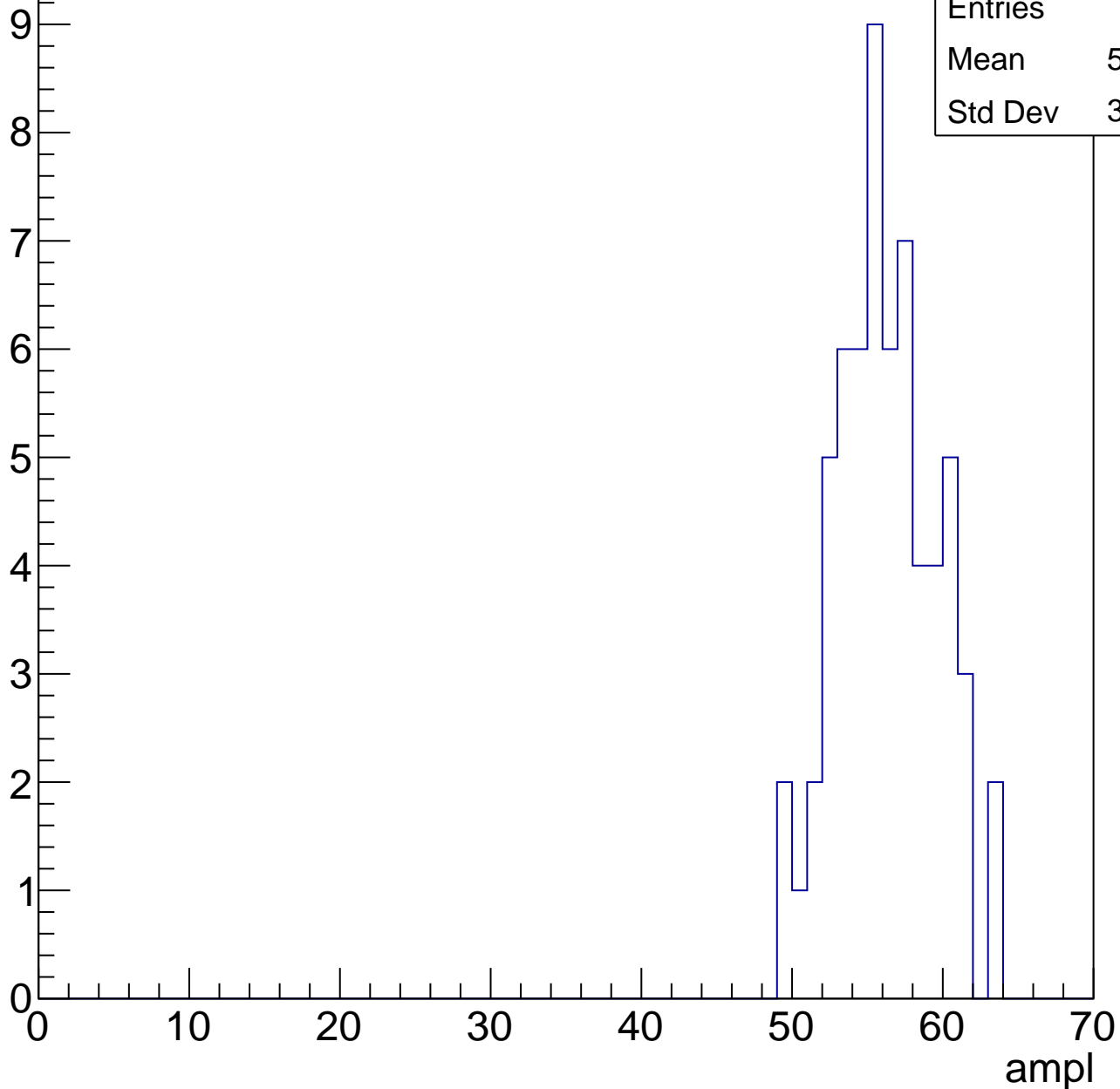
Entries	70
Mean	49.63
Std Dev	3.322



B1L103S, U8-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

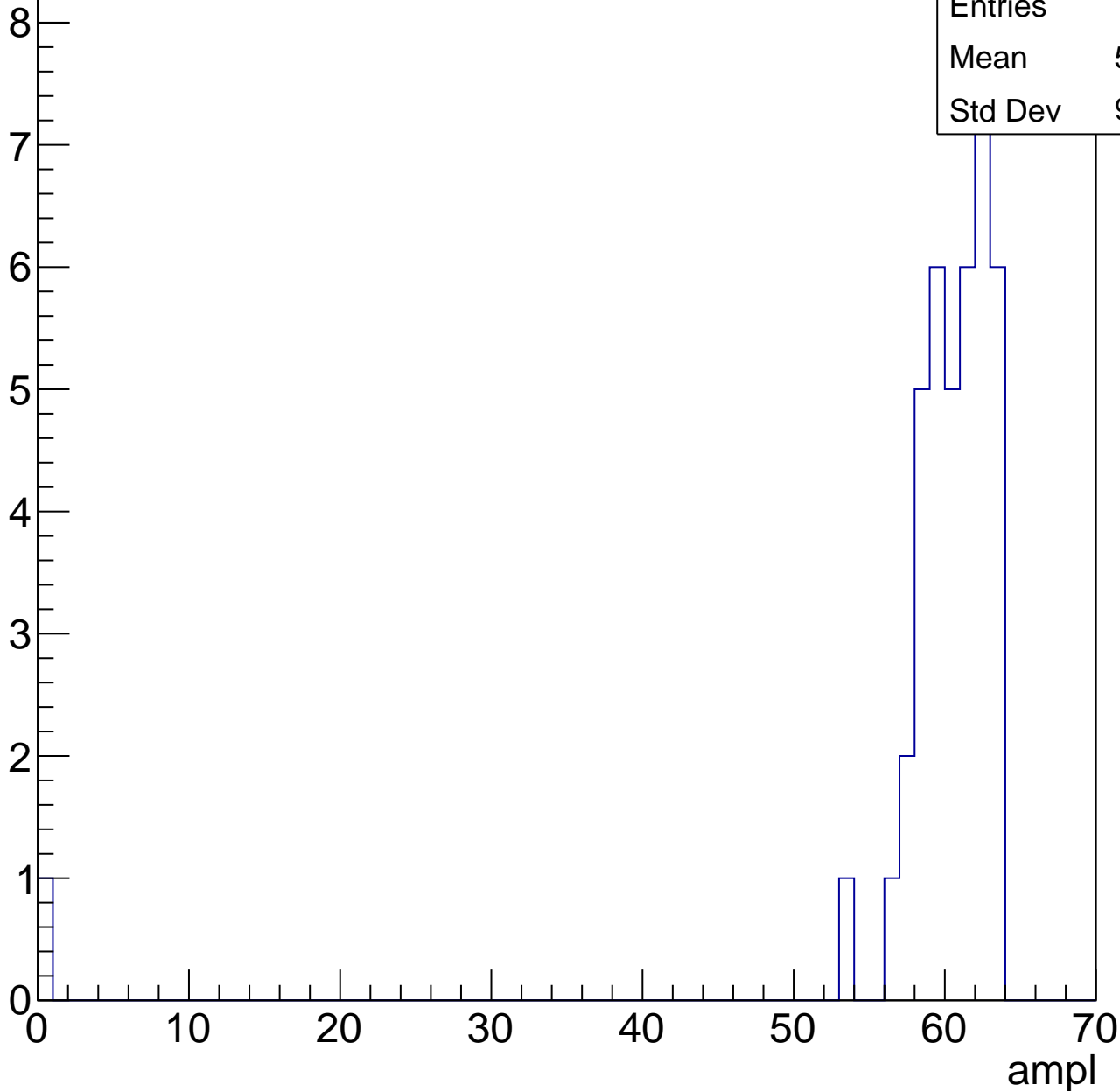


B1L103S, U8-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	58.71
Std Dev	9.541



B1L103S, U8-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

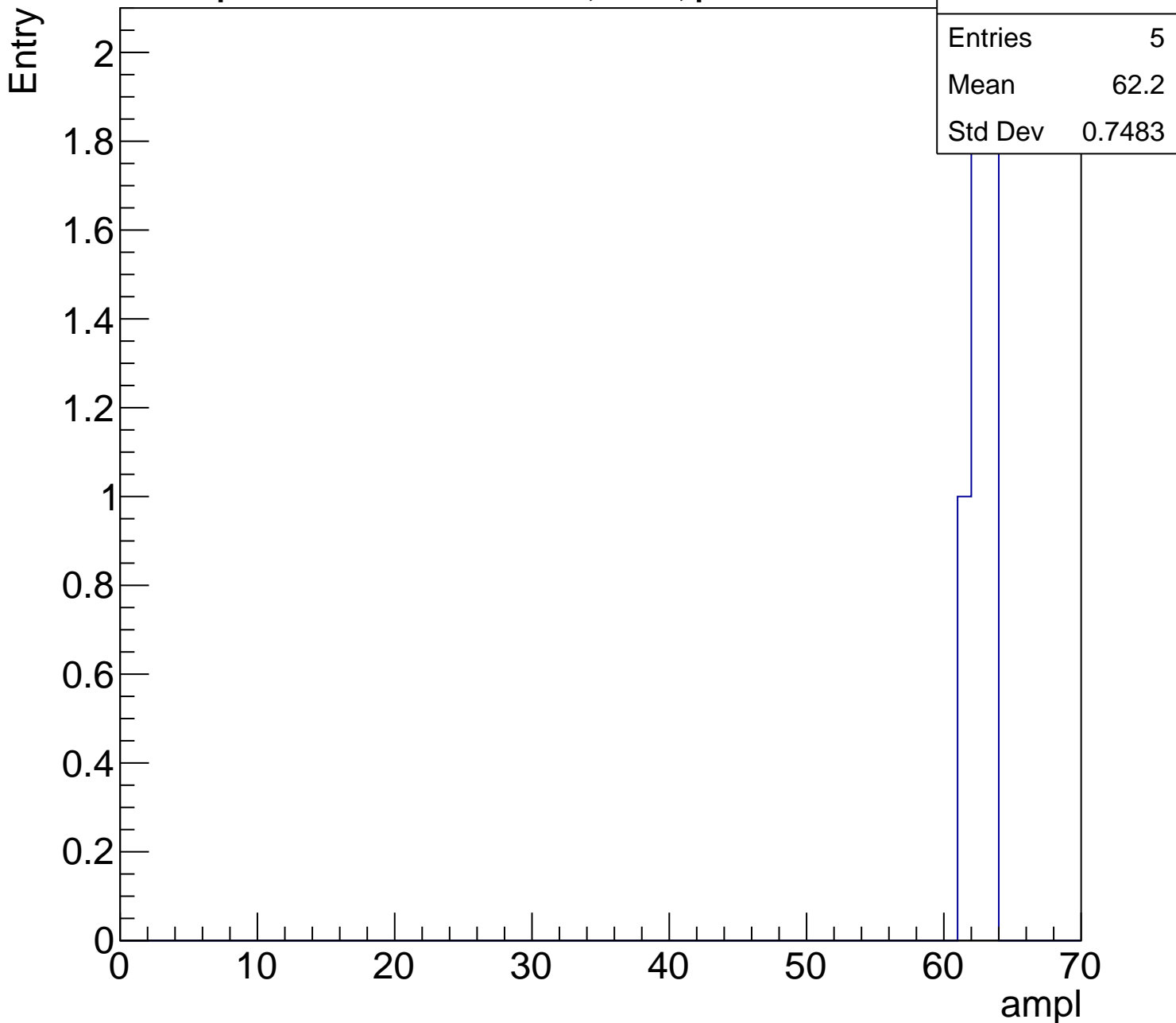
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

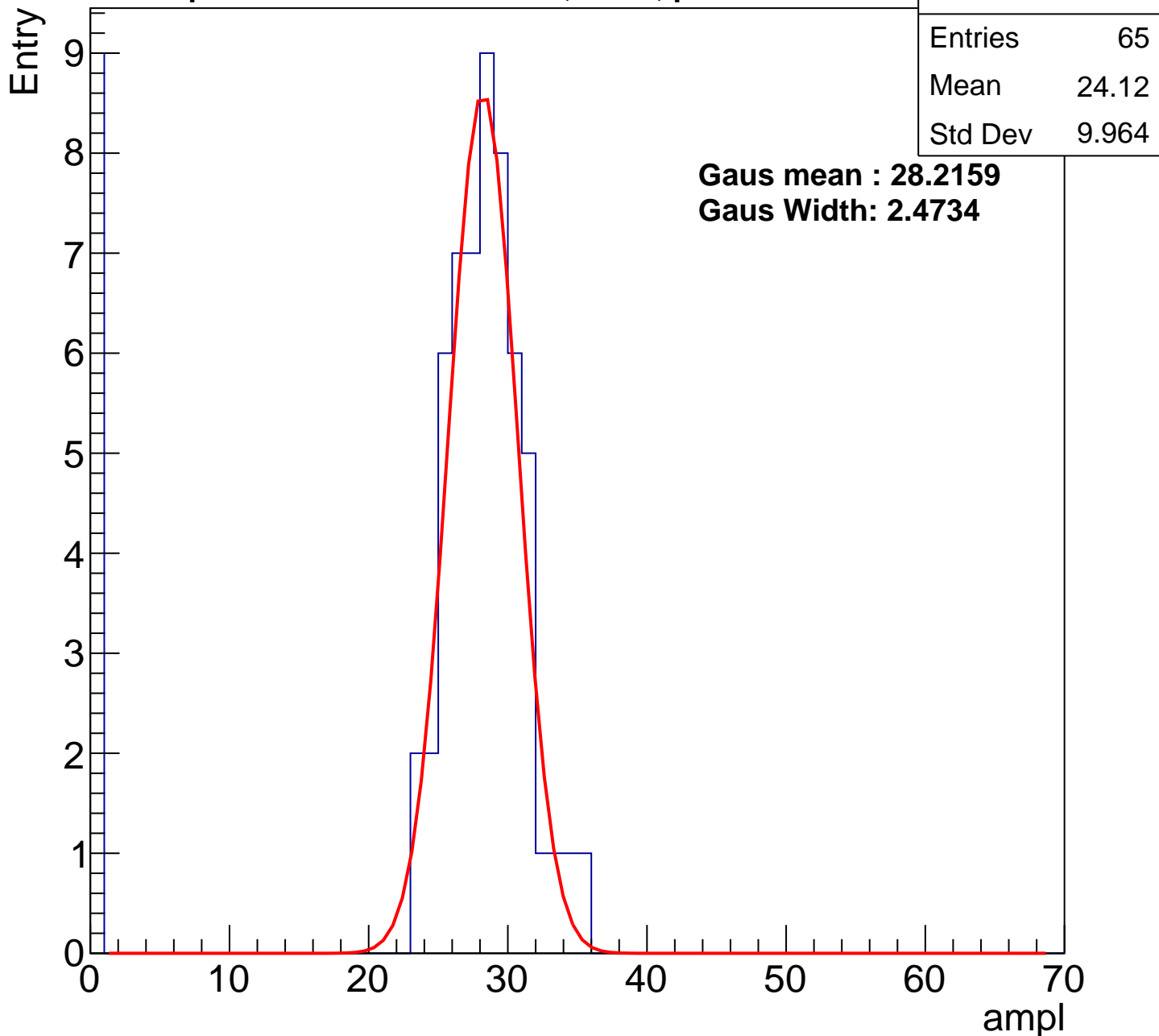
18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch20, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch20, adc1

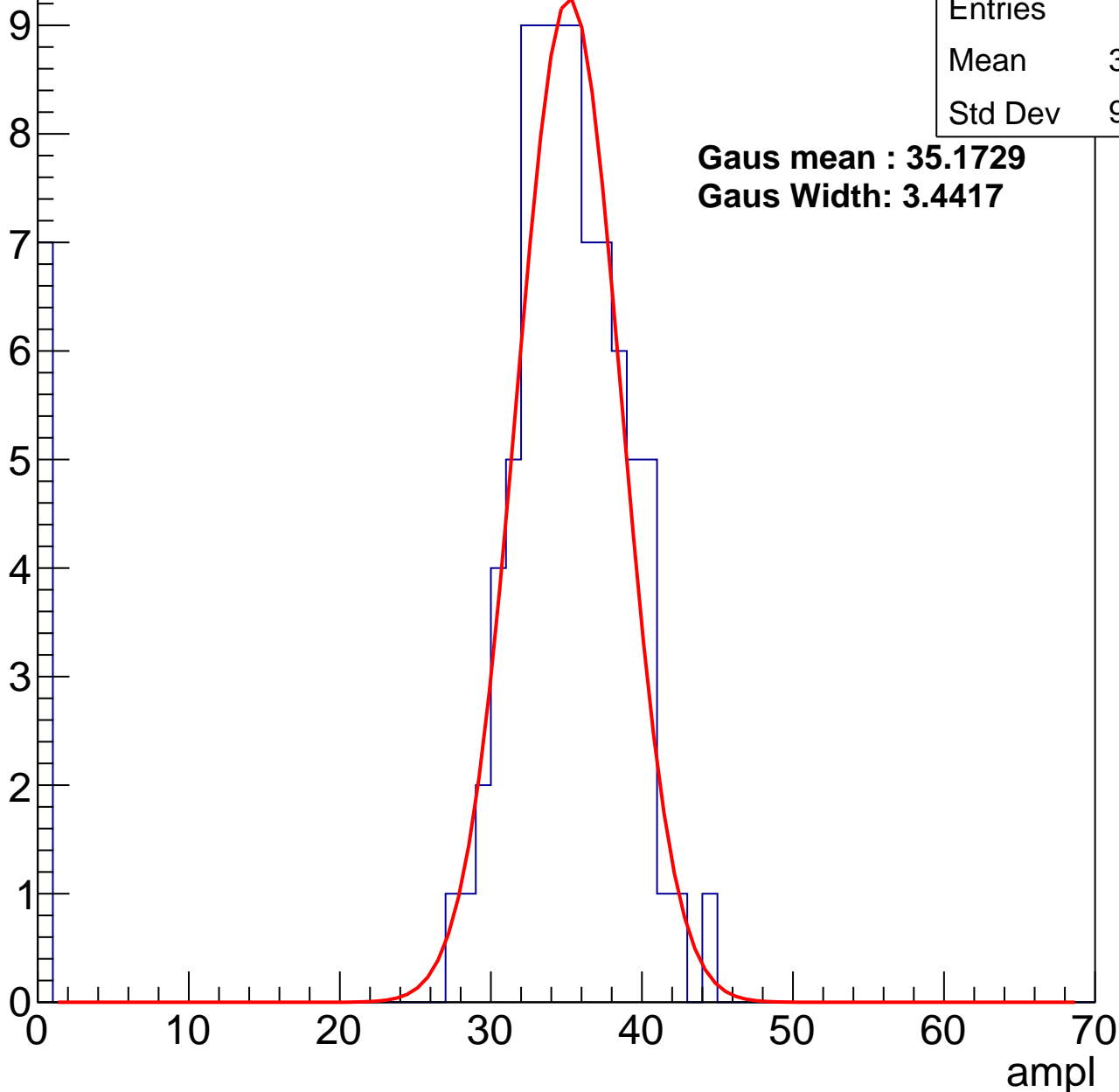
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	32.08
Std Dev	9.925

Gaus mean : 35.1729

Gaus Width: 3.4417



B1L103S, U8-ch20, adc2

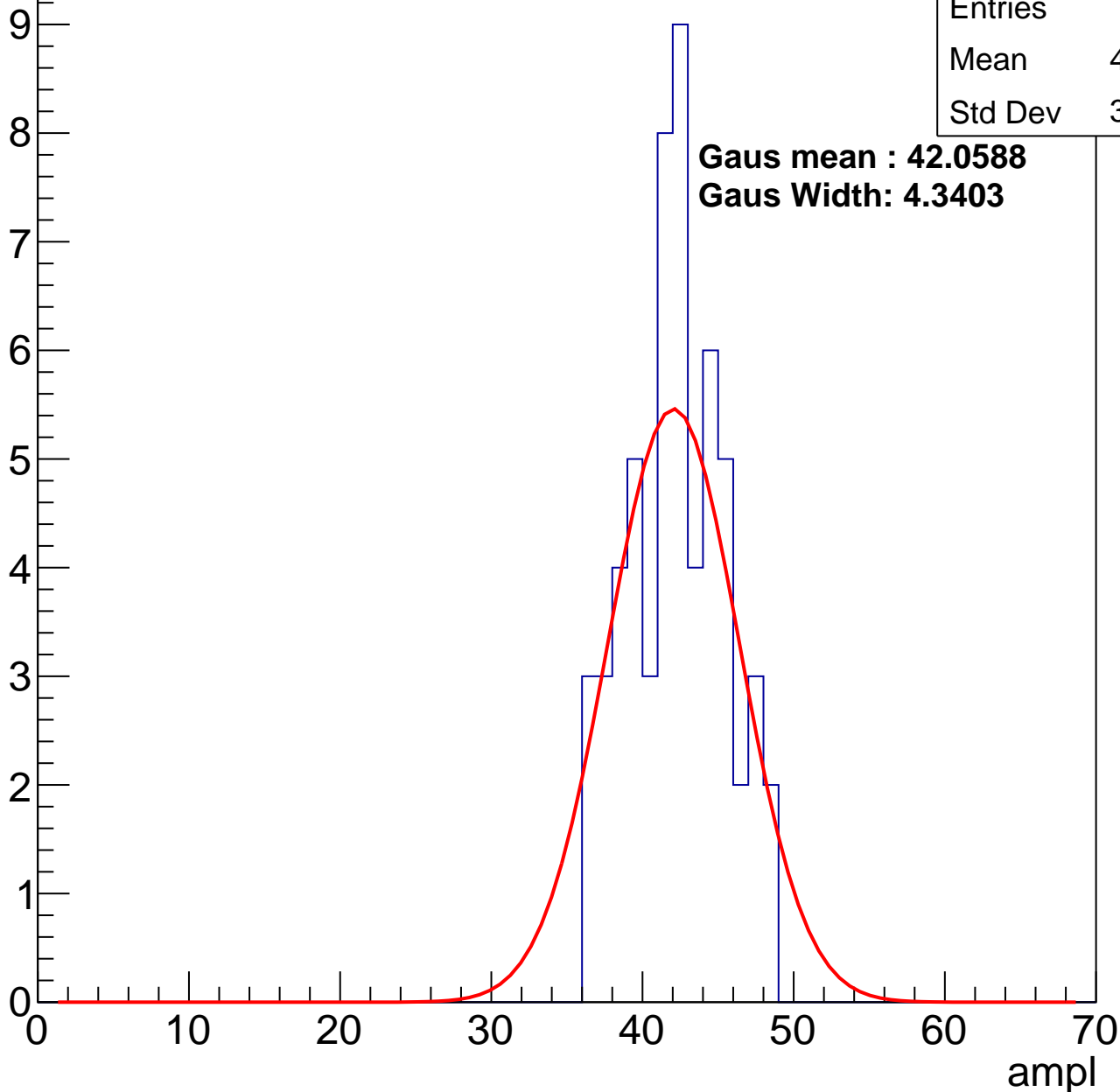
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	41.79
Std Dev	3.139

Gaus mean : 42.0588

Gaus Width: 4.3403

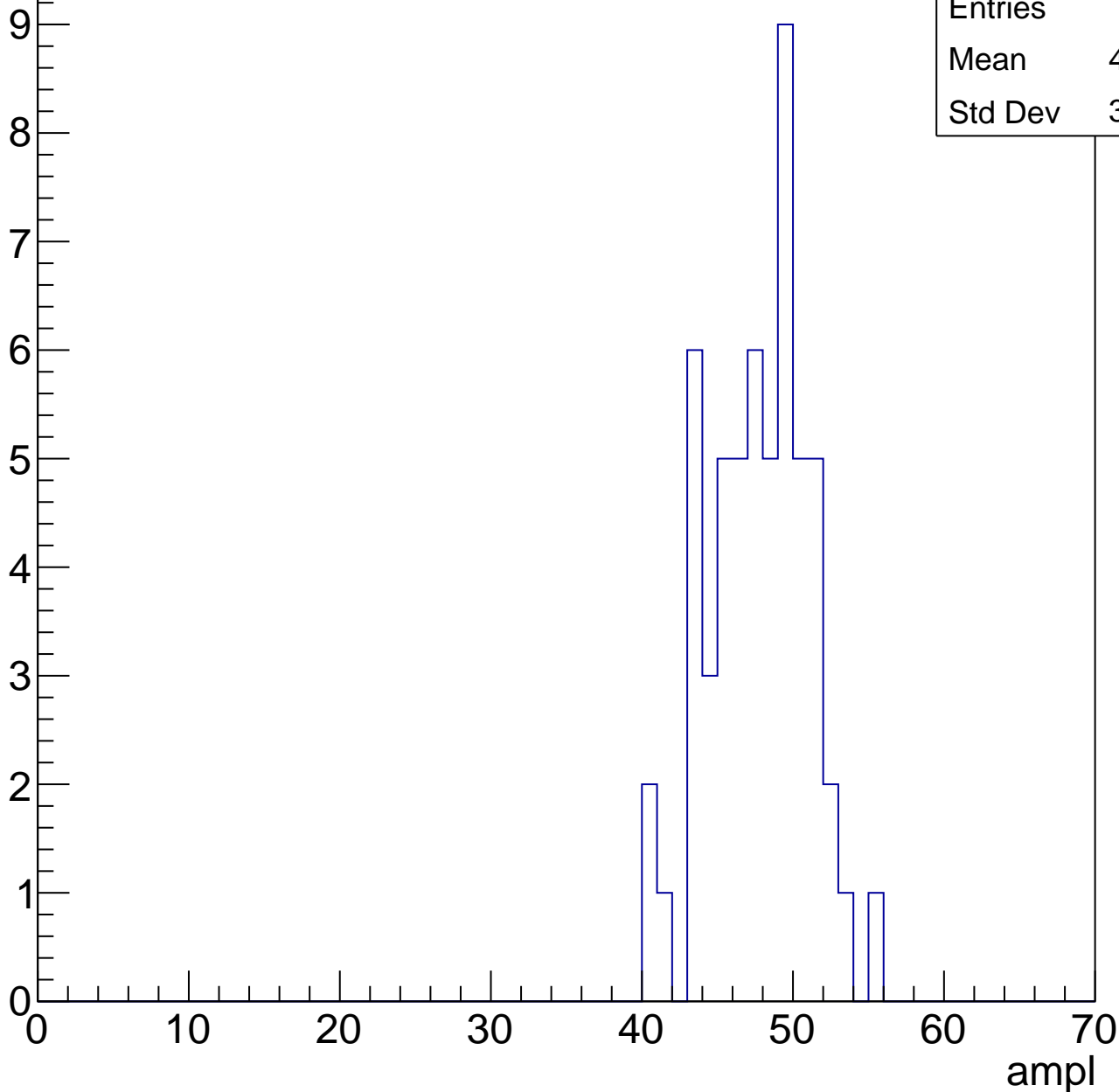


B1L103S, U8-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	47.25
Std Dev	3.258

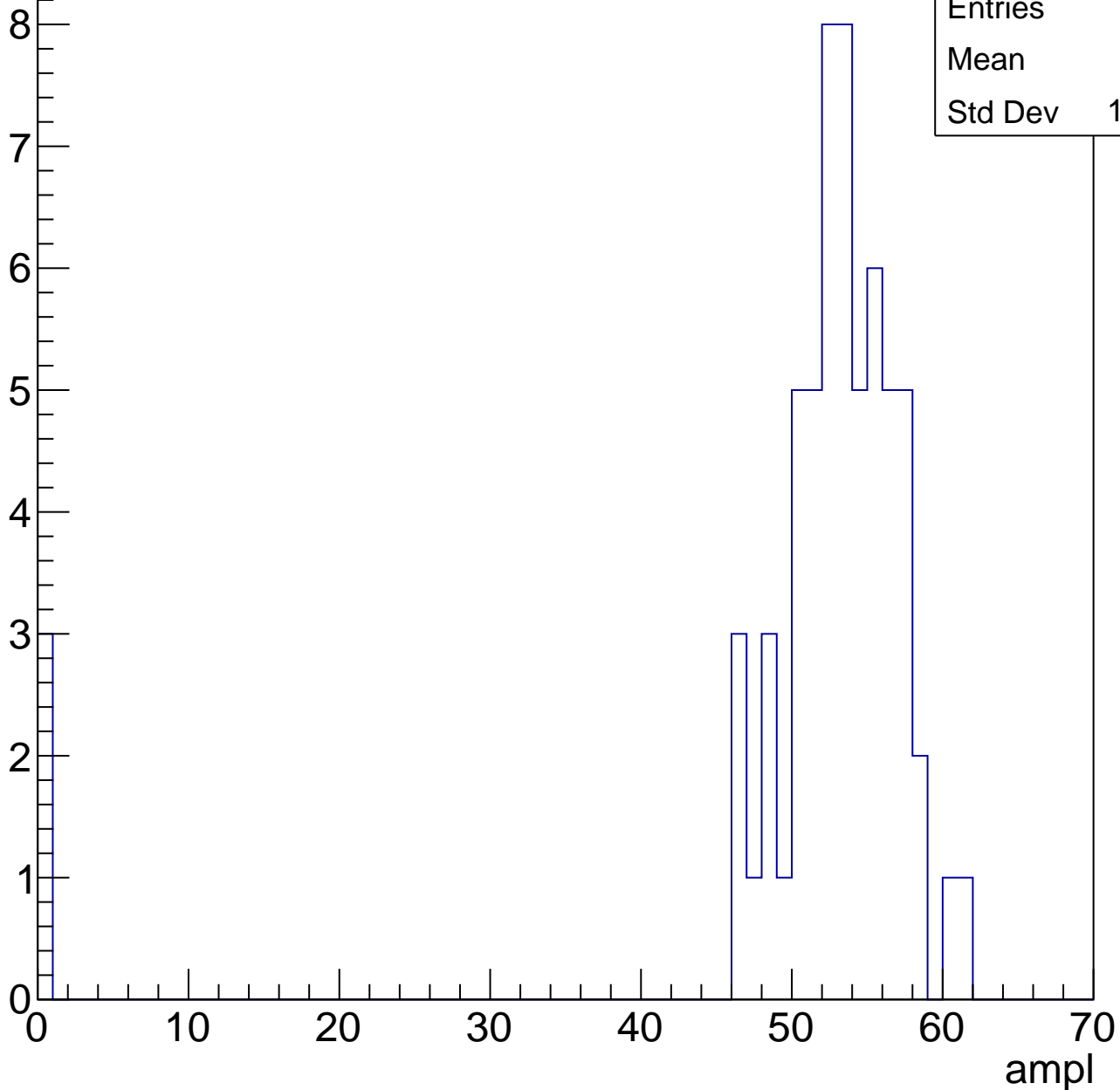


B1L103S, U8-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	50.4
Std Dev	11.83

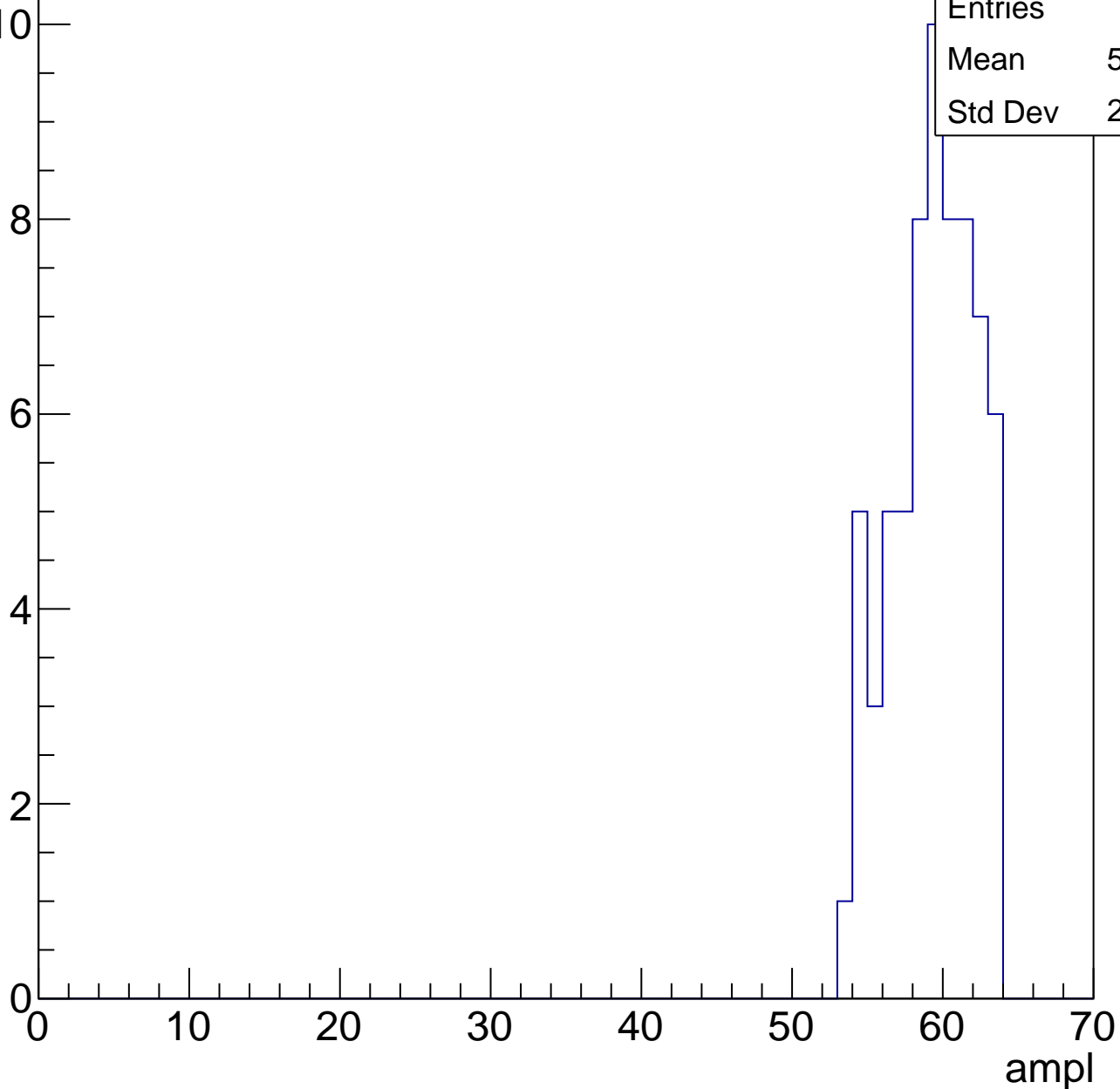


B1L103S, U8-ch20, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

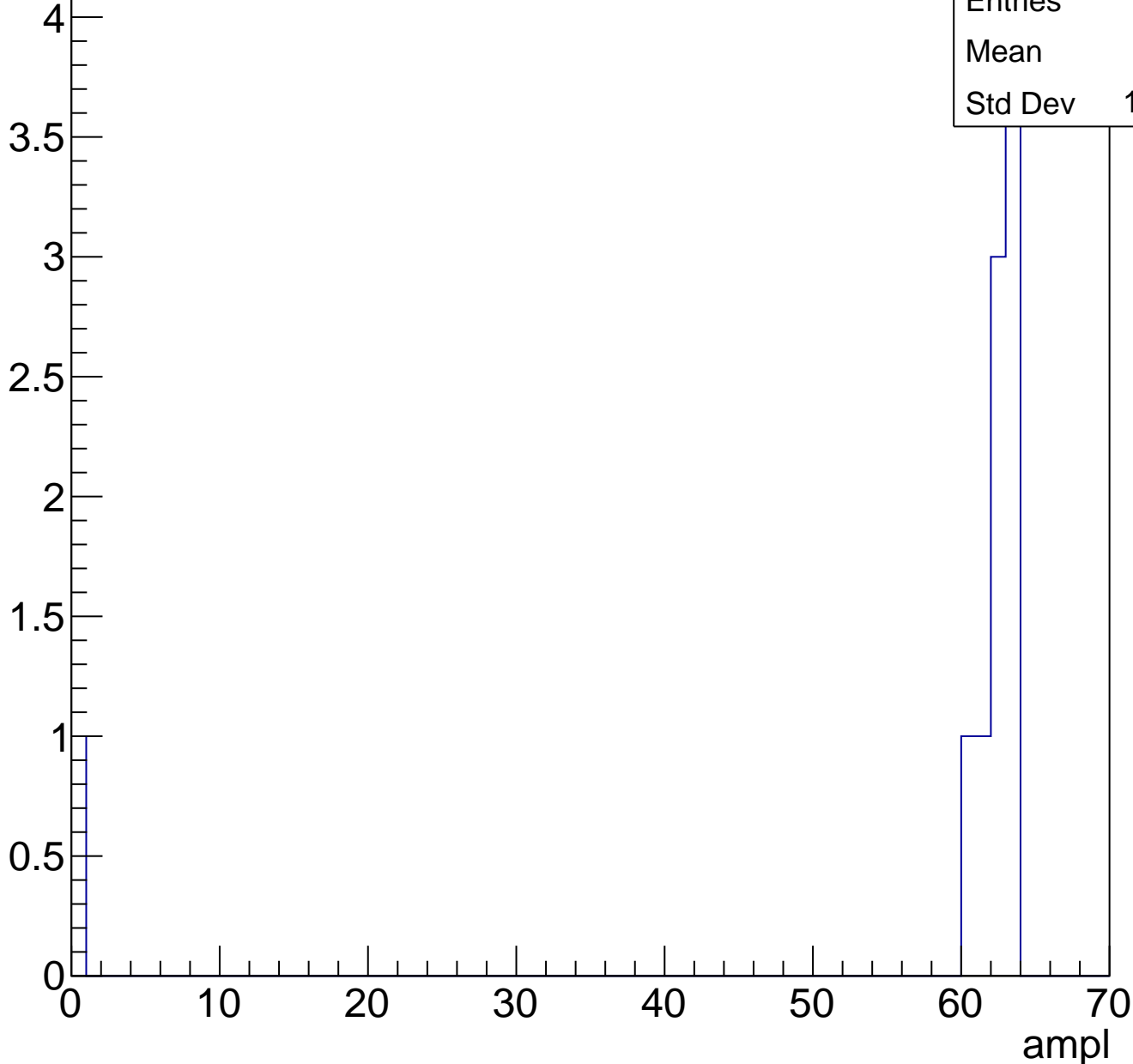
Entries	66
Mean	58.89
Std Dev	2.698



B1L103S, U8-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

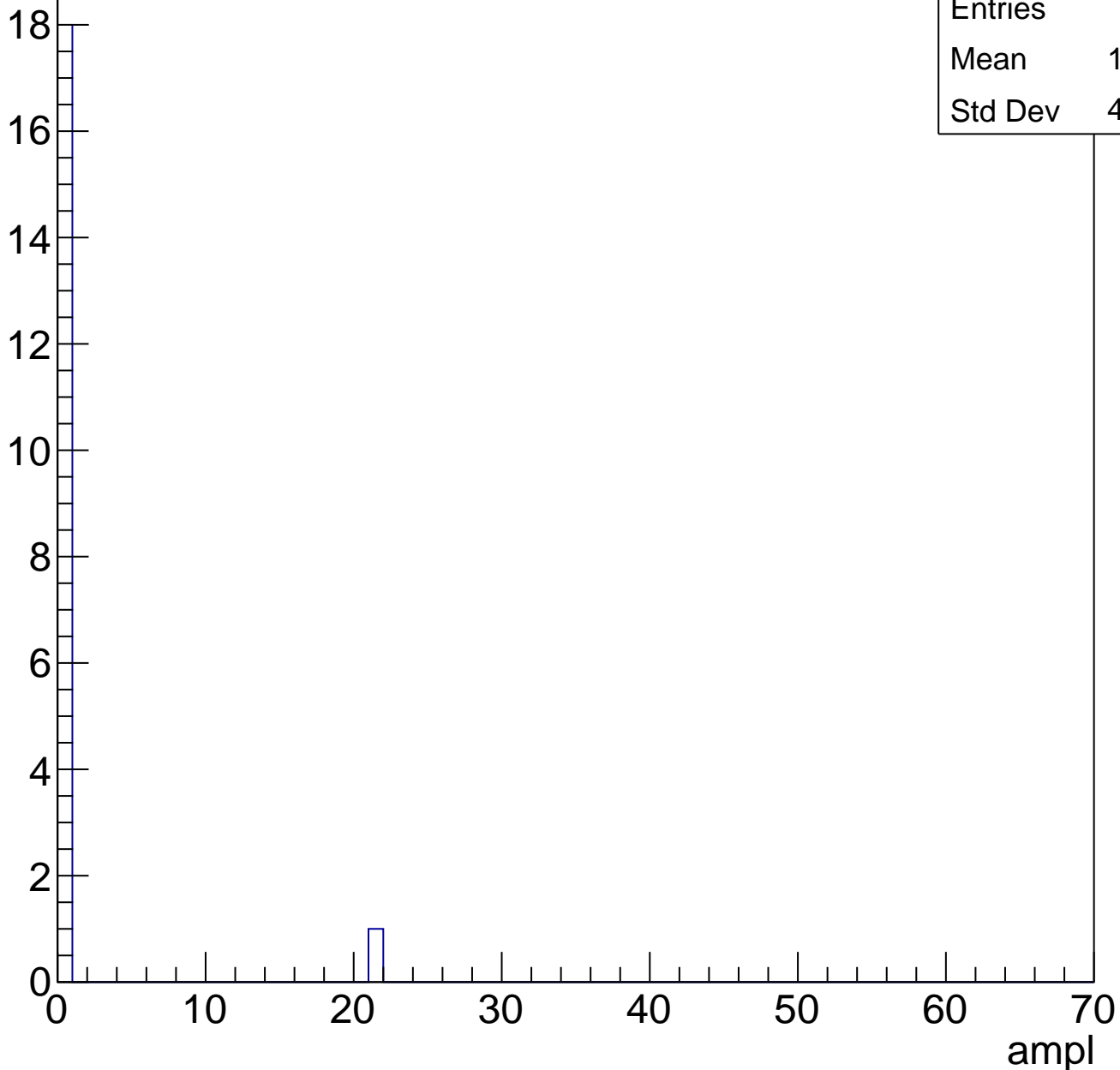


B1L103S, U8-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U8-ch21, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	23.02
Std Dev	12.17

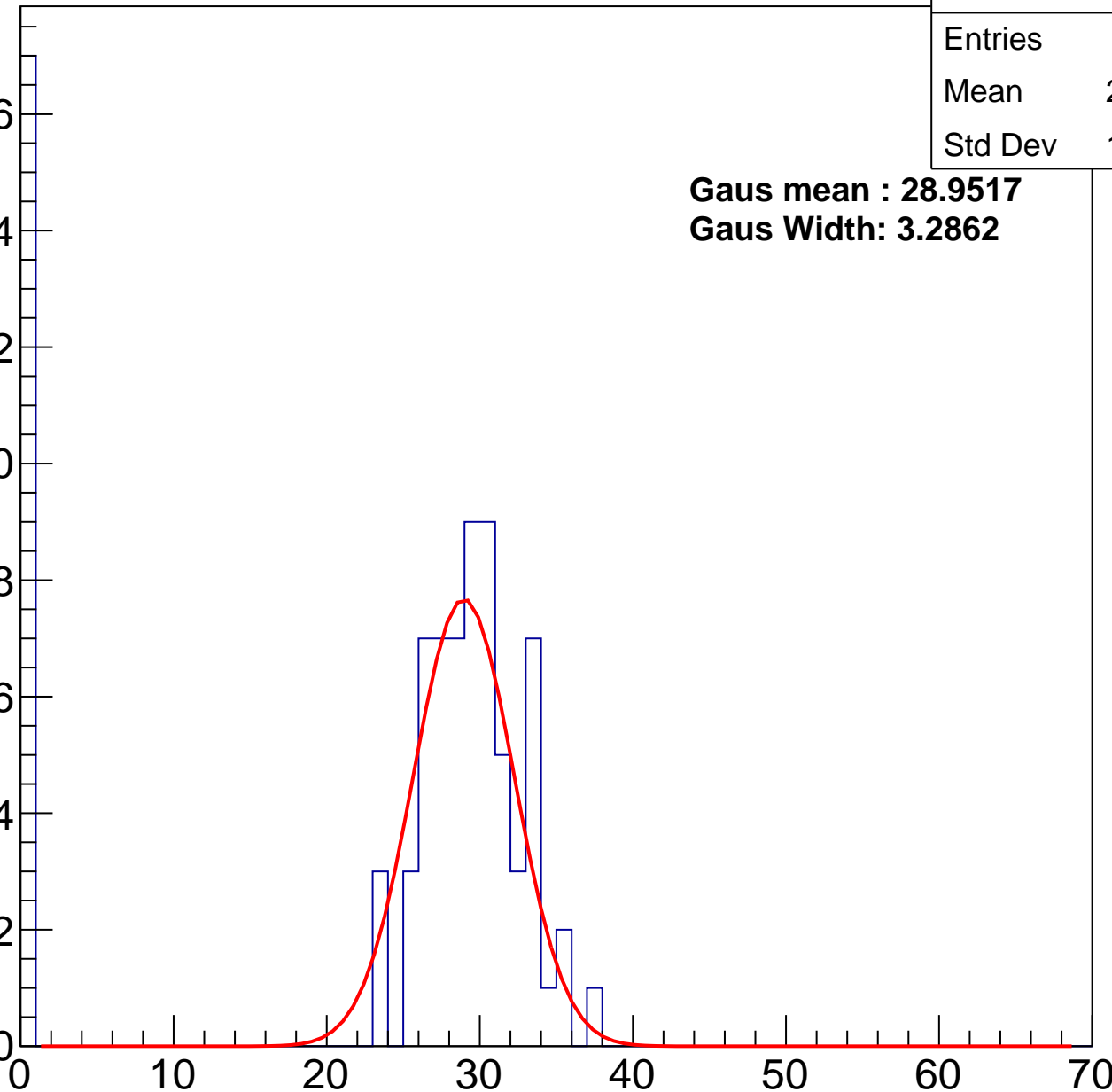
Gaus mean : 28.9517

Gaus Width: 3.2862

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U8-ch21, adc1

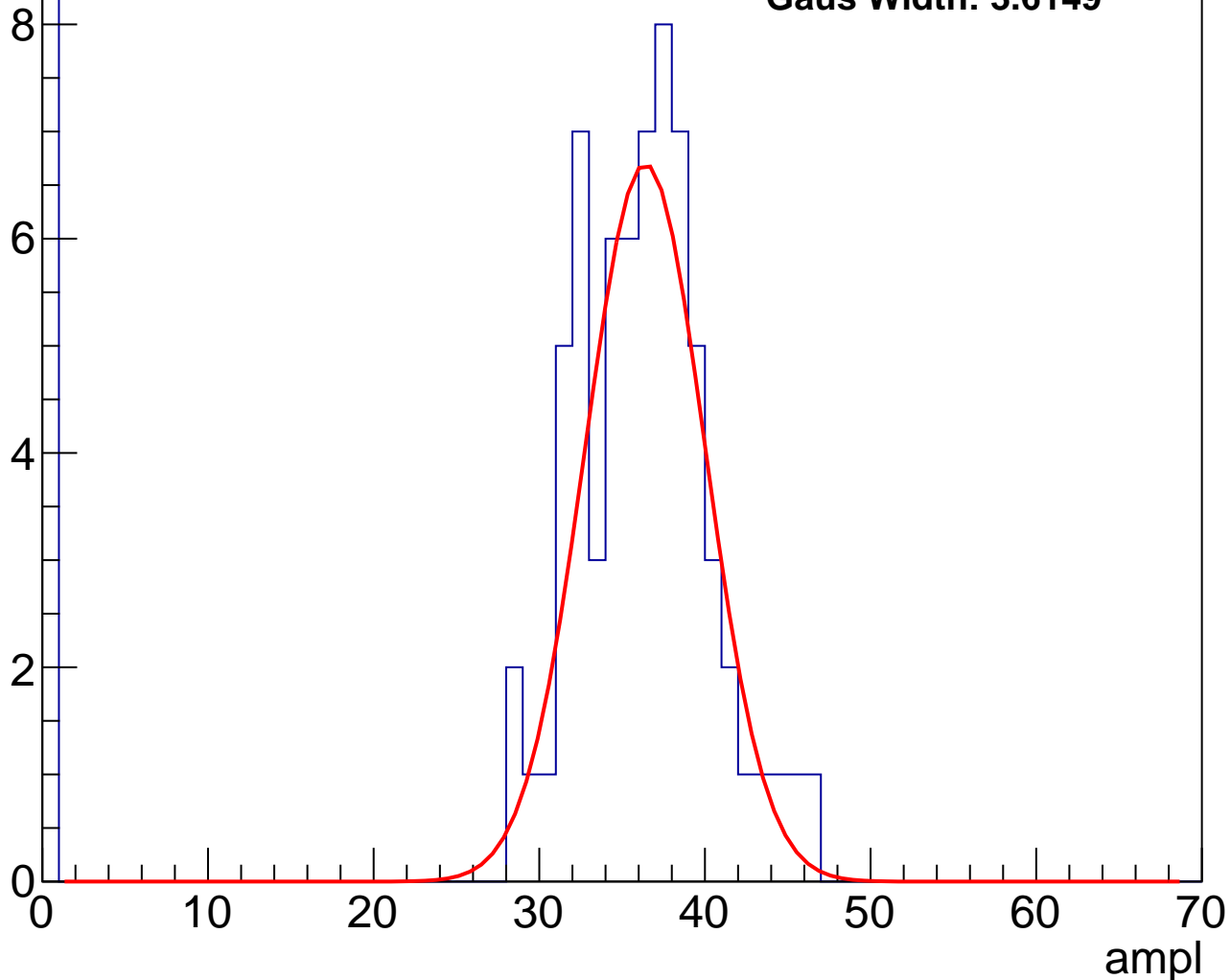
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	31.26
Std Dev	12.52

Gaus mean : 36.3947

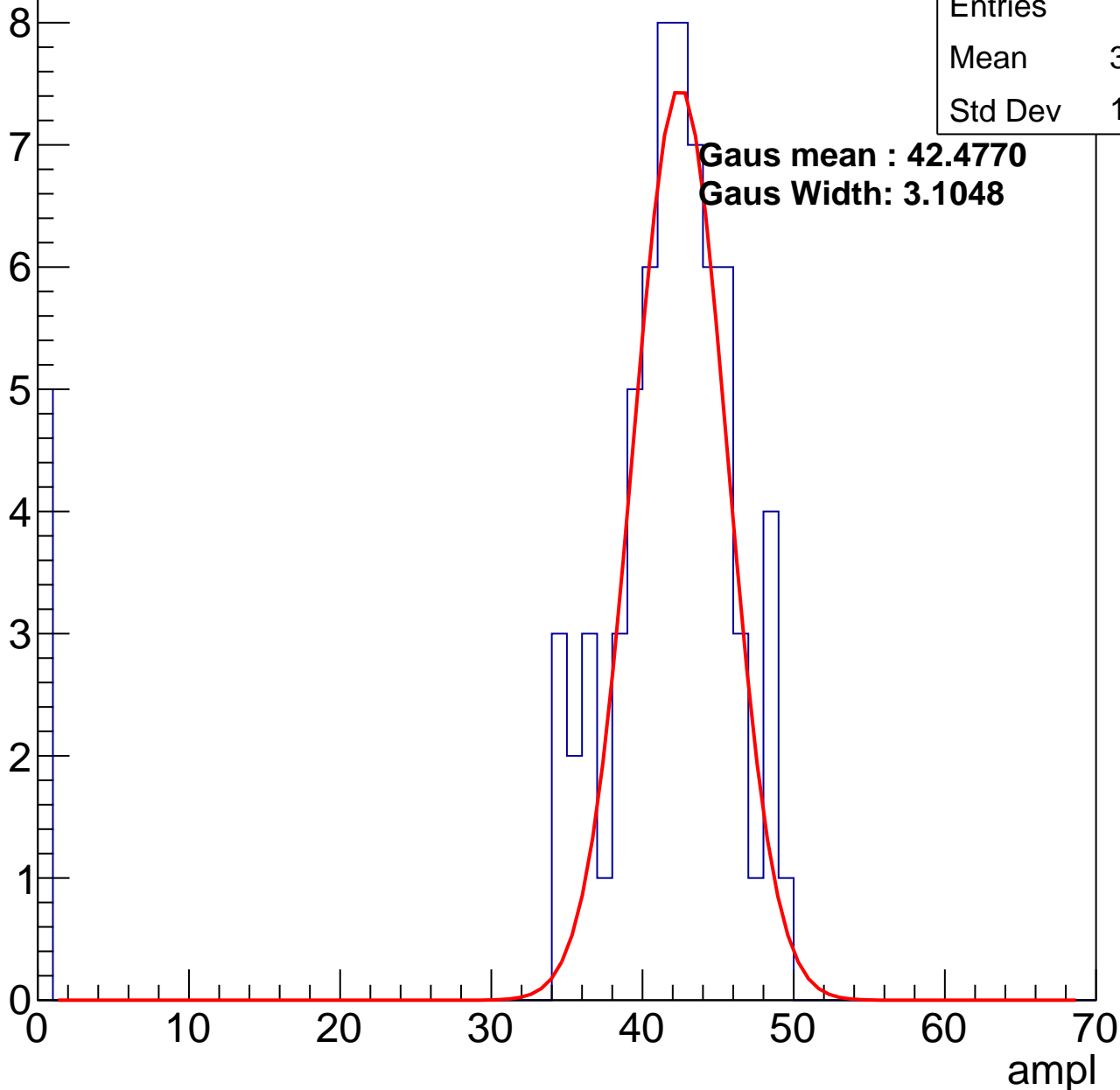
Gaus Width: 3.6149



B1L103S, U8-ch21, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

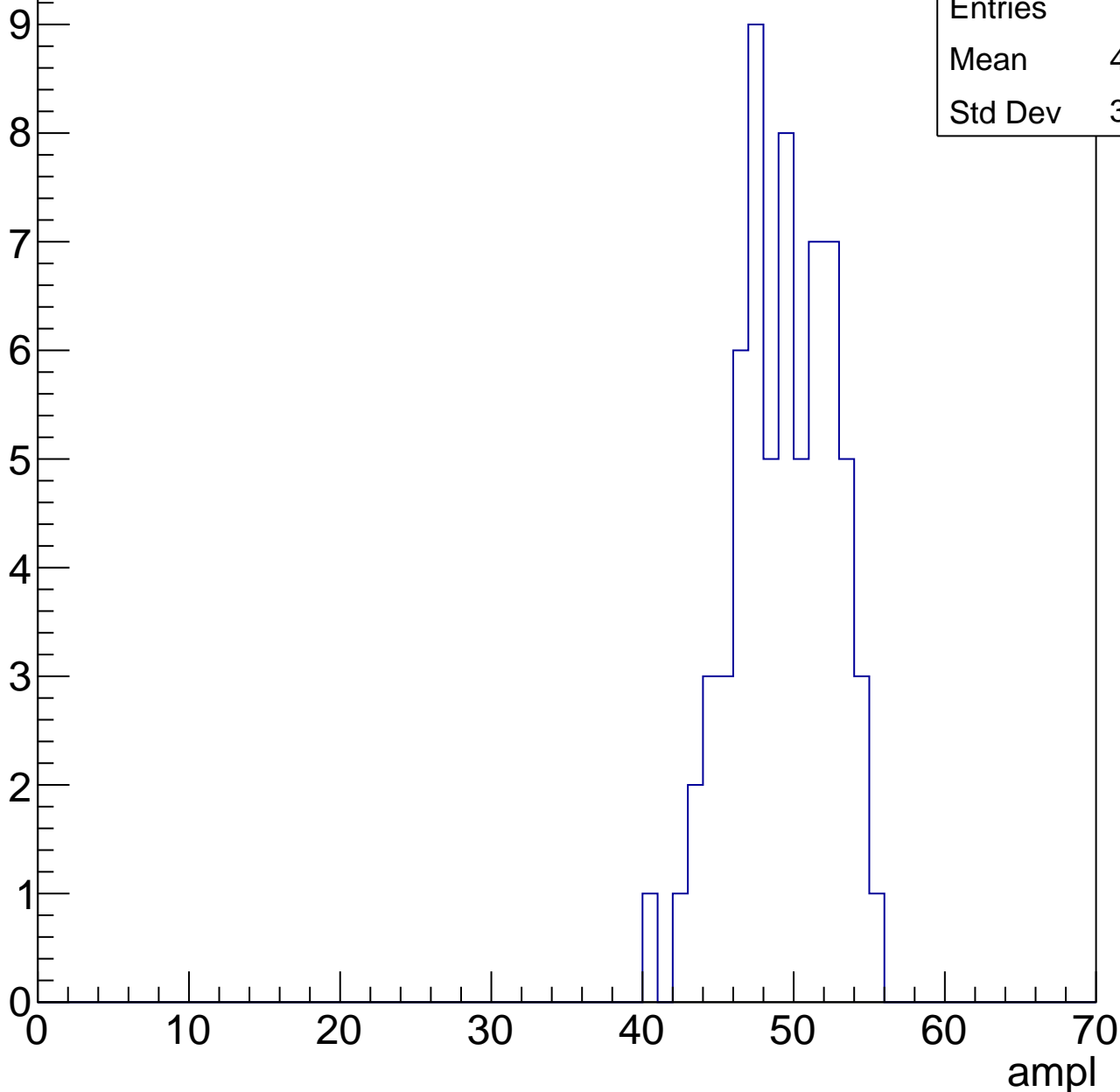


B1L103S, U8-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	48.77
Std Dev	3.265

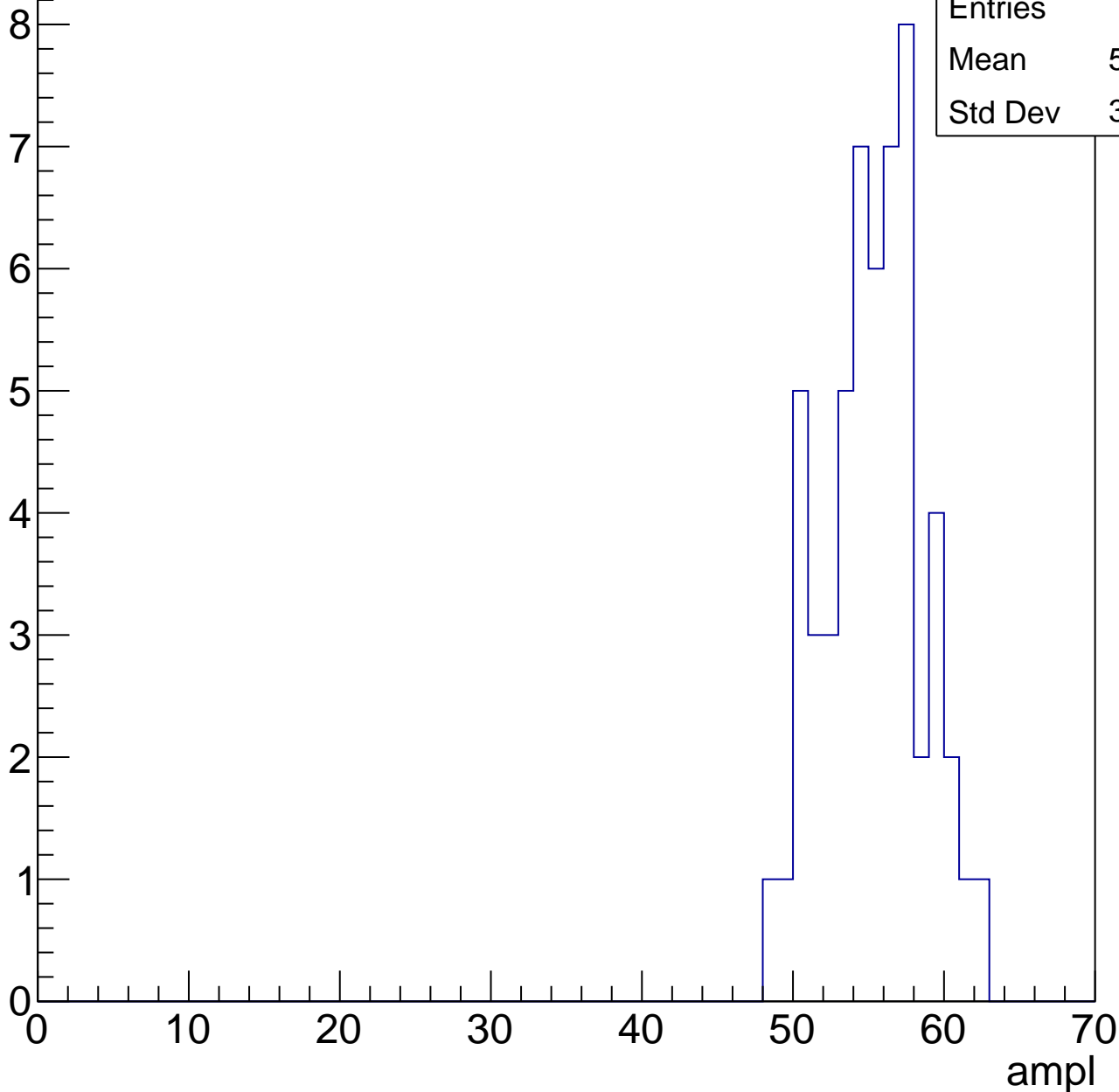


B1L103S, U8-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	54.86
Std Dev	3.182

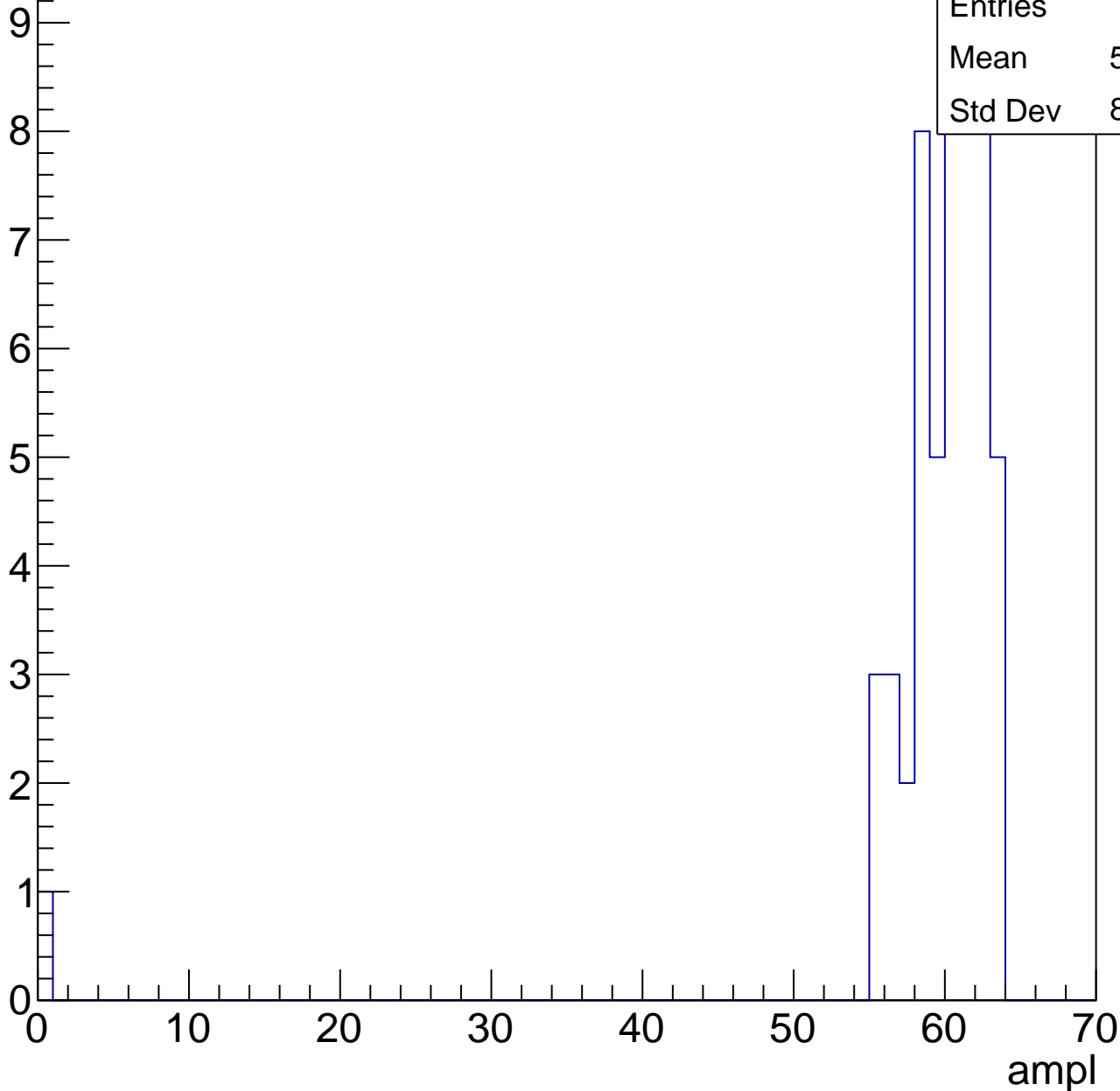


B1L103S, U8-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.64
Std Dev	8.434

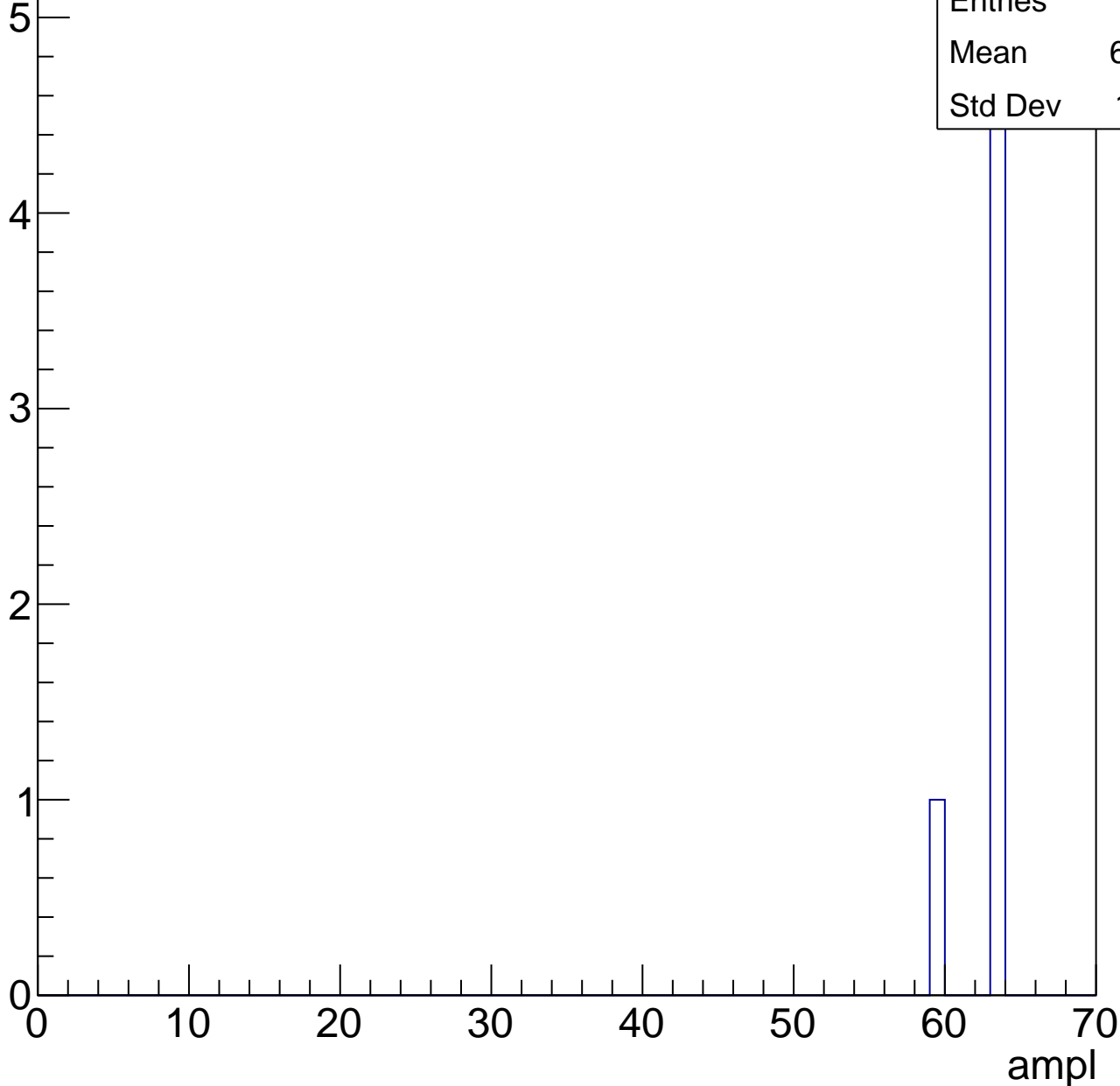


B1L103S, U8-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	6
Mean	62.33
Std Dev	1.491



B1L103S, U8-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.222
Std Dev	5.039

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U8-ch22, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	27
Std Dev	9.72

Gaus mean : 30.8615

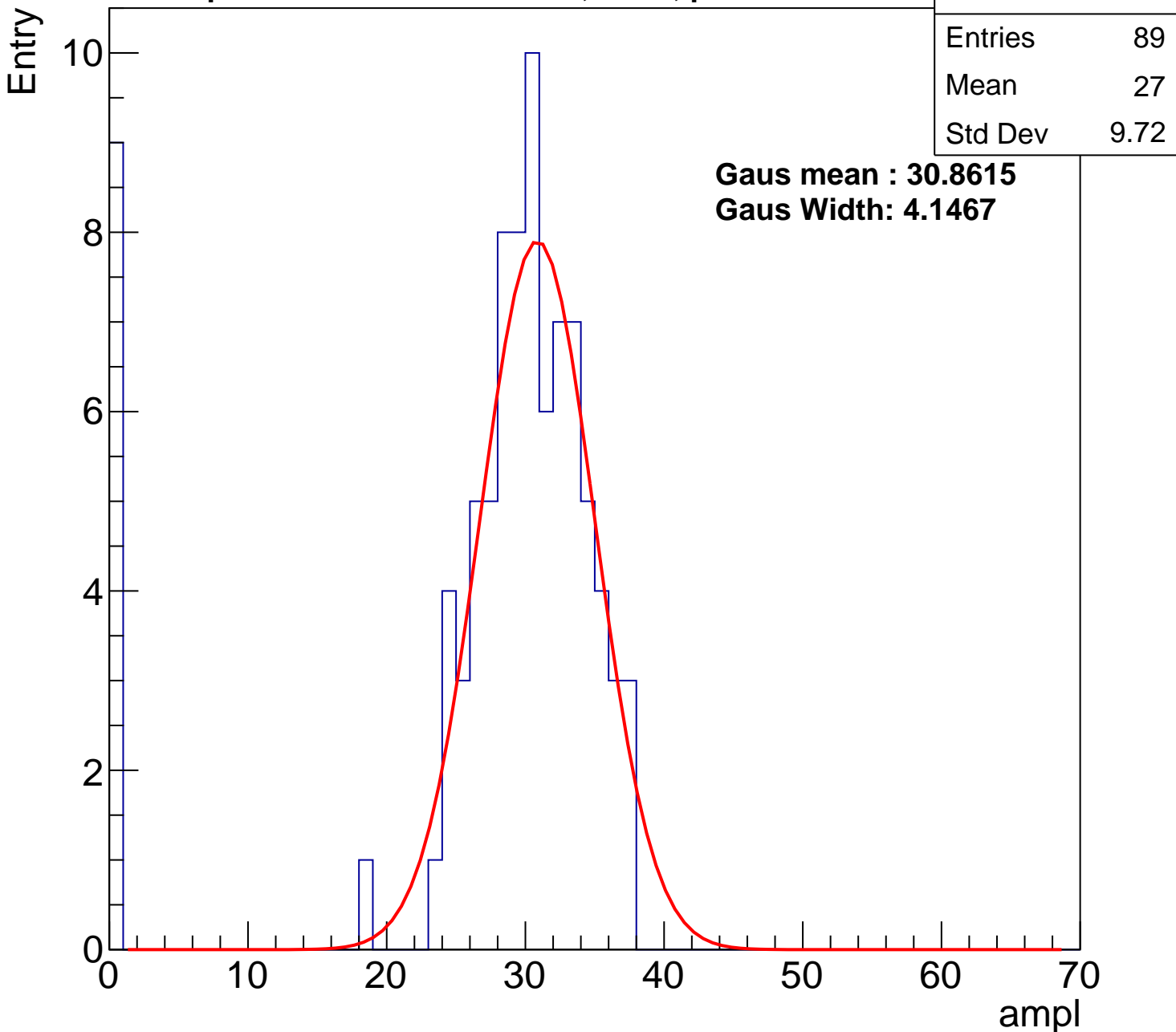
Gaus Width: 4.1467

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch22, adc1

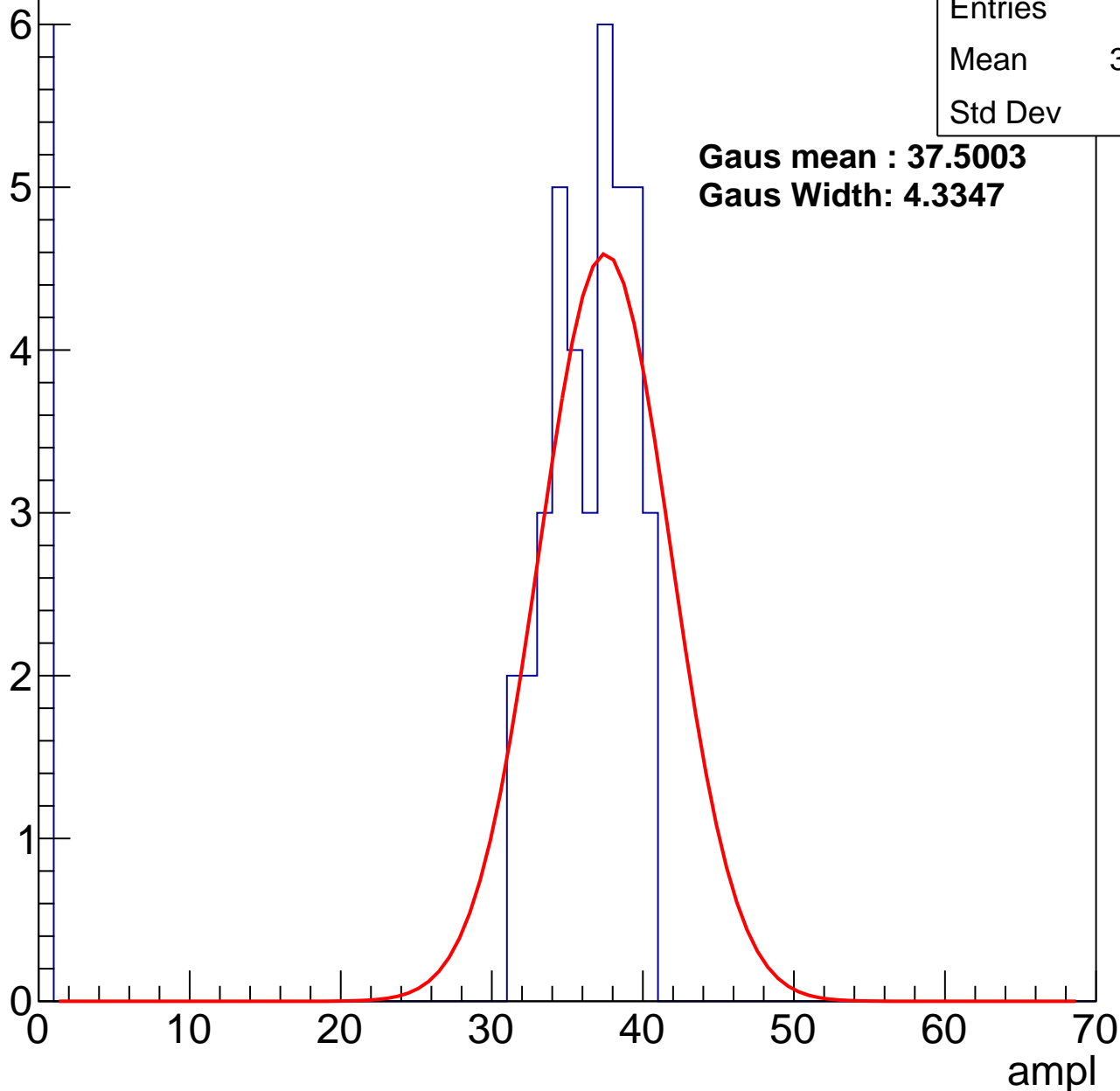
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	31.14
Std Dev	12.6

Gaus mean : 37.5003

Gaus Width: 4.3347



B1L103S, U8-ch22, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	39.8
Std Dev	8.784

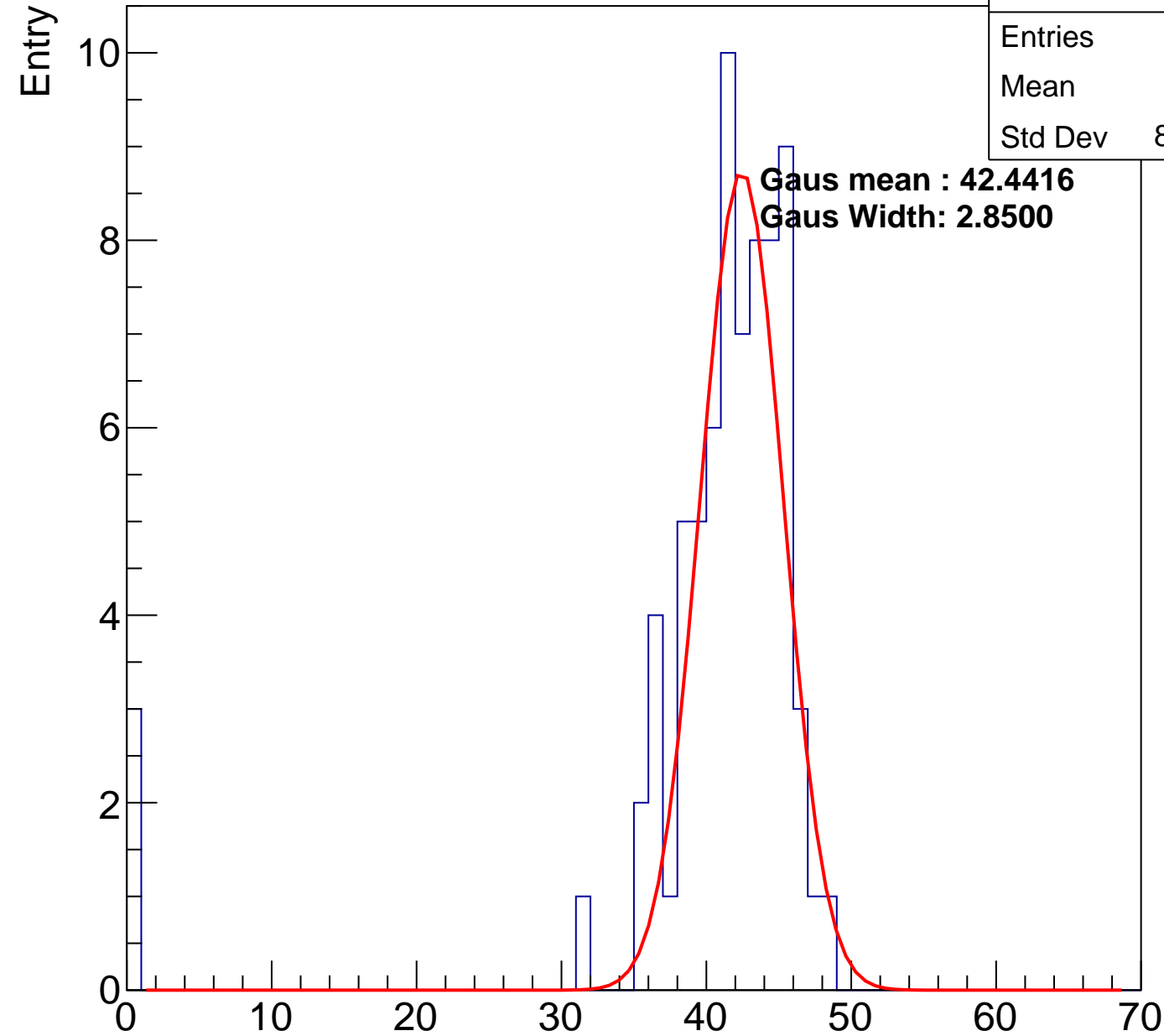
Gaus mean : 42.4416

Gaus Width: 2.8500

Entry

10
8
6
4
2
0

ampl



B1L103S, U8-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	48.6
Std Dev	3.451

Entry

10

8

6

4

2

0

0

10

20

30

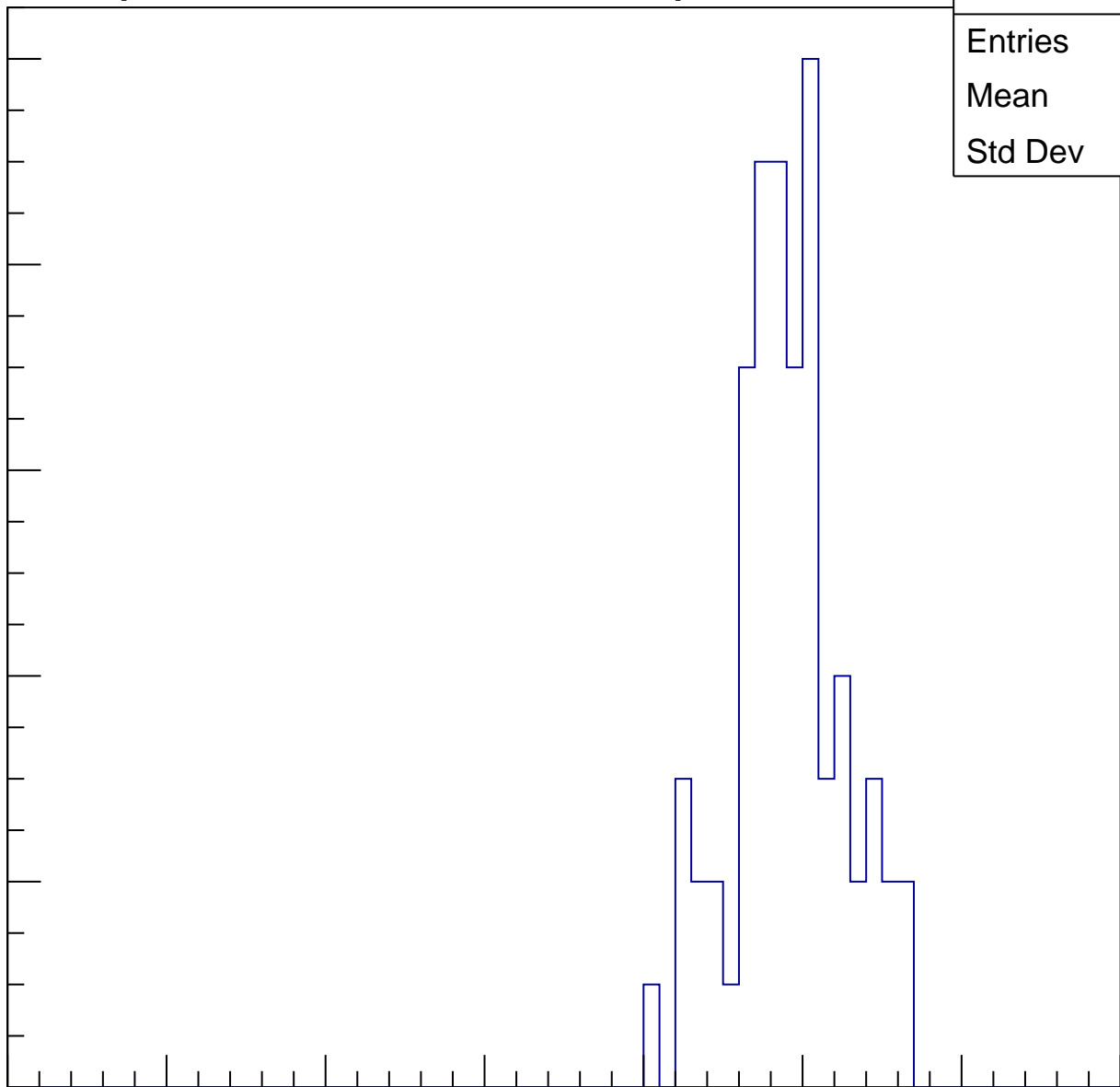
40

50

60

70

ampl

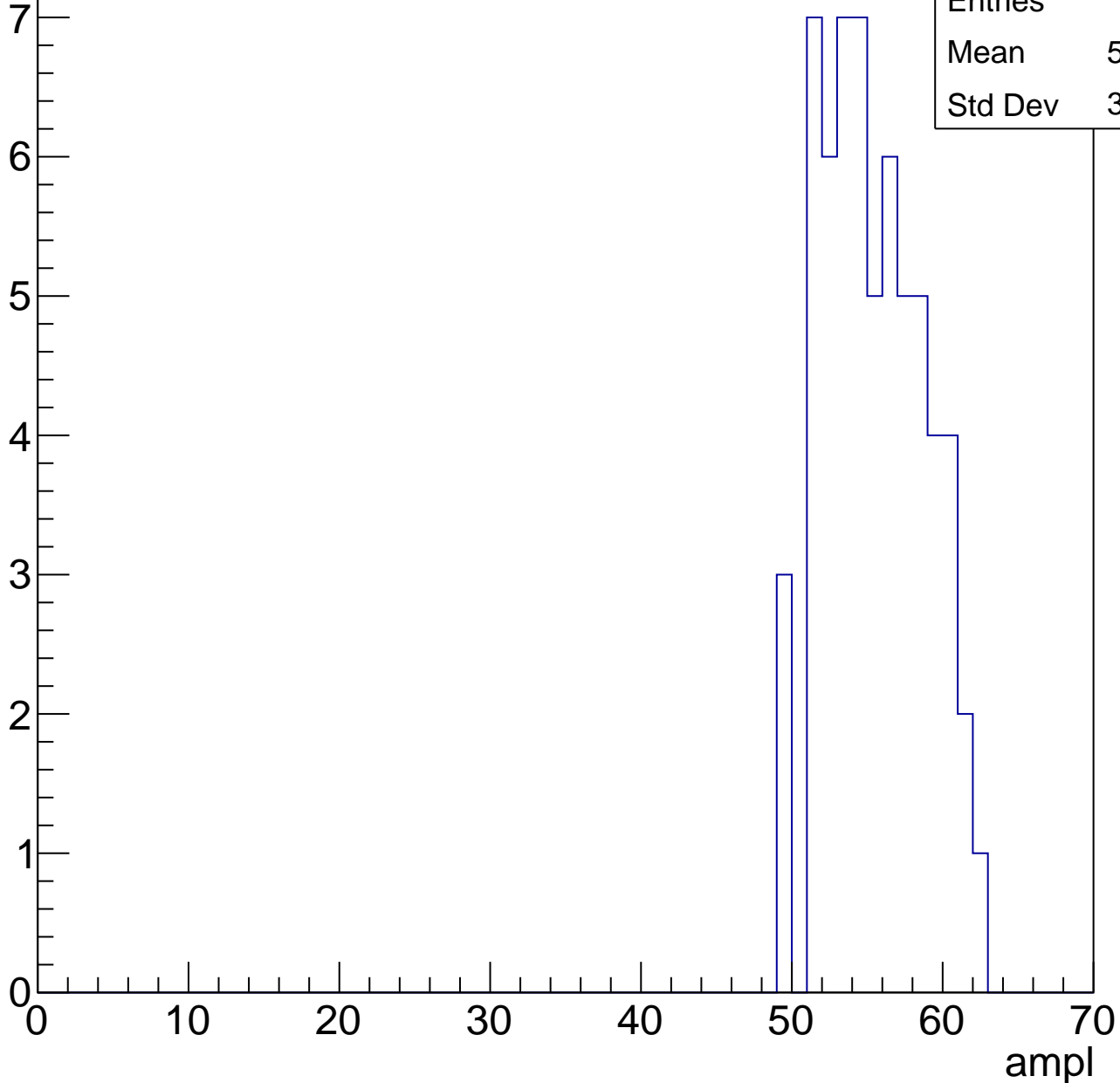


B1L103S, U8-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	55.02
Std Dev	3.275

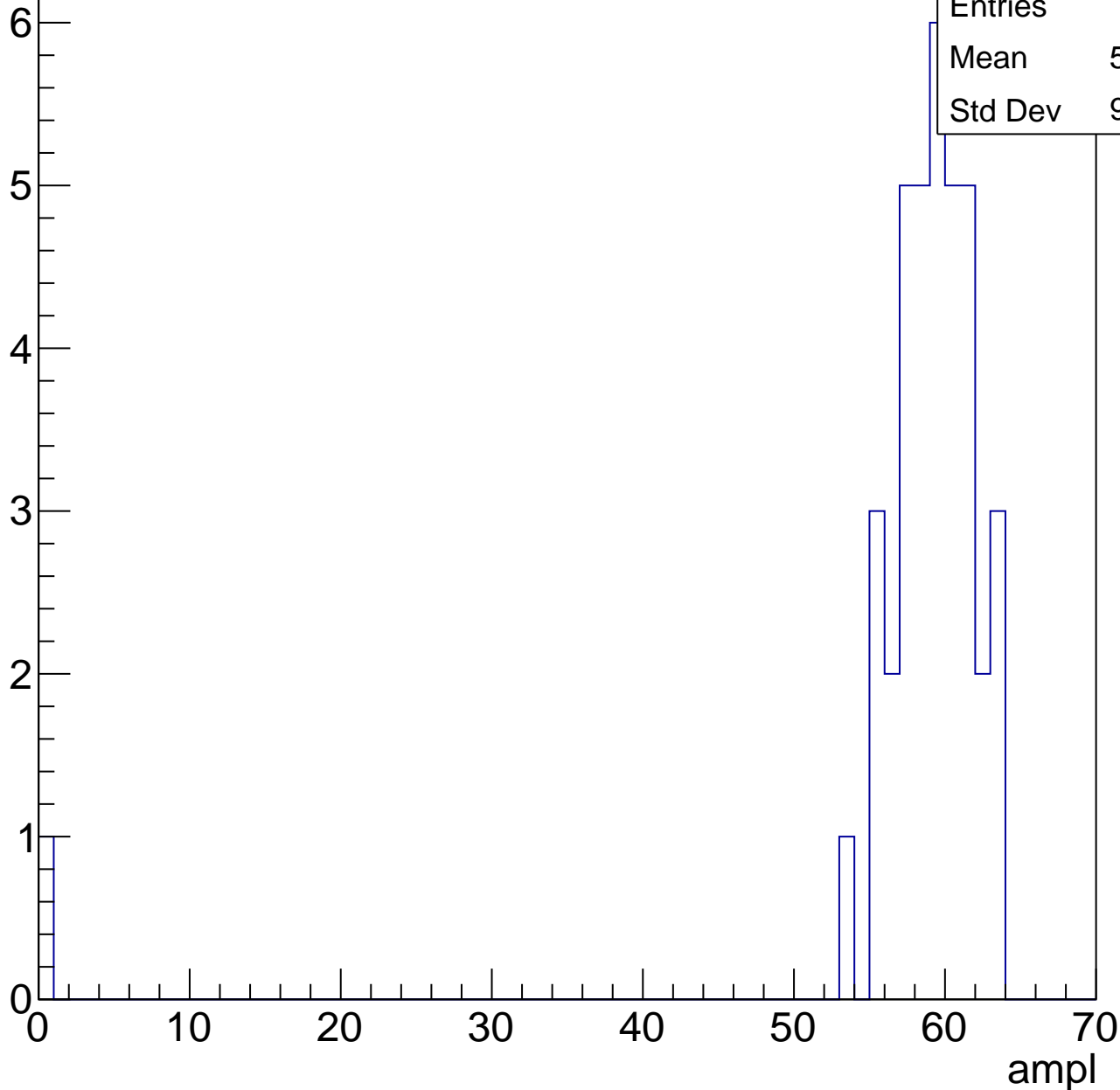


B1L103S, U8-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	57.29
Std Dev	9.717

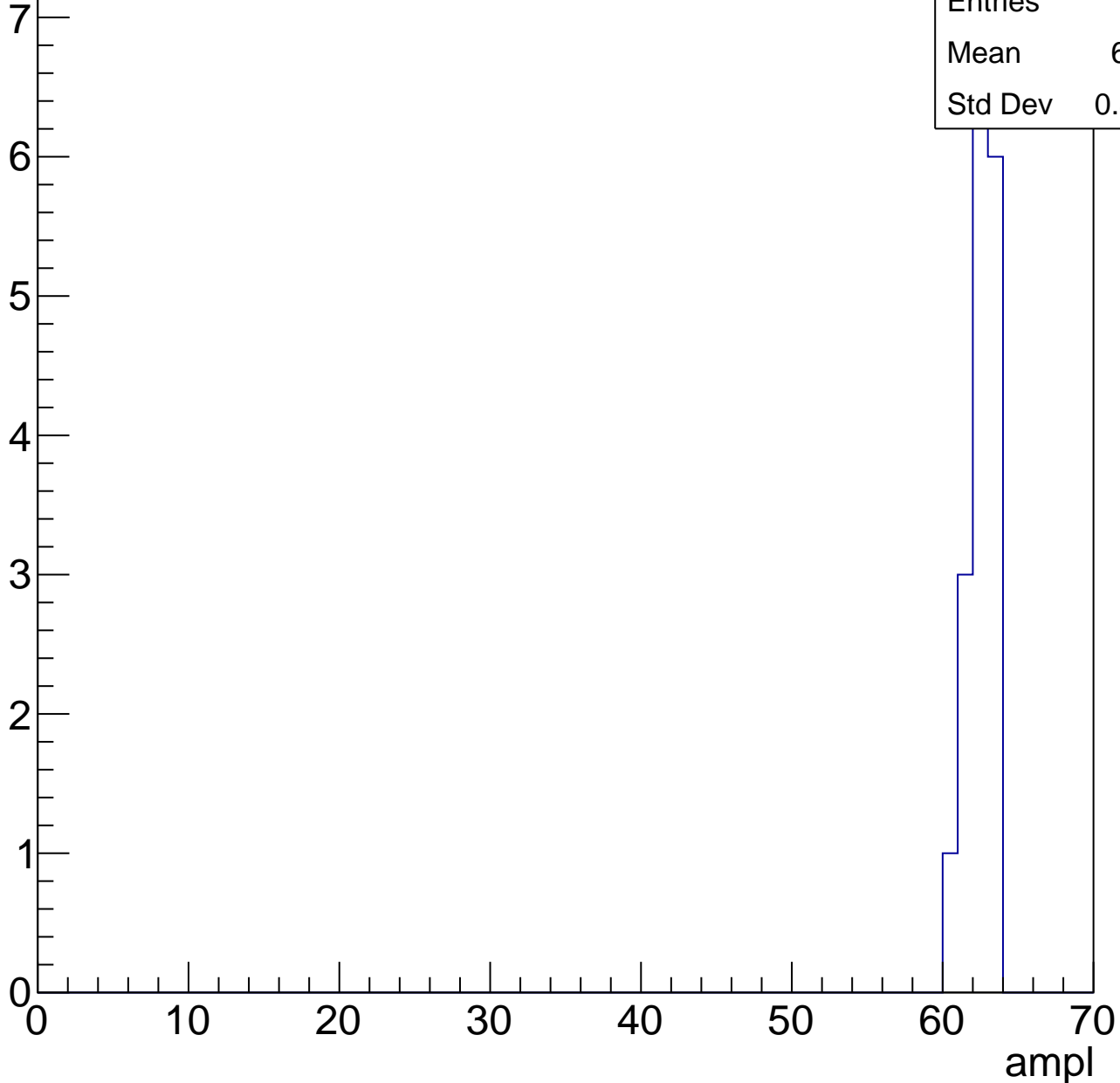


B1L103S, U8-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

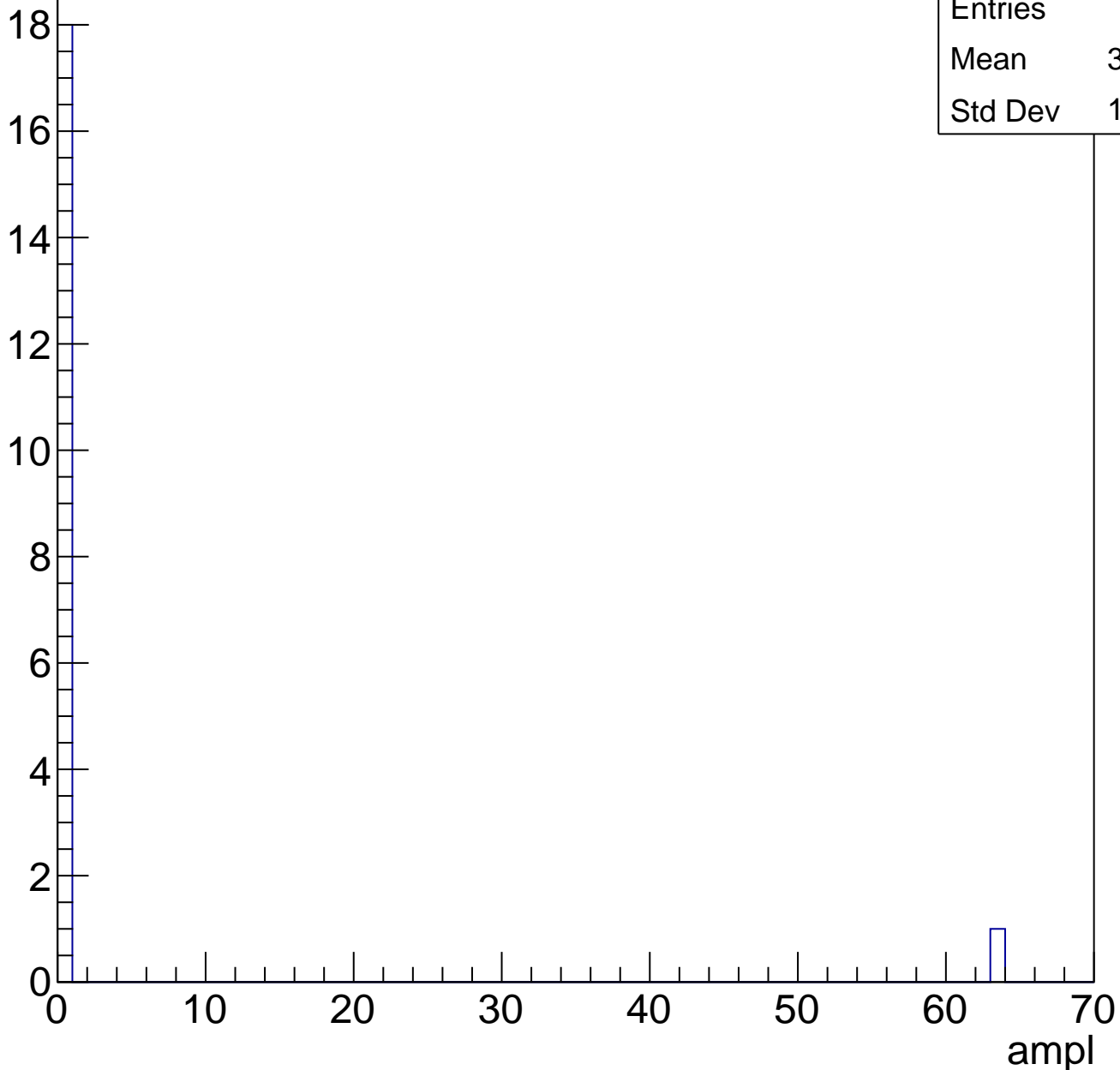
Entries	17
Mean	62.06
Std Dev	0.8725



B1L103S, U8-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	3.316
Std Dev	14.07

B1L103S, U8-ch23, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	25.64
Std Dev	11.65

Gaus mean : 30.6729

Gaus Width: 3.4743

Entry

12

10

8

6

4

2

0

0

10

20

30

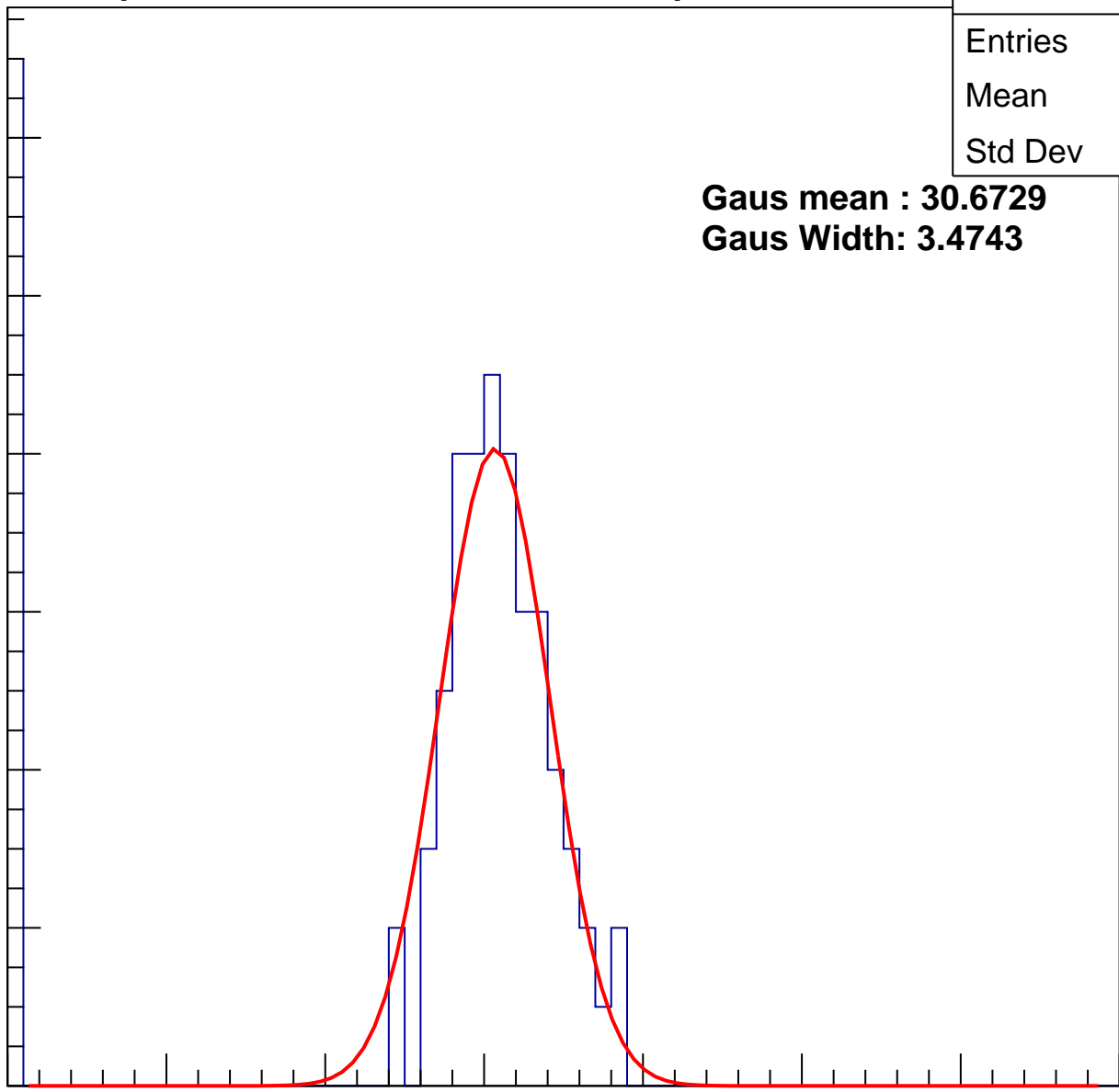
40

50

60

70

ampl



B1L103S, U8-ch23, adc1

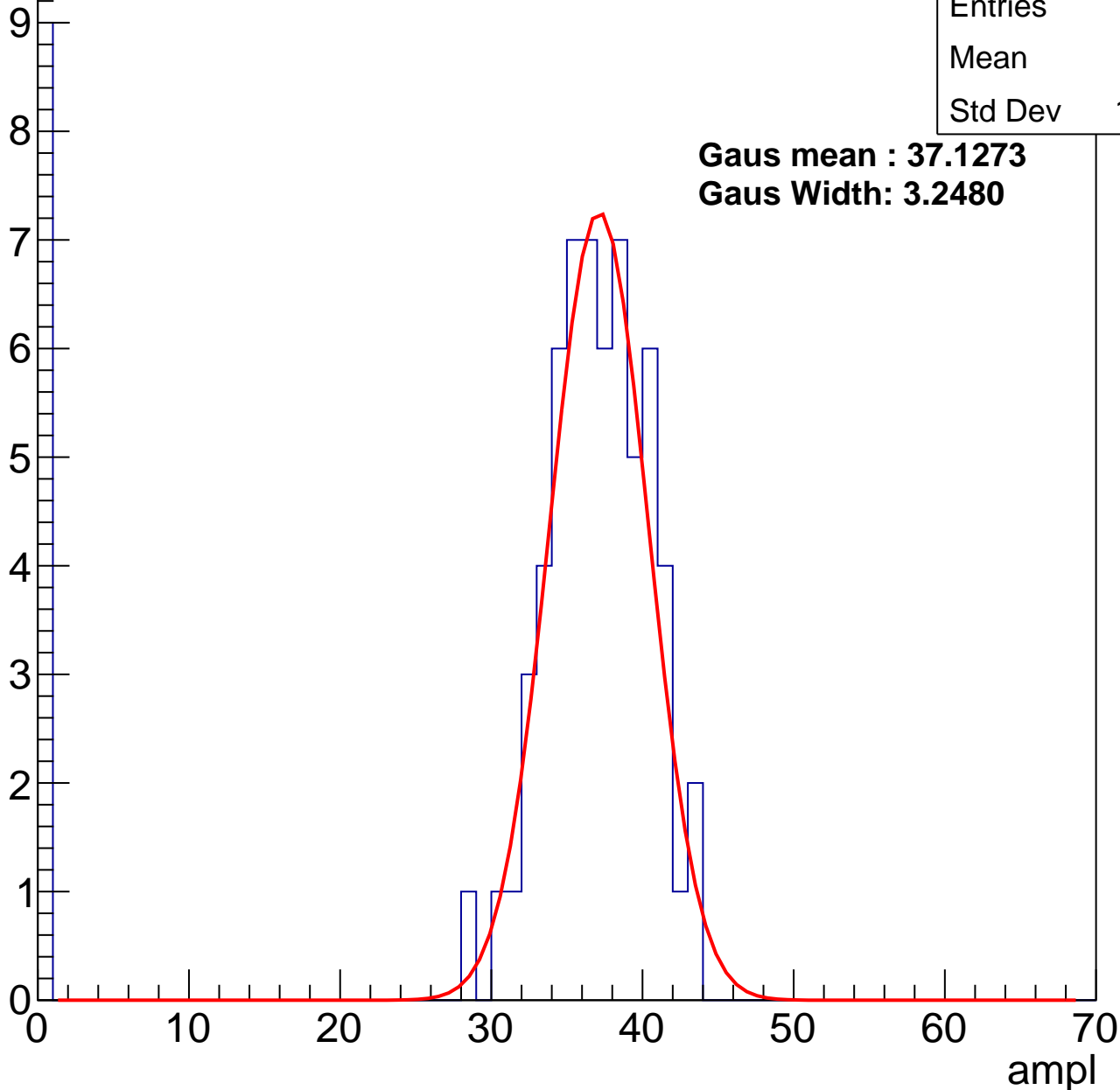
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	31.9
Std Dev	12.61

Gaus mean : 37.1273

Gaus Width: 3.2480



B1L103S, U8-ch23, adc2

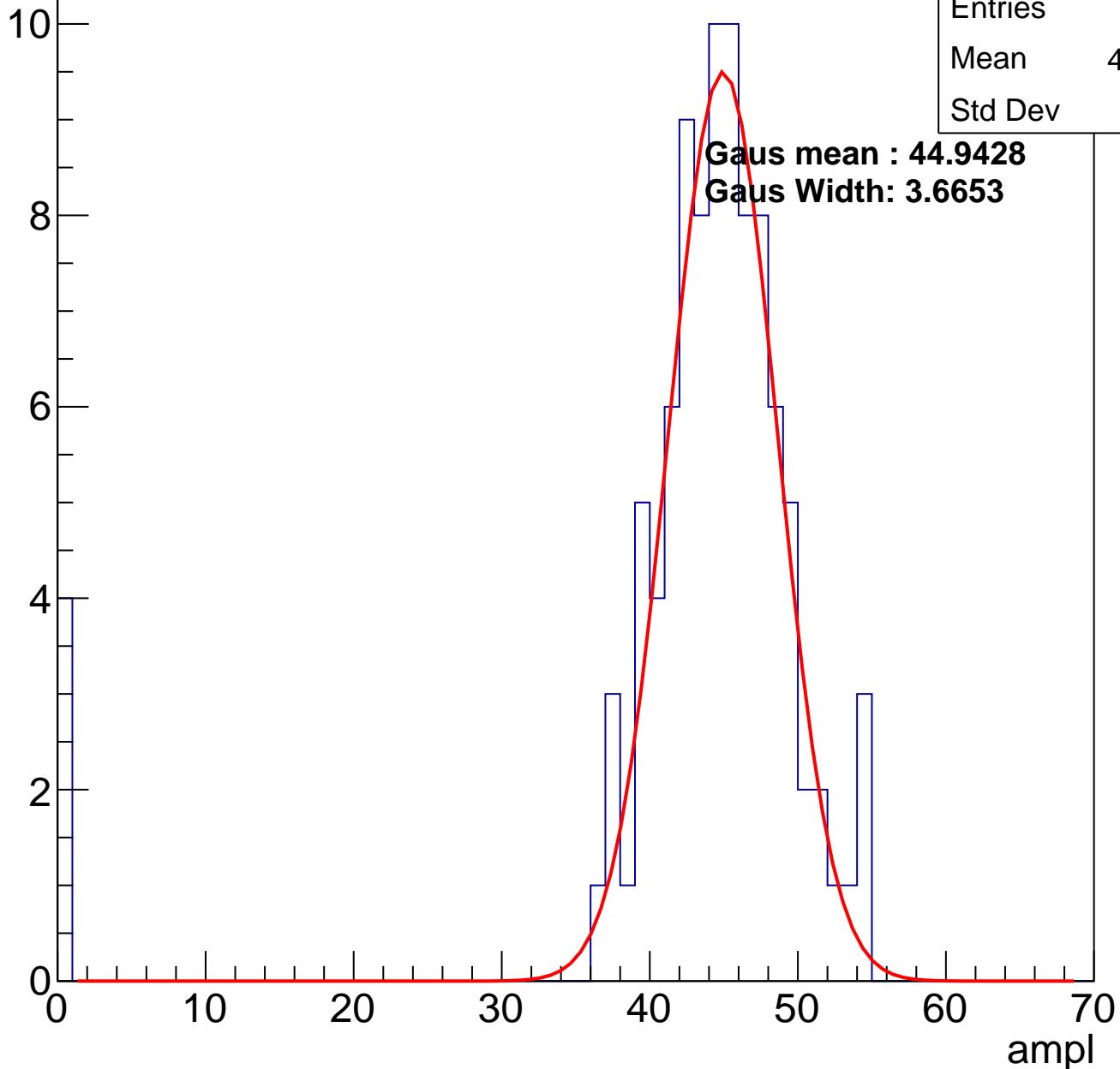
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	42.72
Std Dev	9.67

Gaus mean : 44.9428

Gaus Width: 3.6653

Entry

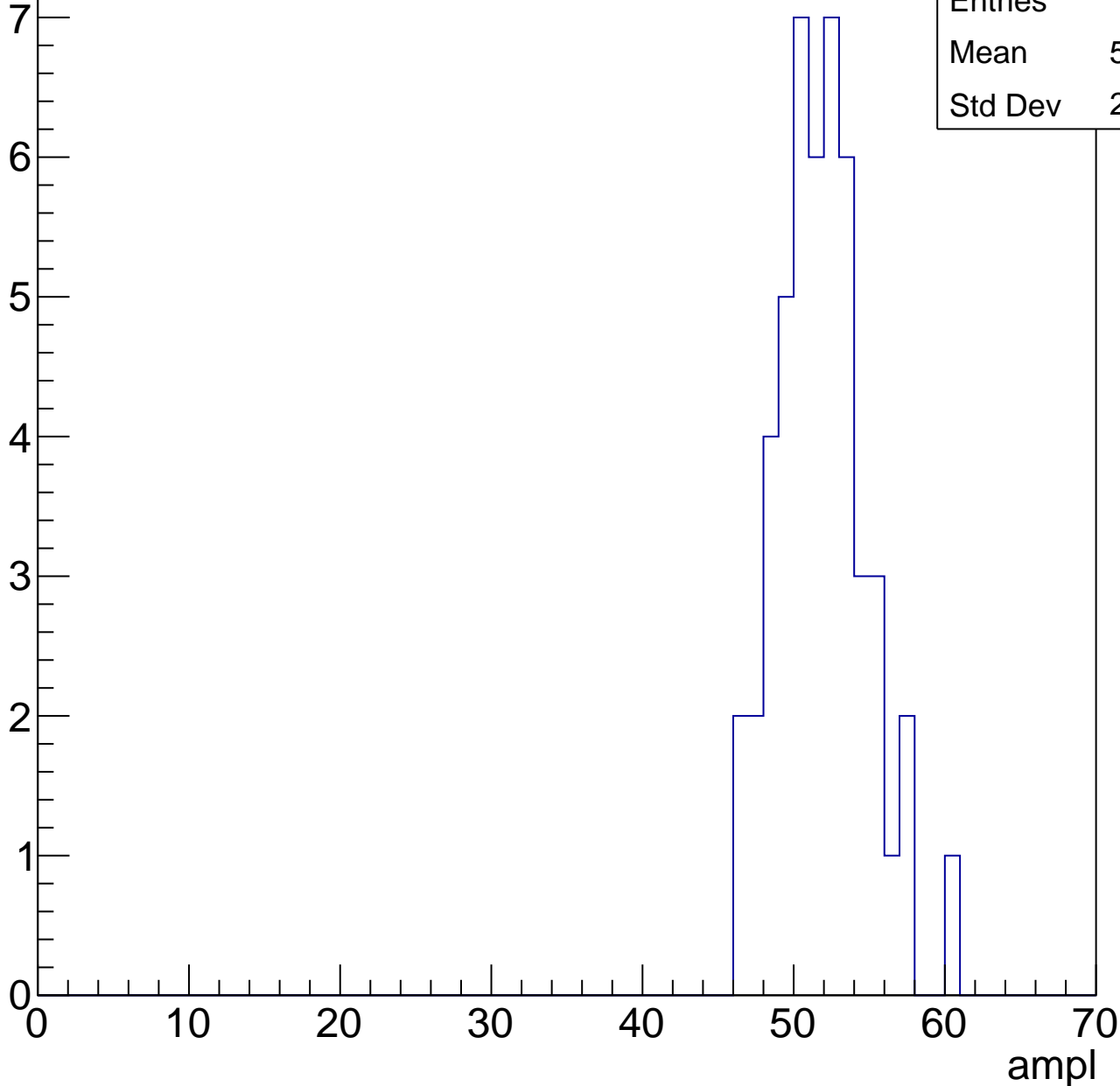


B1L103S, U8-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

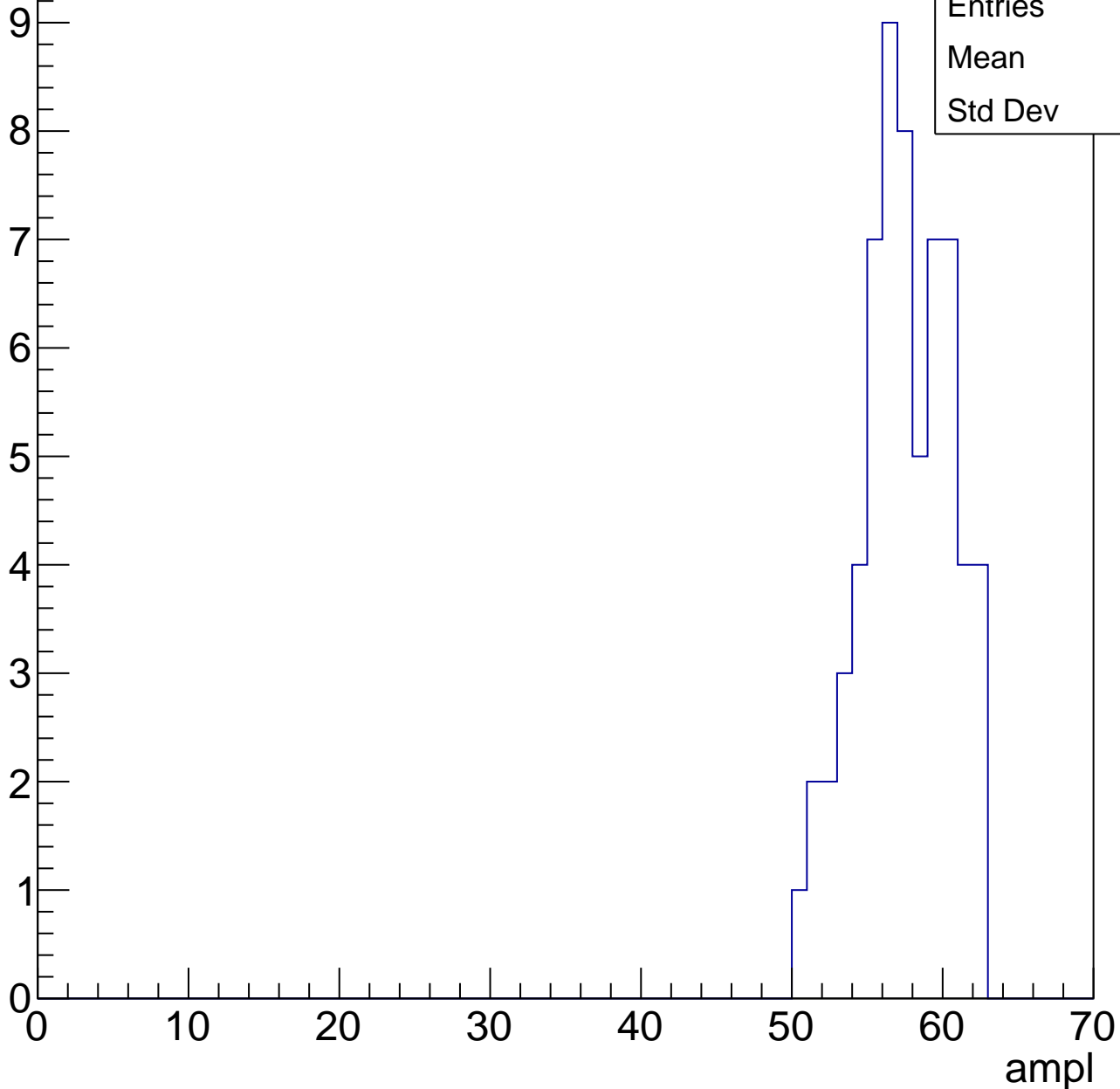
Entries	49
Mean	51.39
Std Dev	2.933



B1L103S, U8-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

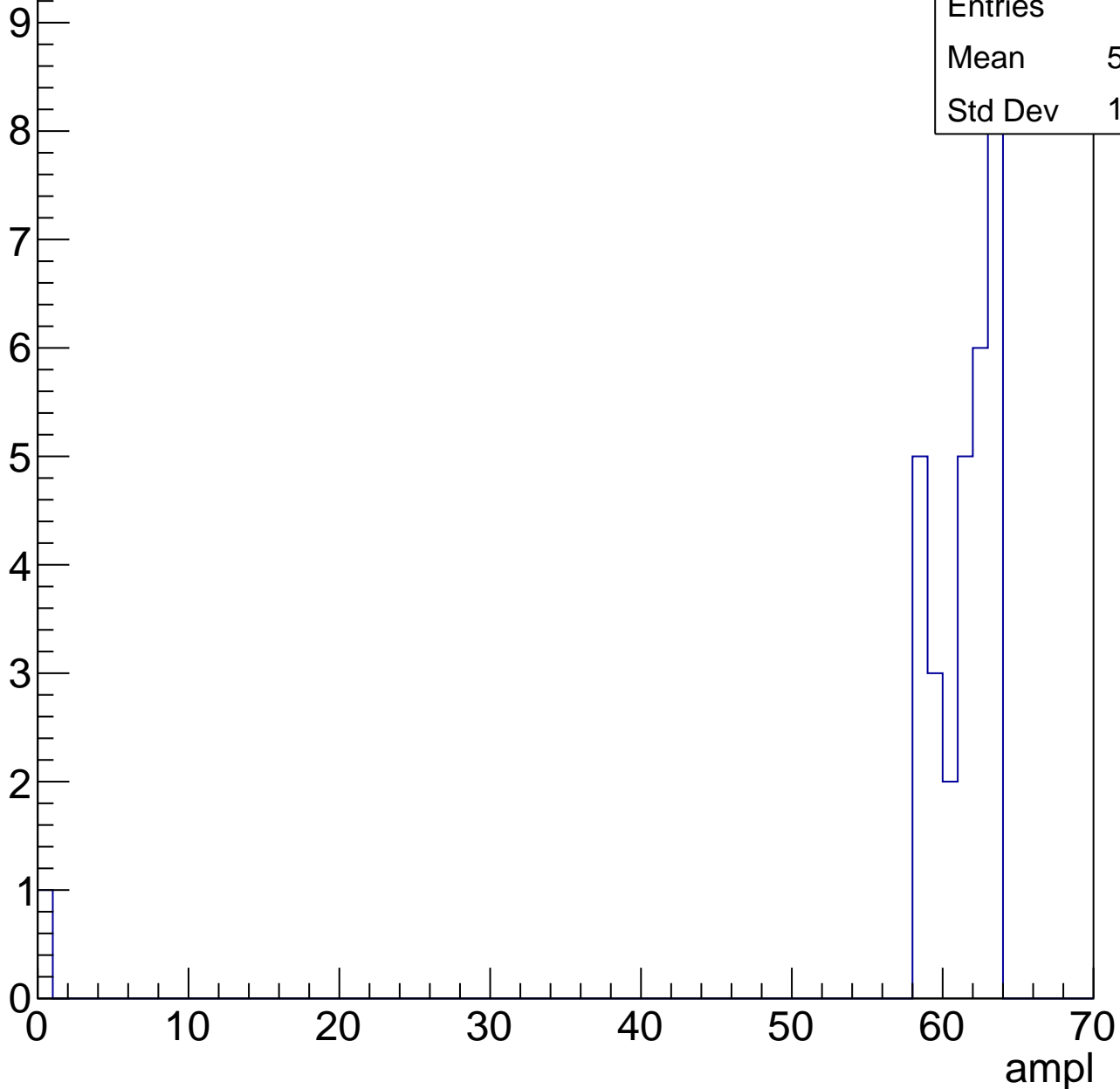


B1L103S, U8-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	59.06
Std Dev	10.93



B1L103S, U8-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch24, adc0

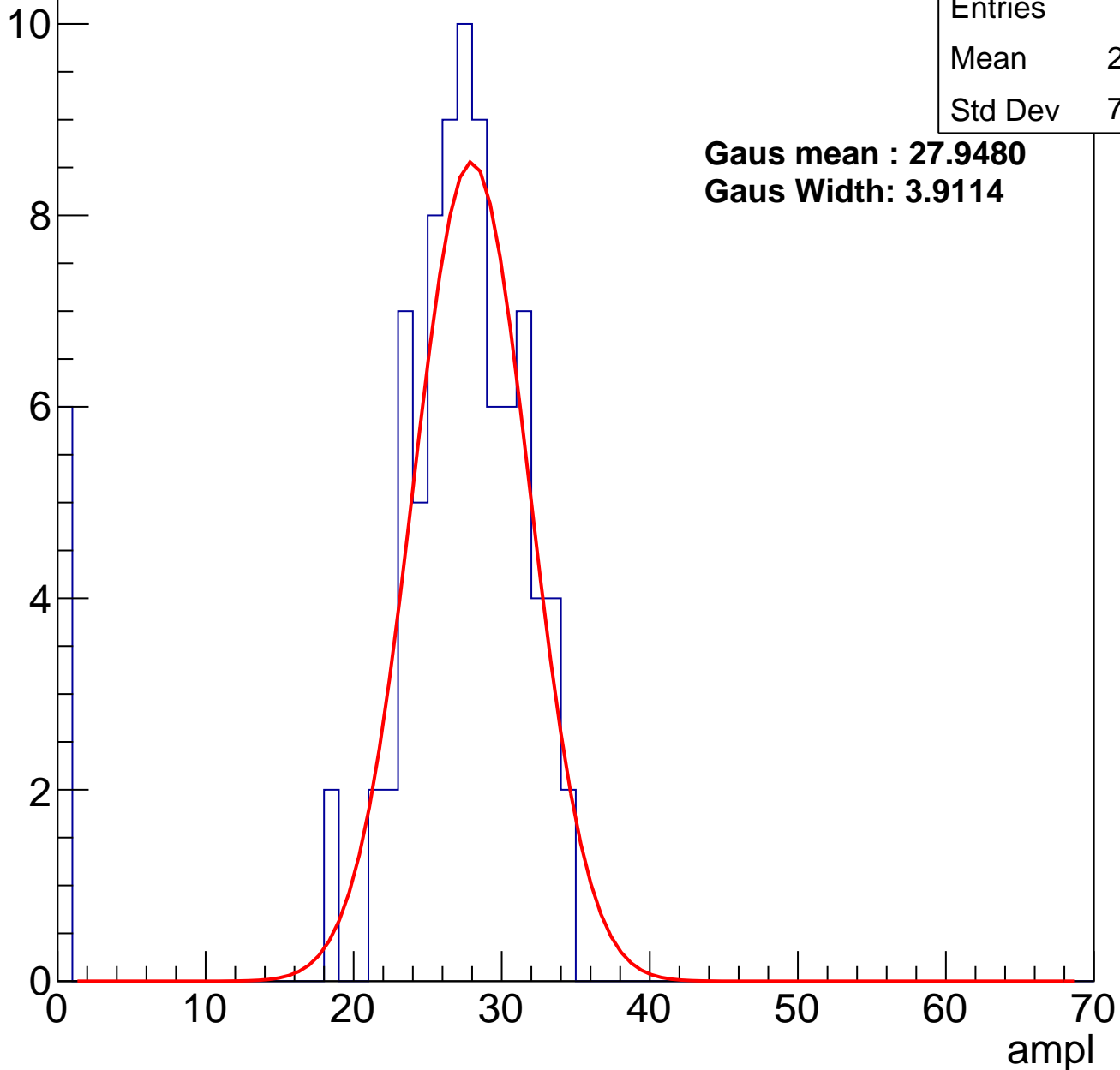
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	25.37
Std Dev	7.615

Gaus mean : 27.9480

Gaus Width: 3.9114

Entry



B1L103S, U8-ch24, adc1

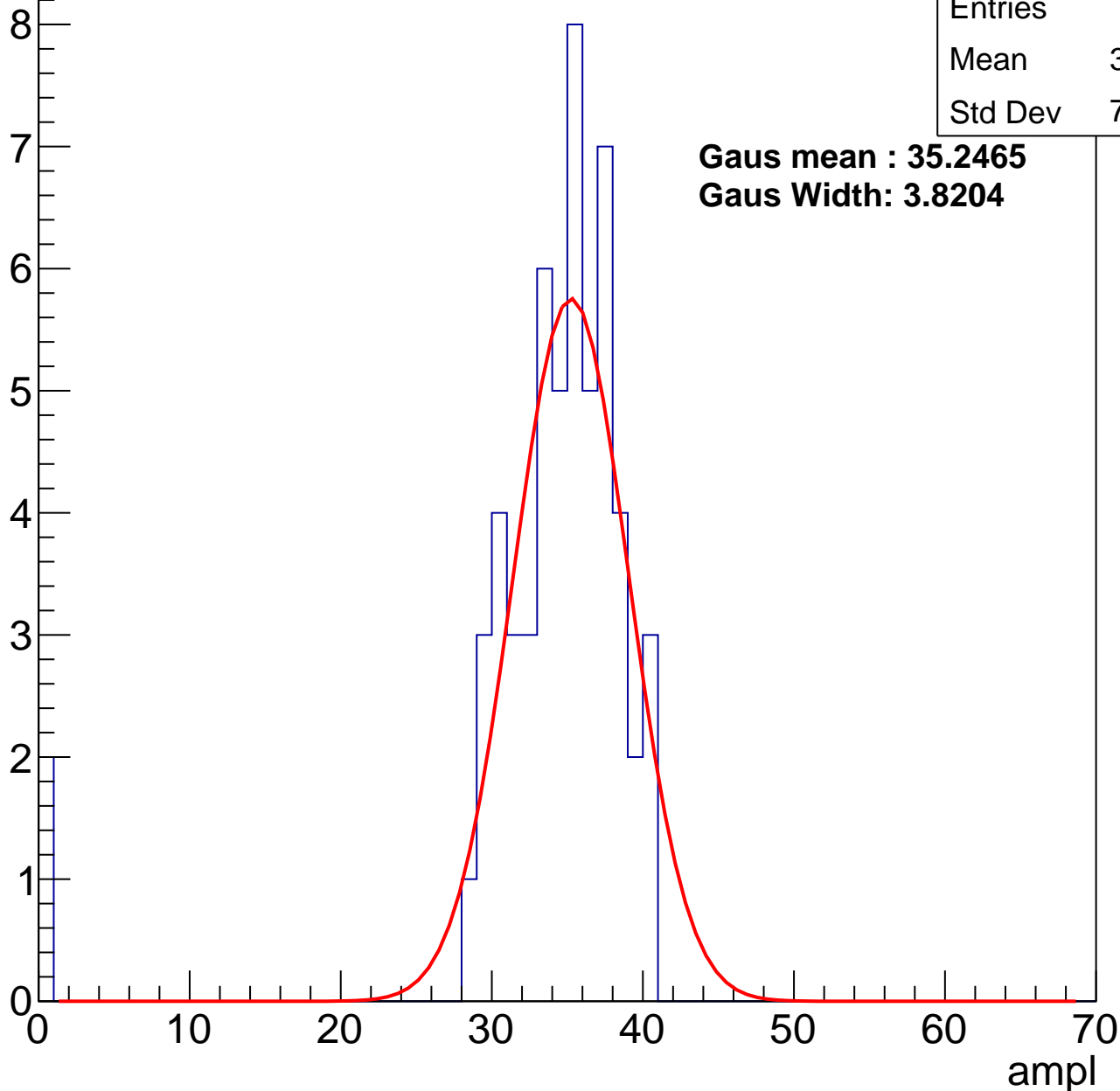
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	33.23
Std Dev	7.086

Gaus mean : 35.2465

Gaus Width: 3.8204



B1L103S, U8-ch24, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	33.88
Std Dev	14.55

Gaus mean : 40.7805

Gaus Width: 4.7761

Entry

10

8

6

4

2

0

0

10

20

30

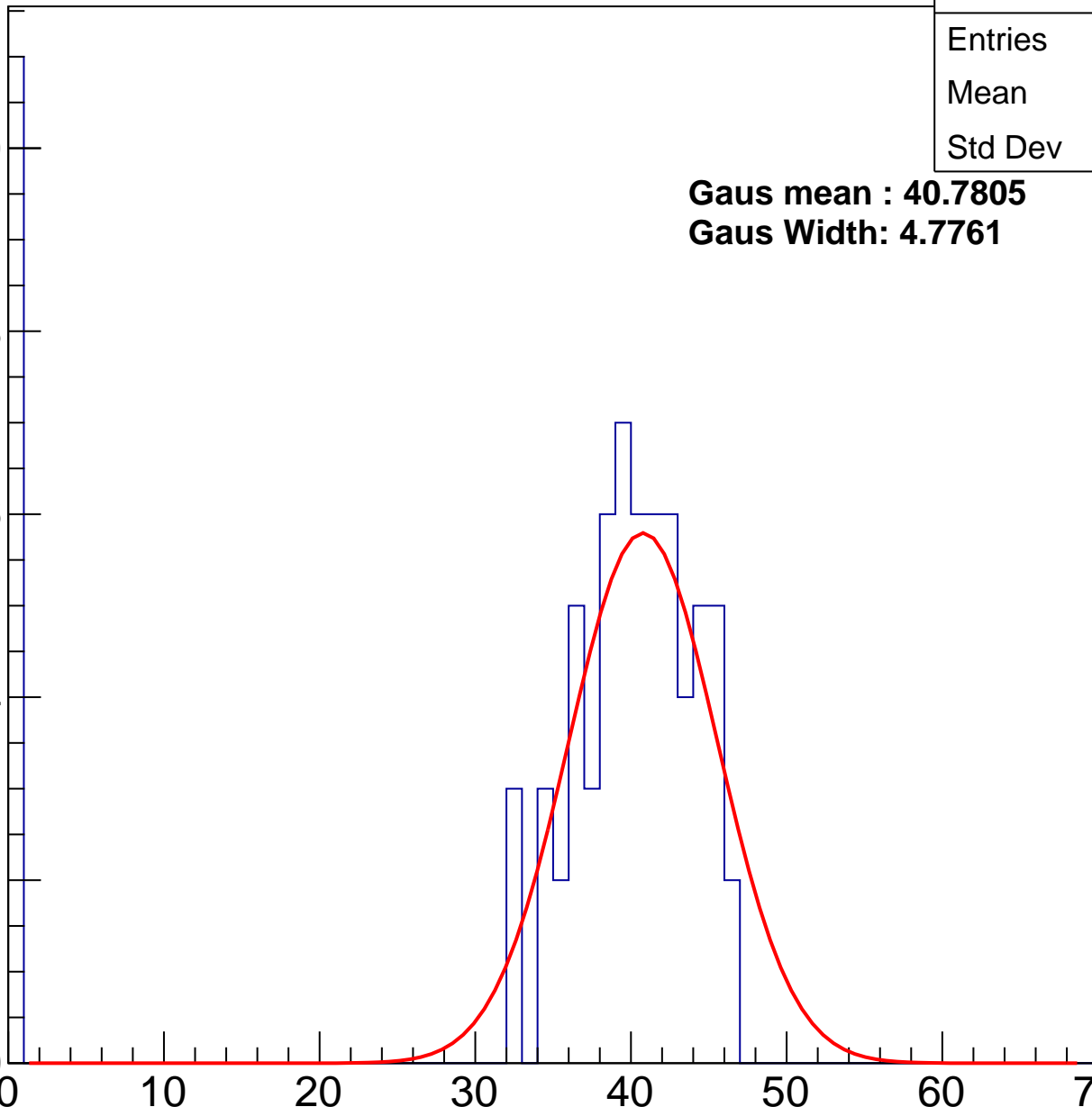
40

50

60

70

ampl

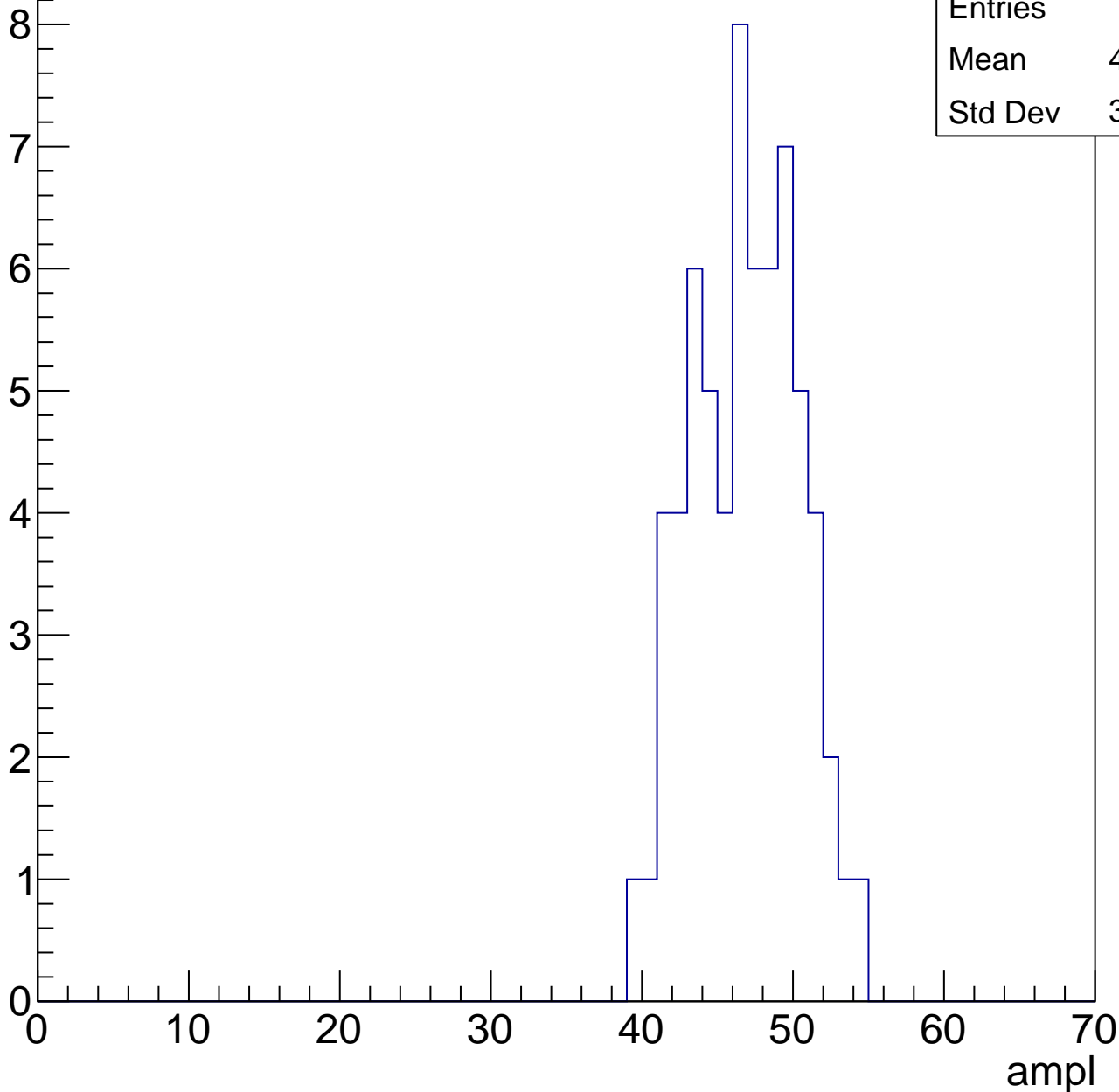


B1L103S, U8-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	46.38
Std Dev	3.454

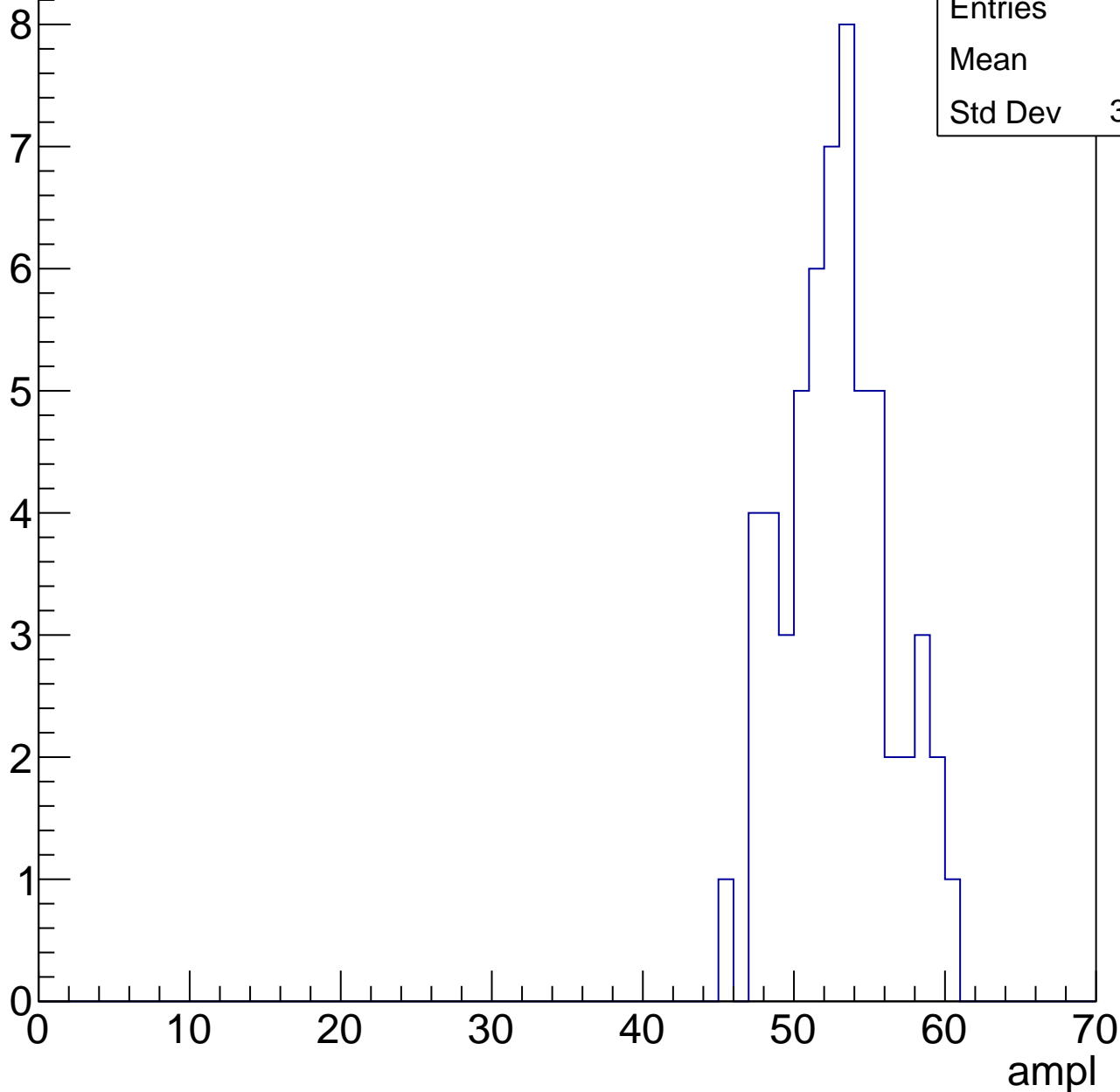


B1L103S, U8-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.4
Std Dev	3.429

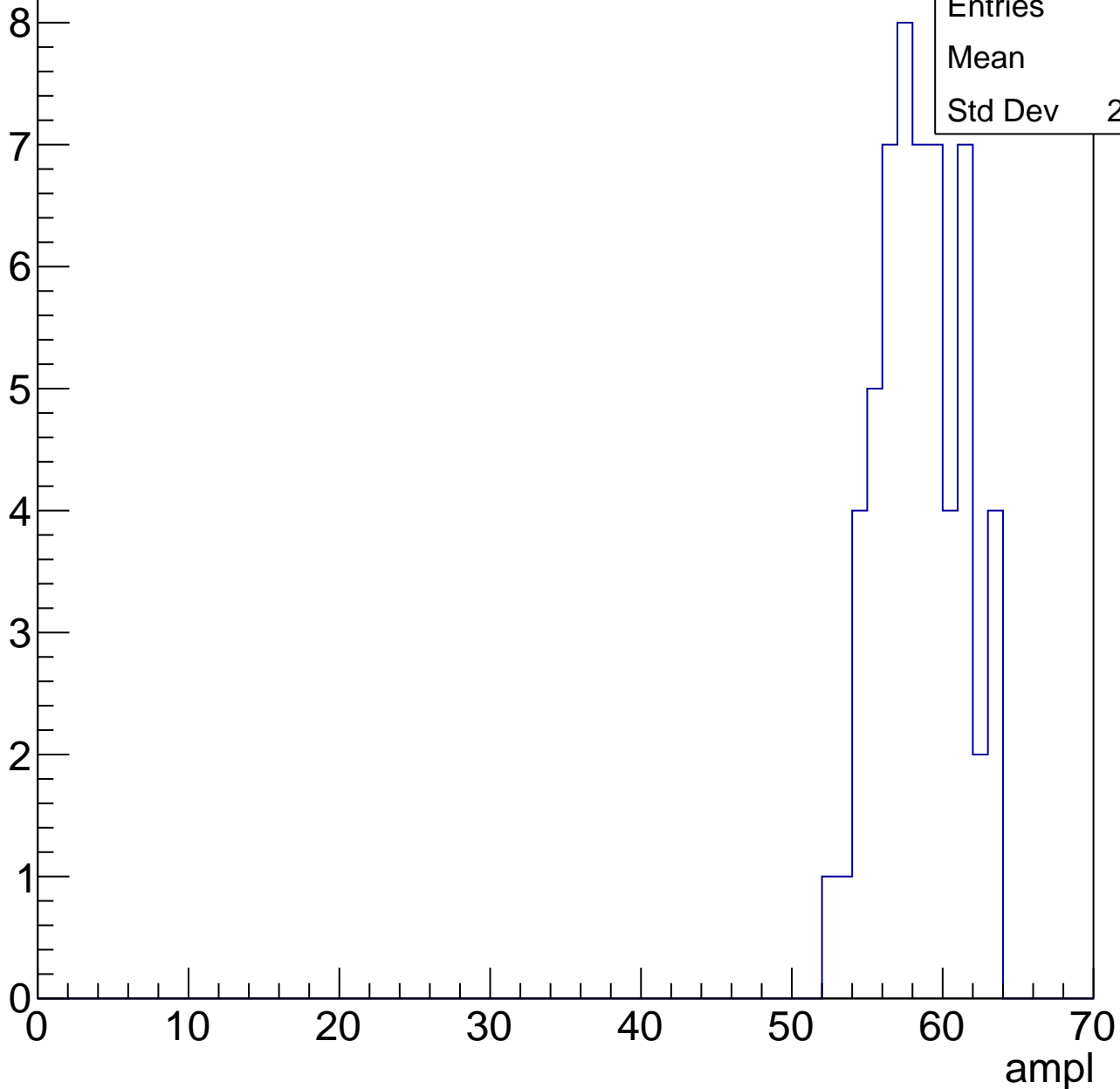


B1L103S, U8-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58
Std Dev	2.727

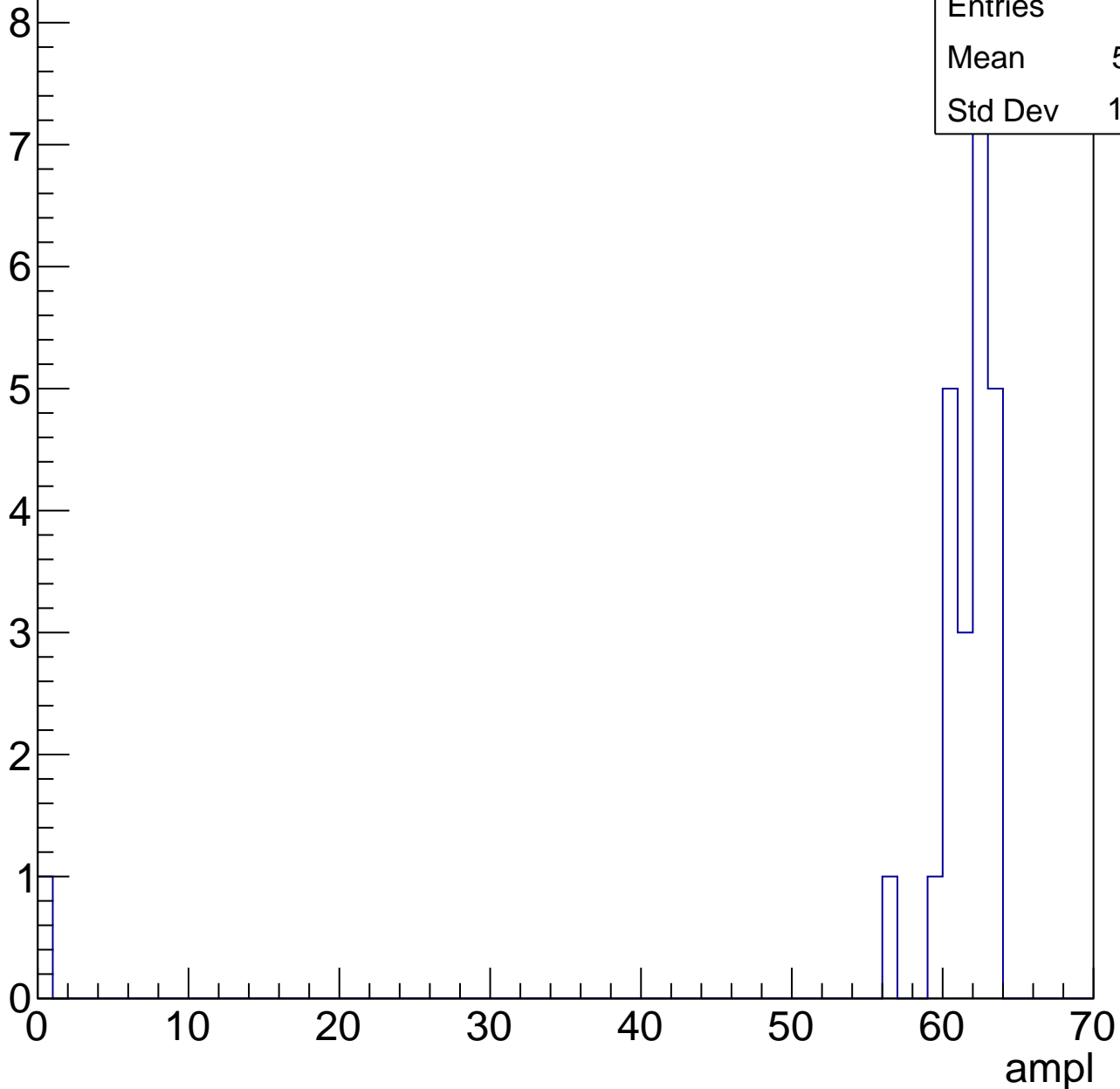


B1L103S, U8-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.71
Std Dev	12.34

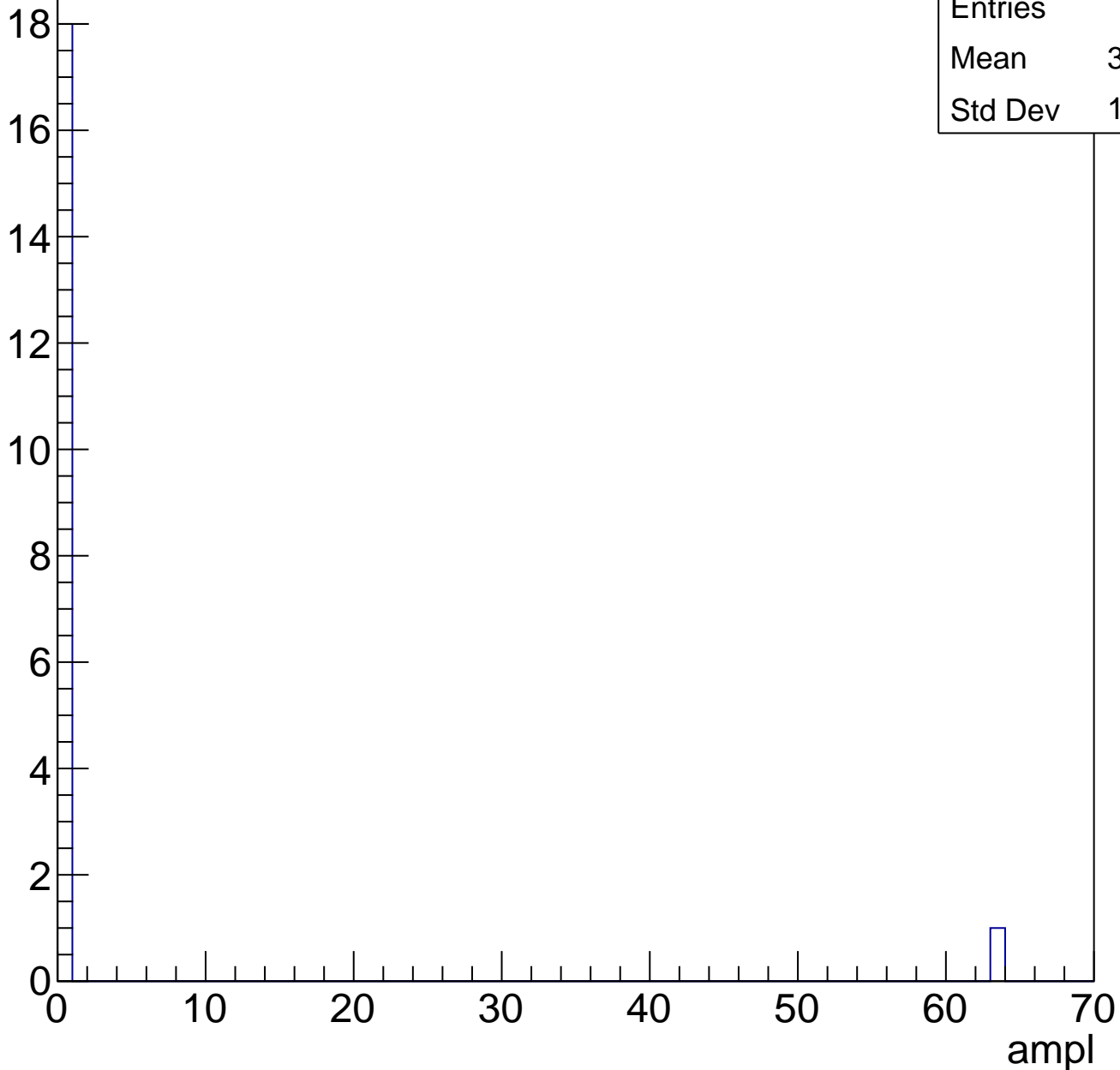


B1L103S, U8-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U8-ch25, adc0

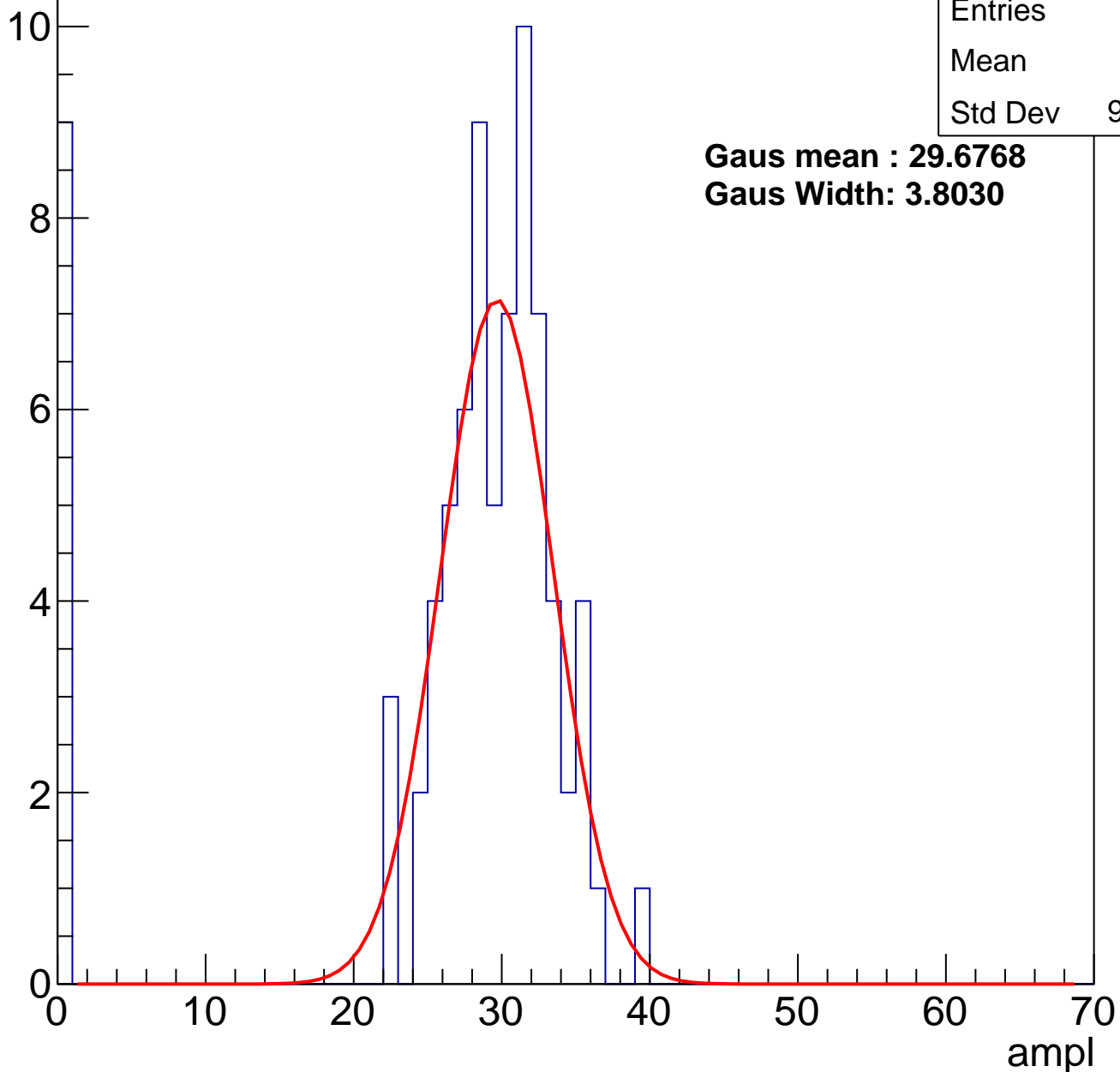
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	26.1
Std Dev	9.912

Gaus mean : 29.6768

Gaus Width: 3.8030

Entry



B1L103S, U8-ch25, adc1

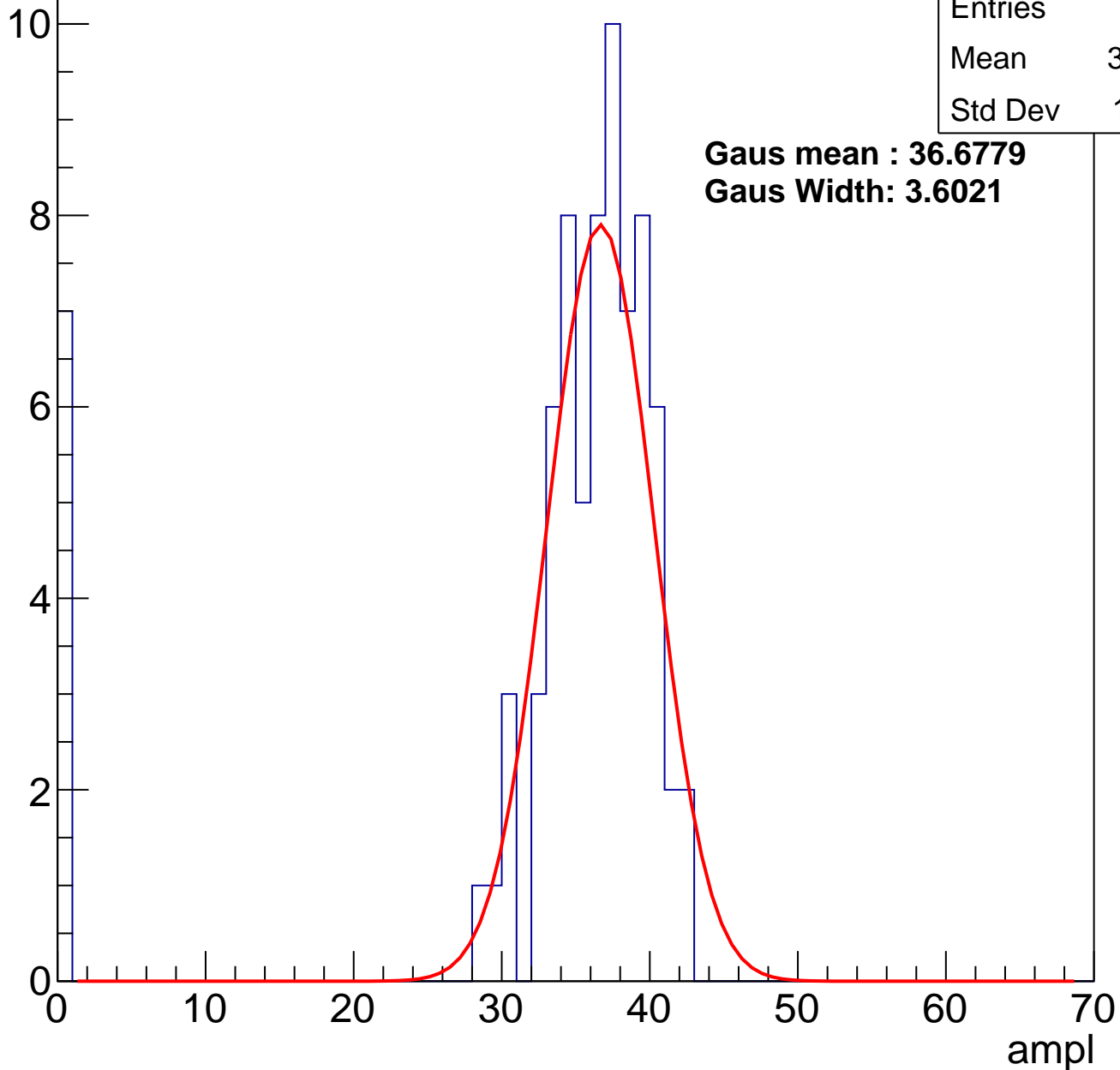
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	32.86
Std Dev	10.81

Gaus mean : 36.6779

Gaus Width: 3.6021

Entry



B1L103S, U8-ch25, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	39.13
Std Dev	12.57

Gaus mean : 43.8316

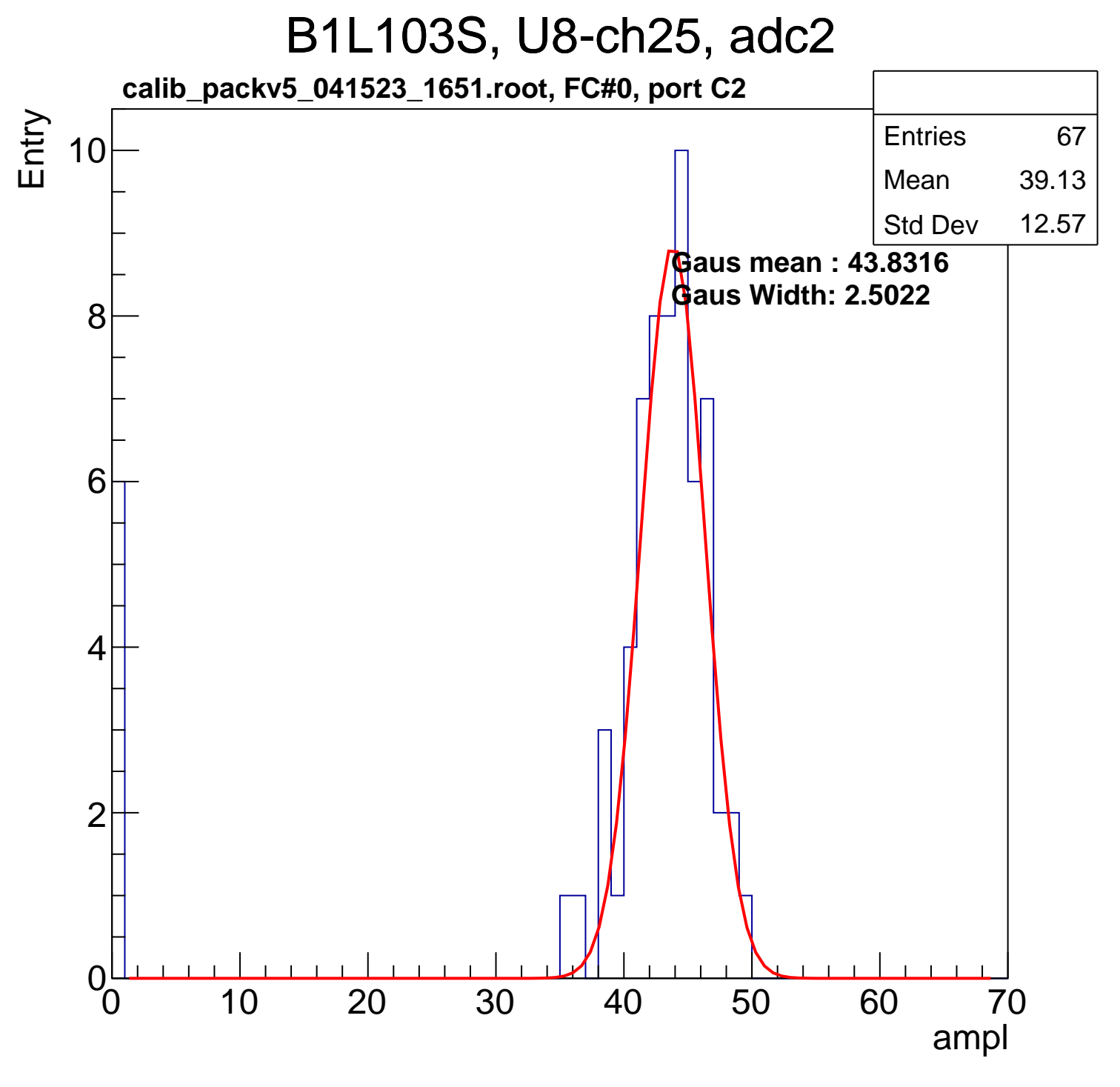
Gaus Width: 2.5022

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

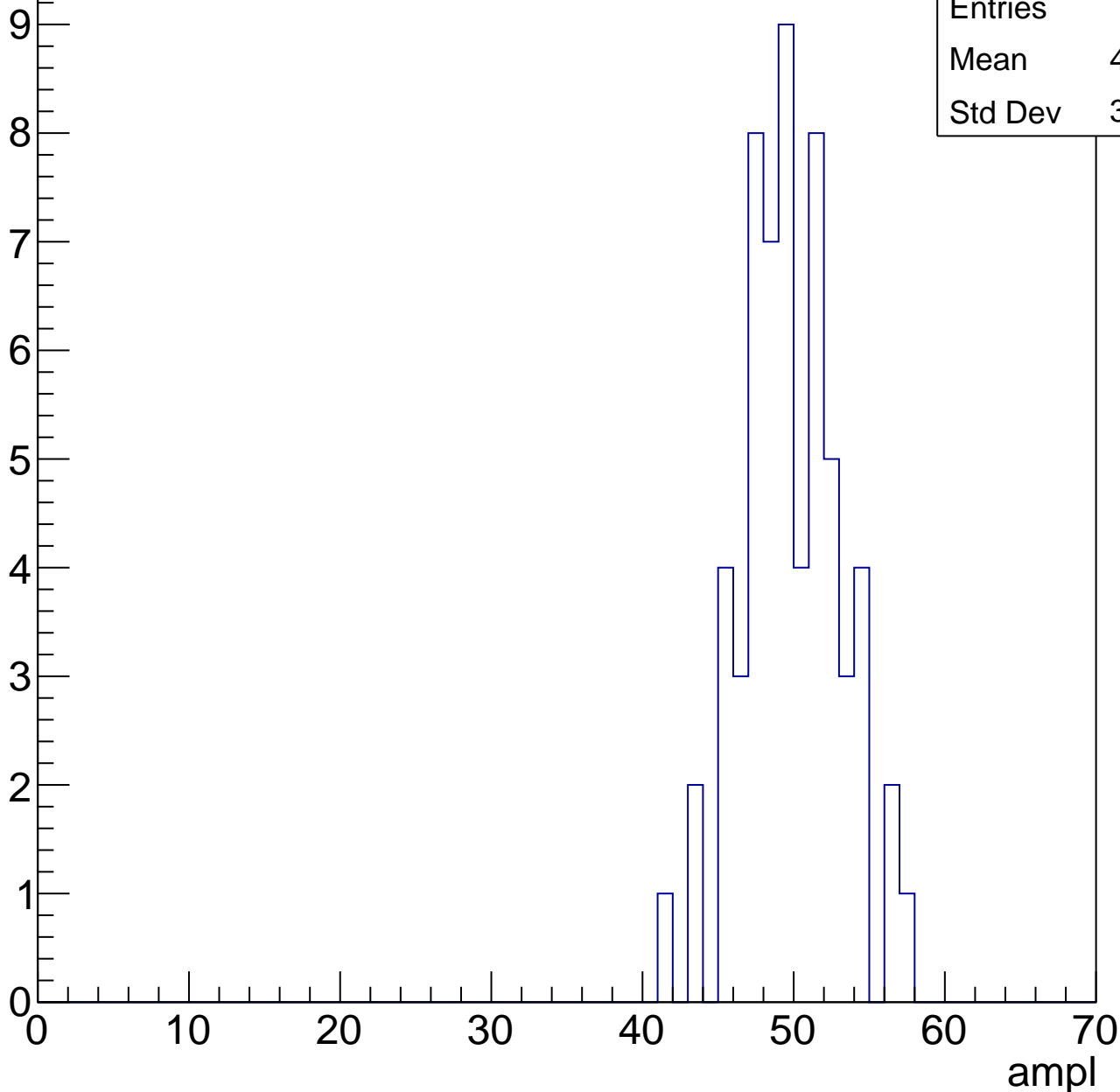


B1L103S, U8-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.34
Std Dev	3.264

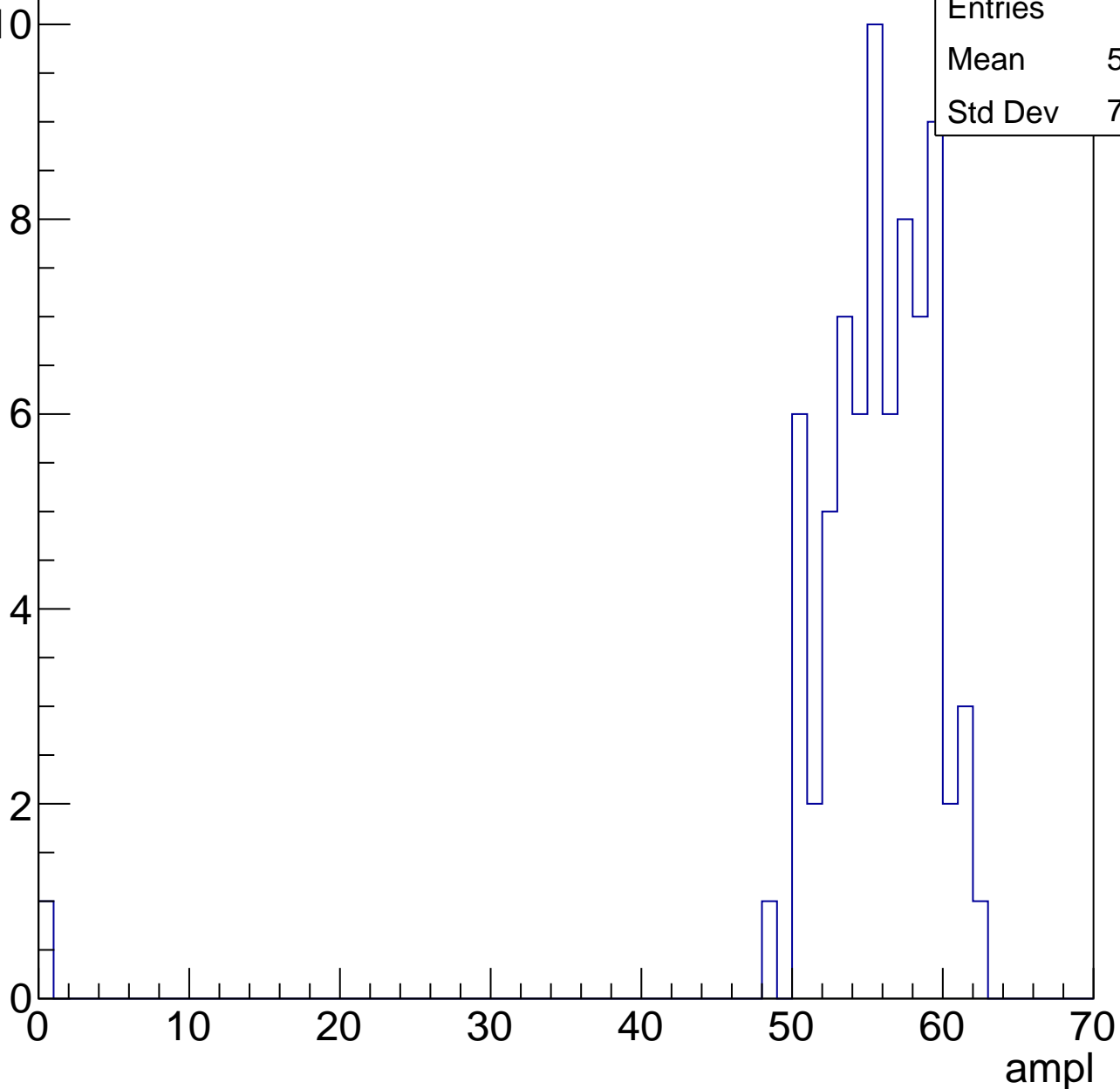


B1L103S, U8-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

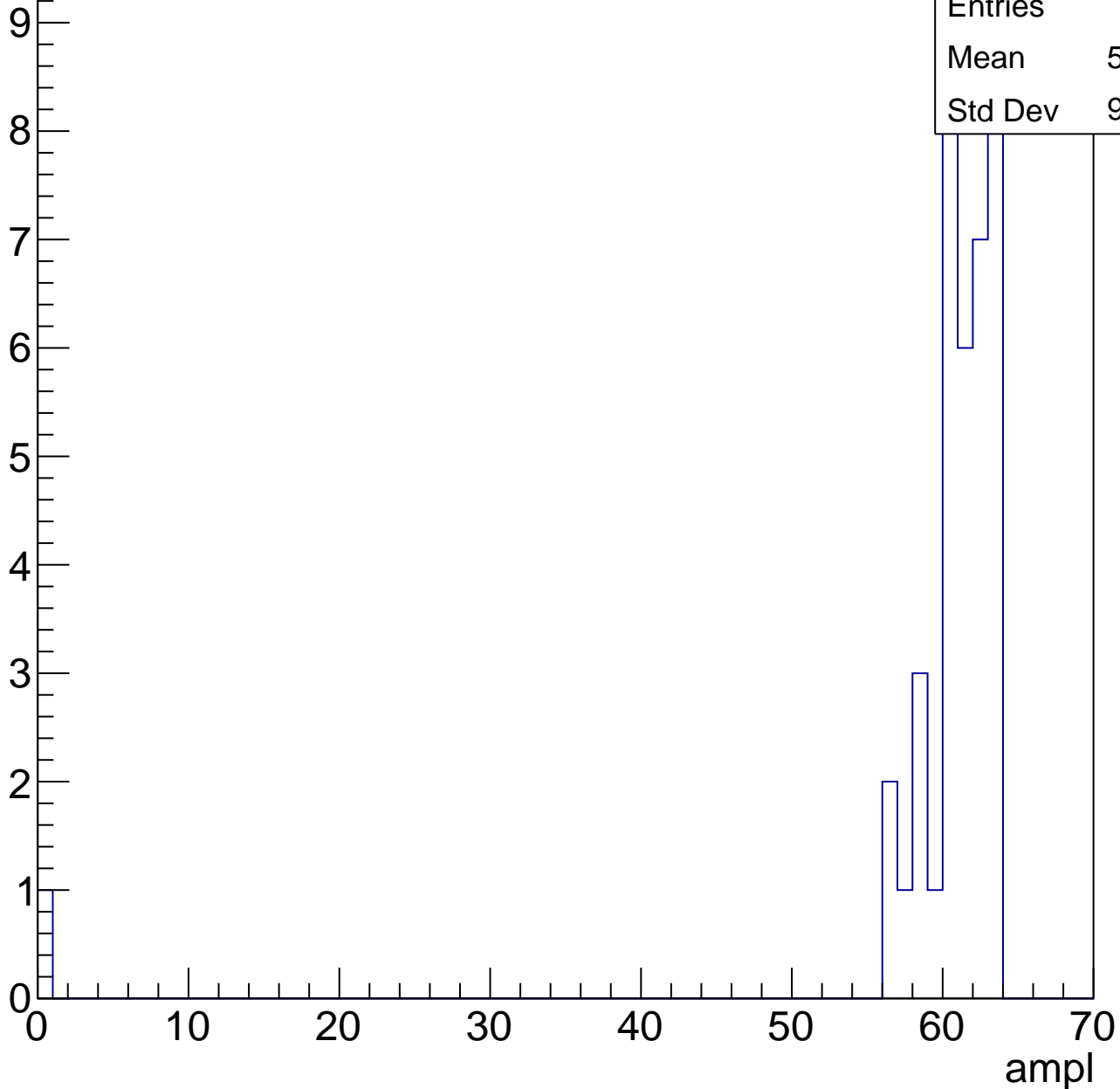
Entries	74
Mean	54.72
Std Dev	7.148



B1L103S, U8-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch26, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	24.3
Std Dev	10.24

Gaus mean : 28.9048

Gaus Width: 3.9606

Entry

10

8

6

4

2

0

0

10

20

30

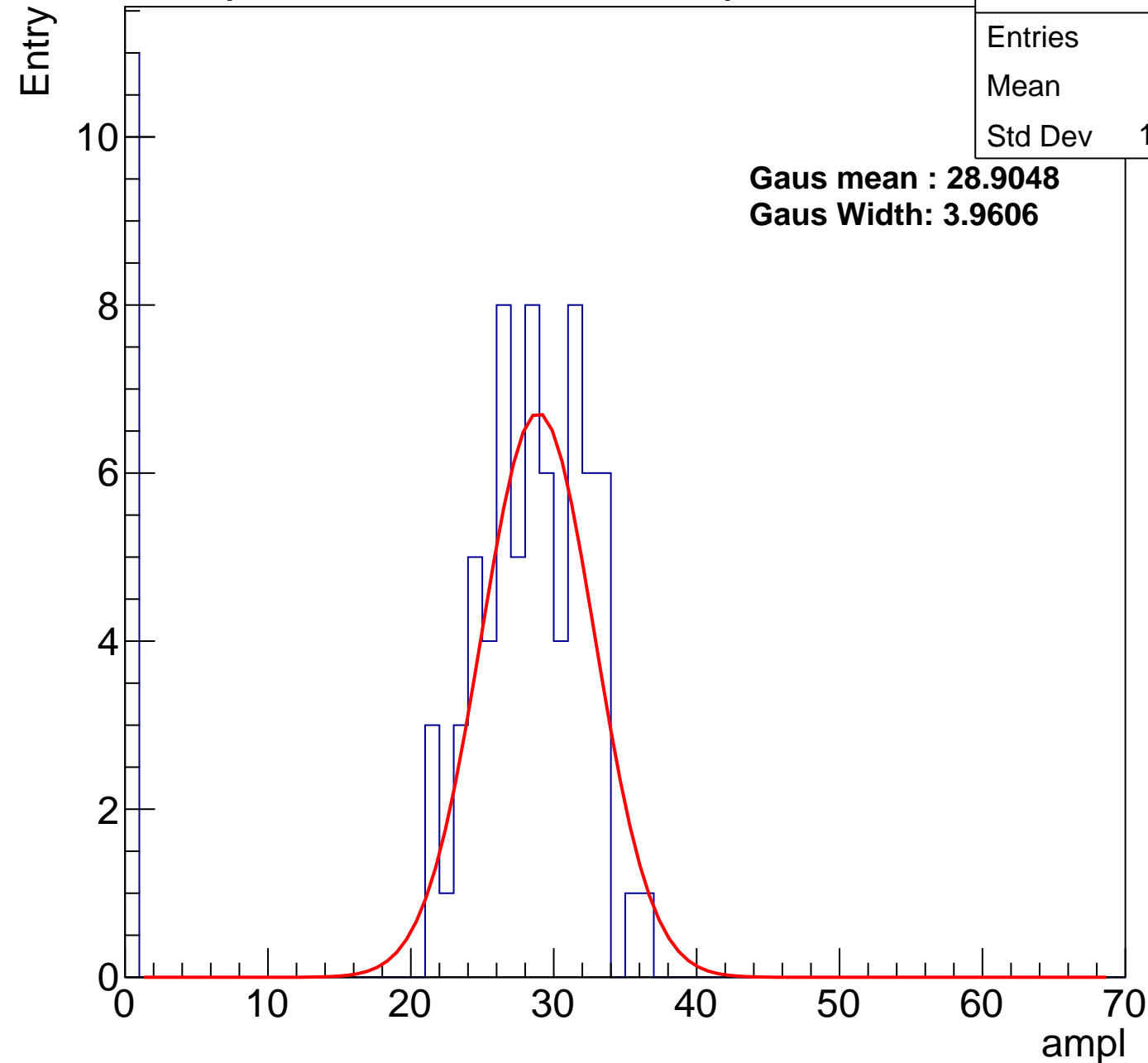
40

50

60

70

ampl



B1L103S, U8-ch26, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	35.07
Std Dev	3.365

Gaus mean : 35.8373

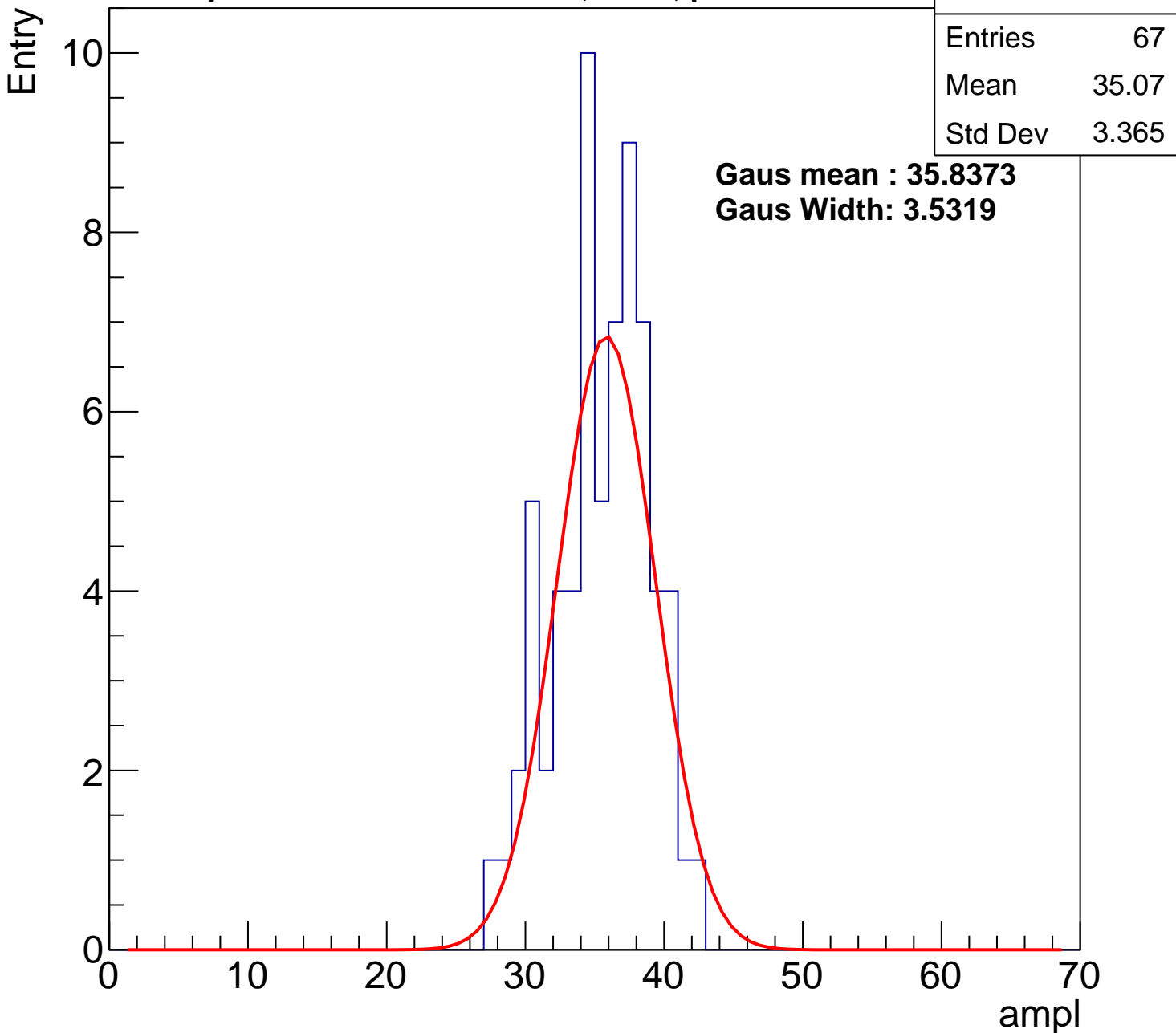
Gaus Width: 3.5319

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch26, adc2

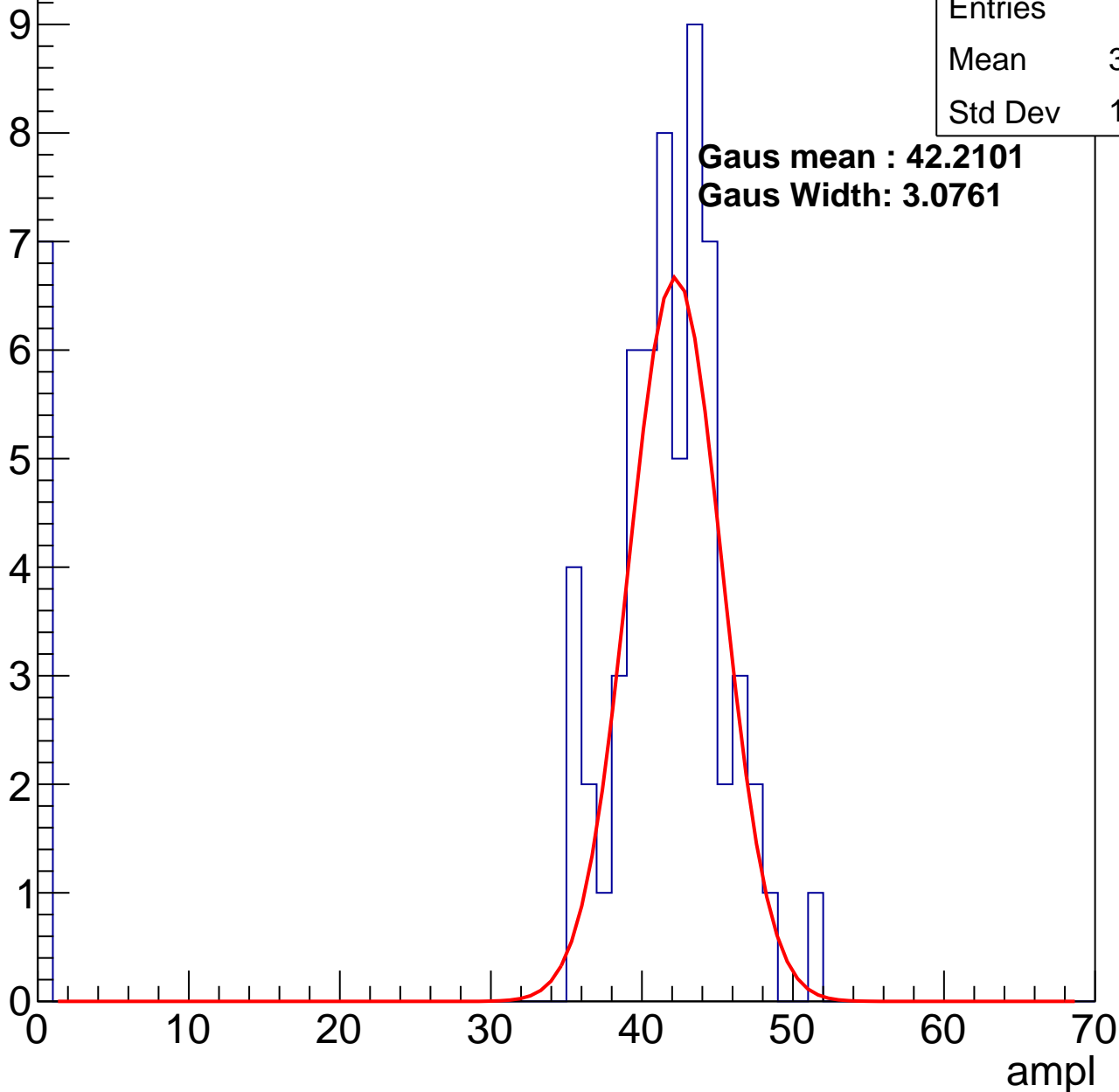
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.18
Std Dev	13.09

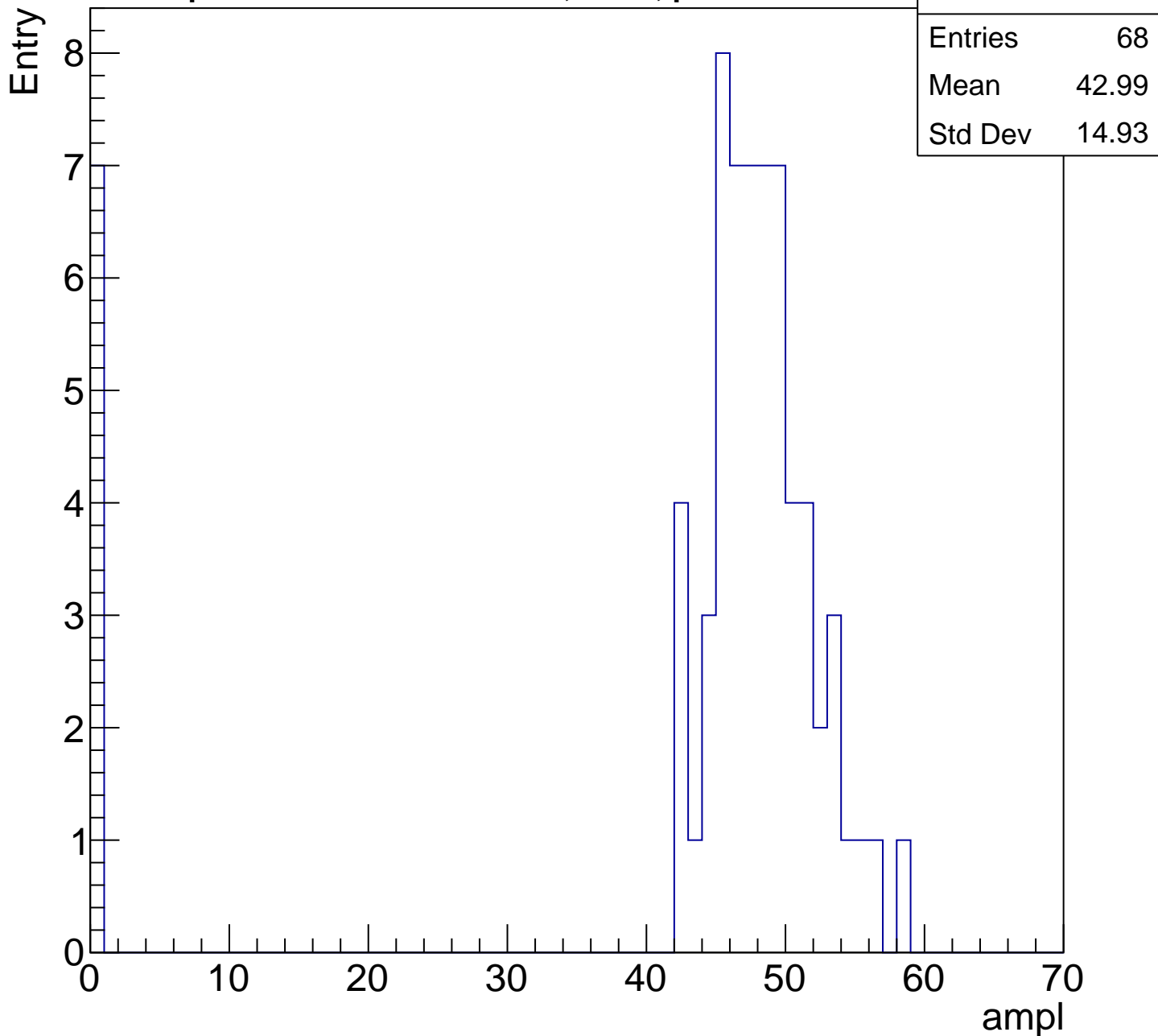
Gaus mean : 42.2101

Gaus Width: 3.0761



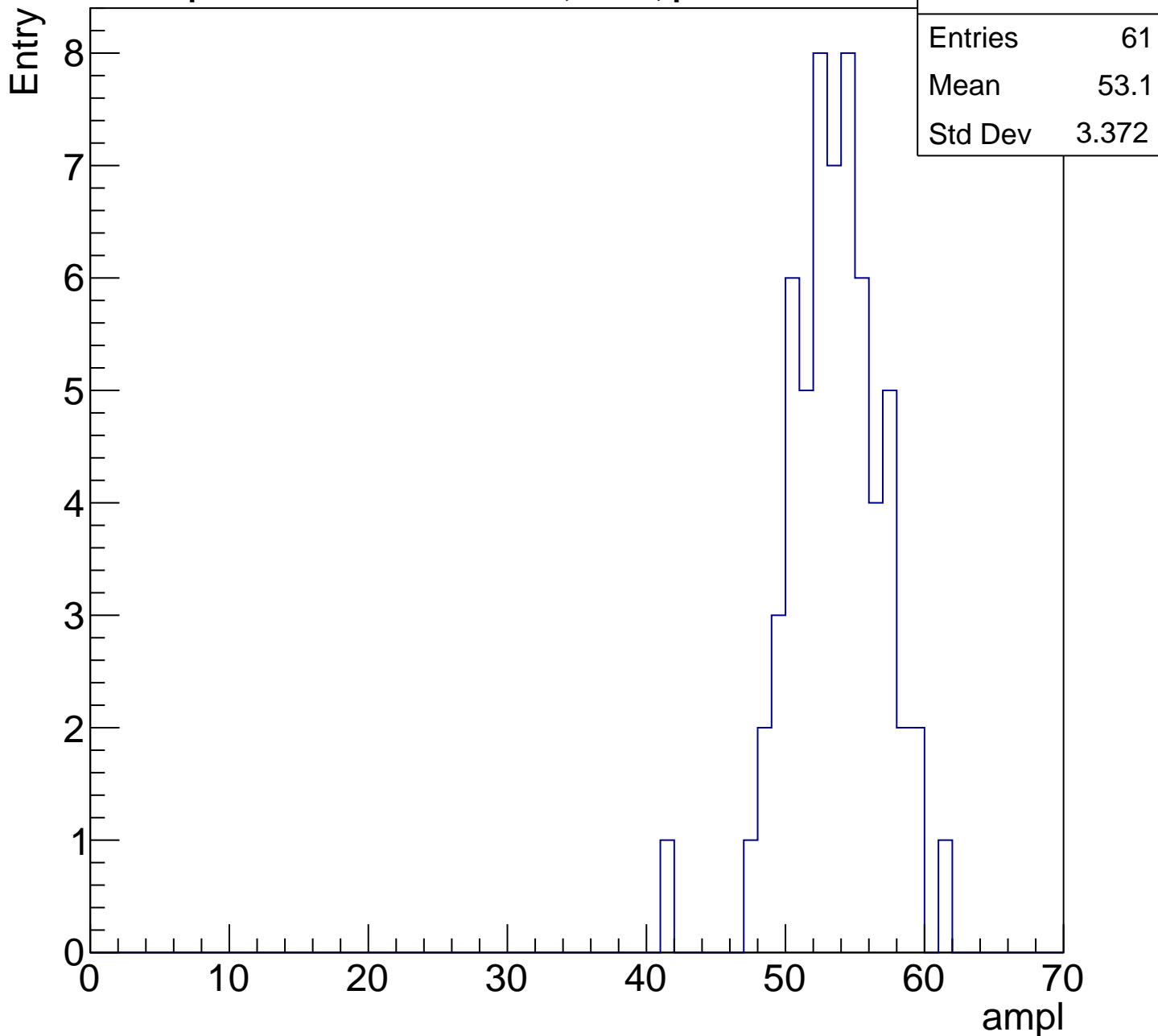
B1L103S, U8-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch26, adc5

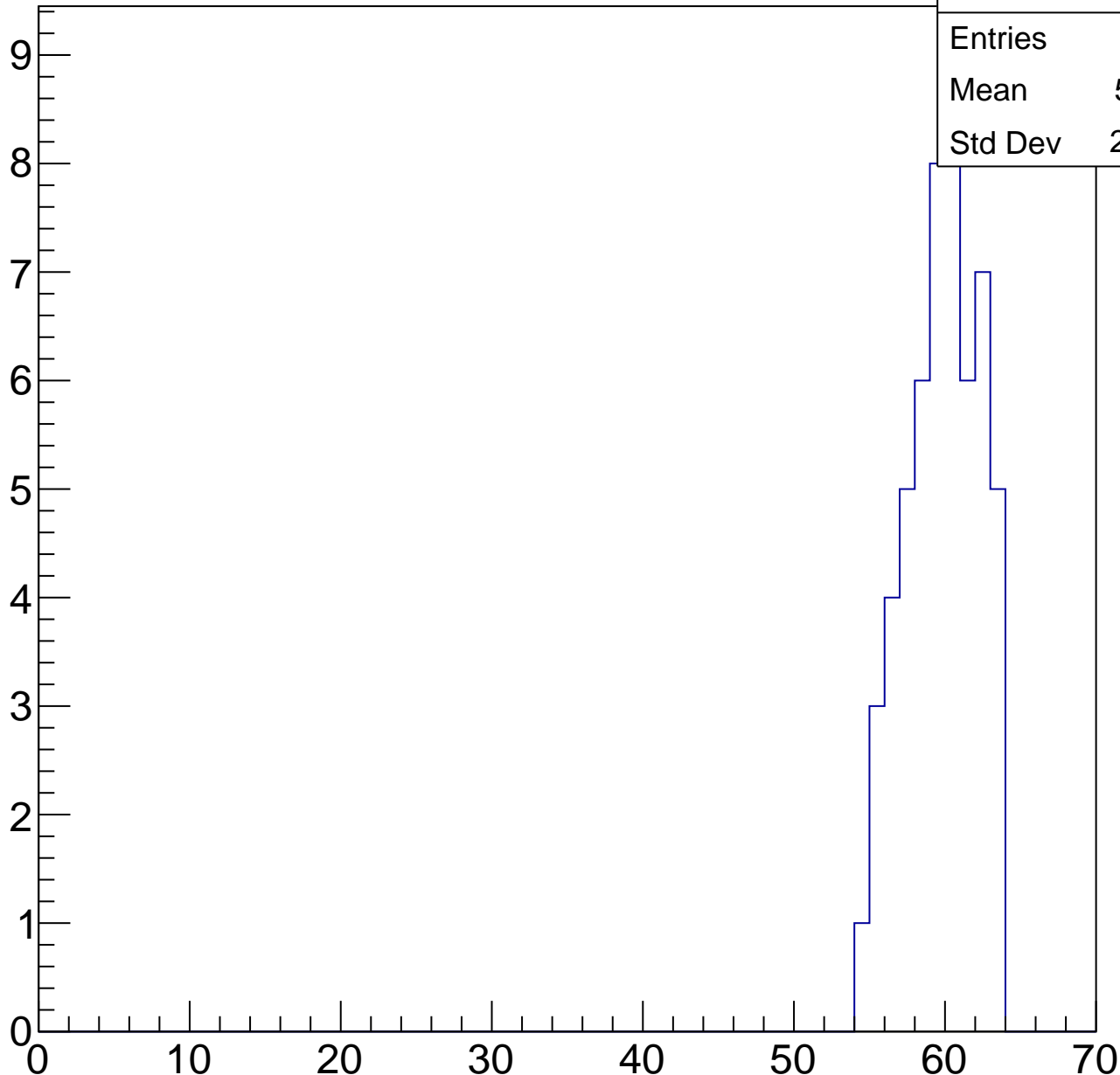
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	59.31
Std Dev	2.379

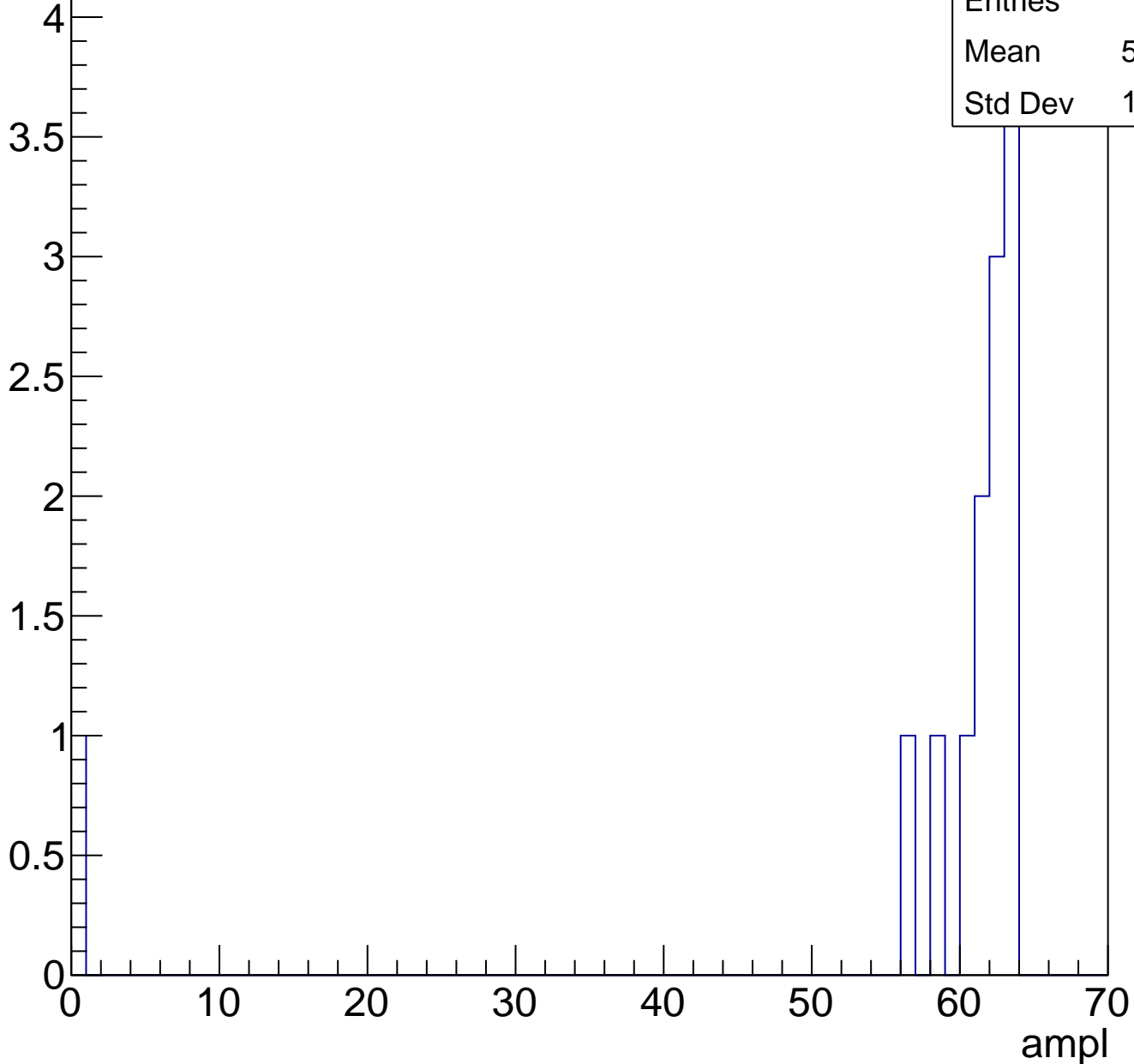
ampl



B1L103S, U8-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

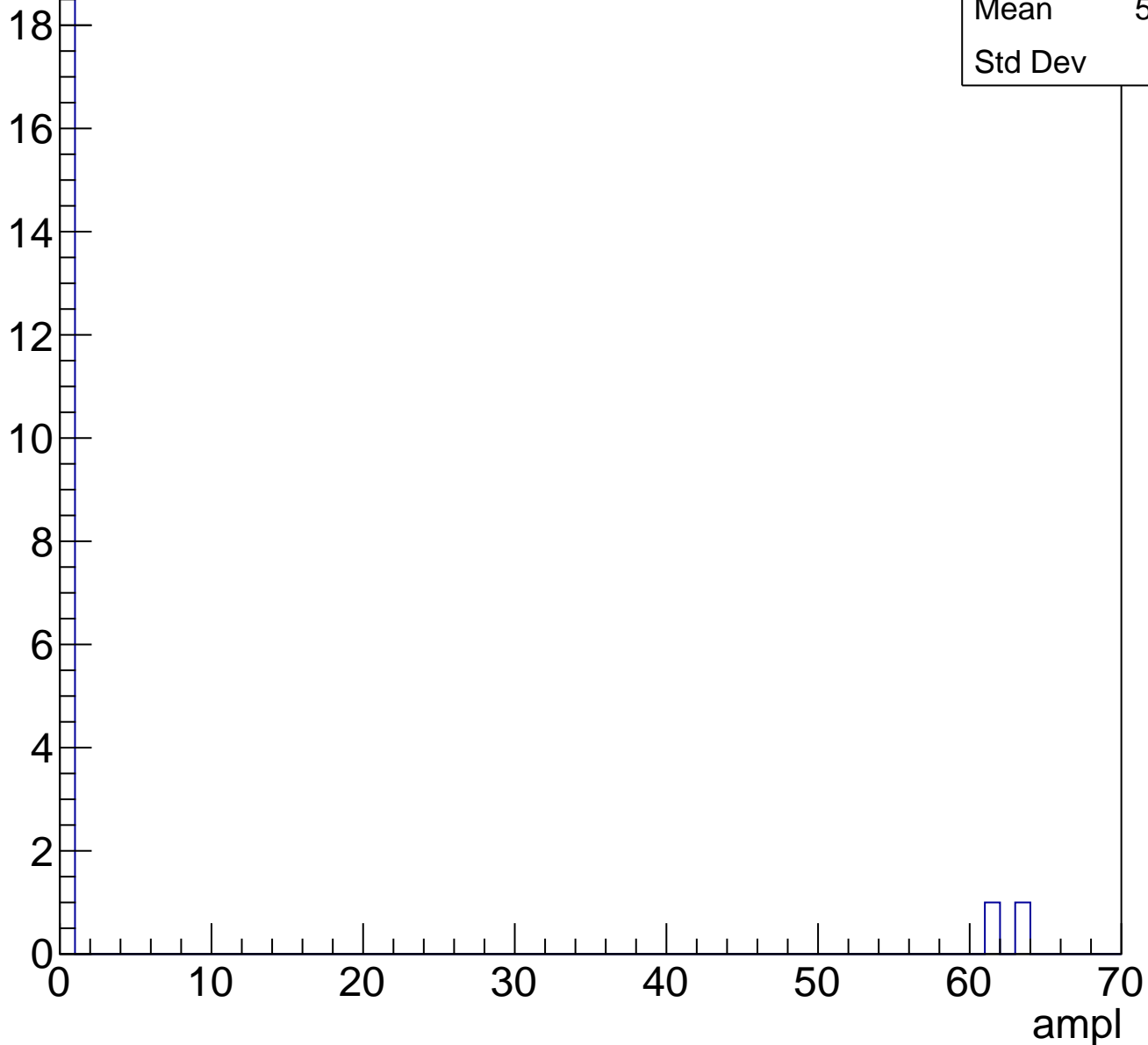
Entry



B1L103S, U8-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	5.905
Std Dev	18.2

B1L103S, U8-ch27, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	25.33
Std Dev	11.25

Gaus mean : 30.4496

Gaus Width: 3.6842

Entry

12

10

8

6

4

2

0

0

10

20

30

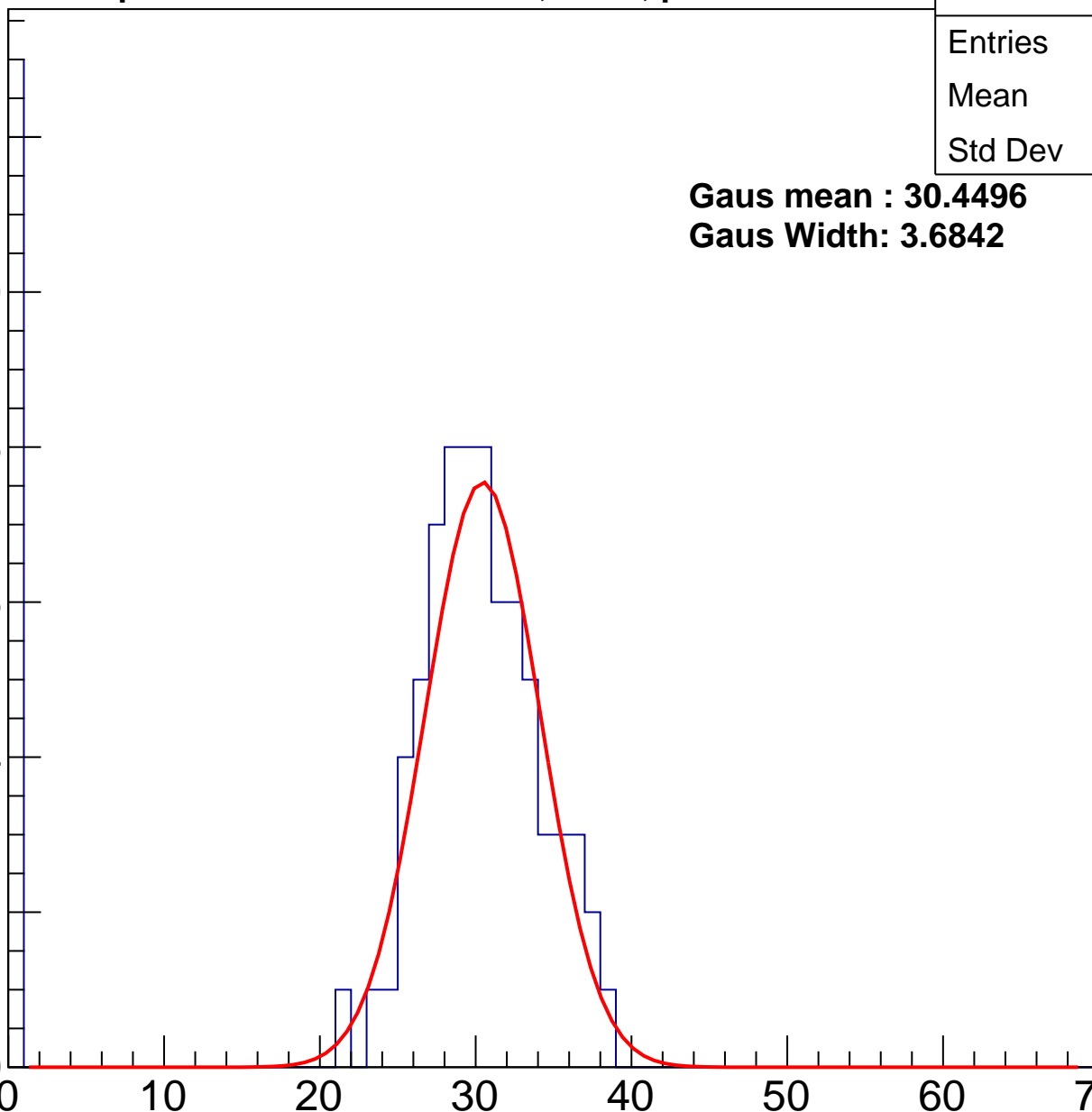
40

50

60

70

ampl



B1L103S, U8-ch27, adc1

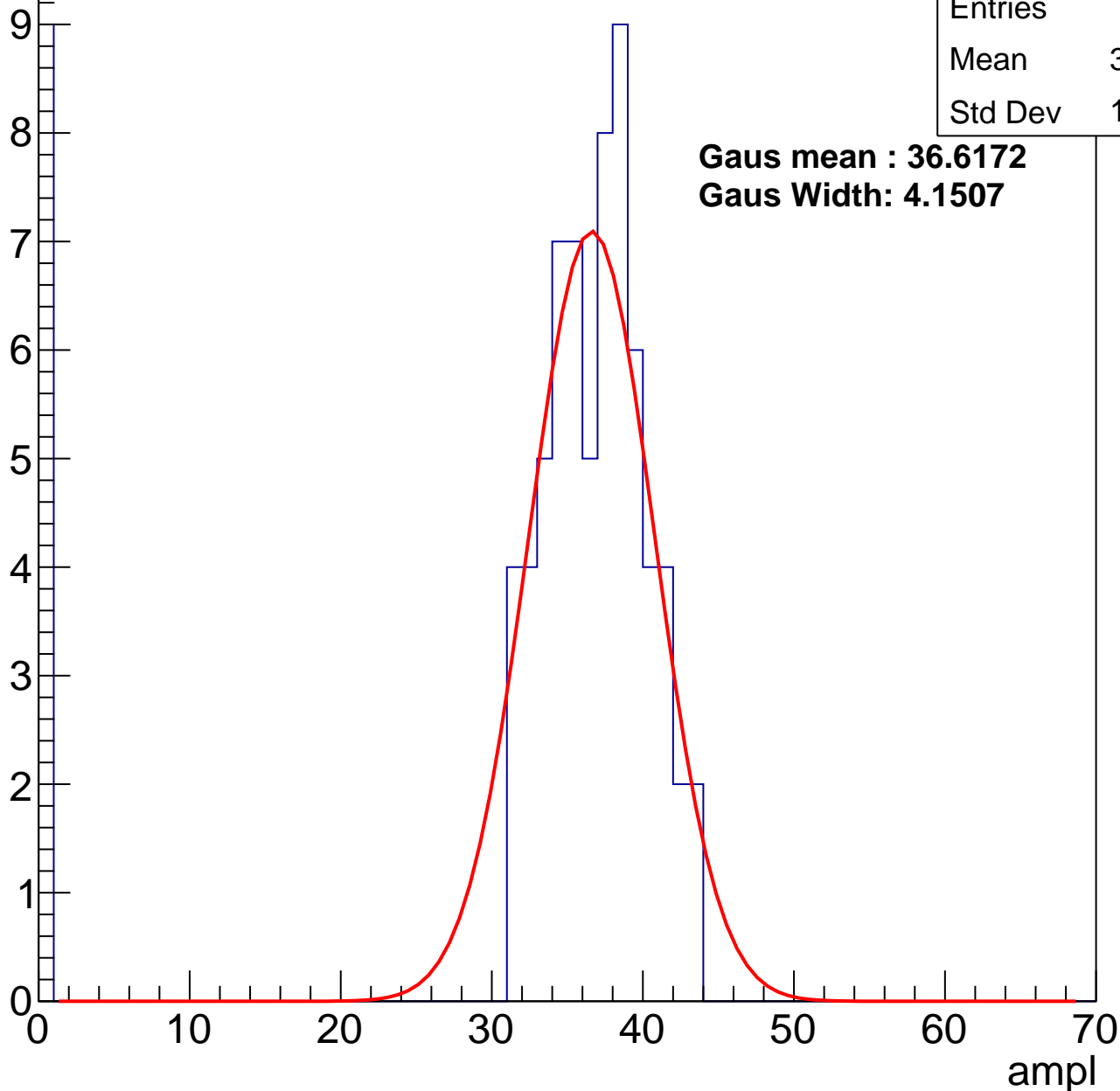
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	32.18
Std Dev	12.16

Gaus mean : 36.6172

Gaus Width: 4.1507



B1L103S, U8-ch27, adc2

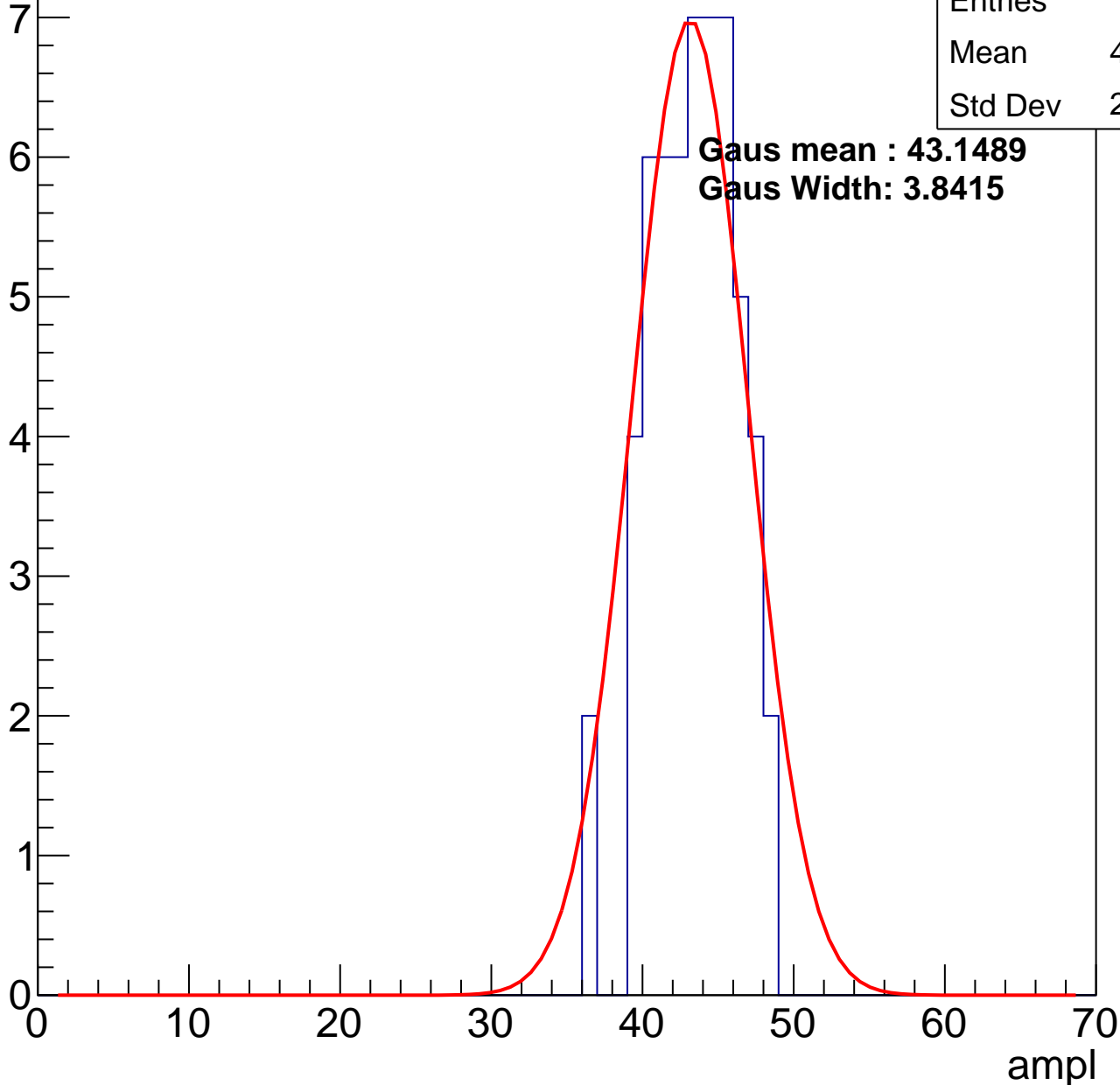
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	42.93
Std Dev	2.802

Gaus mean : 43.1489

Gaus Width: 3.8415

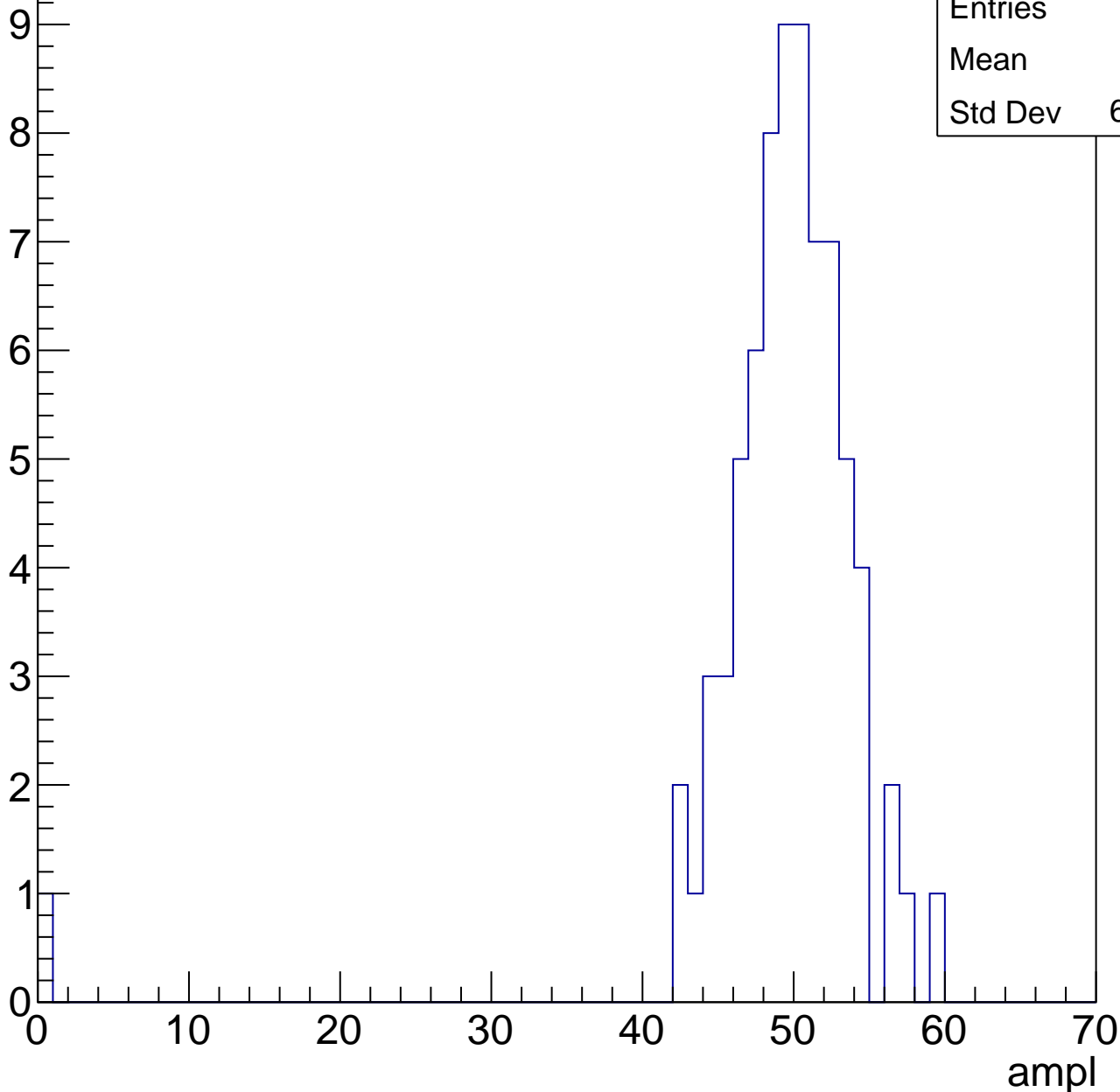


B1L103S, U8-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	48.8
Std Dev	6.652



B1L103S, U8-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	56
Mean	55.46
Std Dev	2.745

Entry

10

8

6

4

2

0

0

10

20

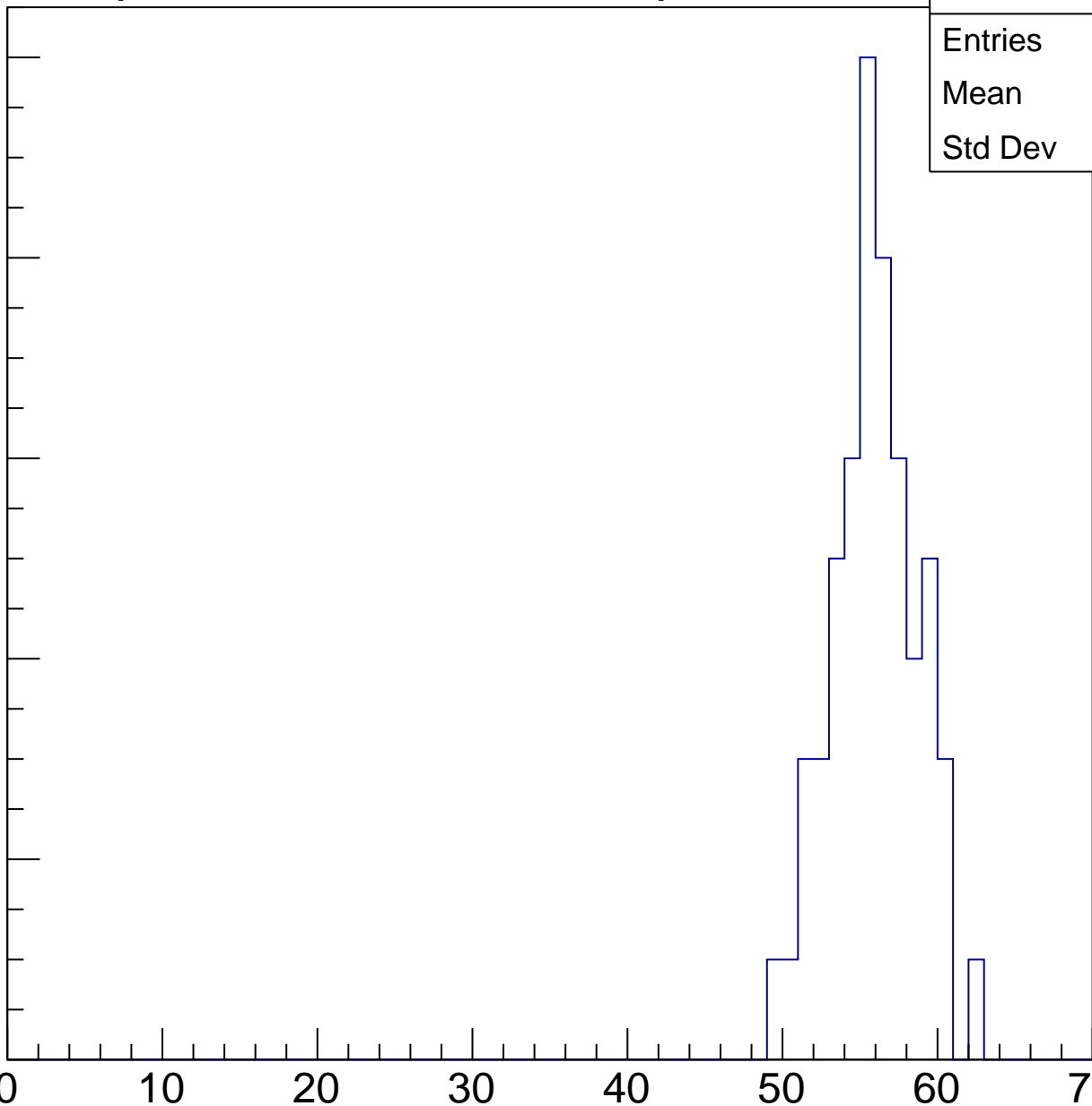
30

40

50

60

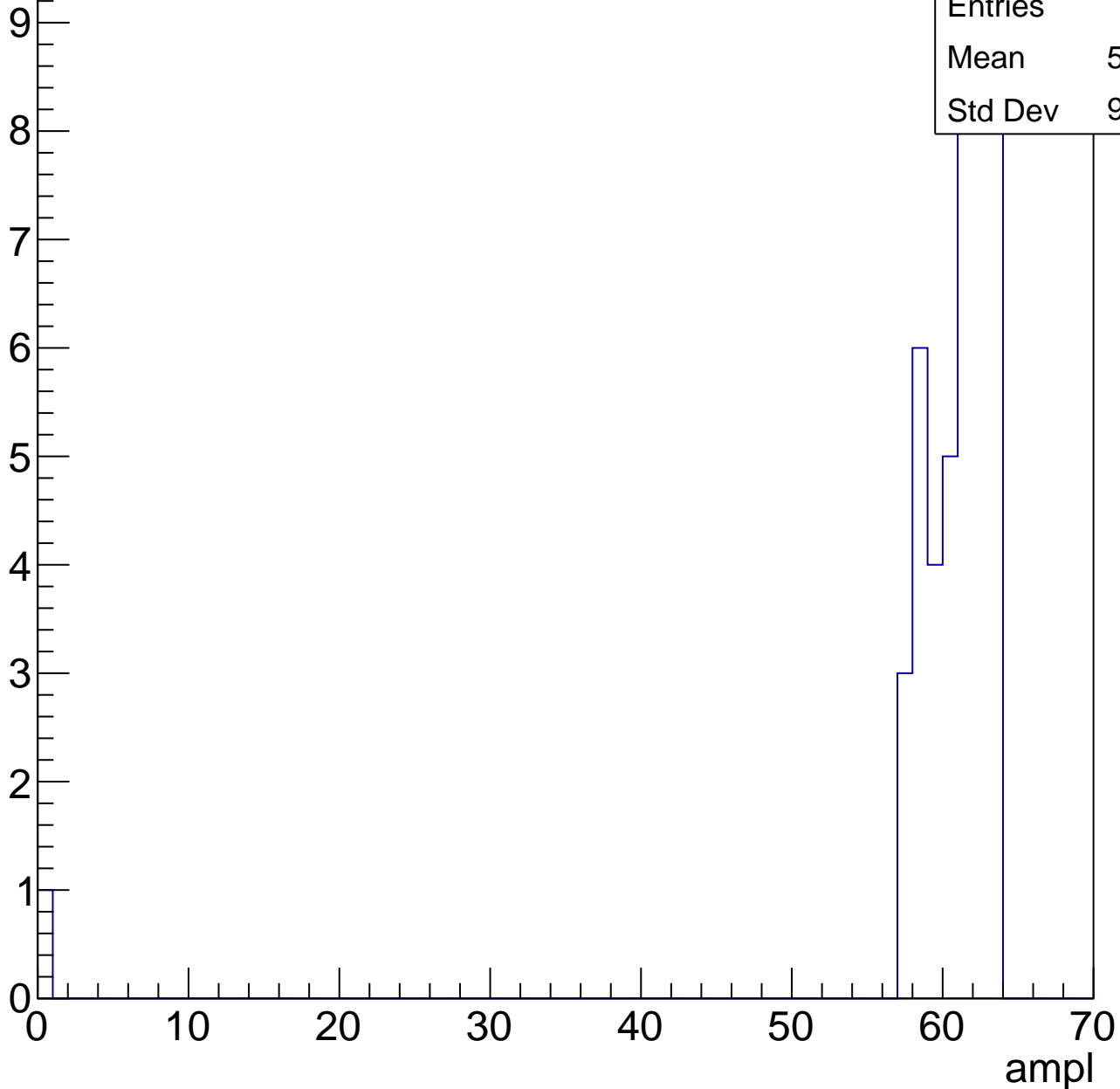
ampl



B1L103S, U8-ch27, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.166

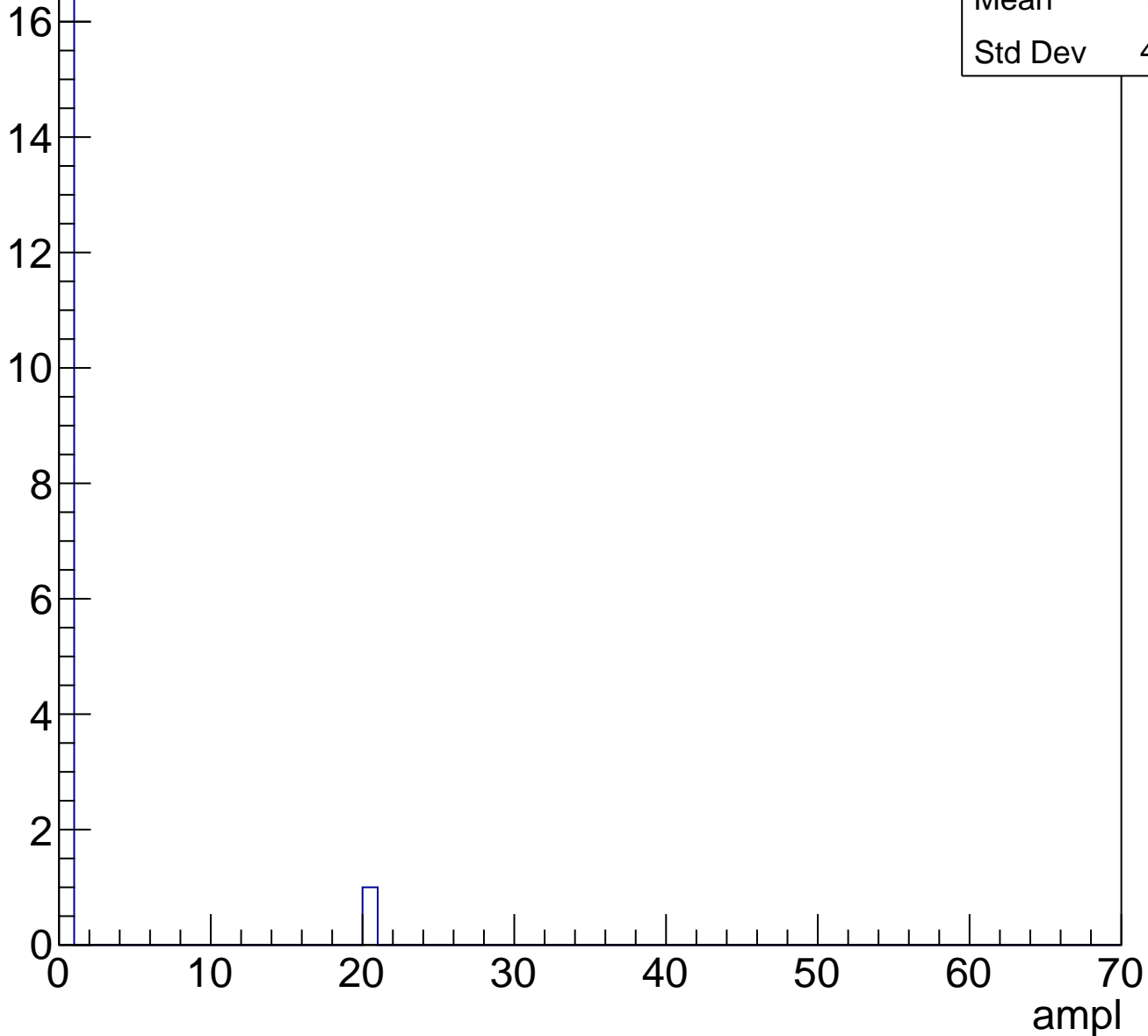
ampl

0 10 20 30 40 50 60 70

B1L103S, U8-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	1.111
Std Dev	4.581

B1L103S, U8-ch28, adc0

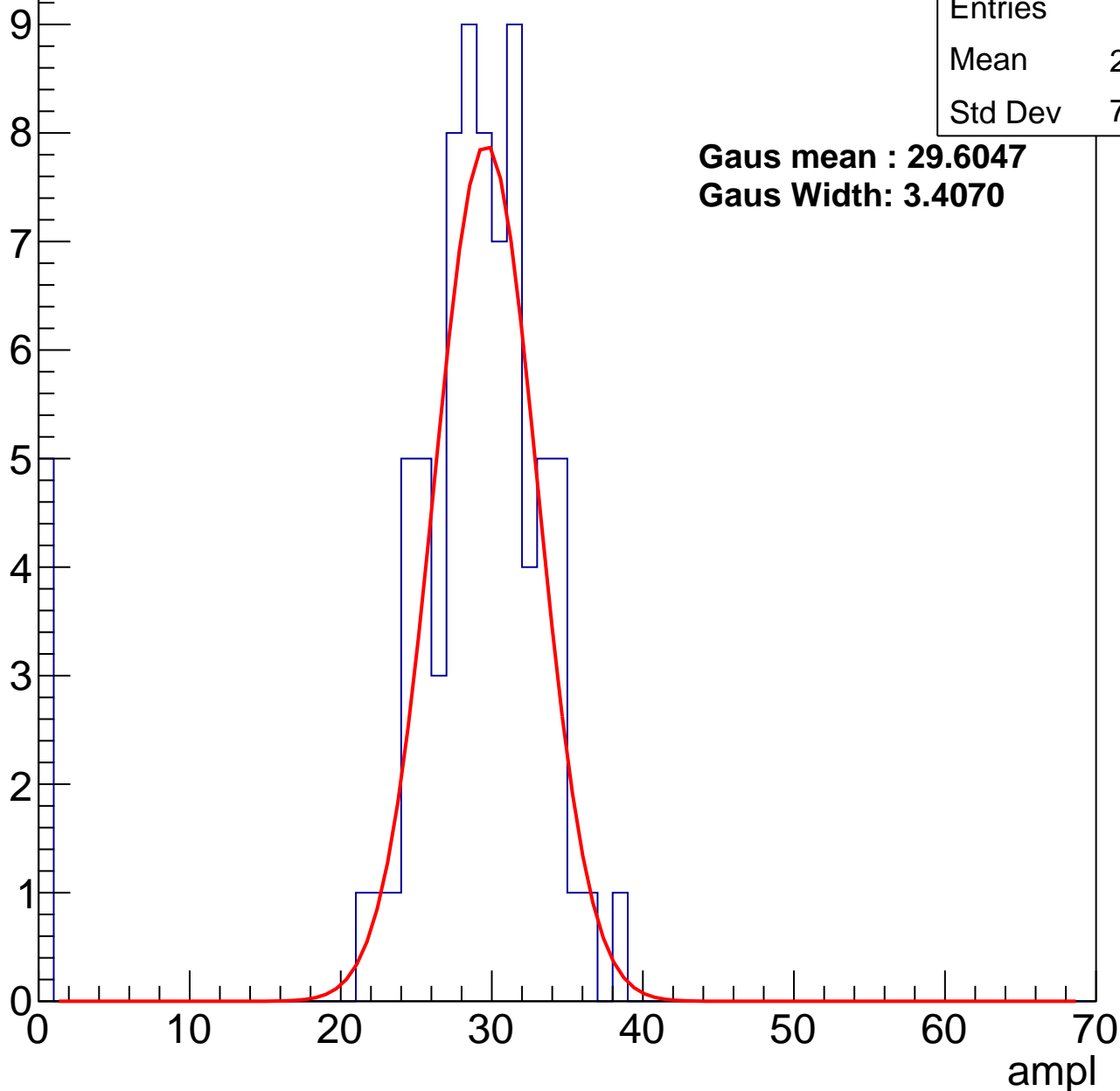
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	27.22
Std Dev	7.815

Gaus mean : 29.6047

Gaus Width: 3.4070



B1L103S, U8-ch28, adc1

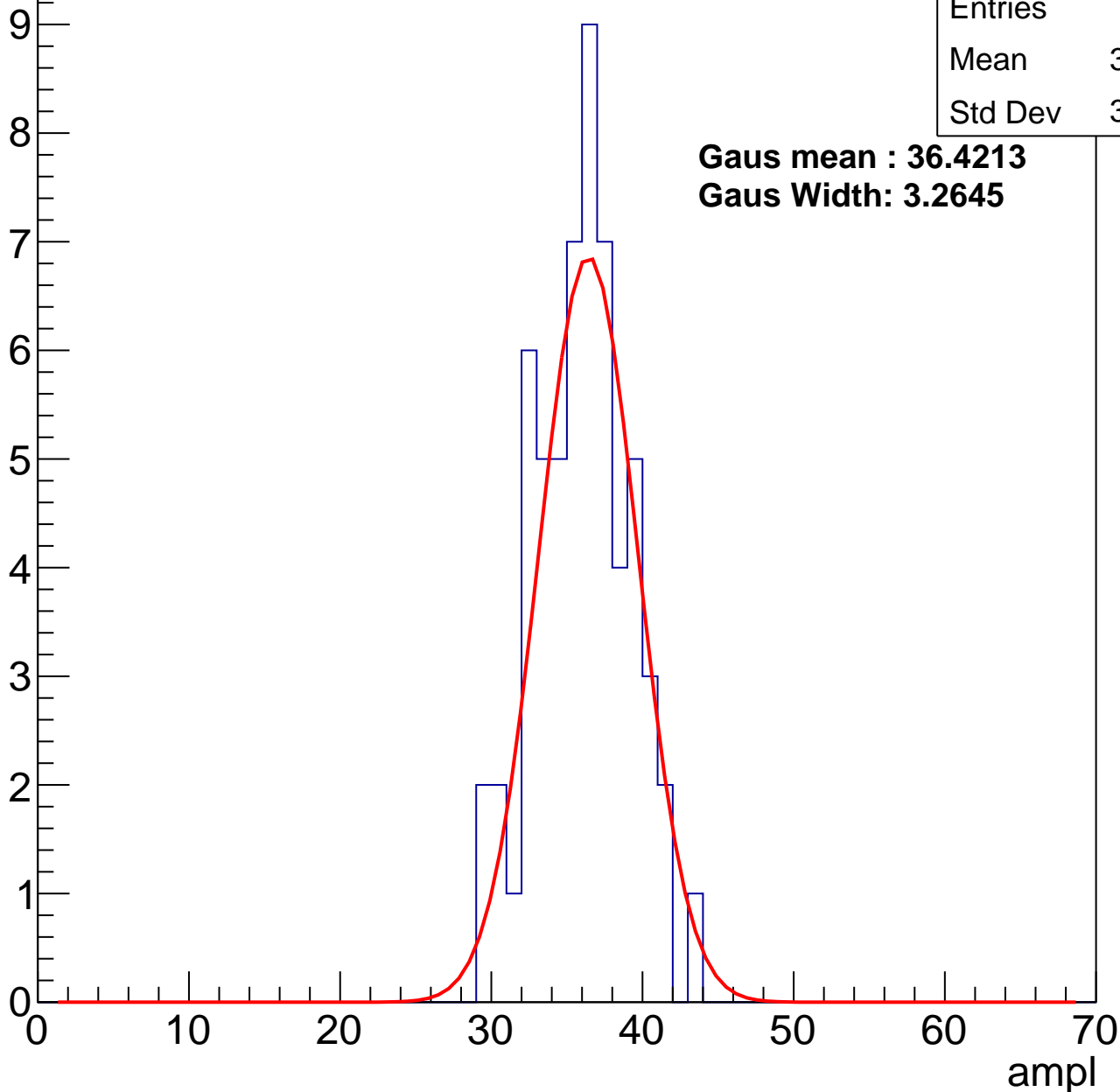
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.53
Std Dev	3.094

Gaus mean : 36.4213

Gaus Width: 3.2645



B1L103S, U8-ch28, adc2

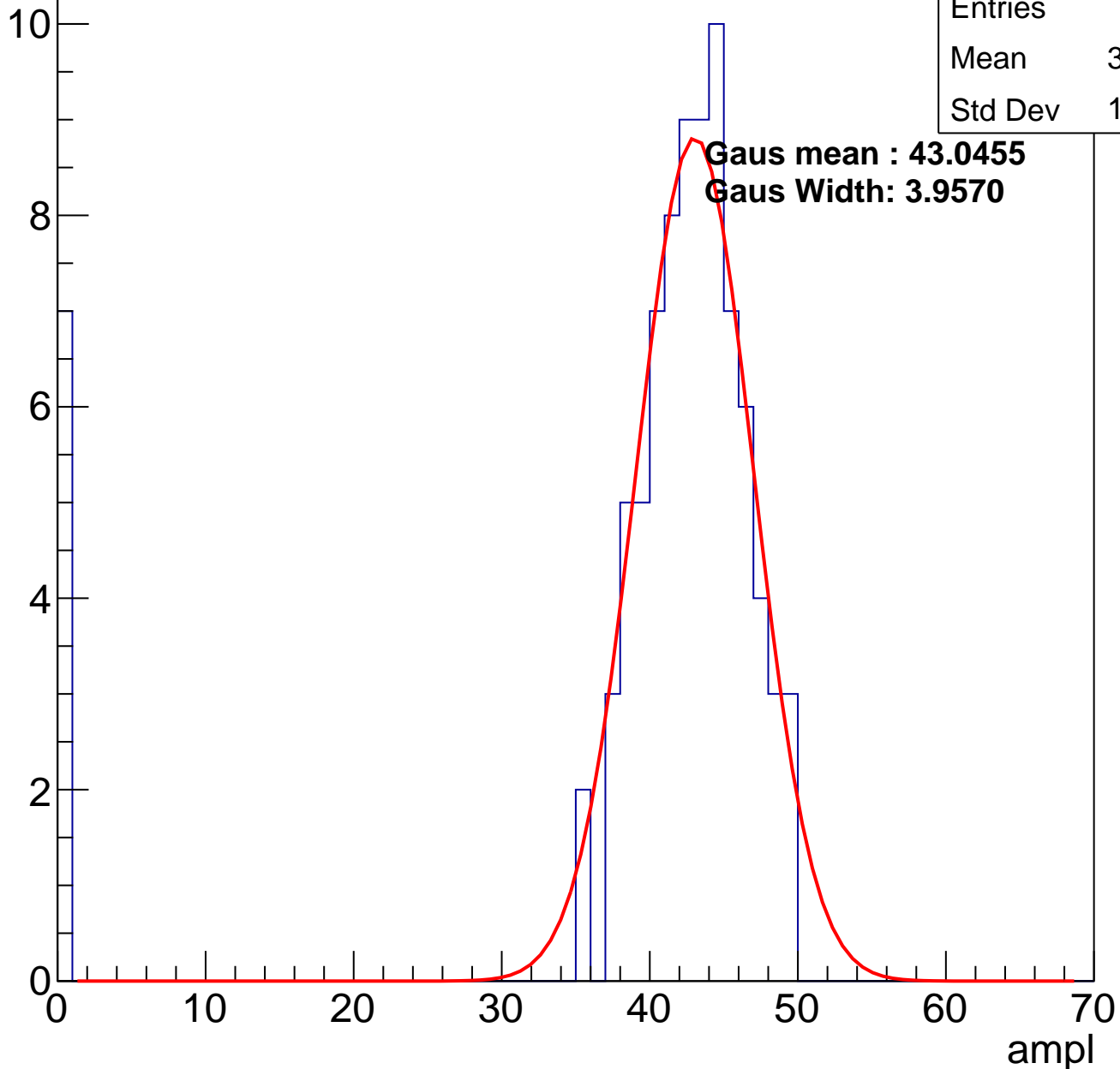
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	39.19
Std Dev	11.94

Gaus mean : 43.0455

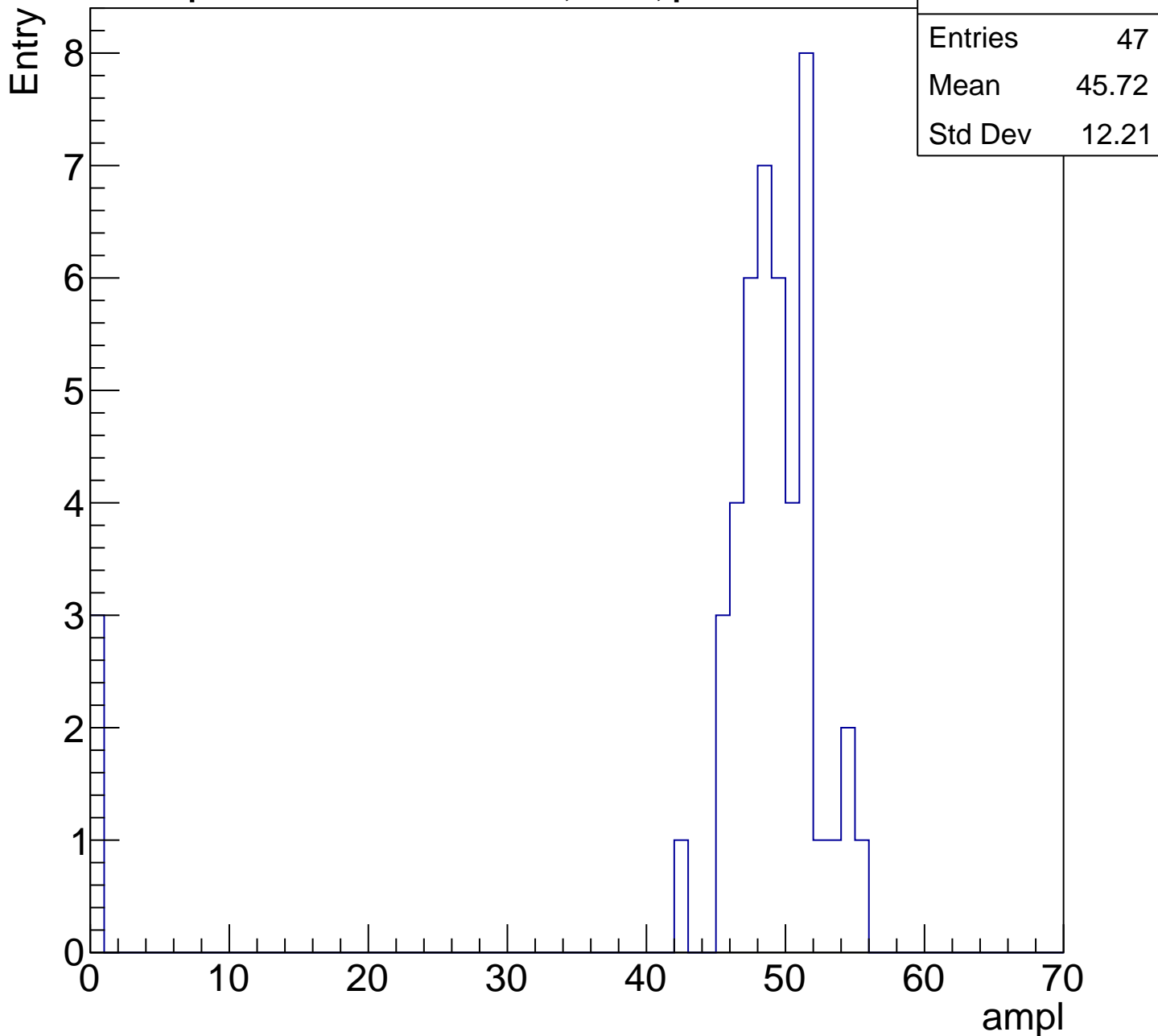
Gaus Width: 3.9570

Entry



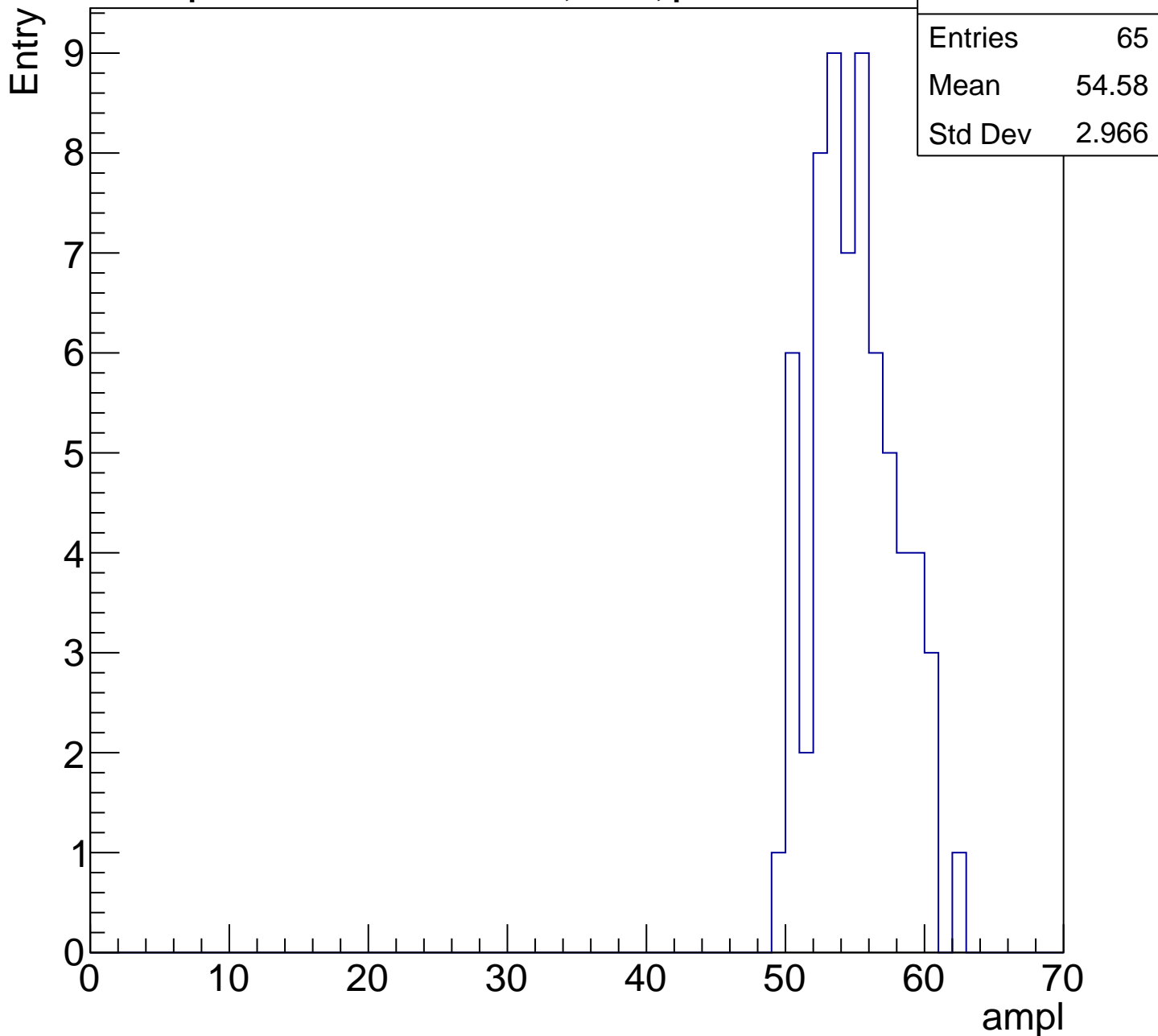
B1L103S, U8-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch28, adc4

calib_packv5_041523_1651.root, FC#0, port C2

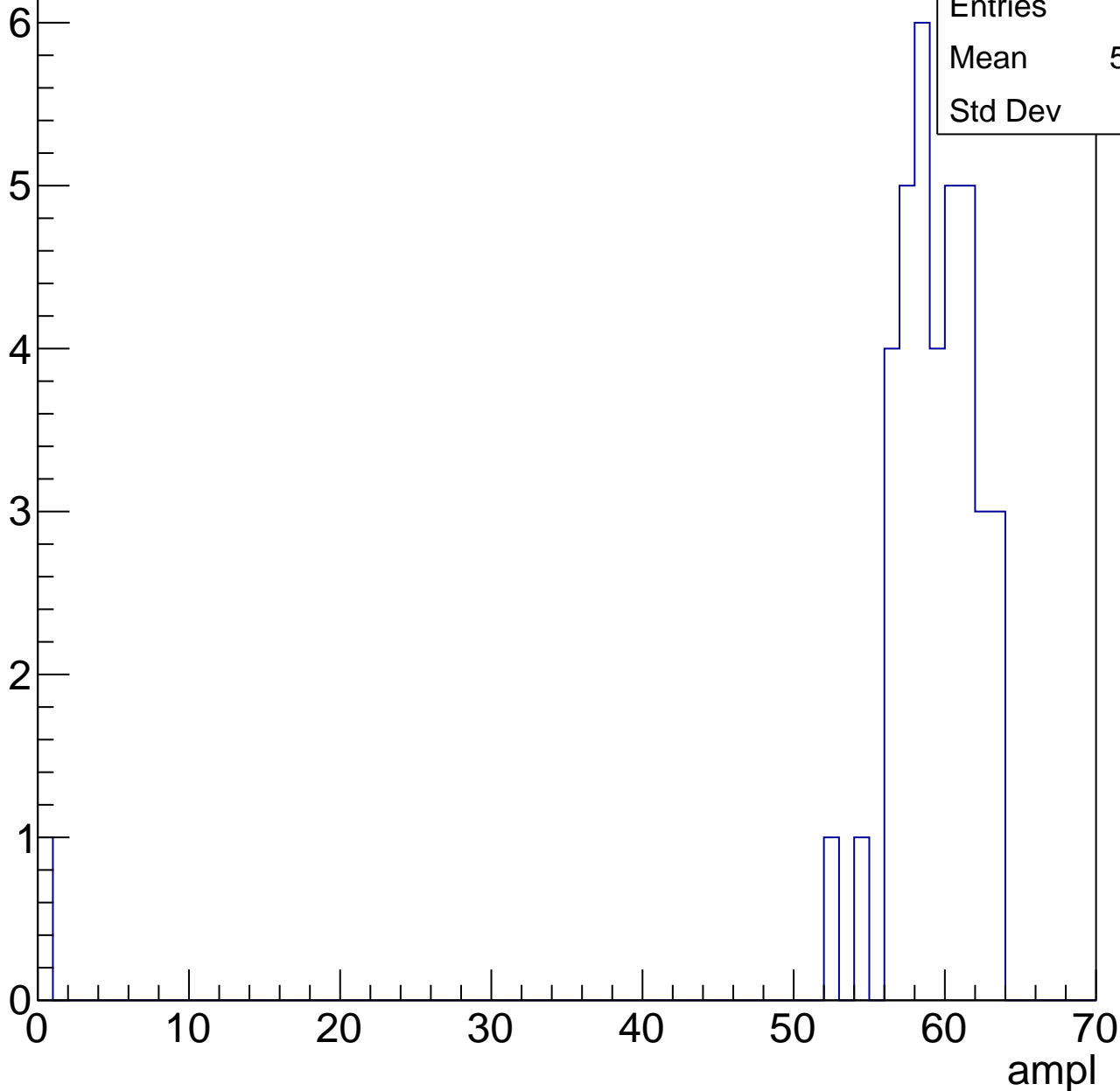


B1L103S, U8-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	57.34
Std Dev	9.75

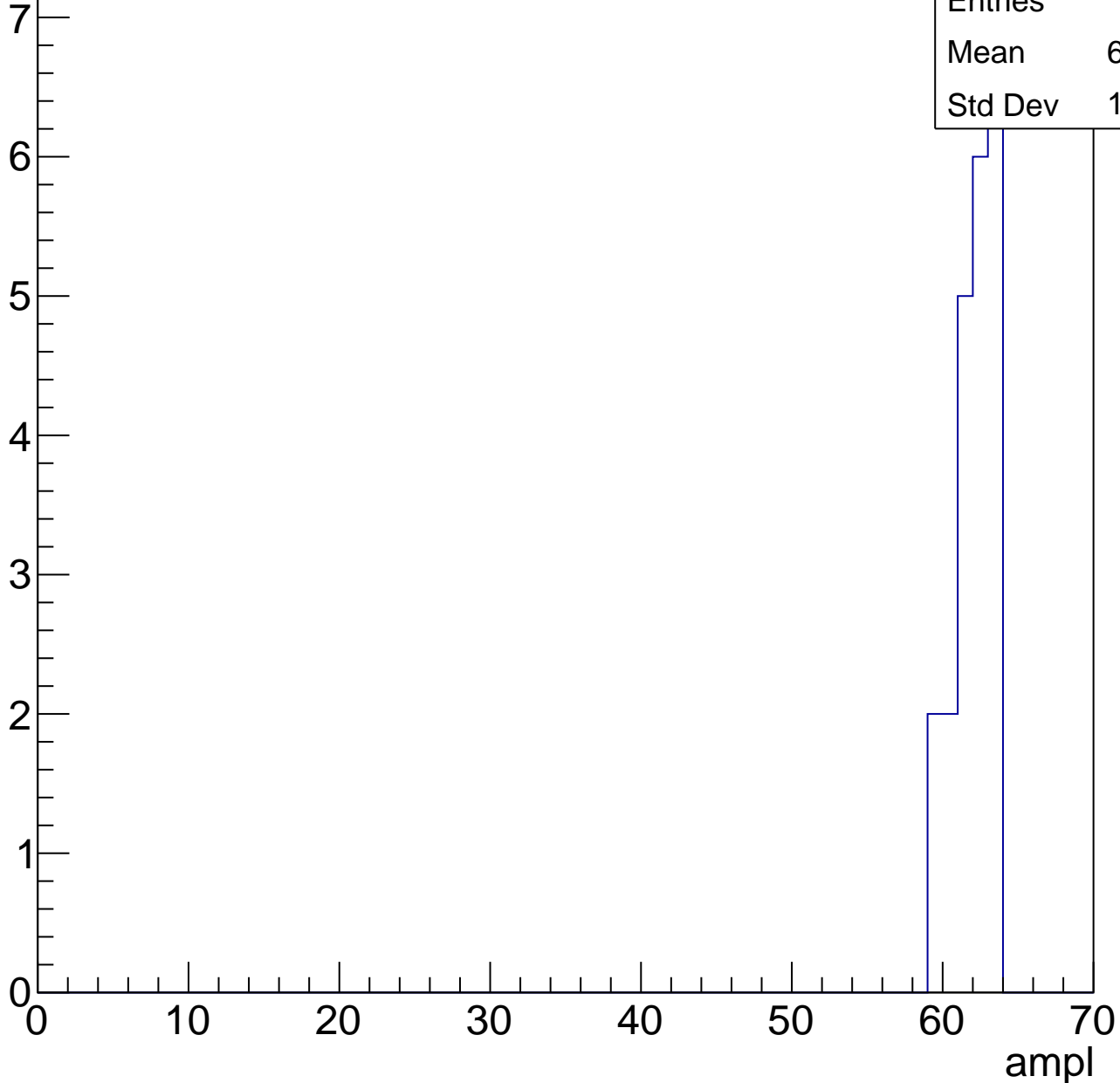


B1L103S, U8-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	61.64
Std Dev	1.263



B1L103S, U8-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch29, adc0

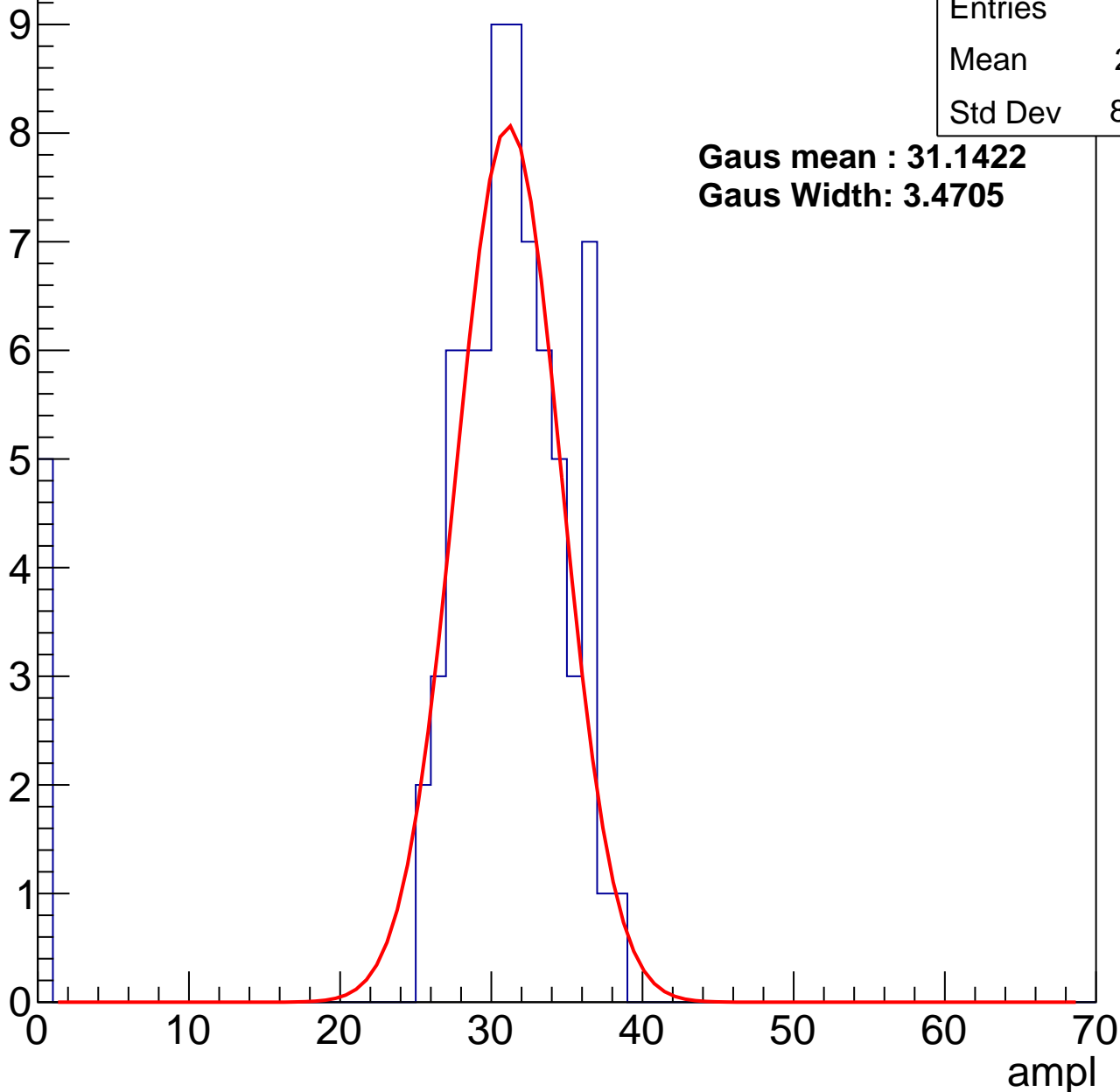
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	29.01
Std Dev	8.287

Gaus mean : 31.1422

Gaus Width: 3.4705



B1L103S, U8-ch29, adc1

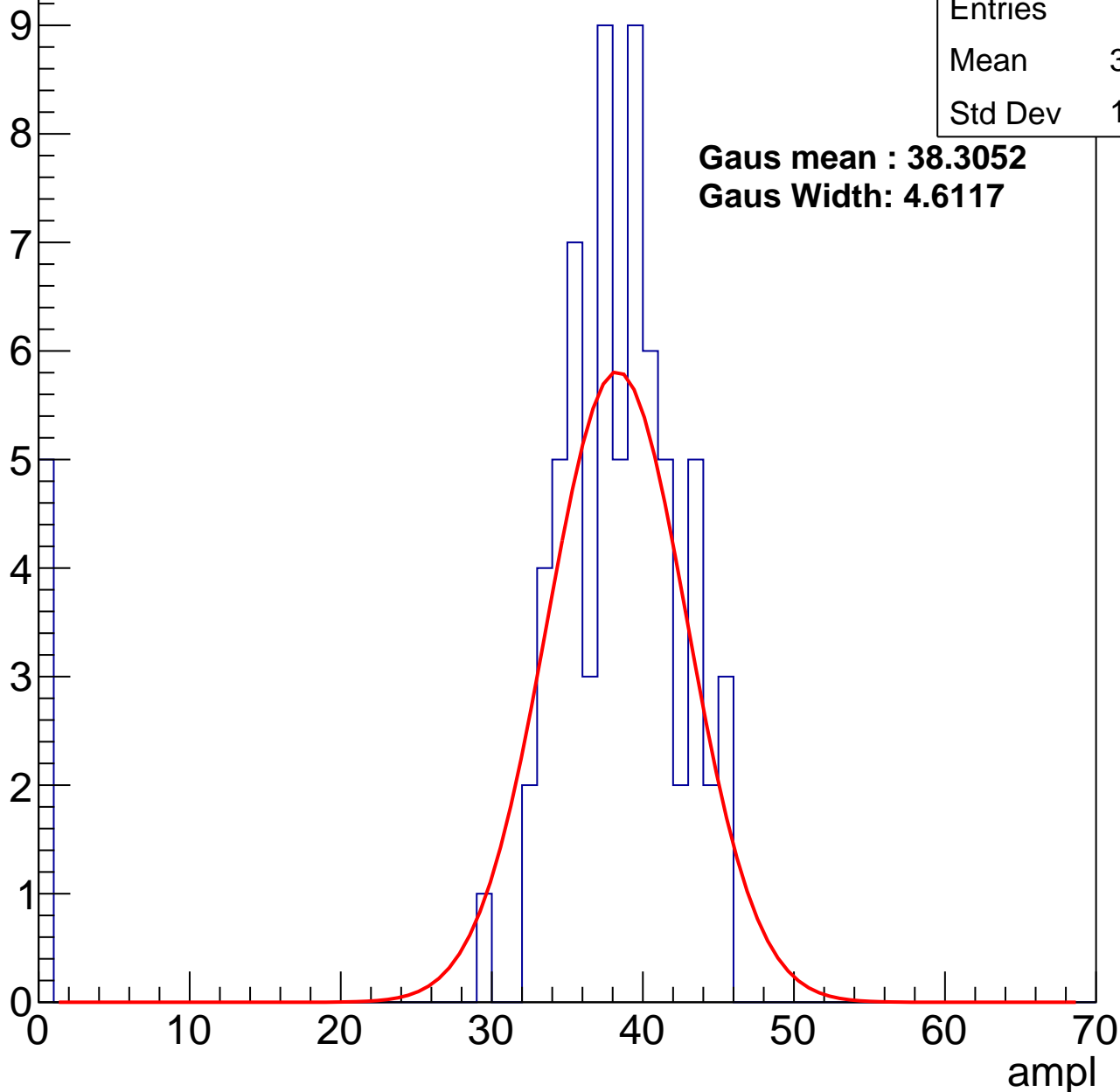
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.47
Std Dev	10.22

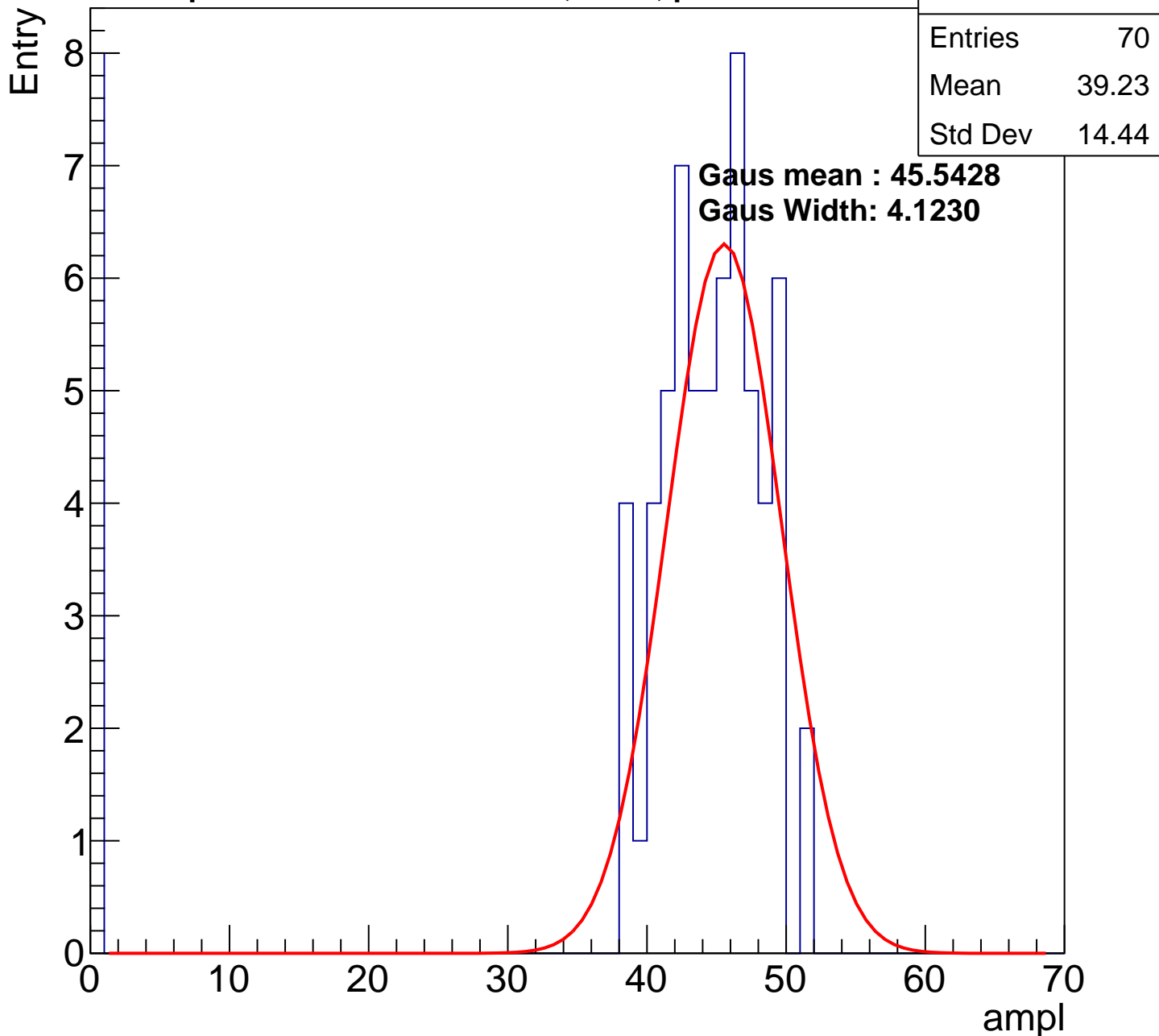
Gaus mean : 38.3052

Gaus Width: 4.6117



B1L103S, U8-ch29, adc2

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

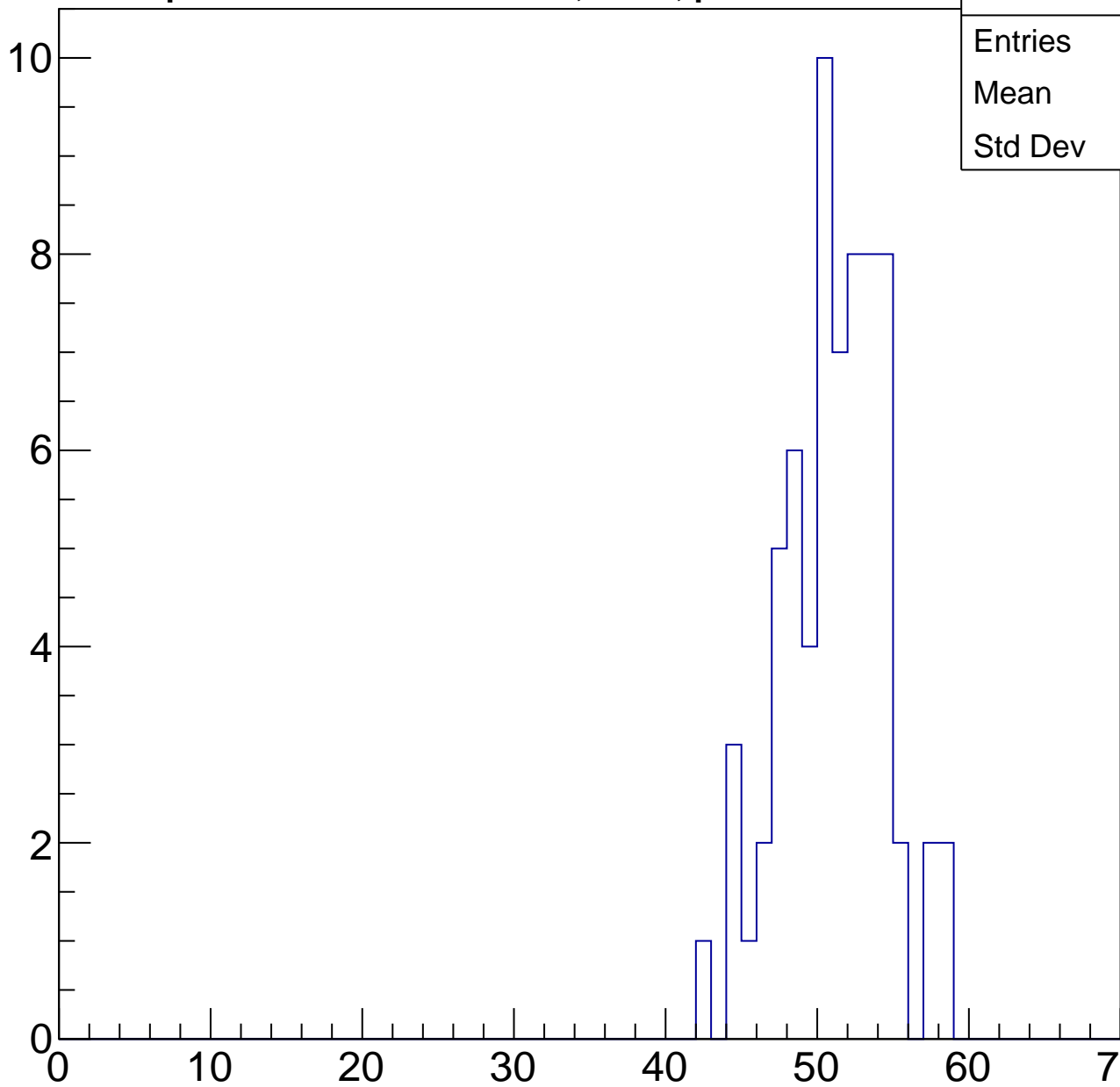
Entries	69
Mean	50.71
Std Dev	3.367

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

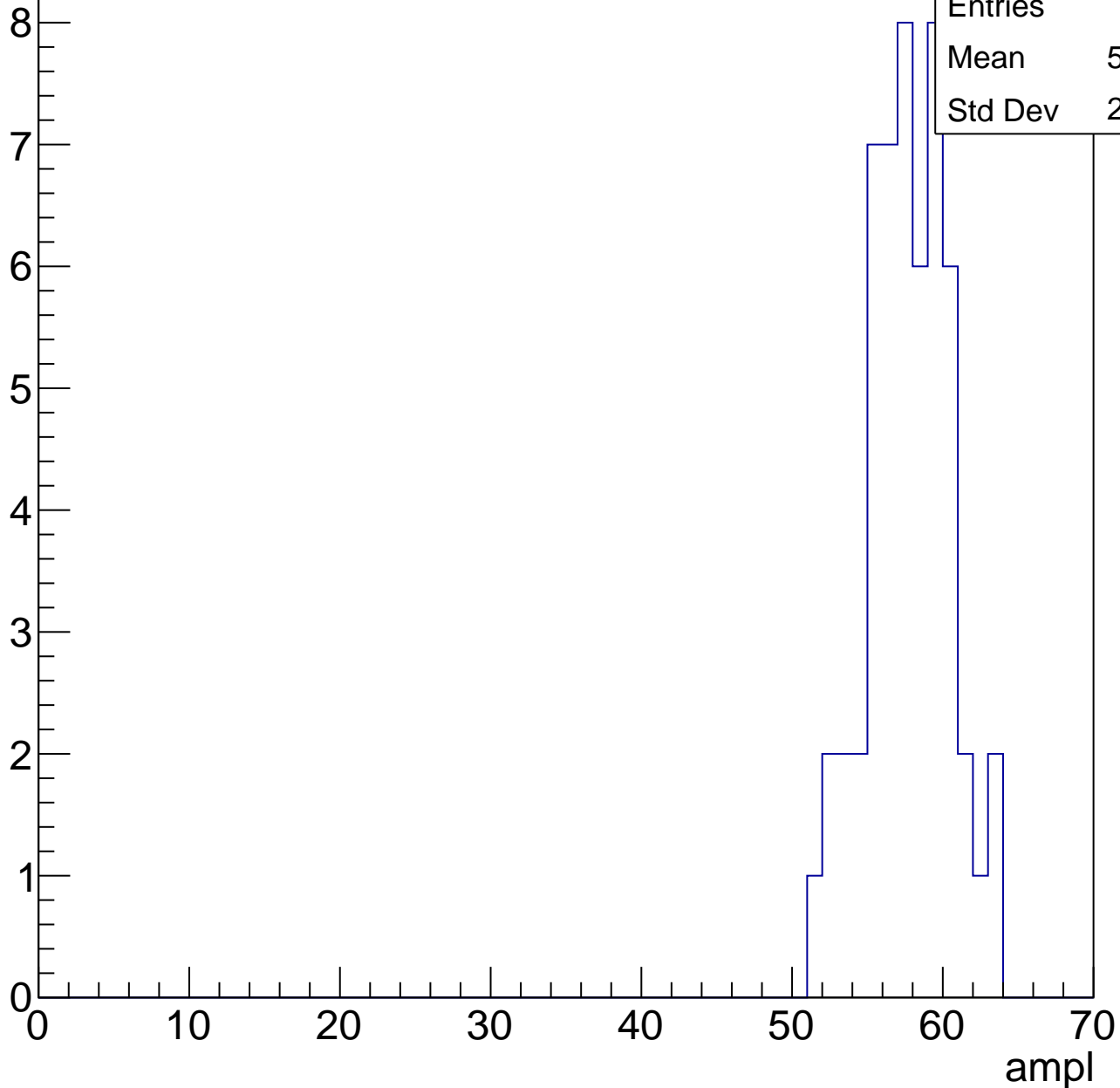
ampl



B1L103S, U8-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



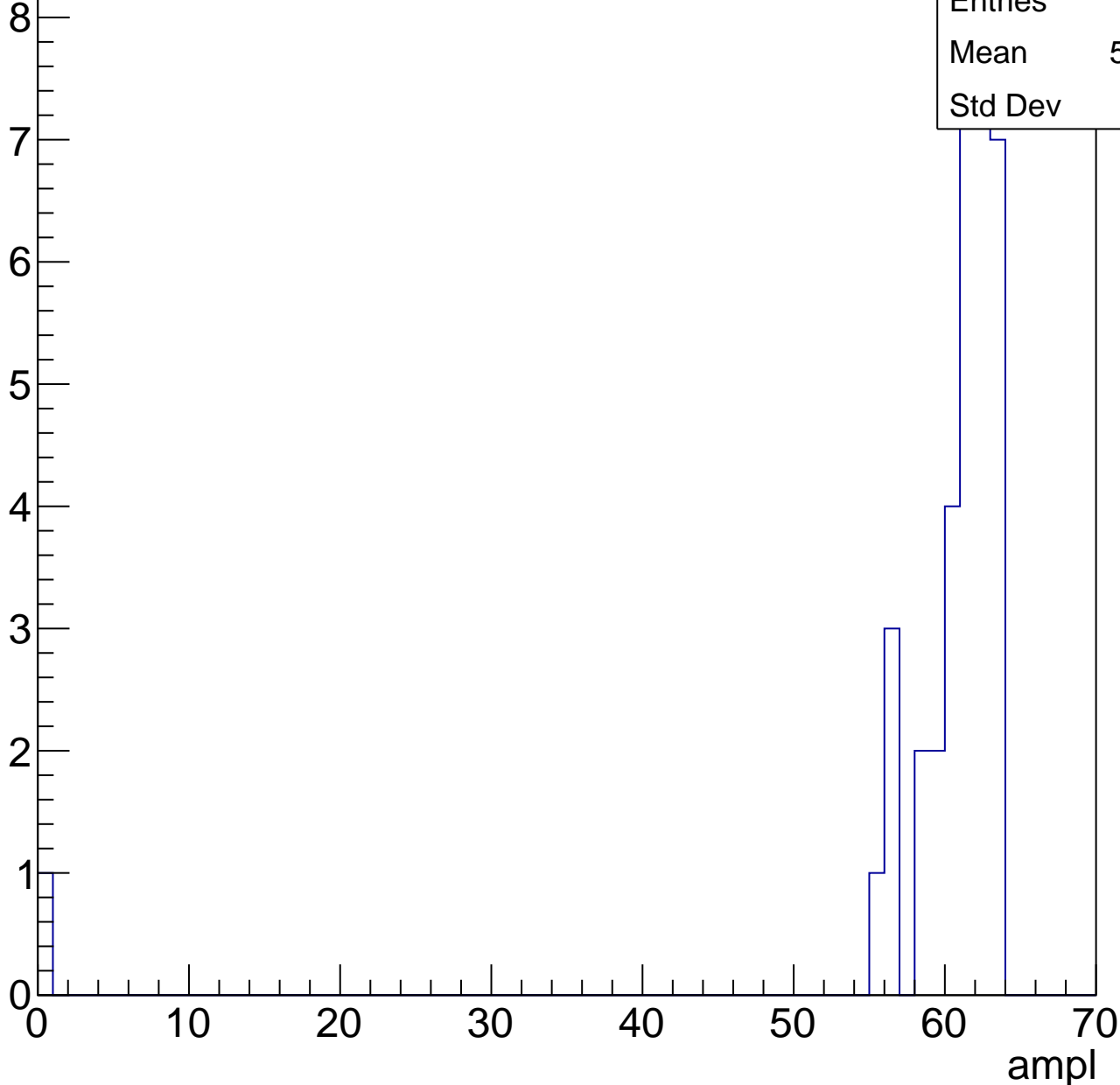
Entries	54
Mean	57.26
Std Dev	2.682

B1L103S, U8-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.94
Std Dev	10.2



B1L103S, U8-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch30, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	24.27
Std Dev	10.99

Gaus mean : 29.2411

Gaus Width: 3.3609

Entry

12

10

8

6

4

2

0

0

10

20

30

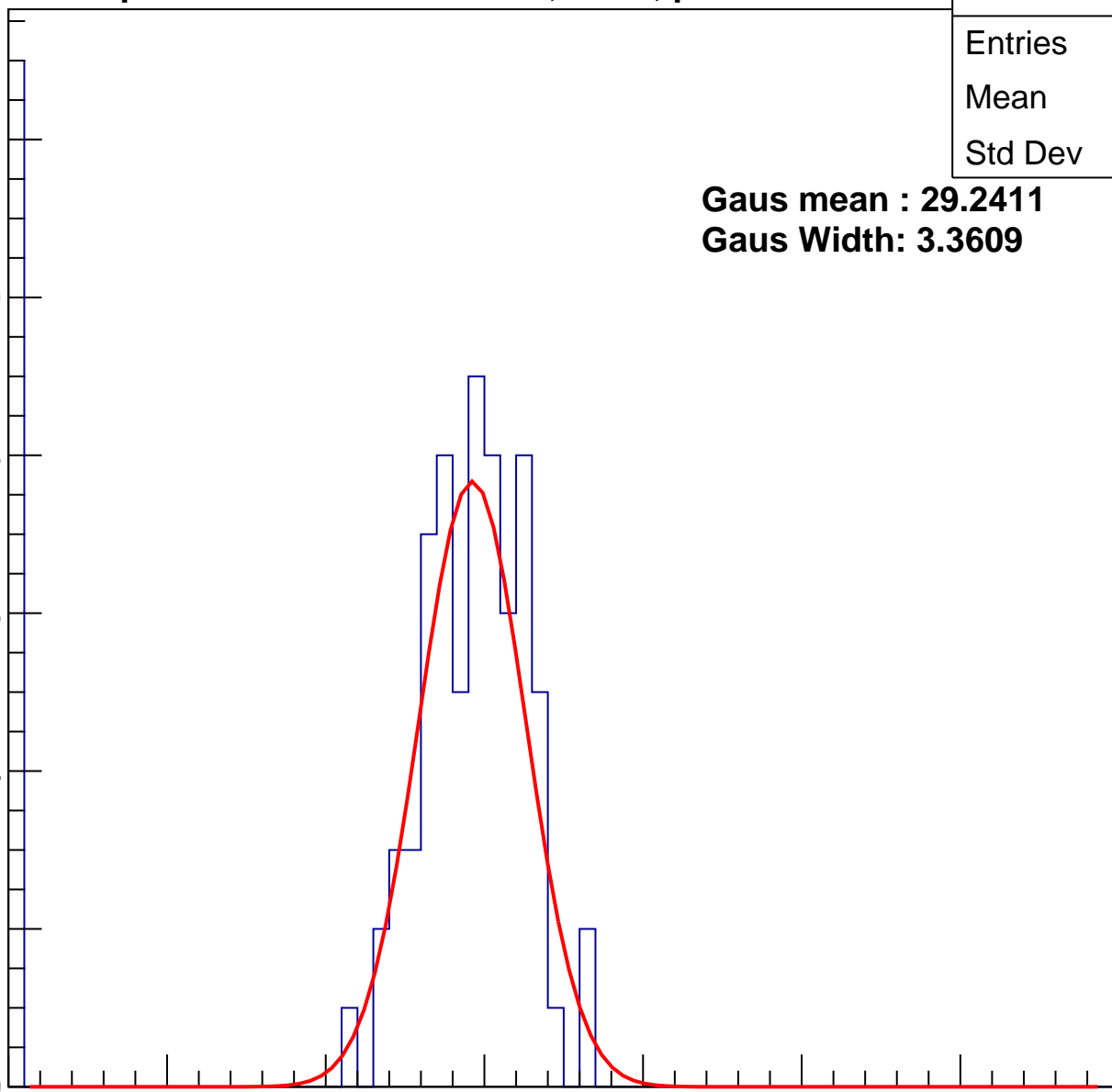
40

50

60

70

ampl



B1L103S, U8-ch30, adc1

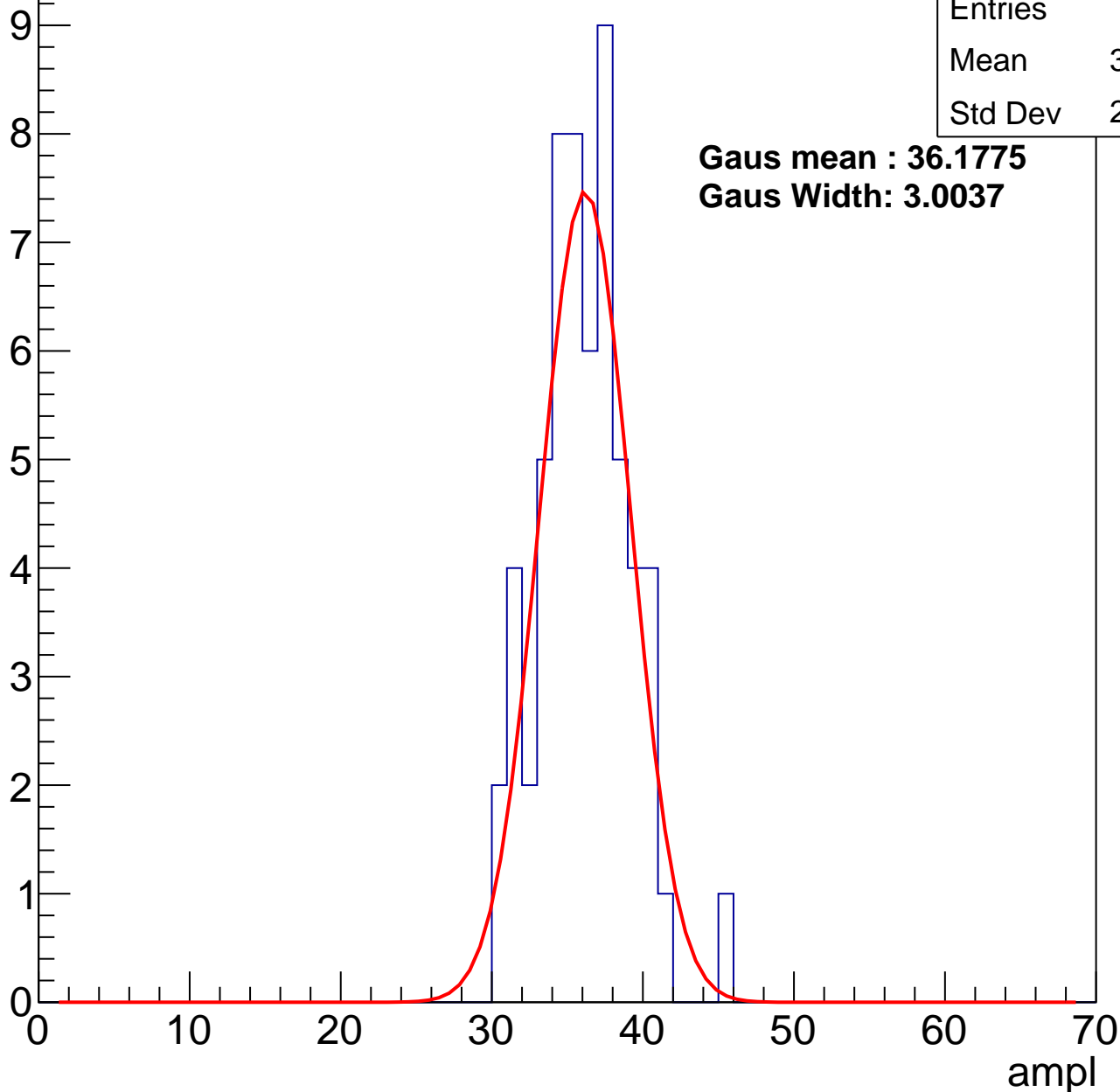
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.69
Std Dev	2.965

Gaus mean : 36.1775

Gaus Width: 3.0037



B1L103S, U8-ch30, adc2

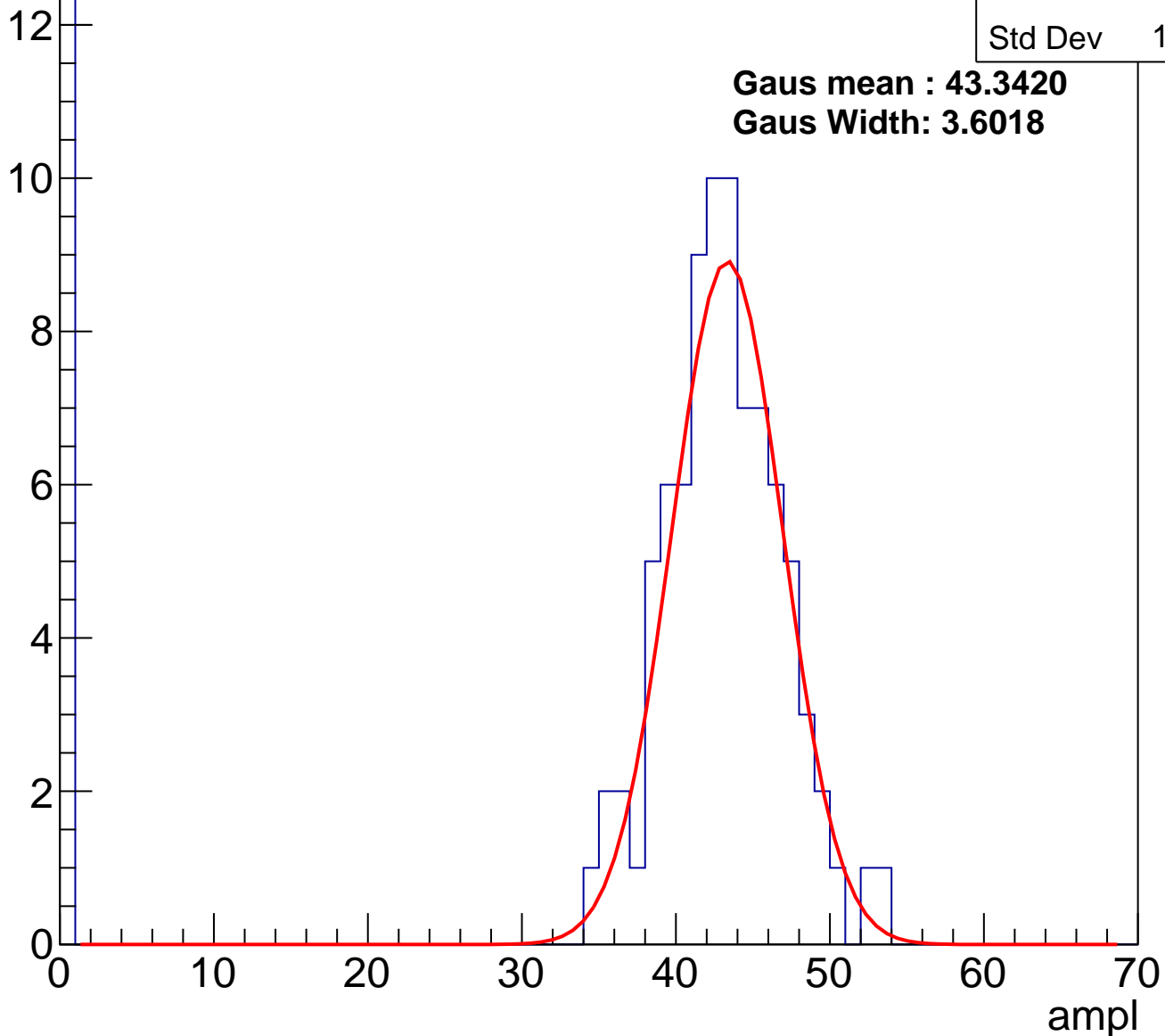
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	98
Mean	37.01
Std Dev	14.89

Gaus mean : 43.3420

Gaus Width: 3.6018

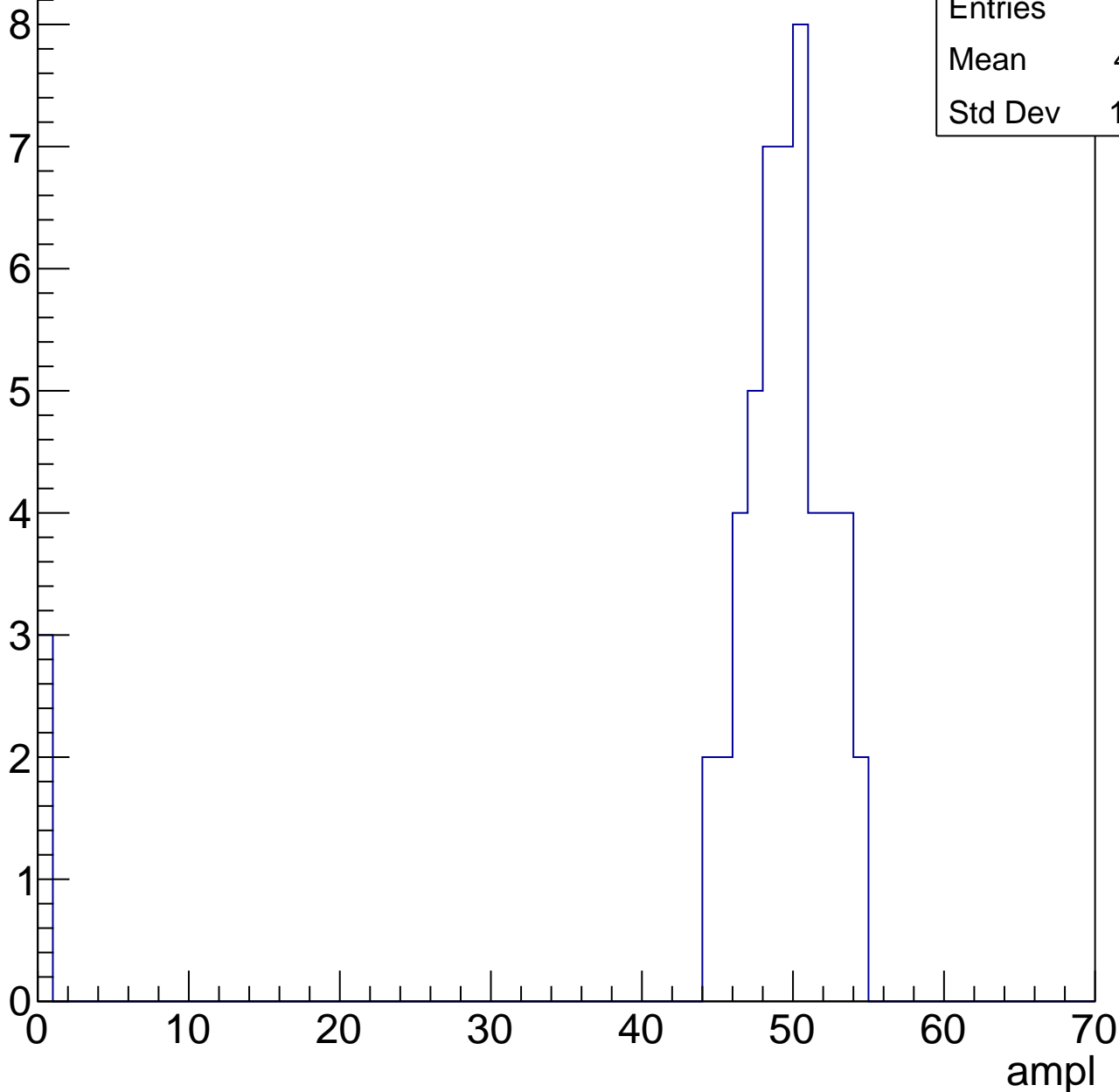


B1L103S, U8-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	46.31
Std Dev	11.72

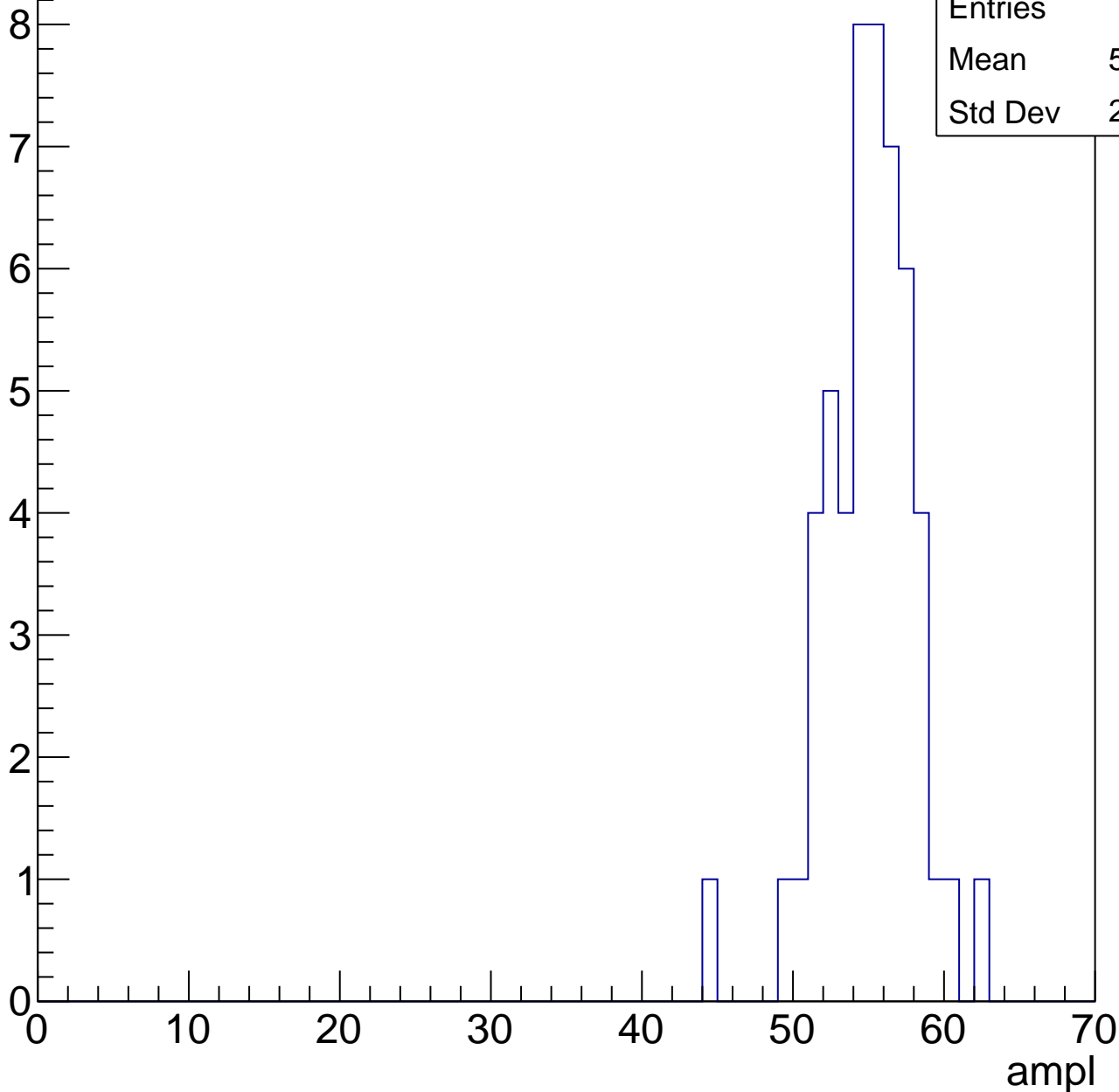


B1L103S, U8-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

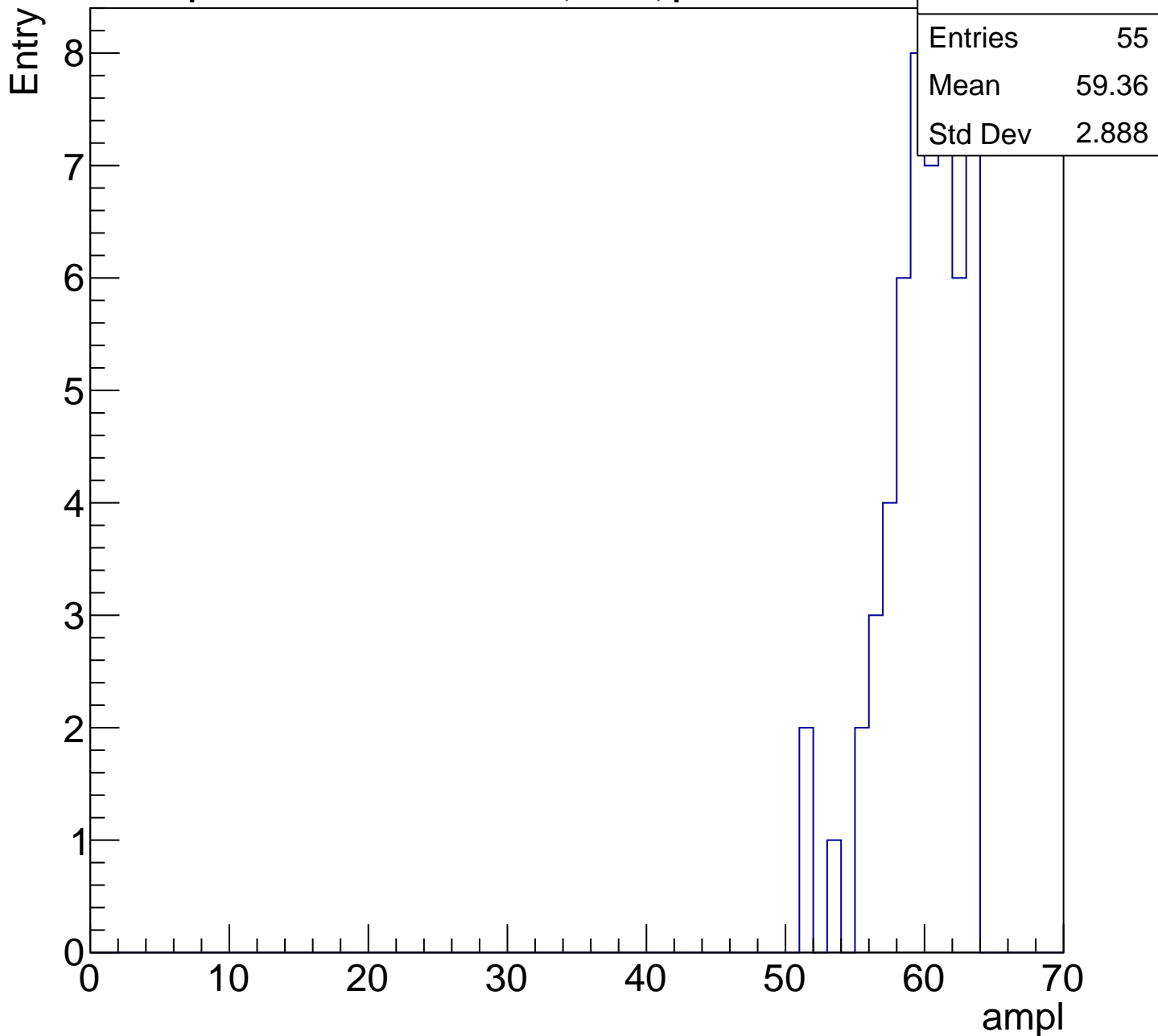
Entry

Entries	52
Mean	54.58
Std Dev	2.983



B1L103S, U8-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

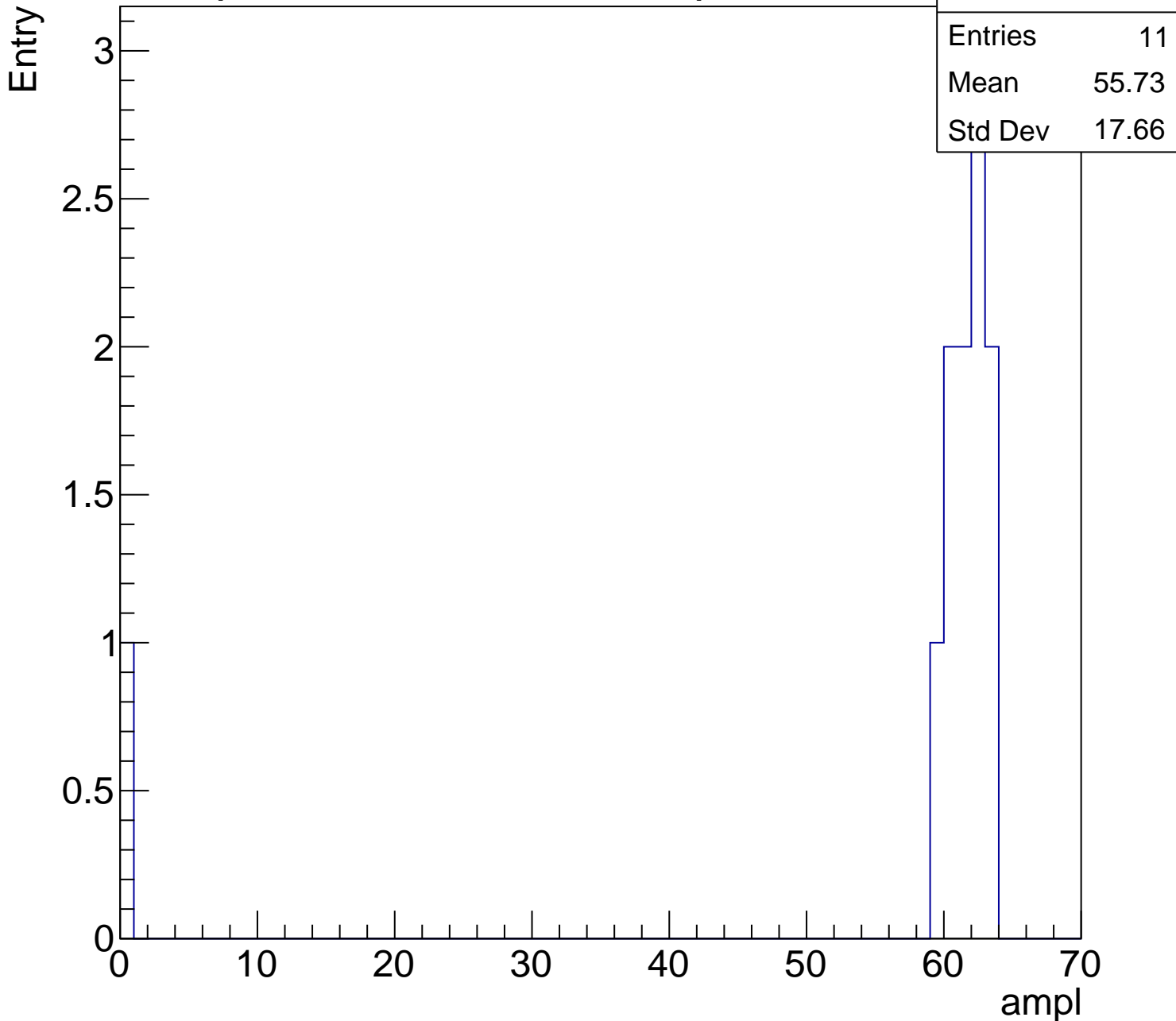
Mean

55.73

Std Dev

17.66

ampl



B1L103S, U8-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch31, adc0

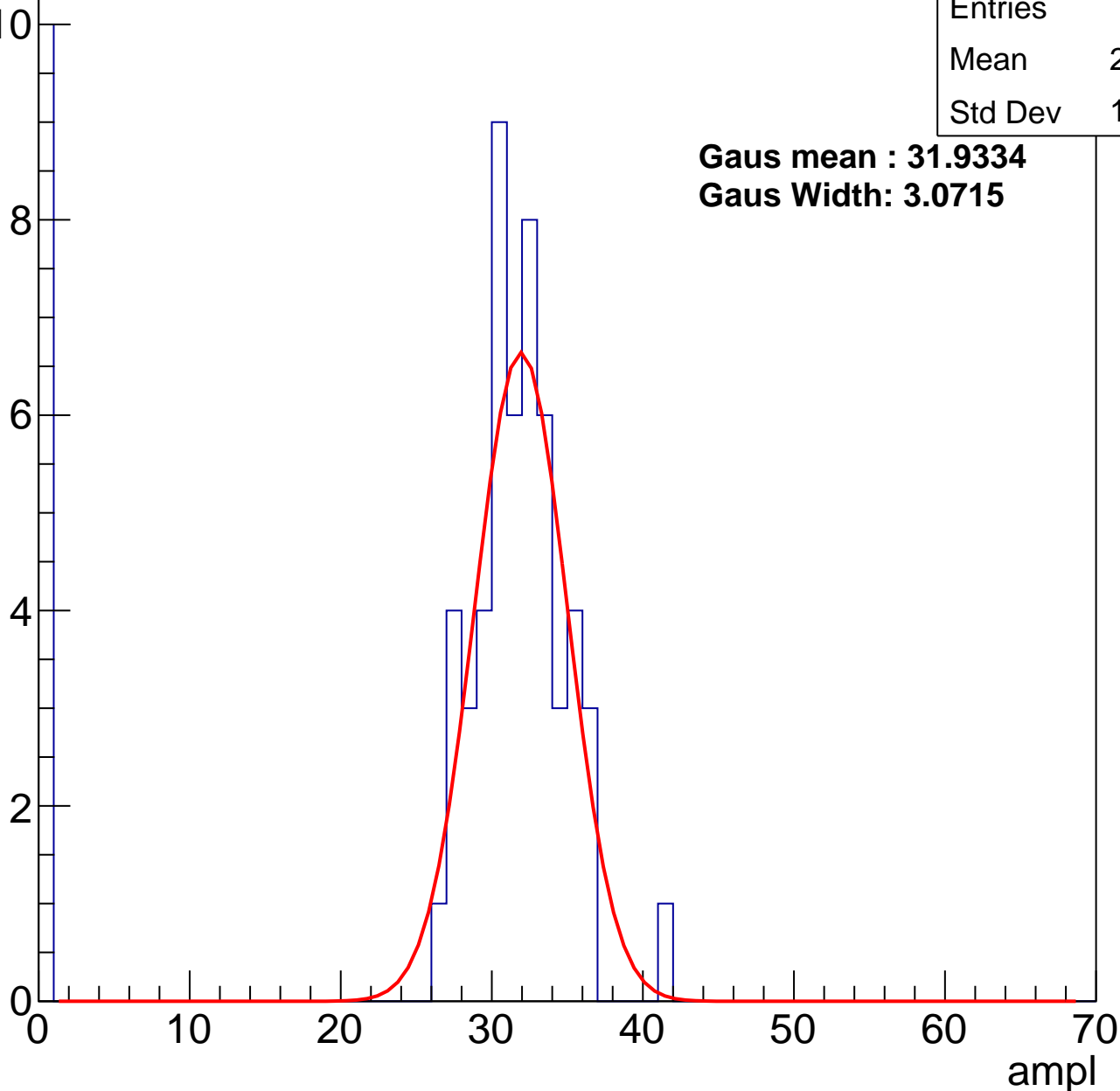
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	26.37
Std Dev	11.86

Gaus mean : 31.9334

Gaus Width: 3.0715



B1L103S, U8-ch31, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	32.51
Std Dev	12.17

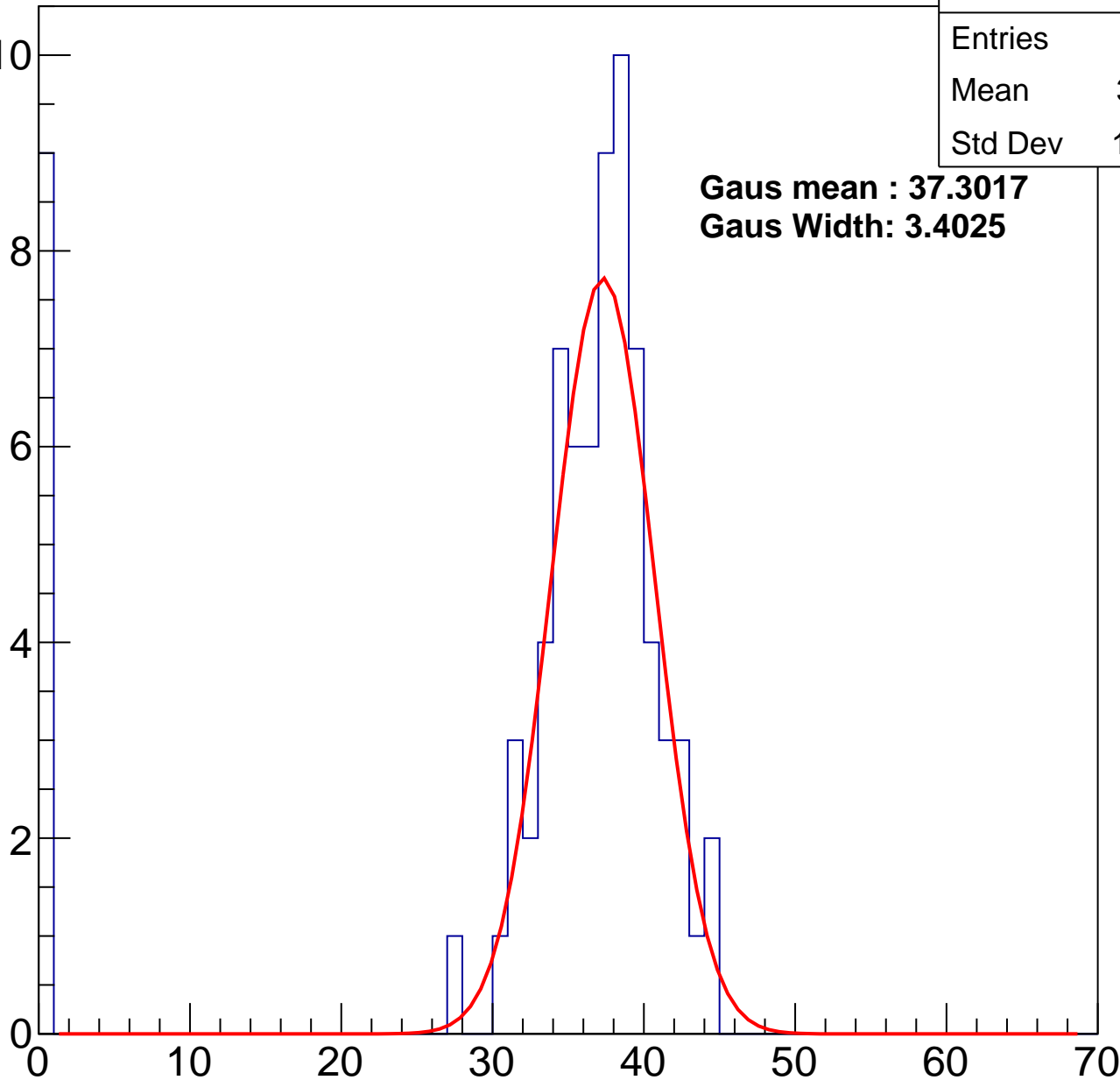
Gaus mean : 37.3017
Gaus Width: 3.4025

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch31, adc2

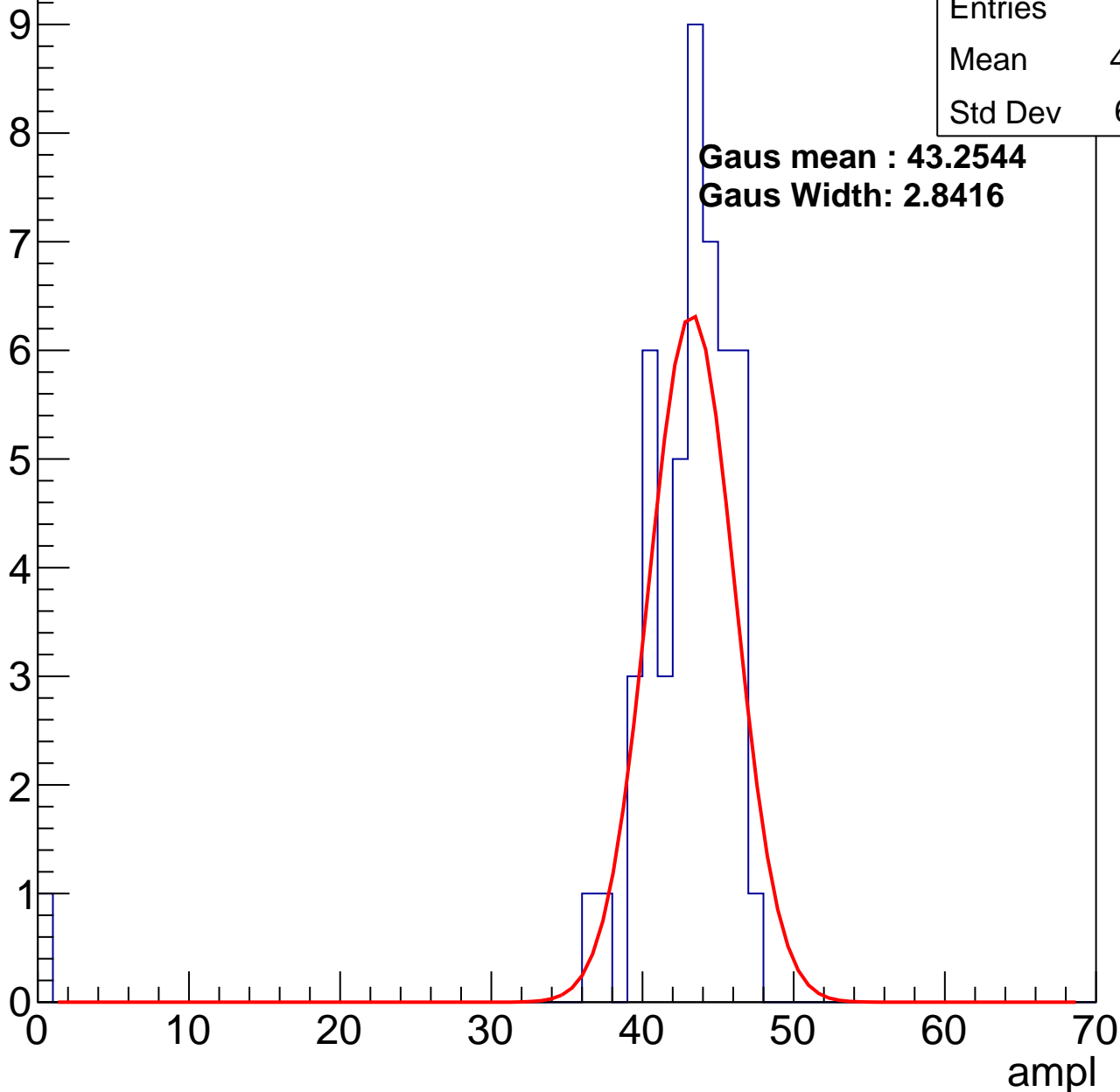
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	41.86
Std Dev	6.531

Gaus mean : 43.2544

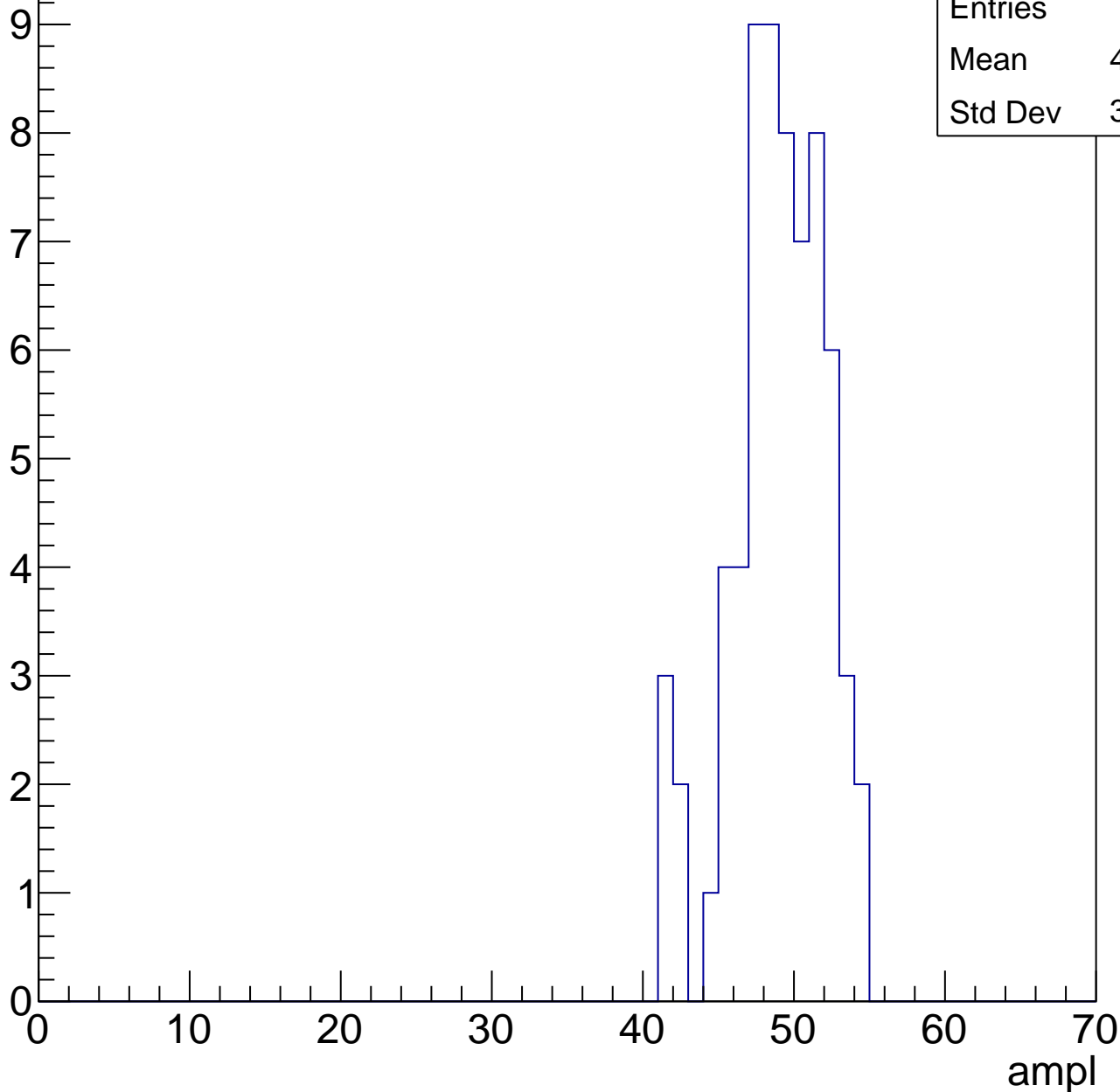
Gaus Width: 2.8416



B1L103S, U8-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

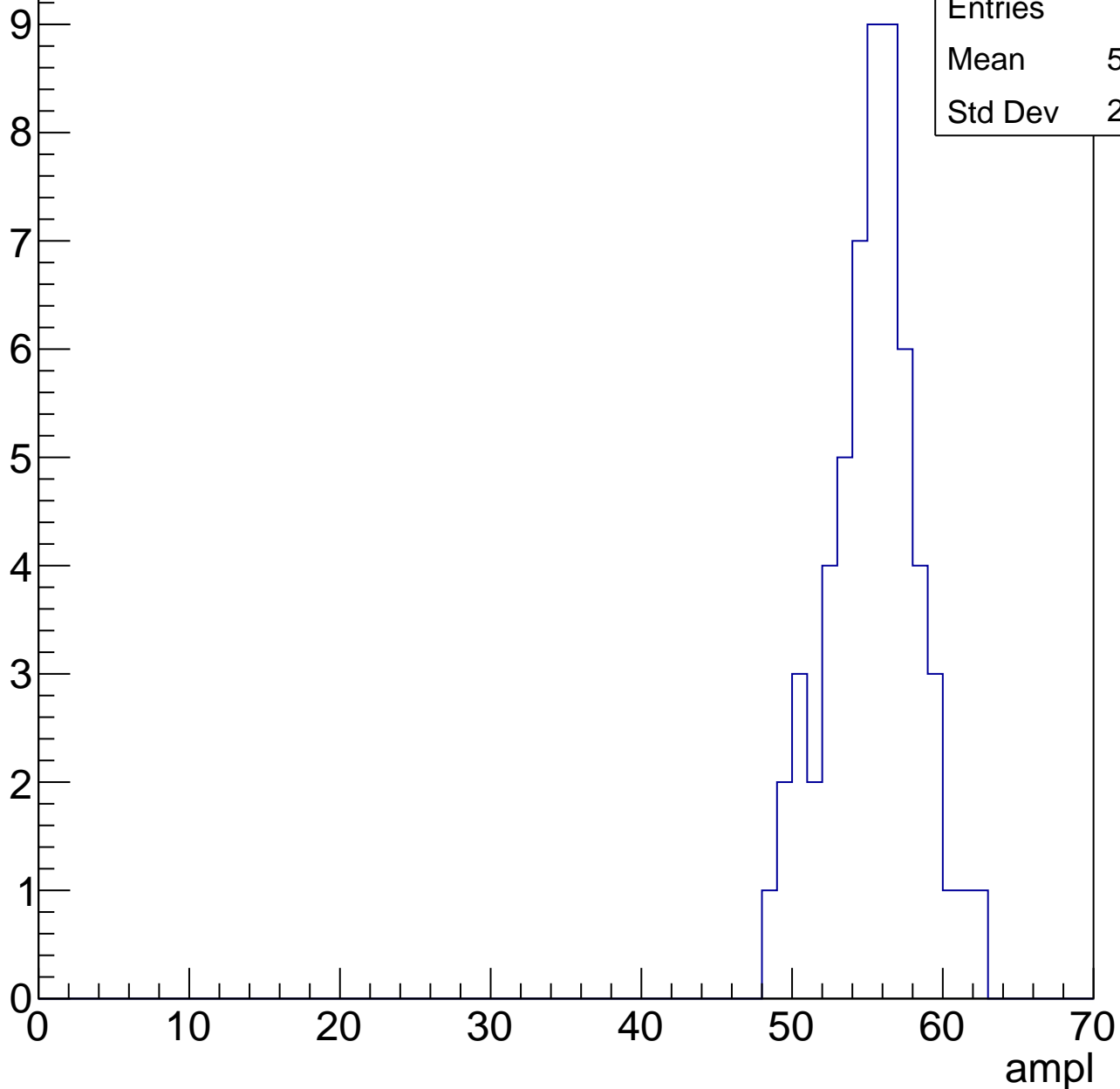
Entry



B1L103S, U8-ch31, adc4

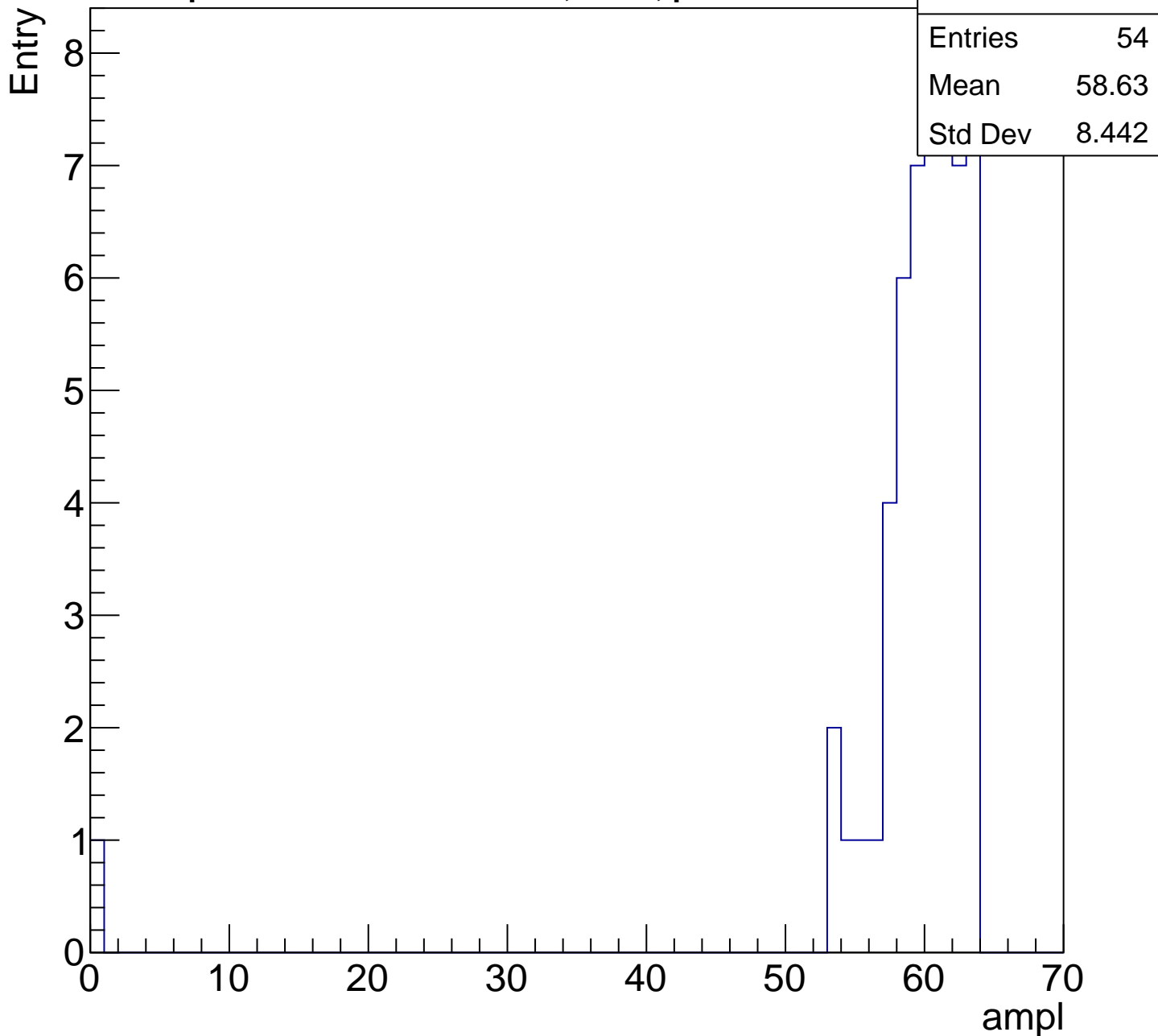
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

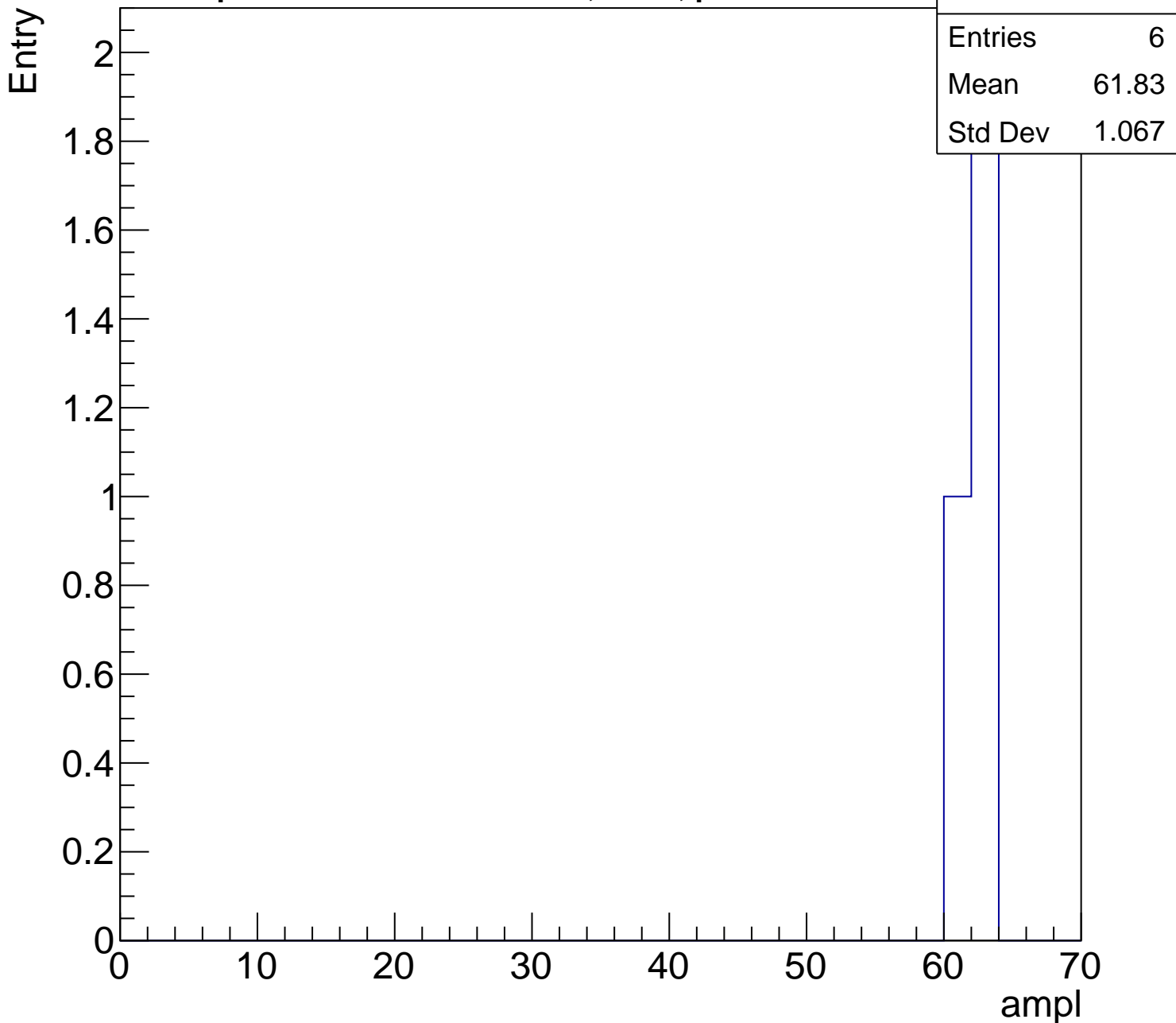
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch31, adc7

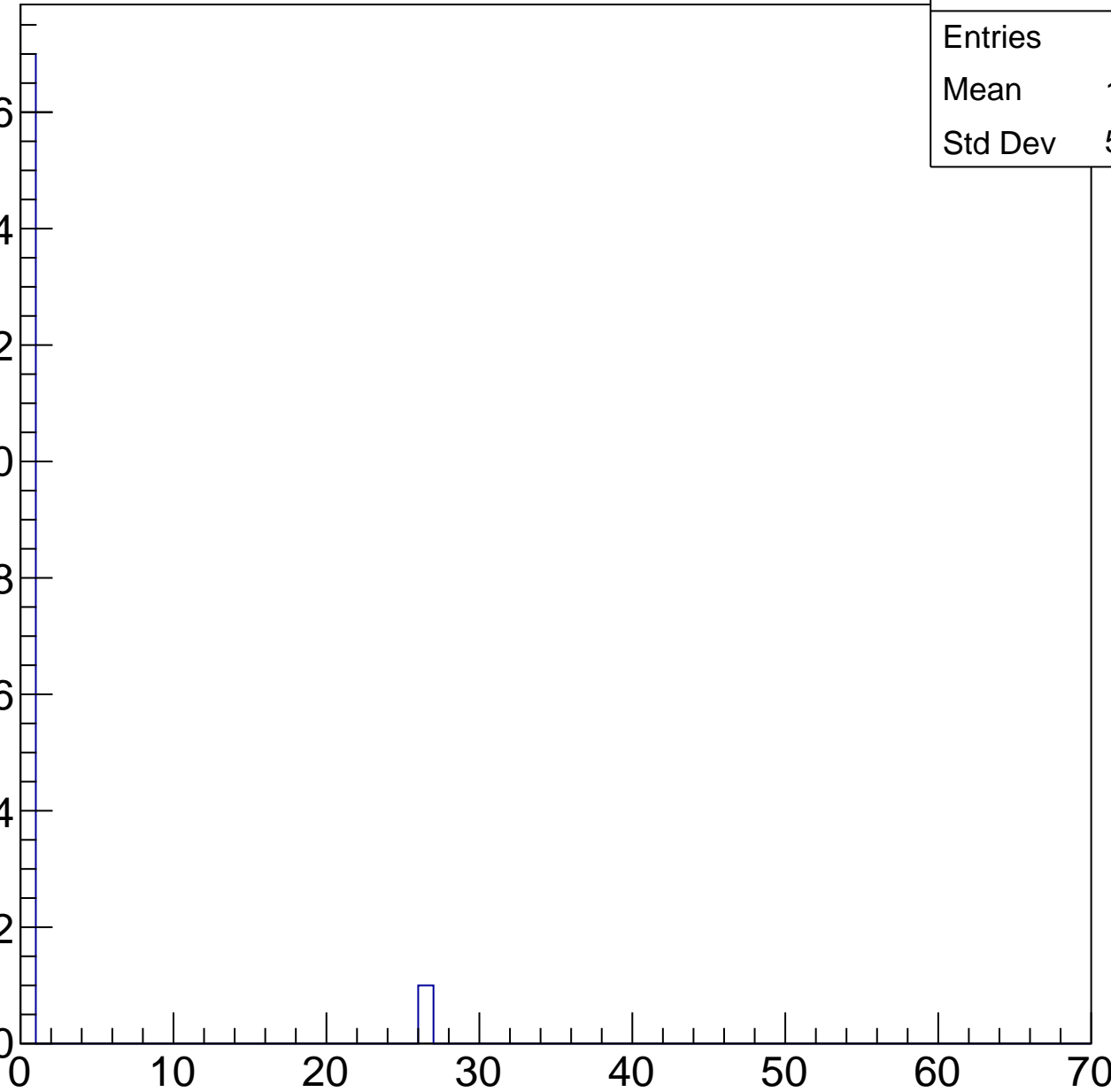
calib_packv5_041523_1651.root, FC#0, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	18
Mean	1.444
Std Dev	5.956

ampl



B1L103S, U8-ch32, adc0

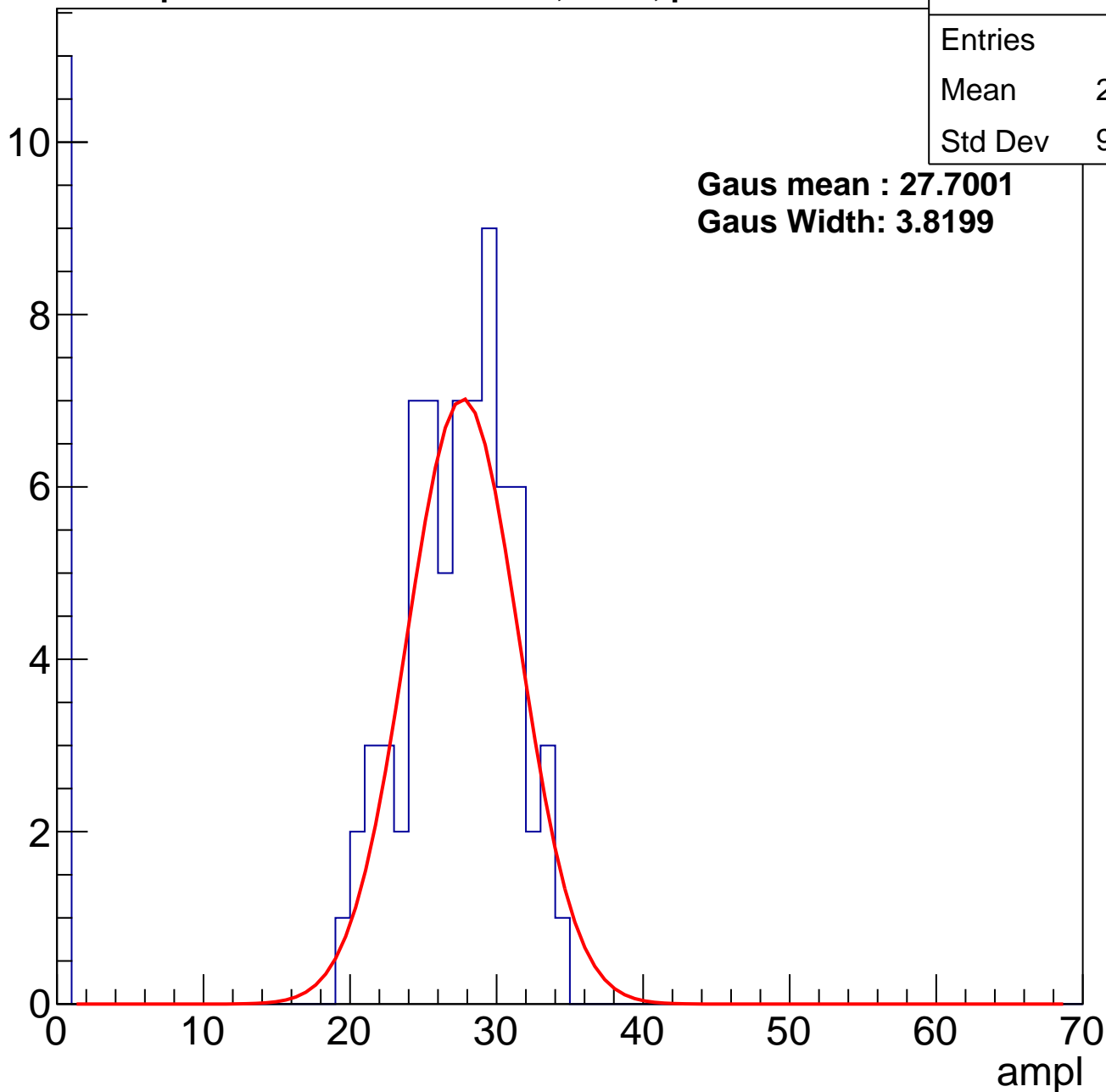
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.37
Std Dev	9.759

Gaus mean : 27.7001

Gaus Width: 3.8199

Entry



B1L103S, U8-ch32, adc1

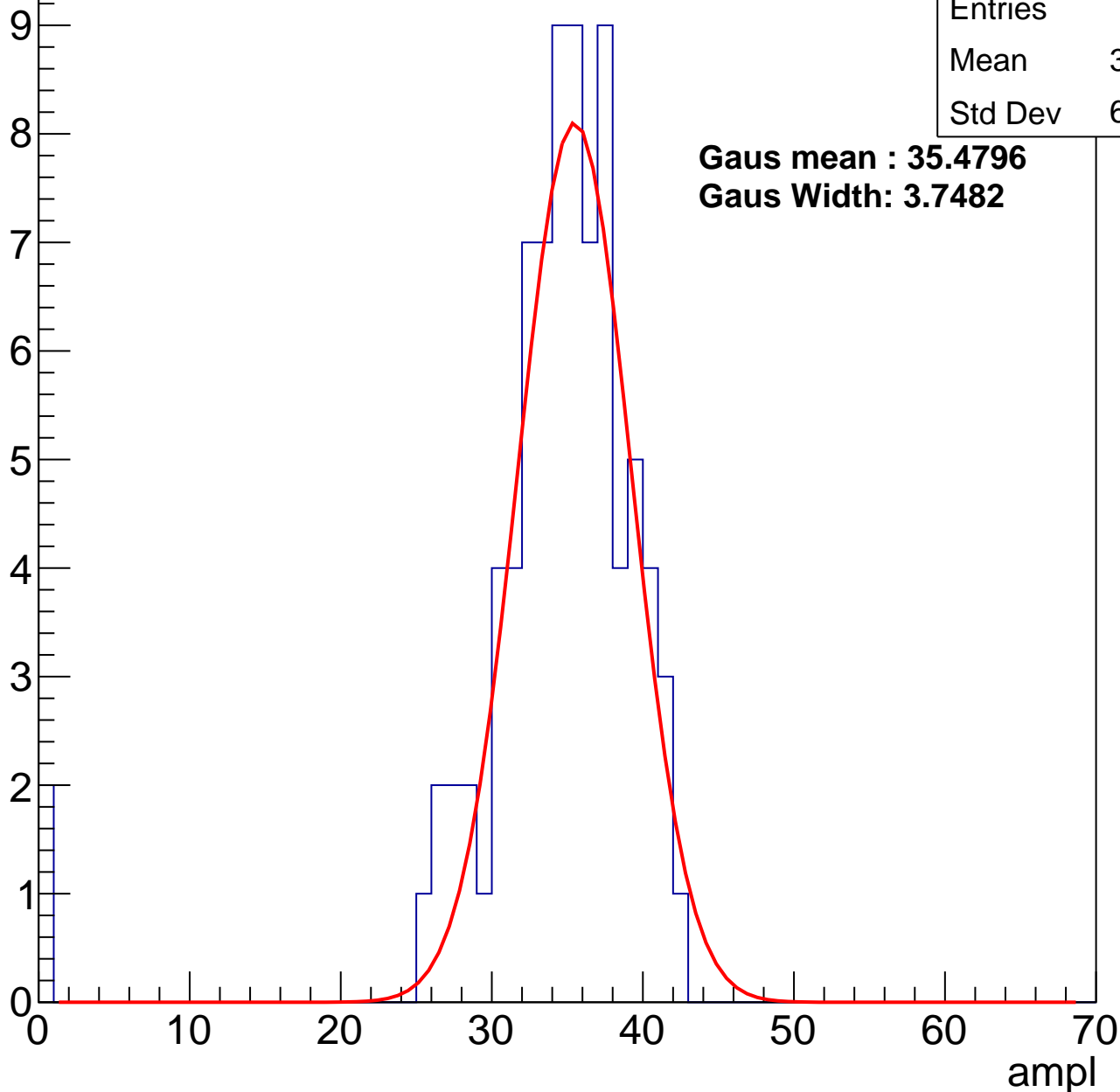
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	33.65
Std Dev	6.496

Gaus mean : 35.4796

Gaus Width: 3.7482



B1L103S, U8-ch32, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	31.66
Std Dev	17.27

Gaus mean : 41.8687

Gaus Width: 3.7203

Entry

12

10

8

6

4

2

0

0

10

20

30

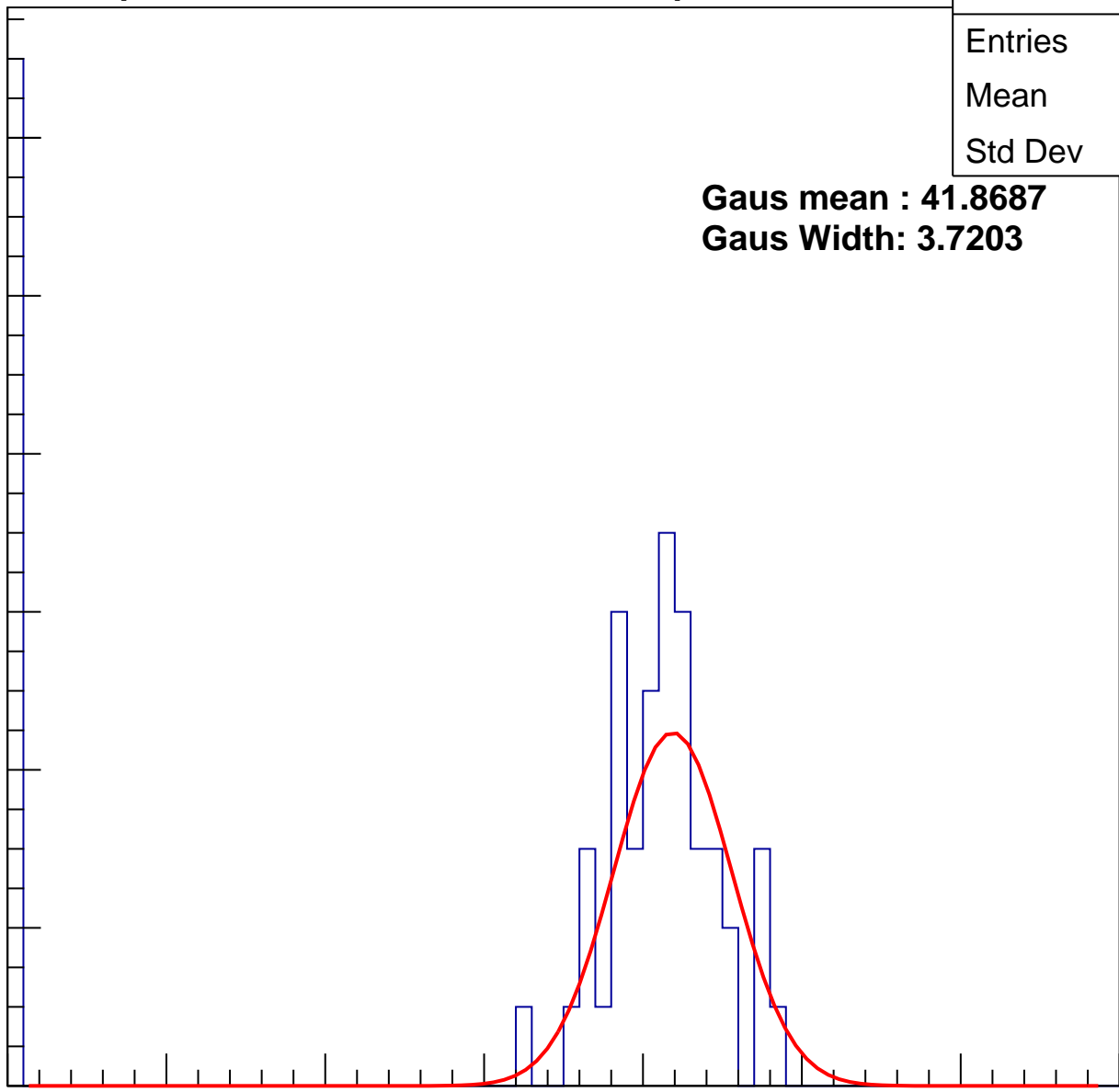
40

50

60

70

ampl

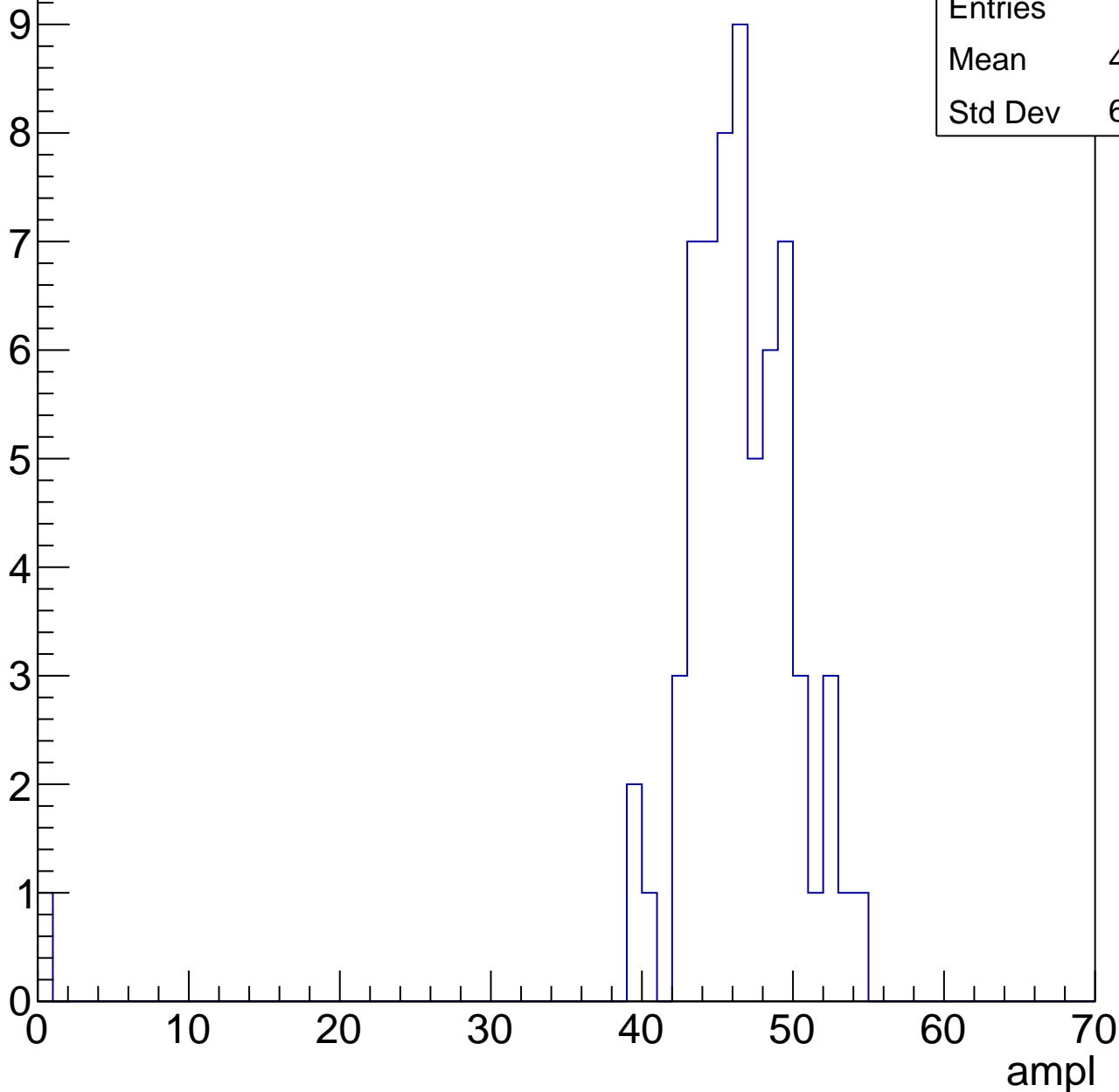


B1L103S, U8-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

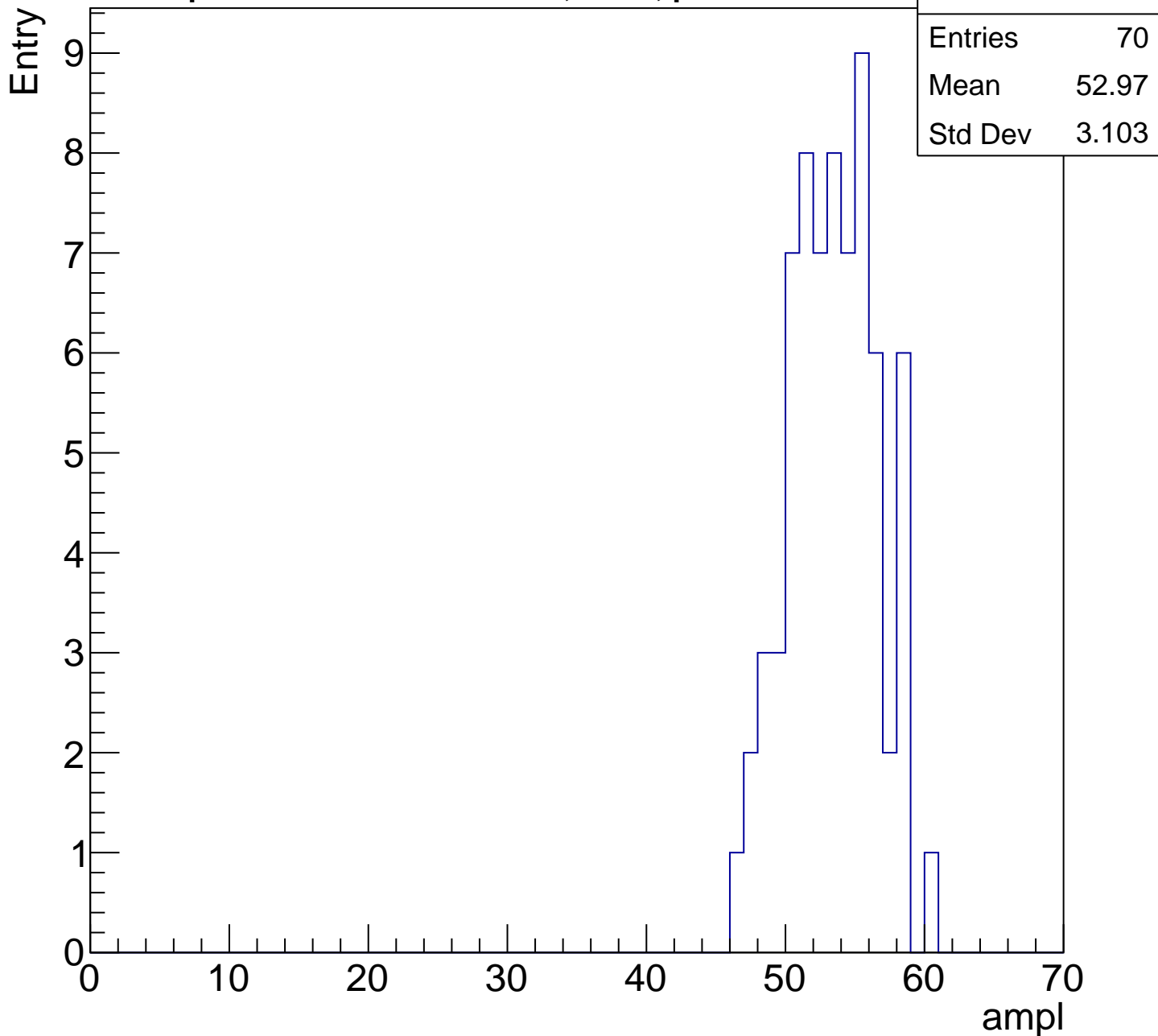
Entry

Entries	65
Mean	45.49
Std Dev	6.524



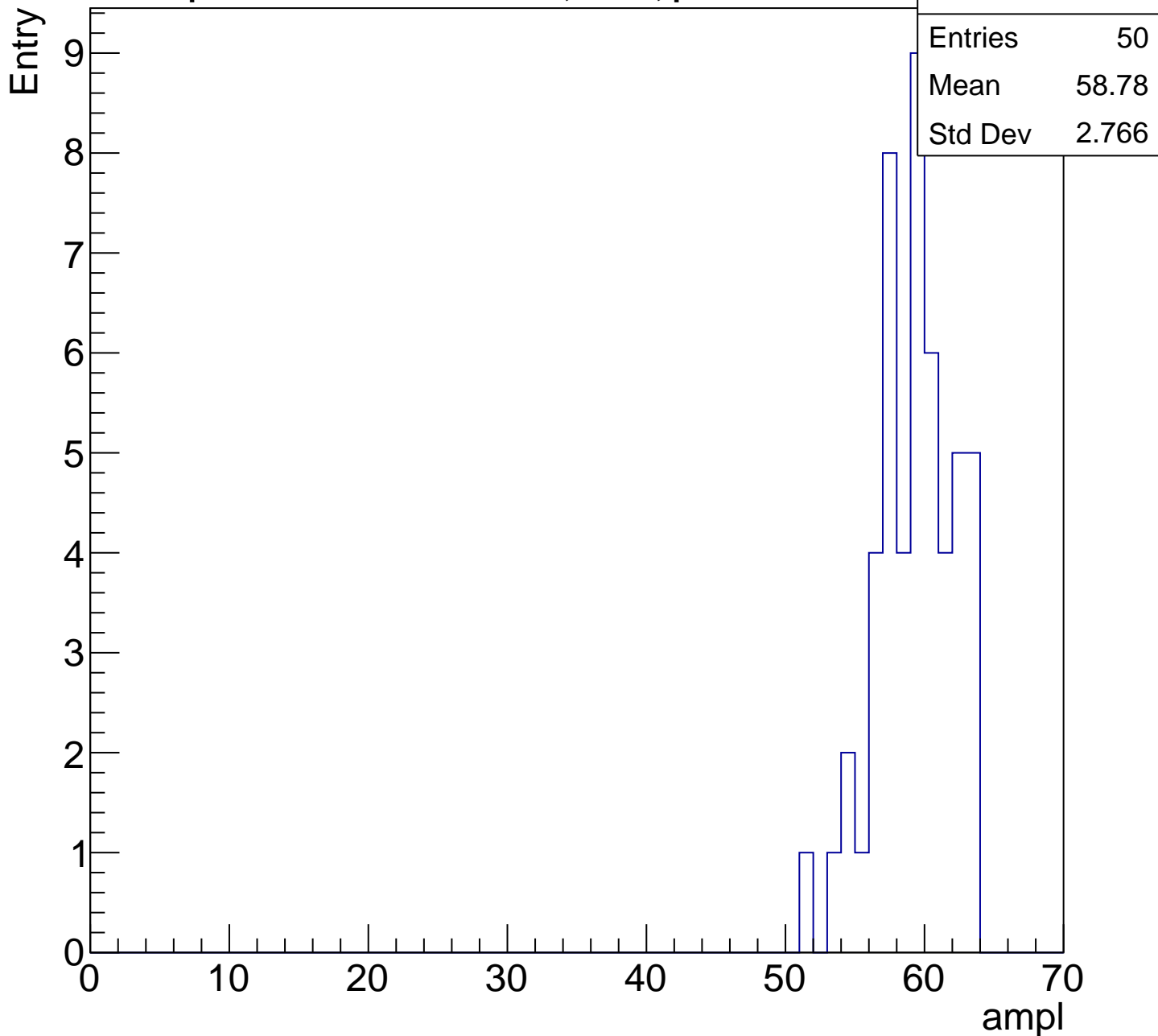
B1L103S, U8-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

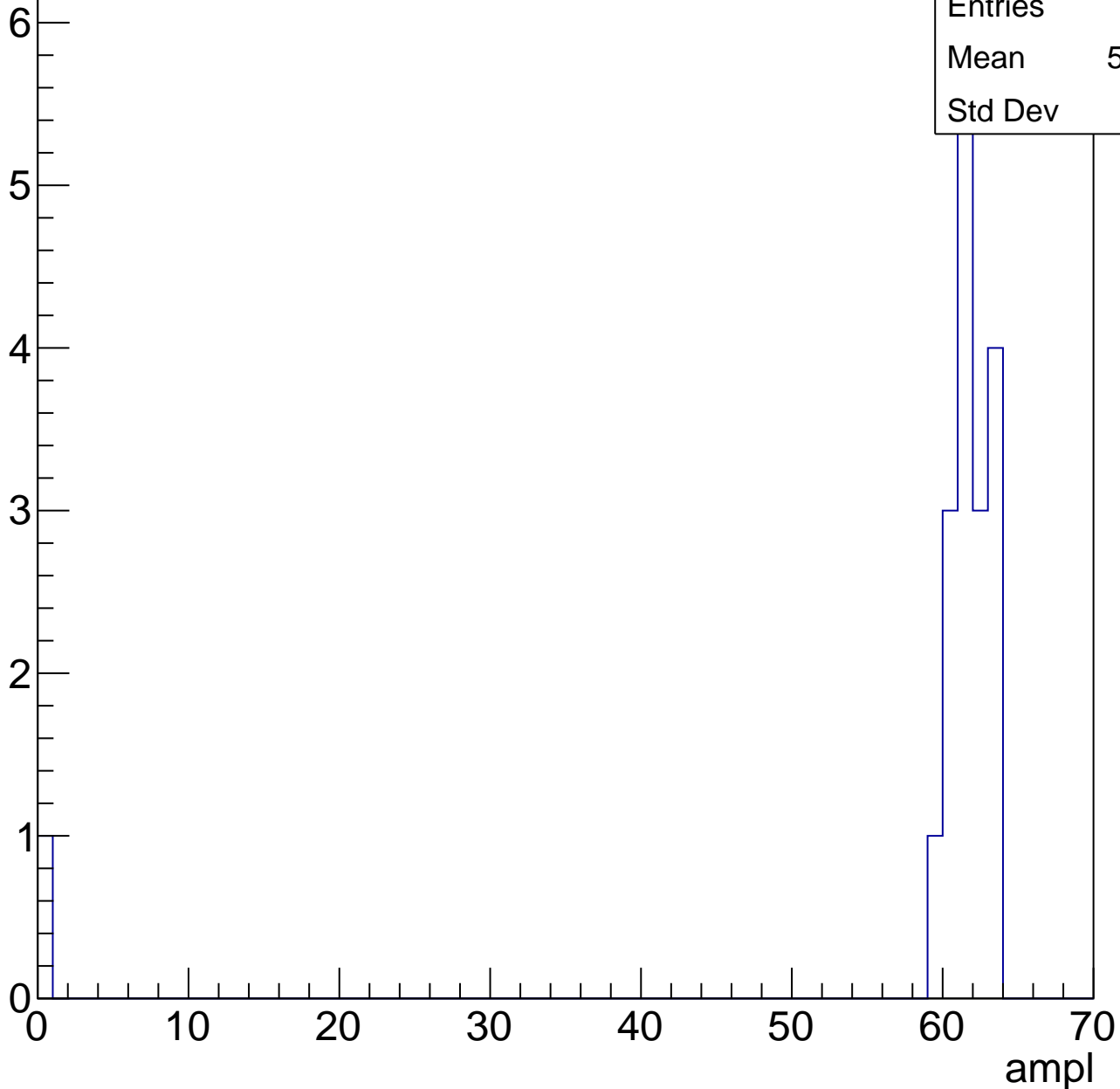


B1L103S, U8-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.94
Std Dev	14.1



B1L103S, U8-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

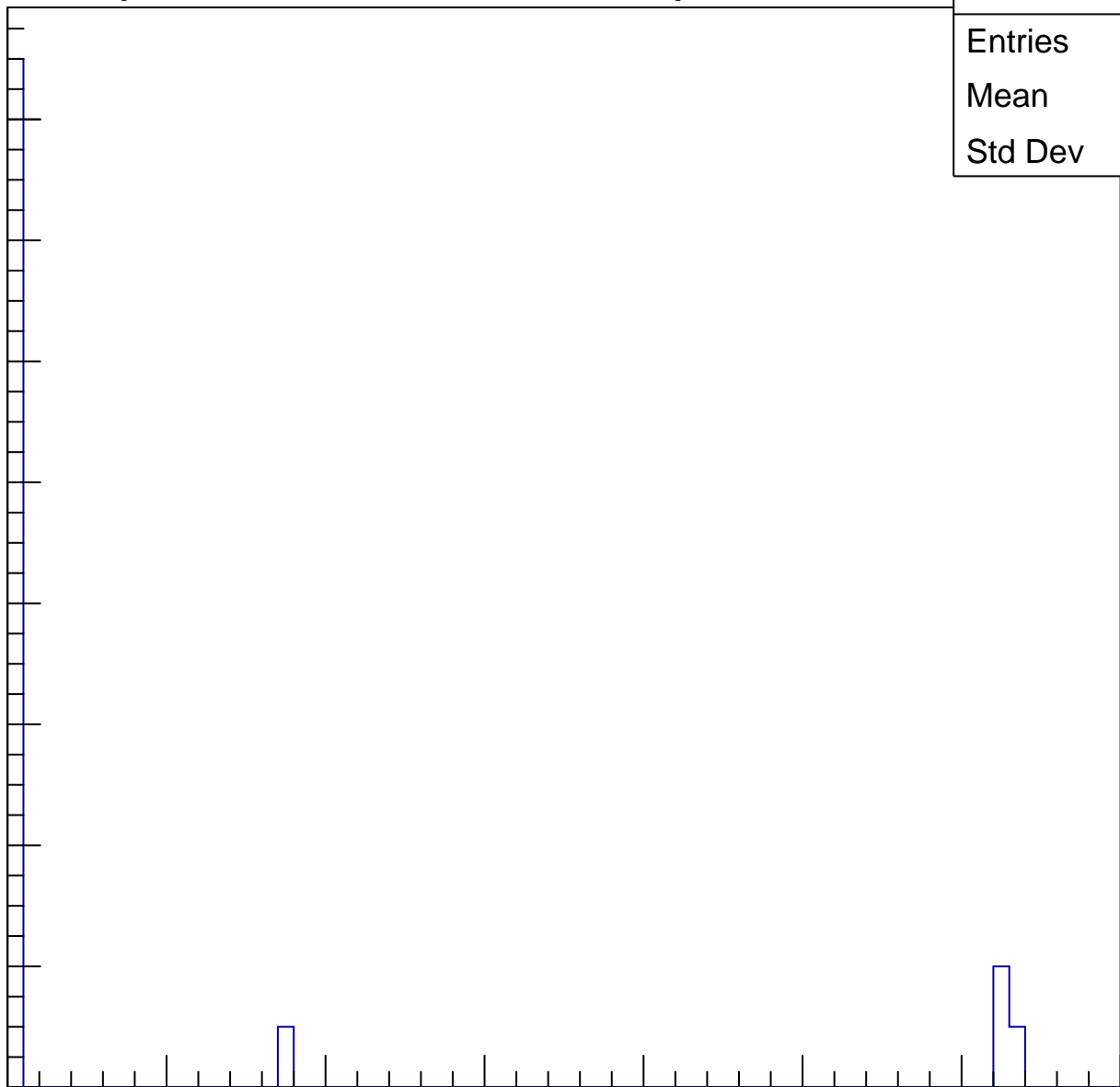
Entries	21
Mean	9.714
Std Dev	21.78

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch33, adc0

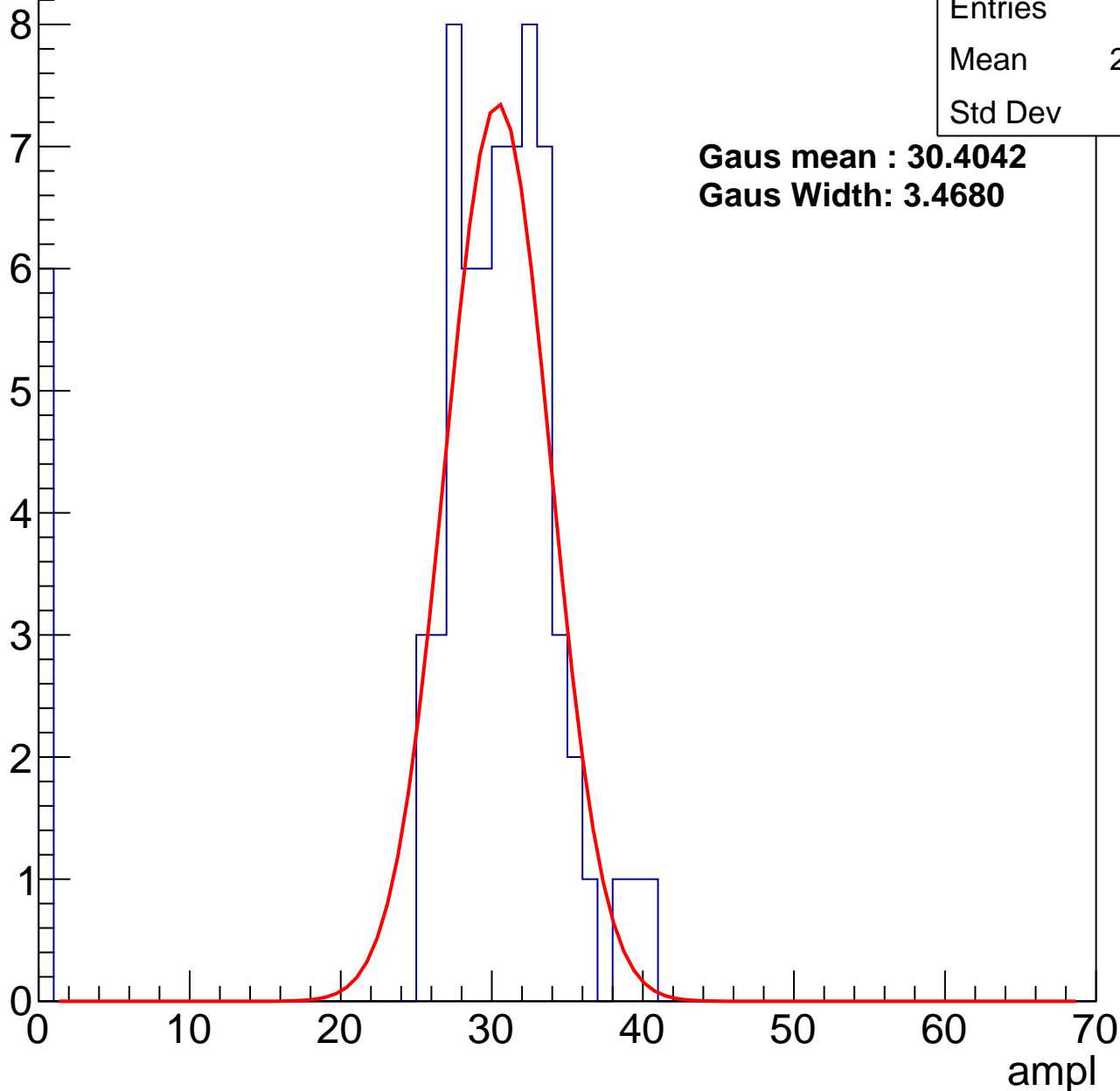
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	27.86
Std Dev	9.09

Gaus mean : 30.4042

Gaus Width: 3.4680



B1L103S, U8-ch33, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	31.65
Std Dev	11.83

Gaus mean : 36.9614

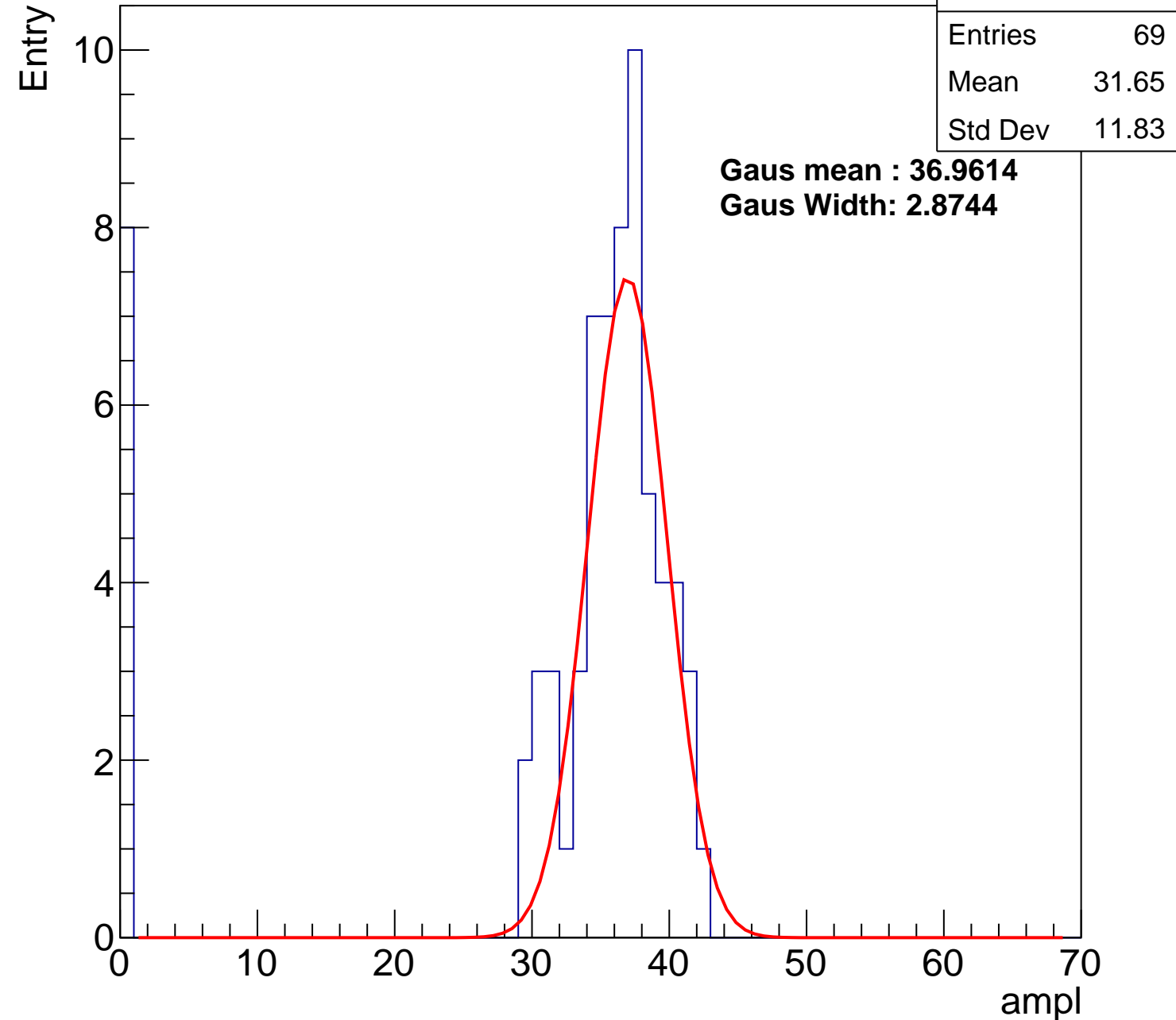
Gaus Width: 2.8744

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch33, adc2

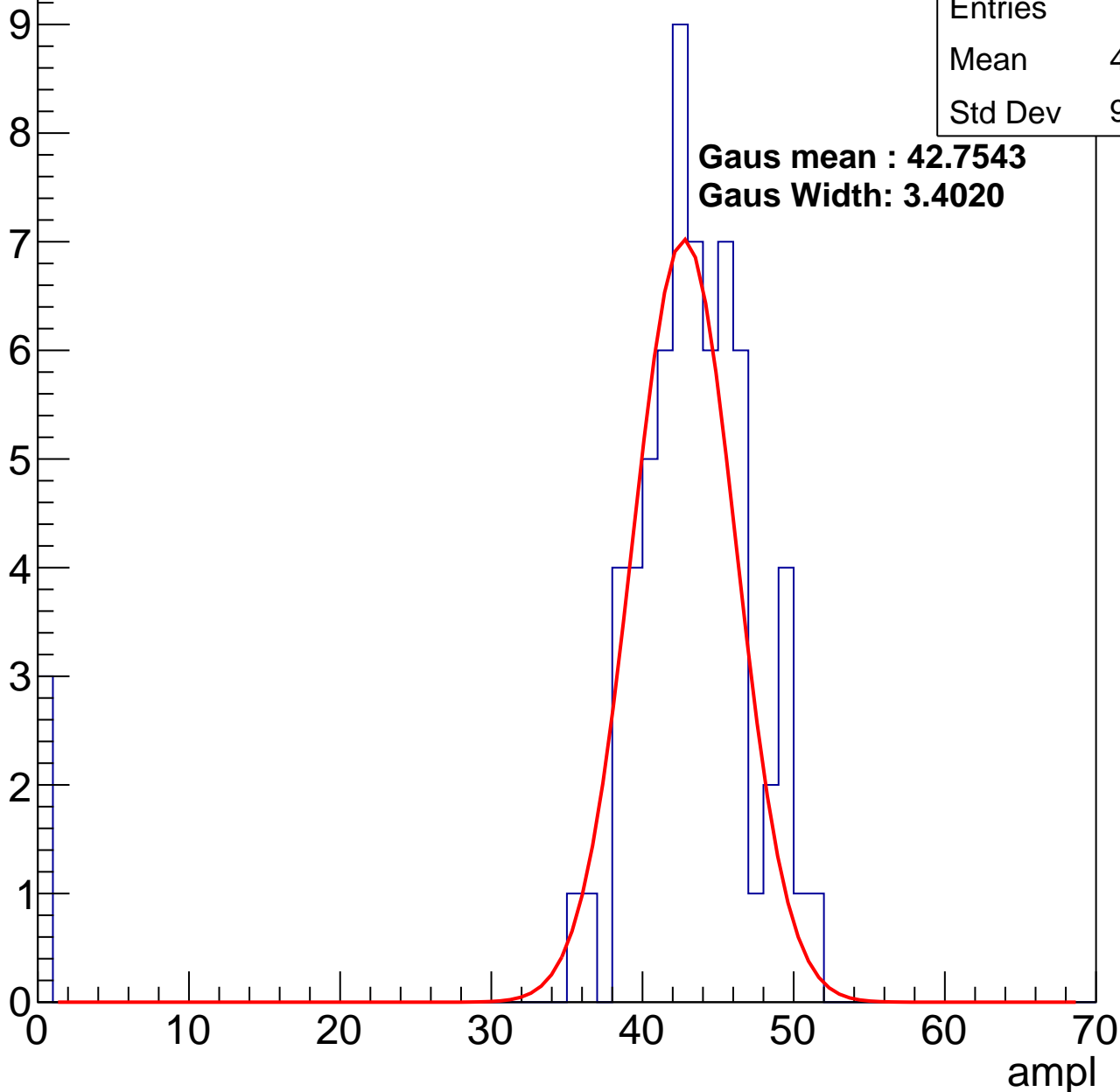
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.16
Std Dev	9.458

Gaus mean : 42.7543

Gaus Width: 3.4020

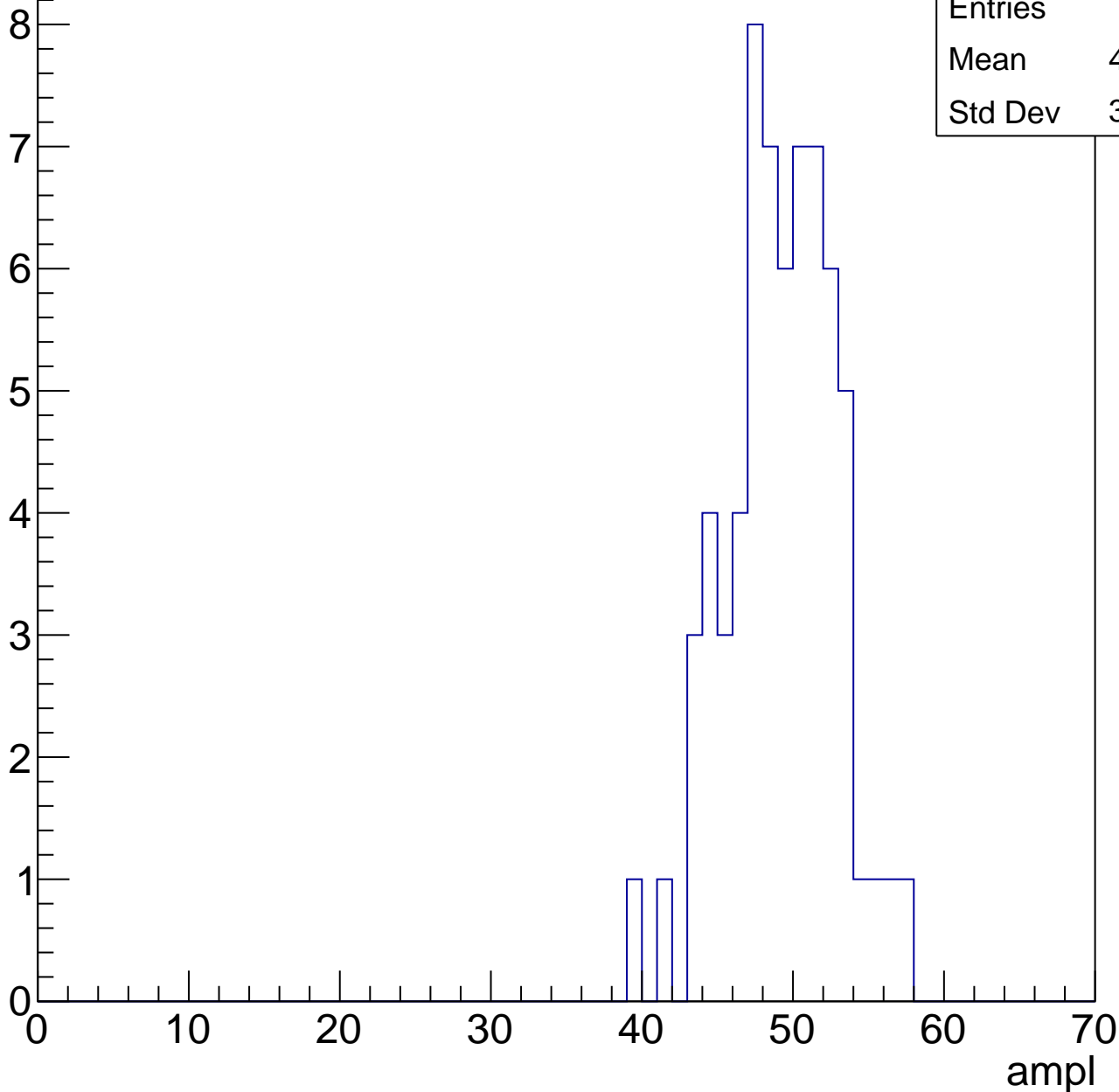


B1L103S, U8-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	48.73
Std Dev	3.553

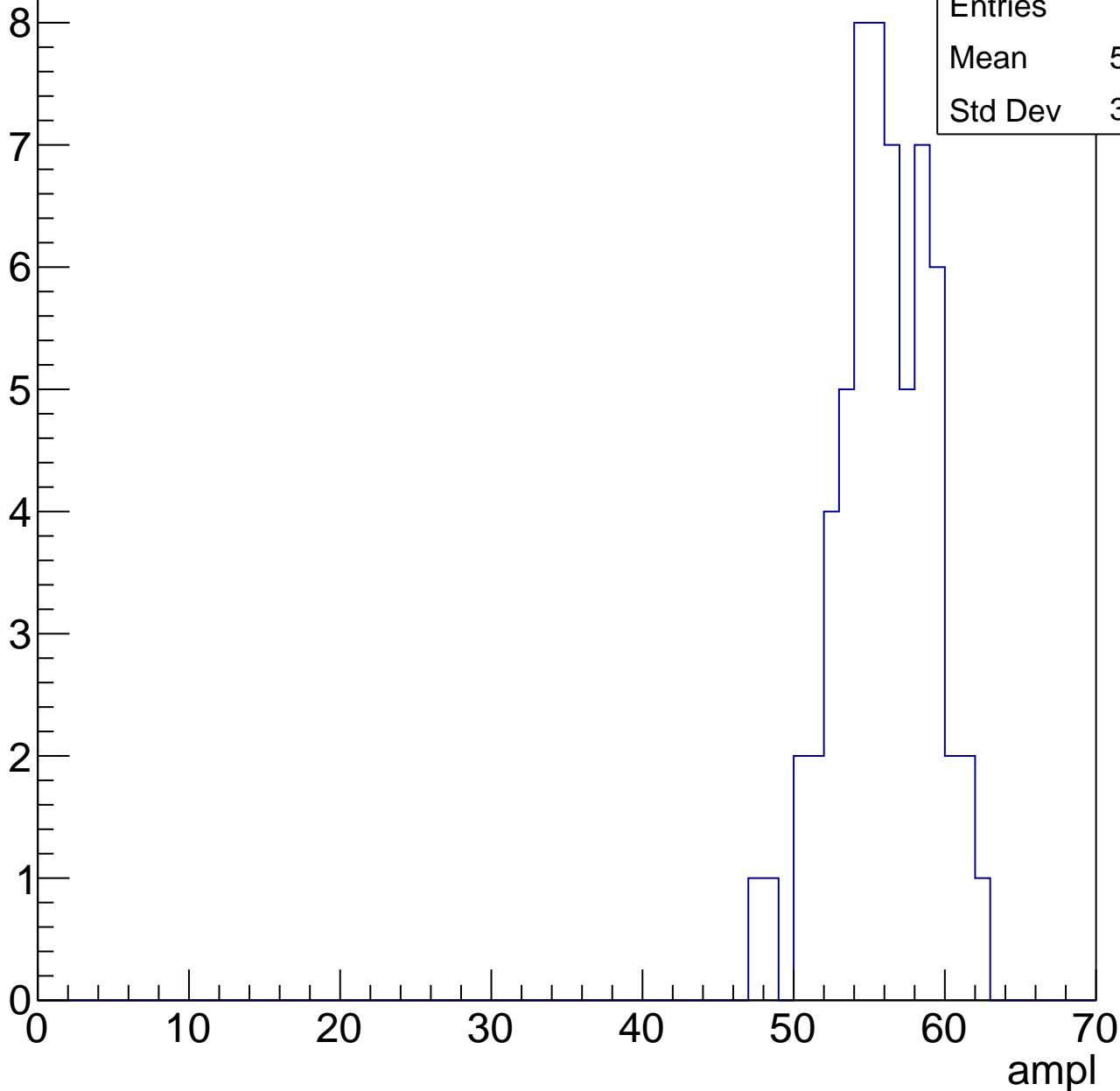


B1L103S, U8-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

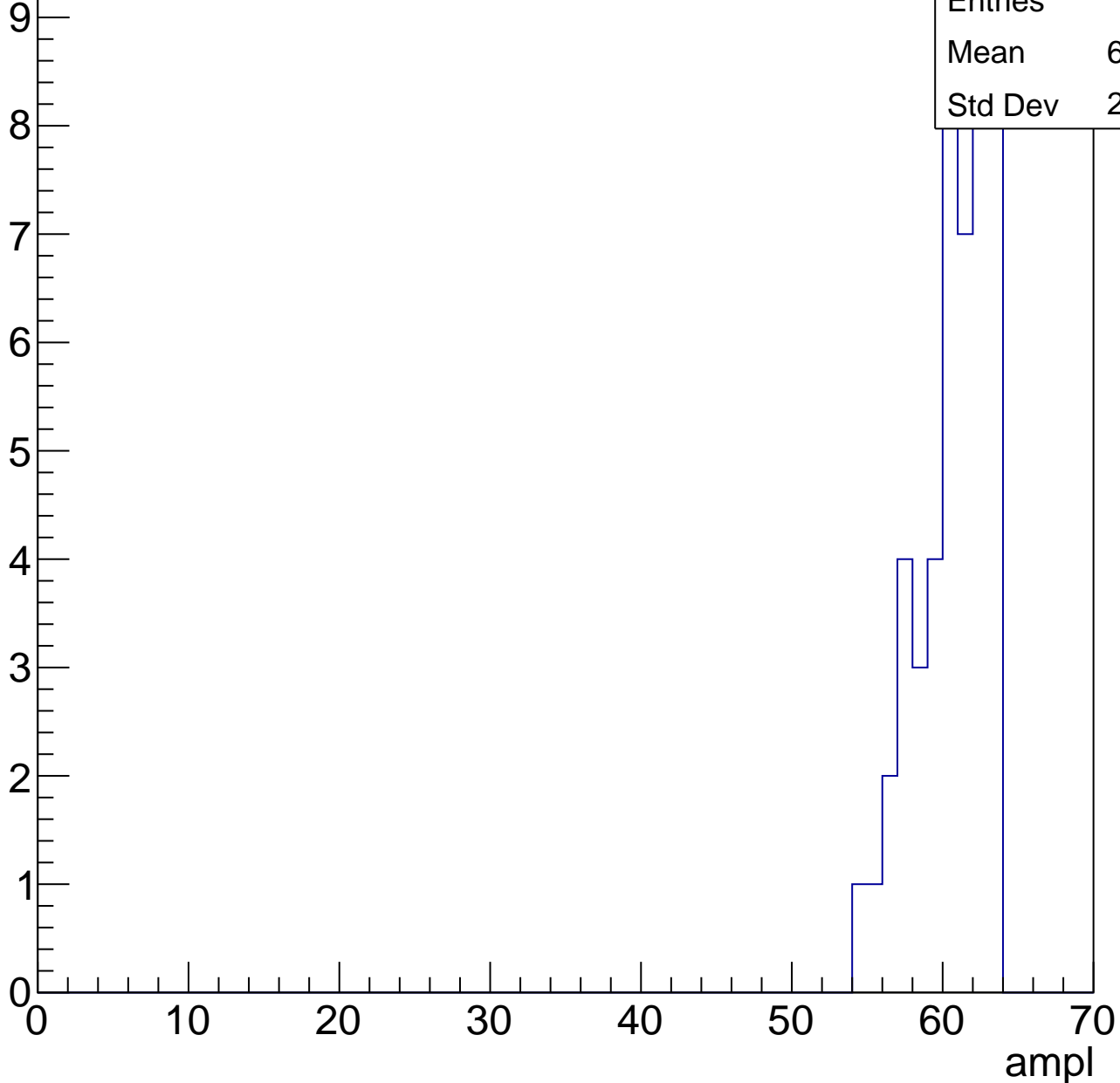
Entries	61
Mean	55.46
Std Dev	3.144



B1L103S, U8-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

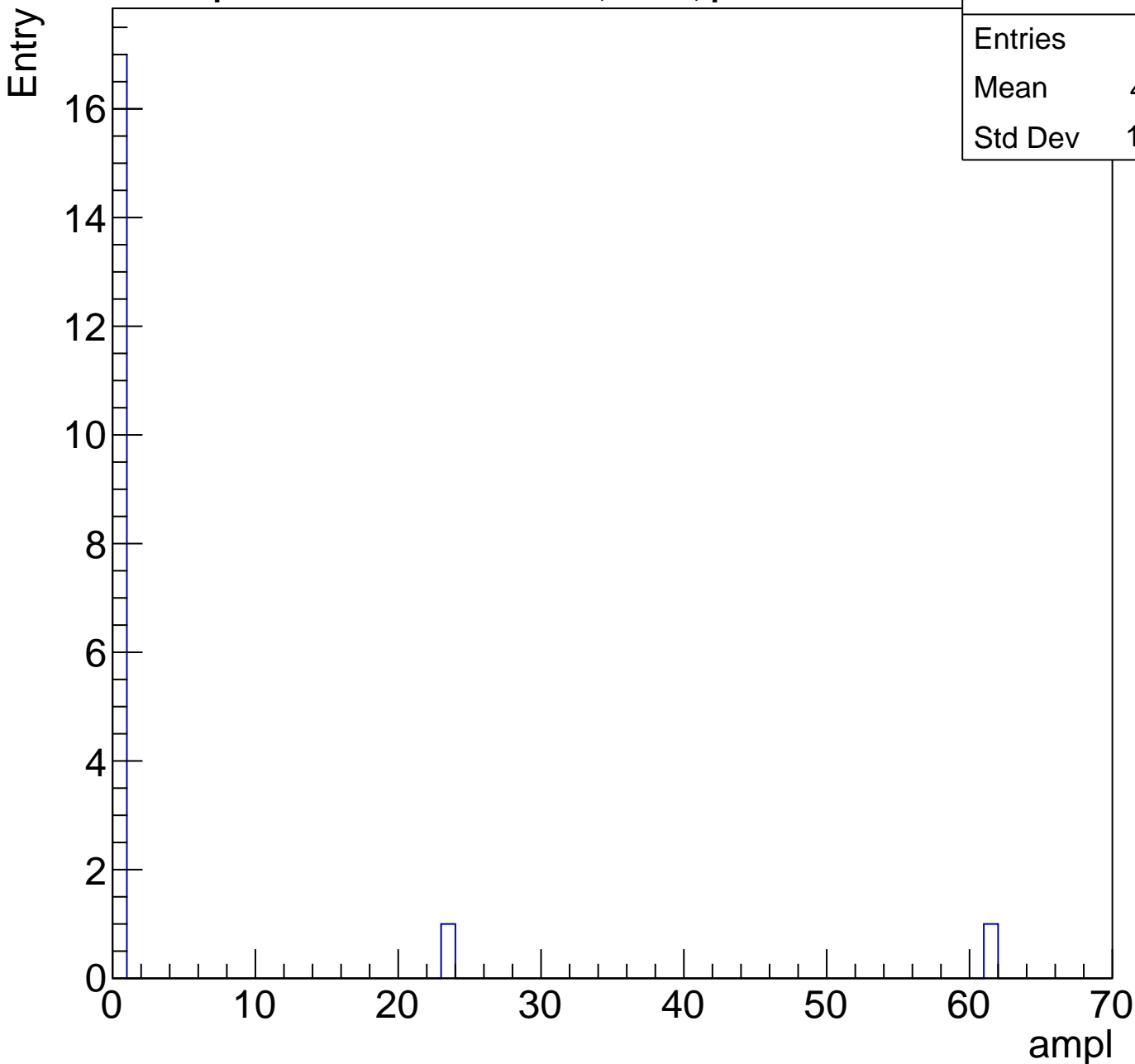
Entry



B1L103S, U8-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.421
Std Dev	14.29



B1L103S, U8-ch34, adc0

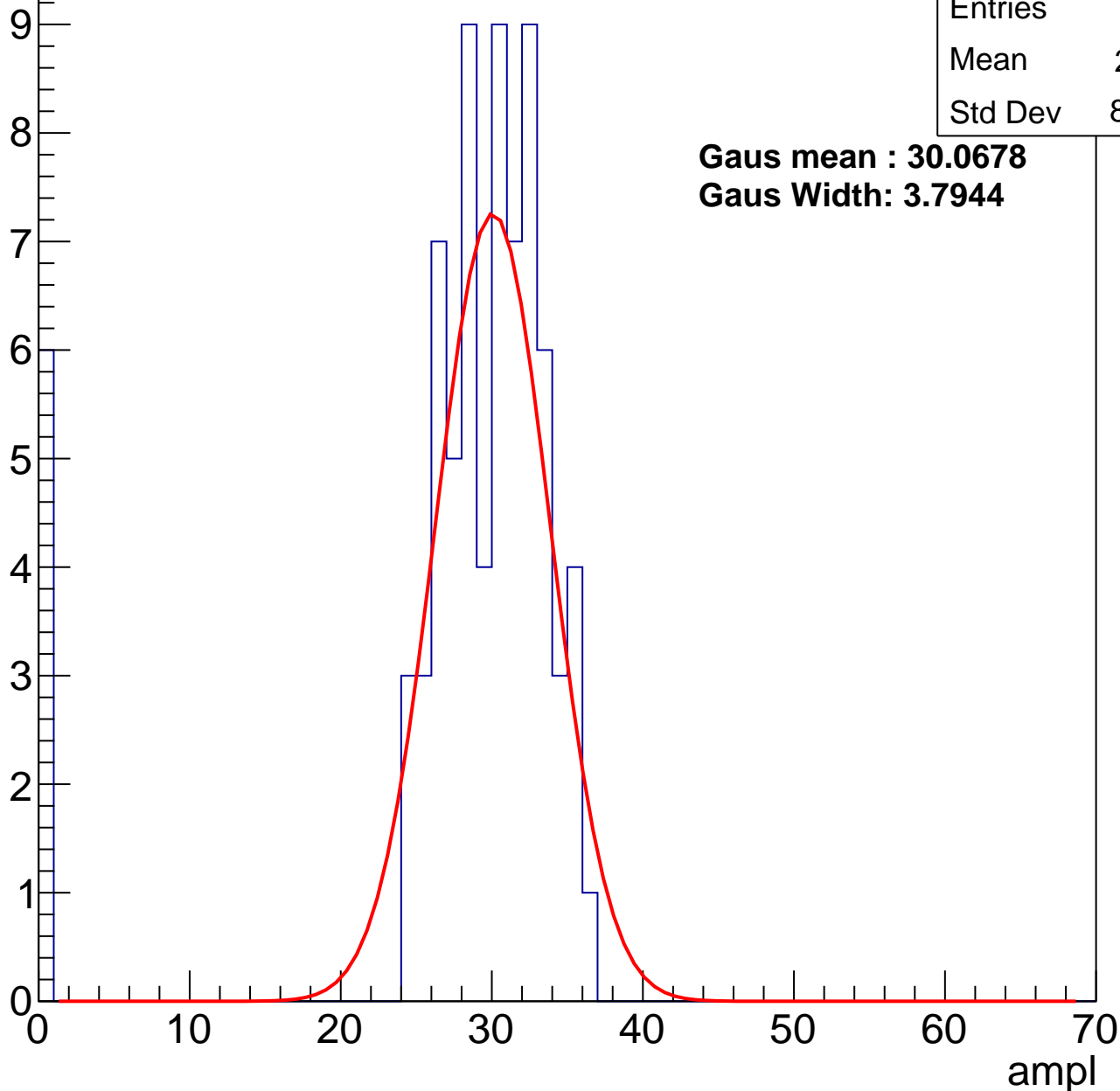
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	27.41
Std Dev	8.546

Gaus mean : 30.0678

Gaus Width: 3.7944



B1L103S, U8-ch34, adc1

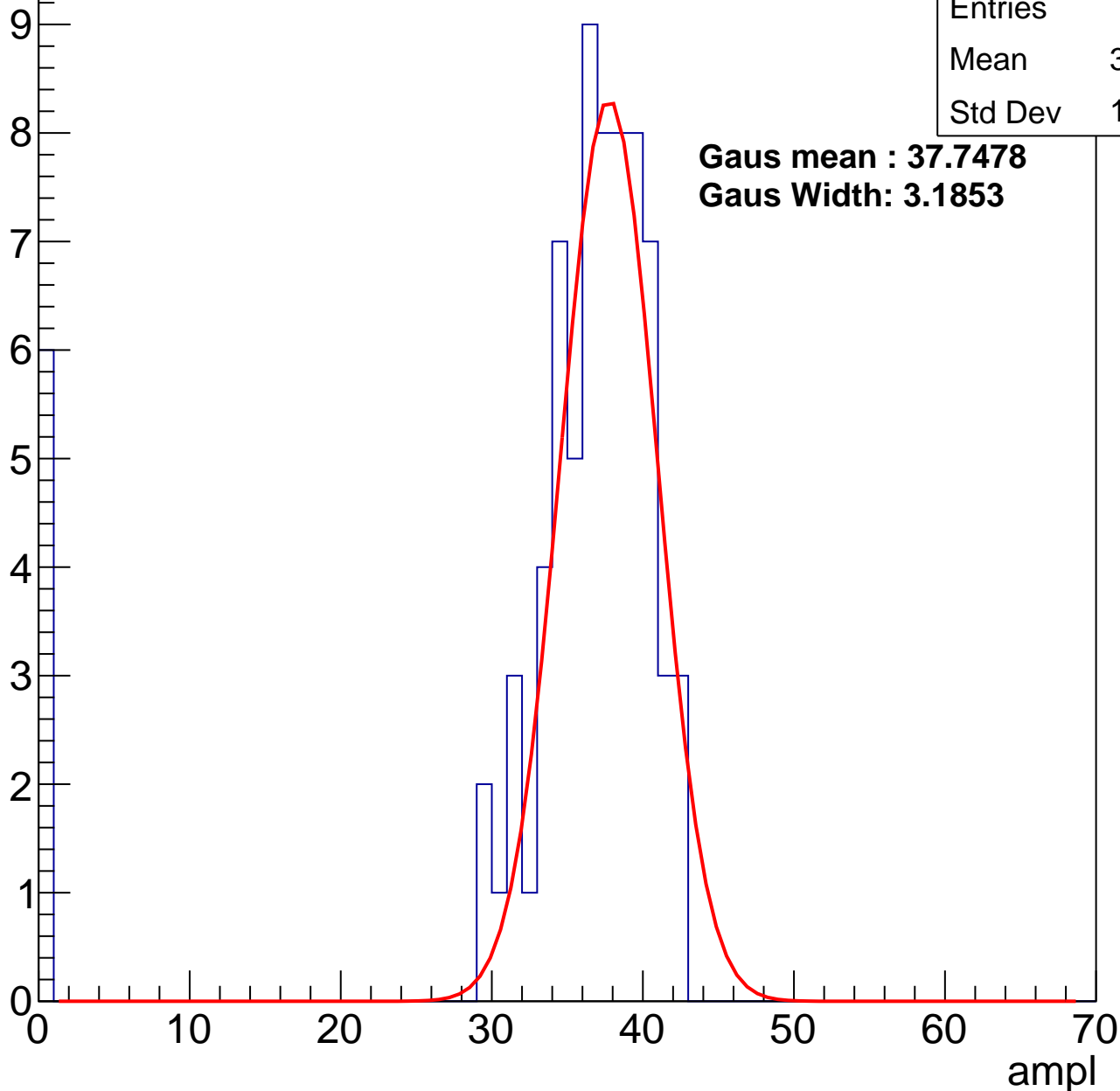
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	33.64
Std Dev	10.37

Gaus mean : 37.7478

Gaus Width: 3.1853



B1L103S, U8-ch34, adc2

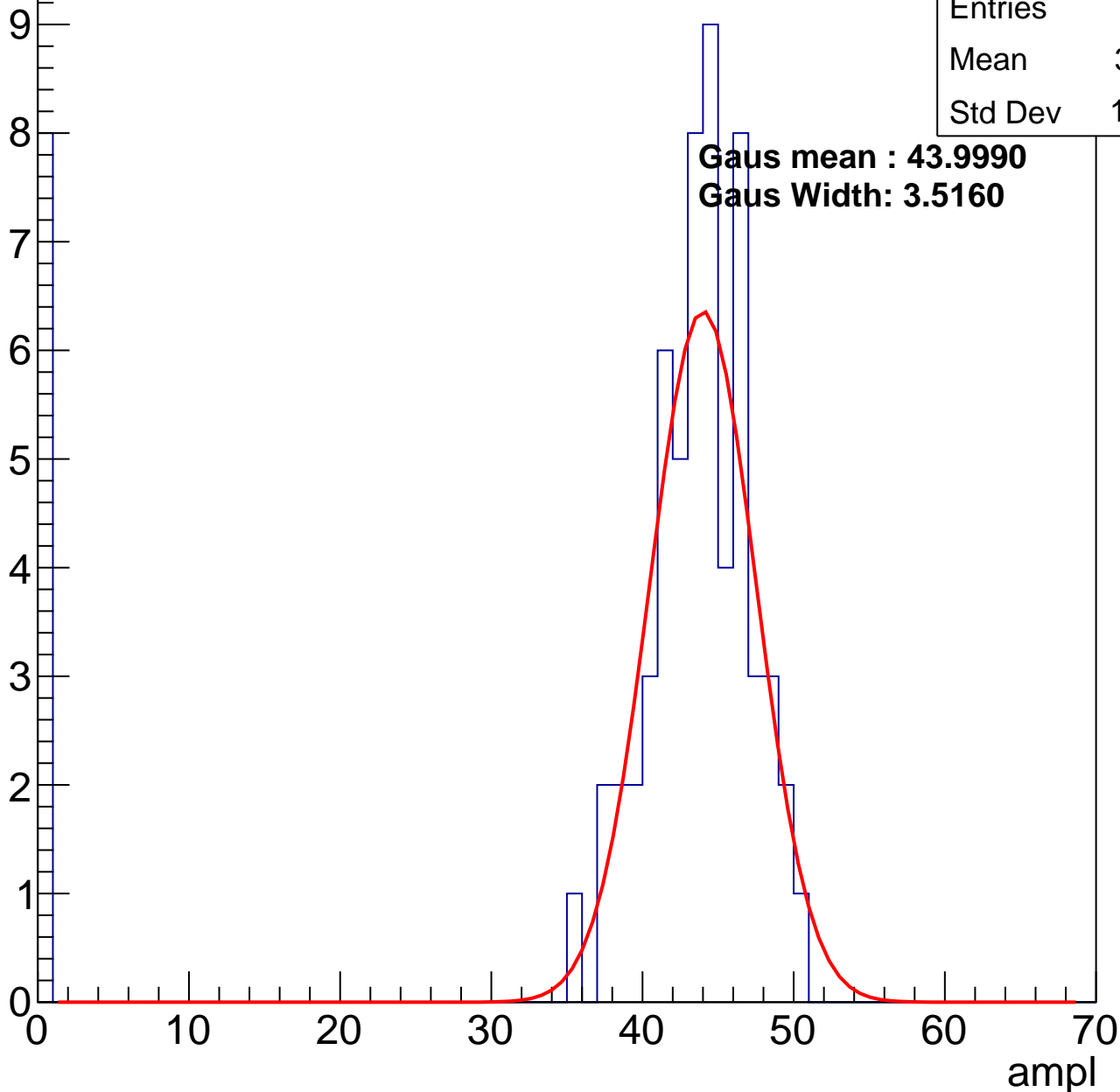
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	38.21
Std Dev	14.39

Gaus mean : 43.9990

Gaus Width: 3.5160

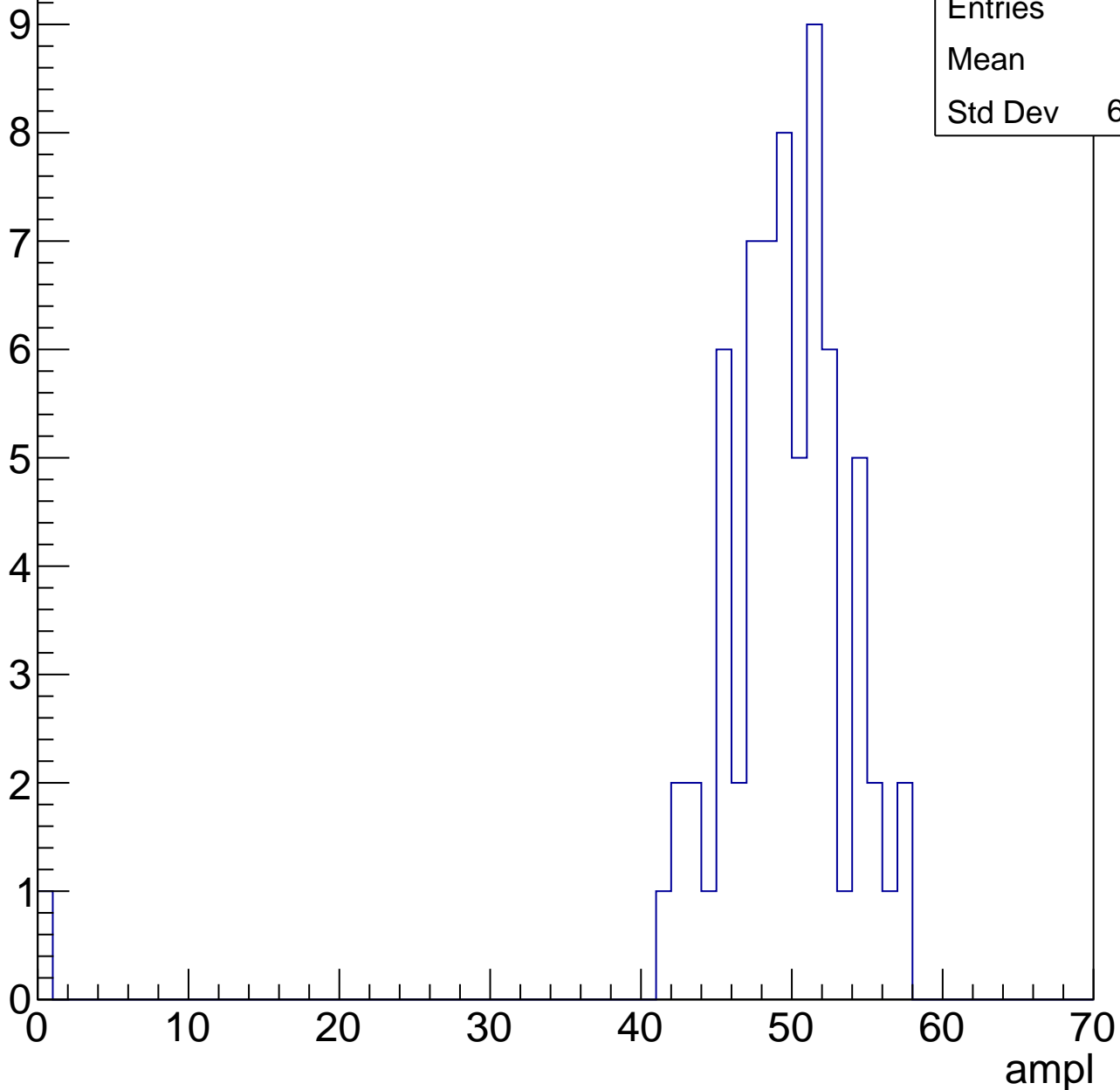


B1L103S, U8-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	48.5
Std Dev	6.942

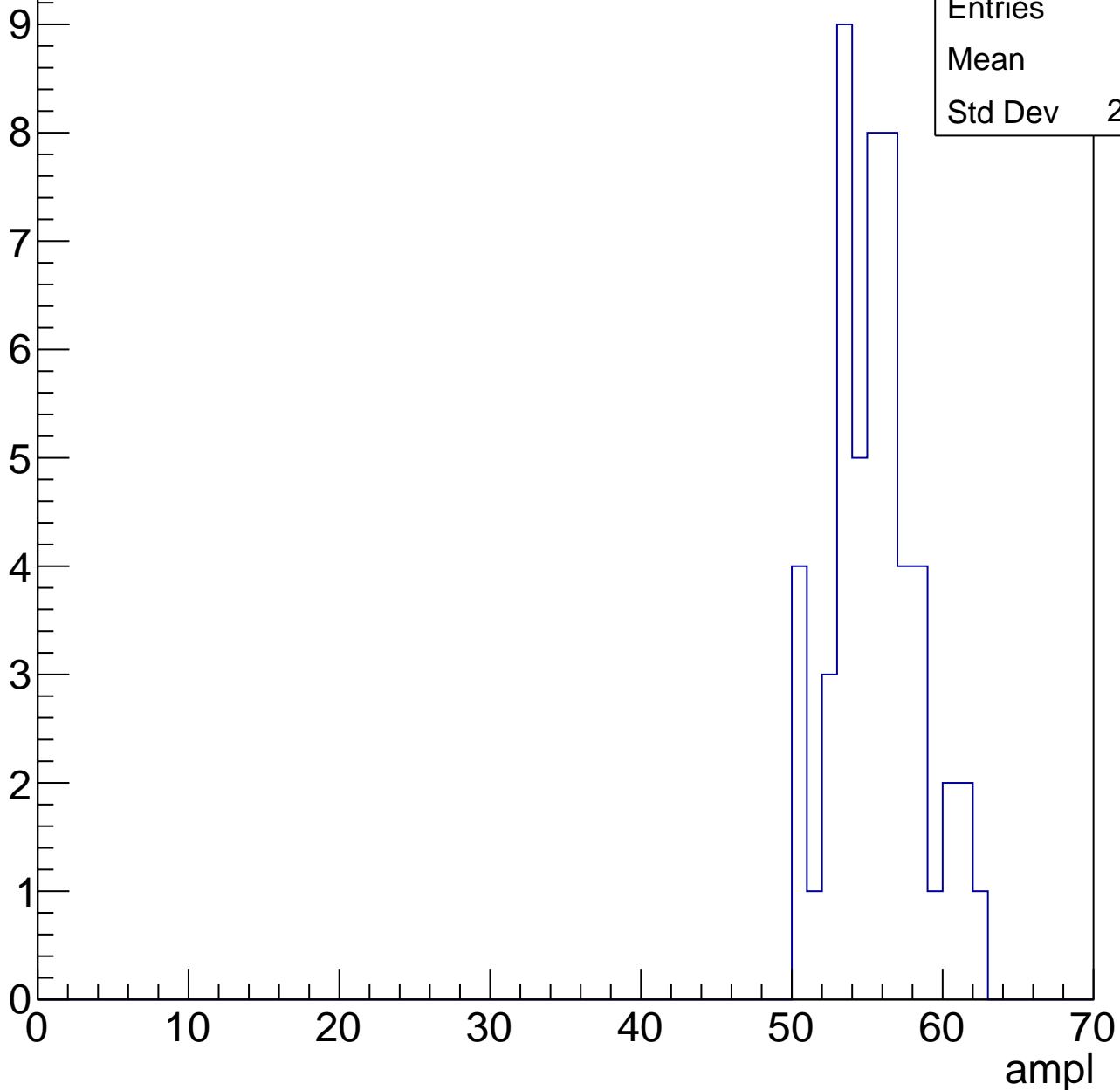


B1L103S, U8-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

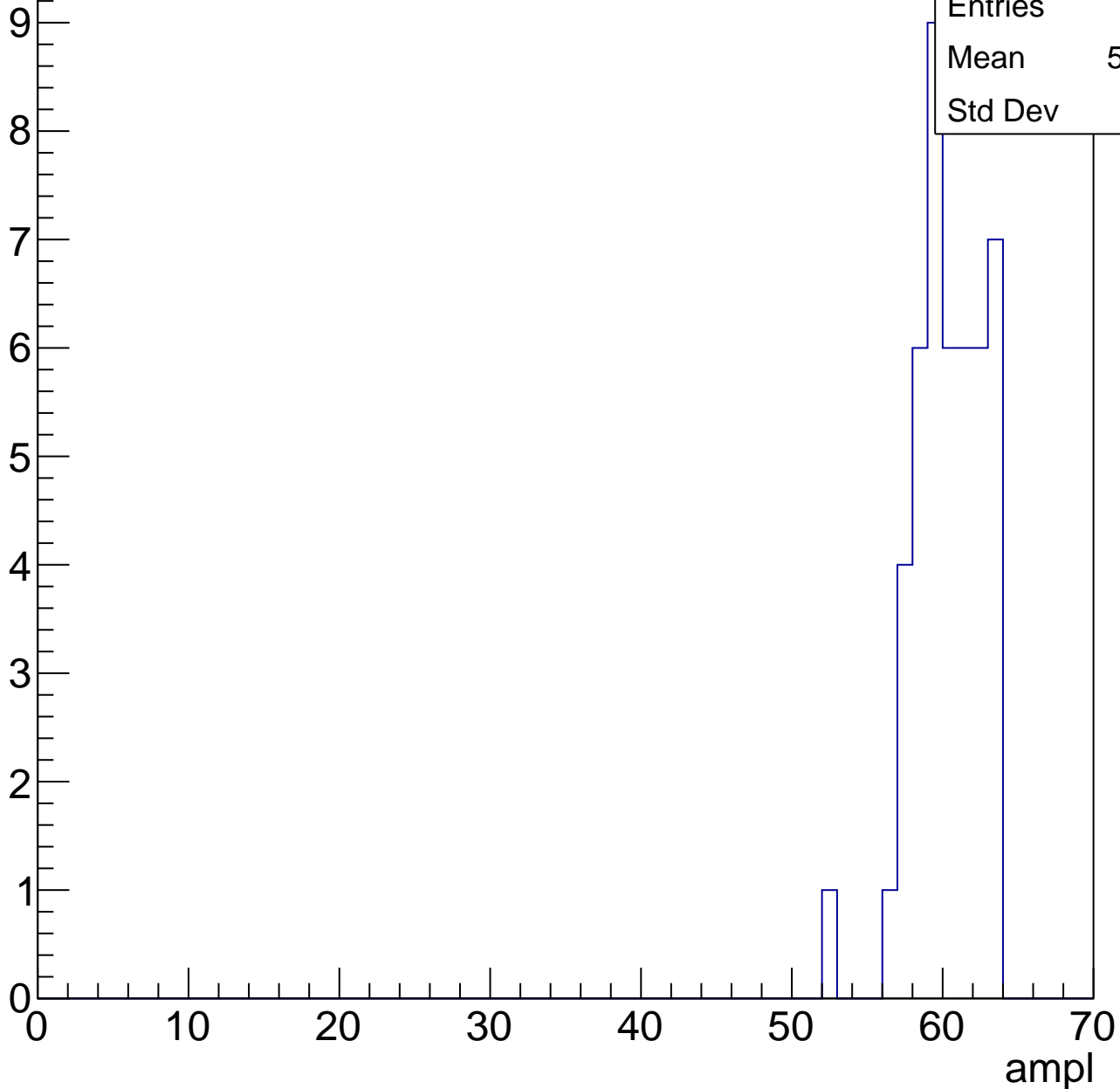
Entries	52
Mean	55.1
Std Dev	2.877



B1L103S, U8-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

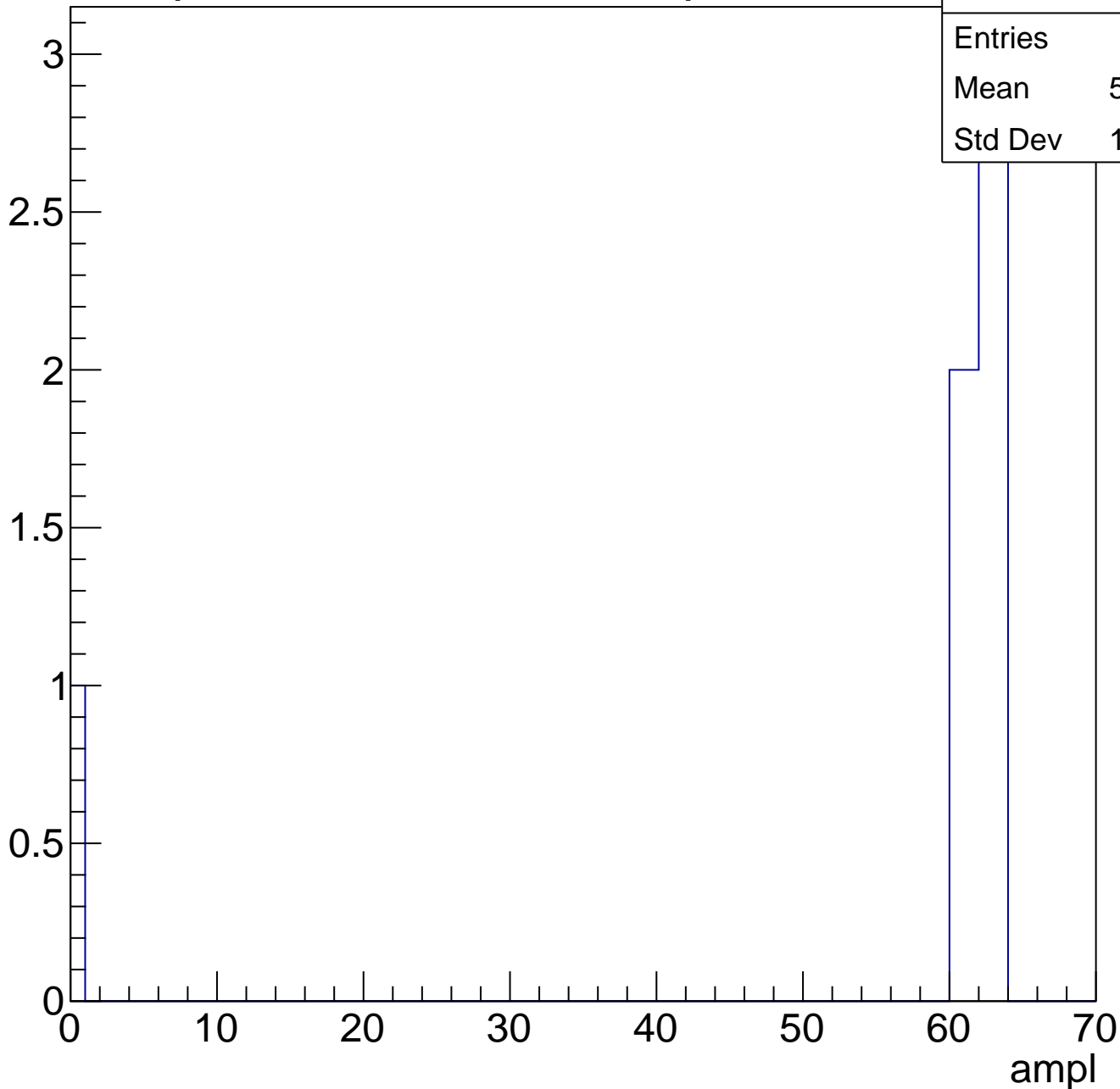
Entry



B1L103S, U8-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch35, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	27.8
Std Dev	9.461

Gaus mean : 31.2670

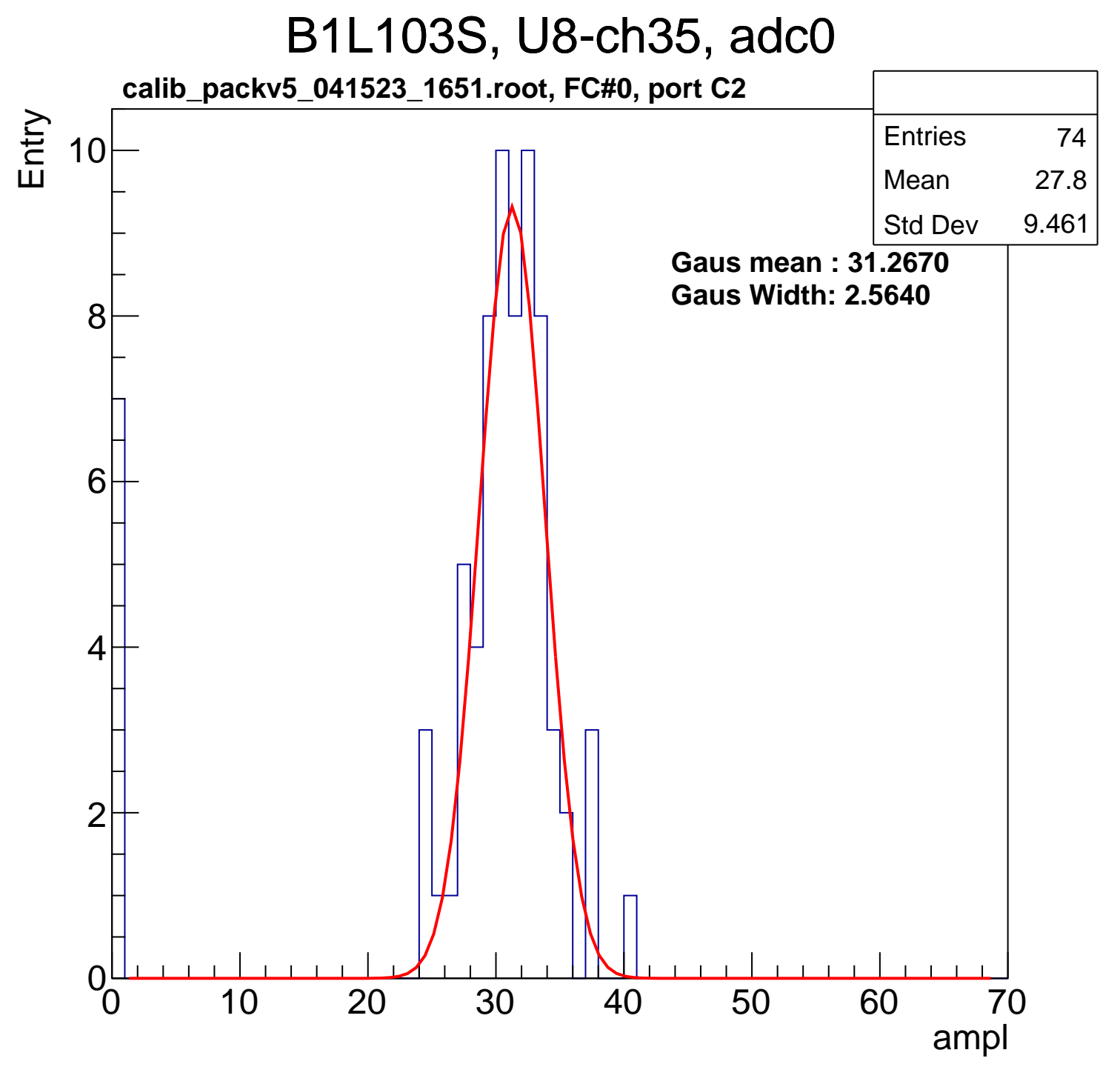
Gaus Width: 2.5640

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch35, adc1

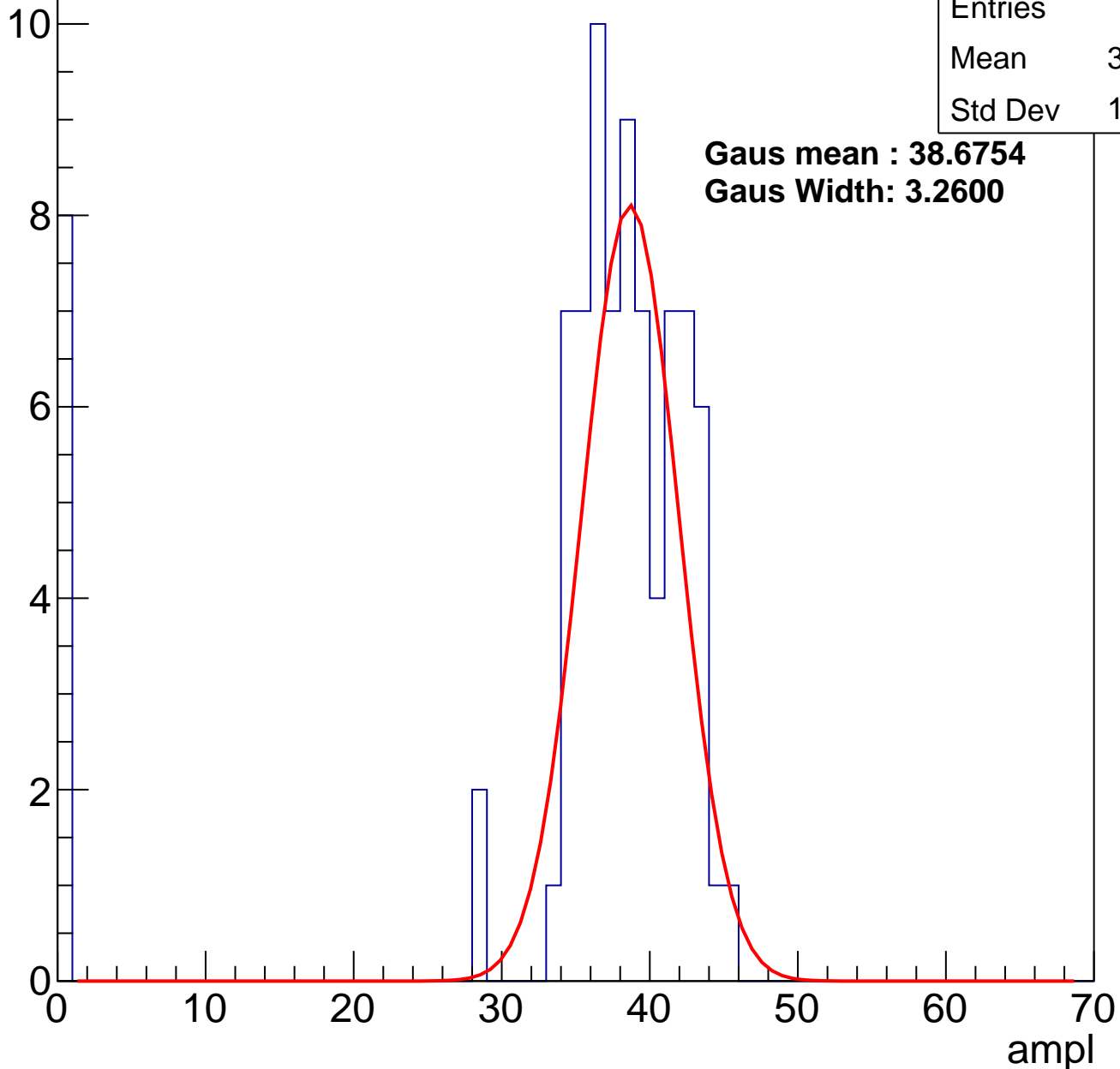
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	34.45
Std Dev	11.64

Gaus mean : 38.6754

Gaus Width: 3.2600

Entry



B1L103S, U8-ch35, adc2

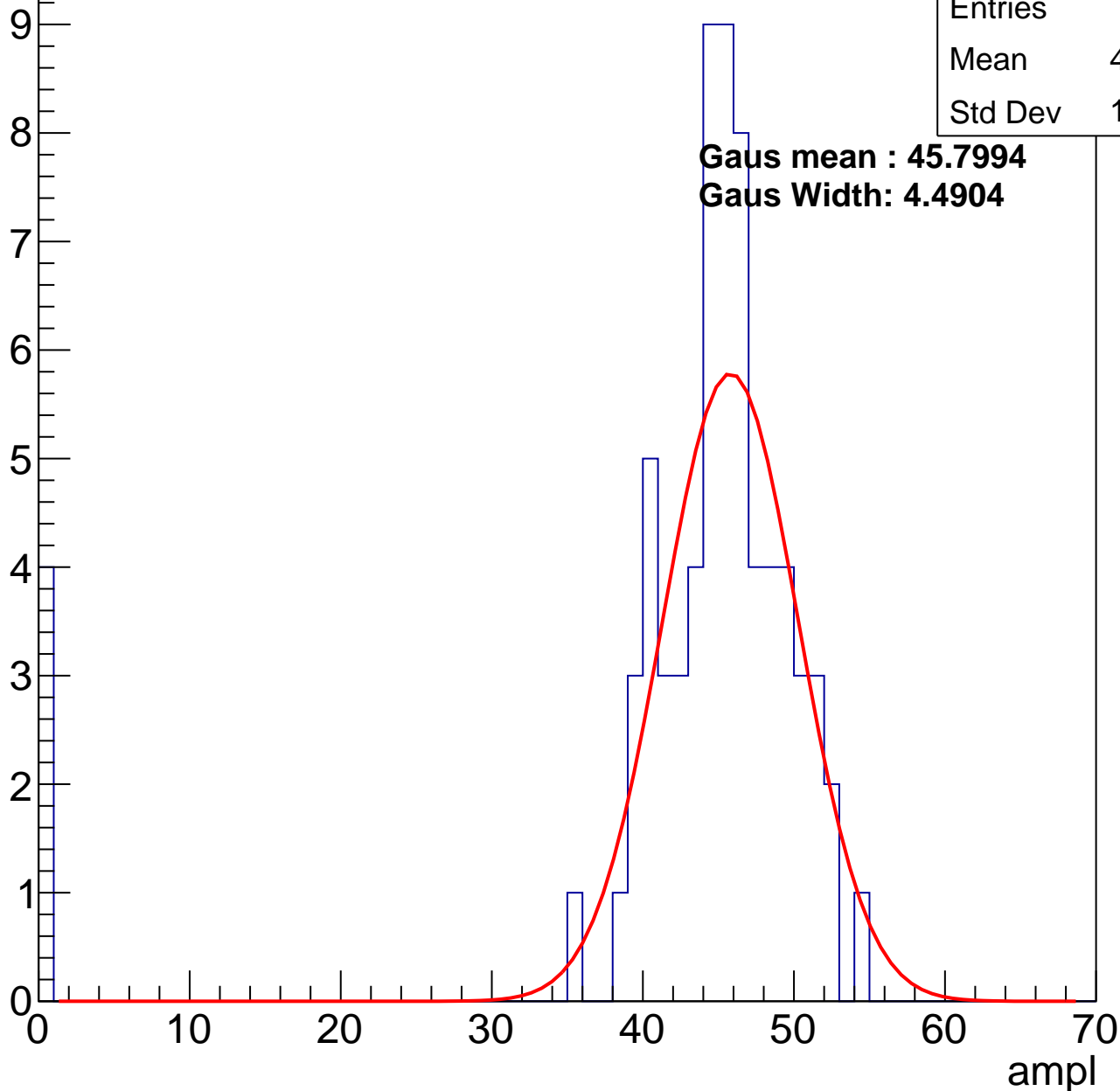
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	42.49
Std Dev	11.02

Gaus mean : 45.7994

Gaus Width: 4.4904

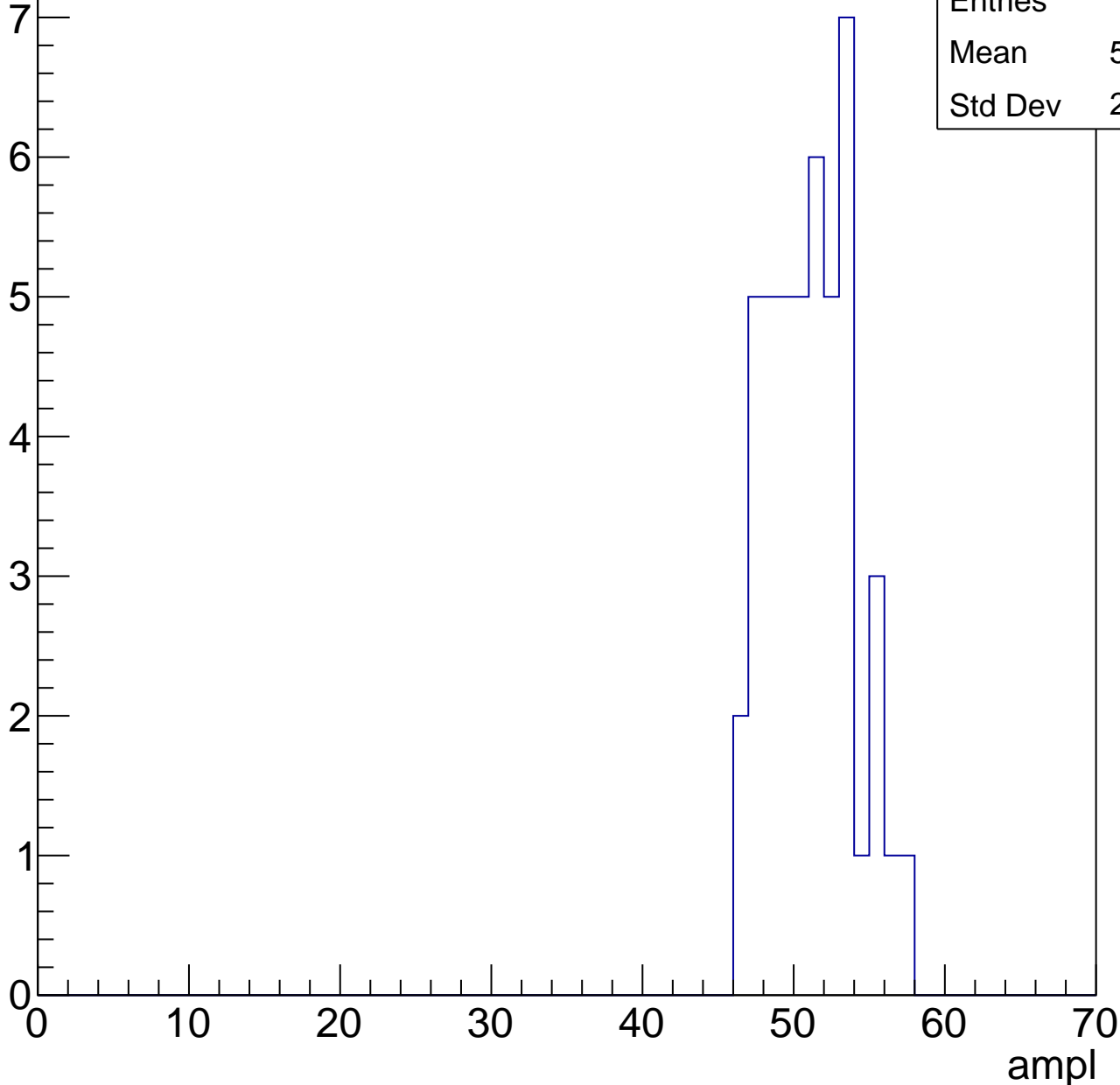


B1L103S, U8-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

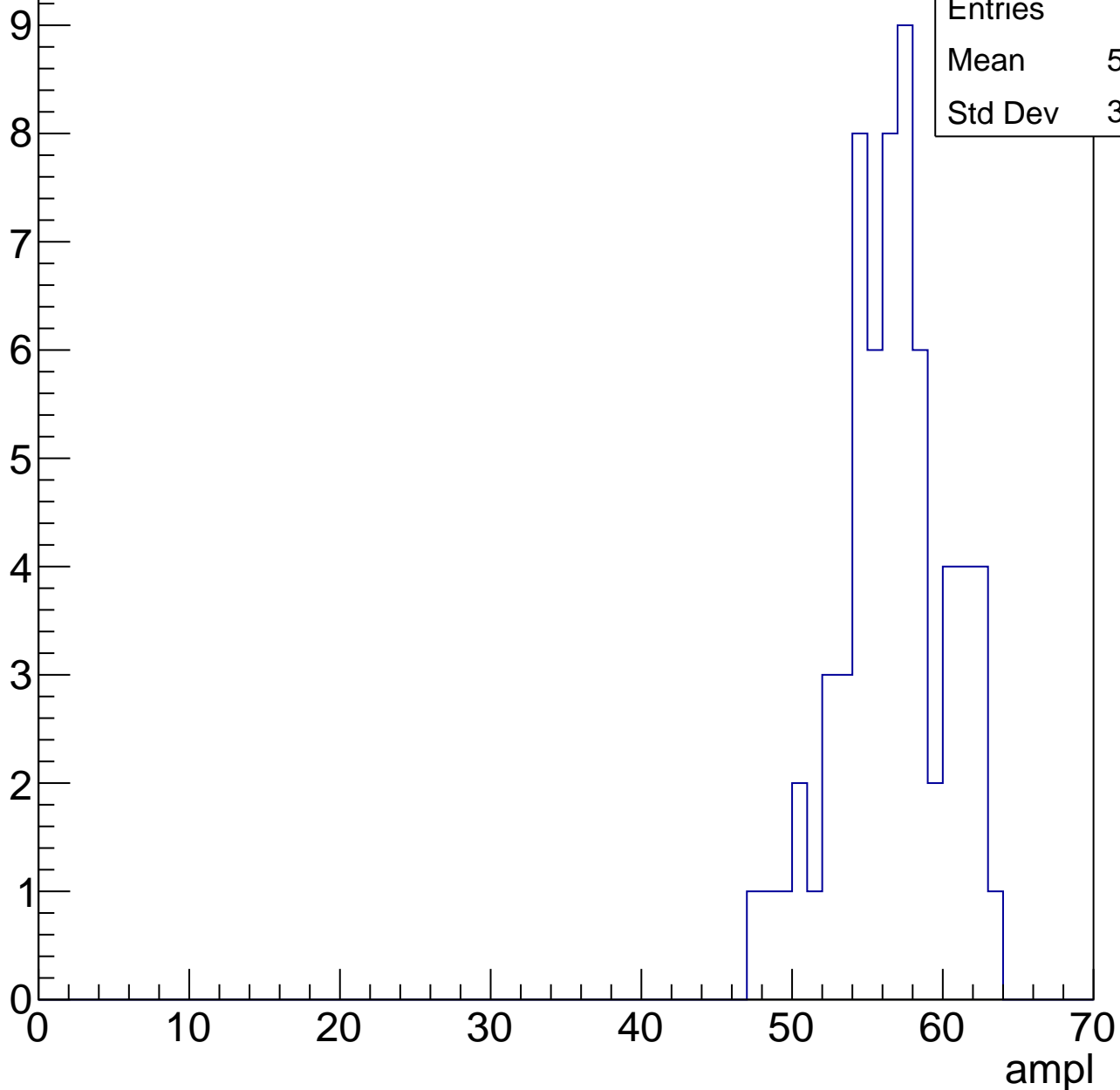
Entries	46
Mean	50.67
Std Dev	2.743



B1L103S, U8-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

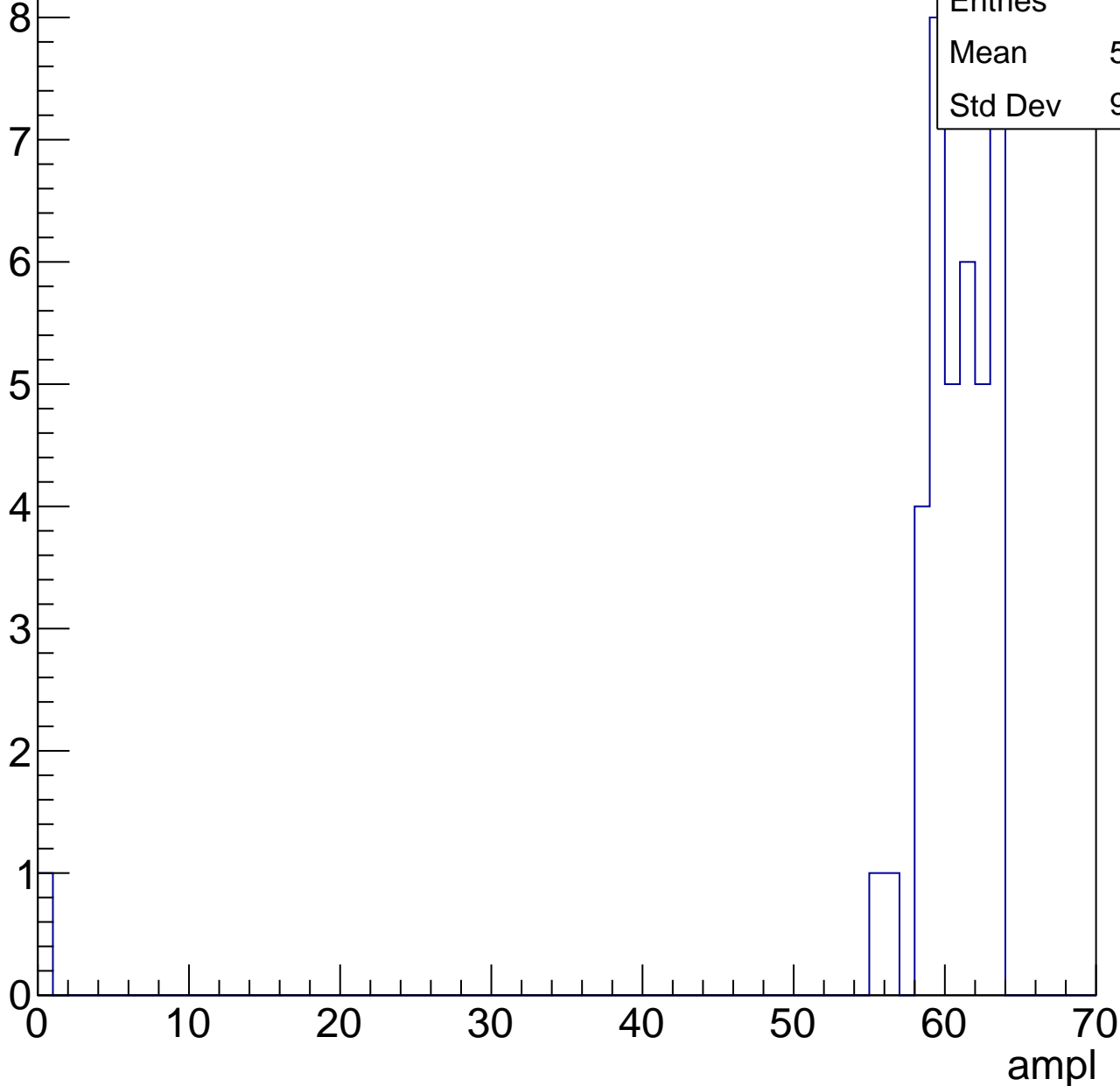


B1L103S, U8-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

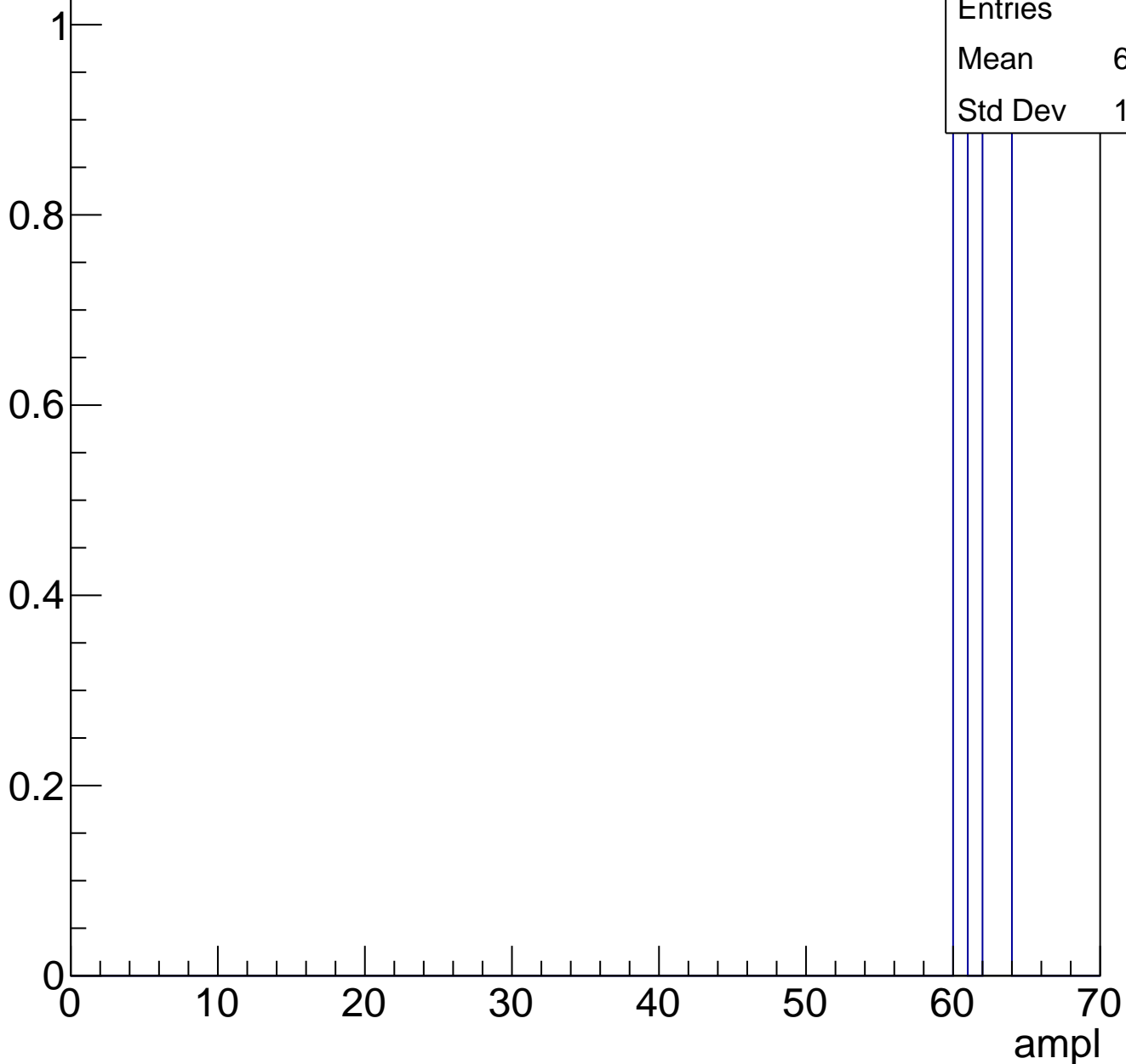
Entries	39
Mean	58.85
Std Dev	9.755



B1L103S, U8-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

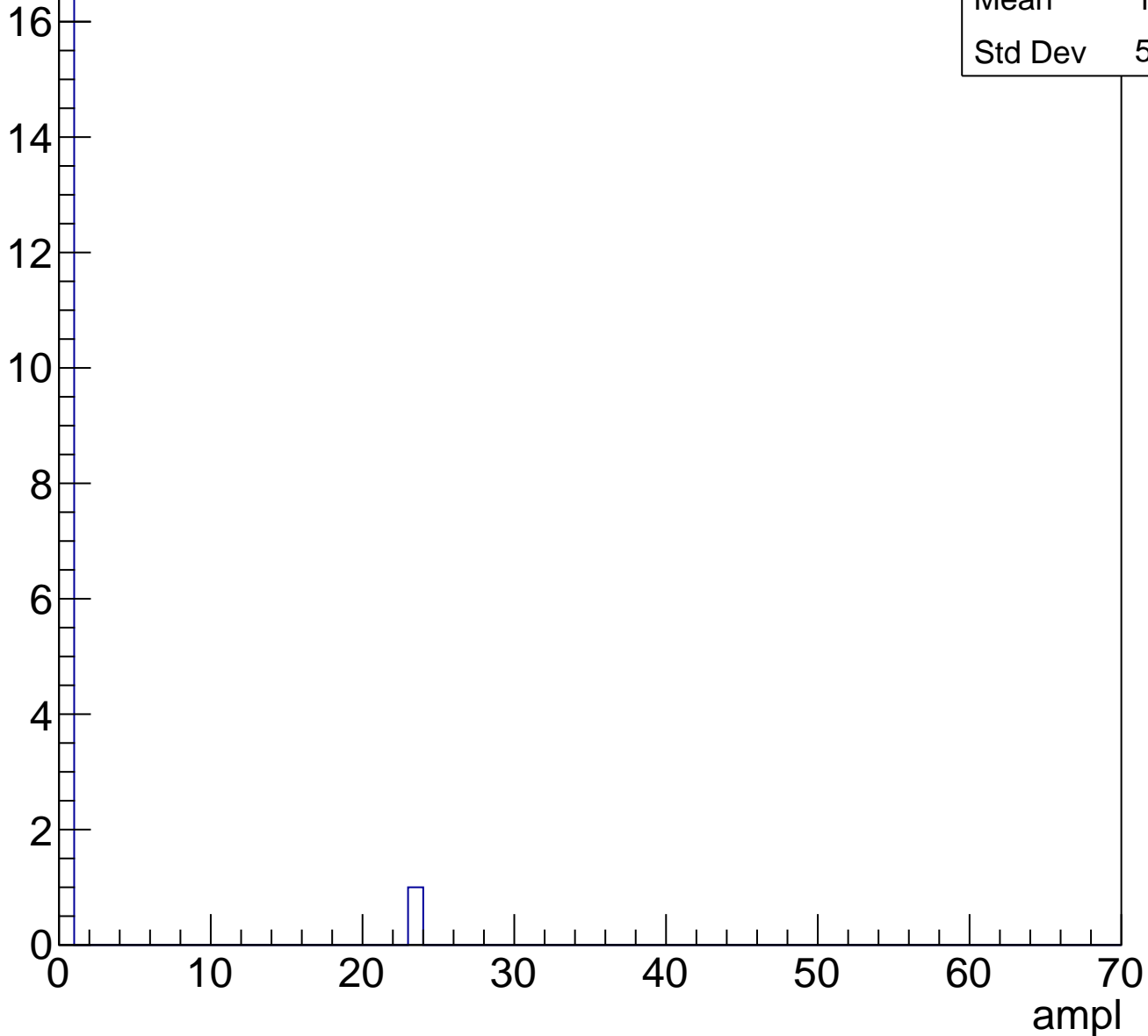
Entry



B1L103S, U8-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	1.278
Std Dev	5.268

B1L103S, U8-ch36, adc0

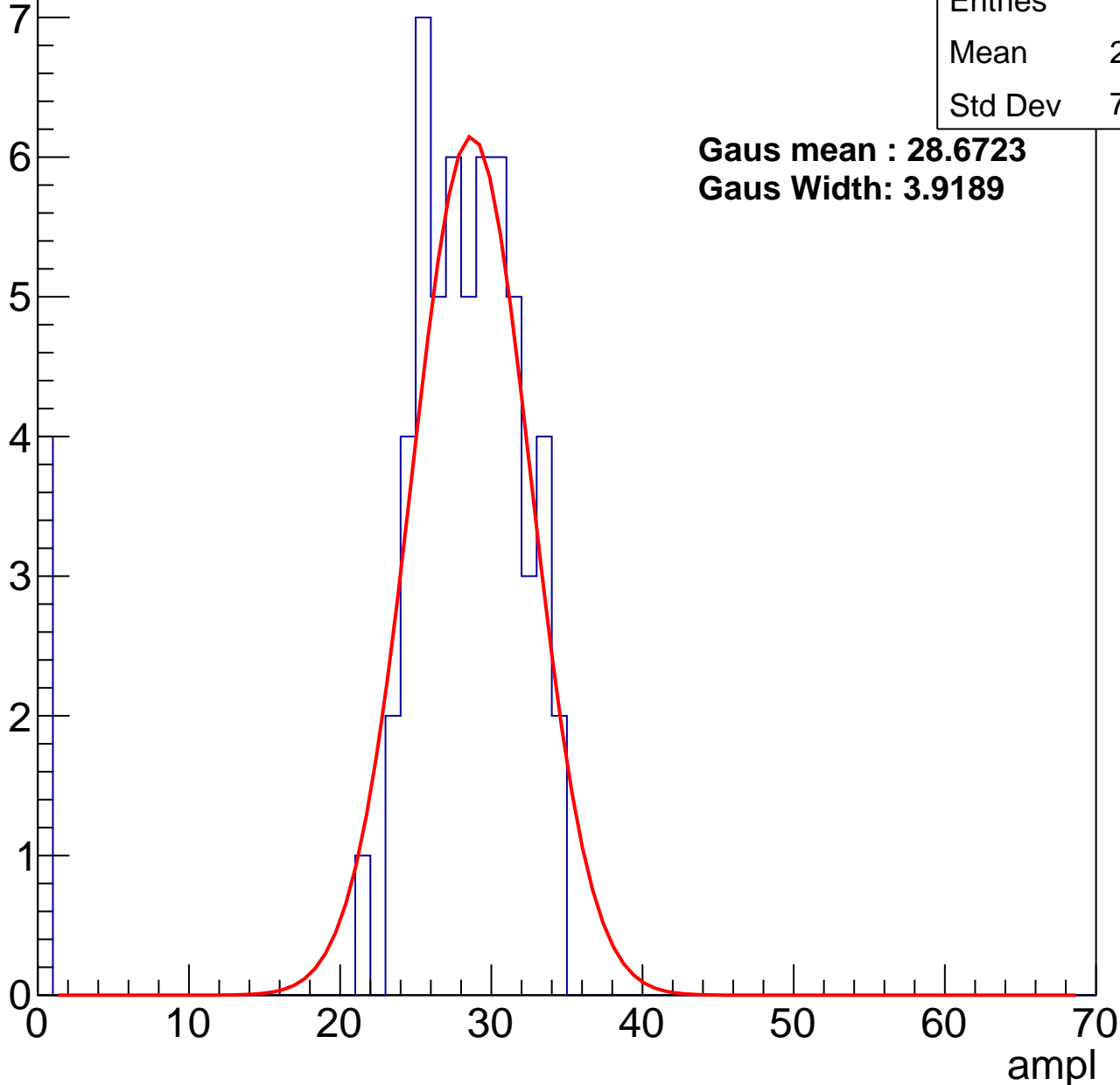
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	26.25
Std Dev	7.637

Gaus mean : 28.6723

Gaus Width: 3.9189



B1L103S, U8-ch36, adc1

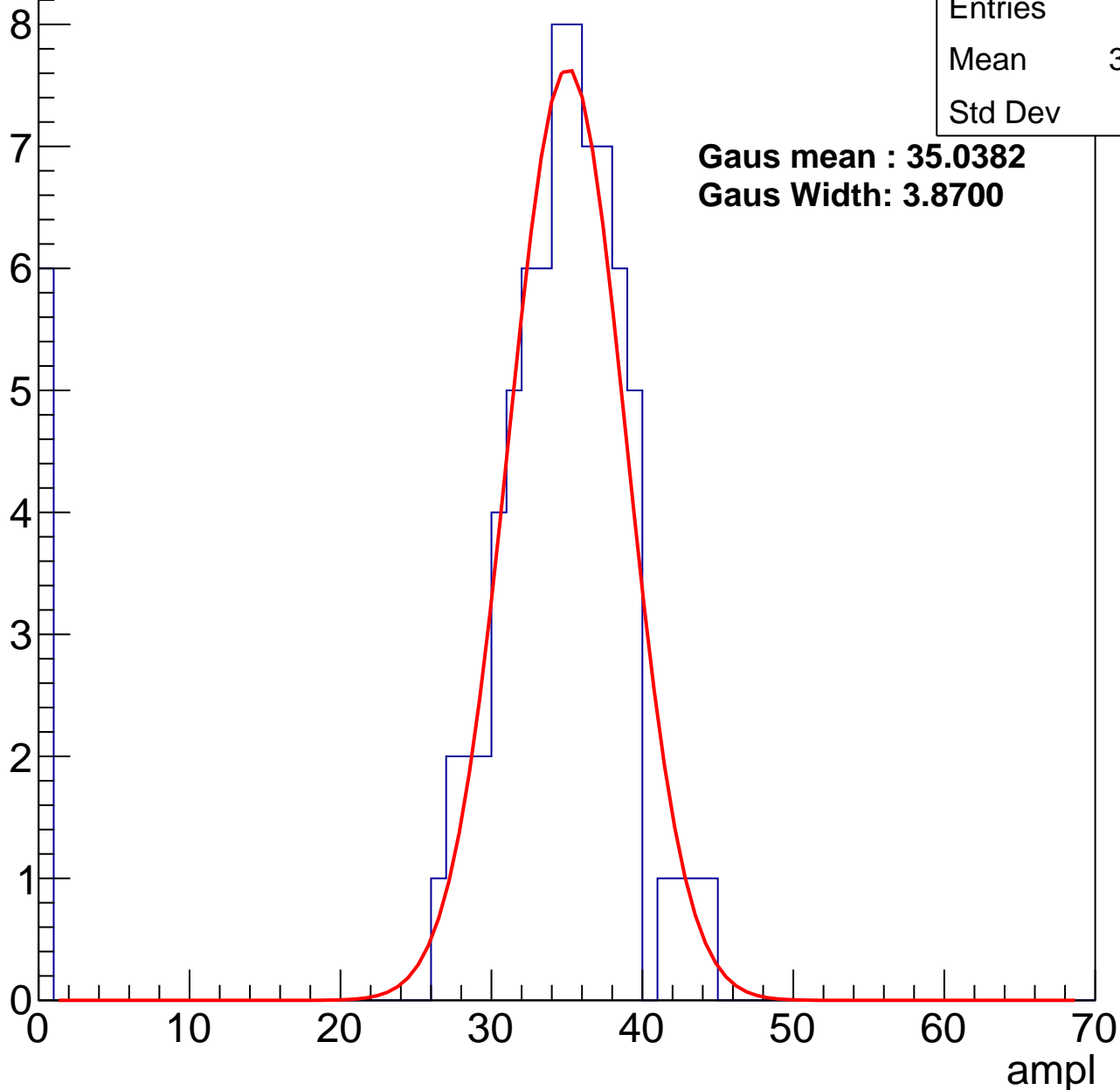
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	31.84
Std Dev	9.81

Gaus mean : 35.0382

Gaus Width: 3.8700



B1L103S, U8-ch36, adc2

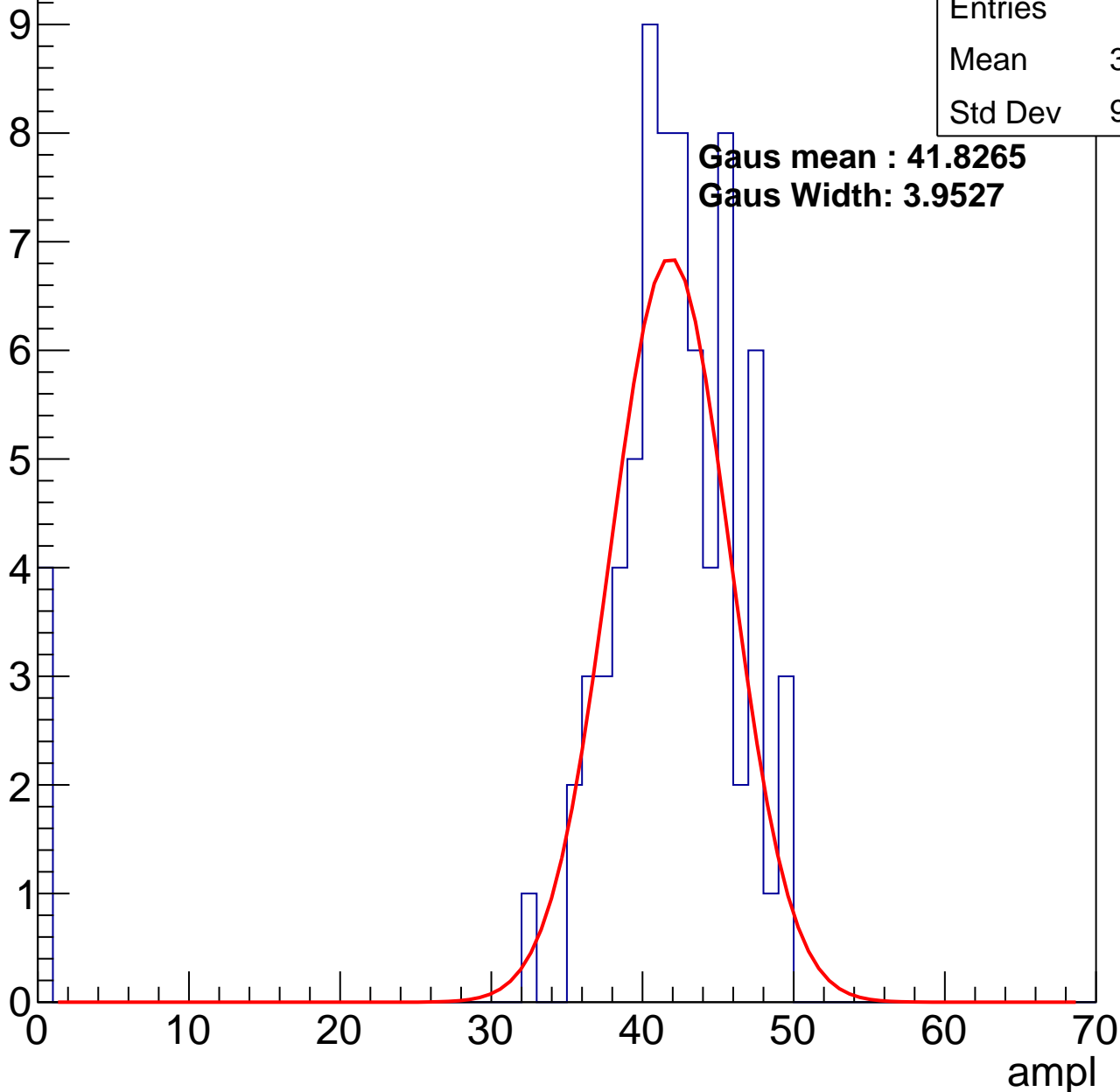
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	39.68
Std Dev	9.956

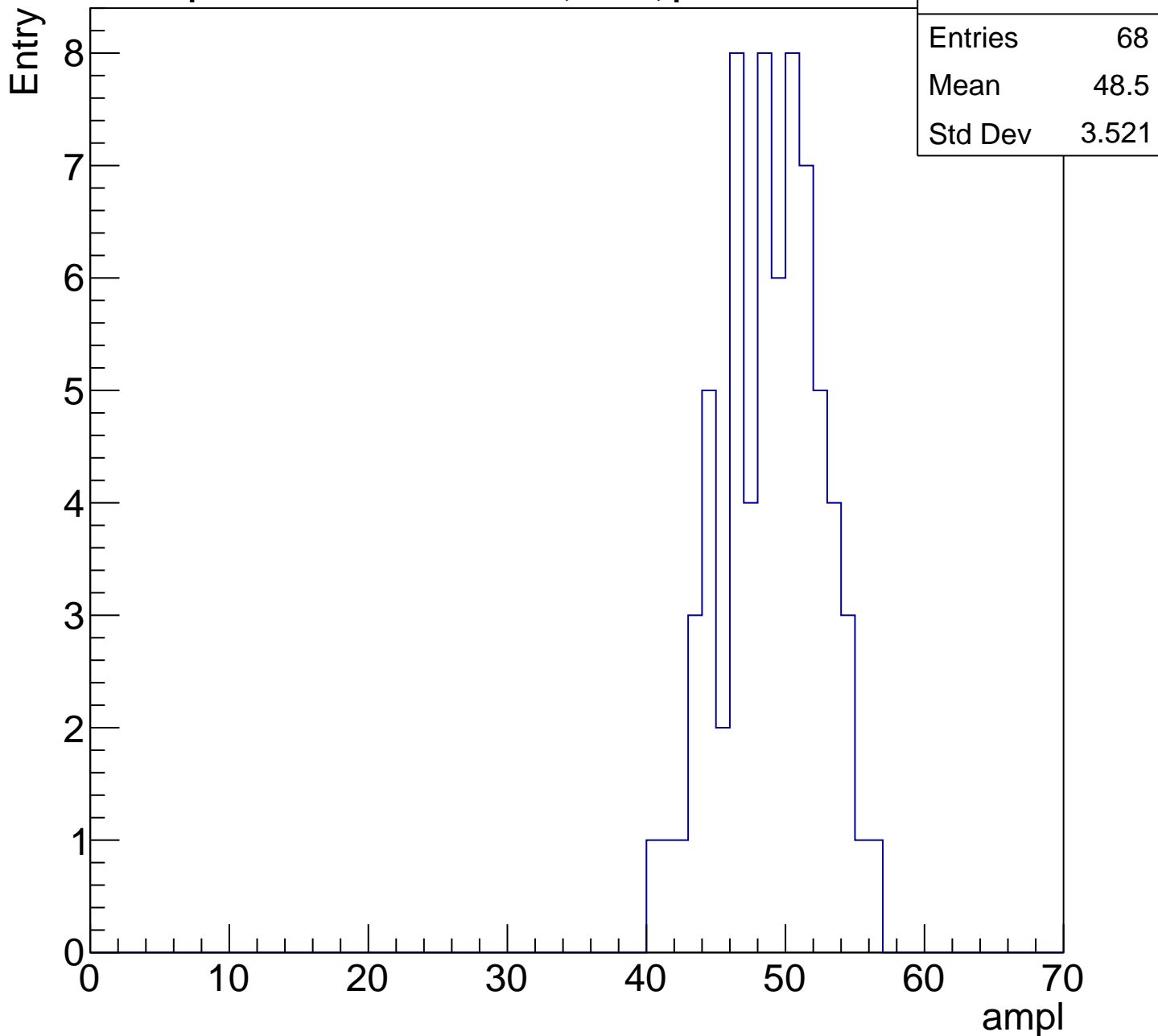
Gaus mean : 41.8265

Gaus Width: 3.9527



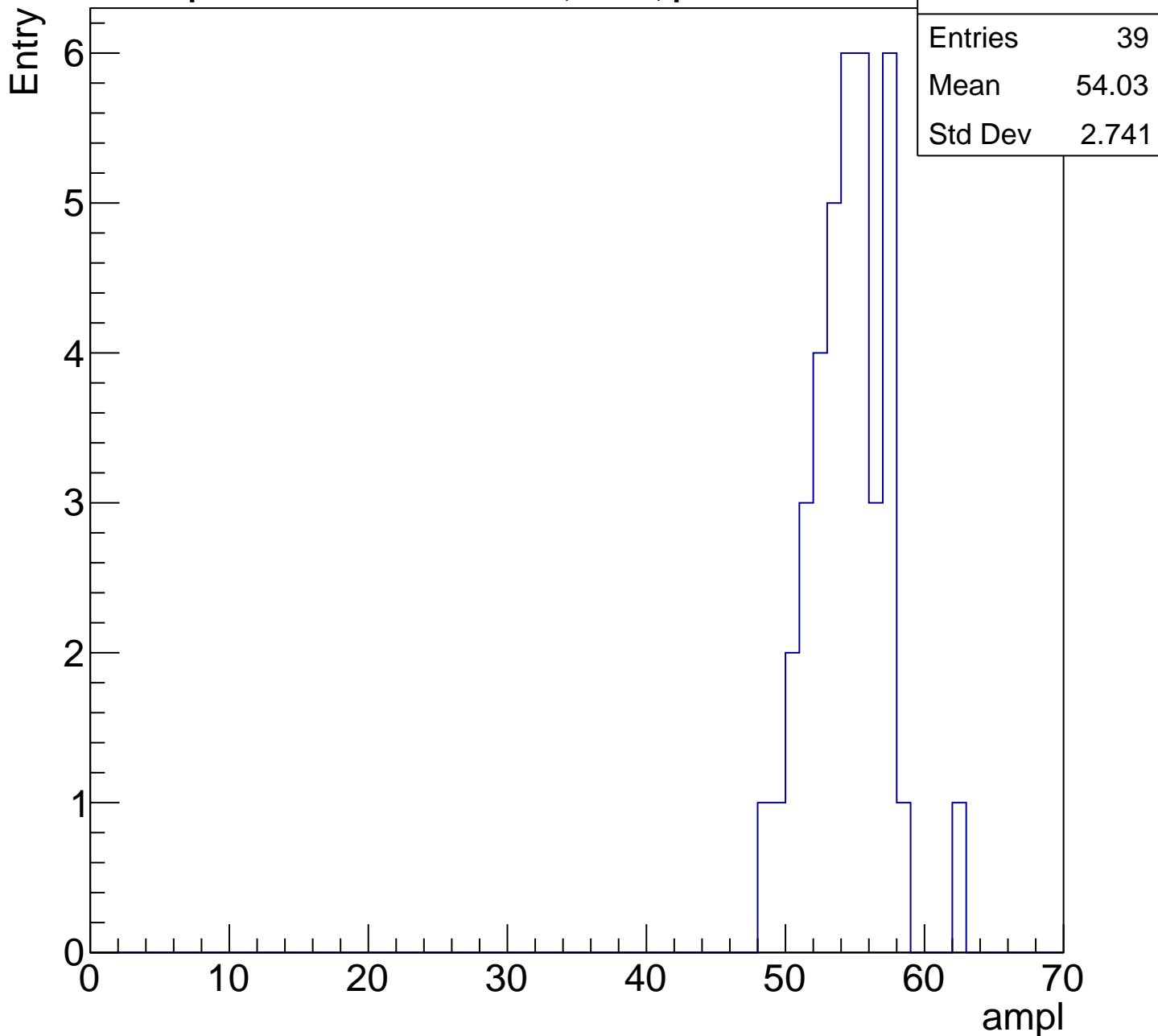
B1L103S, U8-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 67

Mean 58.39

Std Dev 7.665

8

6

4

2

0

0

10

20

30

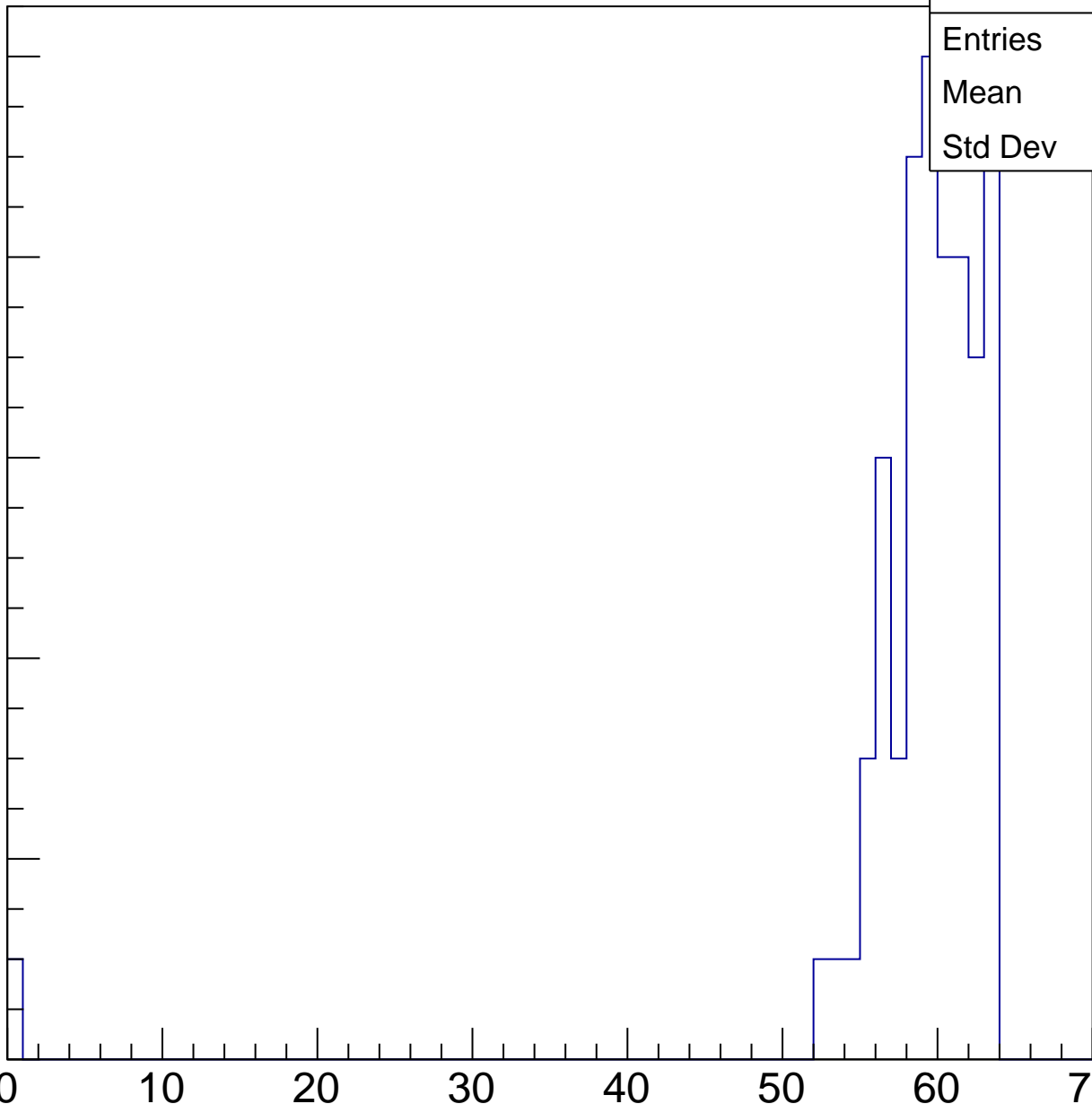
40

50

60

70

ampl



B1L103S, U8-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

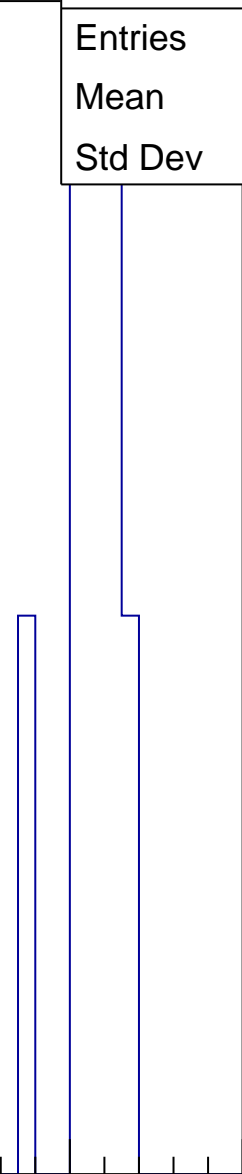
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	60.75
Std Dev	1.714

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch37, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	26.19
Std Dev	11.89

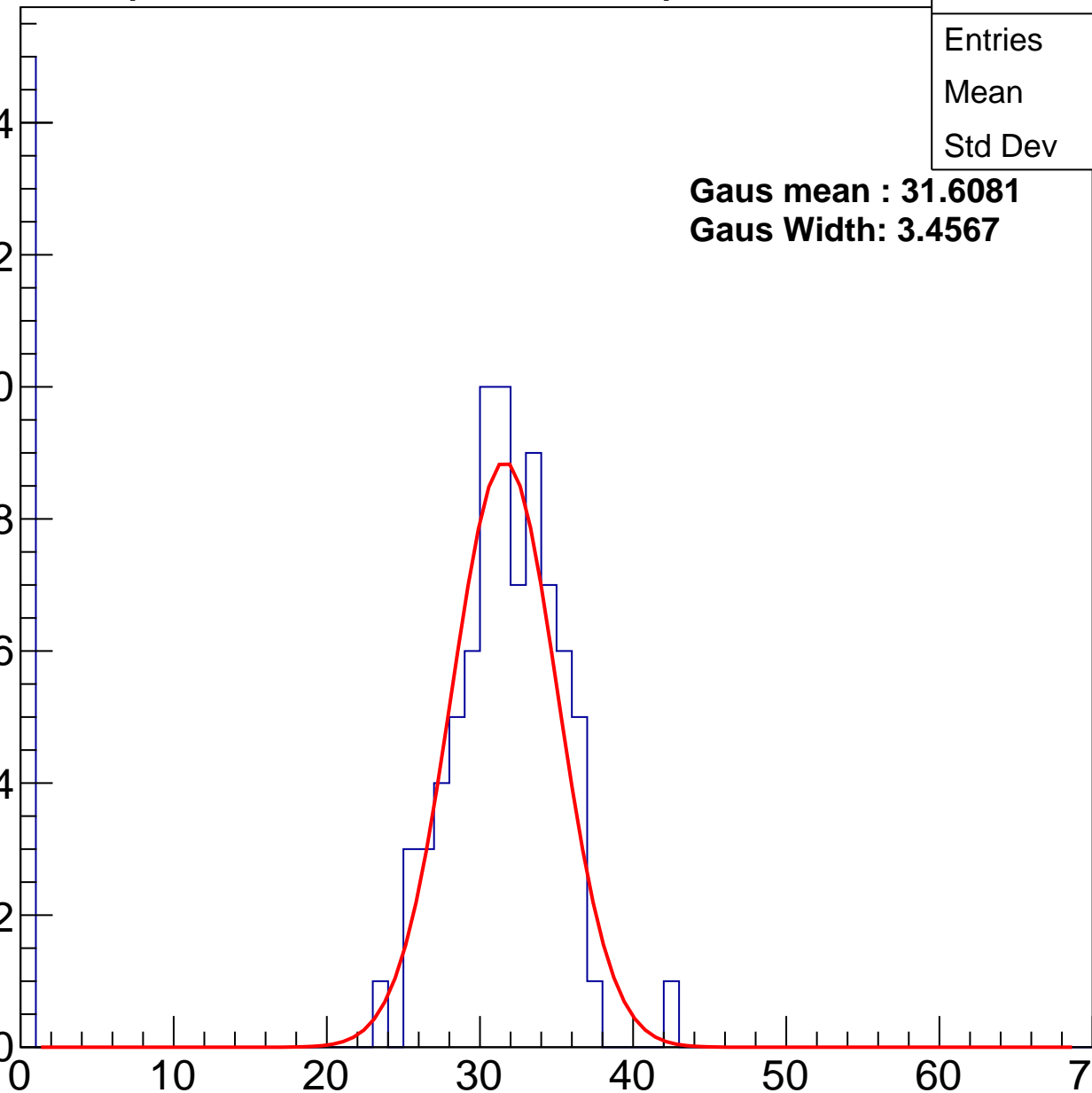
Gaus mean : 31.6081

Gaus Width: 3.4567

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U8-ch37, adc1

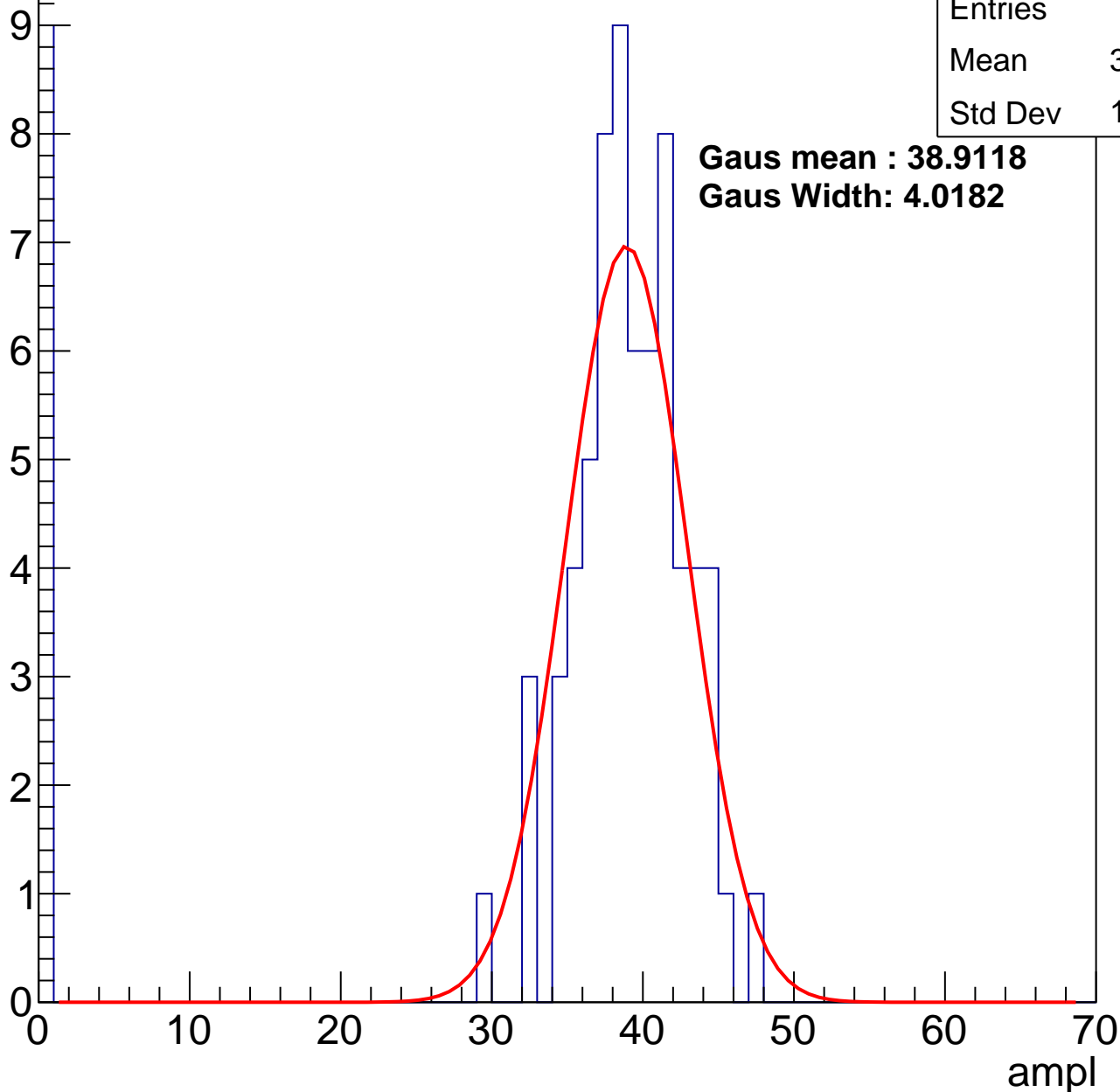
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.14
Std Dev	12.93

Gaus mean : 38.9118

Gaus Width: 4.0182



B1L103S, U8-ch37, adc2

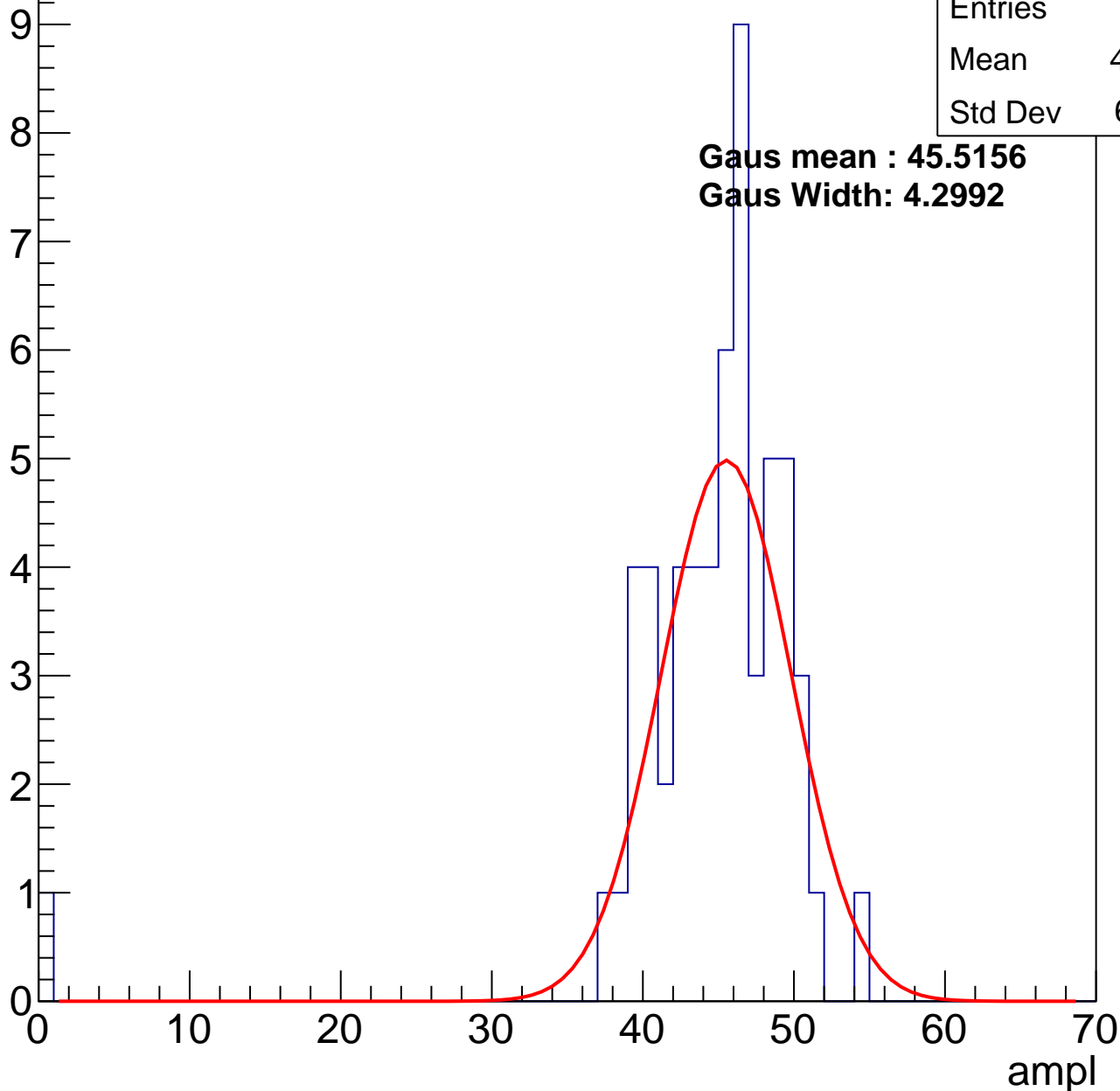
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	44.03
Std Dev	6.891

Gaus mean : 45.5156

Gaus Width: 4.2992

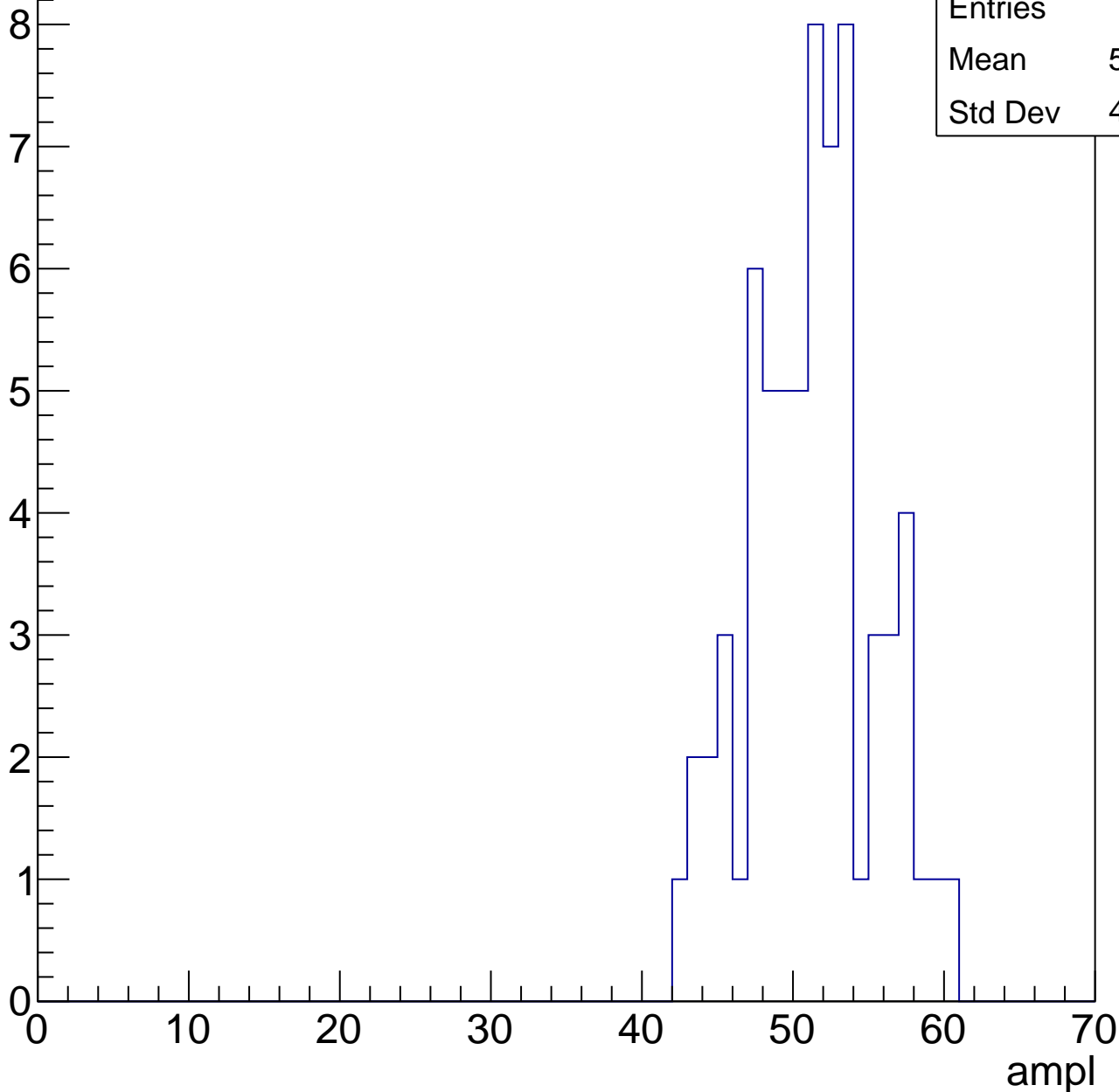


B1L103S, U8-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

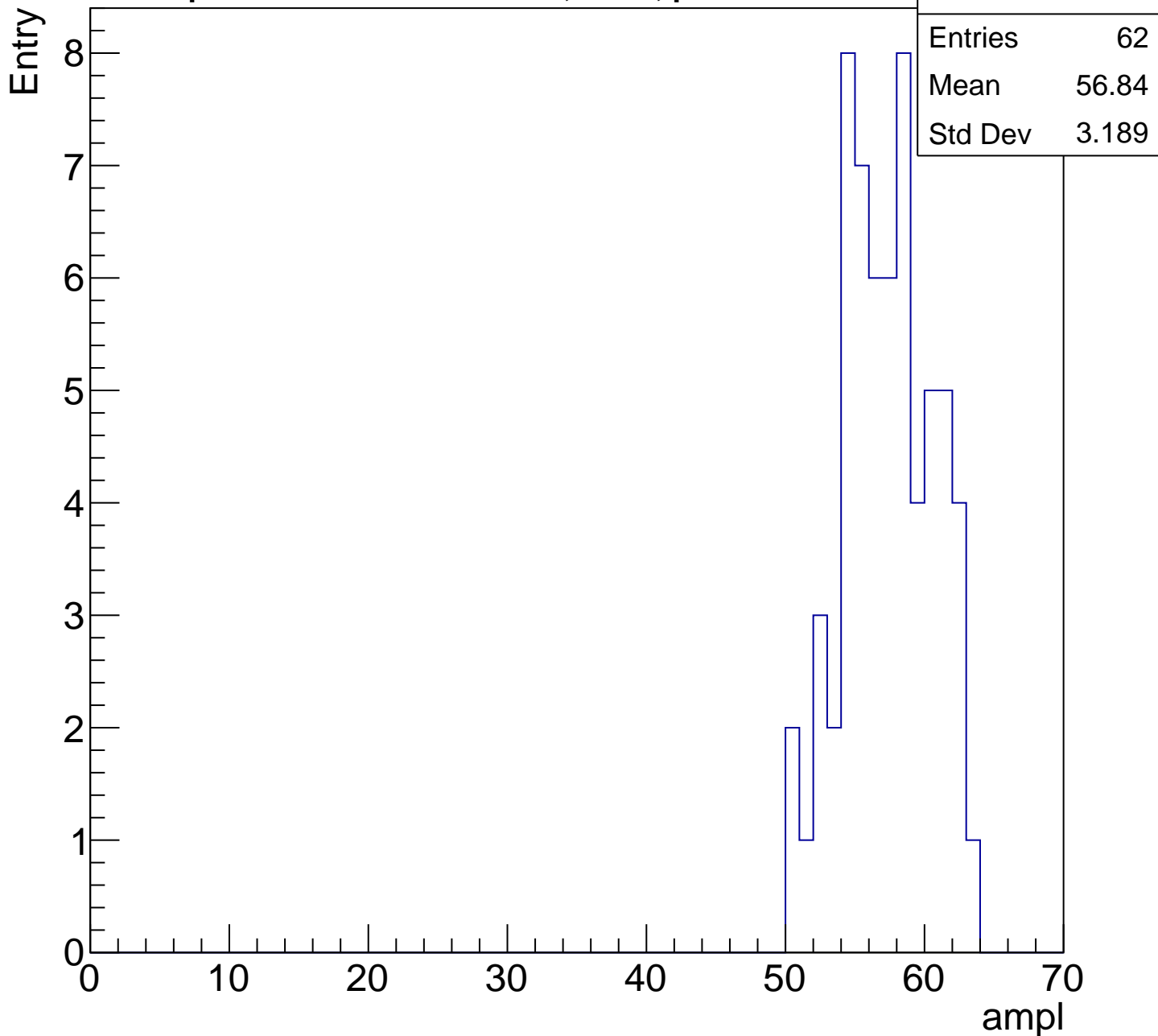
Entry

Entries	67
Mean	50.78
Std Dev	4.073



B1L103S, U8-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

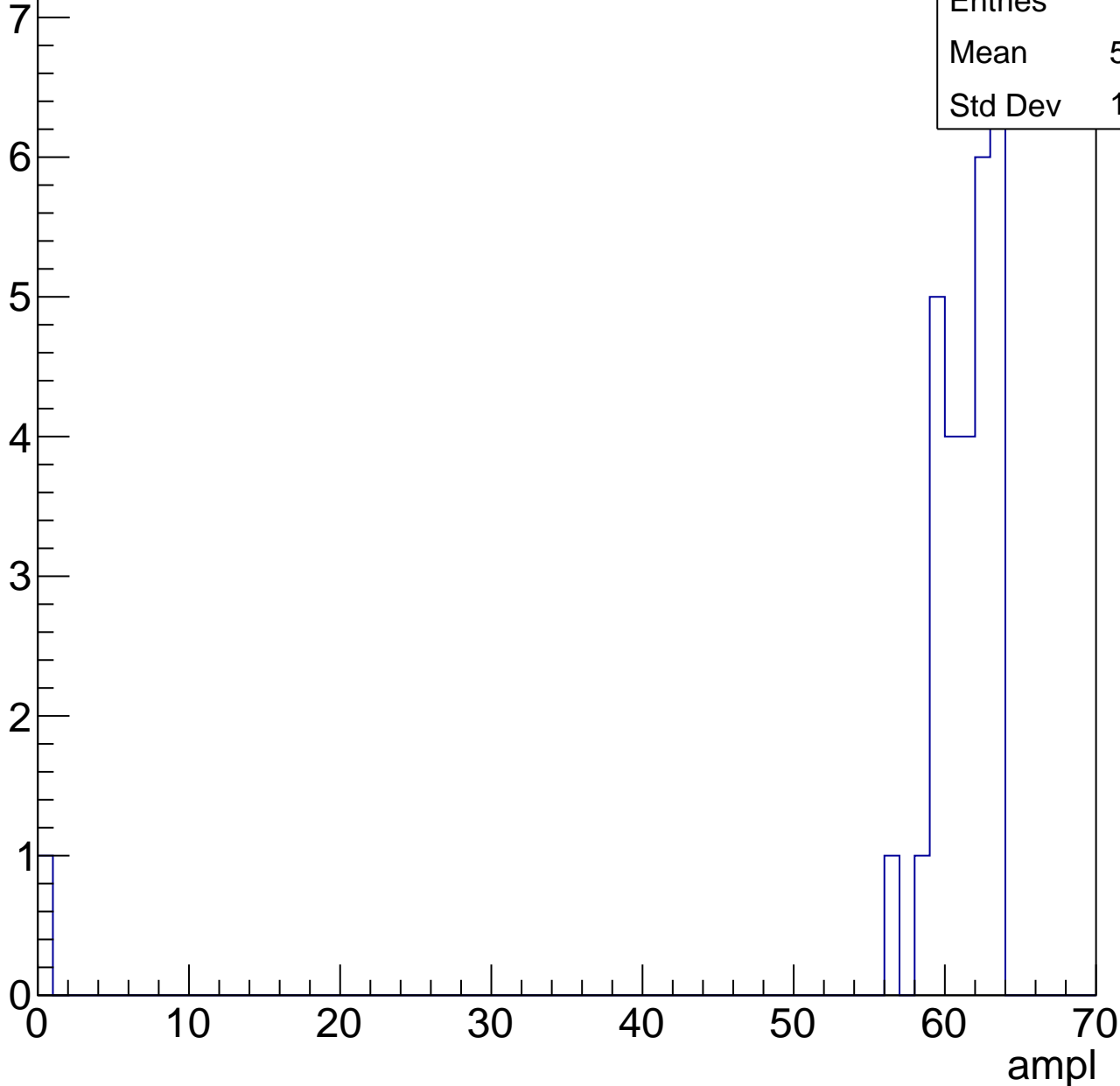


B1L103S, U8-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	58.83
Std Dev	11.26



B1L103S, U8-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

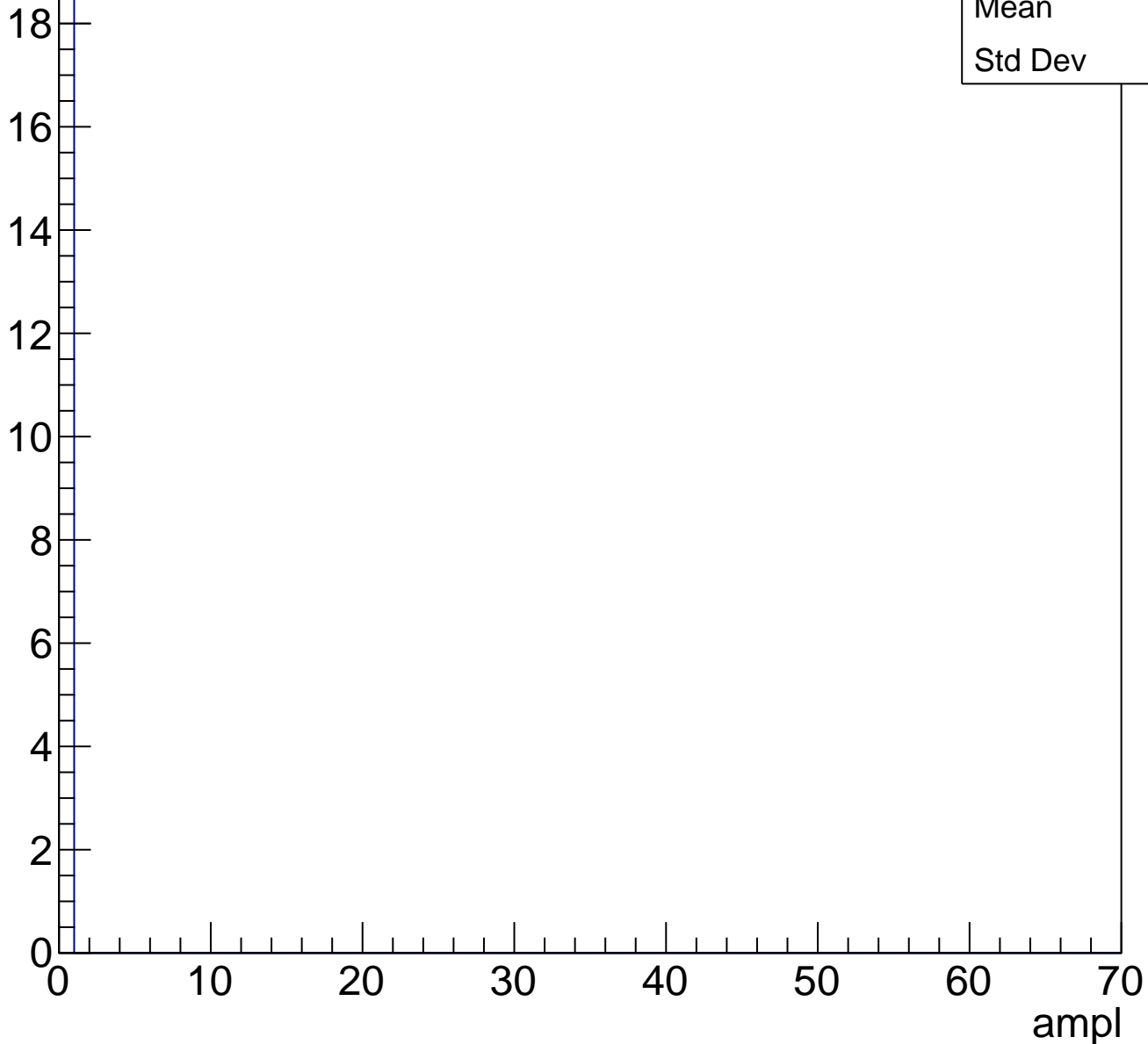


B1L103S, U8-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



B1L103S, U8-ch38, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	24.01
Std Dev	9.309

Gaus mean : 27.8152

Gaus Width: 3.6968

Entry

10

8

6

4

2

0

0

10

20

30

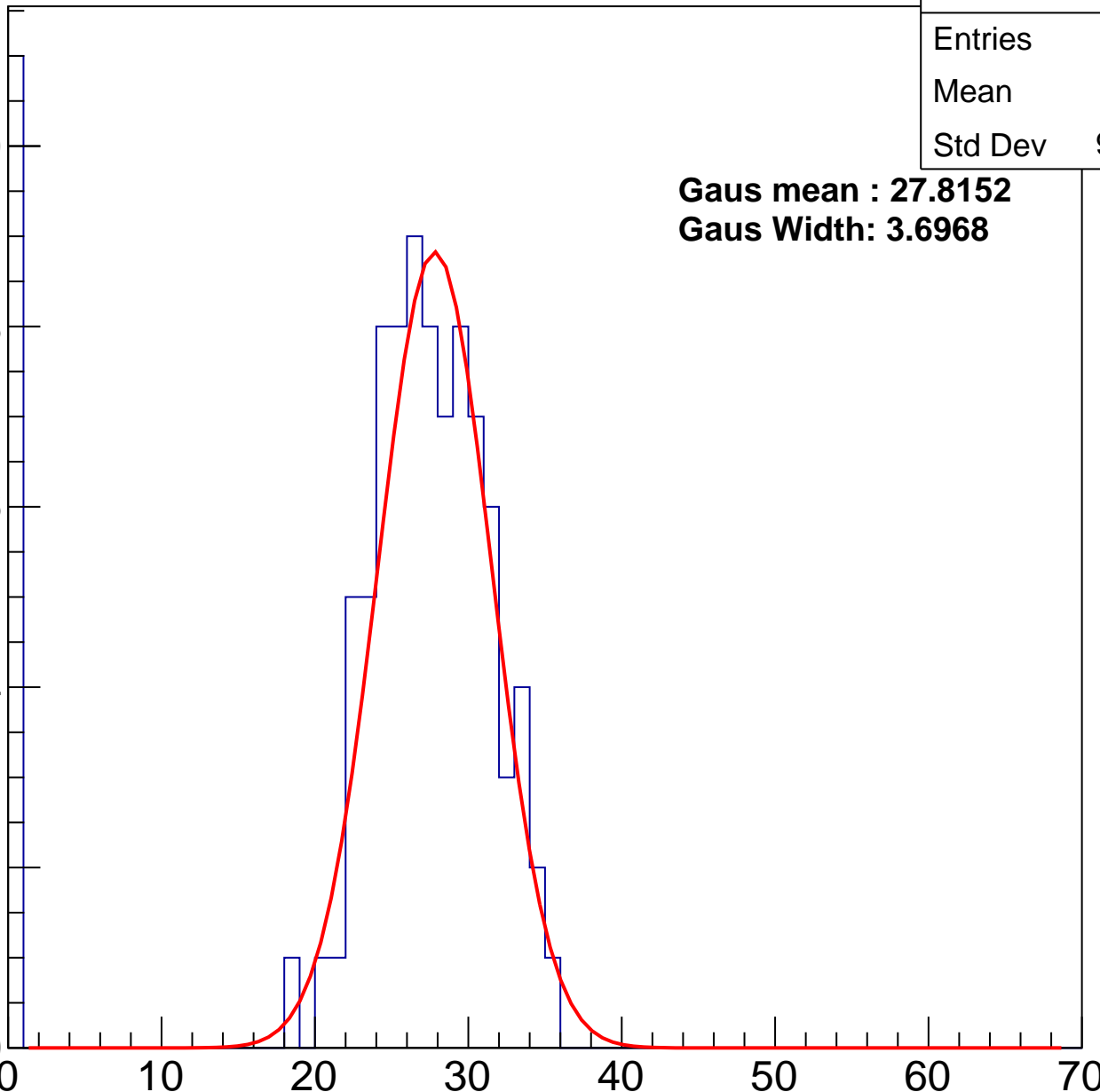
40

50

60

70

ampl



B1L103S, U8-ch38, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	31.67
Std Dev	11.24

Gaus mean : 35.8589

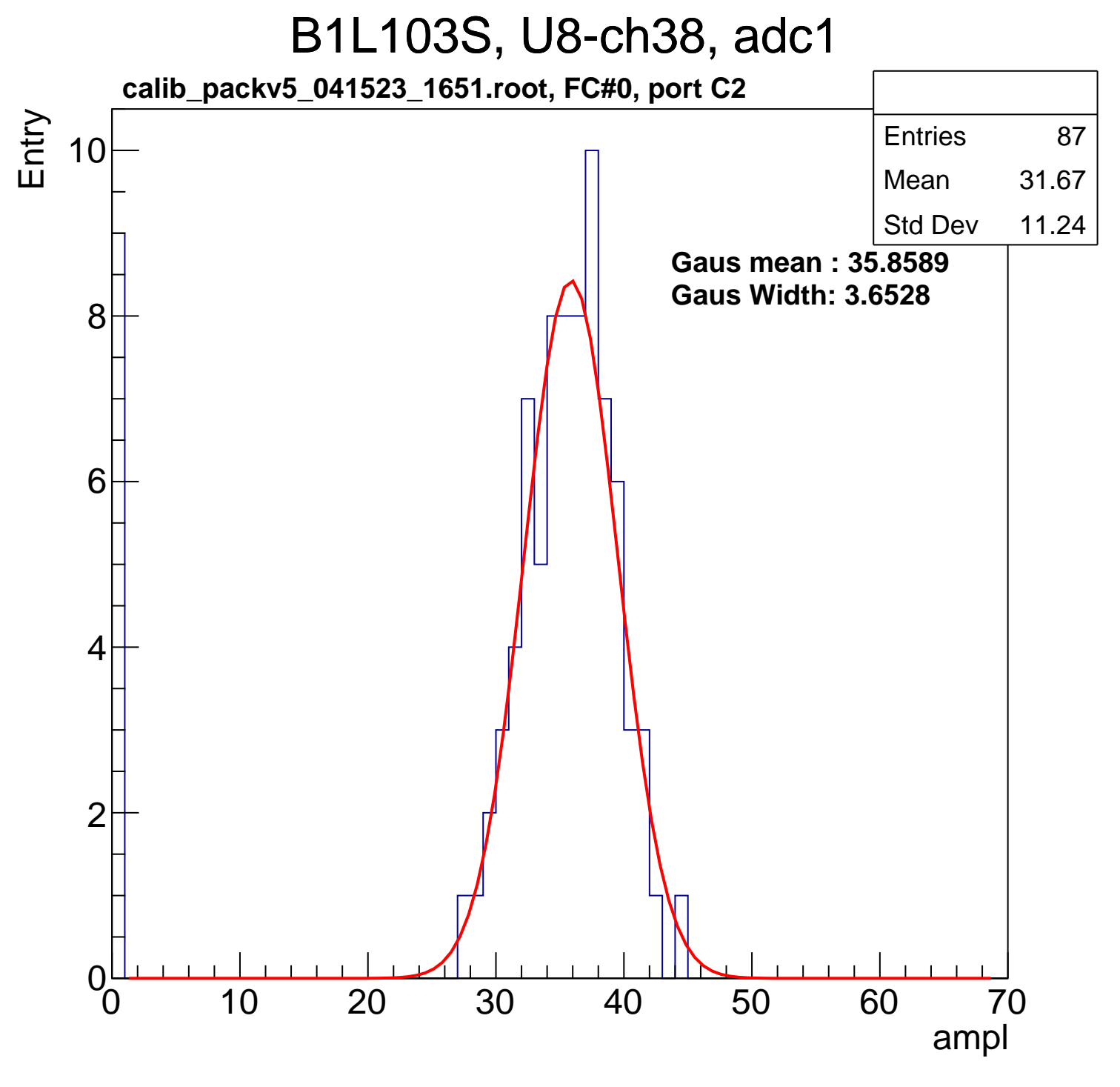
Gaus Width: 3.6528

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch38, adc2

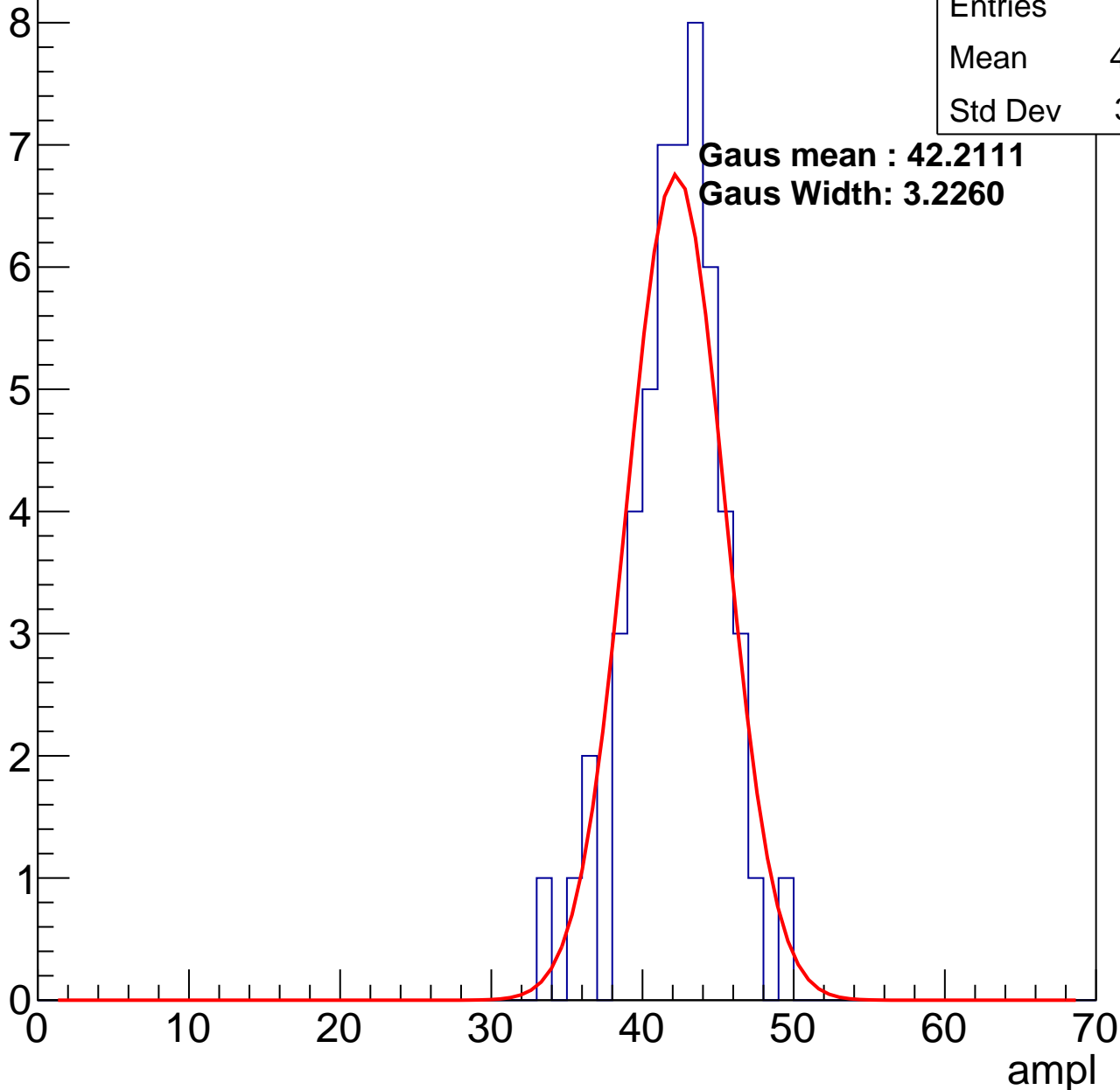
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	41.75
Std Dev	3.071

Gaus mean : 42.2111

Gaus Width: 3.2260

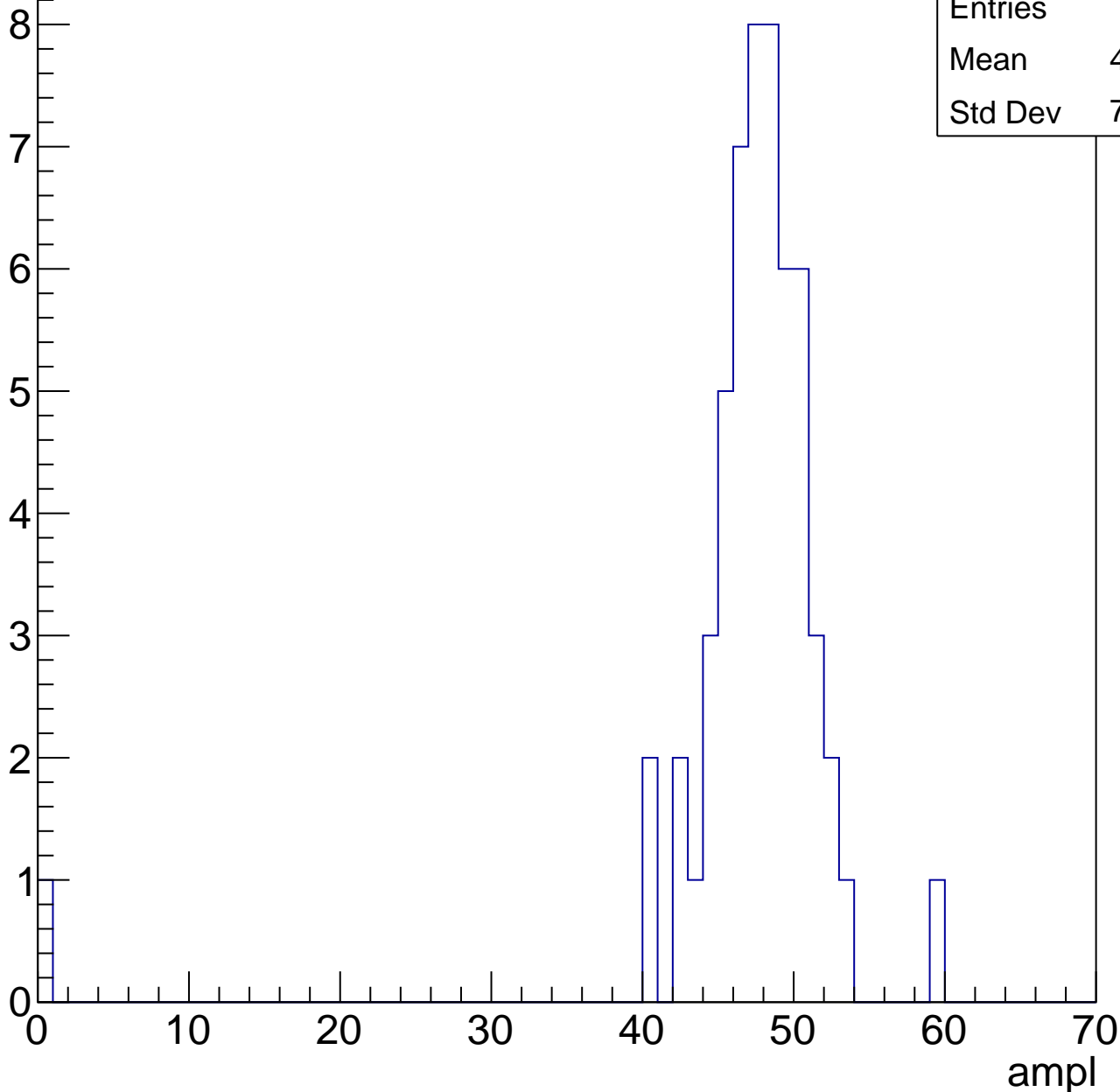


B1L103S, U8-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

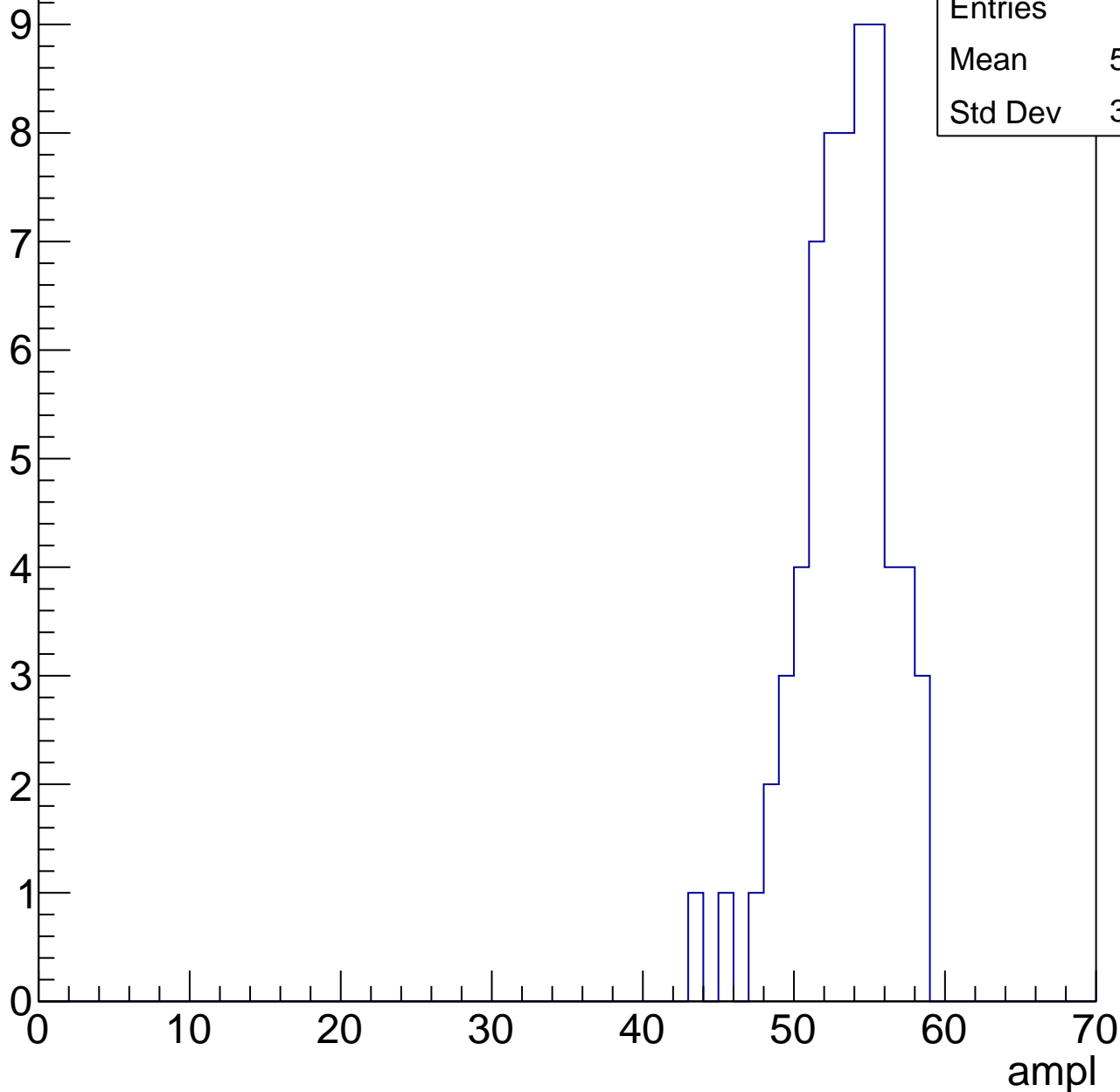
Entries	56
Mean	46.59
Std Dev	7.048



B1L103S, U8-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



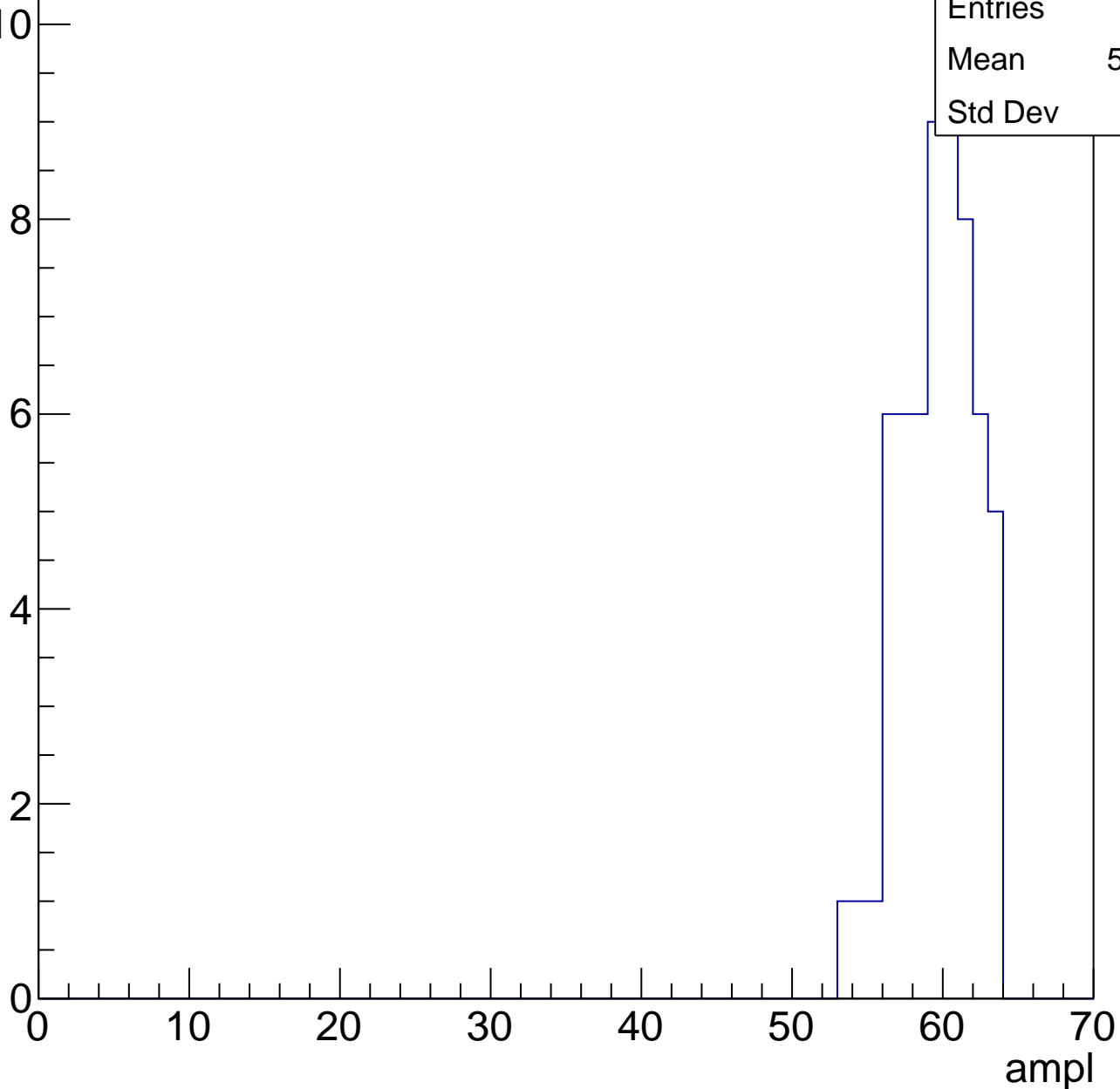
Entries	64
Mean	52.84
Std Dev	3.032

B1L103S, U8-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	59.22
Std Dev	2.38

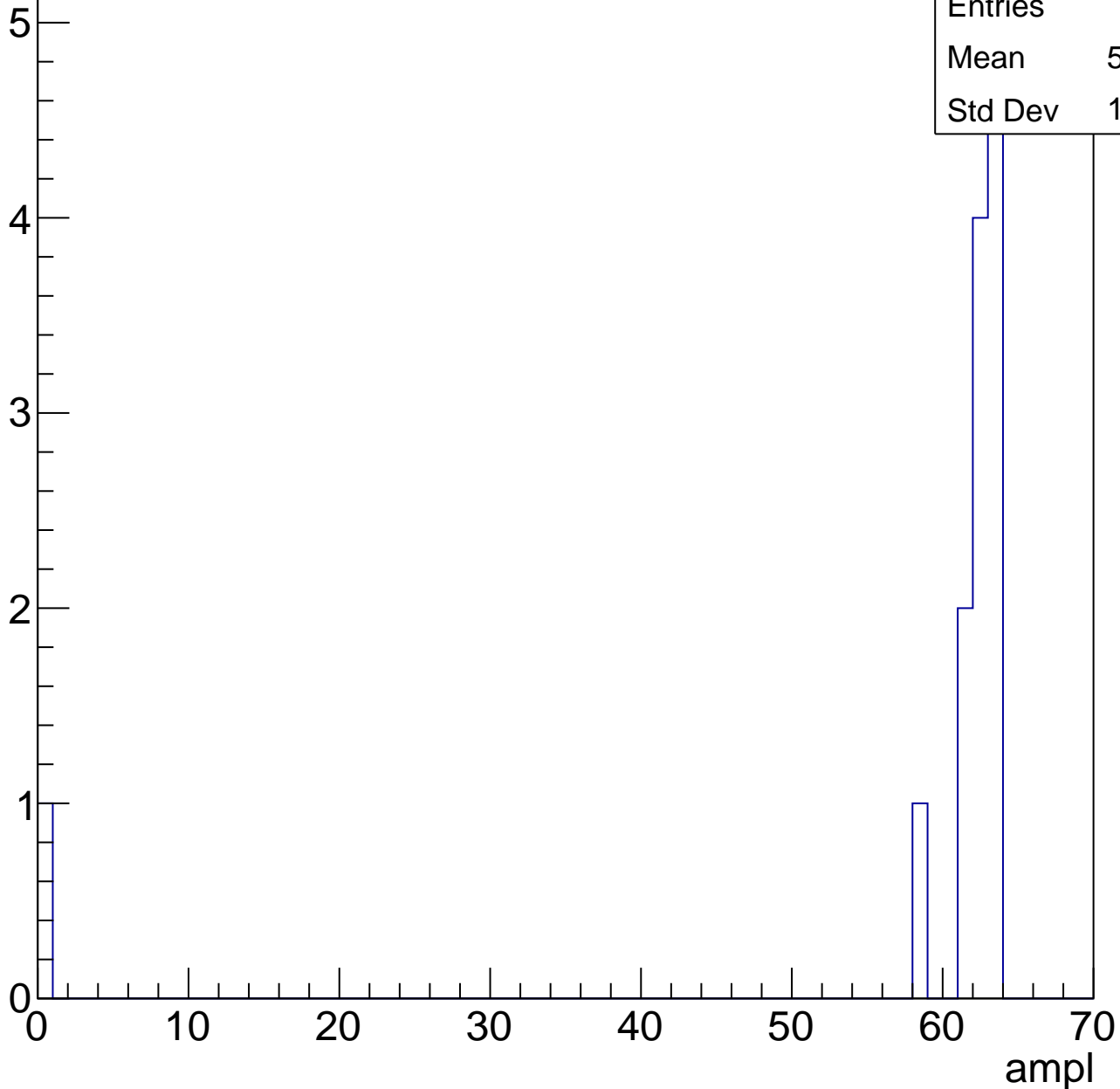


B1L103S, U8-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.15
Std Dev	16.55



B1L103S, U8-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch39, adc0

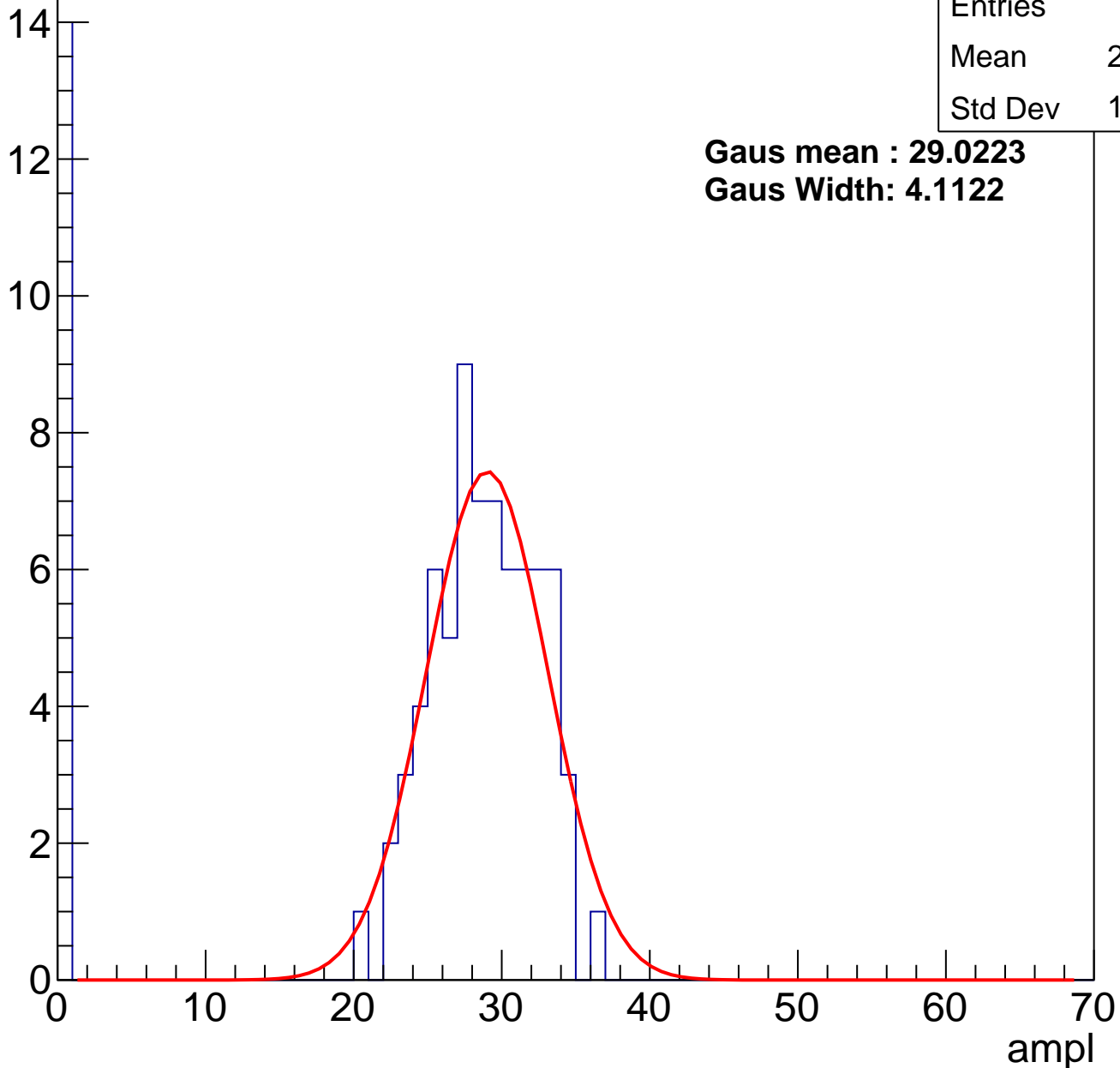
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.78
Std Dev	10.95

Gaus mean : 29.0223

Gaus Width: 4.1122

Entry



B1L103S, U8-ch39, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	32.55
Std Dev	10.93

Gaus mean : 36.3673

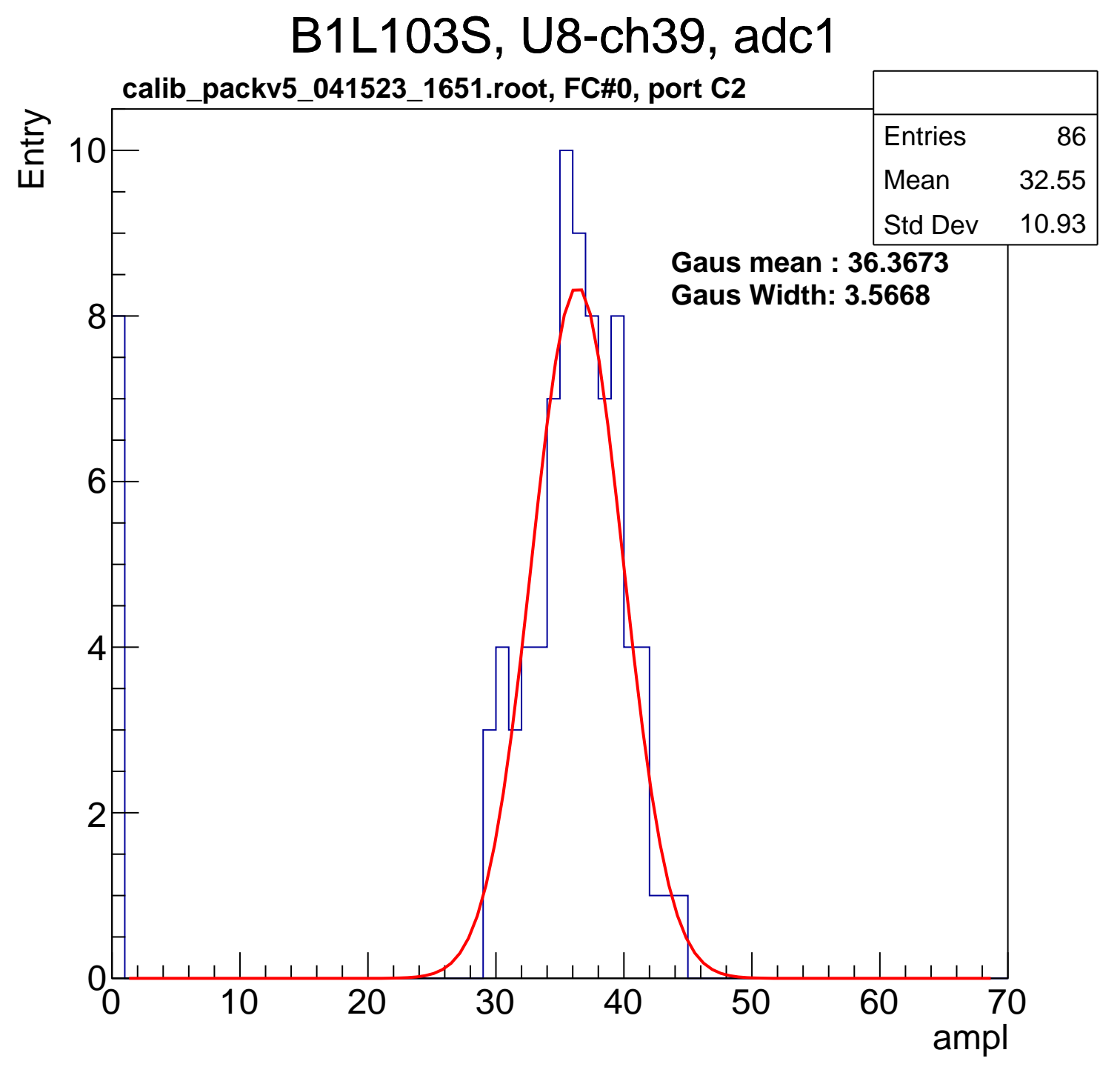
Gaus Width: 3.5668

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch39, adc2

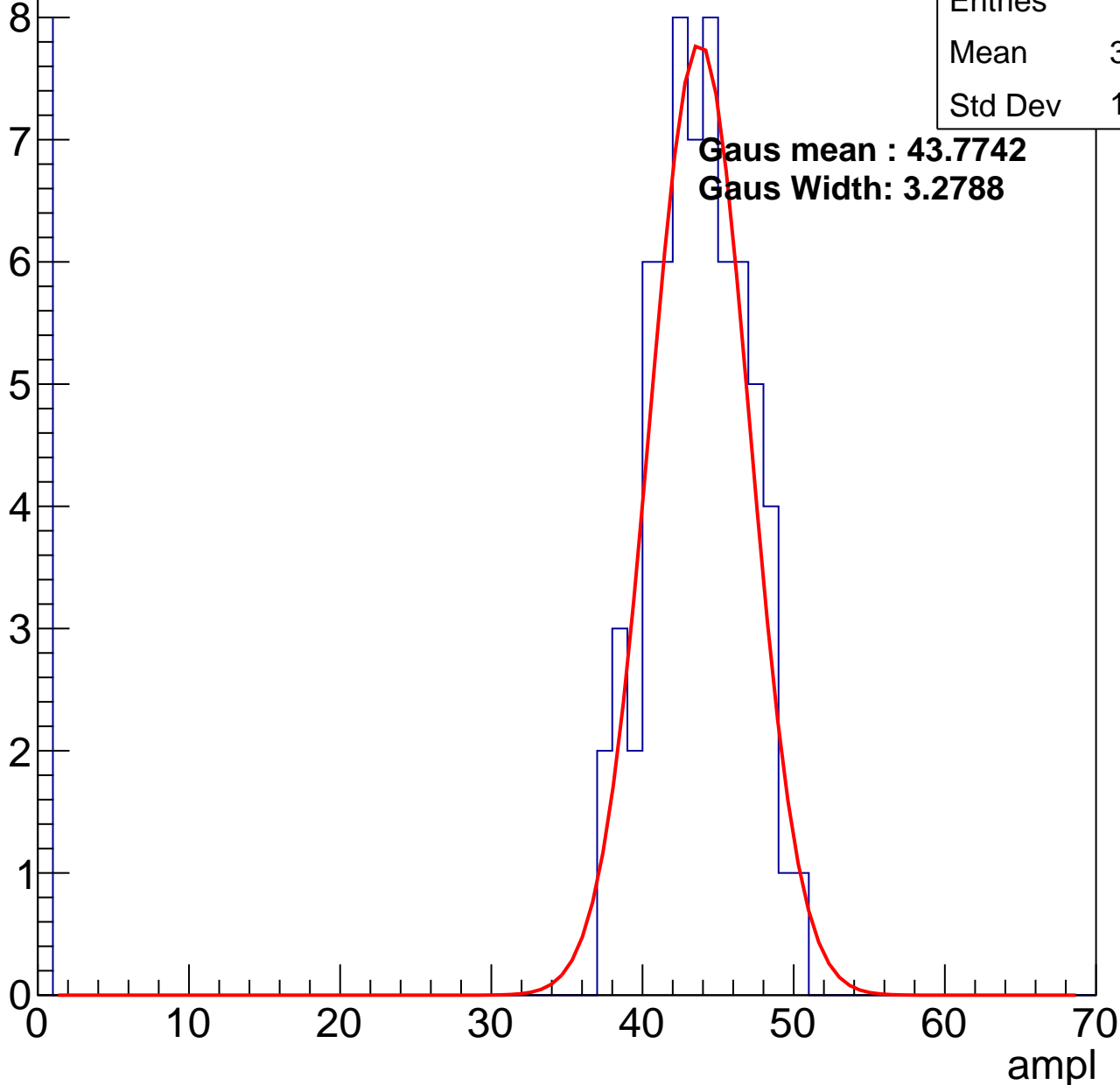
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	38.53
Std Dev	13.82

Gaus mean : 43.7742

Gaus Width: 3.2788

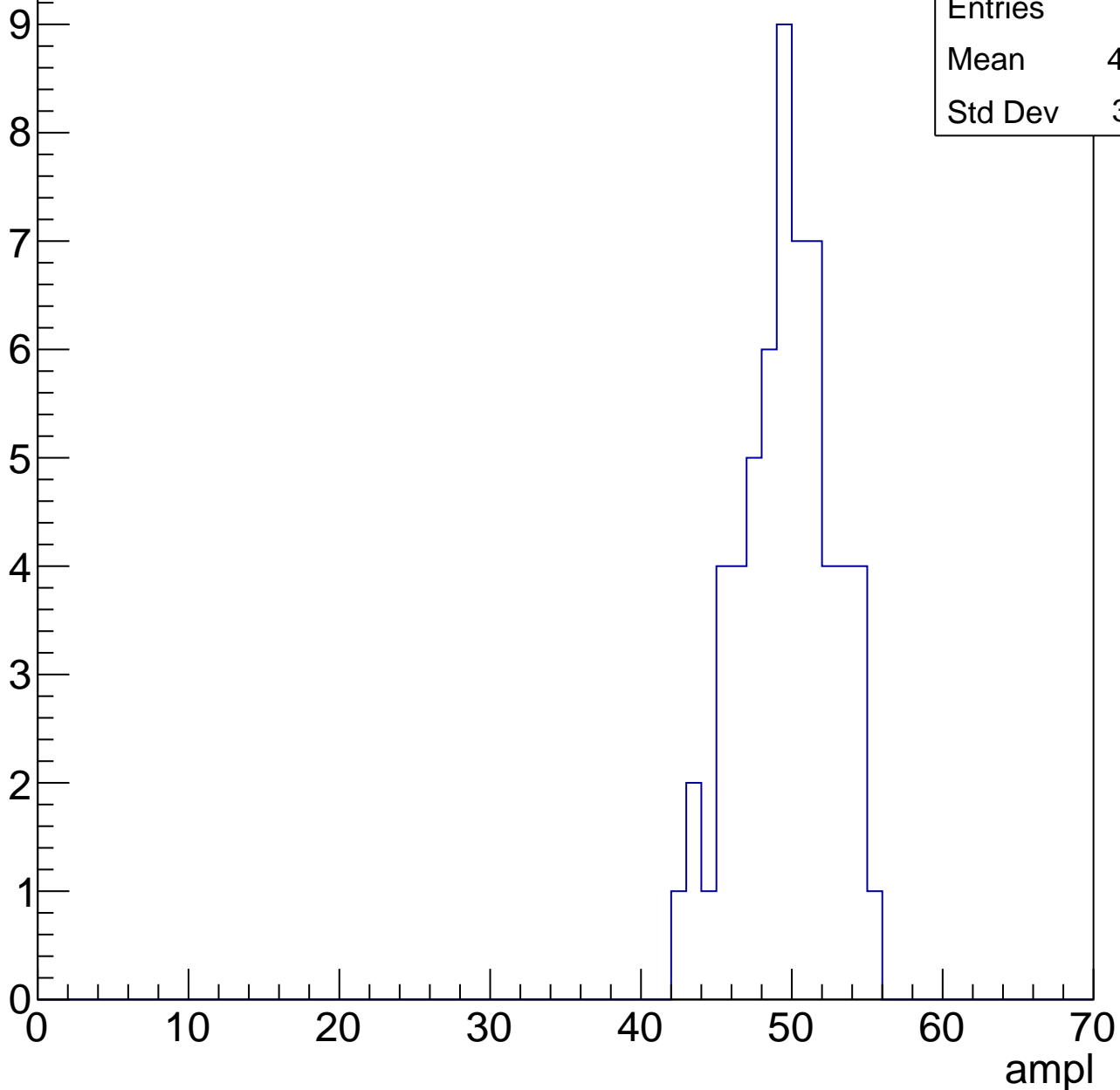


B1L103S, U8-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	49.12
Std Dev	3.031

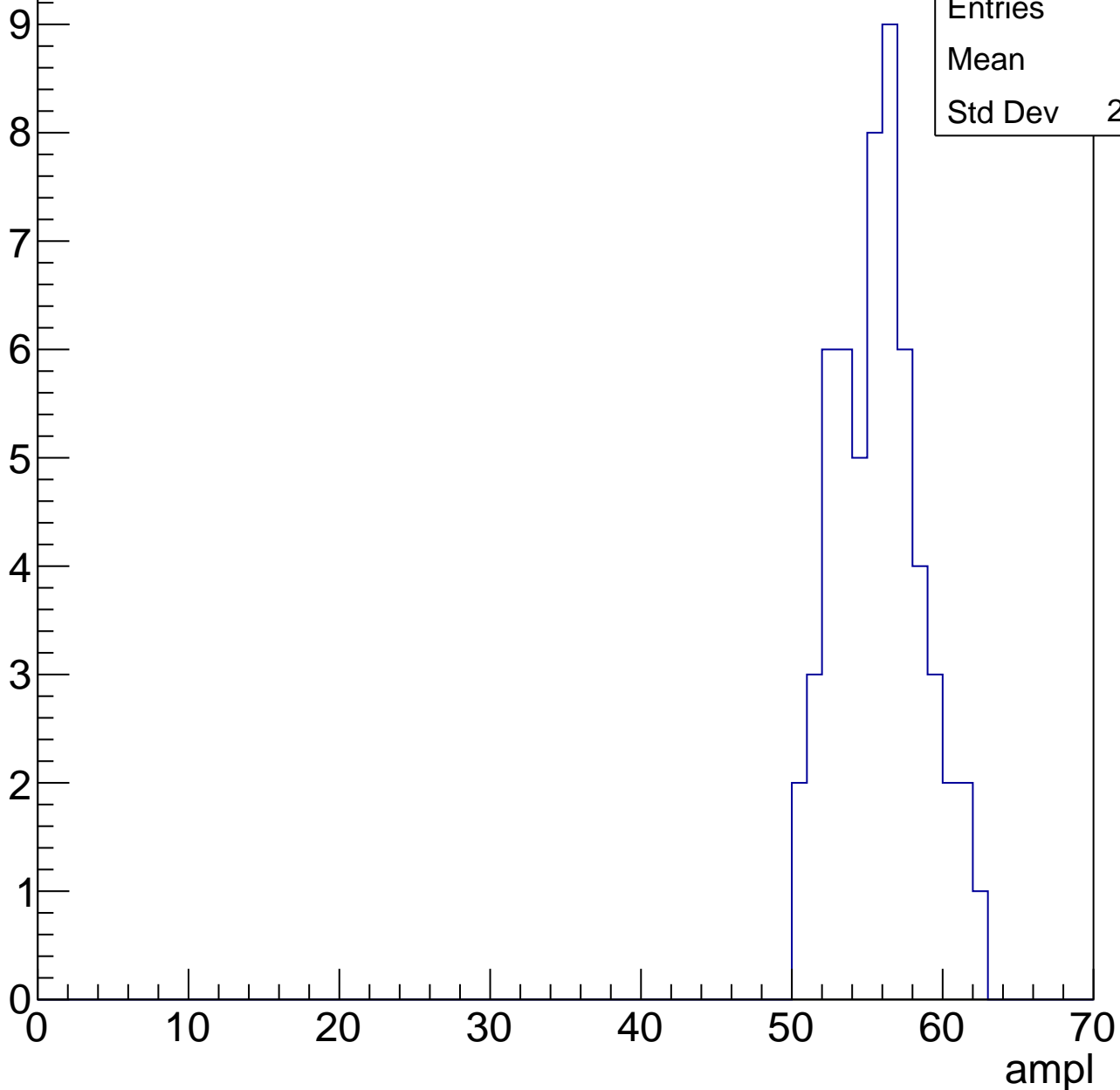


B1L103S, U8-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.3
Std Dev	2.853



B1L103S, U8-ch39, adc5

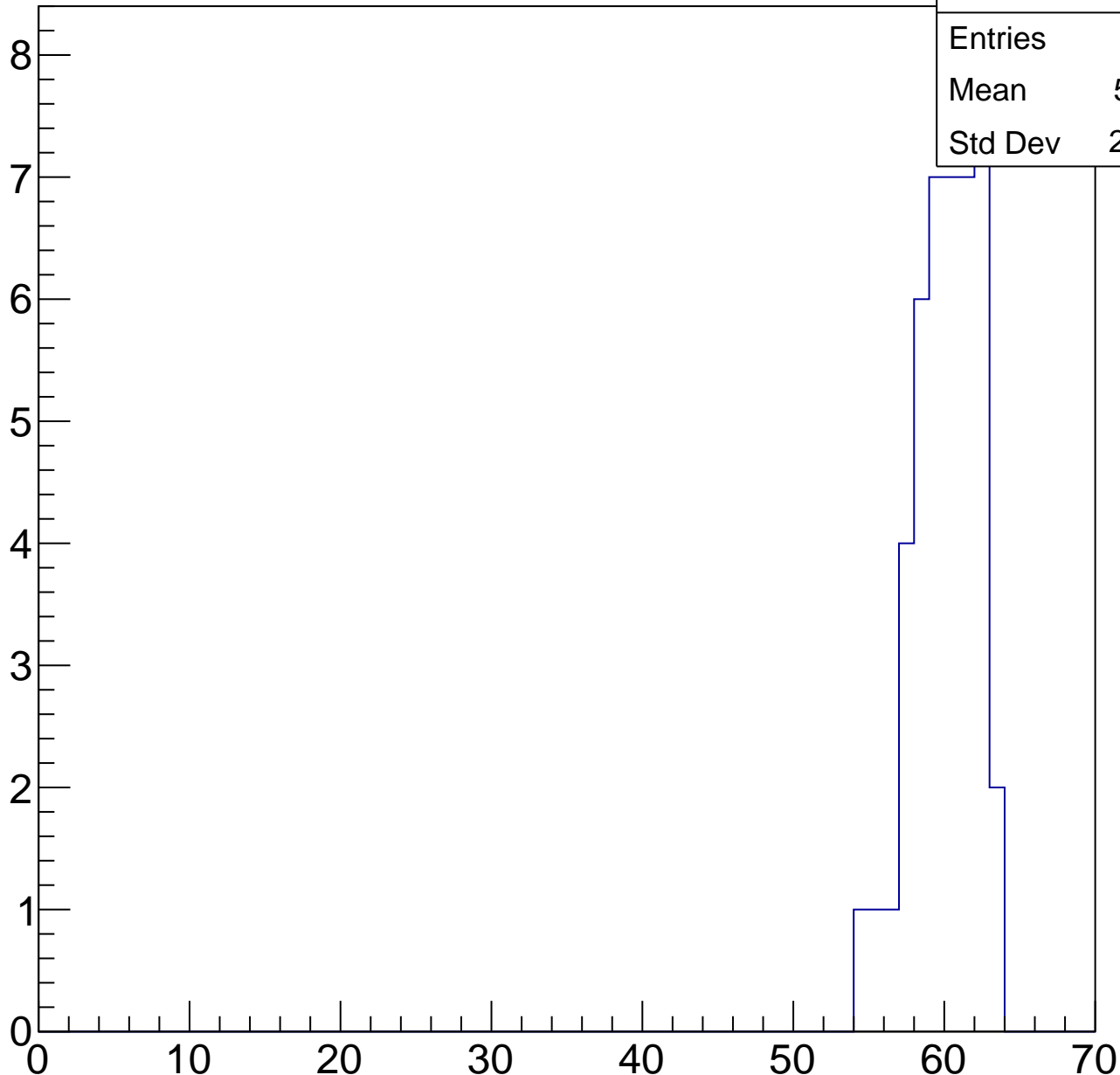
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	59.61
Std Dev	2.102

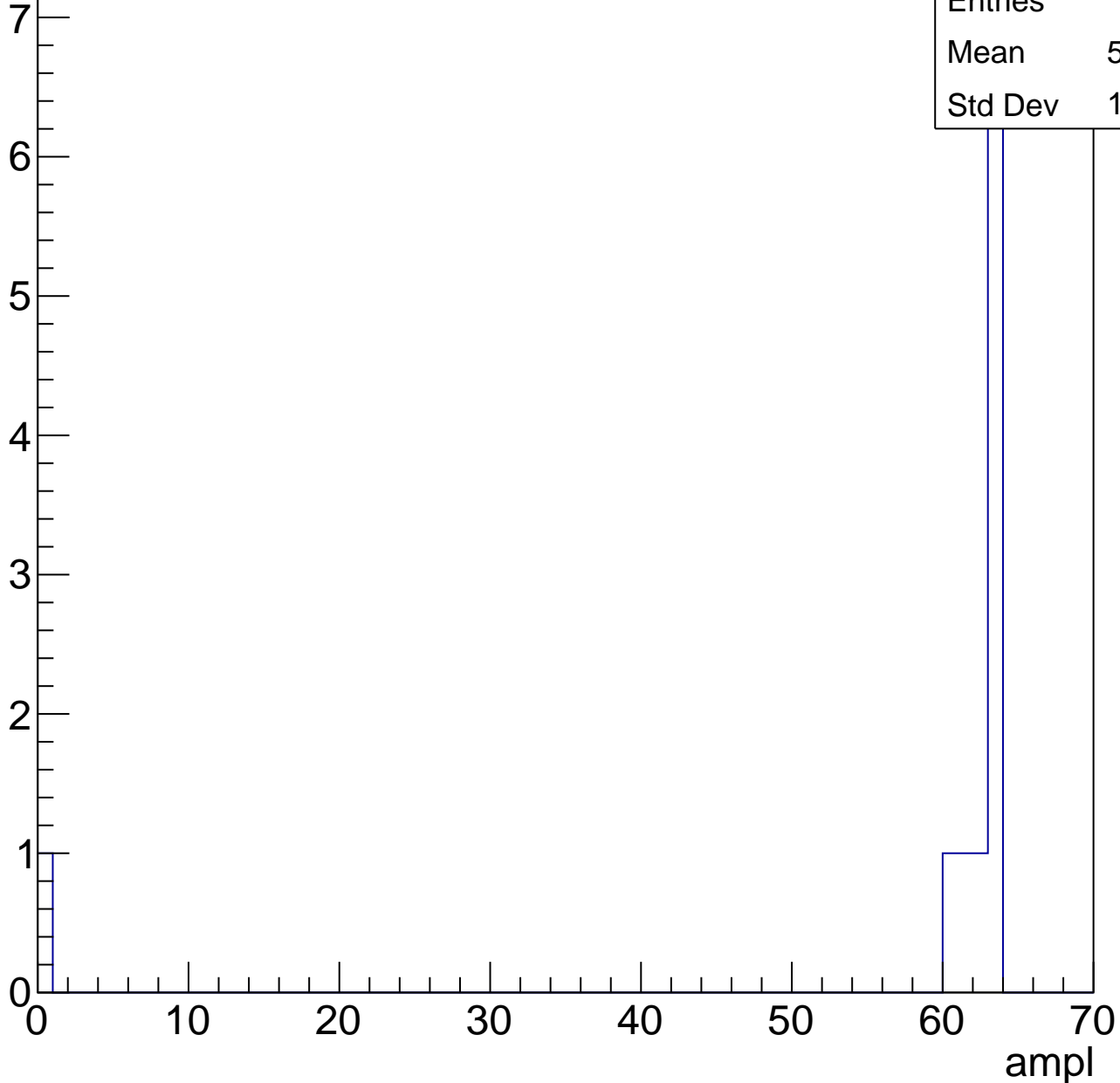
ampl



B1L103S, U8-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	11
Mean	56.73
Std Dev	17.97

B1L103S, U8-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



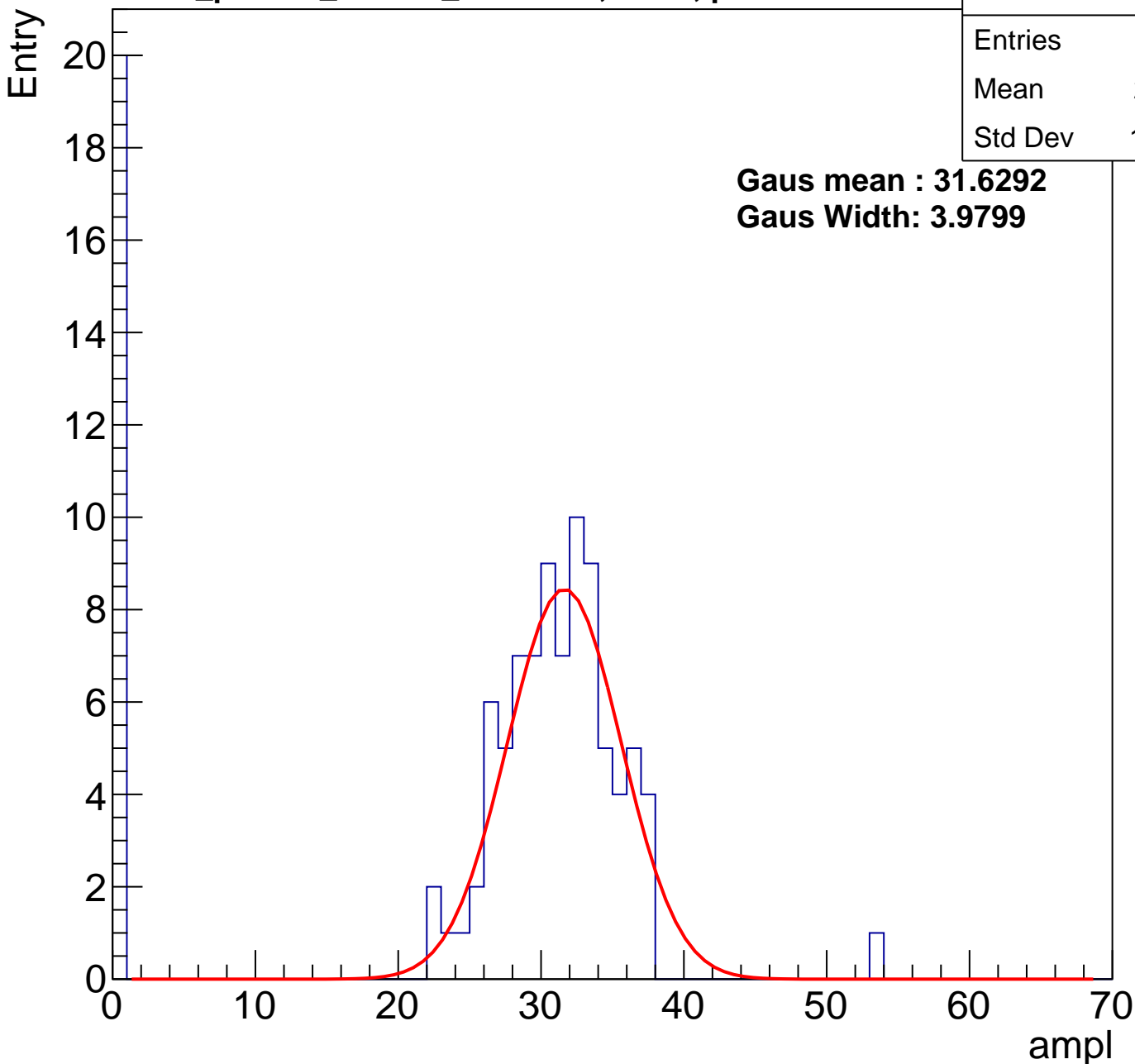
B1L103S, U8-ch40, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	25.01
Std Dev	12.74

Gaus mean : 31.6292

Gaus Width: 3.9799



B1L103S, U8-ch40, adc1

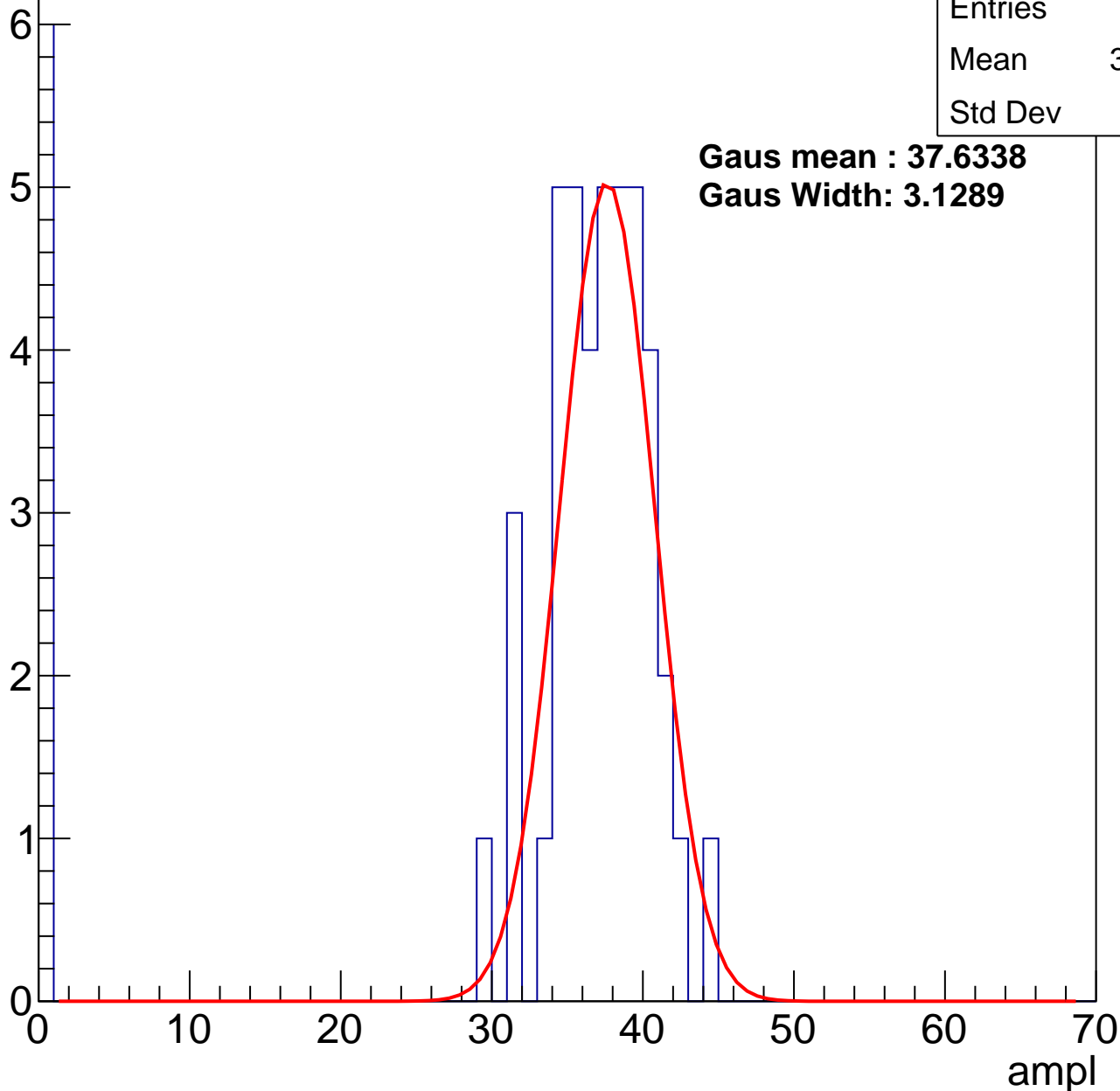
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	32.12
Std Dev	12.5

Gaus mean : 37.6338

Gaus Width: 3.1289



B1L103S, U8-ch40, adc2

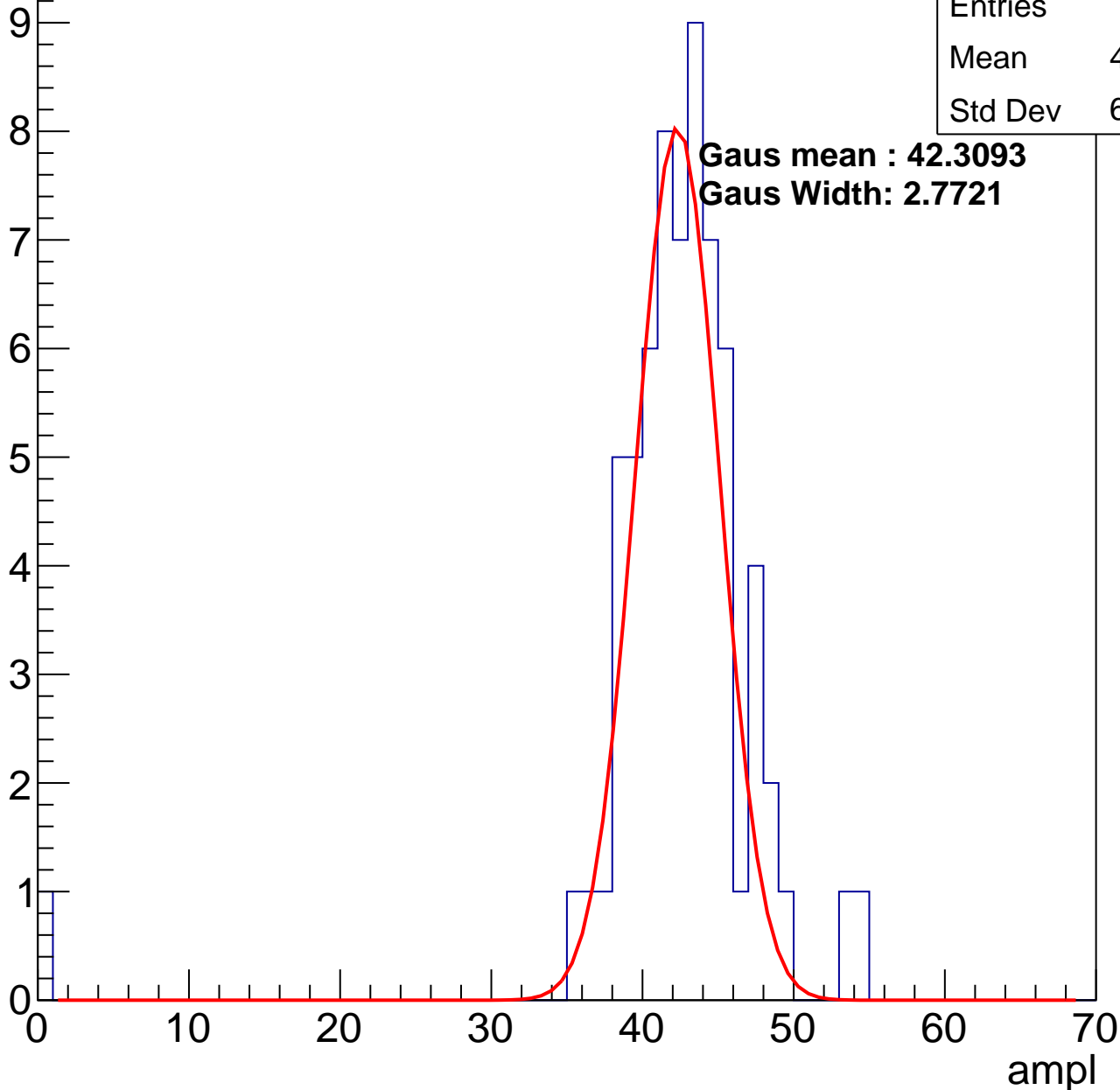
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	41.88
Std Dev	6.257

Gaus mean : 42.3093

Gaus Width: 2.7721

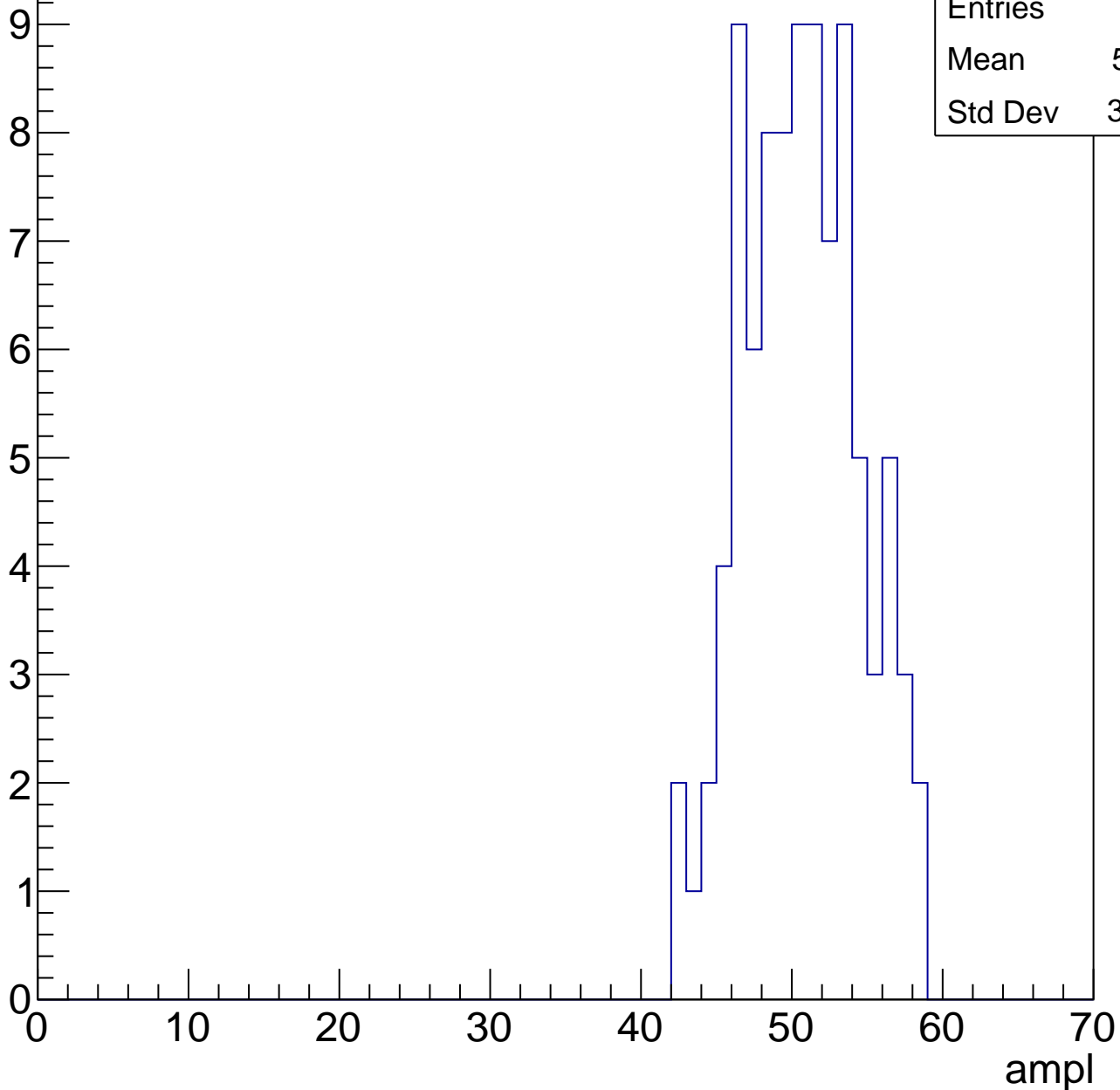


B1L103S, U8-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	92
Mean	50.21
Std Dev	3.775

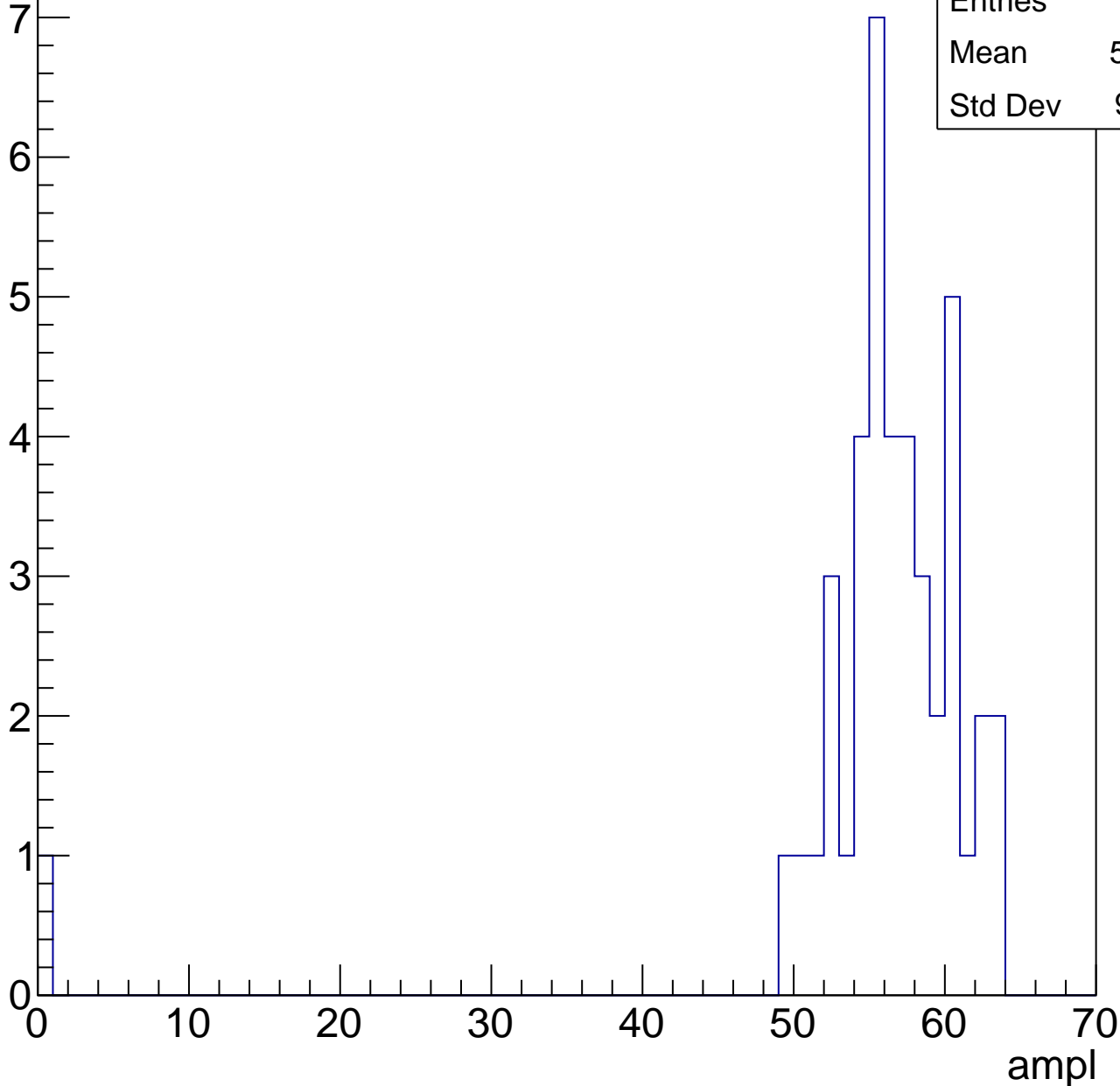


B1L103S, U8-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	55.12
Std Dev	9.261



B1L103S, U8-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.05
Std Dev	1.825

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

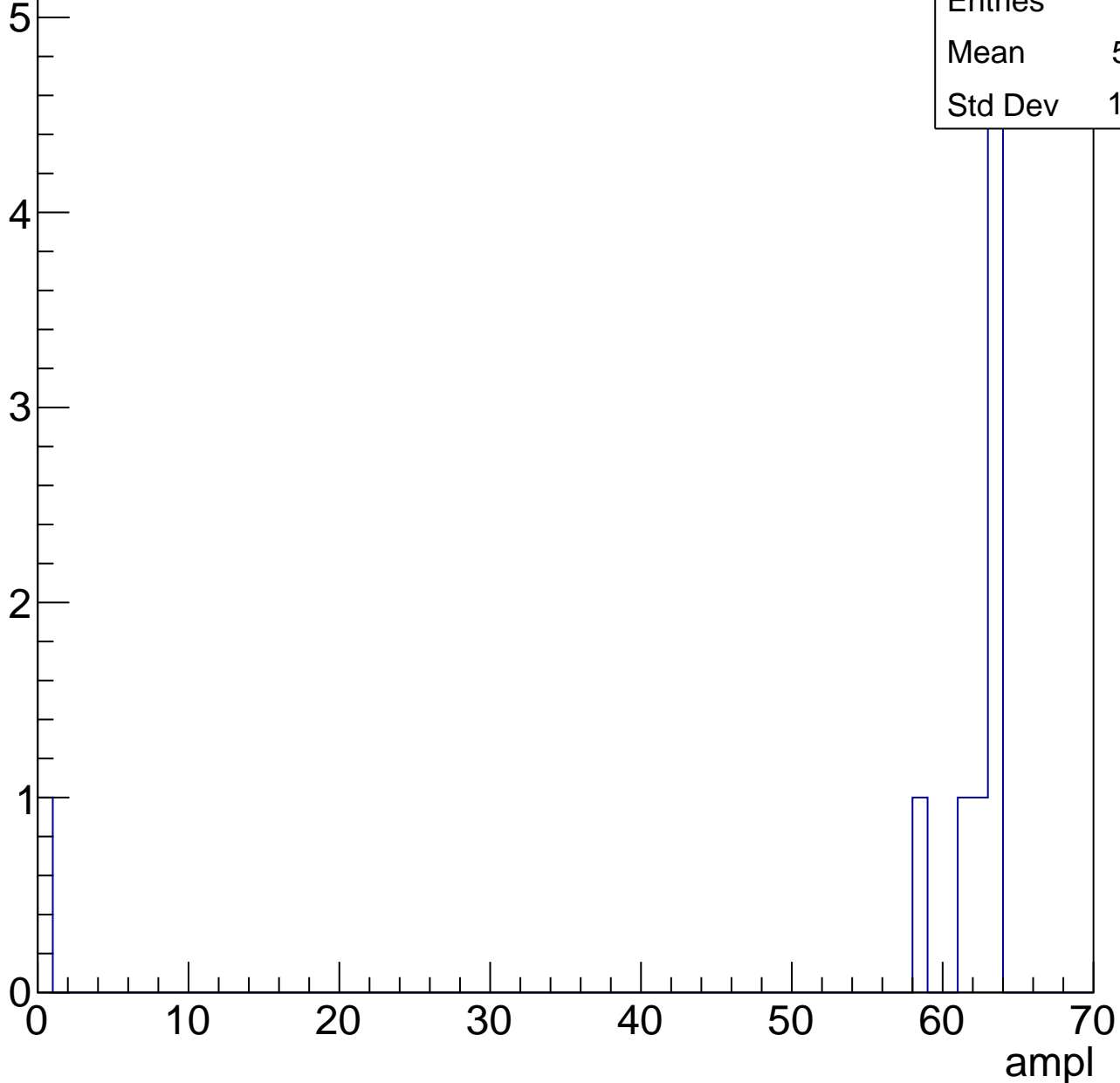
8

B1L103S, U8-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	55.11
Std Dev	19.55



B1L103S, U8-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch41, adc0

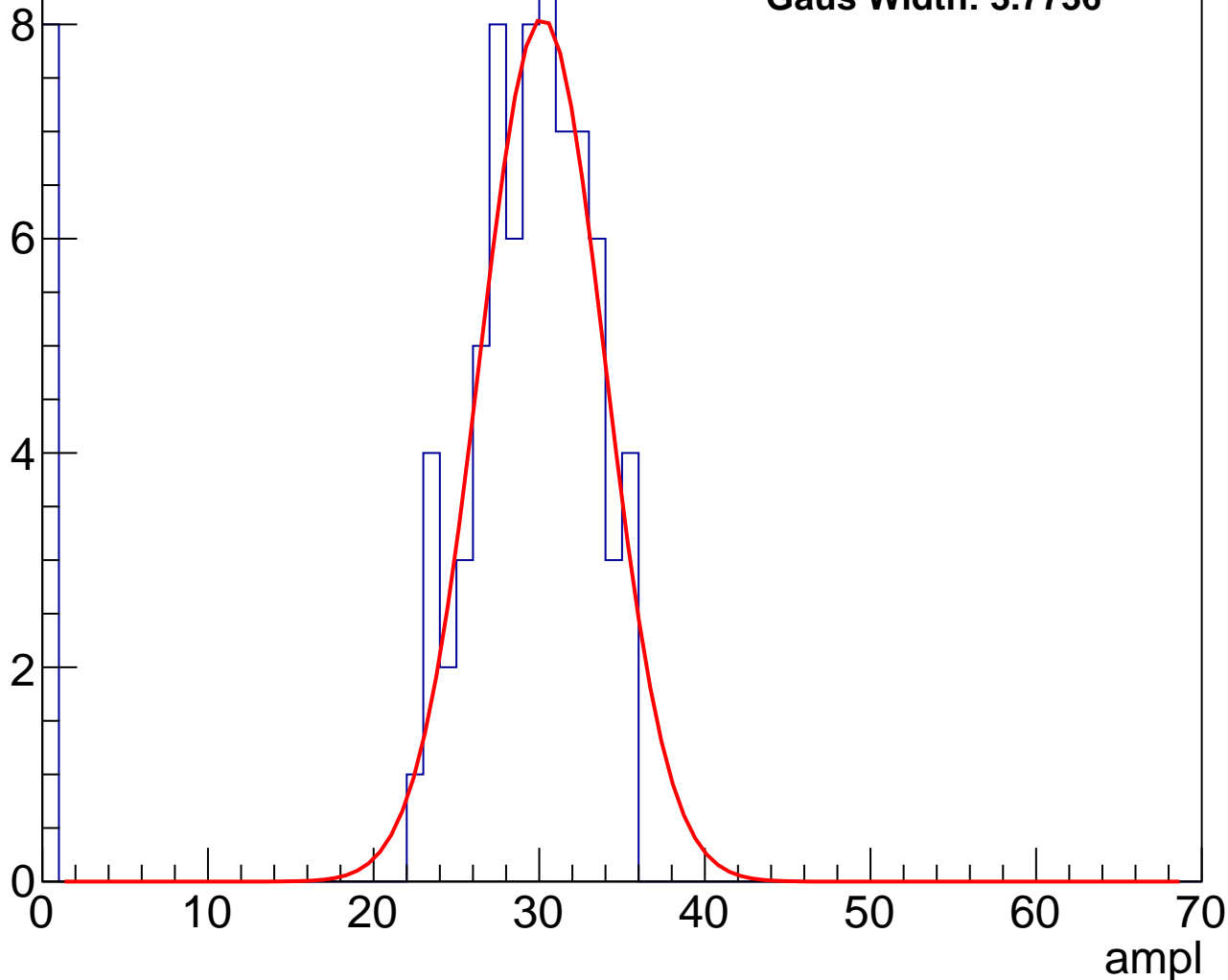
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	26.39
Std Dev	9.213

Gaus mean : 30.1833

Gaus Width: 3.7736



B1L103S, U8-ch41, adc1

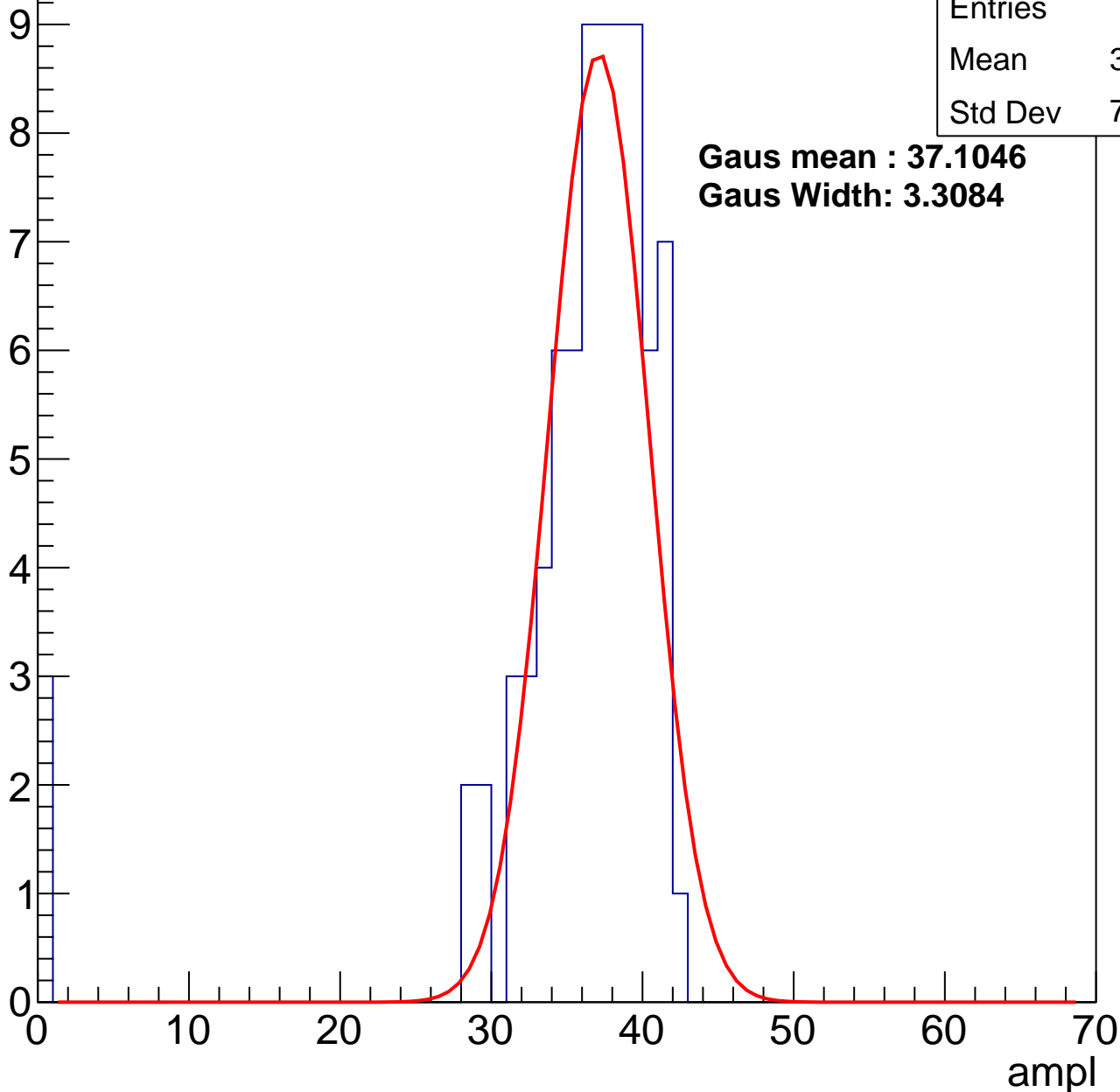
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	35.04
Std Dev	7.683

Gaus mean : 37.1046

Gaus Width: 3.3084



B1L103S, U8-ch41, adc2

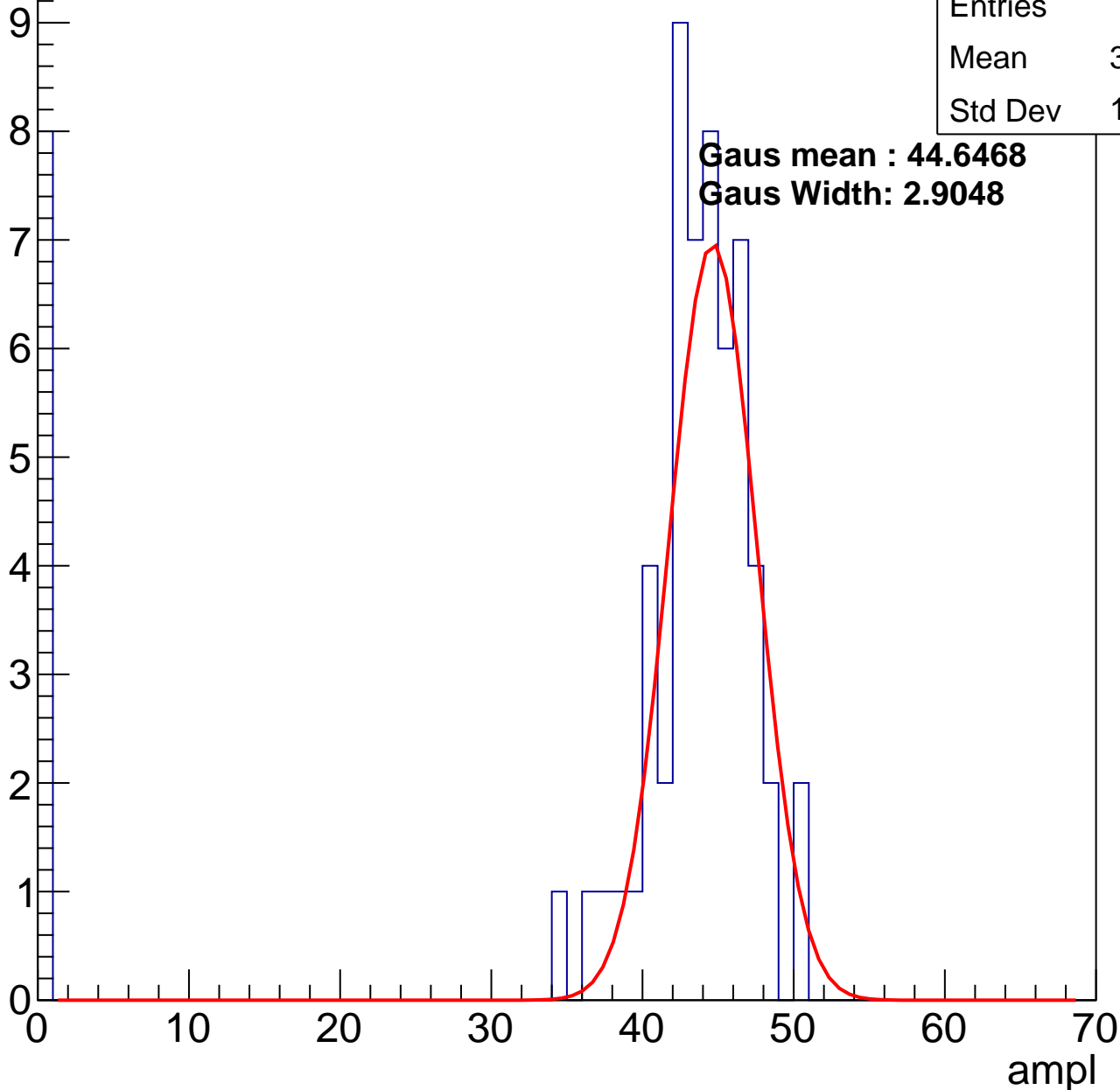
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	38.02
Std Dev	14.67

Gaus mean : 44.6468

Gaus Width: 2.9048



B1L103S, U8-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

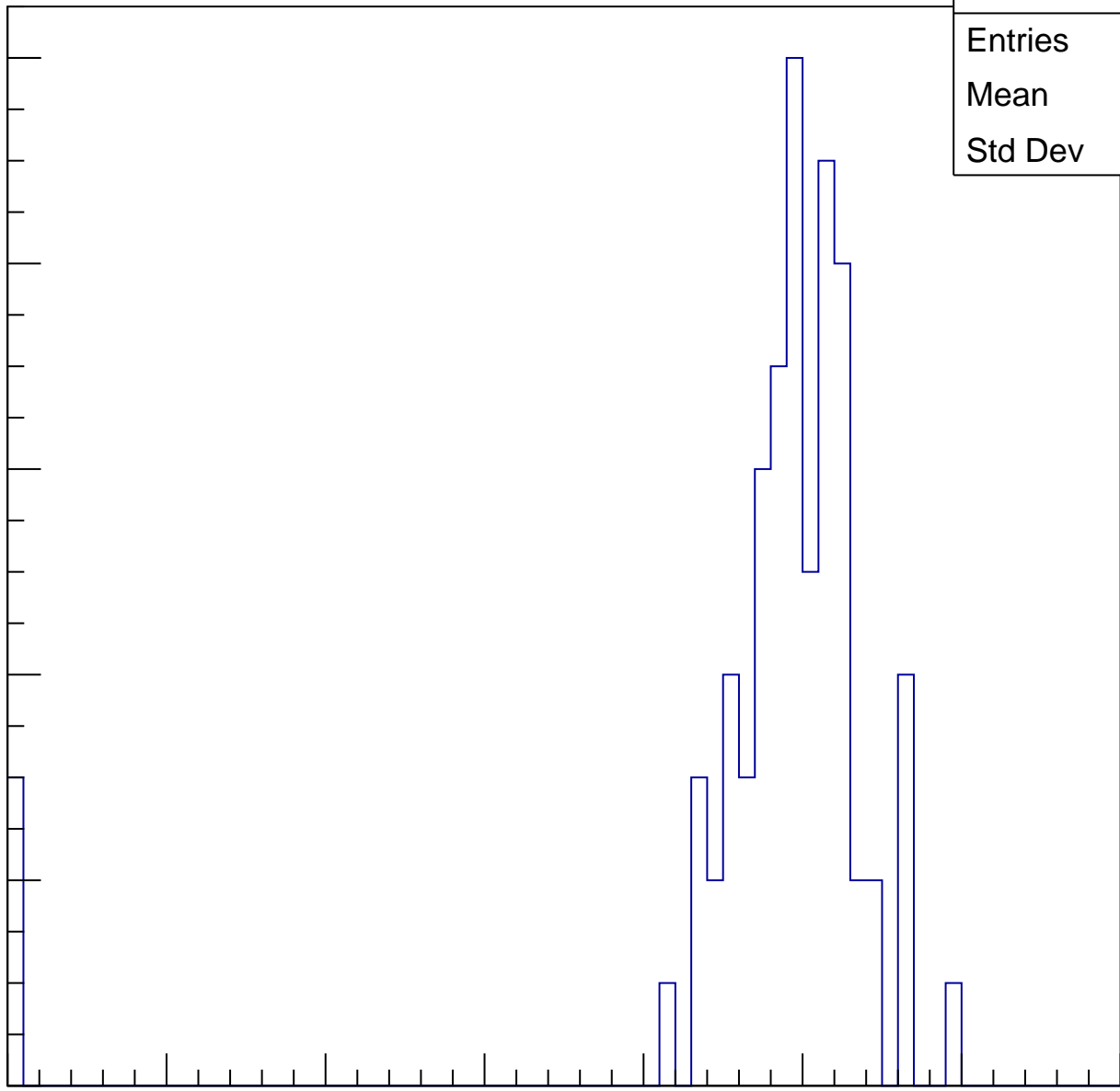
Entries	70
Mean	47.23
Std Dev	10.57

Entry

10
8
6
4
2
0

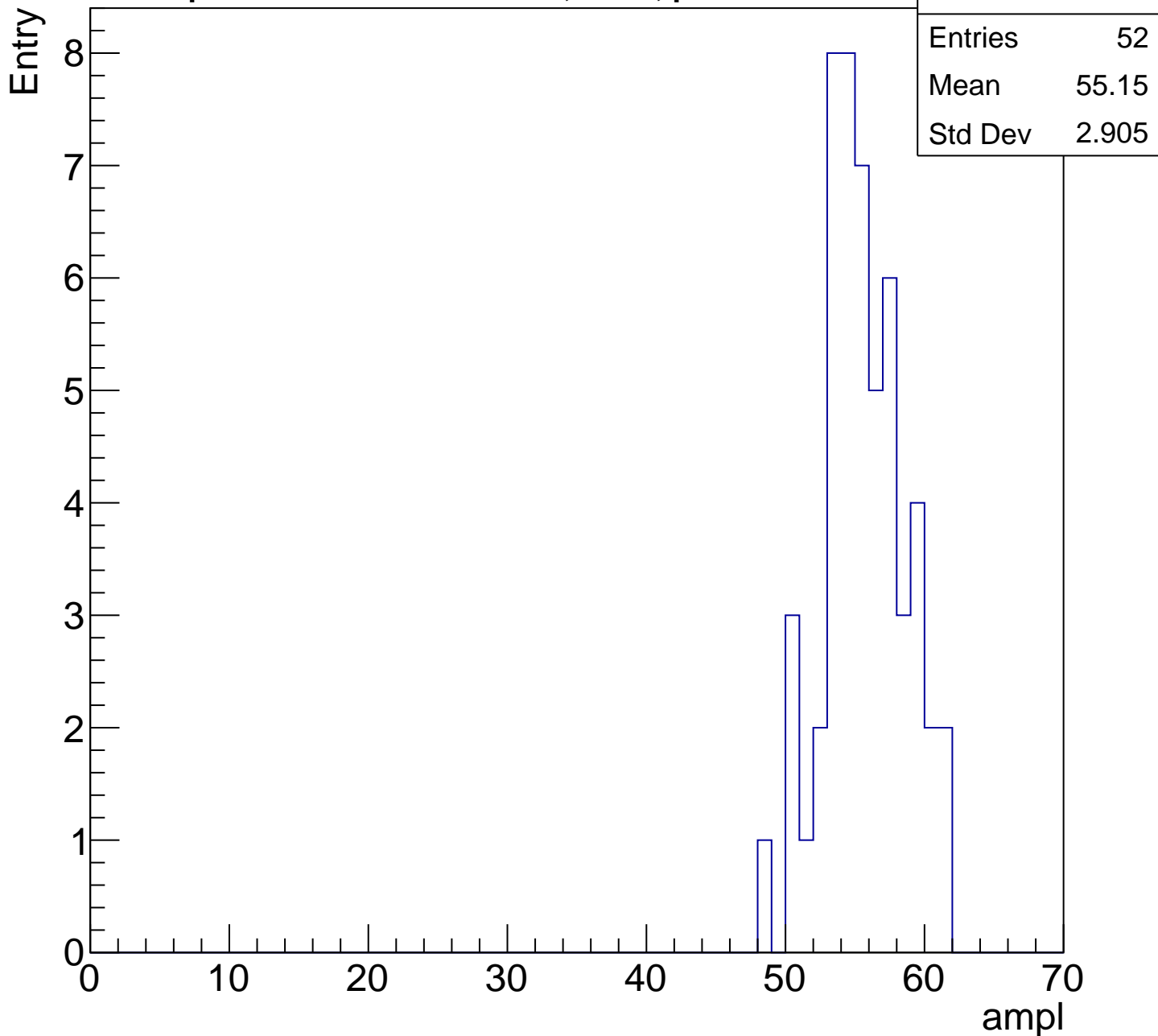
0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2

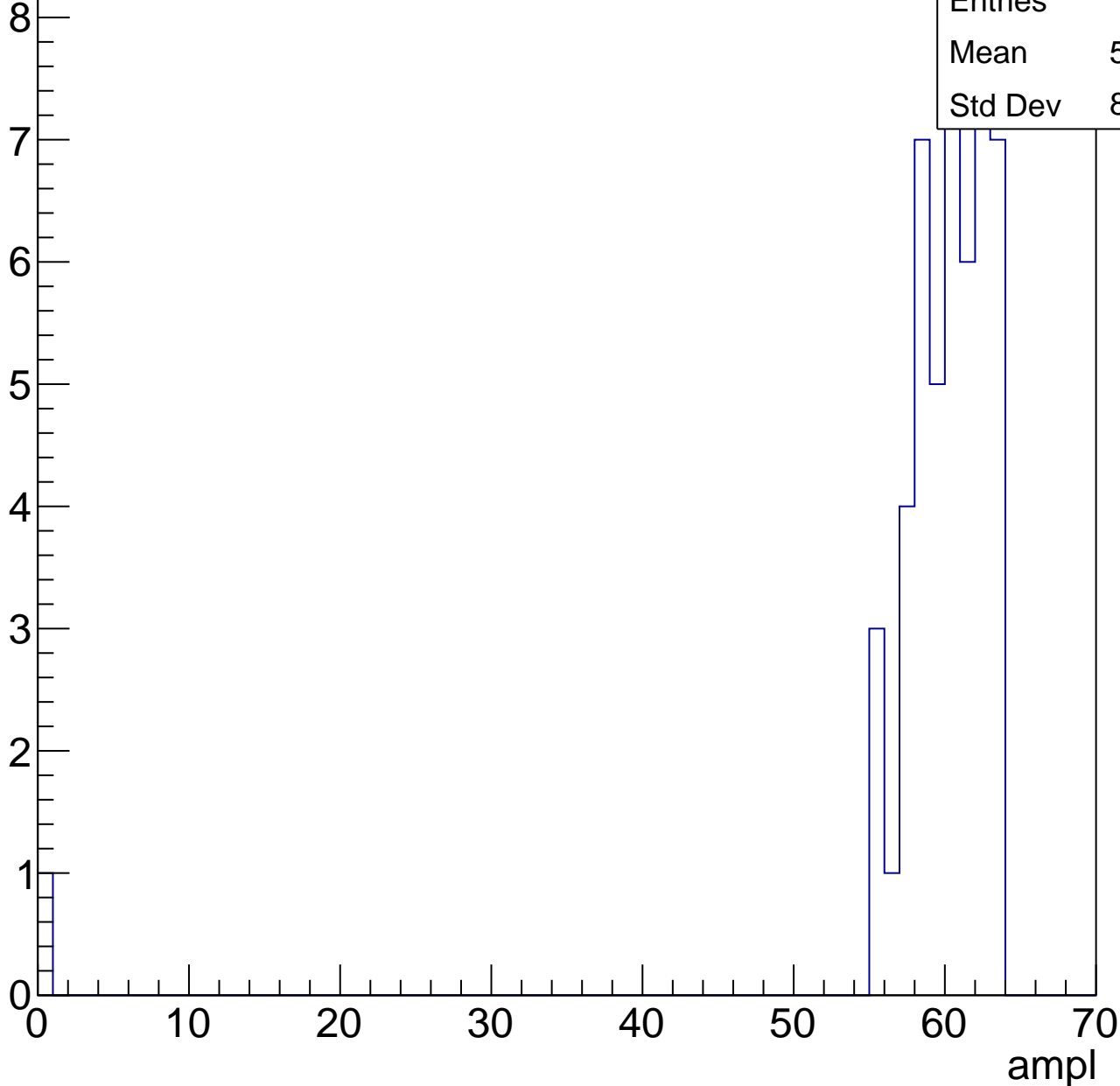


B1L103S, U8-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

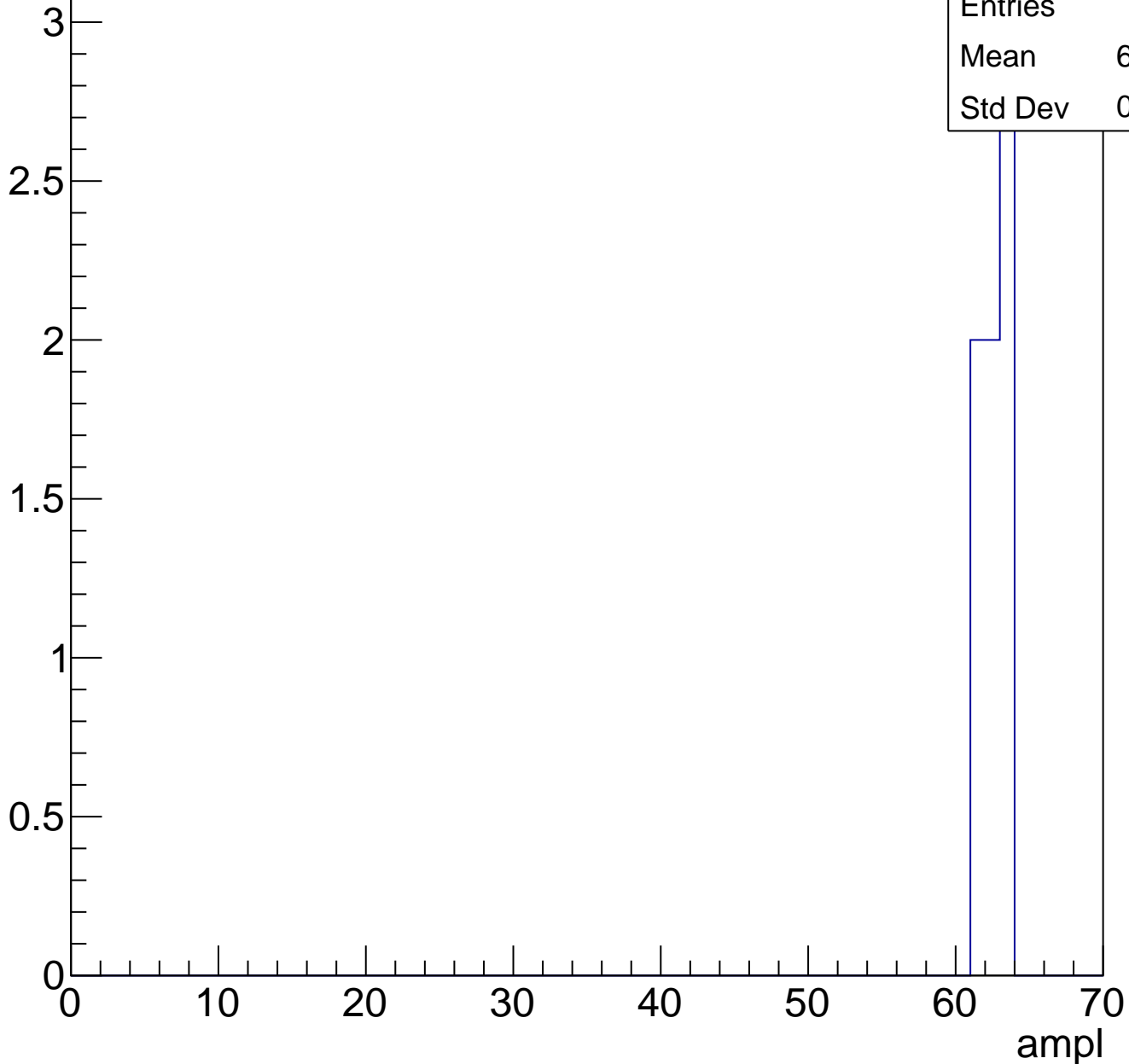
Entries	50
Mean	58.66
Std Dev	8.685



B1L103S, U8-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U8-ch42, adc0

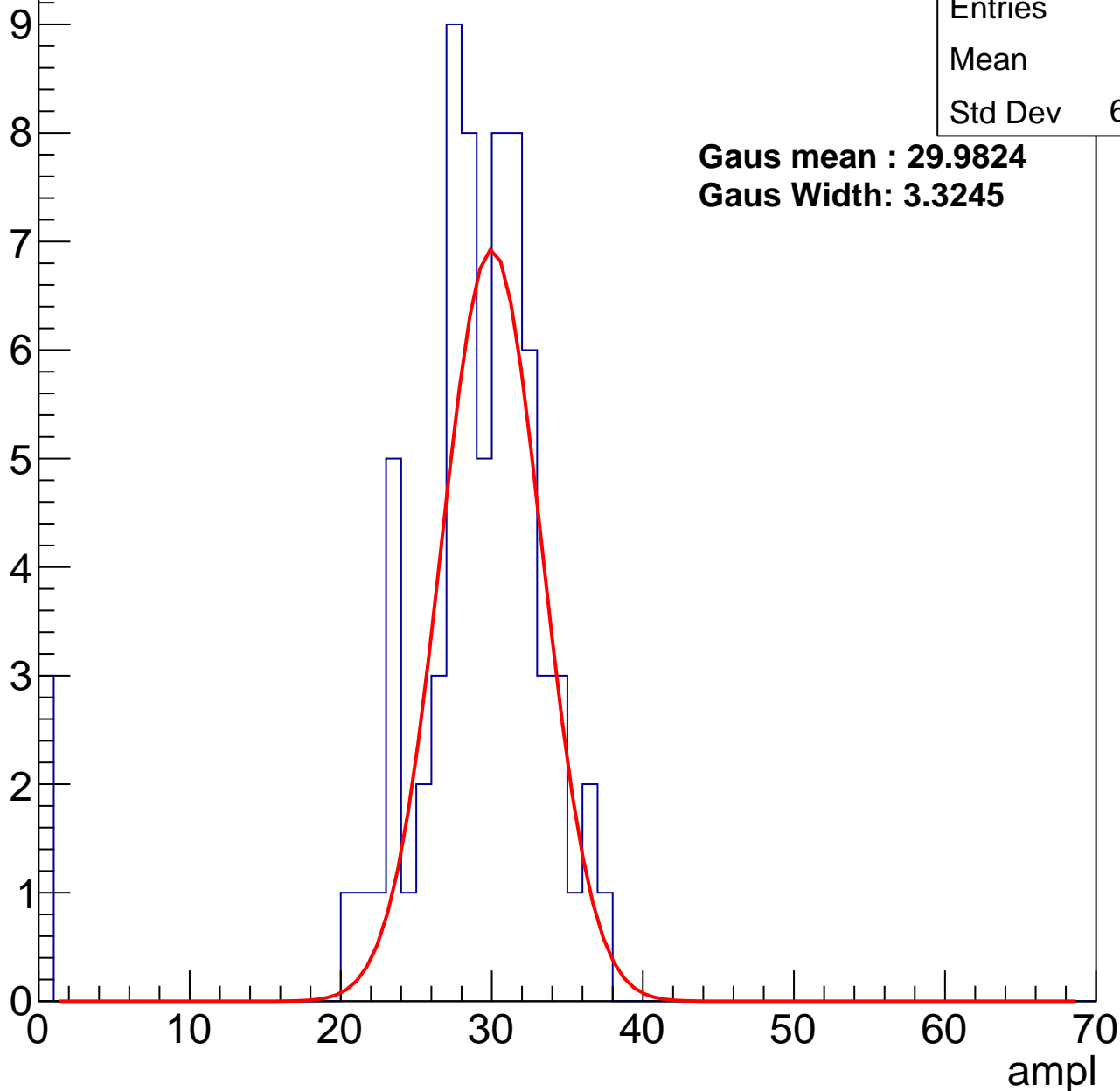
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	27.7
Std Dev	6.839

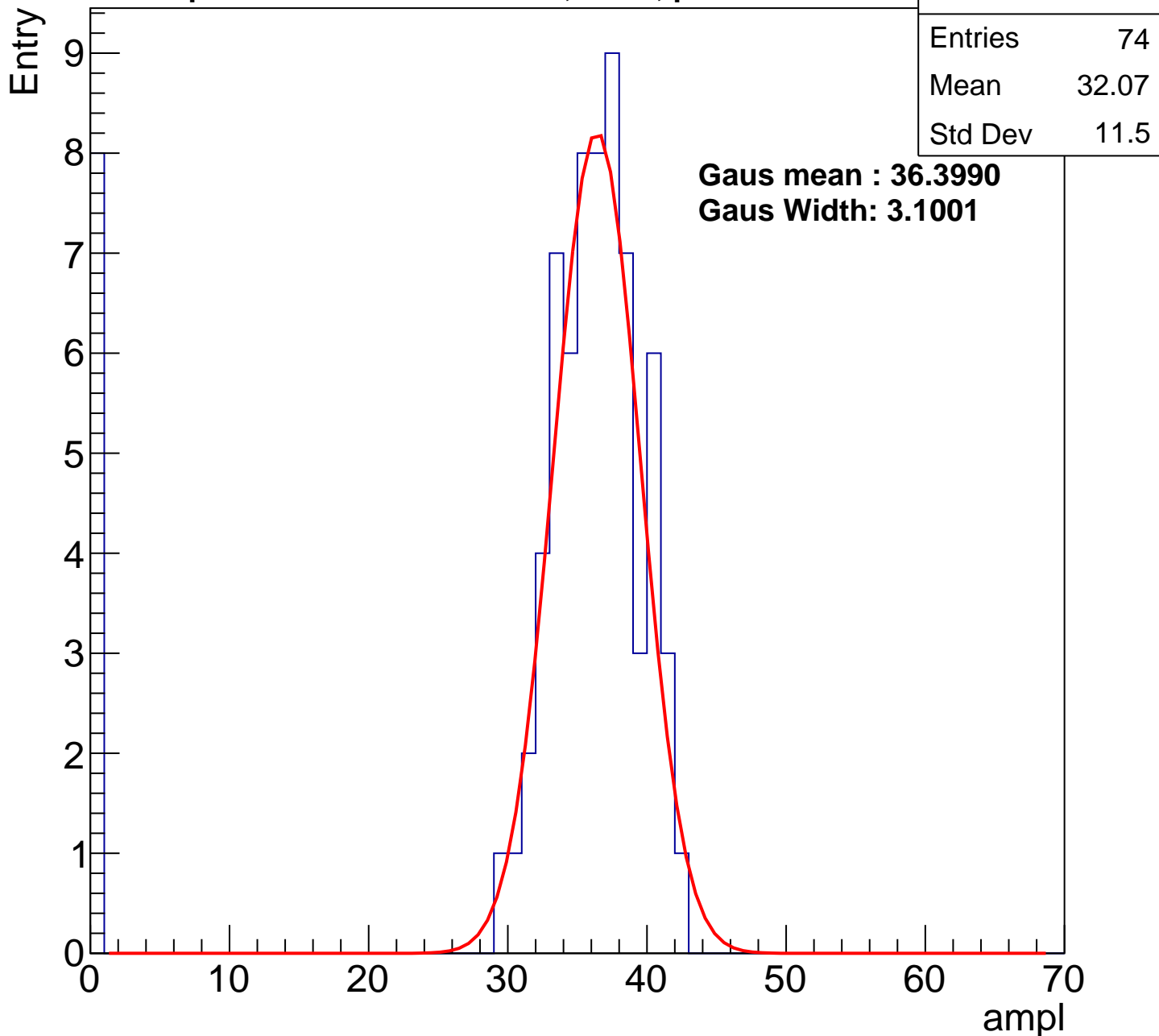
Gaus mean : 29.9824

Gaus Width: 3.3245



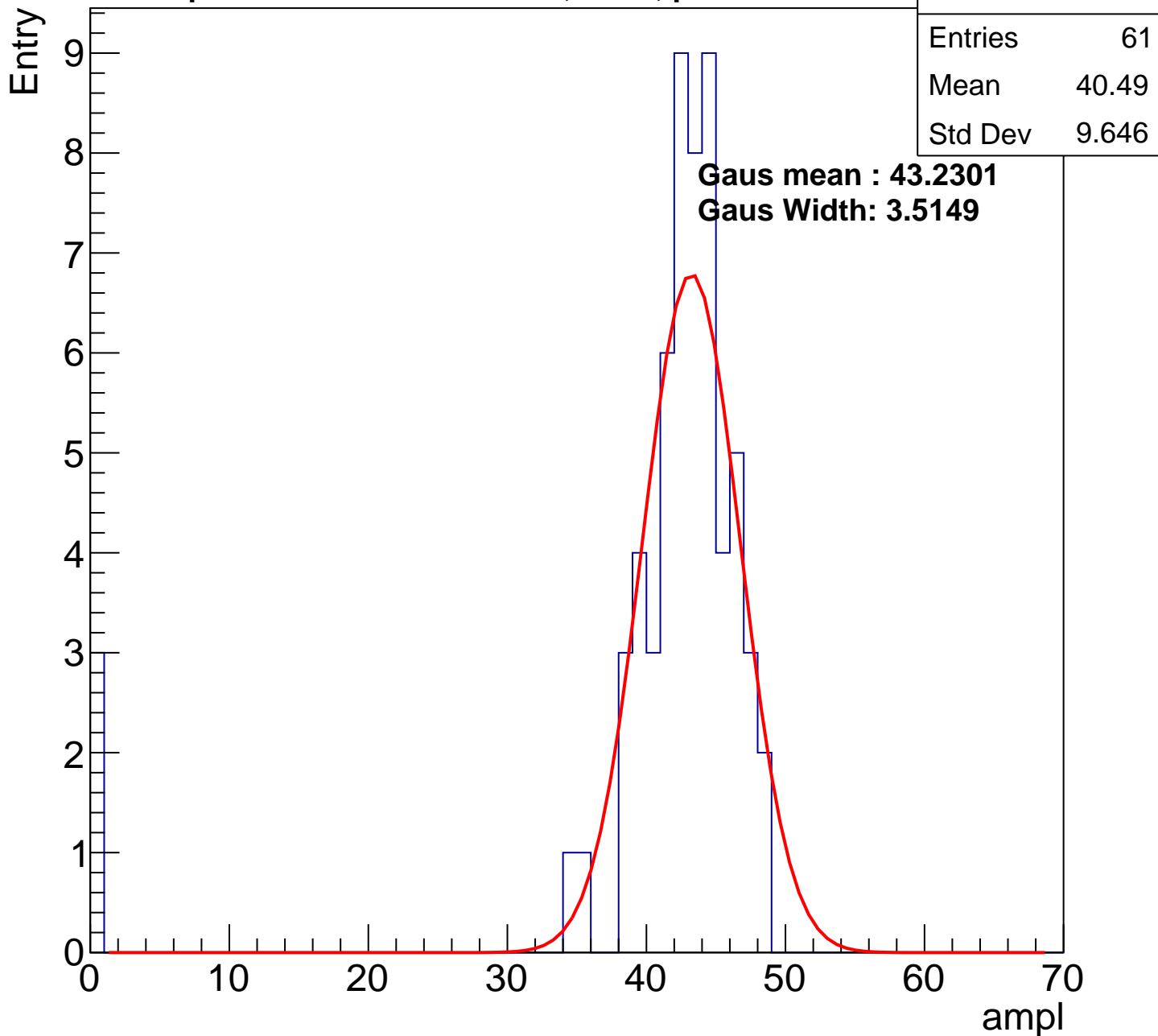
B1L103S, U8-ch42, adc1

calib_packv5_041523_1651.root, FC#0, port C2



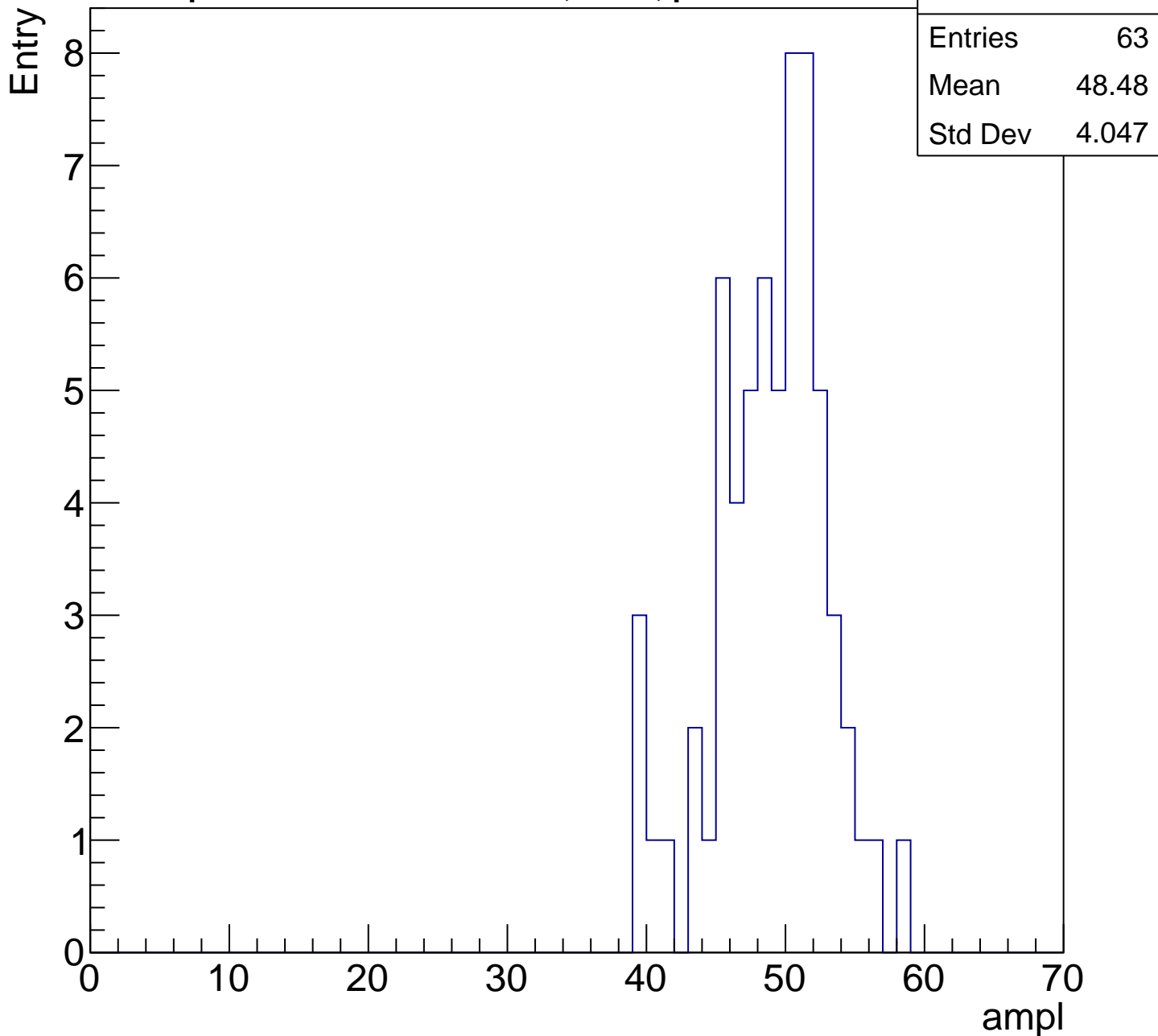
B1L103S, U8-ch42, adc2

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch42, adc3

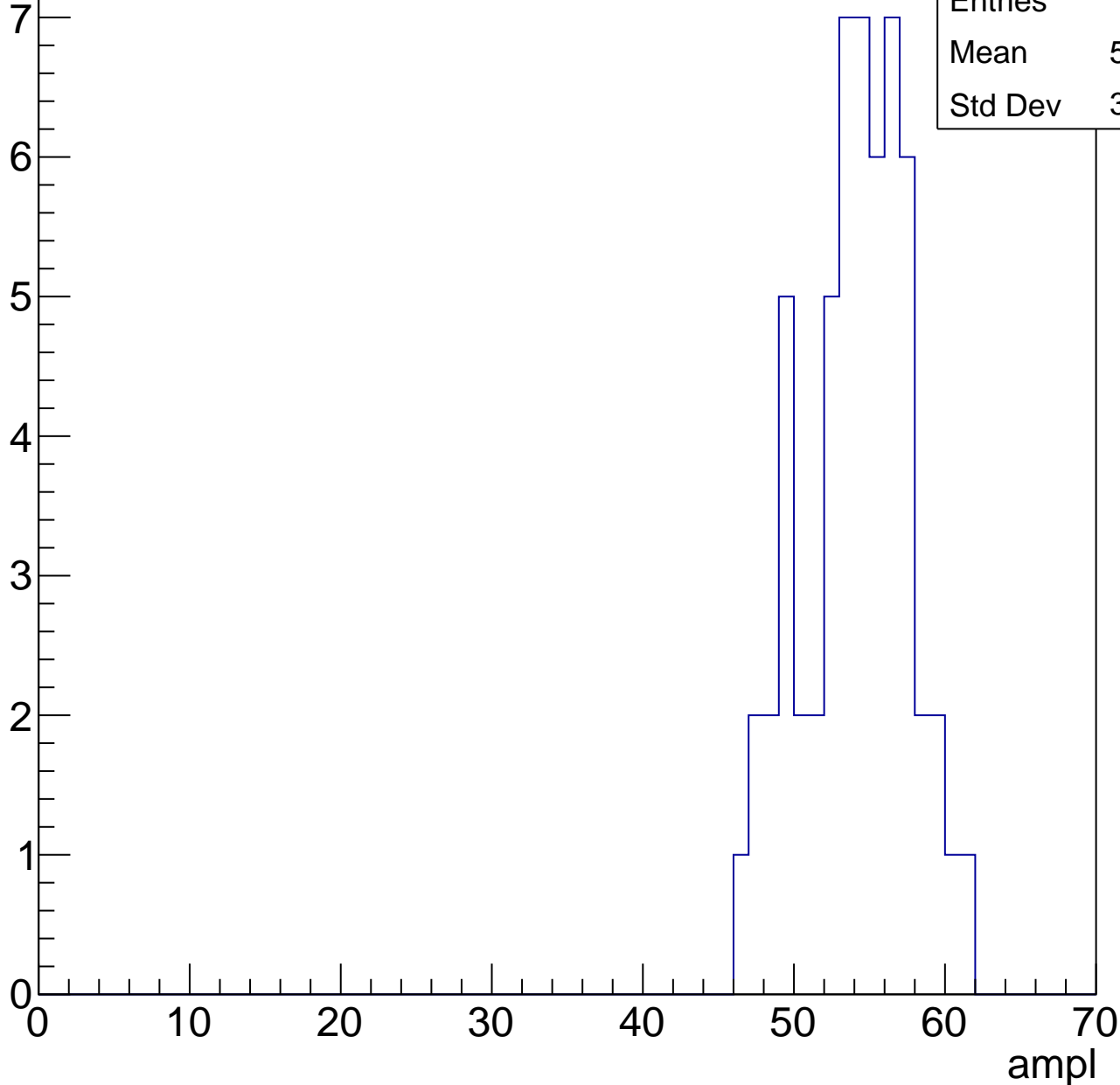
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	58
Mean	53.64
Std Dev	3.433

B1L103S, U8-ch42, adc5

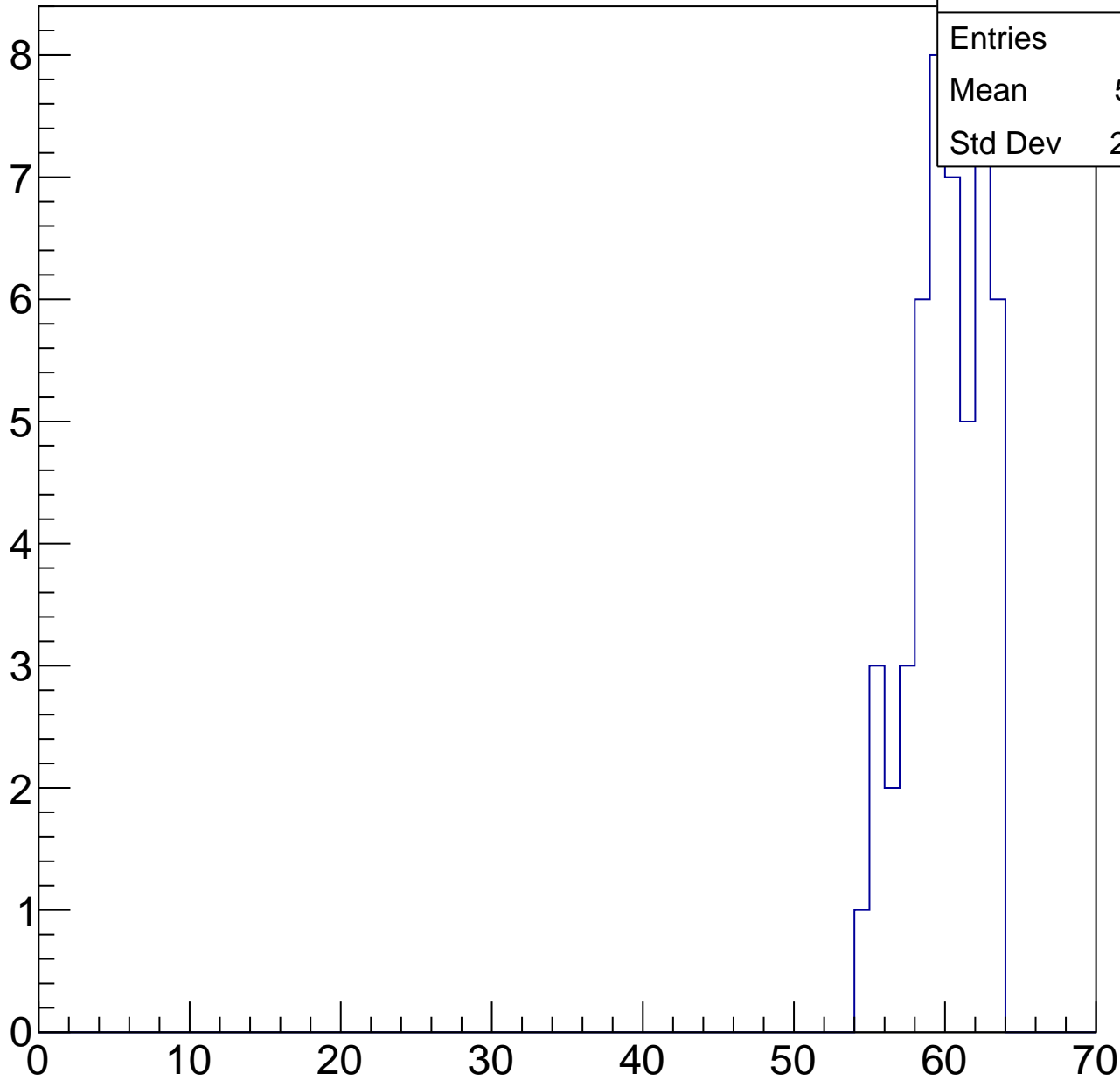
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.61
Std Dev	2.414

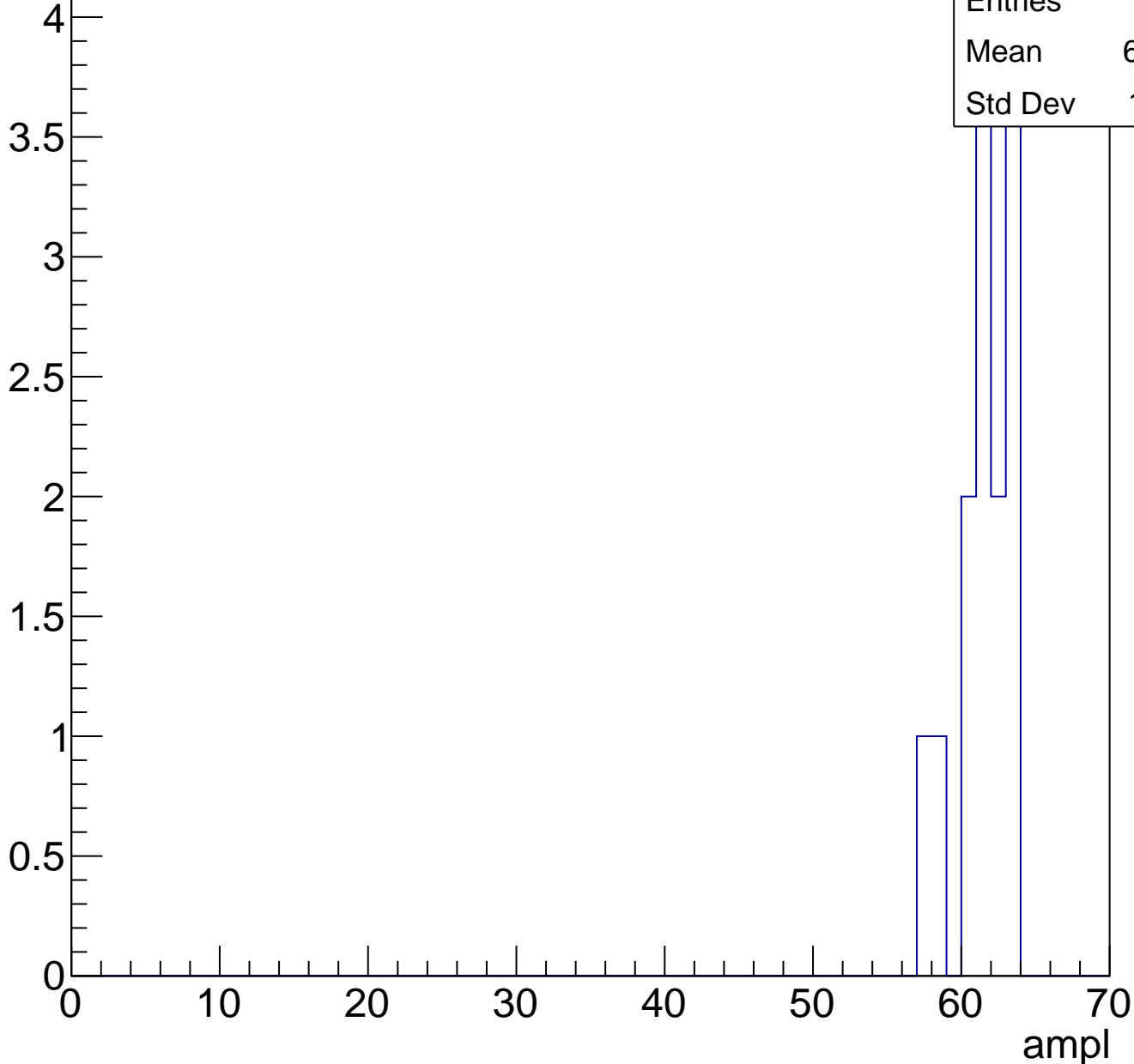
ampl



B1L103S, U8-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch43, adc0

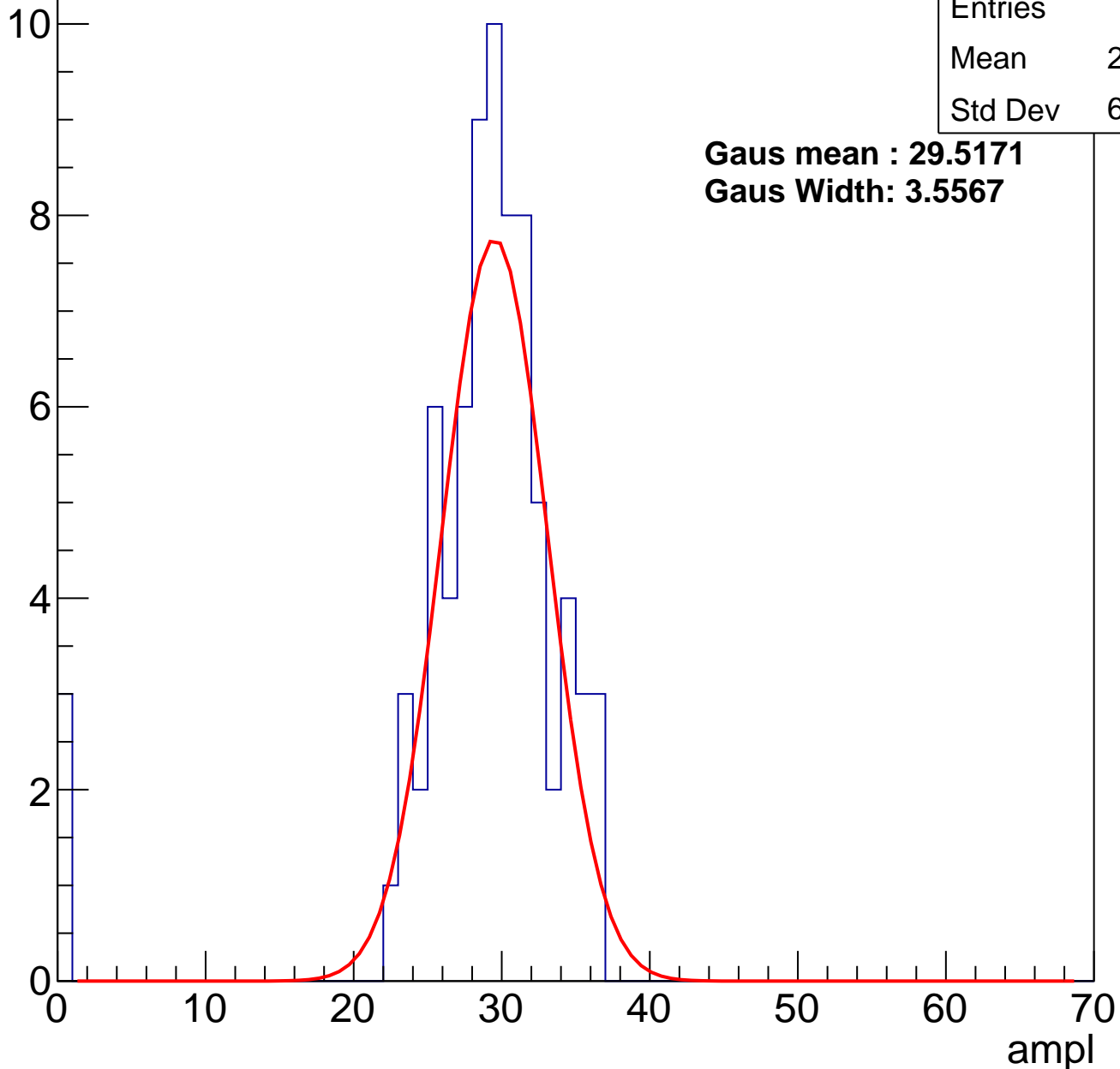
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	28.05
Std Dev	6.544

Gaus mean : 29.5171

Gaus Width: 3.5567

Entry



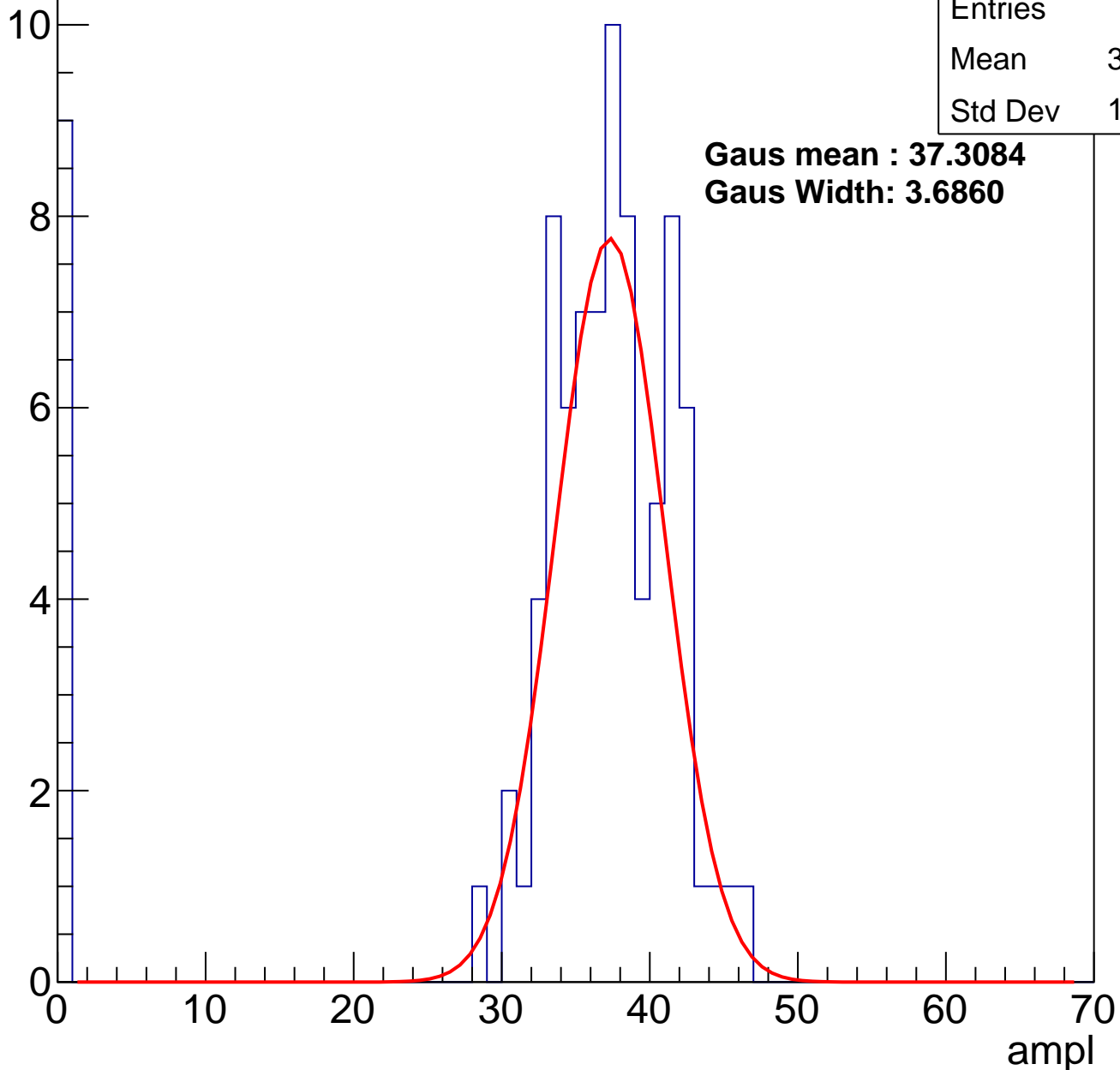
B1L103S, U8-ch43, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	33.33
Std Dev	11.65

Gaus mean : 37.3084
Gaus Width: 3.6860

Entry



B1L103S, U8-ch43, adc2

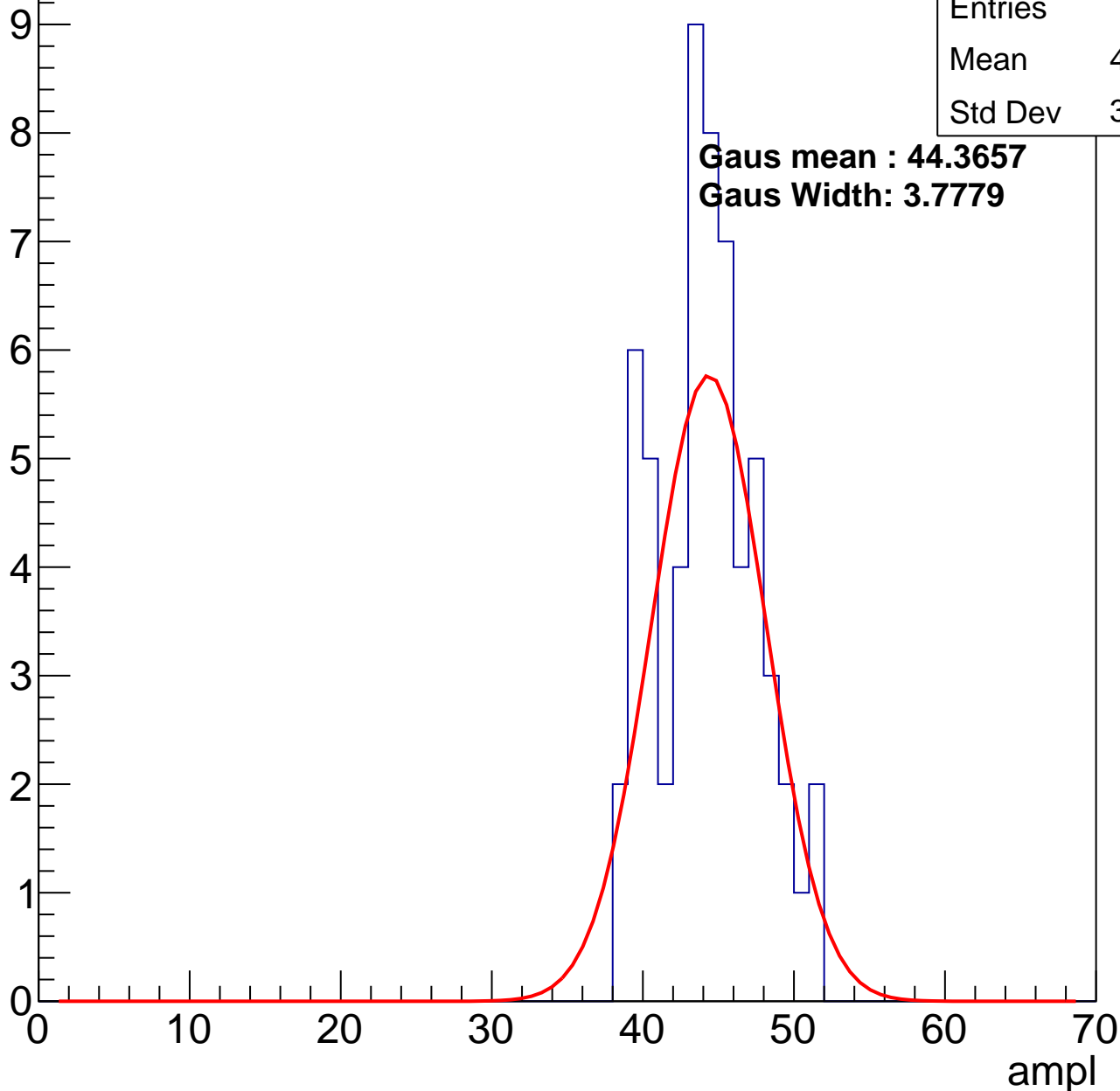
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	43.78
Std Dev	3.272

Gaus mean : 44.3657

Gaus Width: 3.7779

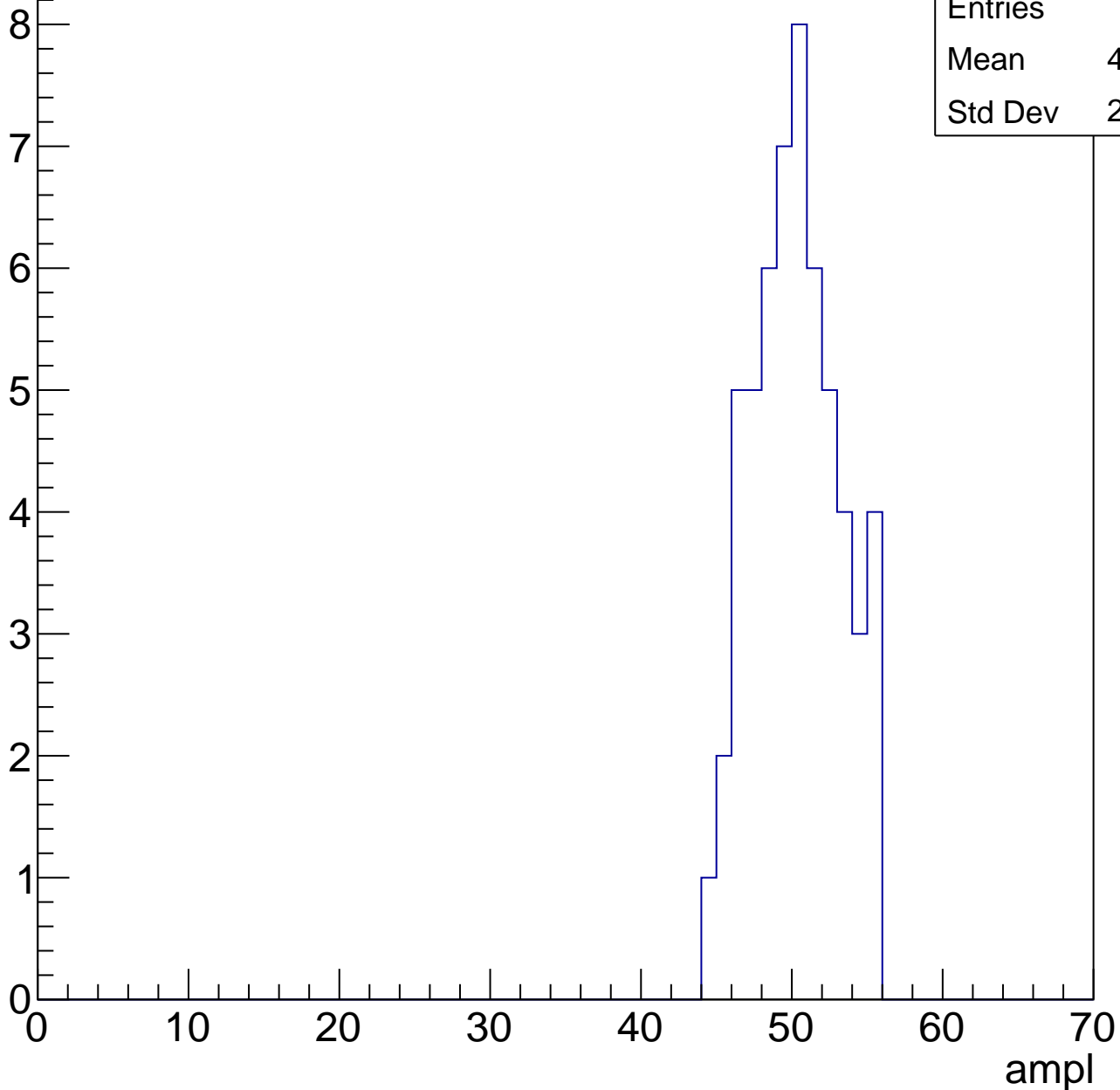


B1L103S, U8-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	49.82
Std Dev	2.835

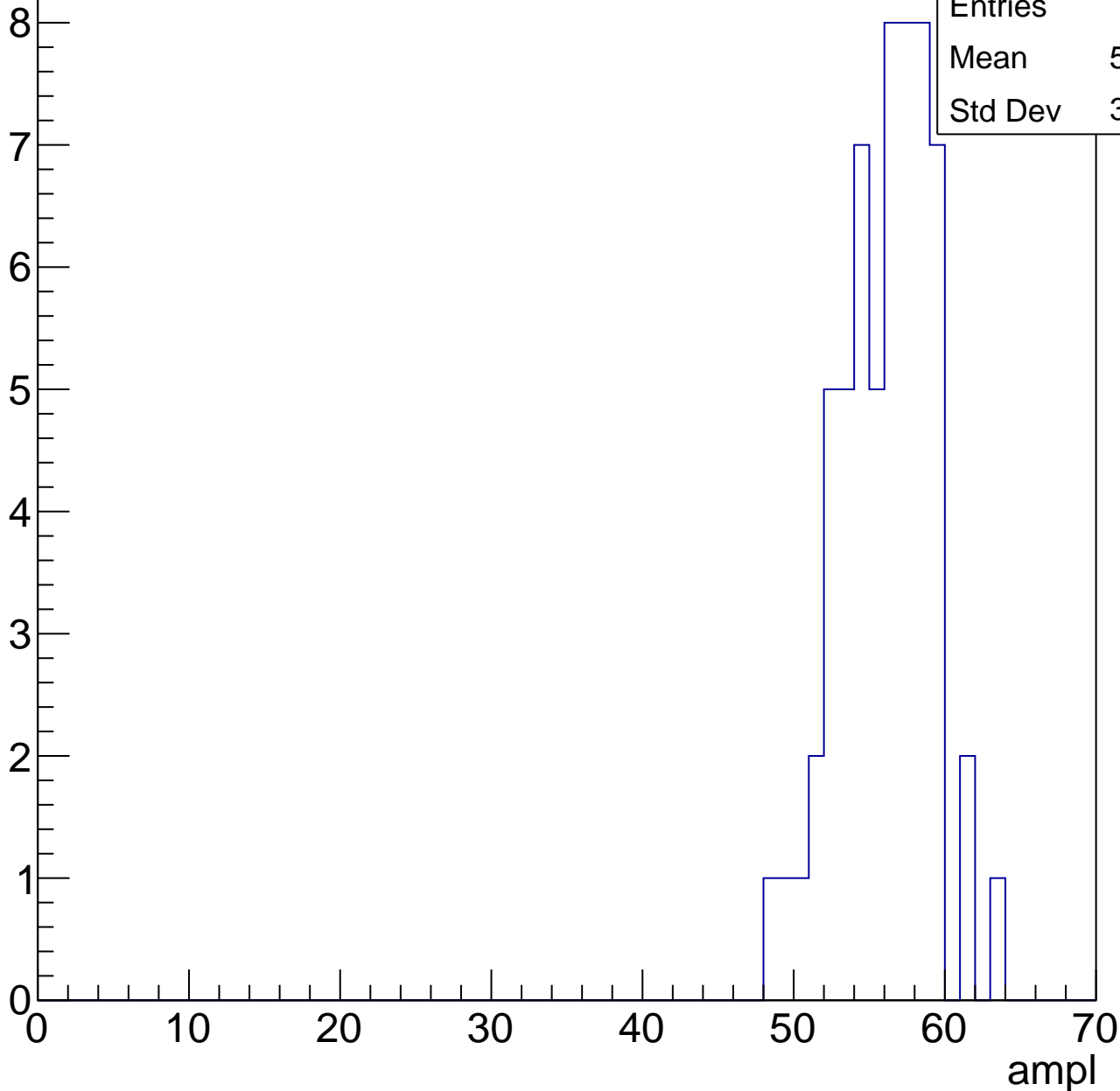


B1L103S, U8-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55.62
Std Dev	3.004



B1L103S, U8-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

Entries

45

Mean

58.96

Std Dev

9.18

ampl

0

10

20

30

40

50

60

70

B1L103S, U8-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch44, adc0

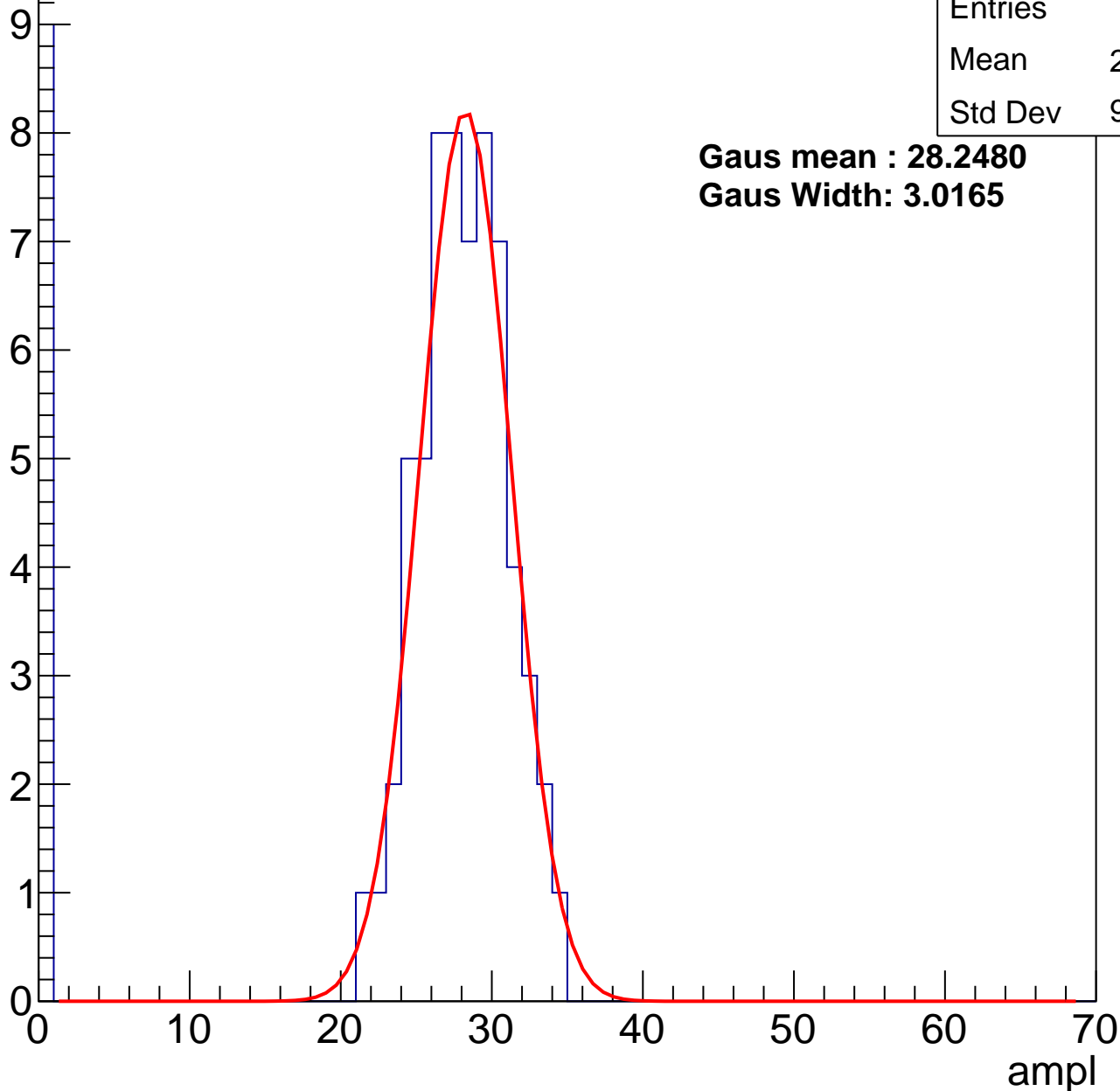
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	24.17
Std Dev	9.583

Gaus mean : 28.2480

Gaus Width: 3.0165



B1L103S, U8-ch44, adc1

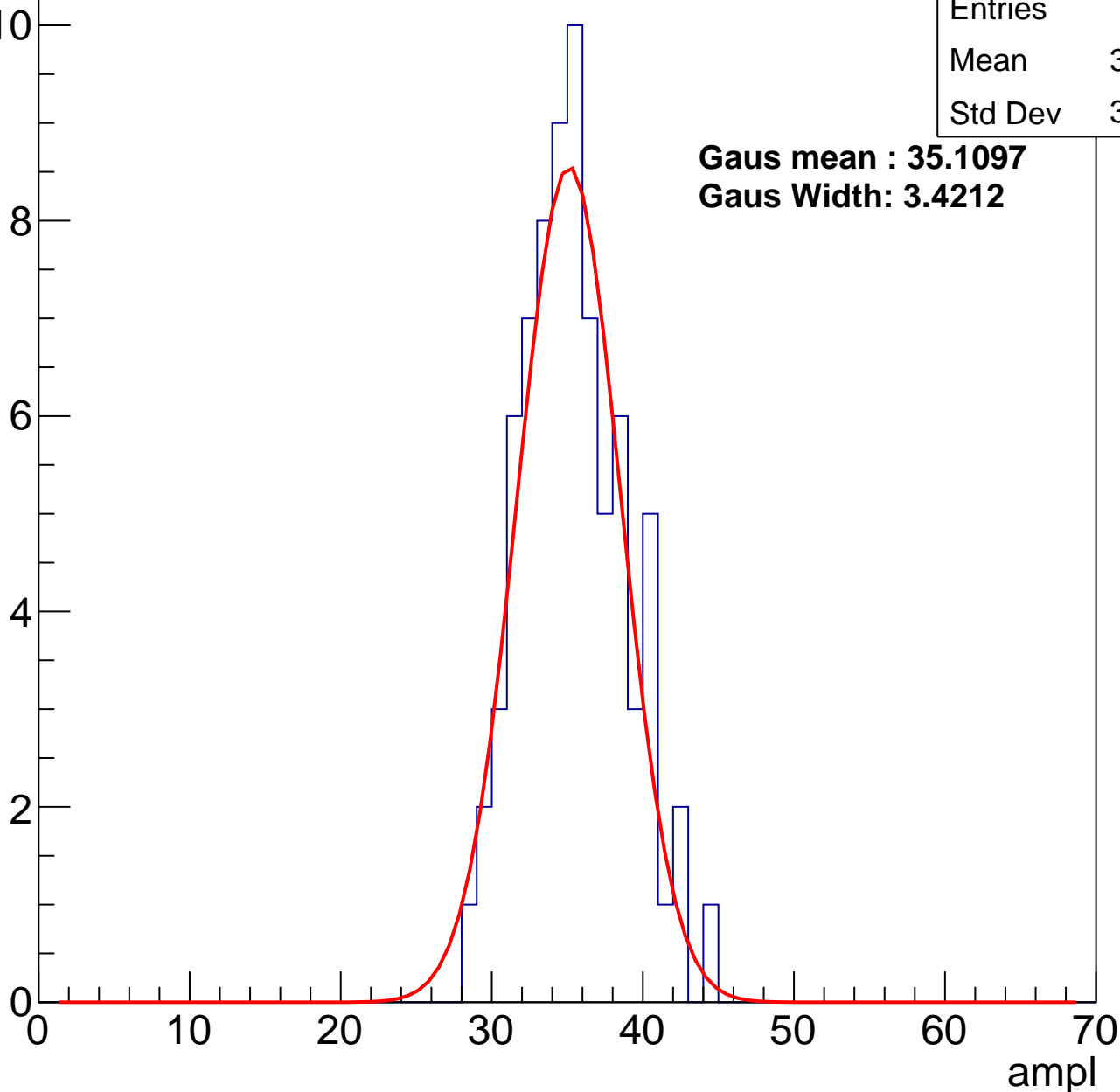
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.96
Std Dev	3.373

Gaus mean : 35.1097

Gaus Width: 3.4212



B1L103S, U8-ch44, adc2

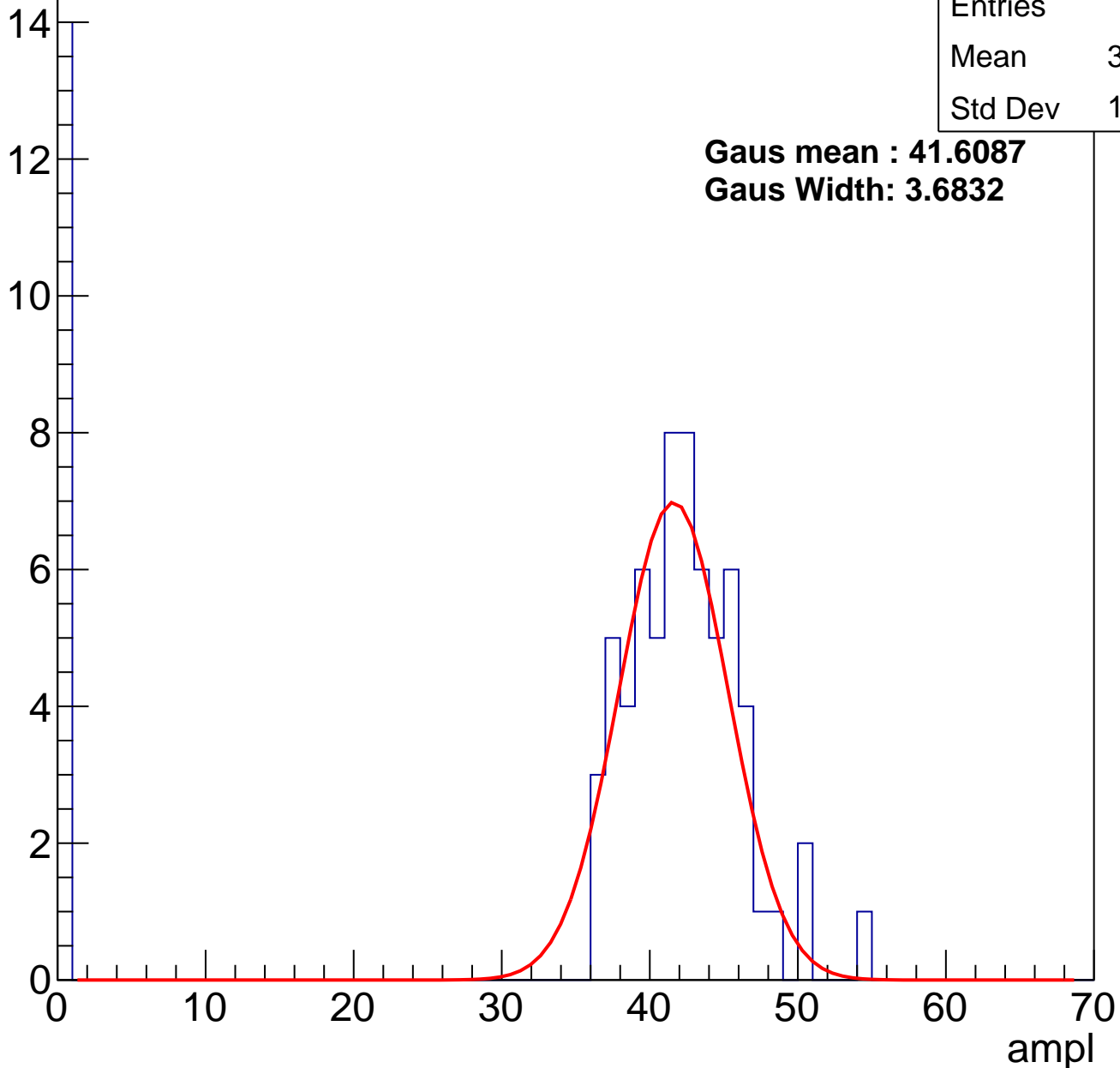
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	34.48
Std Dev	16.34

Gaus mean : 41.6087

Gaus Width: 3.6832

Entry

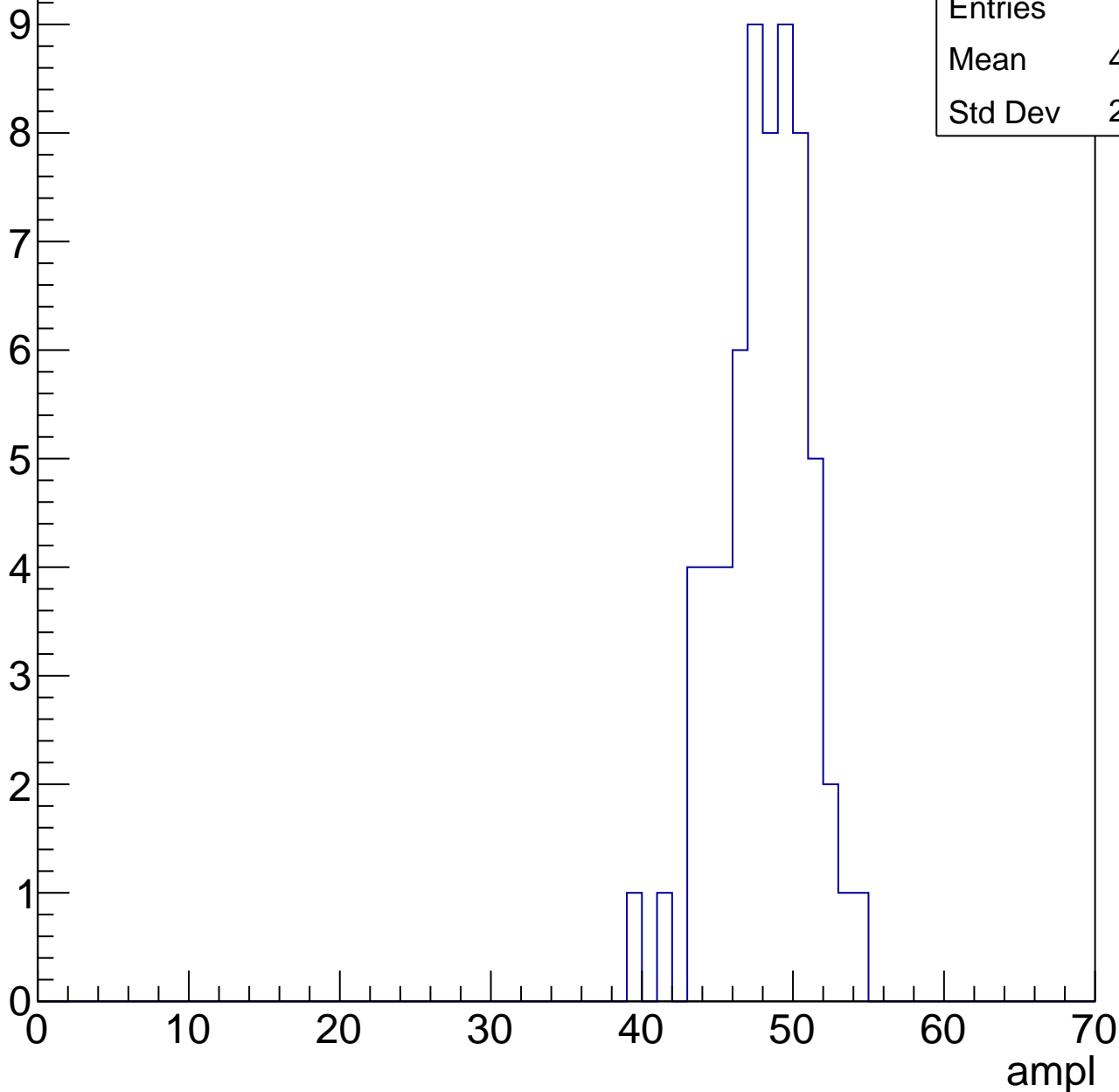


B1L103S, U8-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

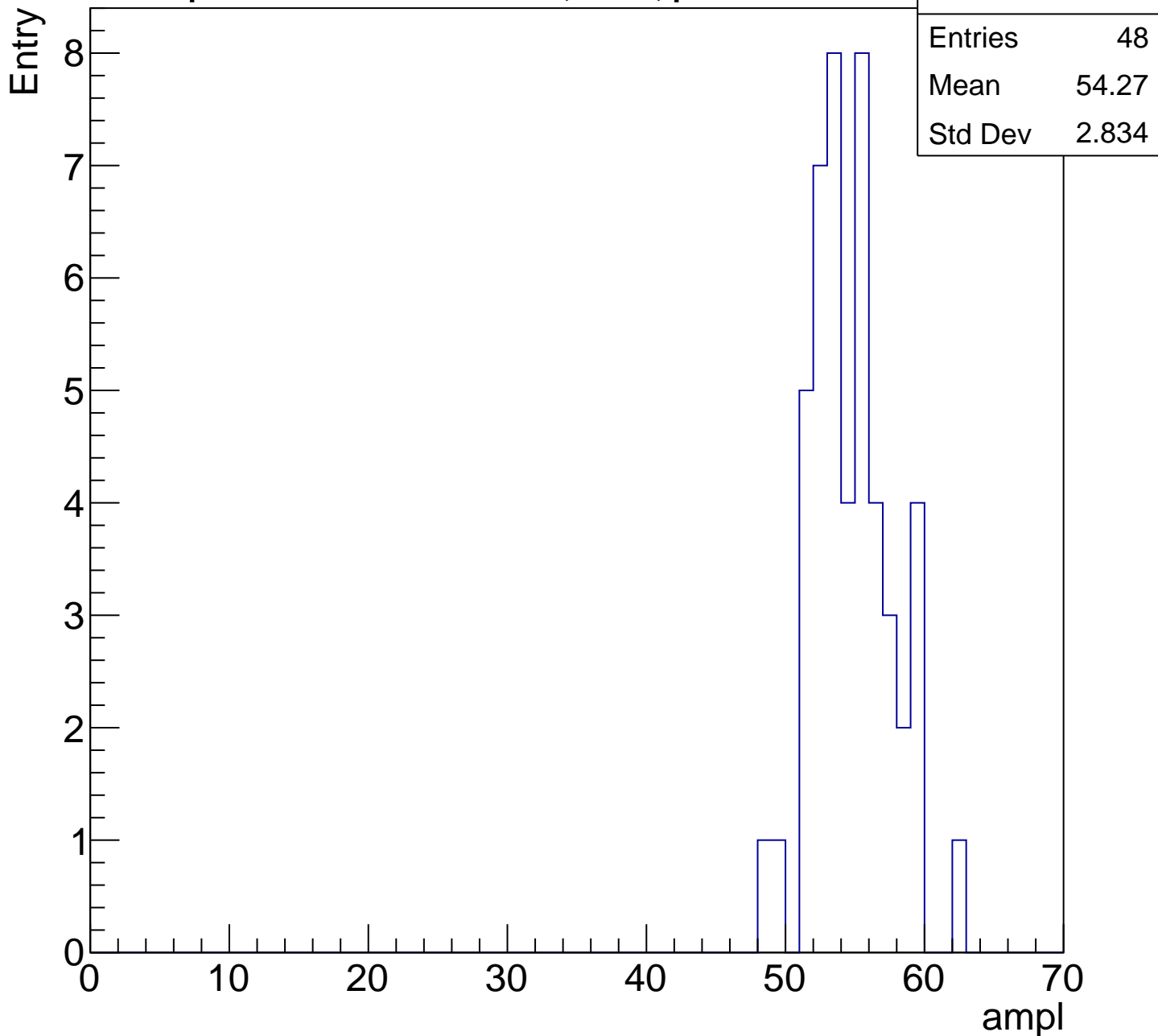
Entry

Entries	63
Mean	47.59
Std Dev	2.904



B1L103S, U8-ch44, adc4

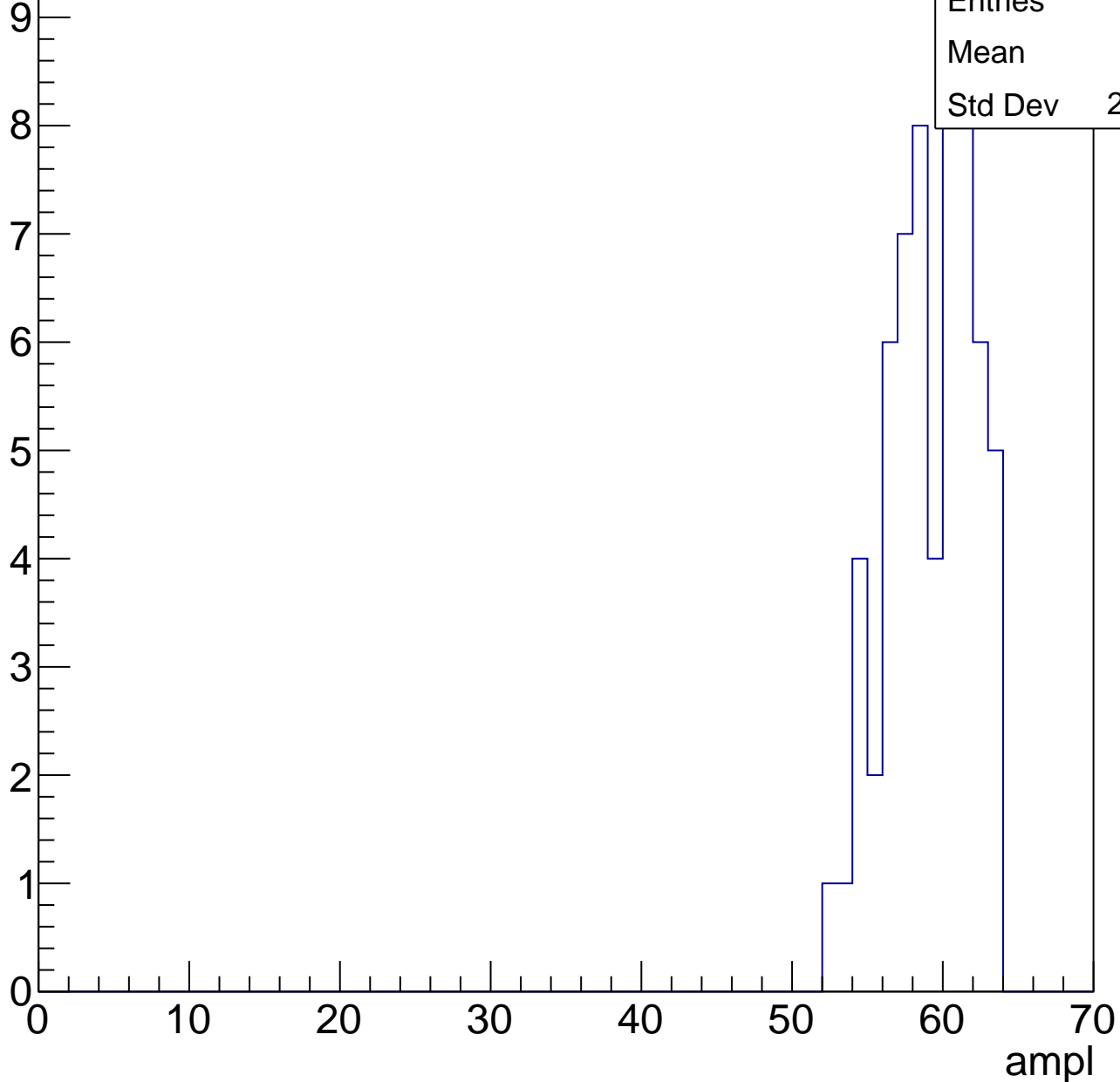
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

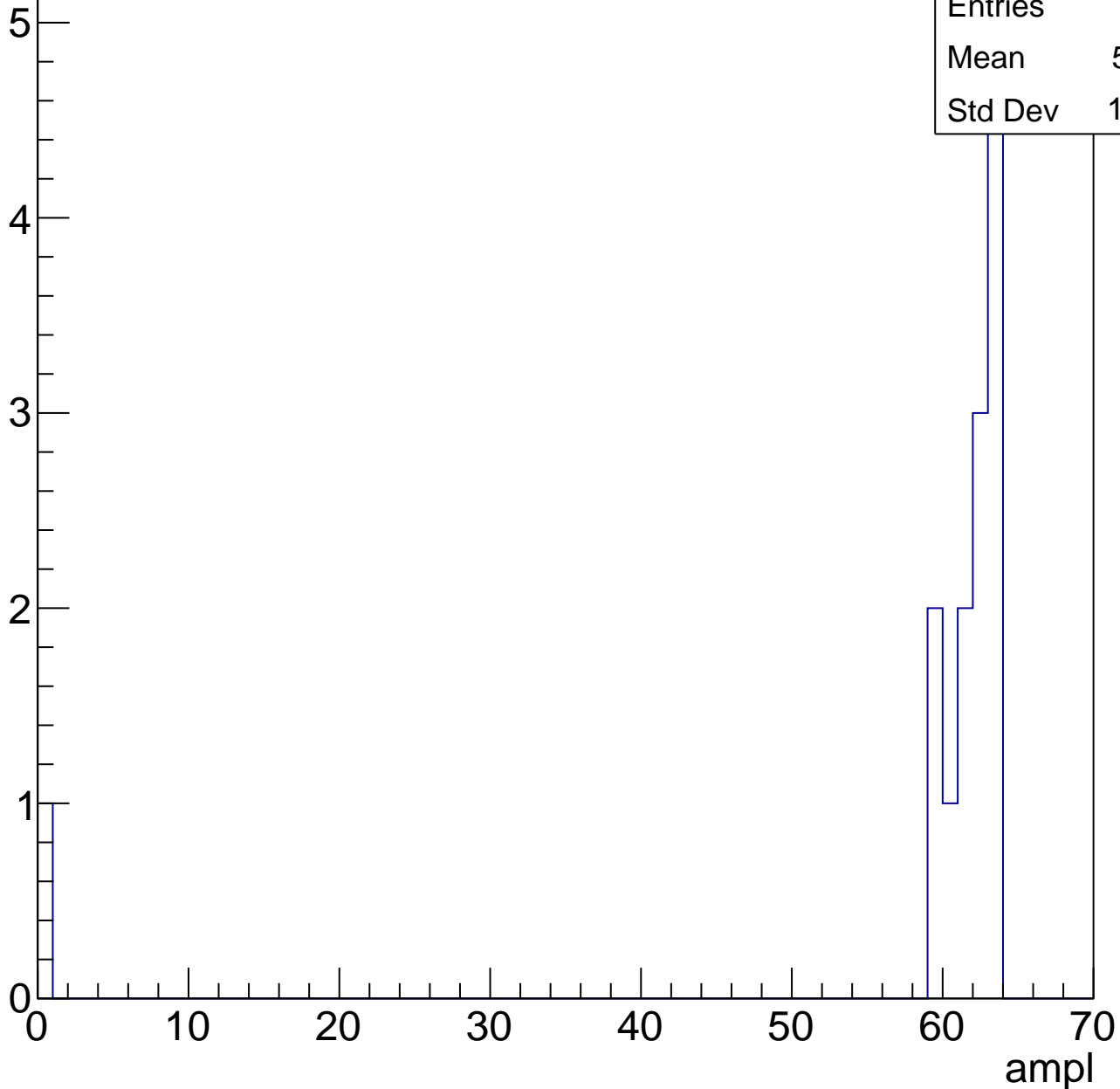


B1L103S, U8-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57.21
Std Dev	15.93



B1L103S, U8-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch45, adc0

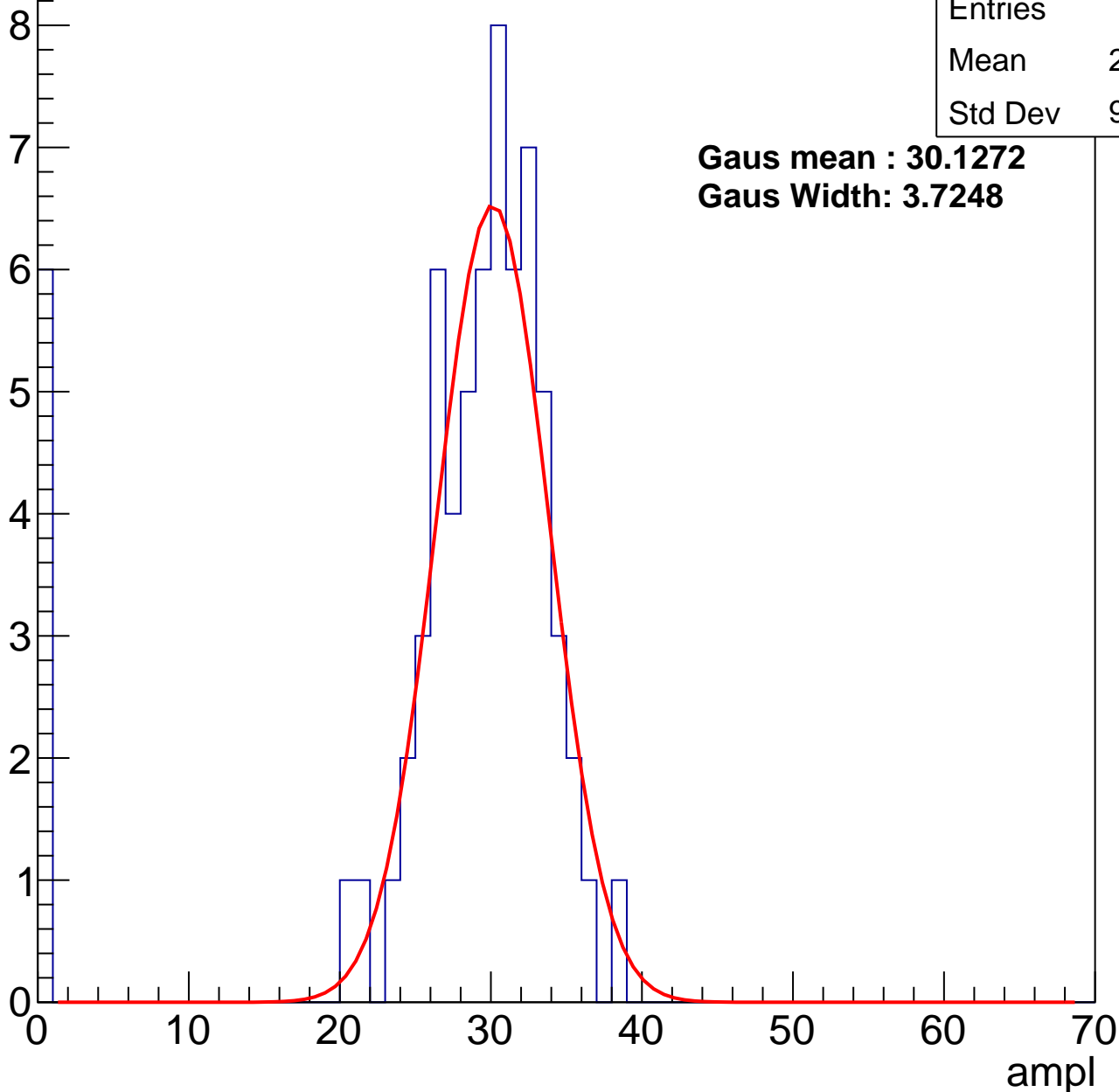
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	26.85
Std Dev	9.025

Gaus mean : 30.1272

Gaus Width: 3.7248



B1L103S, U8-ch45, adc1

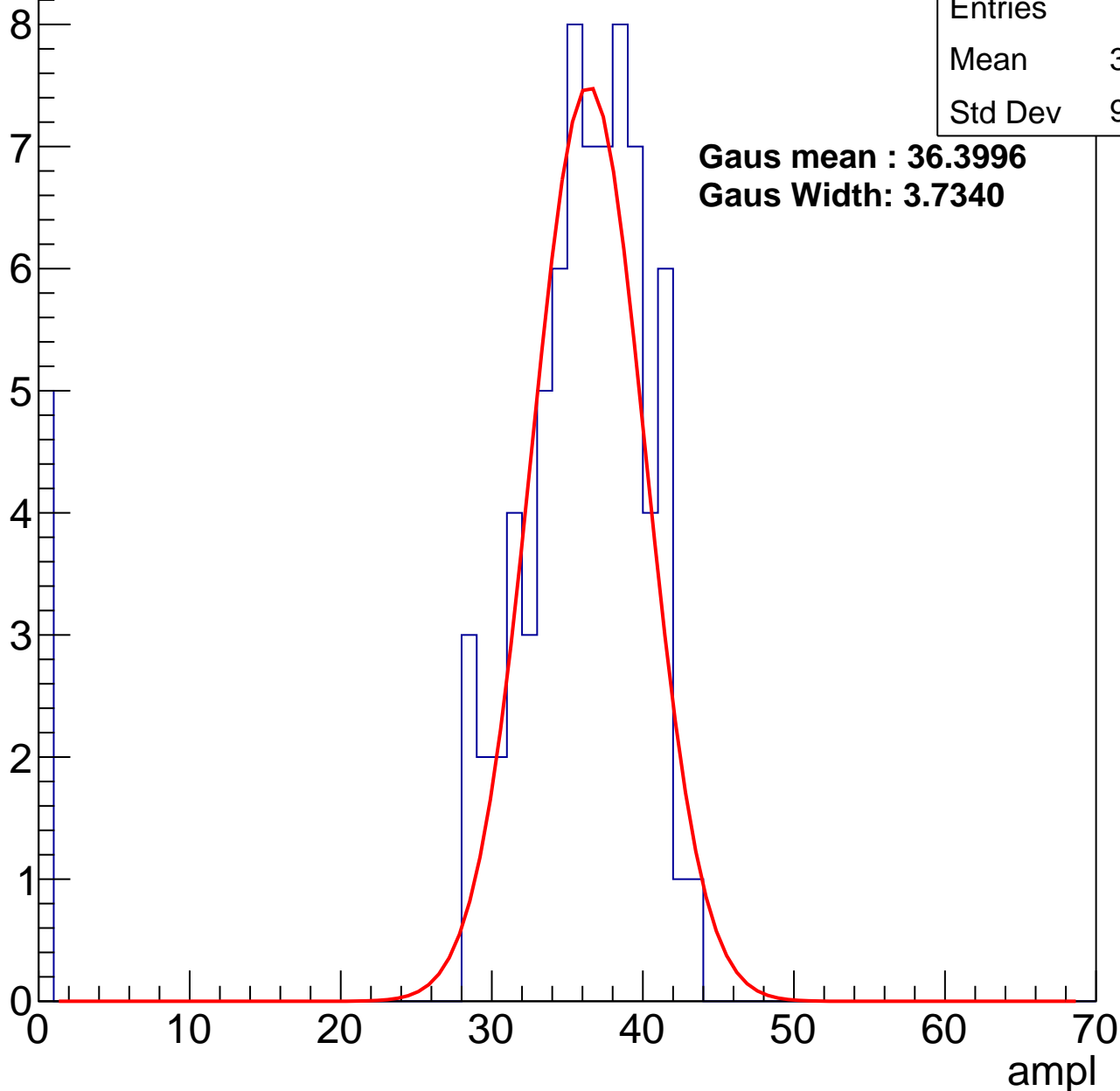
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	33.54
Std Dev	9.408

Gaus mean : 36.3996

Gaus Width: 3.7340



B1L103S, U8-ch45, adc2

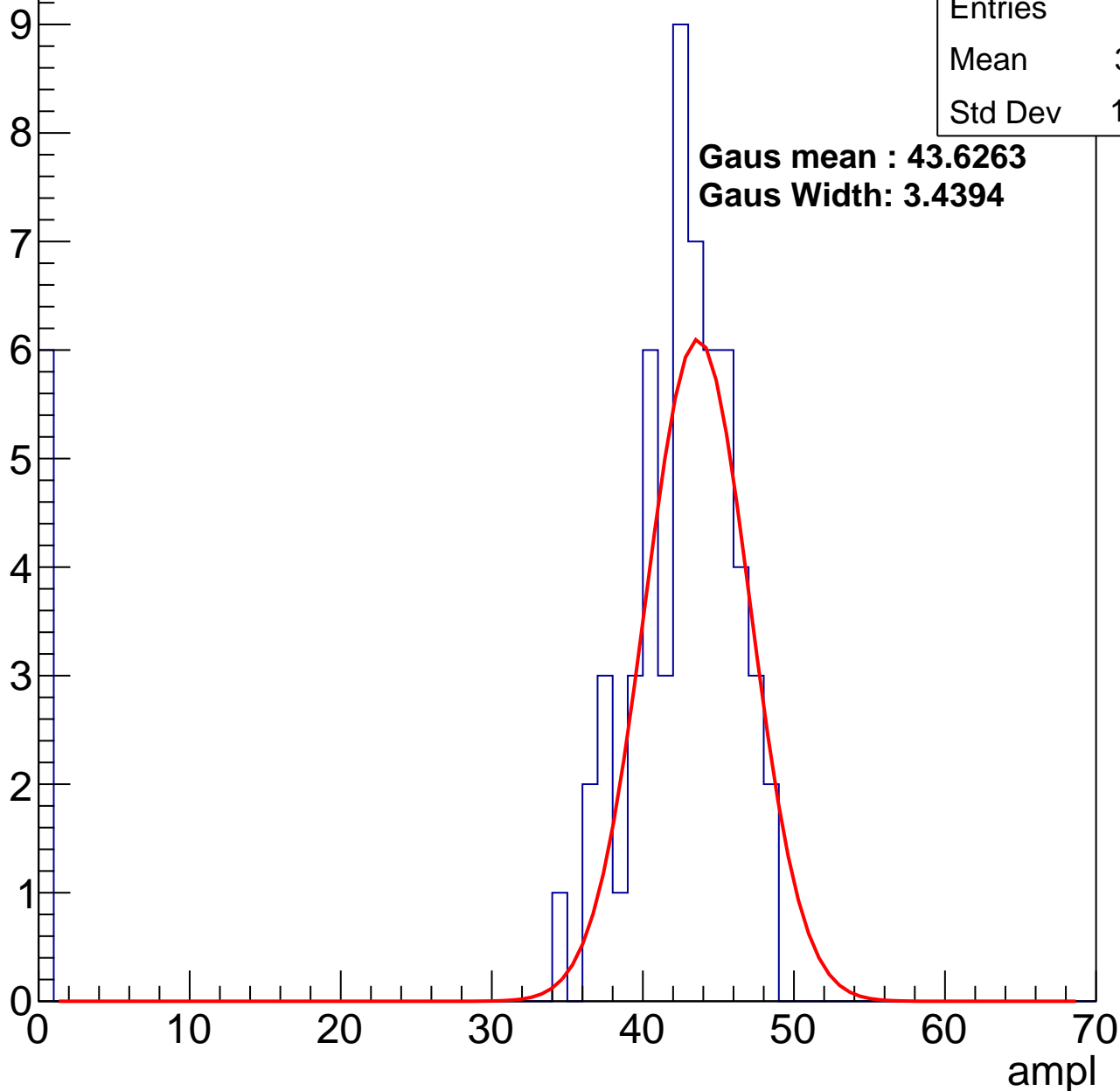
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	38.21
Std Dev	12.87

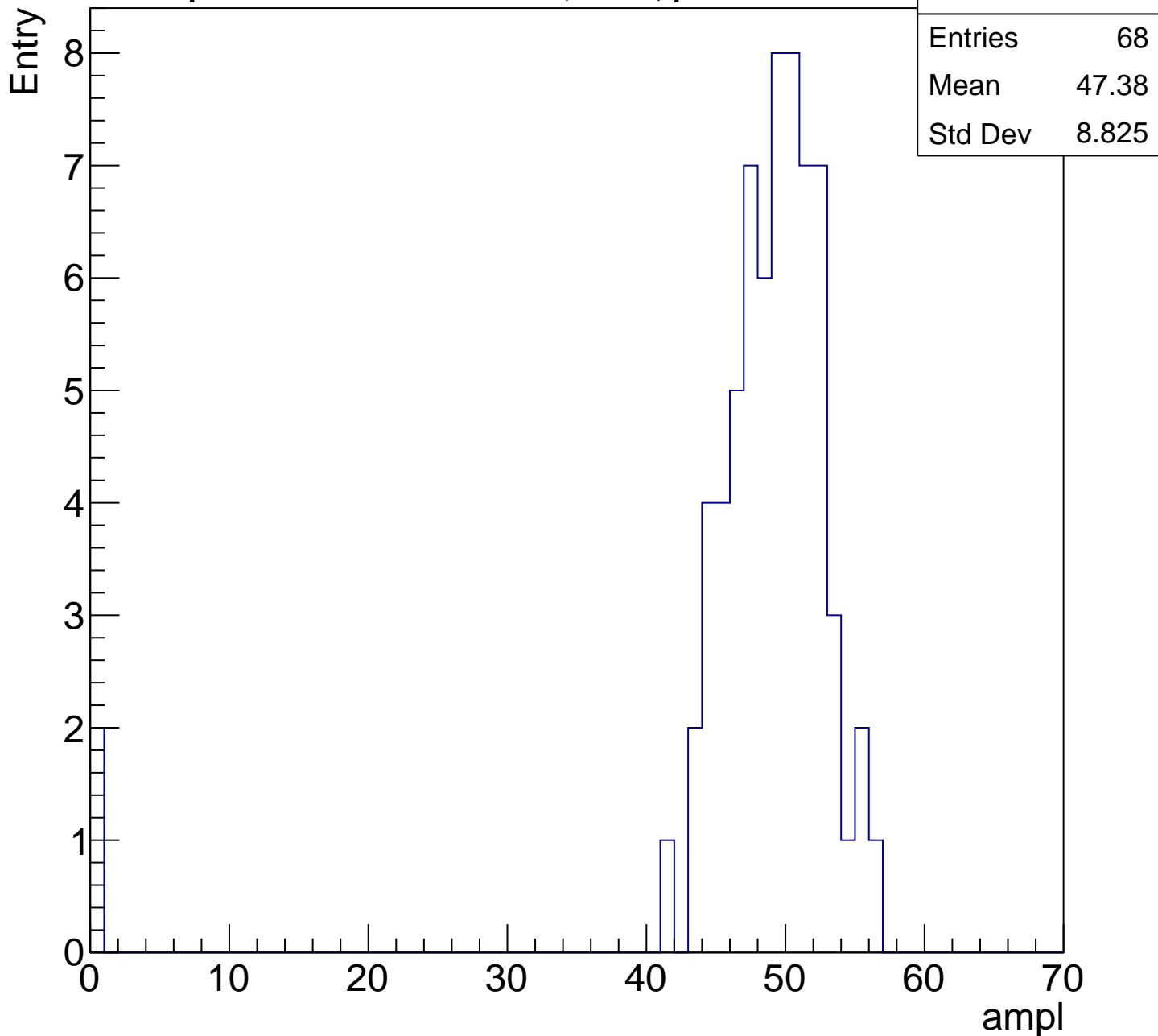
Gaus mean : 43.6263

Gaus Width: 3.4394



B1L103S, U8-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

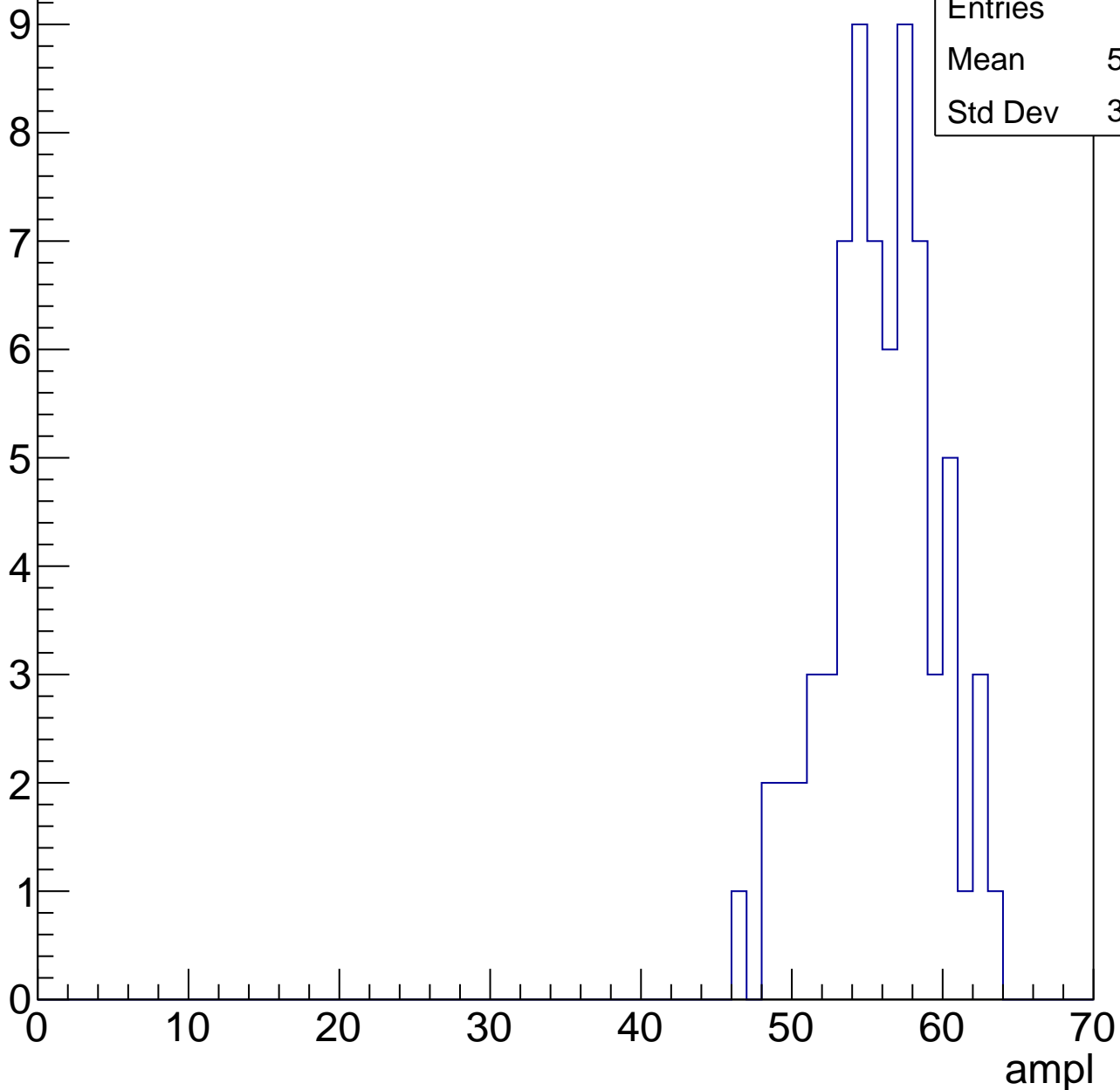


B1L103S, U8-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

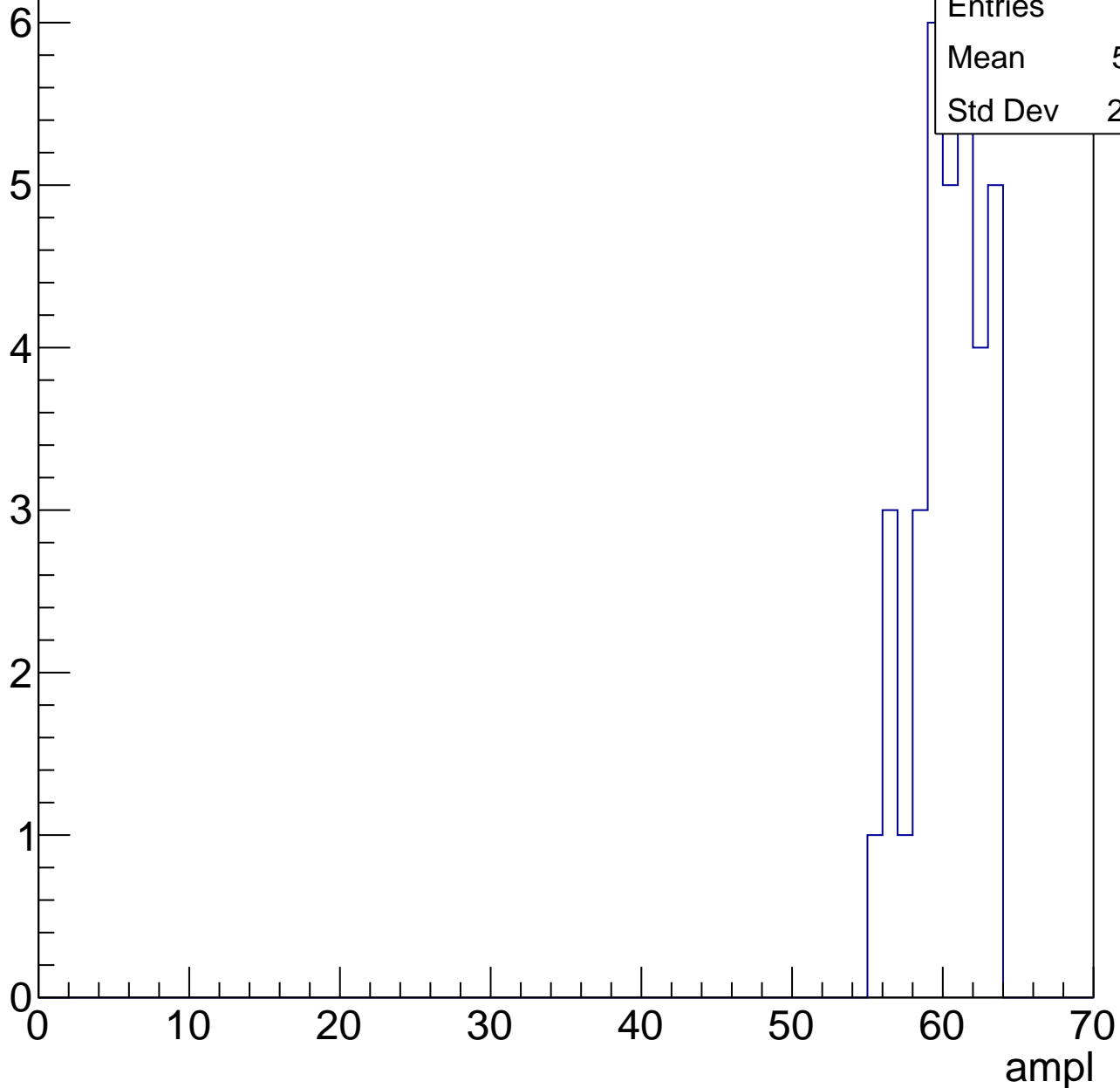
Entries	71
Mean	55.39
Std Dev	3.613



B1L103S, U8-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

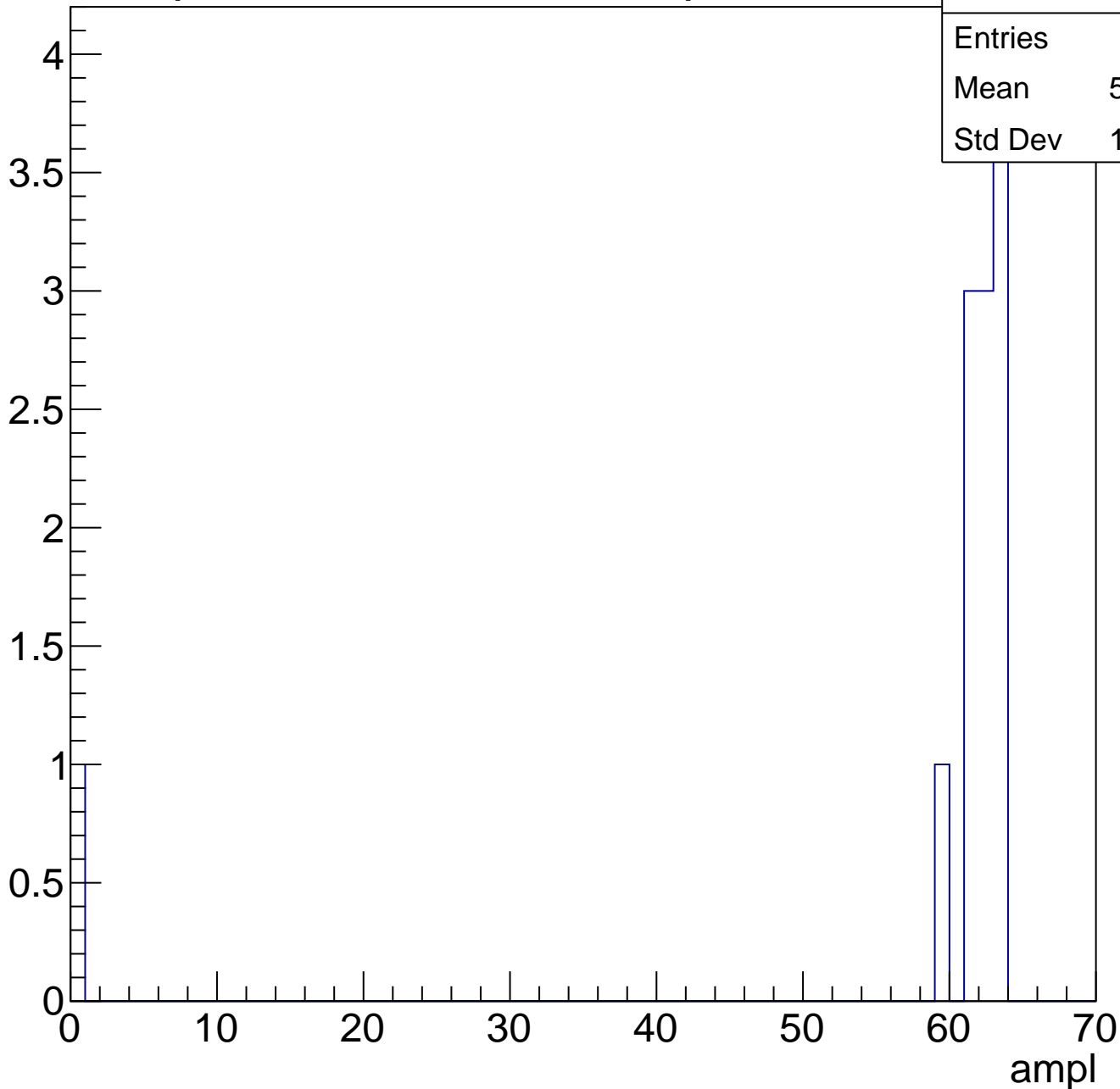


Entries	34
Mean	59.91
Std Dev	2.214

B1L103S, U8-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch46, adc0

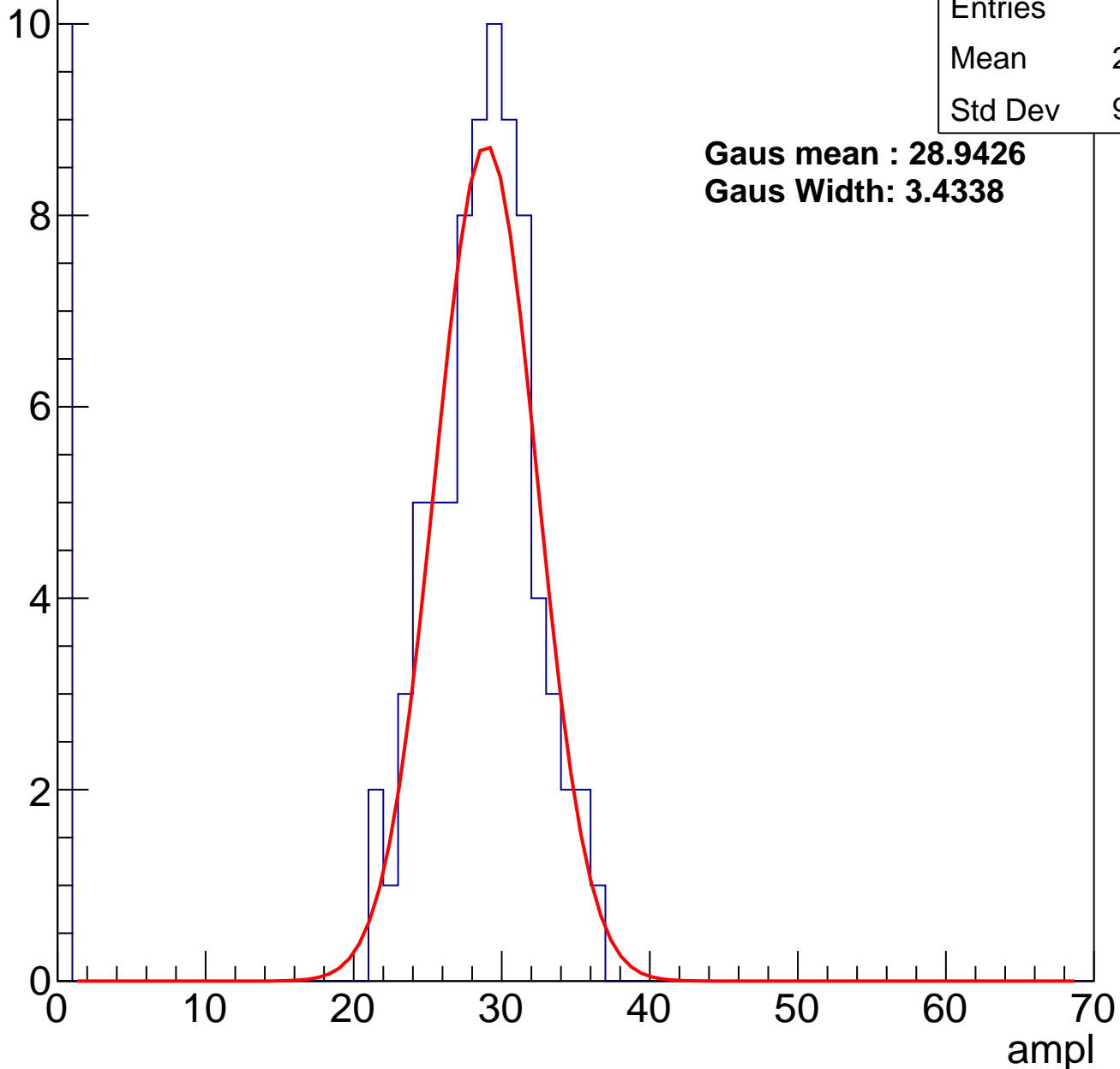
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	25.11
Std Dev	9.571

Gaus mean : 28.9426

Gaus Width: 3.4338

Entry



B1L103S, U8-ch46, adc1

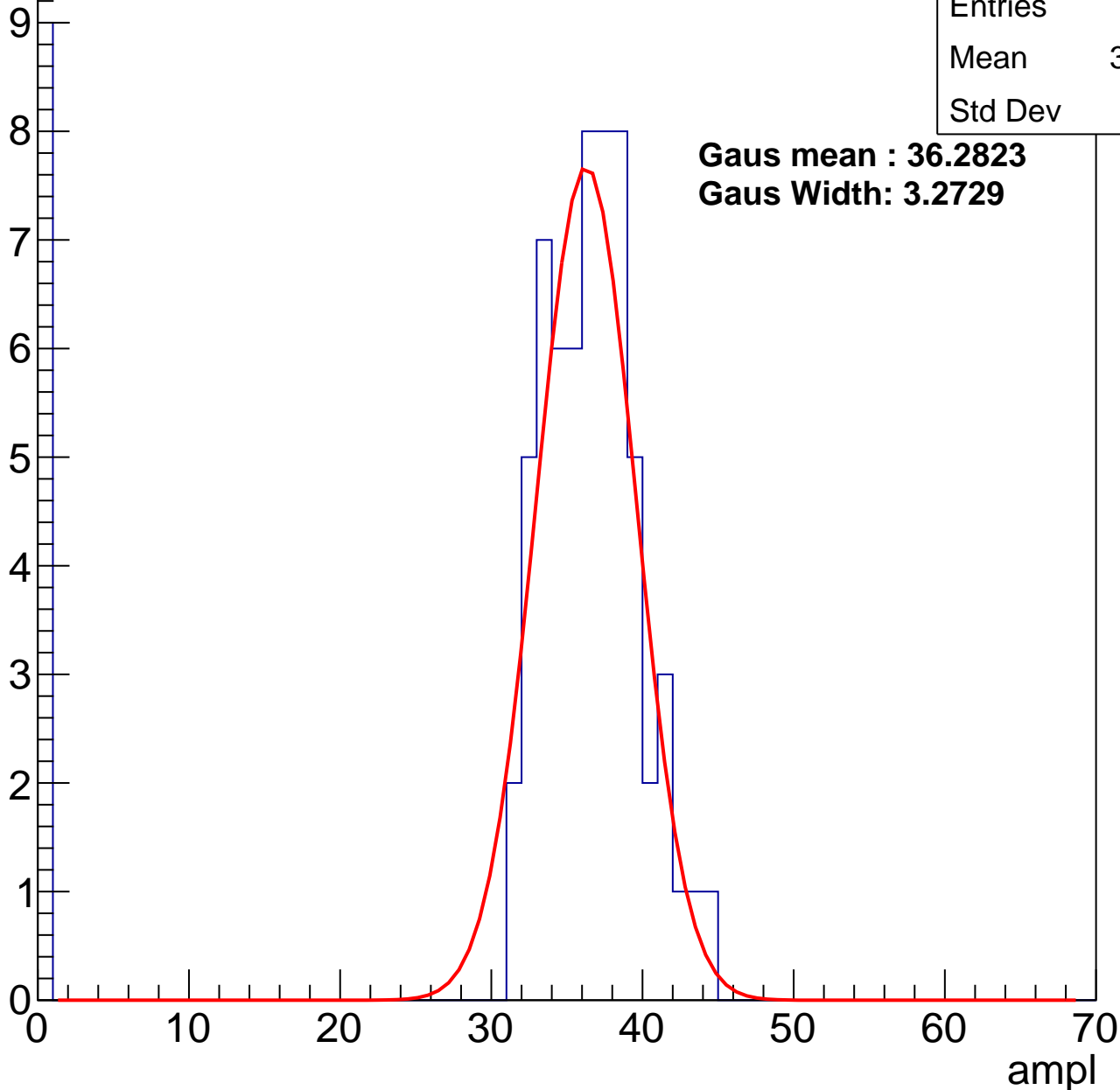
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	31.69
Std Dev	12.3

Gaus mean : 36.2823

Gaus Width: 3.2729



B1L103S, U8-ch46, adc2

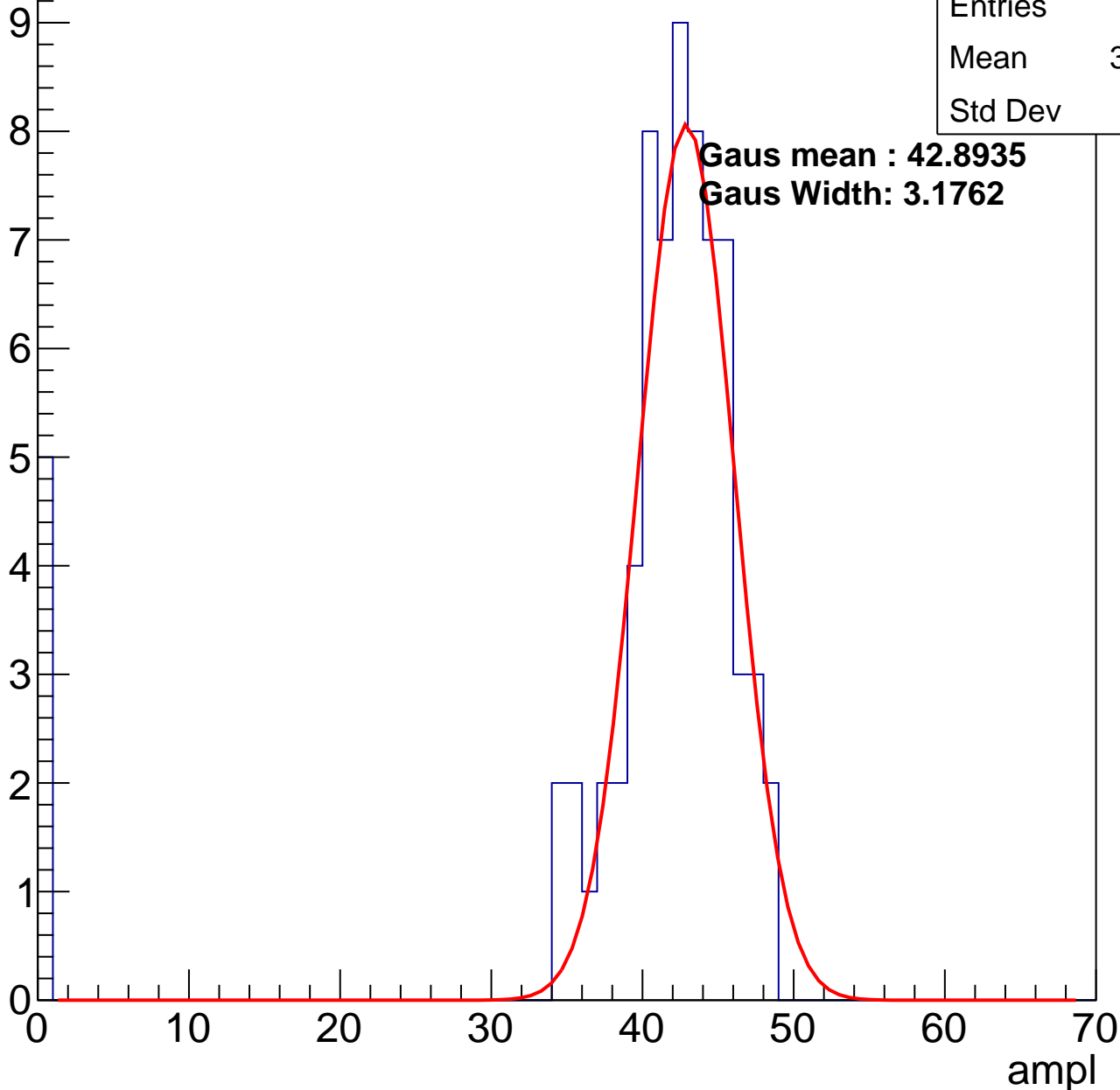
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	38.99
Std Dev	11.1

Gaus mean : 42.8935

Gaus Width: 3.1762

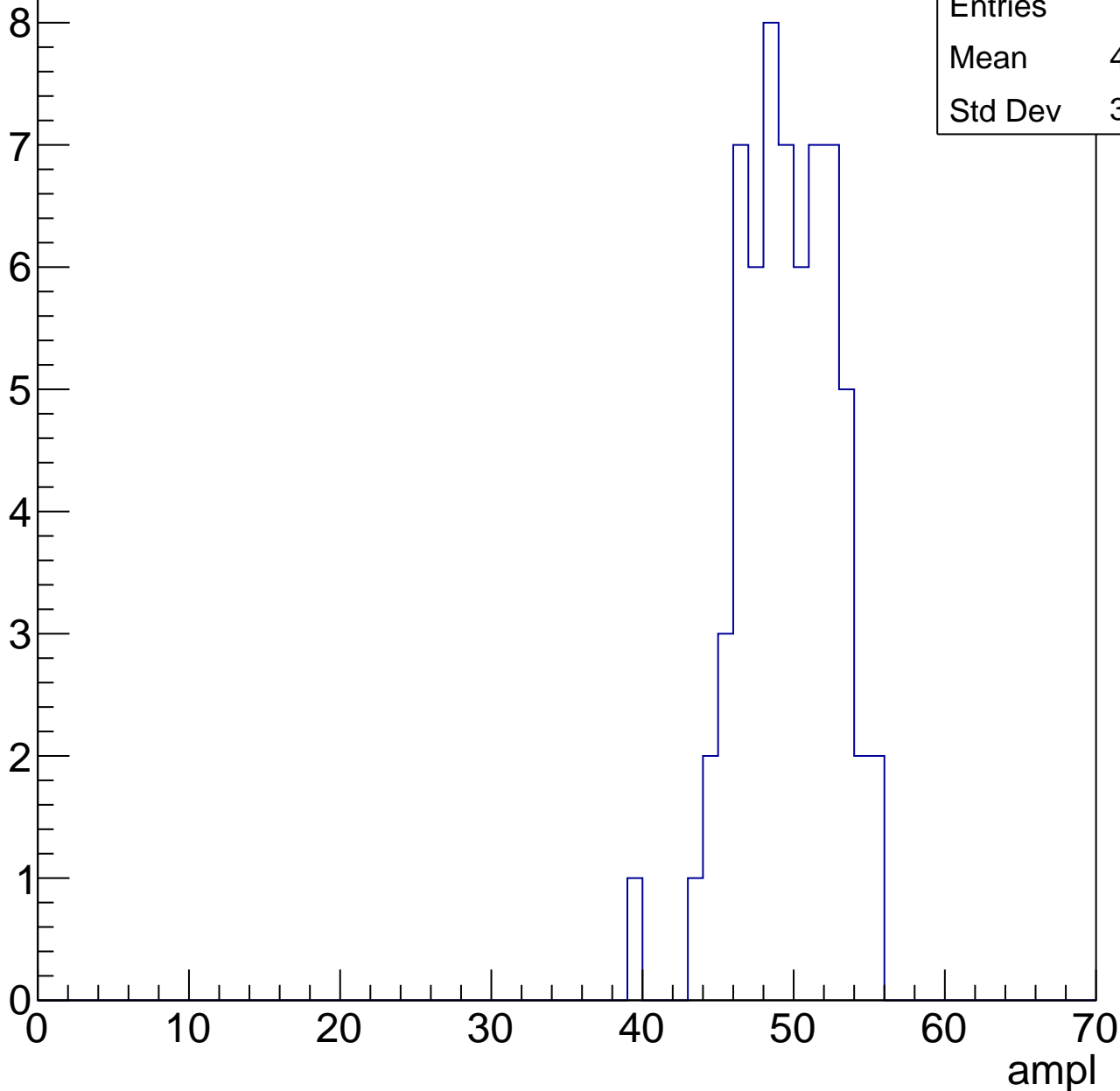


B1L103S, U8-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.06
Std Dev	3.132

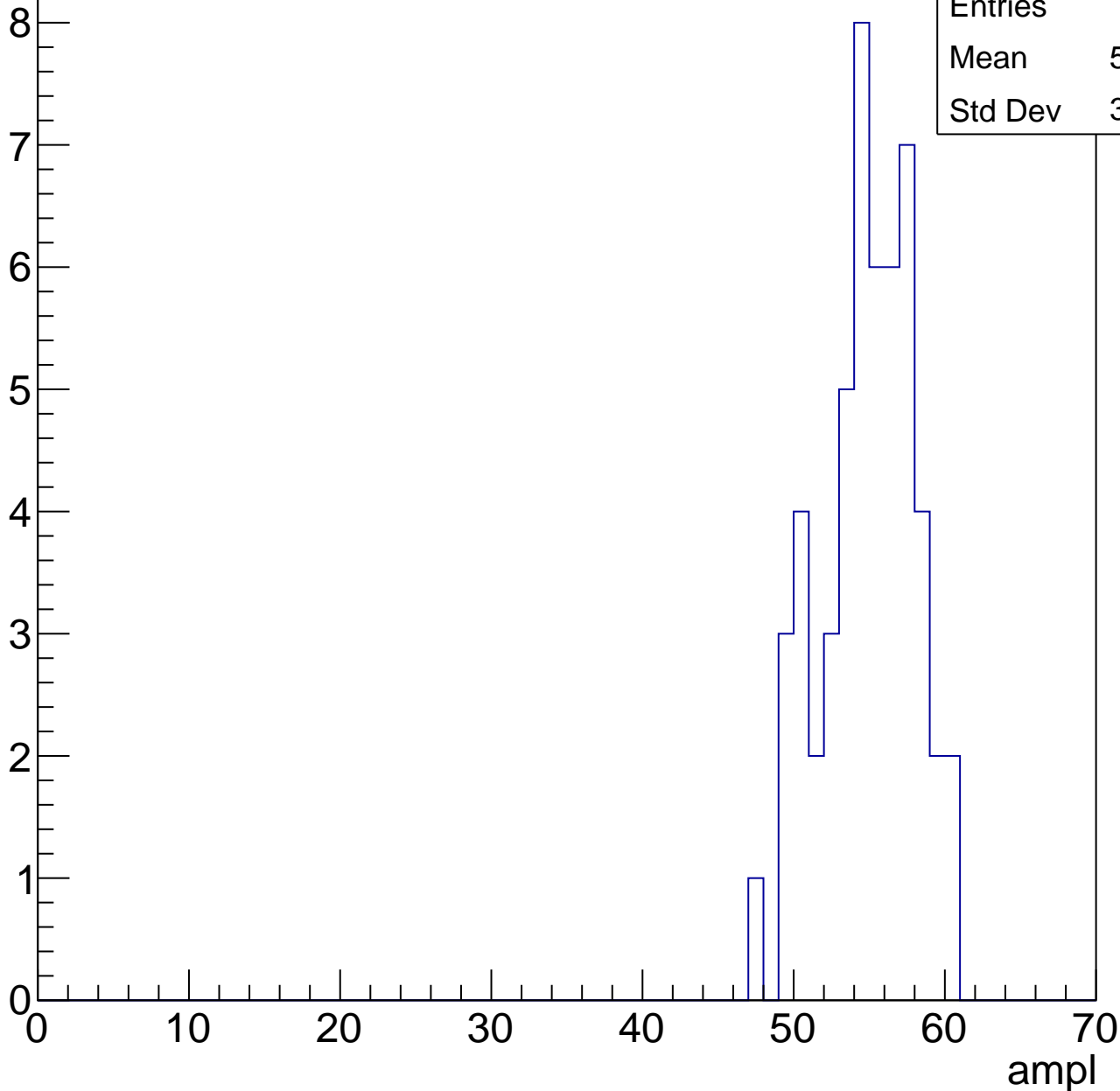


B1L103S, U8-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	54.42
Std Dev	3.049

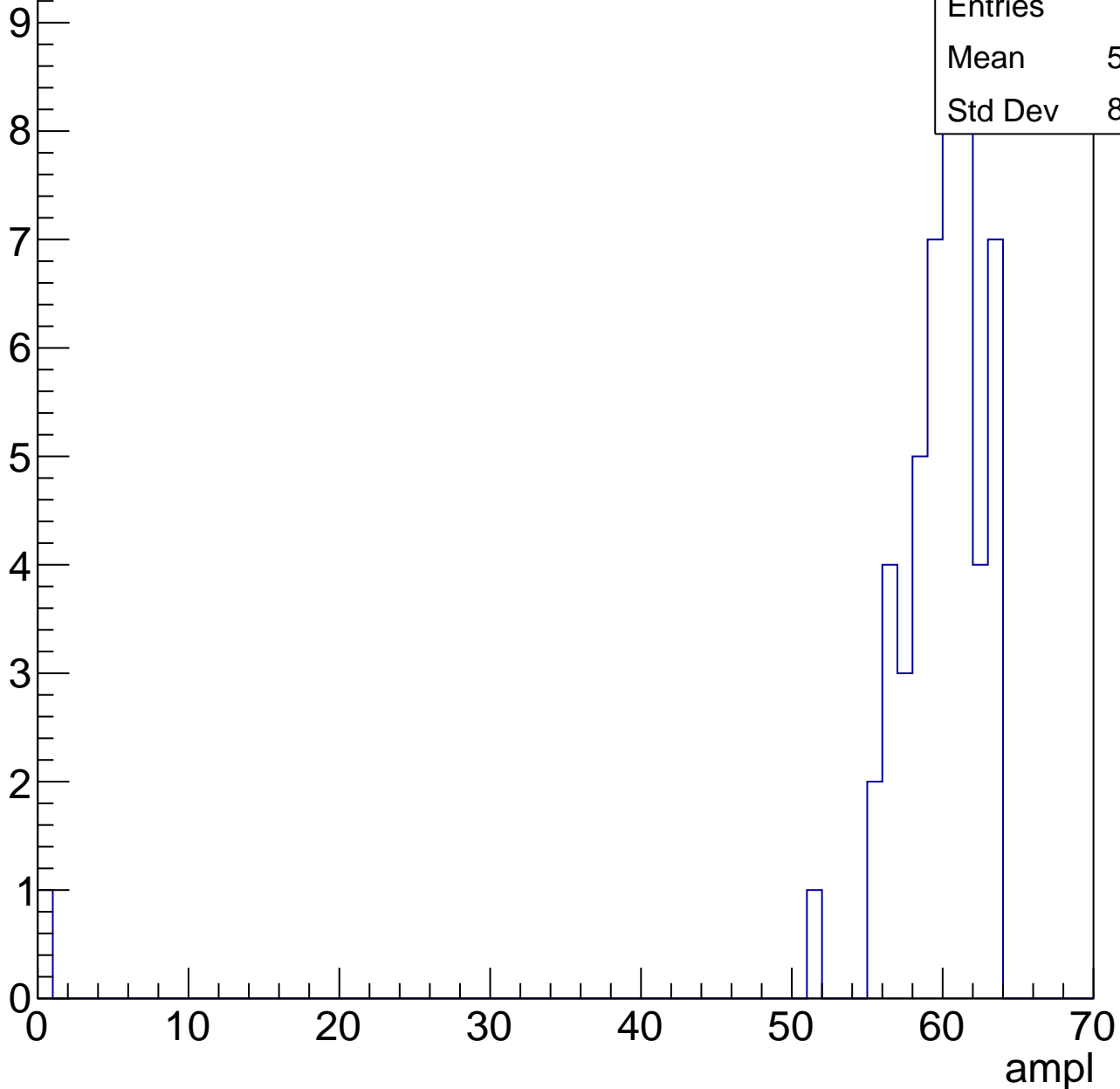


B1L103S, U8-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.37
Std Dev	8.634

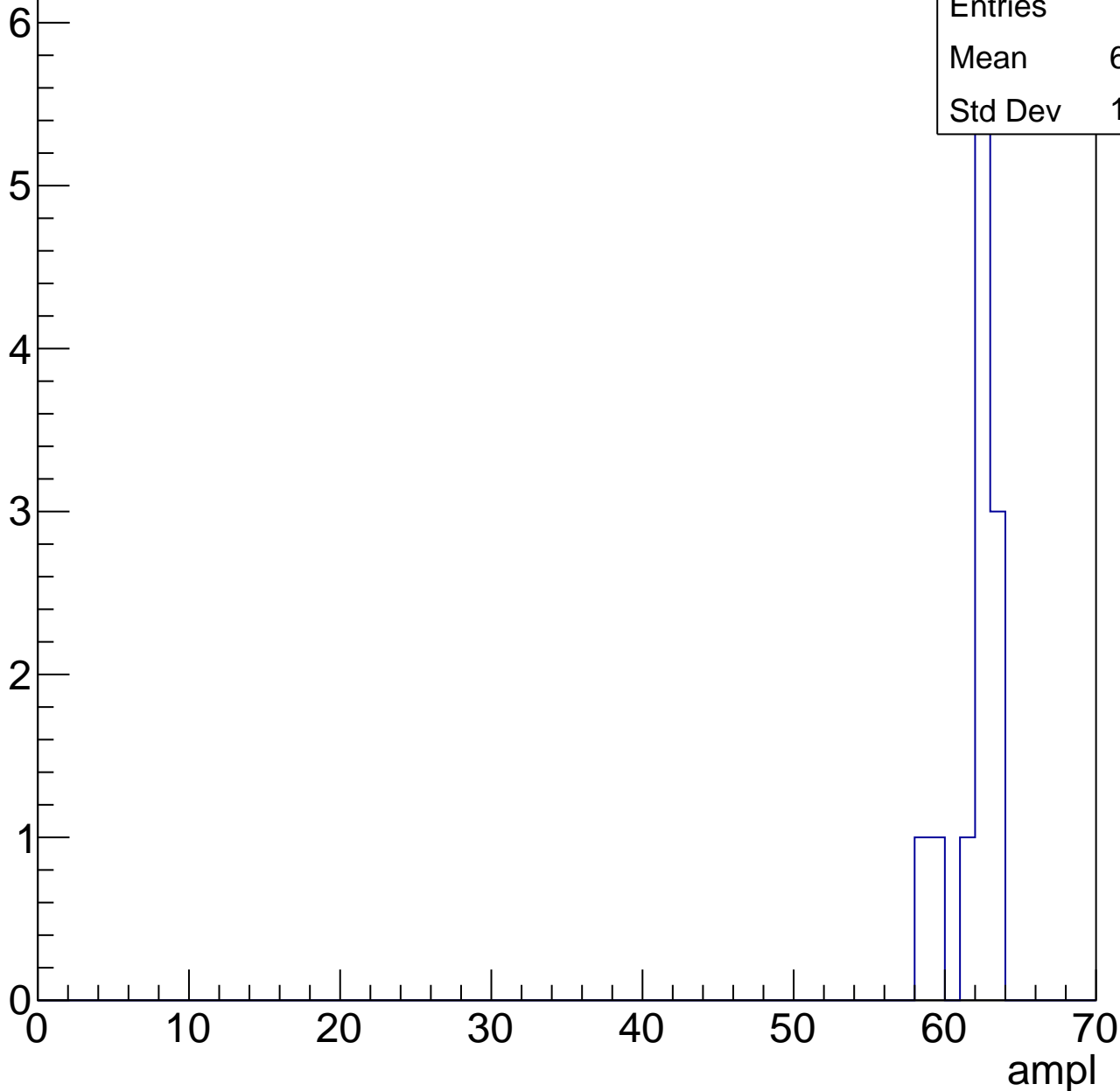


B1L103S, U8-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.498



B1L103S, U8-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch47, adc0

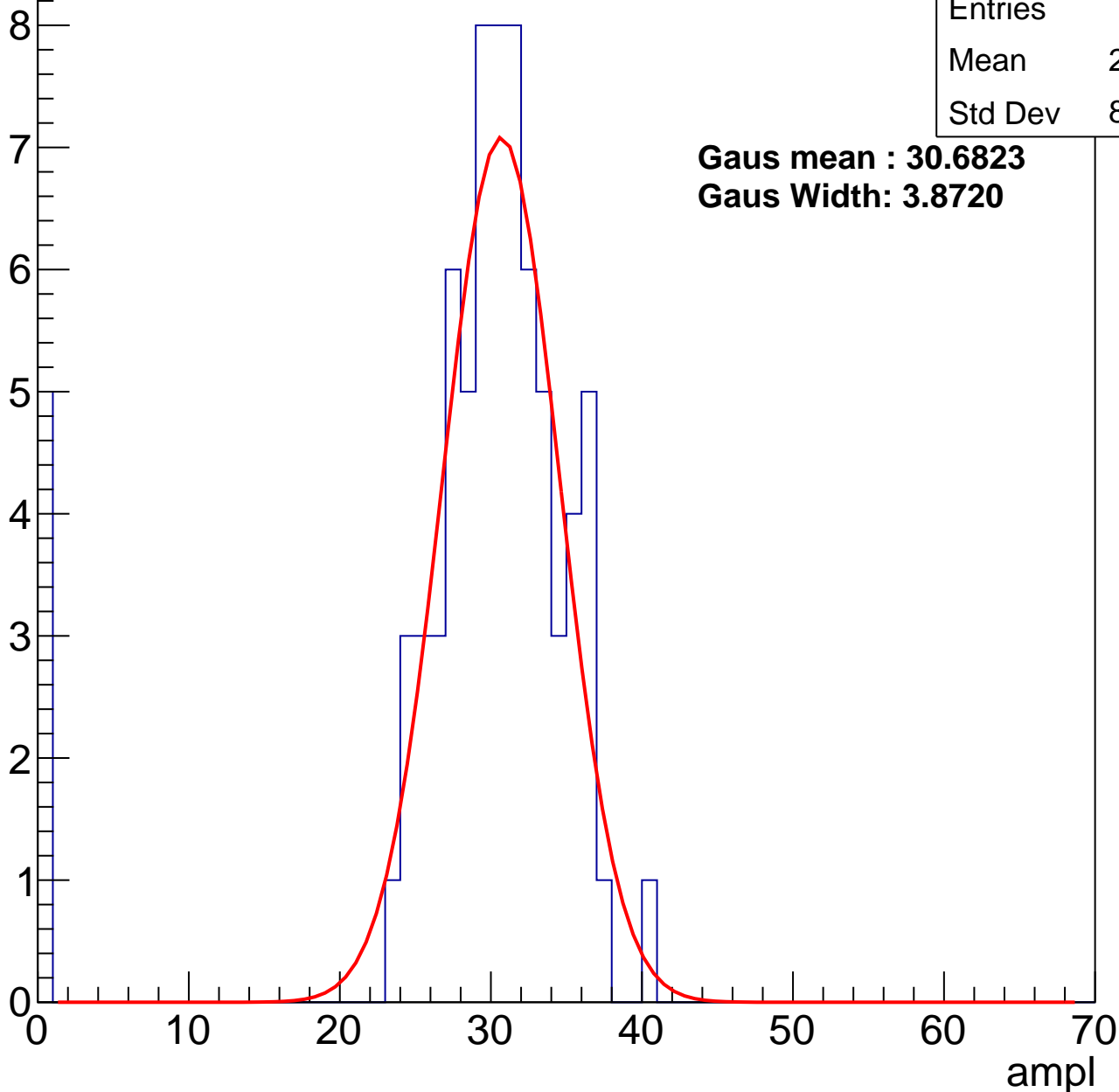
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	28.35
Std Dev	8.335

Gaus mean : 30.6823

Gaus Width: 3.8720



B1L103S, U8-ch47, adc1

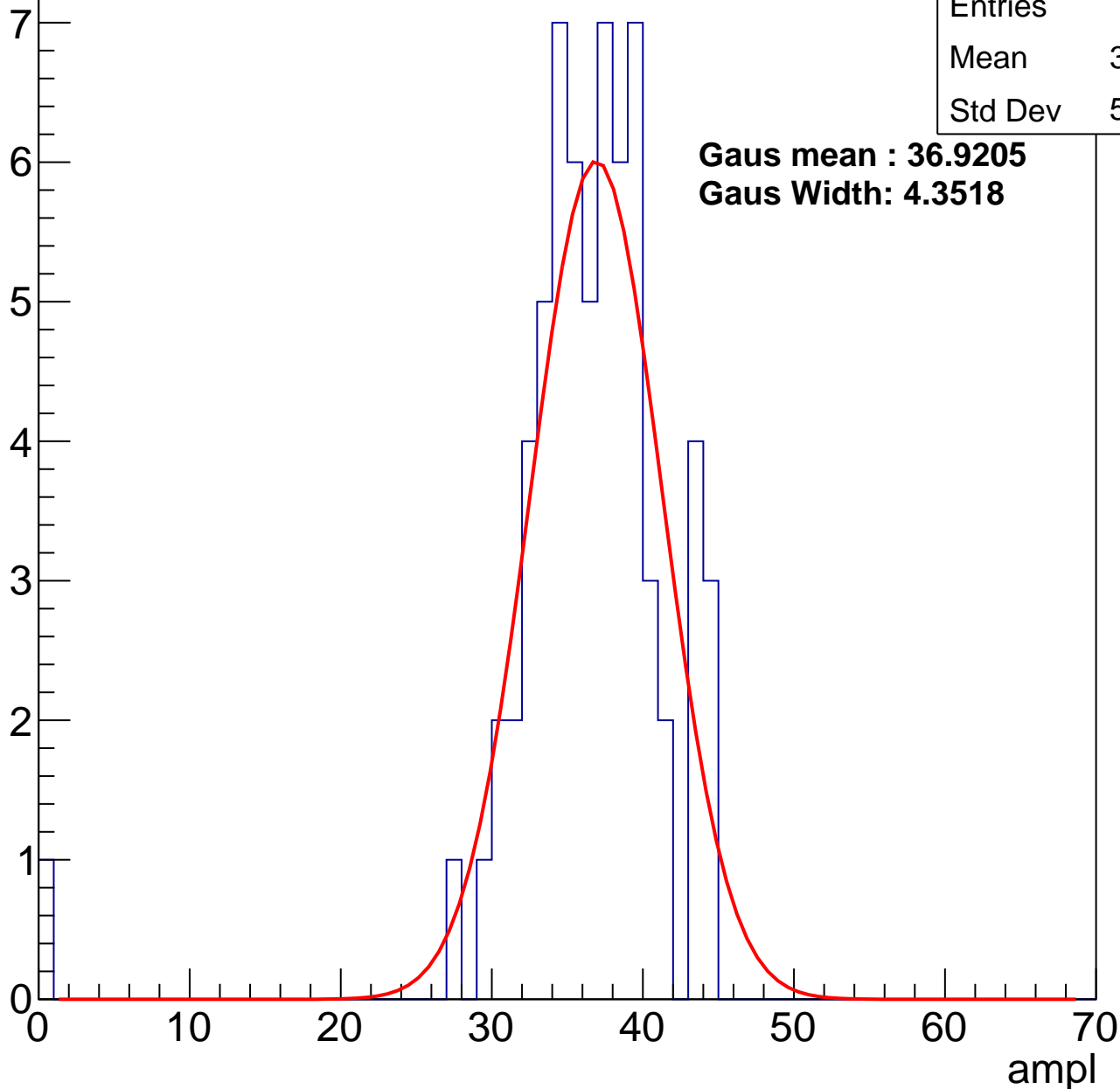
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	35.83
Std Dev	5.864

Gaus mean : 36.9205

Gaus Width: 4.3518



B1L103S, U8-ch47, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	38.41
Std Dev	14.15

Gaus mean : 43.4318

Gaus Width: 3.9426

Entry

10

8

6

4

2

0

0

10

20

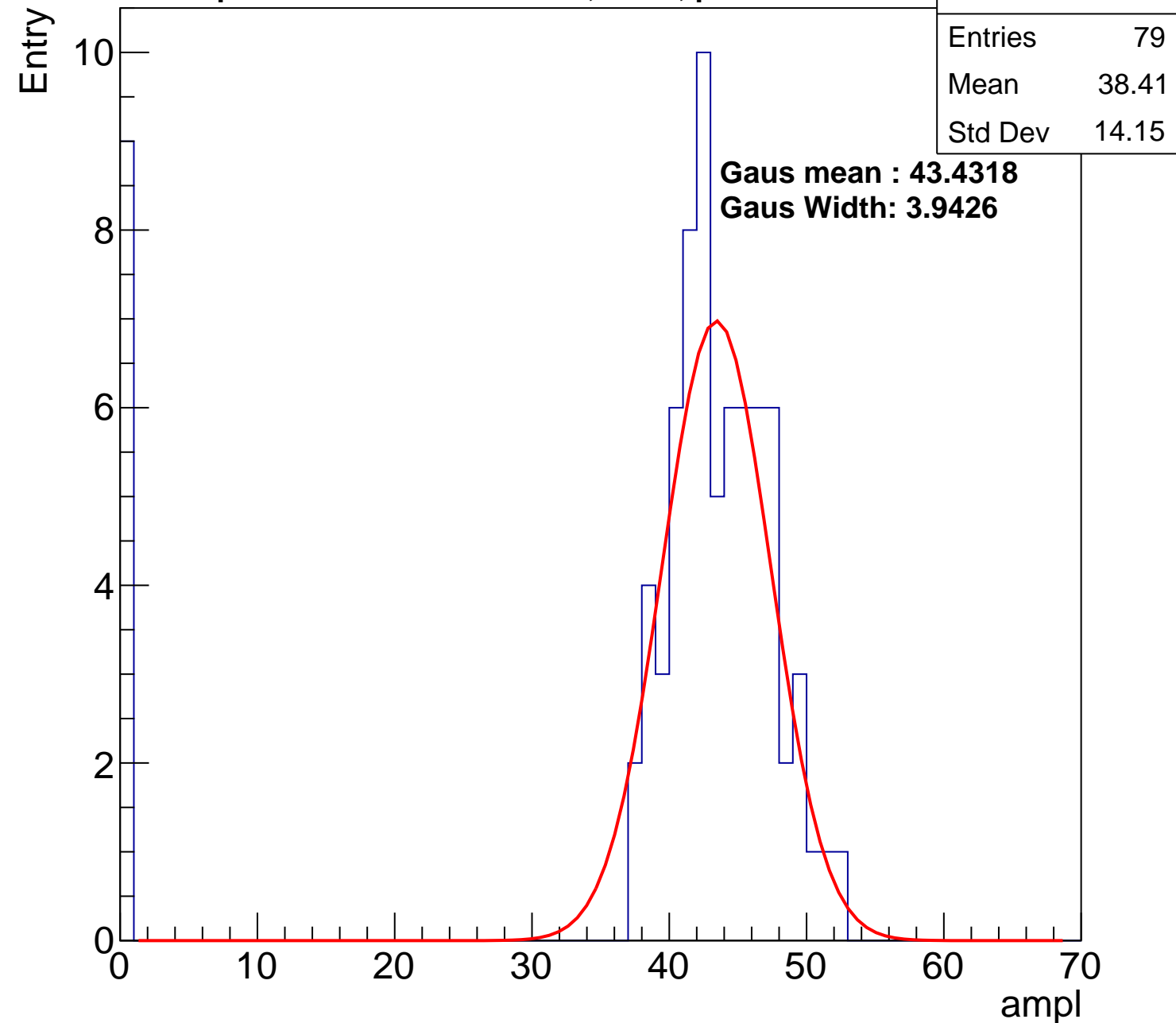
30

40

50

60

ampl

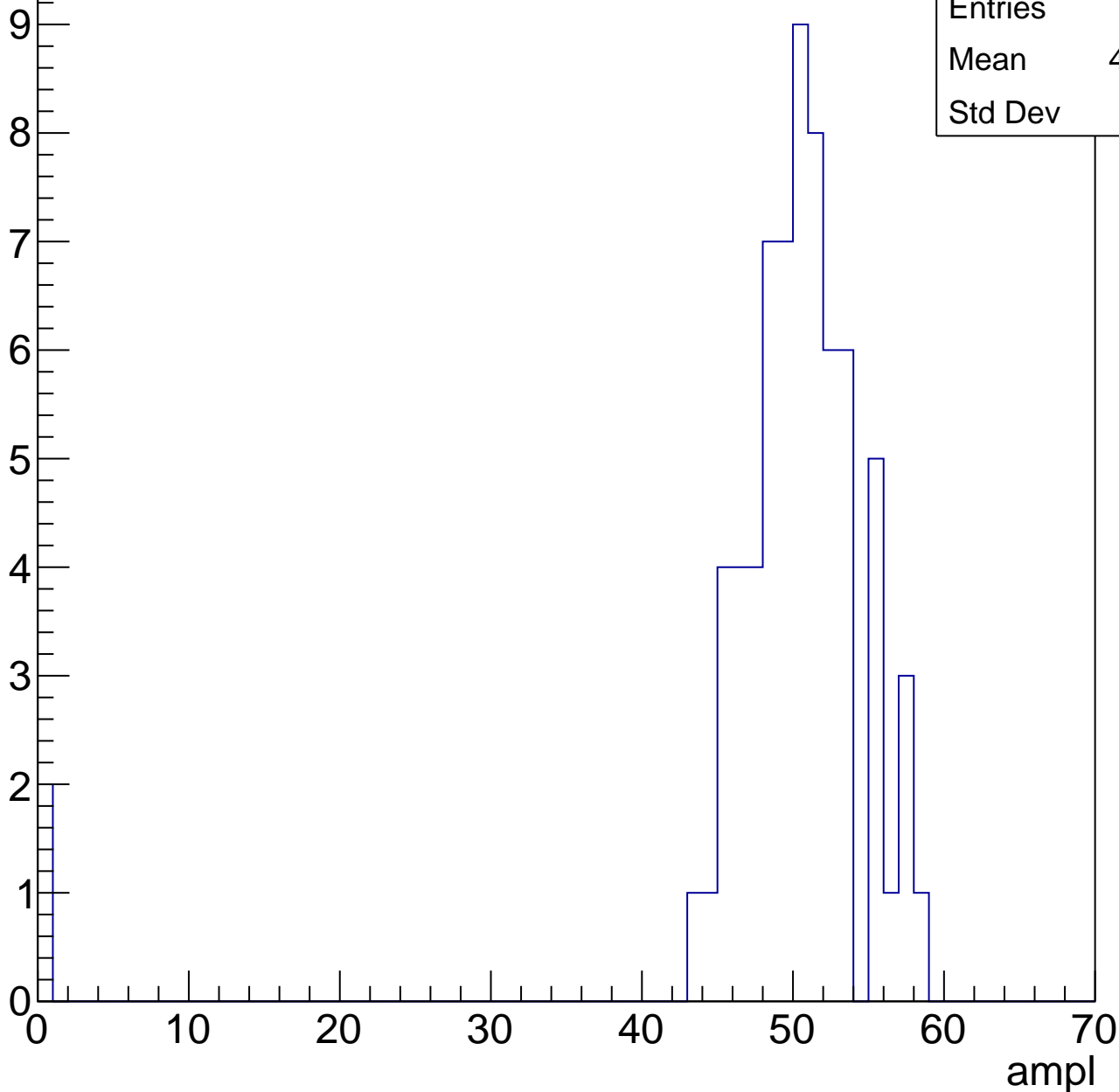


B1L103S, U8-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	48.78
Std Dev	9.07



B1L103S, U8-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

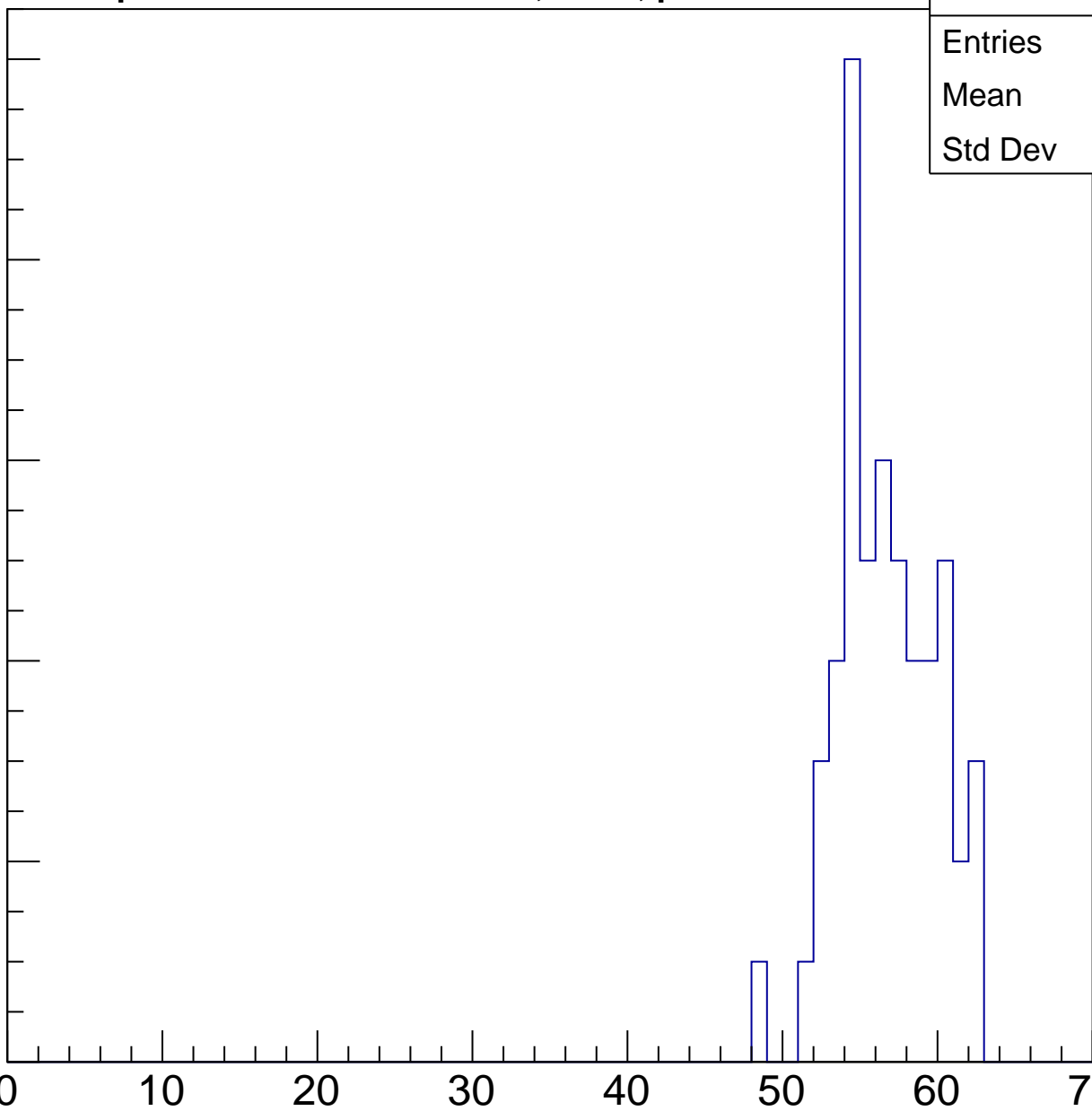
Entries	53
Mean	56.21
Std Dev	3.104

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

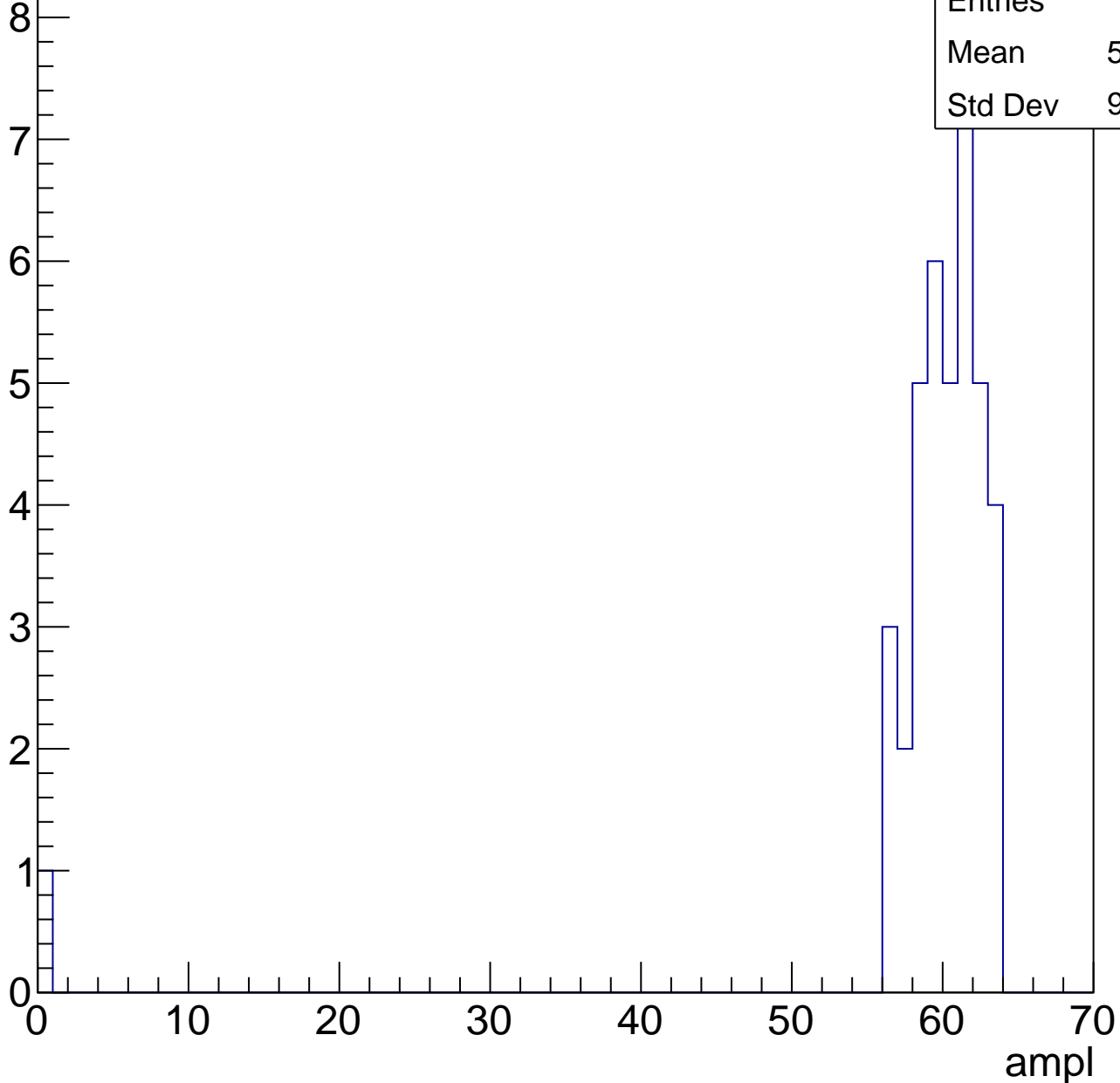


B1L103S, U8-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	58.36
Std Dev	9.675

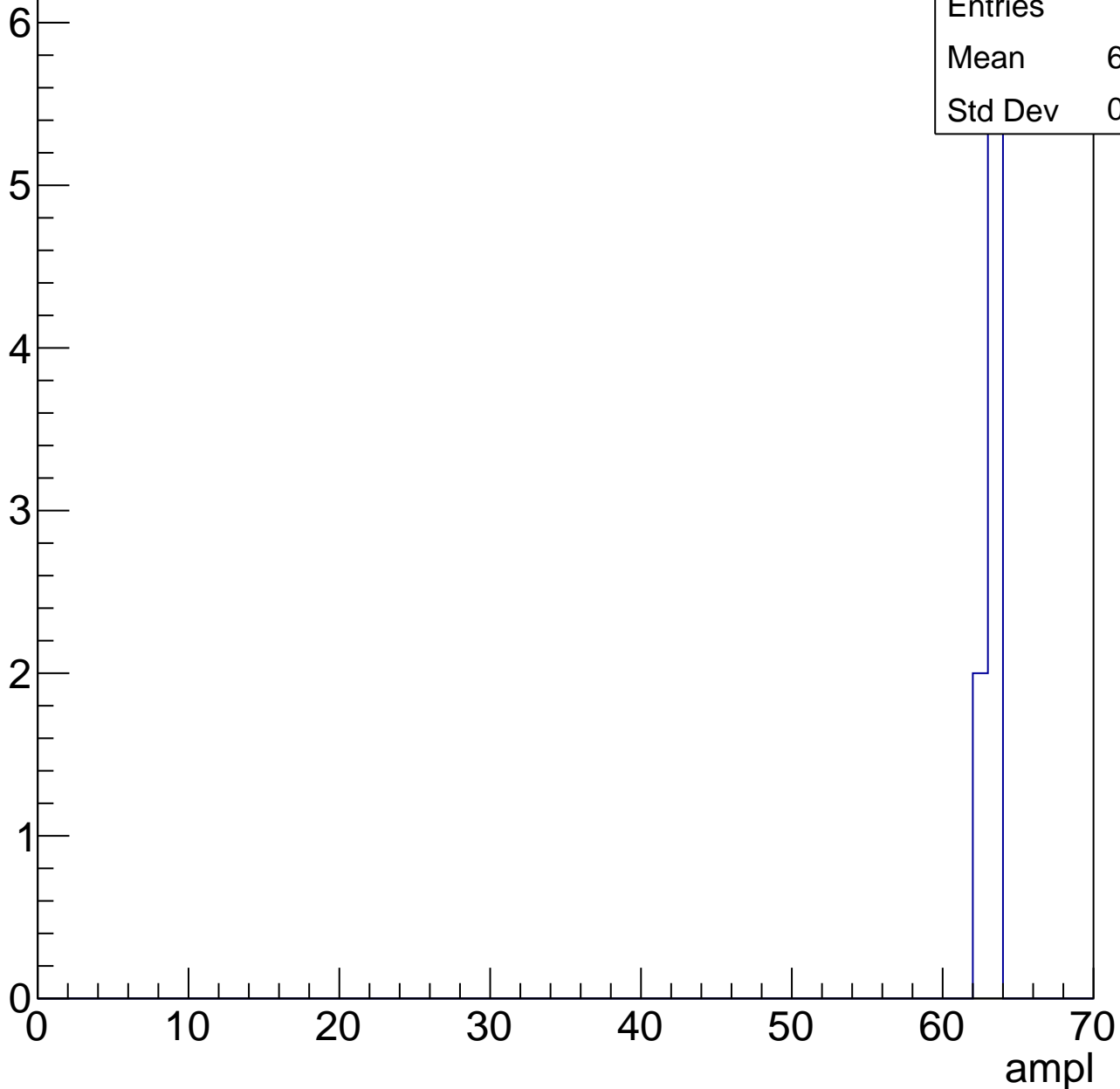


B1L103S, U8-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	8
Mean	62.75
Std Dev	0.433



B1L103S, U8-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch48, adc0

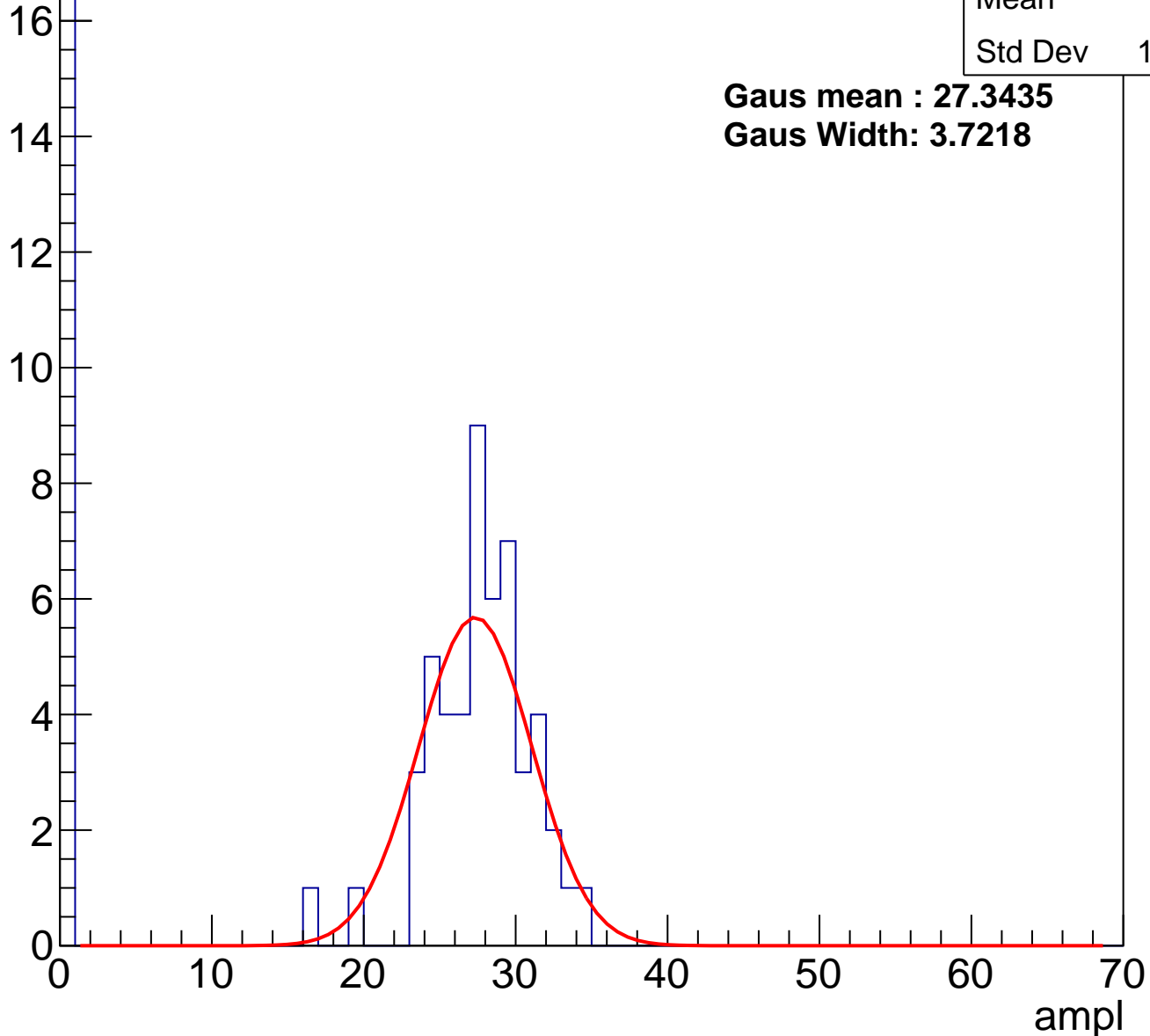
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	20.4
Std Dev	12.12

Gaus mean : 27.3435

Gaus Width: 3.7218

Entry



B1L103S, U8-ch48, adc1

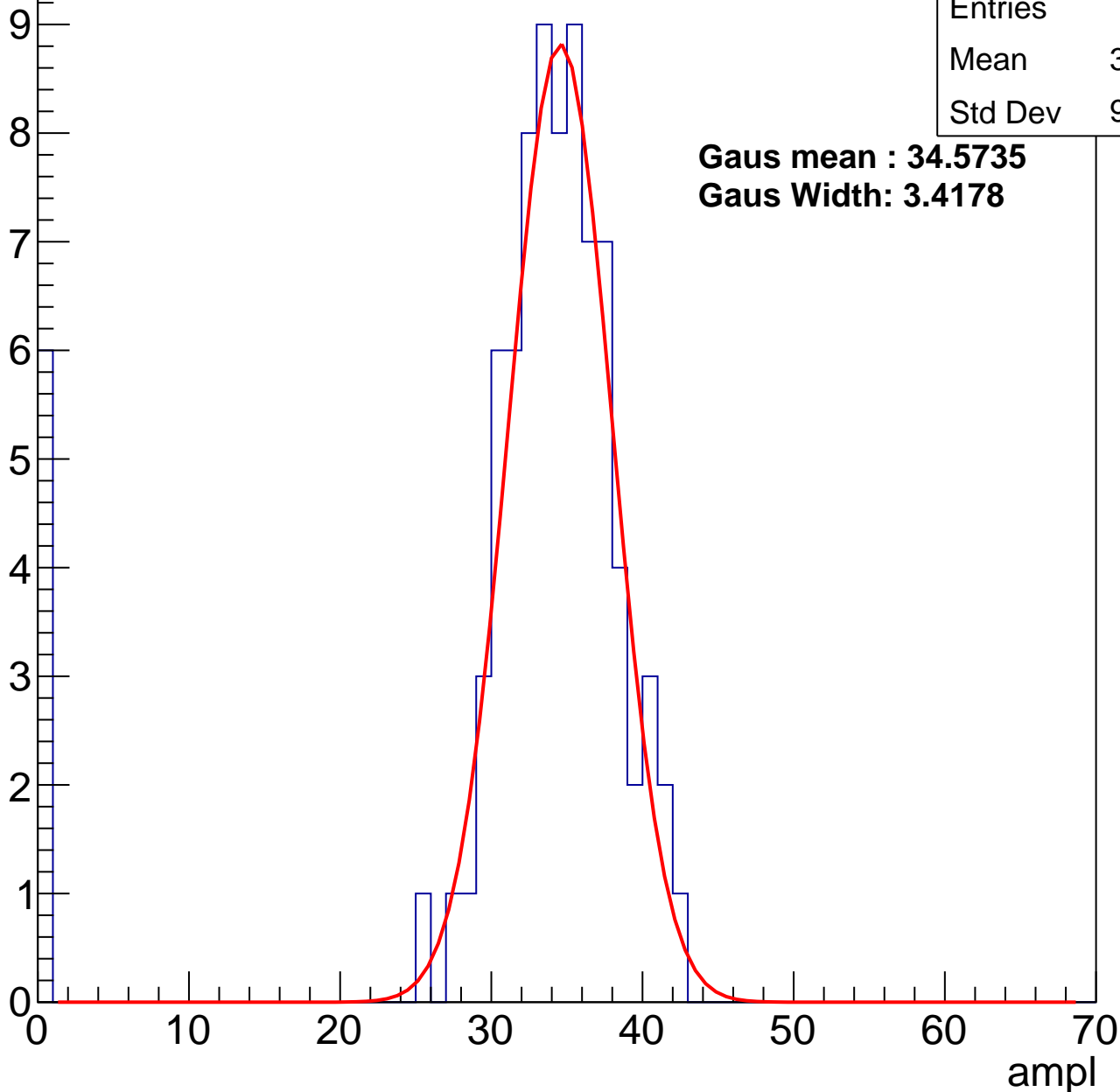
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	31.64
Std Dev	9.377

Gaus mean : 34.5735

Gaus Width: 3.4178

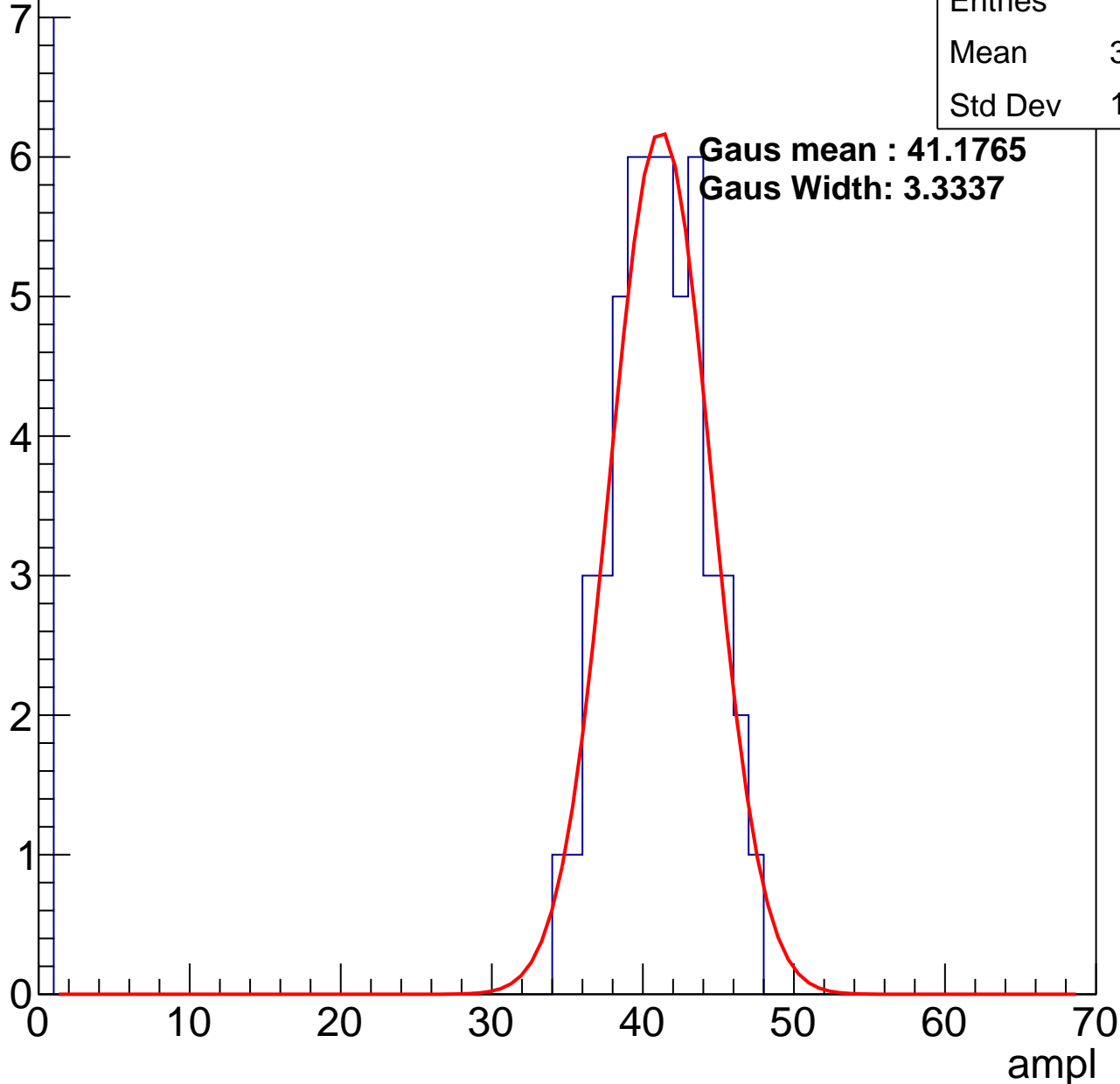


B1L103S, U8-ch48, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	35.72
Std Dev	13.54

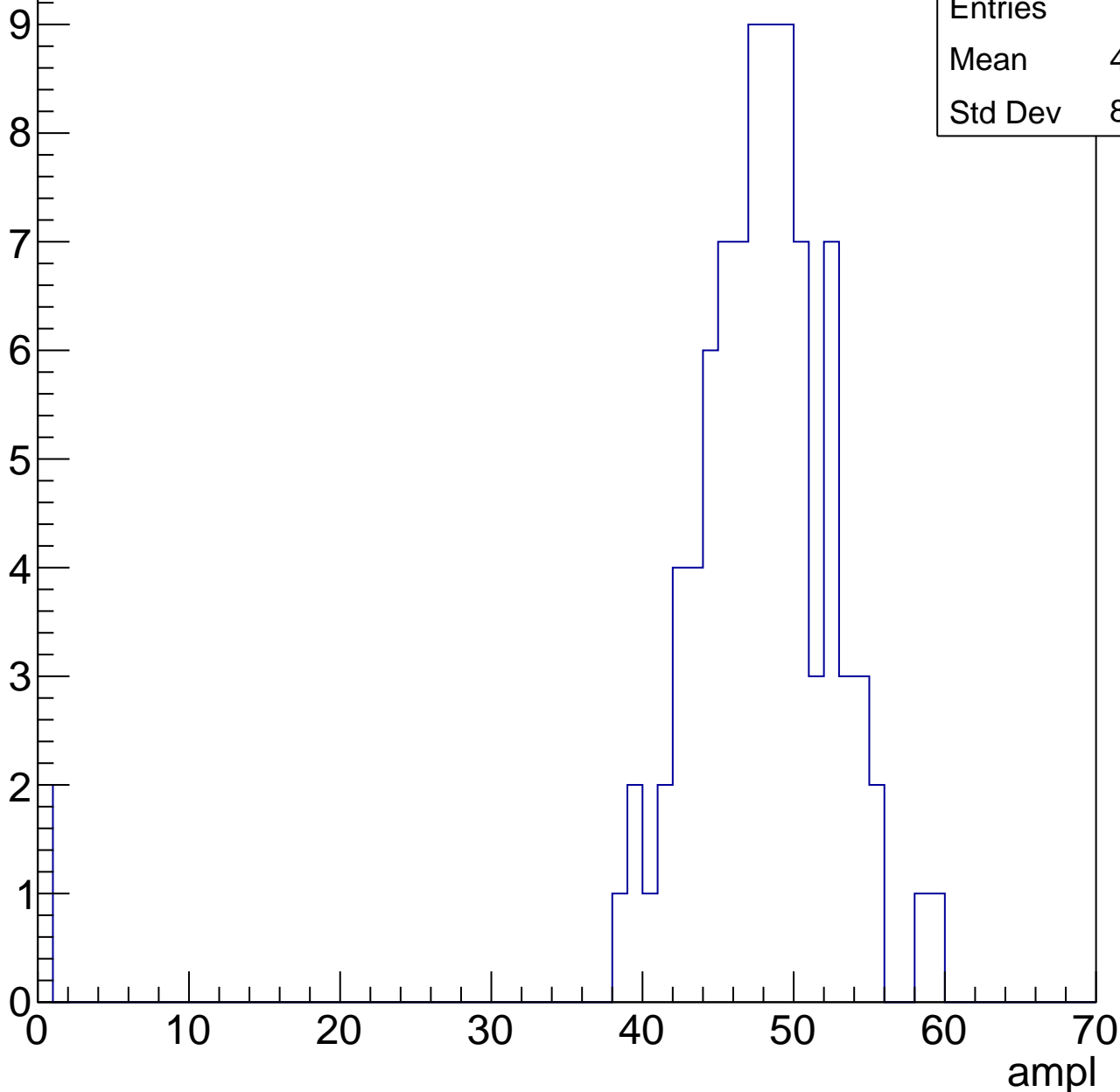


B1L103S, U8-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	90
Mean	46.56
Std Dev	8.139

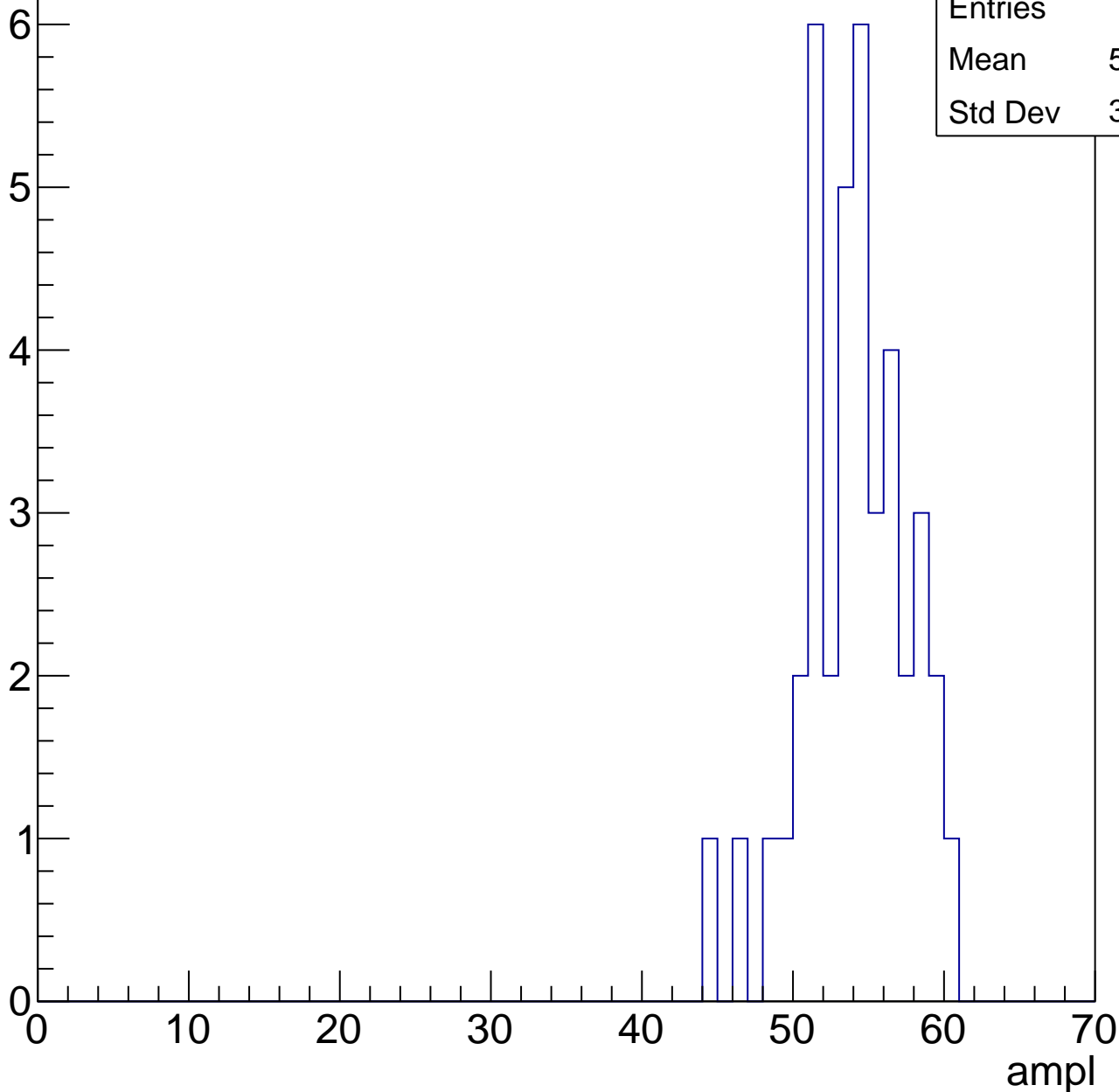


B1L103S, U8-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	53.52
Std Dev	3.486

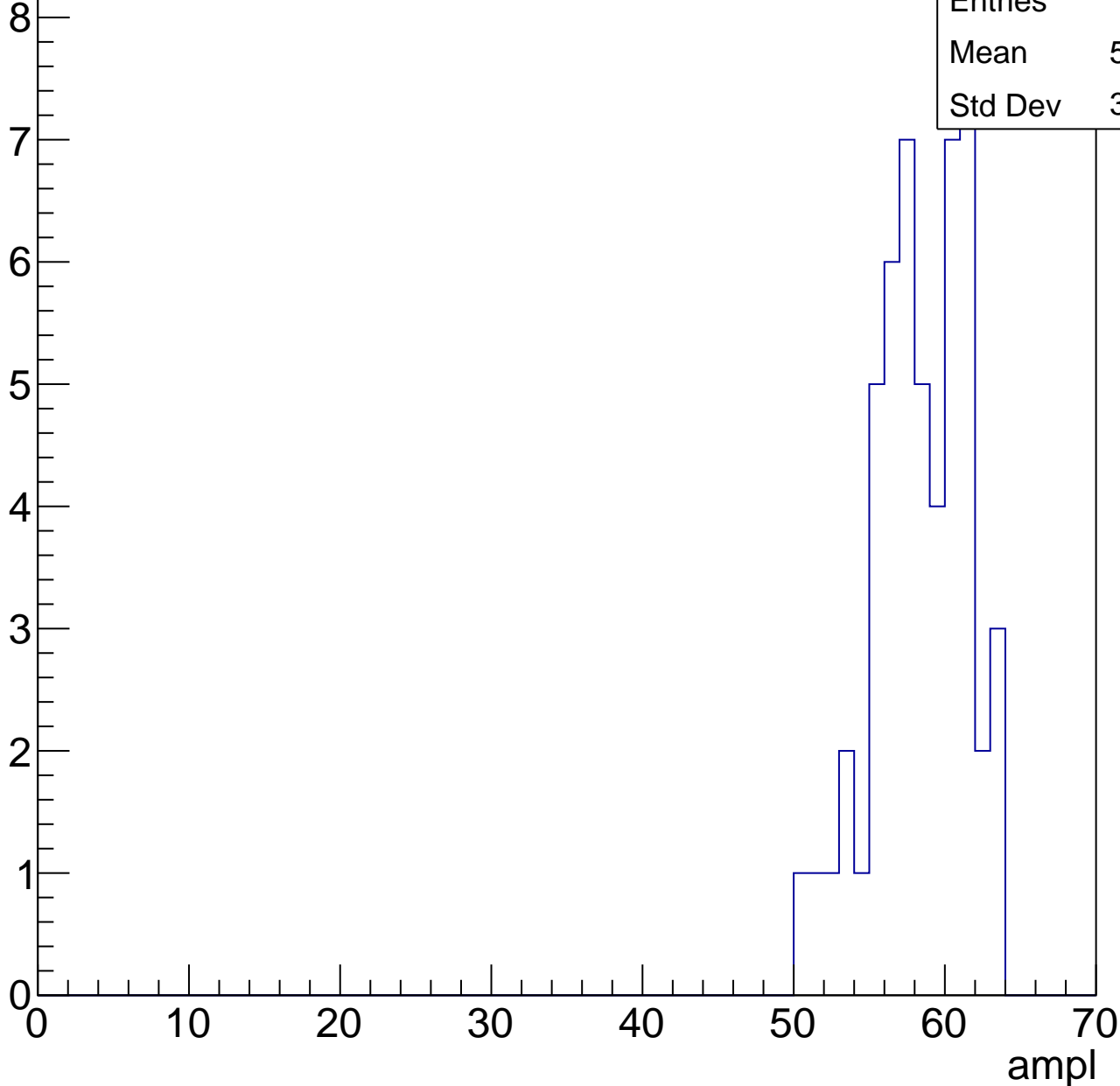


B1L103S, U8-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.92
Std Dev	3.077

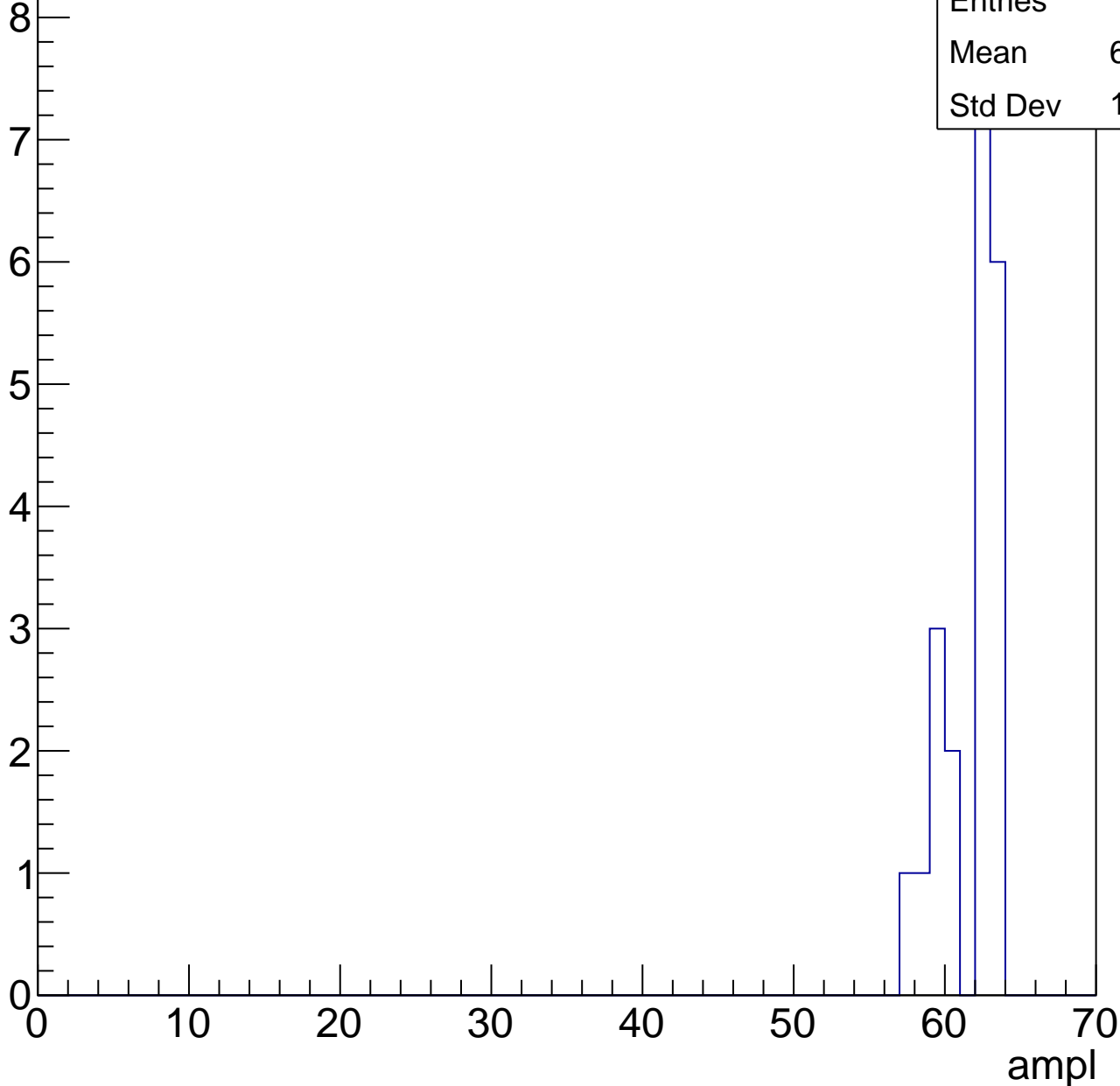


B1L103S, U8-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

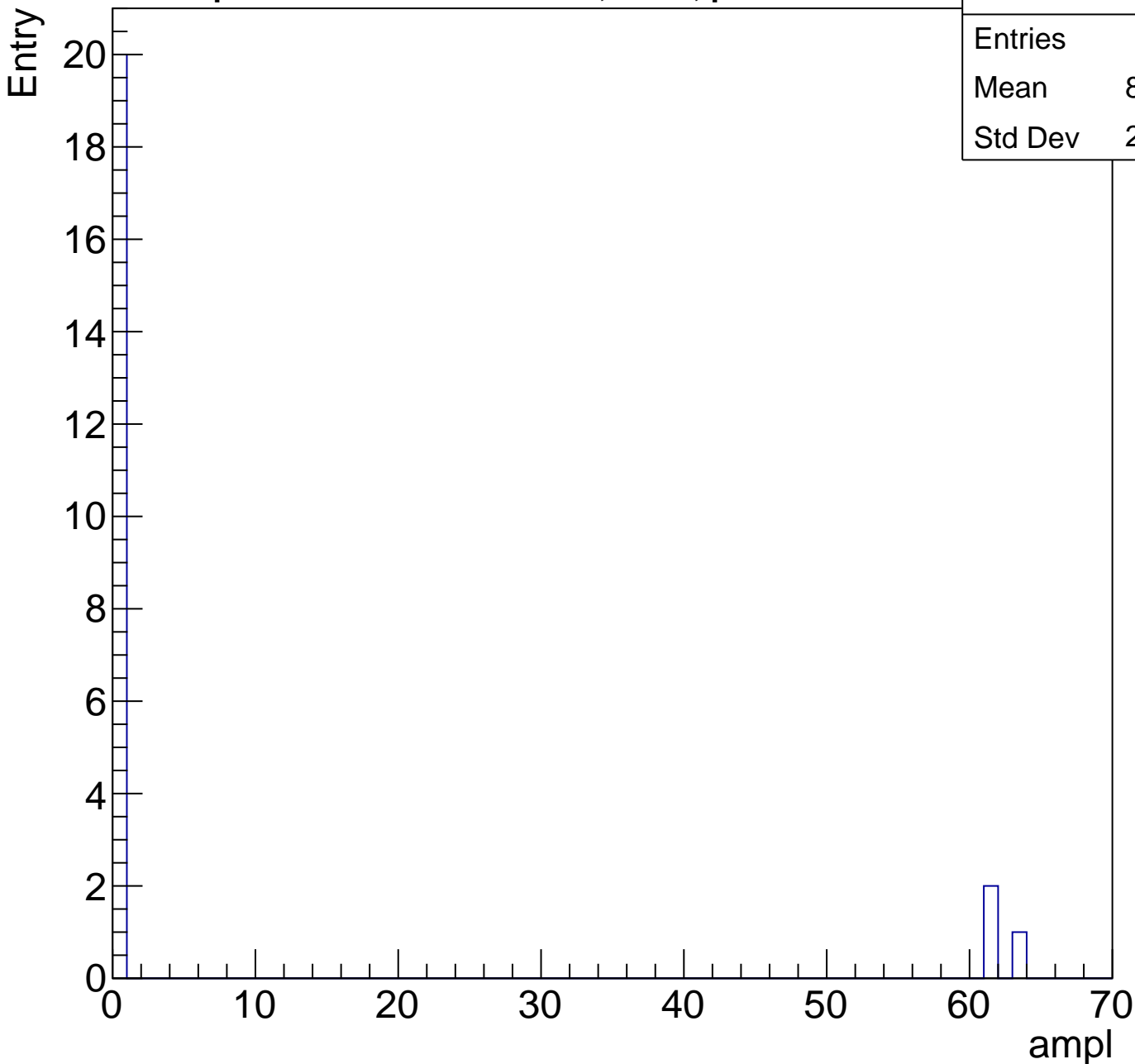
Entries	21
Mean	61.24
Std Dev	1.823



B1L103S, U8-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	8.043
Std Dev	20.77



B1L103S, U8-ch49, adc0

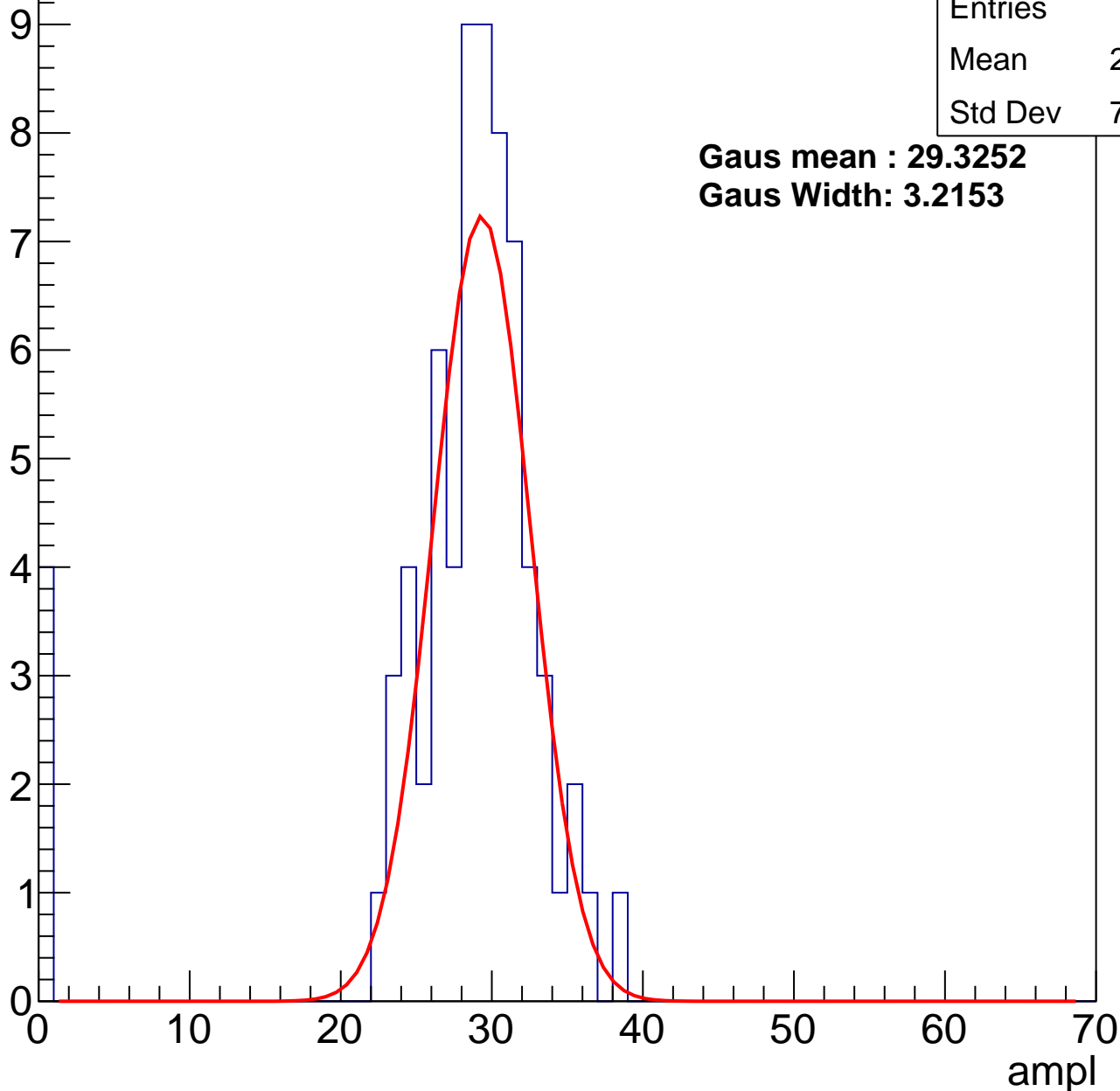
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	27.19
Std Dev	7.472

Gaus mean : 29.3252

Gaus Width: 3.2153



B1L103S, U8-ch49, adc1

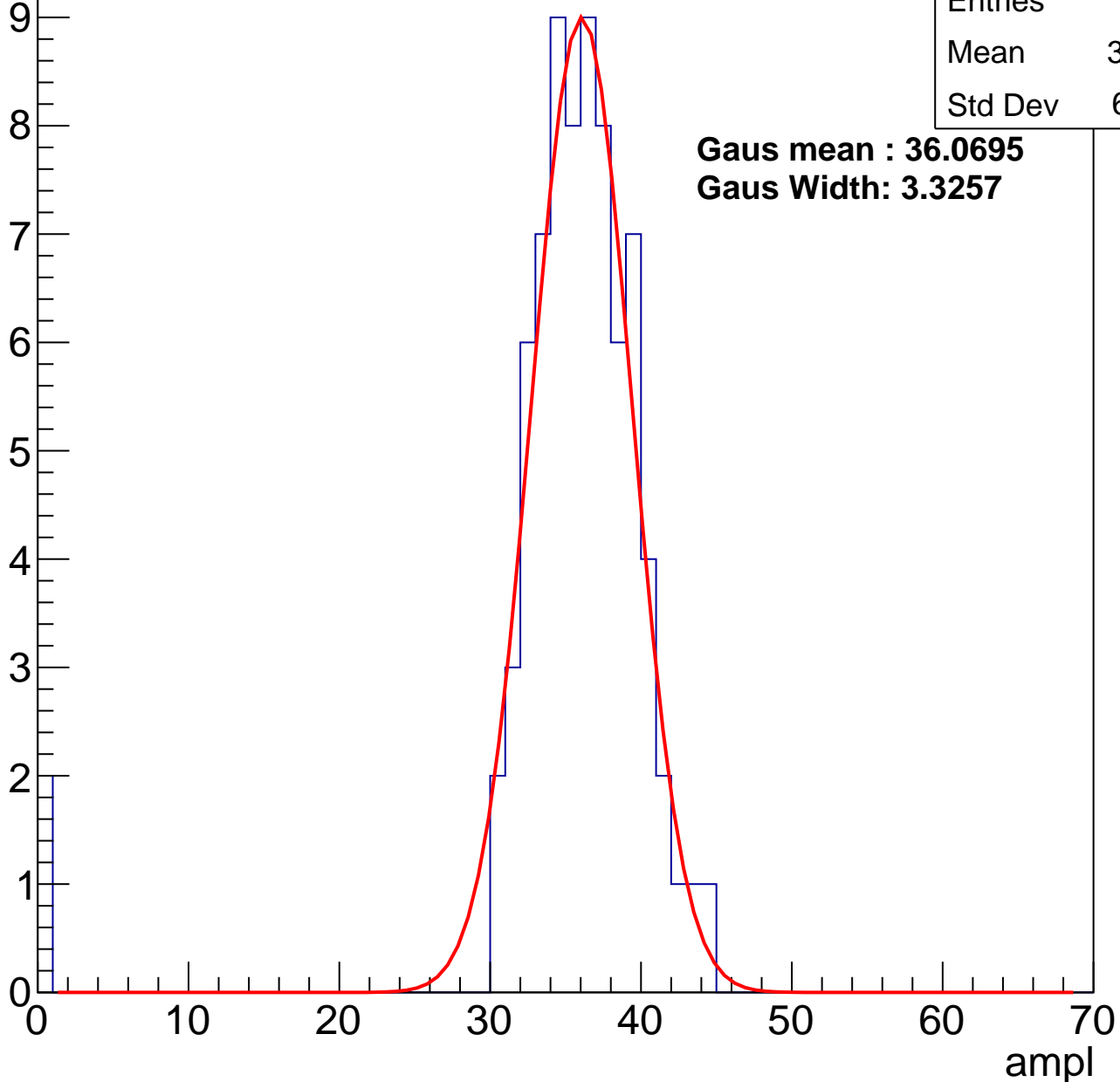
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.92
Std Dev	6.501

Gaus mean : 36.0695

Gaus Width: 3.3257



B1L103S, U8-ch49, adc2

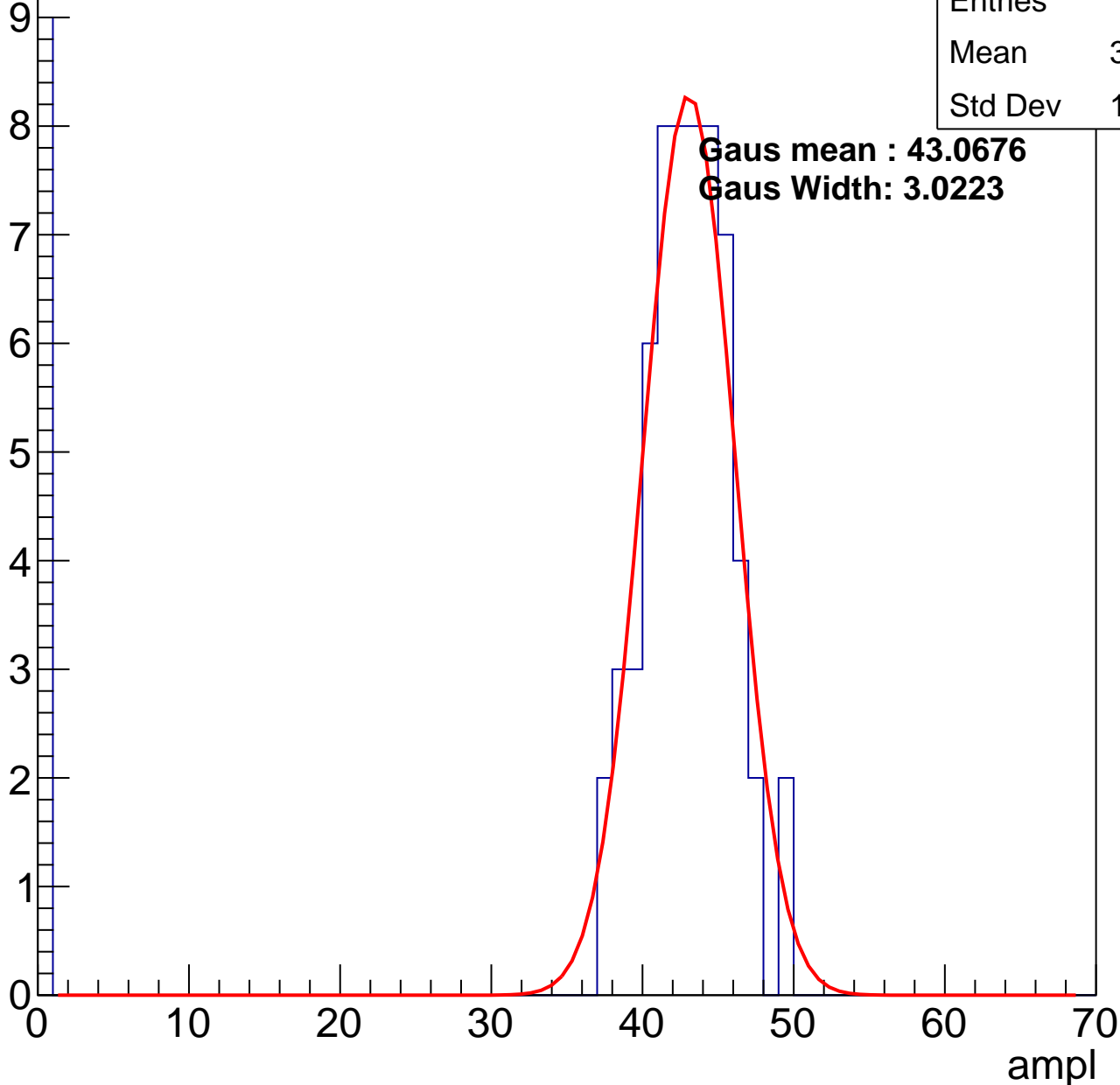
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	37.09
Std Dev	14.47

Gaus mean : 43.0676

Gaus Width: 3.0223



B1L103S, U8-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

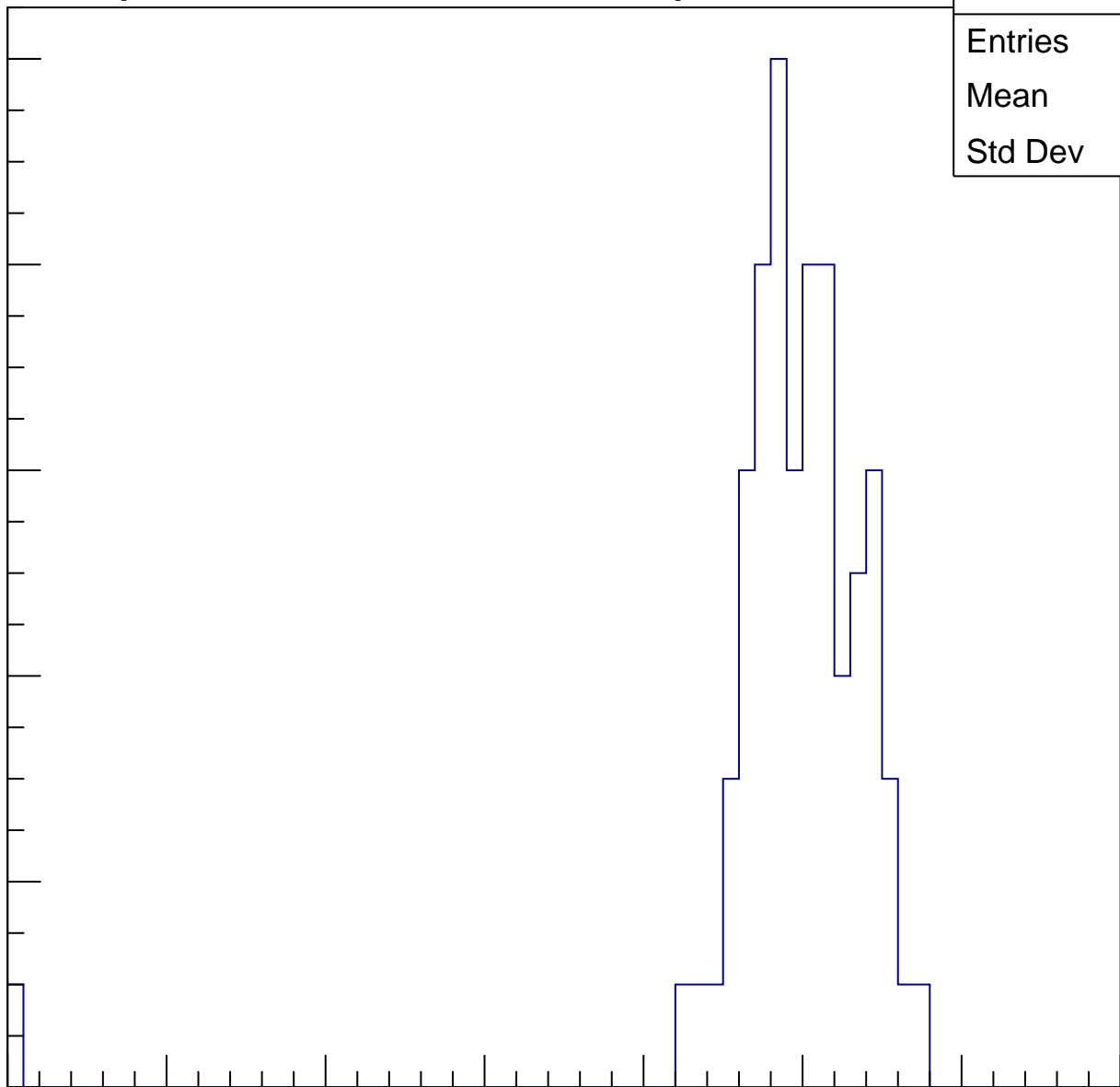
Entries	73
Mean	48.95
Std Dev	6.602

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

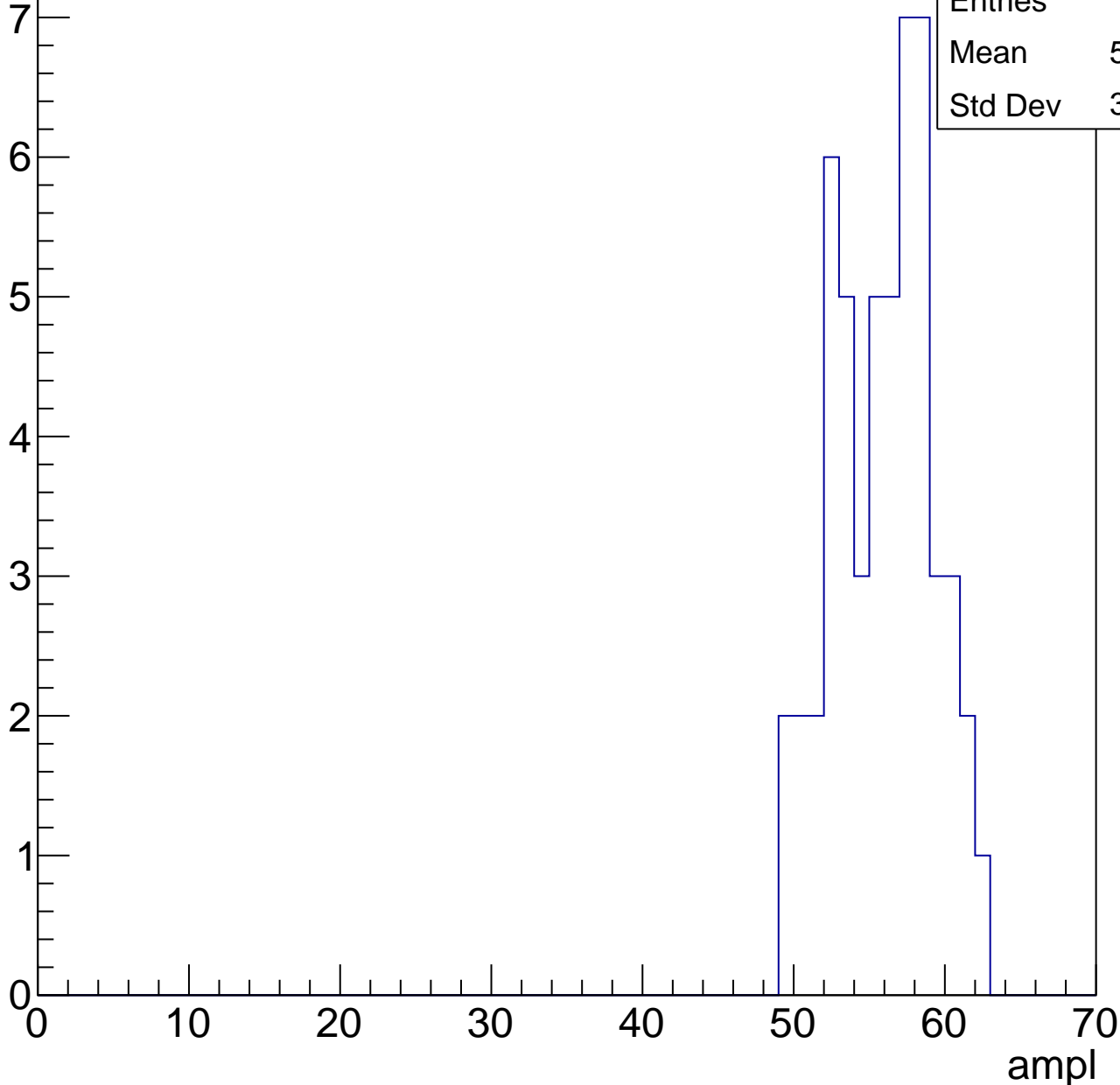


B1L103S, U8-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

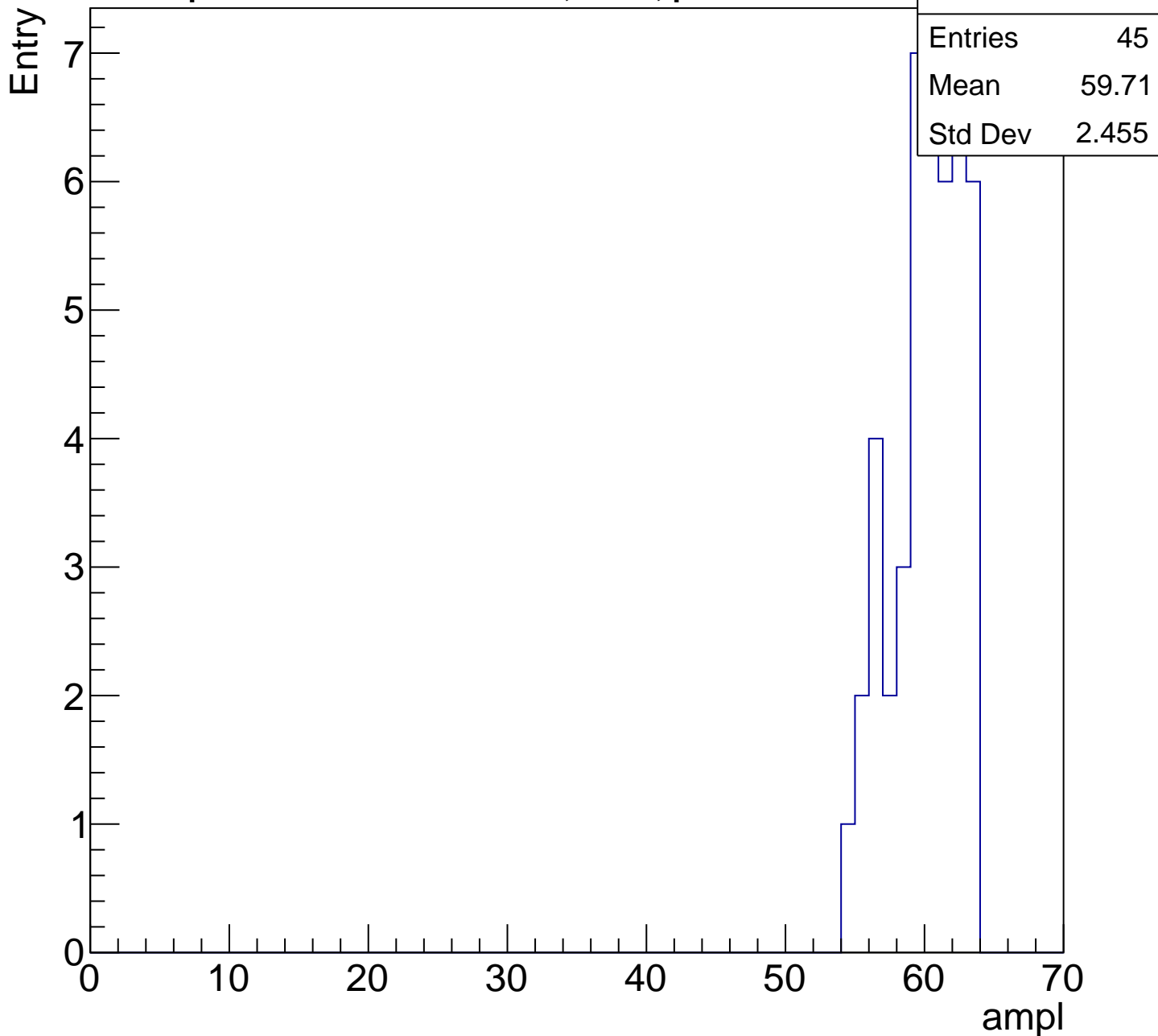
Entry

Entries	53
Mean	55.47
Std Dev	3.248



B1L103S, U8-ch49, adc5

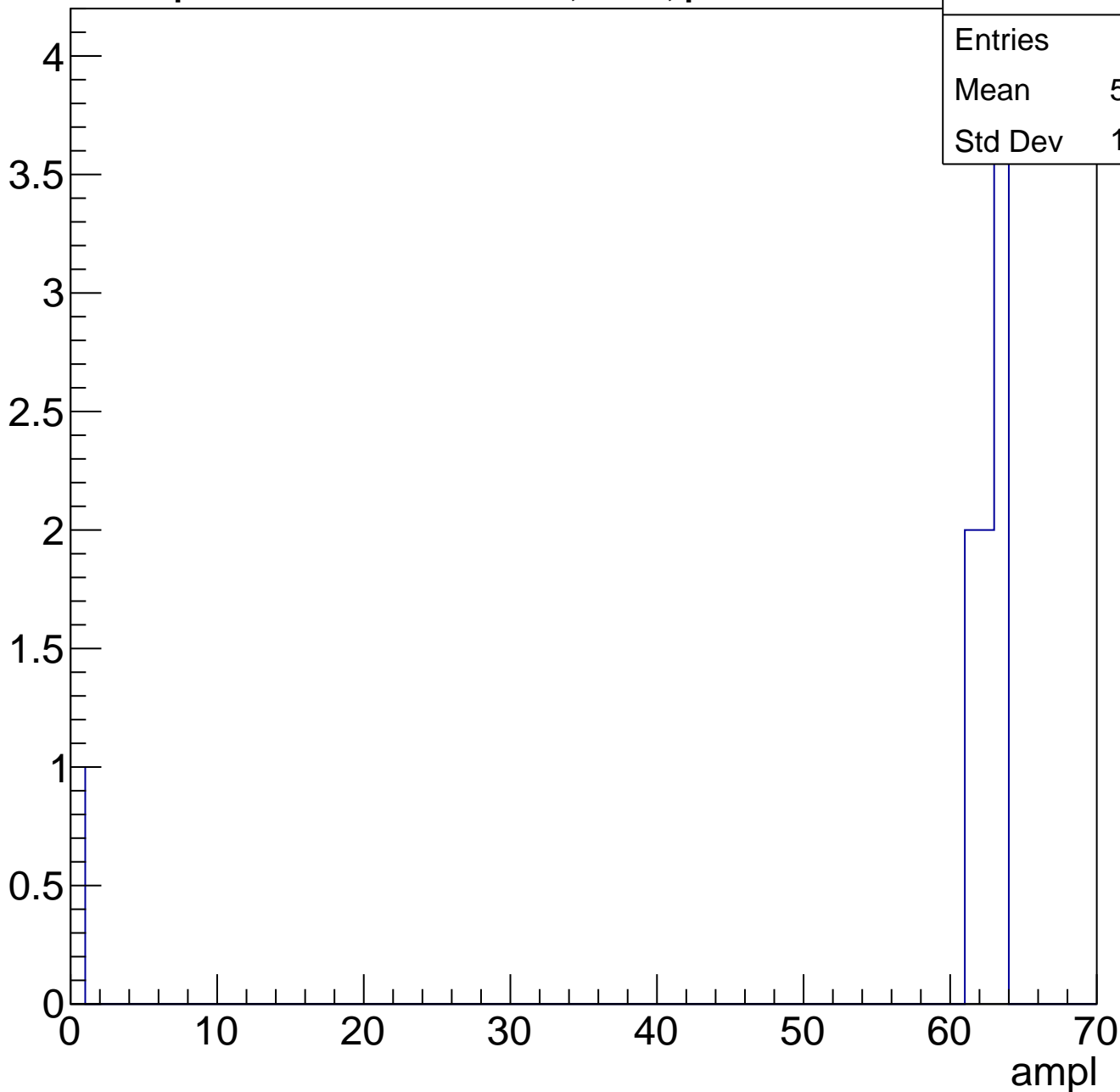
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch50, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	24.85
Std Dev	10.22

Gaus mean : 28.9915

Gaus Width: 3.7457

Entry

10

8

6

4

2

0

ampl

0

10

20

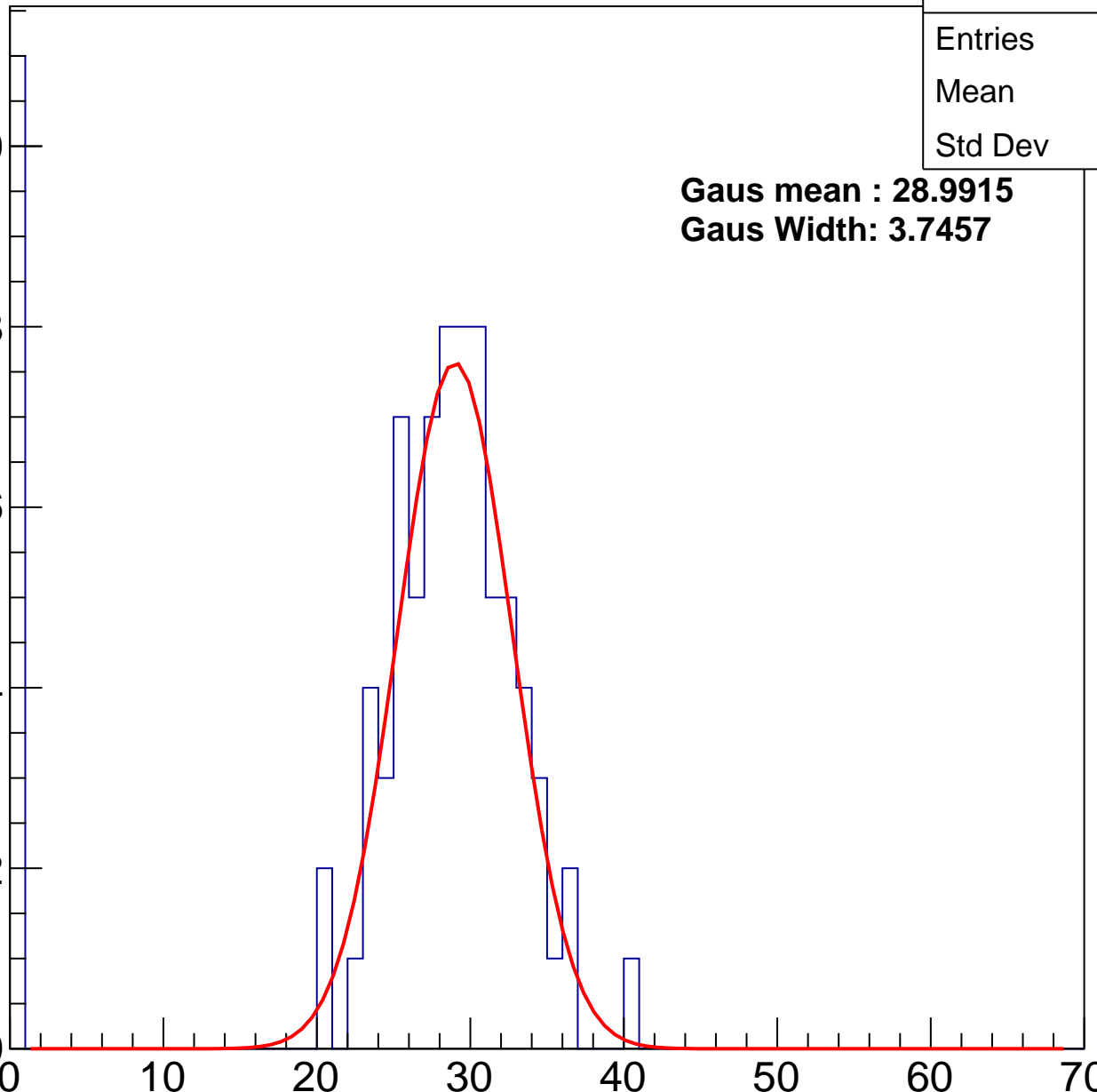
30

40

50

60

70



B1L103S, U8-ch50, adc1

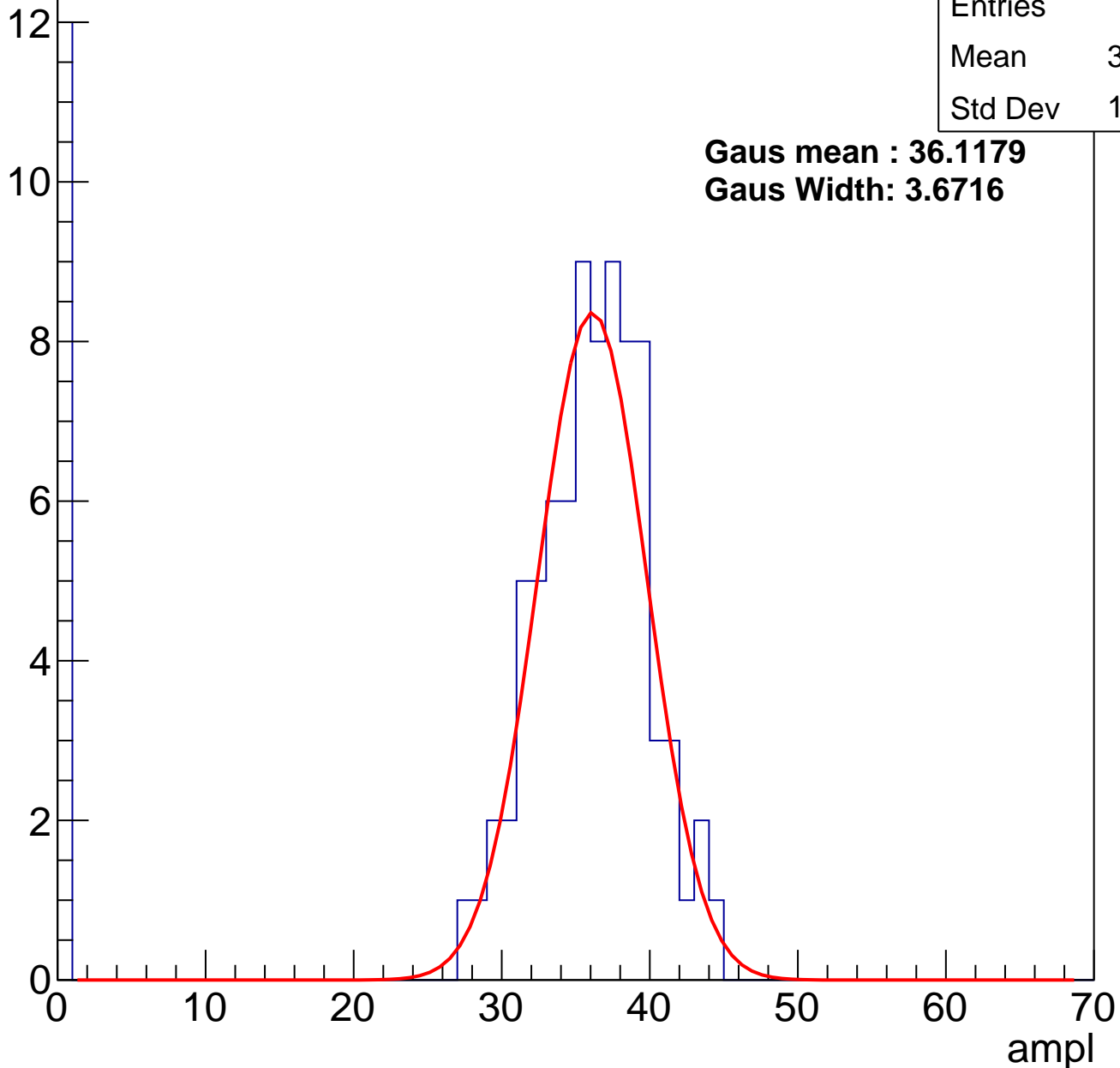
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	31.05
Std Dev	12.49

Gaus mean : 36.1179

Gaus Width: 3.6716

Entry



B1L103S, U8-ch50, adc2

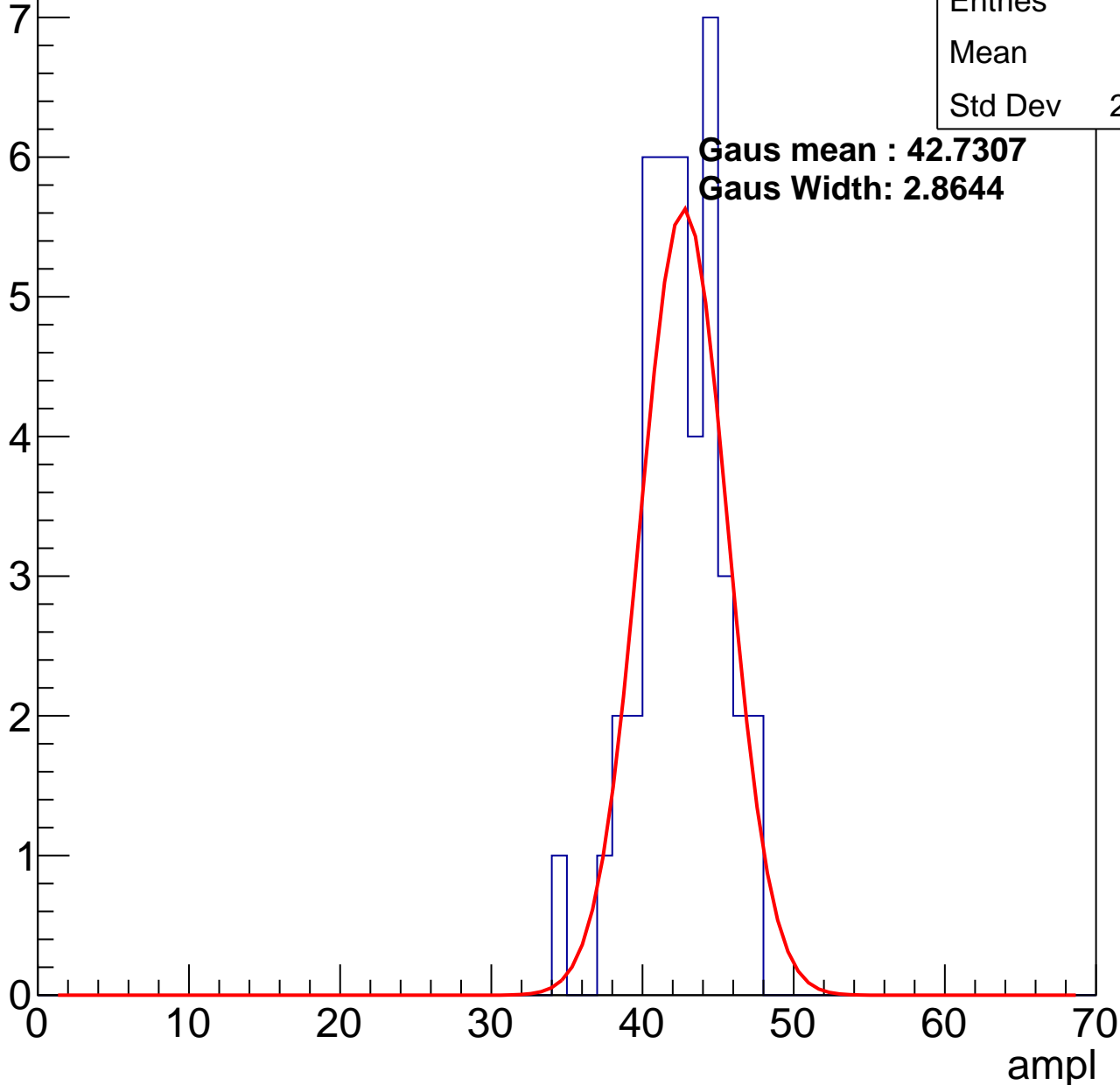
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	42
Std Dev	2.717

Gaus mean : 42.7307

Gaus Width: 2.8644

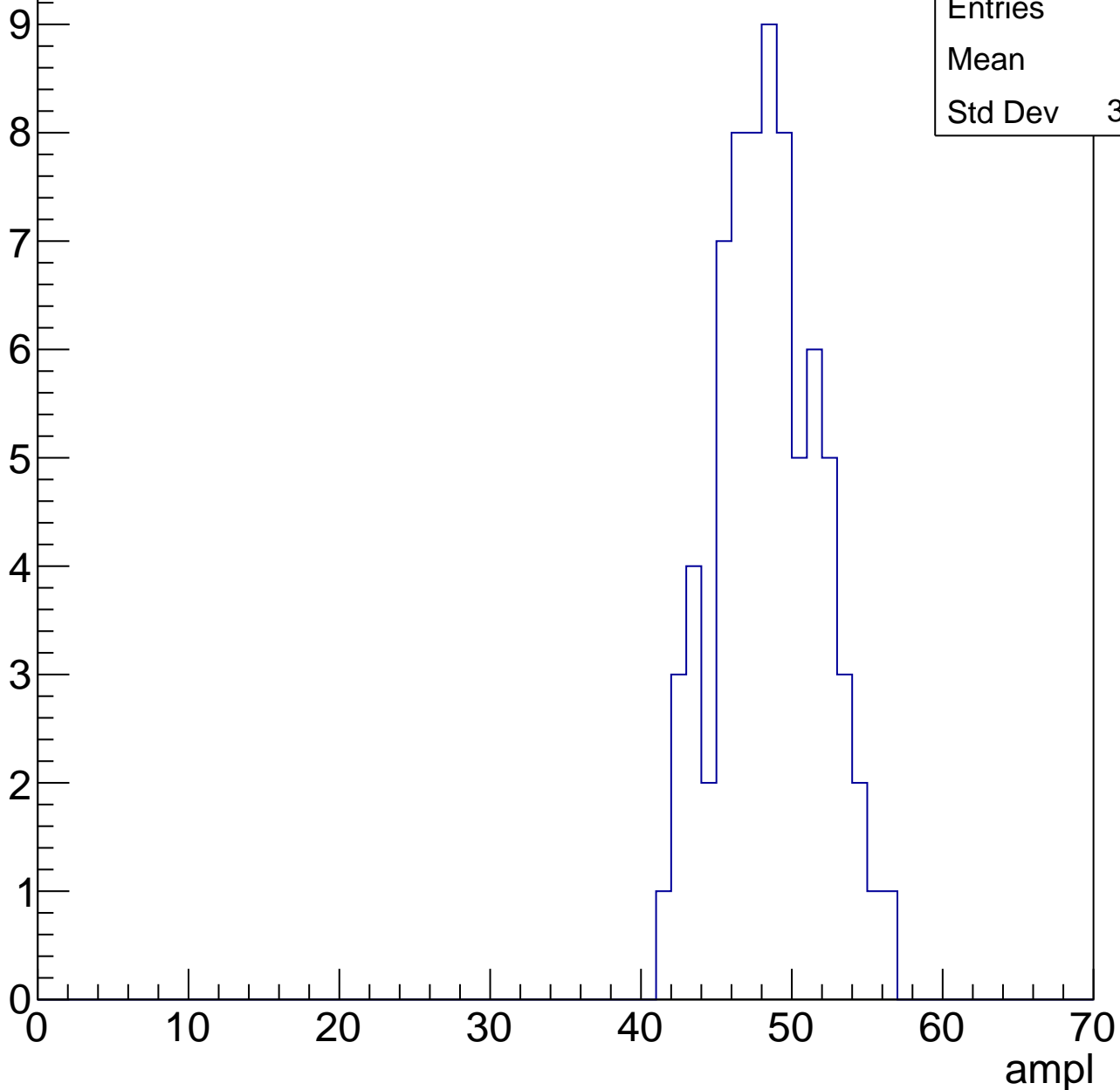


B1L103S, U8-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

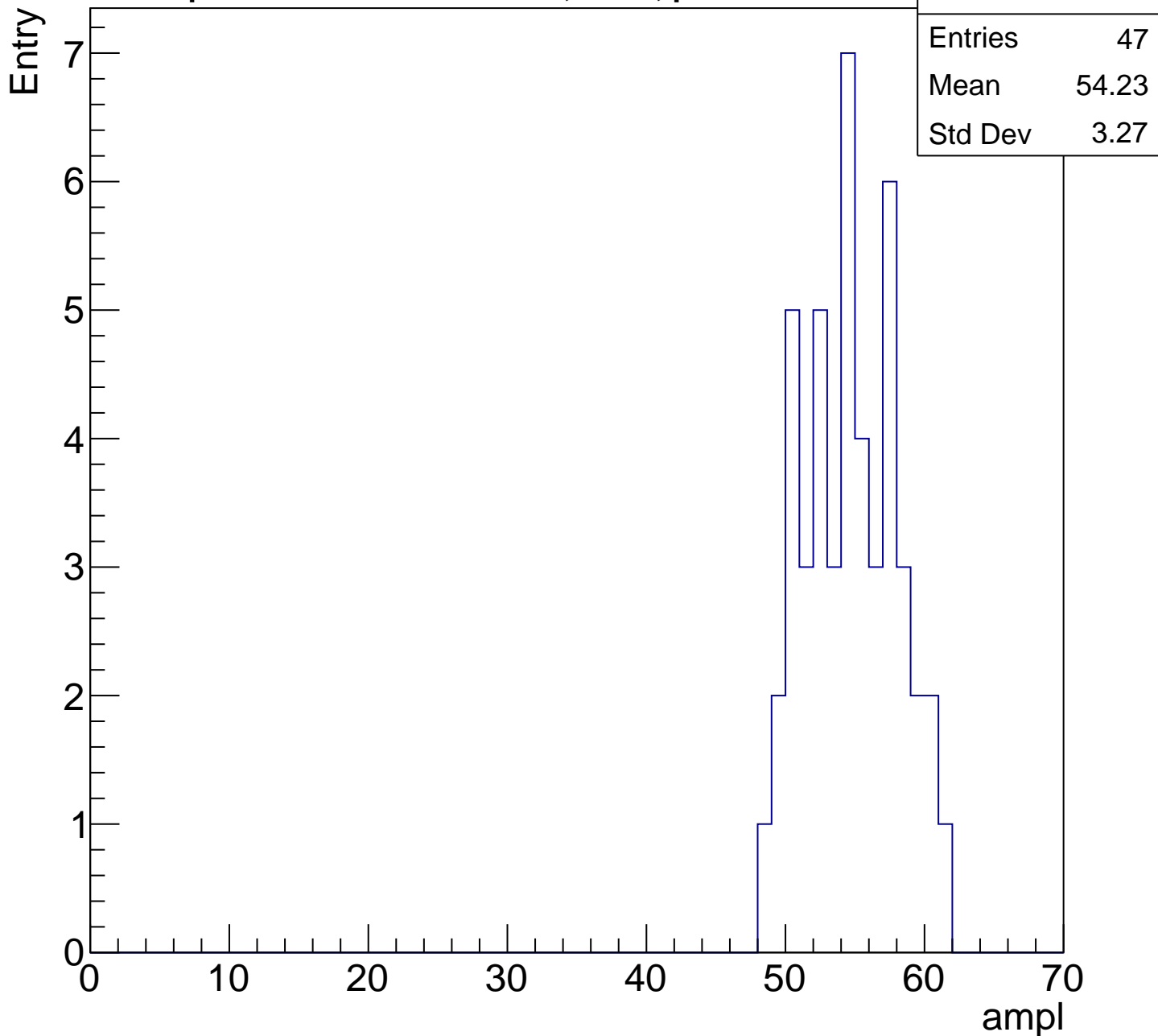
Entry

Entries	73
Mean	48
Std Dev	3.339



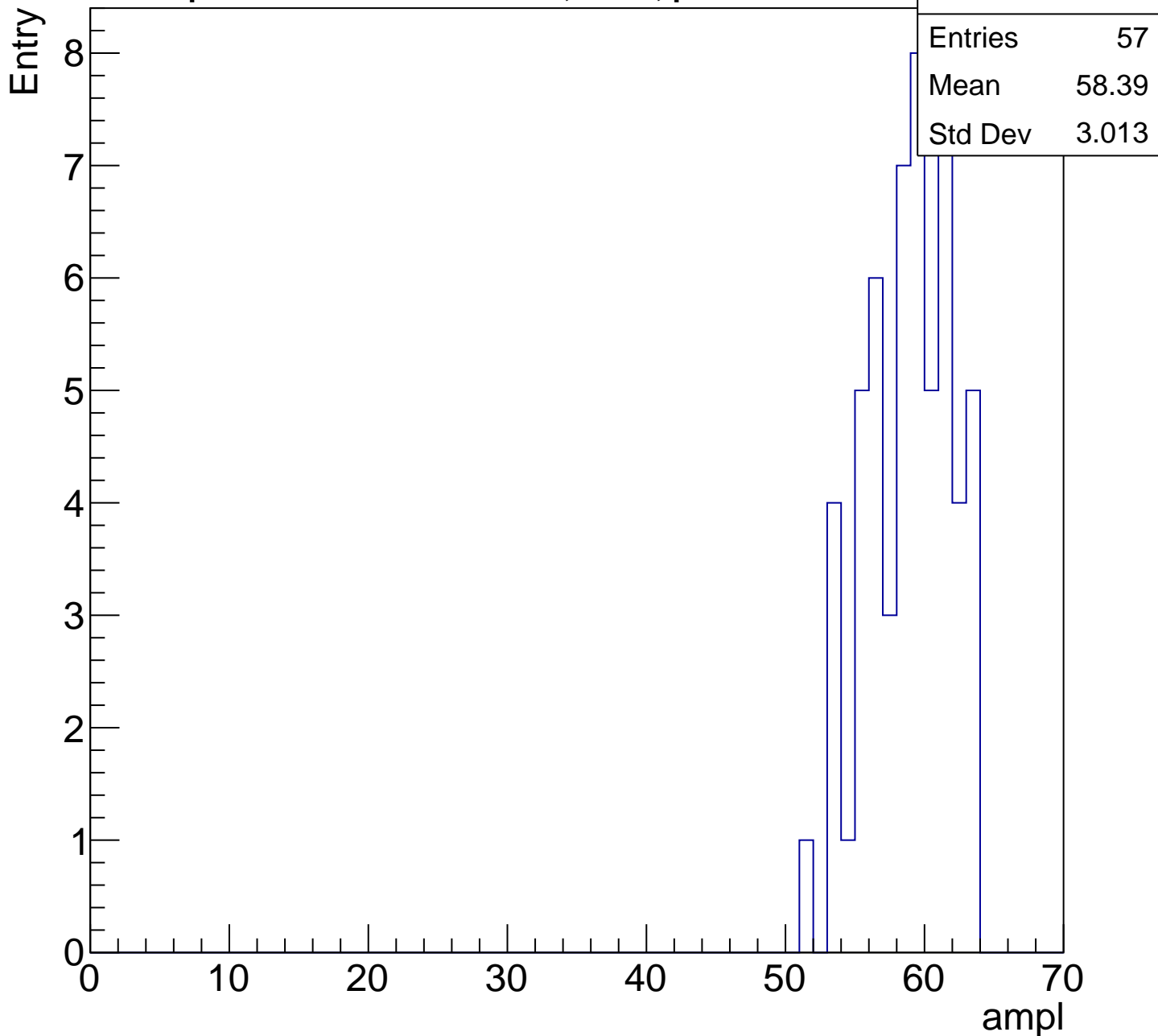
B1L103S, U8-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

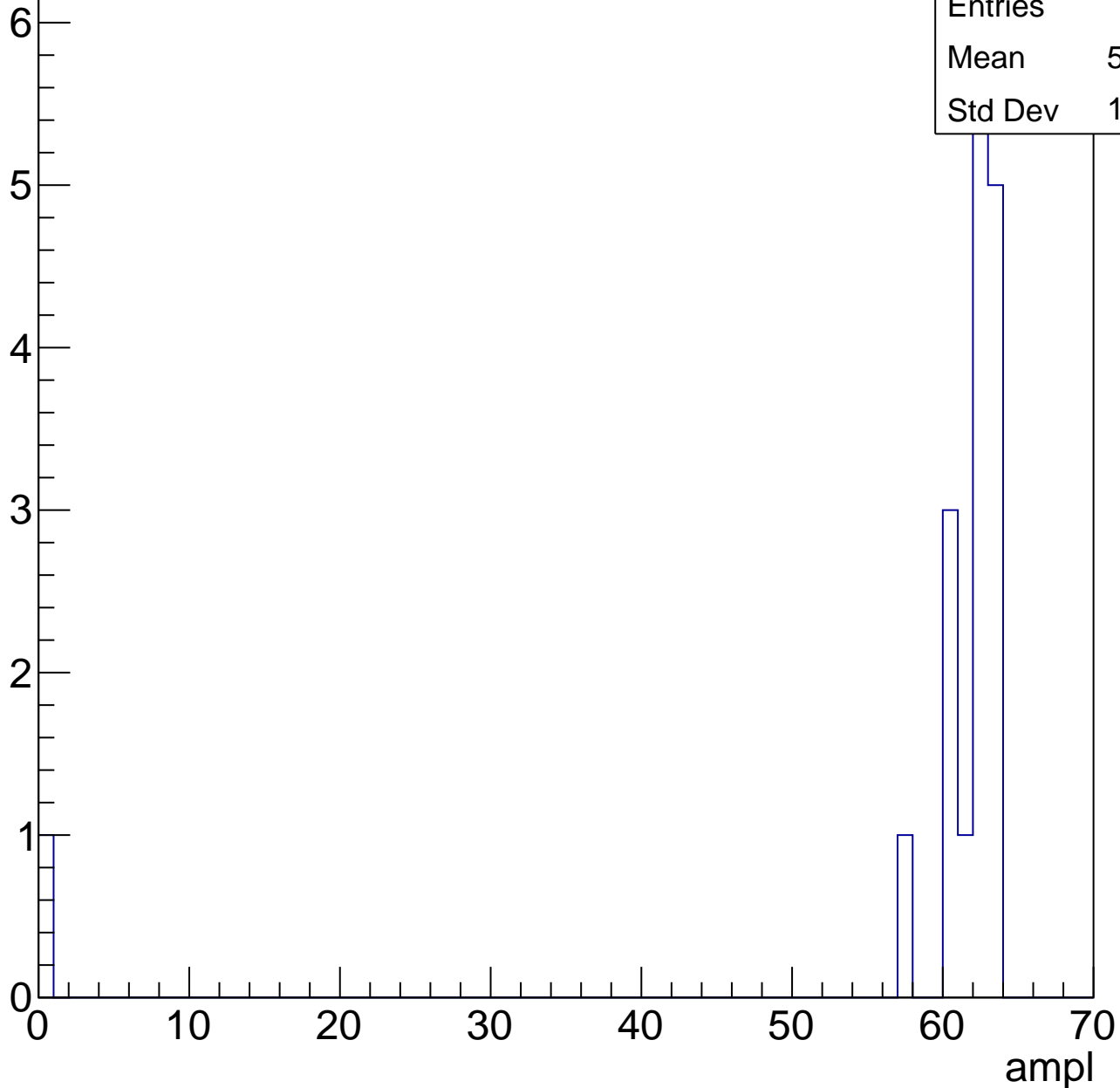


B1L103S, U8-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.94
Std Dev	14.57



B1L103S, U8-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch51, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	26.14
Std Dev	11.87

Gaus mean : 31.4704

Gaus Width: 3.3085

Entry

12

10

8

6

4

2

0

0

10

20

30

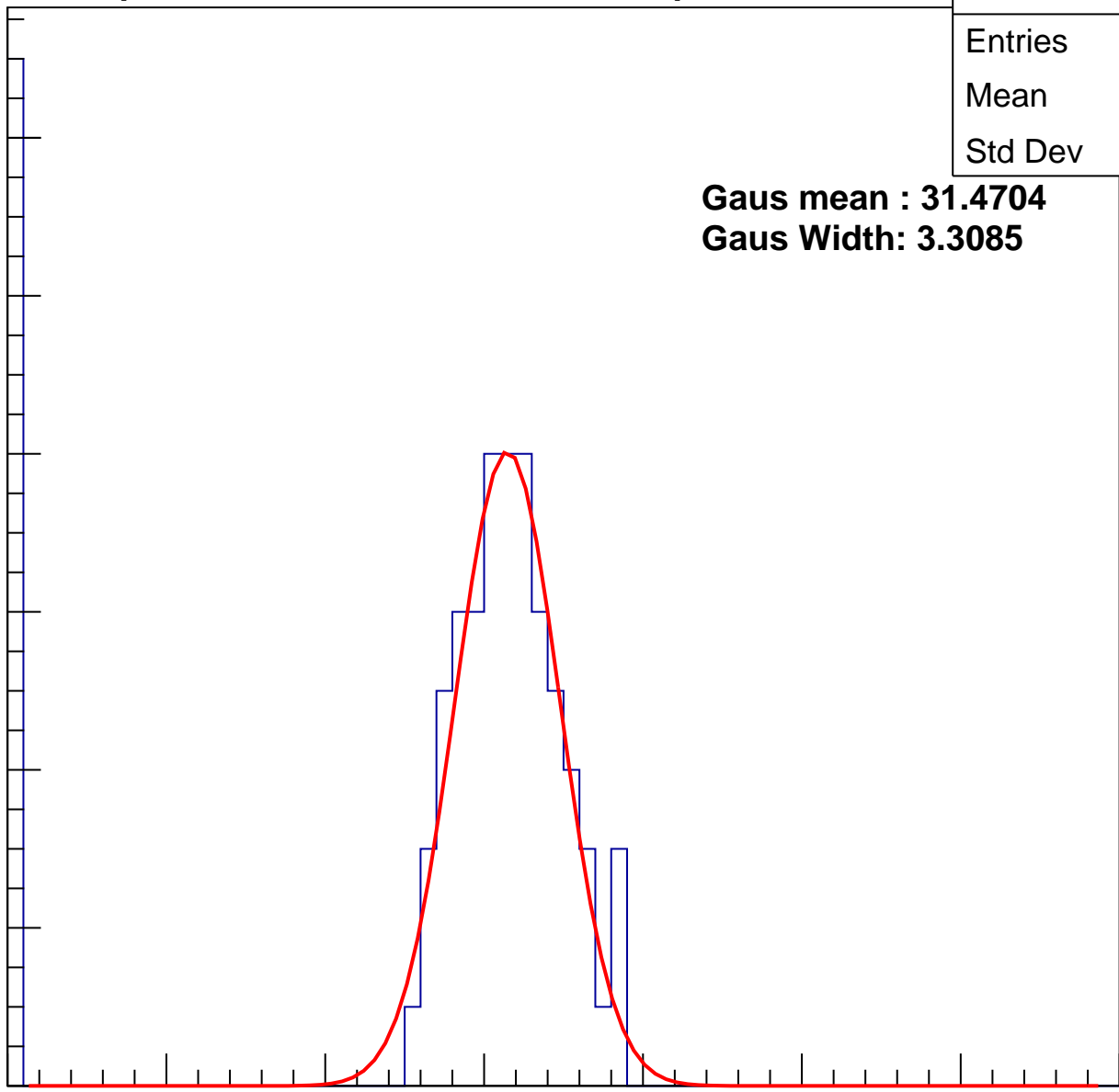
40

50

60

70

ampl



B1L103S, U8-ch51, adc1

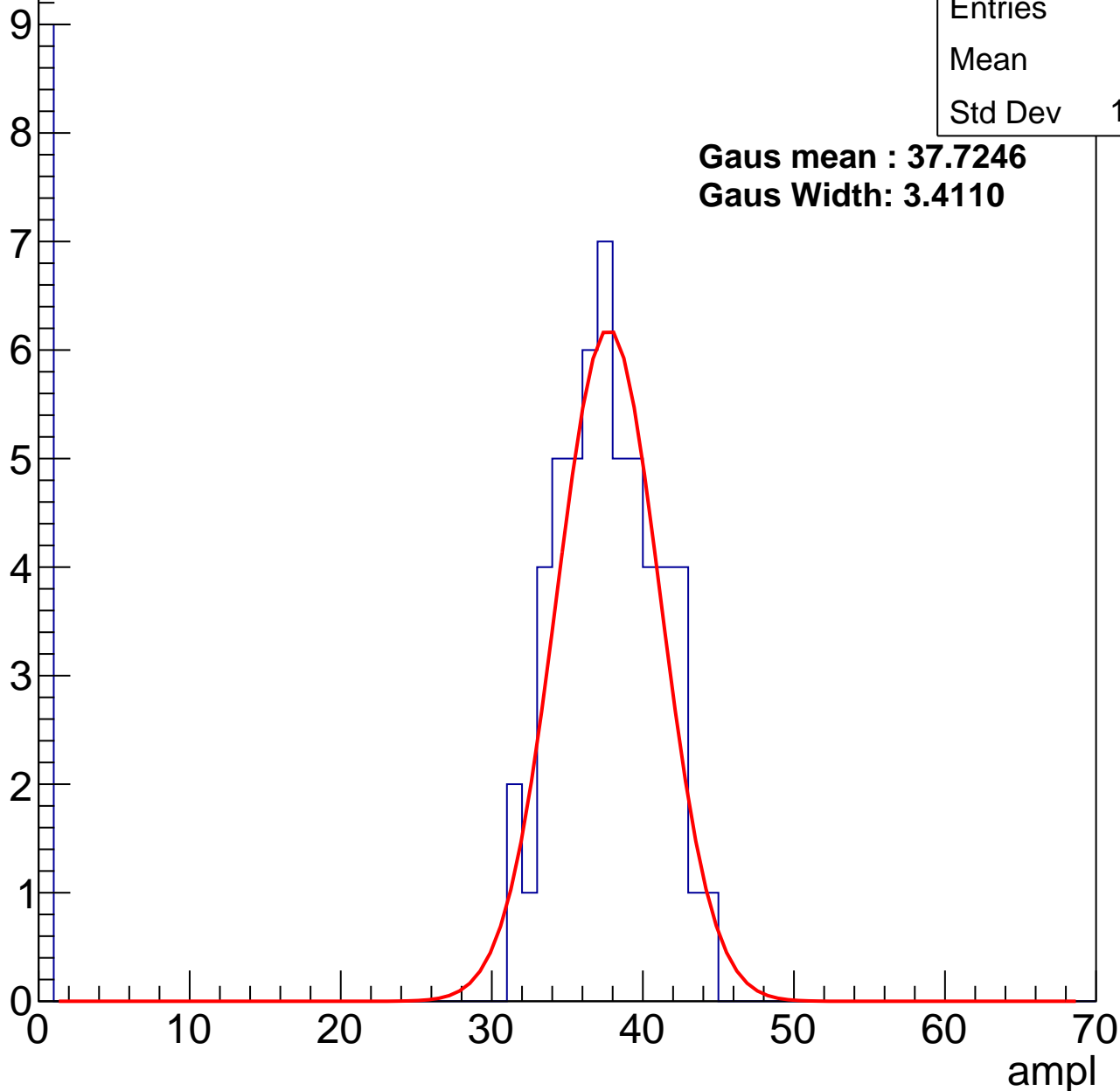
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	31.9
Std Dev	13.35

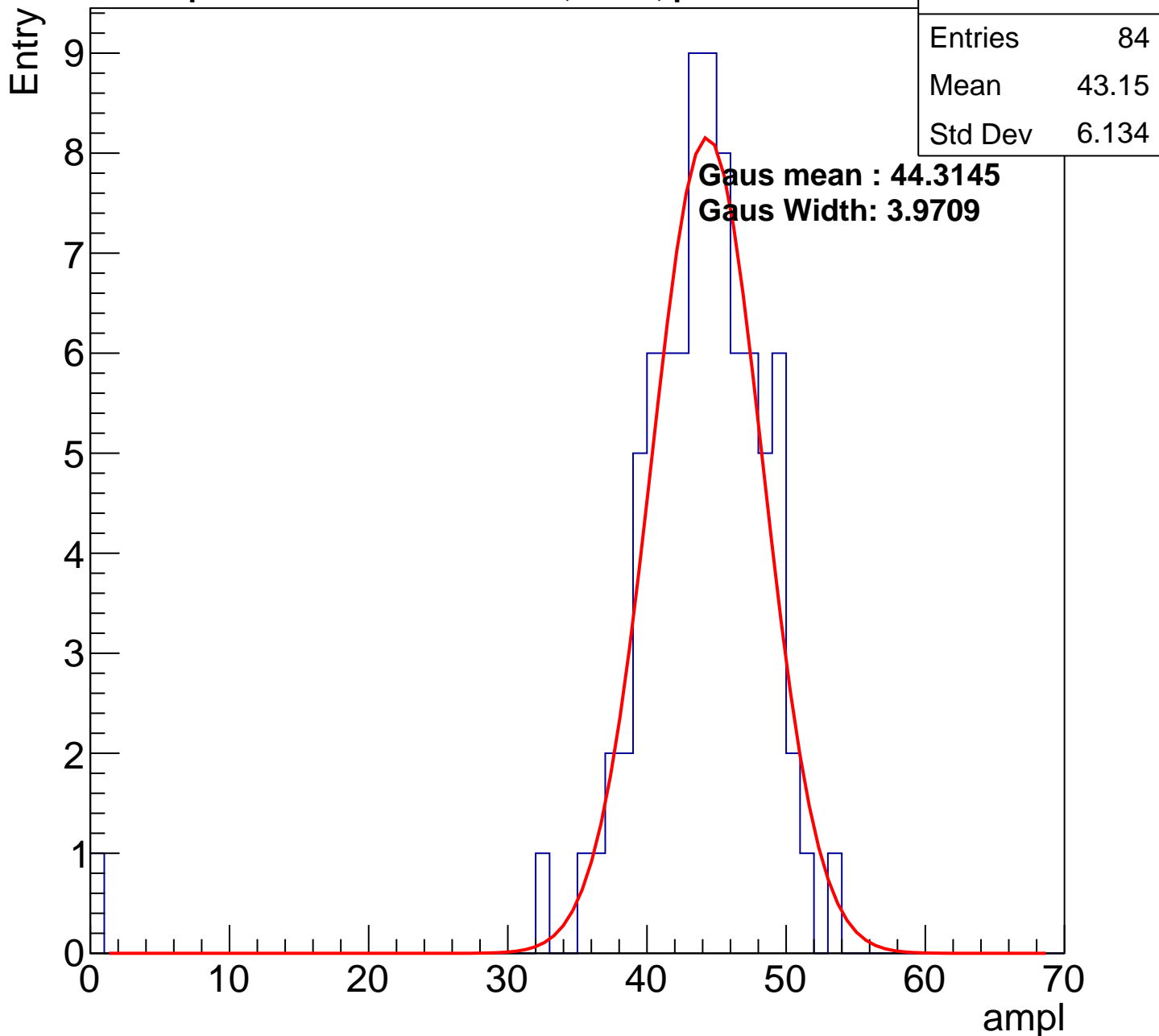
Gaus mean : 37.7246

Gaus Width: 3.4110



B1L103S, U8-ch51, adc2

calib_packv5_041523_1651.root, FC#0, port C2

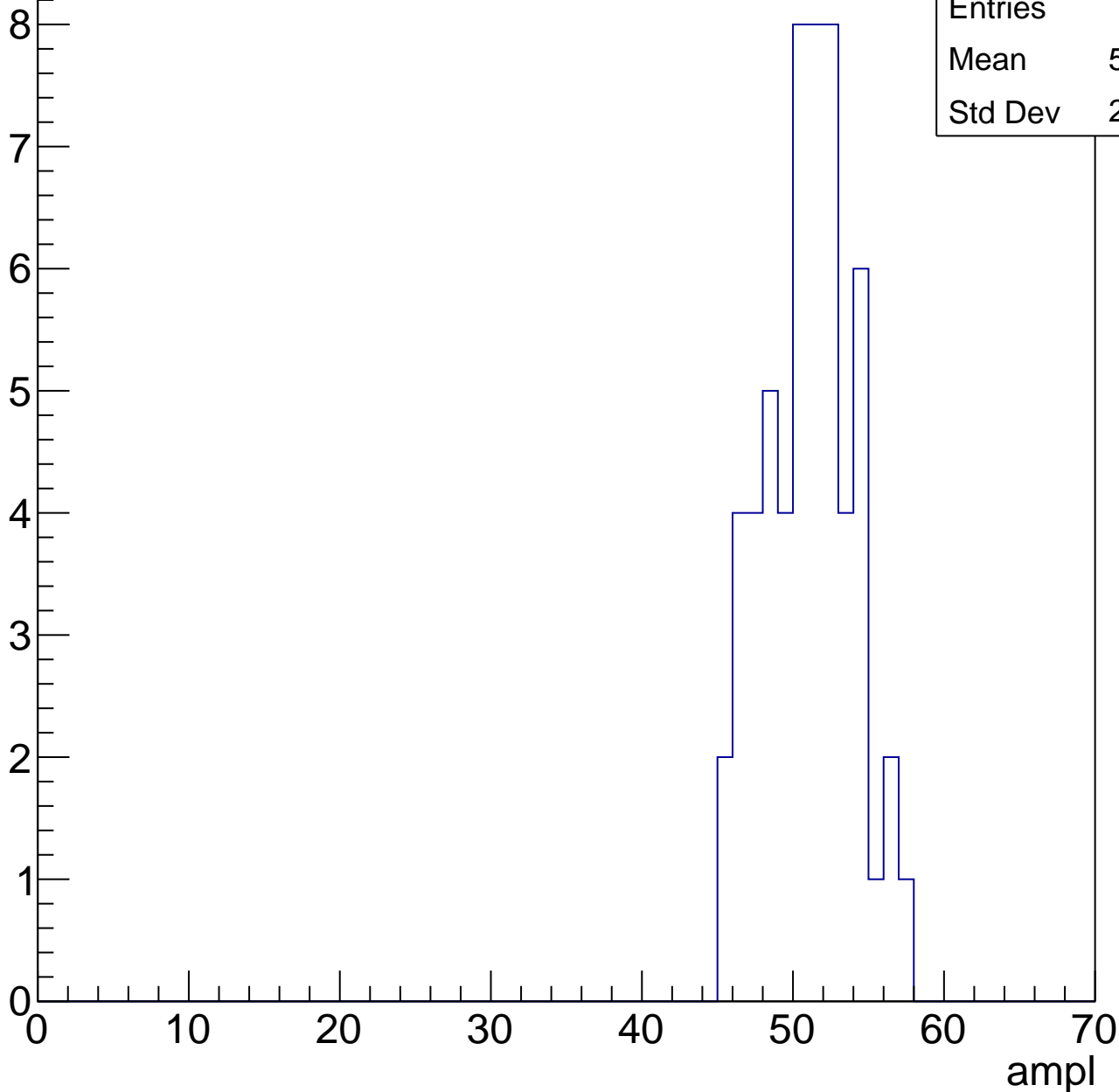


B1L103S, U8-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

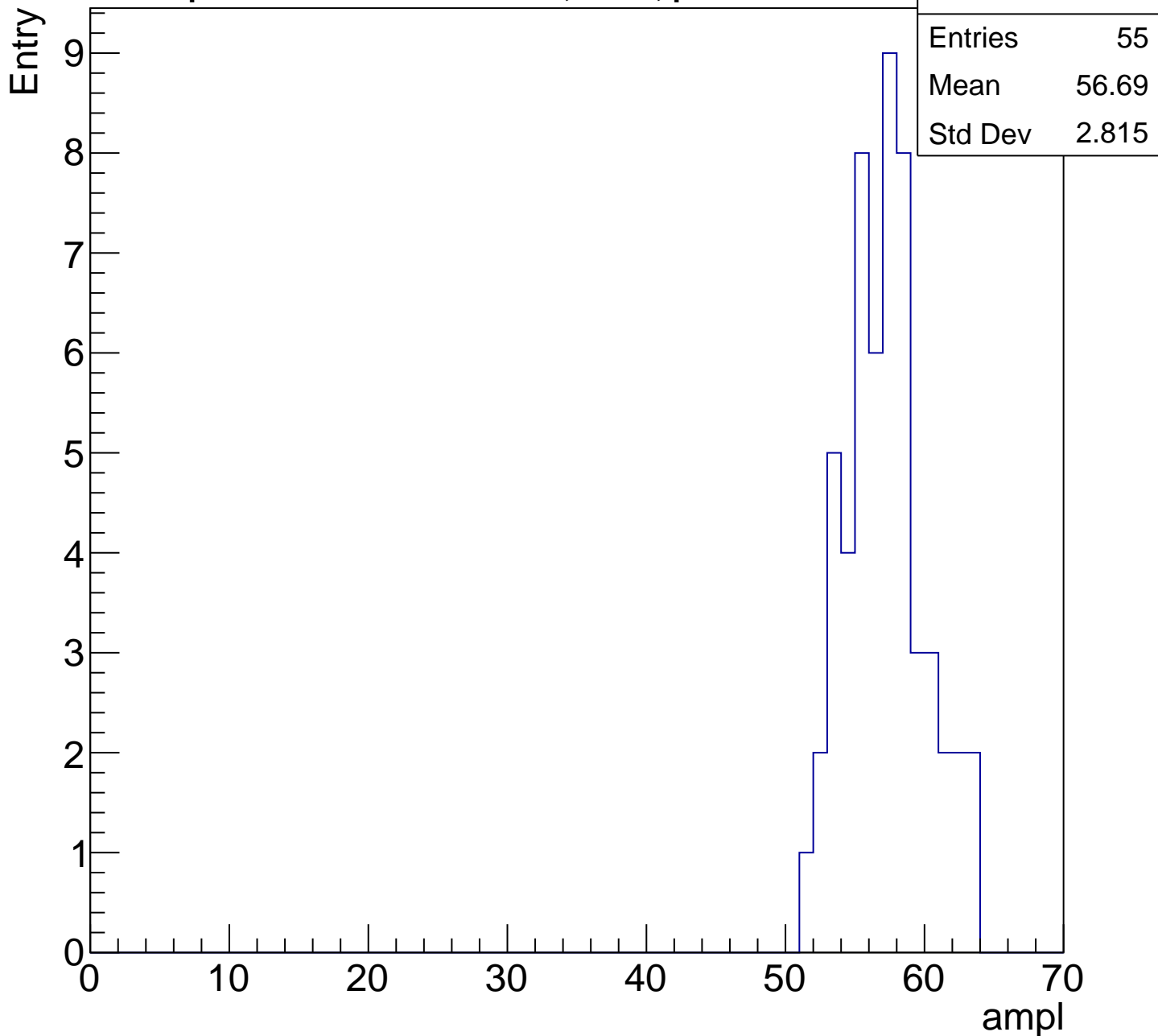
Entry

Entries	57
Mean	50.56
Std Dev	2.884



B1L103S, U8-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 38

Mean 58.71

Std Dev 9.854

ampl

0

10

20

30

40

50

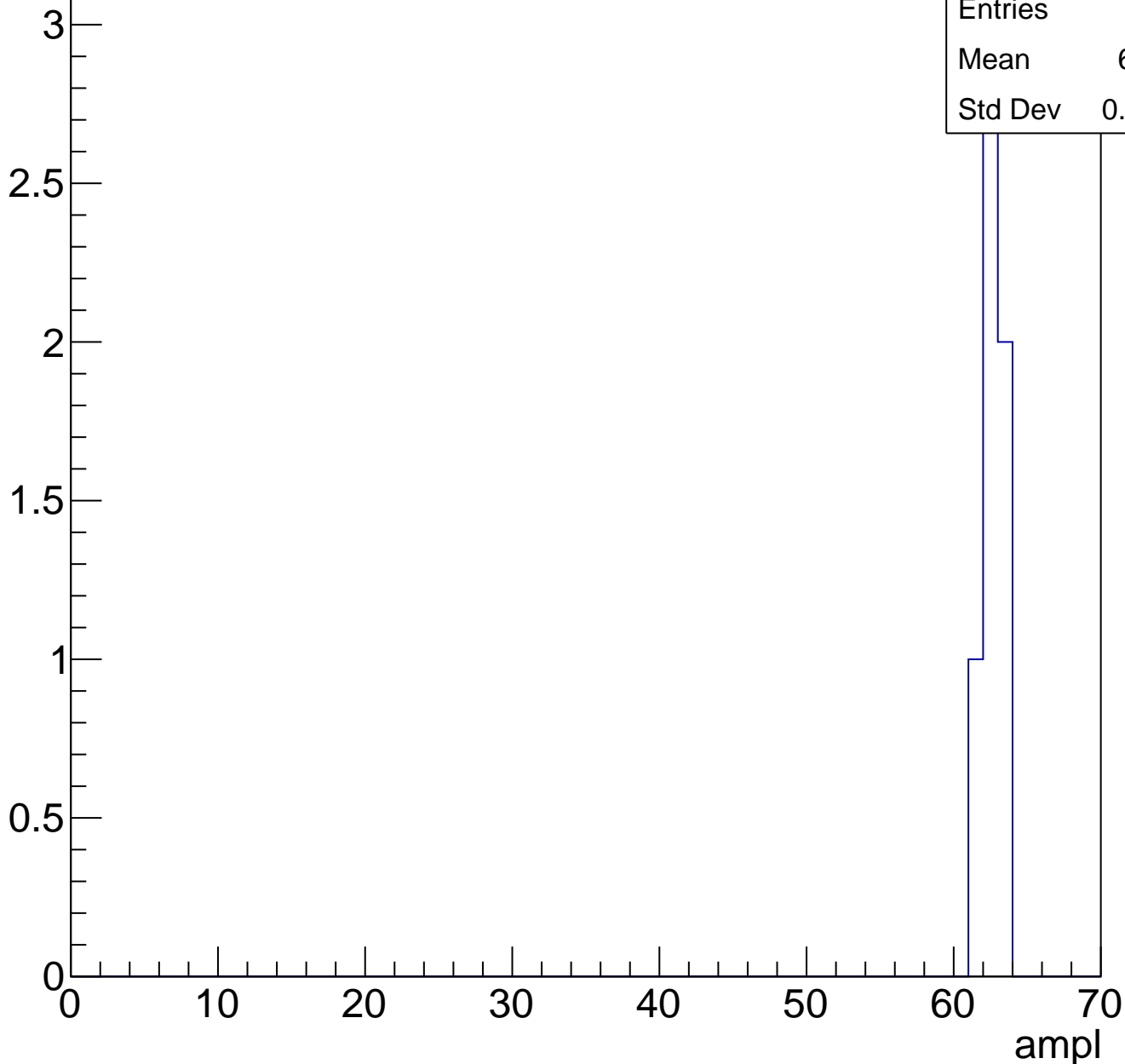
60

70

B1L103S, U8-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

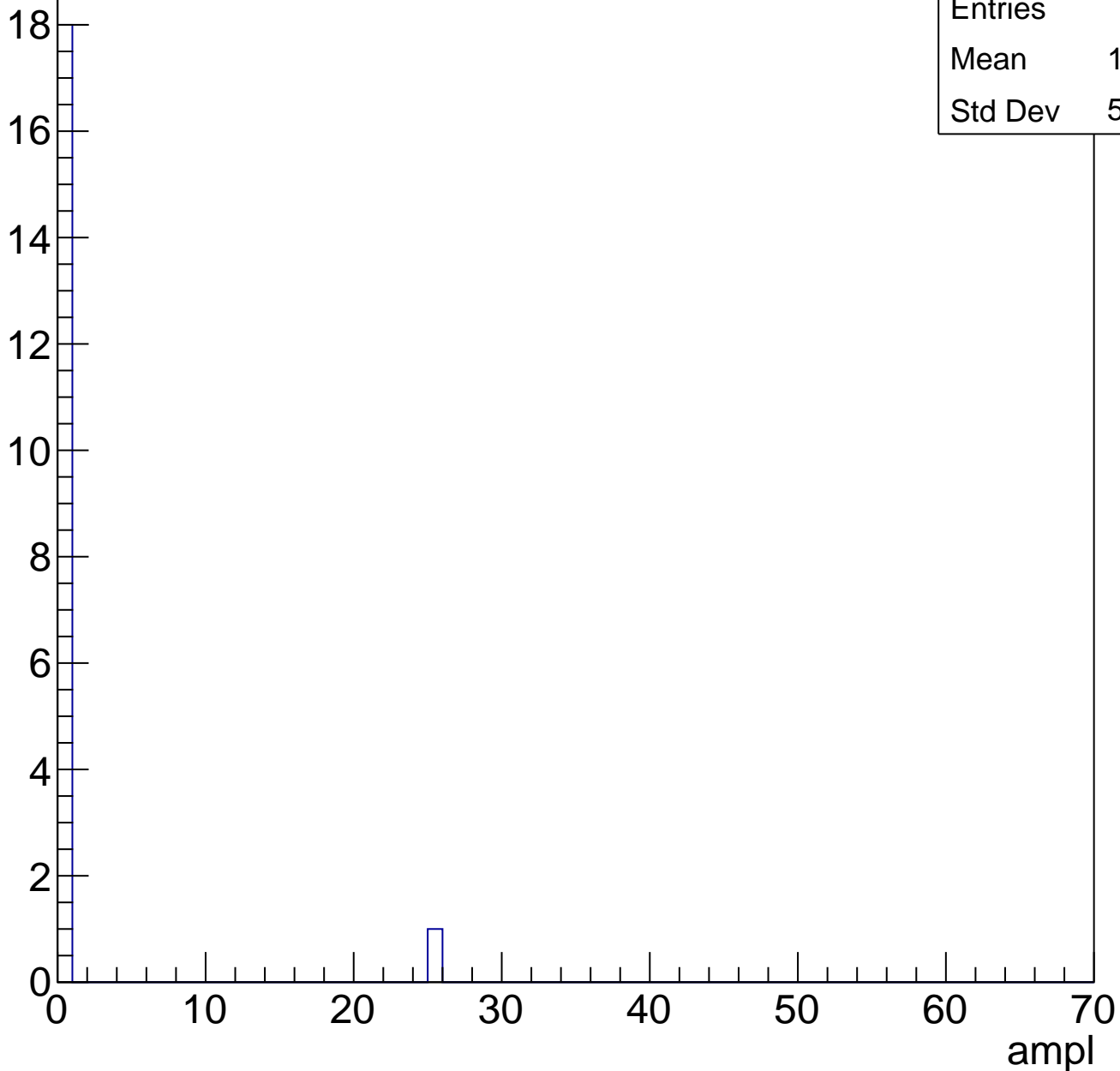


B1L103S, U8-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.316
Std Dev	5.582

Entry



B1L103S, U8-ch52, adc0

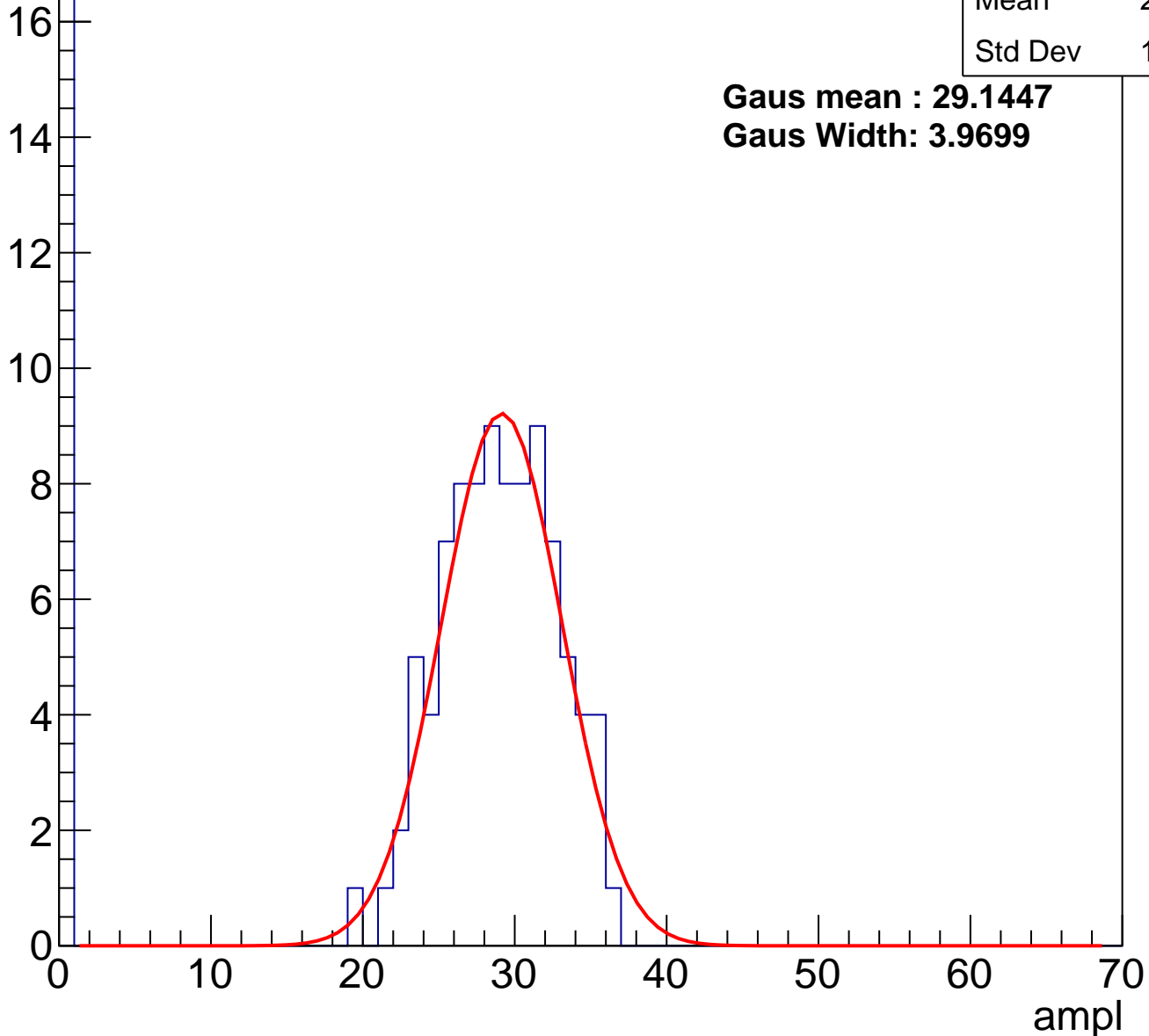
calib_packv5_041523_1651.root, FC#0, port C2

Entries	108
Mean	24.06
Std Dev	10.93

Gaus mean : 29.1447

Gaus Width: 3.9699

Entry



B1L103S, U8-ch52, adc1

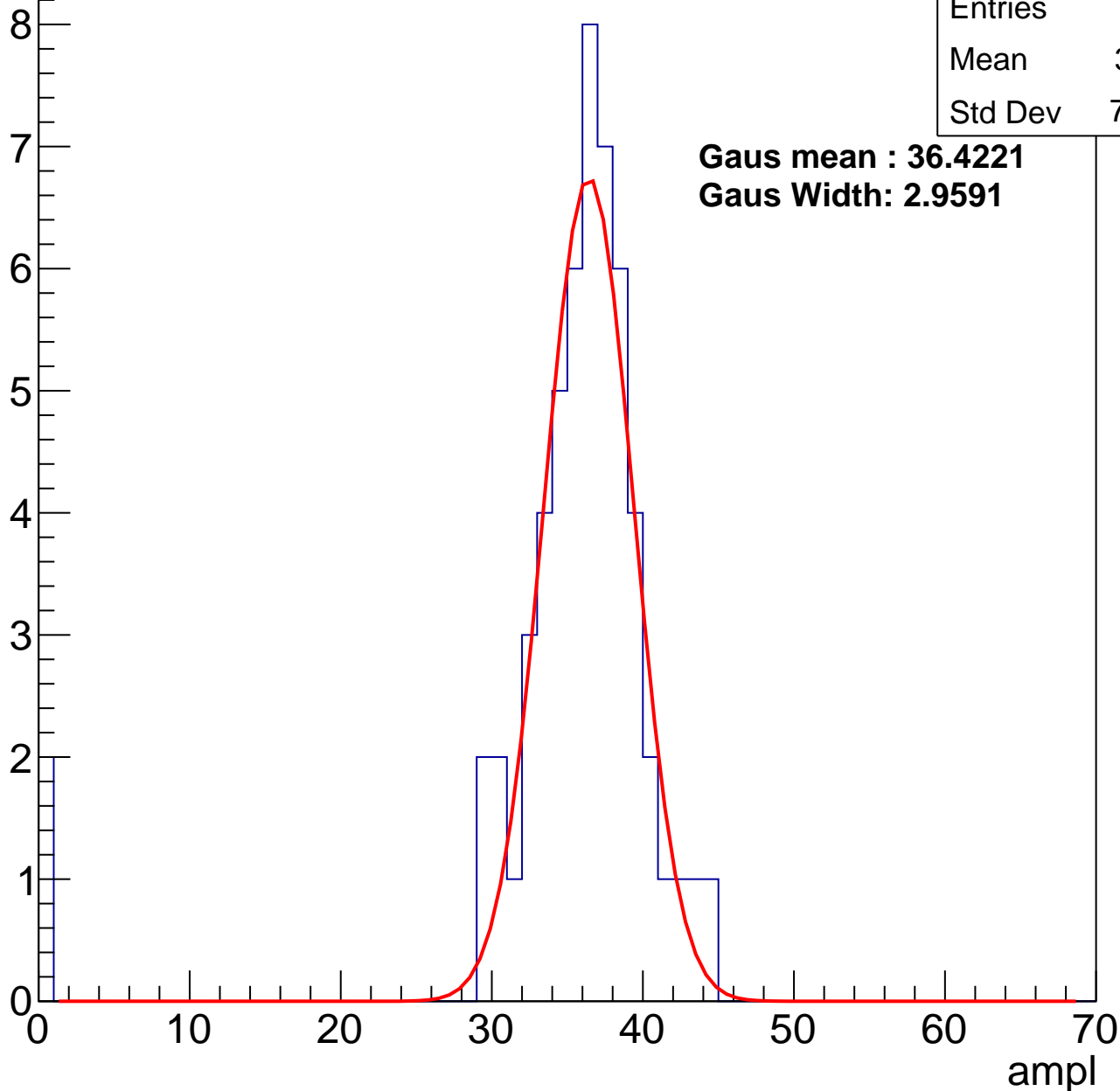
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	34.61
Std Dev	7.394

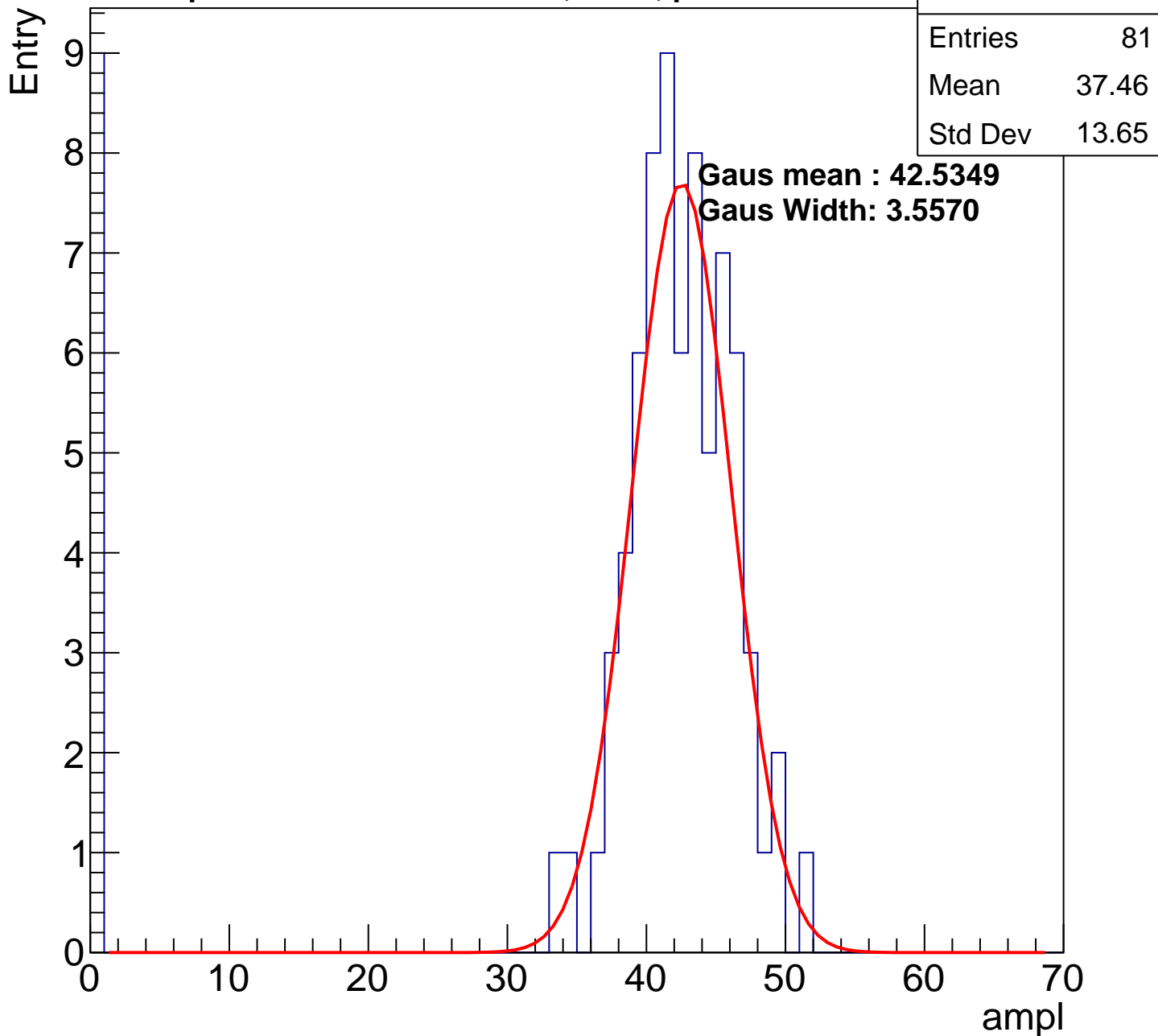
Gaus mean : 36.4221

Gaus Width: 2.9591



B1L103S, U8-ch52, adc2

calib_packv5_041523_1651.root, FC#0, port C2

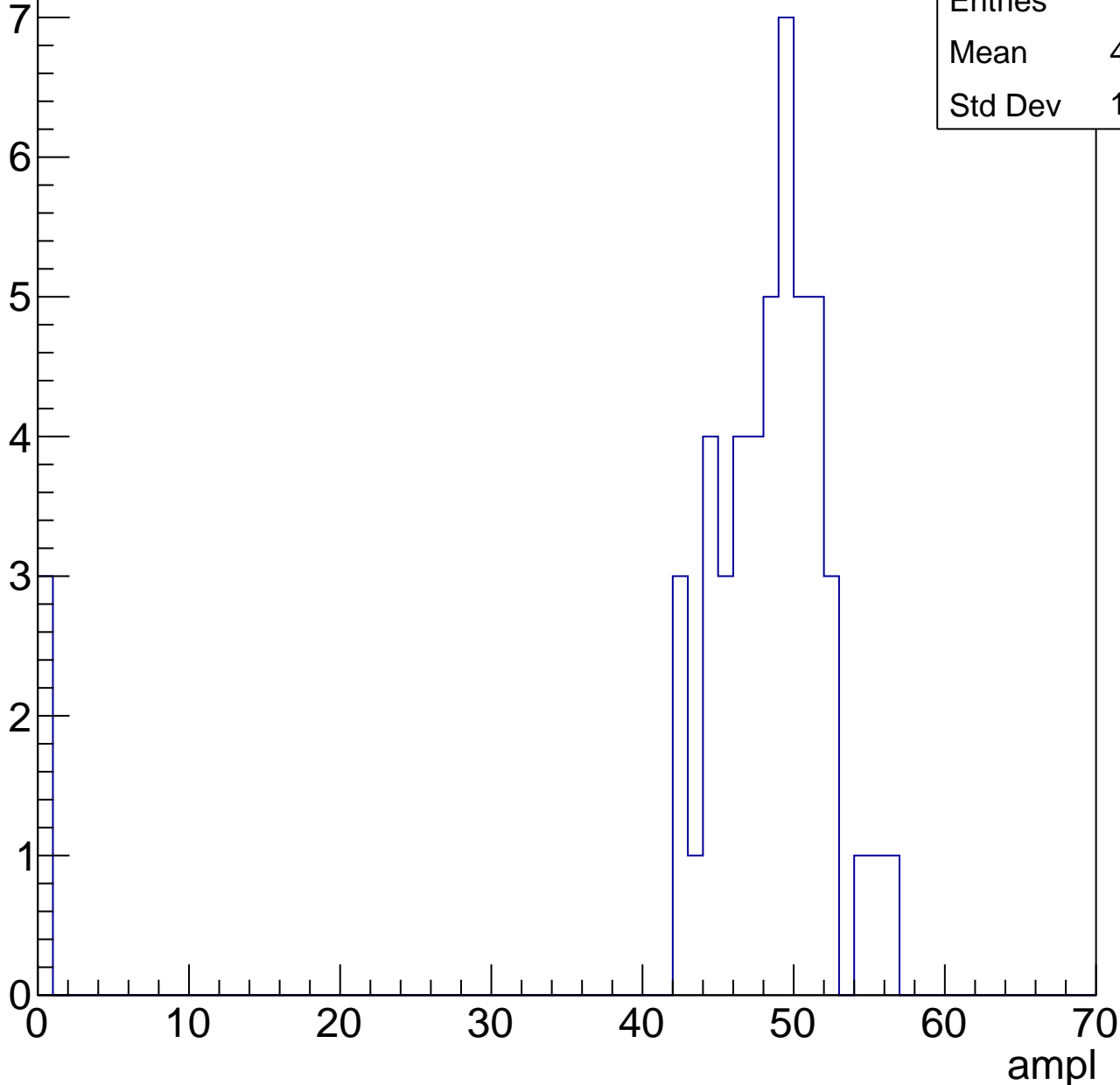


B1L103S, U8-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	45.22
Std Dev	11.86

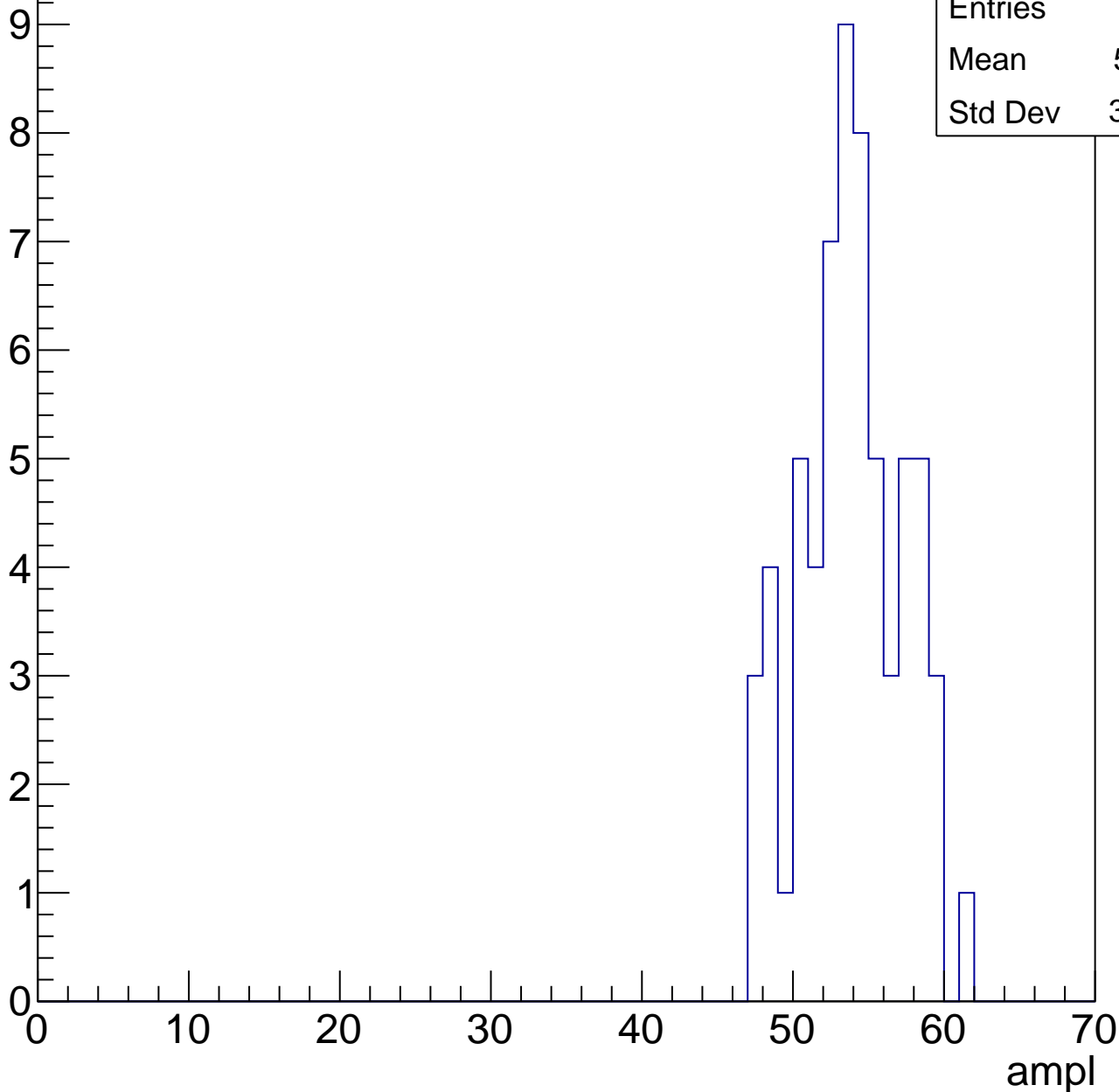


B1L103S, U8-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.41
Std Dev	3.365



B1L103S, U8-ch52, adc5

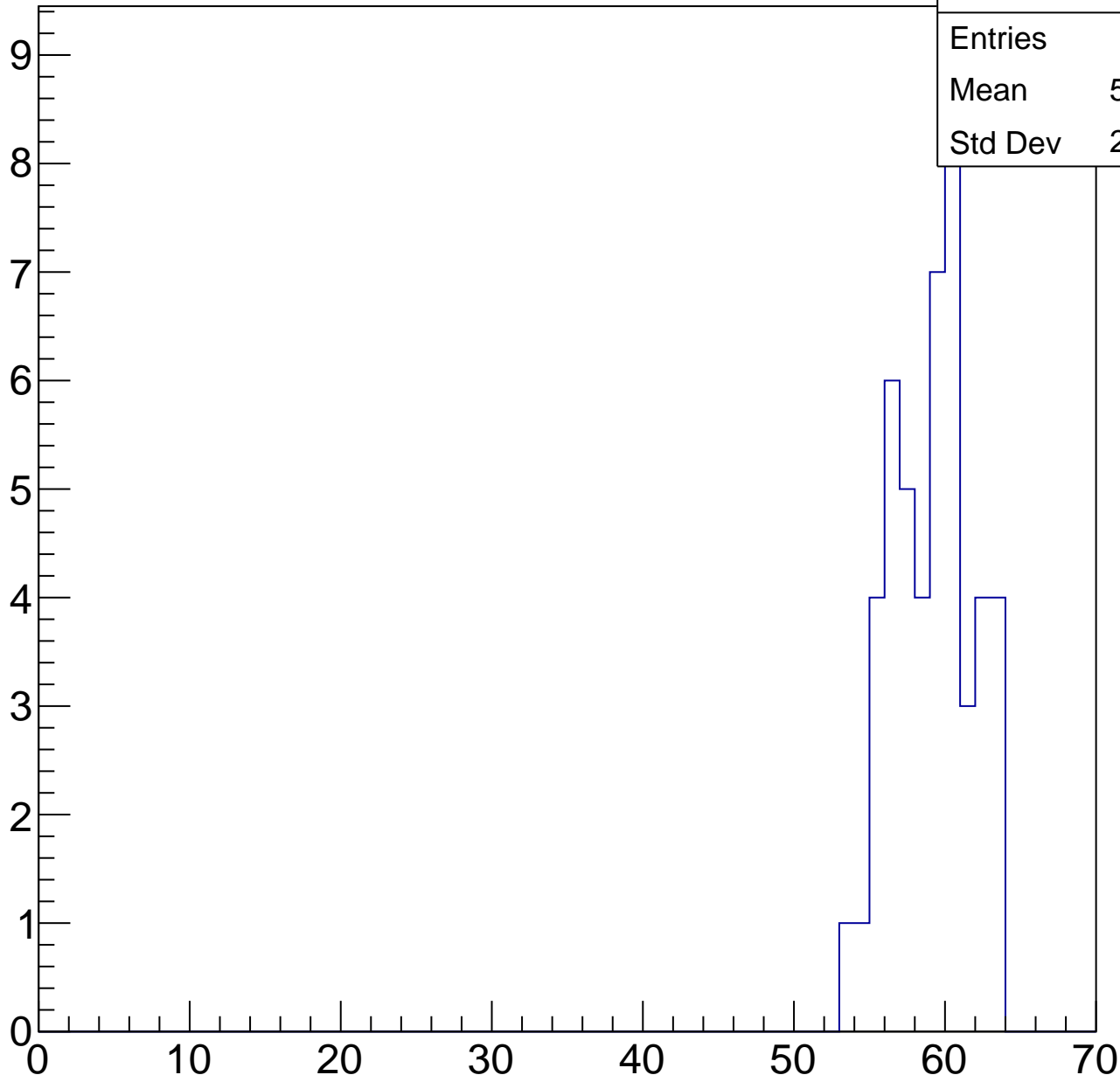
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.67
Std Dev	2.577

ampl

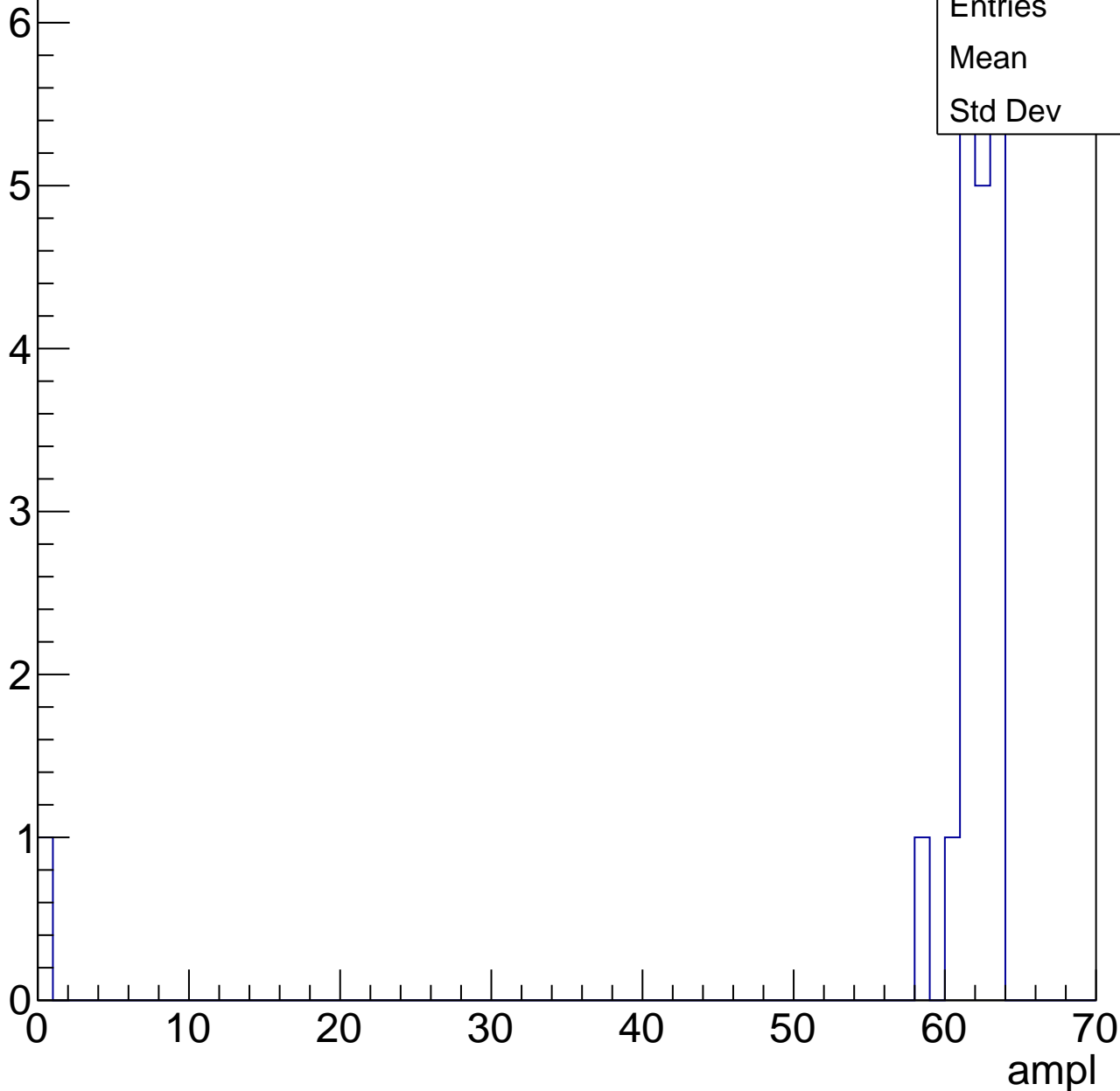


B1L103S, U8-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.6
Std Dev	13.5



B1L103S, U8-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U8-ch53, adc0

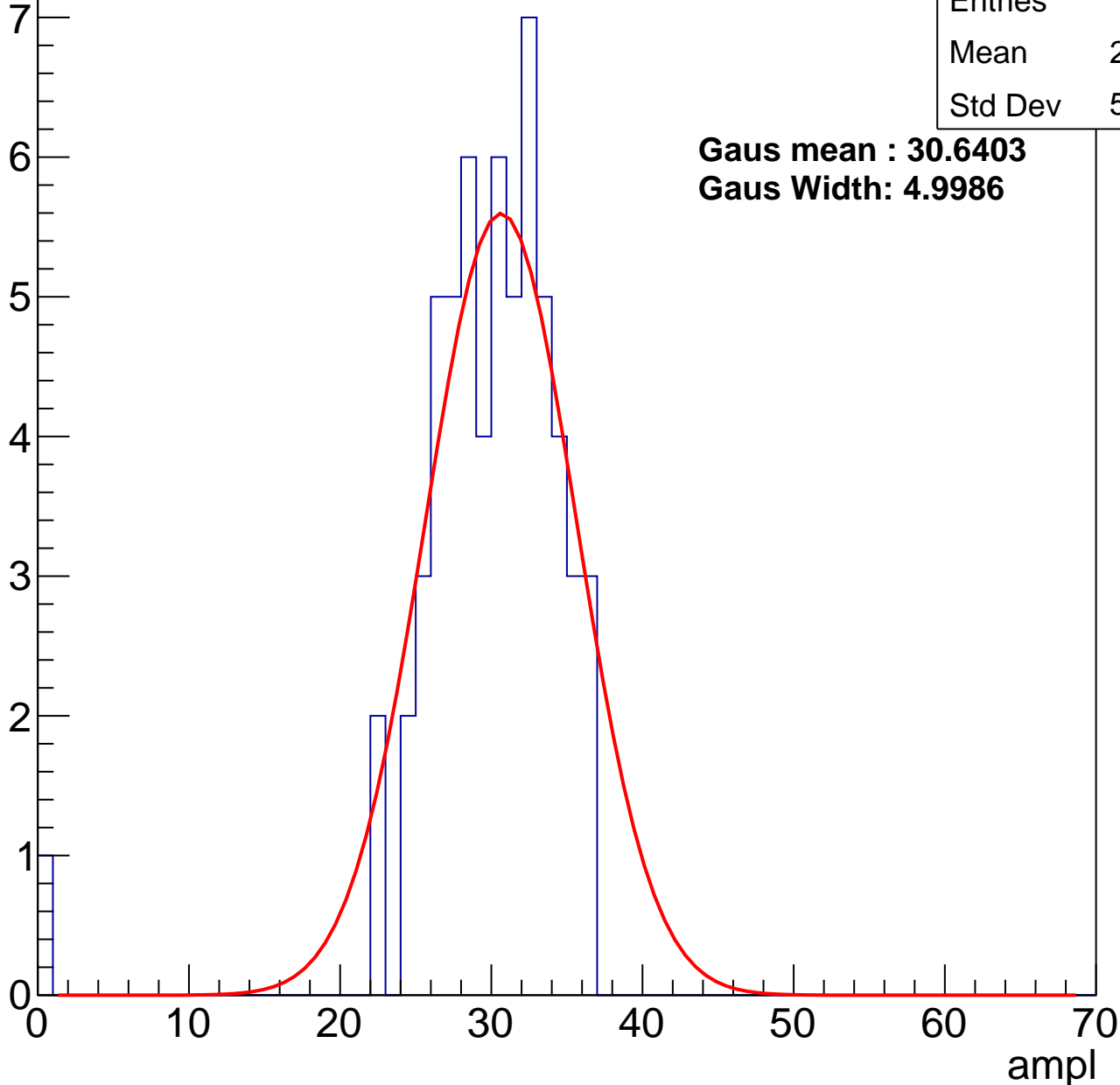
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	29.33
Std Dev	5.162

Gaus mean : 30.6403

Gaus Width: 4.9986

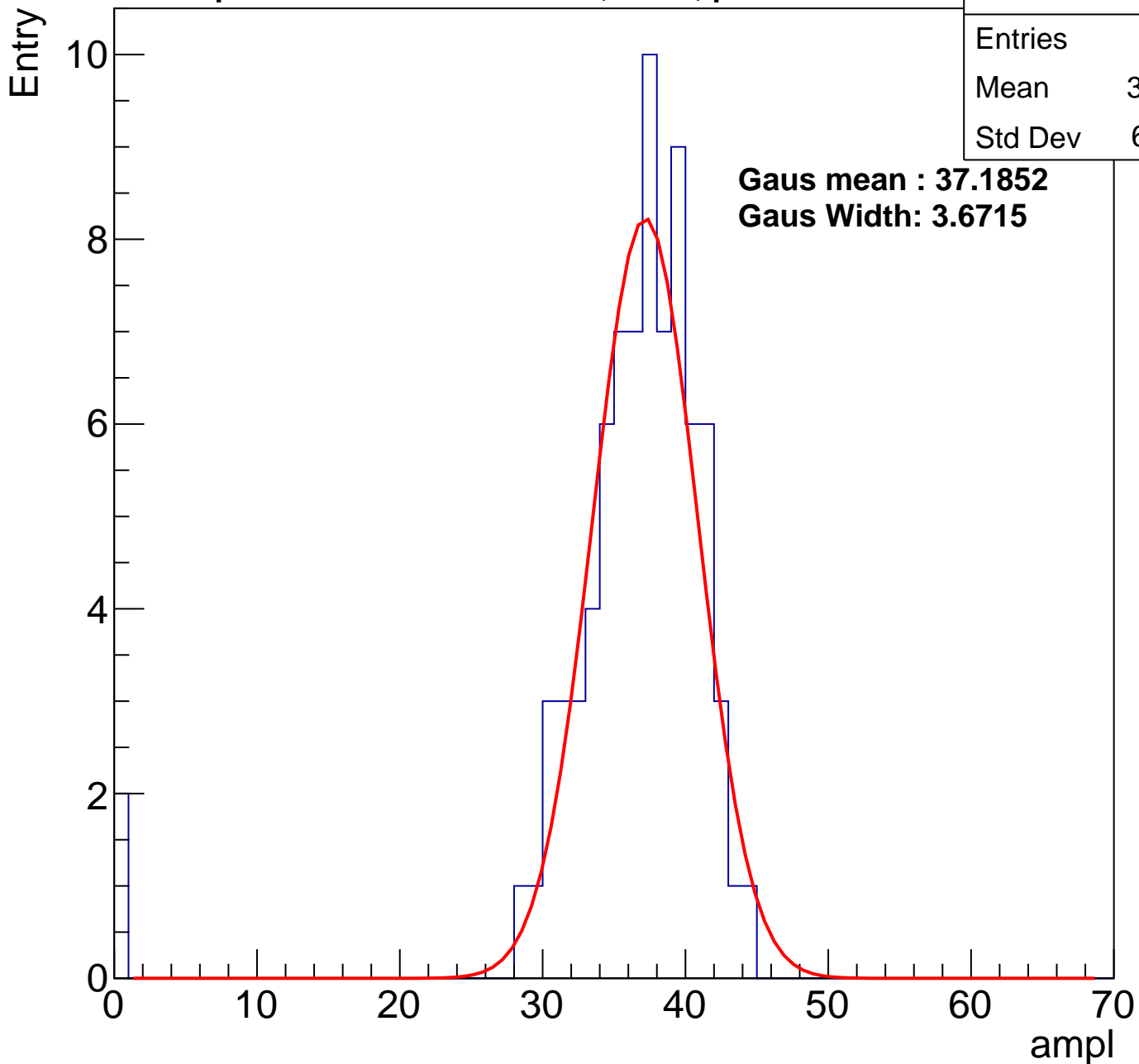


B1L103S, U8-ch53, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	35.69
Std Dev	6.691

Gaus mean : 37.1852
Gaus Width: 3.6715



B1L103S, U8-ch53, adc2

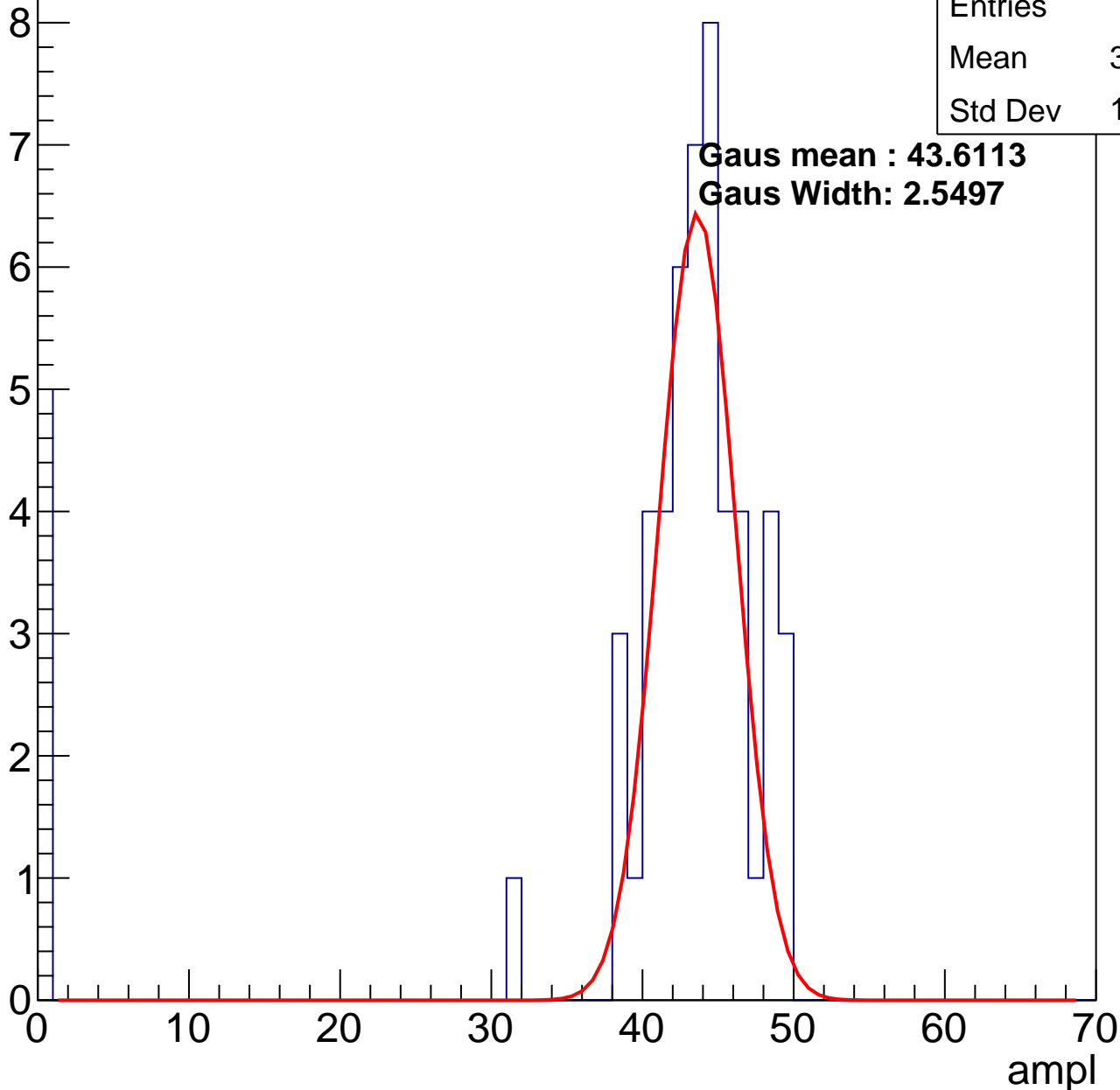
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	39.33
Std Dev	12.85

Gaus mean : 43.6113

Gaus Width: 2.5497

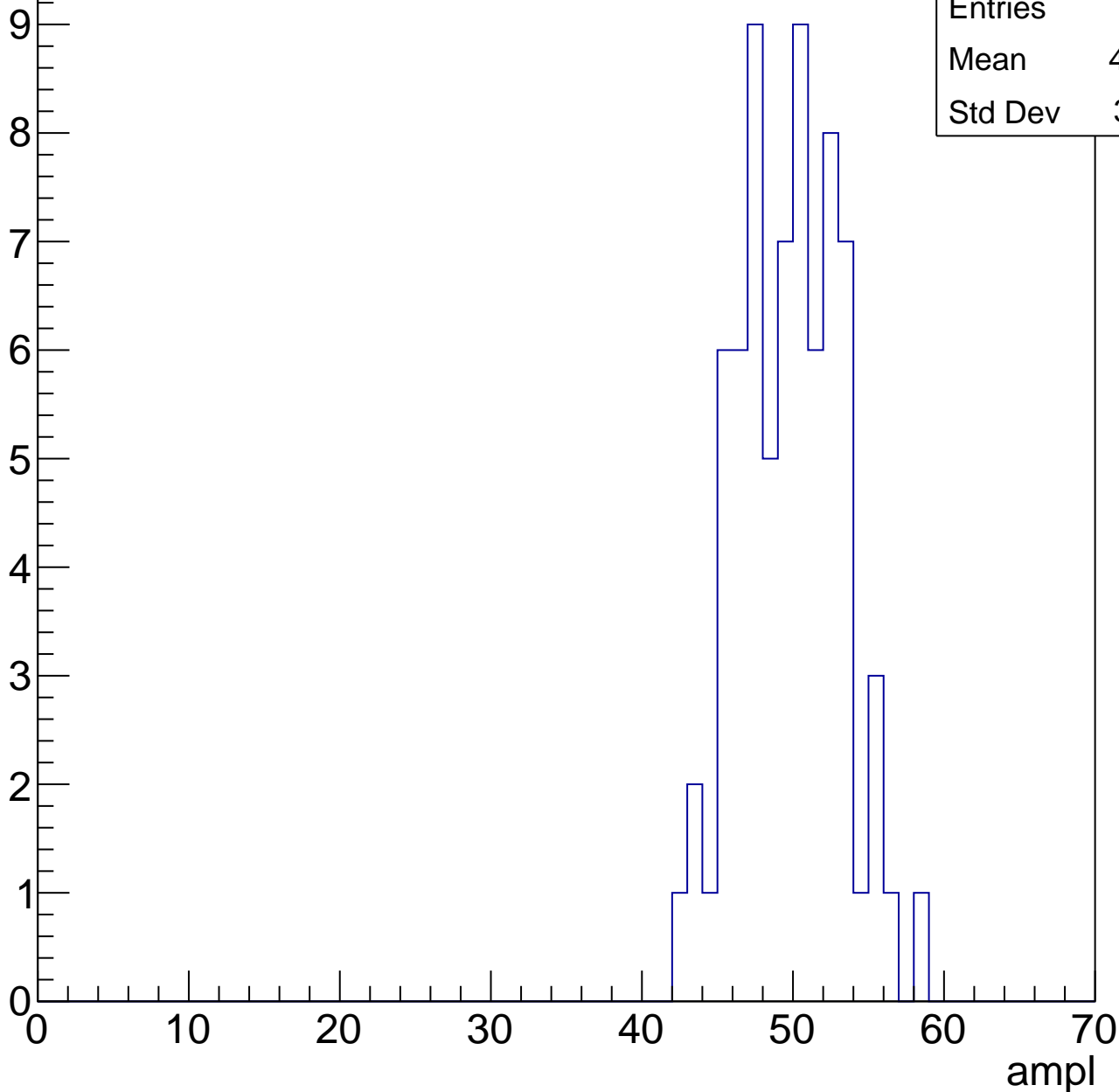


B1L103S, U8-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	49.32
Std Dev	3.331

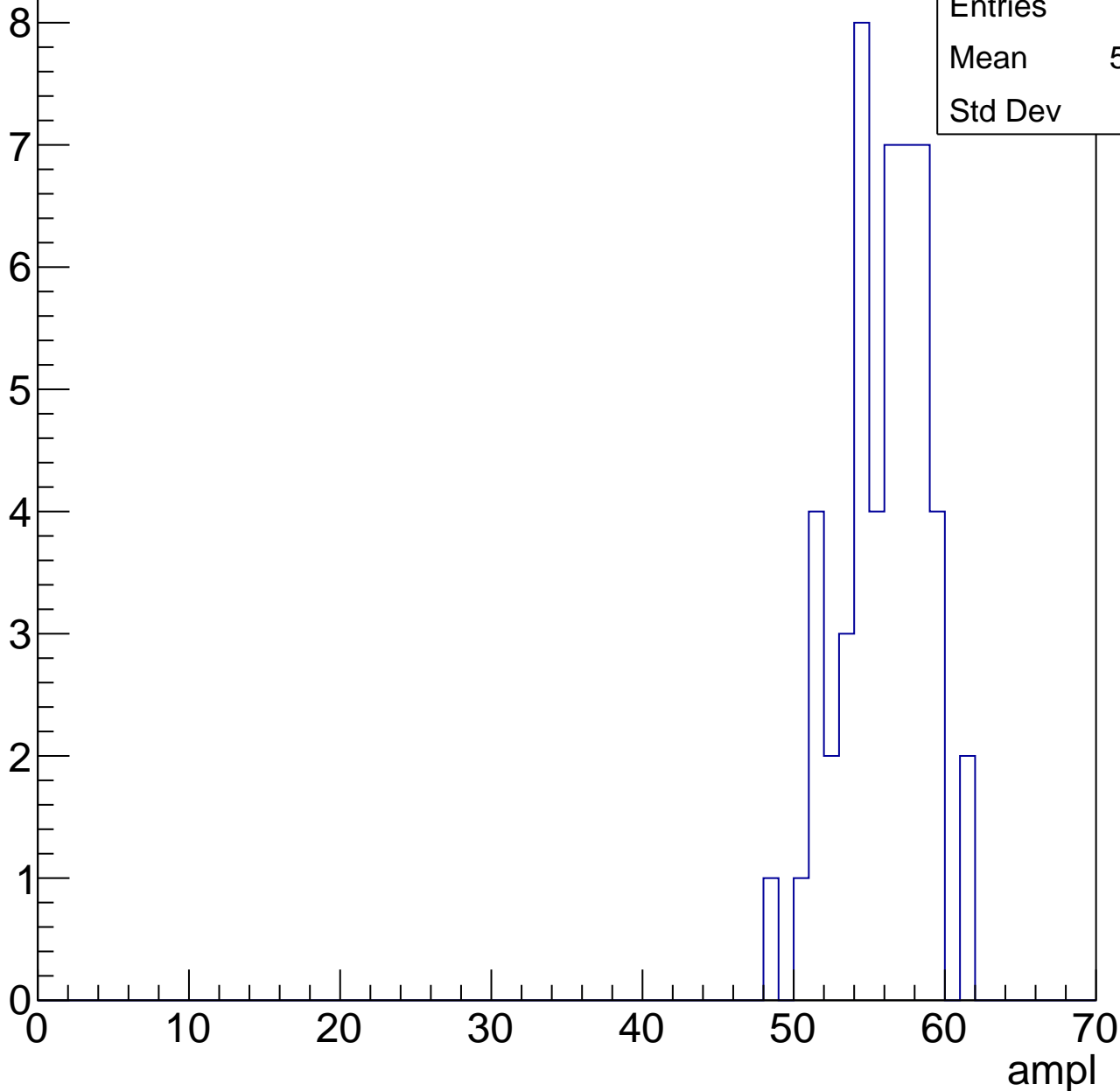


B1L103S, U8-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.44
Std Dev	2.83



B1L103S, U8-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

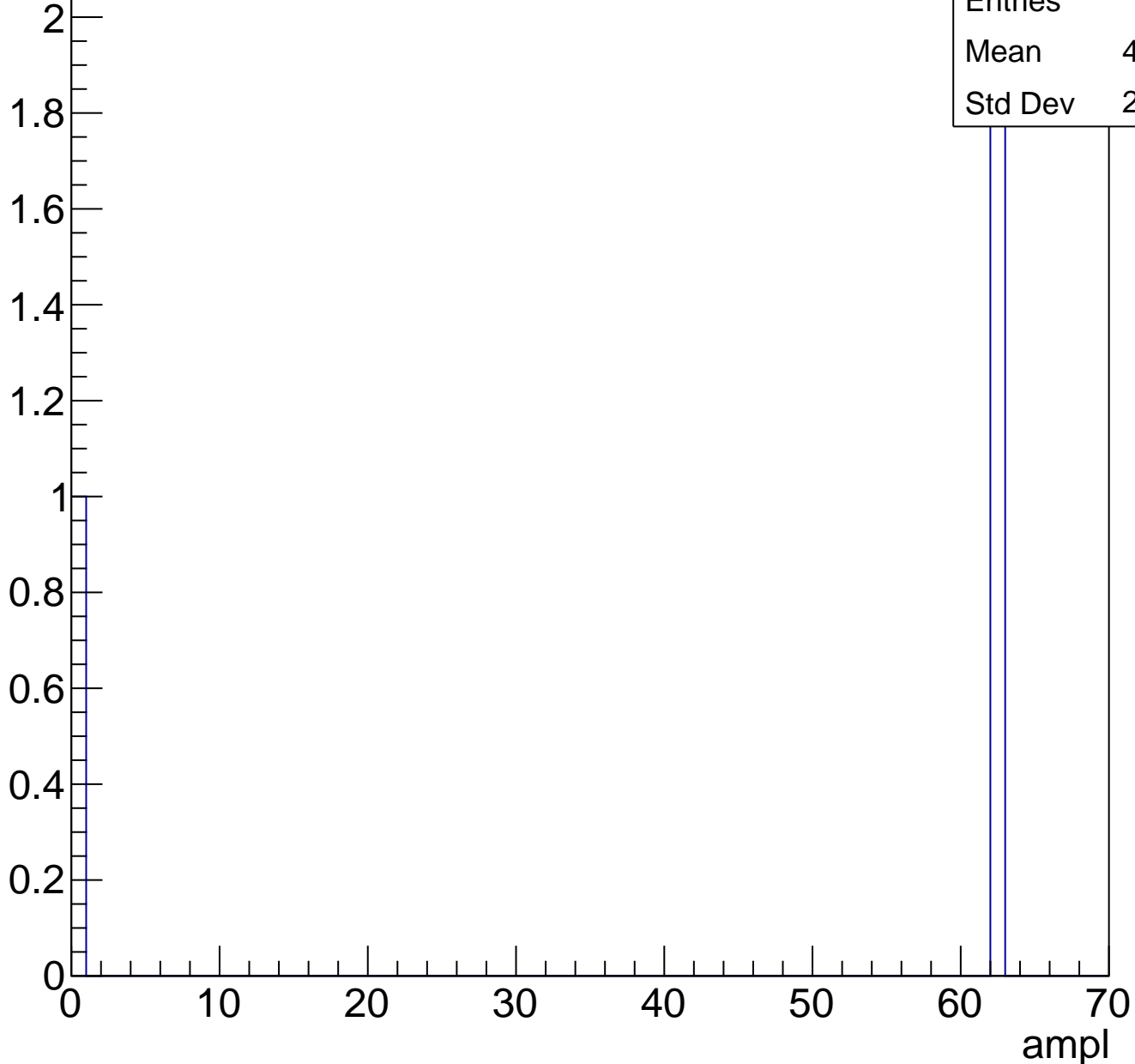
ampl

Entries	53
Mean	60.11
Std Dev	2.431

B1L103S, U8-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U8-ch54, adc0

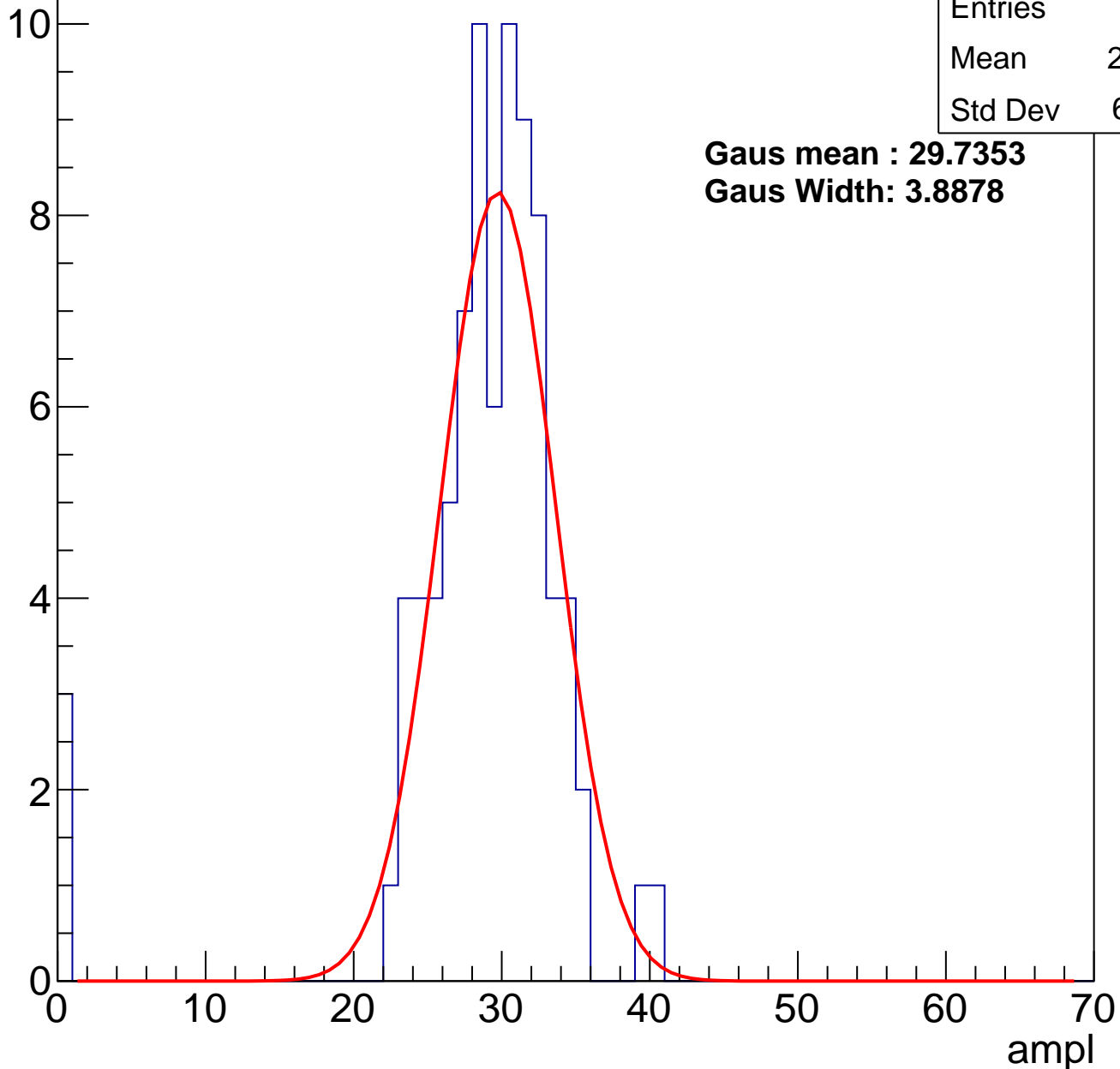
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	28.13
Std Dev	6.471

Gaus mean : 29.7353

Gaus Width: 3.8878

Entry



B1L103S, U8-ch54, adc1

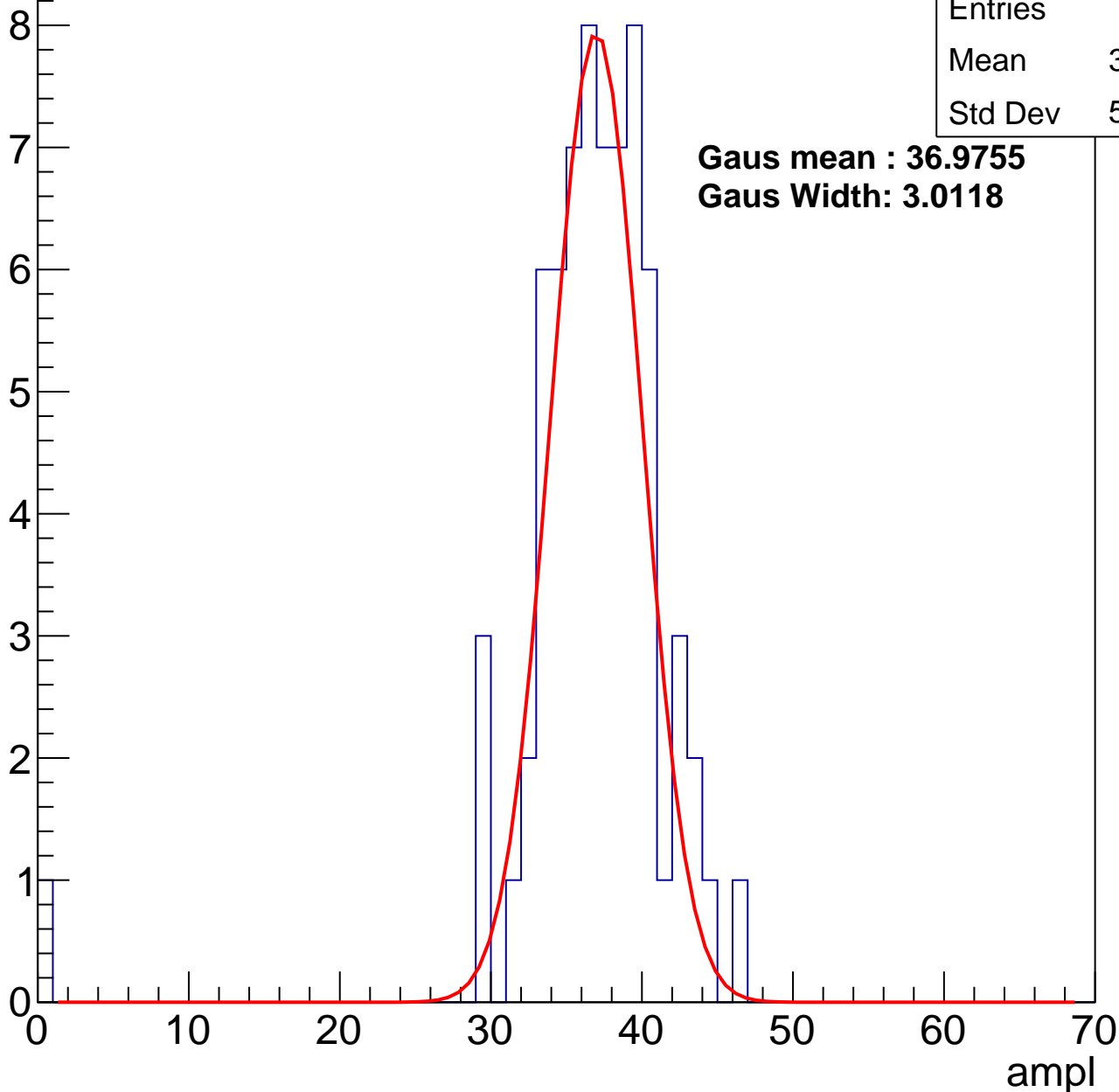
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.24
Std Dev	5.579

Gaus mean : 36.9755

Gaus Width: 3.0118



B1L103S, U8-ch54, adc2

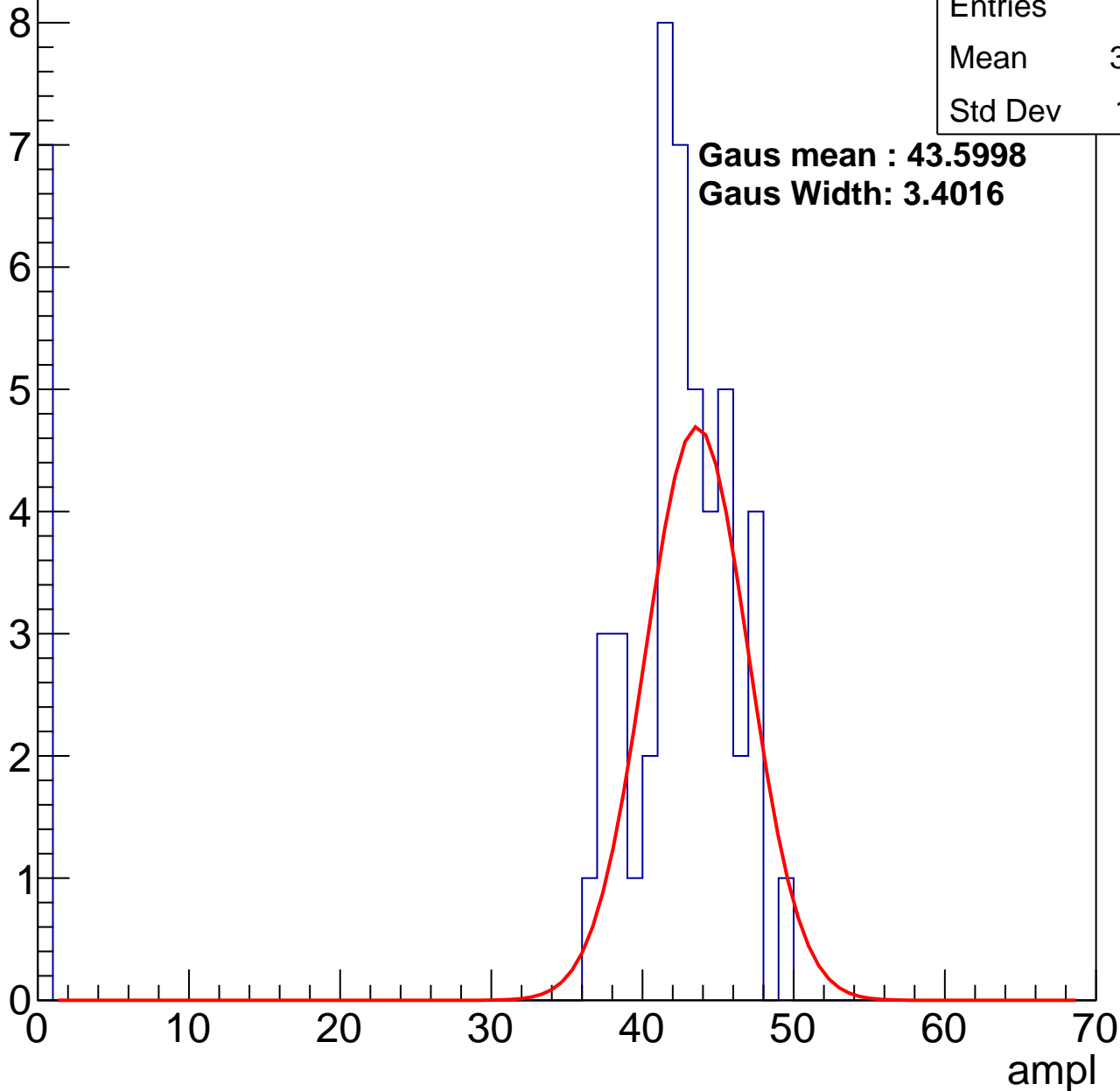
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	36.74
Std Dev	14.61

Gaus mean : 43.5998

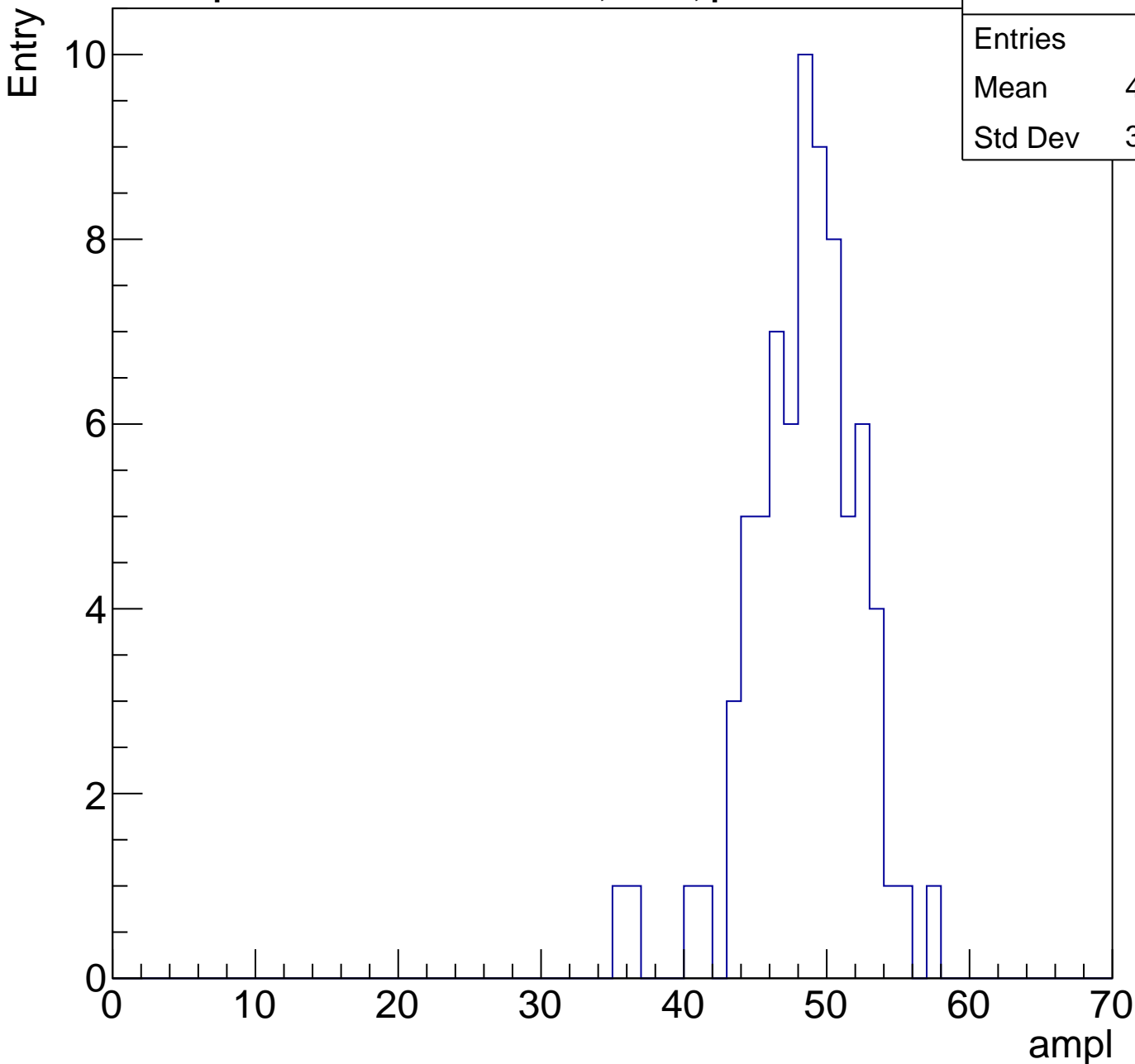
Gaus Width: 3.4016



B1L103S, U8-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	47.95
Std Dev	3.843

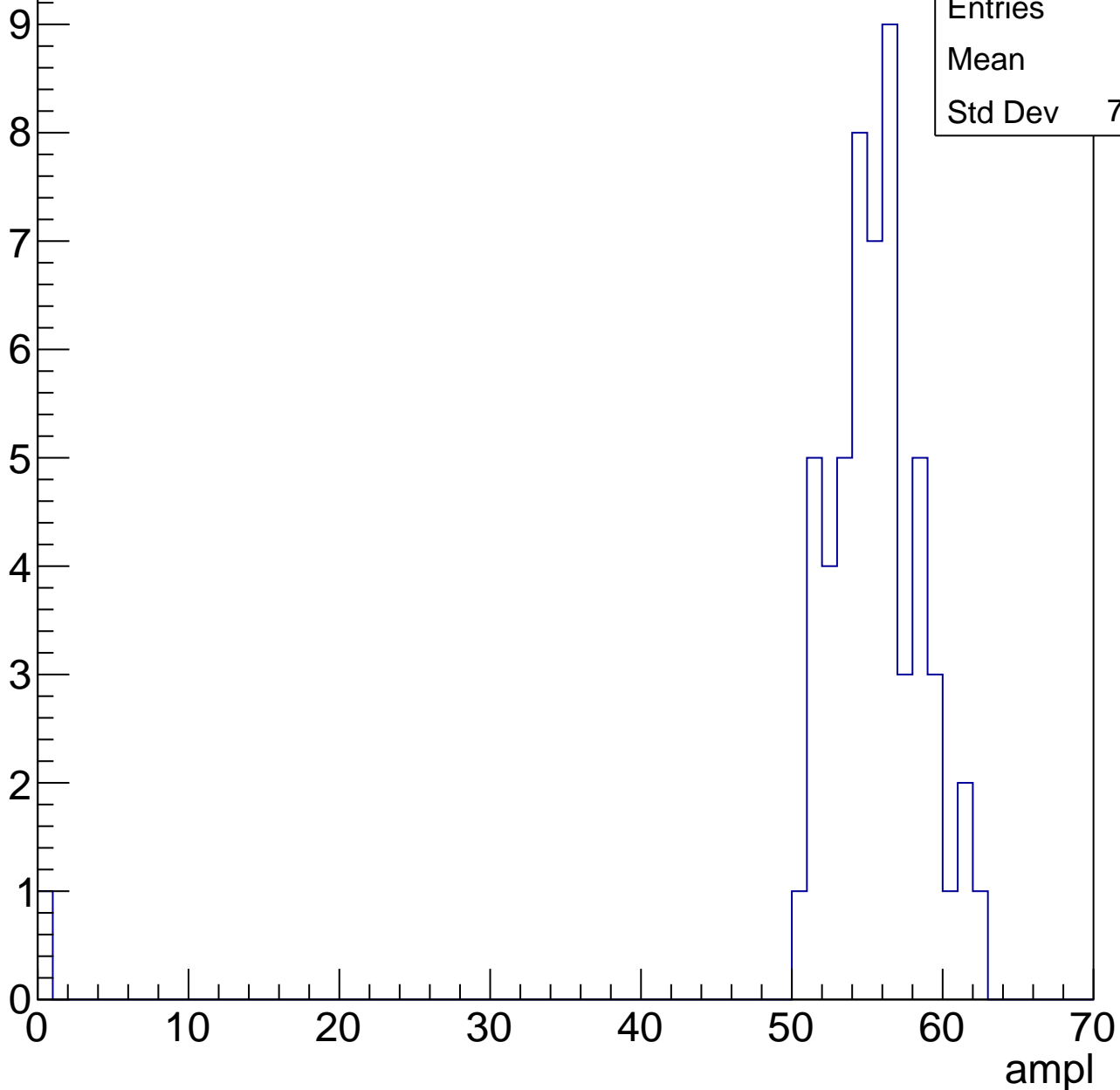


B1L103S, U8-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.2
Std Dev	7.884



B1L103S, U8-ch54, adc5

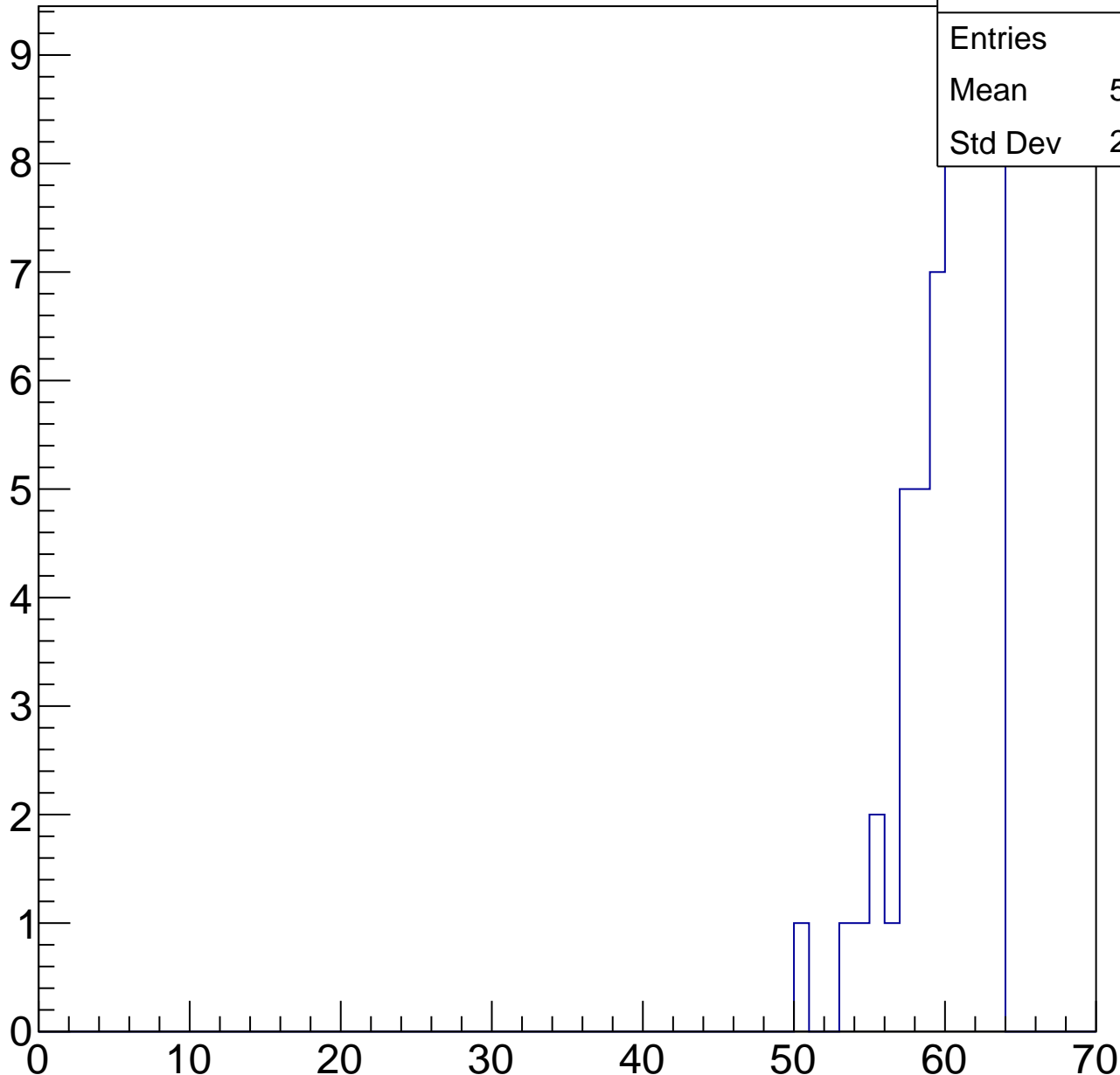
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	59.62
Std Dev	2.762

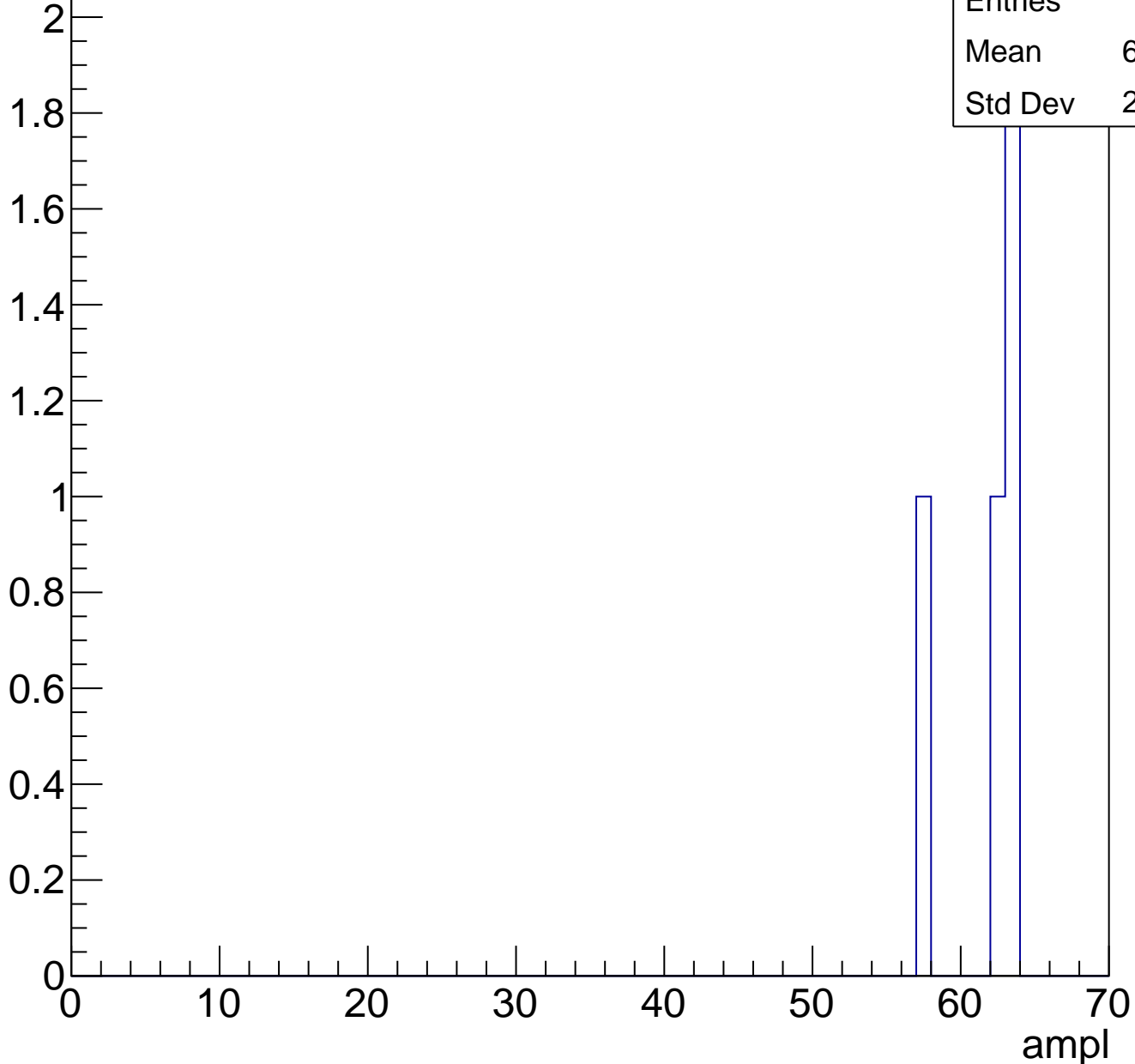
ampl



B1L103S, U8-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



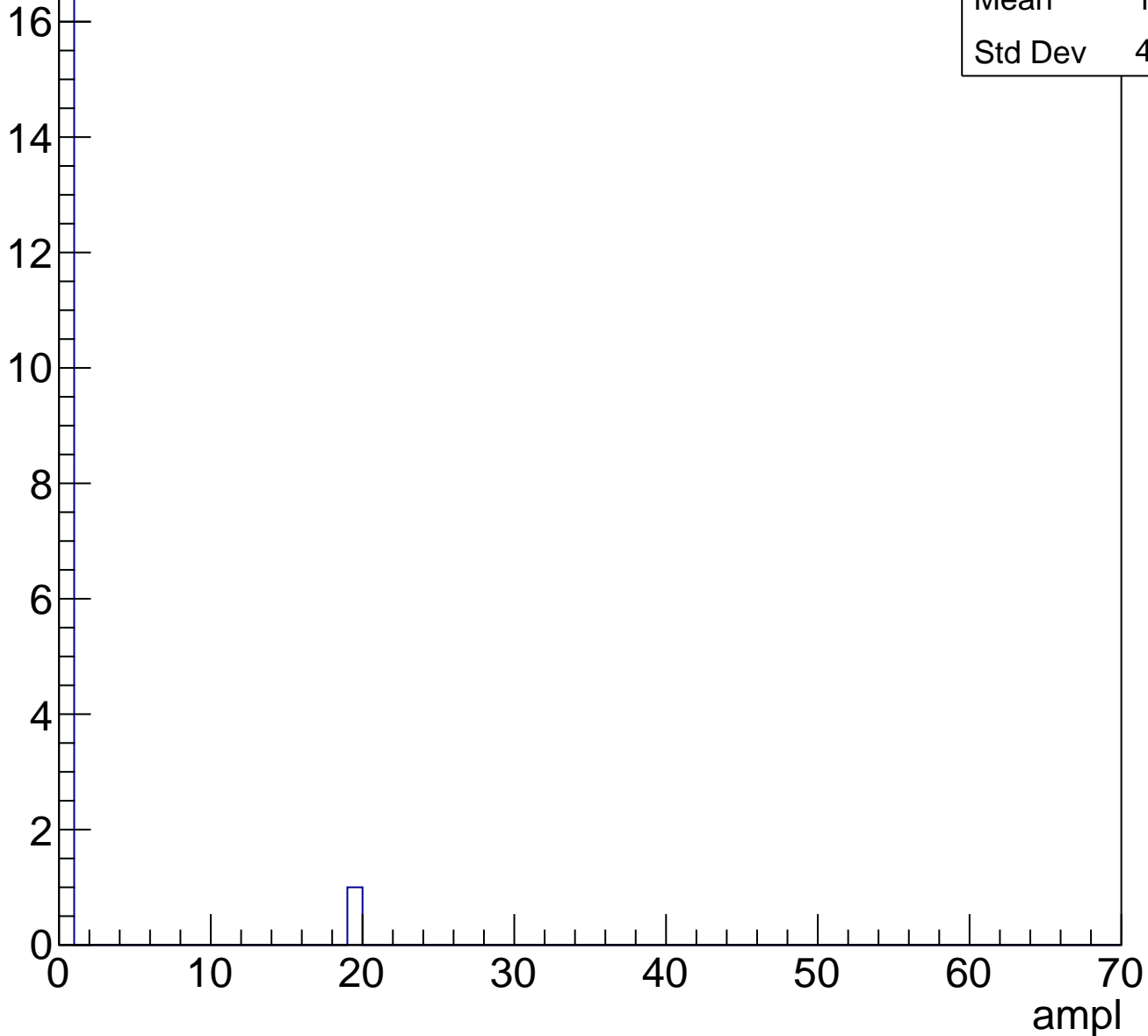
Entries	4
Mean	61.25
Std Dev	2.487

B1L103S, U8-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.056
Std Dev	4.352

Entry



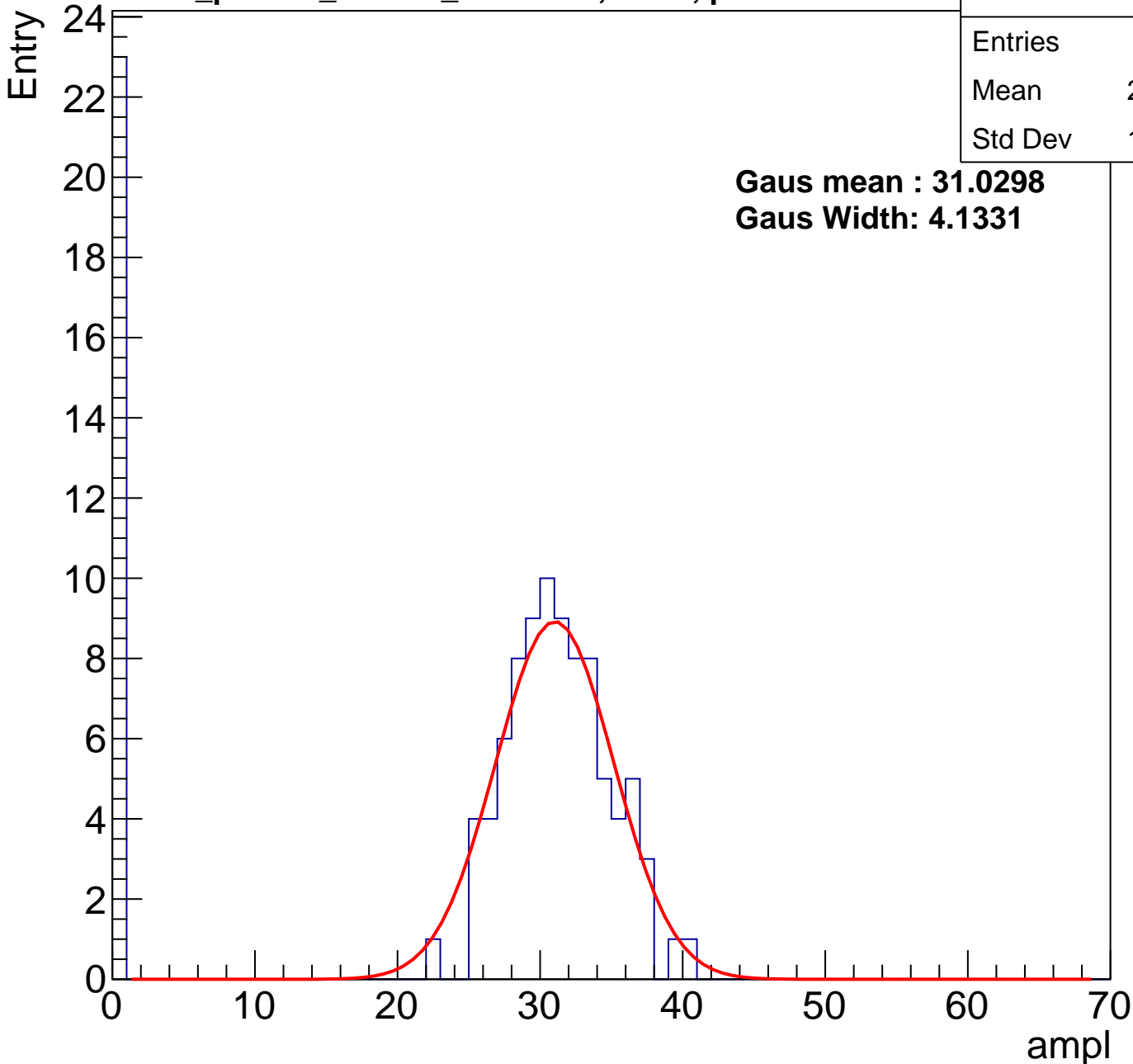
B1L103S, U8-ch55, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	24.33
Std Dev	12.96

Gaus mean : 31.0298

Gaus Width: 4.1331



B1L103S, U8-ch55, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	36.71
Std Dev	7.349

Gaus mean : 38.4114

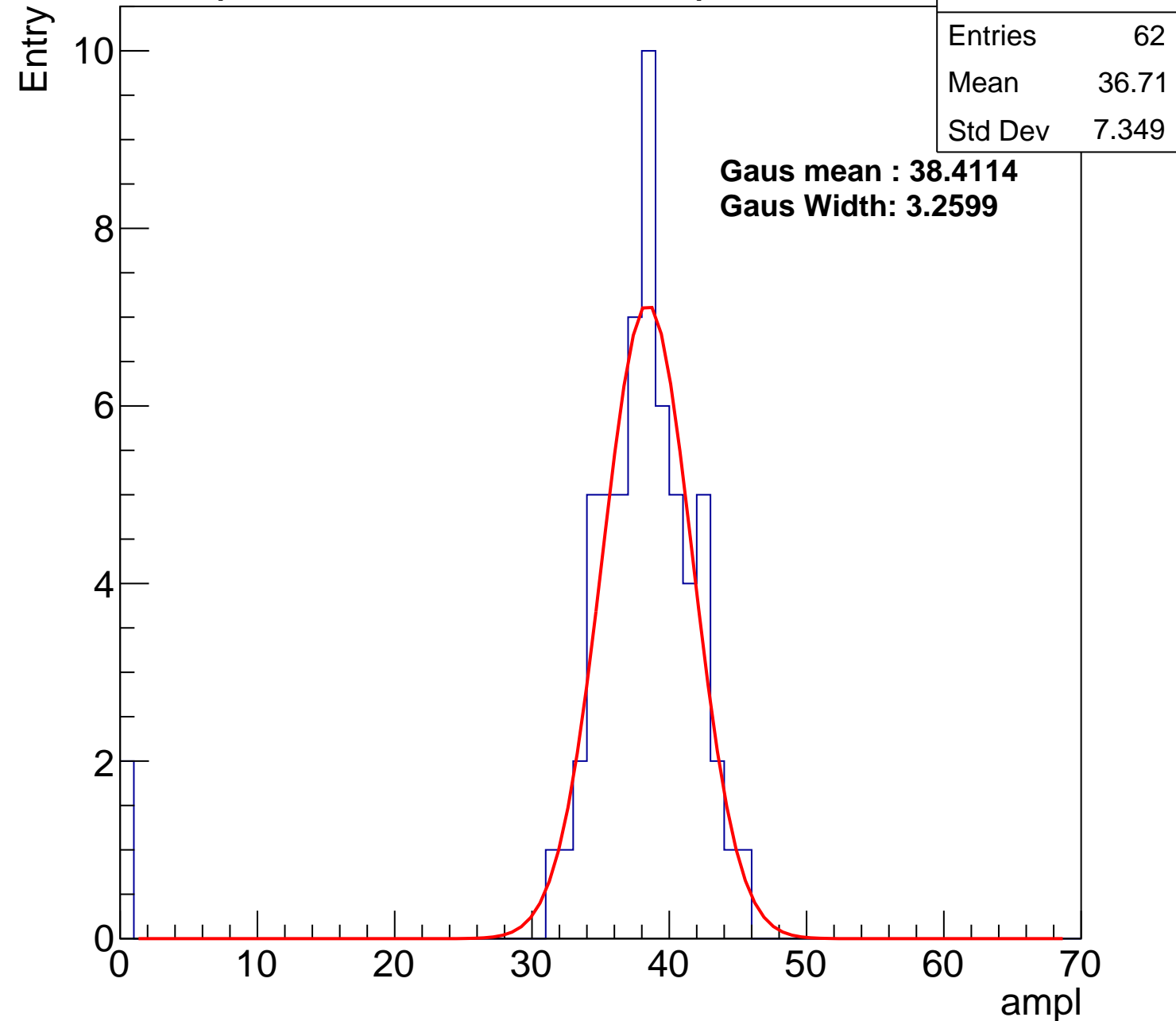
Gaus Width: 3.2599

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch55, adc2

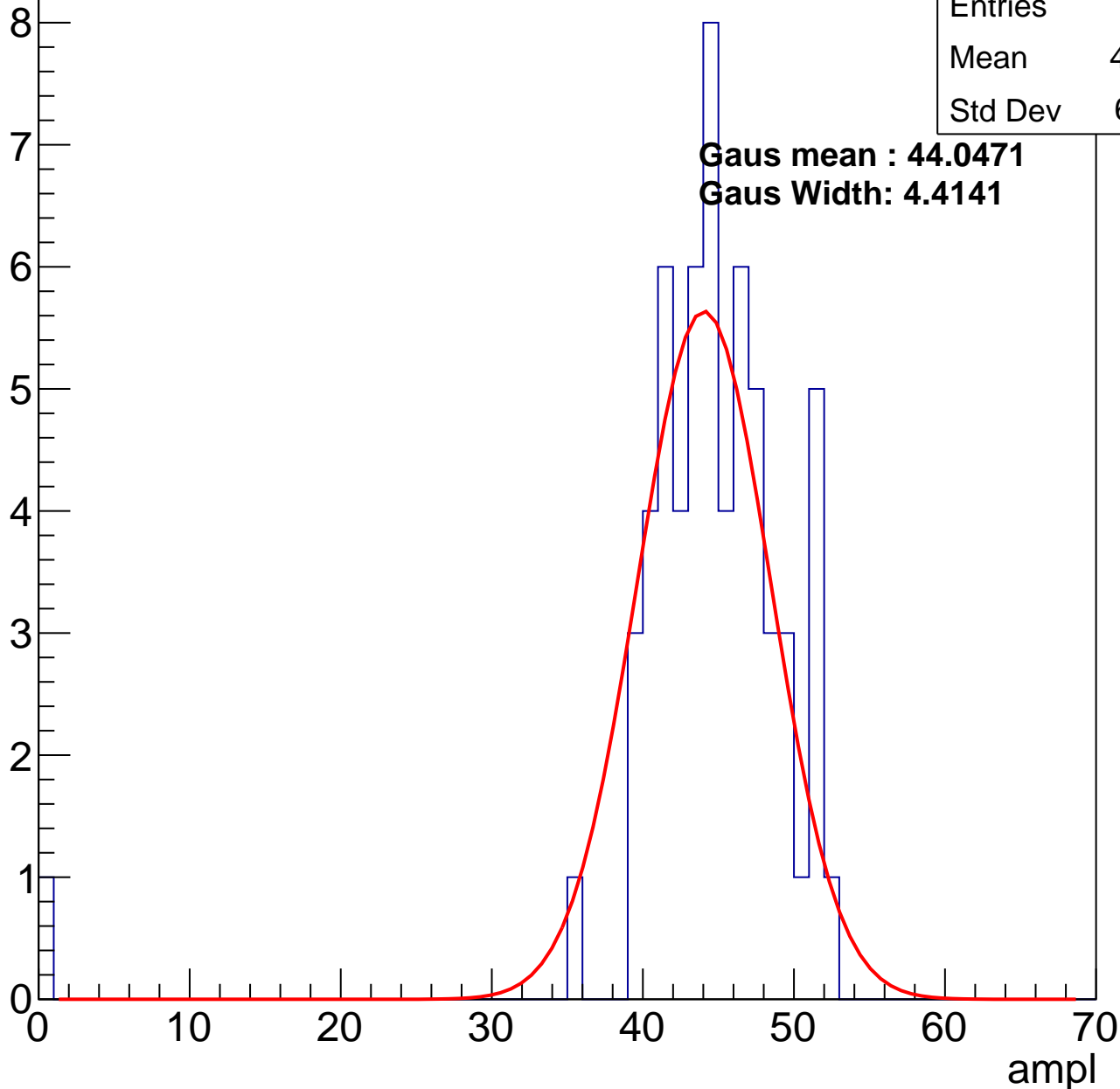
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	43.85
Std Dev	6.741

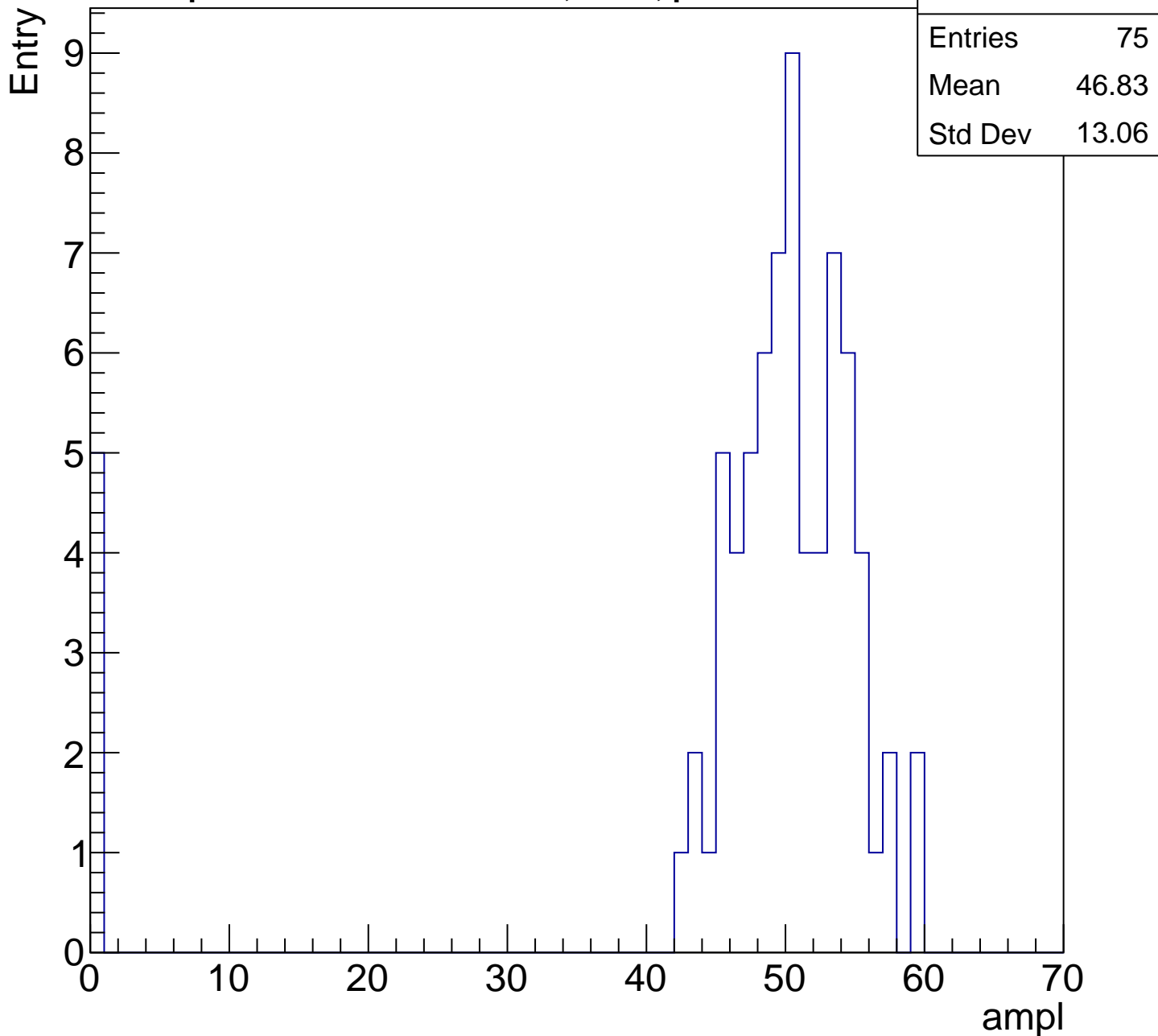
Gaus mean : 44.0471

Gaus Width: 4.4141



B1L103S, U8-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

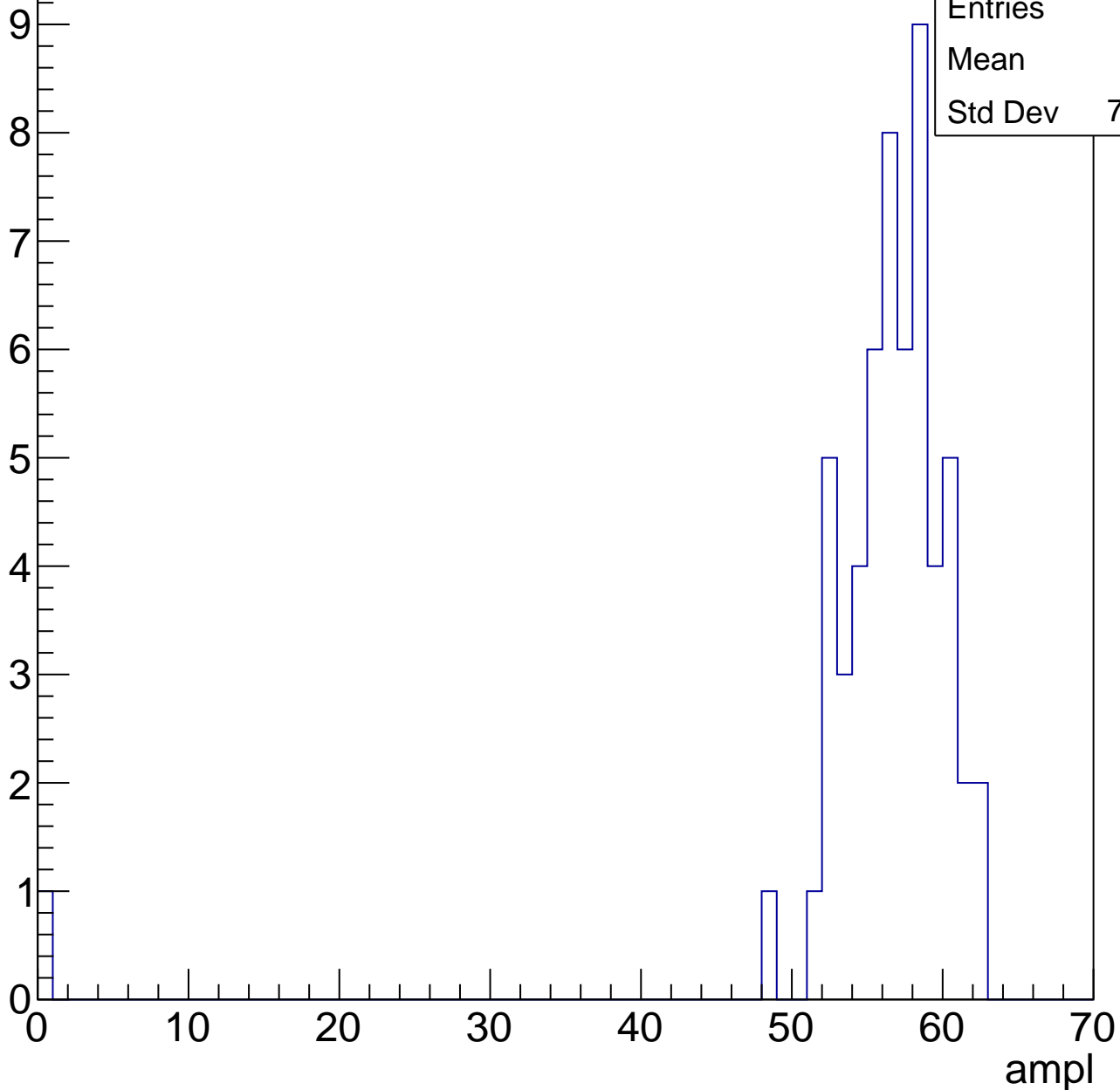


B1L103S, U8-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.4
Std Dev	7.962

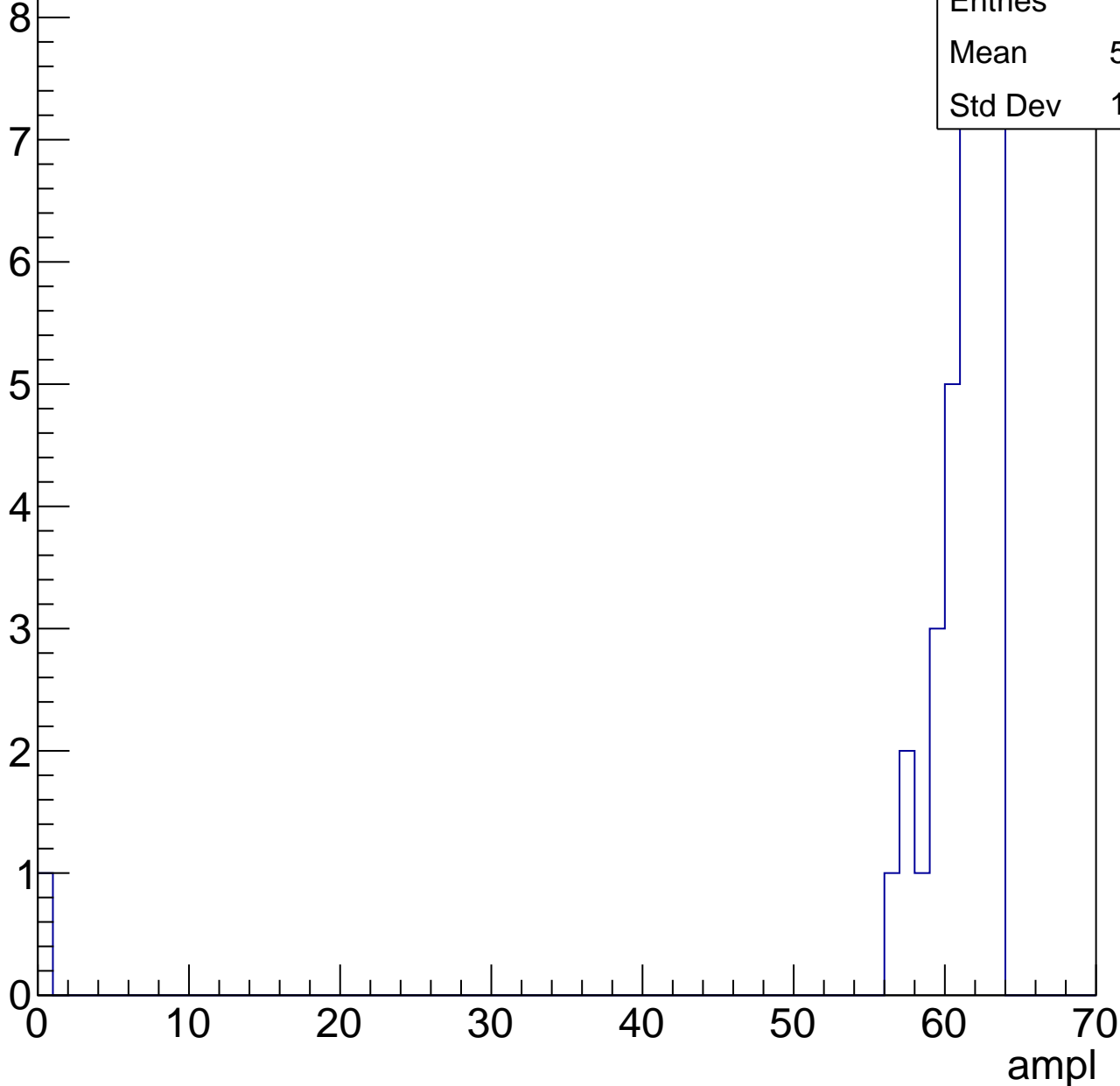


B1L103S, U8-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

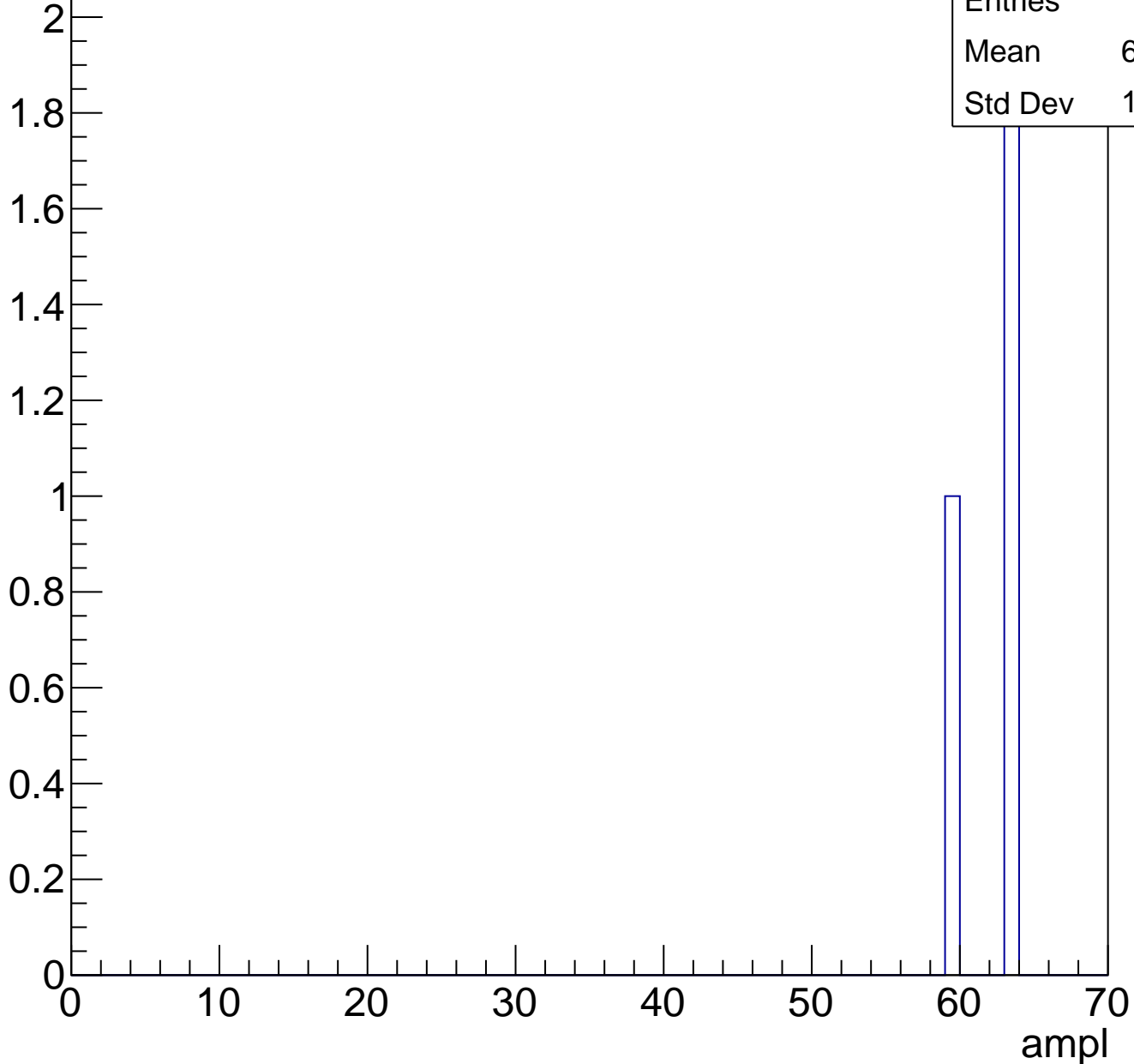
Entries	37
Mean	59.27
Std Dev	10.04



B1L103S, U8-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

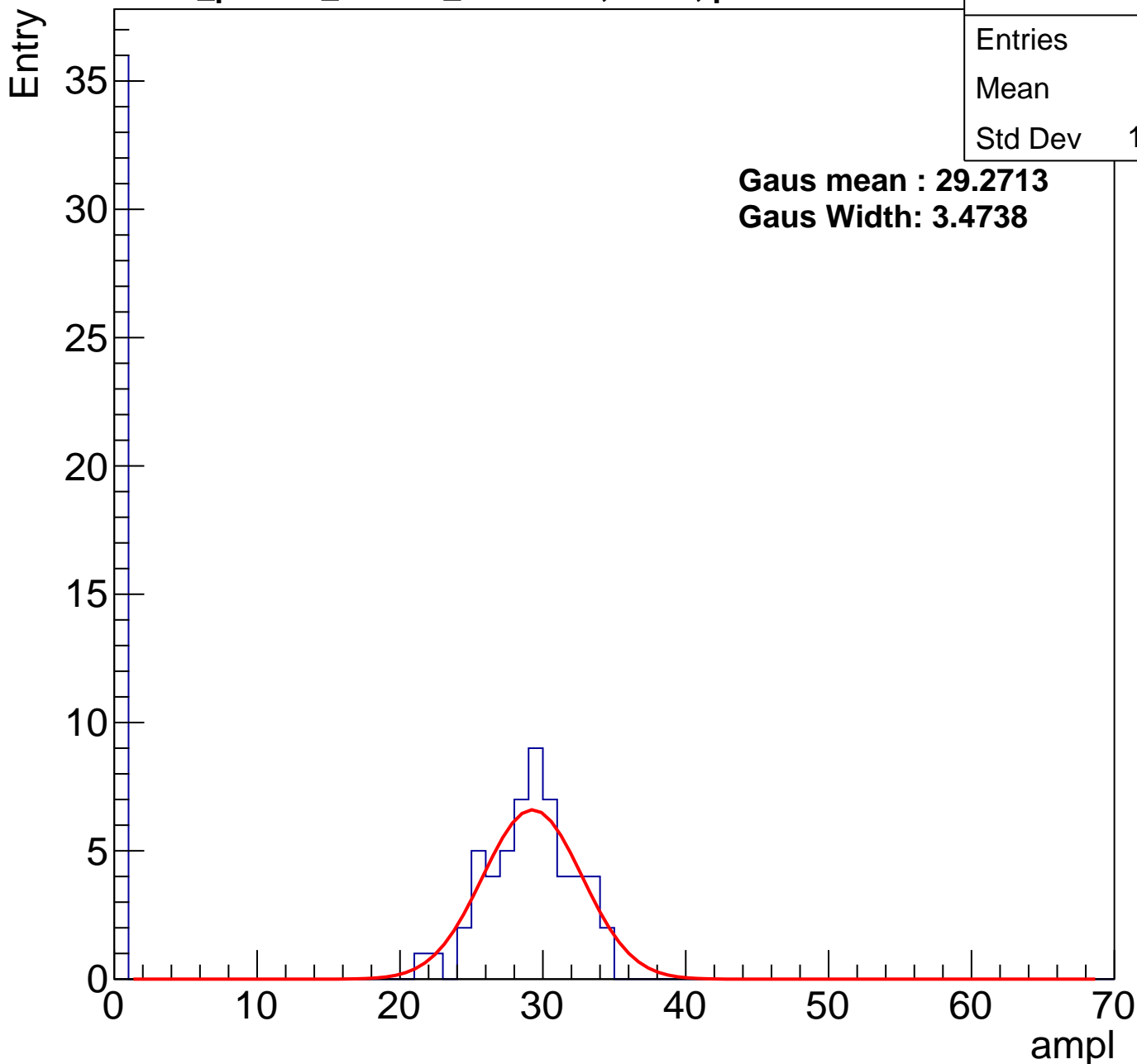
B1L103S, U8-ch56, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	17.3
Std Dev	14.18

Gaus mean : 29.2713

Gaus Width: 3.4738



B1L103S, U8-ch56, adc1

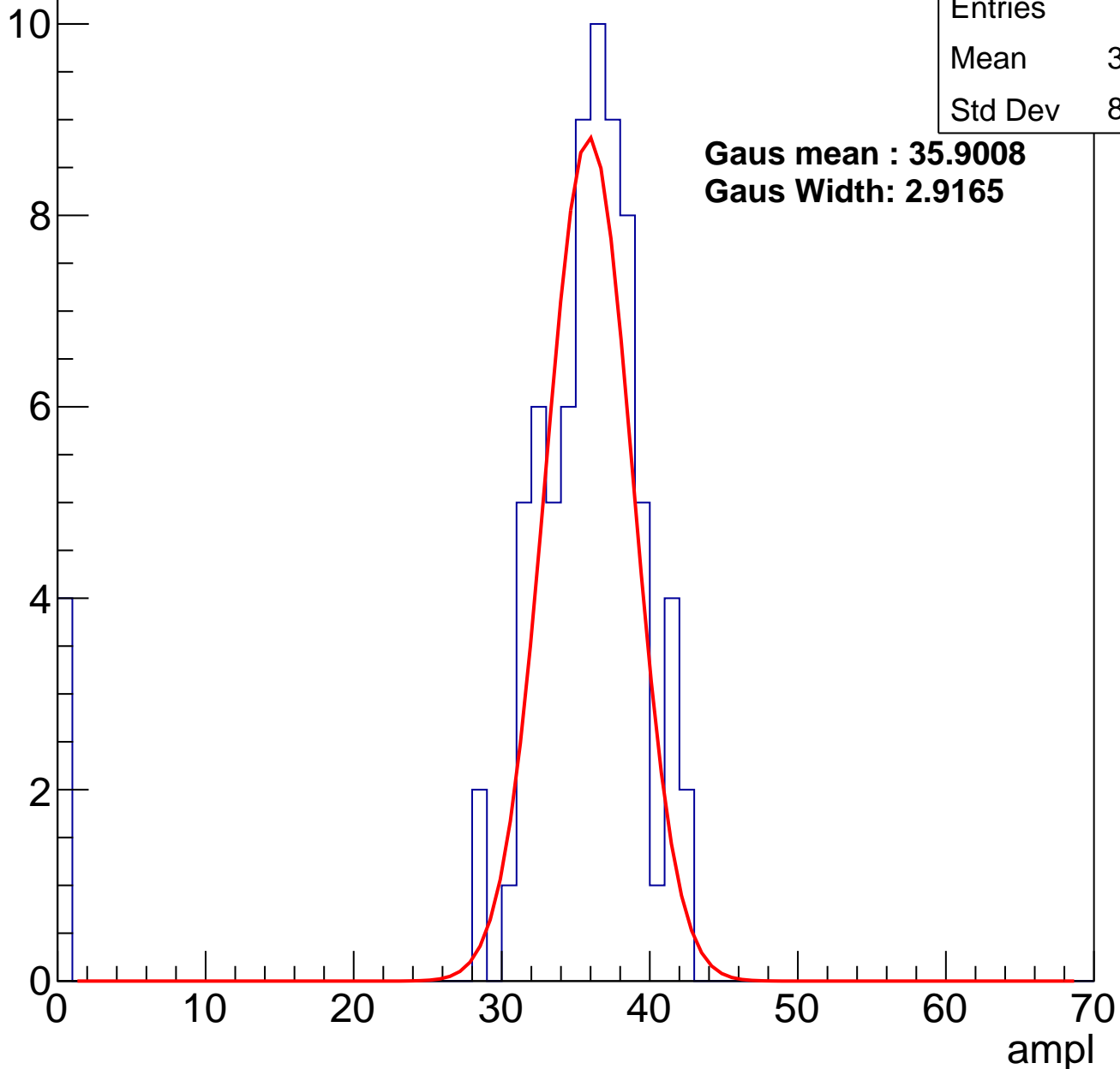
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	33.73
Std Dev	8.469

Gaus mean : 35.9008

Gaus Width: 2.9165

Entry



B1L103S, U8-ch56, adc2

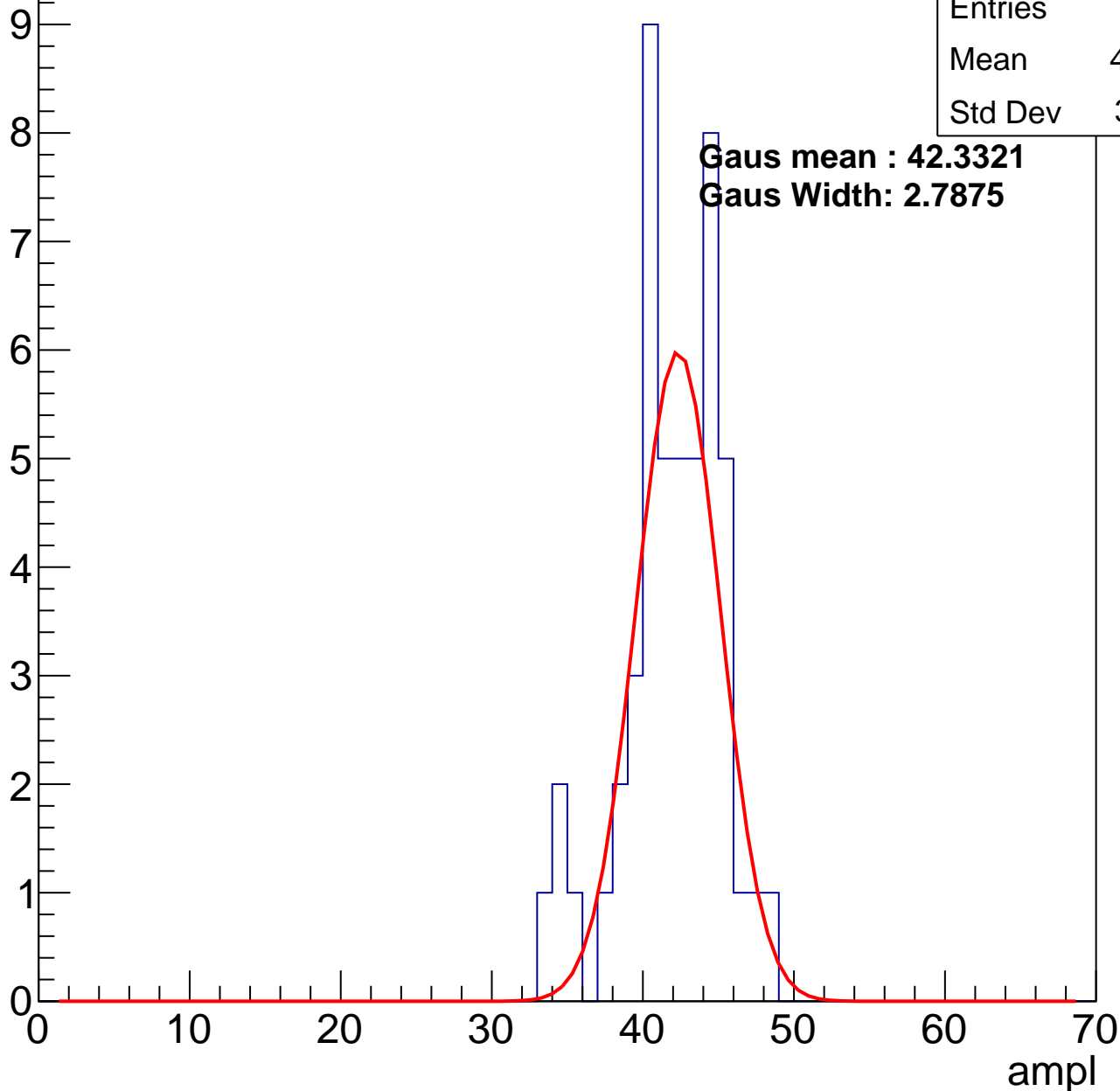
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	41.48
Std Dev	3.251

Gaus mean : 42.3321

Gaus Width: 2.7875

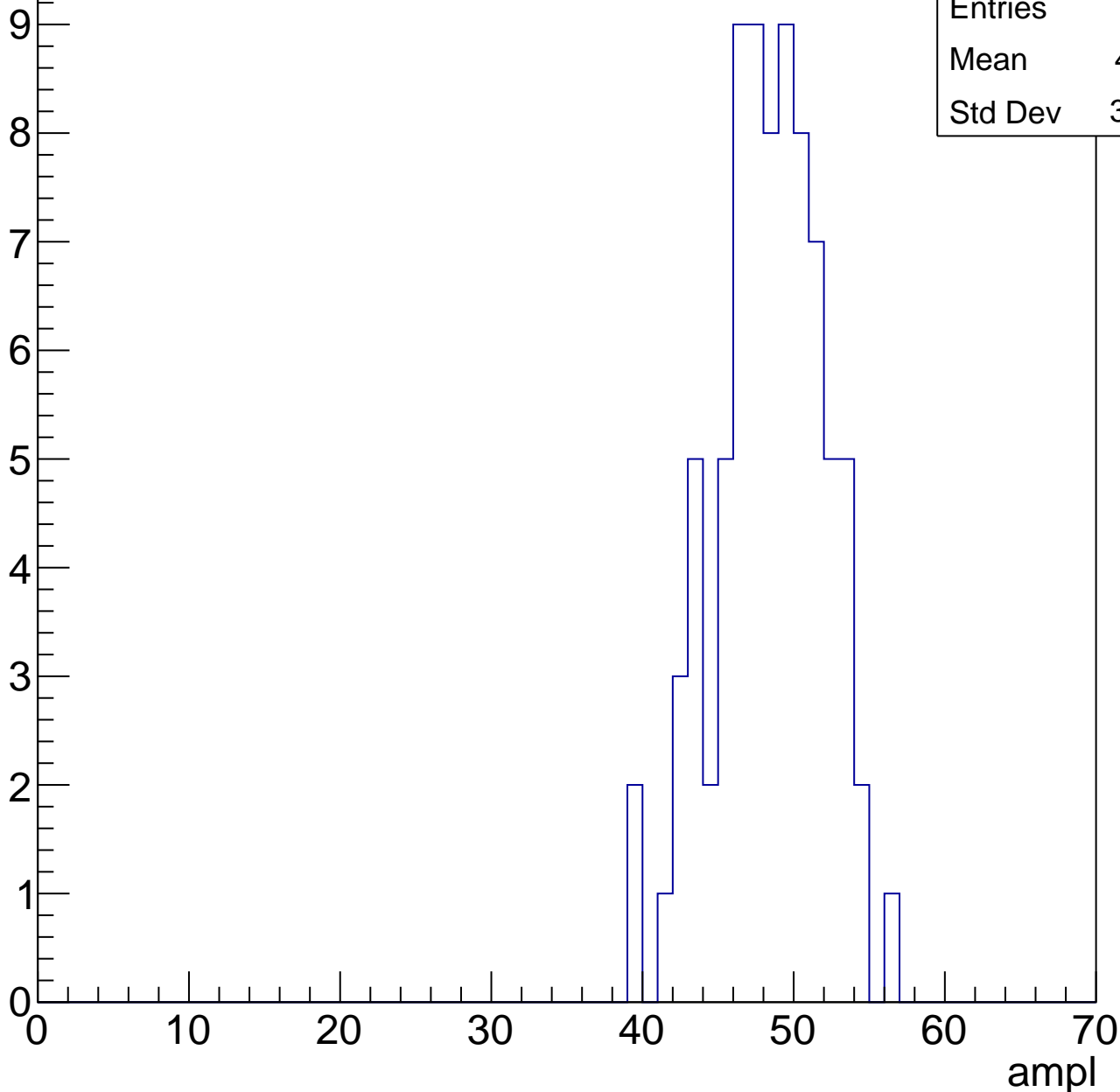


B1L103S, U8-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	47.91
Std Dev	3.532

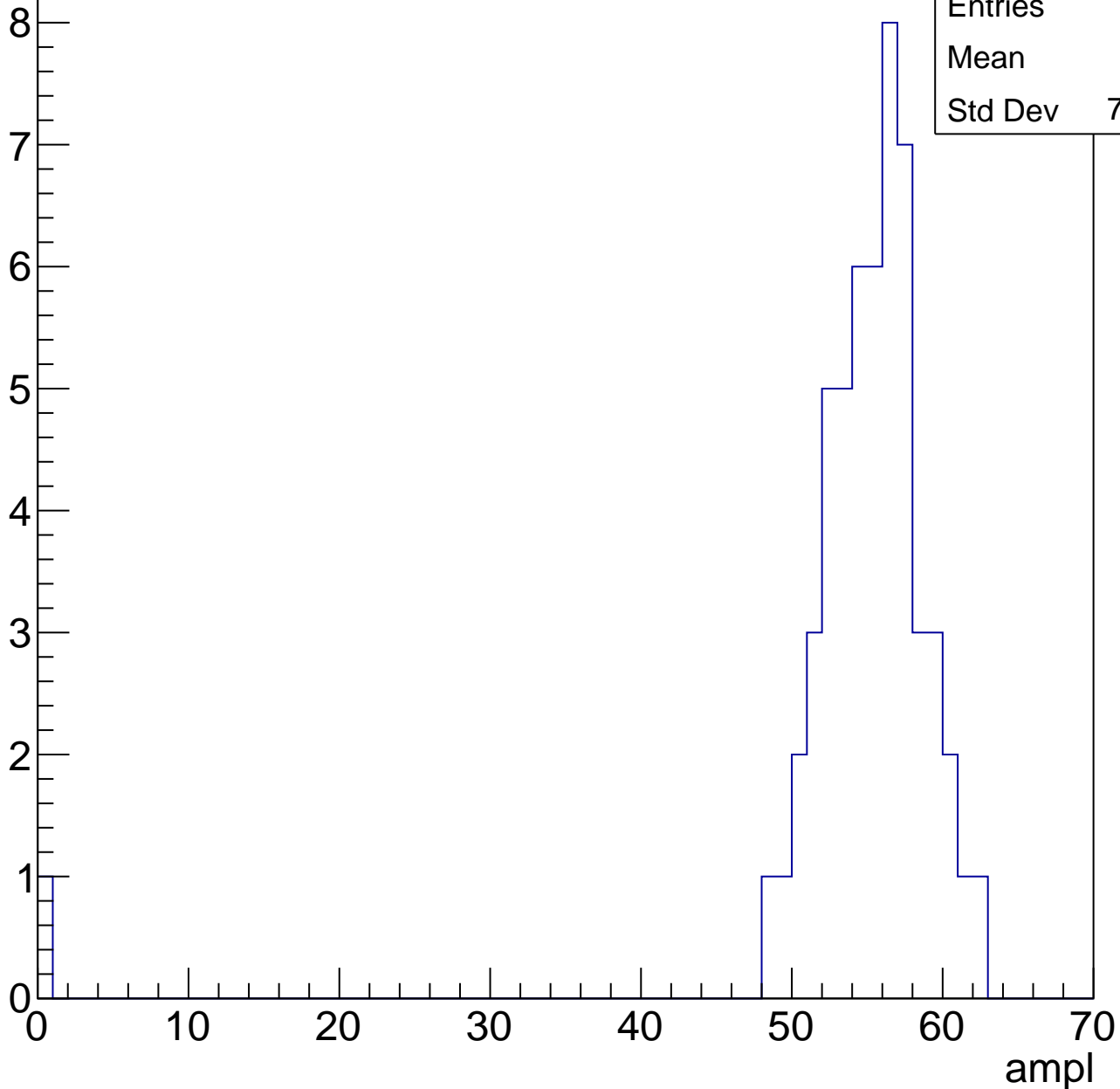


B1L103S, U8-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54
Std Dev	7.943

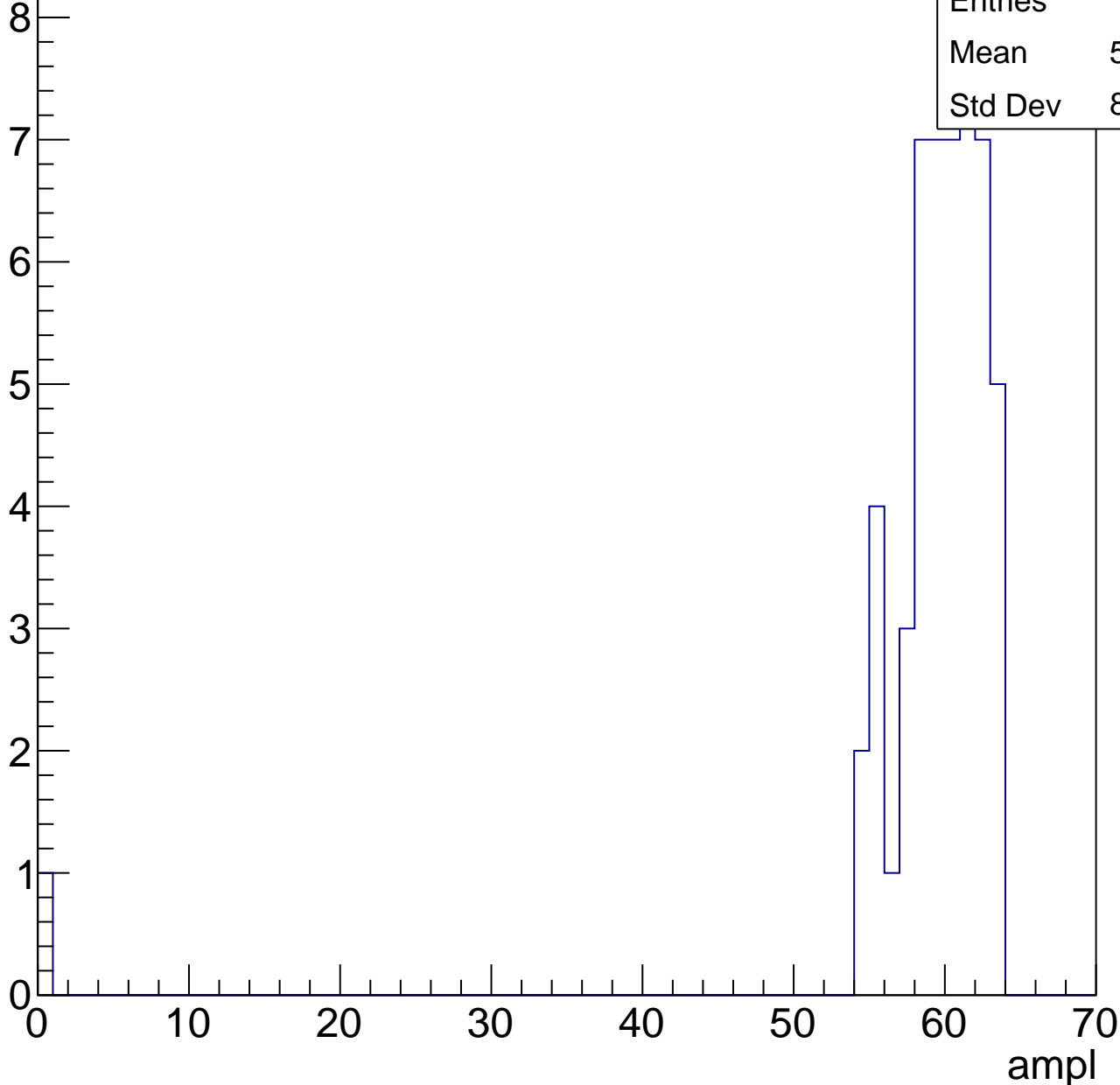


B1L103S, U8-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.29
Std Dev	8.524

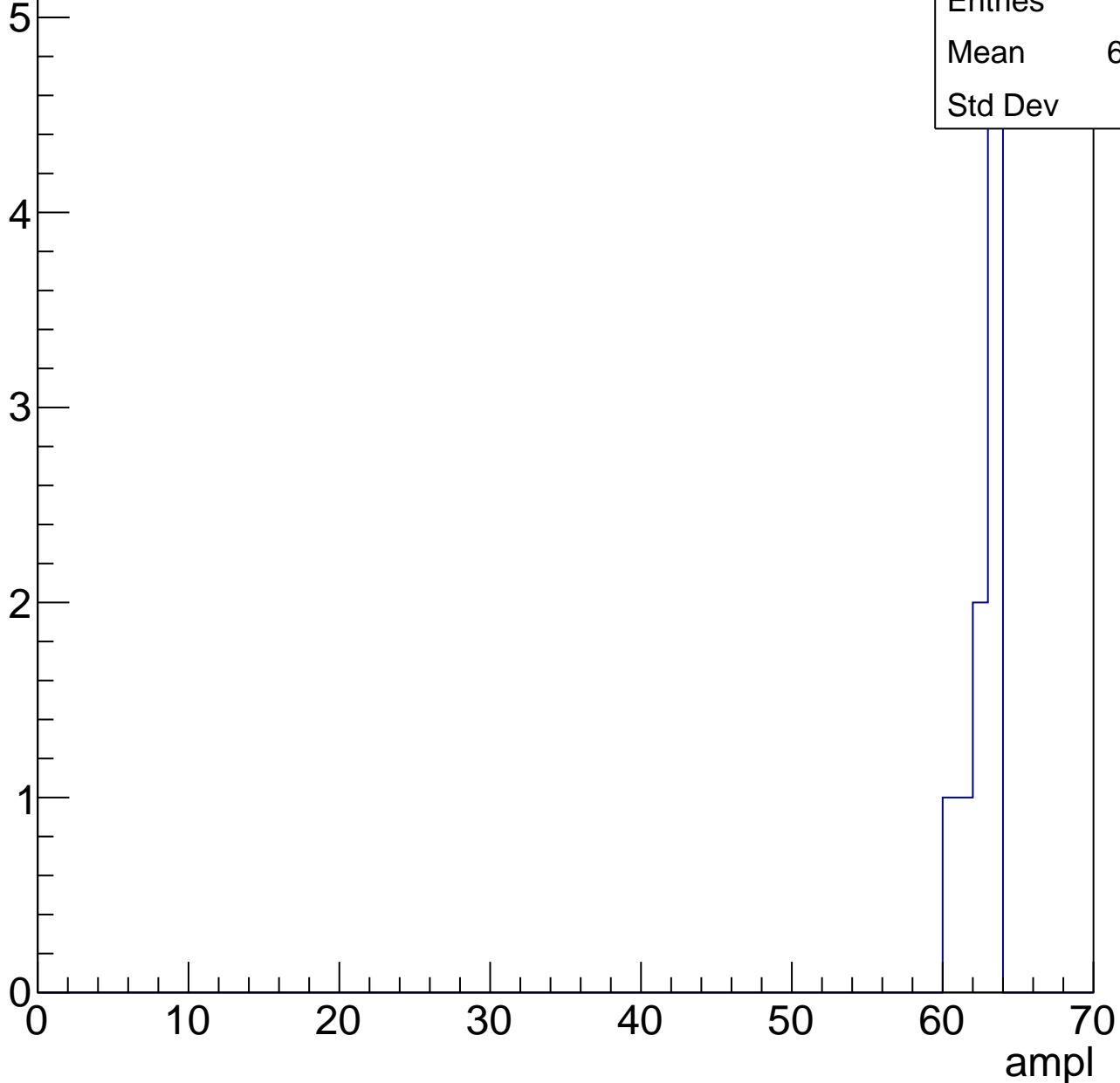


B1L103S, U8-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	62.22
Std Dev	1.03



B1L103S, U8-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch57, adc0

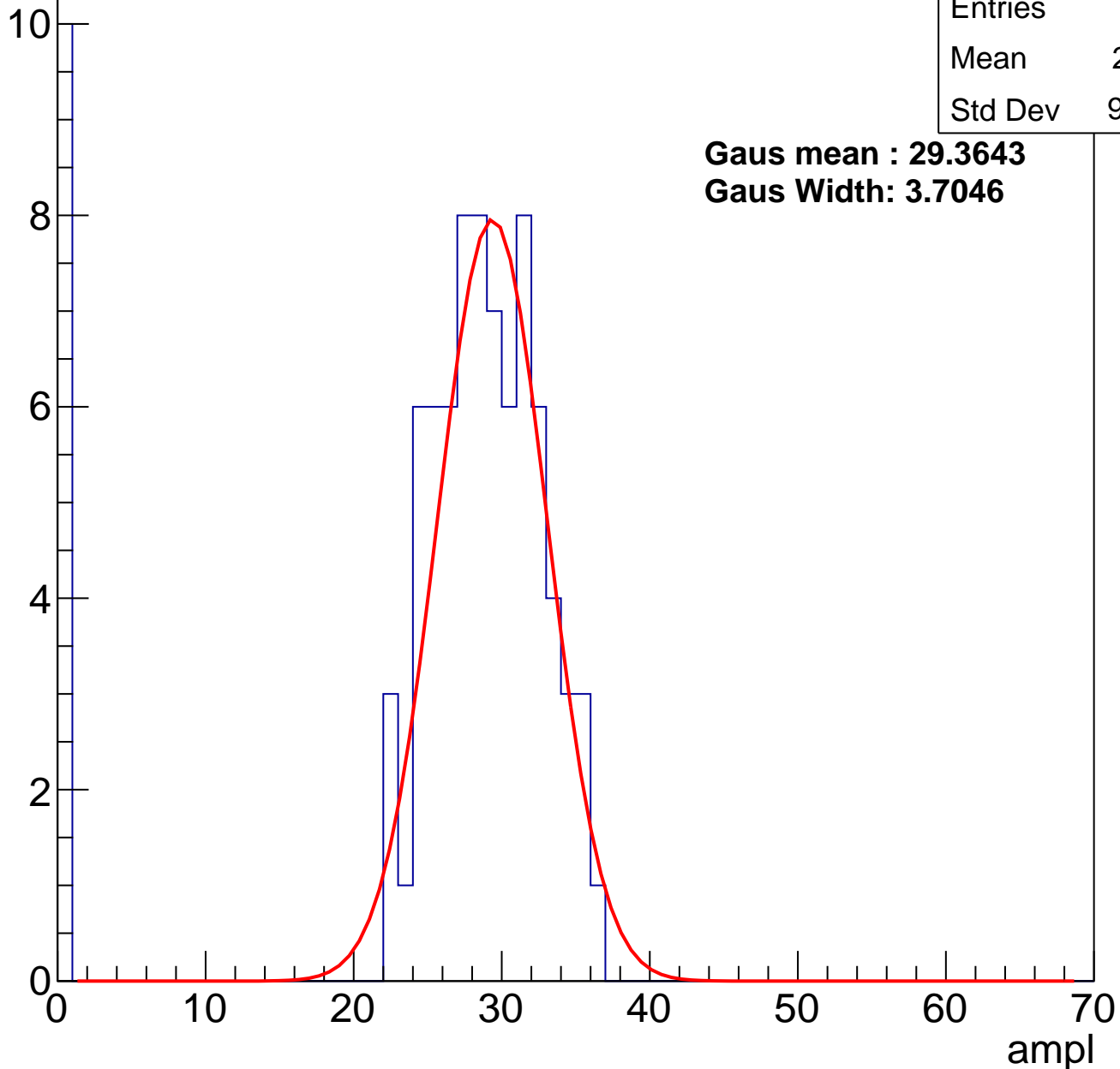
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	25.31
Std Dev	9.736

Gaus mean : 29.3643

Gaus Width: 3.7046

Entry



B1L103S, U8-ch57, adc1

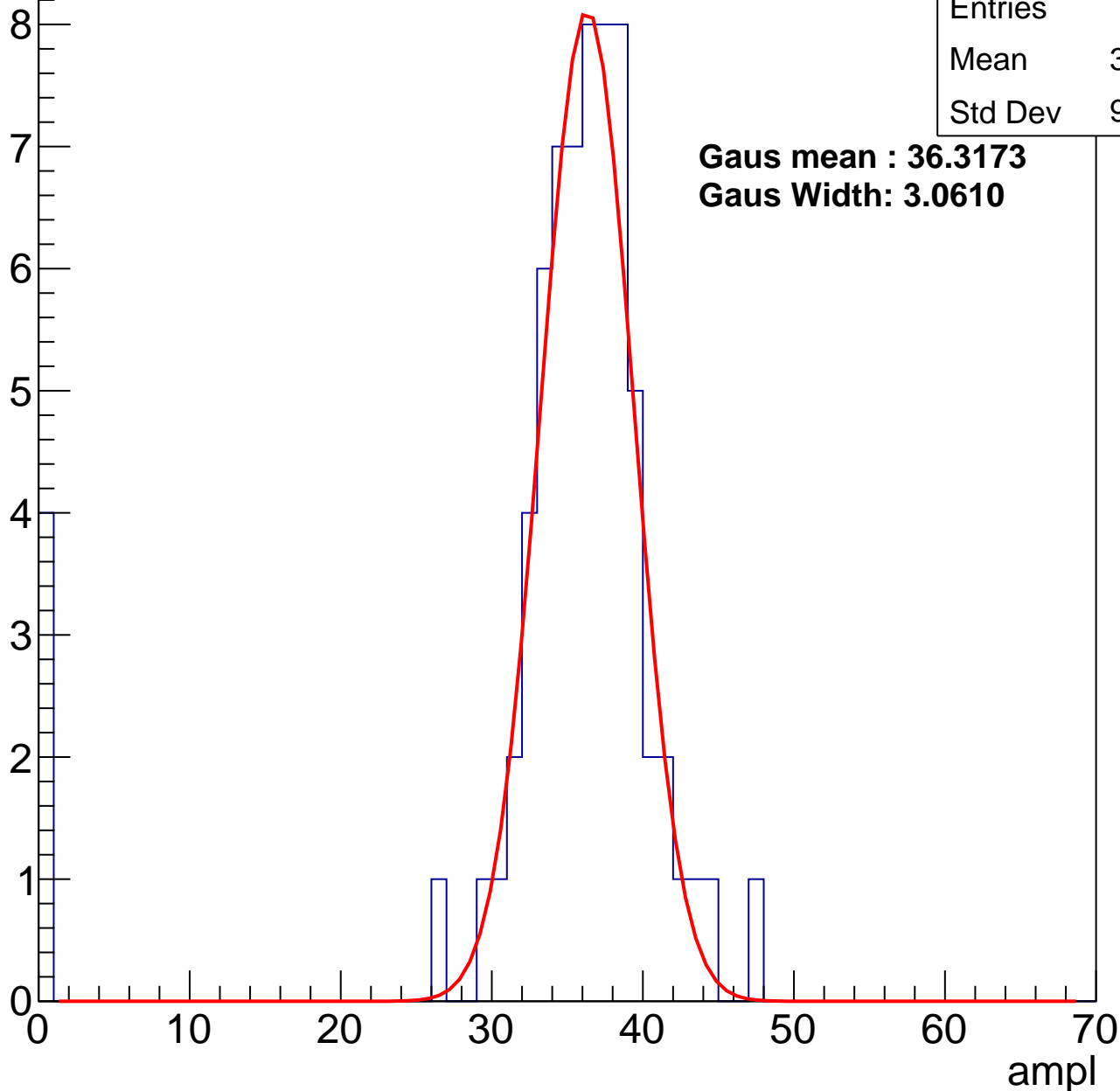
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	33.96
Std Dev	9.037

Gaus mean : 36.3173

Gaus Width: 3.0610



B1L103S, U8-ch57, adc2

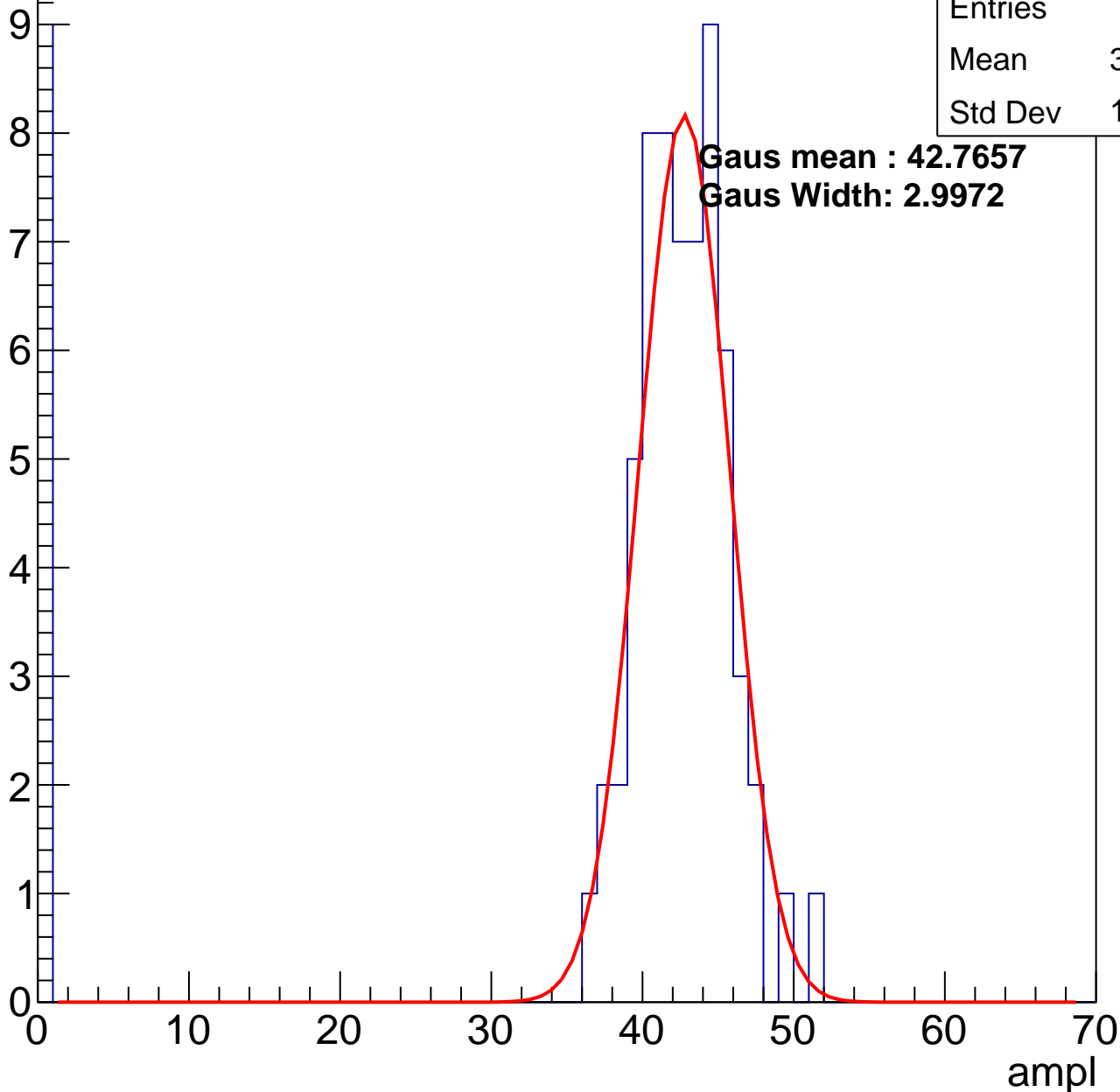
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	36.93
Std Dev	14.33

Gaus mean : 42.7657

Gaus Width: 2.9972

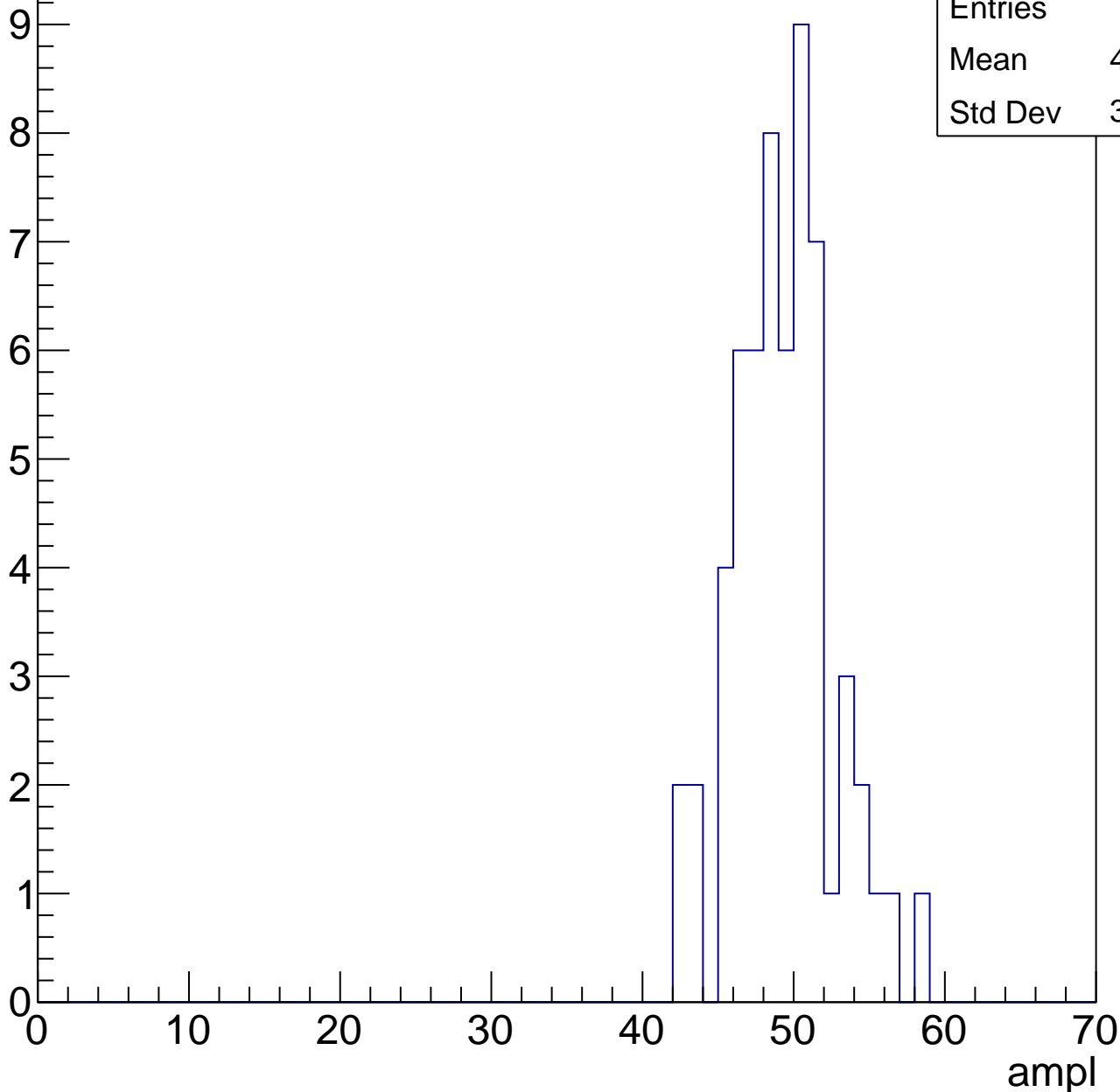


B1L103S, U8-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	48.83
Std Dev	3.263

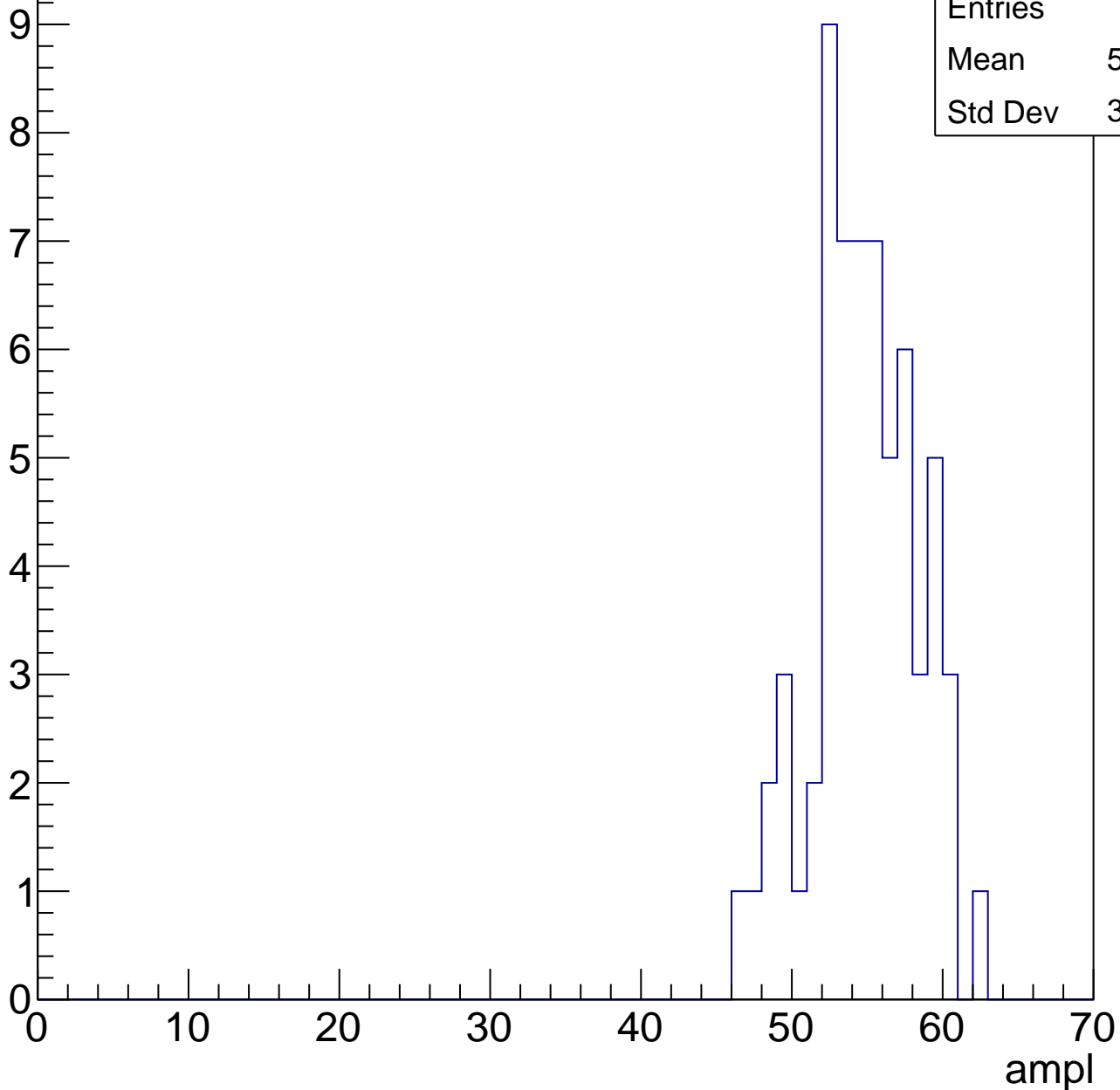


B1L103S, U8-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	54.33
Std Dev	3.464



B1L103S, U8-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

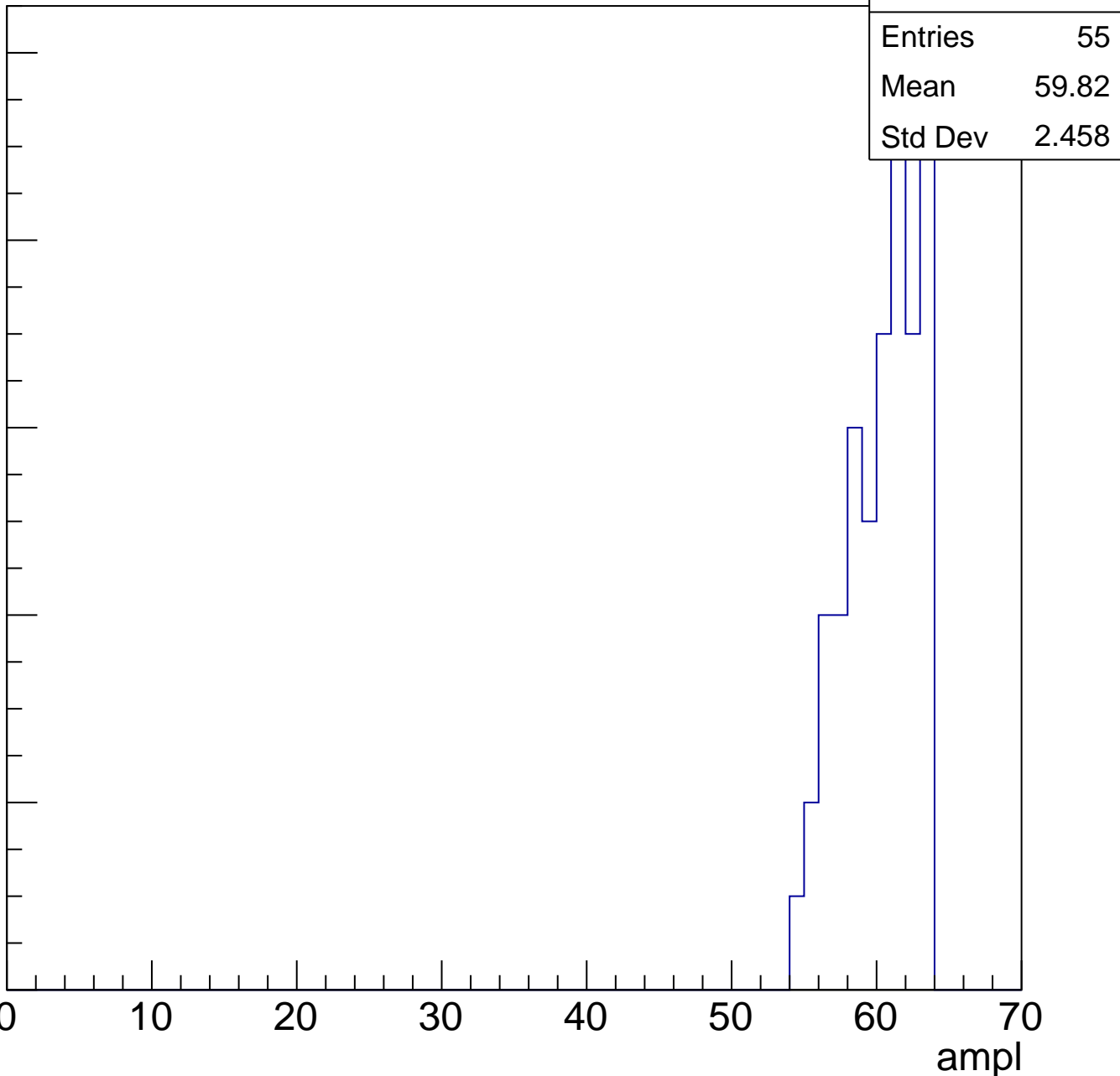
40

50

60

ampl

Entries	55
Mean	59.82
Std Dev	2.458



B1L103S, U8-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	4
Mean	46.75
Std Dev	26.99

B1L103S, U8-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch58, adc0

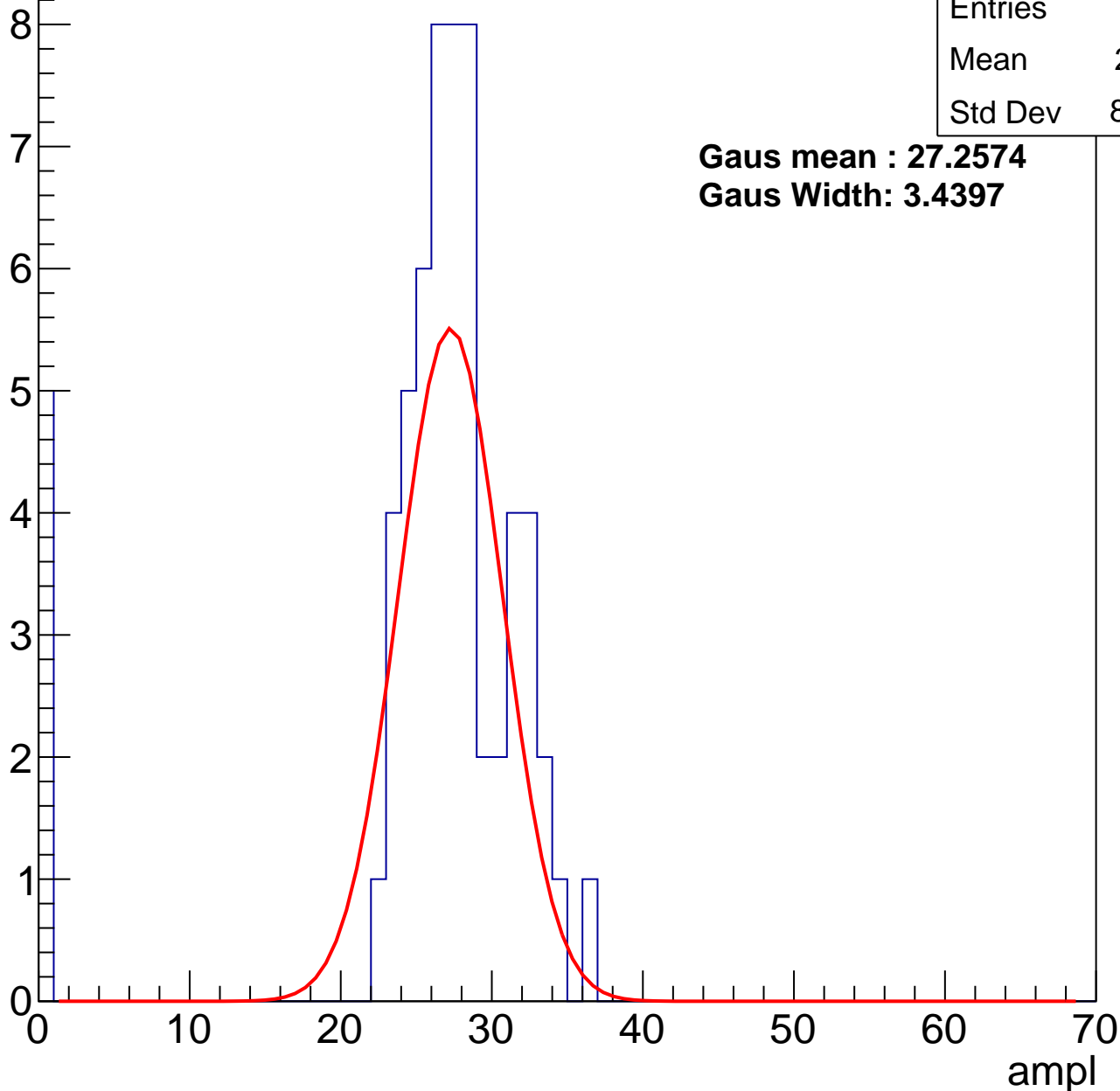
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	25.21
Std Dev	8.114

Gaus mean : 27.2574

Gaus Width: 3.4397



B1L103S, U8-ch58, adc1

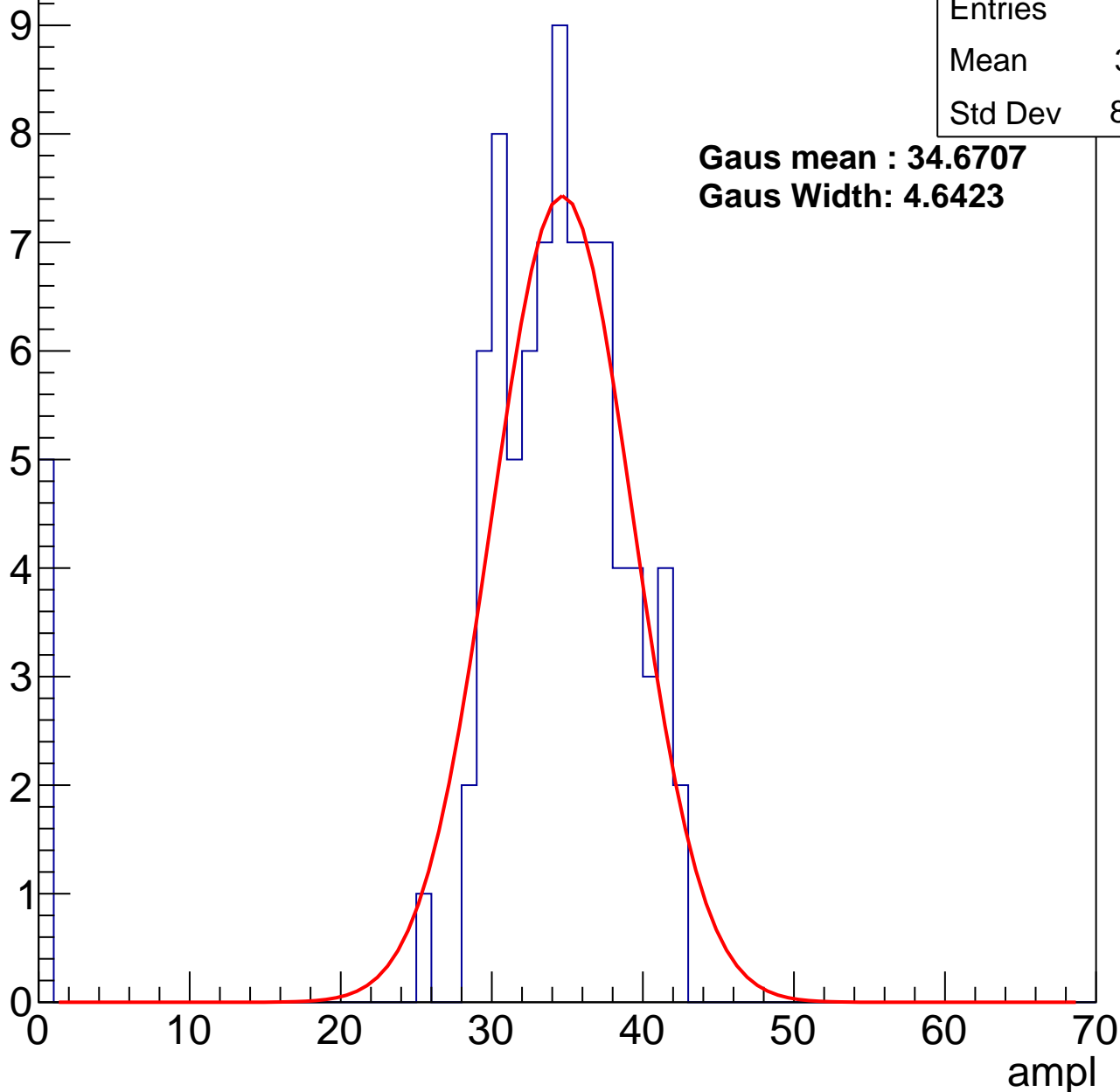
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	32.31
Std Dev	8.797

Gaus mean : 34.6707

Gaus Width: 4.6423



B1L103S, U8-ch58, adc2

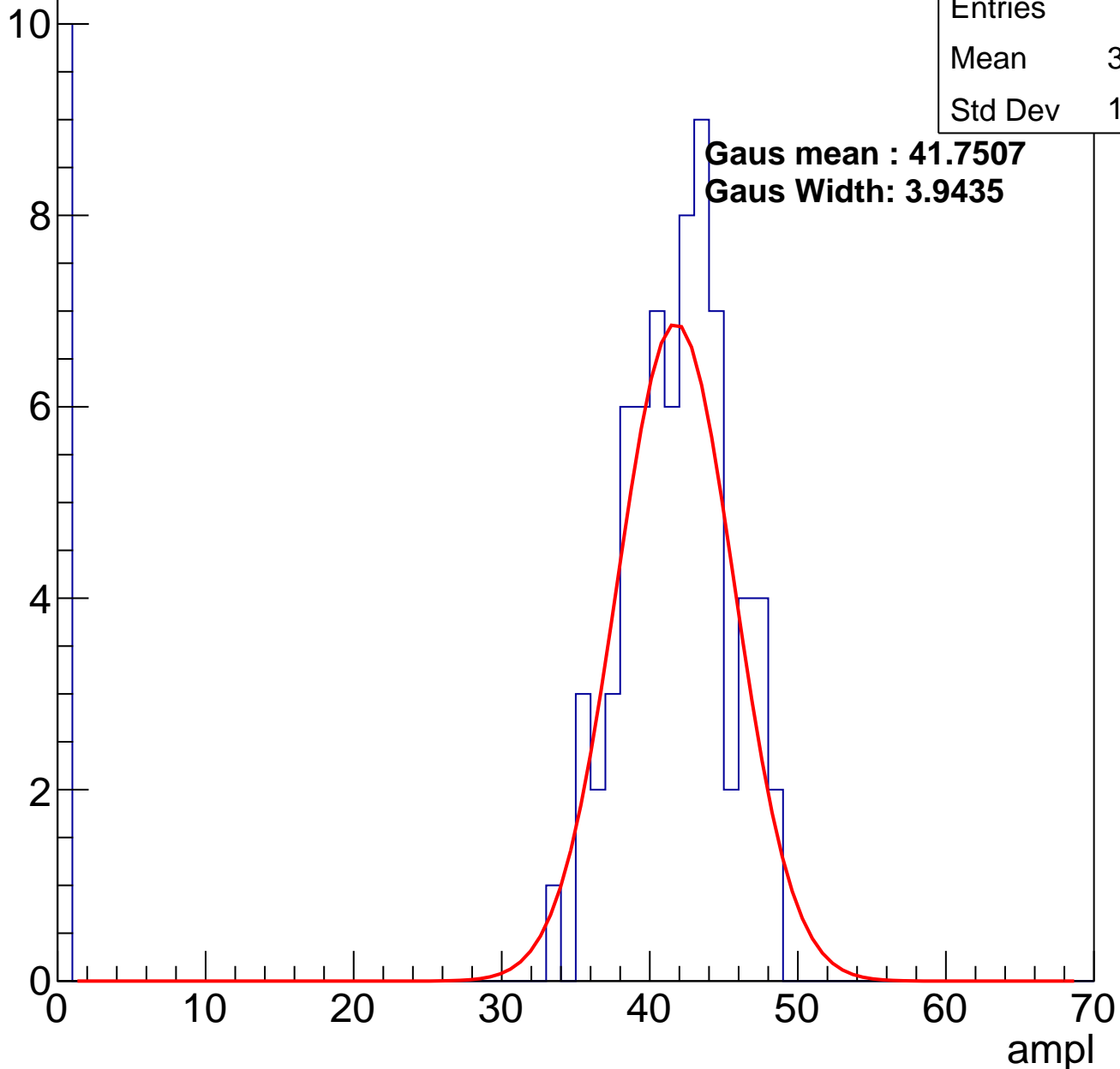
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	36.23
Std Dev	14.06

Gaus mean : 41.7507

Gaus Width: 3.9435

Entry

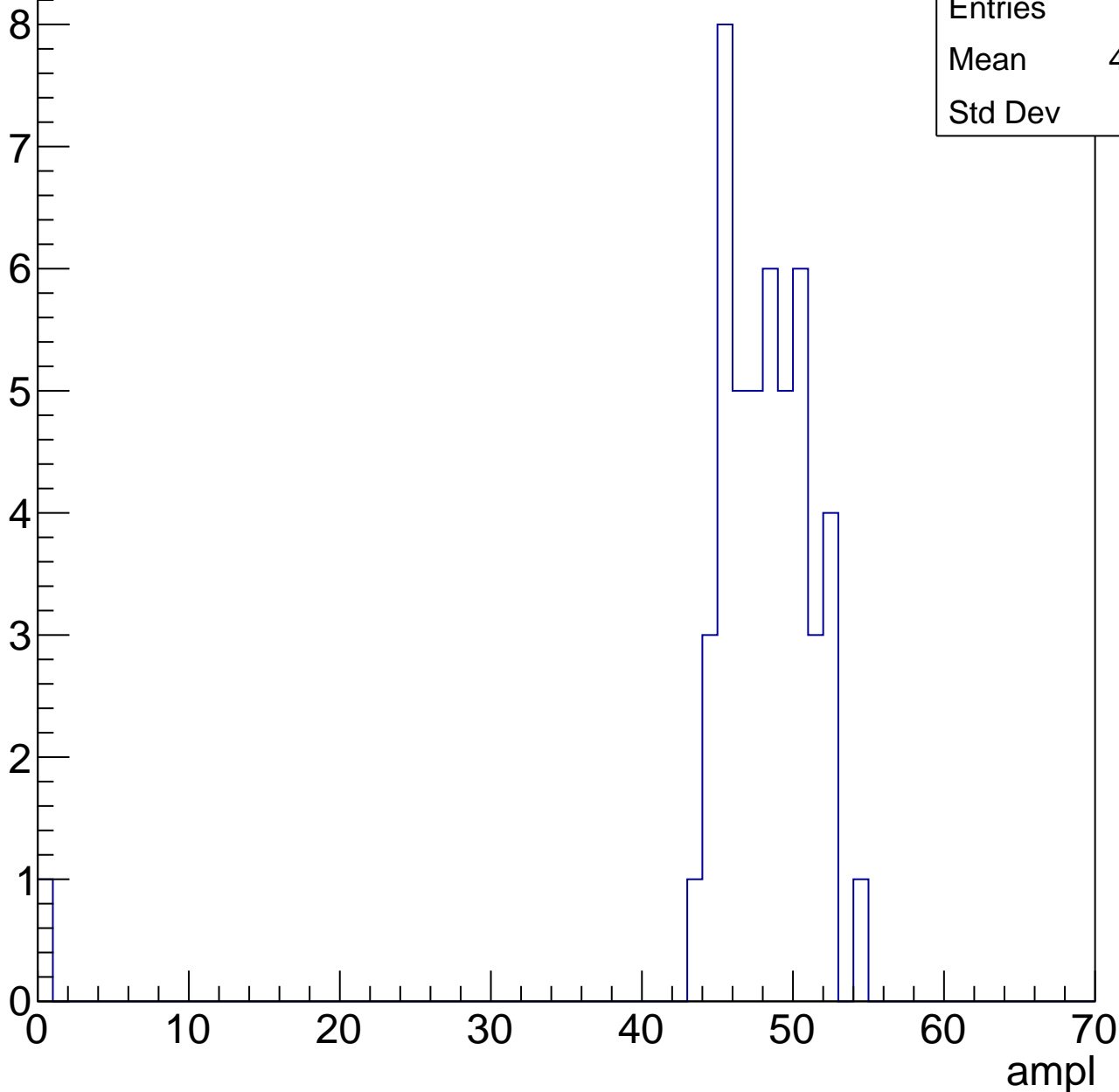


B1L103S, U8-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	46.83
Std Dev	7.31

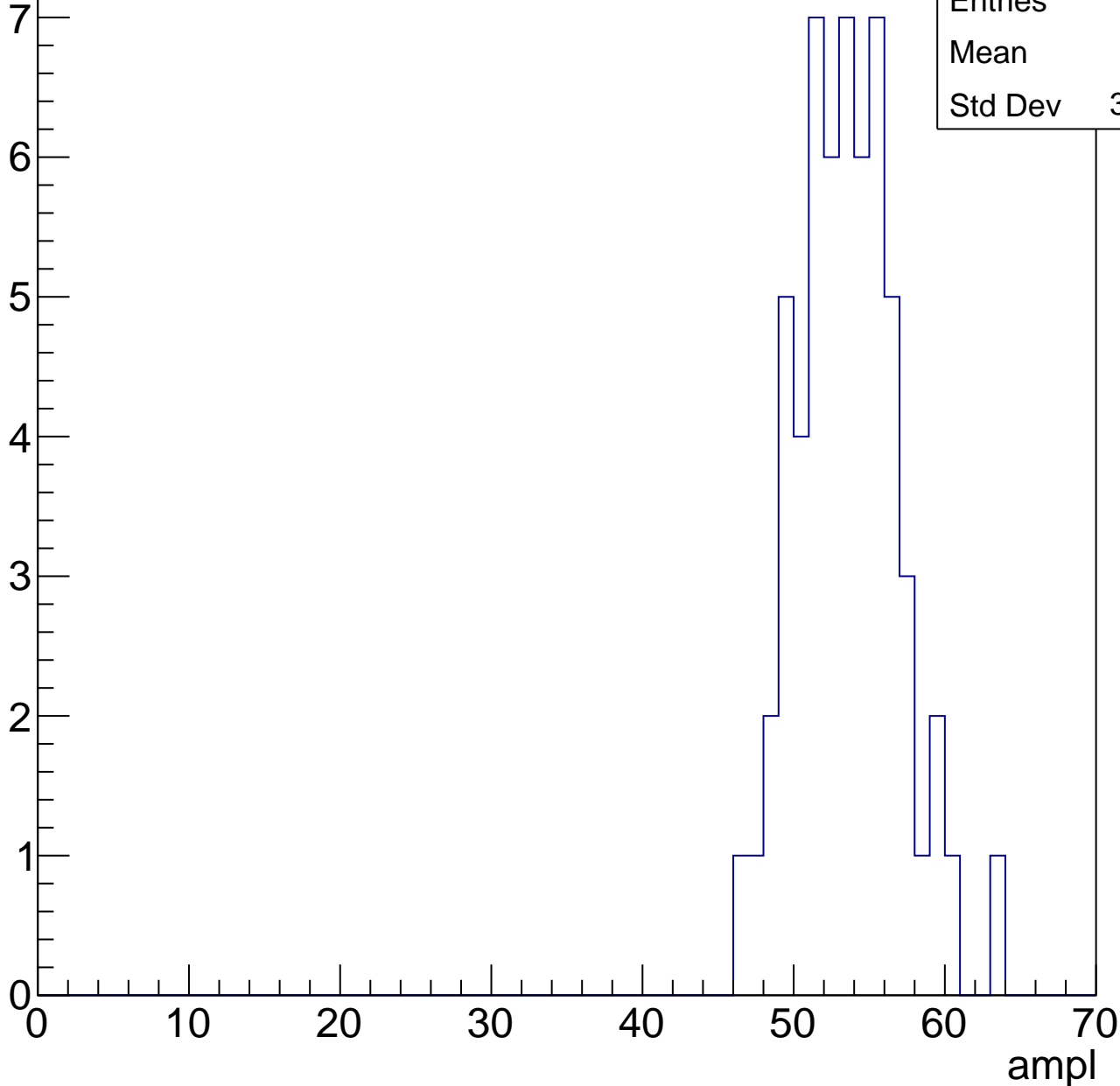


B1L103S, U8-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	53.1
Std Dev	3.338

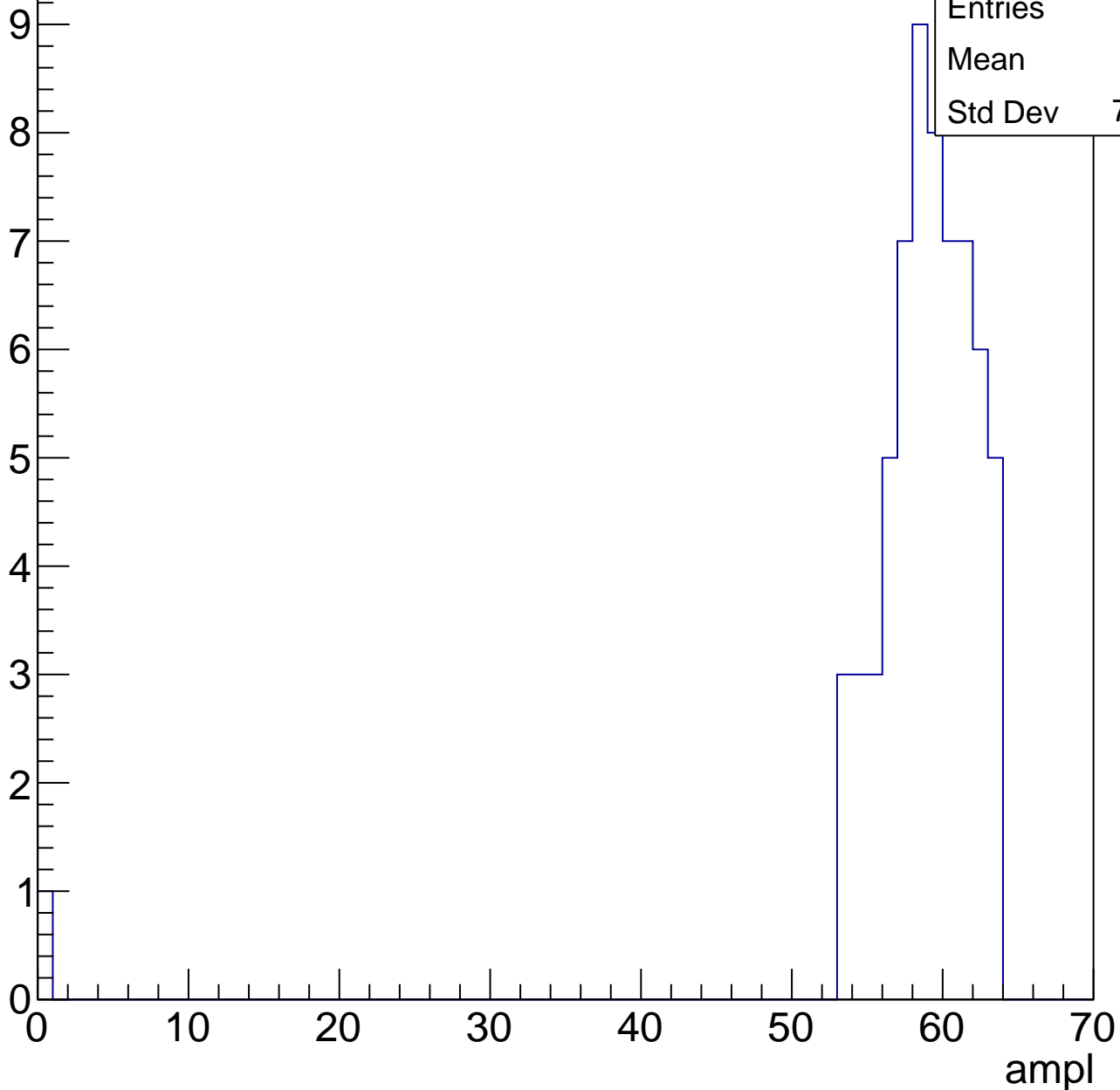


B1L103S, U8-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

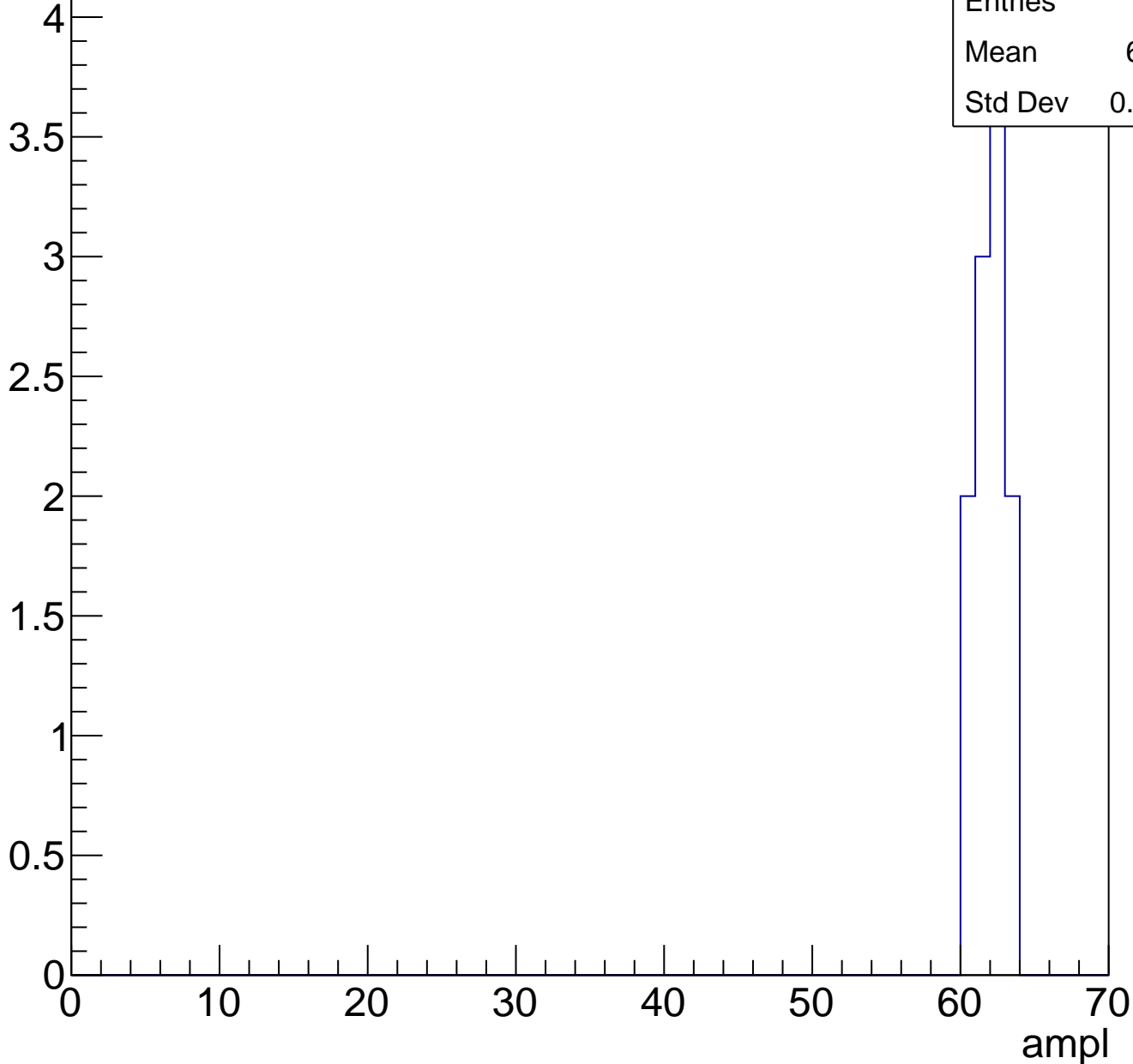
Entries	64
Mean	57.7
Std Dev	7.761



B1L103S, U8-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

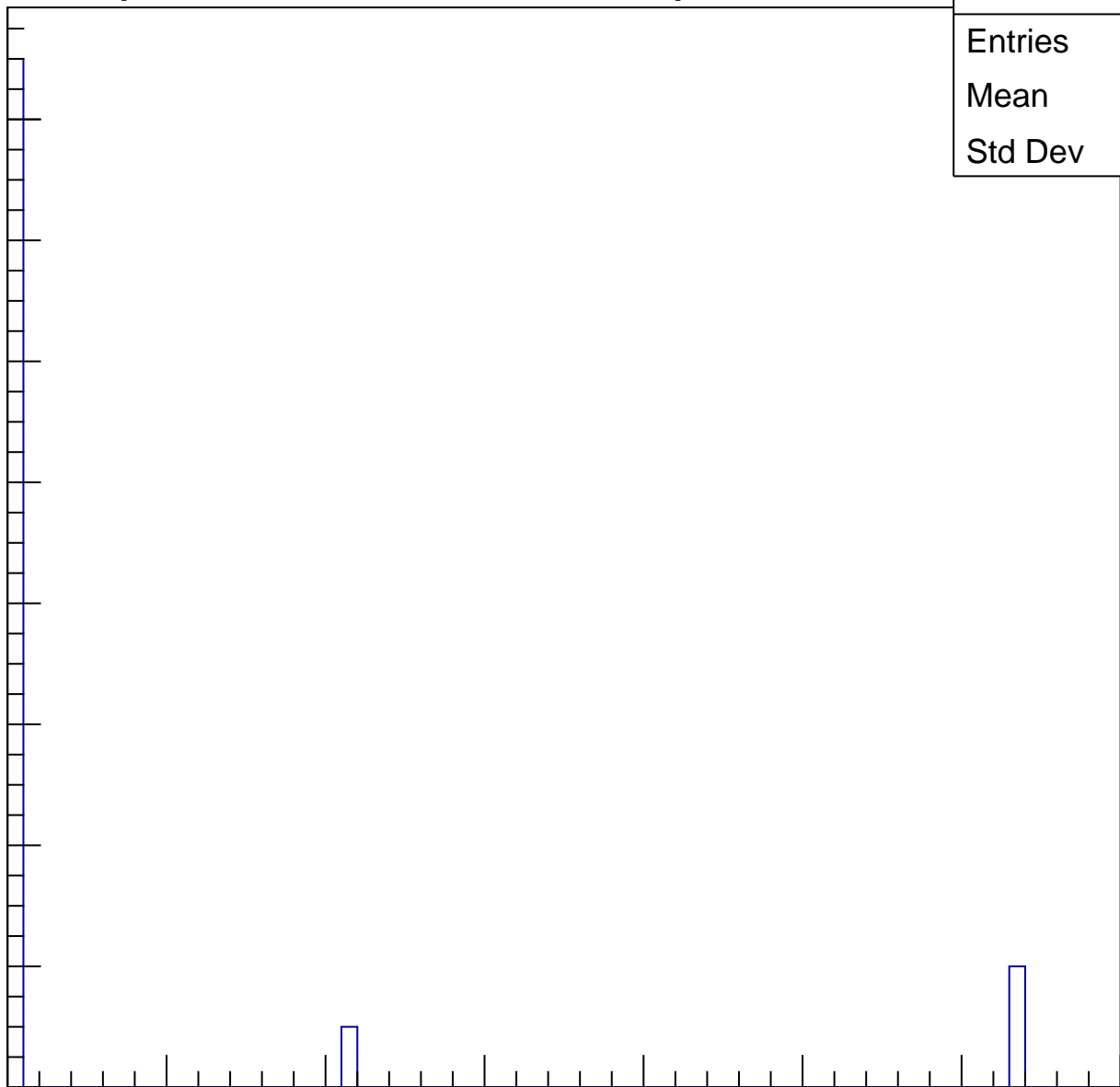
Entries	20
Mean	7.35
Std Dev	19.1

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch59, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	22.63
Std Dev	11.69

Gaus mean : 28.5352

Gaus Width: 3.4331

Entry

12

10

8

6

4

2

0

0

10

20

30

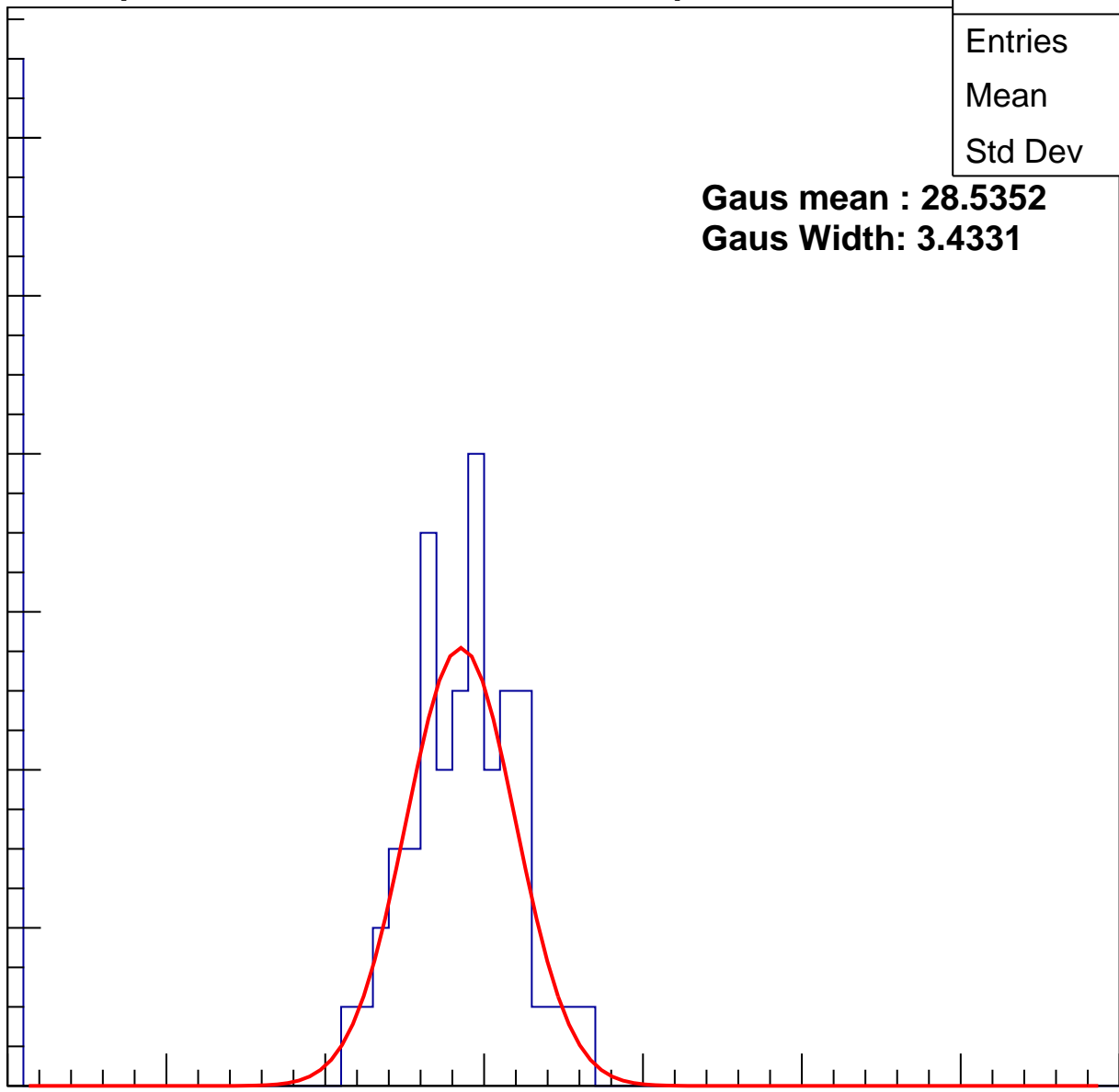
40

50

60

70

ampl



B1L103S, U8-ch59, adc1

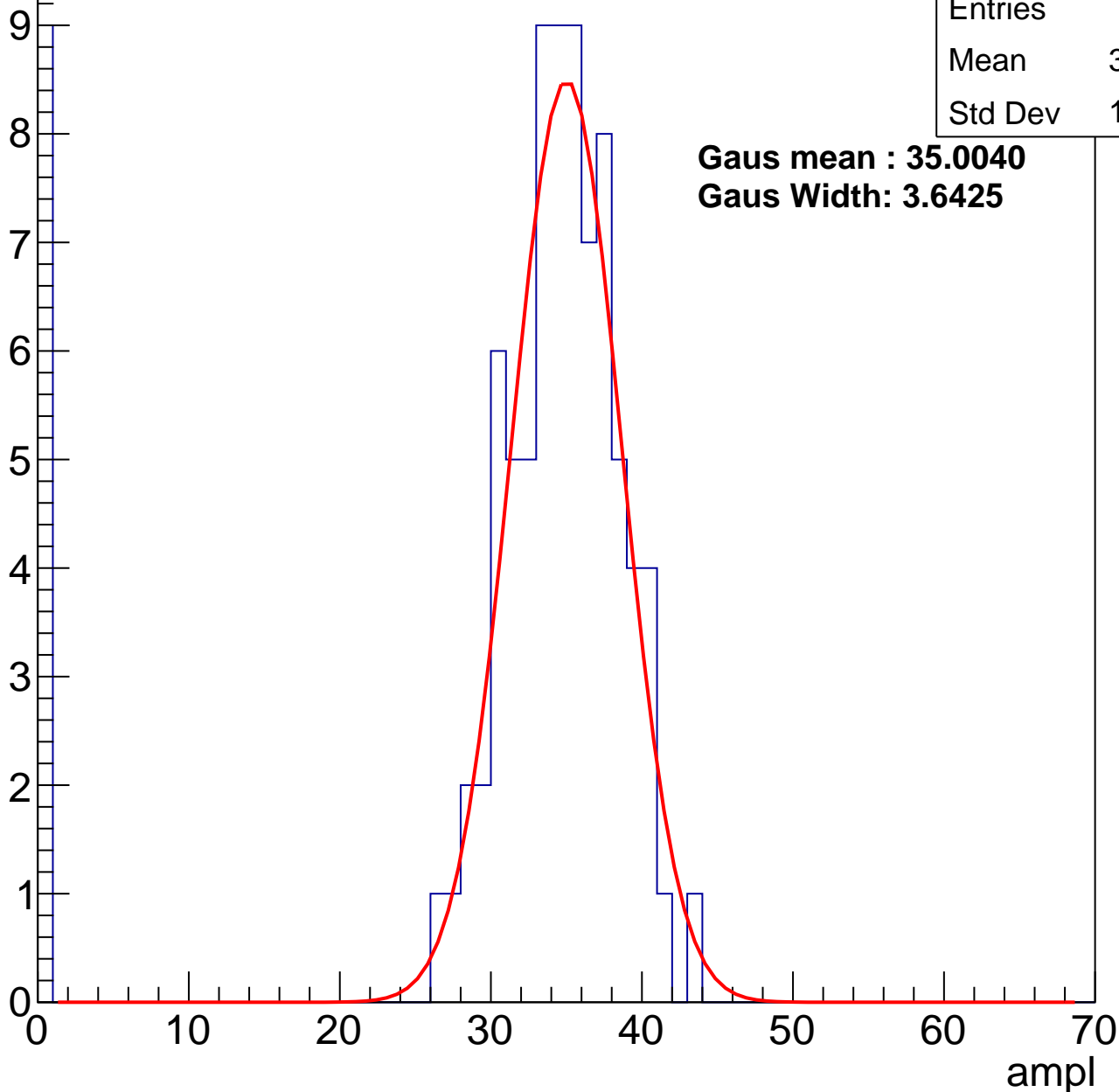
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	30.89
Std Dev	10.93

Gaus mean : 35.0040

Gaus Width: 3.6425



B1L103S, U8-ch59, adc2

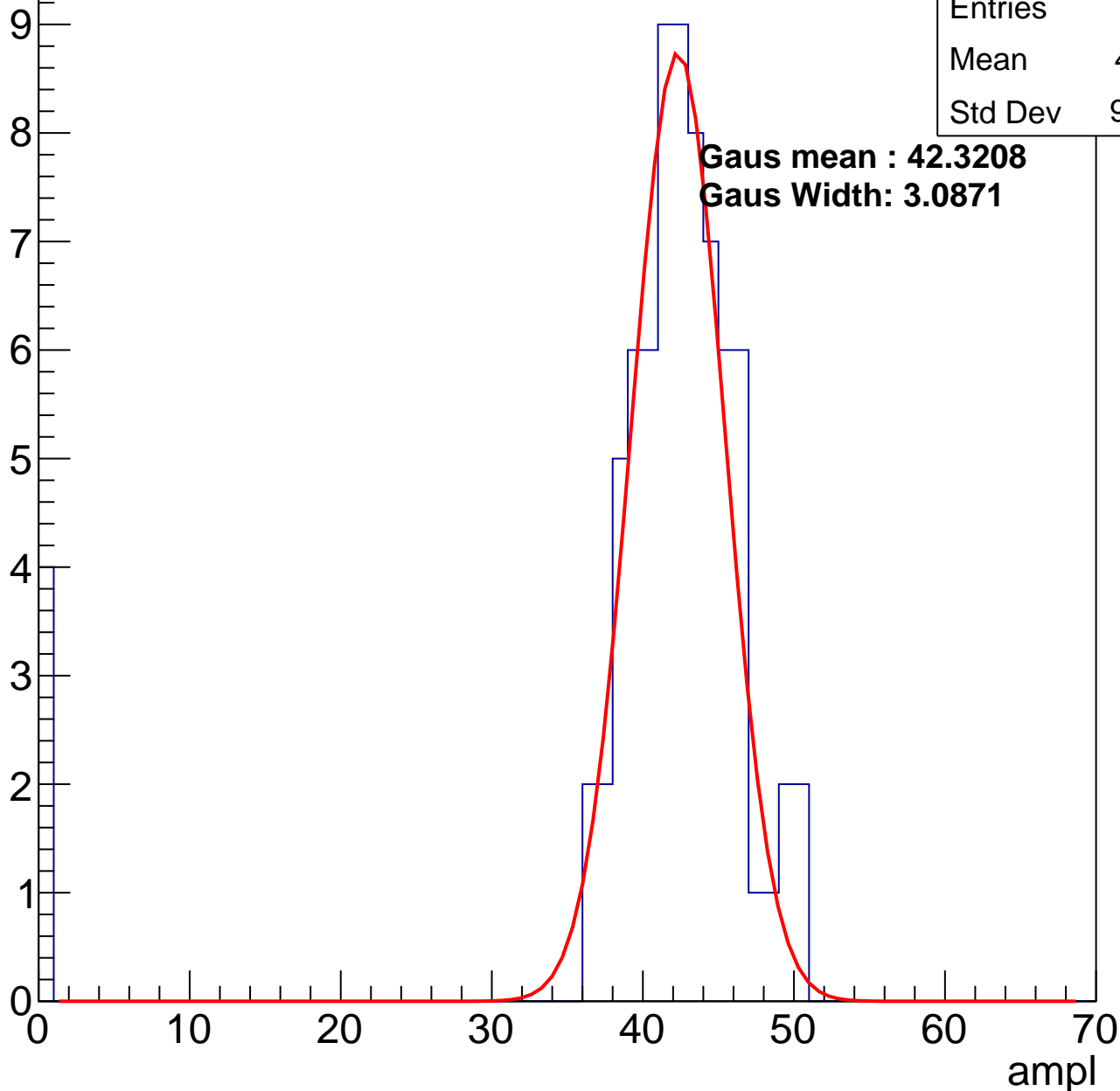
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	40.11
Std Dev	9.964

Gaus mean : 42.3208

Gaus Width: 3.0871

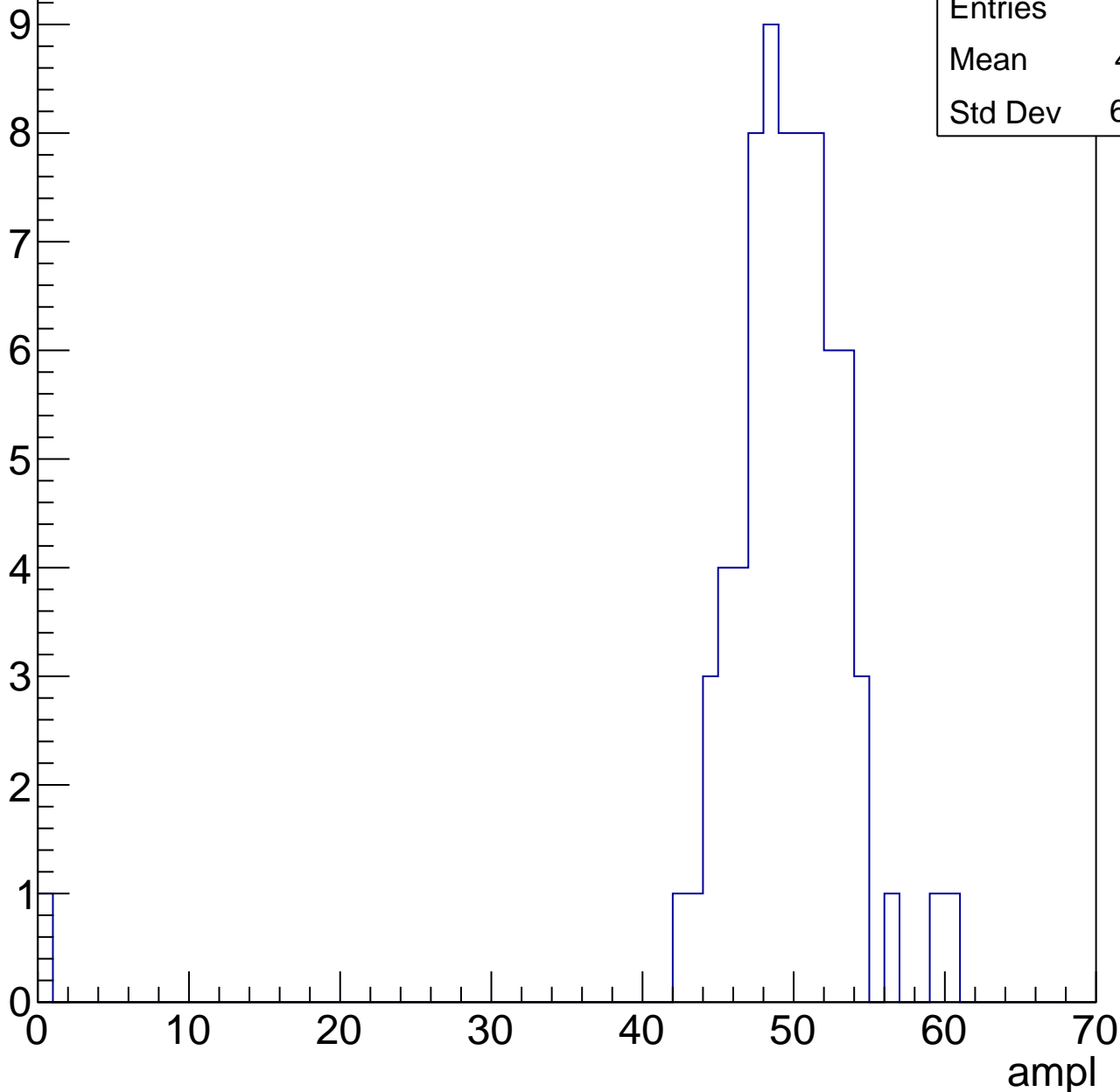


B1L103S, U8-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	48.71
Std Dev	6.653

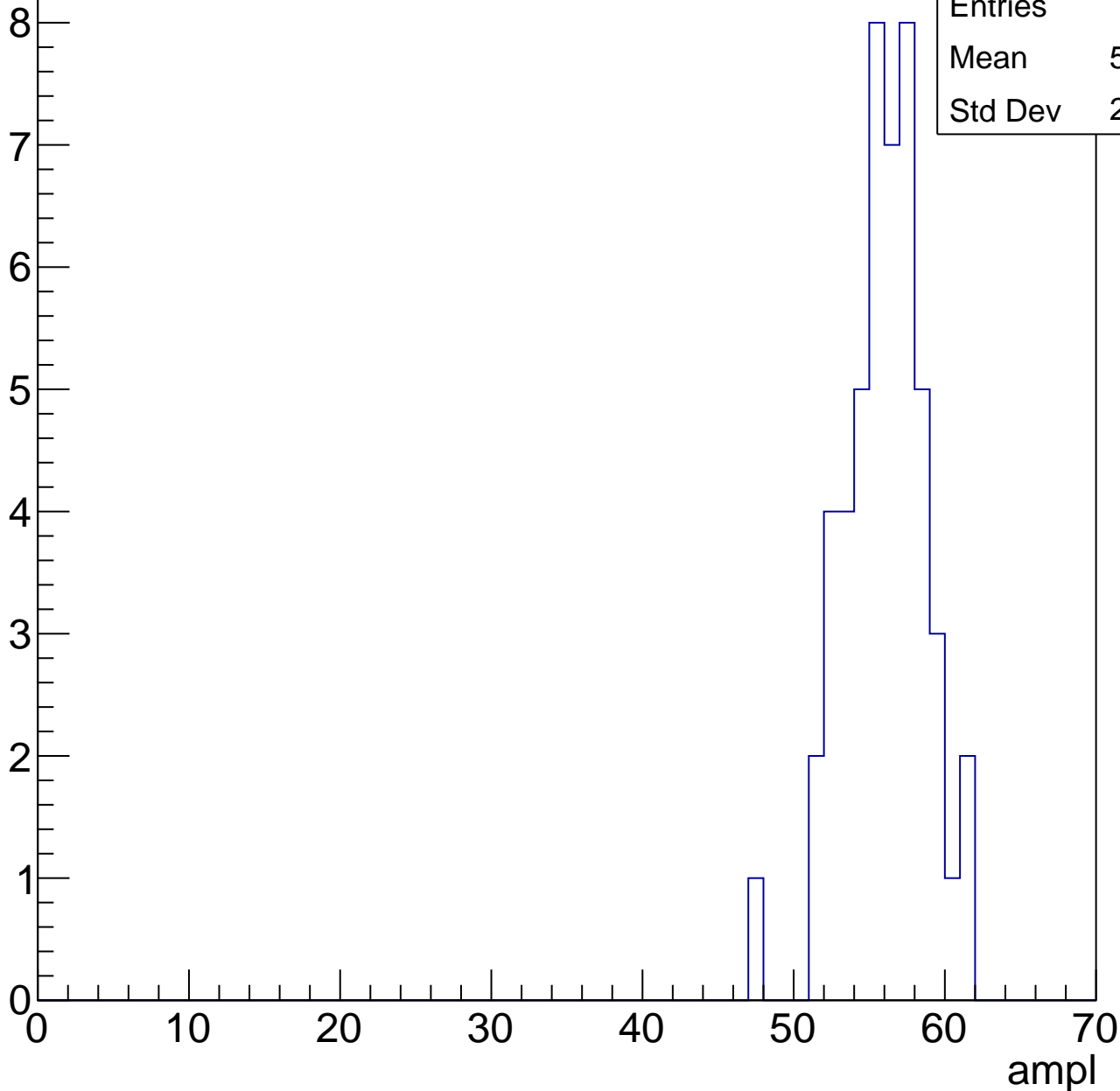


B1L103S, U8-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.52
Std Dev	2.715

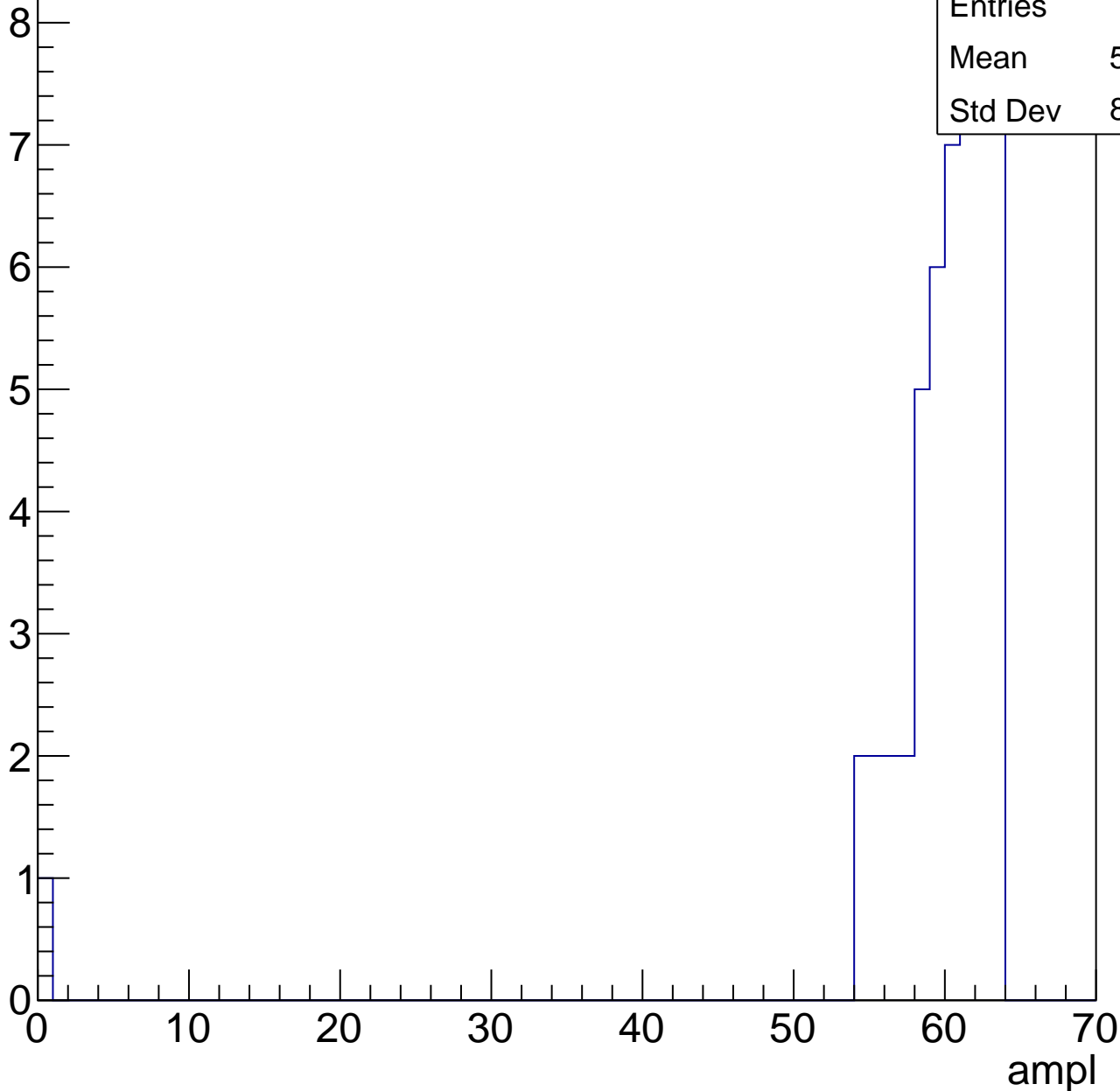


B1L103S, U8-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.75
Std Dev	8.666



B1L103S, U8-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch60, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	21.02
Std Dev	10.54

Gaus mean : 25.9549

Gaus Width: 3.3608

Entry

14

12

10

8

6

4

2

0

0

10

20

30

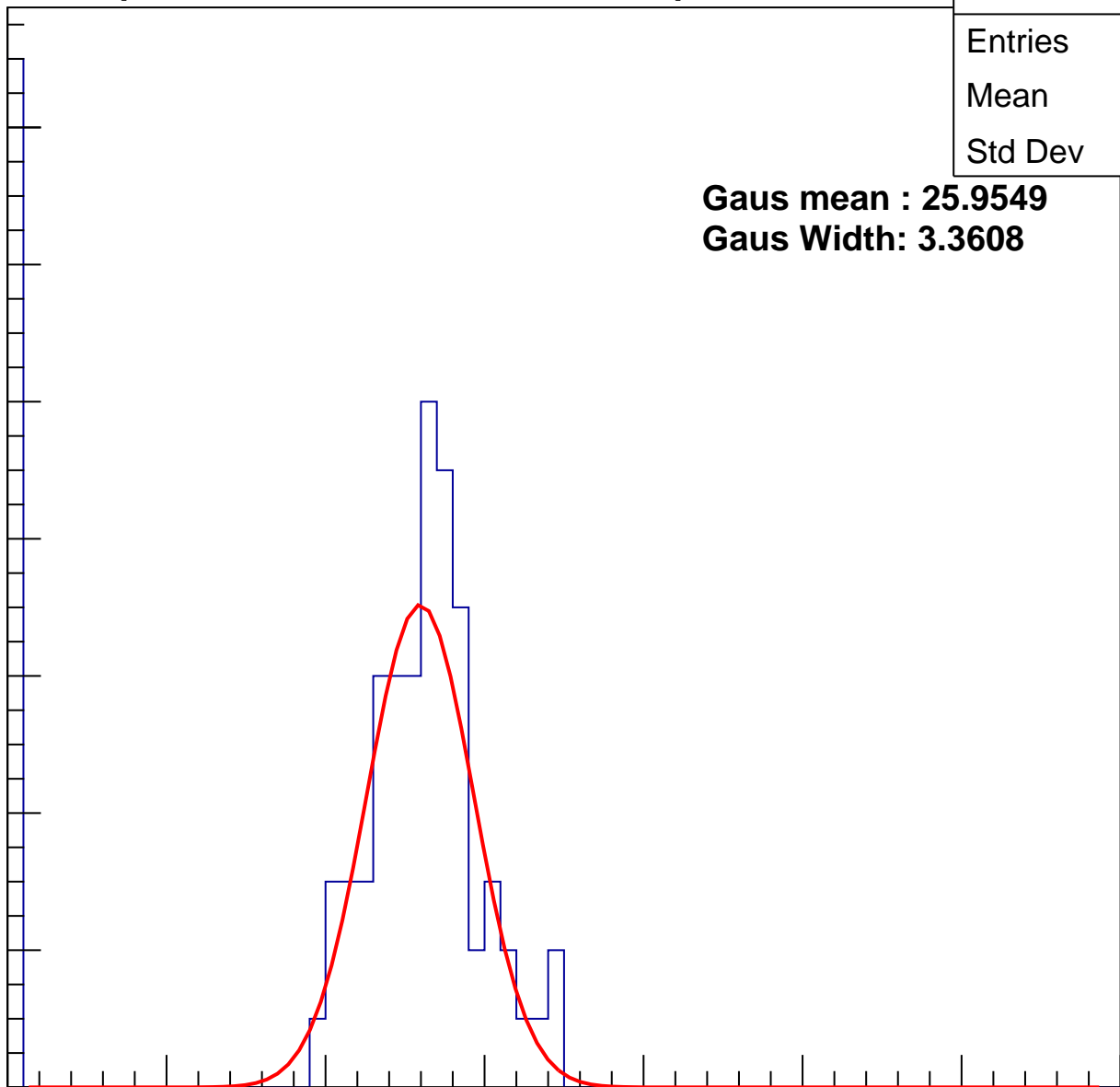
40

50

60

70

ampl



B1L103S, U8-ch60, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	28.52
Std Dev	11.48

Gaus mean : 33.3683

Gaus Width: 3.2730

Entry

10

8

6

4

2

0

0

10

20

30

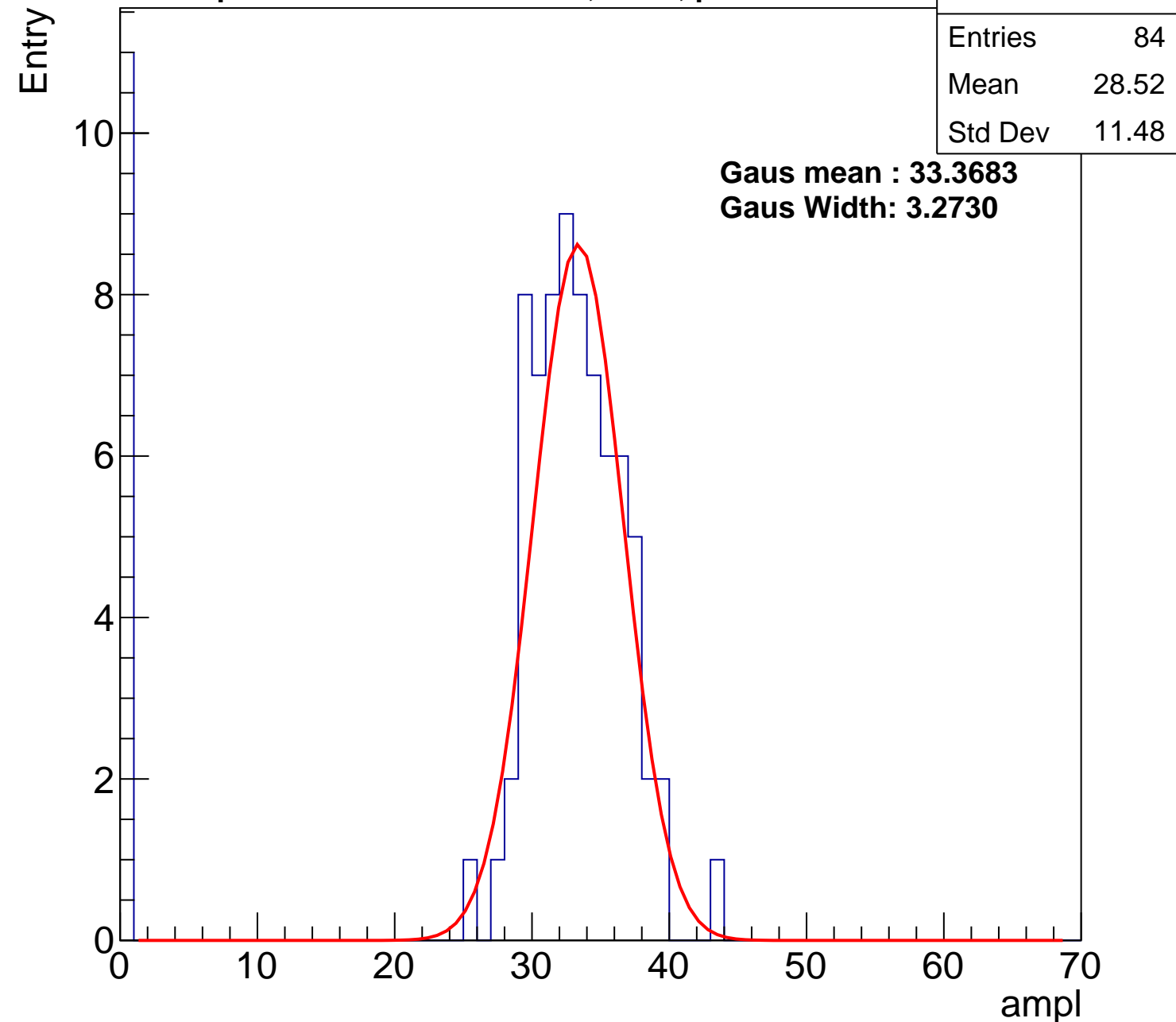
40

50

60

70

ampl



B1L103S, U8-ch60, adc2

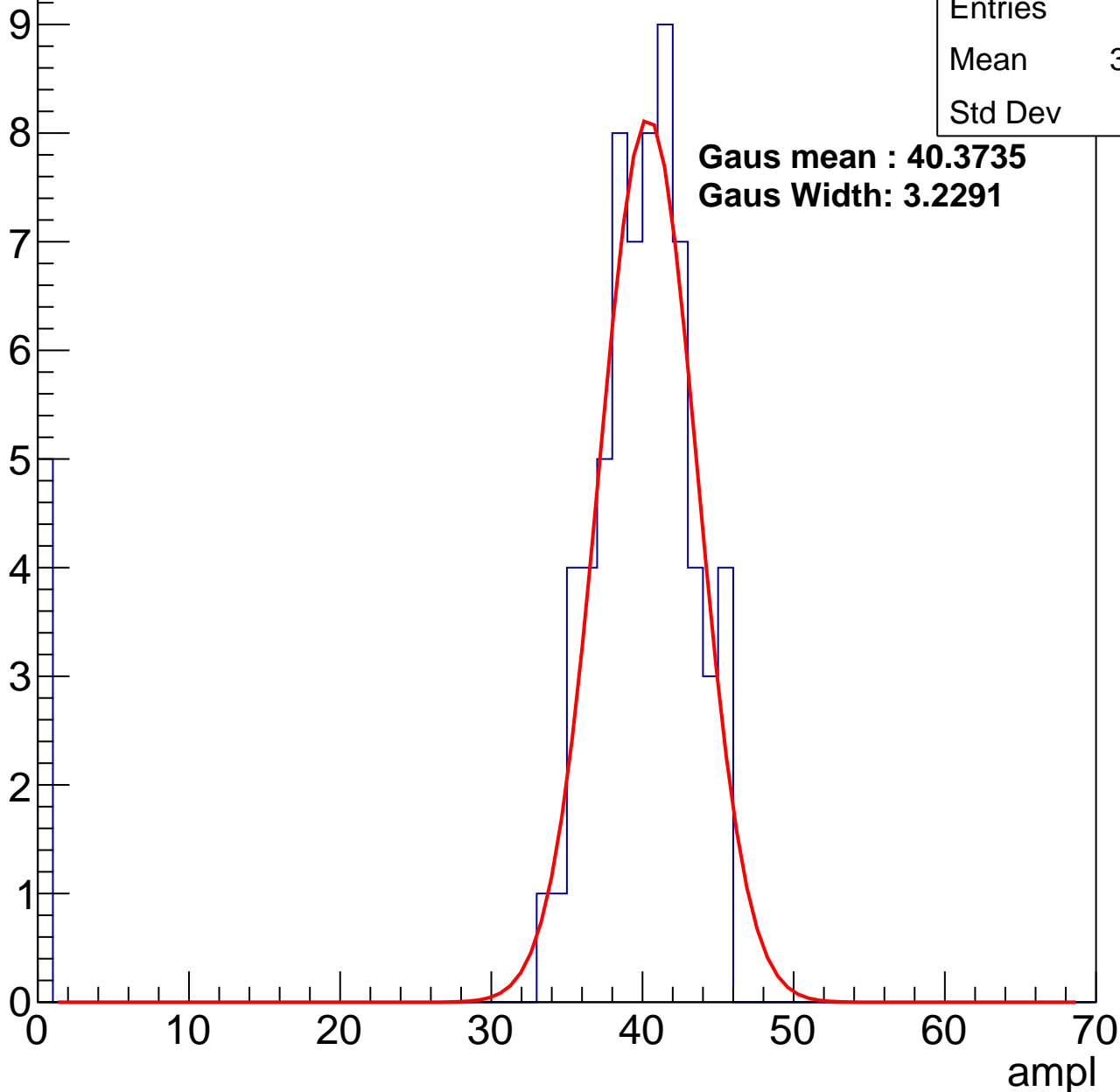
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.86
Std Dev	10.6

Gaus mean : 40.3735

Gaus Width: 3.2291



B1L103S, U8-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

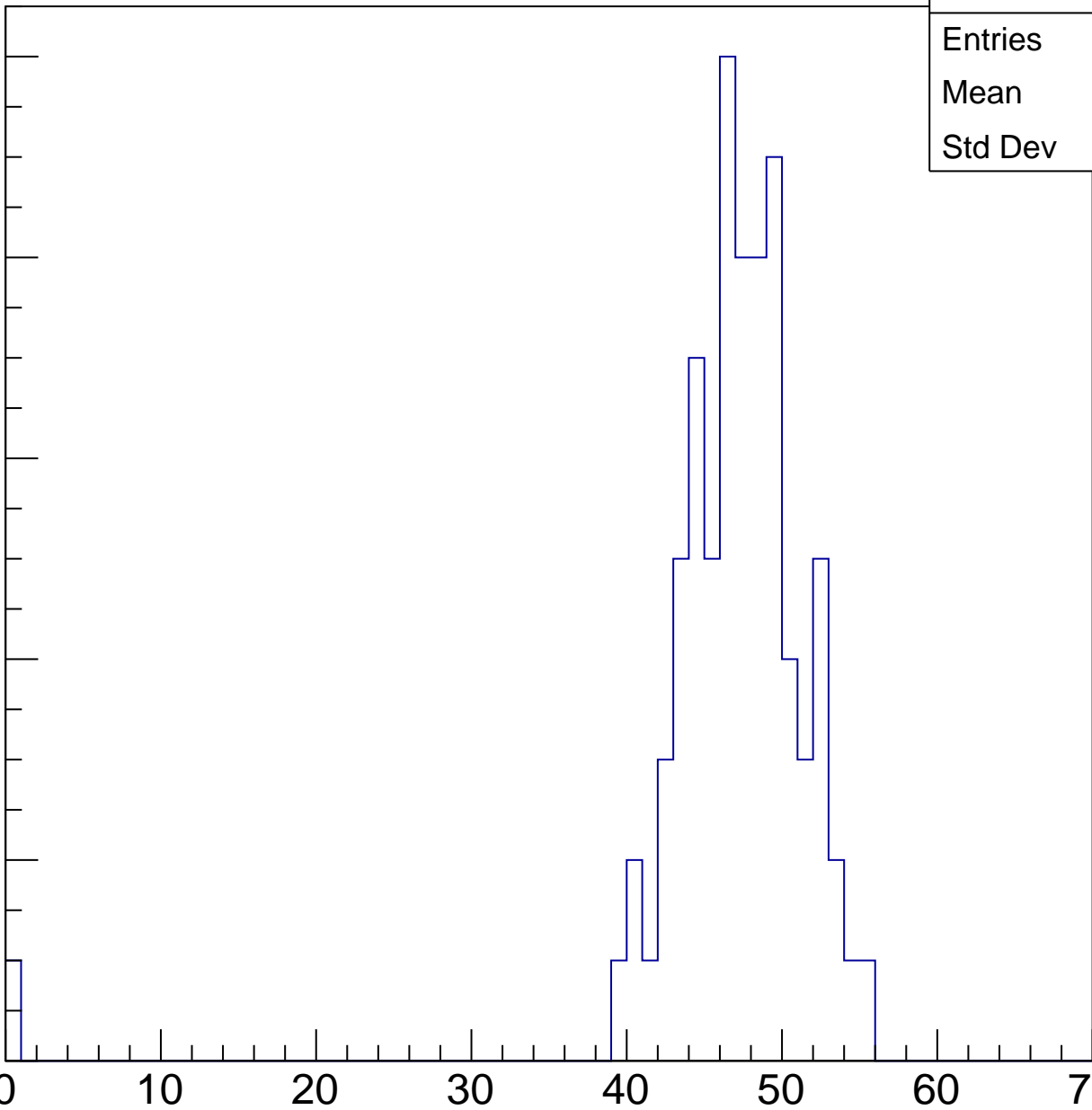
Entries	76
Mean	46.36
Std Dev	6.347

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

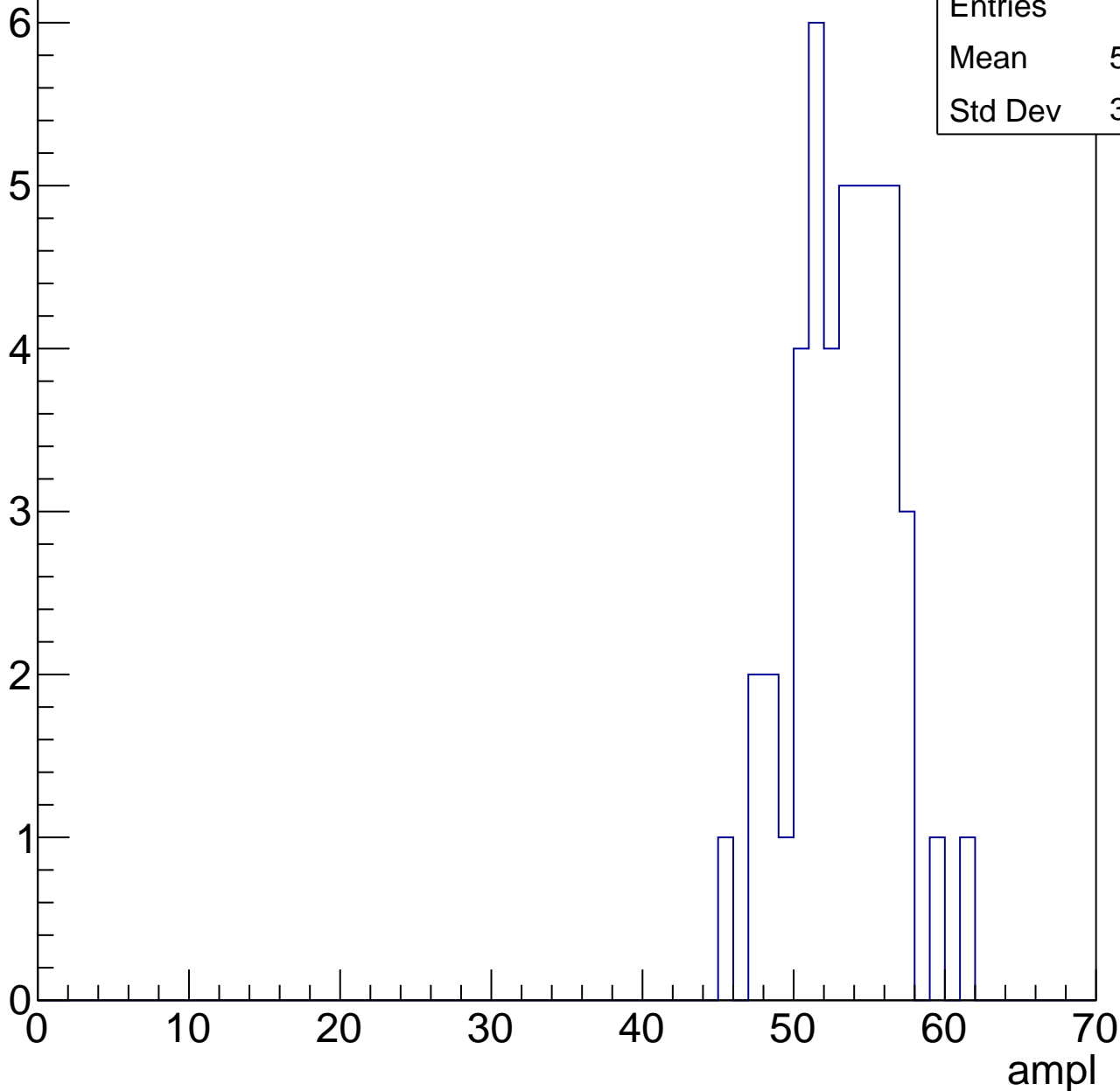


B1L103S, U8-ch60, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	52.87
Std Dev	3.277

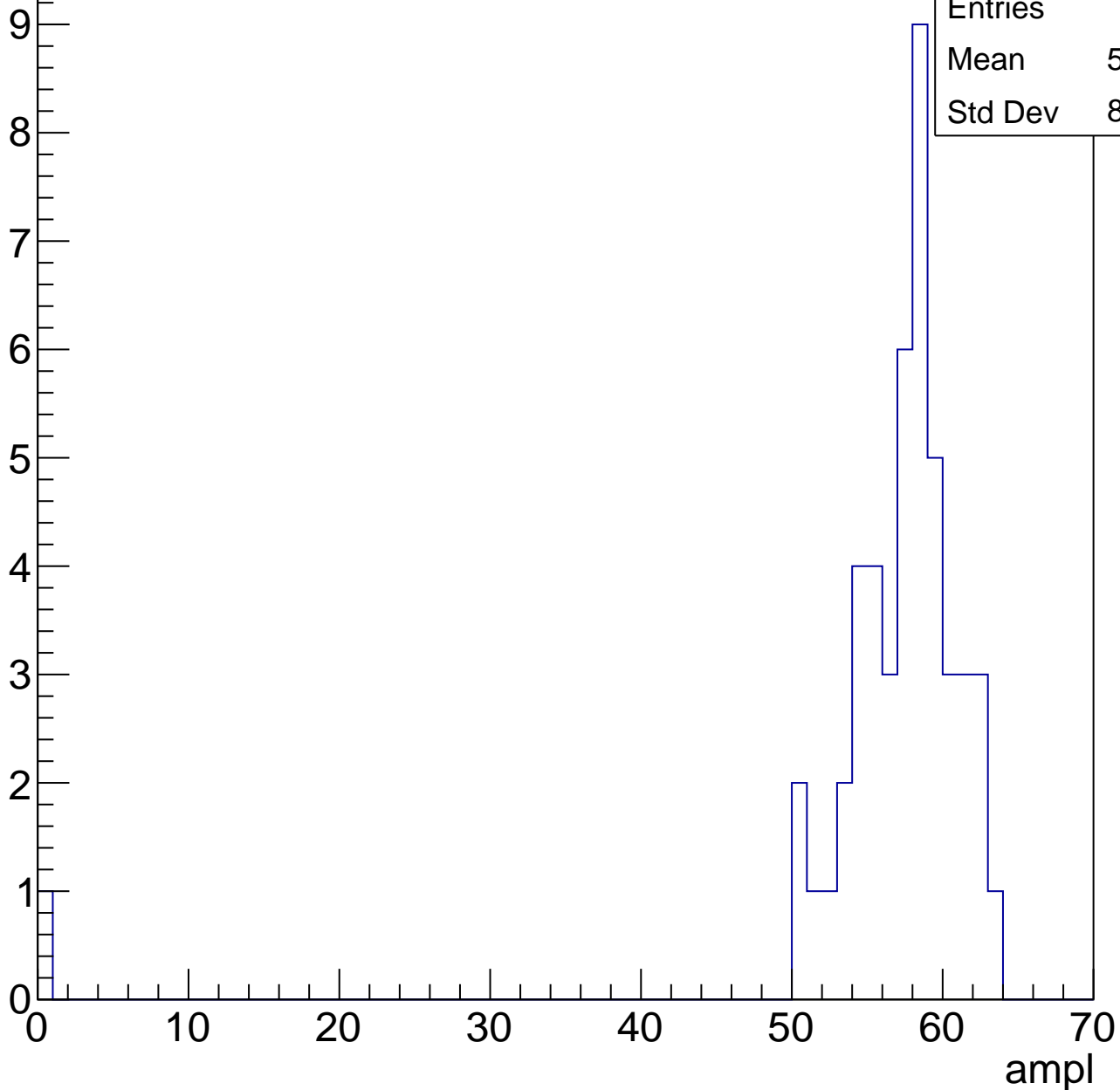


B1L103S, U8-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	55.92
Std Dev	8.725

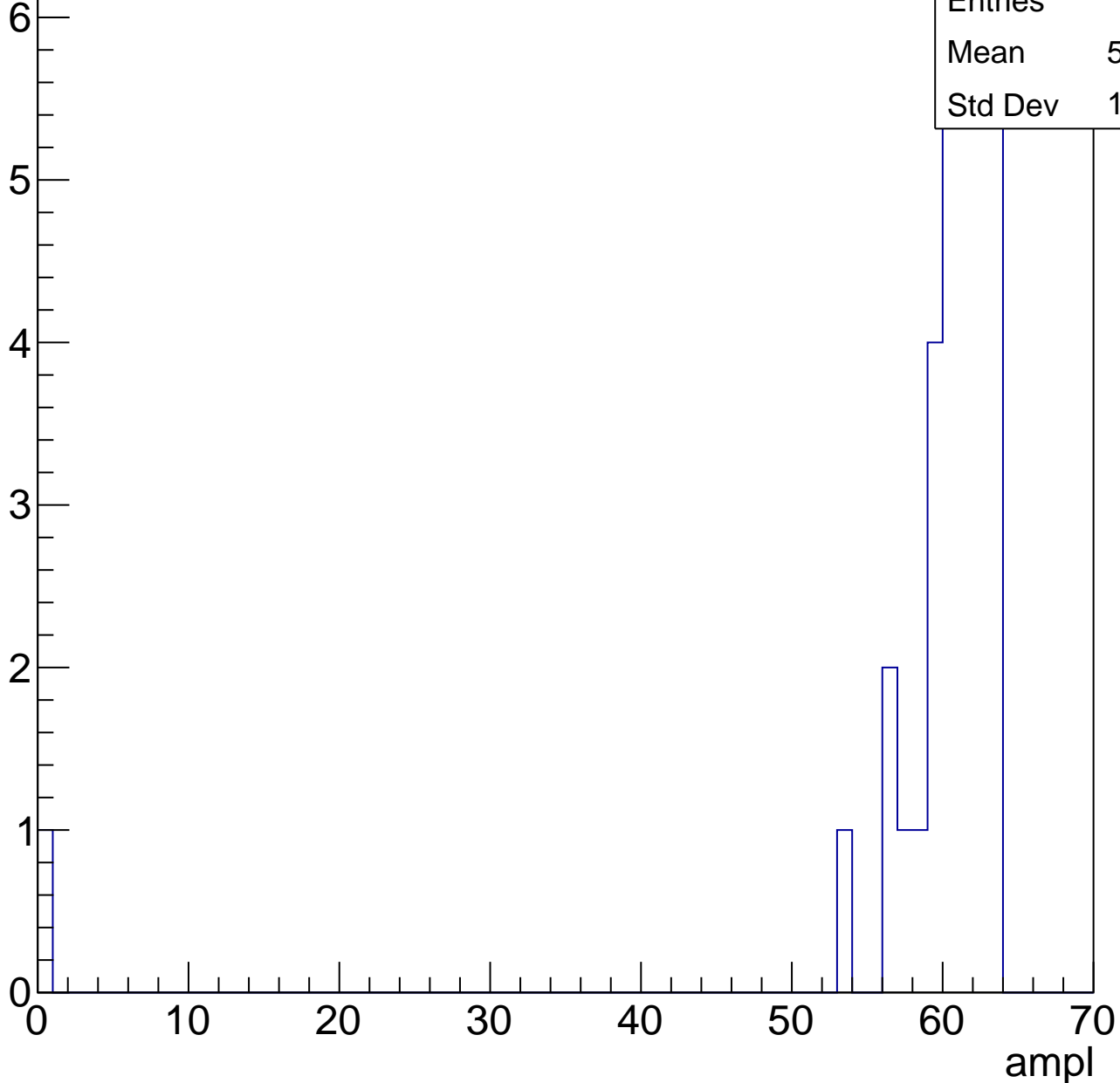


B1L103S, U8-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	58.59
Std Dev	10.45

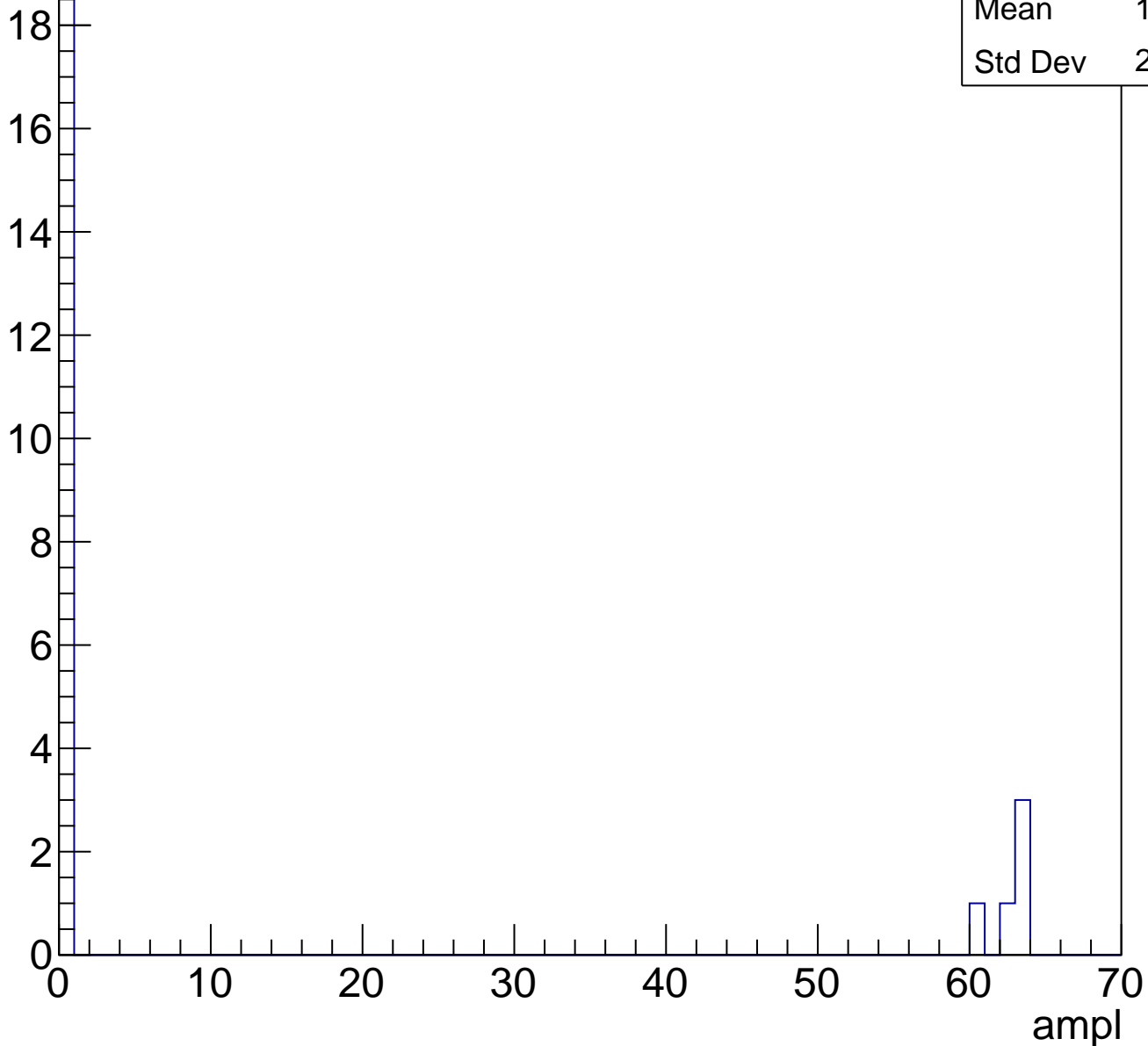


B1L103S, U8-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	12.96
Std Dev	25.27

Entry



B1L103S, U8-ch61, adc0

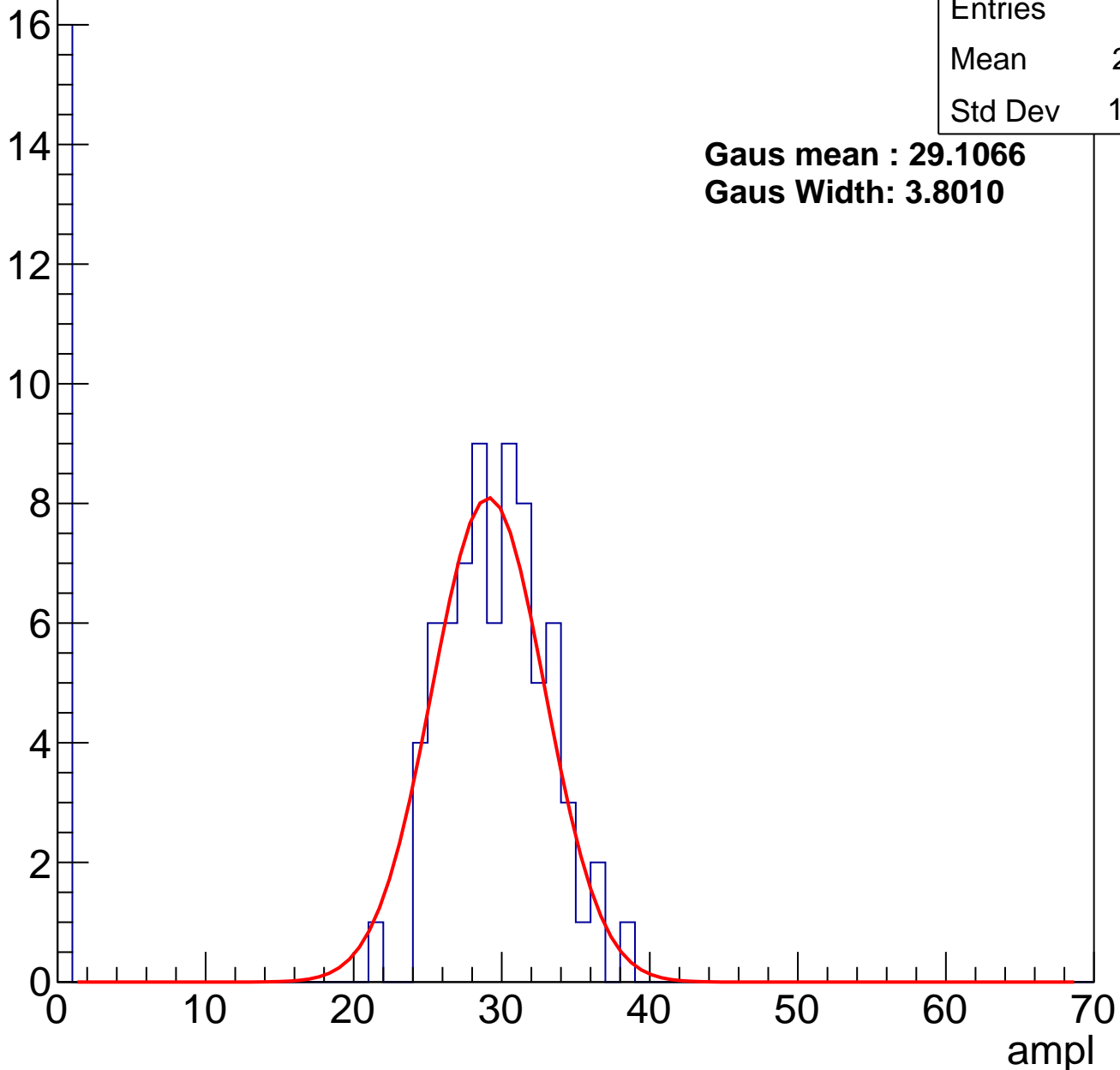
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	24.01
Std Dev	11.57

Gaus mean : 29.1066

Gaus Width: 3.8010

Entry



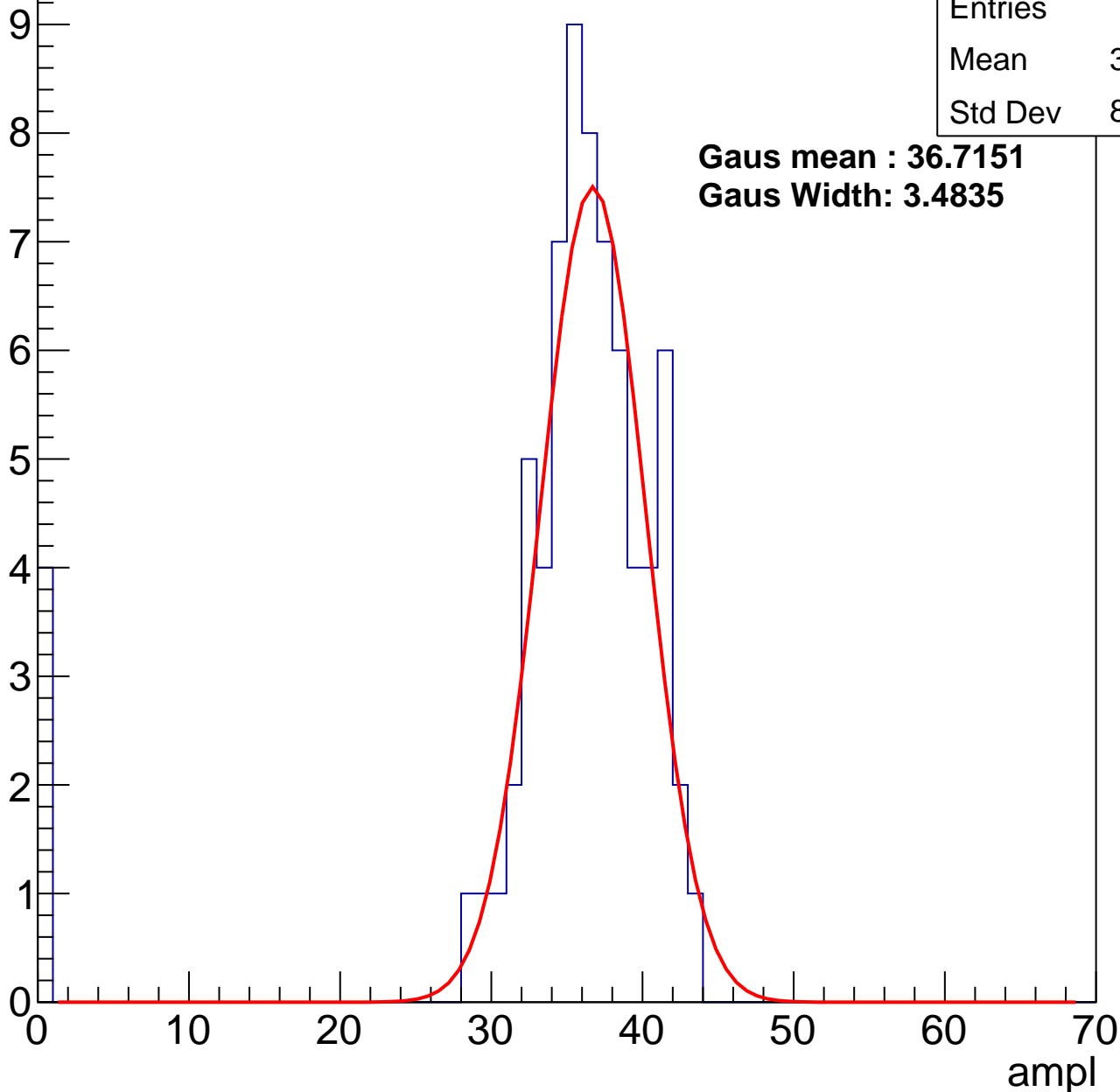
B1L103S, U8-ch61, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	34.14
Std Dev	8.889

Gaus mean : 36.7151
Gaus Width: 3.4835



B1L103S, U8-ch61, adc2

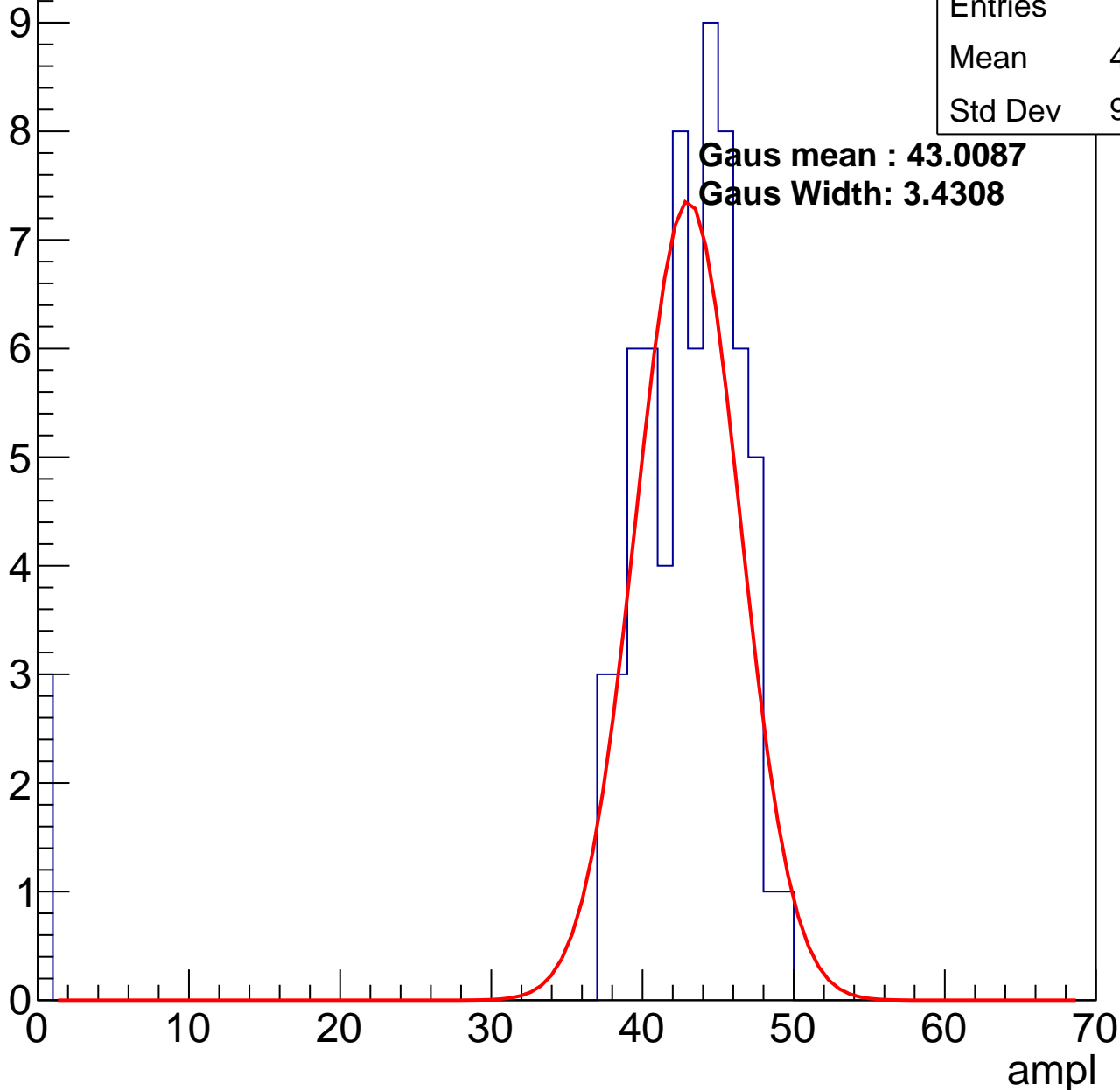
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.88
Std Dev	9.193

Gaus mean : 43.0087

Gaus Width: 3.4308

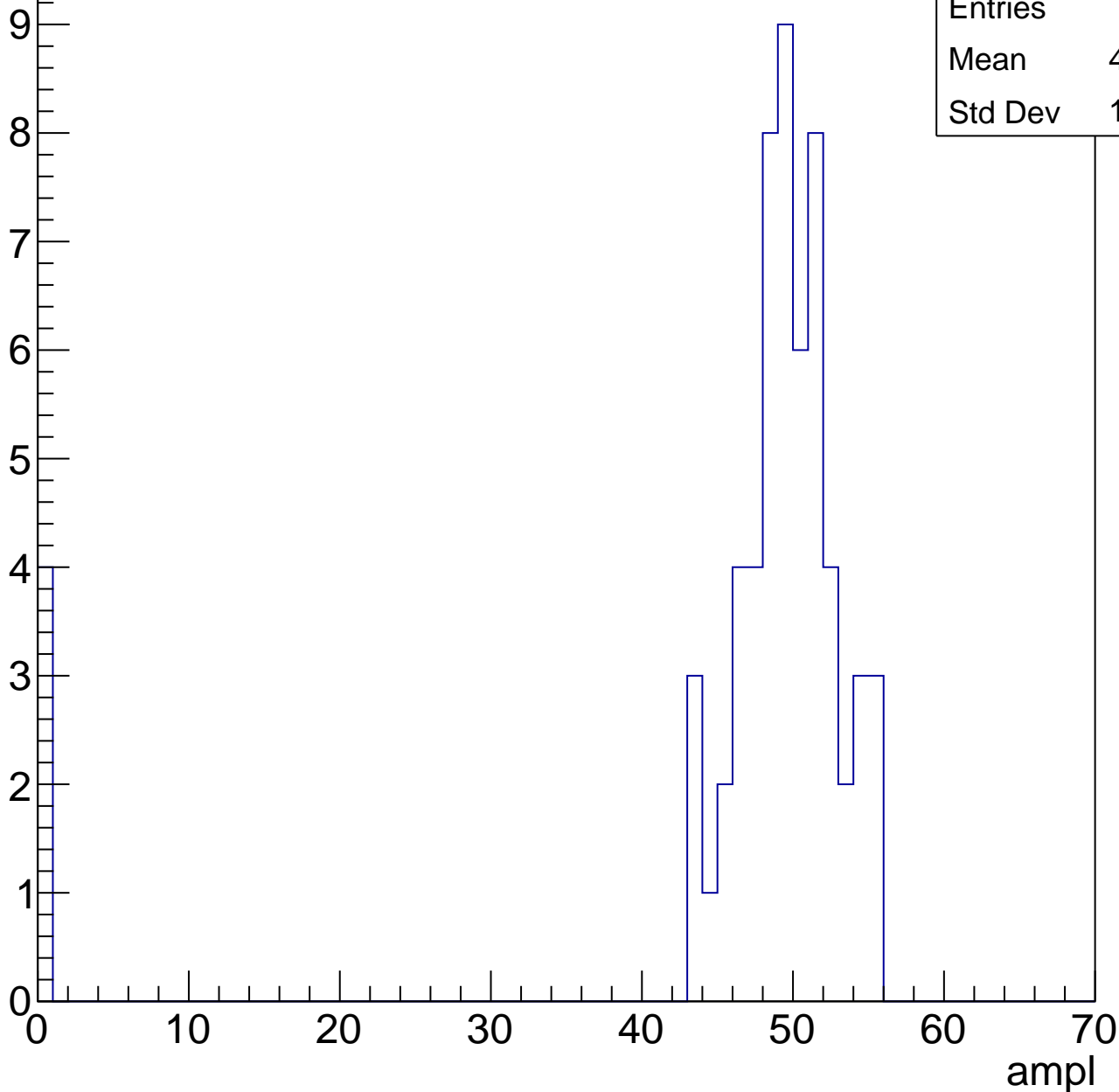


B1L103S, U8-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	46.05
Std Dev	12.54

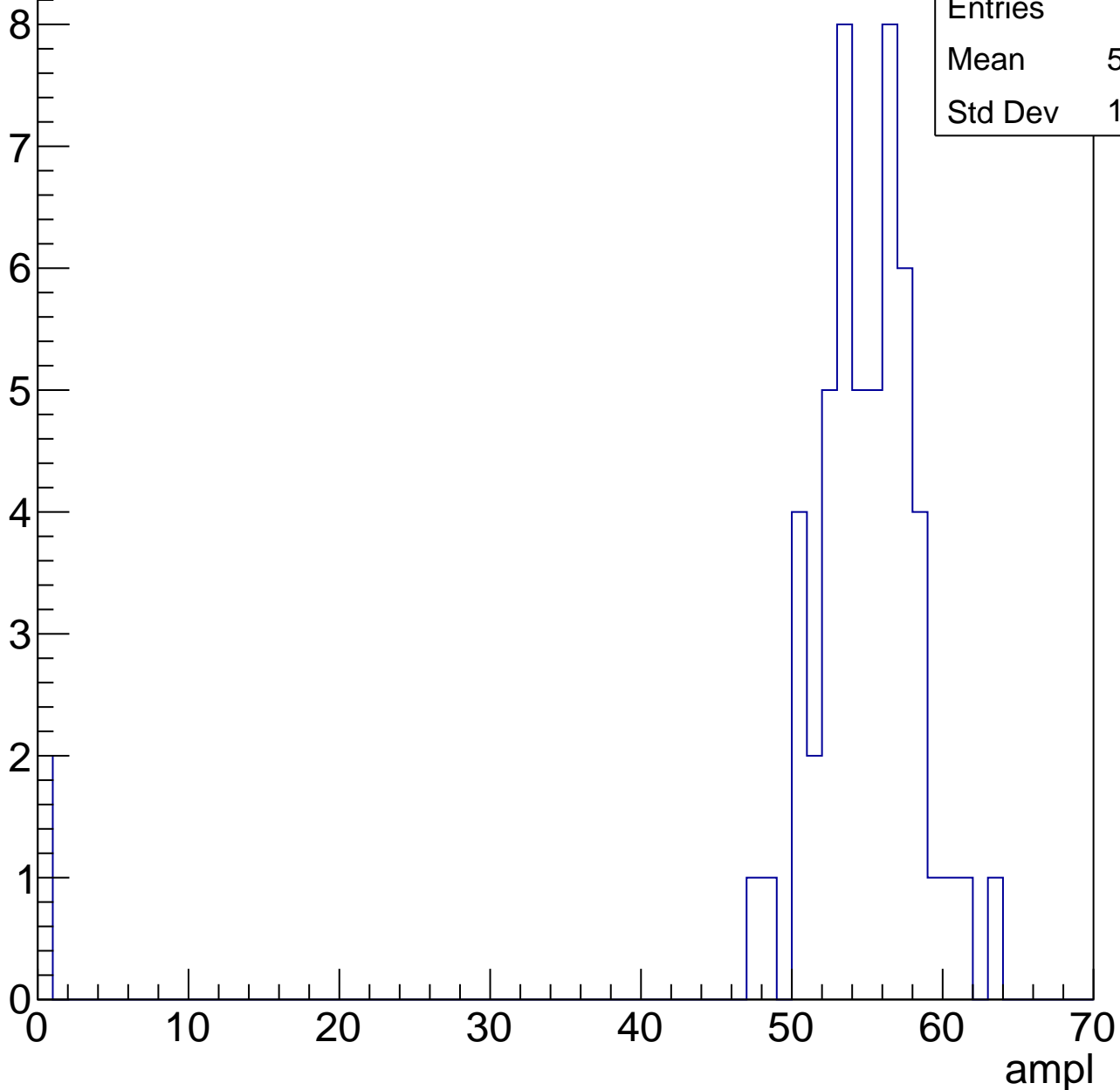


B1L103S, U8-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	52.56
Std Dev	10.67

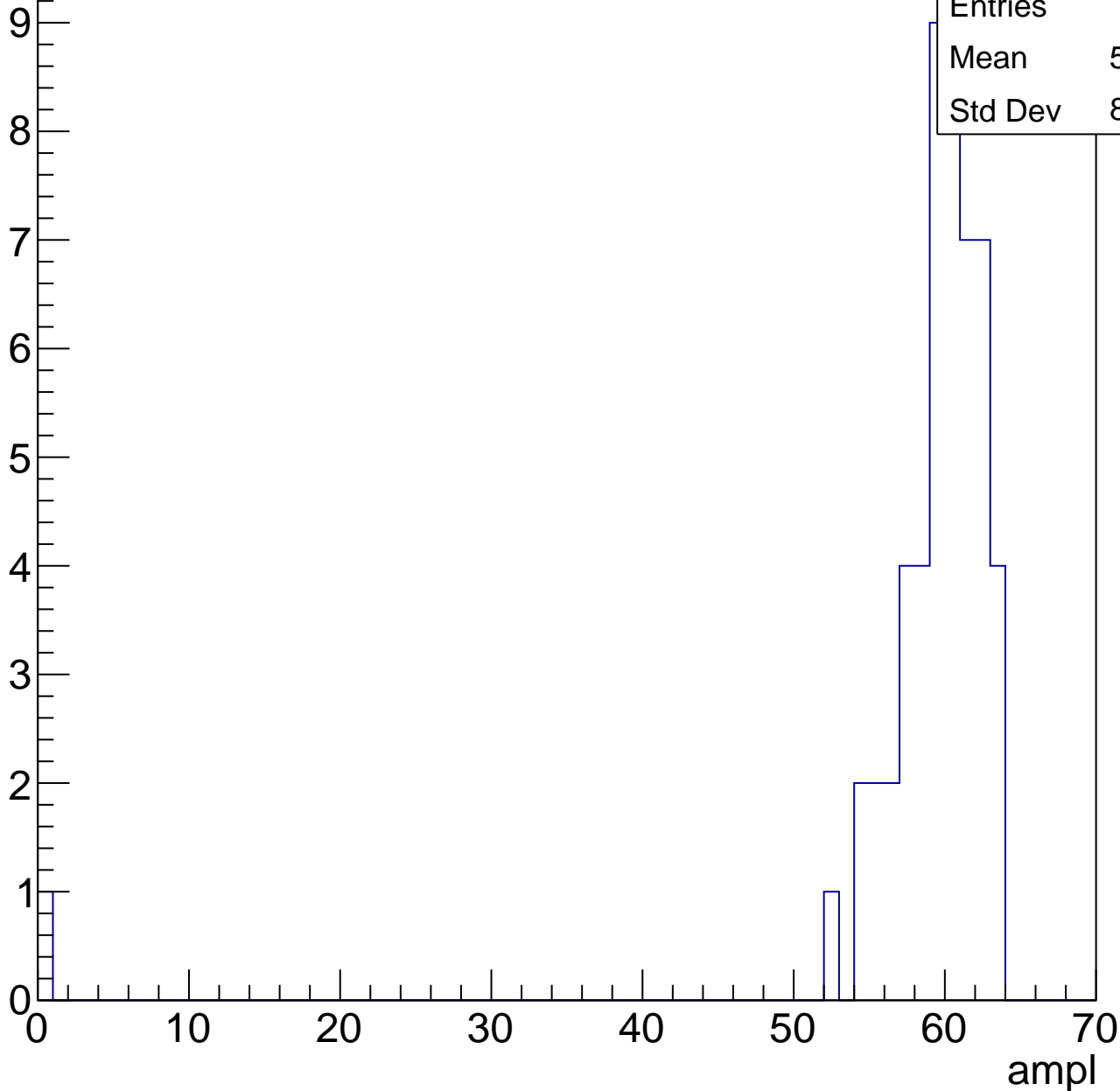


B1L103S, U8-ch61, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.19
Std Dev	8.526

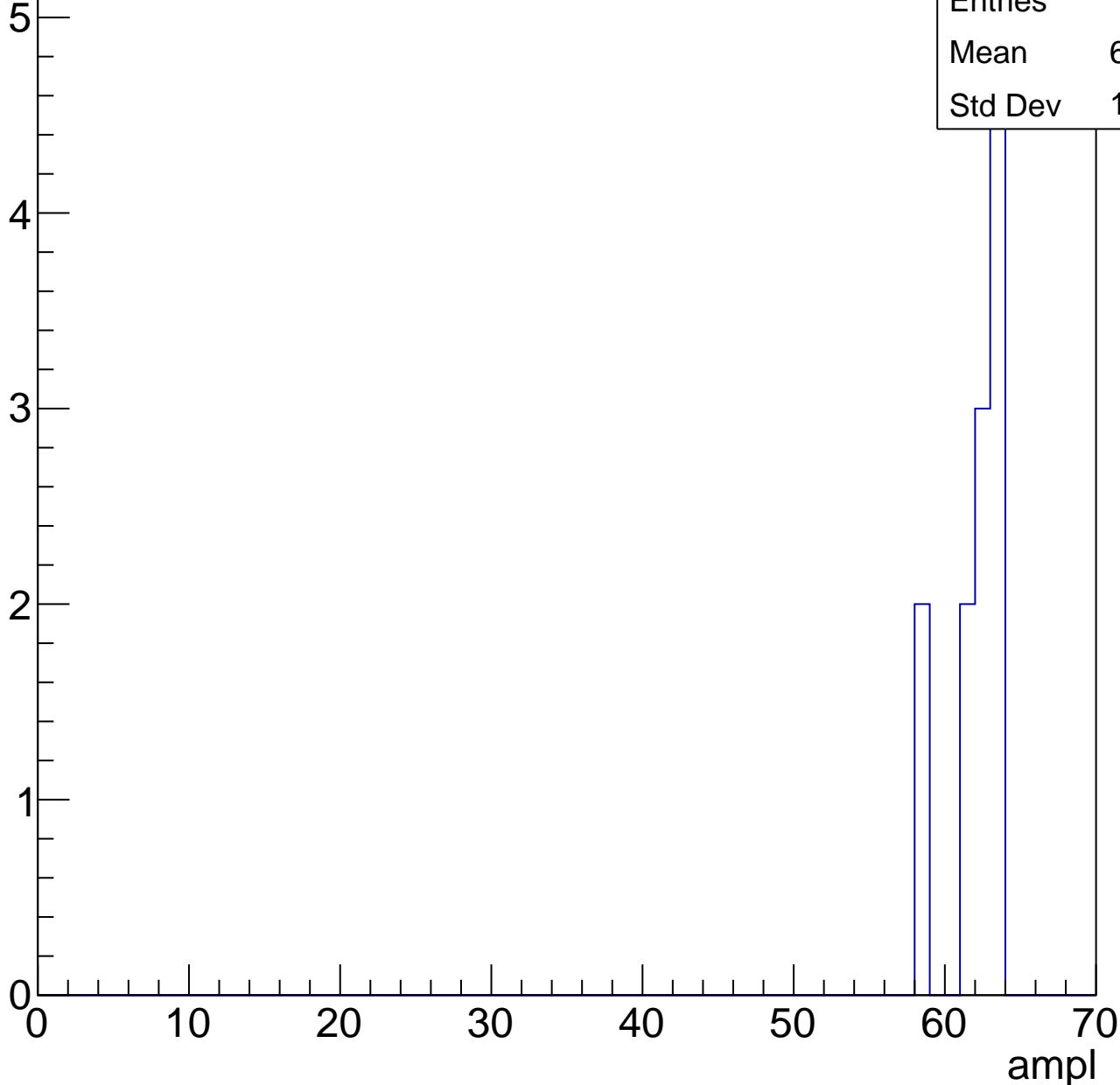


B1L103S, U8-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.754



B1L103S, U8-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch62, adc0

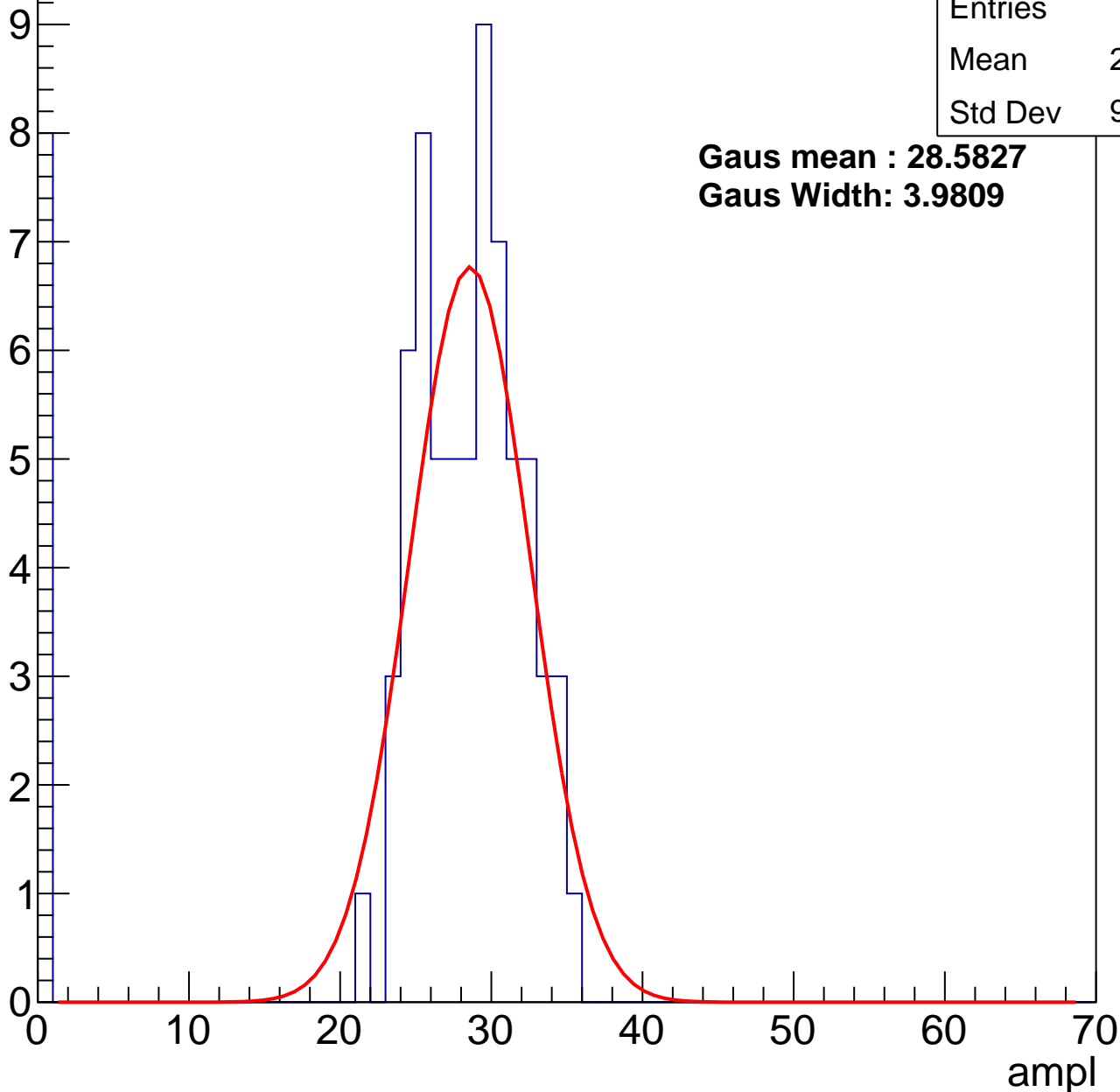
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	25.15
Std Dev	9.286

Gaus mean : 28.5827

Gaus Width: 3.9809



B1L103S, U8-ch62, adc1

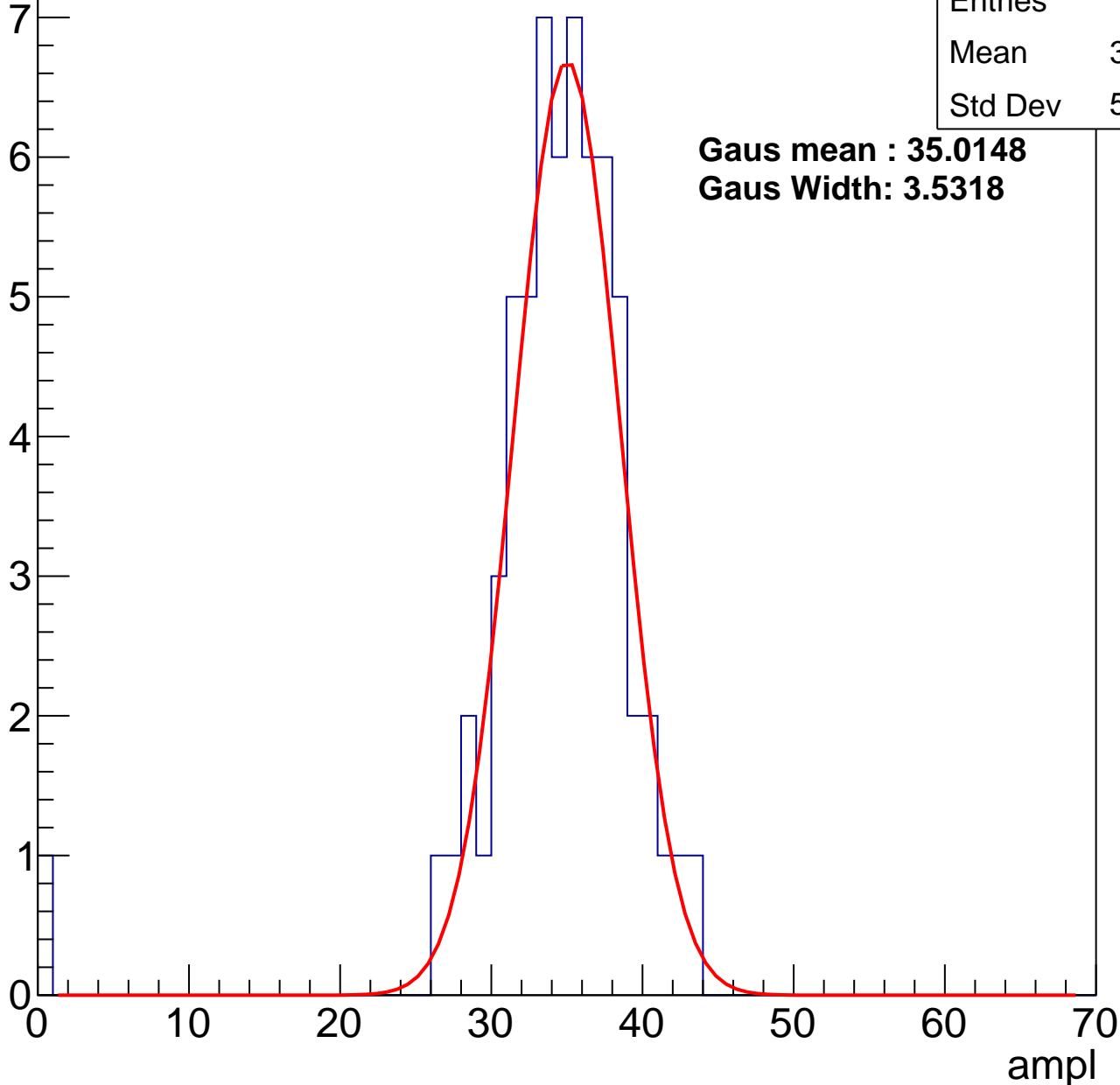
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	33.89
Std Dev	5.587

Gaus mean : 35.0148

Gaus Width: 3.5318



B1L103S, U8-ch62, adc2

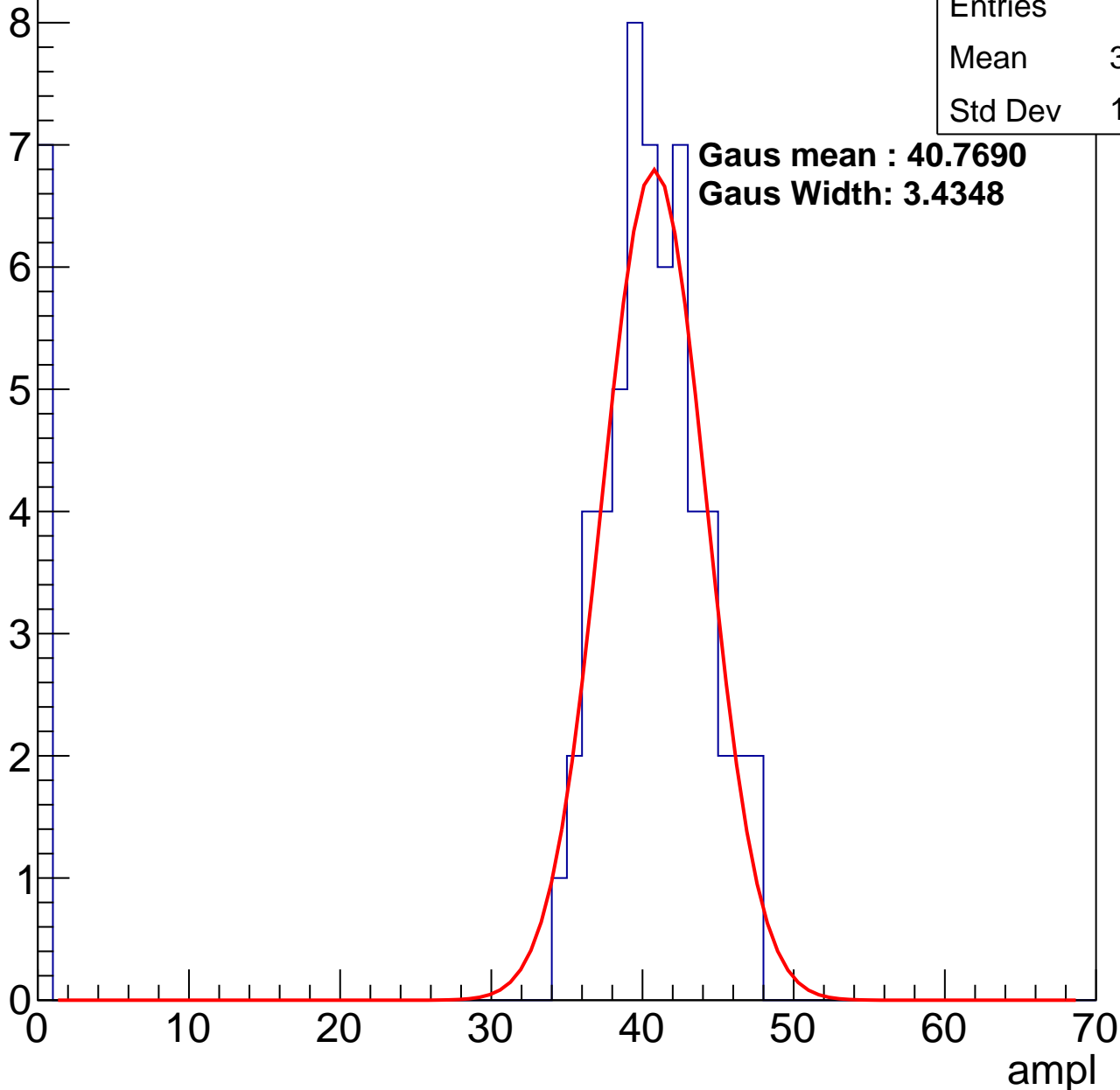
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.03
Std Dev	12.86

Gaus mean : 40.7690

Gaus Width: 3.4348

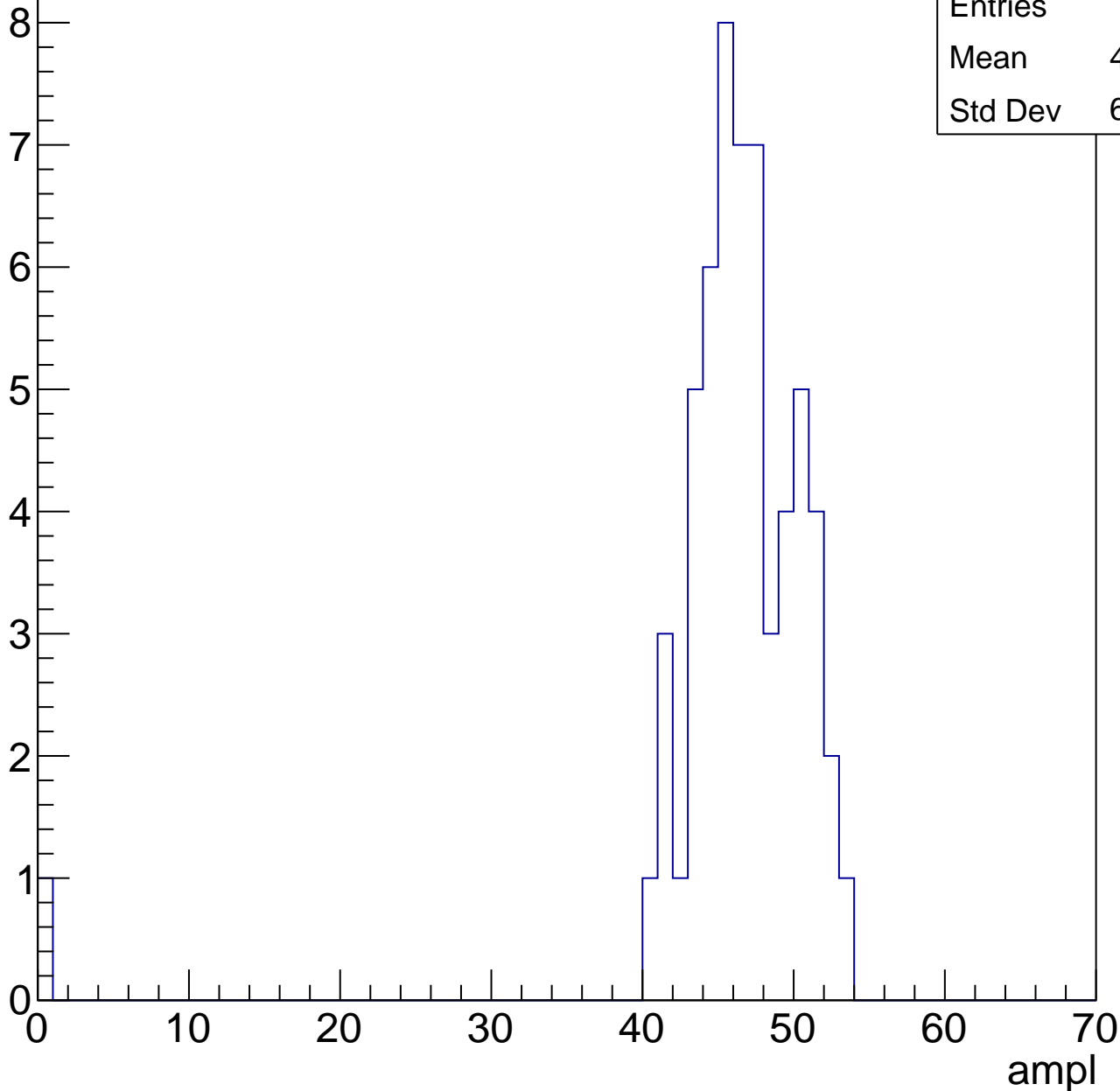


B1L103S, U8-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	45.62
Std Dev	6.782

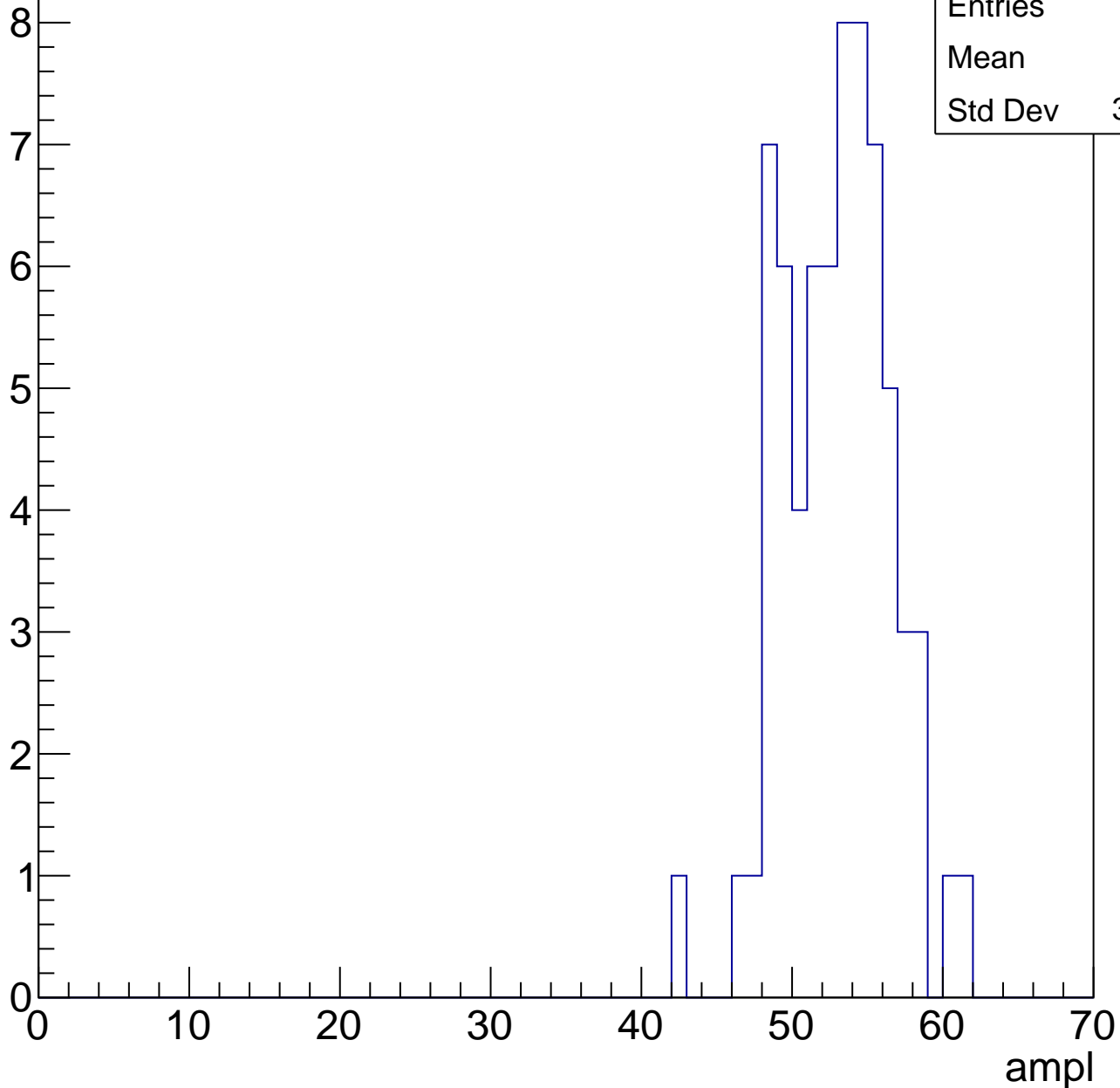


B1L103S, U8-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	52.5
Std Dev	3.521

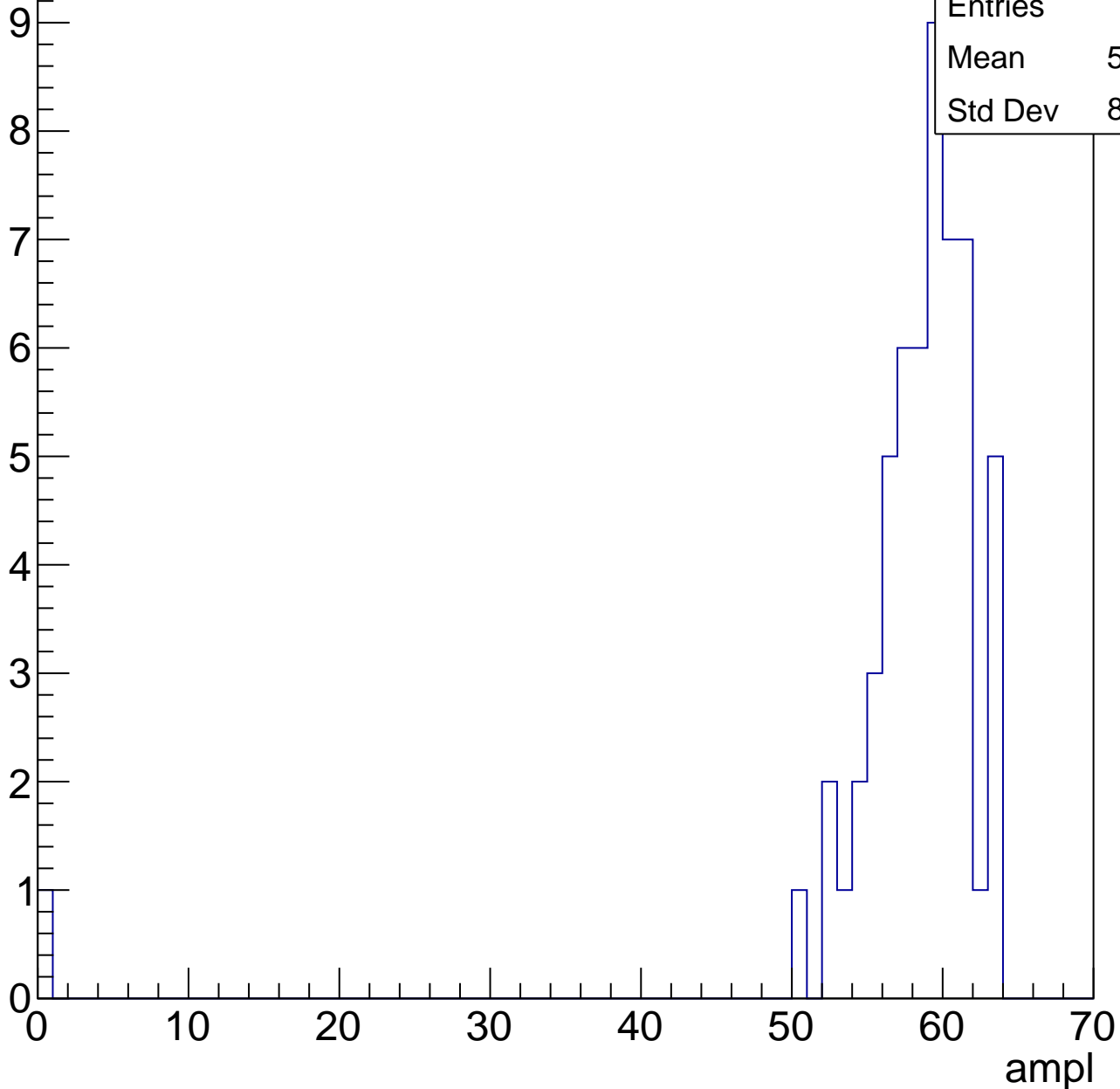


B1L103S, U8-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	57.23
Std Dev	8.257

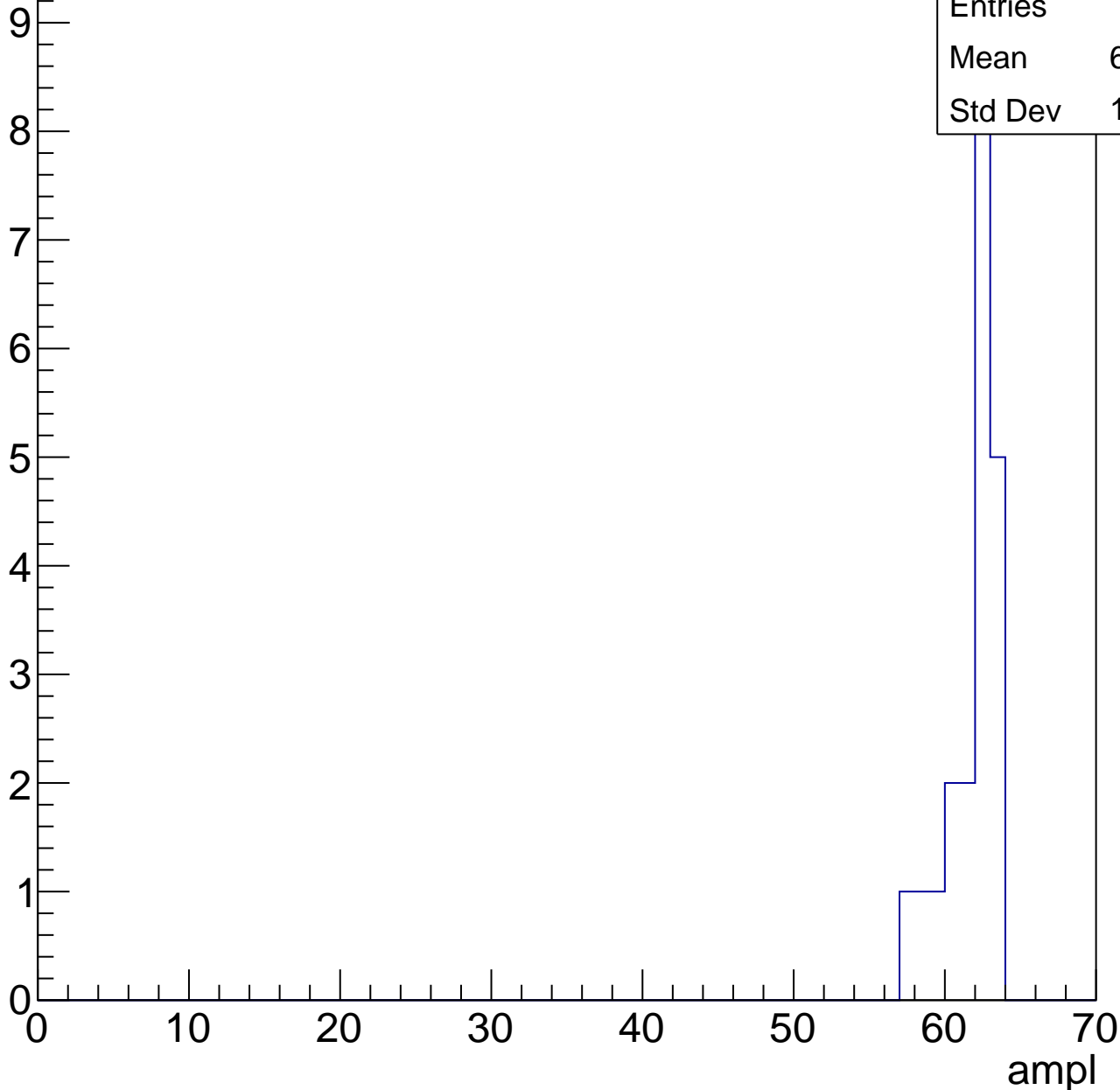


B1L103S, U8-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	61.38
Std Dev	1.647



B1L103S, U8-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.111
Std Dev	4.581

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U8-ch63, adc0

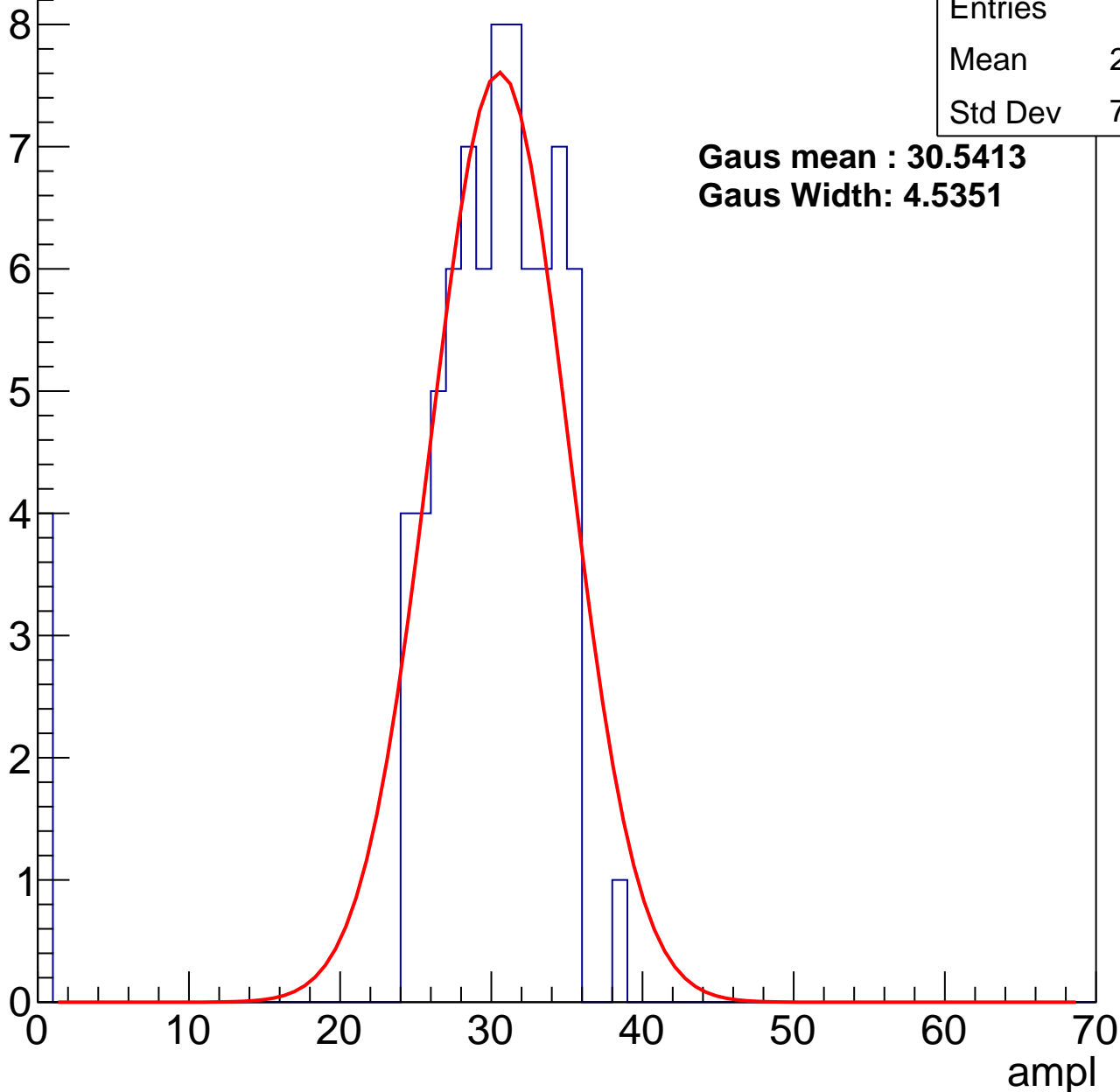
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	28.49
Std Dev	7.378

Gaus mean : 30.5413

Gaus Width: 4.5351



B1L103S, U8-ch63, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	33.41
Std Dev	11.79

Gaus mean : 37.6931

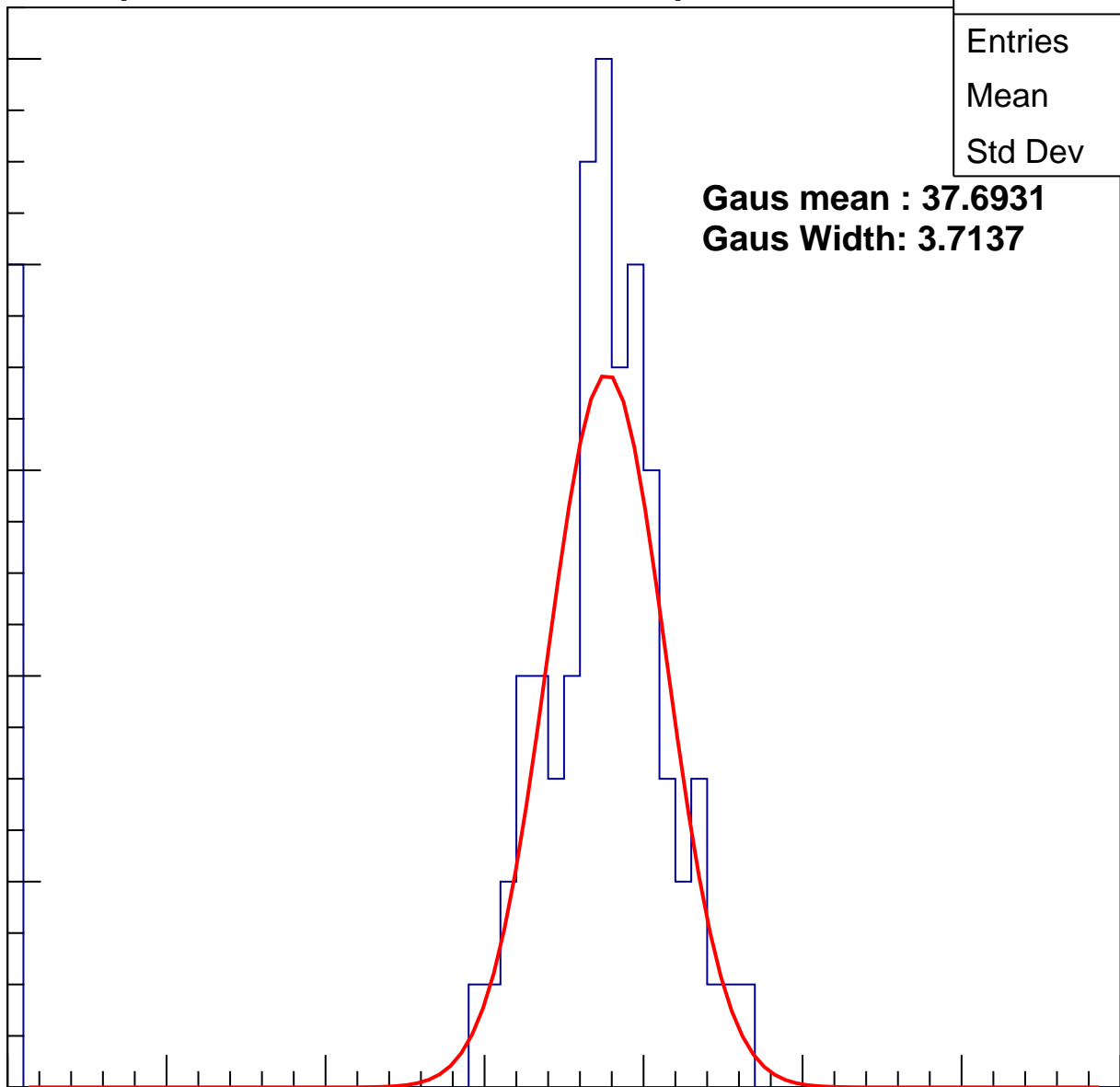
Gaus Width: 3.7137

Entry

10
8
6
4
2
0

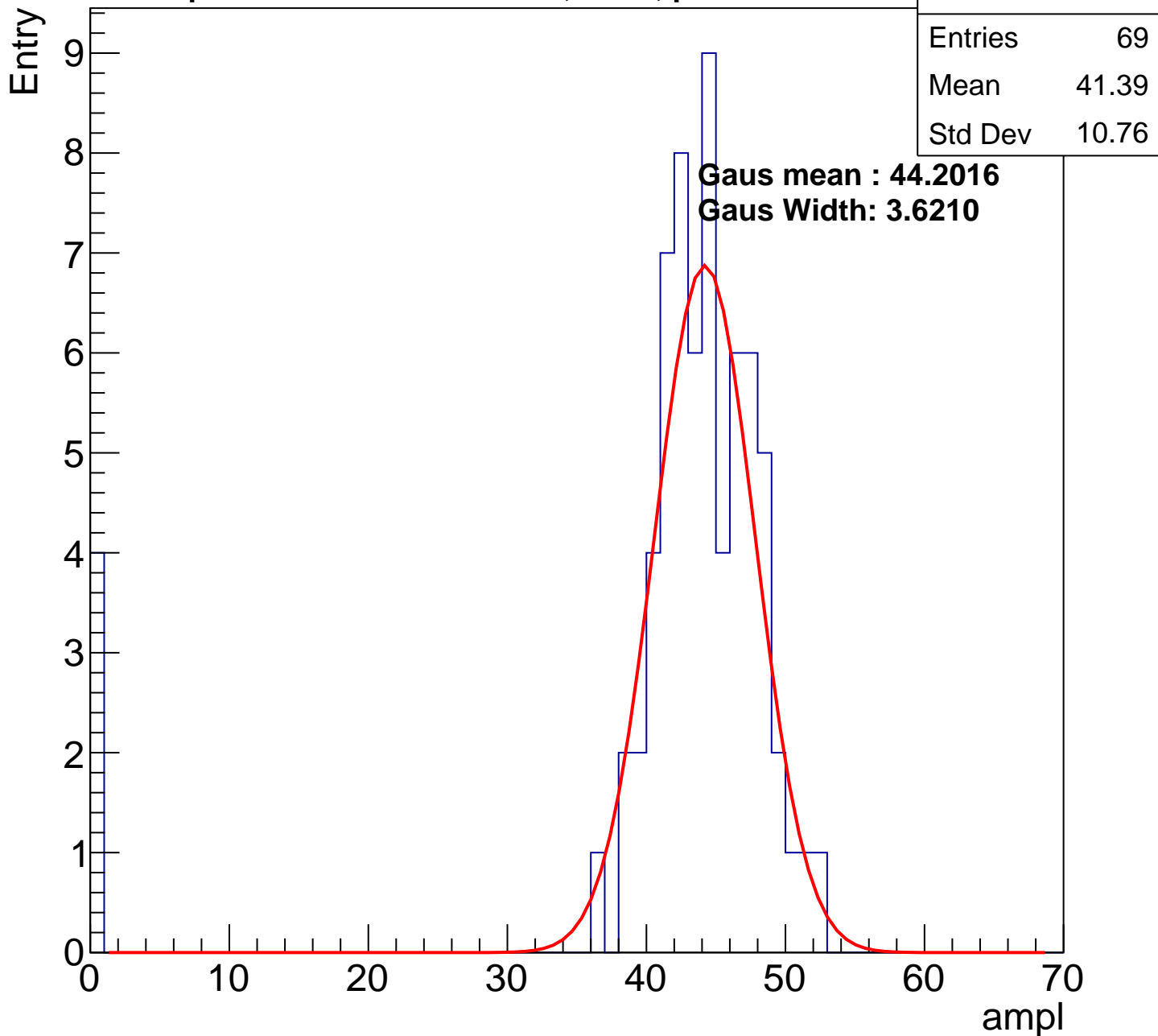
0 10 20 30 40 50 60 70

ampl



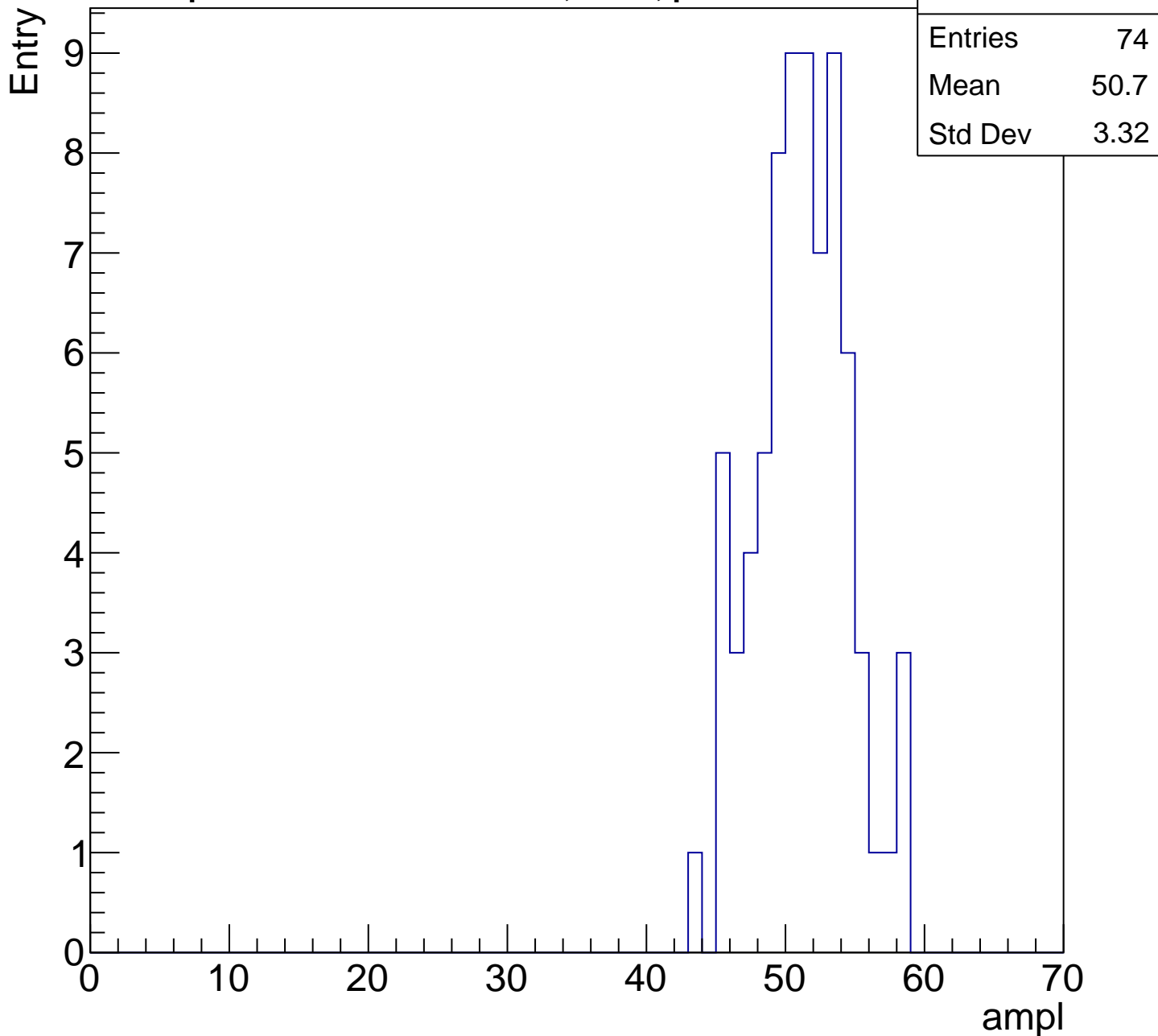
B1L103S, U8-ch63, adc2

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch63, adc3

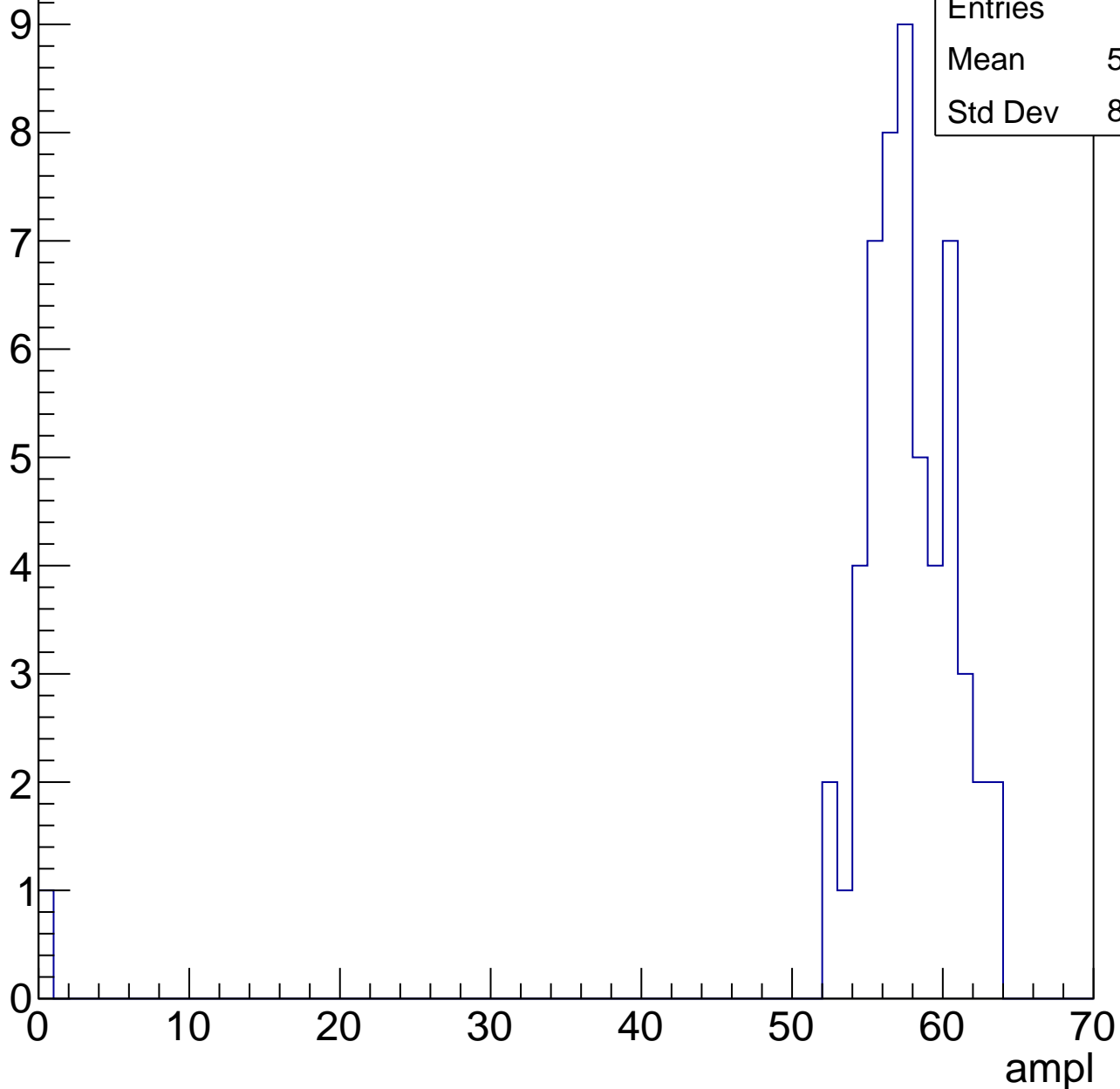
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

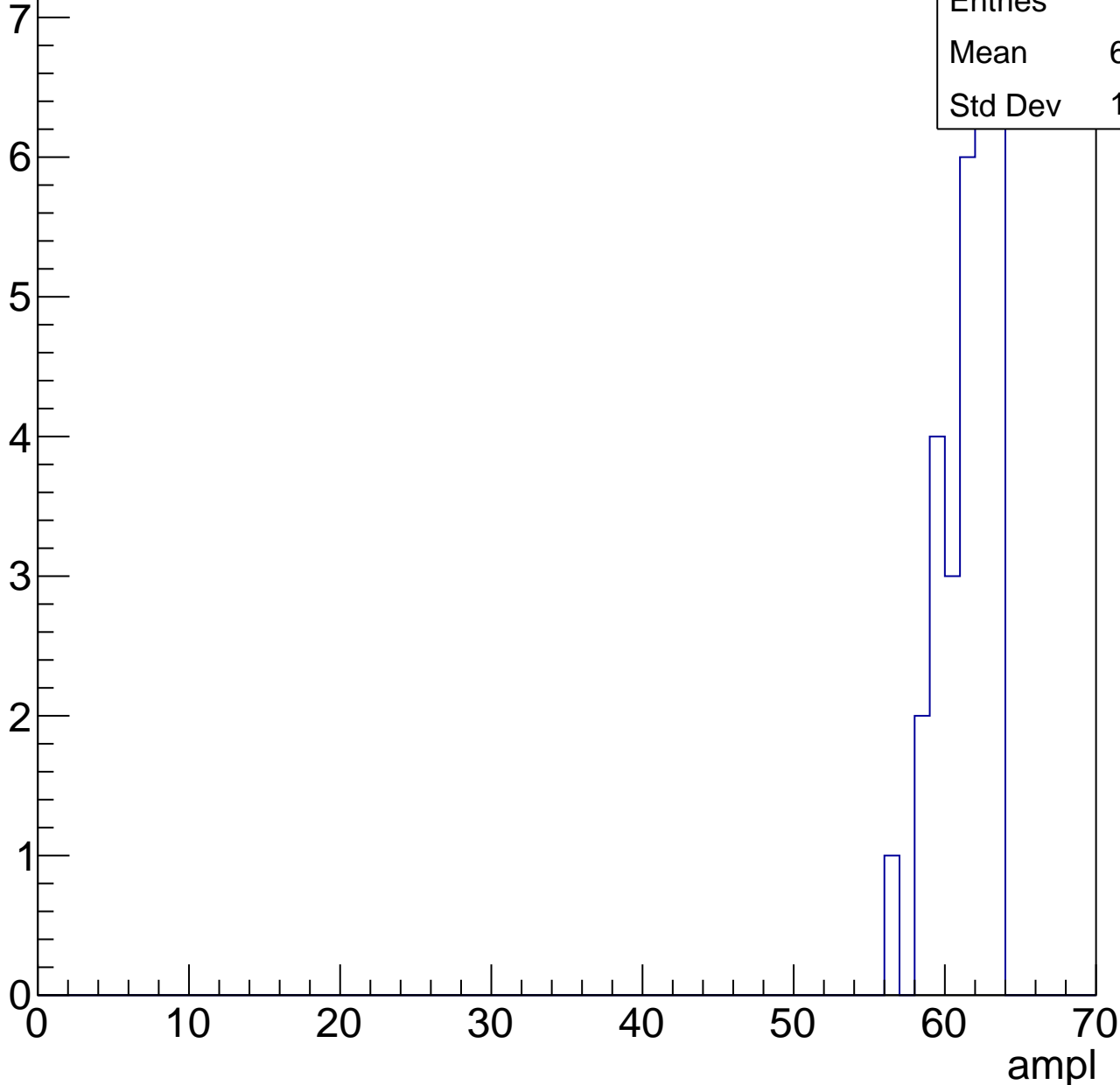


B1L103S, U8-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	60.97
Std Dev	1.798



B1L103S, U8-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

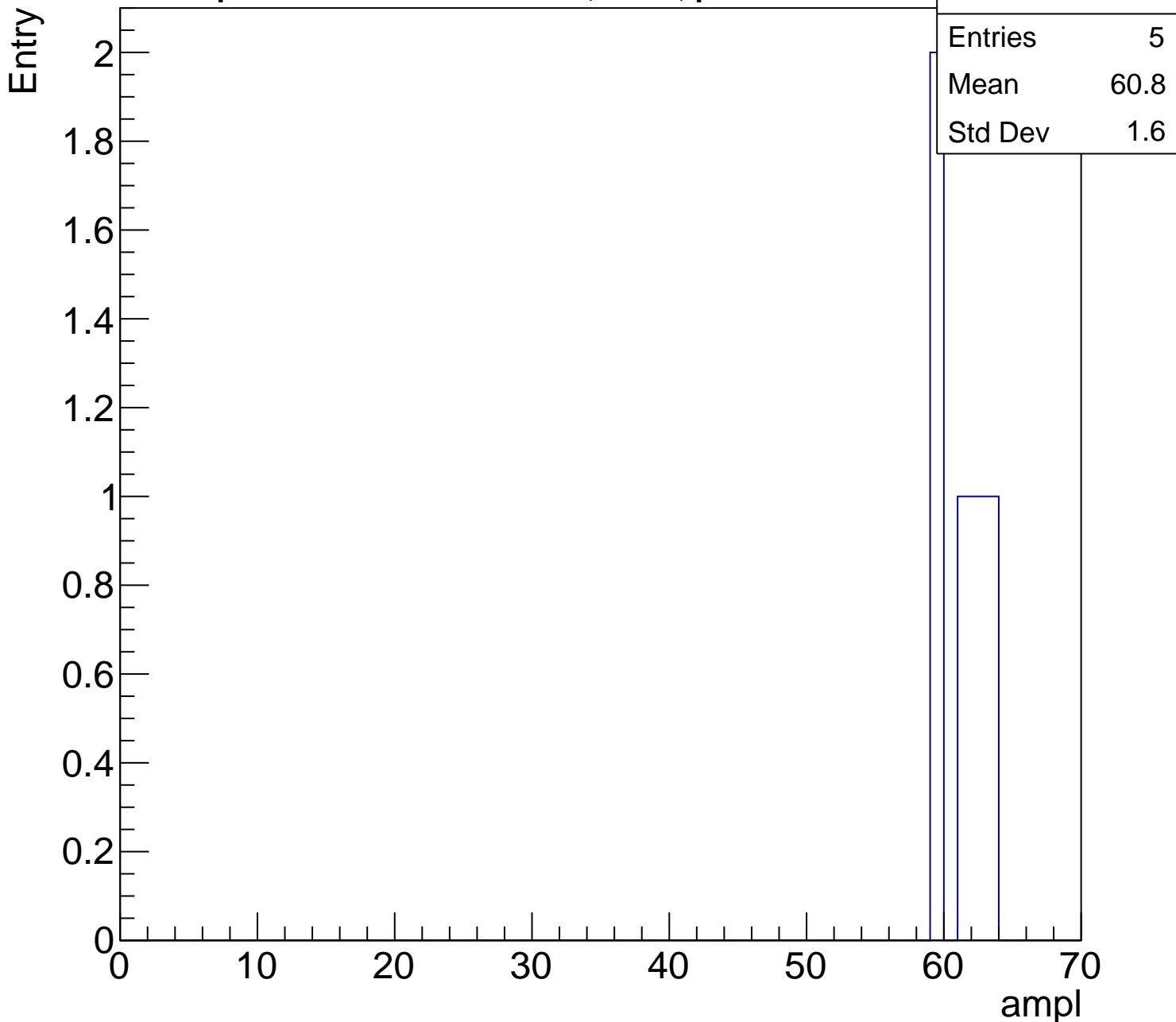
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.8
Std Dev	1.6

0 10 20 30 40 50 60 70

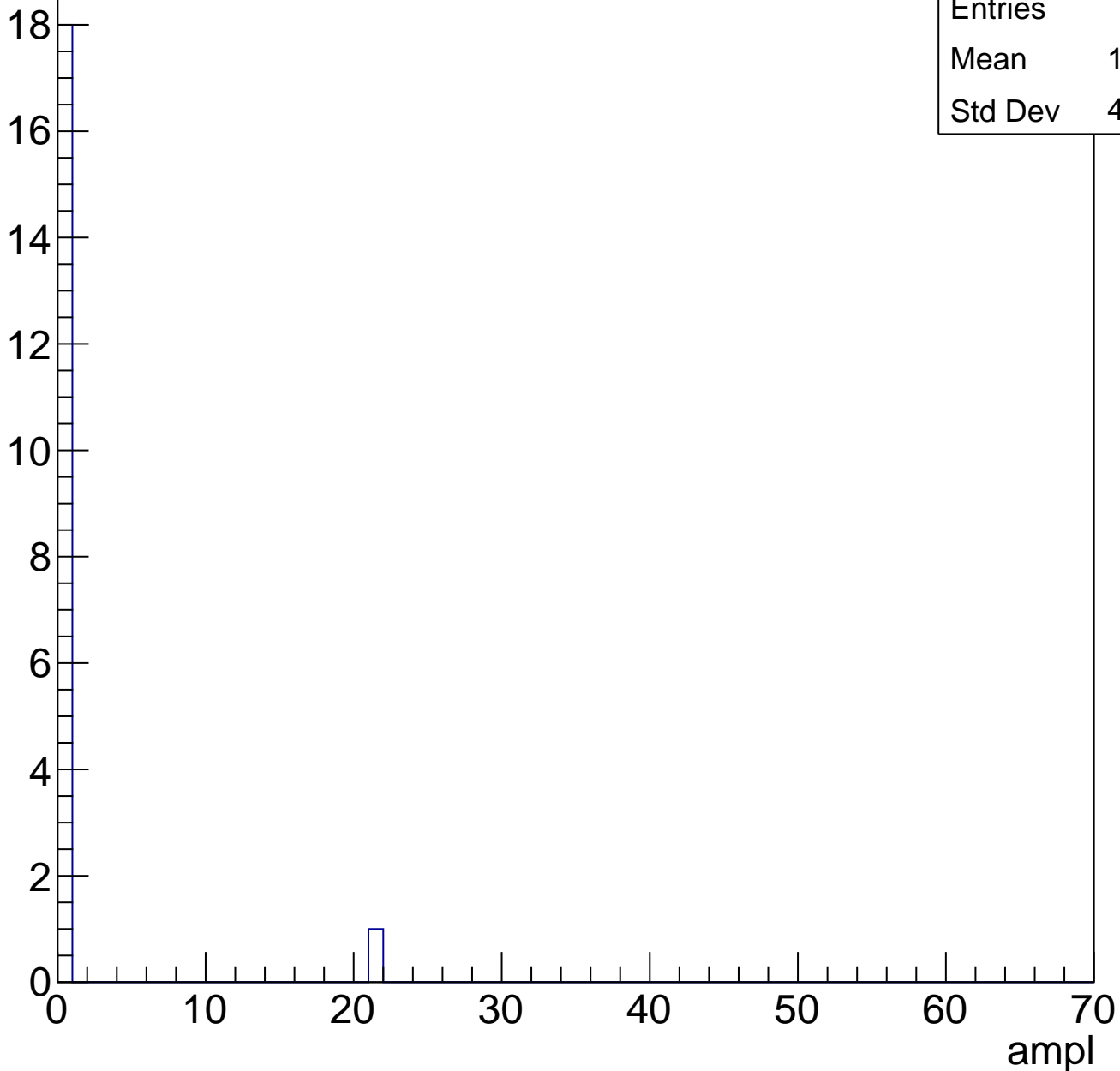
ampl



B1L103S, U8-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1.105
Std Dev	4.689

B1L103S, U8-ch64, adc0

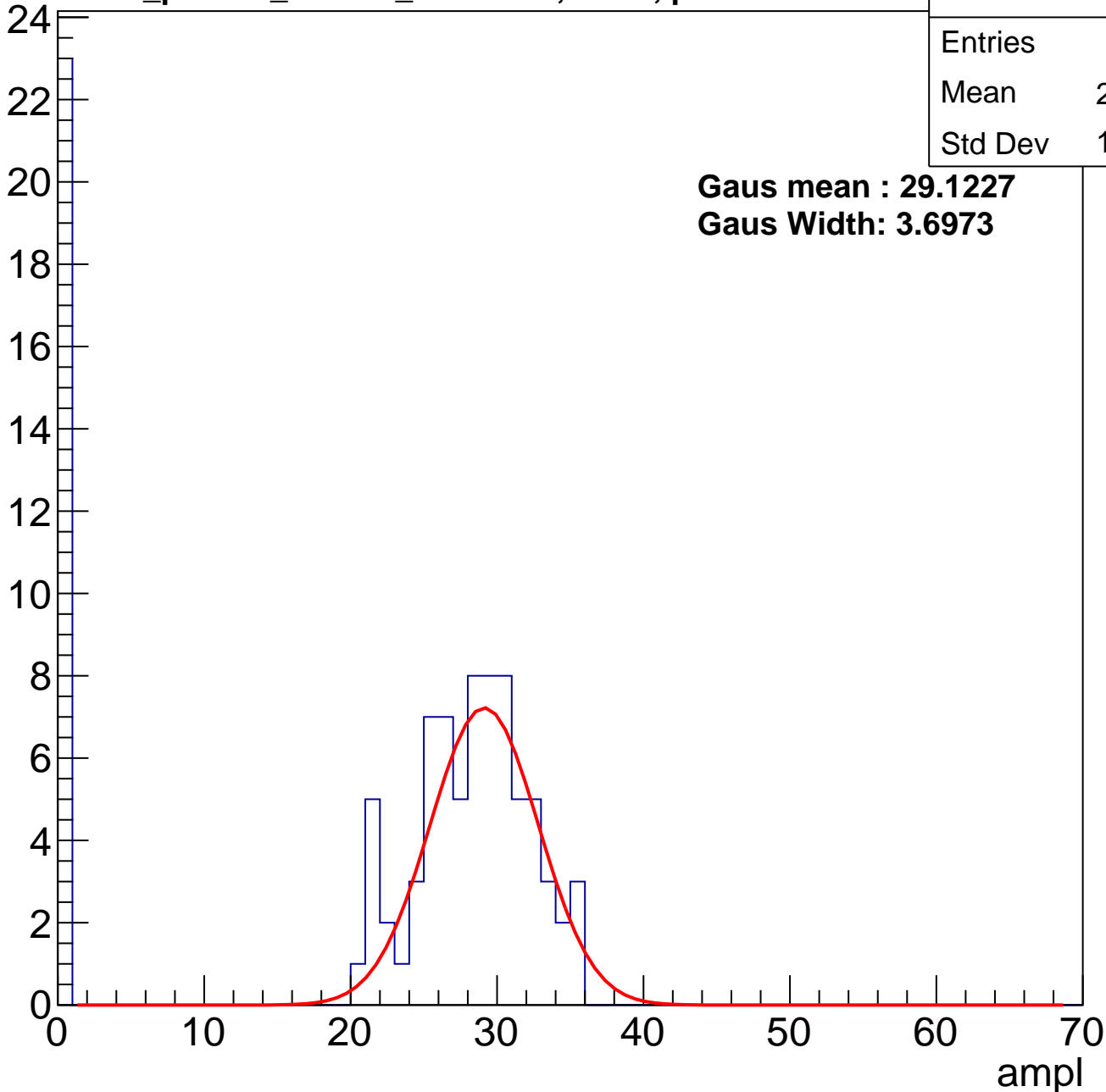
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	21.24
Std Dev	12.35

Gaus mean : 29.1227

Gaus Width: 3.6973

Entry



B1L103S, U8-ch64, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	28.84
Std Dev	13.36

Gaus mean : 34.9688

Gaus Width: 3.1548

Entry

12

10

8

6

4

2

0

0

10

20

30

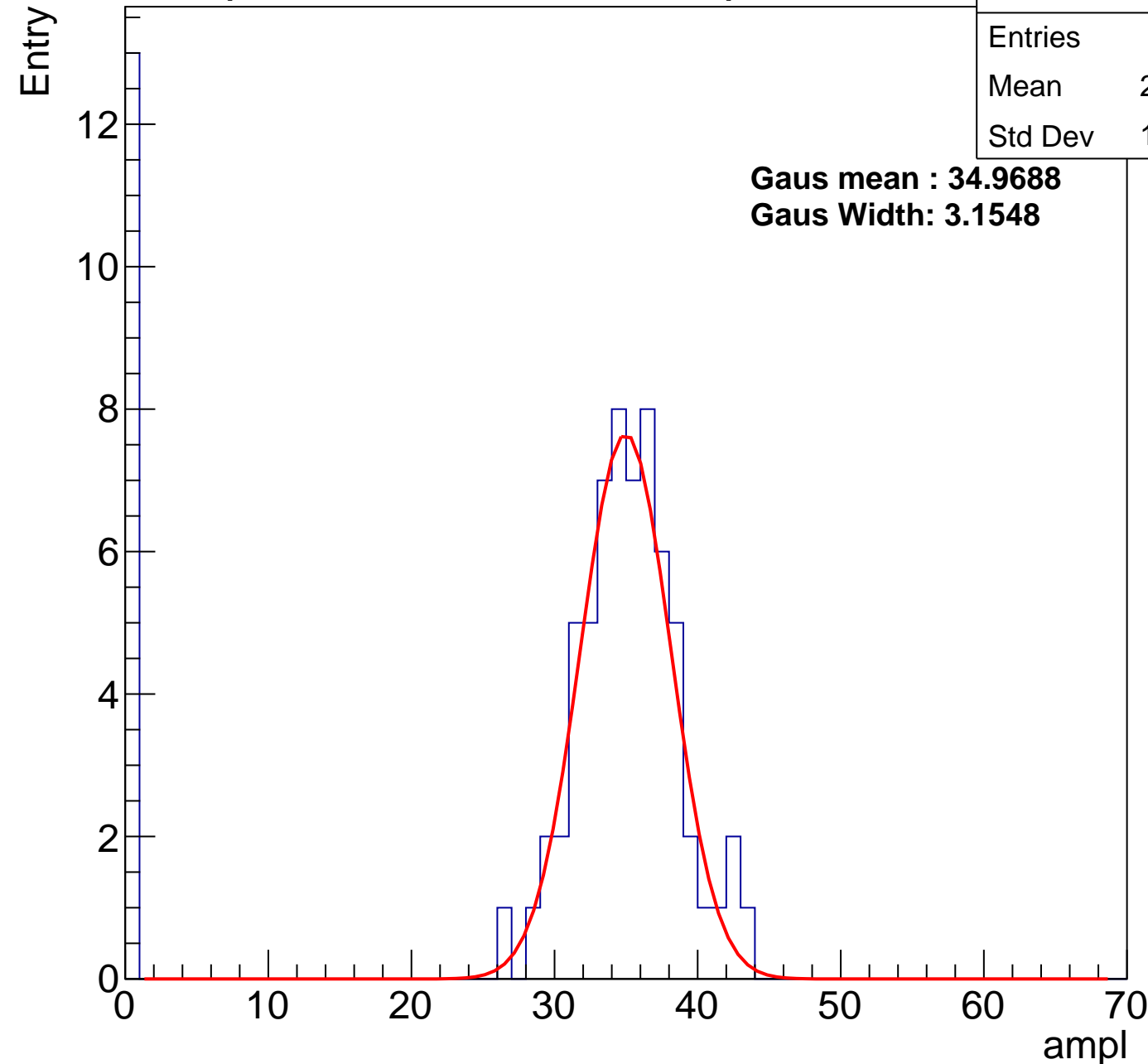
40

50

60

70

ampl



B1L103S, U8-ch64, adc2

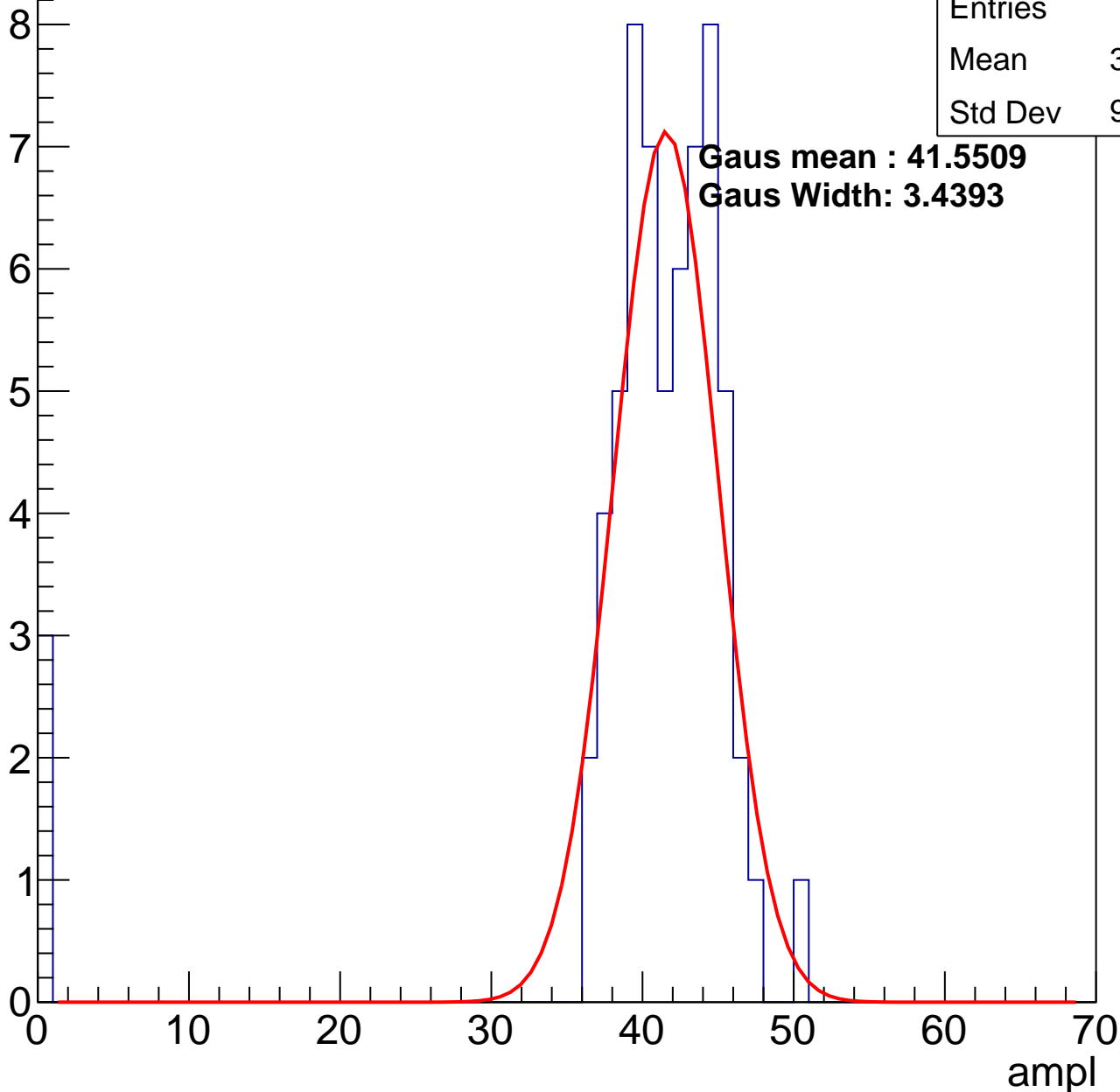
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	39.47
Std Dev	9.223

Gaus mean : 41.5509

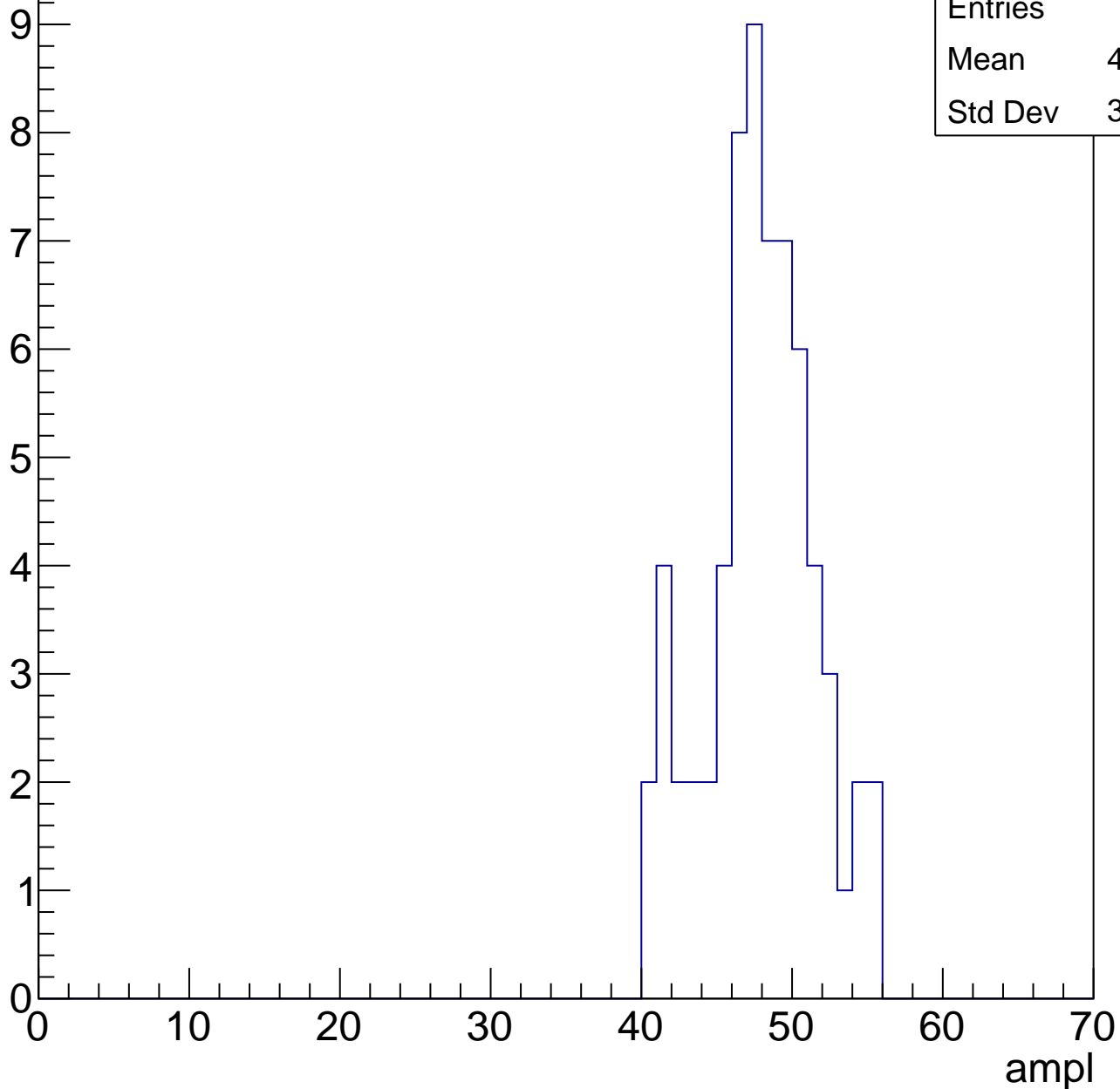
Gaus Width: 3.4393



B1L103S, U8-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

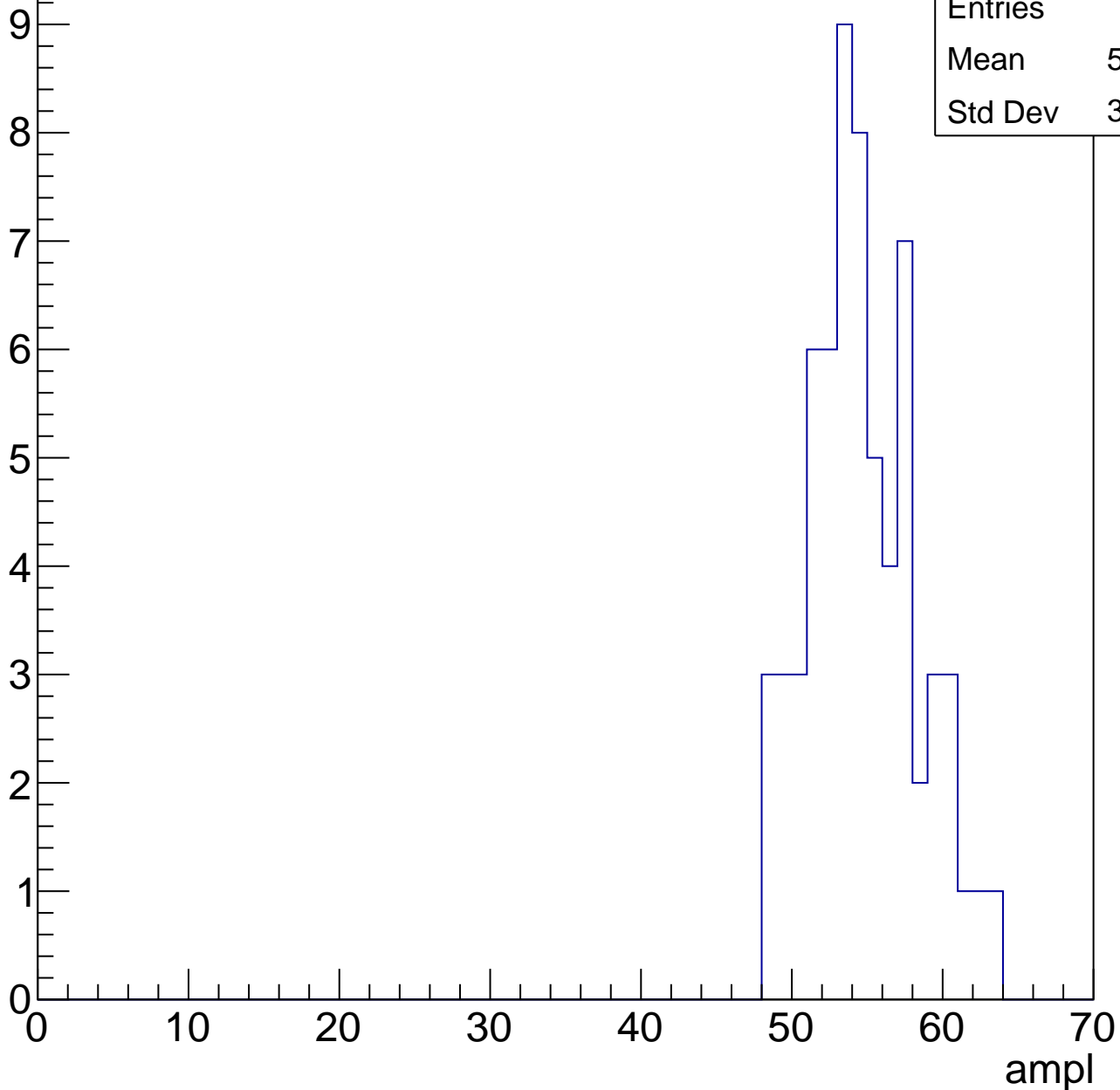


B1L103S, U8-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	54.23
Std Dev	3.525

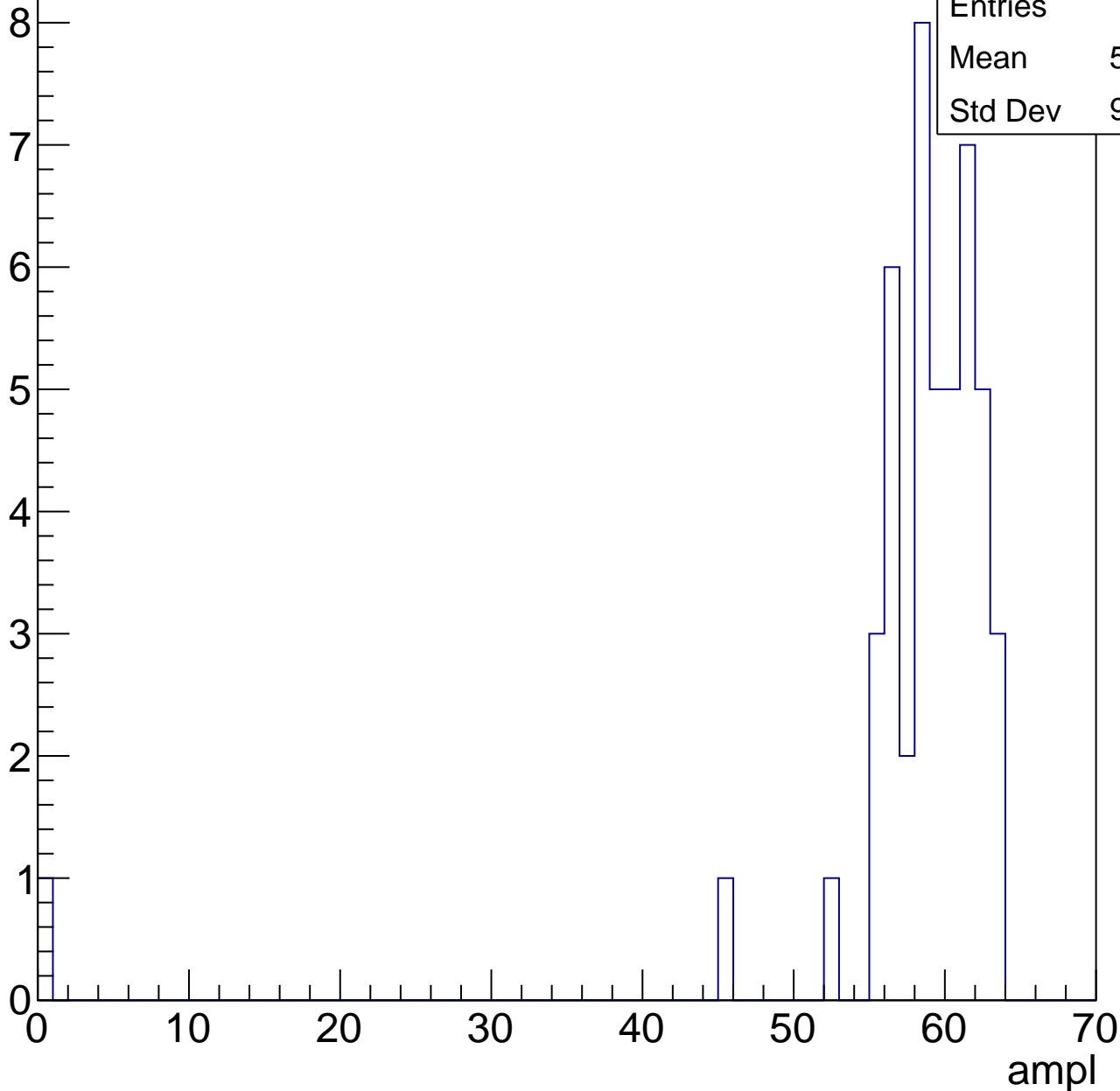


B1L103S, U8-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	57.38
Std Dev	9.047

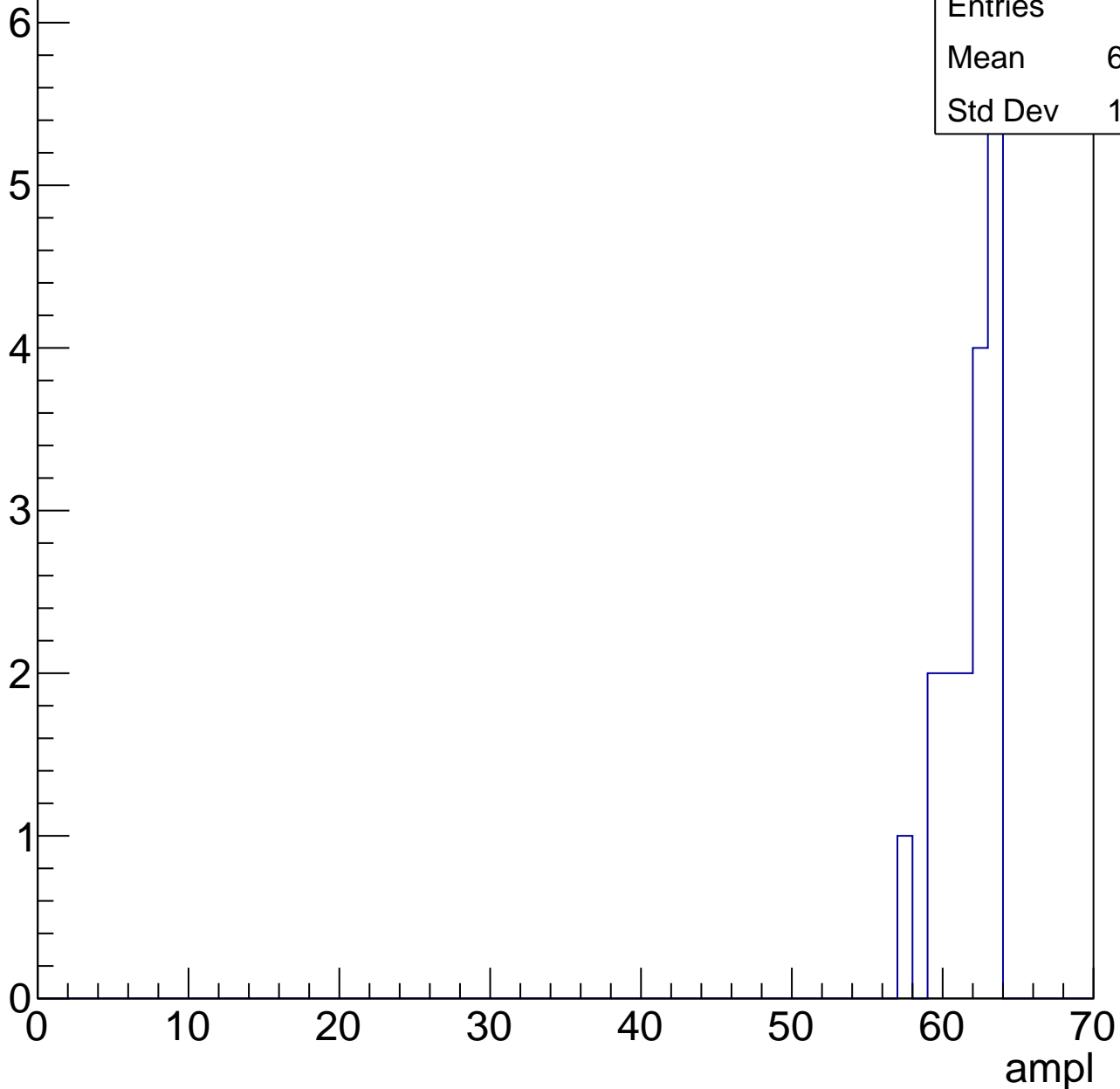


B1L103S, U8-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.35
Std Dev	1.747



B1L103S, U8-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch65, adc0

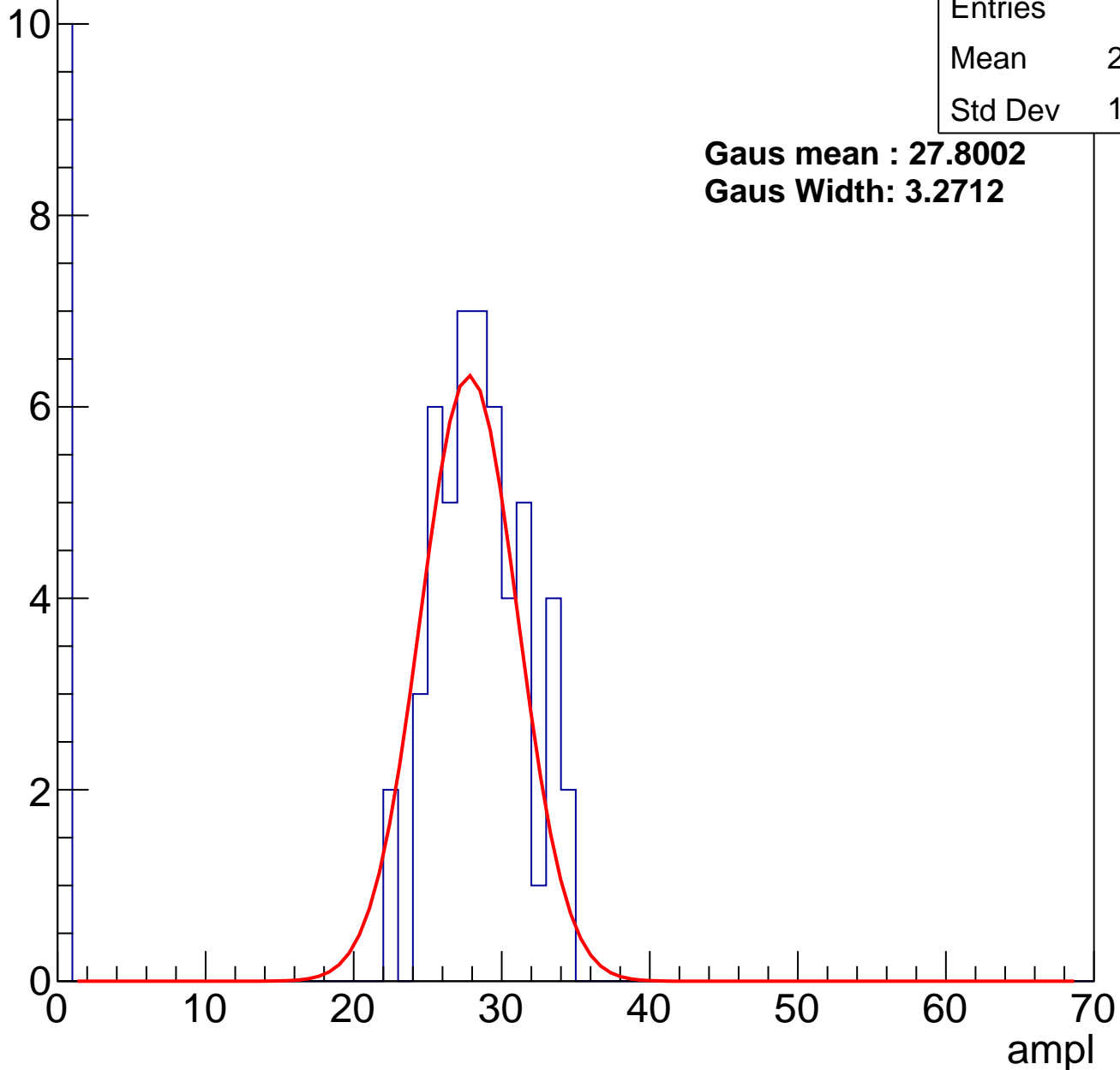
calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	23.58
Std Dev	10.69

Gaus mean : 27.8002

Gaus Width: 3.2712

Entry



B1L103S, U8-ch65, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	29.48
Std Dev	12.95

Gaus mean : 35.0198

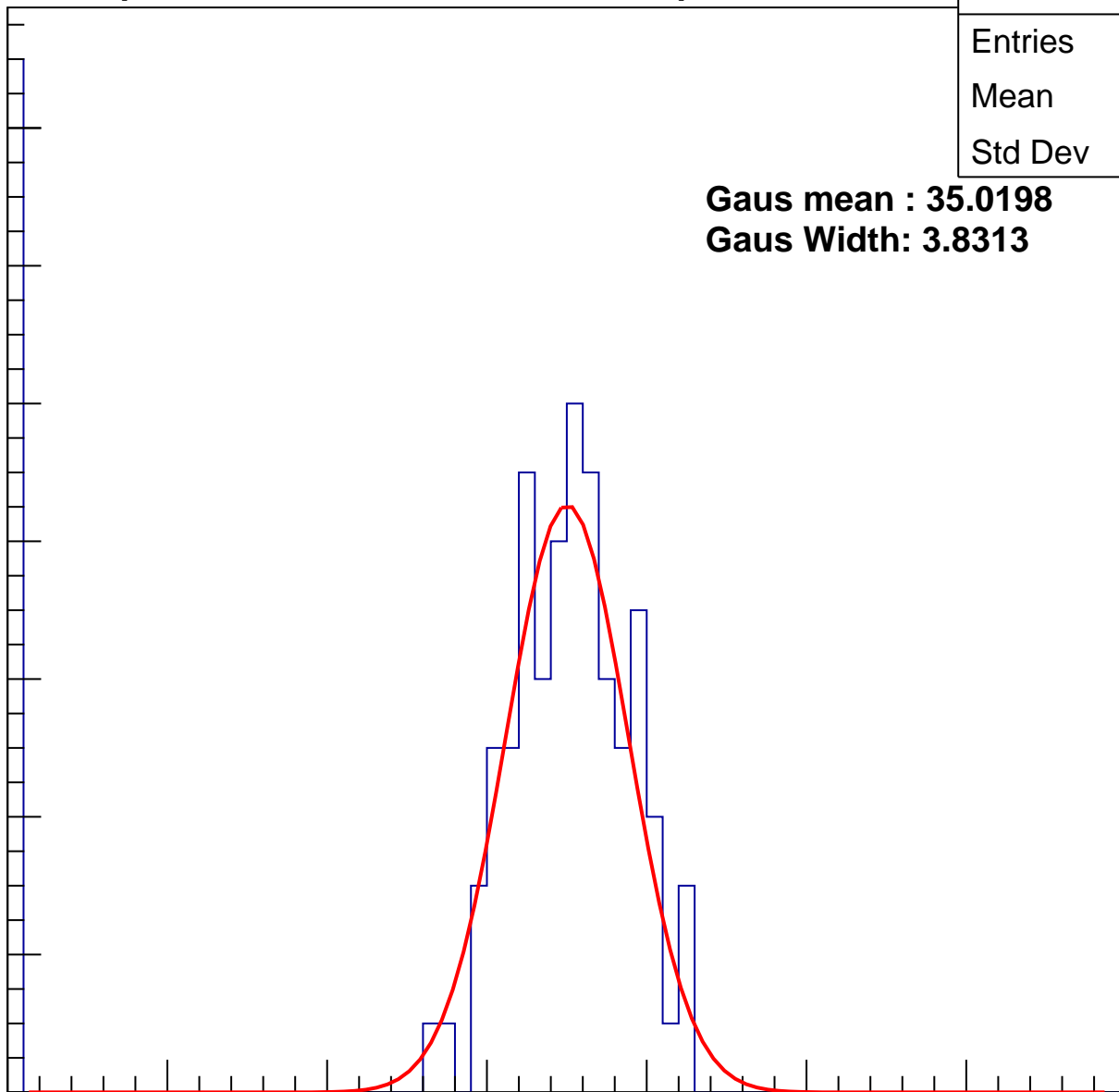
Gaus Width: 3.8313

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

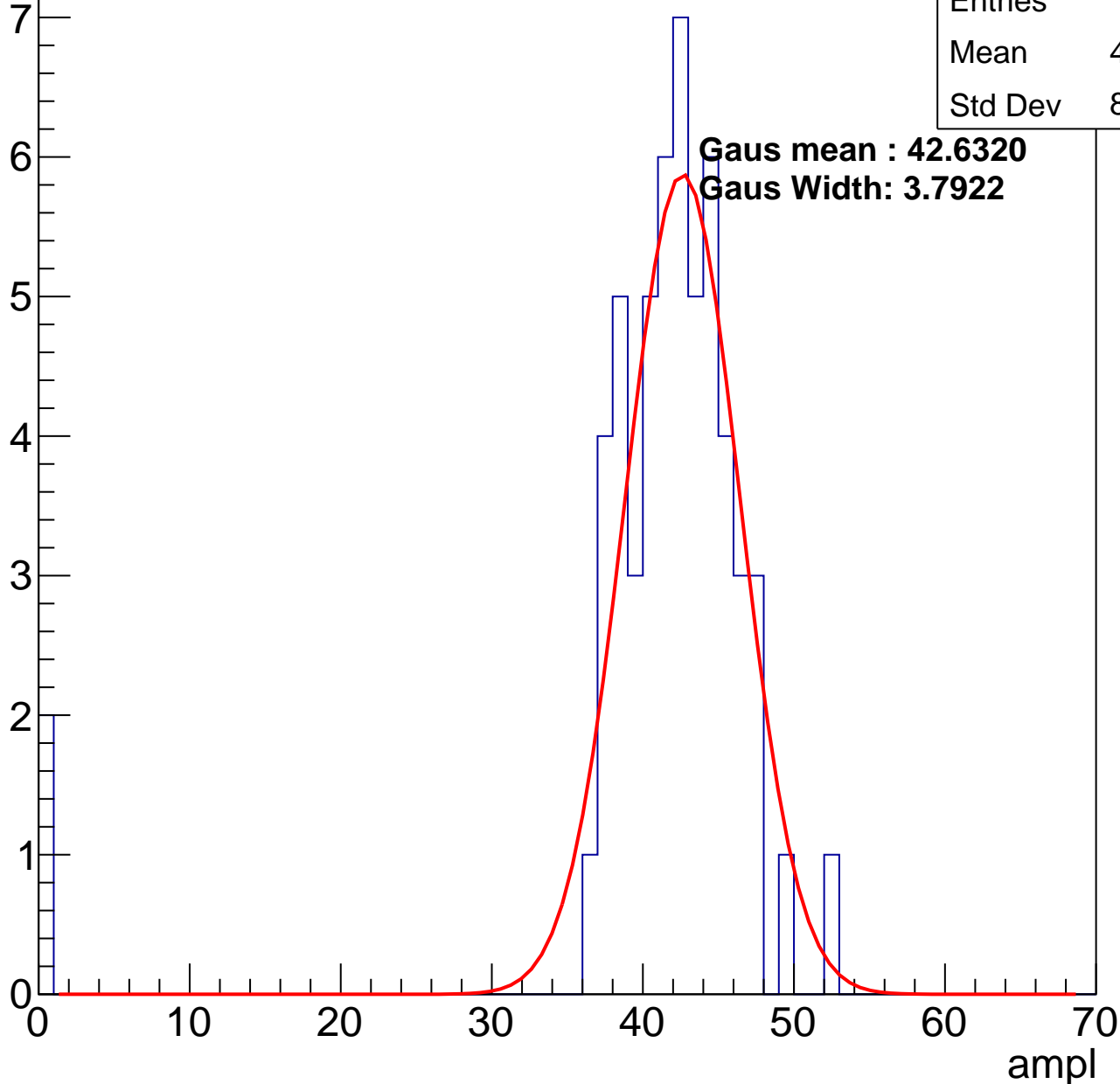


B1L103S, U8-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	40.54
Std Dev	8.466

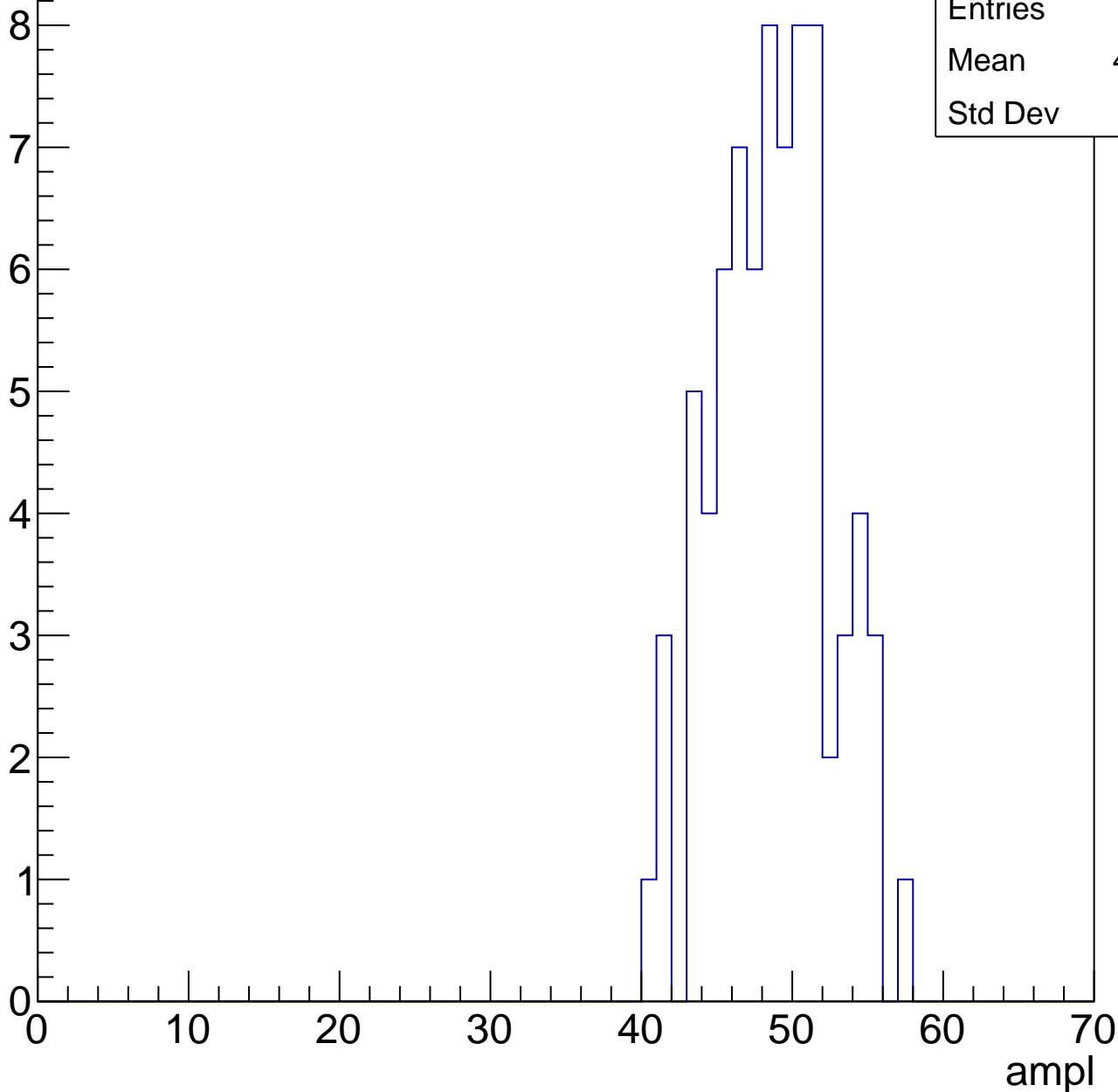


B1L103S, U8-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	48.21
Std Dev	3.76

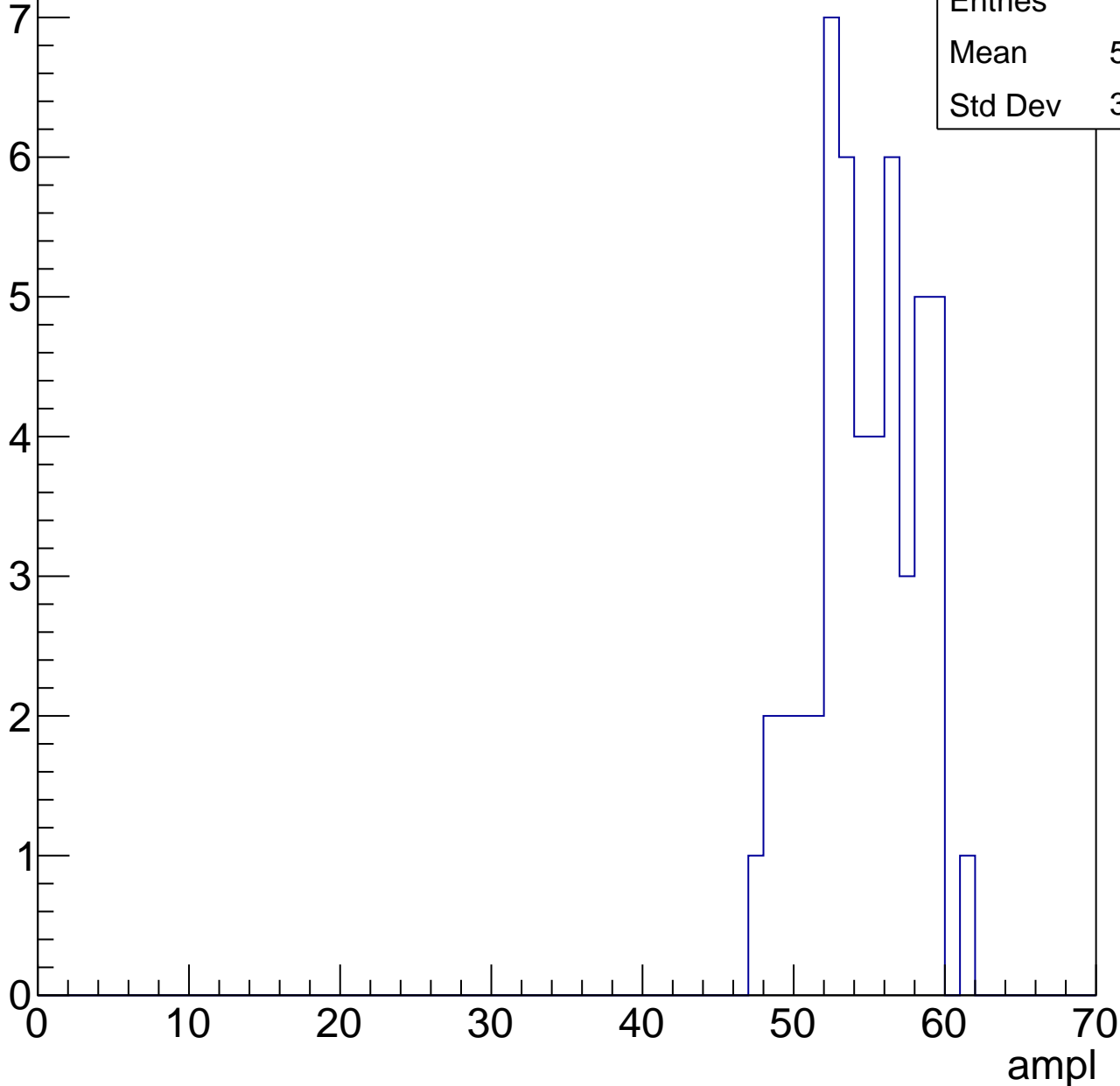


B1L103S, U8-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	54.28
Std Dev	3.347

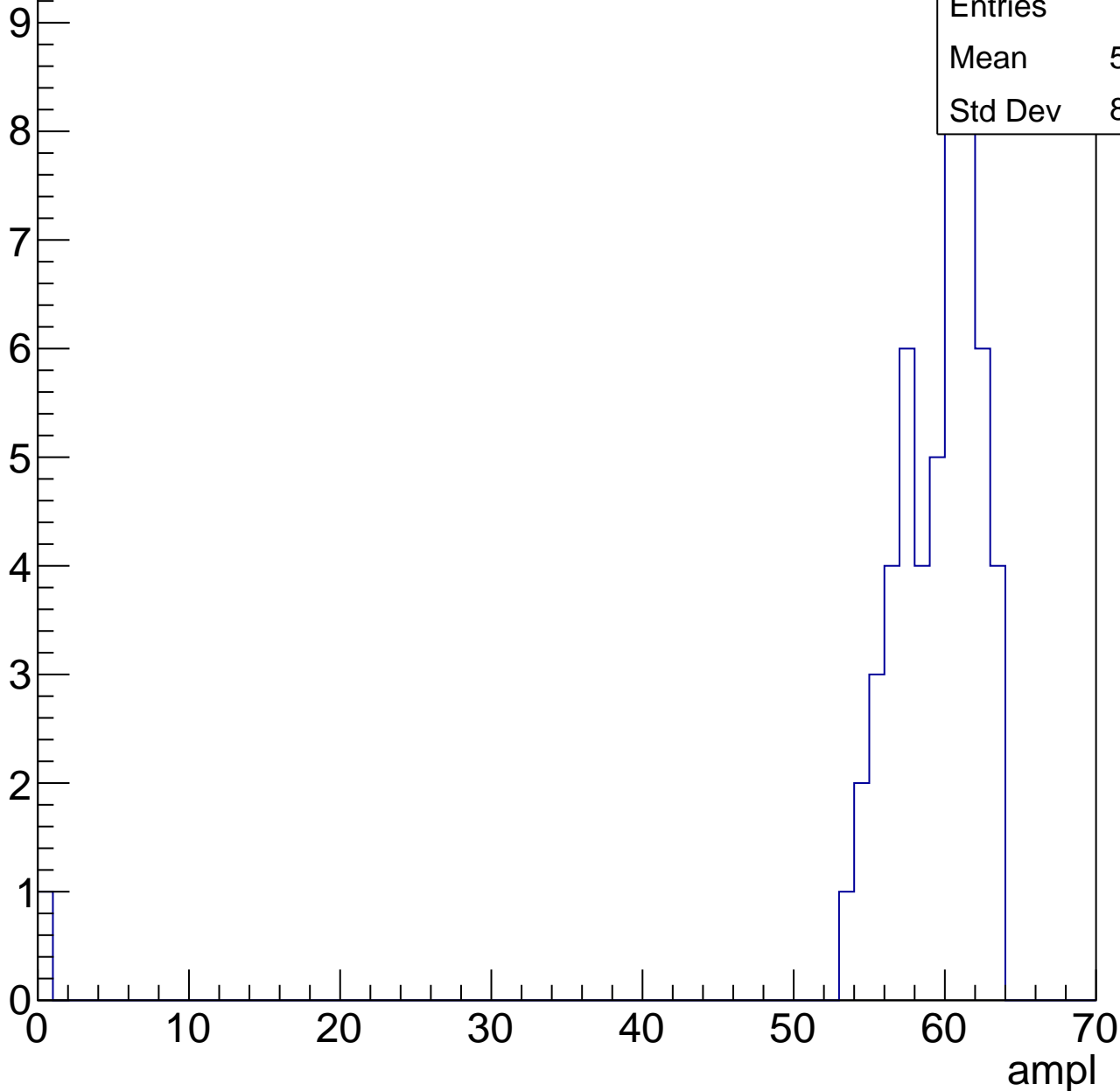


B1L103S, U8-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.94
Std Dev	8.444

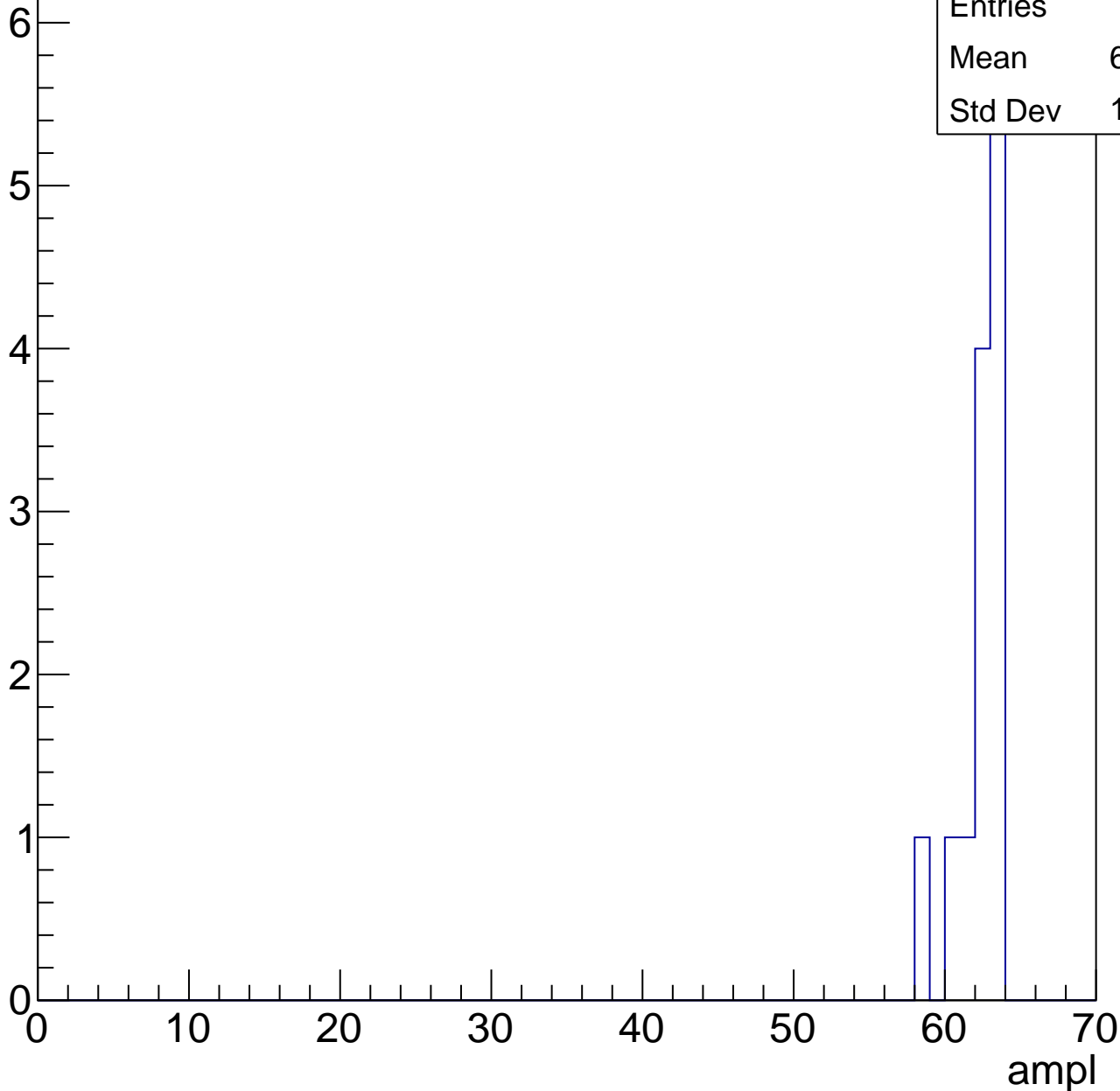


B1L103S, U8-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

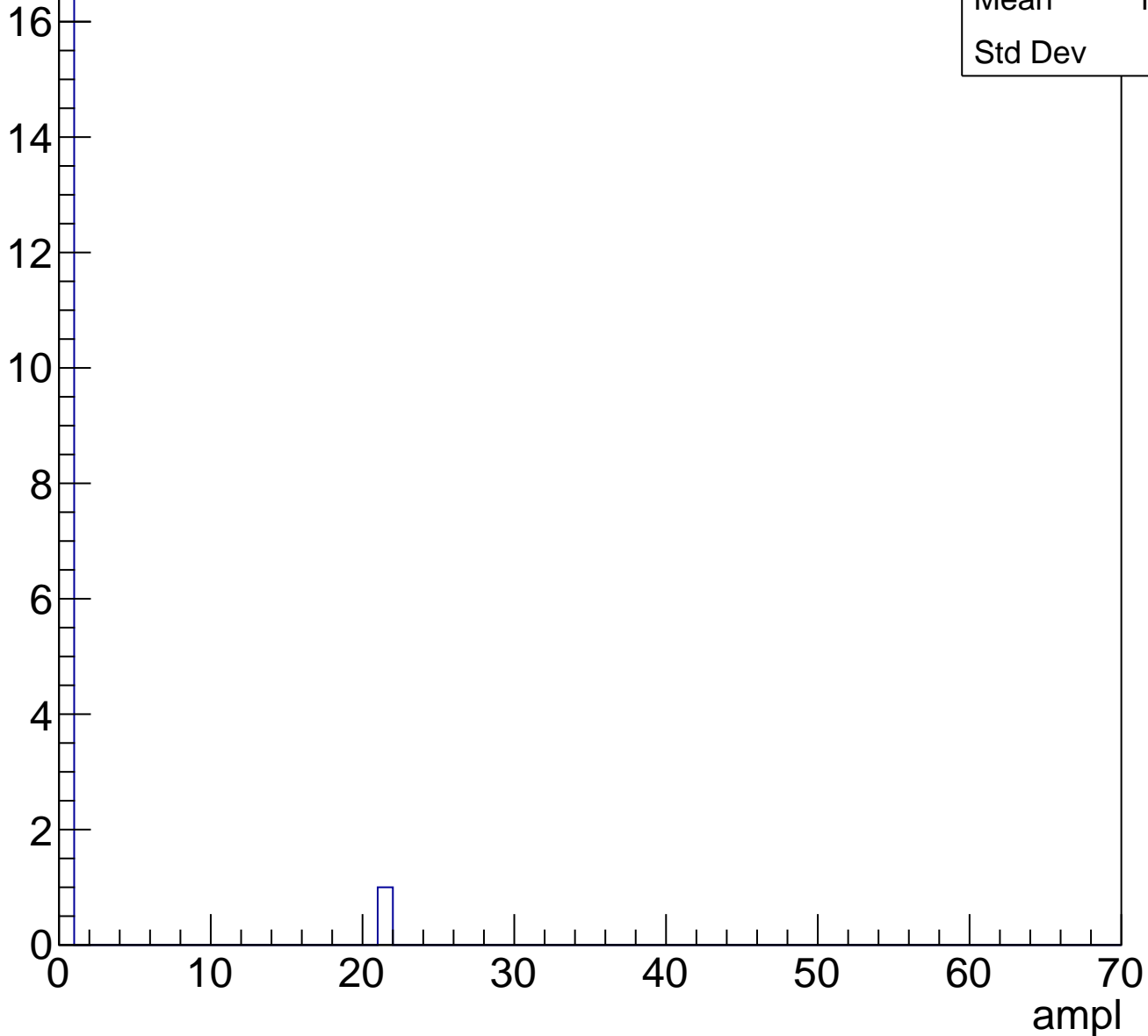
Entries	13
Mean	61.92
Std Dev	1.439



B1L103S, U8-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	1.167
Std Dev	4.81

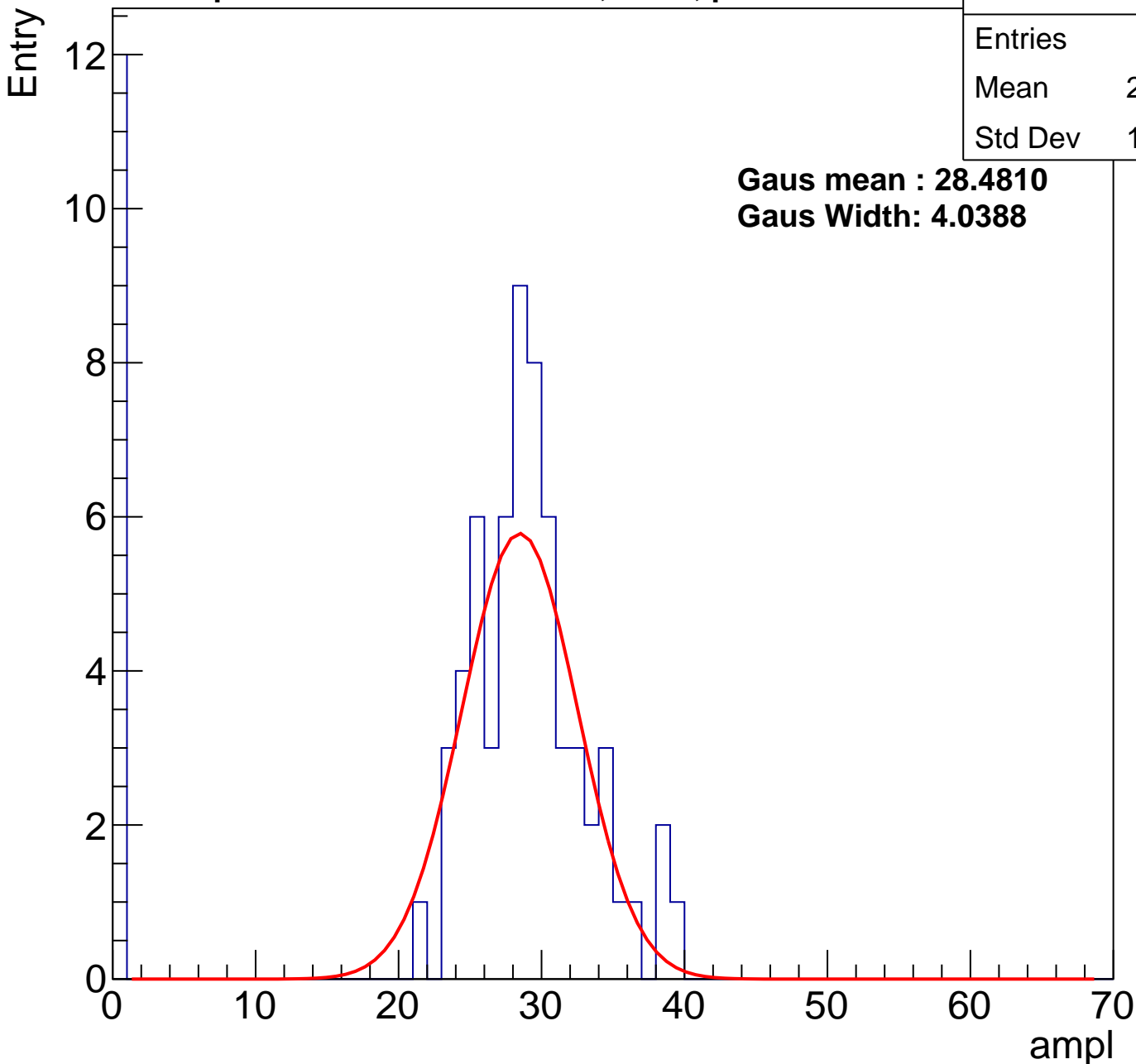
B1L103S, U8-ch66, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	24.09
Std Dev	11.17

Gaus mean : 28.4810

Gaus Width: 4.0388



B1L103S, U8-ch66, adc1

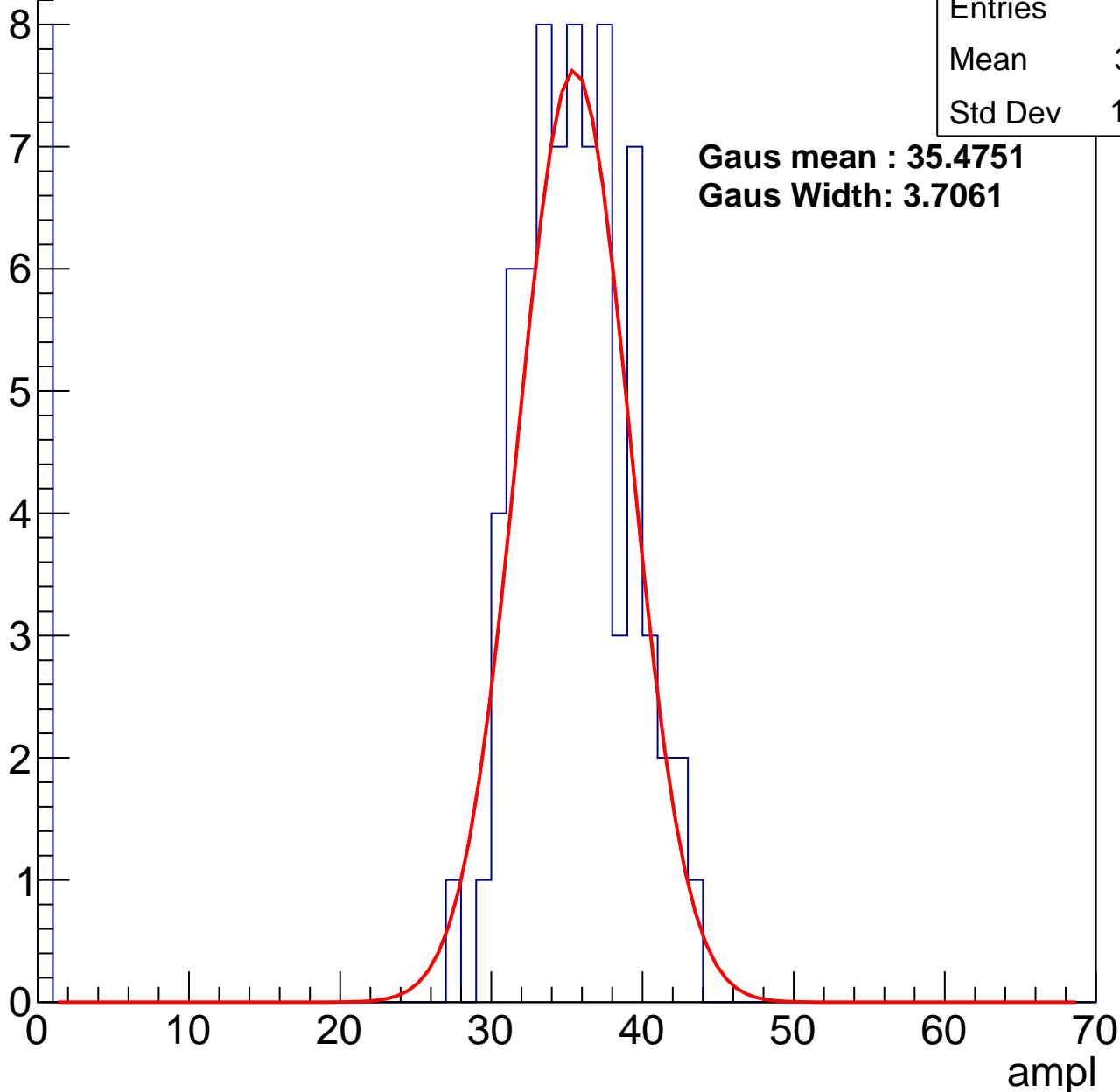
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	31.71
Std Dev	10.92

Gaus mean : 35.4751

Gaus Width: 3.7061



B1L103S, U8-ch66, adc2

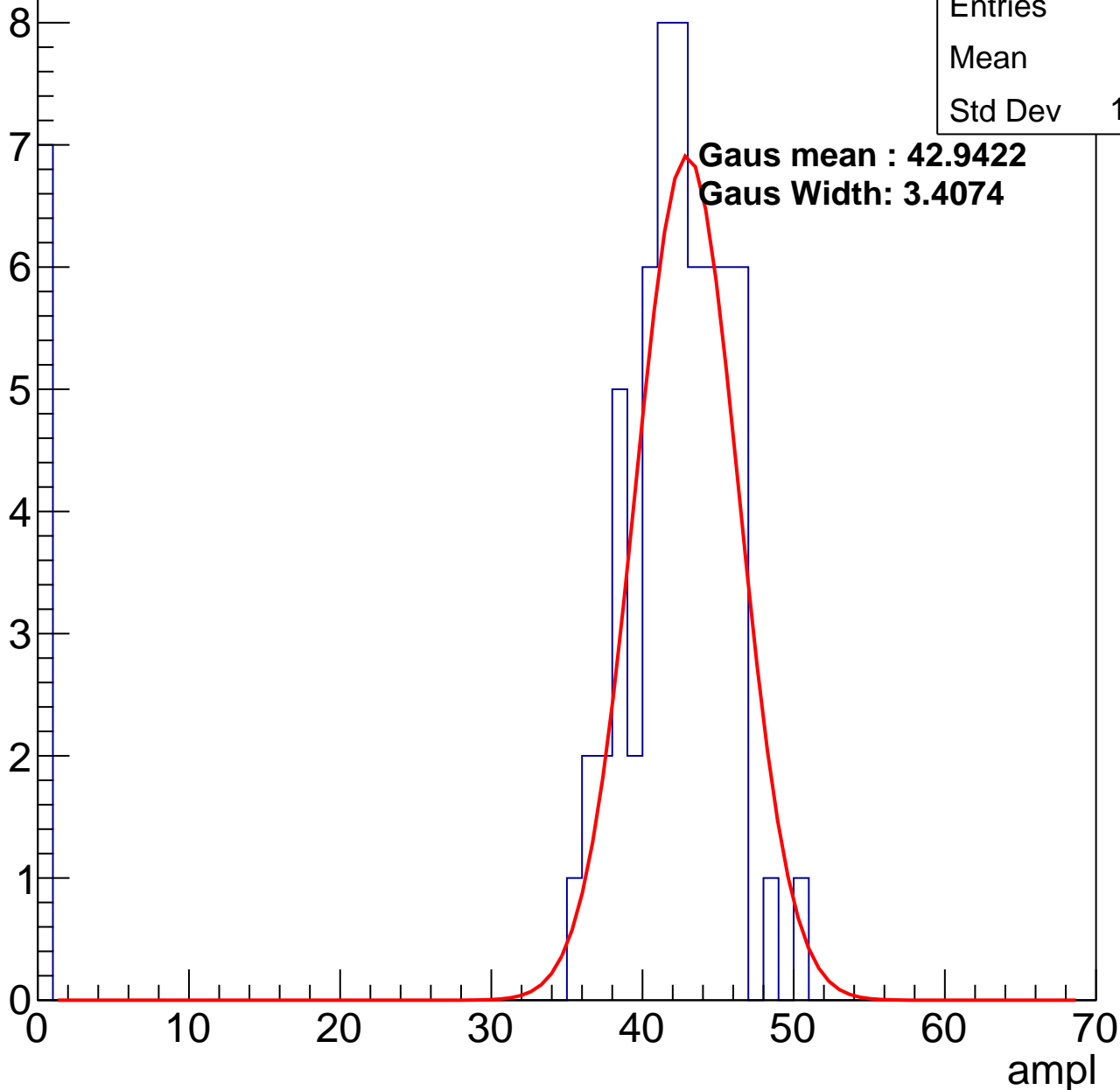
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.6
Std Dev	13.18

Gaus mean : 42.9422

Gaus Width: 3.4074



B1L103S, U8-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2

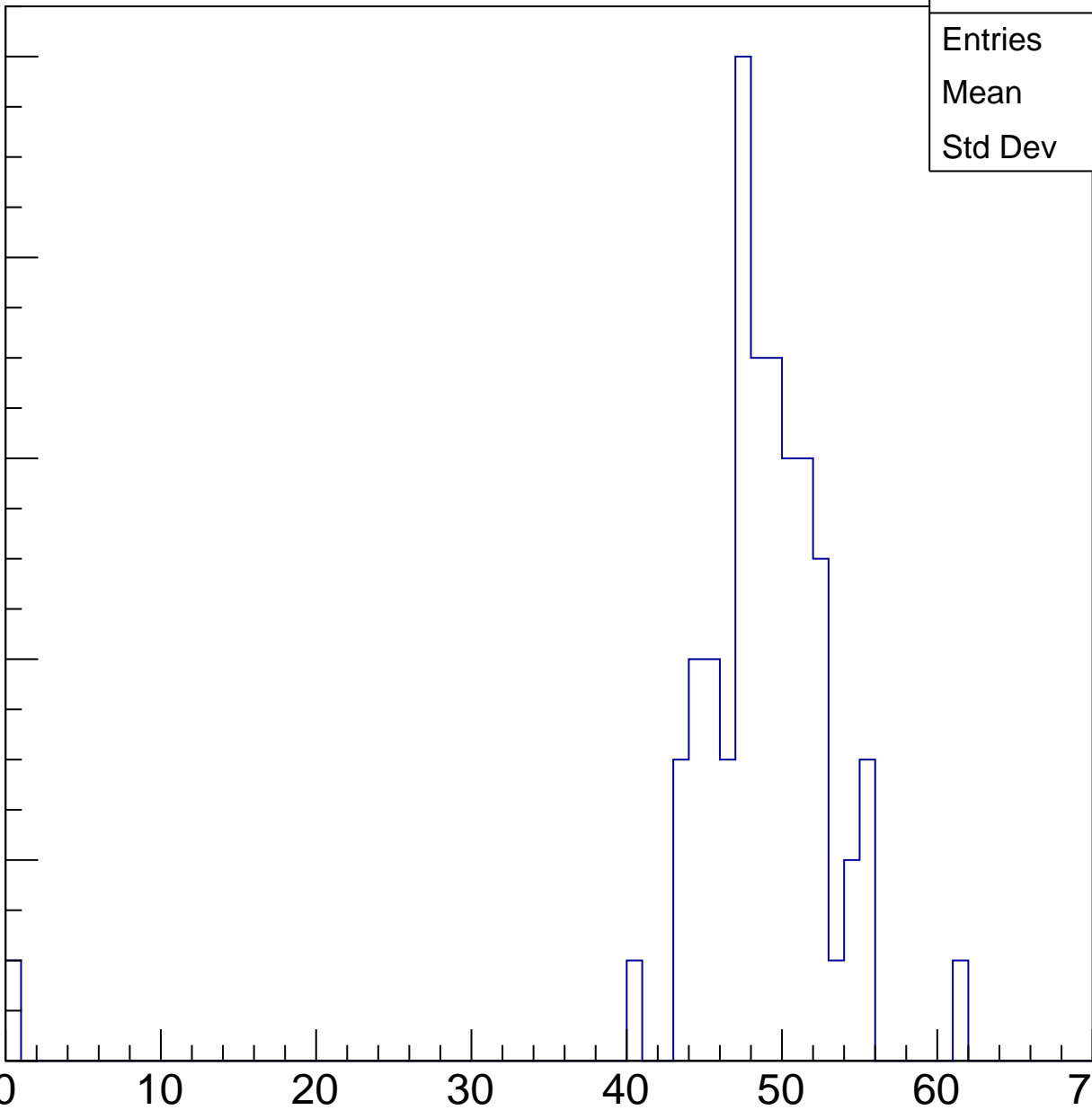
Entries	64
Mean	47.89
Std Dev	7.007

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

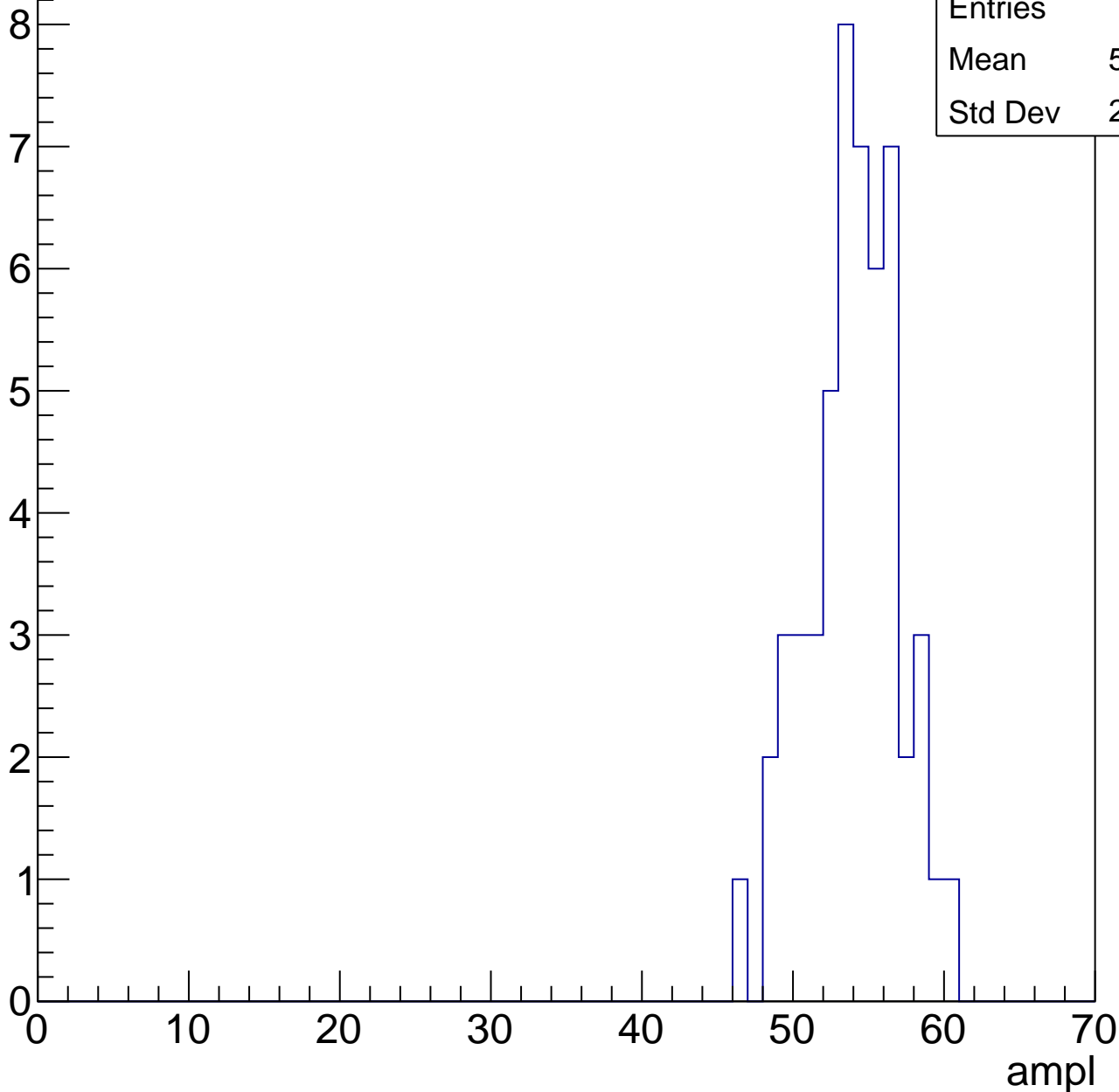


B1L103S, U8-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.52
Std Dev	2.984

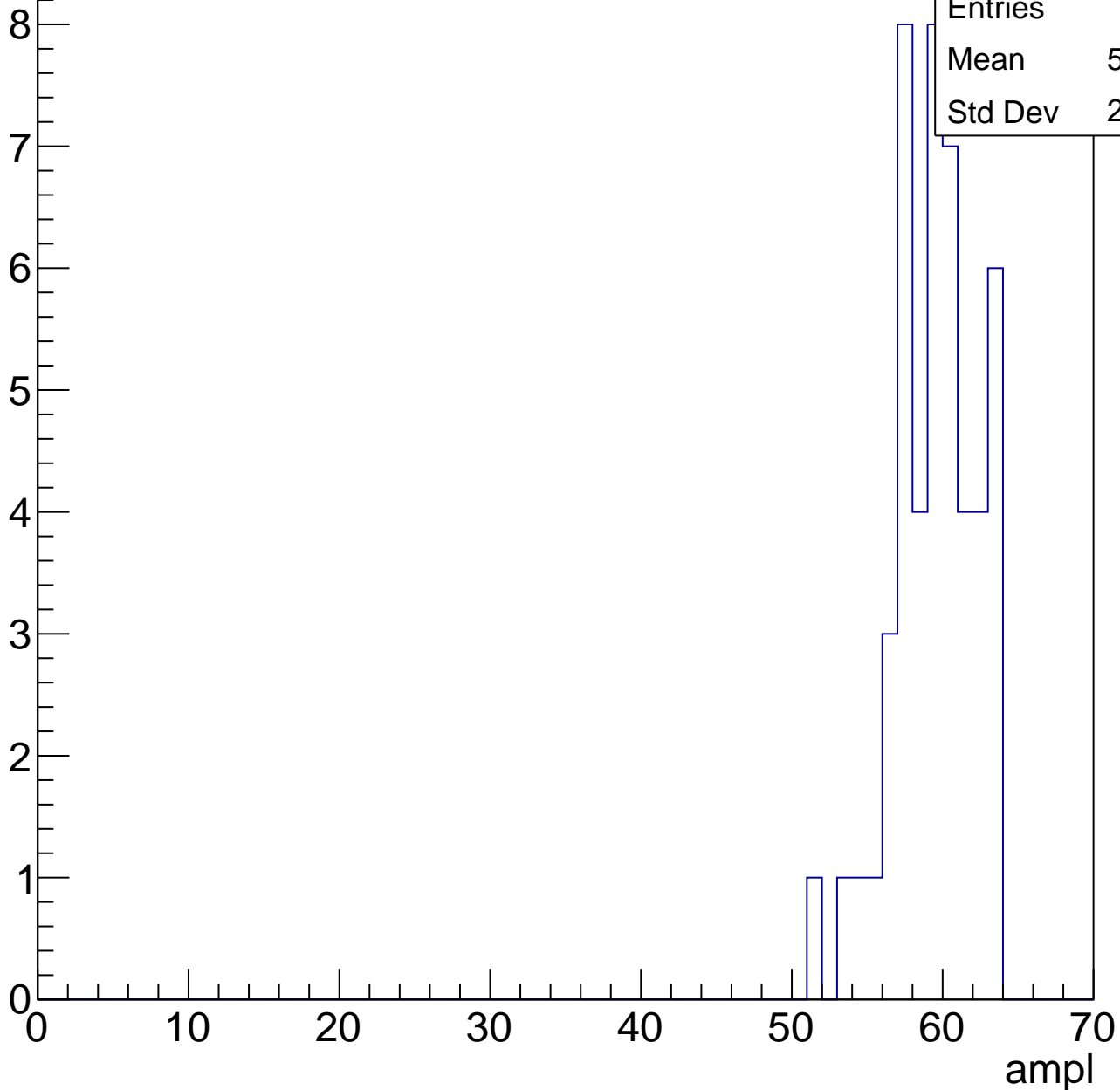


B1L103S, U8-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	58.98
Std Dev	2.735

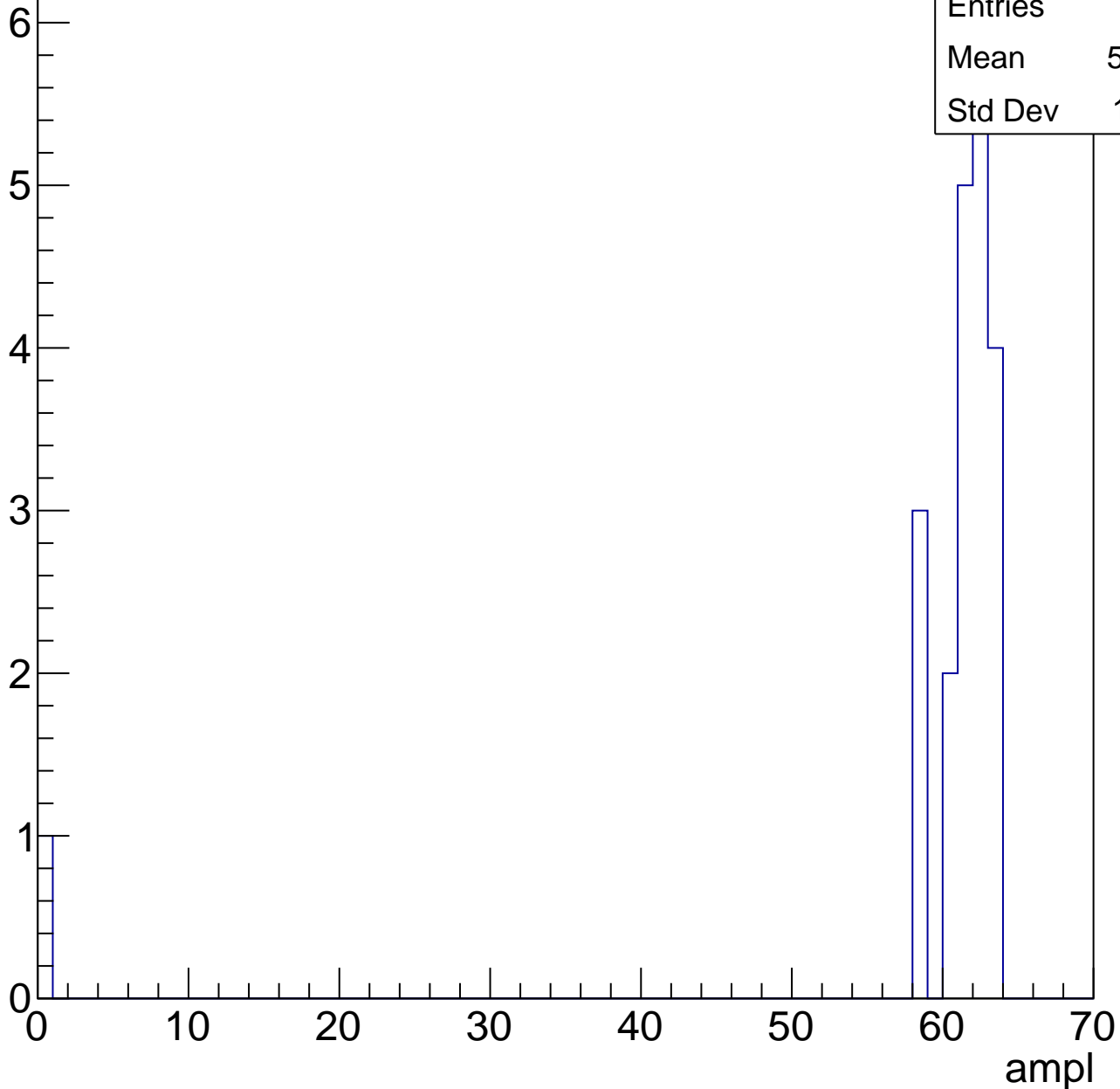


B1L103S, U8-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.24
Std Dev	13.11

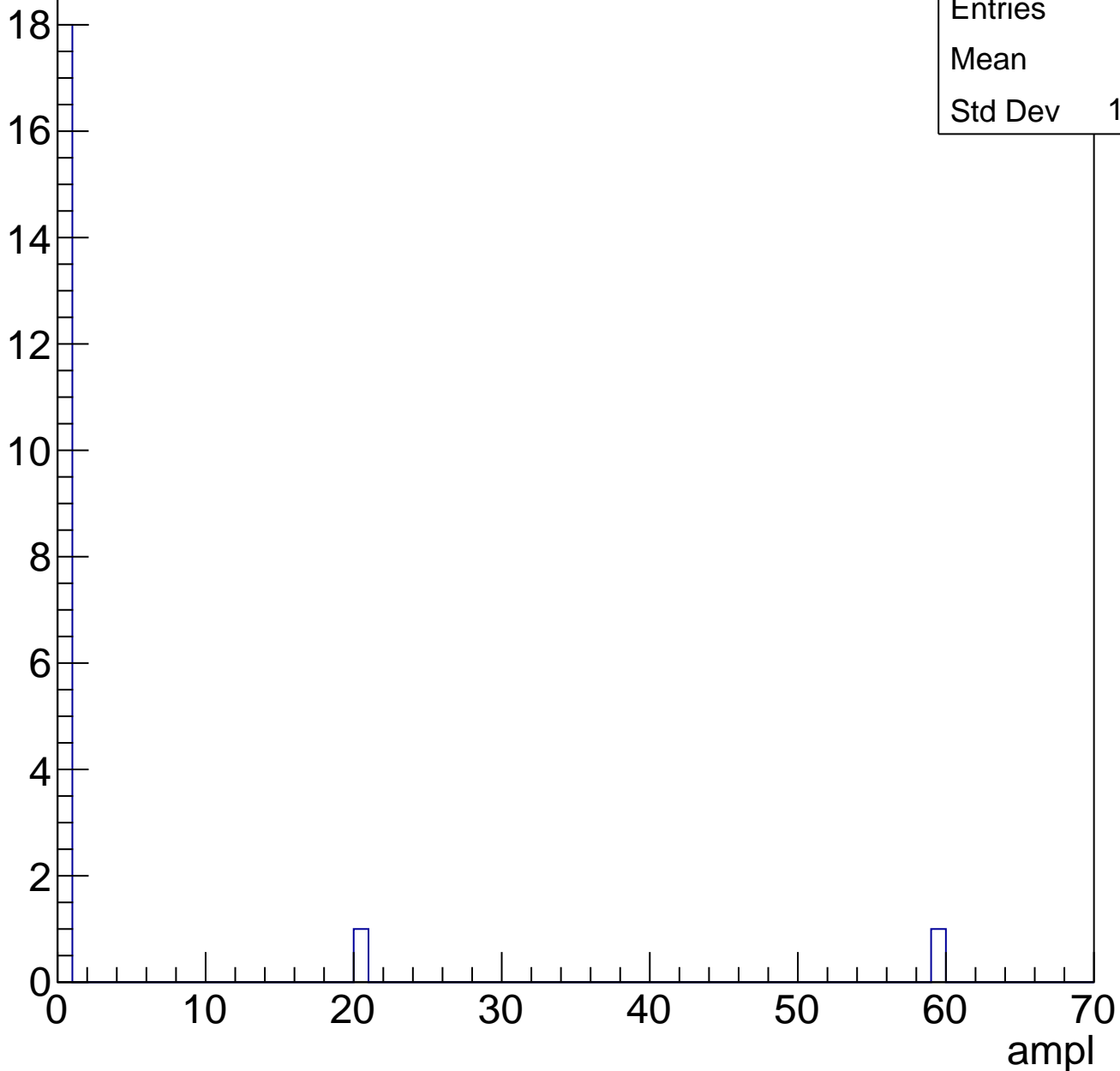


B1L103S, U8-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.95
Std Dev	13.36

Entry



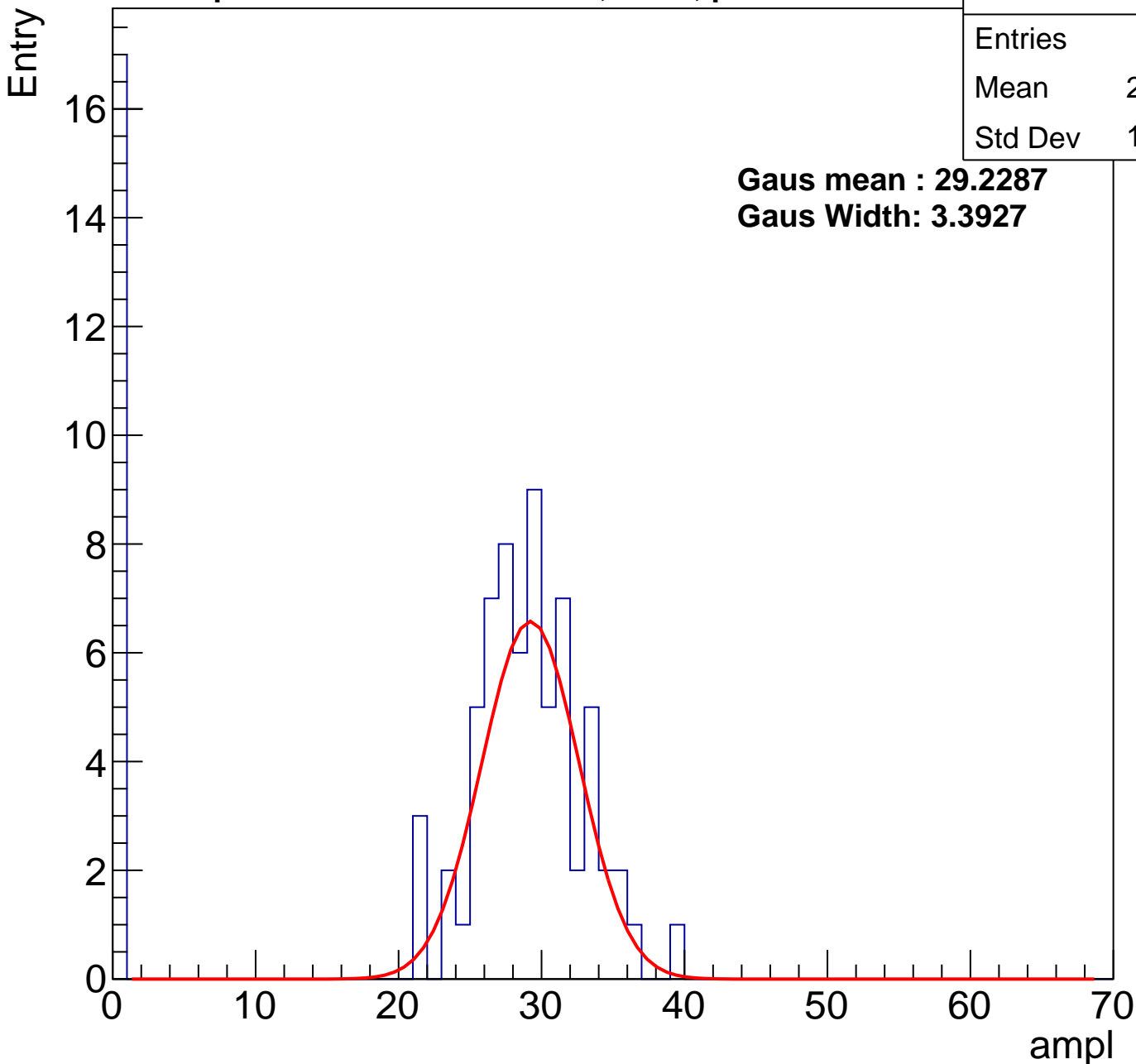
B1L103S, U8-ch67, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	22.82
Std Dev	12.02

Gaus mean : 29.2287

Gaus Width: 3.3927



B1L103S, U8-ch67, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	30.51
Std Dev	12.76

Gaus mean : 35.7269

Gaus Width: 3.9766

Entry

12

10

8

6

4

2

0

0

10

20

30

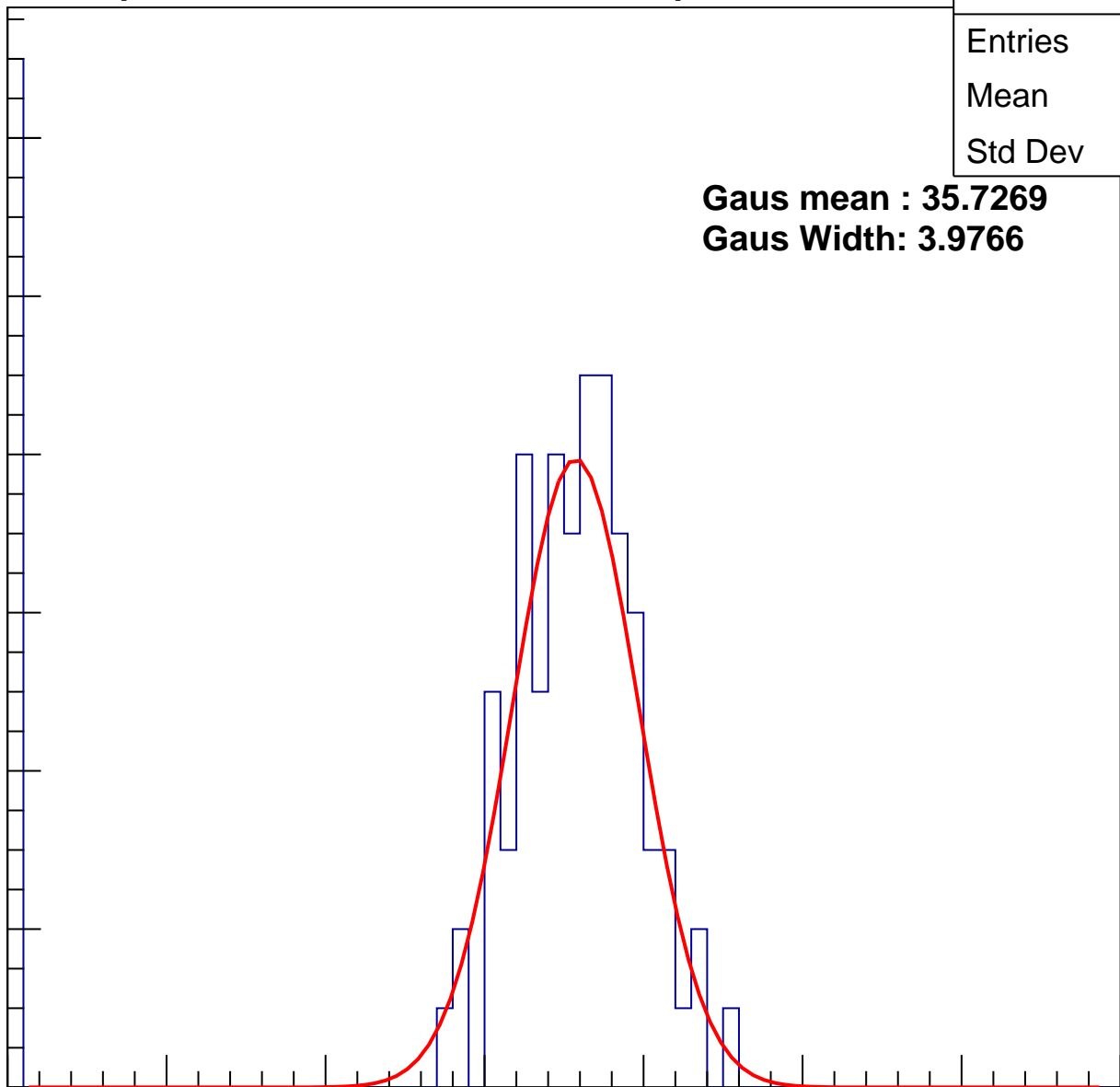
40

50

60

70

ampl



B1L103S, U8-ch67, adc2

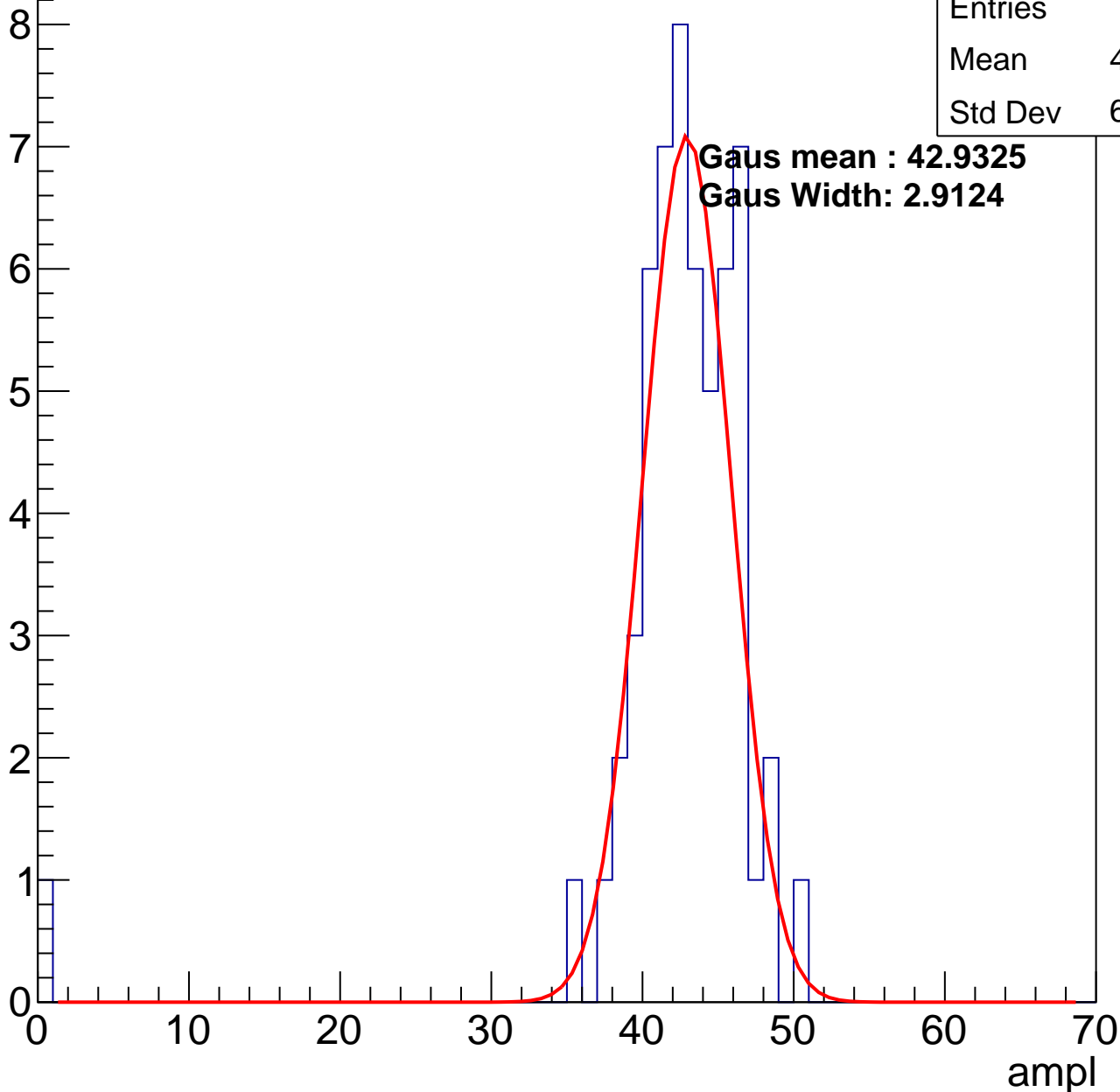
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	41.95
Std Dev	6.326

Gaus mean : 42.9325

Gaus Width: 2.9124

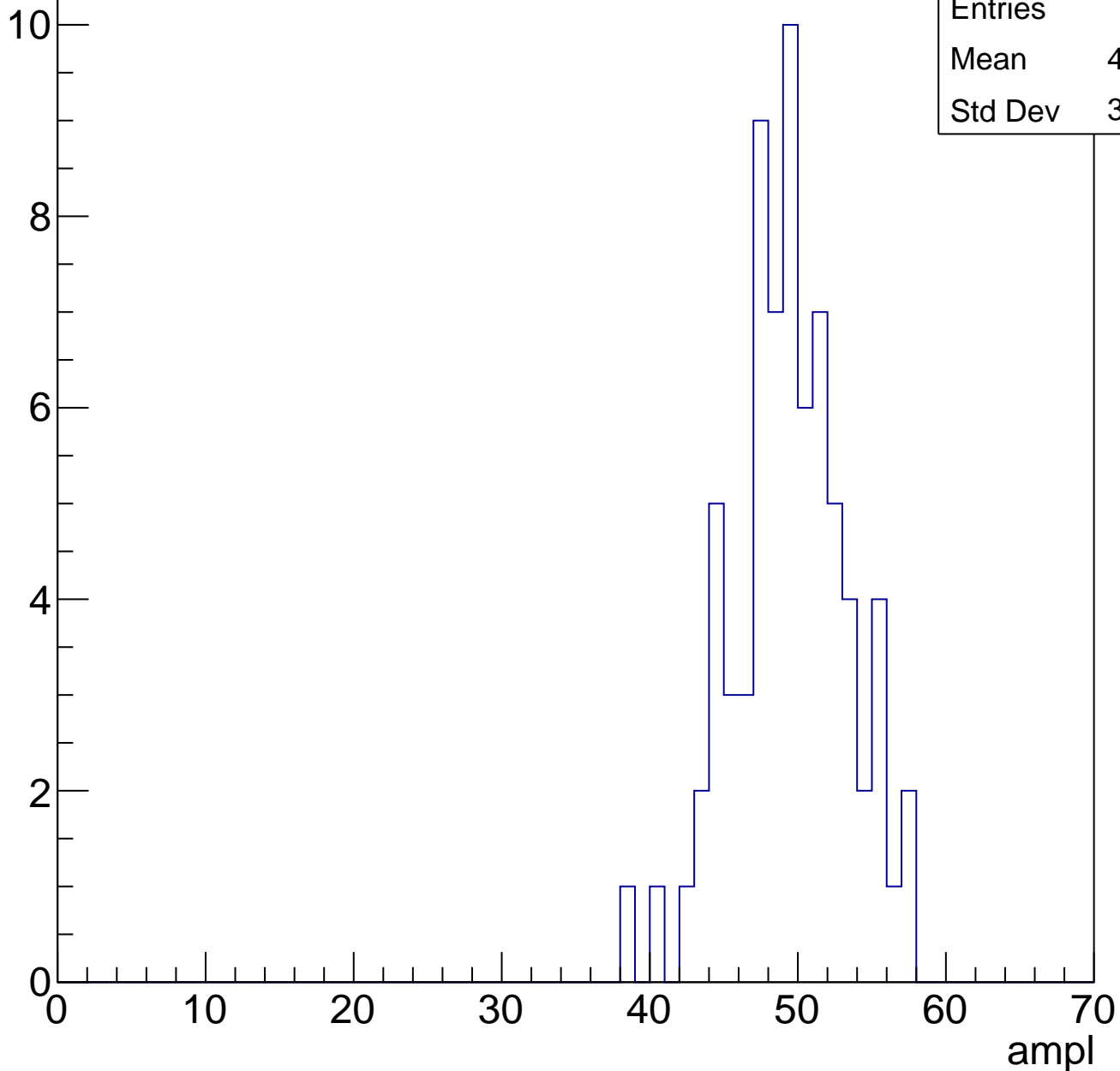


B1L103S, U8-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	48.97
Std Dev	3.846

Entry

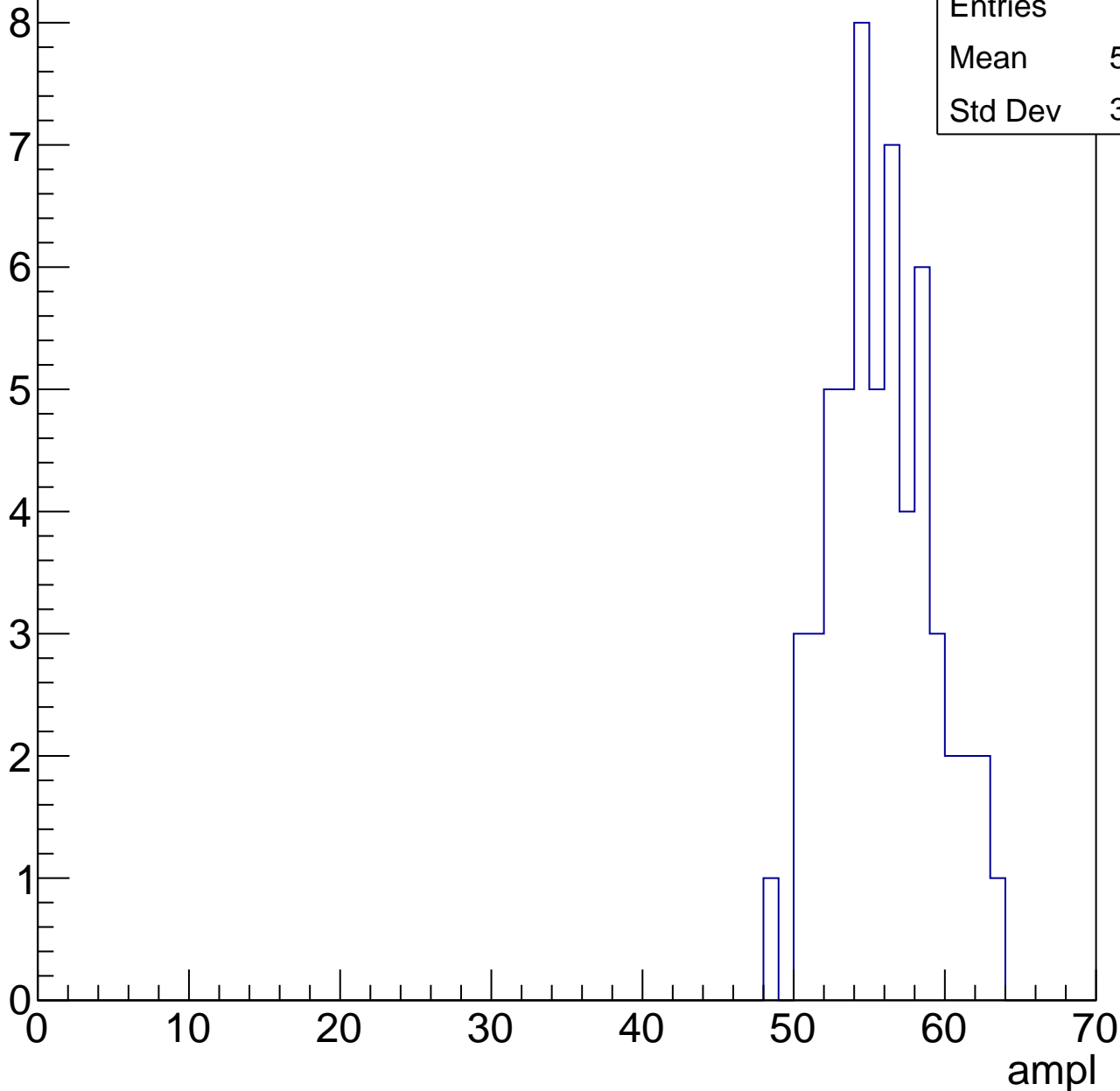


B1L103S, U8-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.39
Std Dev	3.365

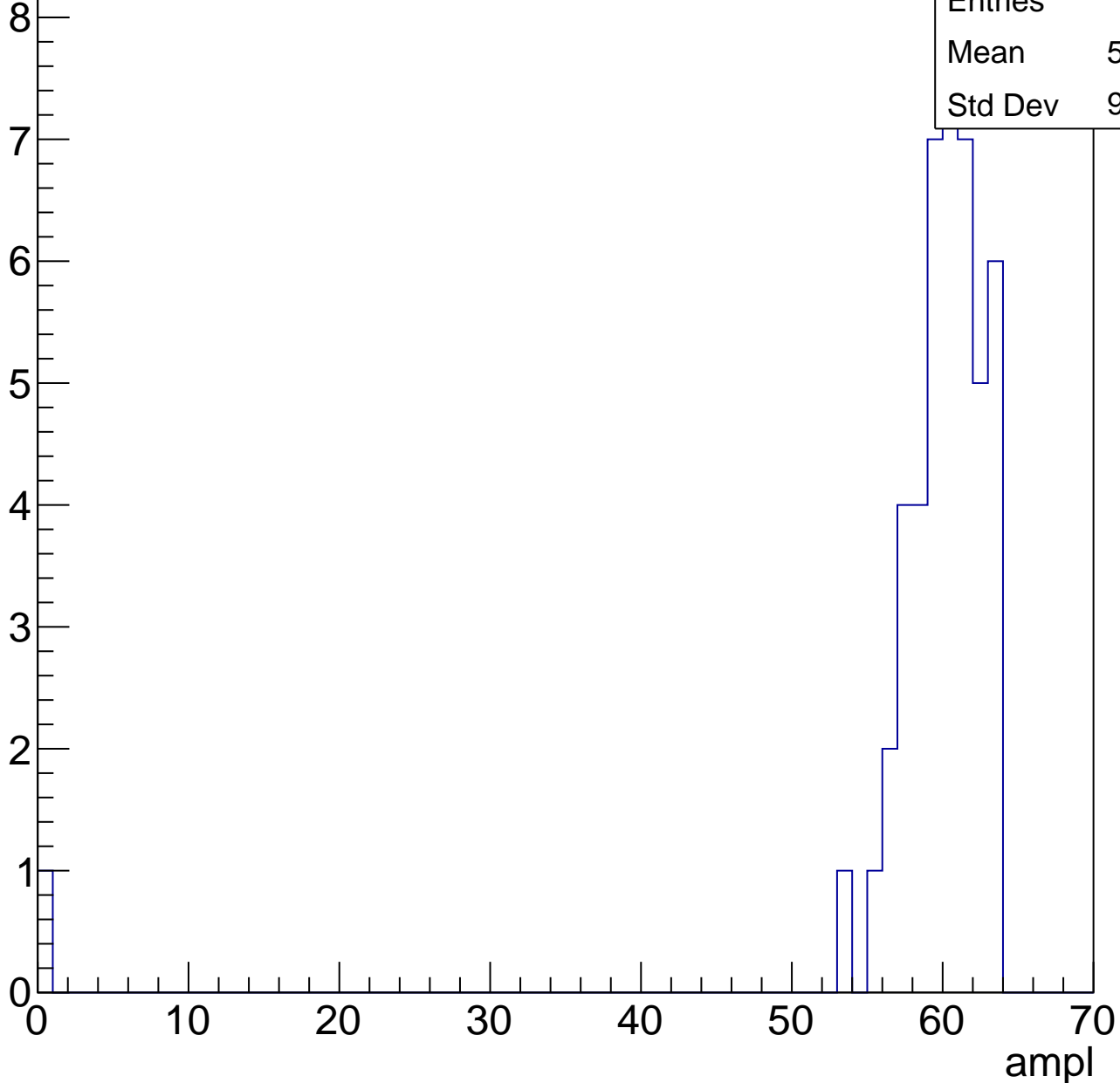


B1L103S, U8-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

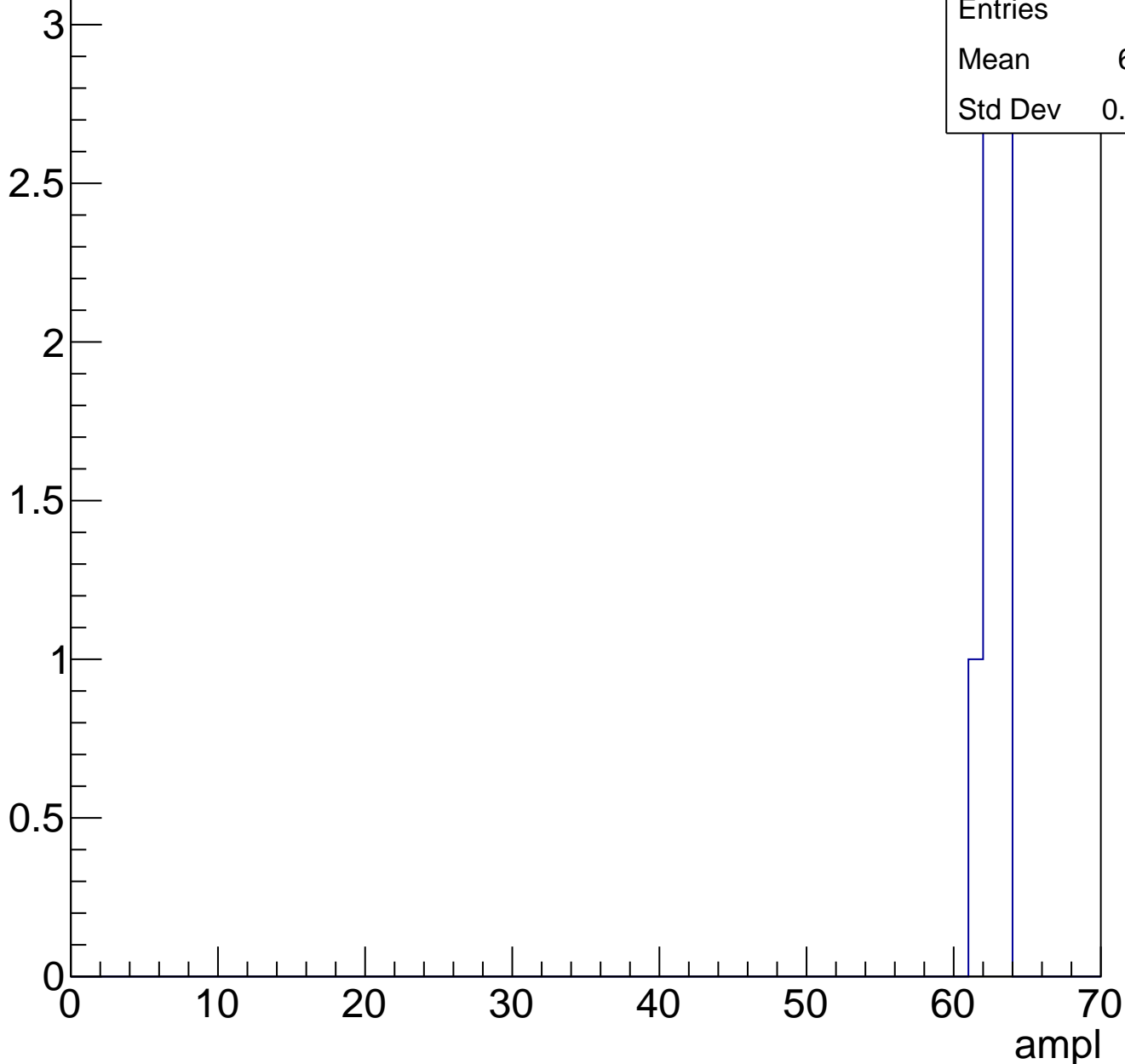
Entries	46
Mean	58.43
Std Dev	9.009



B1L103S, U8-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U8-ch68, adc0

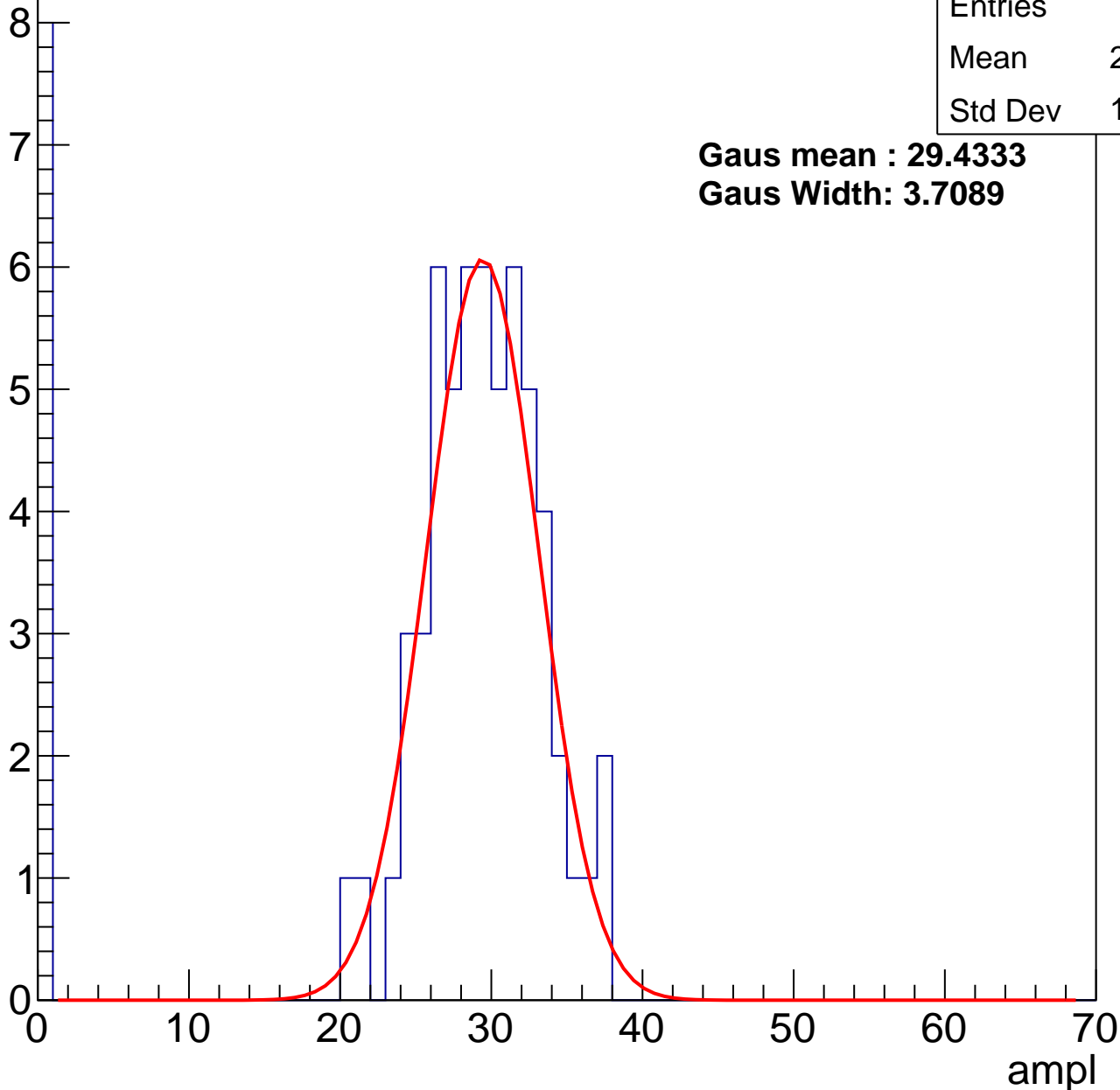
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	25.53
Std Dev	10.09

Gaus mean : 29.4333

Gaus Width: 3.7089



B1L103S, U8-ch68, adc1

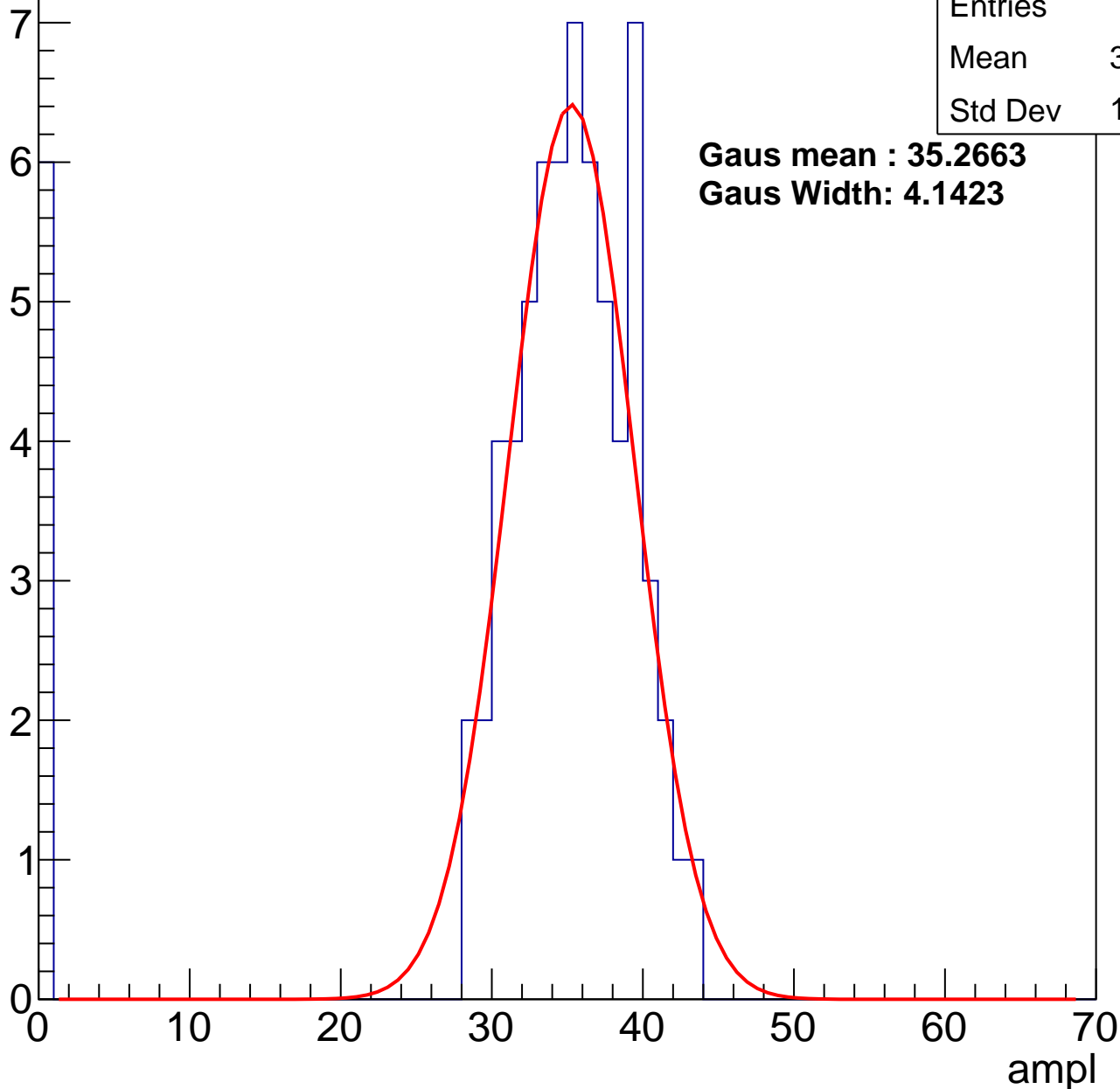
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	32.08
Std Dev	10.34

Gaus mean : 35.2663

Gaus Width: 4.1423



B1L103S, U8-ch68, adc2

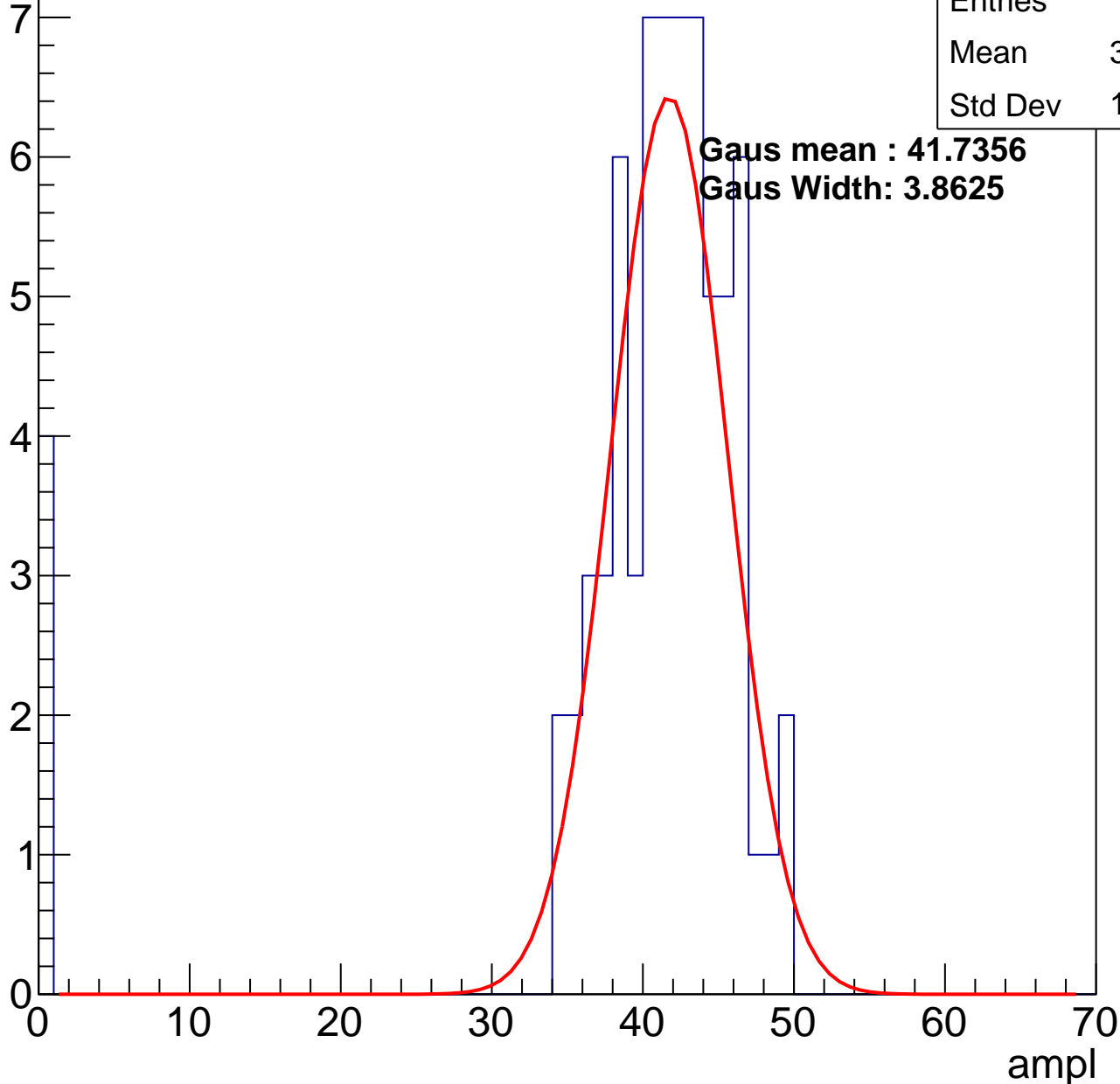
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	39.13
Std Dev	10.18

Gaus mean : 41.7356

Gaus Width: 3.8625

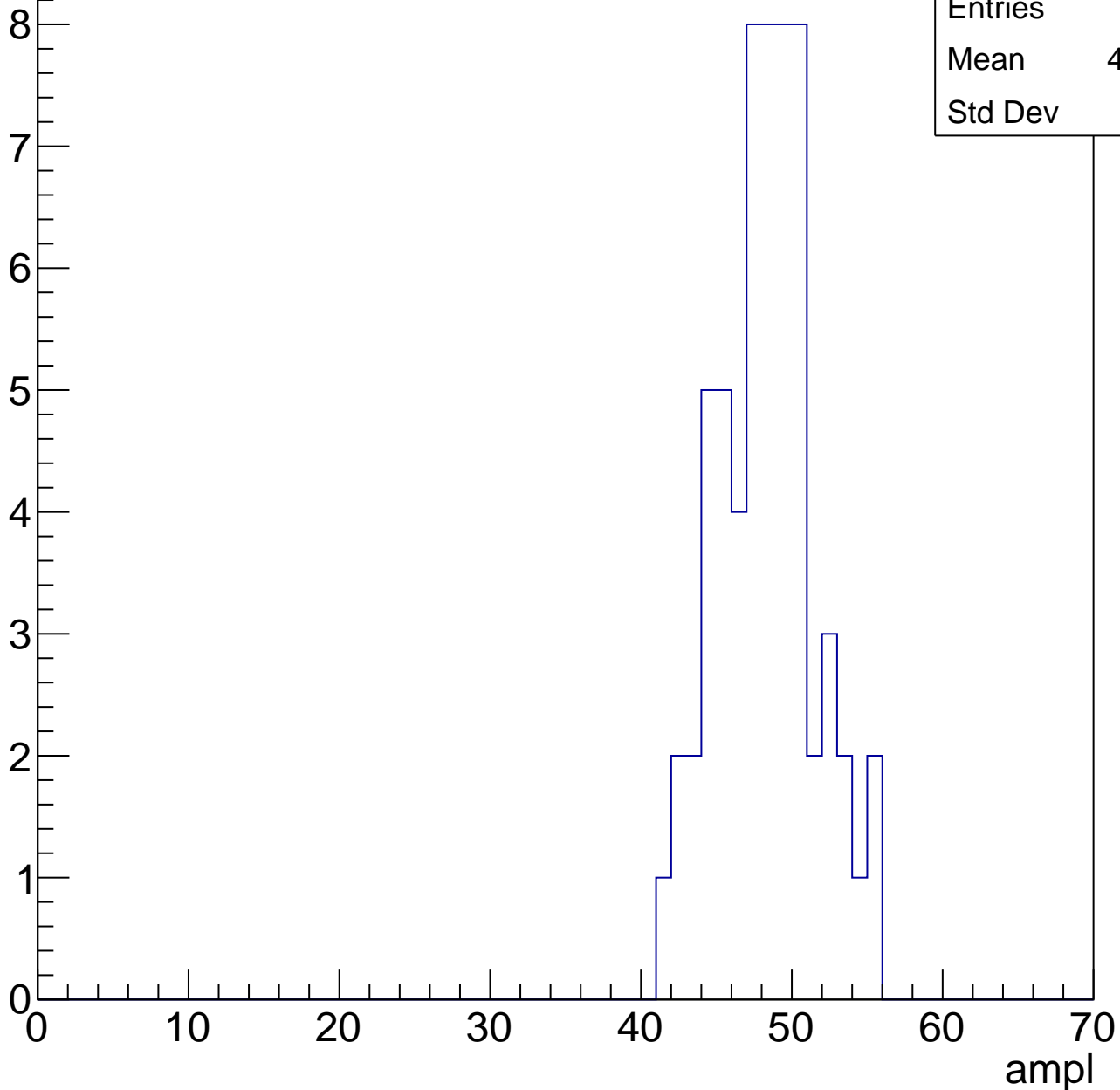


B1L103S, U8-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	47.87
Std Dev	3.16

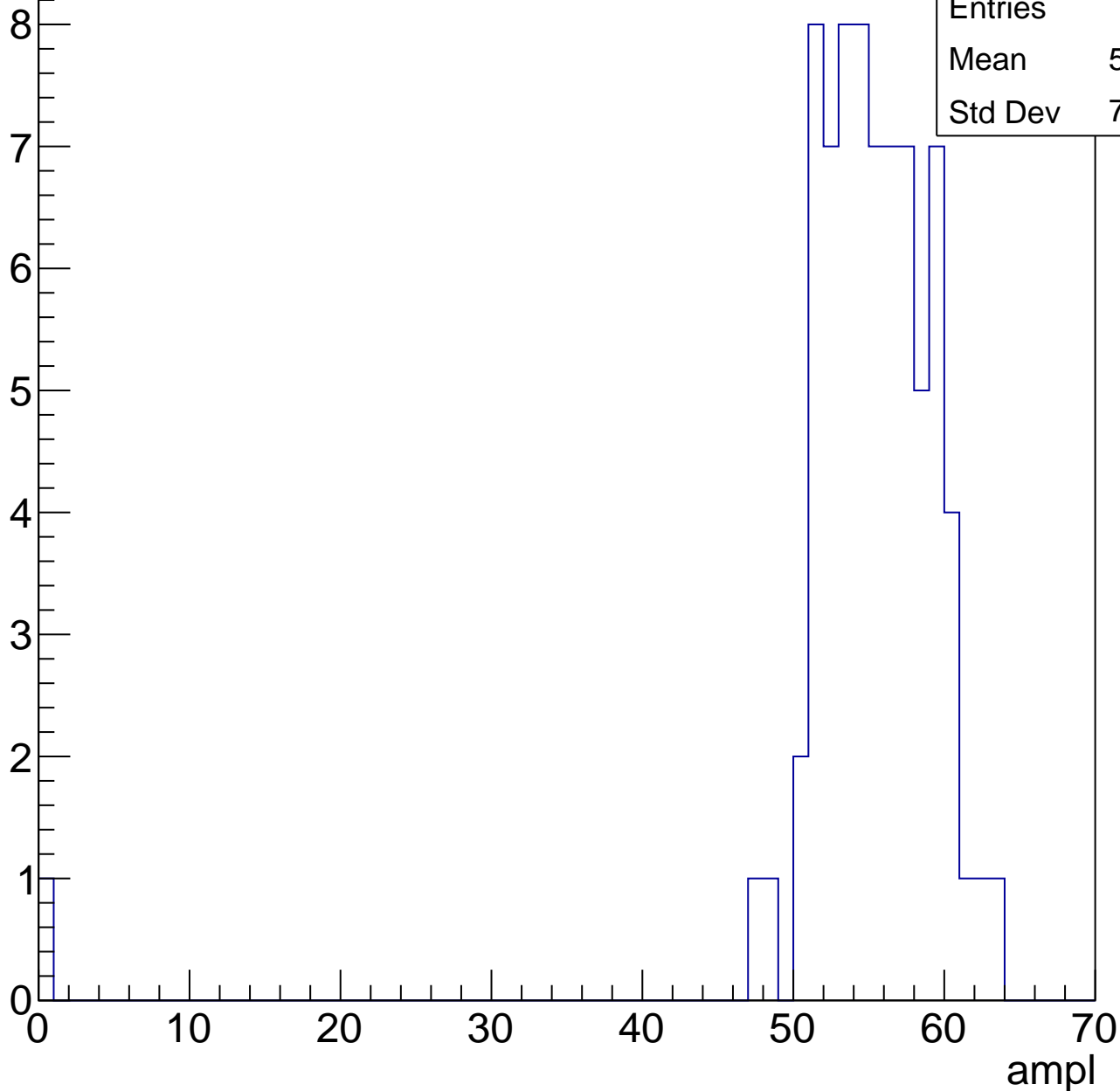


B1L103S, U8-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	54.32
Std Dev	7.094

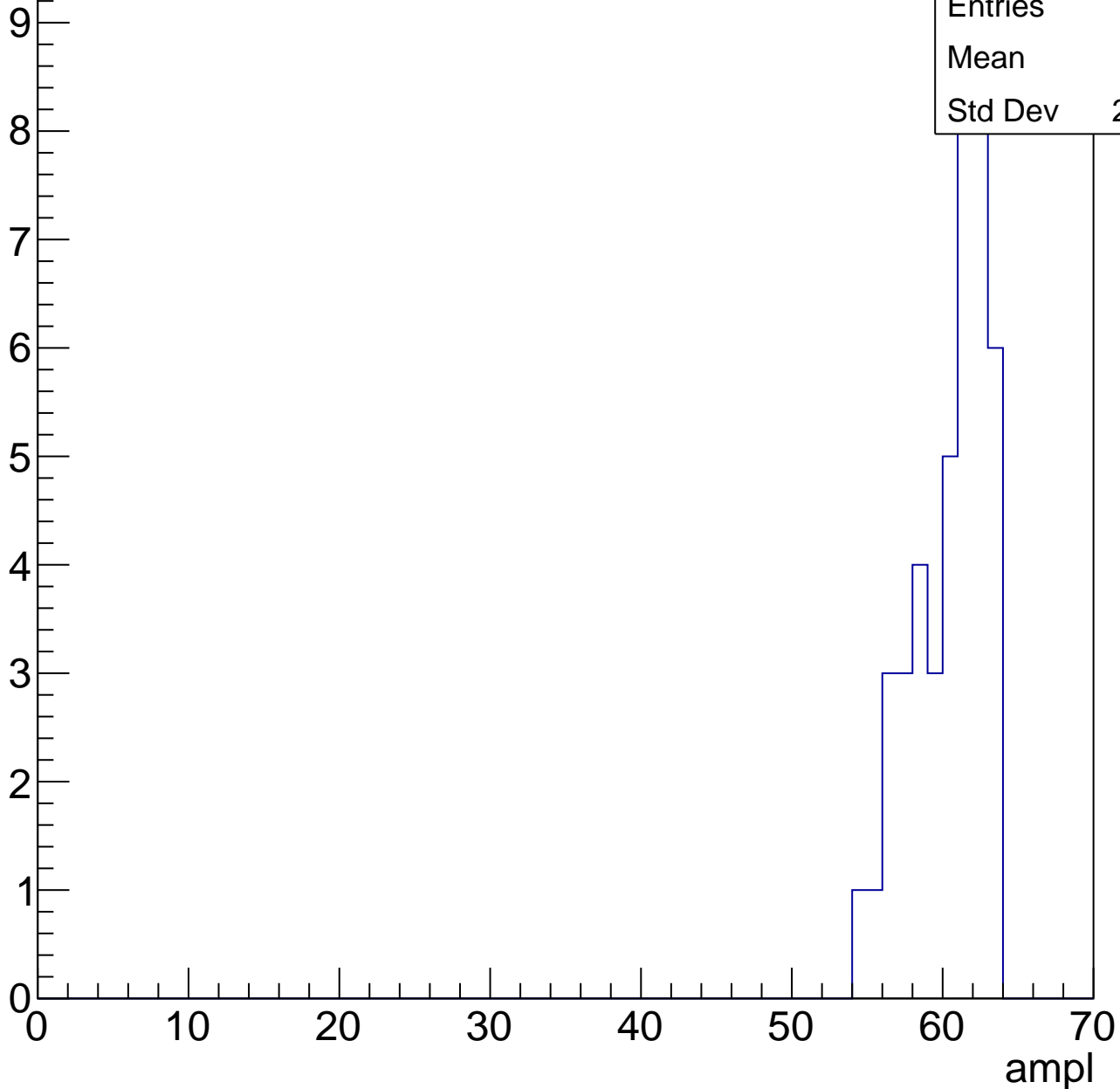


B1L103S, U8-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

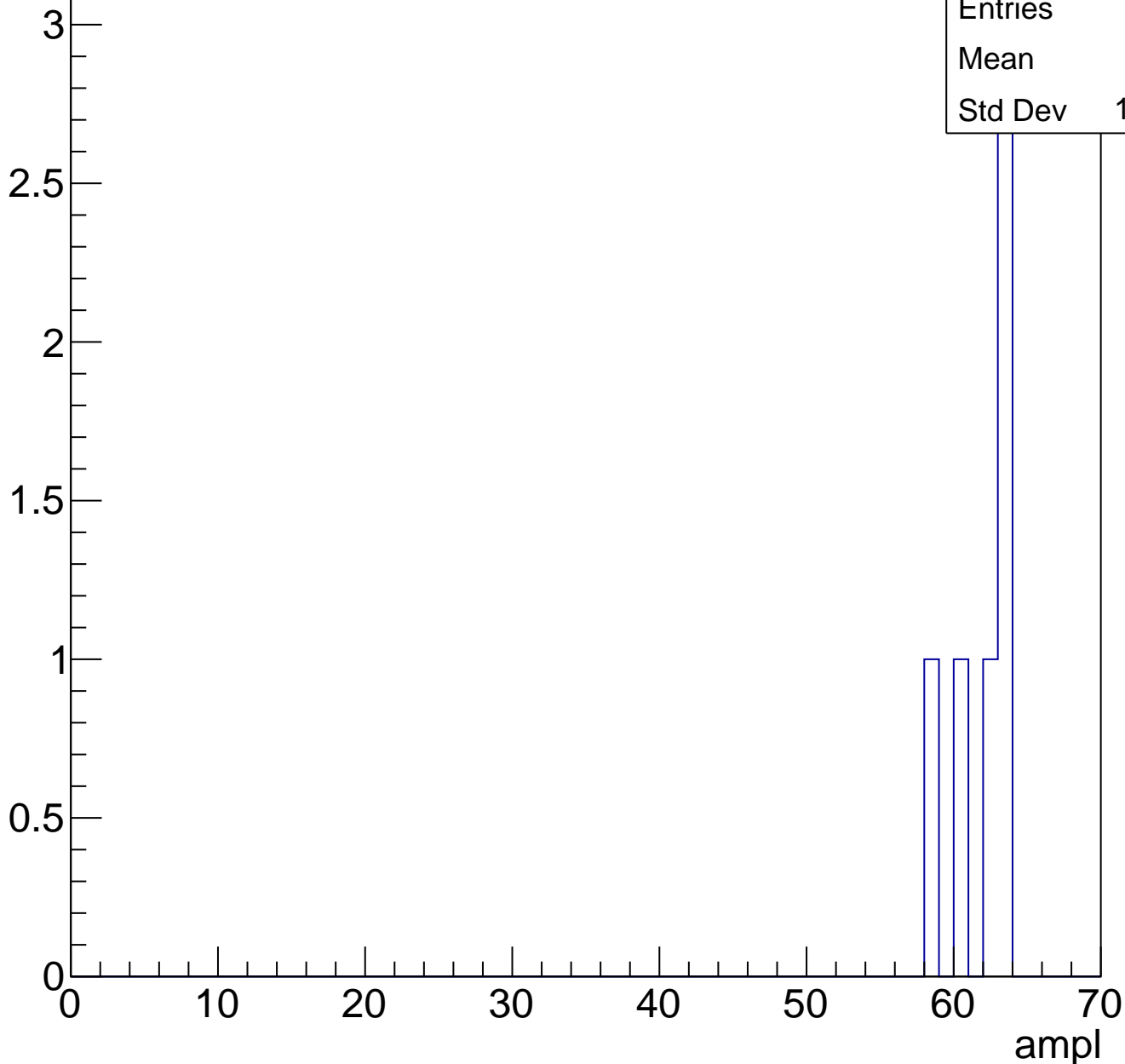
Entries	43
Mean	60
Std Dev	2.411



B1L103S, U8-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch69, adc0

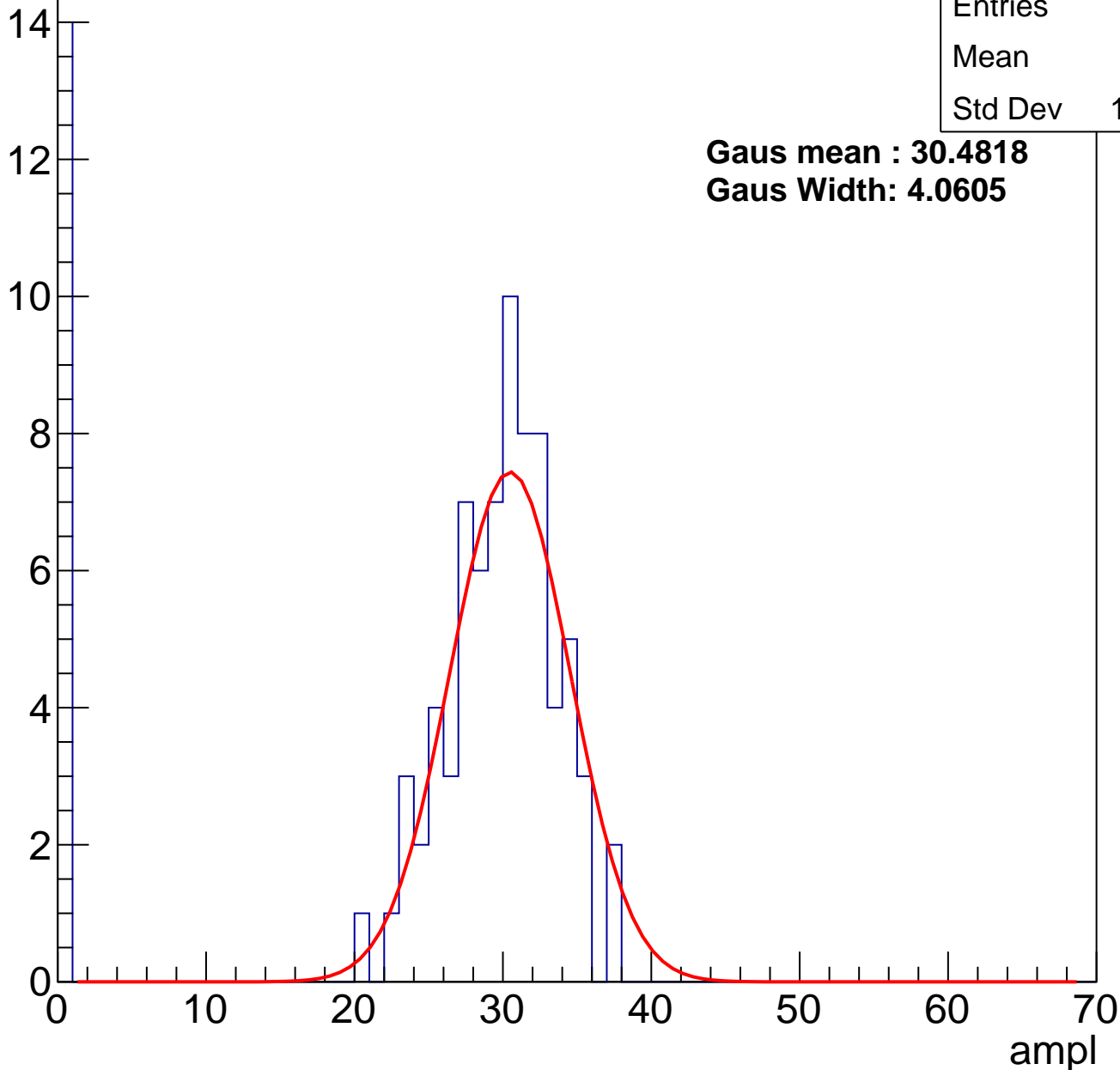
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	24.8
Std Dev	11.27

Gaus mean : 30.4818

Gaus Width: 4.0605

Entry



B1L103S, U8-ch69, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	30.07
Std Dev	14.02

Gaus mean : 36.9685

Gaus Width: 3.8613

Entry

12

10

8

6

4

2

0

0

10

20

30

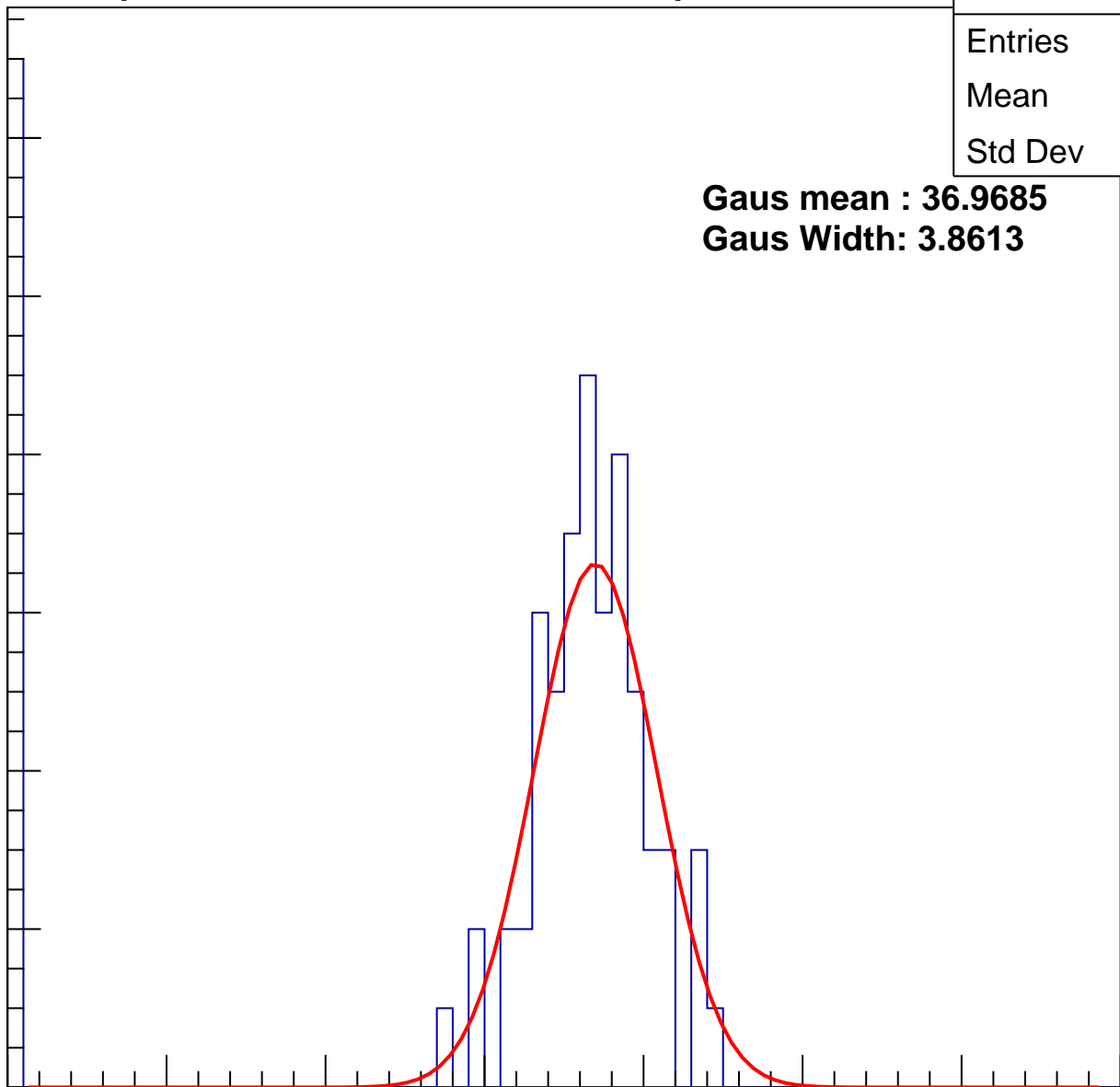
40

50

60

70

ampl



B1L103S, U8-ch69, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	42.47
Std Dev	6.028

Gaus mean : 43.5557

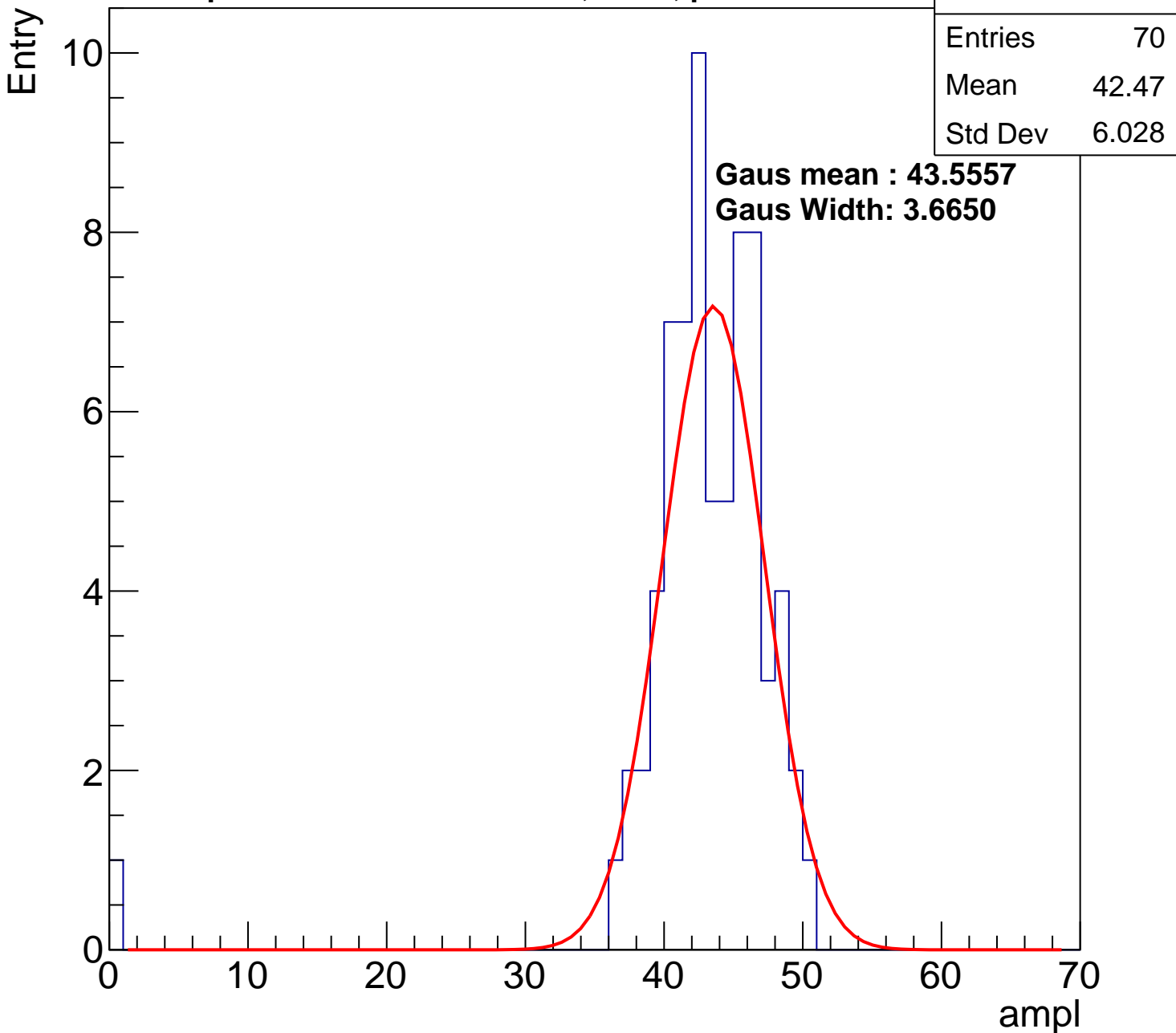
Gaus Width: 3.6650

Entry

10
8
6
4
2
0

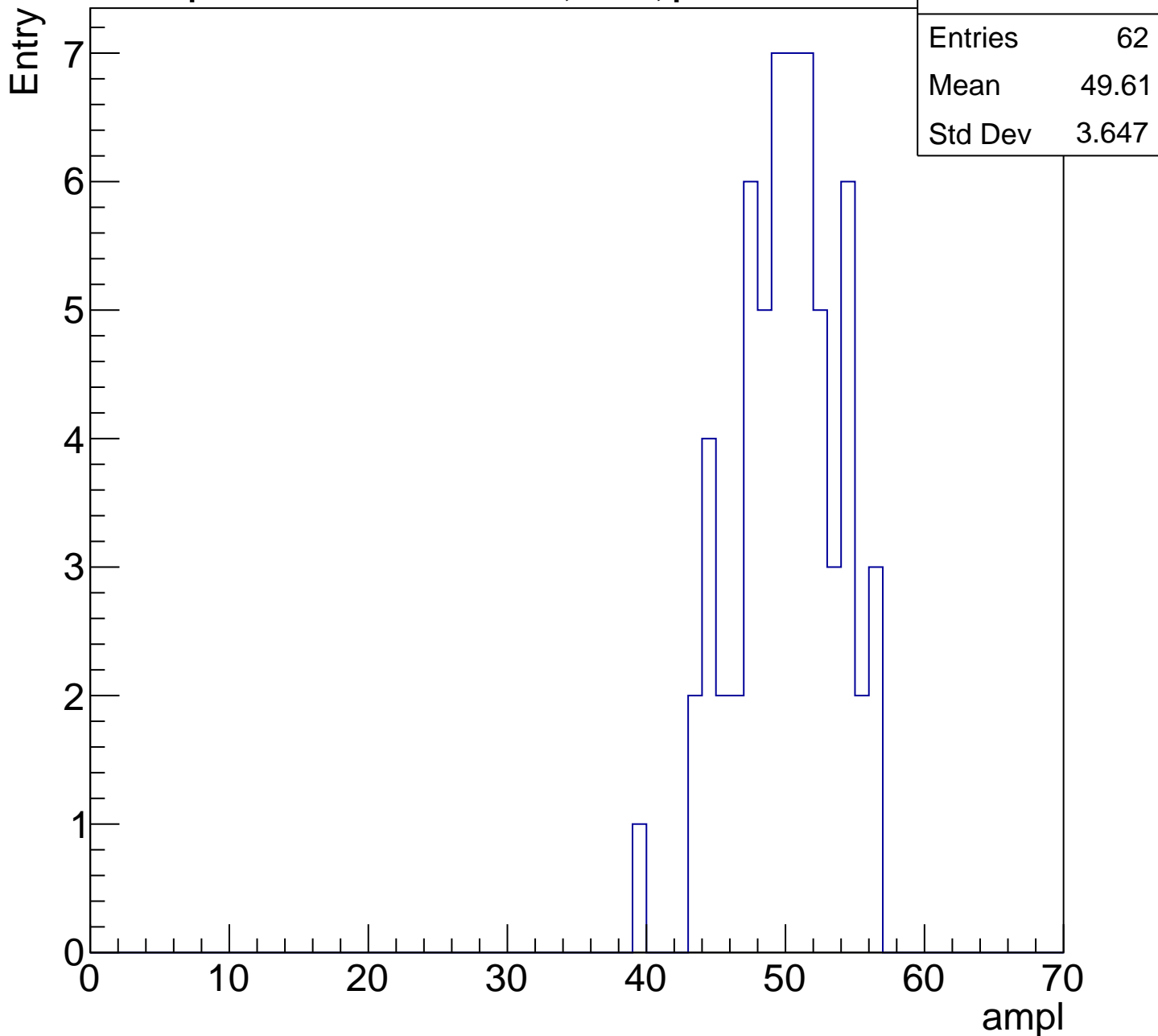
ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

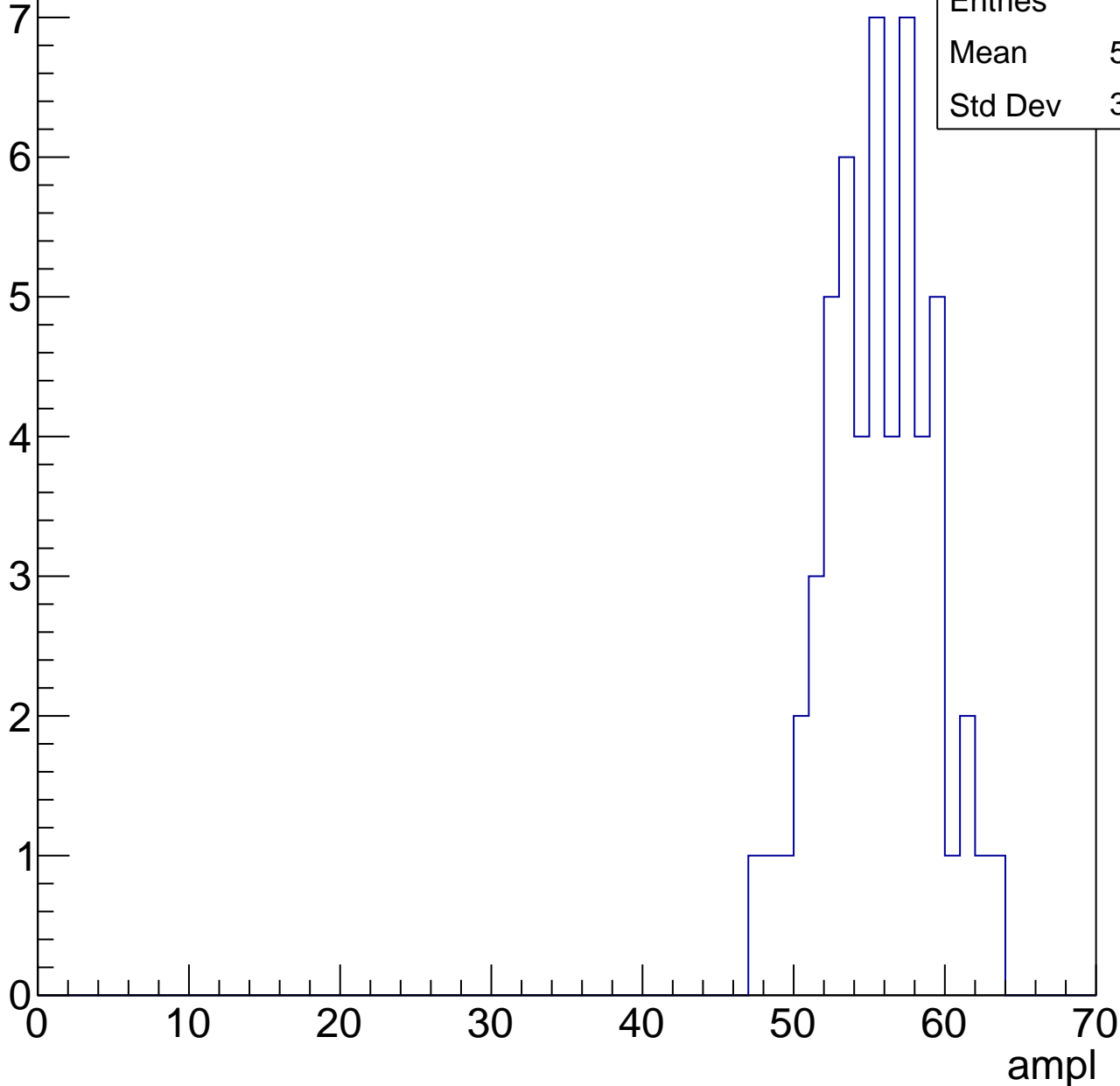


B1L103S, U8-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.15
Std Dev	3.508



B1L103S, U8-ch69, adc5

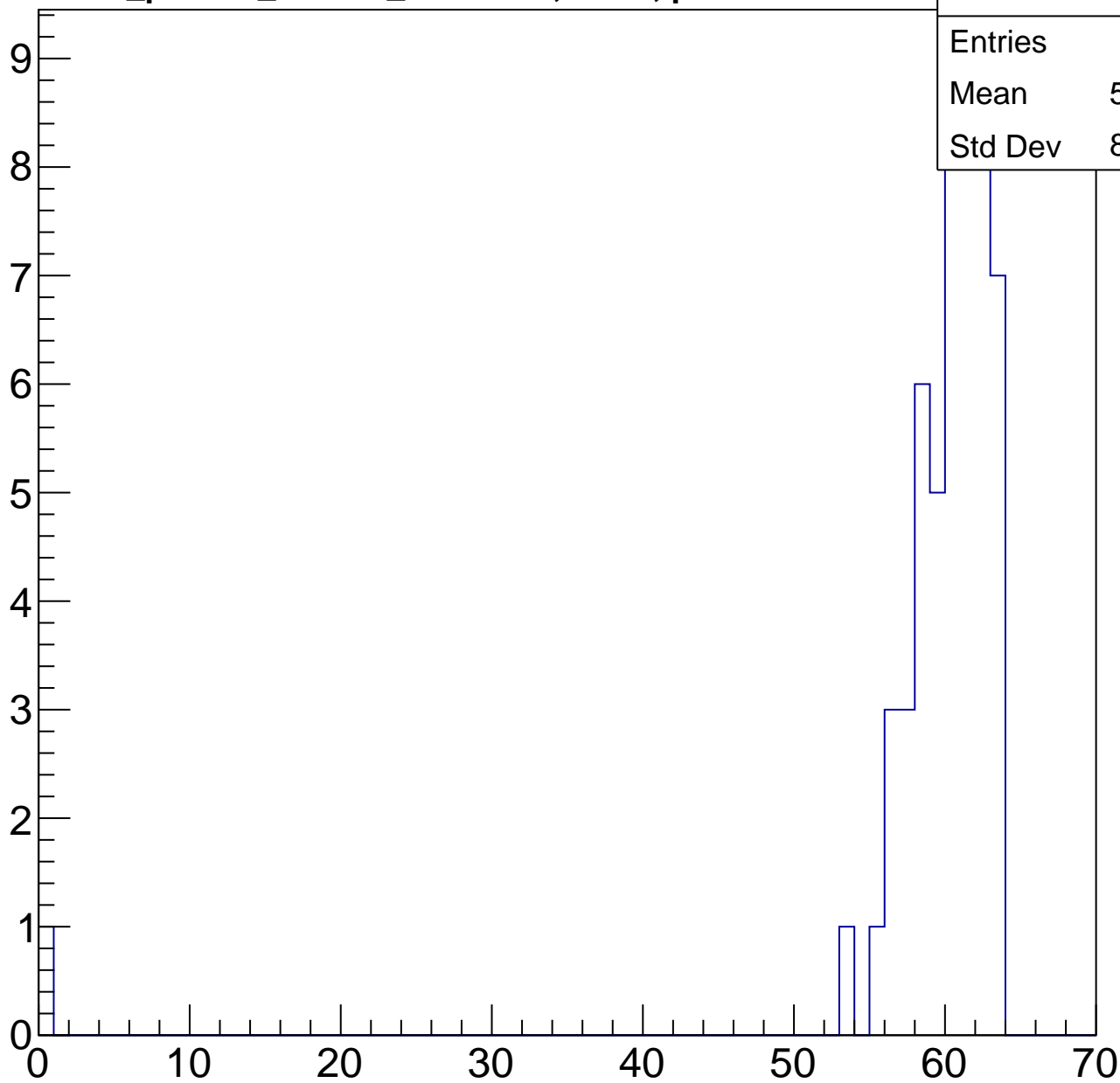
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.75
Std Dev	8.548

ampl



B1L103S, U8-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch70, adc0

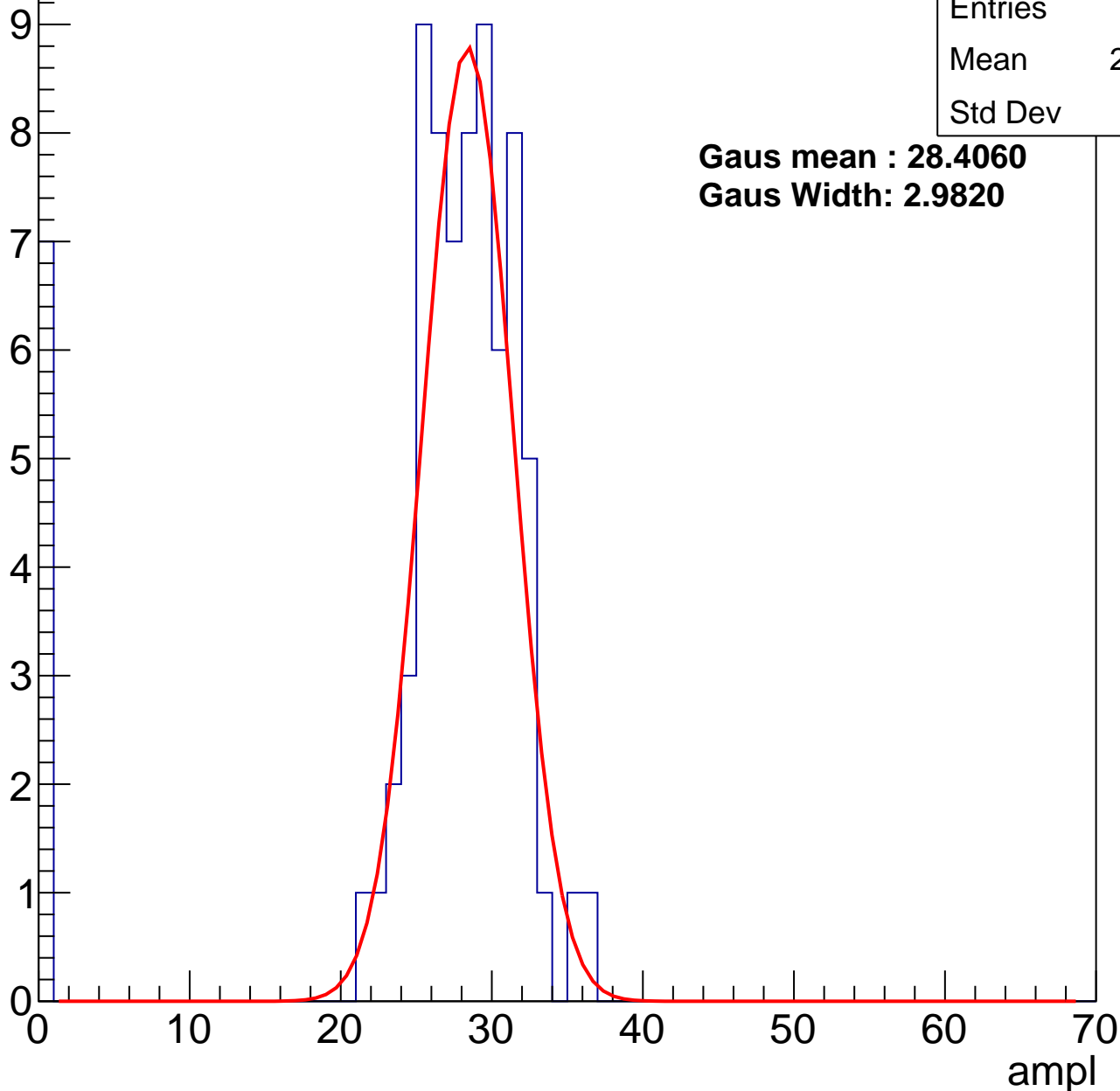
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	25.45
Std Dev	8.54

Gaus mean : 28.4060

Gaus Width: 2.9820



B1L103S, U8-ch70, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	32.29
Std Dev	11.18

Gaus mean : 36.4387

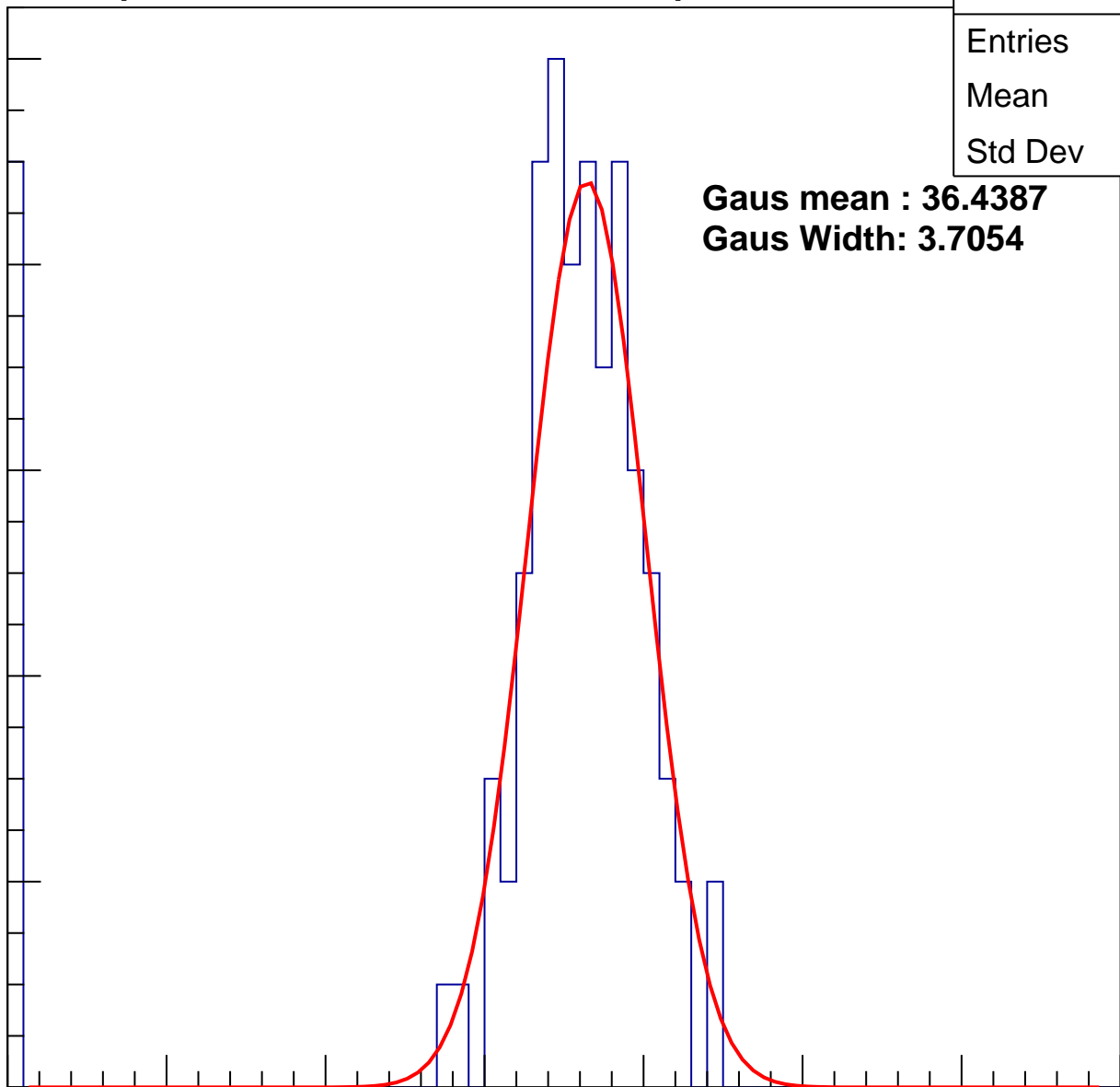
Gaus Width: 3.7054

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

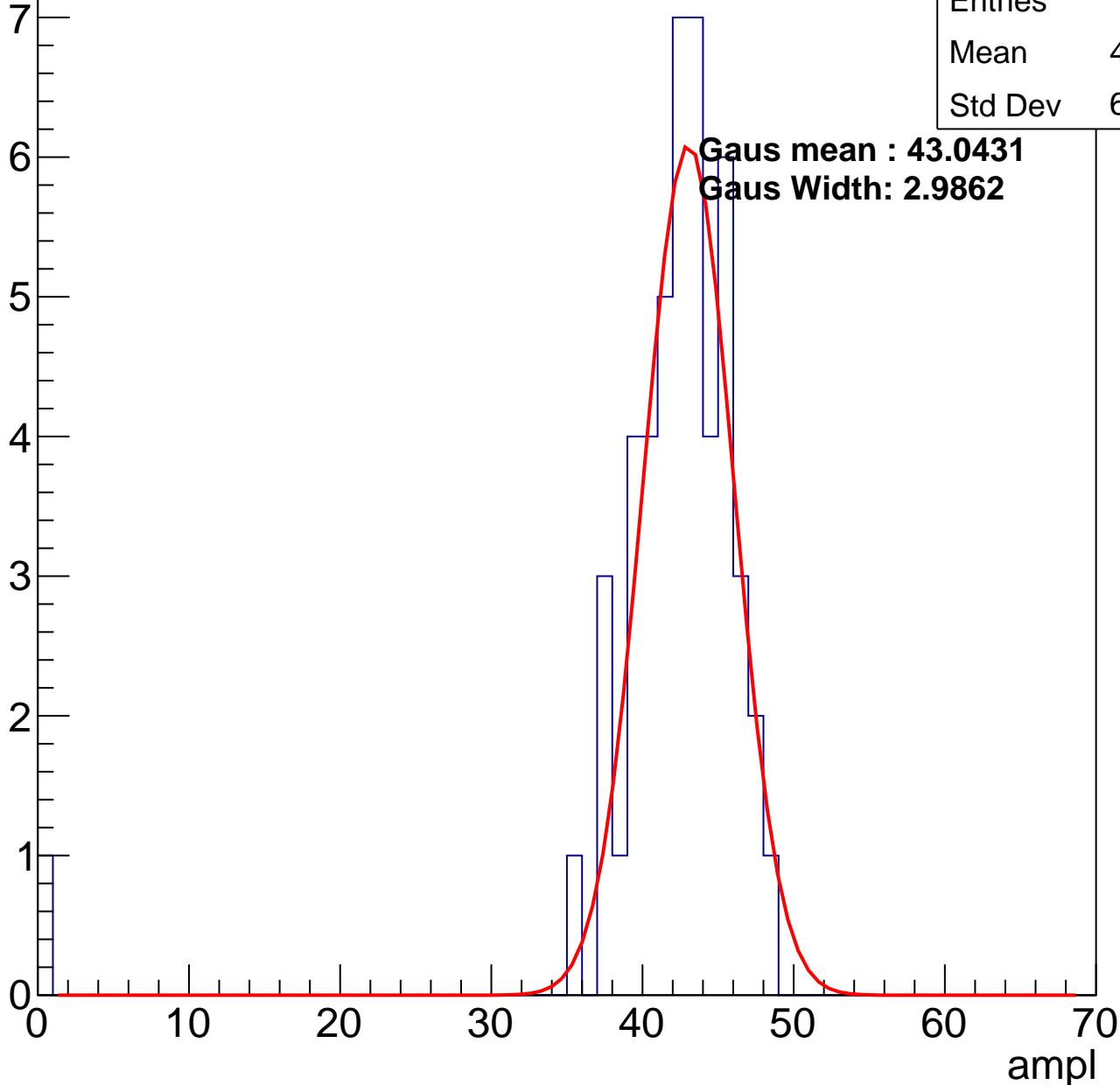


B1L103S, U8-ch70, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	41.35
Std Dev	6.626

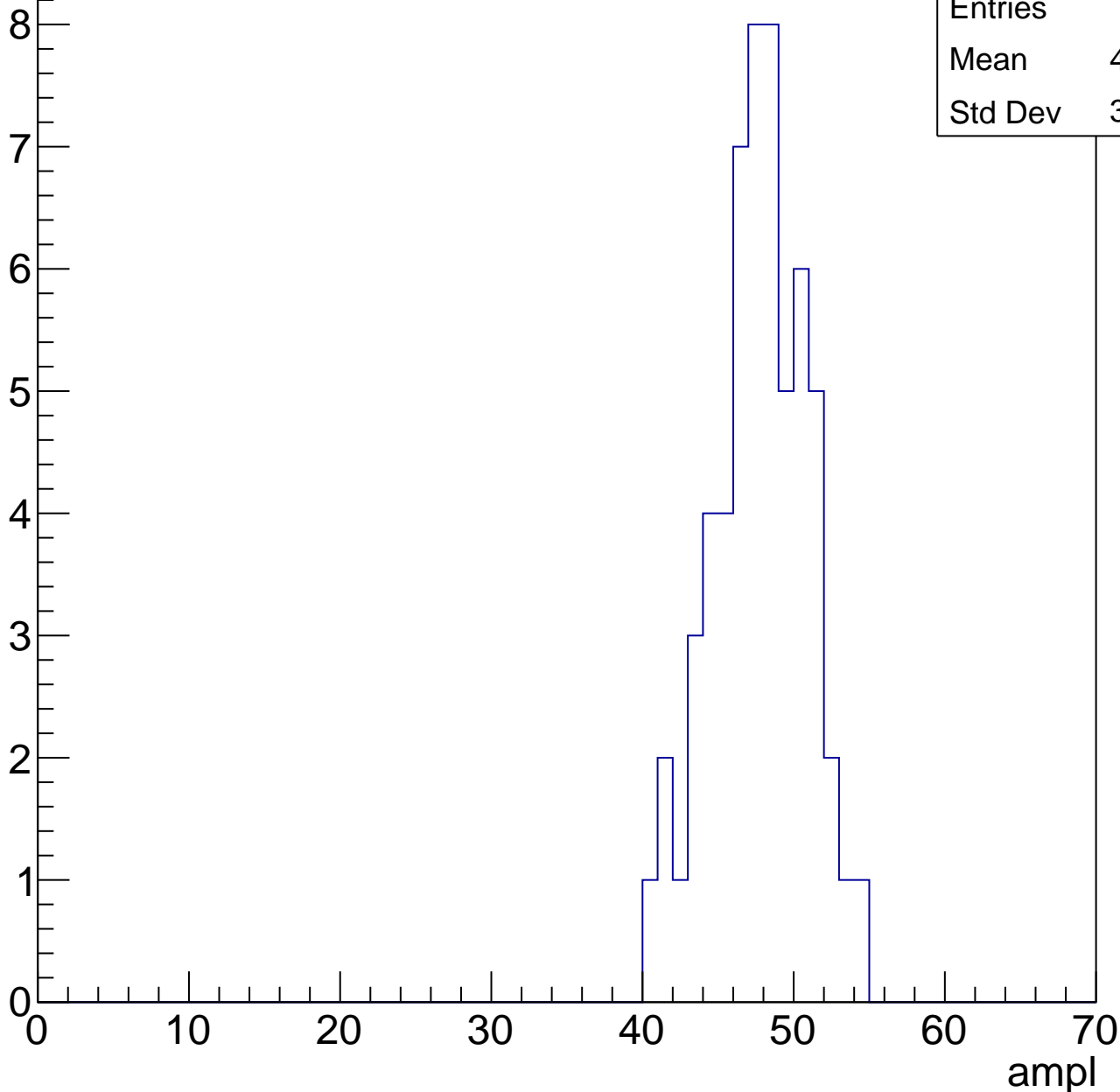


B1L103S, U8-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	47.28
Std Dev	3.067

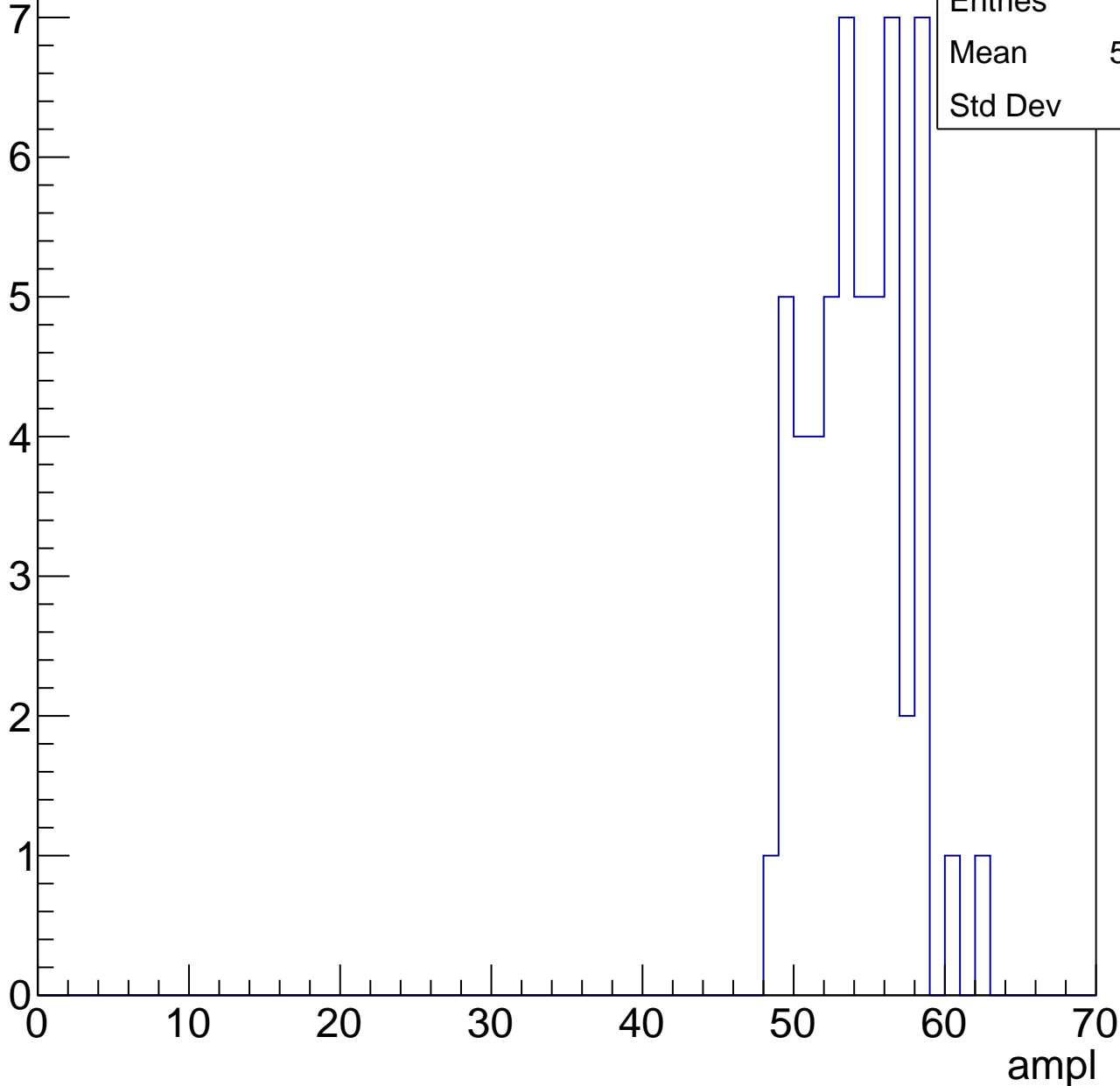


B1L103S, U8-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	53.83
Std Dev	3.19



B1L103S, U8-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

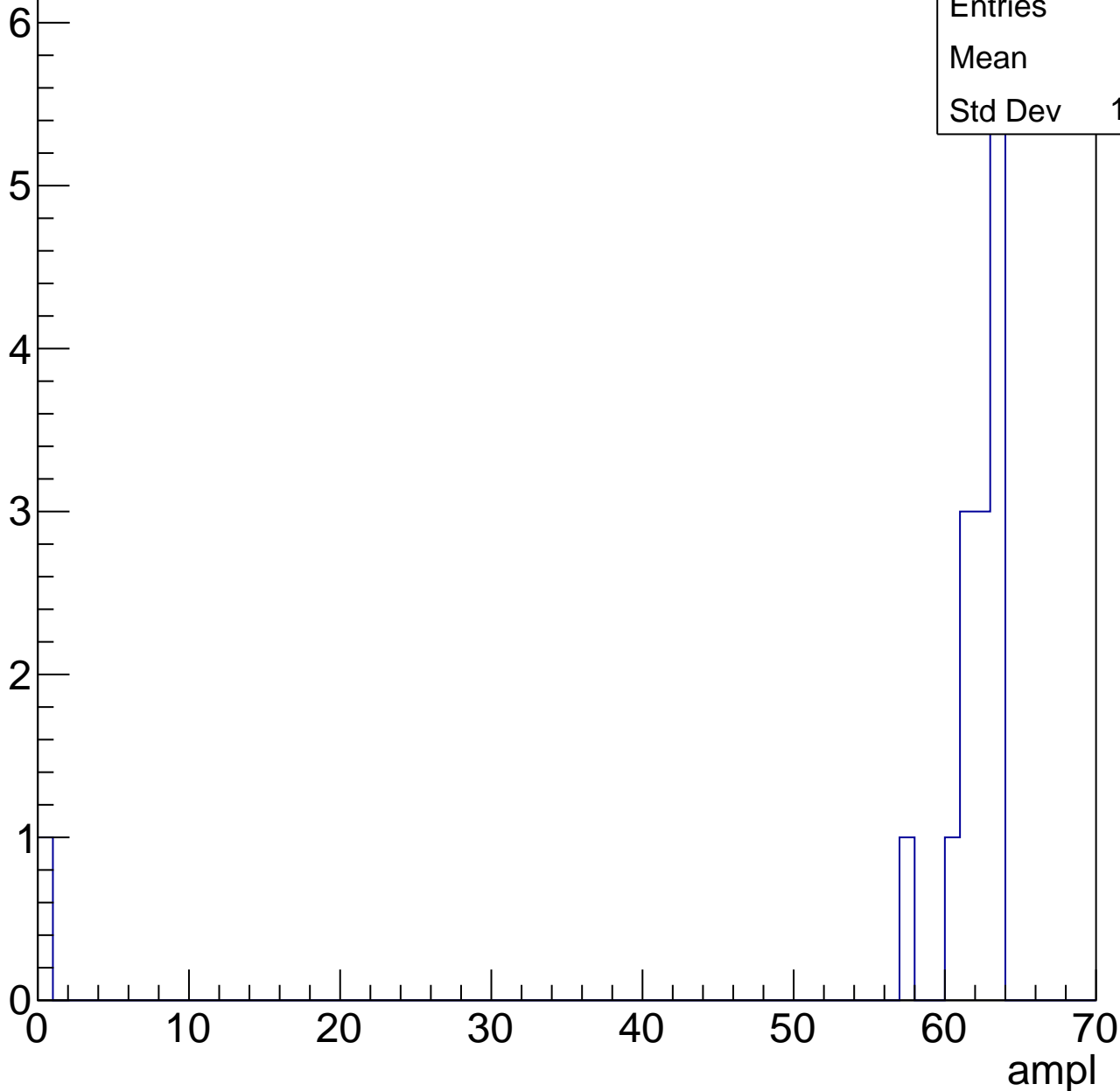
Entries	63
Mean	58.19
Std Dev	3.172

B1L103S, U8-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

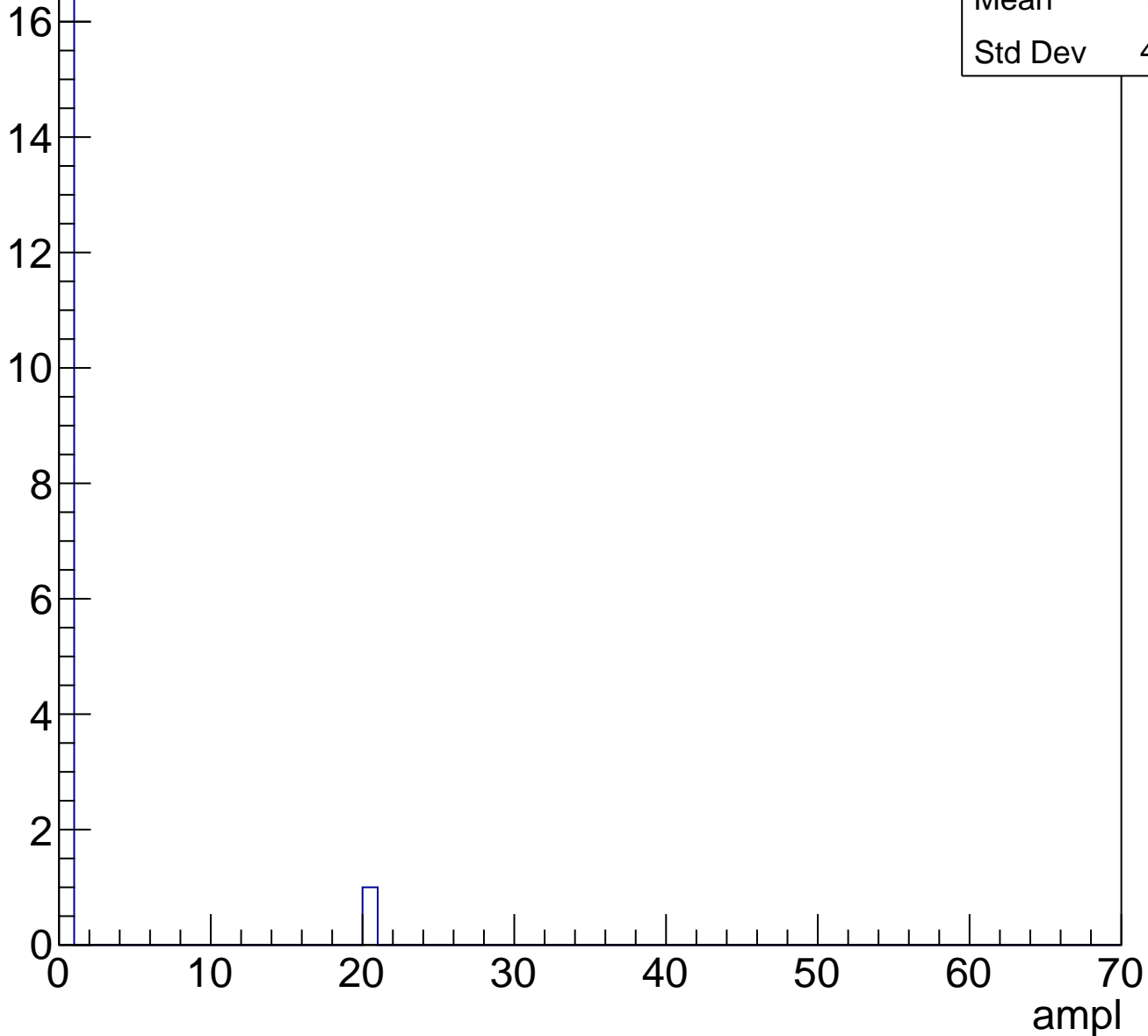
Entries	15
Mean	57.6
Std Dev	15.47



B1L103S, U8-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	1.111
Std Dev	4.581

B1L103S, U8-ch71, adc0

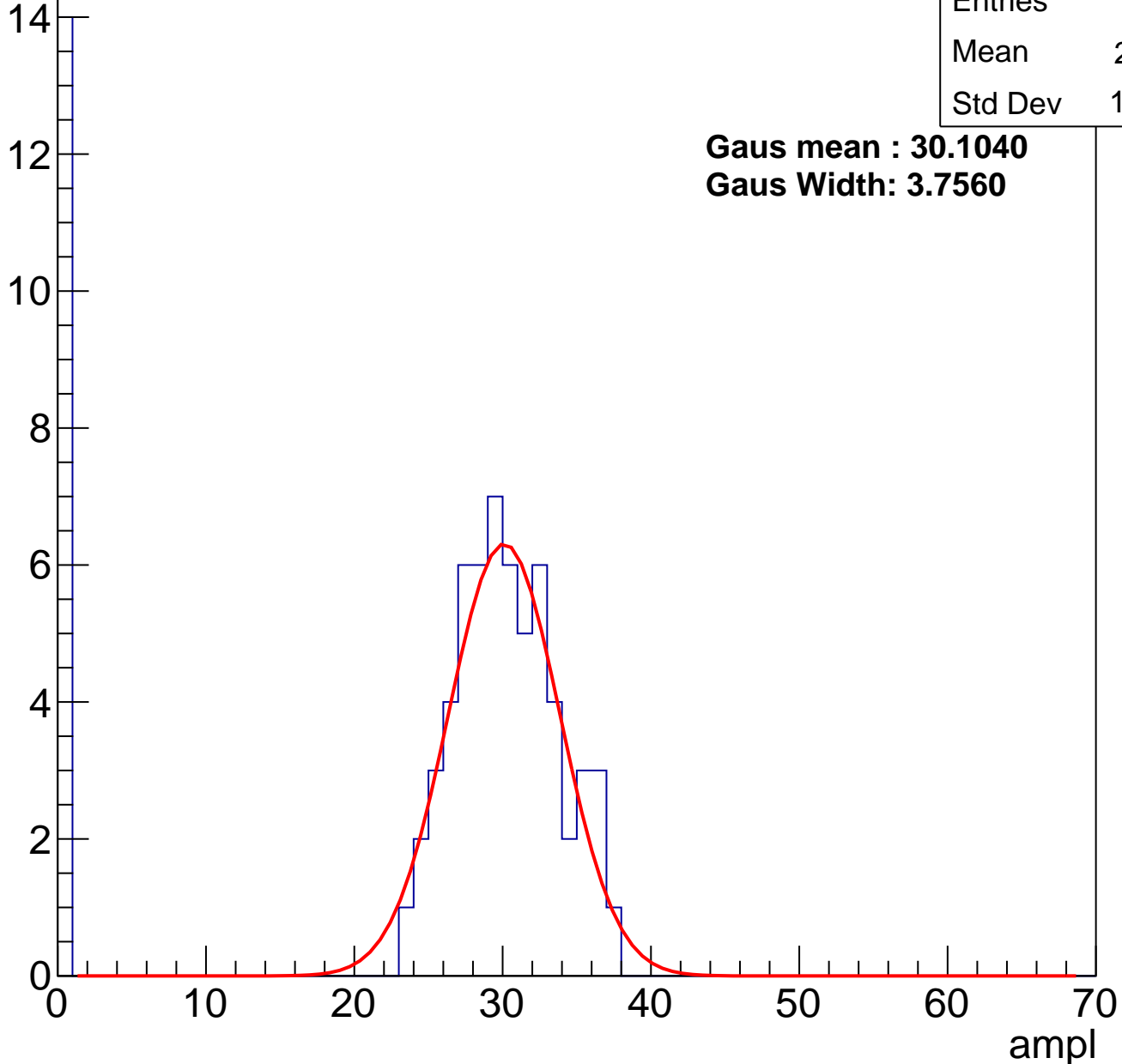
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	24.11
Std Dev	12.13

Gaus mean : 30.1040

Gaus Width: 3.7560

Entry



B1L103S, U8-ch71, adc1

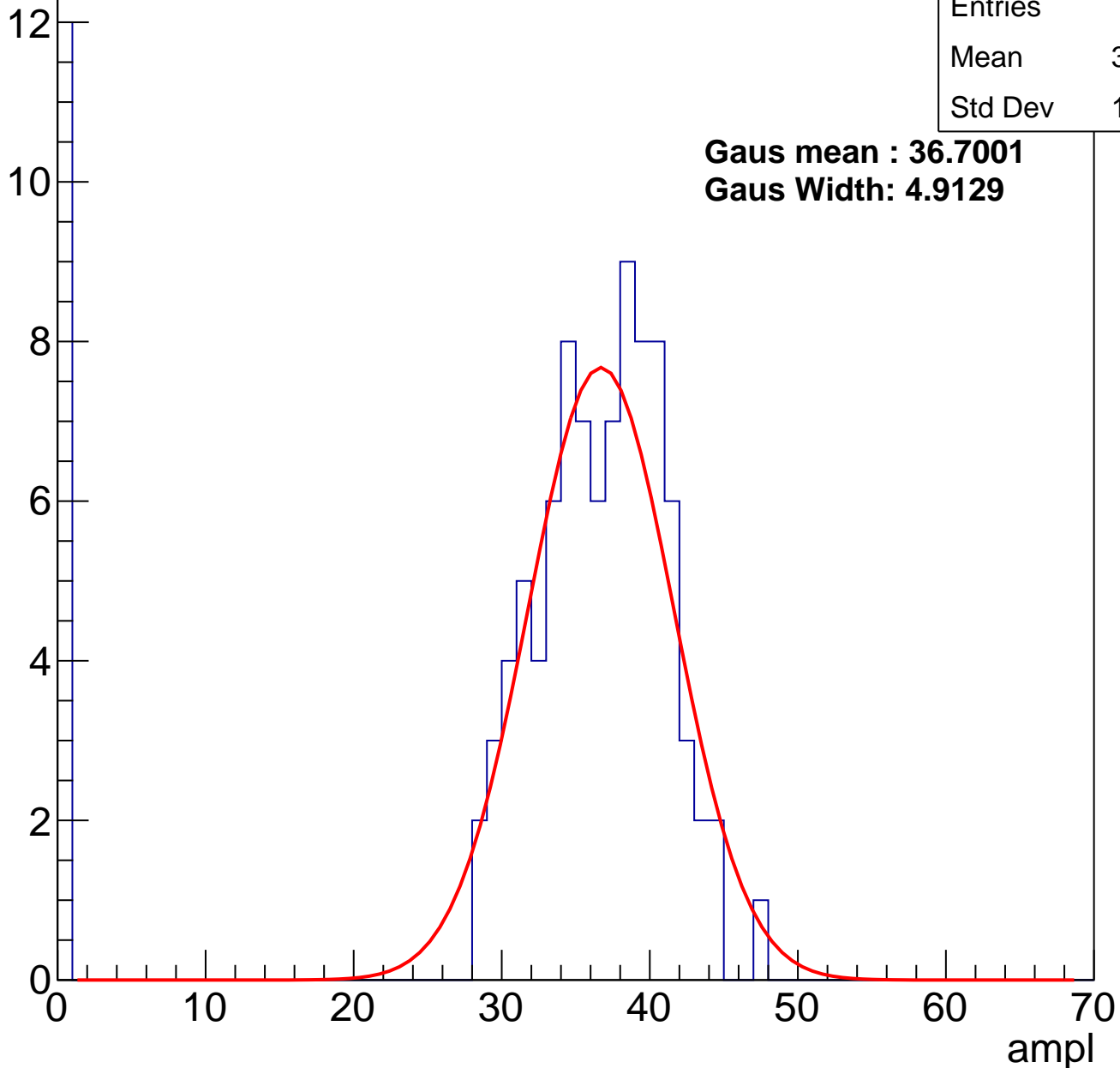
calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	32.07
Std Dev	12.27

Gaus mean : 36.7001

Gaus Width: 4.9129

Entry



B1L103S, U8-ch71, adc2

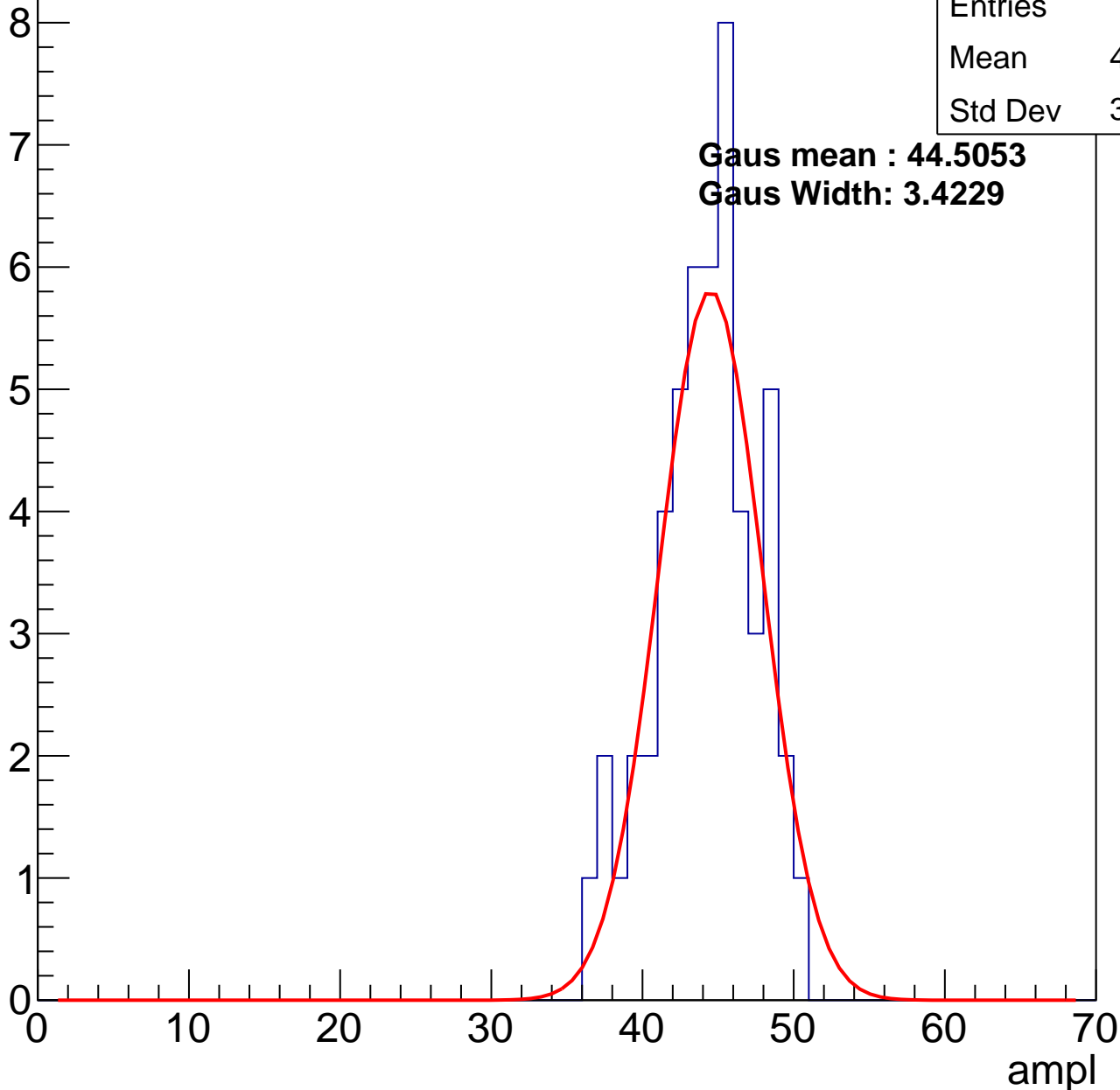
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	43.75
Std Dev	3.269

Gaus mean : 44.5053

Gaus Width: 3.4229

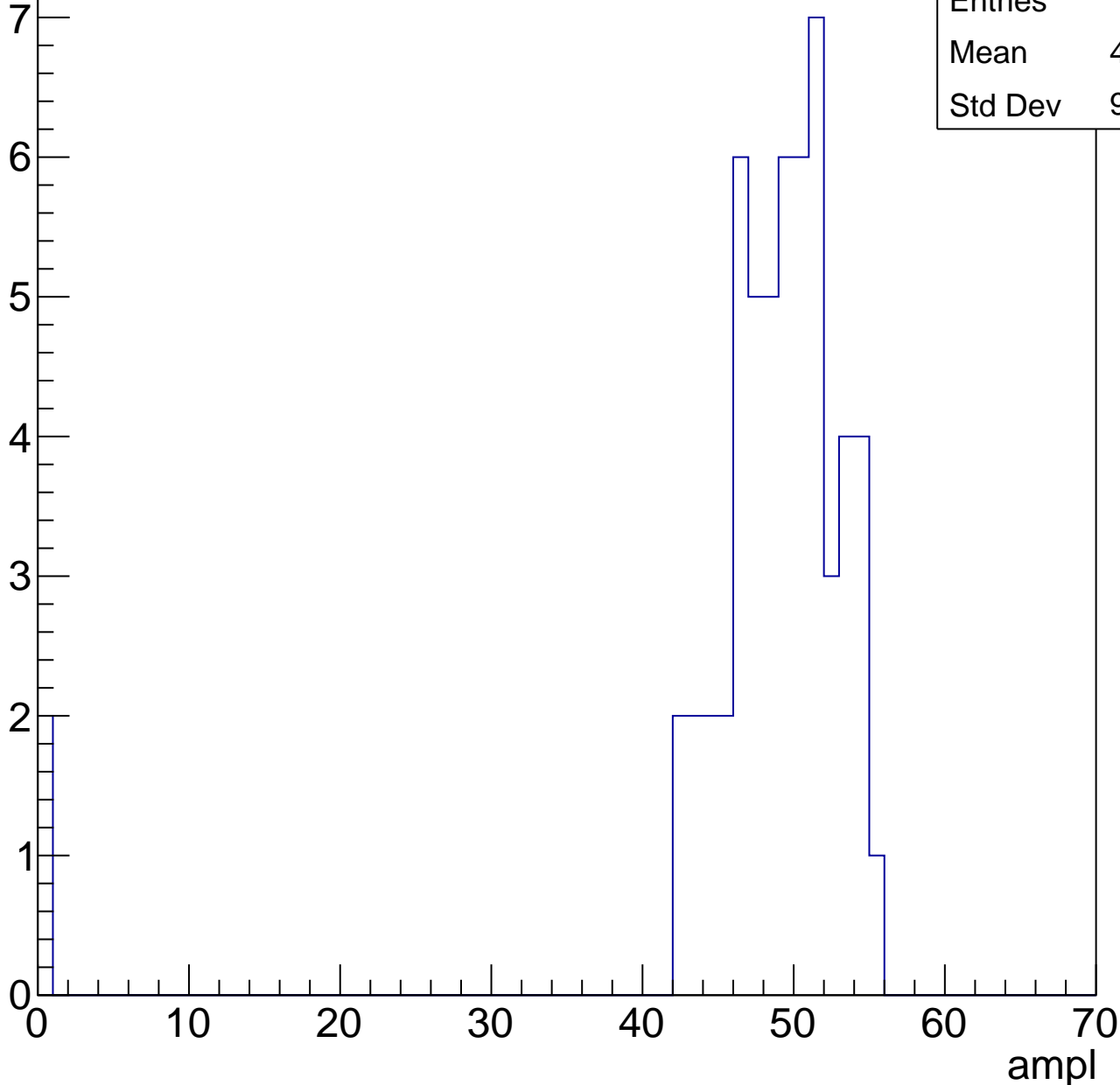


B1L103S, U8-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	47.18
Std Dev	9.555

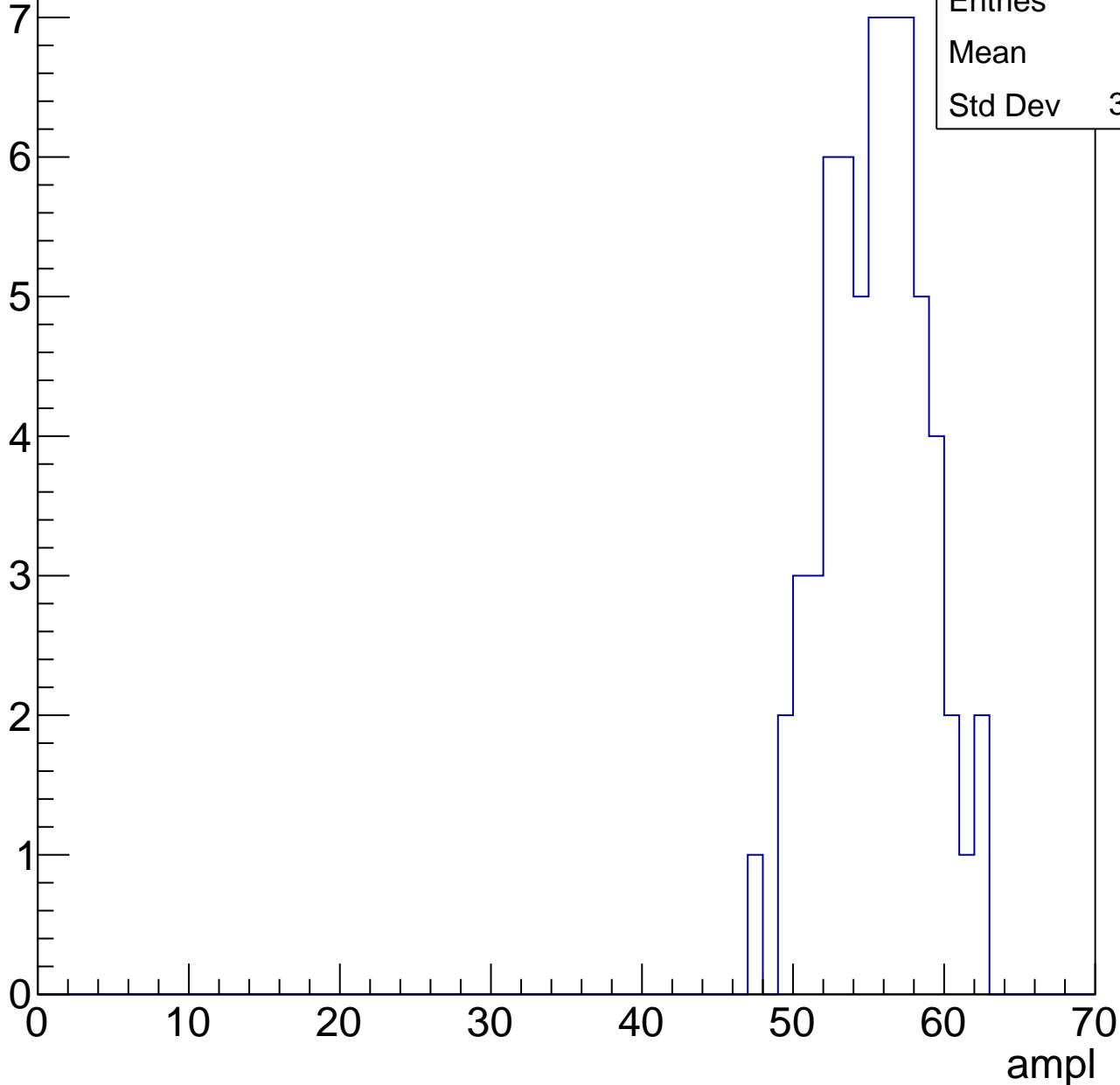


B1L103S, U8-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55
Std Dev	3.314

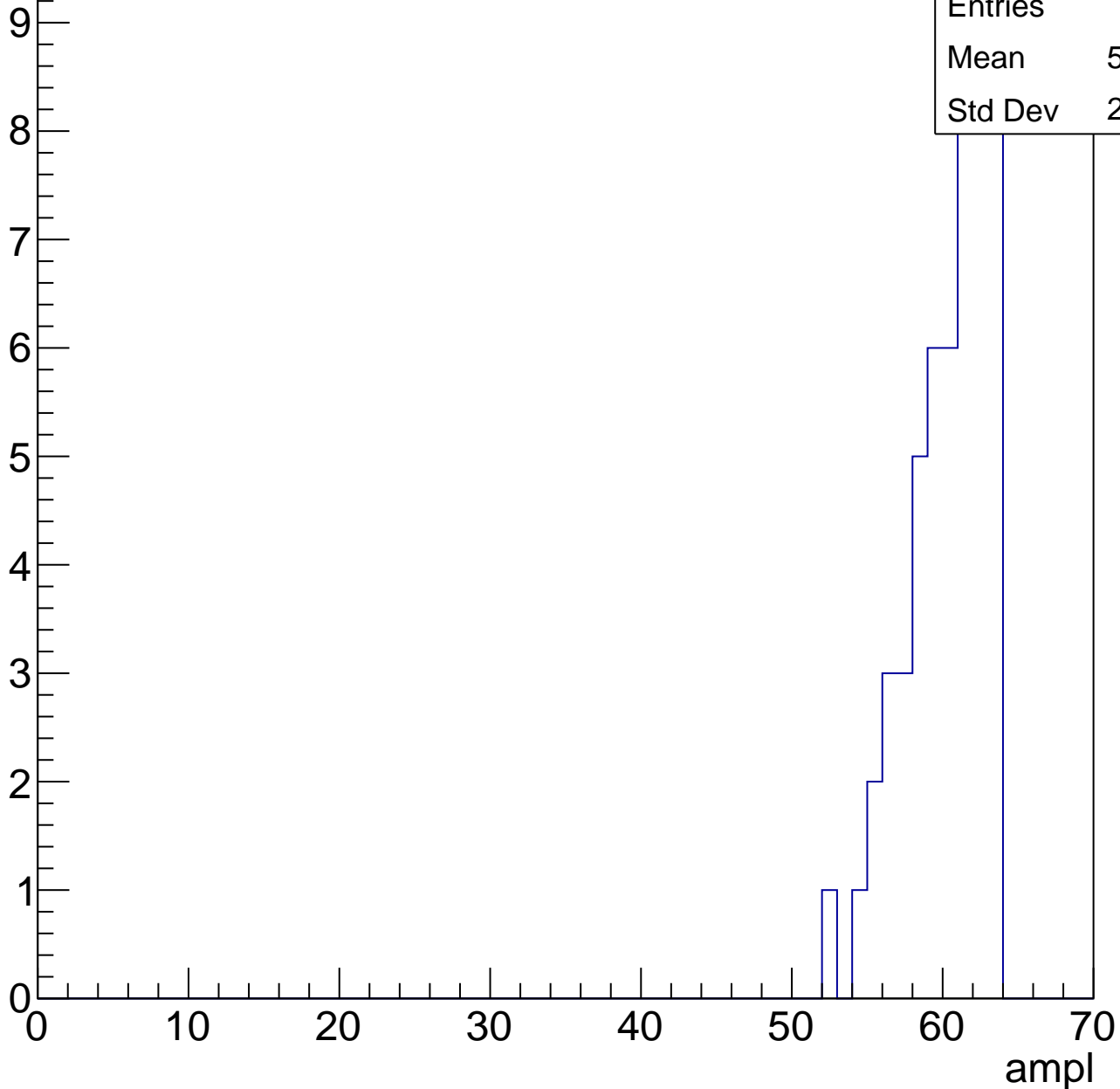


B1L103S, U8-ch71, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

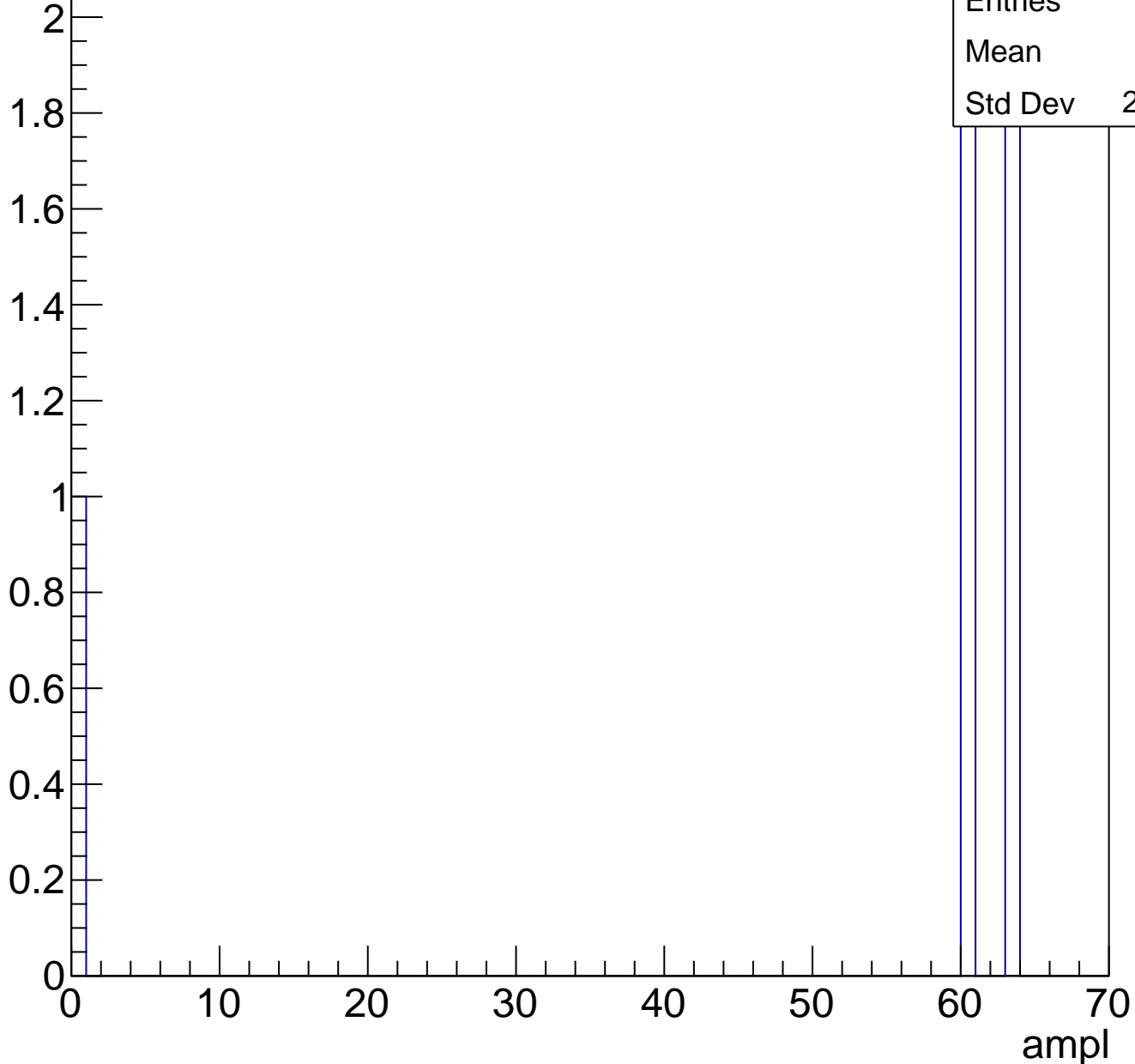
Entries	52
Mean	59.77
Std Dev	2.636



B1L103S, U8-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	49.2
Std Dev	24.64

B1L103S, U8-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch72, adc0

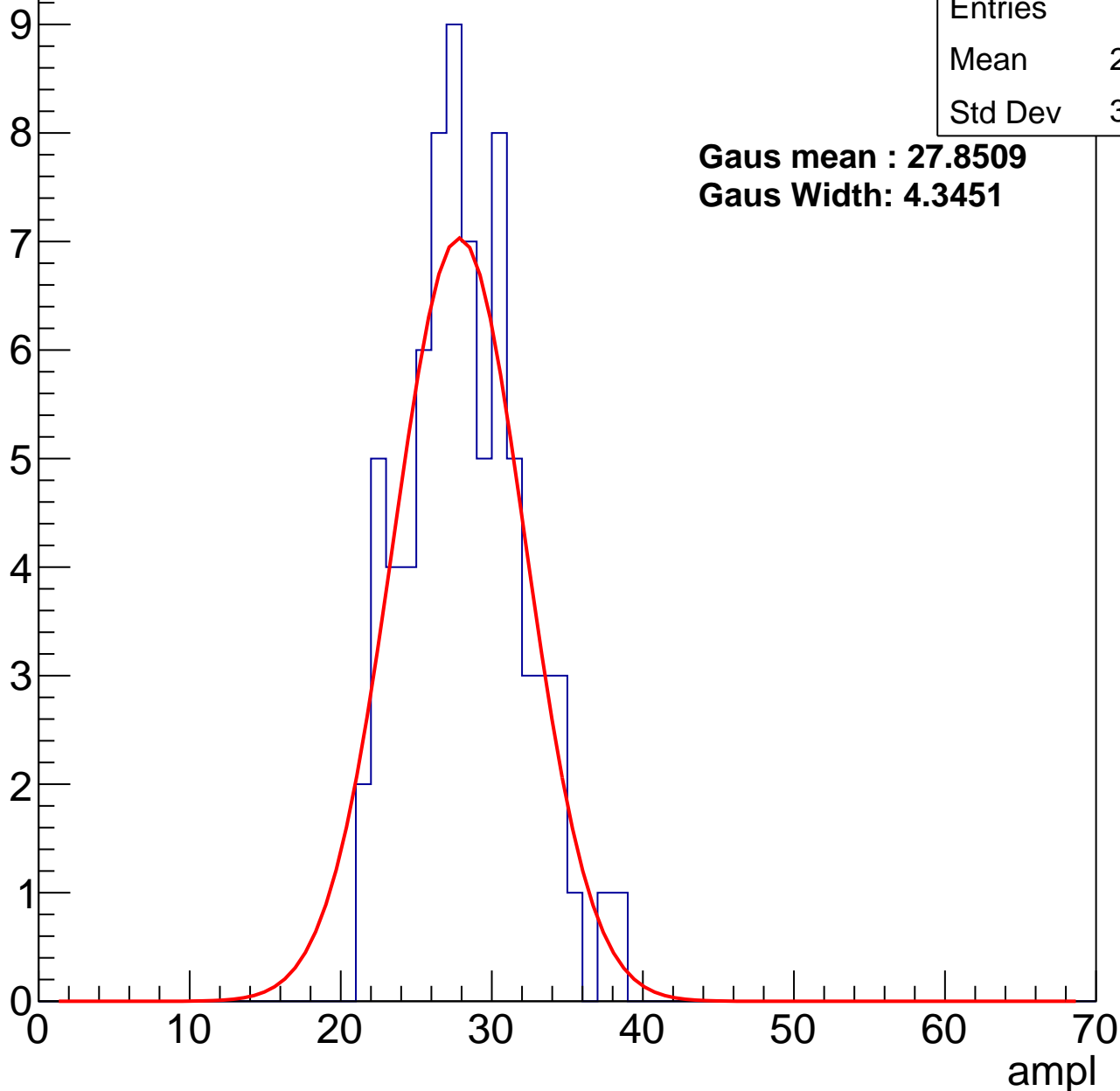
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	27.79
Std Dev	3.789

Gaus mean : 27.8509

Gaus Width: 4.3451



B1L103S, U8-ch72, adc1

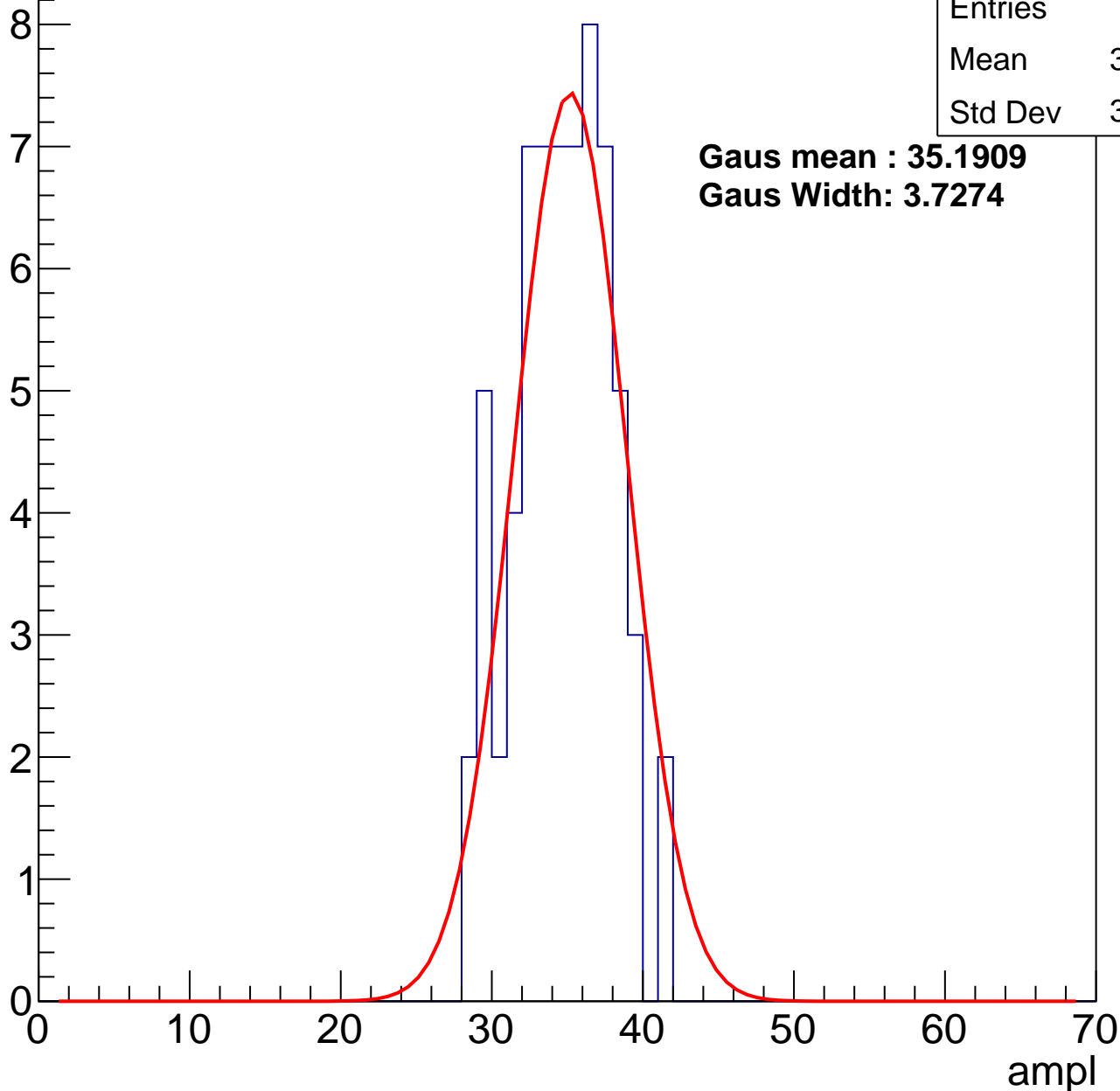
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.23
Std Dev	3.142

Gaus mean : 35.1909

Gaus Width: 3.7274



B1L103S, U8-ch72, adc2

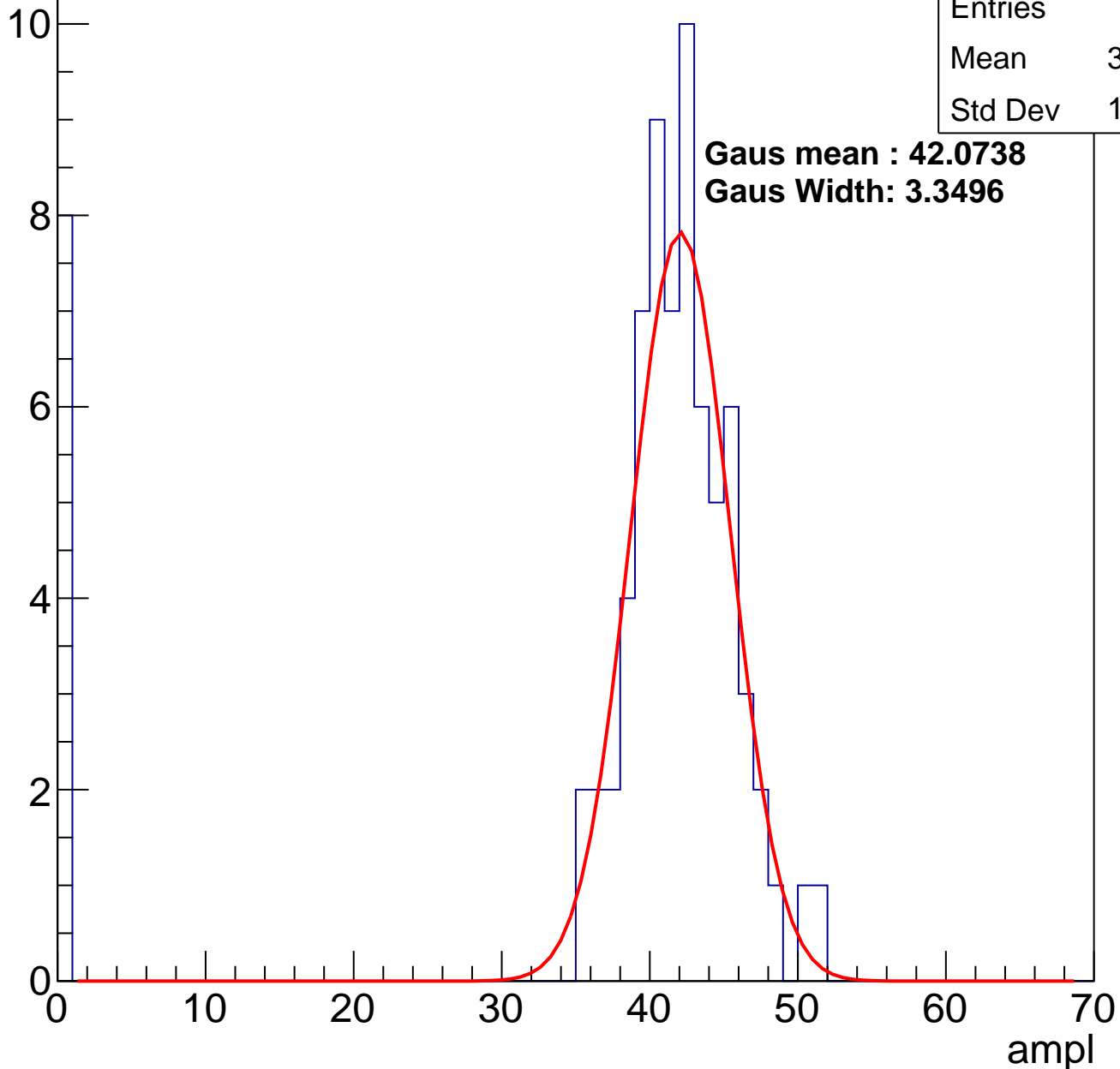
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	37.33
Std Dev	13.18

Gaus mean : 42.0738

Gaus Width: 3.3496

Entry

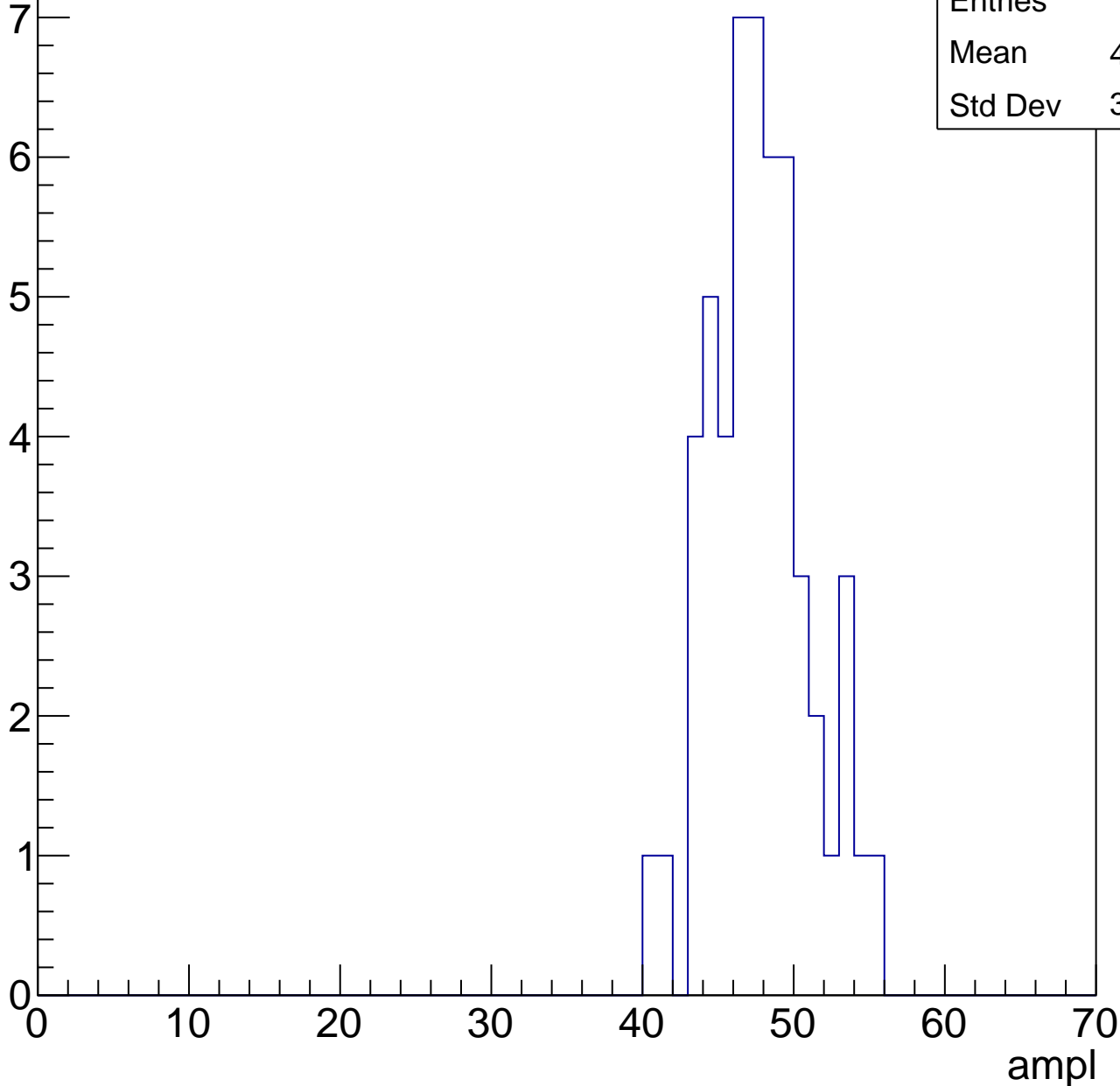


B1L103S, U8-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	47.27
Std Dev	3.247

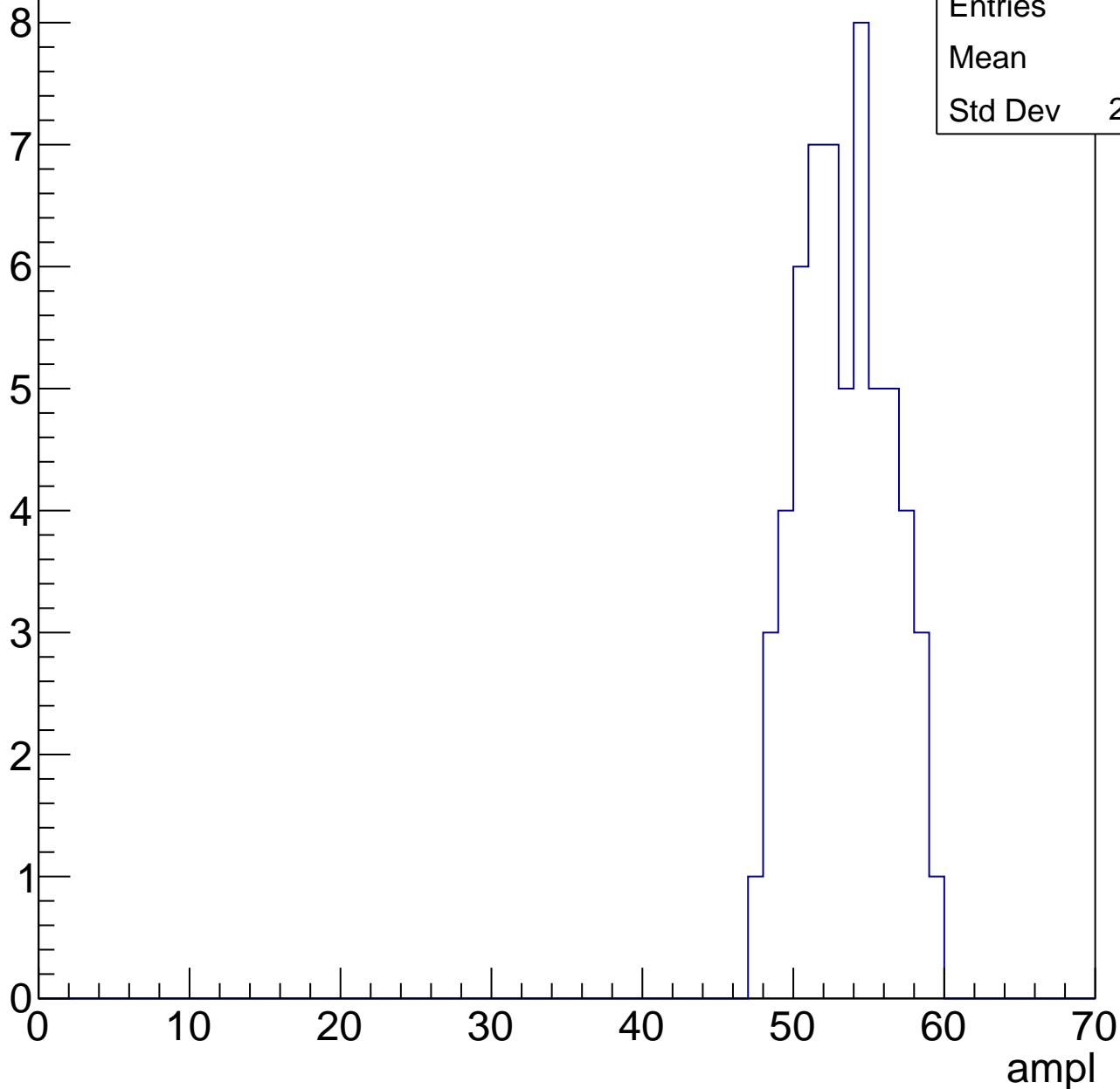


B1L103S, U8-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	52.9
Std Dev	2.944

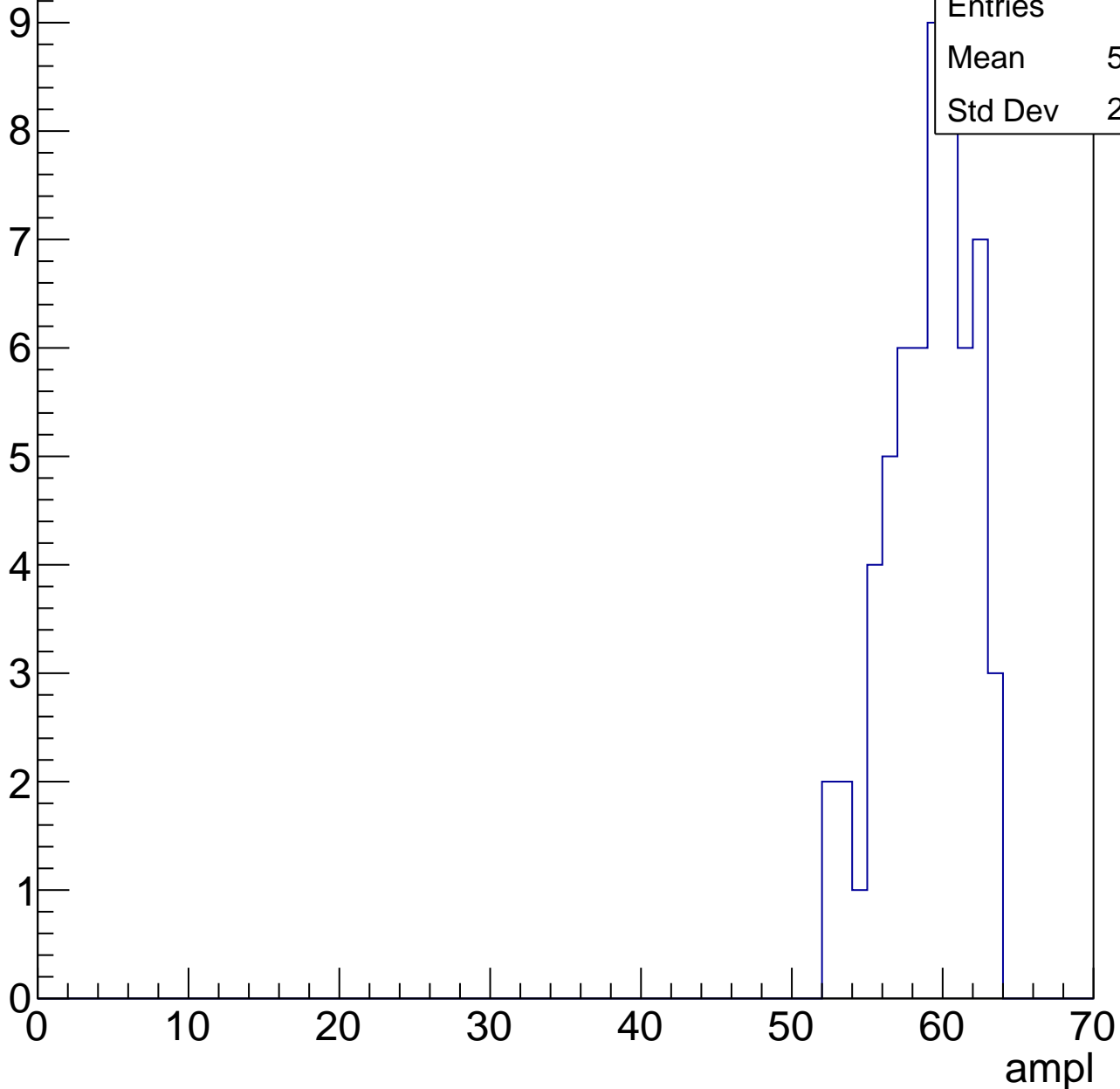


B1L103S, U8-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	58.54
Std Dev	2.806

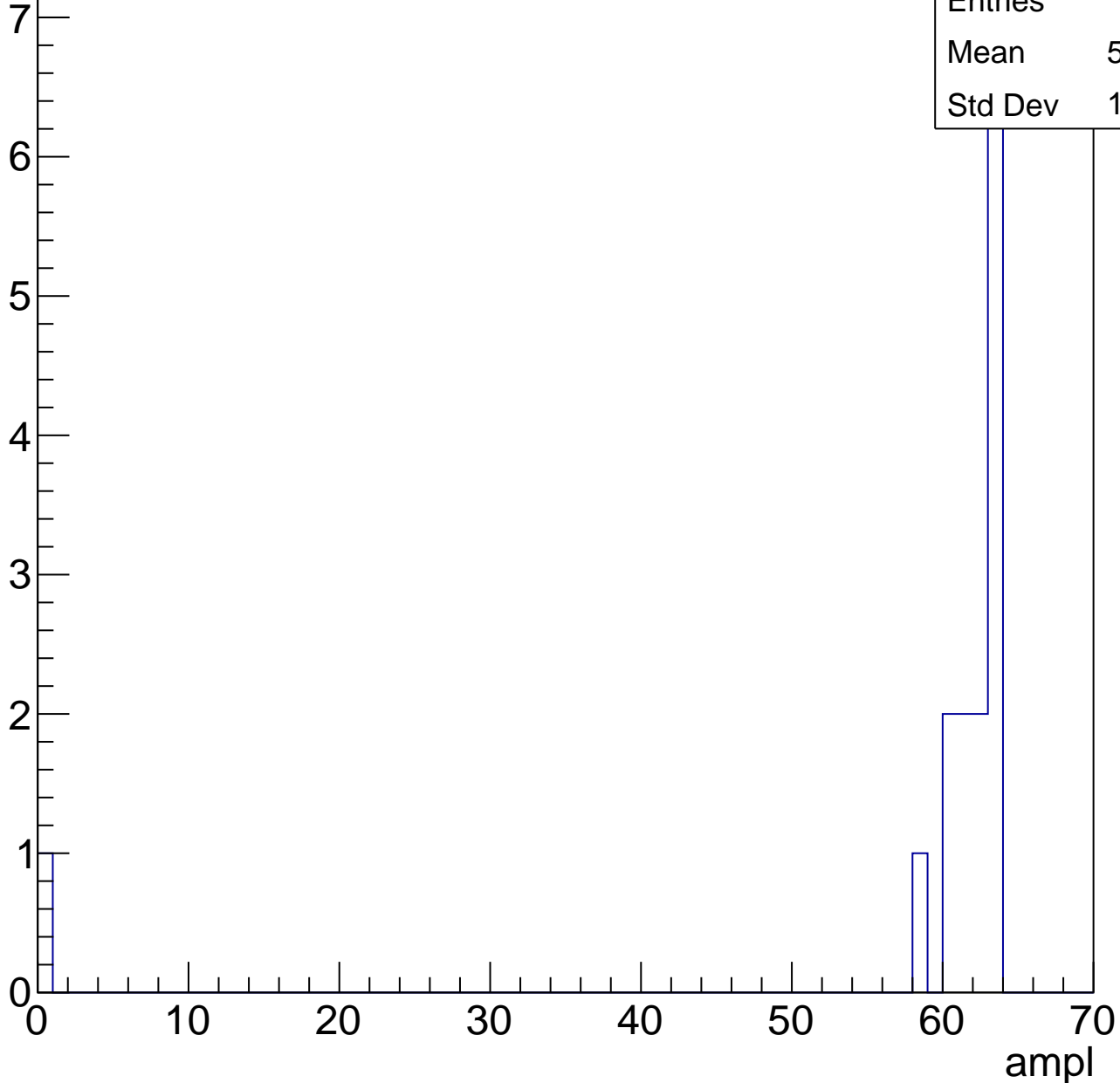


B1L103S, U8-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.67
Std Dev	15.48



B1L103S, U8-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	9.619
Std Dev	21.46

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

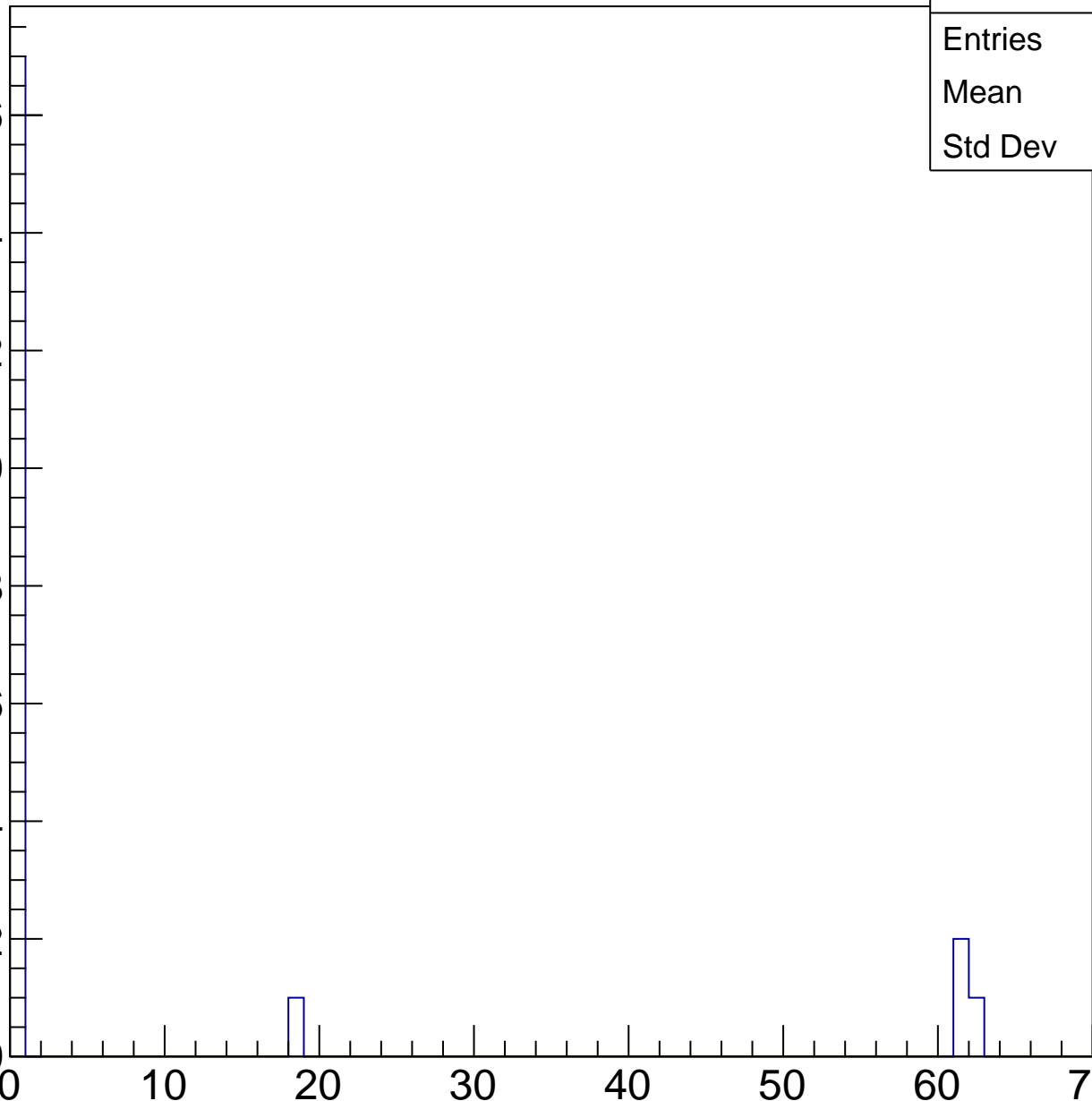
40

50

60

70

ampl



B1L103S, U8-ch73, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	24.58
Std Dev	11.52

Gaus mean : 30.3836

Gaus Width: 3.6570

Entry

12

10

8

6

4

2

0

0

10

20

30

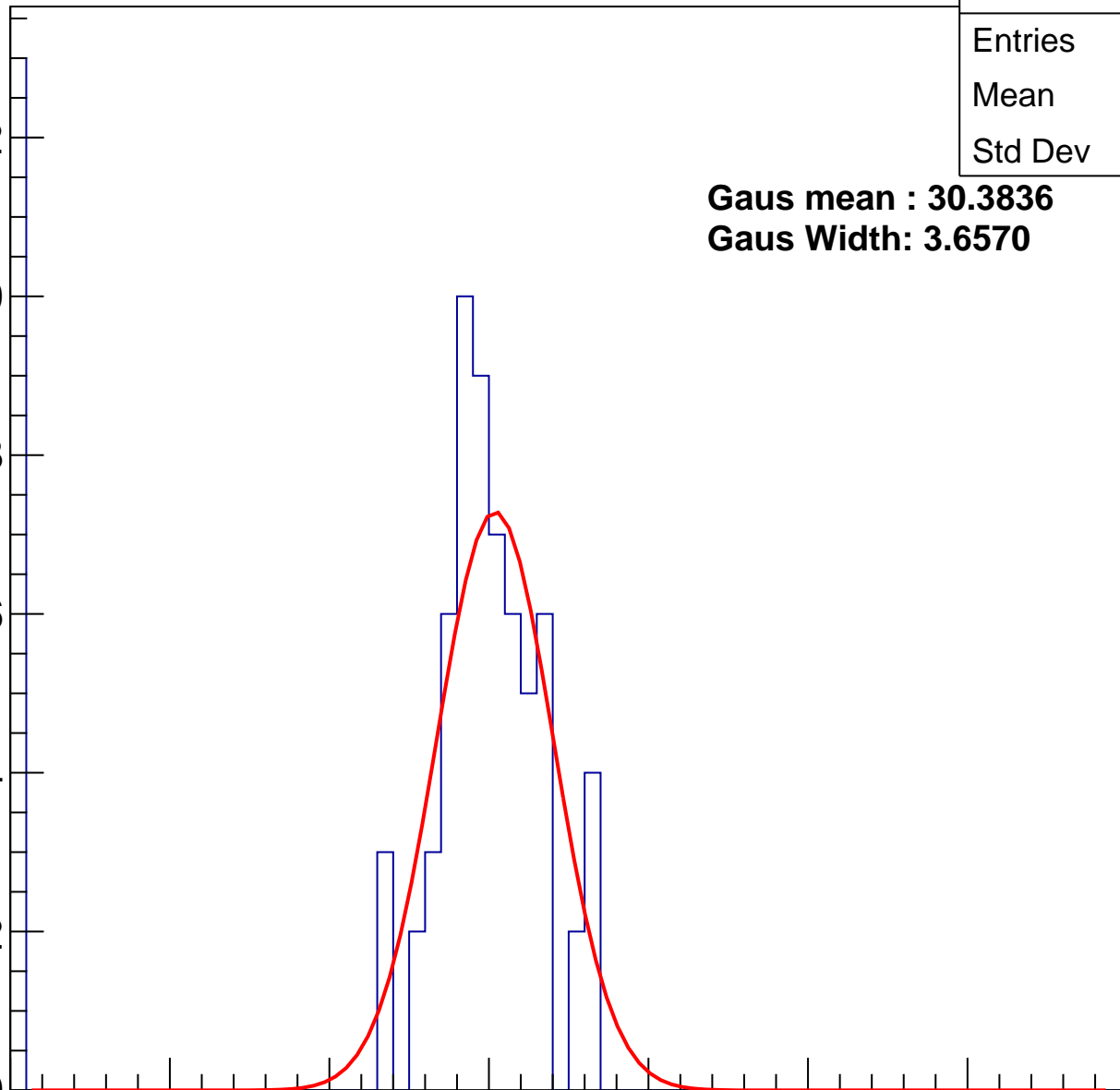
40

50

60

70

ampl



B1L103S, U8-ch73, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.68
Std Dev	12.16

Gaus mean : 37.1239

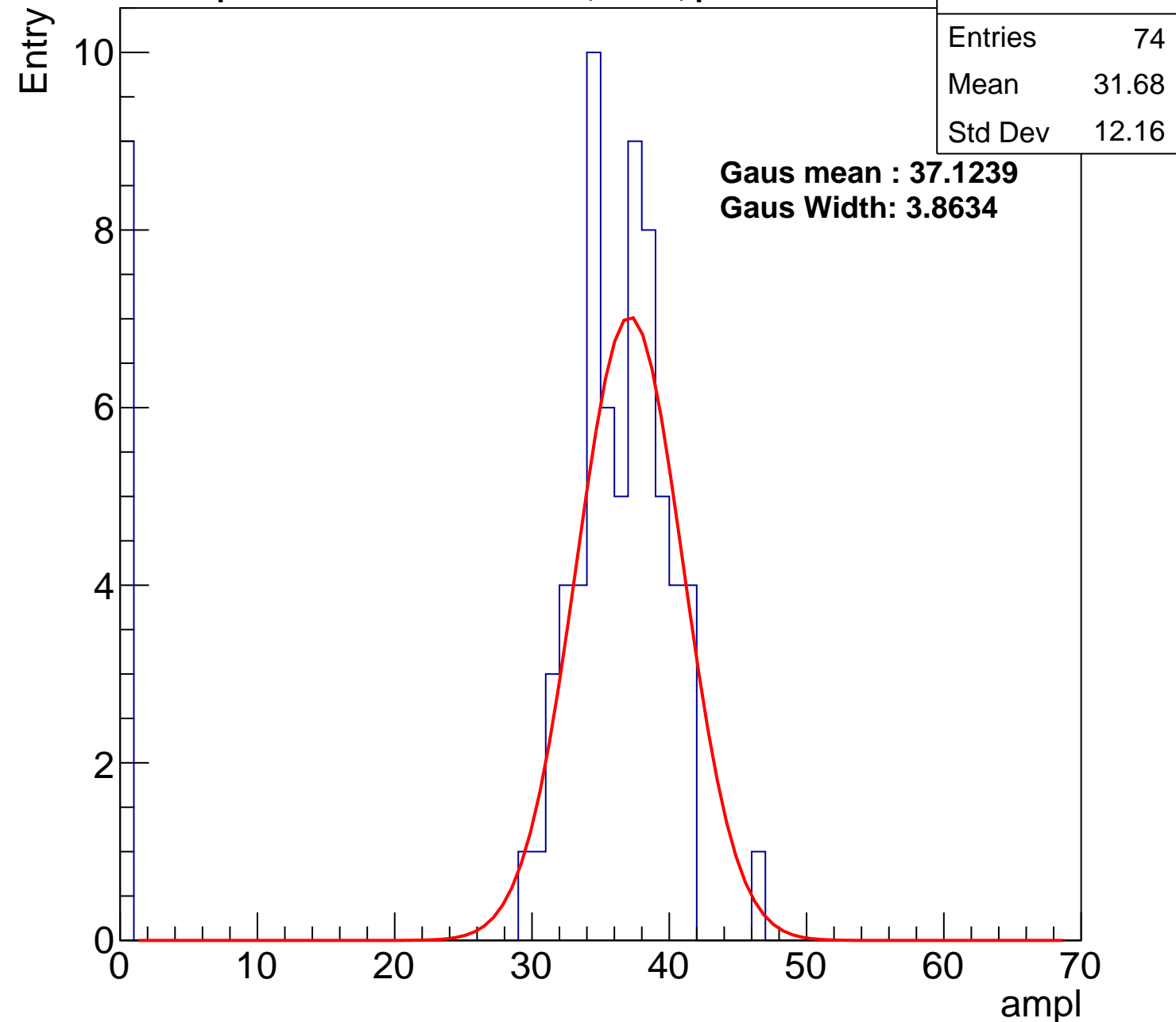
Gaus Width: 3.8634

Entry

10
8
6
4
2
0

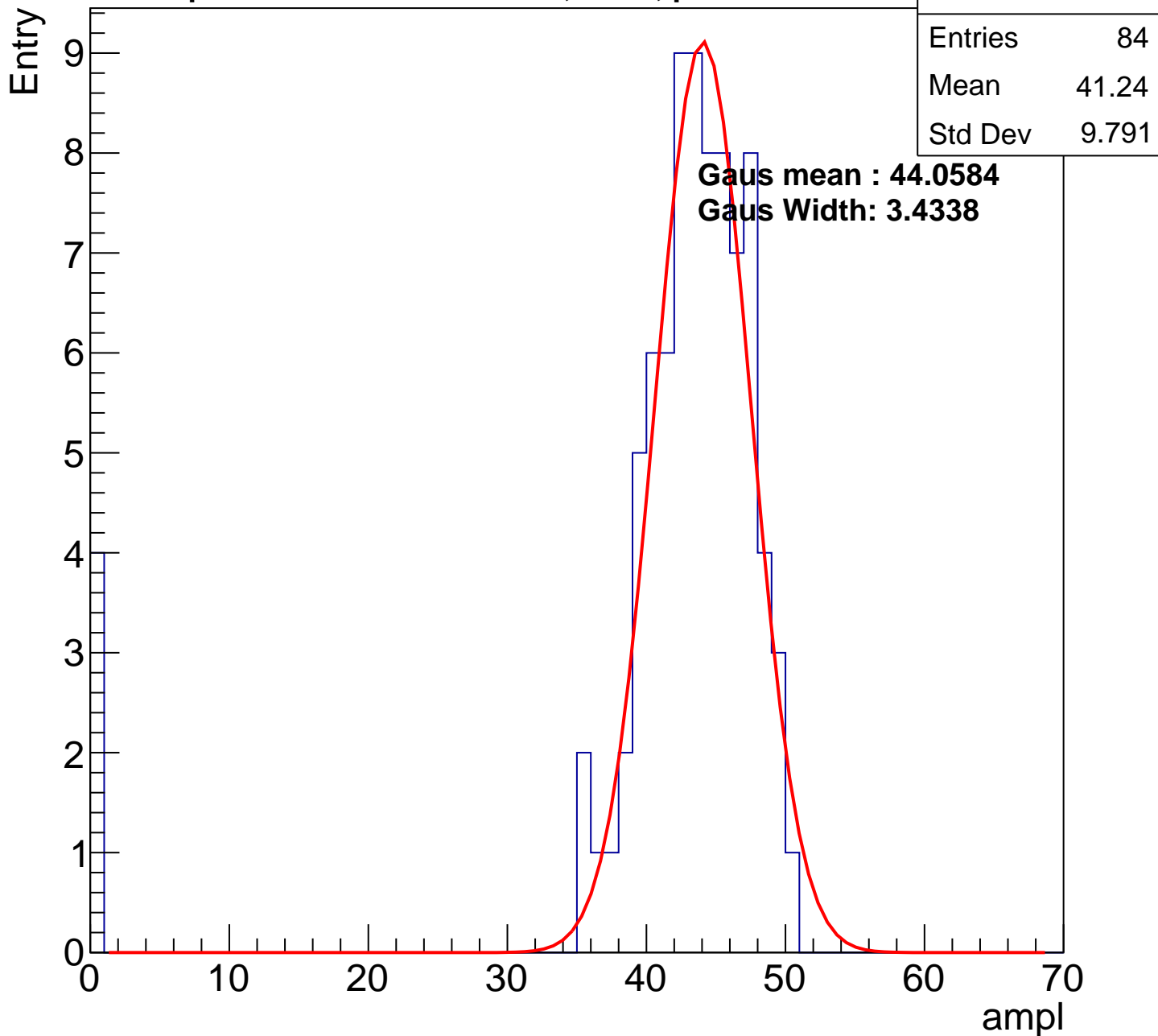
0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch73, adc2

calib_packv5_041523_1651.root, FC#0, port C2

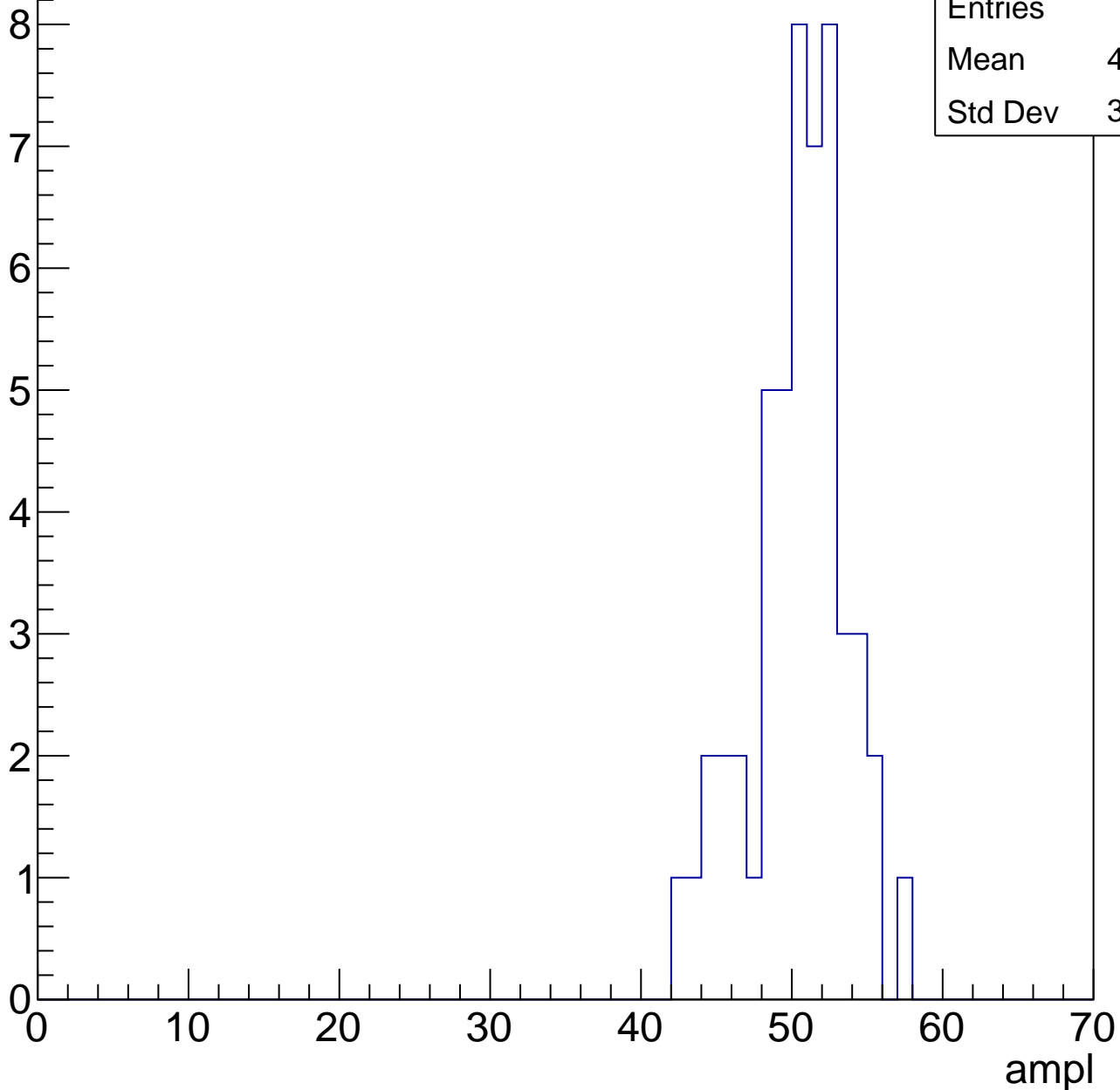


B1L103S, U8-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	49.96
Std Dev	3.174

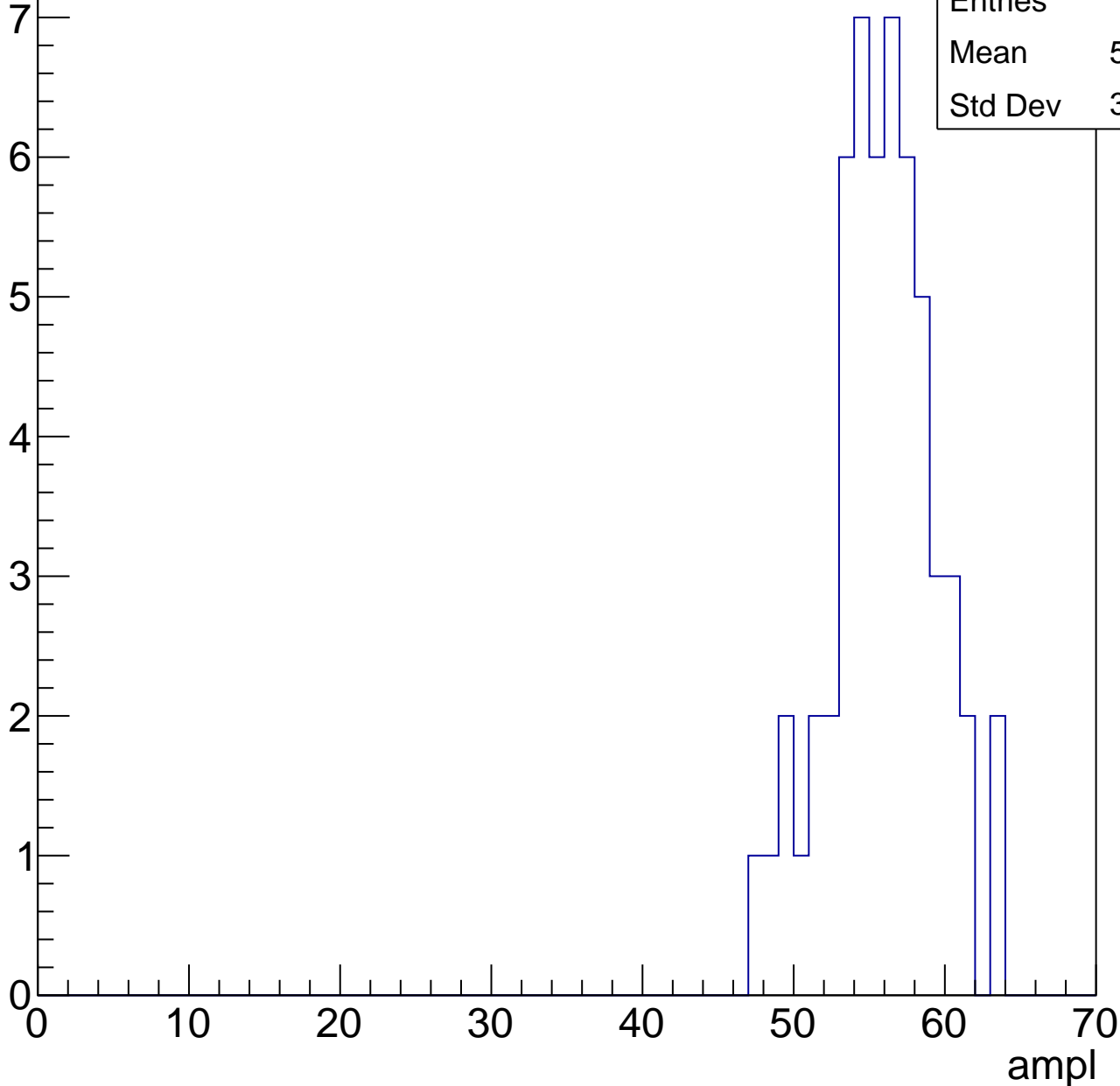


B1L103S, U8-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	55.43
Std Dev	3.484

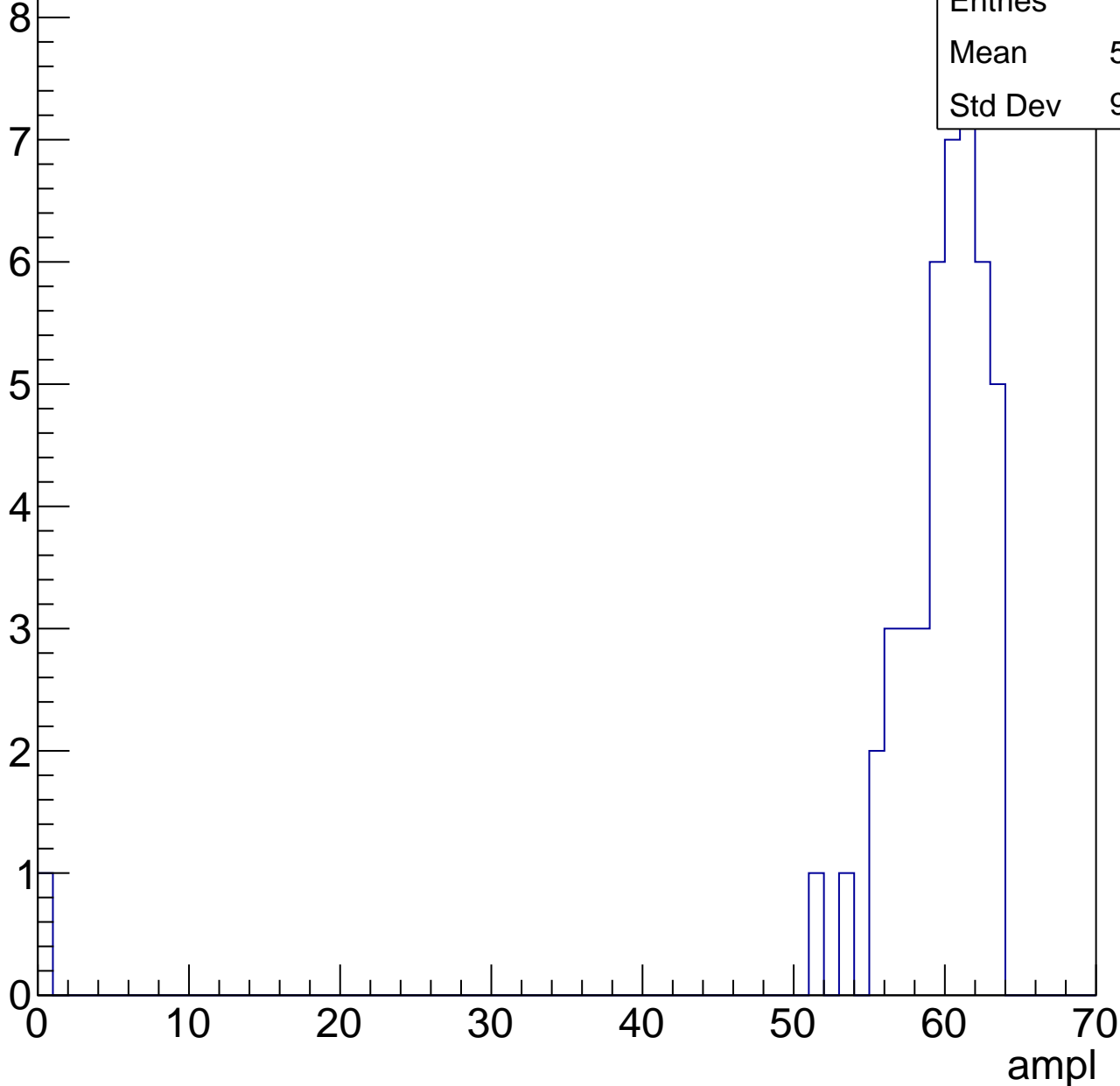


B1L103S, U8-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

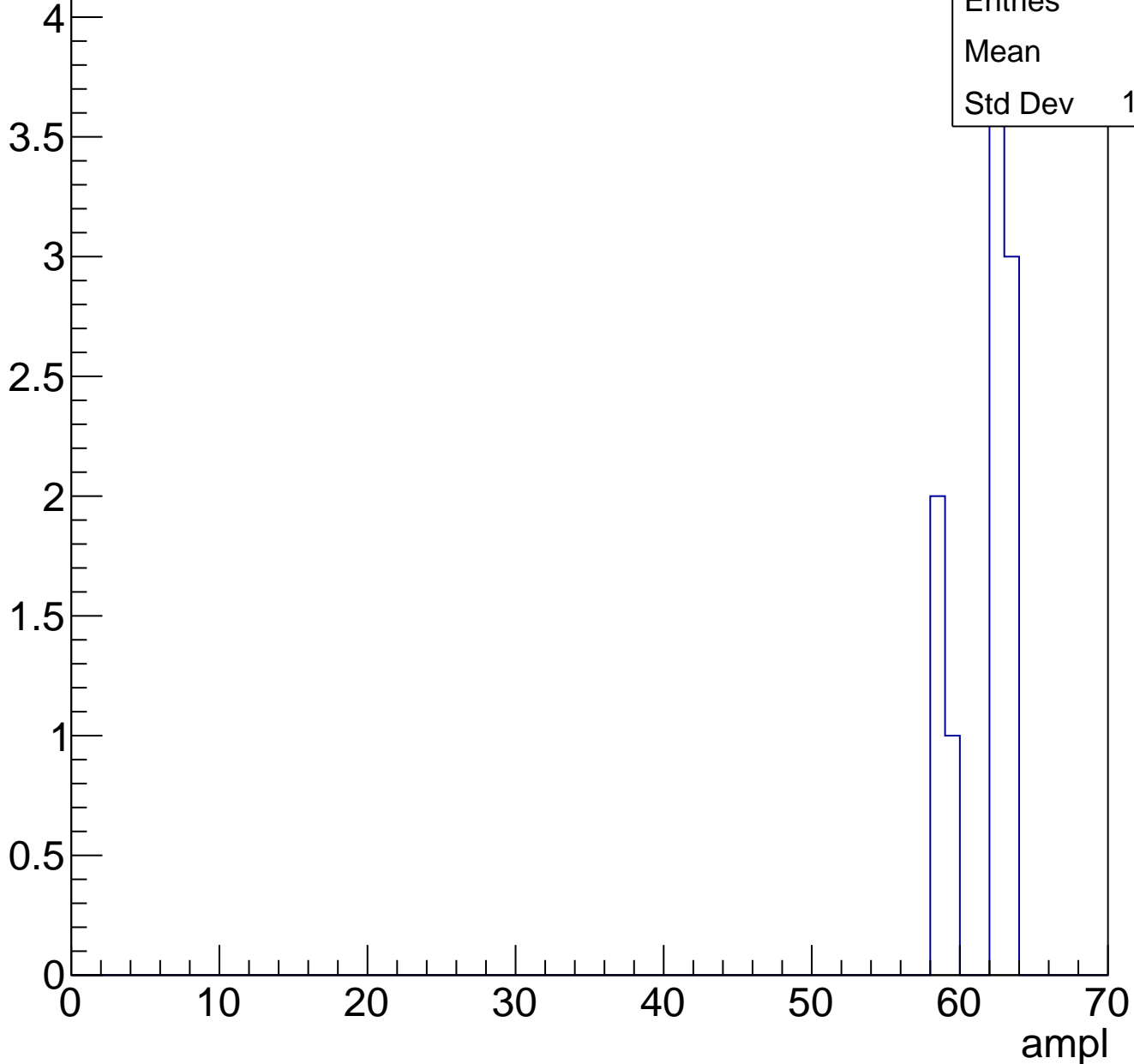
Entries	46
Mean	58.17
Std Dev	9.085



B1L103S, U8-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

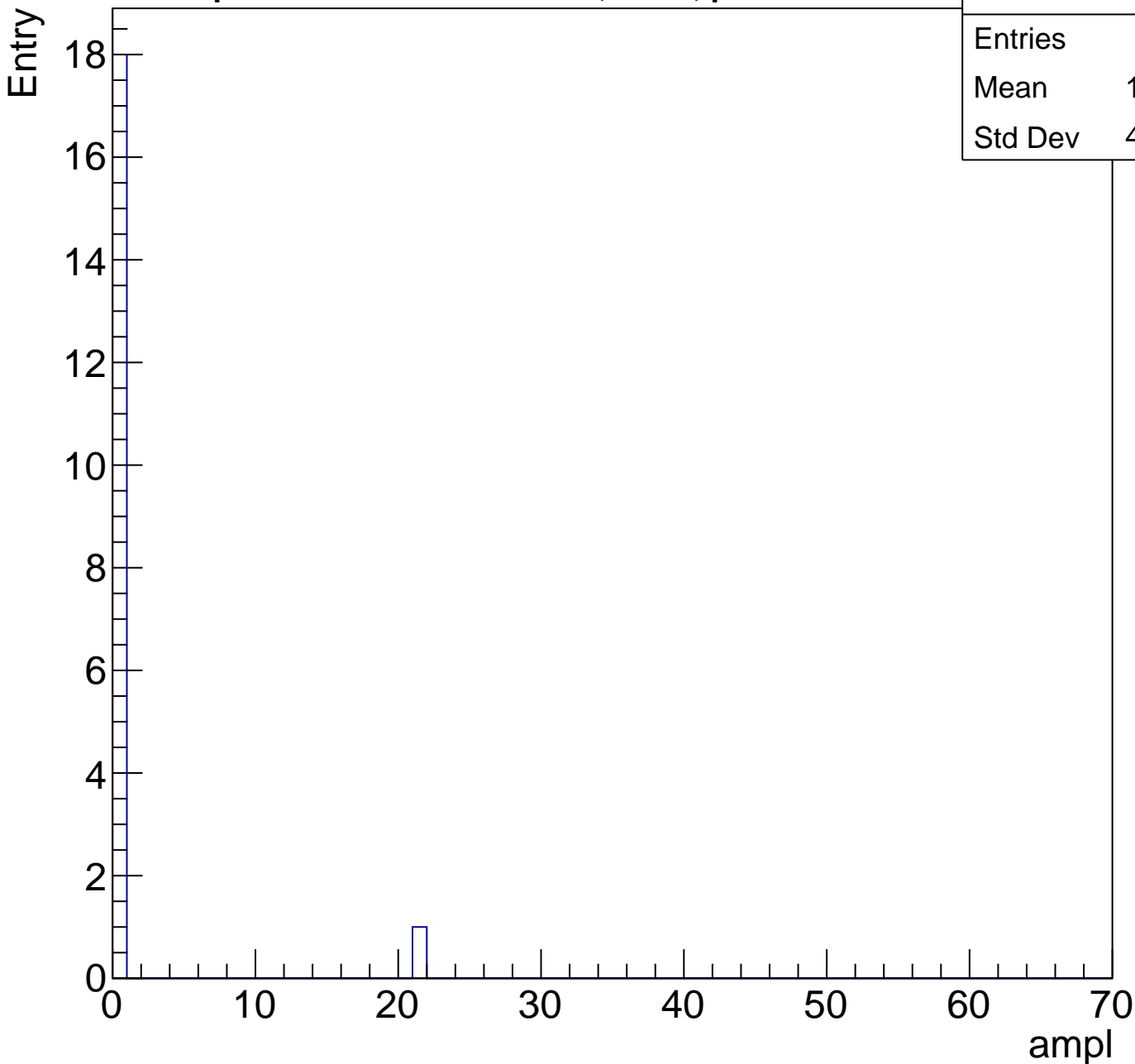
Entry



B1L103S, U8-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689



B1L103S, U8-ch74, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	27.16
Std Dev	8.061

Gaus mean : 29.7781

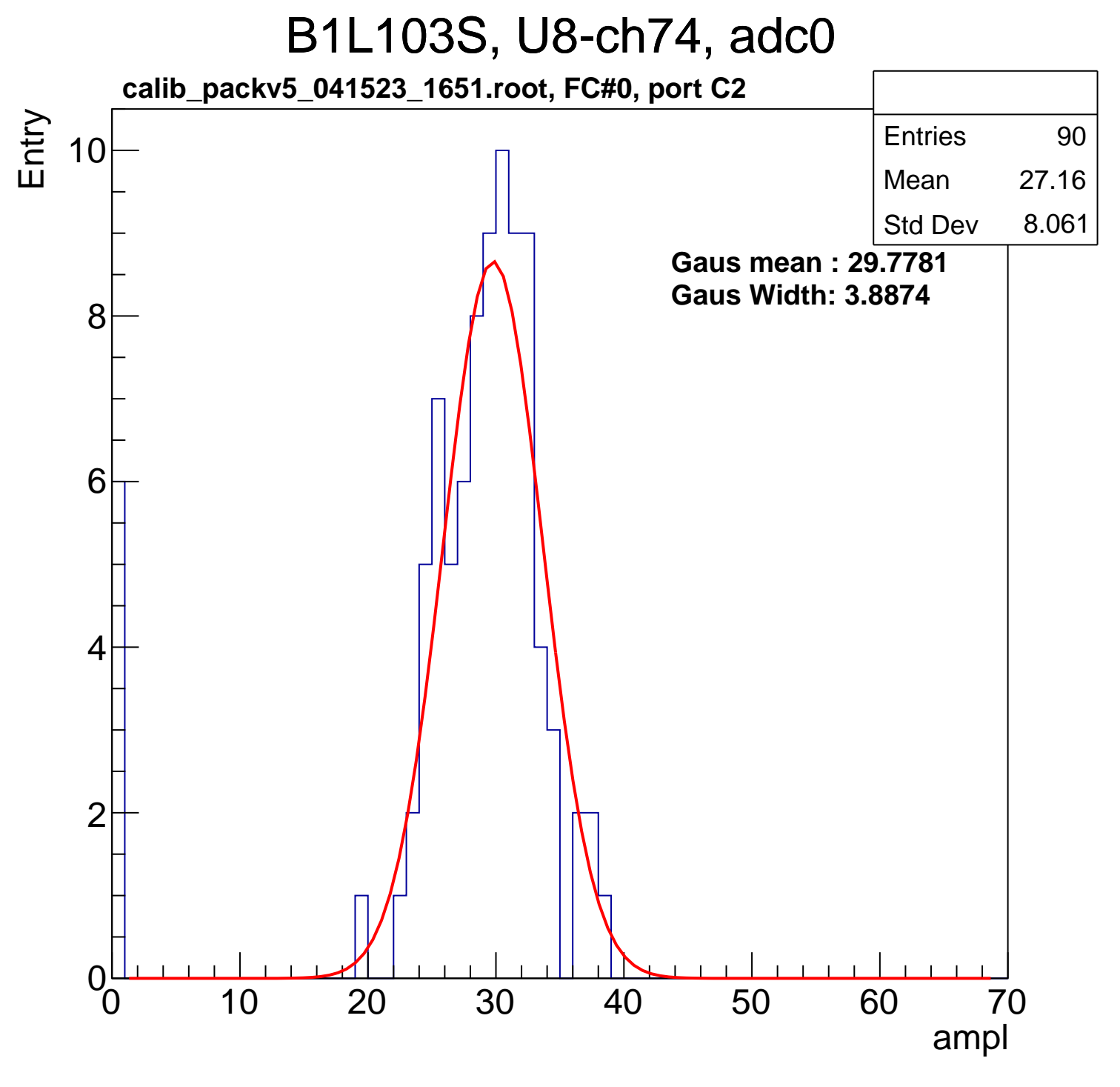
Gaus Width: 3.8874

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch74, adc1

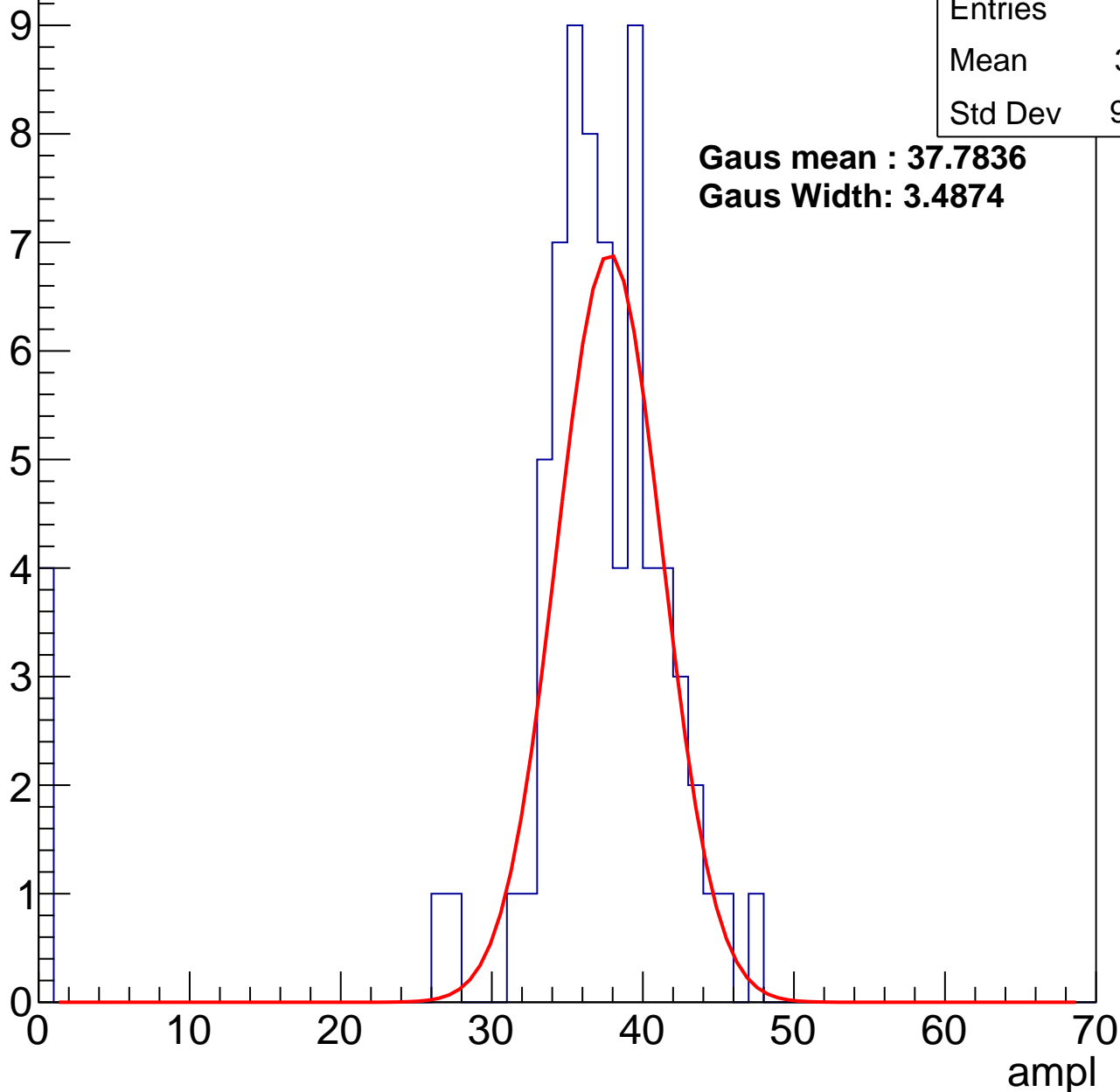
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.01
Std Dev	9.184

Gaus mean : 37.7836

Gaus Width: 3.4874



B1L103S, U8-ch74, adc2

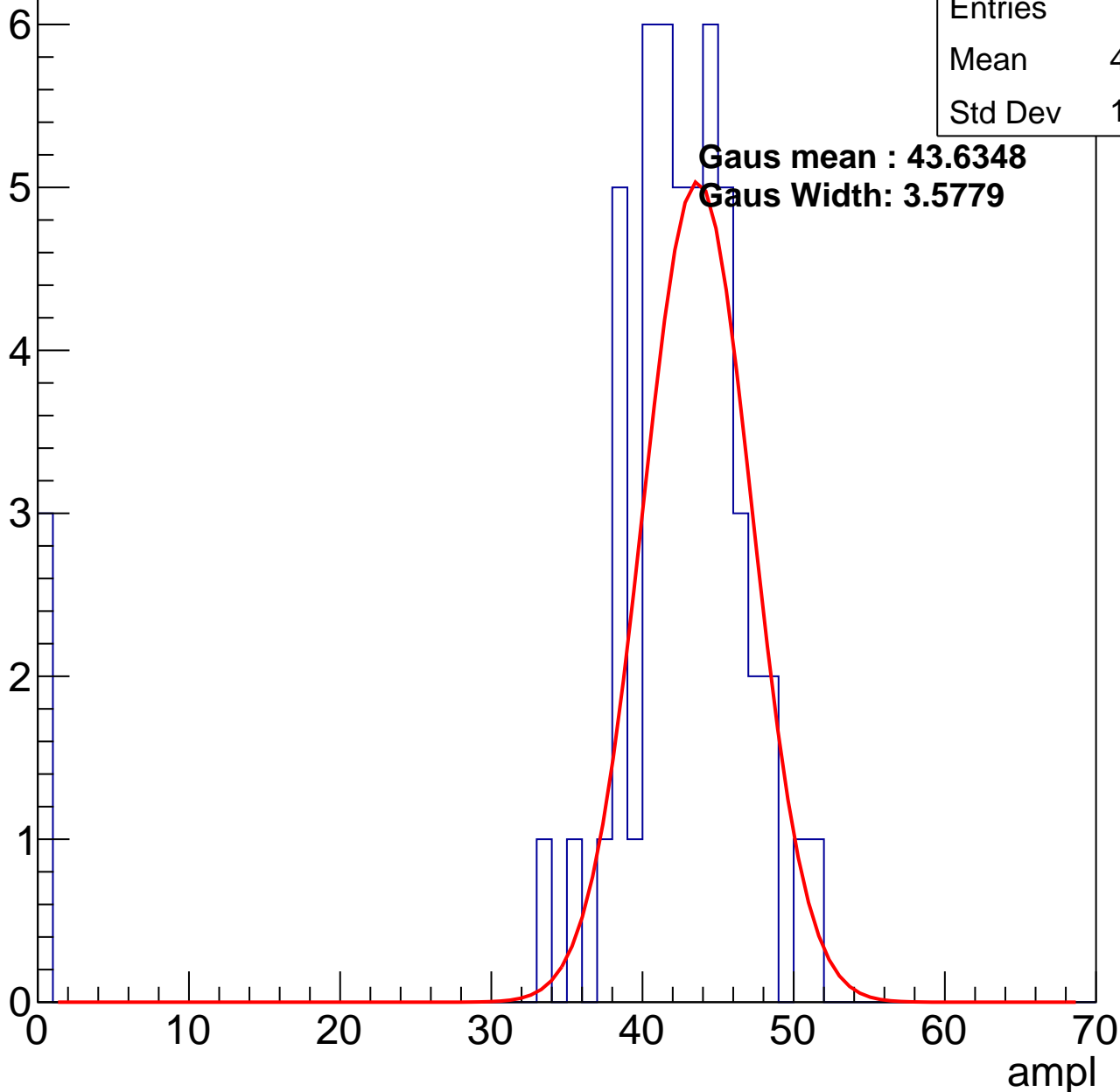
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	40.06
Std Dev	10.32

Gaus mean : 43.6348

Gaus Width: 3.5779

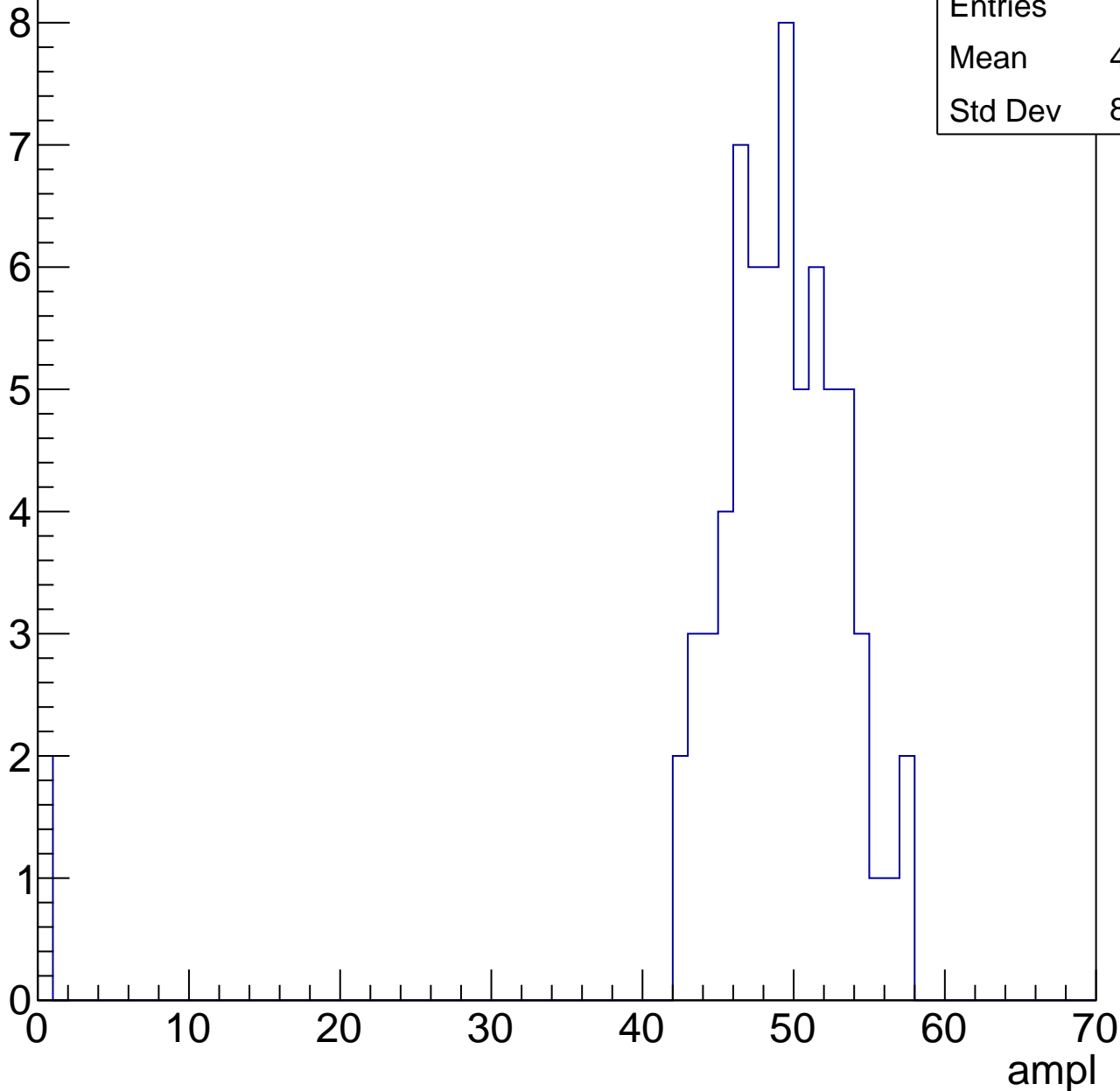


B1L103S, U8-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	47.49
Std Dev	8.949

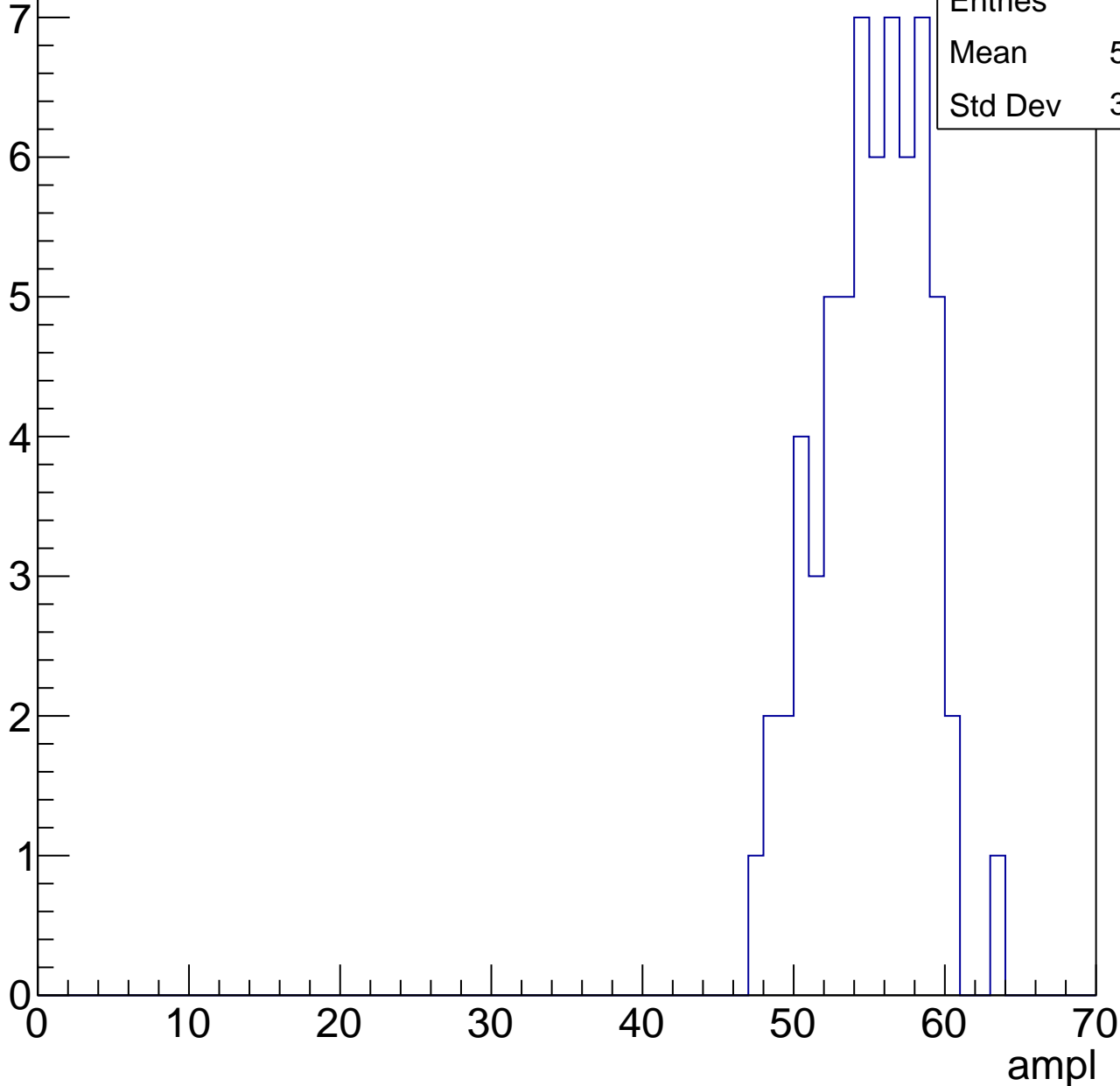


B1L103S, U8-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	54.68
Std Dev	3.417

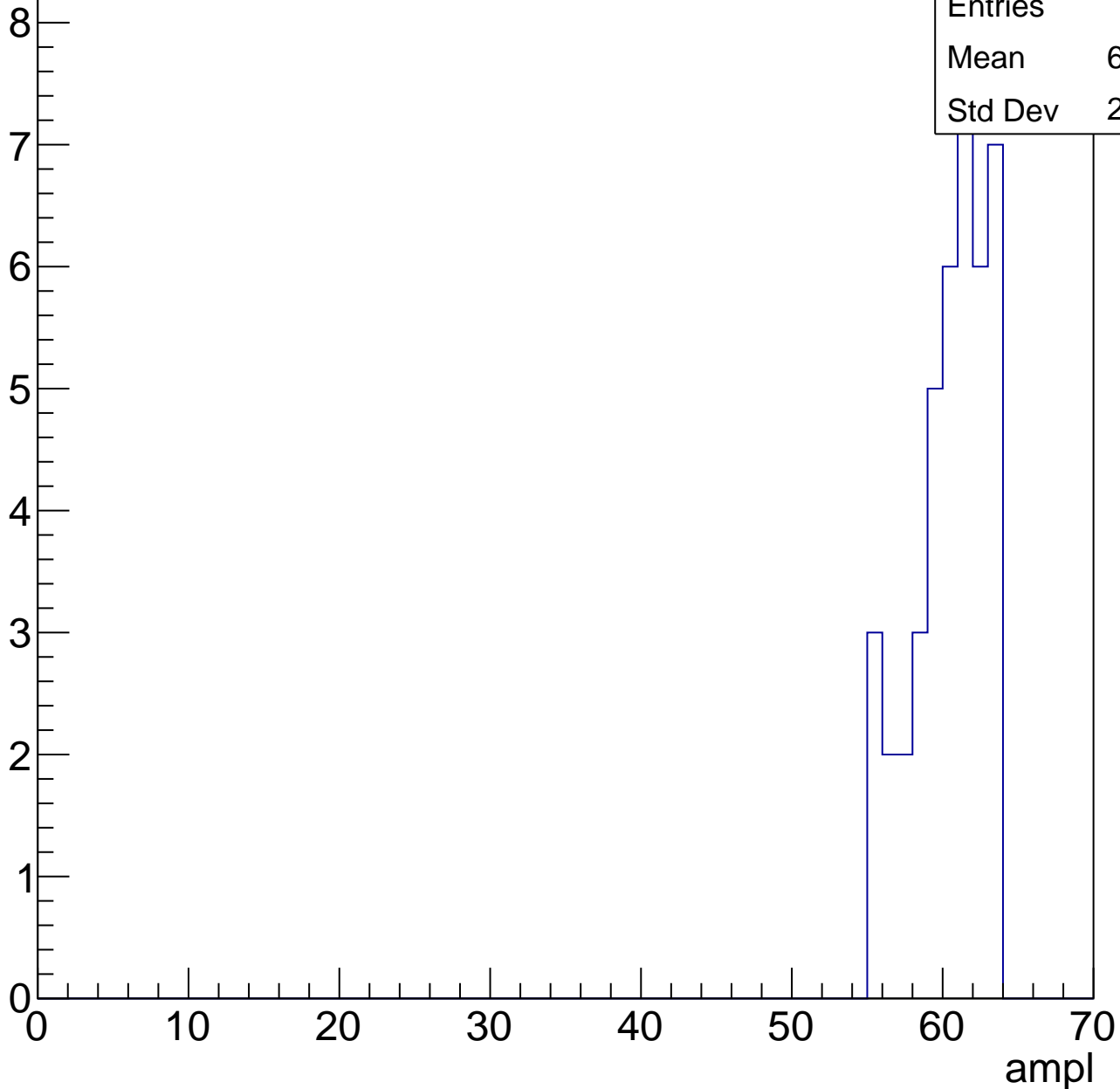


B1L103S, U8-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

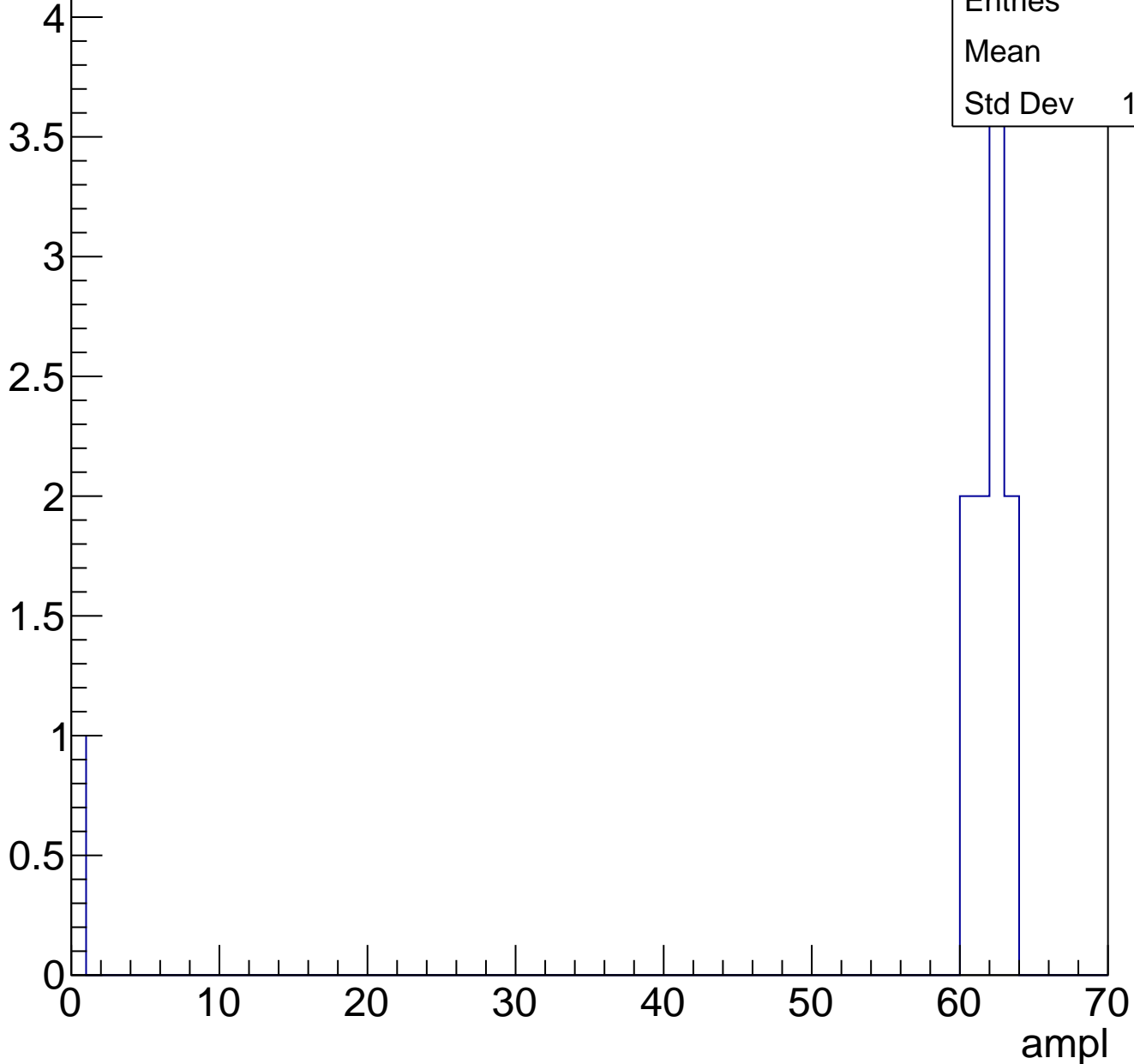
Entries	42
Mean	60.02
Std Dev	2.375



B1L103S, U8-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch75, adc0

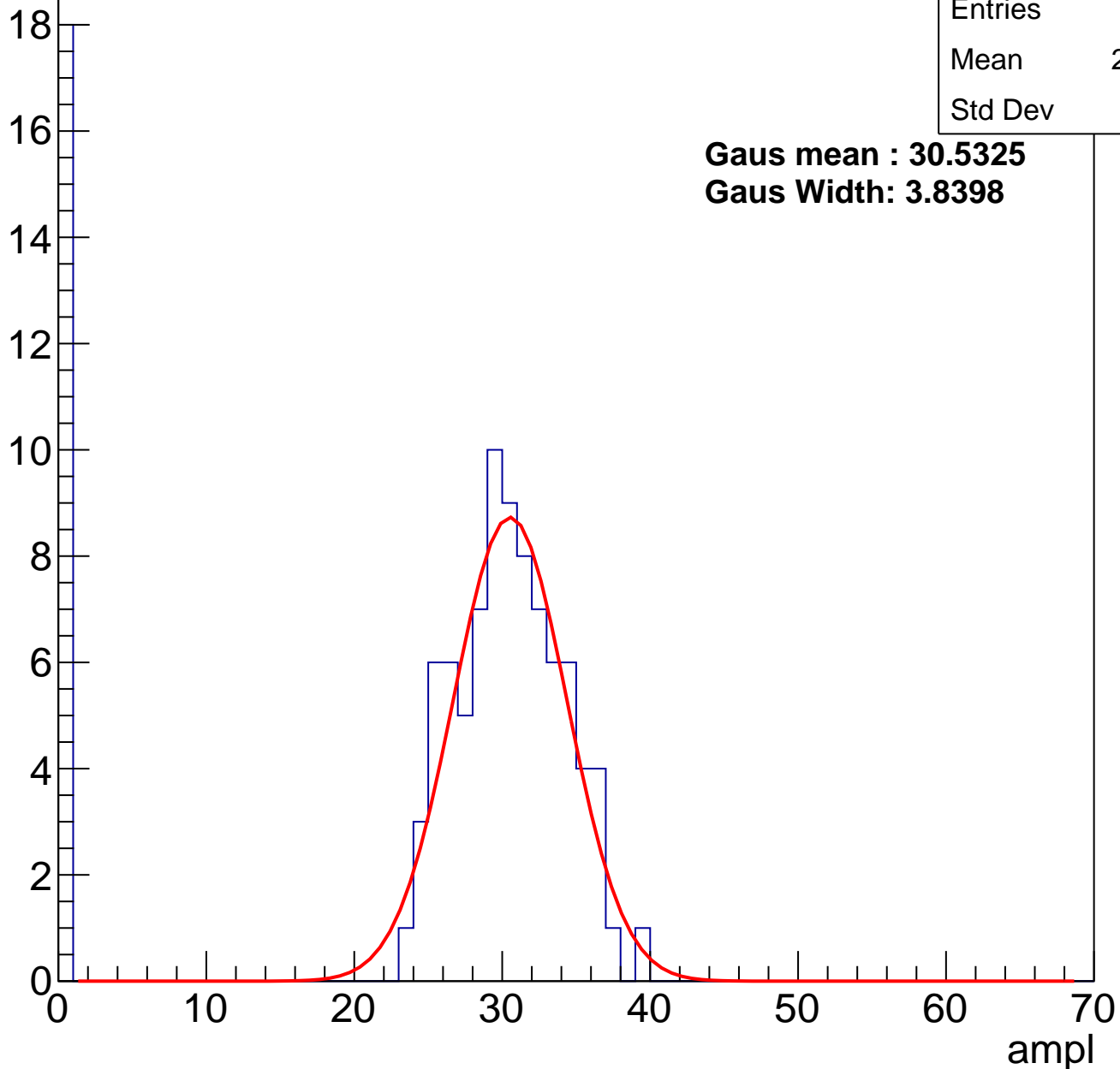
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	24.76
Std Dev	11.9

Gaus mean : 30.5325

Gaus Width: 3.8398

Entry



B1L103S, U8-ch75, adc1

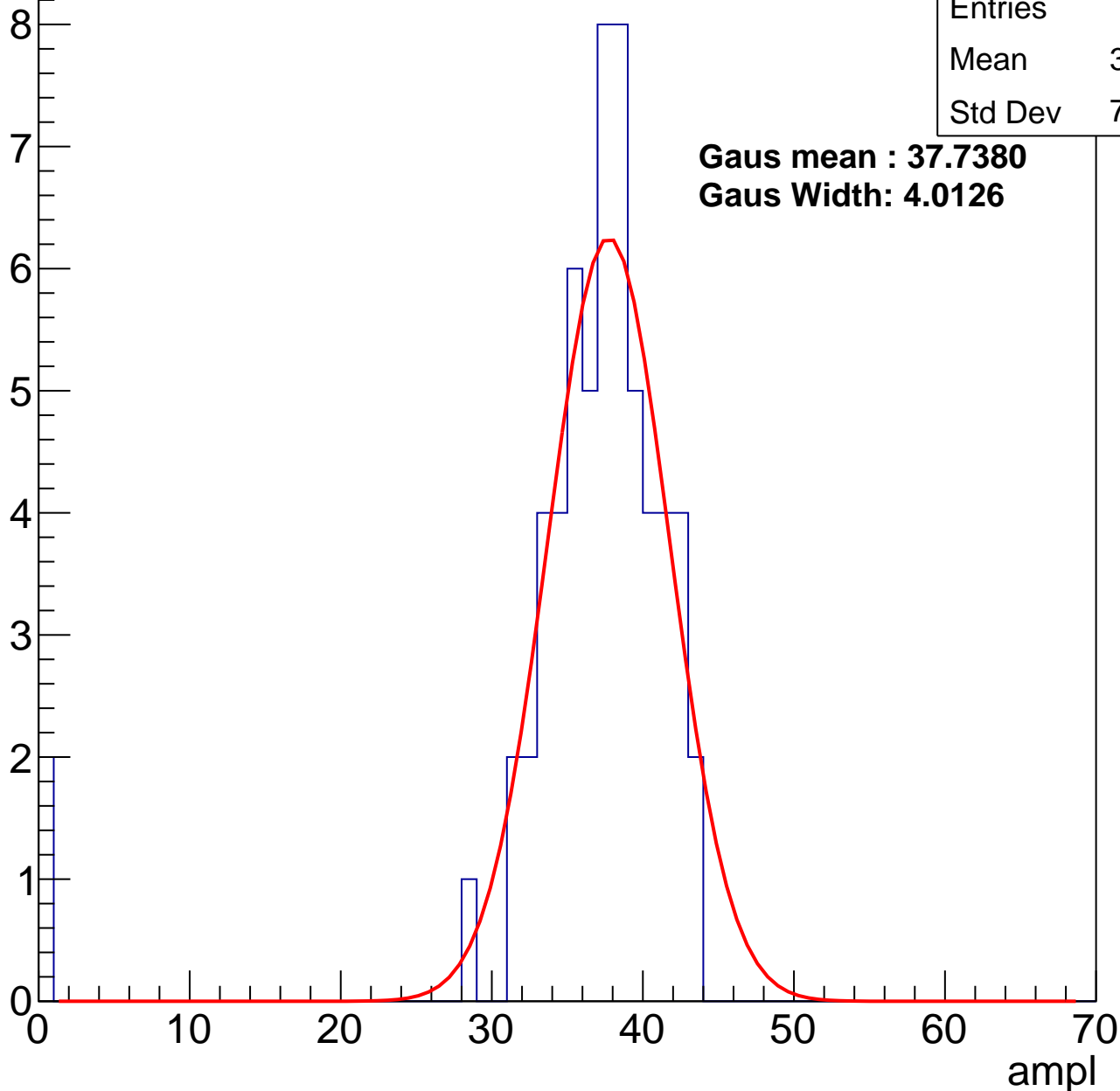
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	35.82
Std Dev	7.338

Gaus mean : 37.7380

Gaus Width: 4.0126



B1L103S, U8-ch75, adc2

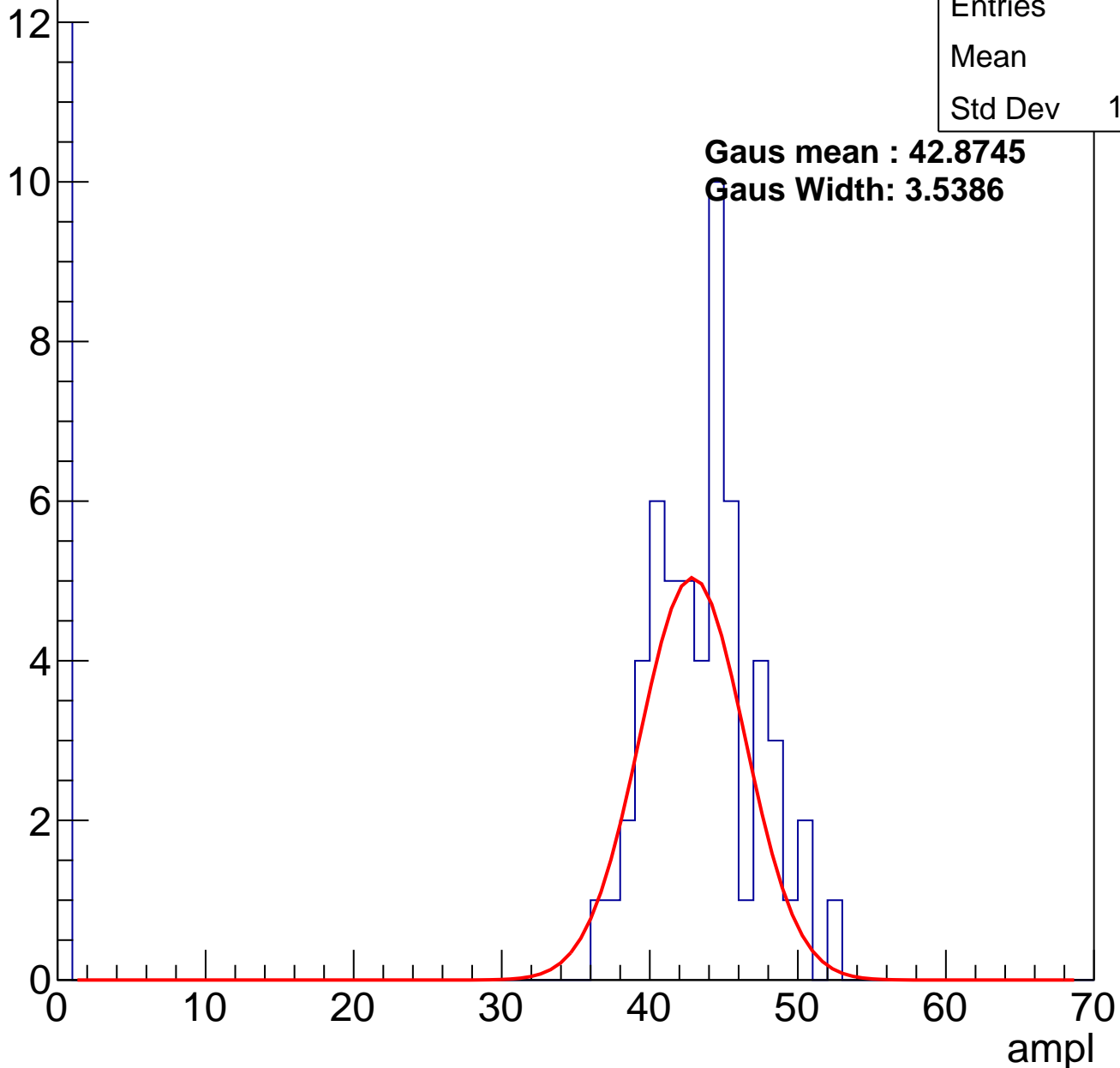
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	35.6
Std Dev	16.78

Gaus mean : 42.8745

Gaus Width: 3.5386

Entry

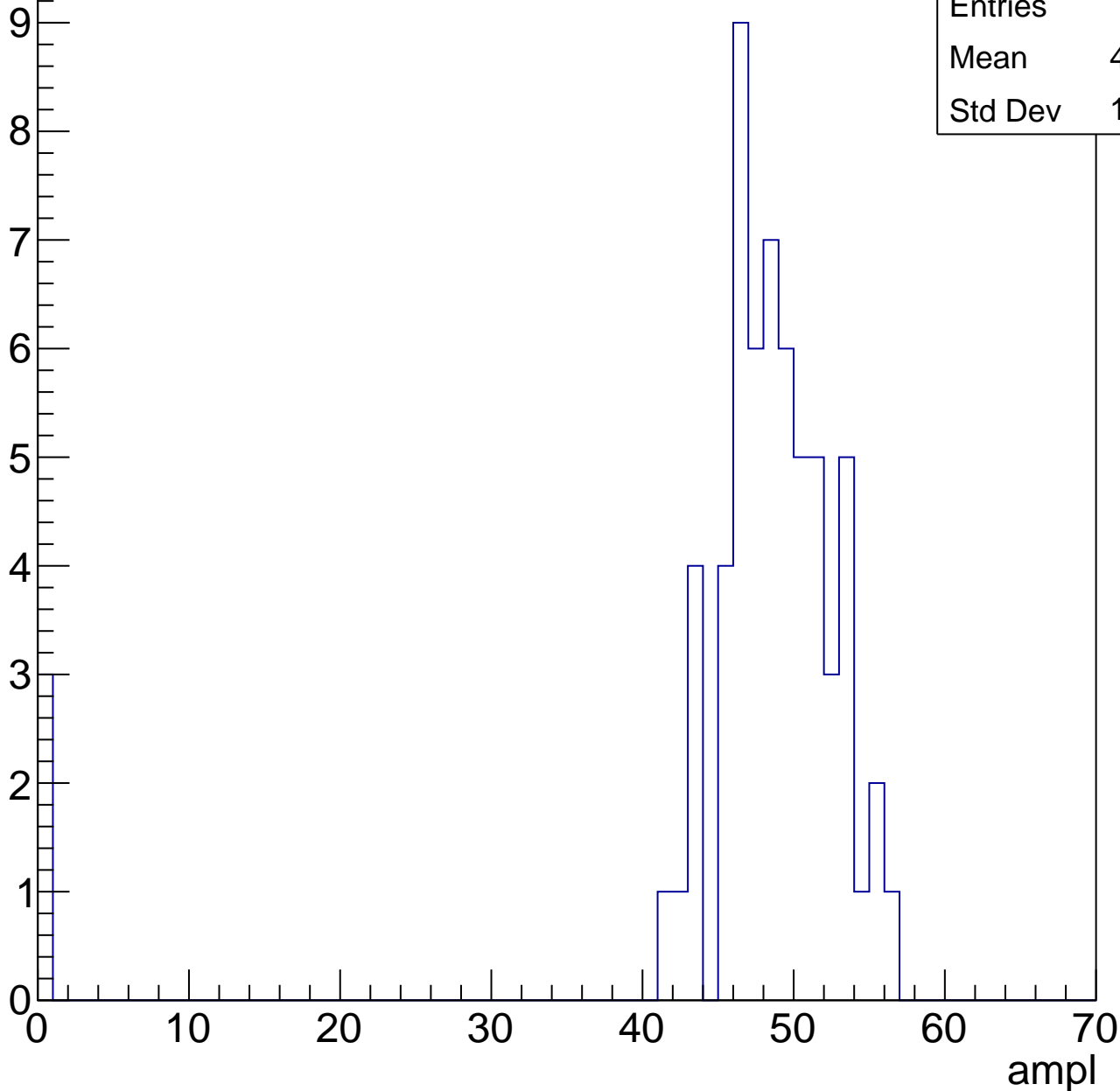


B1L103S, U8-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	46.14
Std Dev	10.84

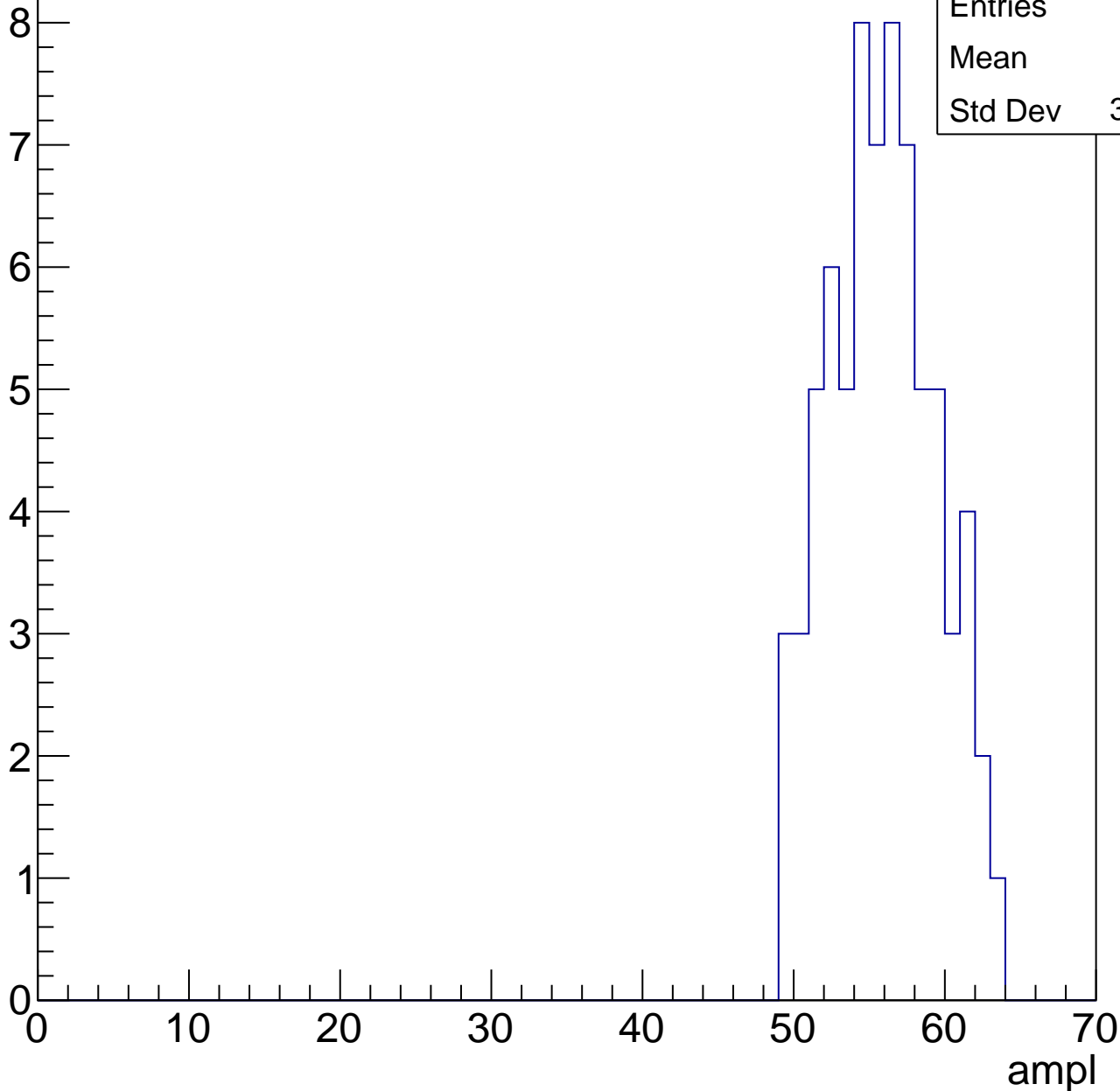


B1L103S, U8-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

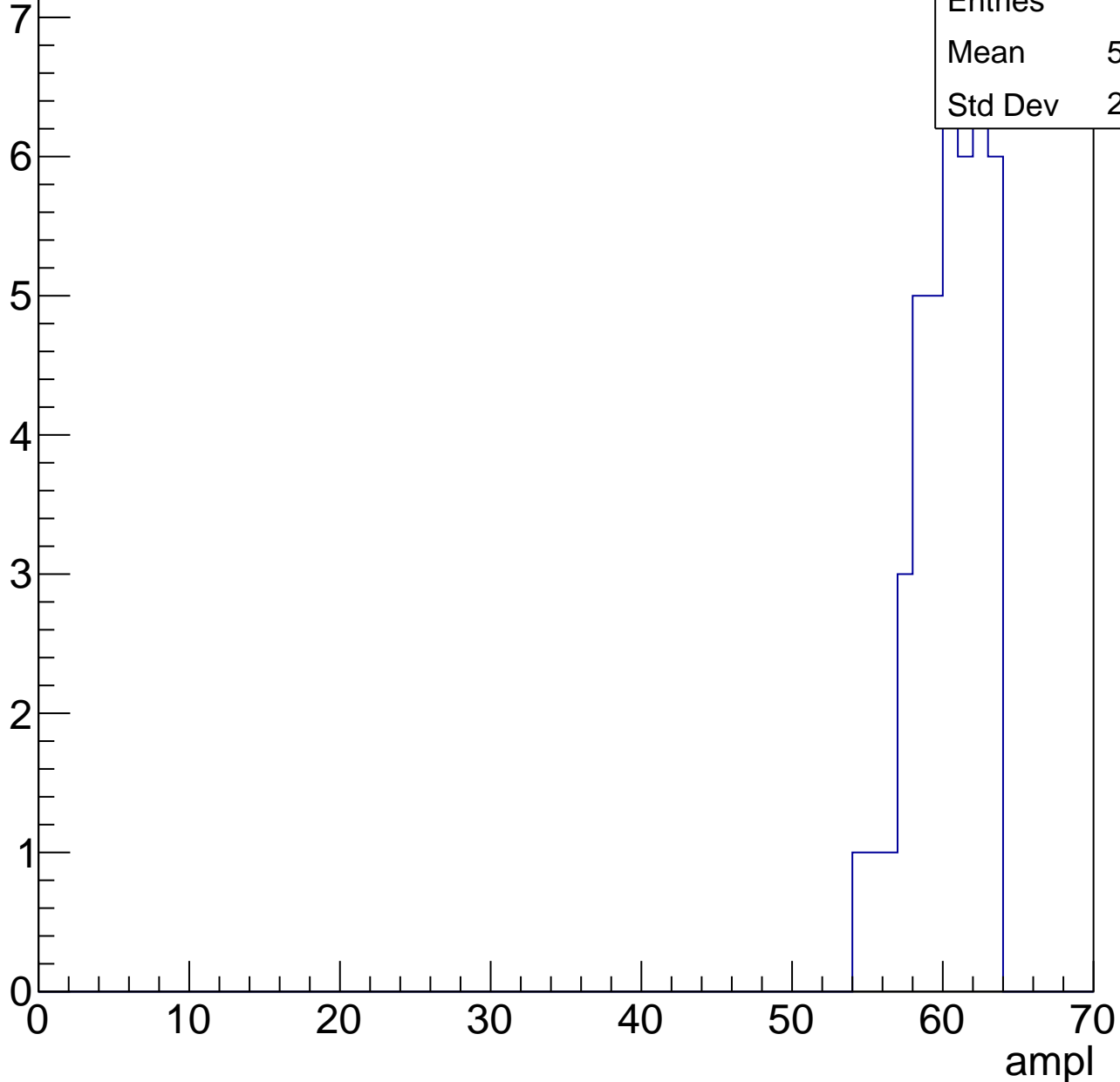
Entries	72
Mean	55.4
Std Dev	3.487



B1L103S, U8-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

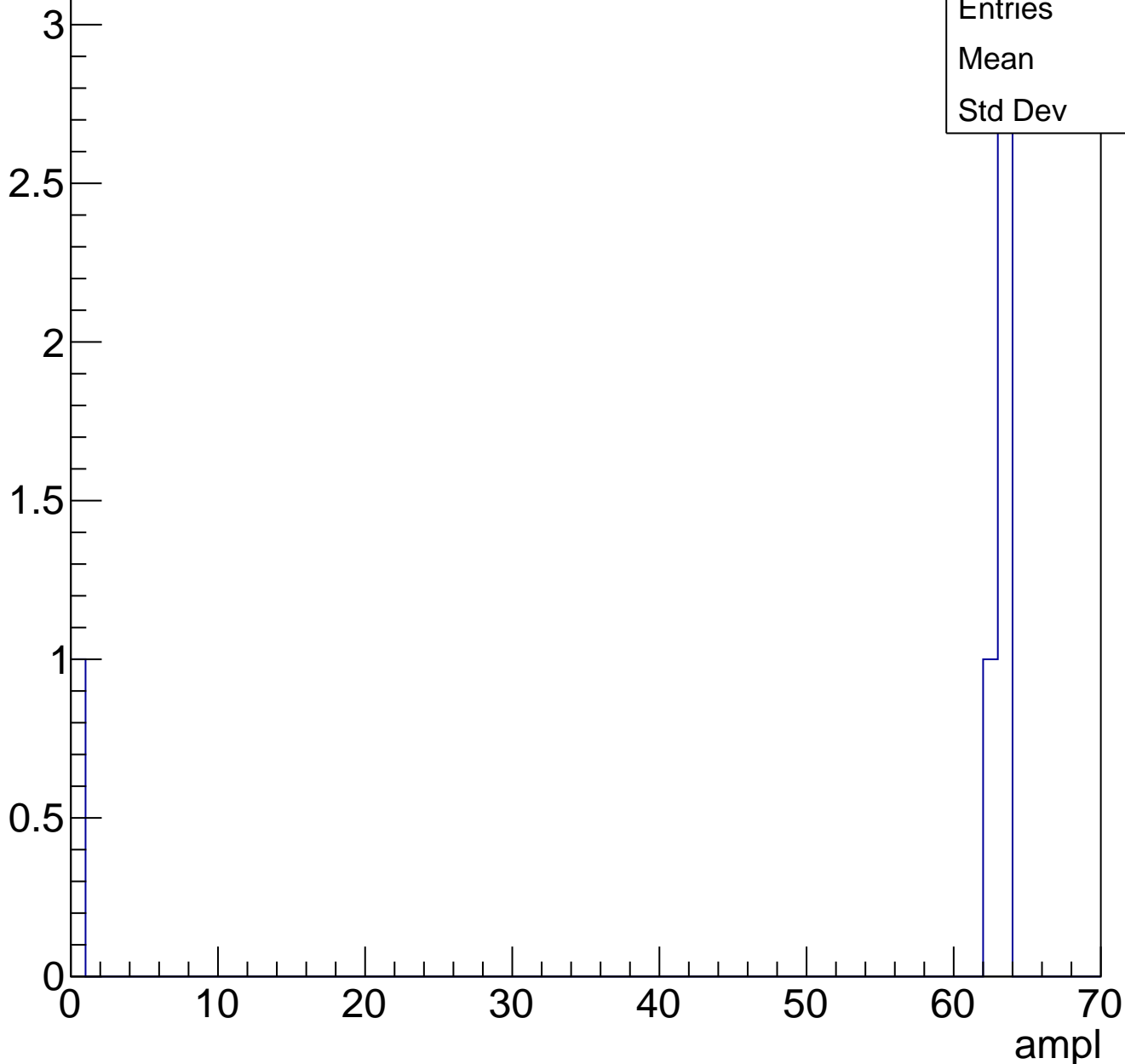
Entry



B1L103S, U8-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U8-ch76, adc0

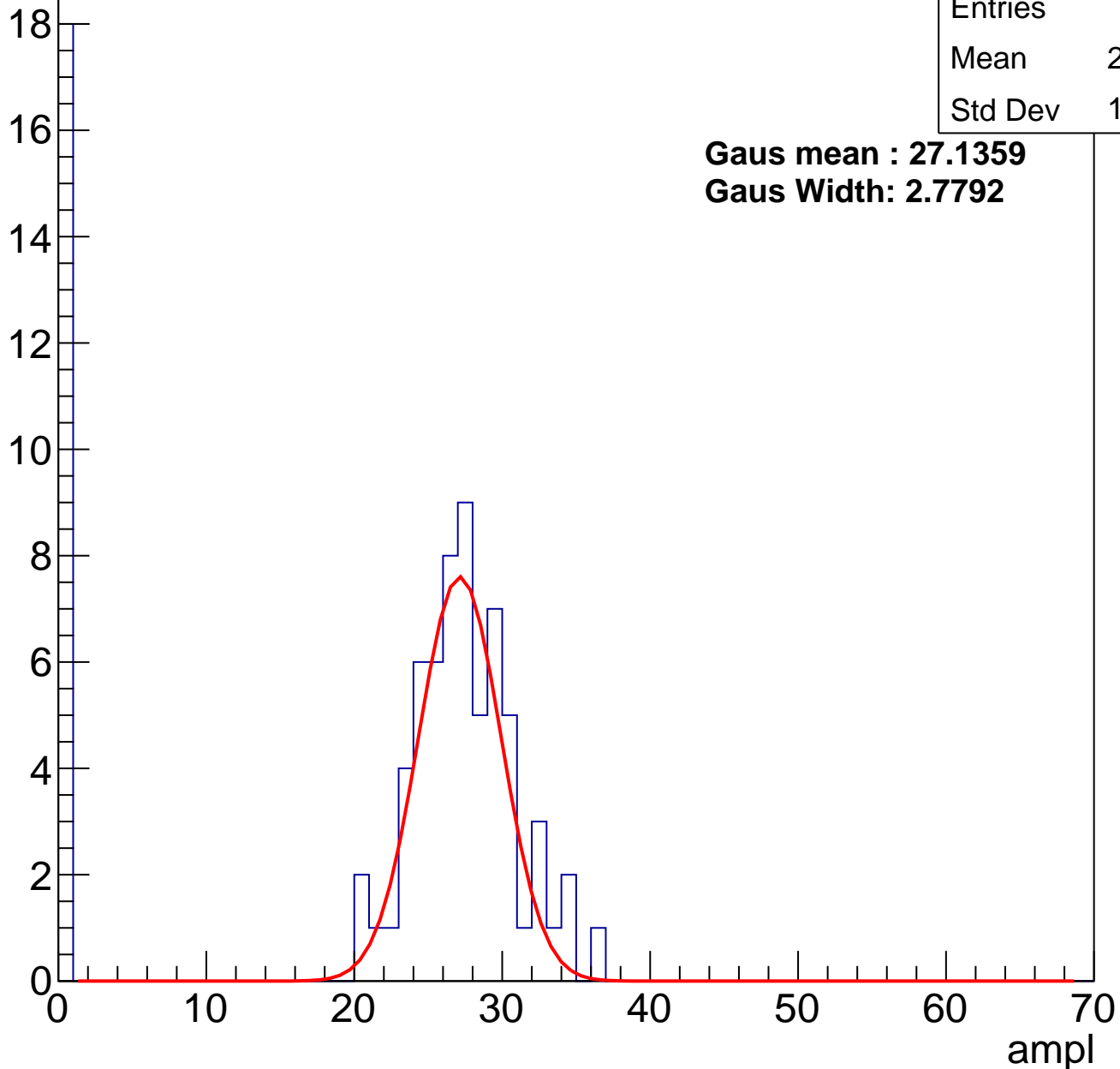
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	20.96
Std Dev	11.67

Gaus mean : 27.1359

Gaus Width: 2.7792

Entry



B1L103S, U8-ch76, adc1

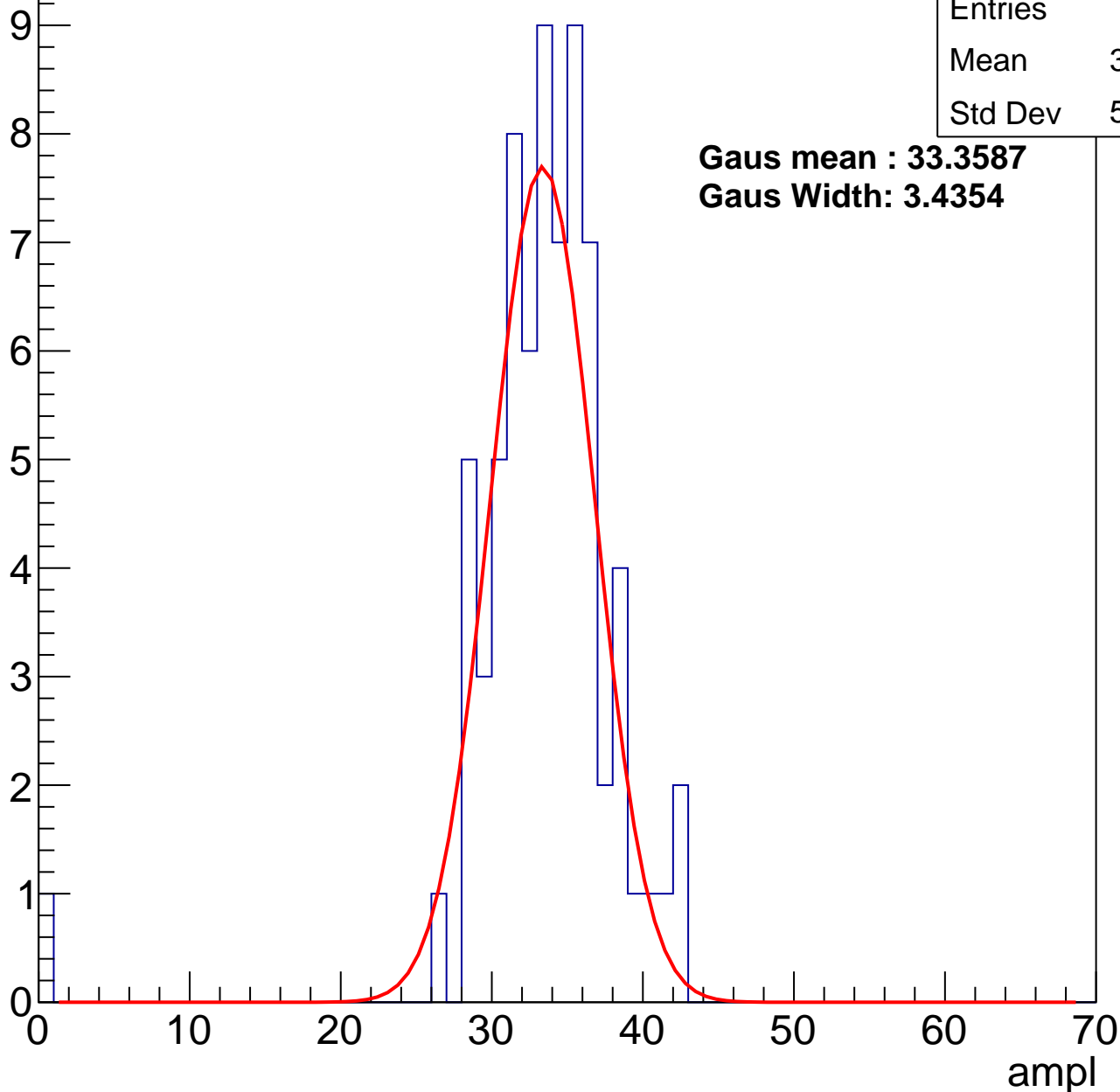
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	32.99
Std Dev	5.195

Gaus mean : 33.3587

Gaus Width: 3.4354



B1L103S, U8-ch76, adc2

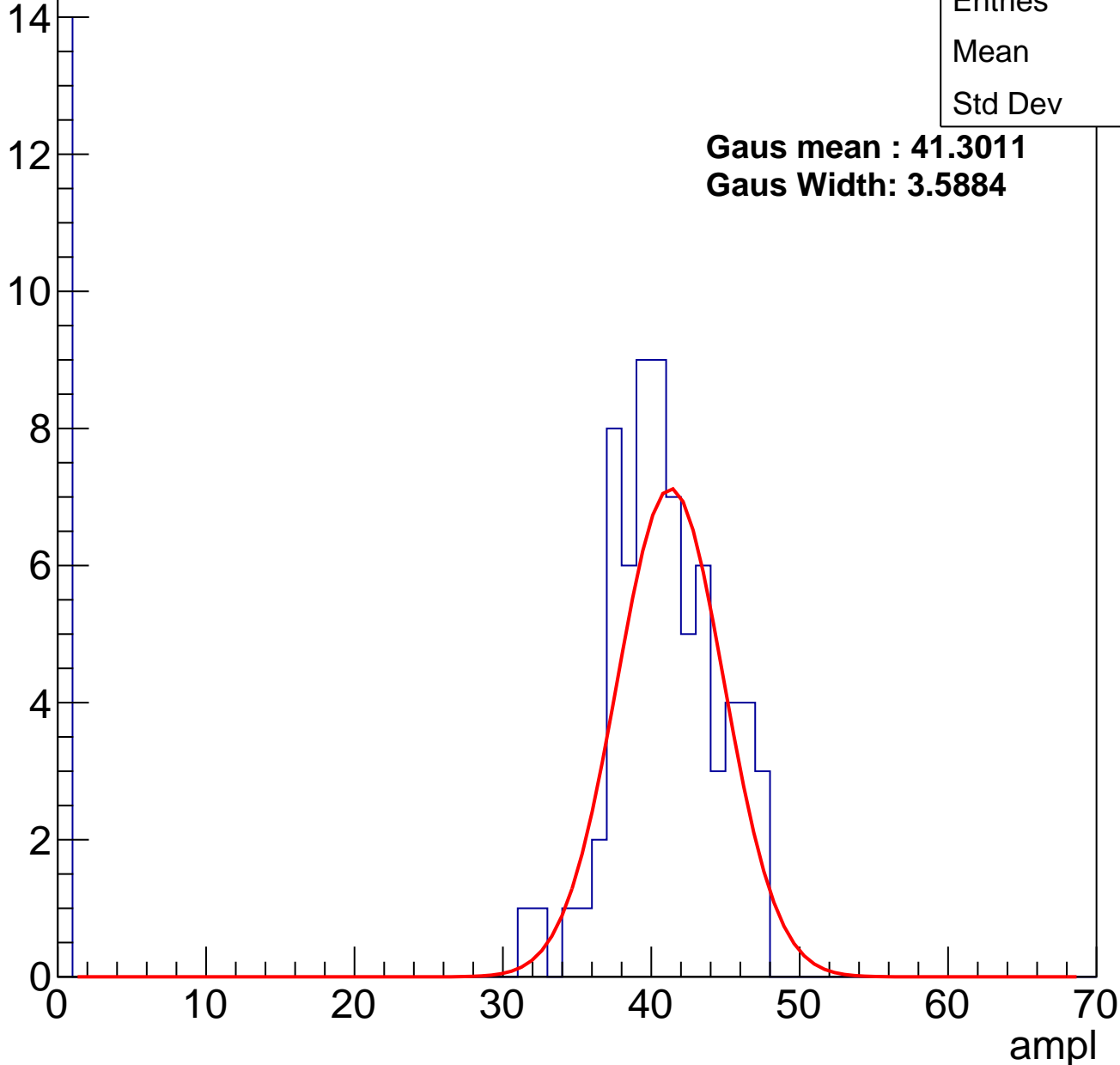
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	33.7
Std Dev	15.4

Gaus mean : 41.3011

Gaus Width: 3.5884

Entry

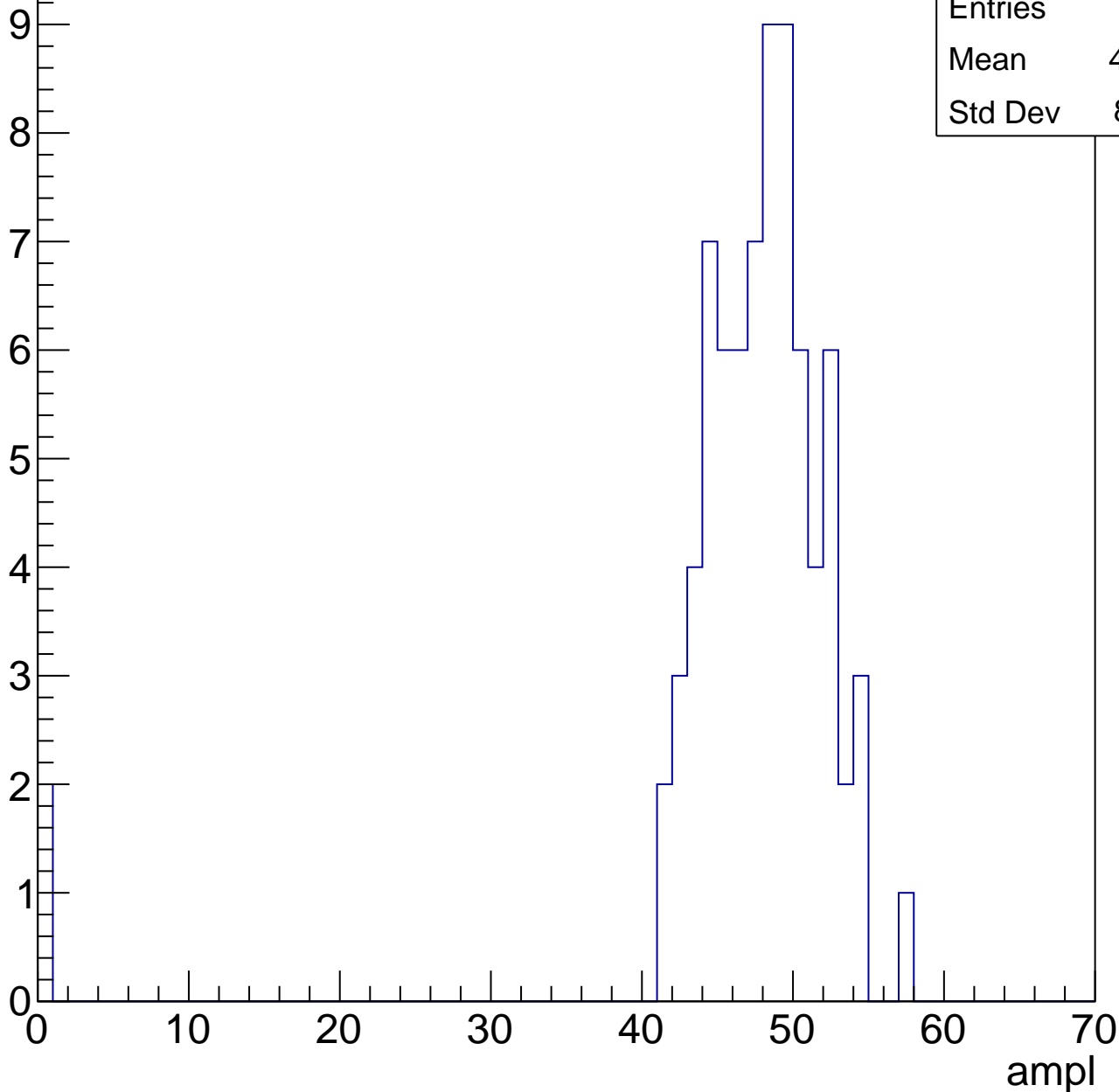


B1L103S, U8-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	46.45
Std Dev	8.321

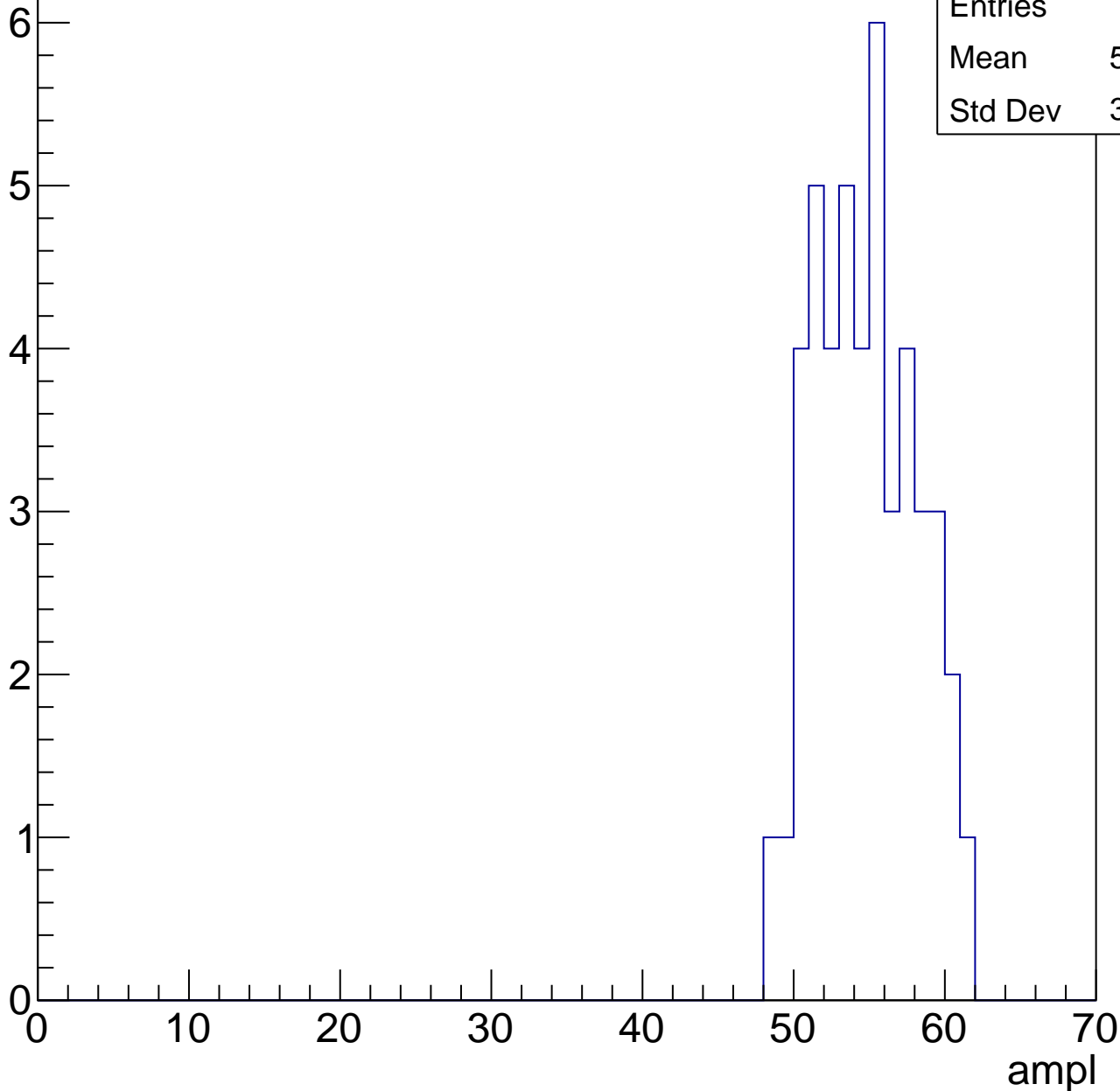


B1L103S, U8-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	54.33
Std Dev	3.244

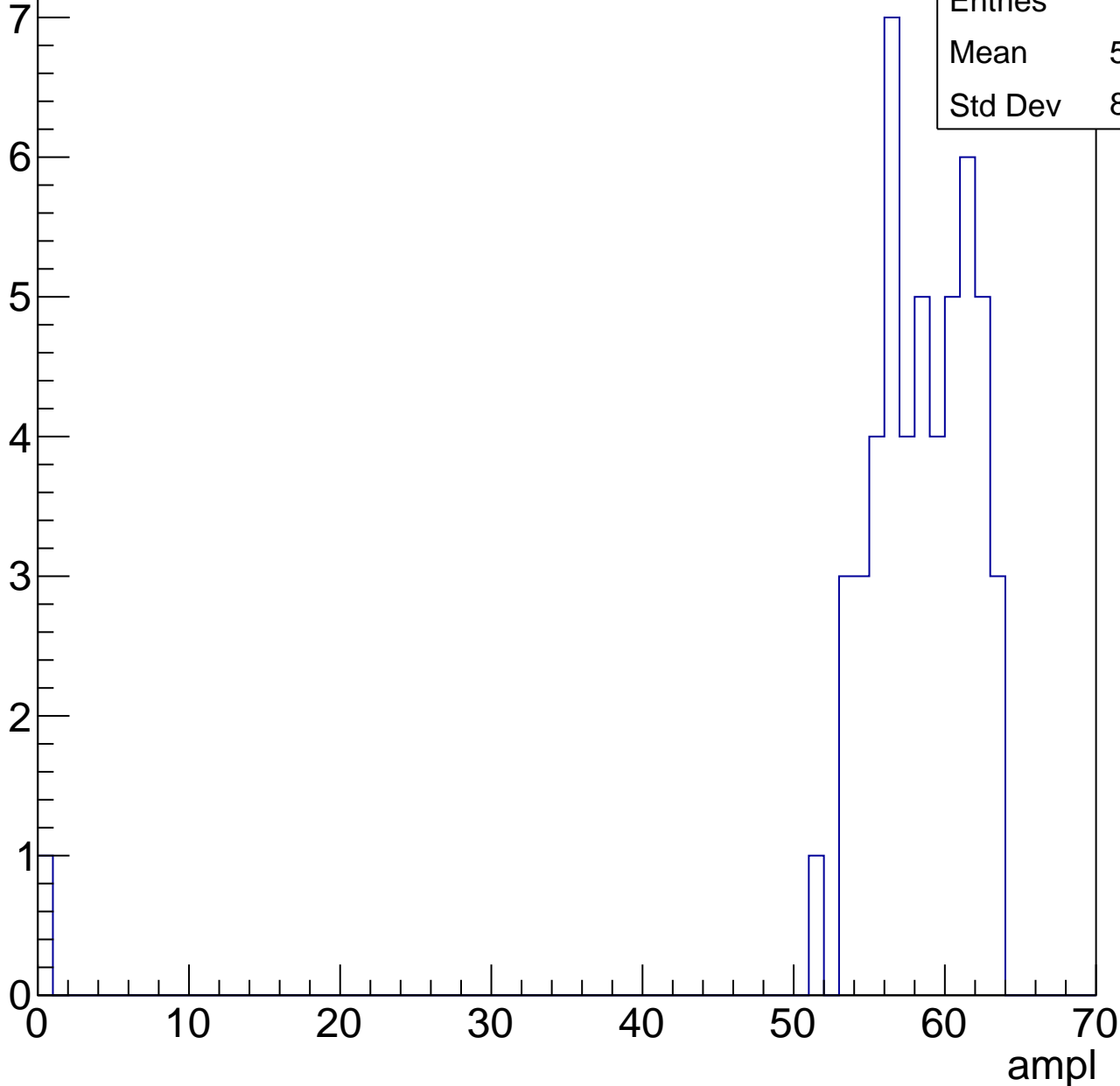


B1L103S, U8-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	56.92
Std Dev	8.607

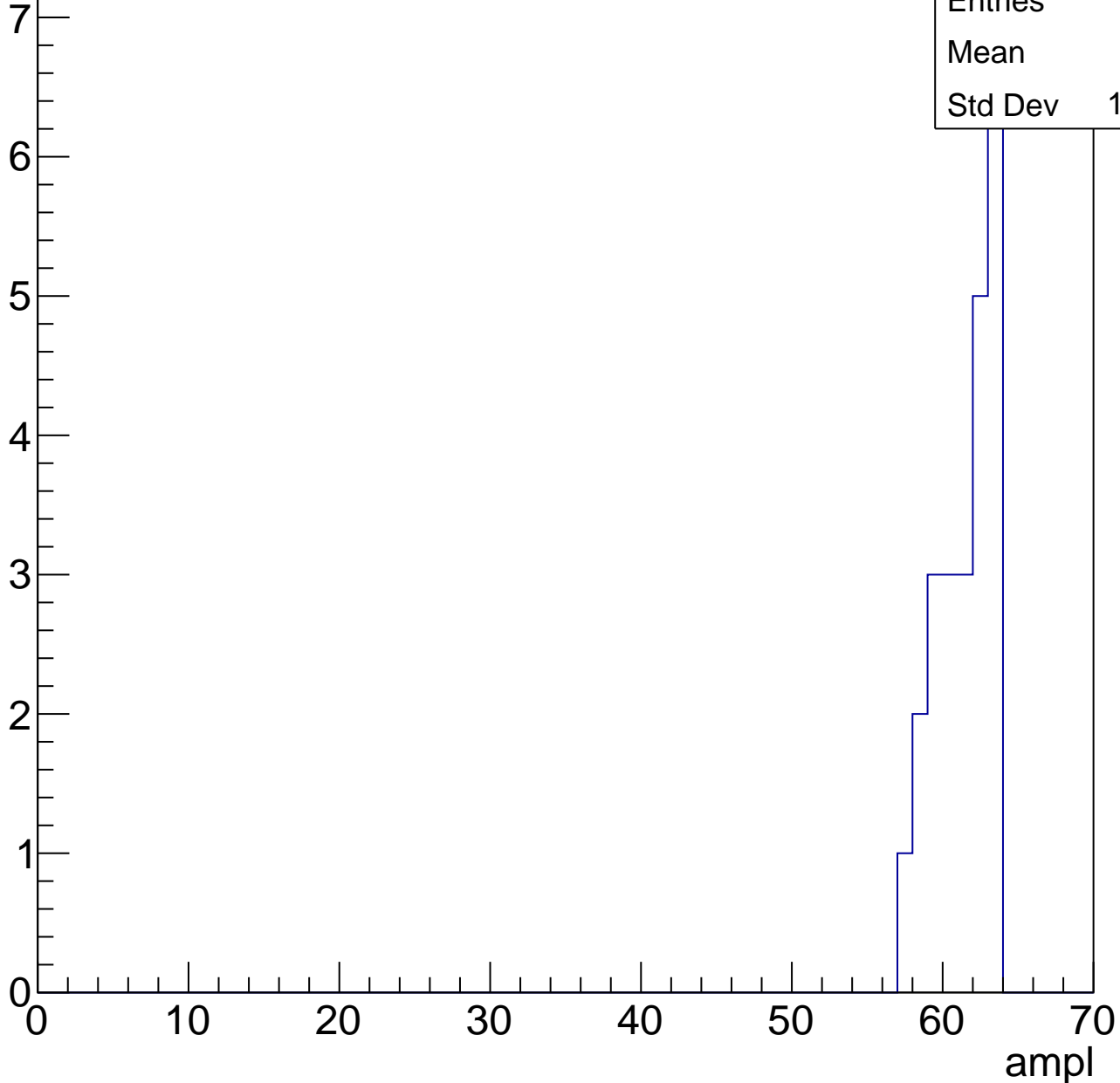


B1L103S, U8-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	61
Std Dev	1.848



B1L103S, U8-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch77, adc0

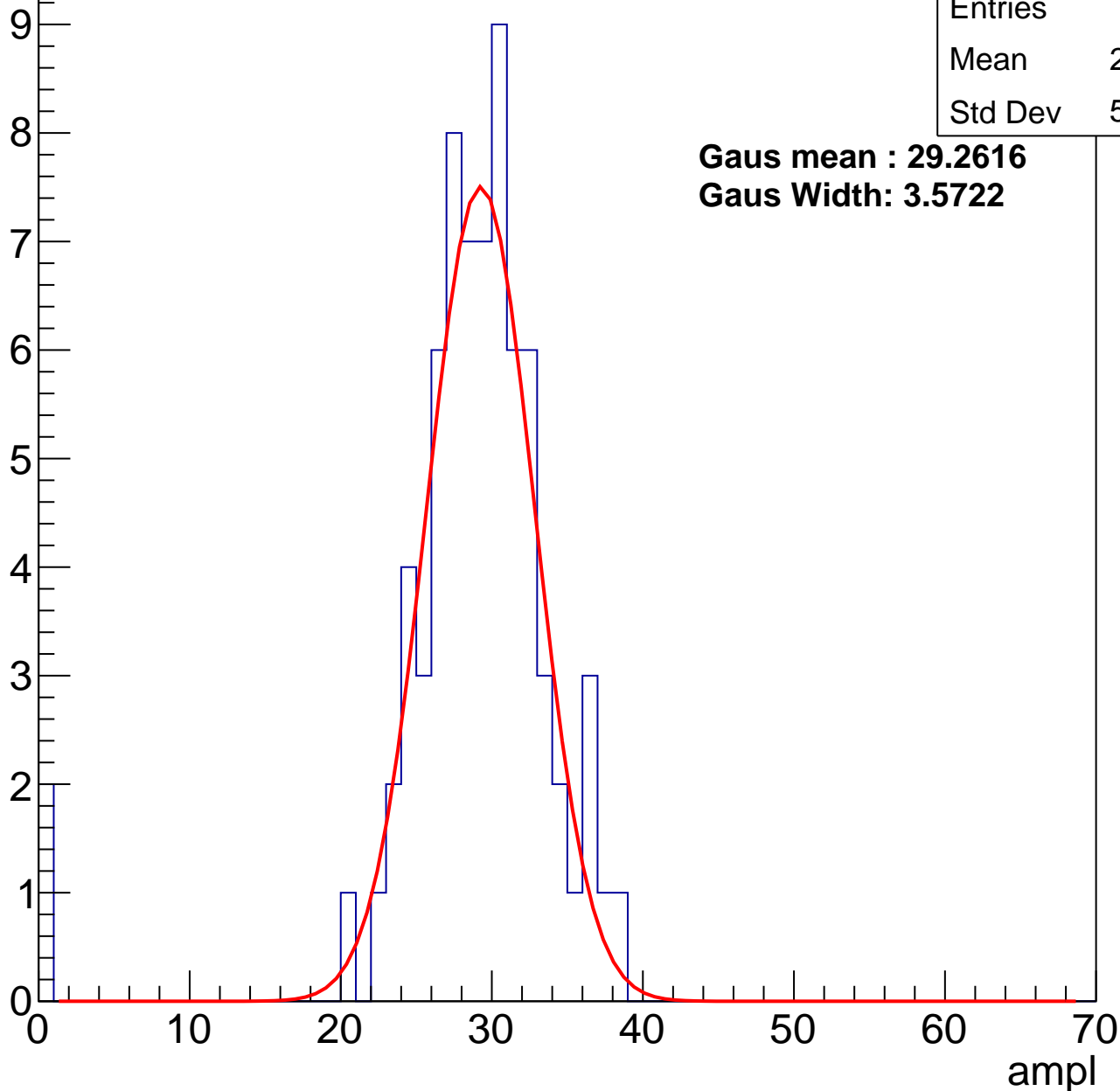
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	28.26
Std Dev	5.966

Gaus mean : 29.2616

Gaus Width: 3.5722



B1L103S, U8-ch77, adc1

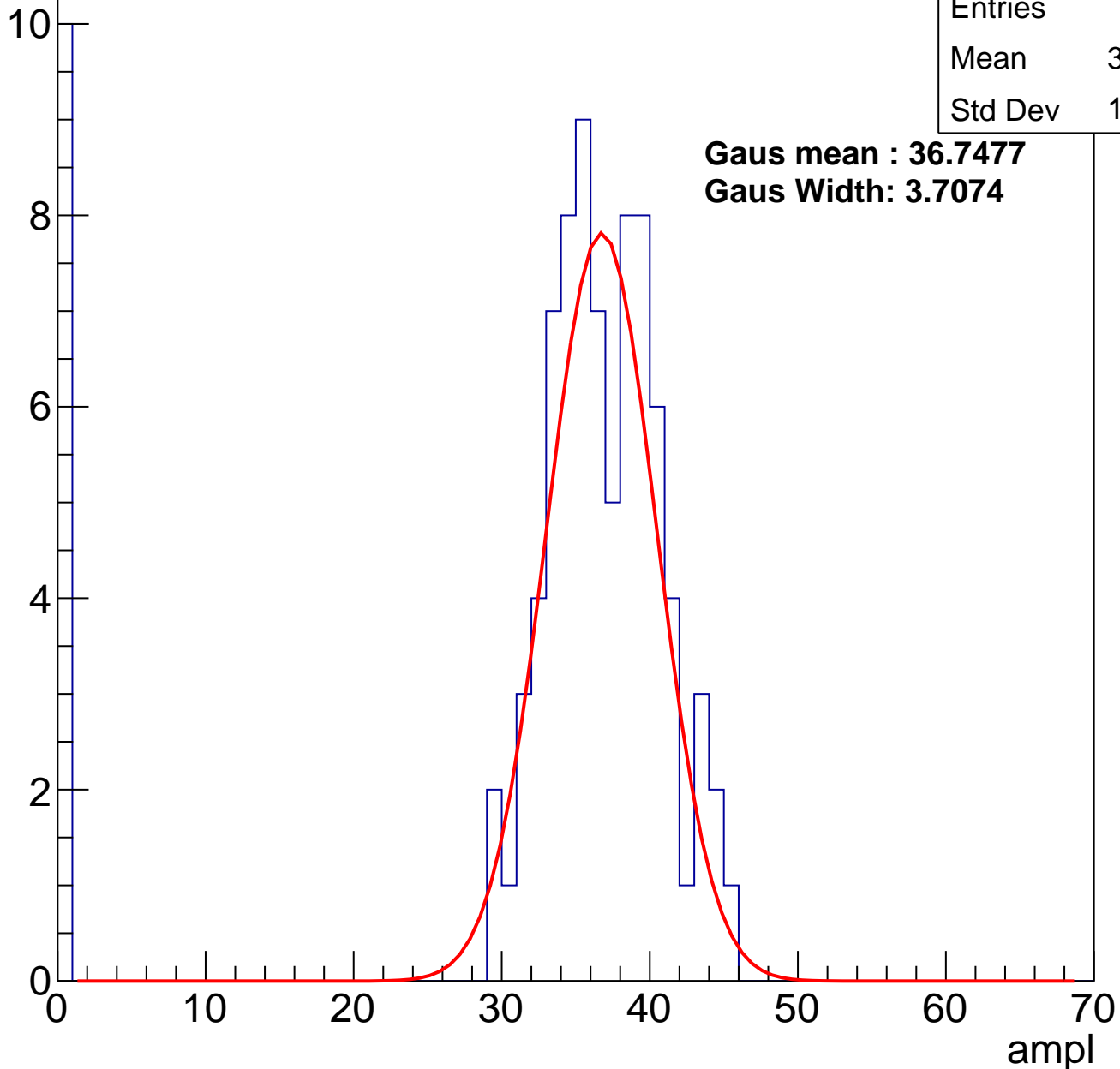
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	32.45
Std Dev	12.04

Gaus mean : 36.7477

Gaus Width: 3.7074

Entry



B1L103S, U8-ch77, adc2

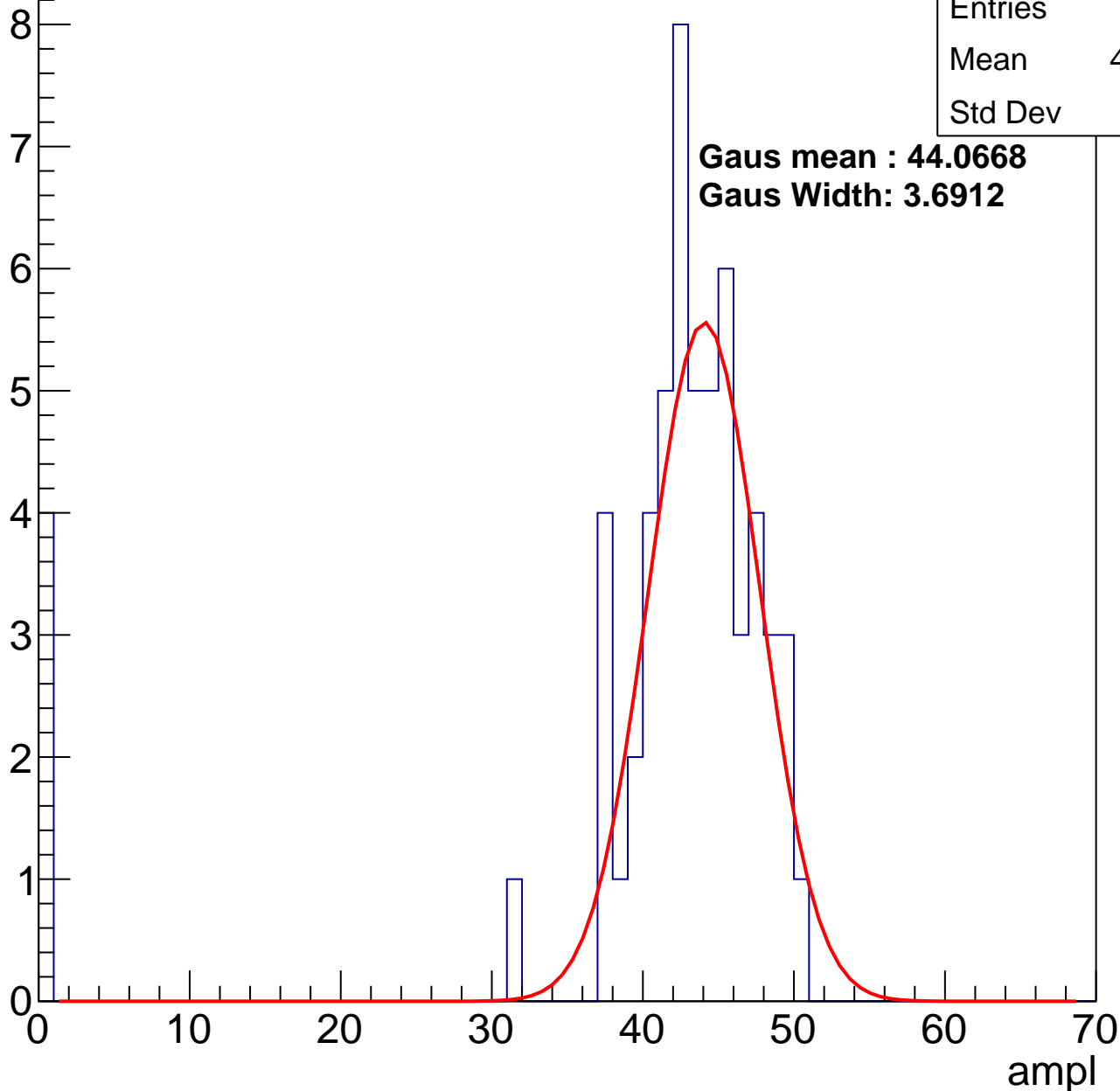
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	40.14
Std Dev	11.4

Gaus mean : 44.0668

Gaus Width: 3.6912

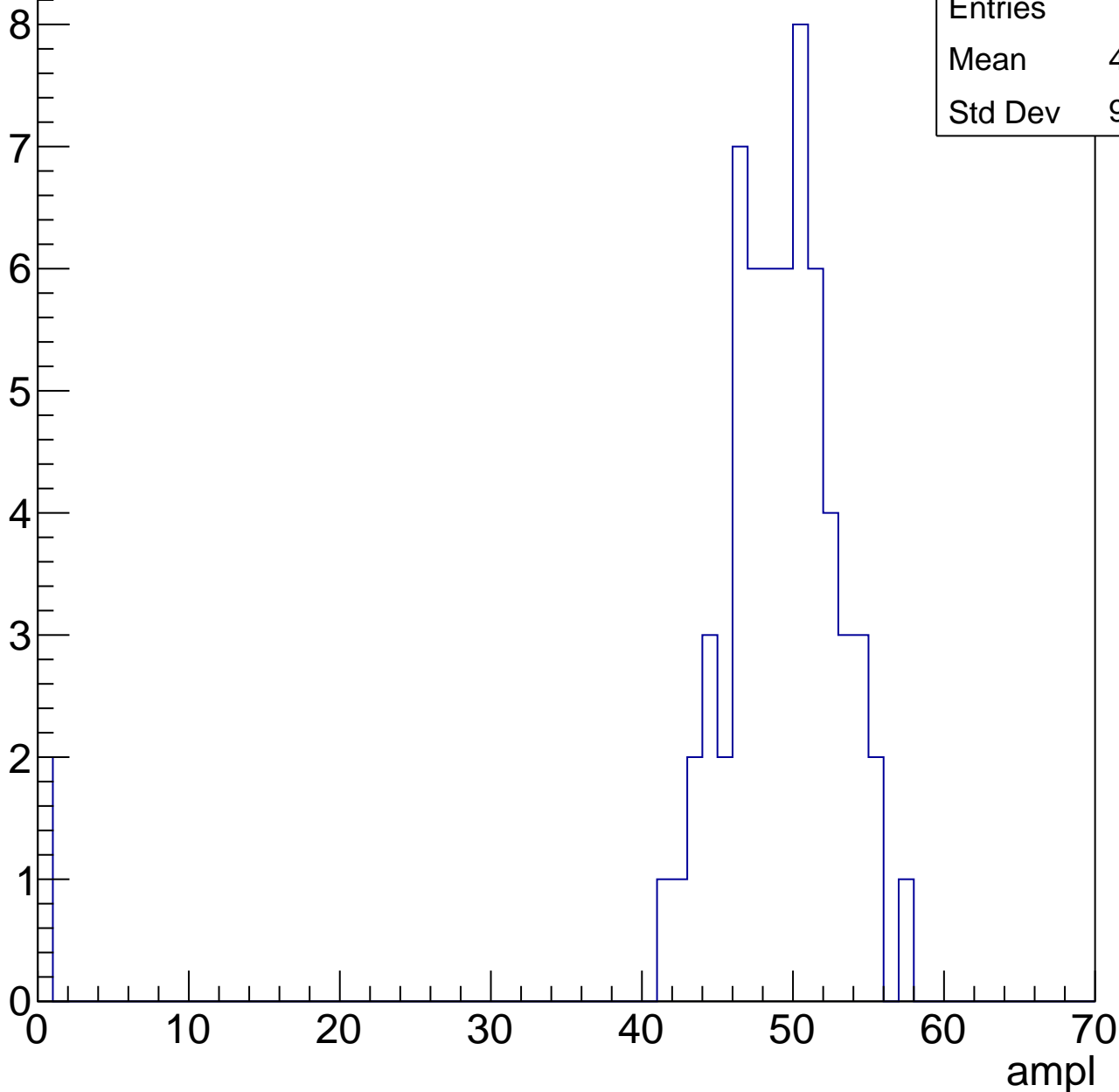


B1L103S, U8-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.29
Std Dev	9.197

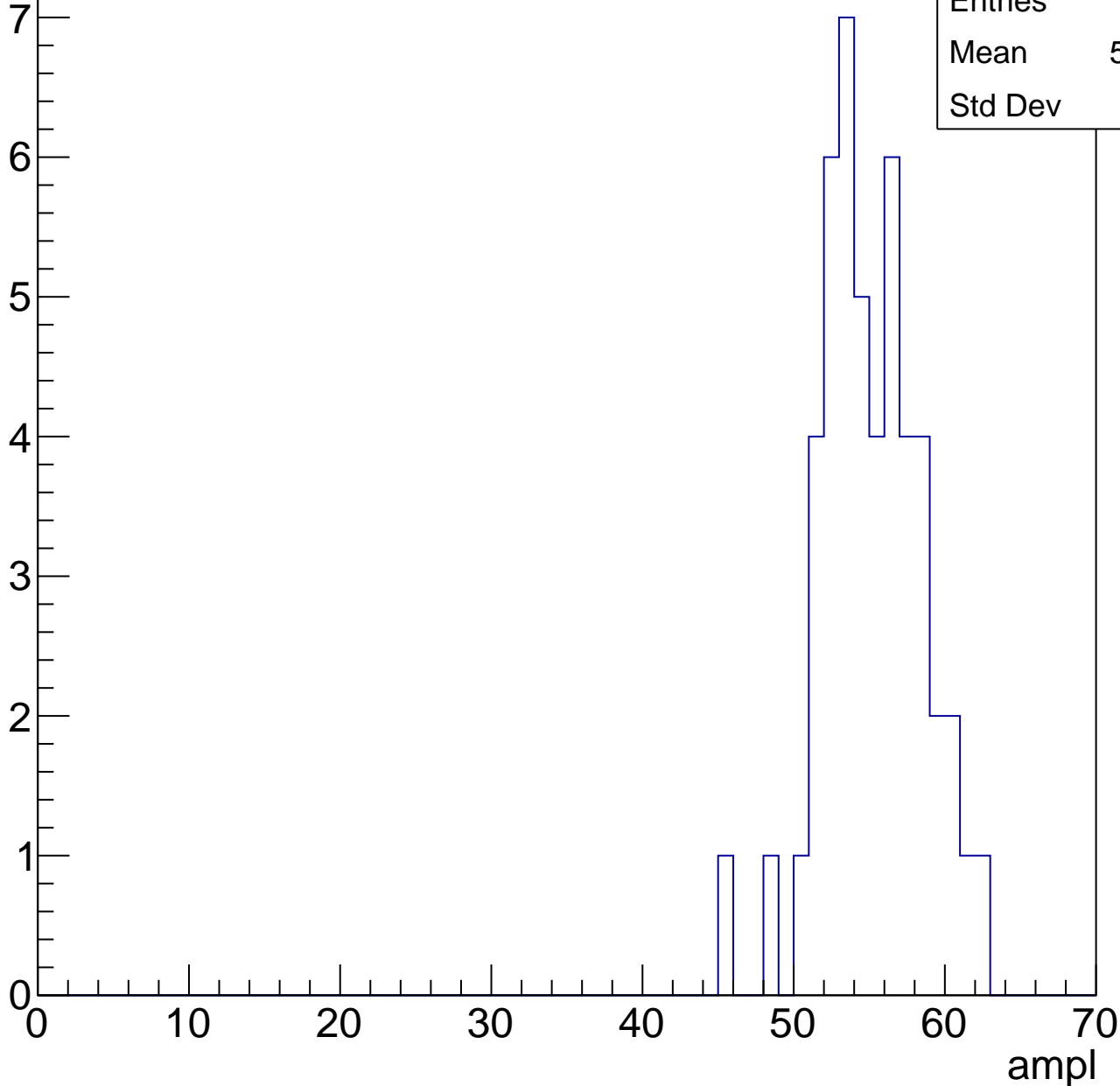


B1L103S, U8-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	54.63
Std Dev	3.33

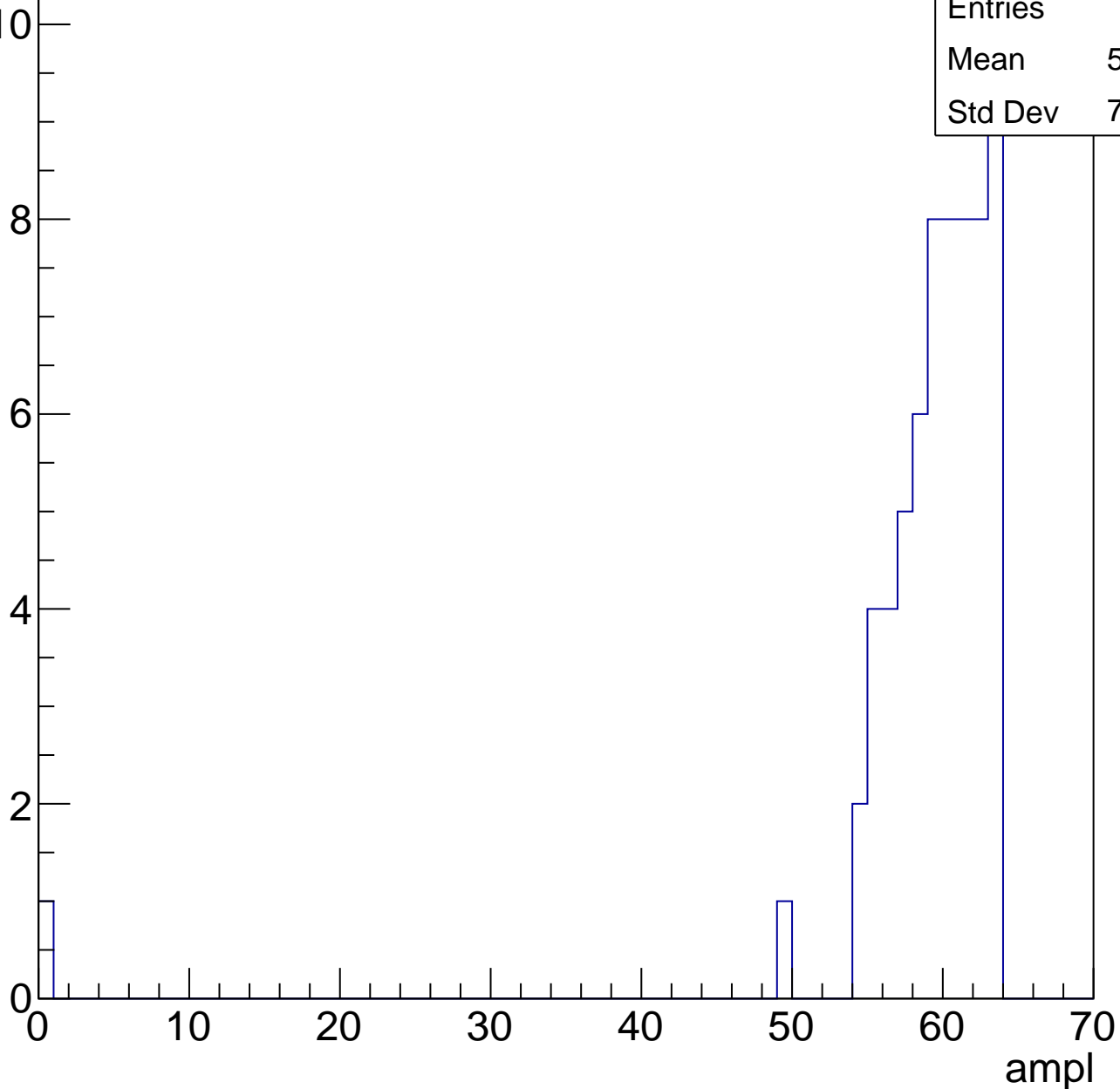


B1L103S, U8-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	58.46
Std Dev	7.855



B1L103S, U8-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch78, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	20.78
Std Dev	13.26

Gaus mean : 29.4635

Gaus Width: 3.6934

Entry

25

20

15

10

5

0

0

10

20

30

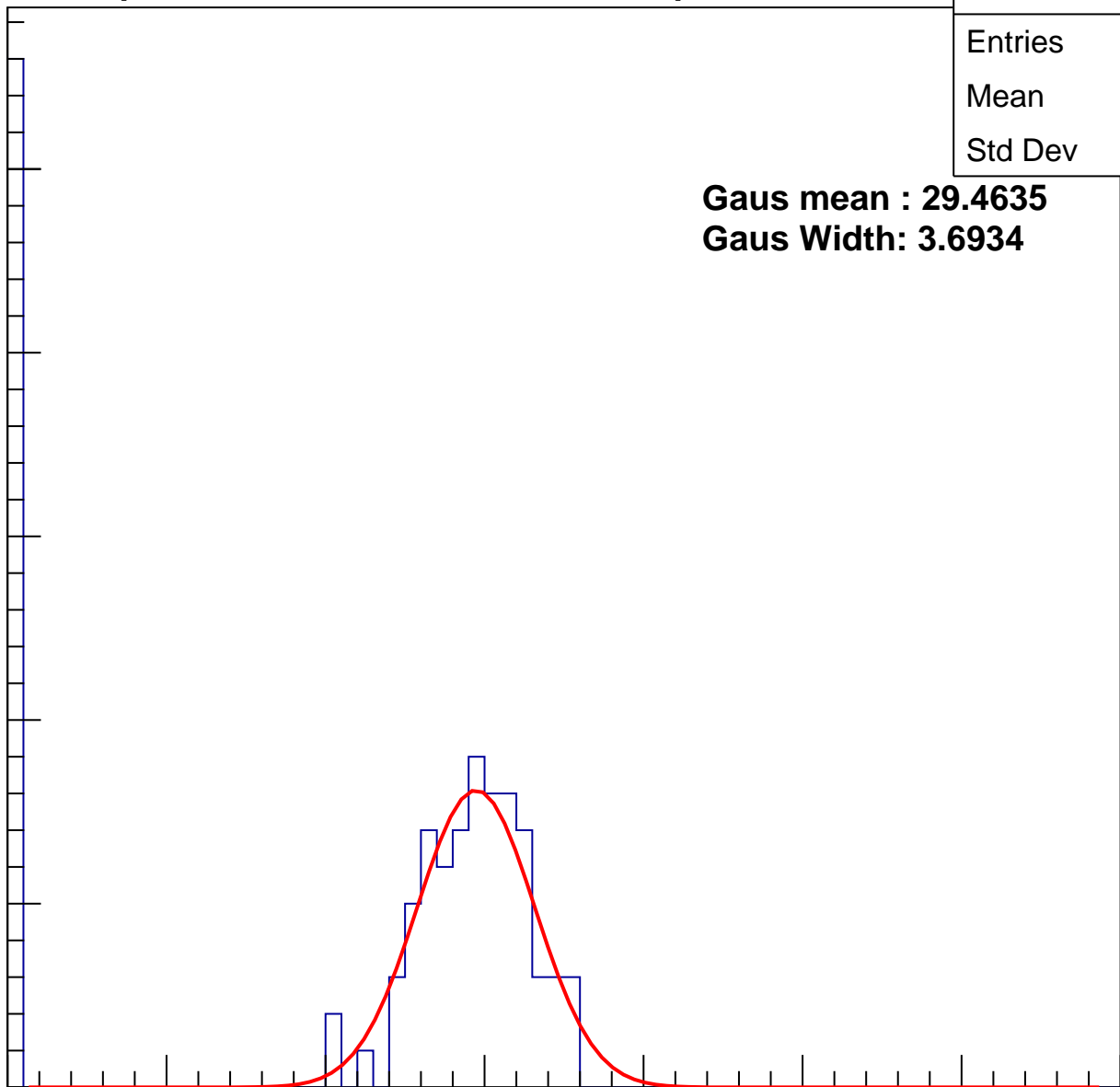
40

50

60

70

ampl



B1L103S, U8-ch78, adc1

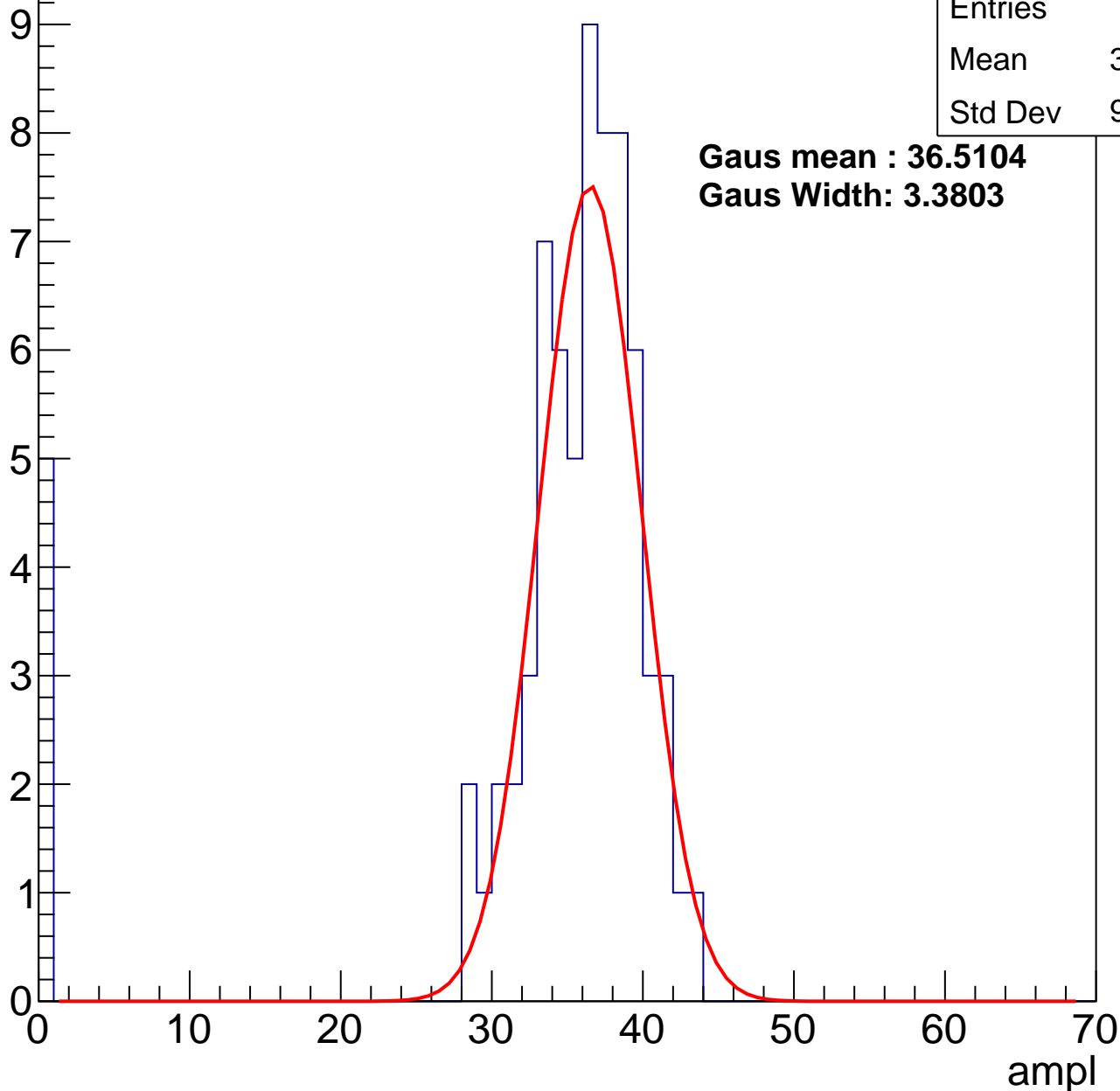
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	33.32
Std Dev	9.649

Gaus mean : 36.5104

Gaus Width: 3.3803



B1L103S, U8-ch78, adc2

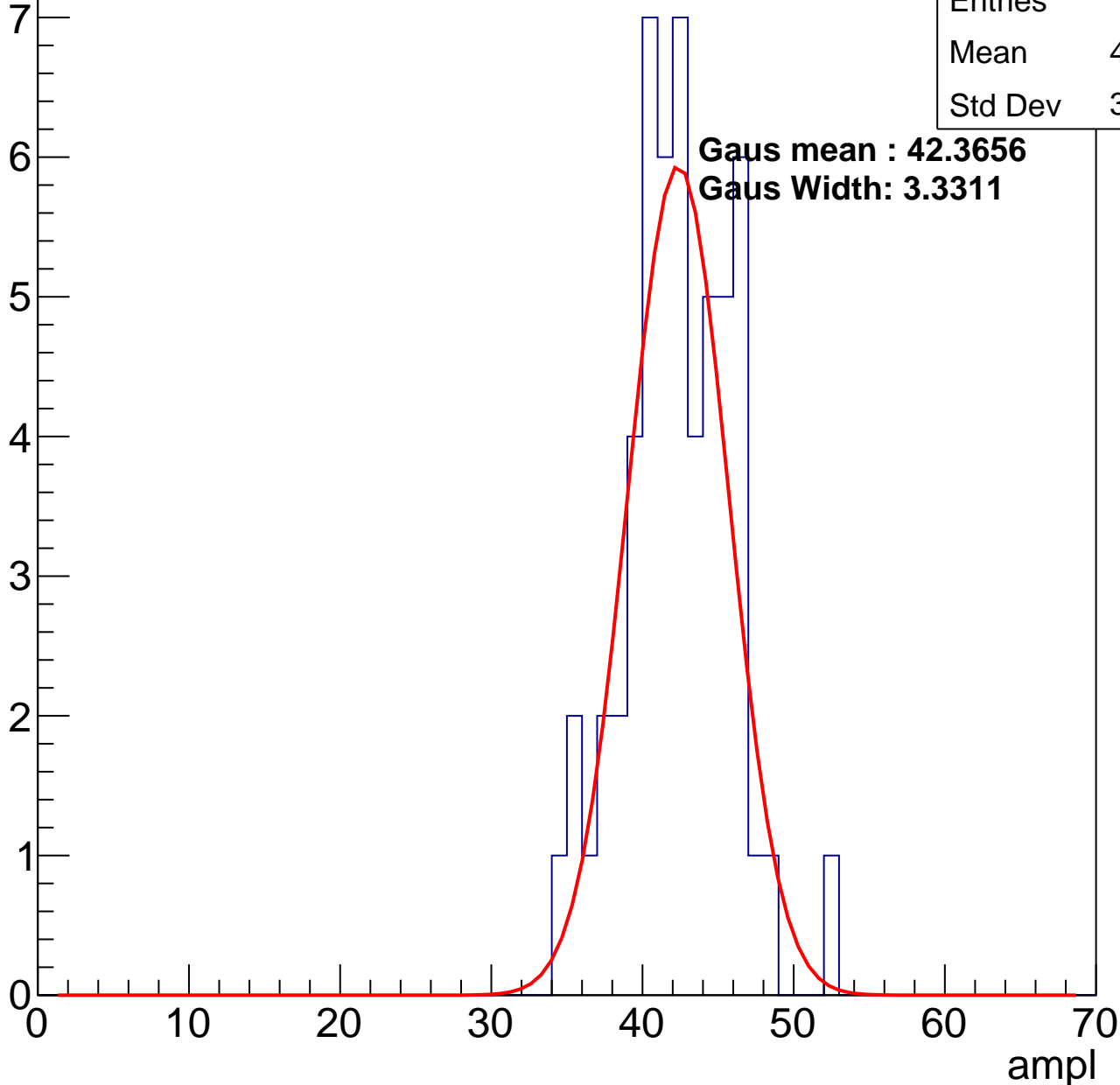
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	41.93
Std Dev	3.505

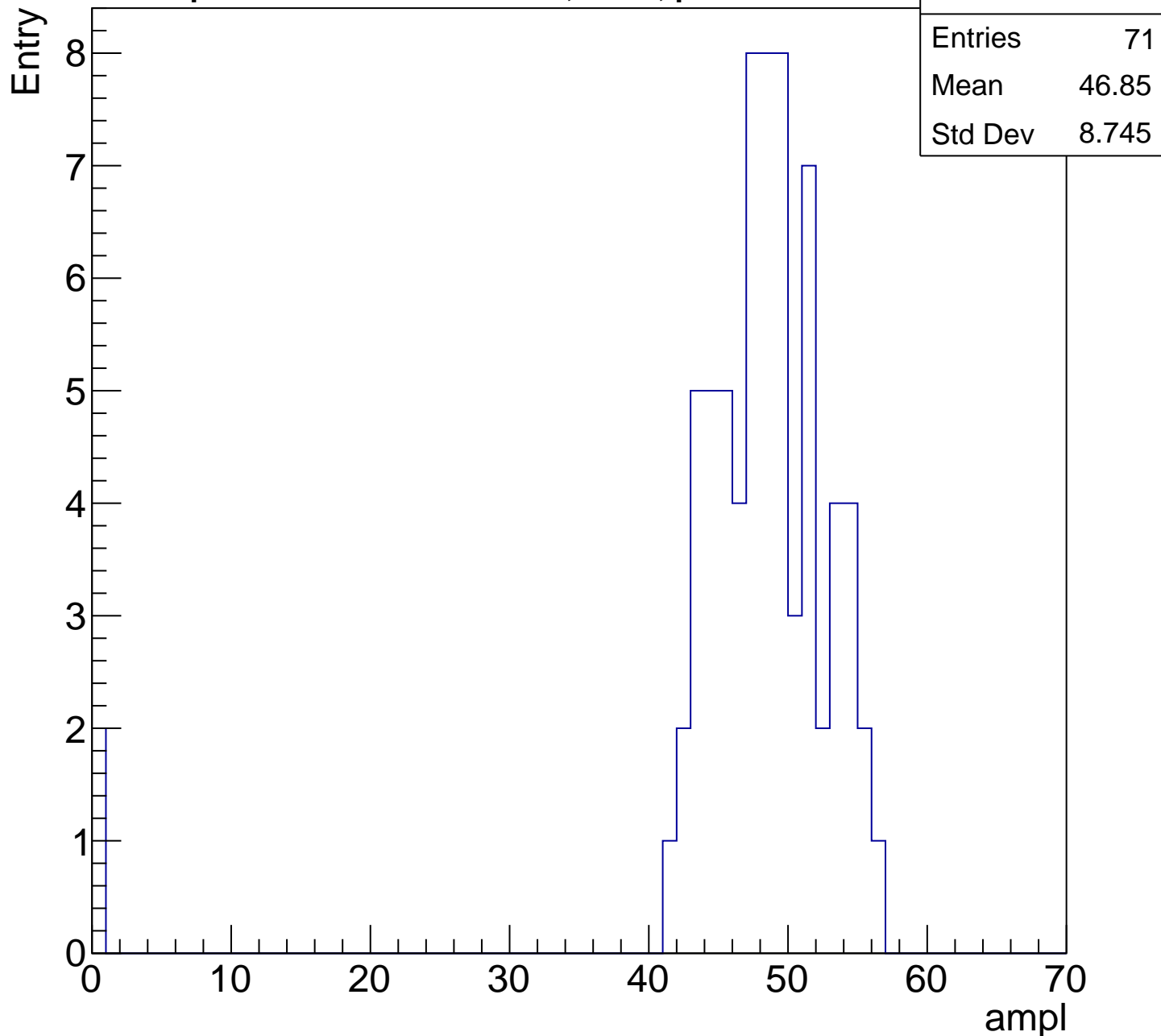
Gaus mean : 42.3656

Gaus Width: 3.3311



B1L103S, U8-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

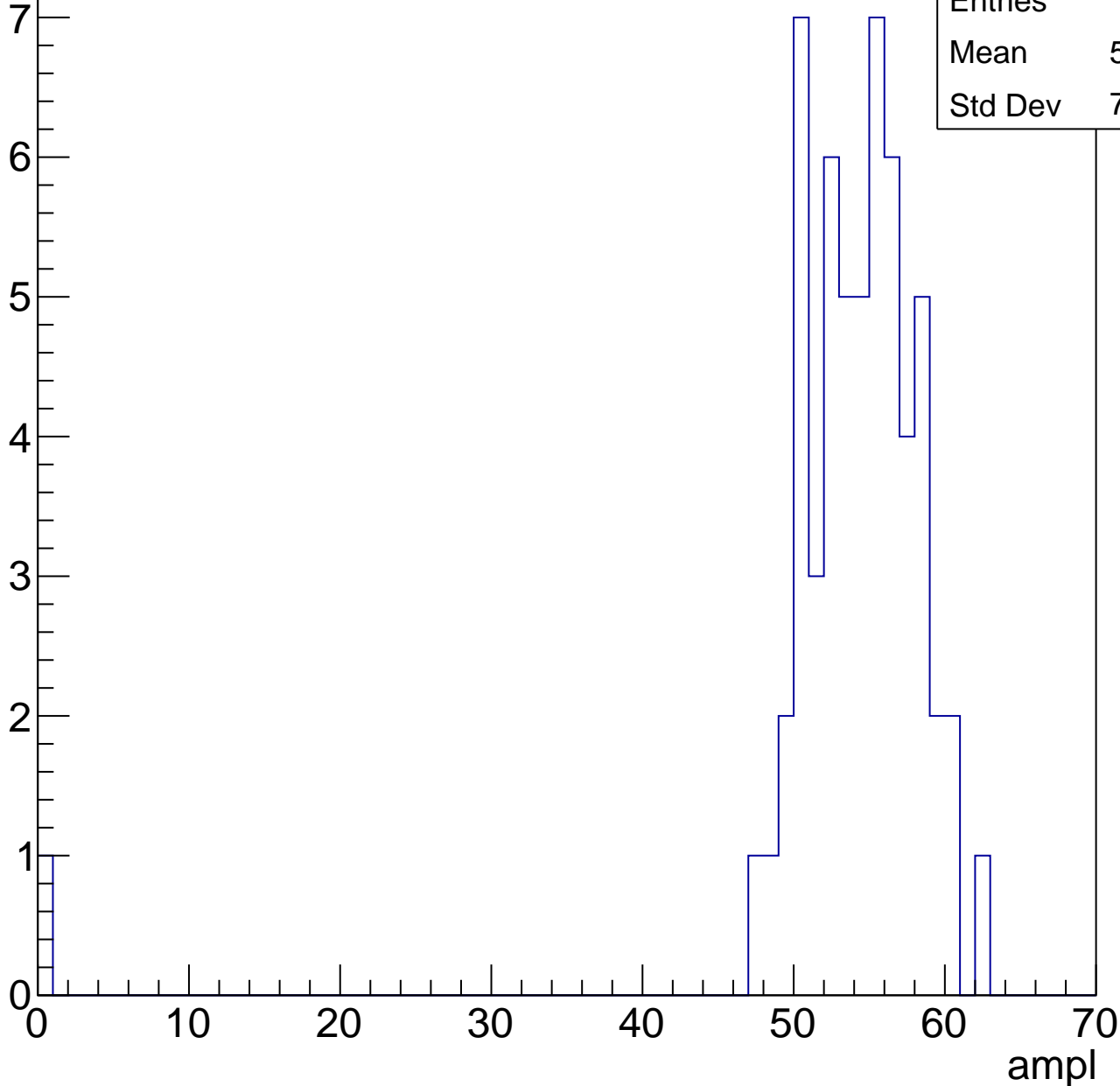


B1L103S, U8-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.14
Std Dev	7.778

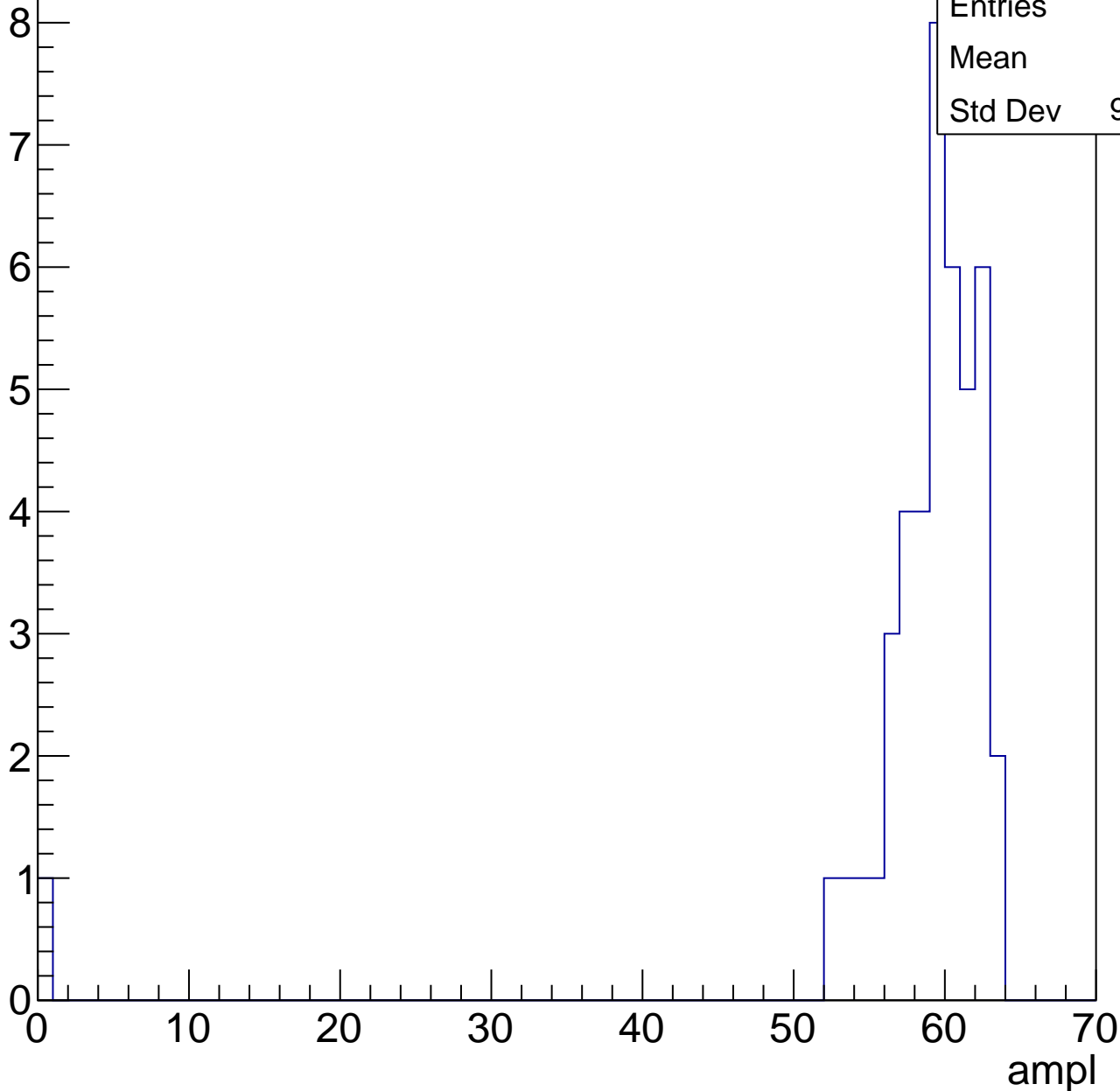


B1L103S, U8-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	57.6
Std Dev	9.254

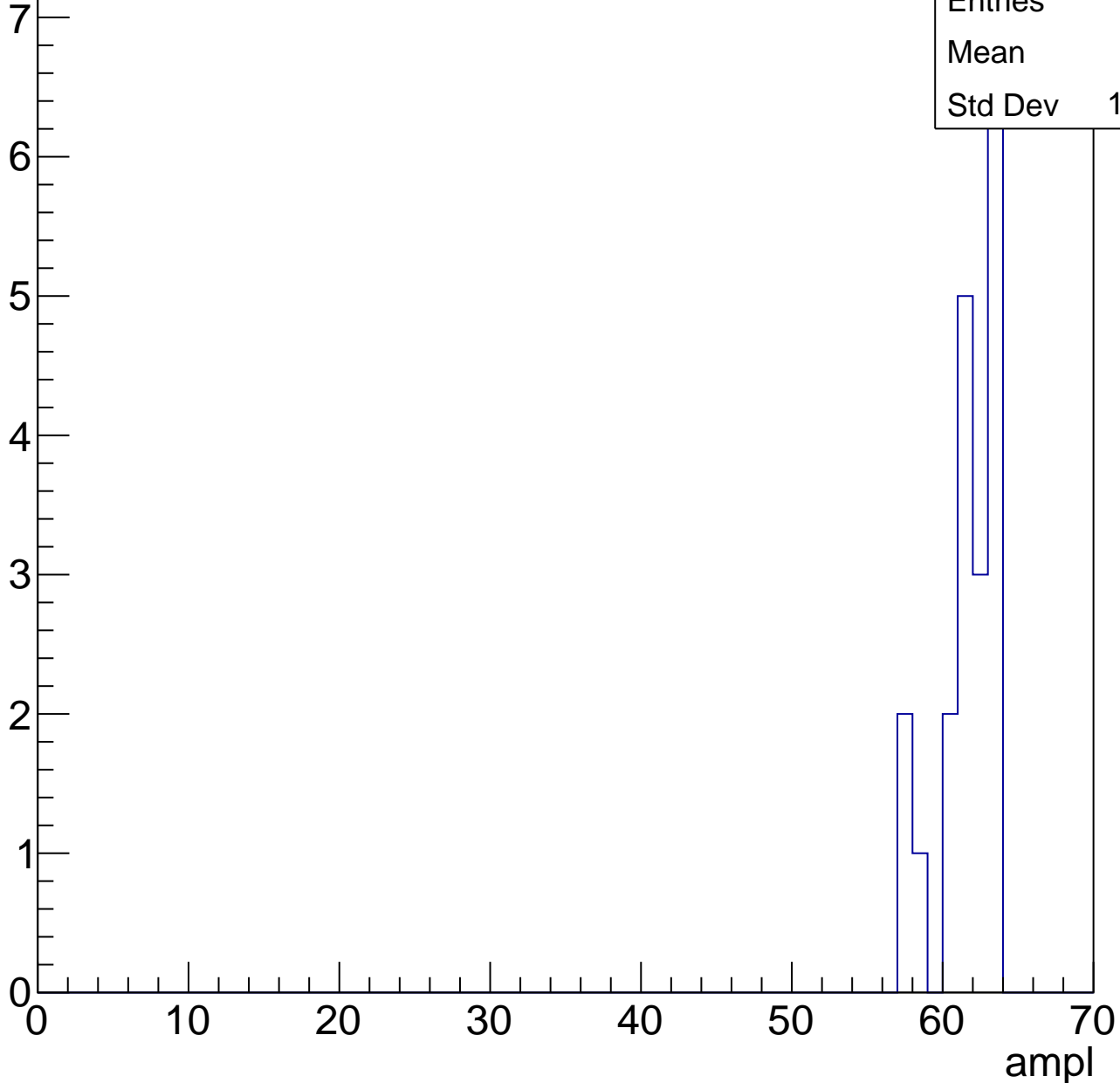


B1L103S, U8-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	61.2
Std Dev	1.913



B1L103S, U8-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U8-ch79, adc0

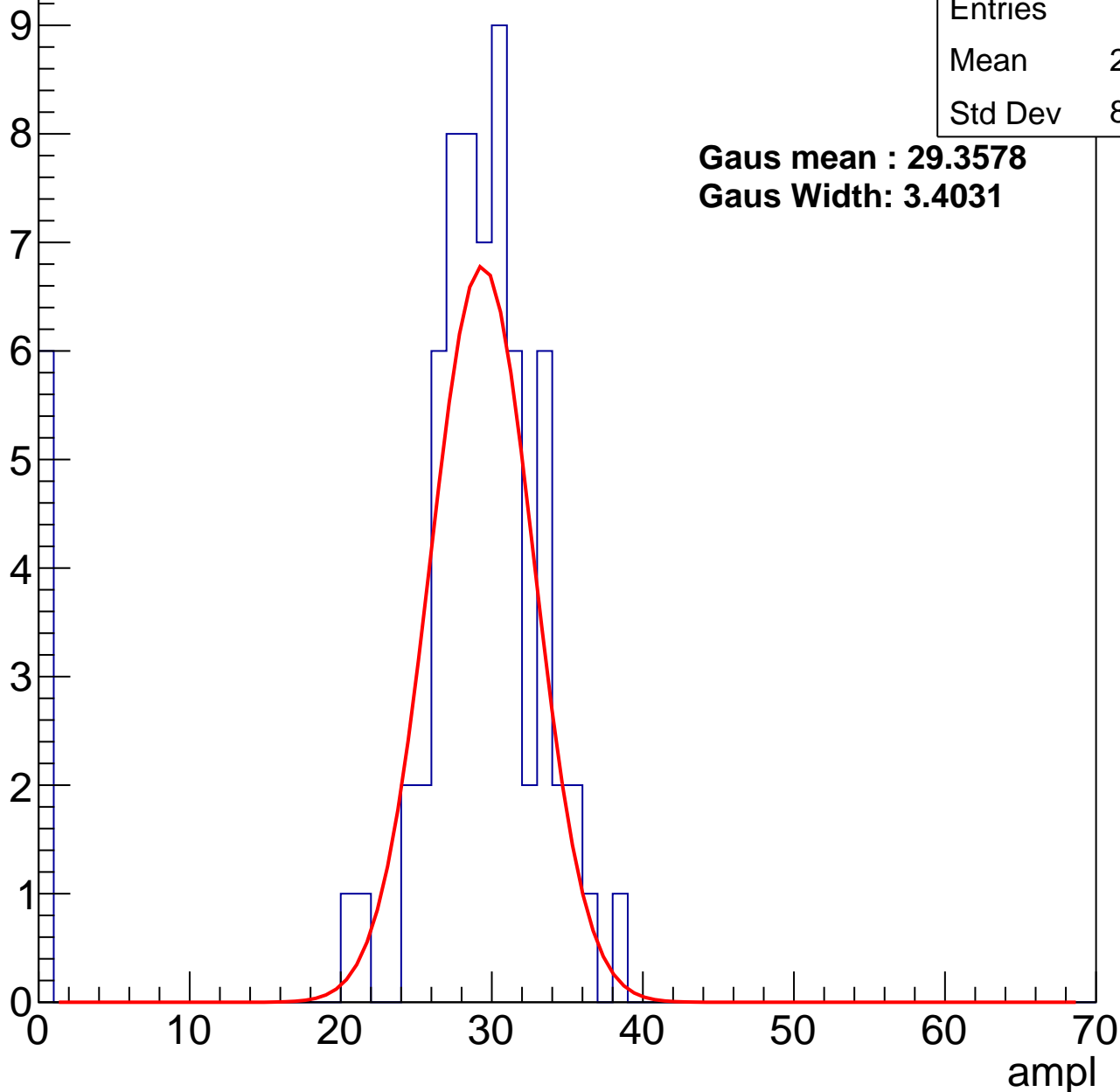
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	26.69
Std Dev	8.776

Gaus mean : 29.3578

Gaus Width: 3.4031



B1L103S, U8-ch79, adc1

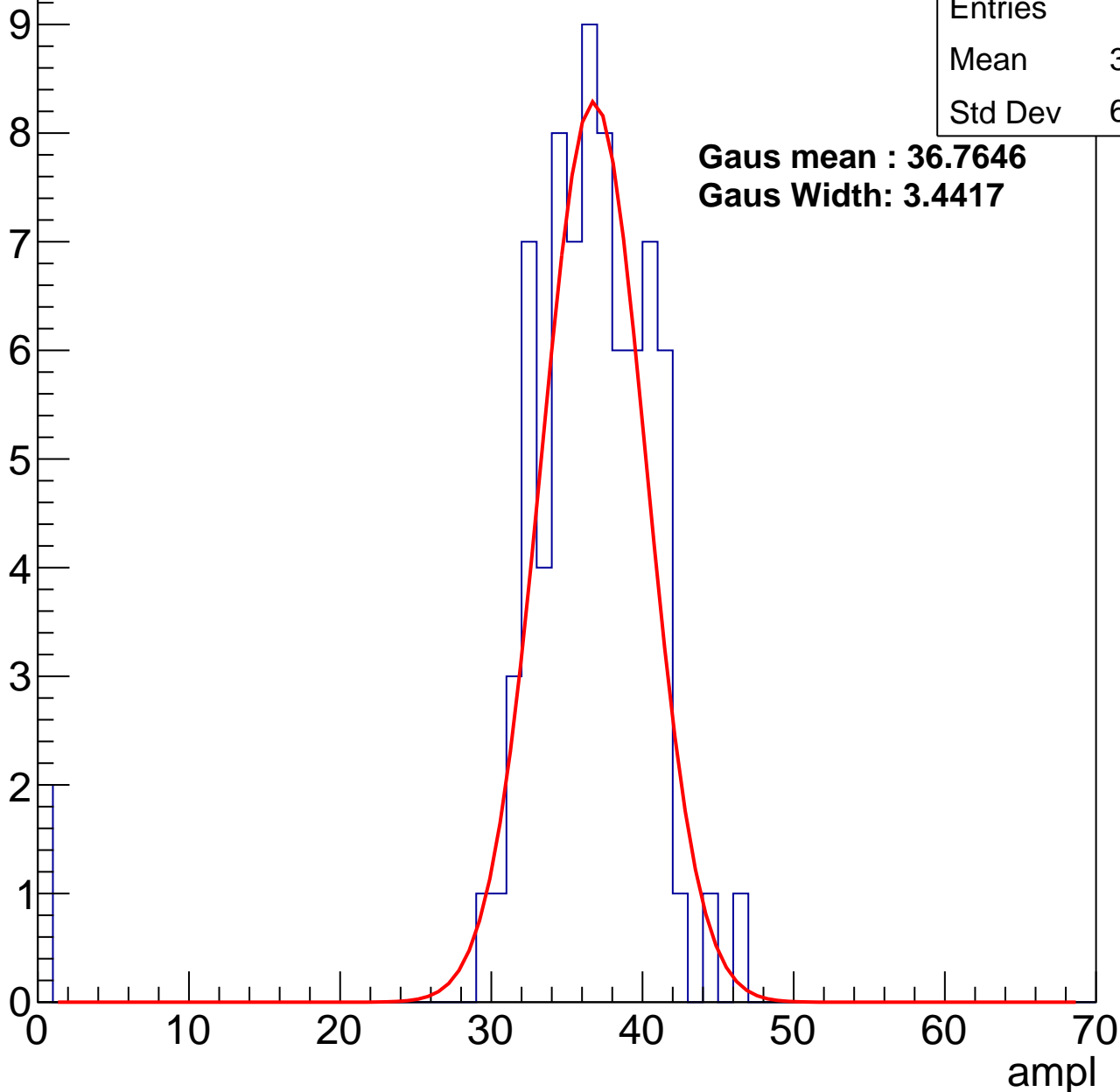
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	35.45
Std Dev	6.663

Gaus mean : 36.7646

Gaus Width: 3.4417



B1L103S, U8-ch79, adc2

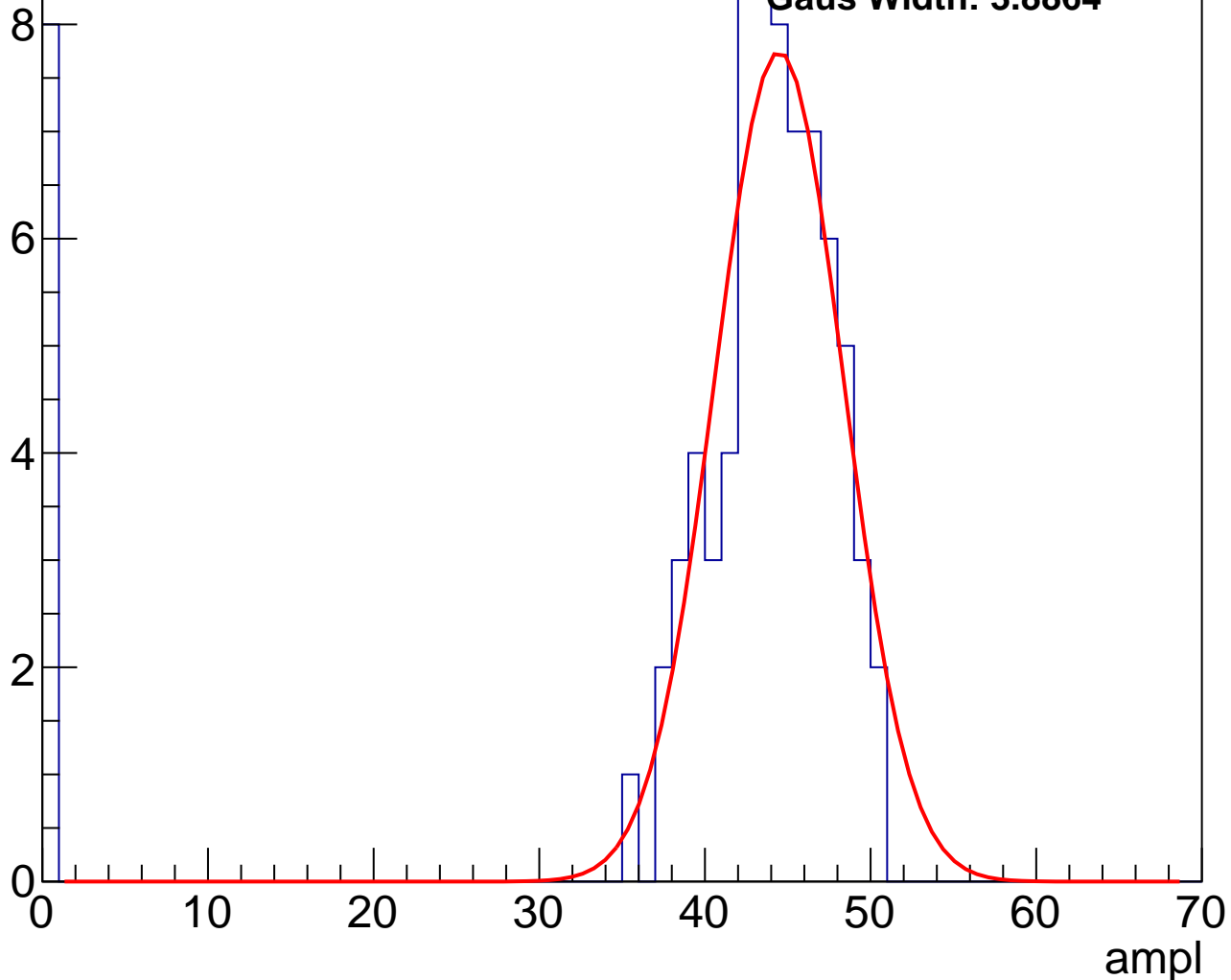
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	39.38
Std Dev	13.33

Gaus mean : 44.4798

Gaus Width: 3.8864

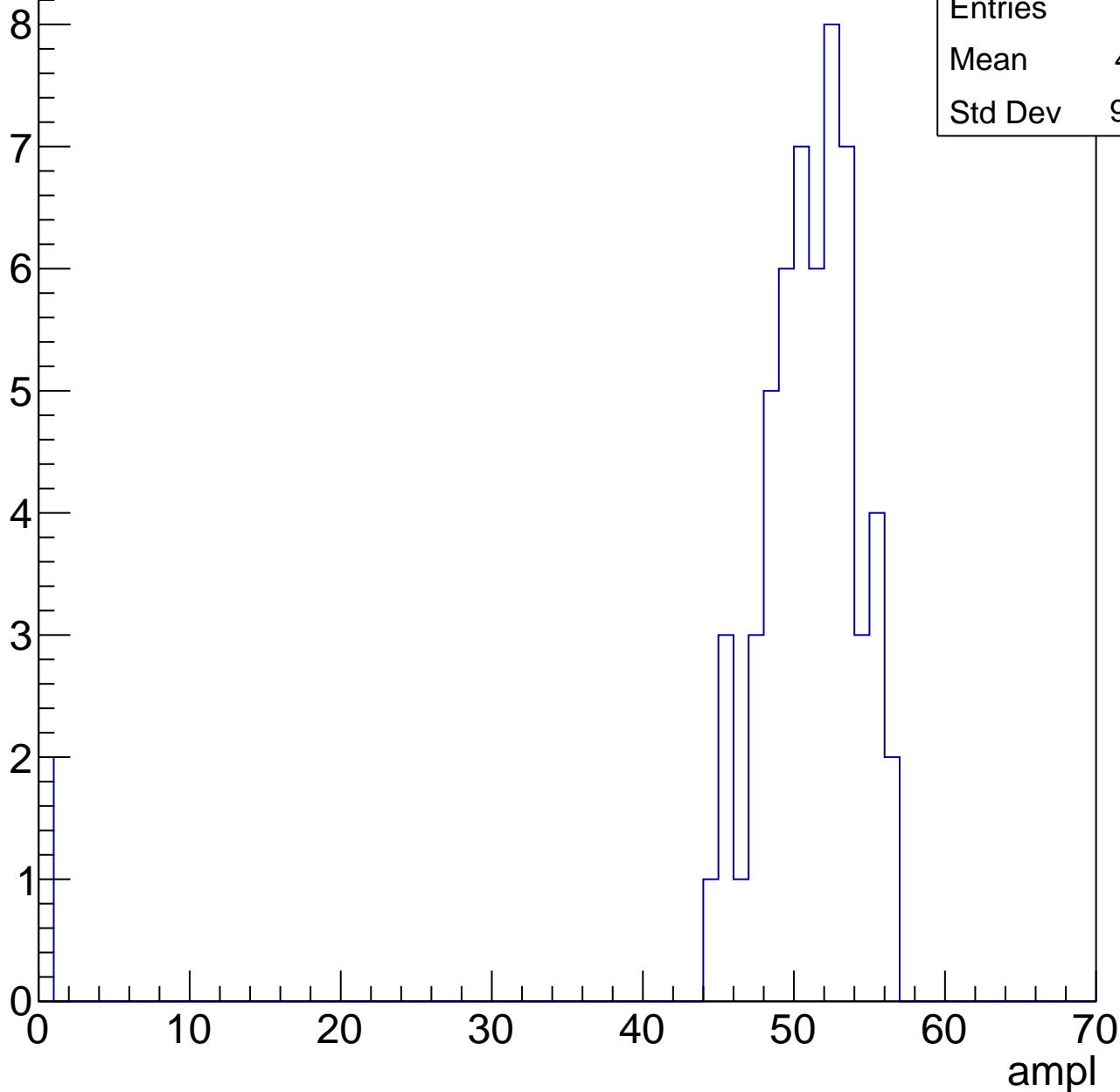


B1L103S, U8-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	48.91
Std Dev	9.678

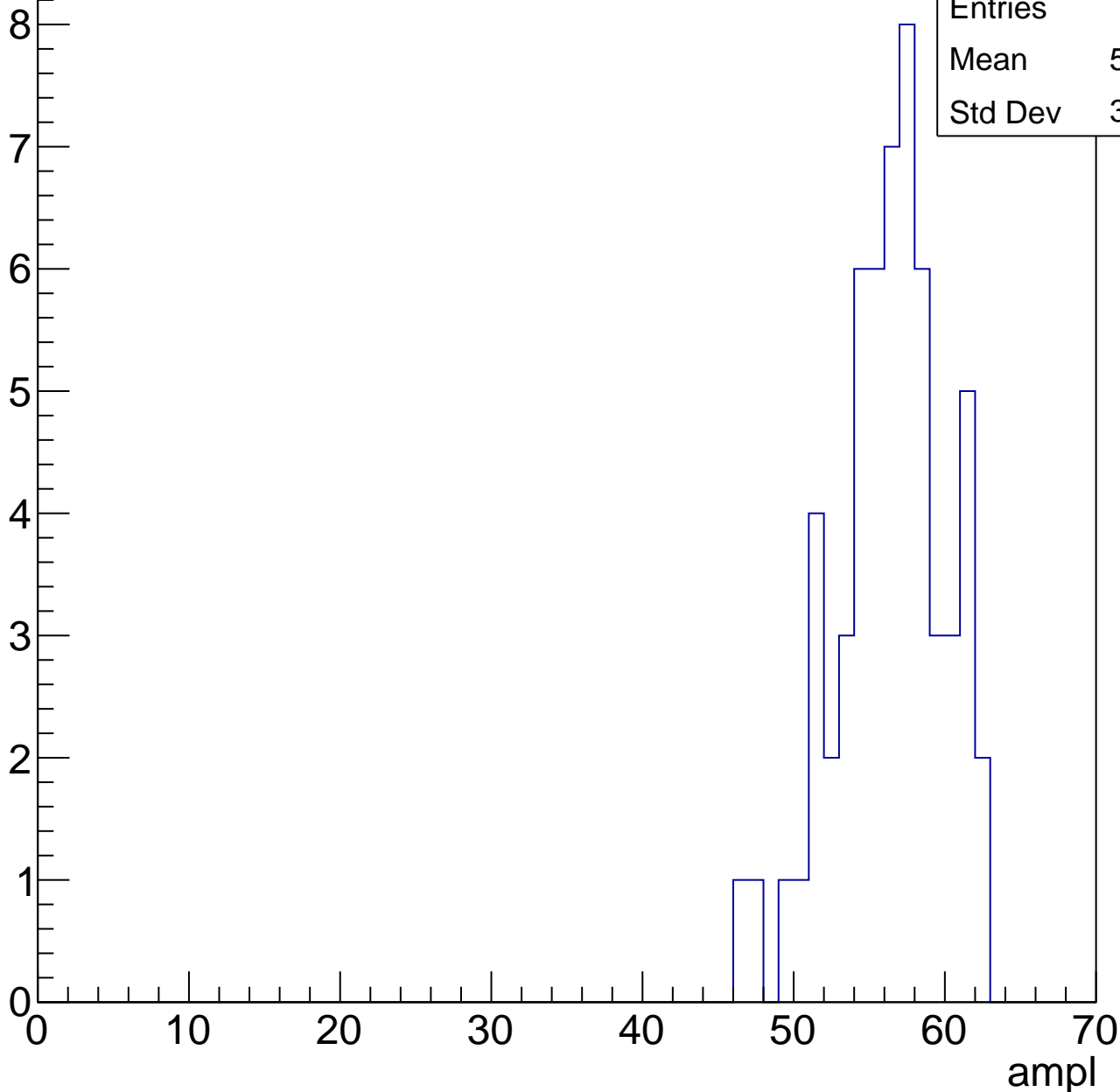


B1L103S, U8-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	55.85
Std Dev	3.583

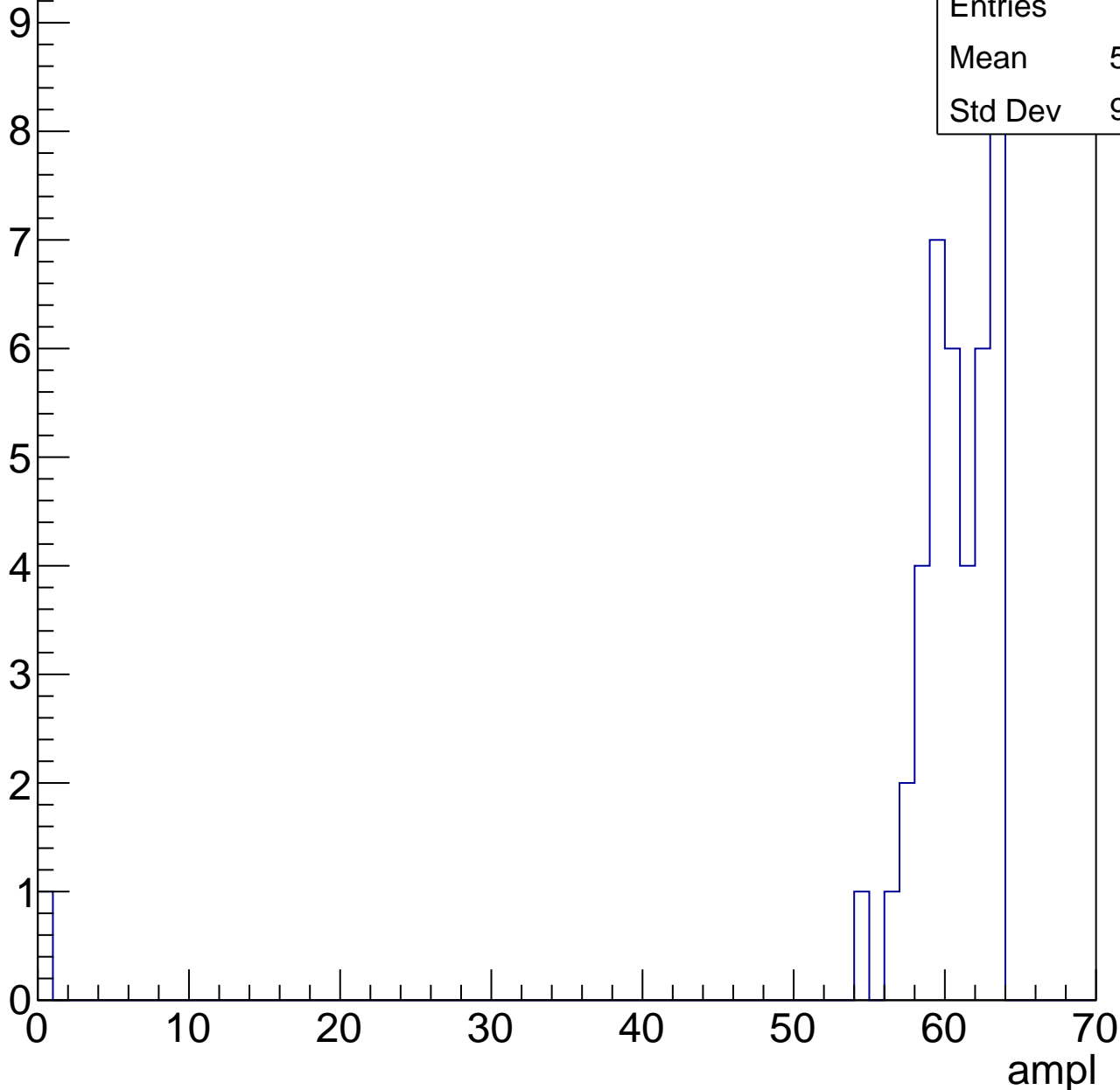


B1L103S, U8-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	58.83
Std Dev	9.558



B1L103S, U8-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

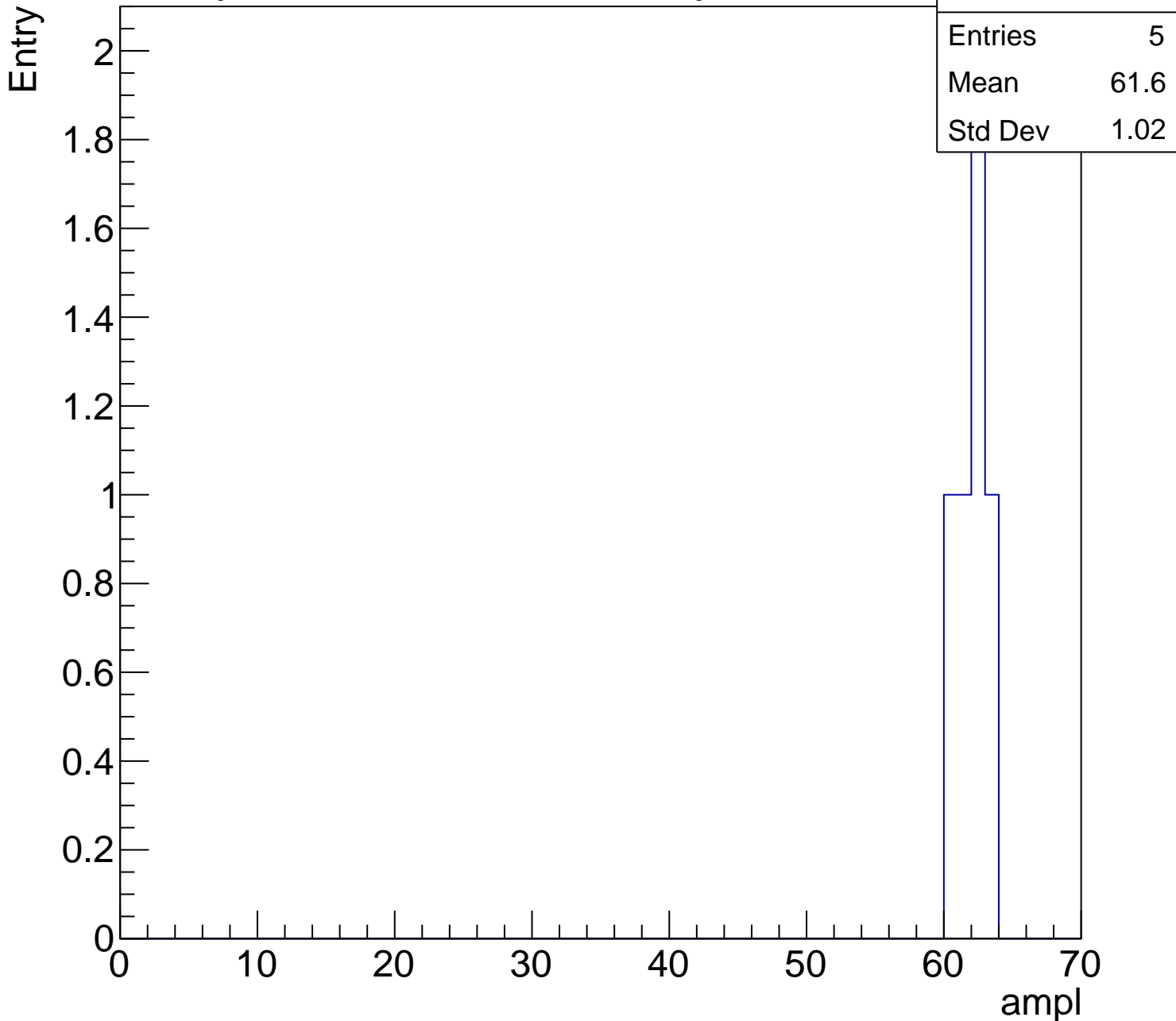
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.02

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch80, adc0

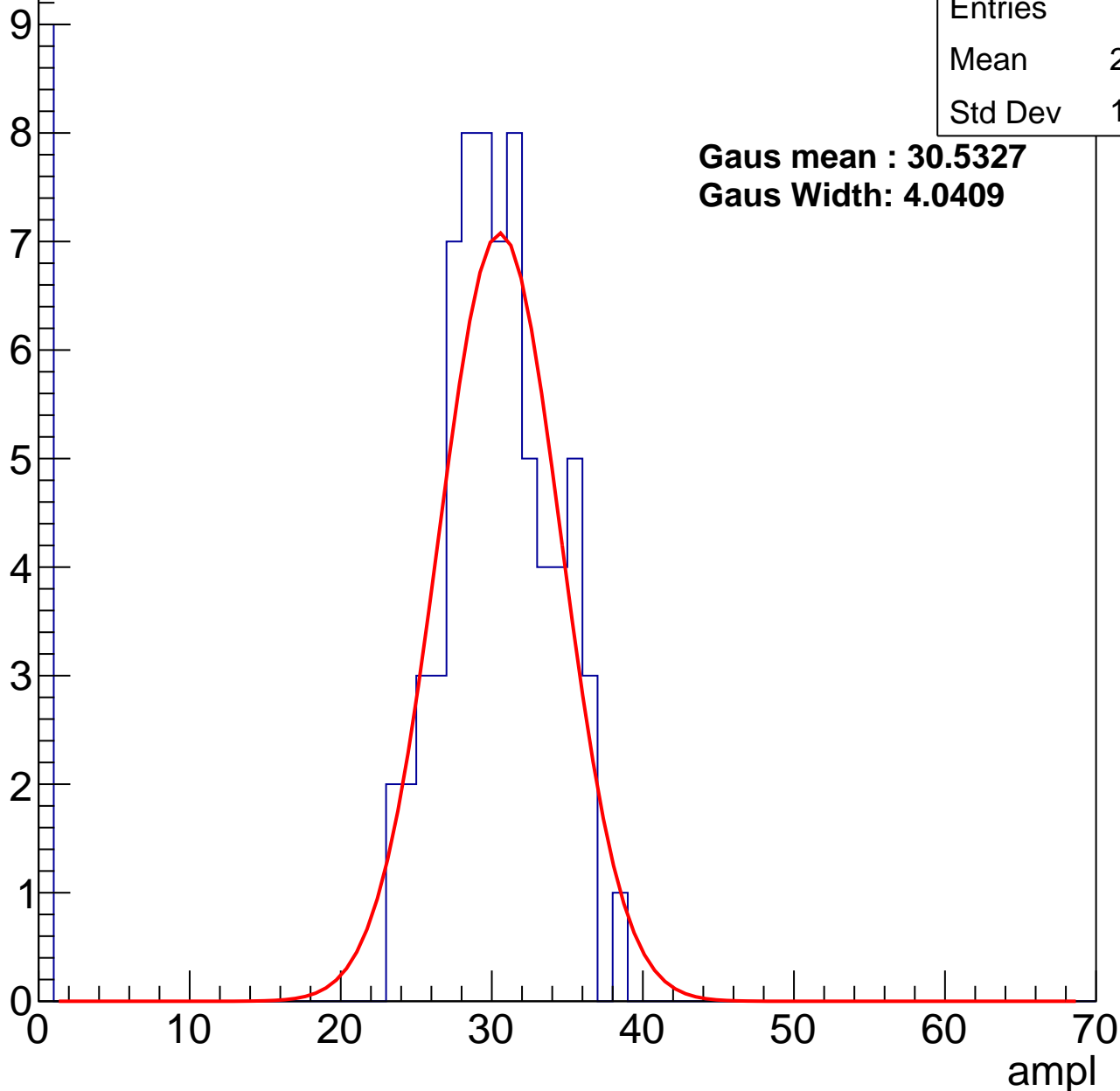
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	26.57
Std Dev	10.06

Gaus mean : 30.5327

Gaus Width: 4.0409



B1L103S, U8-ch80, adc1

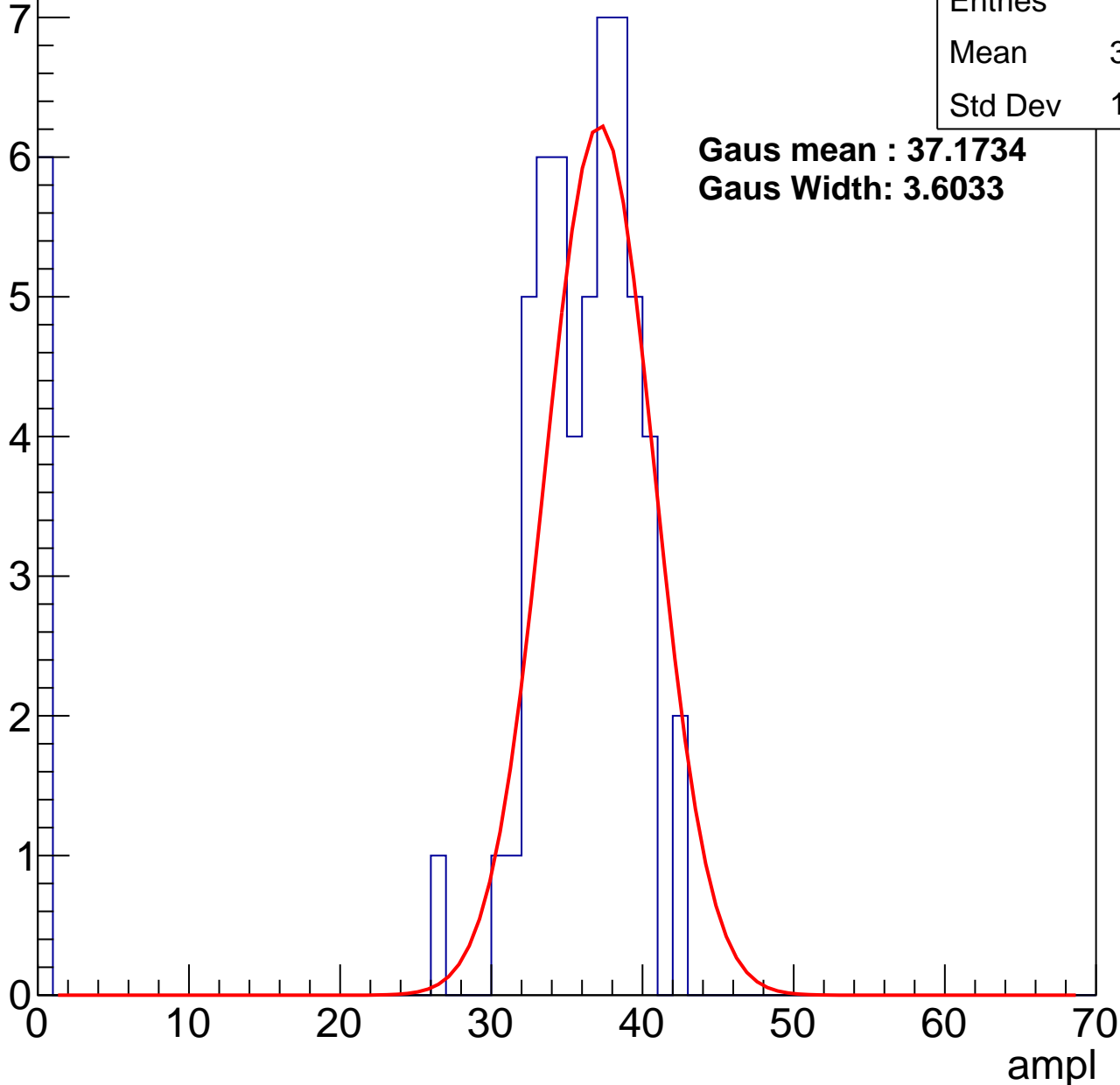
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	32.22
Std Dev	11.15

Gaus mean : 37.1734

Gaus Width: 3.6033



B1L103S, U8-ch80, adc2

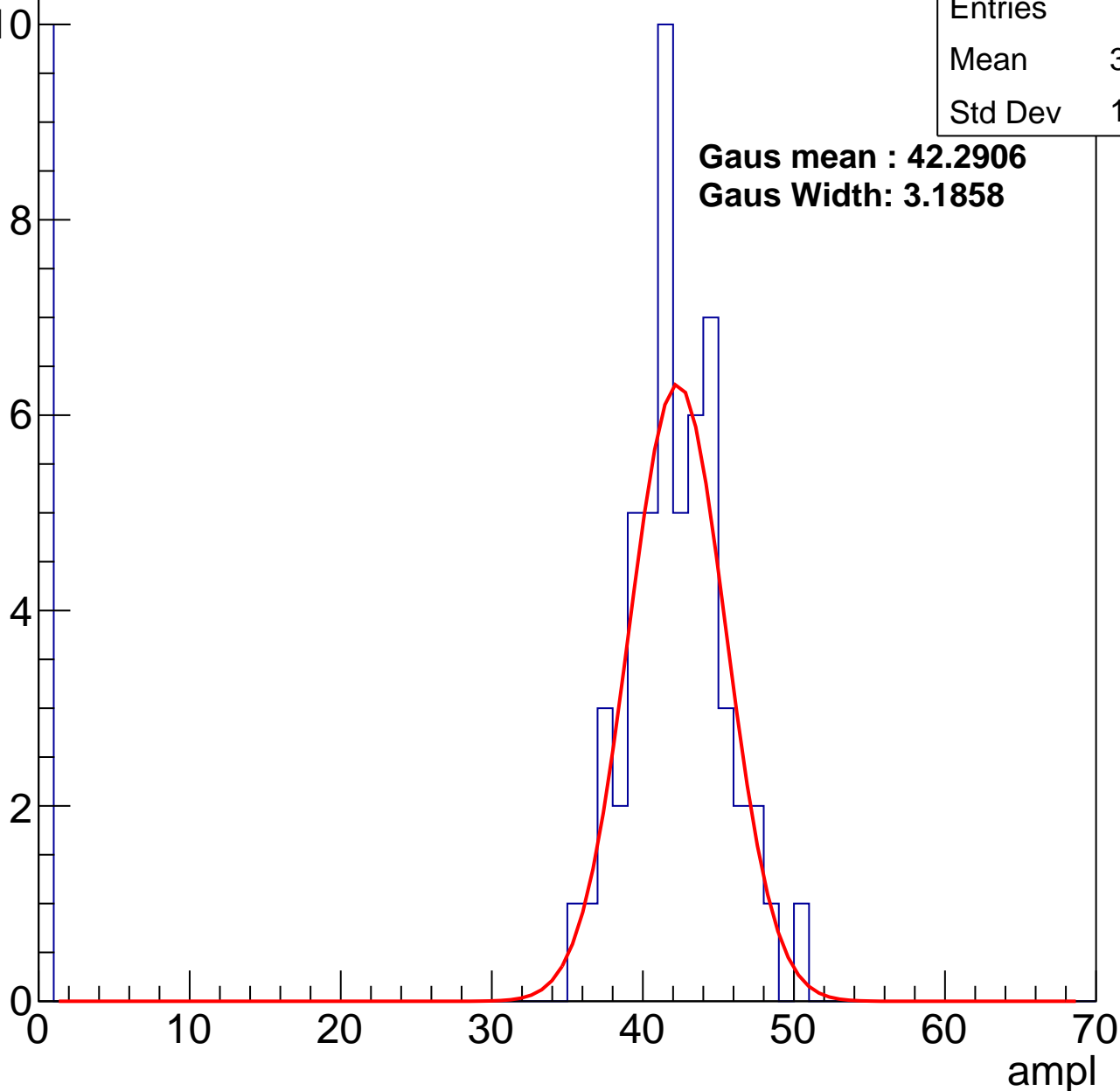
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	35.28
Std Dev	15.44

Gaus mean : 42.2906

Gaus Width: 3.1858

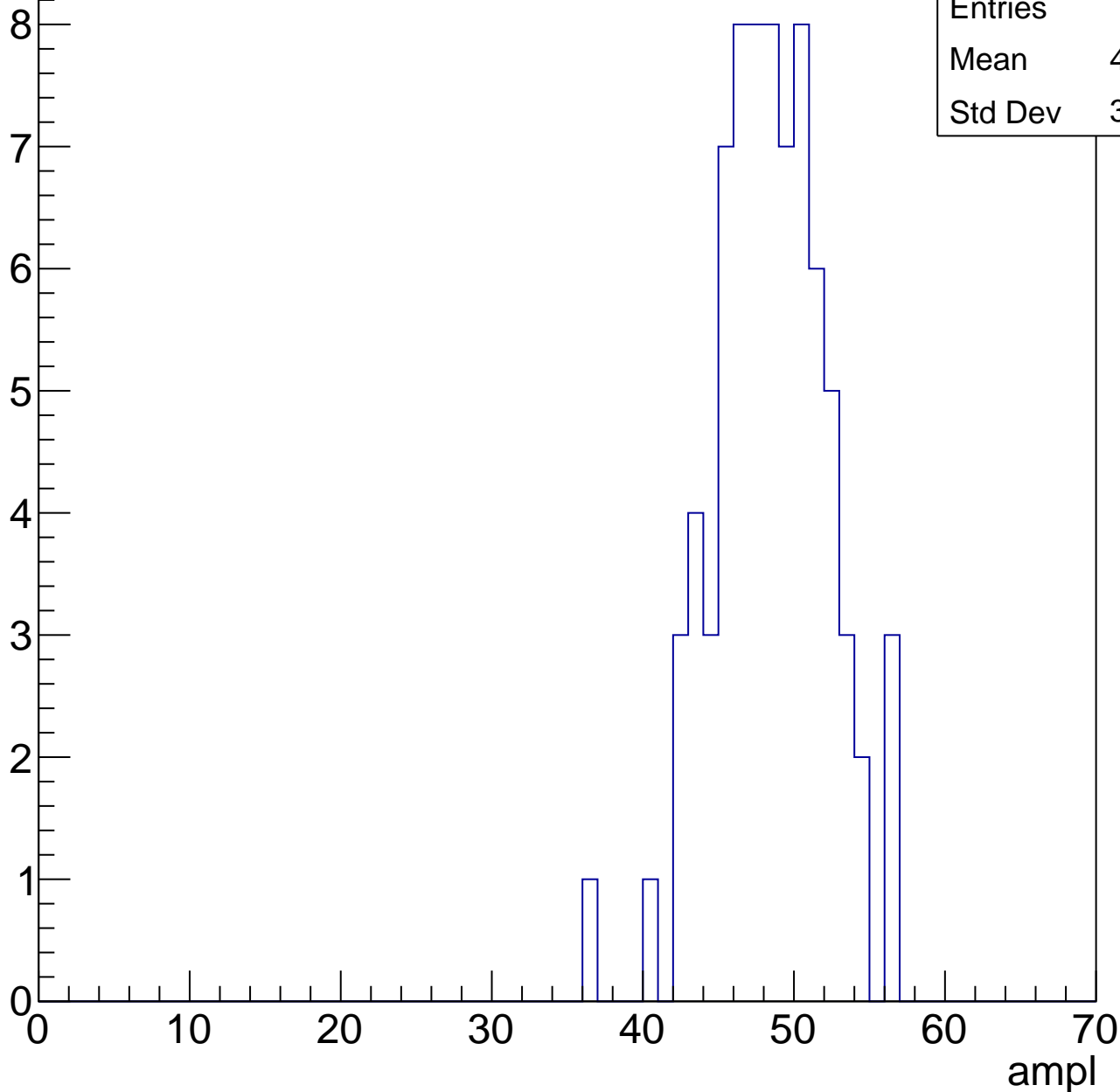


B1L103S, U8-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.96
Std Dev	3.743

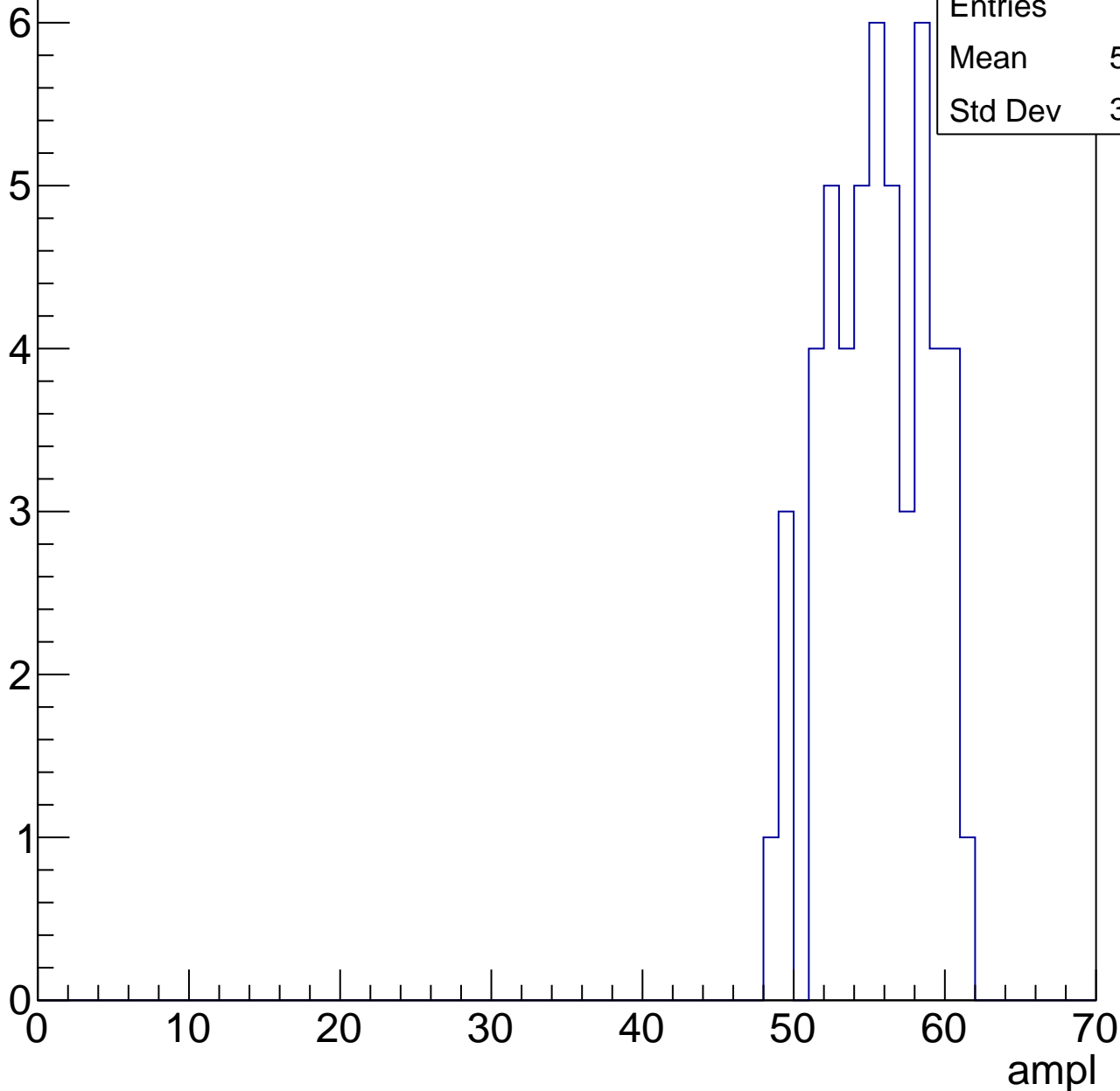


B1L103S, U8-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	55.04
Std Dev	3.308

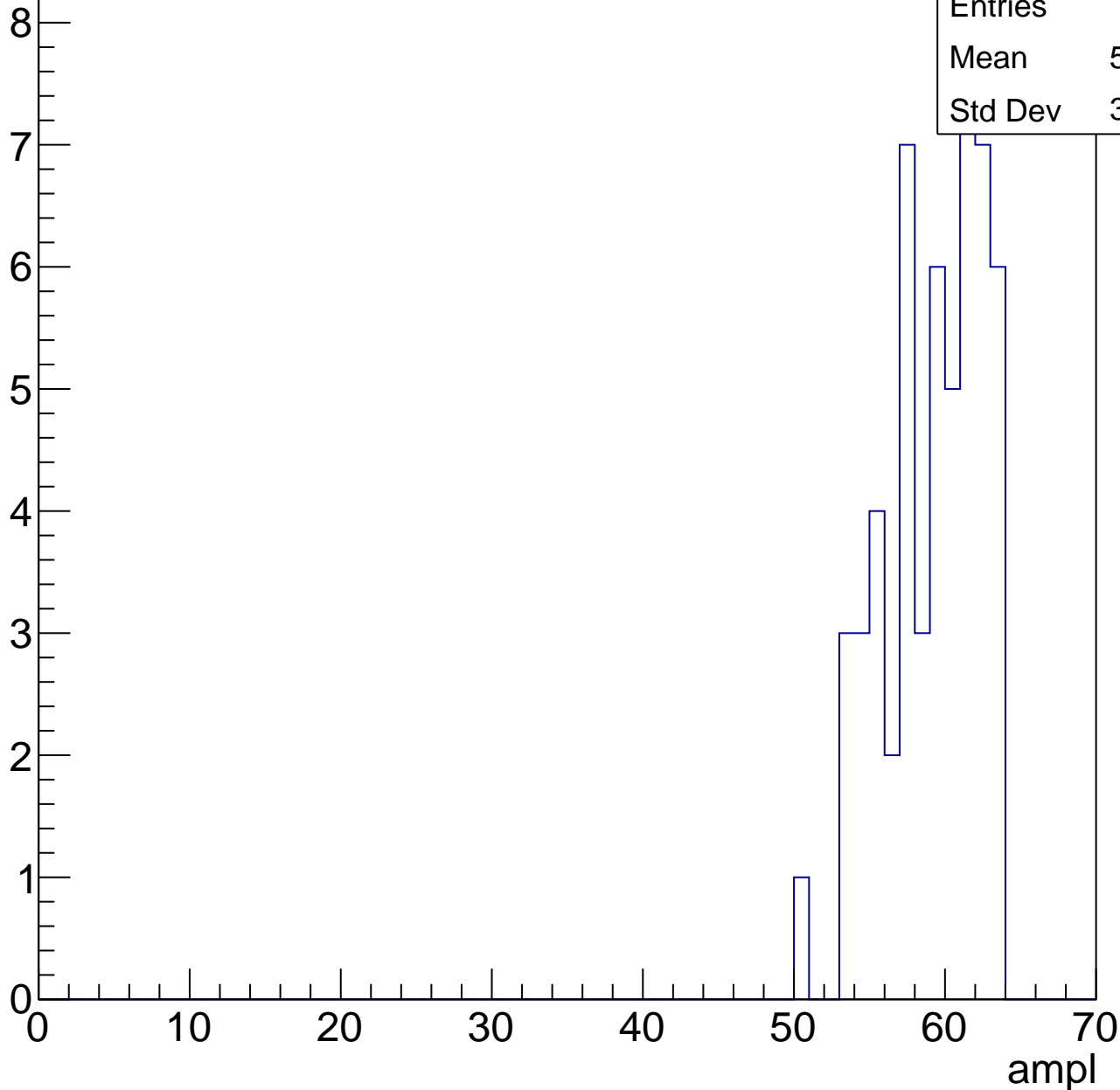


B1L103S, U8-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

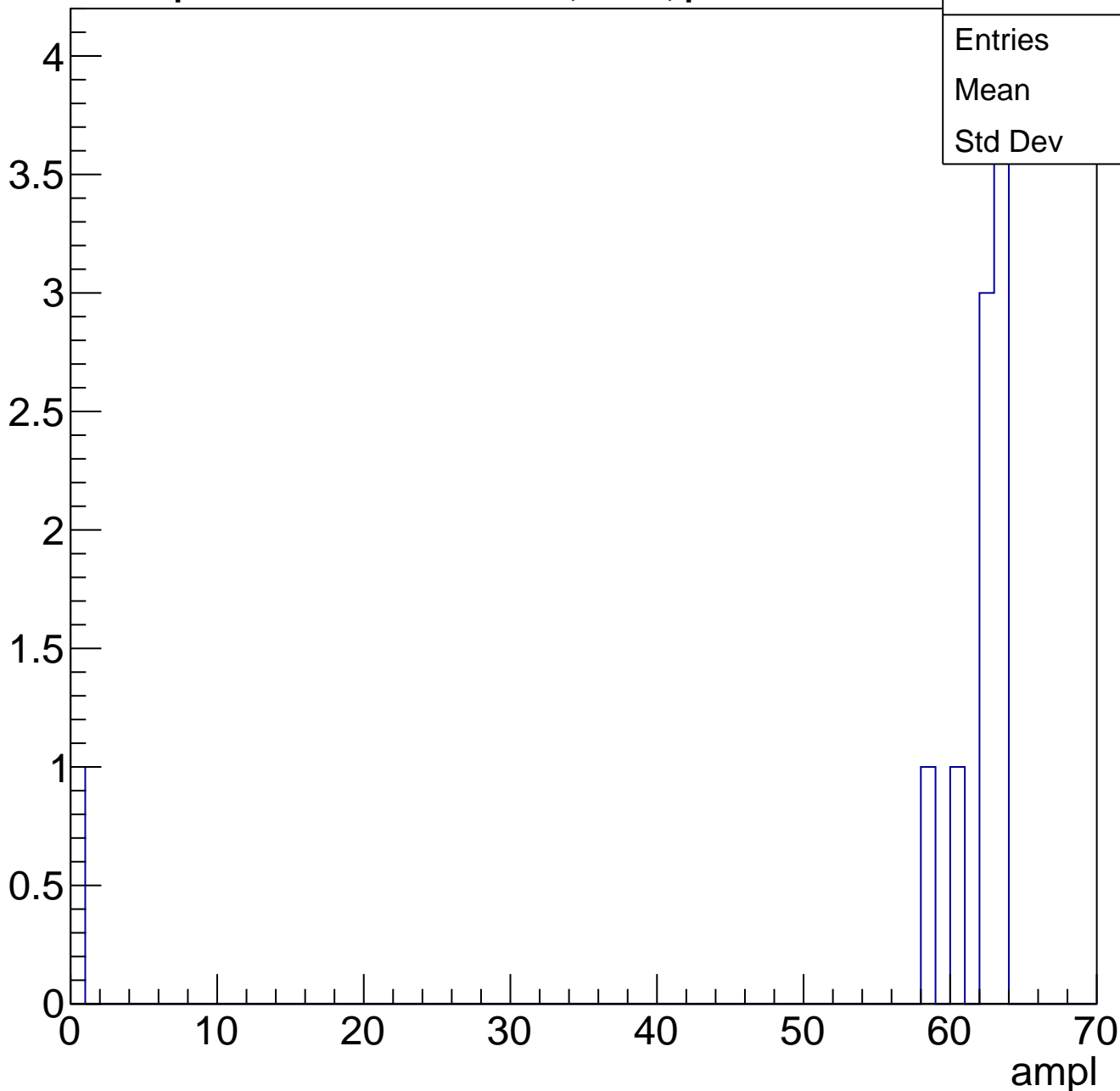
Entries	55
Mean	58.73
Std Dev	3.216



B1L103S, U8-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

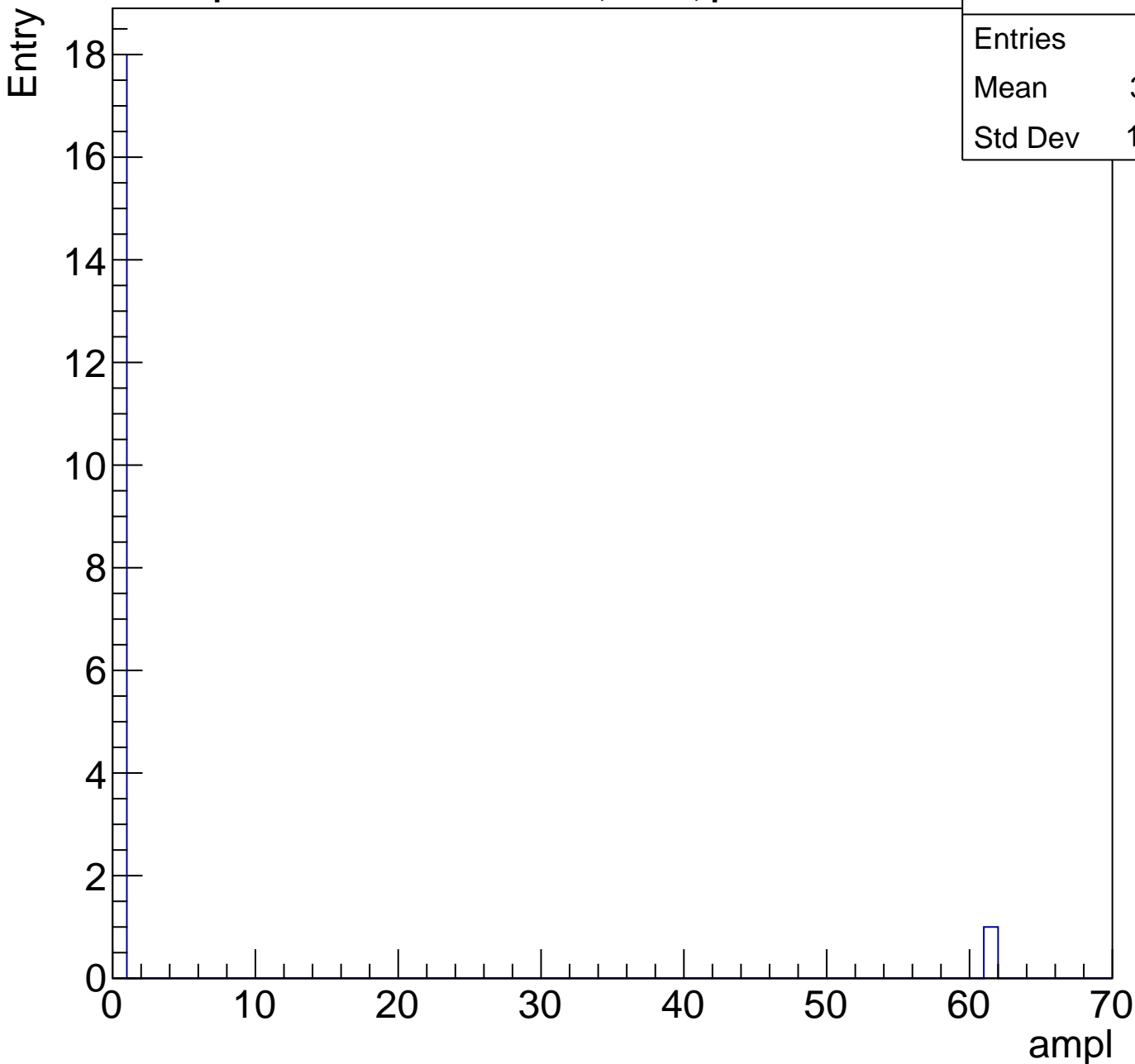
Entry



B1L103S, U8-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.211
Std Dev	13.62



B1L103S, U8-ch81, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	21.83
Std Dev	12.09

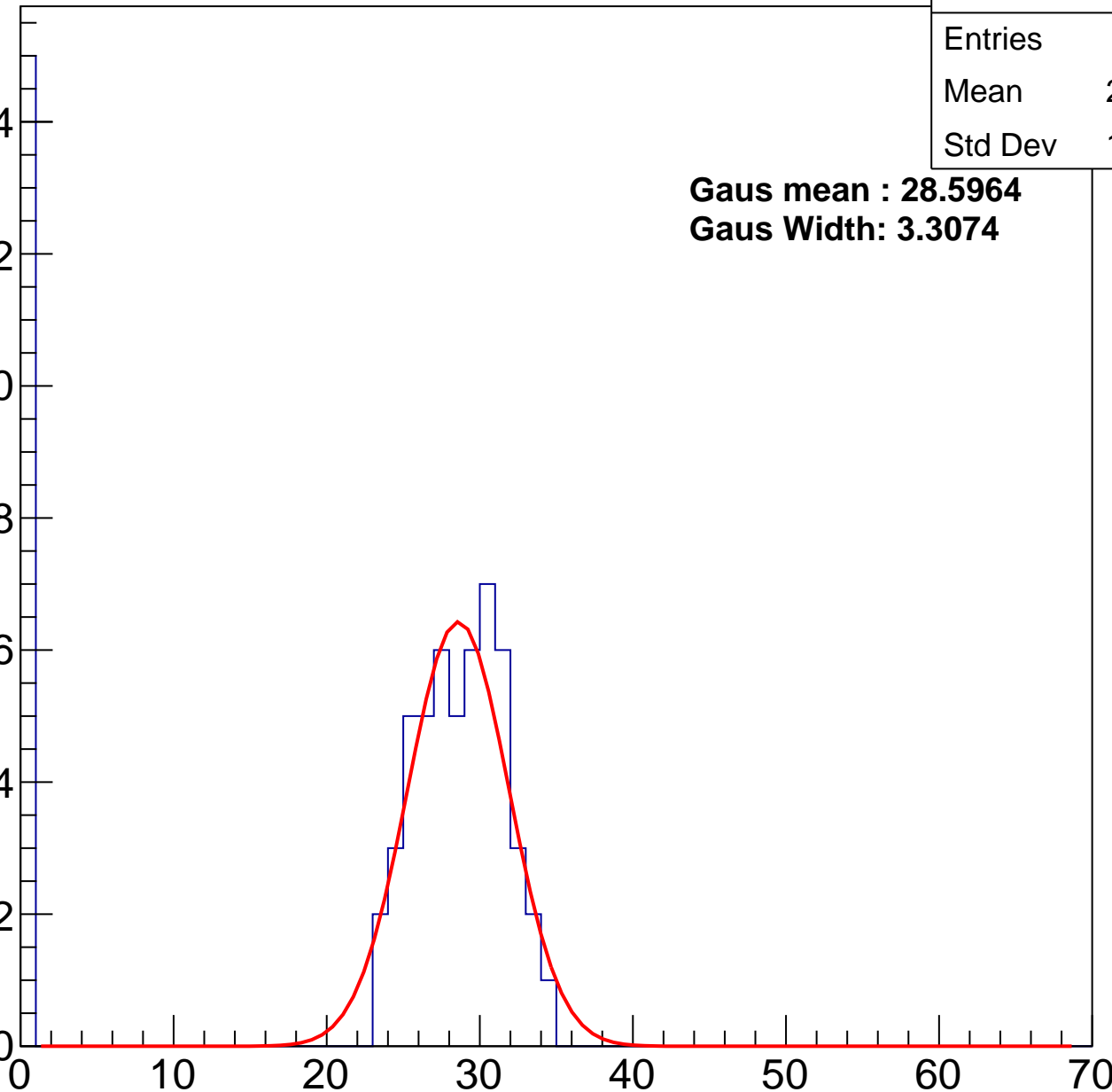
Gaus mean : 28.5964

Gaus Width: 3.3074

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U8-ch81, adc1

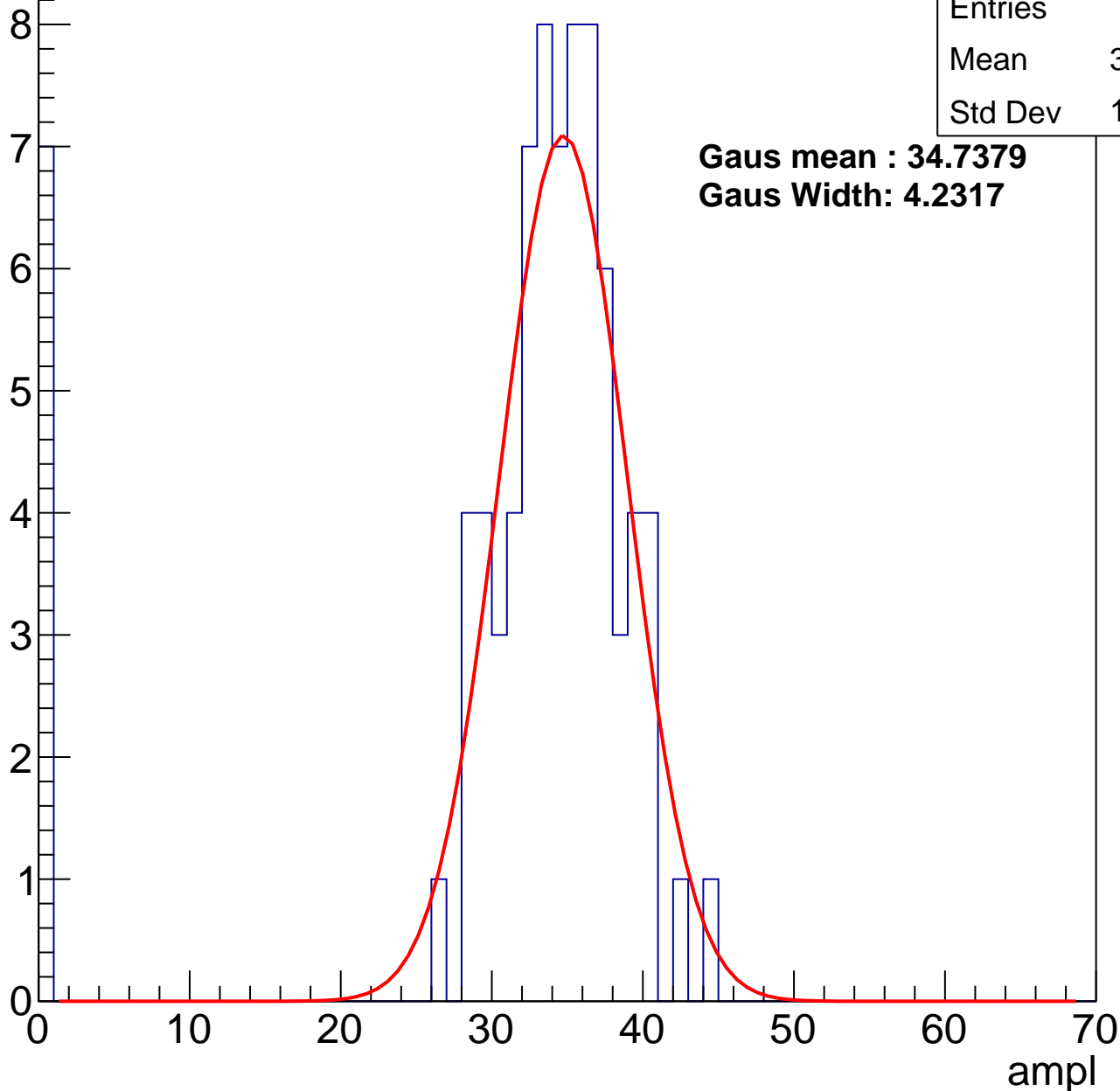
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	31.25
Std Dev	10.29

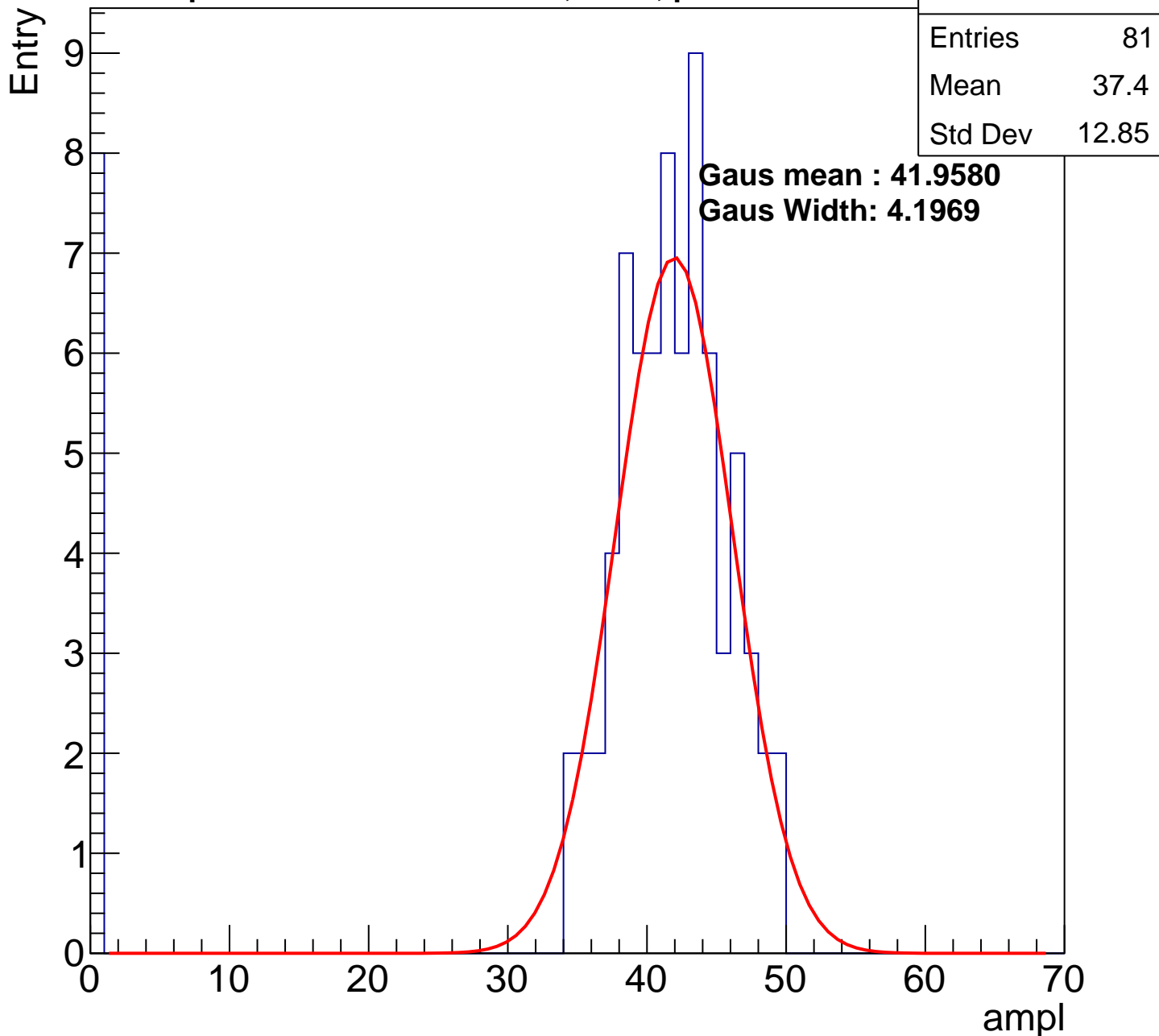
Gaus mean : 34.7379

Gaus Width: 4.2317



B1L103S, U8-ch81, adc2

calib_packv5_041523_1651.root, FC#0, port C2

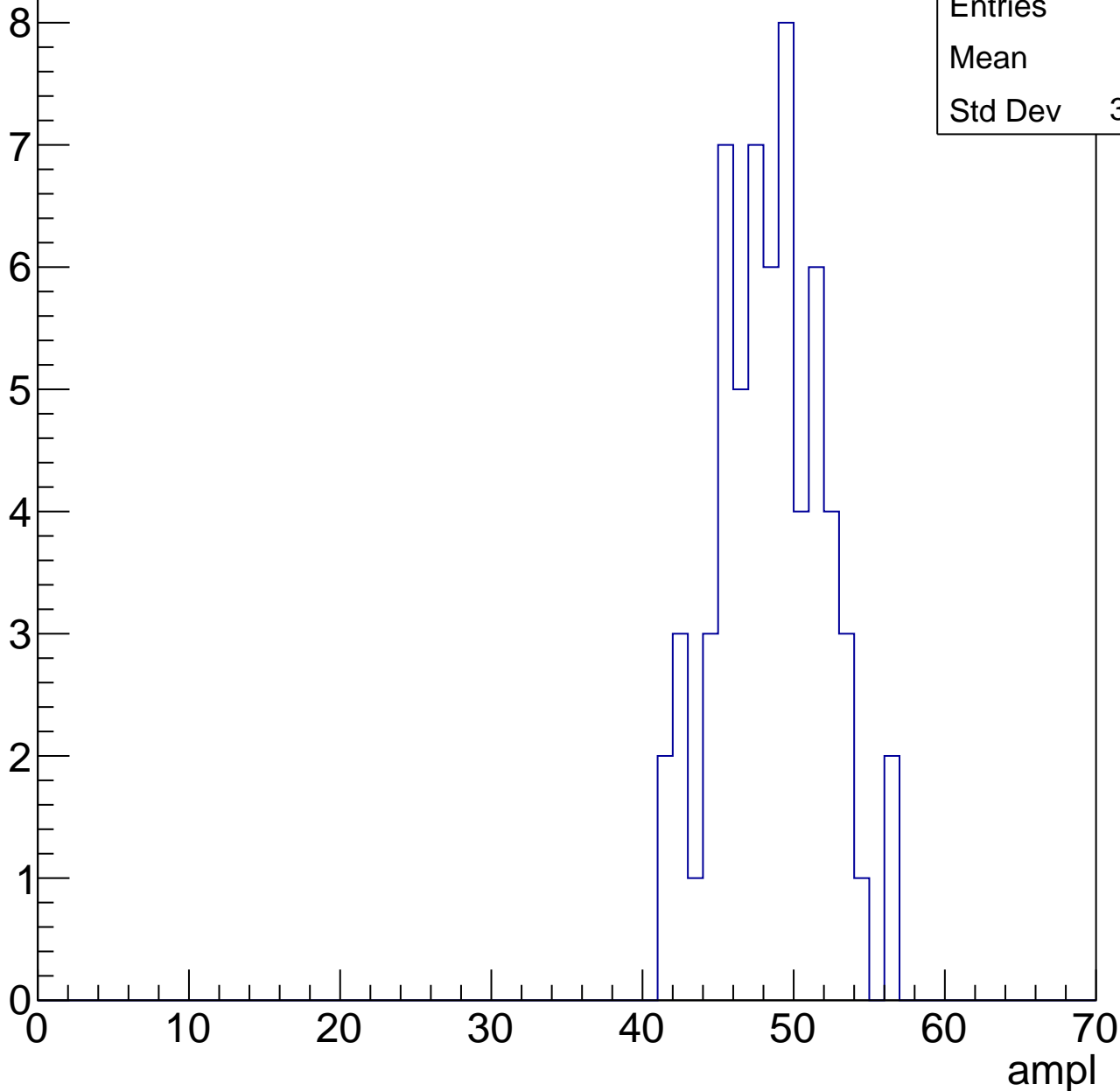


B1L103S, U8-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48
Std Dev	3.478

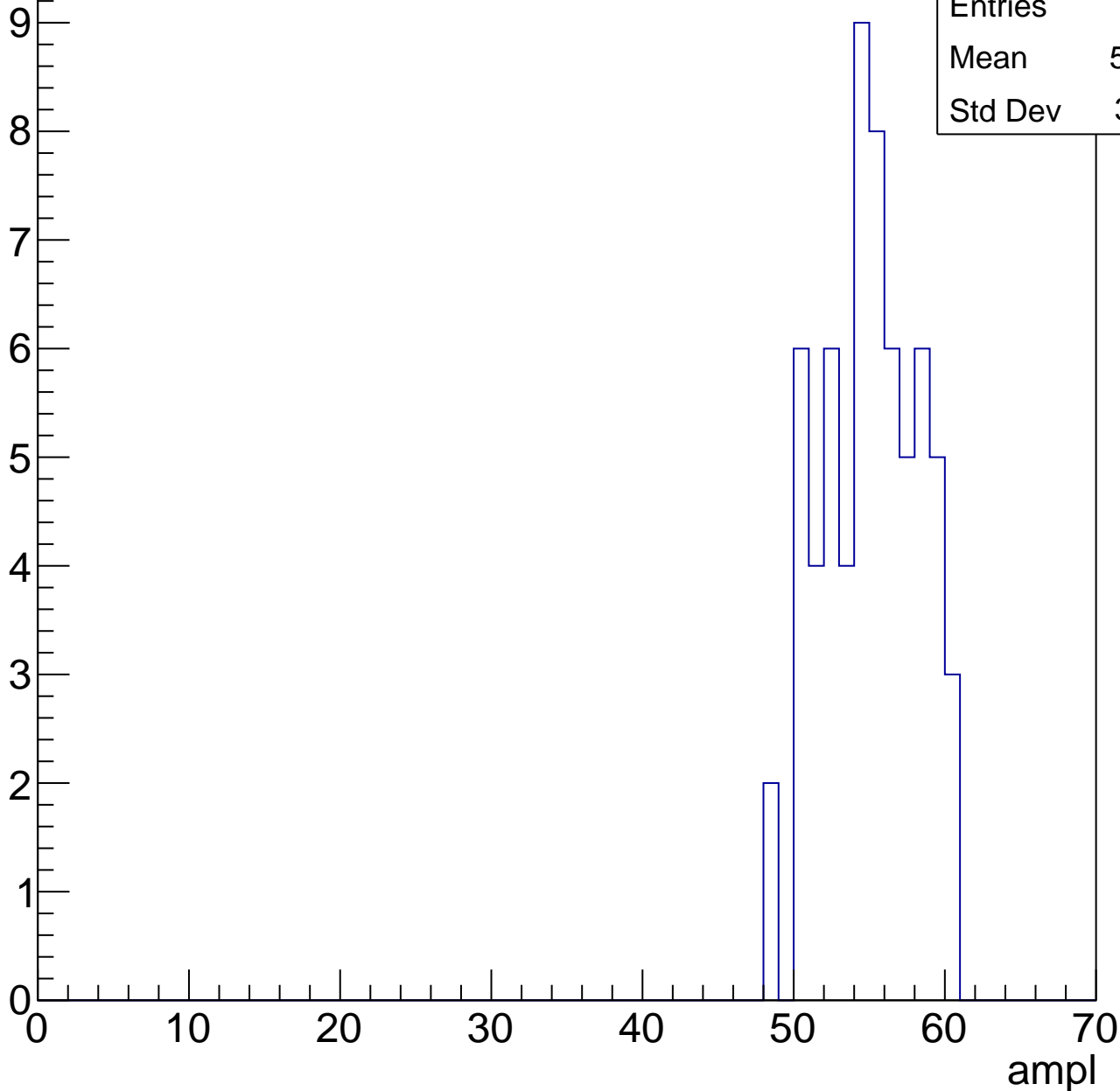


B1L103S, U8-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.59
Std Dev	3.101

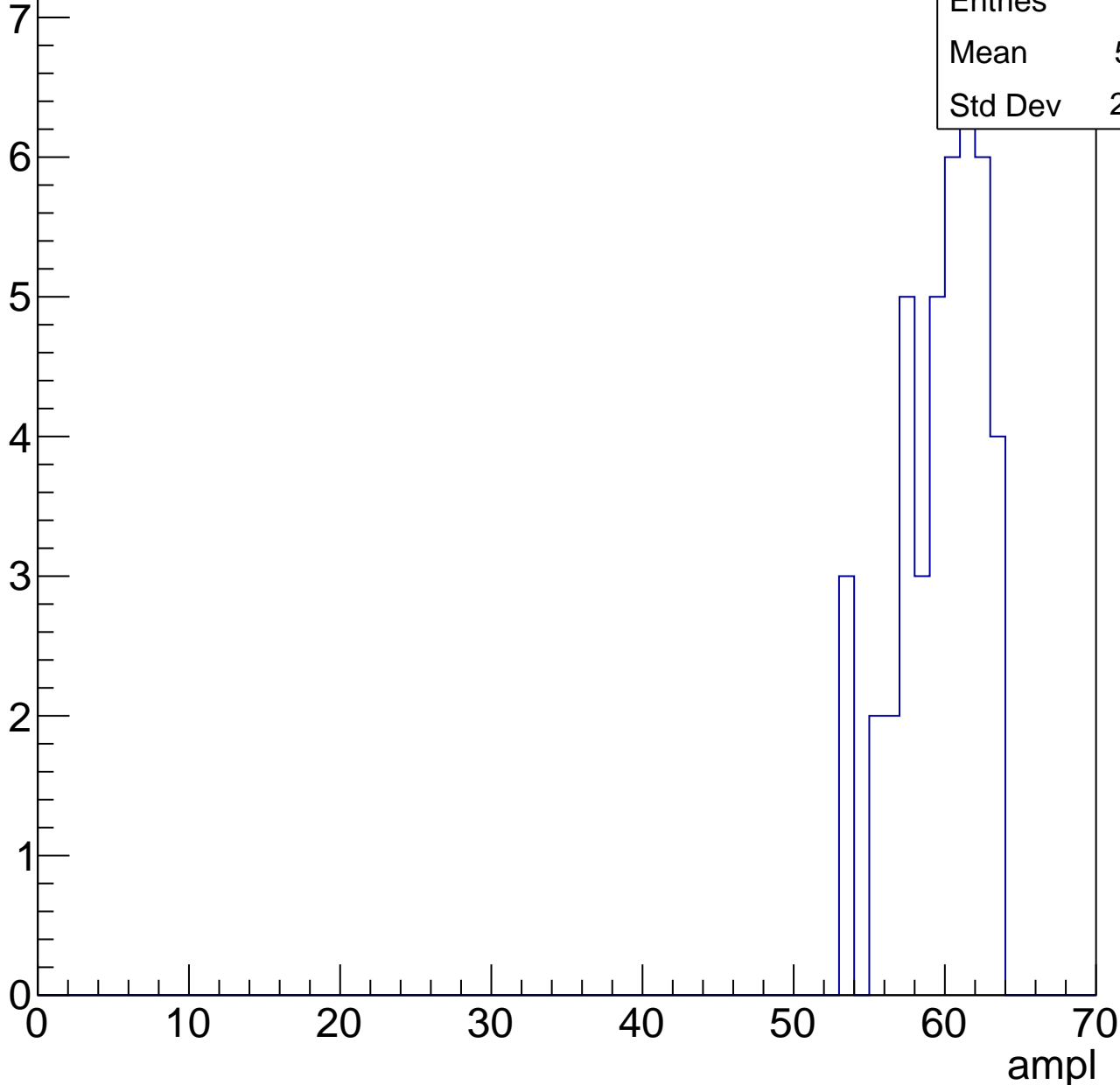


B1L103S, U8-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	59.21
Std Dev	2.775

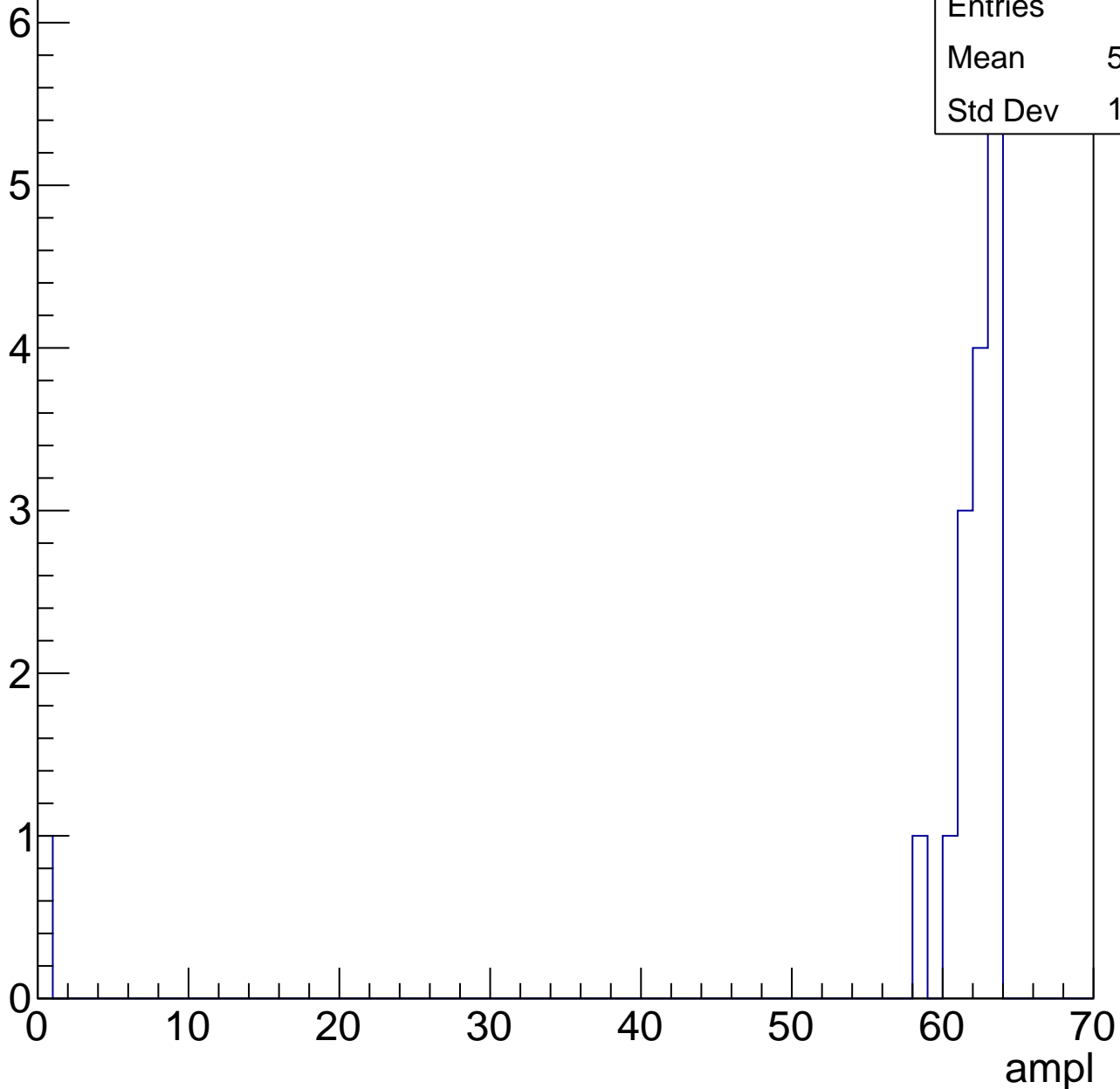


B1L103S, U8-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.94
Std Dev	15.02



B1L103S, U8-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch82, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	134
Mean	15.27
Std Dev	13.72

Gaus mean : 28.9104

Gaus Width: 3.8335

Entry

50

40

30

20

10

0

0

10

20

30

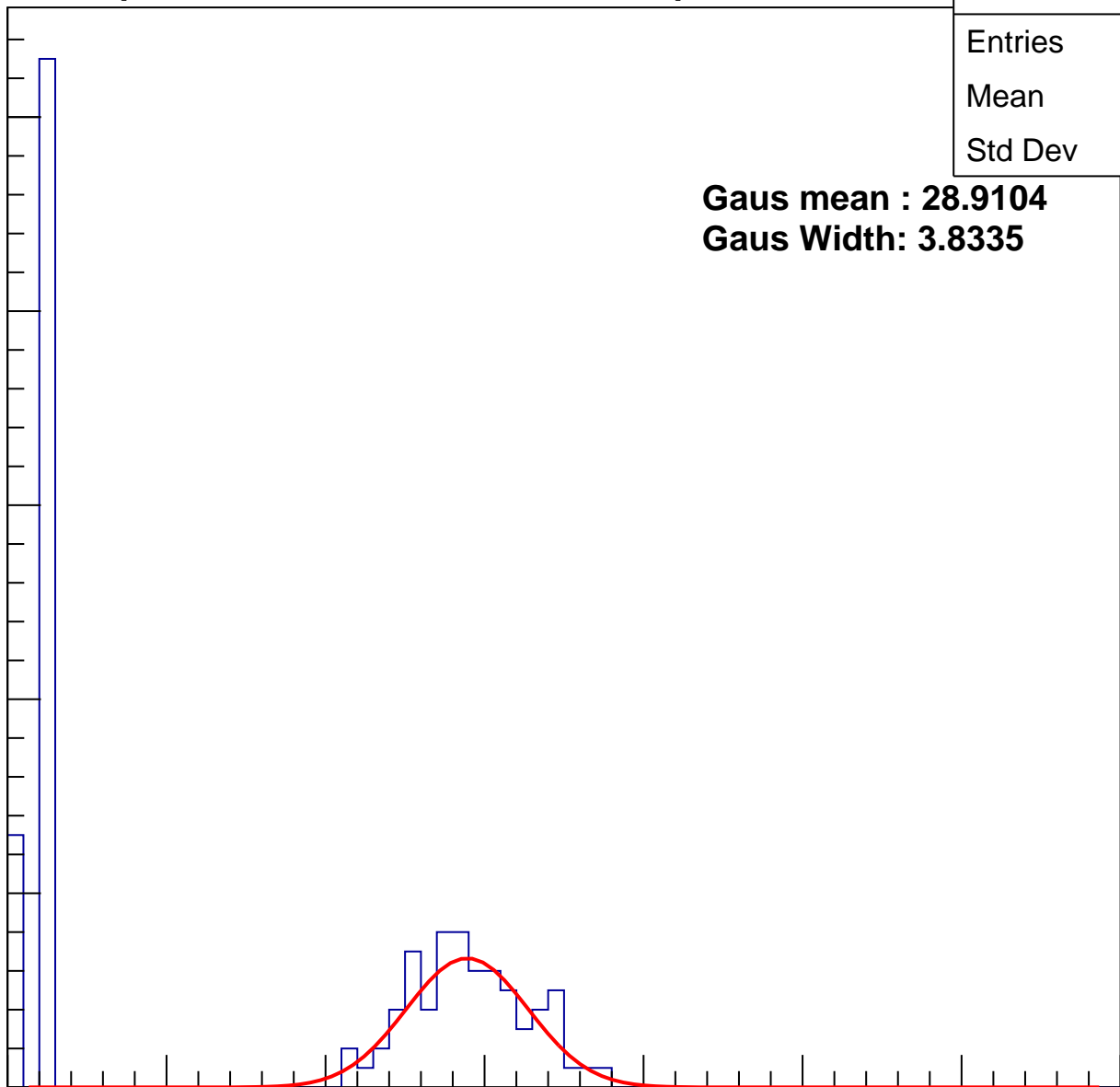
40

50

60

70

ampl



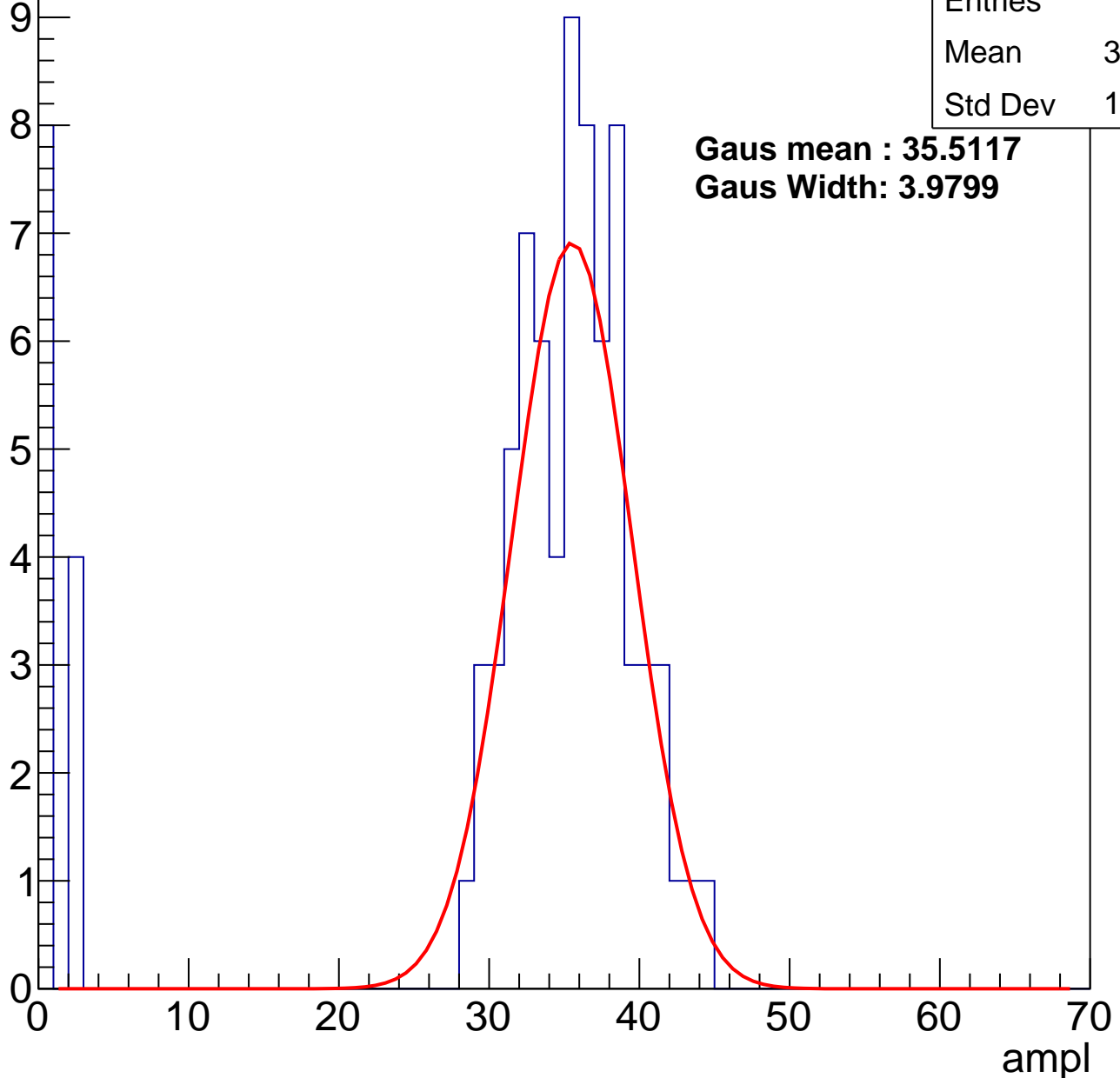
B1L103S, U8-ch82, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	30.29
Std Dev	12.55

Gaus mean : 35.5117
Gaus Width: 3.9799



B1L103S, U8-ch82, adc2

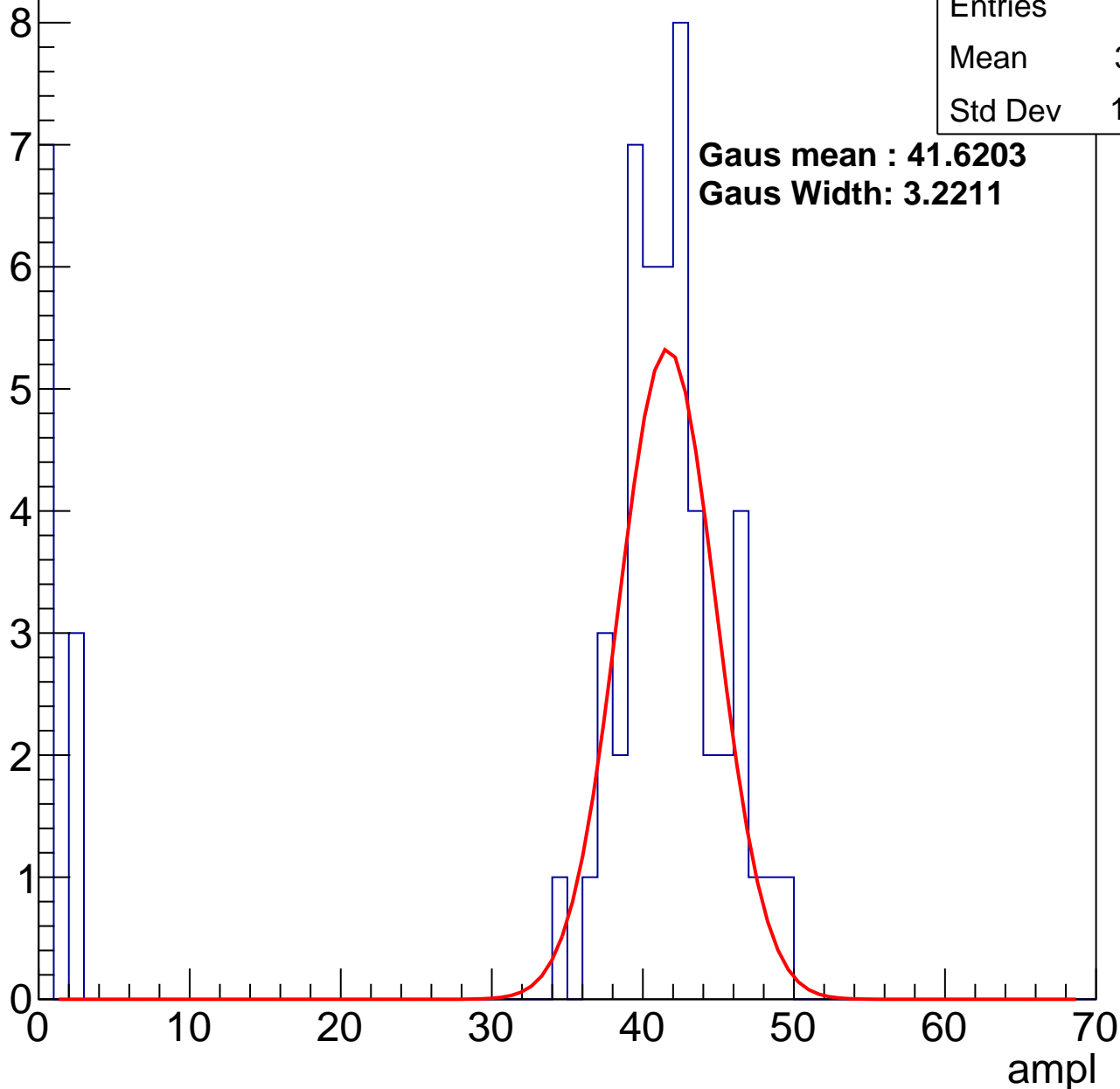
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	34.51
Std Dev	15.59

Gaus mean : 41.6203

Gaus Width: 3.2211

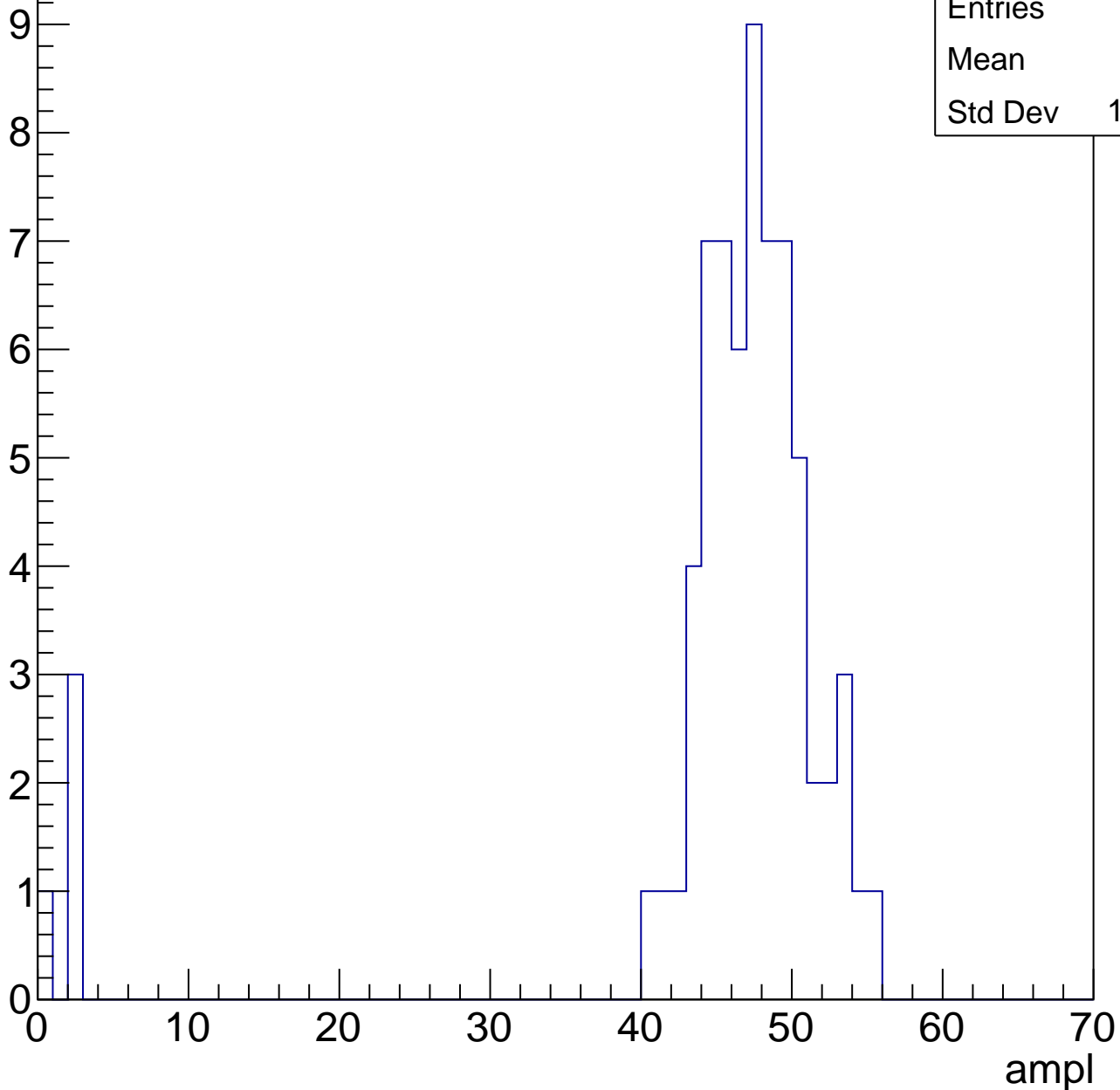


B1L103S, U8-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	44.5
Std Dev	11.19

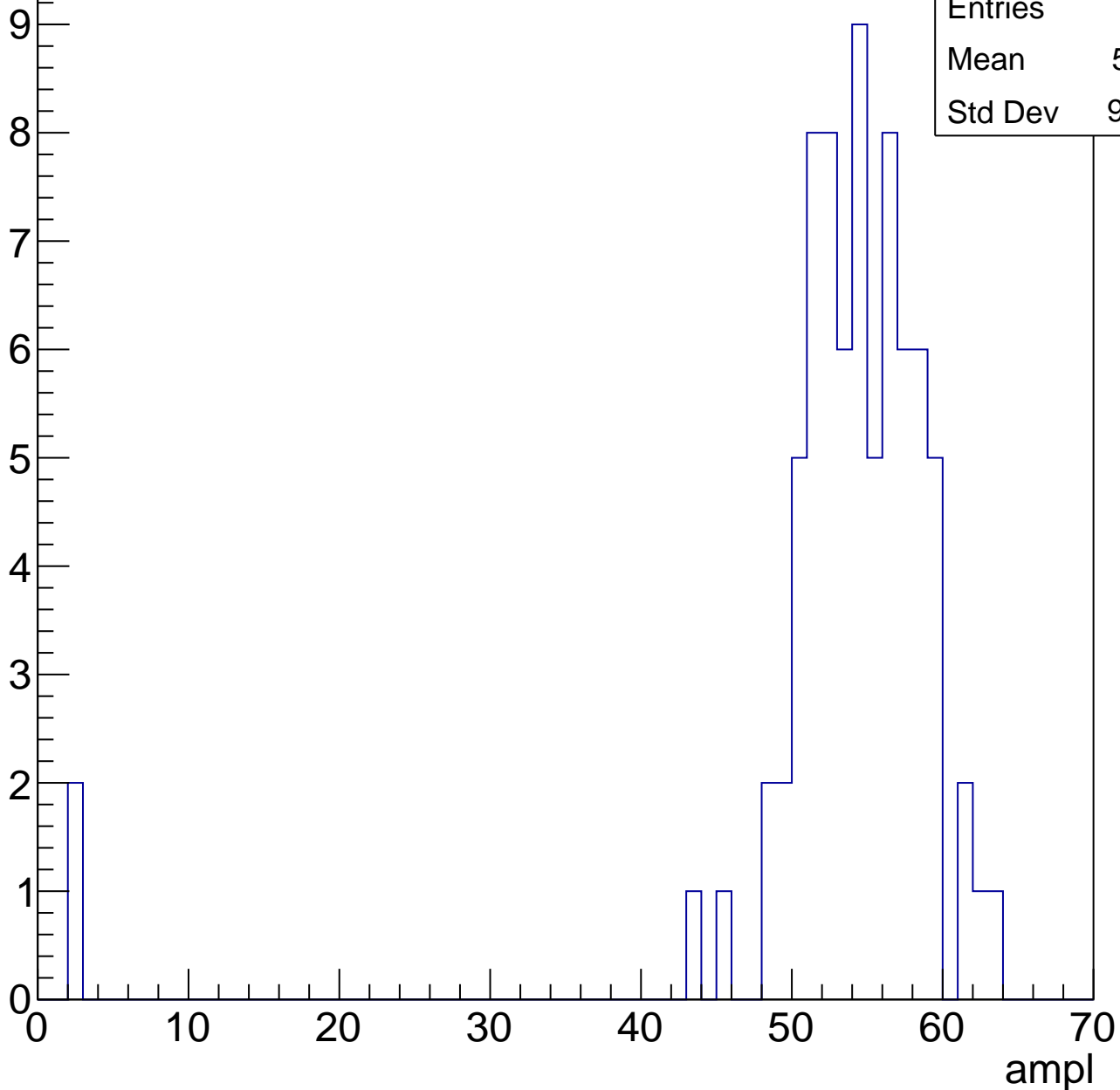


B1L103S, U8-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	52.81
Std Dev	9.034

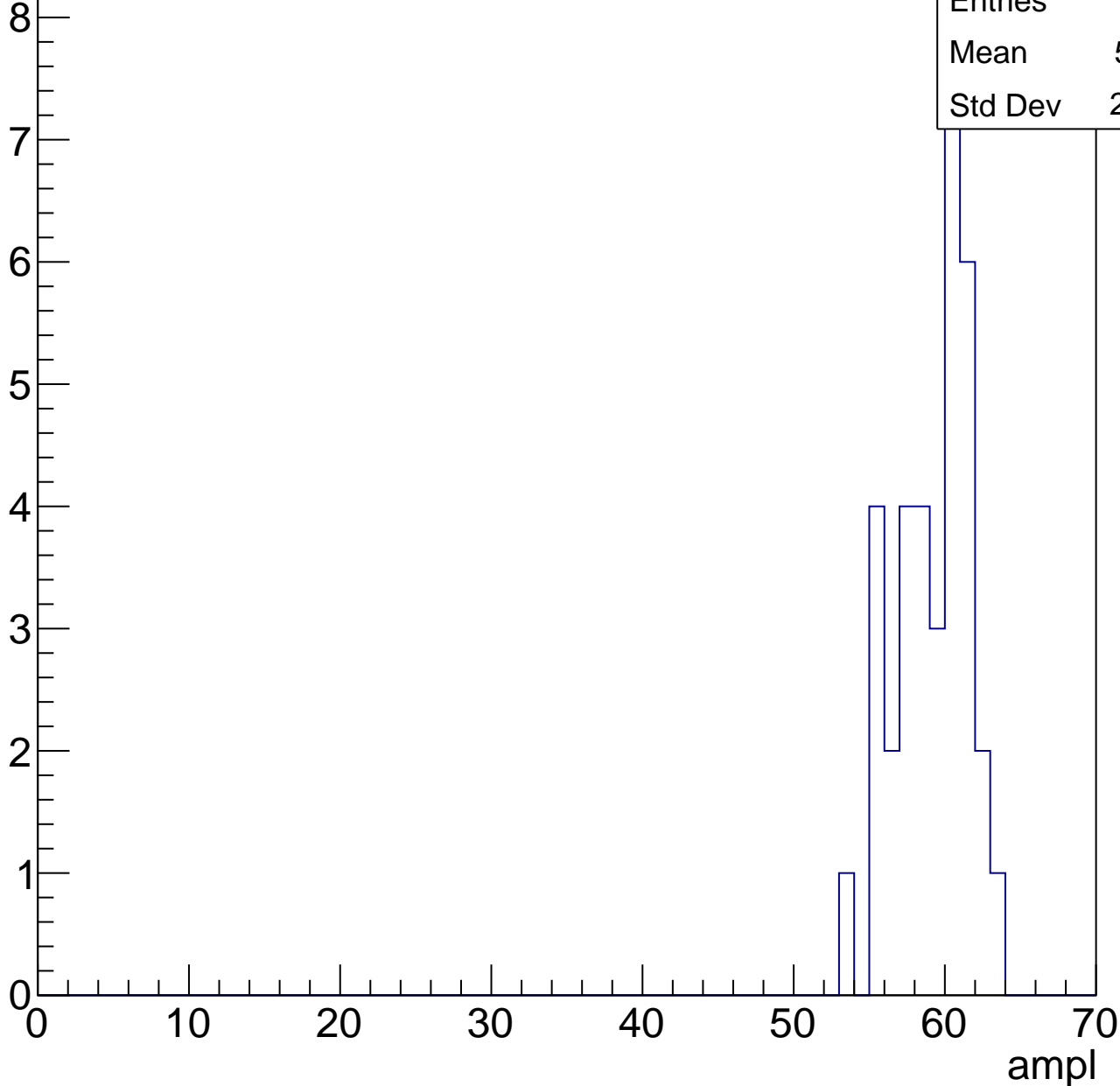


B1L103S, U8-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	58.71
Std Dev	2.397

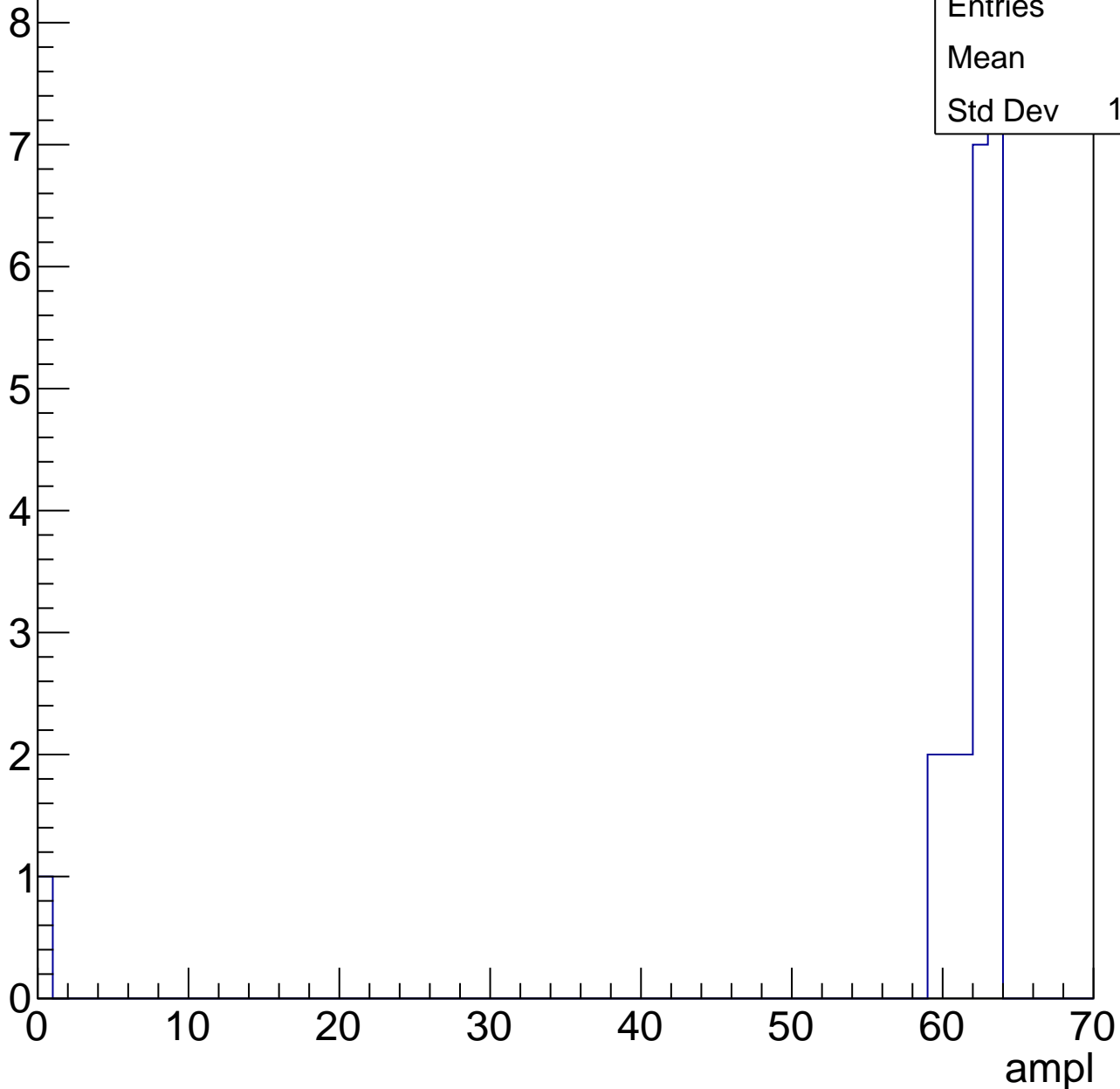


B1L103S, U8-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

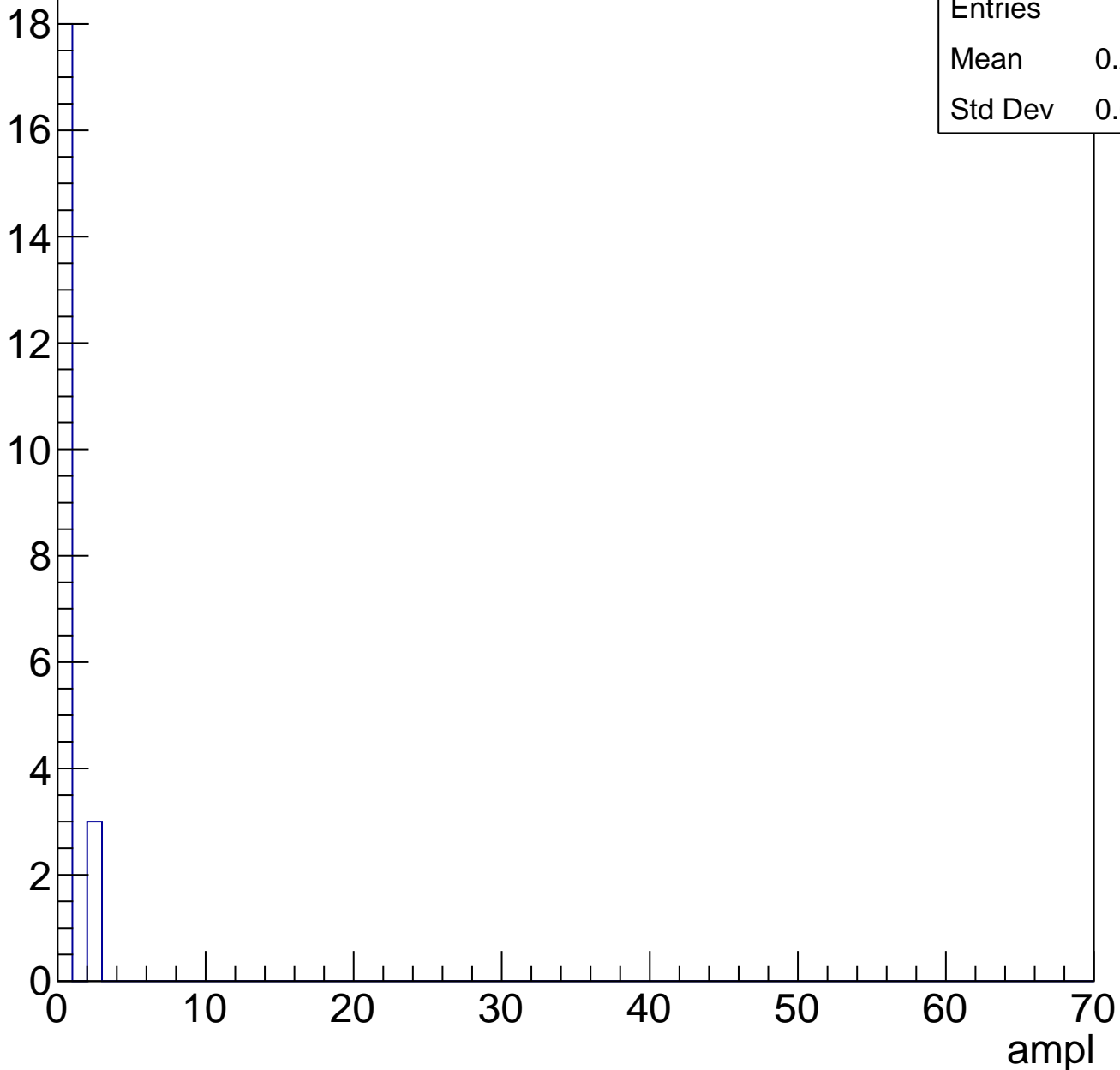
Entries	22
Mean	59
Std Dev	12.94



B1L103S, U8-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	0.2857
Std Dev	0.6999

B1L103S, U8-ch83, adc0

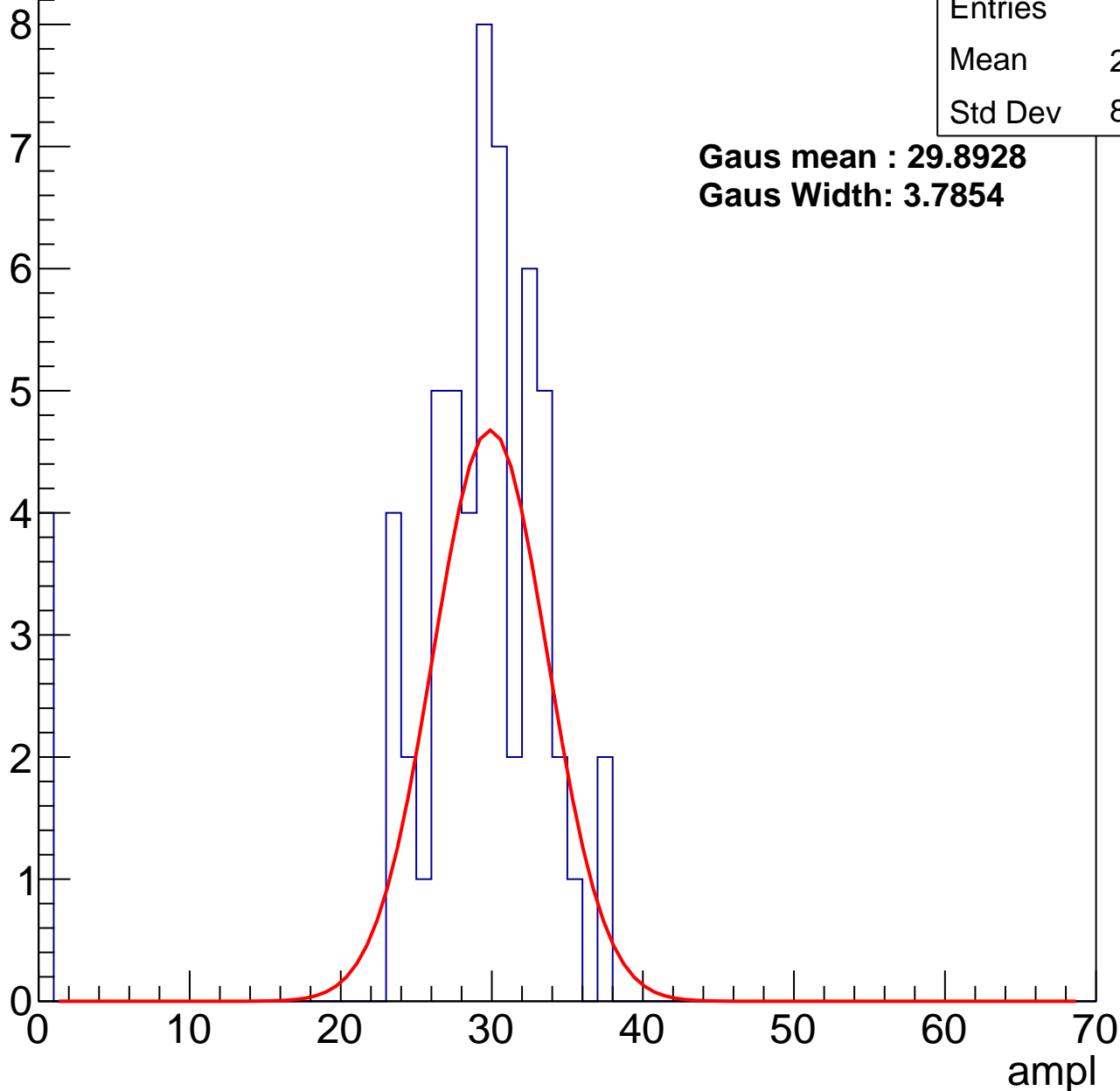
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	27.24
Std Dev	8.123

Gaus mean : 29.8928

Gaus Width: 3.7854



B1L103S, U8-ch83, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	31.91
Std Dev	11.97

Gaus mean : 36.5757

Gaus Width: 3.8488

Entry

10

8

6

4

2

0

0

10

20

30

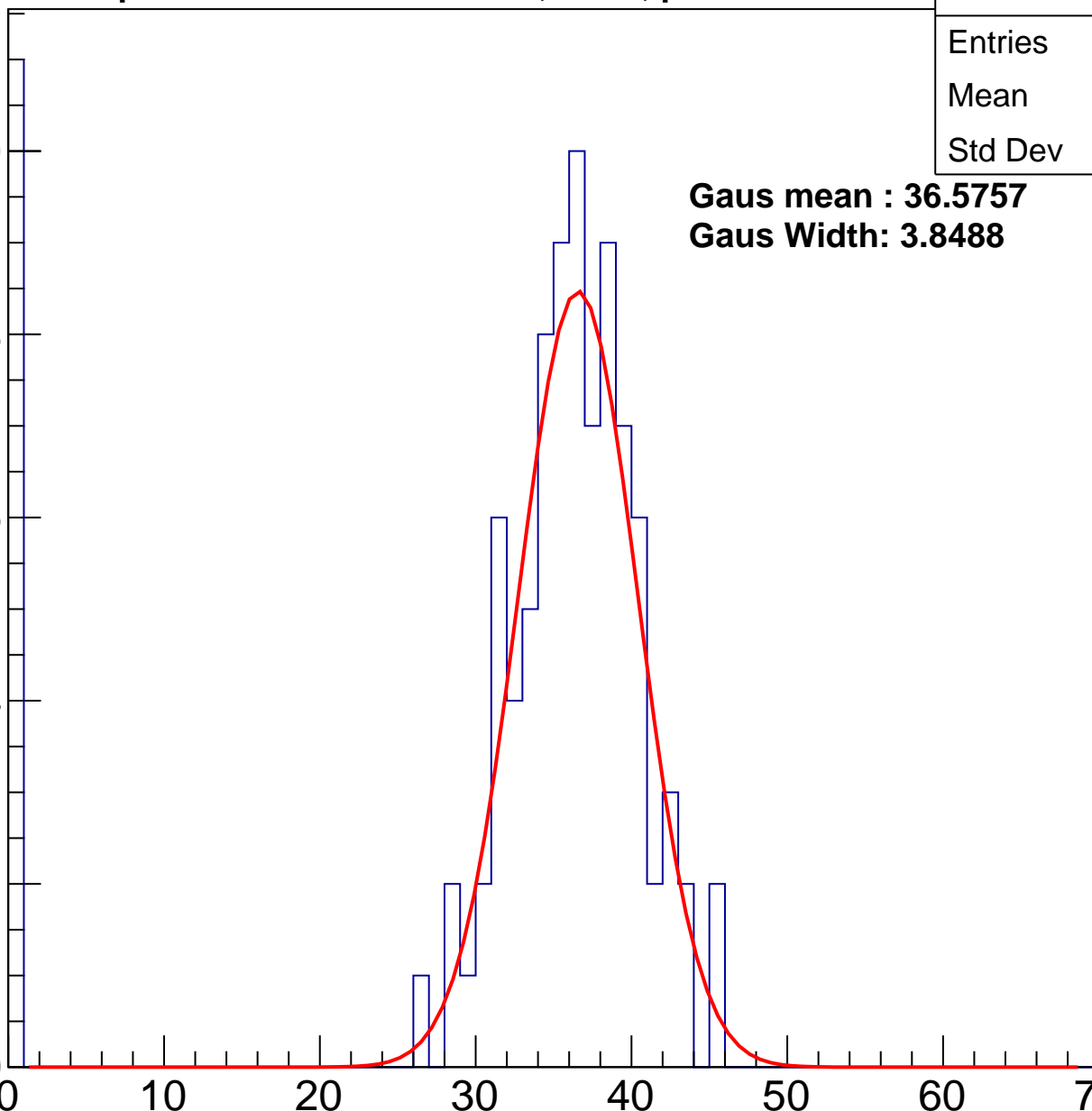
40

50

60

70

ampl



B1L103S, U8-ch83, adc2

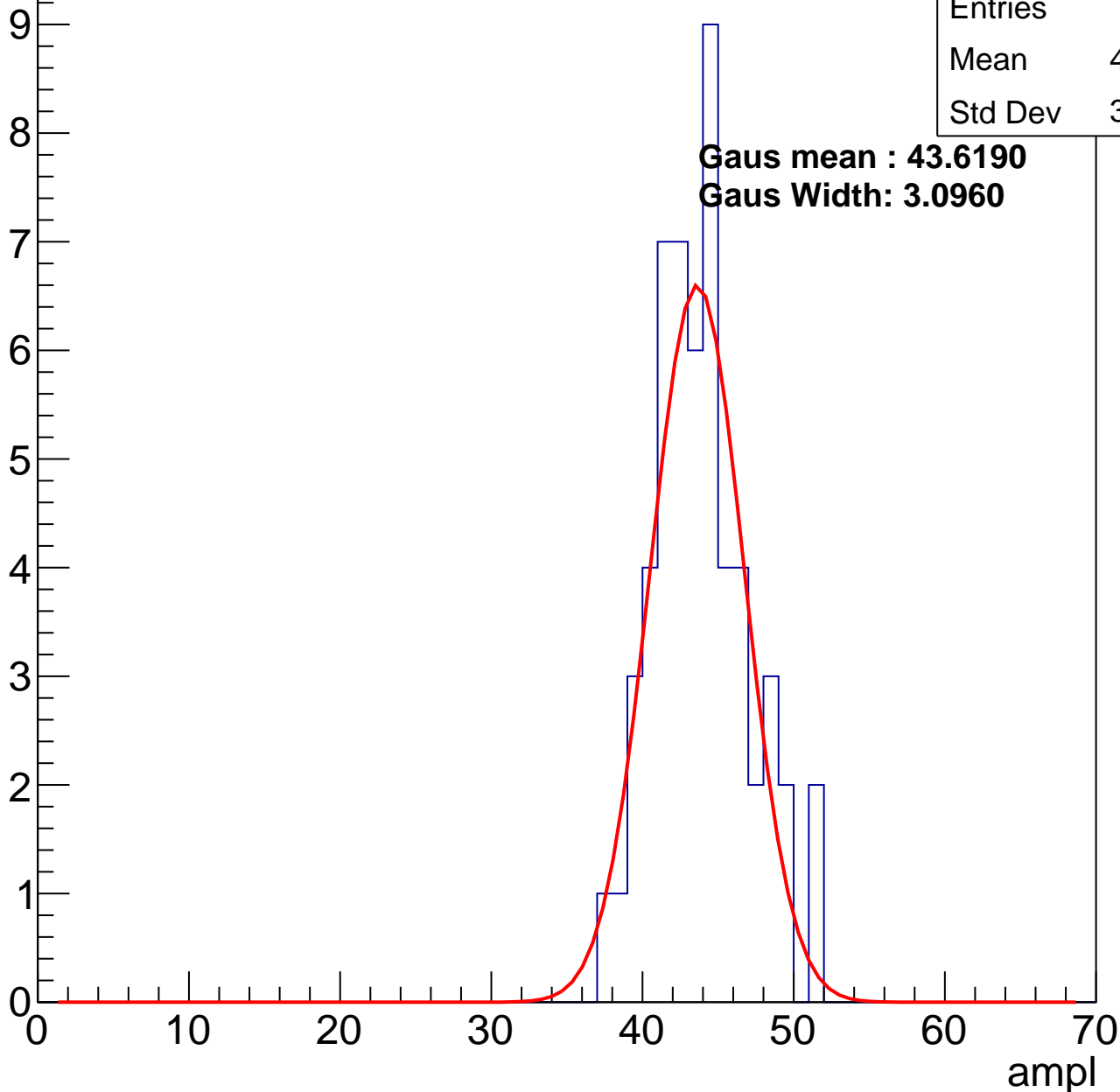
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	43.44
Std Dev	3.126

Gaus mean : 43.6190

Gaus Width: 3.0960

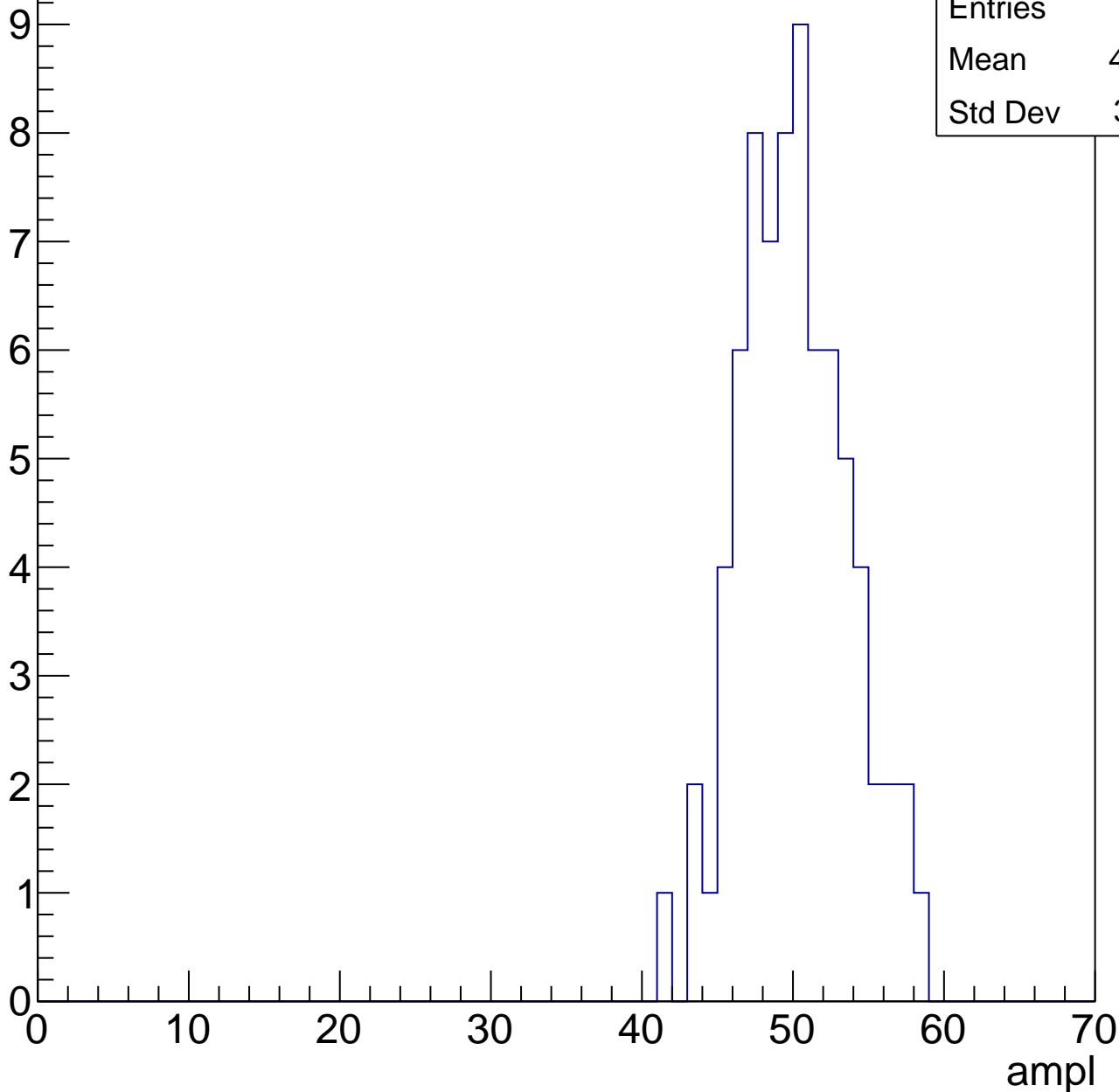


B1L103S, U8-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	49.65
Std Dev	3.551

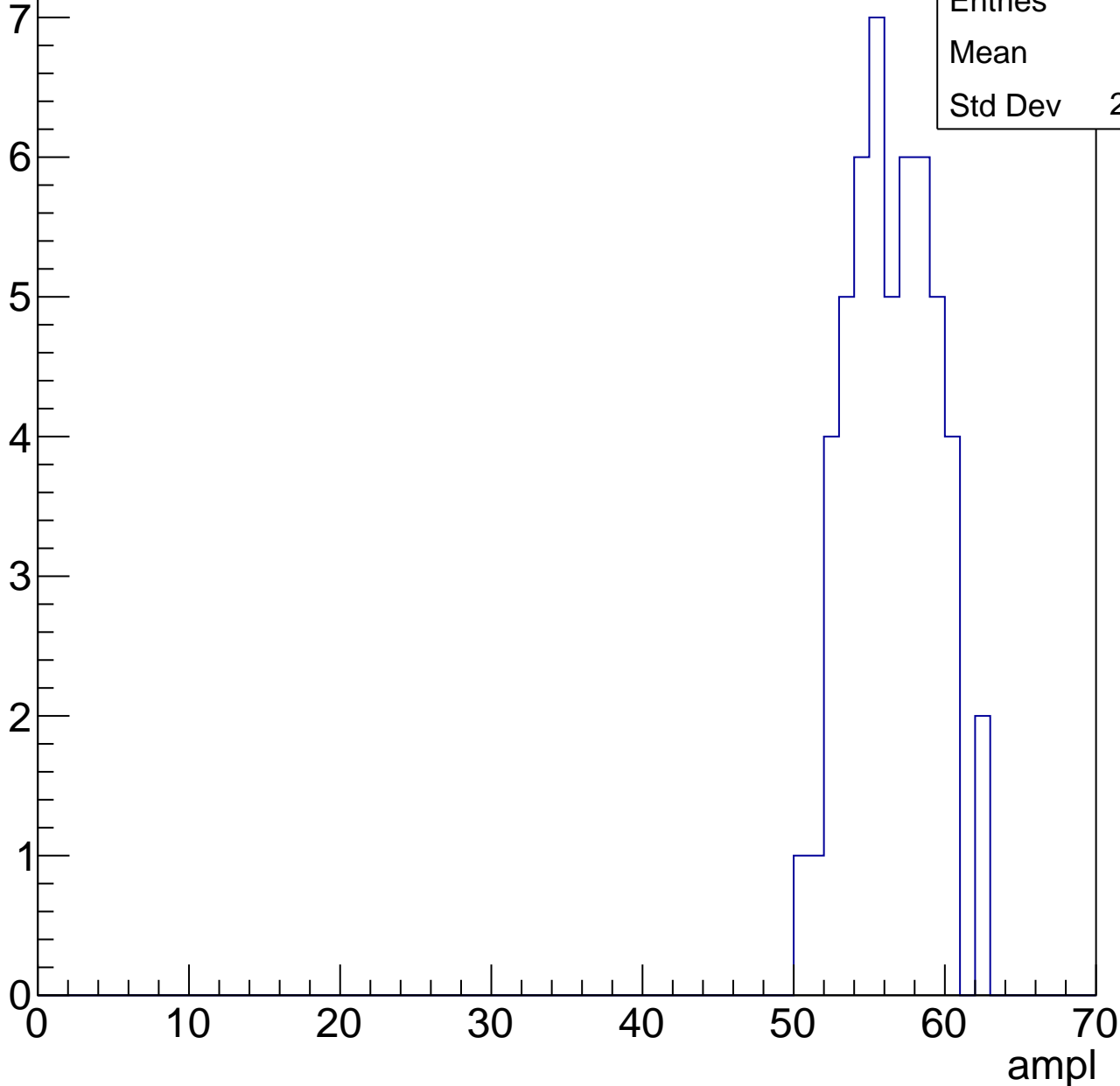


B1L103S, U8-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	56
Std Dev	2.815

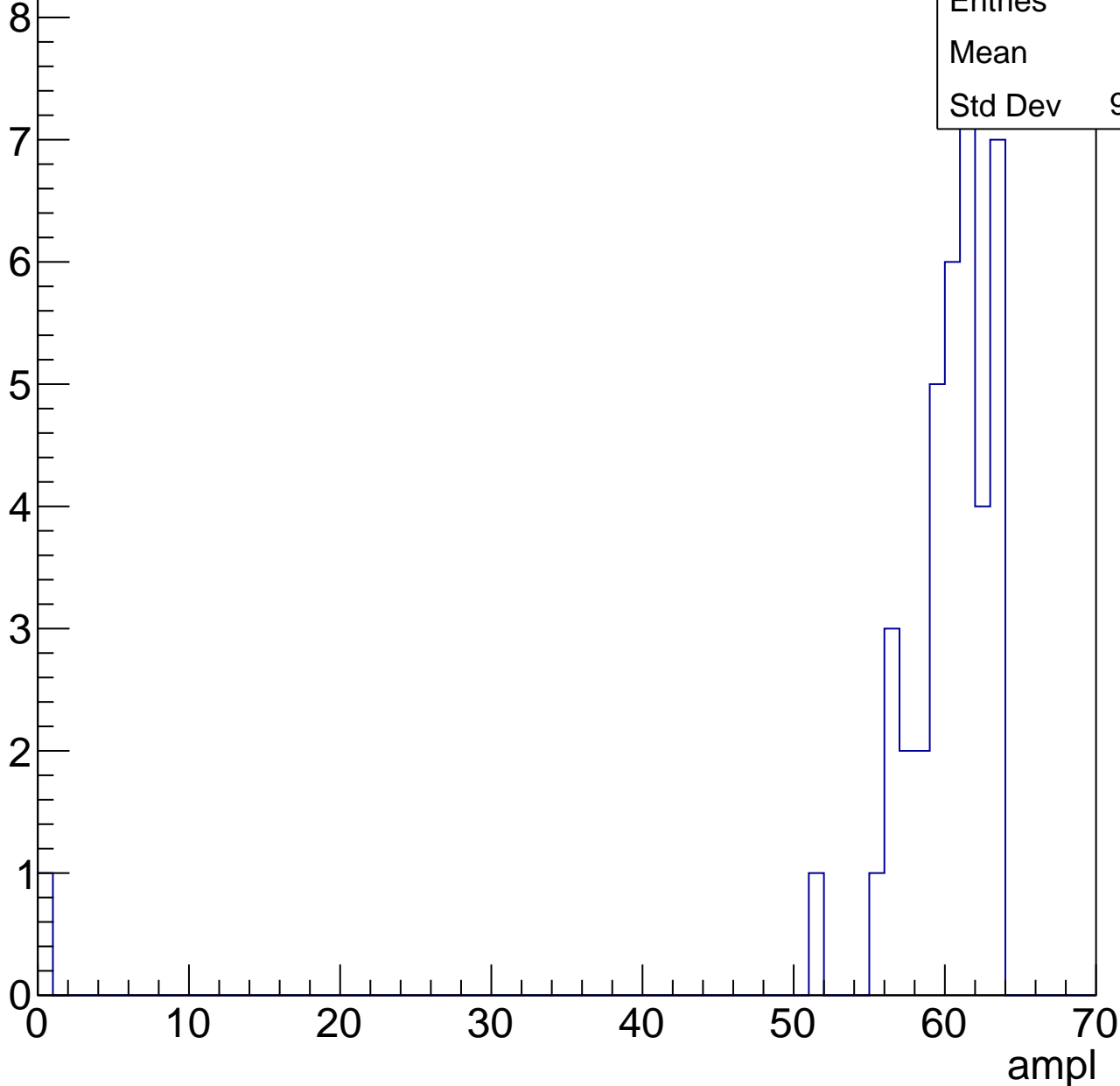


B1L103S, U8-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

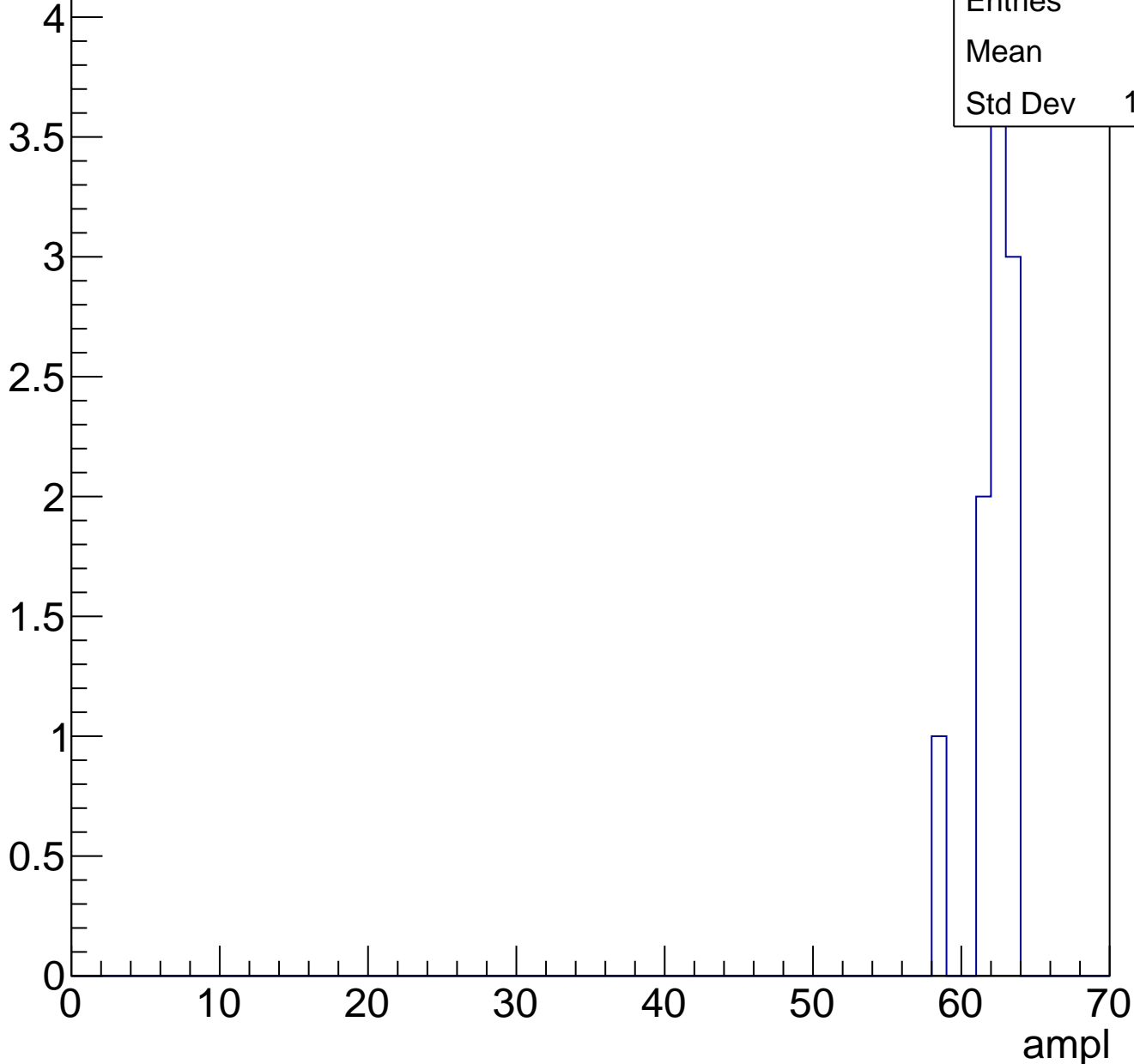
Entries	40
Mean	58.4
Std Dev	9.708



B1L103S, U8-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch84, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	22.96
Std Dev	10.63

Gaus mean : 28.1586

Gaus Width: 3.6612

Entry

12

10

8

6

4

2

0

0

10

20

30

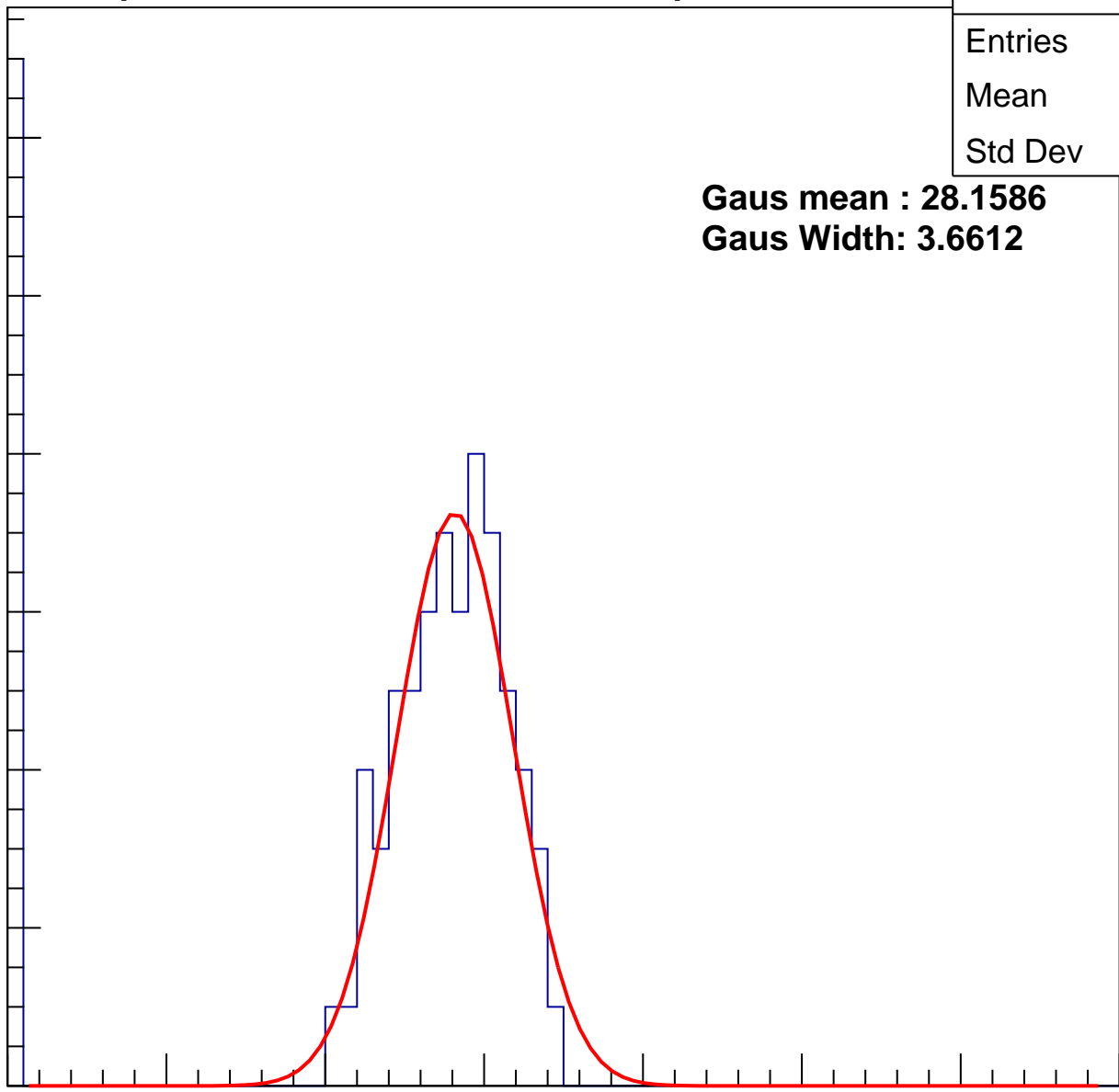
40

50

60

70

ampl



B1L103S, U8-ch84, adc1

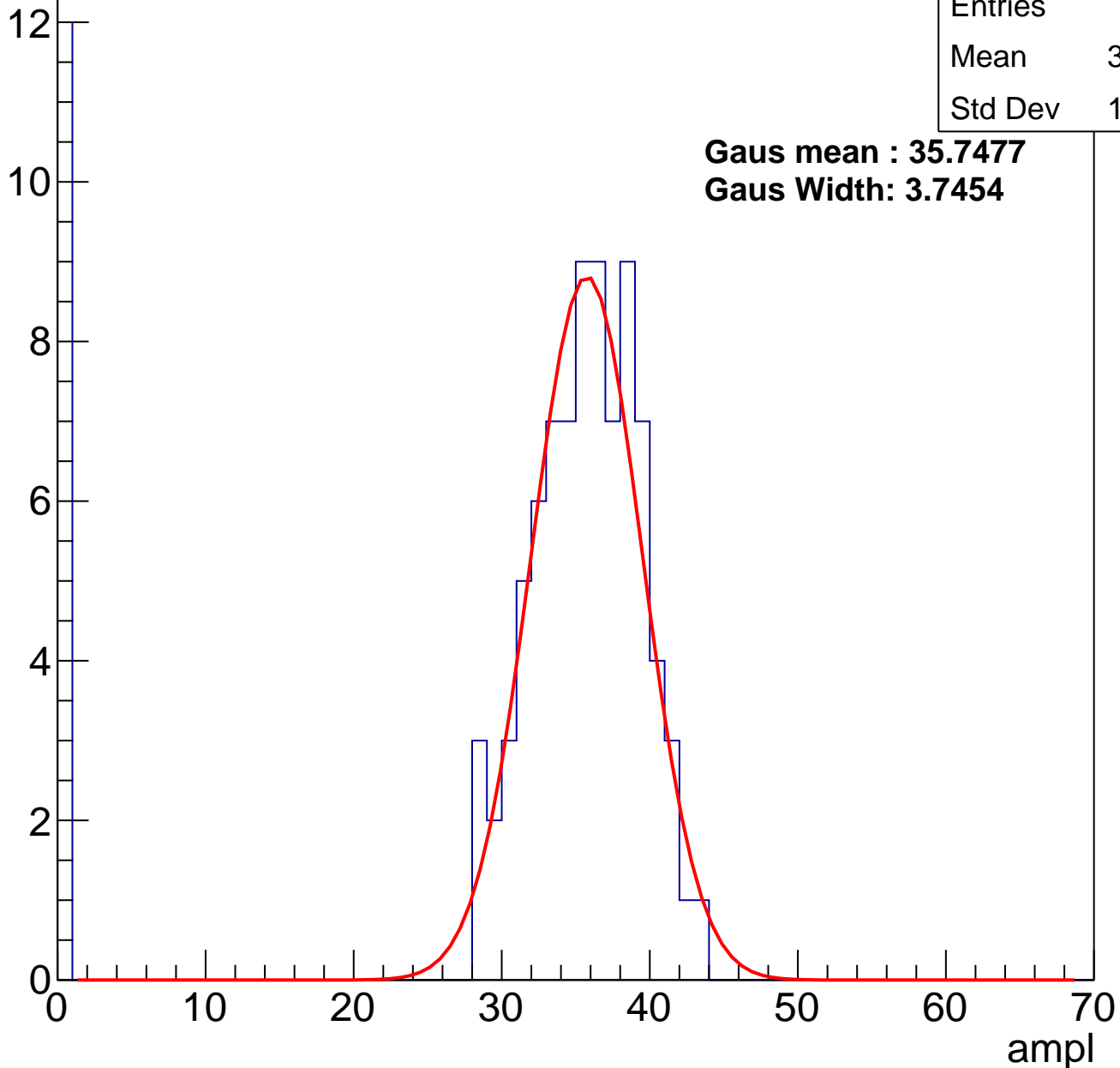
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	30.83
Std Dev	12.17

Gaus mean : 35.7477

Gaus Width: 3.7454

Entry

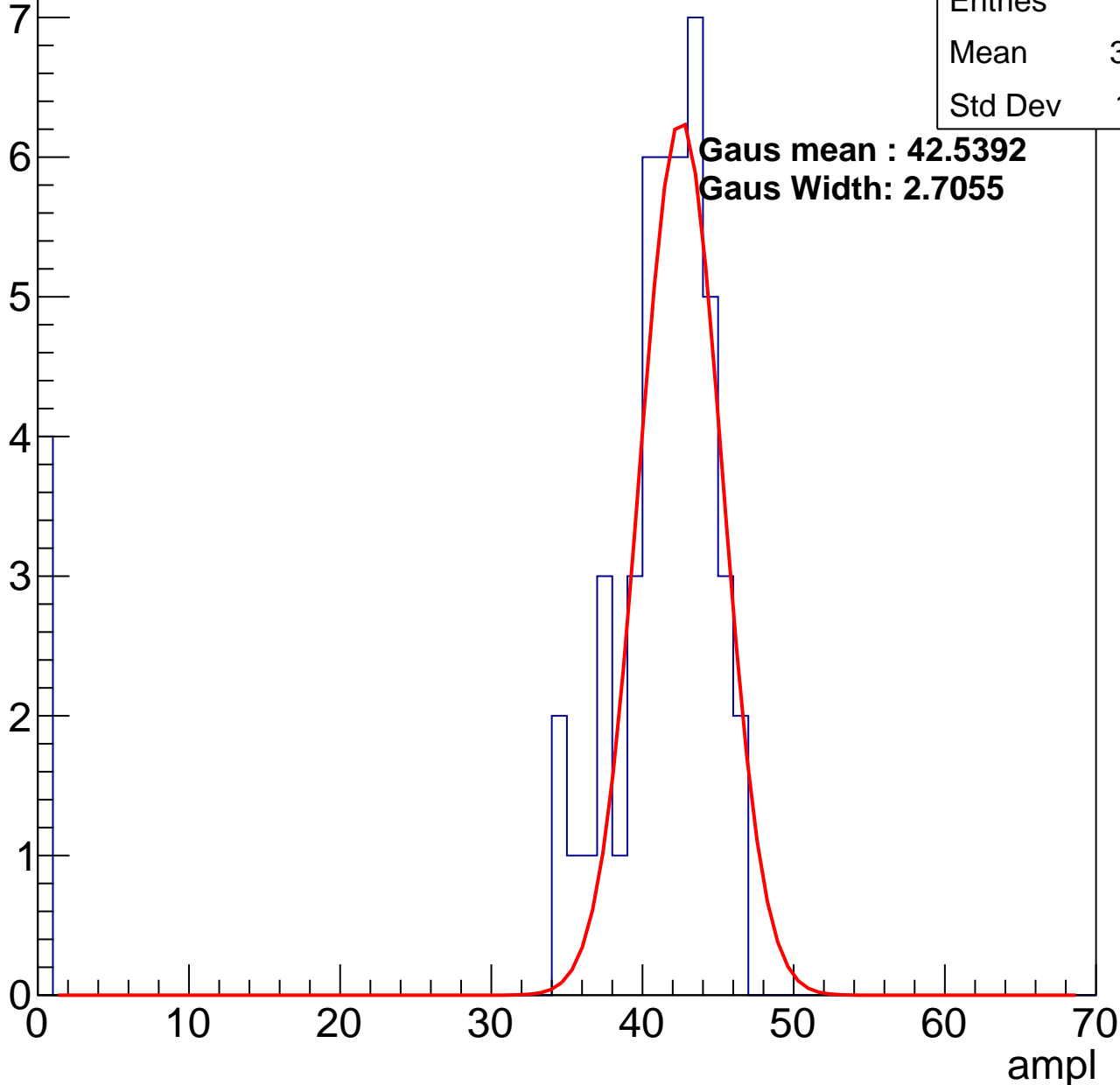


B1L103S, U8-ch84, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

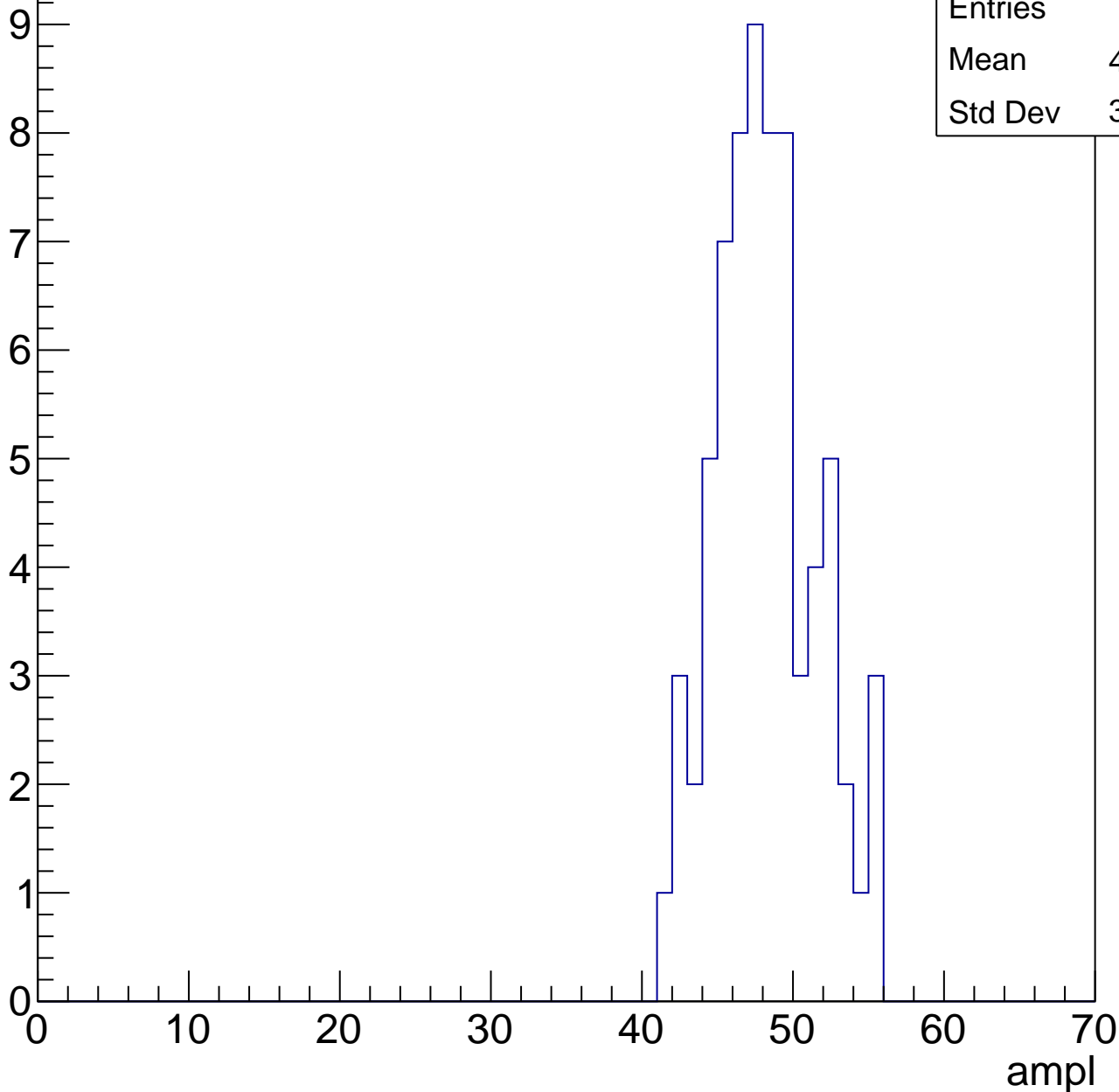
Entries	50
Mean	37.82
Std Dev	11.51



B1L103S, U8-ch84, adc3

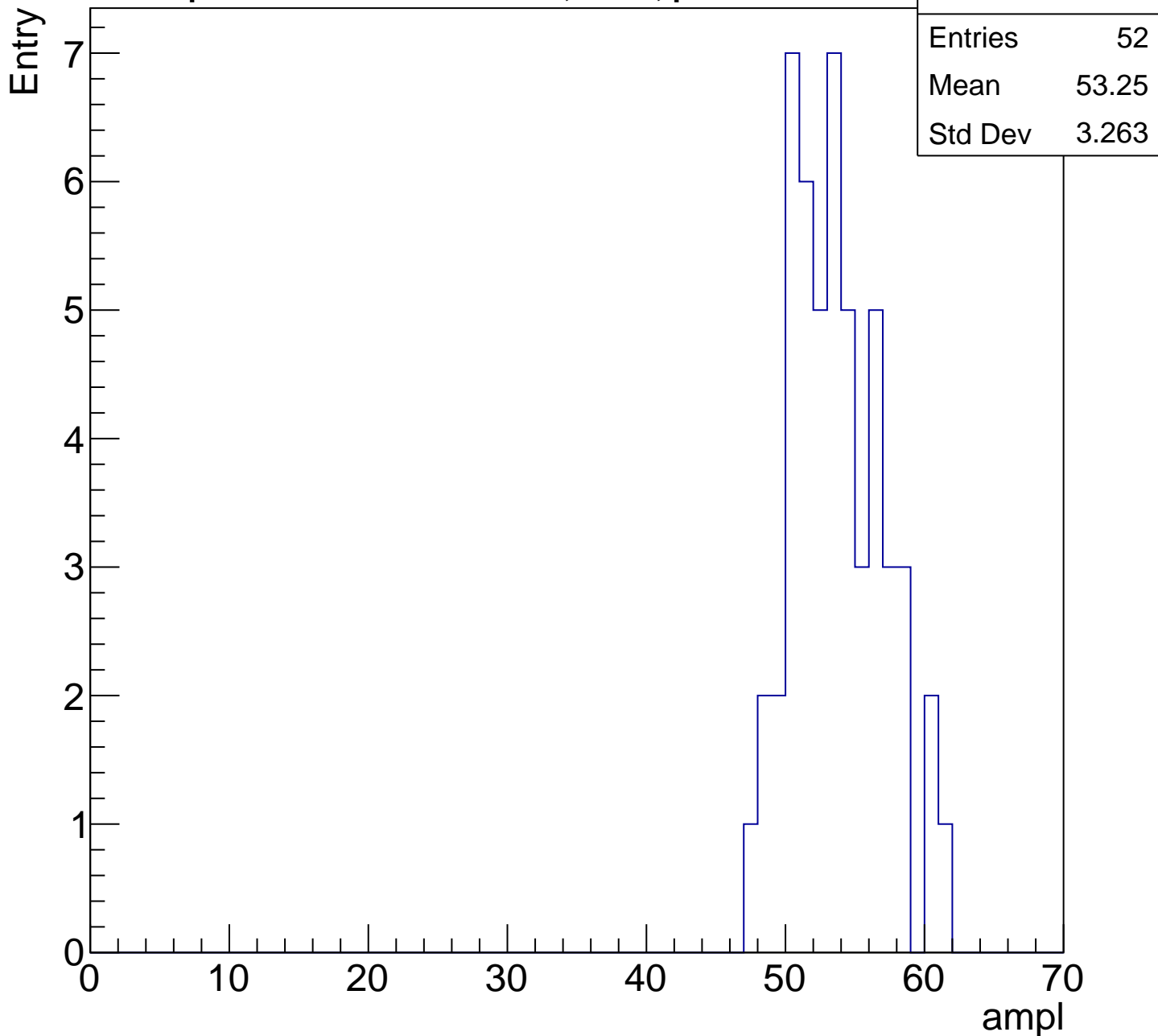
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch84, adc4

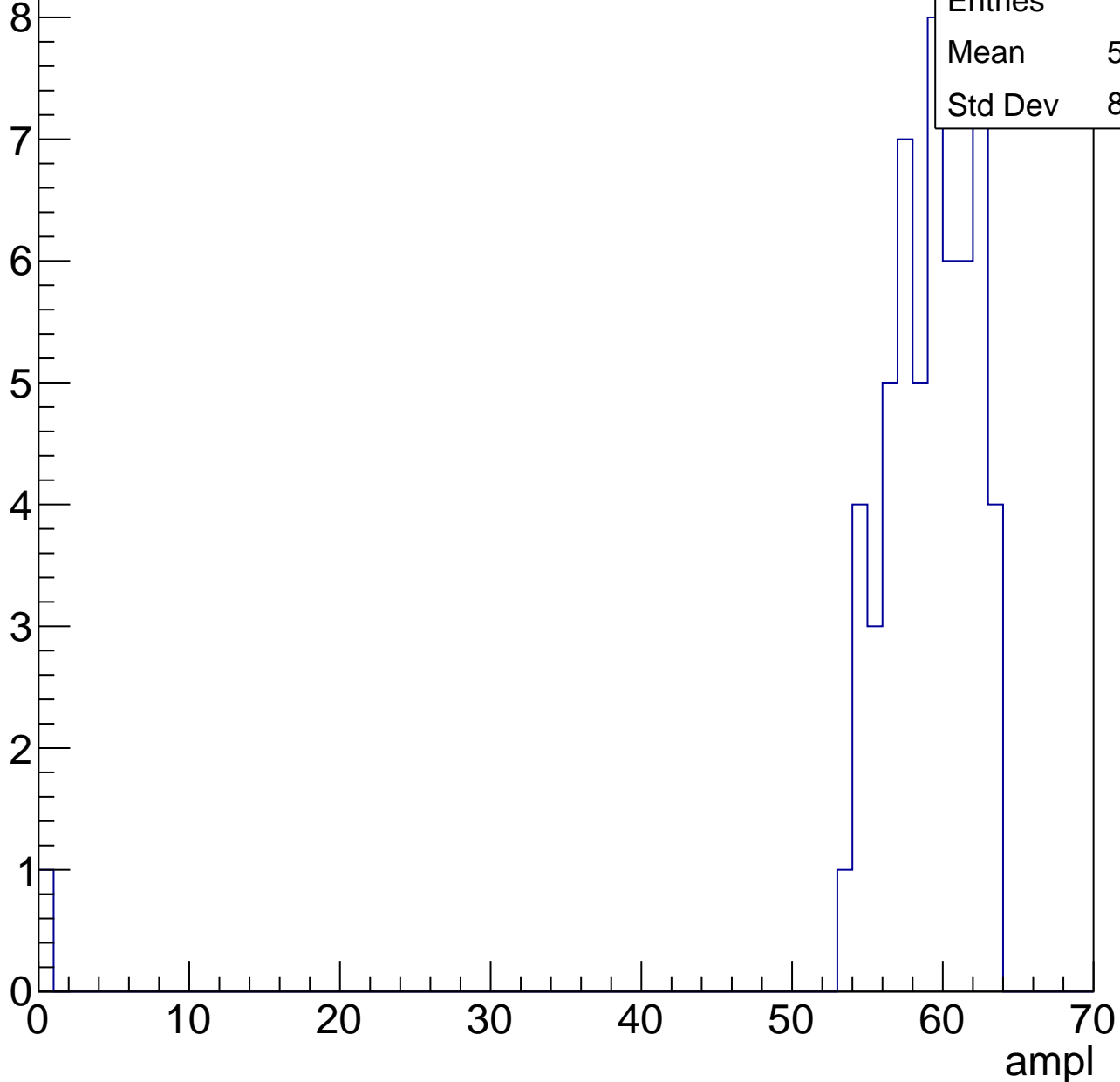
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



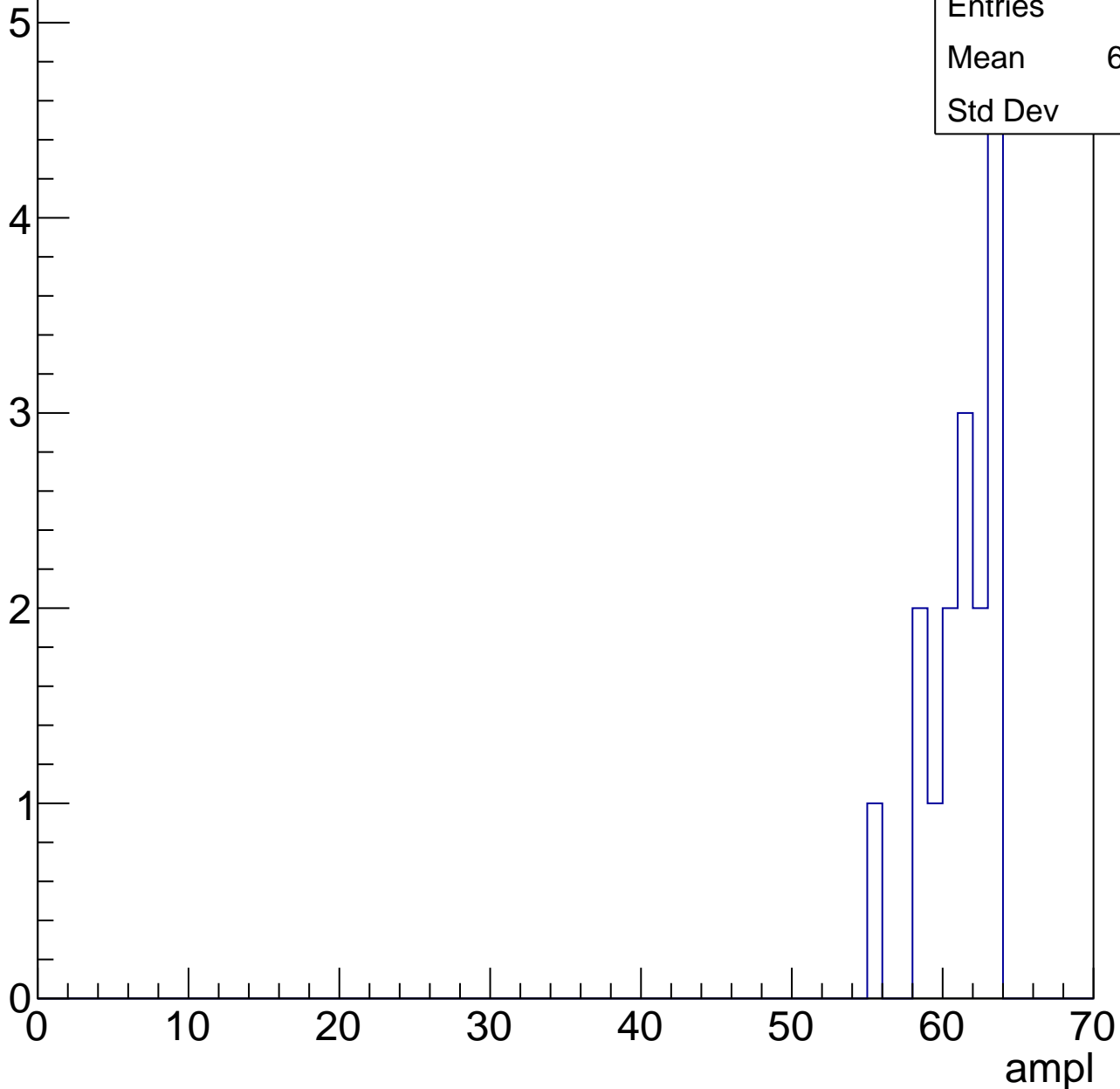
Entries	58
Mean	57.74
Std Dev	8.112

B1L103S, U8-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	60.75
Std Dev	2.25

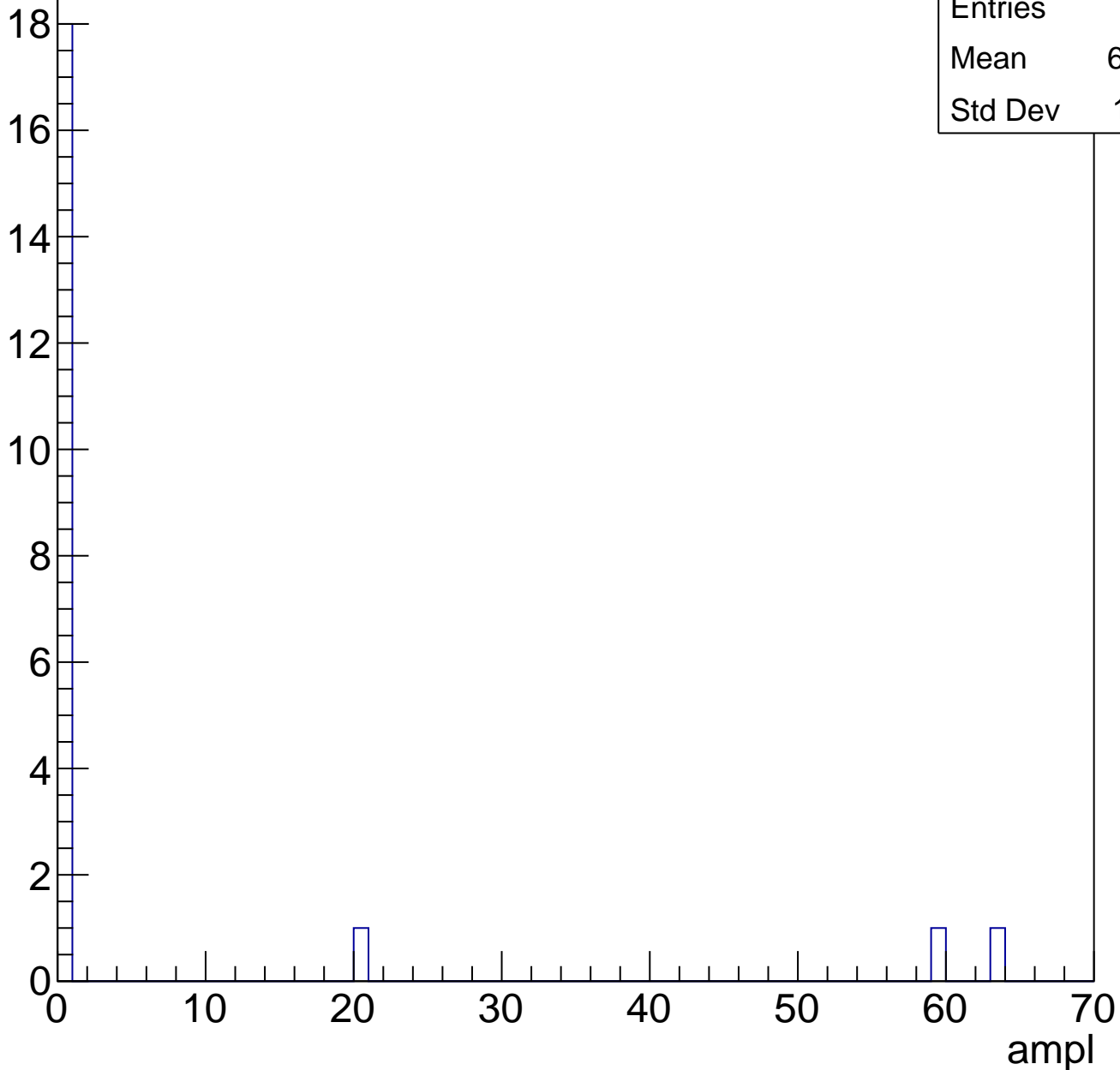


B1L103S, U8-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.762
Std Dev	18.11

Entry



B1L103S, U8-ch85, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	21.55
Std Dev	11.86

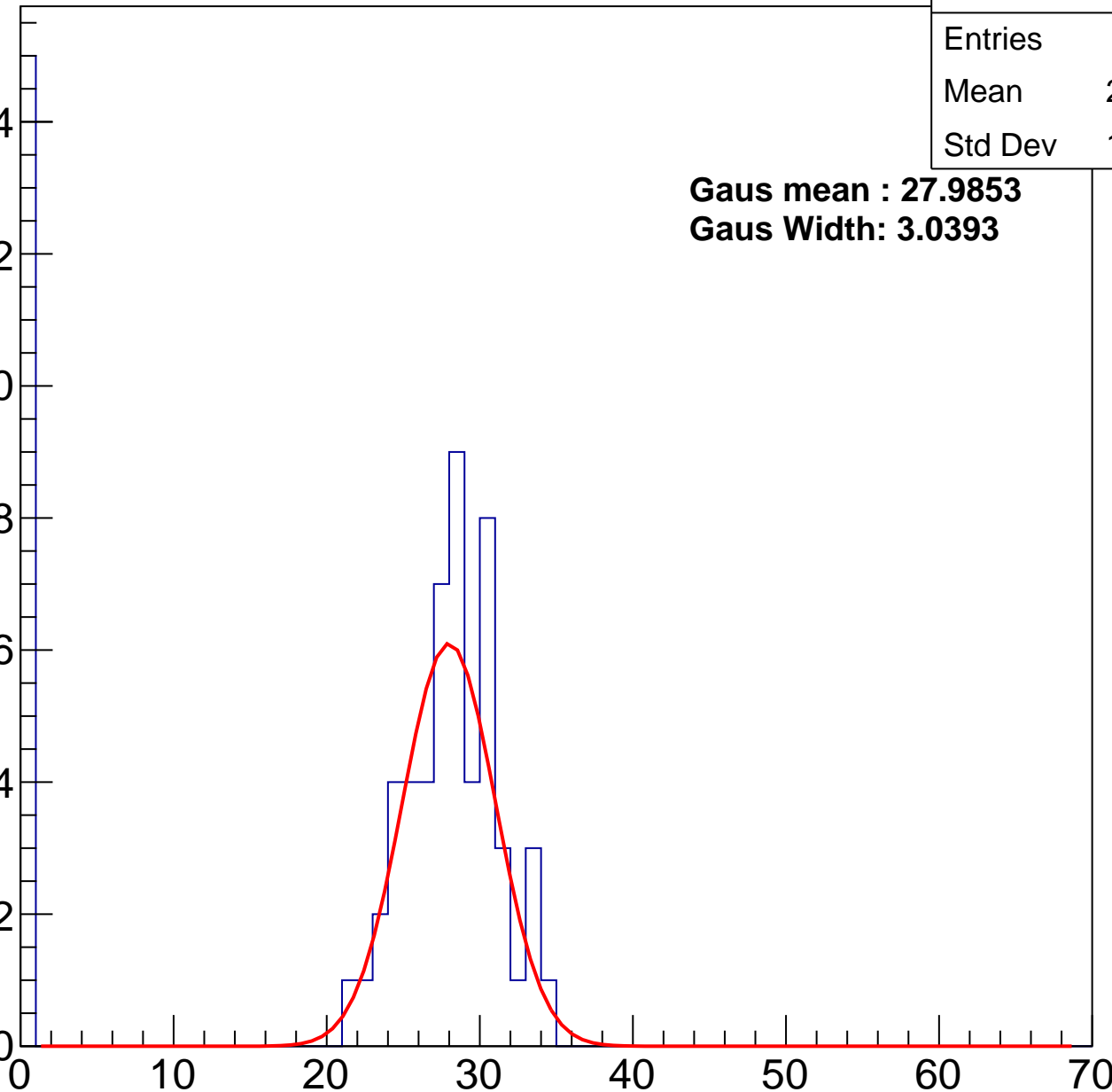
Gaus mean : 27.9853

Gaus Width: 3.0393

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U8-ch85, adc1

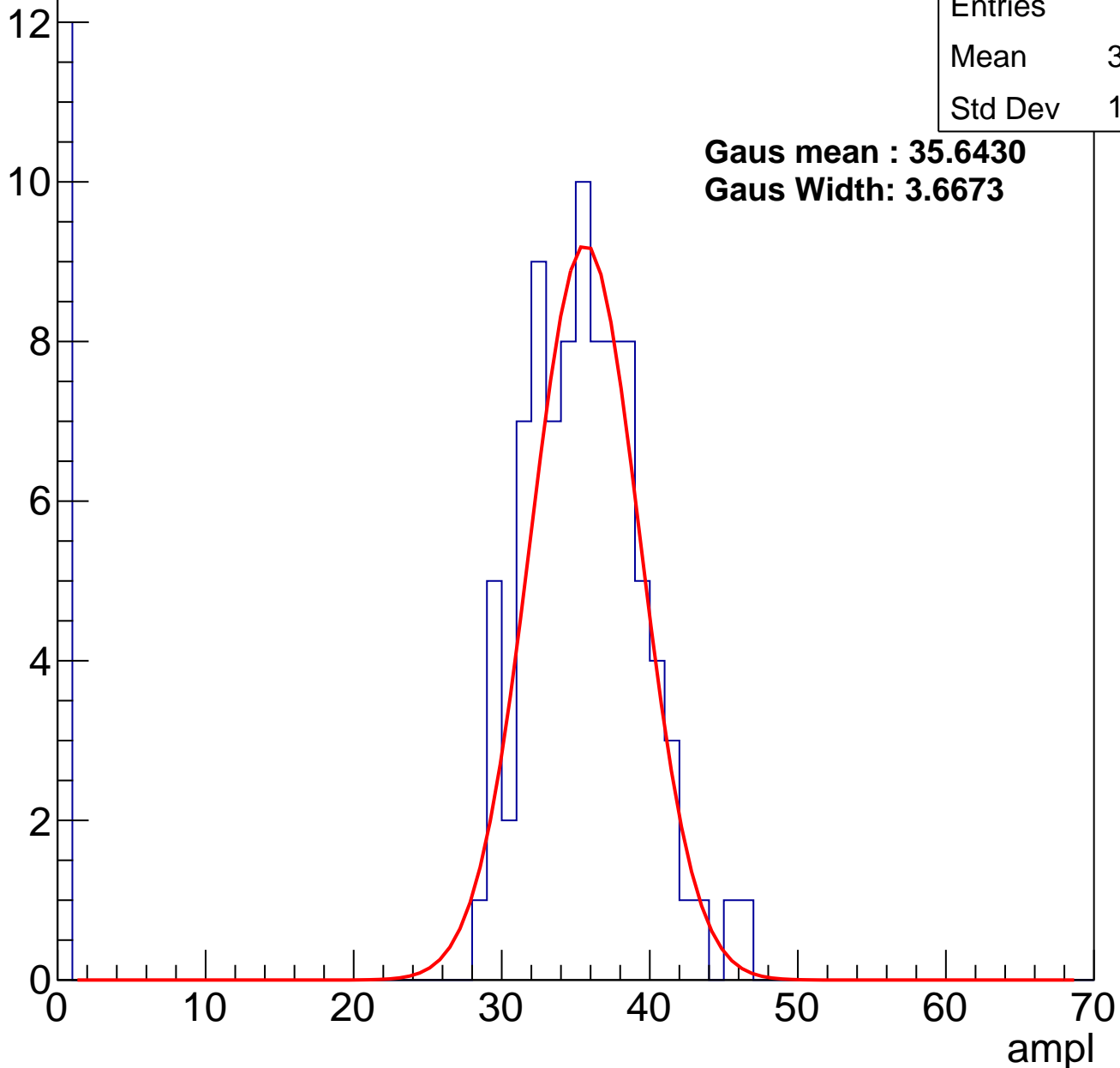
calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	31.02
Std Dev	11.92

Gaus mean : 35.6430

Gaus Width: 3.6673

Entry



B1L103S, U8-ch85, adc2

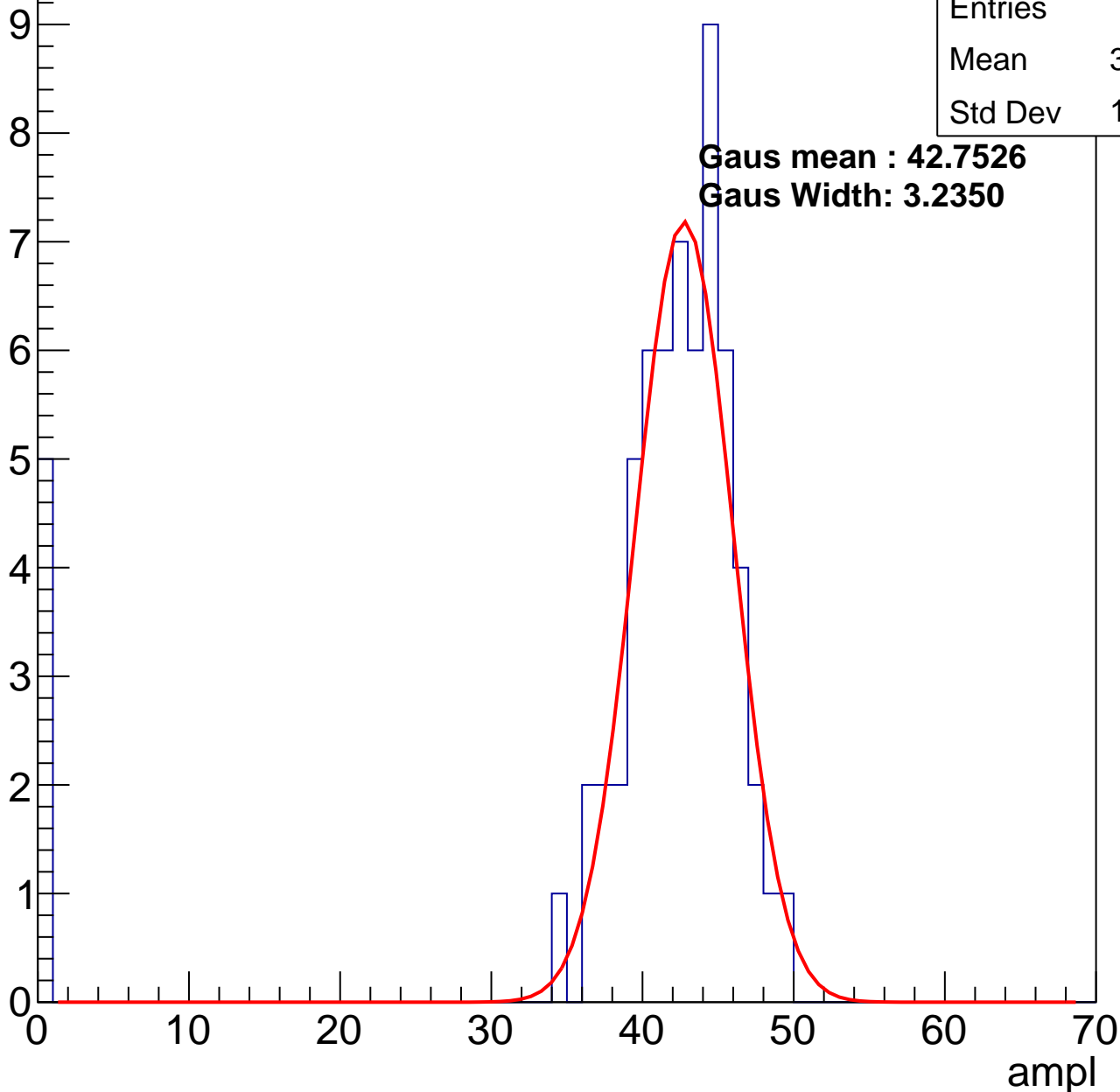
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.92
Std Dev	11.63

Gaus mean : 42.7526

Gaus Width: 3.2350

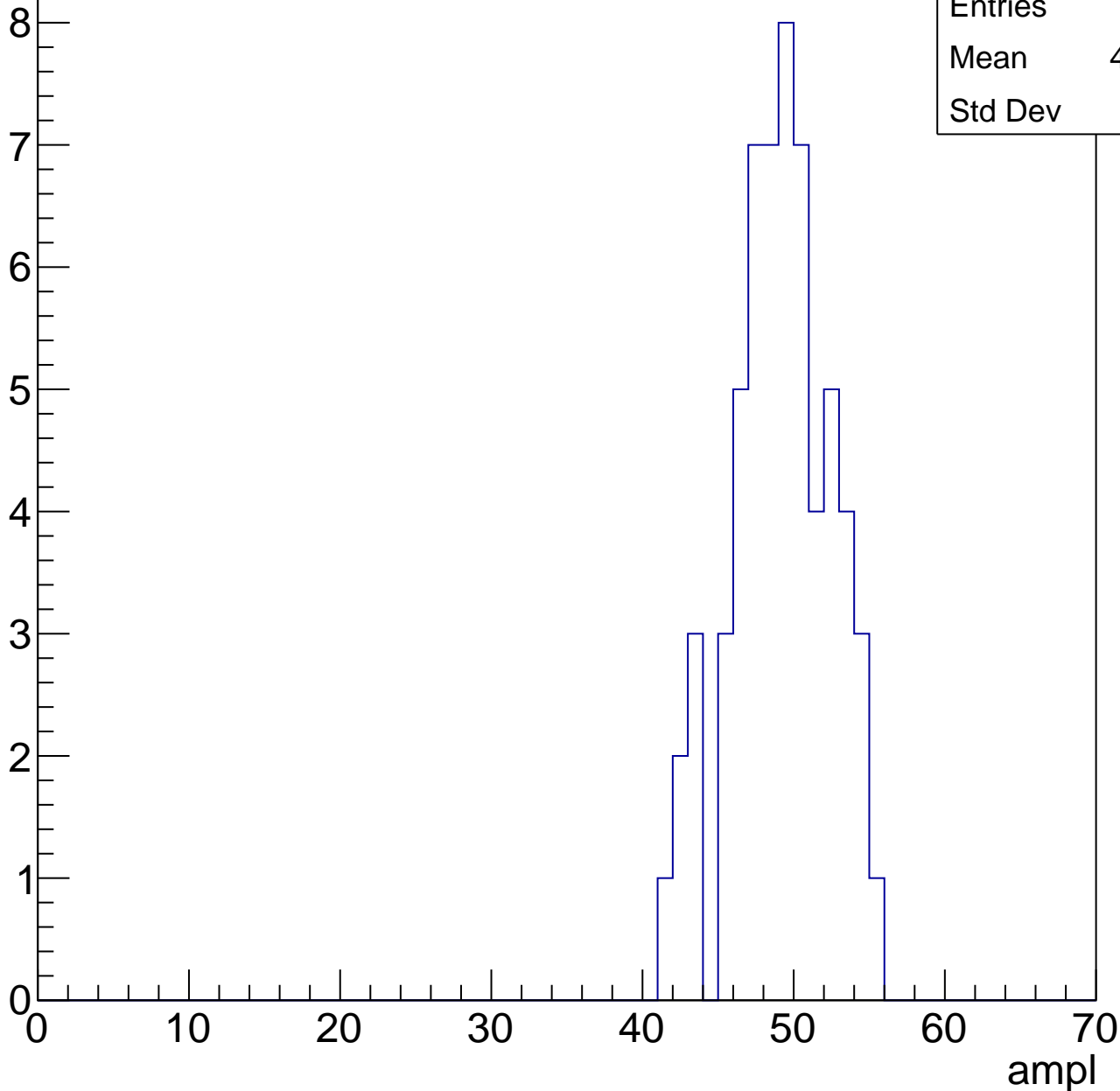


B1L103S, U8-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	48.65
Std Dev	3.25

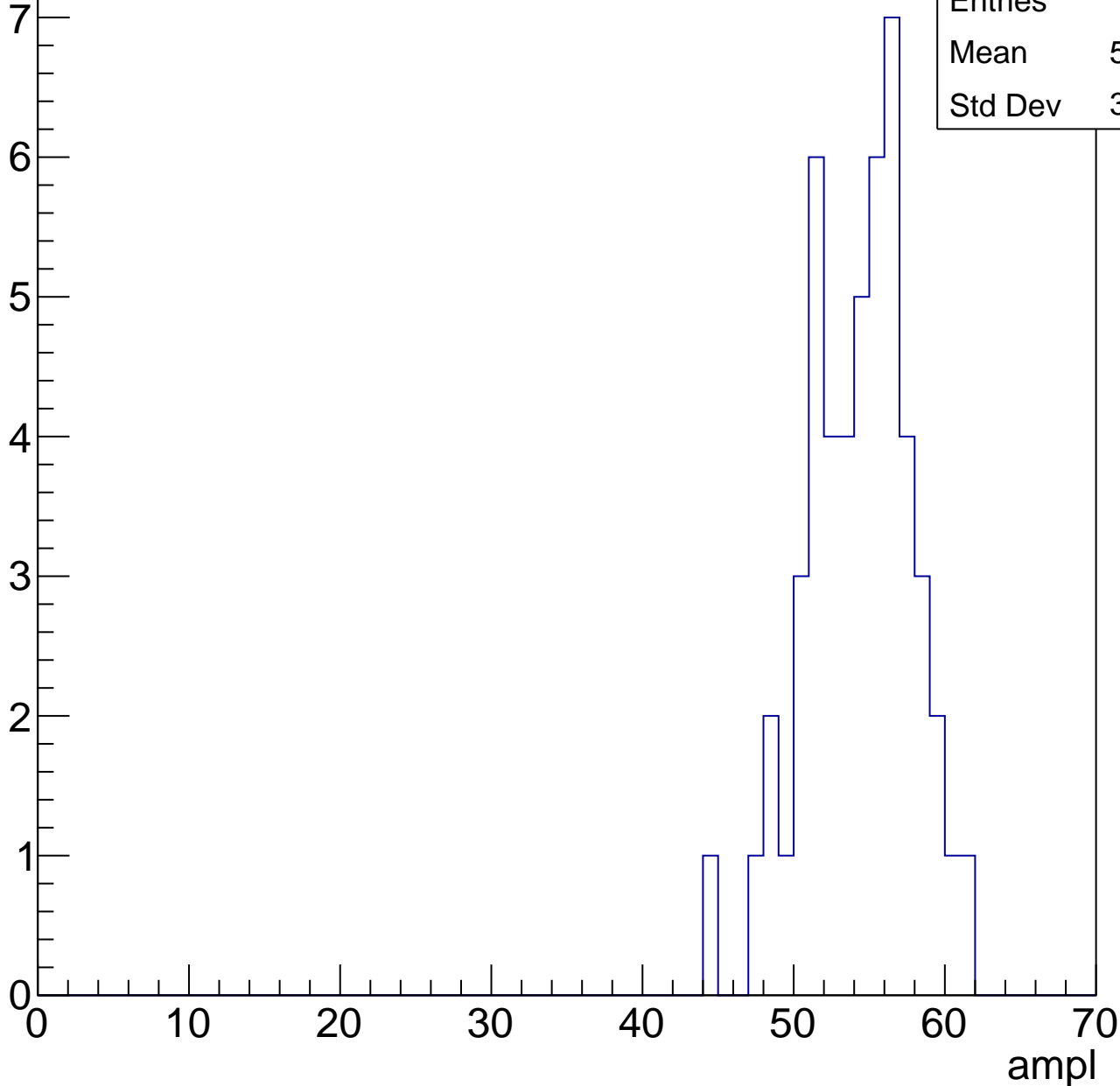


B1L103S, U8-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

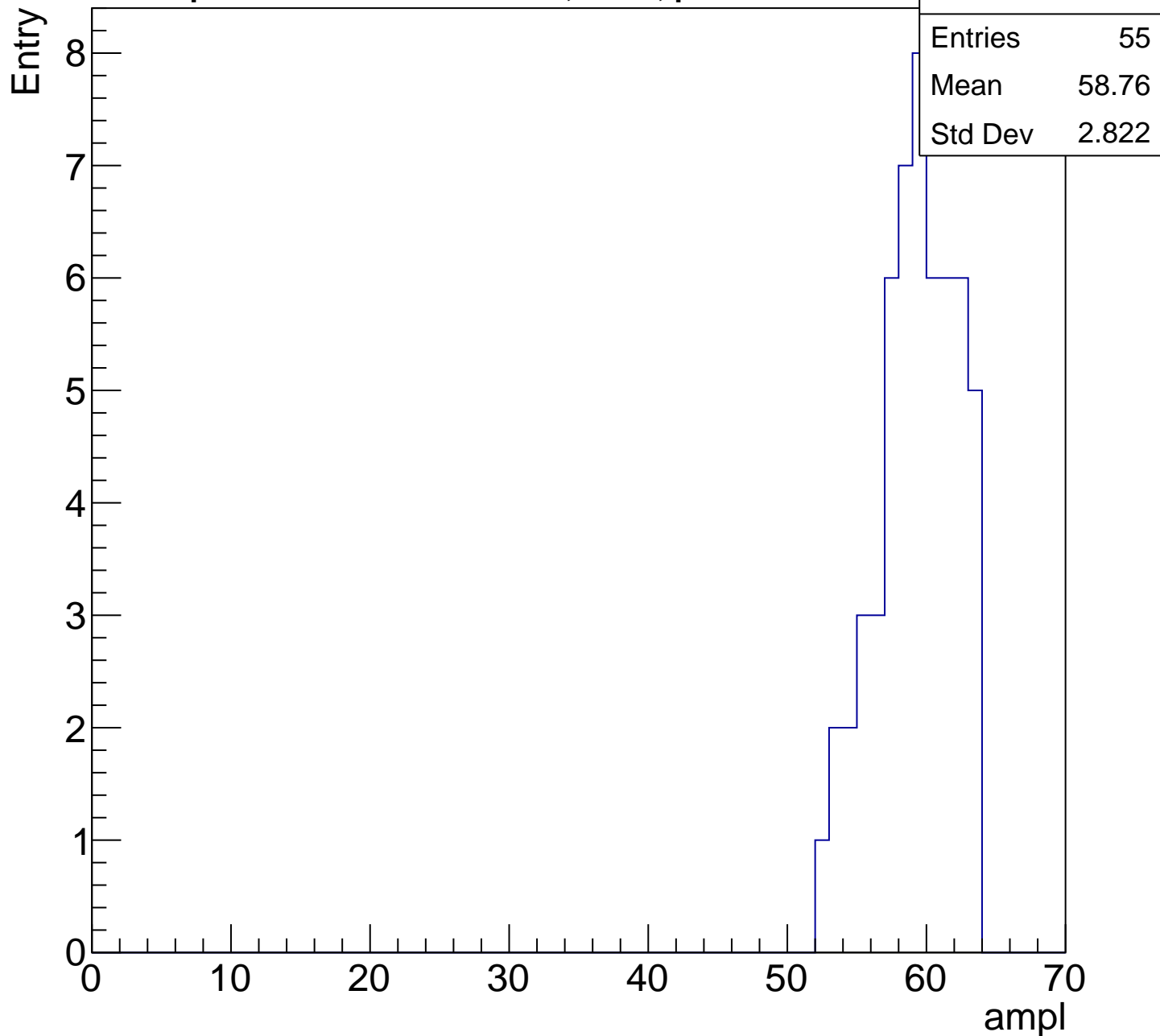
Entry

Entries	51
Mean	53.82
Std Dev	3.485



B1L103S, U8-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

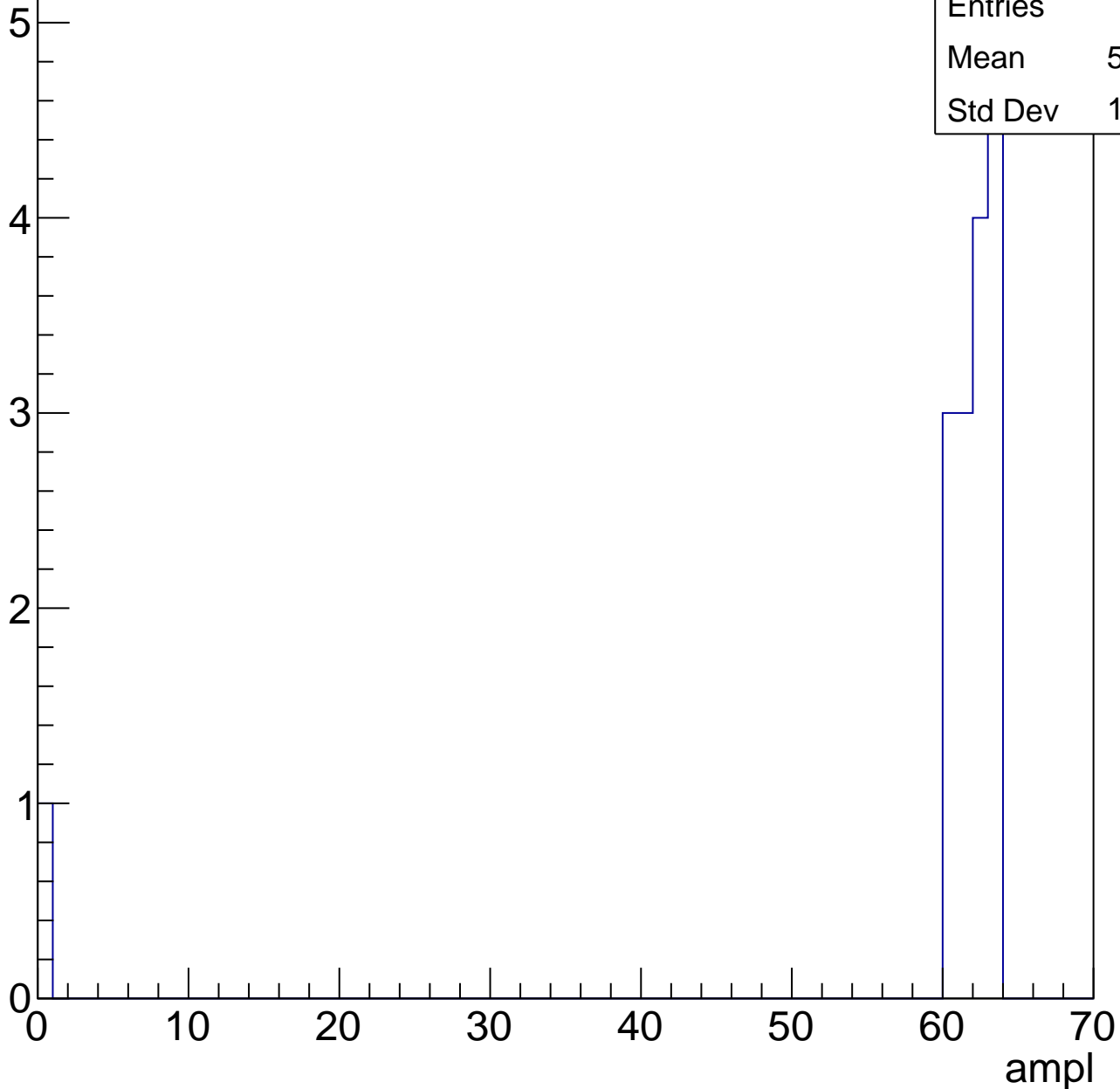


B1L103S, U8-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.88
Std Dev	14.98



B1L103S, U8-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch86, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	23.11
Std Dev	9.64

Gaus mean : 27.2925

Gaus Width: 3.7703

Entry

10

8

6

4

2

0

0

10

20

30

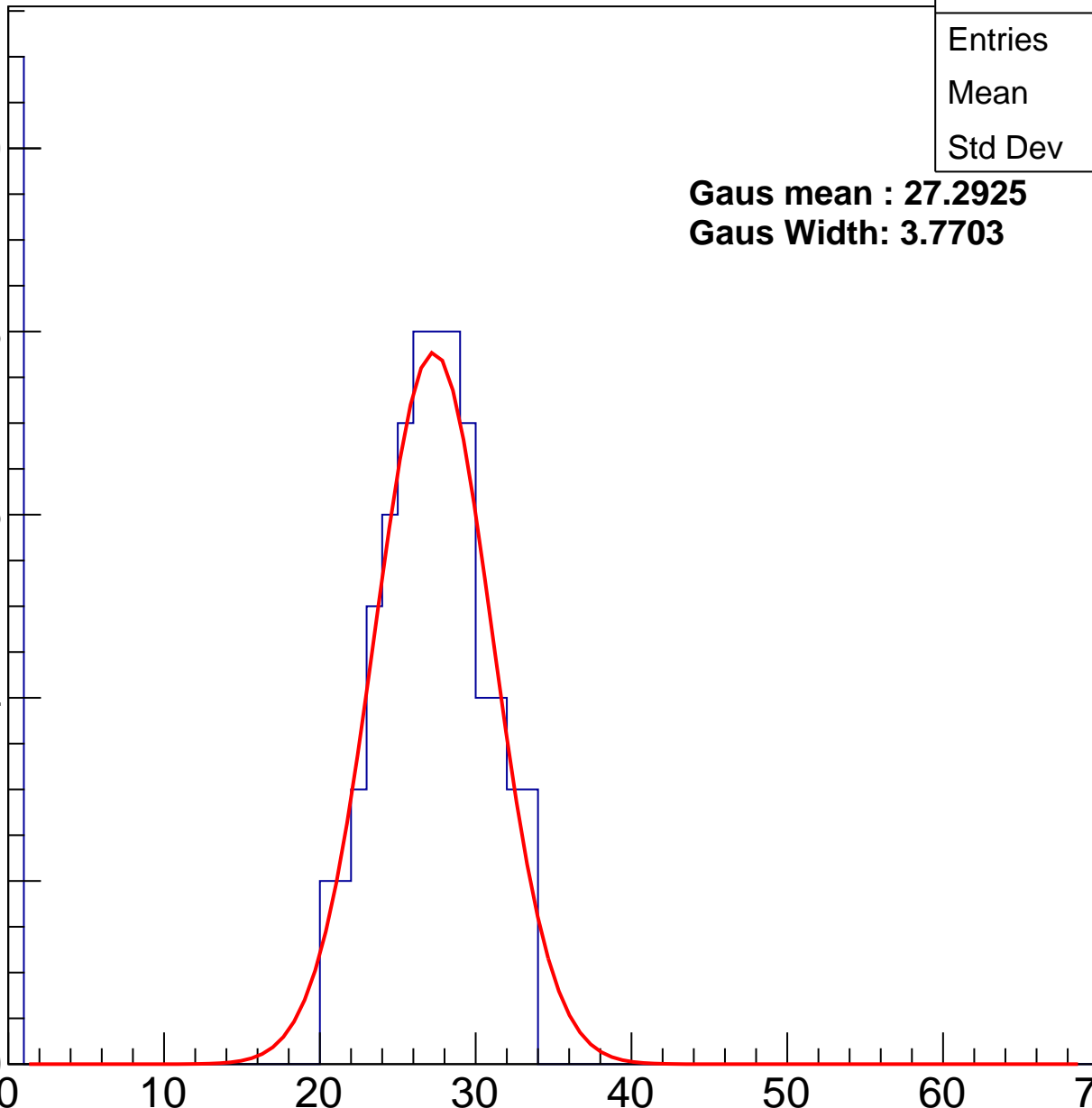
40

50

60

70

ampl



B1L103S, U8-ch86, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	33.25
Std Dev	5.153

Gaus mean : 34.2407

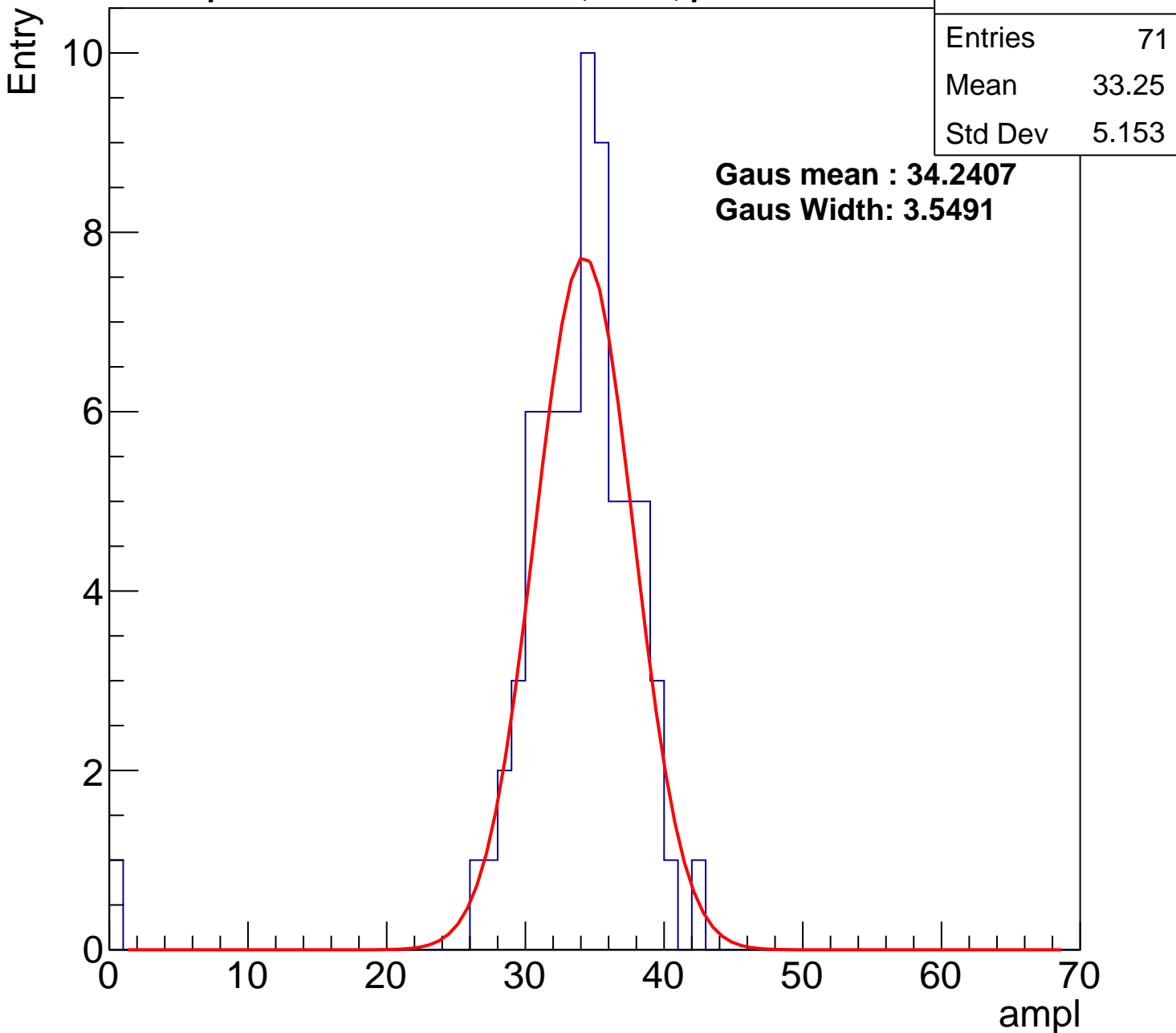
Gaus Width: 3.5491

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch86, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	31.89
Std Dev	16.47

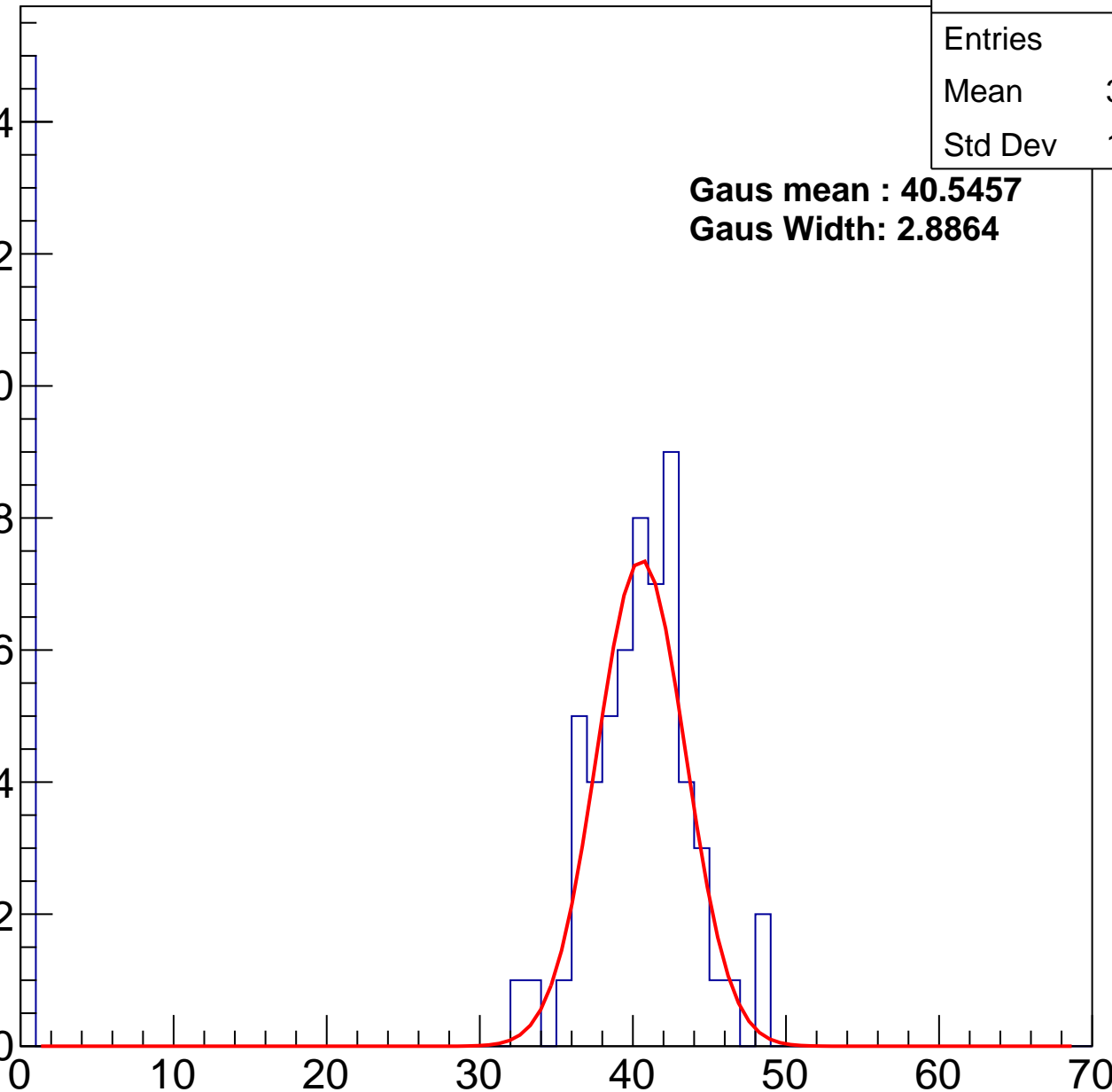
Gaus mean : 40.5457

Gaus Width: 2.8864

Entry

14
12
10
8
6
4
2
0

ampl

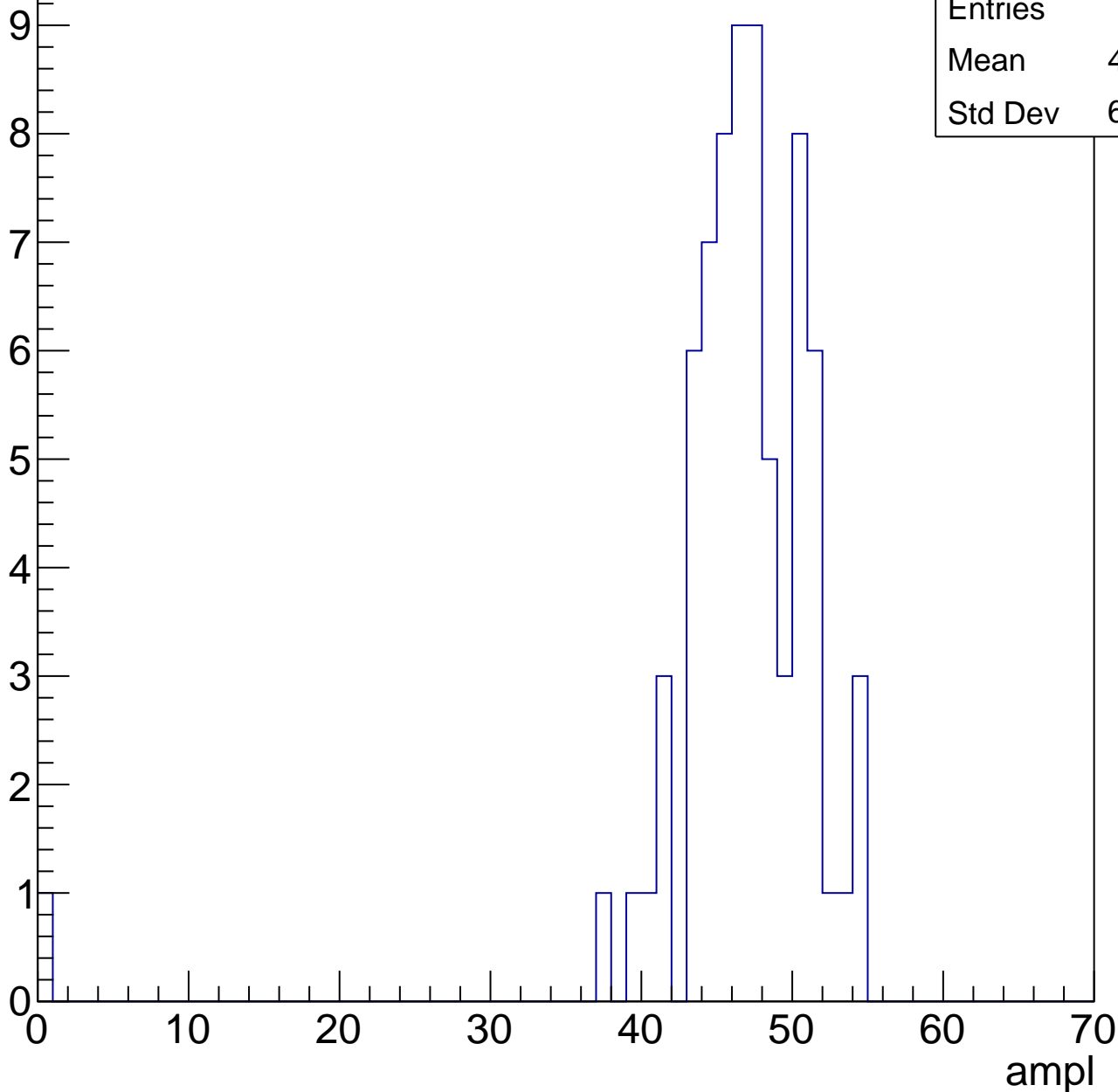


B1L103S, U8-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	46.05
Std Dev	6.472

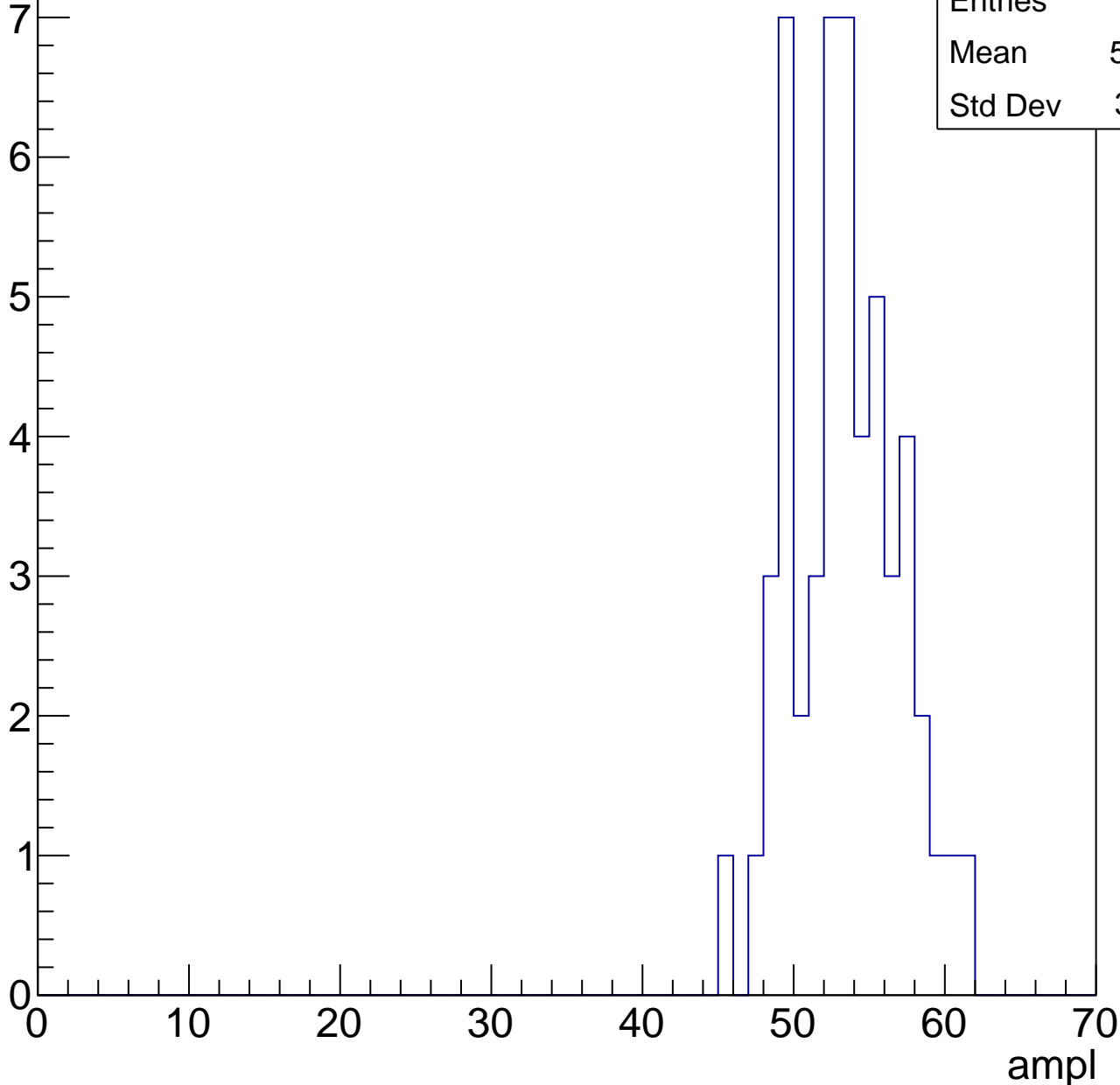


B1L103S, U8-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2

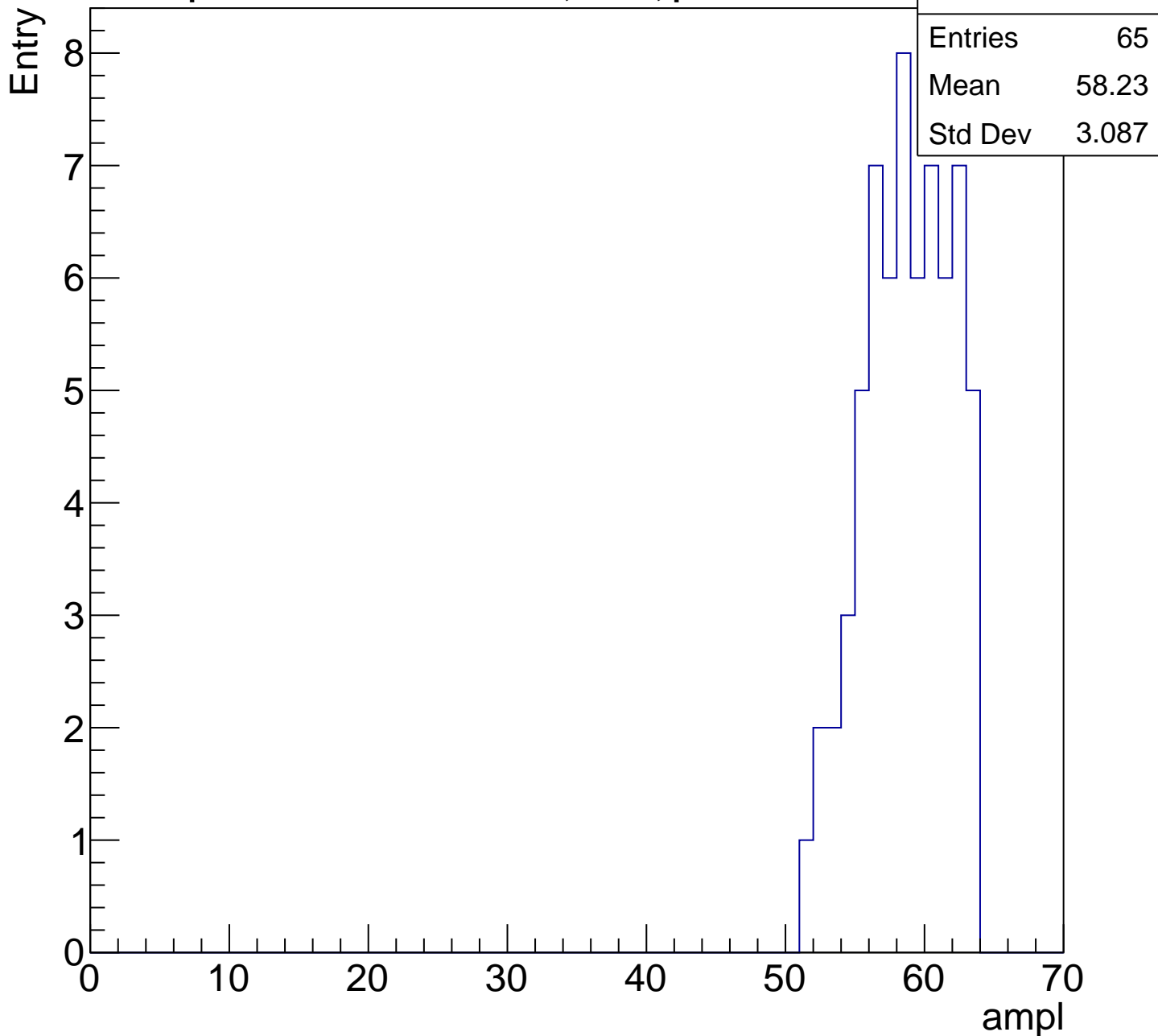
Entry

Entries	52
Mean	52.88
Std Dev	3.501



B1L103S, U8-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

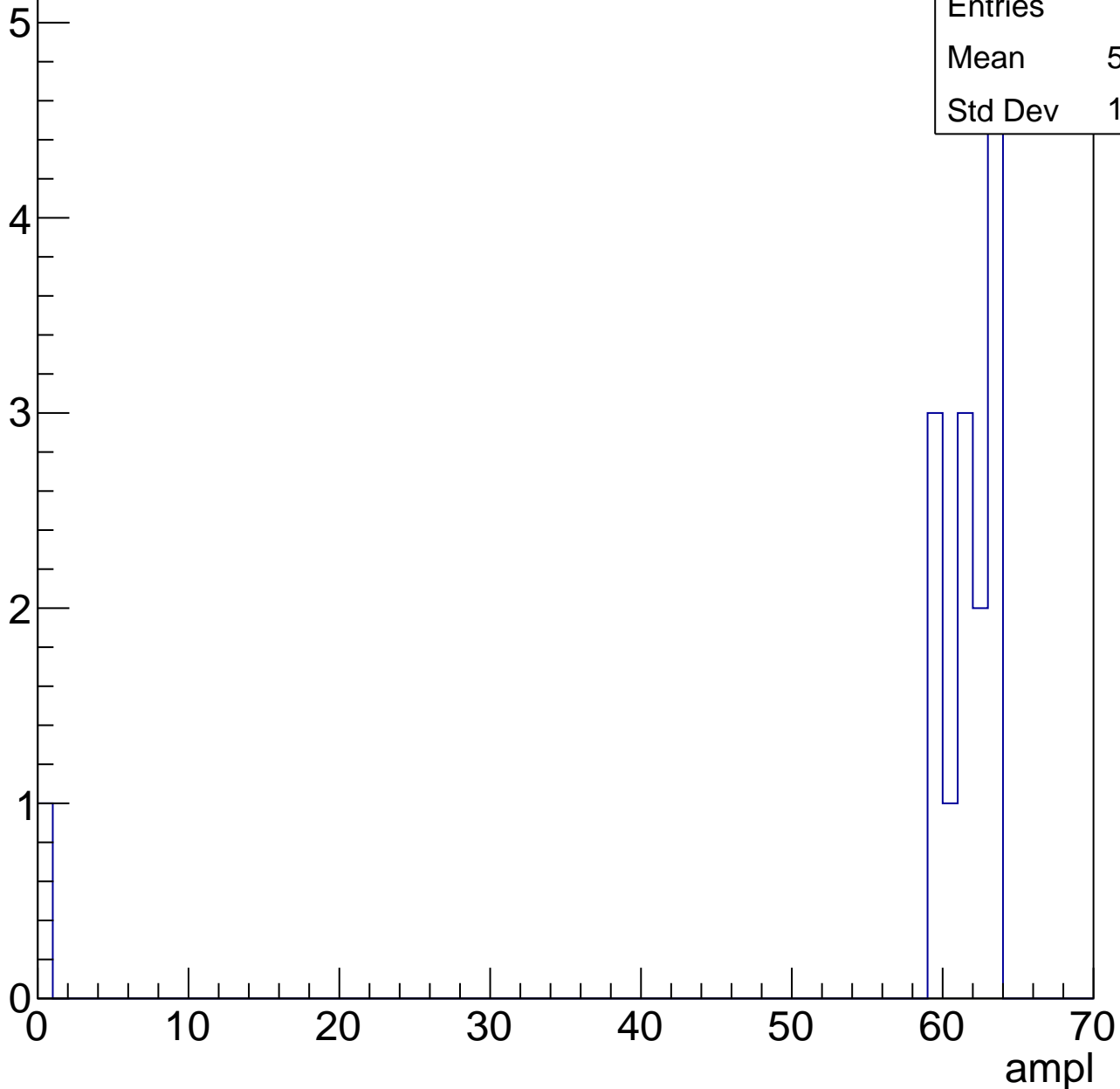


B1L103S, U8-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.27
Std Dev	15.38



B1L103S, U8-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

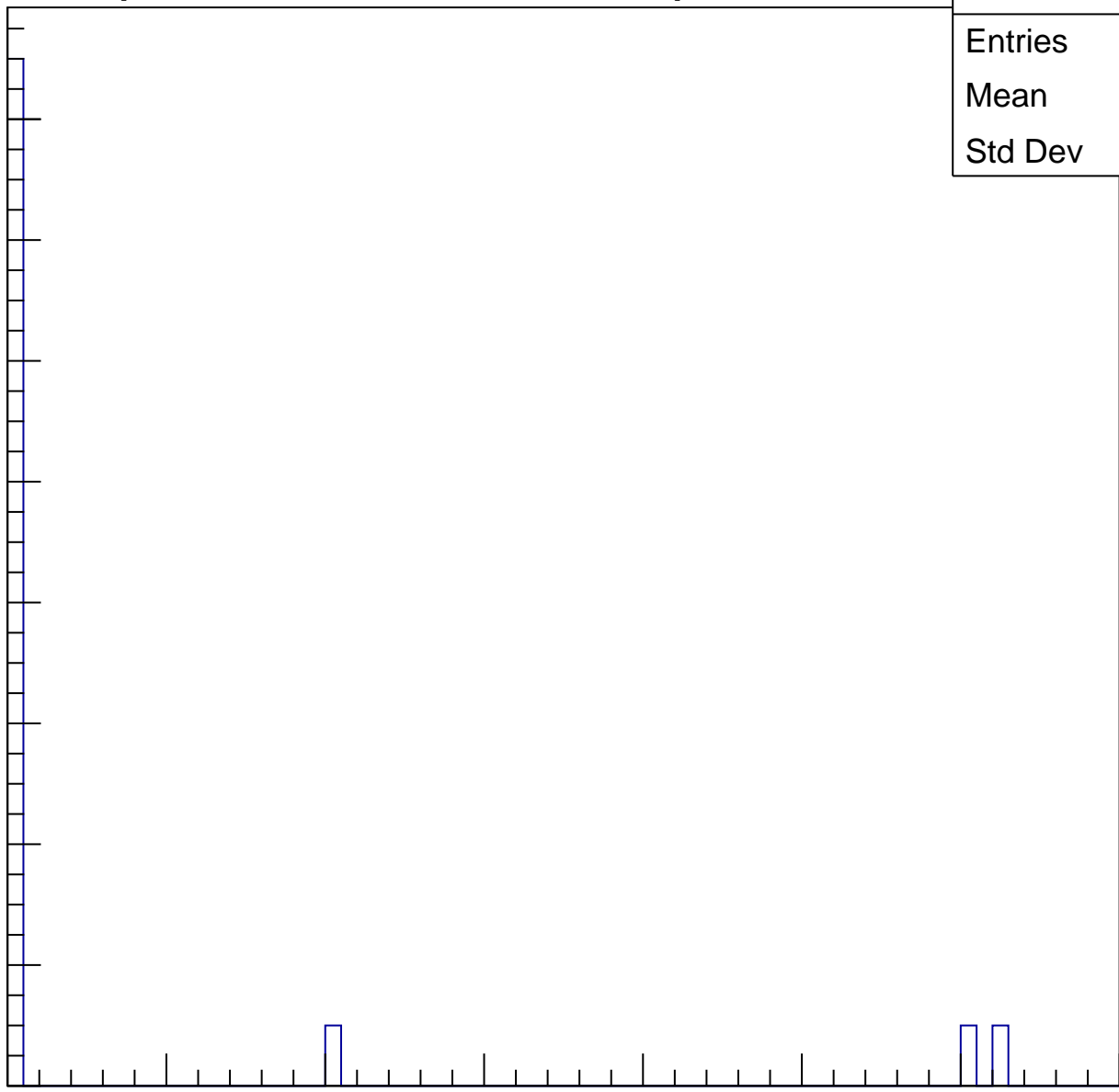
Entries	20
Mean	7.1
Std Dev	18.49

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch87, adc0

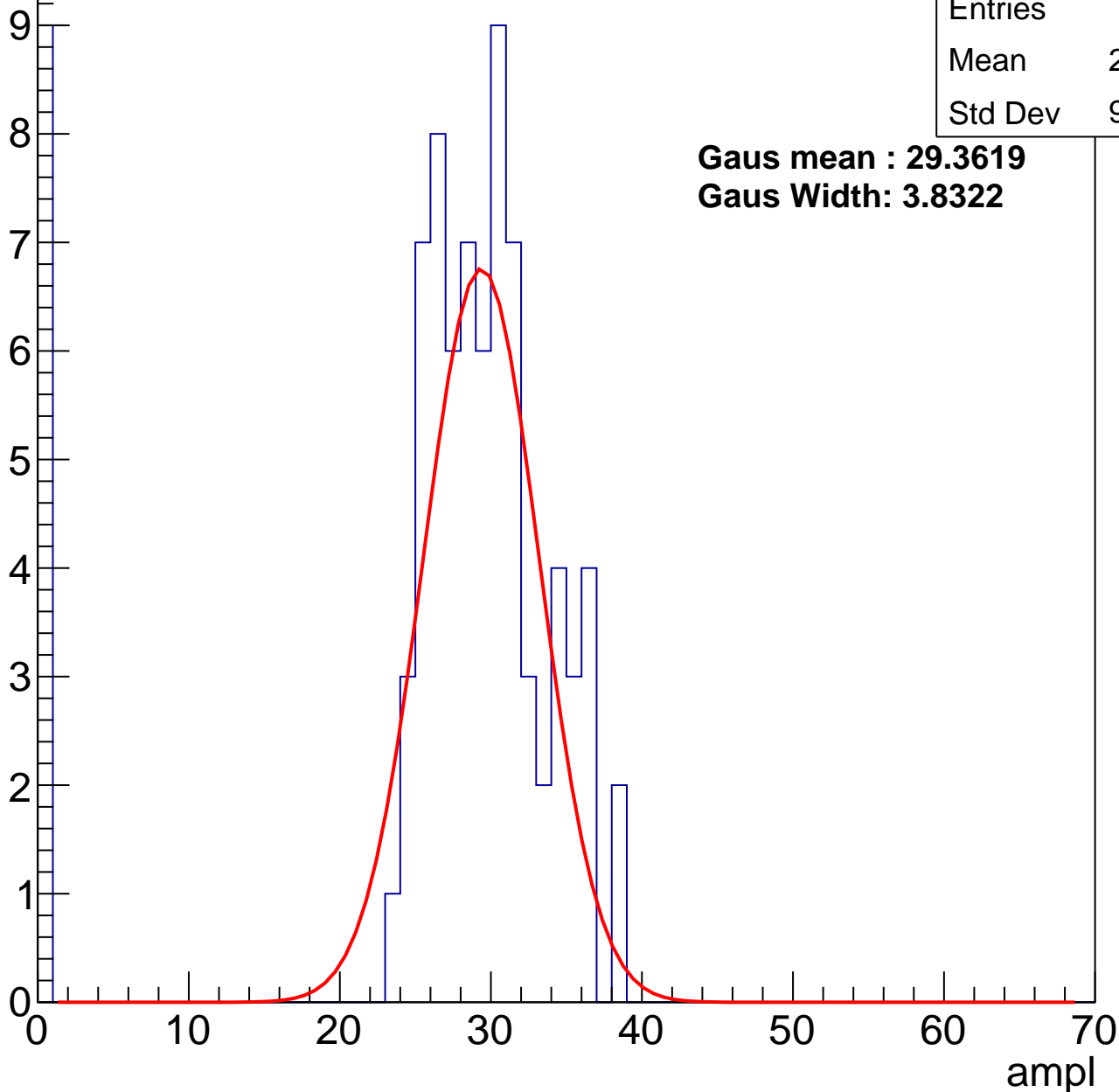
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	26.17
Std Dev	9.882

Gaus mean : 29.3619

Gaus Width: 3.8322



B1L103S, U8-ch87, adc1

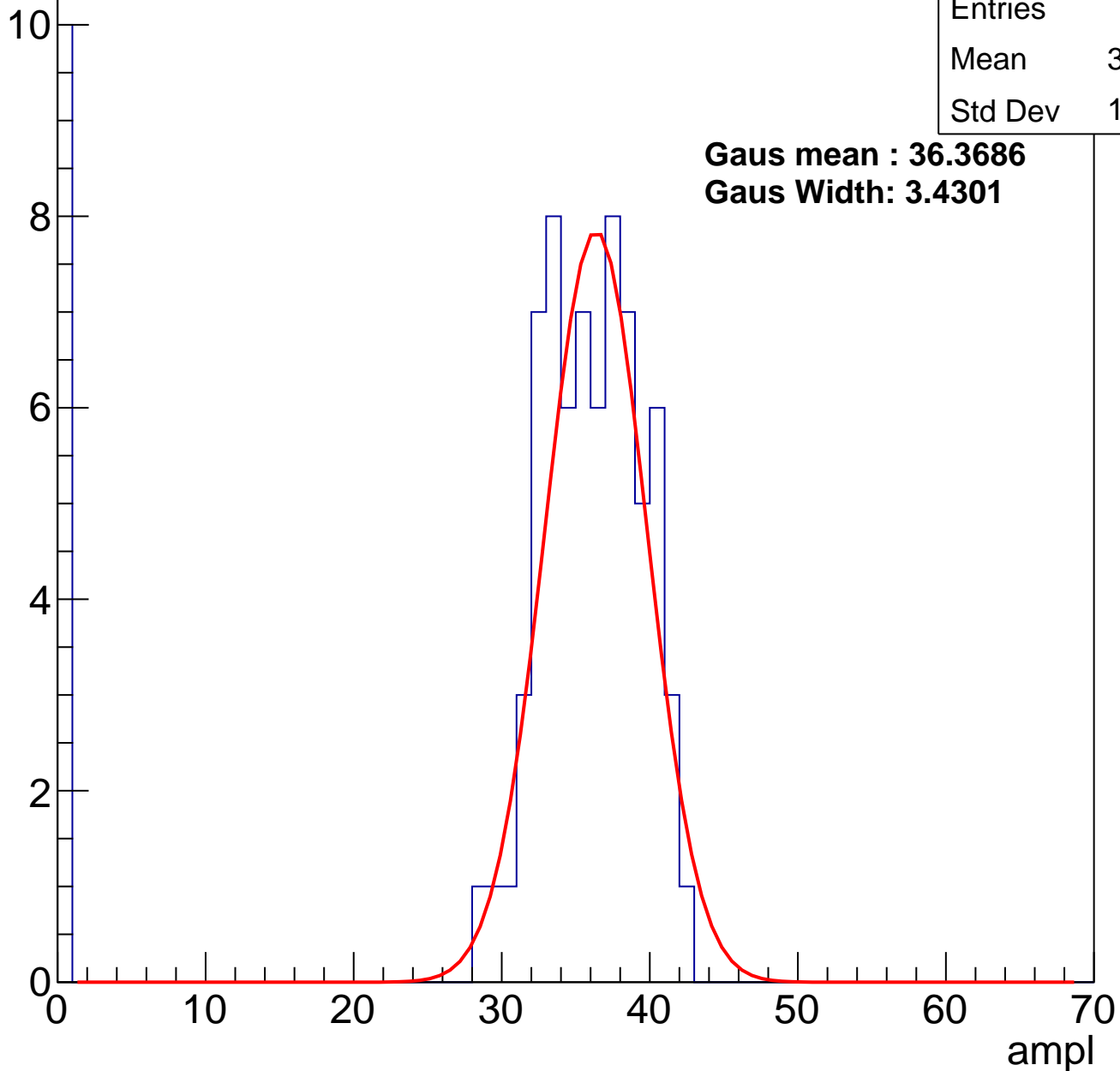
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	31.19
Std Dev	12.16

Gaus mean : 36.3686

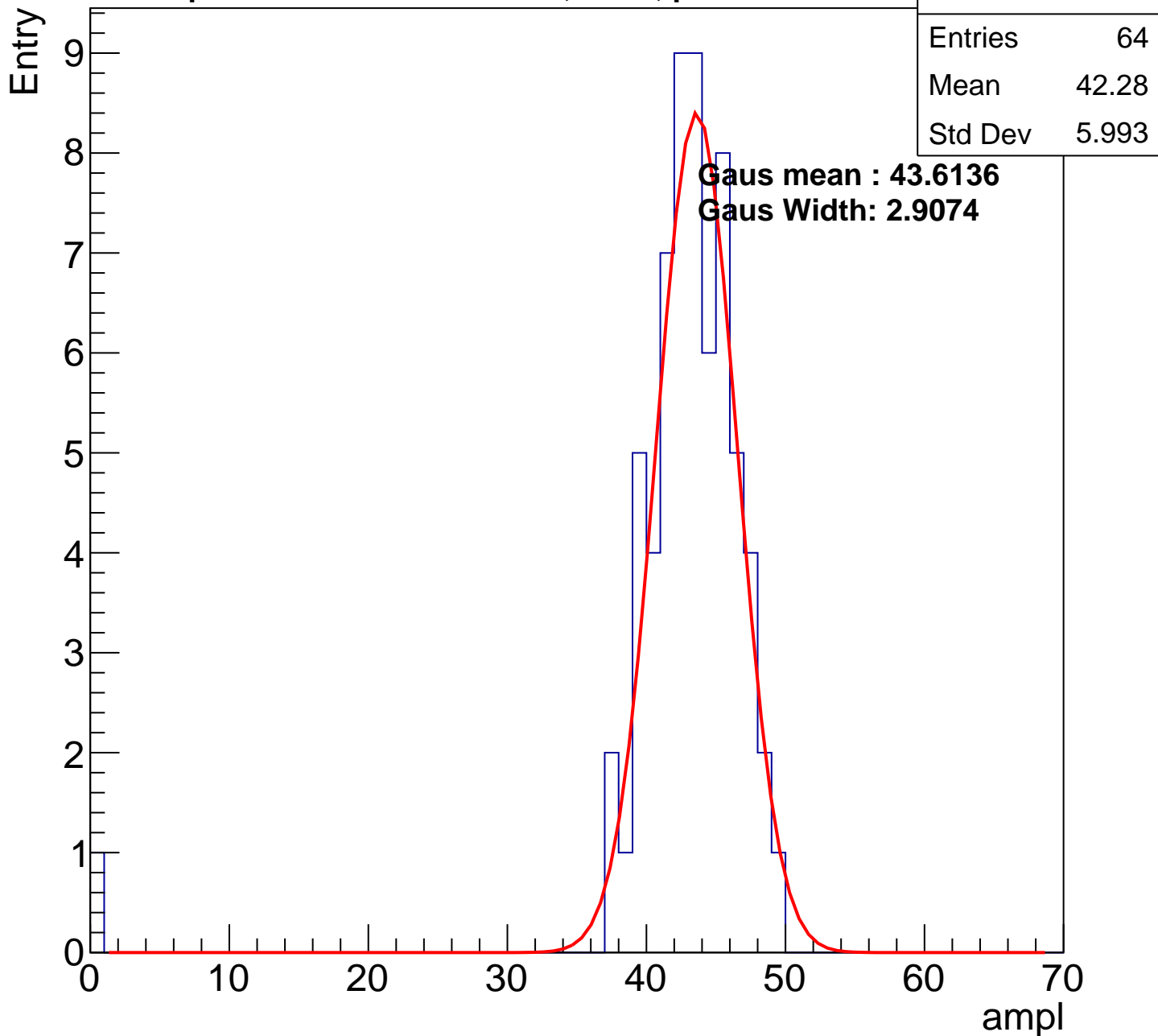
Gaus Width: 3.4301

Entry



B1L103S, U8-ch87, adc2

calib_packv5_041523_1651.root, FC#0, port C2

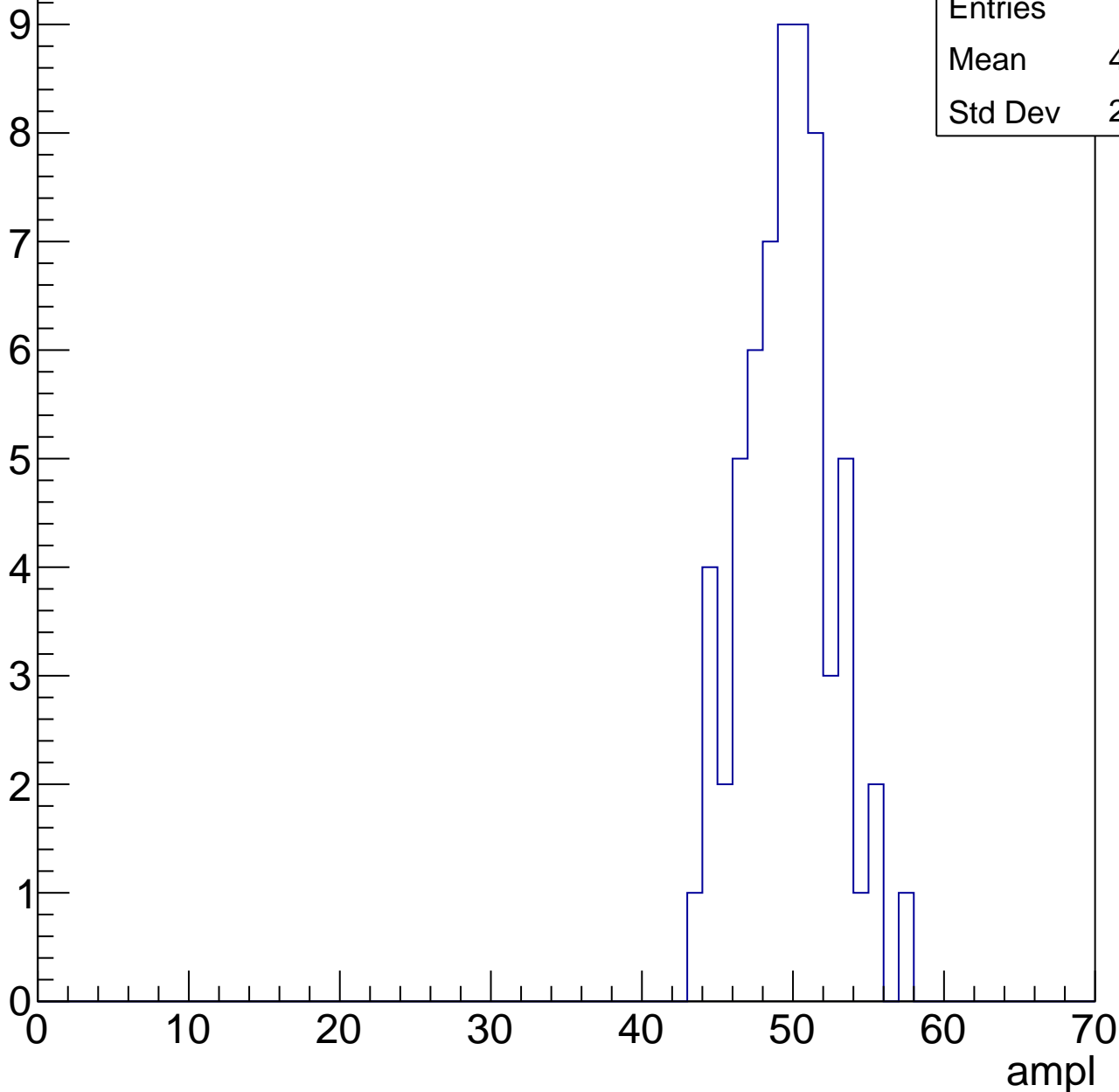


B1L103S, U8-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.17
Std Dev	2.958

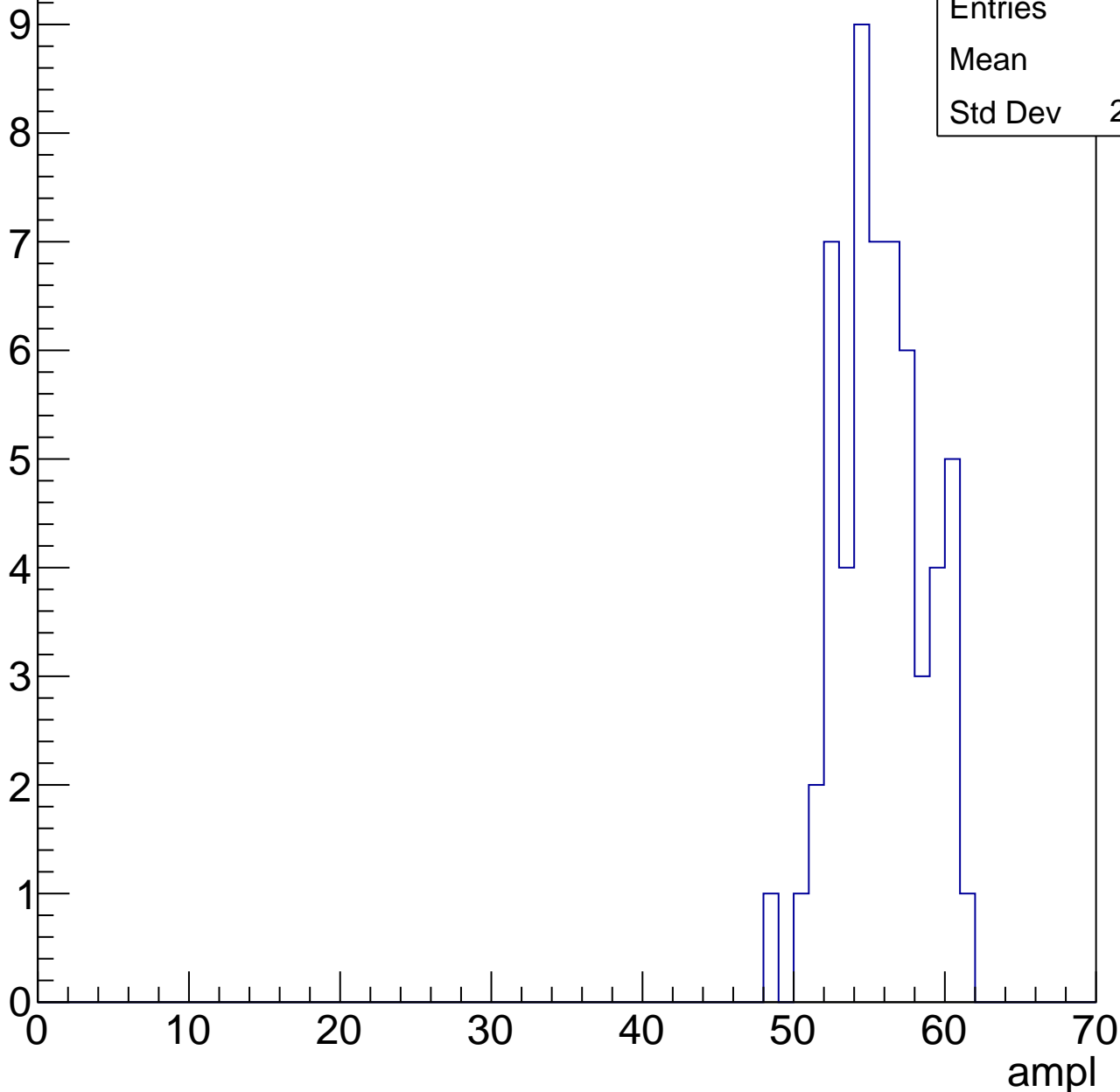


B1L103S, U8-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.3
Std Dev	2.877

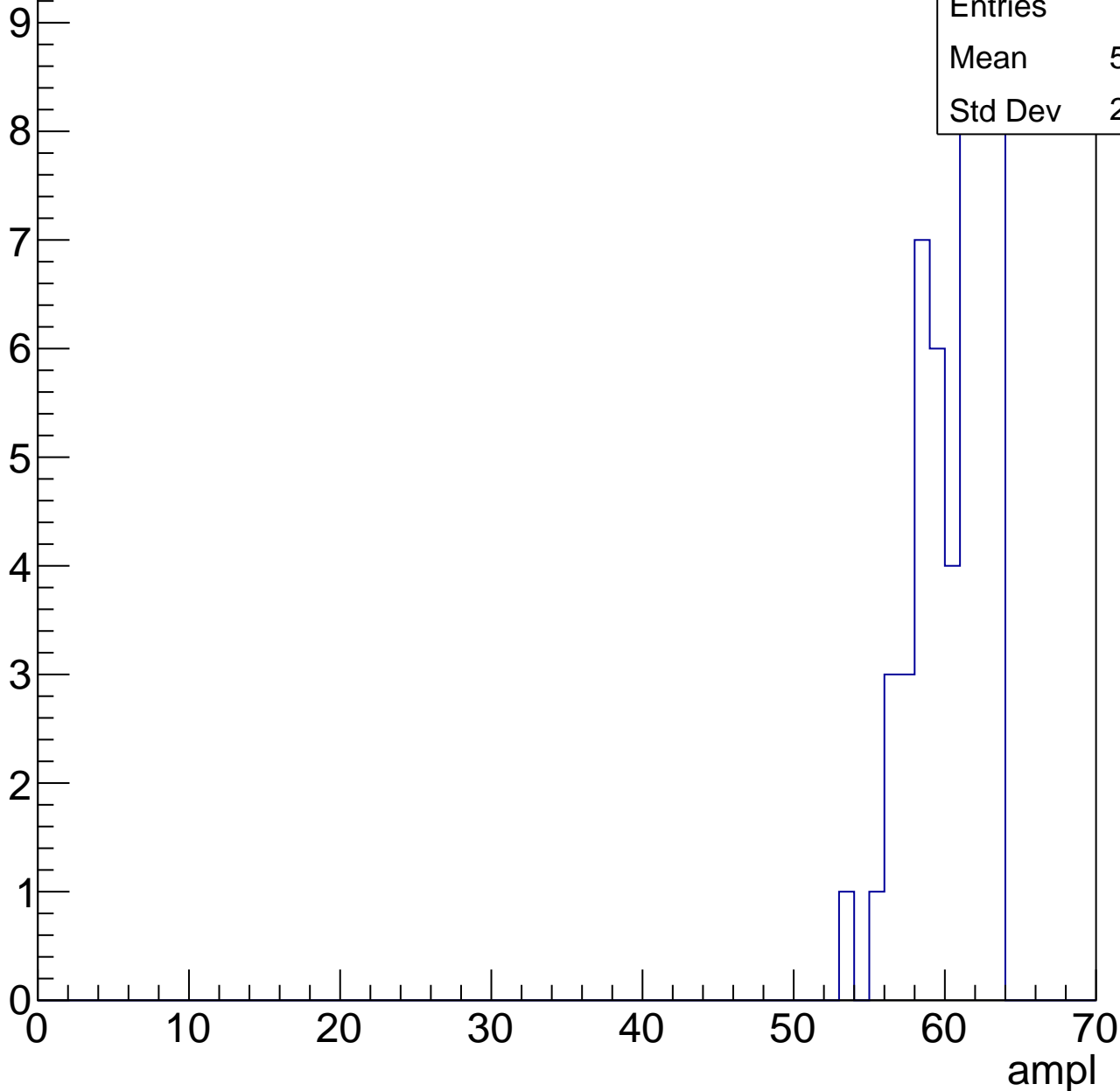


B1L103S, U8-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

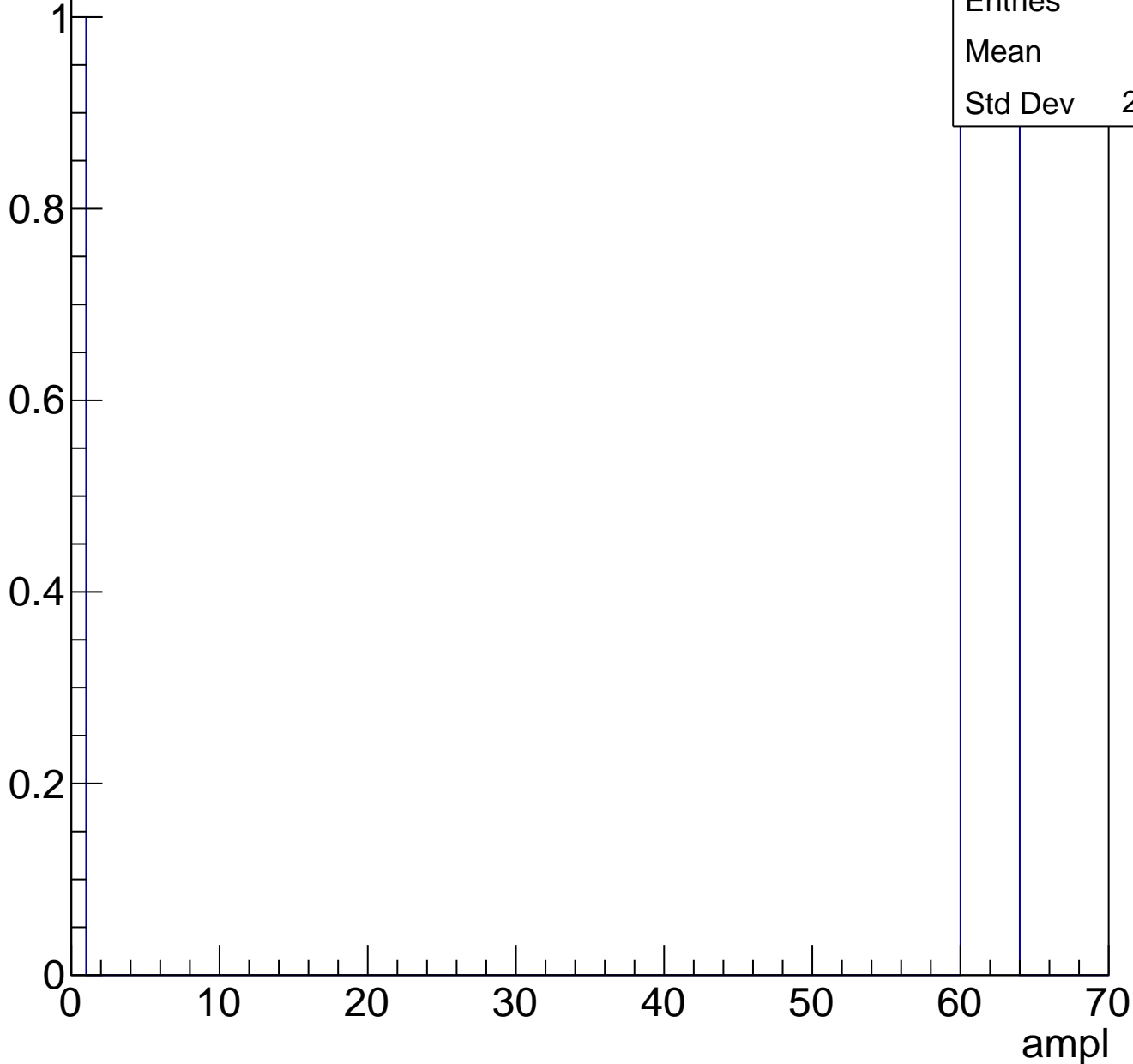
Entries	50
Mean	59.94
Std Dev	2.445



B1L103S, U8-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

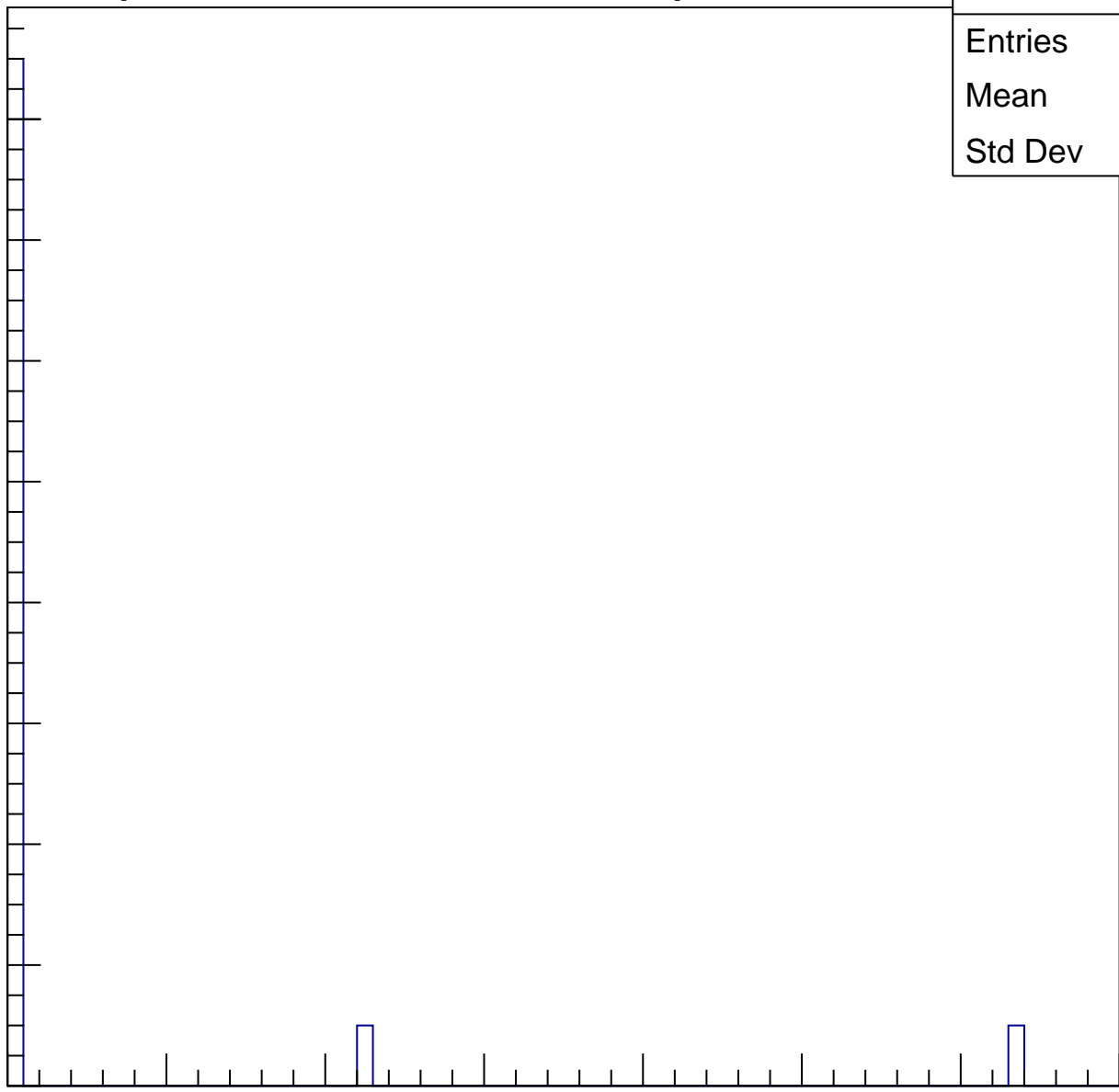
Entries	19
Mean	4.474
Std Dev	14.64

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch88, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	24.05
Std Dev	10.41

Gaus mean : 28.1682

Gaus Width: 3.9156

Entry

12

10

8

6

4

2

0

0

10

20

30

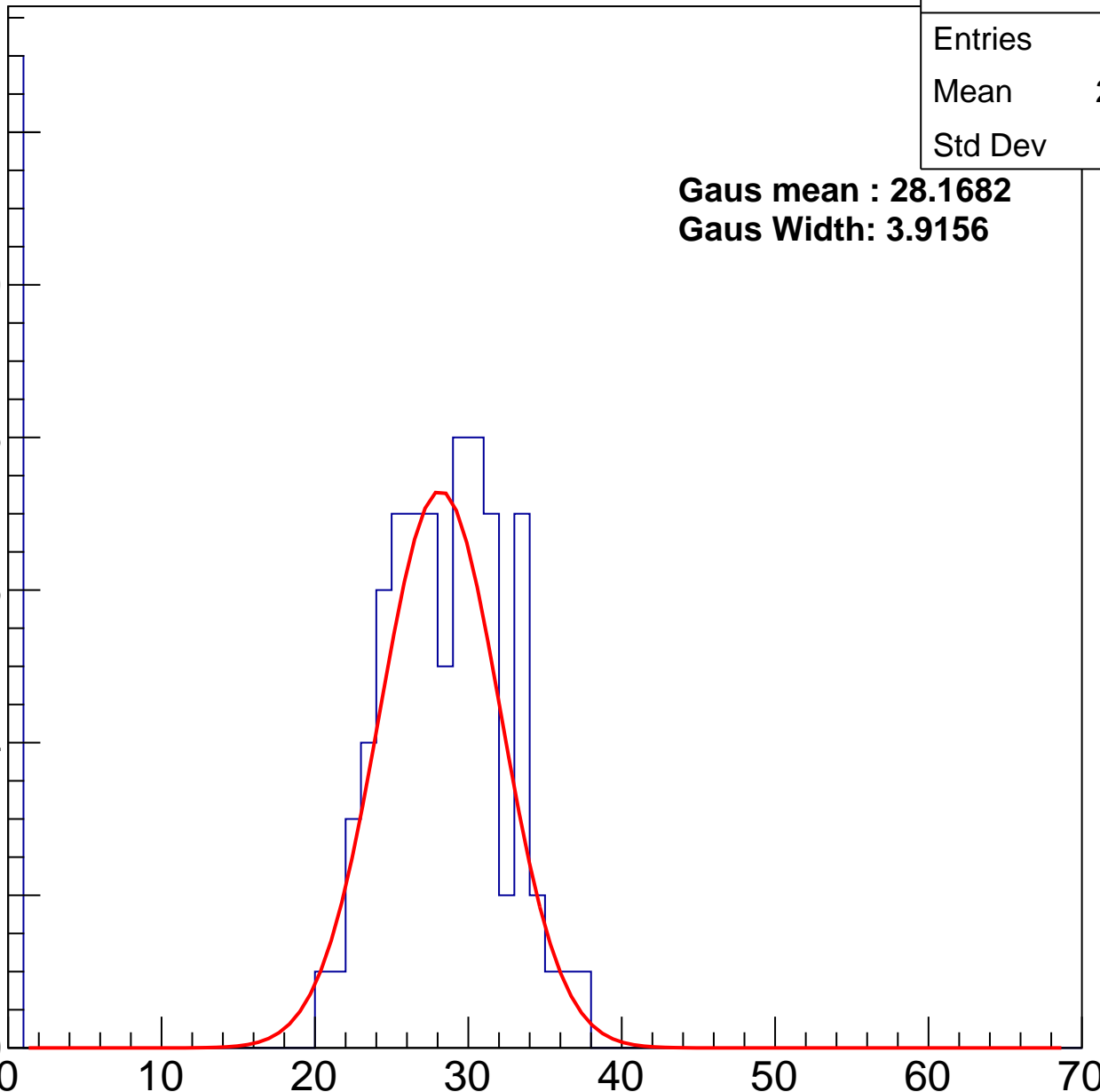
40

50

60

70

ampl



B1L103S, U8-ch88, adc1

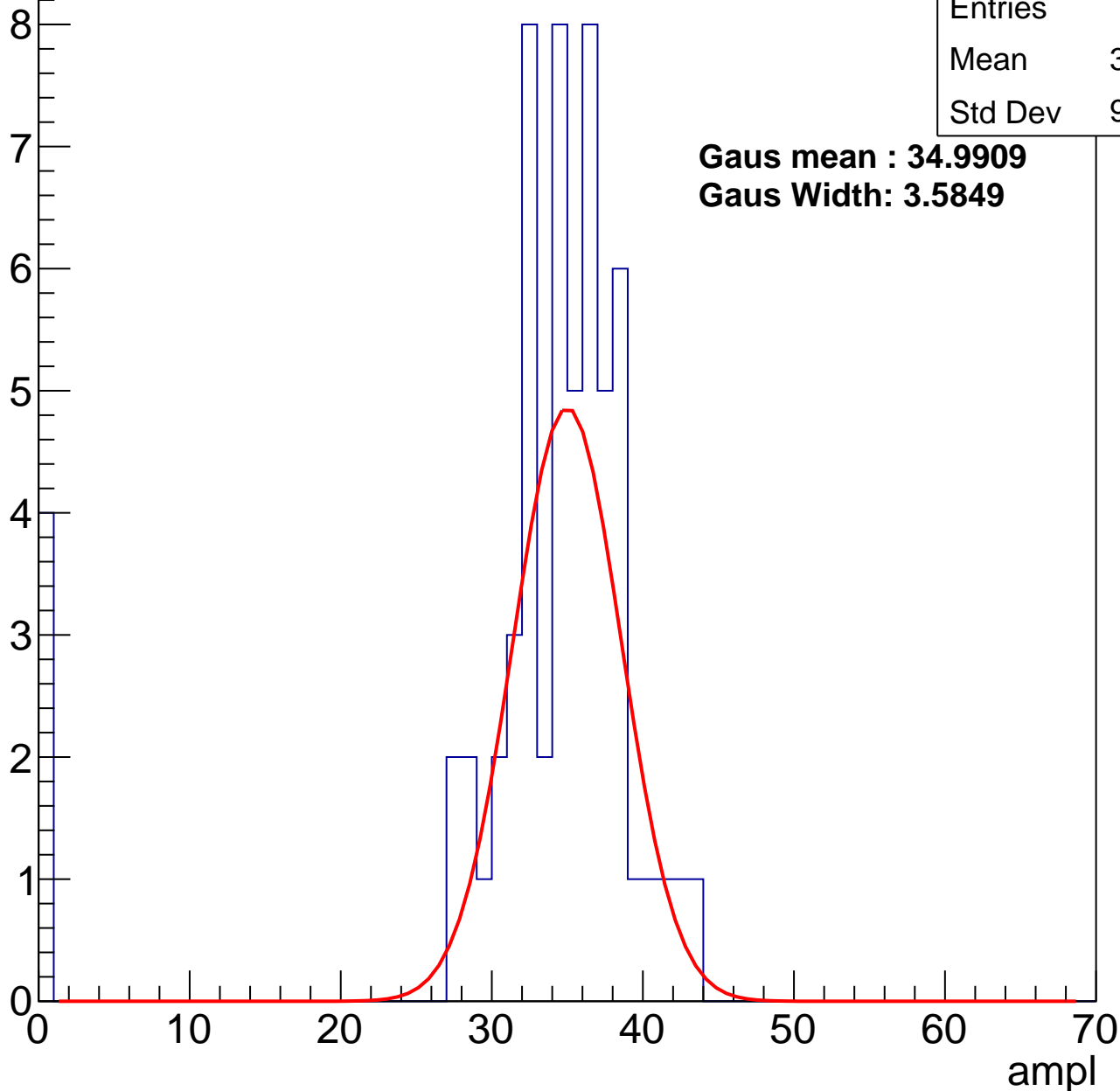
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	32.25
Std Dev	9.195

Gaus mean : 34.9909

Gaus Width: 3.5849



B1L103S, U8-ch88, adc2

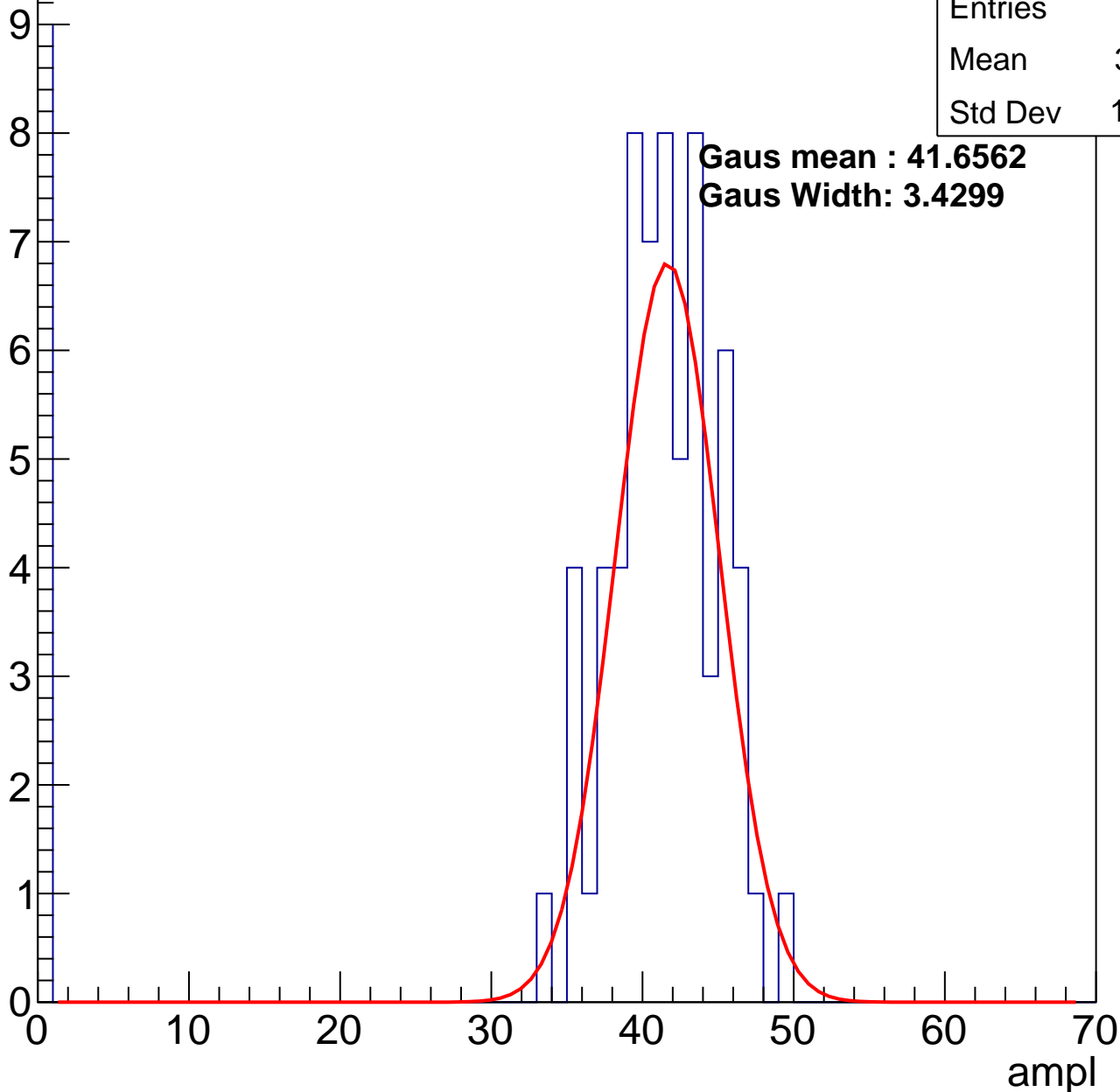
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.01
Std Dev	13.76

Gaus mean : 41.6562

Gaus Width: 3.4299

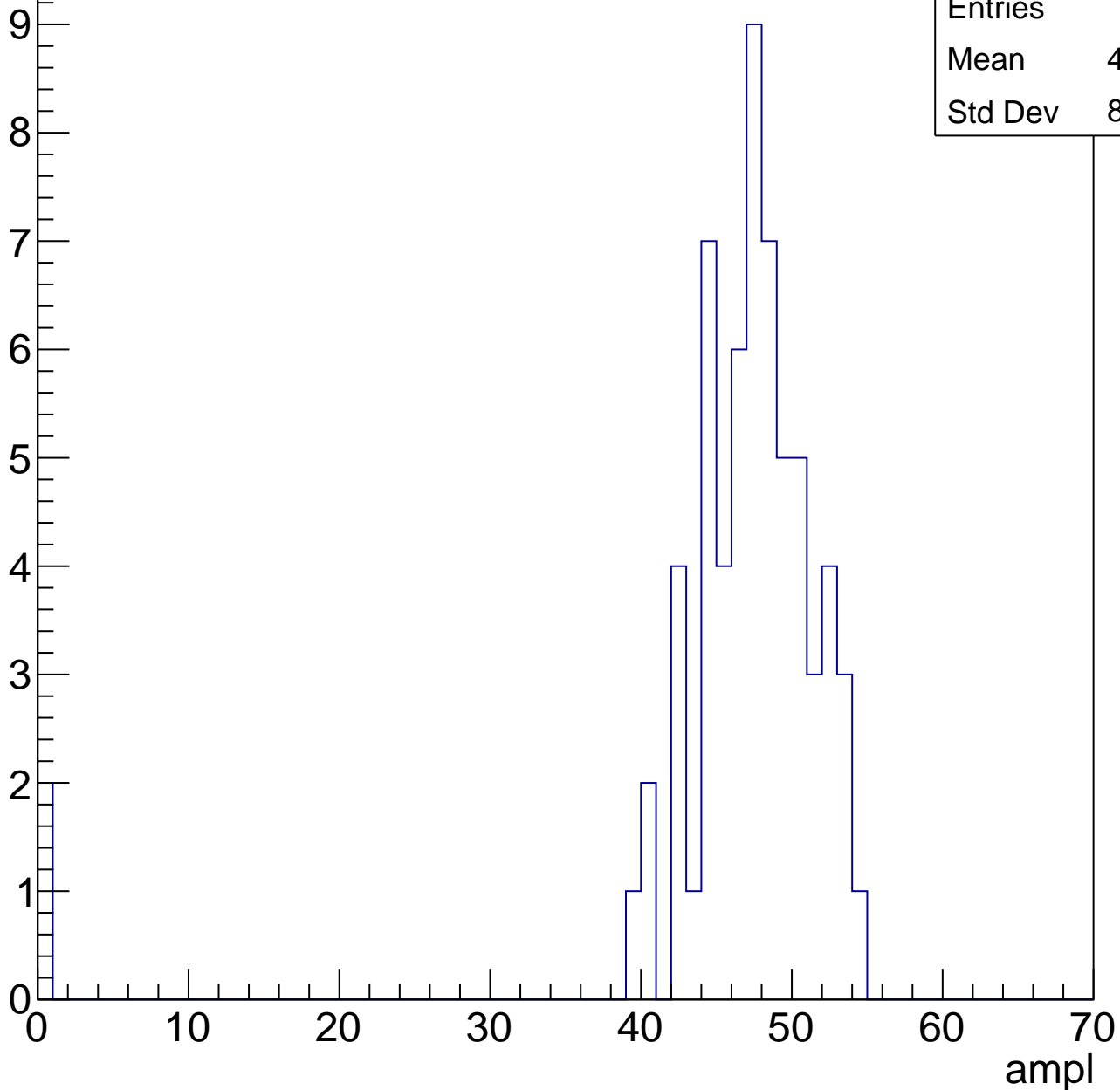


B1L103S, U8-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

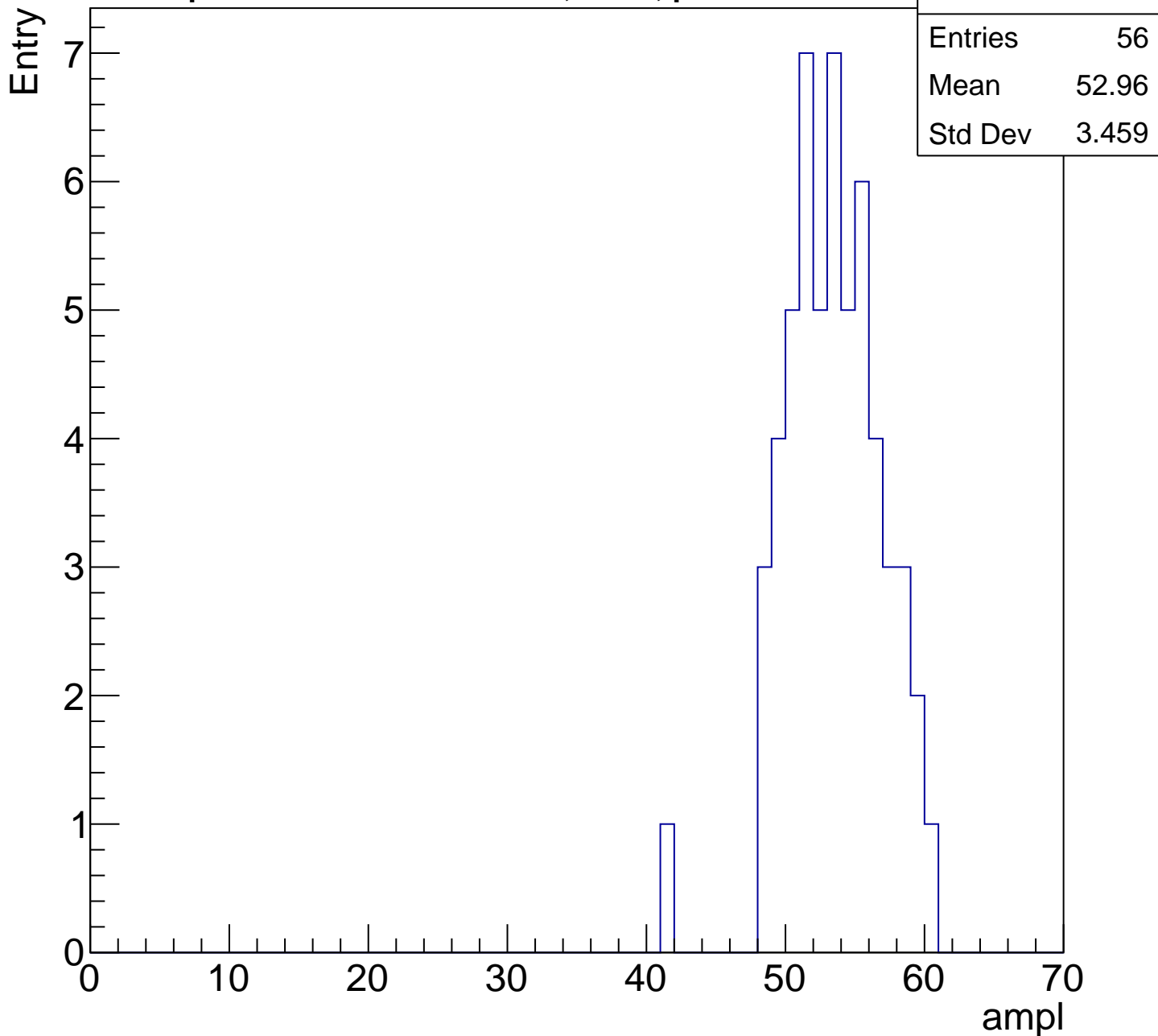
Entry

Entries	64
Mean	45.66
Std Dev	8.876



B1L103S, U8-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

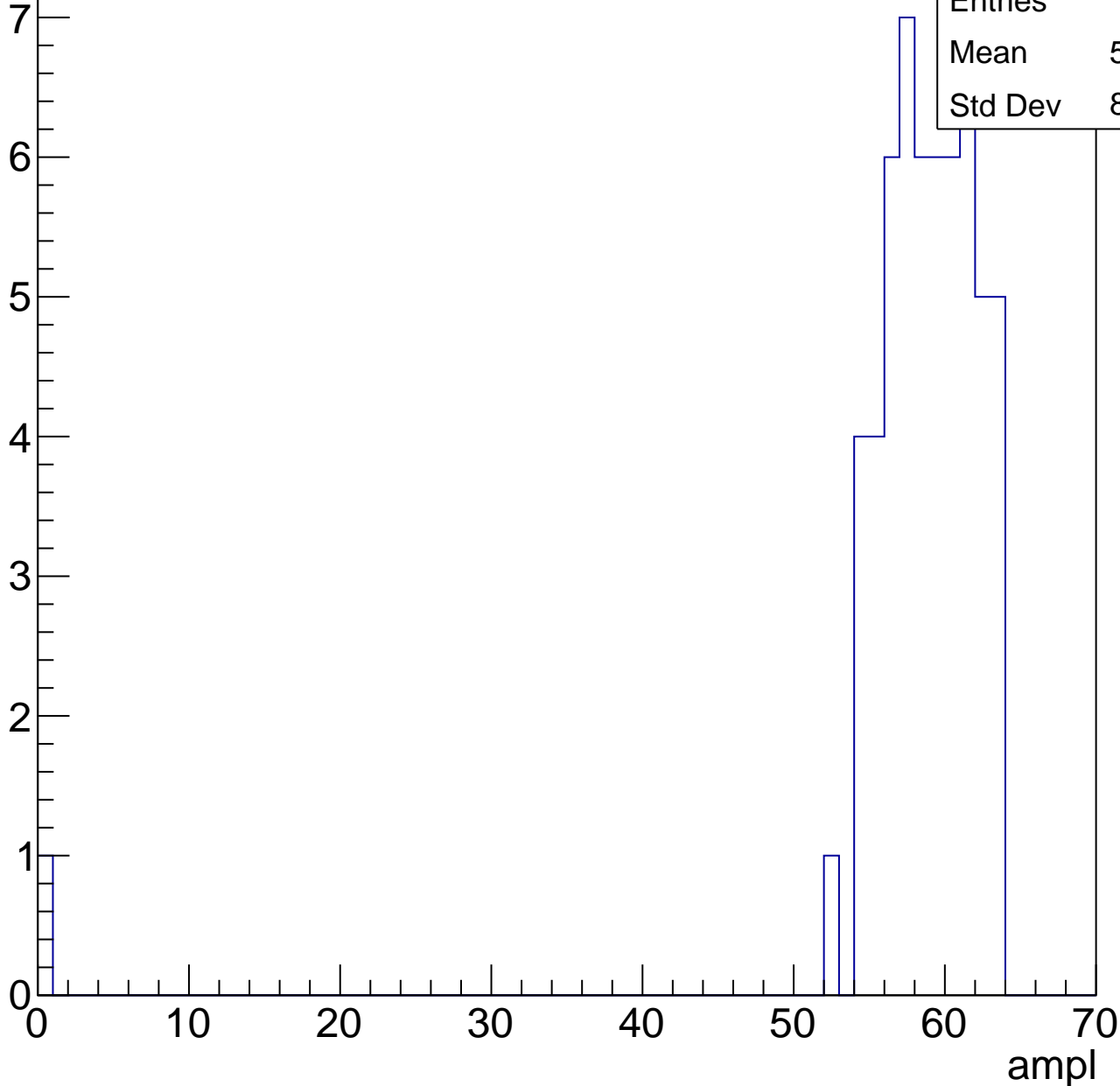


B1L103S, U8-ch88, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.53
Std Dev	8.112

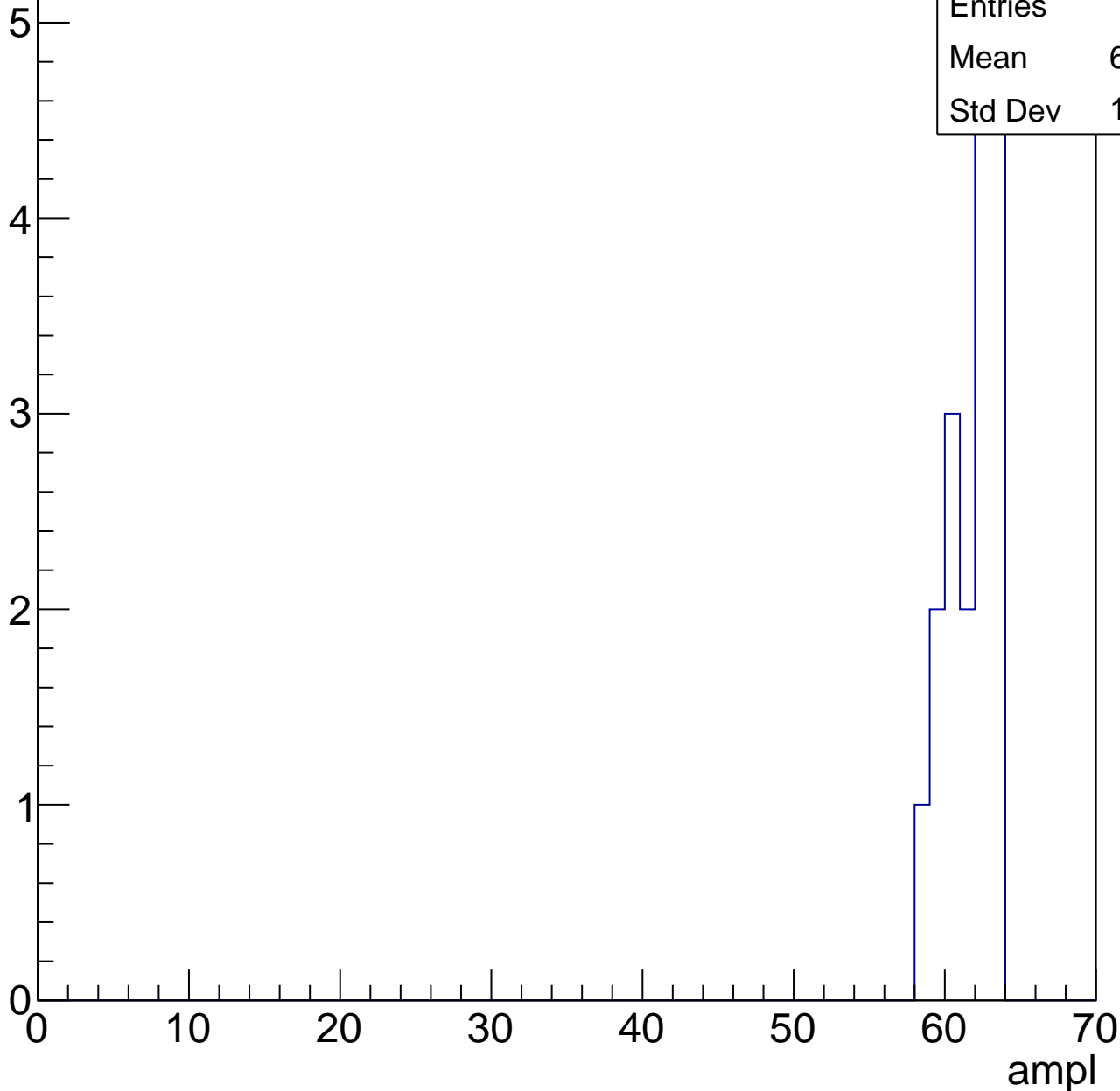


B1L103S, U8-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.28
Std Dev	1.557



B1L103S, U8-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U8-ch89, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	60
Mean	23.53
Std Dev	11.47

Gaus mean : 29.5127

Gaus Width: 3.5236

Entry

10

8

6

4

2

0

0

10

20

30

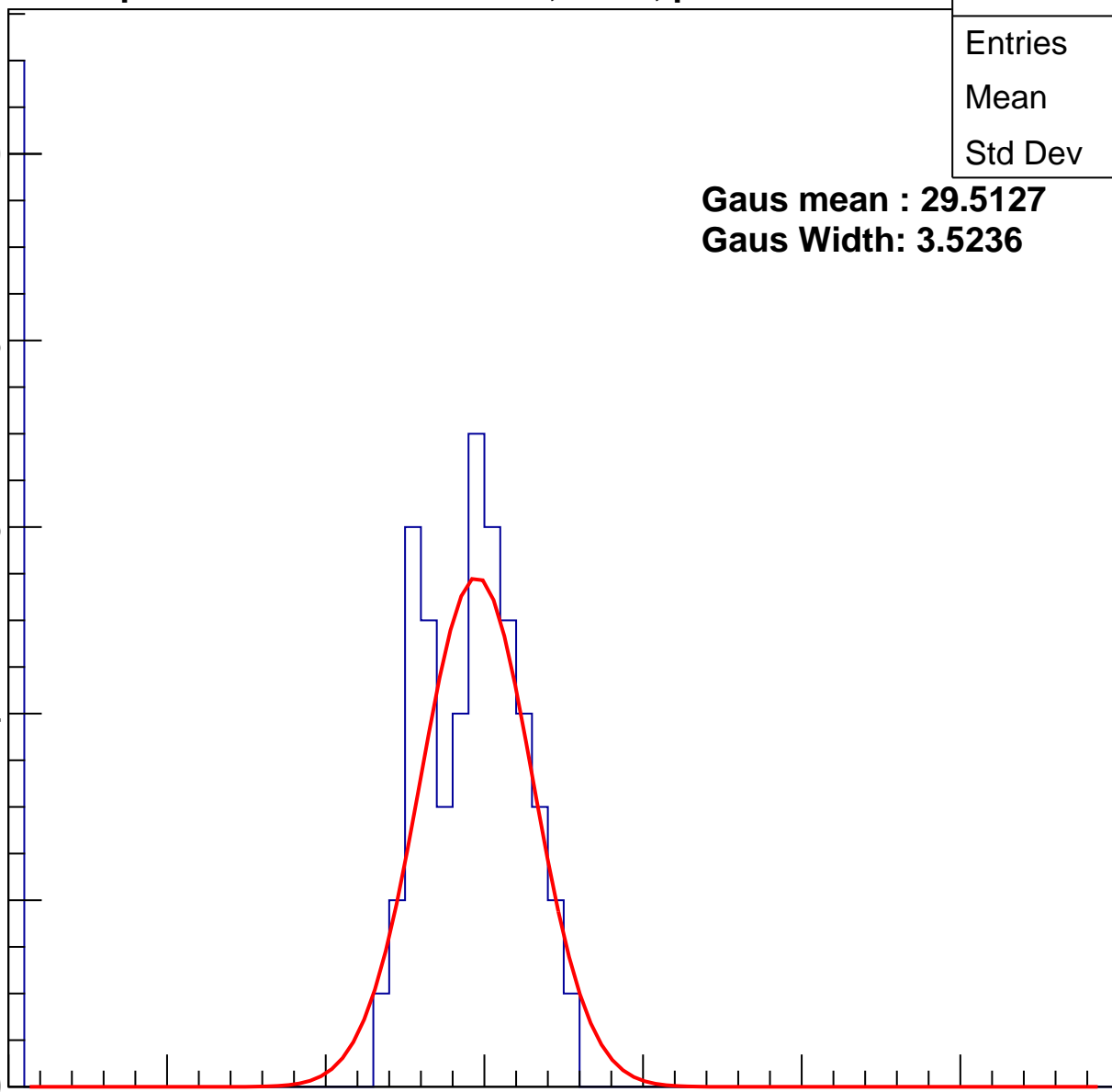
40

50

60

70

ampl



B1L103S, U8-ch89, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	29.3
Std Dev	12.79

Gaus mean : 34.9062

Gaus Width: 3.9770

Entry

12

10

8

6

4

2

0

0

10

20

30

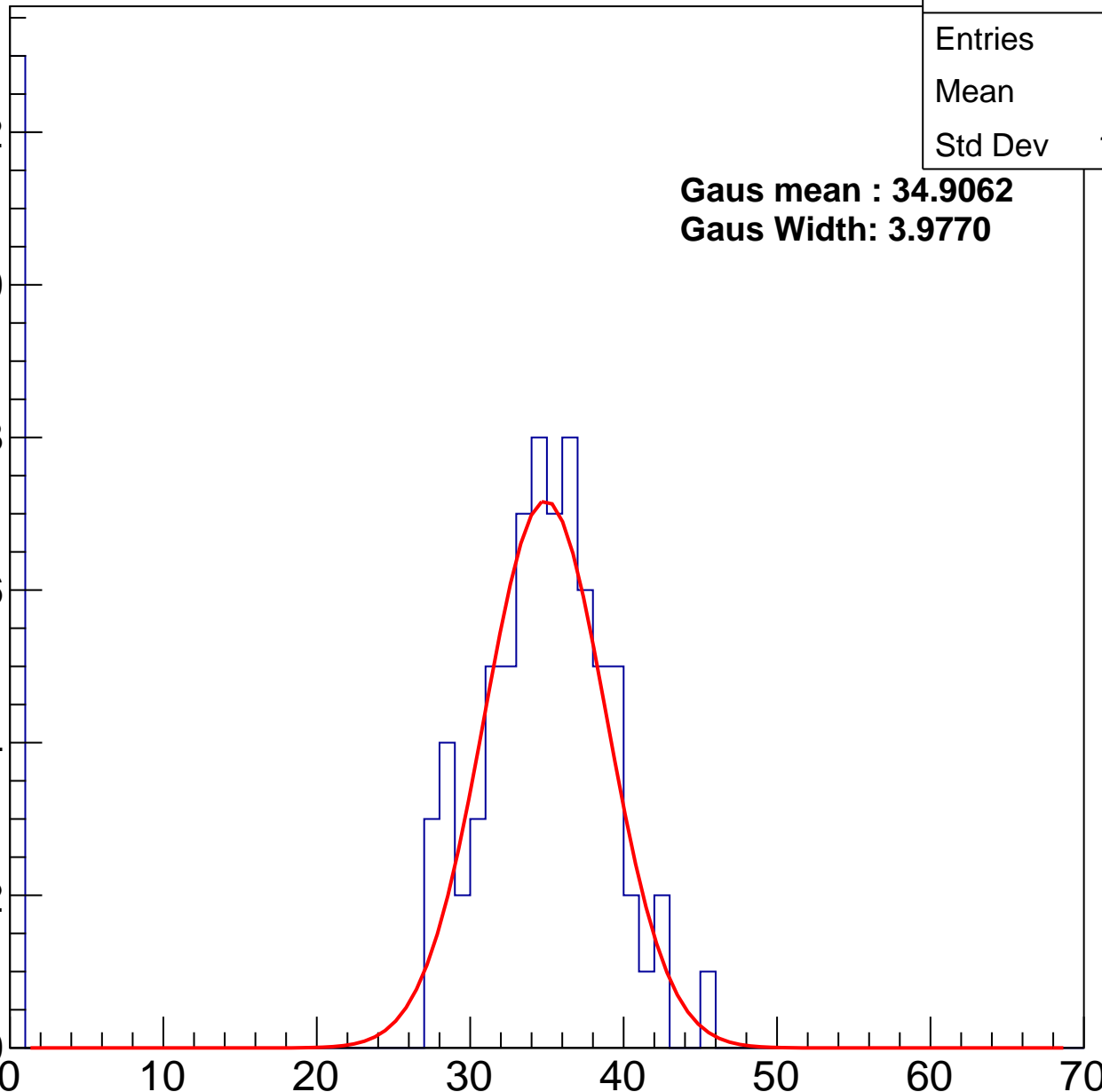
40

50

60

70

ampl



B1L103S, U8-ch89, adc2

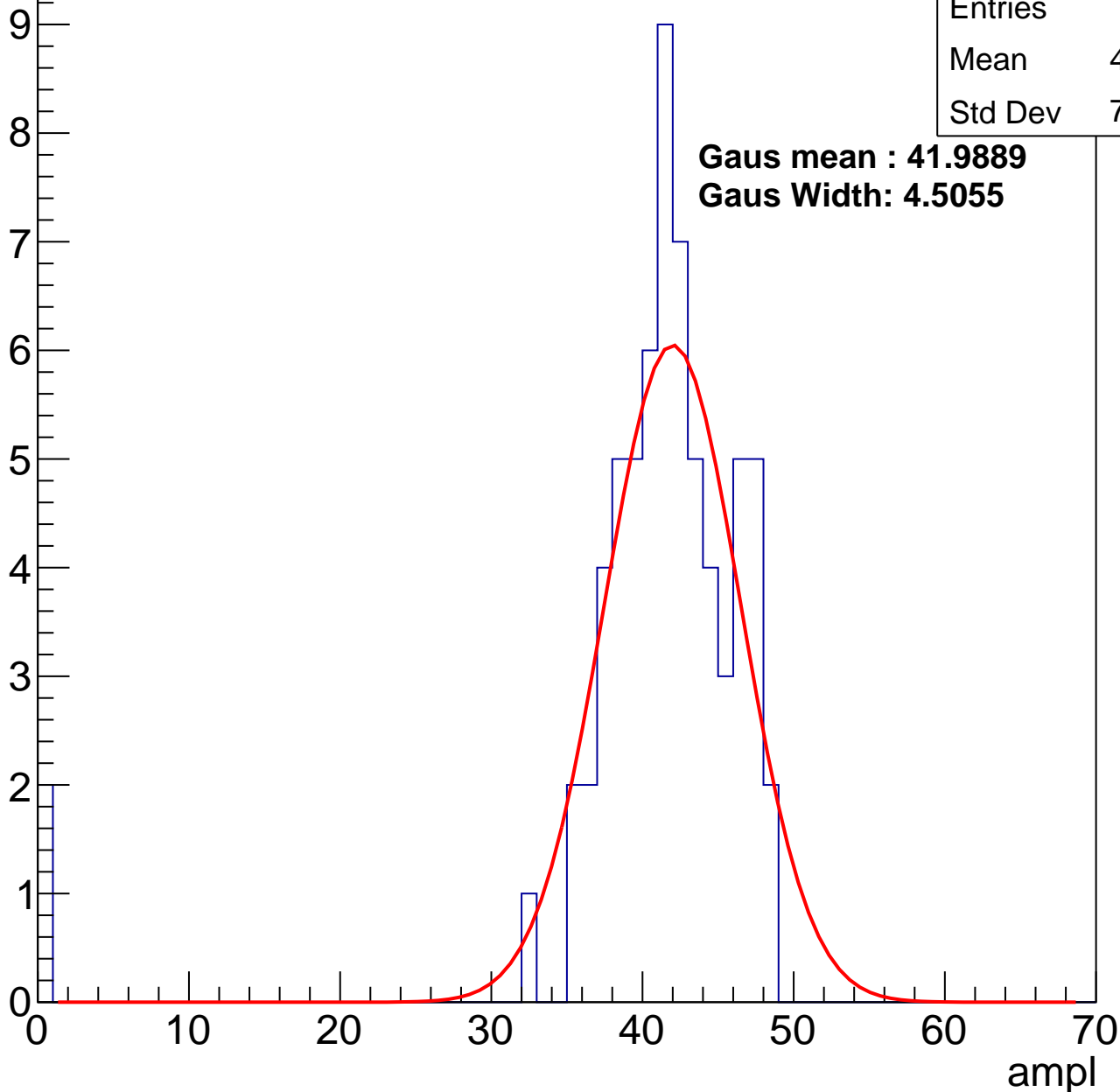
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	40.25
Std Dev	7.895

Gaus mean : 41.9889

Gaus Width: 4.5055

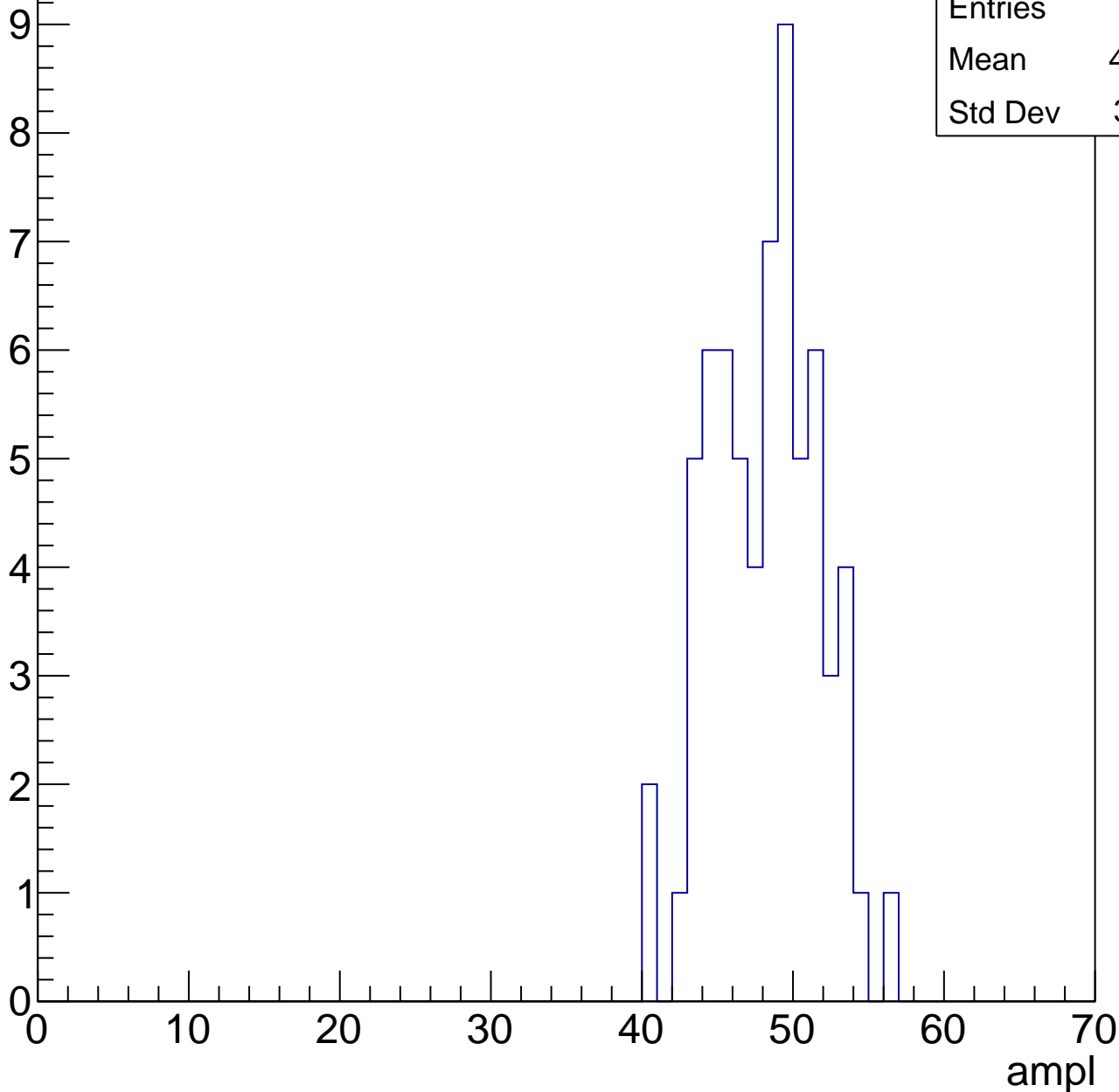


B1L103S, U8-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	47.69
Std Dev	3.481

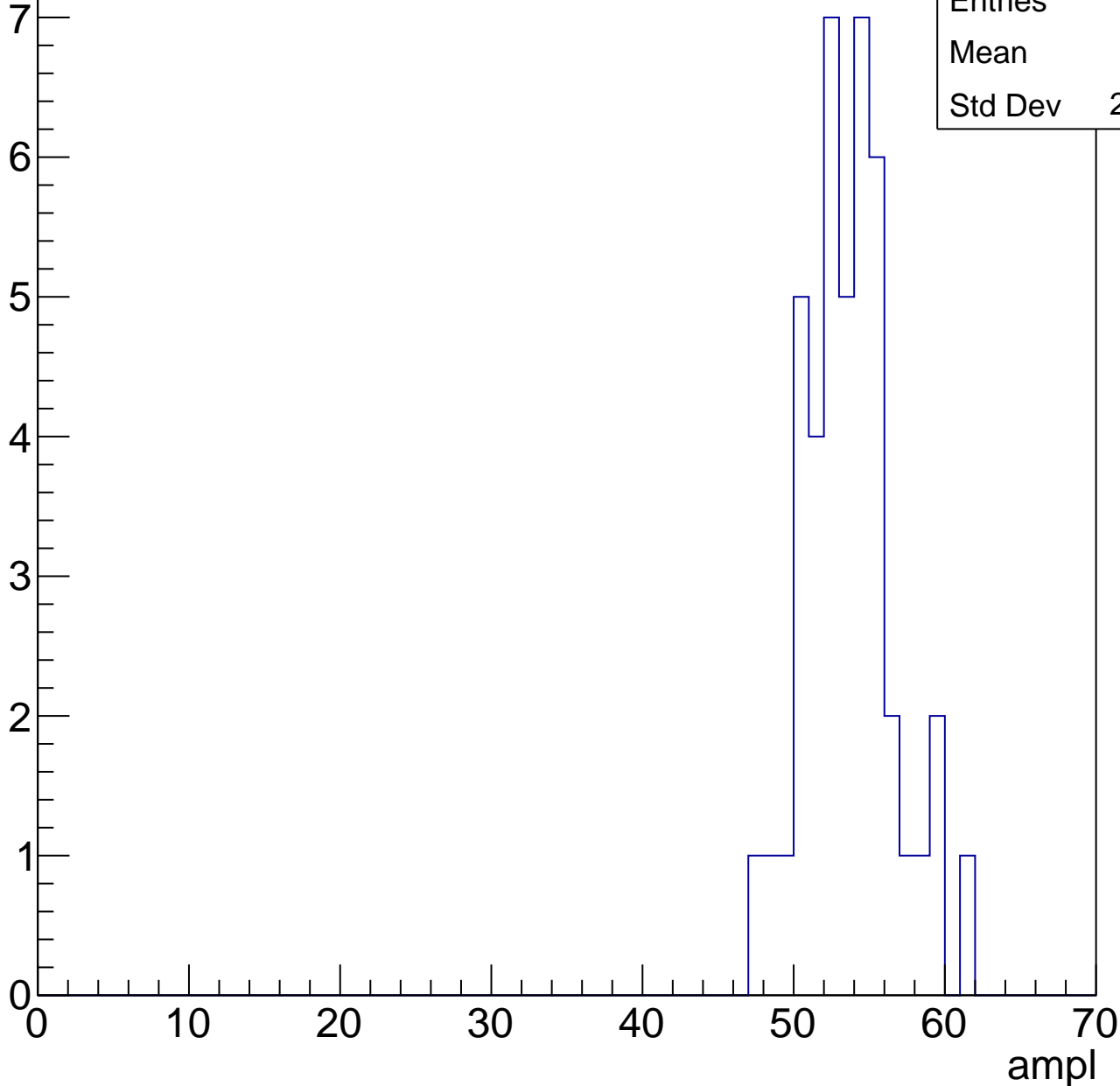


B1L103S, U8-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	53.2
Std Dev	2.897

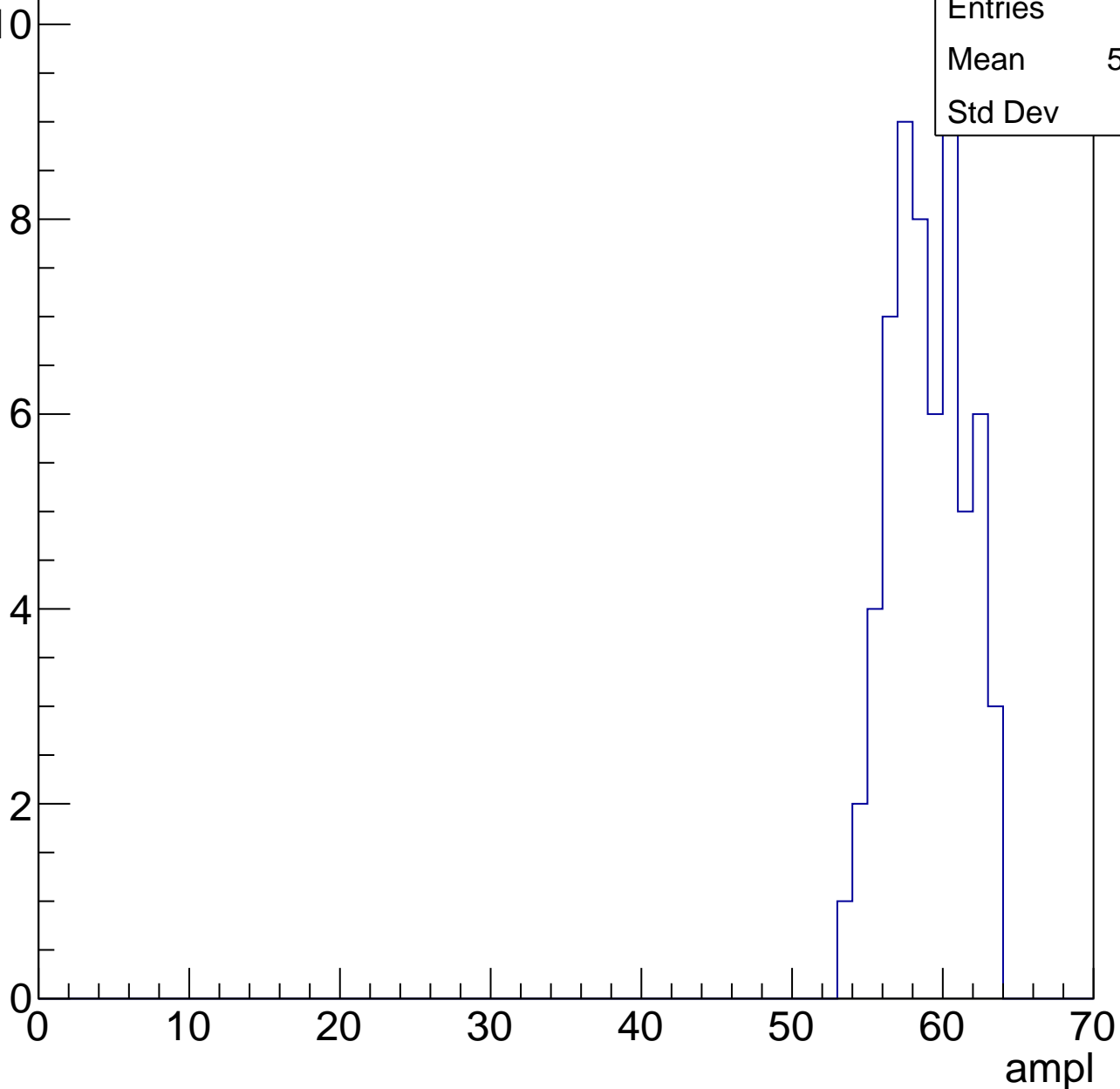


B1L103S, U8-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	58.52
Std Dev	2.48

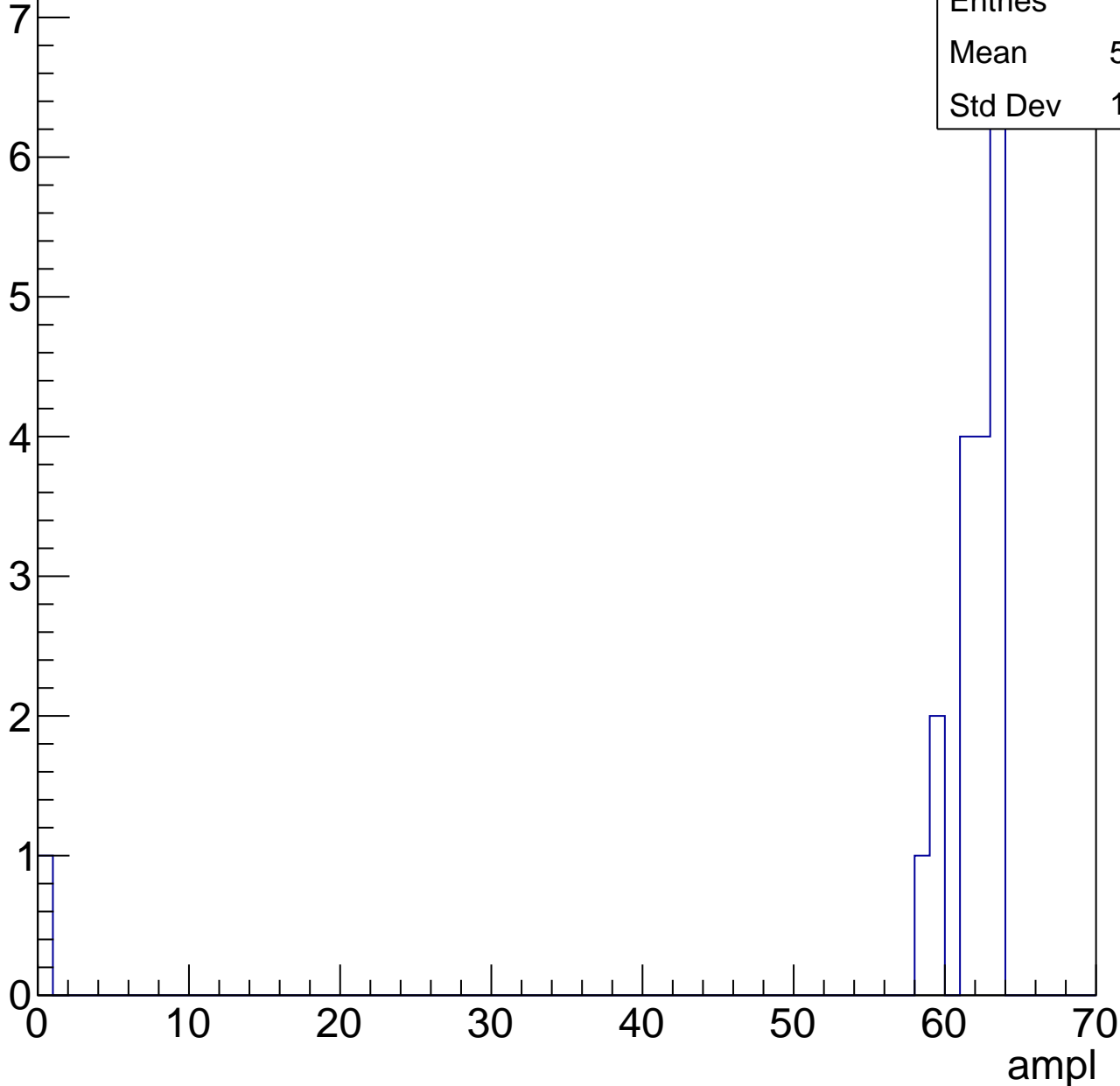


B1L103S, U8-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58.37
Std Dev	13.84



B1L103S, U8-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



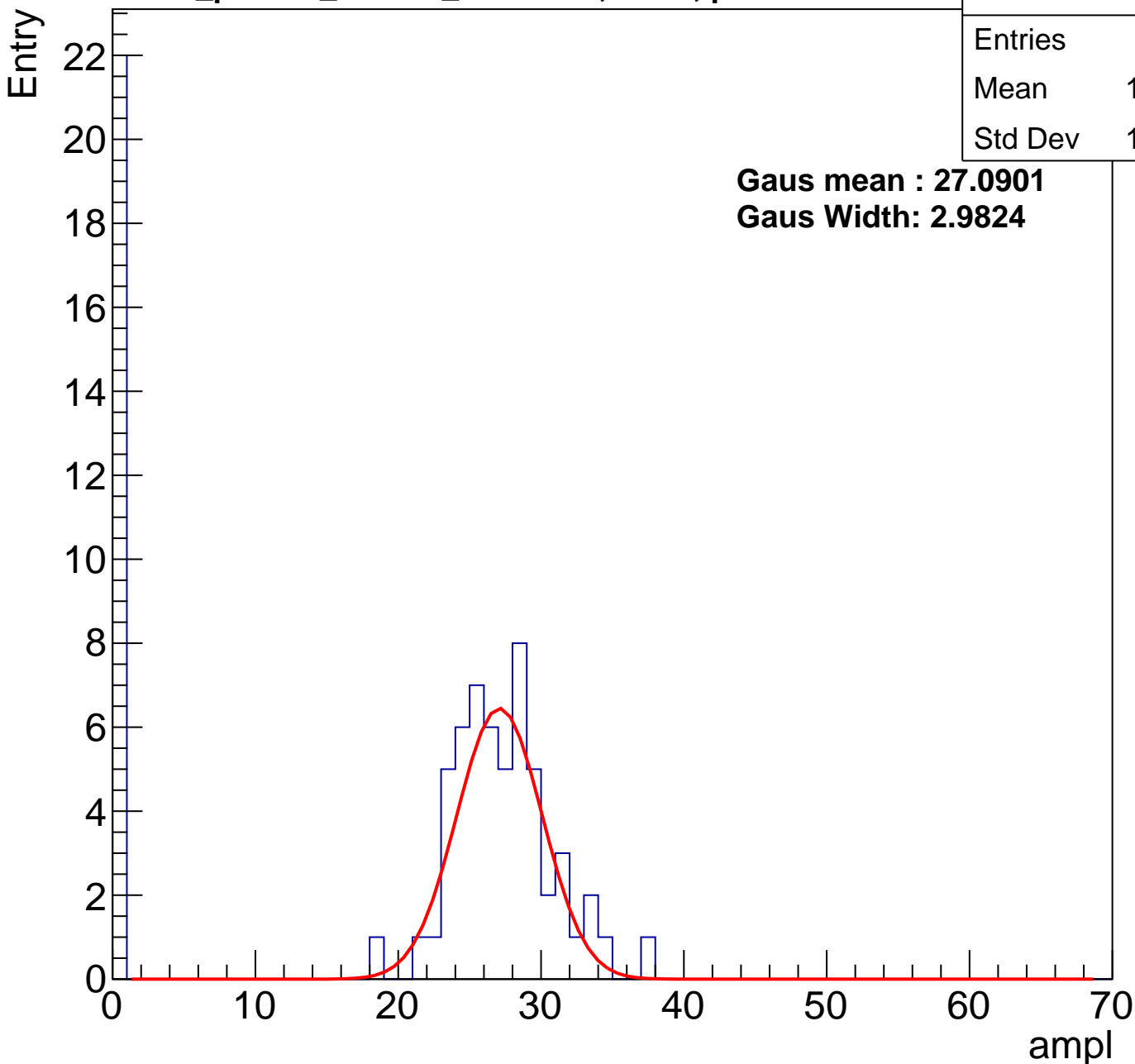
B1L103S, U8-ch90, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	19.18
Std Dev	12.47

Gaus mean : 27.0901

Gaus Width: 2.9824



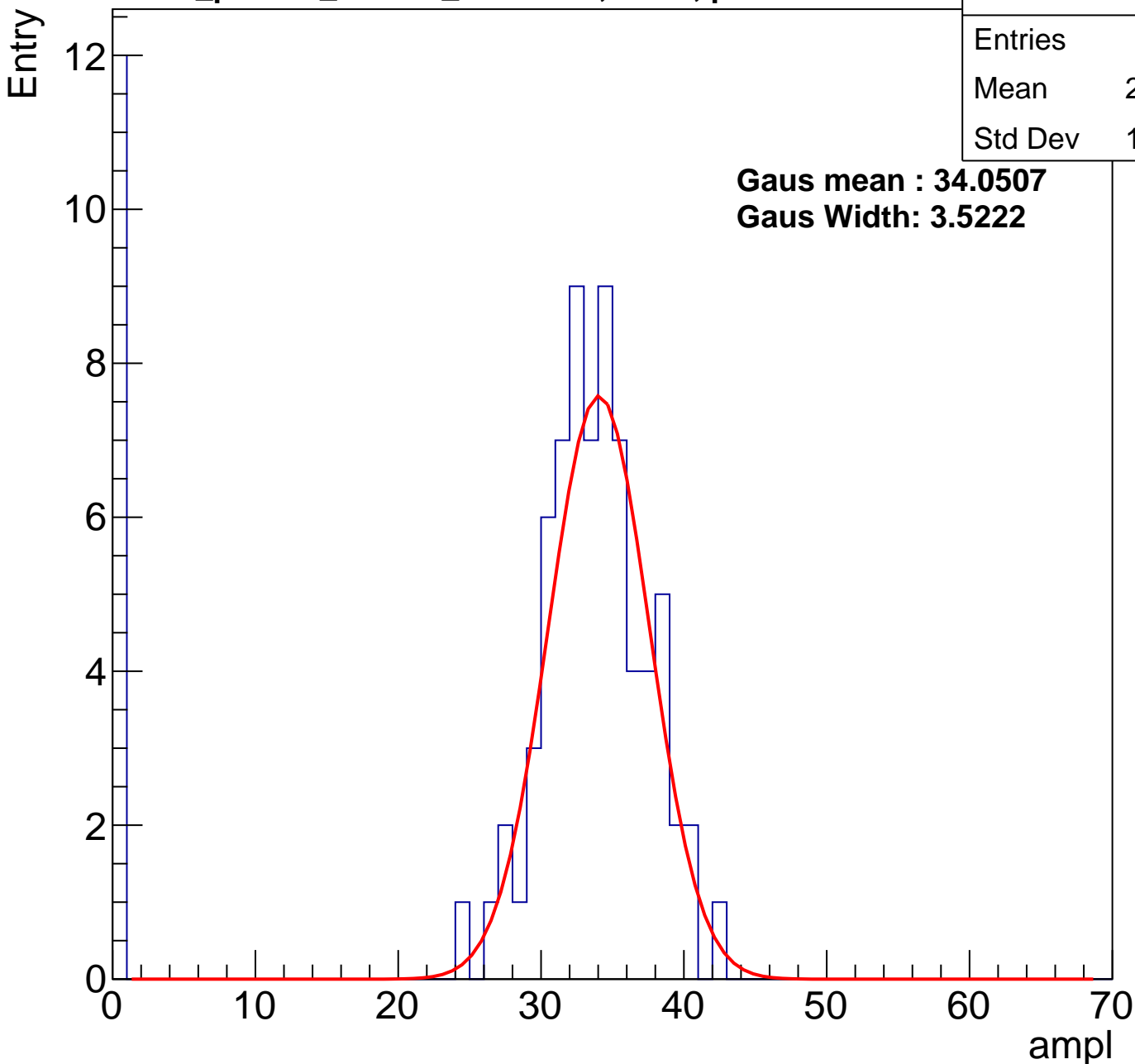
B1L103S, U8-ch90, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	28.53
Std Dev	12.17

Gaus mean : 34.0507

Gaus Width: 3.5222



B1L103S, U8-ch90, adc2

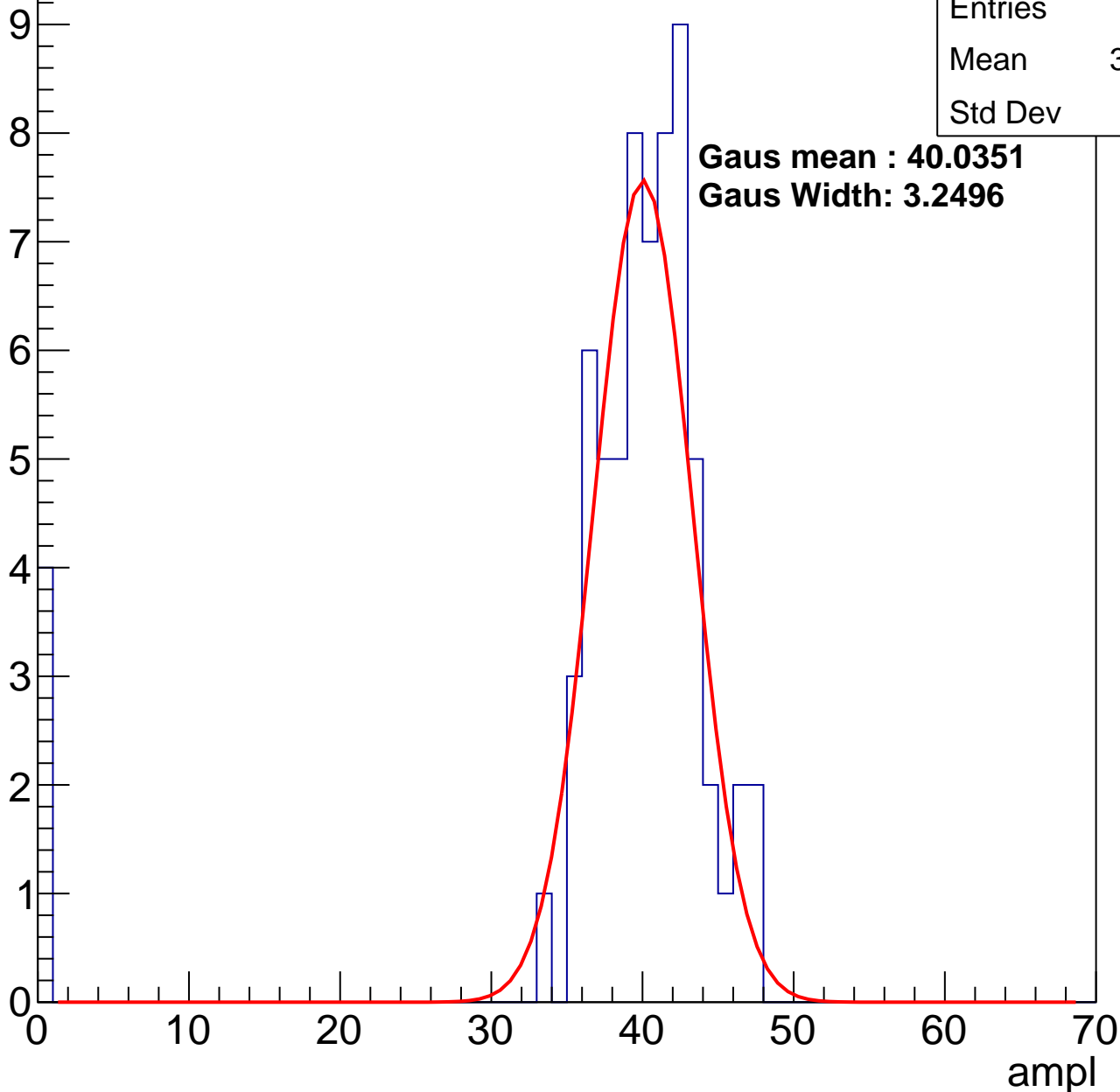
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.66
Std Dev	9.88

Gaus mean : 40.0351

Gaus Width: 3.2496



B1L103S, U8-ch90, adc3

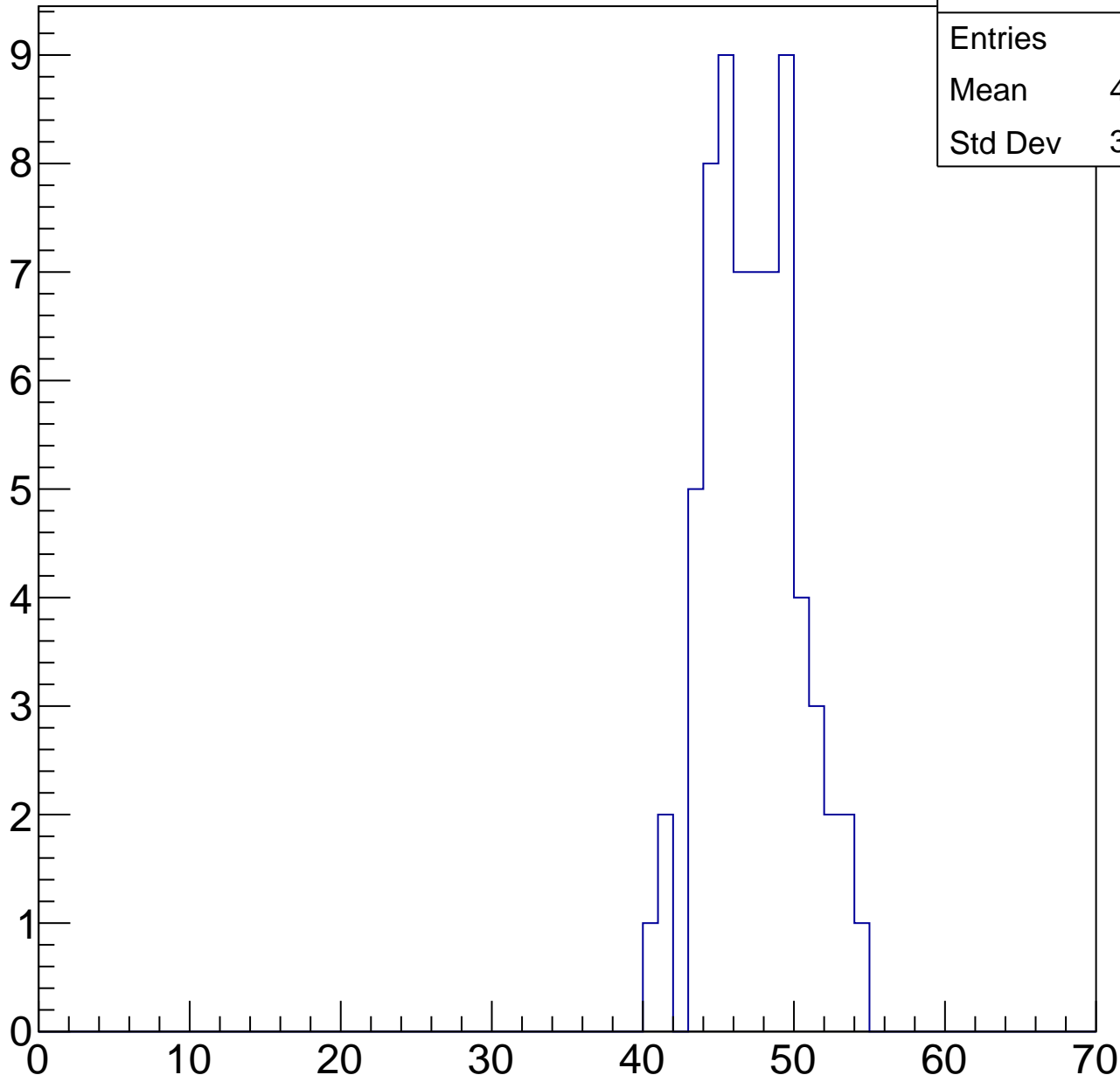
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	46.85
Std Dev	3.024

ampl

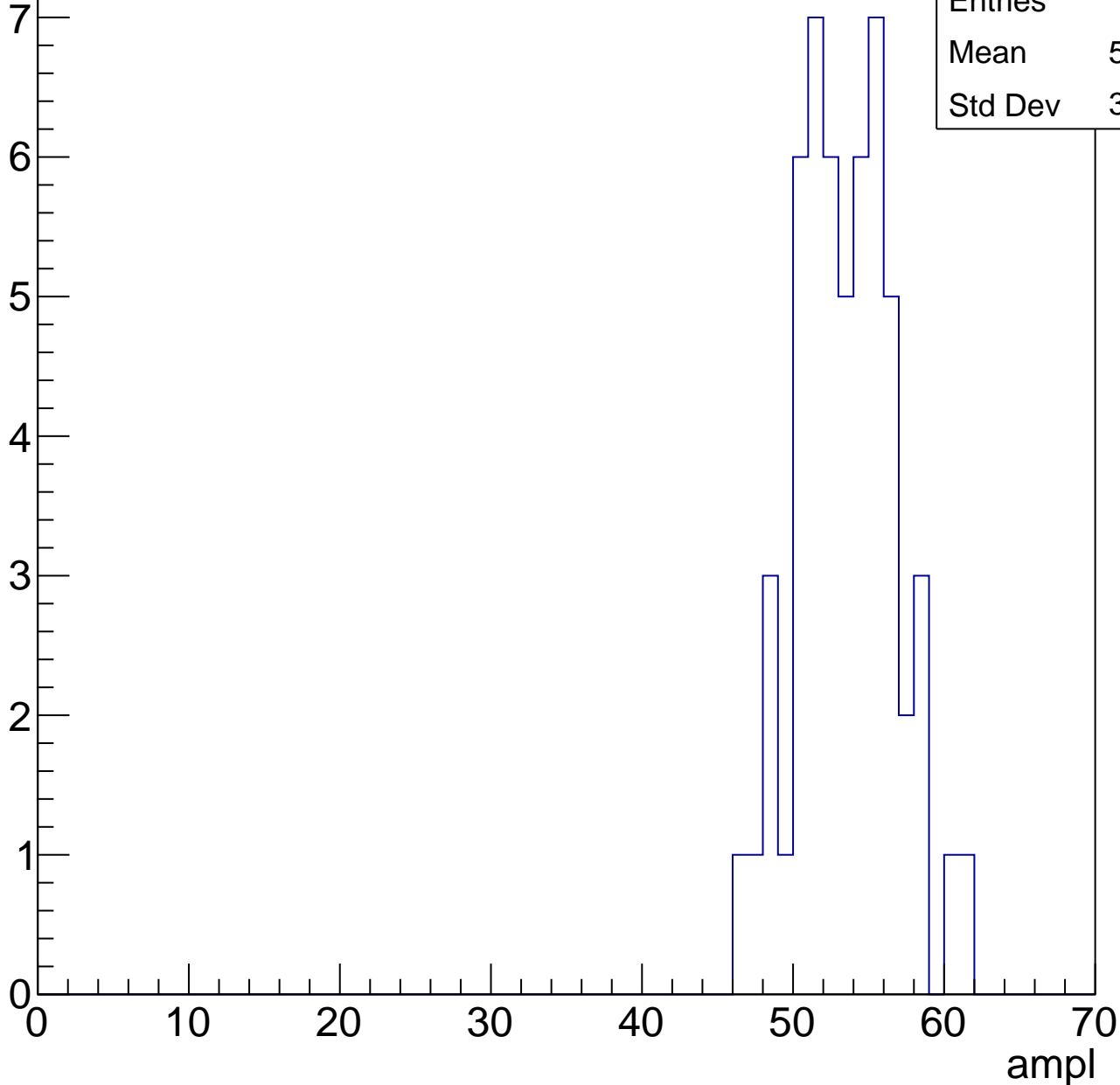


B1L103S, U8-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.05
Std Dev	3.199

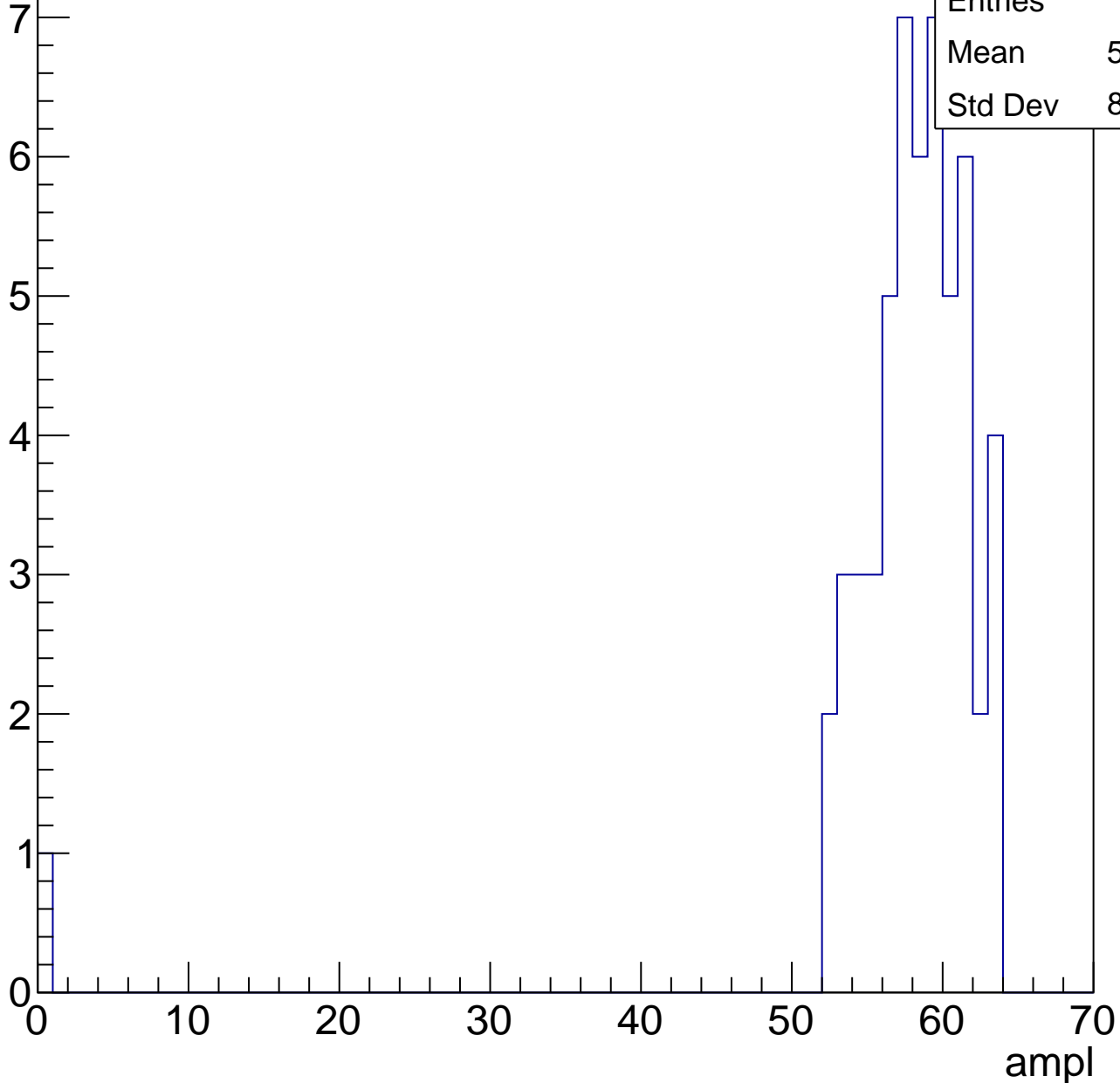


B1L103S, U8-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.89
Std Dev	8.344

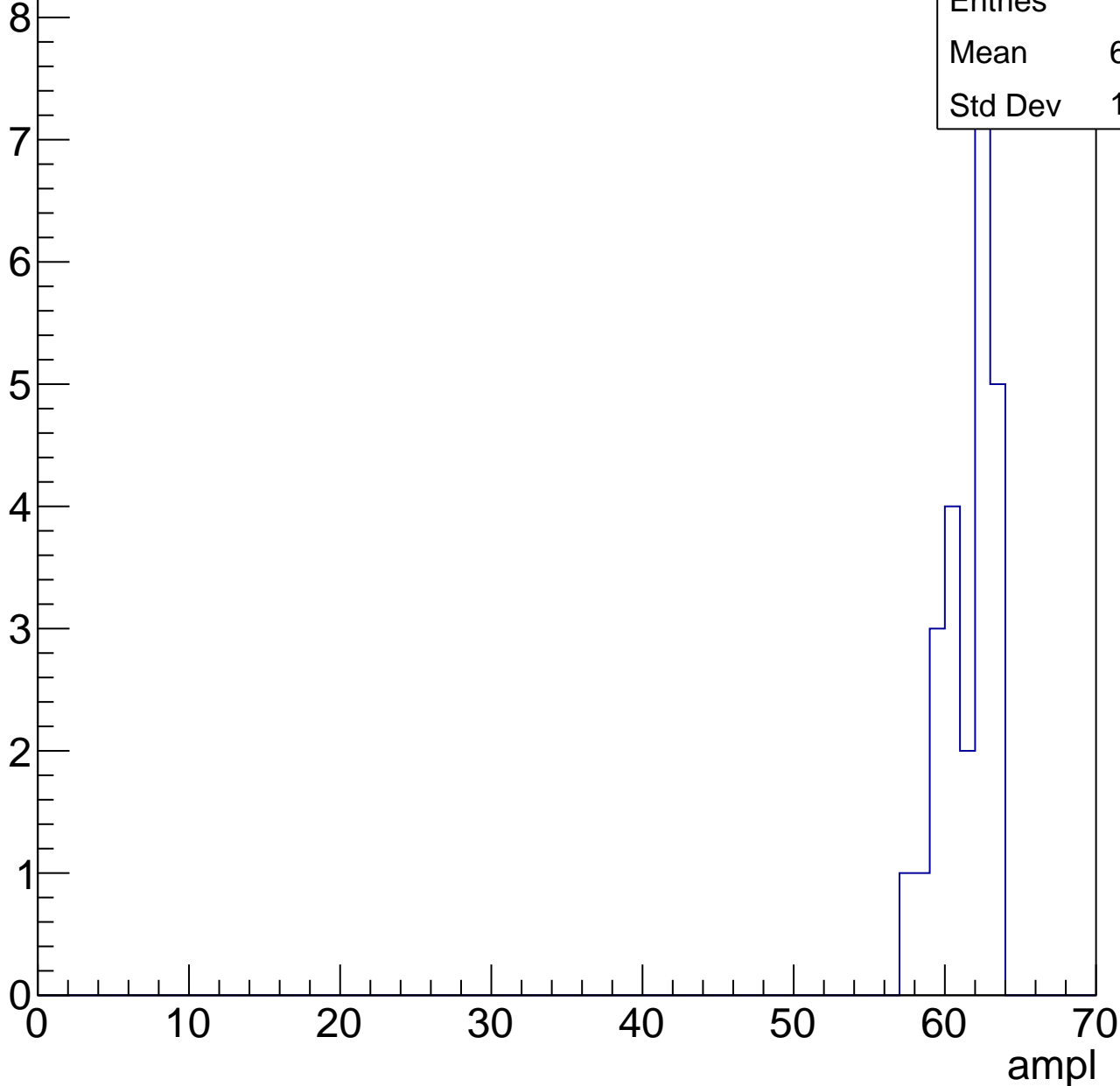


B1L103S, U8-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	61.04
Std Dev	1.695

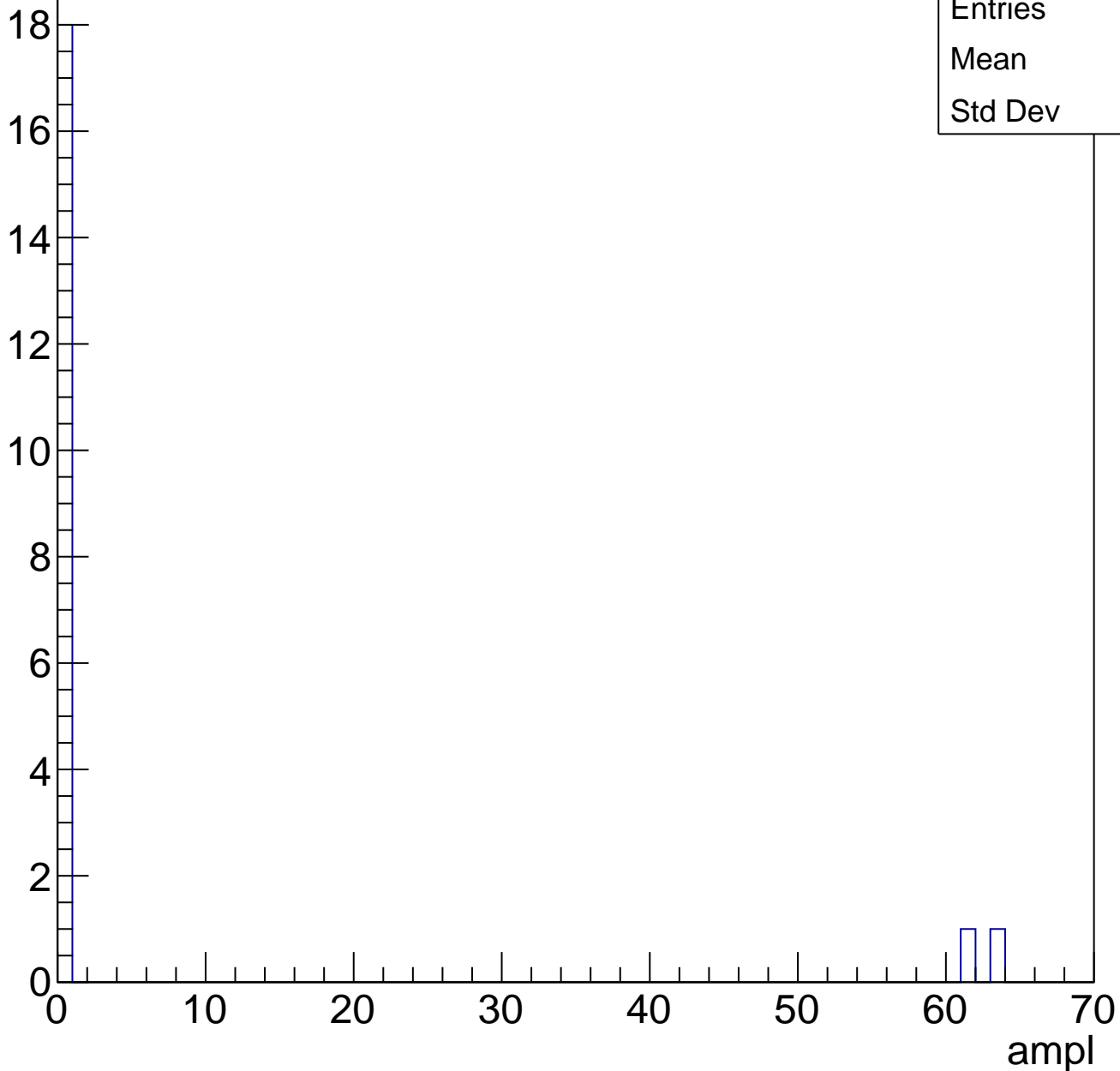


B1L103S, U8-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.2
Std Dev	18.6

Entry



B1L103S, U8-ch91, adc0

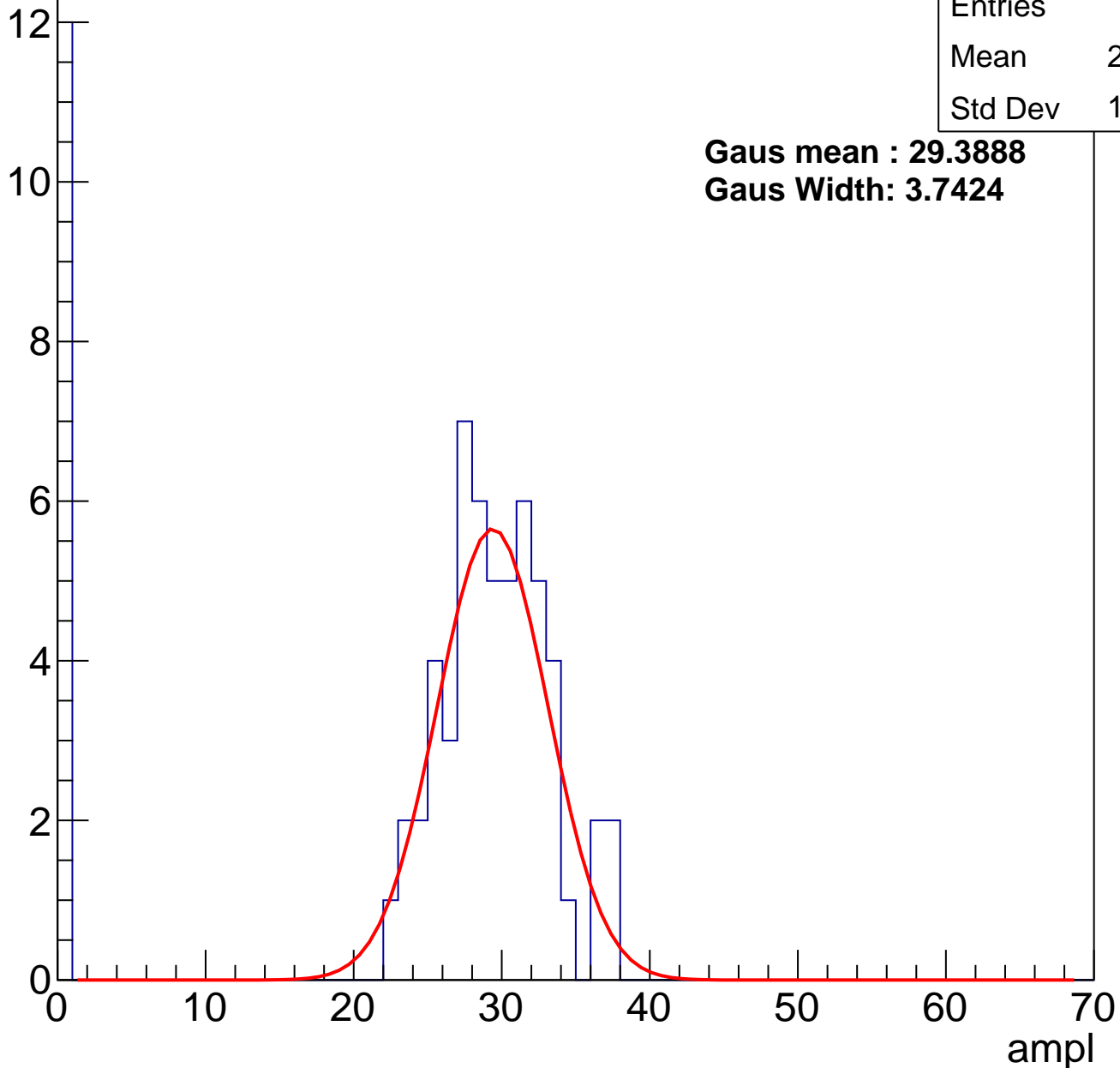
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	23.94
Std Dev	11.63

Gaus mean : 29.3888

Gaus Width: 3.7424

Entry



B1L103S, U8-ch91, adc1

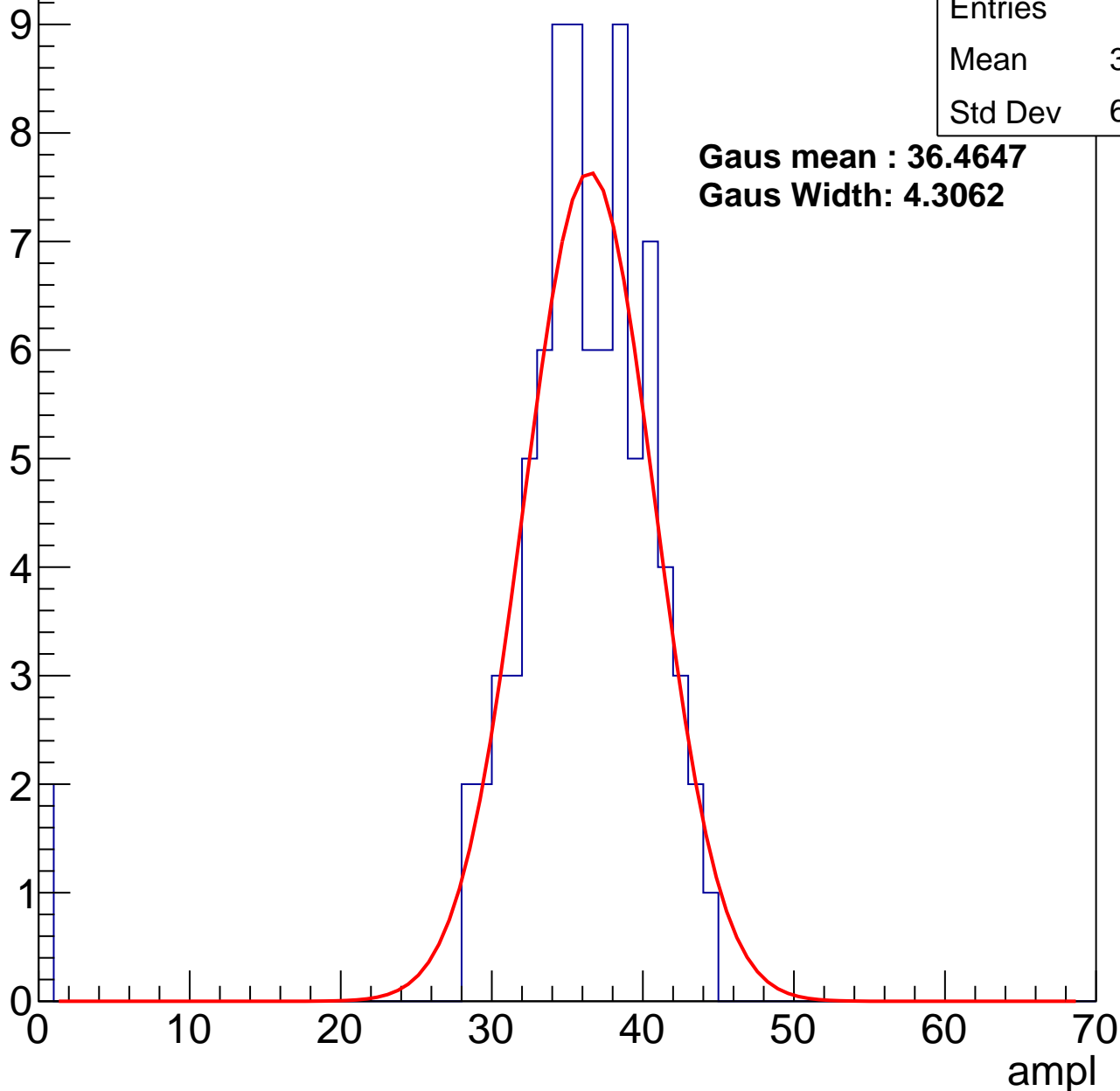
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	35.13
Std Dev	6.624

Gaus mean : 36.4647

Gaus Width: 4.3062



B1L103S, U8-ch91, adc2

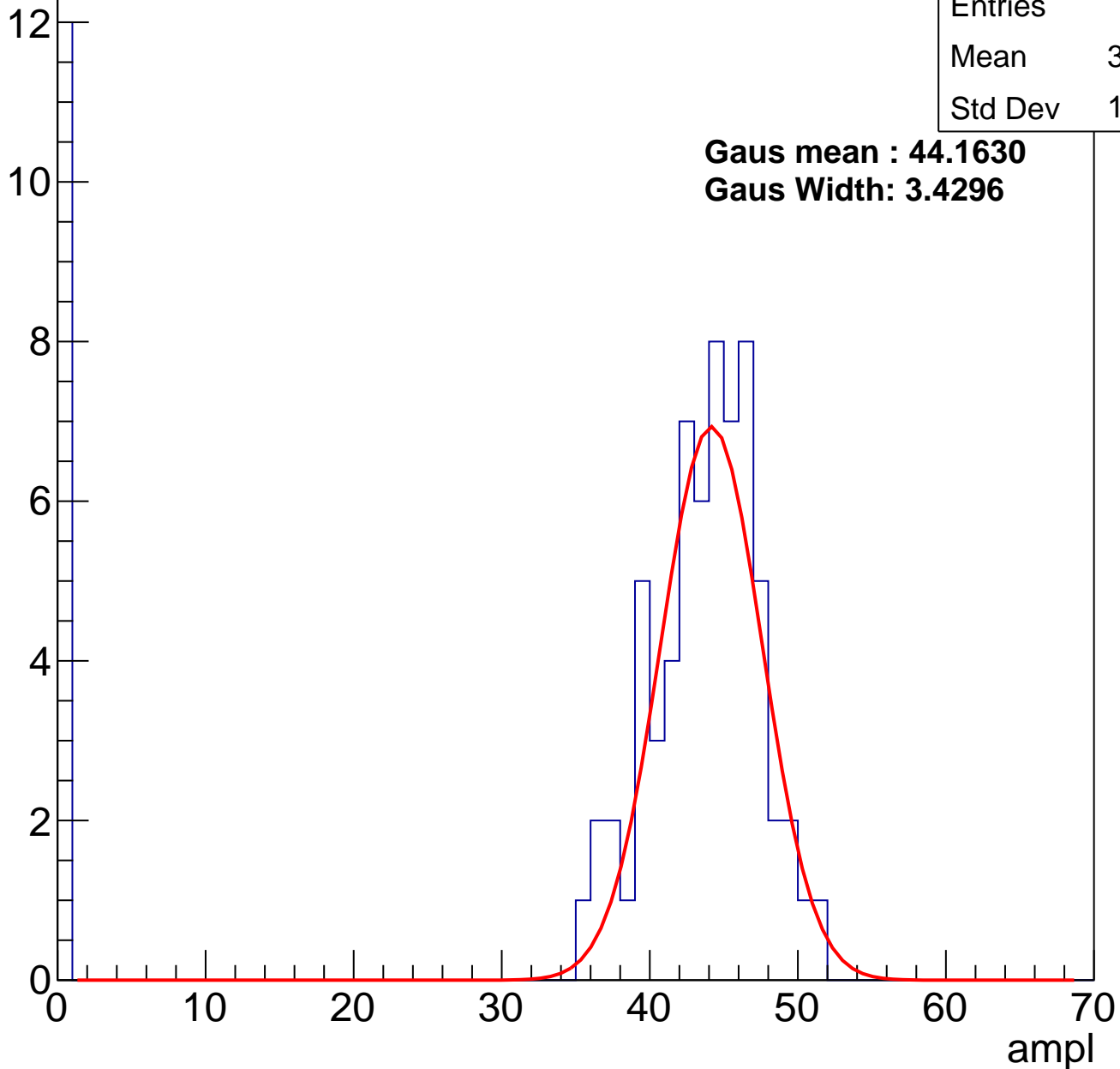
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	36.56
Std Dev	16.04

Gaus mean : 44.1630

Gaus Width: 3.4296

Entry

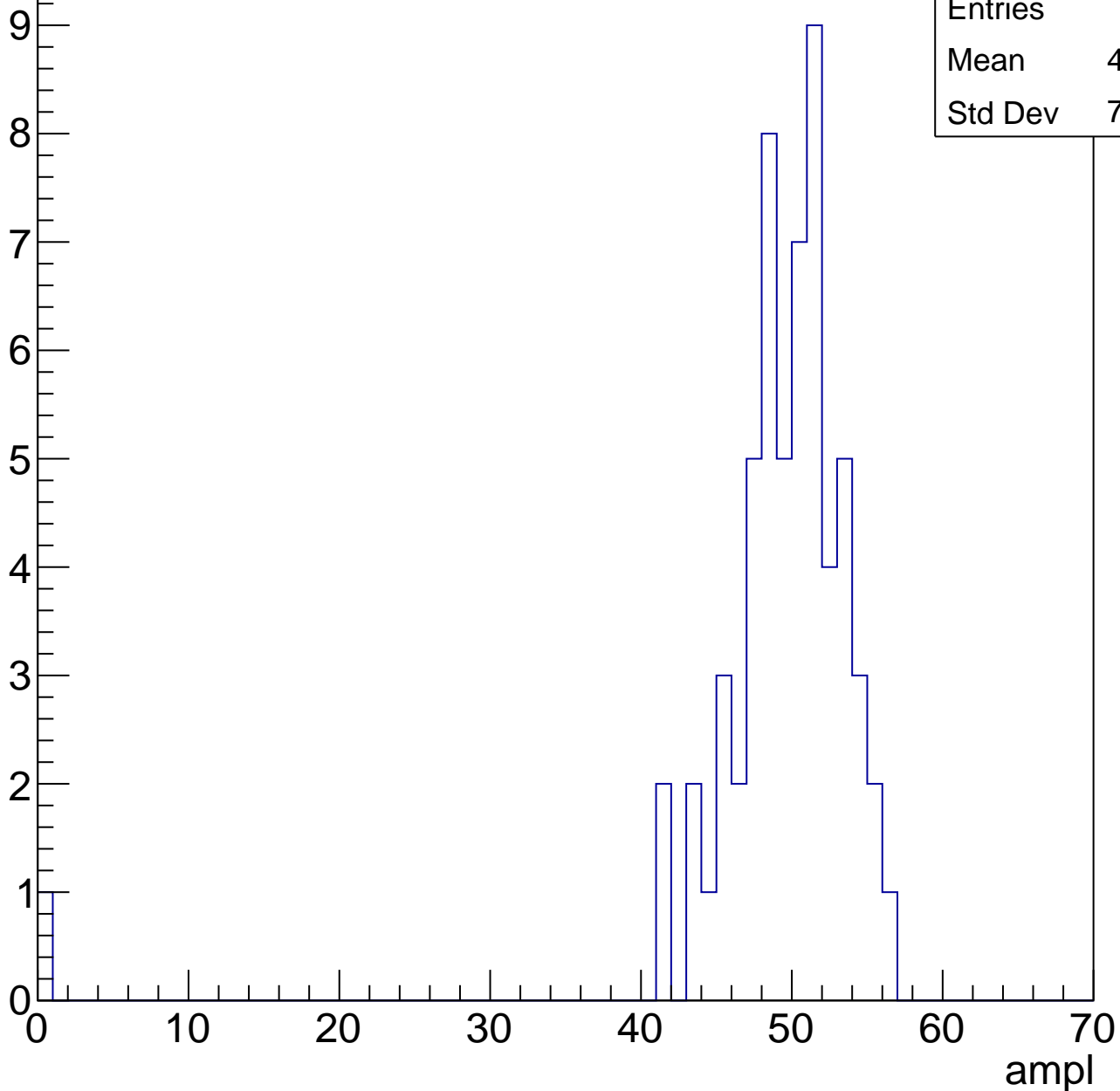


B1L103S, U8-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	48.55
Std Dev	7.145

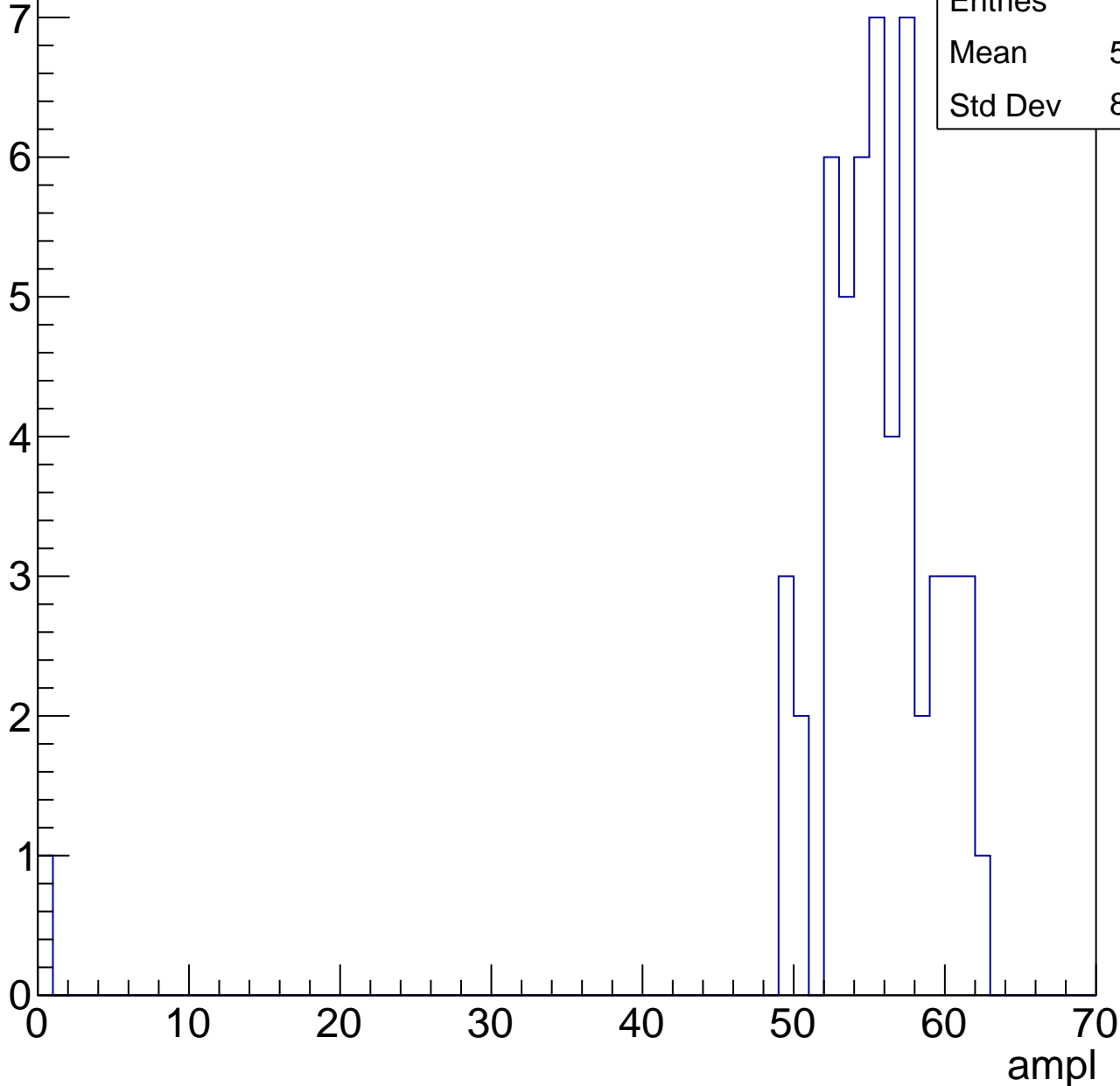


B1L103S, U8-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2

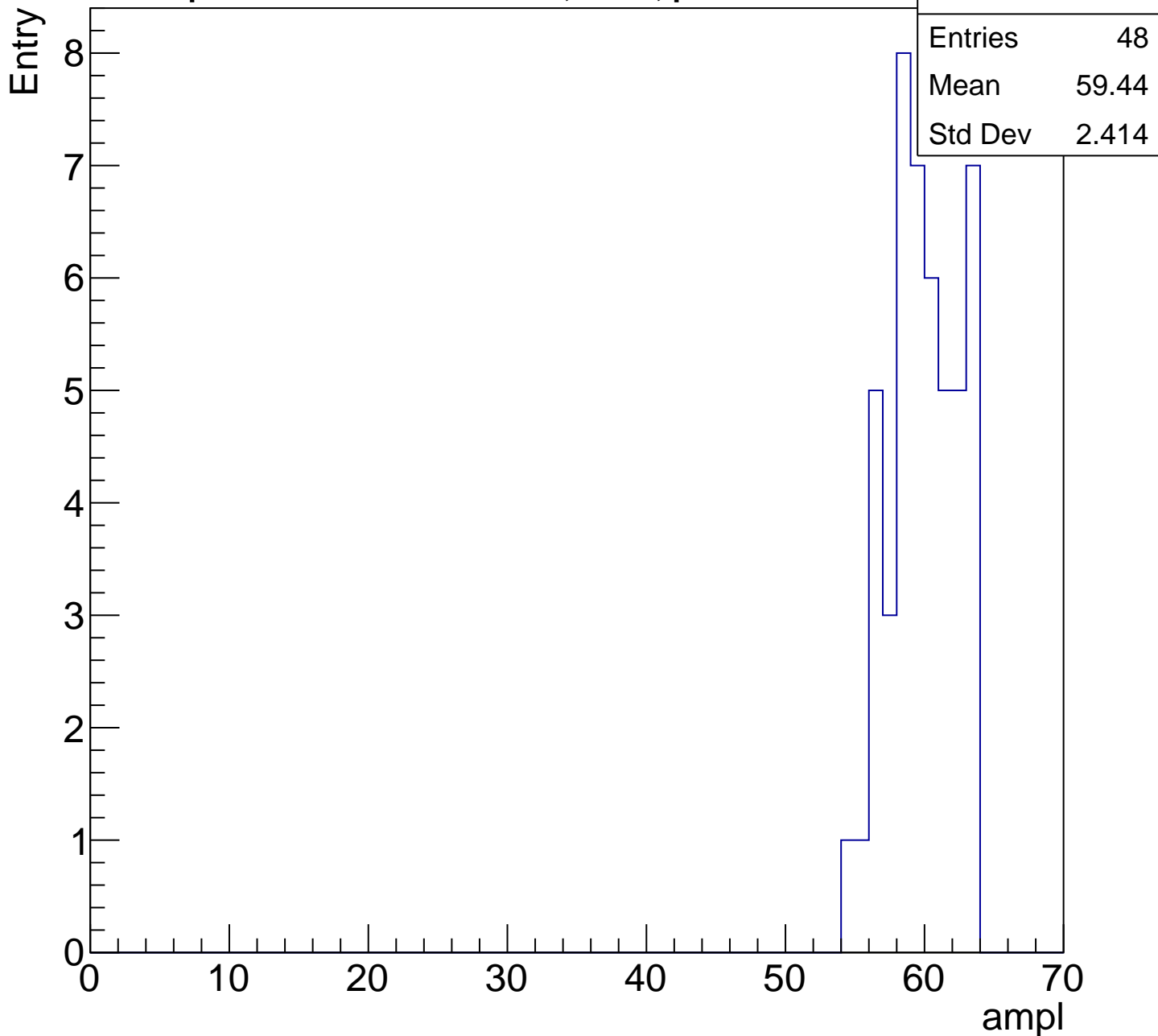
Entry

Entries	53
Mean	54.23
Std Dev	8.197



B1L103S, U8-ch91, adc5

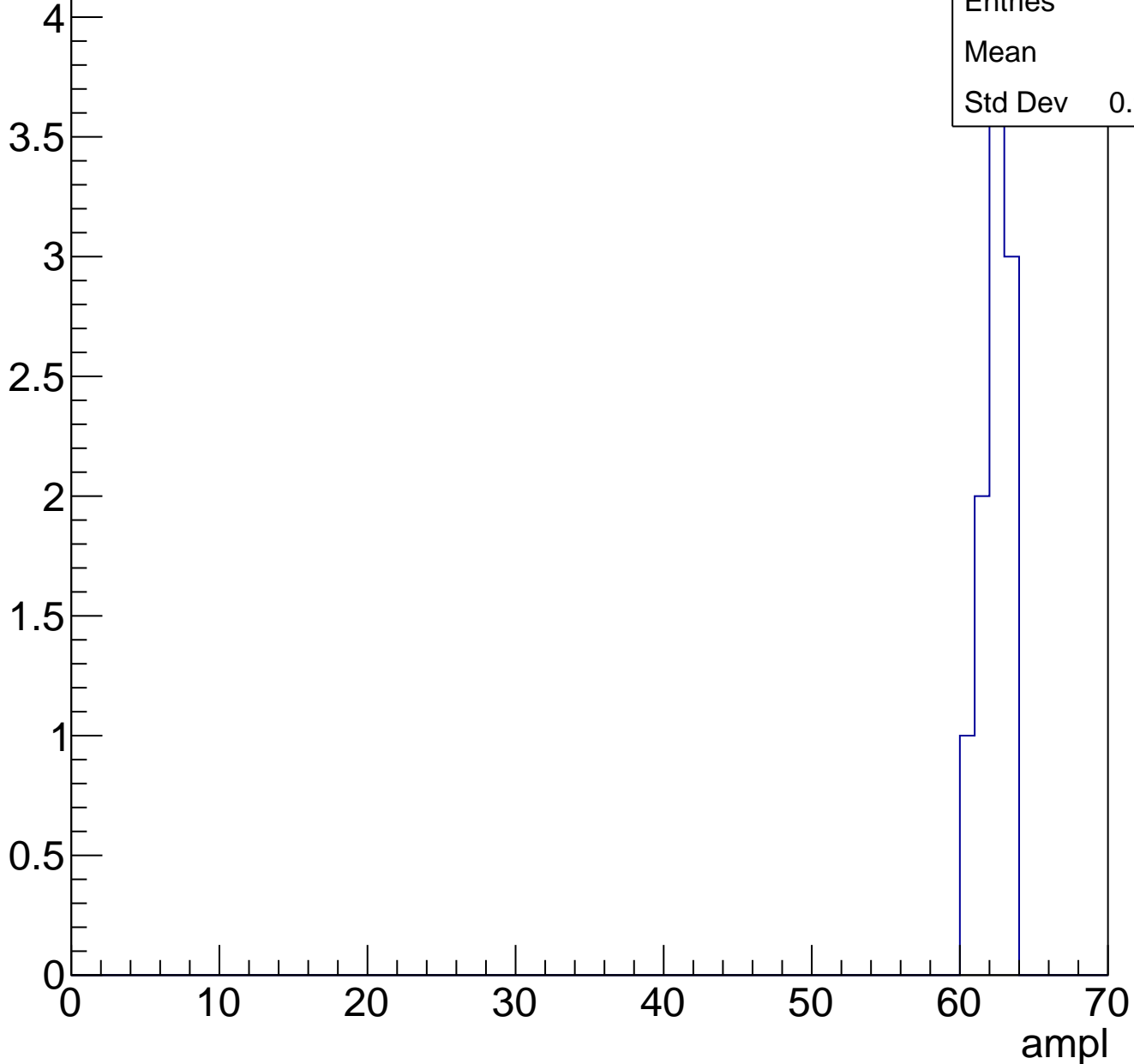
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U8-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

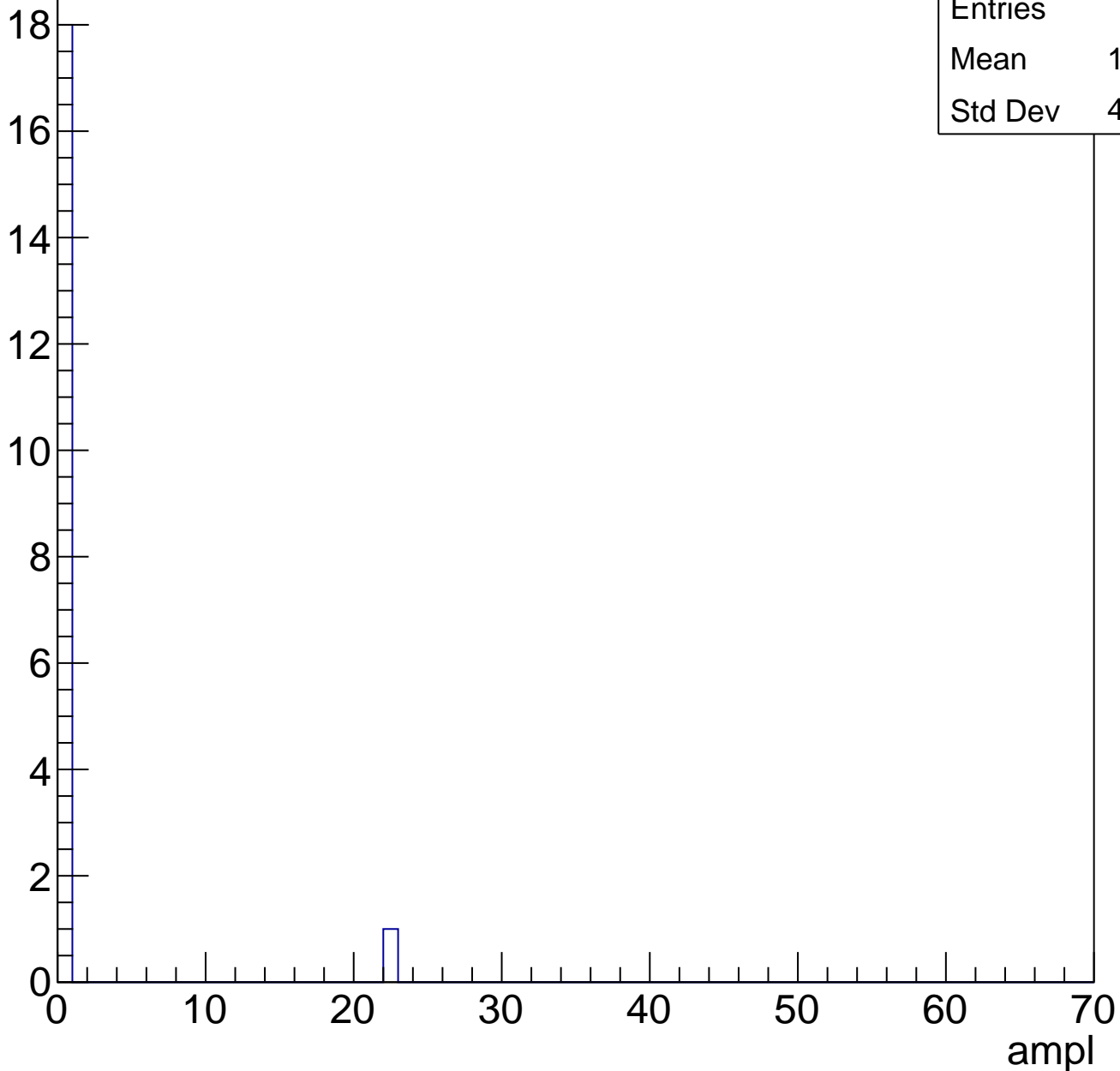


Entries	10
Mean	61.9
Std Dev	0.9434

B1L103S, U8-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1.158
Std Dev	4.913

B1L103S, U8-ch92, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	23.22
Std Dev	11.21

Gaus mean : 29.0978

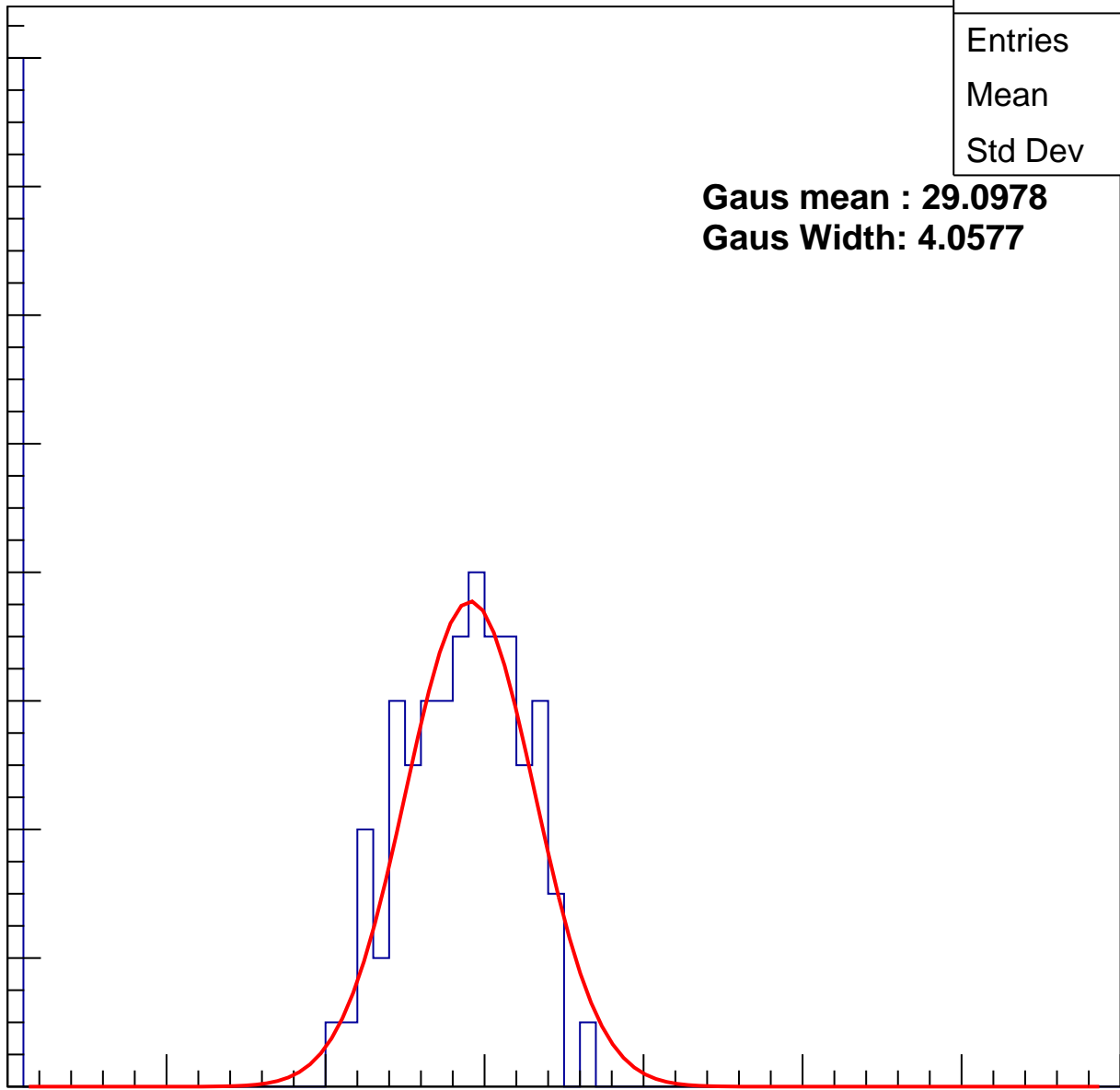
Gaus Width: 4.0577

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch92, adc1

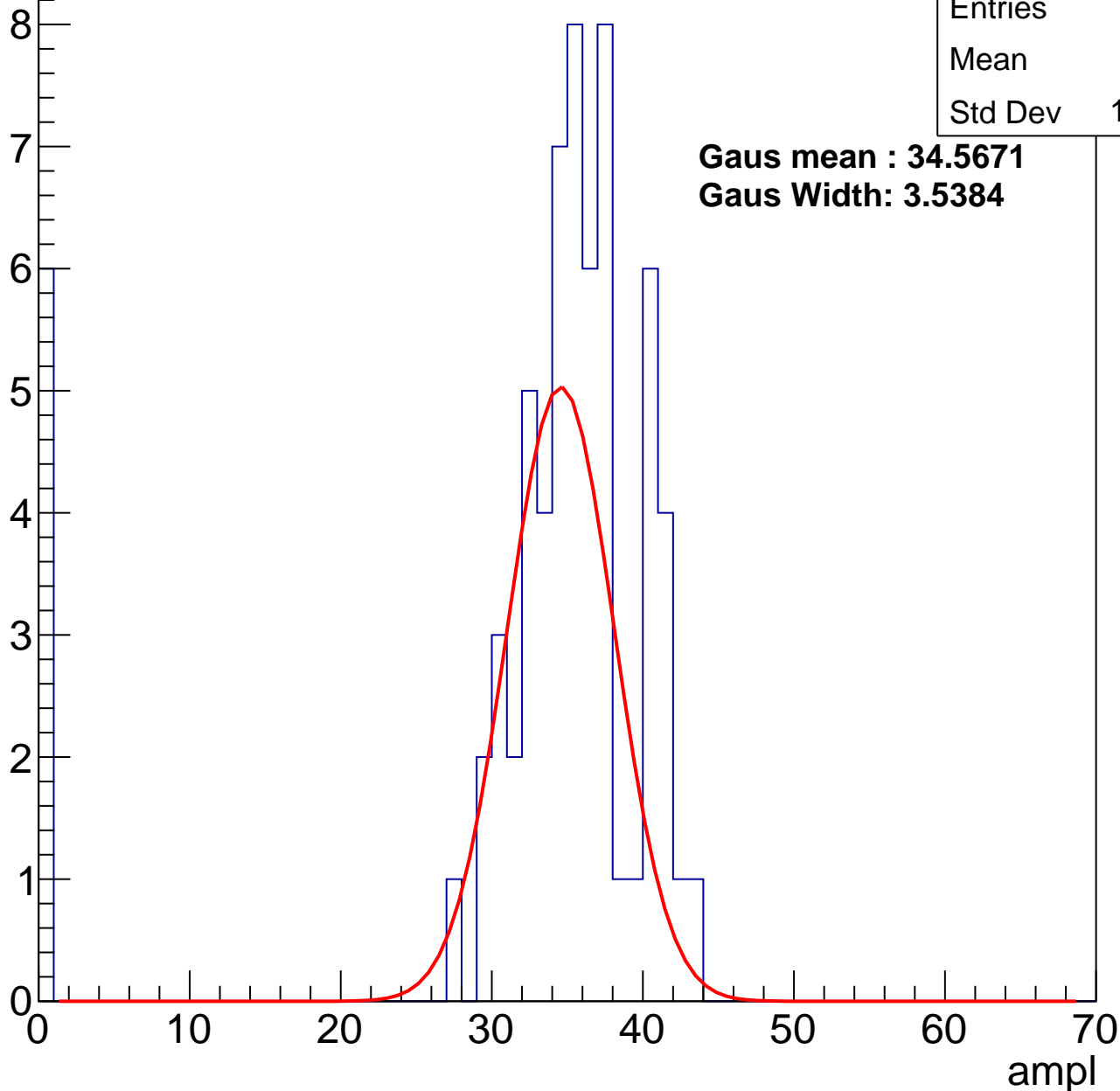
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	32.2
Std Dev	10.74

Gaus mean : 34.5671

Gaus Width: 3.5384



B1L103S, U8-ch92, adc2

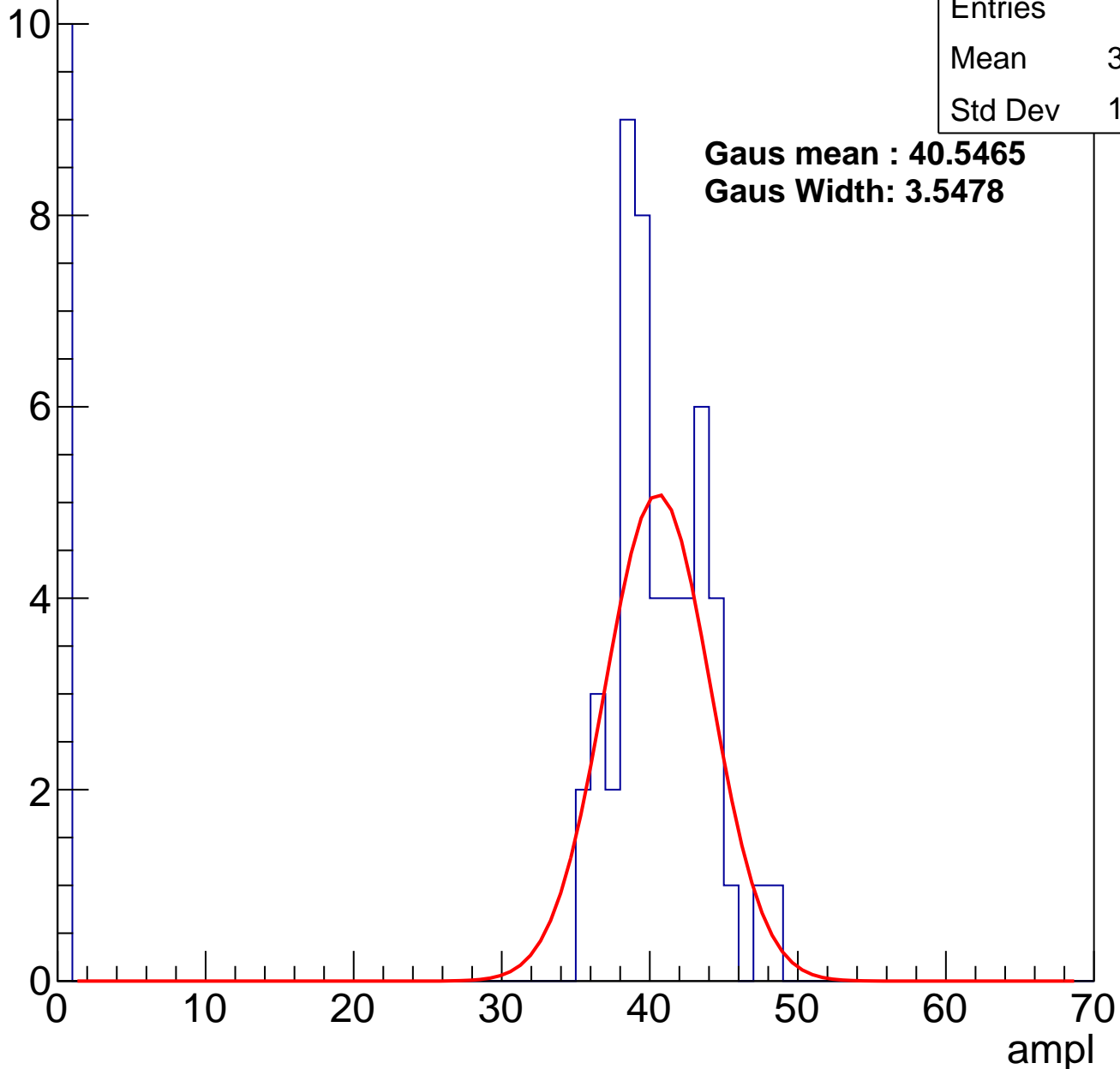
calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	33.42
Std Dev	15.34

Gaus mean : 40.5465

Gaus Width: 3.5478

Entry



B1L103S, U8-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

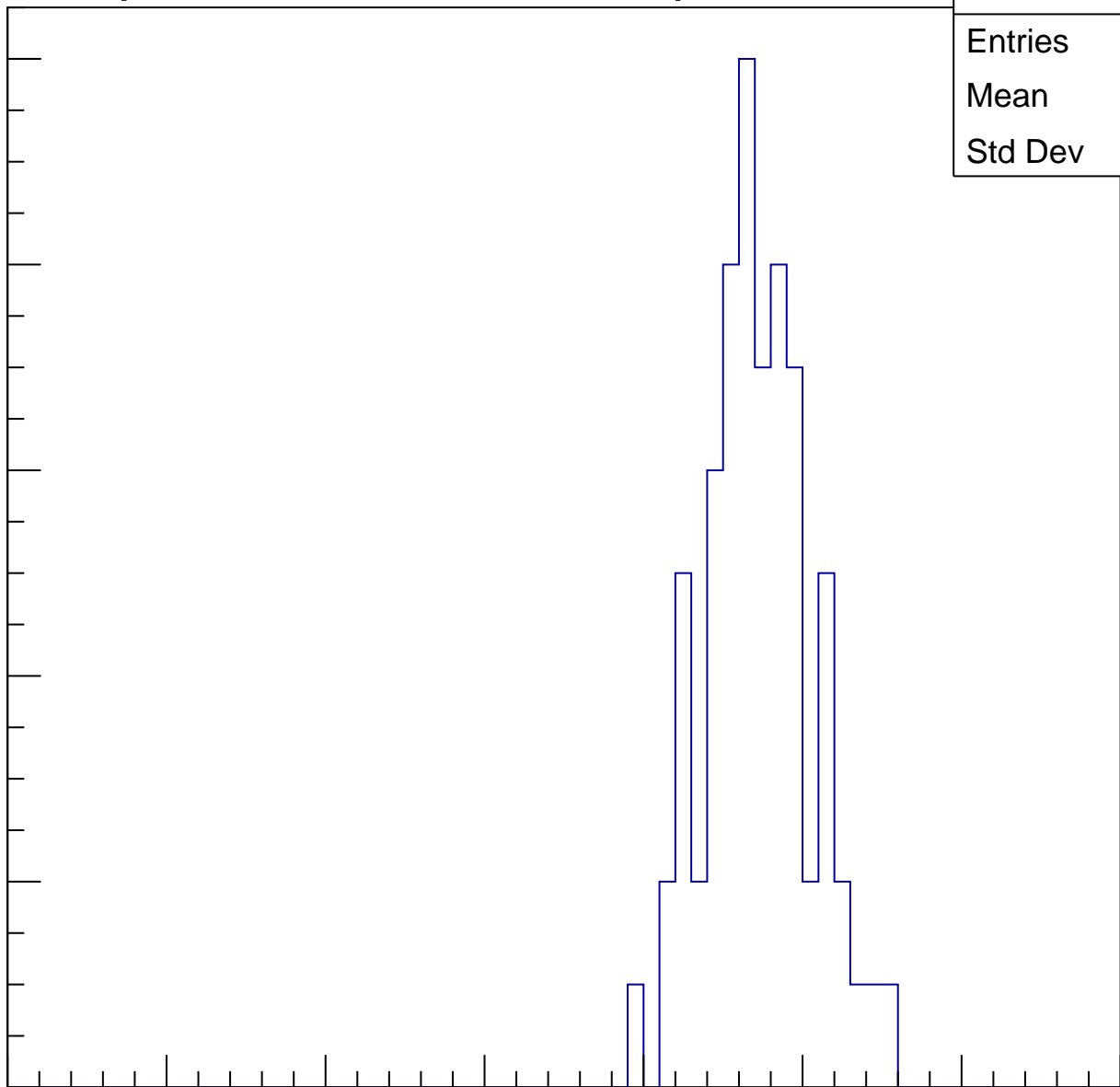
Entries	68
Mean	46.74
Std Dev	3.248

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

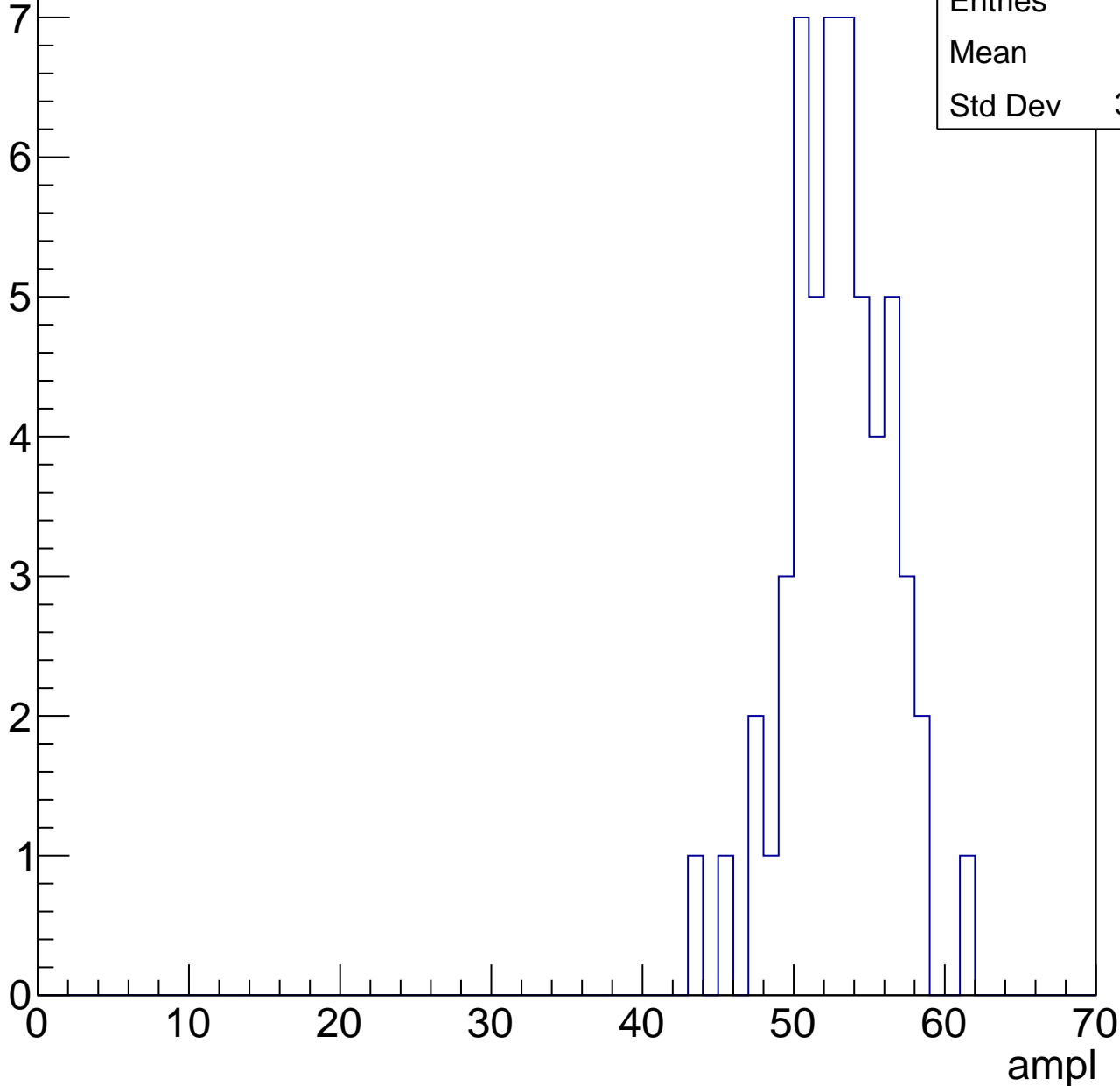


B1L103S, U8-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	52.5
Std Dev	3.371

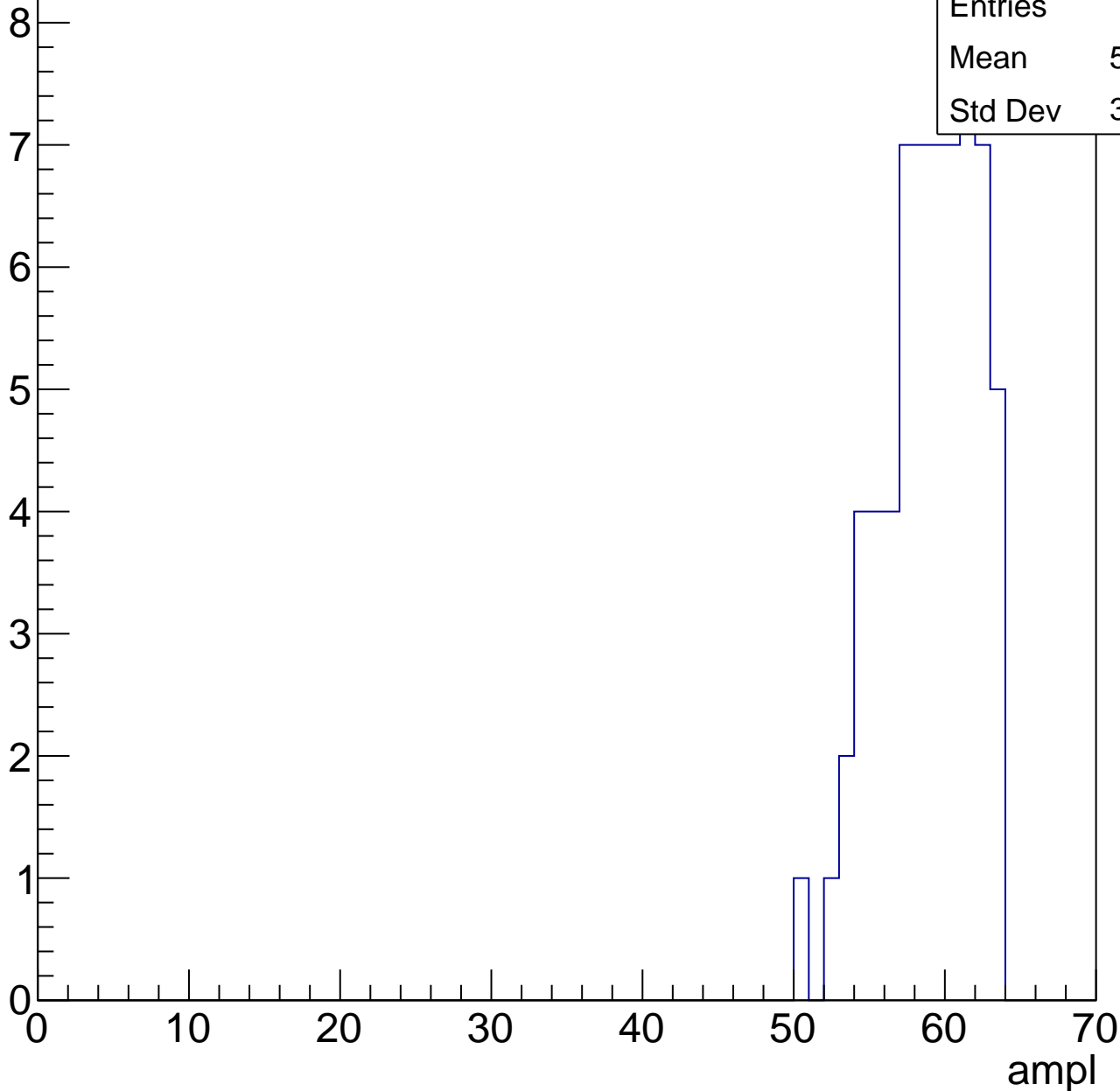


B1L103S, U8-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

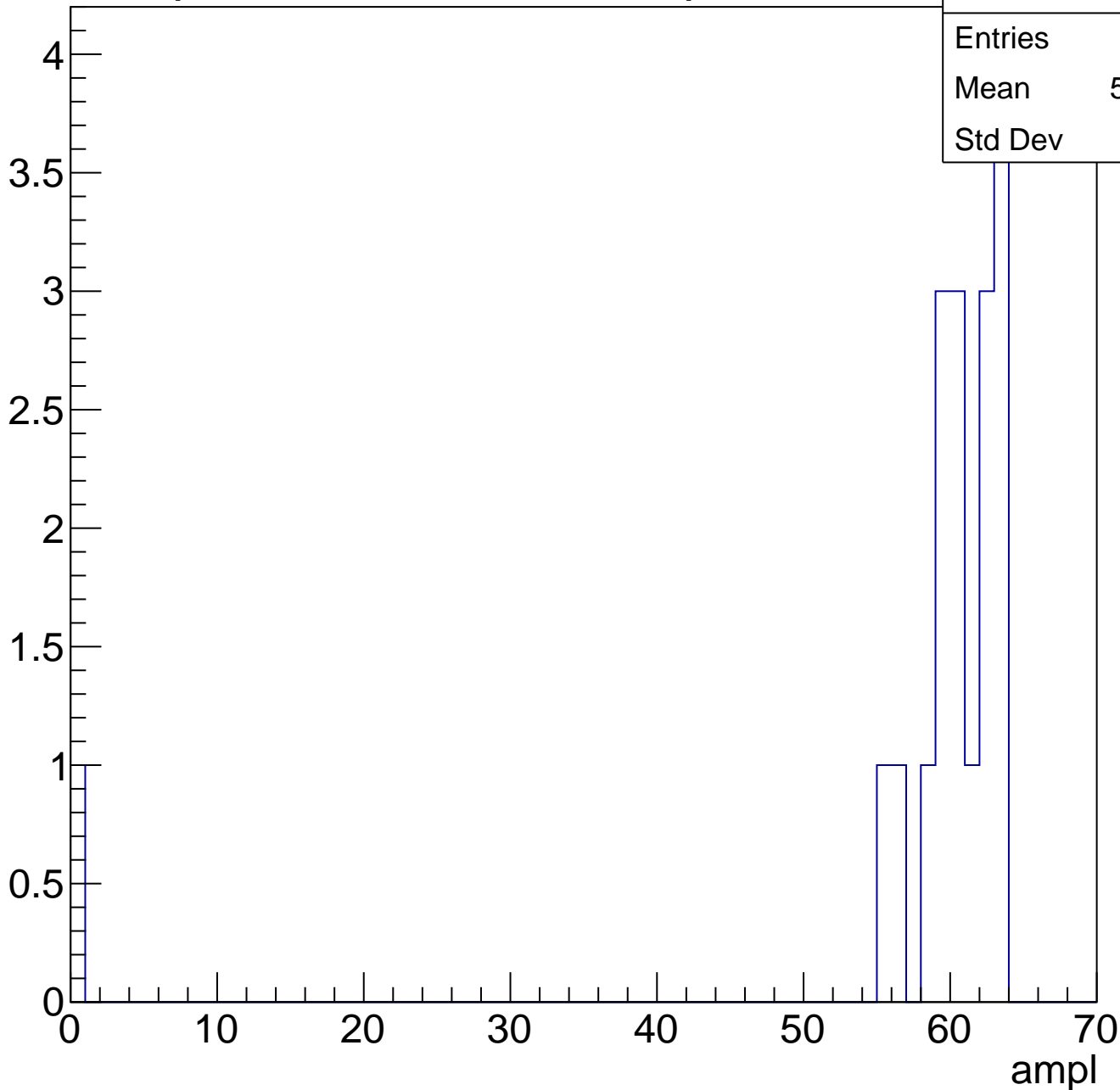
Entries	64
Mean	58.48
Std Dev	3.067



B1L103S, U8-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

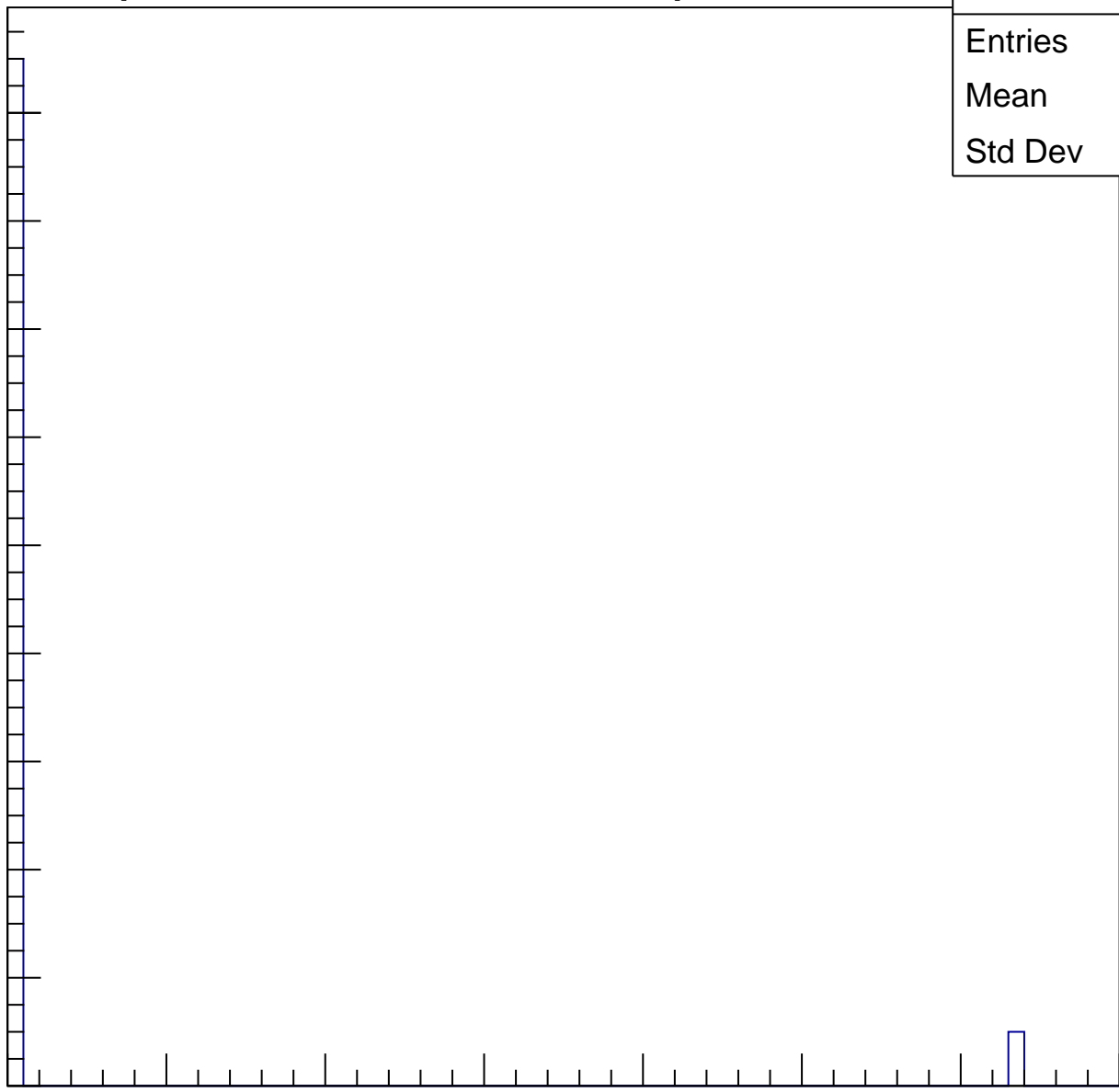
Entry

18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	3.15
Std Dev	13.73

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch93, adc0

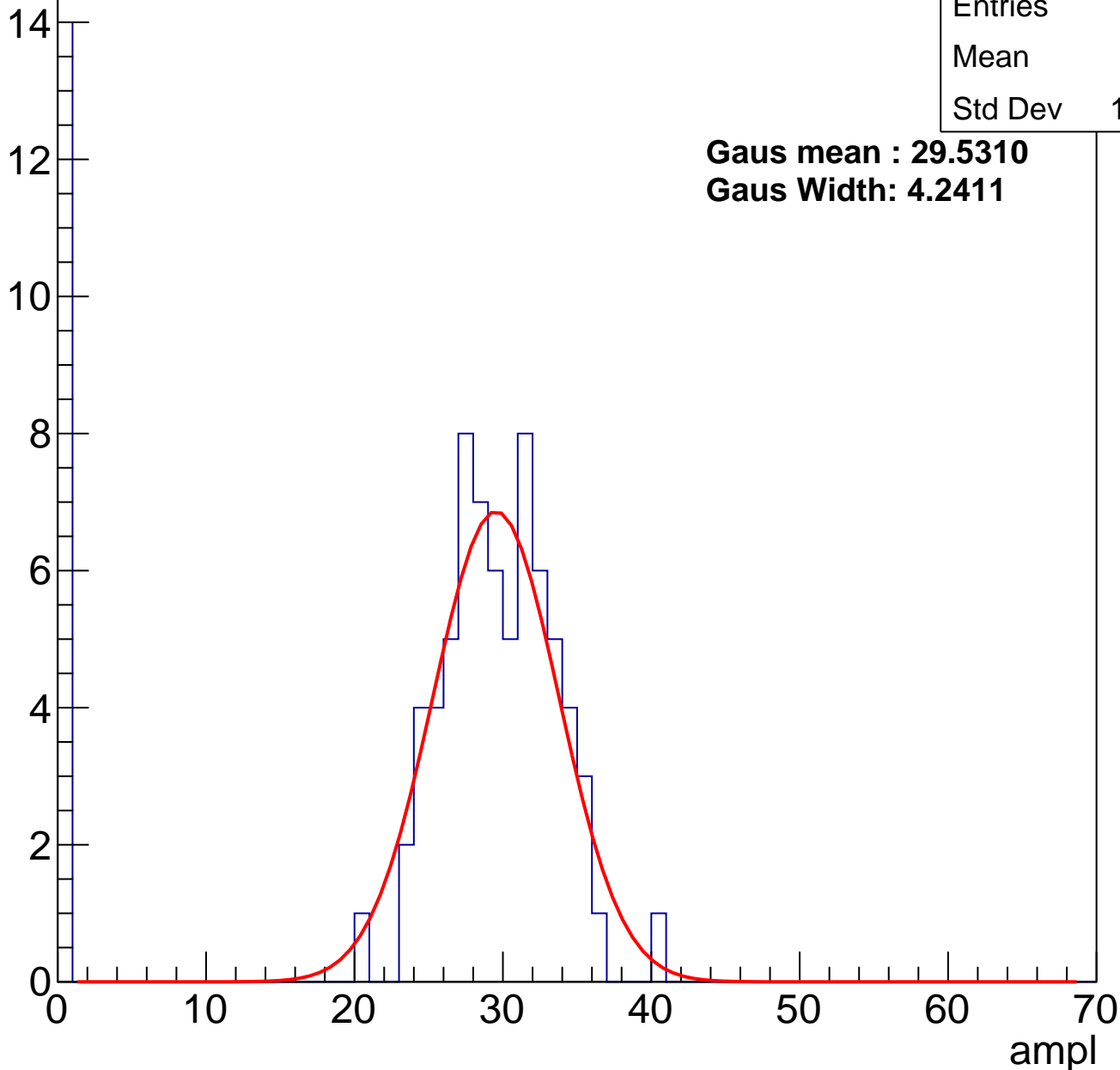
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	24.4
Std Dev	11.42

Gaus mean : 29.5310

Gaus Width: 4.2411

Entry



B1L103S, U8-ch93, adc1

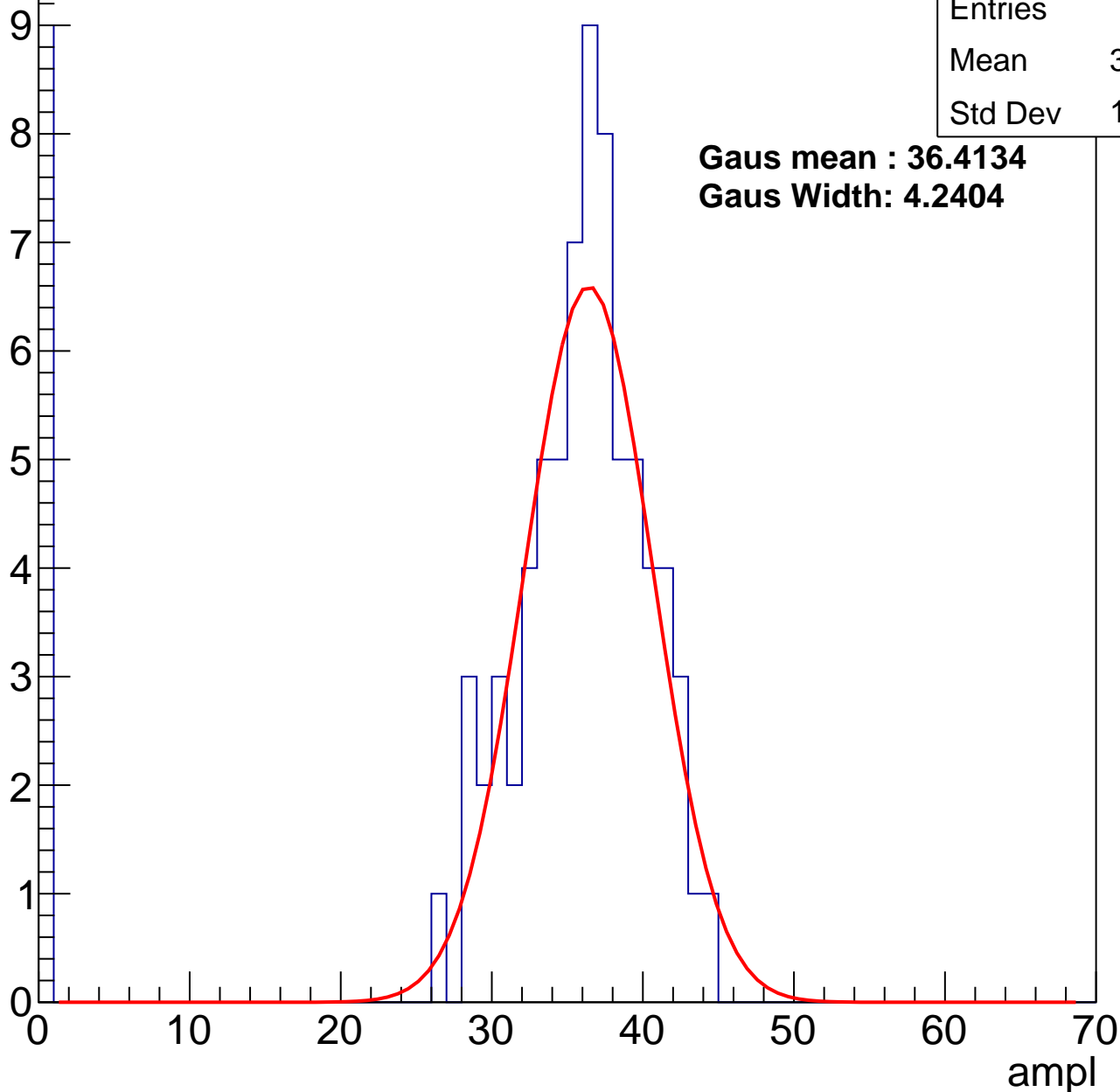
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	31.73
Std Dev	11.83

Gaus mean : 36.4134

Gaus Width: 4.2404



B1L103S, U8-ch93, adc2

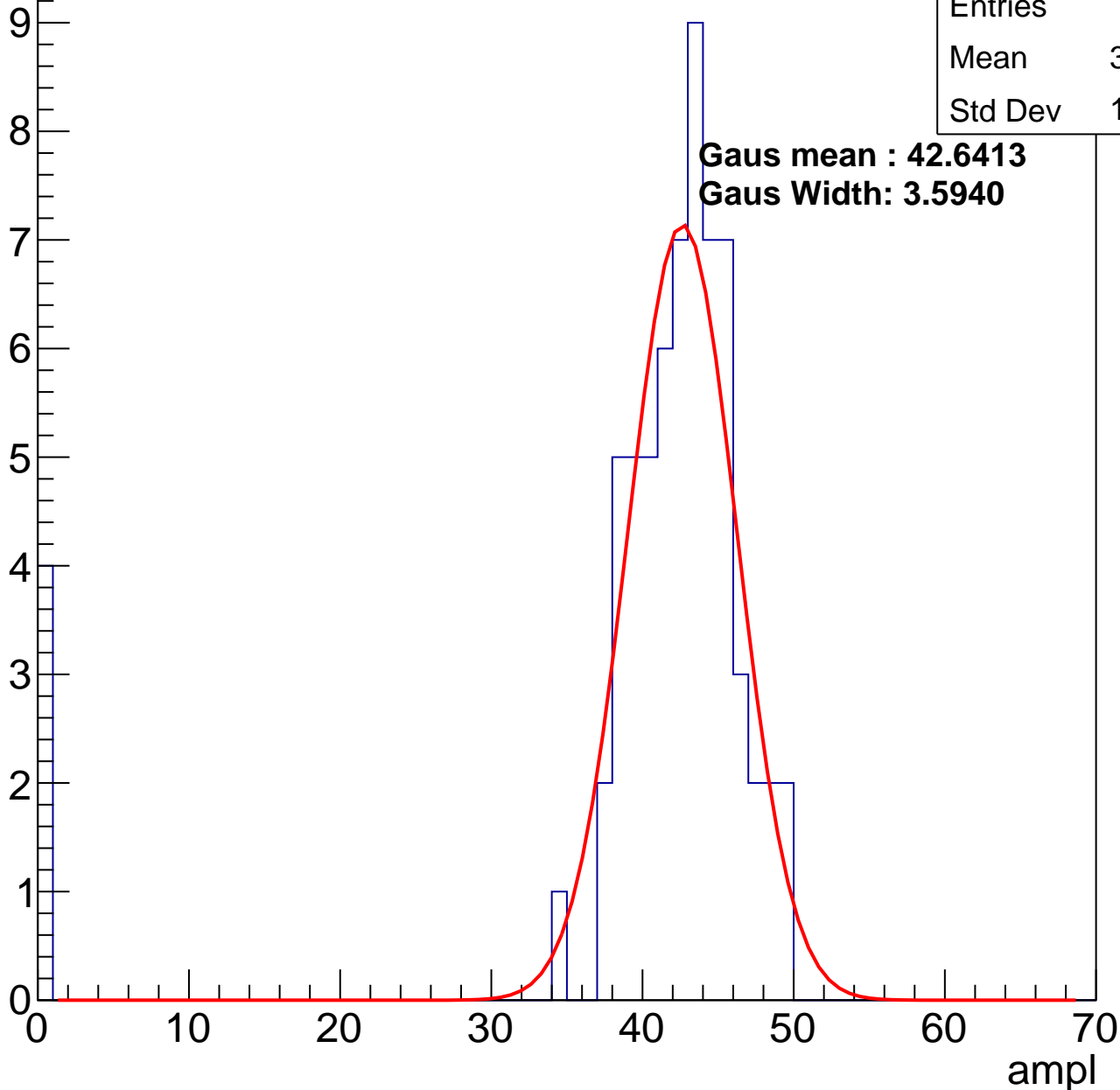
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	39.84
Std Dev	10.49

Gaus mean : 42.6413

Gaus Width: 3.5940



B1L103S, U8-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	49.67
Std Dev	6.483

Entry

10

8

6

4

2

0

0

10

20

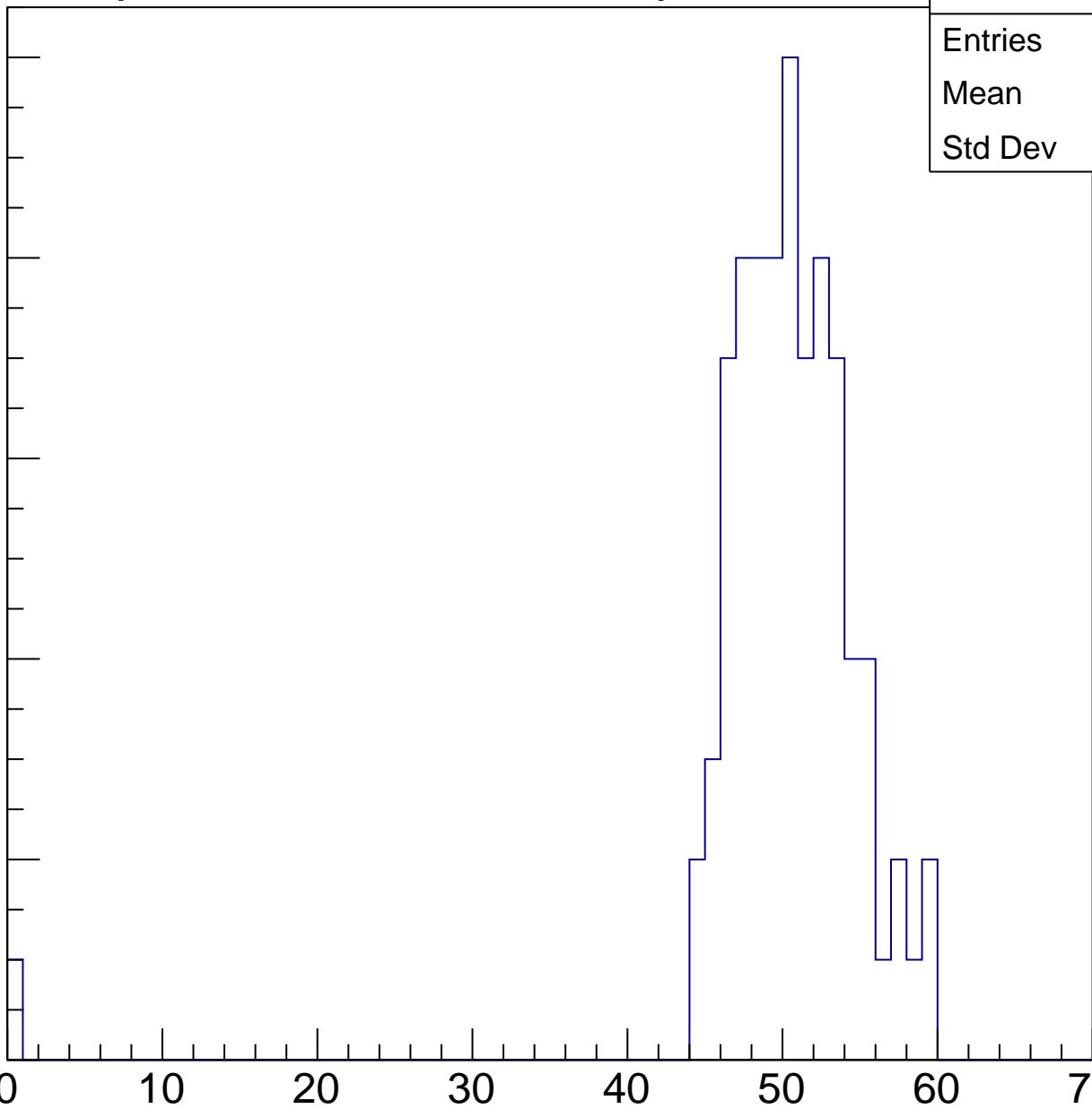
30

40

50

60

ampl

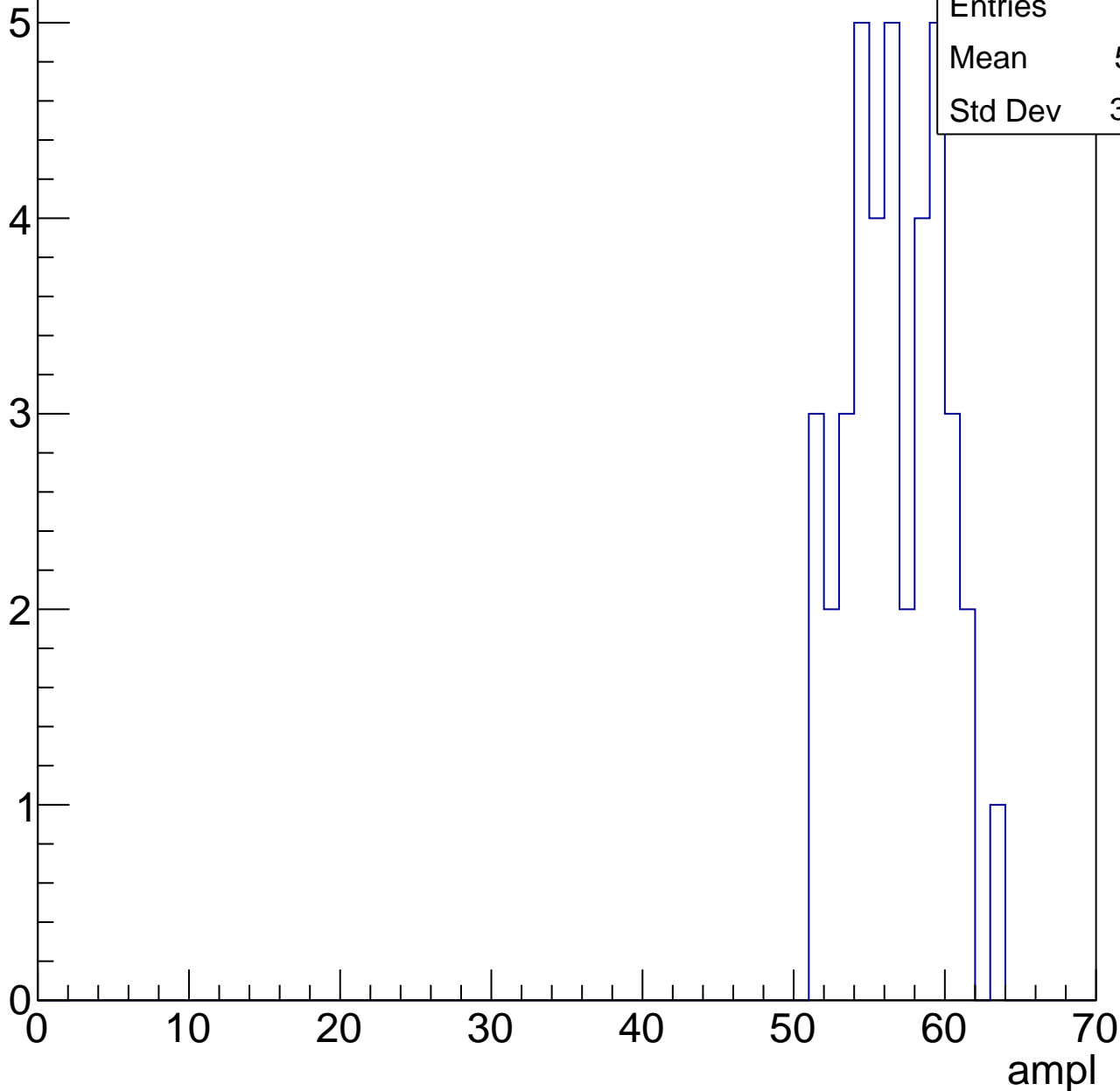


B1L103S, U8-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	56.21
Std Dev	3.065

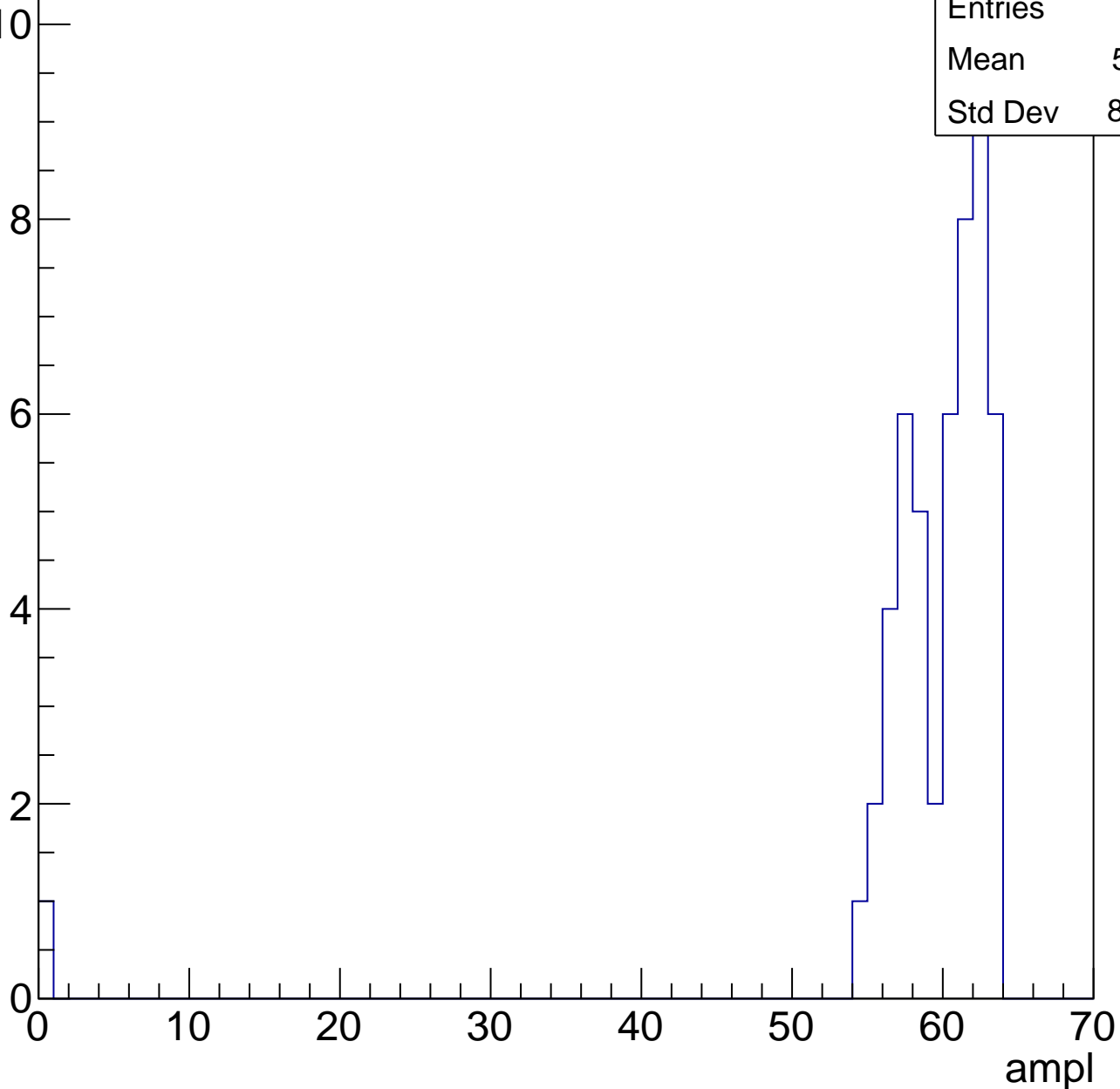


B1L103S, U8-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.51
Std Dev	8.649



B1L103S, U8-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

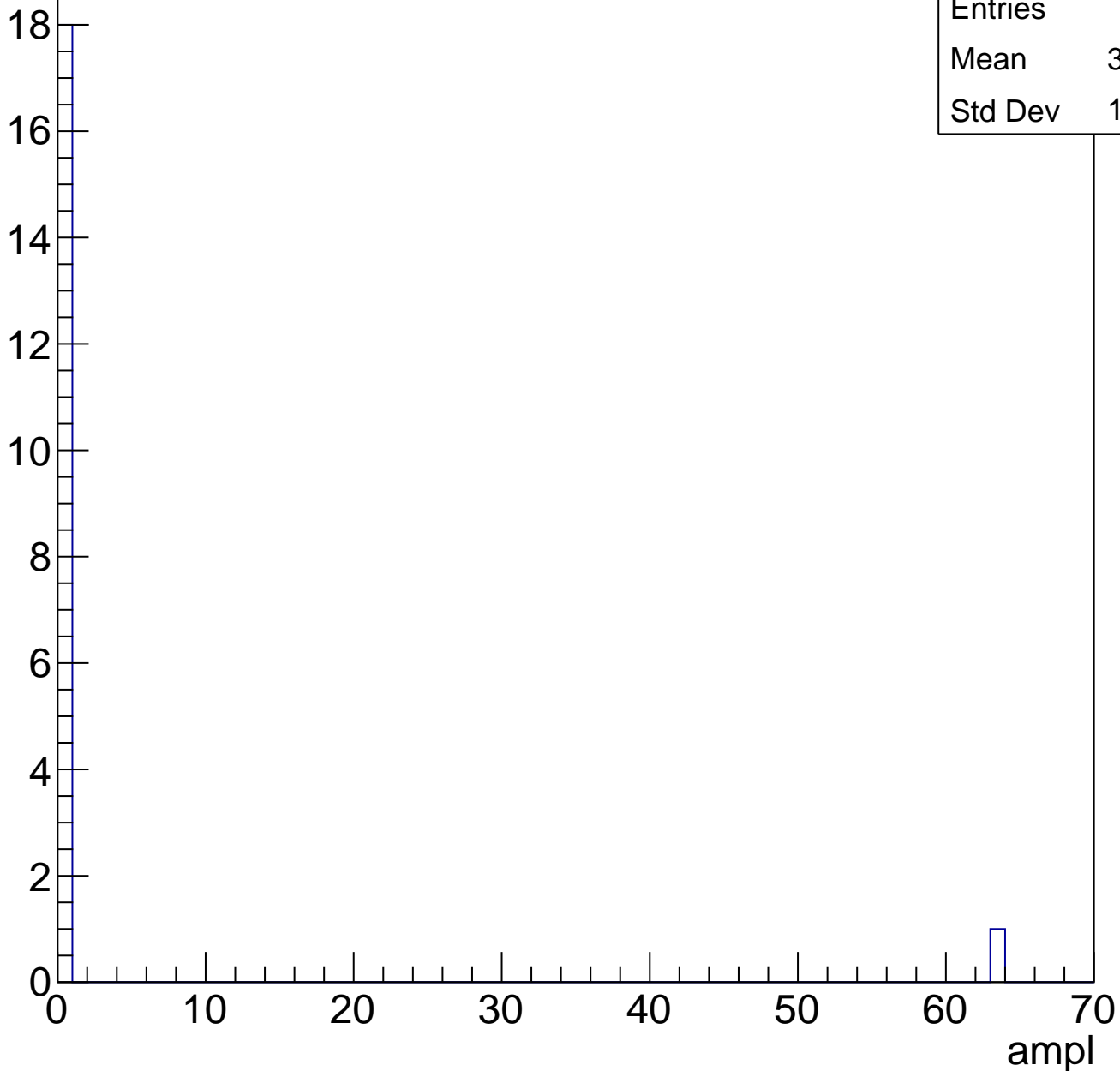
Entry



B1L103S, U8-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch94, adc0

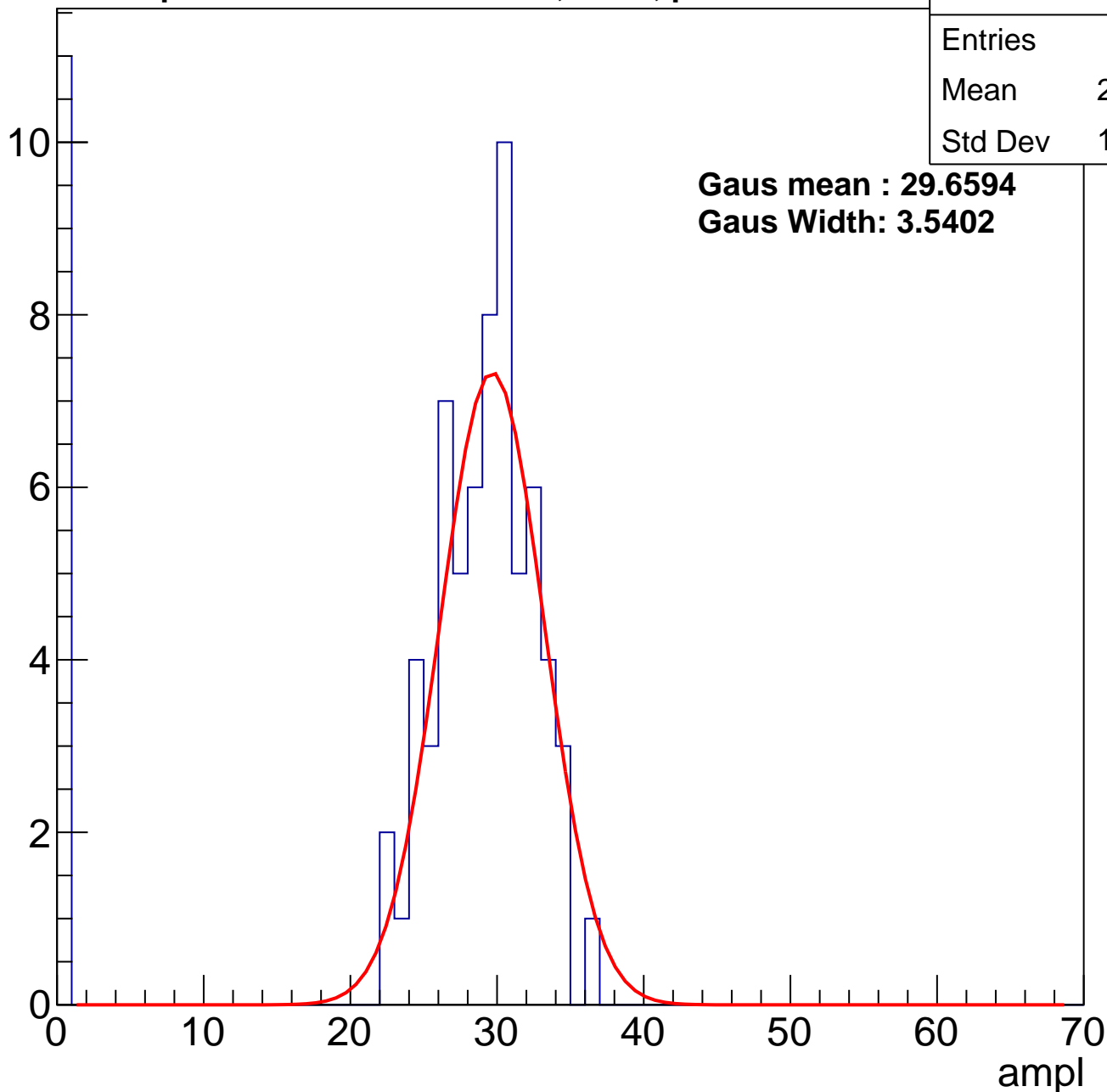
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	24.63
Std Dev	10.54

Gaus mean : 29.6594

Gaus Width: 3.5402

Entry



B1L103S, U8-ch94, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	31.56
Std Dev	10.98

Gaus mean : 35.3103

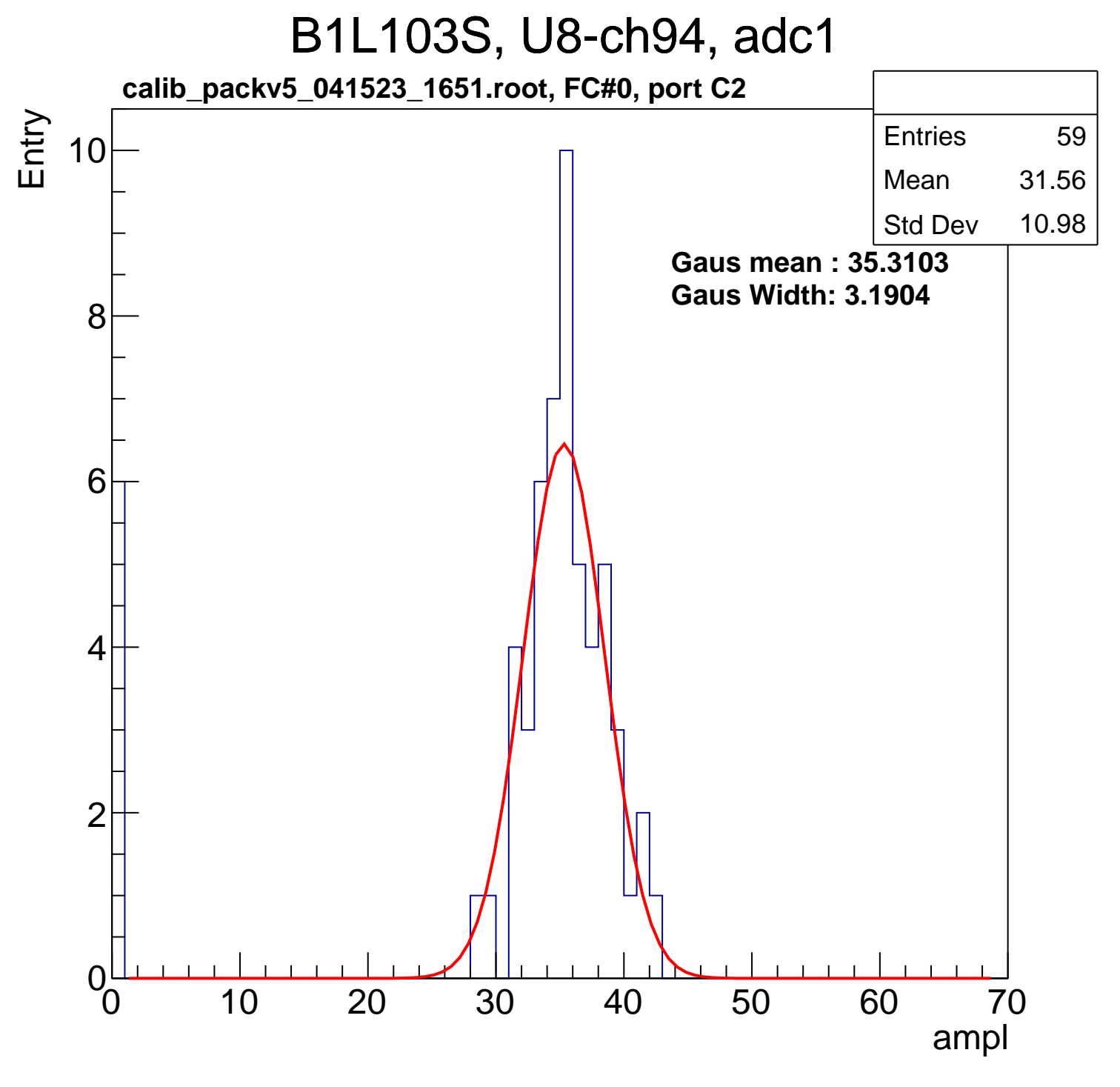
Gaus Width: 3.1904

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch94, adc2

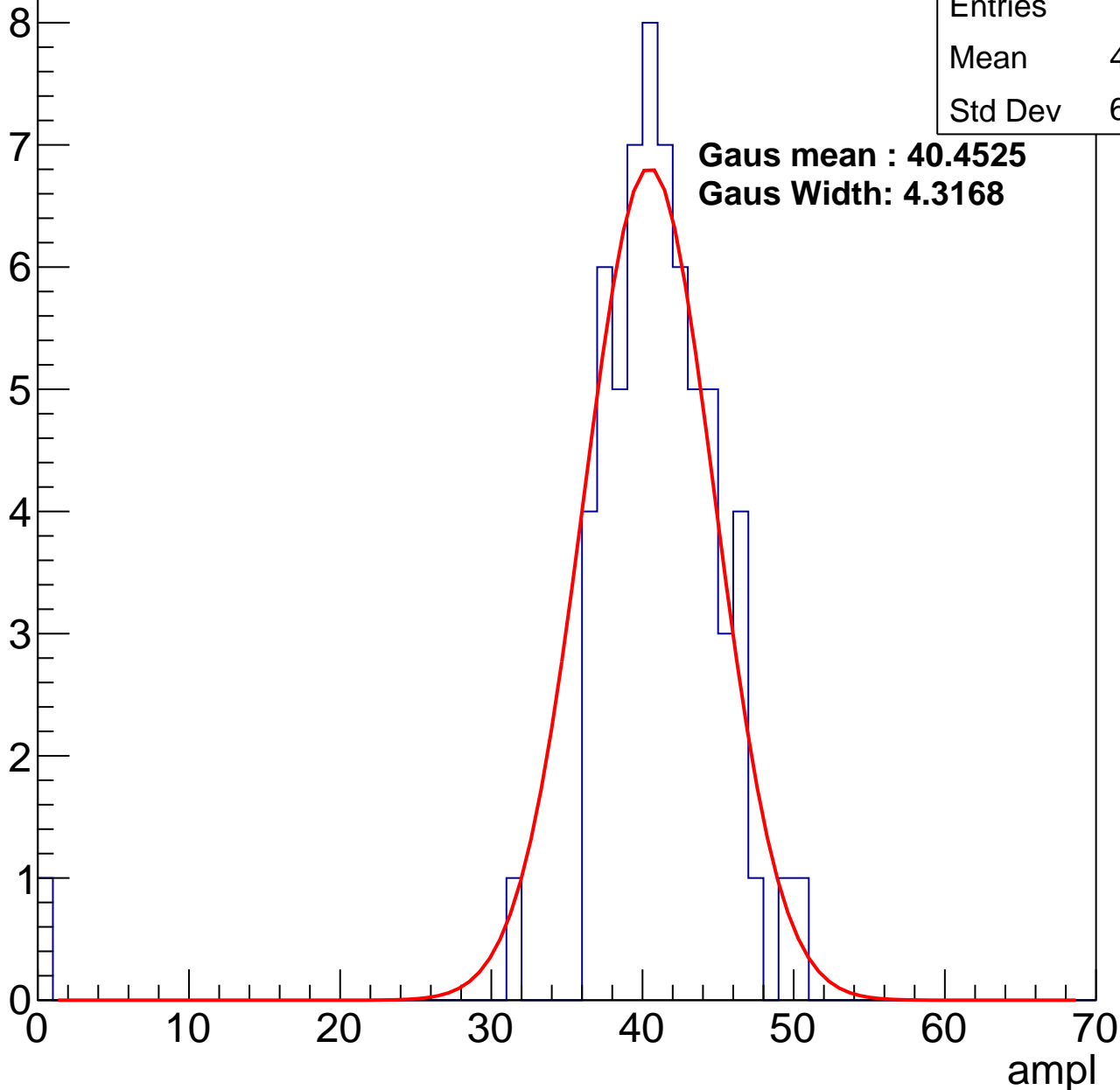
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	40.29
Std Dev	6.109

Gaus mean : 40.4525

Gaus Width: 4.3168

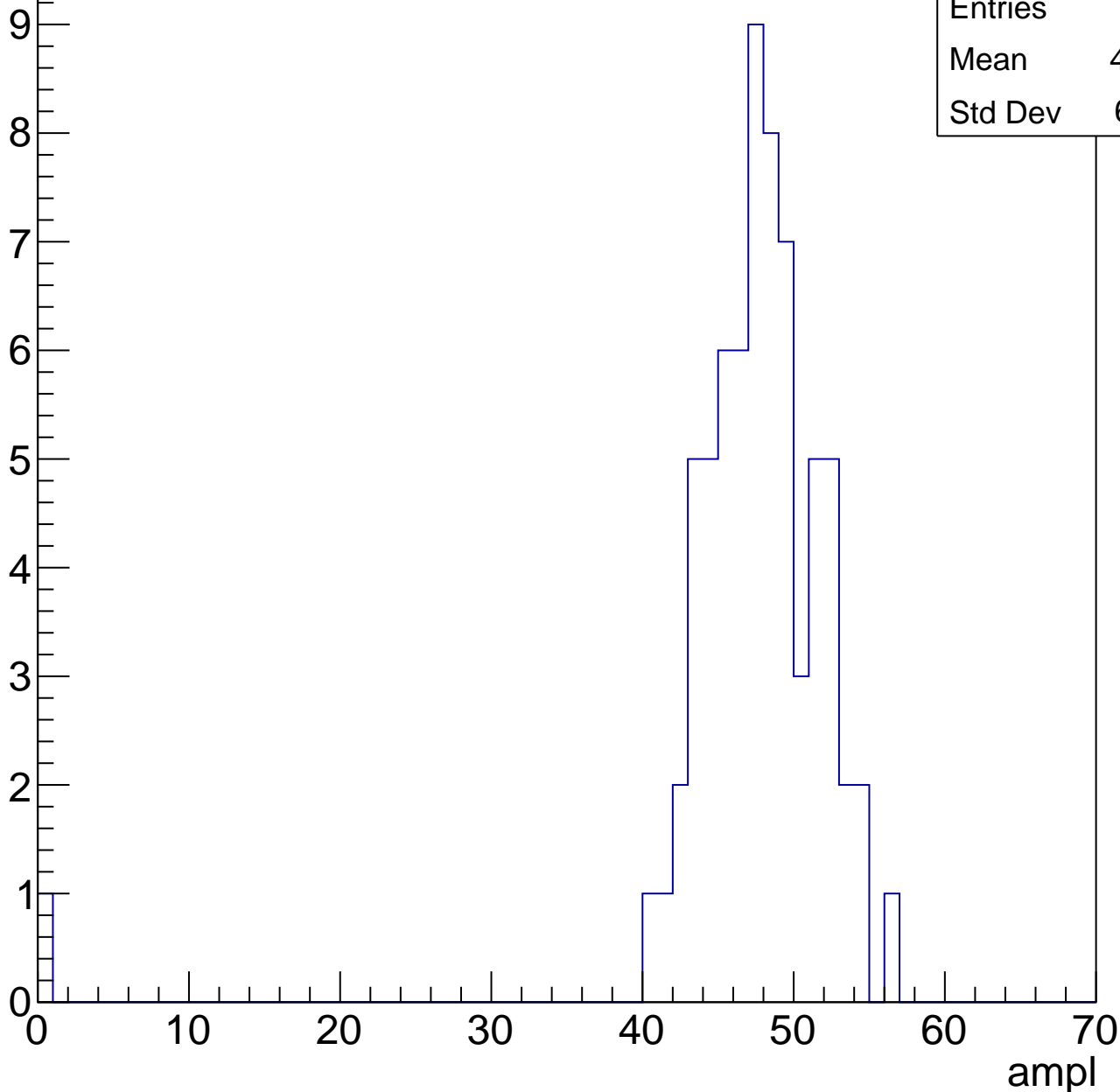


B1L103S, U8-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	46.83
Std Dev	6.611

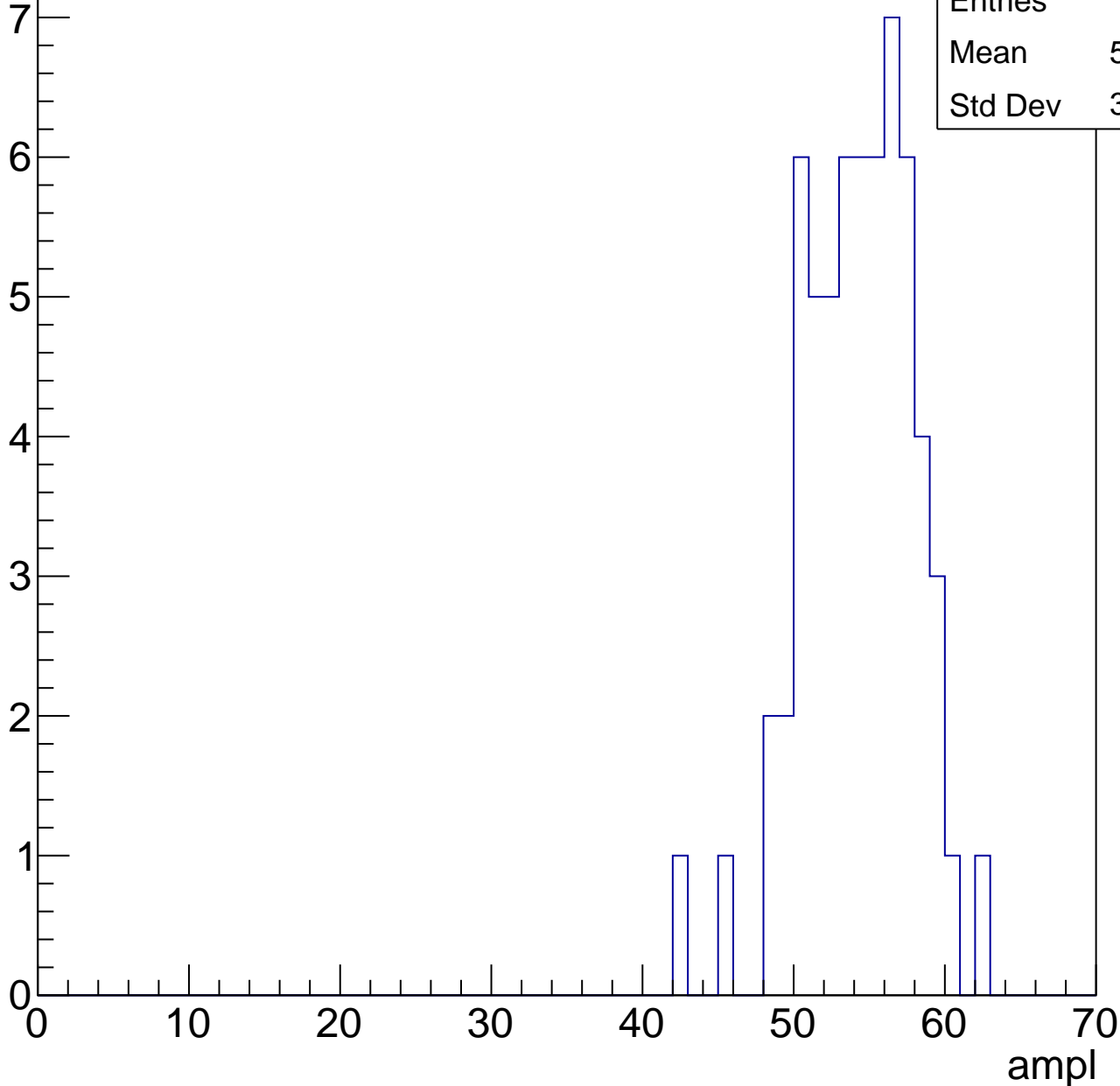


B1L103S, U8-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	53.76
Std Dev	3.675

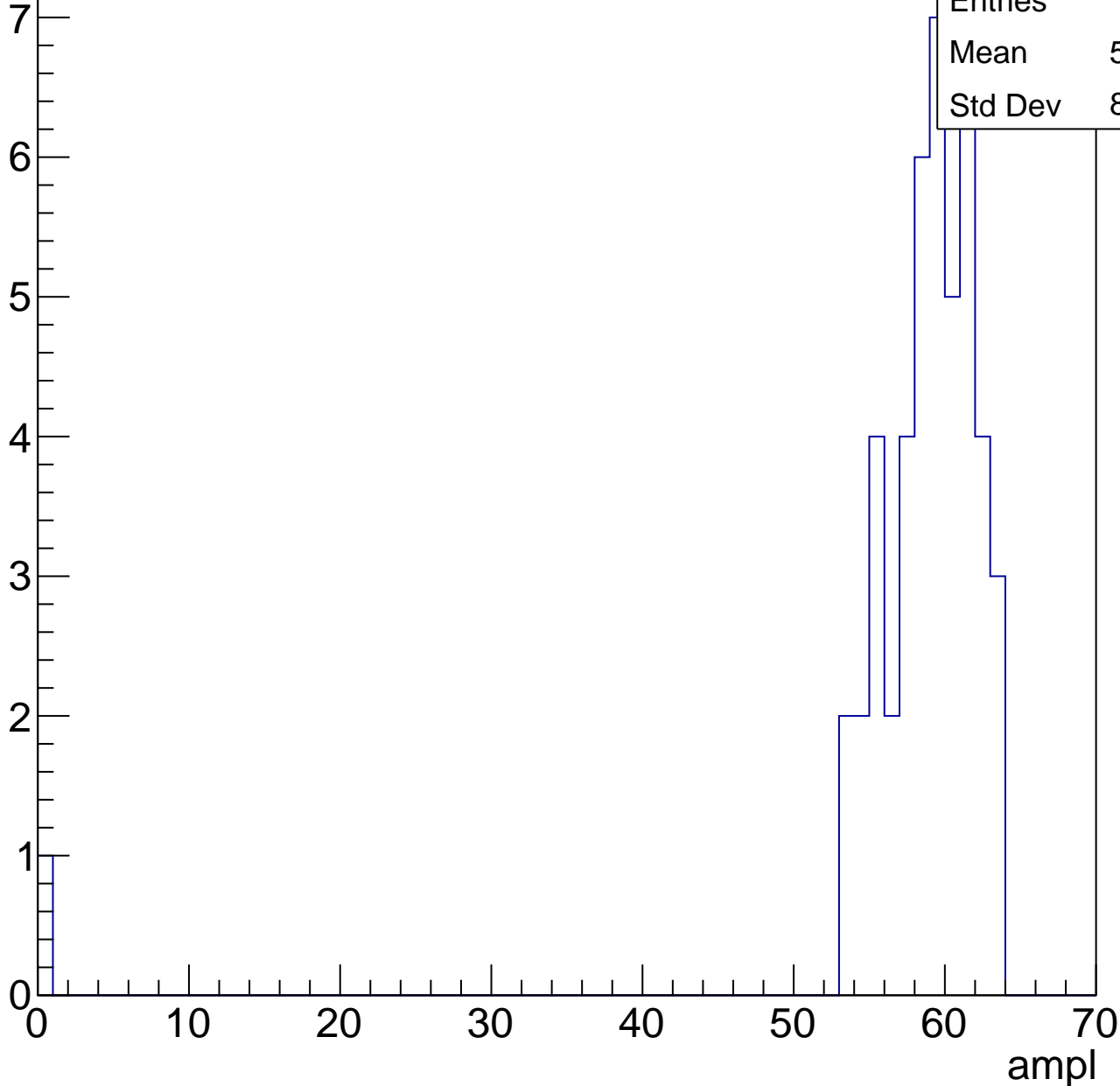


B1L103S, U8-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	57.43
Std Dev	8.882

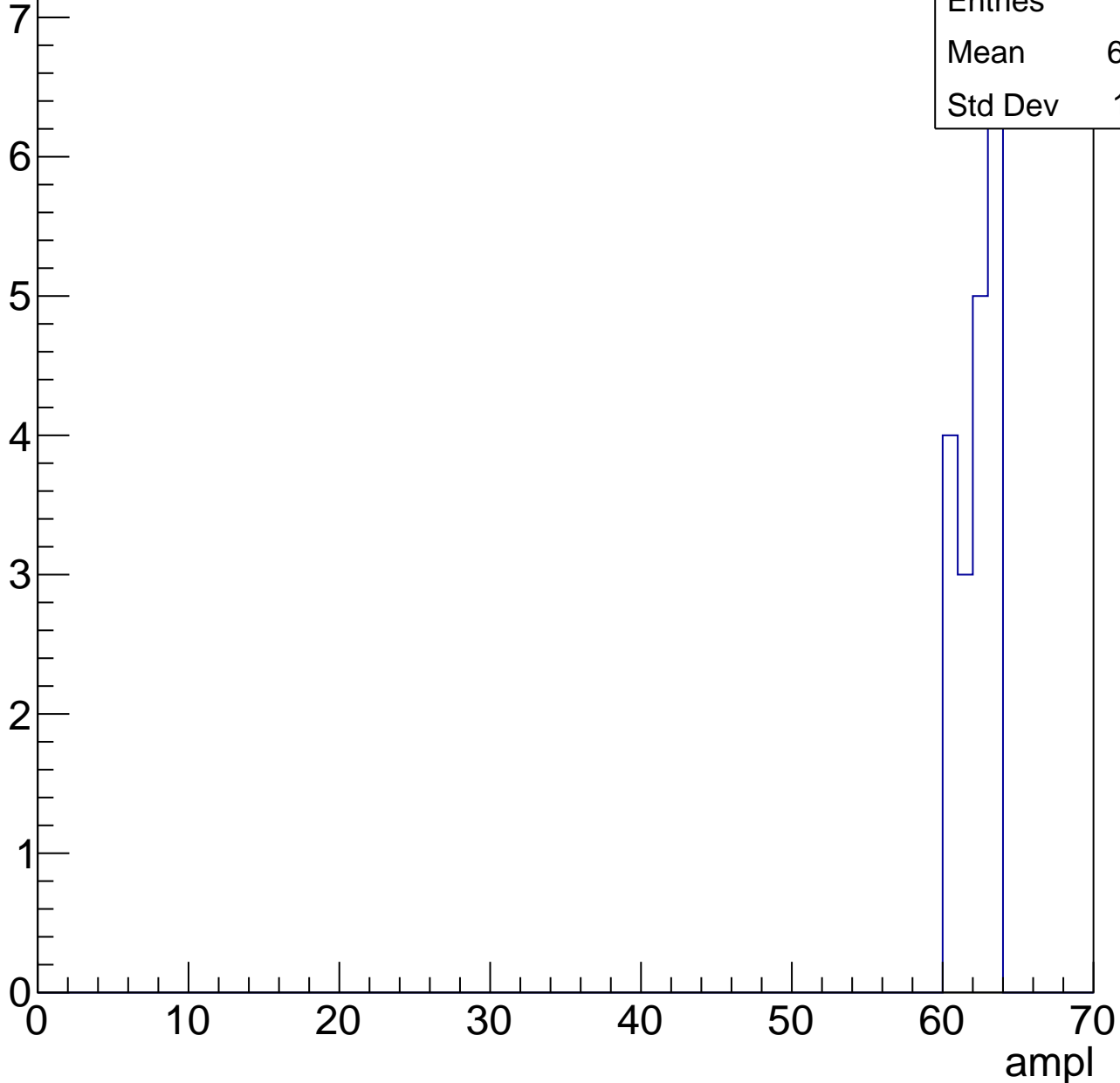


B1L103S, U8-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61.79
Std Dev	1.151



B1L103S, U8-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch95, adc0

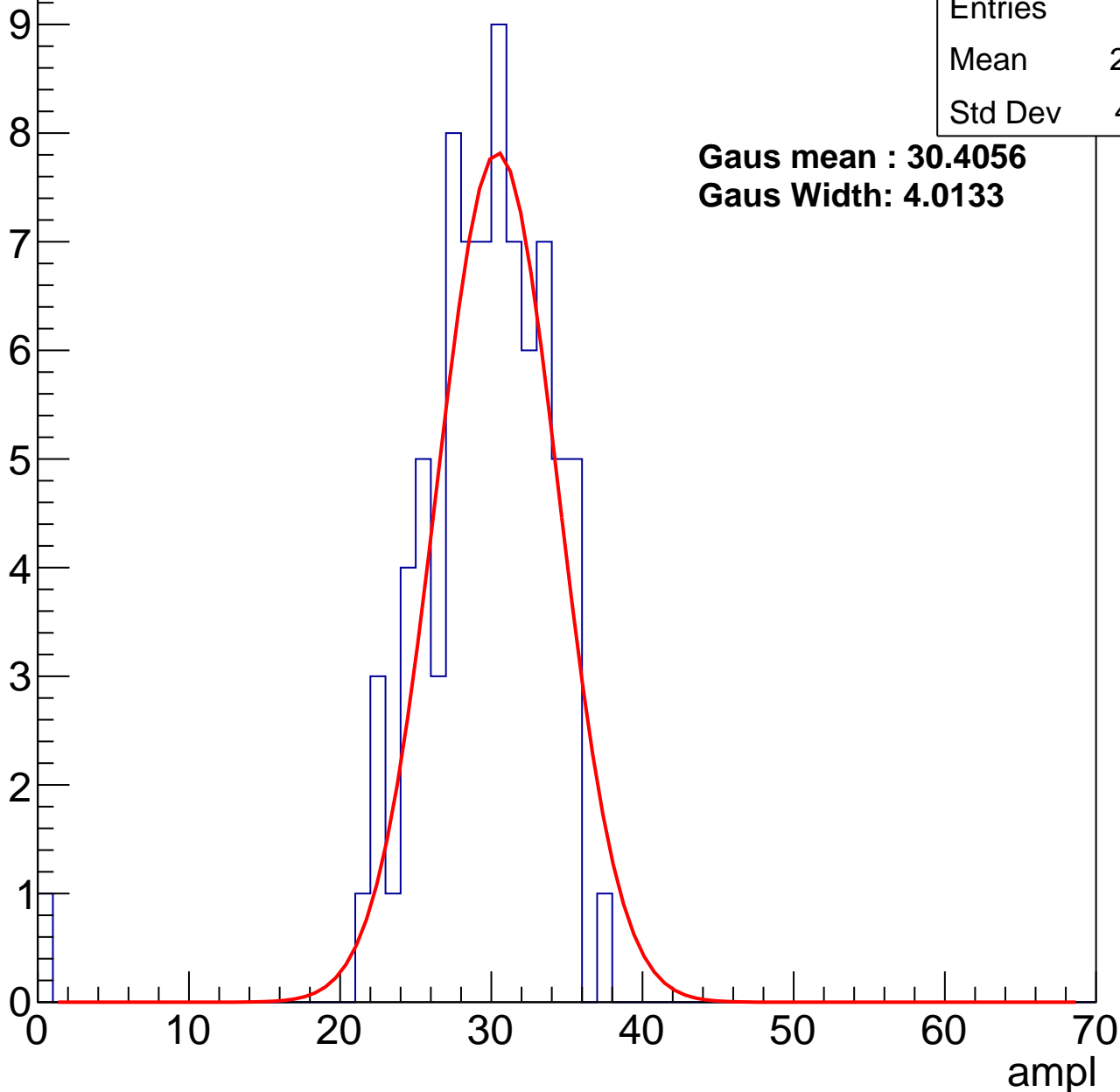
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	28.95
Std Dev	4.881

Gaus mean : 30.4056

Gaus Width: 4.0133



B1L103S, U8-ch95, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	33.47
Std Dev	11.69

Gaus mean : 37.7922

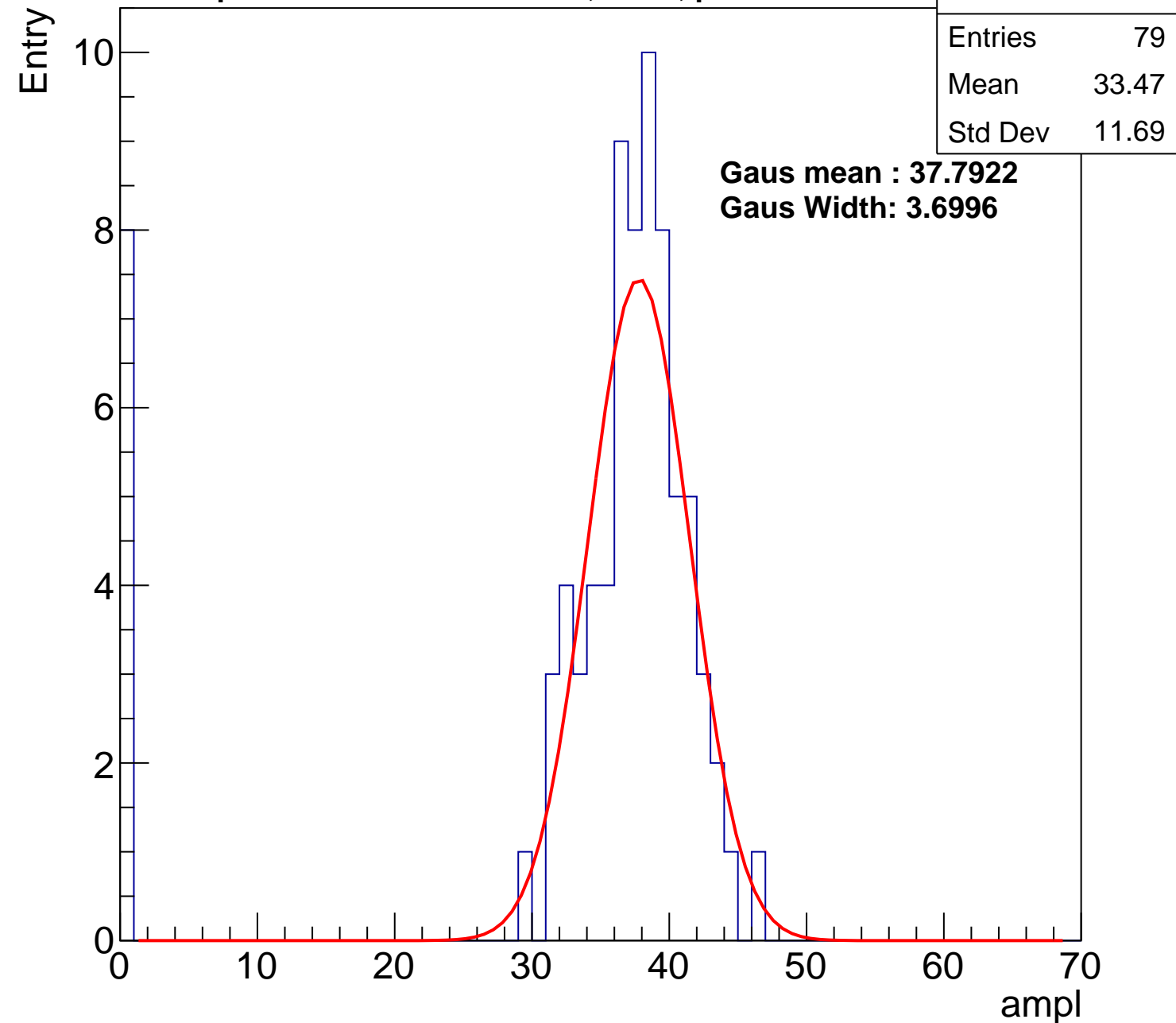
Gaus Width: 3.6996

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

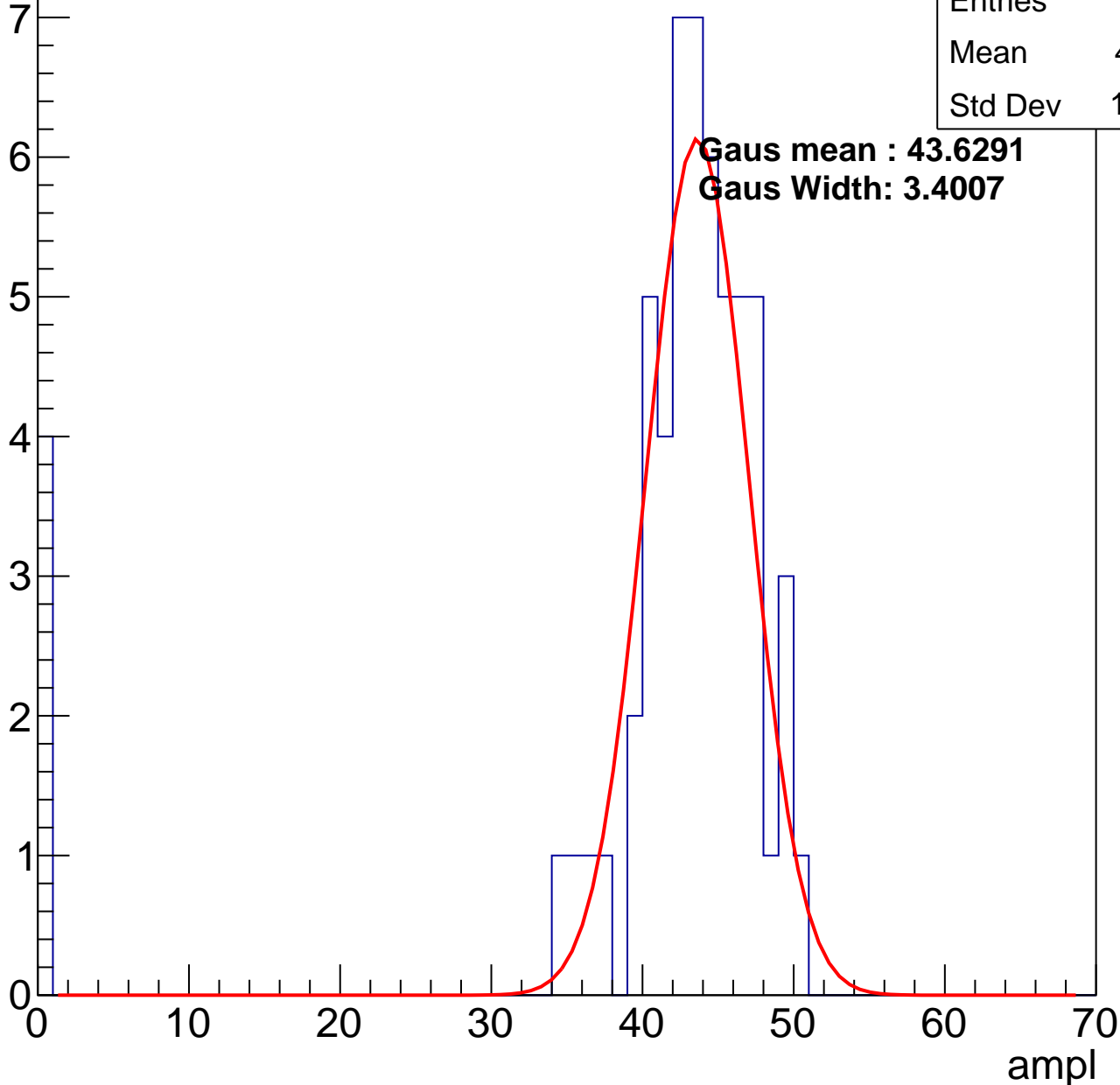


B1L103S, U8-ch95, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	40.31
Std Dev	11.37

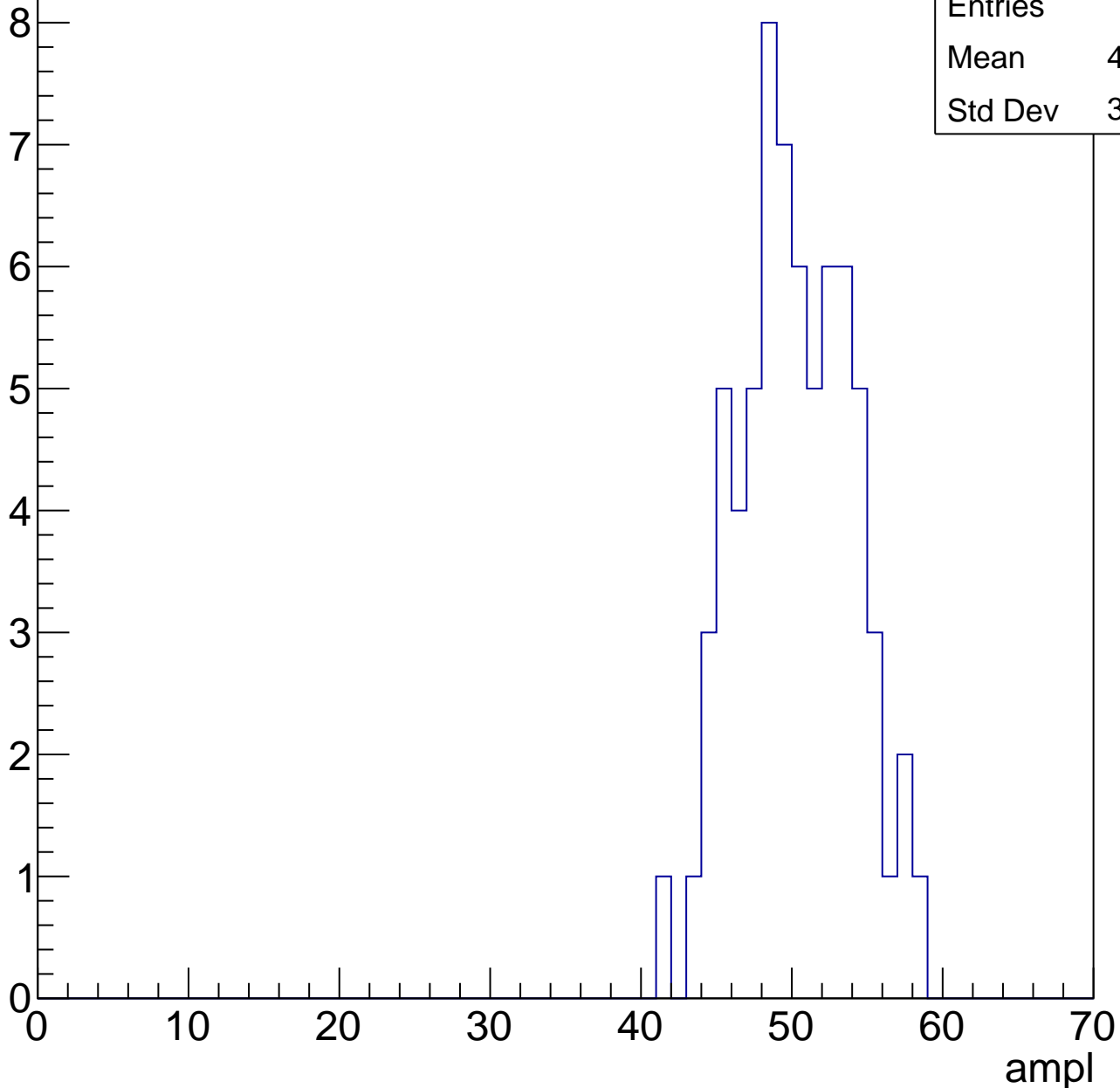


B1L103S, U8-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	49.78
Std Dev	3.698

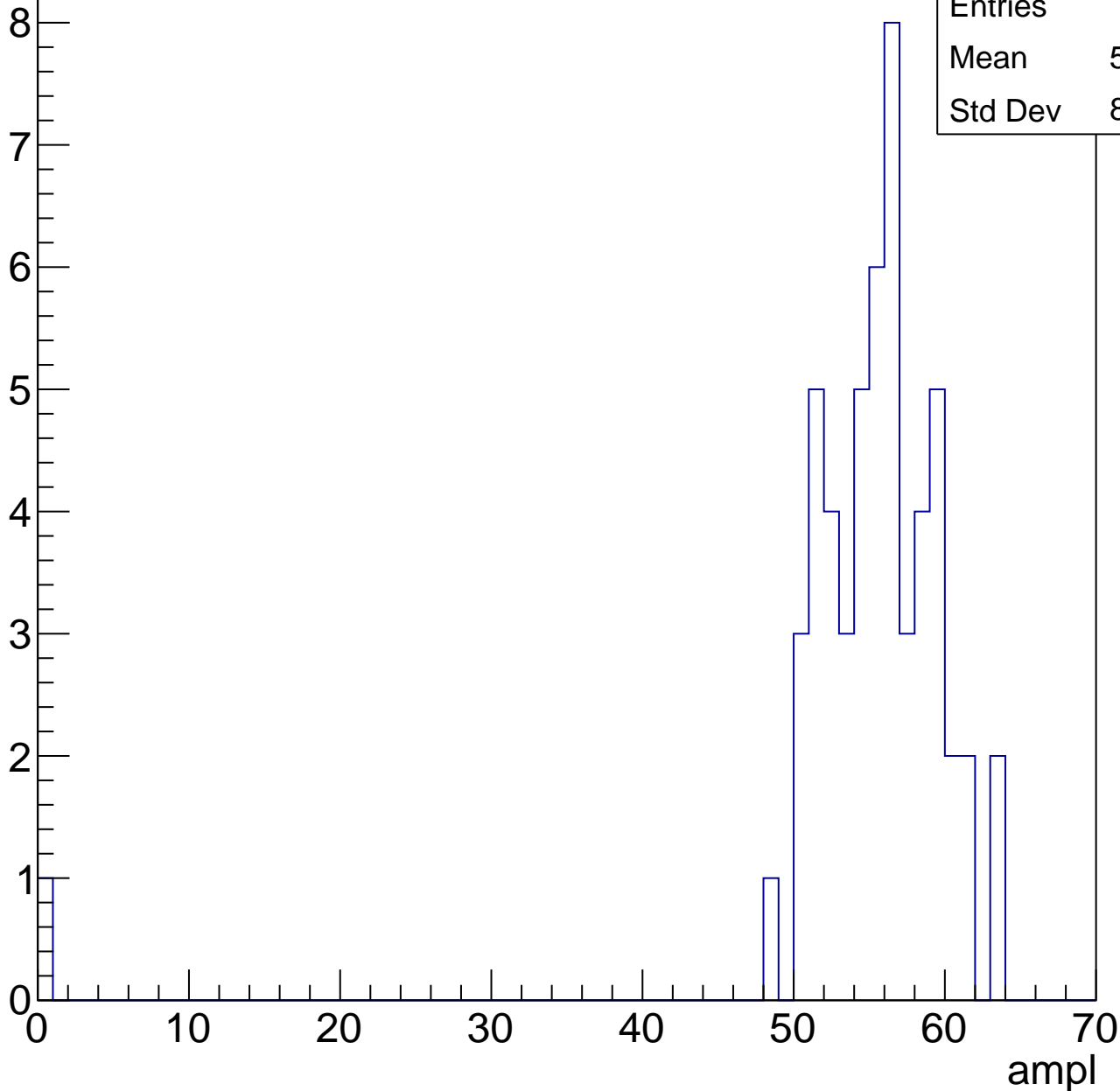


B1L103S, U8-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	54.33
Std Dev	8.215



B1L103S, U8-ch95, adc5

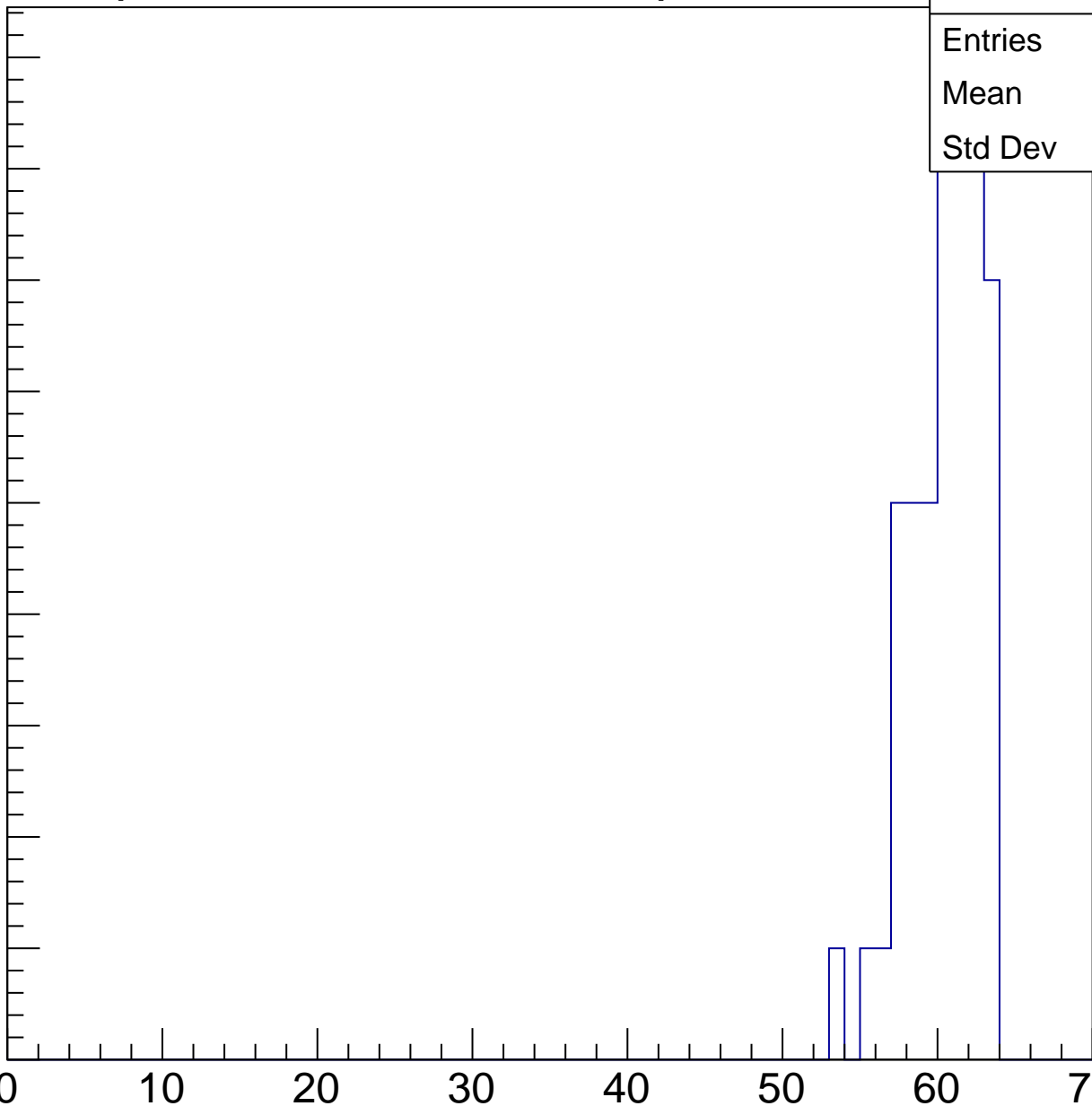
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	60.02
Std Dev	2.311

ampl



B1L103S, U8-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

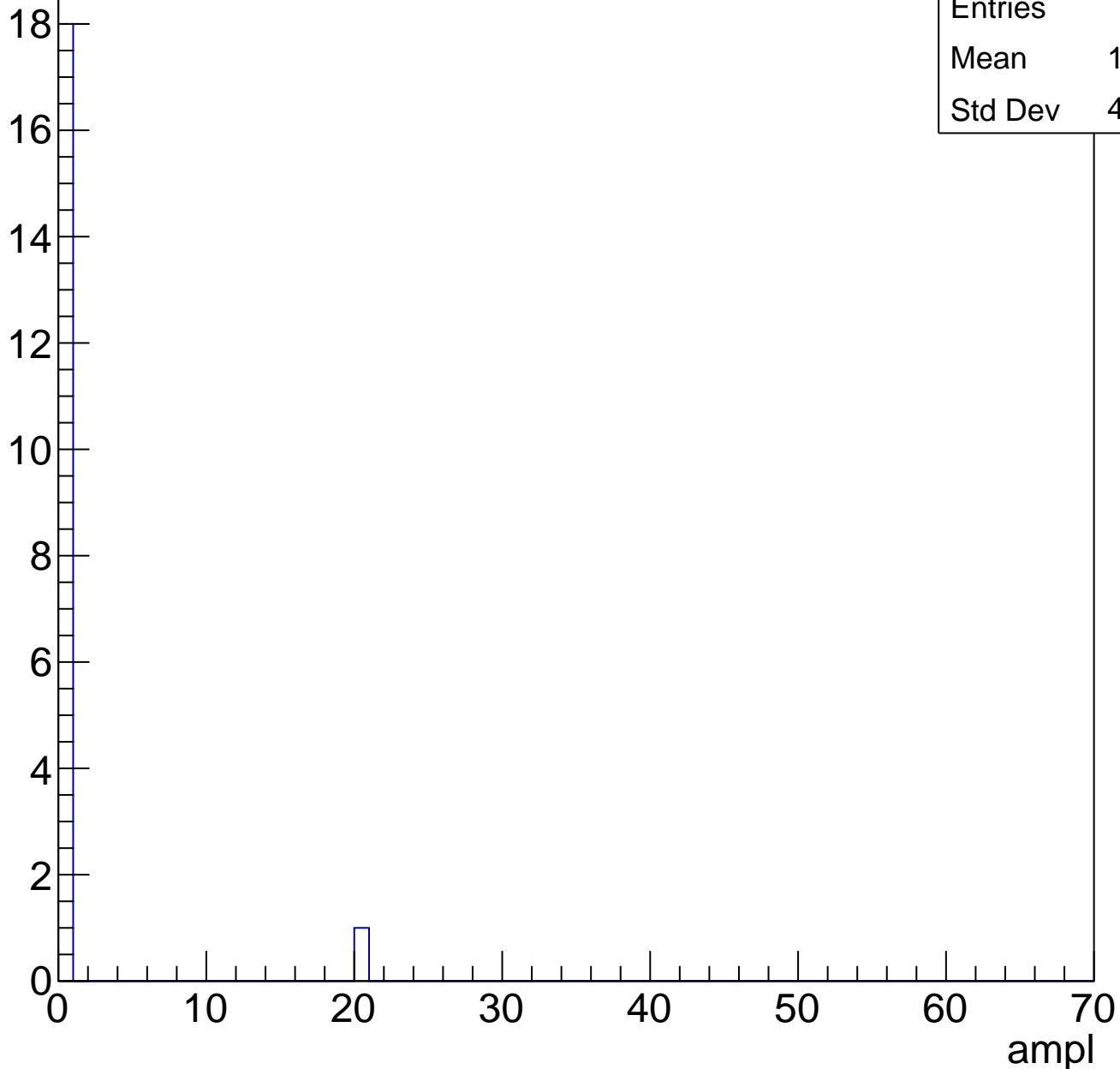


B1L103S, U8-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U8-ch96, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	20.1
Std Dev	12.72

Gaus mean : 28.0773

Gaus Width: 4.6362

Entry

25

20

15

10

5

0

0

10

20

30

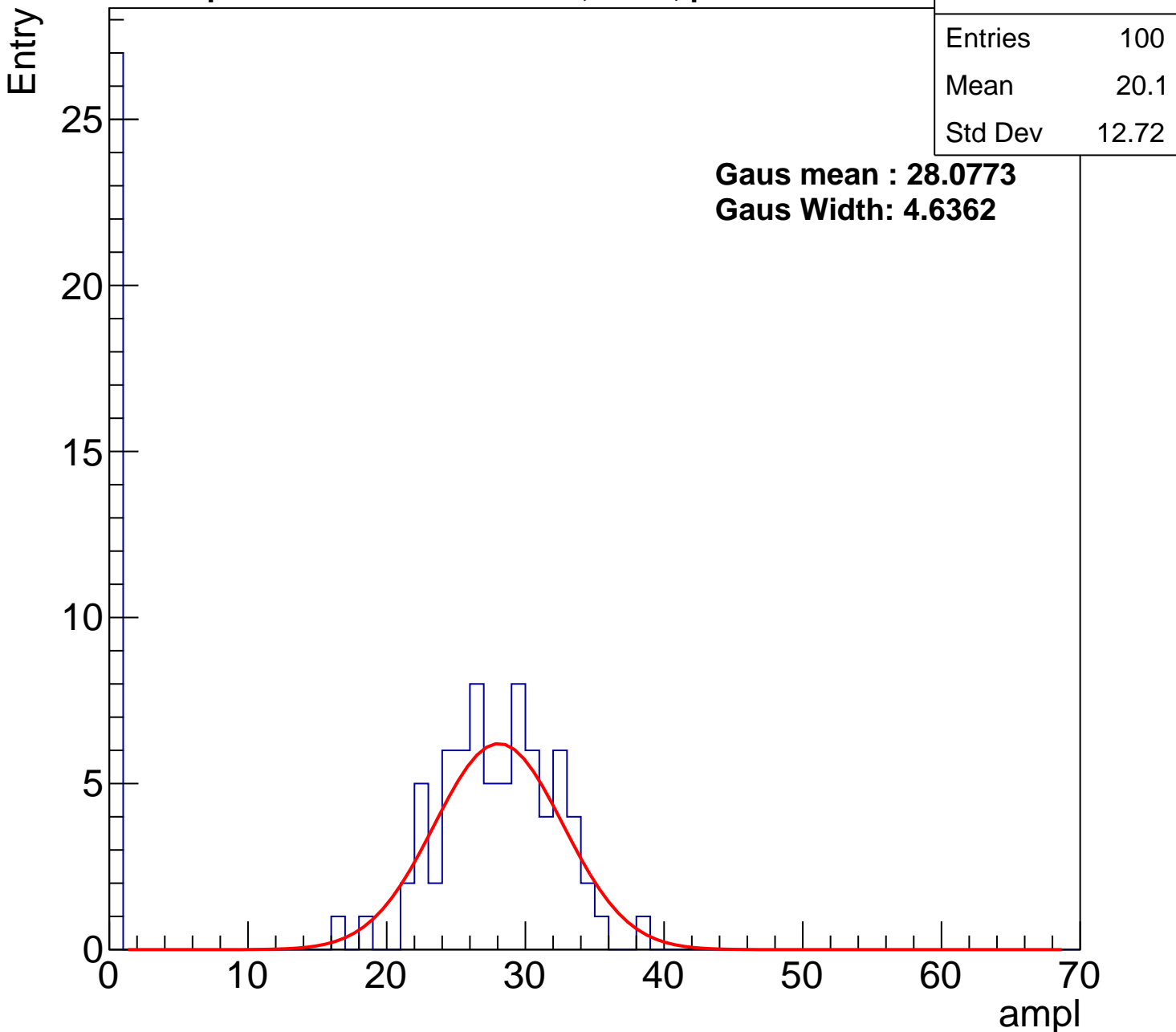
40

50

60

70

ampl



B1L103S, U8-ch96, adc1

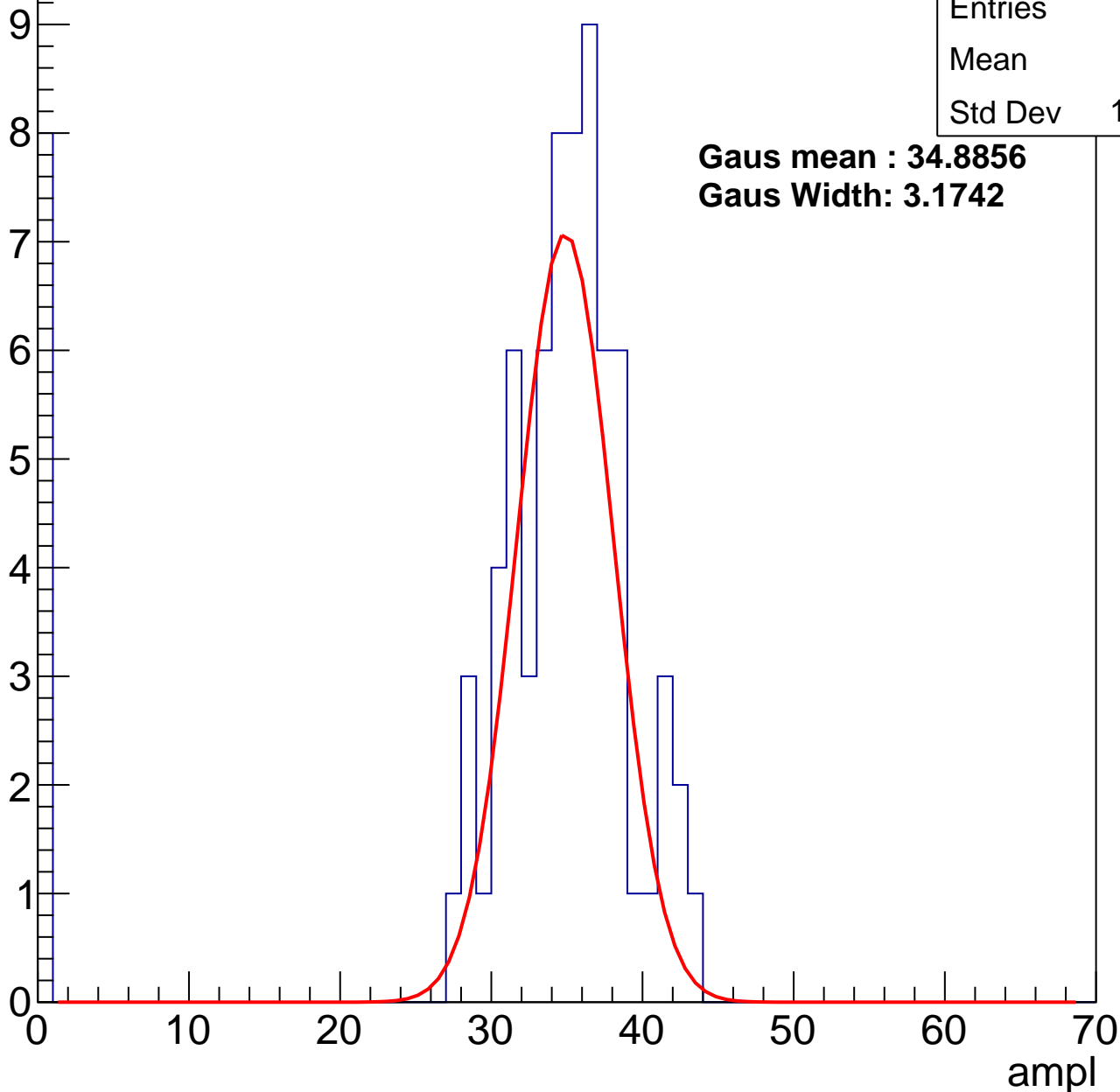
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	31.1
Std Dev	11.13

Gaus mean : 34.8856

Gaus Width: 3.1742



B1L103S, U8-ch96, adc2

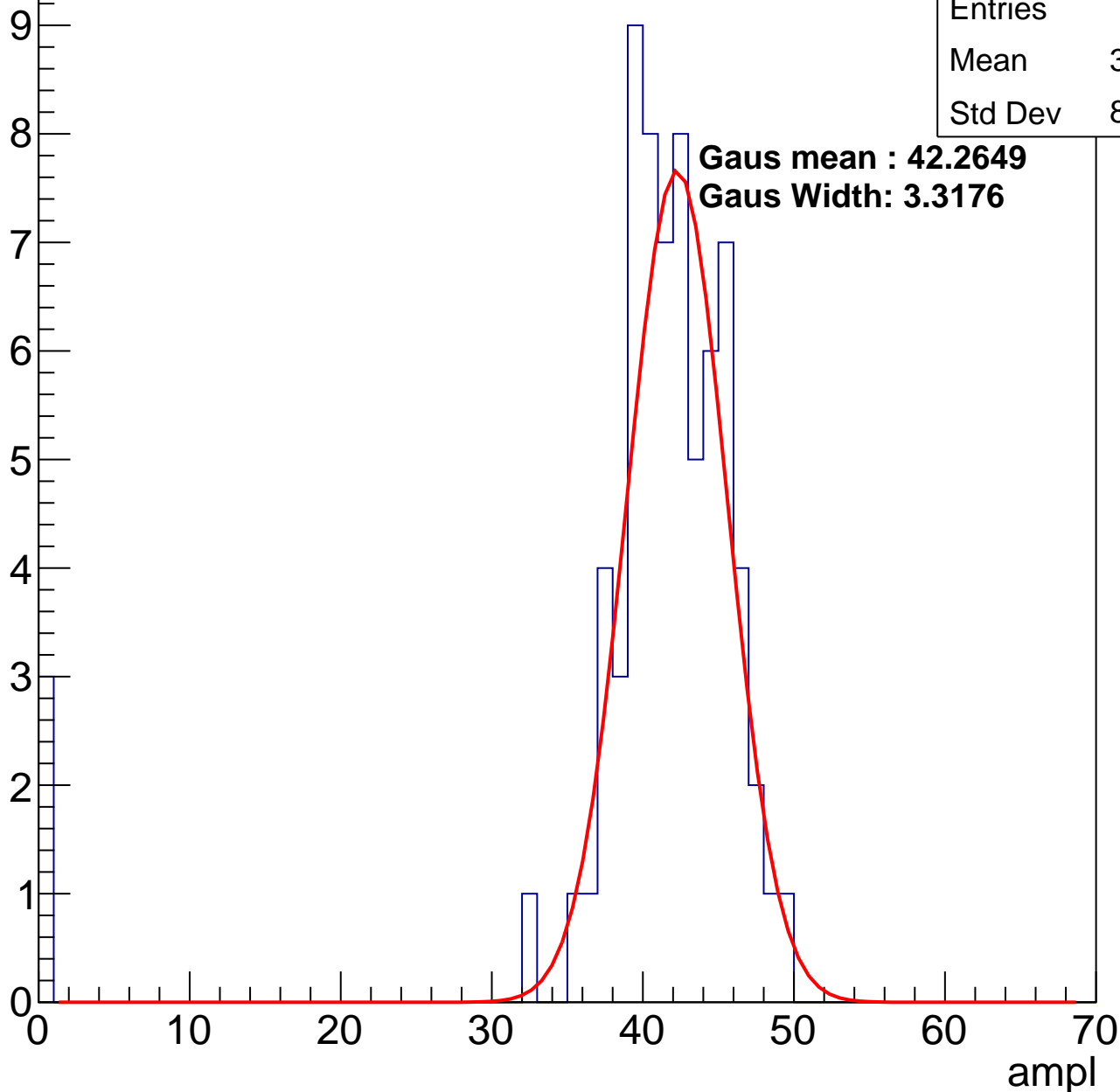
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	39.83
Std Dev	8.962

Gaus mean : 42.2649

Gaus Width: 3.3176

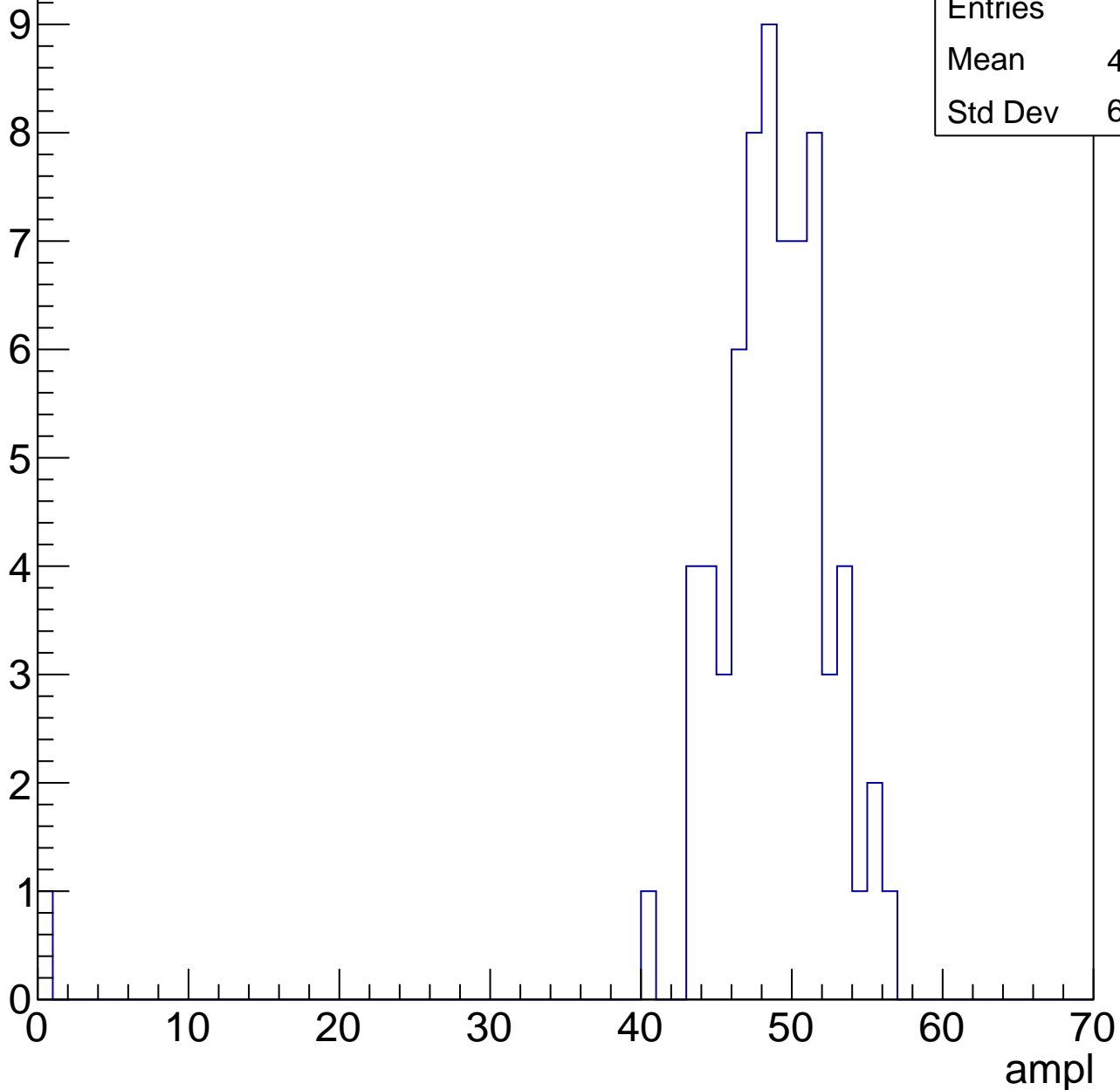


B1L103S, U8-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	47.77
Std Dev	6.638

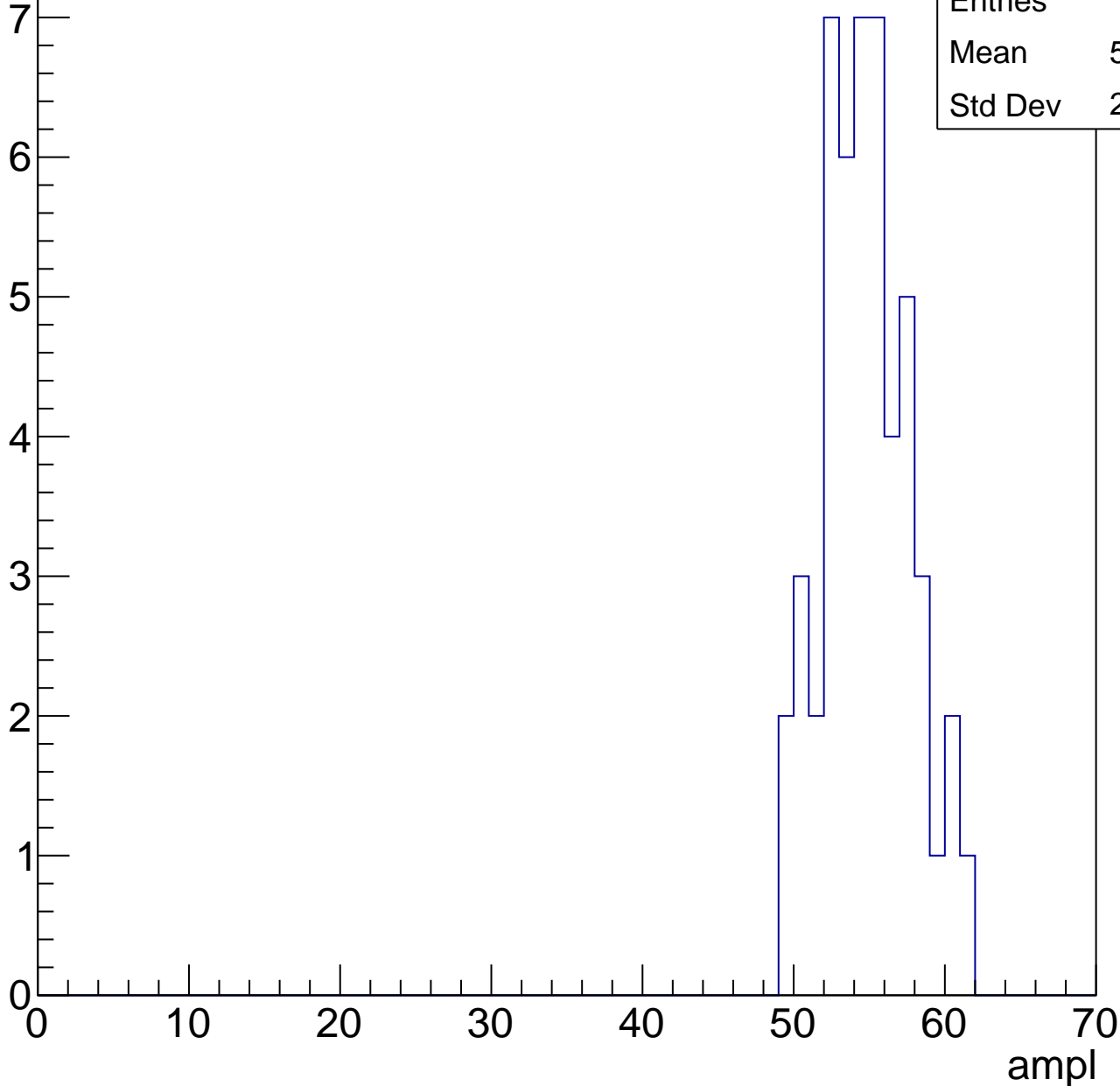


B1L103S, U8-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	54.36
Std Dev	2.848



B1L103S, U8-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries

45

Mean

59.16

Std Dev

2.547

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

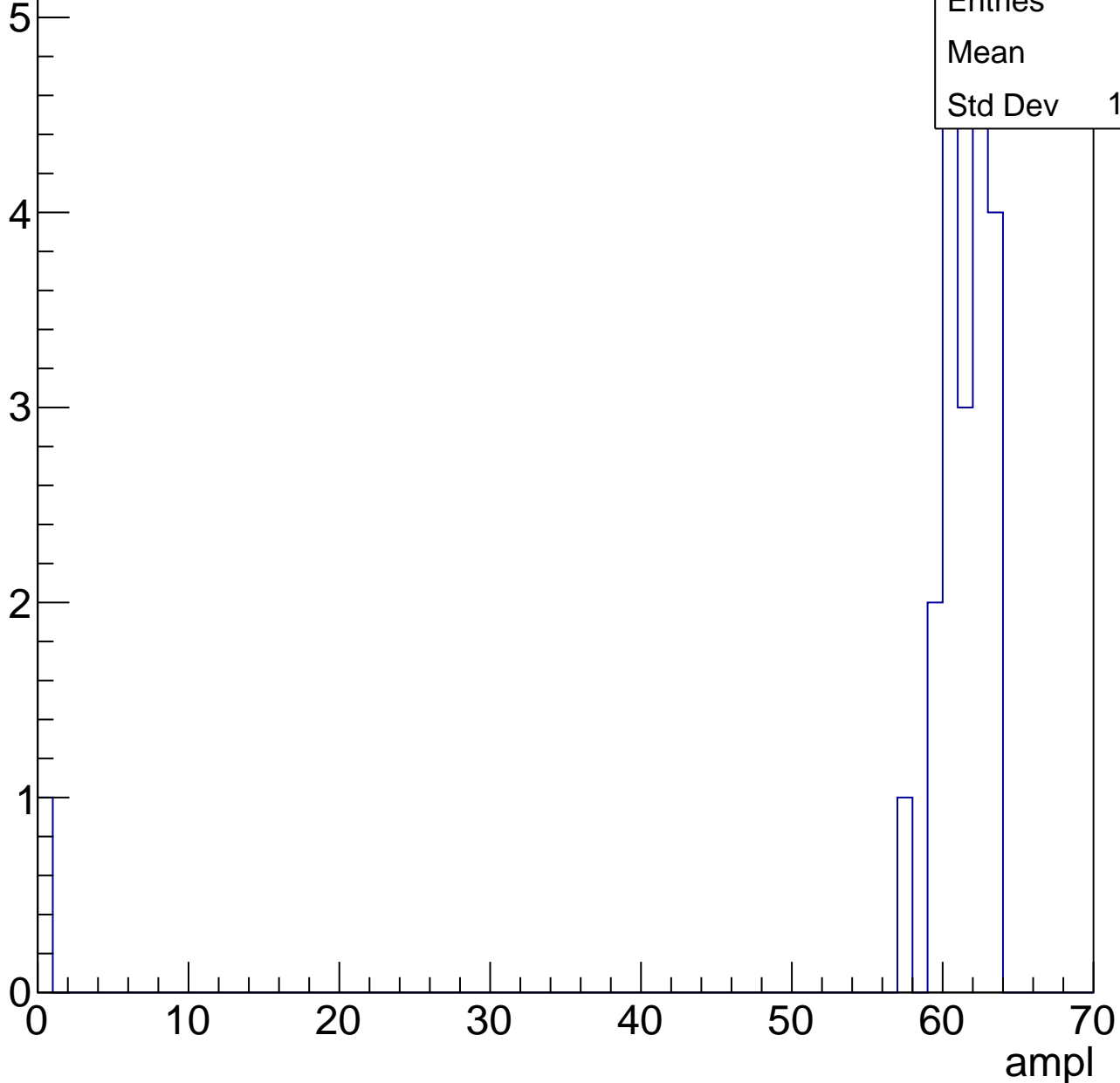
7

B1L103S, U8-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.1
Std Dev	13.08



B1L103S, U8-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



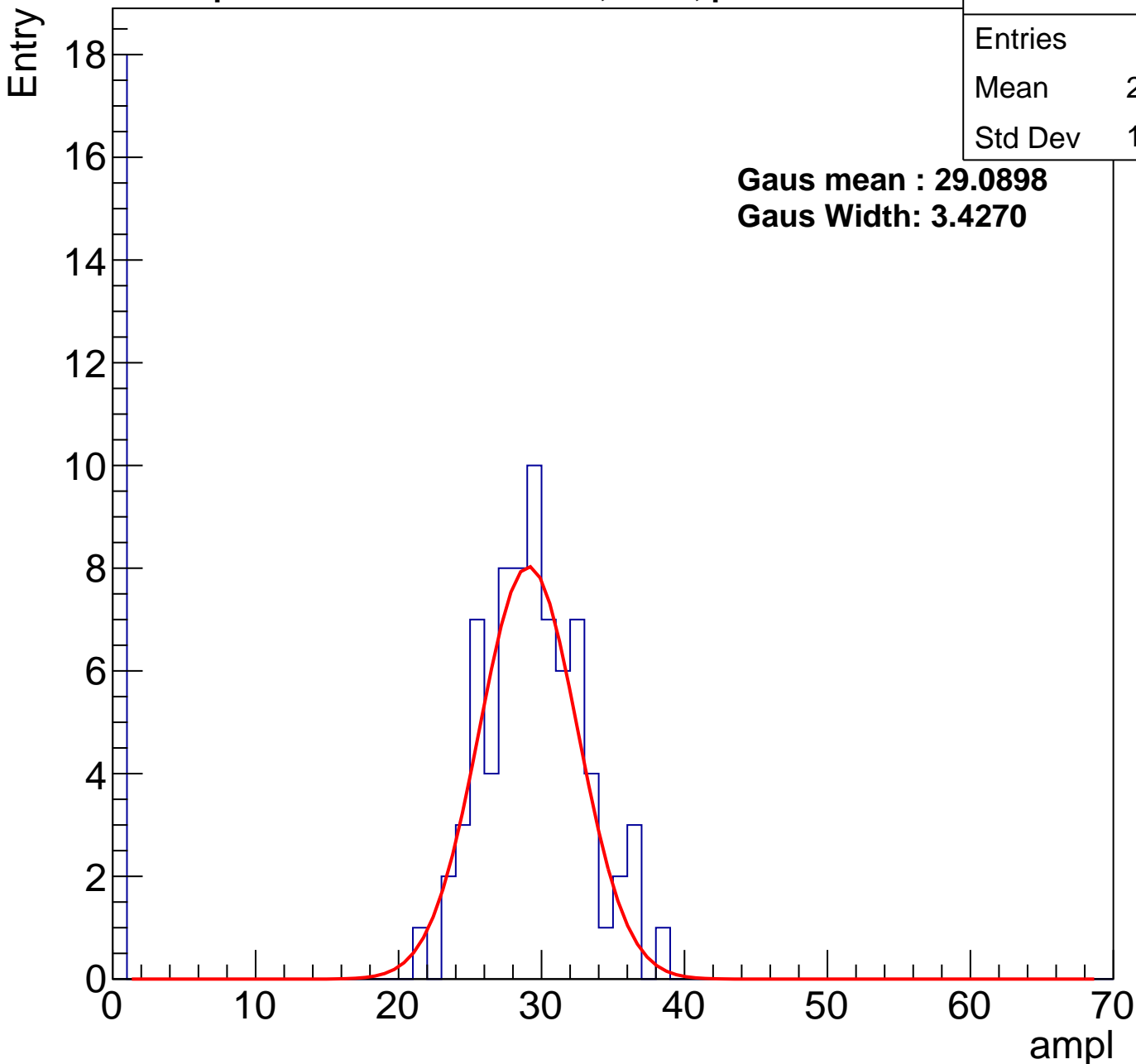
B1L103S, U8-ch97, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	23.37
Std Dev	11.93

Gaus mean : 29.0898

Gaus Width: 3.4270



B1L103S, U8-ch97, adc1

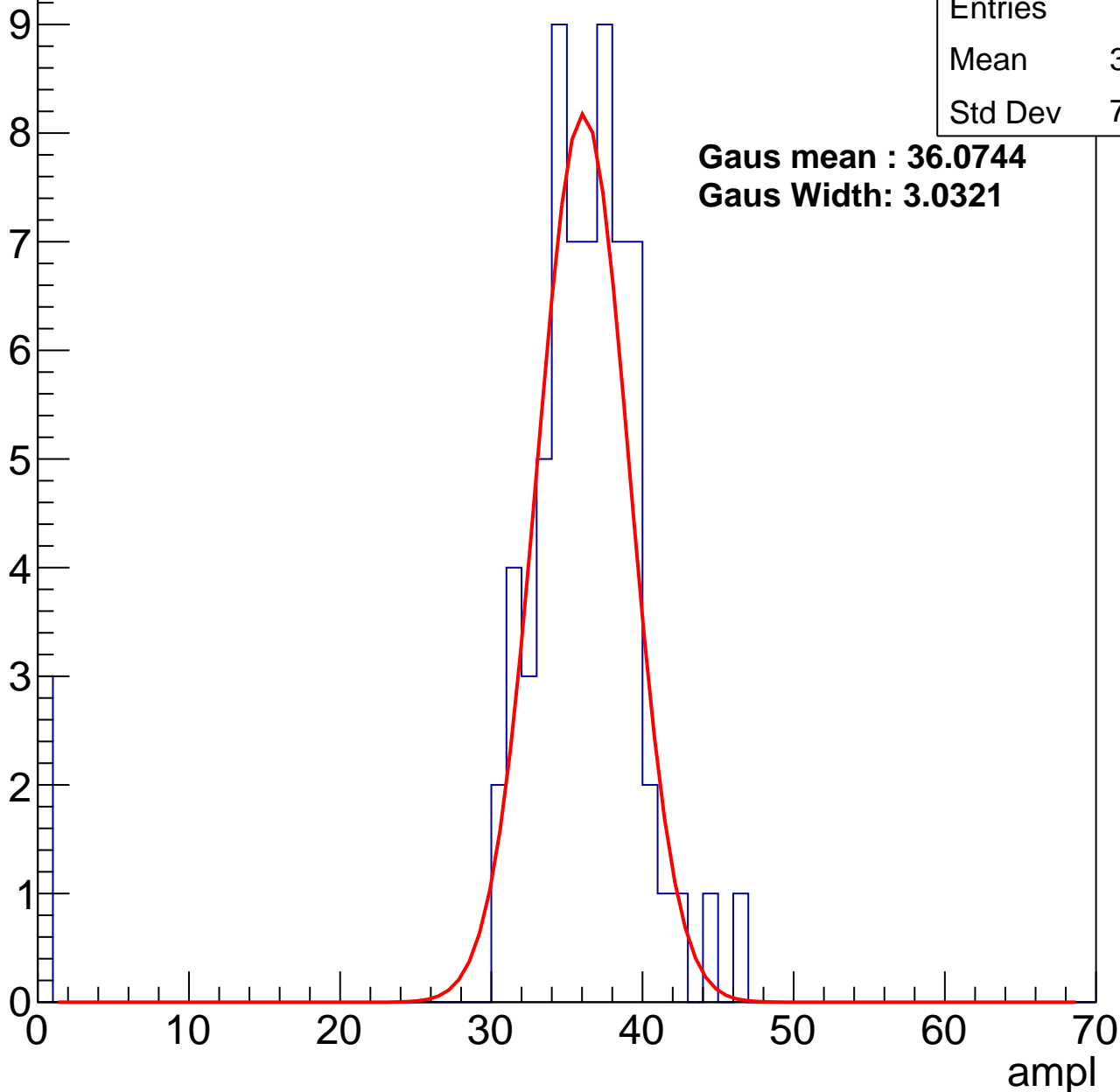
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	34.39
Std Dev	7.962

Gaus mean : 36.0744

Gaus Width: 3.0321



B1L103S, U8-ch97, adc2

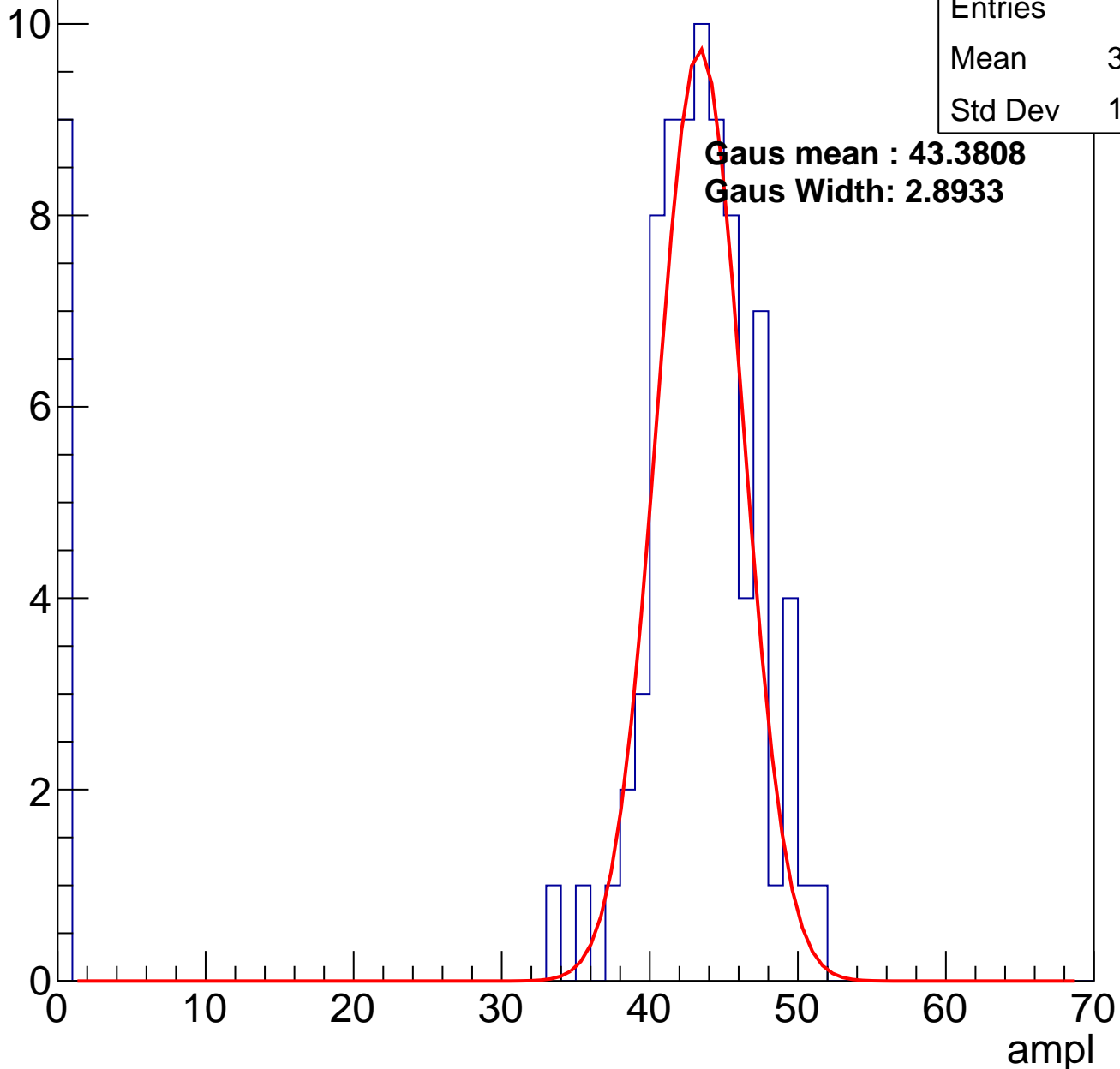
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	38.74
Std Dev	13.45

Gaus mean : 43.3808

Gaus Width: 2.8933

Entry

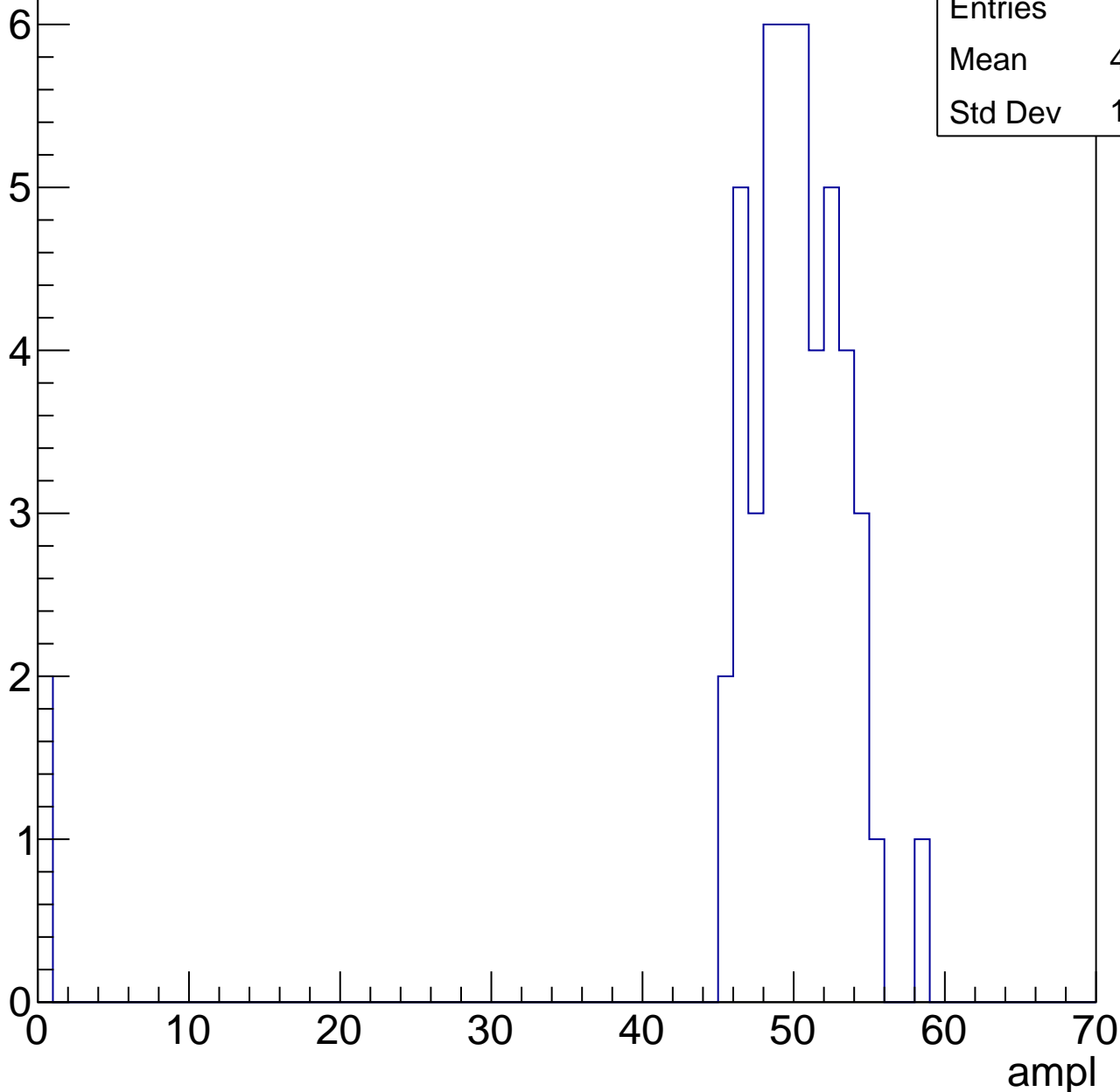


B1L103S, U8-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	47.79
Std Dev	10.36

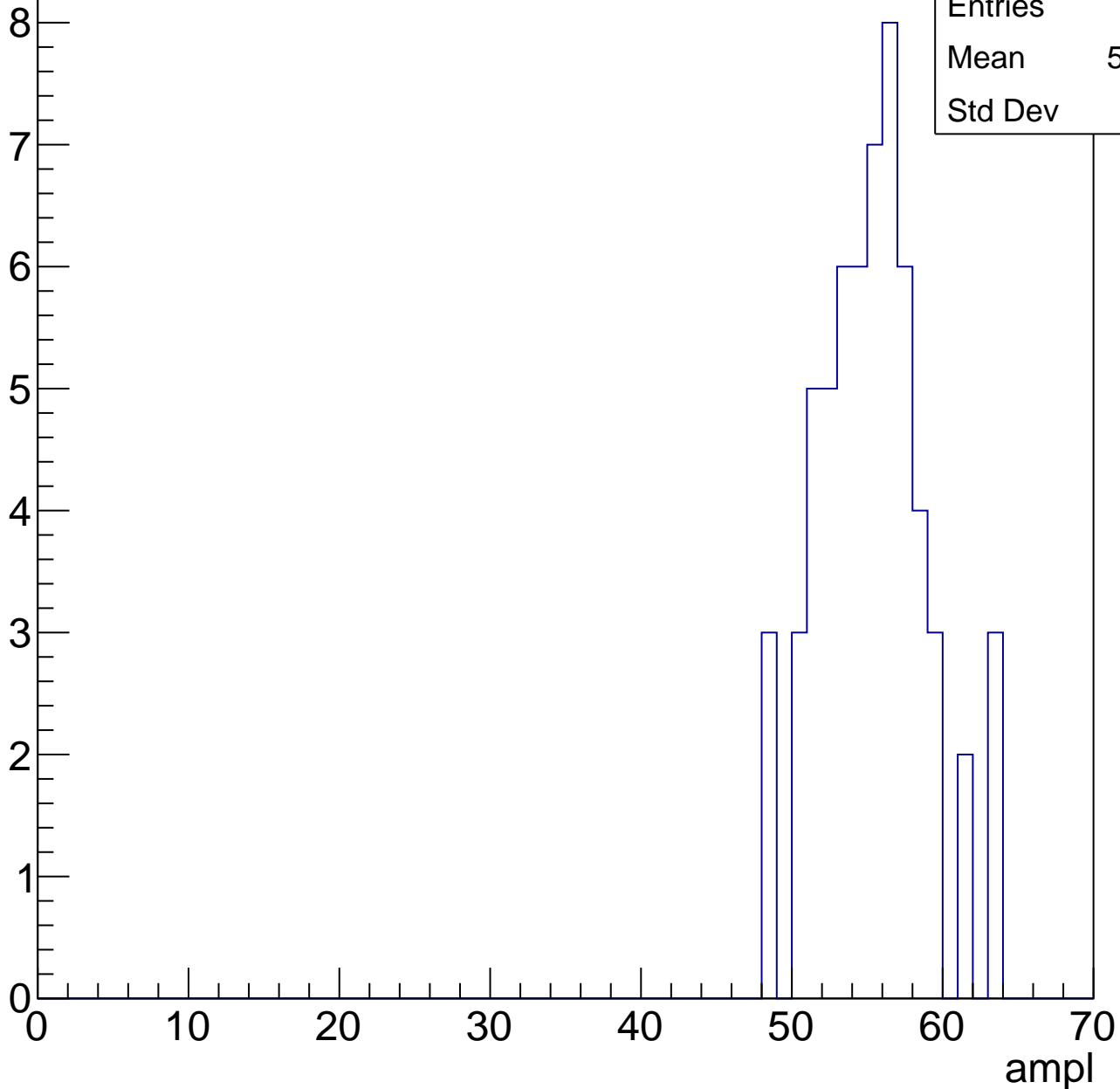


B1L103S, U8-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	54.85
Std Dev	3.52

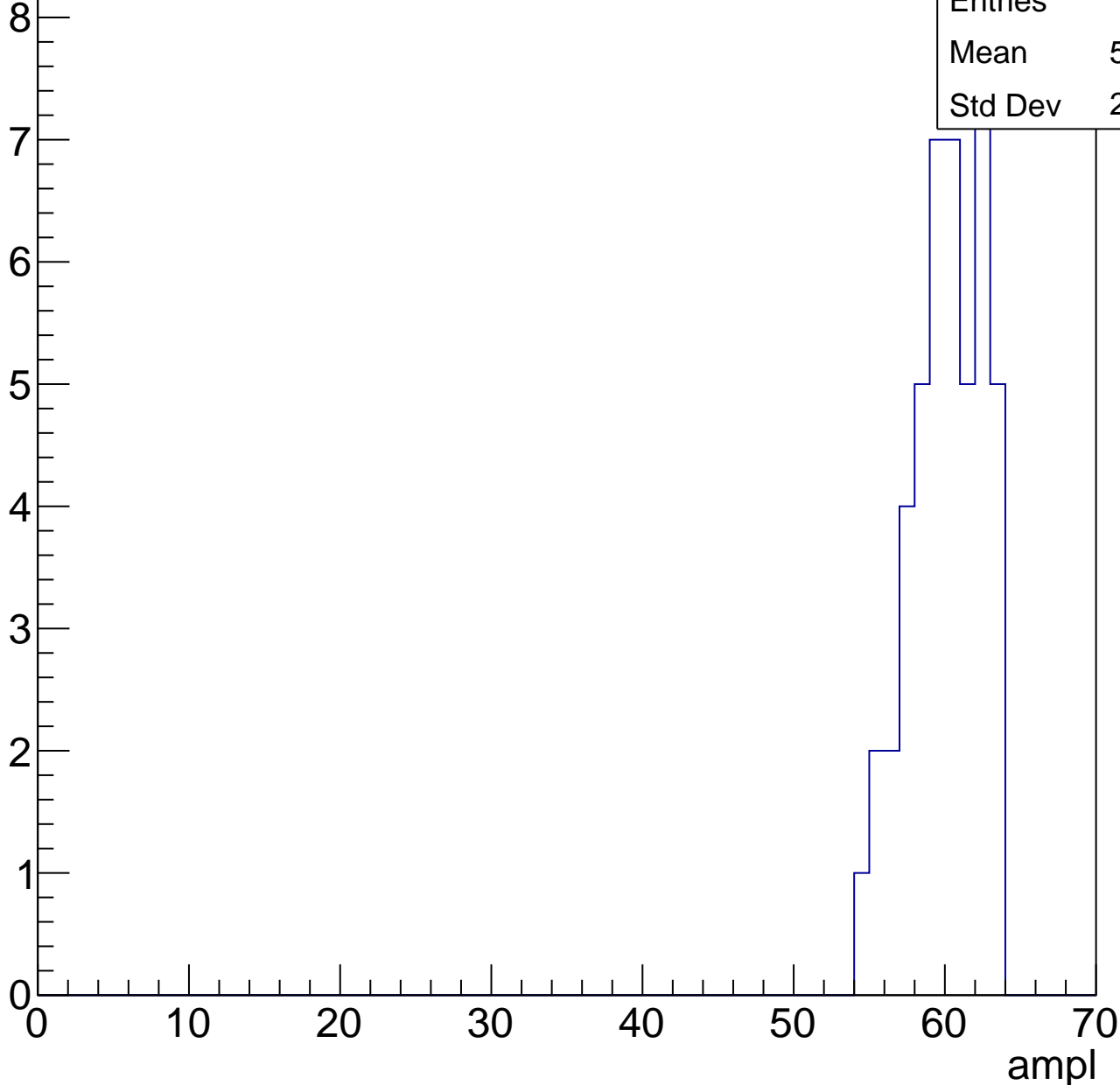


B1L103S, U8-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	59.63
Std Dev	2.362



B1L103S, U8-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

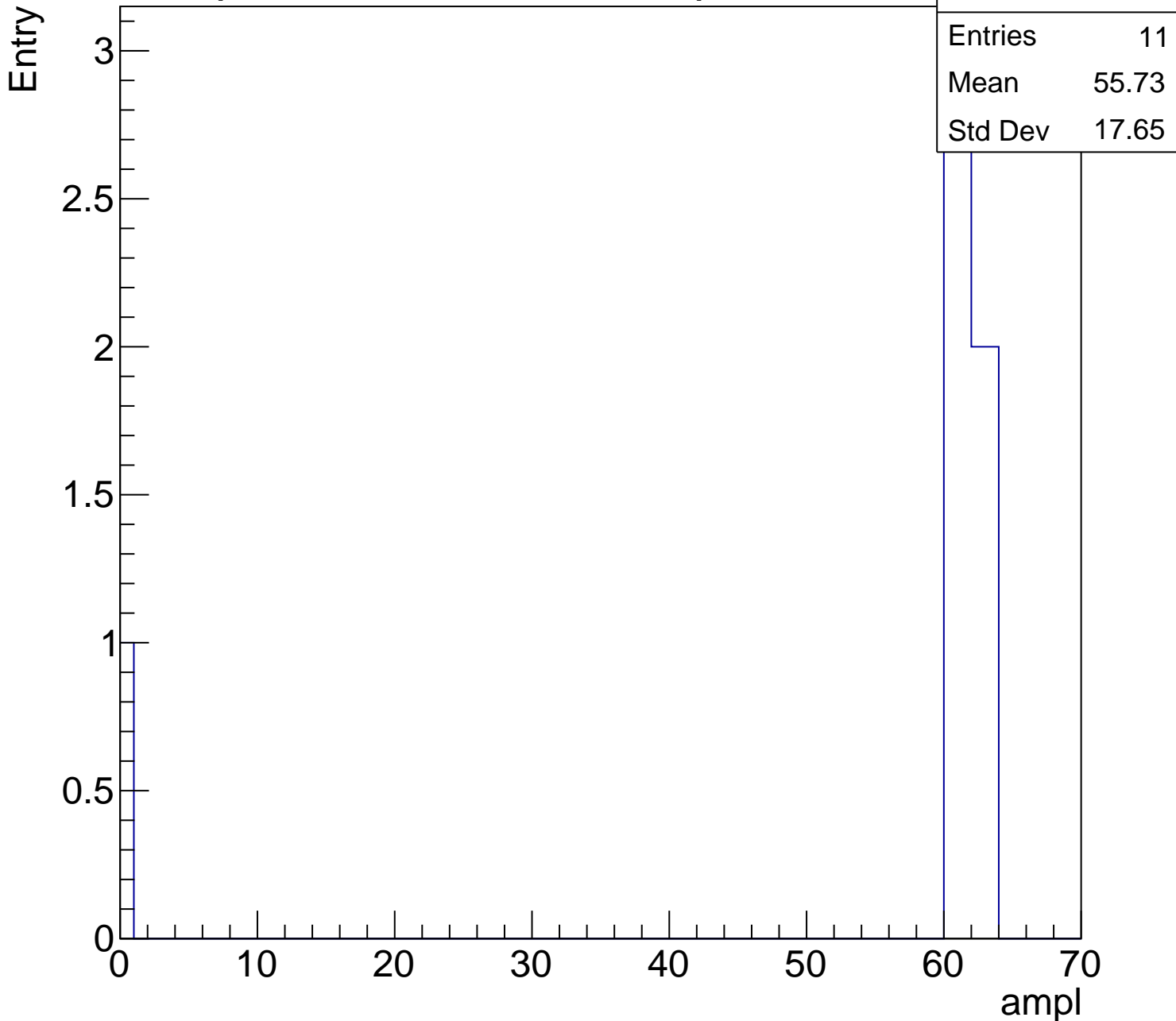
1

0.5

0

ampl

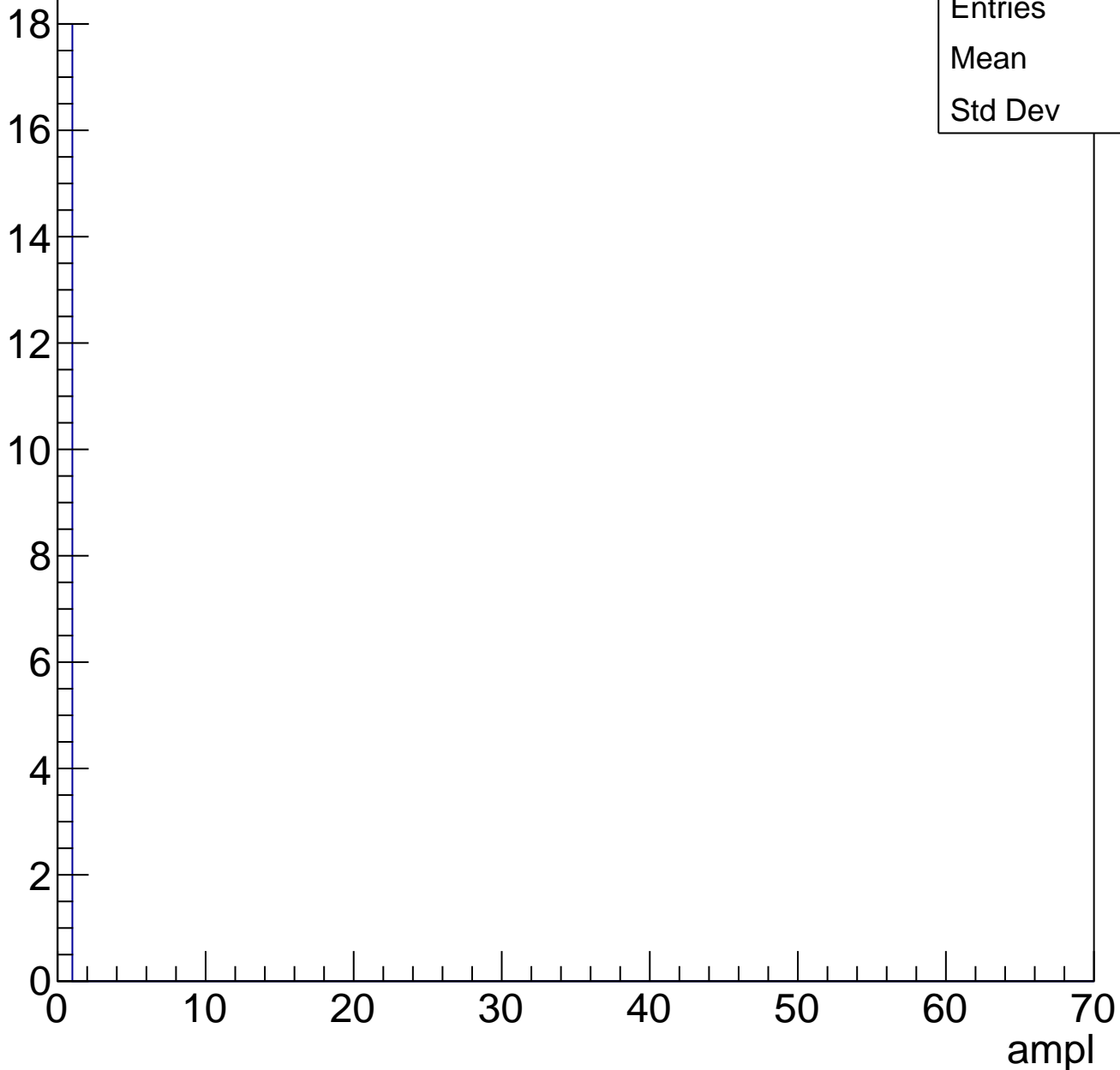
Entries	11
Mean	55.73
Std Dev	17.65



B1L103S, U8-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch98, adc0

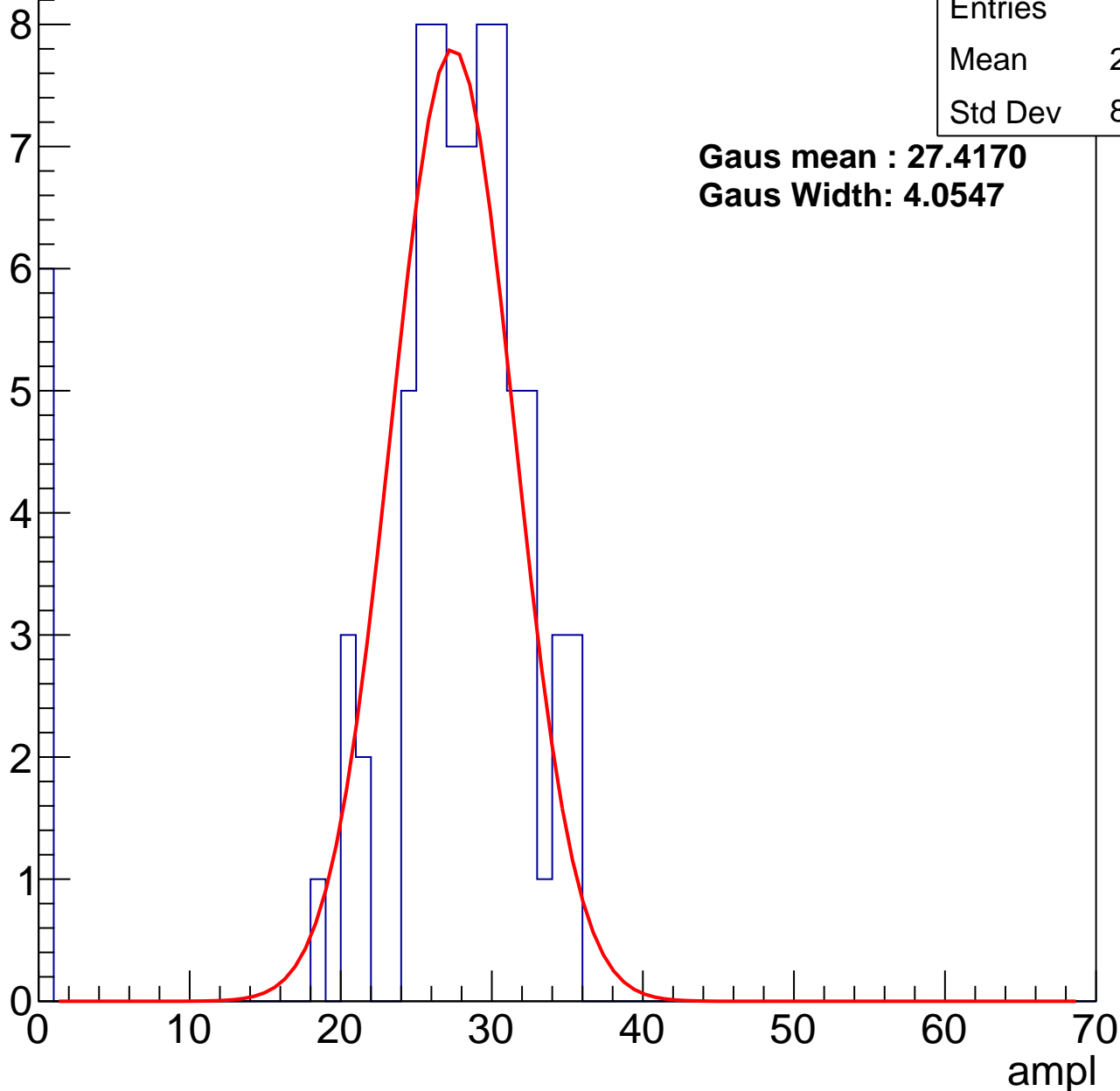
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	25.75
Std Dev	8.159

Gaus mean : 27.4170

Gaus Width: 4.0547



B1L103S, U8-ch98, adc1

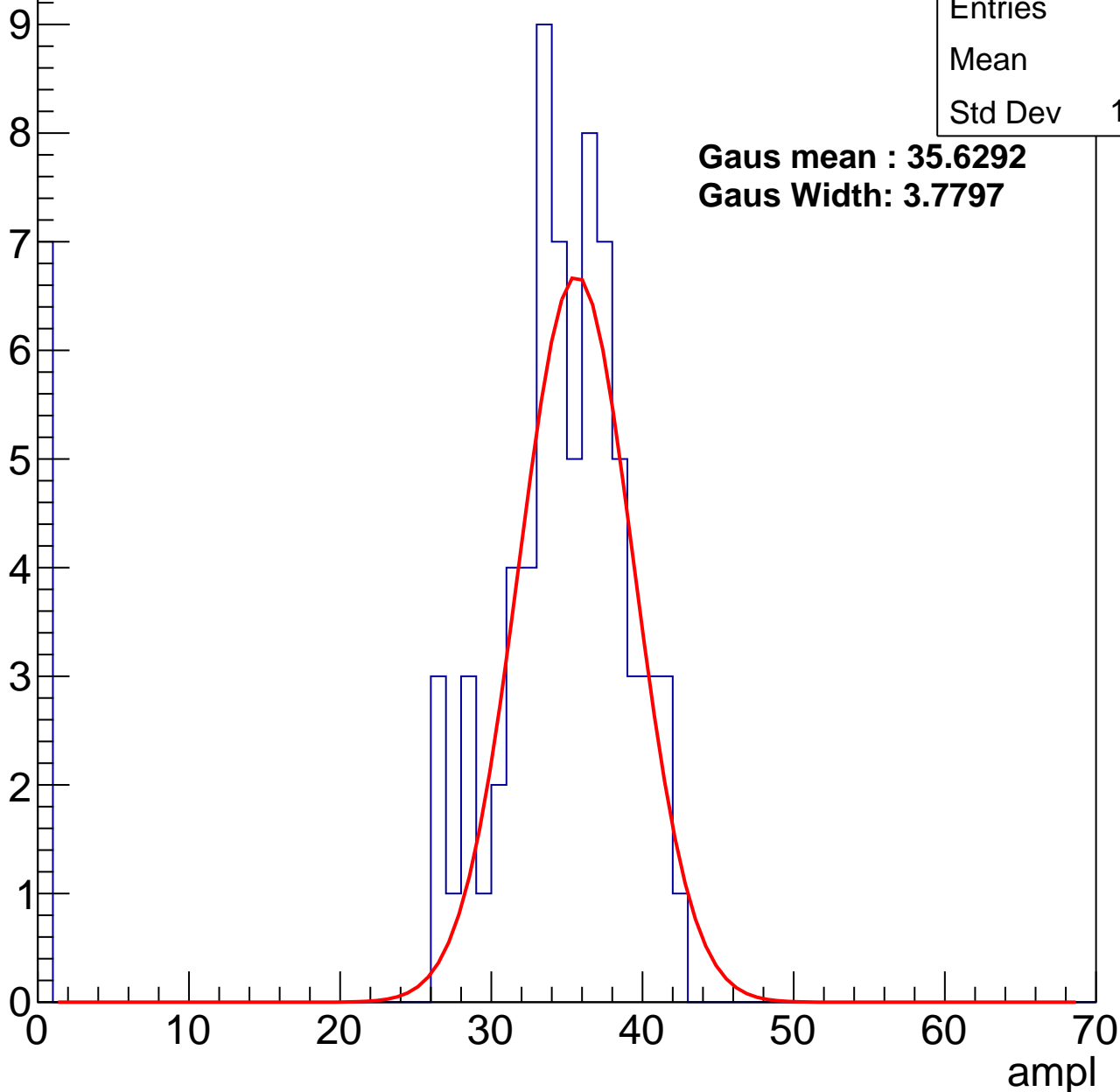
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	31.3
Std Dev	10.63

Gaus mean : 35.6292

Gaus Width: 3.7797



B1L103S, U8-ch98, adc2

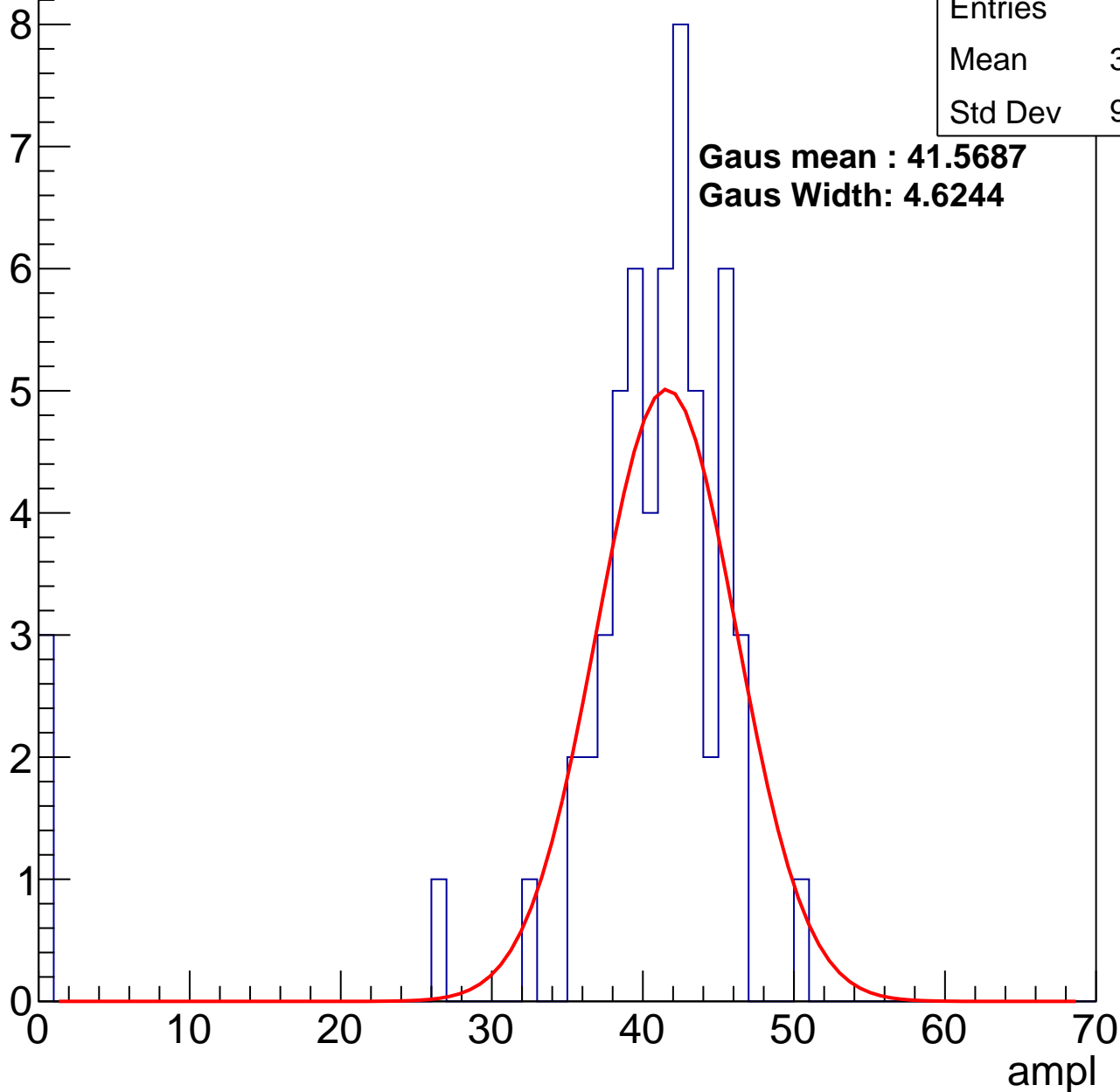
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	38.59
Std Dev	9.784

Gaus mean : 41.5687

Gaus Width: 4.6244

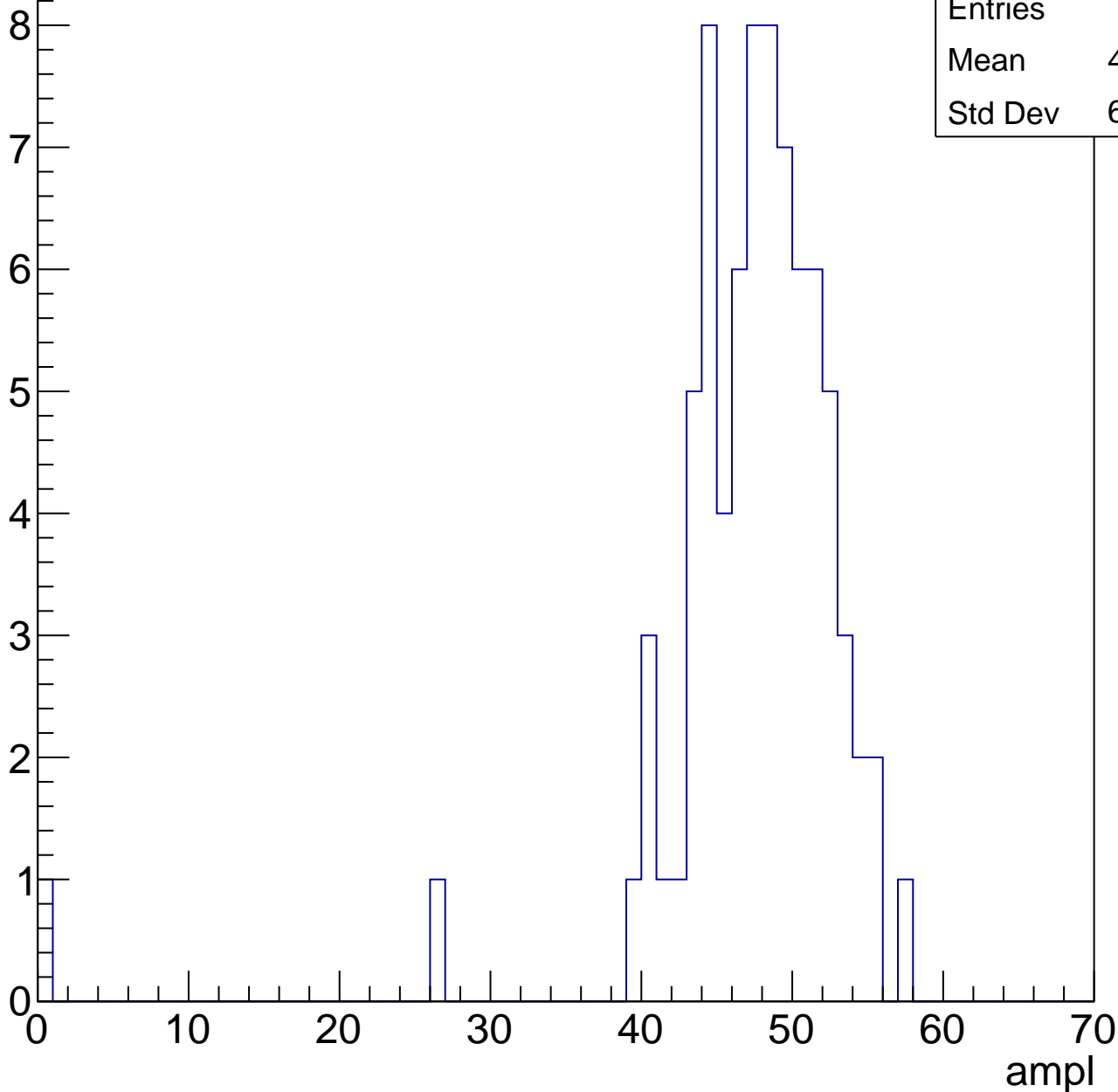


B1L103S, U8-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	46.76
Std Dev	6.969

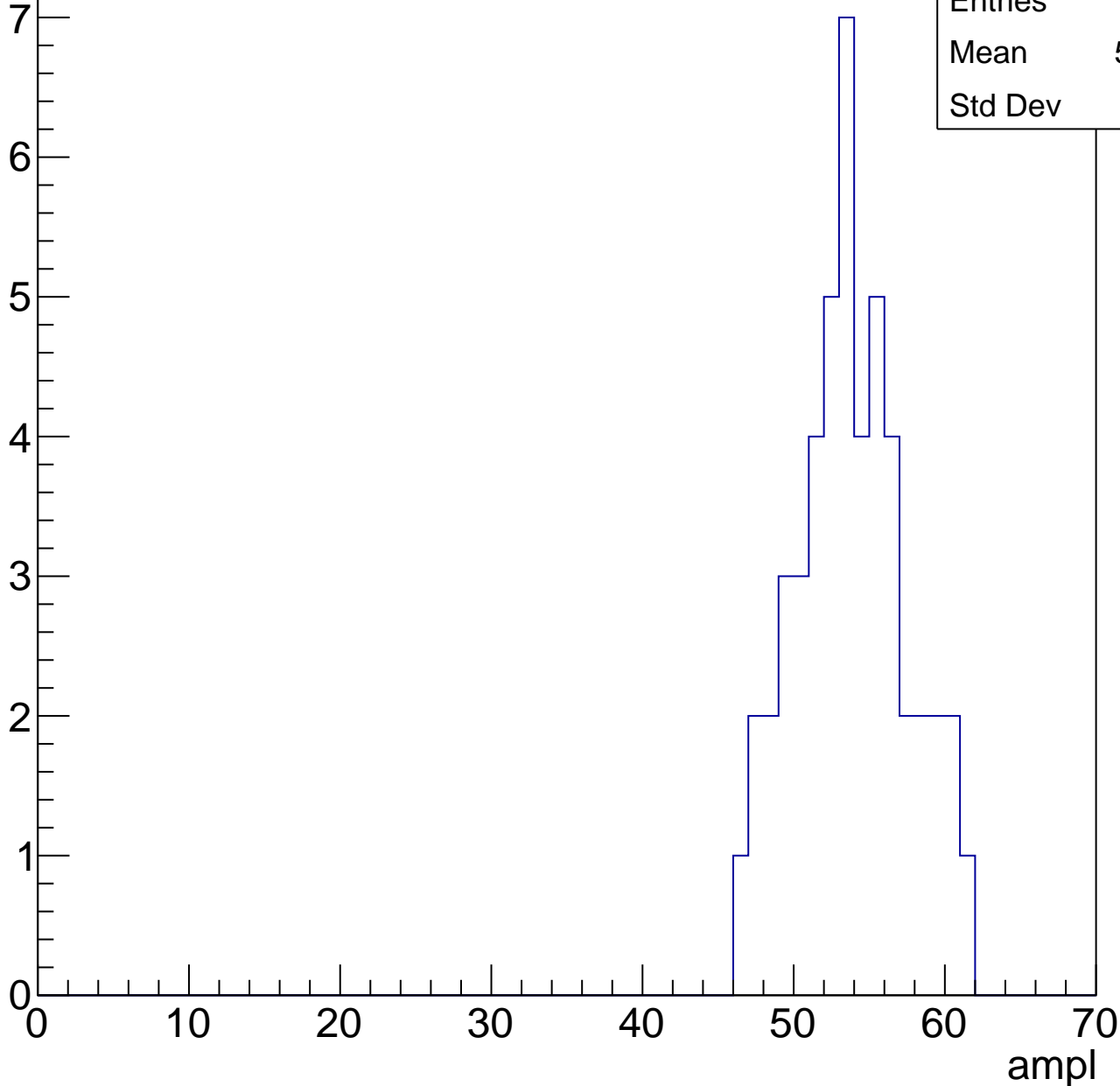


B1L103S, U8-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

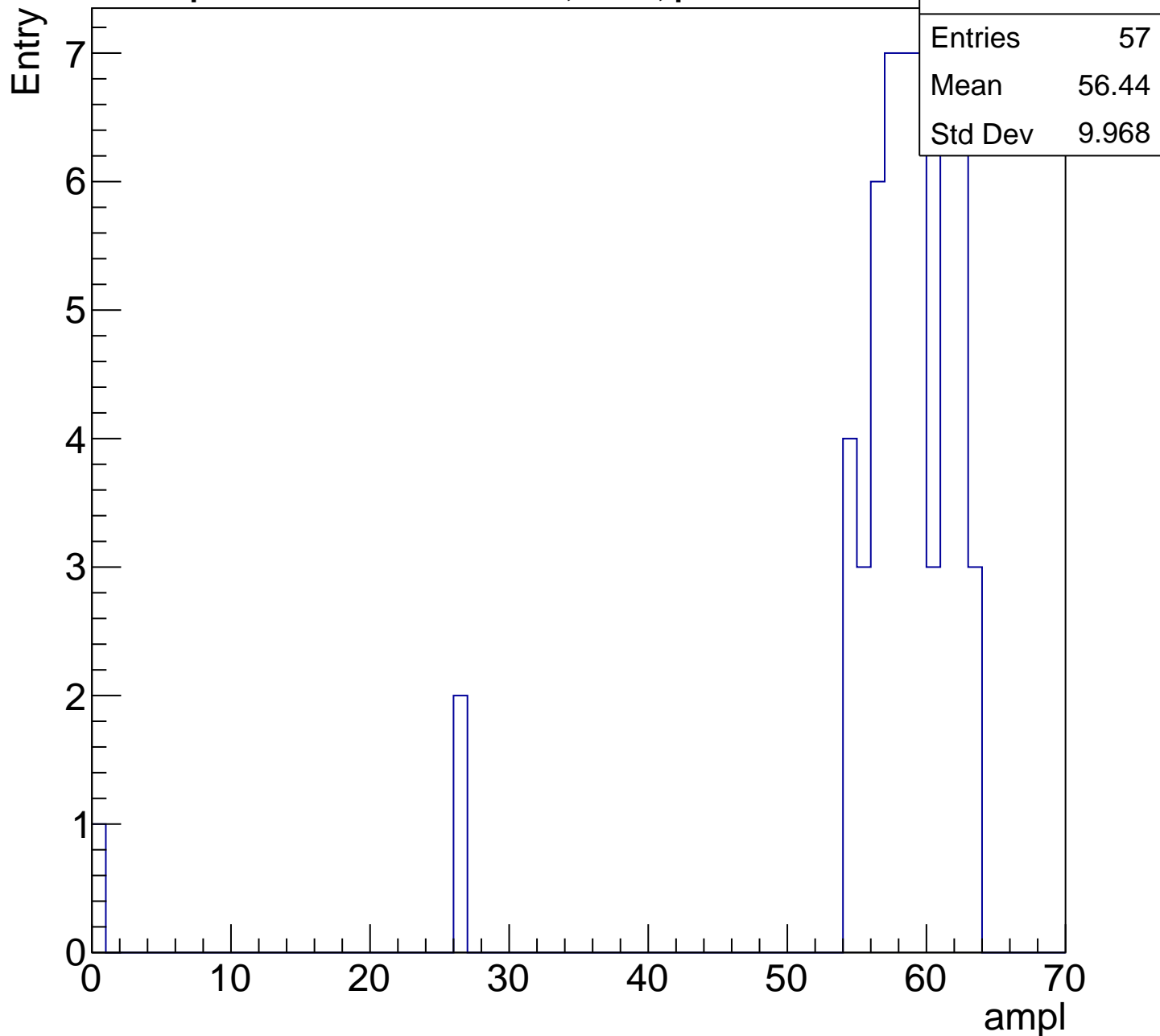
Entry

Entries	49
Mean	53.31
Std Dev	3.61



B1L103S, U8-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

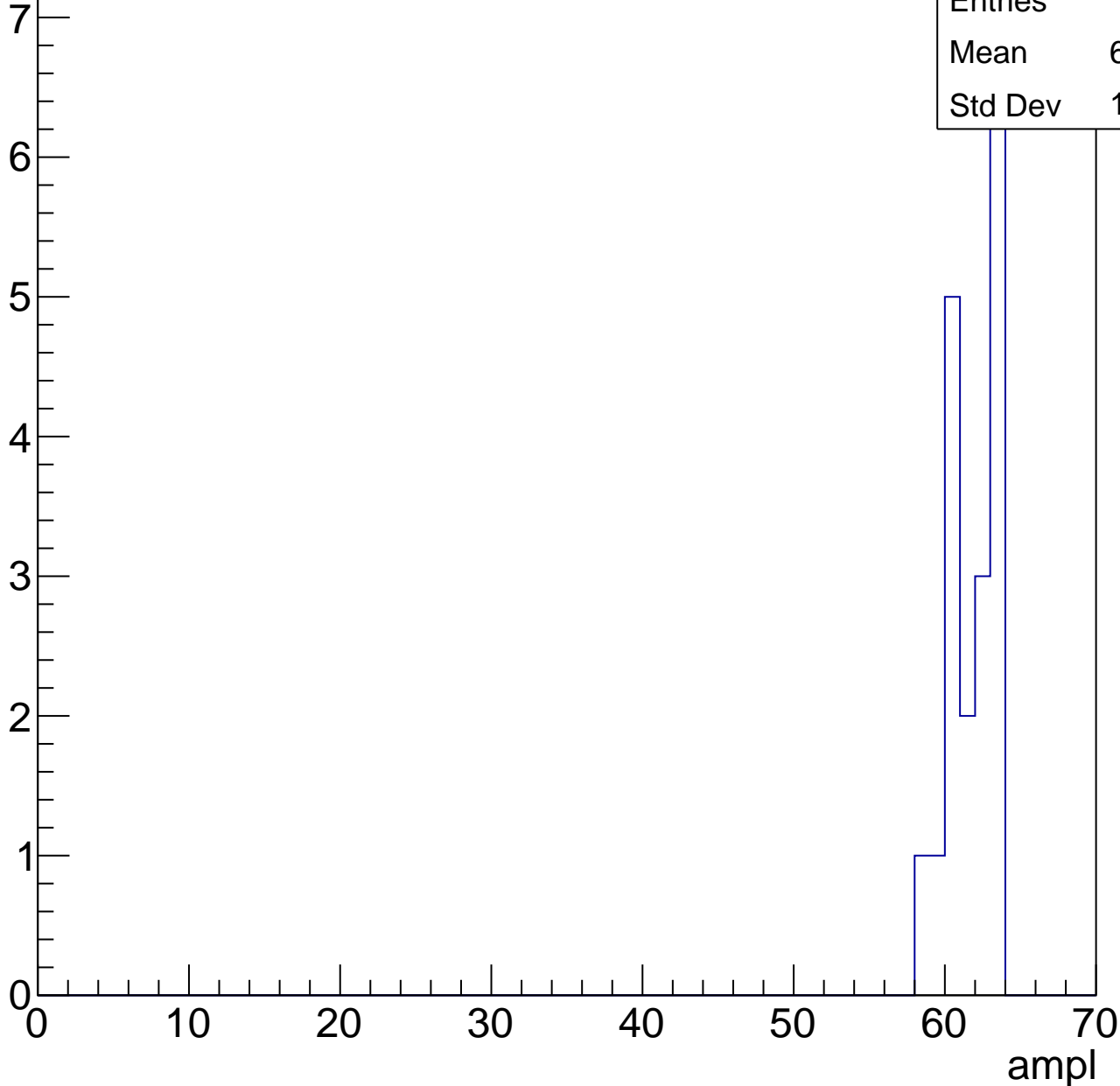


B1L103S, U8-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61.37
Std Dev	1.563



B1L103S, U8-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch99, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	23.24
Std Dev	12.13

Gaus mean : 29.4306

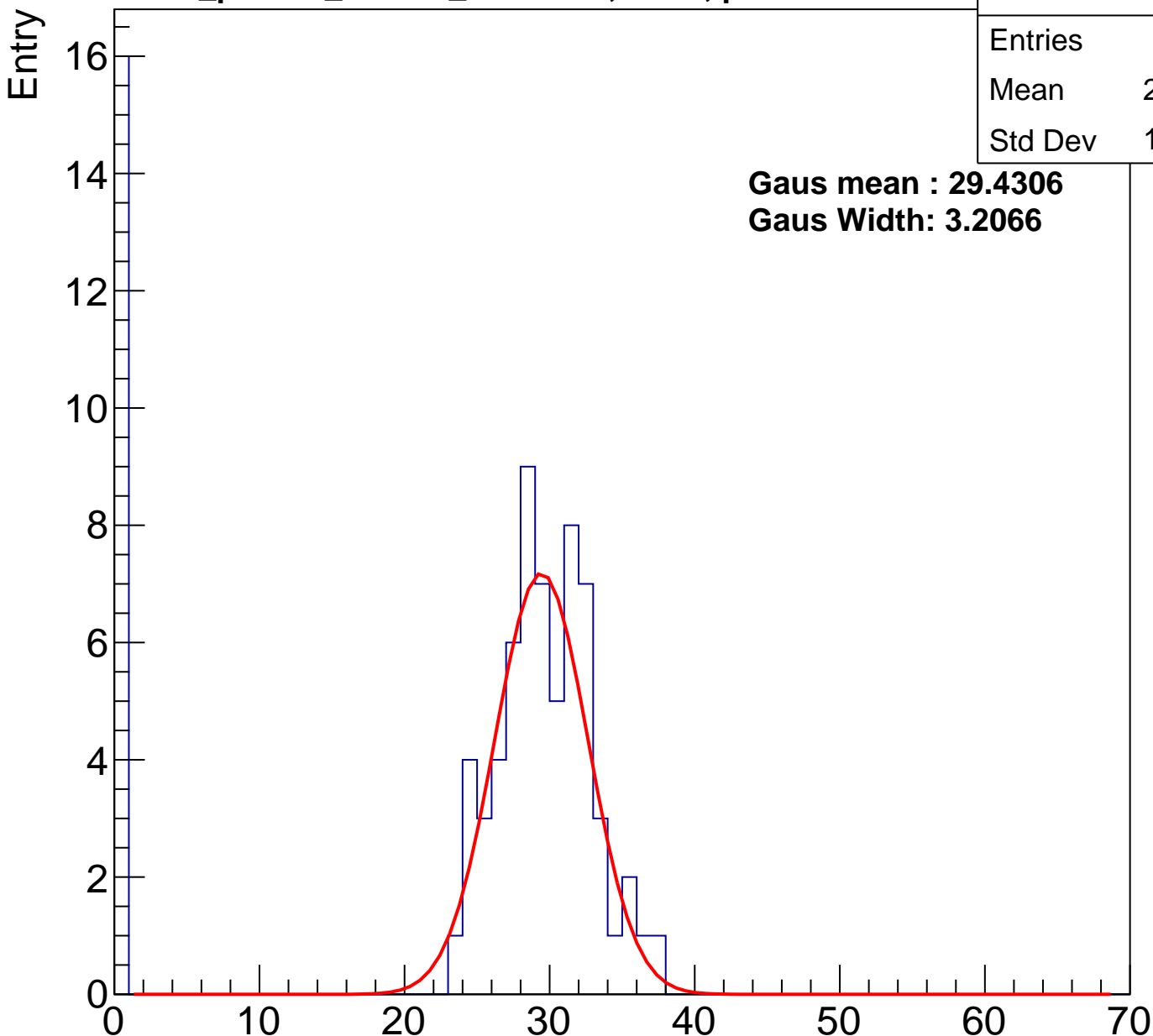
Gaus Width: 3.2066

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch99, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	30.69
Std Dev	13.37

Gaus mean : 36.8908

Gaus Width: 3.3633

Entry

12

10

8

6

4

2

0

0

10

20

30

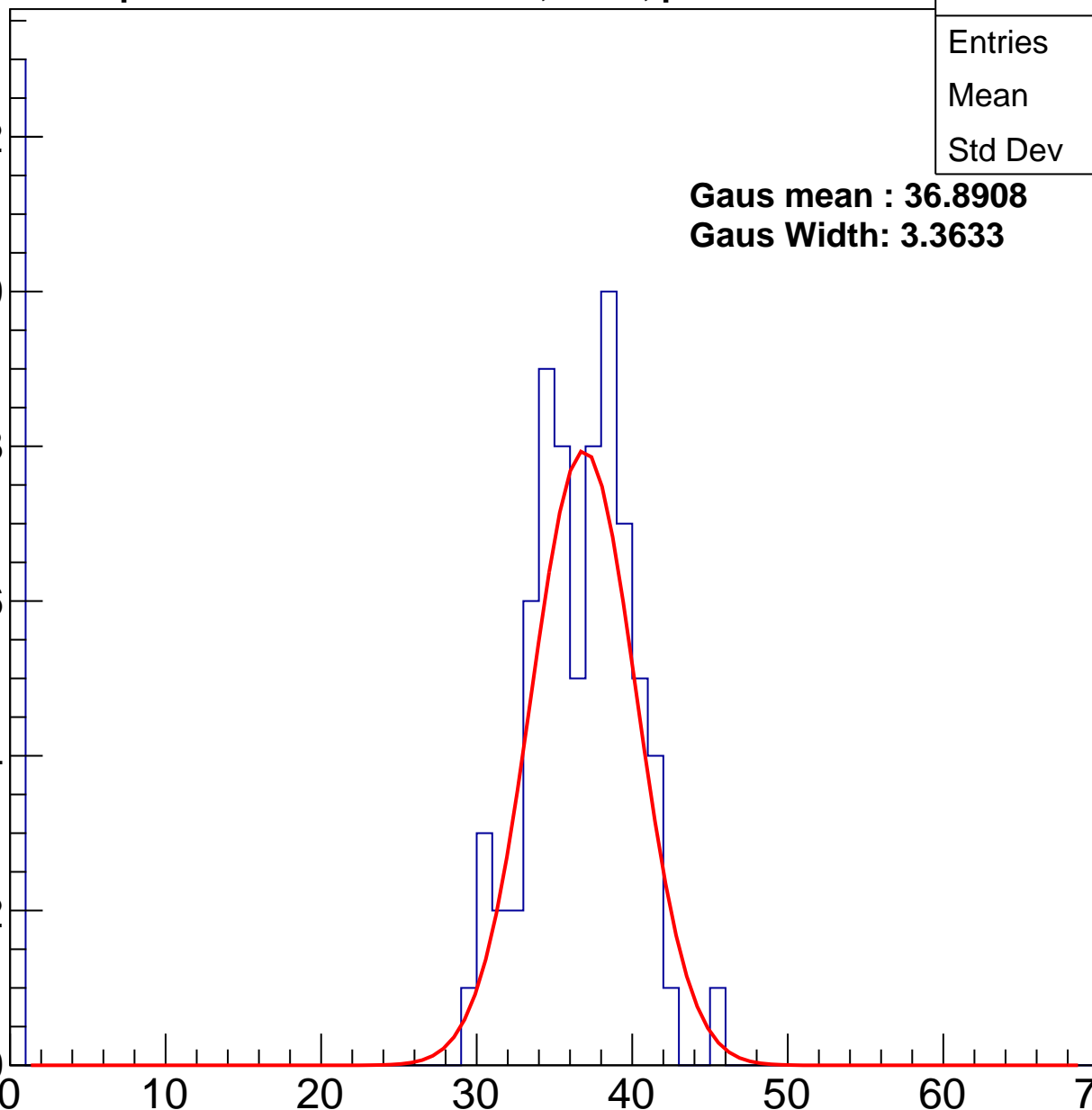
40

50

60

70

ampl



B1L103S, U8-ch99, adc2

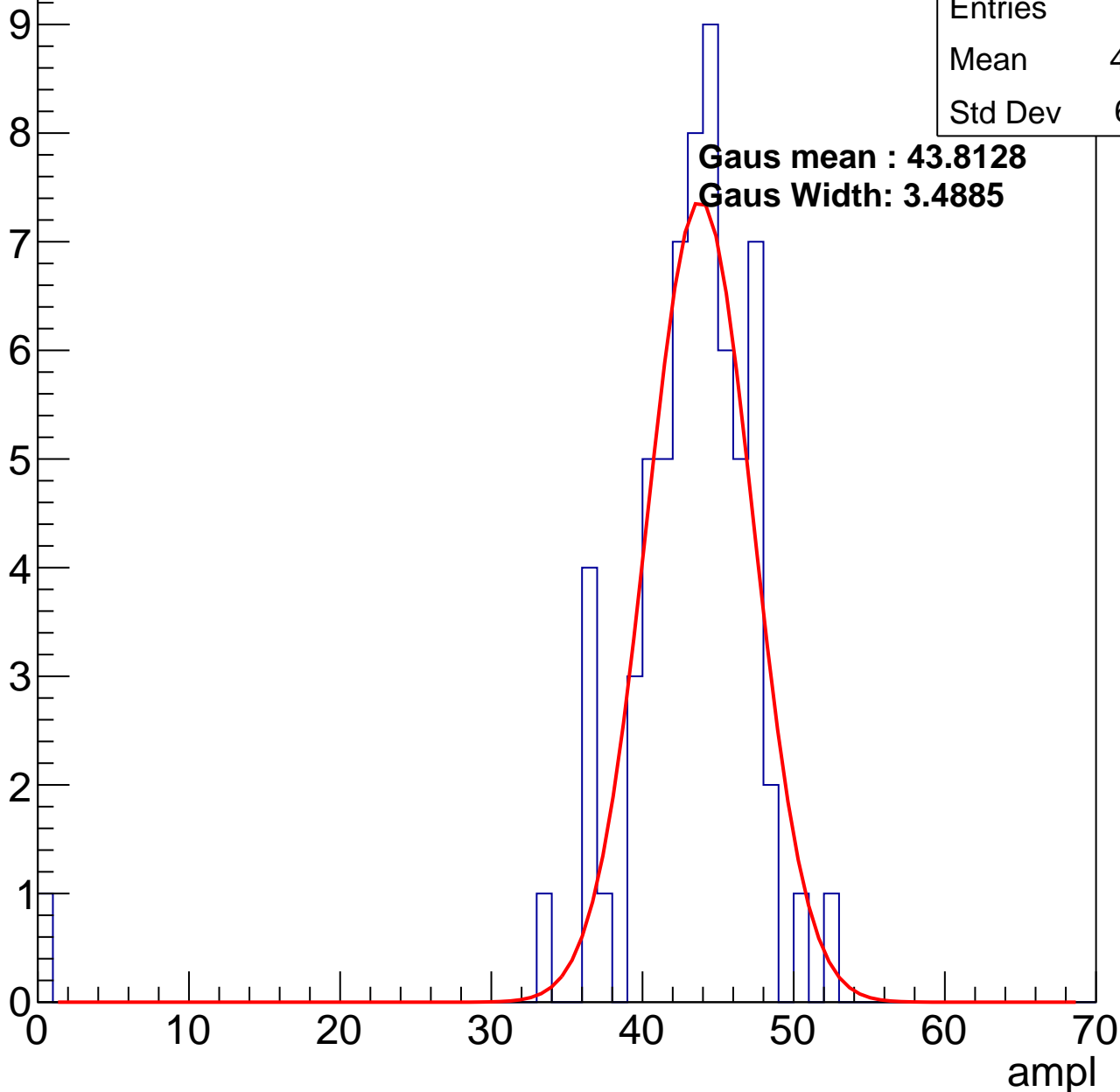
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.38
Std Dev	6.331

Gaus mean : 43.8128

Gaus Width: 3.4885

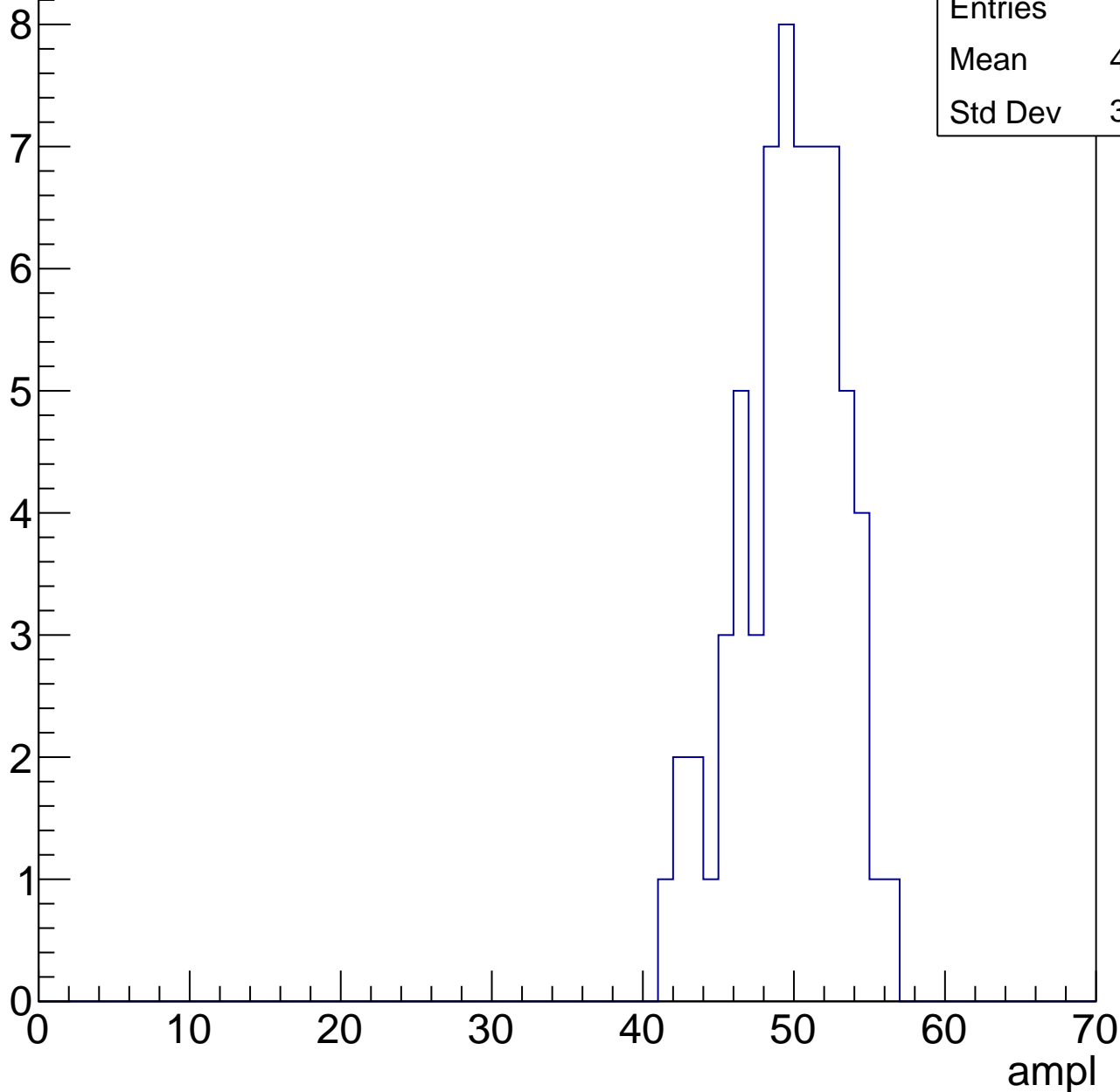


B1L103S, U8-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.25
Std Dev	3.377

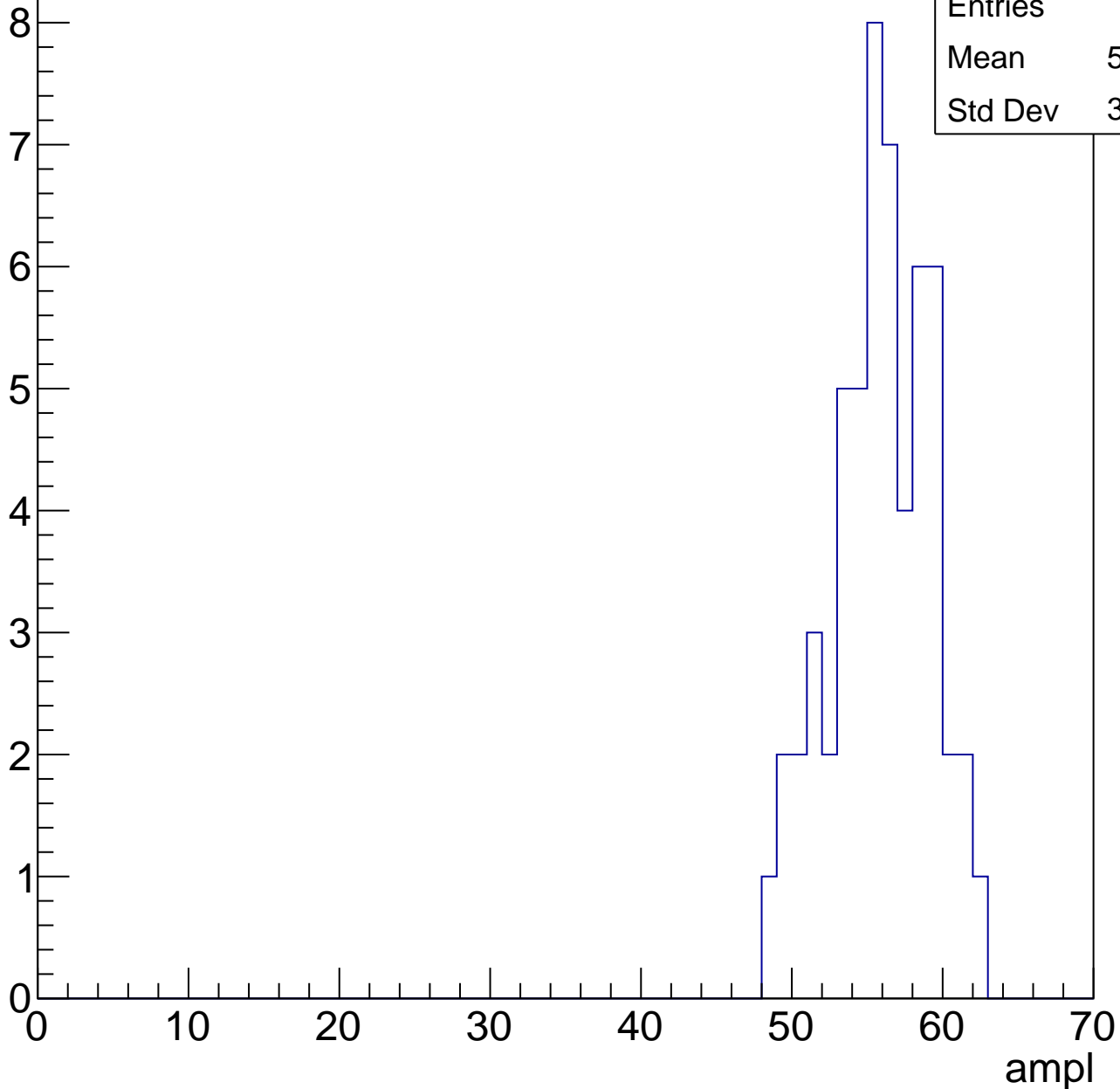


B1L103S, U8-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	55.43
Std Dev	3.262

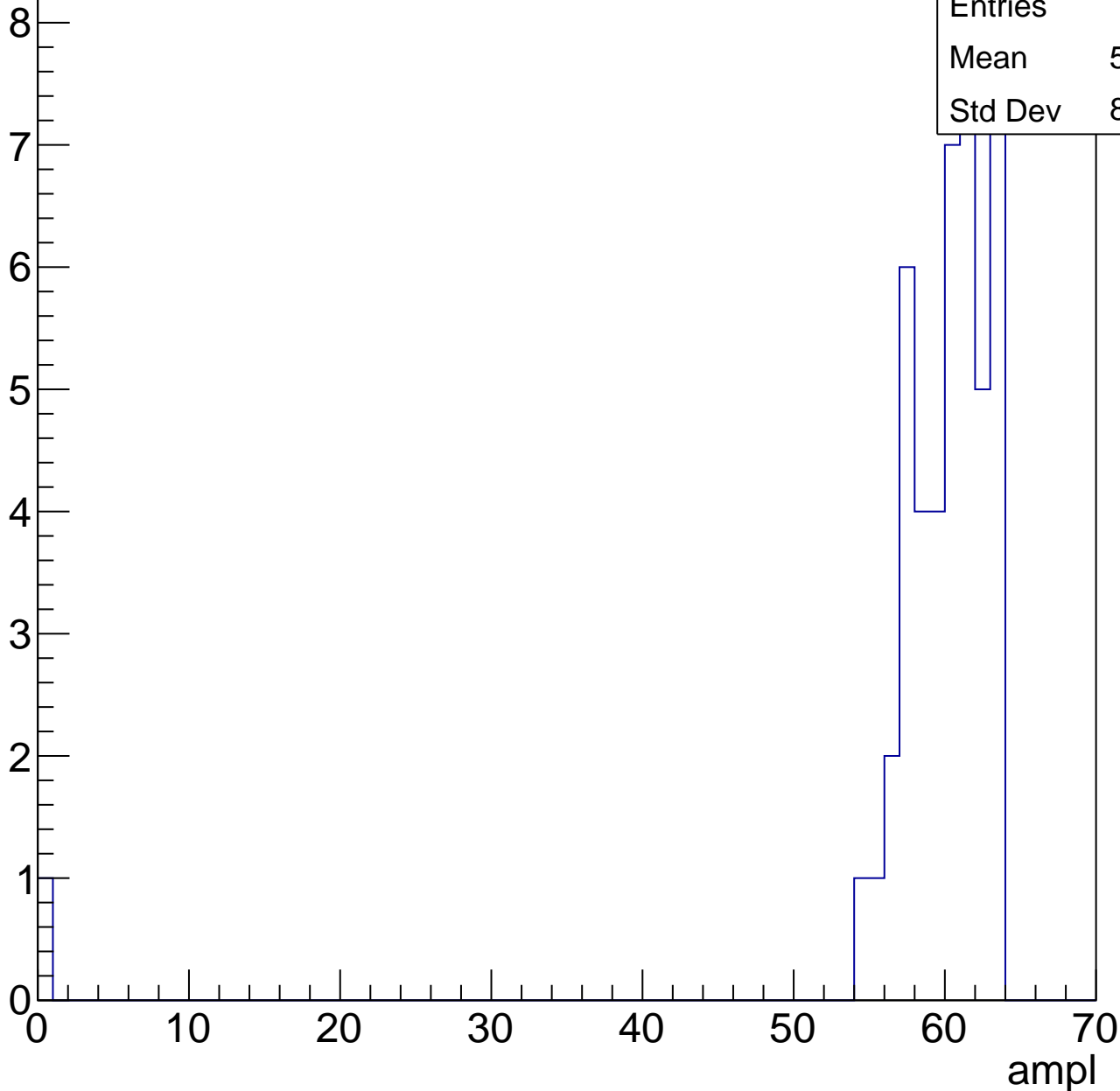


B1L103S, U8-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

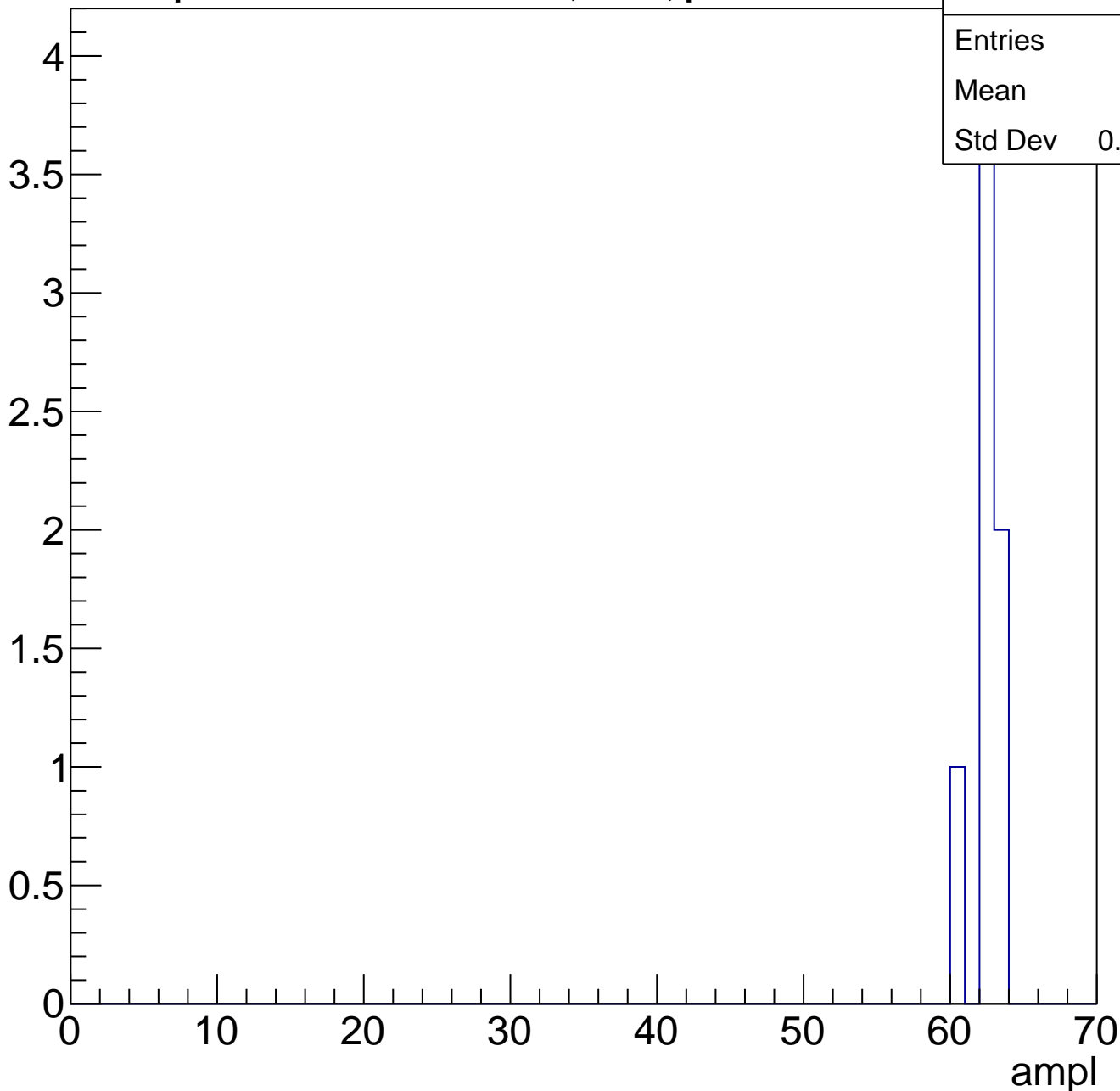
Entries	47
Mean	58.57
Std Dev	8.958



B1L103S, U8-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

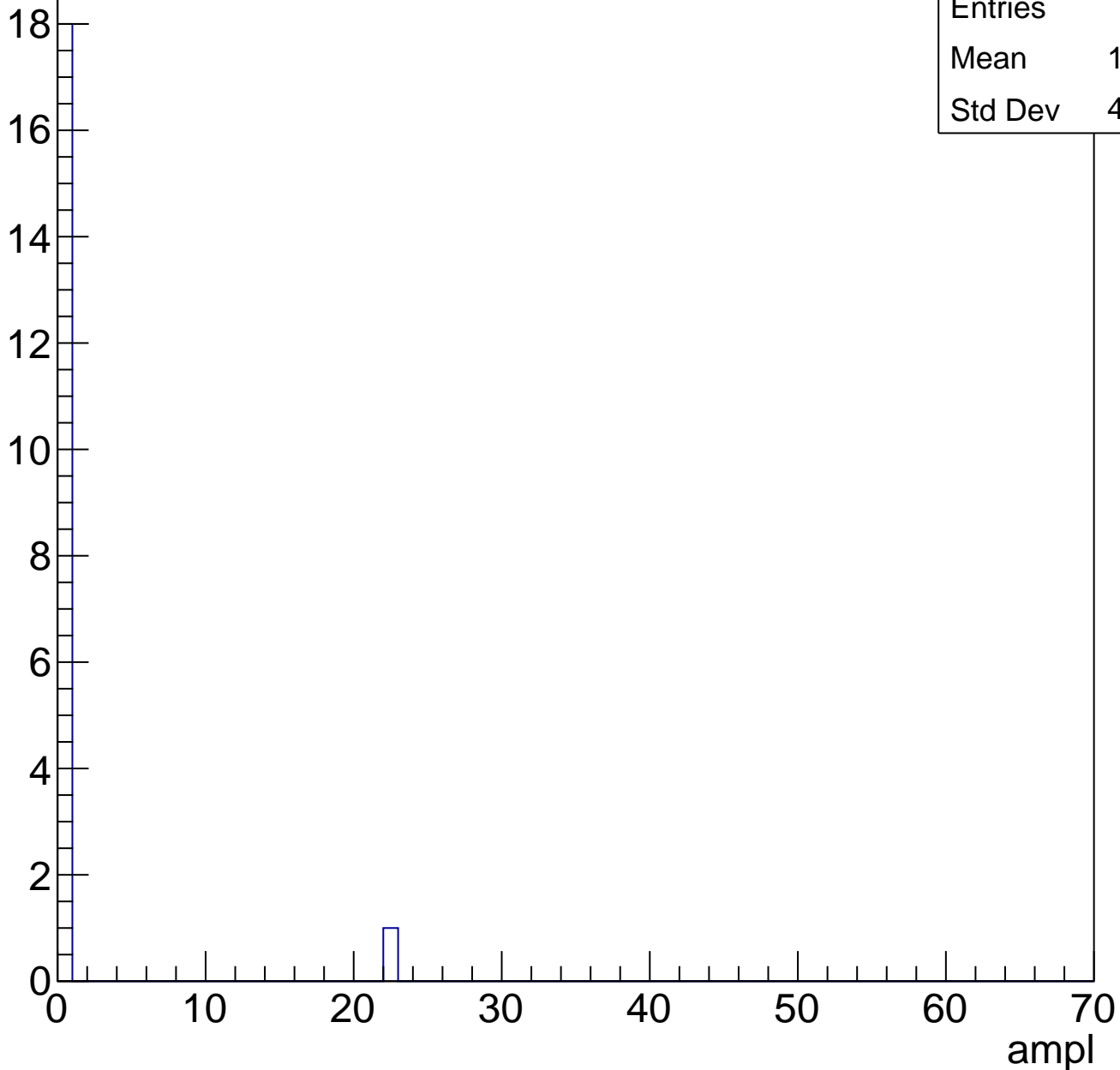


B1L103S, U8-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U8-ch100, adc0

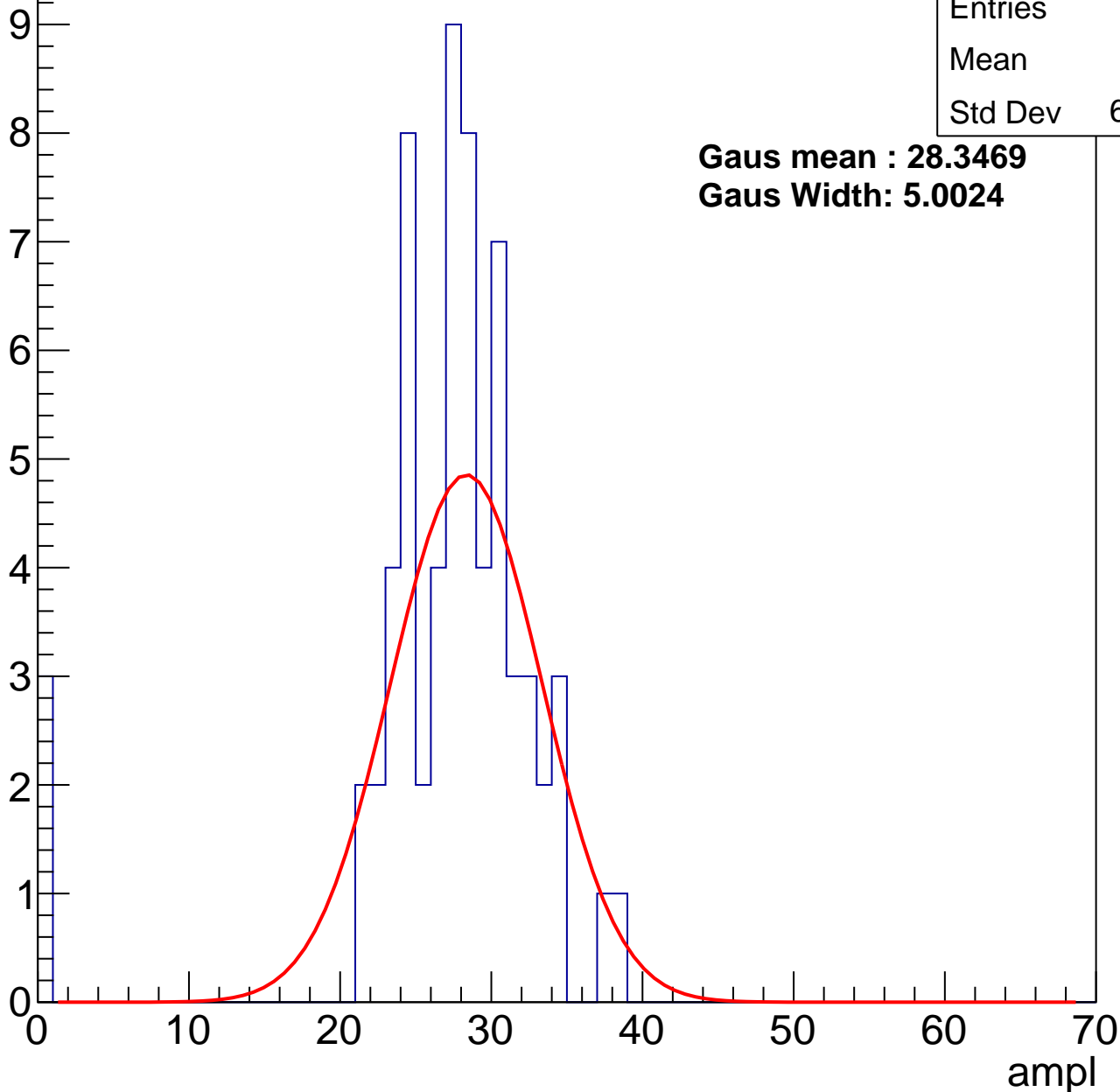
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	26.5
Std Dev	6.843

Gaus mean : 28.3469

Gaus Width: 5.0024



B1L103S, U8-ch100, adc1

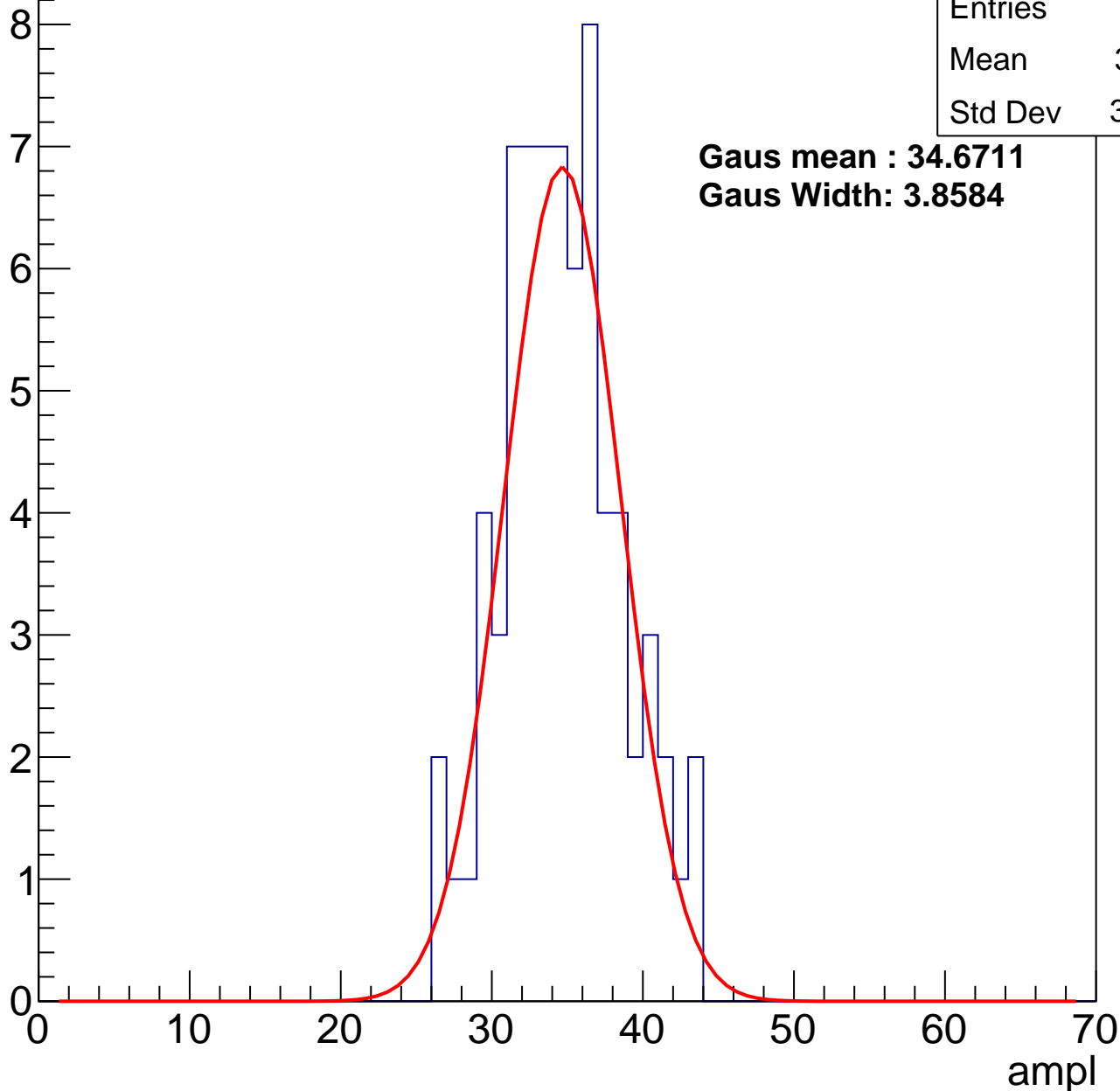
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.21
Std Dev	3.893

Gaus mean : 34.6711

Gaus Width: 3.8584



B1L103S, U8-ch100, adc2

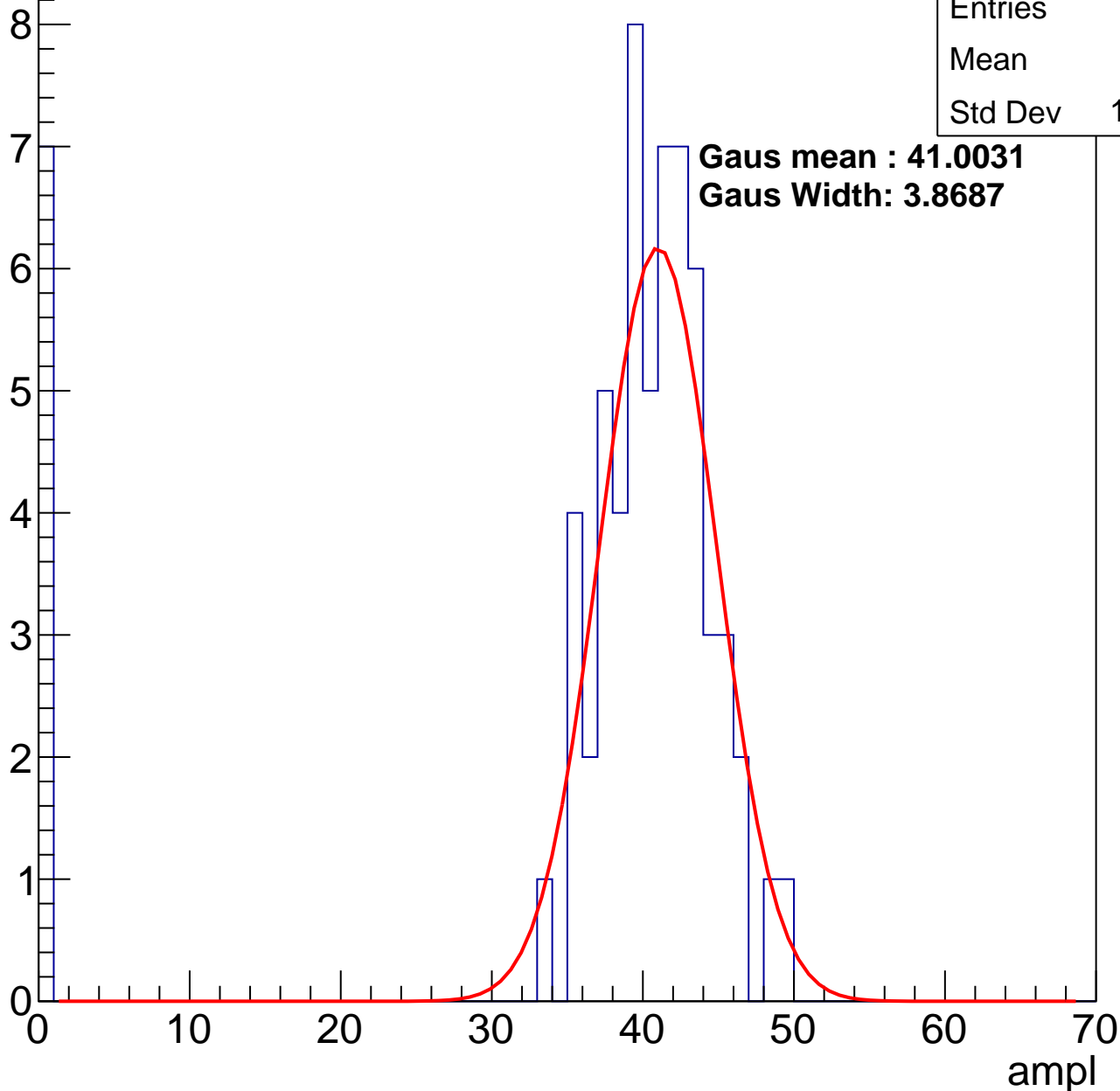
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.2
Std Dev	12.87

Gaus mean : 41.0031

Gaus Width: 3.8687

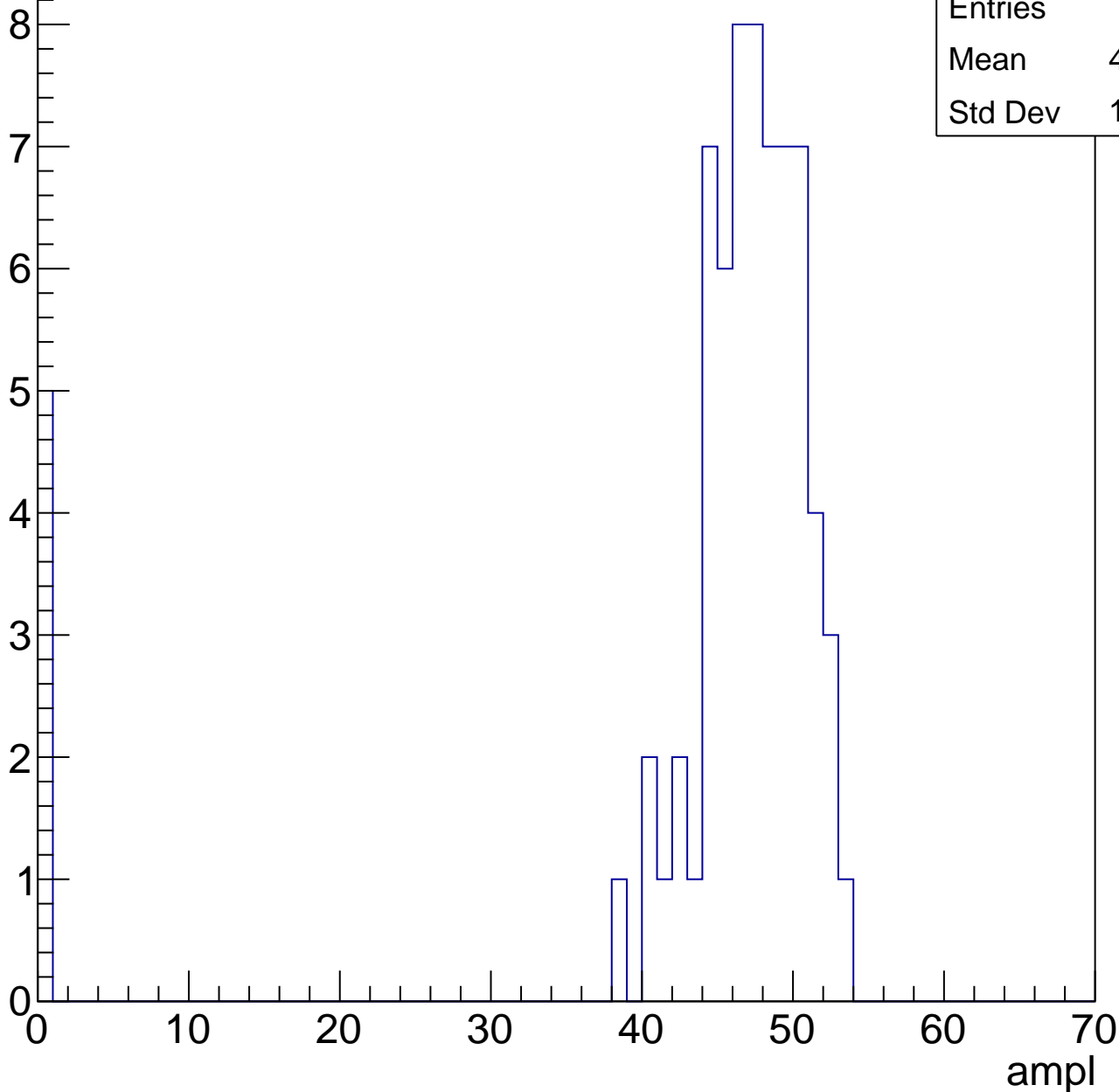


B1L103S, U8-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

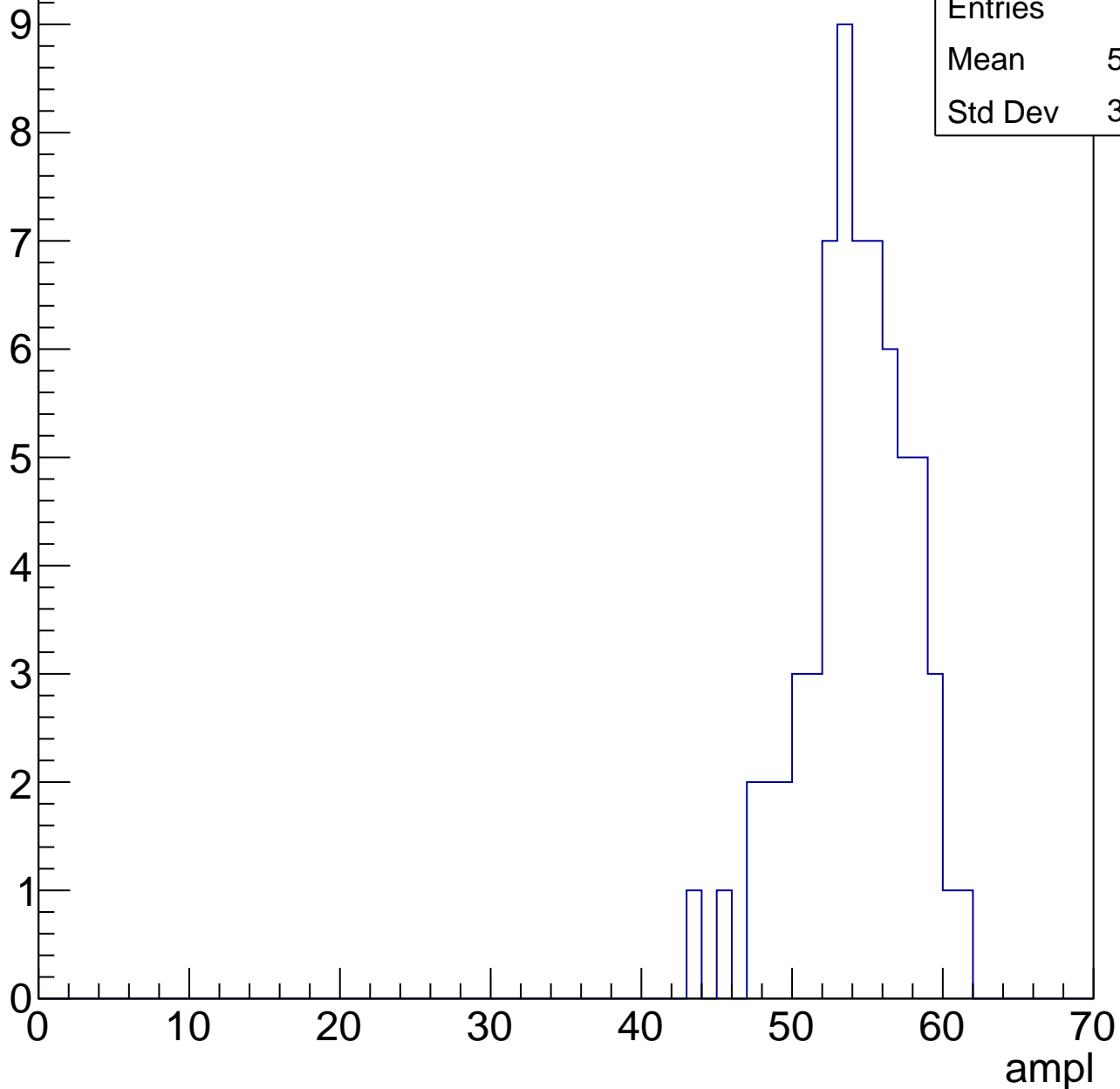
Entries	70
Mean	43.57
Std Dev	12.46



B1L103S, U8-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

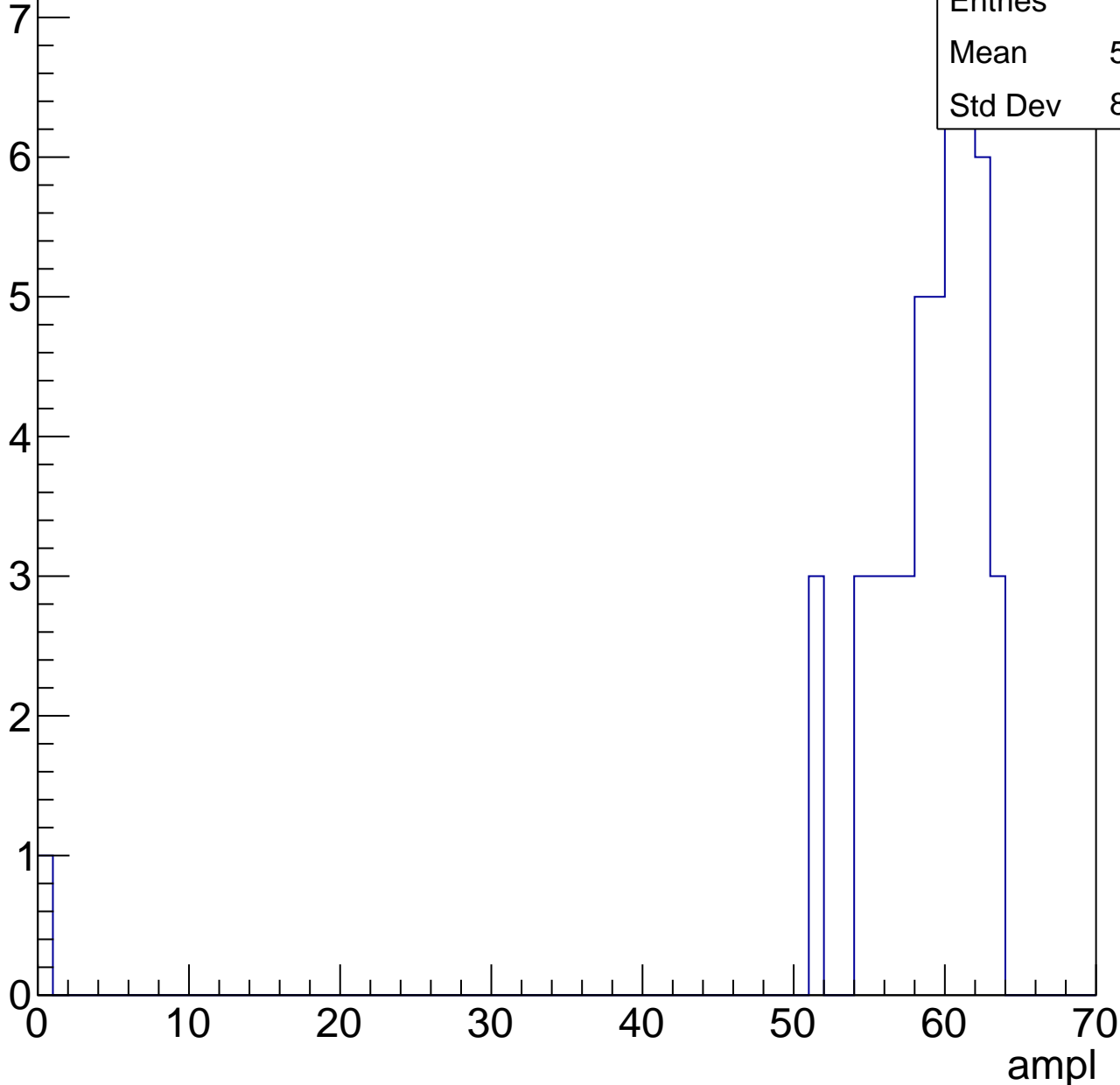


B1L103S, U8-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.39
Std Dev	8.864

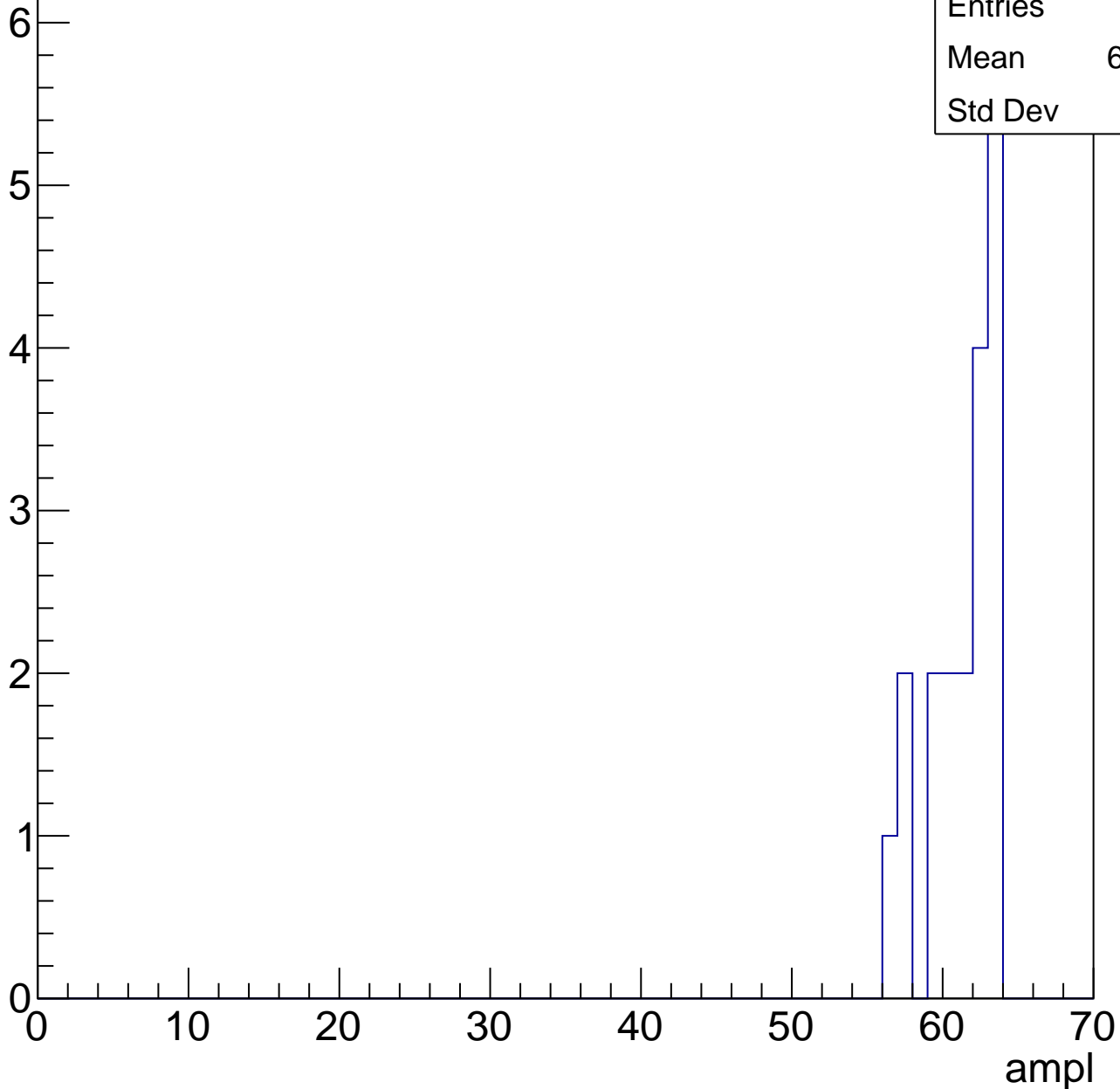


B1L103S, U8-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	60.84
Std Dev	2.23



B1L103S, U8-ch100, adc7

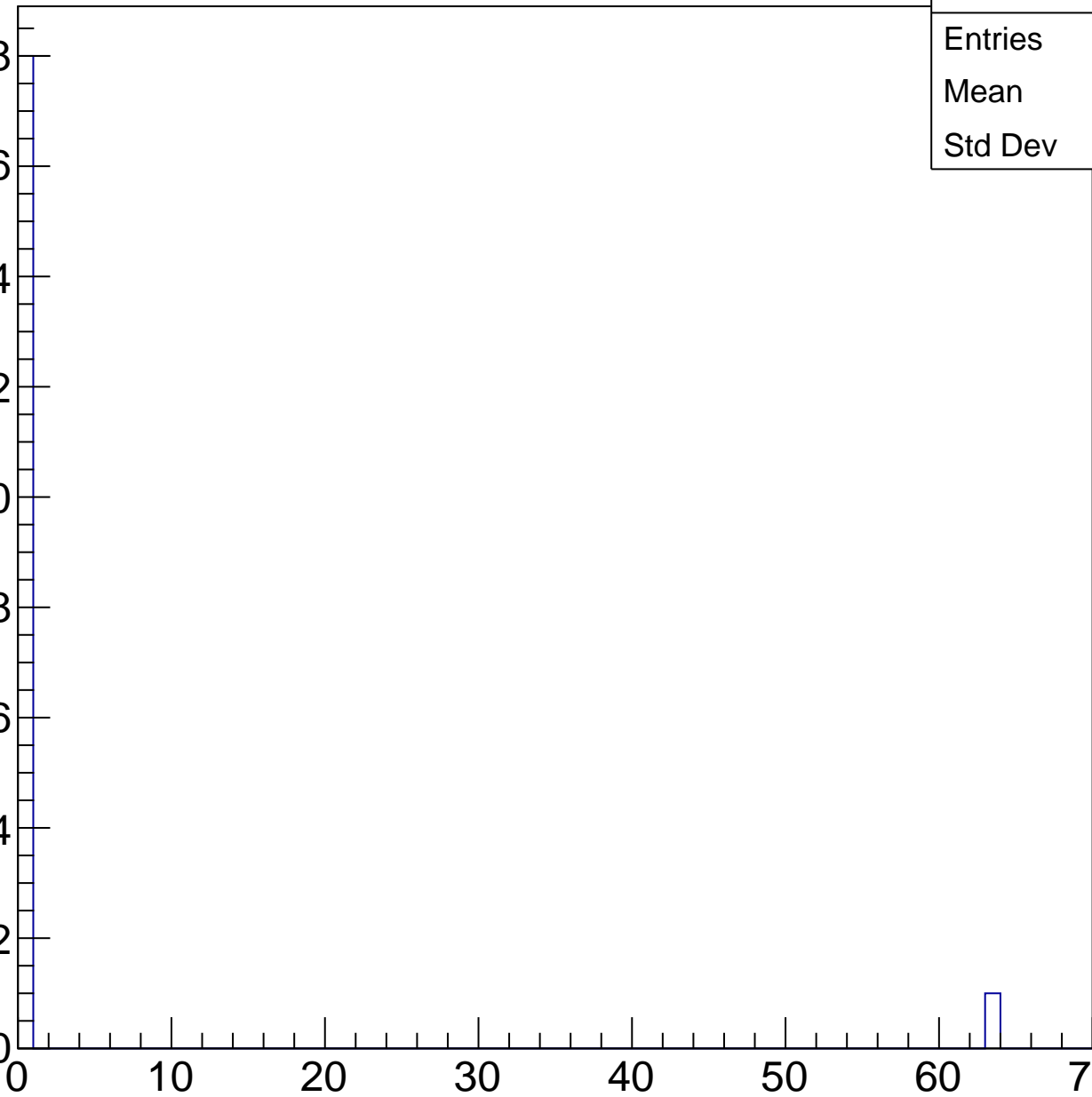
calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	3.316
Std Dev	14.07

ampl



B1L103S, U8-ch101, adc0

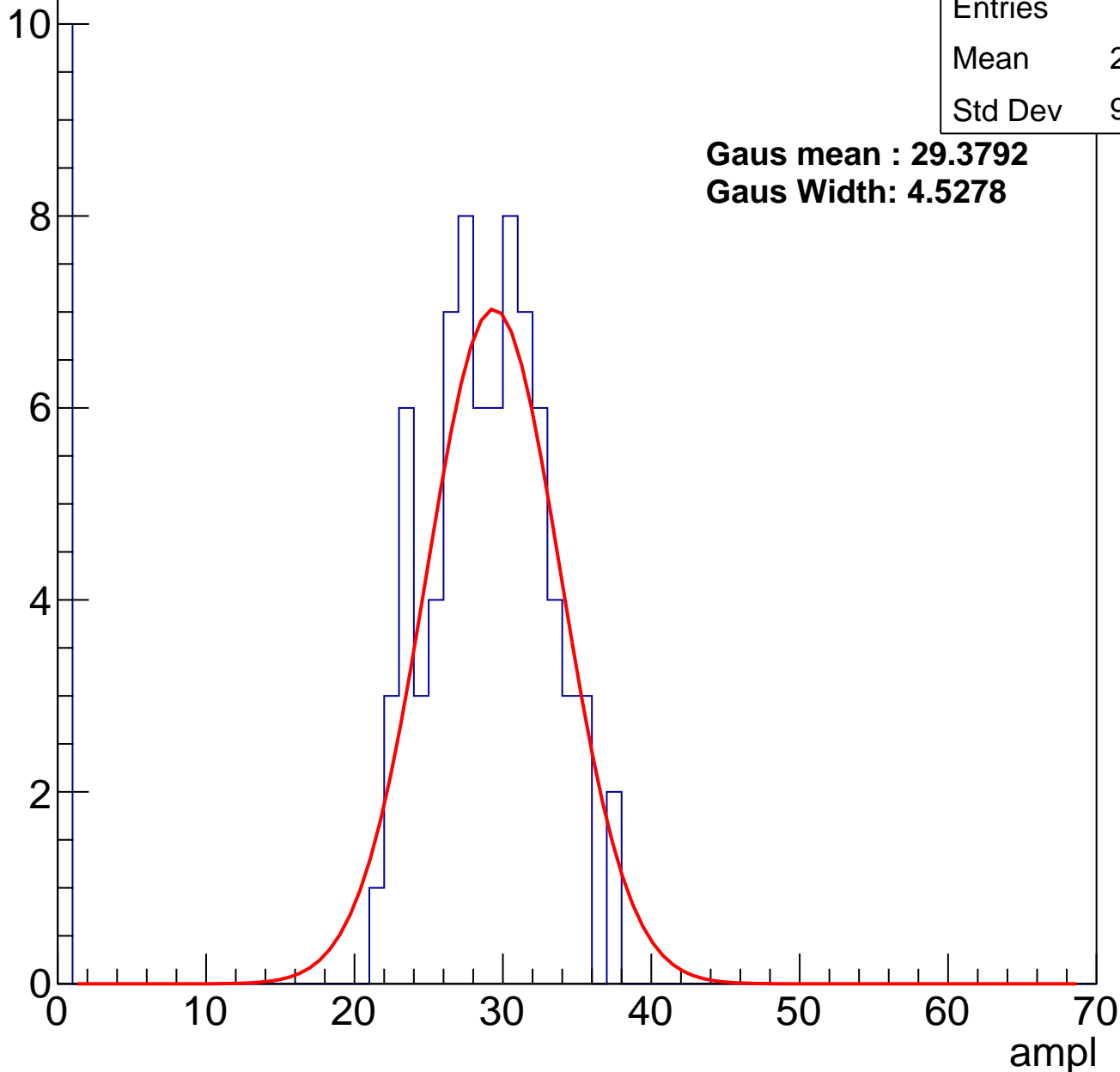
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	25.28
Std Dev	9.788

Gaus mean : 29.3792

Gaus Width: 4.5278

Entry



B1L103S, U8-ch101, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	28.94
Std Dev	13.6

Gaus mean : 35.7168

Gaus Width: 5.1079

Entry

10

8

6

4

2

0

0

10

20

30

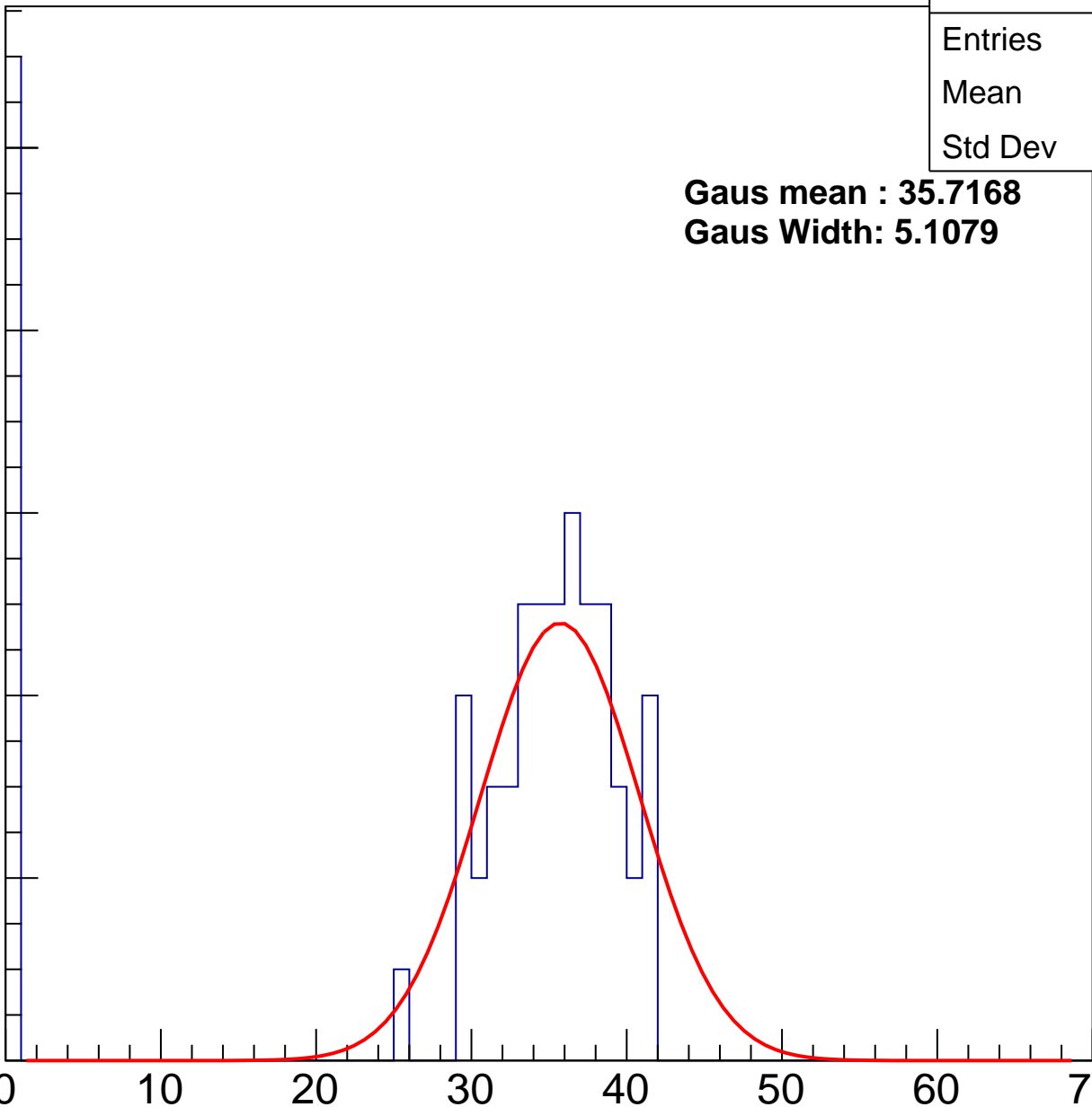
40

50

60

70

ampl



B1L103S, U8-ch101, adc2

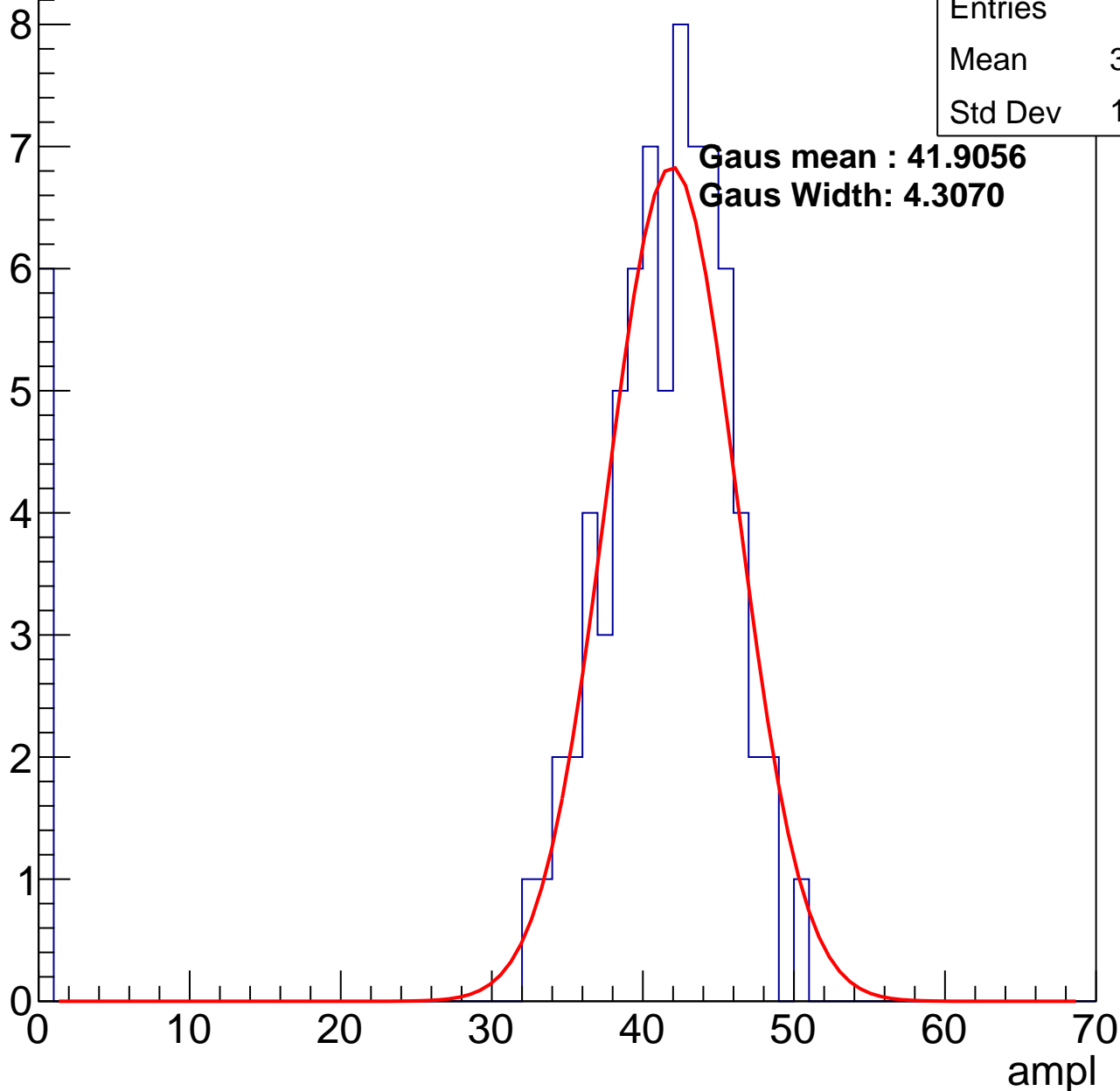
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	38.05
Std Dev	11.52

Gaus mean : 41.9056

Gaus Width: 4.3070

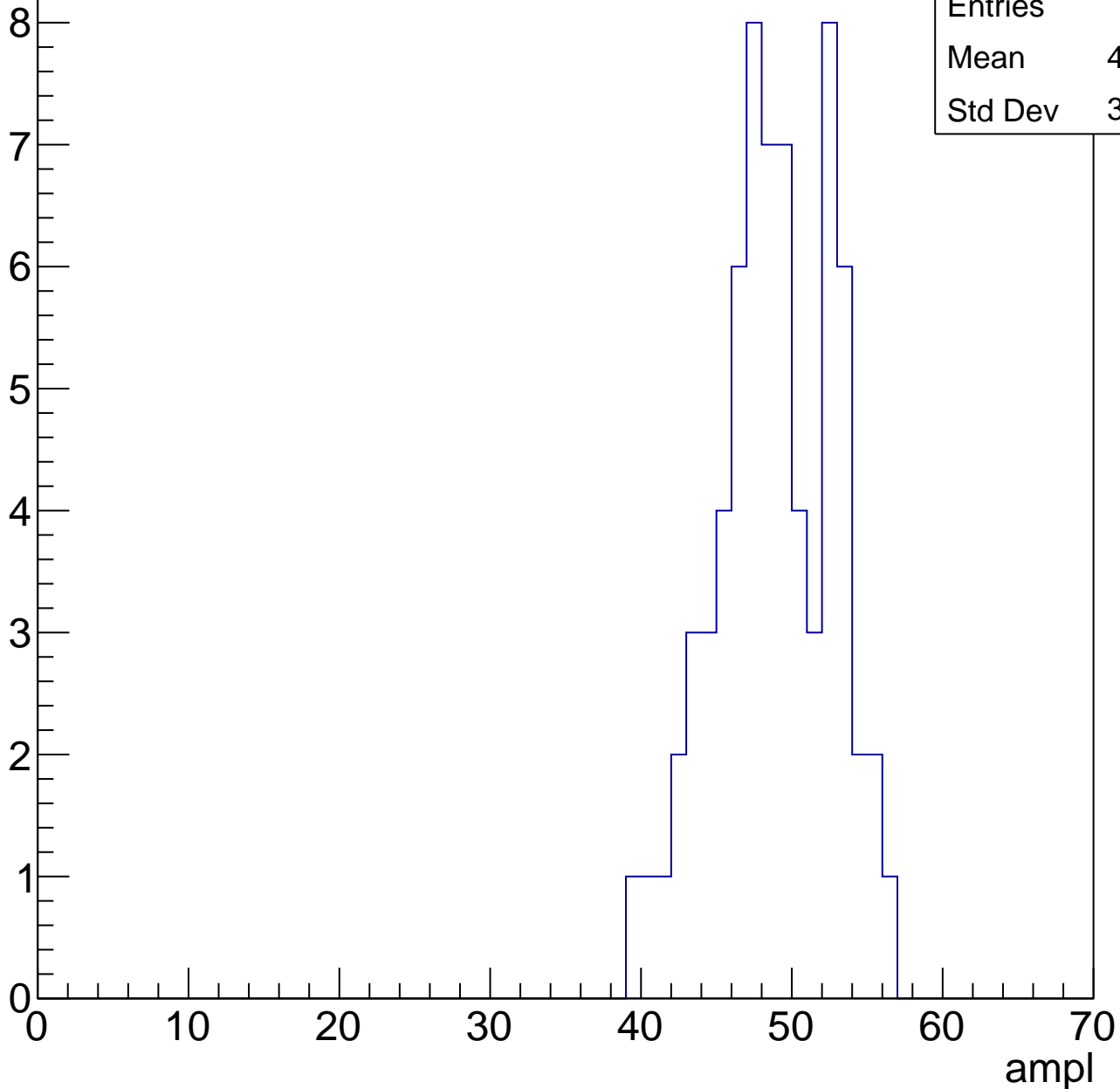


B1L103S, U8-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	48.36
Std Dev	3.826

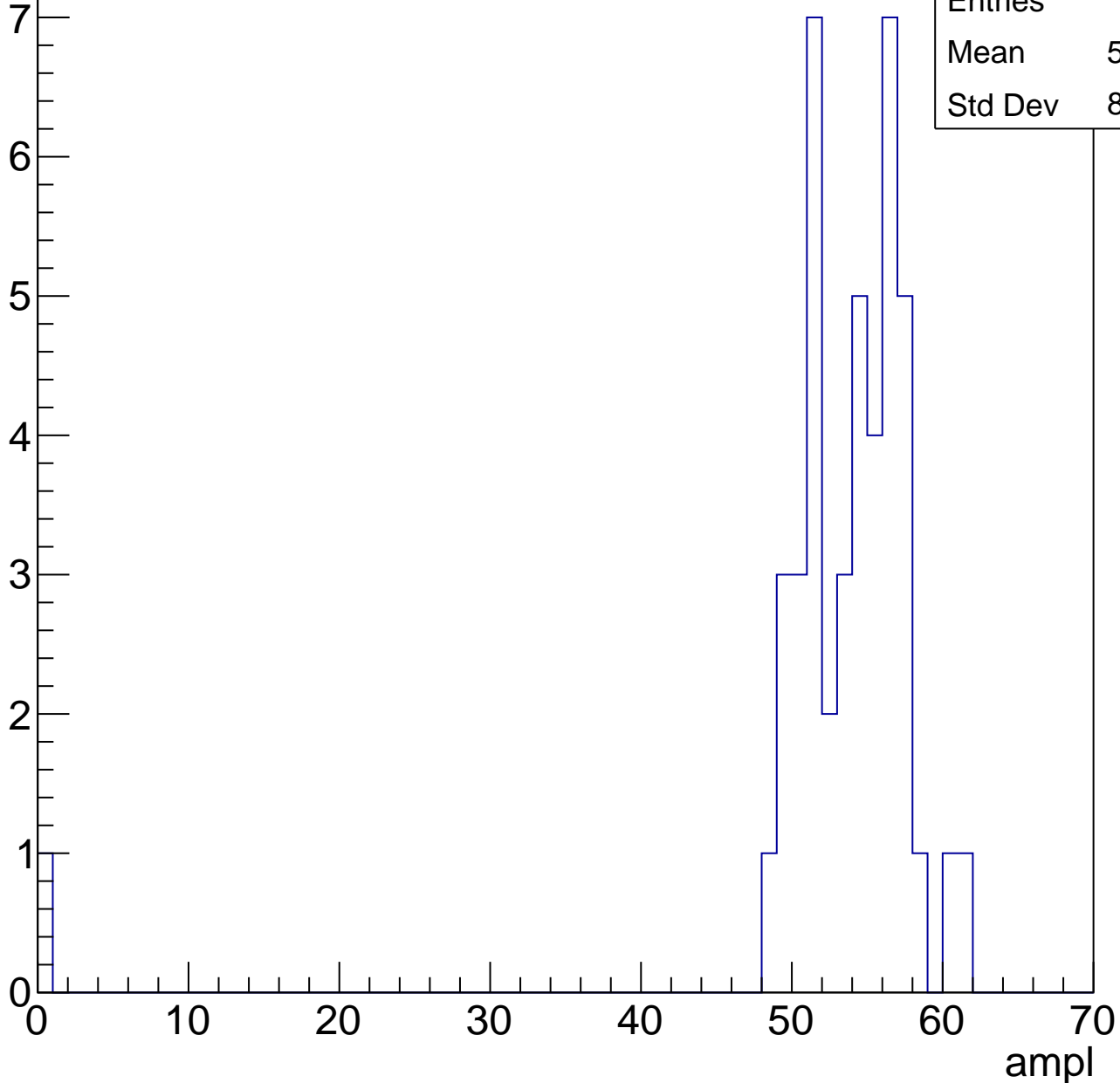


B1L103S, U8-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	52.52
Std Dev	8.569

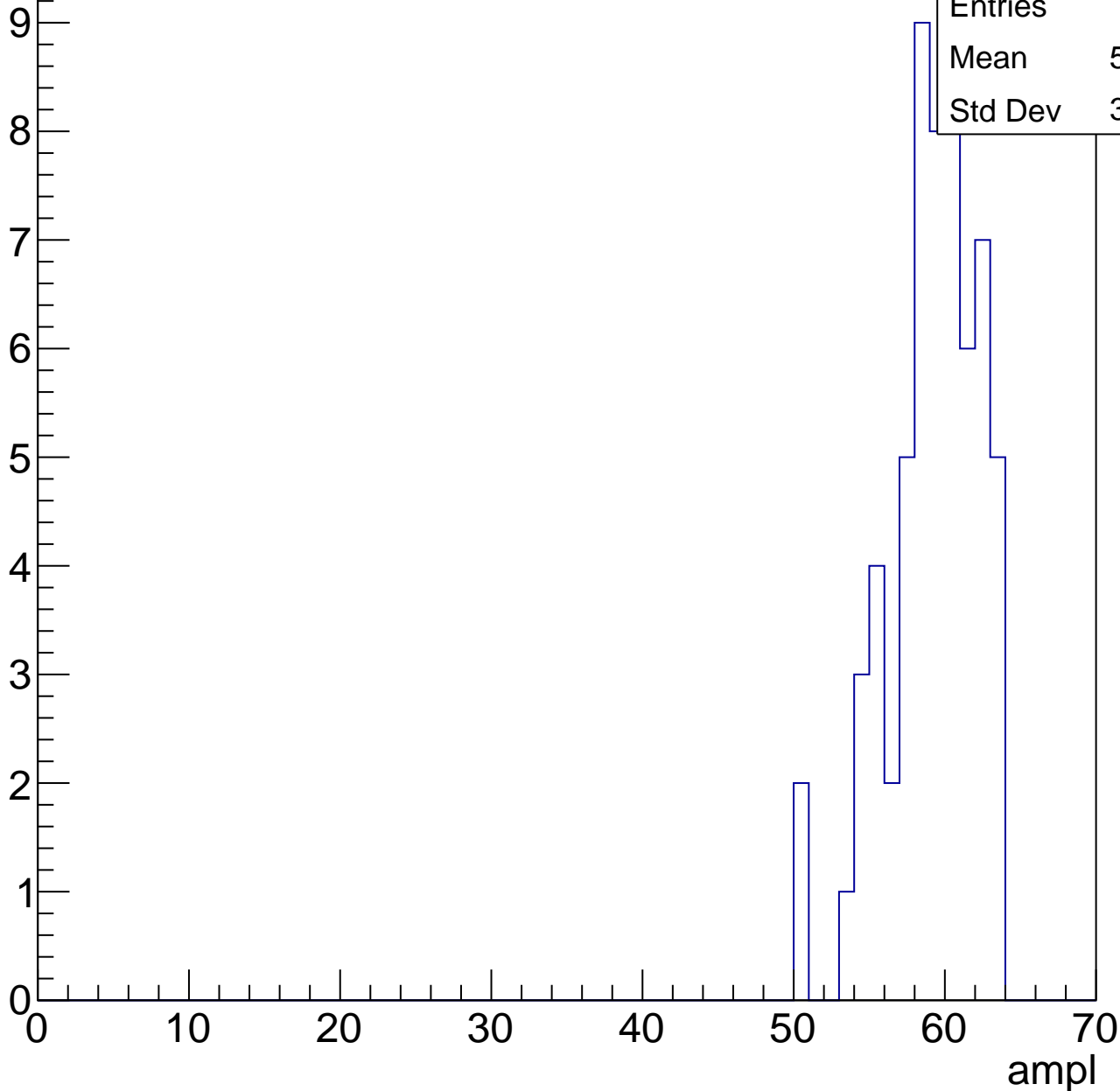


B1L103S, U8-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

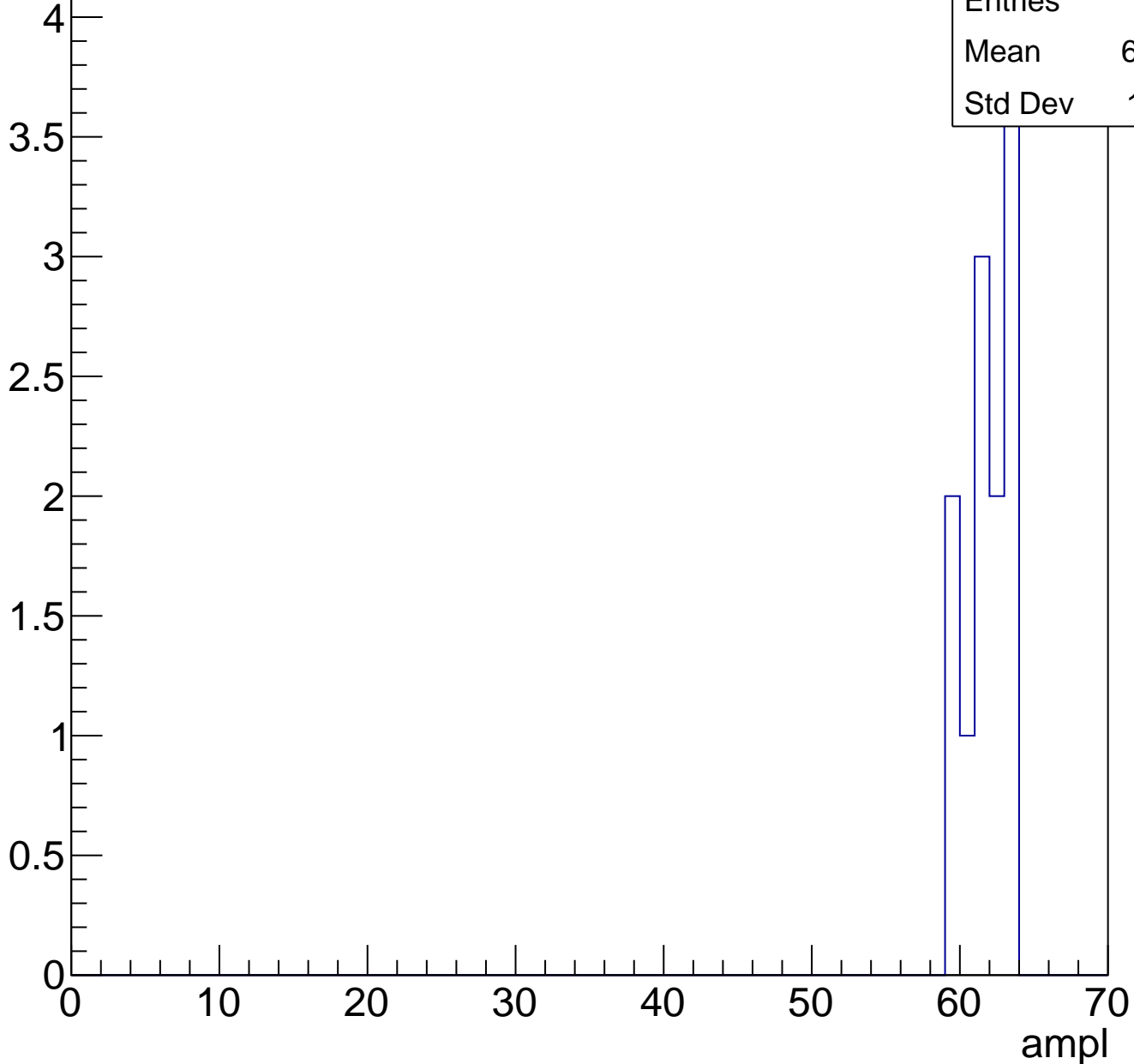
Entries	60
Mean	58.68
Std Dev	3.036



B1L103S, U8-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

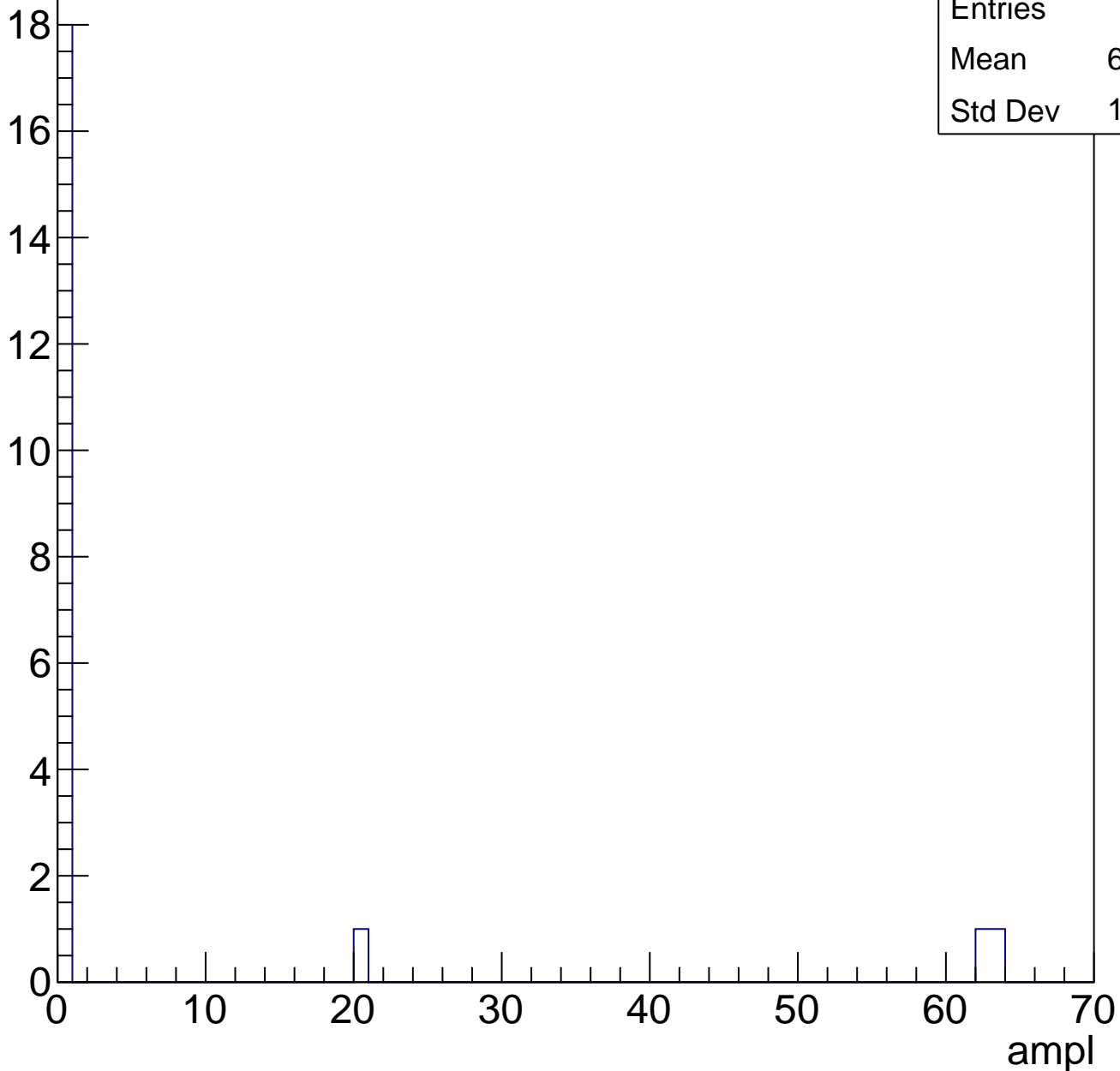


B1L103S, U8-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.905
Std Dev	18.53

Entry



B1L103S, U8-ch102, adc0

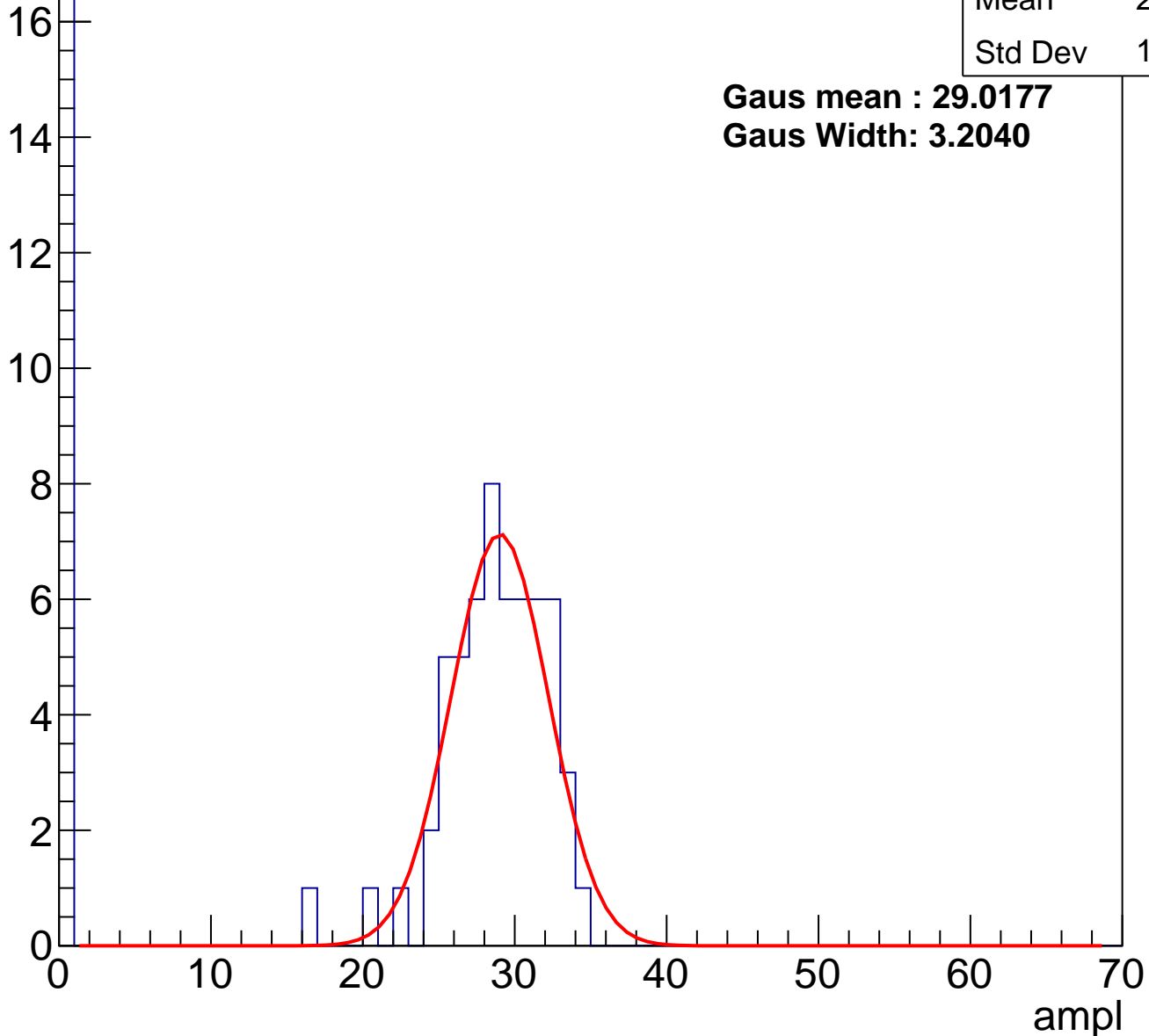
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	21.78
Std Dev	12.25

Gaus mean : 29.0177

Gaus Width: 3.2040

Entry



B1L103S, U8-ch102, adc1

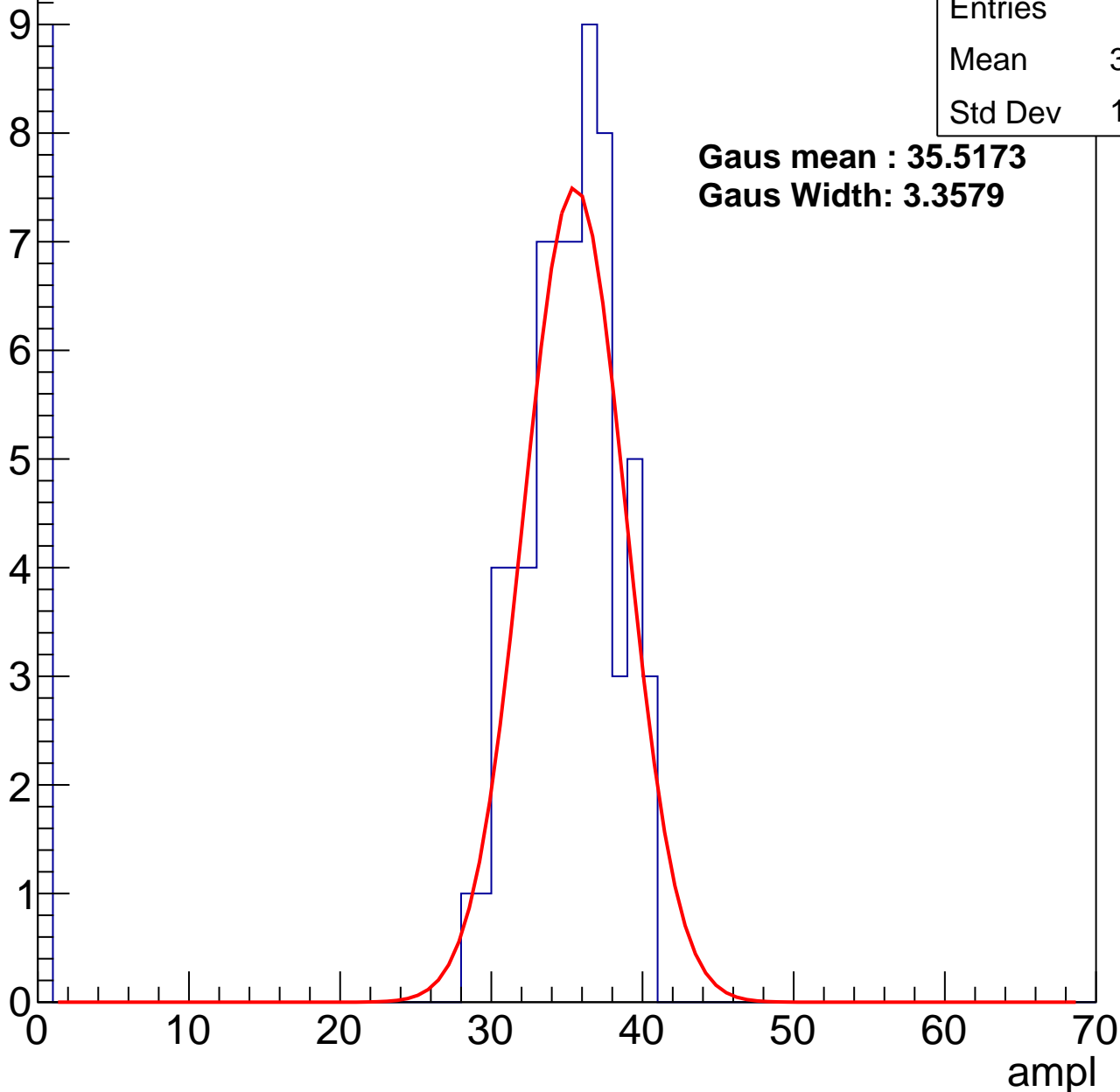
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	30.44
Std Dev	11.83

Gaus mean : 35.5173

Gaus Width: 3.3579



B1L103S, U8-ch102, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	39.5
Std Dev	7.736

Gaus mean : 41.6765

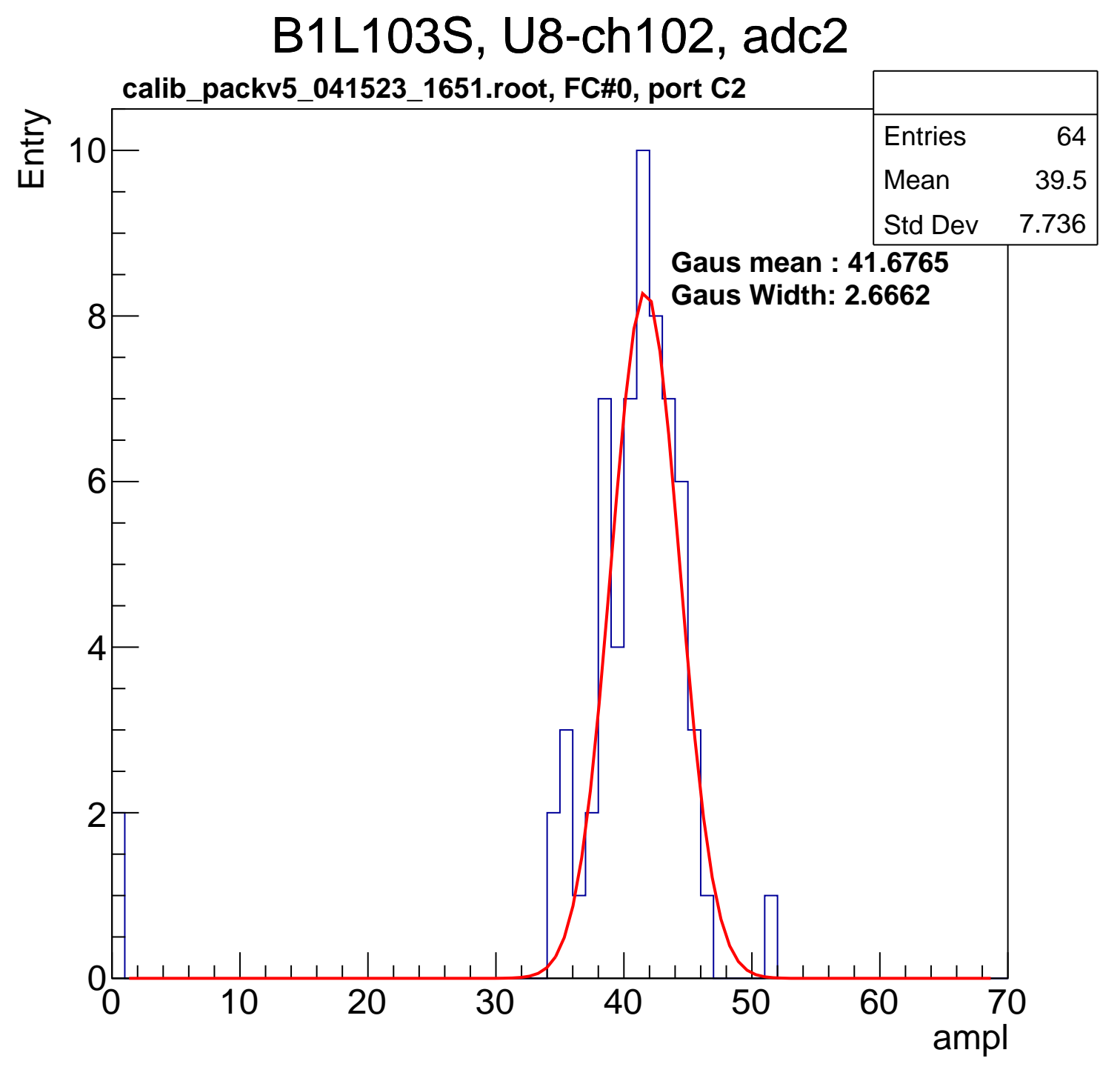
Gaus Width: 2.6662

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

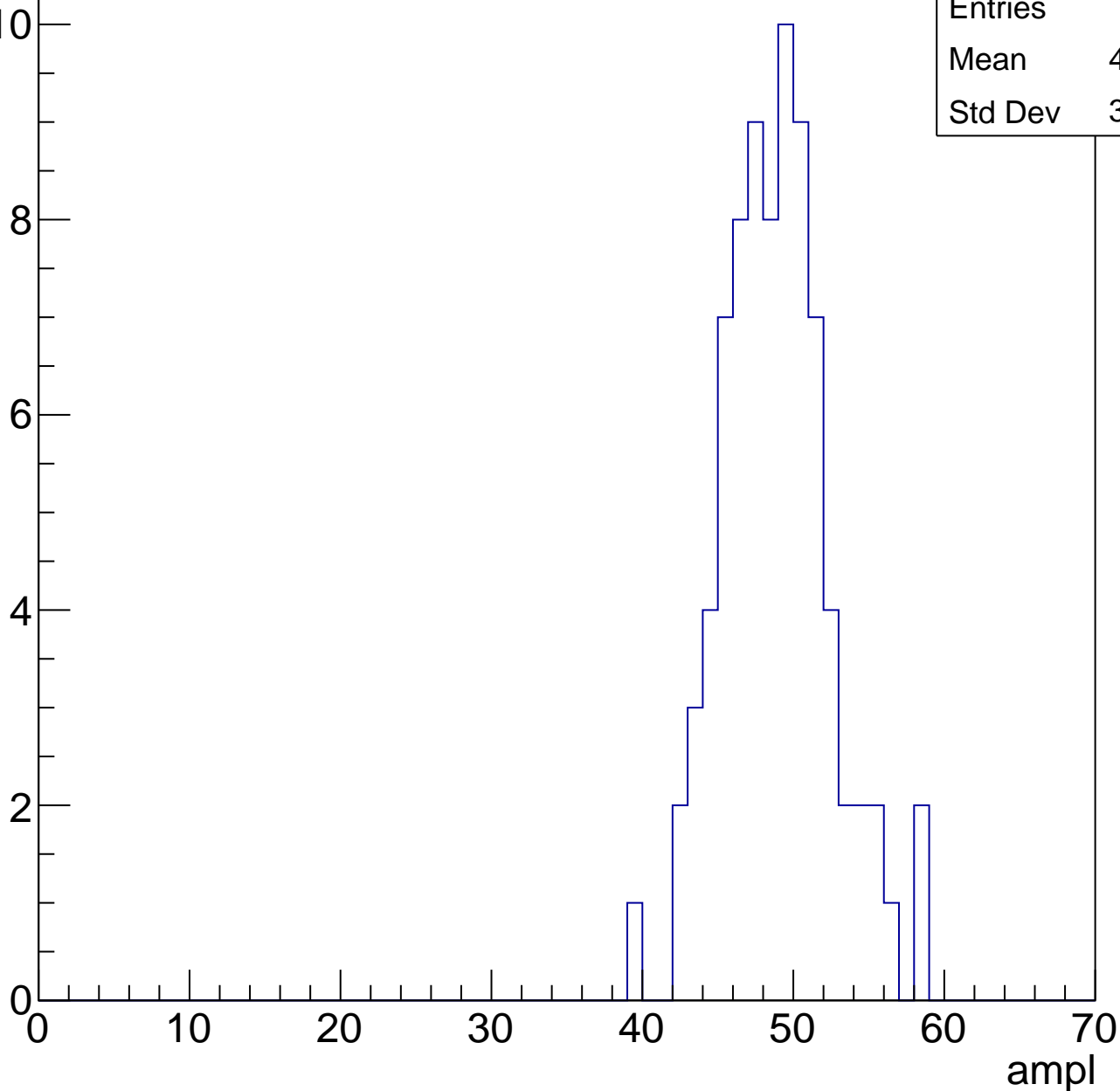


B1L103S, U8-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	48.38
Std Dev	3.585

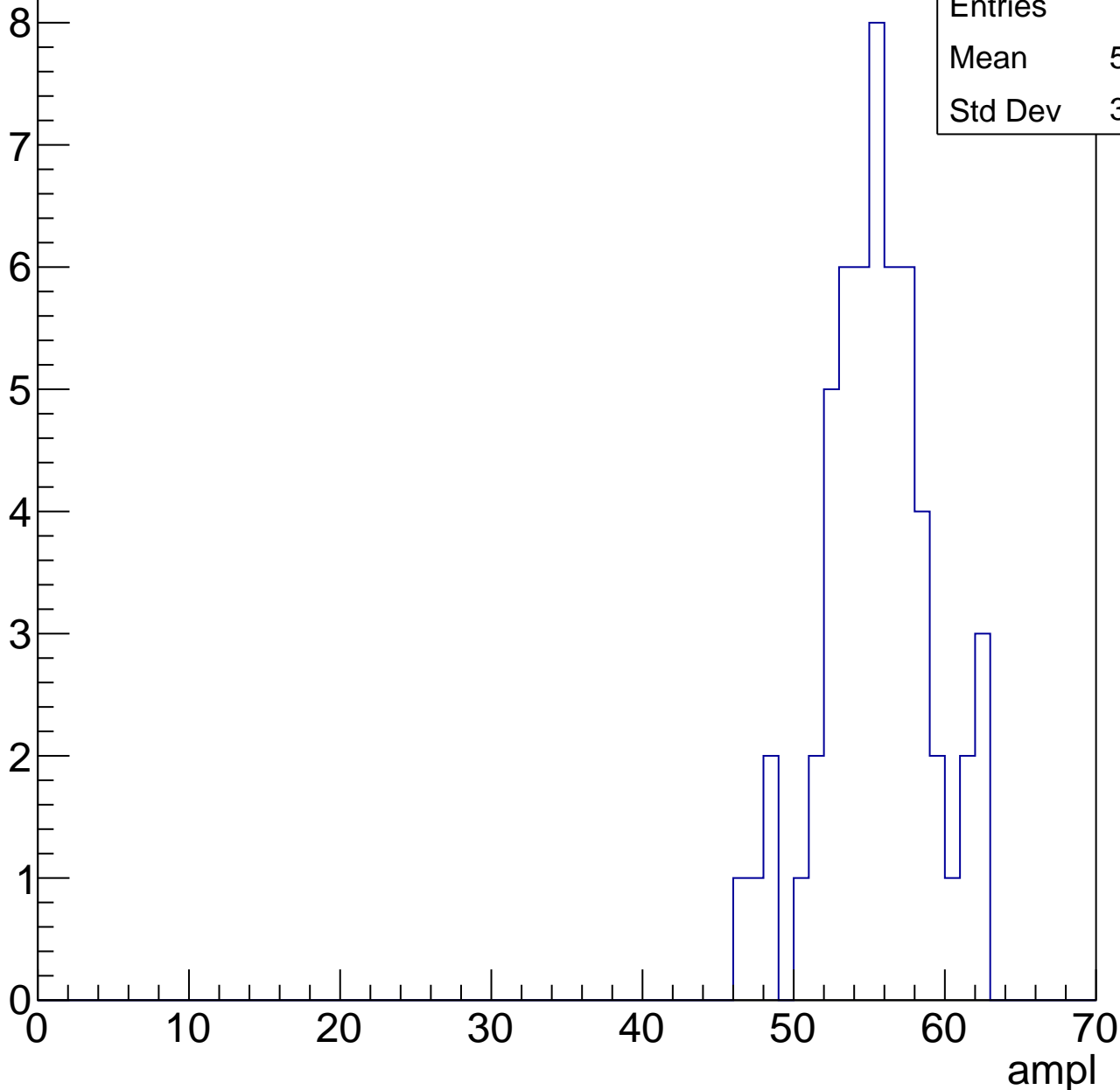


B1L103S, U8-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

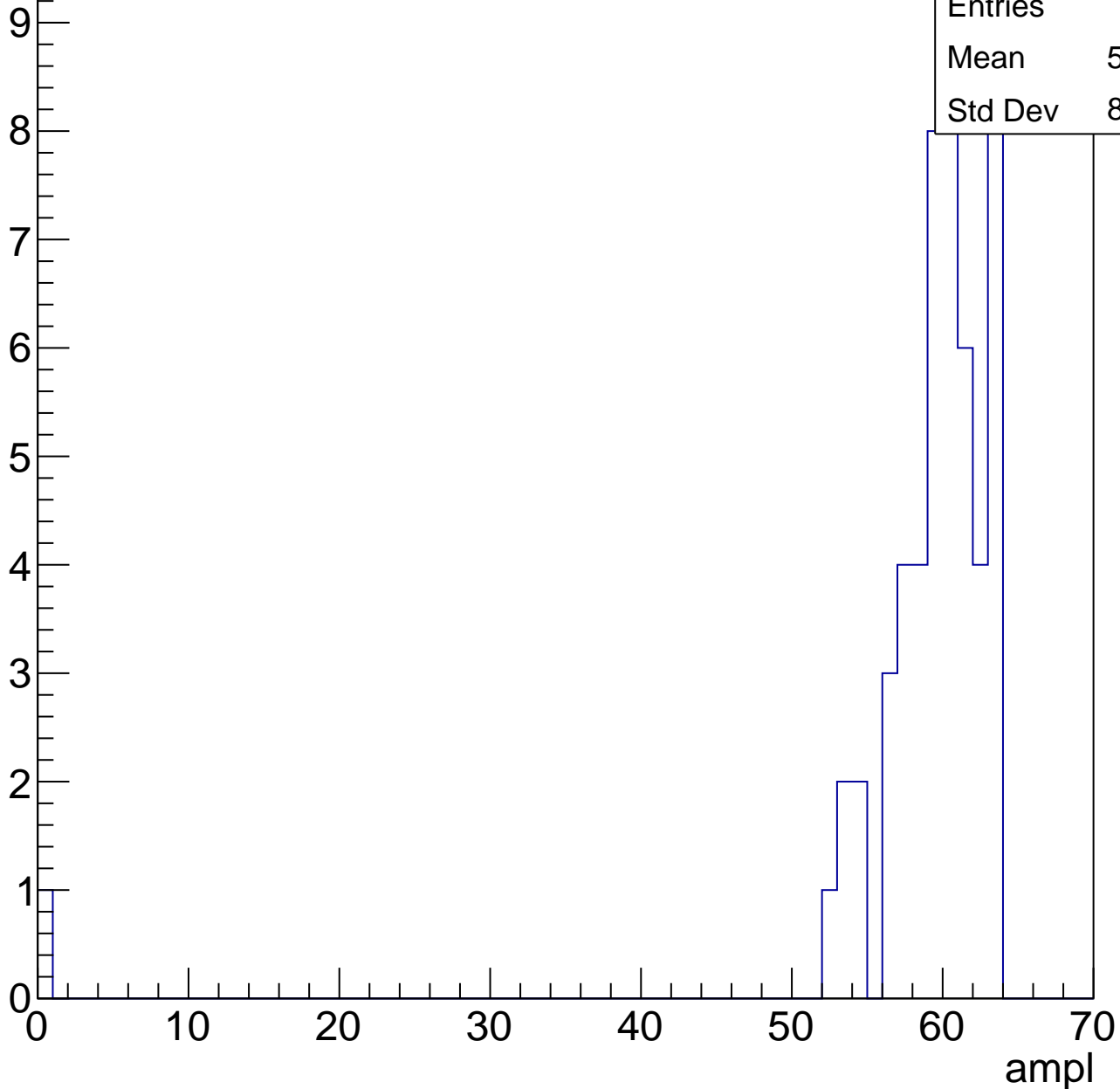
Entries	56
Mean	54.98
Std Dev	3.578



B1L103S, U8-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

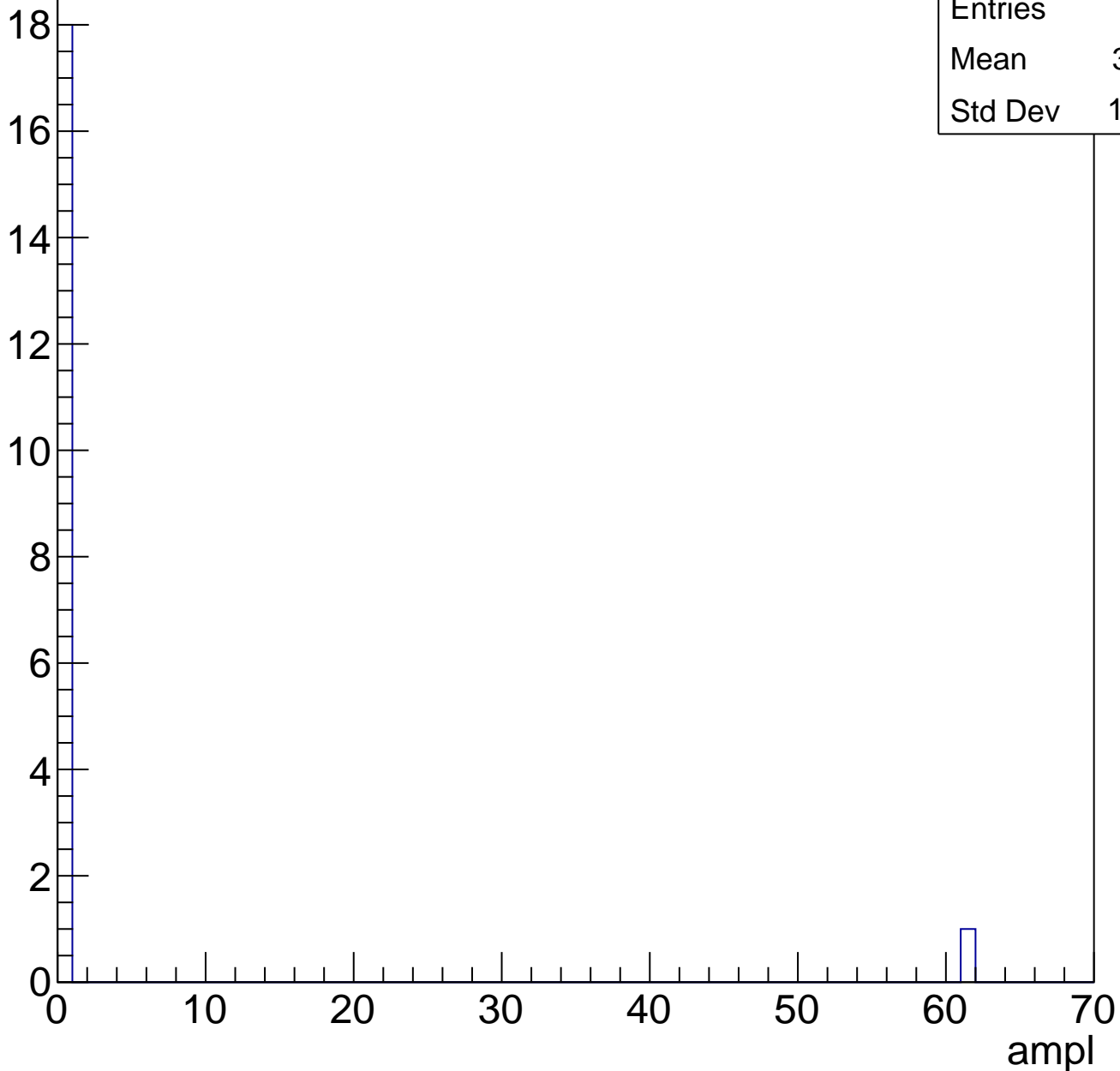


B1L103S, U8-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.211
Std Dev	13.62

Entry



B1L103S, U8-ch103, adc0

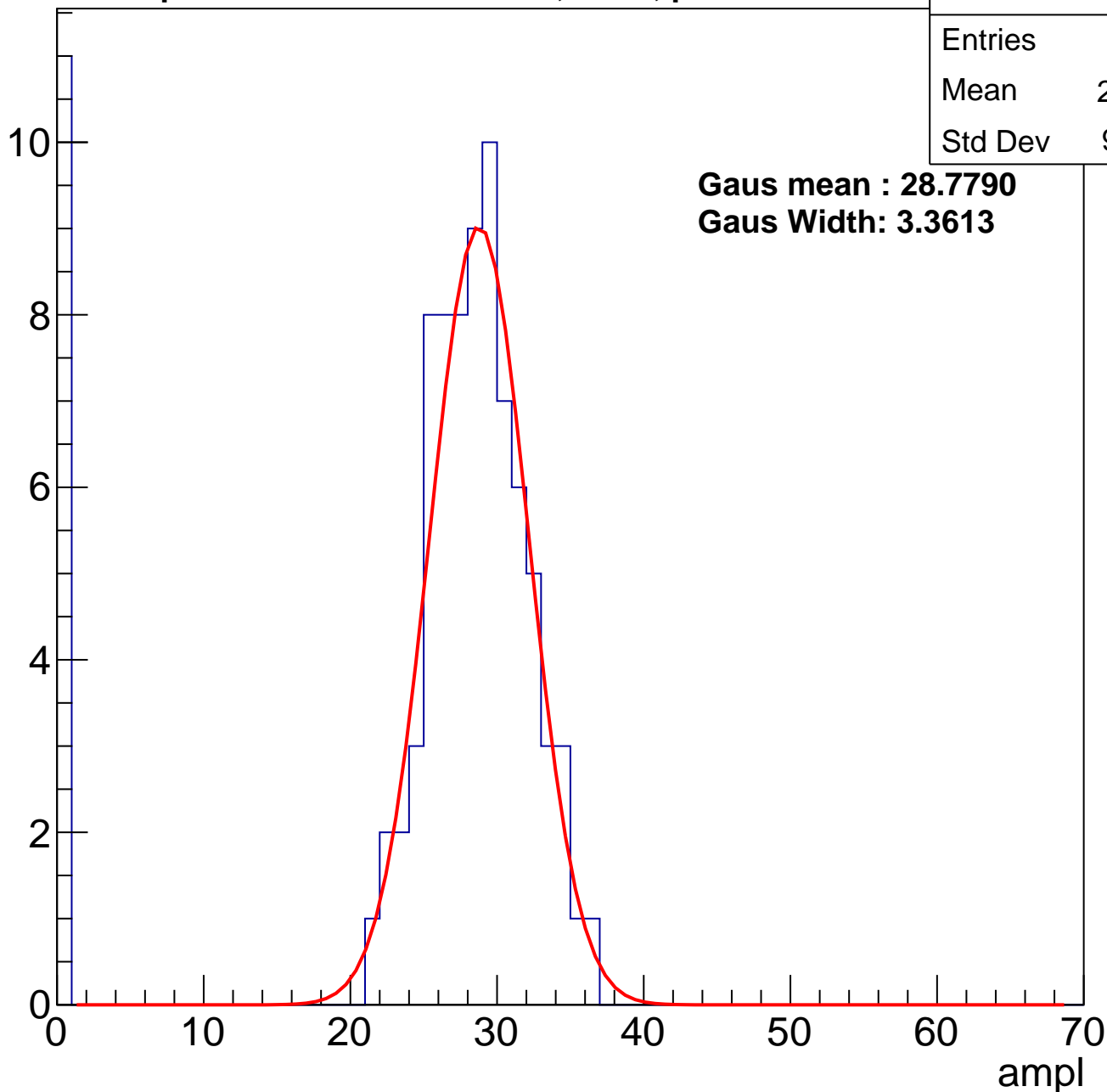
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	24.74
Std Dev	9.821

Gaus mean : 28.7790

Gaus Width: 3.3613

Entry



B1L103S, U8-ch103, adc1

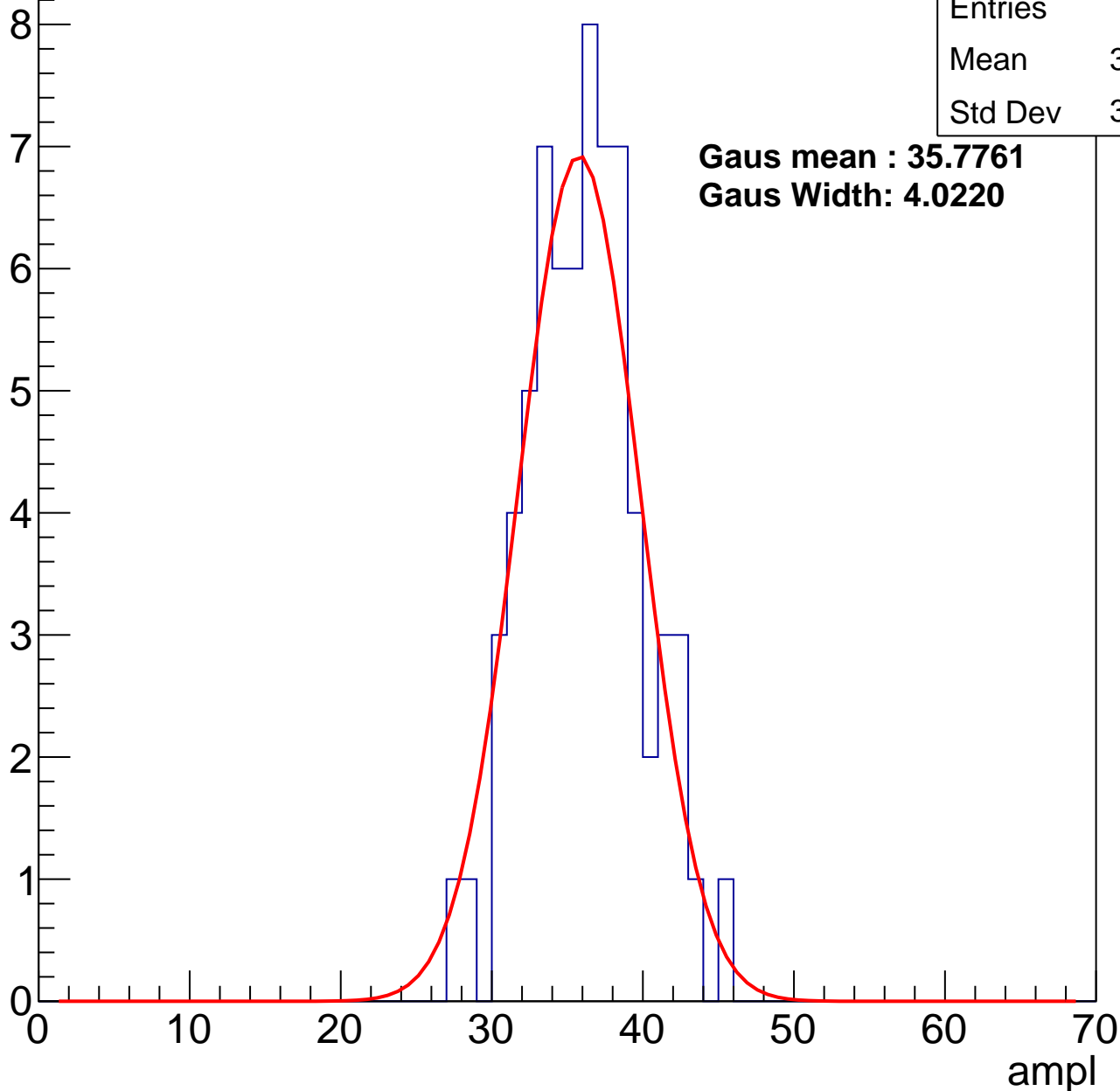
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.65
Std Dev	3.678

Gaus mean : 35.7761

Gaus Width: 4.0220



B1L103S, U8-ch103, adc2

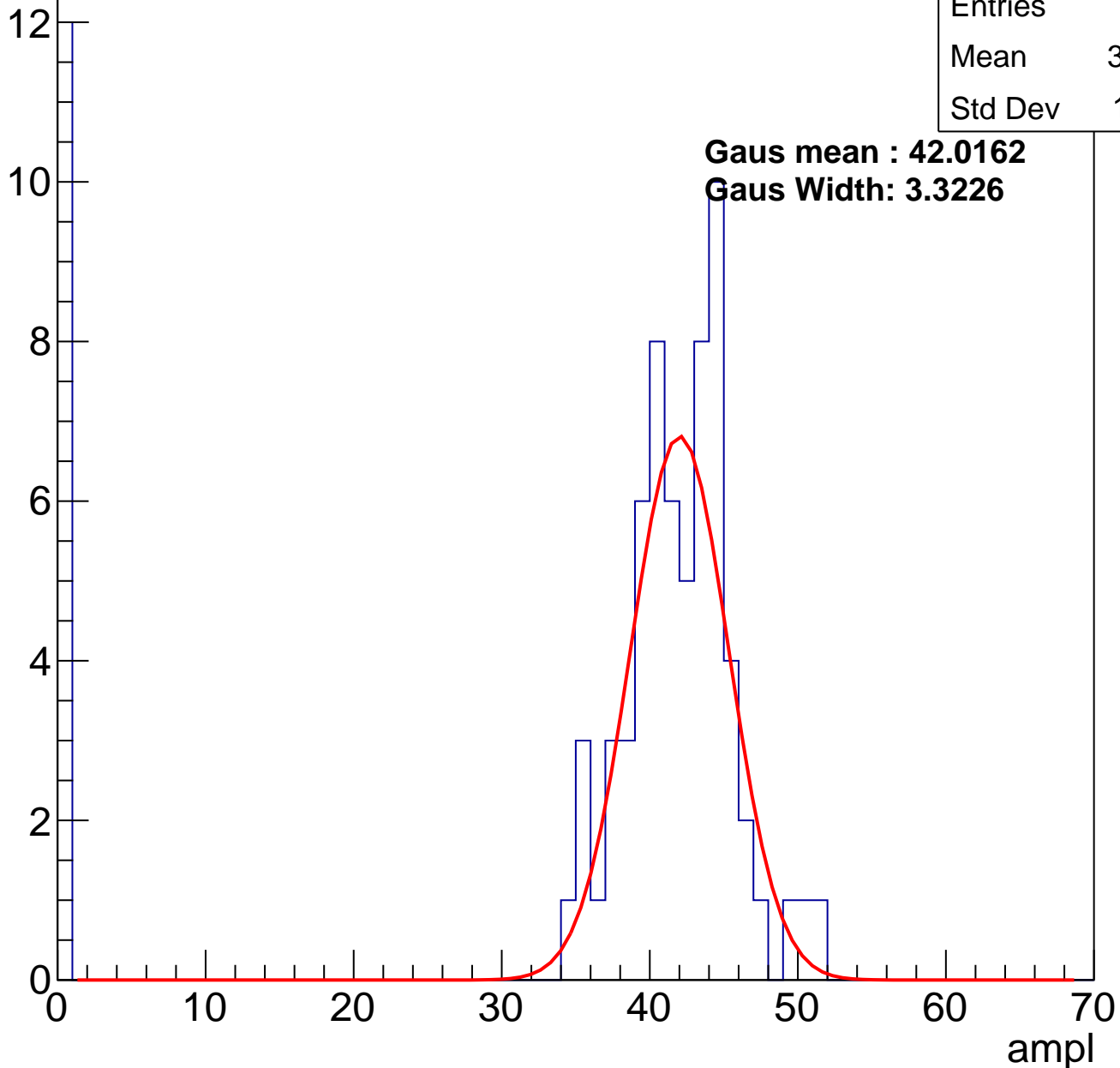
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	35.04
Std Dev	15.51

Gaus mean : 42.0162

Gaus Width: 3.3226

Entry

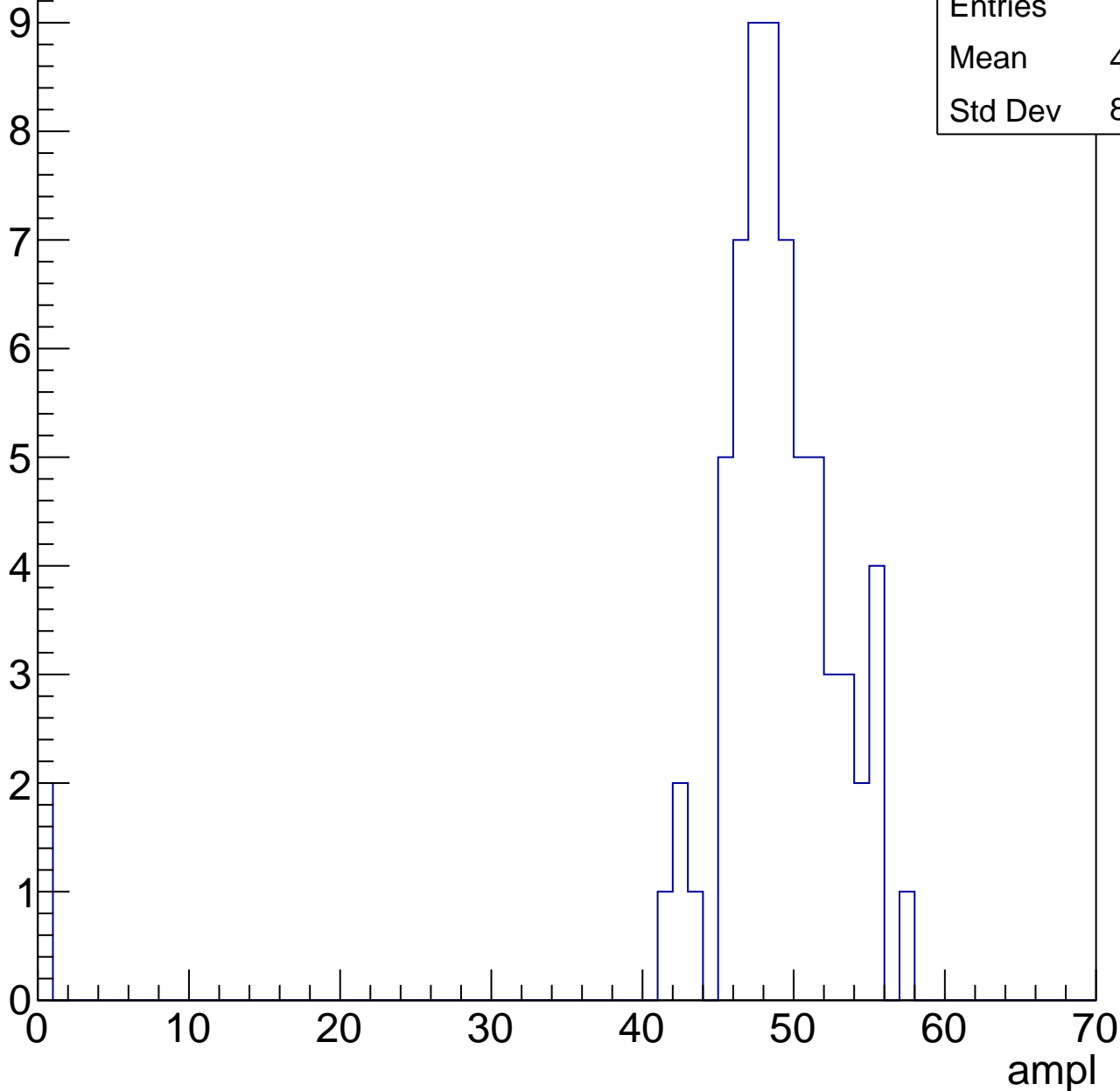


B1L103S, U8-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	47.24
Std Dev	8.997

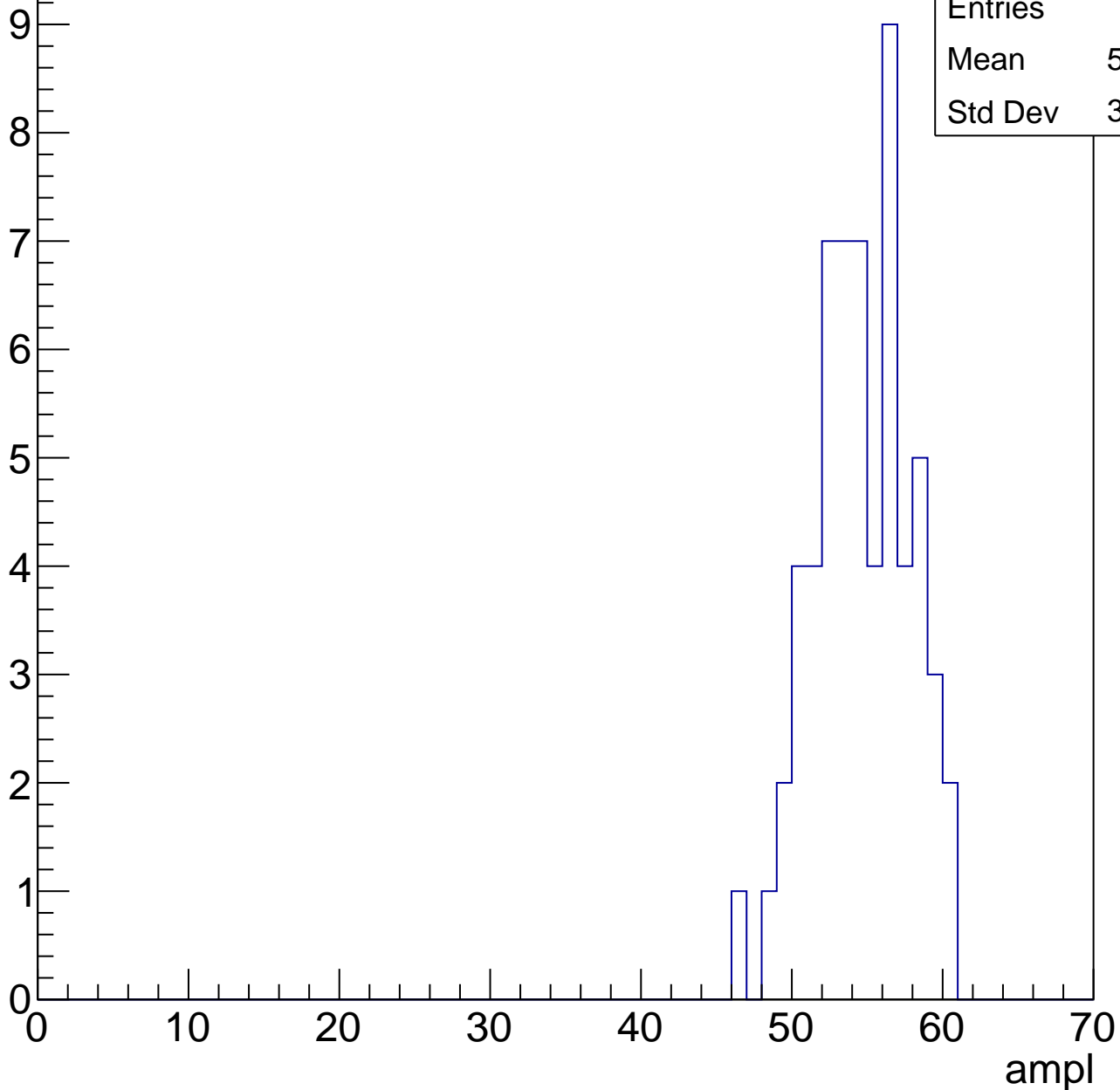


B1L103S, U8-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.13
Std Dev	3.128

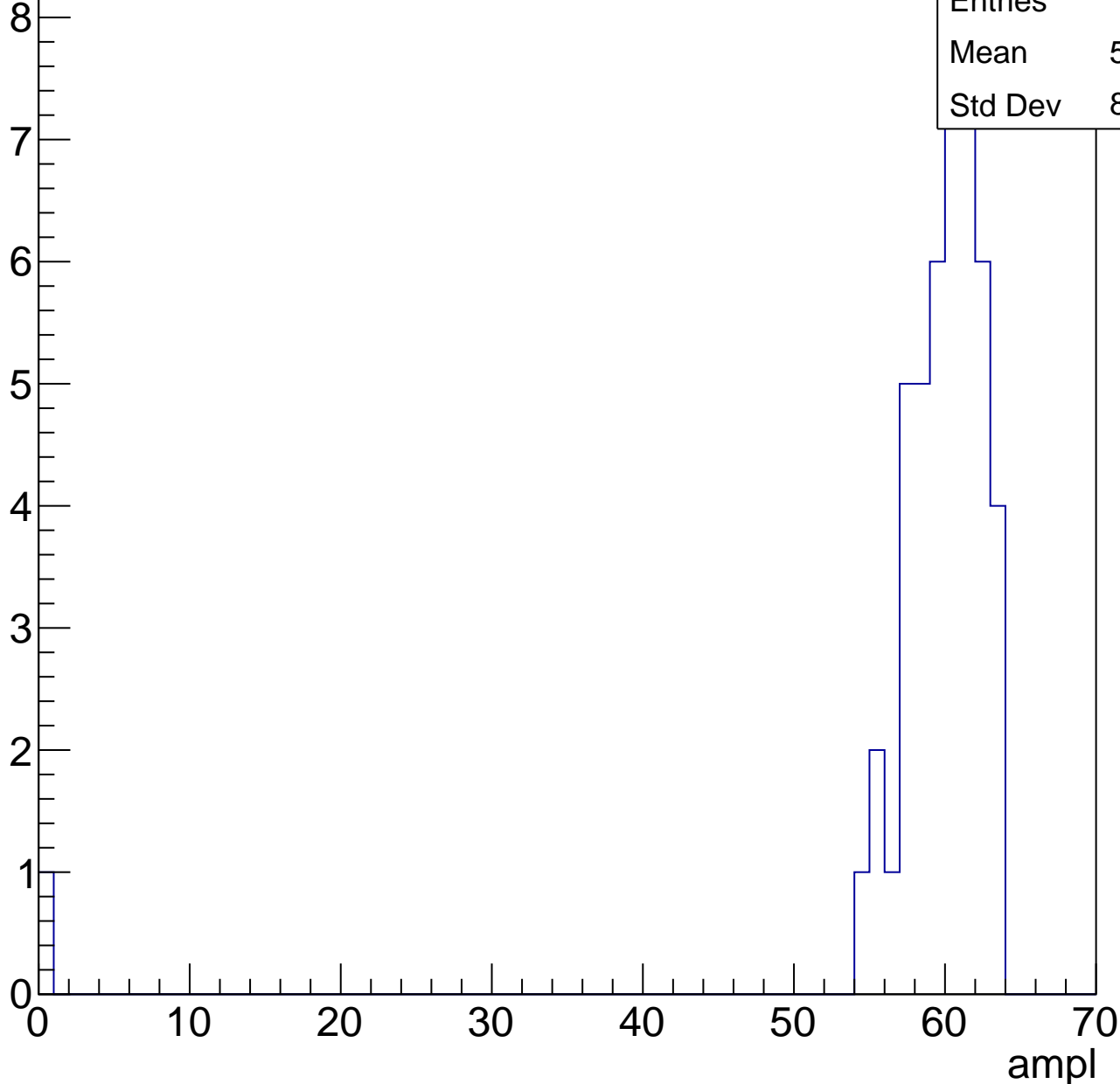


B1L103S, U8-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.32
Std Dev	8.882

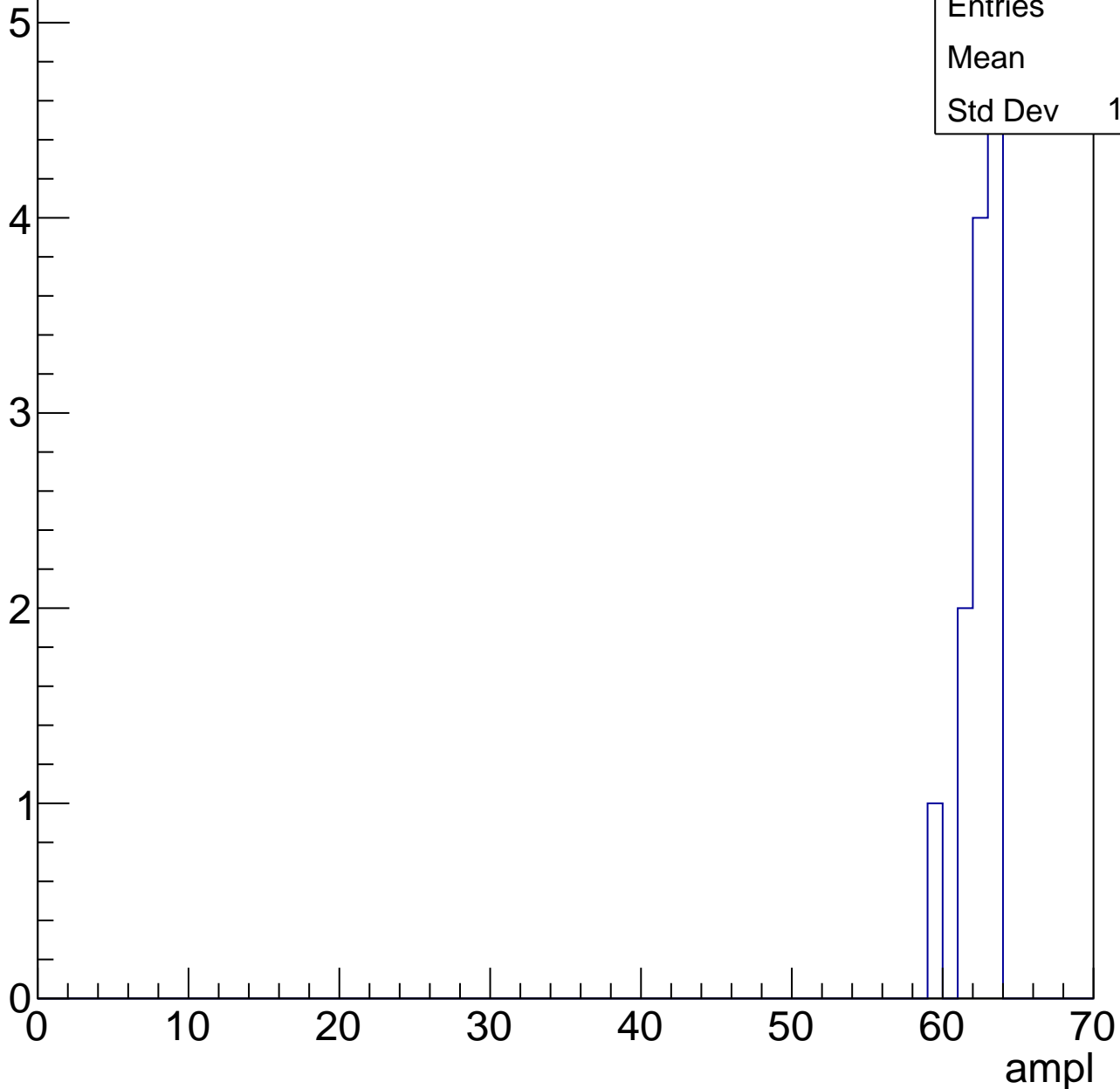


B1L103S, U8-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62
Std Dev	1.155

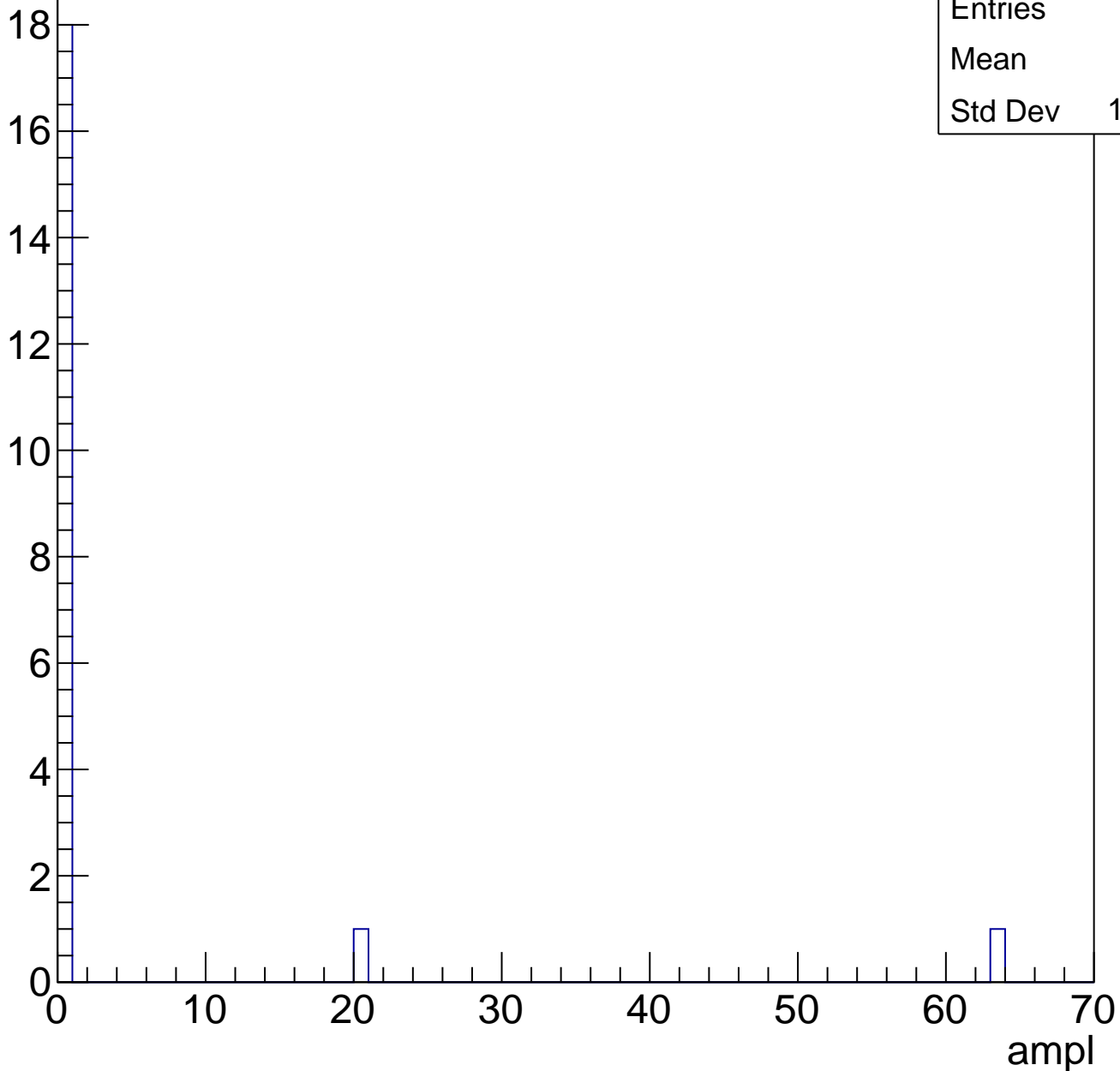


B1L103S, U8-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.15
Std Dev	14.19

Entry



B1L103S, U8-ch104, adc0

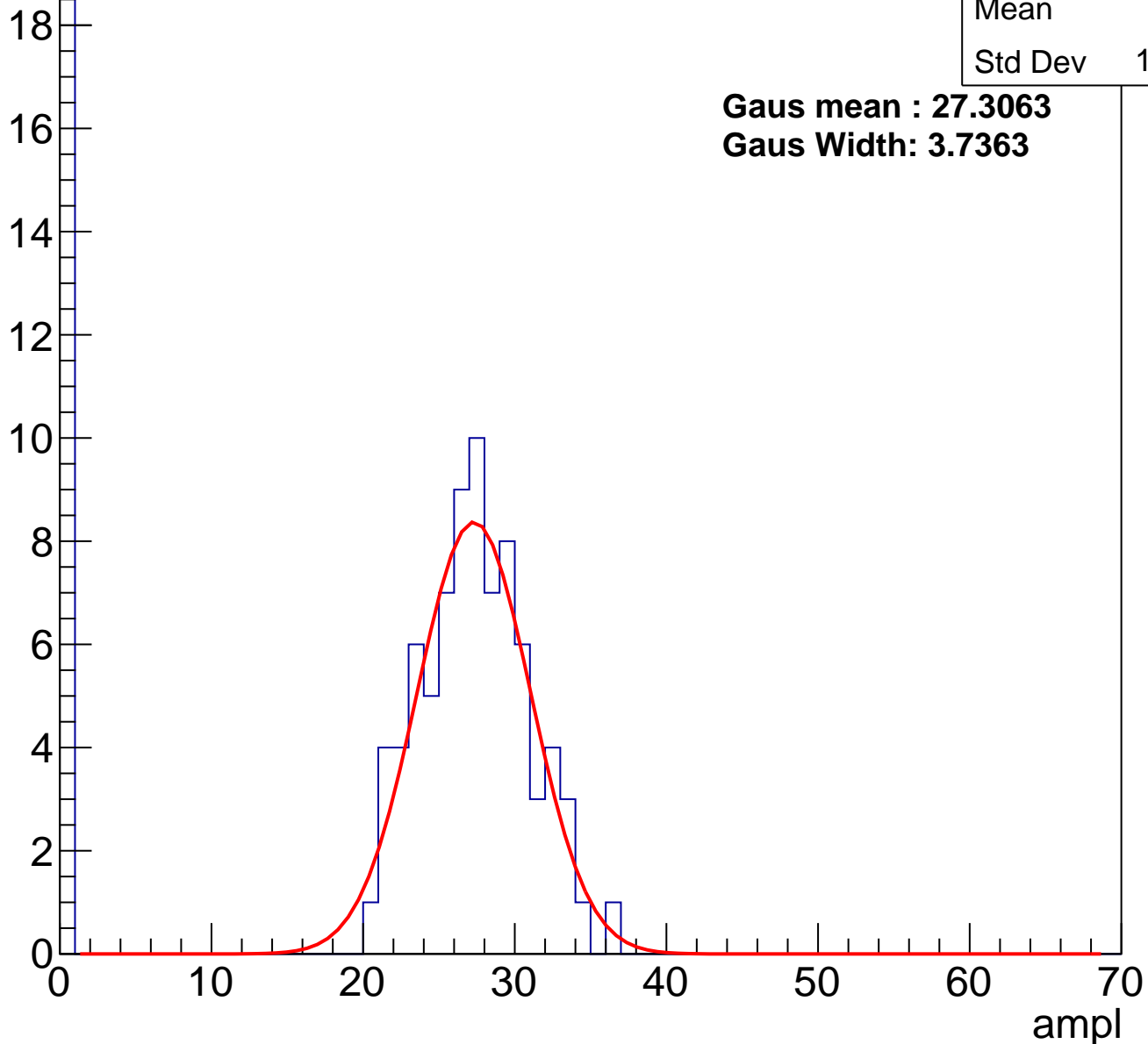
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	21.7
Std Dev	11.09

Gaus mean : 27.3063

Gaus Width: 3.7363

Entry



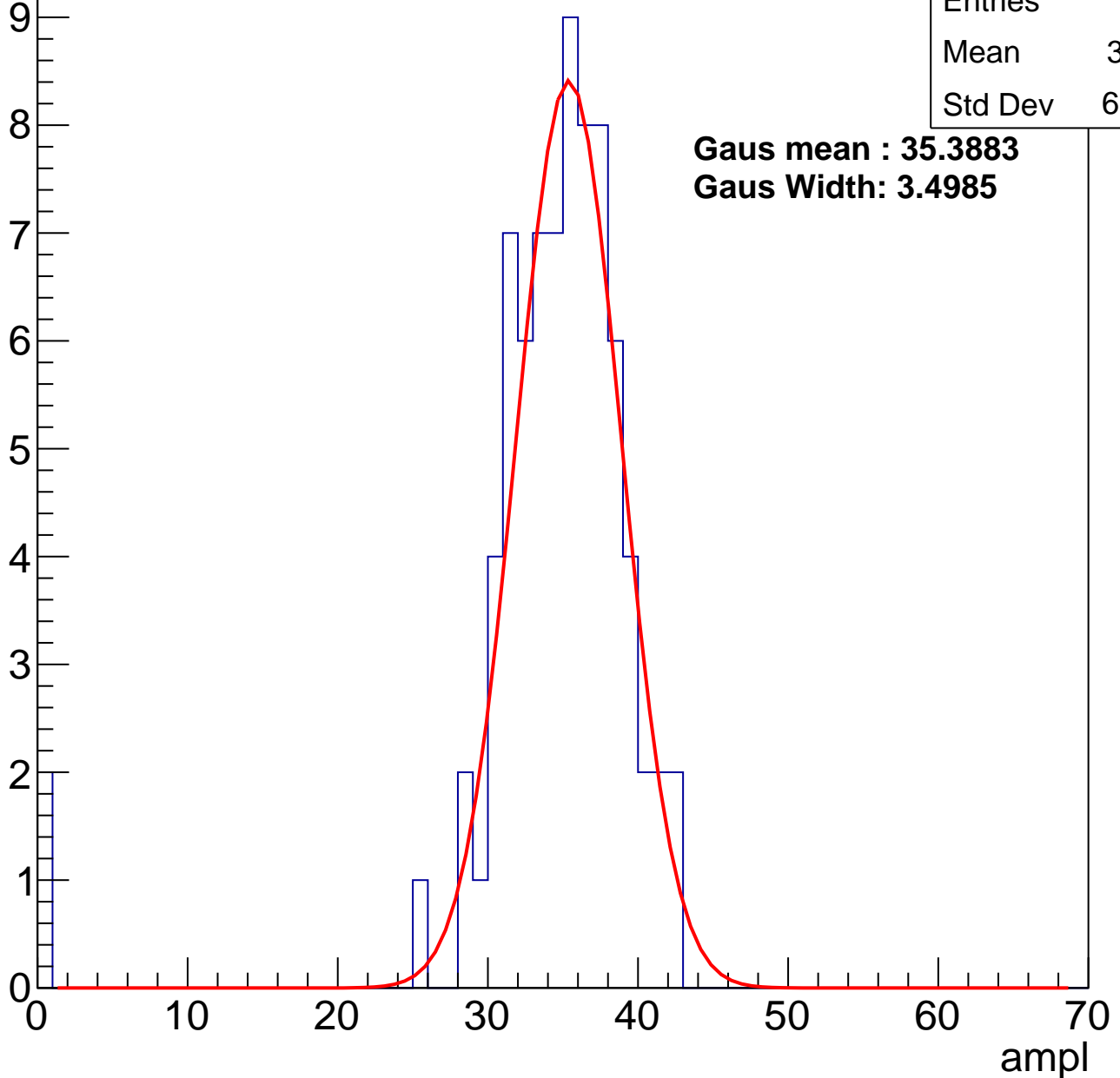
B1L103S, U8-ch104, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	33.81
Std Dev	6.457

Gaus mean : 35.3883
Gaus Width: 3.4985



B1L103S, U8-ch104, adc2

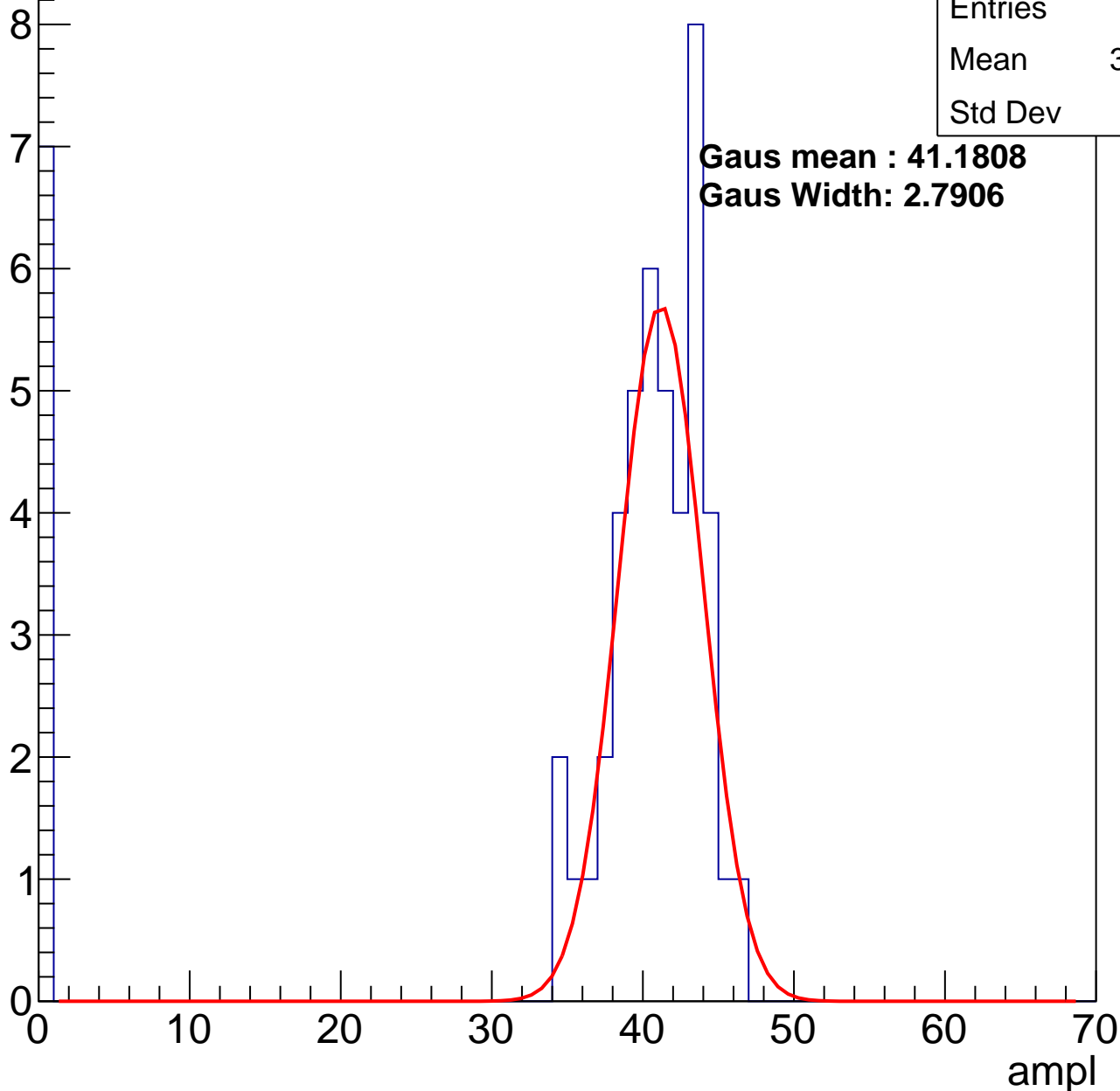
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	34.98
Std Dev	14.2

Gaus mean : 41.1808

Gaus Width: 2.7906

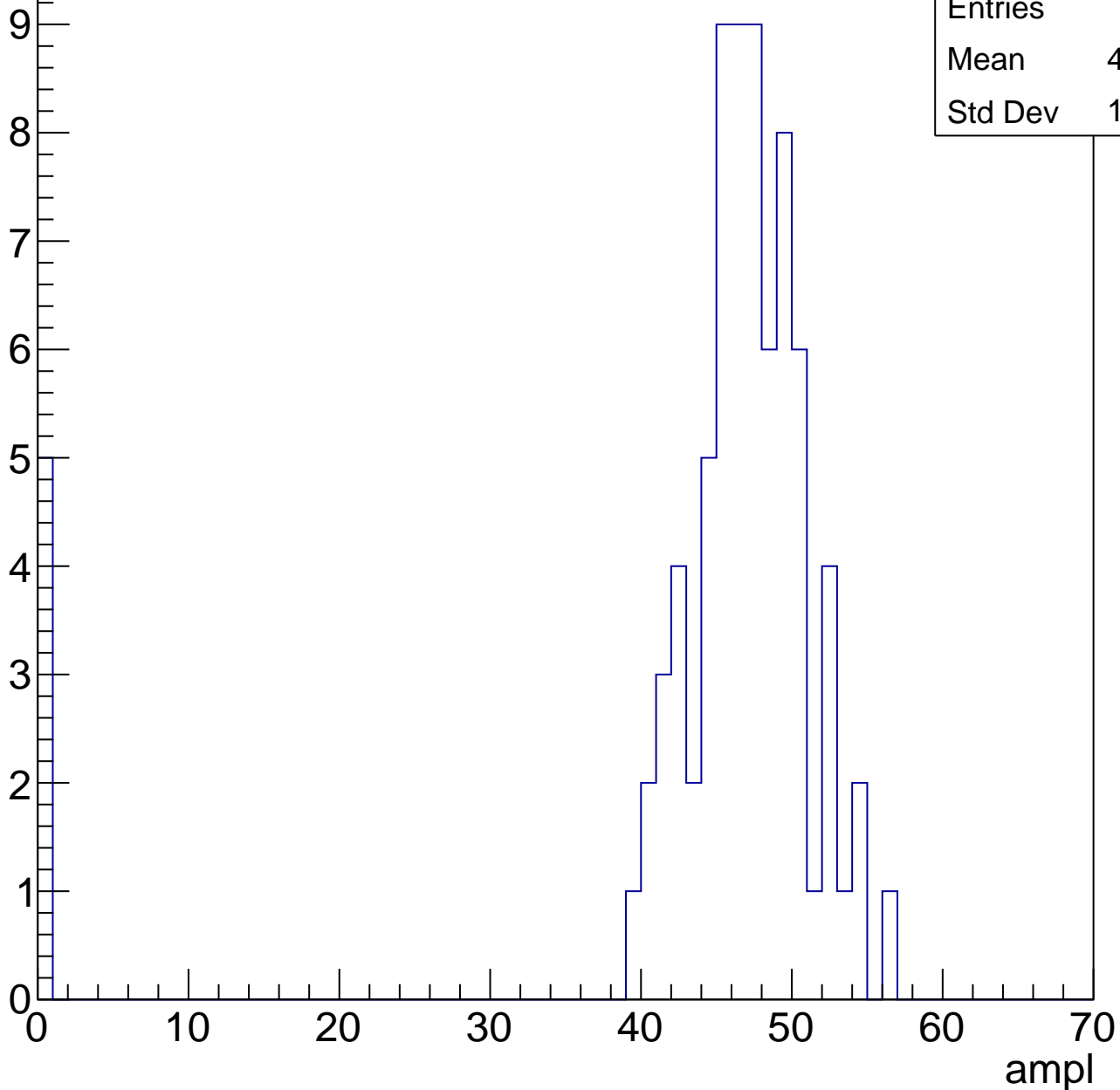


B1L103S, U8-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	43.77
Std Dev	11.95

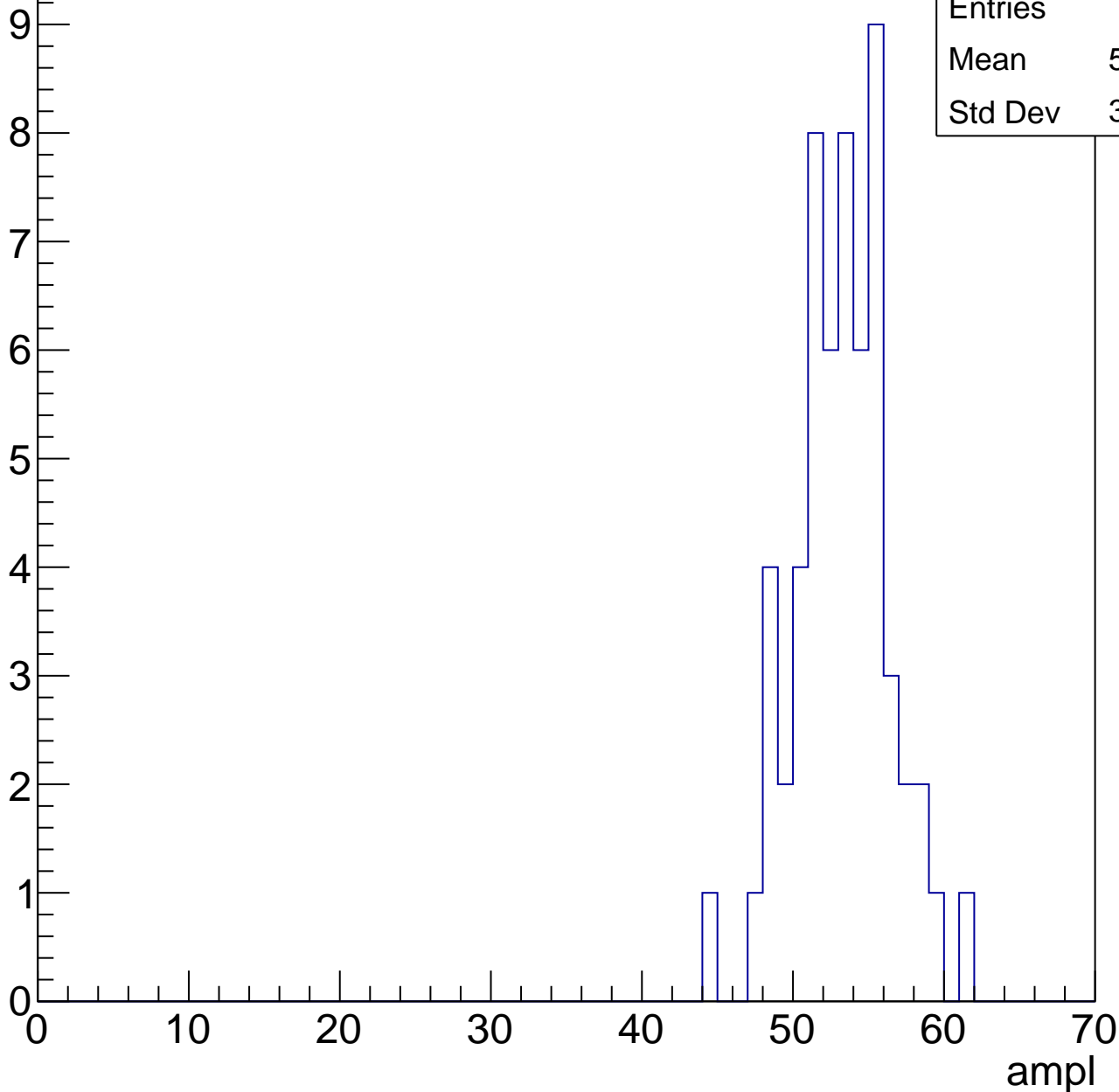


B1L103S, U8-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.79
Std Dev	3.139

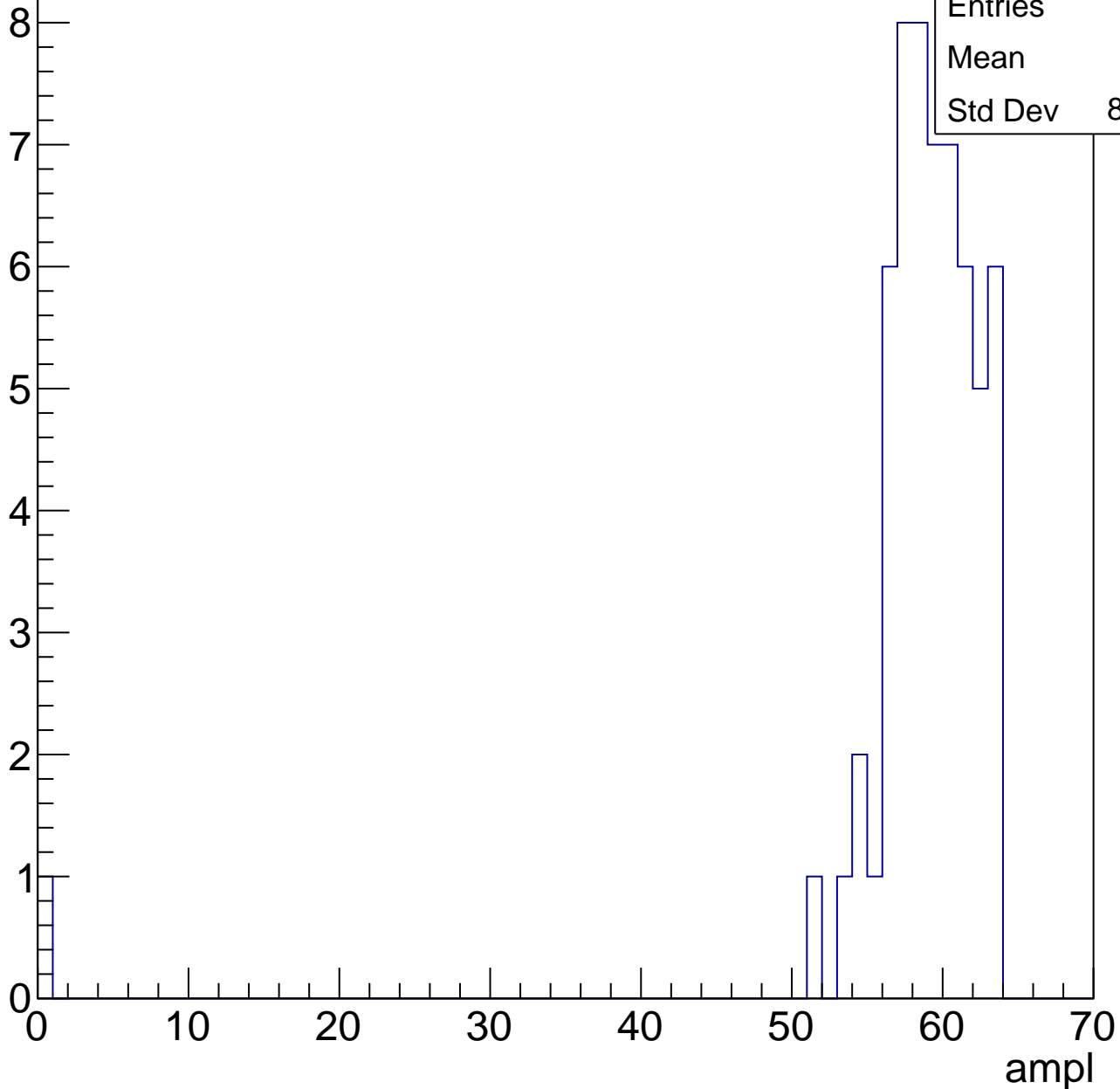


B1L103S, U8-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.8
Std Dev	8.054

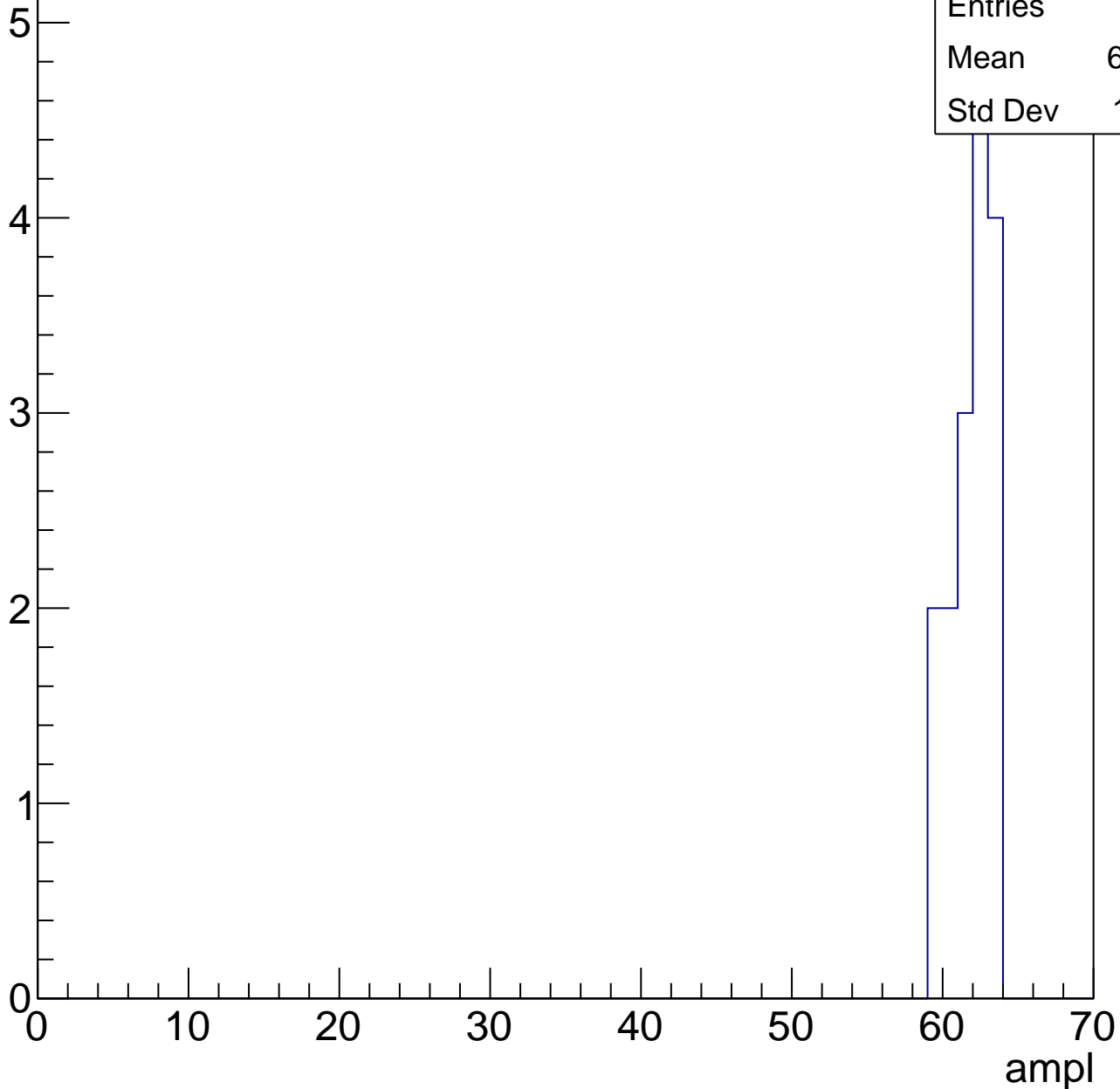


B1L103S, U8-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.44
Std Dev	1.321



B1L103S, U8-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U8-ch105, adc0

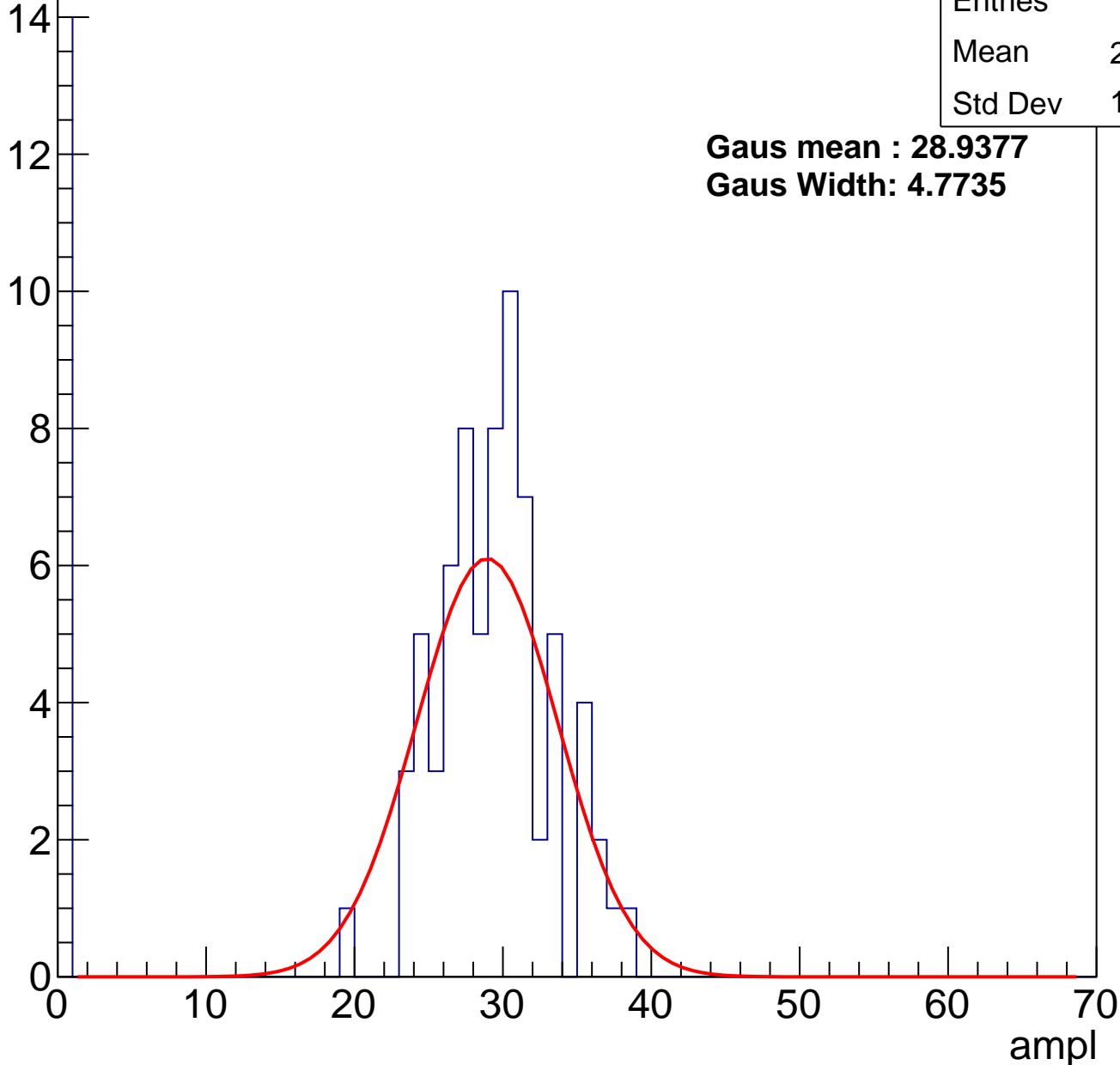
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	24.24
Std Dev	11.29

Gaus mean : 28.9377

Gaus Width: 4.7735

Entry



B1L103S, U8-ch105, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	29.25
Std Dev	13.1

Gaus mean : 35.3515

Gaus Width: 3.2281

Entry

10

8

6

4

2

0

0

10

20

30

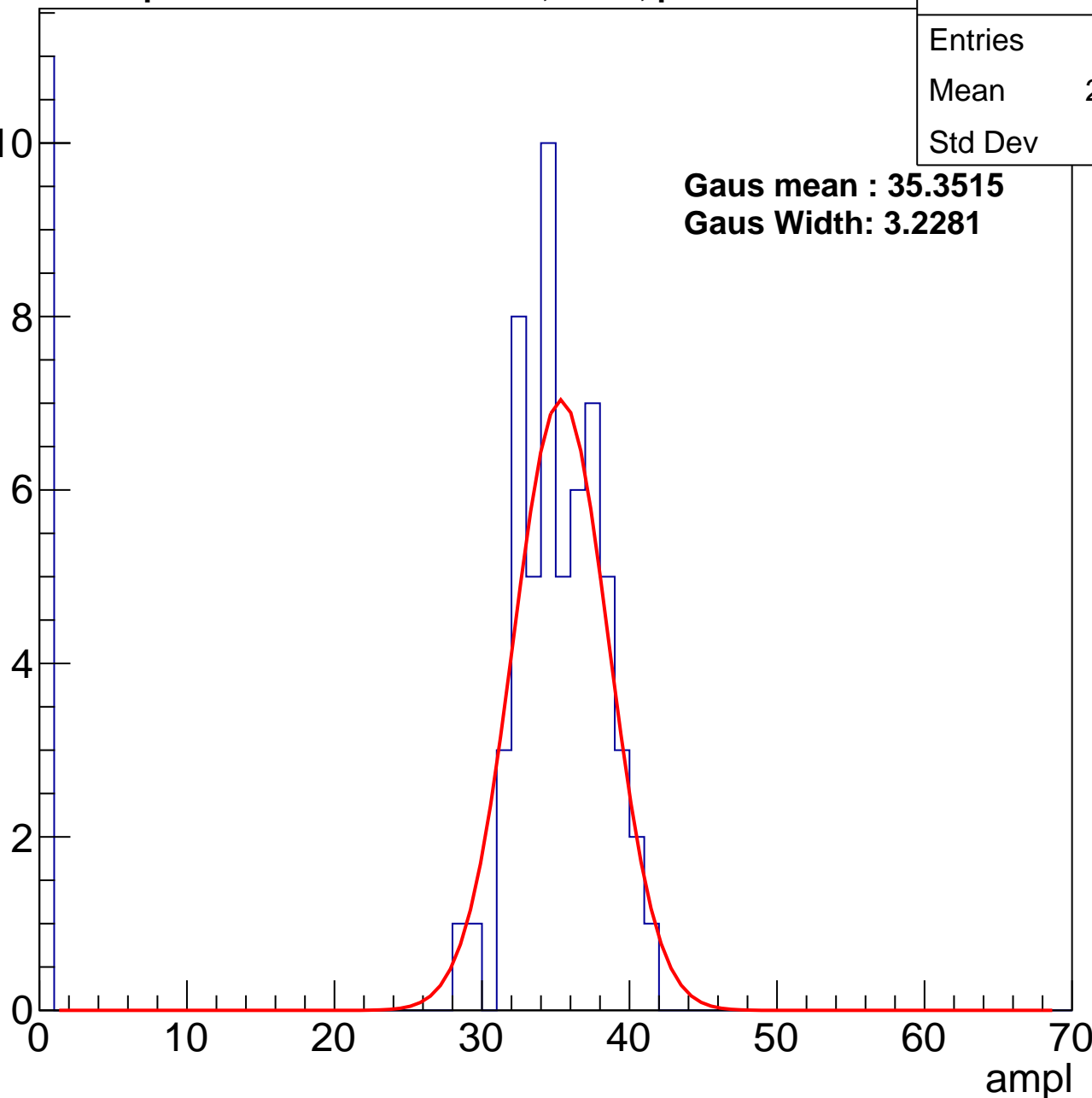
40

50

60

70

ampl



B1L103S, U8-ch105, adc2

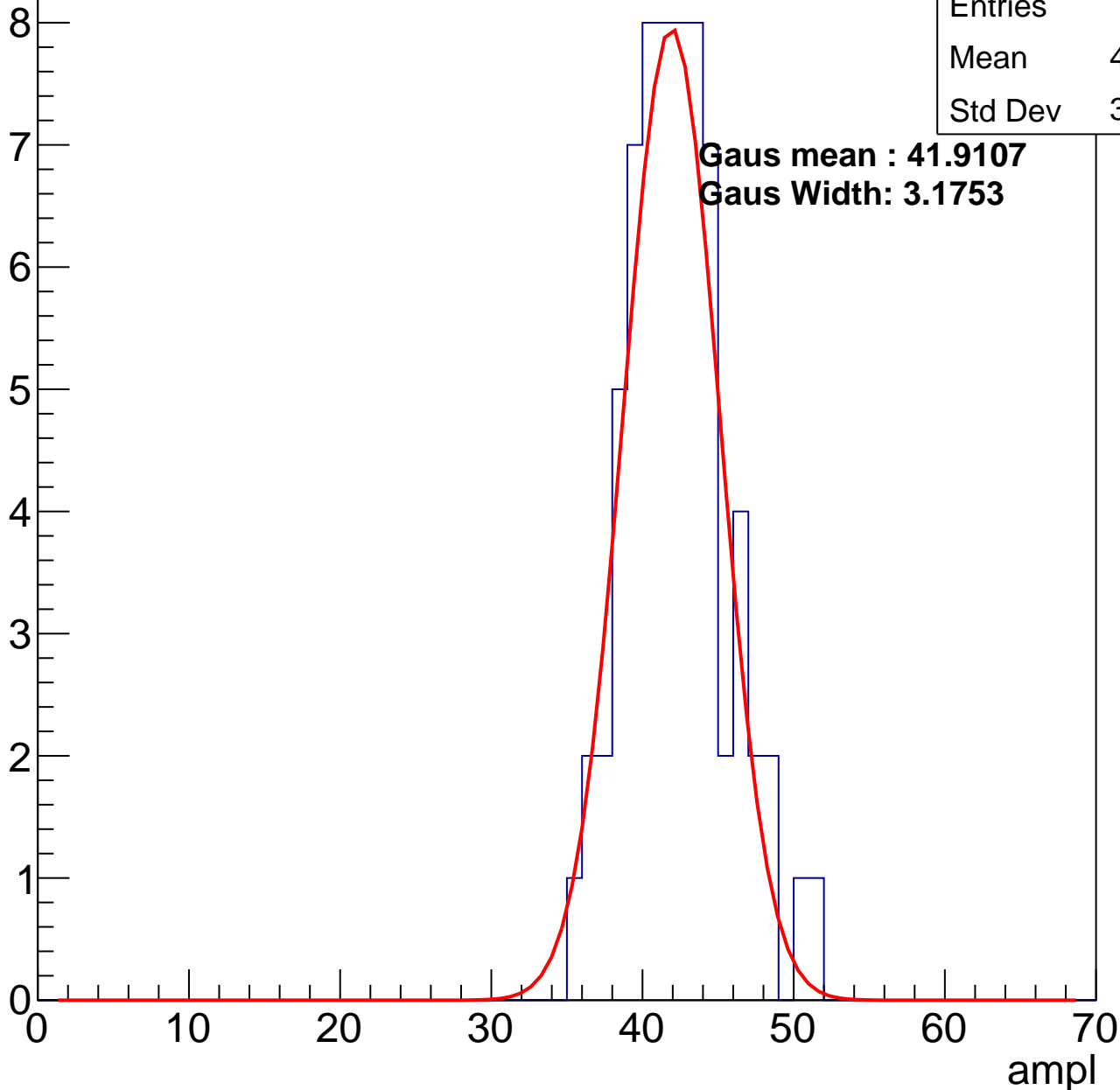
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.84
Std Dev	3.293

Gaus mean : 41.9107

Gaus Width: 3.1753

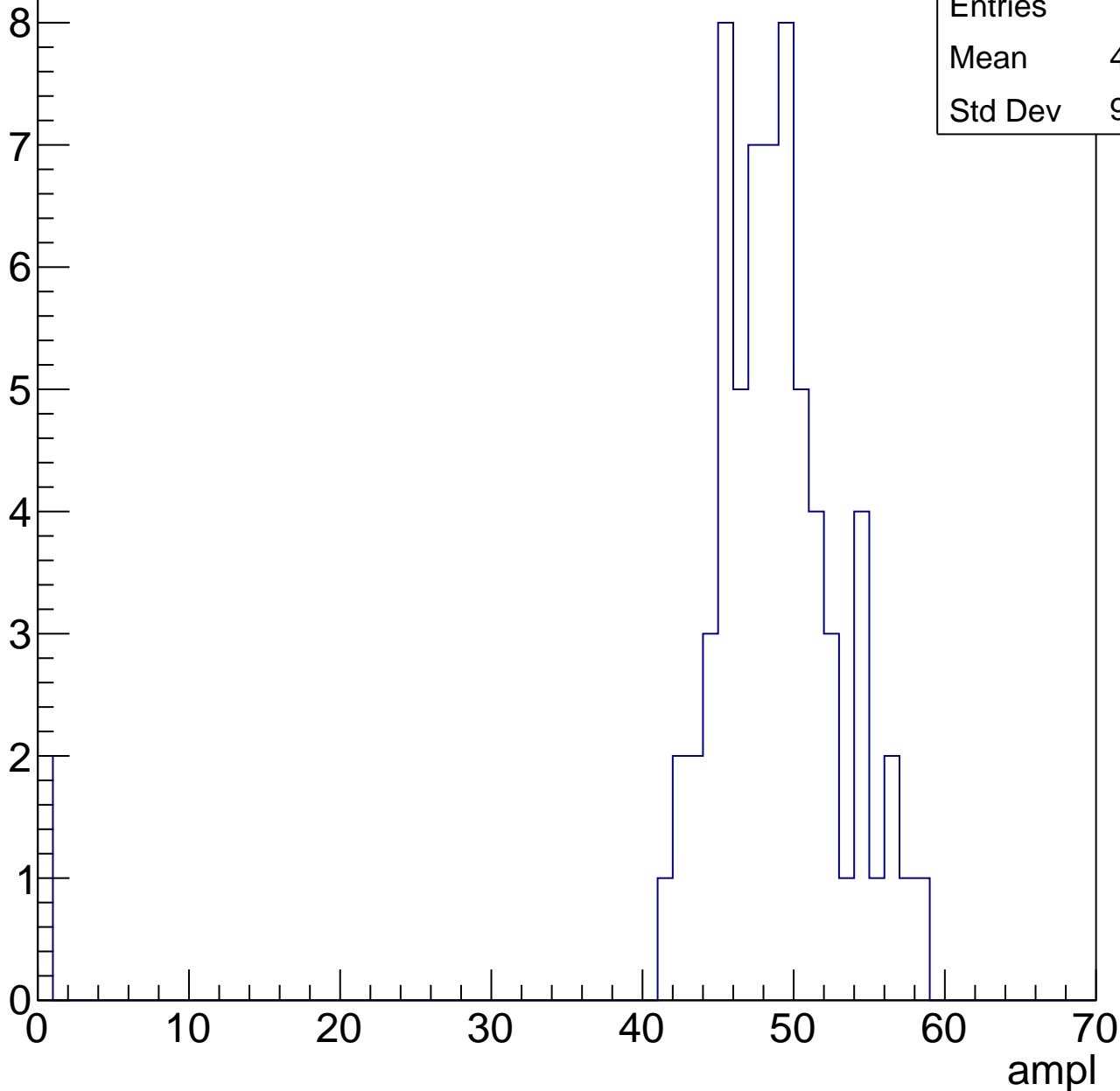


B1L103S, U8-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.03
Std Dev	9.062

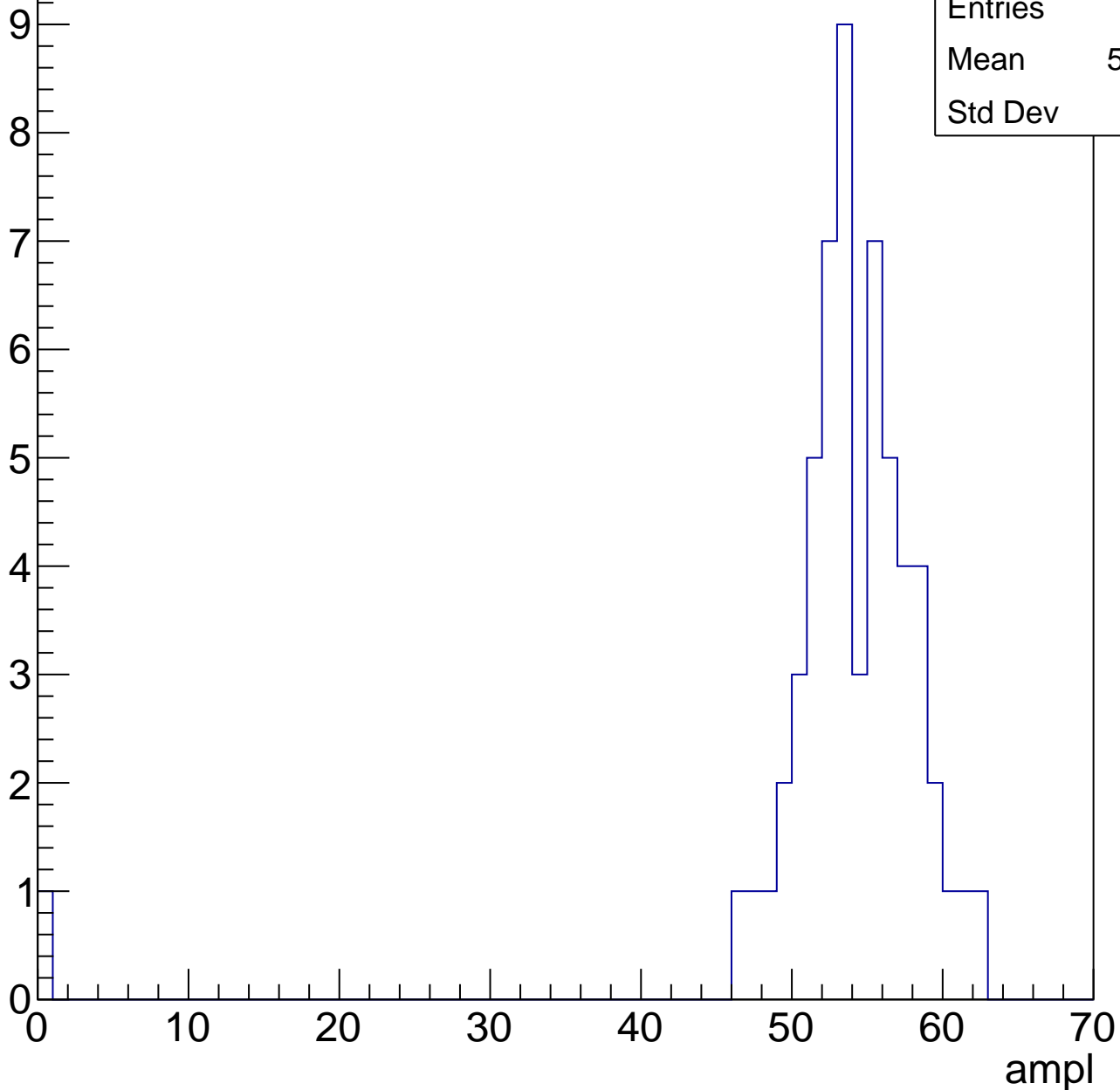


B1L103S, U8-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.98
Std Dev	7.78

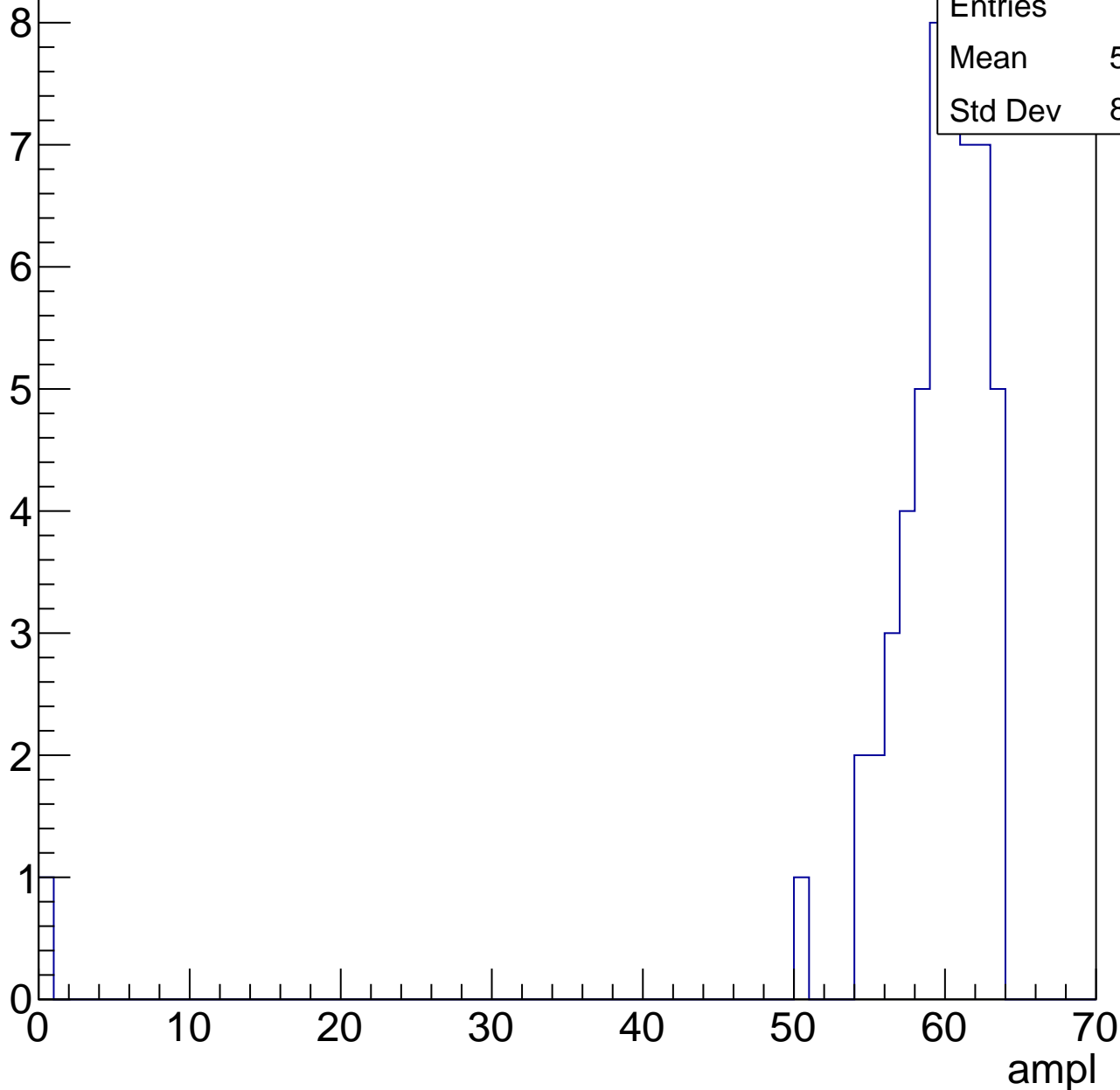


B1L103S, U8-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

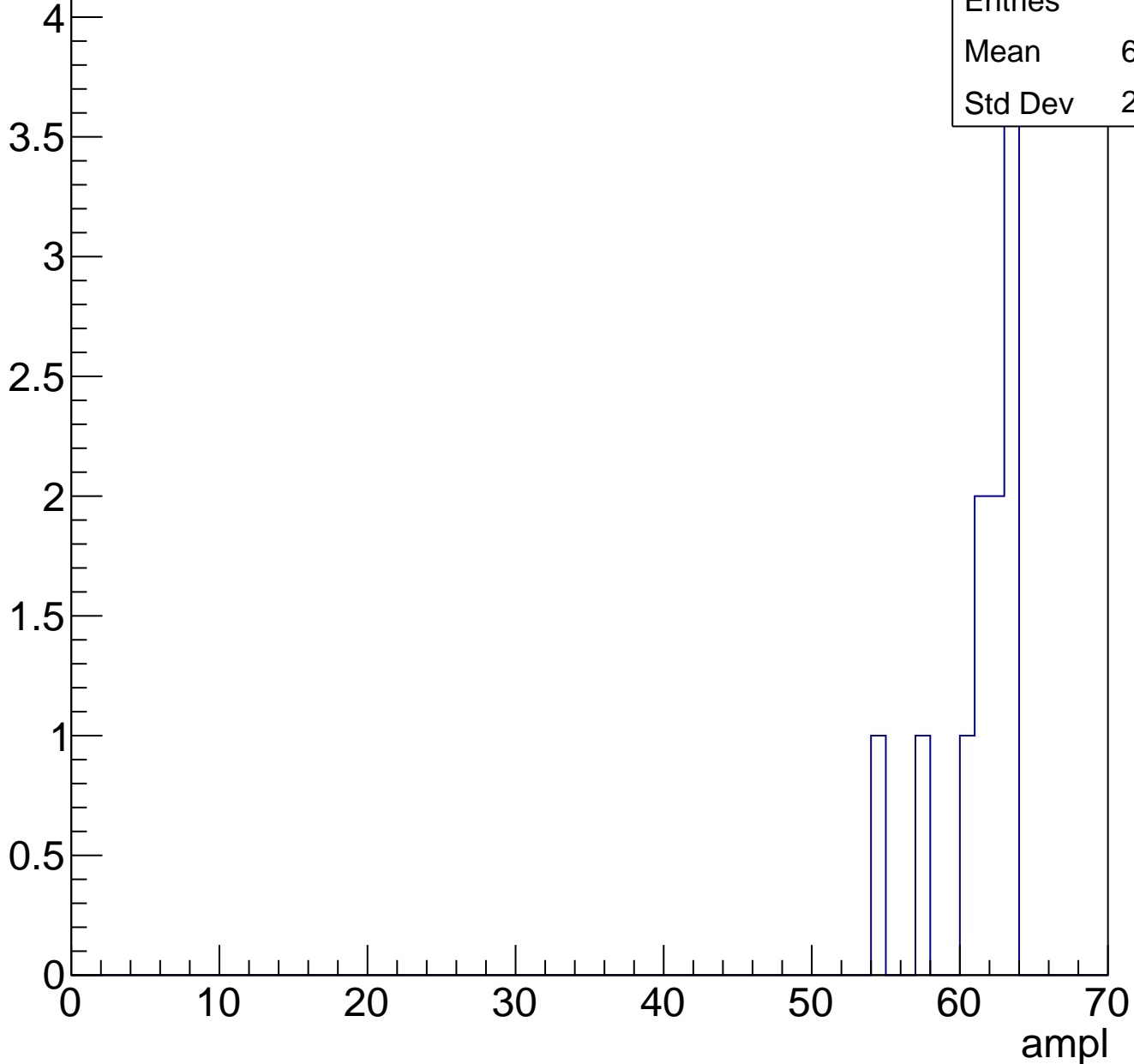
Entries	53
Mean	58.15
Std Dev	8.504



B1L103S, U8-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	11
Mean	60.82
Std Dev	2.757

B1L103S, U8-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U8-ch106, adc0

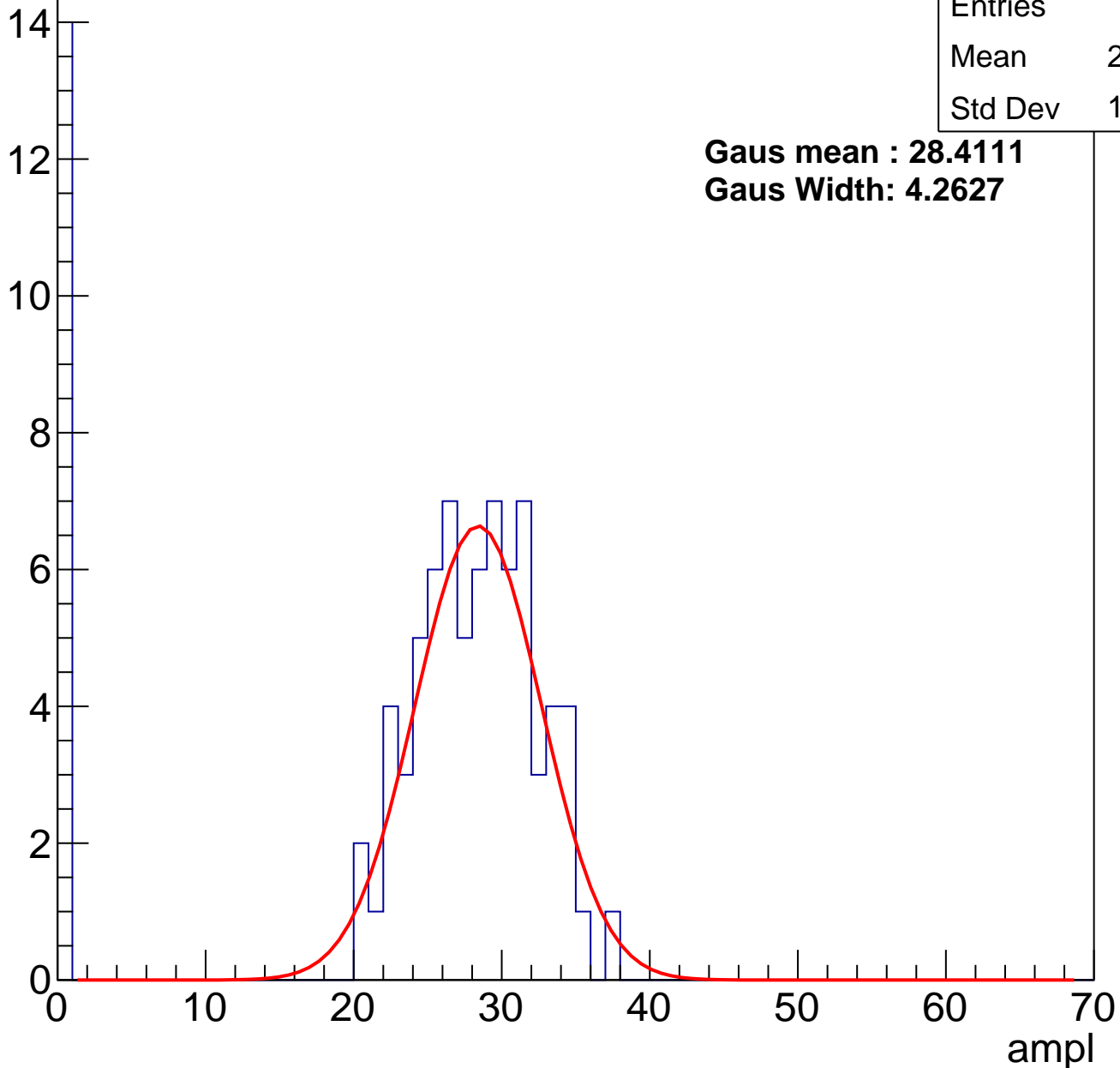
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.36
Std Dev	10.89

Gaus mean : 28.4111

Gaus Width: 4.2627

Entry



B1L103S, U8-ch106, adc1

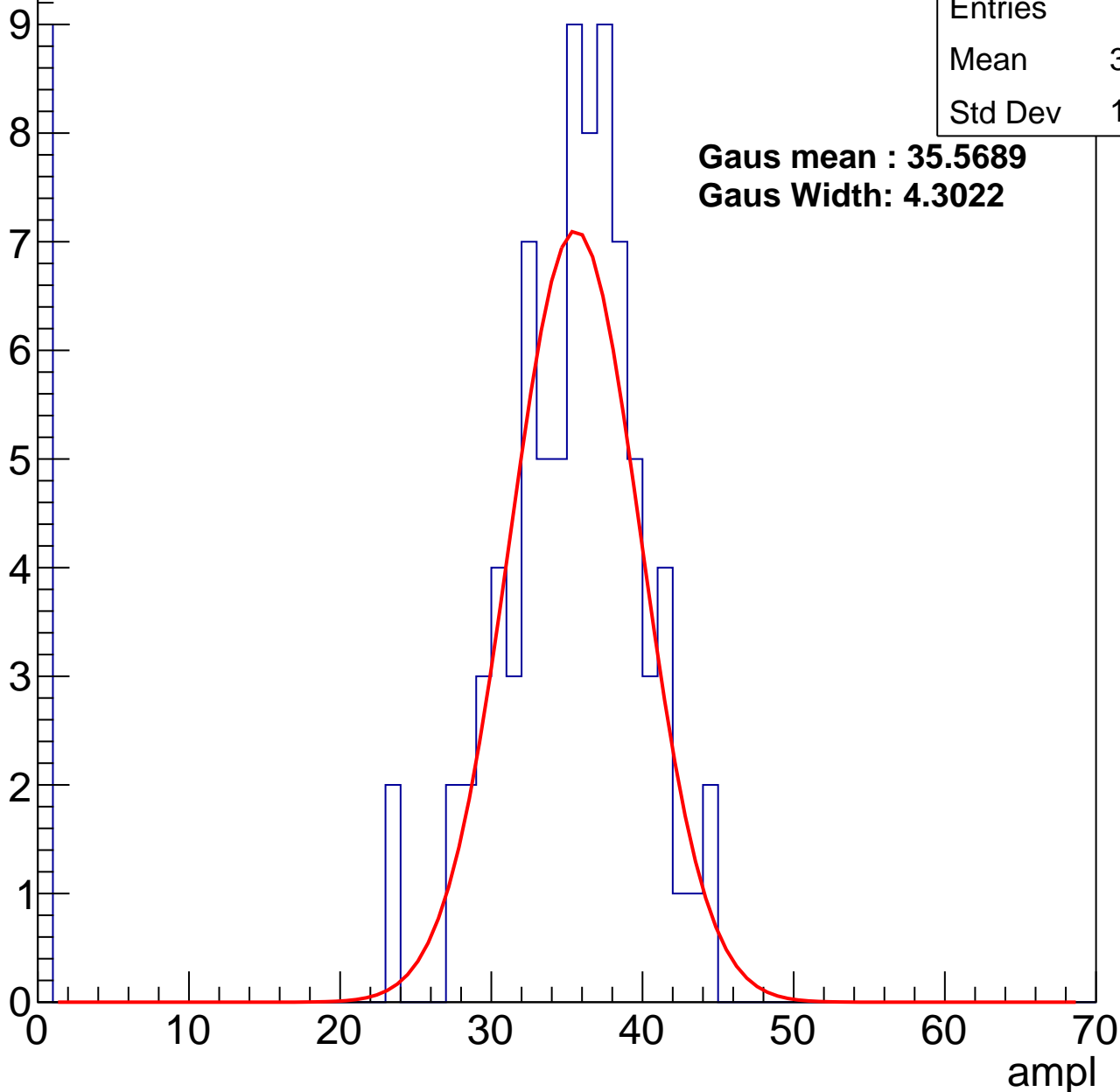
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	91
Mean	31.53
Std Dev	11.23

Gaus mean : 35.5689

Gaus Width: 4.3022



B1L103S, U8-ch106, adc2

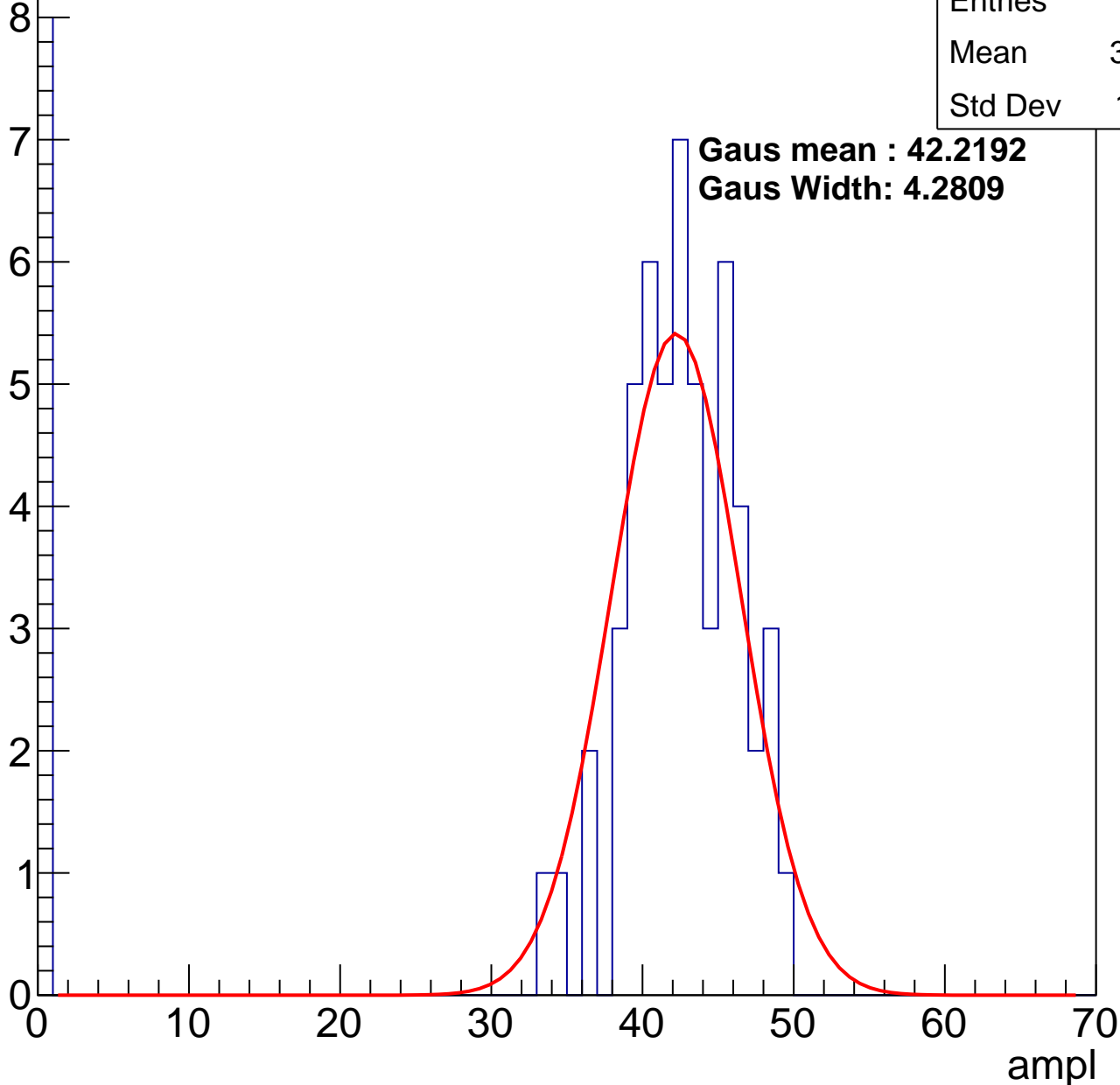
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	36.69
Std Dev	14.51

Gaus mean : 42.2192

Gaus Width: 4.2809

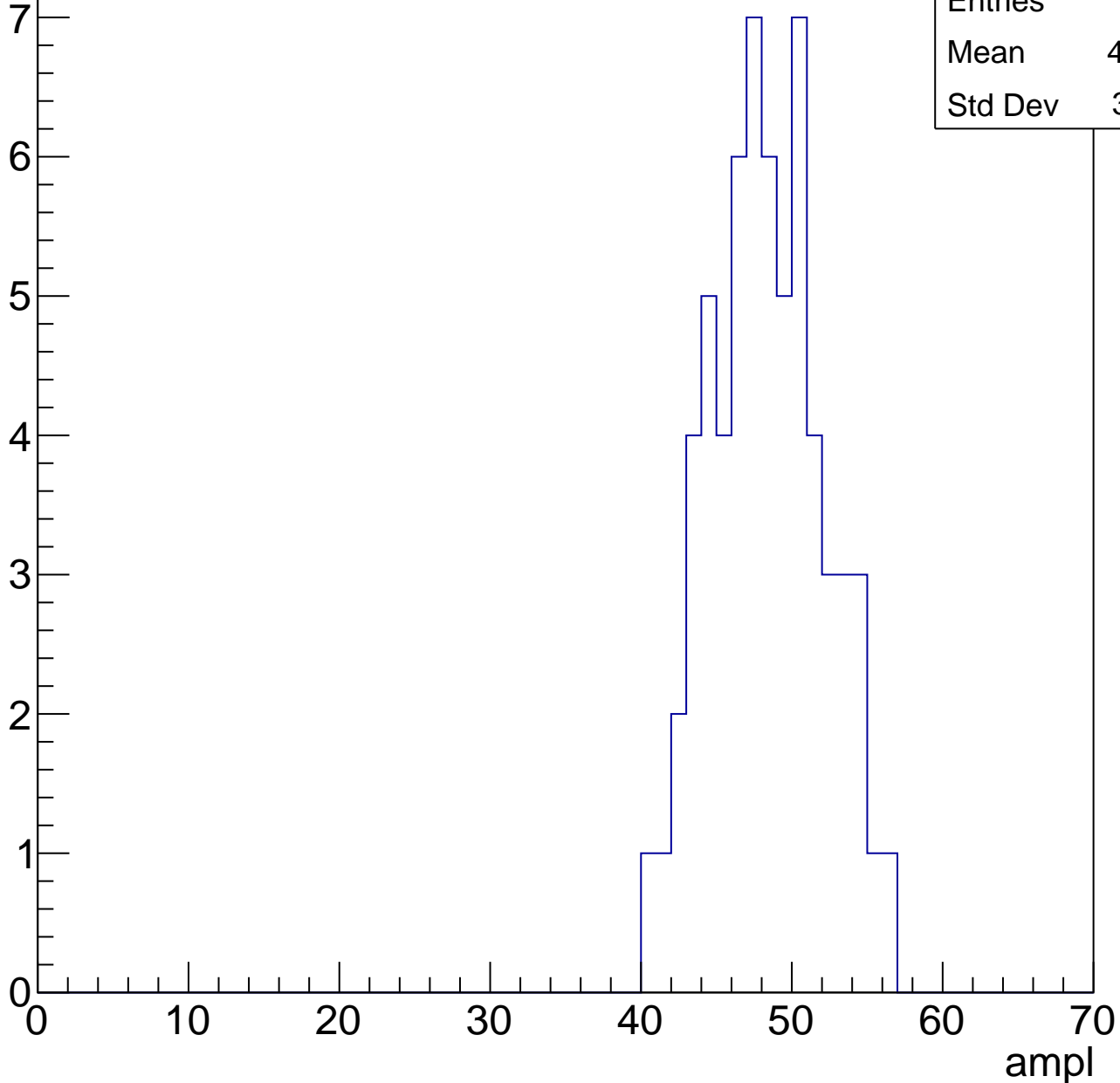


B1L103S, U8-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.89
Std Dev	3.661

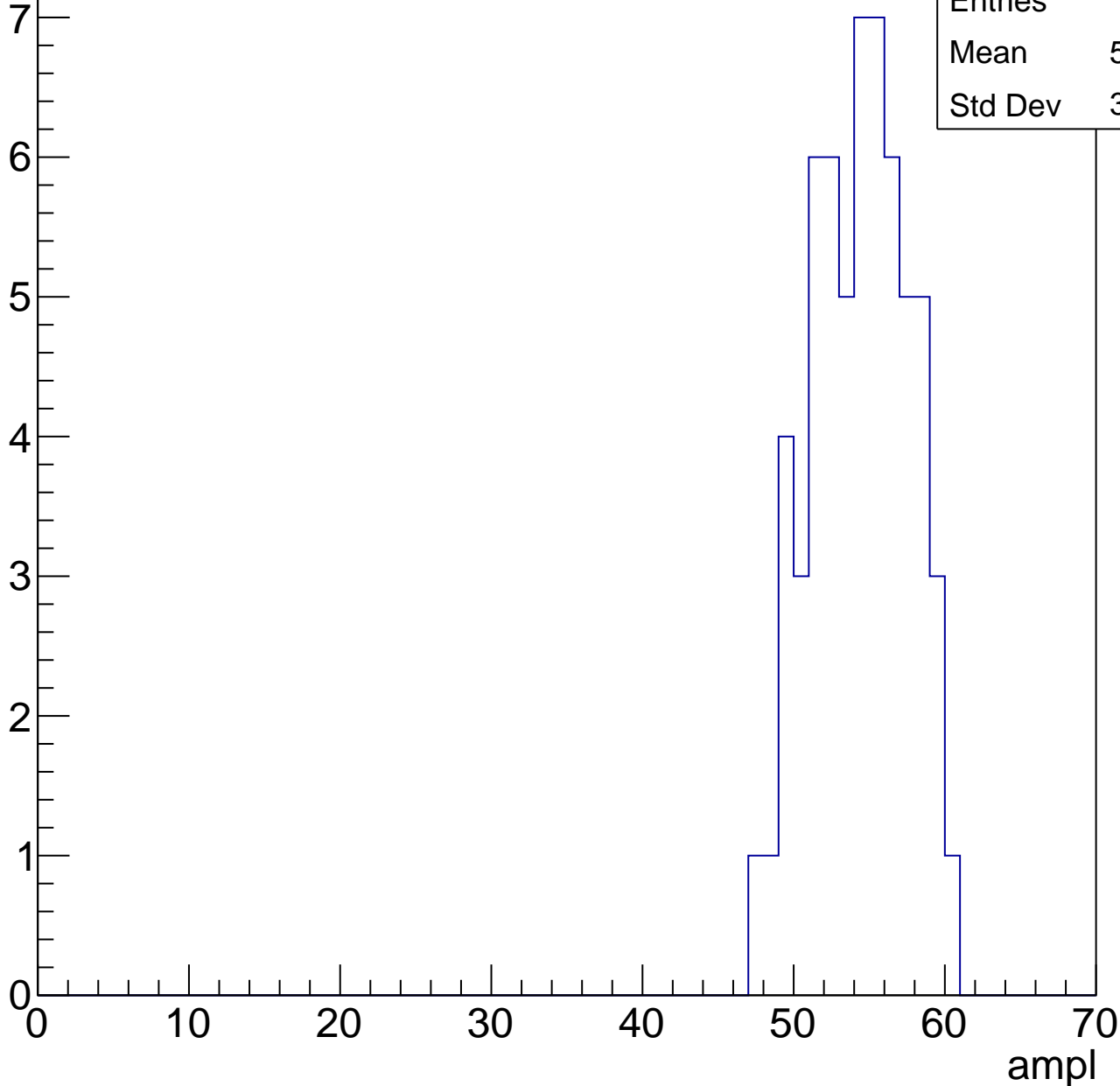


B1L103S, U8-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	53.92
Std Dev	3.116

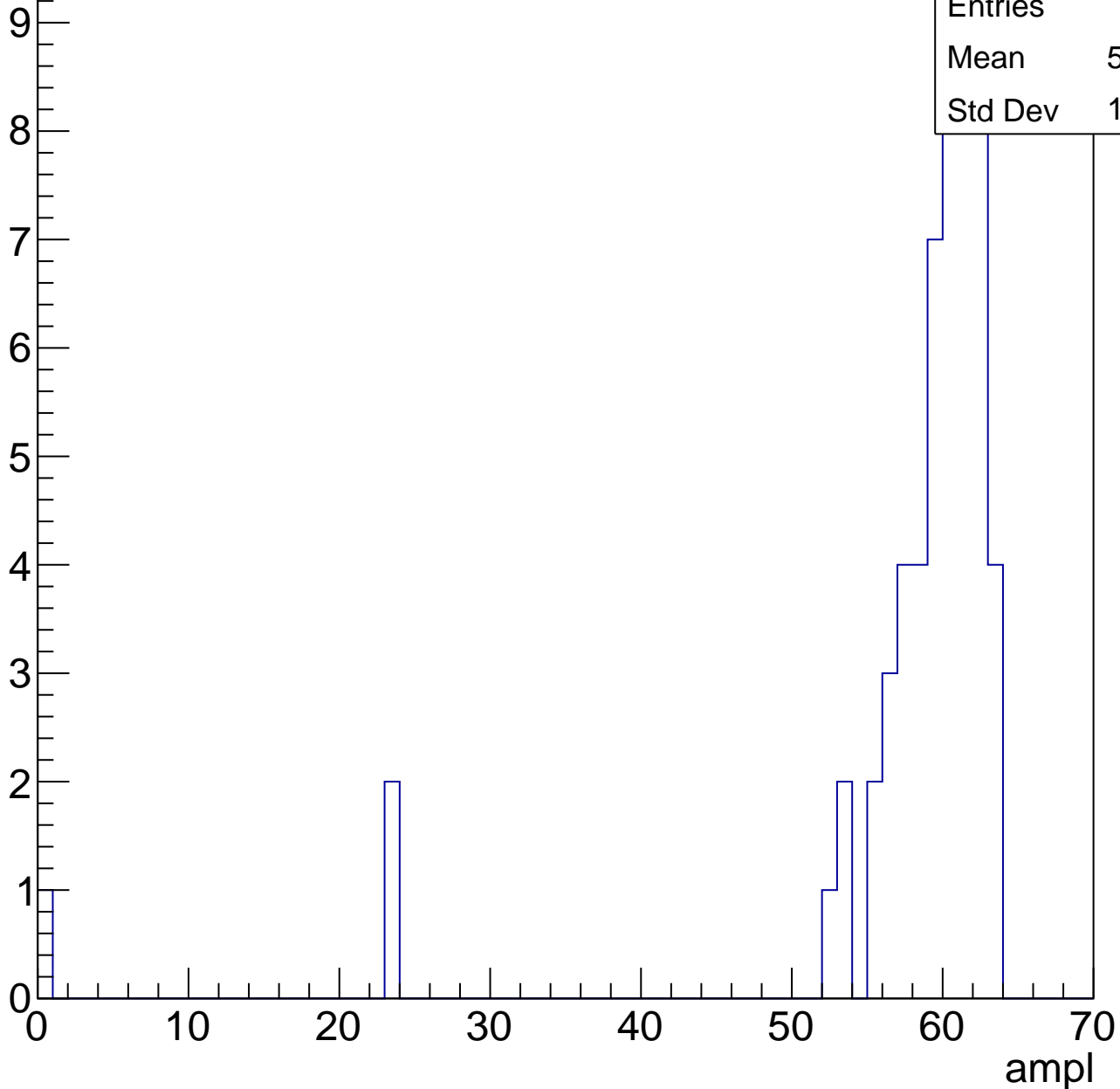


B1L103S, U8-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	56.95
Std Dev	10.64

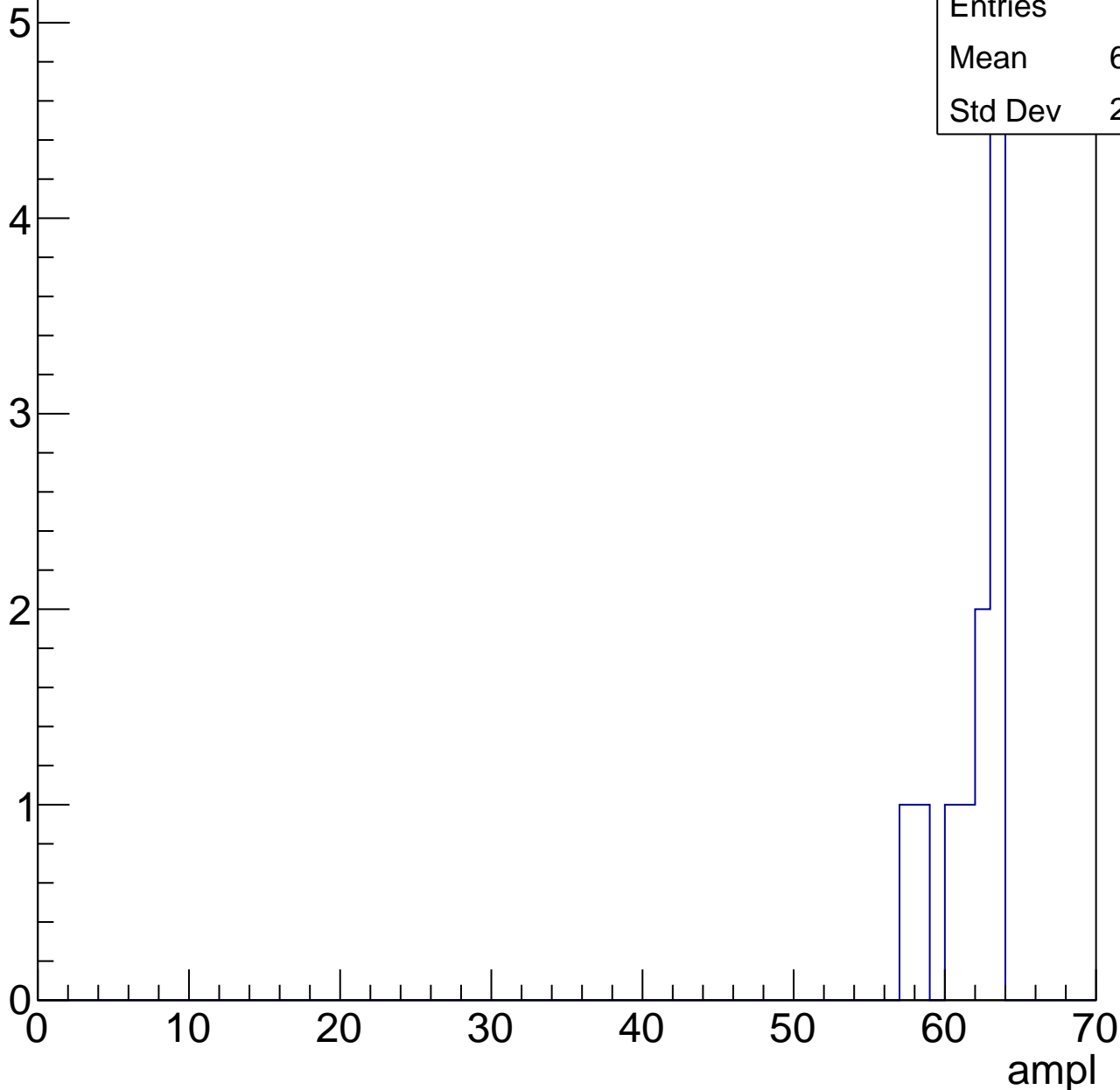


B1L103S, U8-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	61.36
Std Dev	2.057



B1L103S, U8-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U8-ch107, adc0

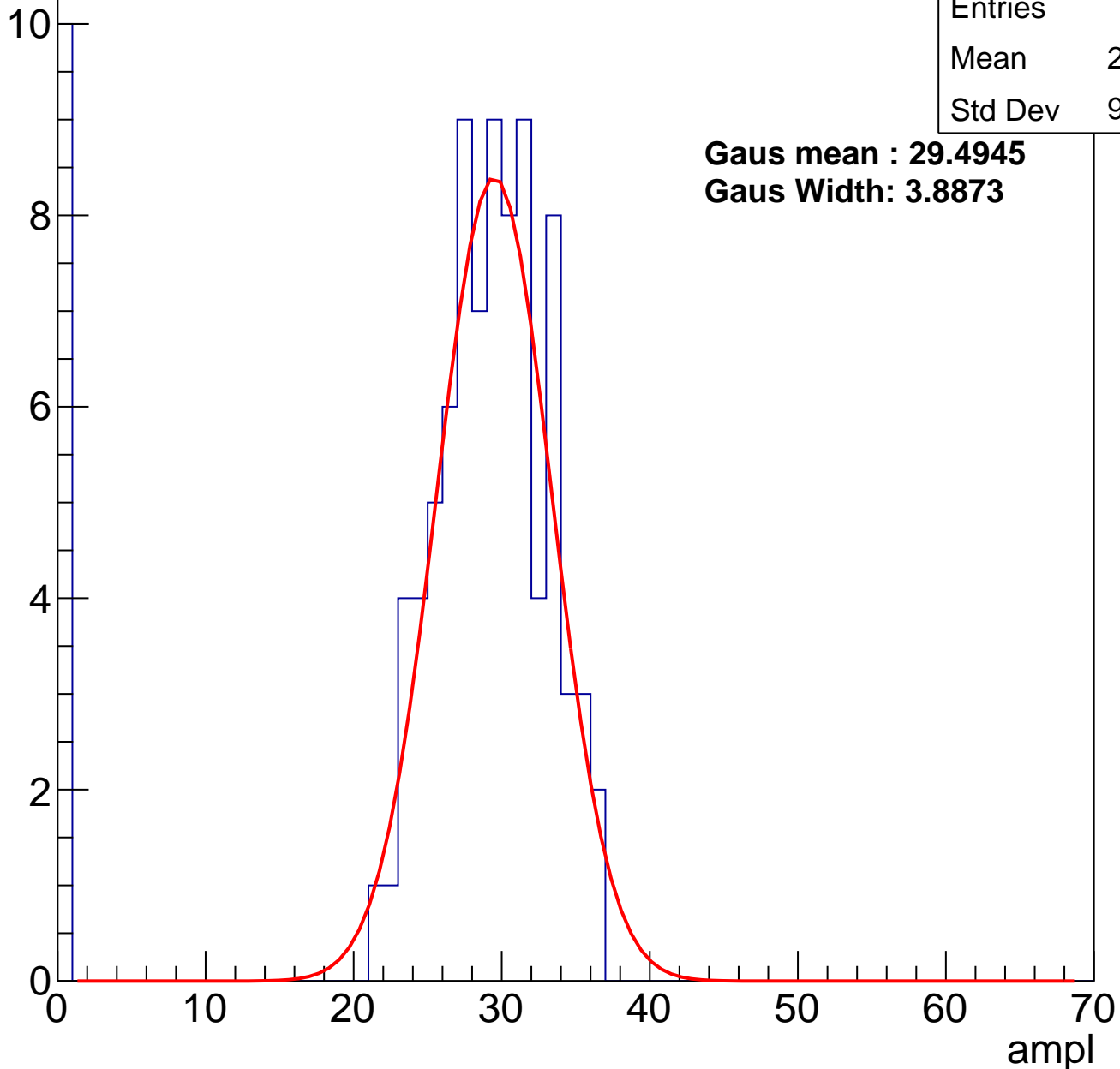
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	25.83
Std Dev	9.559

Gaus mean : 29.4945

Gaus Width: 3.8873

Entry



B1L103S, U8-ch107, adc1

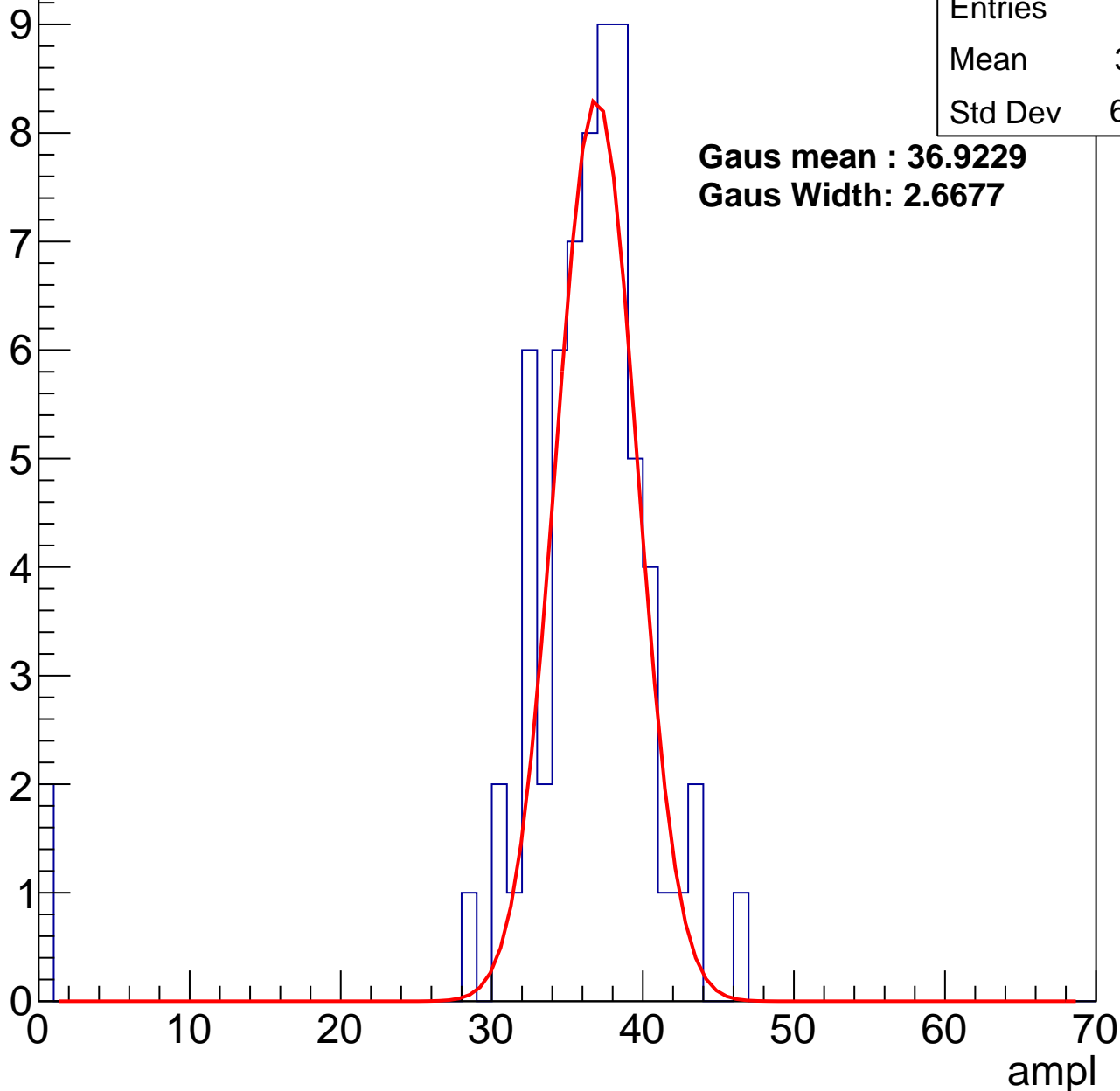
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.21
Std Dev	6.985

Gaus mean : 36.9229

Gaus Width: 2.6677



B1L103S, U8-ch107, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	41.67
Std Dev	8.763

Gaus mean : 43.7869

Gaus Width: 2.9847

Entry

10

8

6

4

2

0

0

10

20

30

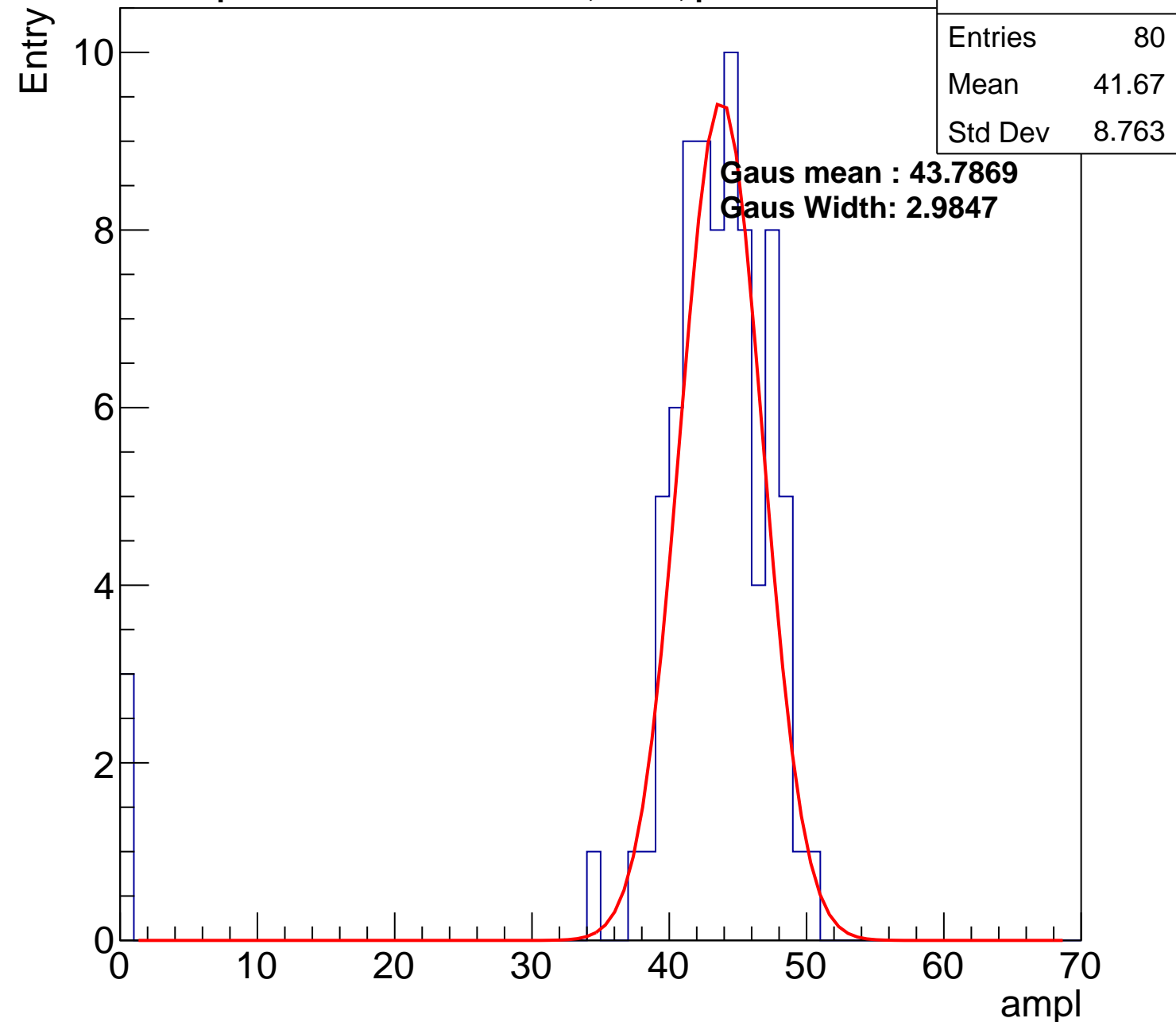
40

50

60

70

ampl

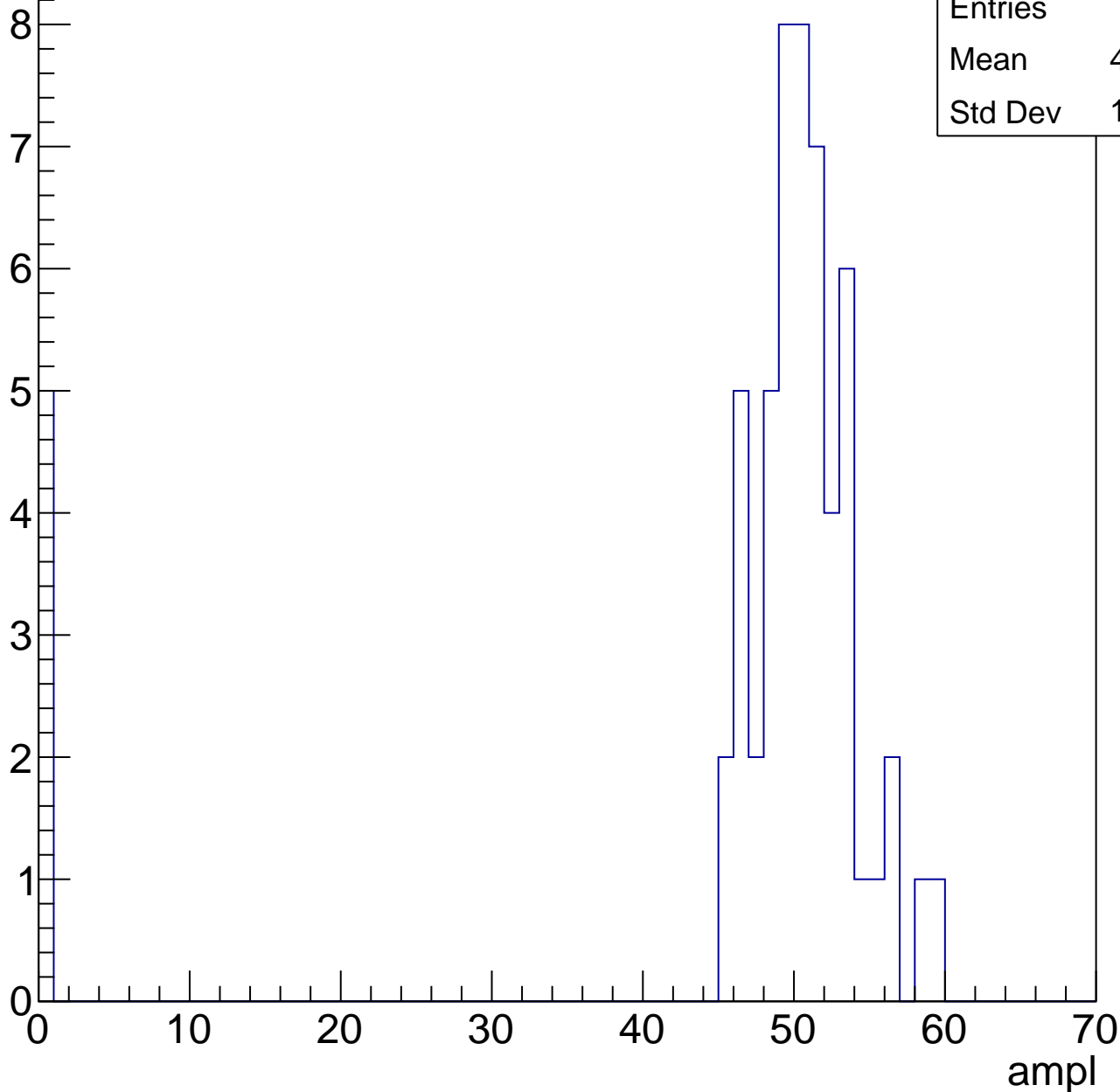


B1L103S, U8-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

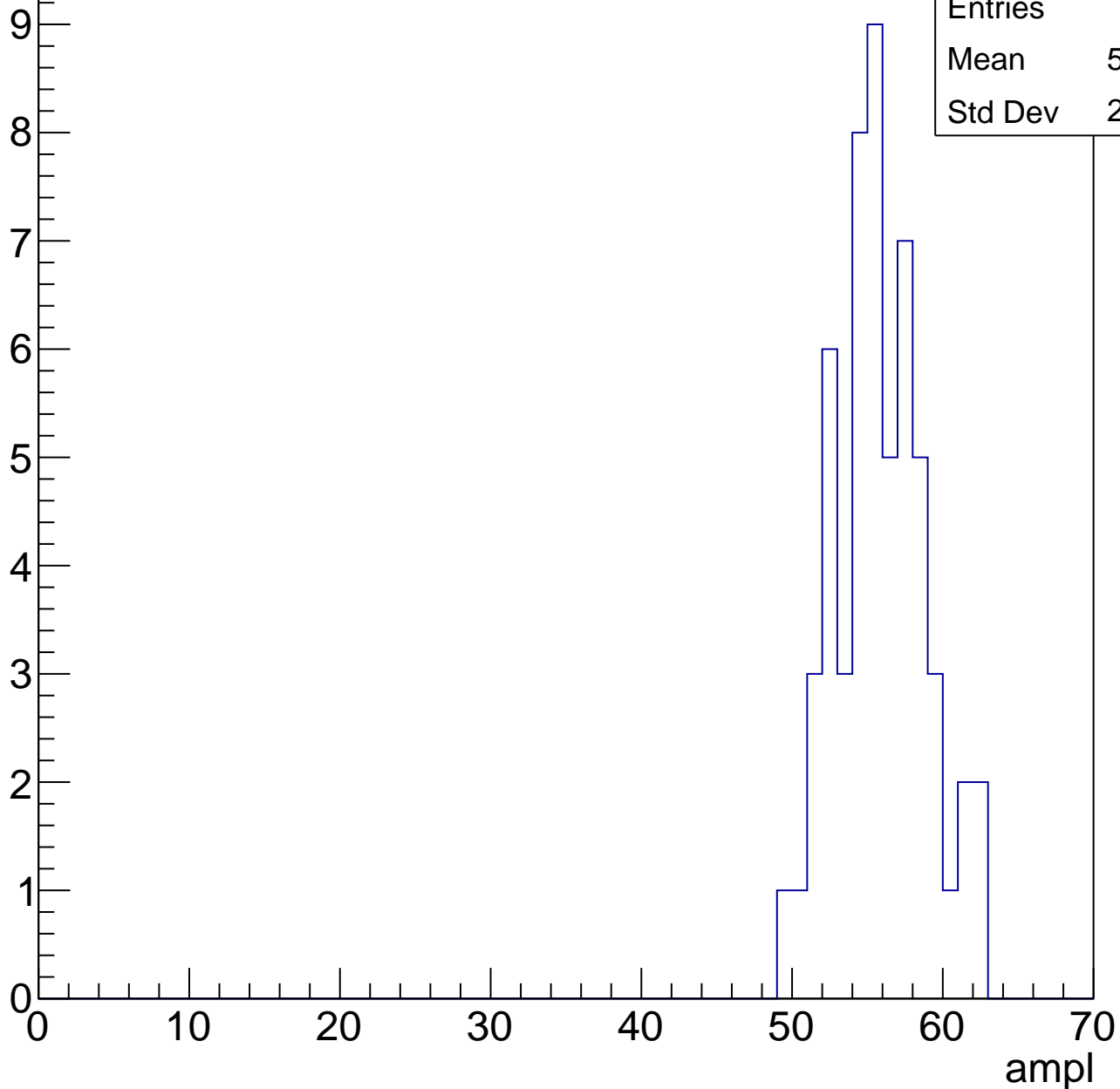
Entries	58
Mean	45.98
Std Dev	14.43



B1L103S, U8-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch107, adc5

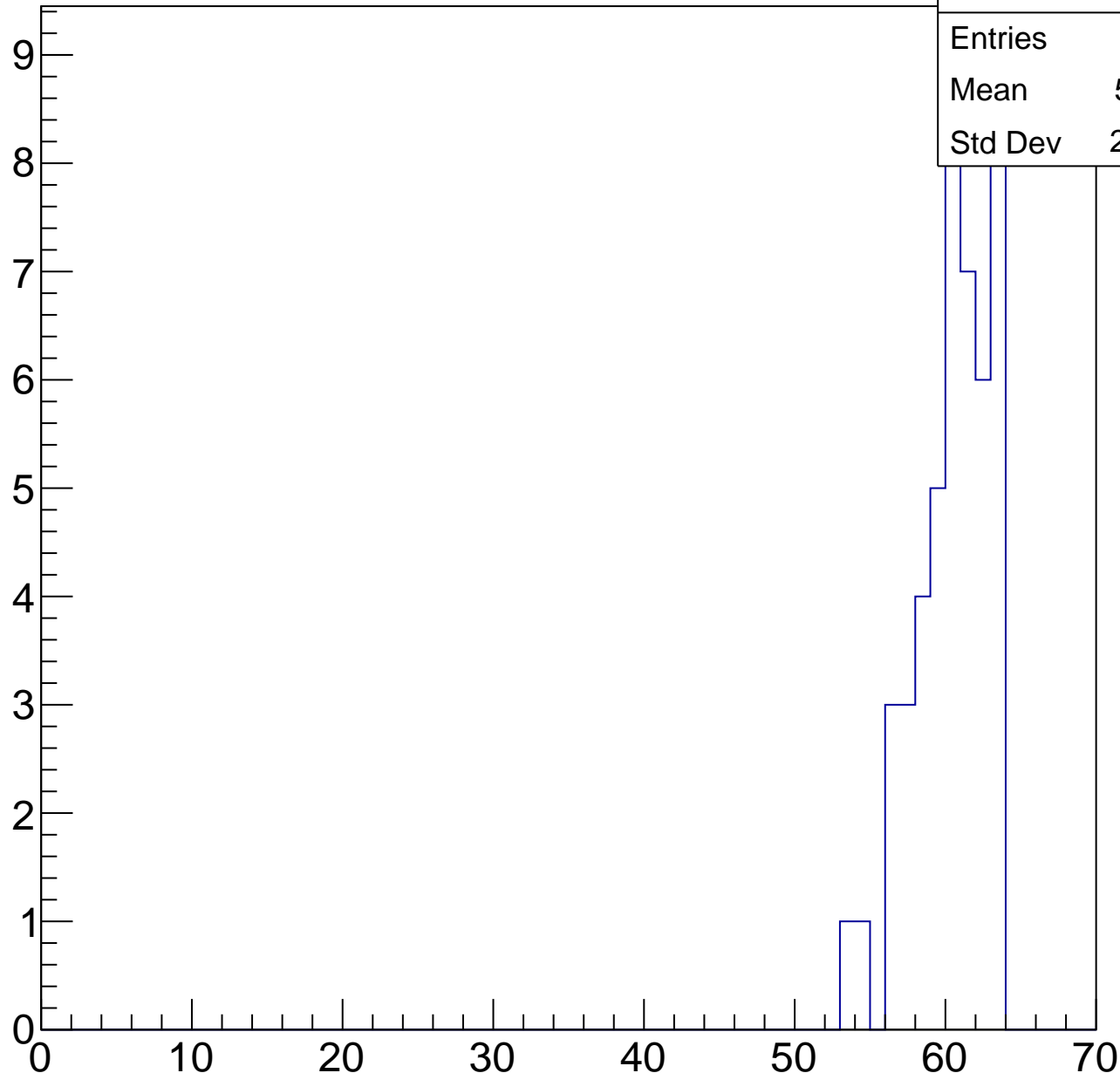
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.91
Std Dev	2.457

ampl



B1L103S, U8-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

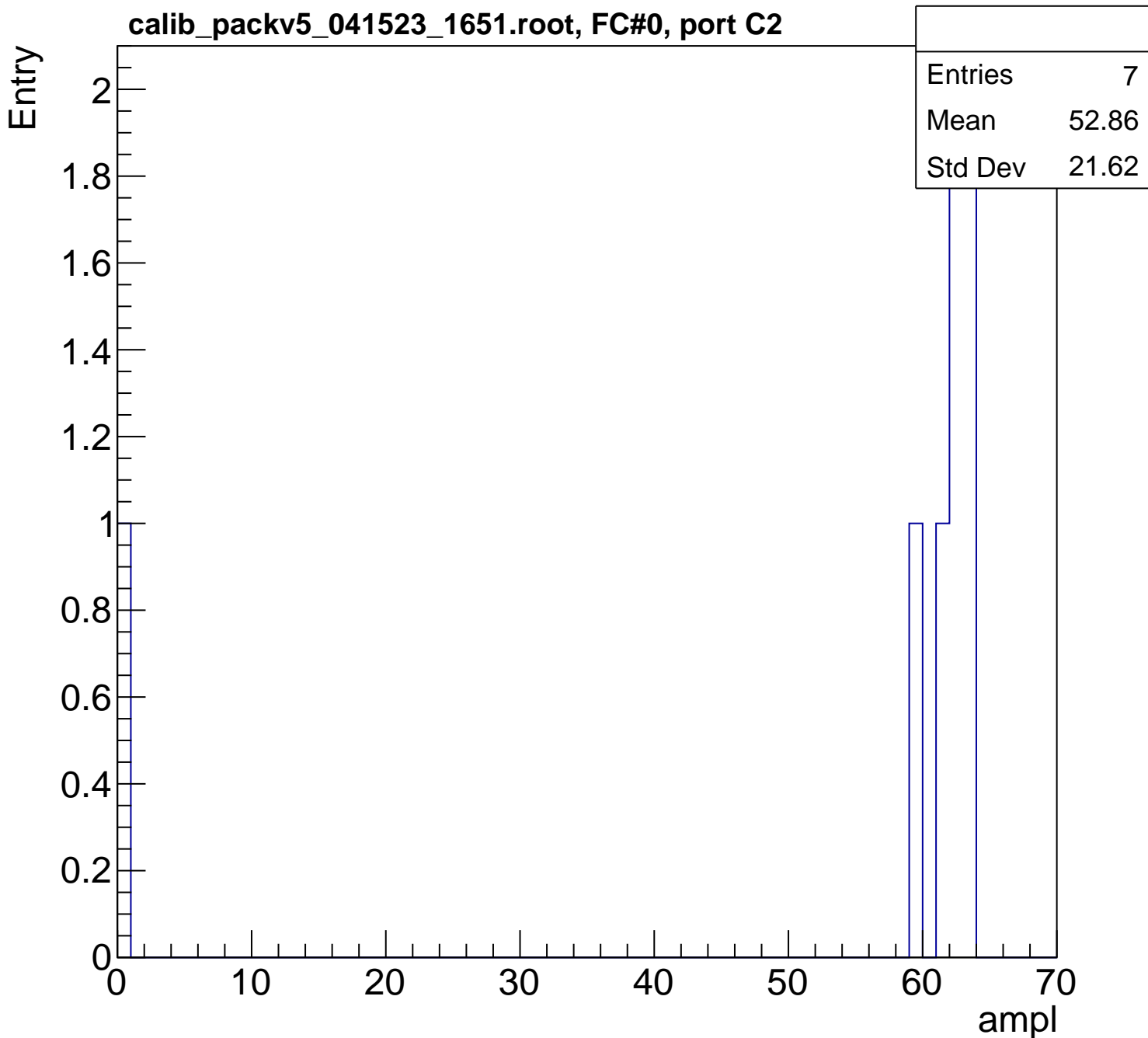
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.62

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

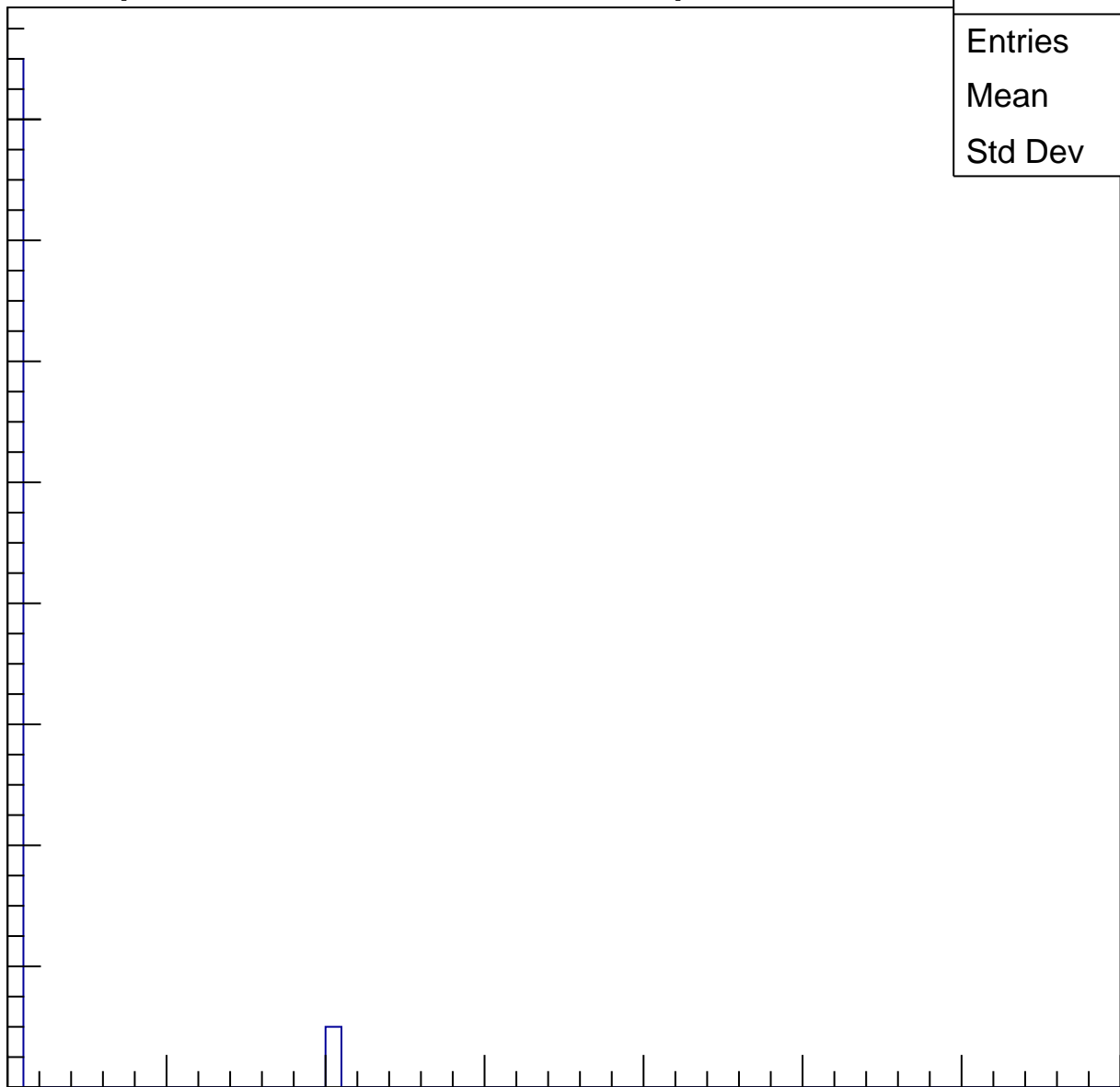
Entries	18
Mean	1.111
Std Dev	4.581

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch108, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	22.28
Std Dev	11.39

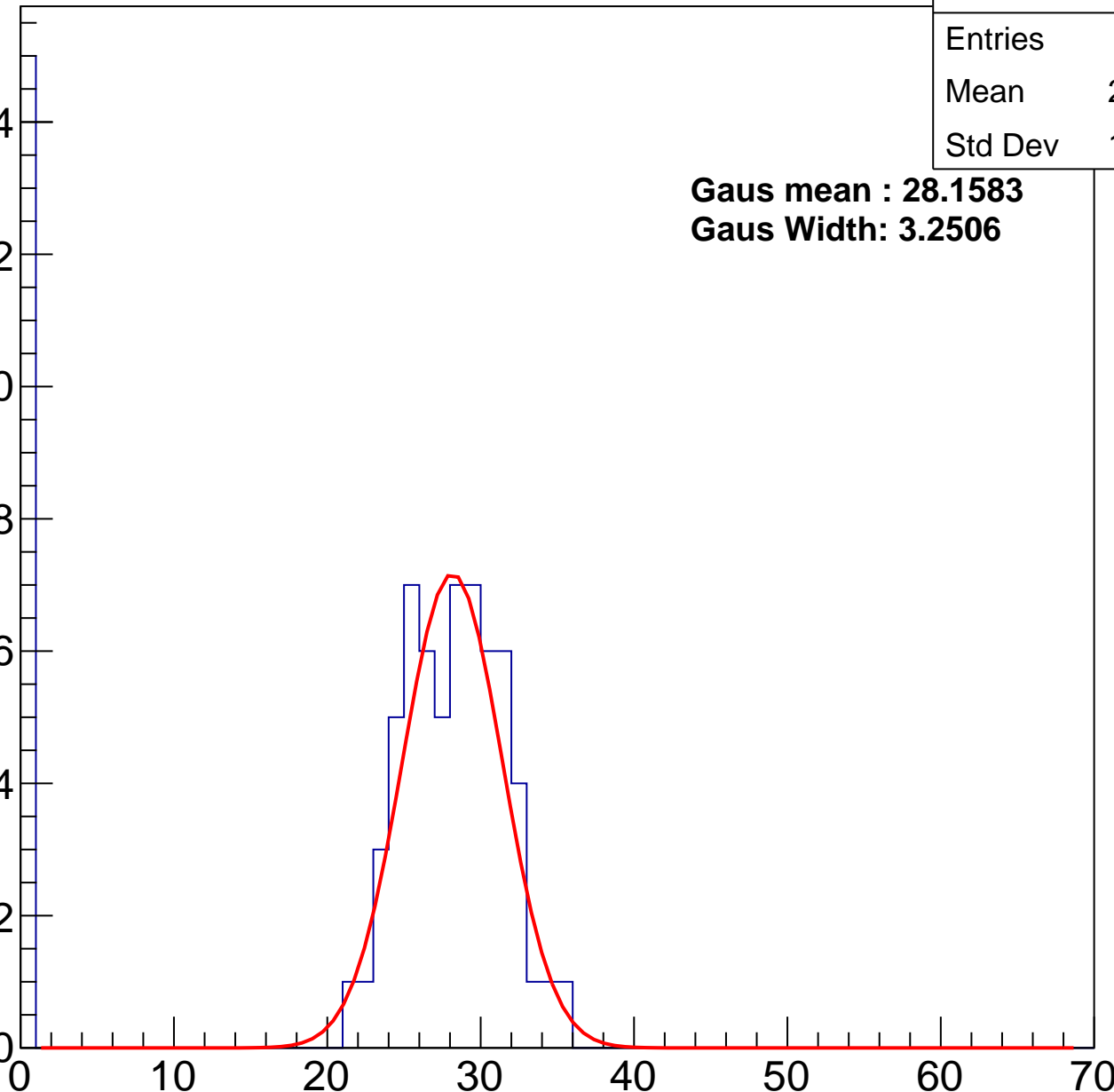
Gaus mean : 28.1583

Gaus Width: 3.2506

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U8-ch108, adc1

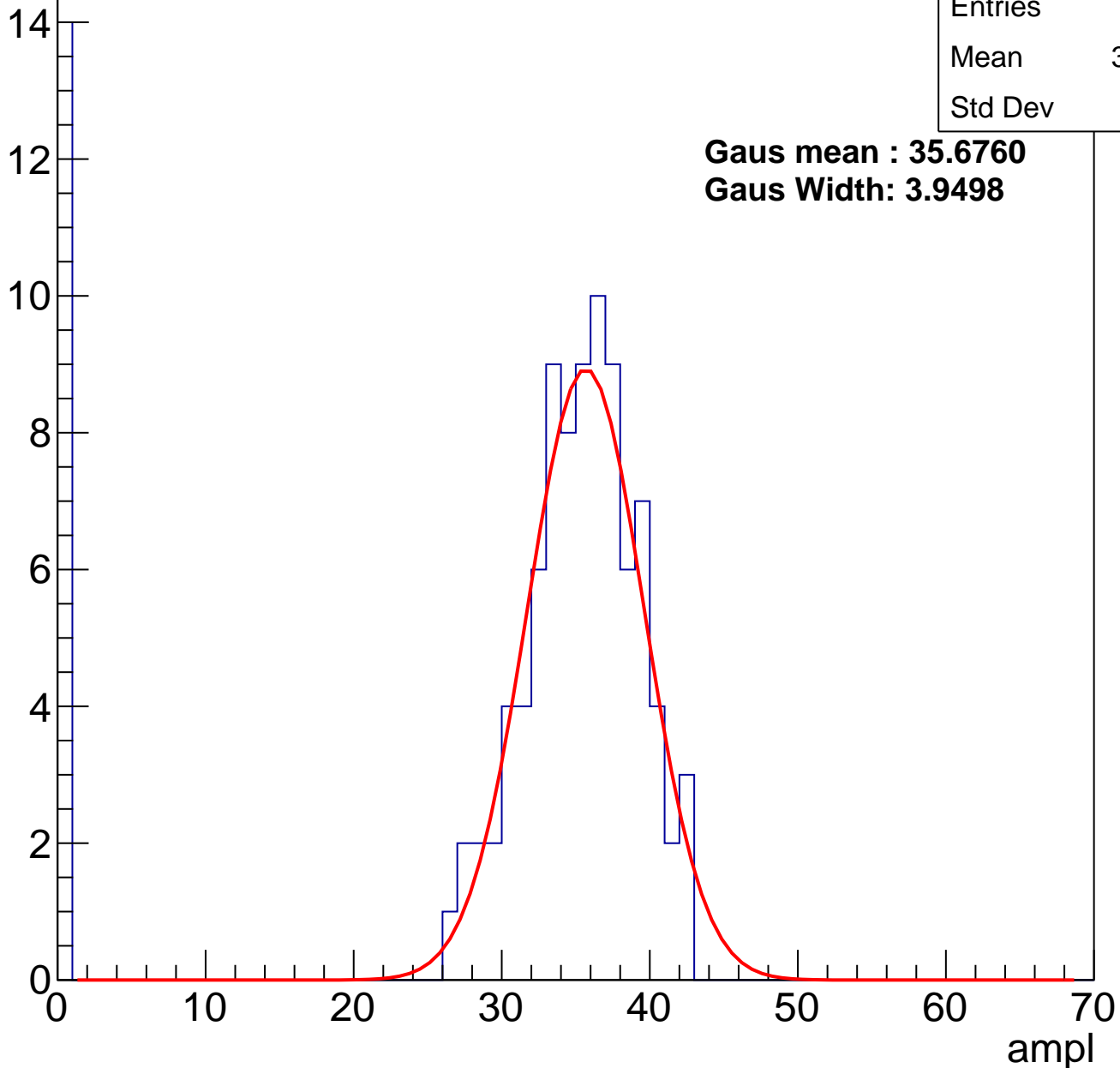
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	30.16
Std Dev	12.5

Gaus mean : 35.6760

Gaus Width: 3.9498

Entry



B1L103S, U8-ch108, adc2

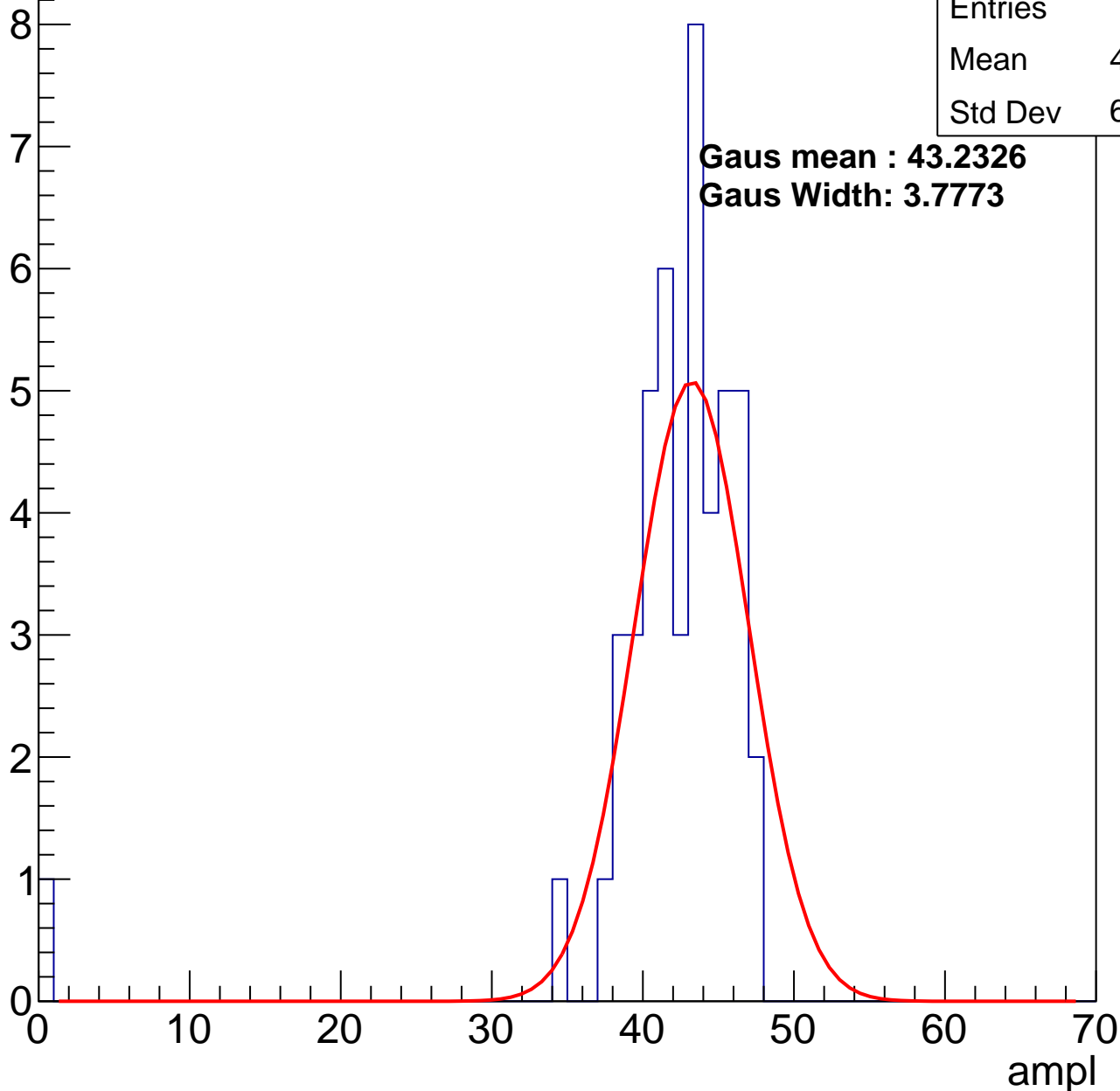
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	41.34
Std Dev	6.736

Gaus mean : 43.2326

Gaus Width: 3.7773

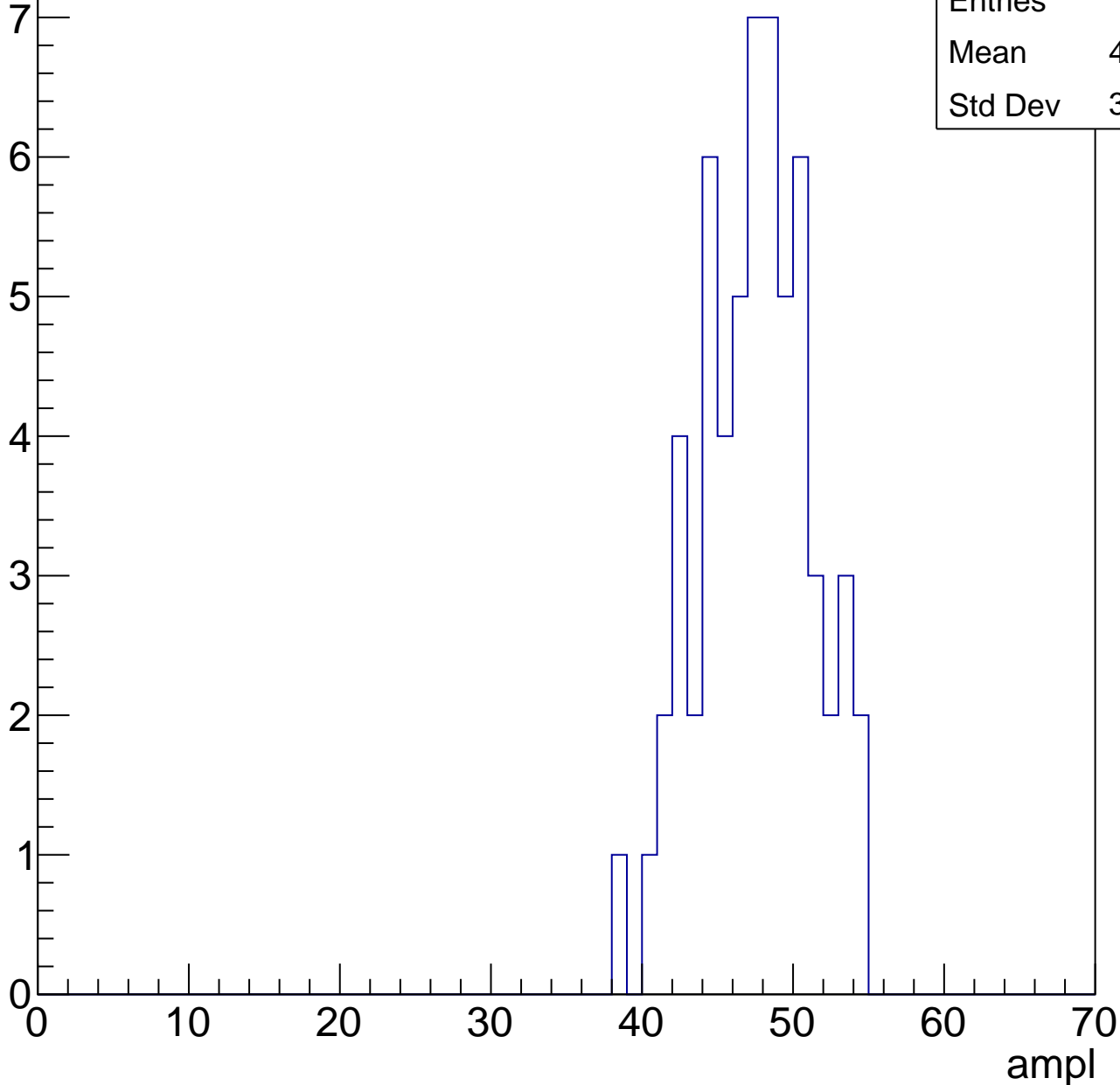


B1L103S, U8-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	47.03
Std Dev	3.638

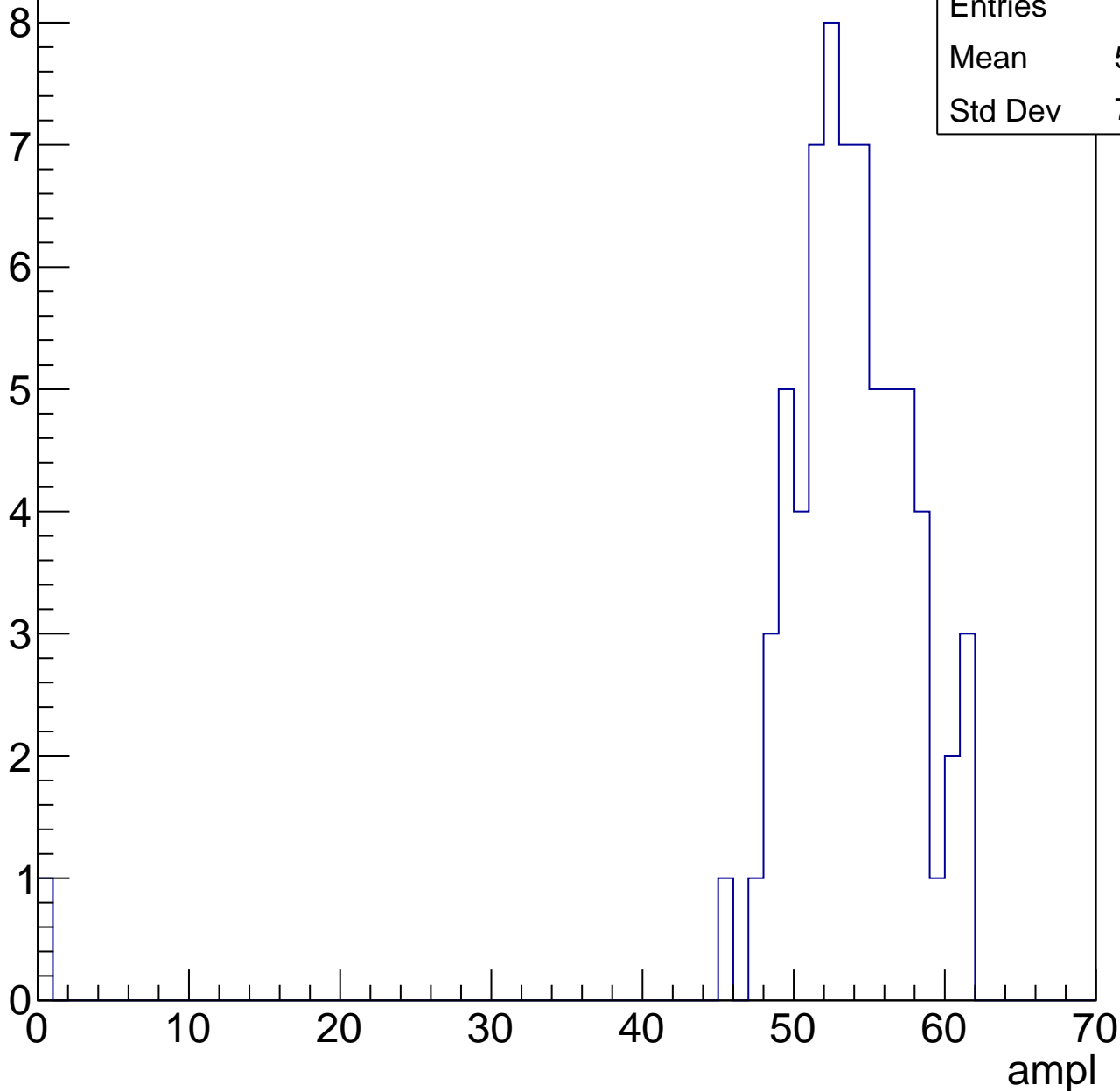


B1L103S, U8-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	52.71
Std Dev	7.331

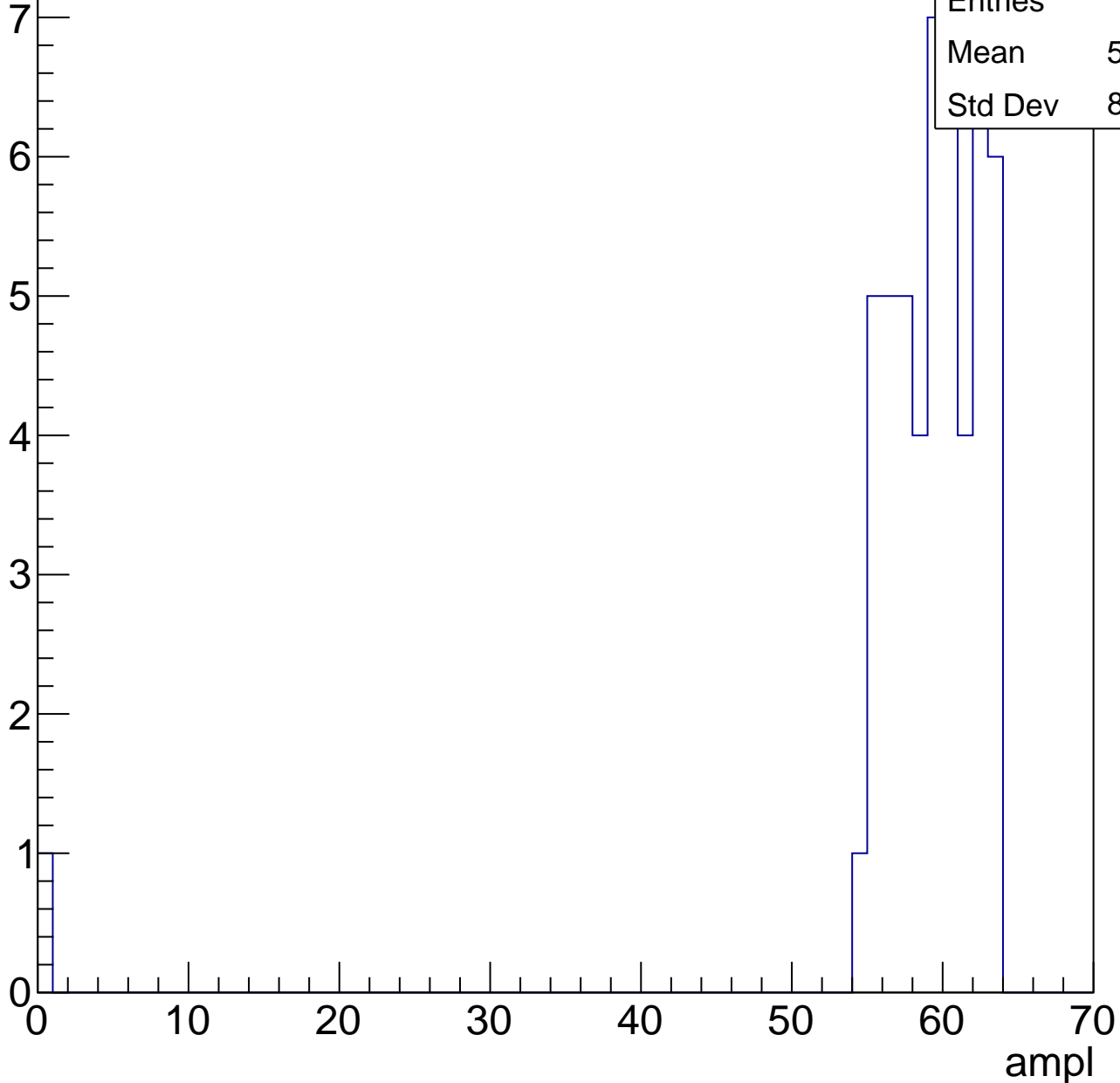


B1L103S, U8-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

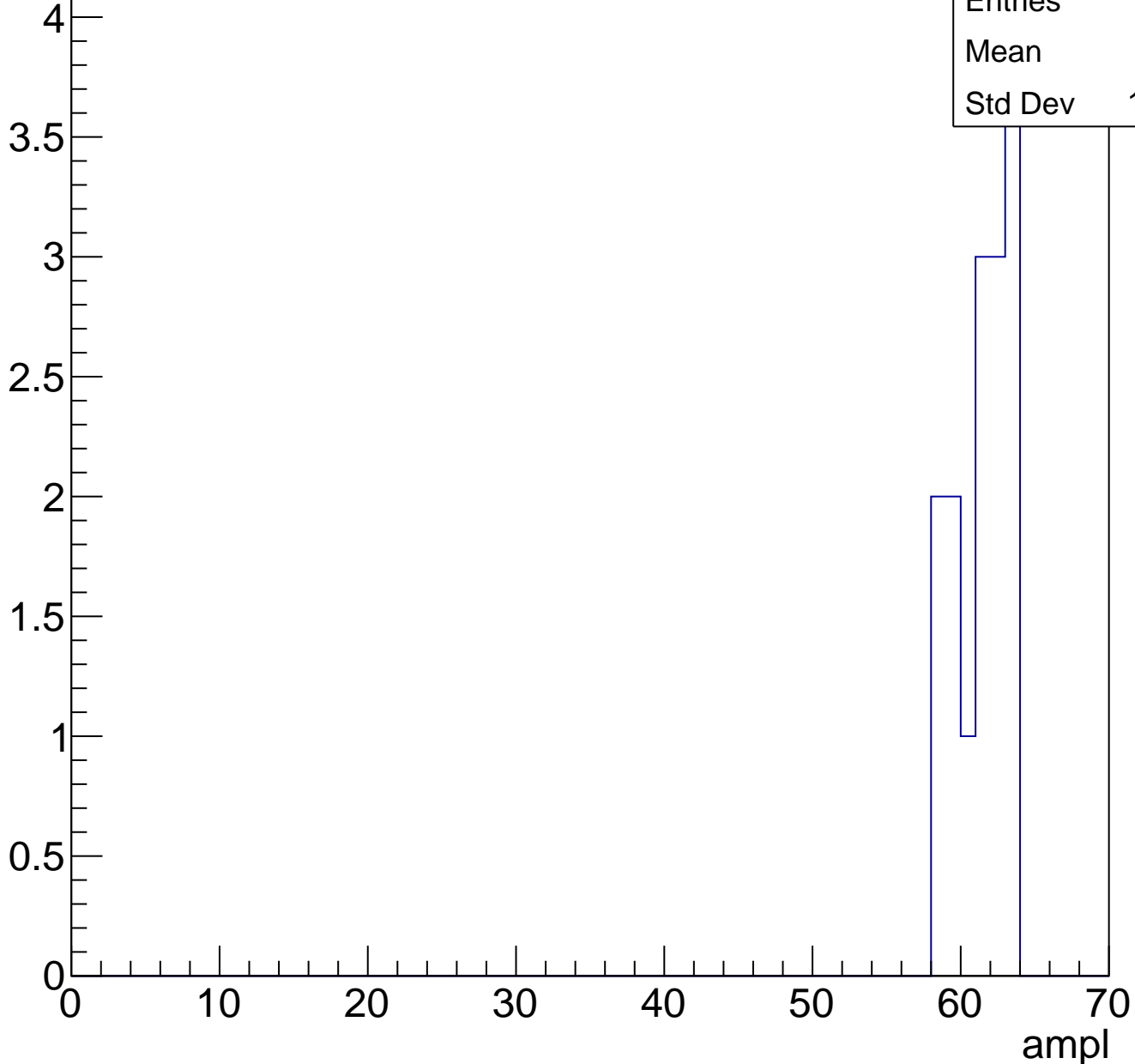
Entries	52
Mean	57.98
Std Dev	8.529



B1L103S, U8-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

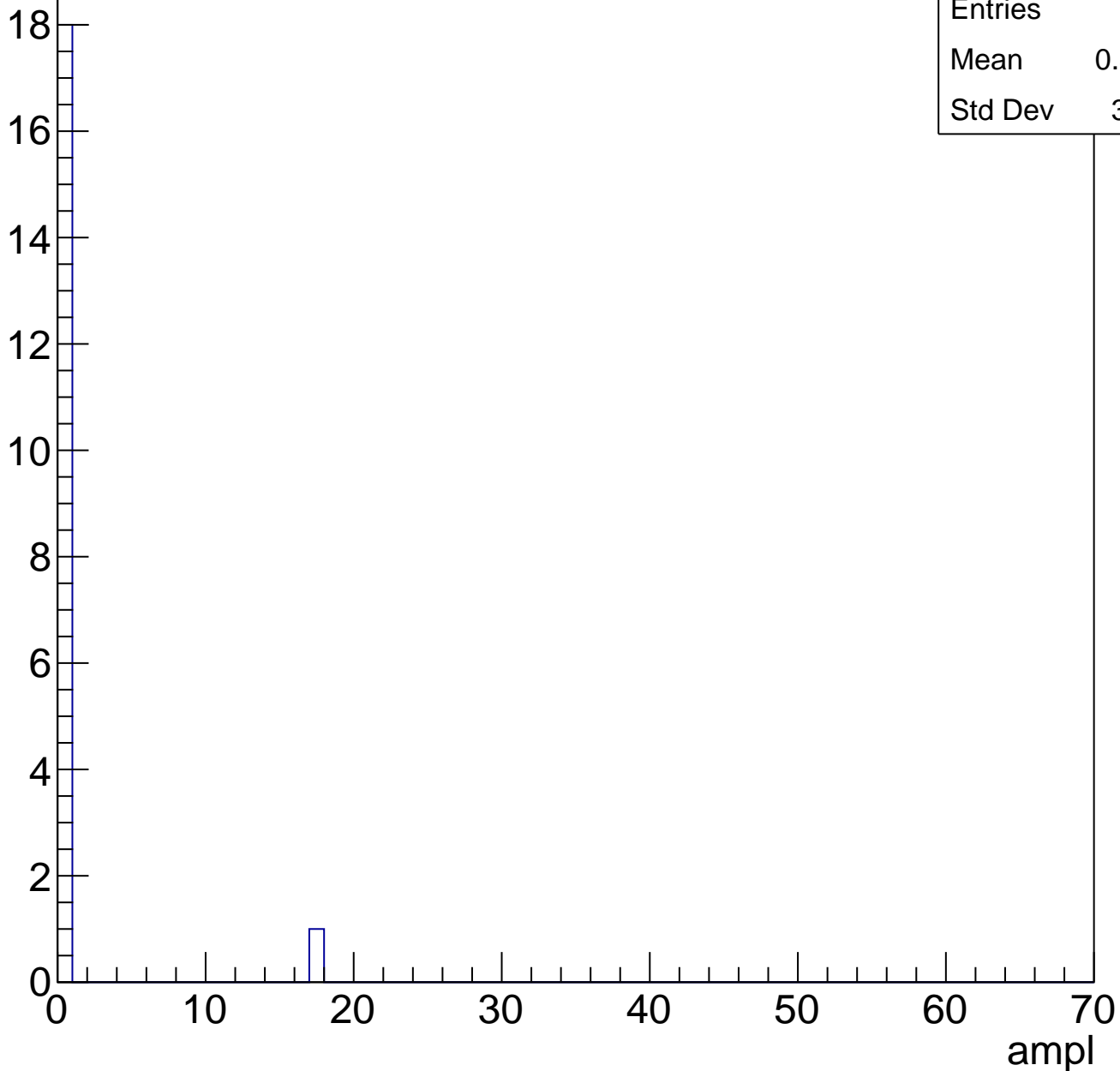


B1L103S, U8-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0.8947
Std Dev	3.796

Entry



B1L103S, U8-ch109, adc0

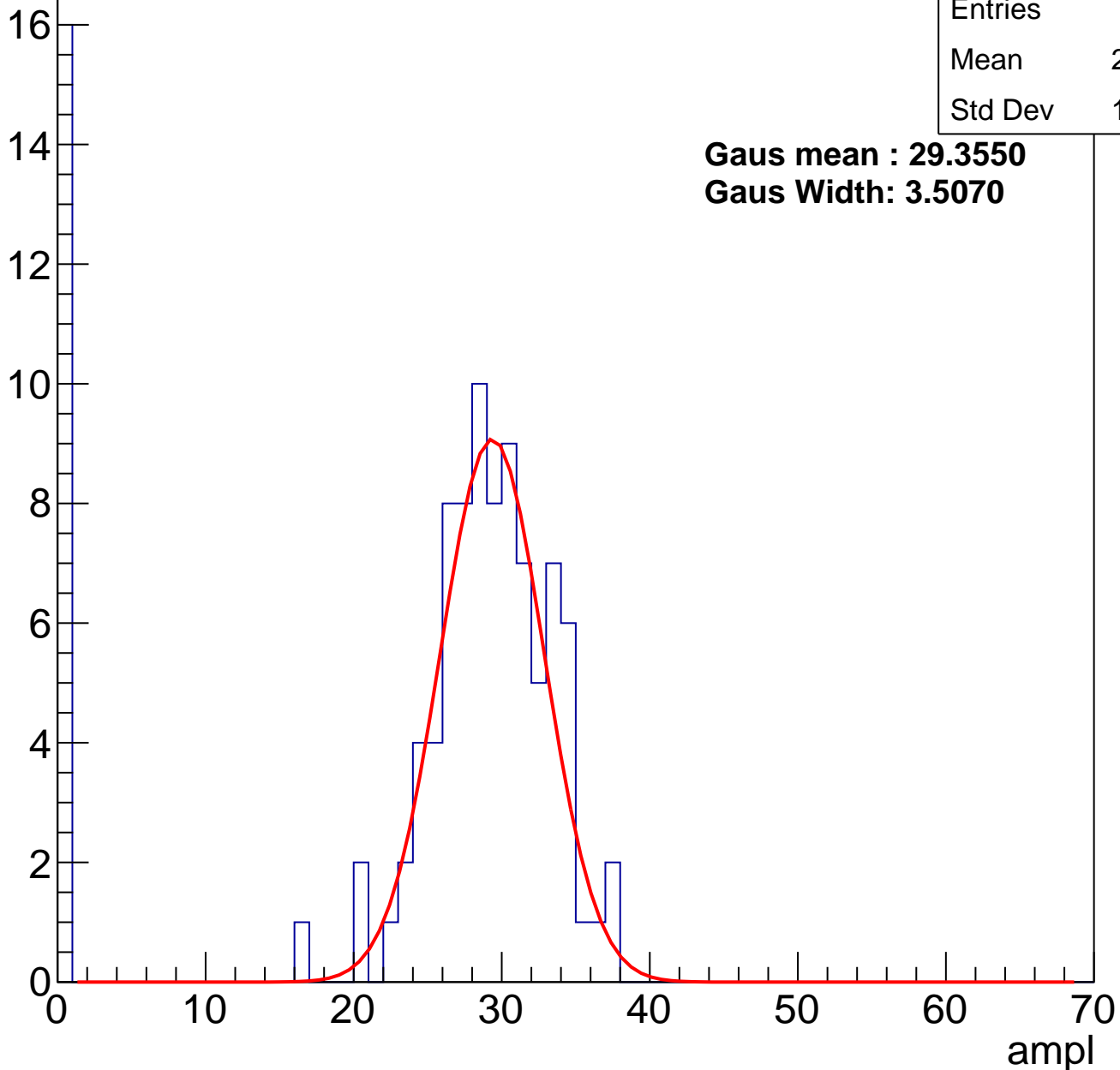
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	24.34
Std Dev	11.08

Gaus mean : 29.3550

Gaus Width: 3.5070

Entry



B1L103S, U8-ch109, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	30.22
Std Dev	13.73

Gaus mean : 35.3592

Gaus Width: 4.6230

Entry

10

8

6

4

2

0

0

10

20

30

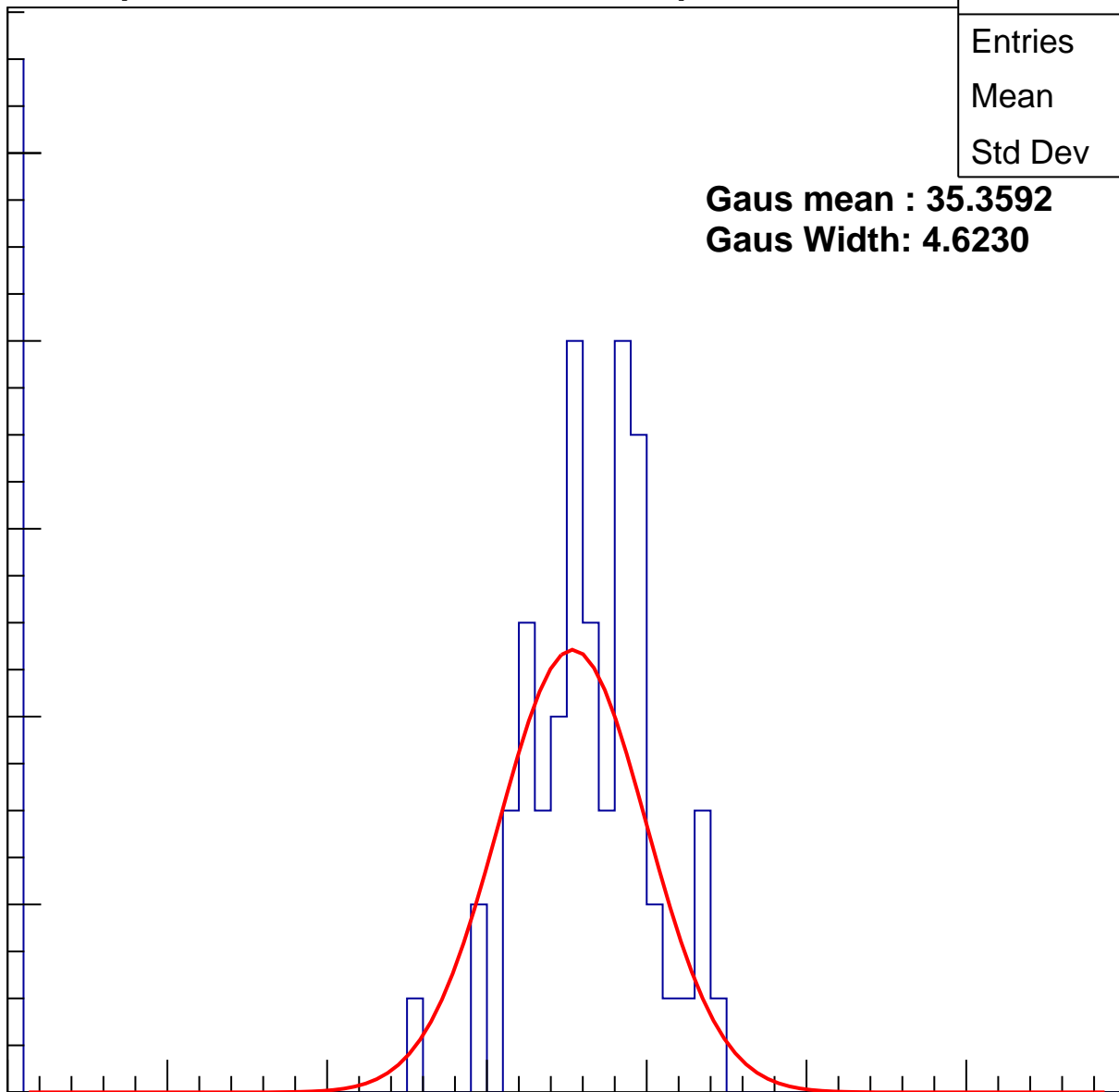
40

50

60

70

ampl



B1L103S, U8-ch109, adc2

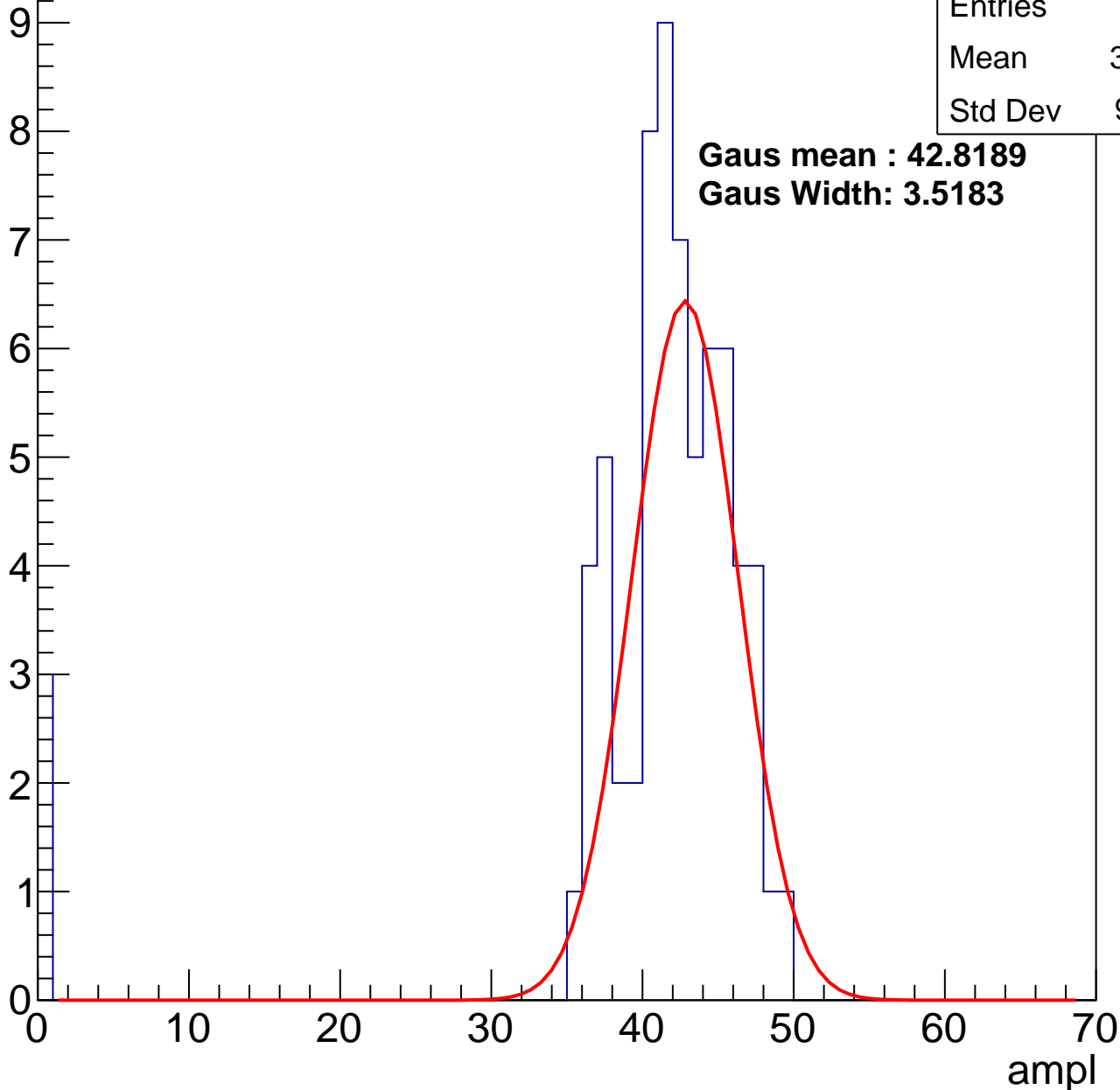
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	39.99
Std Dev	9.201

Gaus mean : 42.8189

Gaus Width: 3.5183

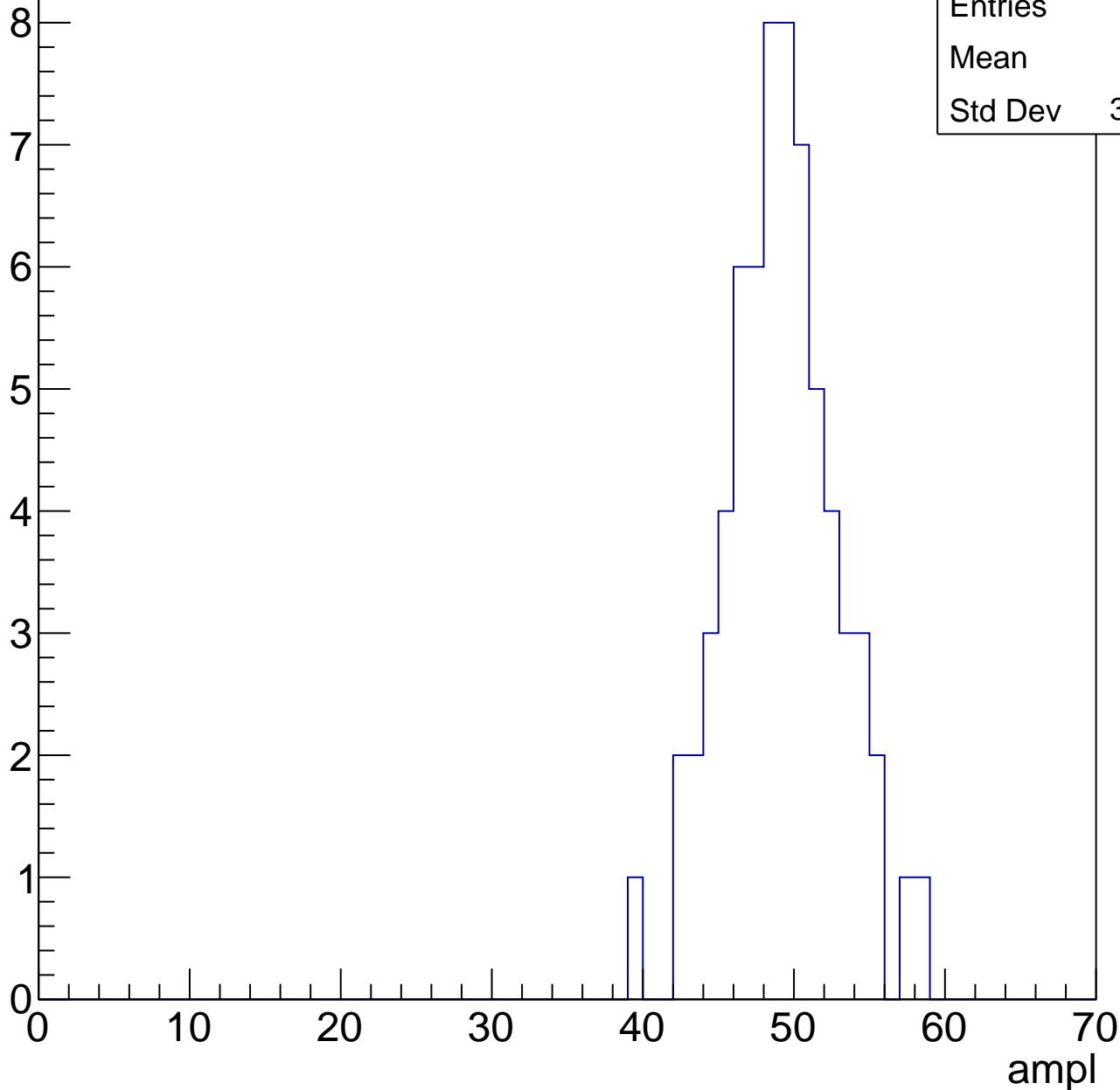


B1L103S, U8-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

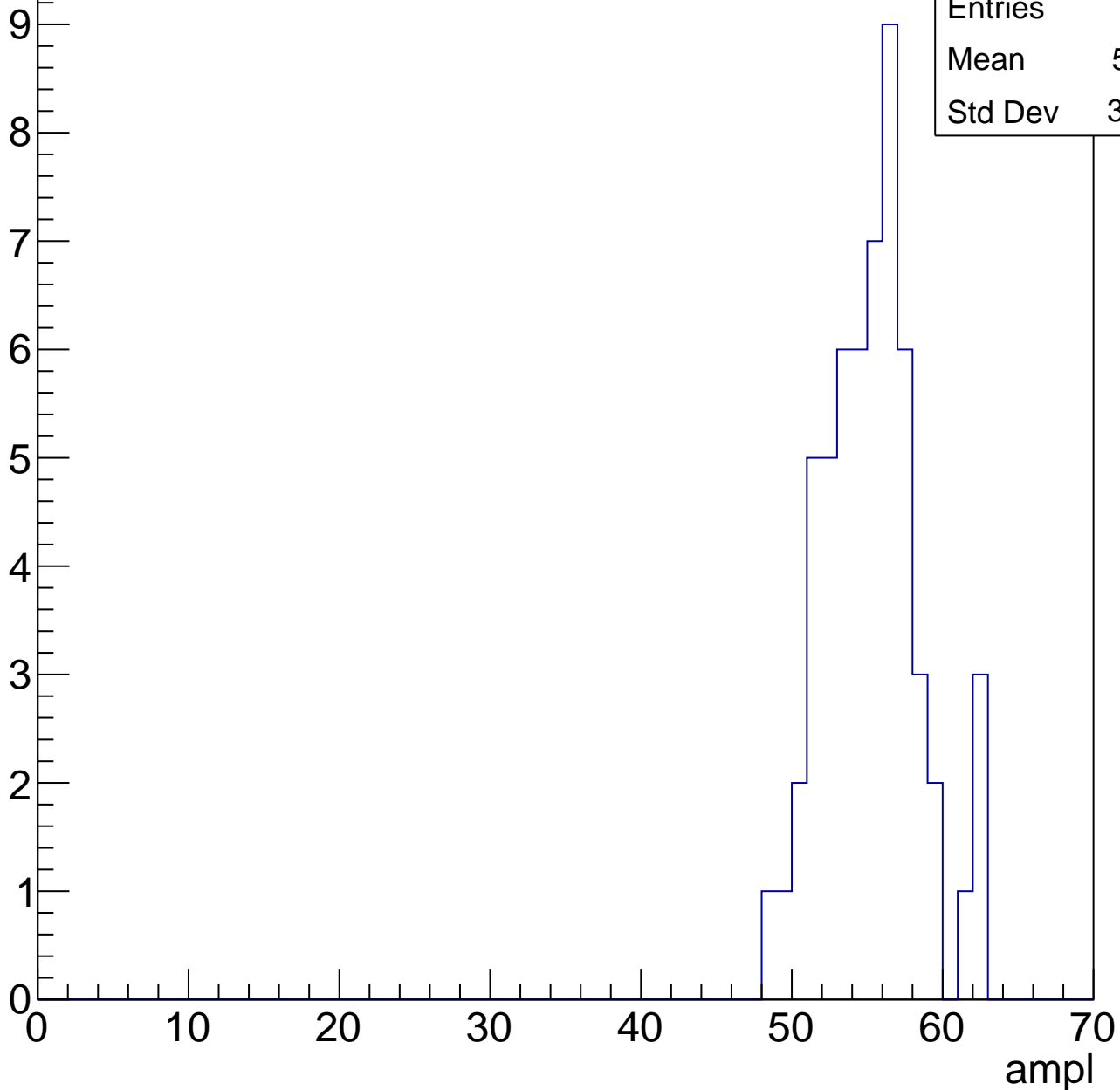
Entries	66
Mean	48.7
Std Dev	3.676



B1L103S, U8-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

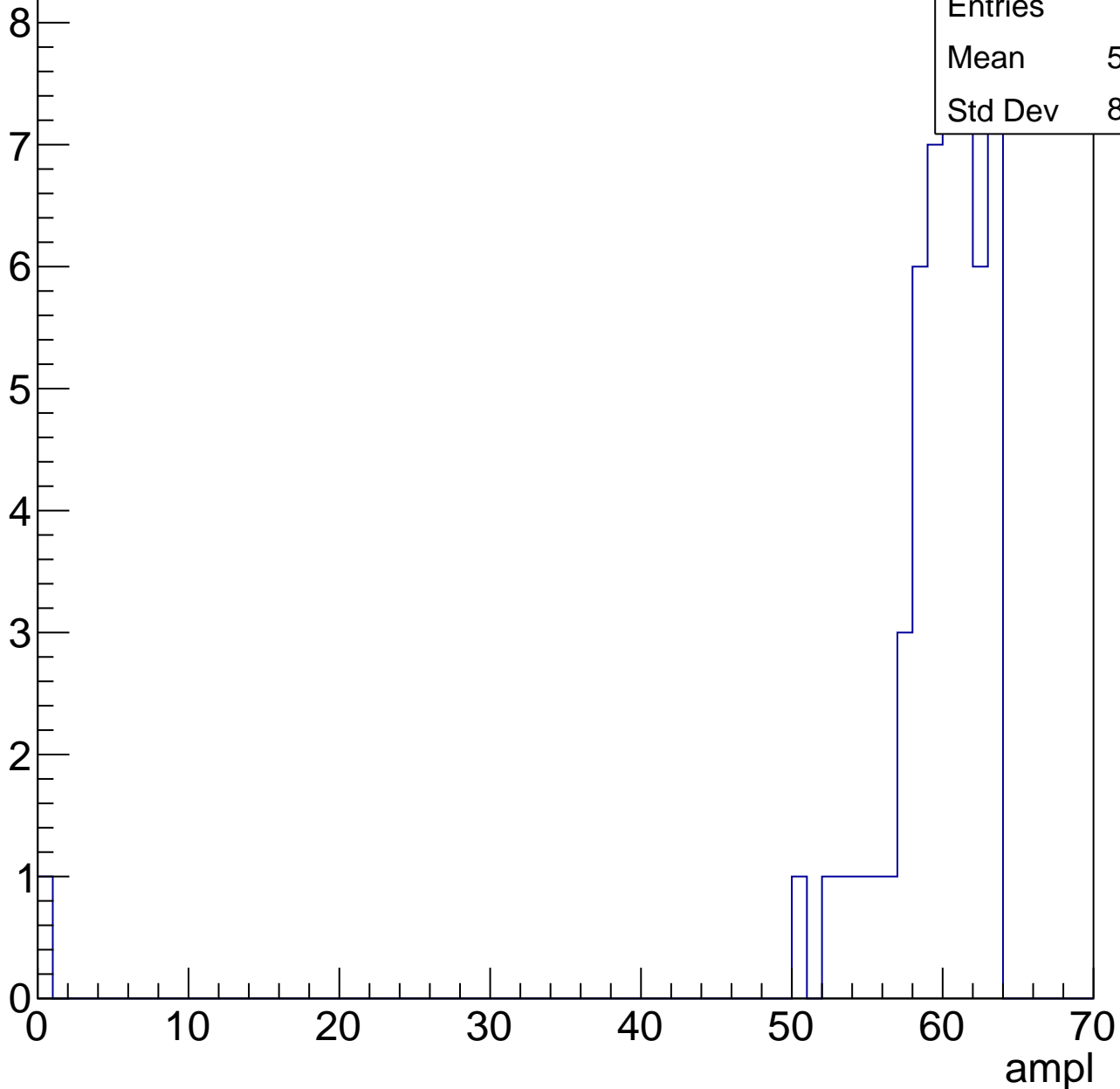


B1L103S, U8-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.42
Std Dev	8.599



B1L103S, U8-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

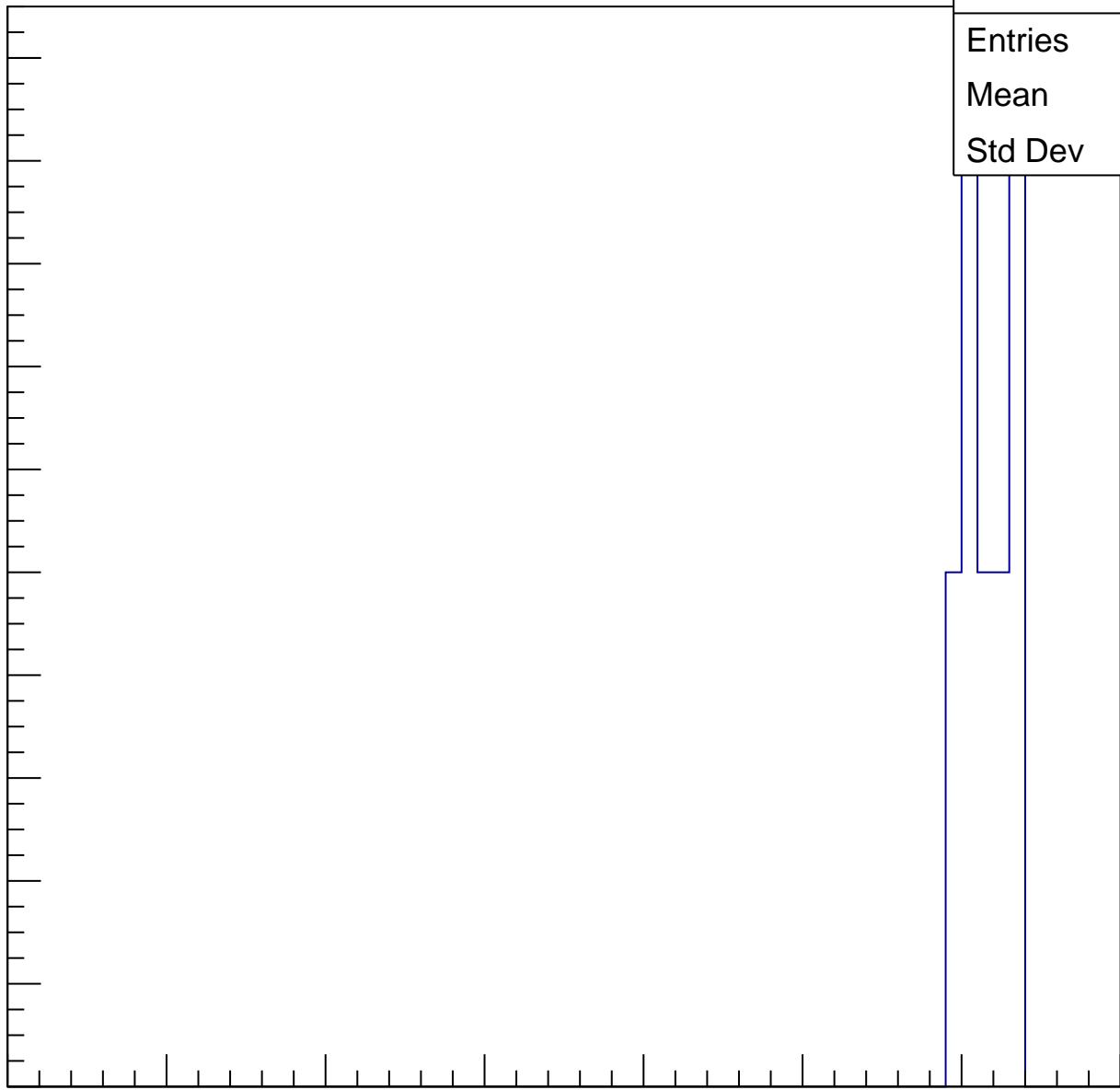
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.14
Std Dev	1.457

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch110, adc0

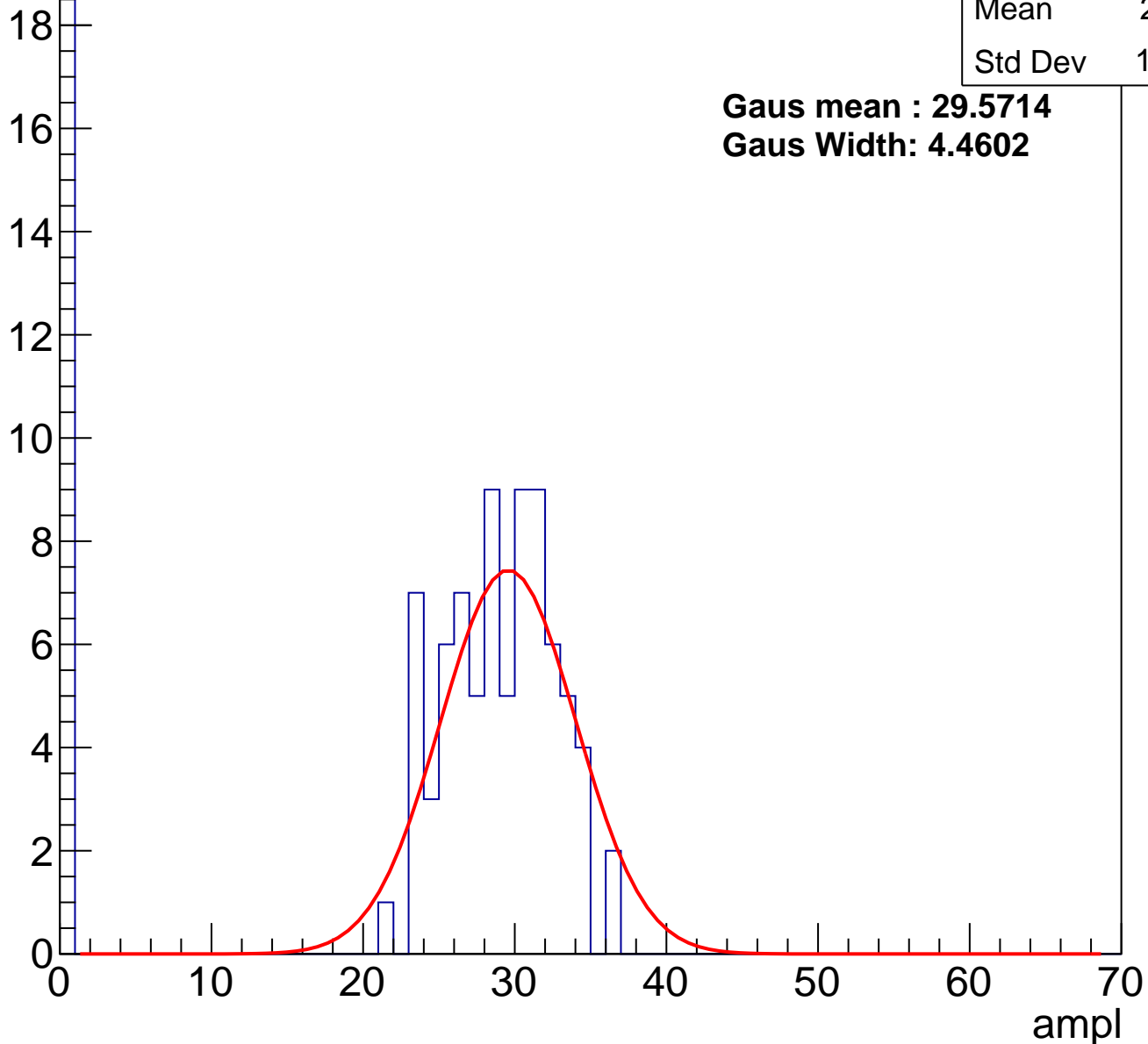
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	23.01
Std Dev	11.78

Gaus mean : 29.5714

Gaus Width: 4.4602

Entry



B1L103S, U8-ch110, adc1

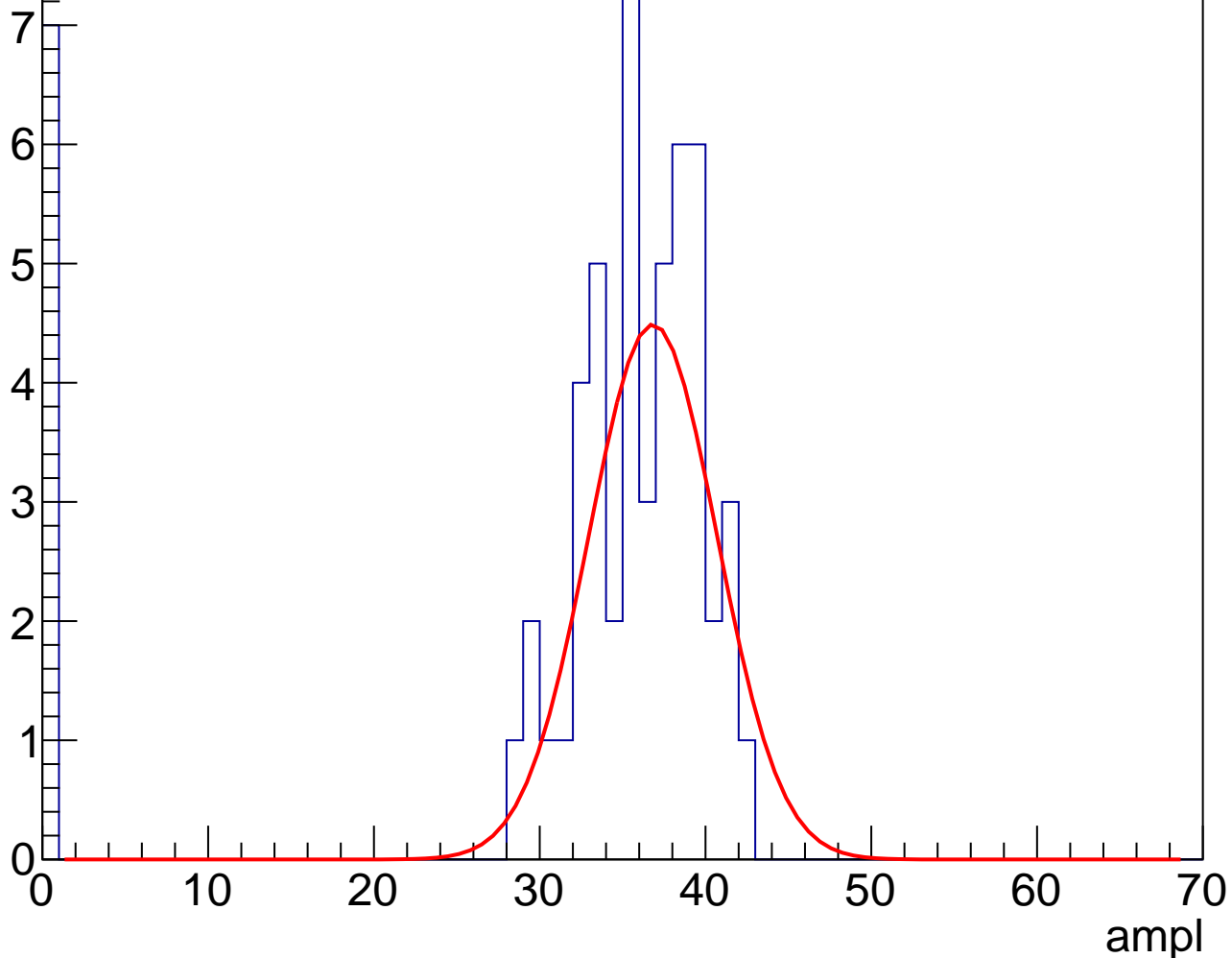
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	31.43
Std Dev	12.06

Gaus mean : 36.8285

Gaus Width: 3.8626



B1L103S, U8-ch110, adc2

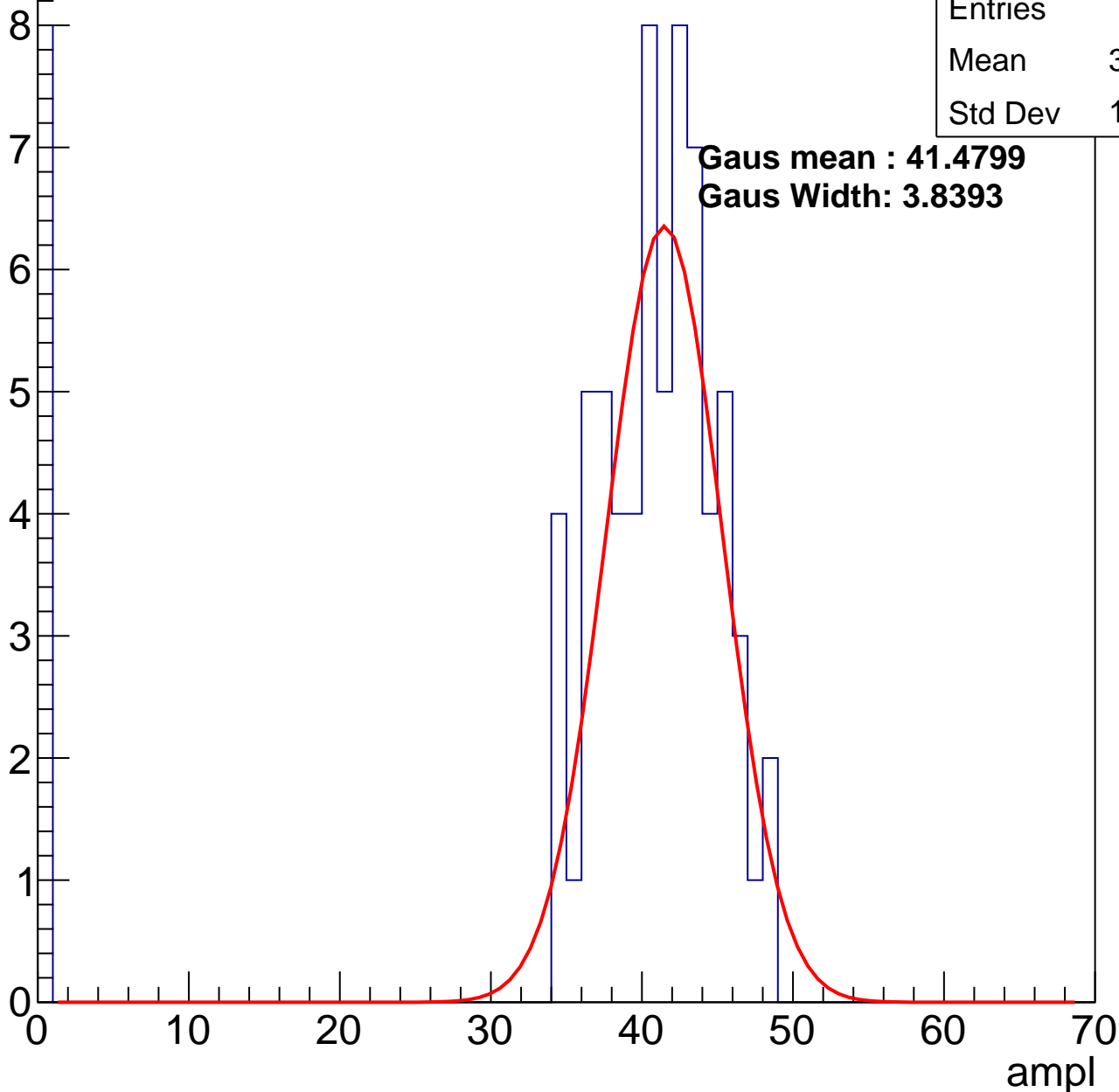
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.32
Std Dev	13.09

Gaus mean : 41.4799

Gaus Width: 3.8393

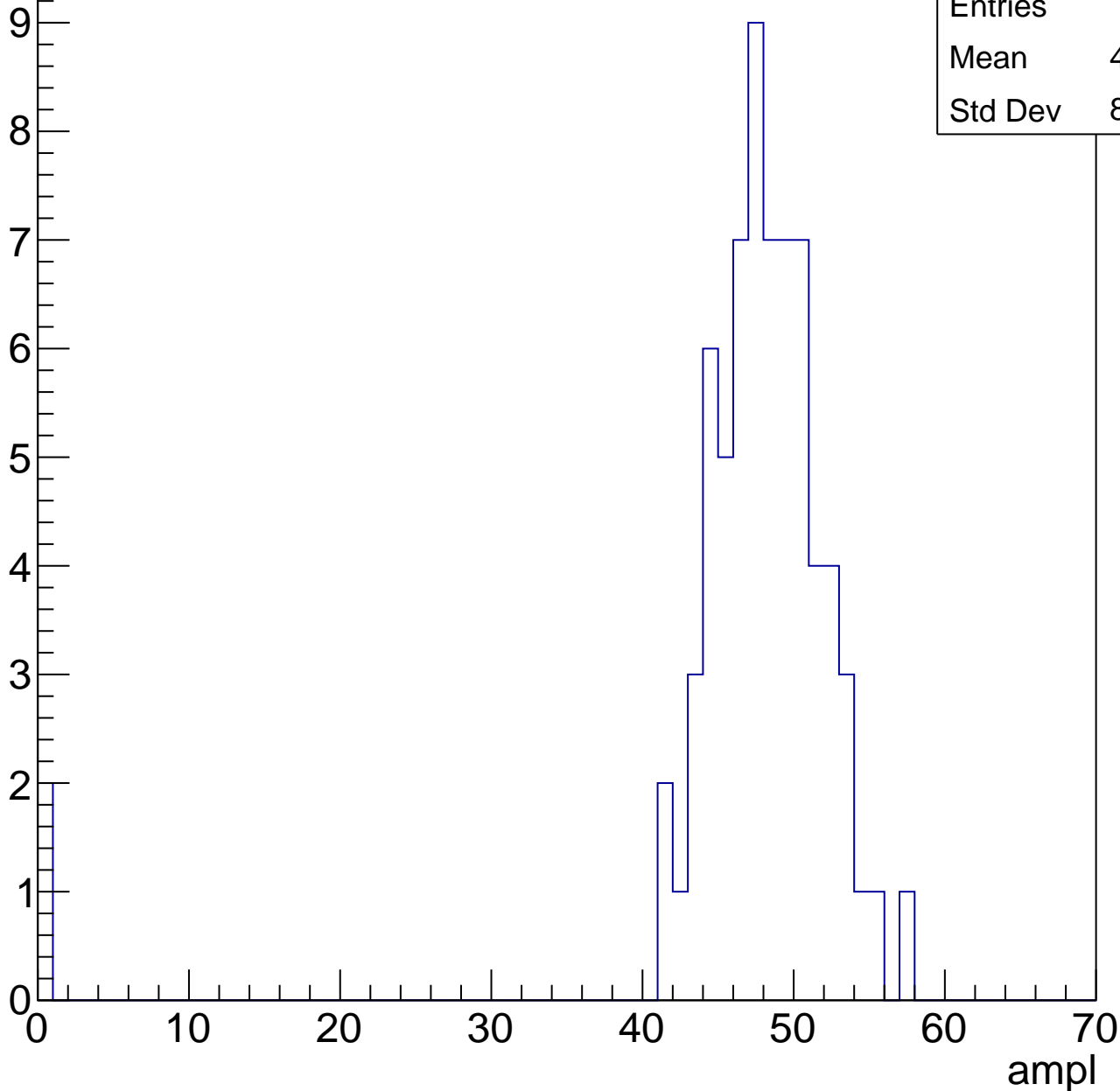


B1L103S, U8-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	46.47
Std Dev	8.625

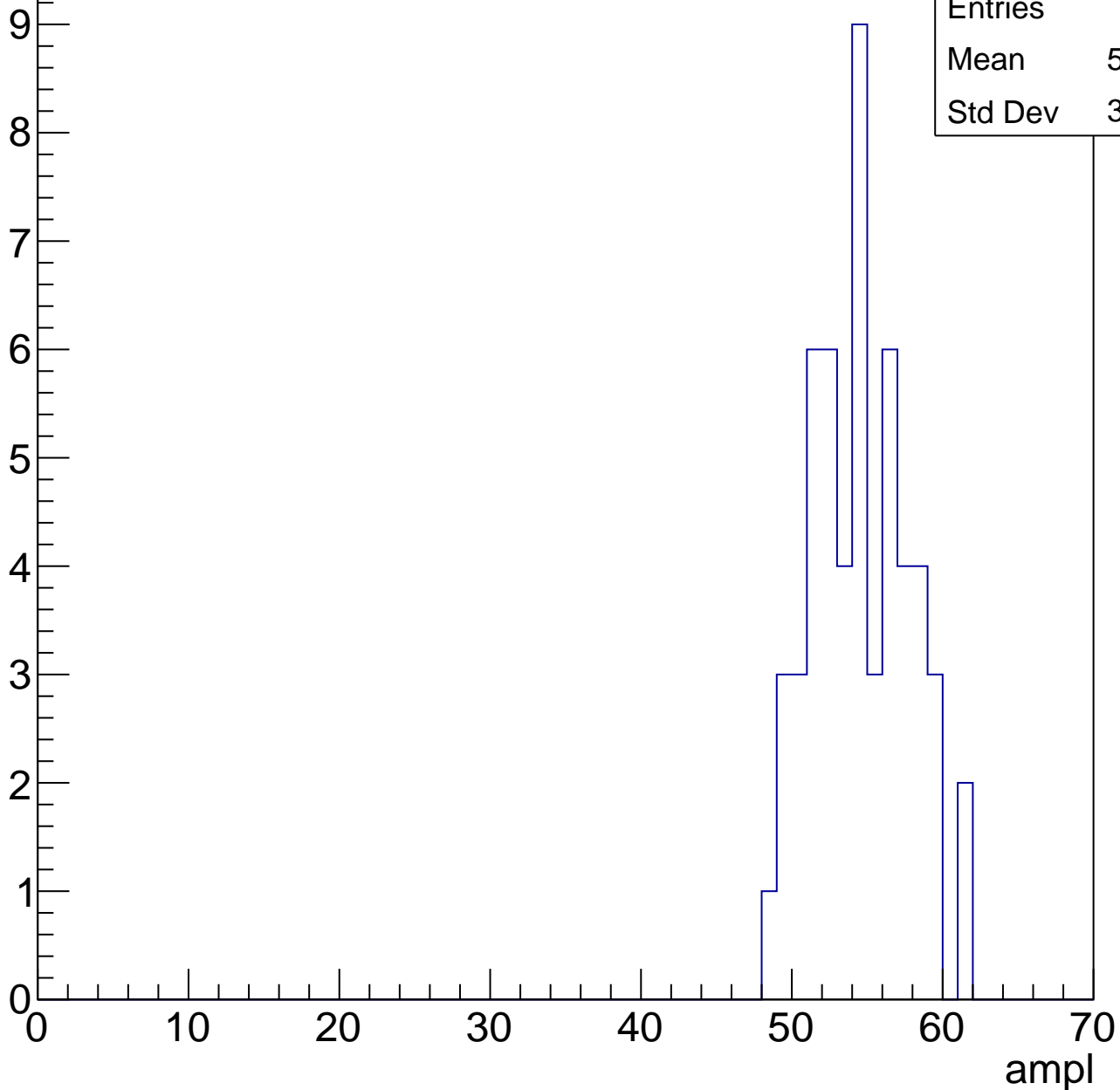


B1L103S, U8-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	54.09
Std Dev	3.164

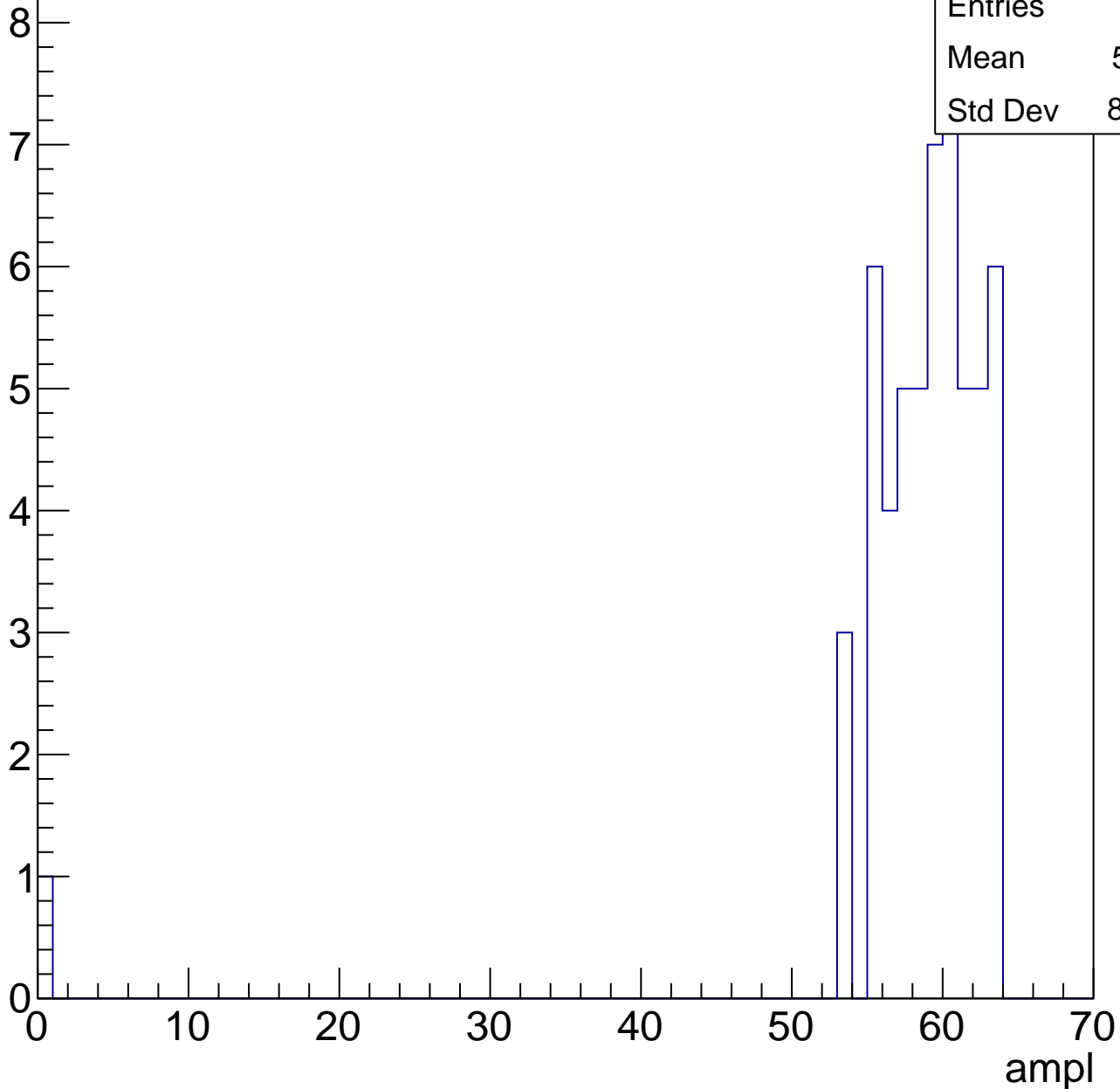


B1L103S, U8-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

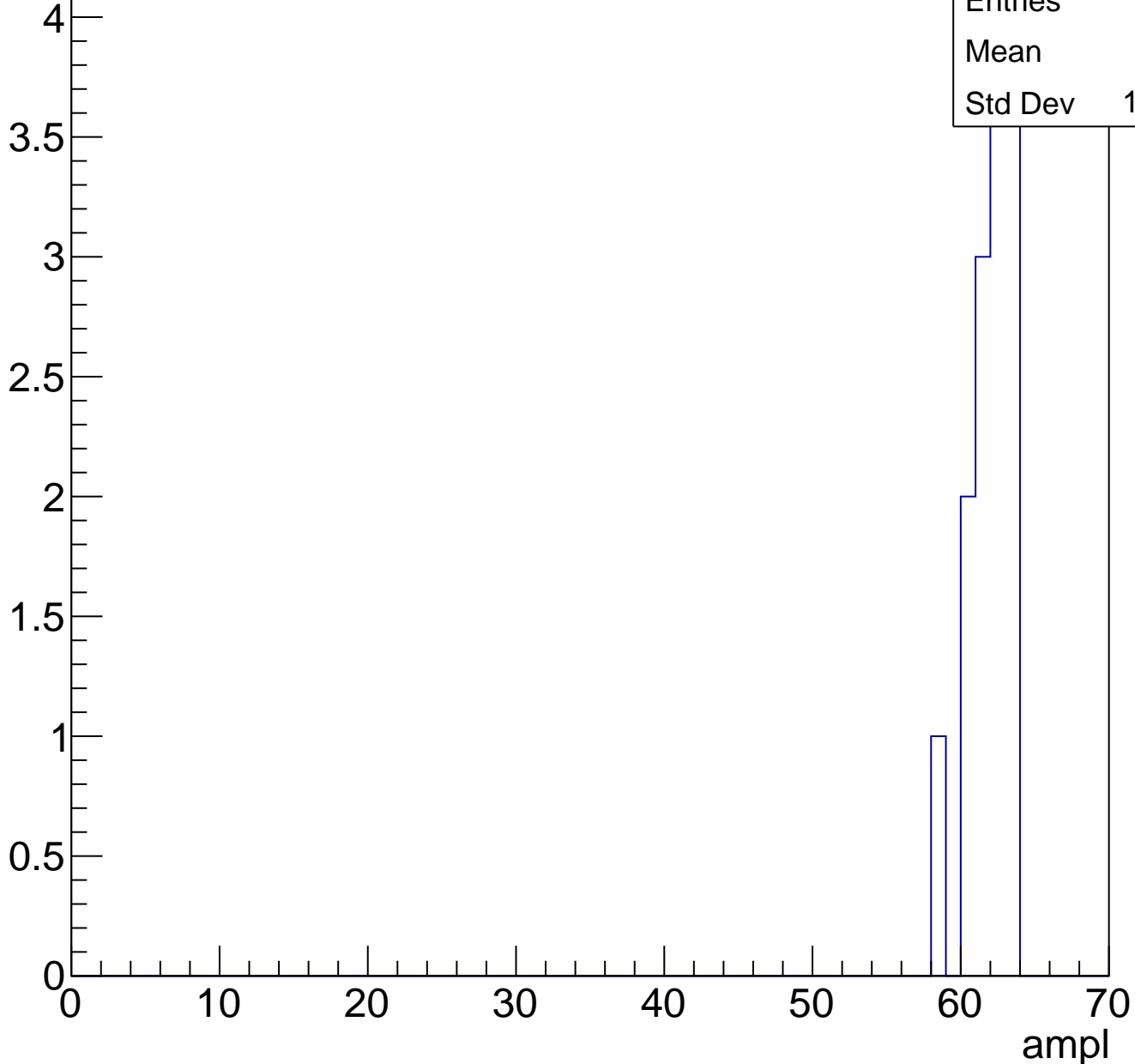
Entries	55
Mean	57.71
Std Dev	8.338



B1L103S, U8-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U8-ch111, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	23.63
Std Dev	12.34

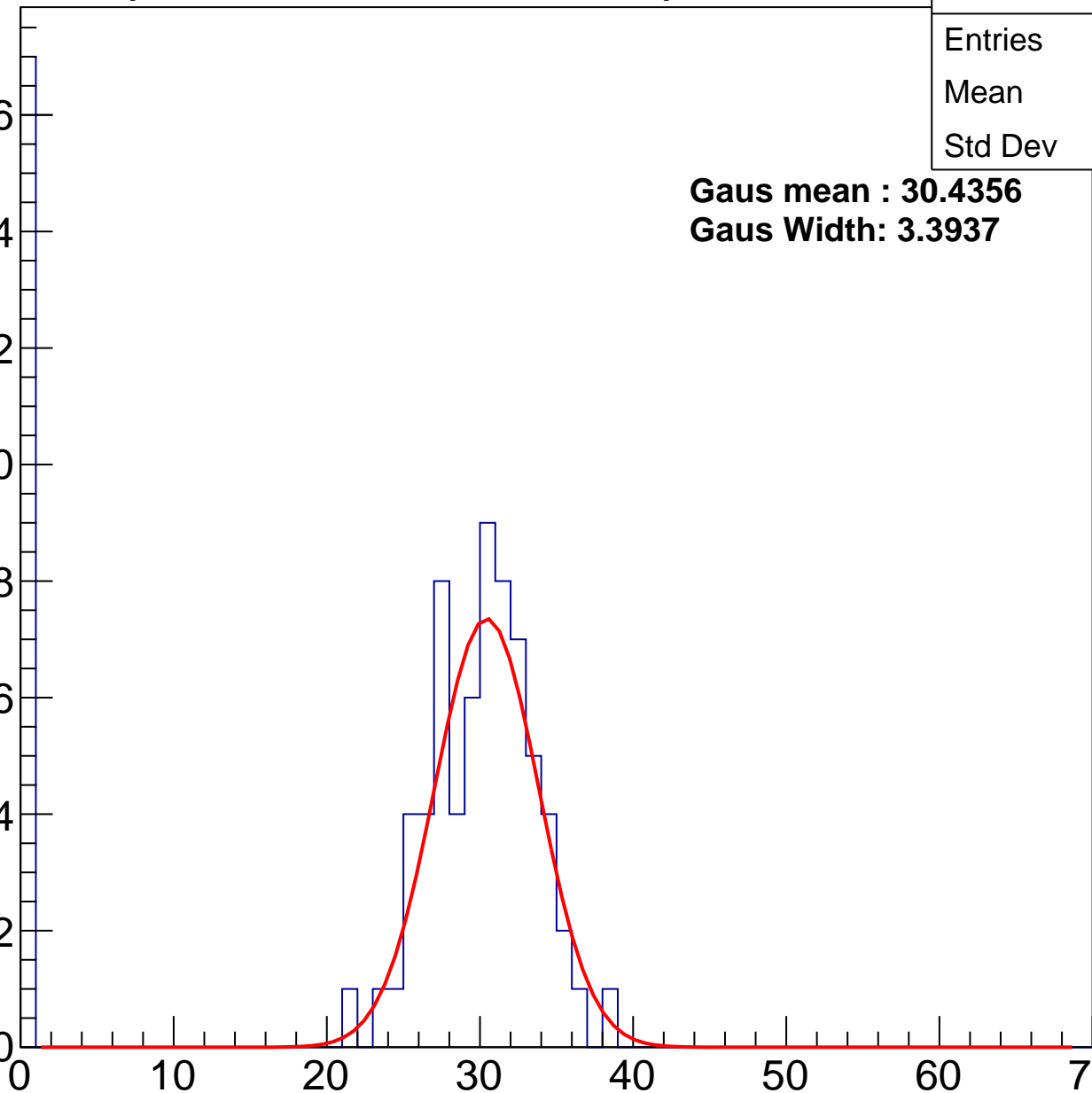
Gaus mean : 30.4356

Gaus Width: 3.3937

Entry

16
14
12
10
8
6
4
2
0

ampl



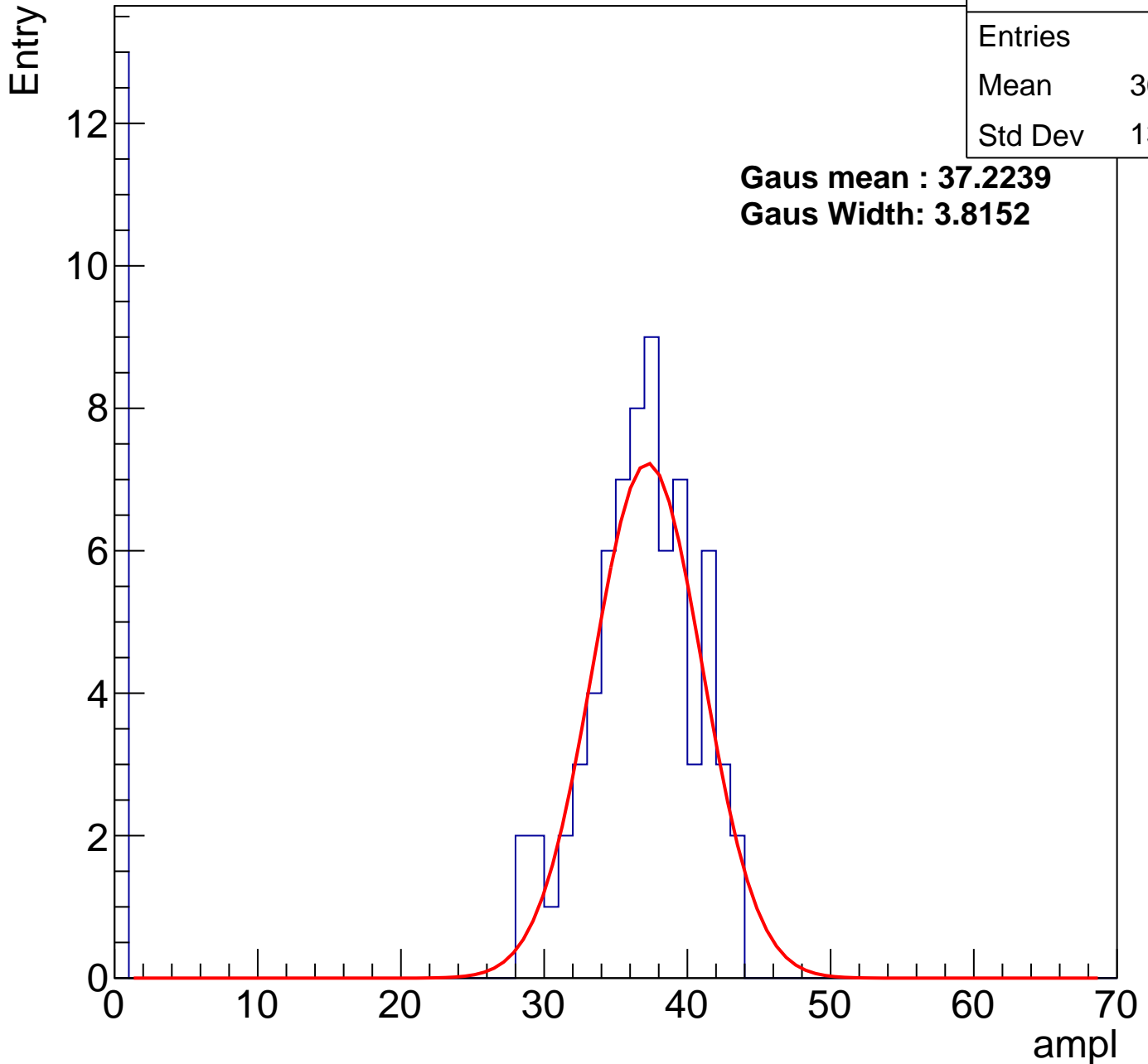
B1L103S, U8-ch111, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	30.75
Std Dev	13.57

Gaus mean : 37.2239

Gaus Width: 3.8152



B1L103S, U8-ch111, adc2

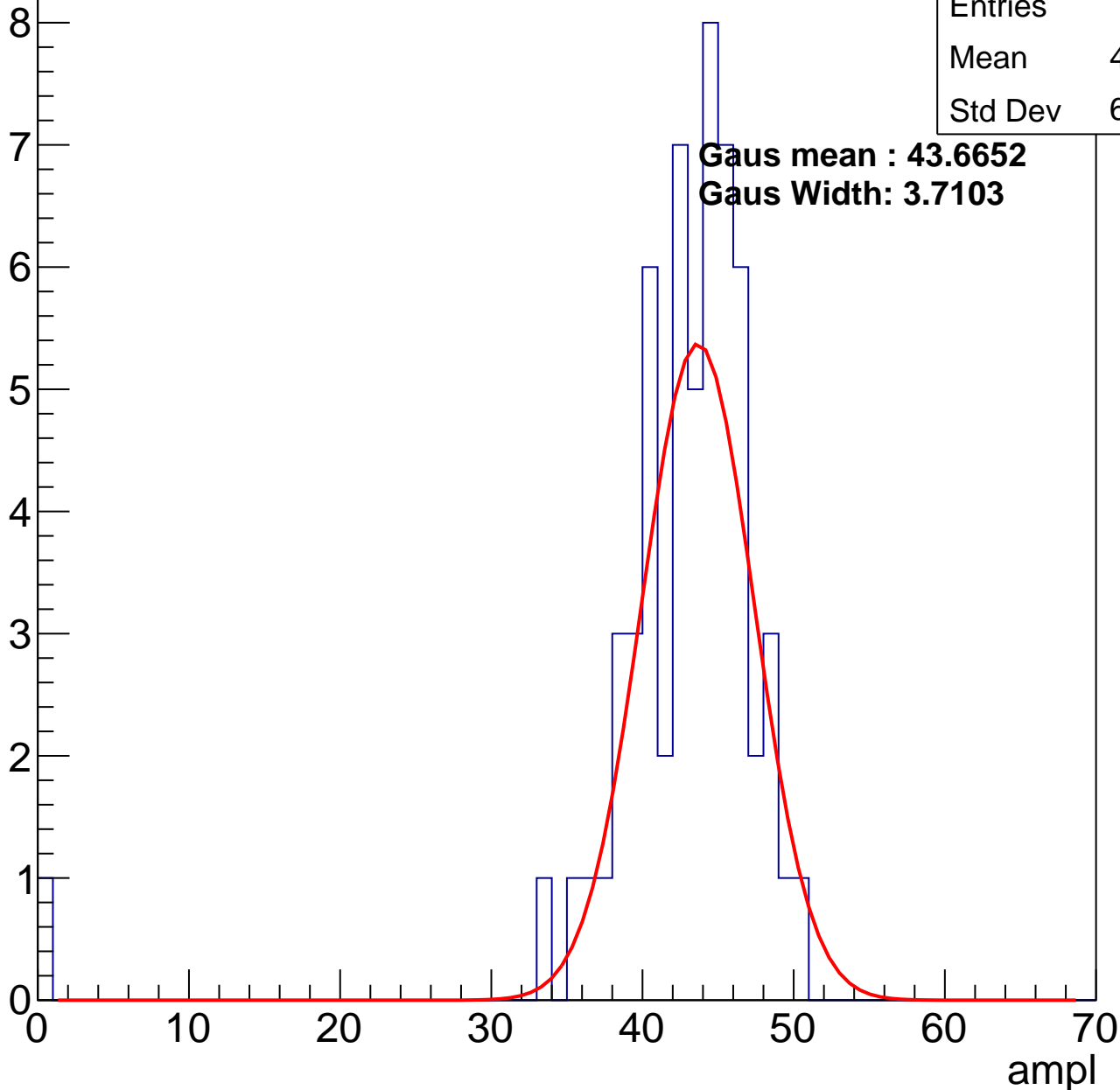
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42.08
Std Dev	6.539

Gaus mean : 43.6652

Gaus Width: 3.7103

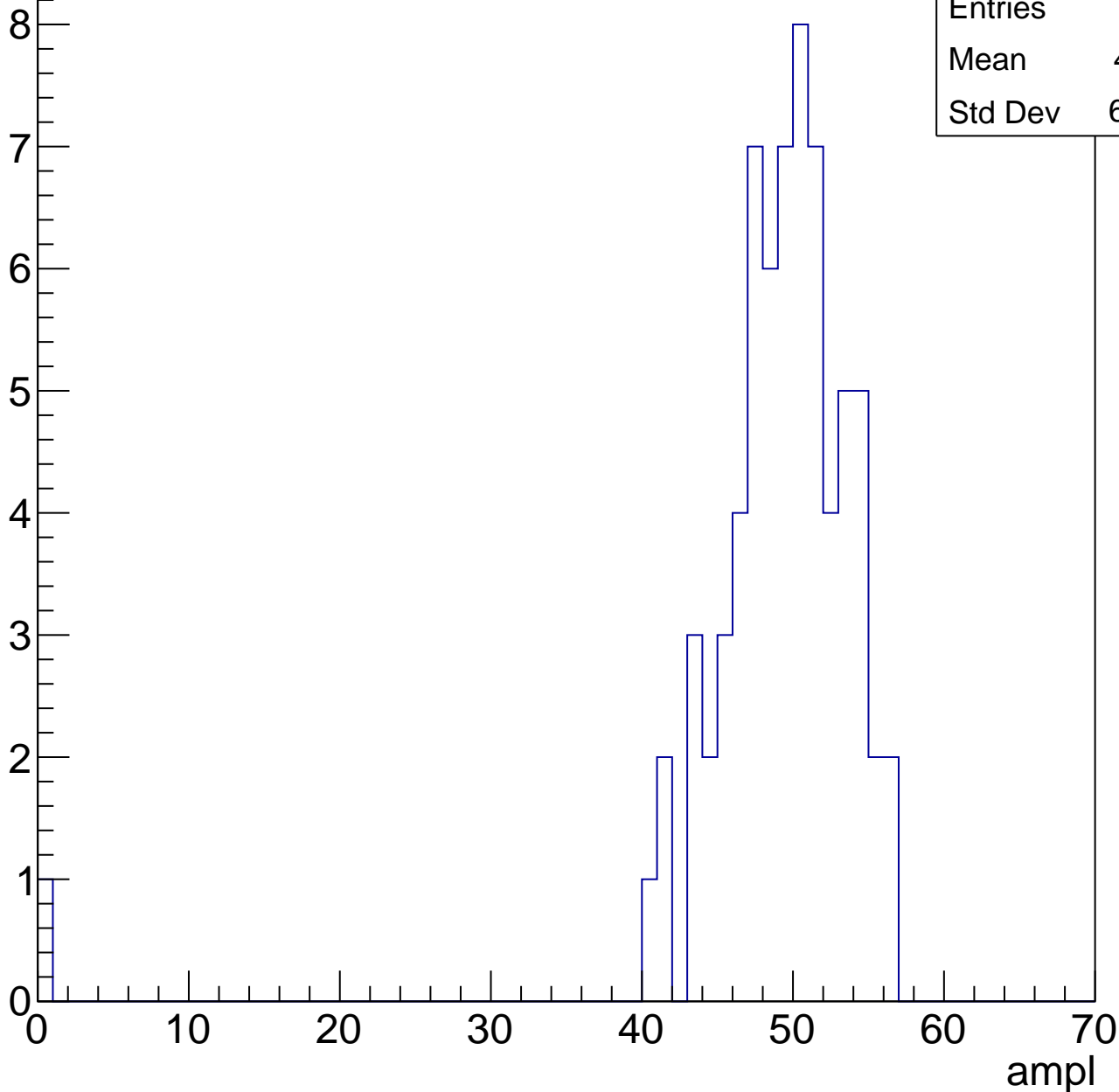


B1L103S, U8-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	48.41
Std Dev	6.925

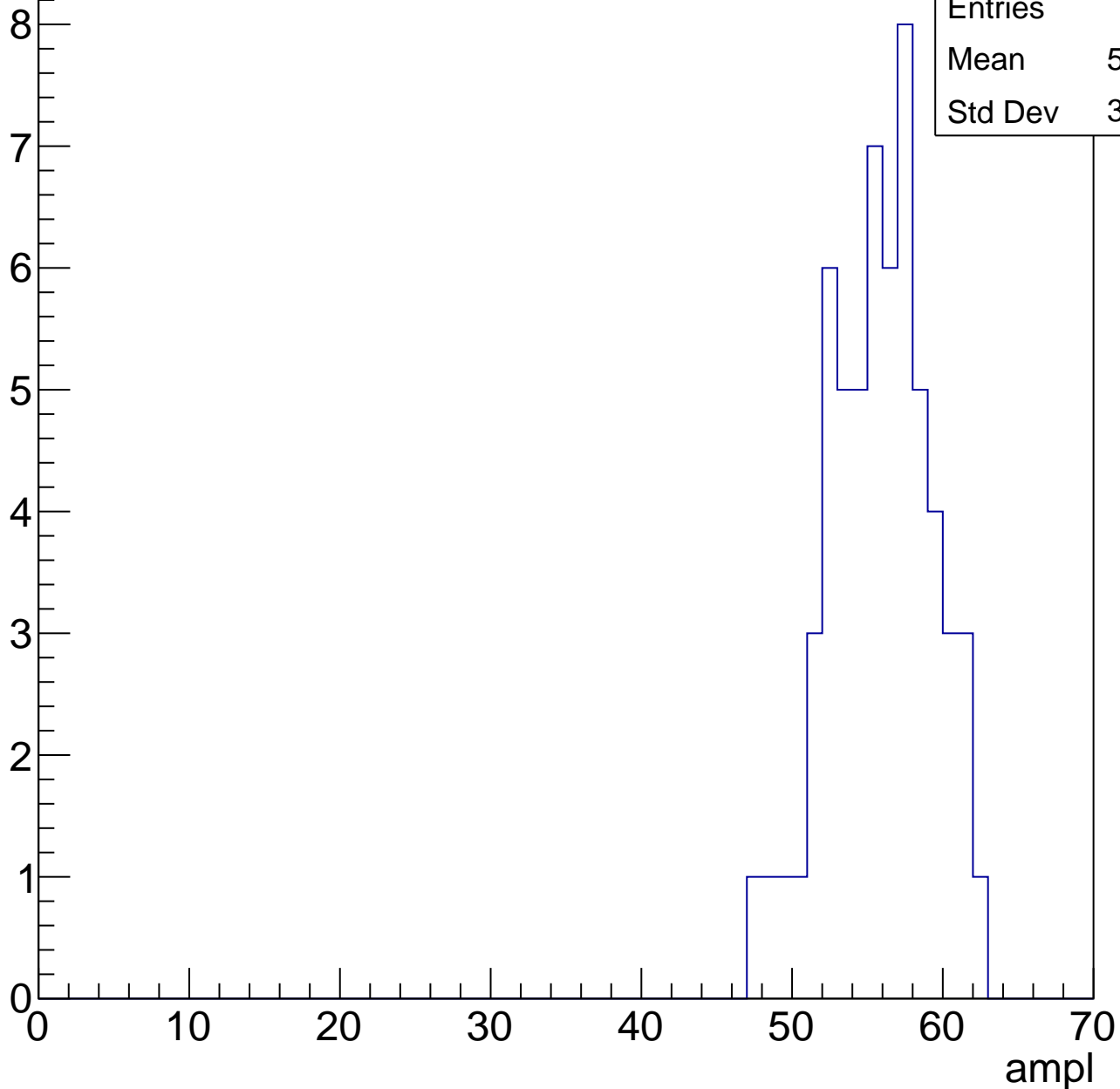


B1L103S, U8-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.37
Std Dev	3.346

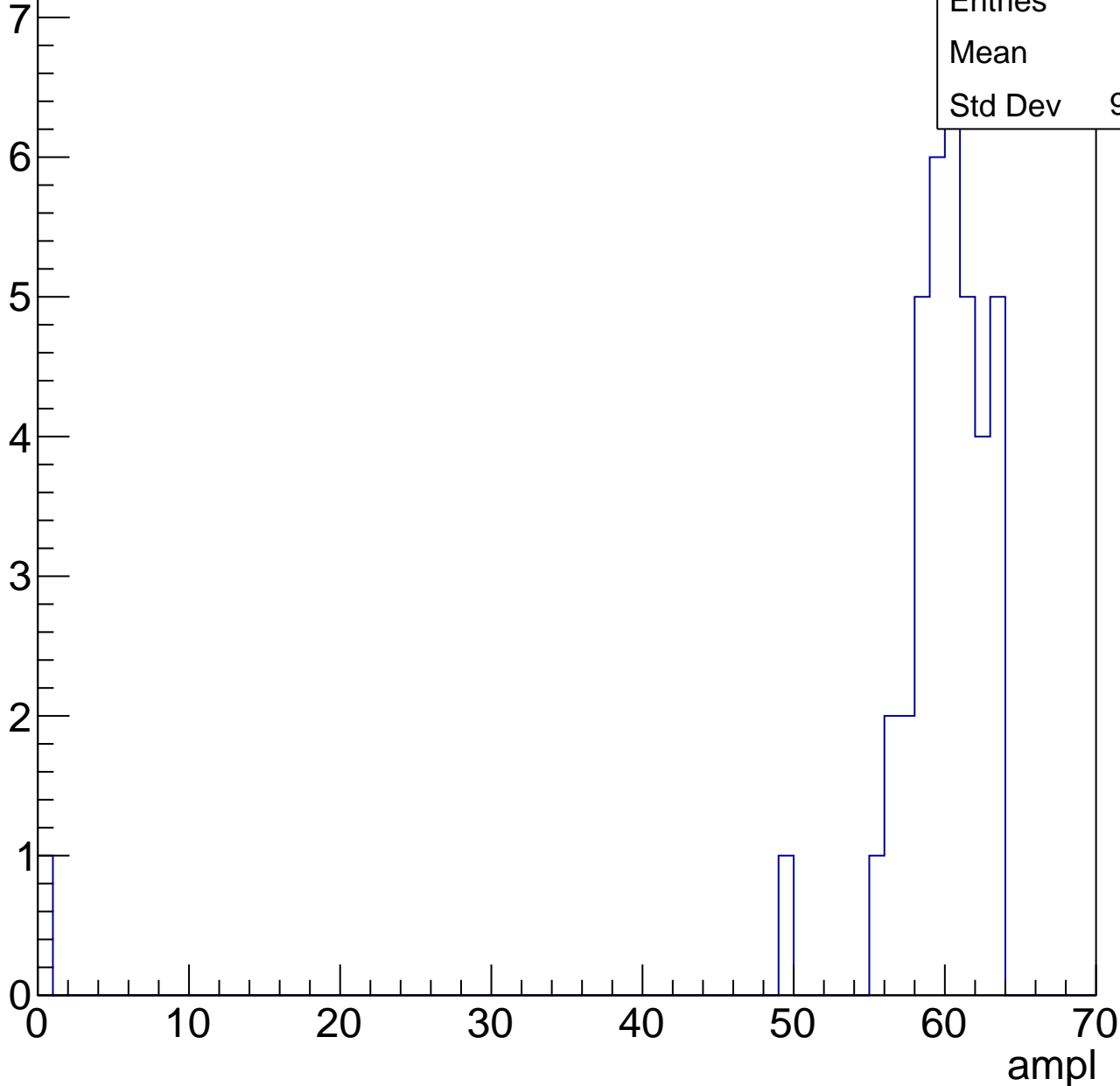


B1L103S, U8-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	58
Std Dev	9.782

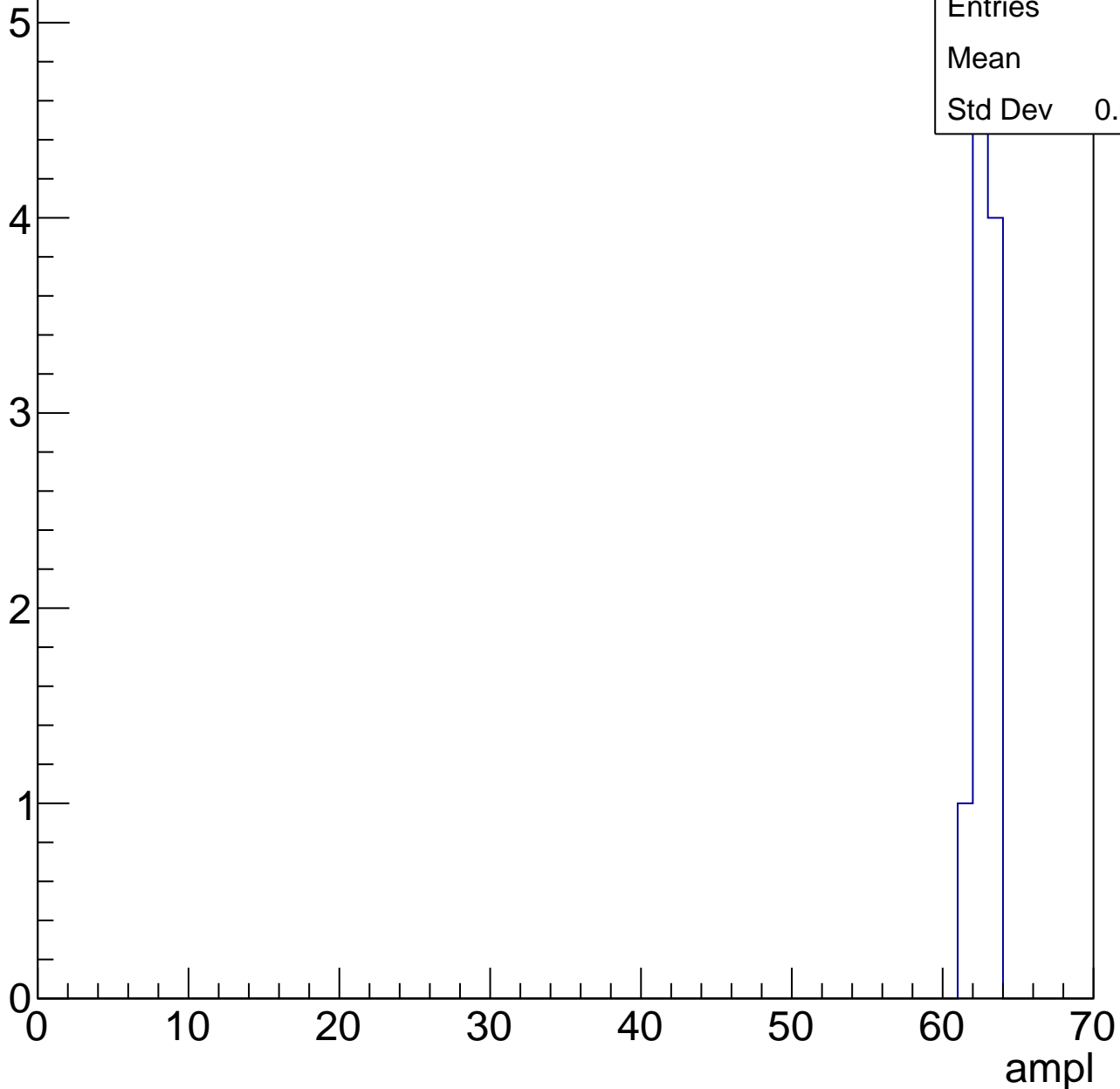


B1L103S, U8-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	62.3
Std Dev	0.6403



B1L103S, U8-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U8-ch112, adc0

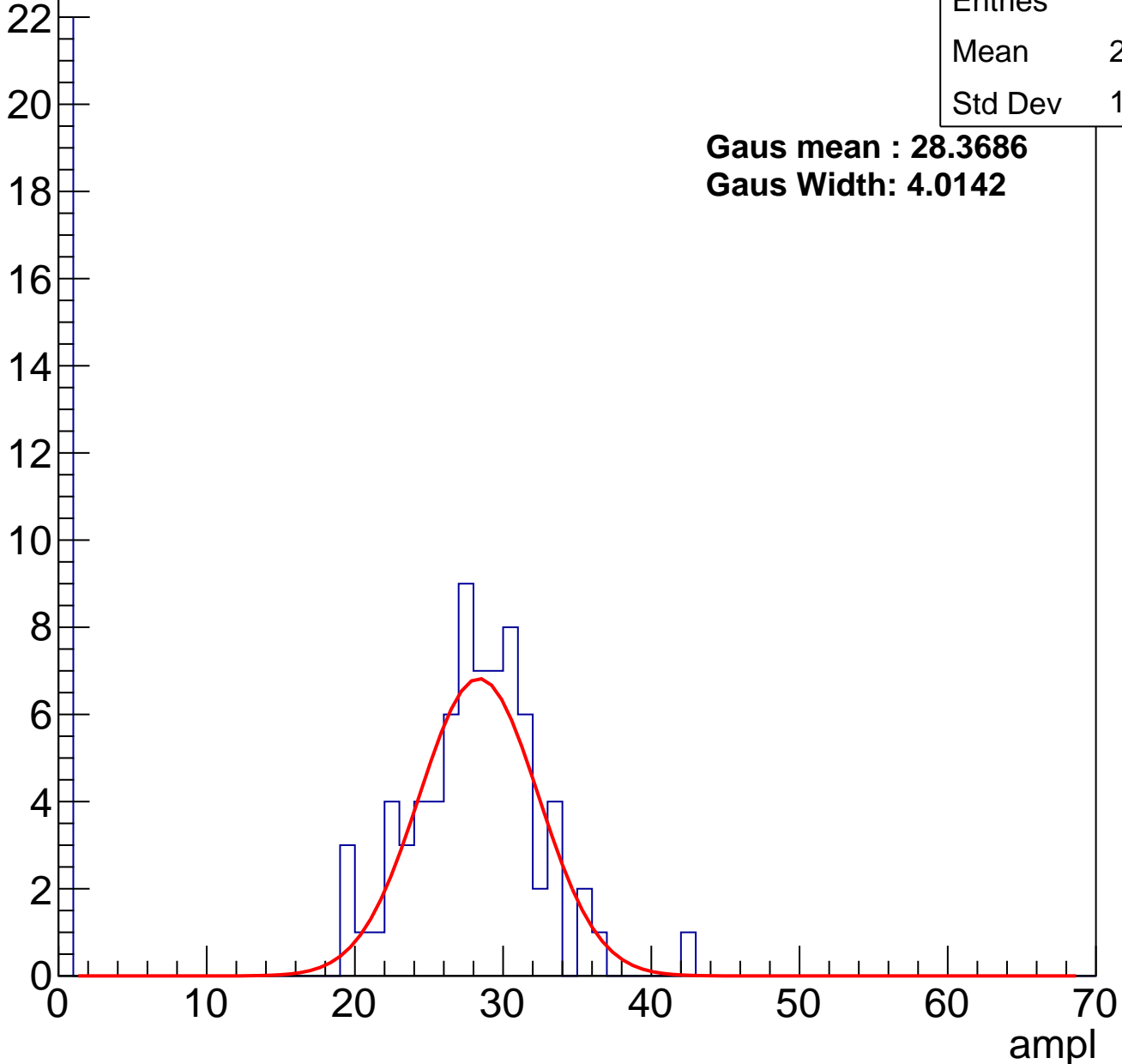
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	21.25
Std Dev	12.23

Gaus mean : 28.3686

Gaus Width: 4.0142

Entry



B1L103S, U8-ch112, adc1

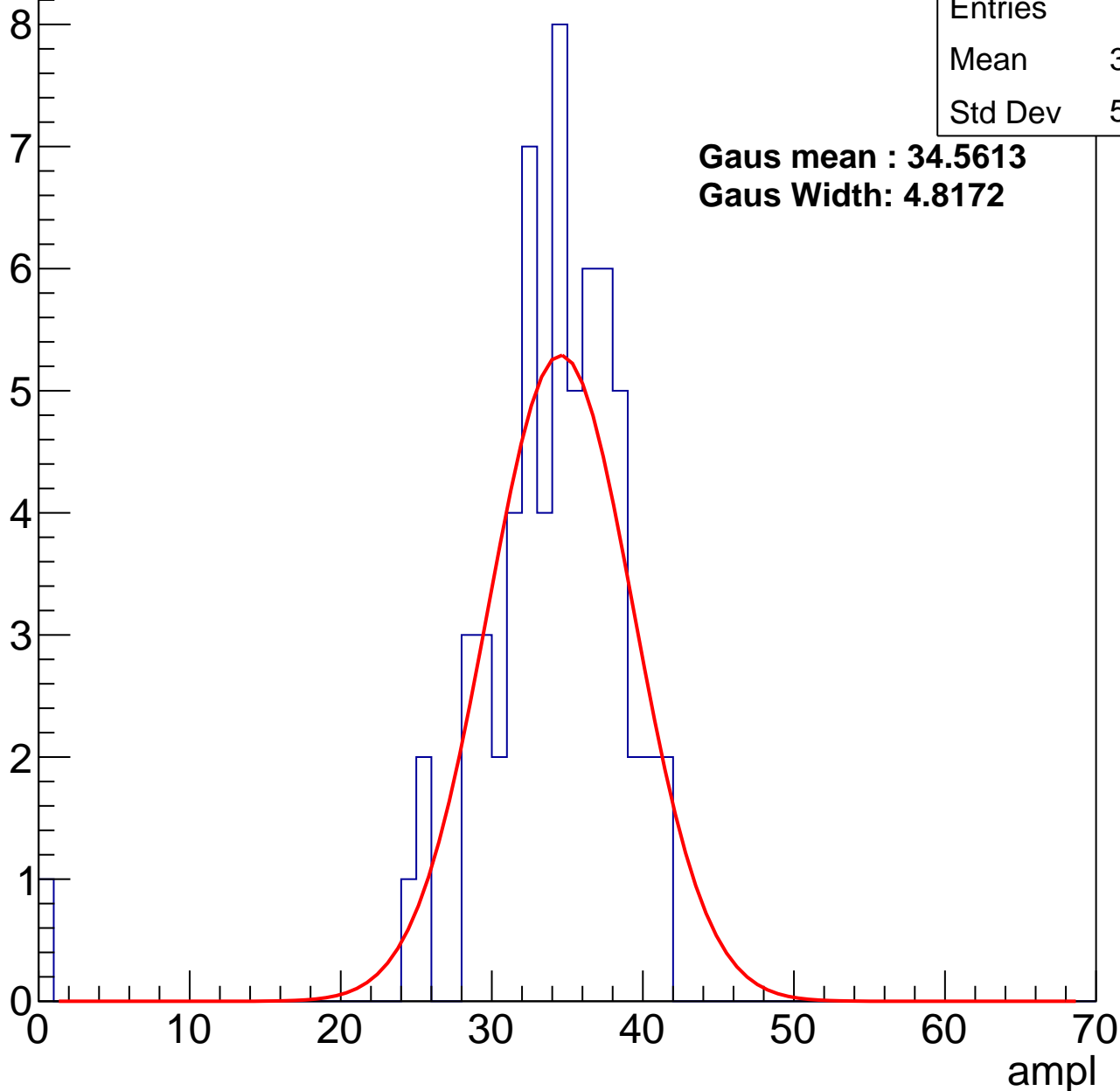
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	33.33
Std Dev	5.713

Gaus mean : 34.5613

Gaus Width: 4.8172



B1L103S, U8-ch112, adc2

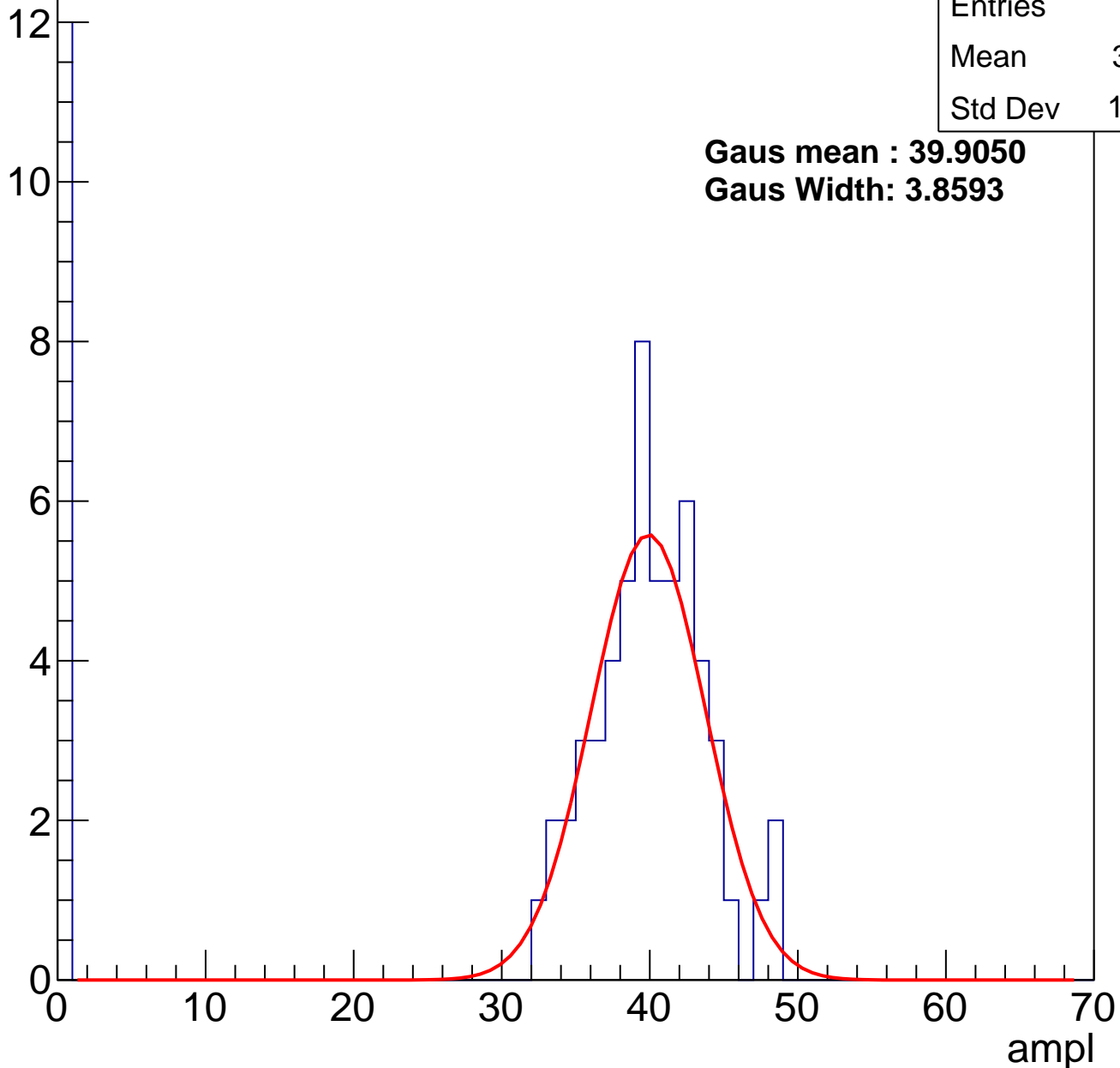
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	32.51
Std Dev	15.54

Gaus mean : 39.9050

Gaus Width: 3.8593

Entry

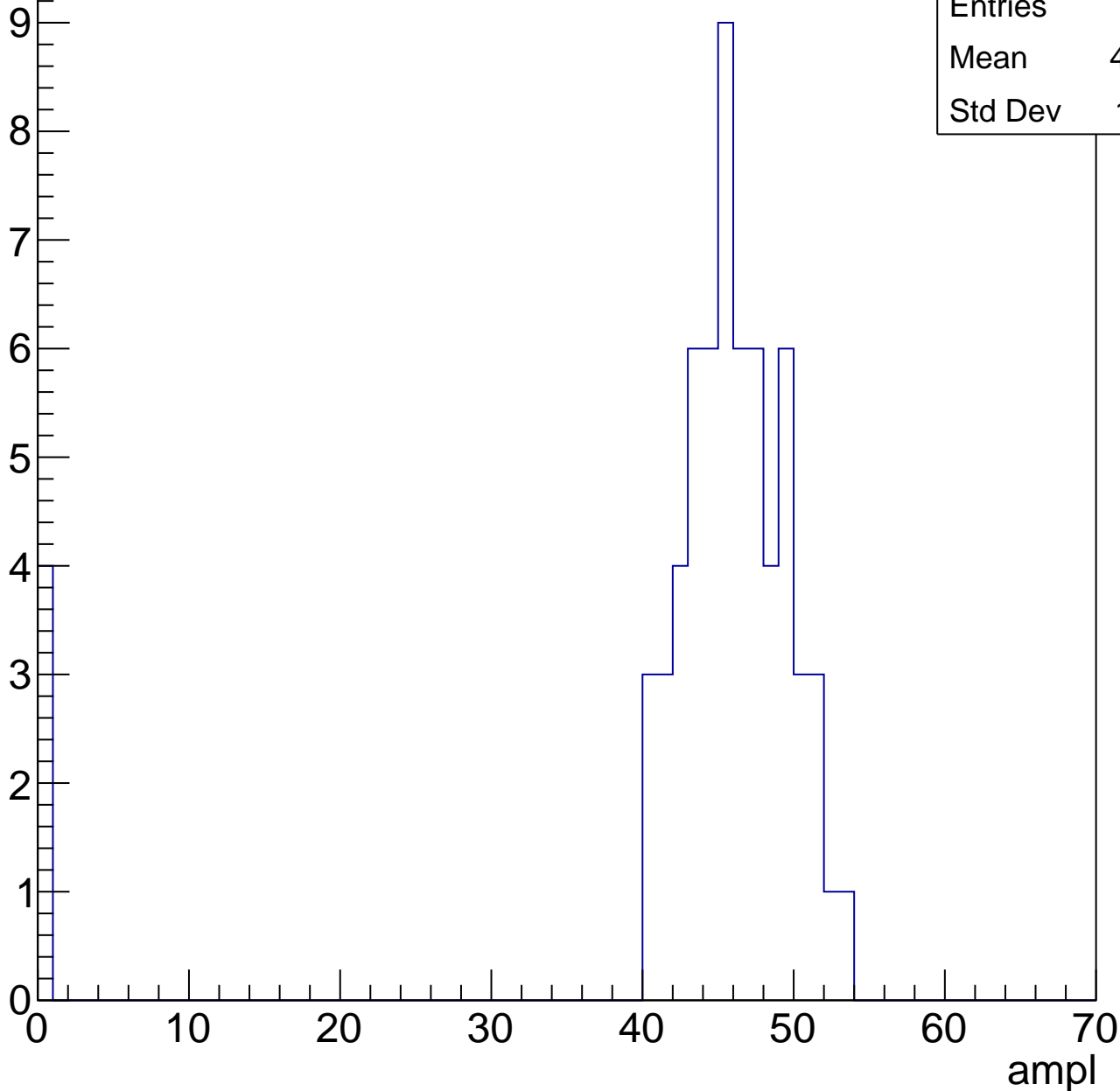


B1L103S, U8-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	42.92
Std Dev	11.41

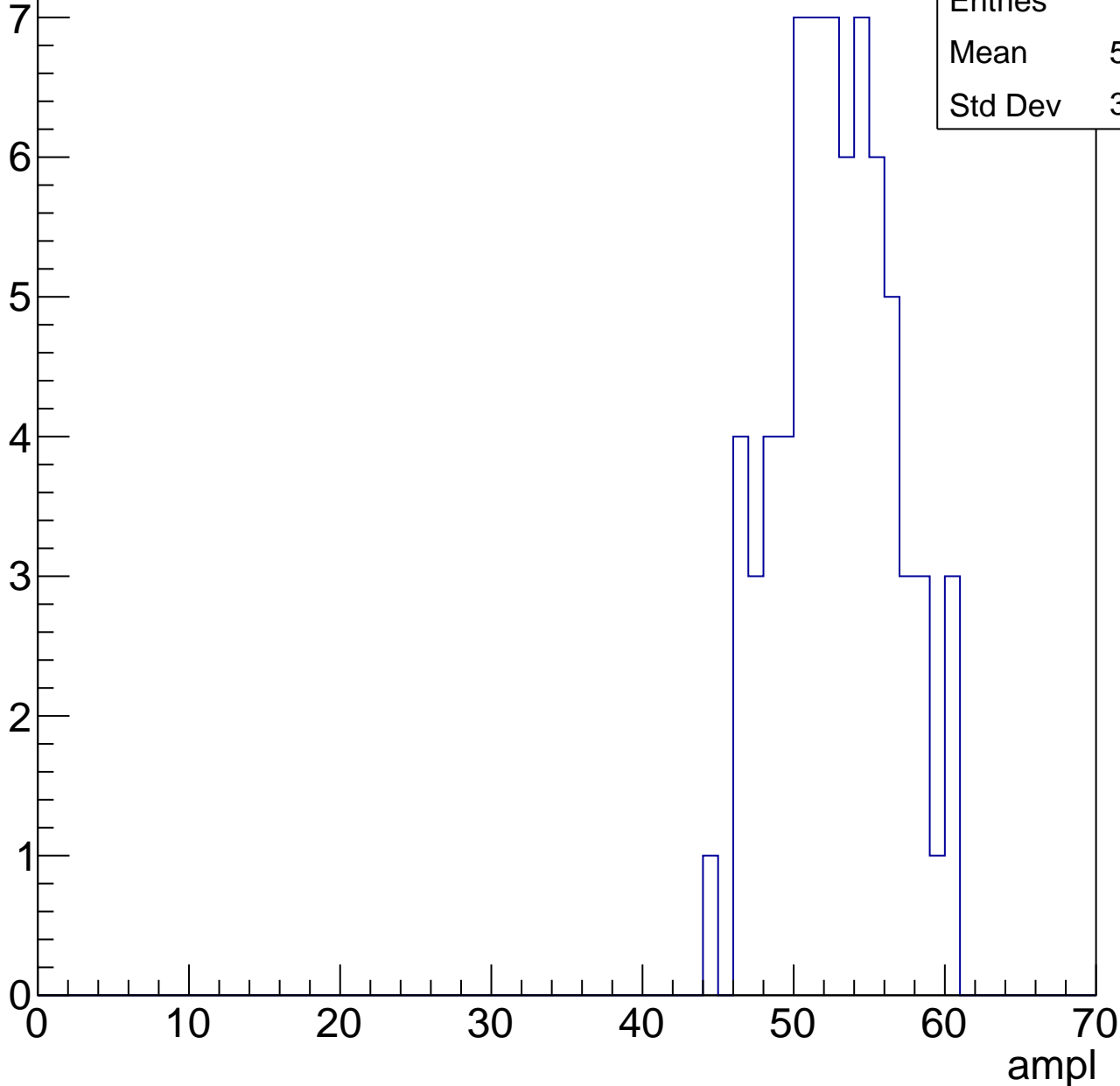


B1L103S, U8-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	52.37
Std Dev	3.754



B1L103S, U8-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	57.78
Std Dev	7.675

Entry

10

8

6

4

2

0

0

10

20

30

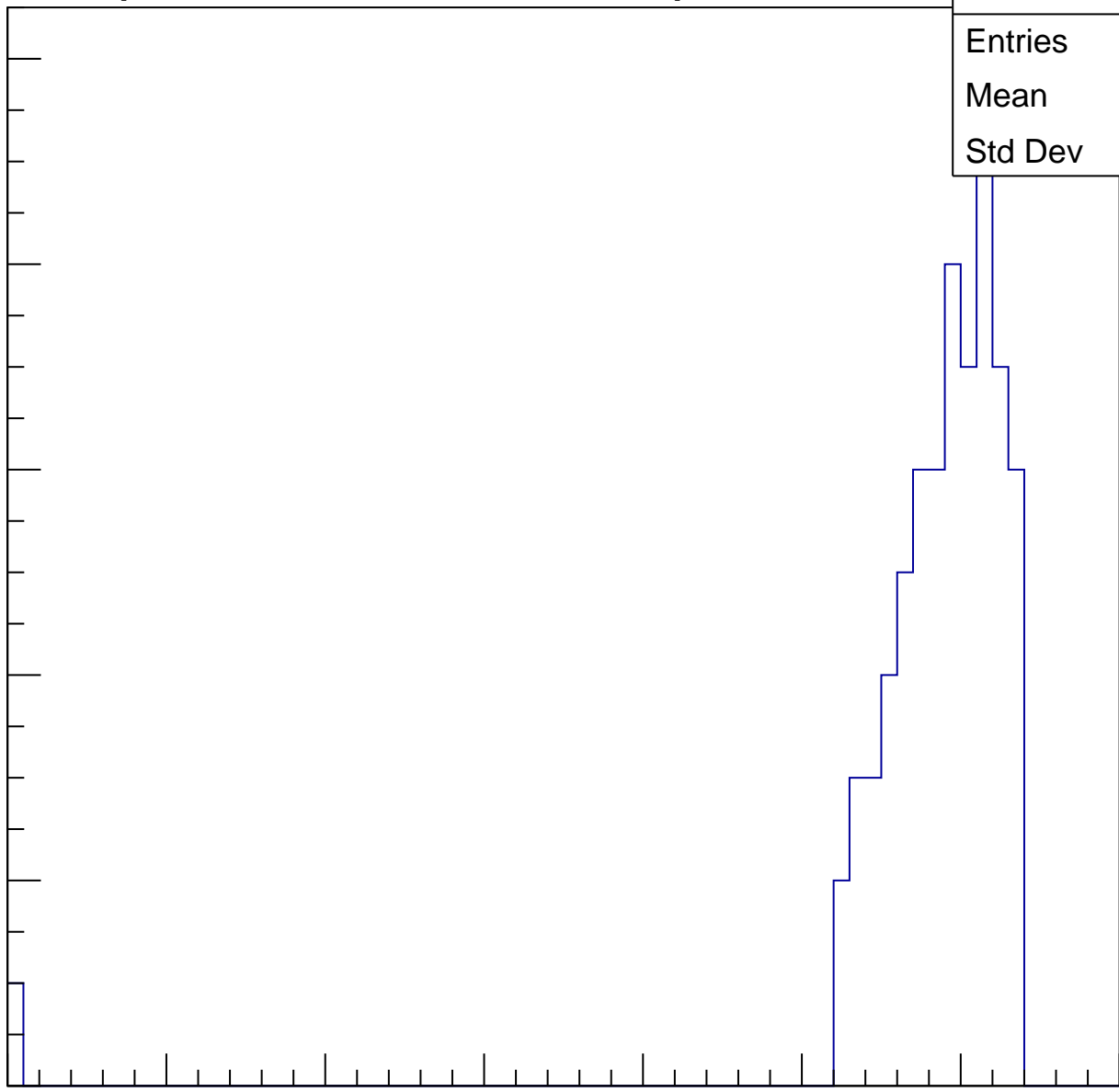
40

50

60

70

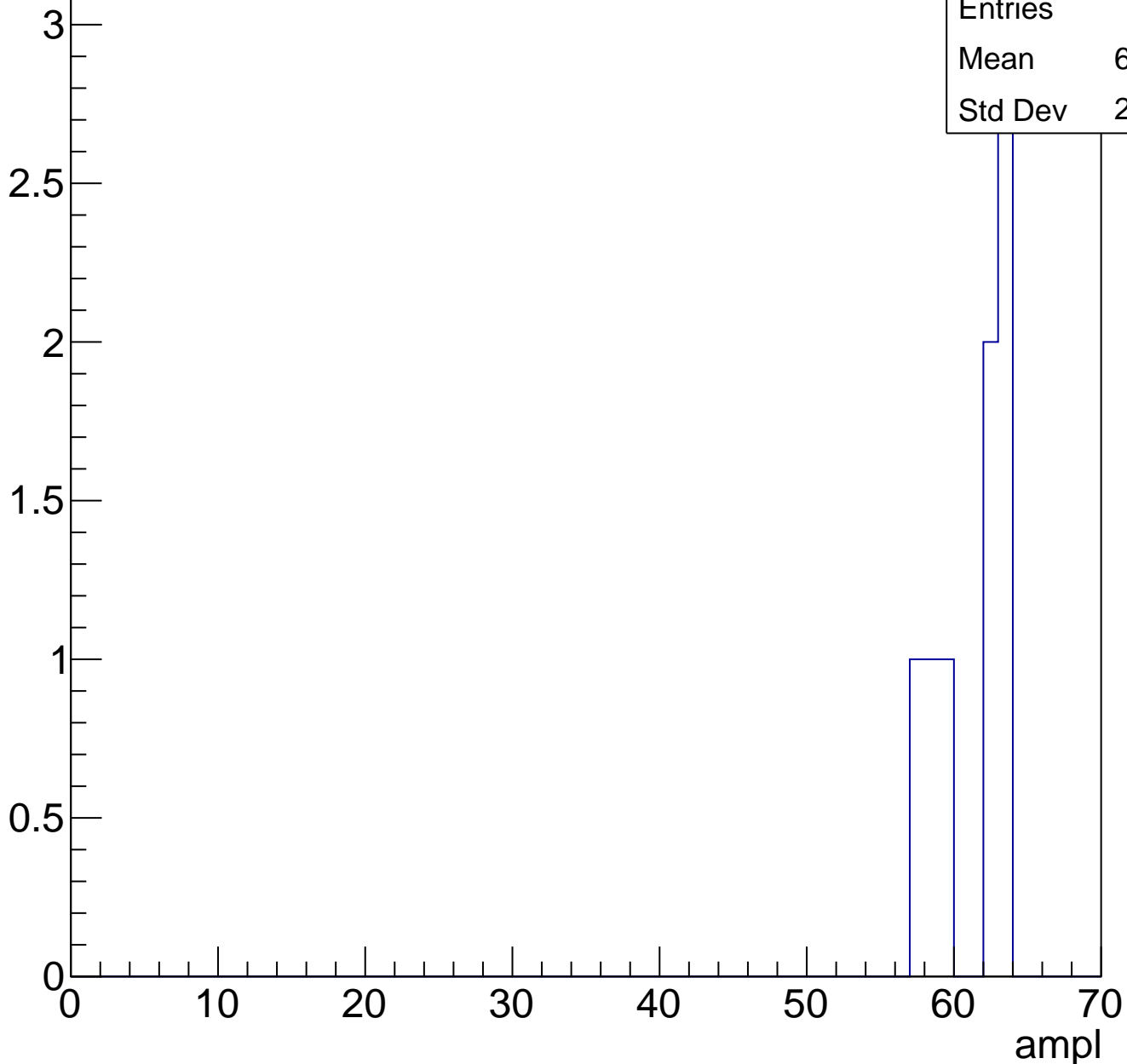
ampl



B1L103S, U8-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U8-ch113, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	23.74
Std Dev	11.26

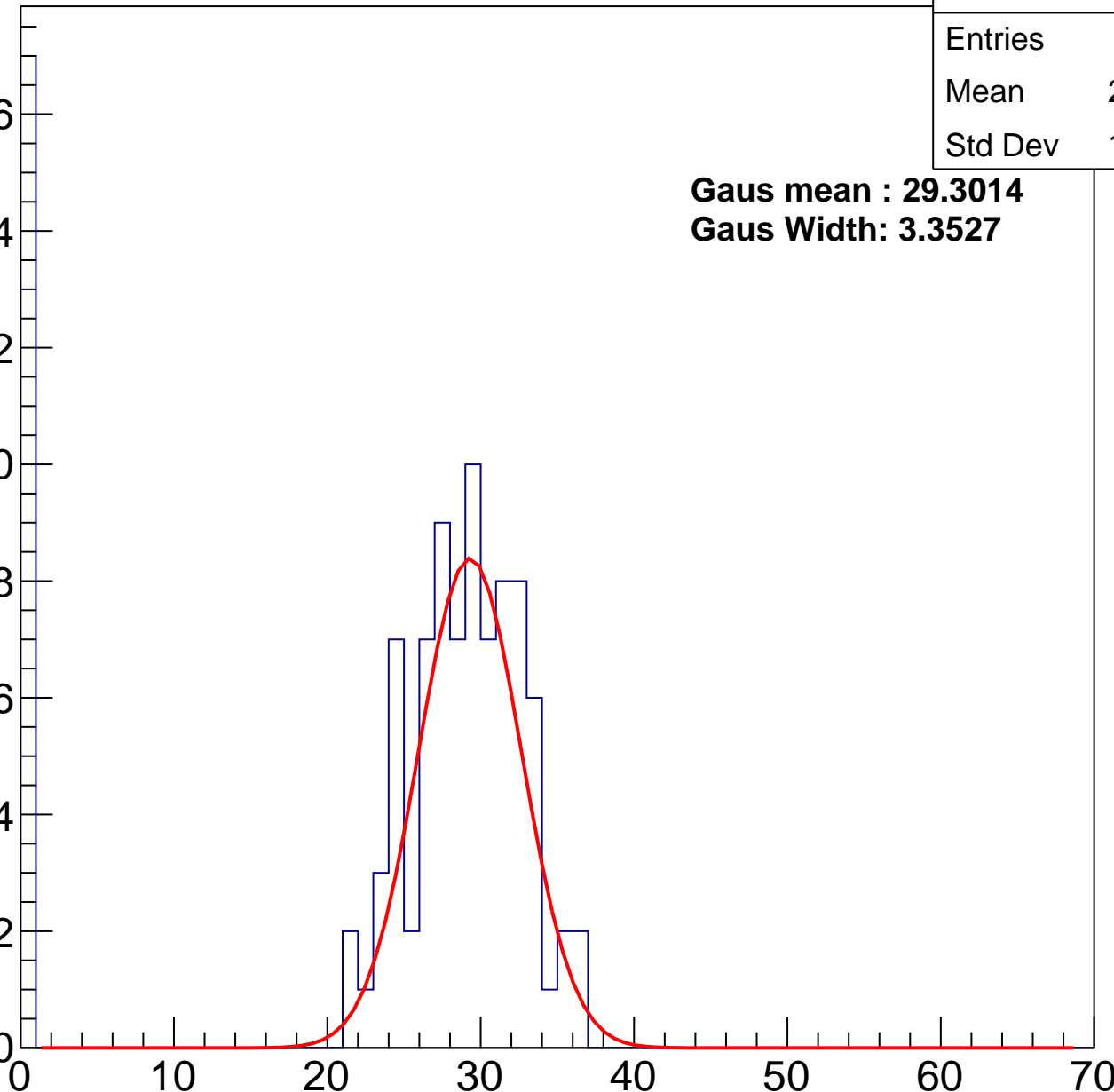
Gaus mean : 29.3014

Gaus Width: 3.3527

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U8-ch113, adc1

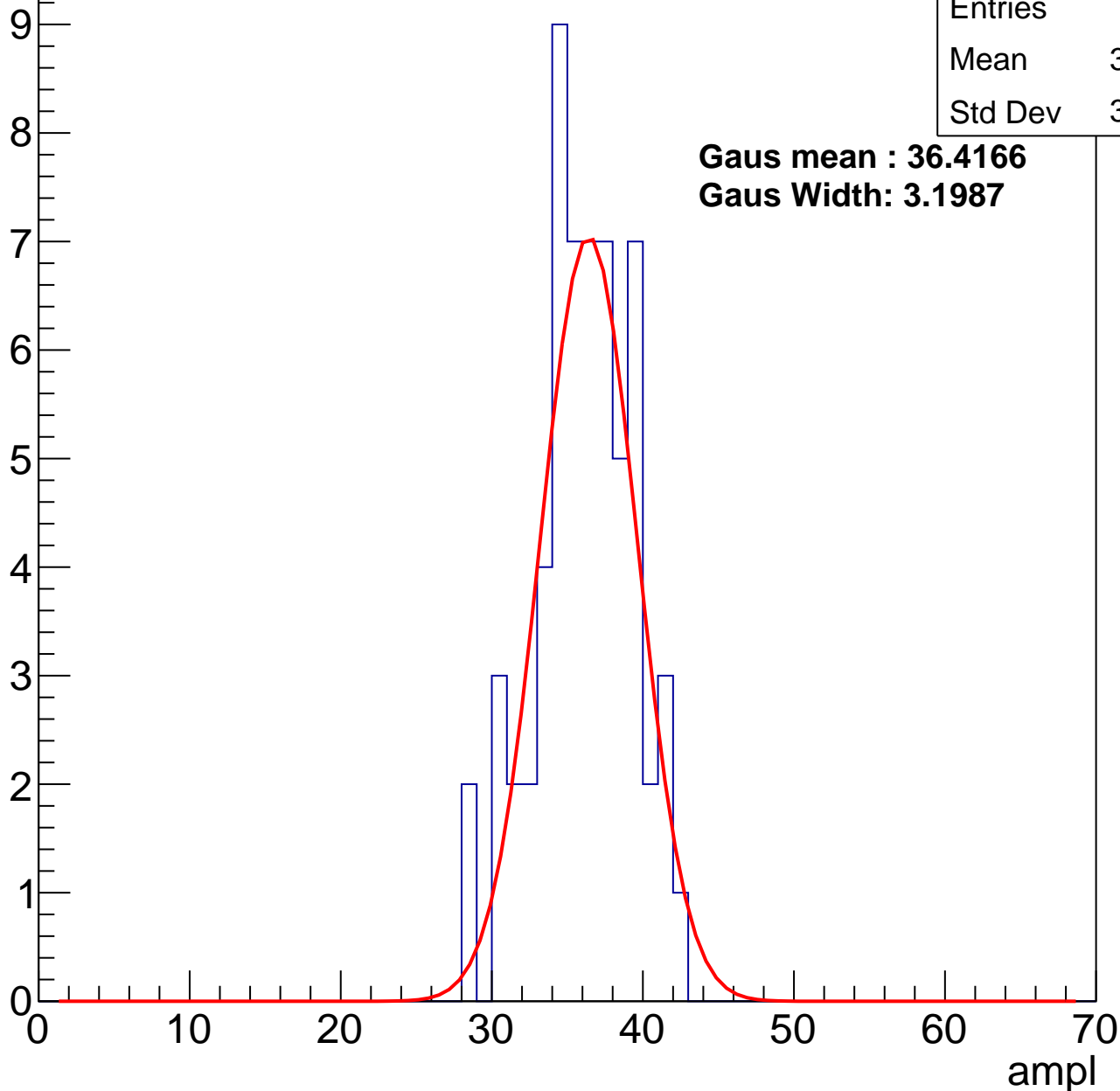
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	35.64
Std Dev	3.193

Gaus mean : 36.4166

Gaus Width: 3.1987



B1L103S, U8-ch113, adc2

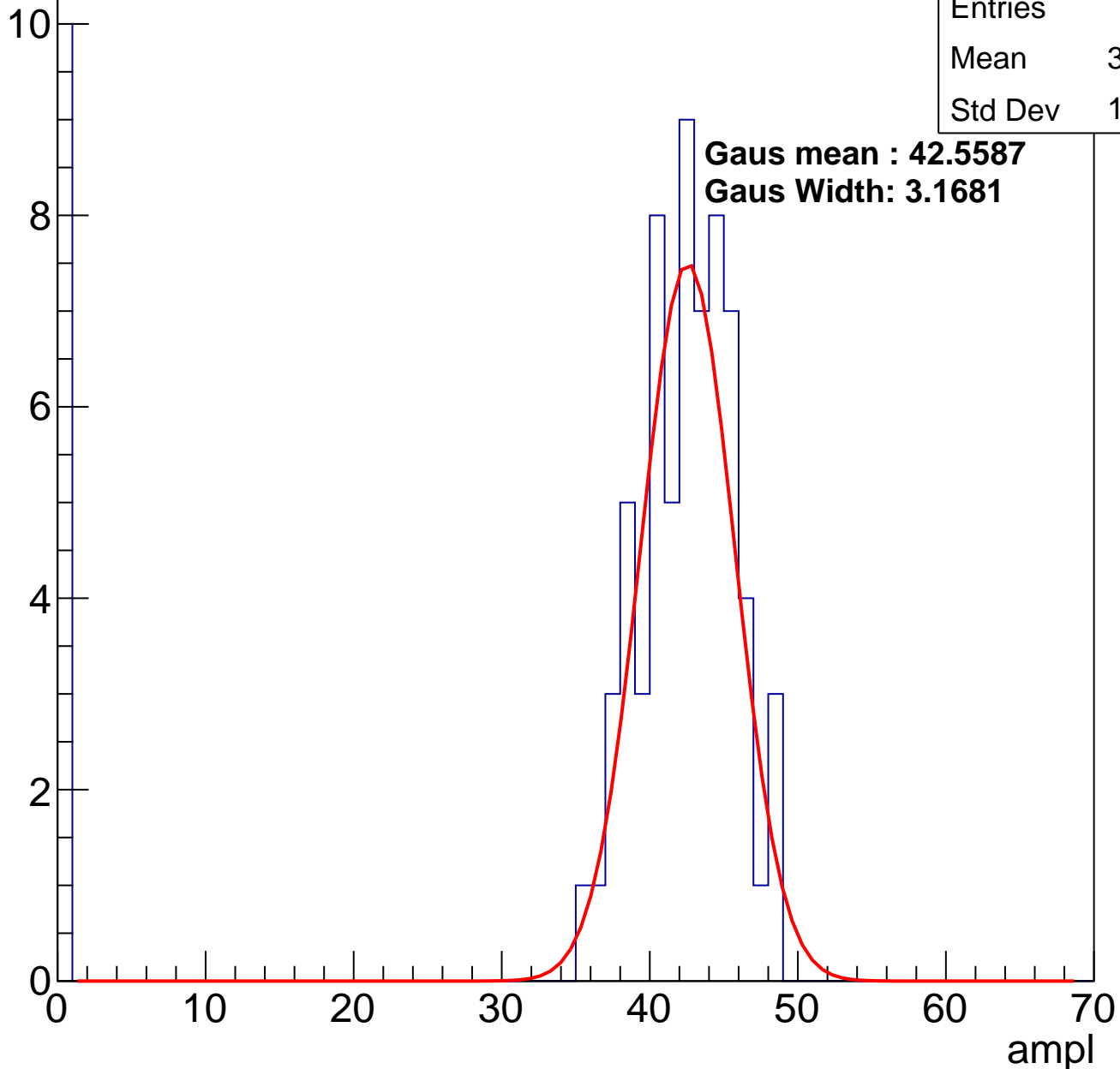
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	36.47
Std Dev	14.58

Gaus mean : 42.5587

Gaus Width: 3.1681

Entry



B1L103S, U8-ch113, adc3

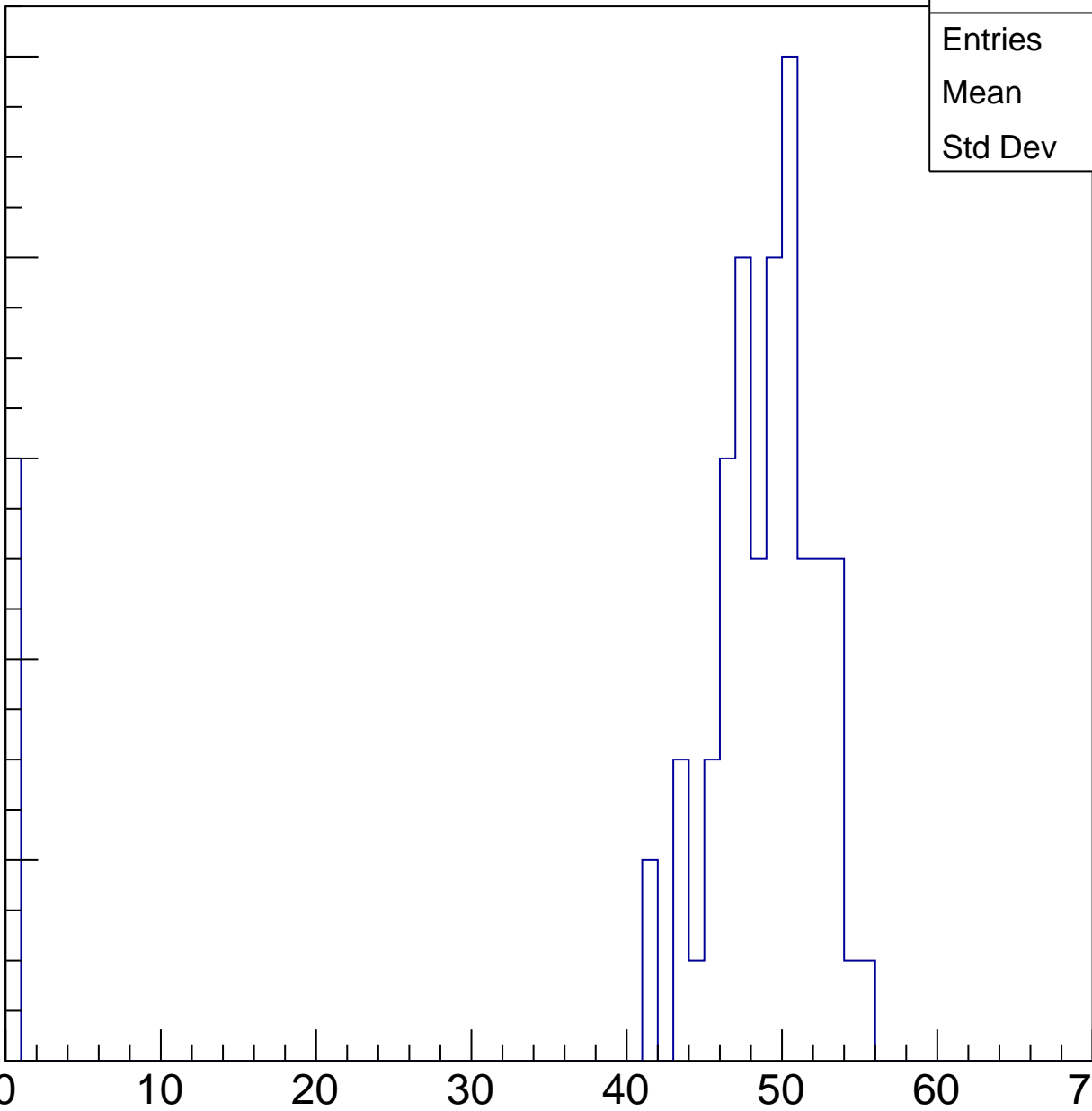
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	44.39
Std Dev	14.02

Entry

10
8
6
4
2
0

ampl

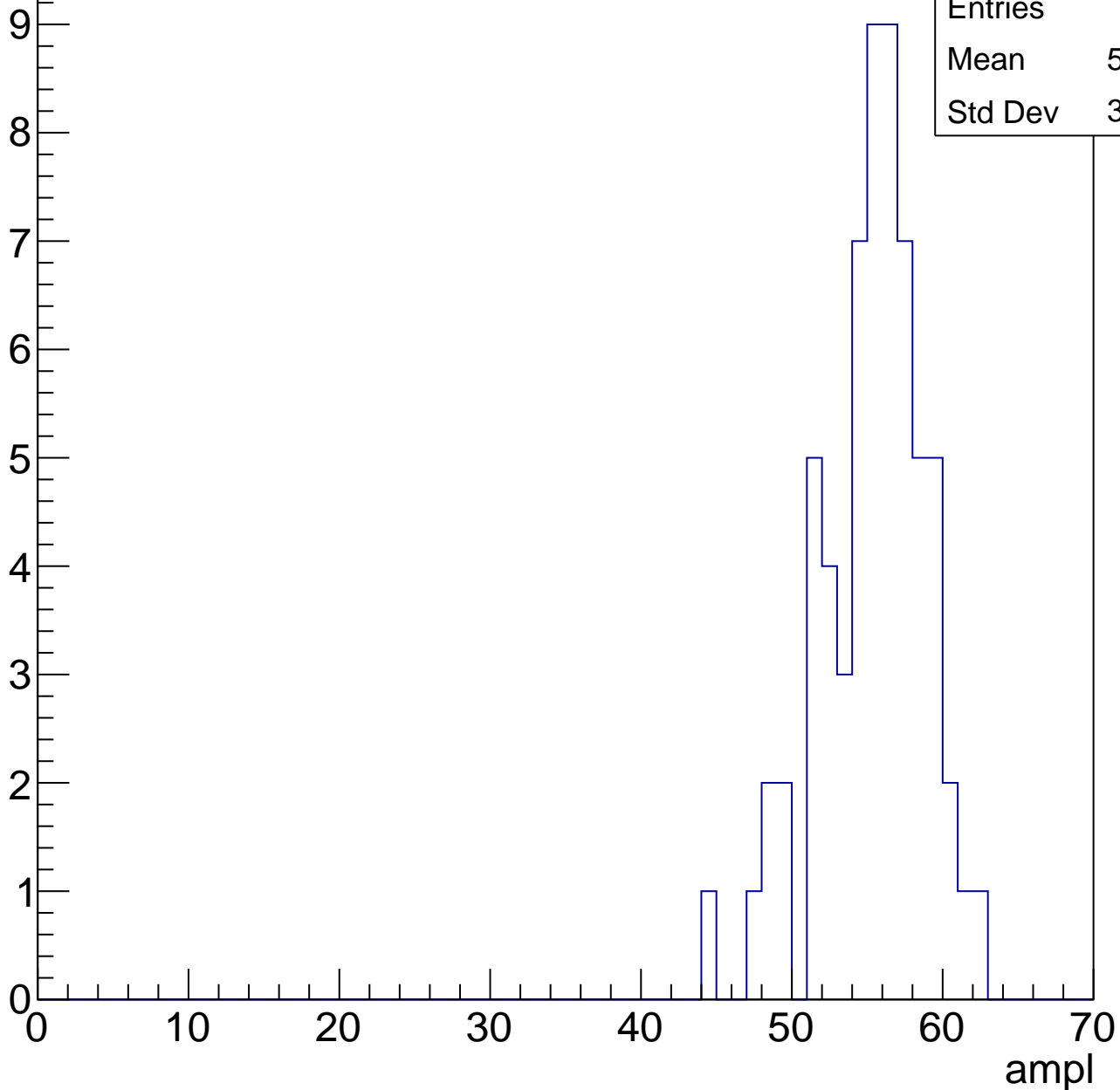


B1L103S, U8-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.86
Std Dev	3.504

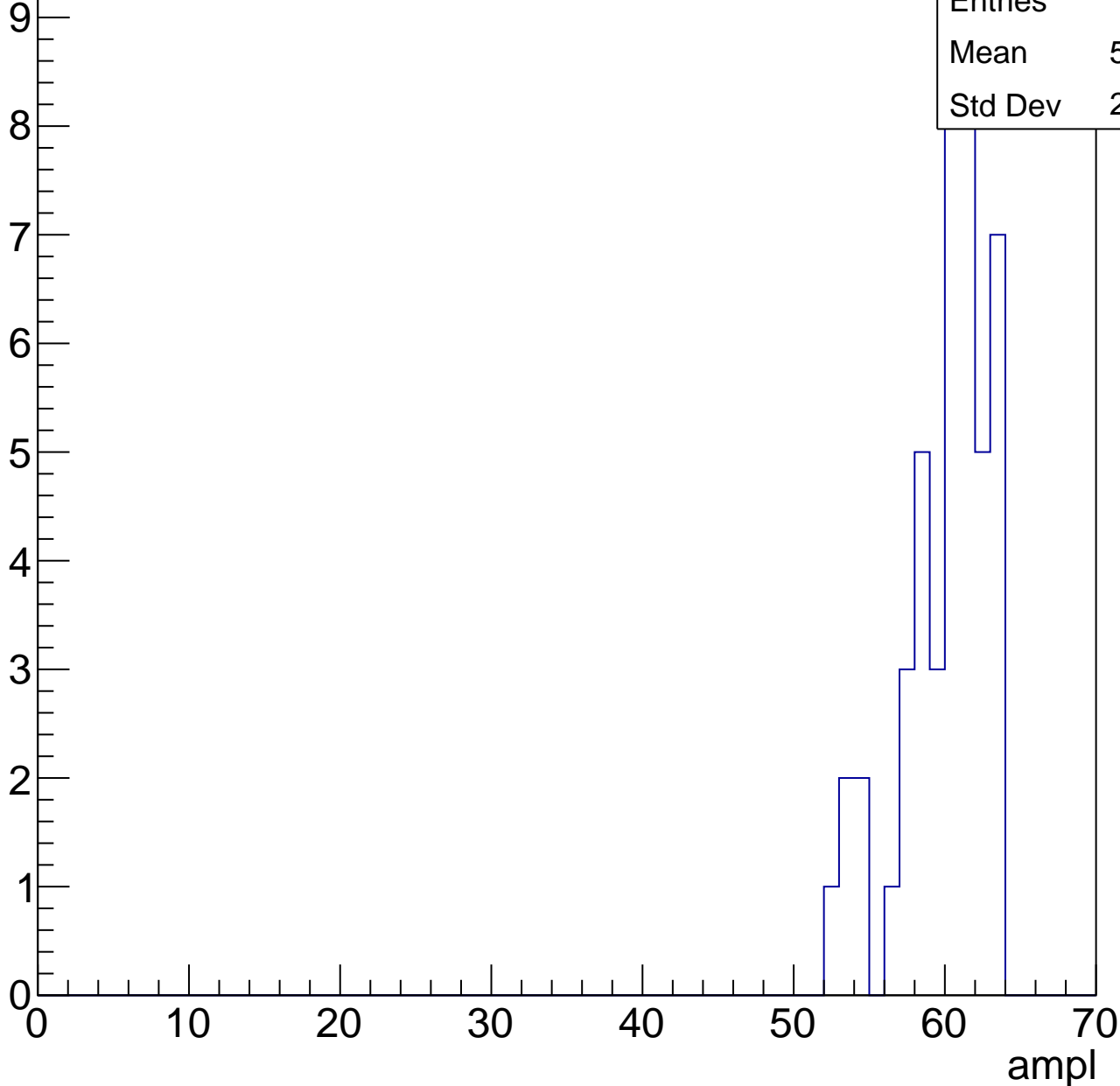


B1L103S, U8-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

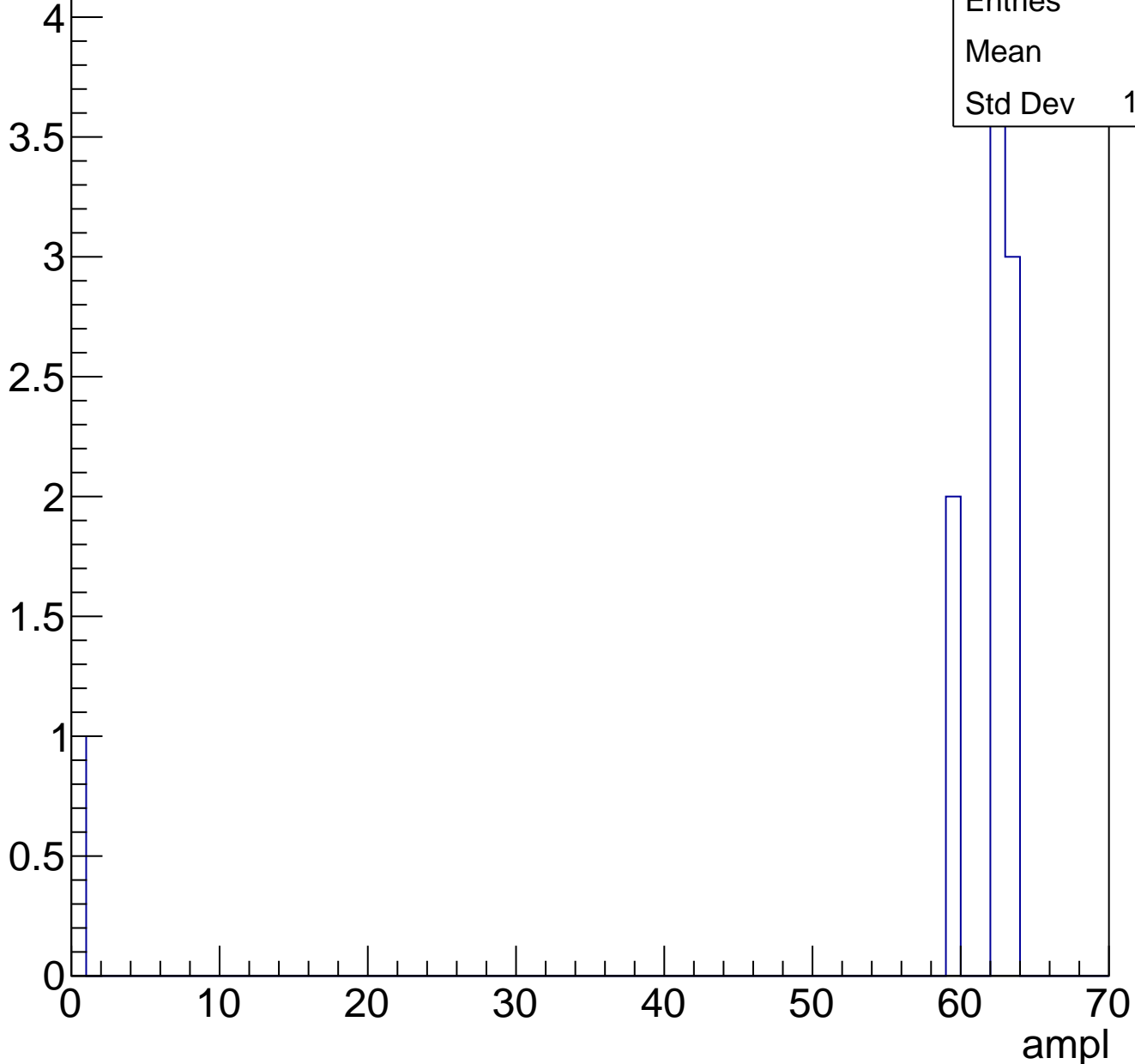
Entries	46
Mean	59.57
Std Dev	2.887



B1L103S, U8-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch114, adc0

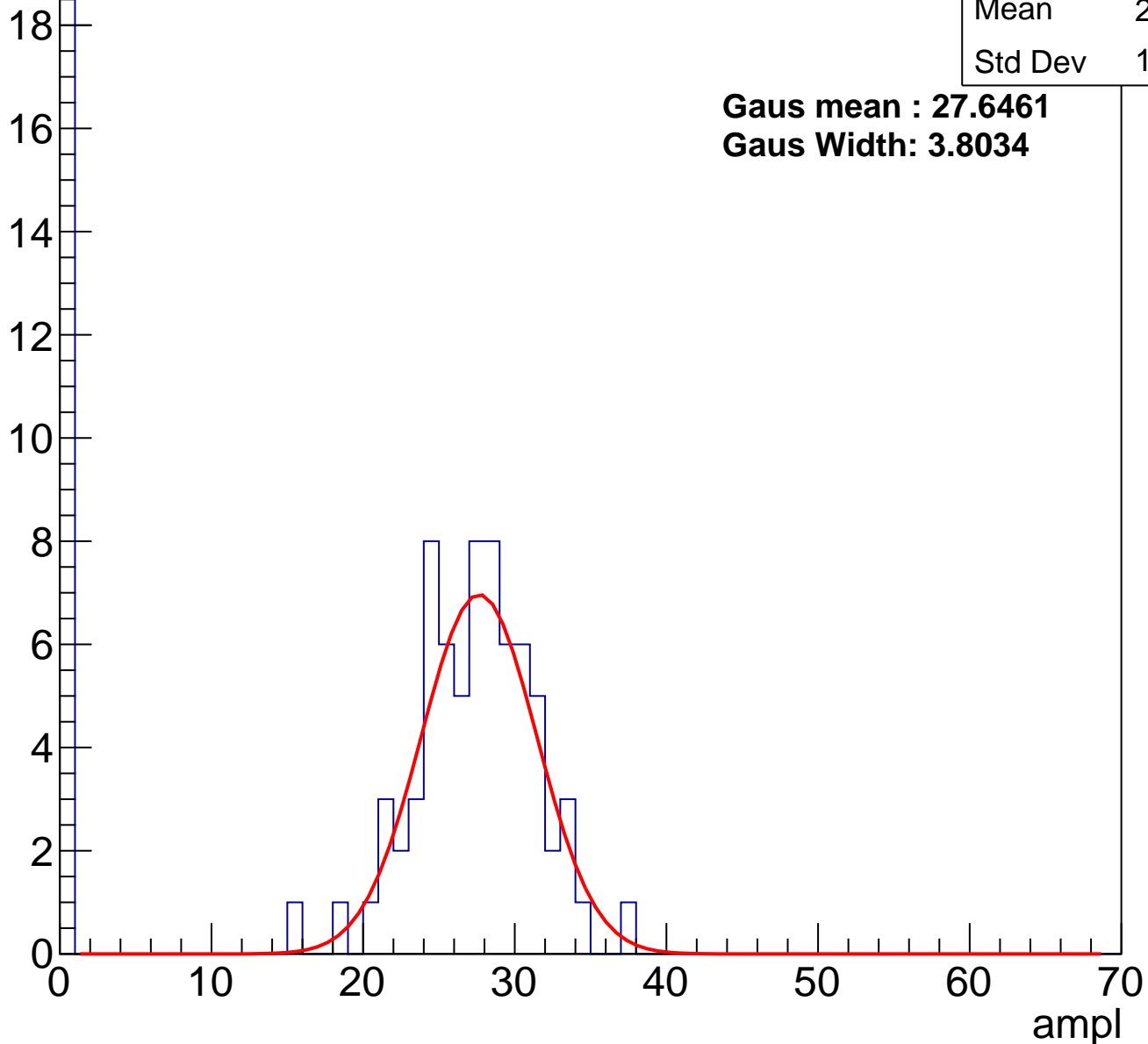
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	21.17
Std Dev	11.55

Gaus mean : 27.6461

Gaus Width: 3.8034

Entry



B1L103S, U8-ch114, adc1

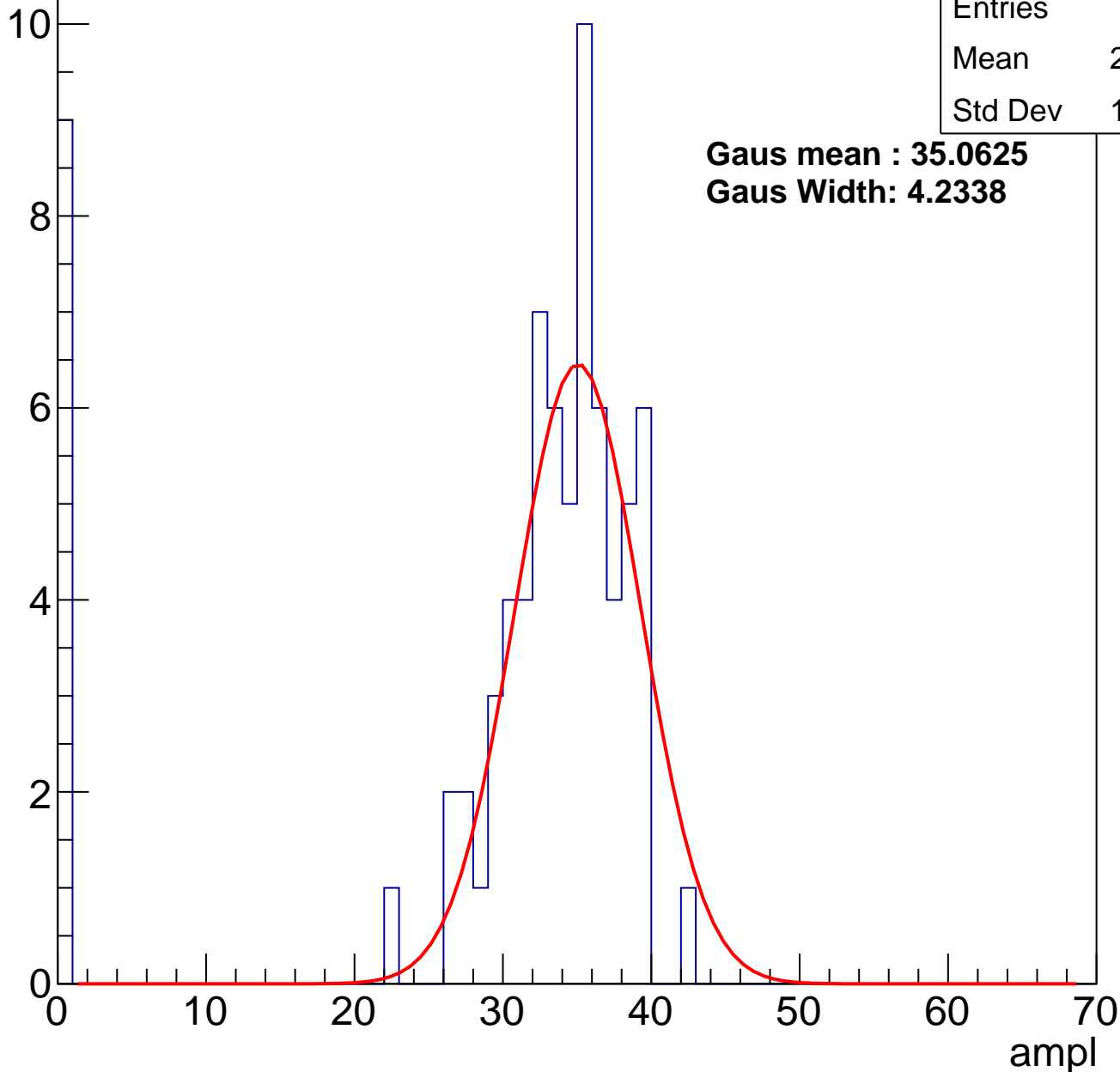
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	29.72
Std Dev	11.47

Gaus mean : 35.0625

Gaus Width: 4.2338

Entry



B1L103S, U8-ch114, adc2

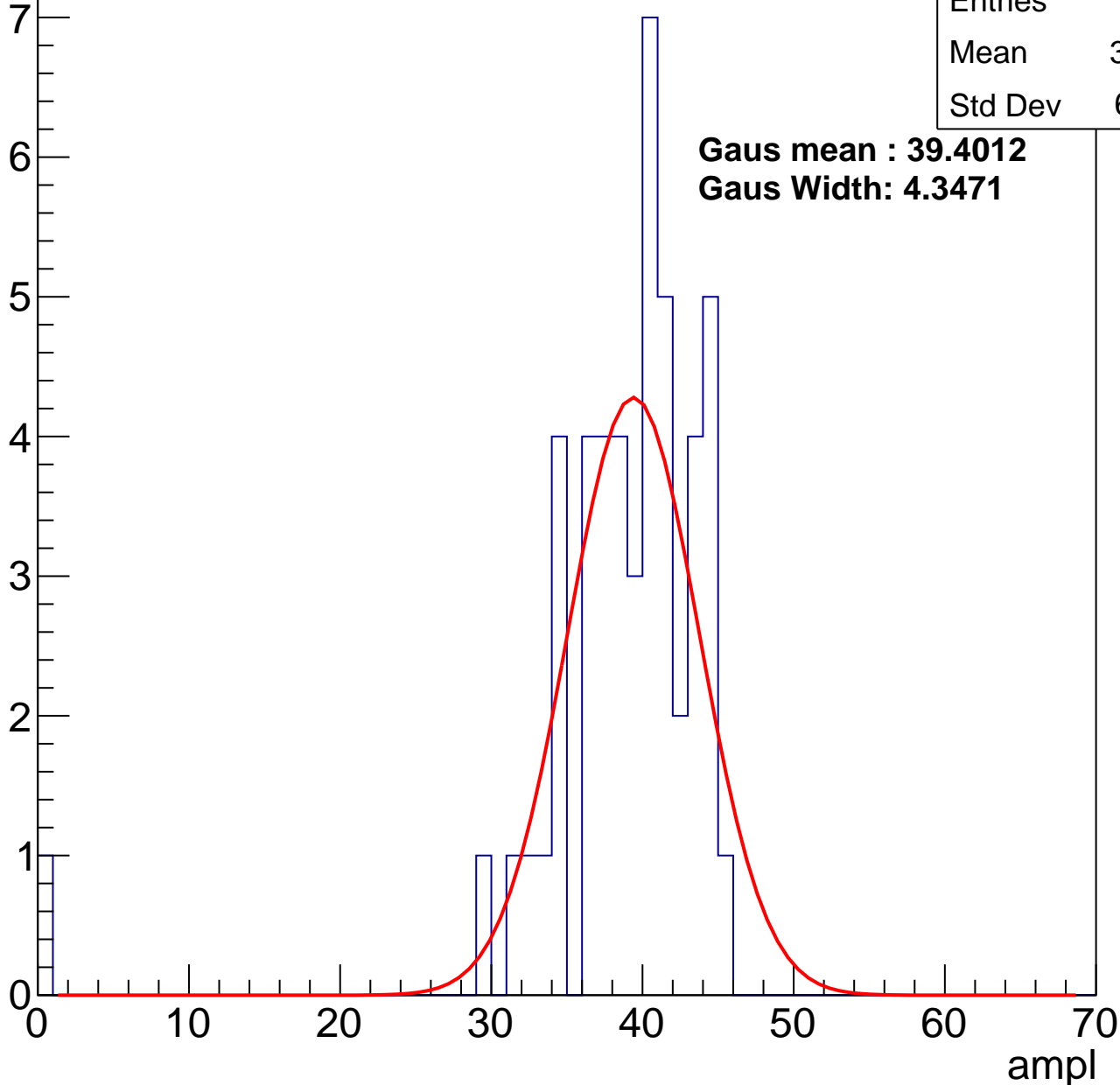
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	38.08
Std Dev	6.701

Gaus mean : 39.4012

Gaus Width: 4.3471

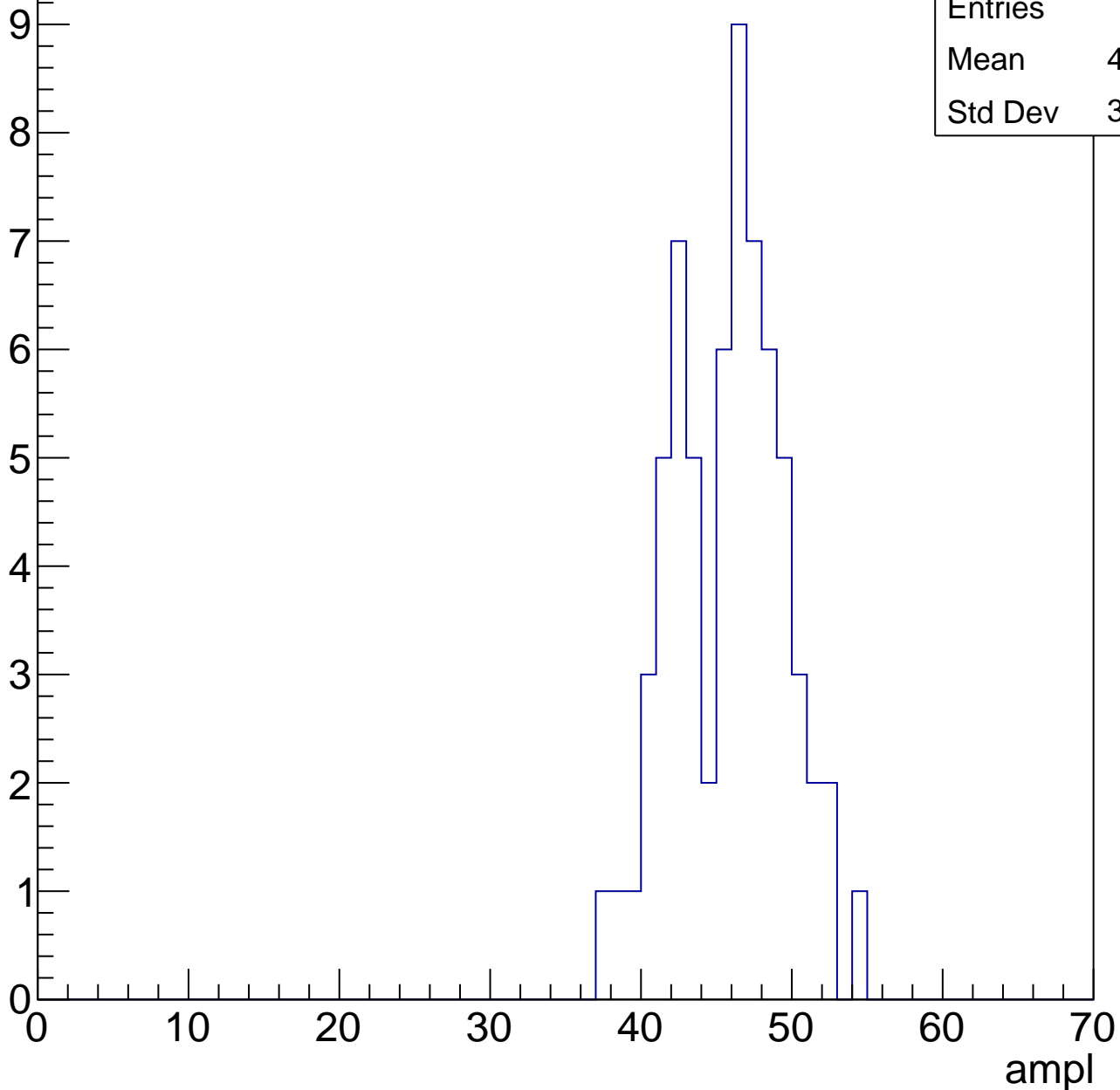


B1L103S, U8-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	45.33
Std Dev	3.649

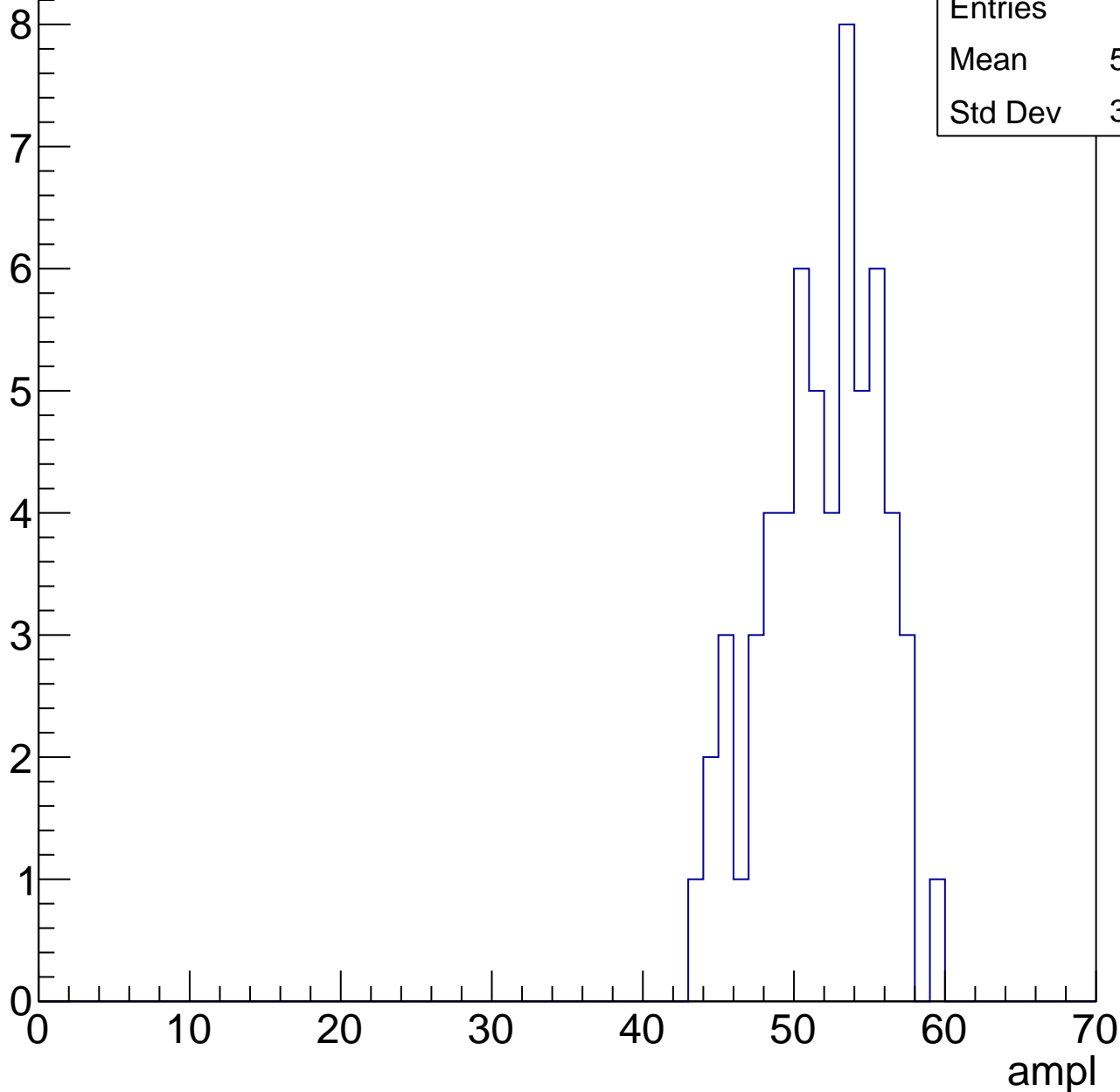


B1L103S, U8-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

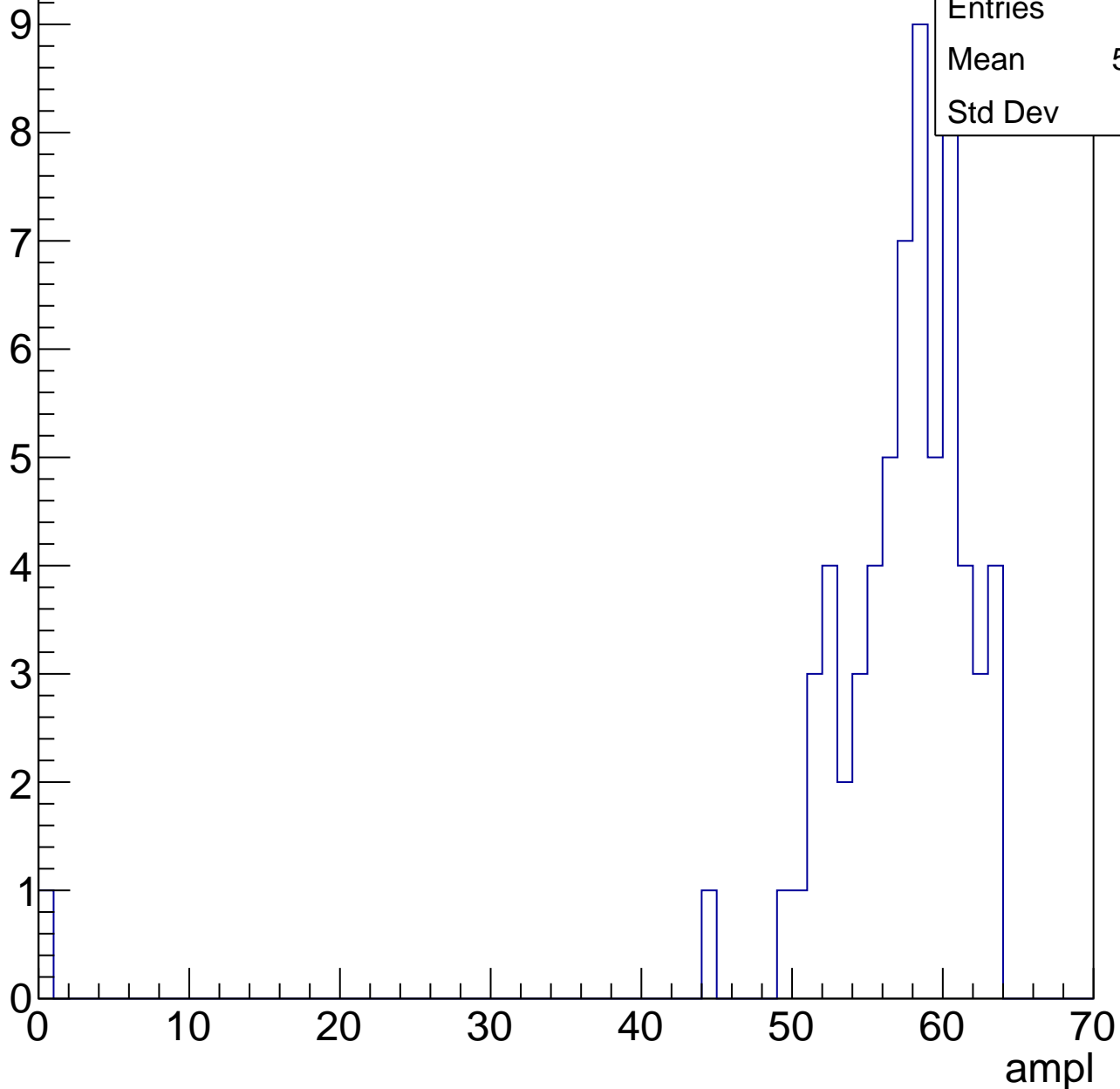
Entries	60
Mean	51.37
Std Dev	3.746



B1L103S, U8-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



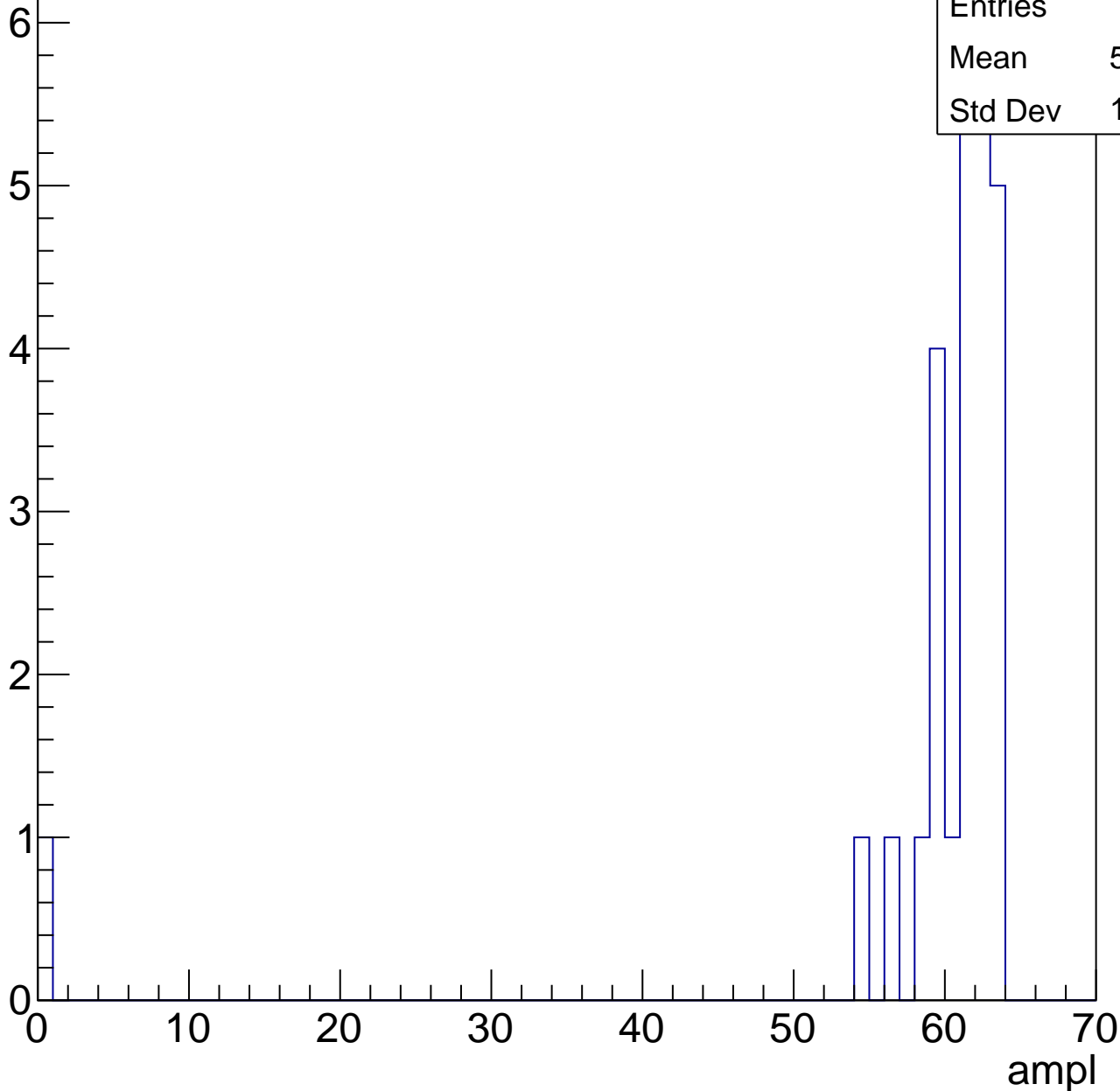
Entries	66
Mean	56.21
Std Dev	7.95

B1L103S, U8-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.35
Std Dev	11.87



B1L103S, U8-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U8-ch115, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	21.73
Std Dev	12.8

Gaus mean : 29.6075

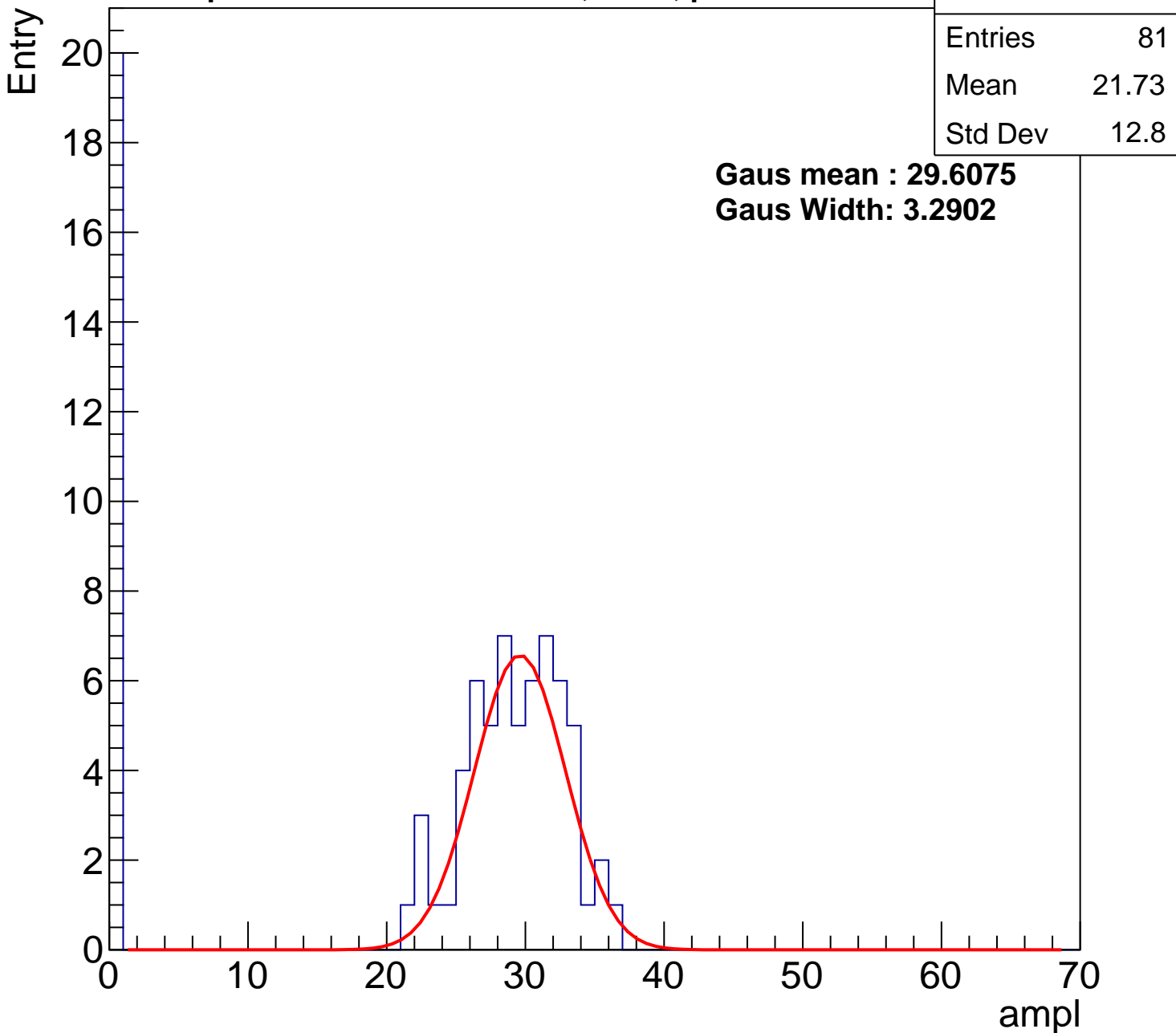
Gaus Width: 3.2902

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch115, adc1

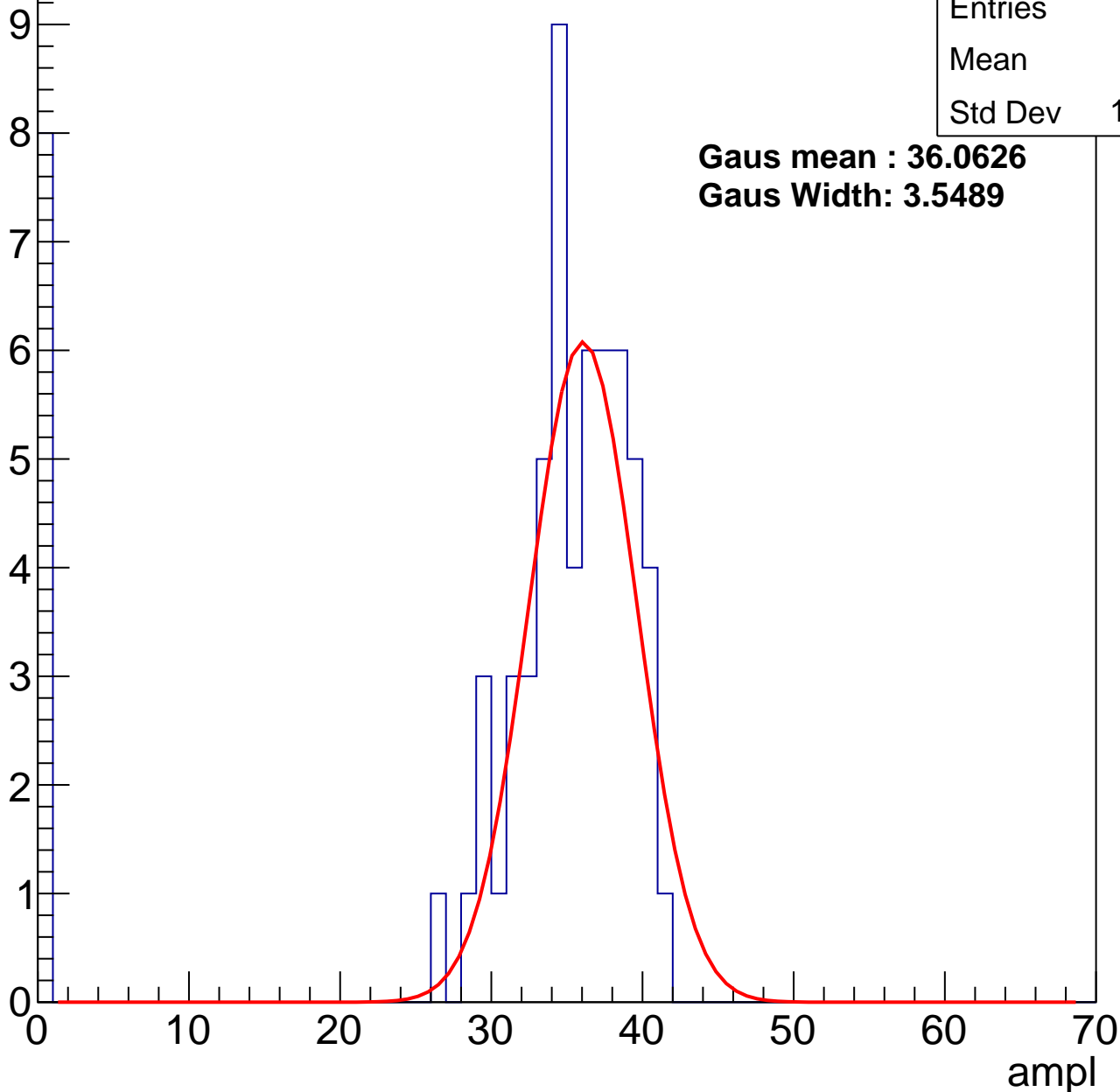
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	30.8
Std Dev	11.87

Gaus mean : 36.0626

Gaus Width: 3.5489



B1L103S, U8-ch115, adc2

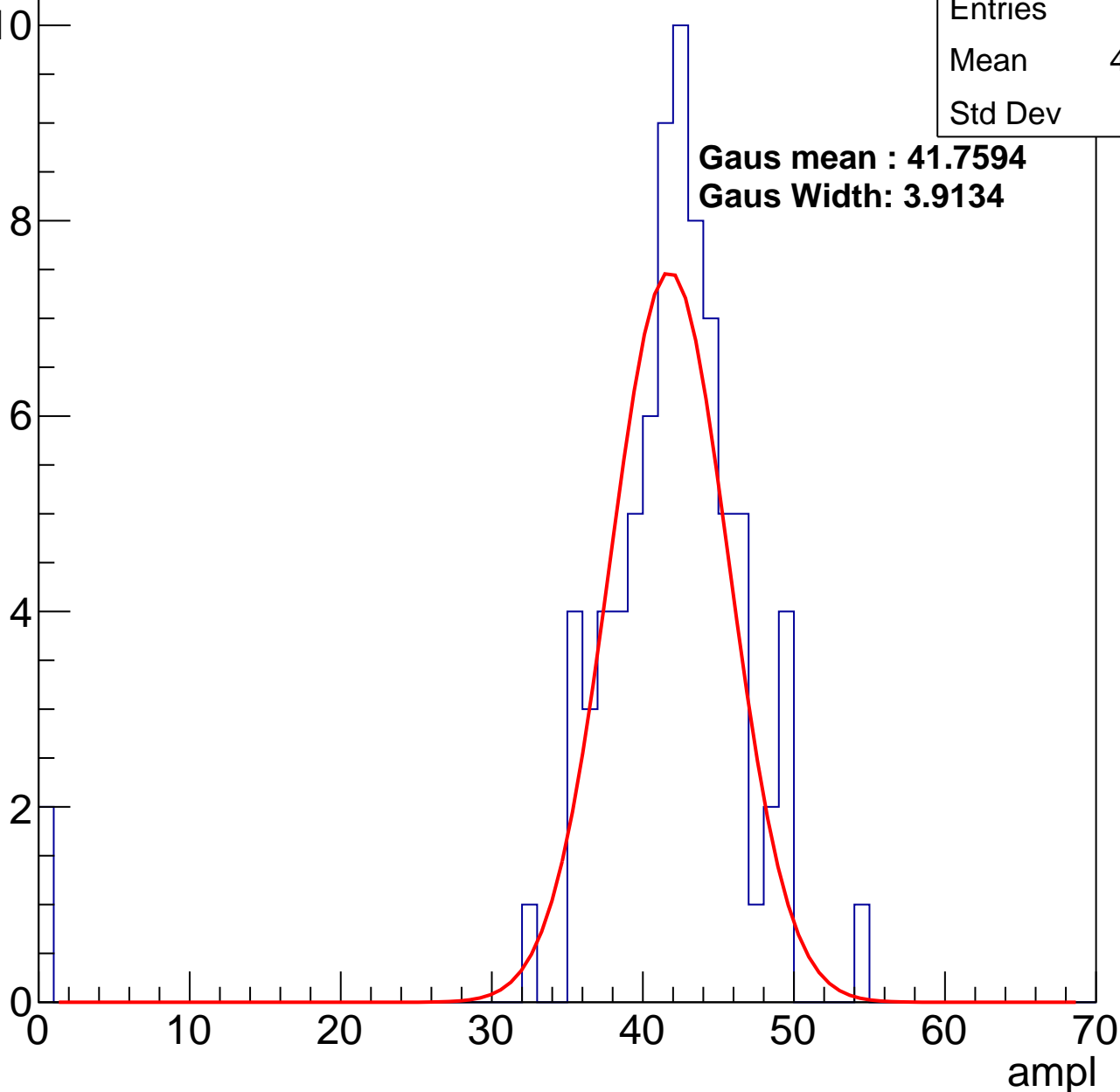
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	40.79
Std Dev	7.58

Gaus mean : 41.7594

Gaus Width: 3.9134

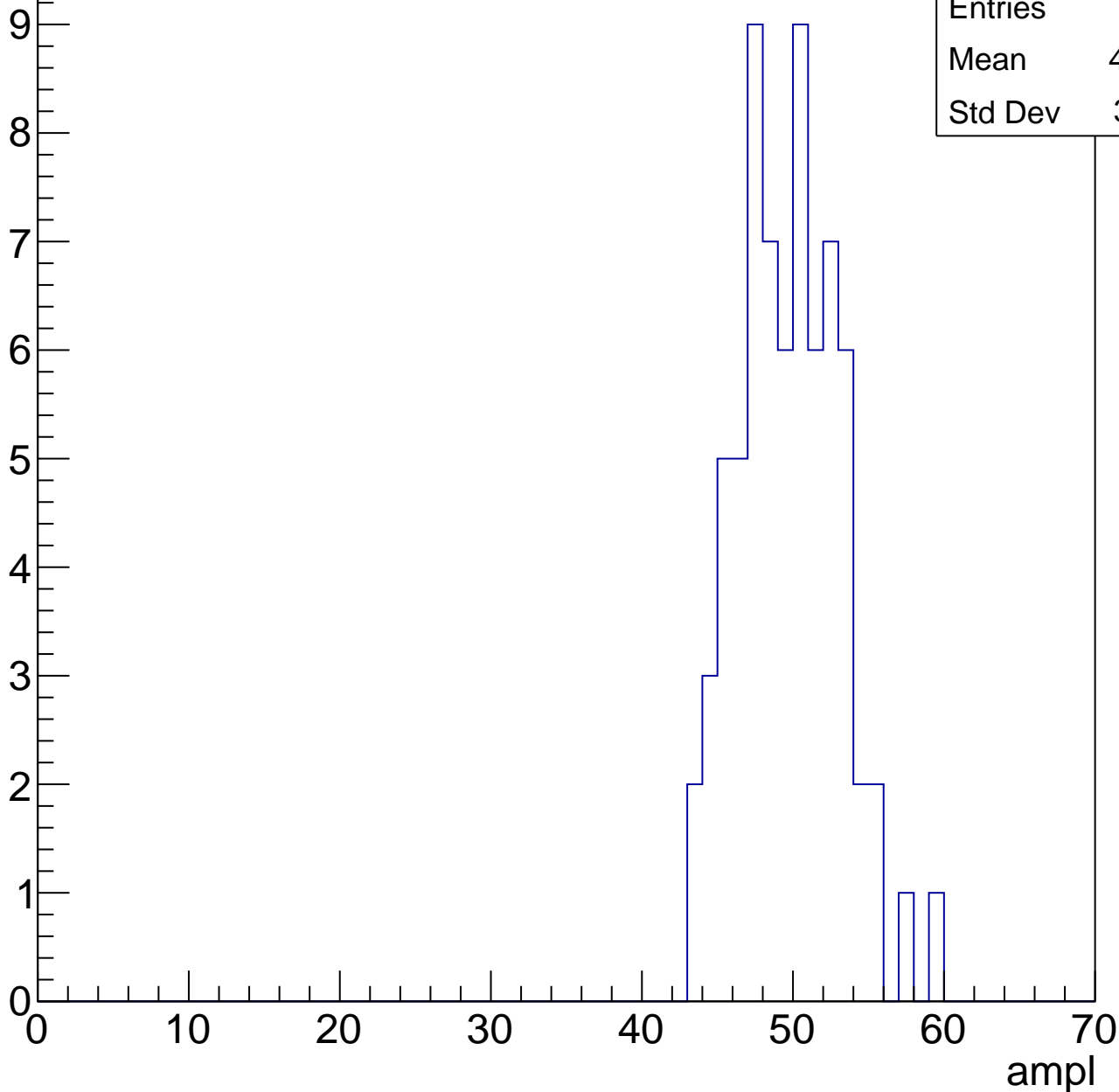


B1L103S, U8-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	49.27
Std Dev	3.331

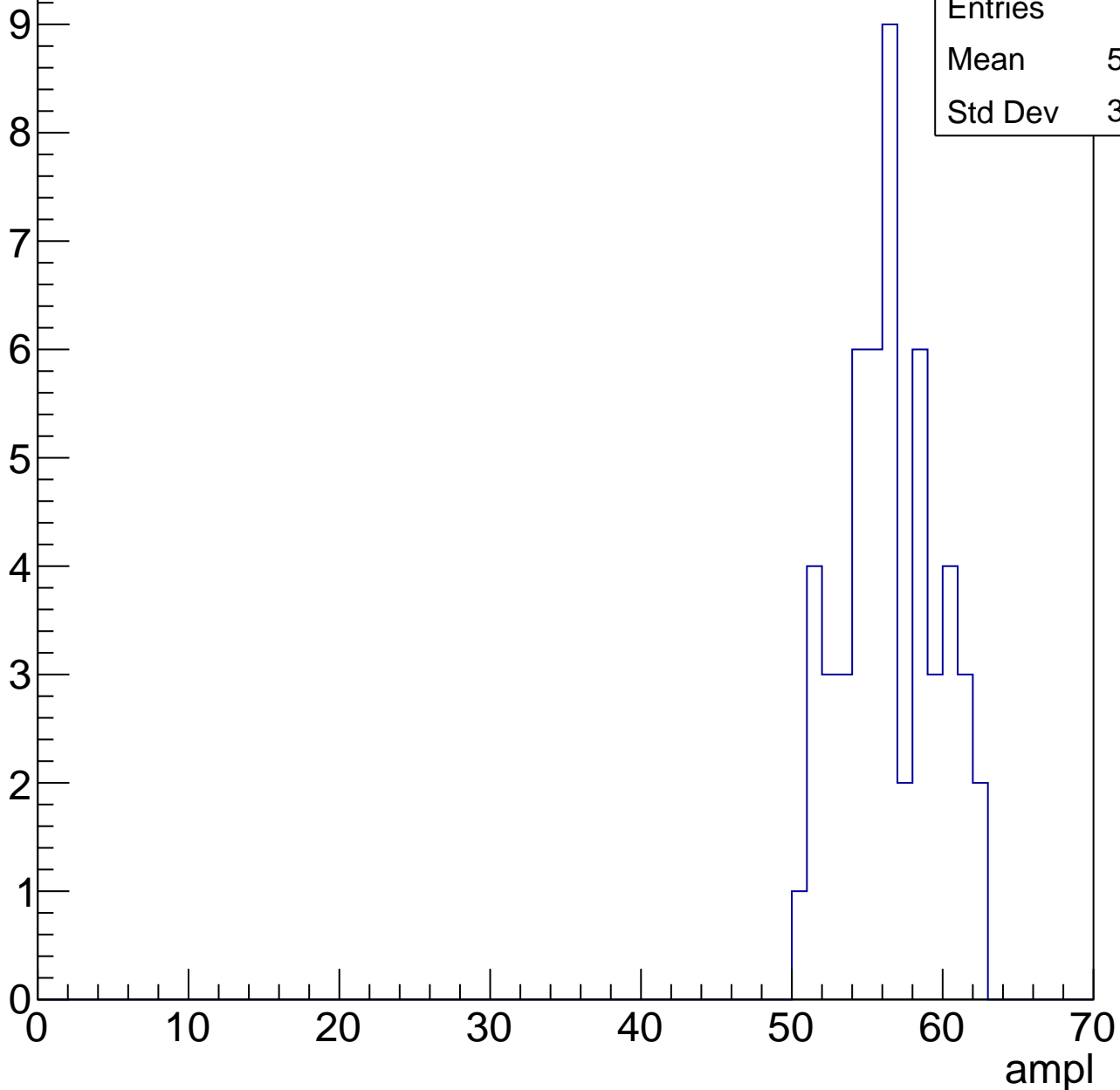


B1L103S, U8-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

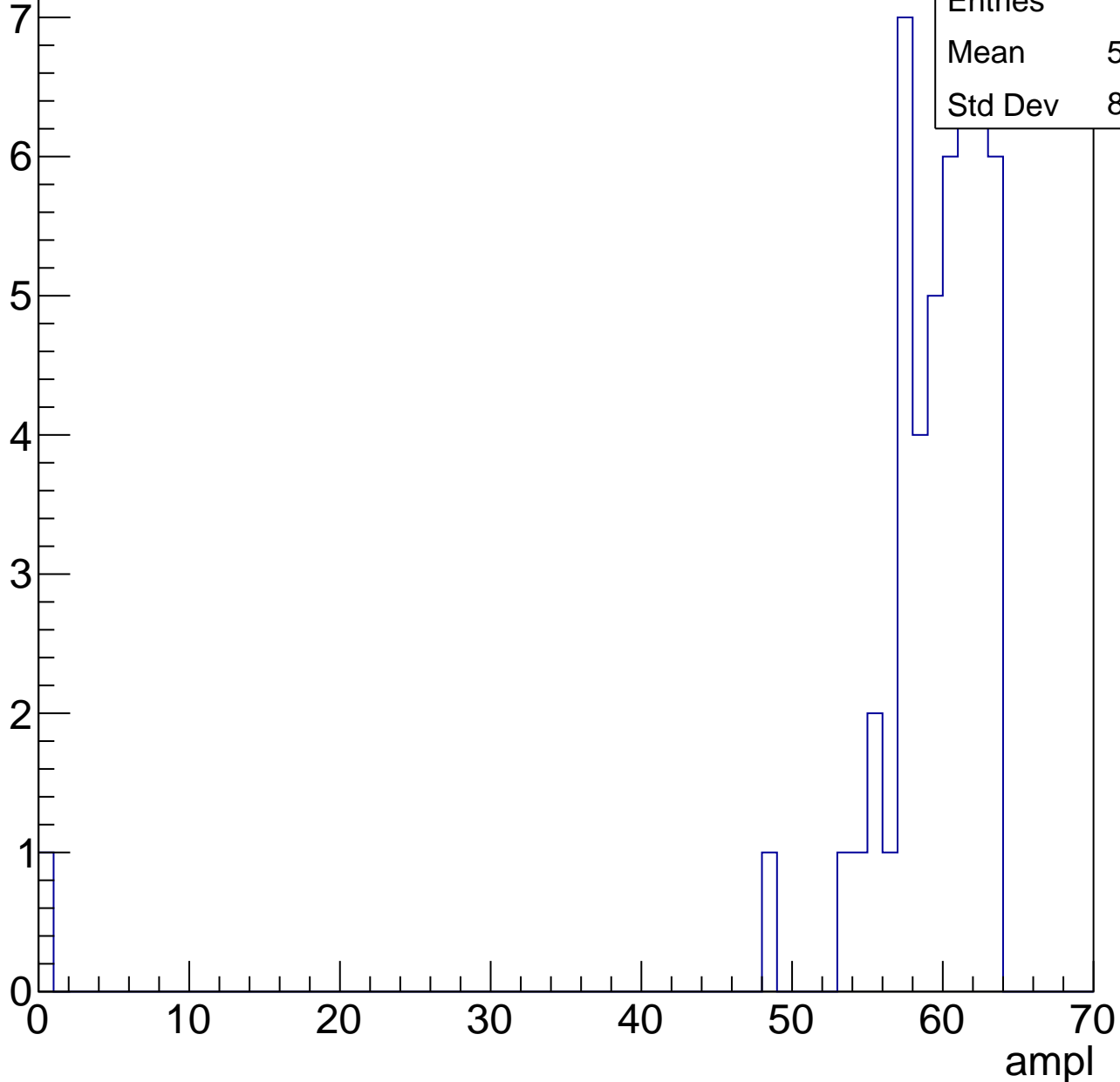
Entries	52
Mean	56.02
Std Dev	3.116



B1L103S, U8-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

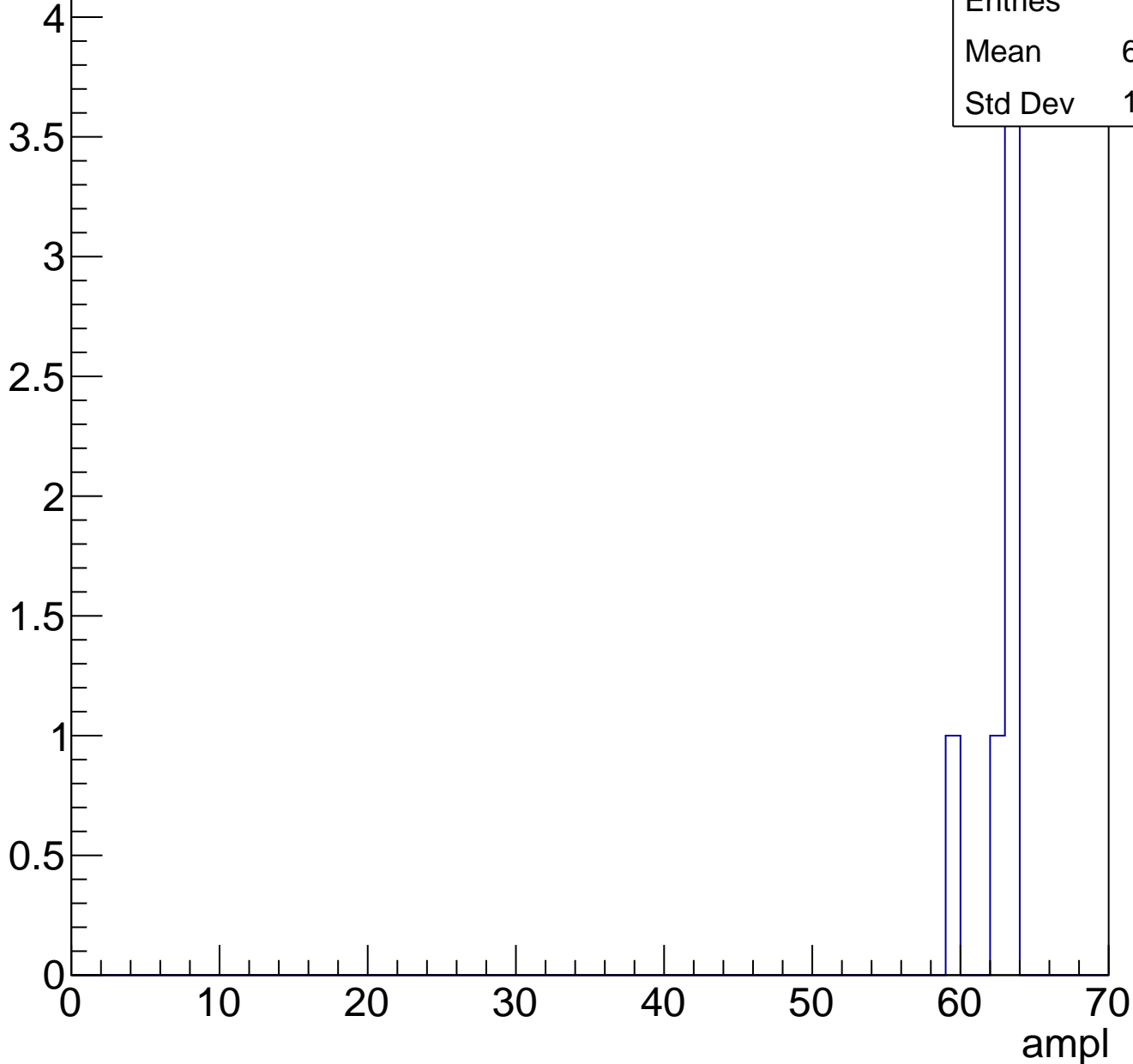
Entry



B1L103S, U8-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

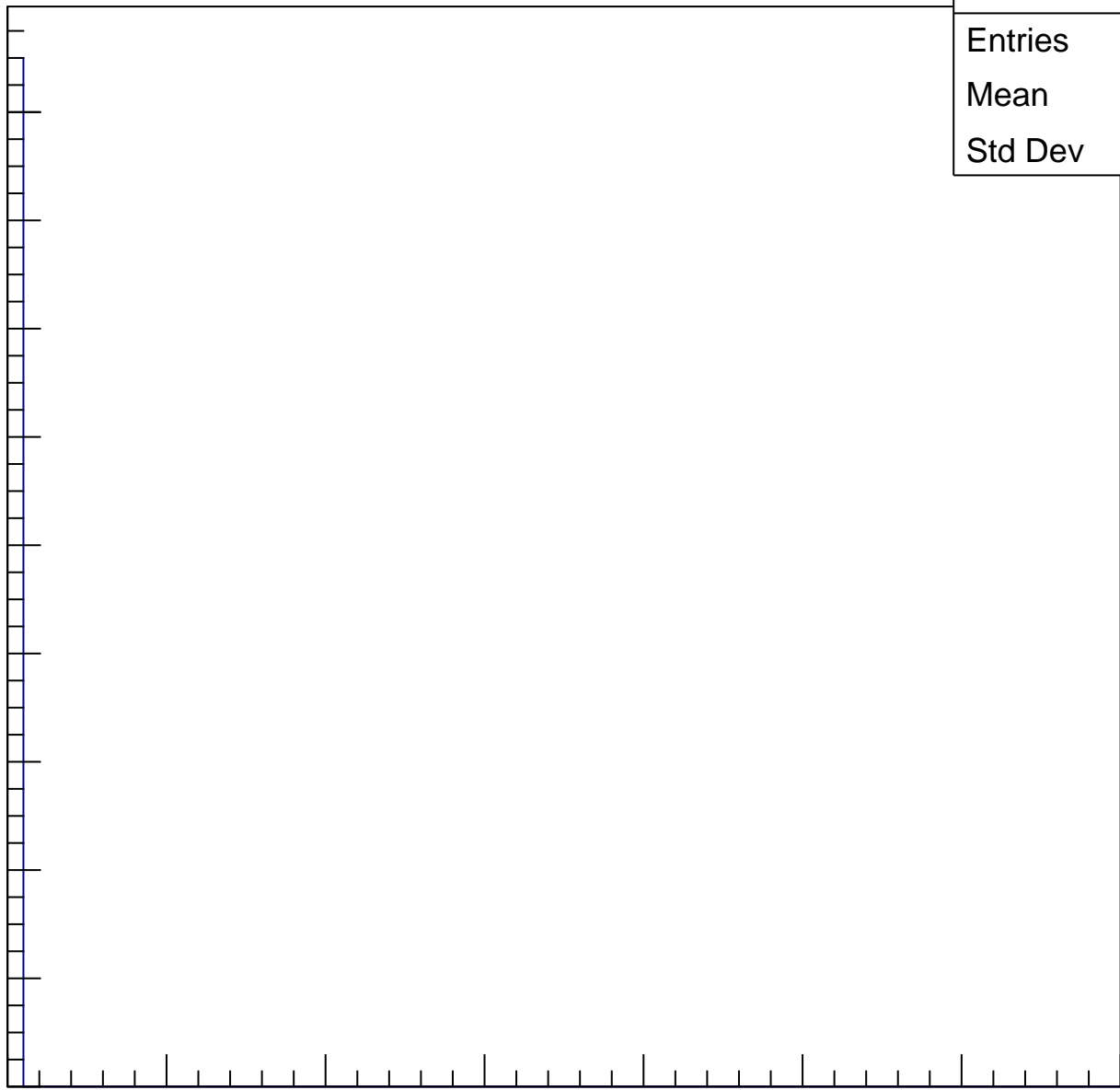
Entries	19
Mean	0
Std Dev	0

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U8-ch116, adc0

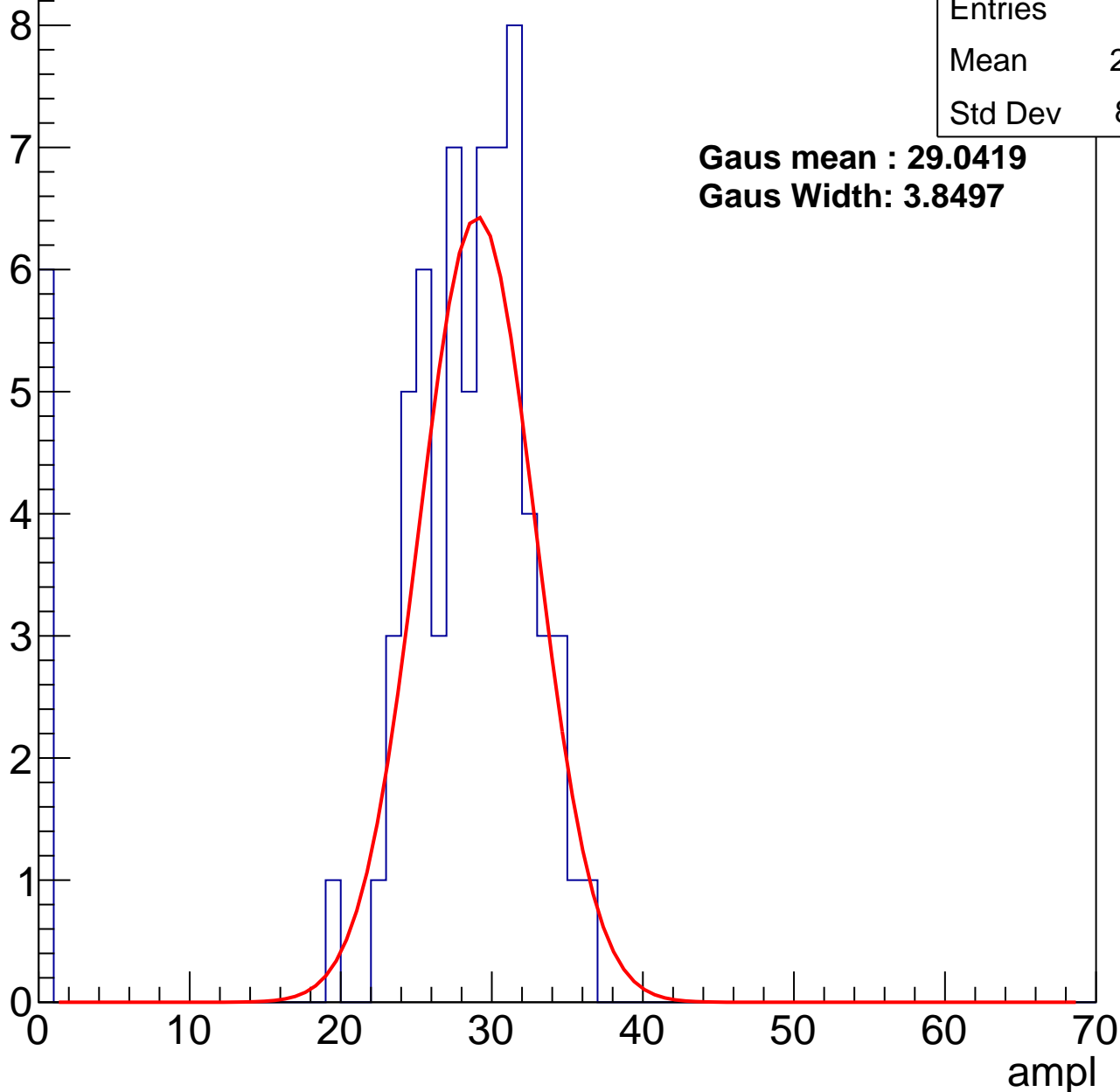
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	26.03
Std Dev	8.591

Gaus mean : 29.0419

Gaus Width: 3.8497



B1L103S, U8-ch116, adc1

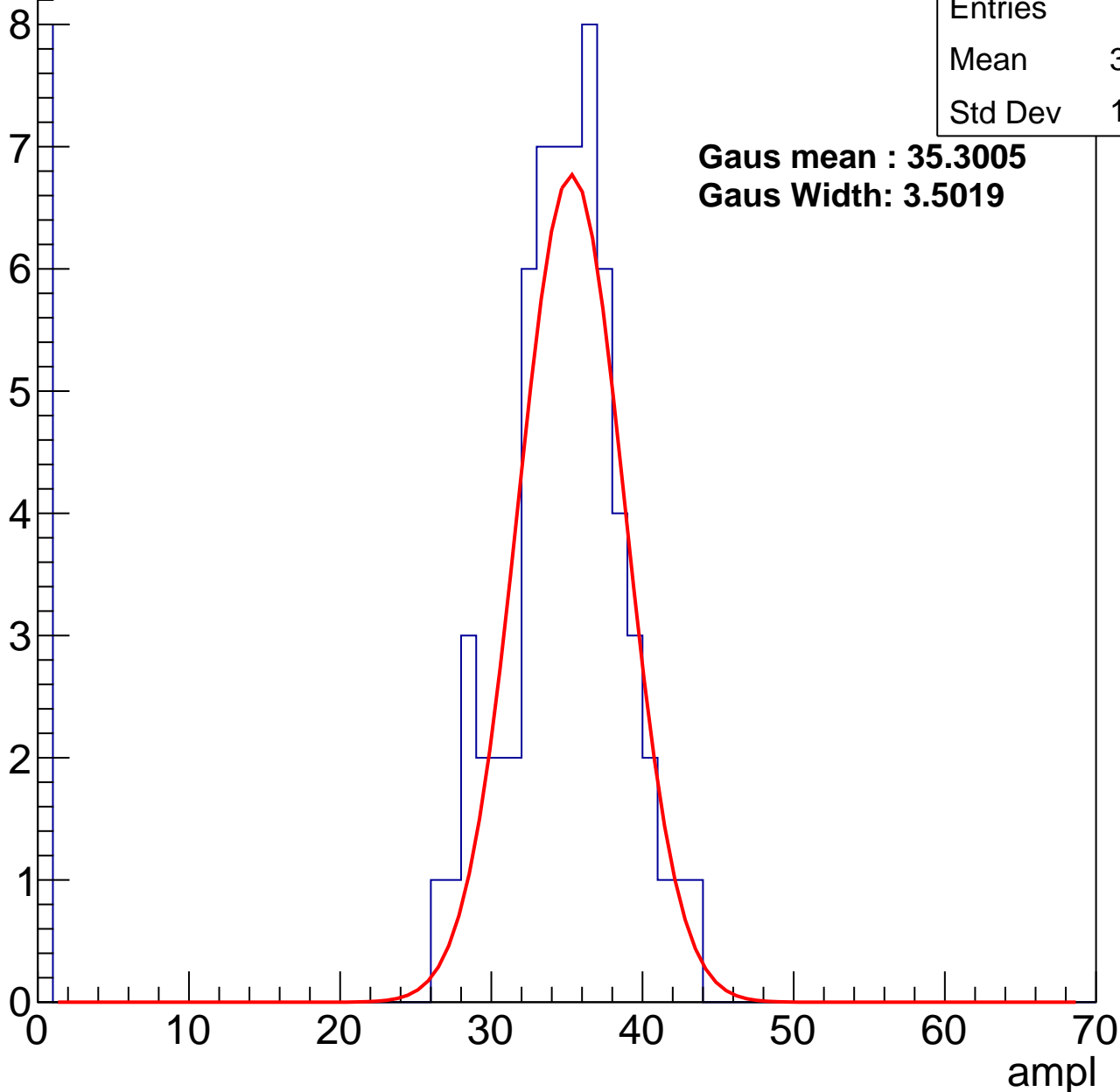
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	30.67
Std Dev	11.36

Gaus mean : 35.3005

Gaus Width: 3.5019



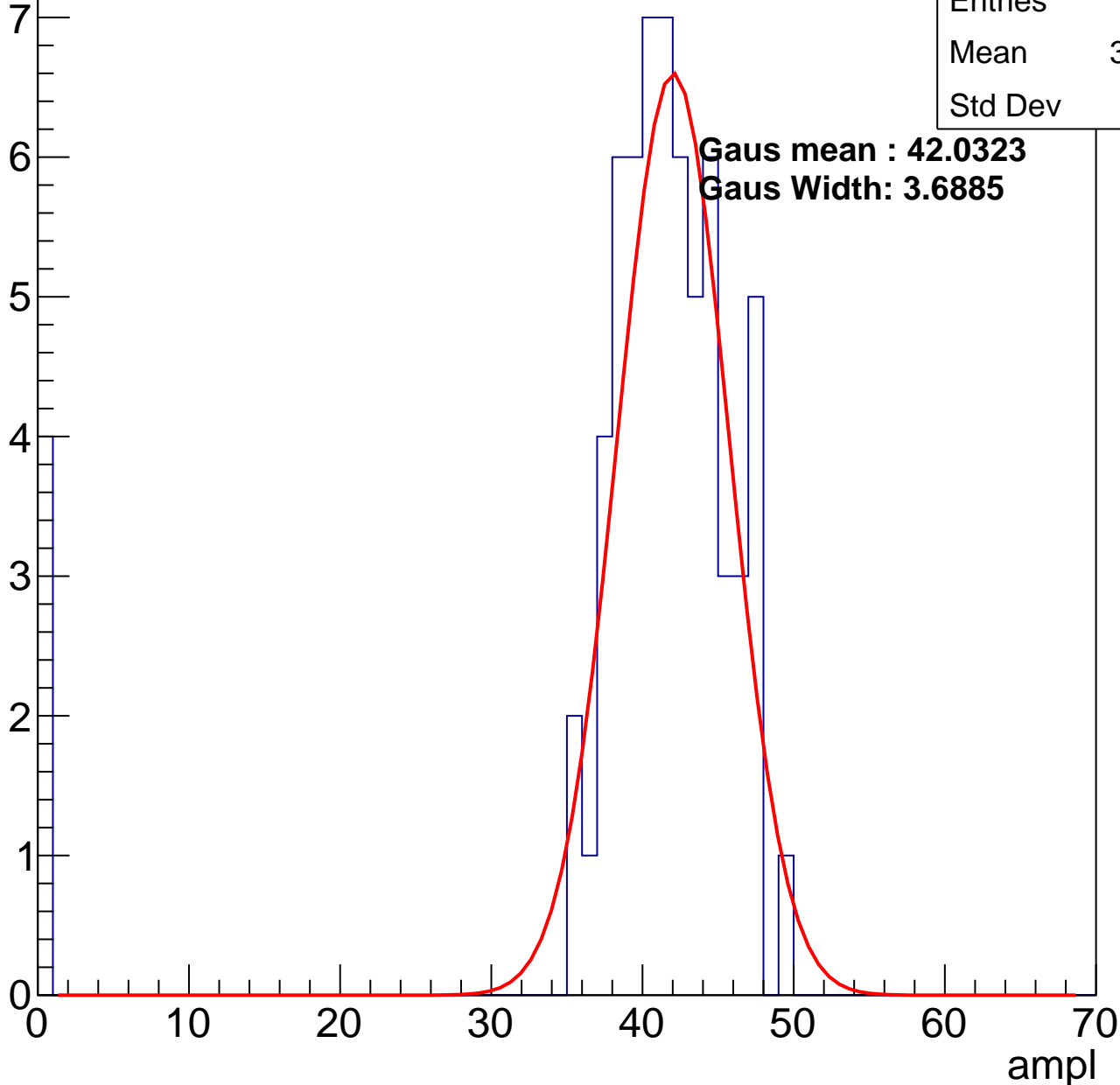
B1L103S, U8-ch116, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	38.95
Std Dev	10.4

Gaus mean : 42.0323
Gaus Width: 3.6885

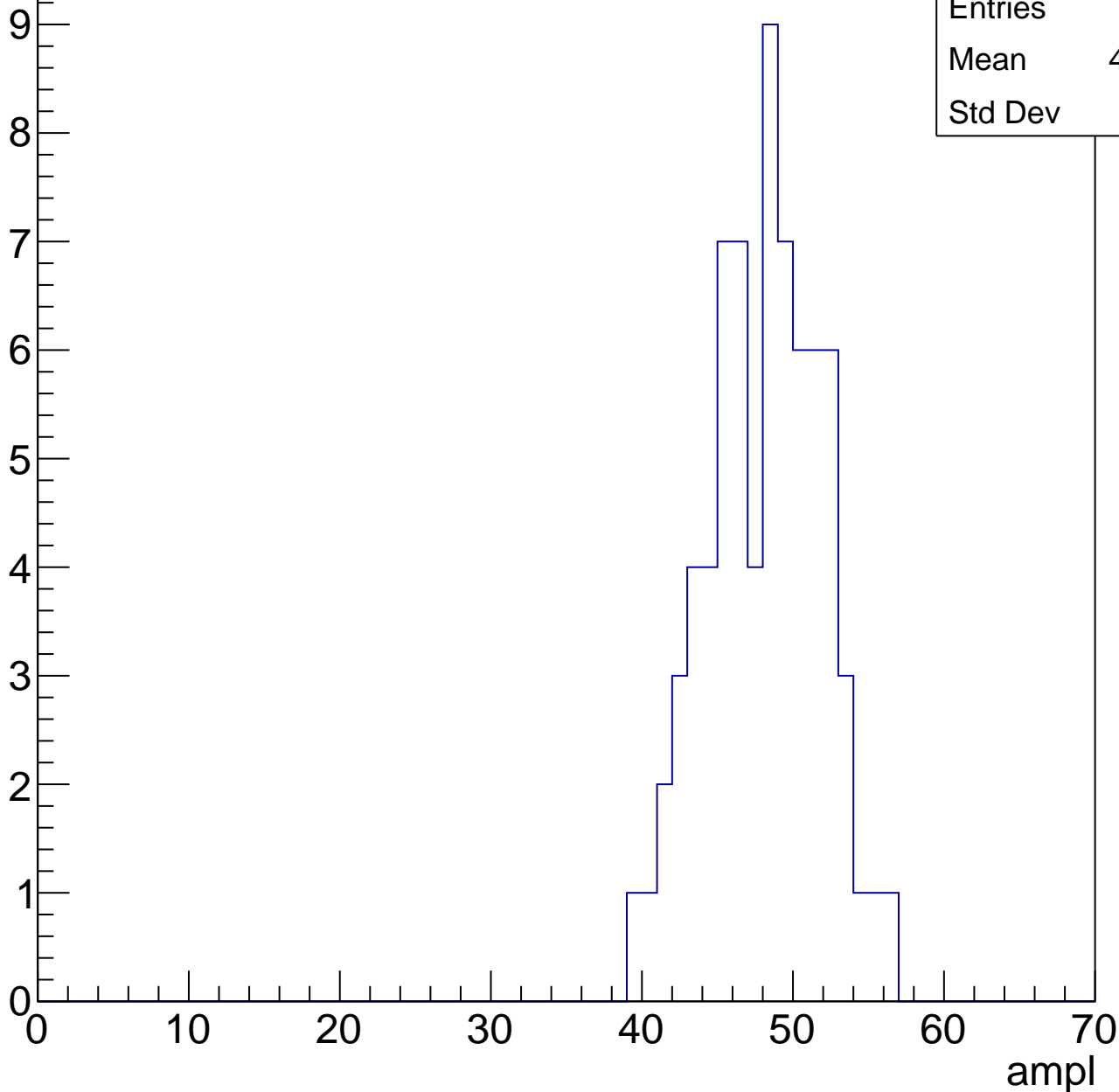


B1L103S, U8-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	47.63
Std Dev	3.71

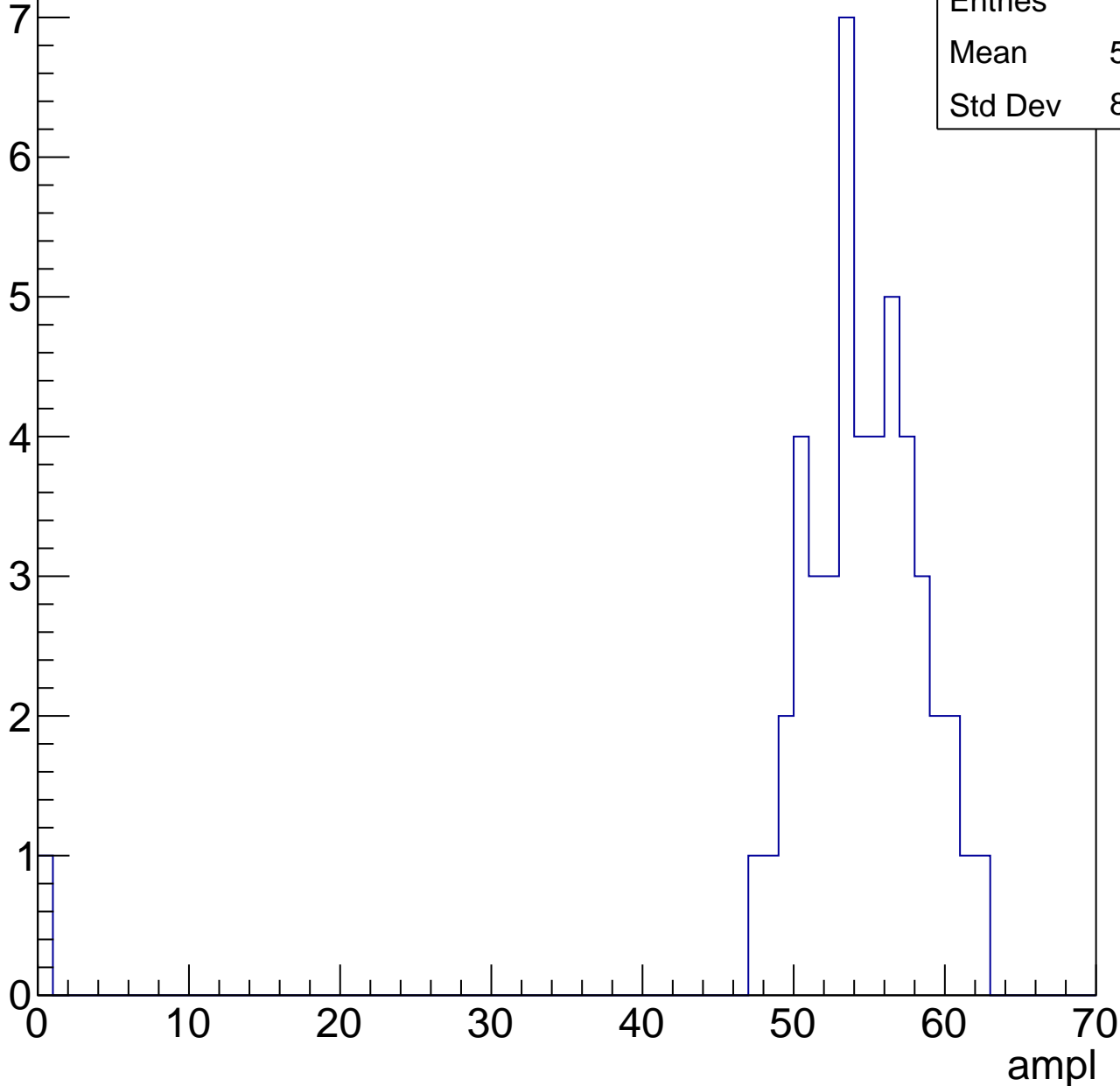


B1L103S, U8-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	53.17
Std Dev	8.503

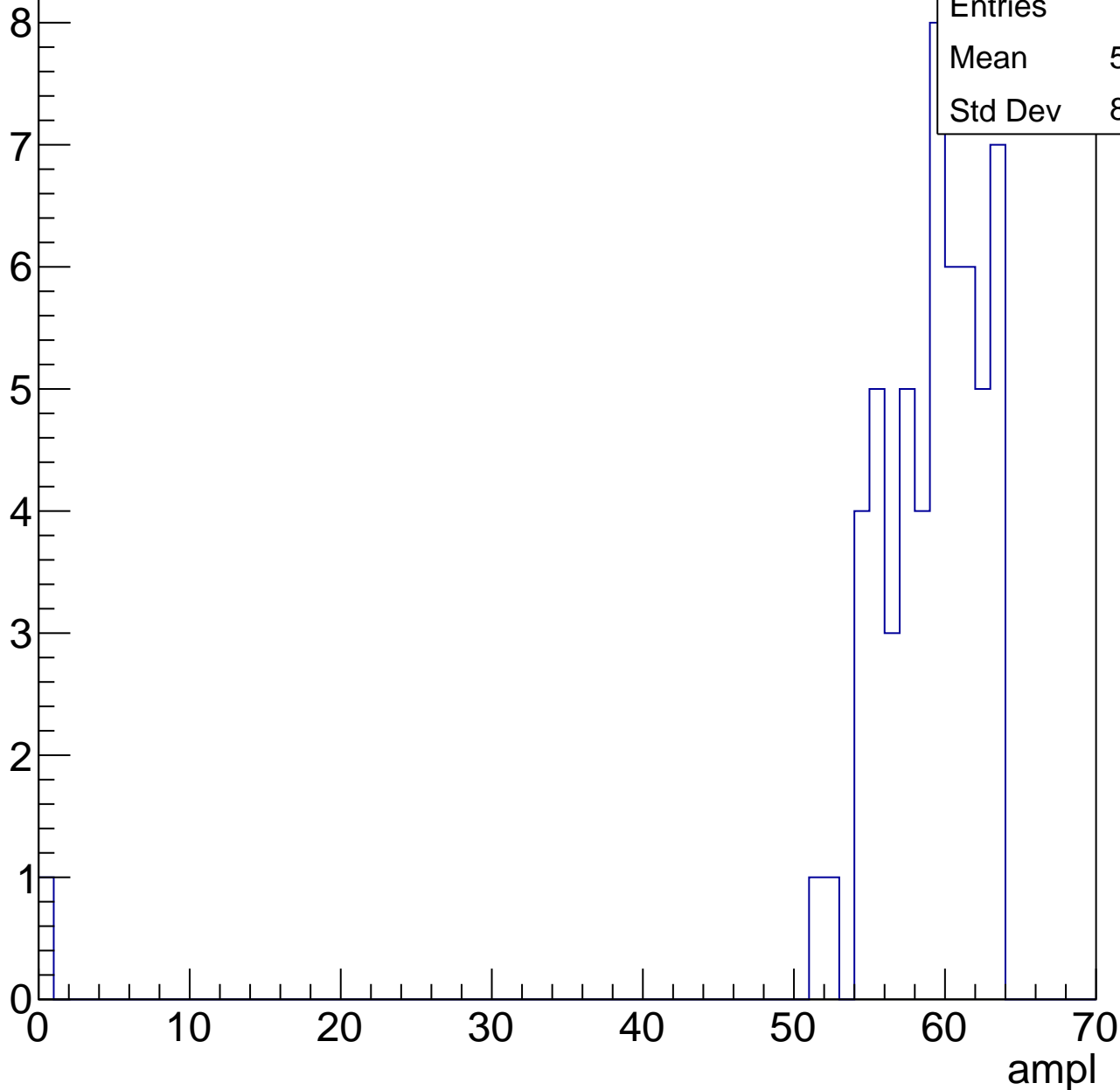


B1L103S, U8-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

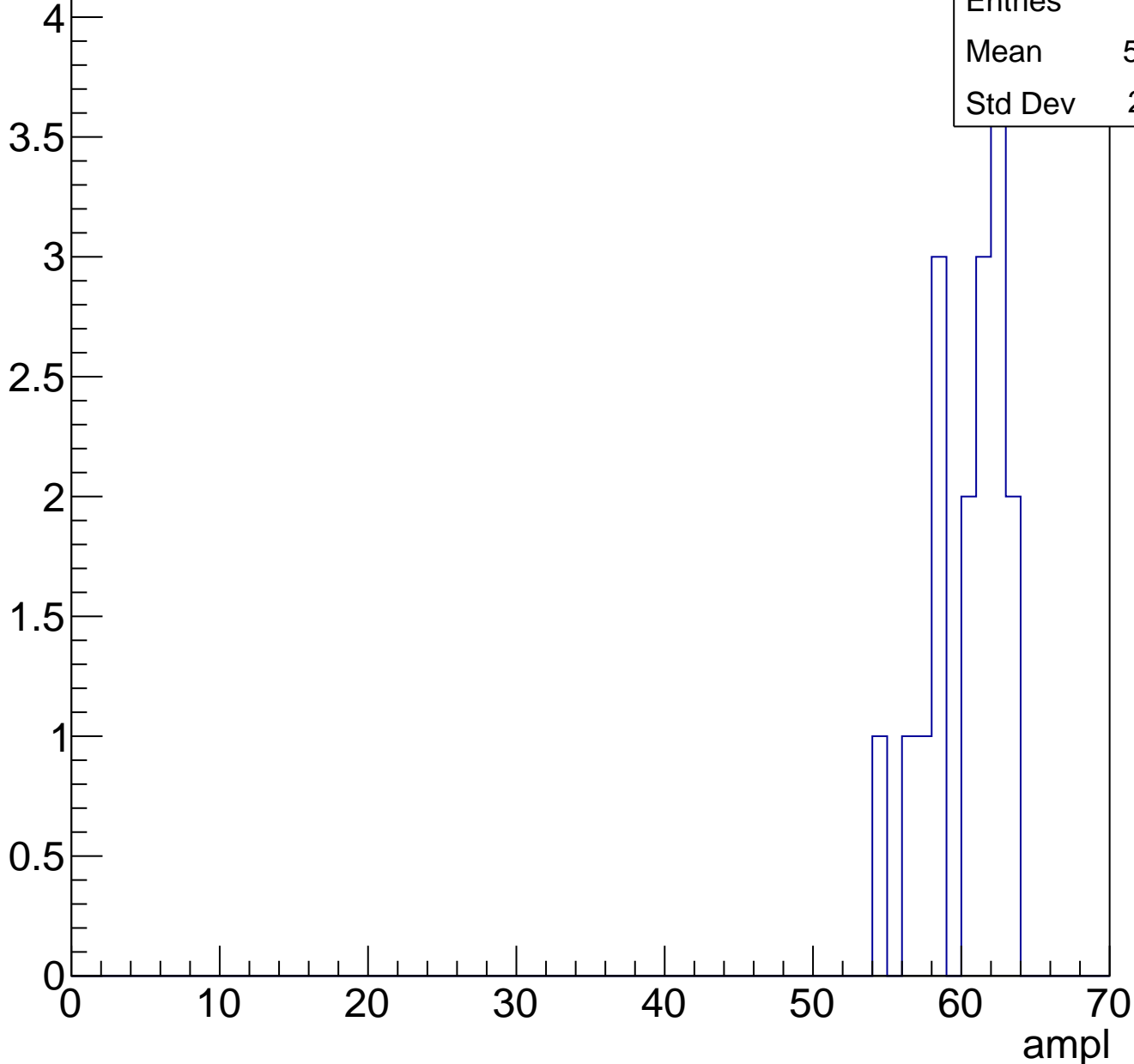
Entries	56
Mean	57.64
Std Dev	8.355



B1L103S, U8-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U8-ch117, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	20.14
Std Dev	13

Gaus mean : 27.2788

Gaus Width: 3.2348

Entry

25

20

15

10

5

0

0

10

20

30

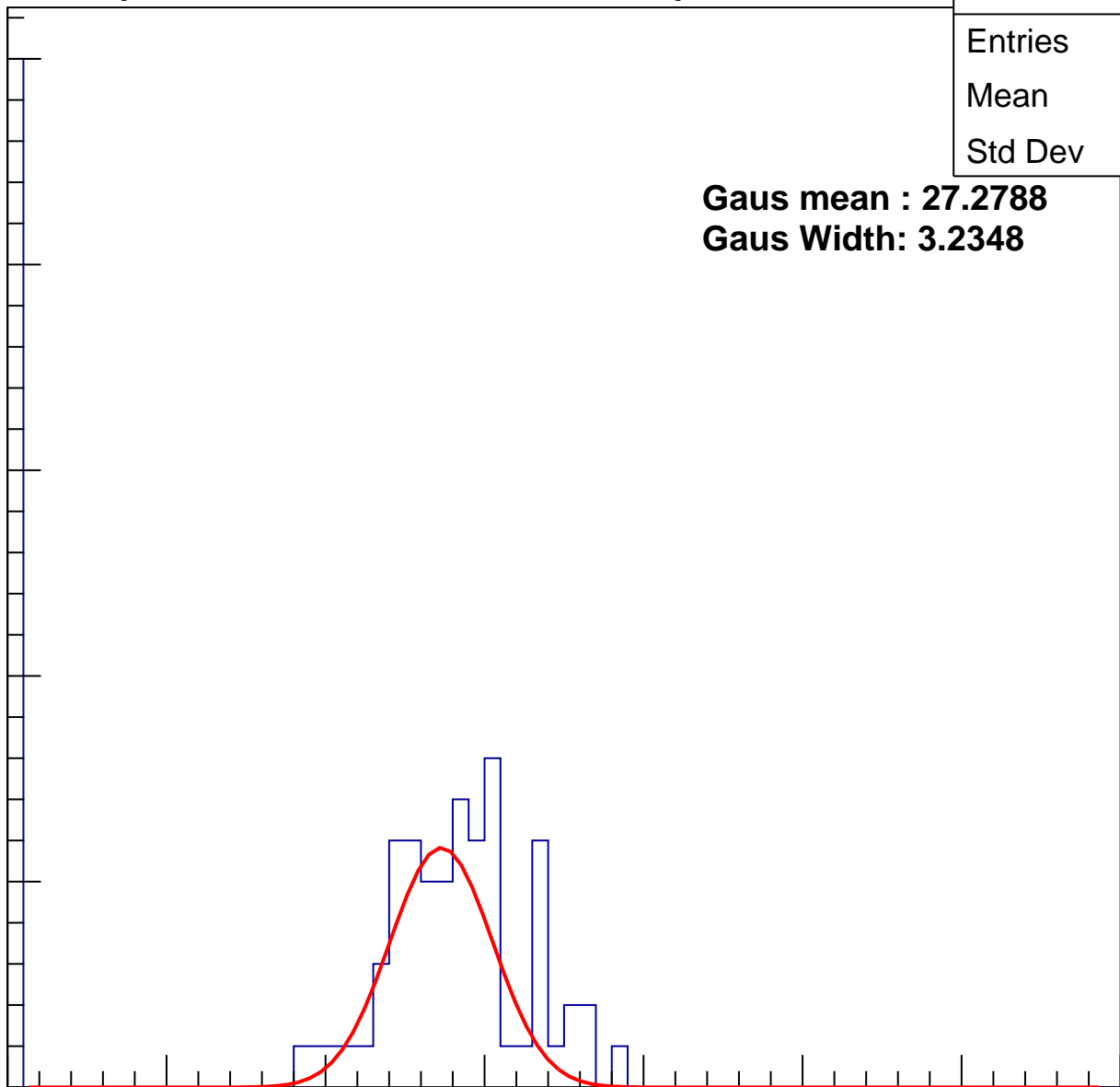
40

50

60

70

ampl



B1L103S, U8-ch117, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	30.35
Std Dev	11.83

Gaus mean : 35.2592

Gaus Width: 4.3670

Entry

10

8

6

4

2

0

0

10

20

30

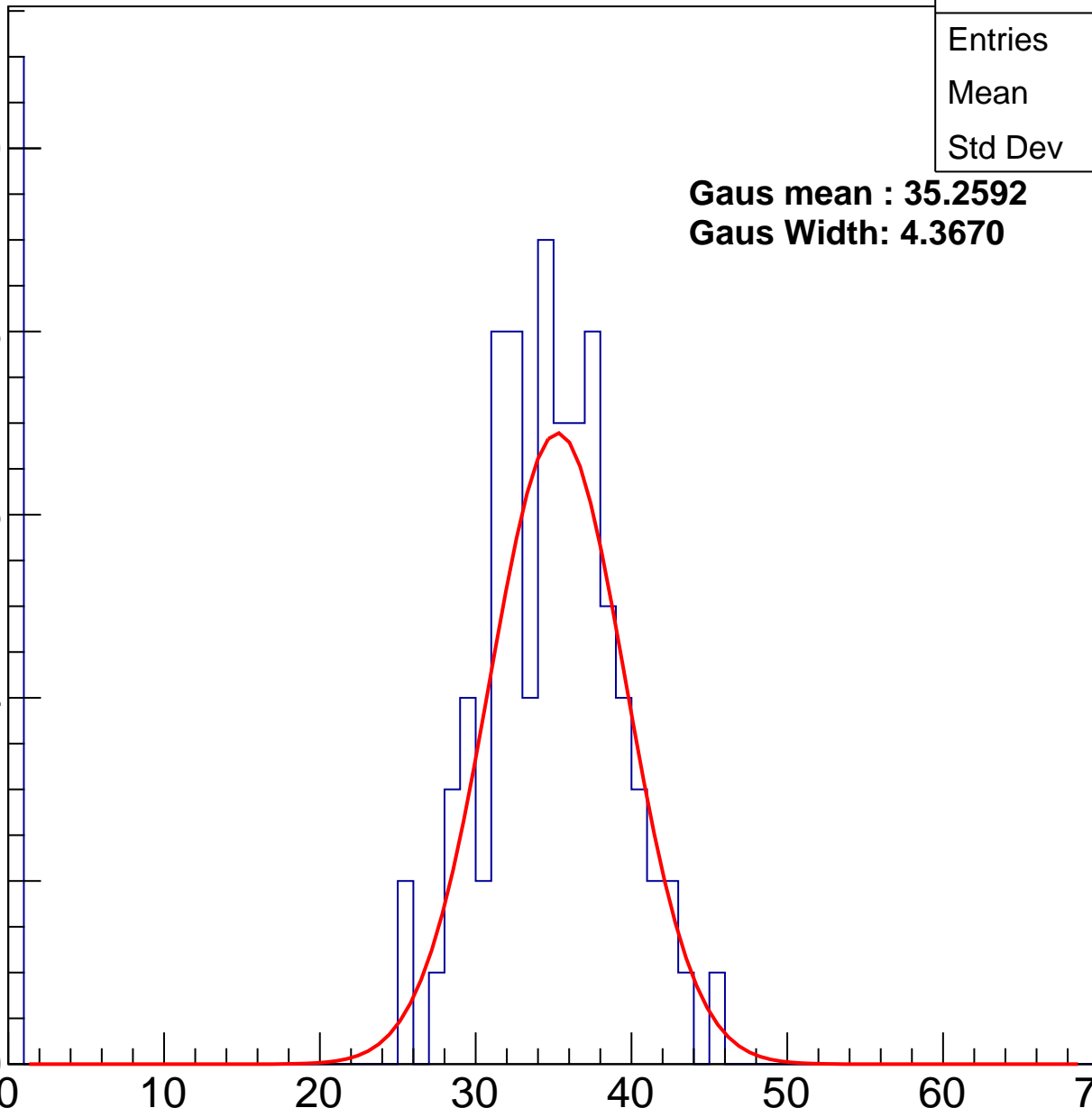
40

50

60

70

ampl



B1L103S, U8-ch117, adc2

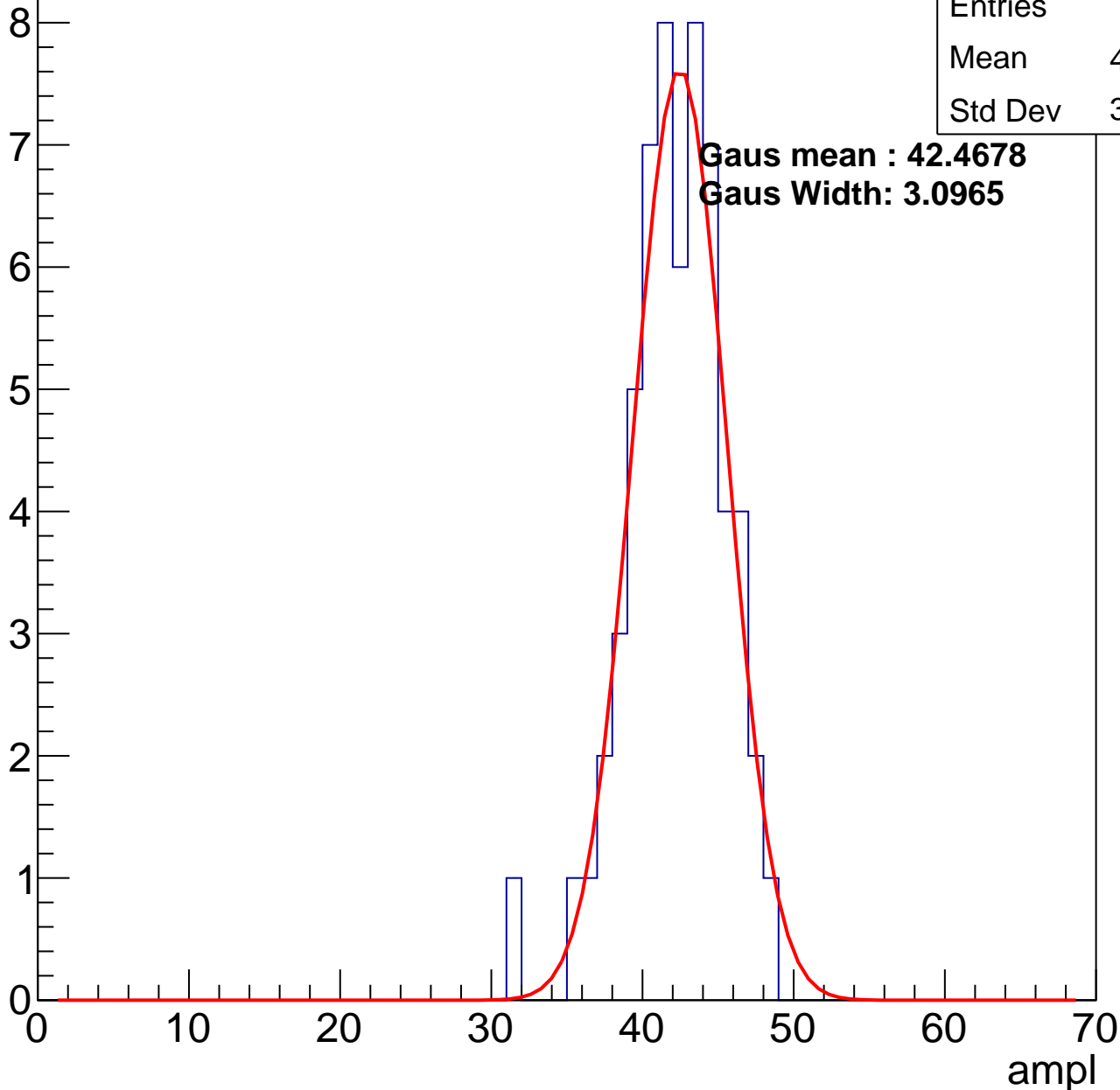
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	41.72
Std Dev	3.168

Gaus mean : 42.4678

Gaus Width: 3.0965

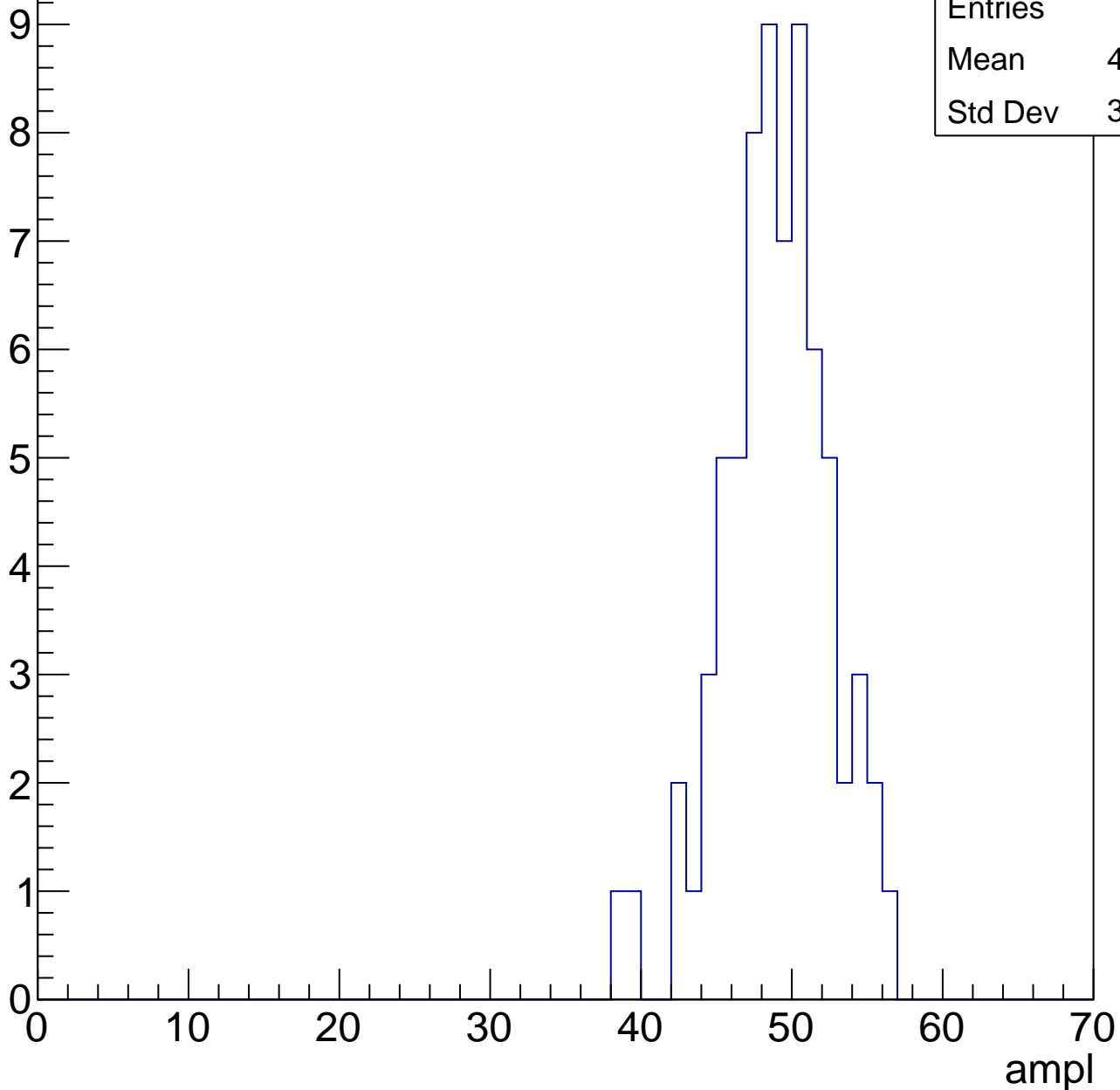


B1L103S, U8-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	48.46
Std Dev	3.548

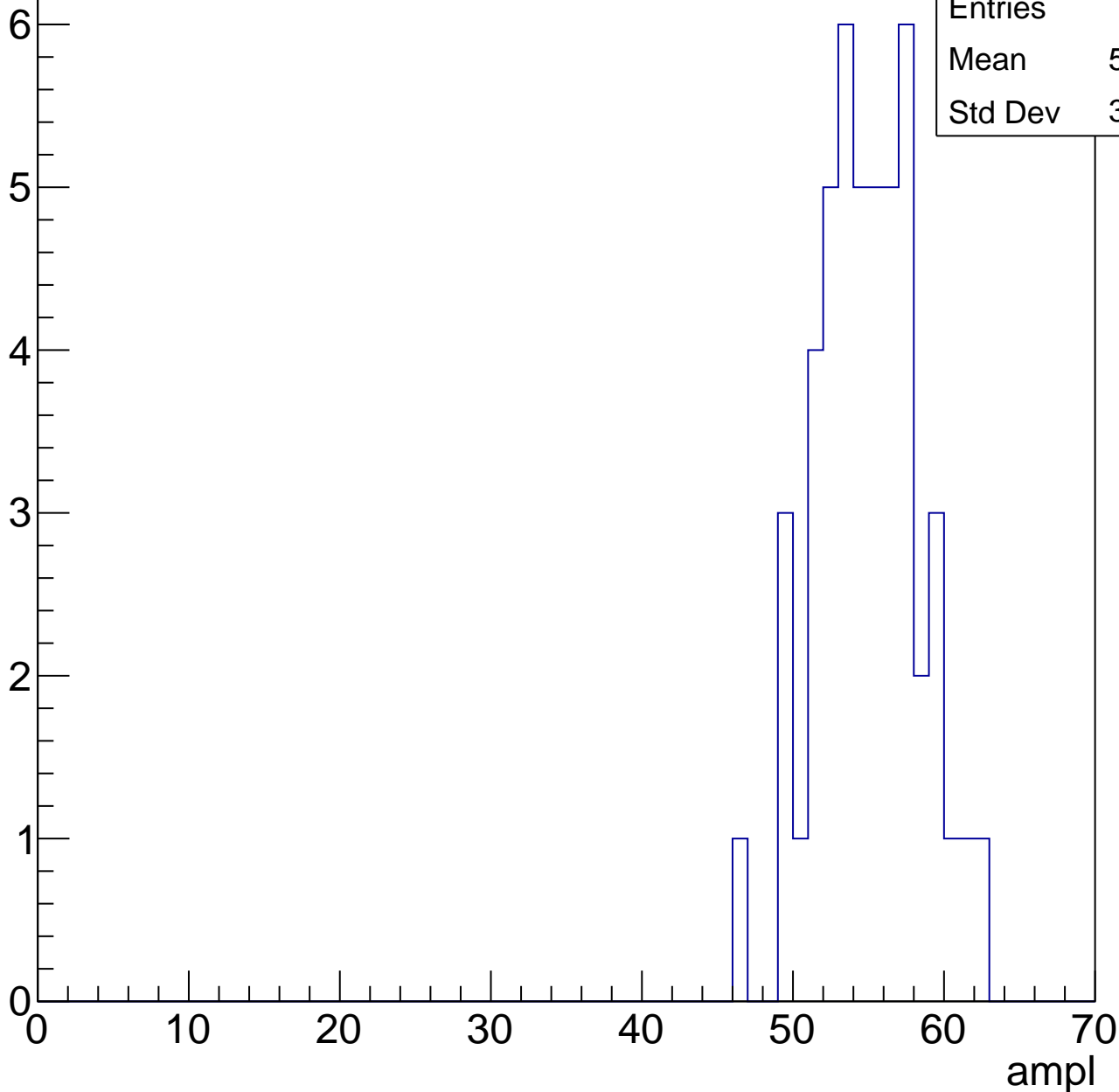


B1L103S, U8-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	54.45
Std Dev	3.326



B1L103S, U8-ch117, adc5

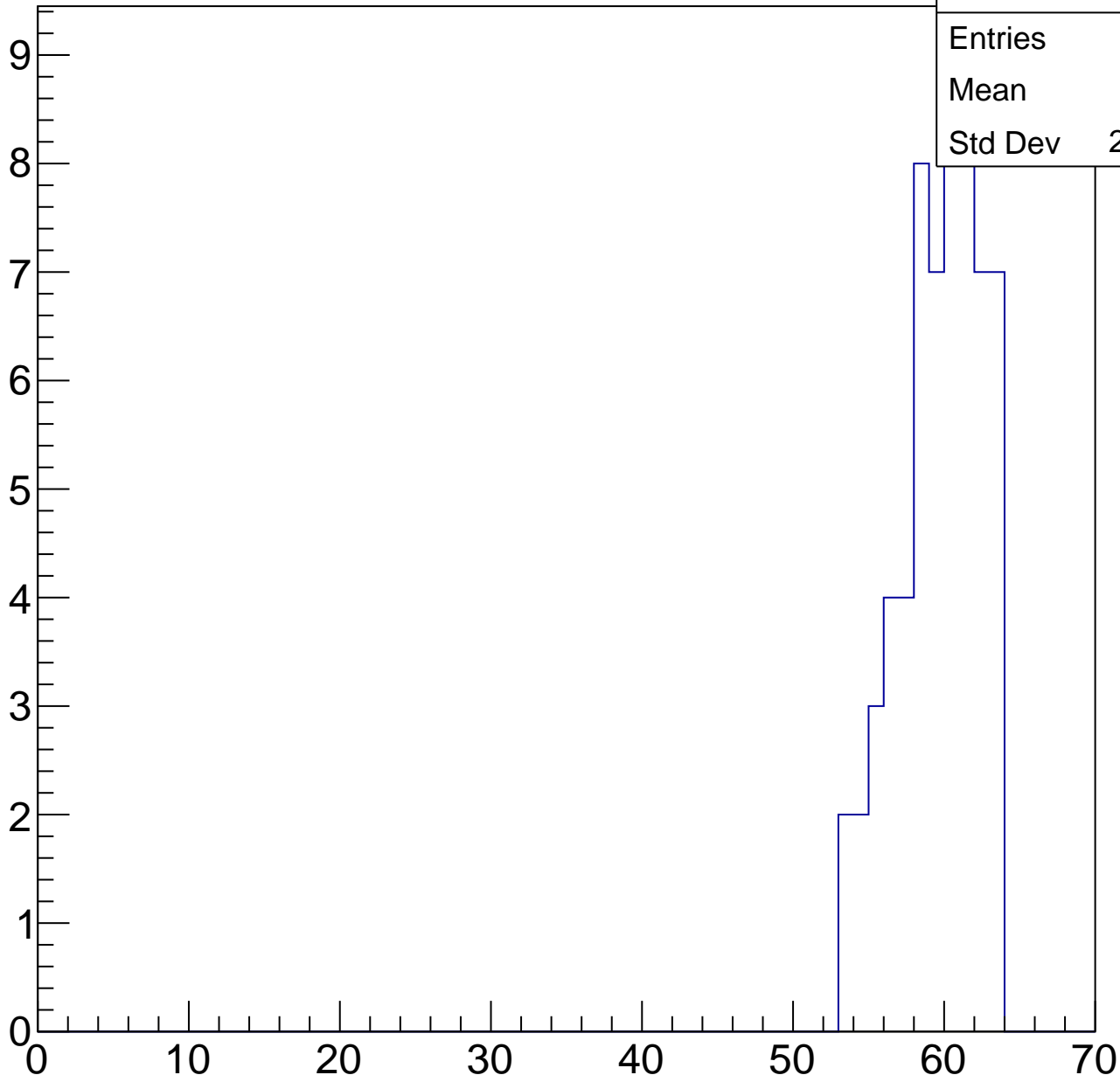
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	59.2
Std Dev	2.697

ampl



B1L103S, U8-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch118, adc0

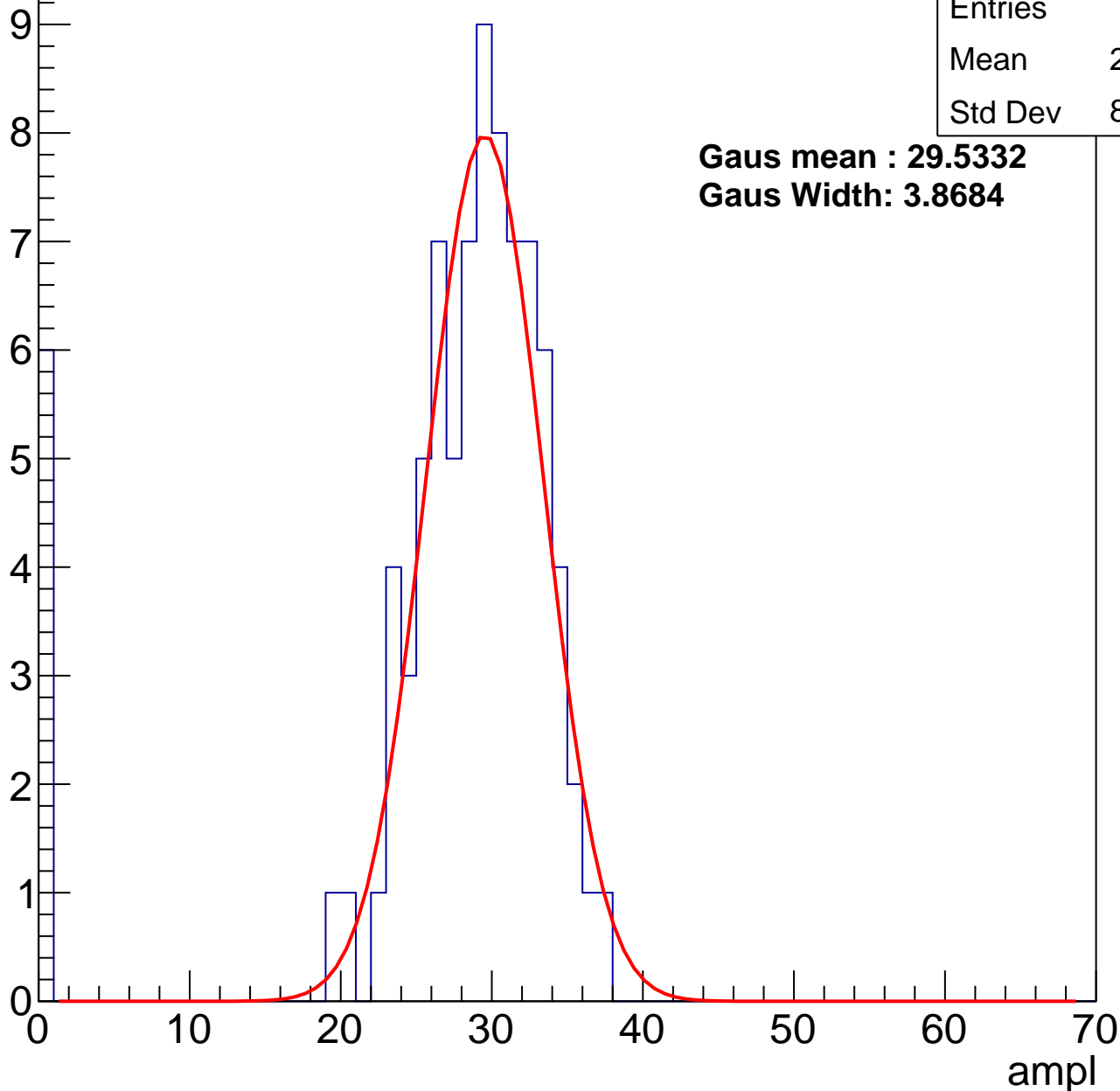
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	26.85
Std Dev	8.226

Gaus mean : 29.5332

Gaus Width: 3.8684



B1L103S, U8-ch118, adc1

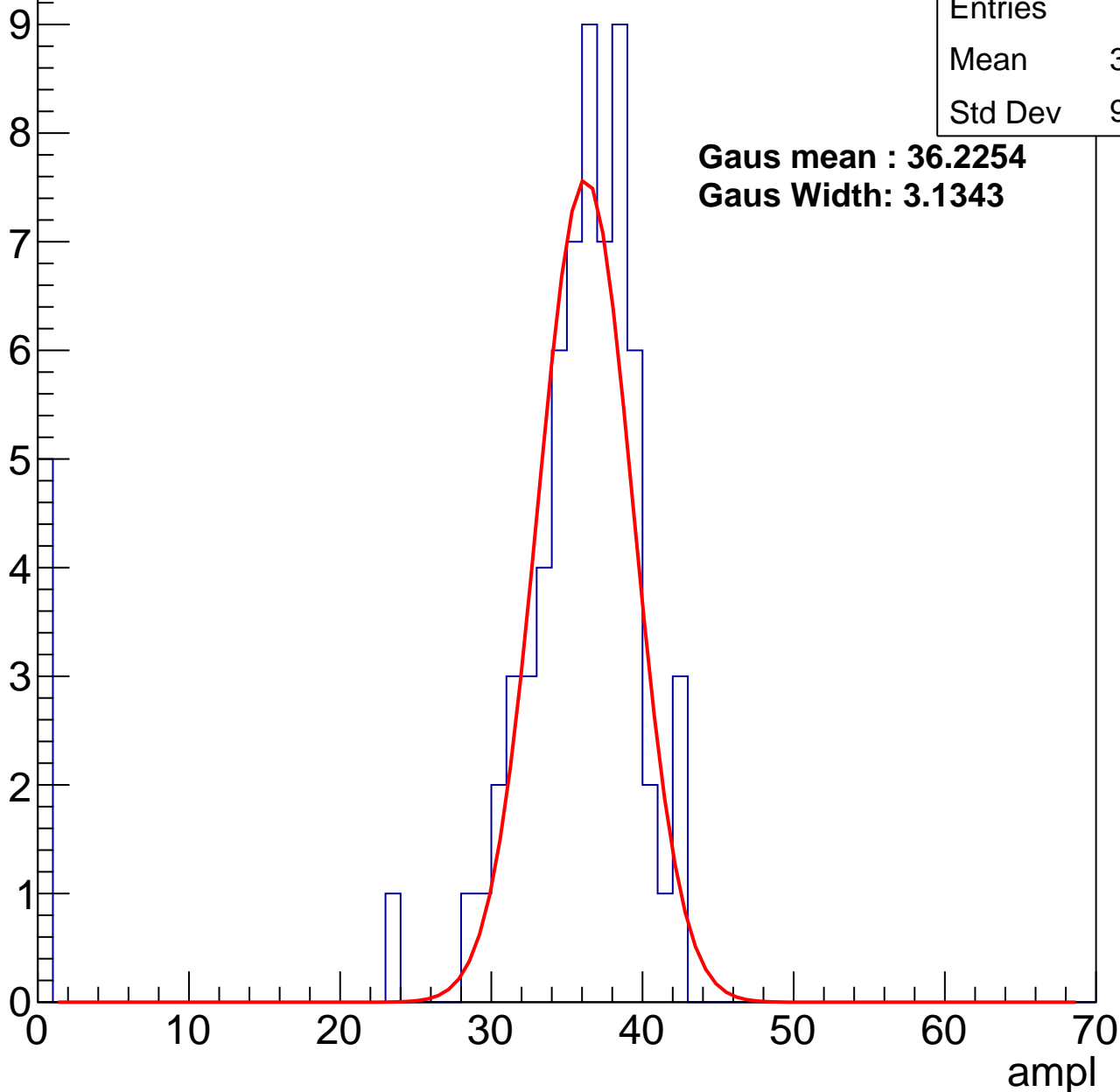
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	33.09
Std Dev	9.777

Gaus mean : 36.2254

Gaus Width: 3.1343



B1L103S, U8-ch118, adc2

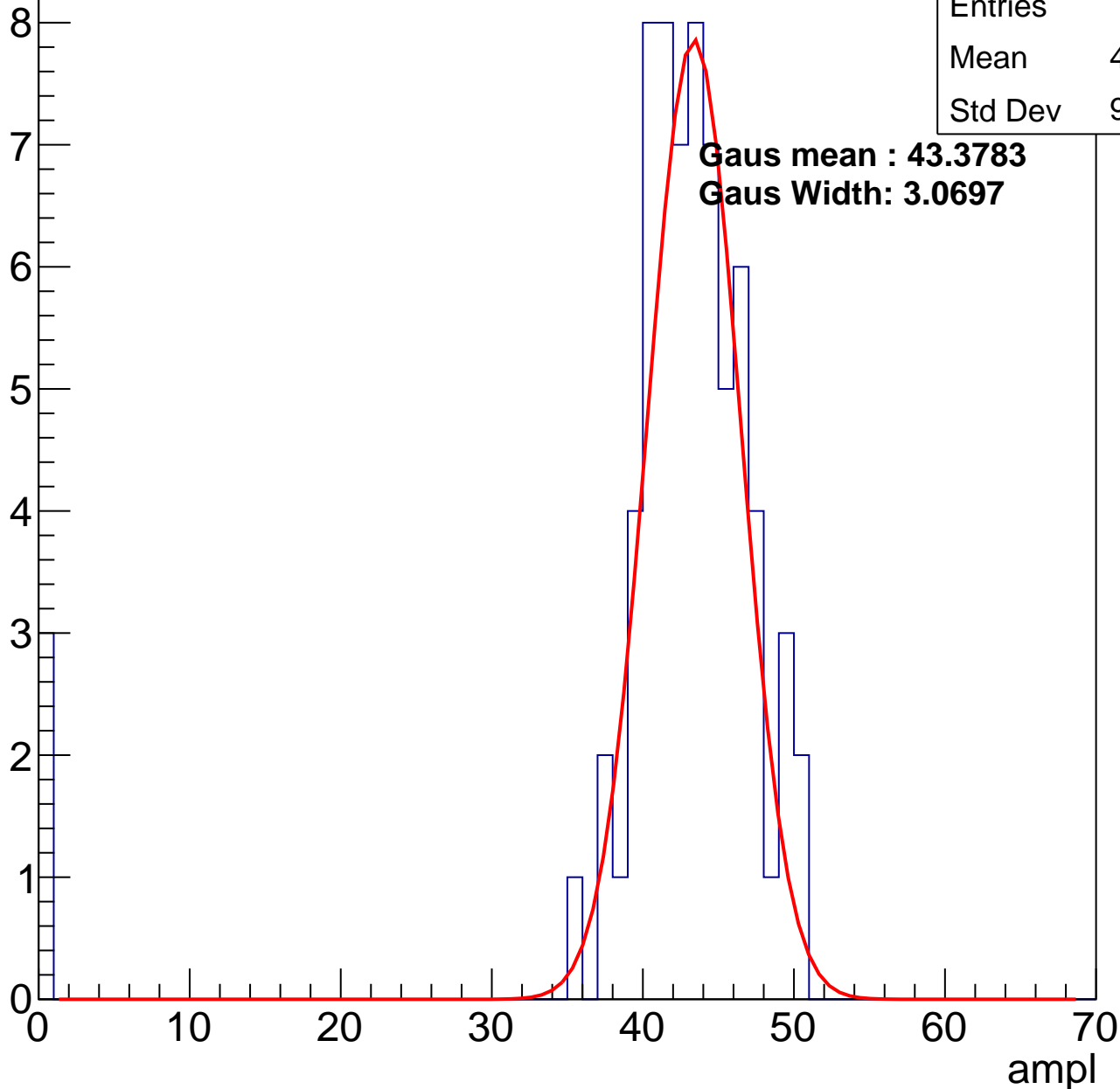
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	41.16
Std Dev	9.275

Gaus mean : 43.3783

Gaus Width: 3.0697

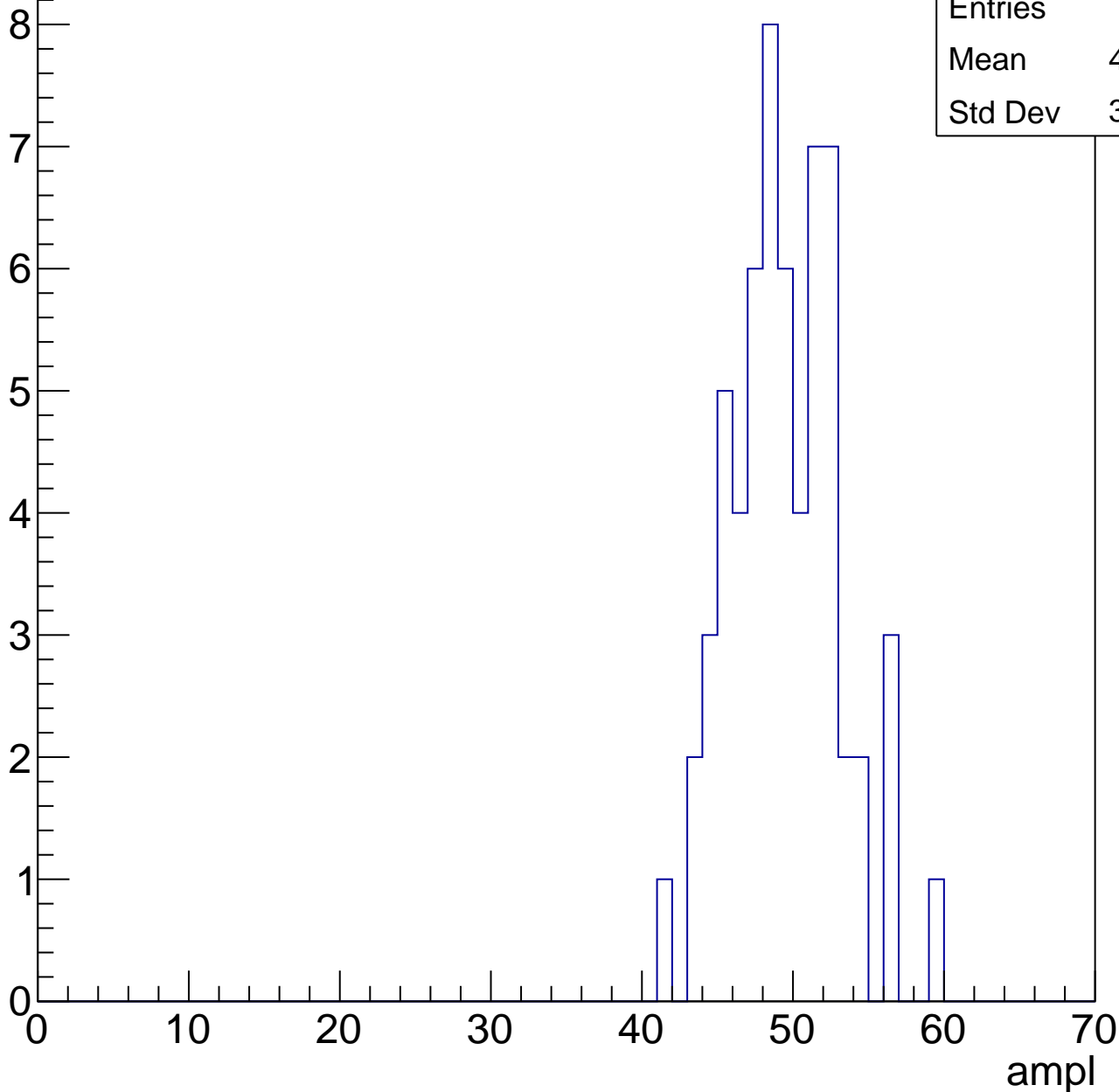


B1L103S, U8-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.02
Std Dev	3.583

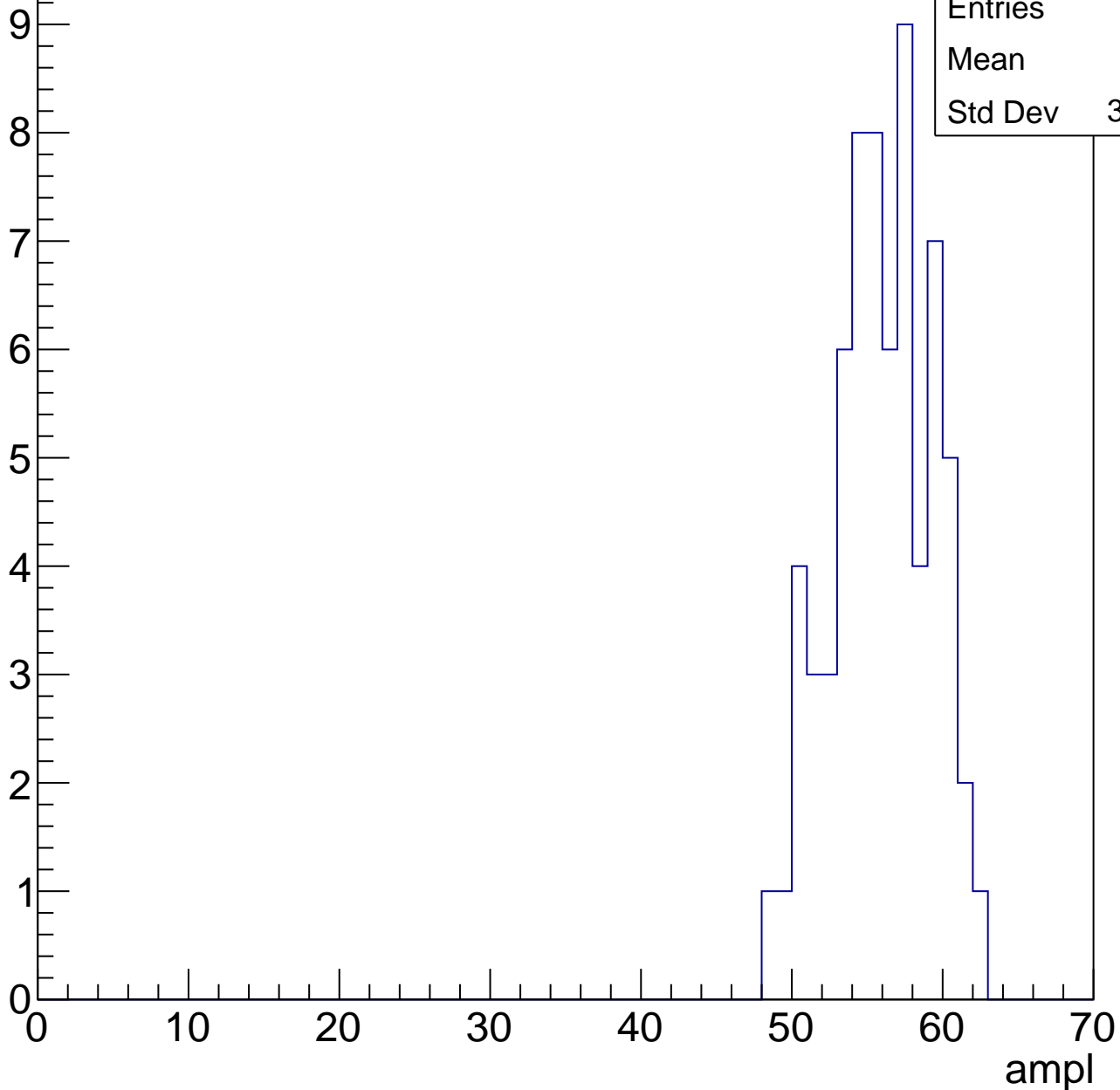


B1L103S, U8-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	55.5
Std Dev	3.234

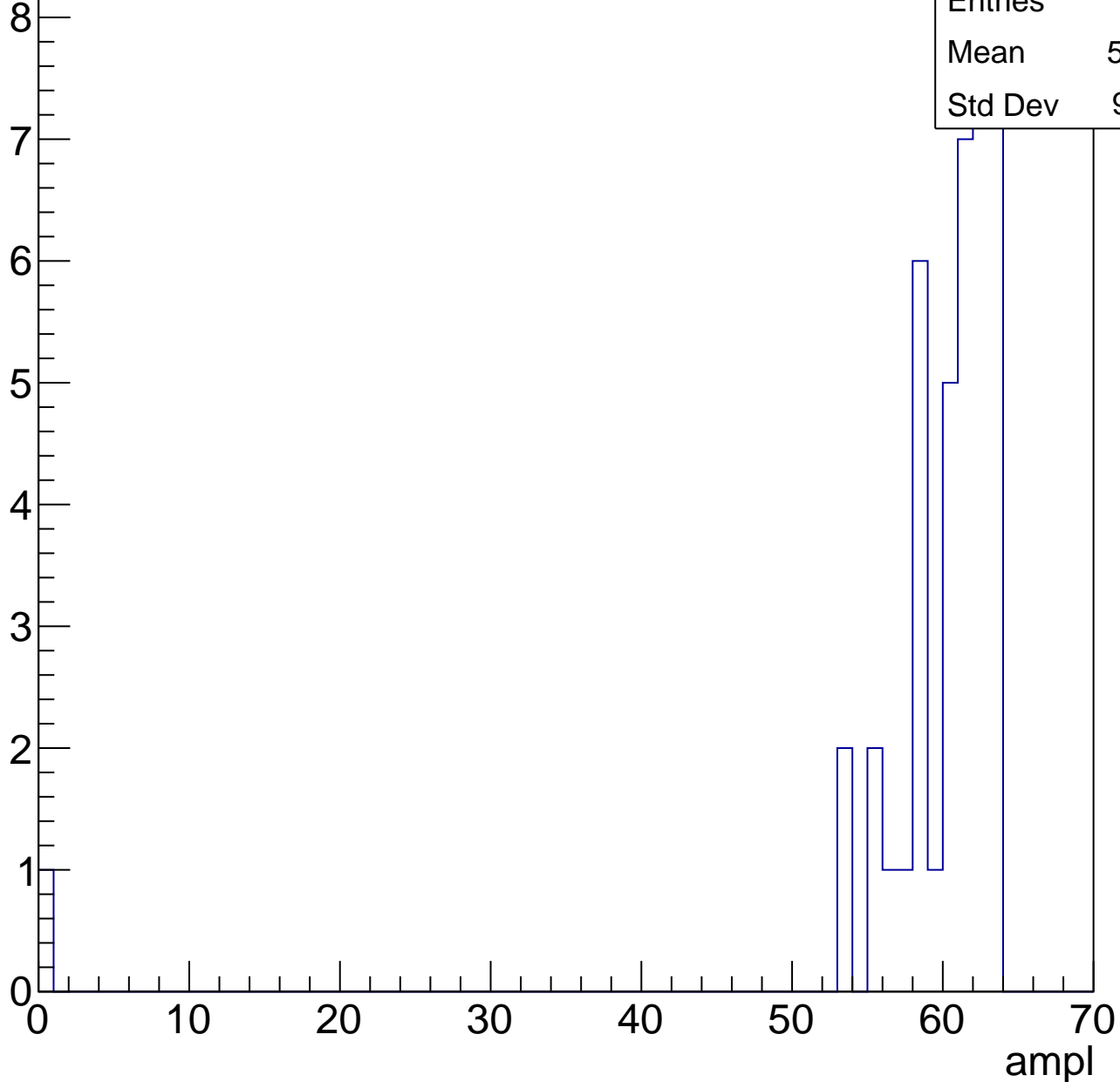


B1L103S, U8-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

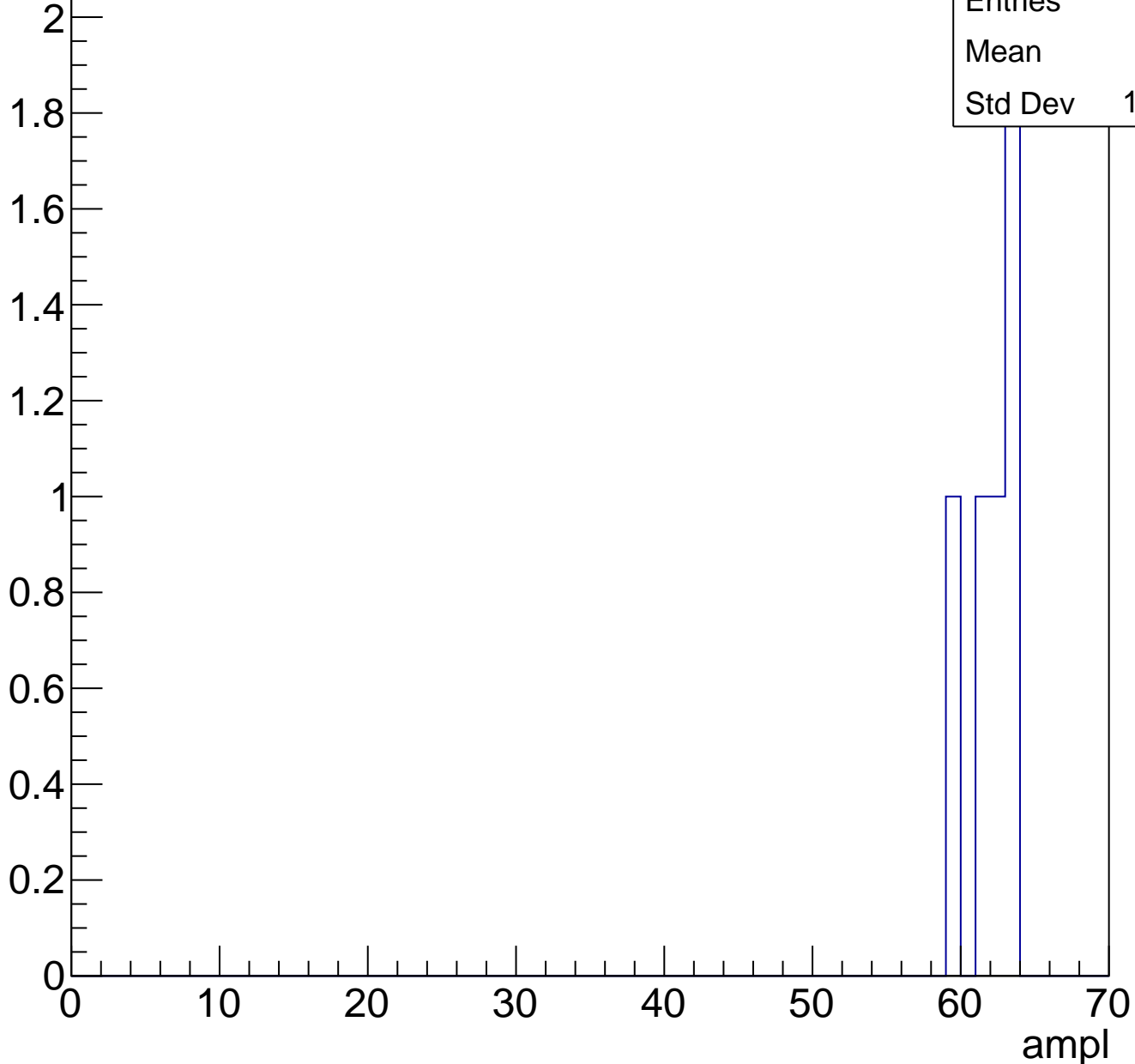
Entries	42
Mean	58.64
Std Dev	9.551



B1L103S, U8-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	61.6
Std Dev	1.497

B1L103S, U8-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch119, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	27.78
Std Dev	9.054

Gaus mean : 30.8190

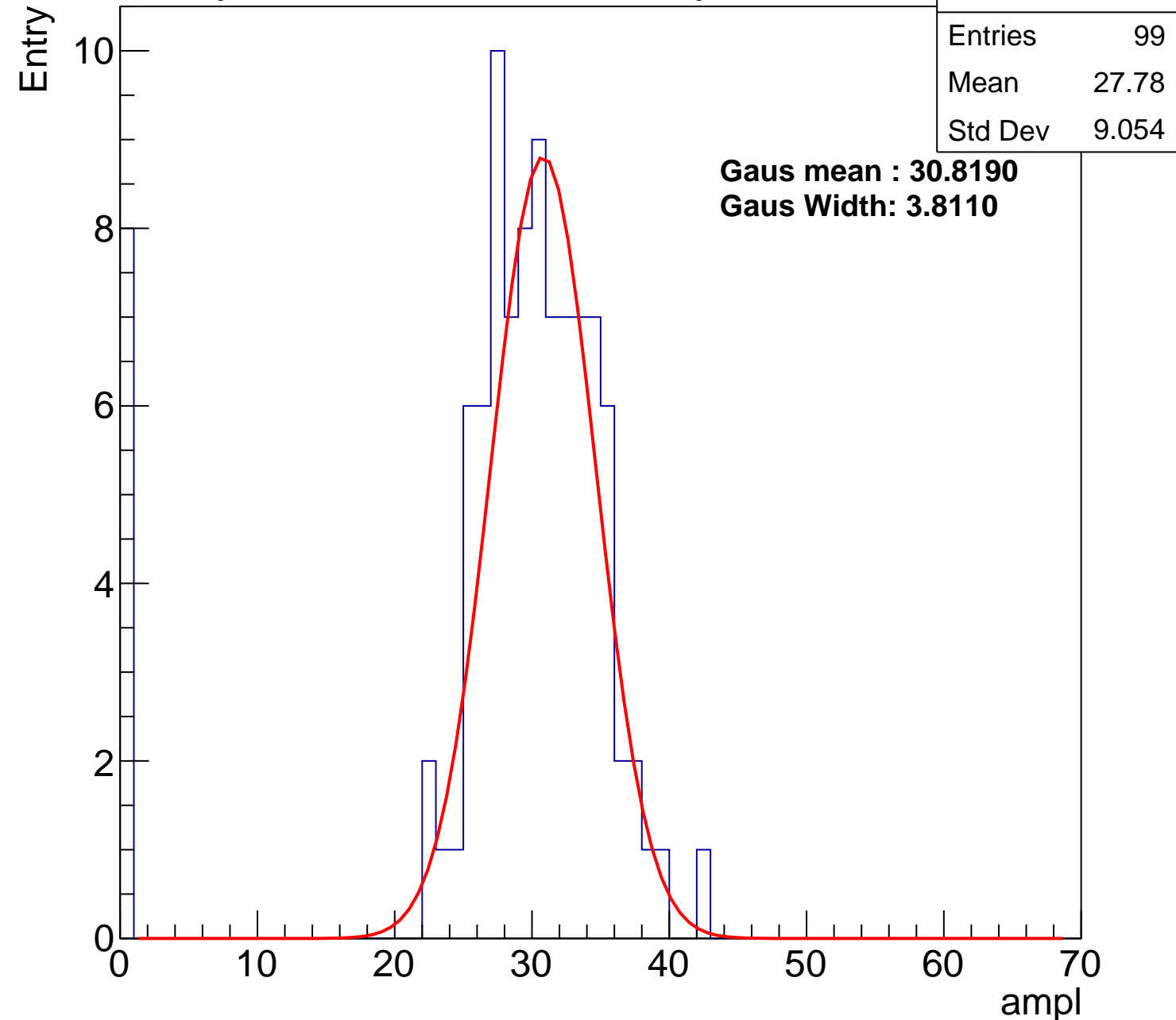
Gaus Width: 3.8110

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch119, adc1

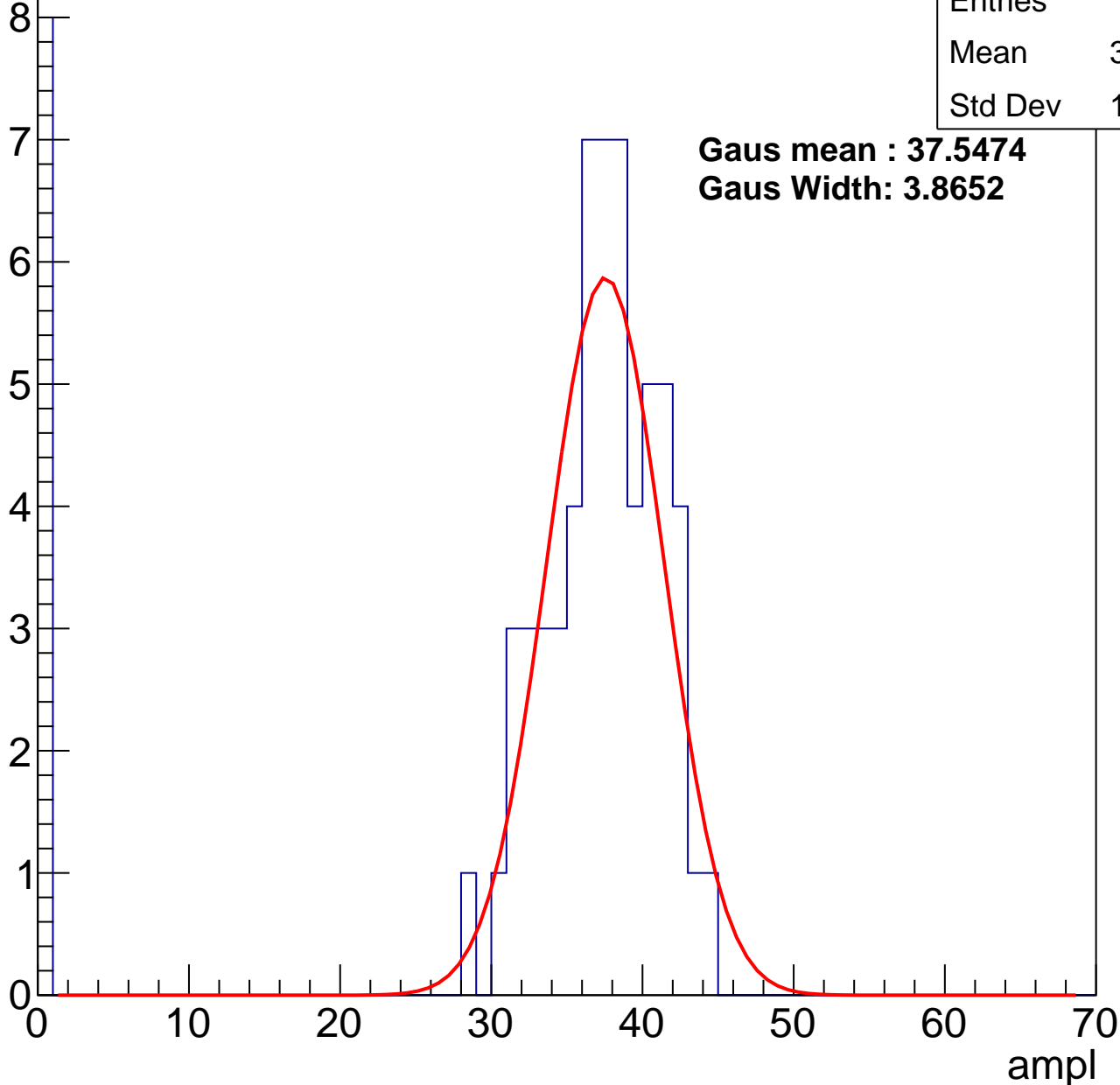
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.55
Std Dev	12.44

Gaus mean : 37.5474

Gaus Width: 3.8652



B1L103S, U8-ch119, adc2

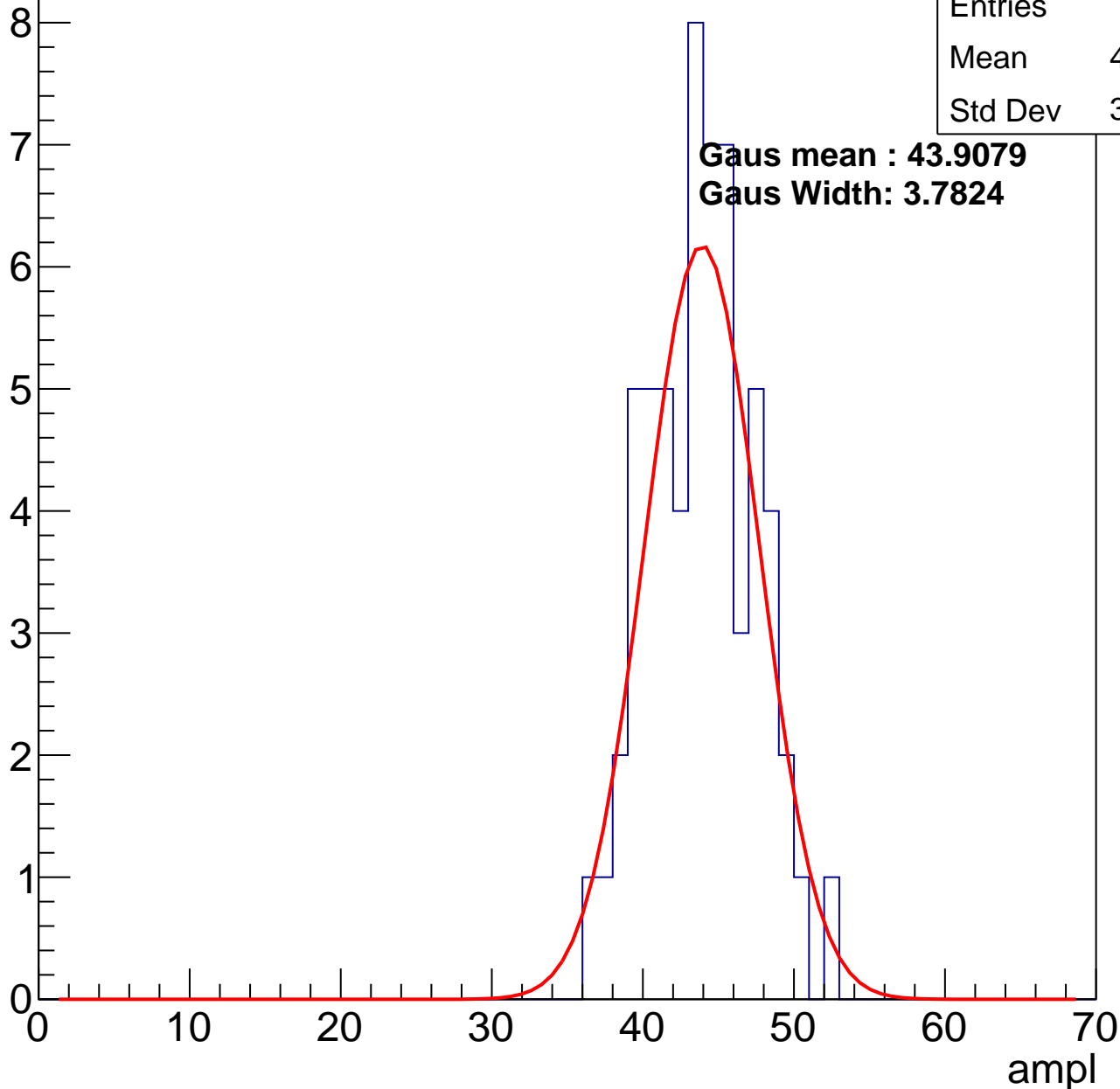
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	43.43
Std Dev	3.428

Gaus mean : 43.9079

Gaus Width: 3.7824

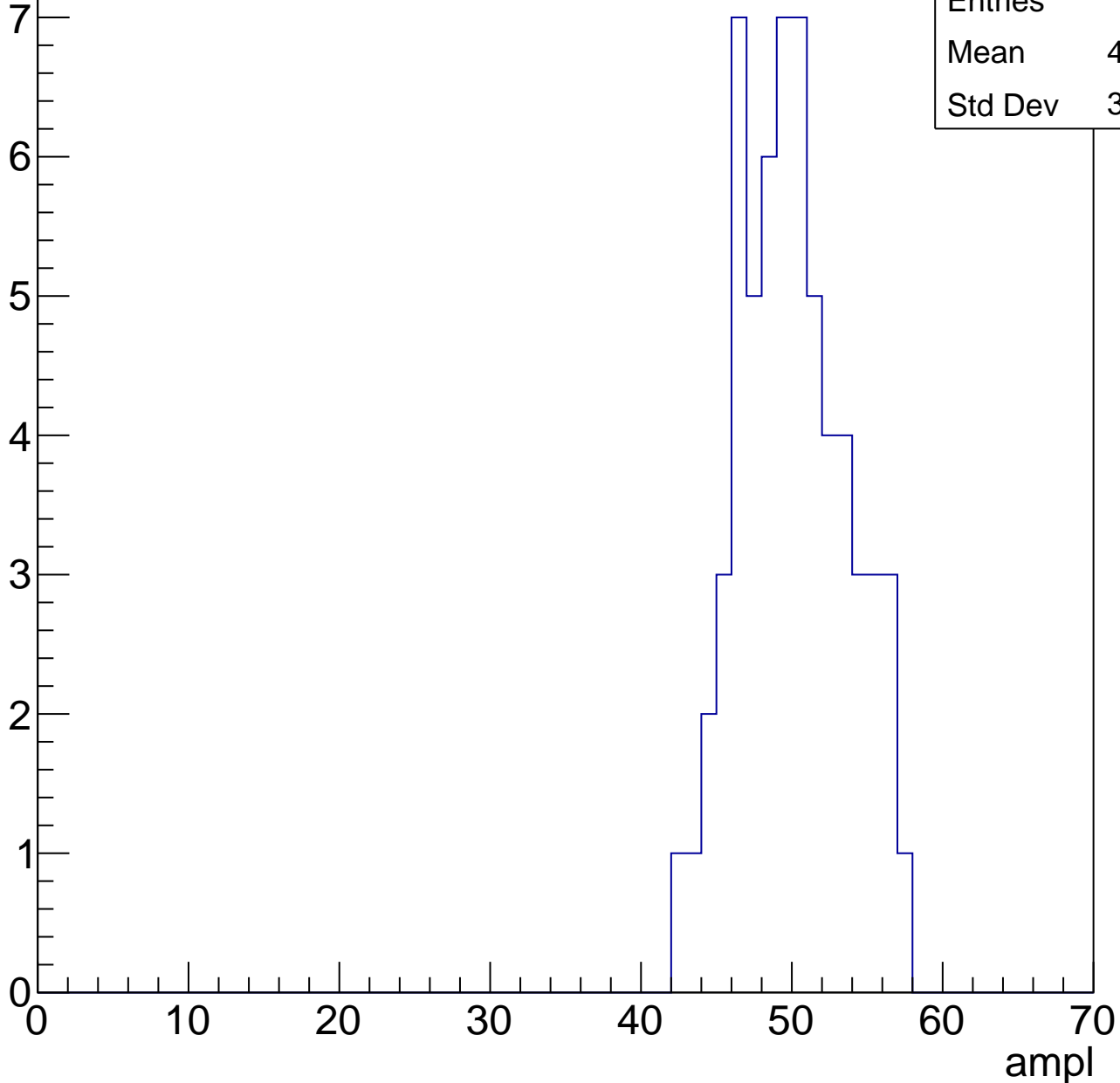


B1L103S, U8-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	49.56
Std Dev	3.532

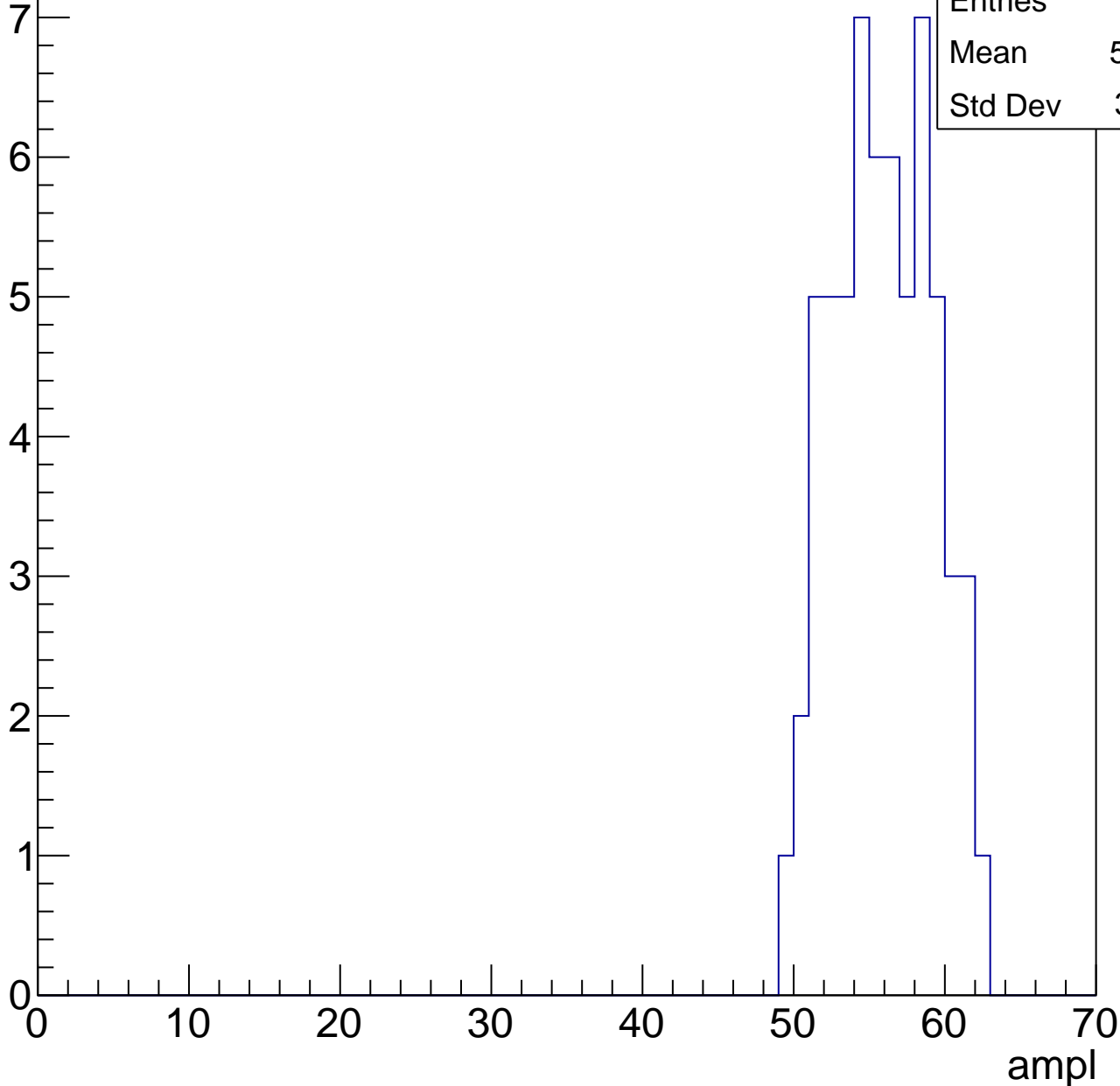


B1L103S, U8-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55.48
Std Dev	3.201

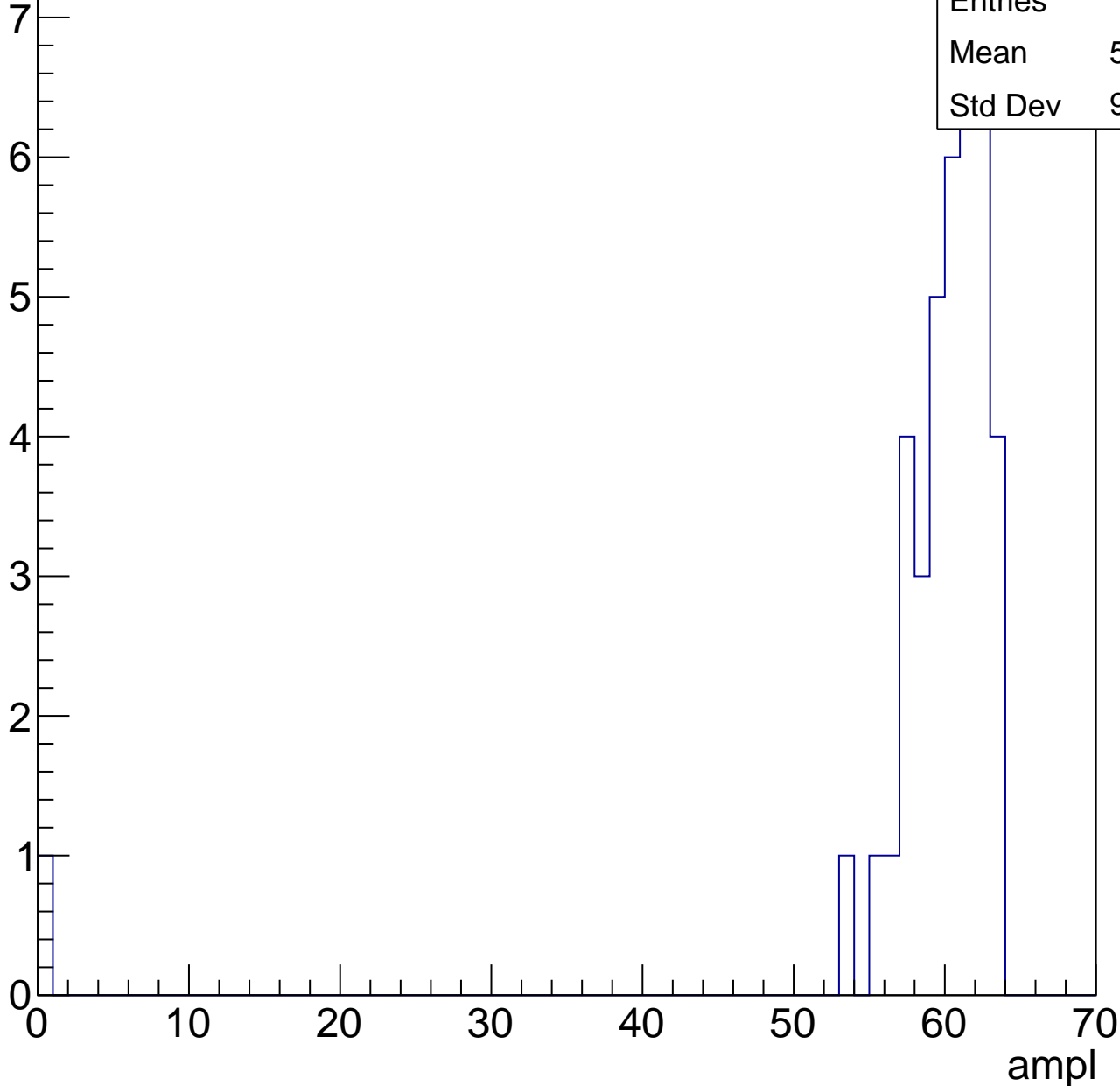


B1L103S, U8-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	58.35
Std Dev	9.624

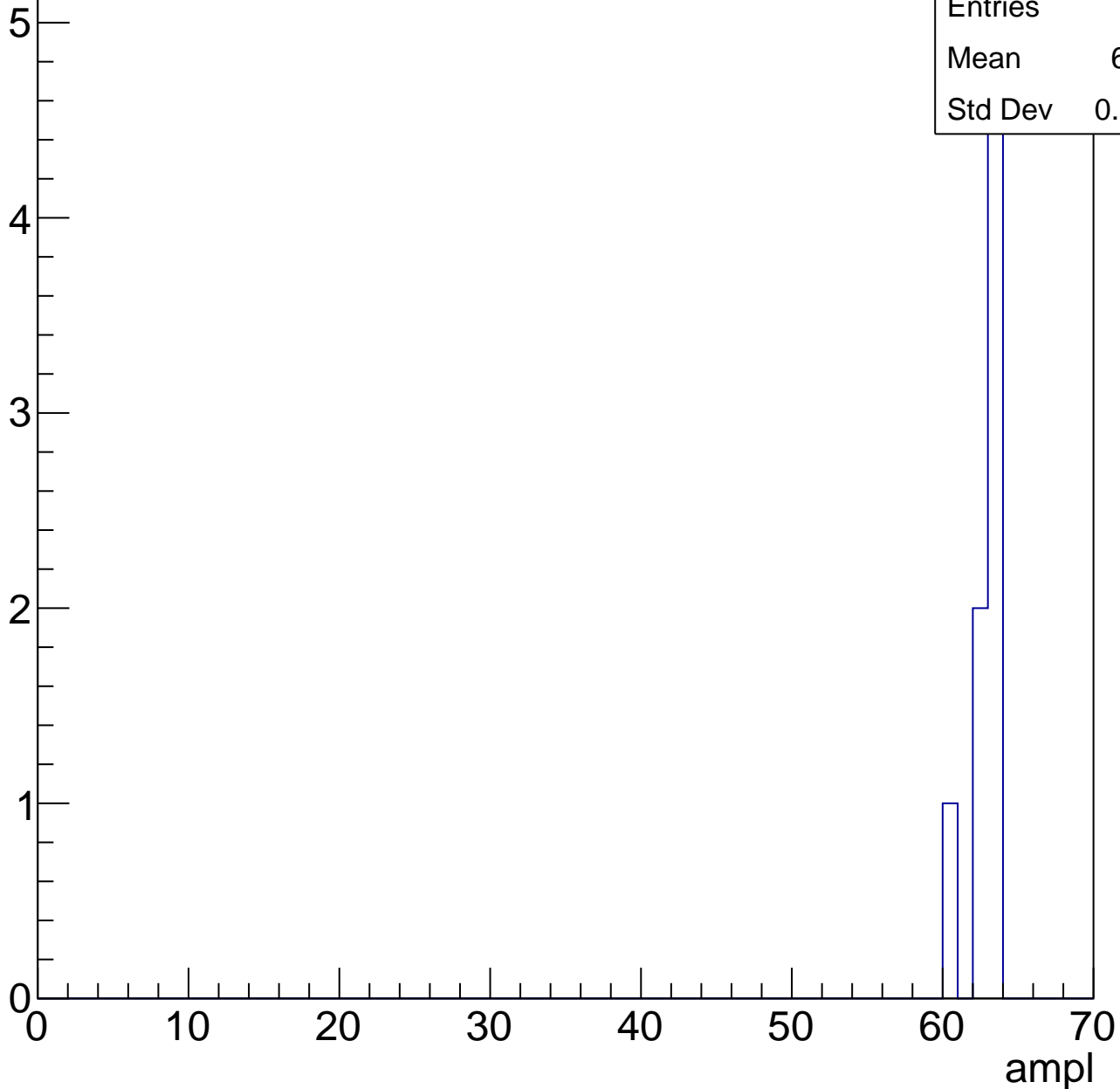


B1L103S, U8-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B1L103S, U8-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U8-ch120, adc0

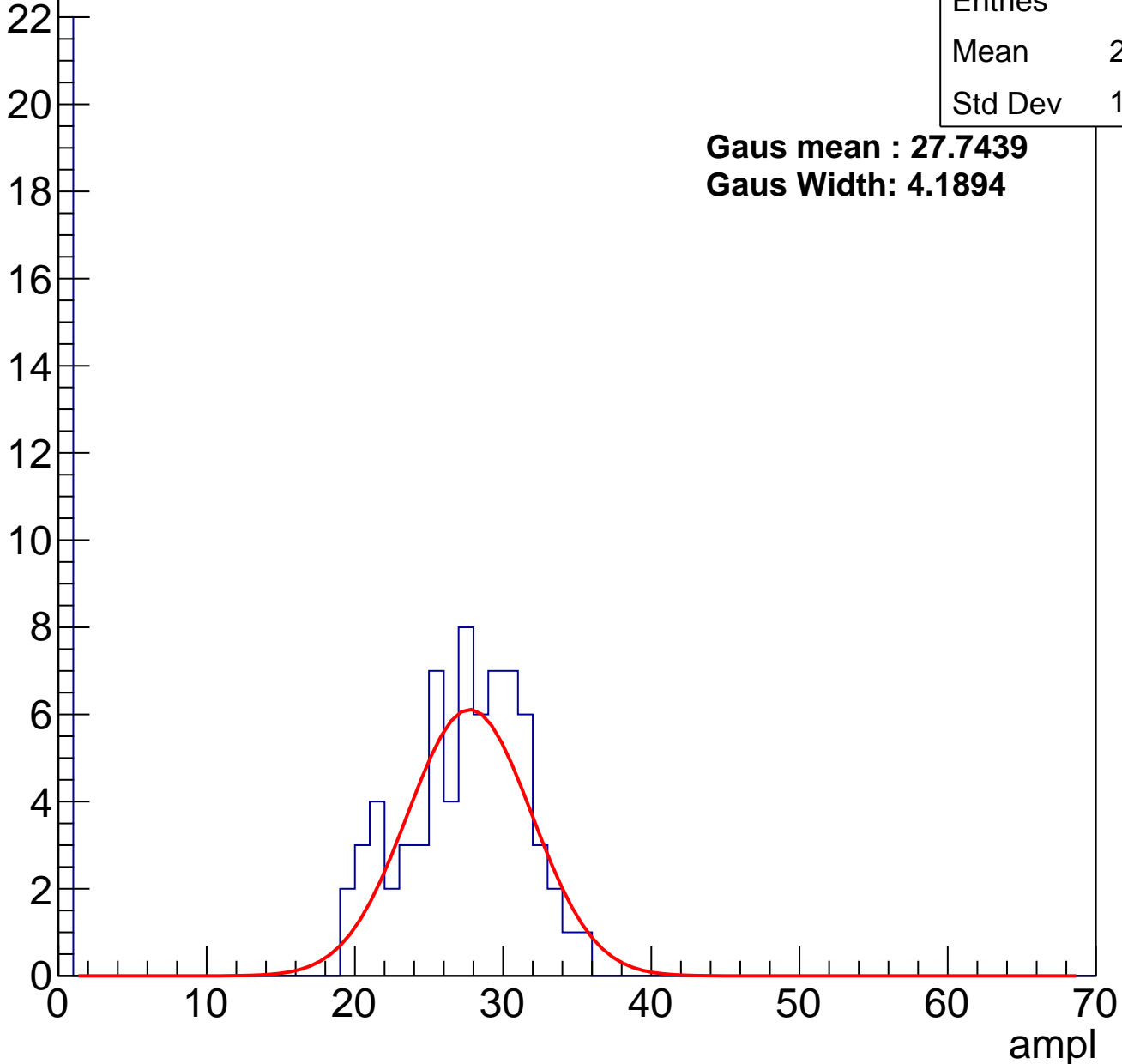
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	20.44
Std Dev	12.02

Gaus mean : 27.7439

Gaus Width: 4.1894

Entry



B1L103S, U8-ch120, adc1

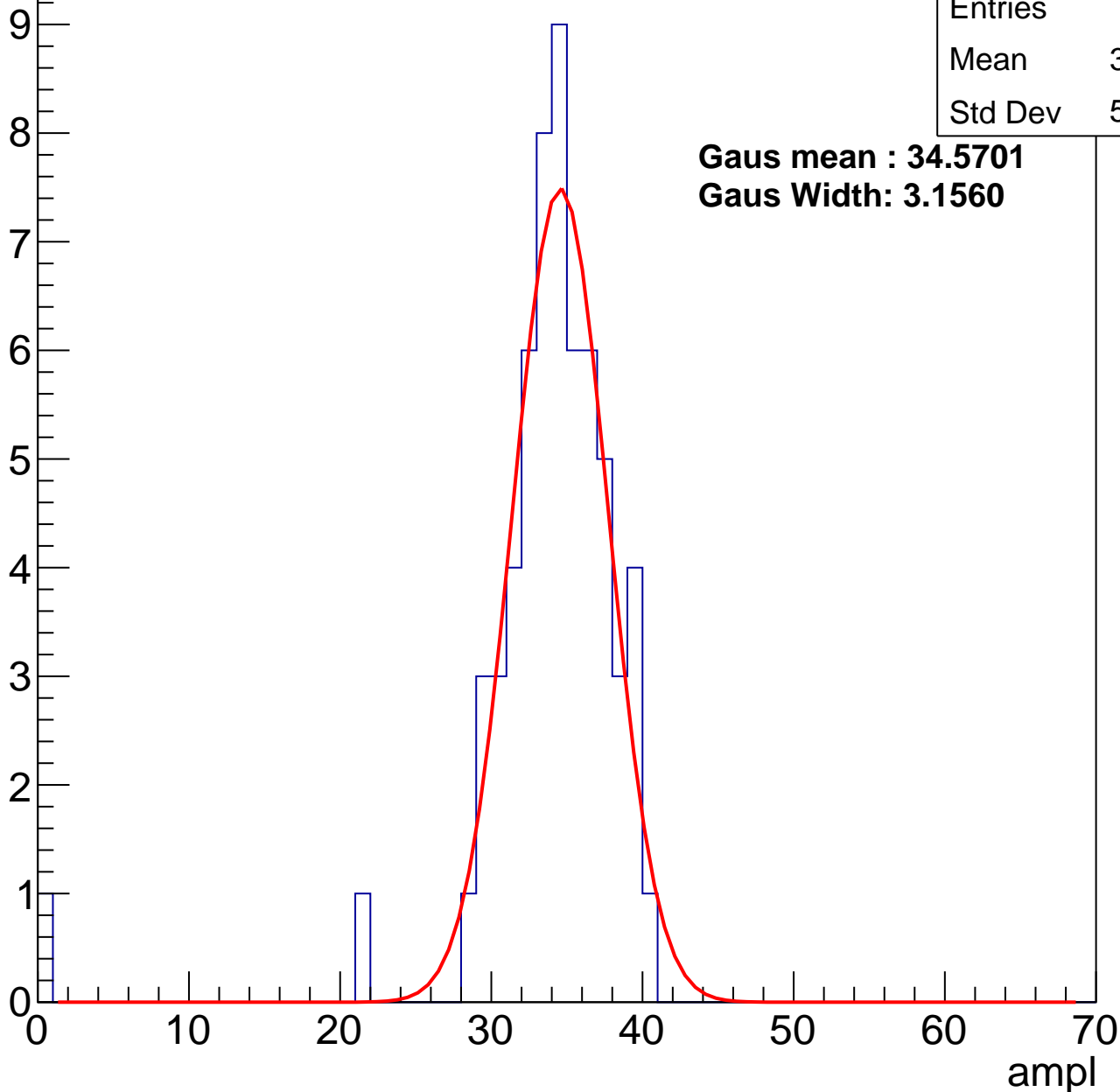
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	33.33
Std Dev	5.407

Gaus mean : 34.5701

Gaus Width: 3.1560



B1L103S, U8-ch120, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	34
Std Dev	15.51

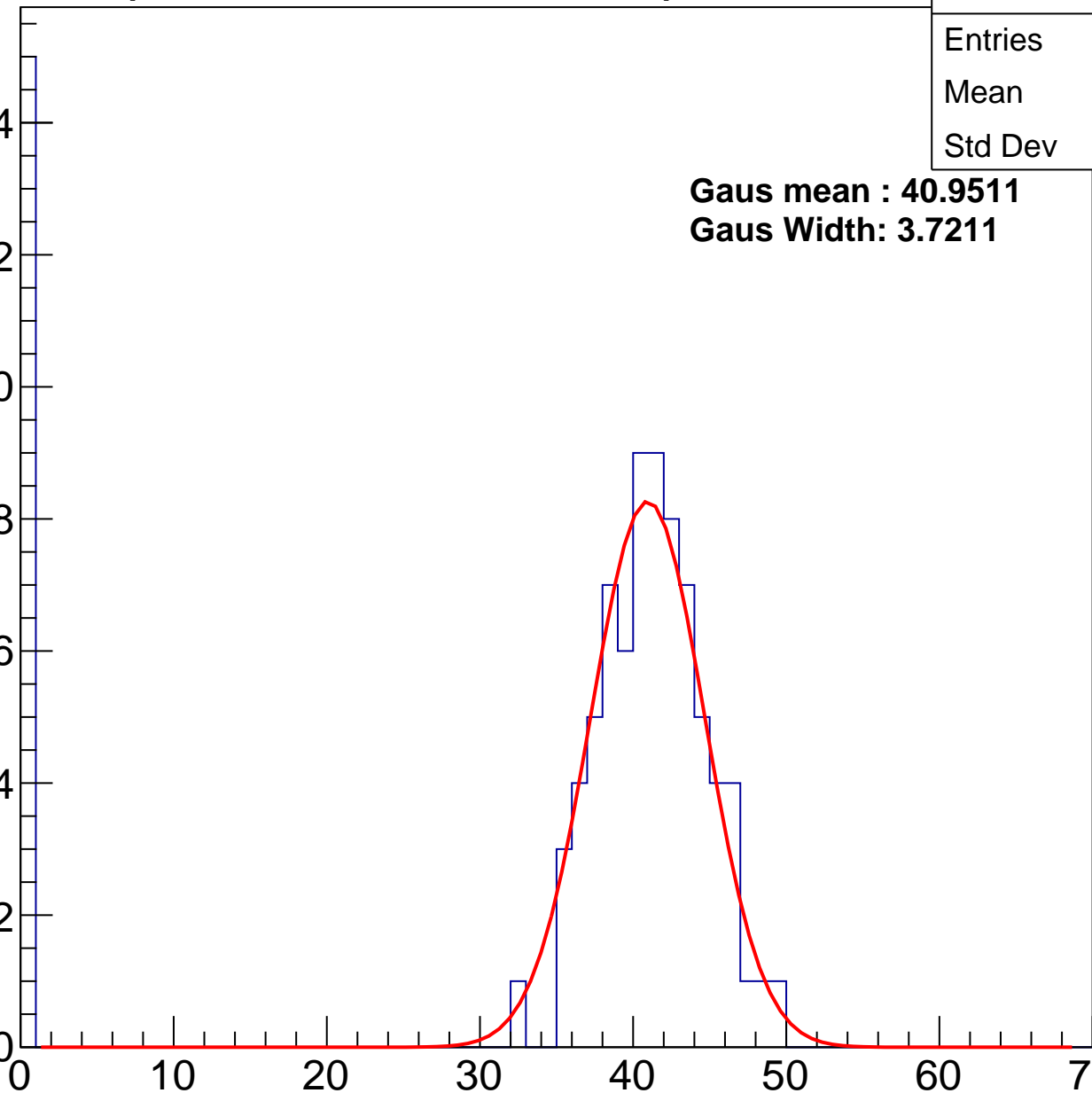
Gaus mean : 40.9511

Gaus Width: 3.7211

Entry

14
12
10
8
6
4
2
0

ampl

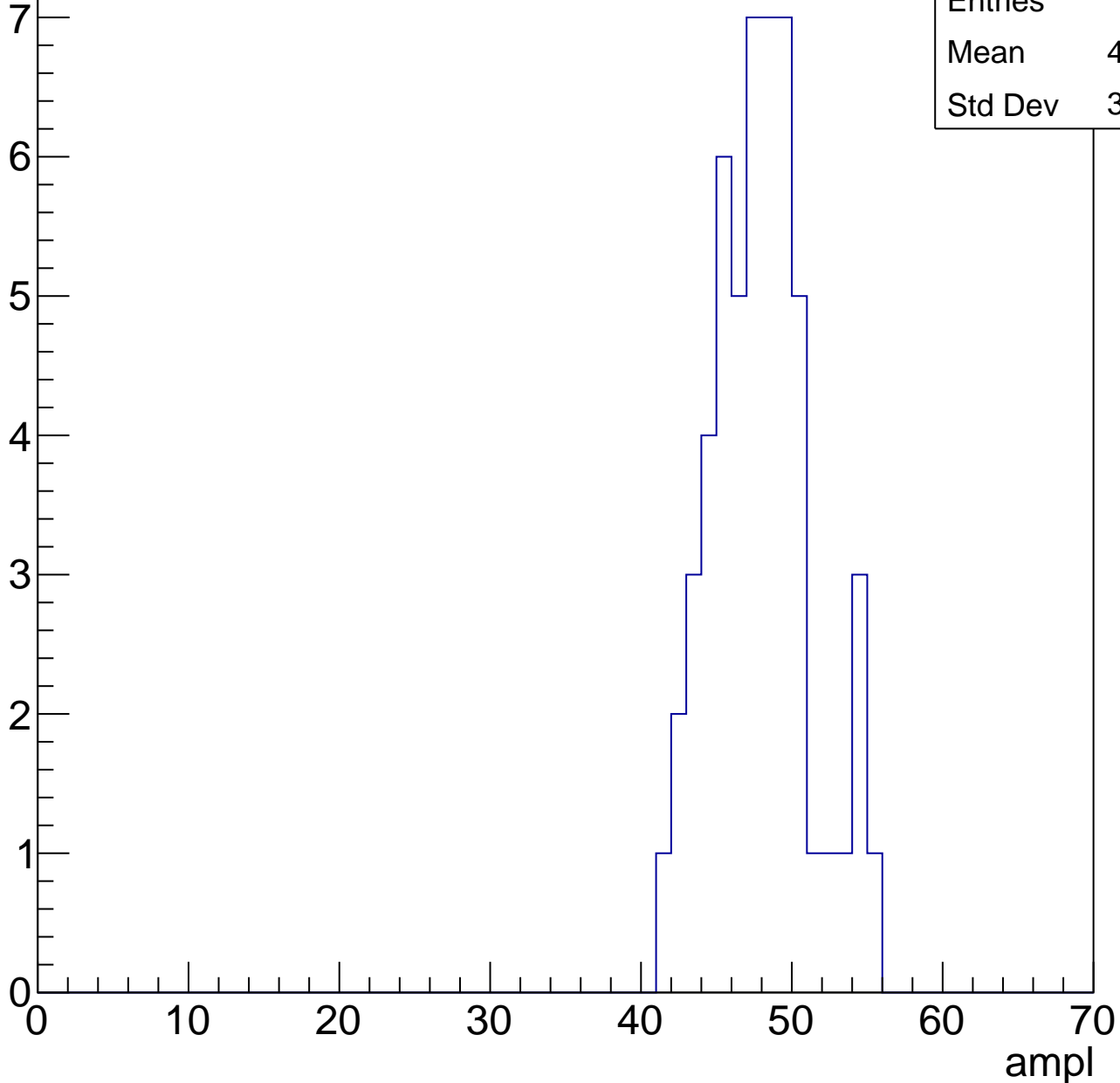


B1L103S, U8-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	47.43
Std Dev	3.212

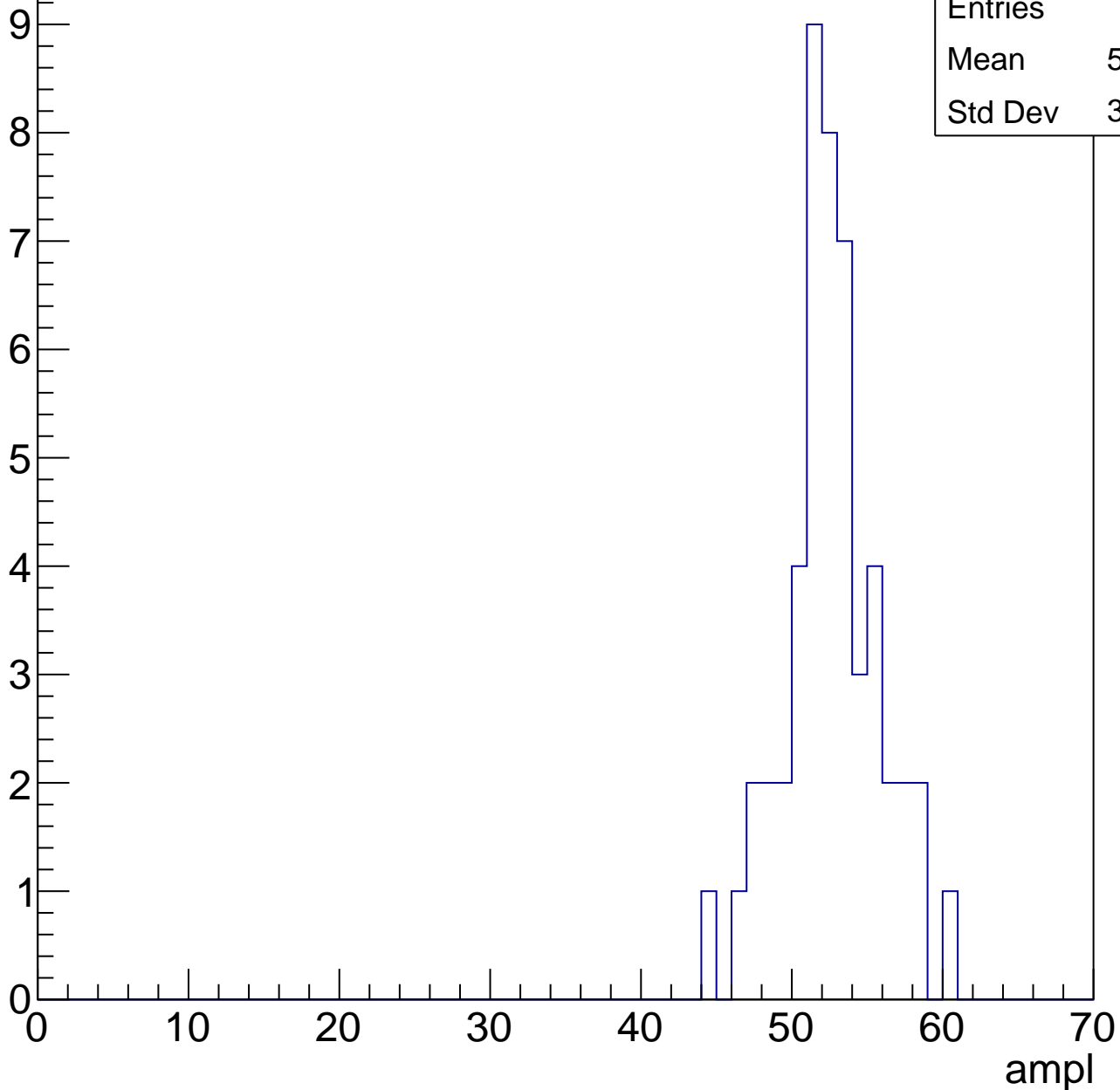


B1L103S, U8-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

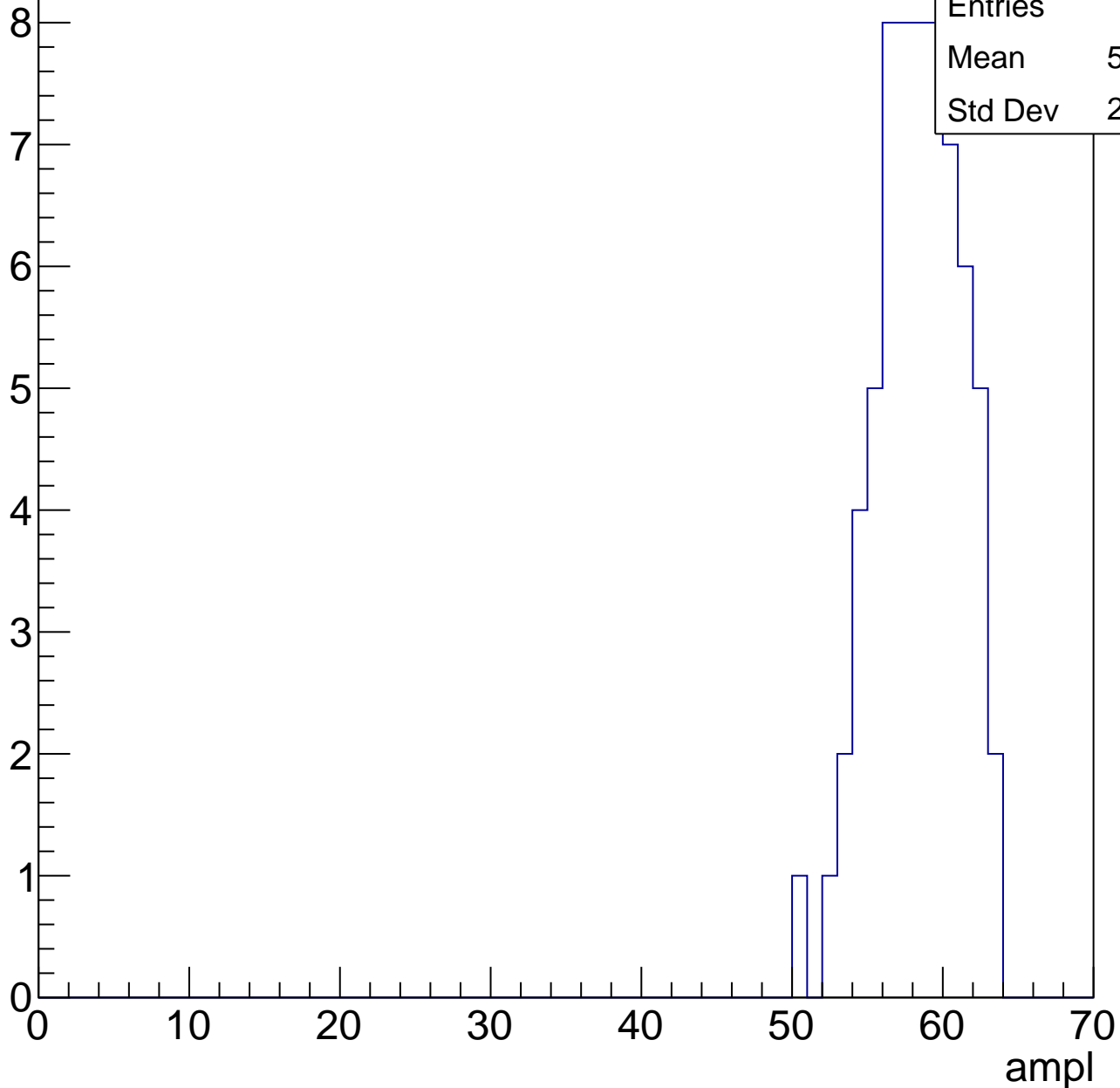
Entries	50
Mean	52.16
Std Dev	3.152



B1L103S, U8-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



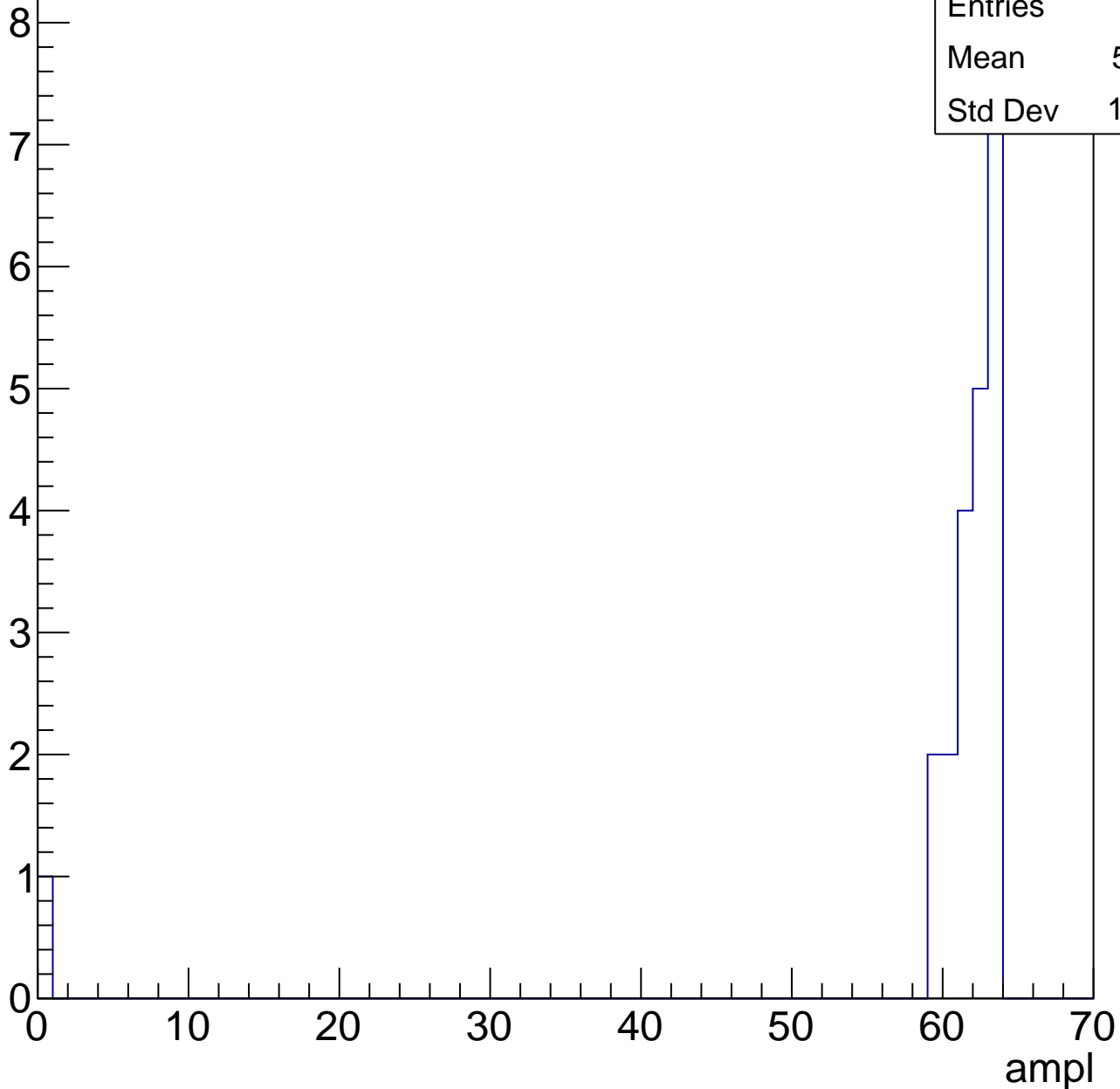
Entries	65
Mean	57.86
Std Dev	2.822

B1L103S, U8-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.91
Std Dev	12.92



B1L103S, U8-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch121, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	27.04
Std Dev	8.048

Gaus mean : 29.7001

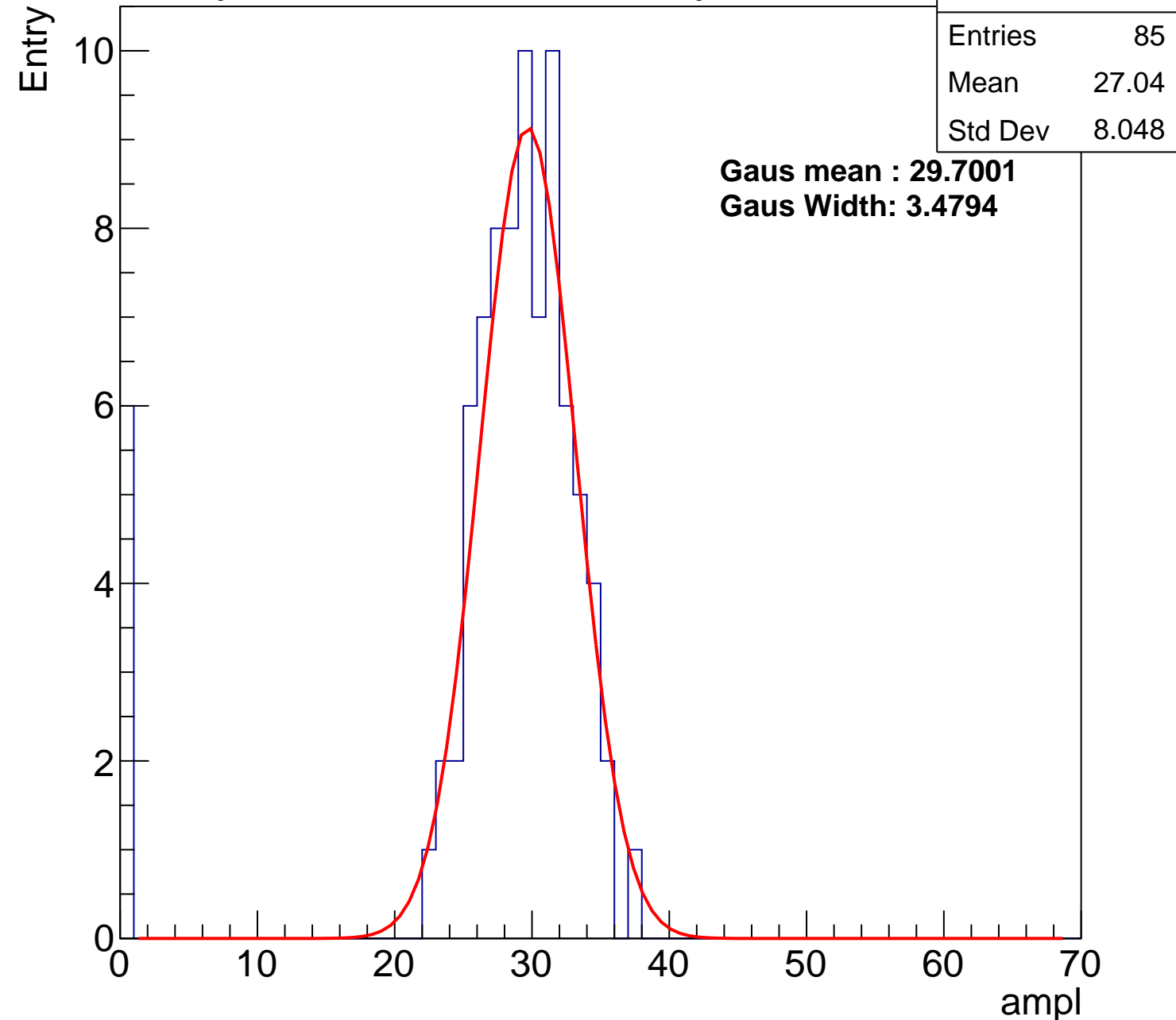
Gaus Width: 3.4794

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U8-ch121, adc1

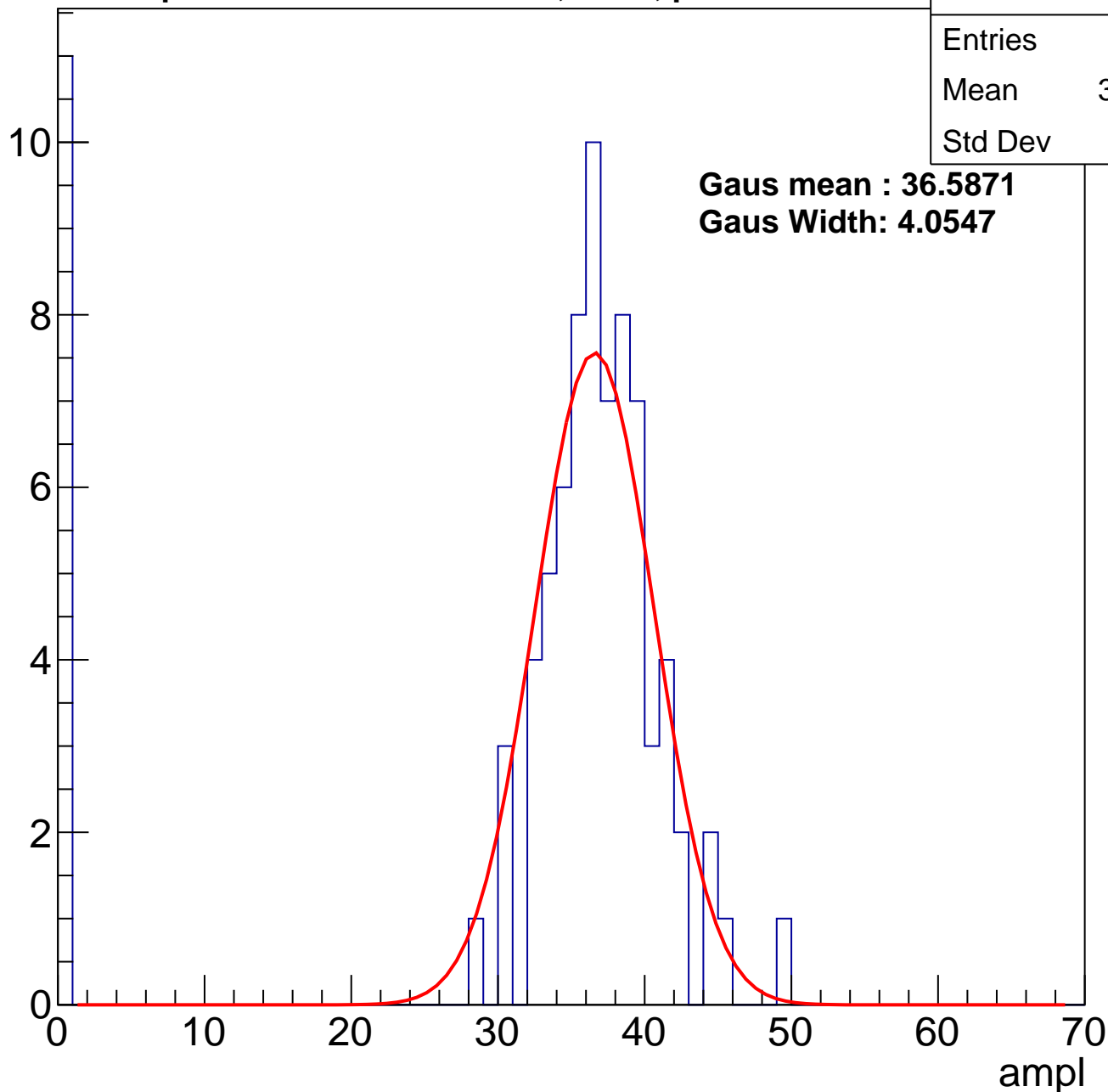
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	31.82
Std Dev	12.9

Gaus mean : 36.5871

Gaus Width: 4.0547

Entry



B1L103S, U8-ch121, adc2

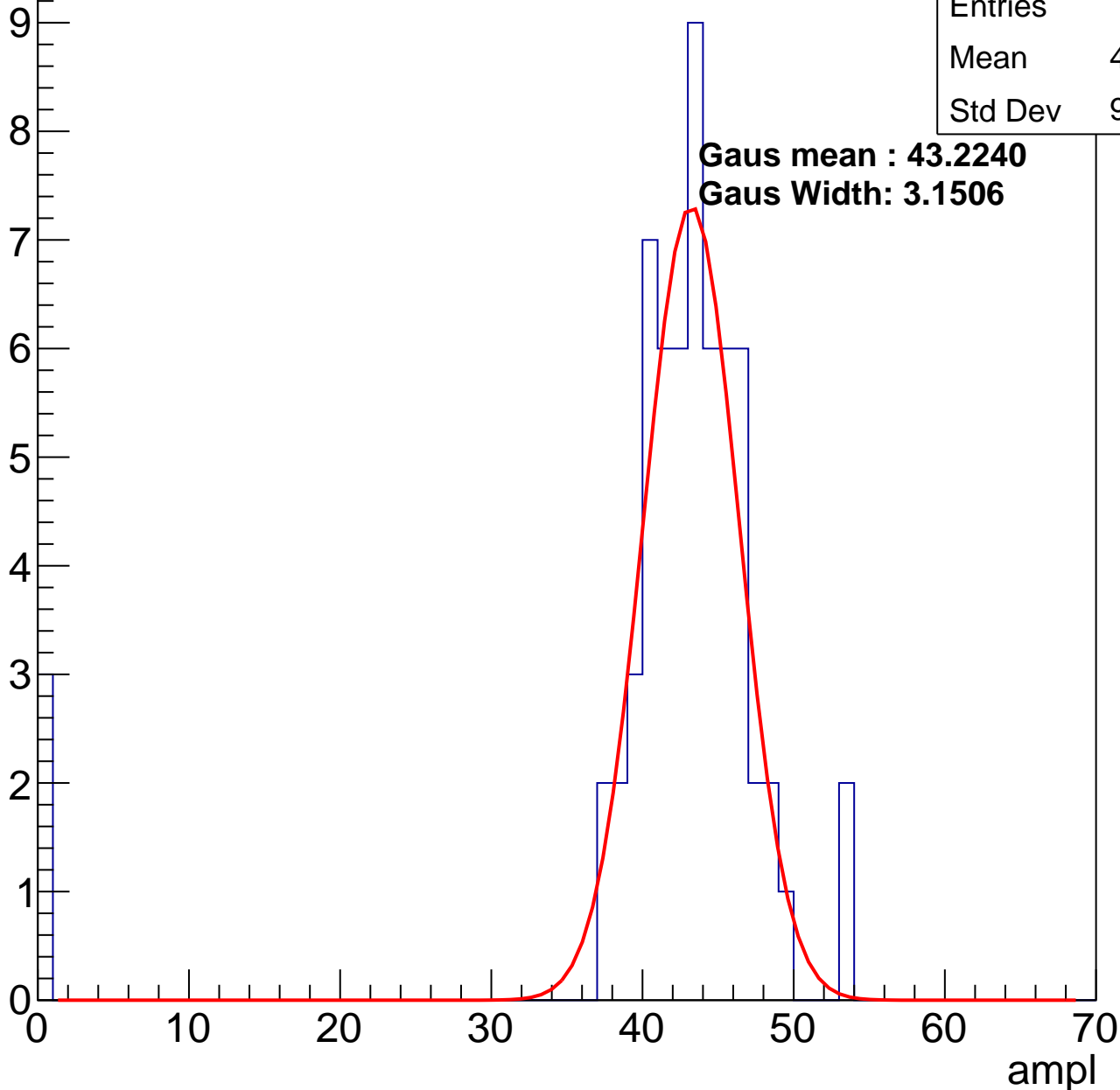
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	41.06
Std Dev	9.739

Gaus mean : 43.2240

Gaus Width: 3.1506

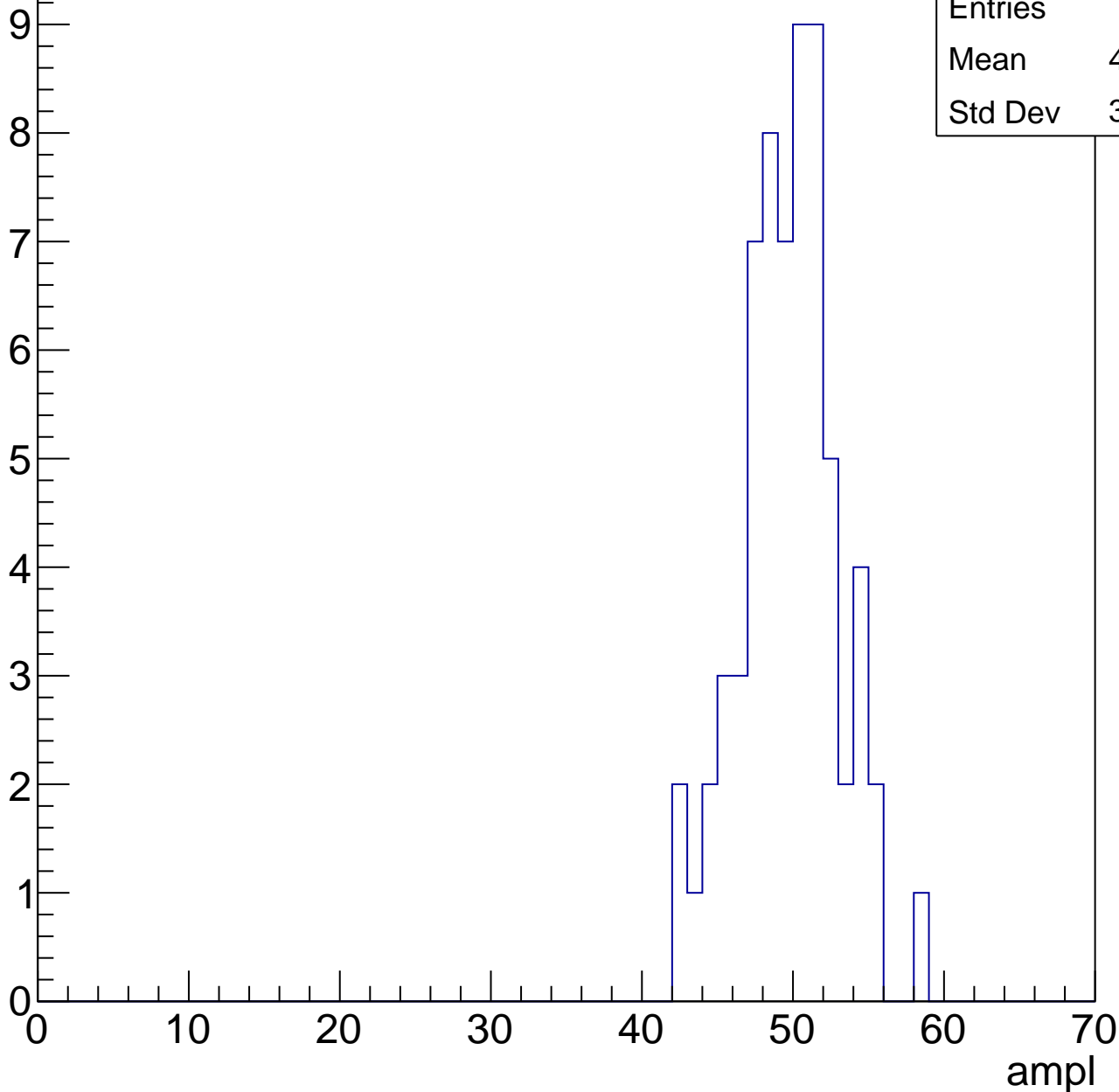


B1L103S, U8-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	49.28
Std Dev	3.199



B1L103S, U8-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

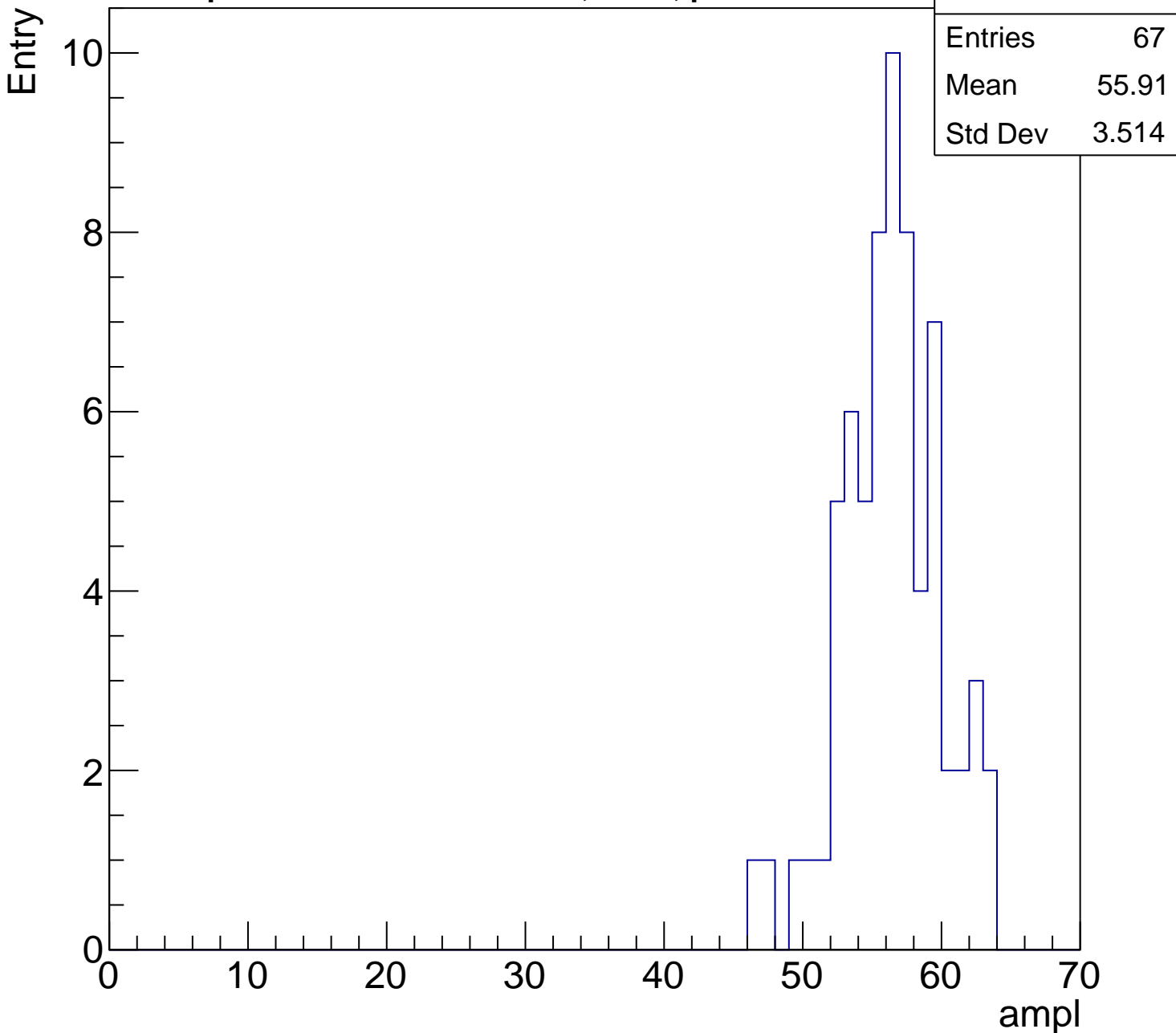
Entries	67
Mean	55.91
Std Dev	3.514

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

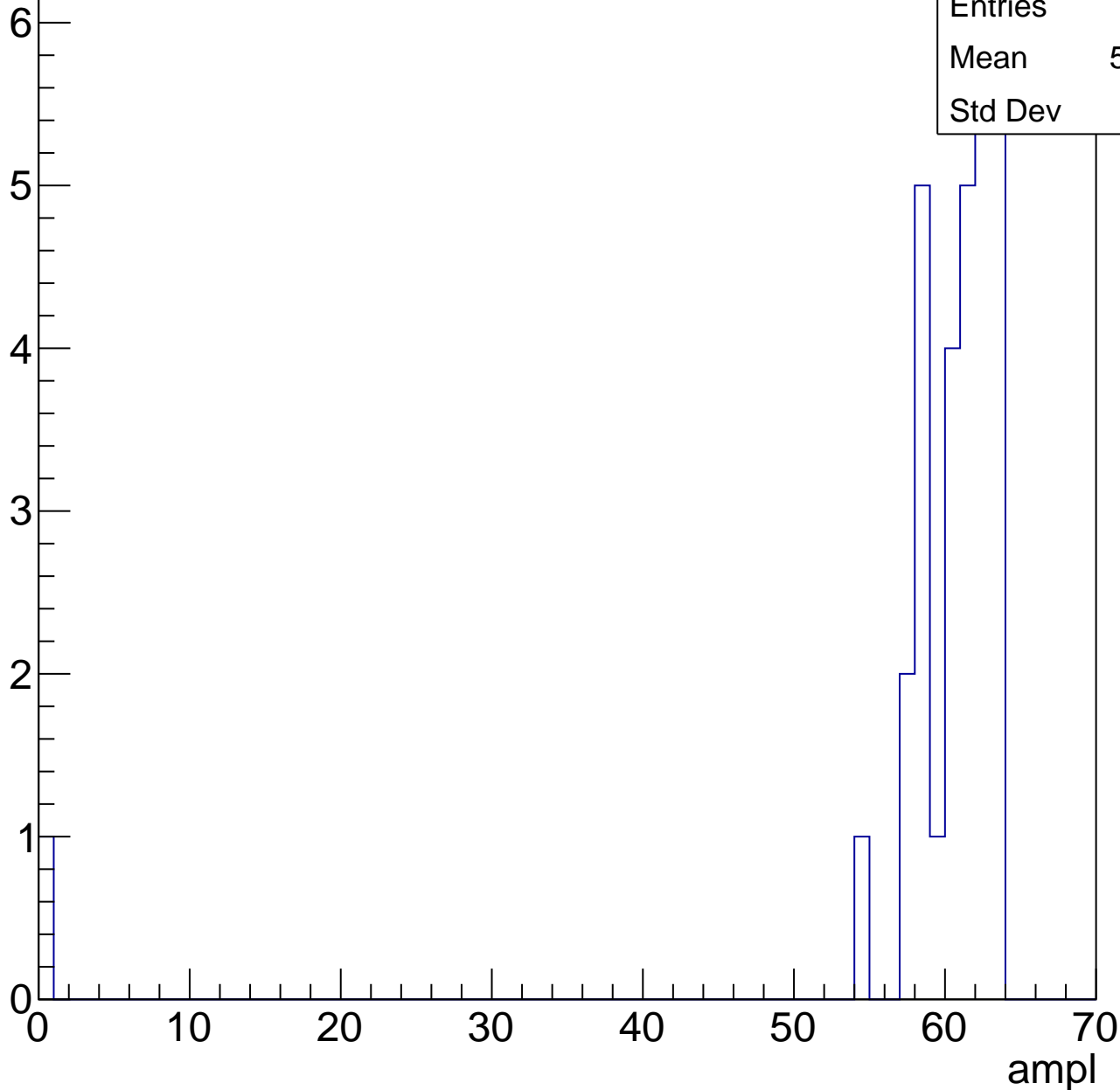


B1L103S, U8-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

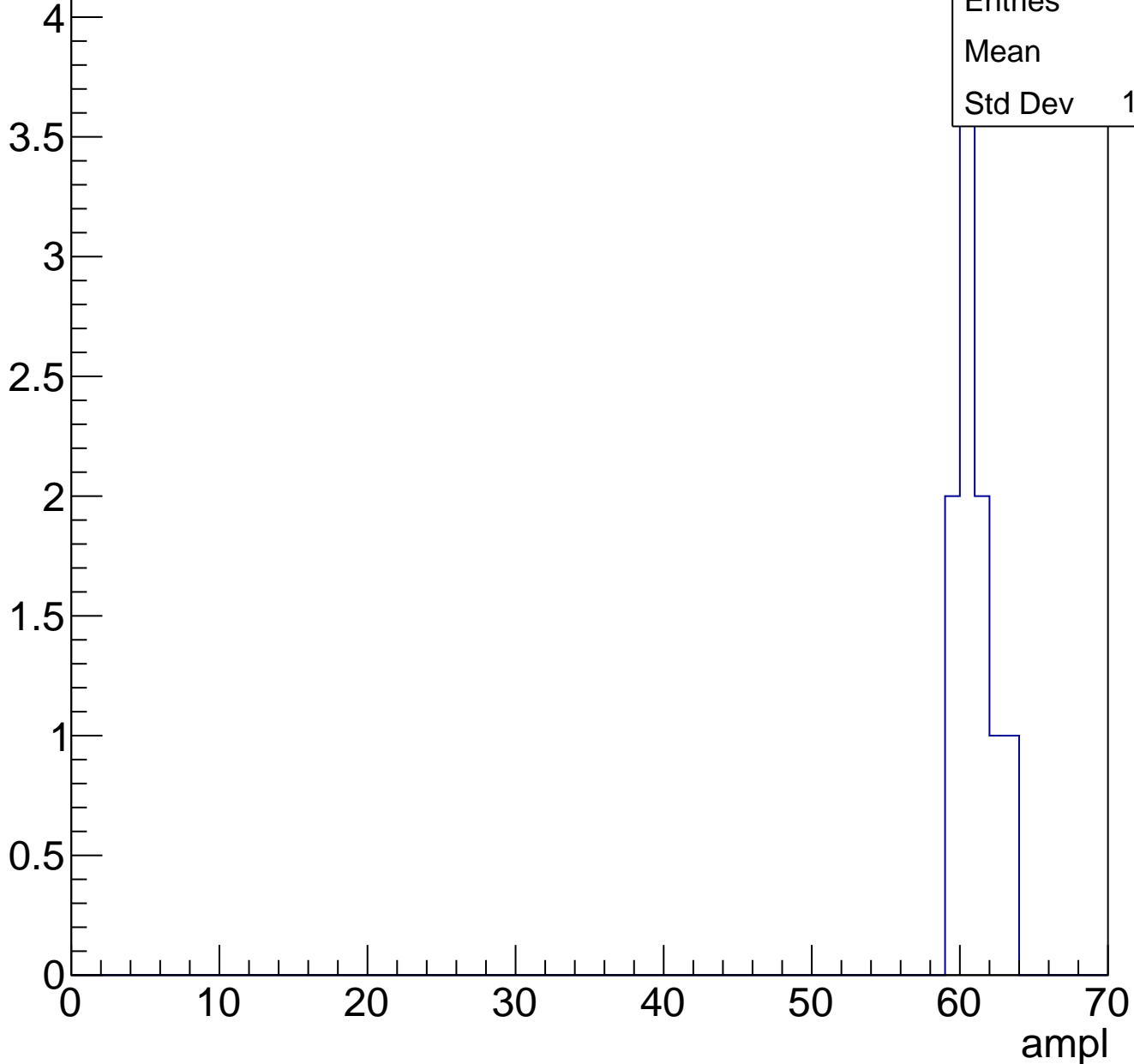
Entries	31
Mean	58.45
Std Dev	10.9



B1L103S, U8-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

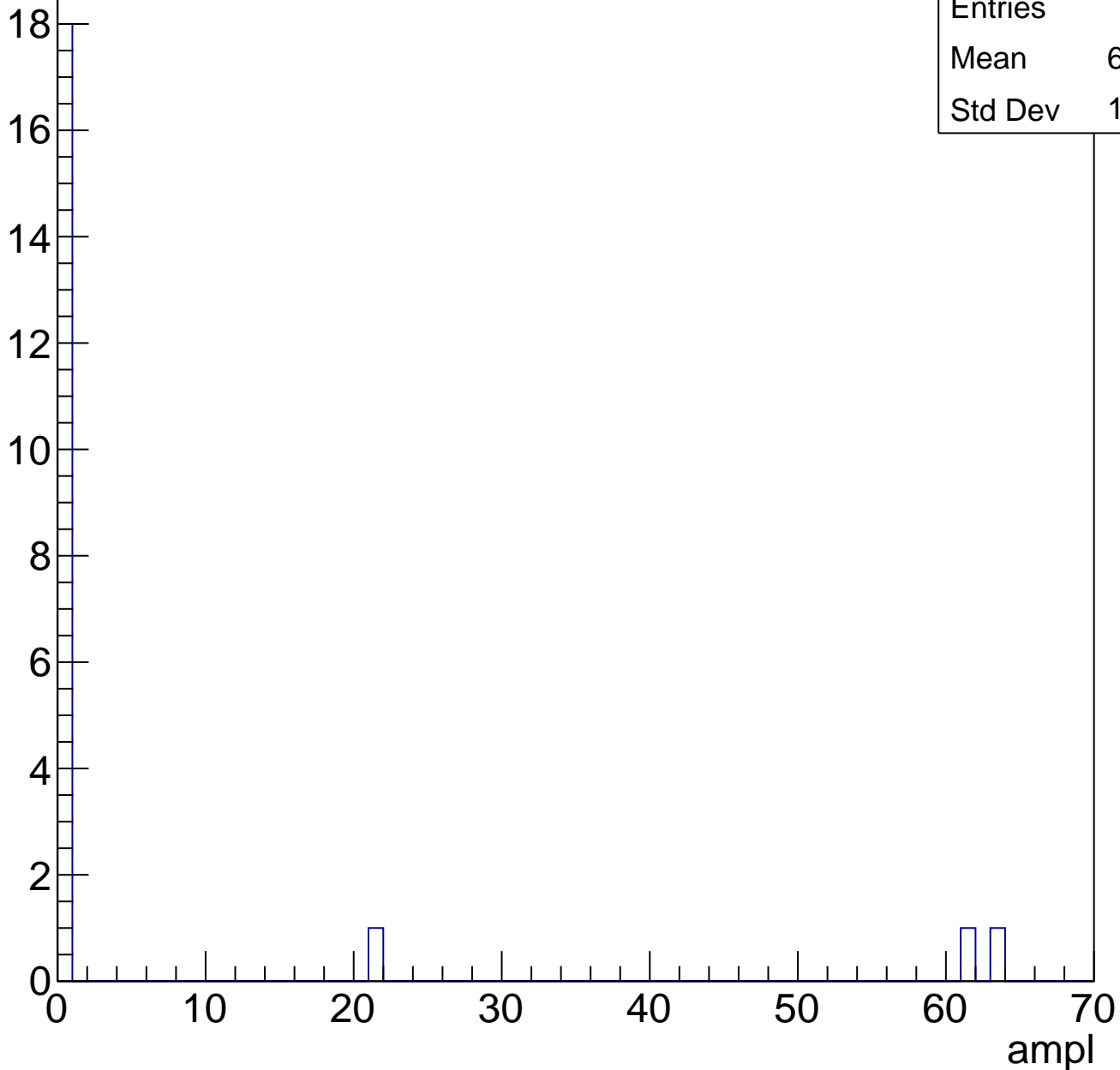


B1L103S, U8-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.905
Std Dev	18.43

Entry



B1L103S, U8-ch122, adc0

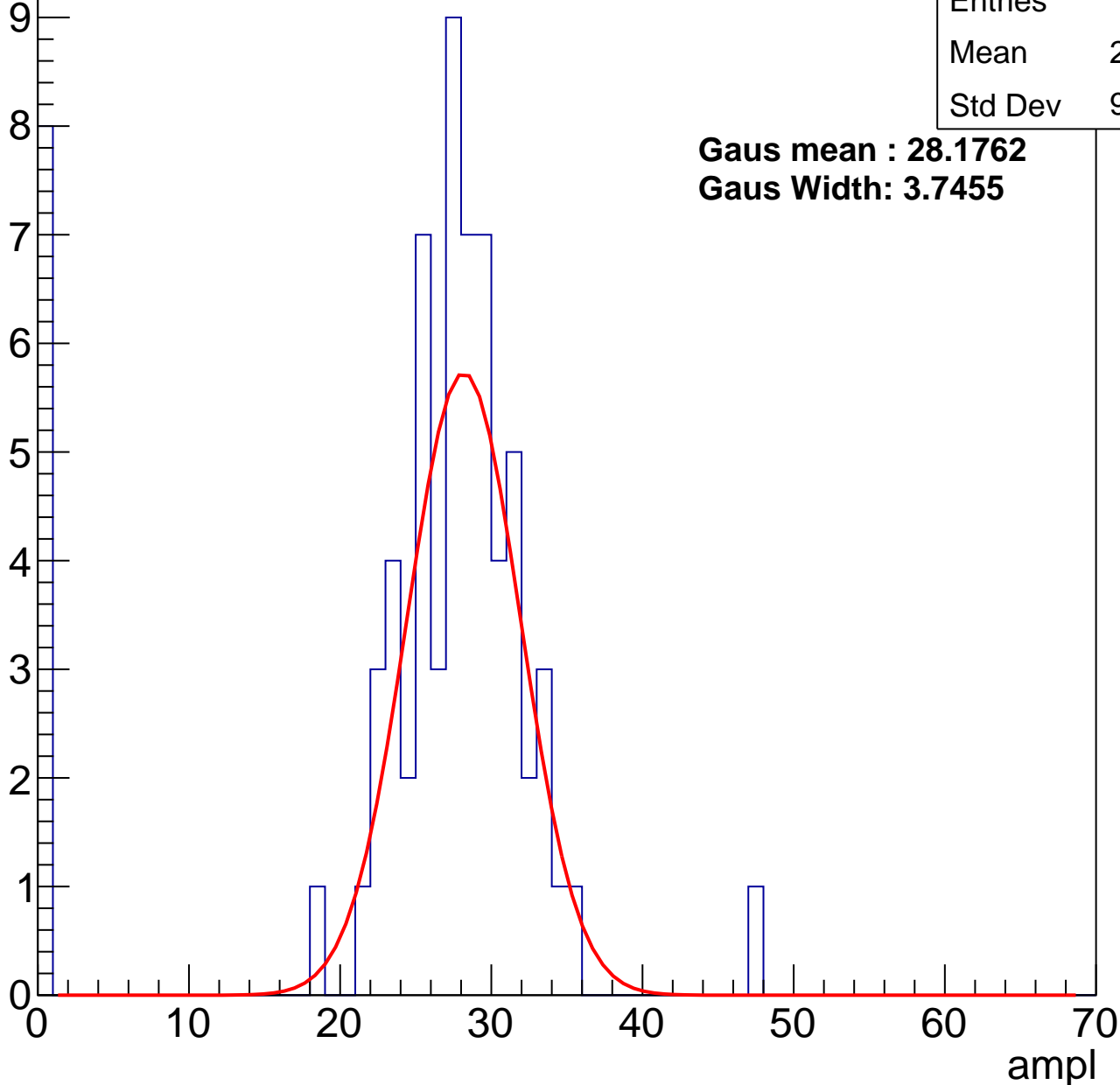
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	24.55
Std Dev	9.739

Gaus mean : 28.1762

Gaus Width: 3.7455



B1L103S, U8-ch122, adc1

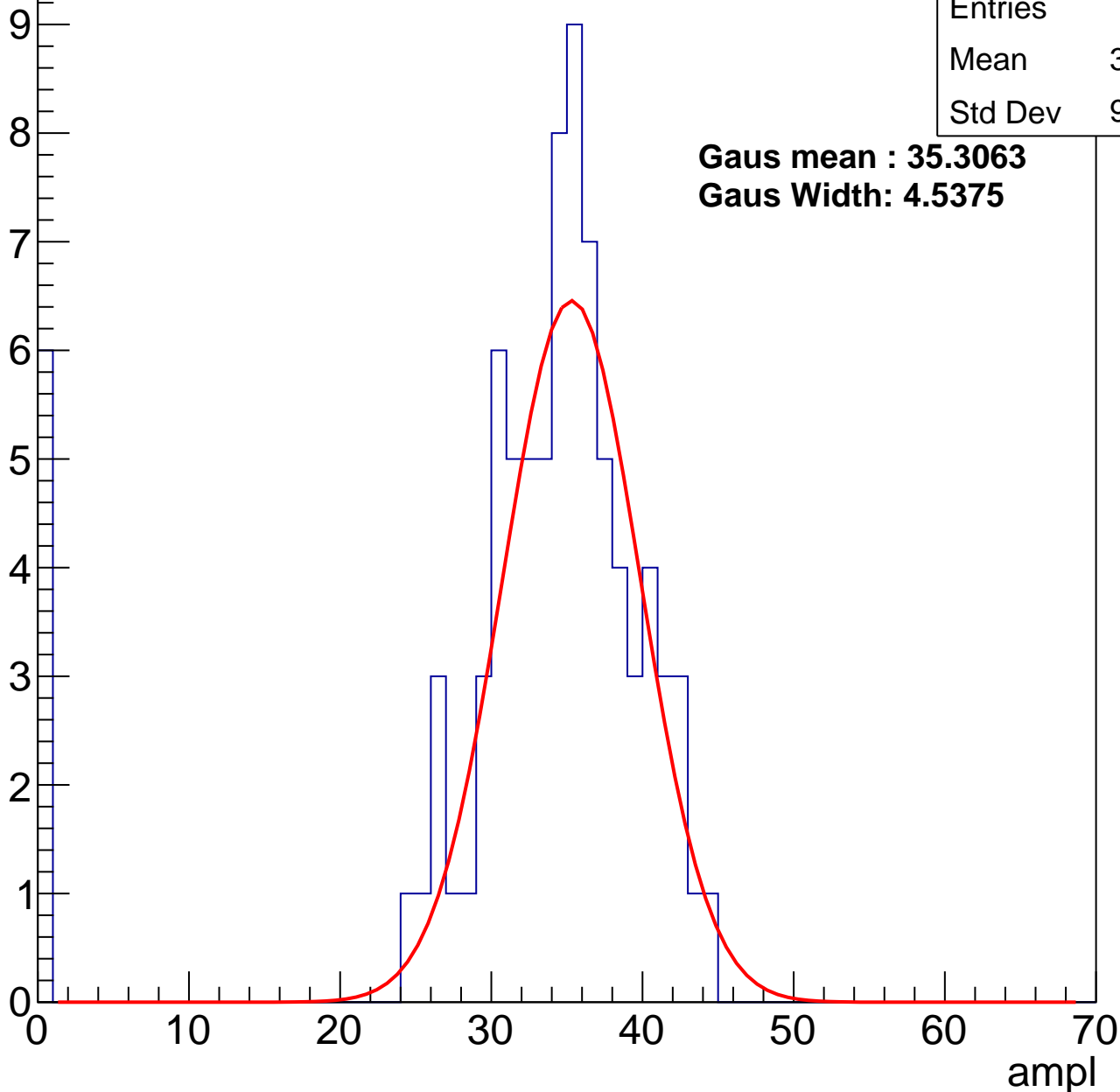
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	31.98
Std Dev	9.805

Gaus mean : 35.3063

Gaus Width: 4.5375



B1L103S, U8-ch122, adc2

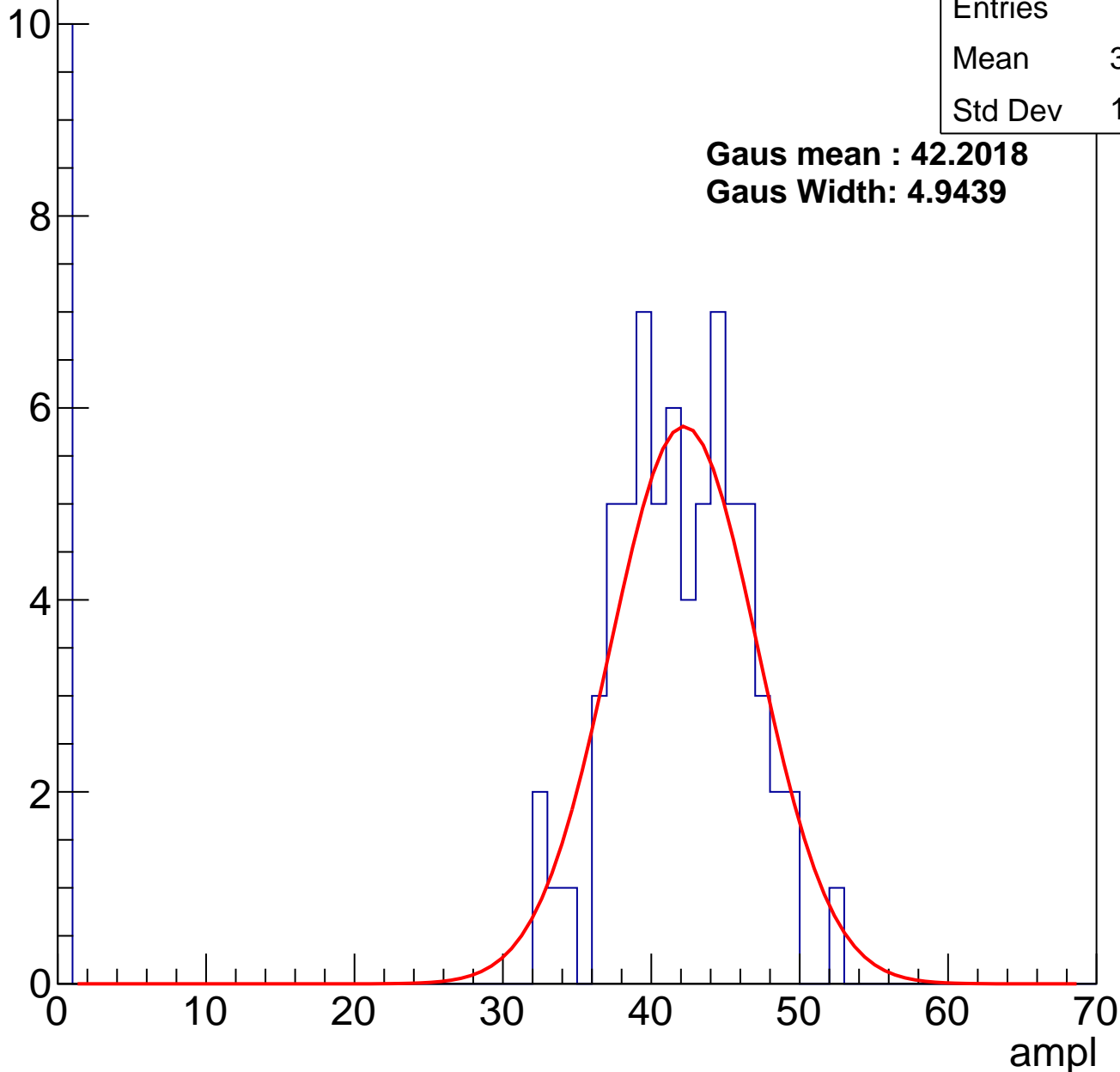
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	36.28
Std Dev	14.37

Gaus mean : 42.2018

Gaus Width: 4.9439

Entry

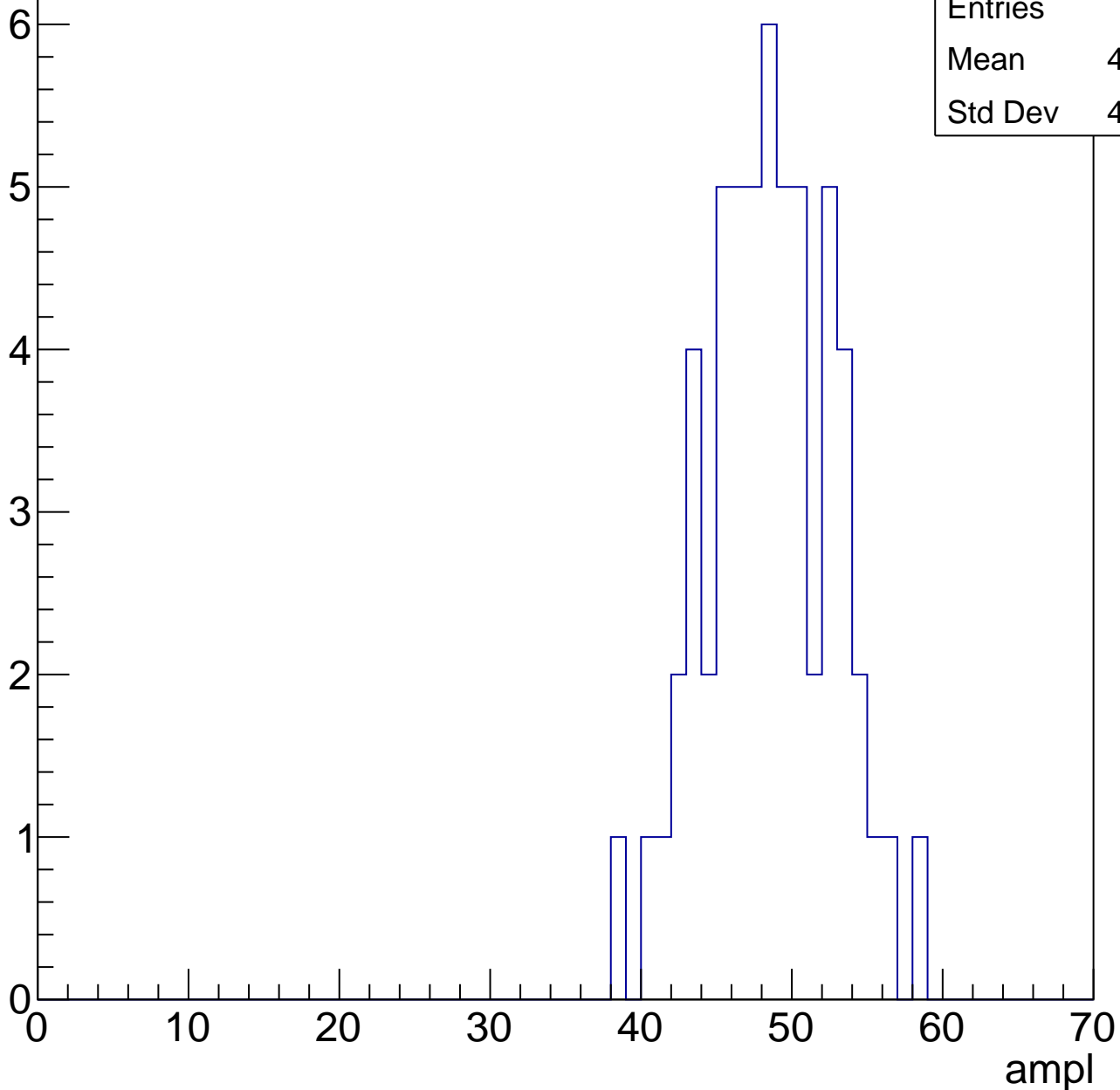


B1L103S, U8-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	48.05
Std Dev	4.142

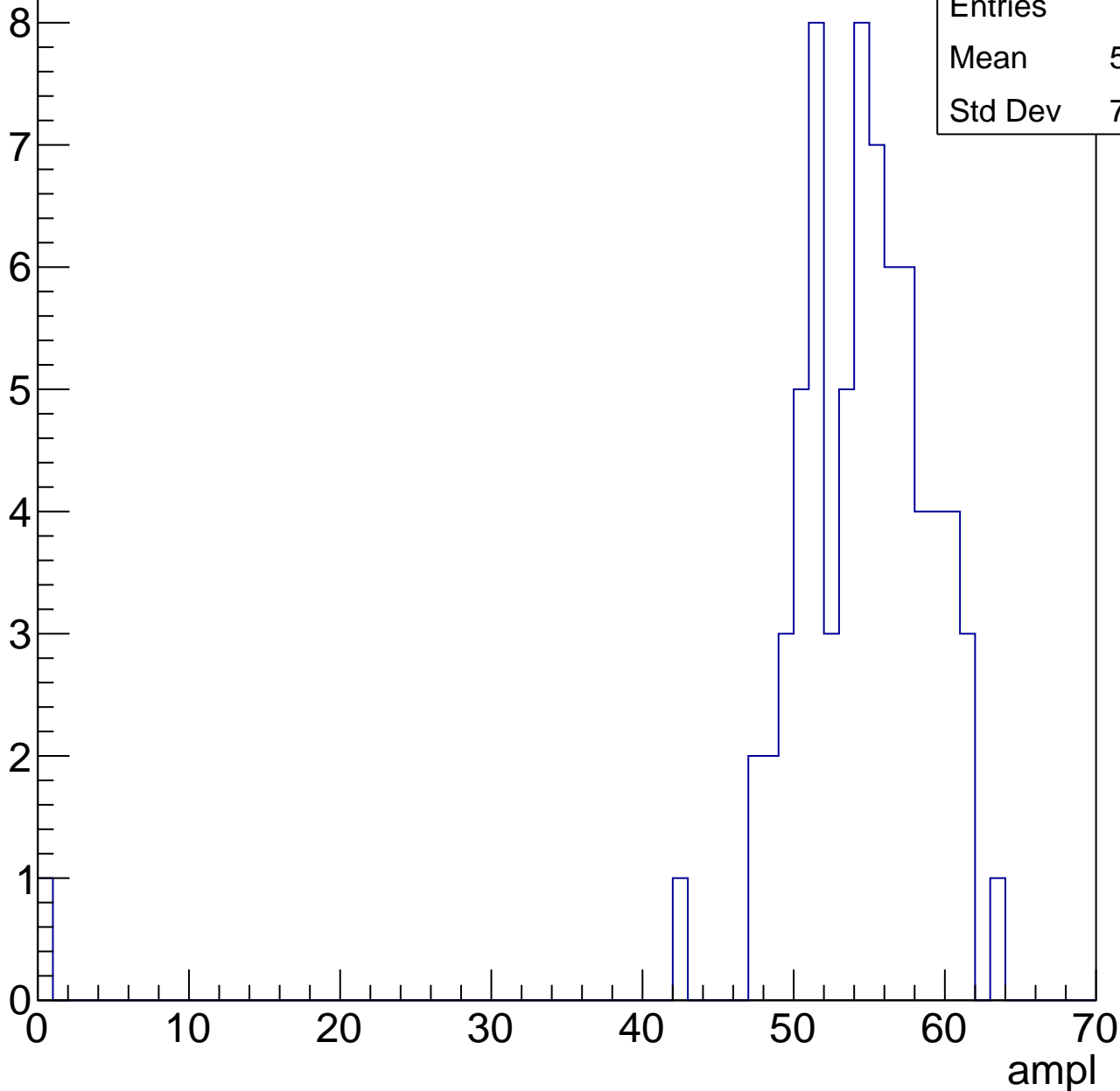


B1L103S, U8-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	53.52
Std Dev	7.474

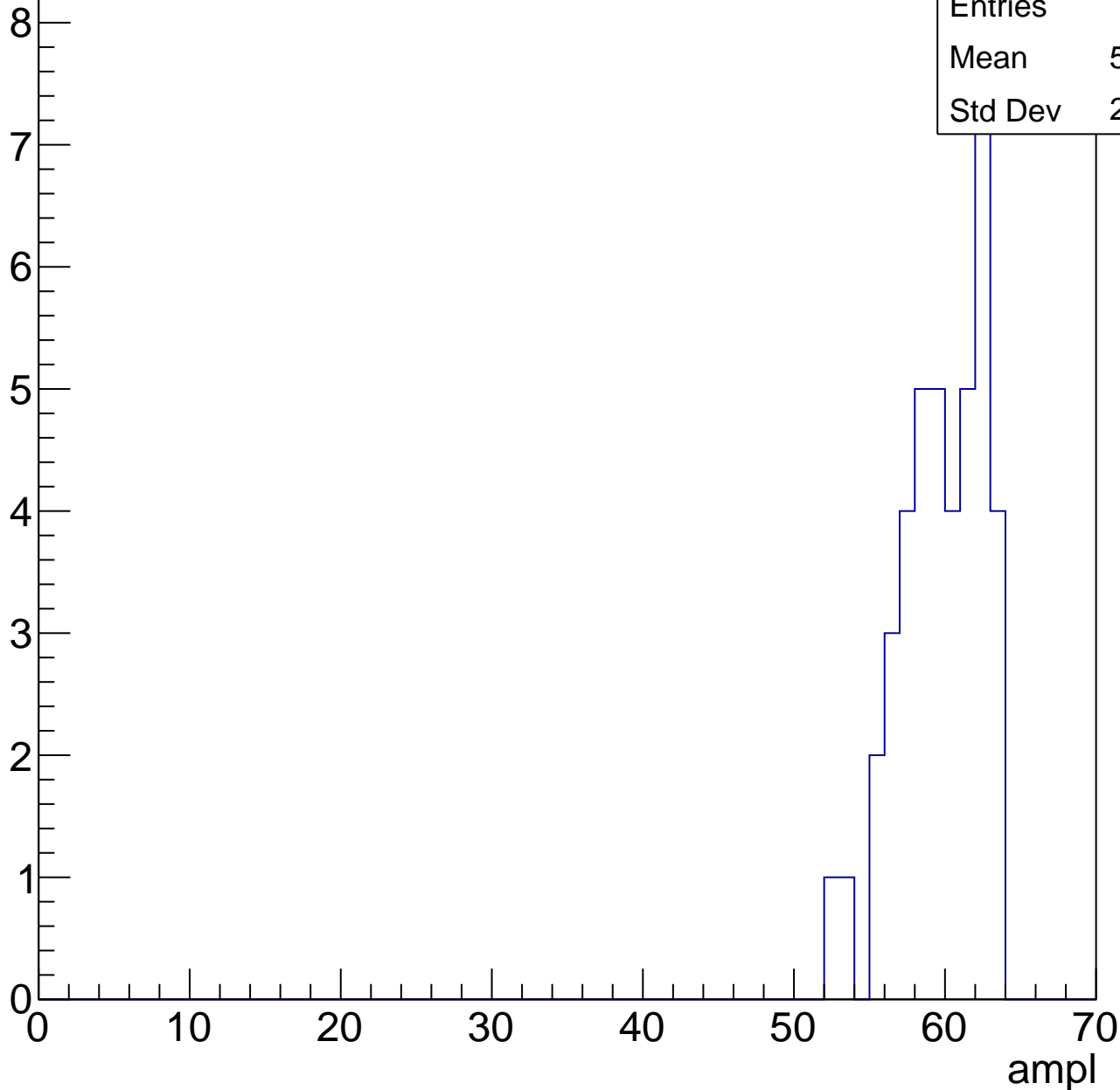


B1L103S, U8-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	59.26
Std Dev	2.769

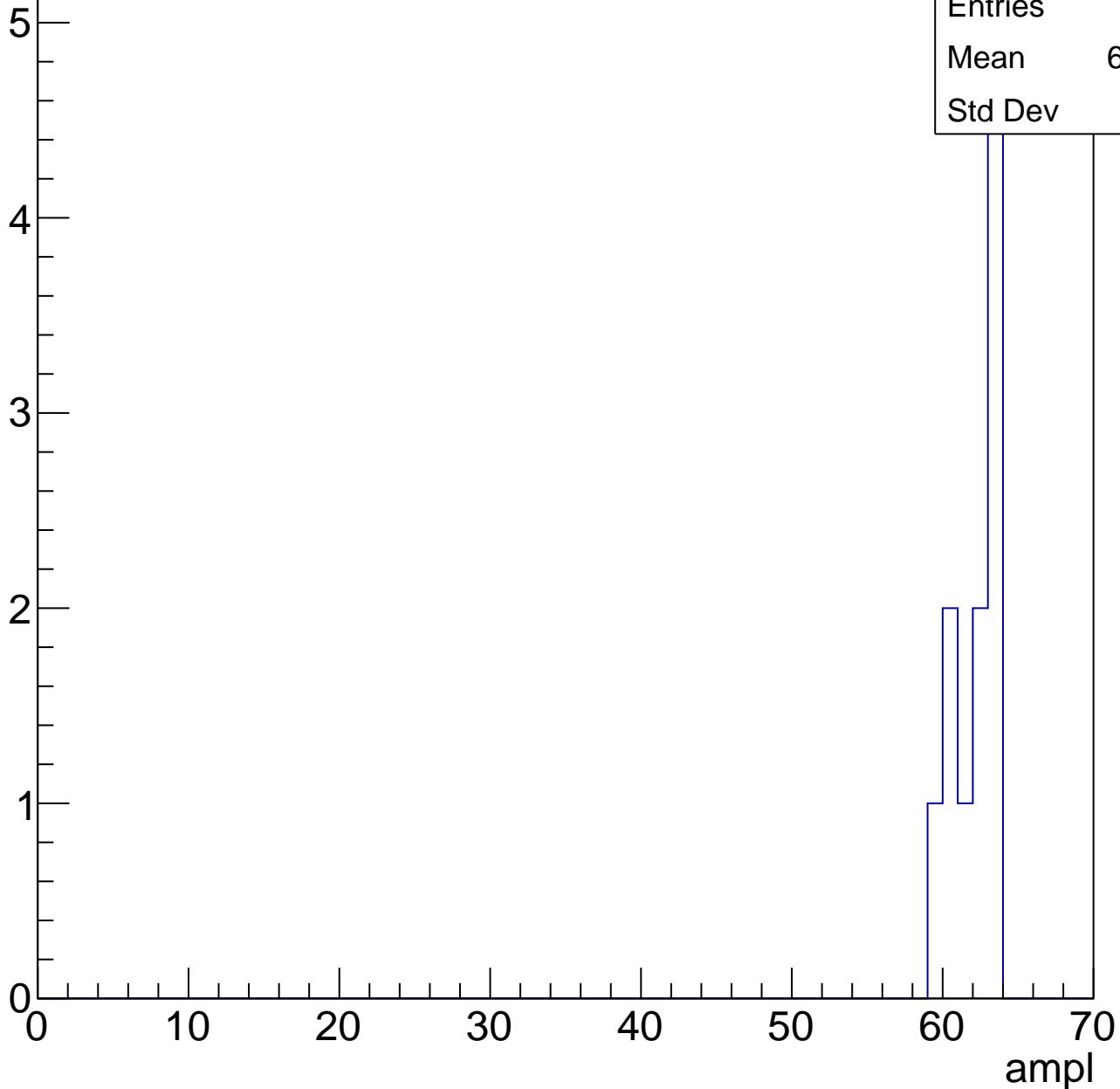


B1L103S, U8-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	61.73
Std Dev	1.42



B1L103S, U8-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.105
Std Dev	13.94

Entry

16

14

12

10

8

6

4

2

0

0

10

20

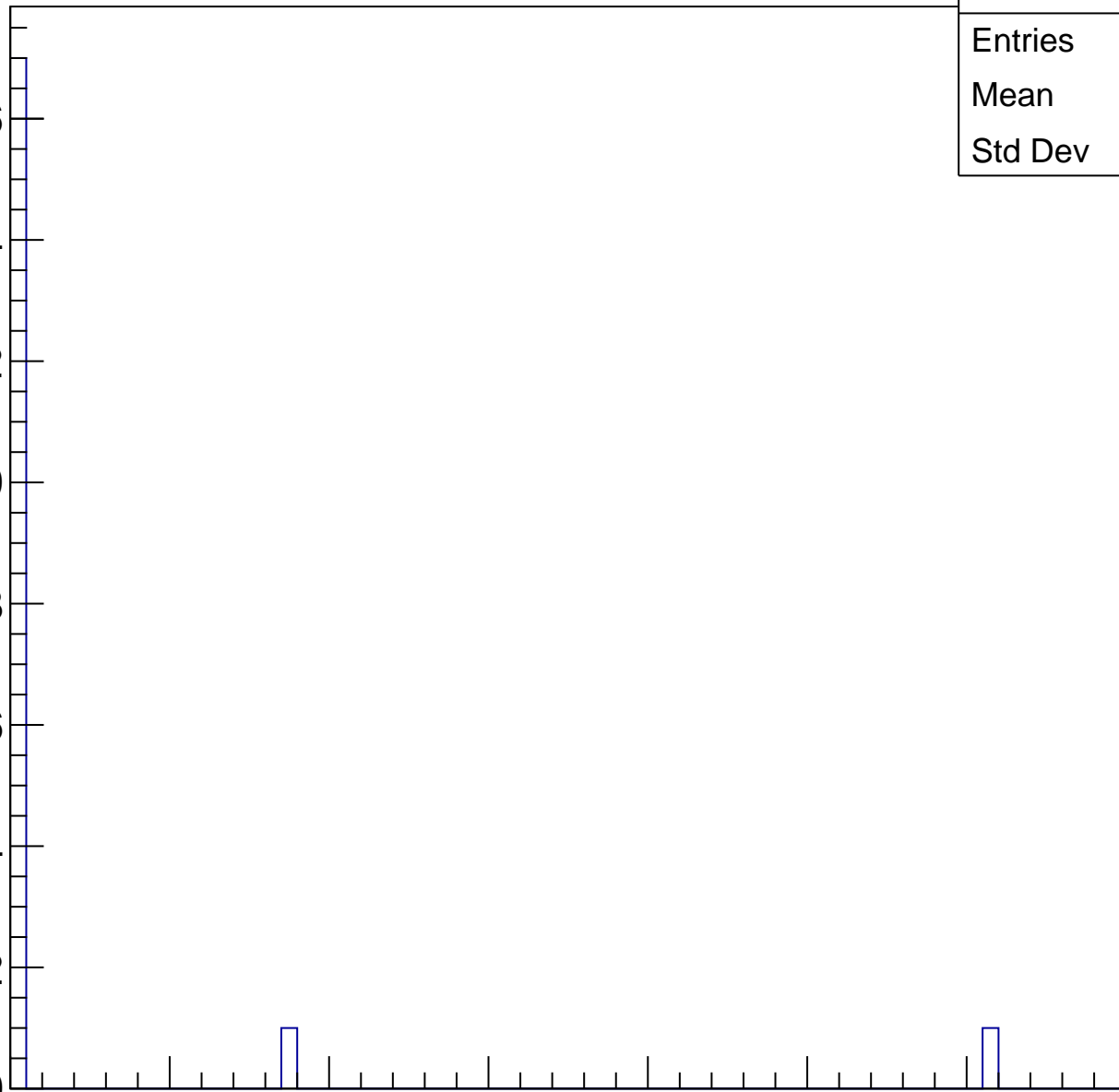
30

40

50

60

ampl



B1L103S, U8-ch123, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	24.89
Std Dev	11.79

Gaus mean : 30.7088

Gaus Width: 3.5551

Entry

12

10

8

6

4

2

0

0

10

20

30

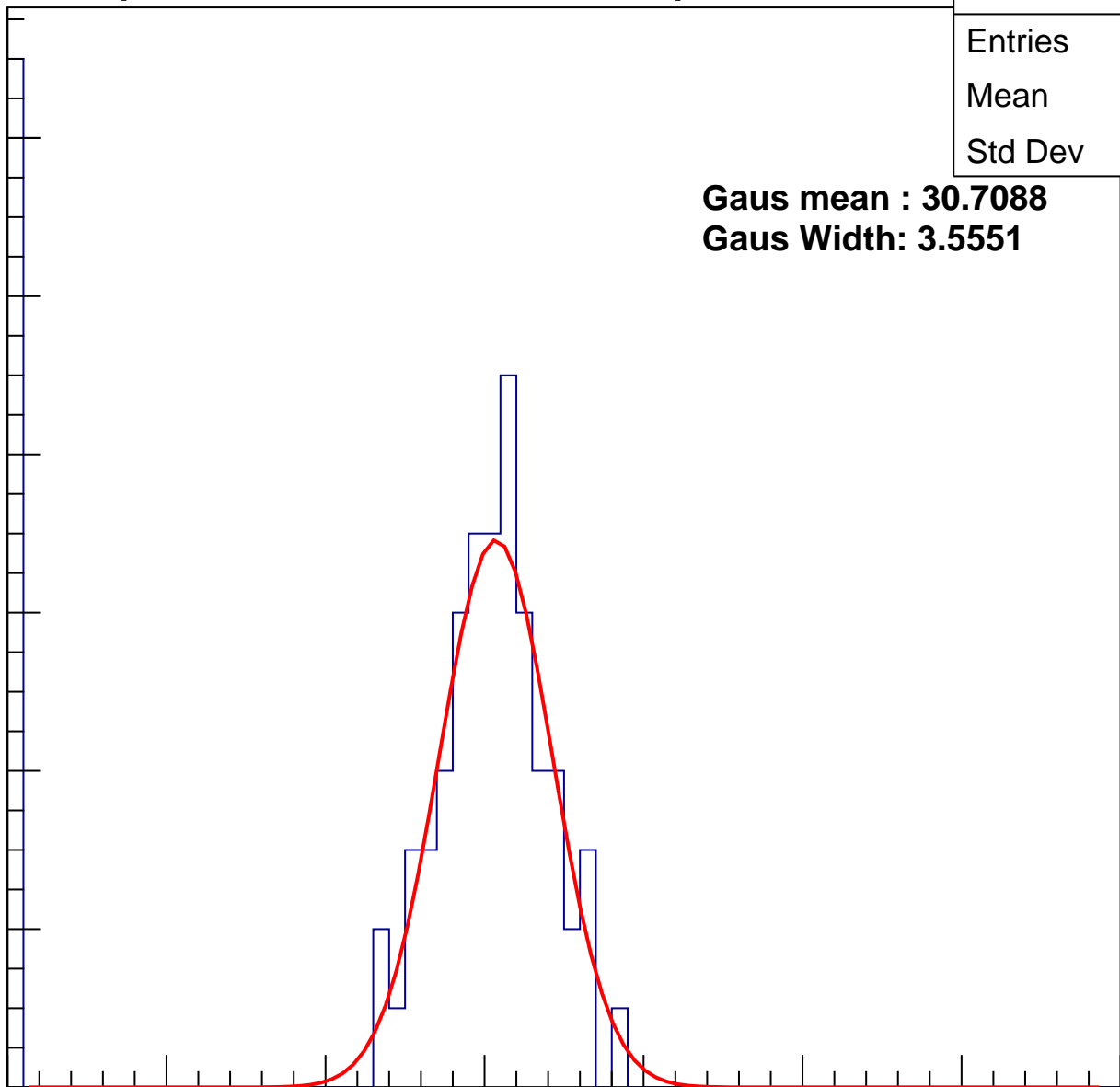
40

50

60

70

ampl



B1L103S, U8-ch123, adc1

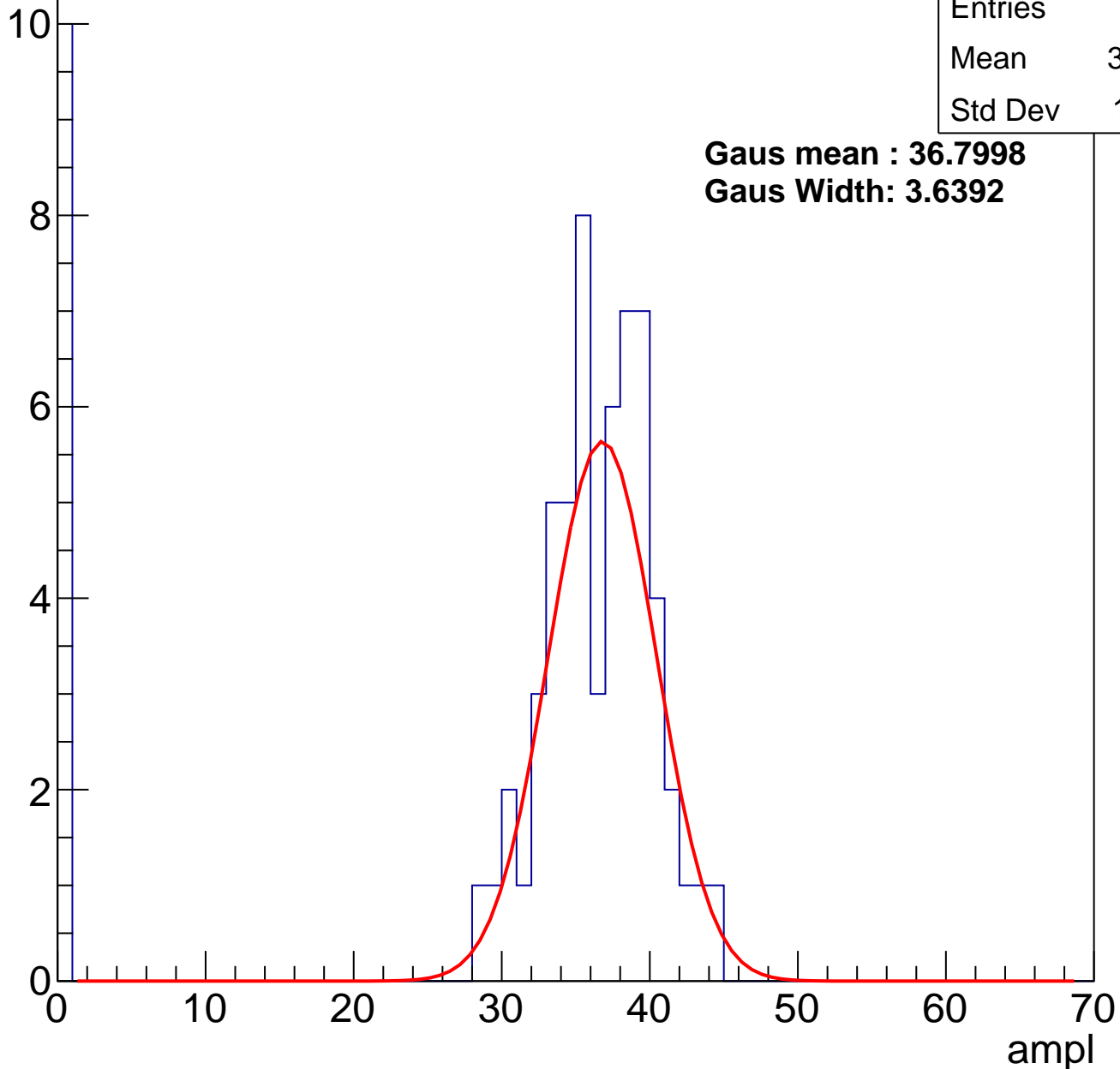
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	30.87
Std Dev	13.21

Gaus mean : 36.7998

Gaus Width: 3.6392

Entry



B1L103S, U8-ch123, adc2

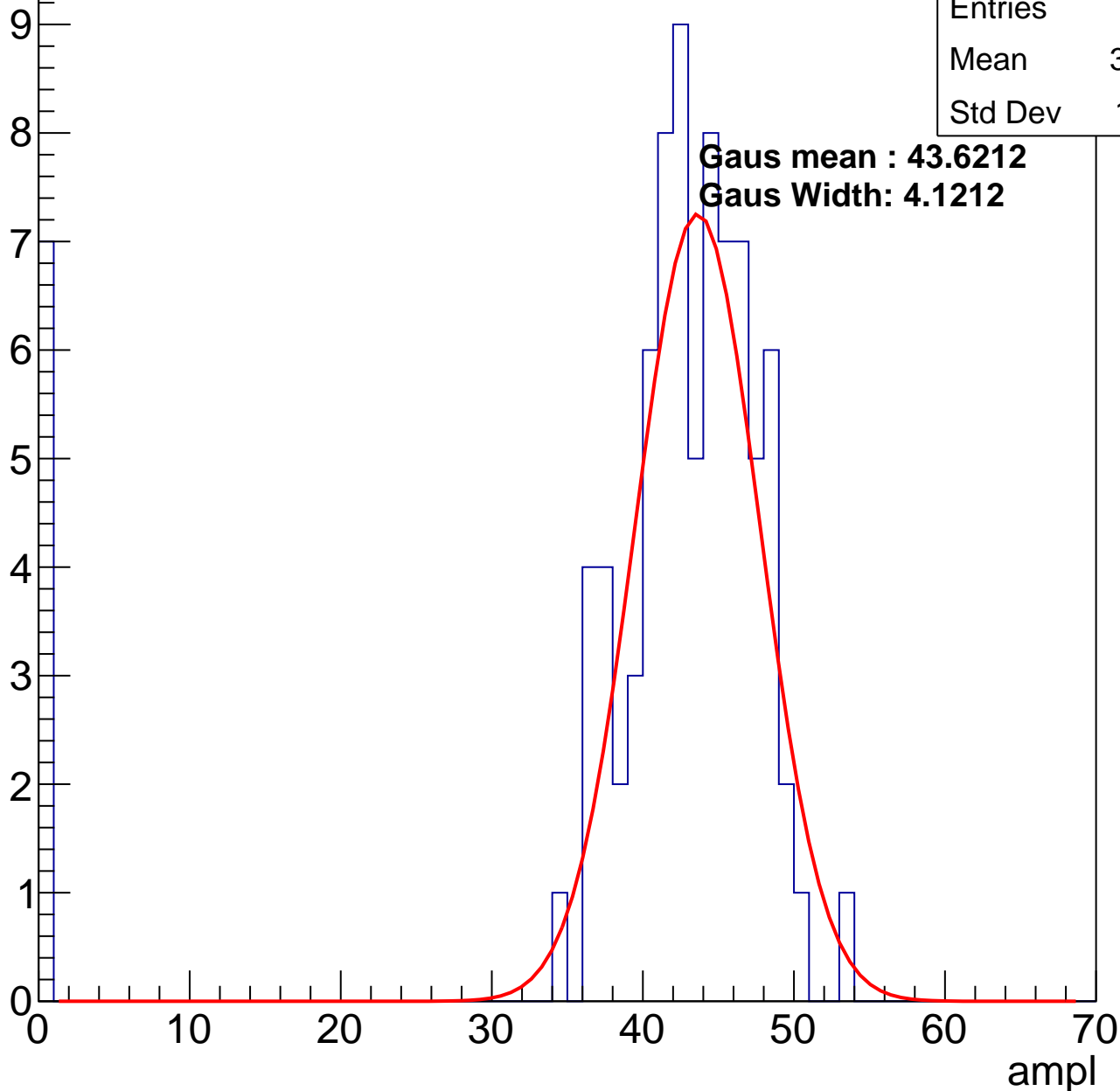
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	39.45
Std Dev	12.31

Gaus mean : 43.6212

Gaus Width: 4.1212

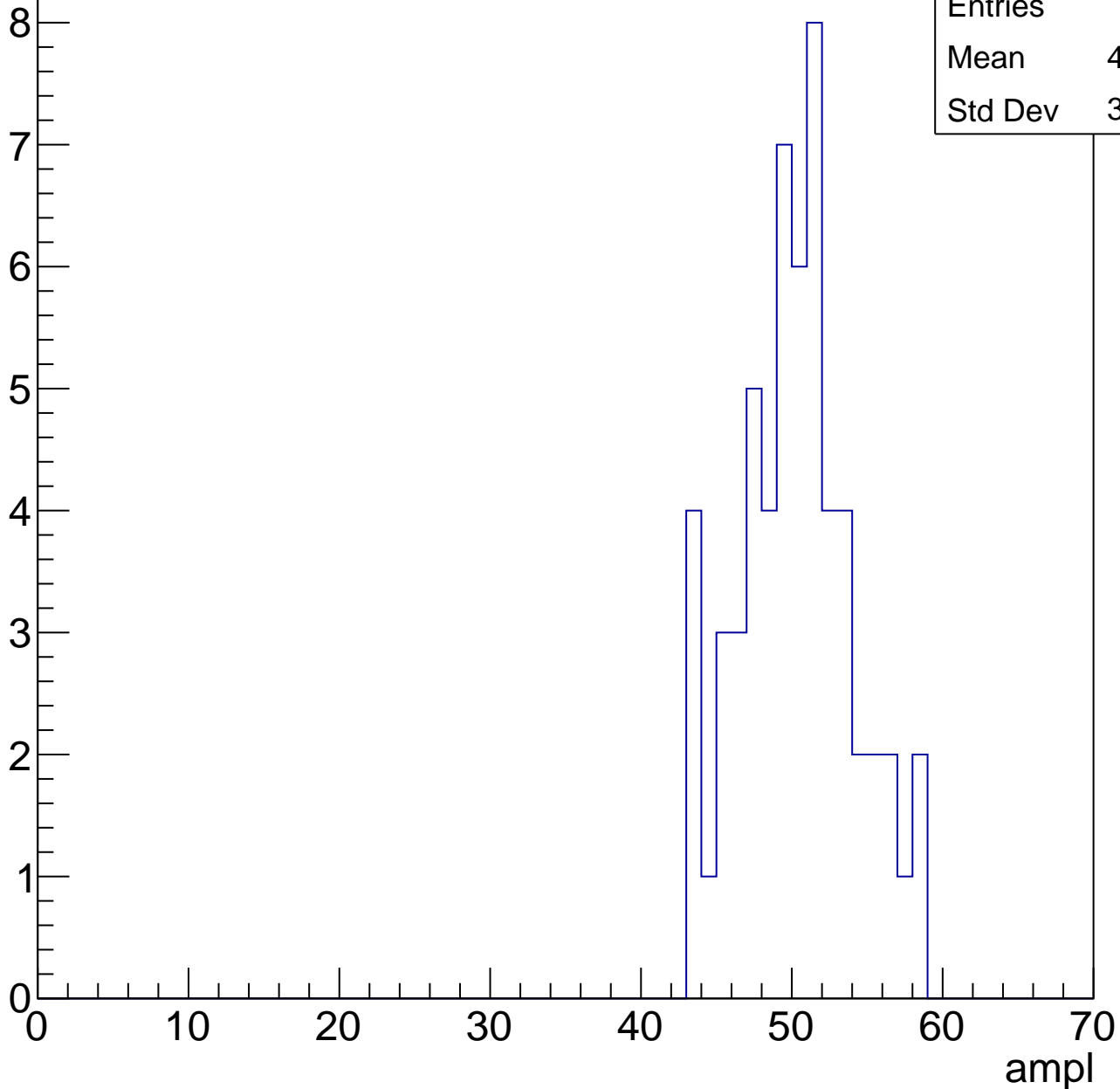


B1L103S, U8-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

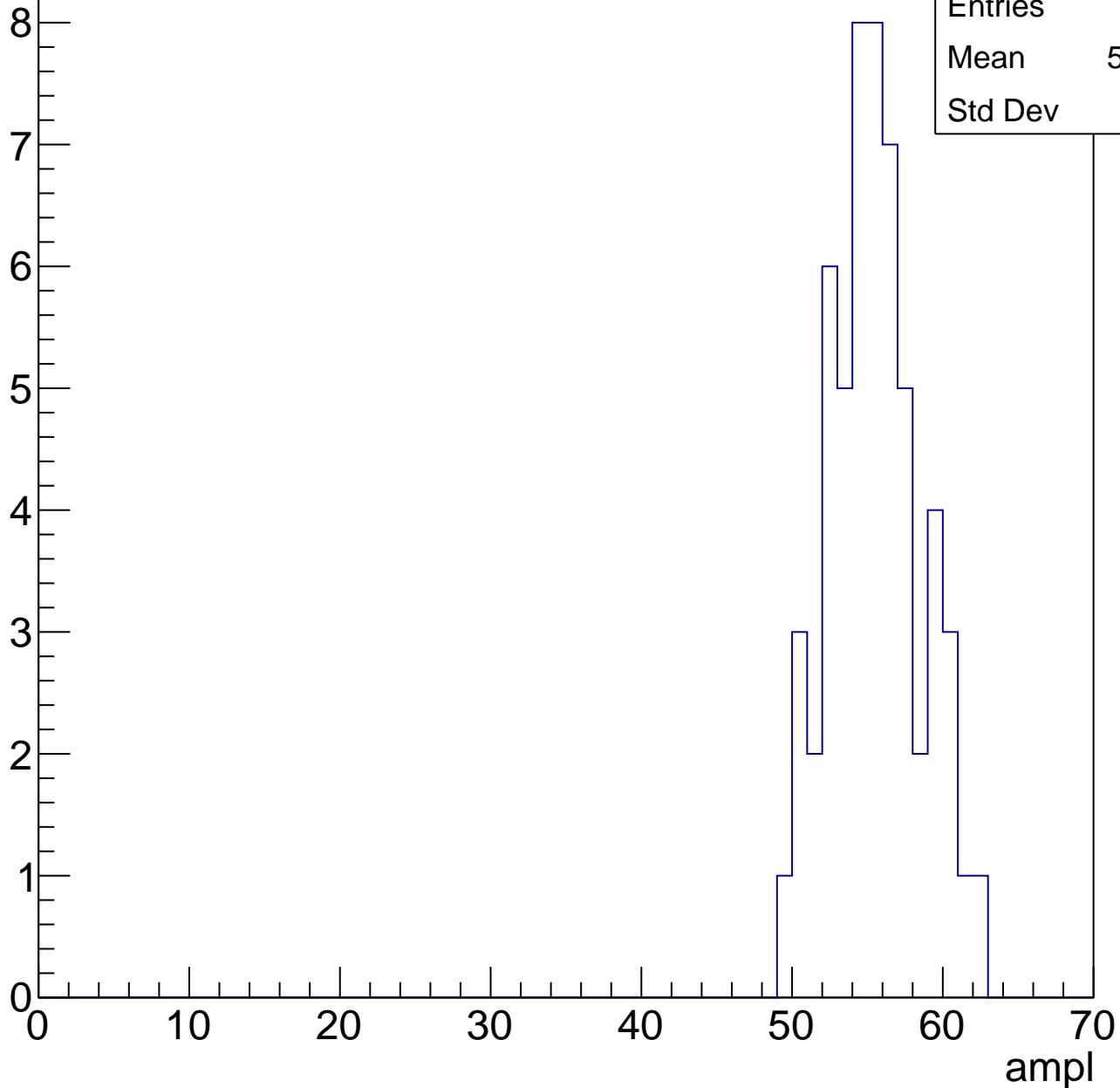
Entries	58
Mean	49.83
Std Dev	3.742



B1L103S, U8-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

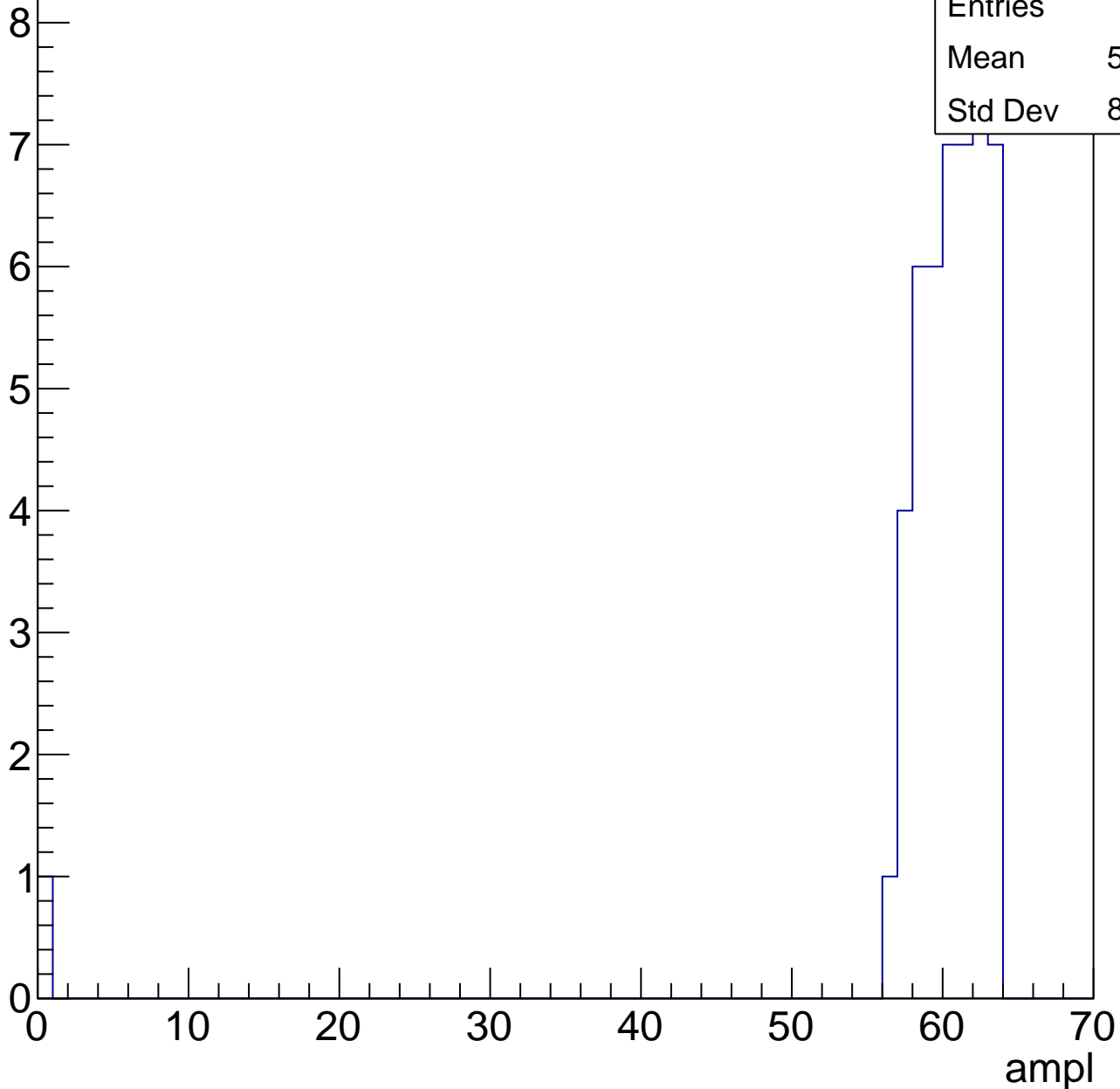


B1L103S, U8-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.94
Std Dev	8.909



B1L103S, U8-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch124, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	25.53
Std Dev	10.41

Gaus mean : 29.8565

Gaus Width: 3.8376

Entry

10

8

6

4

2

0

0

10

20

30

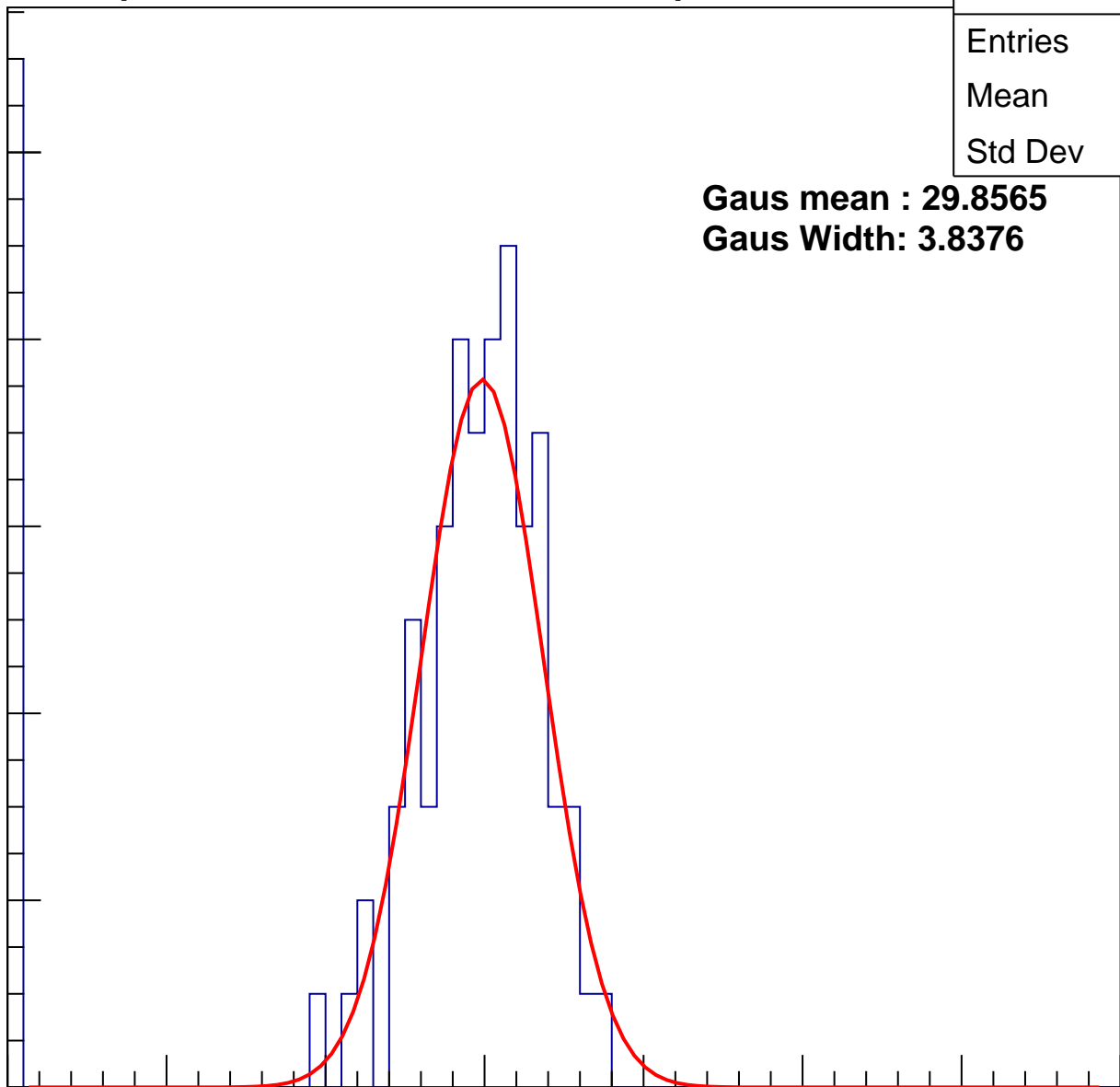
40

50

60

70

ampl



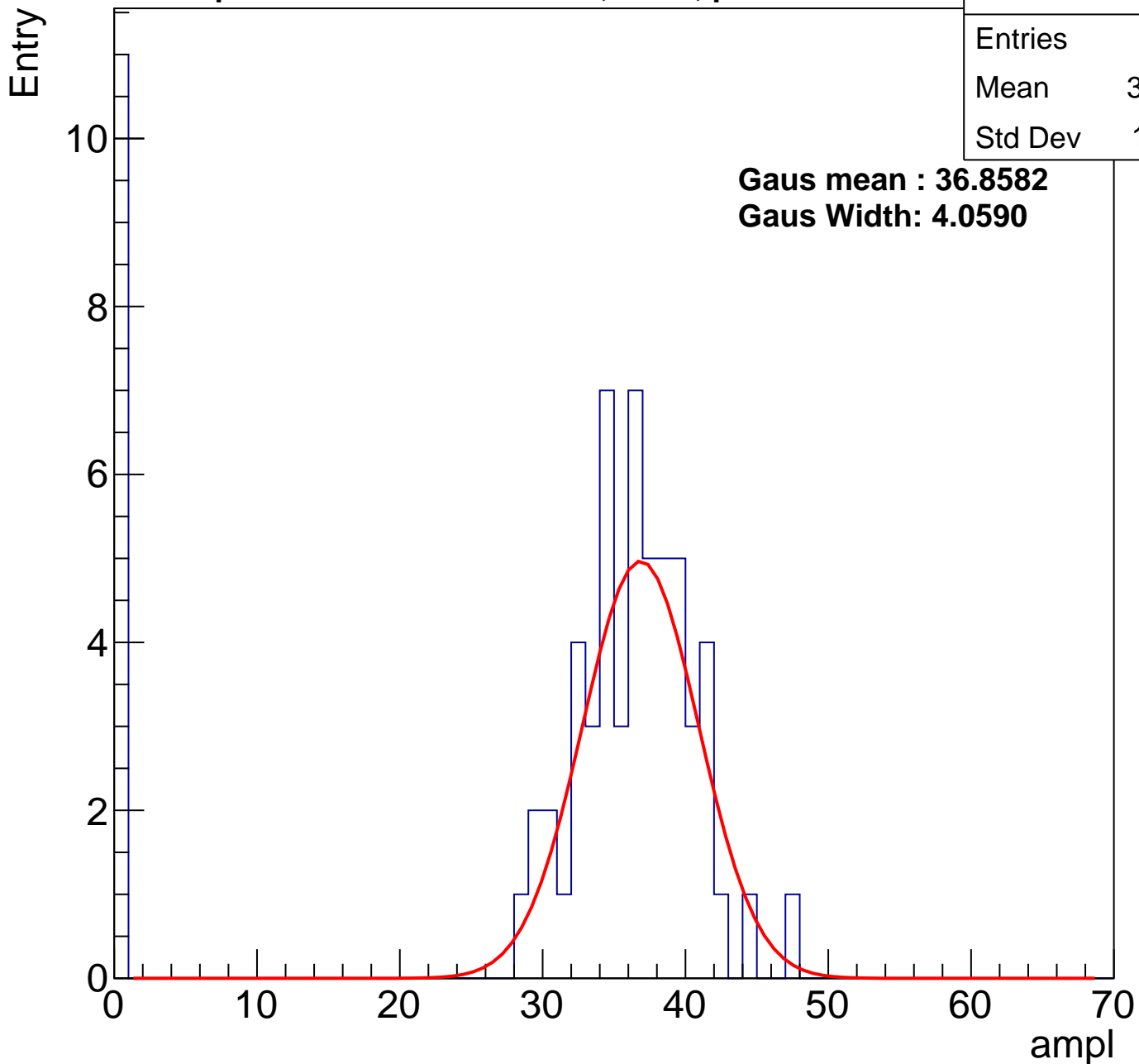
B1L103S, U8-ch124, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	30.09
Std Dev	13.91

Gaus mean : 36.8582

Gaus Width: 4.0590



B1L103S, U8-ch124, adc2

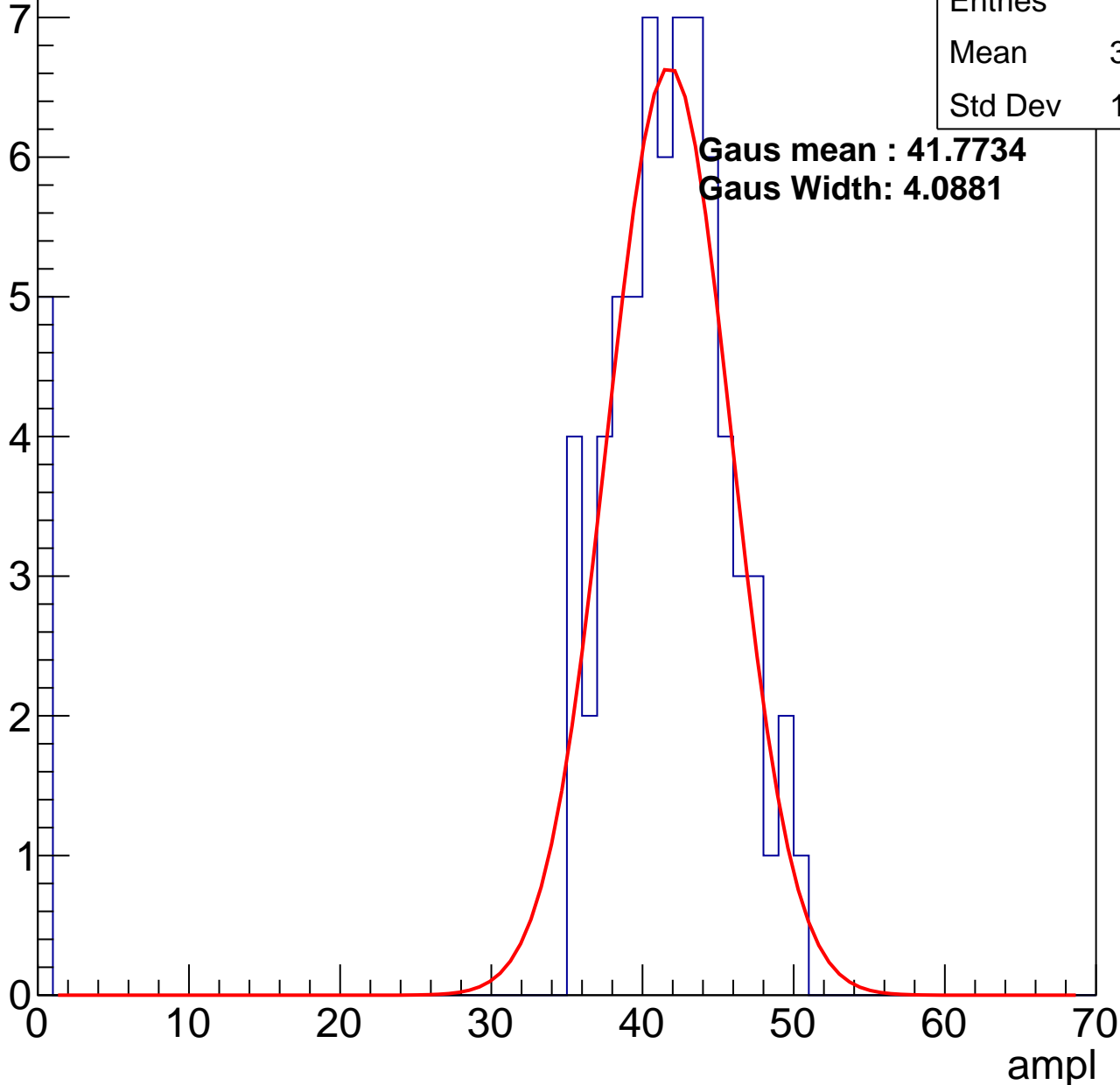
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	38.68
Std Dev	11.14

Gaus mean : 41.7734

Gaus Width: 4.0881

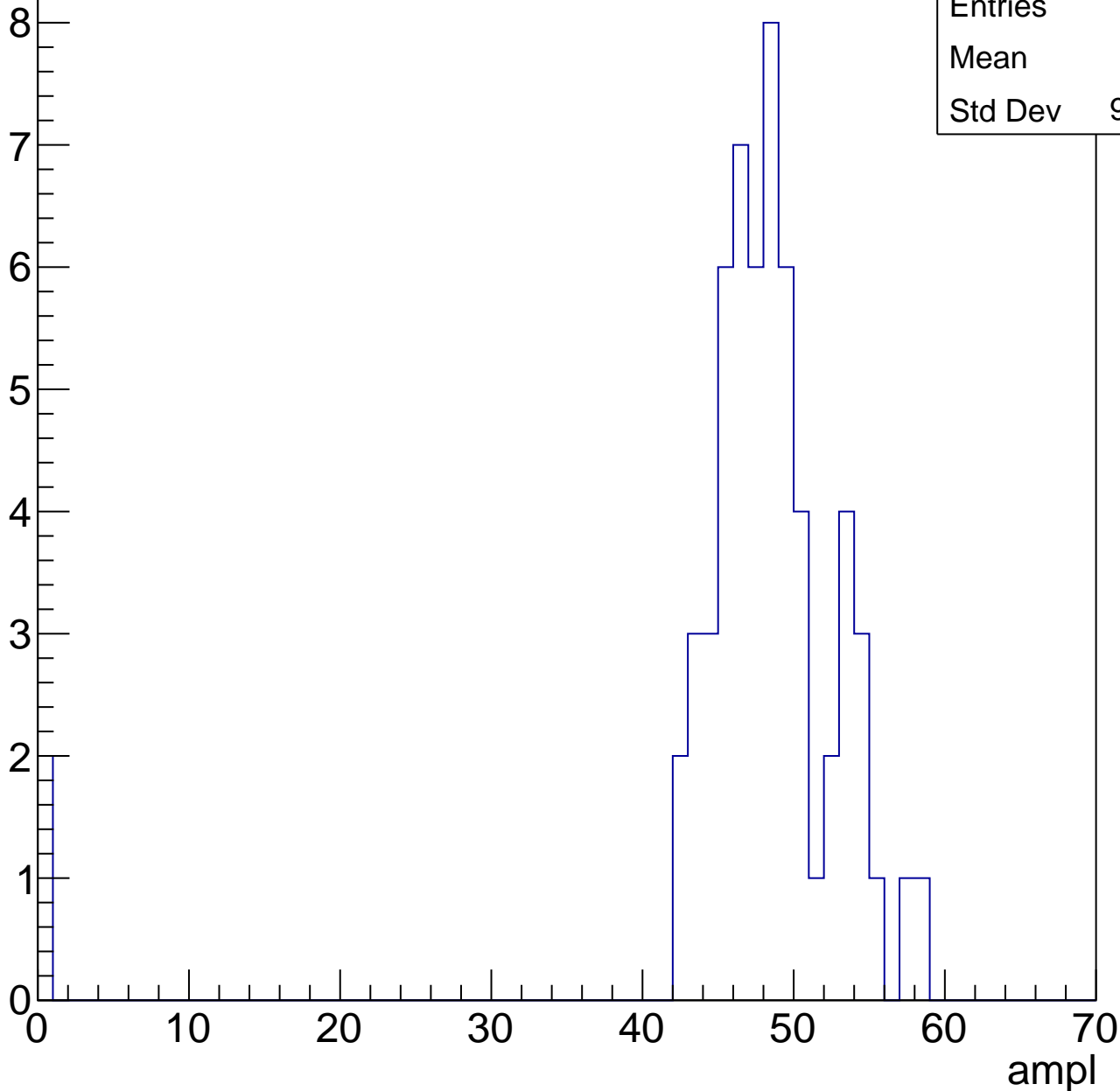


B1L103S, U8-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	46.6
Std Dev	9.376



B1L103S, U8-ch124, adc4

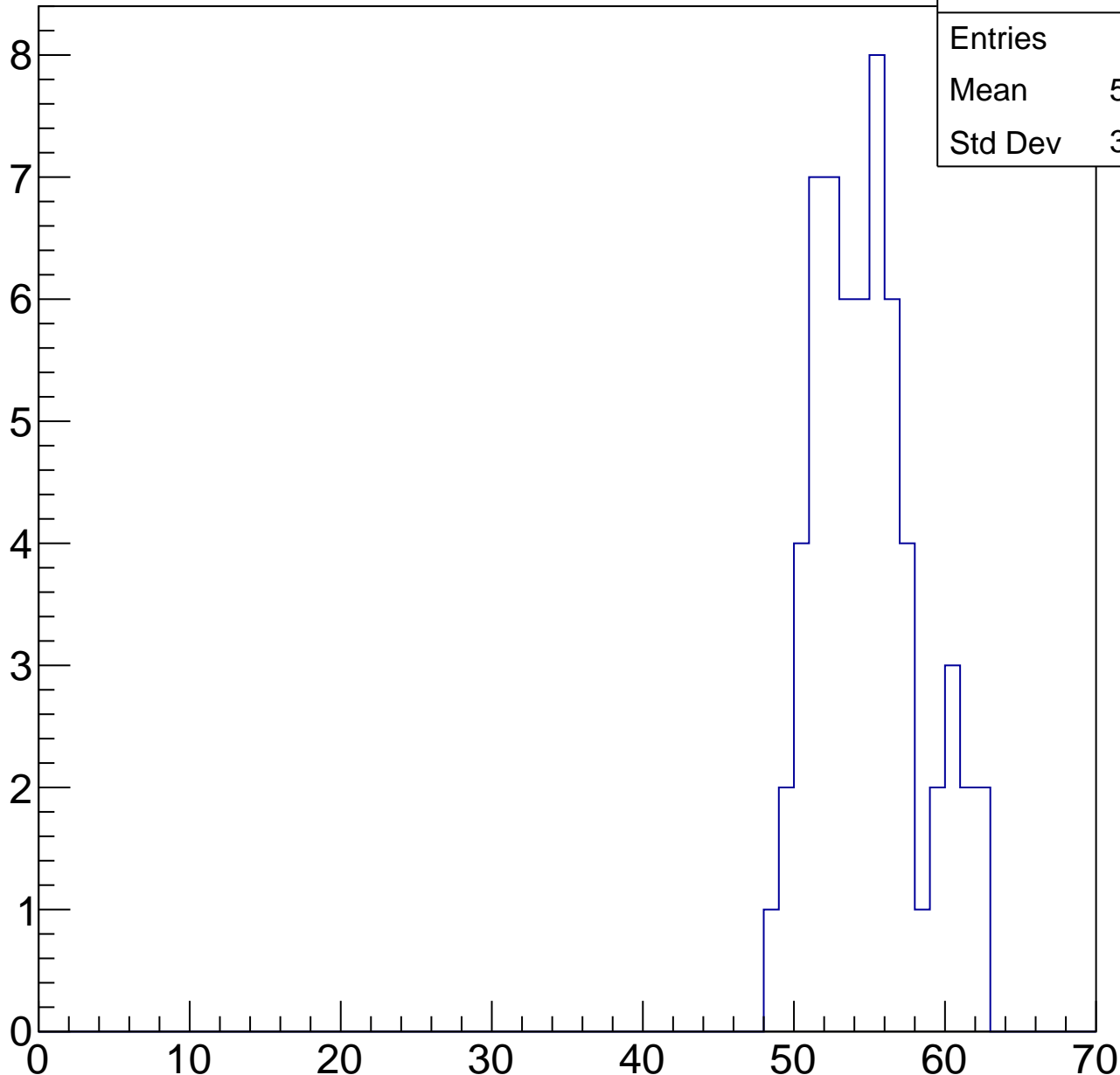
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	54.34
Std Dev	3.406

ampl

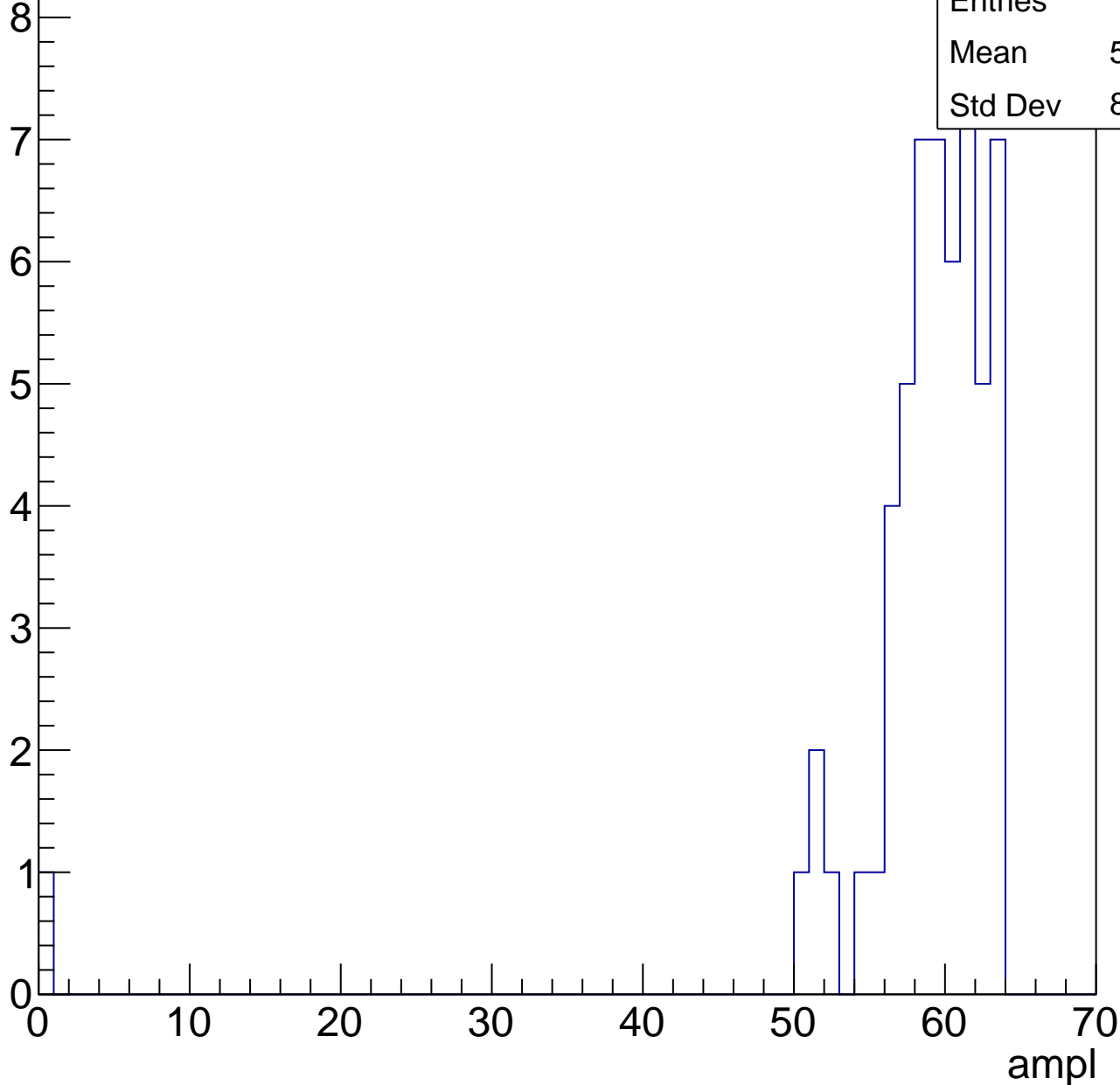


B1L103S, U8-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

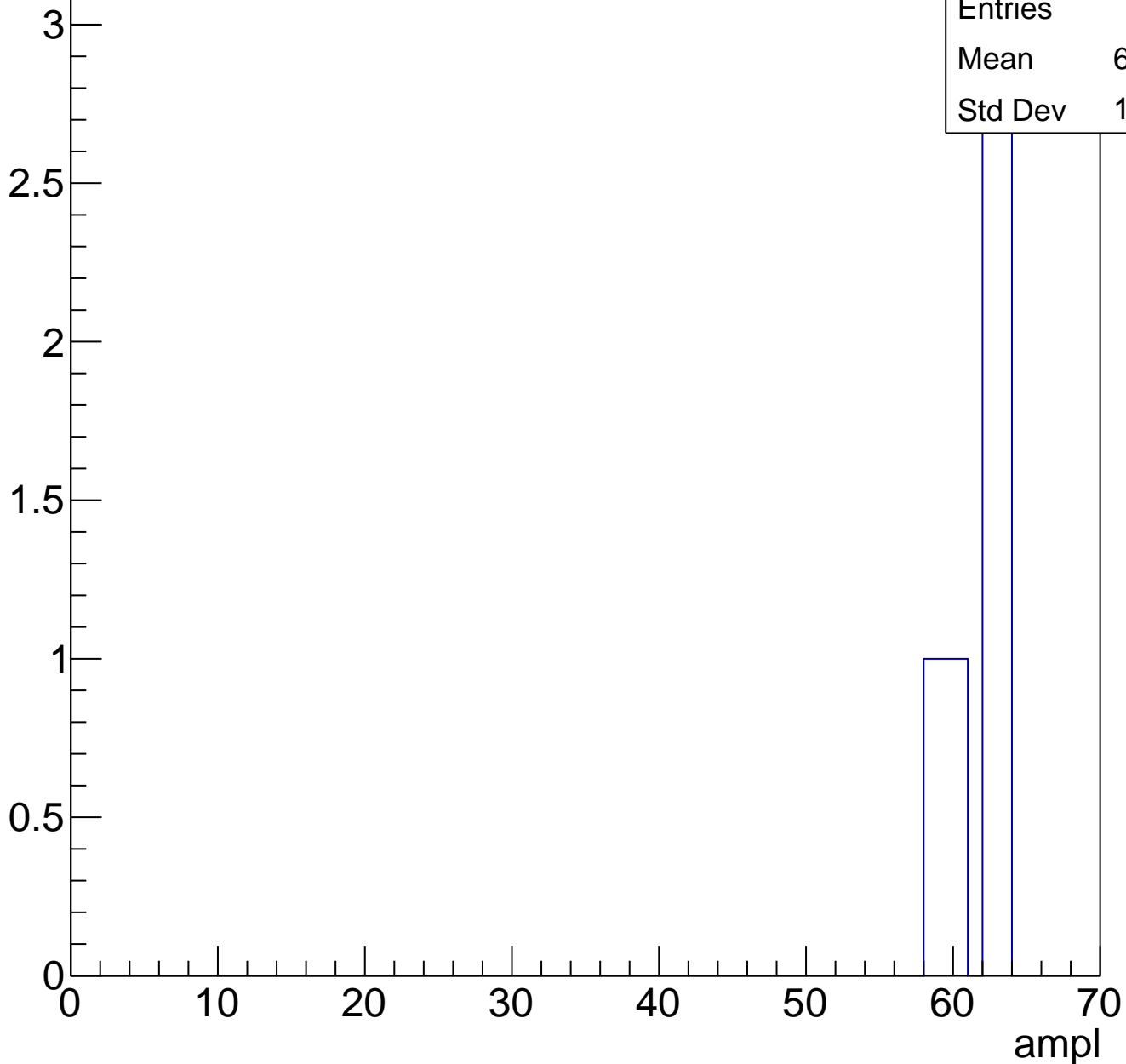
Entries	56
Mean	57.86
Std Dev	8.414



B1L103S, U8-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U8-ch125, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	25.7
Std Dev	10.17

Gaus mean : 29.8647

Gaus Width: 4.1695

Entry

10

8

6

4

2

0

0

10

20

30

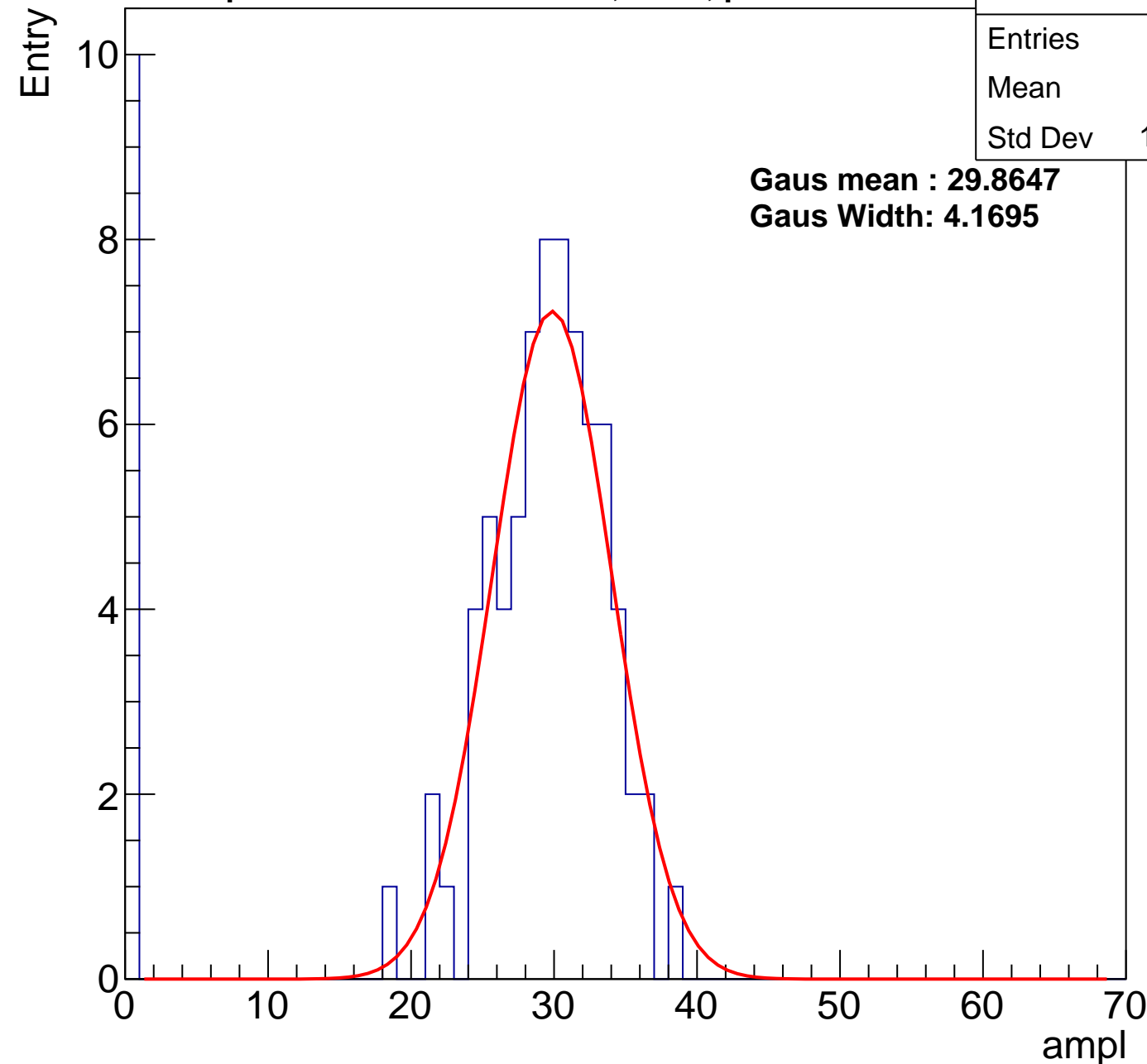
40

50

60

70

ampl



B1L103S, U8-ch125, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	32.37
Std Dev	12.23

Gaus mean : 37.3653

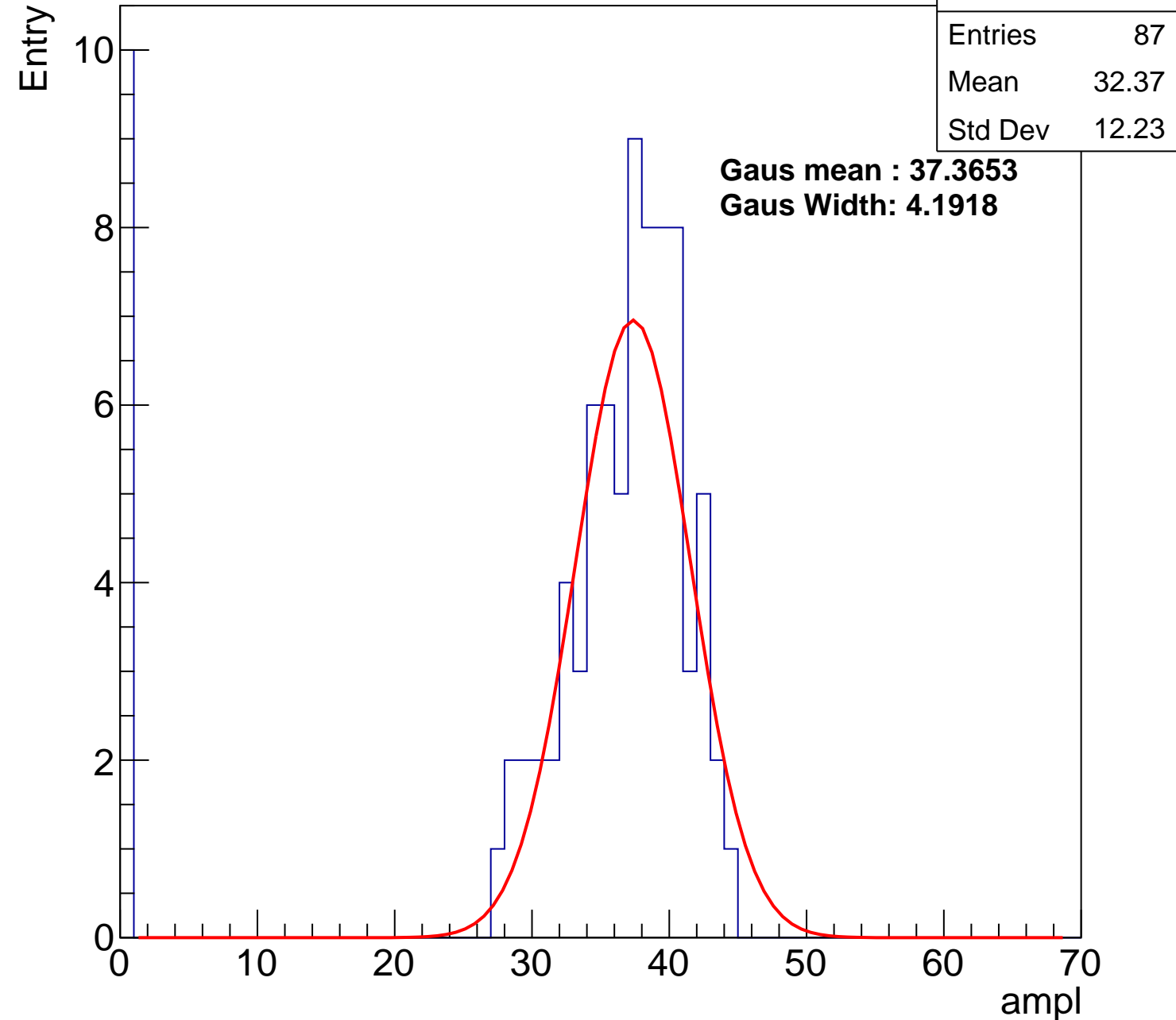
Gaus Width: 4.1918

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

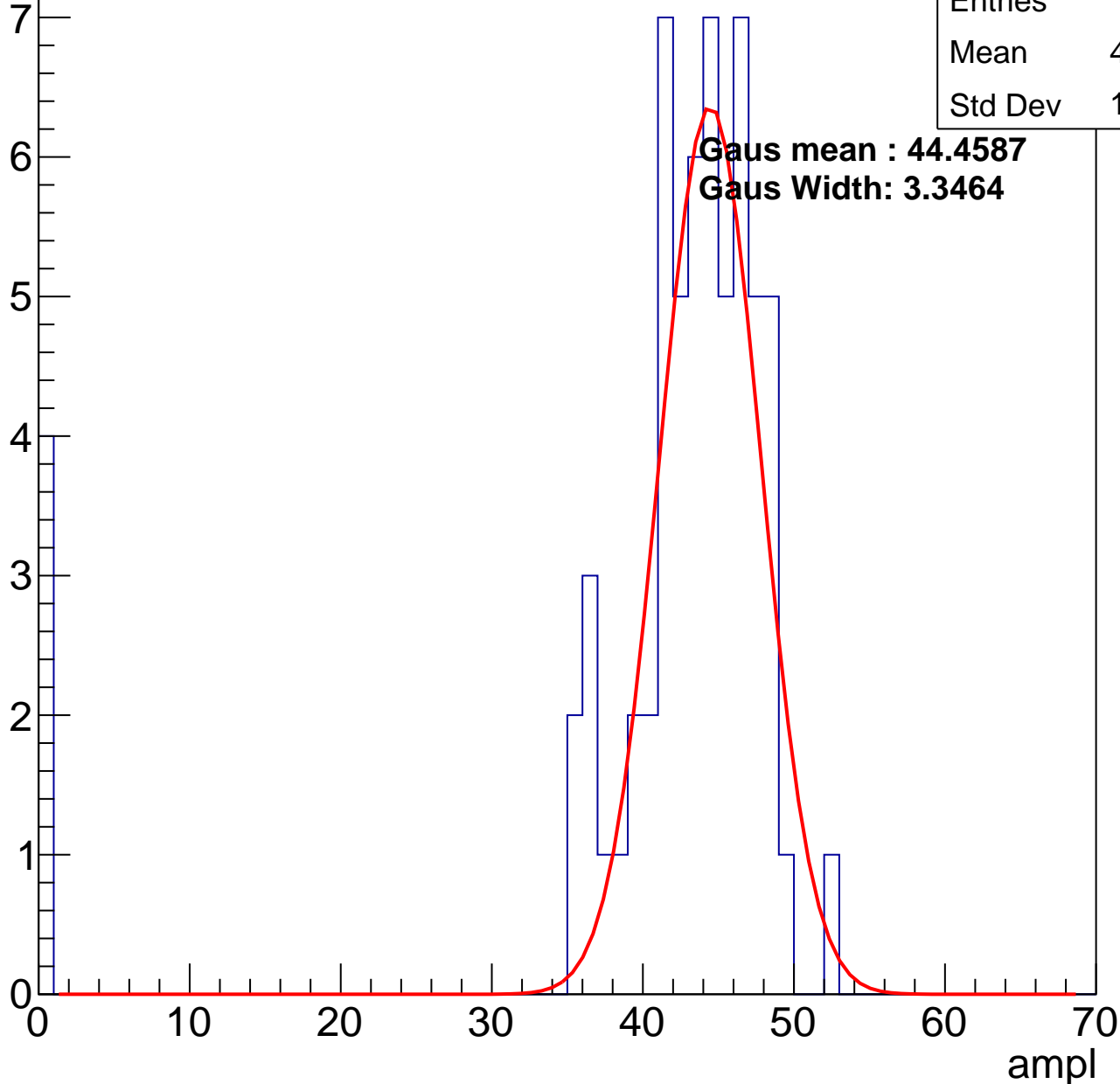


B1L103S, U8-ch125, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

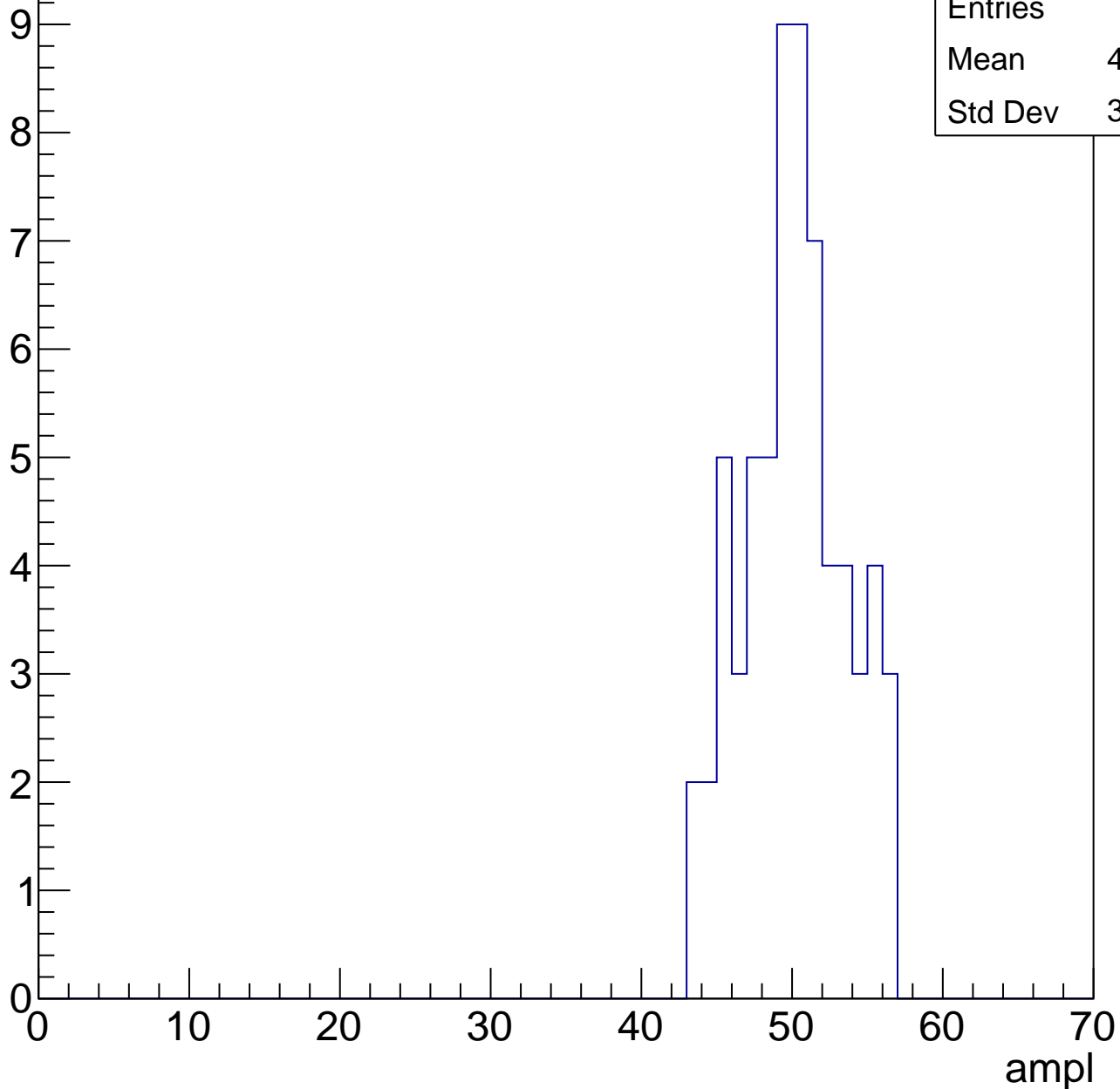
Entries	64
Mean	40.58
Std Dev	11.08



B1L103S, U8-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	65
Mean	49.69
Std Dev	3.342

B1L103S, U8-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

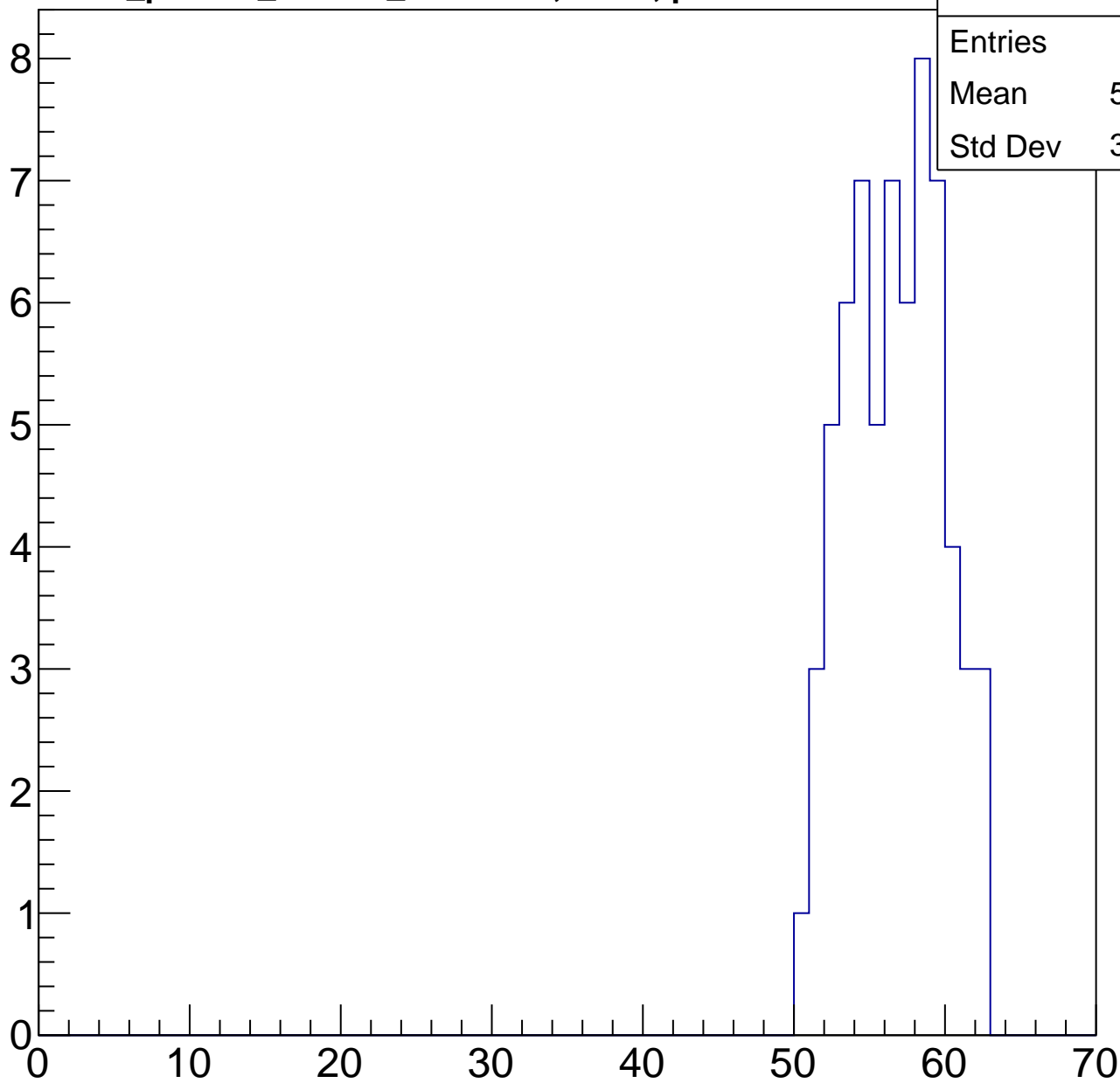
Entry

8
7
6
5
4
3
2
1
0

Entries	65
Mean	56.22
Std Dev	3.096

ampl

0 10 20 30 40 50 60 70

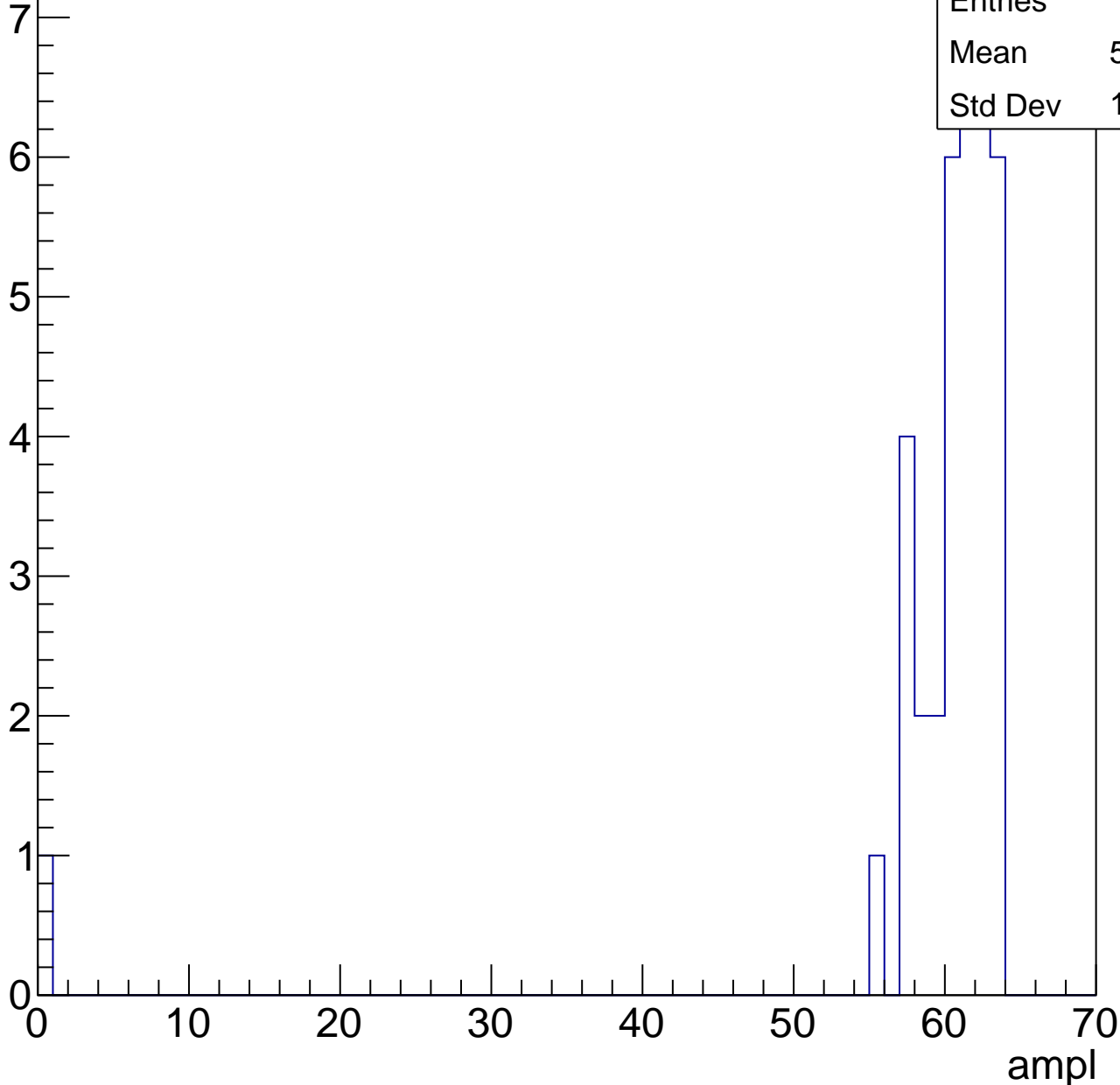


B1L103S, U8-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

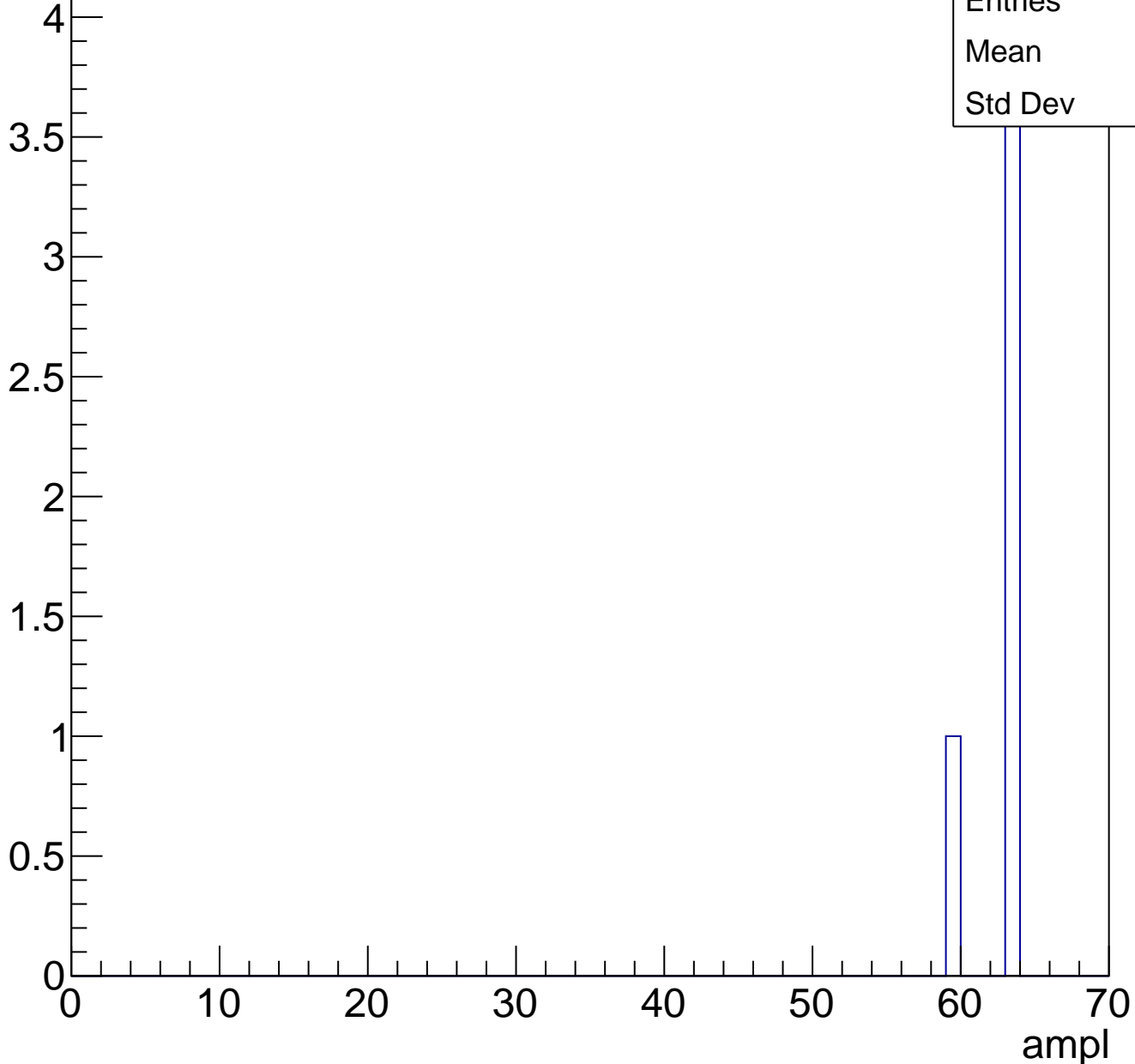
Entries	36
Mean	58.78
Std Dev	10.15



B1L103S, U8-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U8-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U8-ch126, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	23.89
Std Dev	9.563

Gaus mean : 27.6432

Gaus Width: 4.3914

Entry

12

10

8

6

4

2

0

0

10

20

30

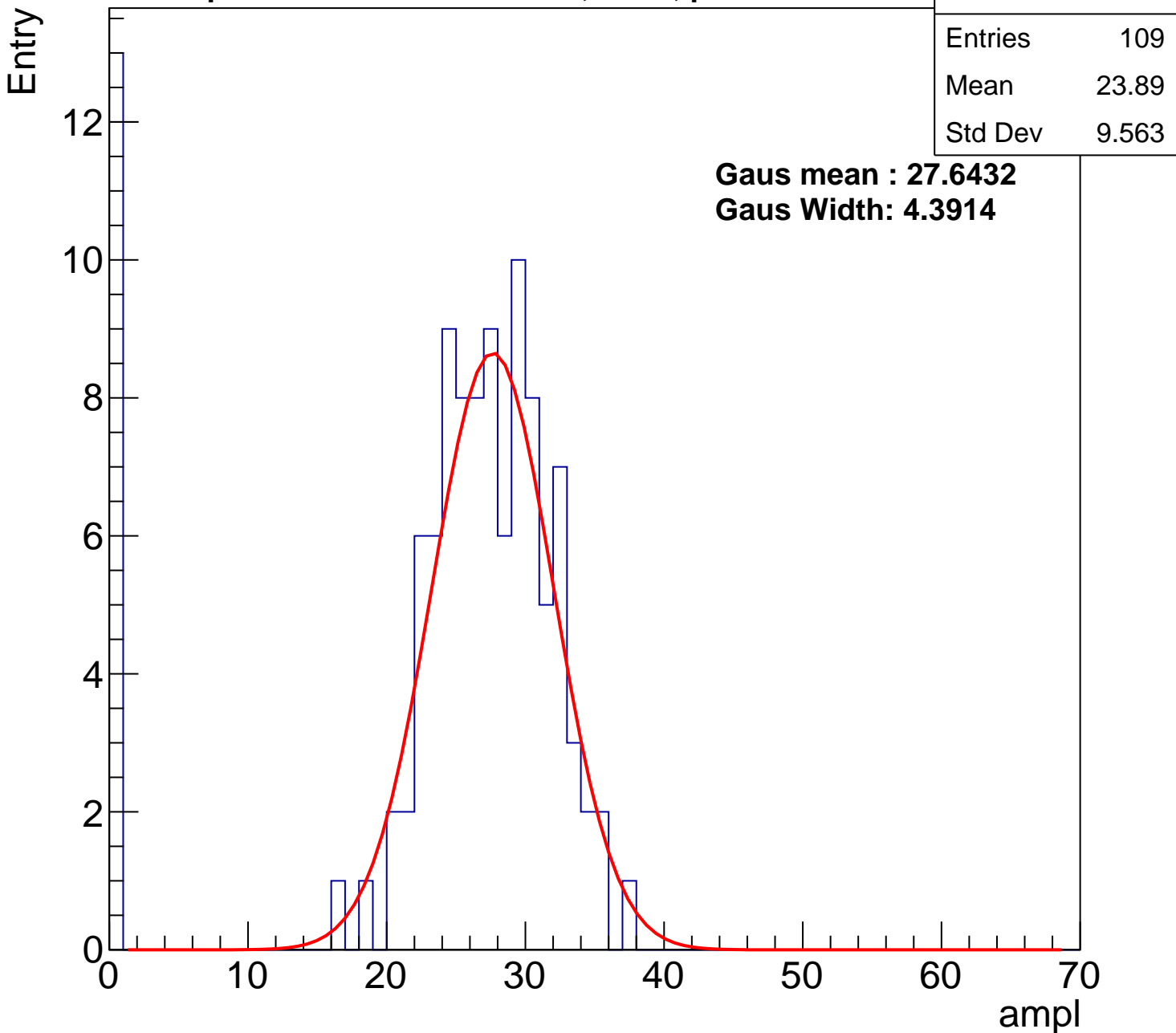
40

50

60

70

ampl



B1L103S, U8-ch126, adc1

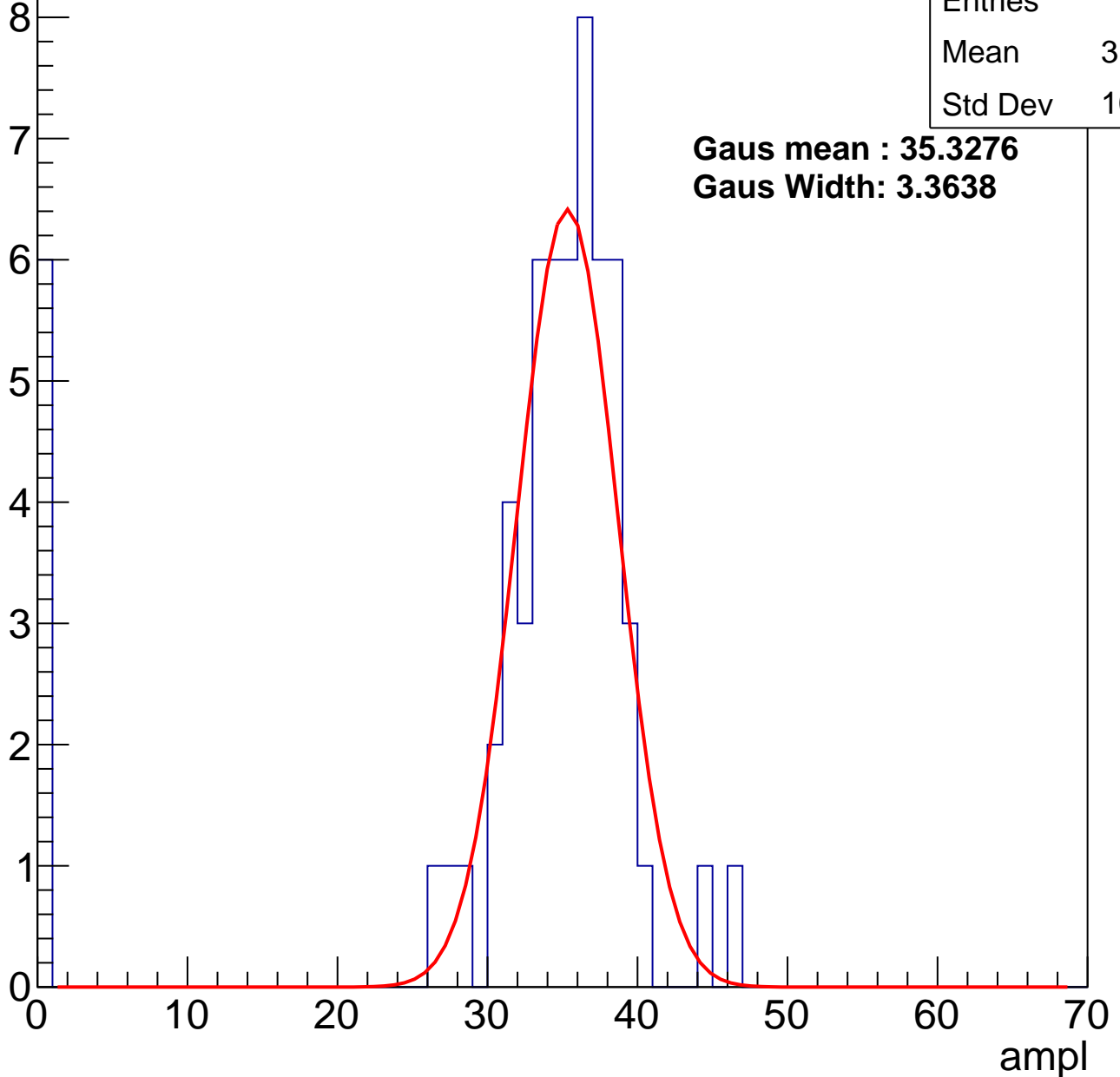
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	31.58
Std Dev	10.89

Gaus mean : 35.3276

Gaus Width: 3.3638



B1L103S, U8-ch126, adc2

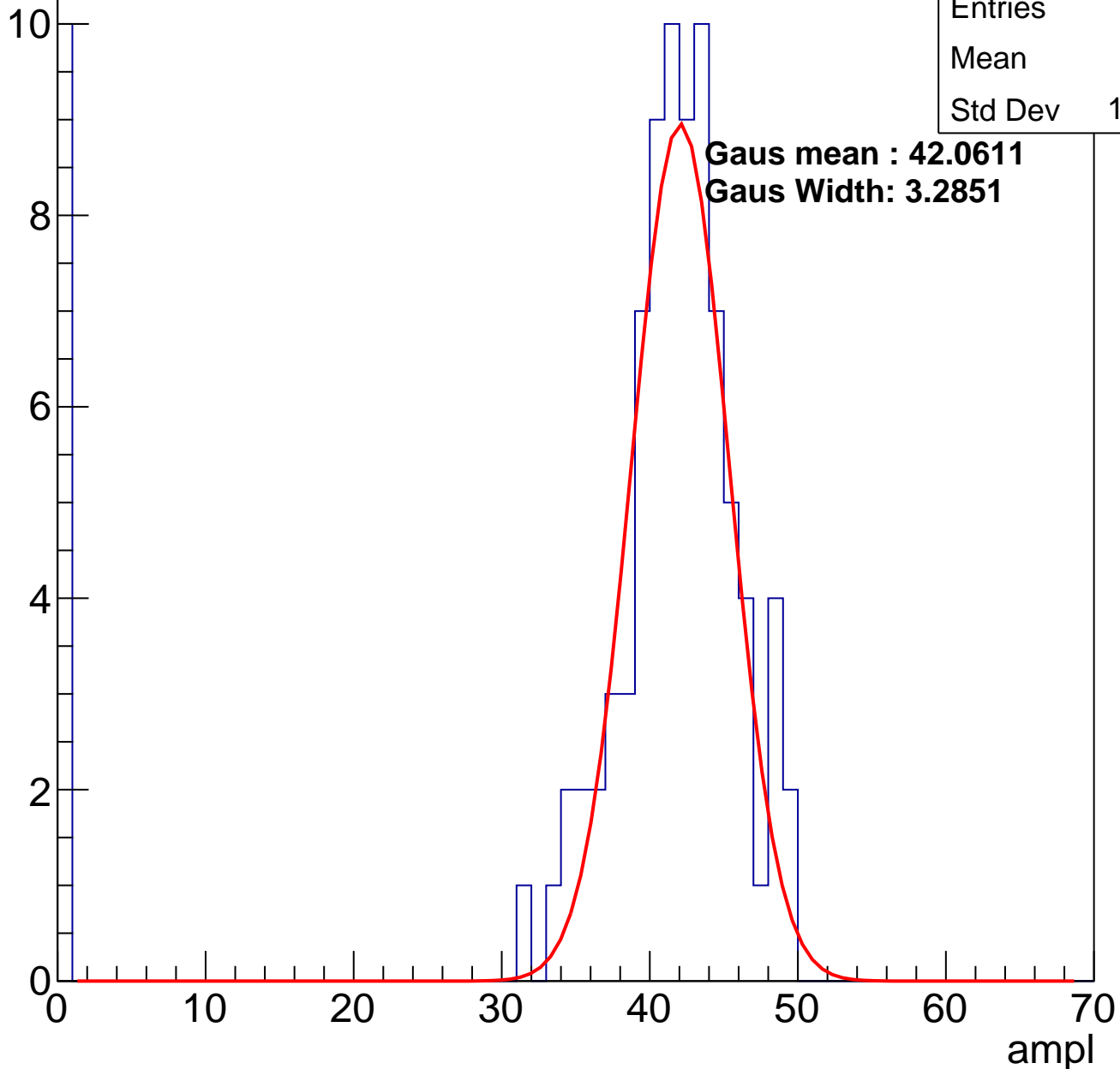
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	37
Std Dev	13.39

Gaus mean : 42.0611

Gaus Width: 3.2851

Entry

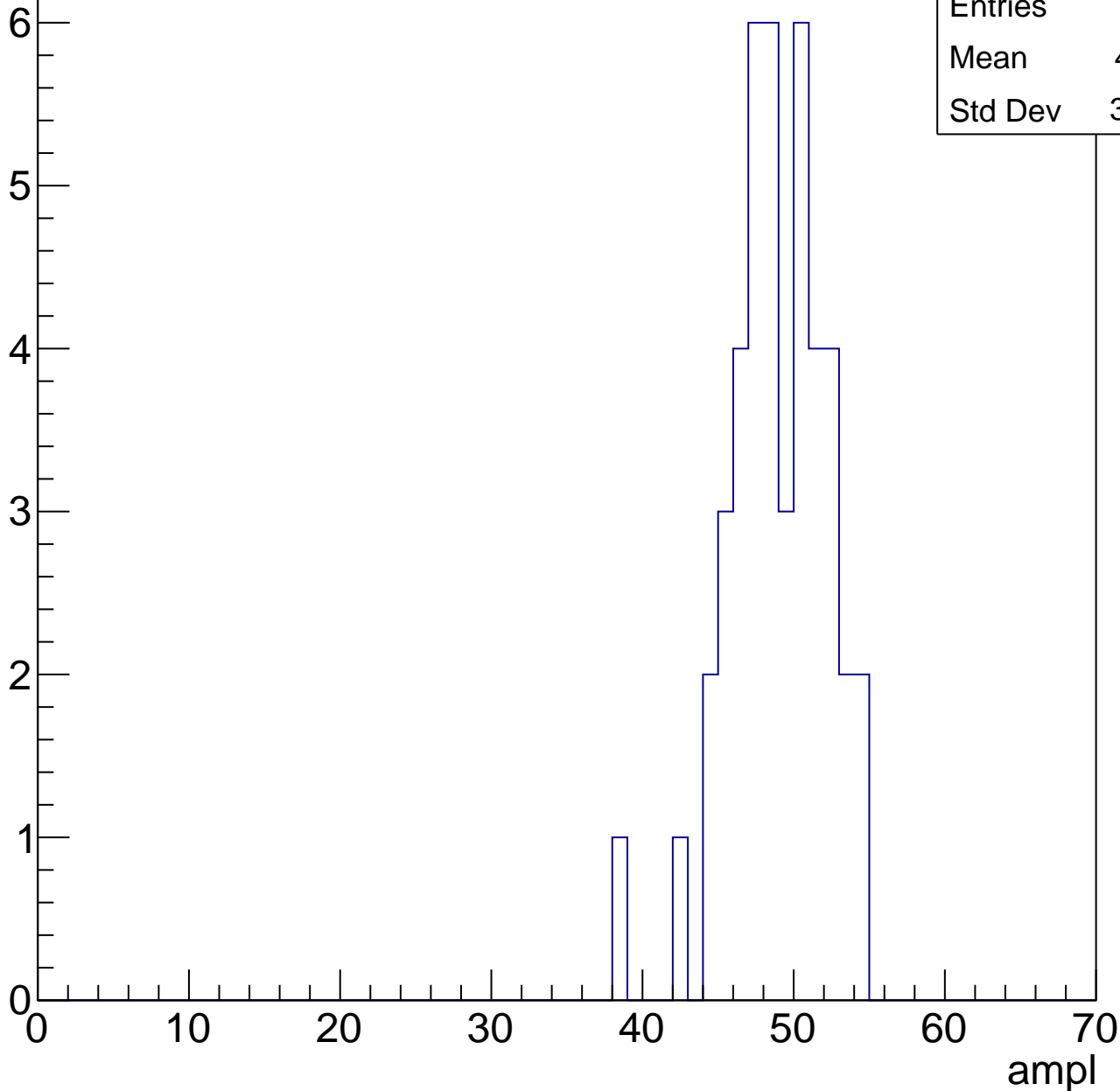


B1L103S, U8-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	48.41
Std Dev	3.229

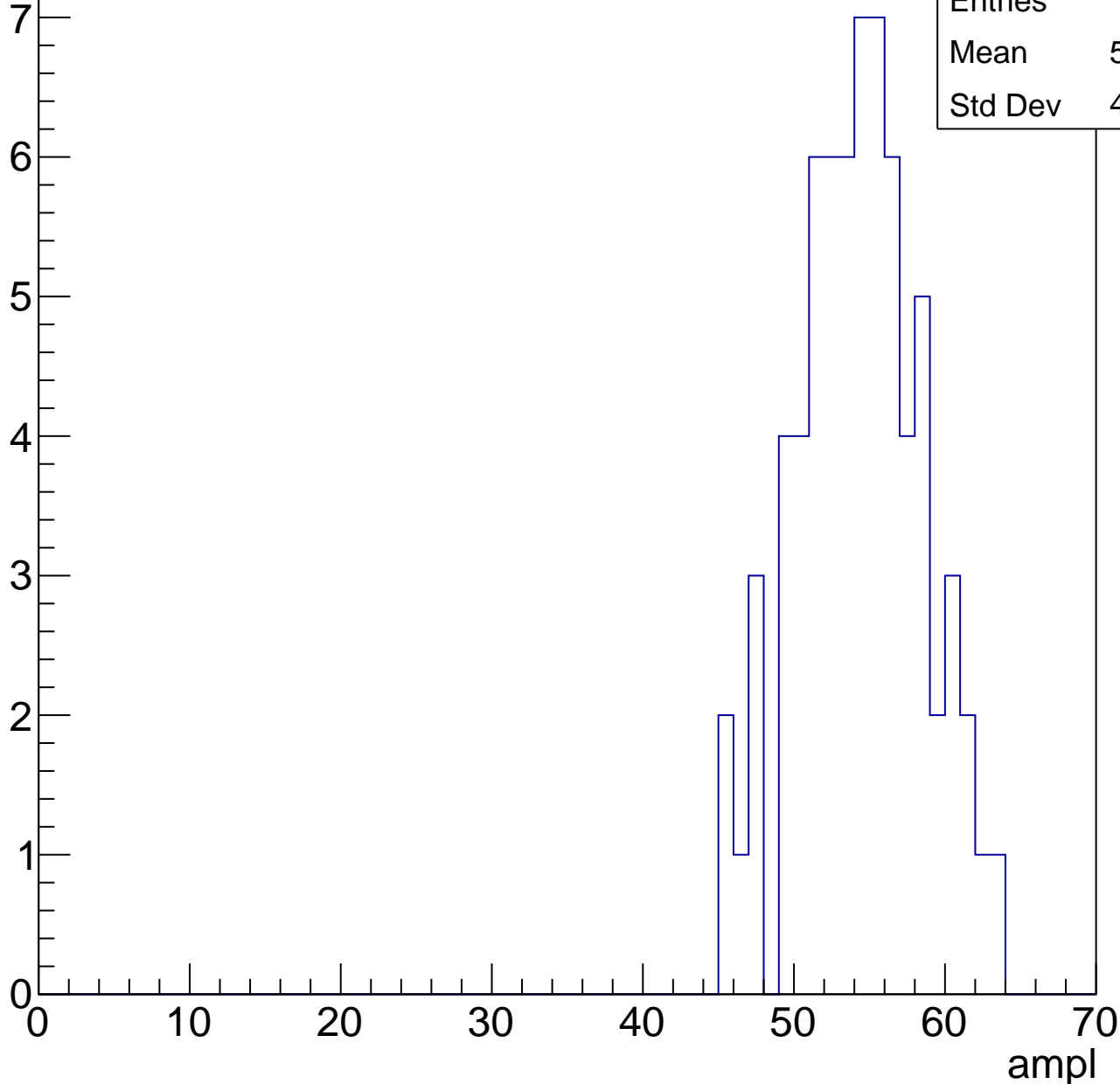


B1L103S, U8-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	53.87
Std Dev	4.085

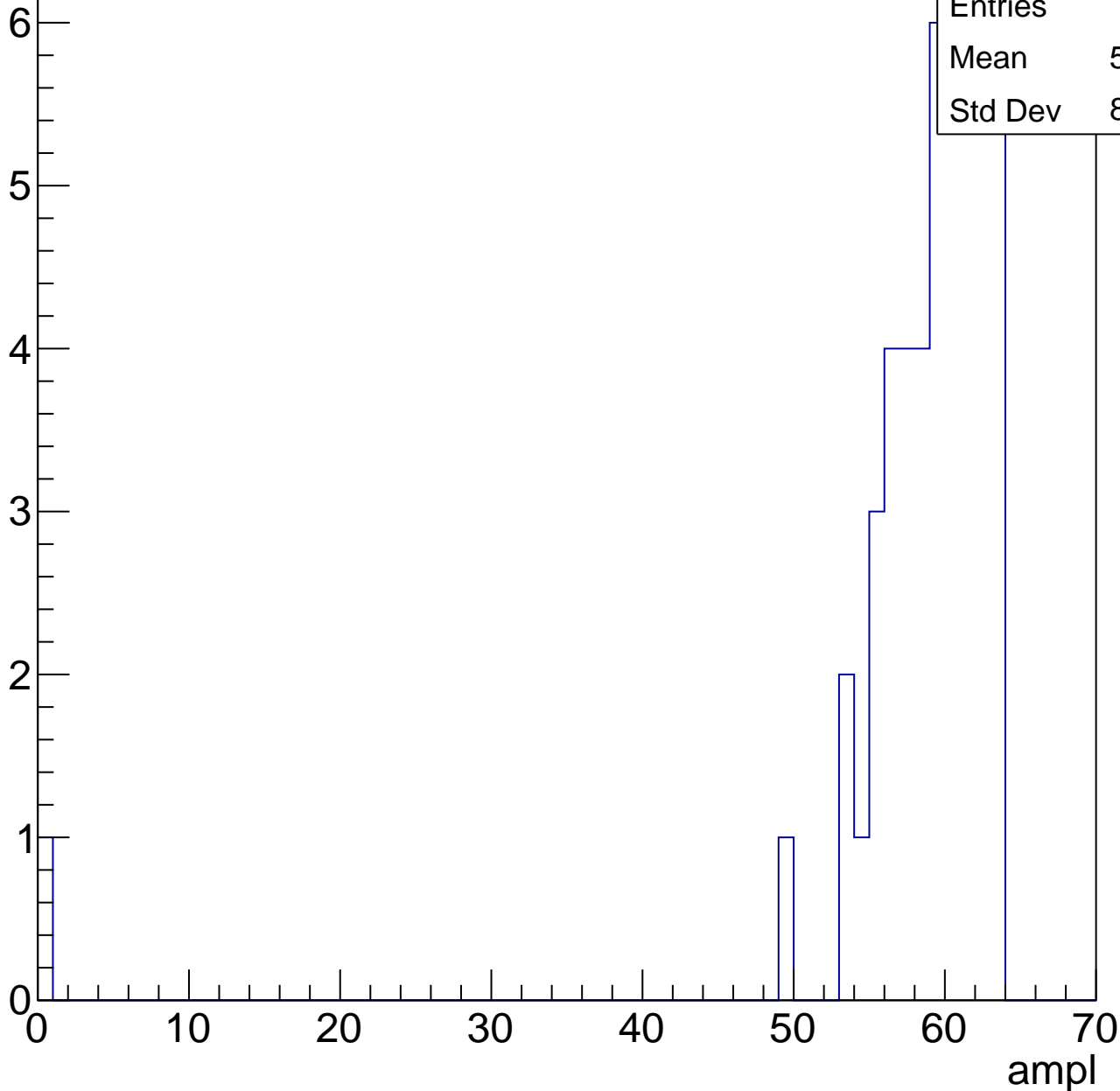


B1L103S, U8-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

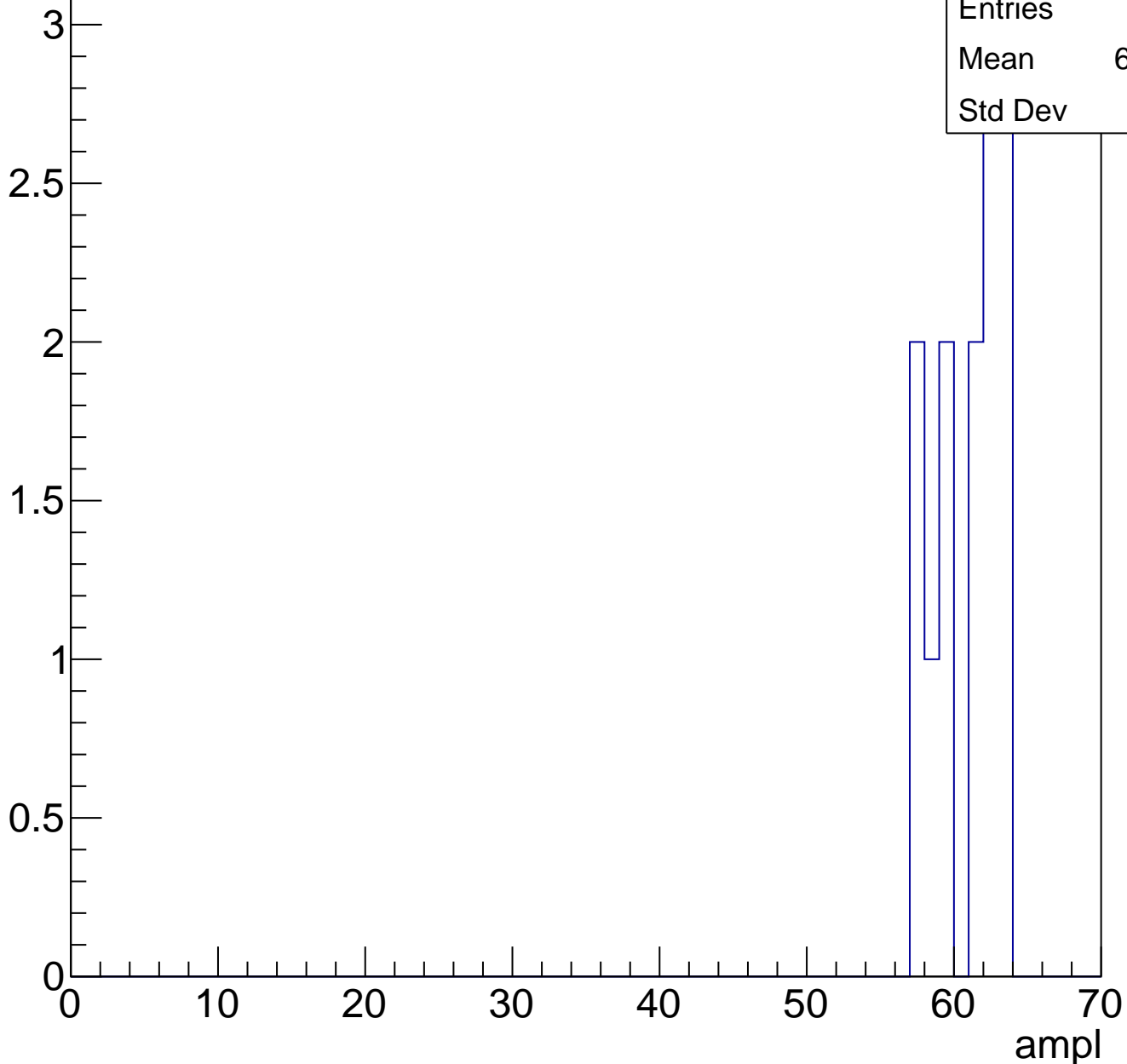
Entries	50
Mean	57.76
Std Dev	8.815



B1L103S, U8-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	13
Mean	60.54
Std Dev	2.17

B1L103S, U8-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U8-ch127, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	23.7
Std Dev	10.5

Gaus mean : 28.3918

Gaus Width: 4.0336

Entry

10

8

6

4

2

0

0

10

20

30

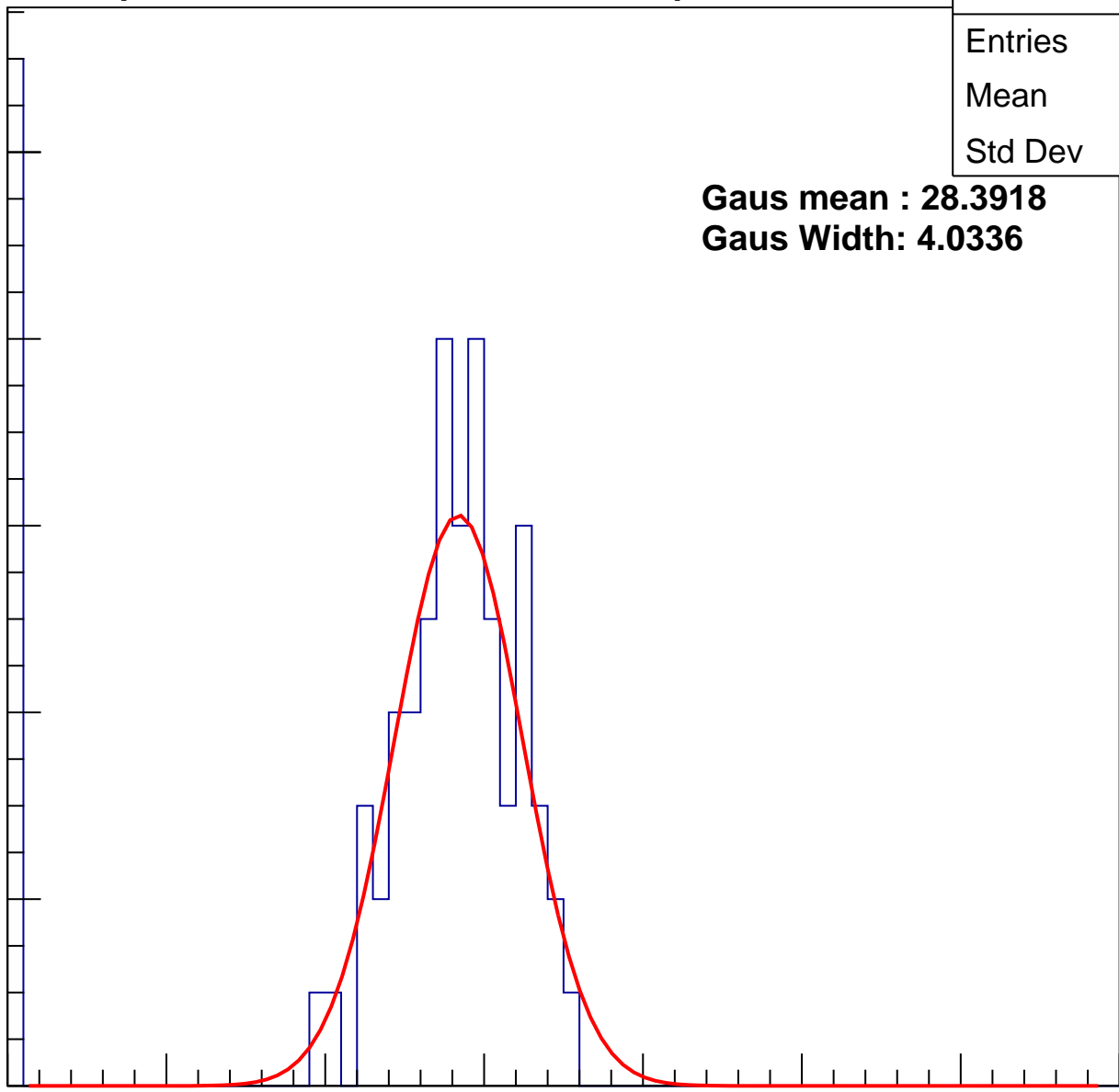
40

50

60

70

ampl



B1L103S, U8-ch127, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	29.84
Std Dev	12.88

Gaus mean : 35.0957

Gaus Width: 4.3170

Entry

10

8

6

4

2

0

0

10

20

30

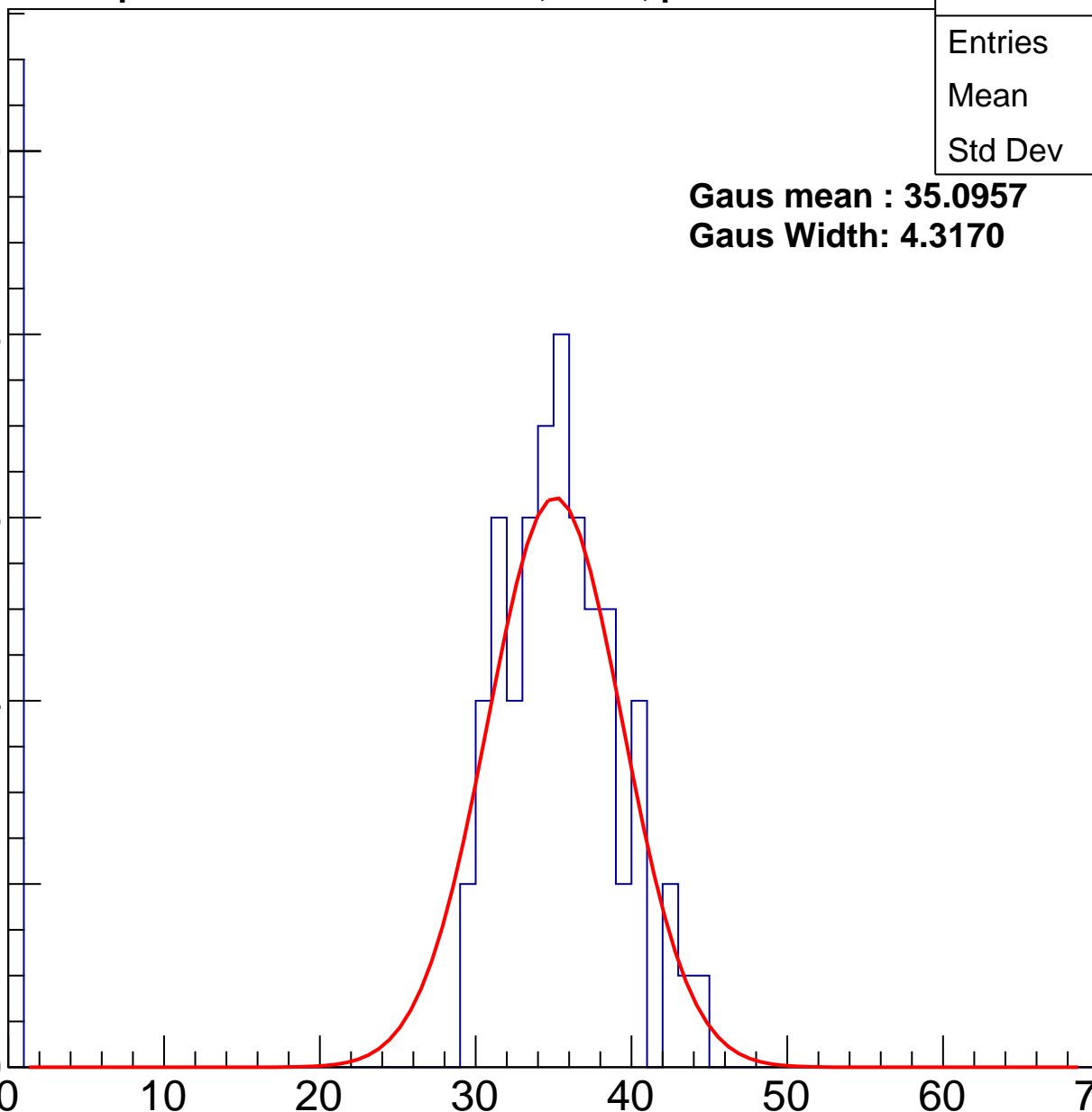
40

50

60

70

ampl



B1L103S, U8-ch127, adc2

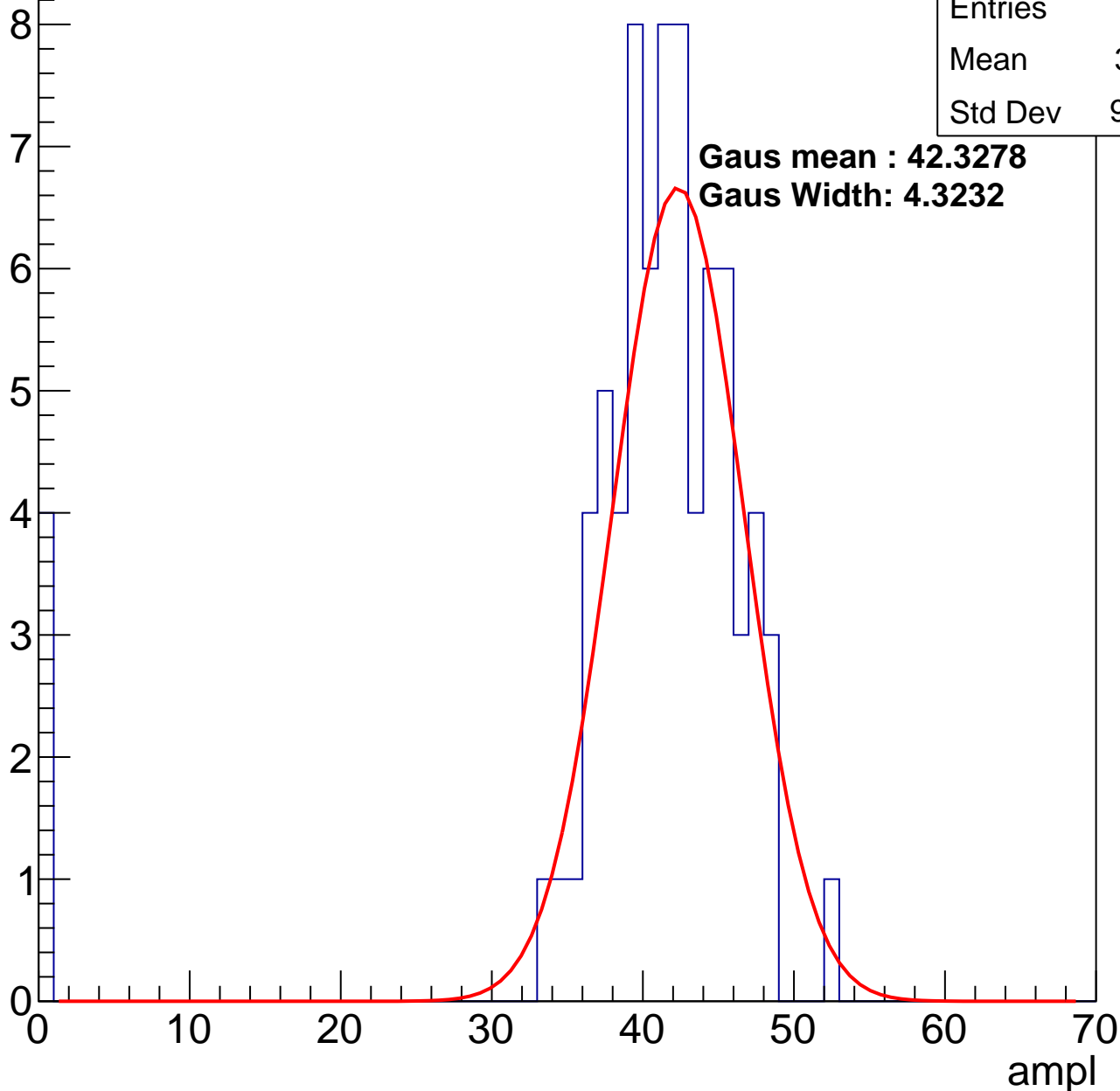
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	39.31
Std Dev	9.914

Gaus mean : 42.3278

Gaus Width: 4.3232

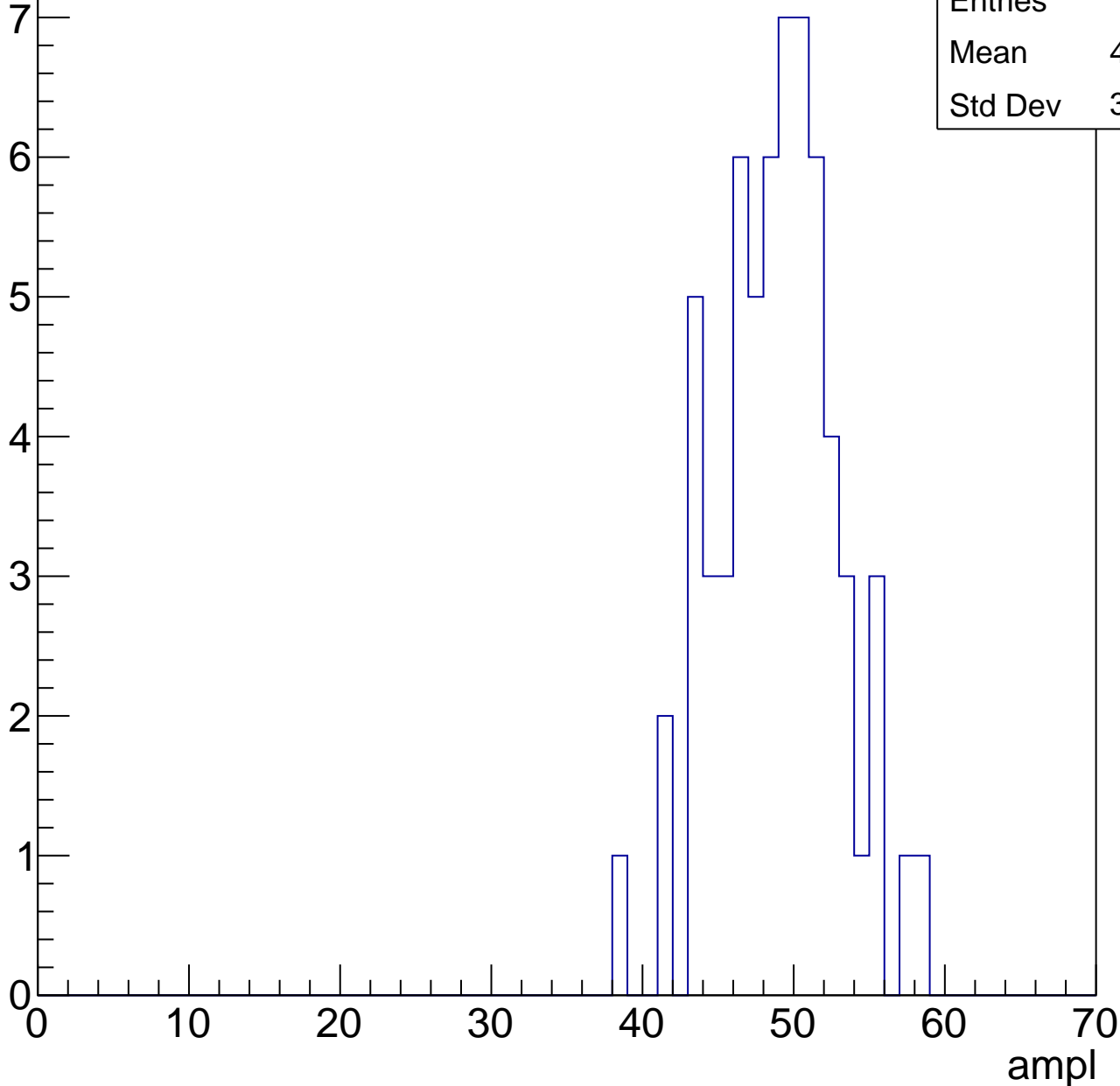


B1L103S, U8-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

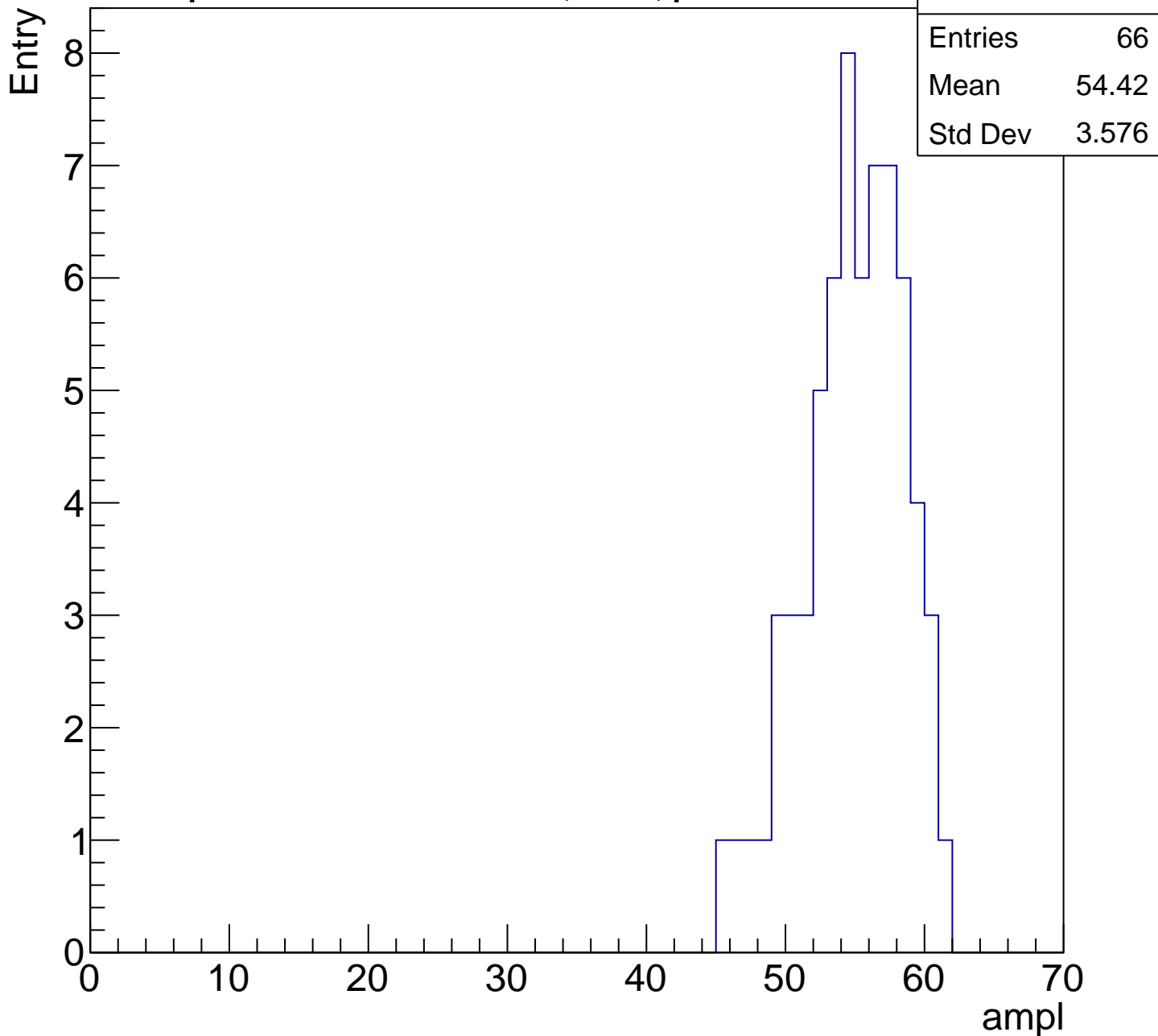
Entry

Entries	64
Mean	48.45
Std Dev	3.964



B1L103S, U8-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

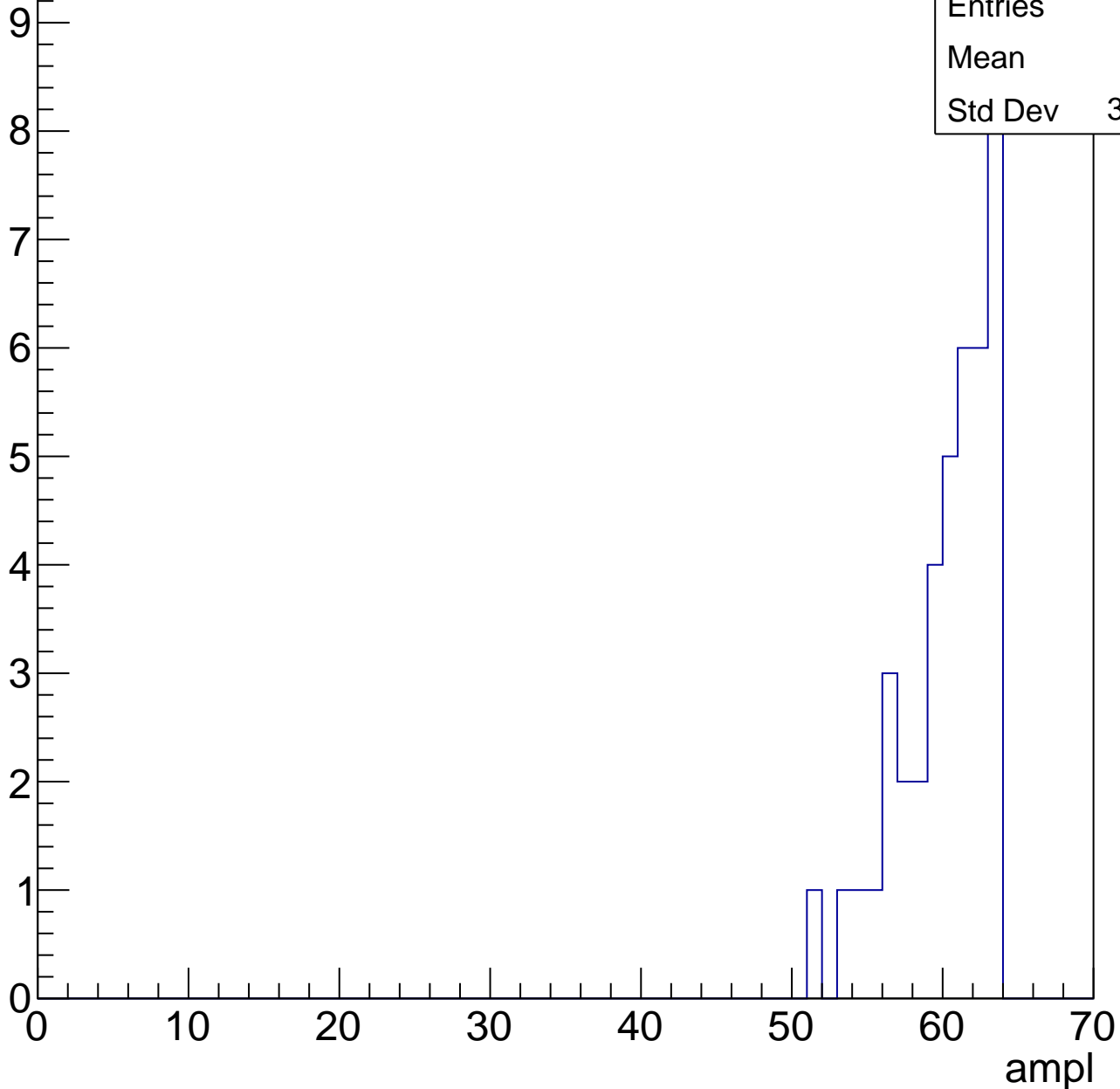


B1L103S, U8-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

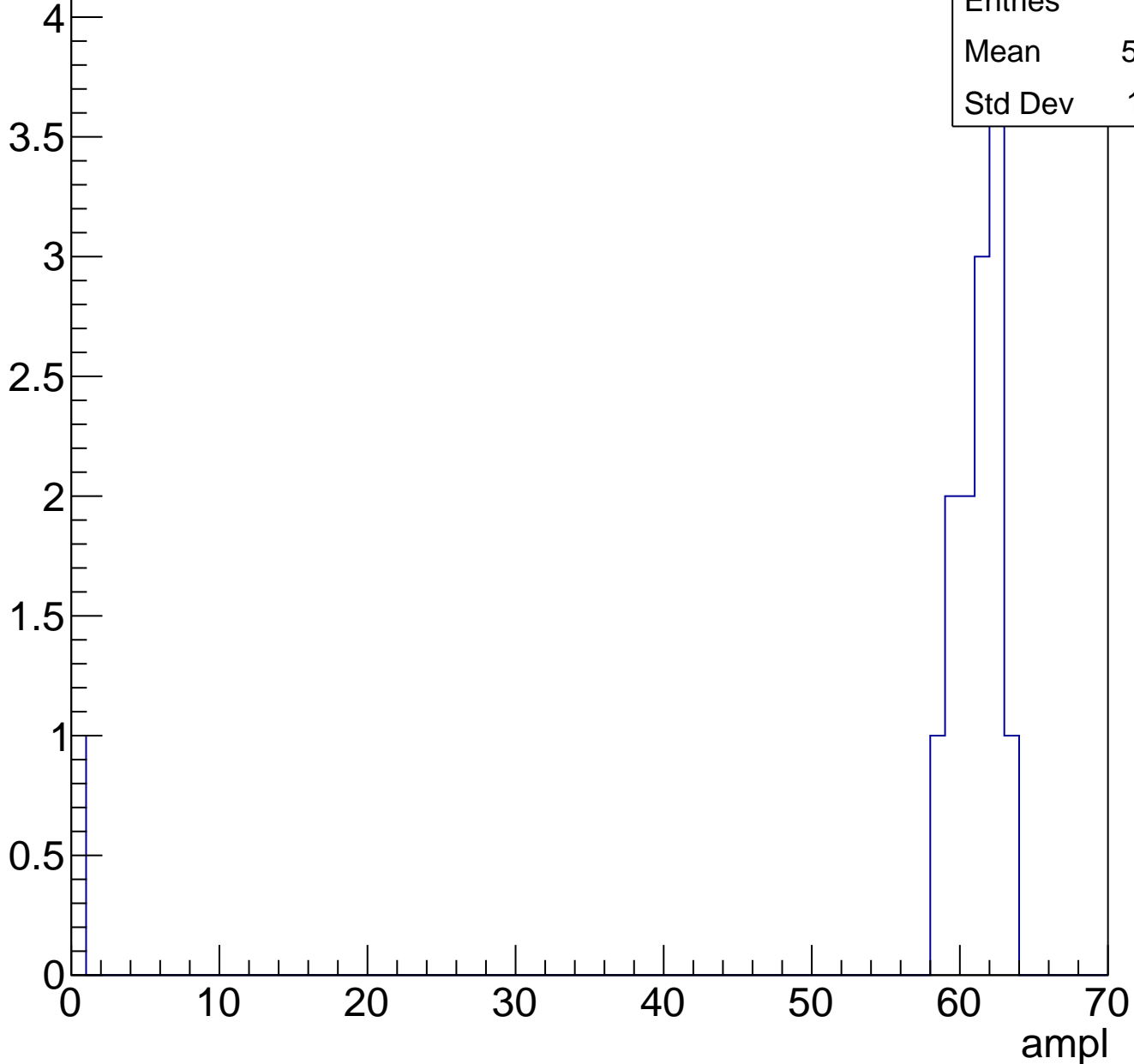
Entries	41
Mean	59.8
Std Dev	3.038



B1L103S, U8-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	14
Mean	56.43
Std Dev	15.71

B1L103S, U8-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U8-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	0
Std Dev	0

Entry

