



# B0L102S, U26-ch0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	43.93
Std Dev	11.96

Turn on : 26.1198

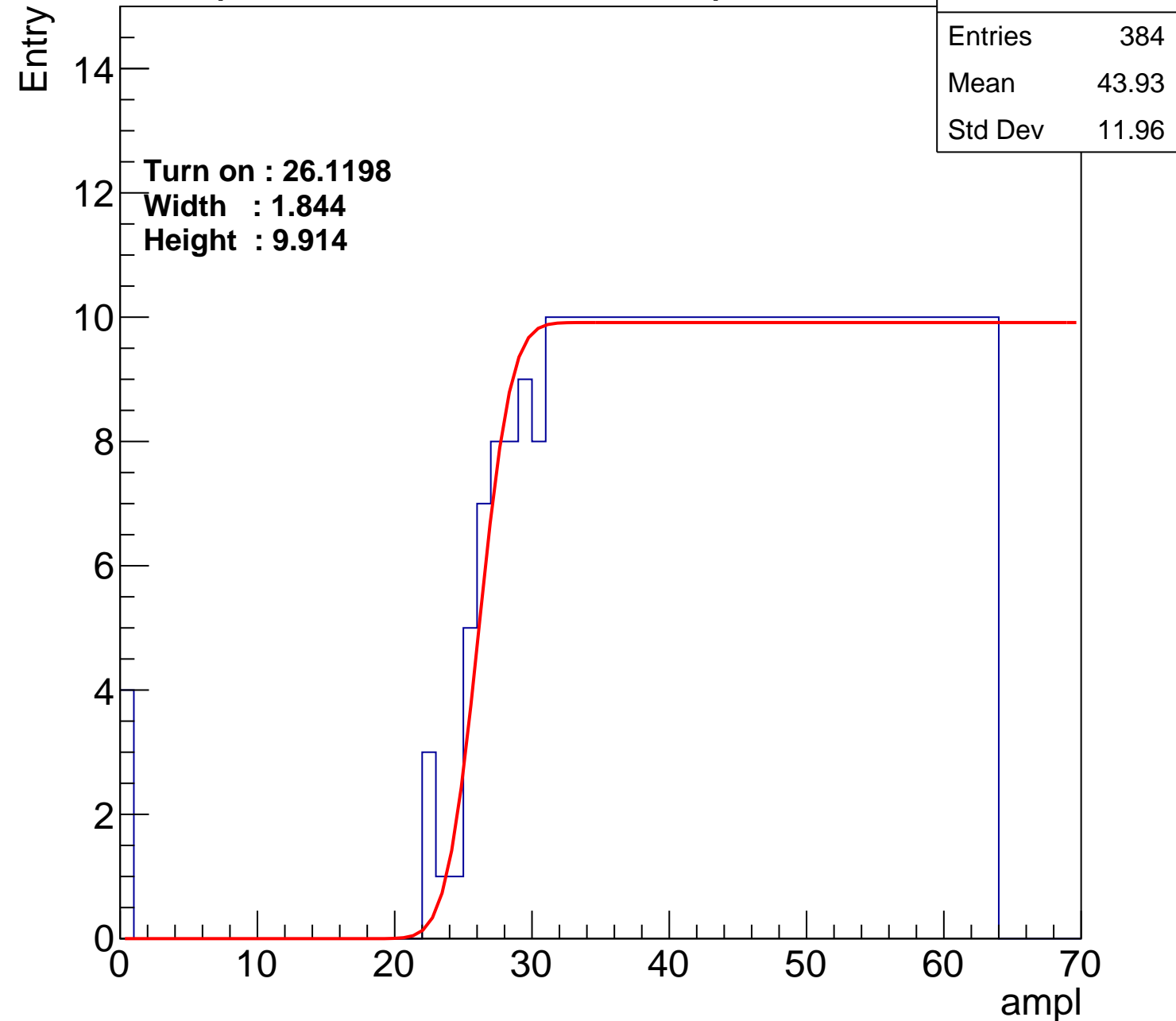
Width : 1.844

Height : 9.914

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 28.0014

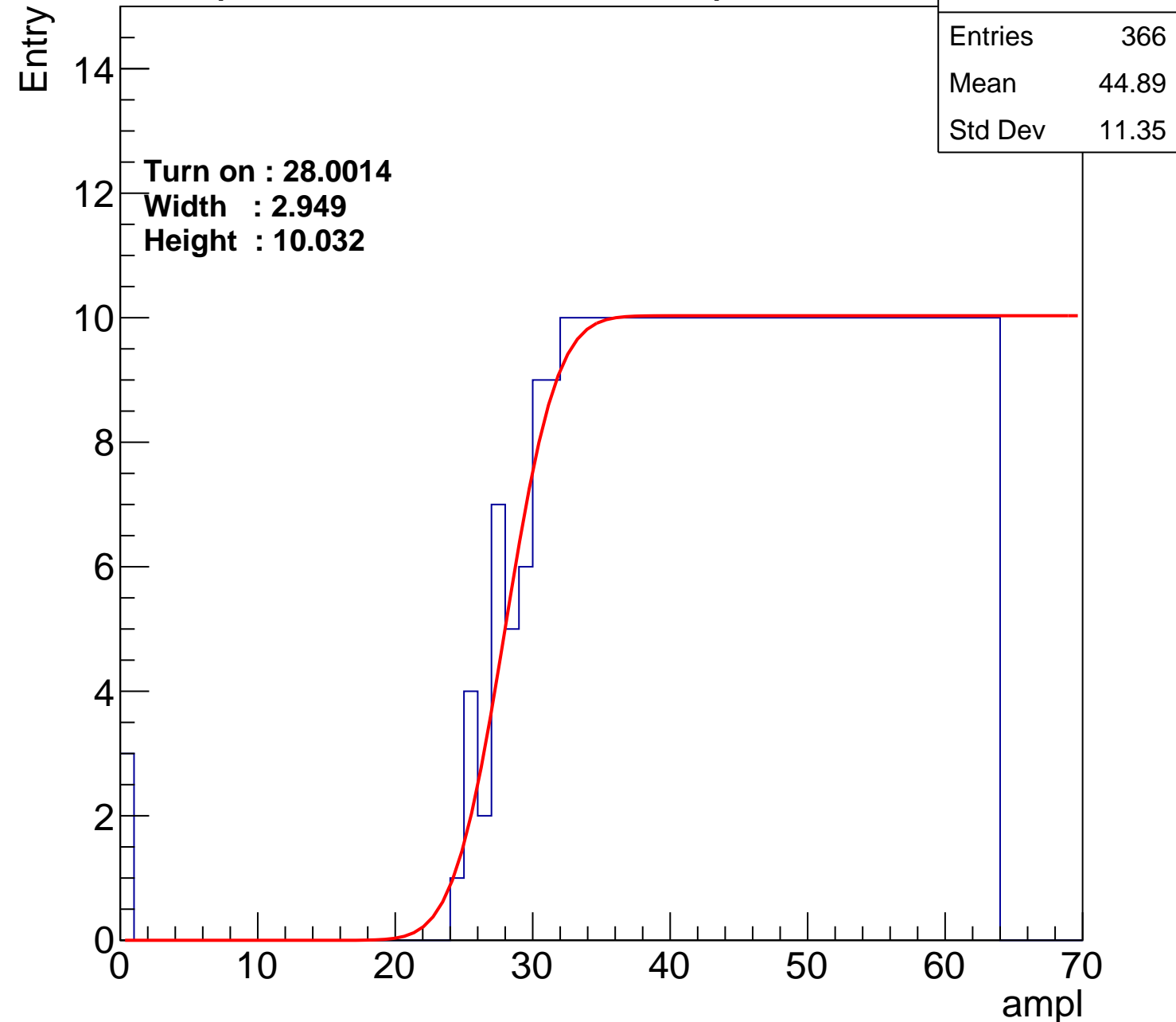
Width : 2.949

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	45.13
Std Dev	10.9

Turn on : 28.4341

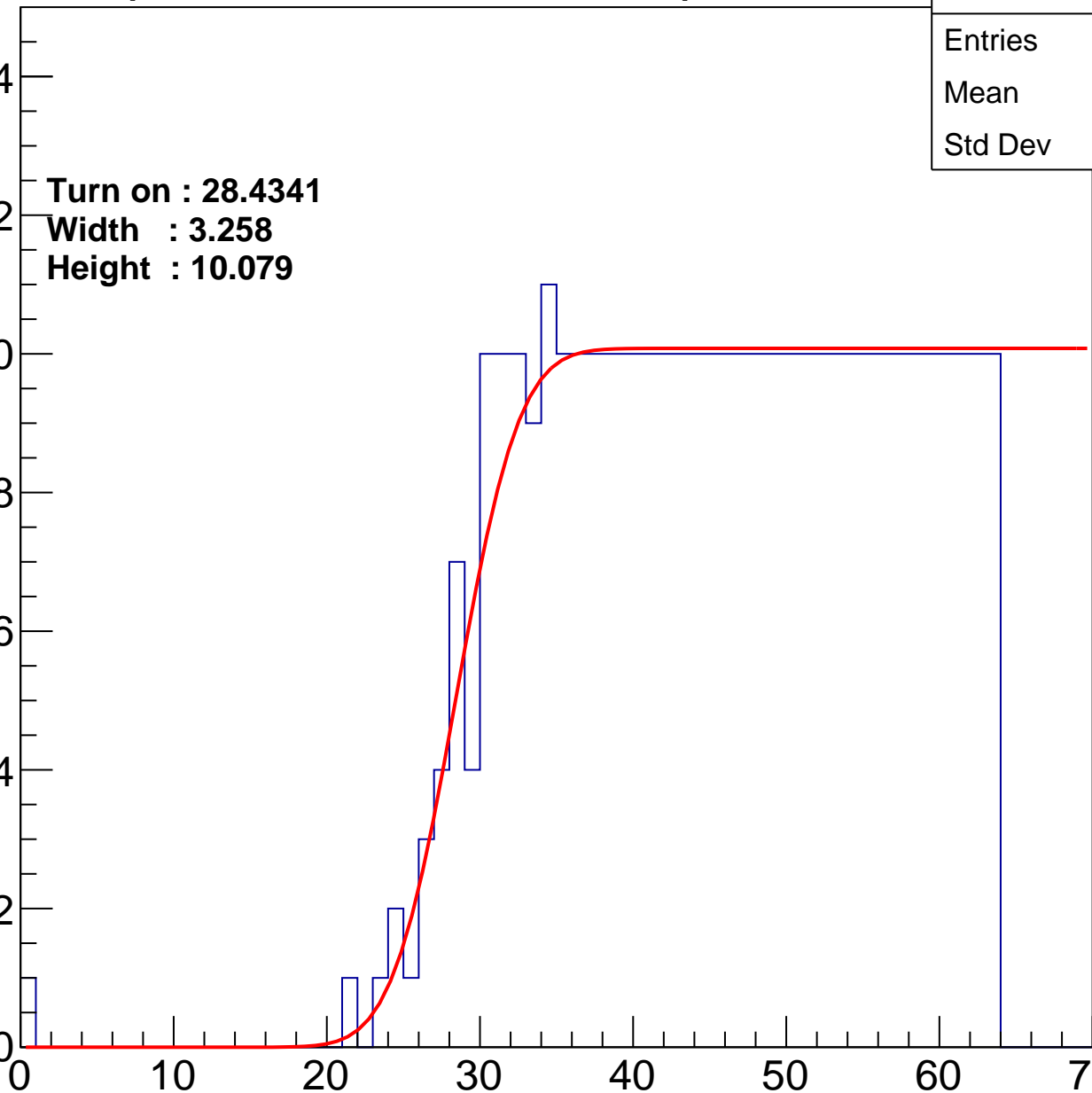
Width : 3.258

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch3

calib\_packv5\_042523\_0143.root, FC#12, port B1

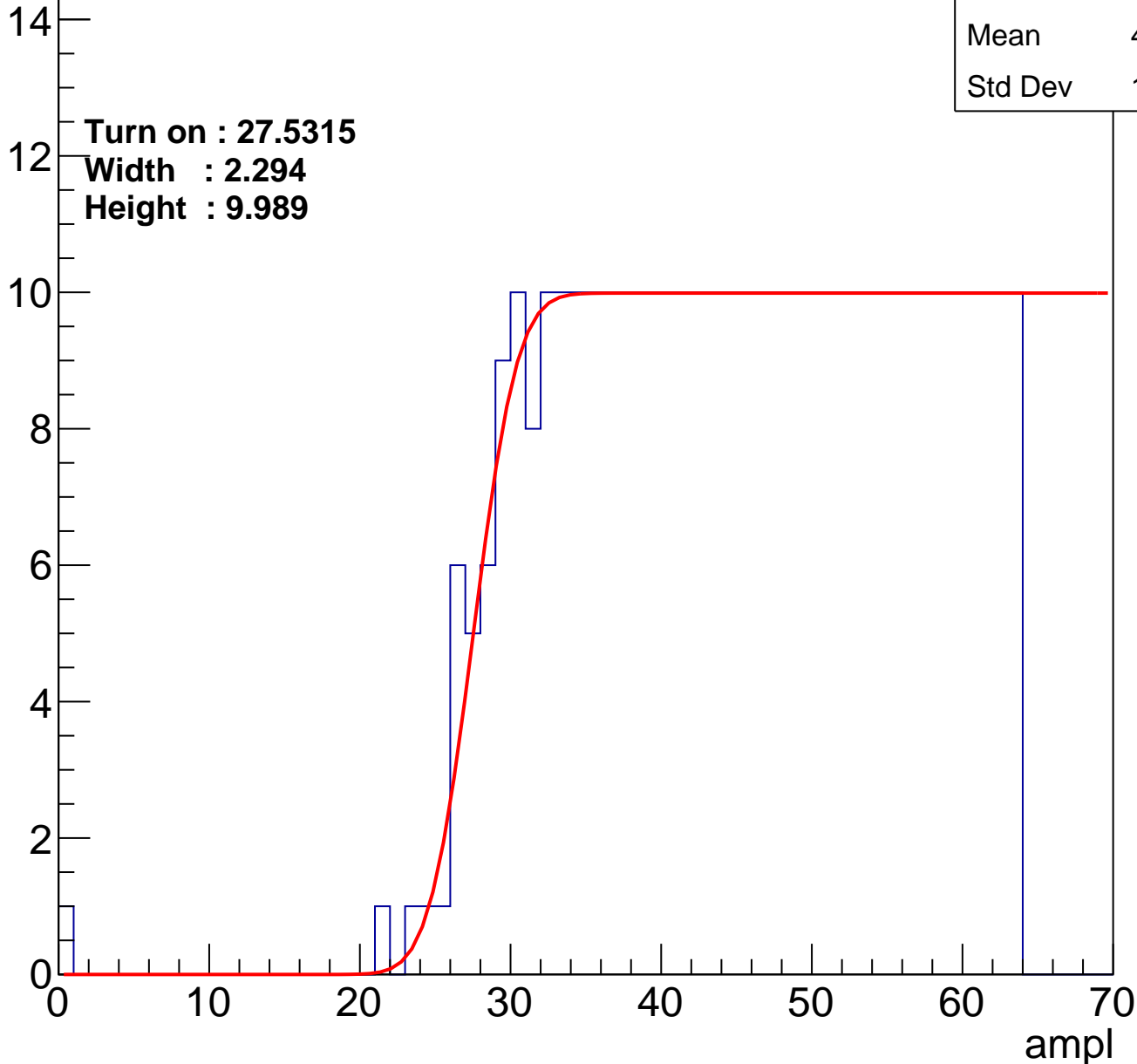
Entry

Entries	369
Mean	44.88
Std Dev	11.02

Turn on : 27.5315

Width : 2.294

Height : 9.989



# B0L102S, U26-ch4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.39
Std Dev	11.42

**Turn on : 26.3827**

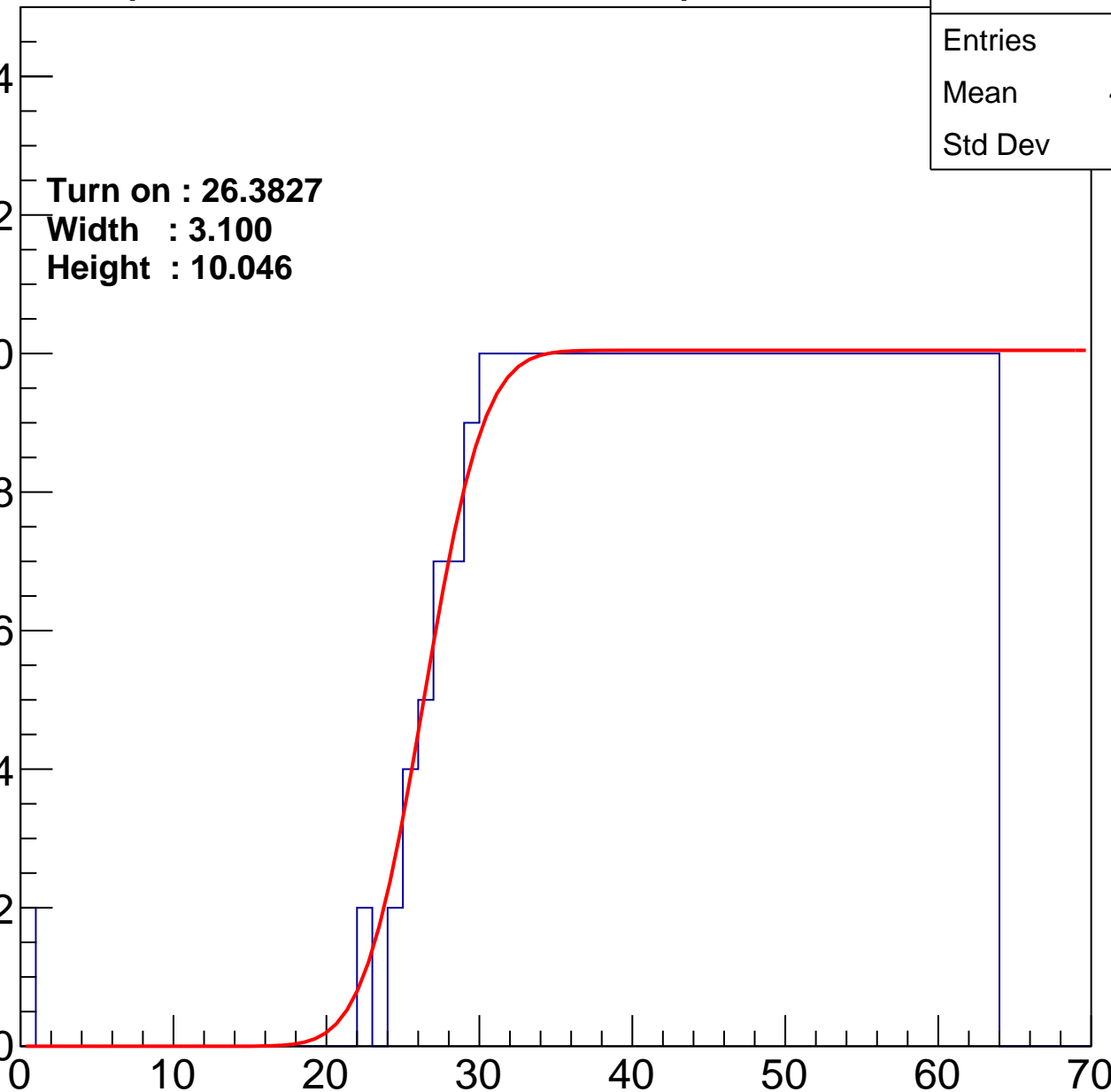
**Width : 3.100**

**Height : 10.046**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.96
Std Dev	11.15

Turn on : 27.7693

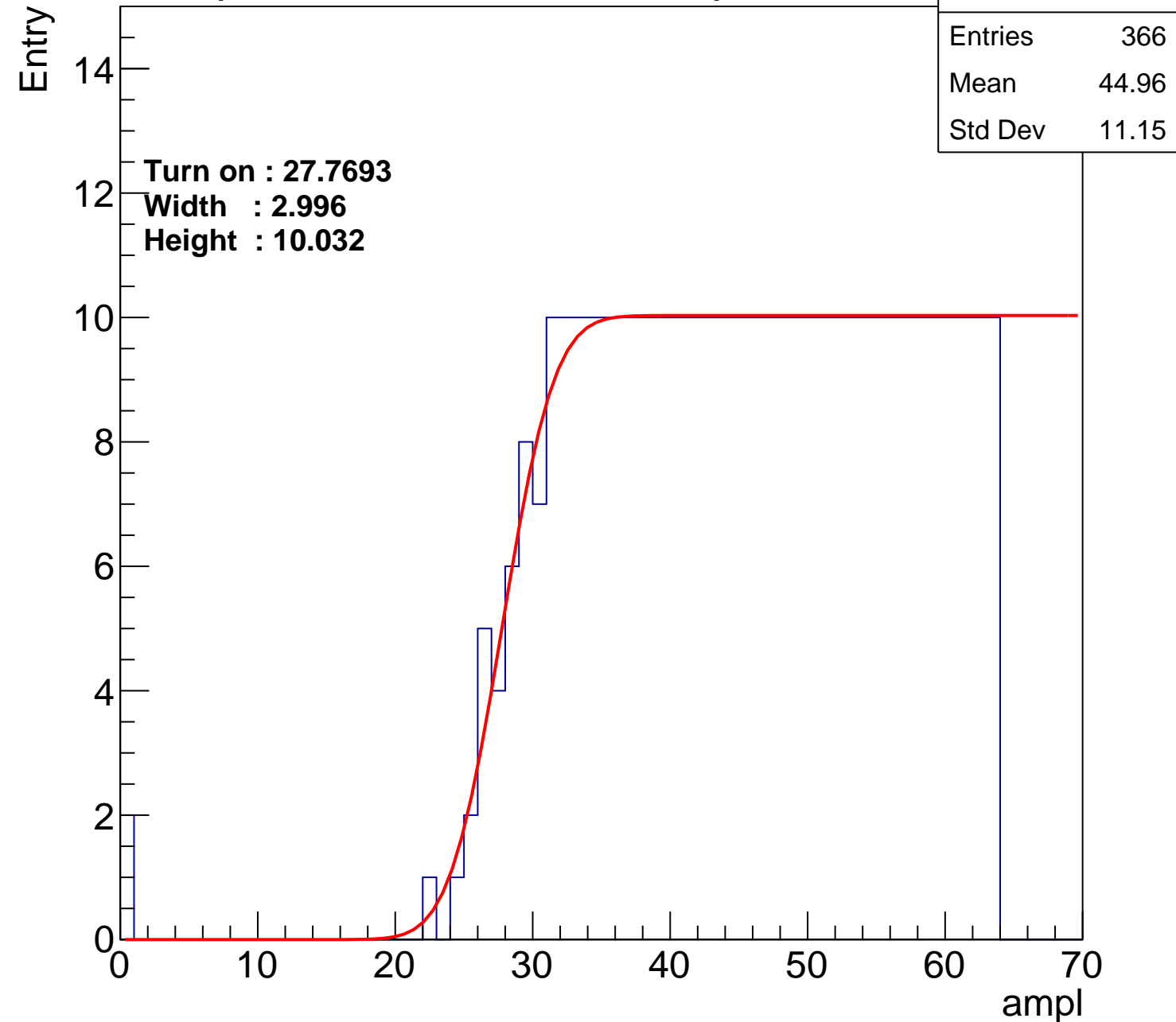
Width : 2.996

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch6

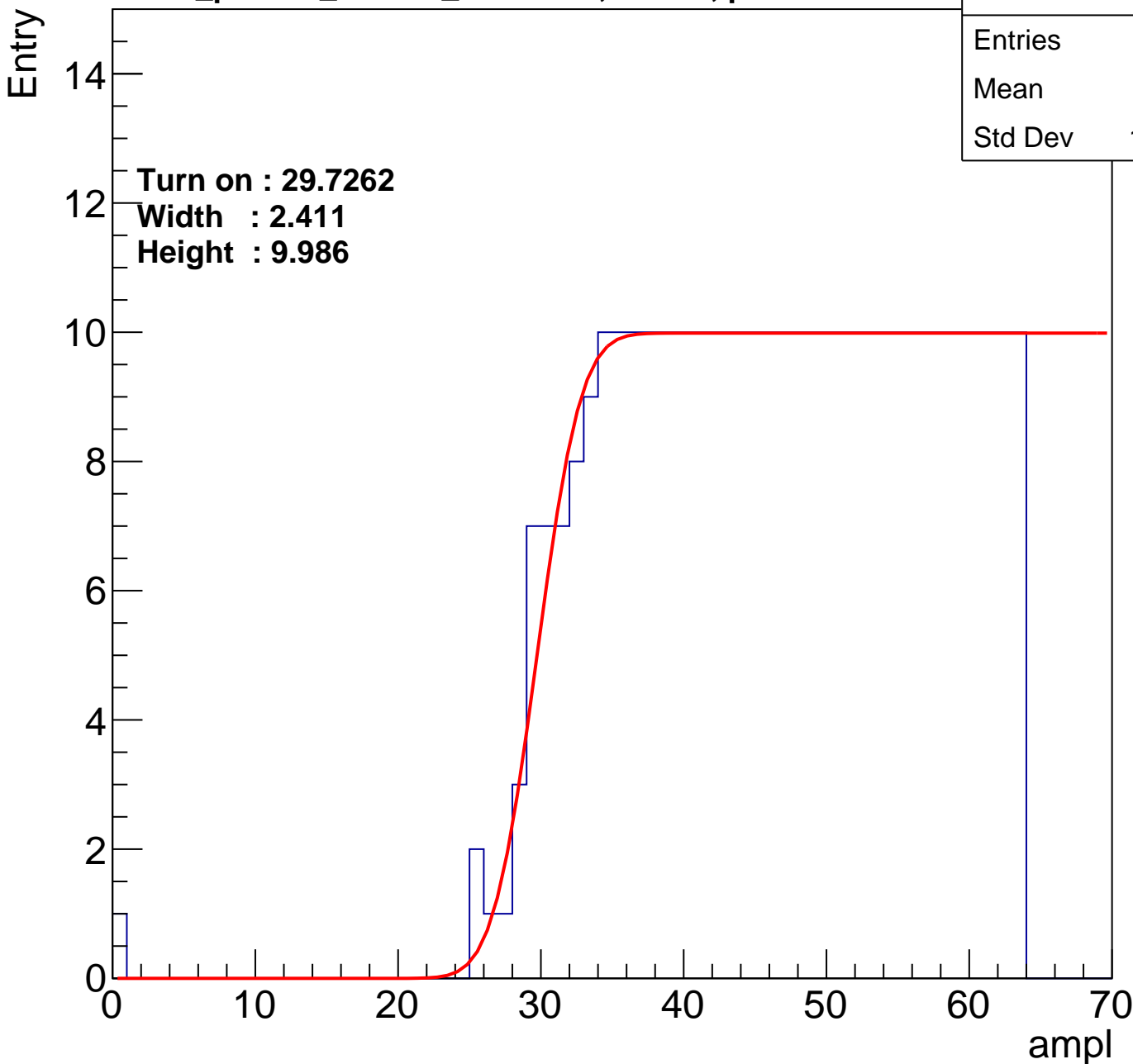
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

**Turn on : 29.7262**

**Width : 2.411**

**Height : 9.986**

Entries	346
Mean	46.01
Std Dev	10.42





# B0L102S, U26-ch7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 26.4423

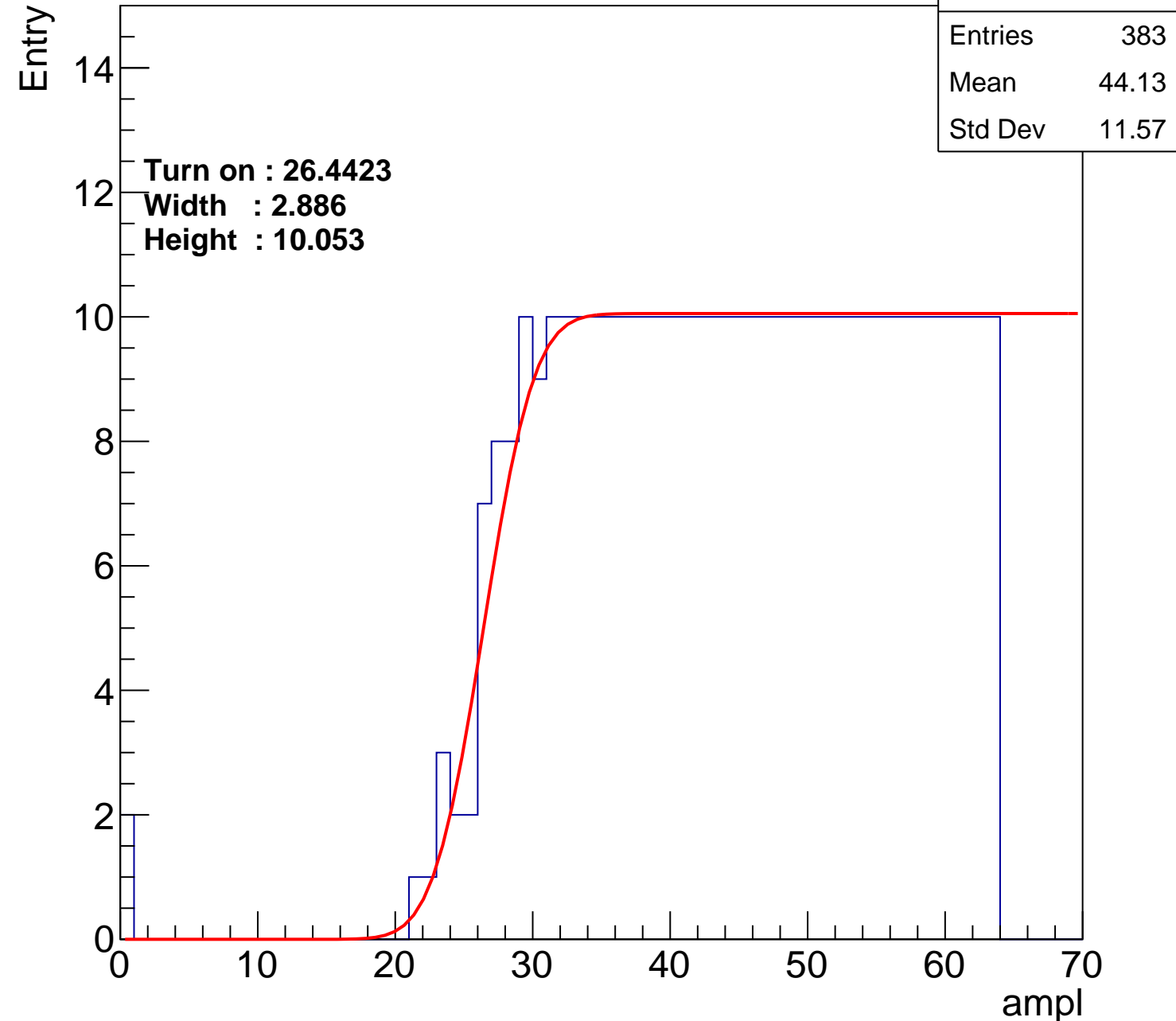
Width : 2.886

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch8

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.78
Std Dev	11.08

**Turn on : 26.8345**

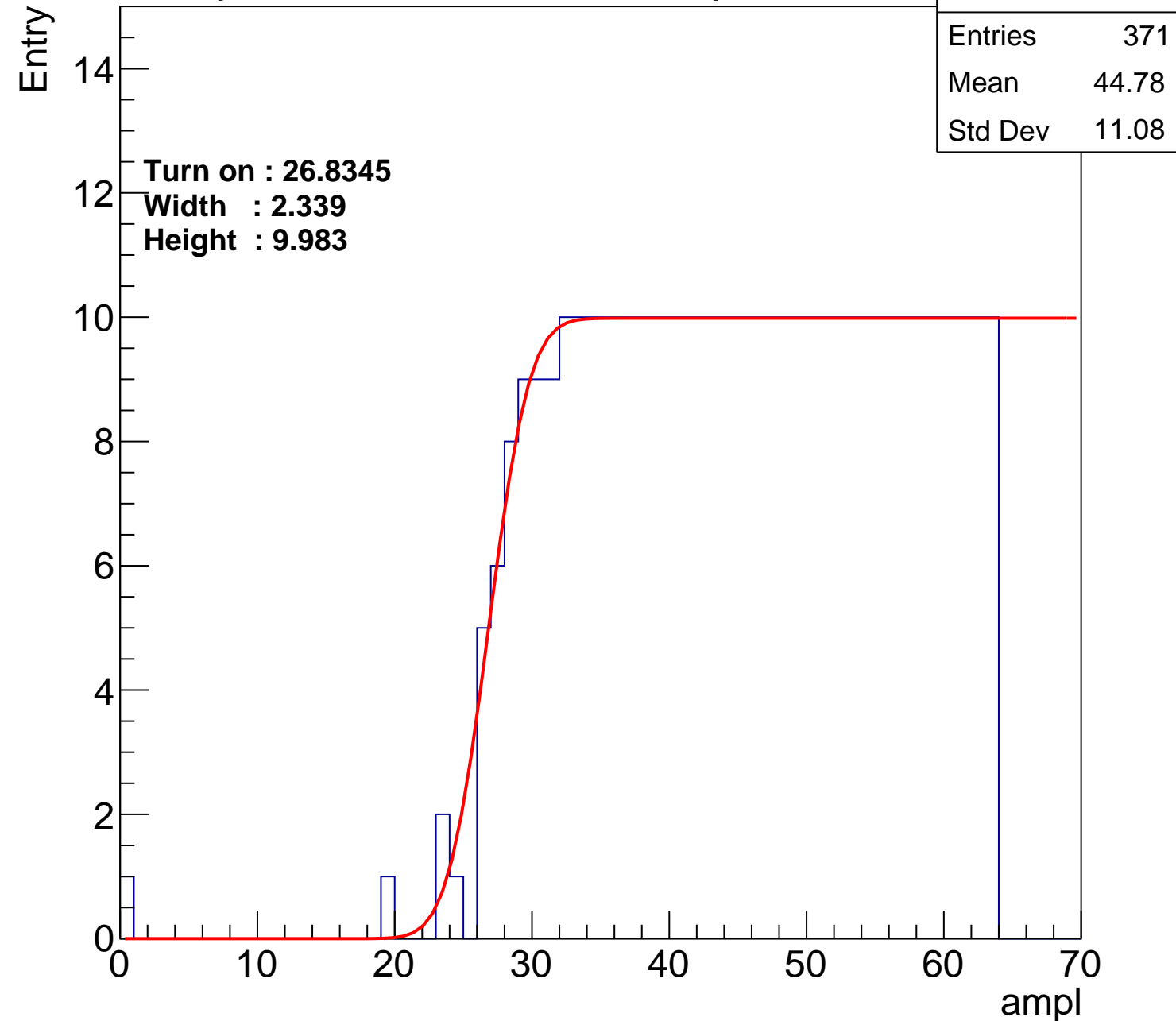
**Width : 2.339**

**Height : 9.983**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch9

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.31
Std Dev	11.3

**Turn on : 26.0577**

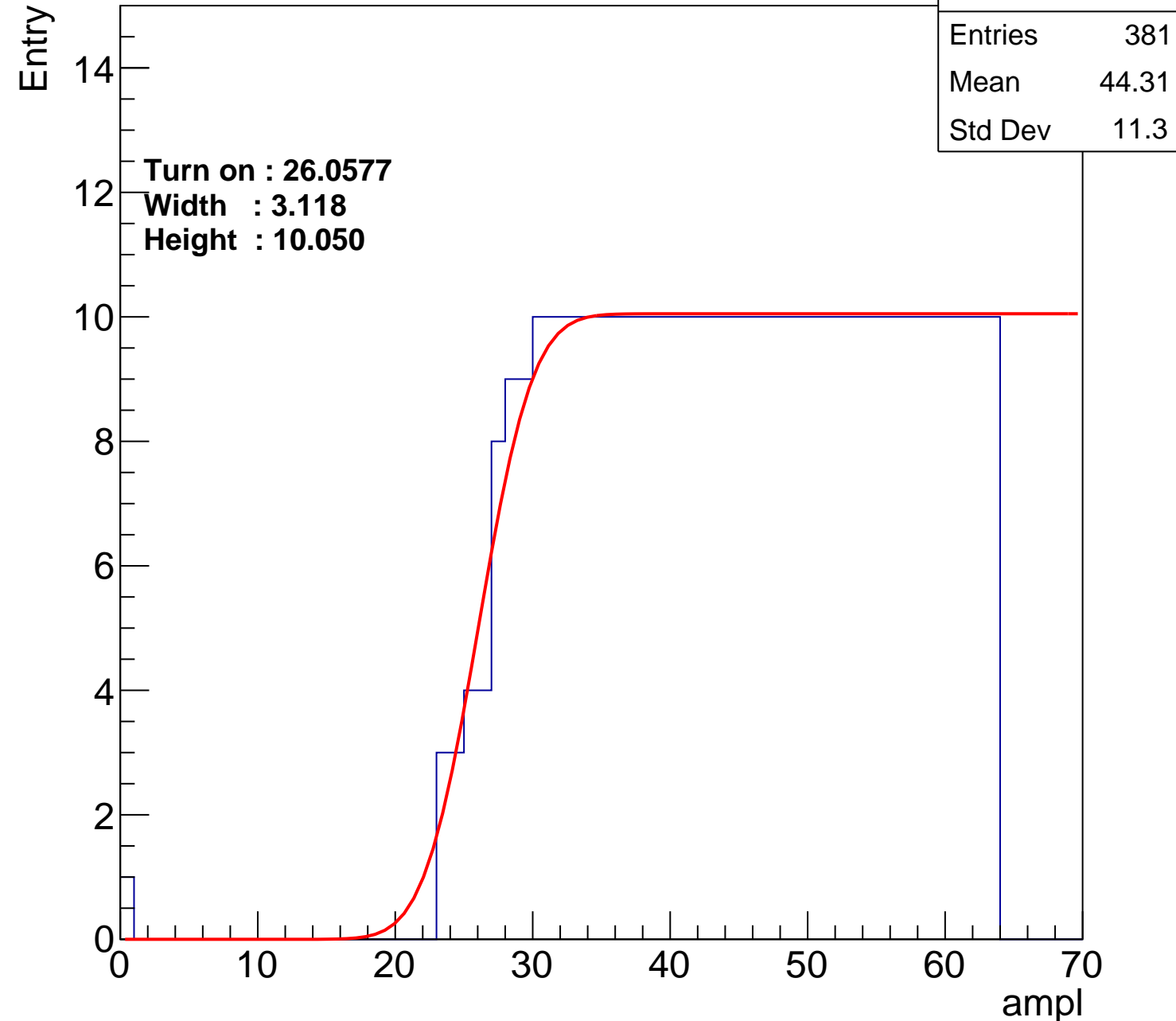
**Width : 3.118**

**Height : 10.050**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch10

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.9817

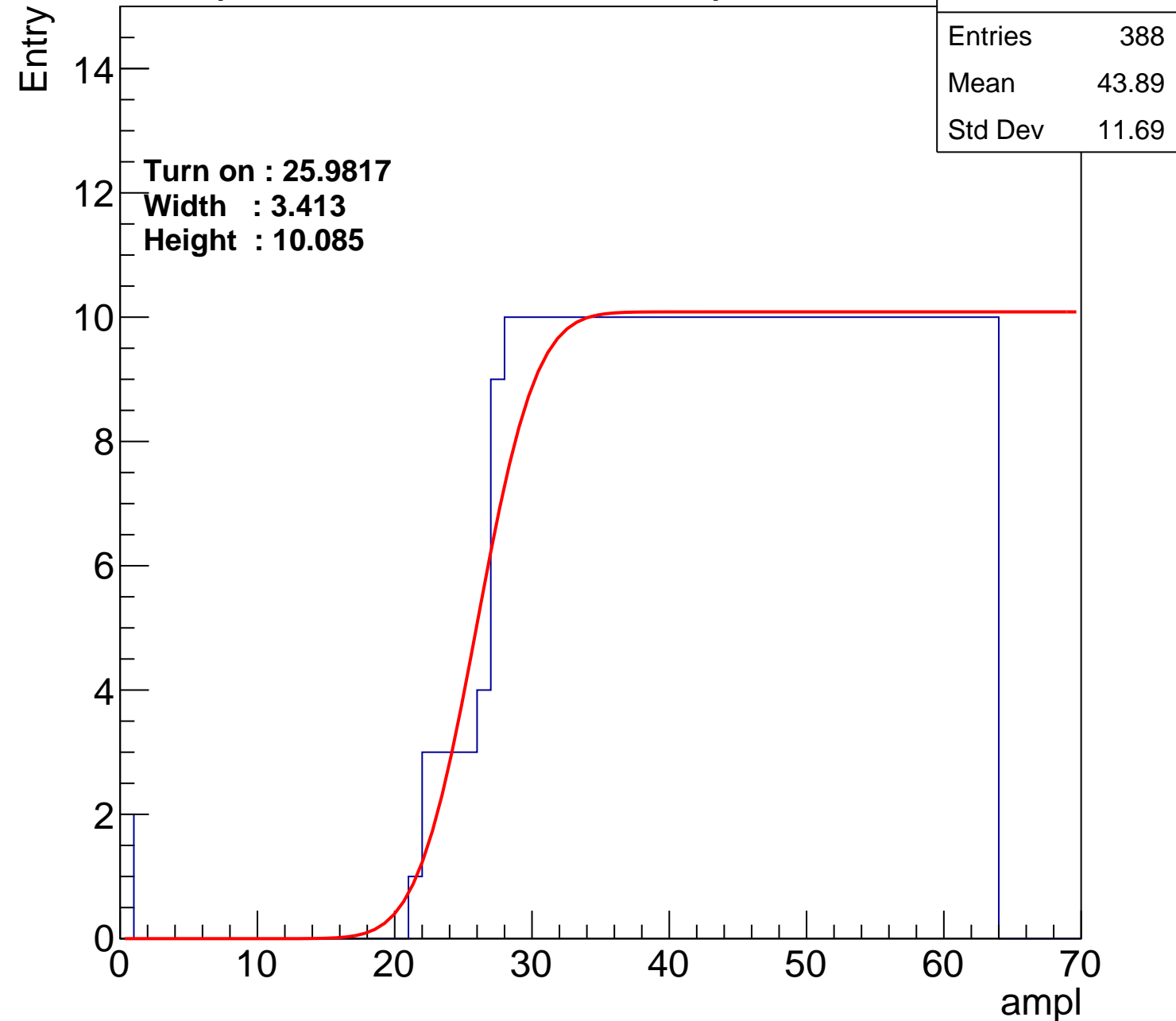
Width : 3.413

Height : 10.085

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch11

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.98
Std Dev	11.12

Turn on : 28.1133

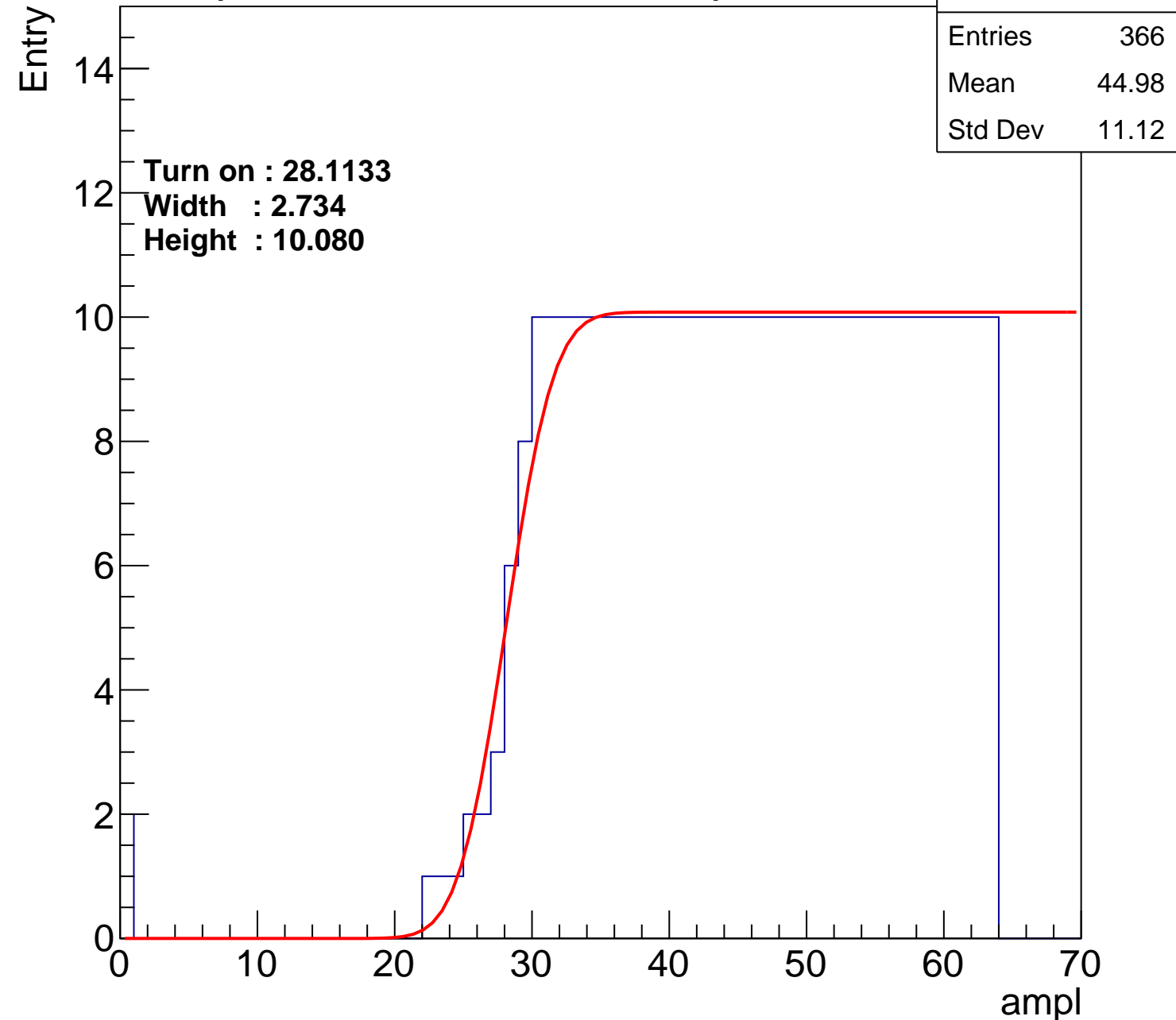
Width : 2.734

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch12

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	360
Mean	45.31
Std Dev	10.82

Turn on : 28.3429

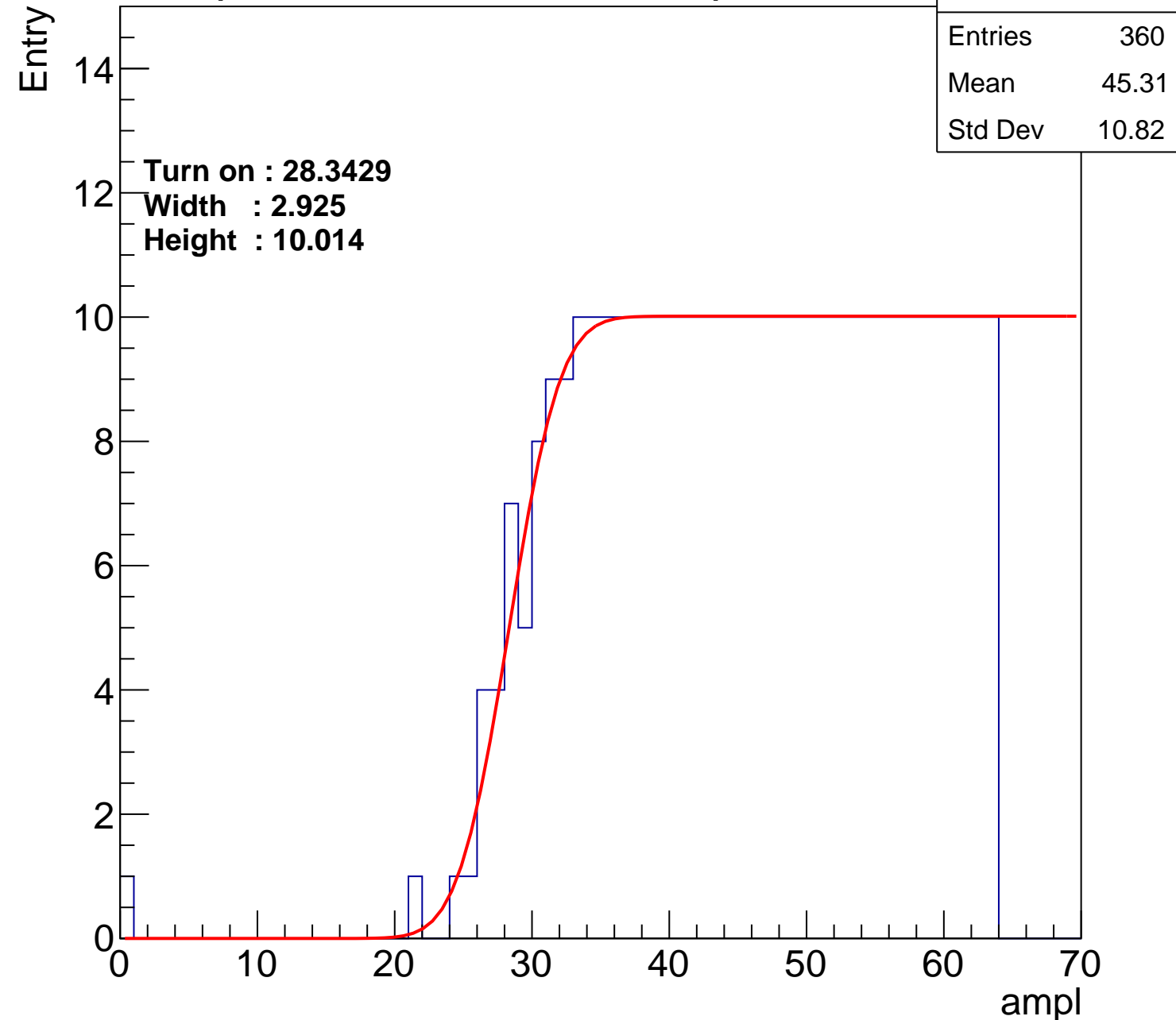
Width : 2.925

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch13

calib\_packv5\_042523\_0143.root, FC#12, port B1

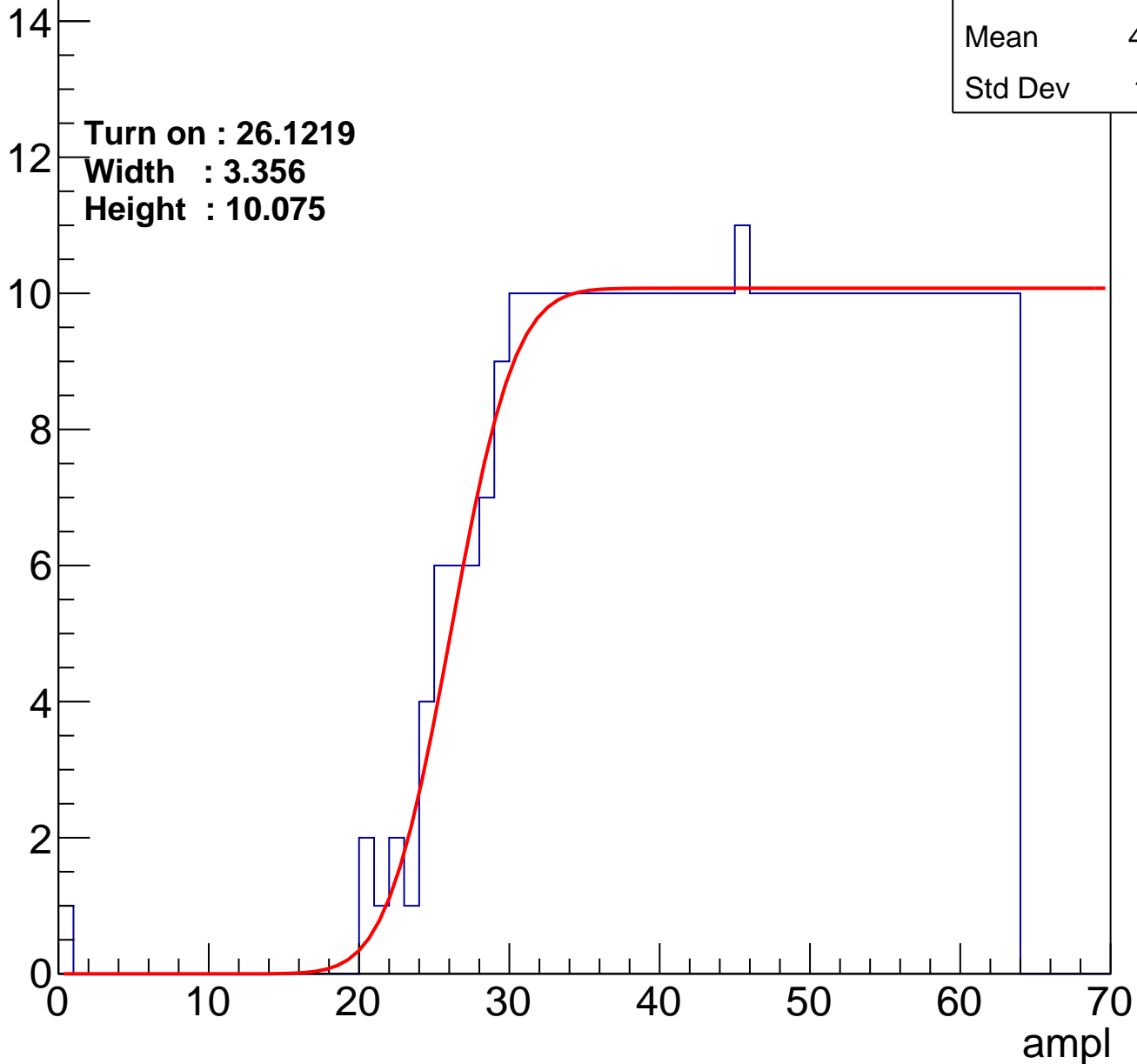
Entries	386
Mean	44.05
Std Dev	11.51

Turn on : 26.1219

Width : 3.356

Height : 10.075

Entry



# B0L102S, U26-ch14

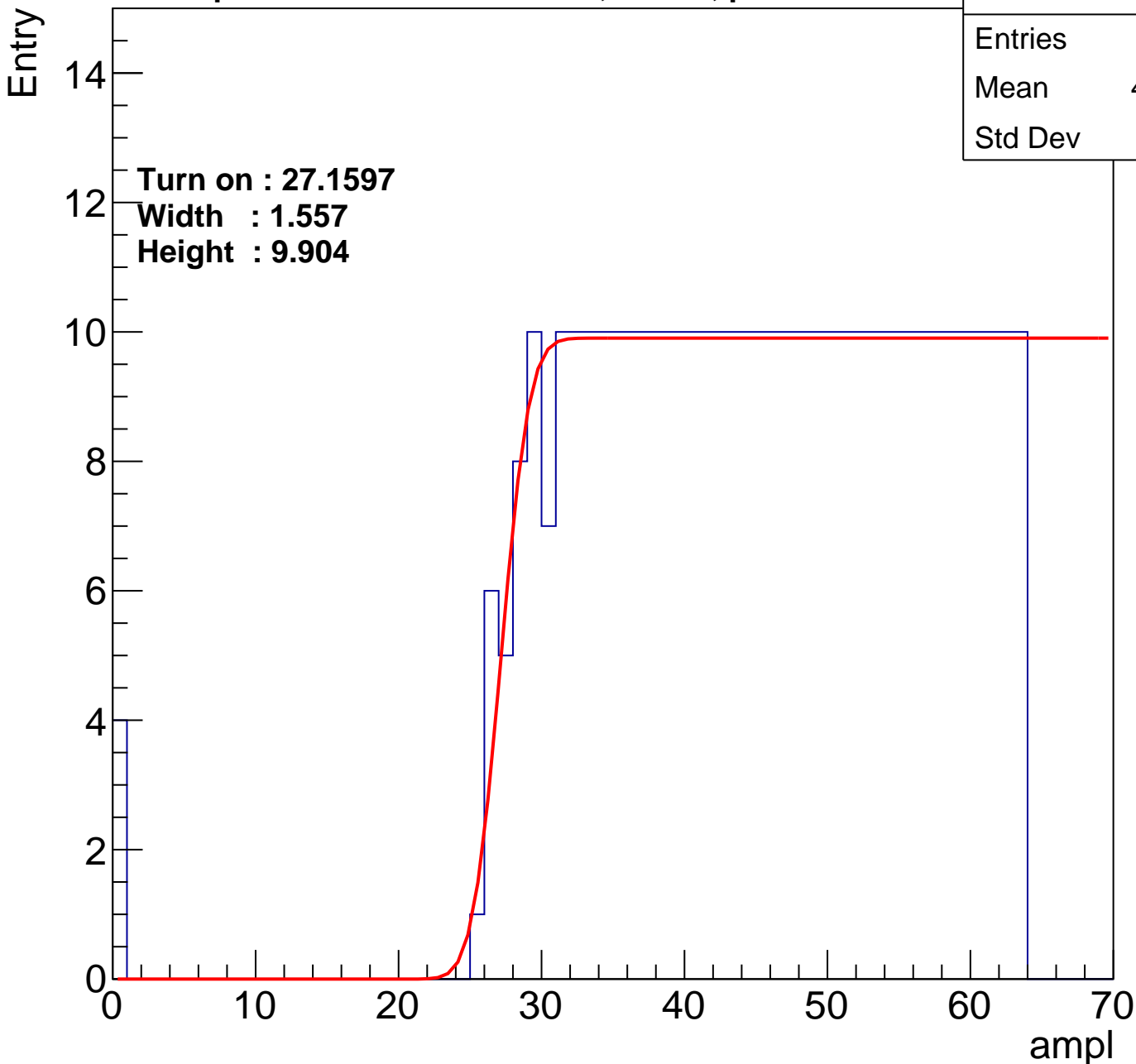
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

**Turn on : 27.1597**

**Width : 1.557**

**Height : 9.904**

Entries	371
Mean	44.61
Std Dev	11.6





# B0L102S, U26-ch15

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.7
Std Dev	11.62

Turn on : 27.7051

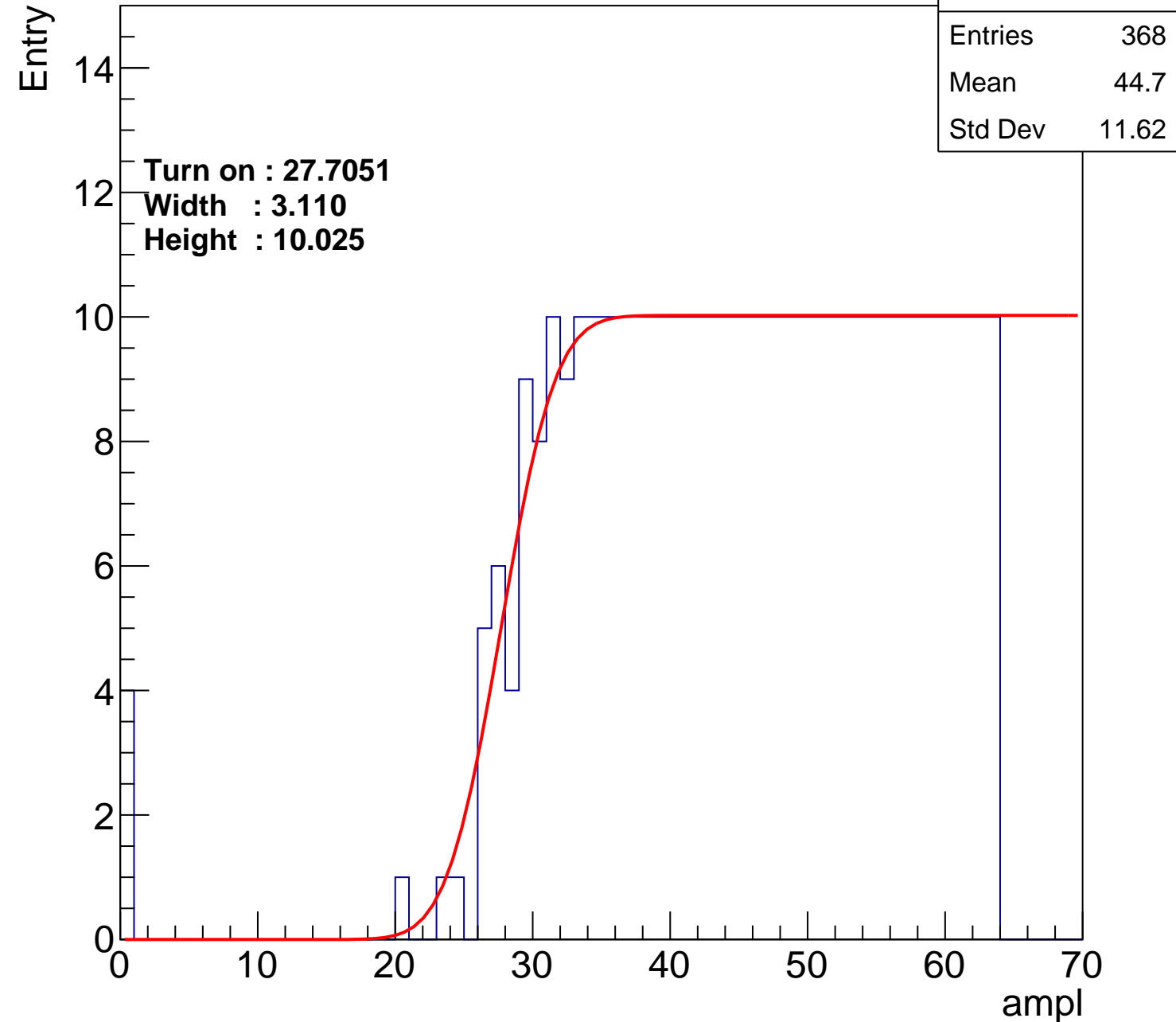
Width : 3.110

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch16

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.9
Std Dev	11.2

Turn on : 27.8014

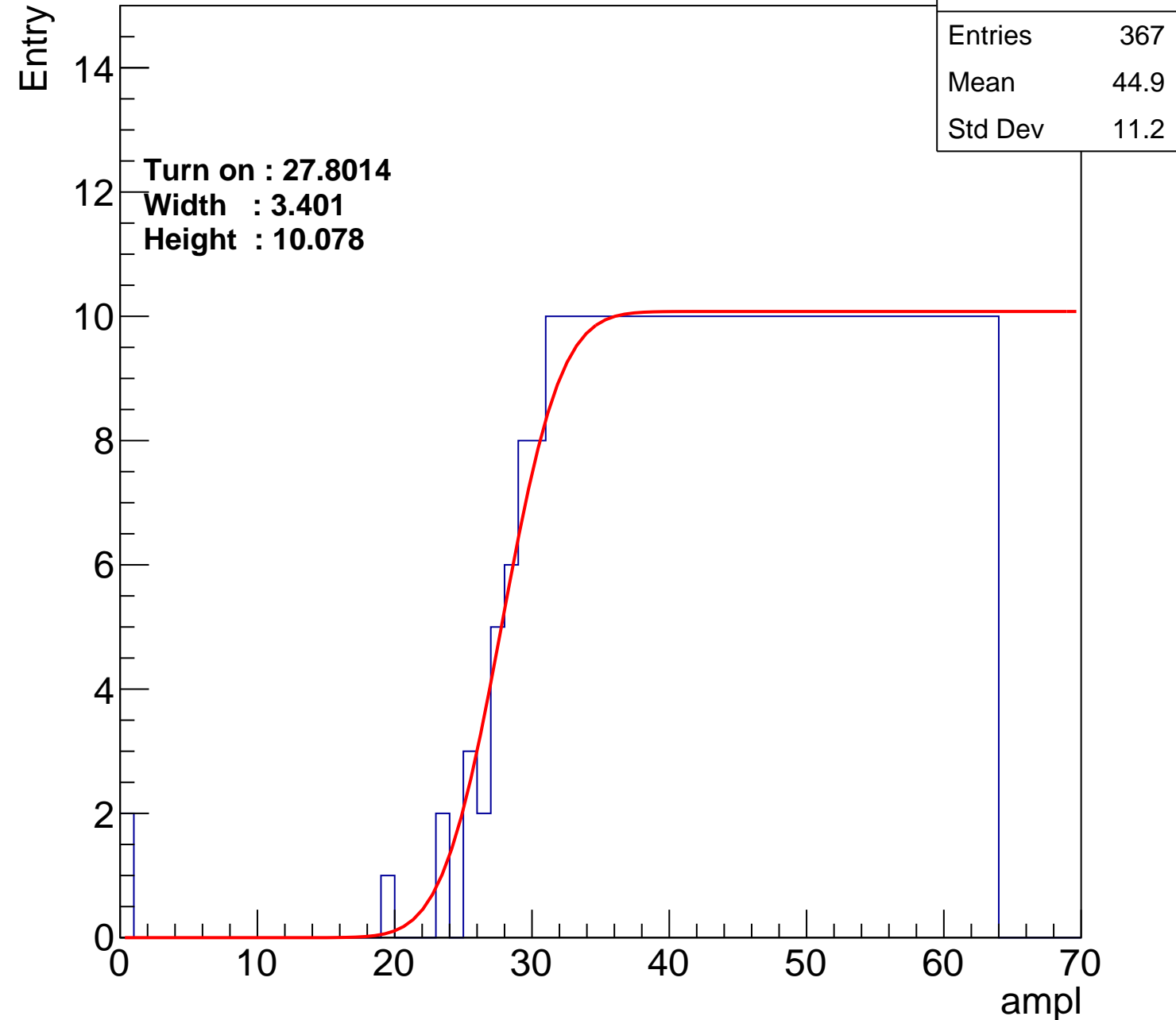
Width : 3.401

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch17

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.76
Std Dev	11.26

Turn on : 26.4245

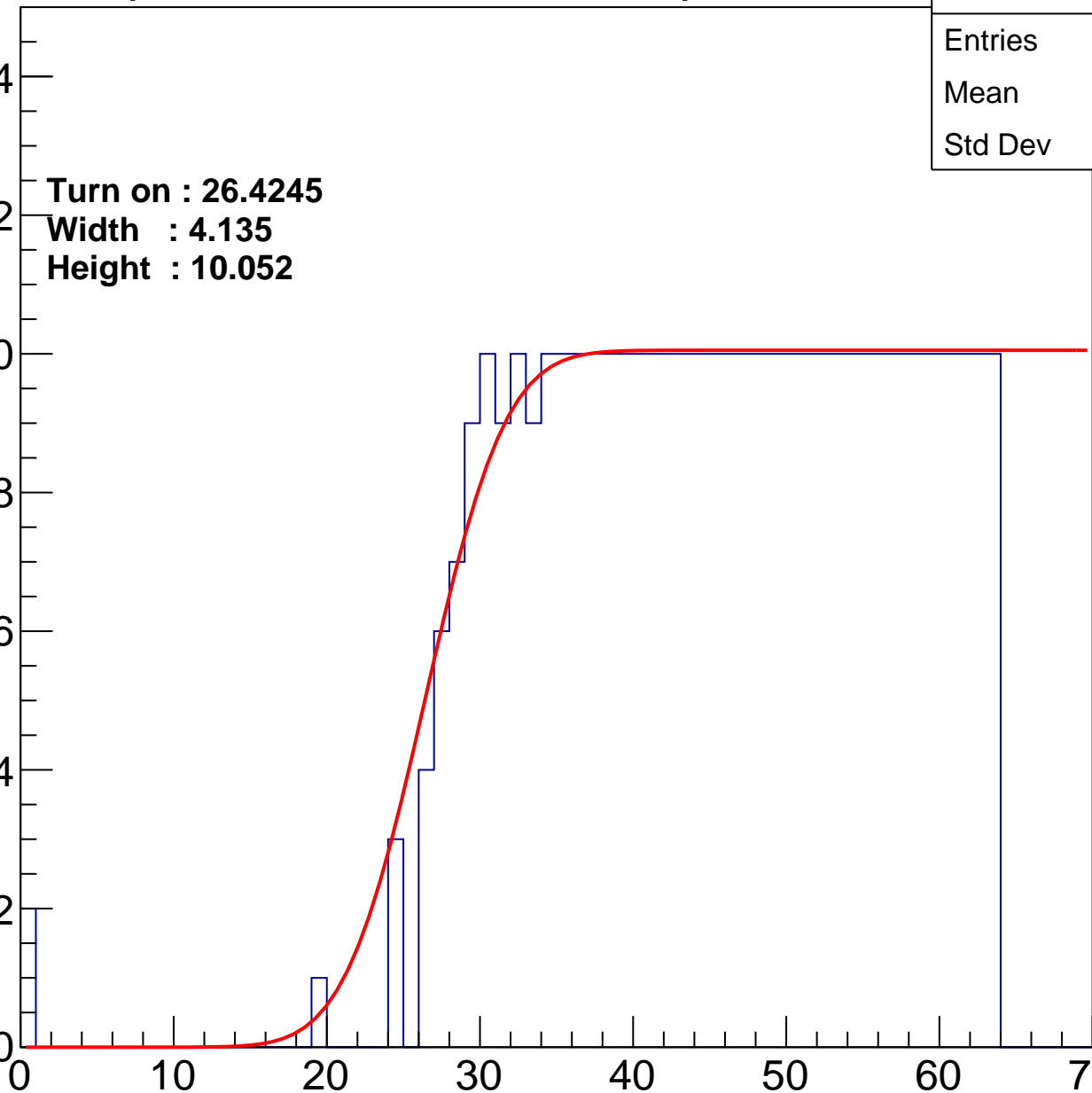
Width : 4.135

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch18

calib\_packv5\_042523\_0143.root, FC#12, port B1

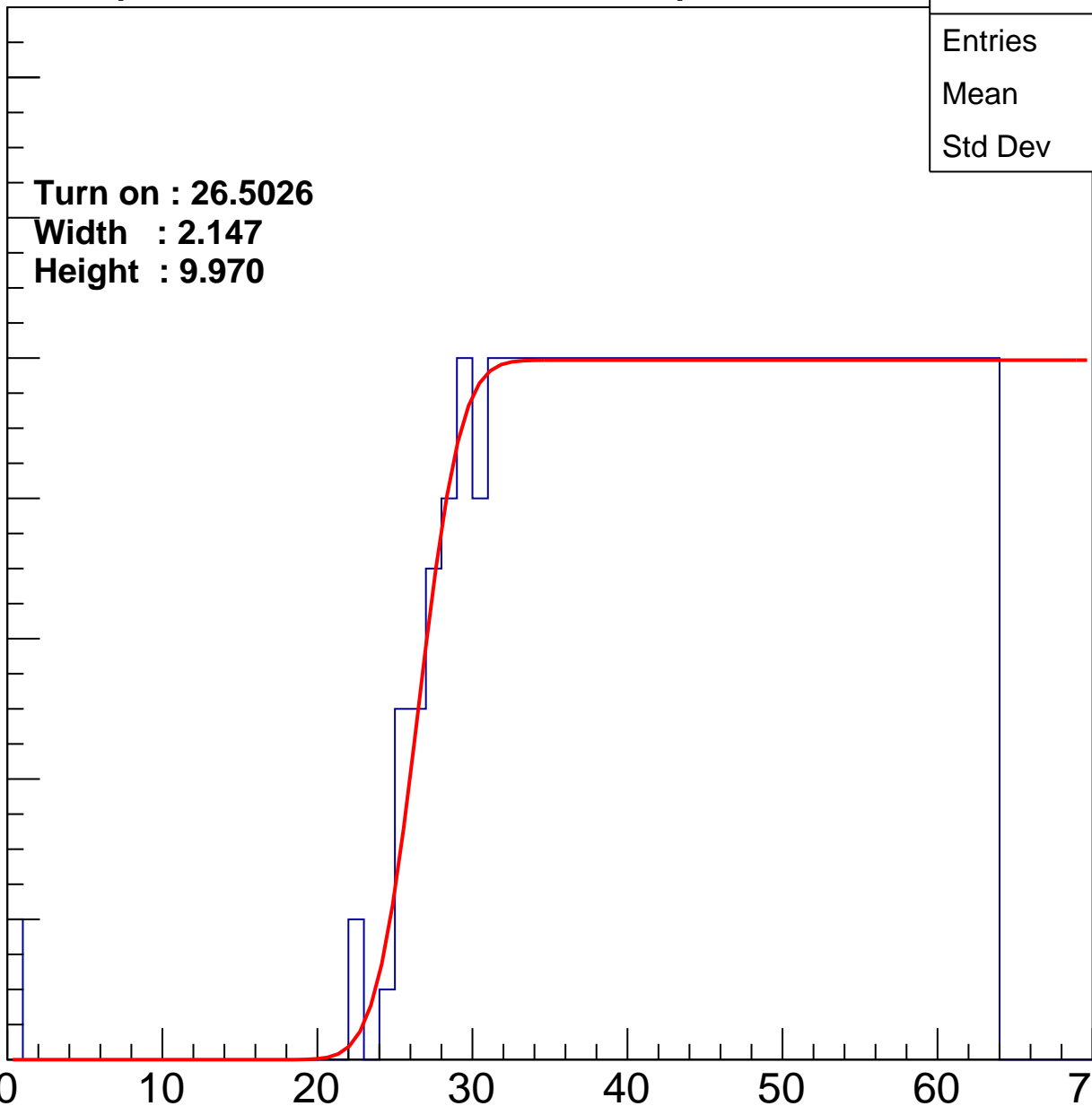
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.5026**  
**Width : 2.147**  
**Height : 9.970**

Entries	378
Mean	44.38
Std Dev	11.43

ampl



# B0L102S, U26-ch19

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.71
Std Dev	11.32

**Turn on : 26.6927**

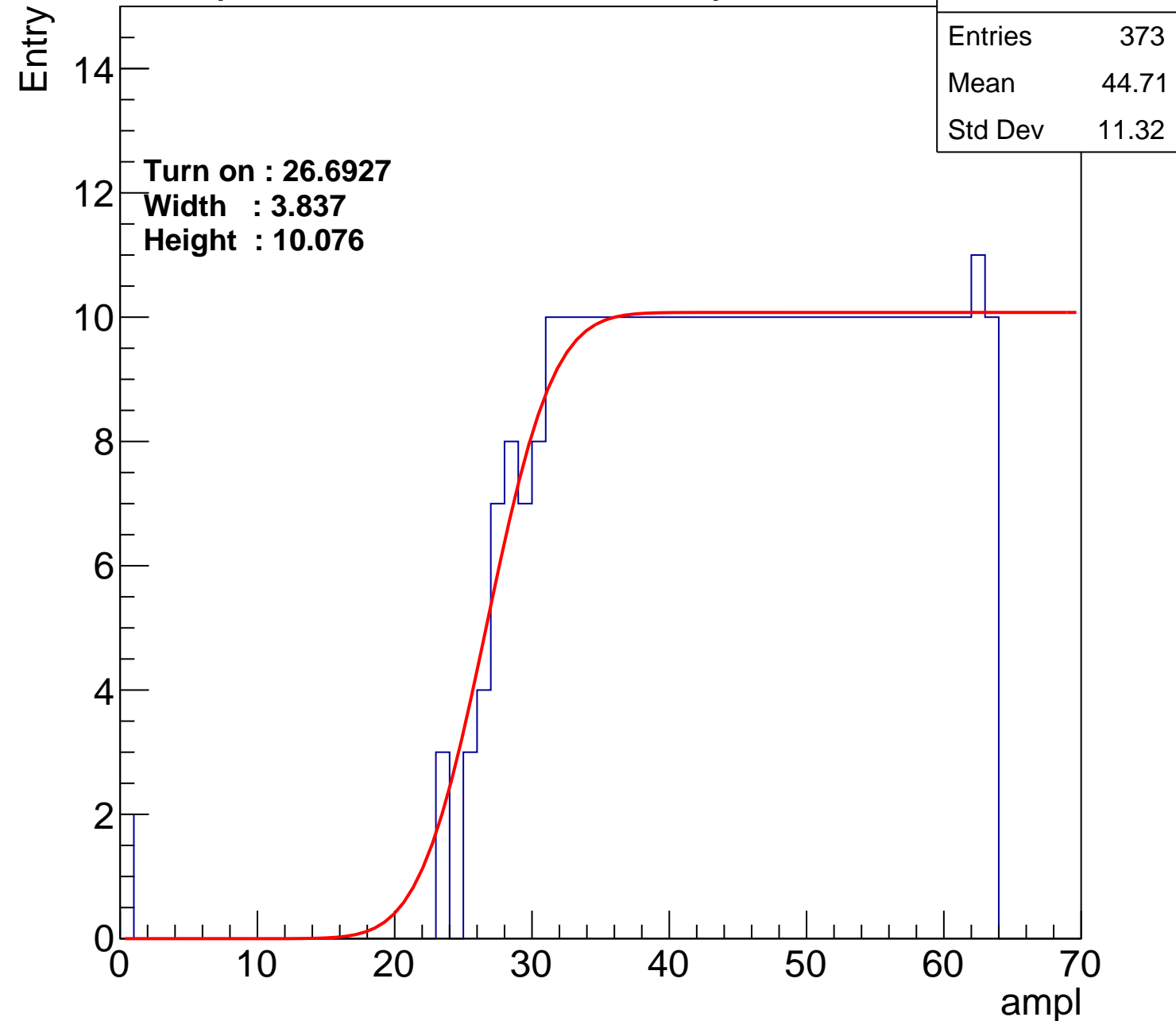
**Width : 3.837**

**Height : 10.076**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch20

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.33
Std Dev	11.59

Turn on : 26.6133

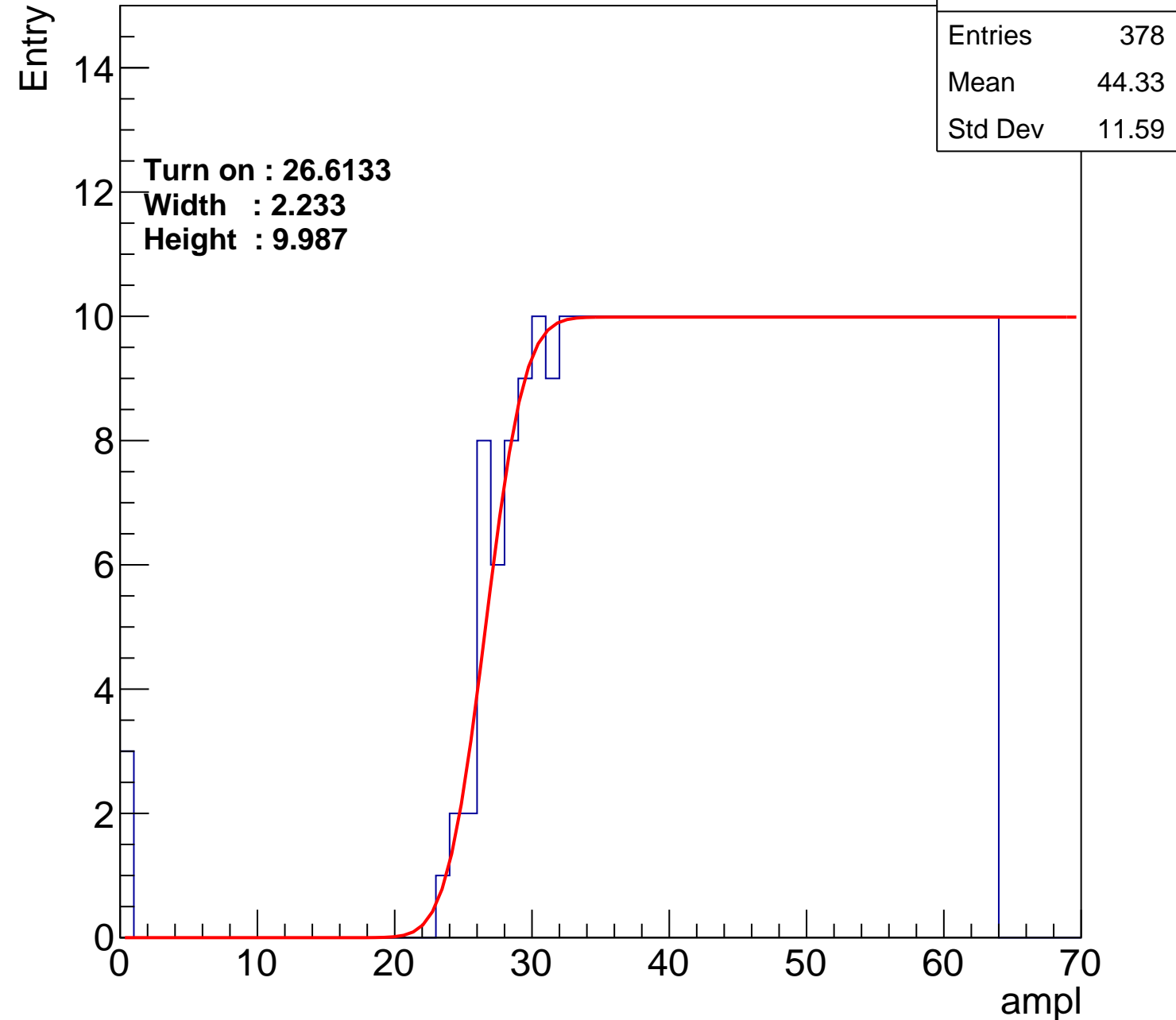
Width : 2.233

Height : 9.987

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch21

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.7
Std Dev	11.78

Turn on : 25.5699

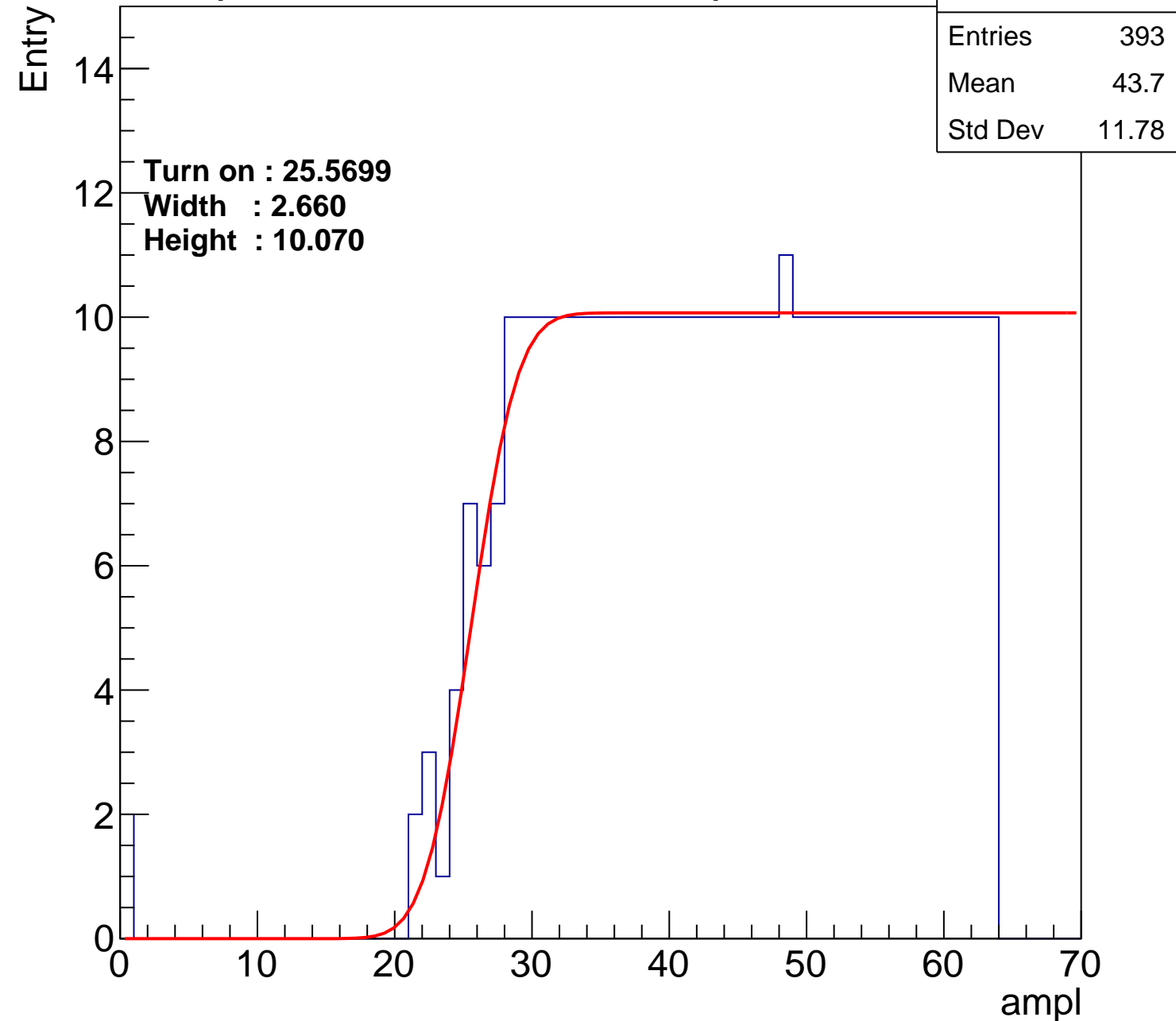
Width : 2.660

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch22

calib\_packv5\_042523\_0143.root, FC#12, port B1

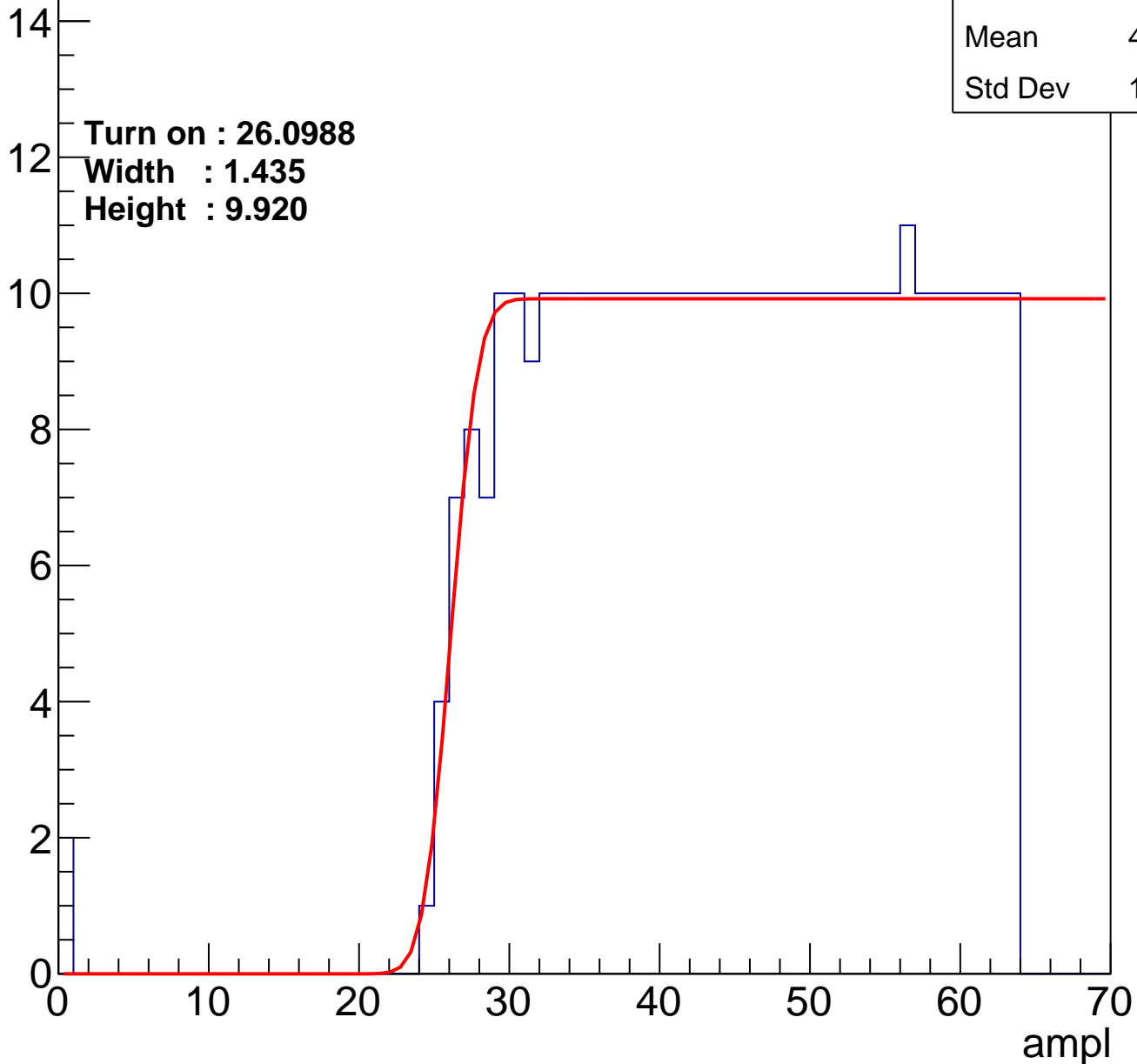
Entries	379
Mean	44.44
Std Dev	11.38

Turn on : 26.0988

Width : 1.435

Height : 9.920

Entry





# B0L102S, U26-ch23

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.89
Std Dev	11.01

**Turn on : 27.2407**

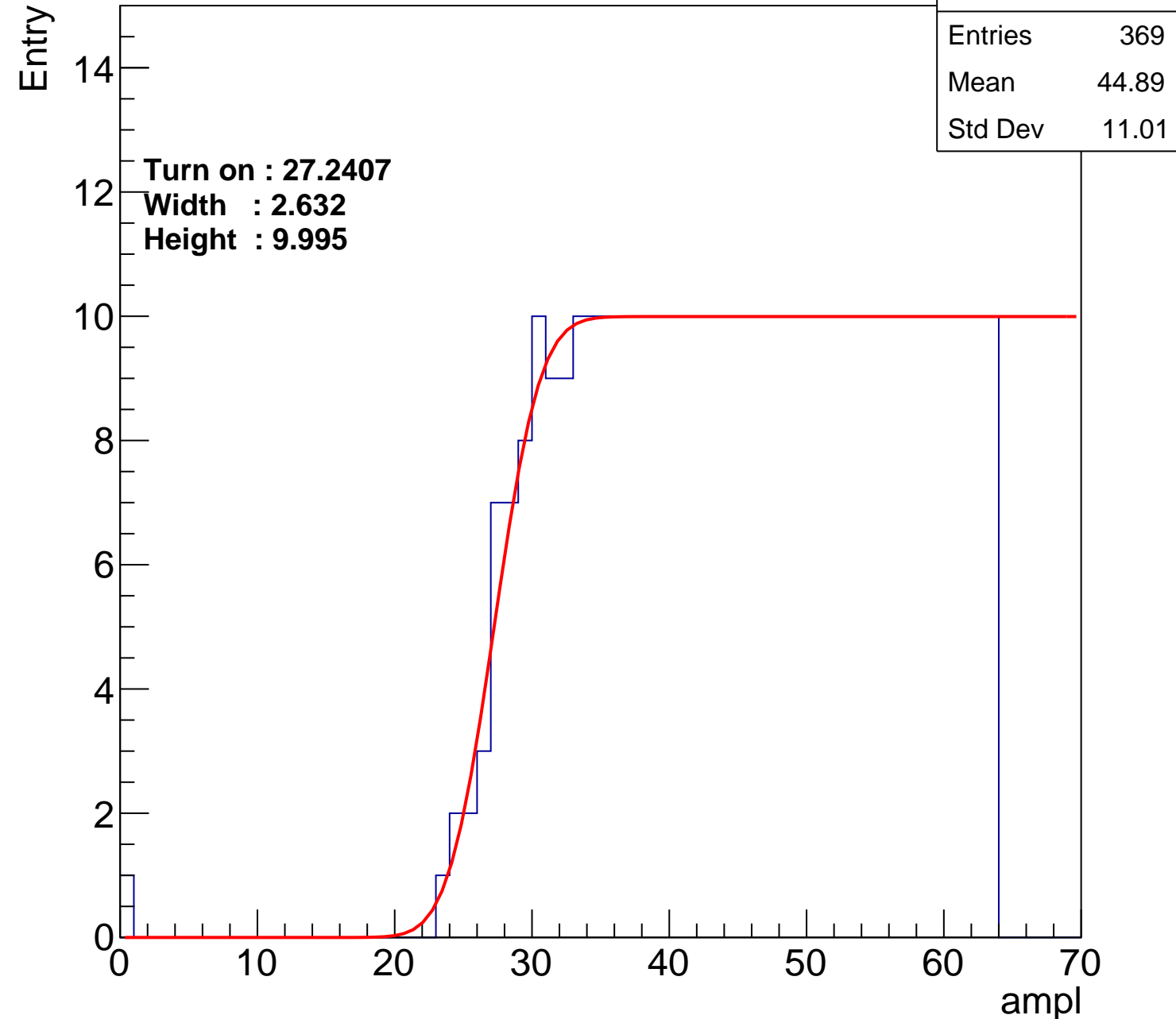
**Width : 2.632**

**Height : 9.995**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch24

calib\_packv5\_042523\_0143.root, FC#12, port B1

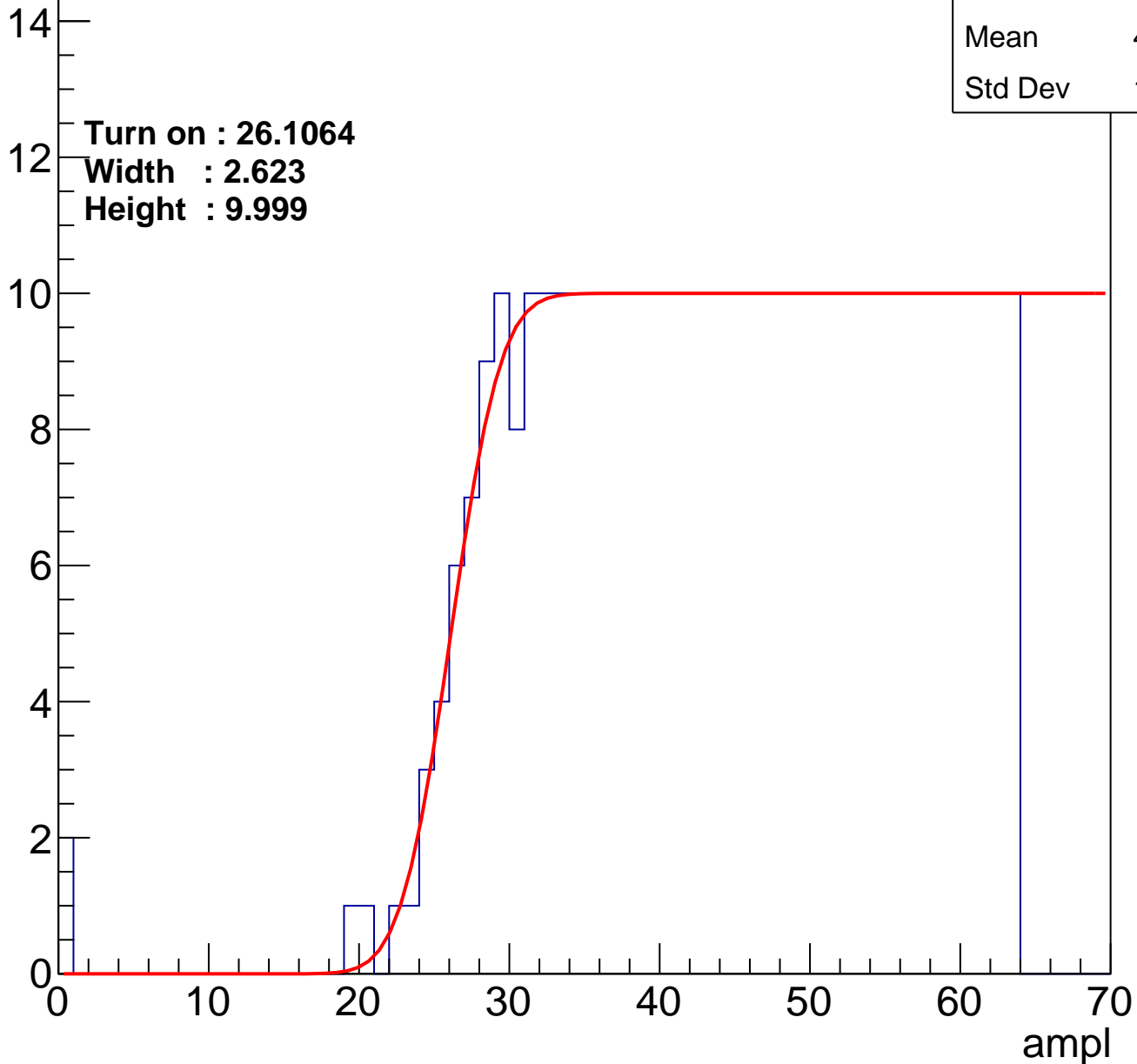
Entries	383
Mean	44.11
Std Dev	11.61

Turn on : 26.1064

Width : 2.623

Height : 9.999

Entry



# B0L102S, U26-ch25

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	44.94
Std Dev	11.38

Turn on : 28.7301

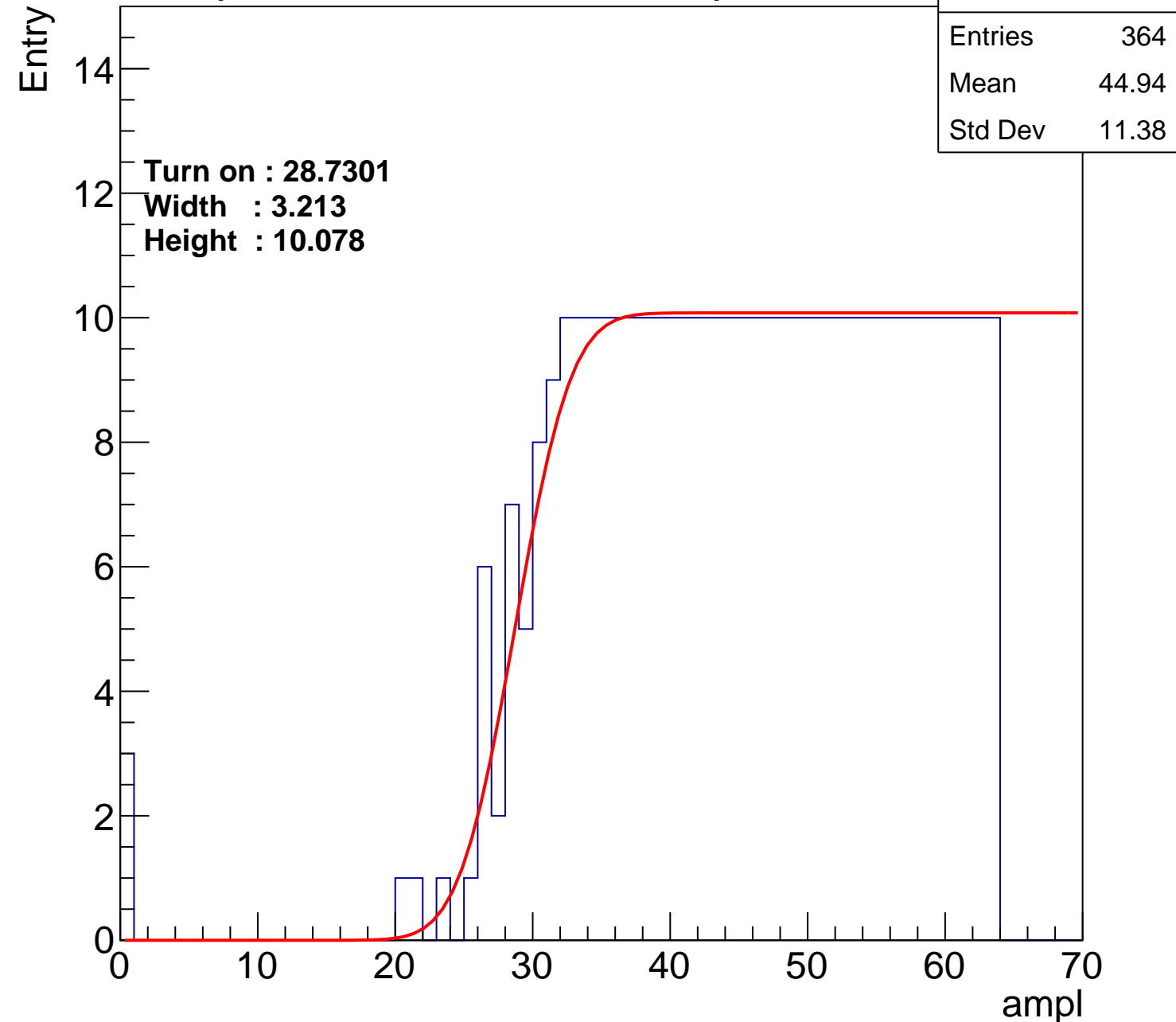
Width : 3.213

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch26

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	363
Mean	45.14
Std Dev	10.92

Turn on : 27.9025

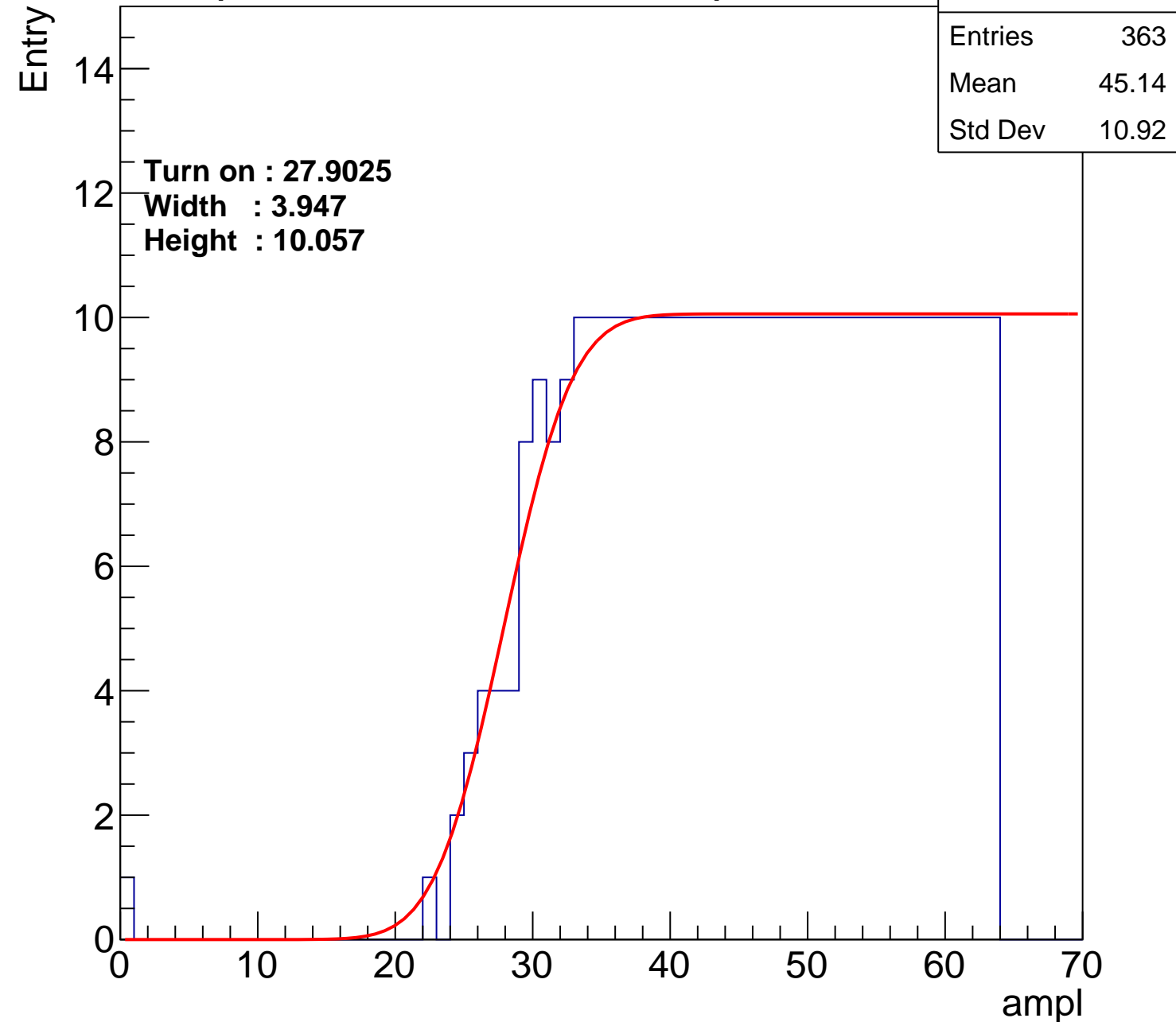
Width : 3.947

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch27

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	358
Mean	45.18
Std Dev	11.4

Turn on : 28.5461

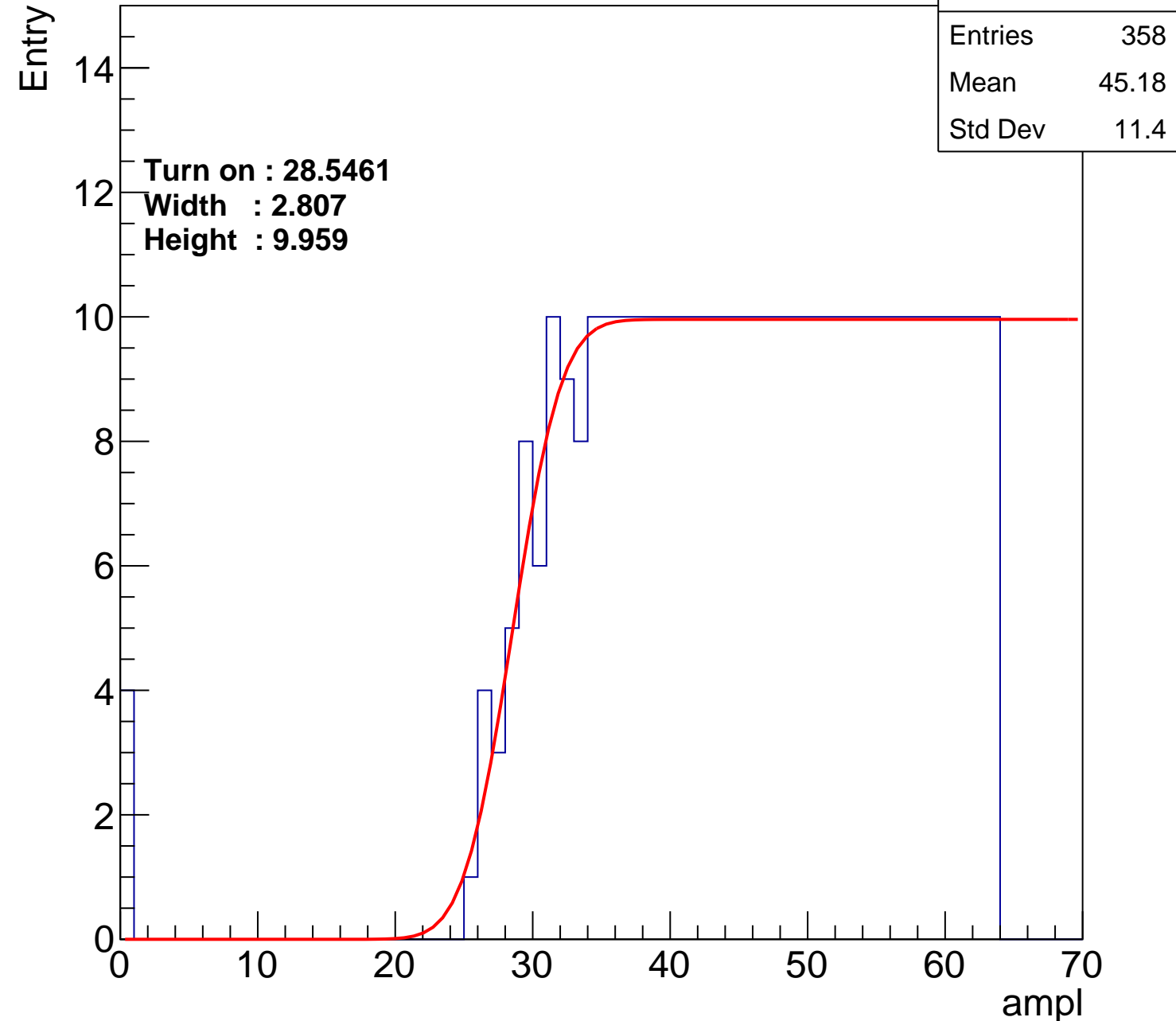
Width : 2.807

Height : 9.959

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch28

calib\_packv5\_042523\_0143.root, FC#12, port B1

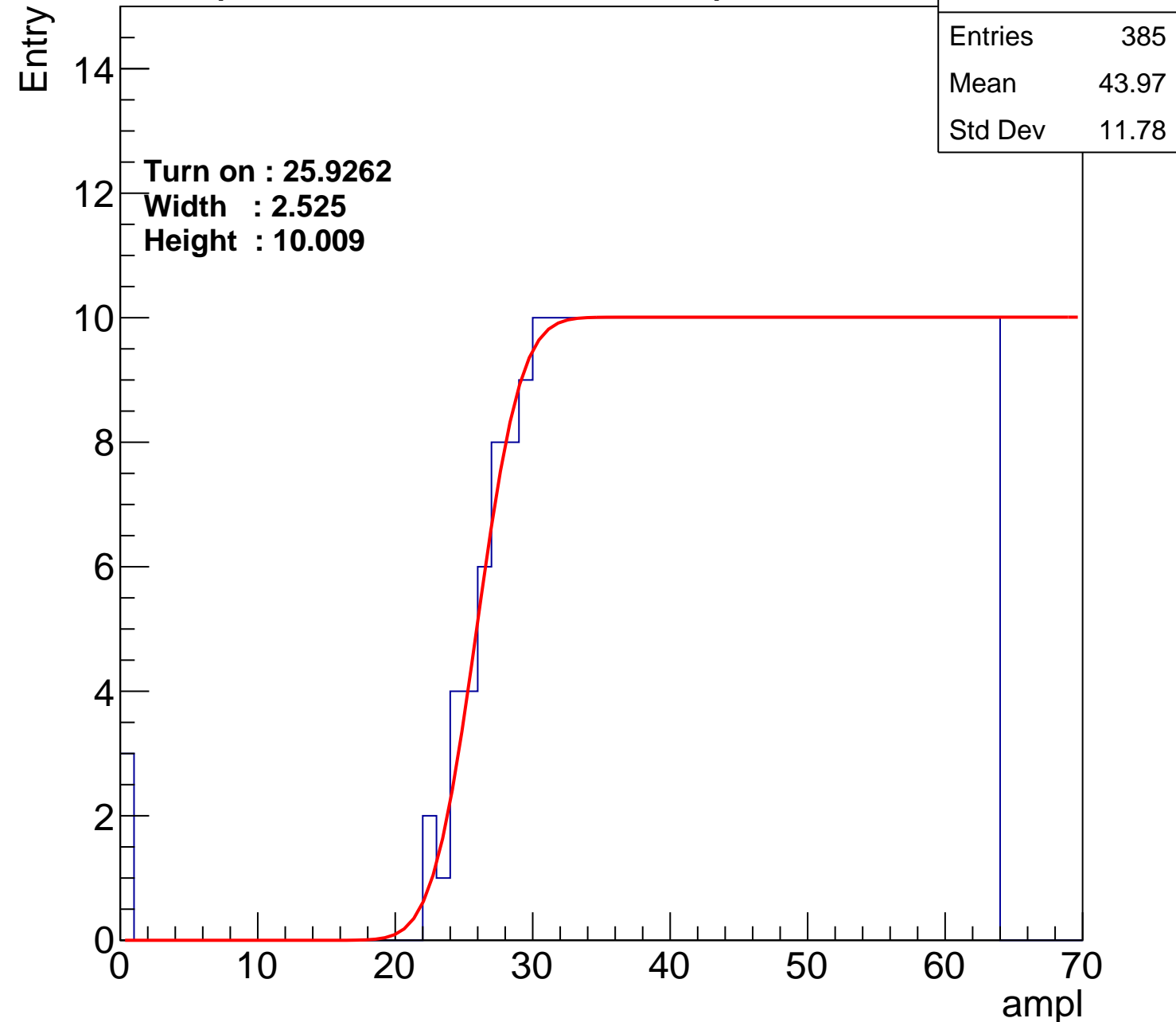
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.9262  
Width : 2.525  
Height : 10.009

Entries	385
Mean	43.97
Std Dev	11.78

ampl



# B0L102S, U26-ch29

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	358
Mean	45.34
Std Dev	10.96

Turn on : 28.9528

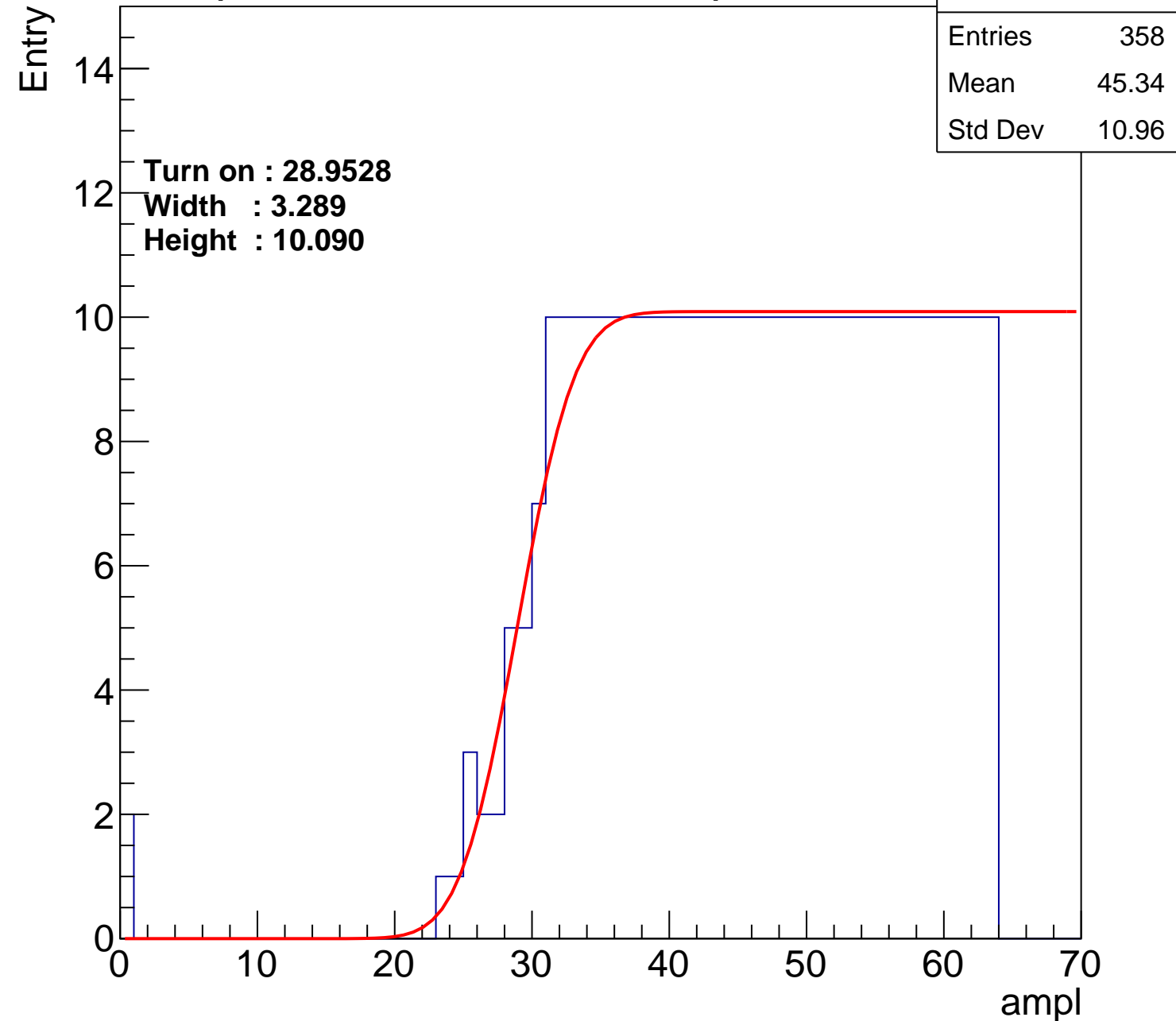
Width : 3.289

Height : 10.090

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch30

calib\_packv5\_042523\_0143.root, FC#12, port B1

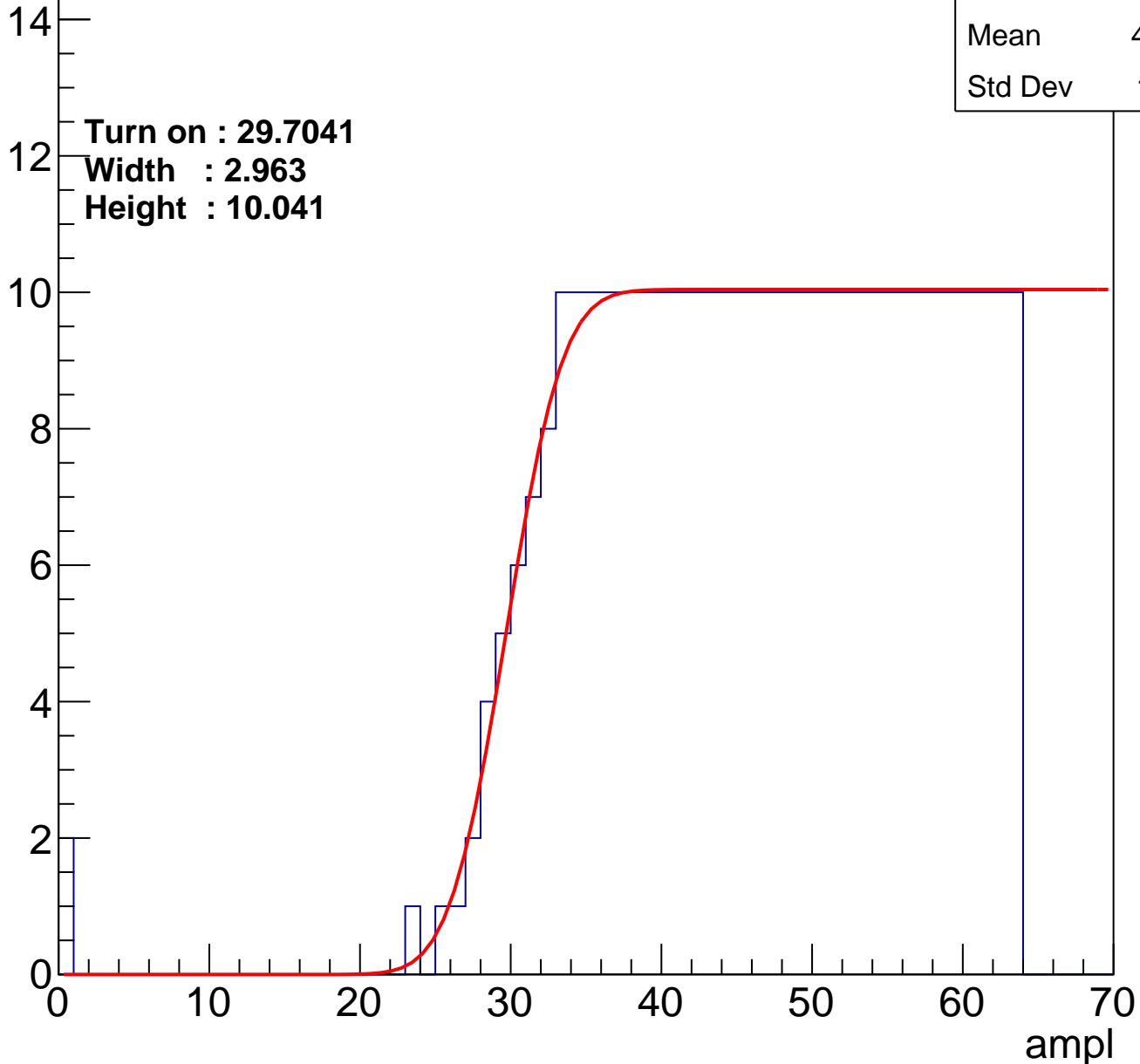
Entries	347
Mean	45.87
Std Dev	10.71

Turn on : 29.7041

Width : 2.963

Height : 10.041

Entry





# B0L102S, U26-ch31

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.18
Std Dev	11.42

Turn on : 25.9898

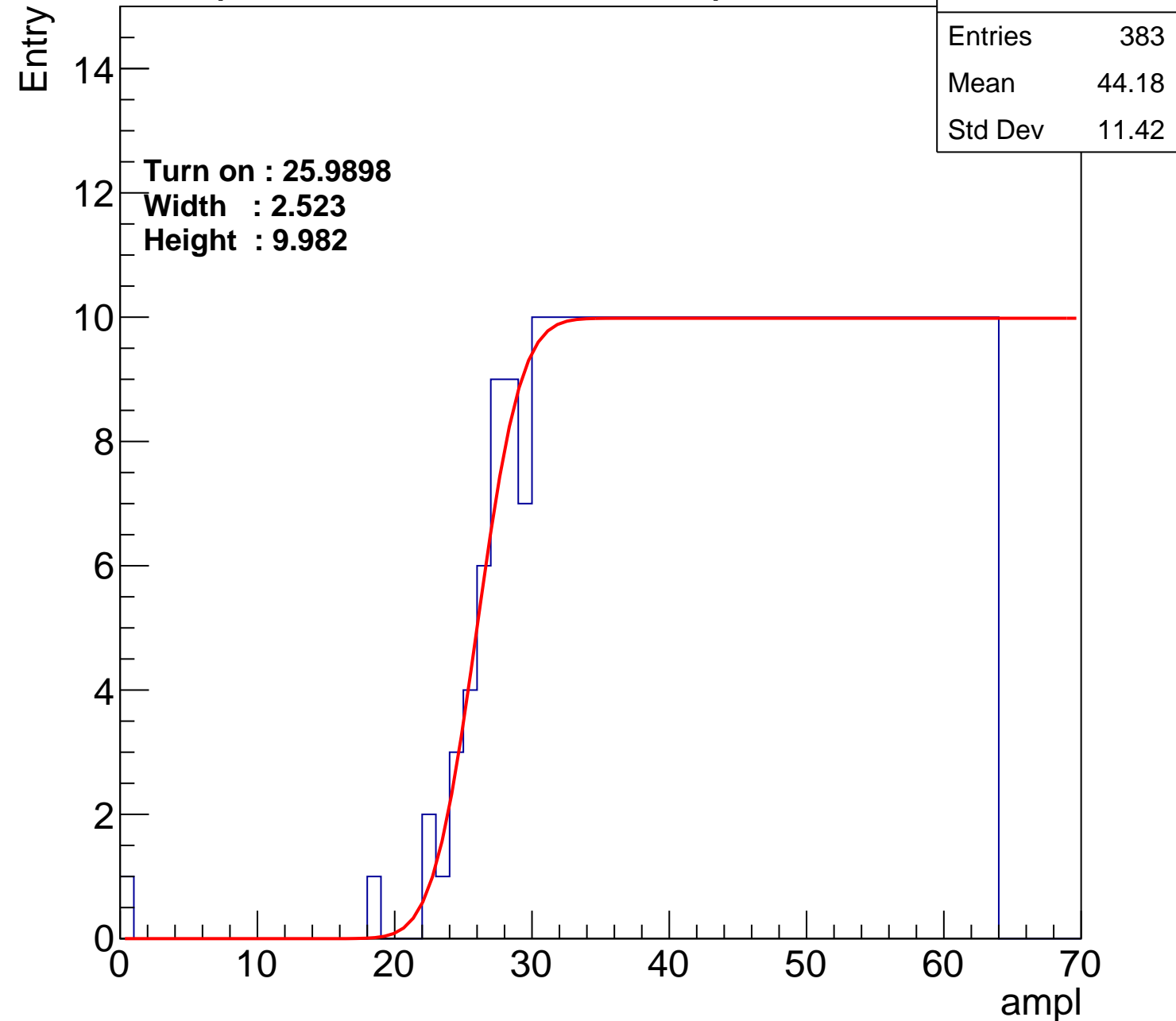
Width : 2.523

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch32

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	358
Mean	45.34
Std Dev	10.97

Turn on : 28.7701

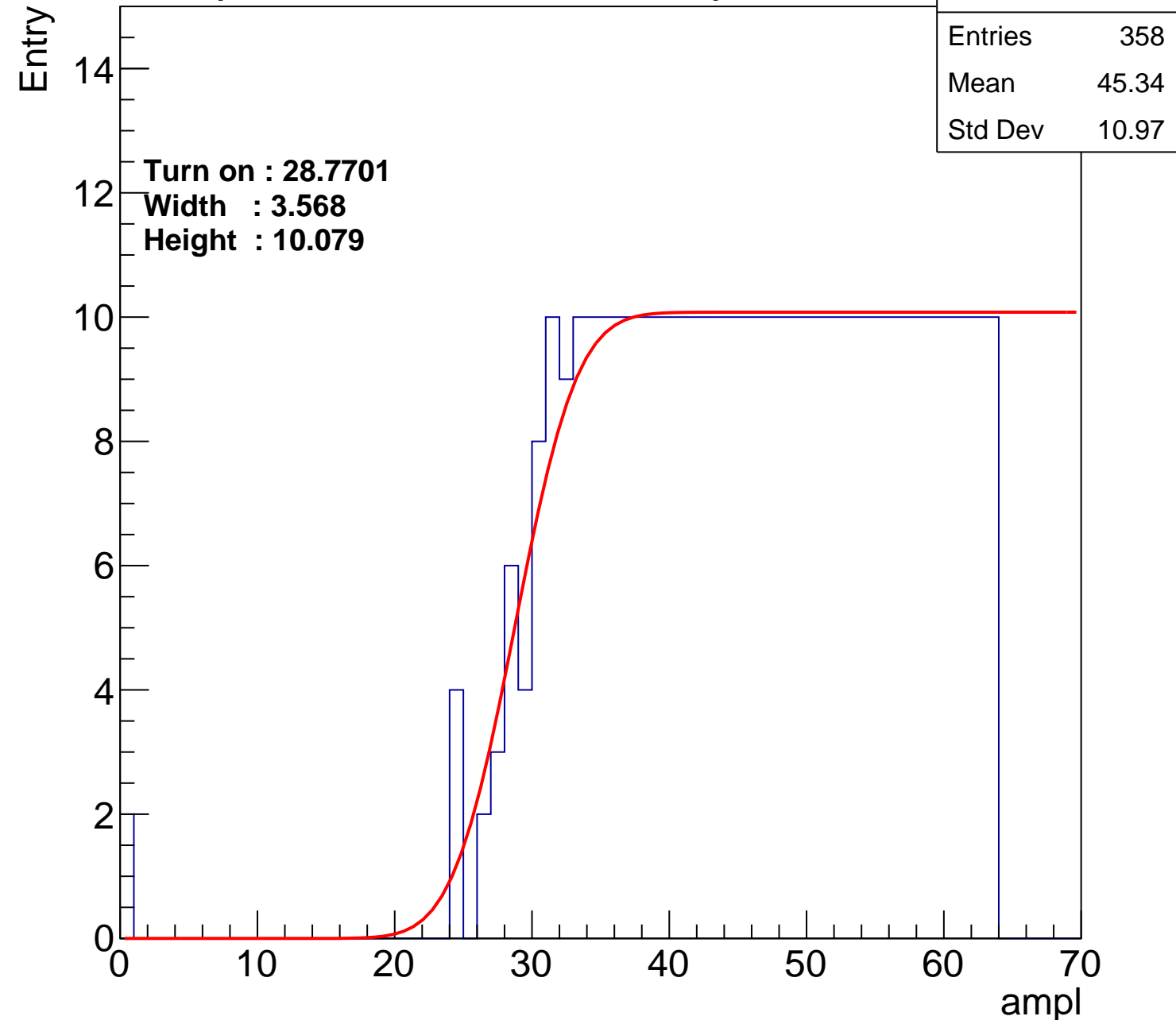
Width : 3.568

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch33

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	359
Mean	45.36
Std Dev	10.76

Turn on : 28.0625

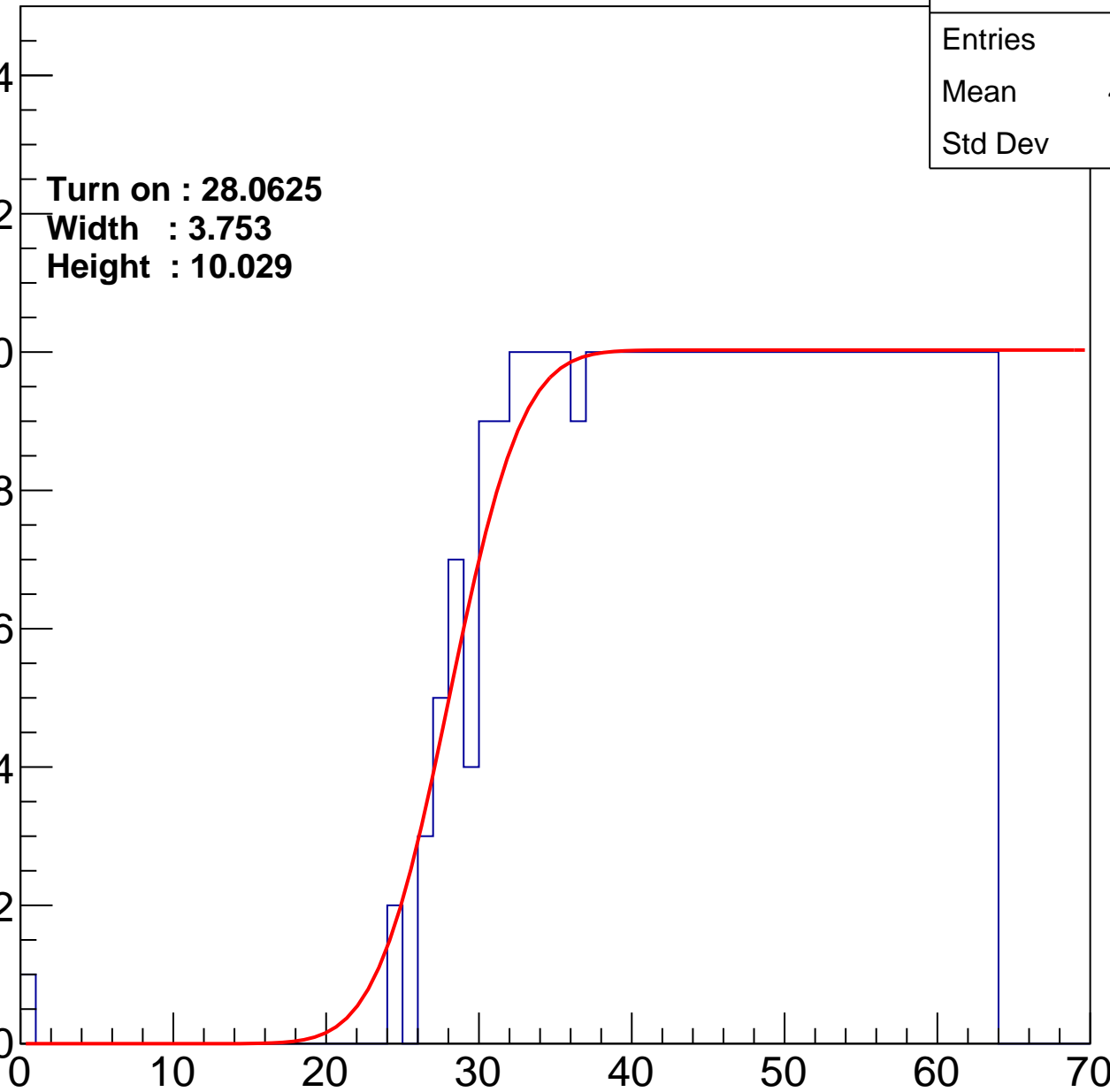
Width : 3.753

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch34

calib\_packv5\_042523\_0143.root, FC#12, port B1

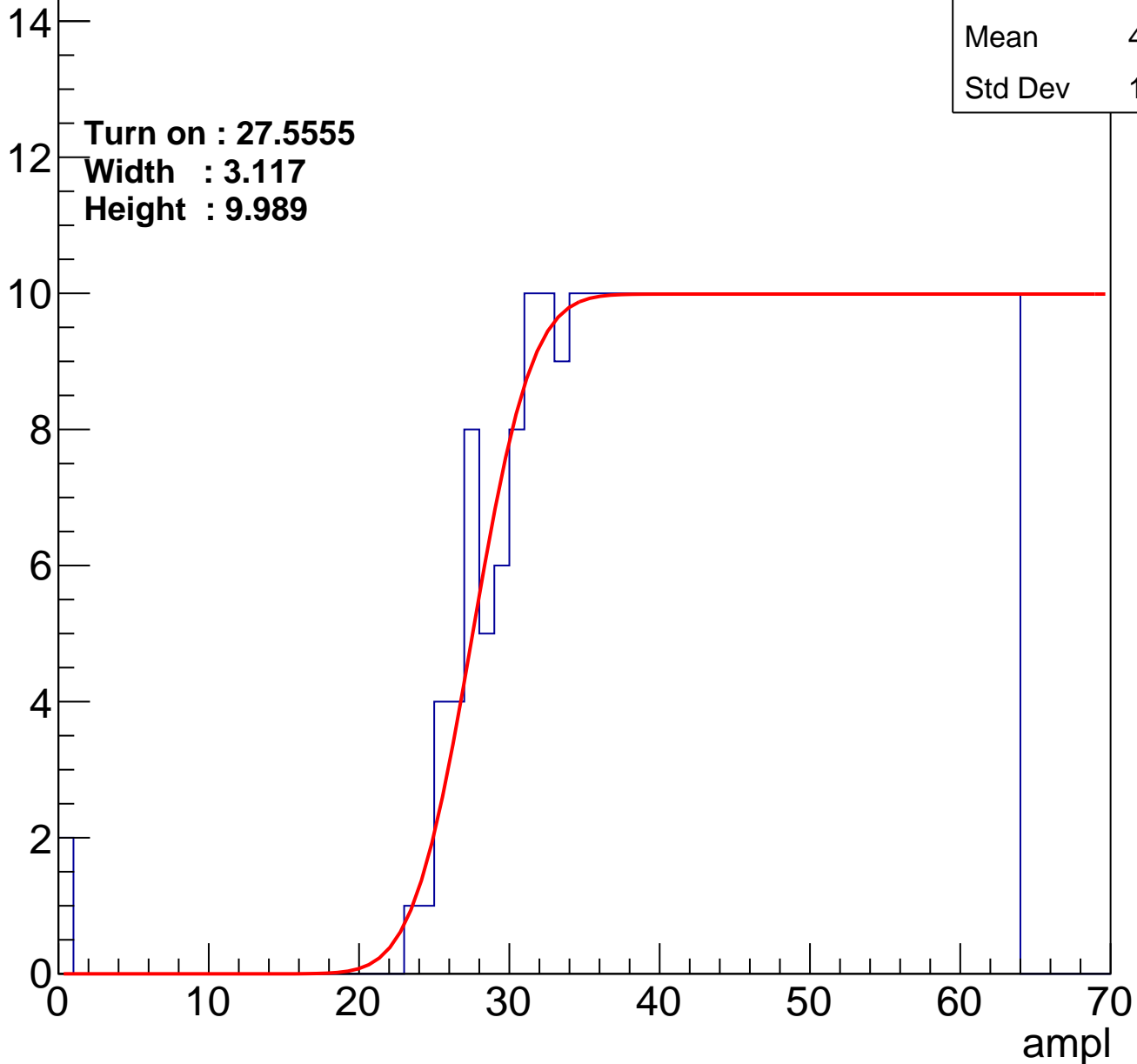
Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.5555

Width : 3.117

Height : 9.989

Entry



# B0L102S, U26-ch35

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	347
Mean	45.93
Std Dev	10.51

**Turn on : 29.9396**

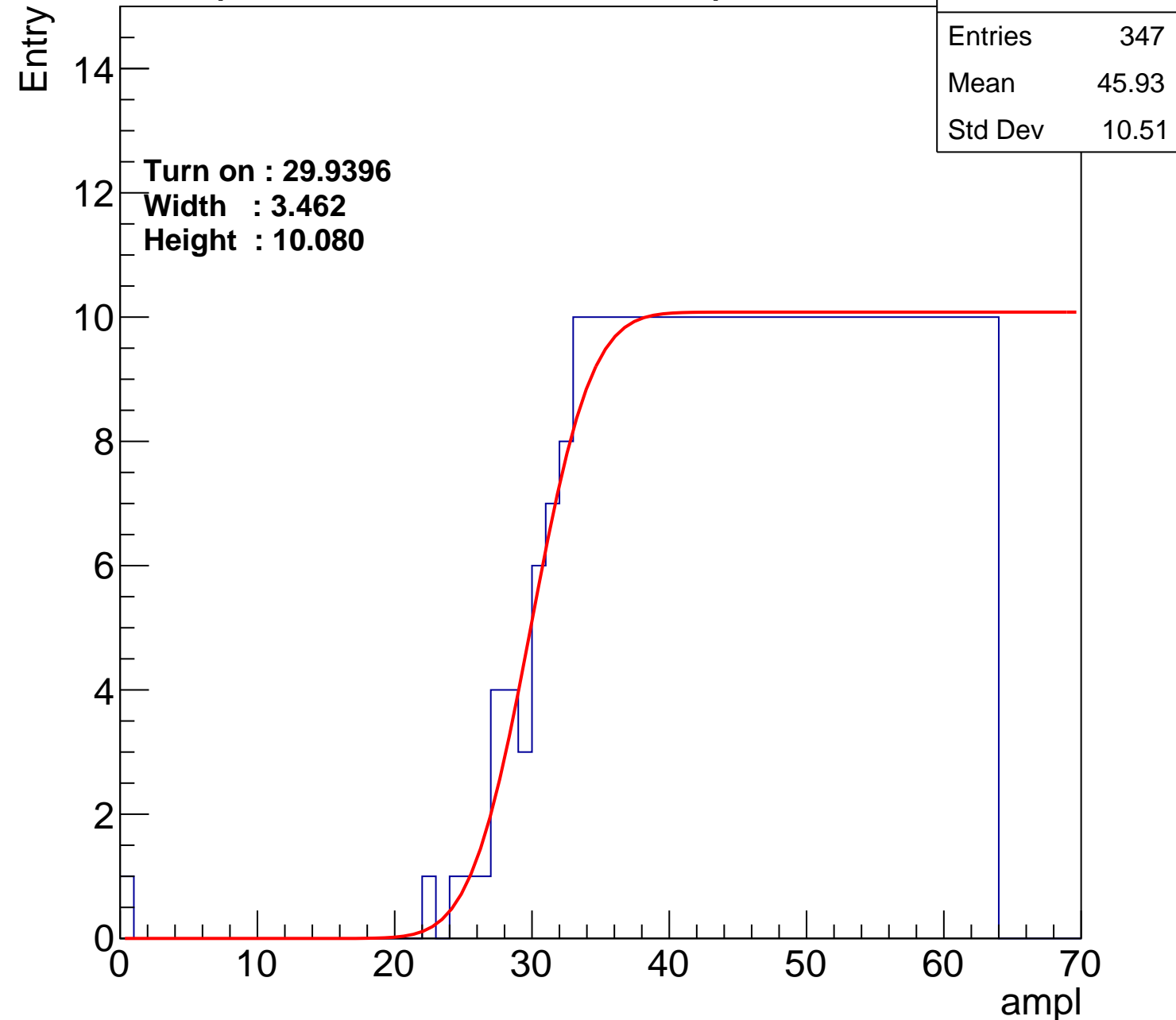
**Width : 3.462**

**Height : 10.080**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch36

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.9
Std Dev	11.04

Turn on : 27.7908

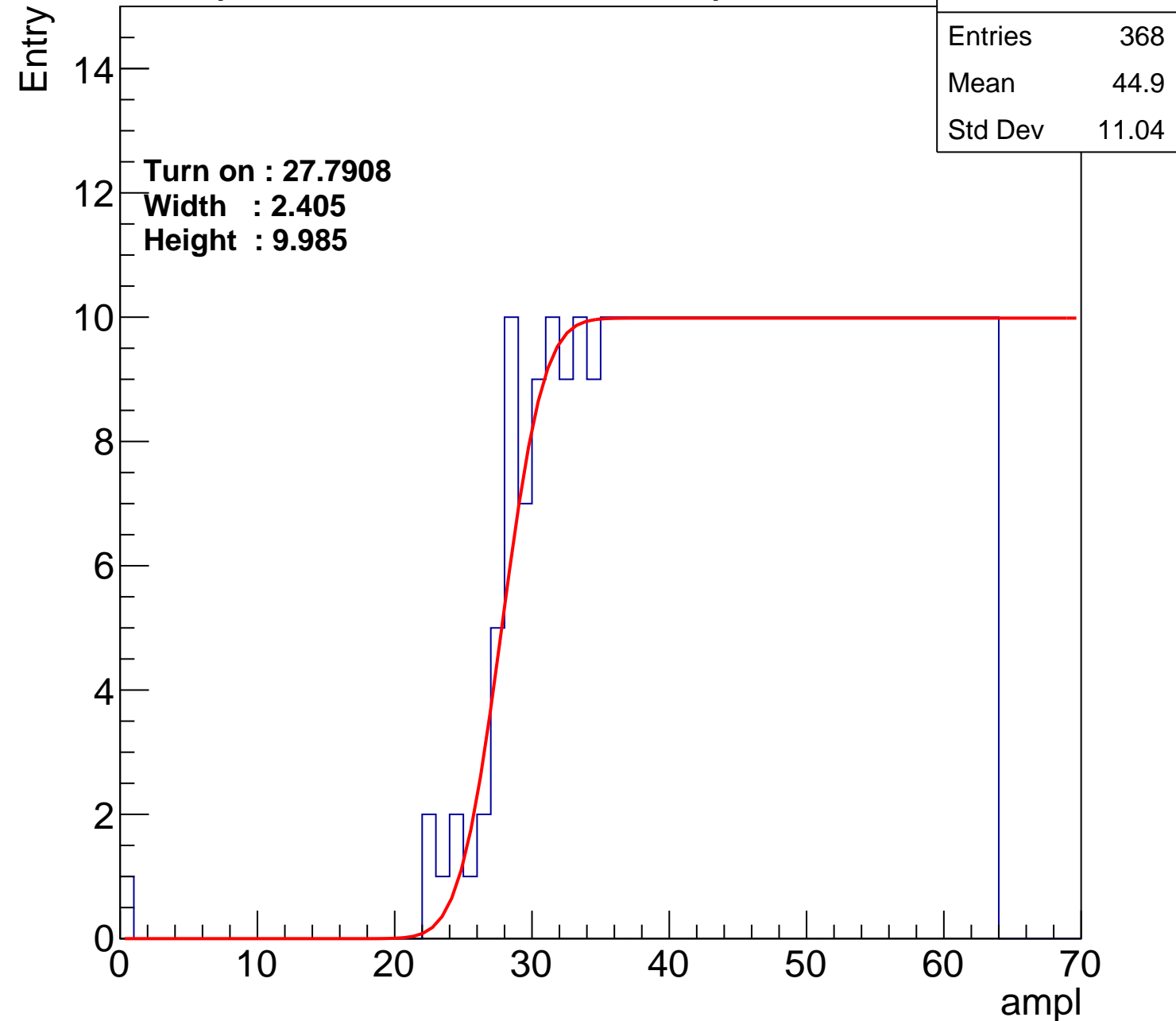
Width : 2.405

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch37

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.82
Std Dev	11.86

Turn on : 26.2207

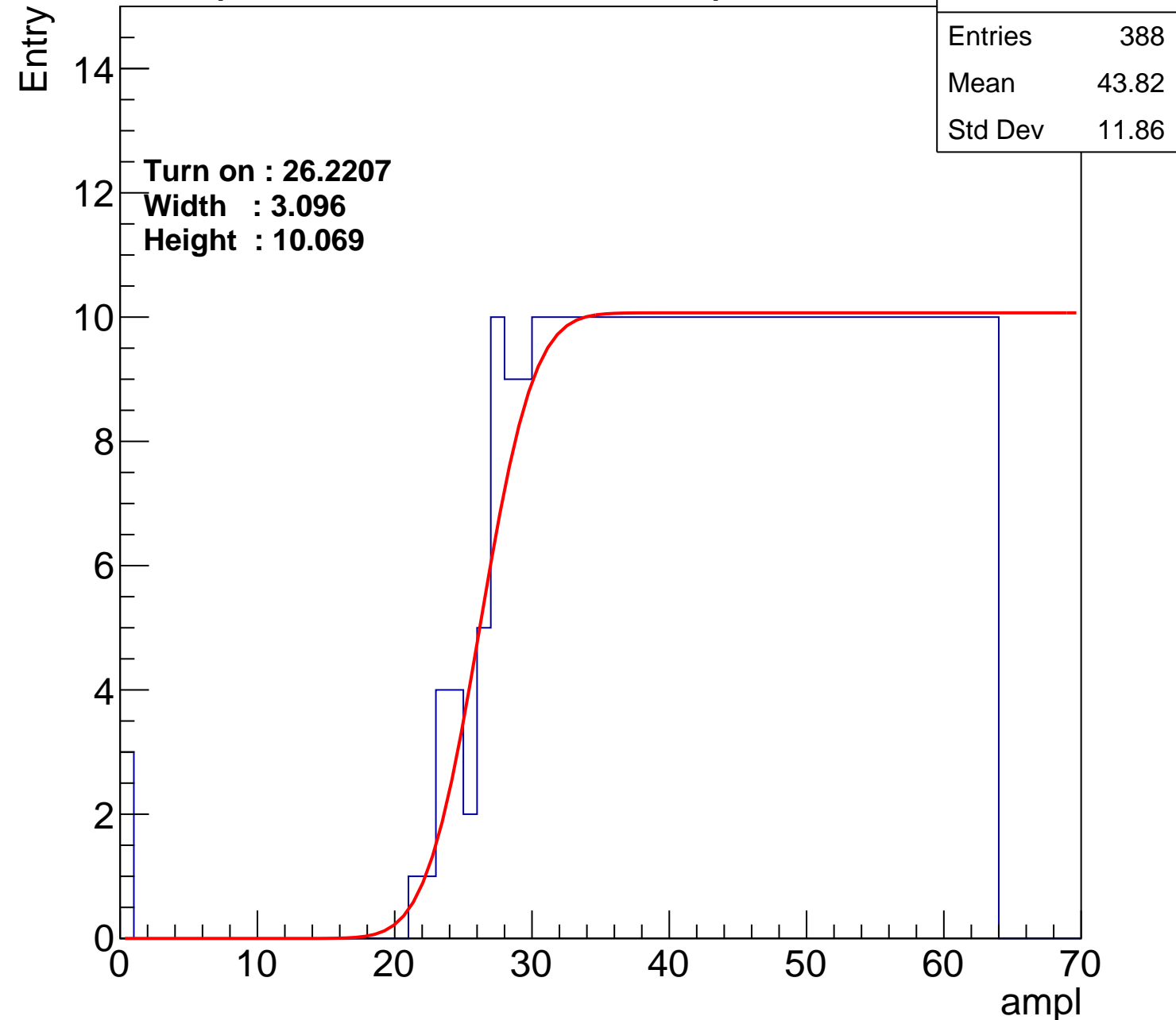
Width : 3.096

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch38

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	372
Mean	44.7
Std Dev	11.15

Turn on : 27.0429

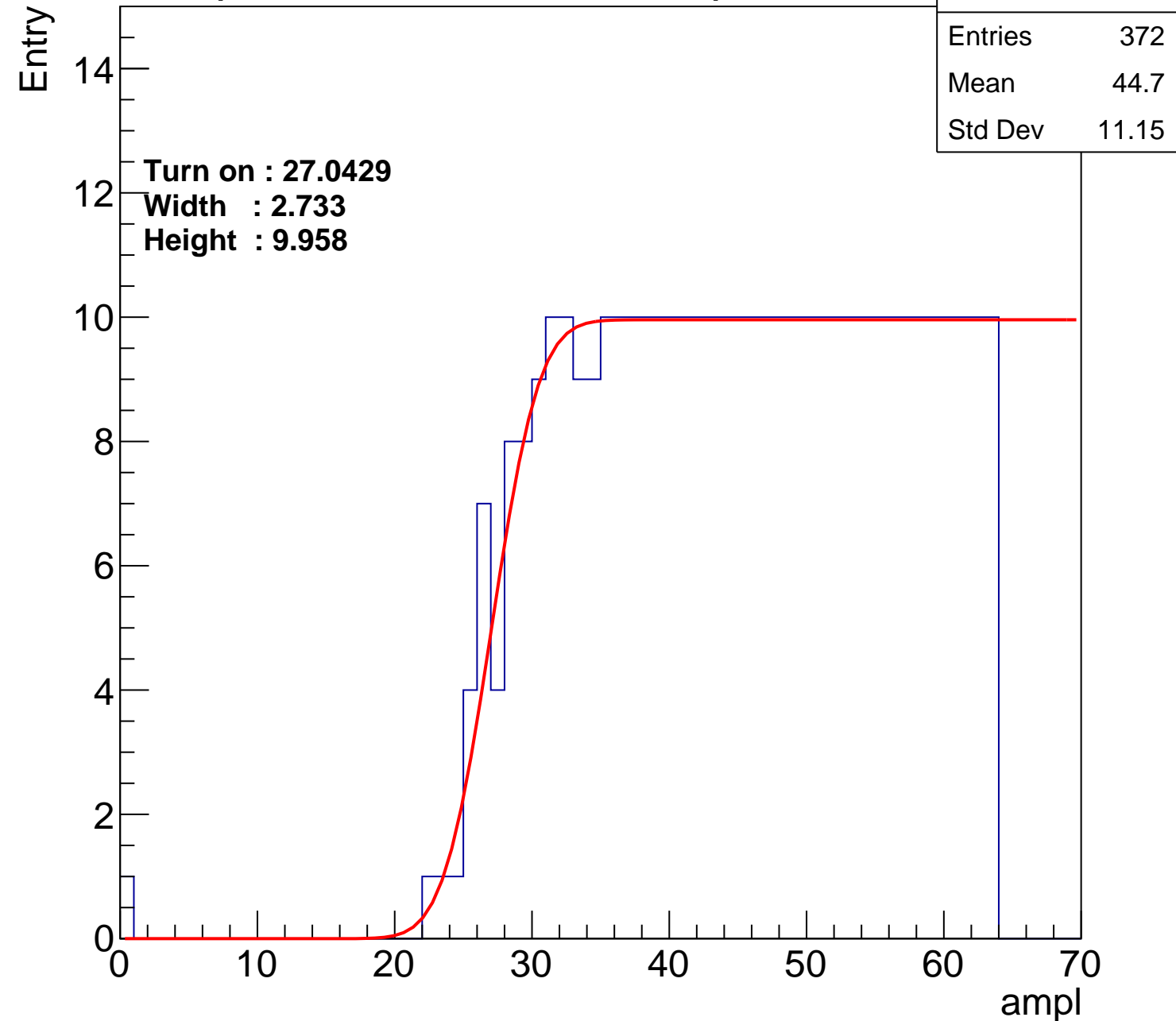
Width : 2.733

Height : 9.958

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U26-ch39

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	386
Mean	43.94
Std Dev	11.79

Turn on : 26.2631

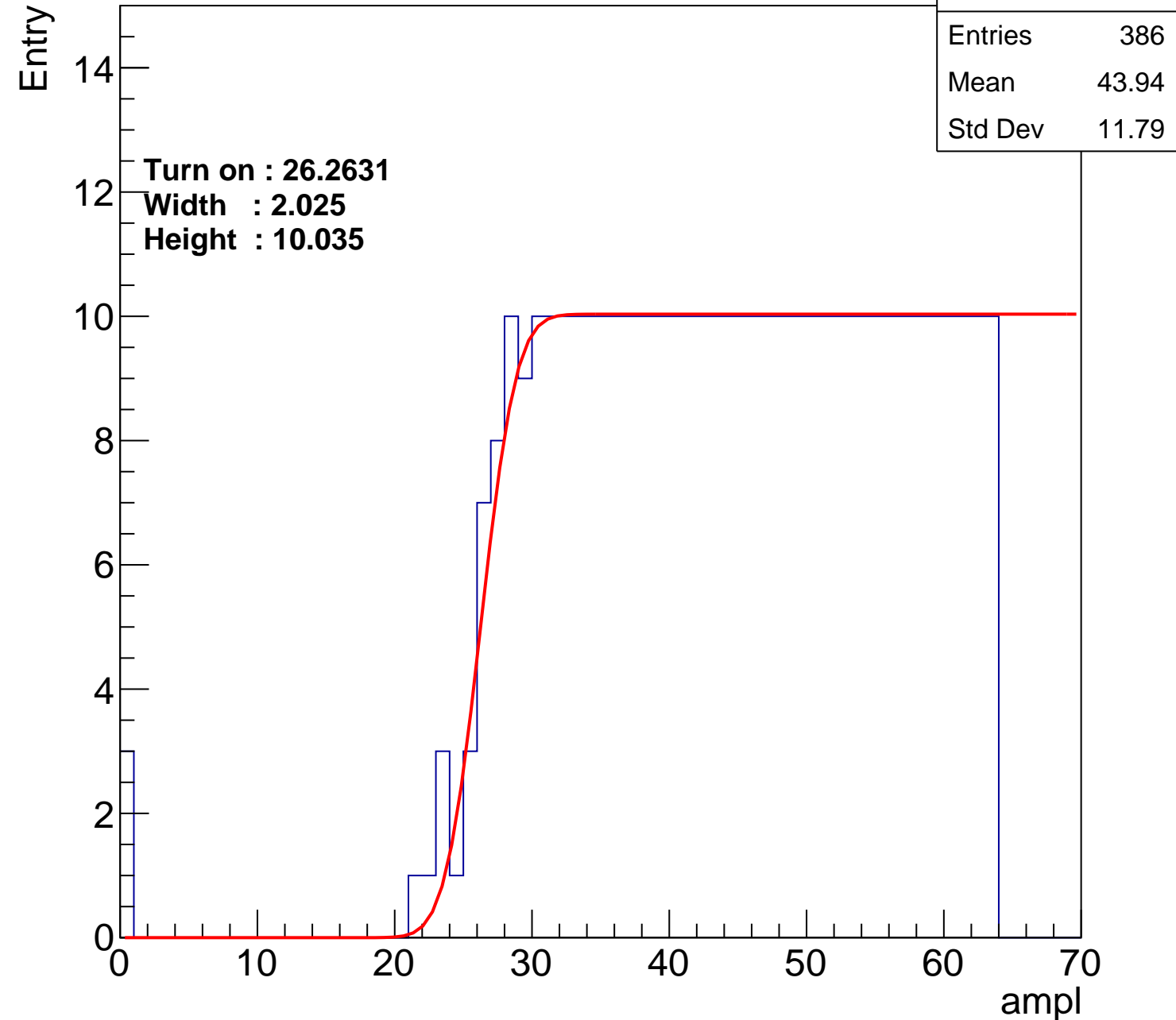
Width : 2.025

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch40

calib\_packv5\_042523\_0143.root, FC#12, port B1

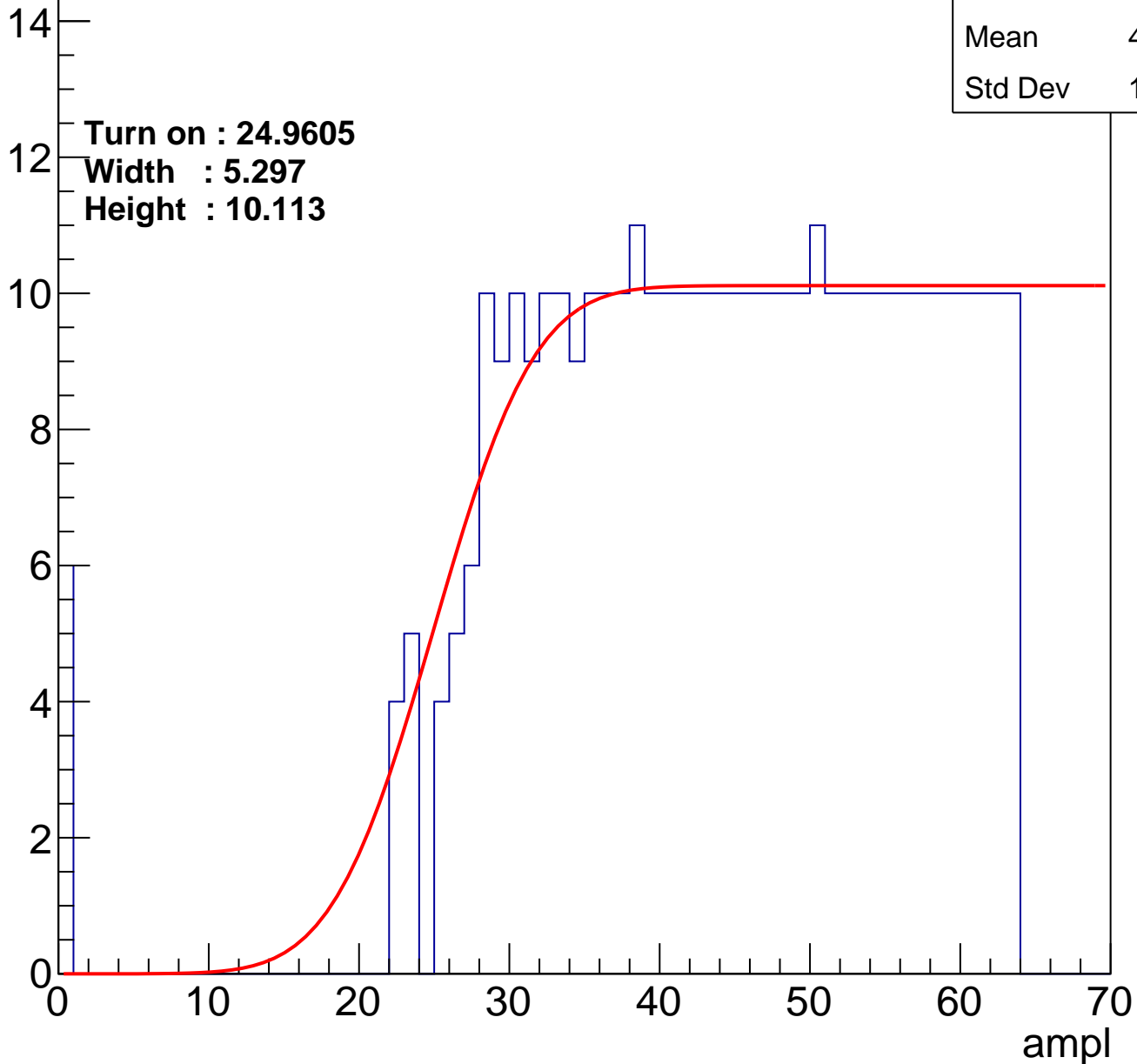
Entries	389
Mean	43.62
Std Dev	12.39

Turn on : 24.9605

Width : 5.297

Height : 10.113

Entry



# B0L102S, U26-ch41

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	372
Mean	44.62
Std Dev	11.46

Turn on : 27.1434

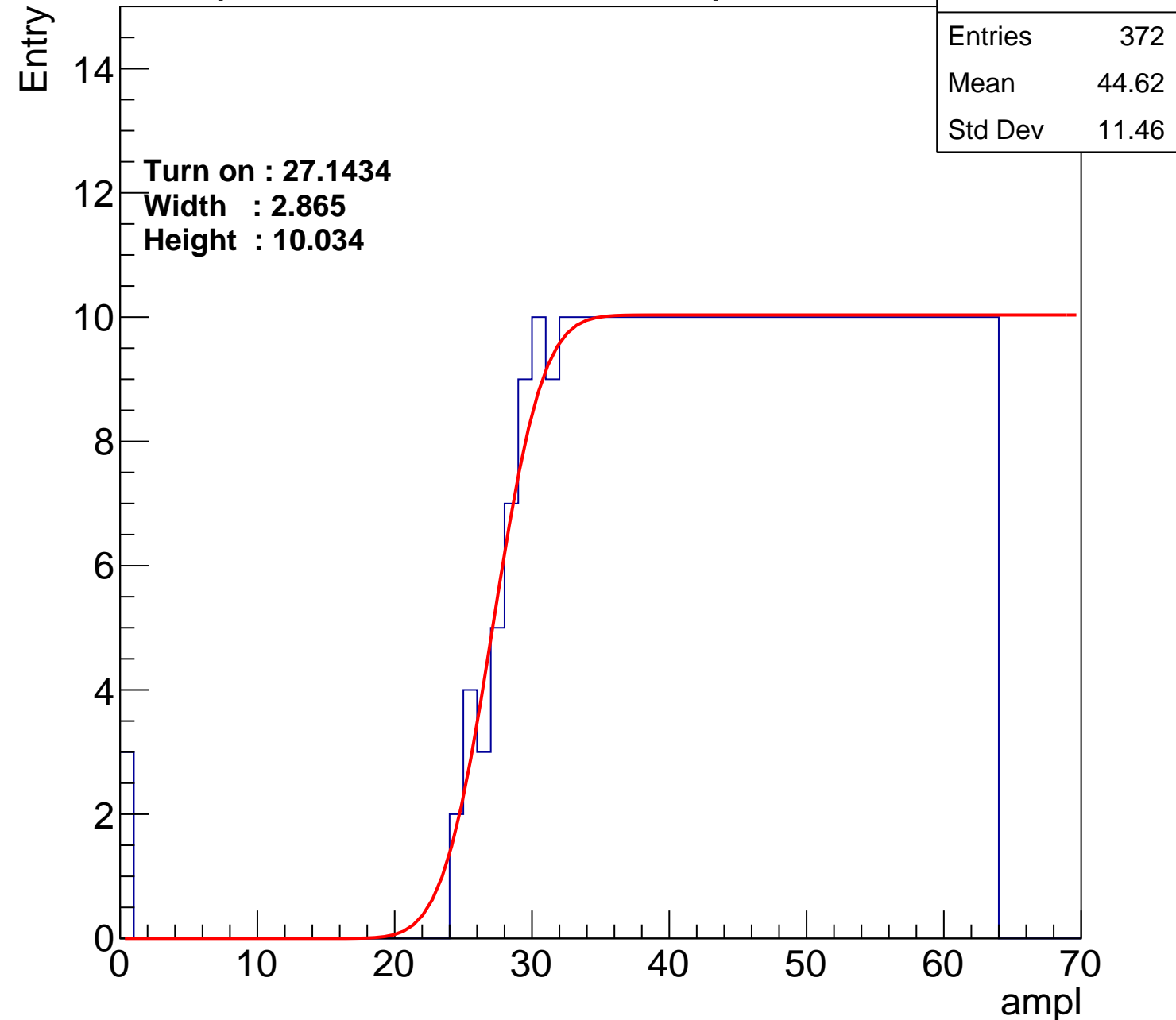
Width : 2.865

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch42

calib\_packv5\_042523\_0143.root, FC#12, port B1

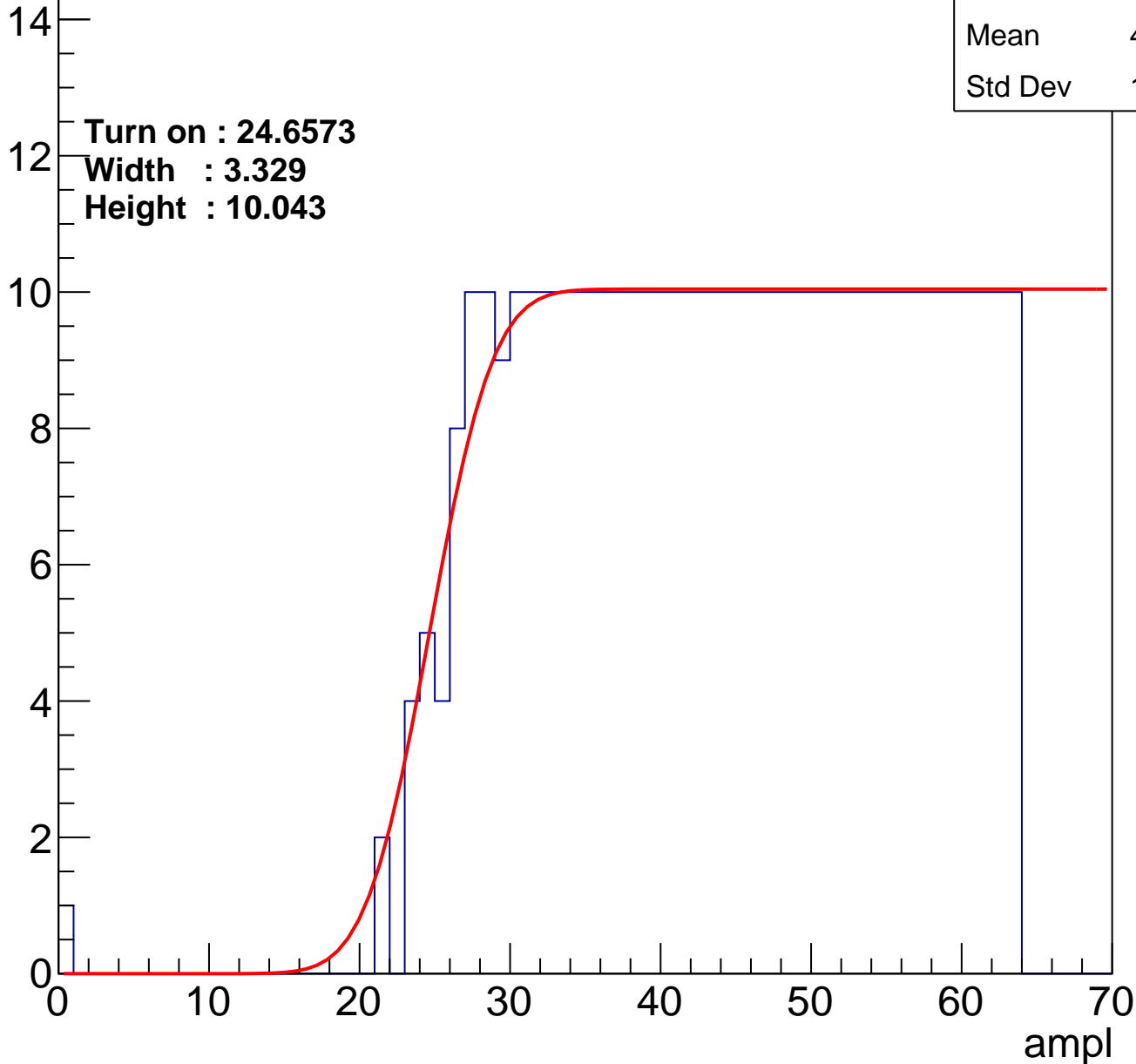
Entries	393
Mean	43.72
Std Dev	11.62

**Turn on : 24.6573**

**Width : 3.329**

**Height : 10.043**

Entry



# B0L102S, U26-ch43

calib\_packv5\_042523\_0143.root, FC#12, port B1

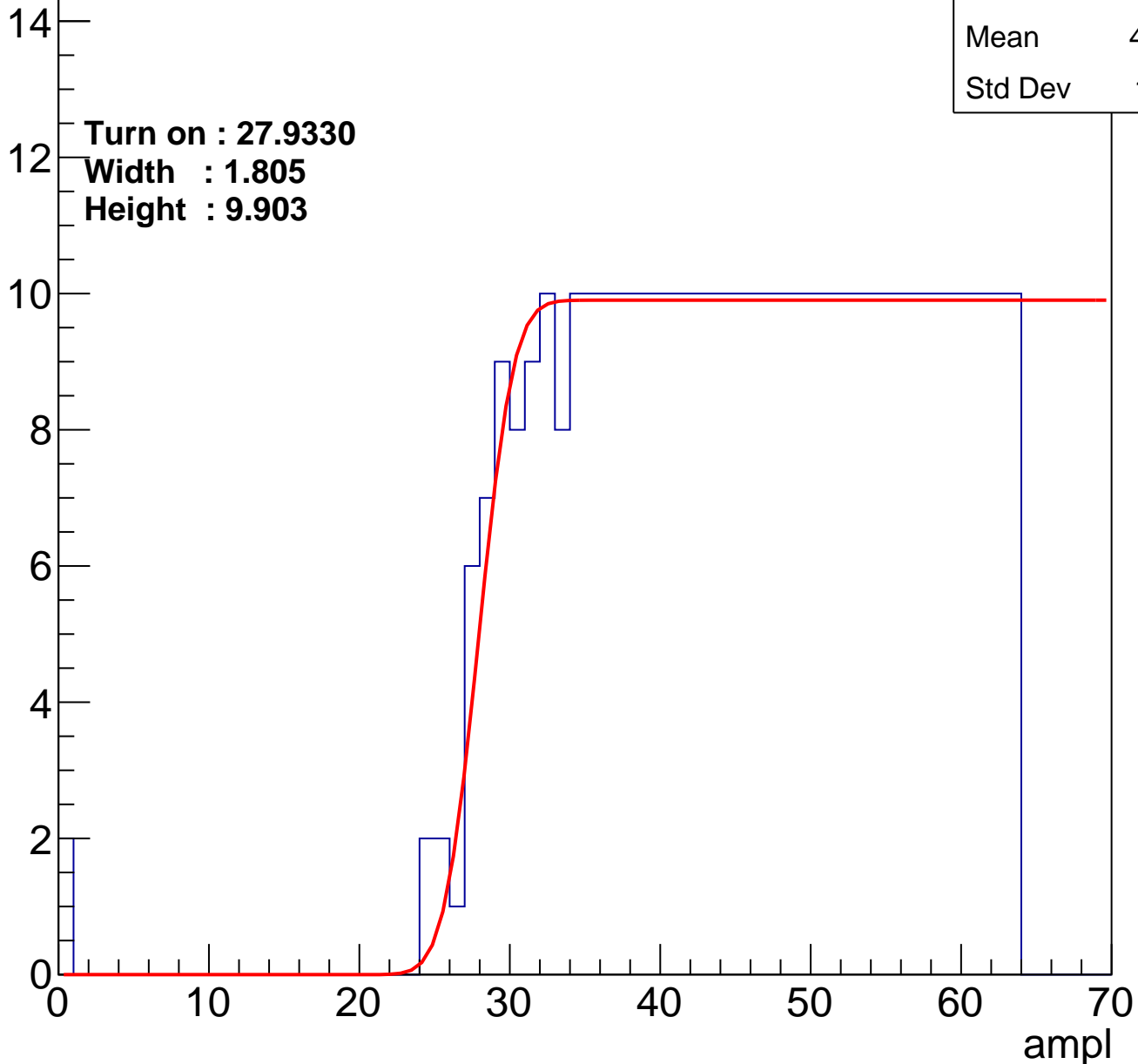
Entries	364
Mean	45.04
Std Dev	11.11

Turn on : 27.9330

Width : 1.805

Height : 9.903

Entry



# B0L102S, U26-ch44

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	403
Mean	43.21
Std Dev	11.92

Turn on : 23.7089

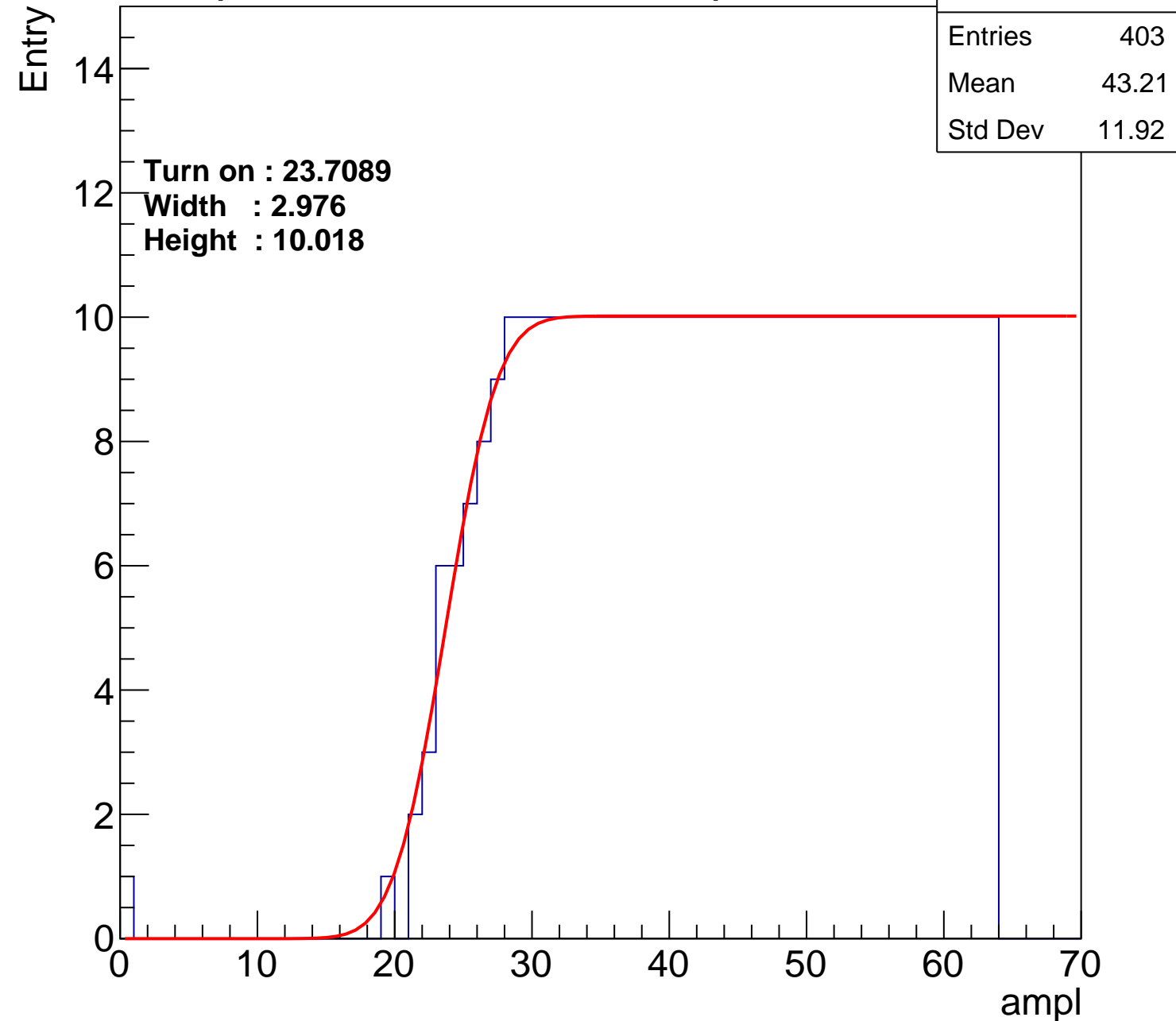
Width : 2.976

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch45

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	396
Mean	43.37
Std Dev	12.22

**Turn on : 24.8095**

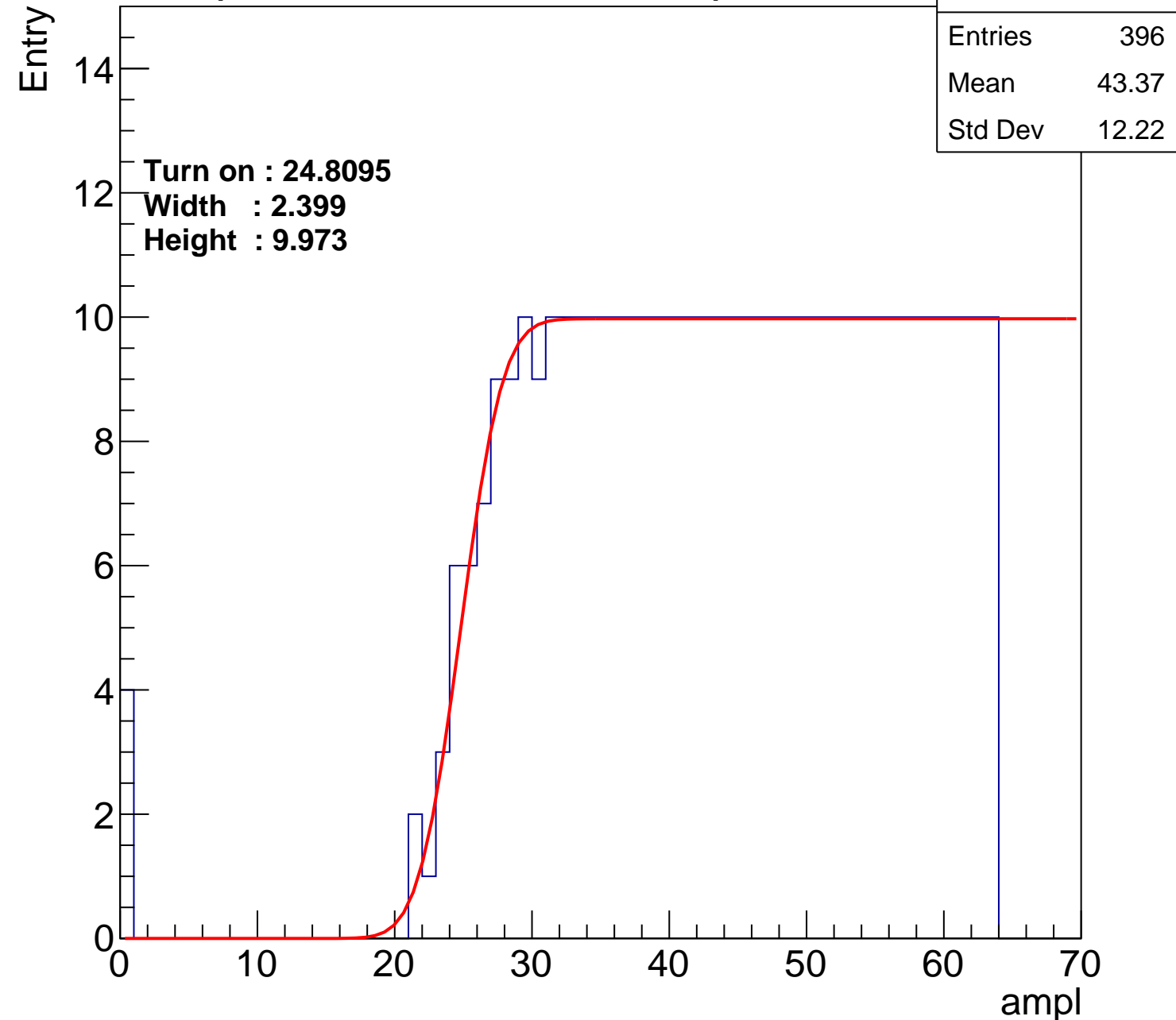
**Width : 2.399**

**Height : 9.973**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch46

calib\_packv5\_042523\_0143.root, FC#12, port B1

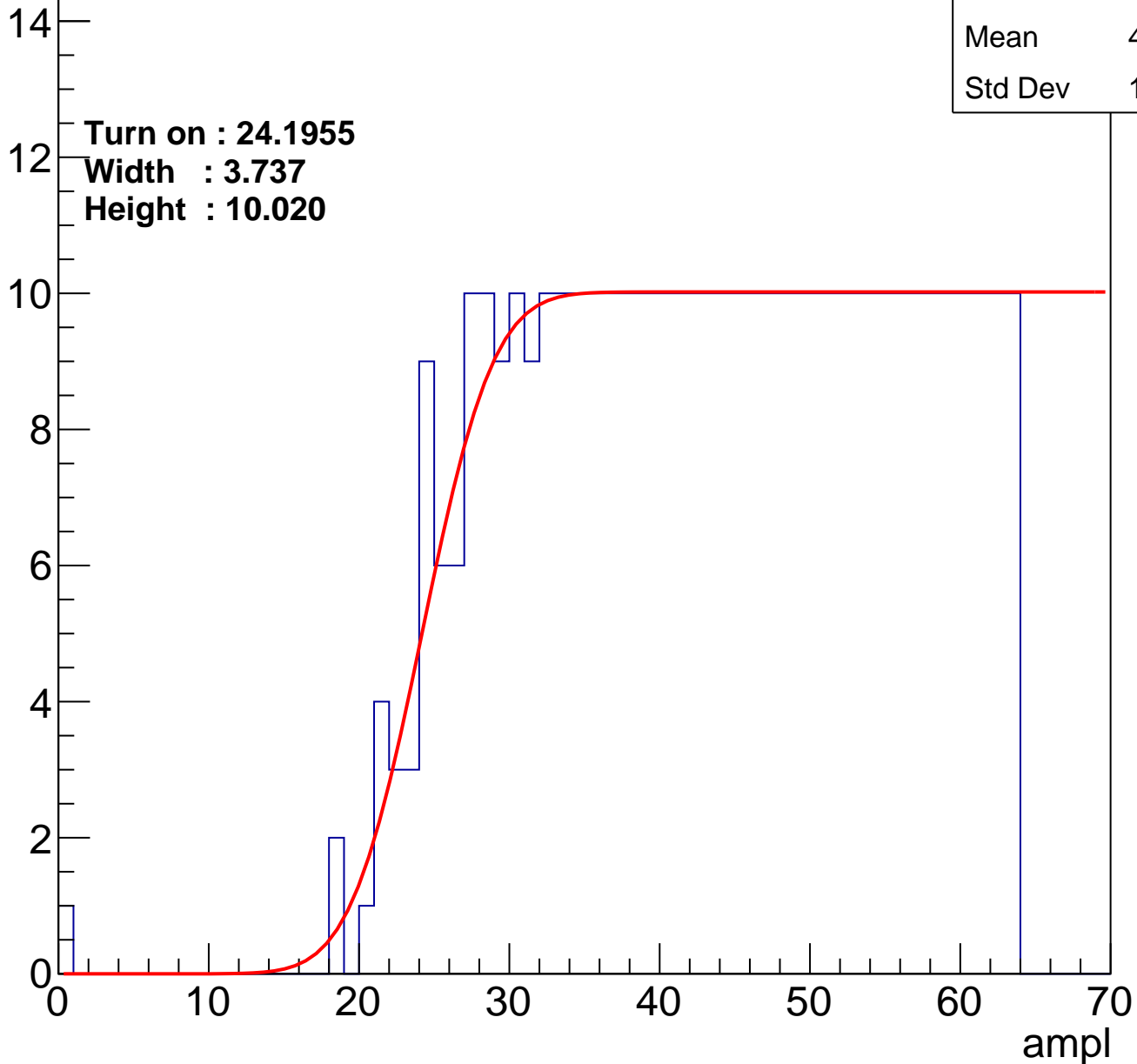
Entries	403
Mean	43.14
Std Dev	12.03

Turn on : 24.1955

Width : 3.737

Height : 10.020

Entry





# B0L102S, U26-ch47

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 27.0788

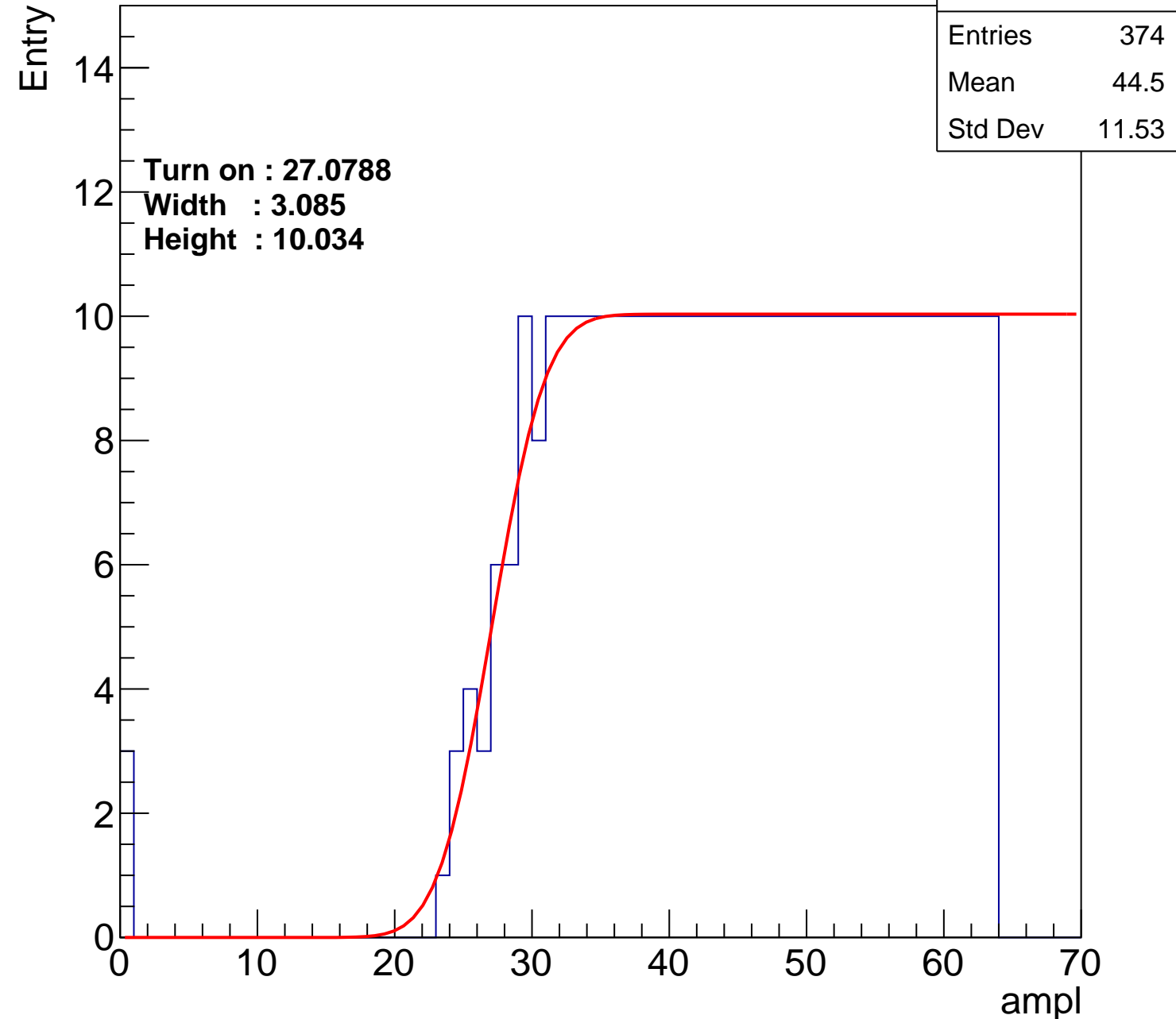
Width : 3.085

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#12, port B1**

**calib\_packv5\_042523\_0143.root, FC#12, port B1**

Turn on : 27.6209  
Width : 3.539  
Height : 10.033



# B0L102S, U26-ch49

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.8172

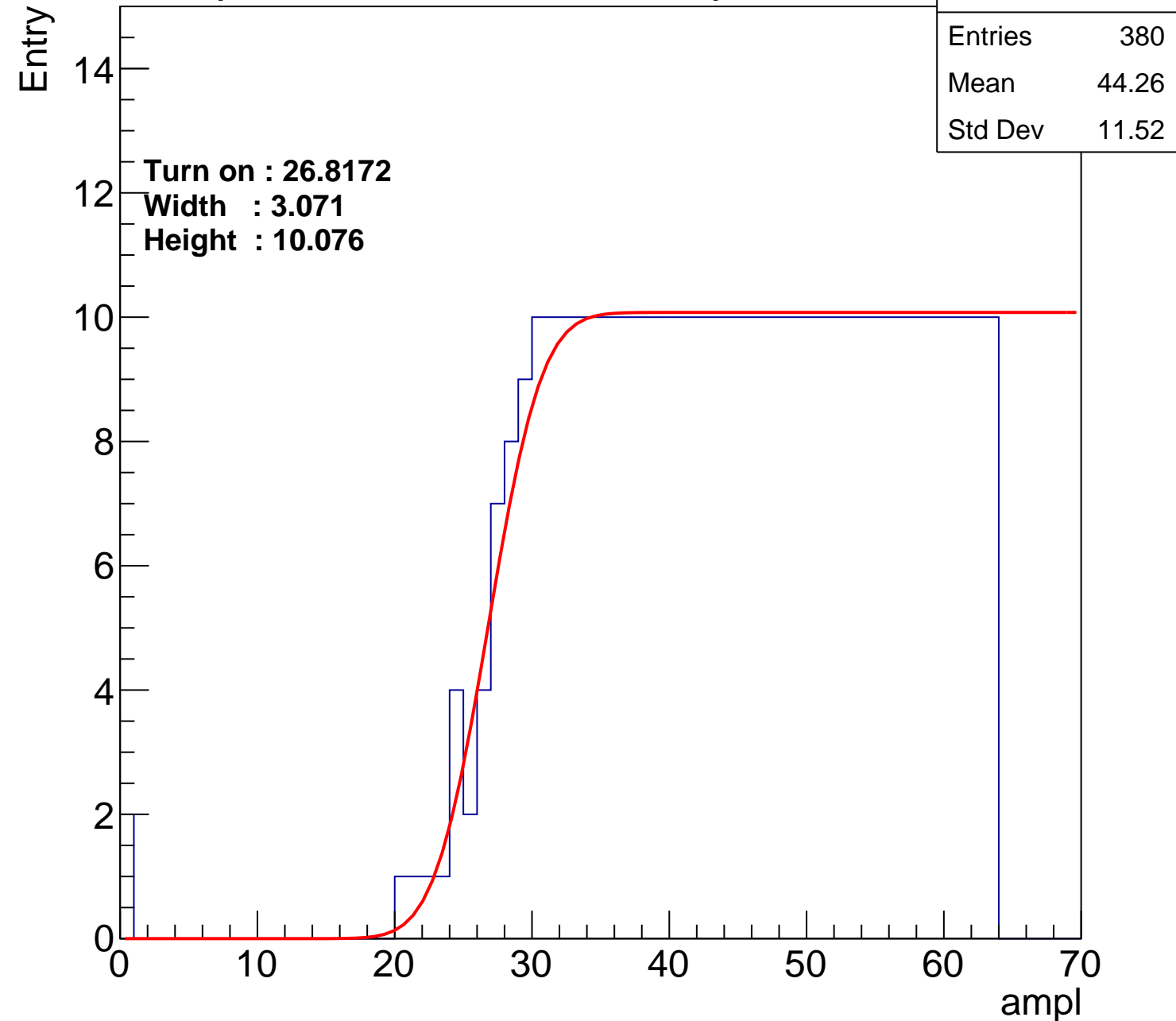
Width : 3.071

Height : 10.076

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch50

calib\_packv5\_042523\_0143.root, FC#12, port B1

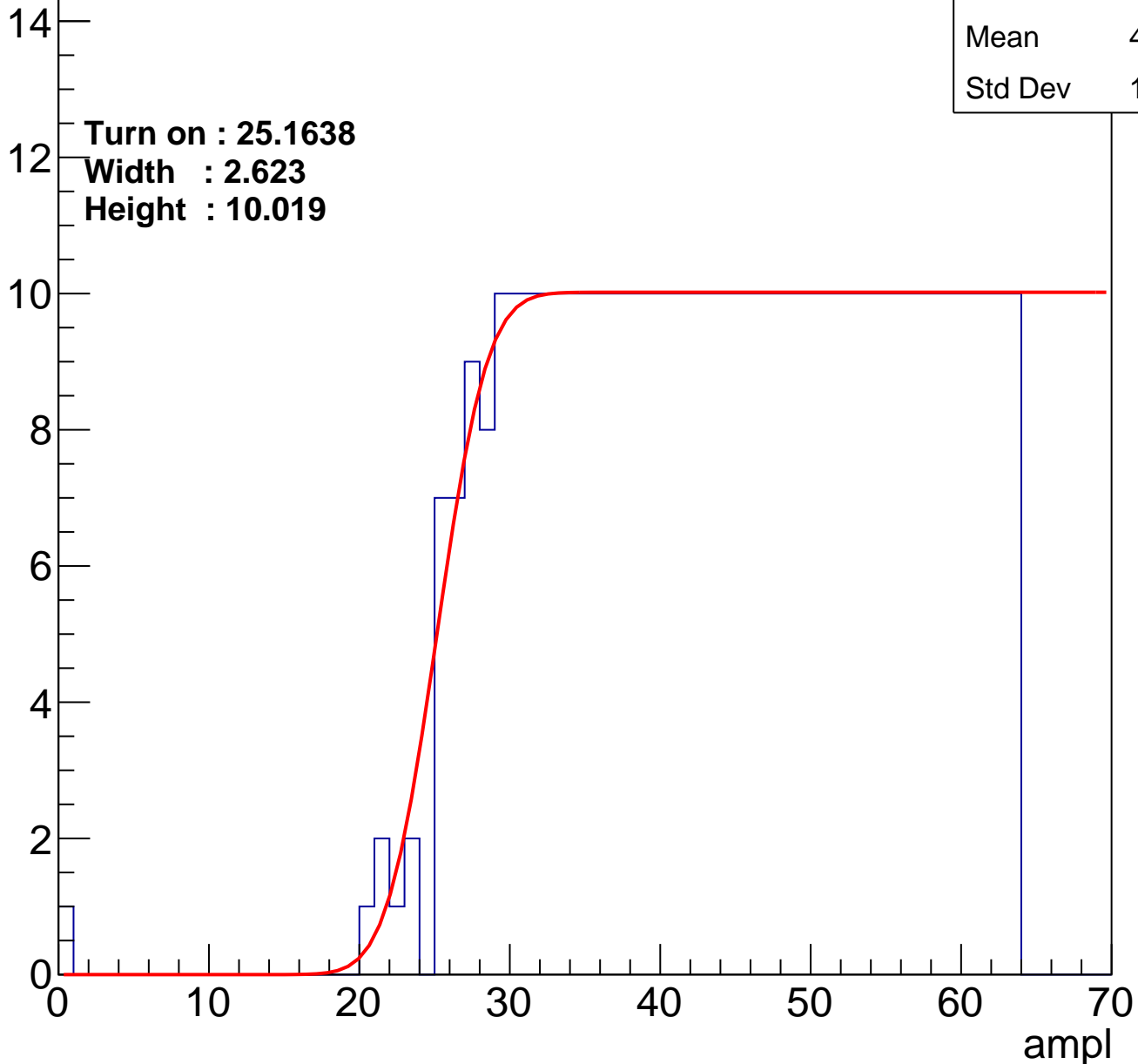
Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 25.1638

Width : 2.623

Height : 10.019

Entry



# B0L102S, U26-ch51

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.81
Std Dev	11.83

**Turn on : 25.4355**

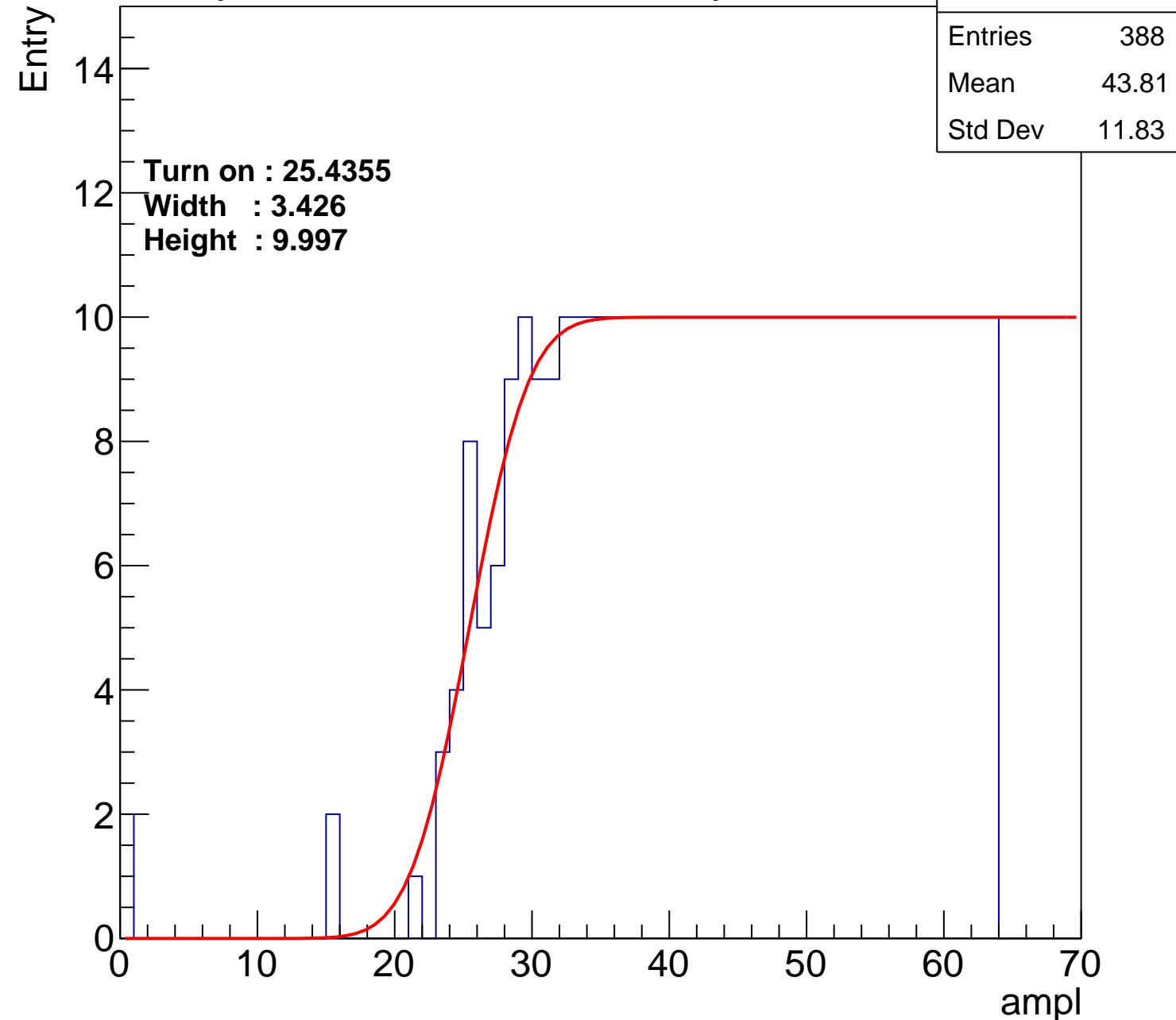
**Width : 3.426**

**Height : 9.997**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch52

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.7
Std Dev	11.57

**Turn on : 27.7959**

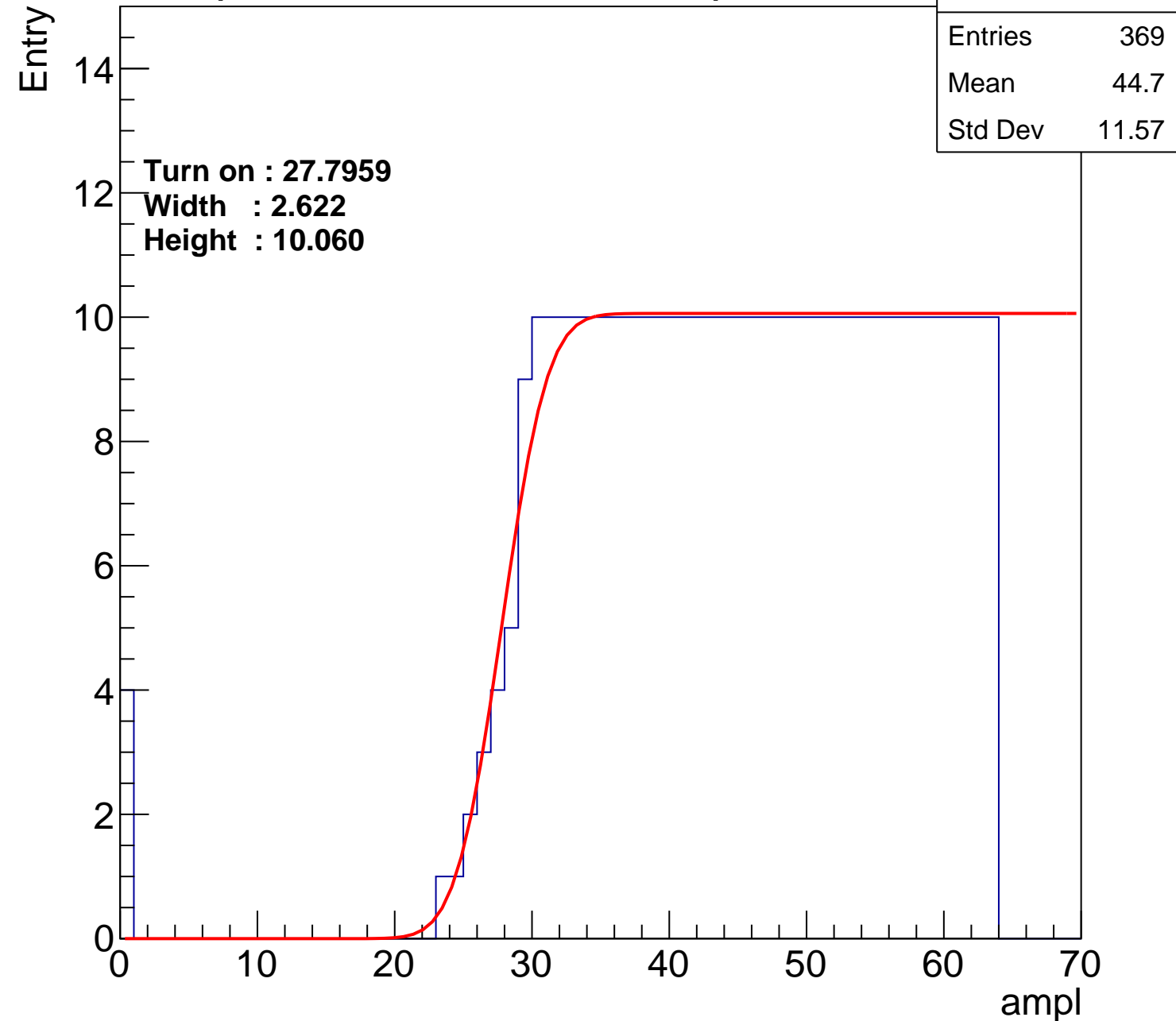
**Width : 2.622**

**Height : 10.060**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch53

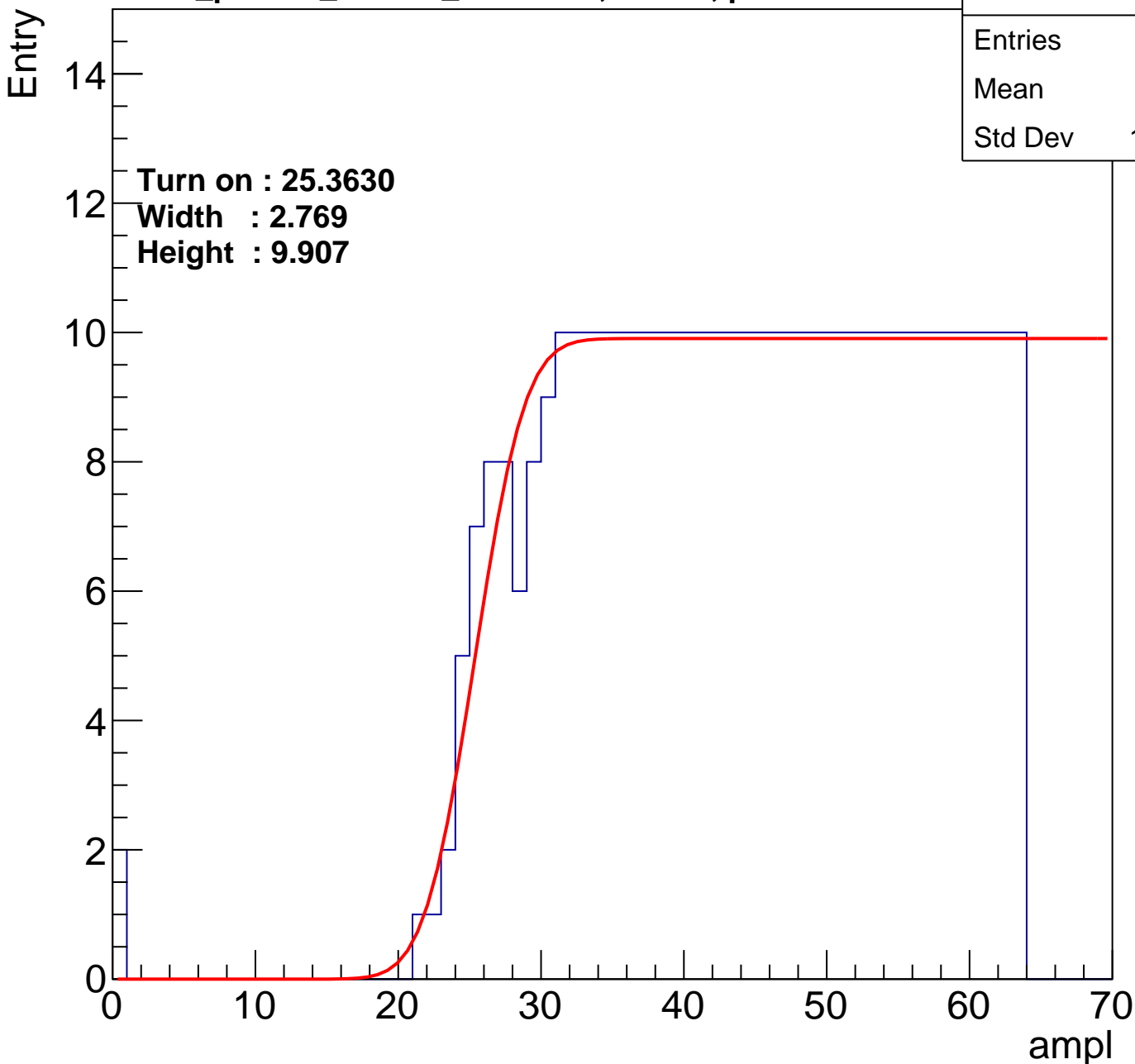
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

**Turn on : 25.3630**

**Width : 2.769**

**Height : 9.907**

Entries	387
Mean	43.9
Std Dev	11.72



# B0L102S, U26-ch54

calib\_packv5\_042523\_0143.root, FC#12, port B1

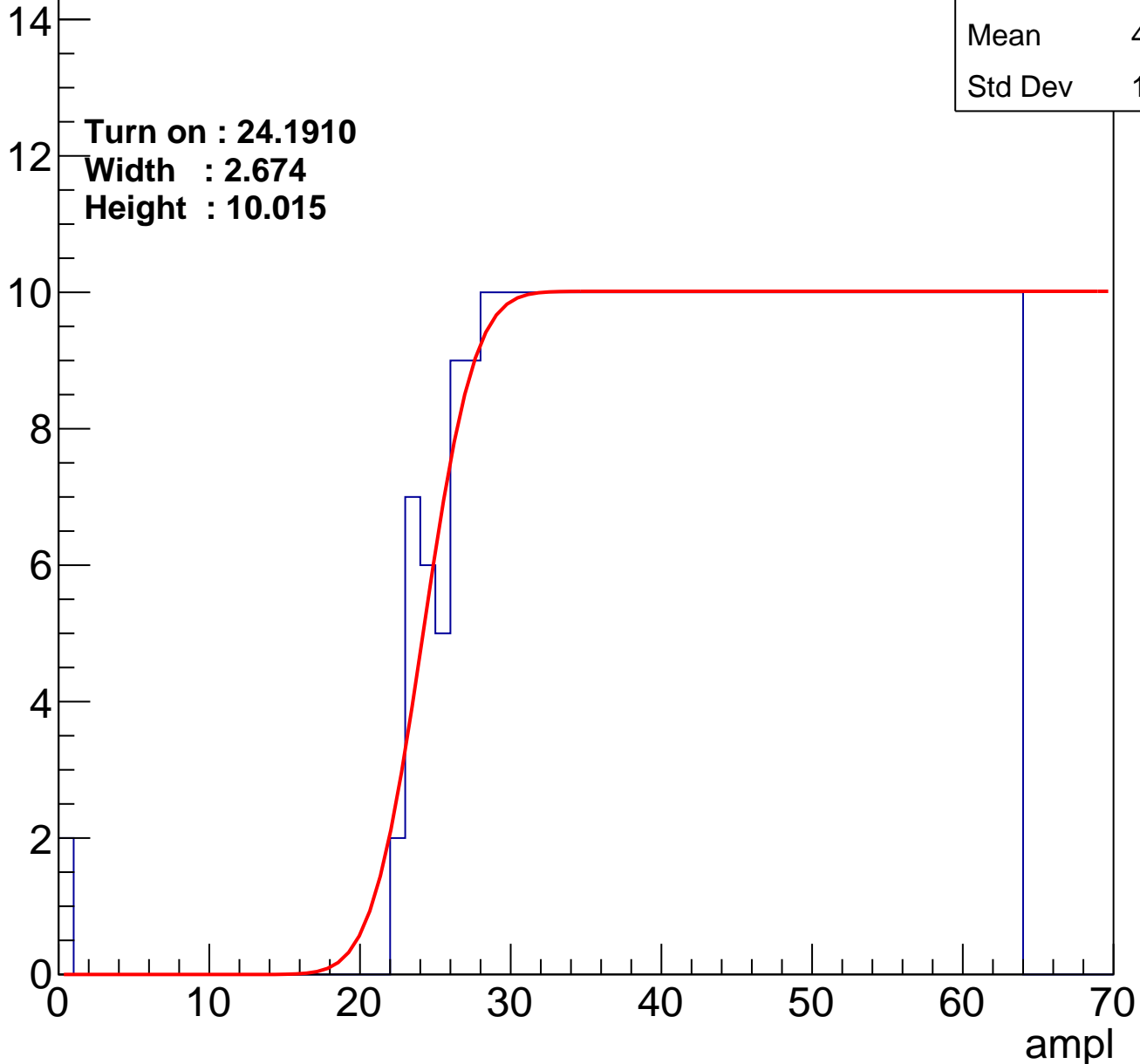
Entries	400
Mean	43.33
Std Dev	11.95

Turn on : 24.1910

Width : 2.674

Height : 10.015

Entry





# B0L102S, U26-ch55

calib\_packv5\_042523\_0143.root, FC#12, port B1

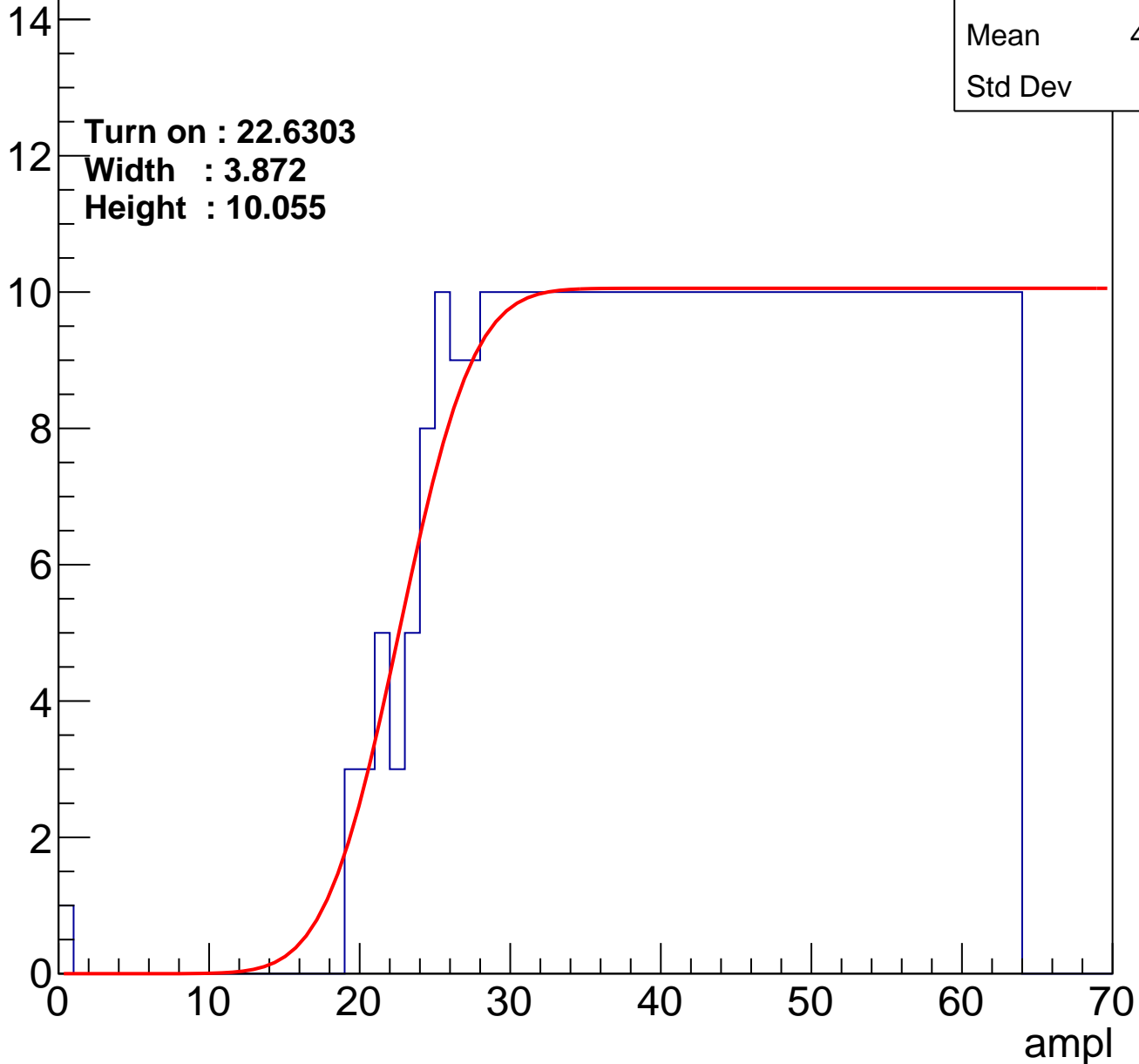
Entries	416
Mean	42.55
Std Dev	12.3

Turn on : 22.6303

Width : 3.872

Height : 10.055

Entry



# B0L102S, U26-ch56

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.97
Std Dev	11.23

Turn on : 27.9950

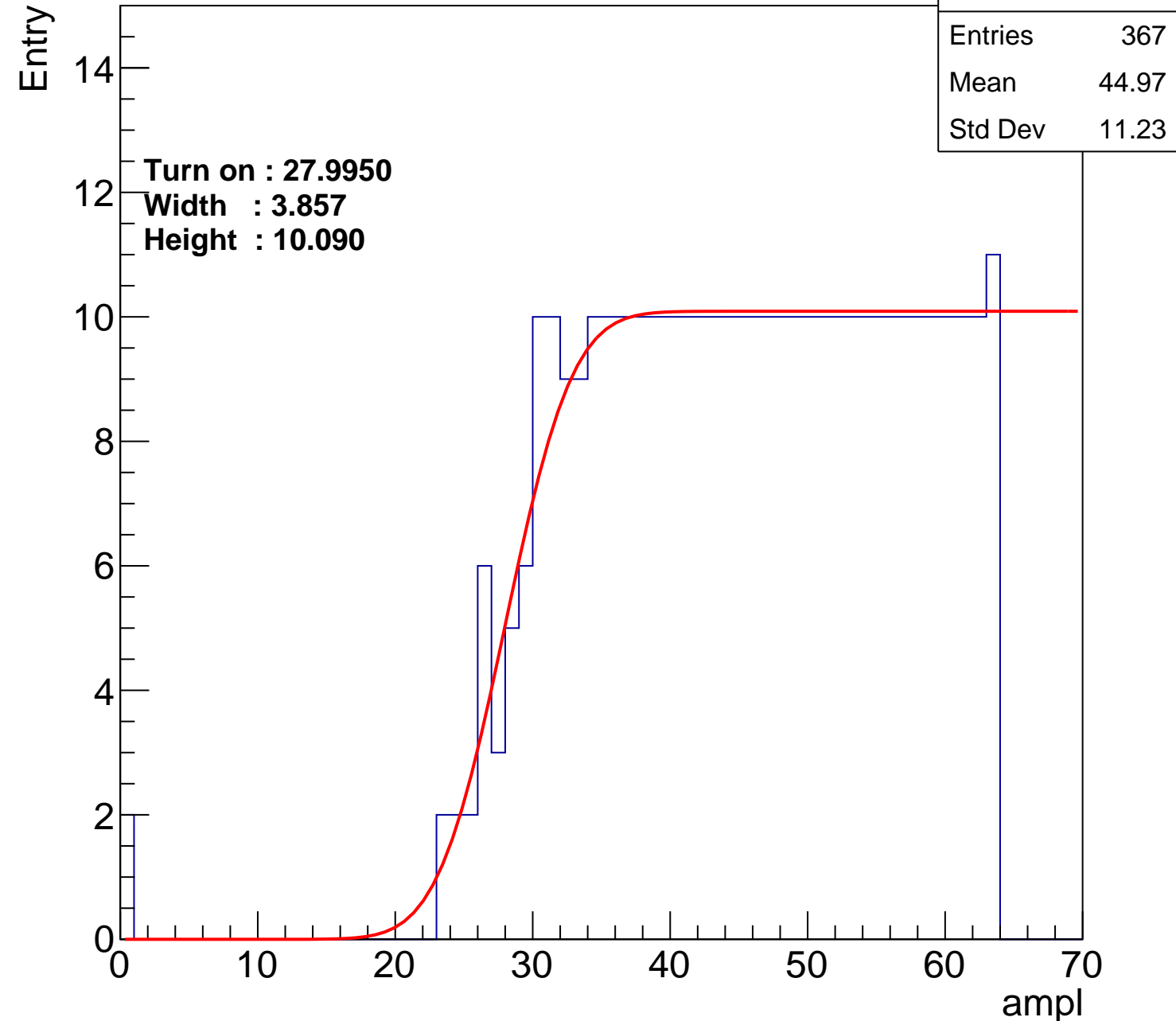
Width : 3.857

Height : 10.090

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch57

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.74
Std Dev	12.05

Turn on : 25.6569

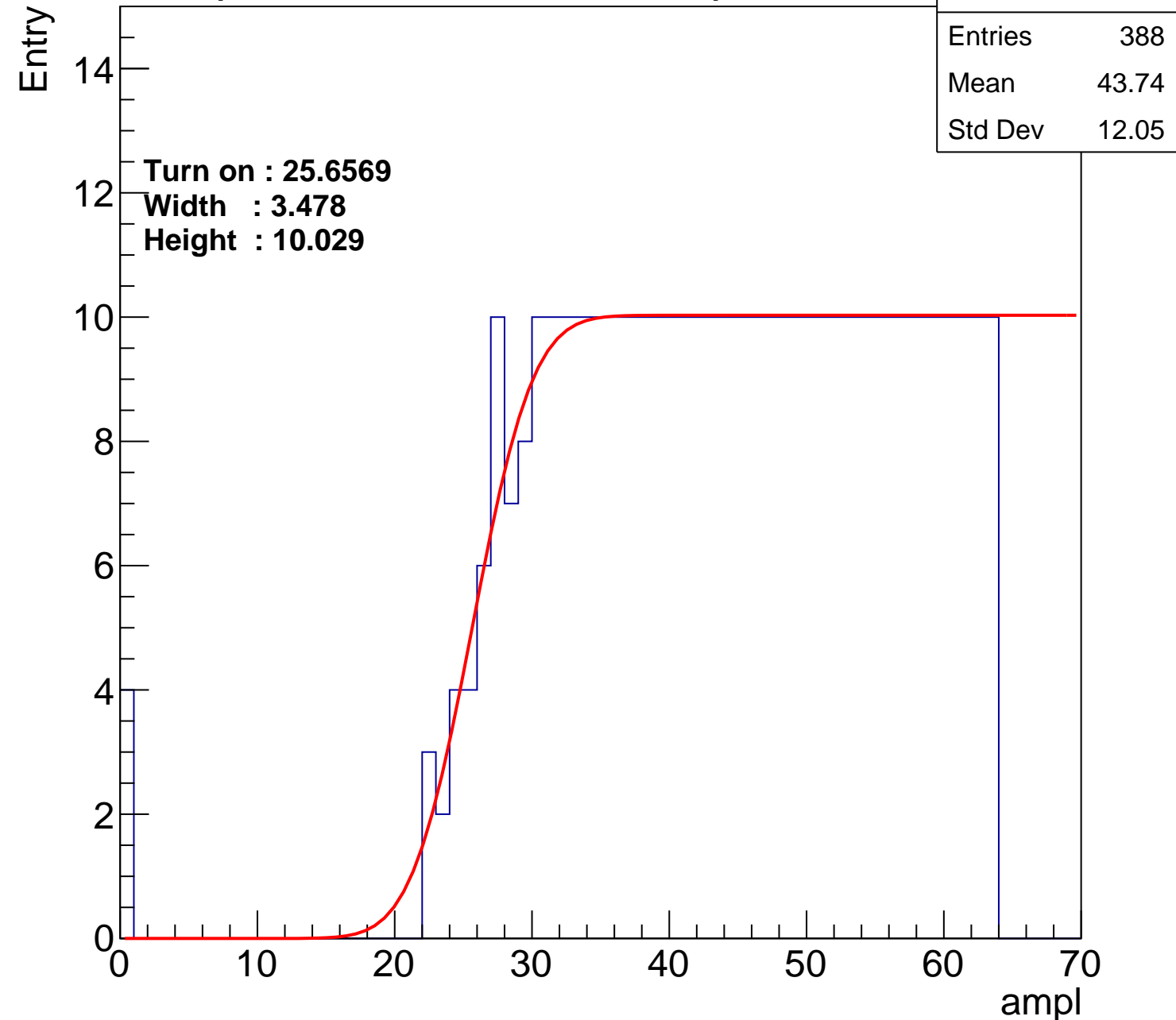
Width : 3.478

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch58

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.0502

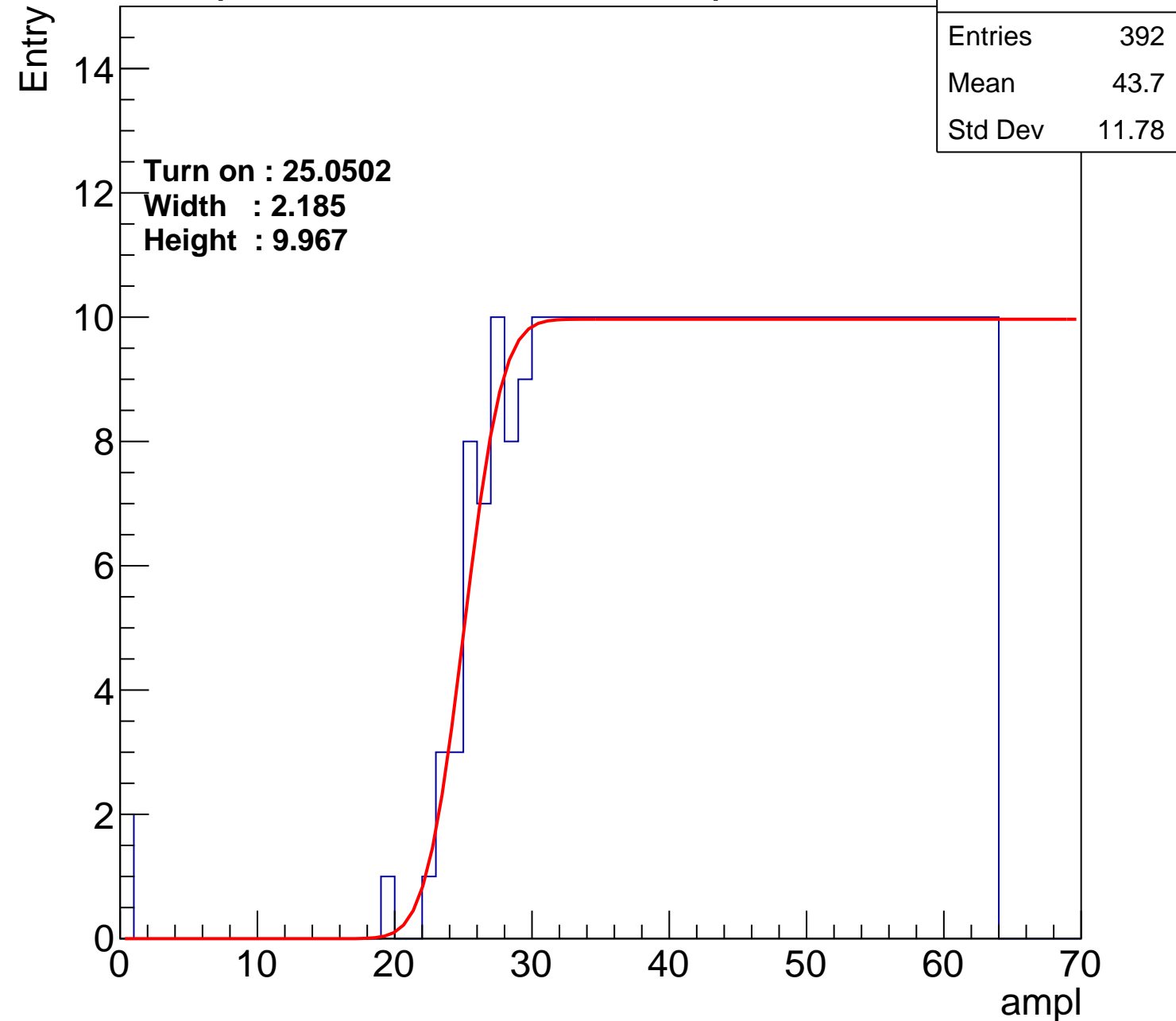
Width : 2.185

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch59

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.59
Std Dev	11.31

Turn on : 26.7802

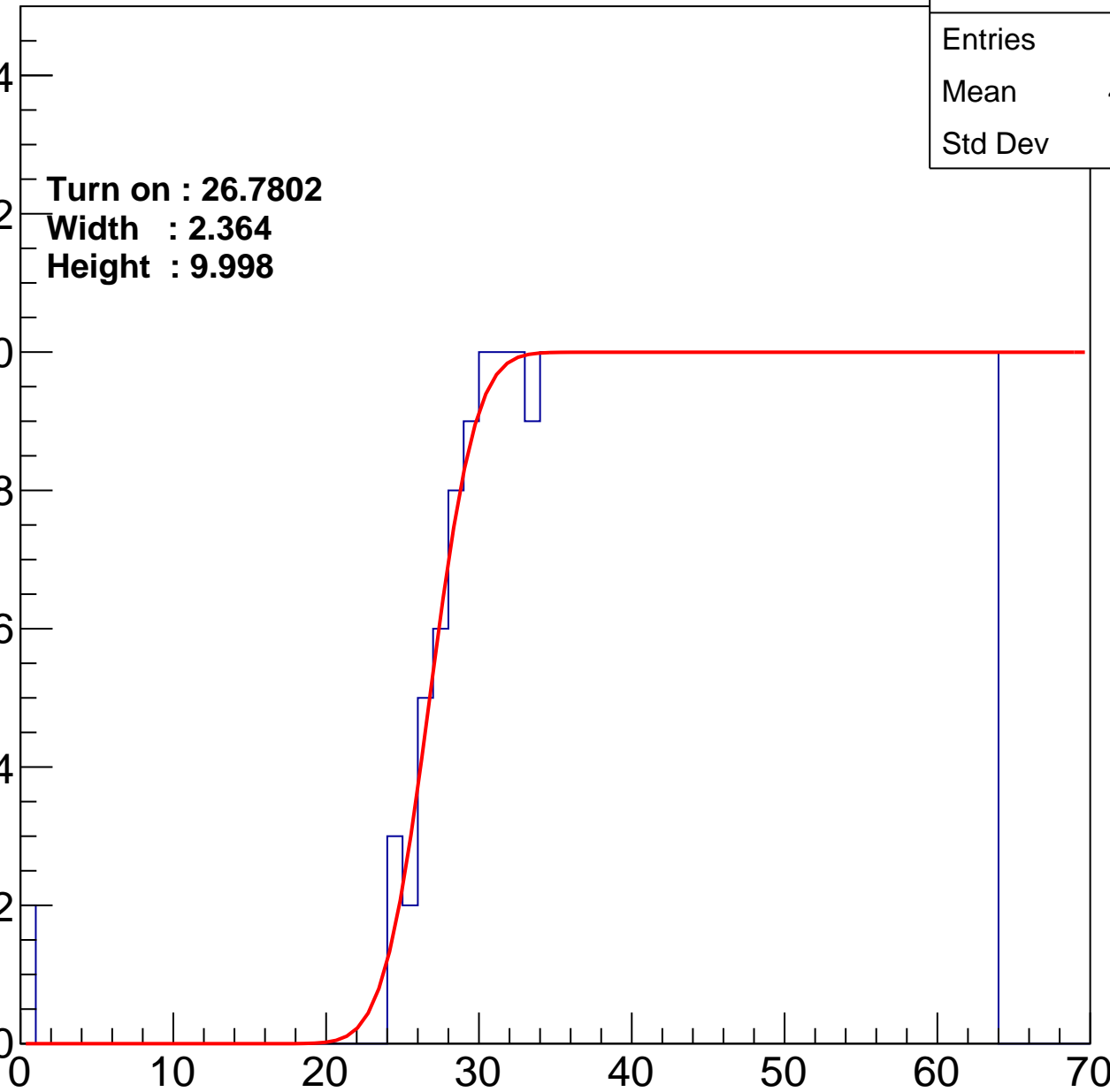
Width : 2.364

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch60

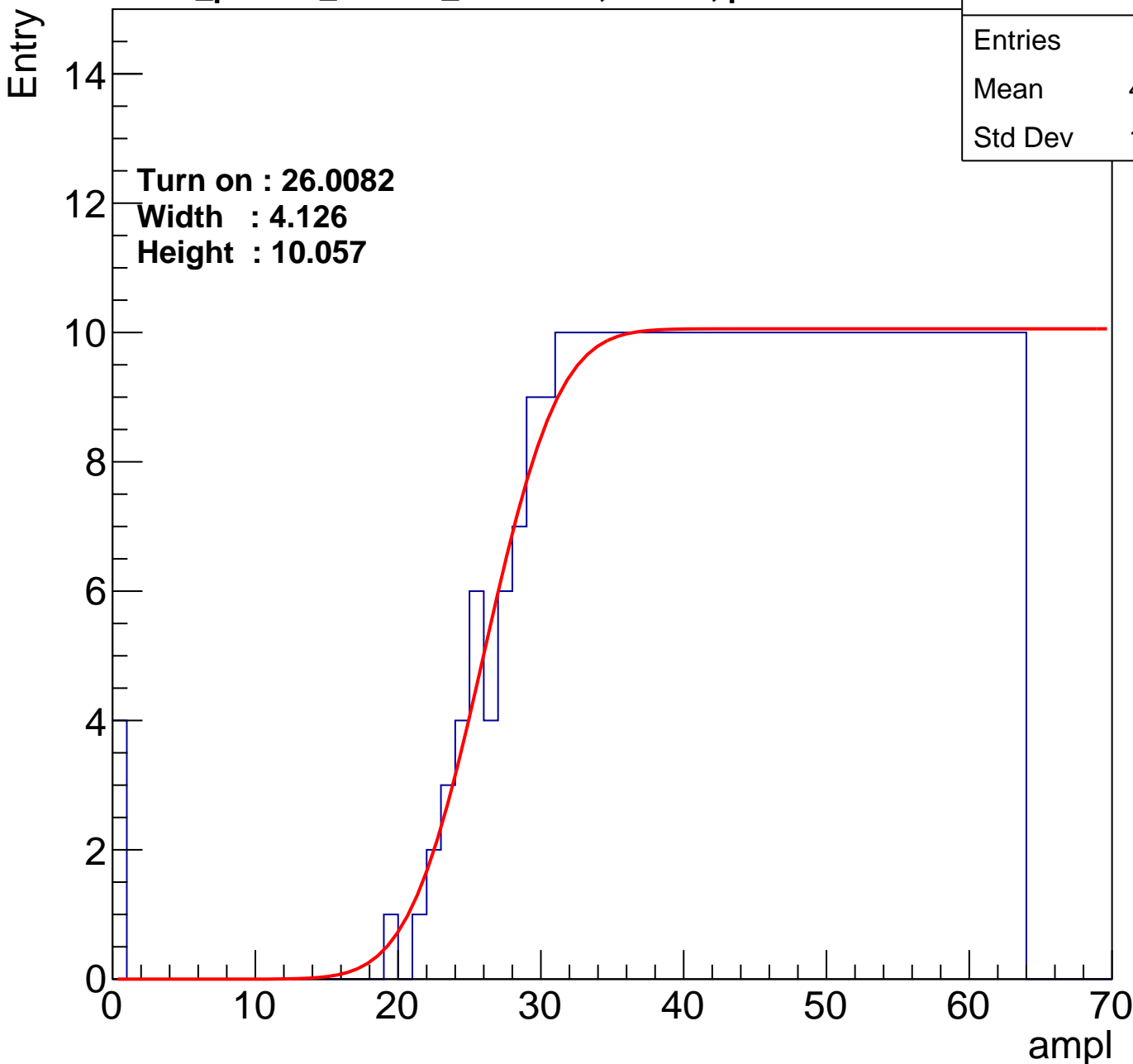
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

Entries	386
Mean	43.79
Std Dev	12.09

**Turn on : 26.0082**

**Width : 4.126**

**Height : 10.057**



# B0L102S, U26-ch61

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.26
Std Dev	11.39

Turn on : 26.0929

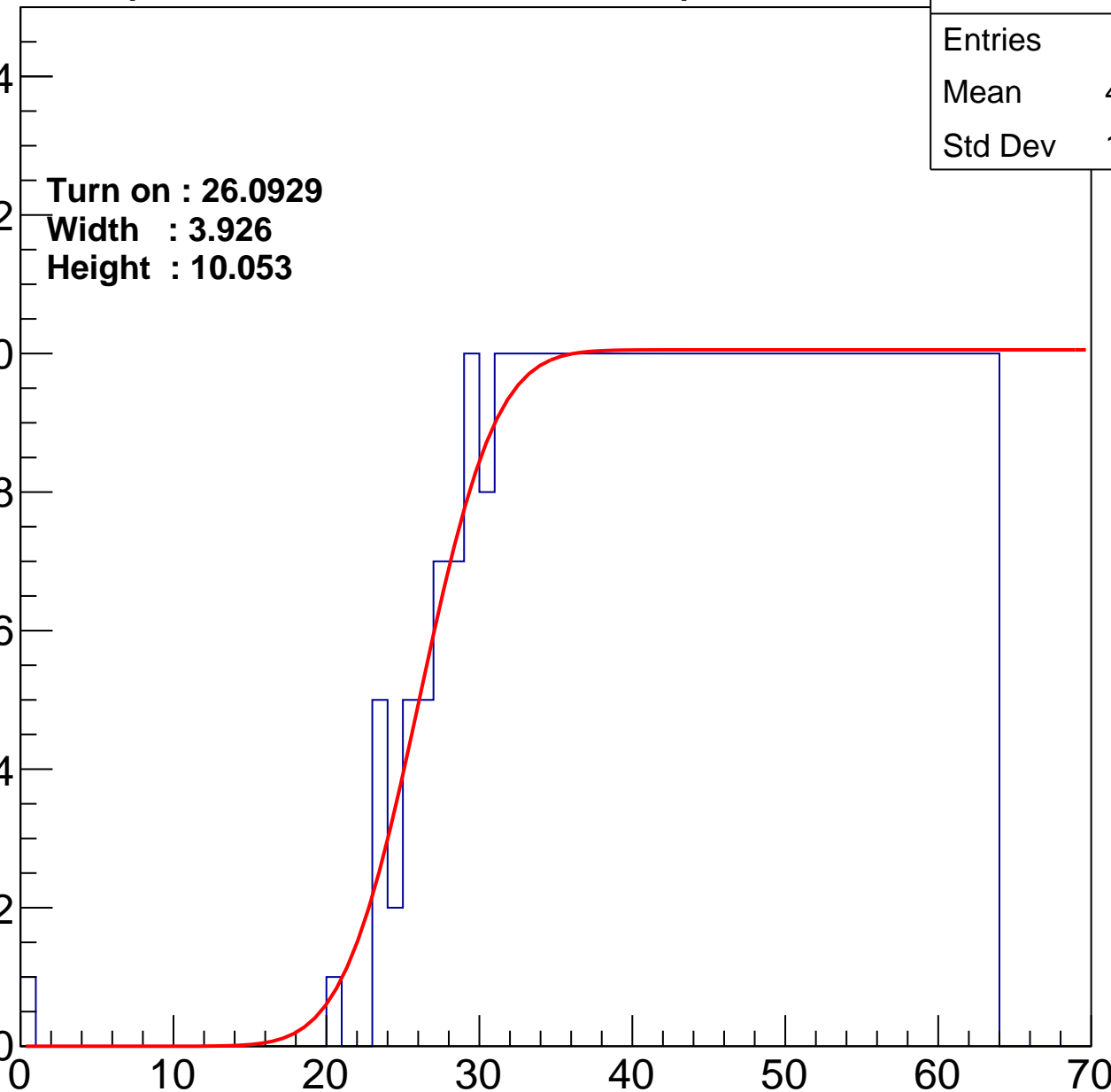
Width : 3.926

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch62

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.77
Std Dev	12.3

Turn on : 26.3823

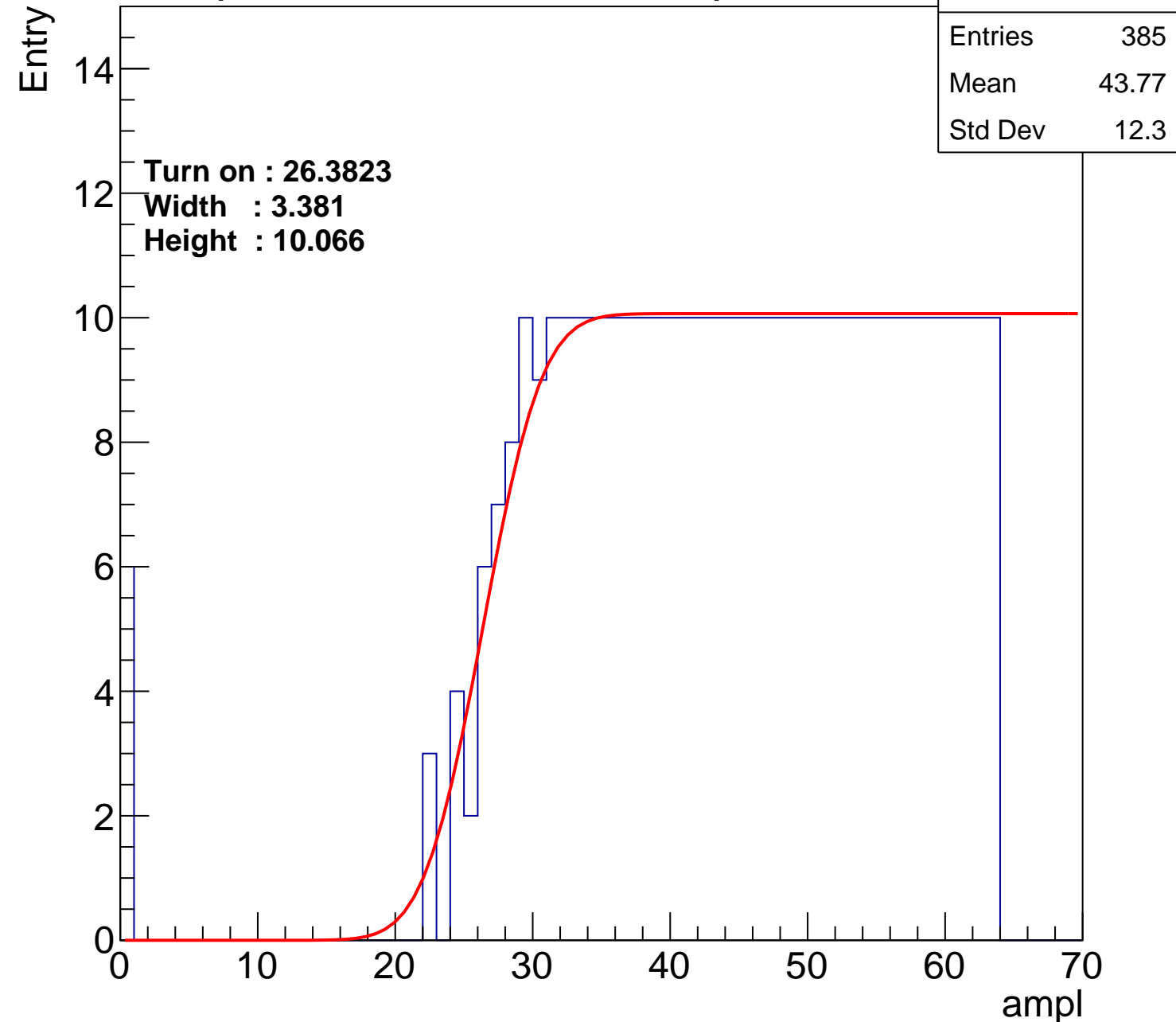
Width : 3.381

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U26-ch63

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.91
Std Dev	11.31

Turn on : 27.6332

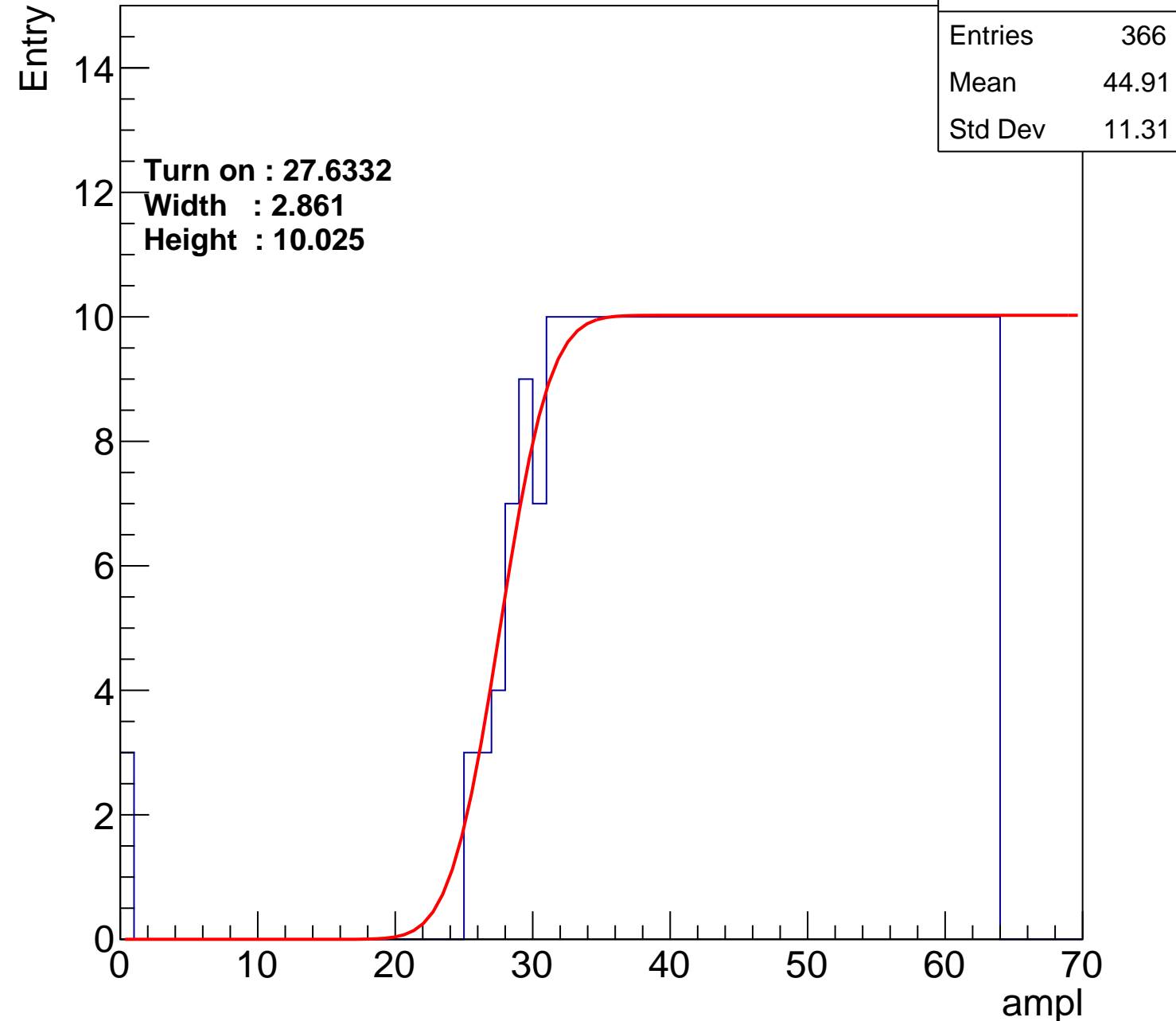
Width : 2.861

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch64

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 26.5998

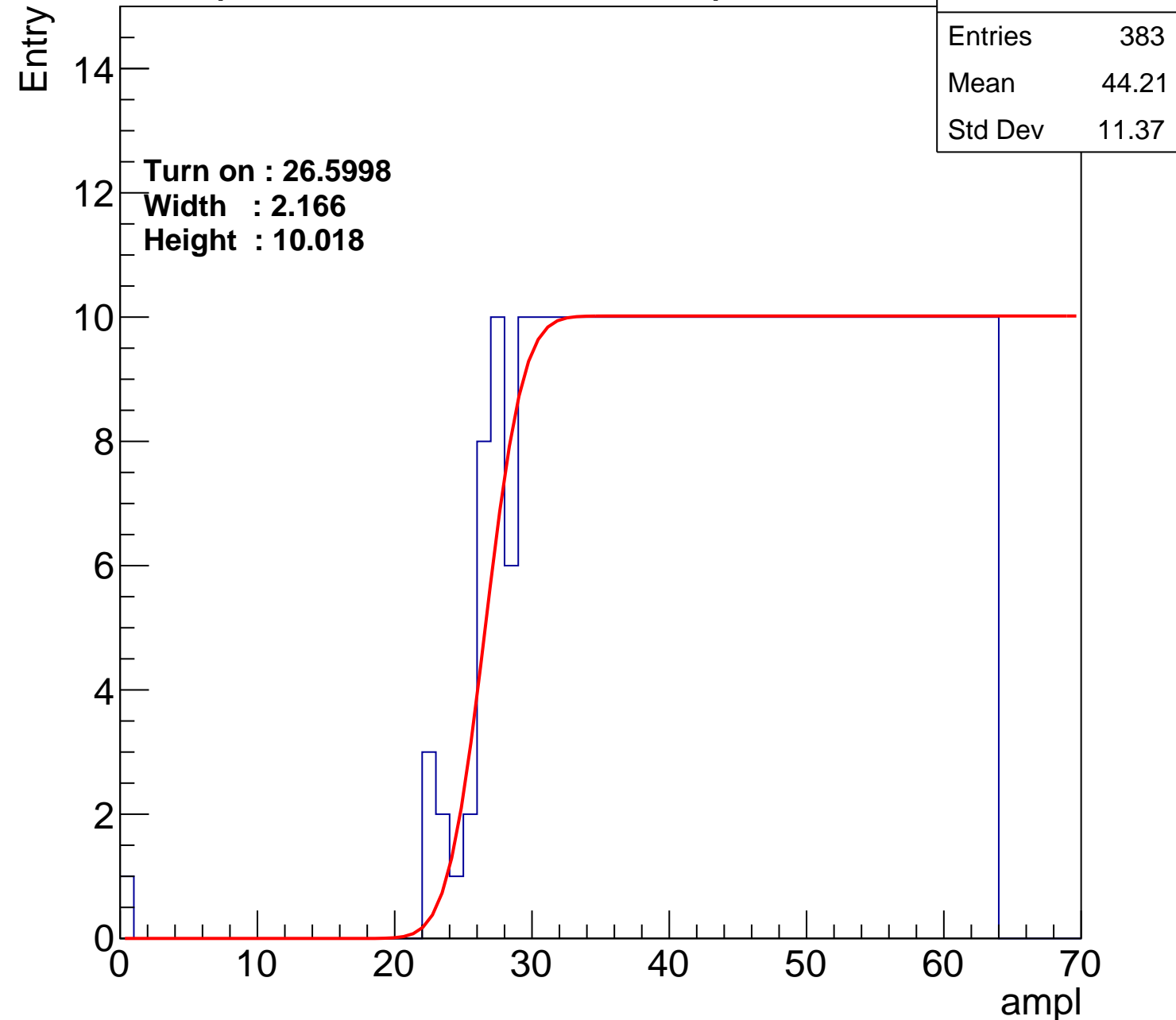
Width : 2.166

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch65

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.98
Std Dev	11.71

**Turn on : 25.9975**

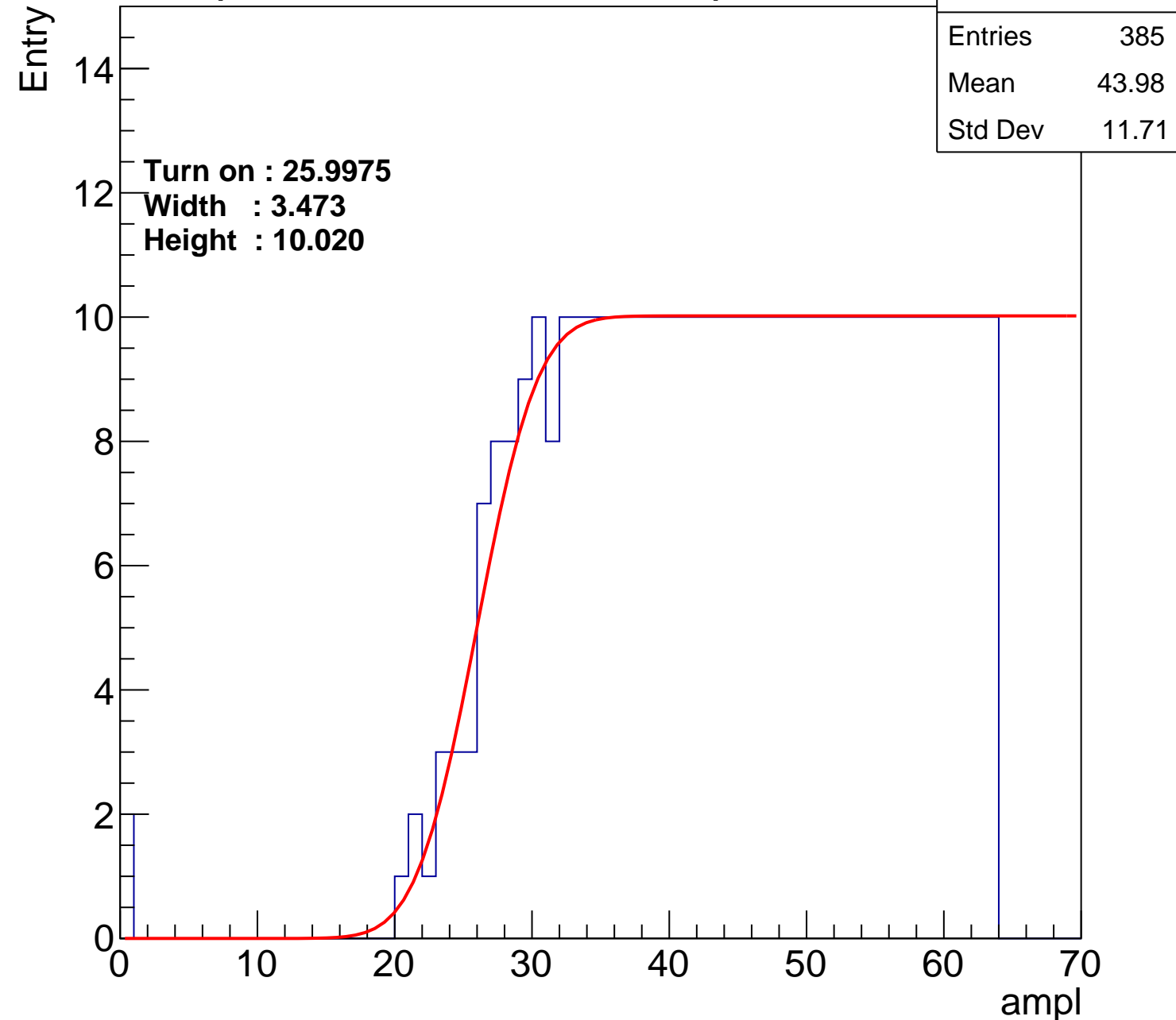
**Width : 3.473**

**Height : 10.020**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch66

calib\_packv5\_042523\_0143.root, FC#12, port B1

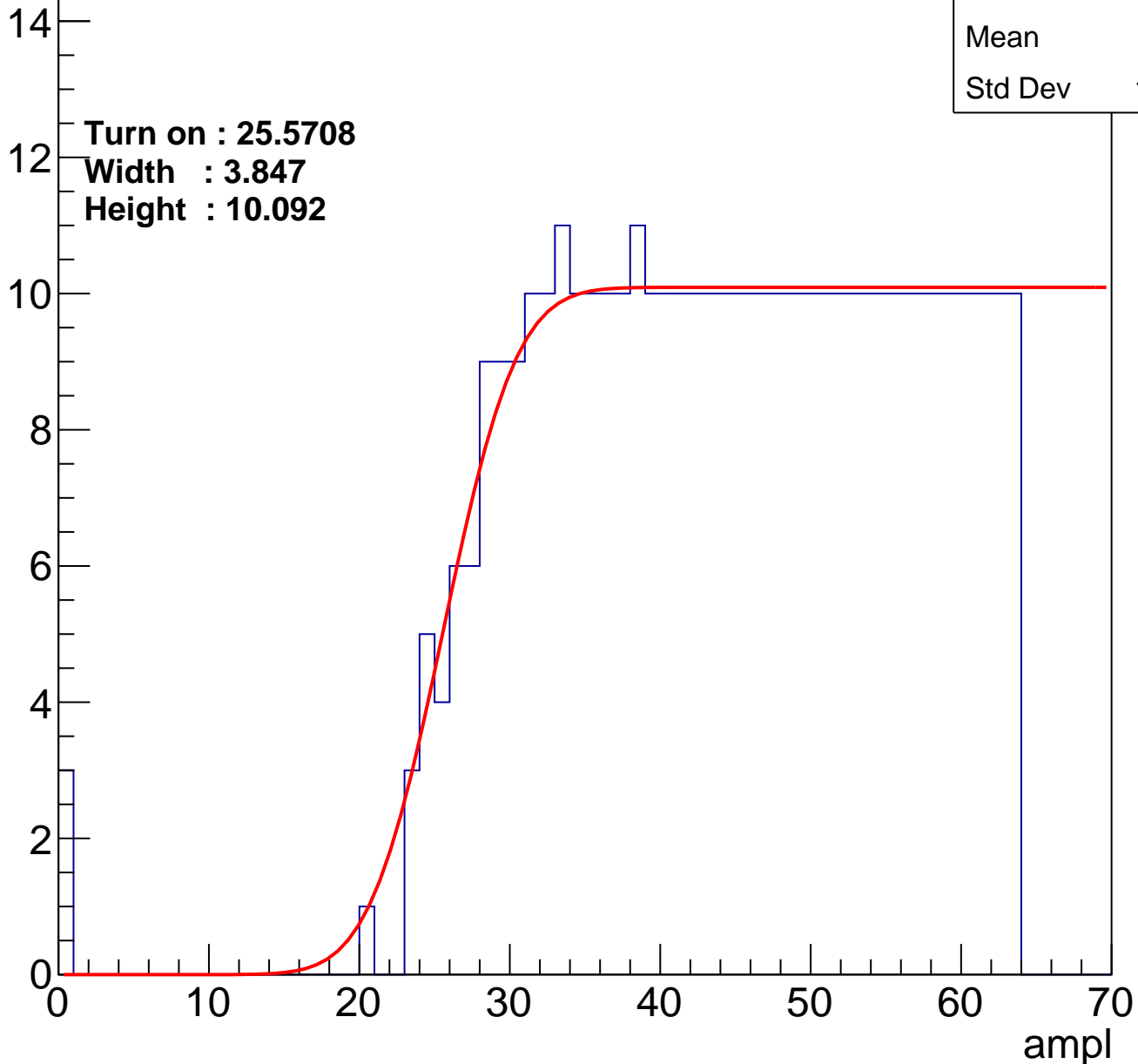
Entries	387
Mean	43.9
Std Dev	11.81

Turn on : 25.5708

Width : 3.847

Height : 10.092

Entry



# B0L102S, U26-ch67

calib\_packv5\_042523\_0143.root, FC#12, port B1

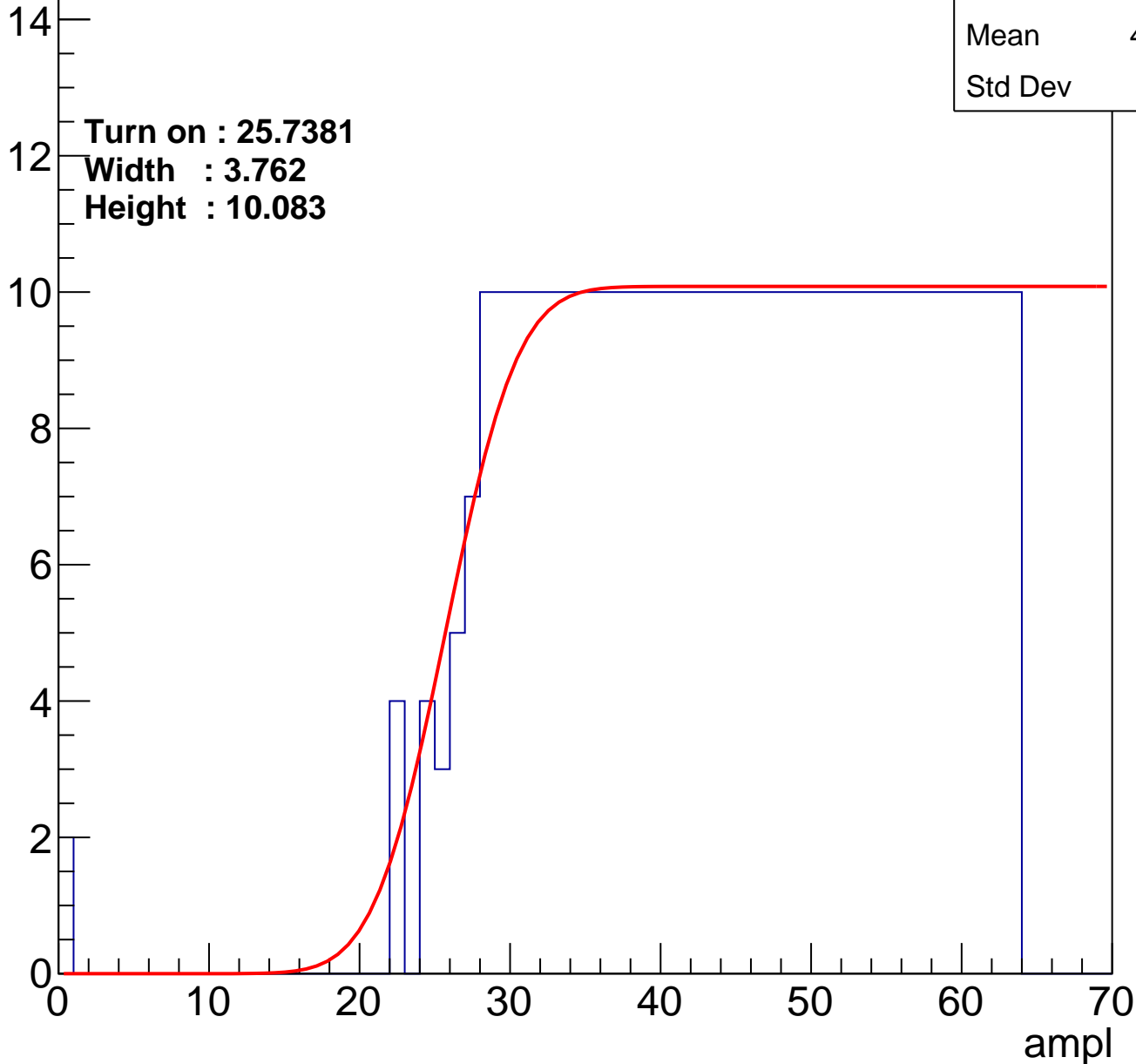
Entries	385
Mean	44.05
Std Dev	11.6

Turn on : 25.7381

Width : 3.762

Height : 10.083

Entry



# B0L102S, U26-ch68

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.78
Std Dev	11.64

Turn on : 26.0514

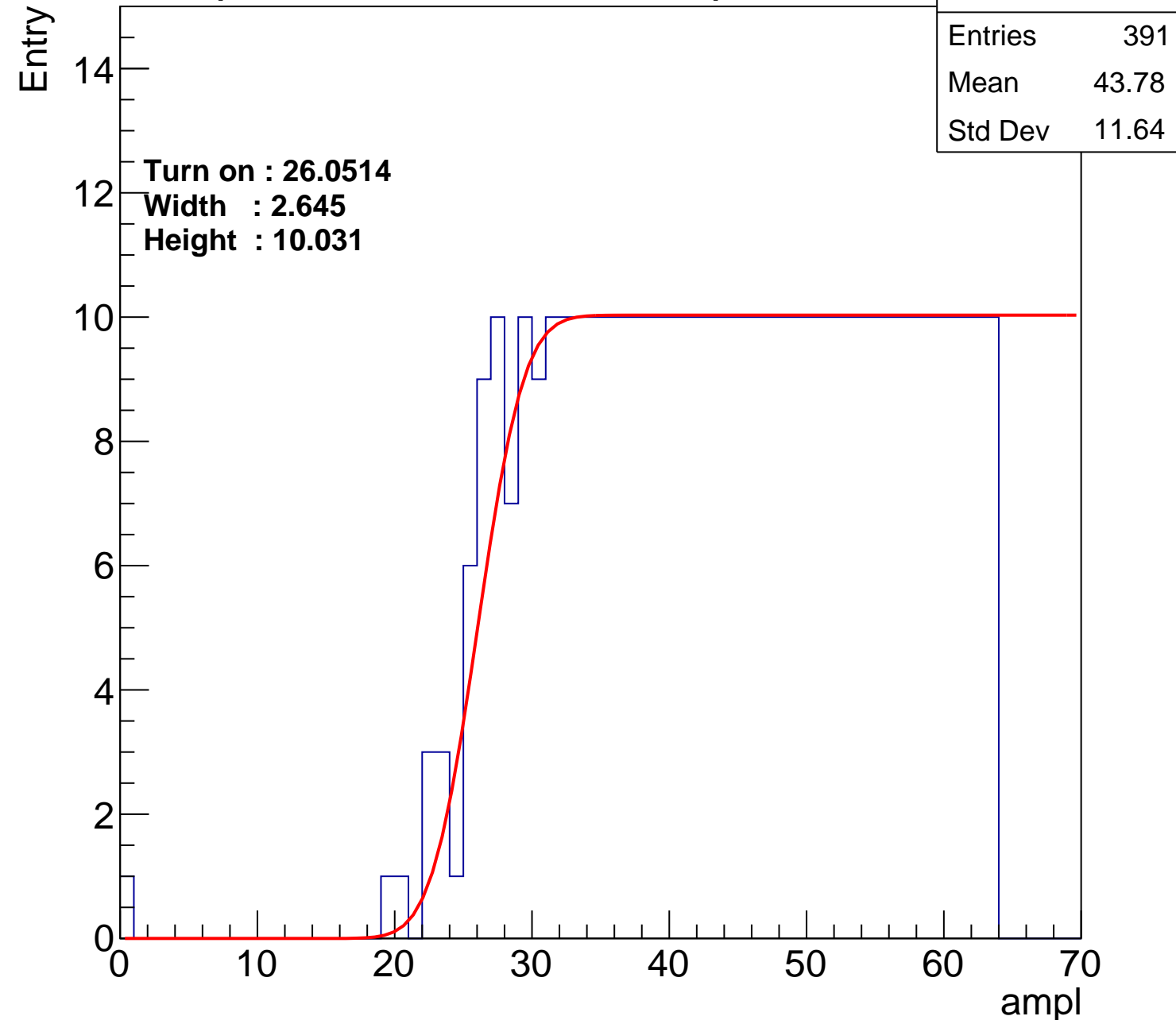
Width : 2.645

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch69

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	44.17
Std Dev	11.34

Turn on : 25.9278

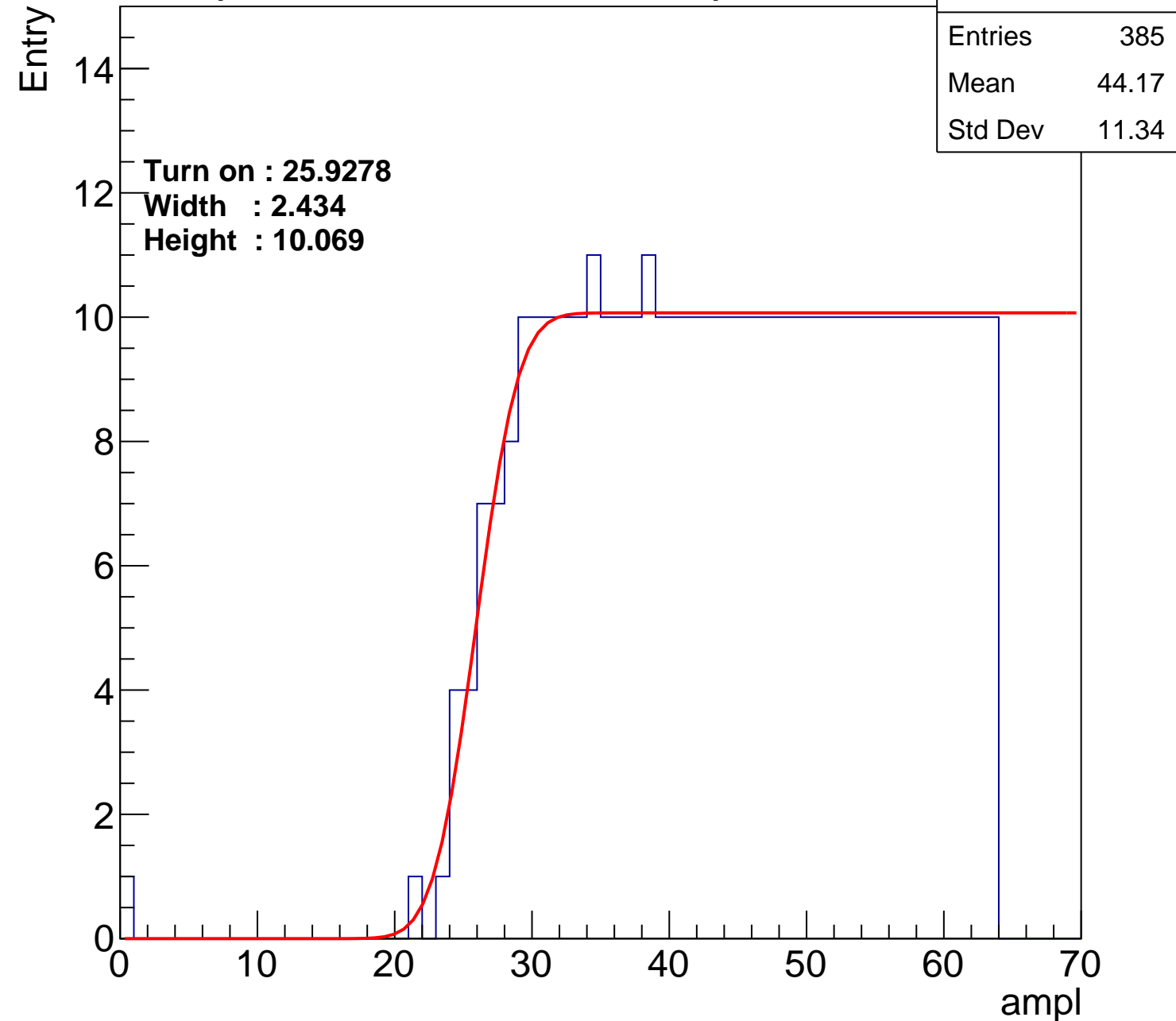
Width : 2.434

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch70

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.64
Std Dev	11.83

Turn on : 25.5595

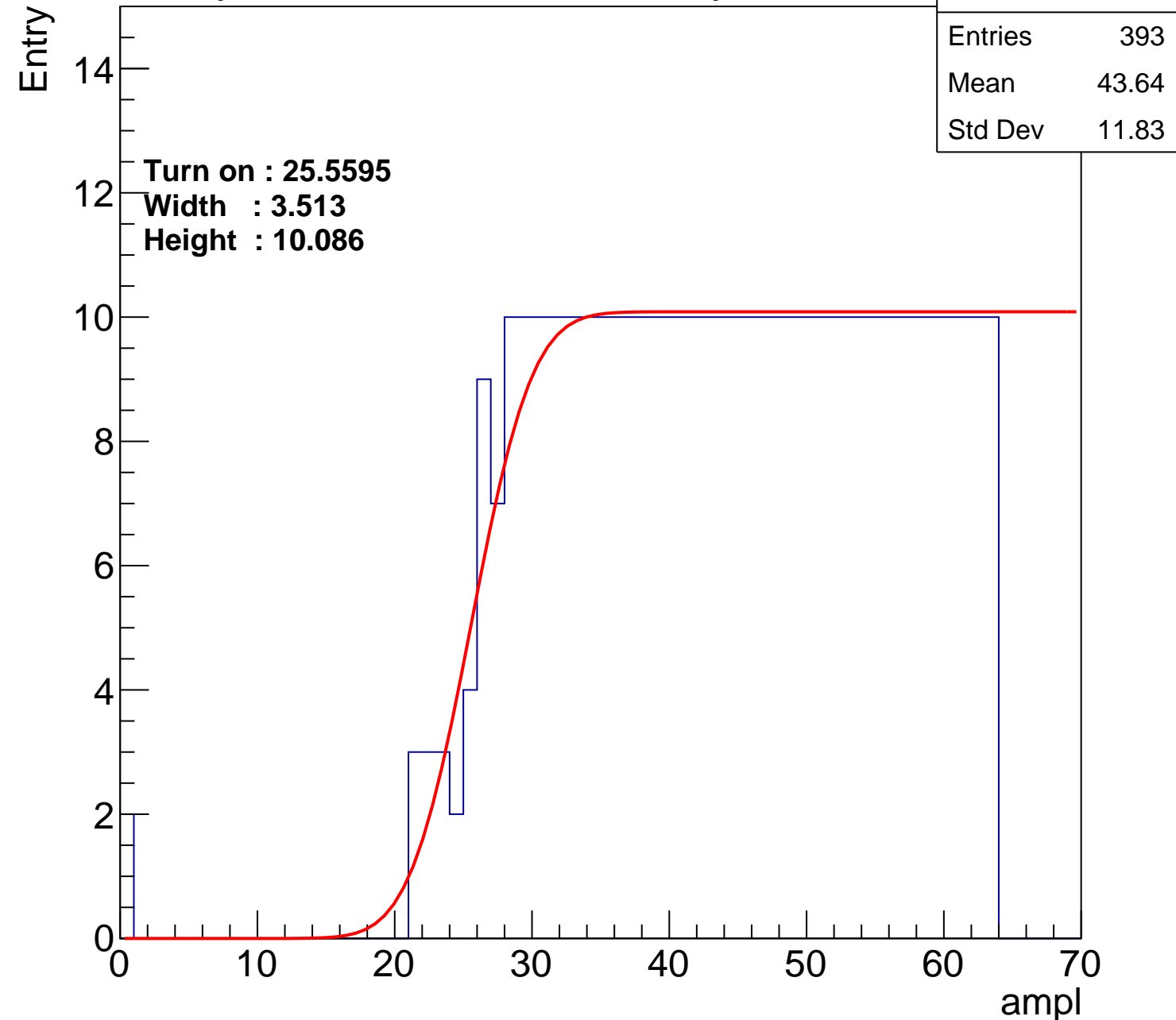
Width : 3.513

Height : 10.086

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U26-ch71

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.81
Std Dev	11.45

Turn on : 28.3702

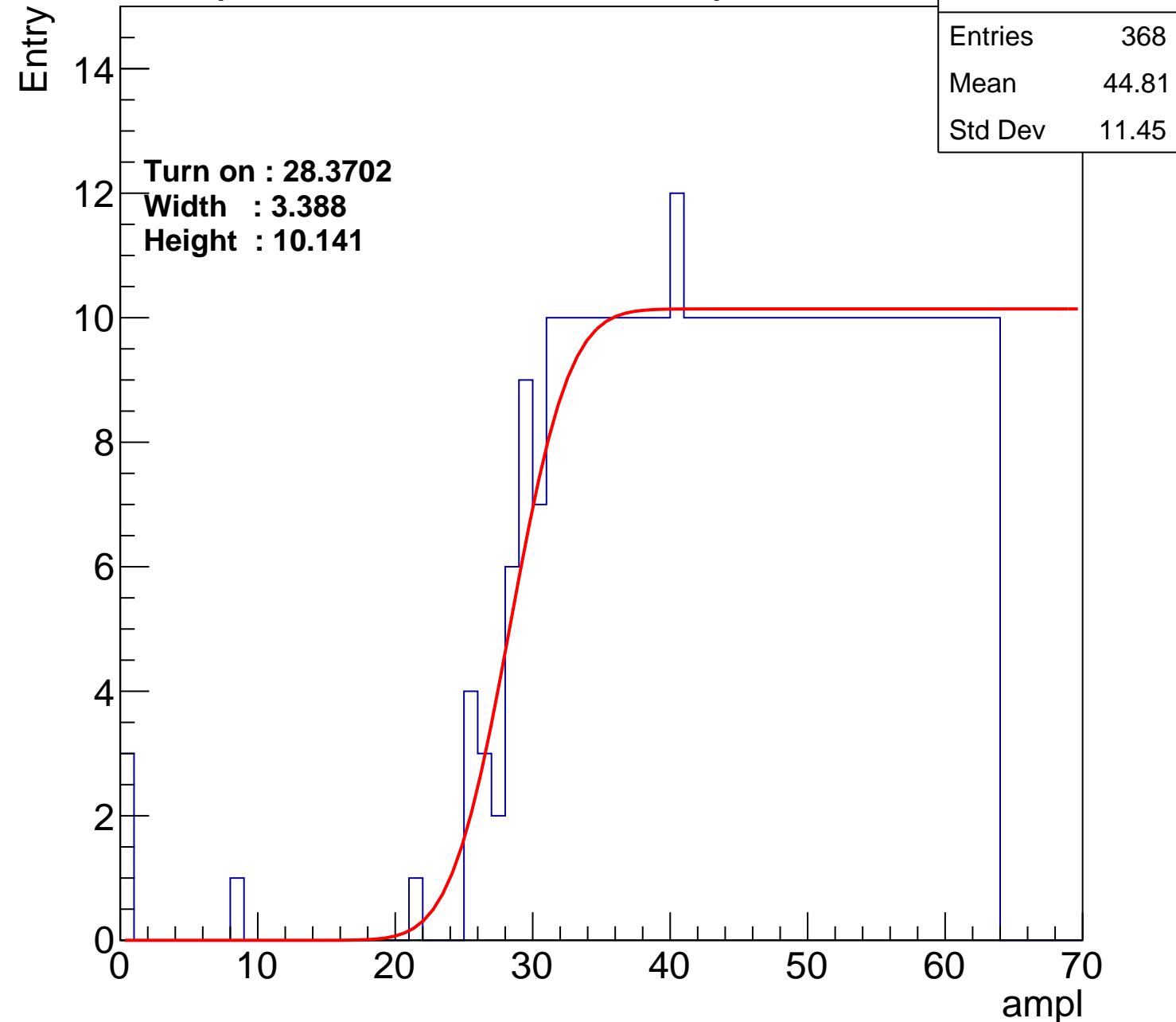
Width : 3.388

Height : 10.141

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch72

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.92
Std Dev	11.87

Turn on : 26.1907

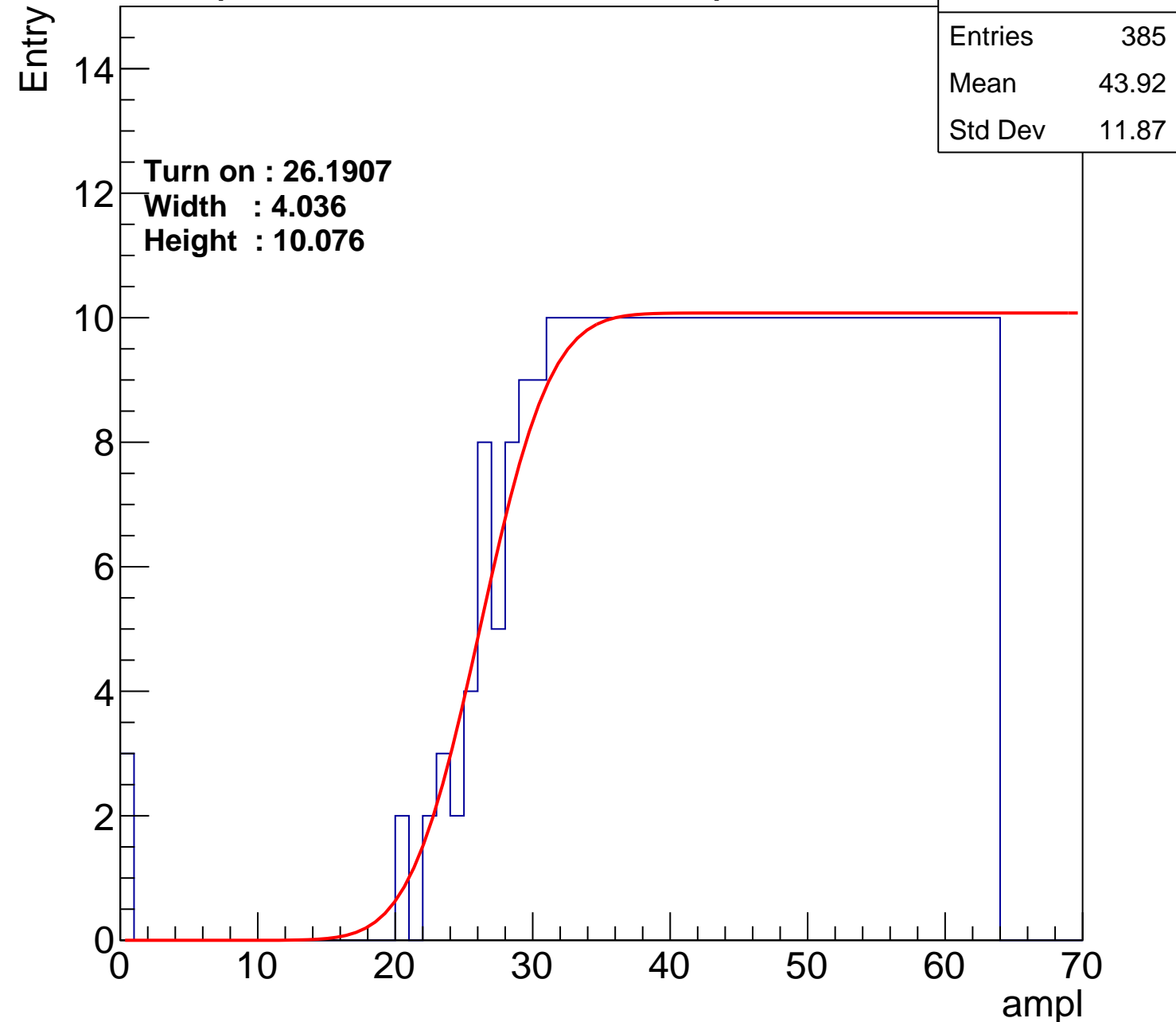
Width : 4.036

Height : 10.076

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch73

calib\_packv5\_042523\_0143.root, FC#12, port B1

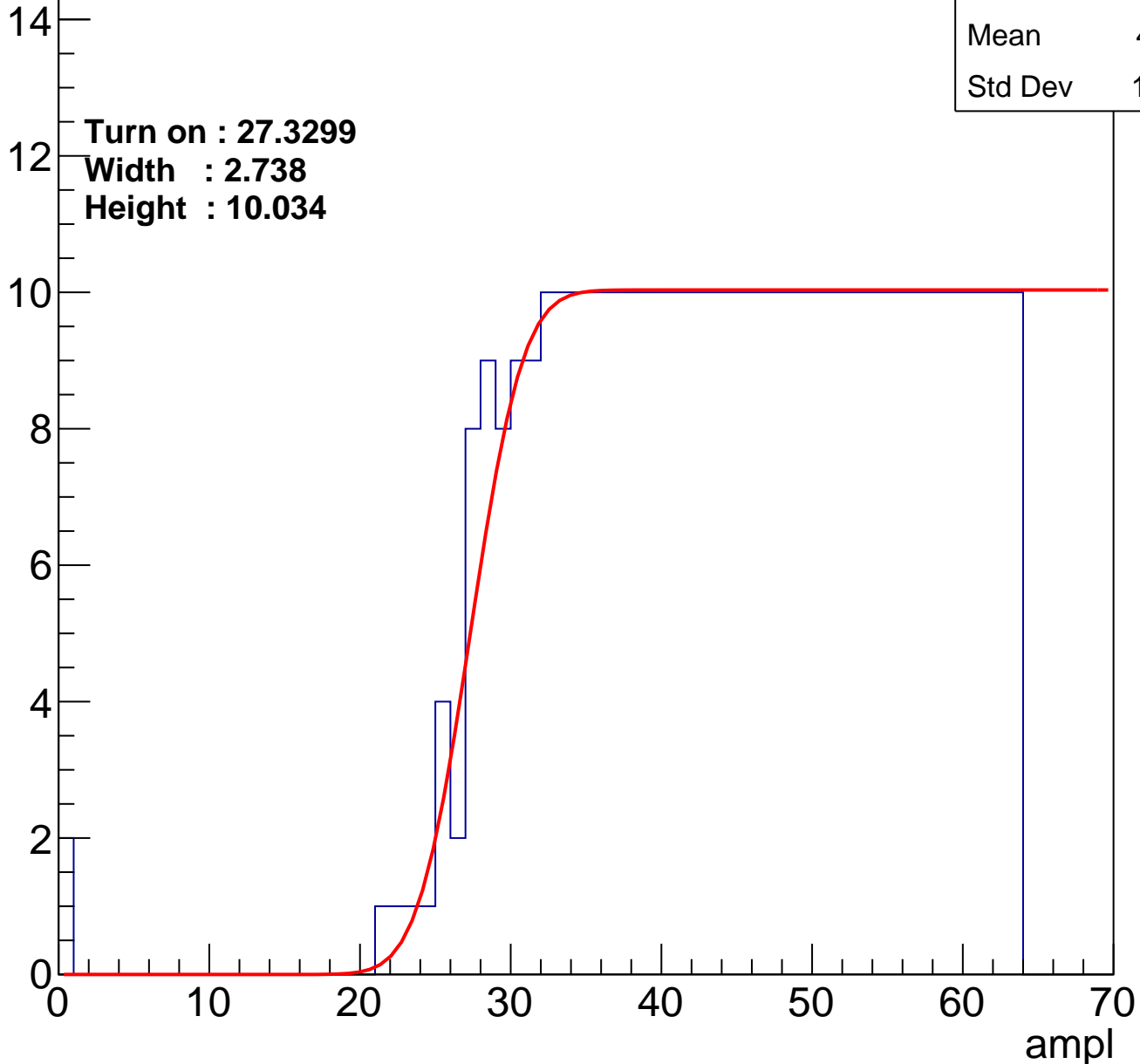
Entries	375
Mean	44.51
Std Dev	11.39

Turn on : 27.3299

Width : 2.738

Height : 10.034

Entry



# B0L102S, U26-ch74

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	362
Mean	45.07
Std Dev	11.27

**Turn on : 28.6226**

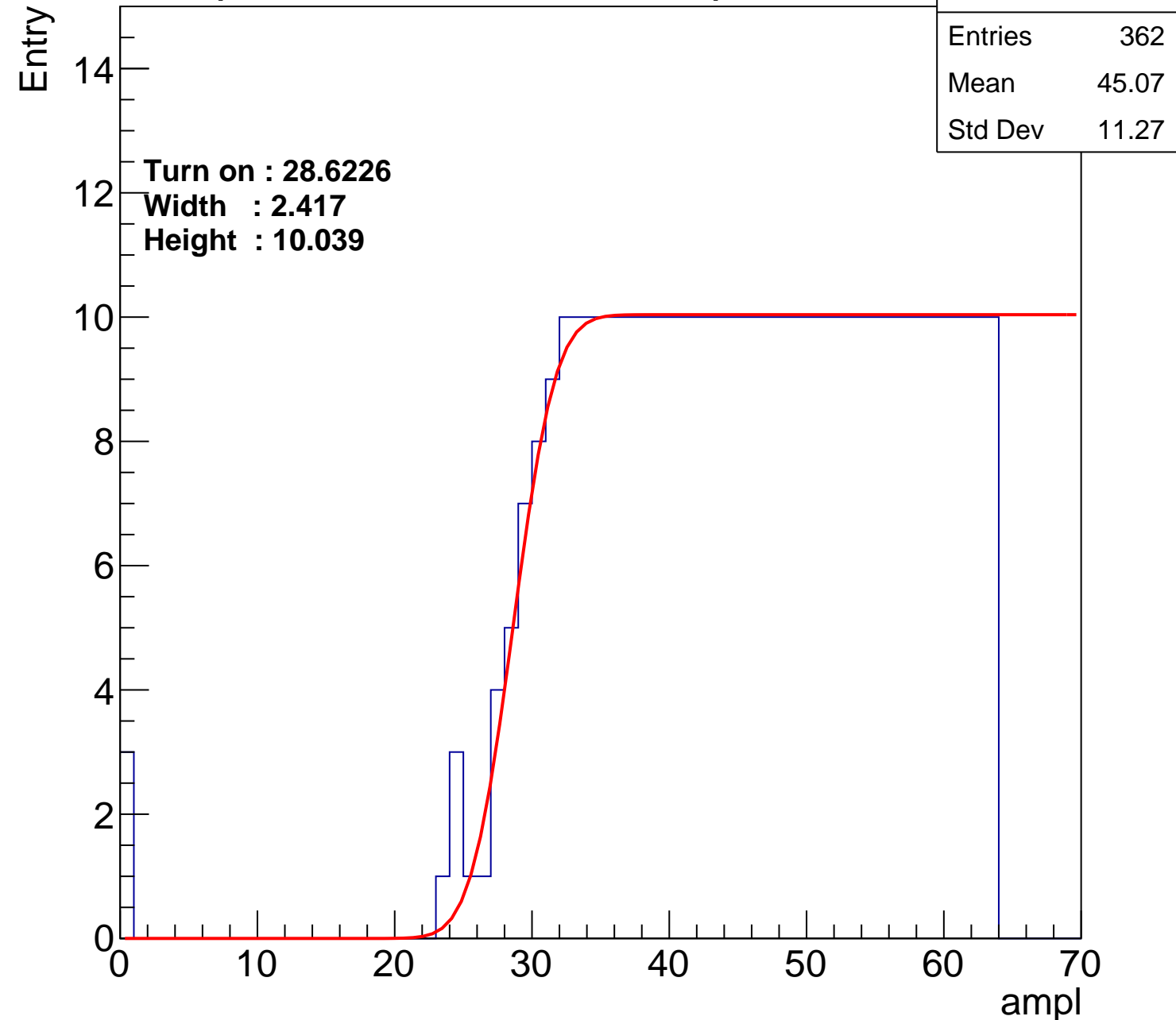
**Width : 2.417**

**Height : 10.039**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch75

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.35
Std Dev	11.66

Turn on : 26.8288

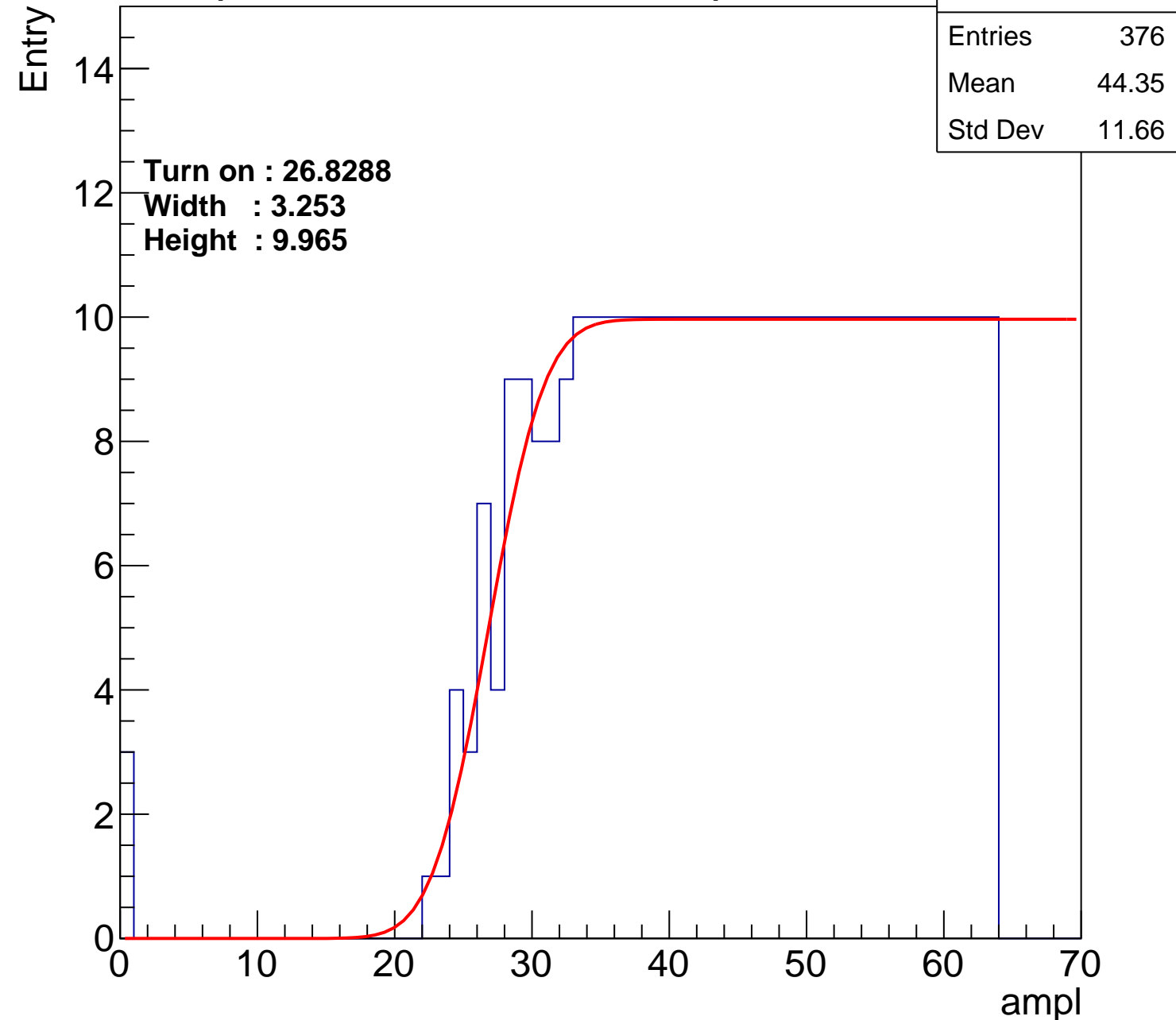
Width : 3.253

Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch76

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.7
Std Dev	12.06

**Turn on : 25.6202**

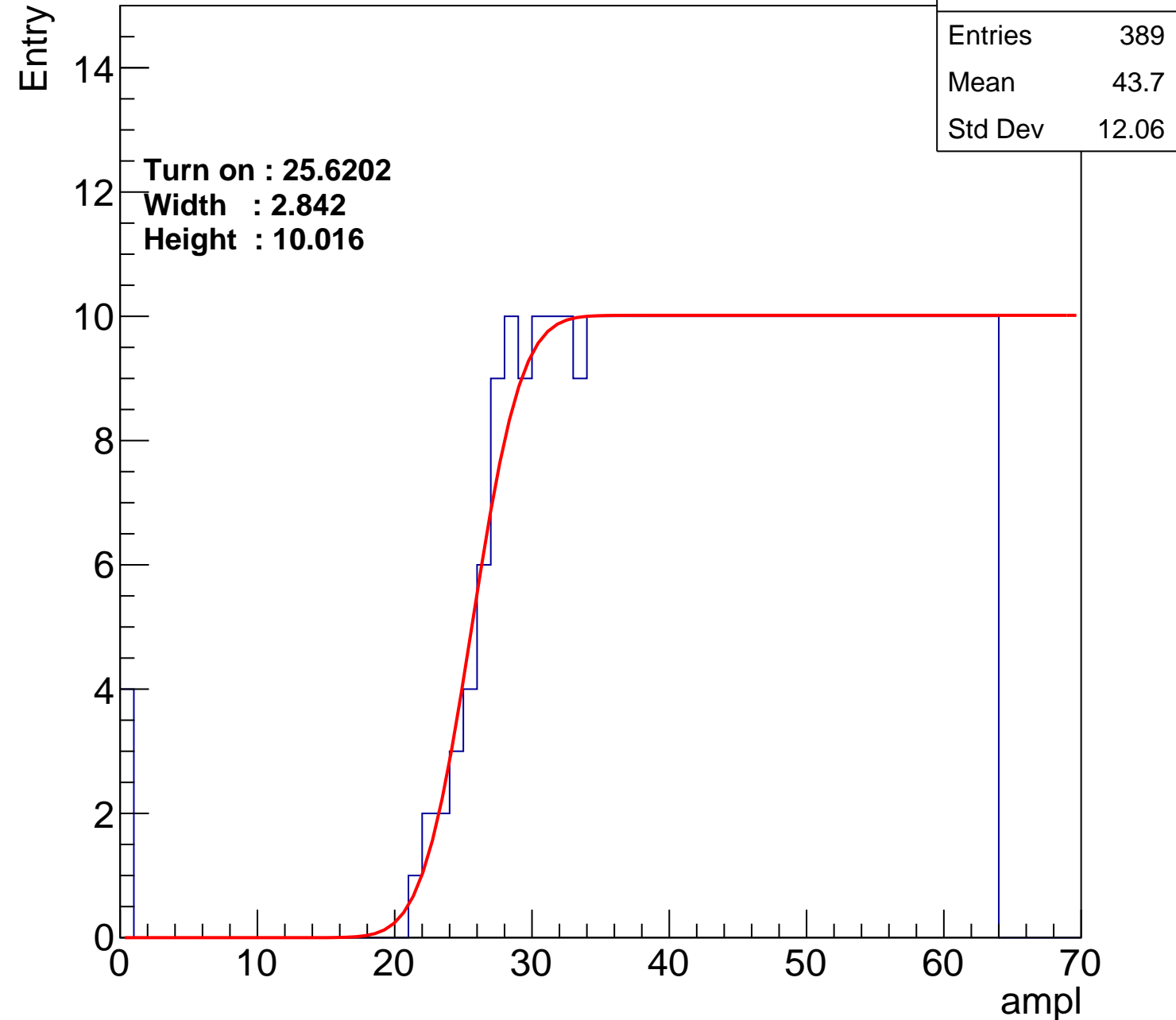
**Width : 2.842**

**Height : 10.016**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch77

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.86
Std Dev	11.12

Turn on : 27.4183

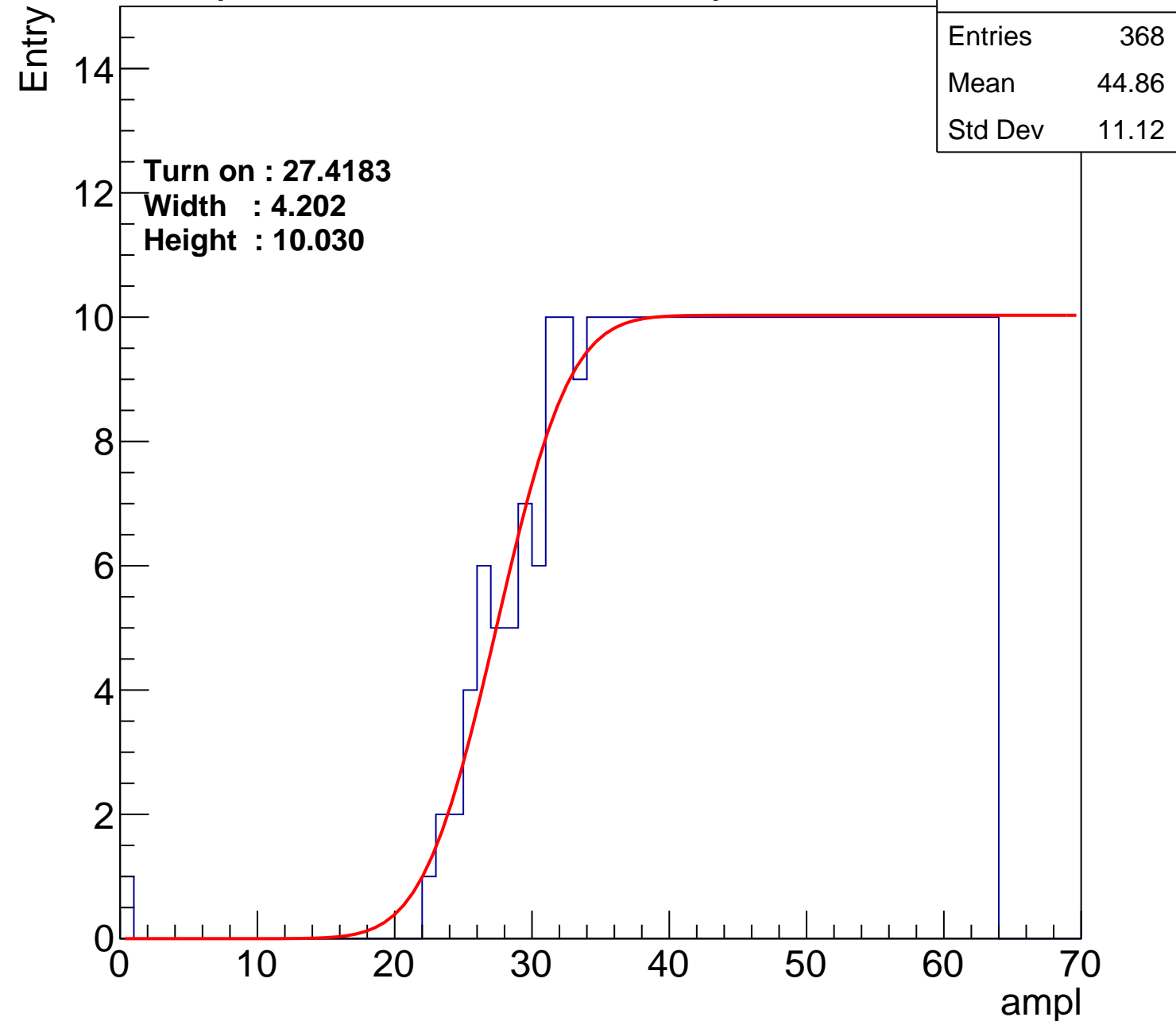
Width : 4.202

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch78

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.67
Std Dev	11.35

Turn on : 27.7474

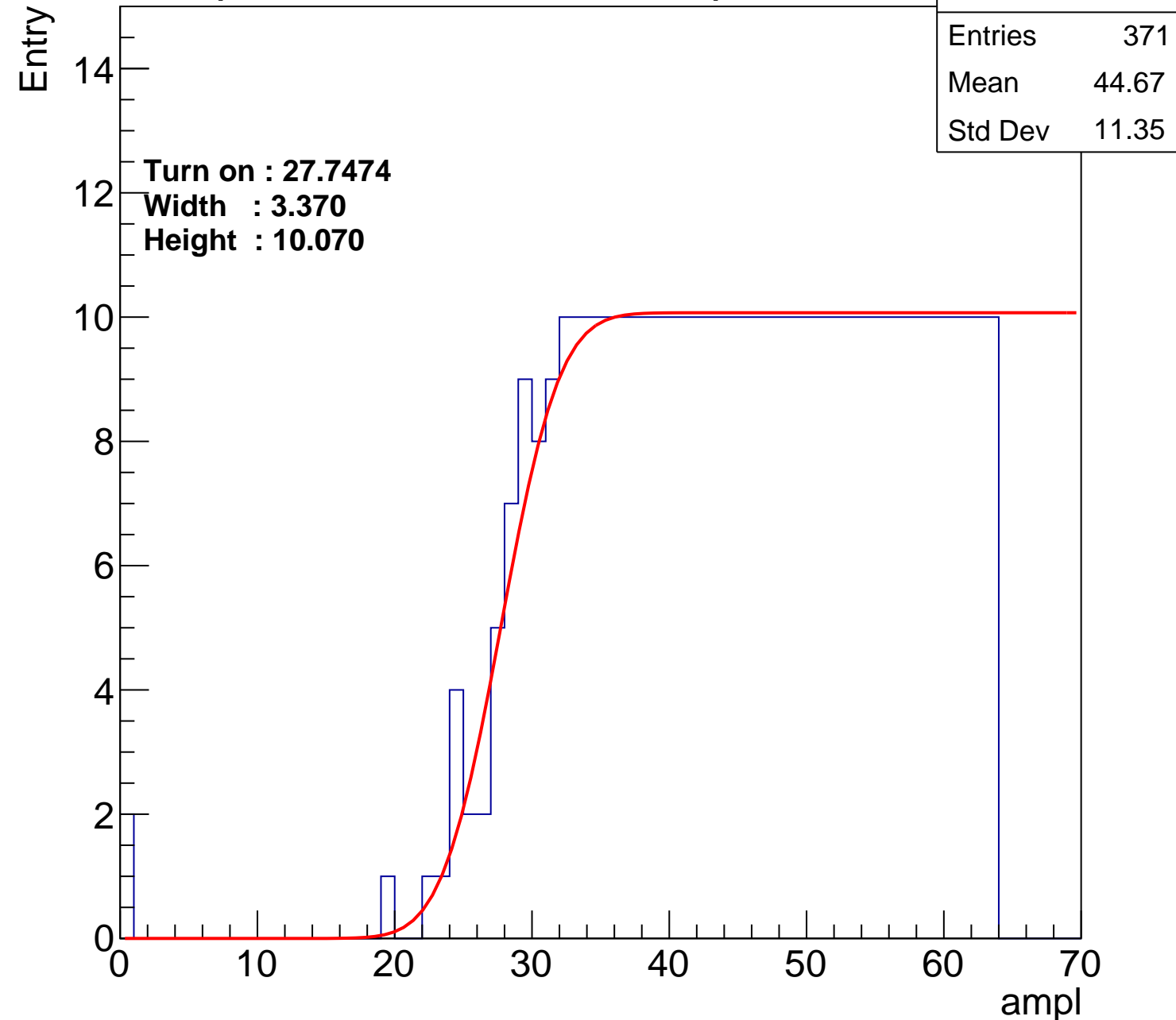
Width : 3.370

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U26-ch79

calib\_packv5\_042523\_0143.root, FC#12, port B1

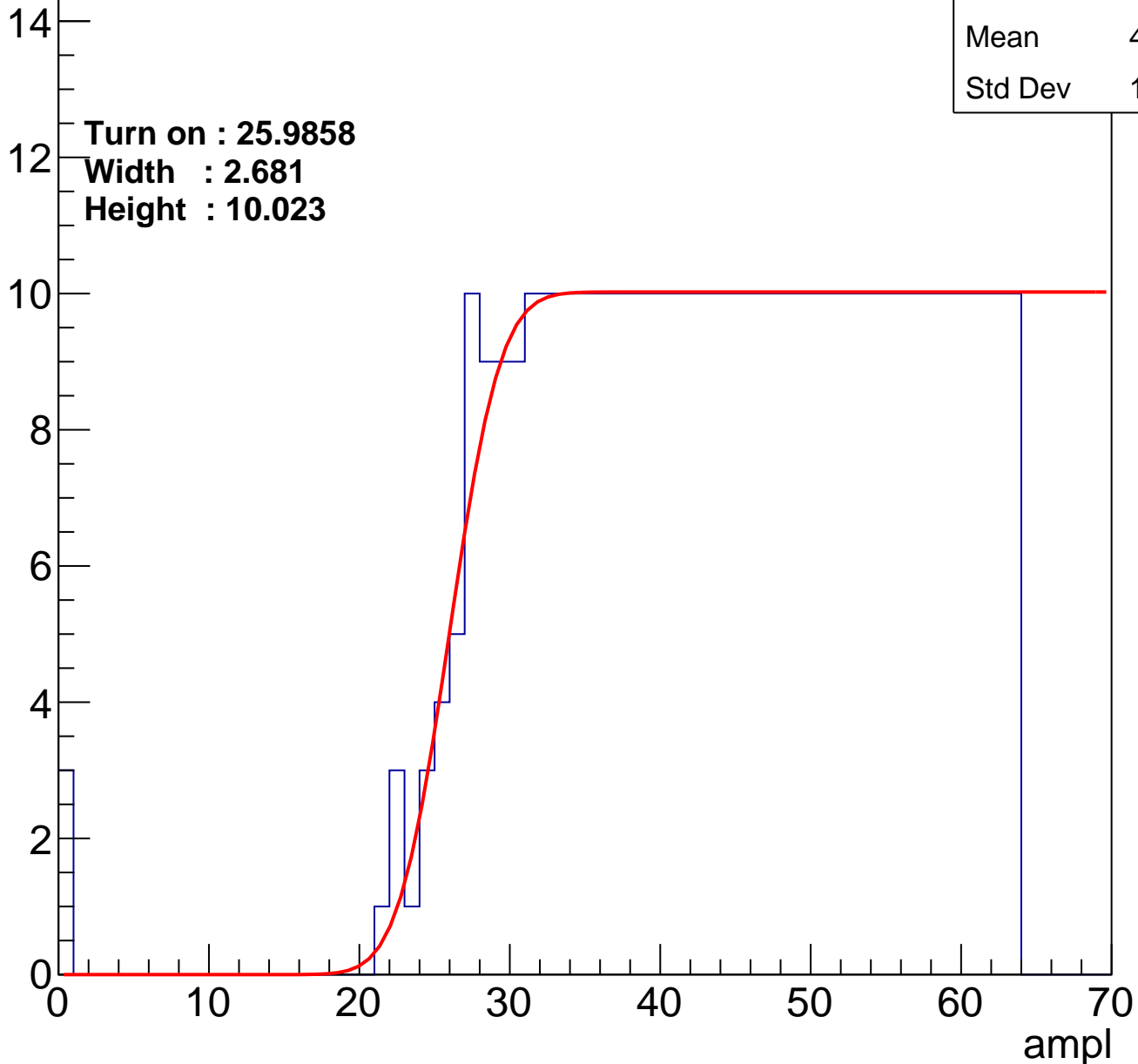
Entries	387
Mean	43.86
Std Dev	11.85

Turn on : 25.9858

Width : 2.681

Height : 10.023

Entry



# B0L102S, U26-ch80

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	399
Mean	43.33
Std Dev	12

Turn on : 24.8790

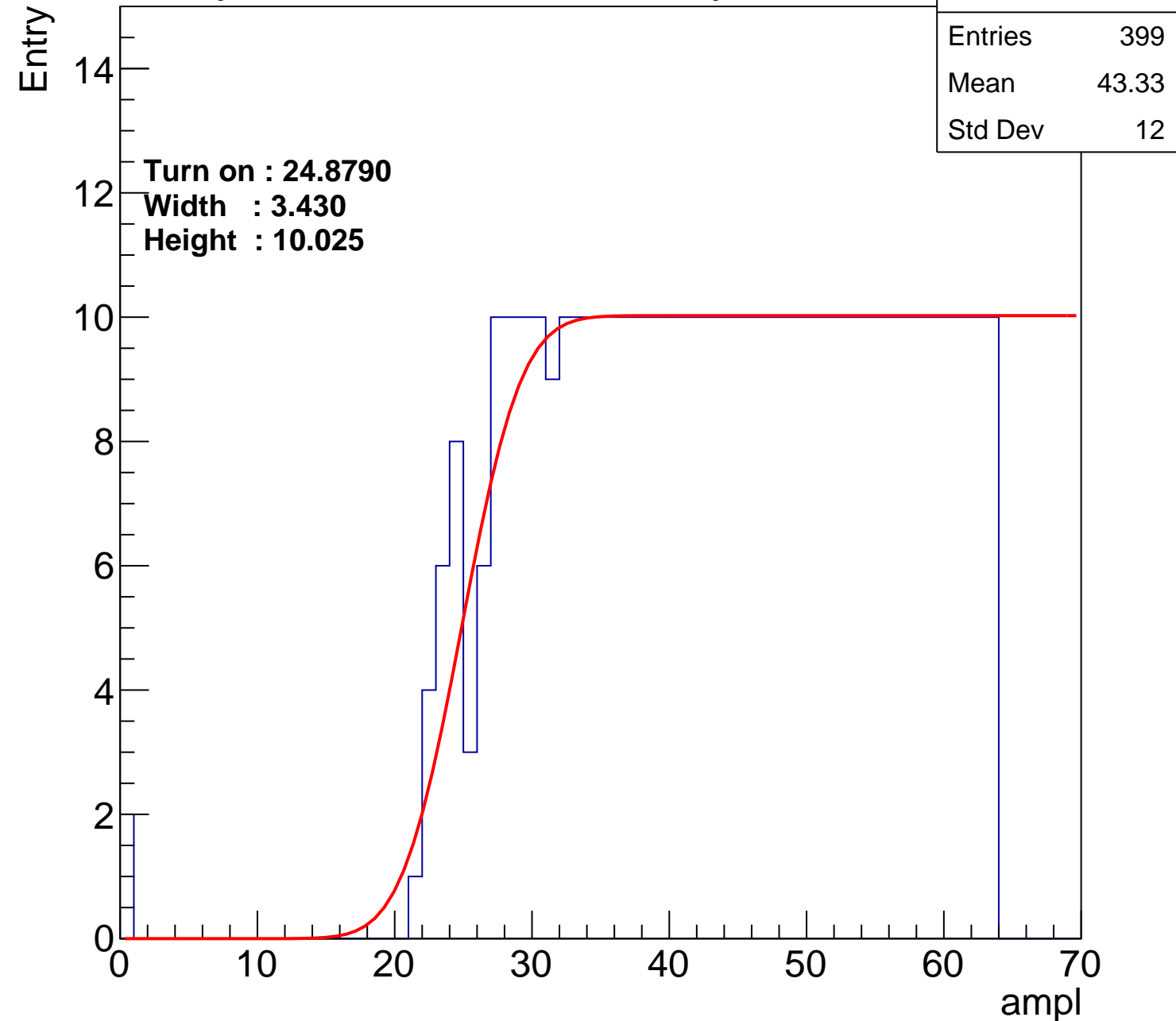
Width : 3.430

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch81

calib\_packv5\_042523\_0143.root, FC#12, port B1

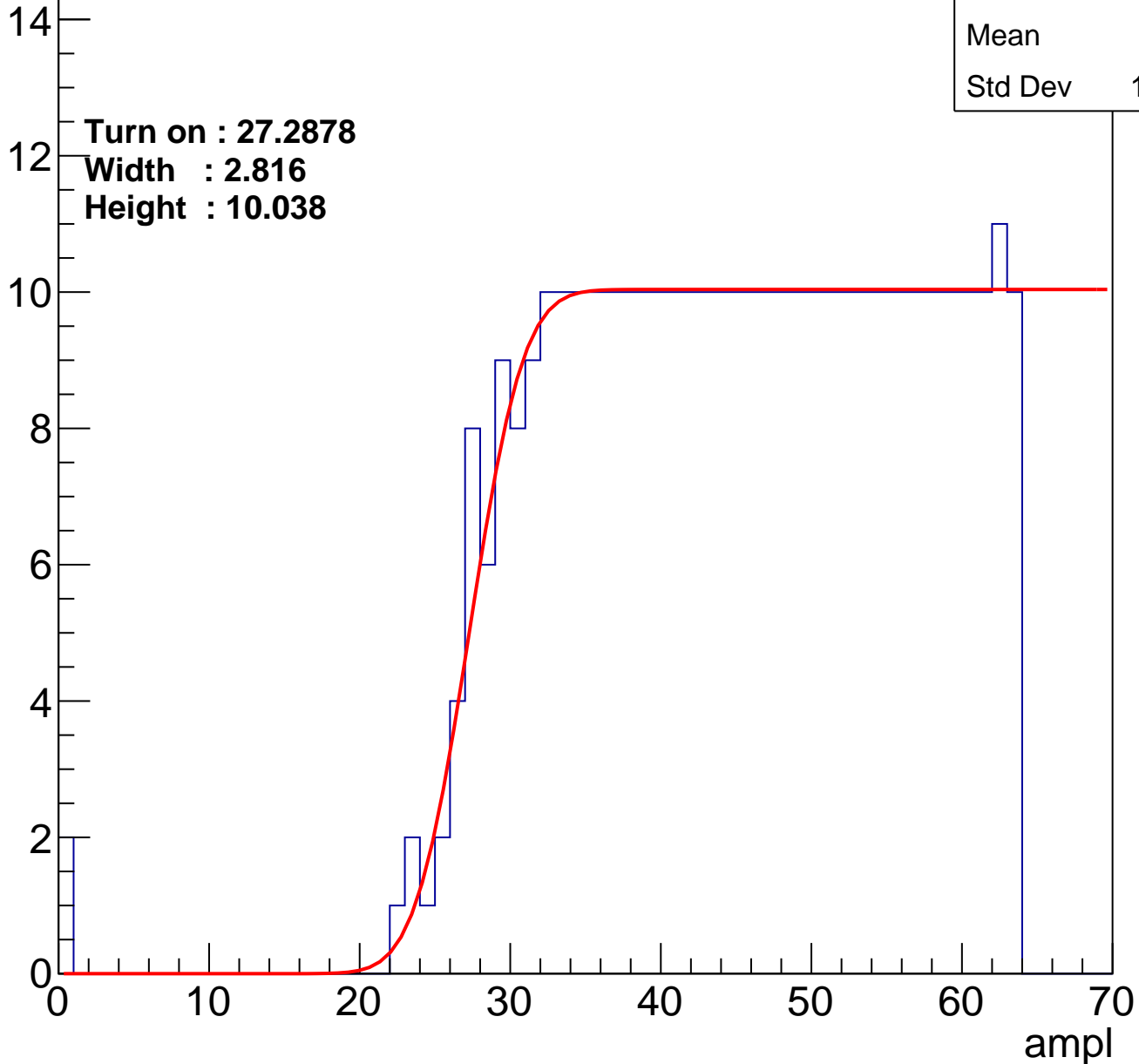
Entries	373
Mean	44.7
Std Dev	11.34

Turn on : 27.2878

Width : 2.816

Height : 10.038

Entry



# B0L102S, U26-ch82

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.65
Std Dev	11.65

**Turn on : 28.1913**

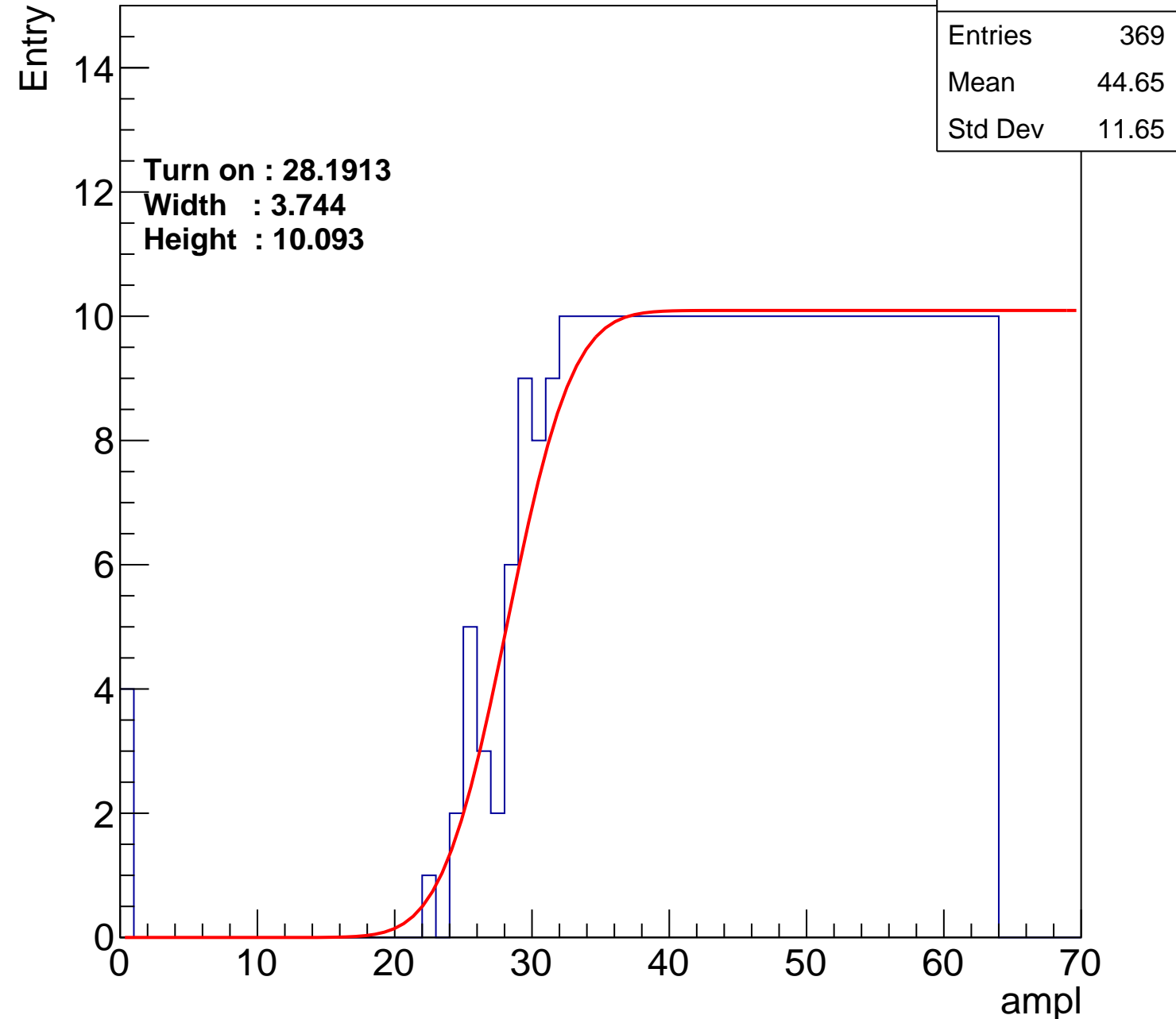
**Width : 3.744**

**Height : 10.093**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

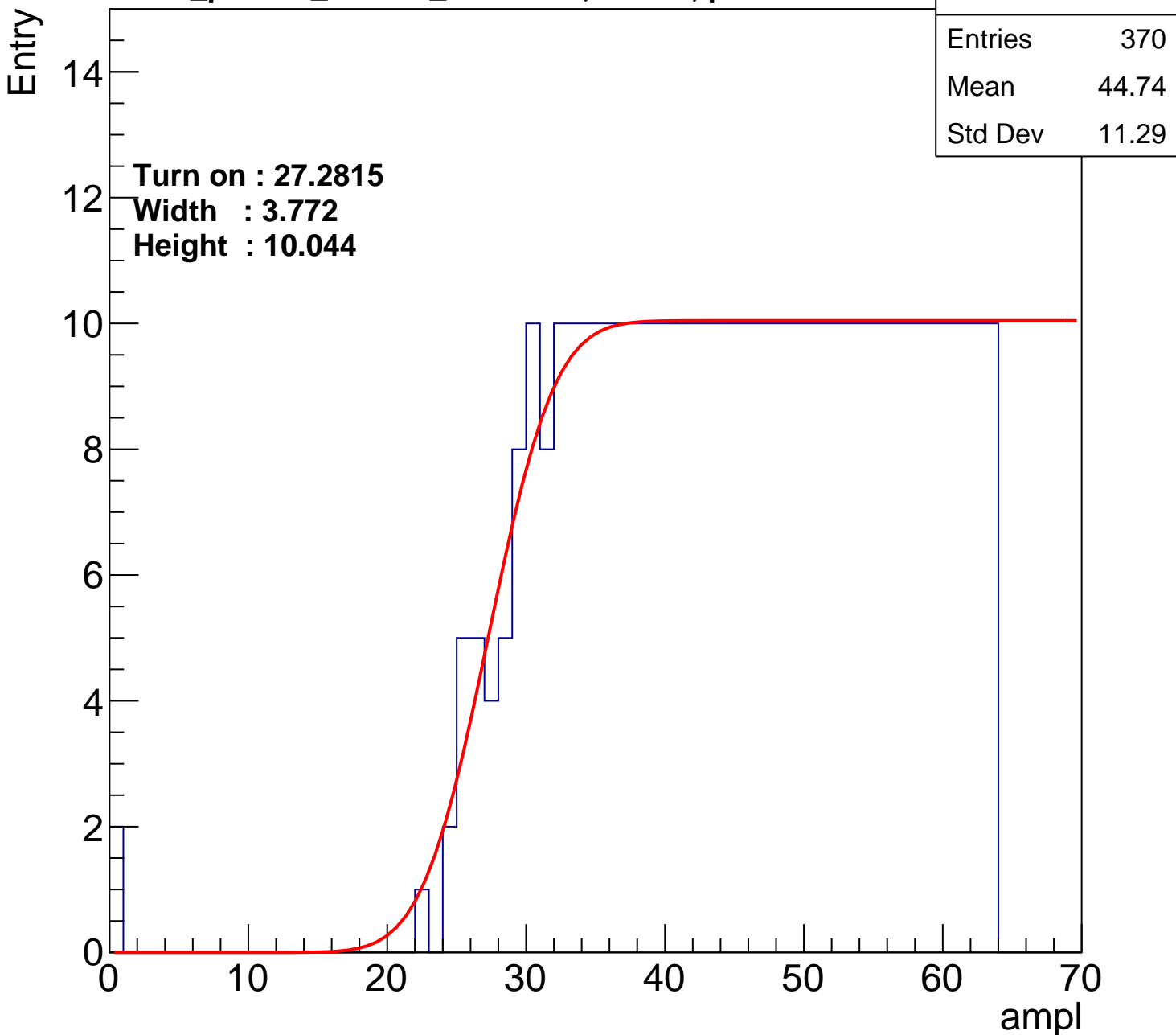


**calib\_packv5\_042523\_0143.root, FC#12, port B1**

**calib\_packv5\_042523\_0143.root, FC#12, port B1**

Entries	370
Mean	44.74
Std Dev	11.29

**Height : 10.044**



# B0L102S, U26-ch84

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 25.4906

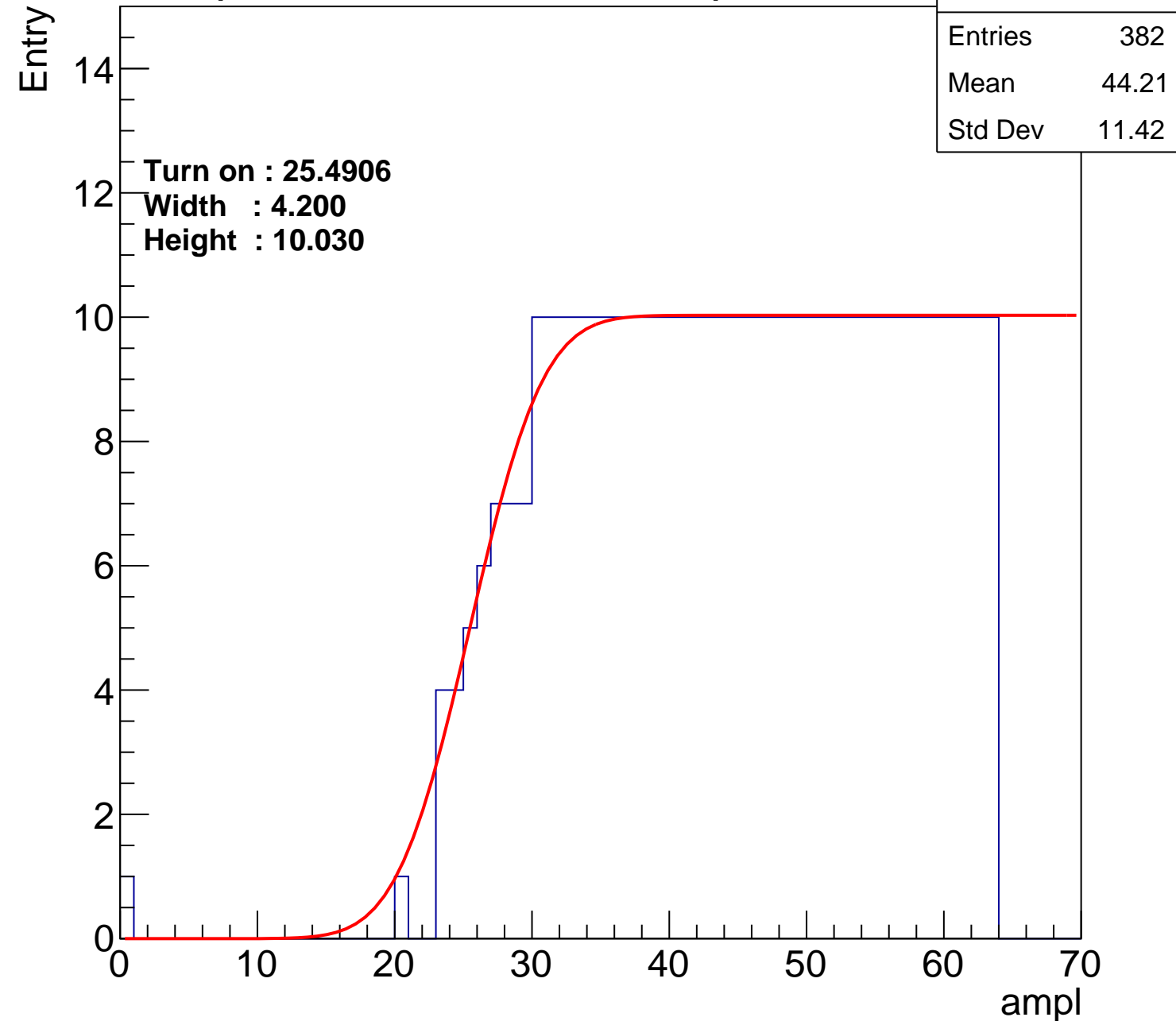
Width : 4.200

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch85

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 26.8270

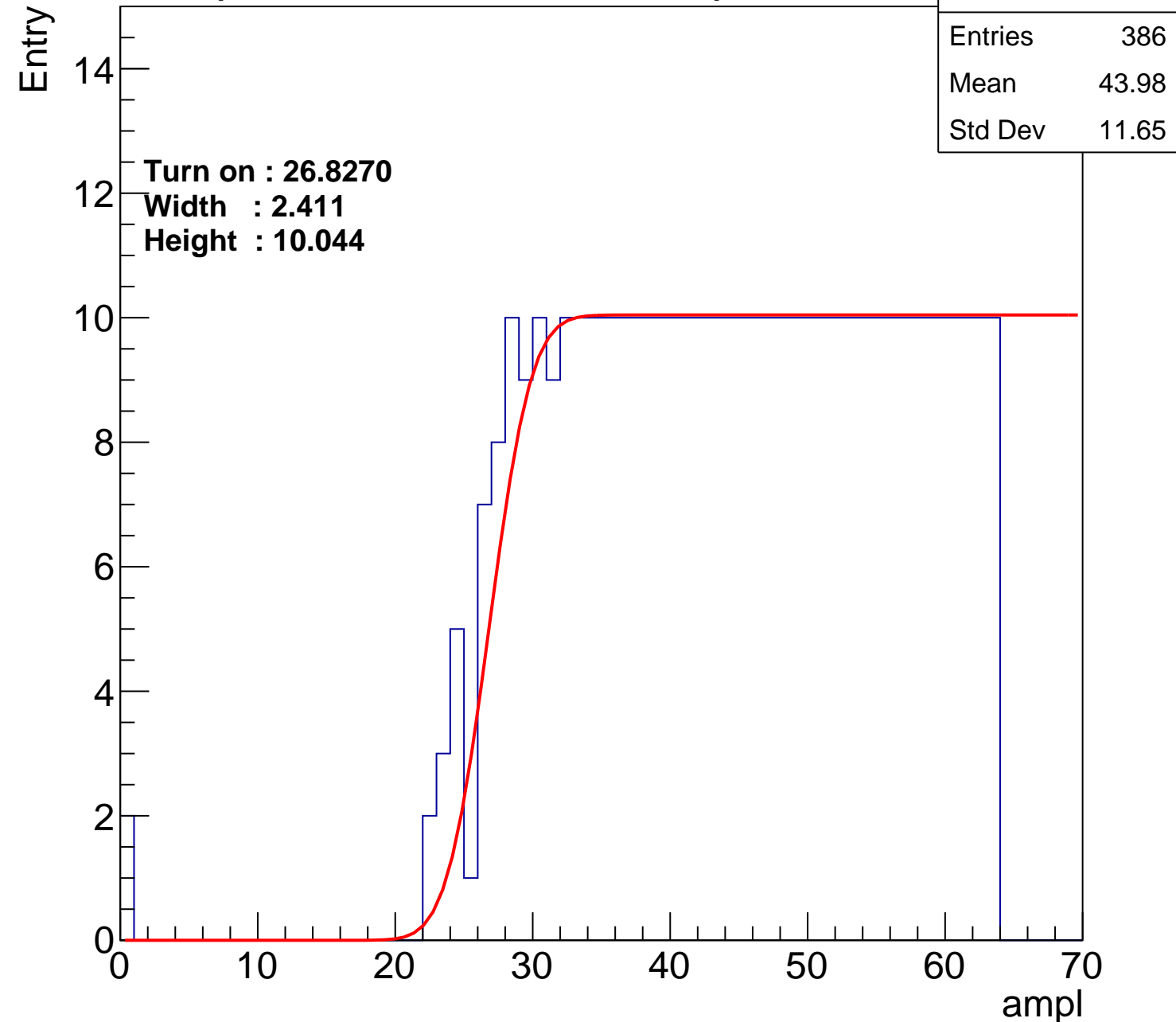
Width : 2.411

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch86

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.2267

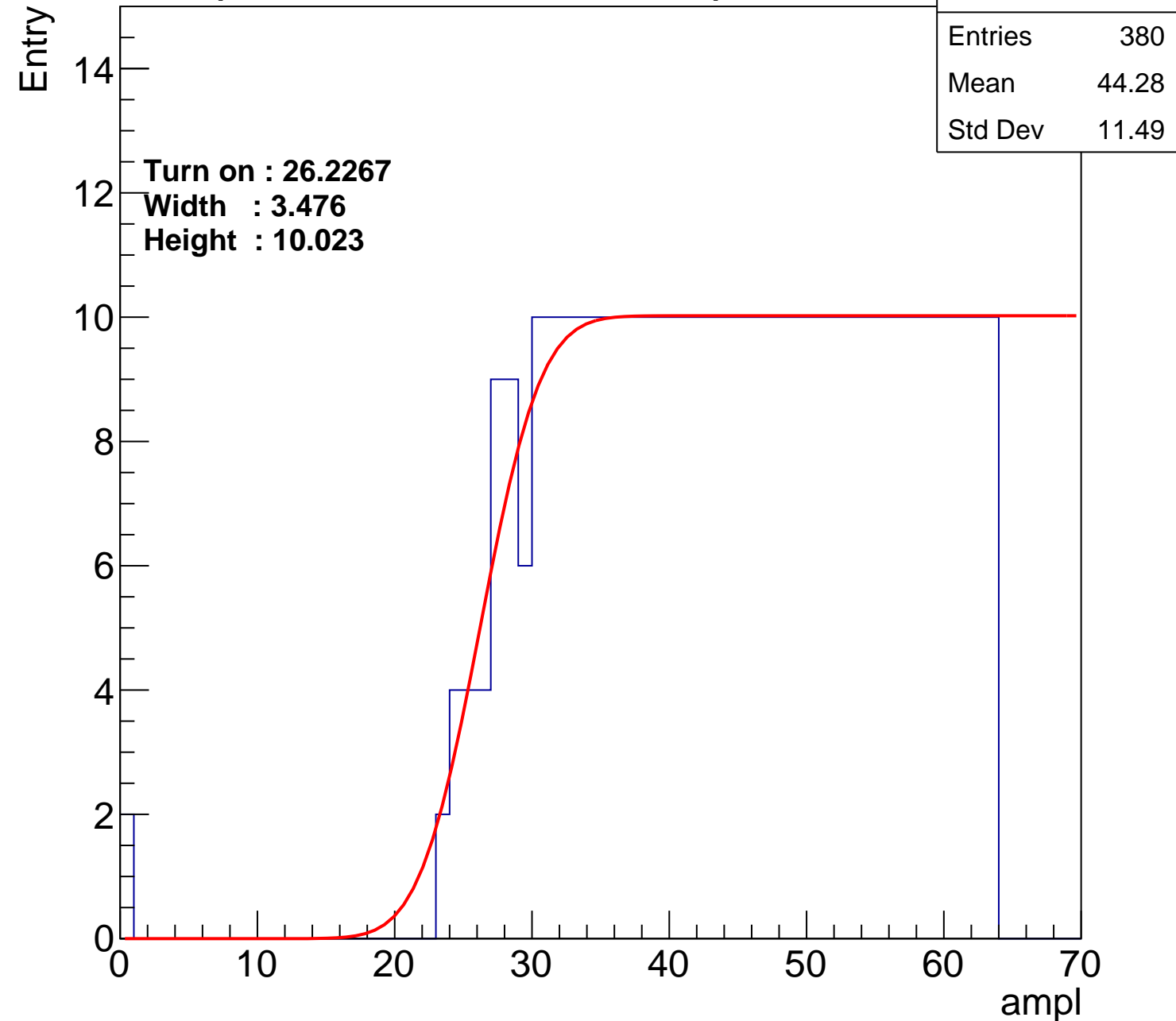
Width : 3.476

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U26-ch87

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.77
Std Dev	11.37

Turn on : 27.4425

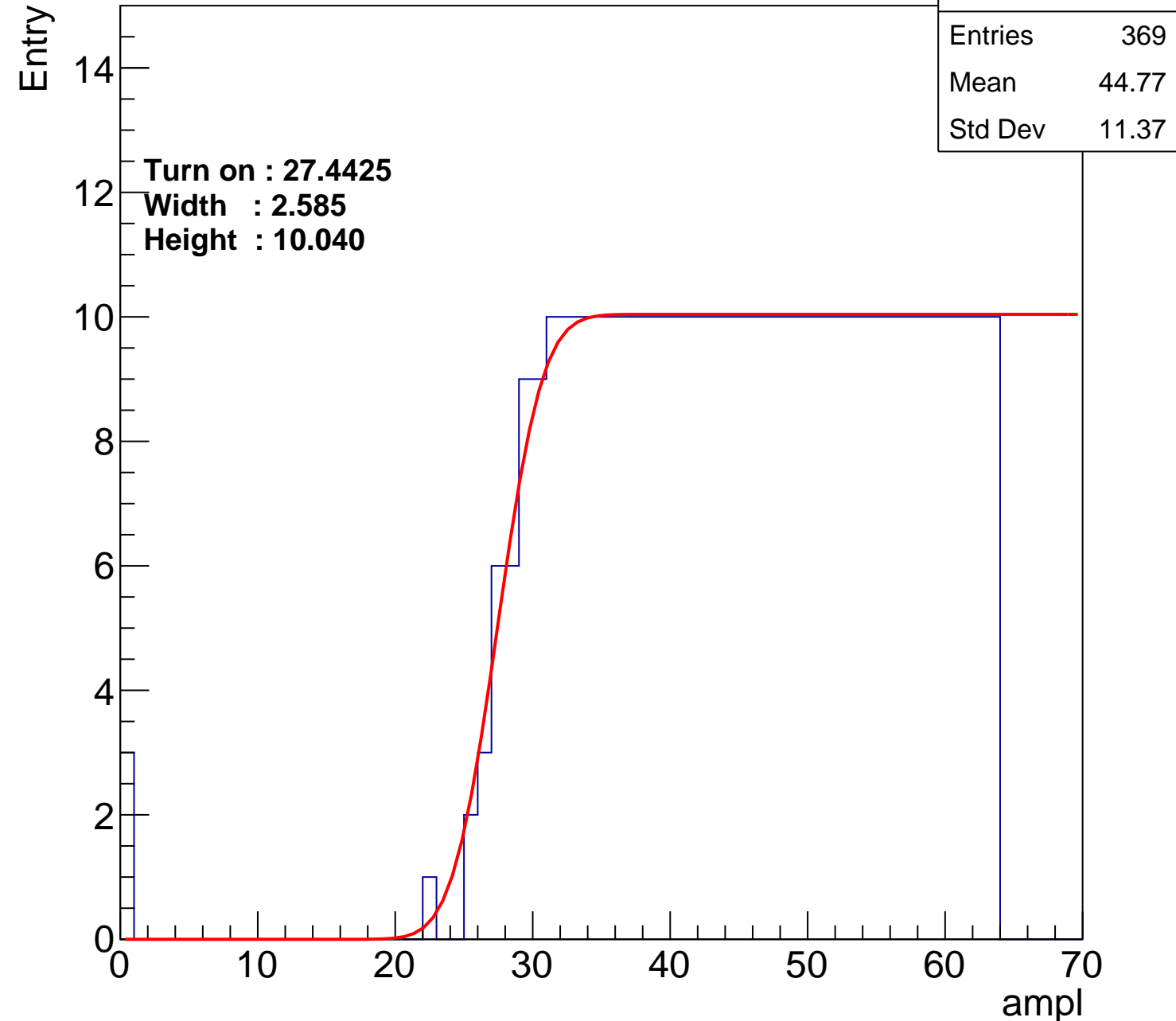
Width : 2.585

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch88

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.52
Std Dev	11.91

Turn on : 25.0212

Width : 2.556

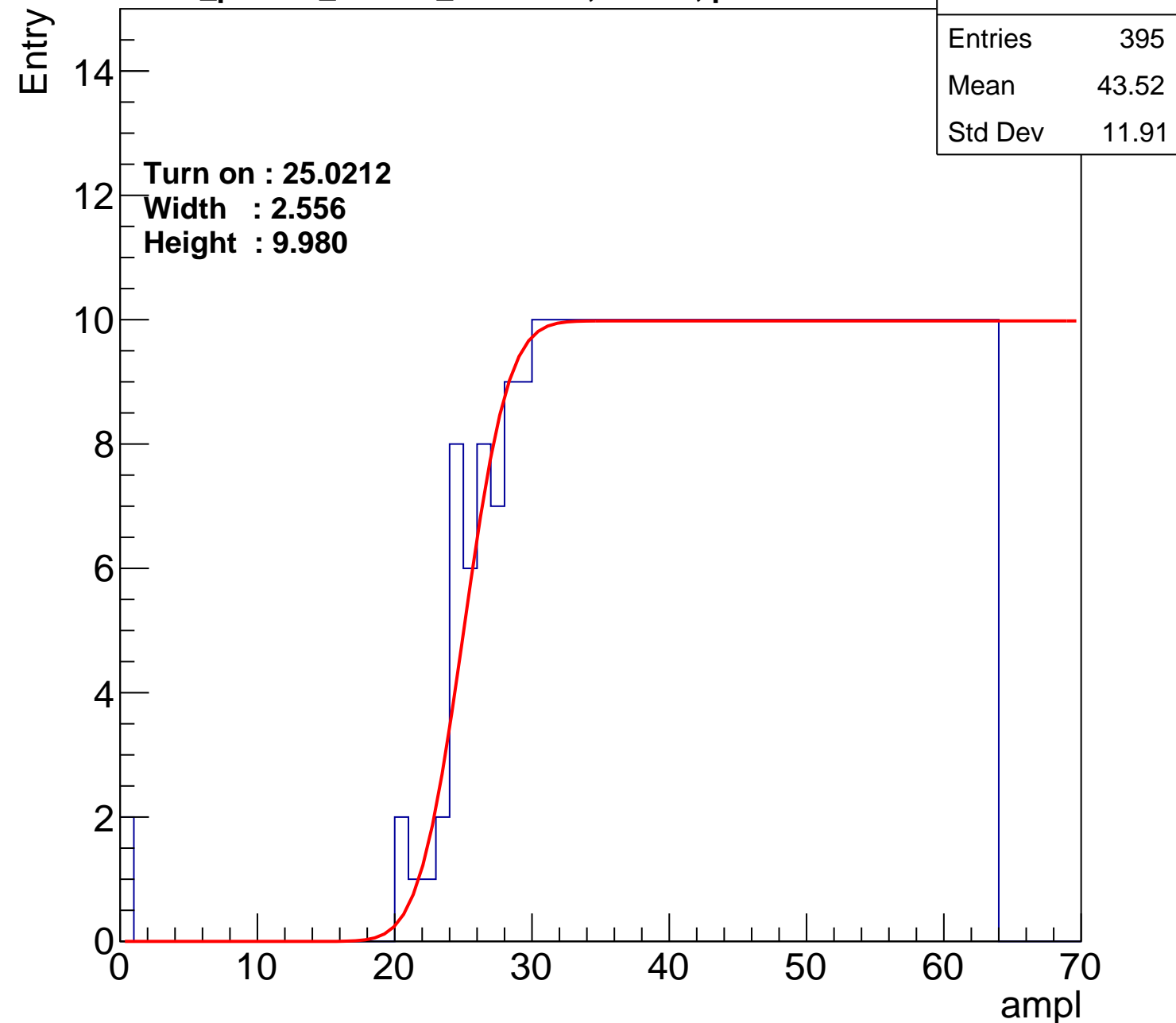
Height : 9.980

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U26-ch89

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	359
Mean	45.26
Std Dev	11.05

**Turn on : 29.0507**

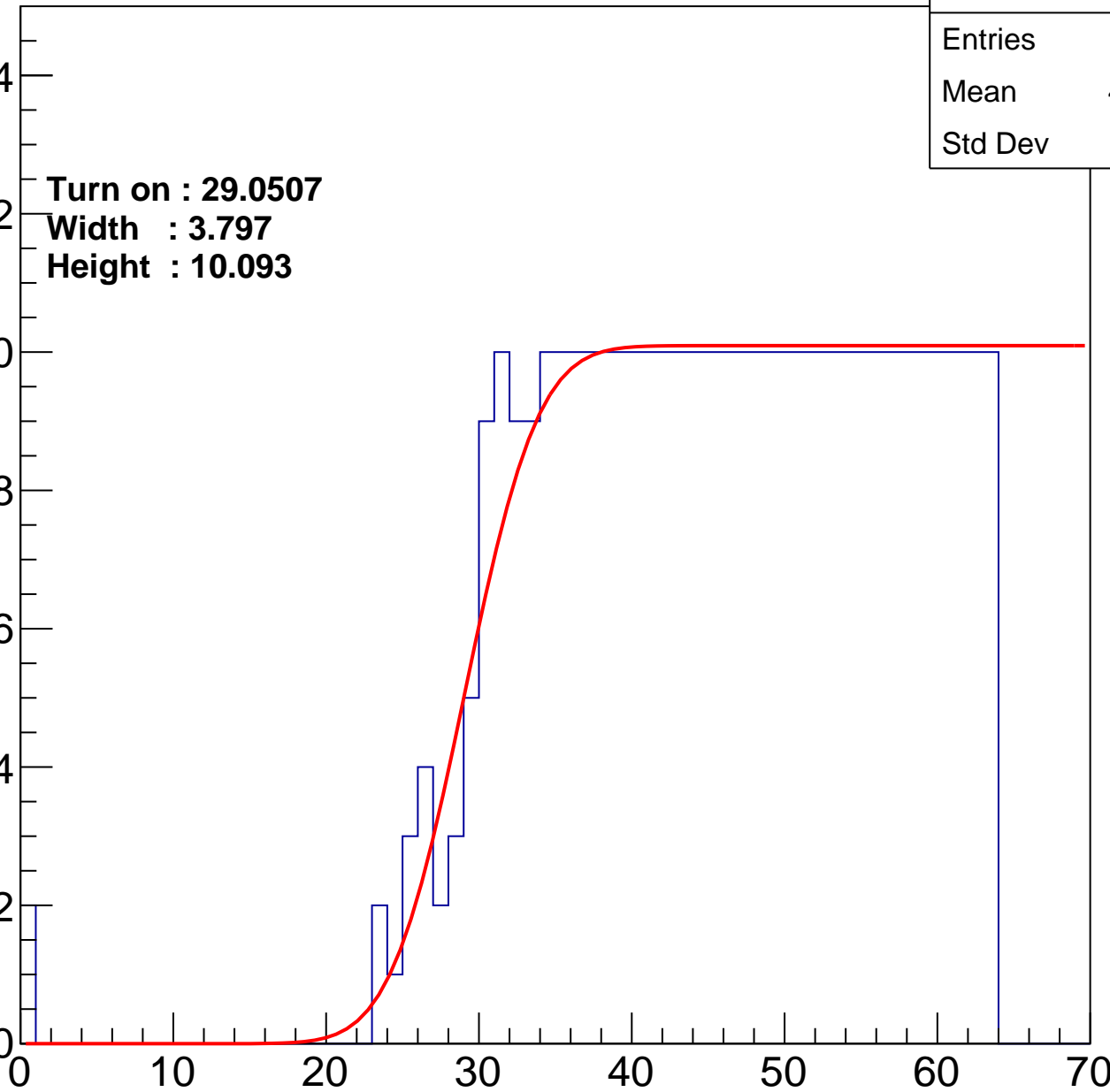
**Width : 3.797**

**Height : 10.093**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch90

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.15
Std Dev	11.46

Turn on : 25.3454

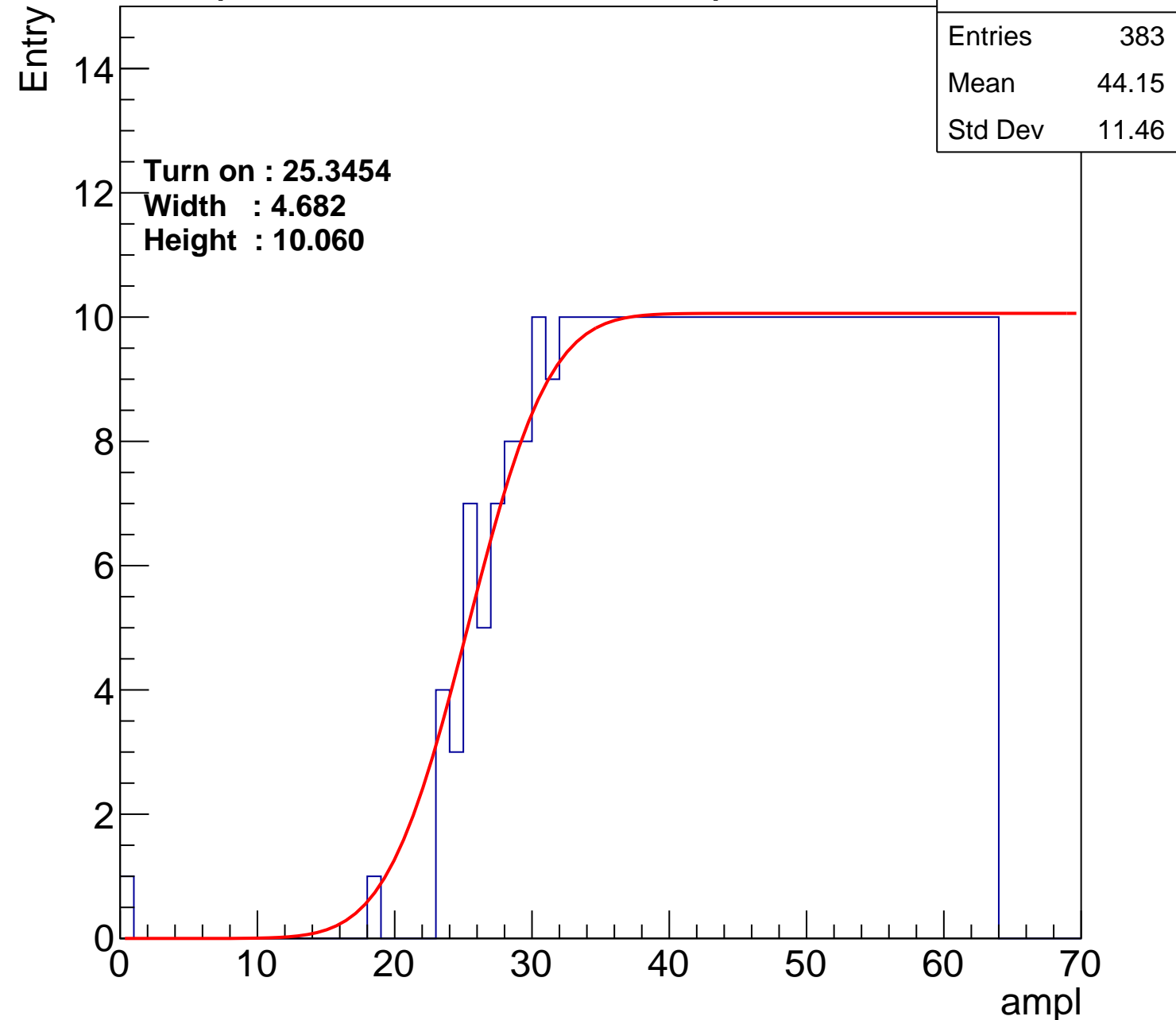
Width : 4.682

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch91

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.98
Std Dev	11.7

Turn on : 25.7031

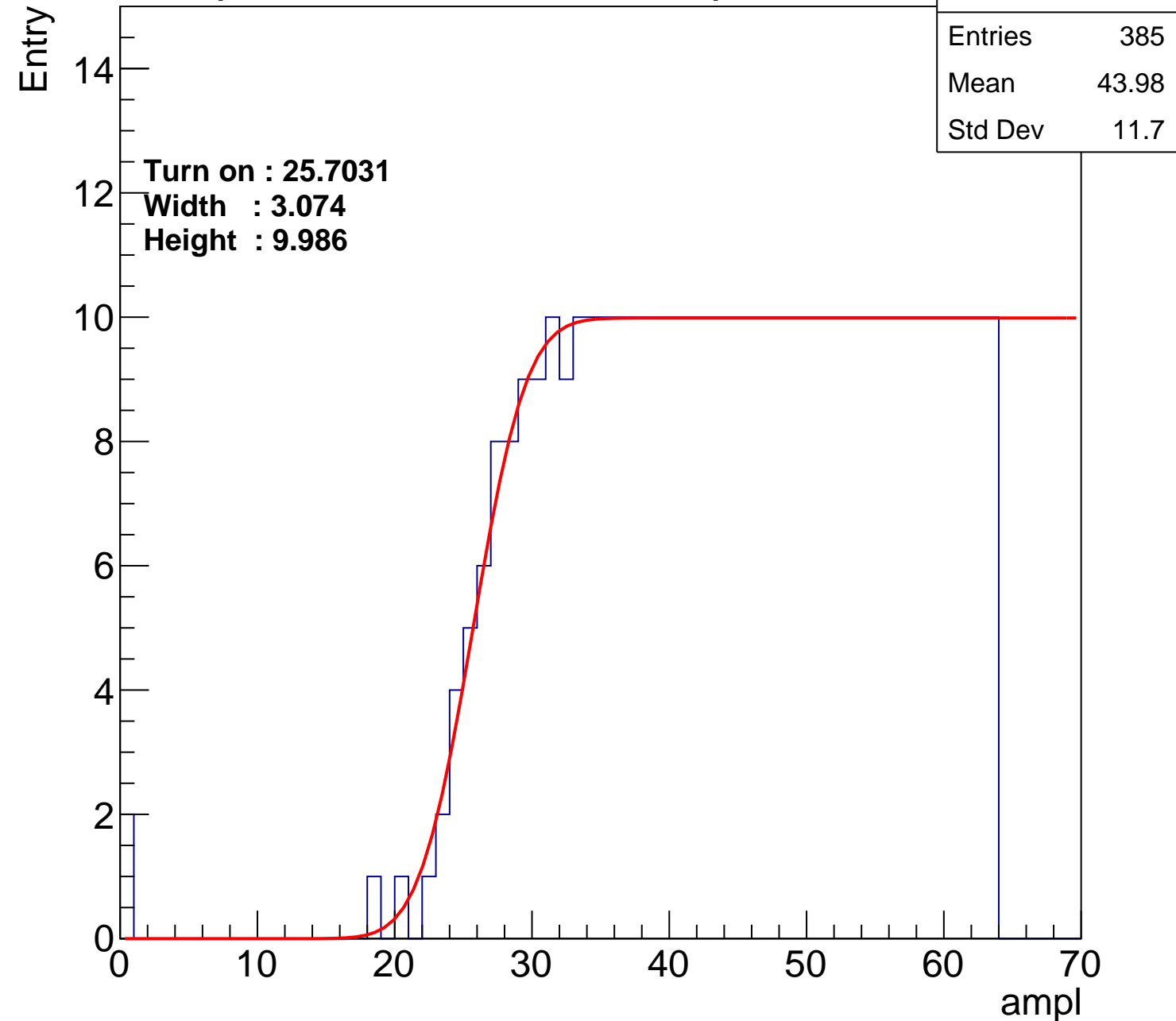
Width : 3.074

Height : 9.986

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch92

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.84
Std Dev	11.88

Turn on : 25.8443

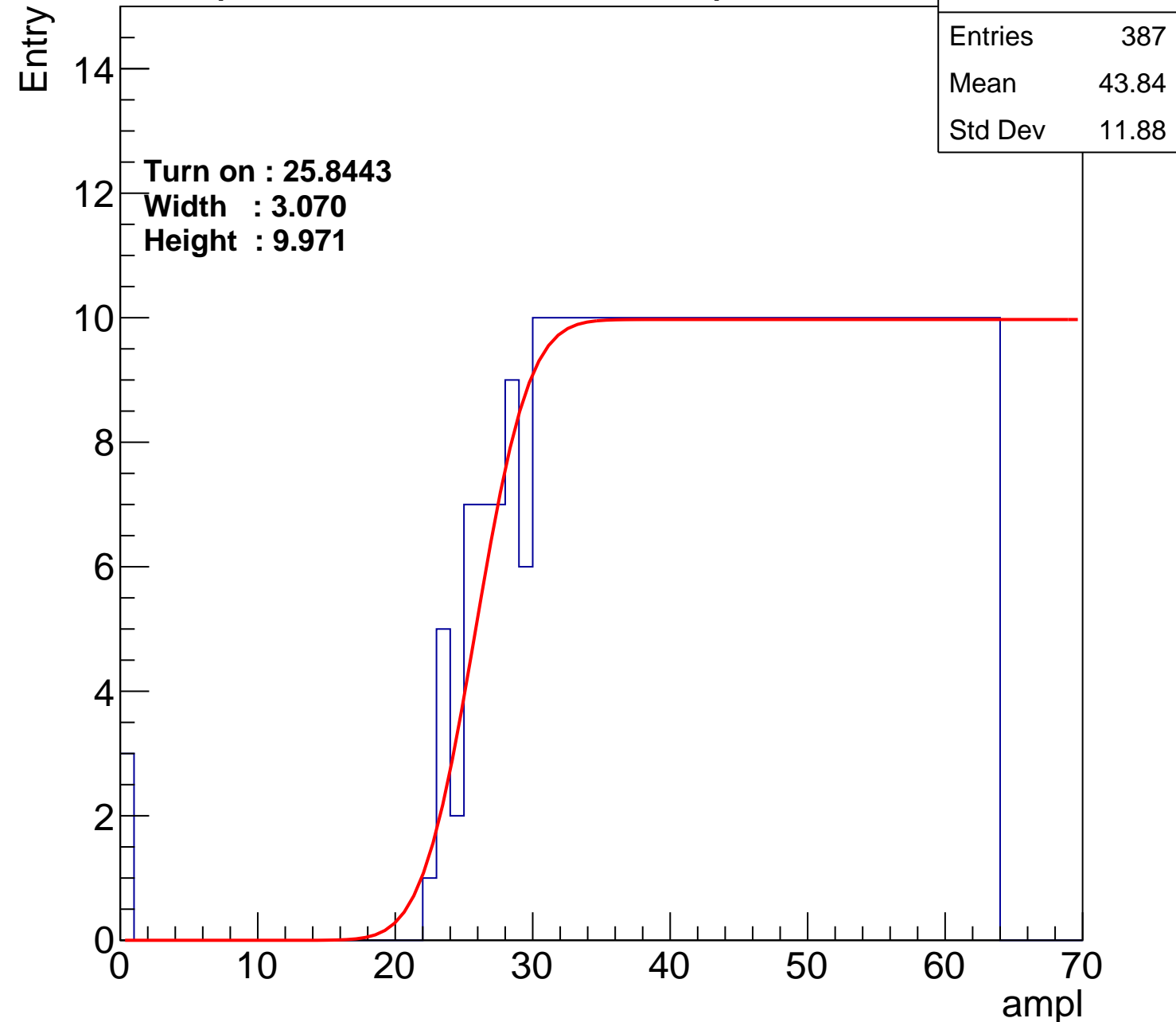
Width : 3.070

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch93

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.18
Std Dev	11.74

Turn on : 26.0429

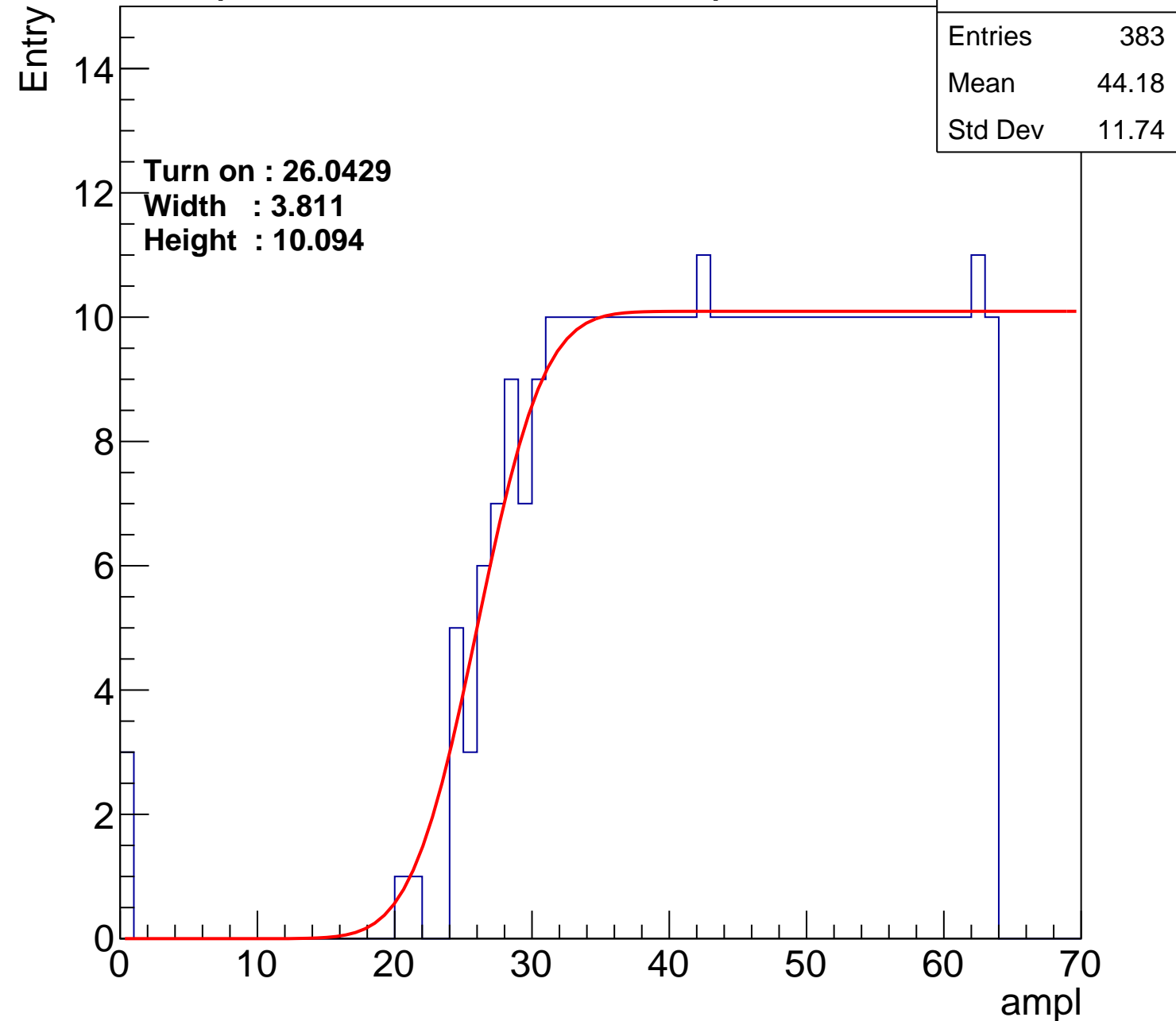
Width : 3.811

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch94

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.3
Std Dev	11.51

Turn on : 26.4112

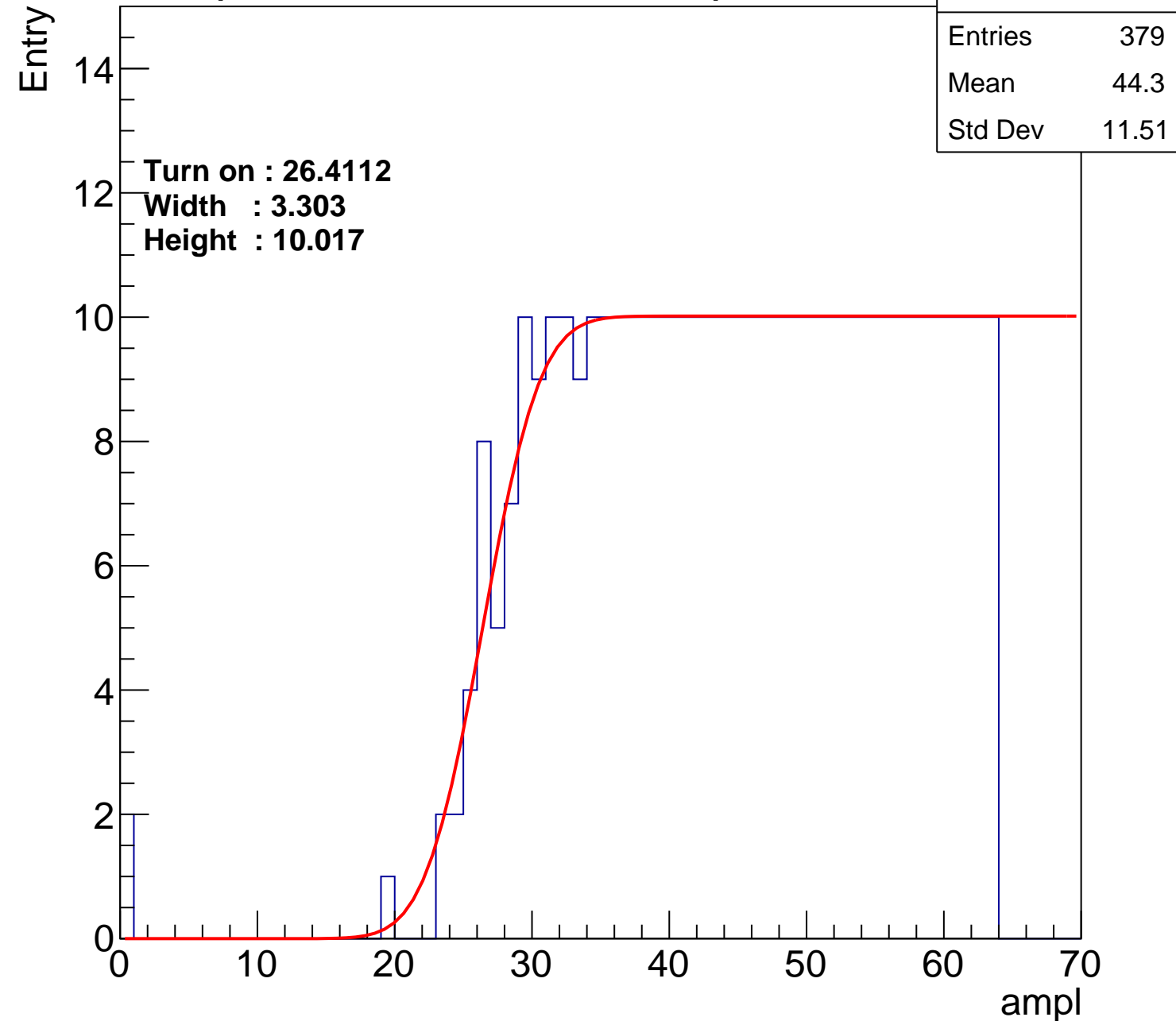
Width : 3.303

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U26-ch95

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.83
Std Dev	11.11

Turn on : 27.3354

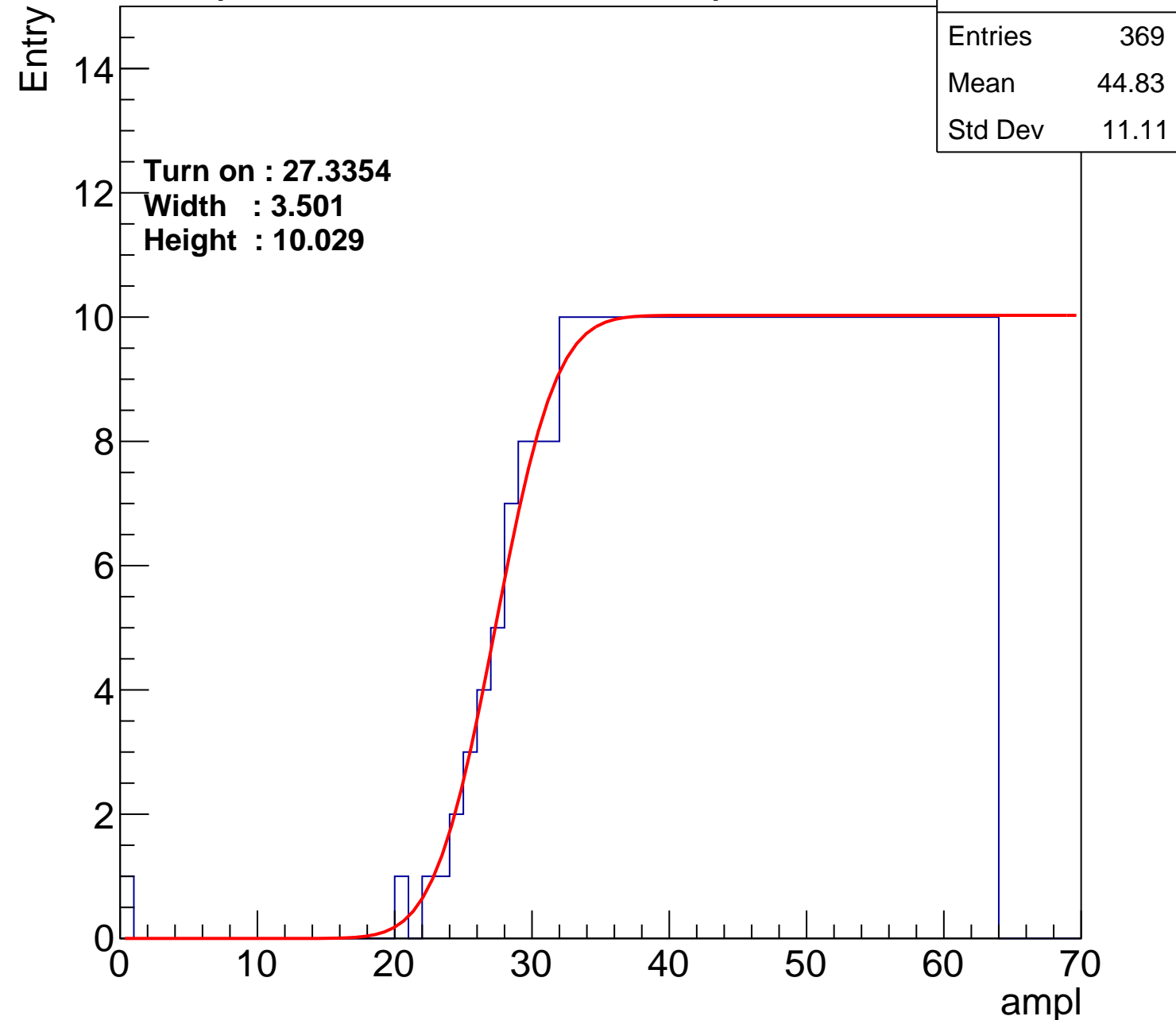
Width : 3.501

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch96

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	44.03
Std Dev	11.62

**Turn on : 25.8066**

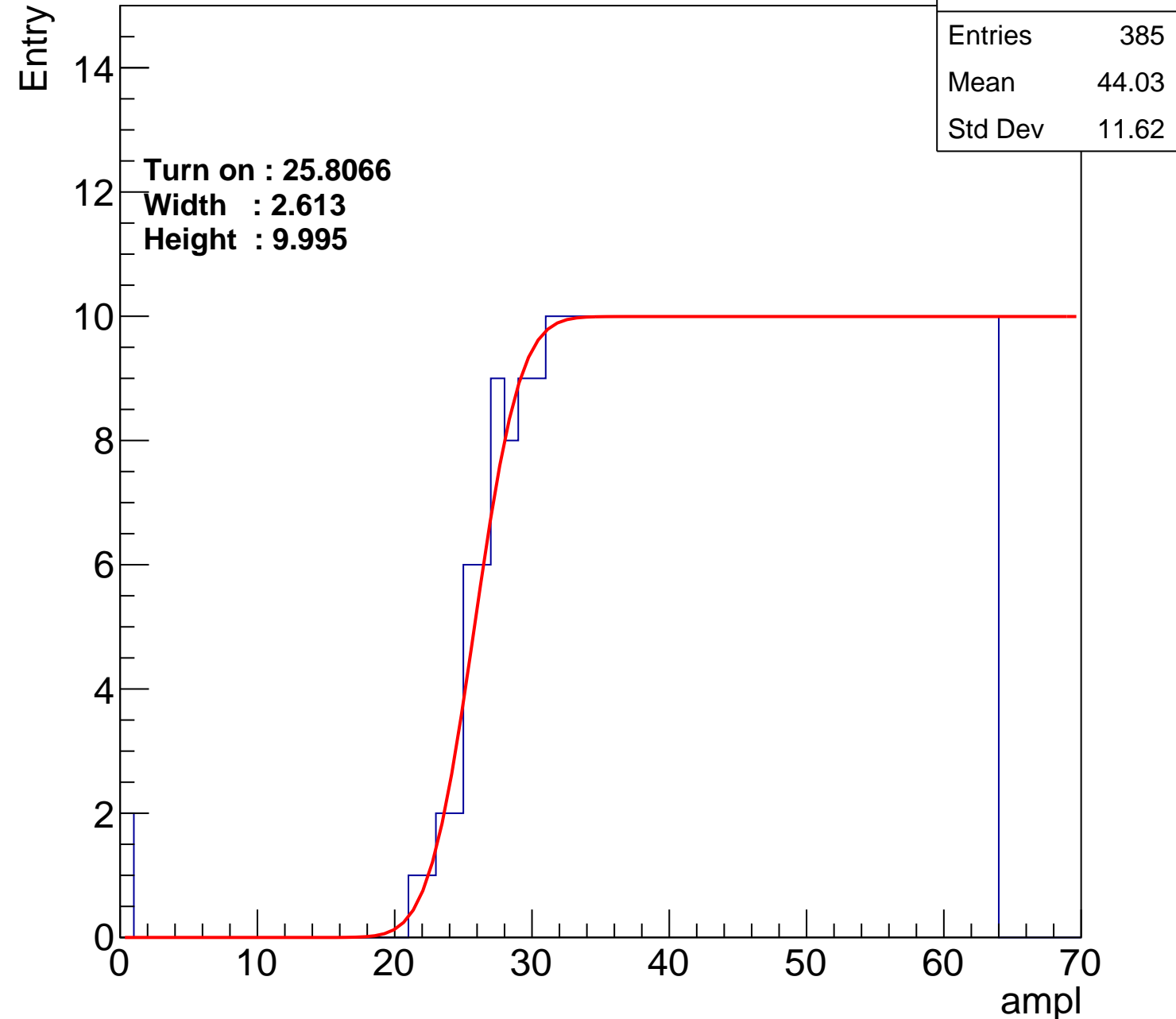
**Width : 2.613**

**Height : 9.995**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch97

calib\_packv5\_042523\_0143.root, FC#12, port B1

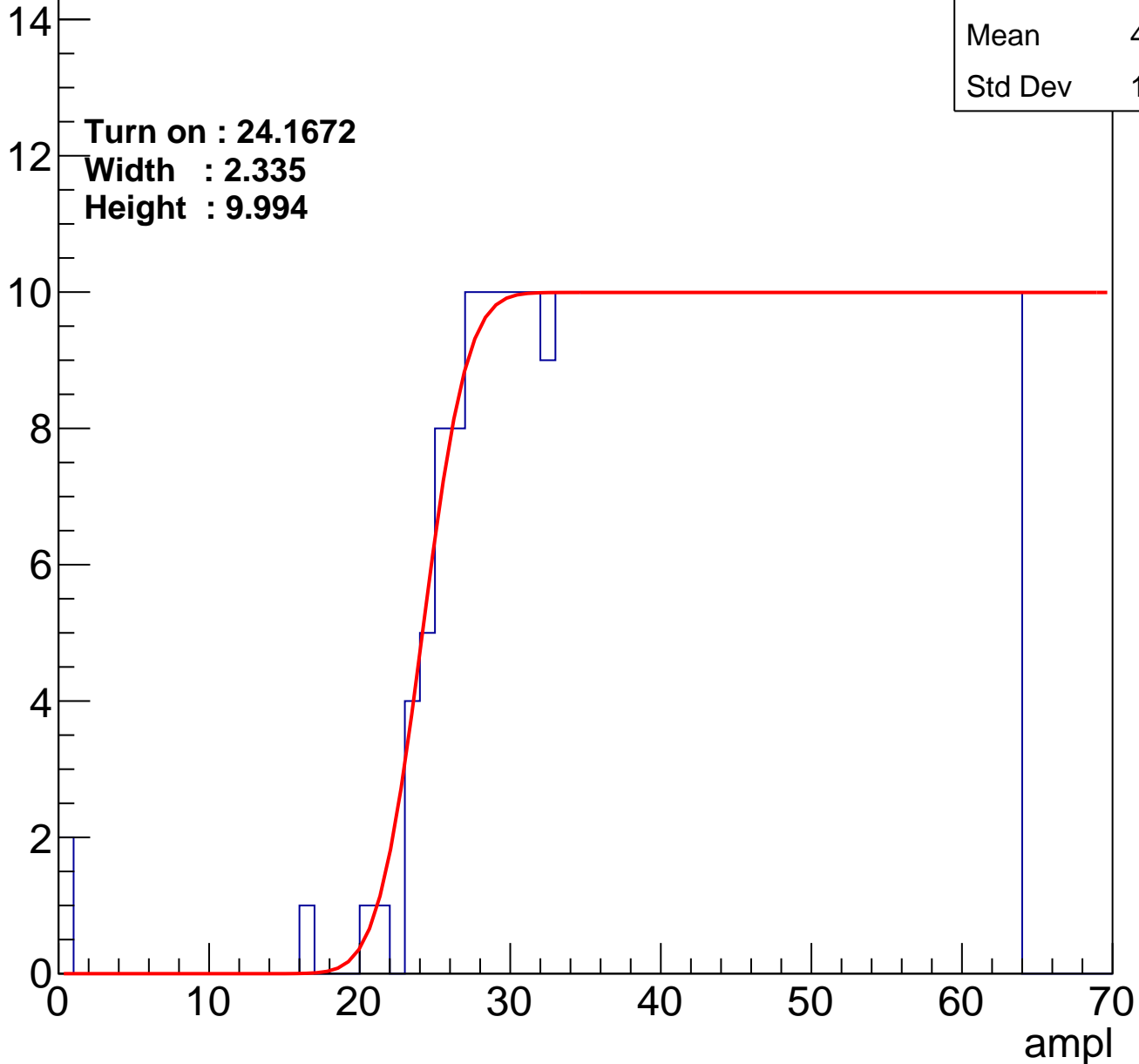
Entries	399
Mean	43.35
Std Dev	11.98

**Turn on : 24.1672**

**Width : 2.335**

**Height : 9.994**

Entry



# B0L102S, U26-ch98

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.92
Std Dev	11.69

Turn on : 25.6537

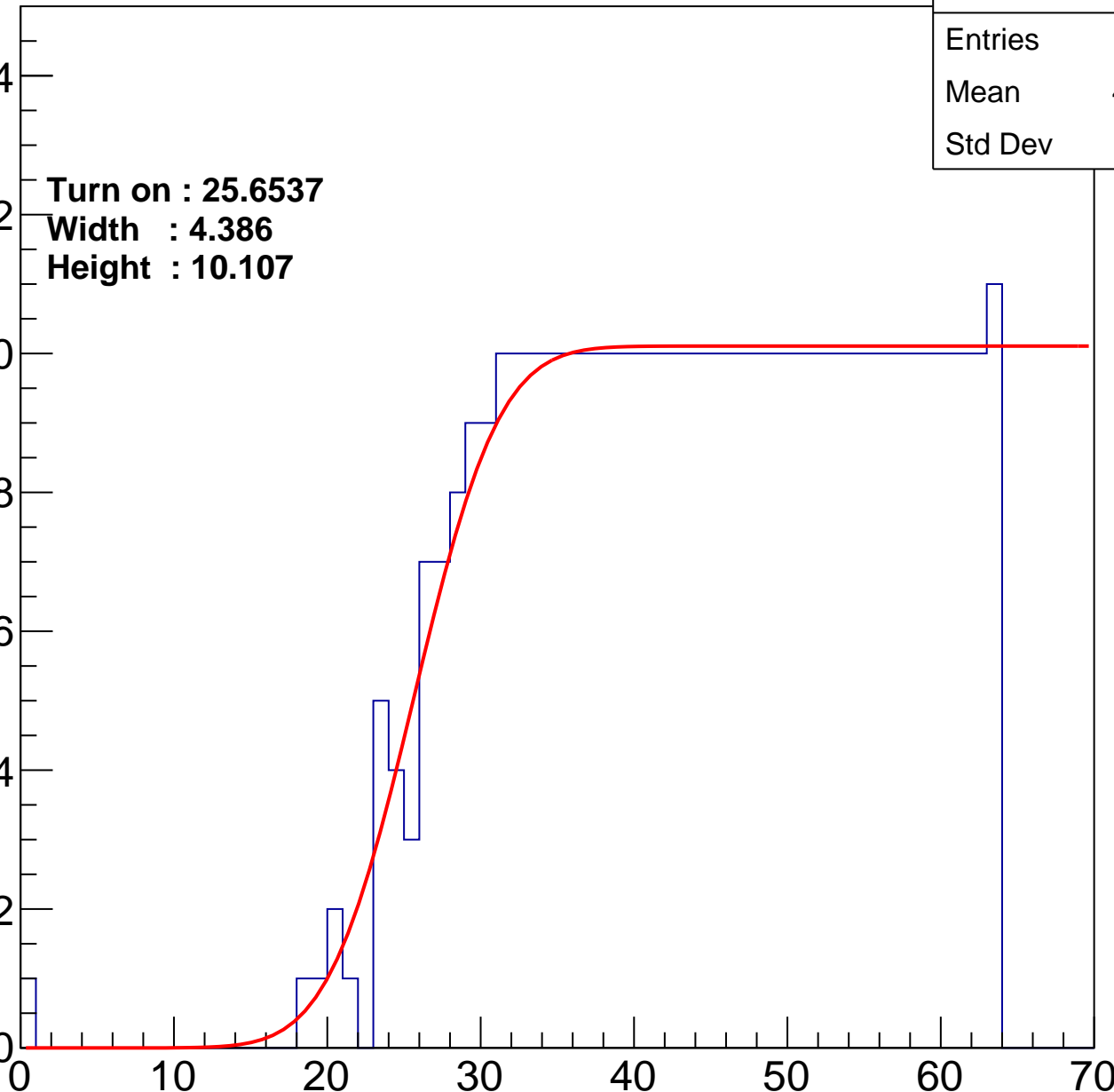
Width : 4.386

Height : 10.107

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch99

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.61
Std Dev	11.98

Turn on : 24.6525

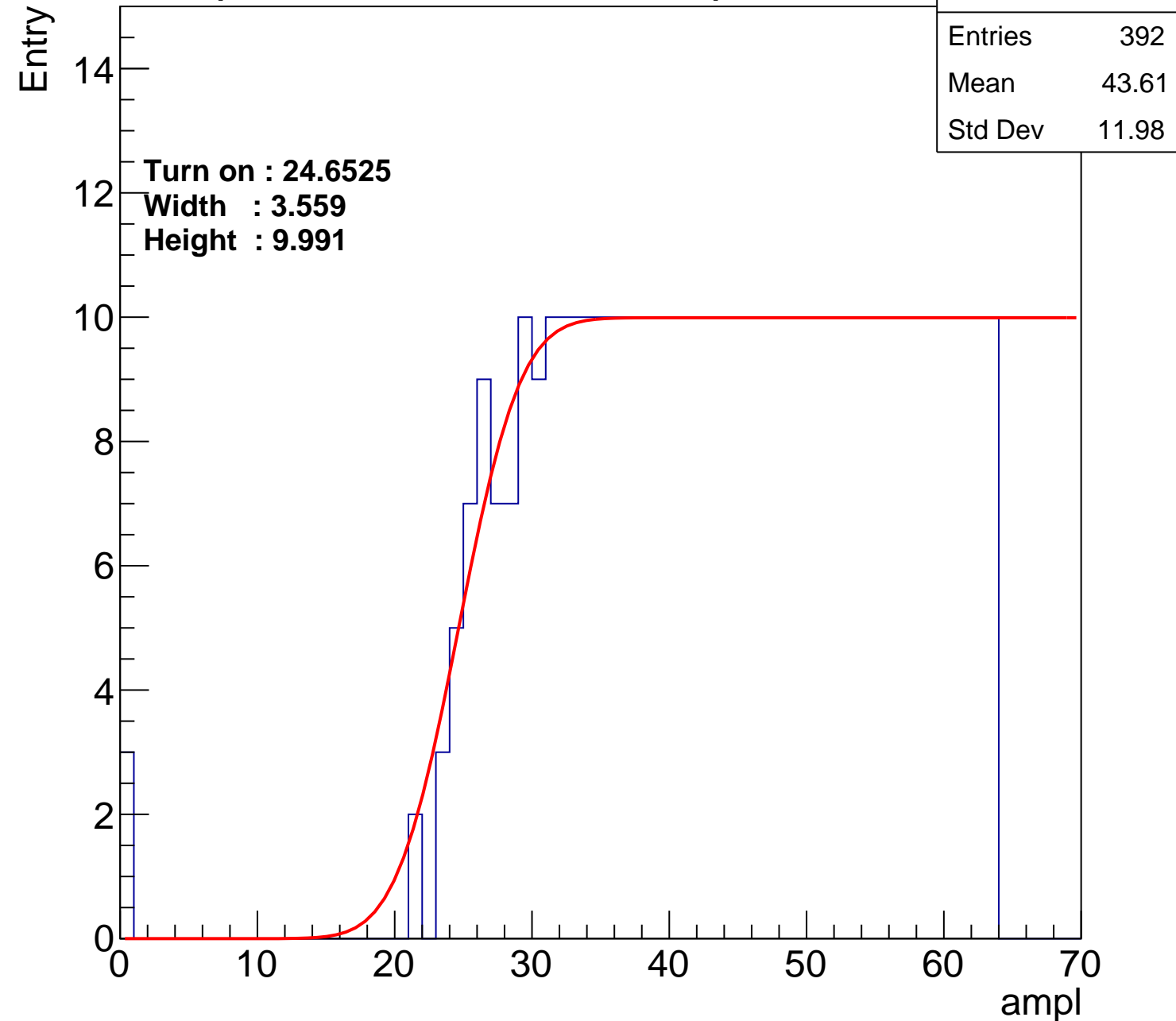
Width : 3.559

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch100

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	45.06
Std Dev	11.46

Turn on : 28.4579

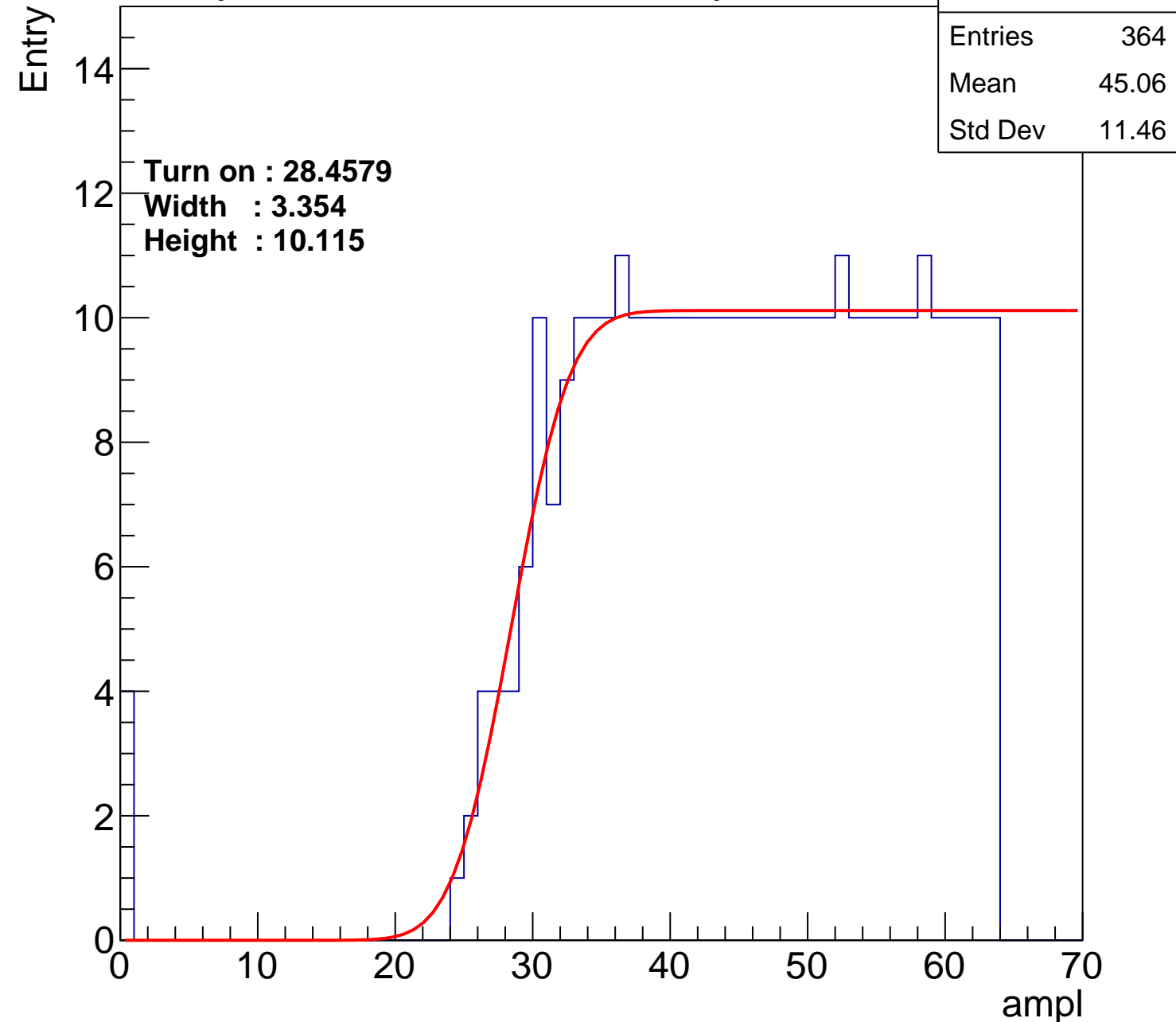
Width : 3.354

Height : 10.115

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch101

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	377
Mean	44.43
Std Dev	11.4

Turn on : 26.5290

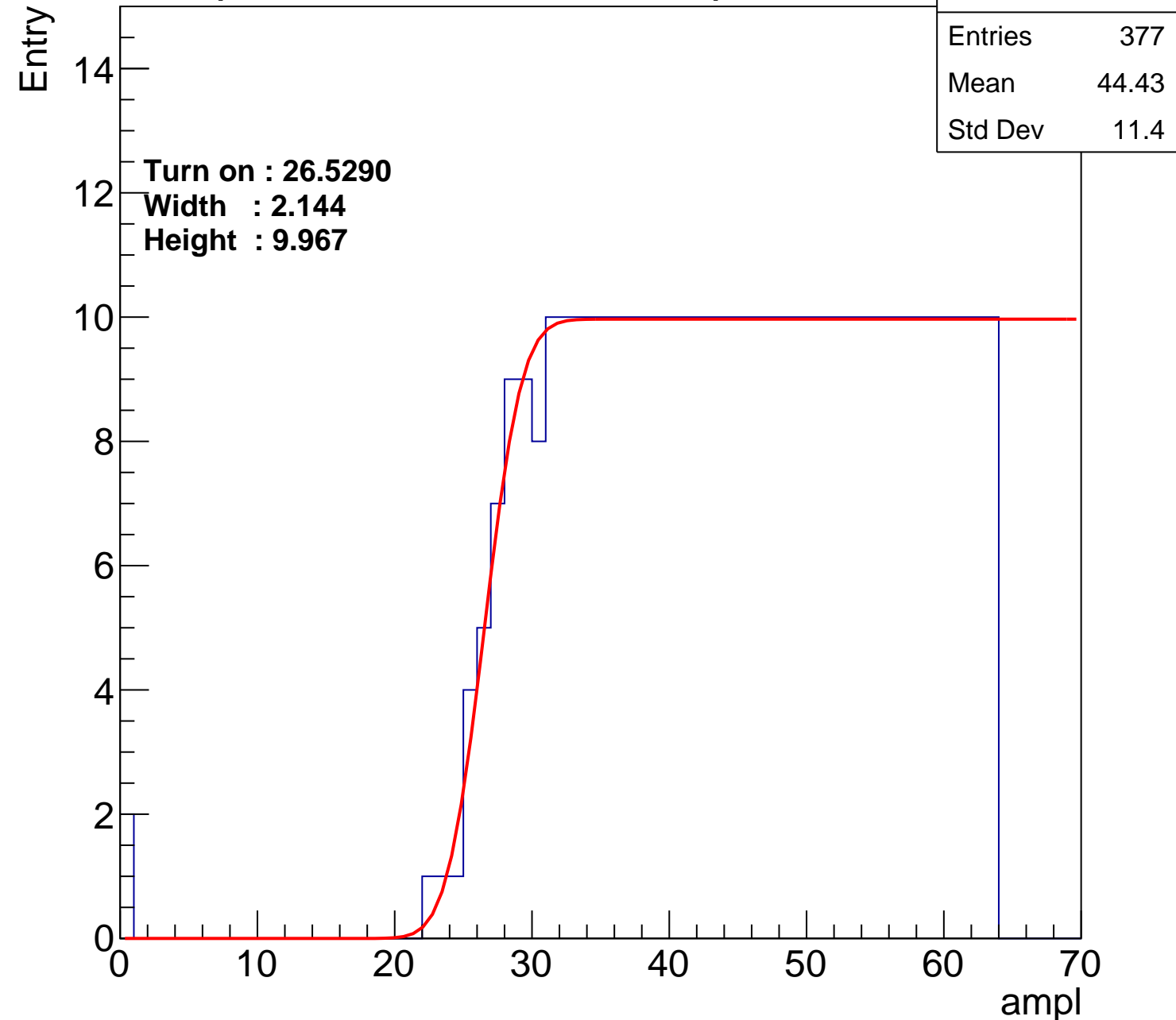
Width : 2.144

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch102

calib\_packv5\_042523\_0143.root, FC#12, port B1

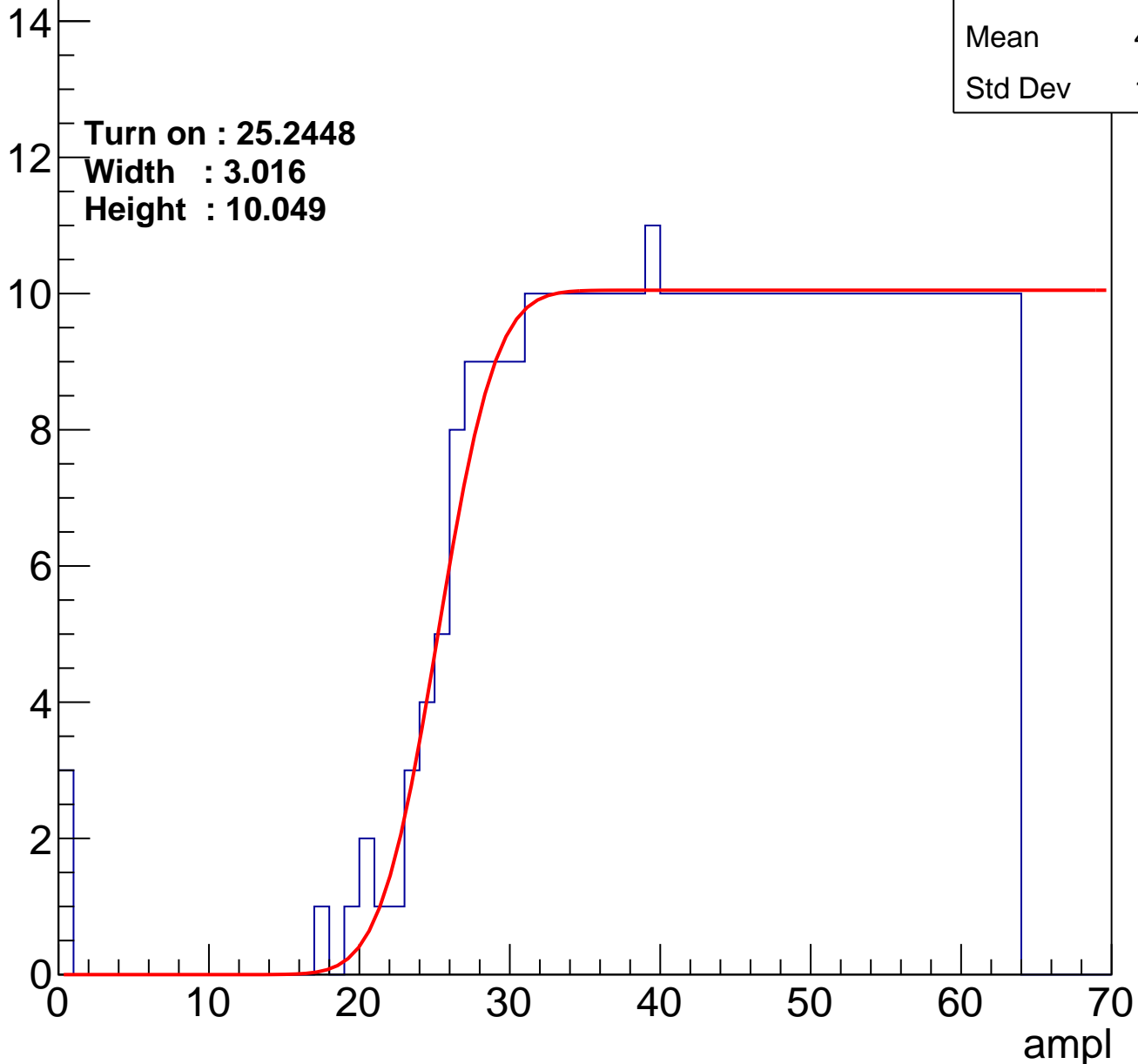
Entries	396
Mean	43.41
Std Dev	12.11

Turn on : 25.2448

Width : 3.016

Height : 10.049

Entry





# B0L102S, U26-ch103

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.64
Std Dev	11.56

Turn on : 27.9415

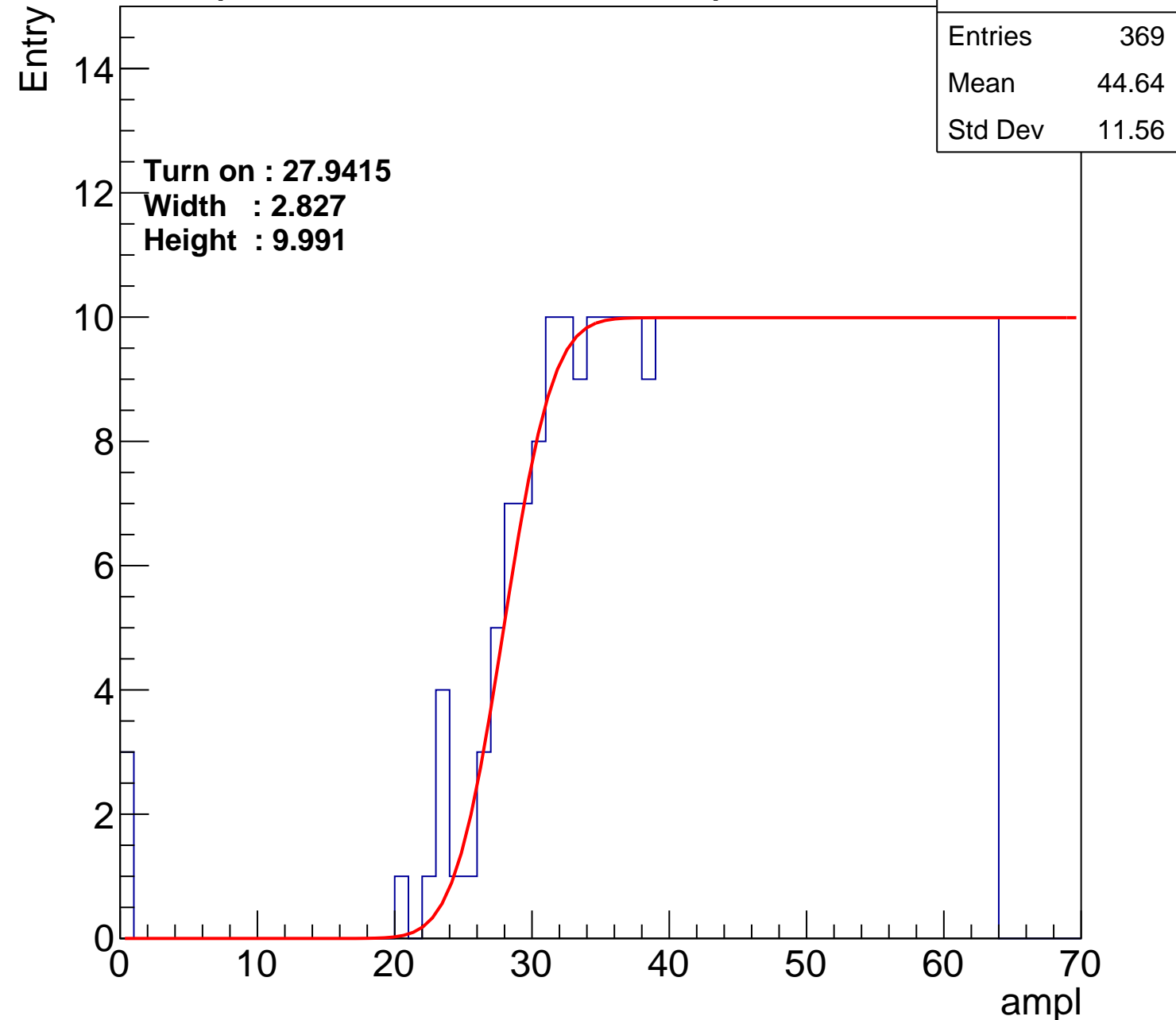
Width : 2.827

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch104

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.25
Std Dev	12.35

Turn on : 25.1216

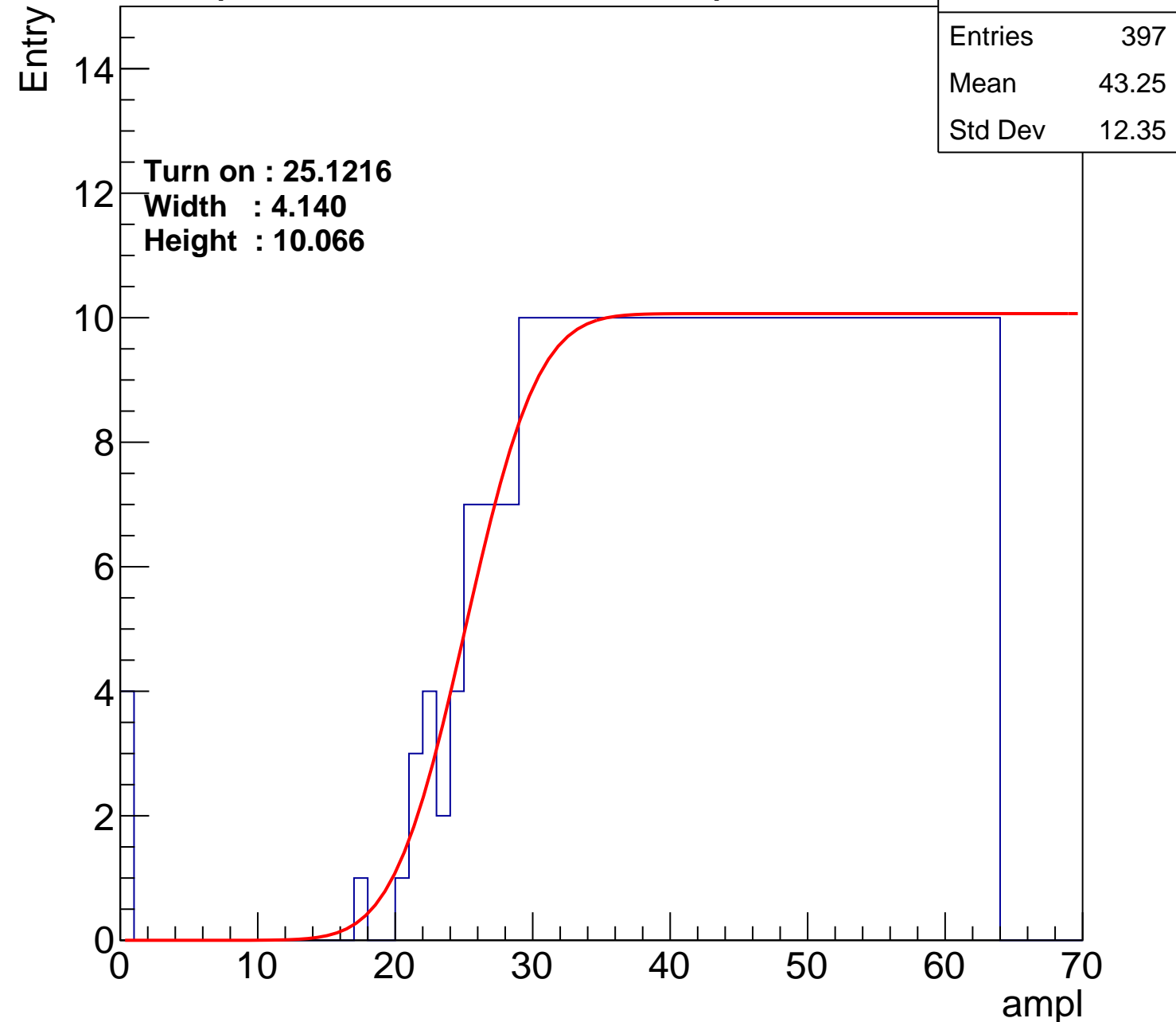
Width : 4.140

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch105

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.91
Std Dev	11.88

Turn on : 25.2132

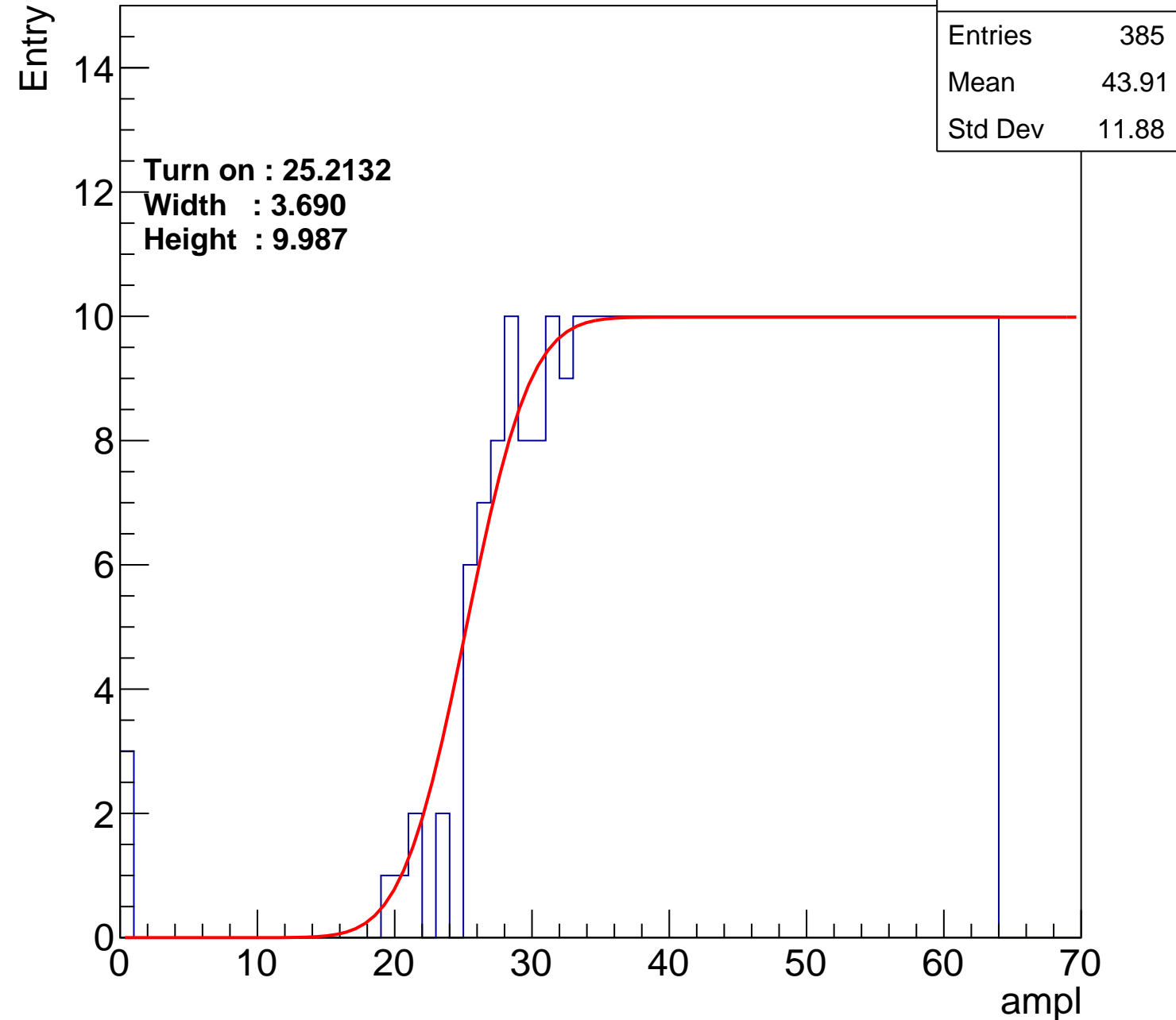
Width : 3.690

Height : 9.987

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch106

calib\_packv5\_042523\_0143.root, FC#12, port B1

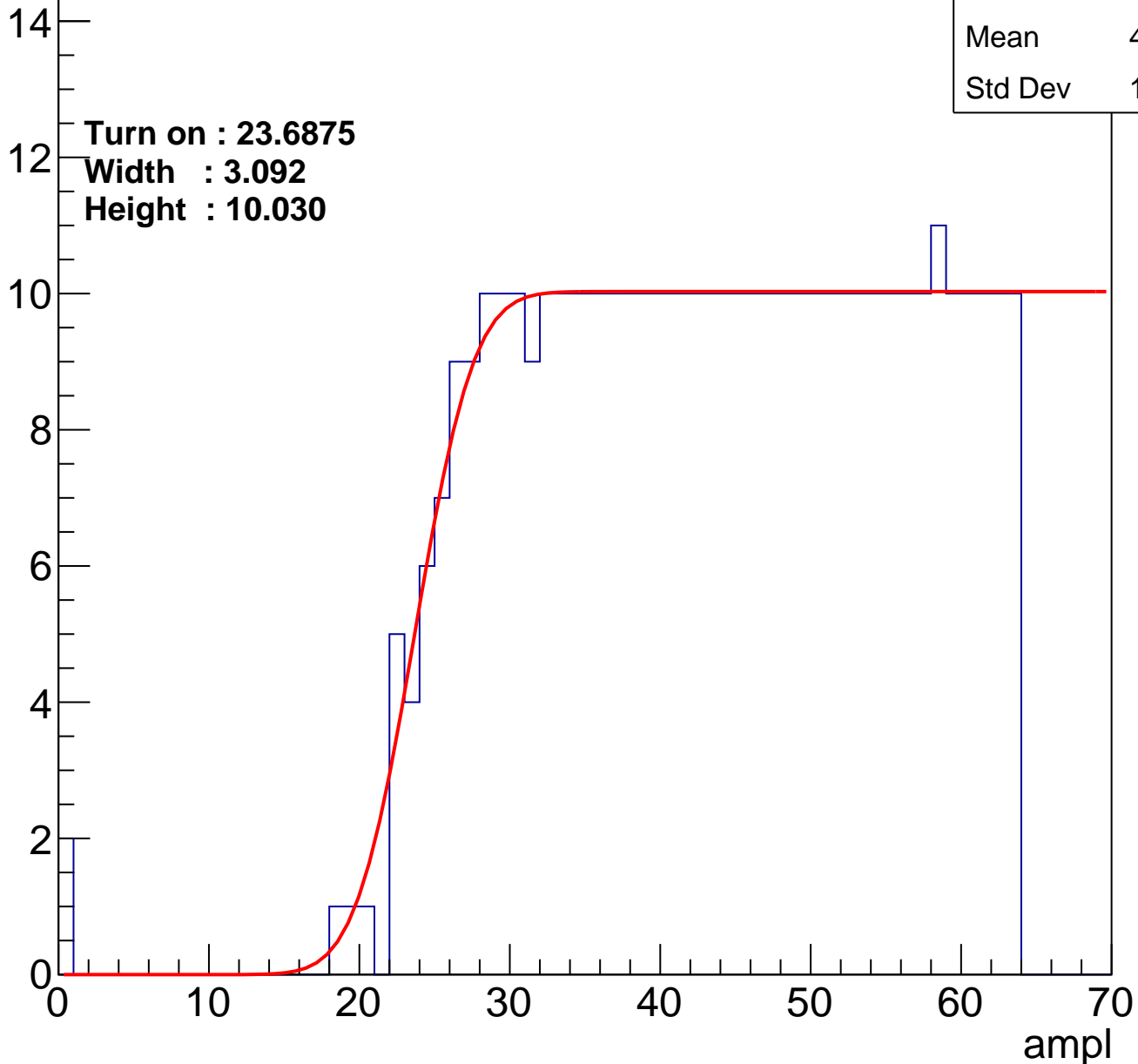
Entries	405
Mean	43.12
Std Dev	12.15

Turn on : 23.6875

Width : 3.092

Height : 10.030

Entry



# B0L102S, U26-ch107

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	386
Mean	44.04
Std Dev	11.57

Turn on : 25.8267

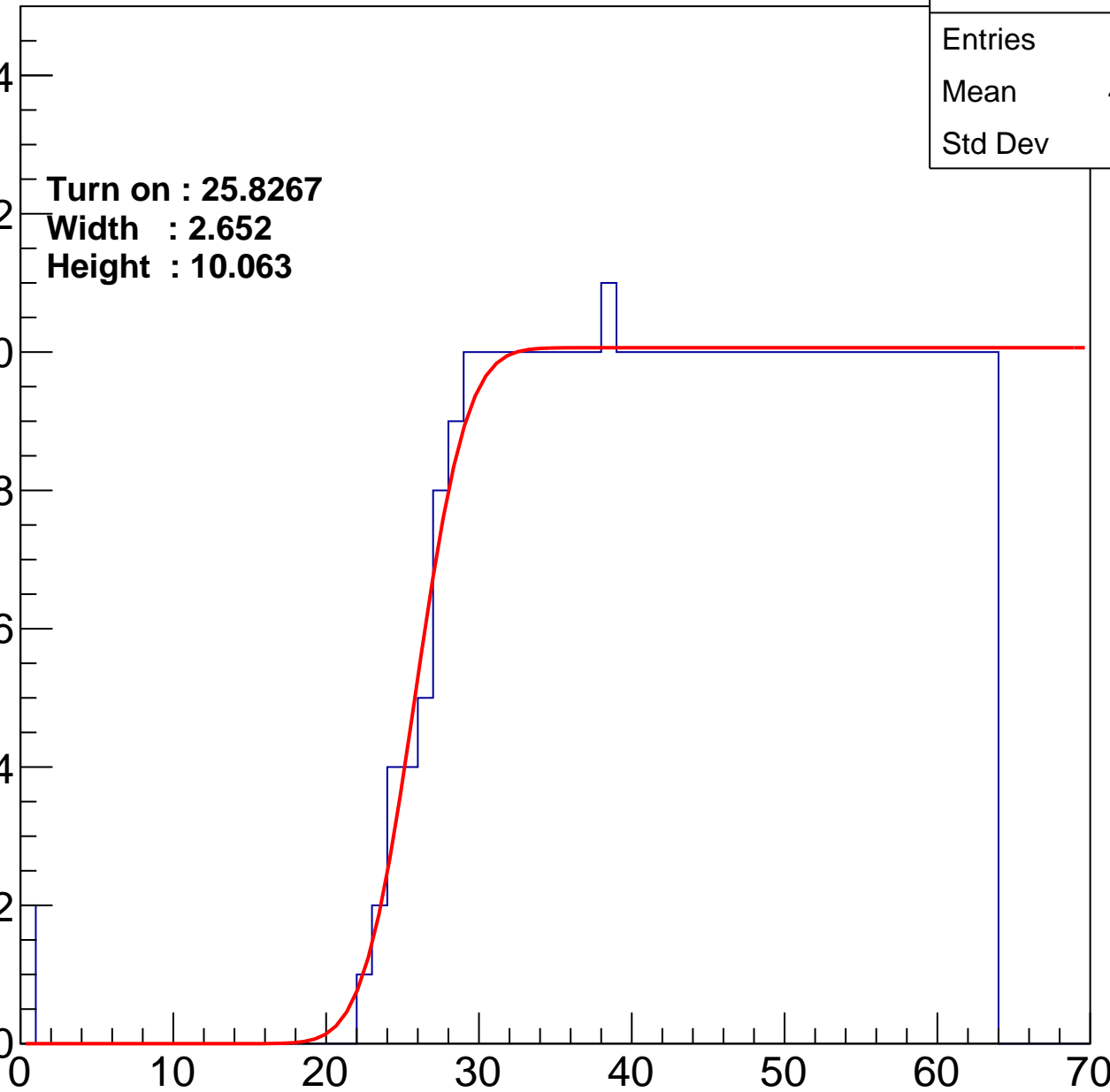
Width : 2.652

Height : 10.063

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch108

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.62
Std Dev	11.18

Turn on : 27.3878

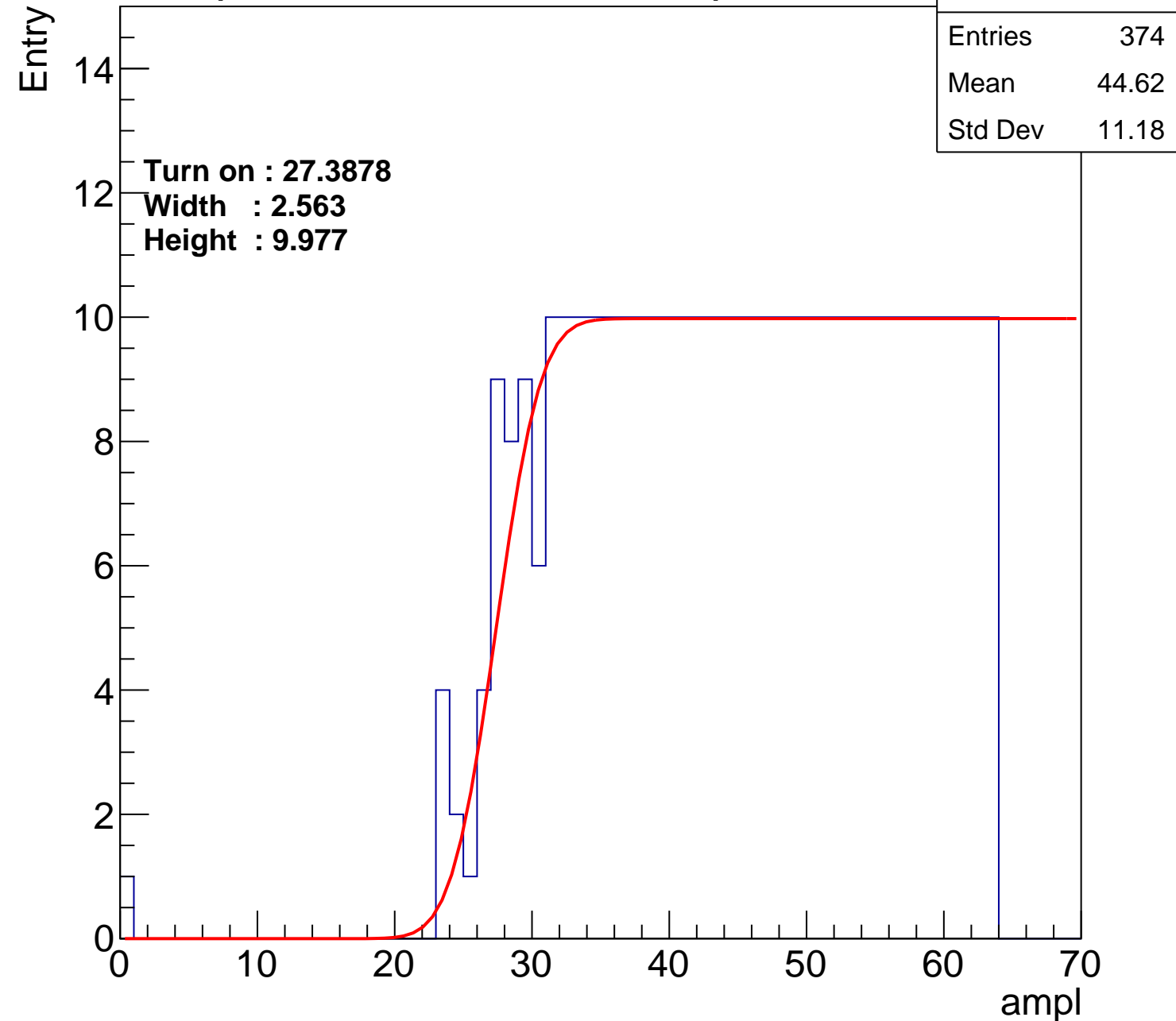
Width : 2.563

Height : 9.977

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch109

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.21
Std Dev	11.42

Turn on : 26.3112

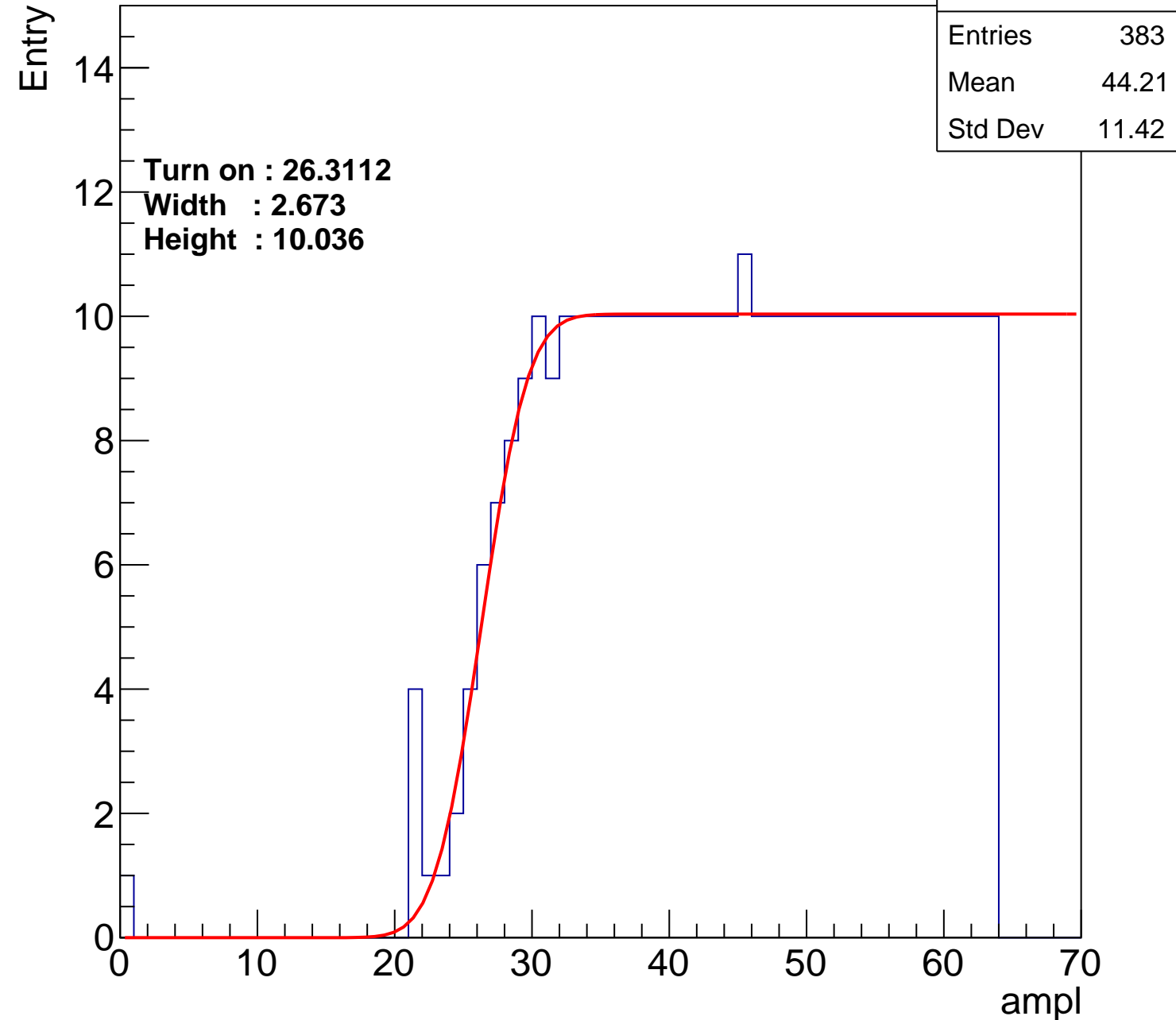
Width : 2.673

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch110

calib\_packv5\_042523\_0143.root, FC#12, port B1

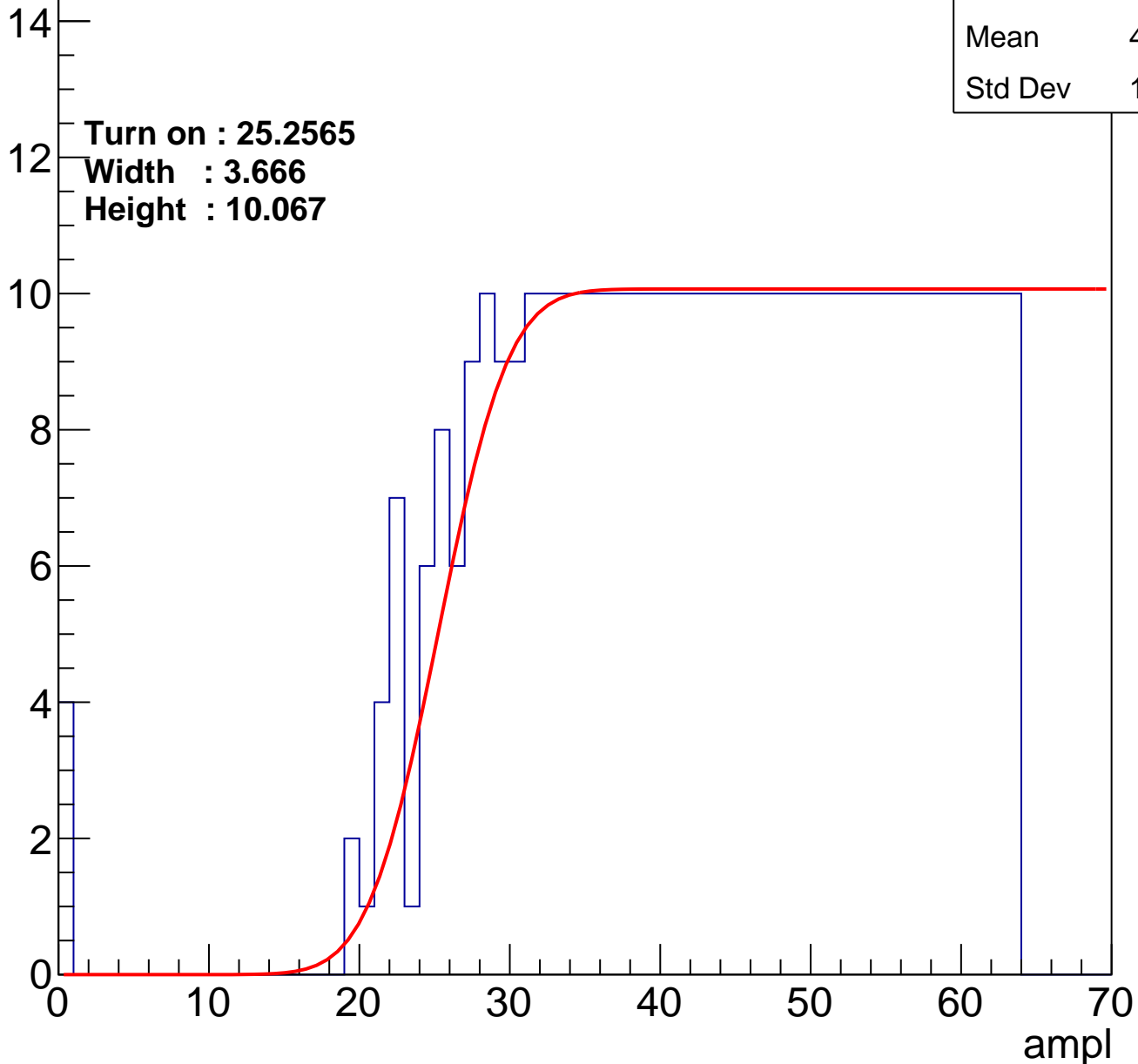
Entries	406
Mean	42.82
Std Dev	12.56

Turn on : 25.2565

Width : 3.666

Height : 10.067

Entry





# B0L102S, U26-ch111

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.17
Std Dev	11.65

Turn on : 26.1371

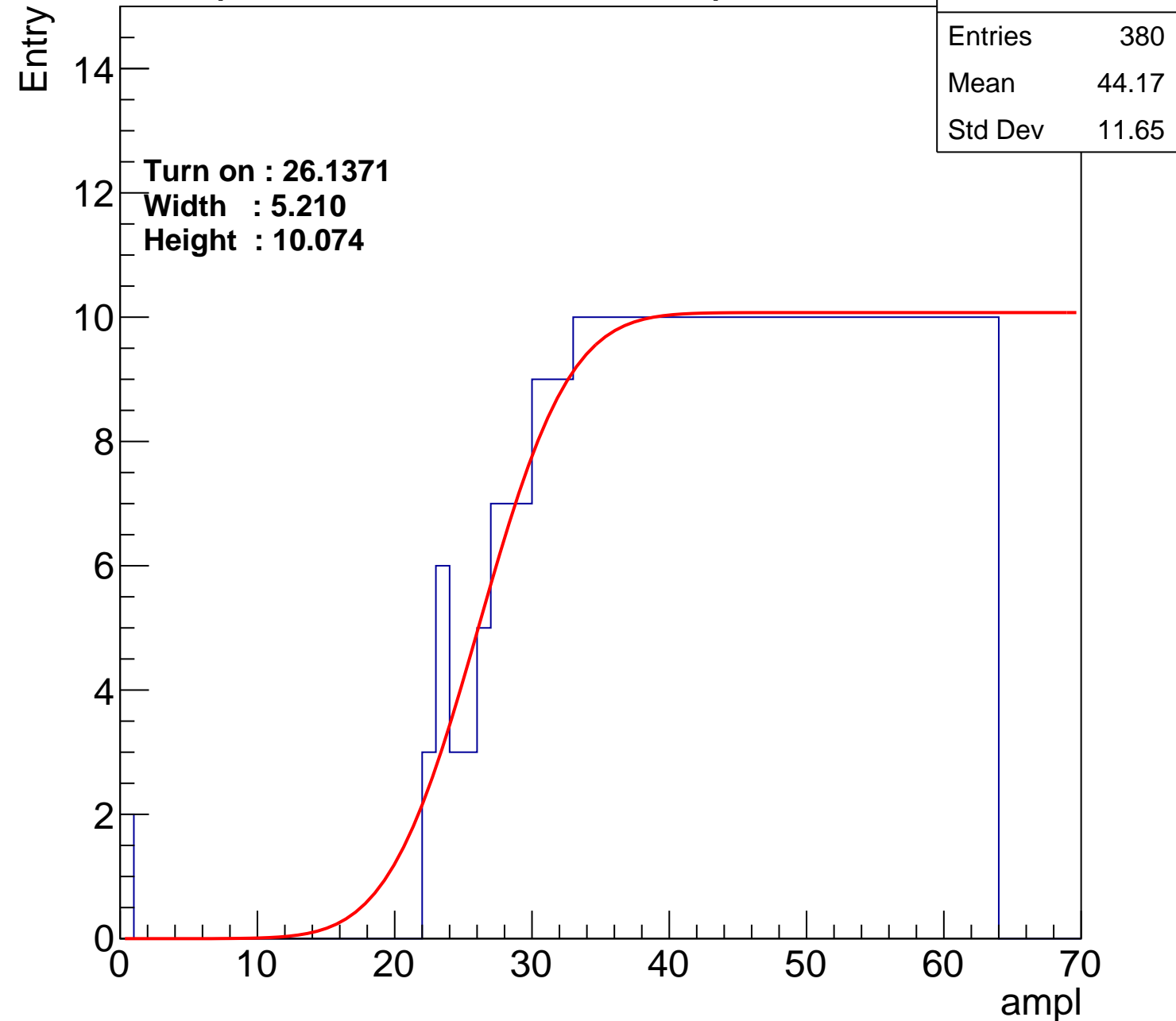
Width : 5.210

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch112

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	390
Mean	43.77
Std Dev	11.78

Turn on : 26.0979

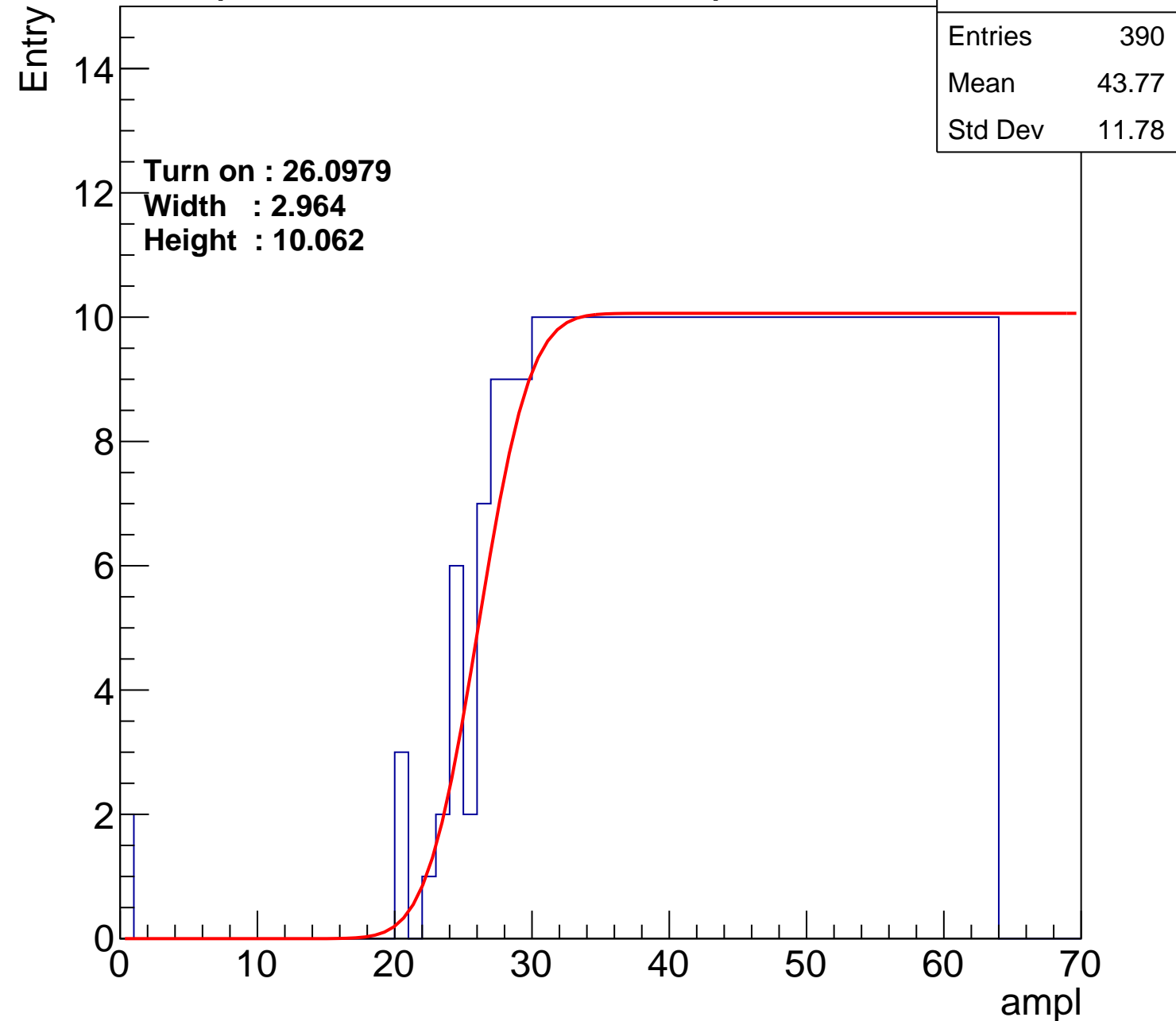
Width : 2.964

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch113

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.69
Std Dev	12.01

Turn on : 25.5085

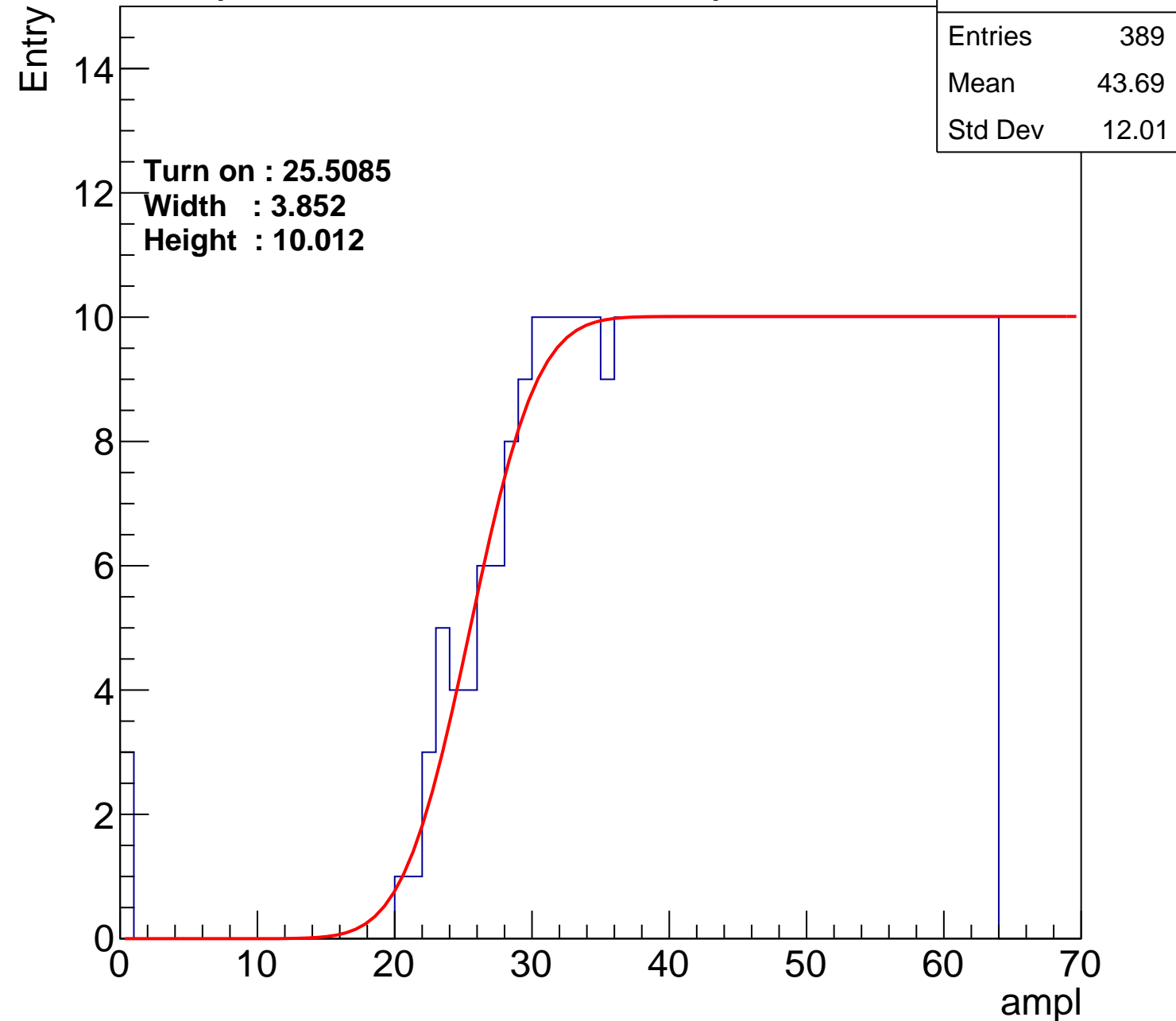
Width : 3.852

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch114

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.86
Std Dev	11.94

Turn on : 26.3685

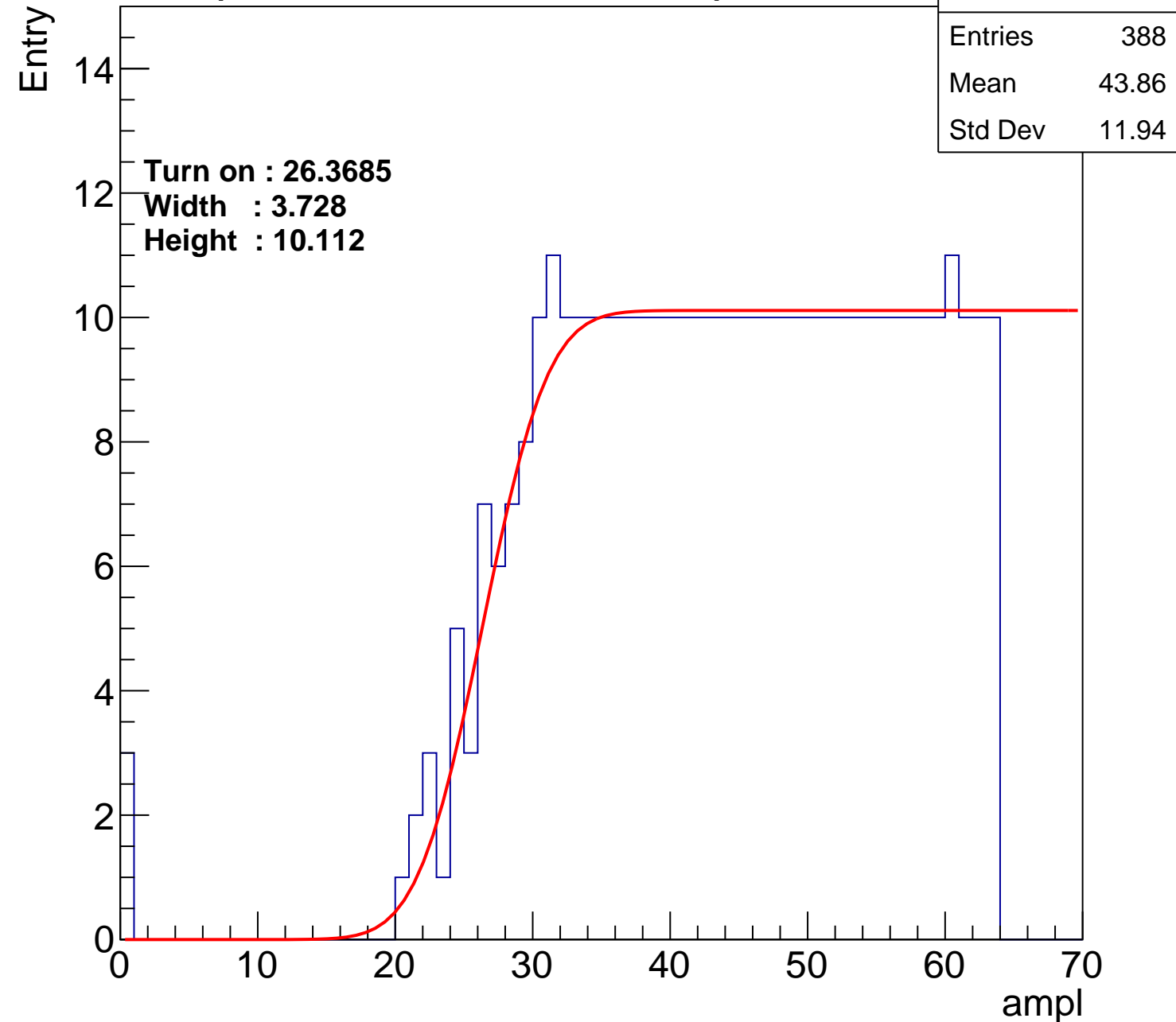
Width : 3.728

Height : 10.112

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch115

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	390
Mean	43.75
Std Dev	11.81

Turn on : 25.7145

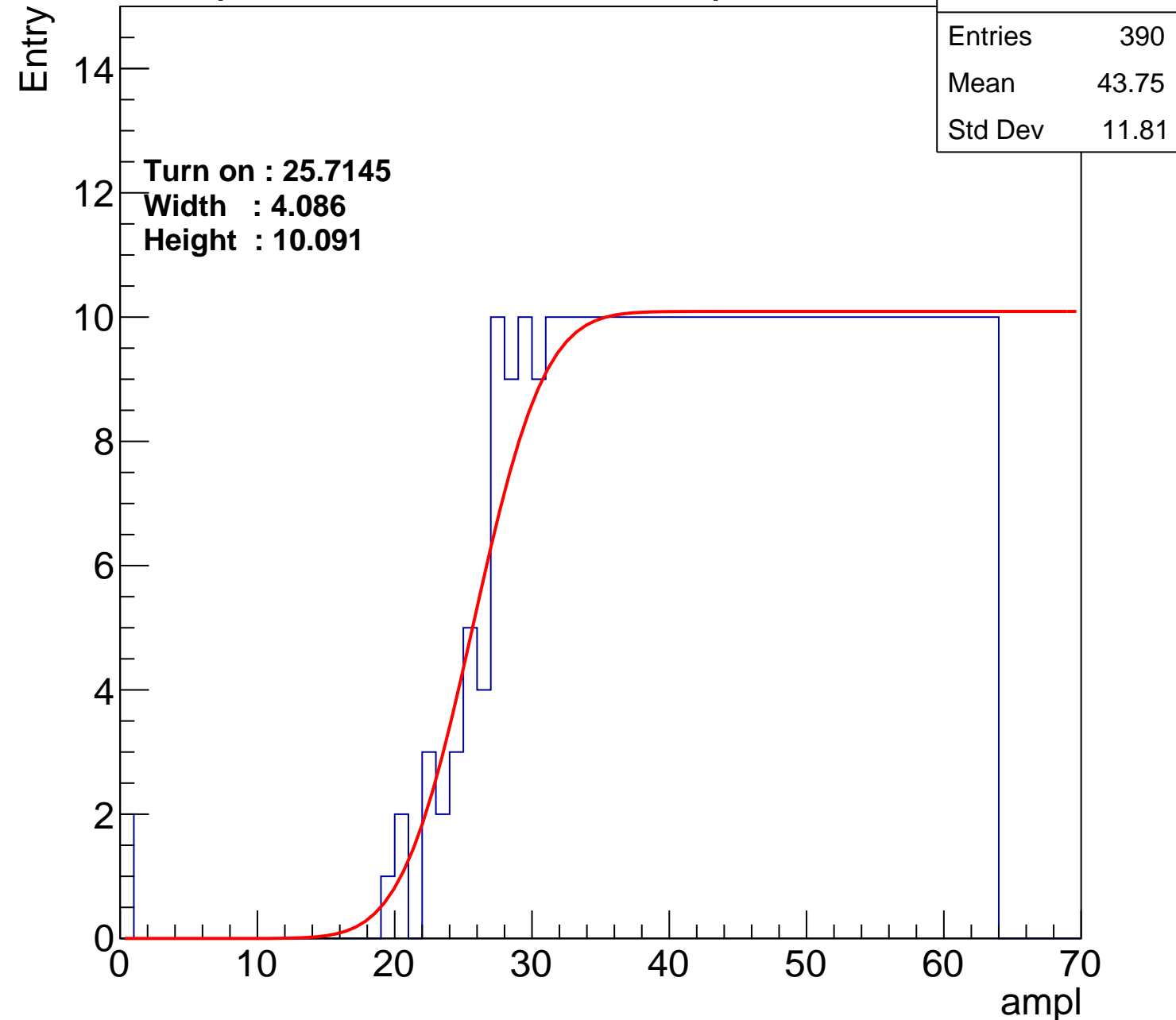
Width : 4.086

Height : 10.091

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch116

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.28
Std Dev	11.68

Turn on : 27.0410

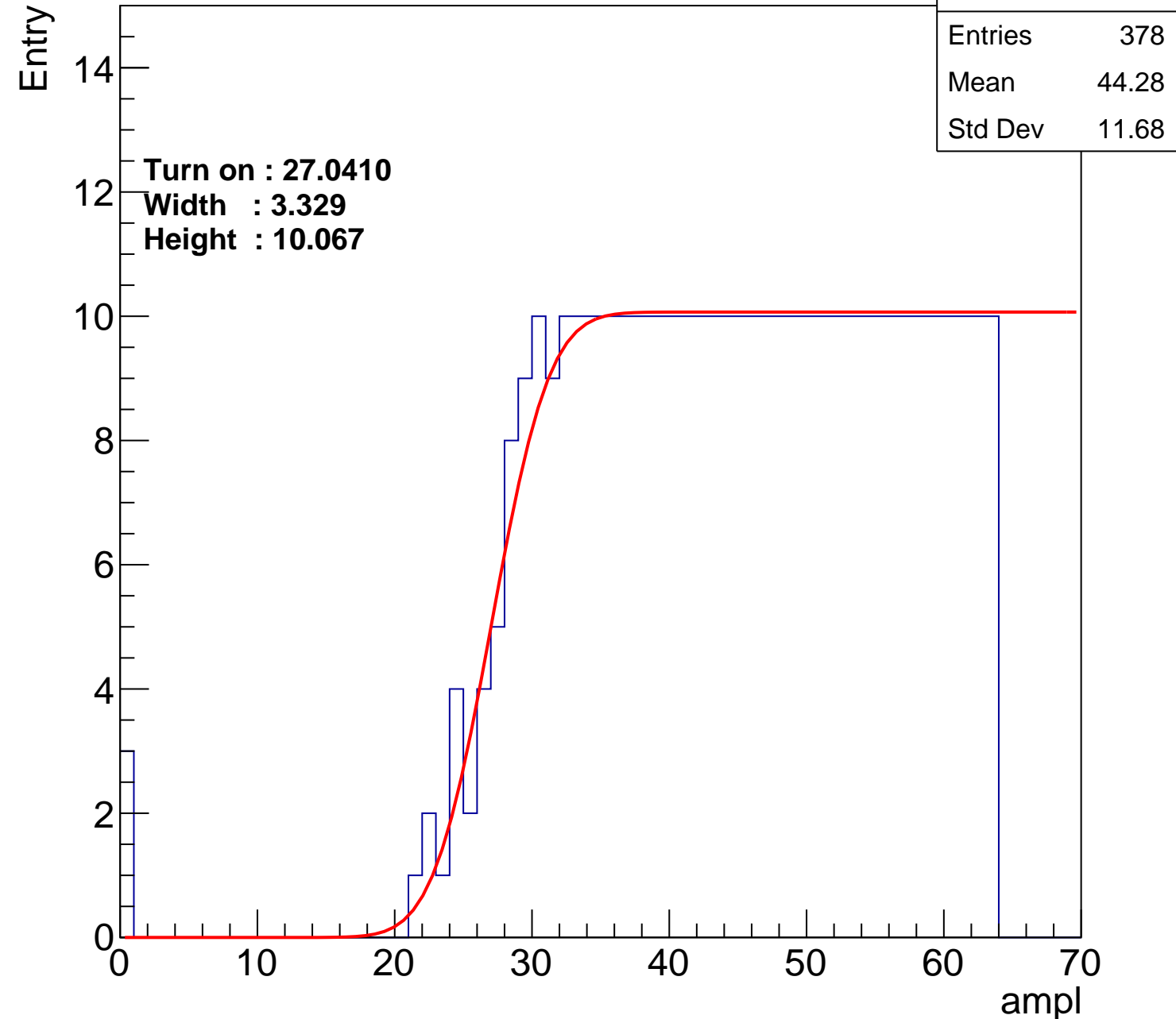
Width : 3.329

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch117

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.37
Std Dev	11.47

Turn on : 27.0391

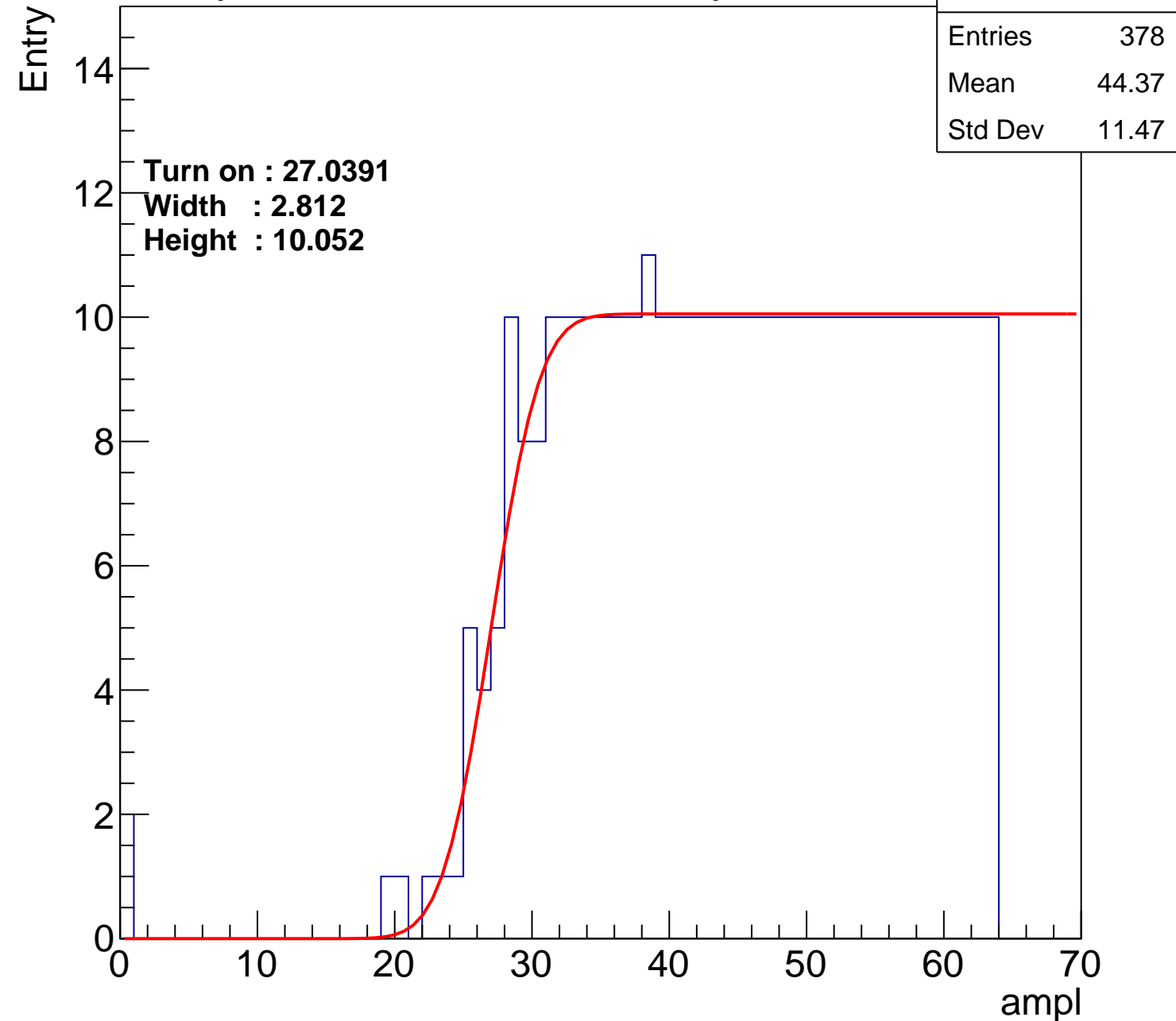
Width : 2.812

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch118

calib\_packv5\_042523\_0143.root, FC#12, port B1

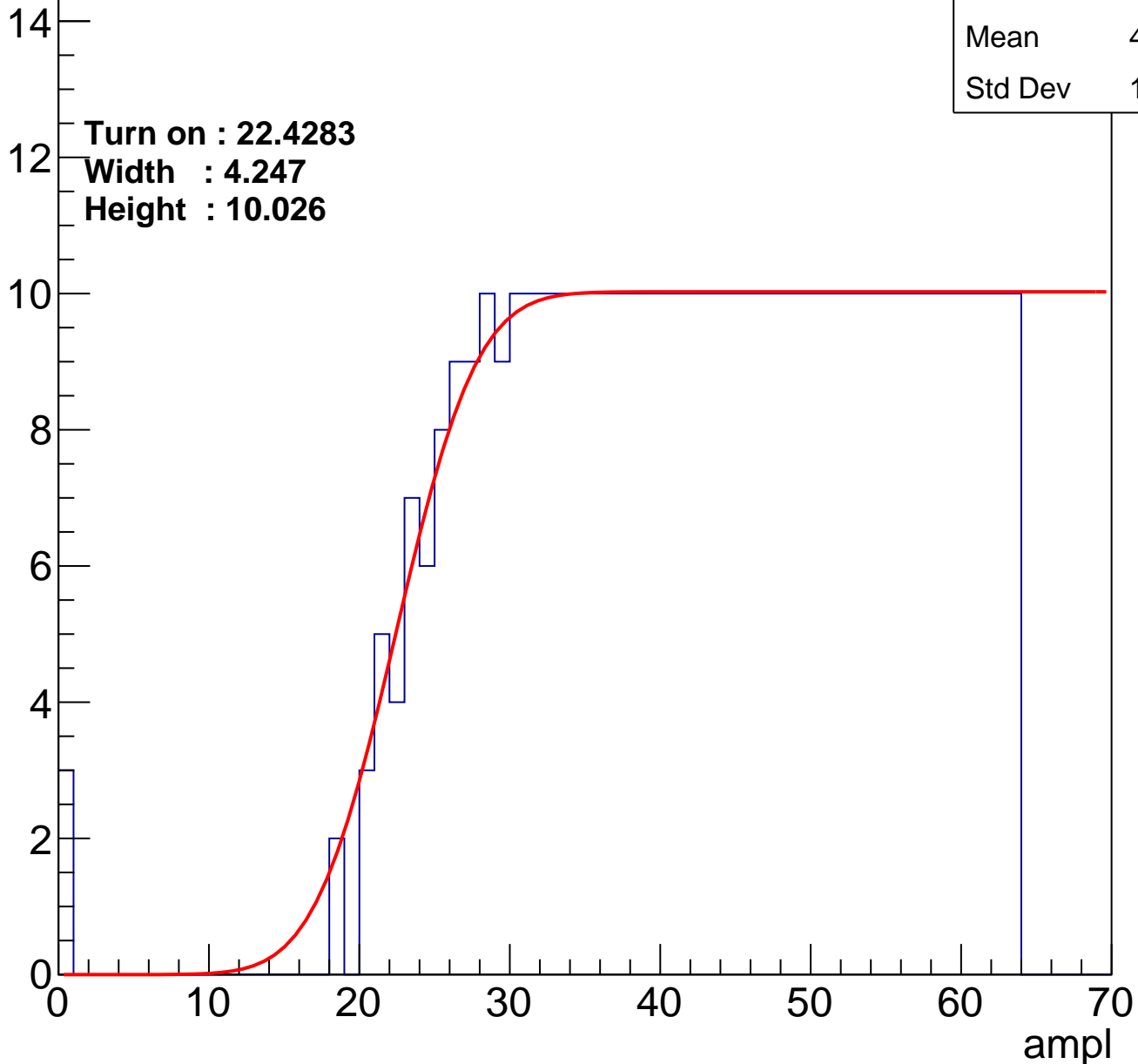
Entries	415
Mean	42.46
Std Dev	12.59

Turn on : 22.4283

Width : 4.247

Height : 10.026

Entry





# B0L102S, U26-ch119

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.51
Std Dev	11.47

Turn on : 26.9244

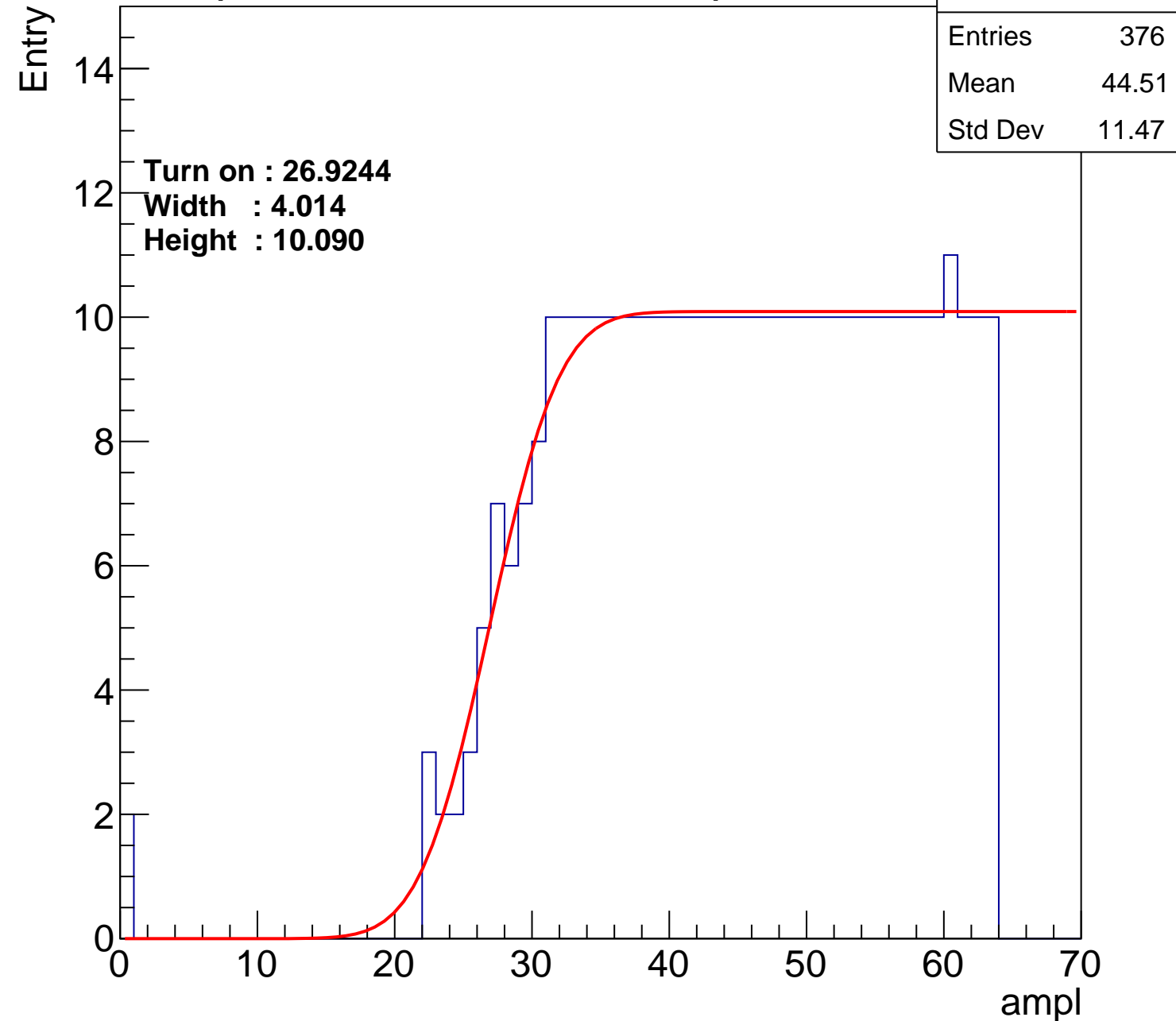
Width : 4.014

Height : 10.090

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch120

calib\_packv5\_042523\_0143.root, FC#12, port B1

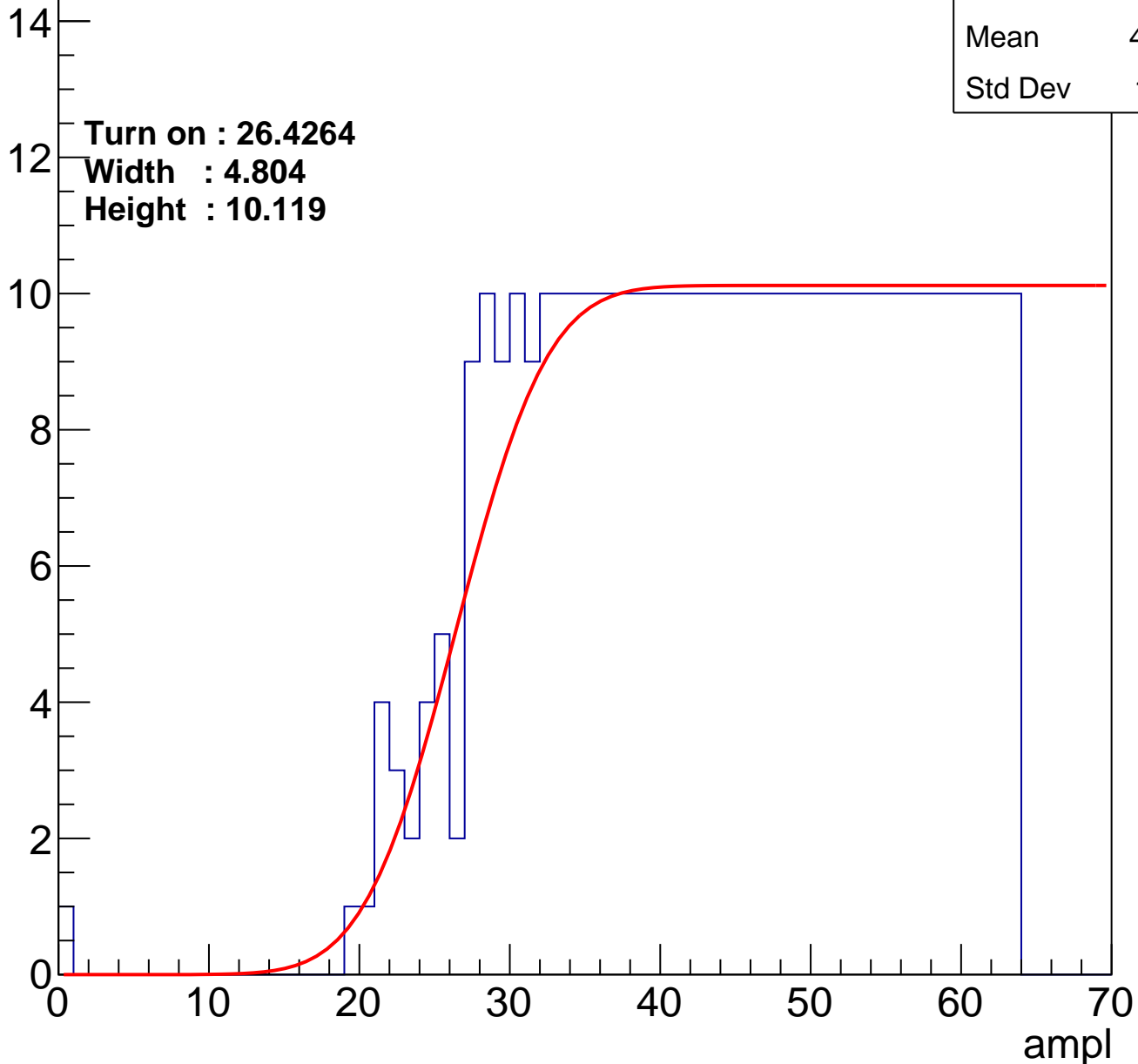
Entries	390
Mean	43.77
Std Dev	11.71

Turn on : 26.4264

Width : 4.804

Height : 10.119

Entry



# B0L102S, U26-ch121

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	400
Mean	43.25
Std Dev	12.07

Turn on : 24.0445

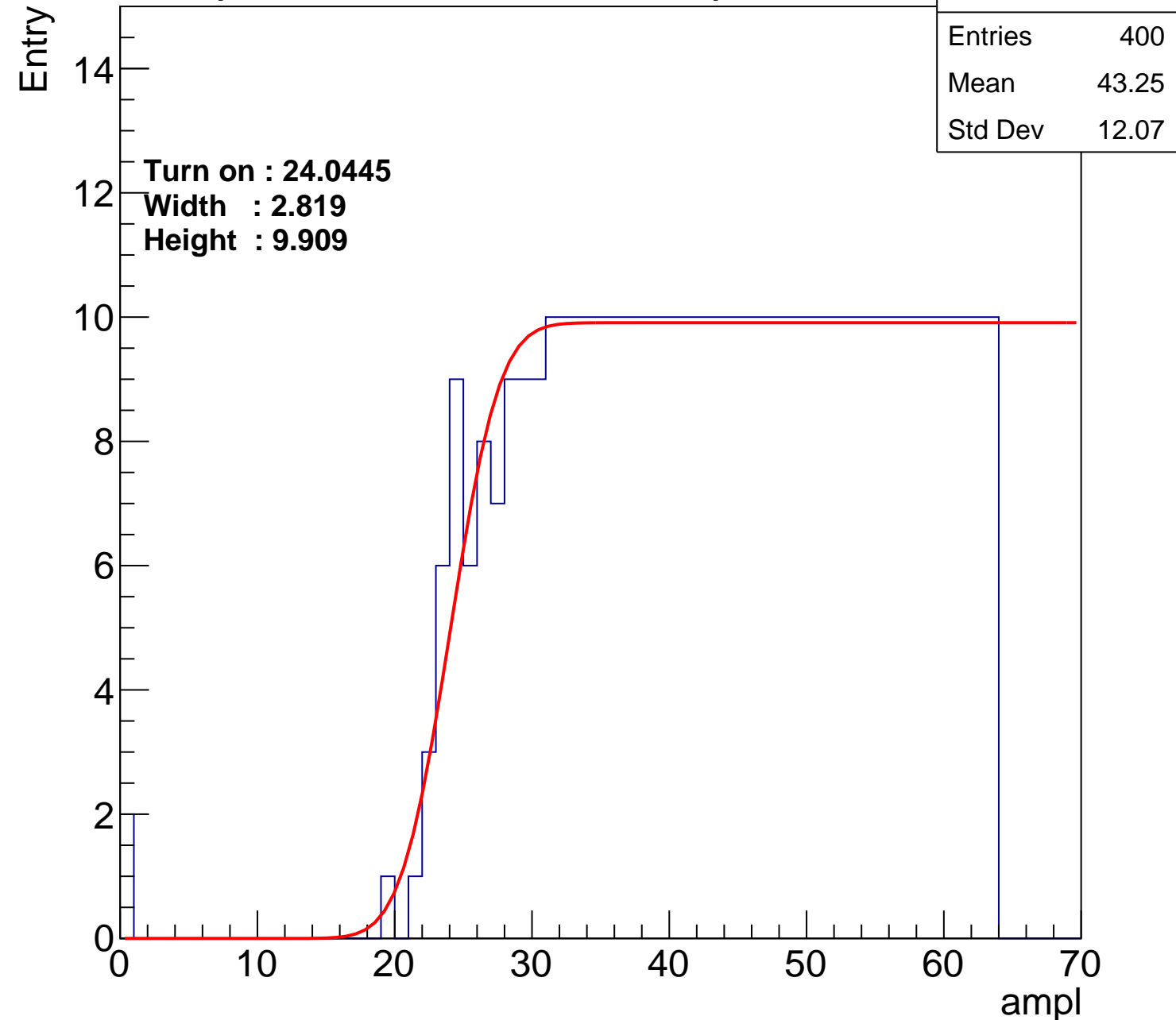
Width : 2.819

Height : 9.909

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch122

calib\_packv5\_042523\_0143.root, FC#12, port B1

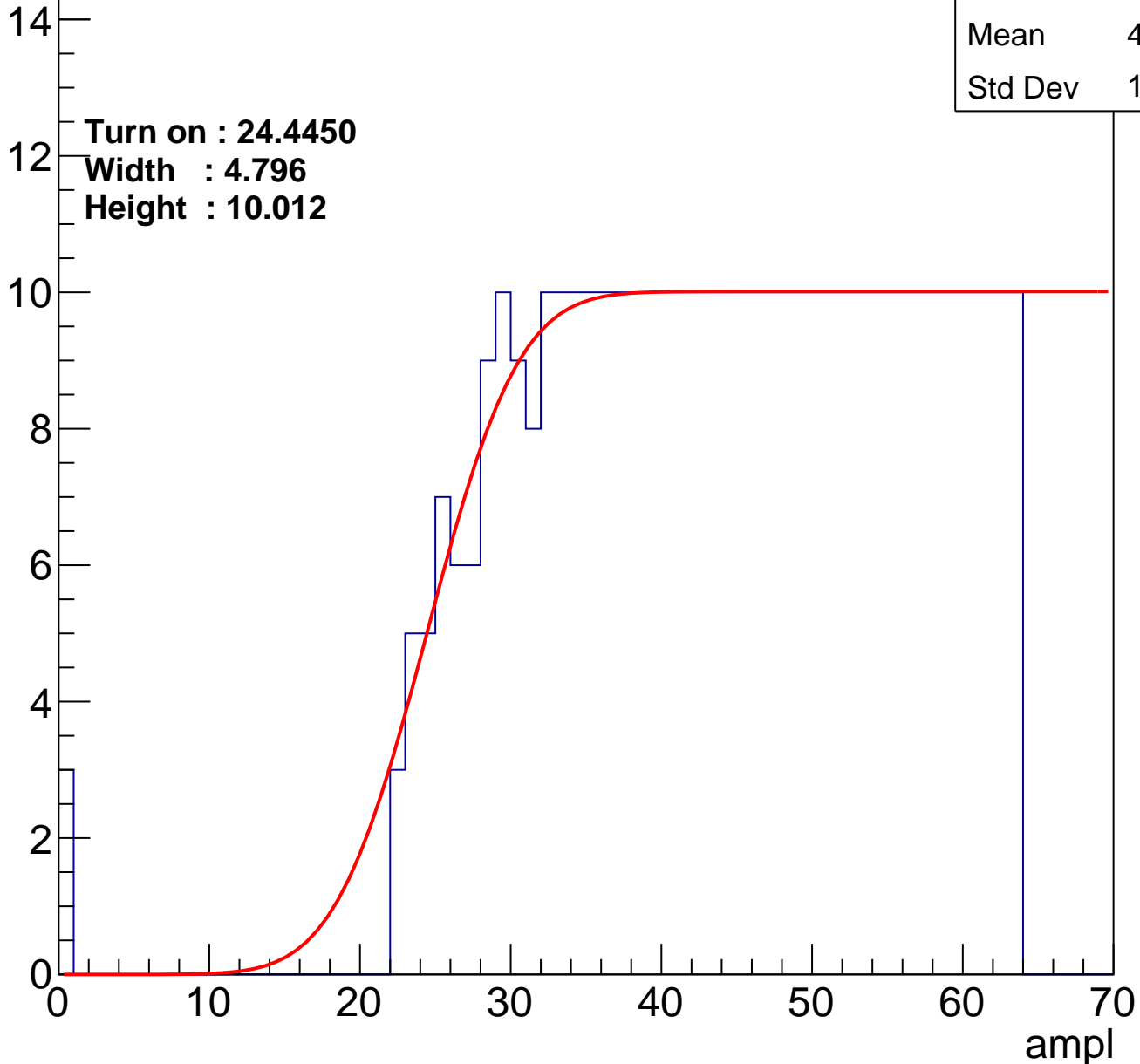
Entries	391
Mean	43.62
Std Dev	12.02

Turn on : 24.4450

Width : 4.796

Height : 10.012

Entry



# B0L102S, U26-ch123

calib\_packv5\_042523\_0143.root, FC#12, port B1

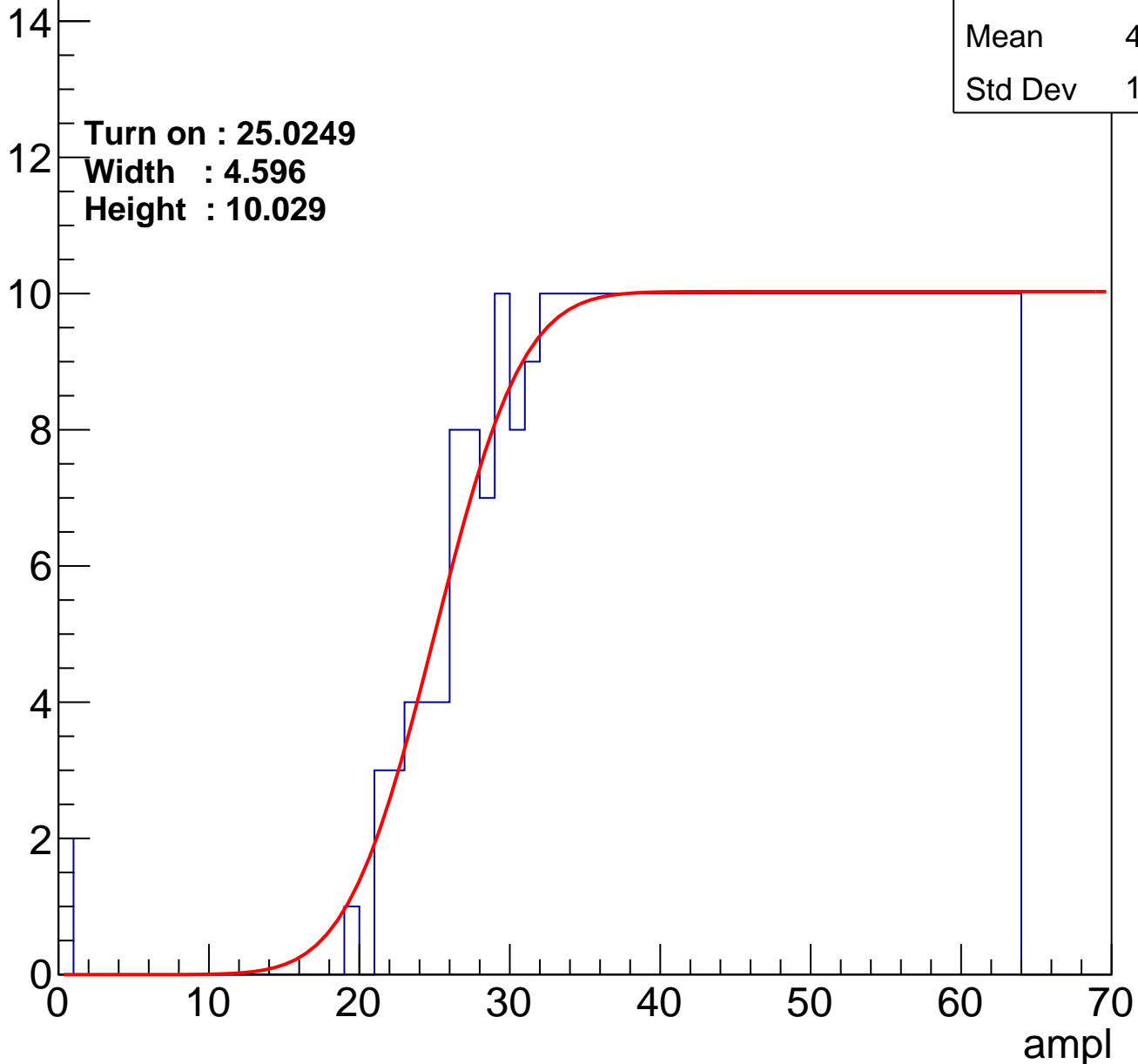
Entries	391
Mean	43.64
Std Dev	11.92

Turn on : 25.0249

Width : 4.596

Height : 10.029

Entry



# B0L102S, U26-ch124

calib\_packv5\_042523\_0143.root, FC#12, port B1

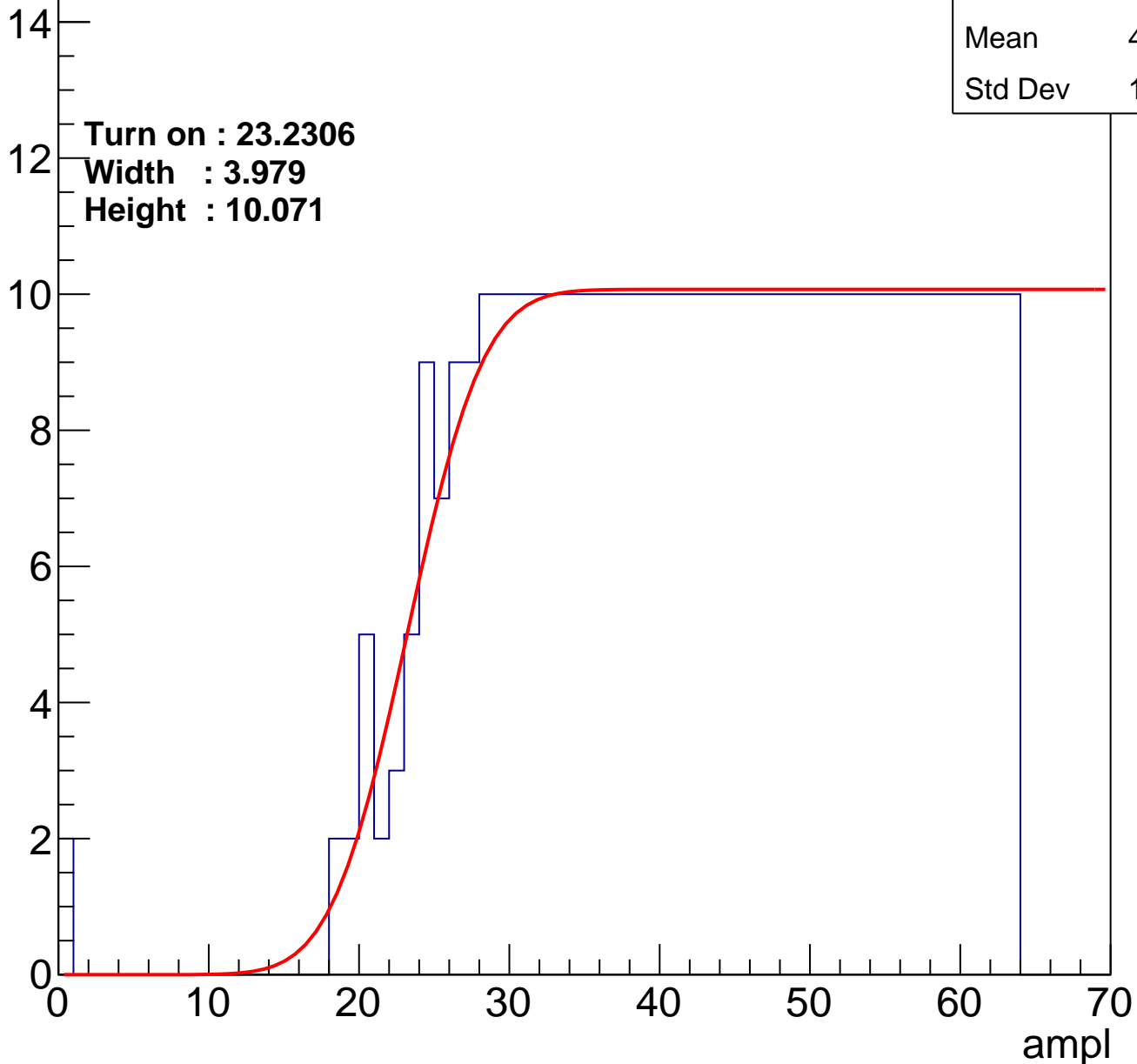
Entries	415
Mean	42.52
Std Dev	12.46

Turn on : 23.2306

Width : 3.979

Height : 10.071

Entry



# B0L102S, U26-ch125

calib\_packv5\_042523\_0143.root, FC#12, port B1

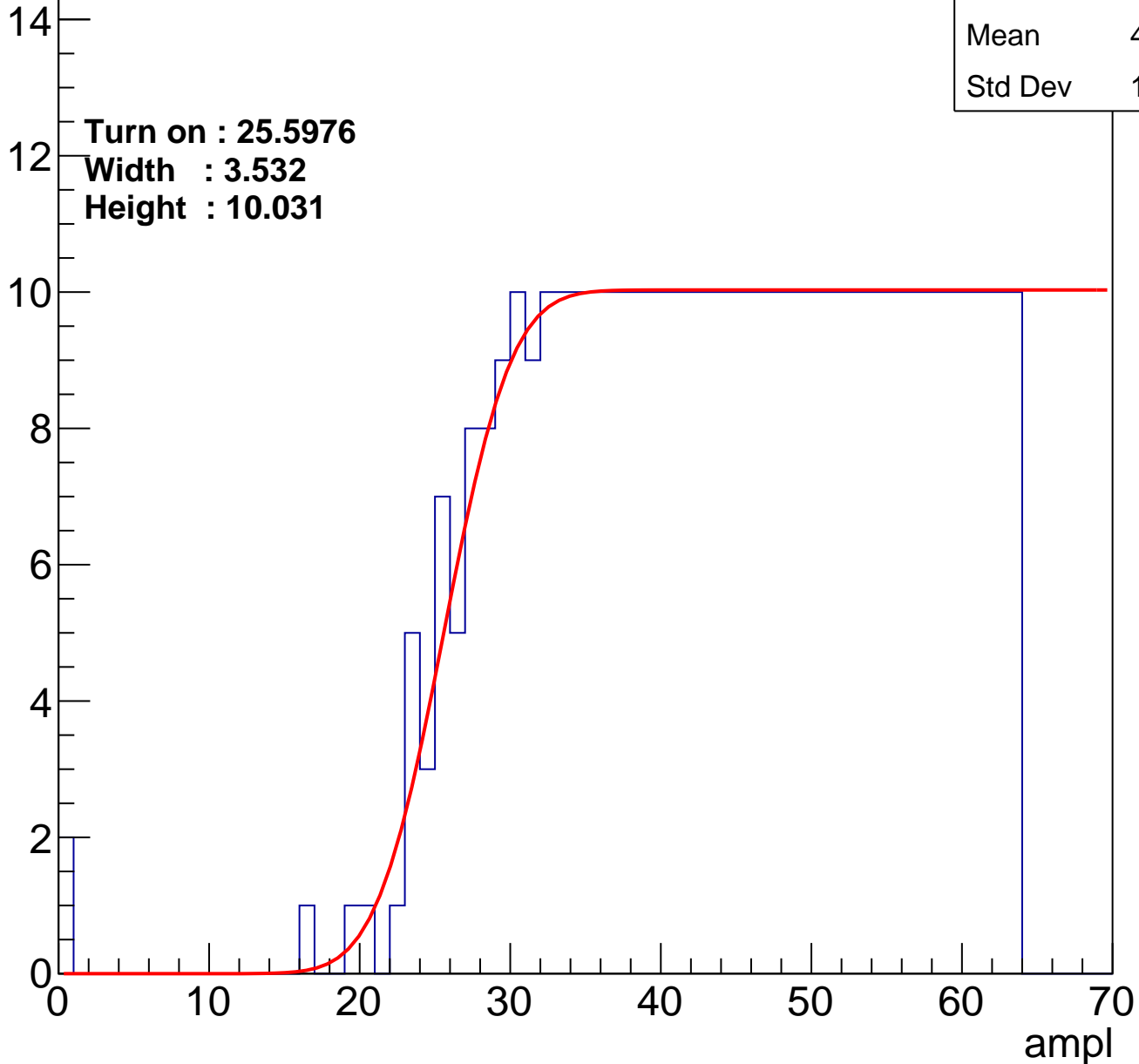
Entries	390
Mean	43.72
Std Dev	11.87

Turn on : 25.5976

Width : 3.532

Height : 10.031

Entry



# B0L102S, U26-ch126

calib\_packv5\_042523\_0143.root, FC#12, port B1

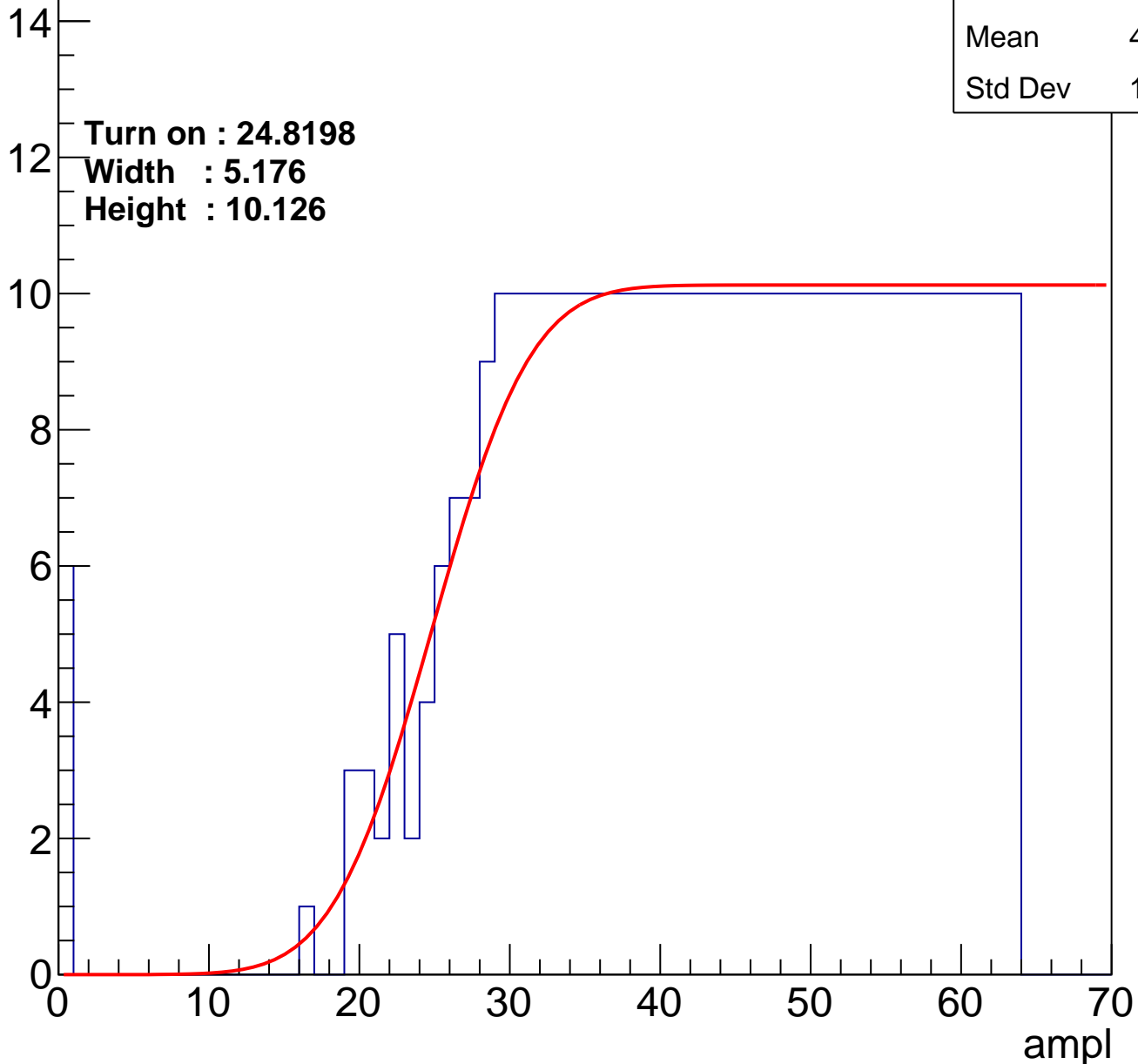
Entries	405
Mean	42.72
Std Dev	12.88

Turn on : 24.8198

Width : 5.176

Height : 10.126

Entry





# B0L102S, U26-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.49
Std Dev	12.2

Turn on : 26.0581

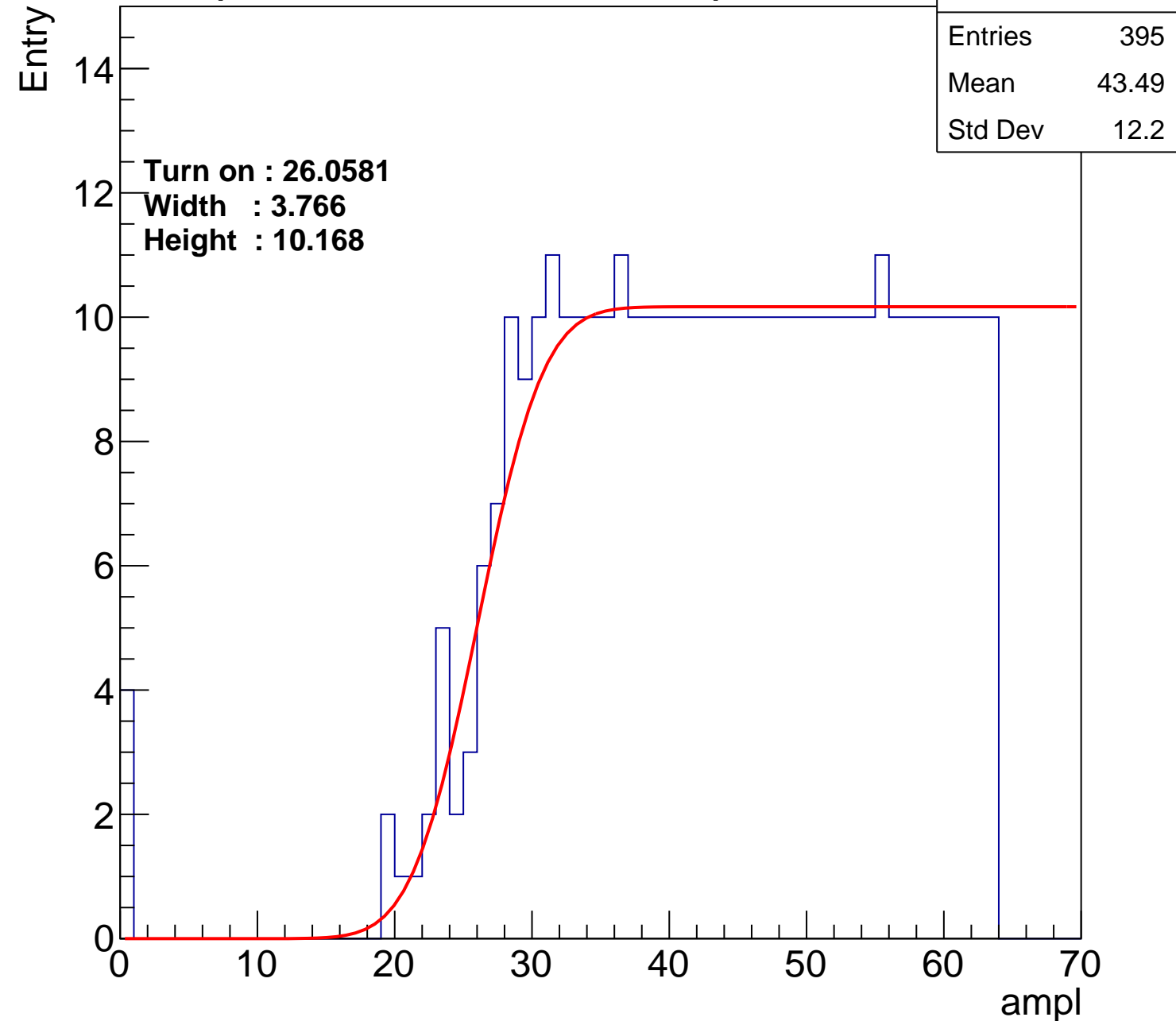
Width : 3.766

Height : 10.168

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U26-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.49
Std Dev	12.2

Turn on : 26.0581

Width : 3.766

Height : 10.168

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

