



# B0L002S, U25-ch0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.95
Std Dev	11.17

Turn on : 27.9840

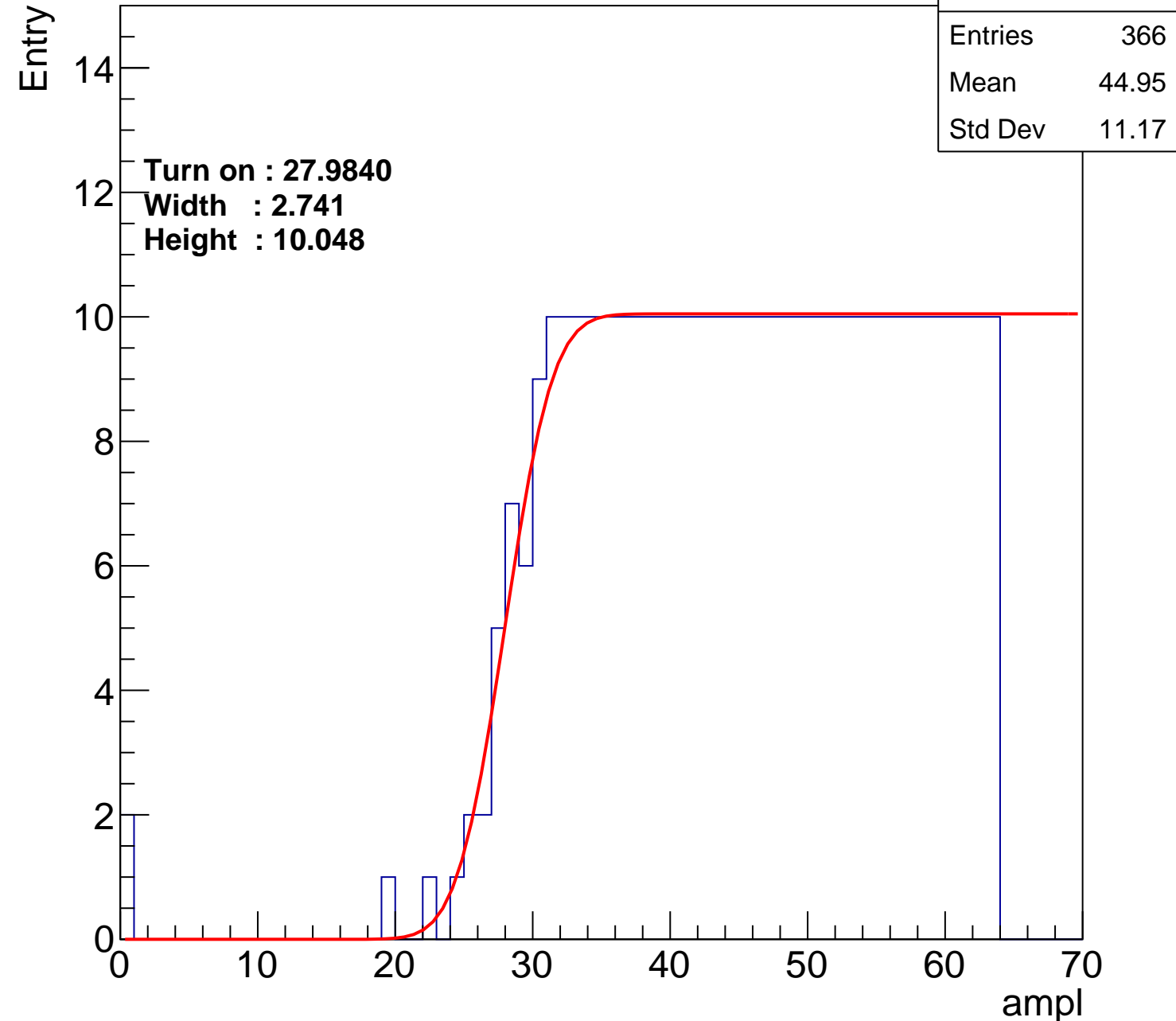
Width : 2.741

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.87
Std Dev	11.38

Turn on : 28.4090

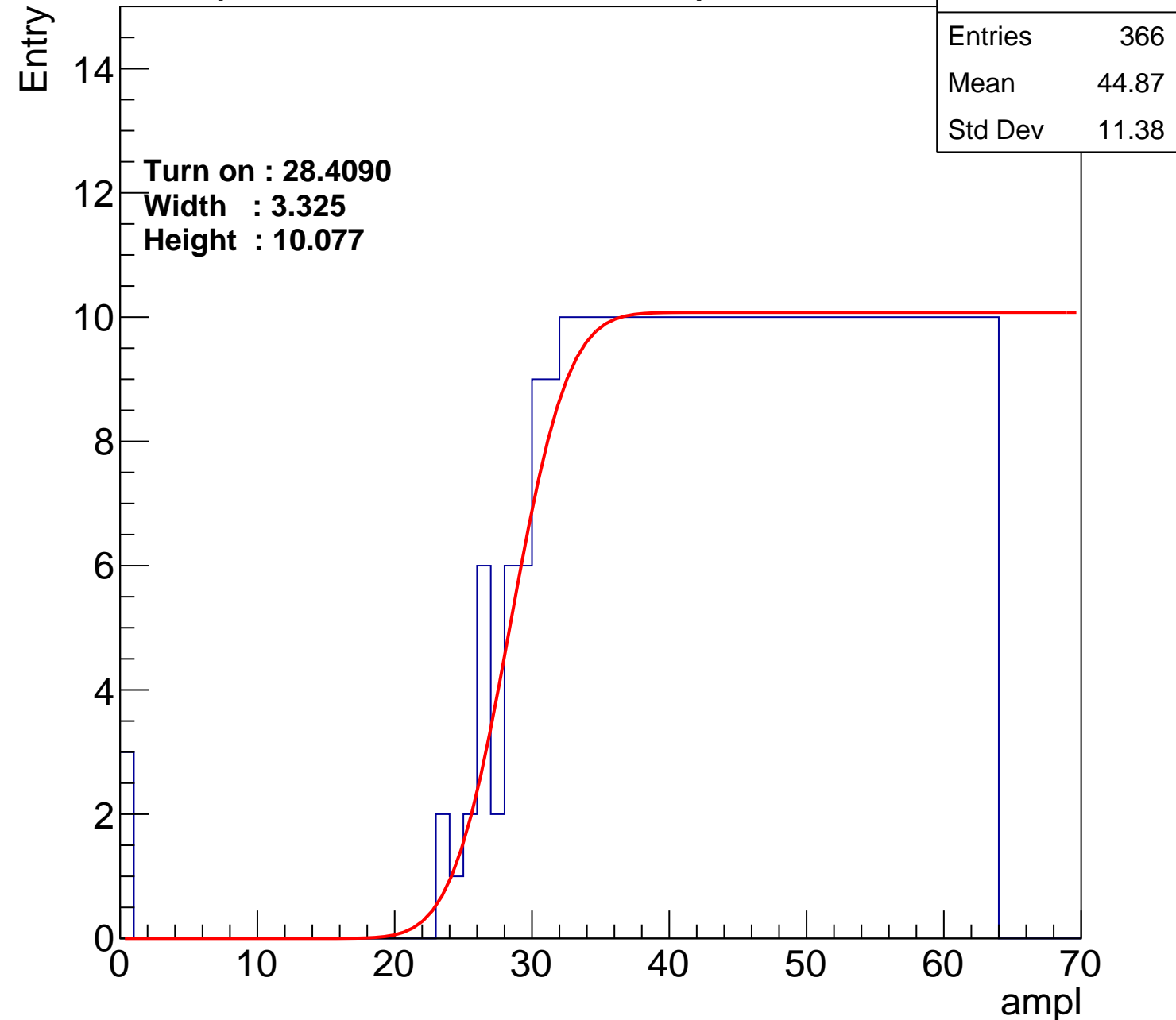
Width : 3.325

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.18
Std Dev	11.06

Turn on : 28.5127

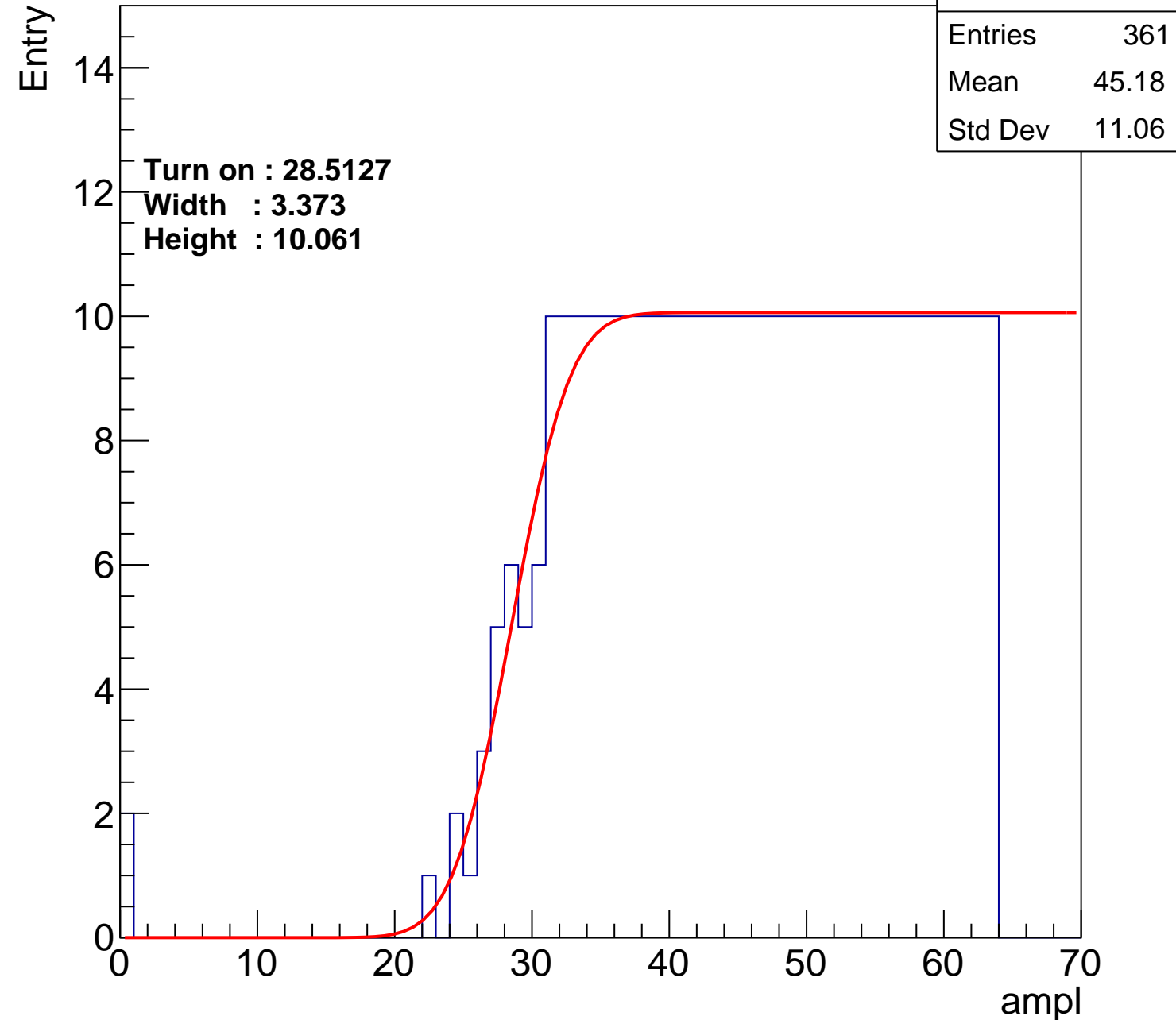
Width : 3.373

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.25
Std Dev	11.34

Turn on : 28.7873

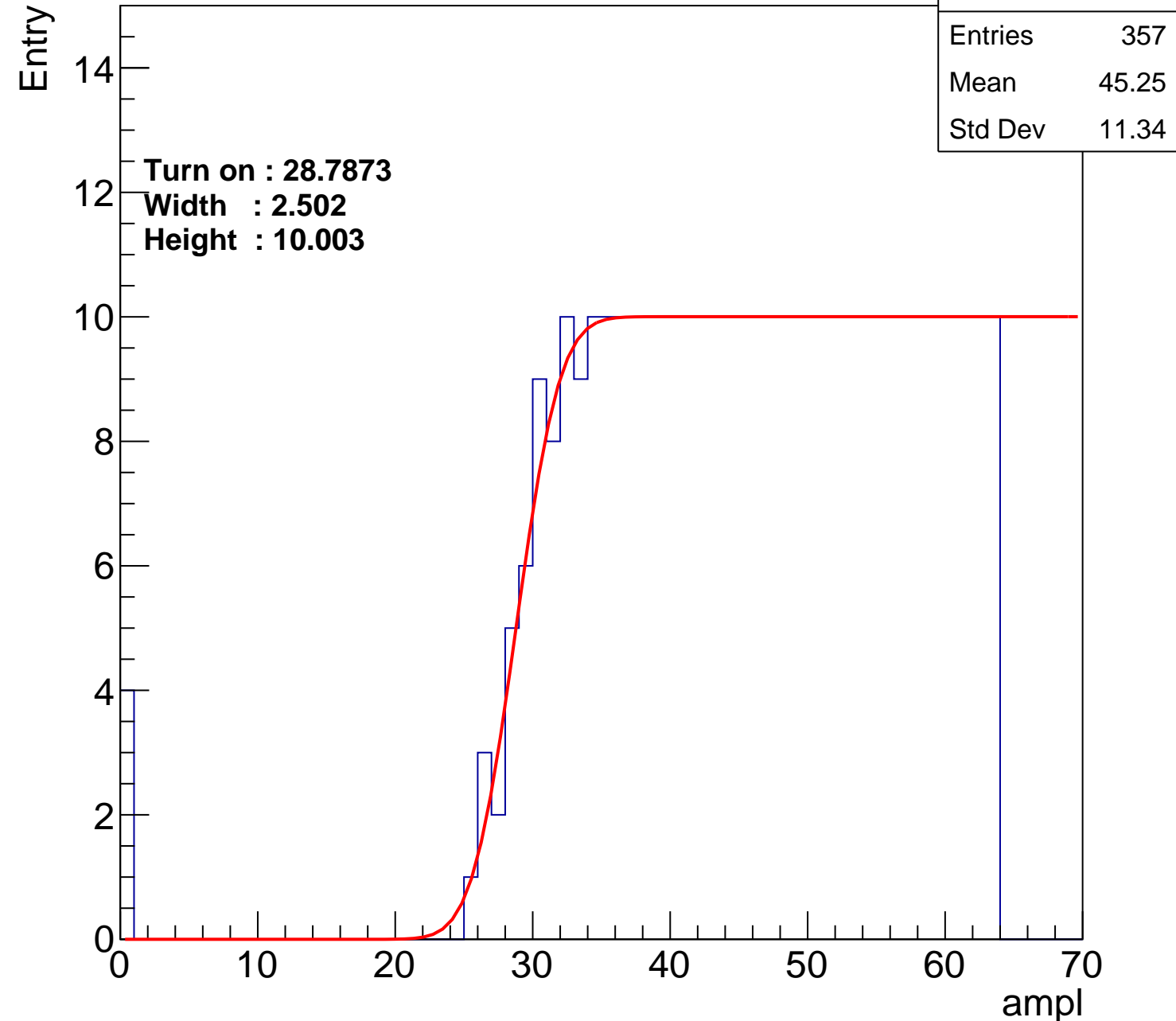
Width : 2.502

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	341
Mean	46.19
Std Dev	10.52

Turn on : 30.0570

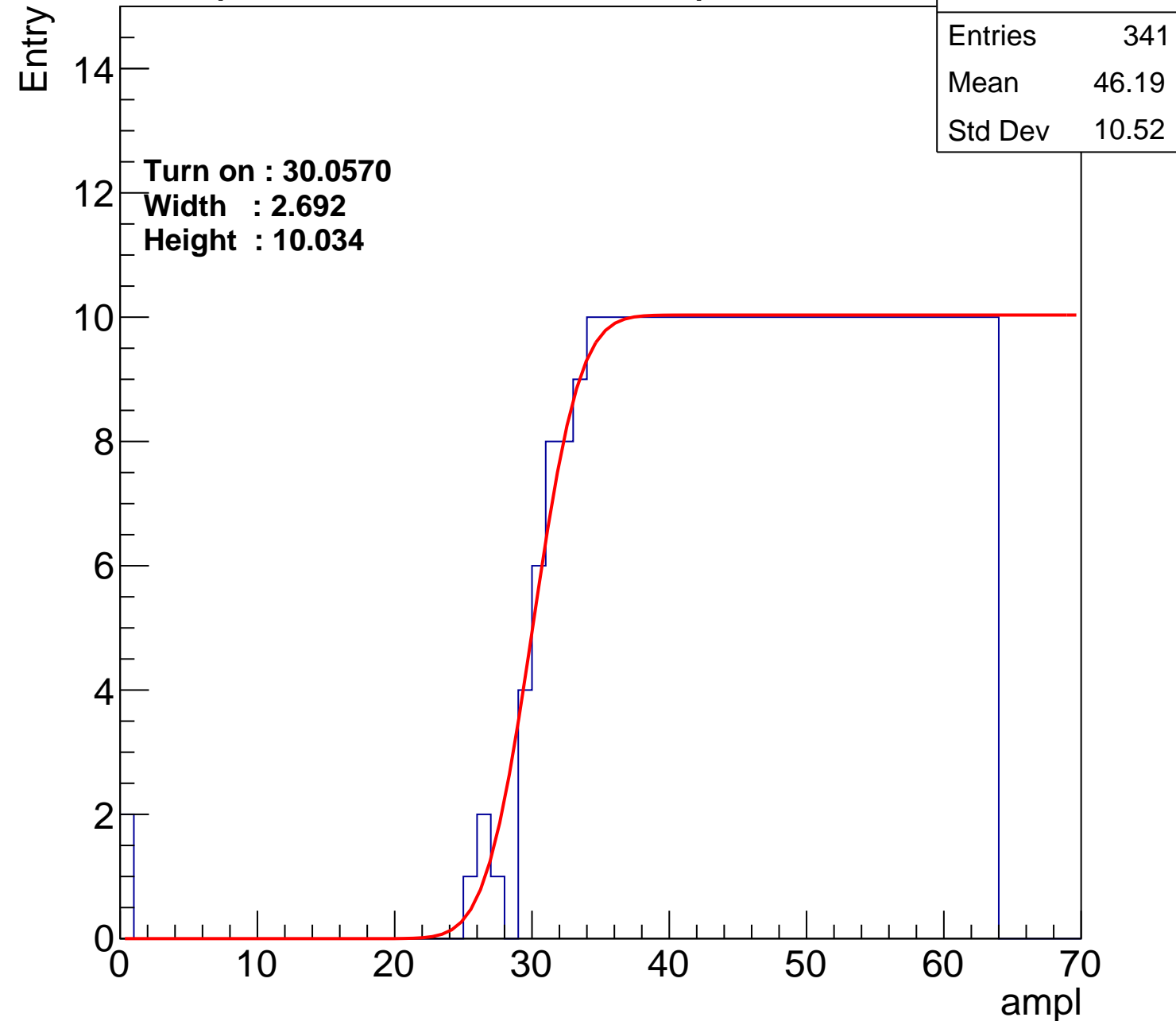
Width : 2.692

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch5

calib\_packv5\_042523\_0143.root, FC#8, port C1

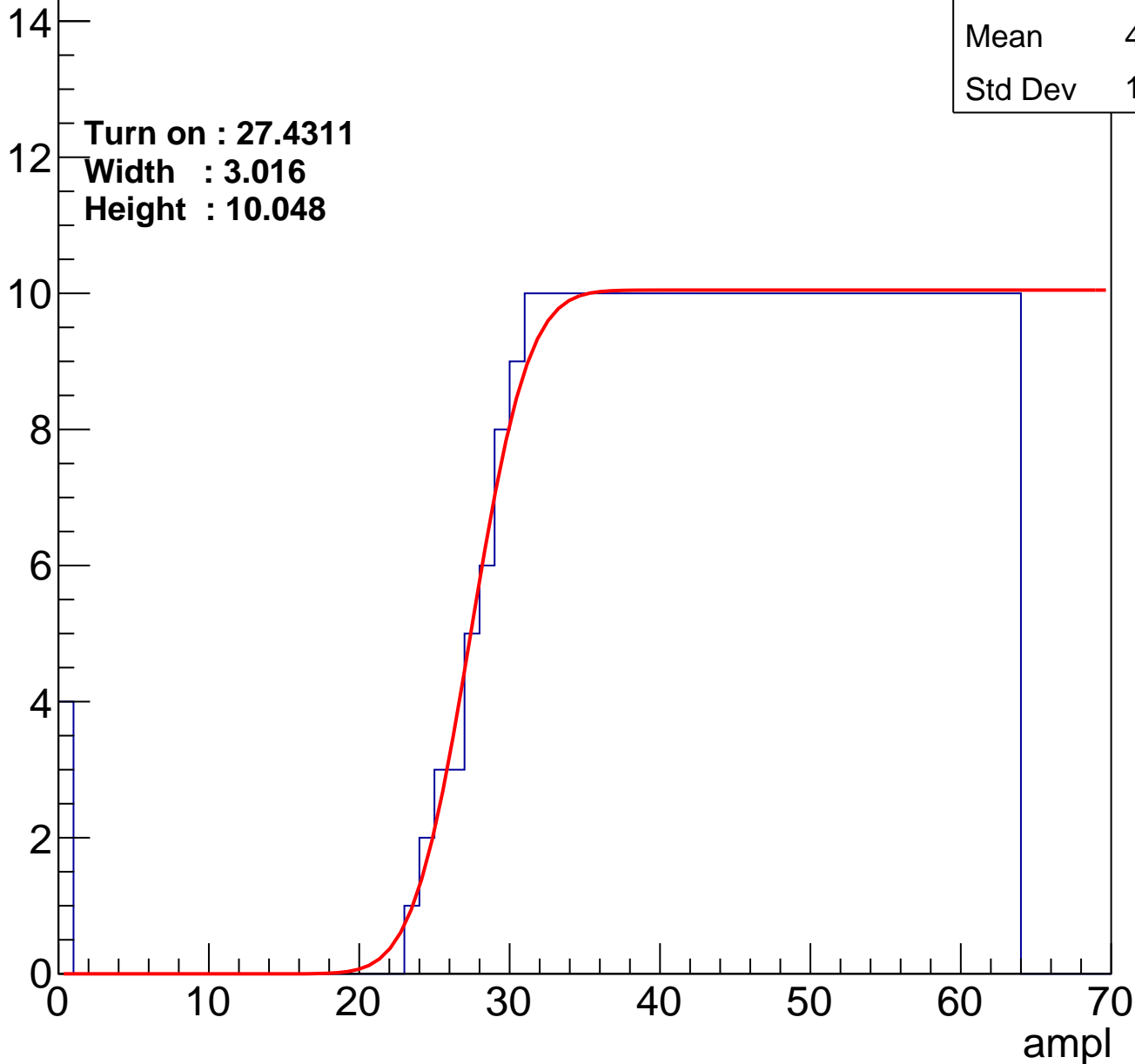
Entry

Entries	371
Mean	44.58
Std Dev	11.65

Turn on : 27.4311

Width : 3.016

Height : 10.048



# B0L002S, U25-ch6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.16
Std Dev	10.89

Turn on : 27.7563

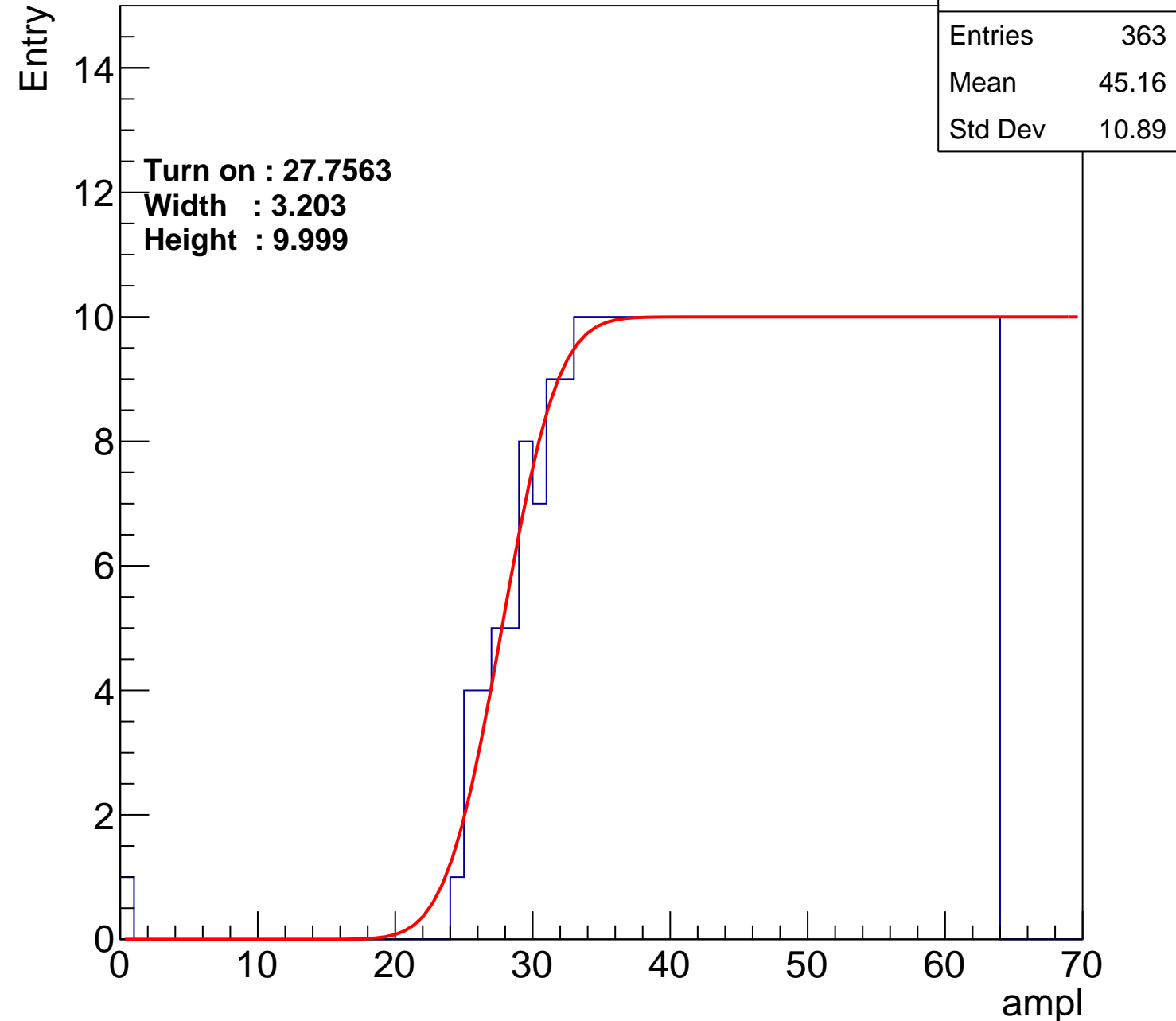
Width : 3.203

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch7

calib\_packv5\_042523\_0143.root, FC#8, port C1

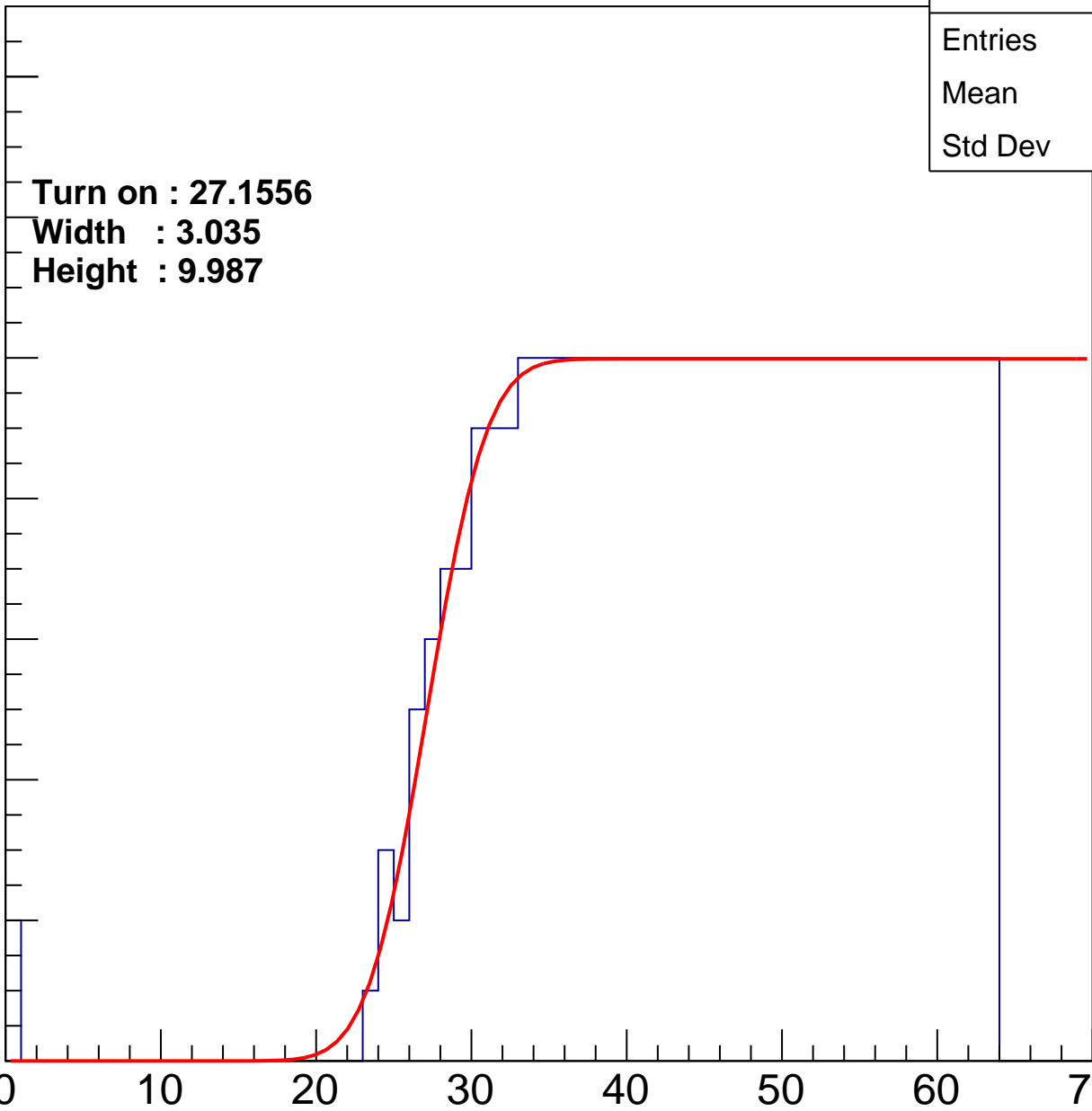
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.1556  
Width : 3.035  
Height : 9.987

Entries	370
Mean	44.74
Std Dev	11.28

ampl



# B0L002S, U25-ch8

calib\_packv5\_042523\_0143.root, FC#8, port C1

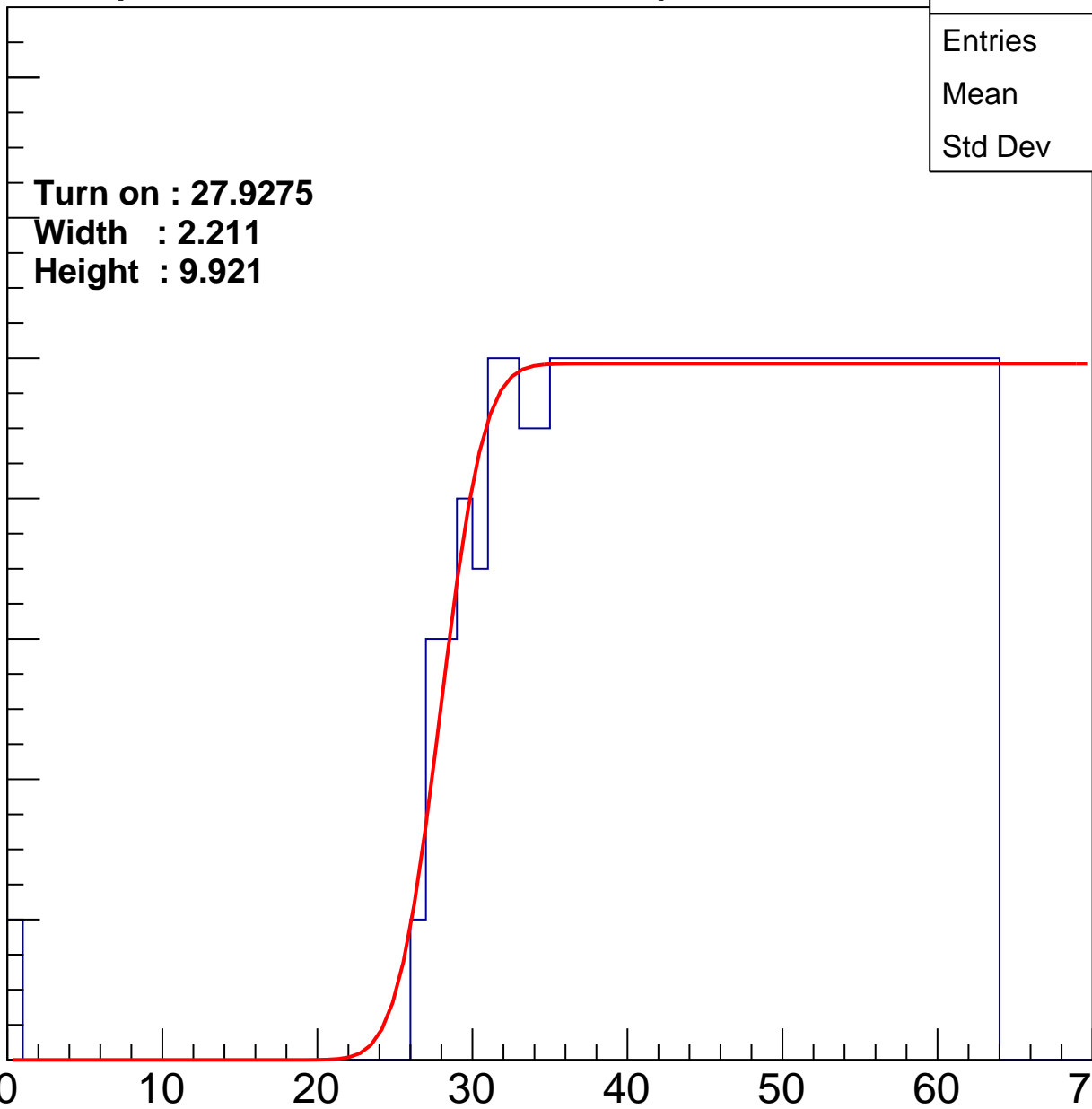
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.9275  
Width : 2.211  
Height : 9.921

Entries	359
Mean	45.31
Std Dev	10.95

ampl



# B0L002S, U25-ch9

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	347
Mean	45.89
Std Dev	10.69

Turn on : 30.0694

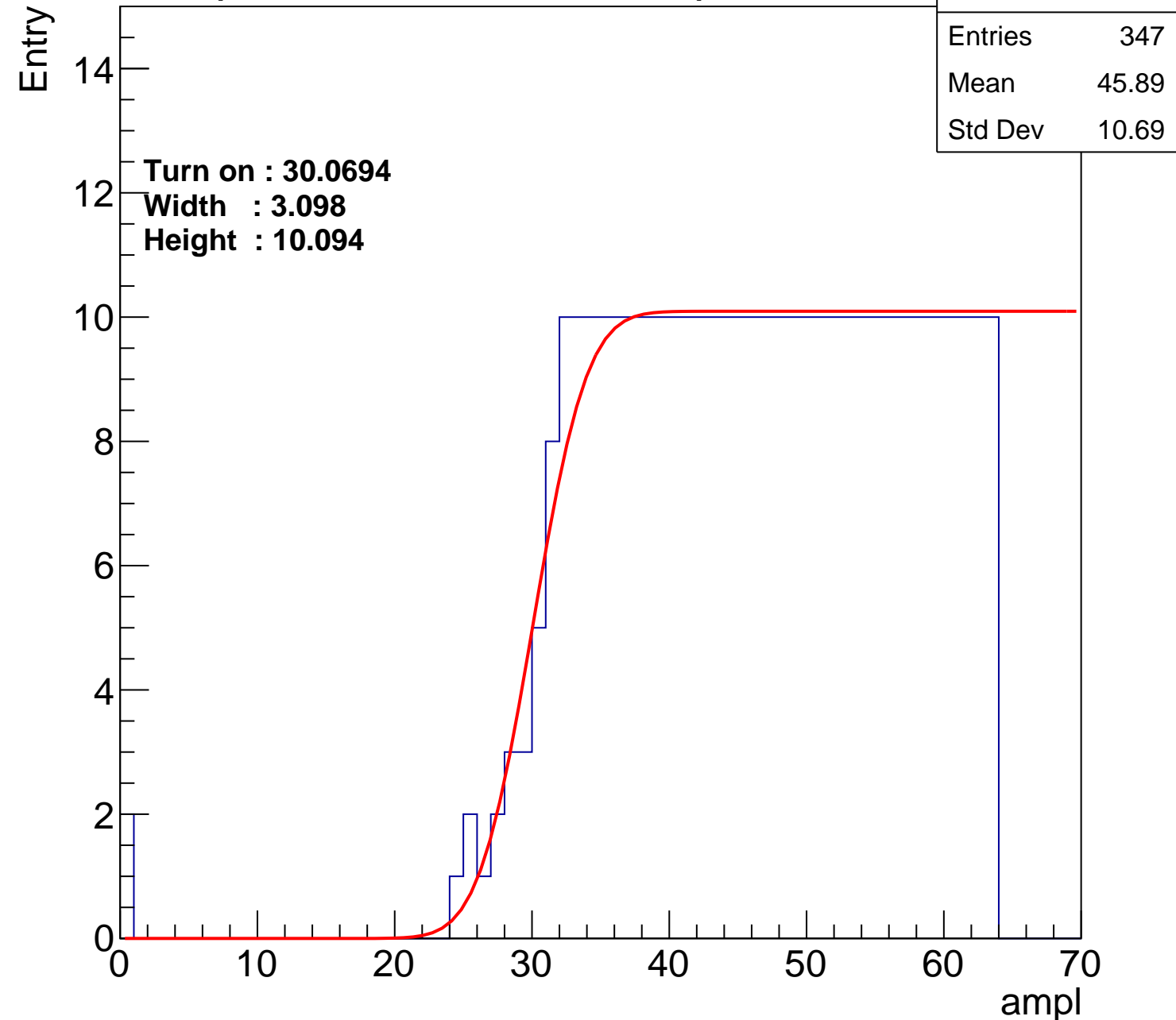
Width : 3.098

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch10

calib\_packv5\_042523\_0143.root, FC#8, port C1

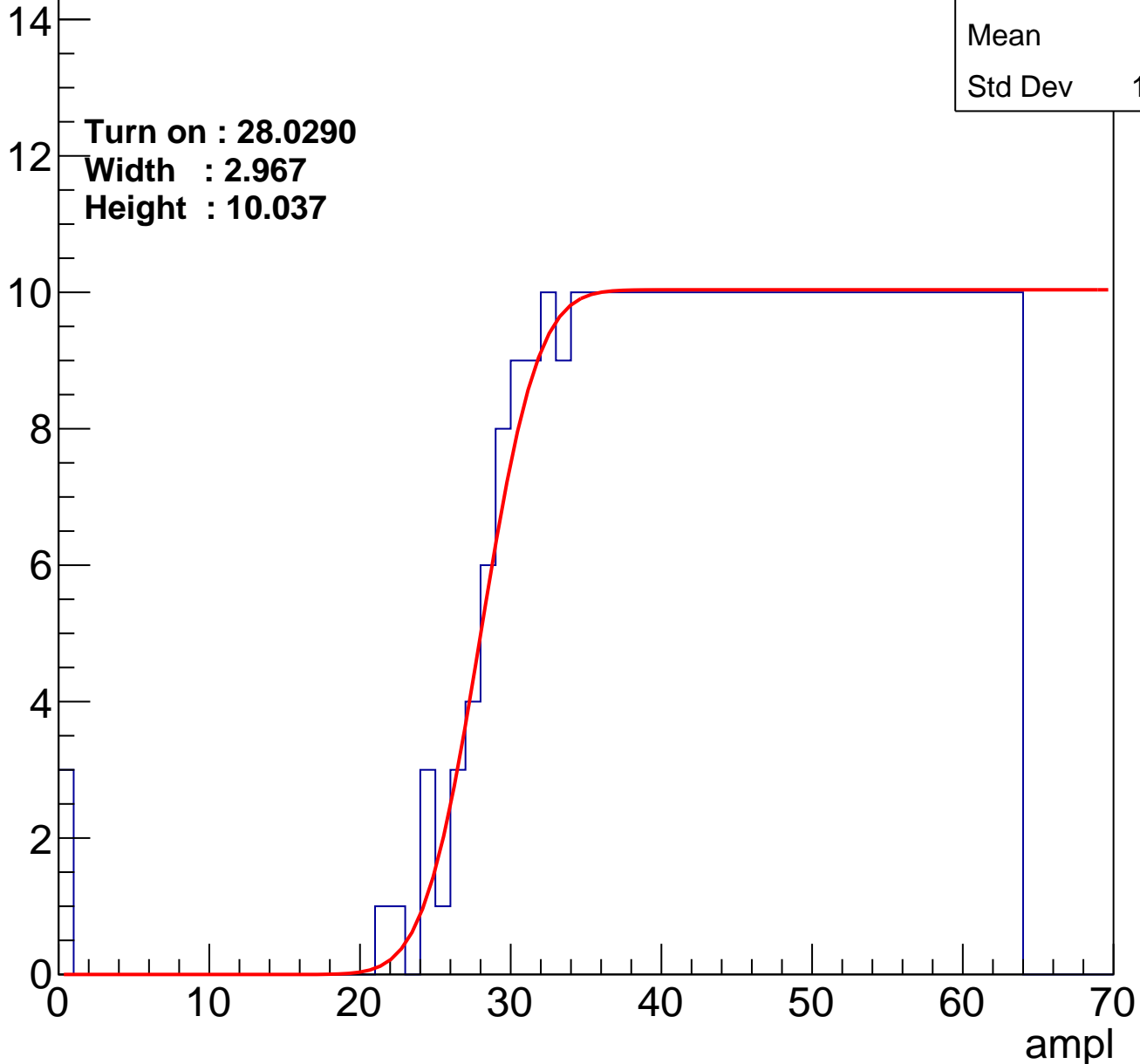
Entries	367
Mean	44.8
Std Dev	11.43

Turn on : 28.0290

Width : 2.967

Height : 10.037

Entry



# B0L002S, U25-ch11

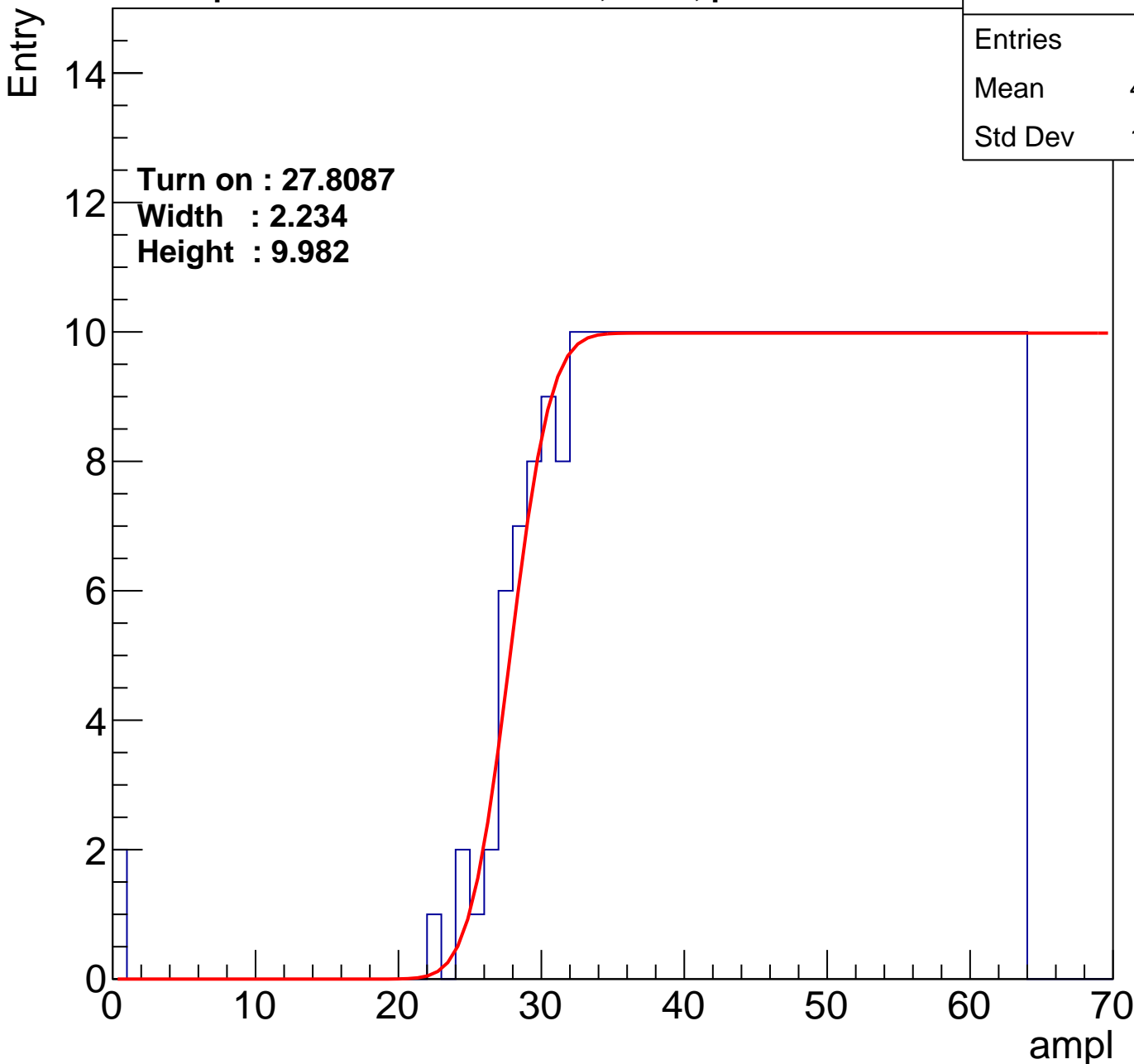
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	366
Mean	44.96
Std Dev	11.15

**Turn on : 27.8087**

**Width : 2.234**

**Height : 9.982**



# B0L002S, U25-ch12

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	44.99
Std Dev	11.3

Turn on : 27.9666

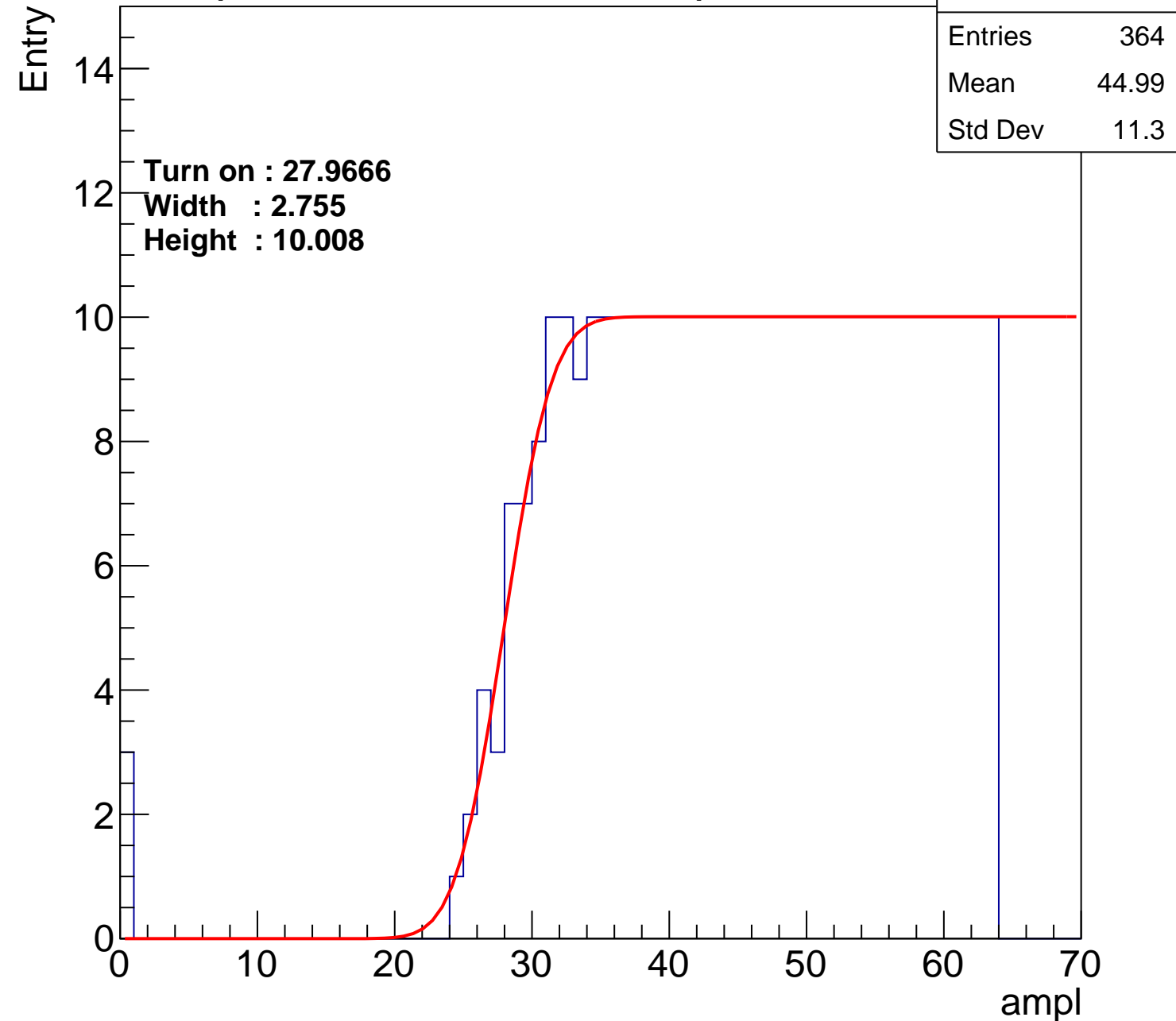
Width : 2.755

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch13

calib\_packv5\_042523\_0143.root, FC#8, port C1

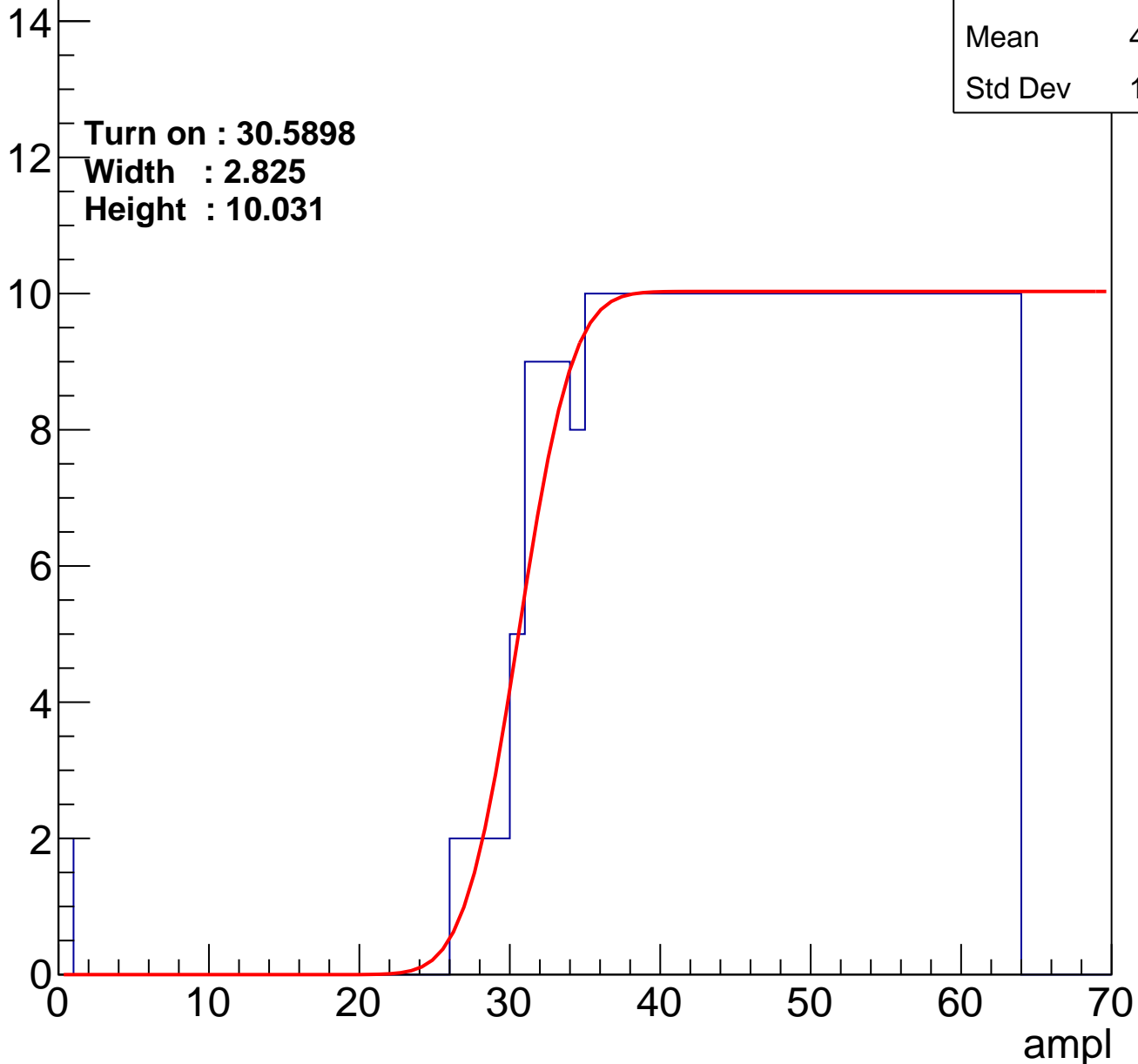
Entries	340
Mean	46.22
Std Dev	10.52

Turn on : 30.5898

Width : 2.825

Height : 10.031

Entry



# B0L002S, U25-ch14

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 27.8780

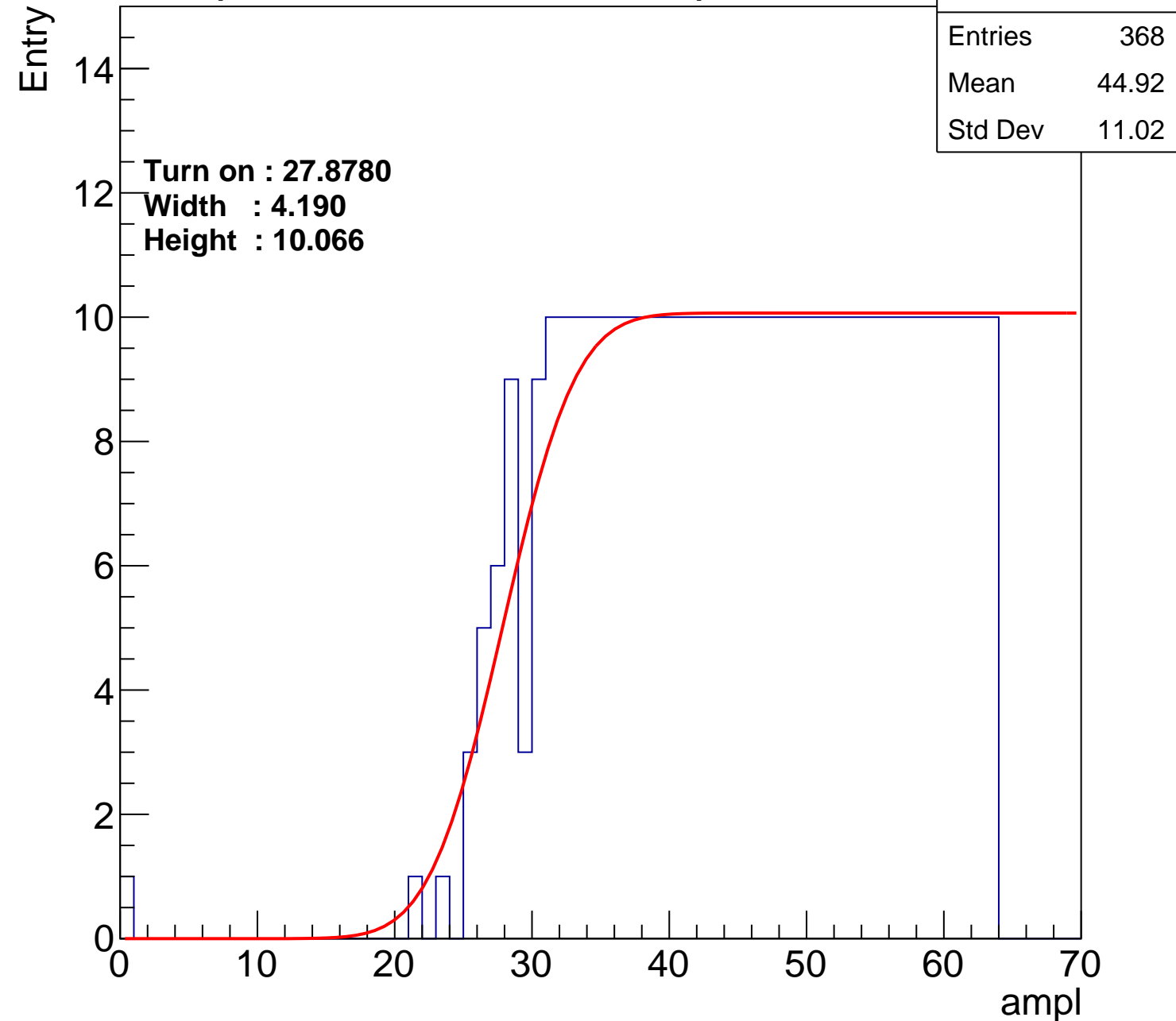
Width : 4.190

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch15

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.18
Std Dev	11.06

**Turn on : 27.7939**

**Width : 3.591**

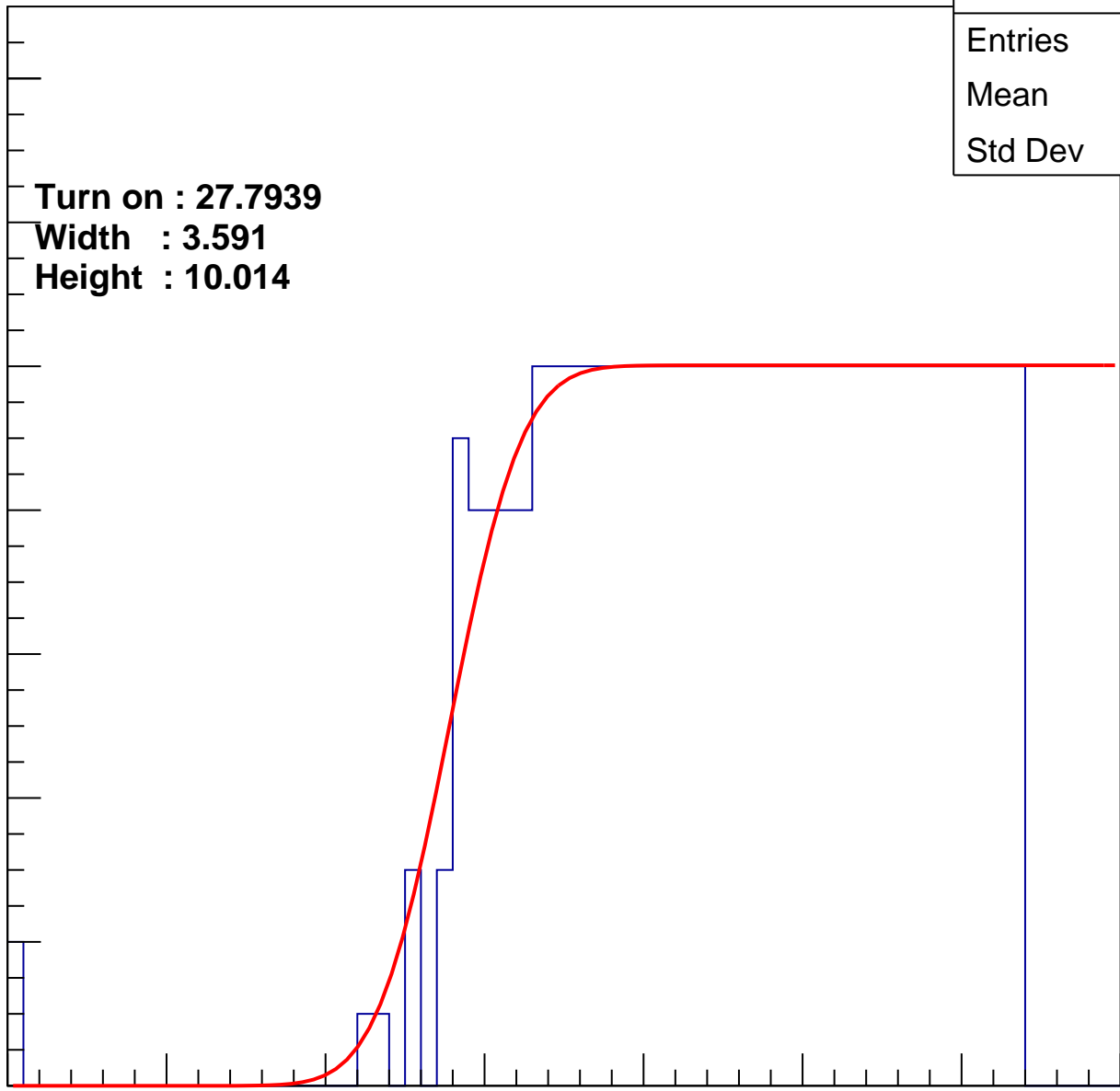
**Height : 10.014**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U25-ch16

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	351
Mean	45.68
Std Dev	10.79

**Turn on : 28.8853**

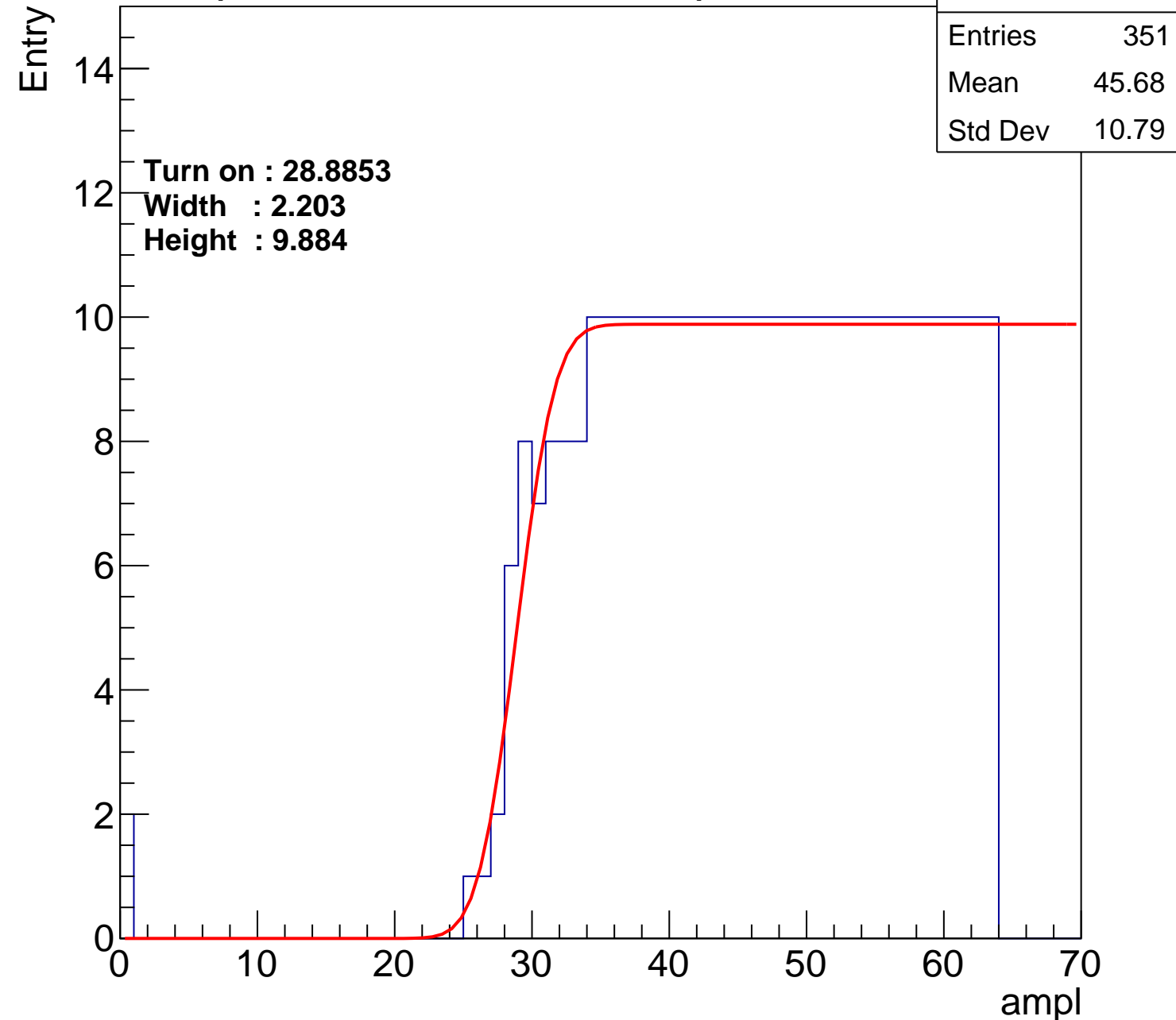
**Width : 2.203**

**Height : 9.884**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch17

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	350
Mean	45.77
Std Dev	10.61

Turn on : 29.5062

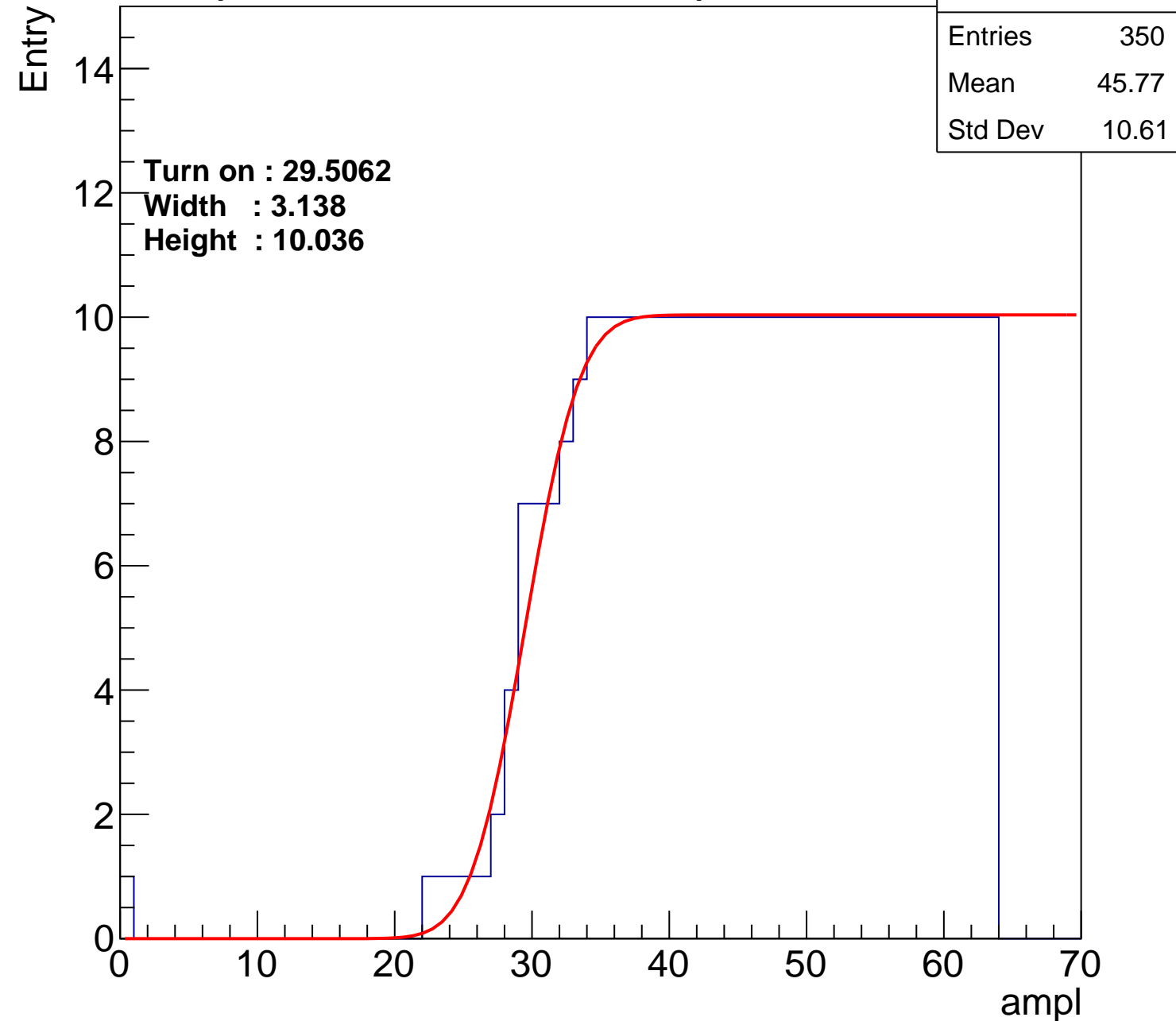
Width : 3.138

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch18

calib\_packv5\_042523\_0143.root, FC#8, port C1

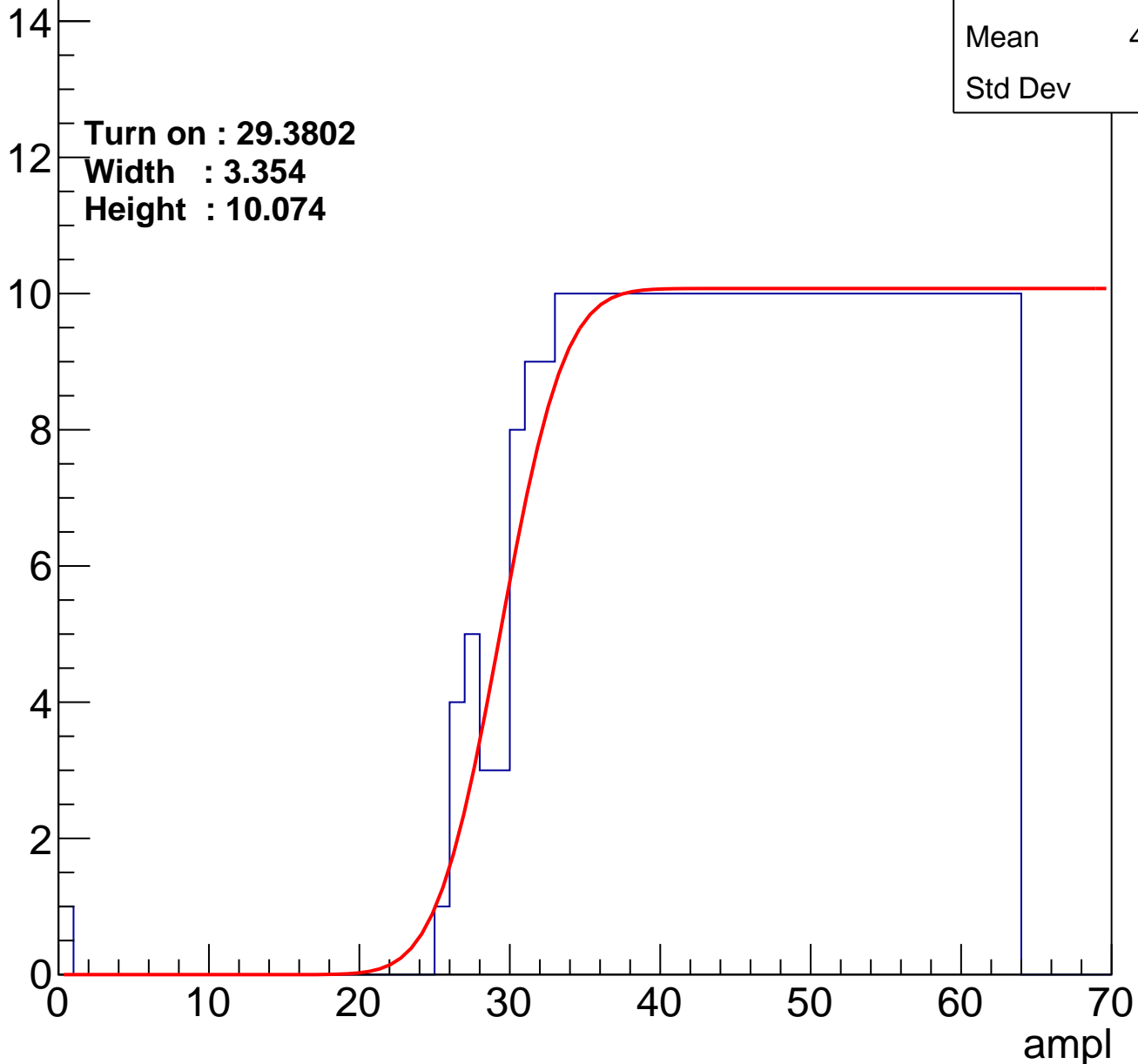
Entries	353
Mean	45.67
Std Dev	10.6

Turn on : 29.3802

Width : 3.354

Height : 10.074

Entry



# B0L002S, U25-ch19

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.33
Std Dev	10.72

Turn on : 27.9763

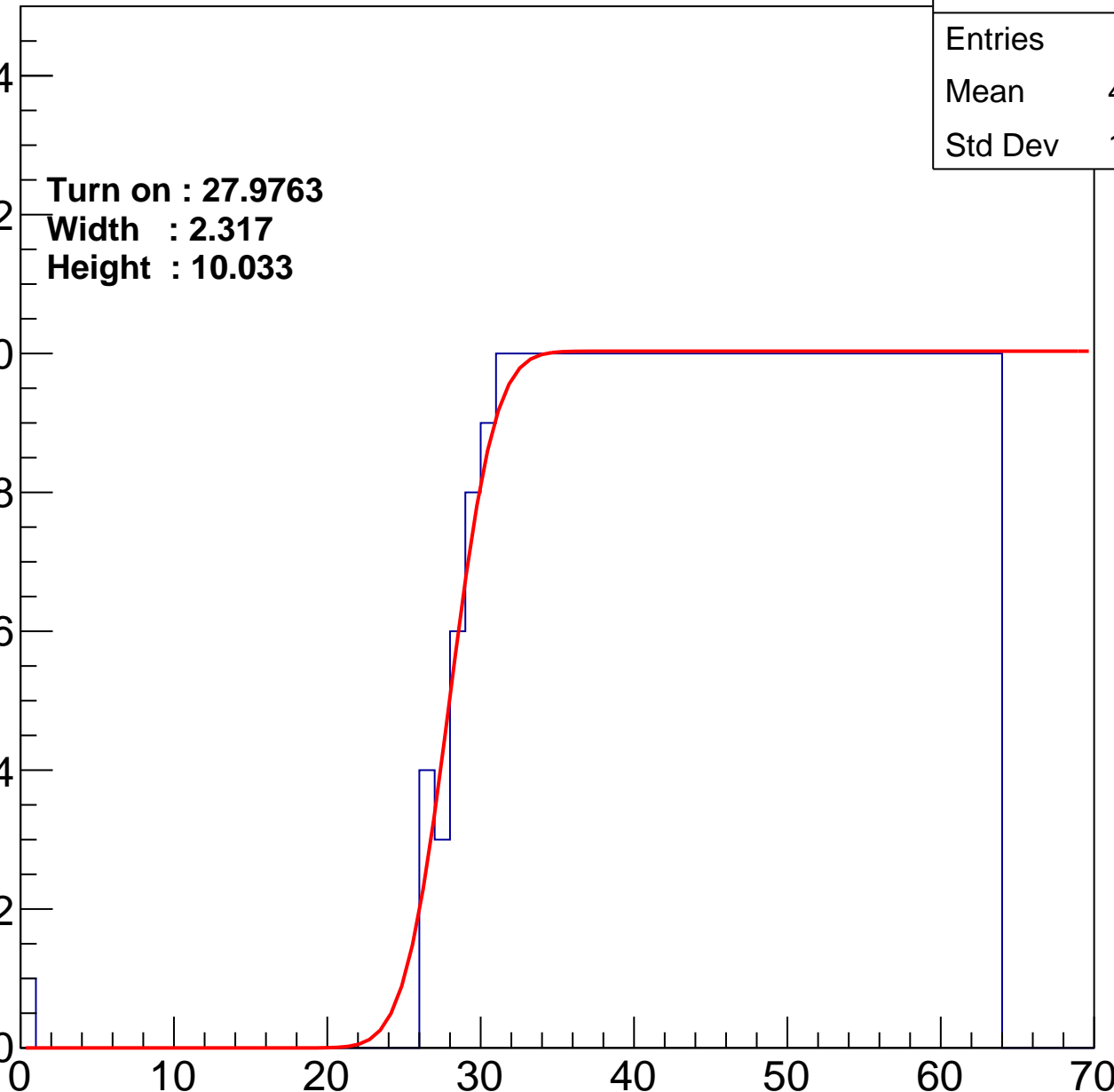
Width : 2.317

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch20

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	378
Mean	44.35
Std Dev	11.48

Turn on : 26.6293

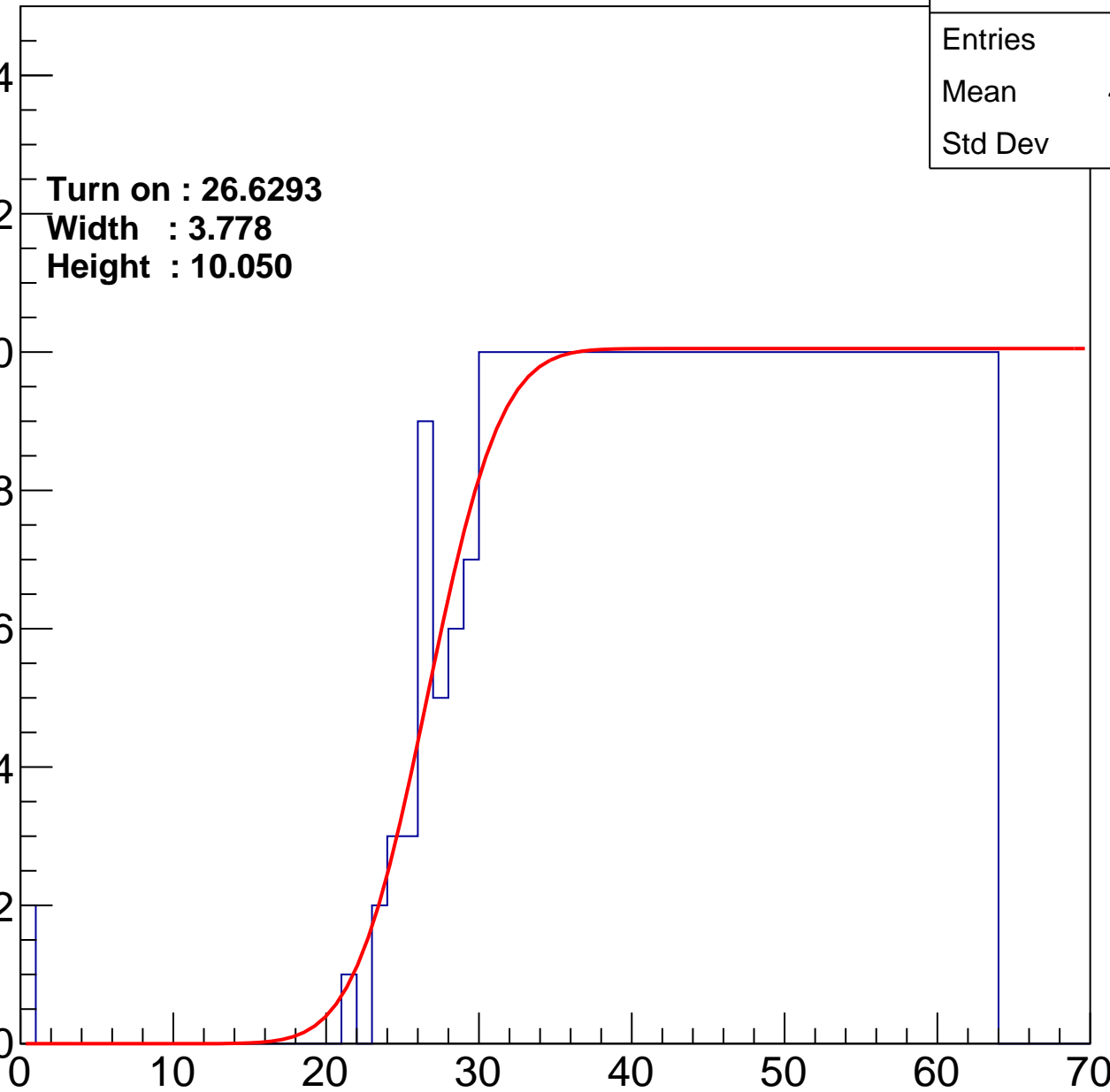
Width : 3.778

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch21

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	349
Mean	45.62
Std Dev	11.1

Turn on : 30.0104

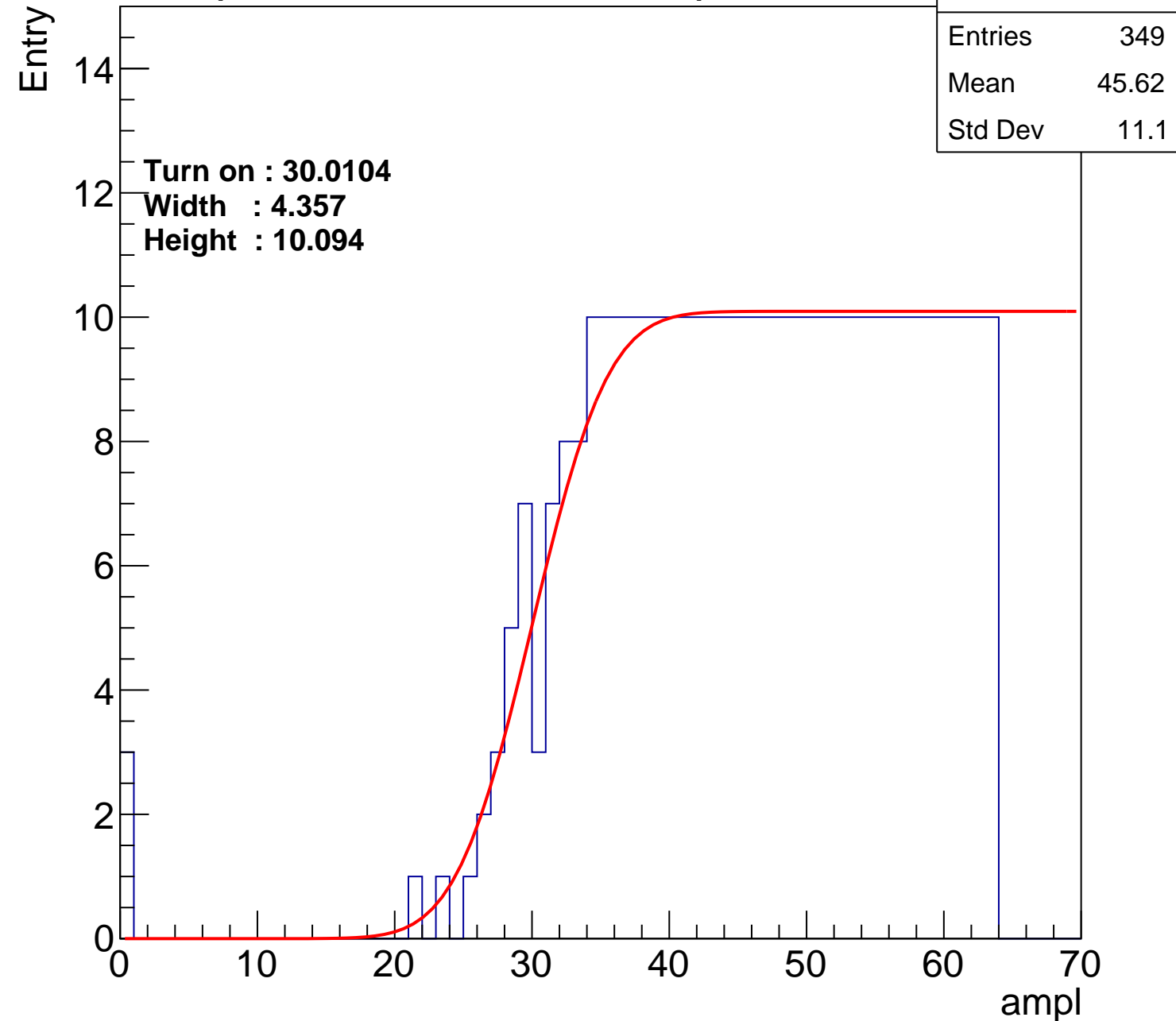
Width : 4.357

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch22

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	350
Mean	45.83
Std Dev	10.49

**Turn on : 29.2483**

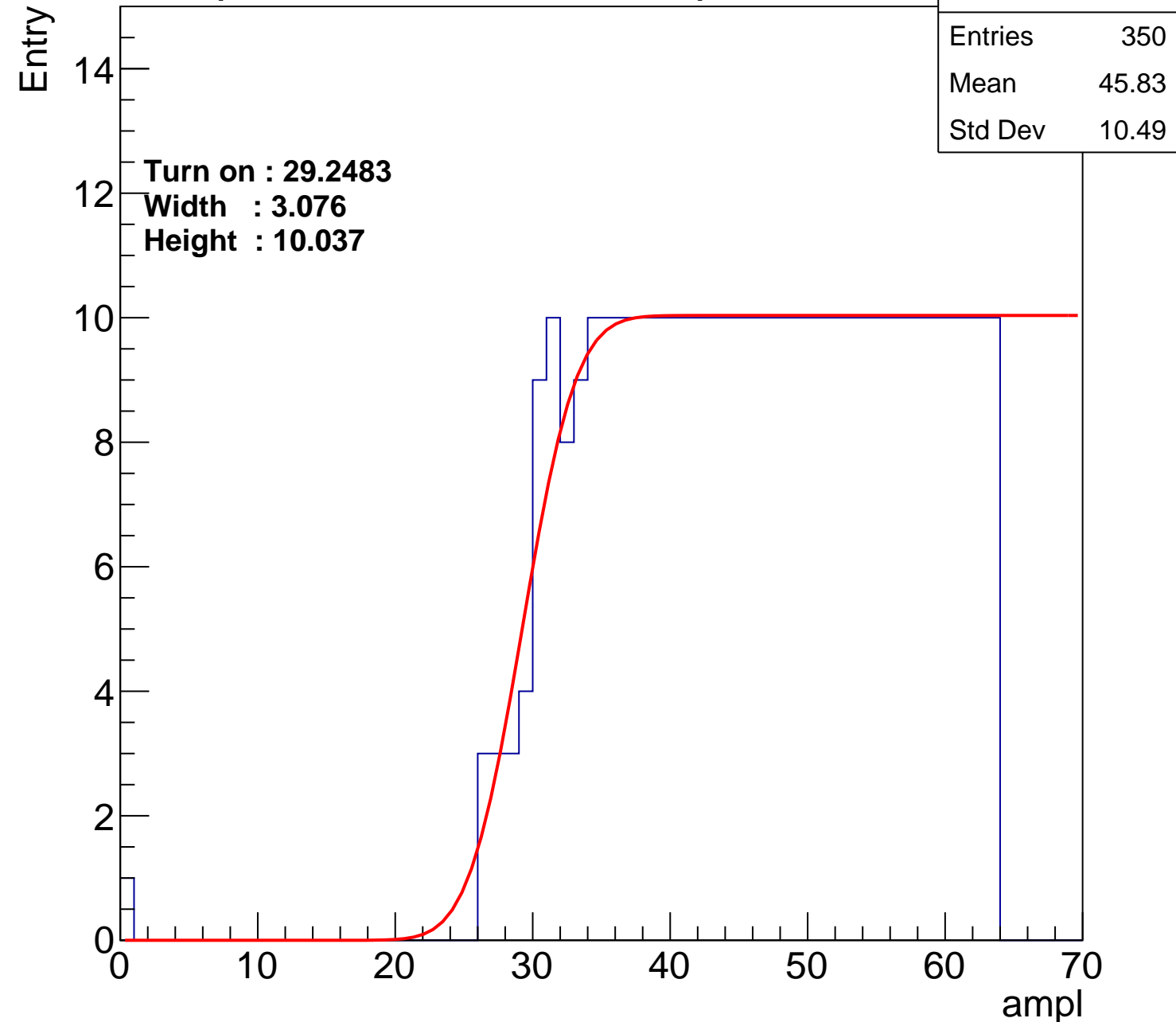
**Width : 3.076**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch23

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.26
Std Dev	10.83

Turn on : 28.0124

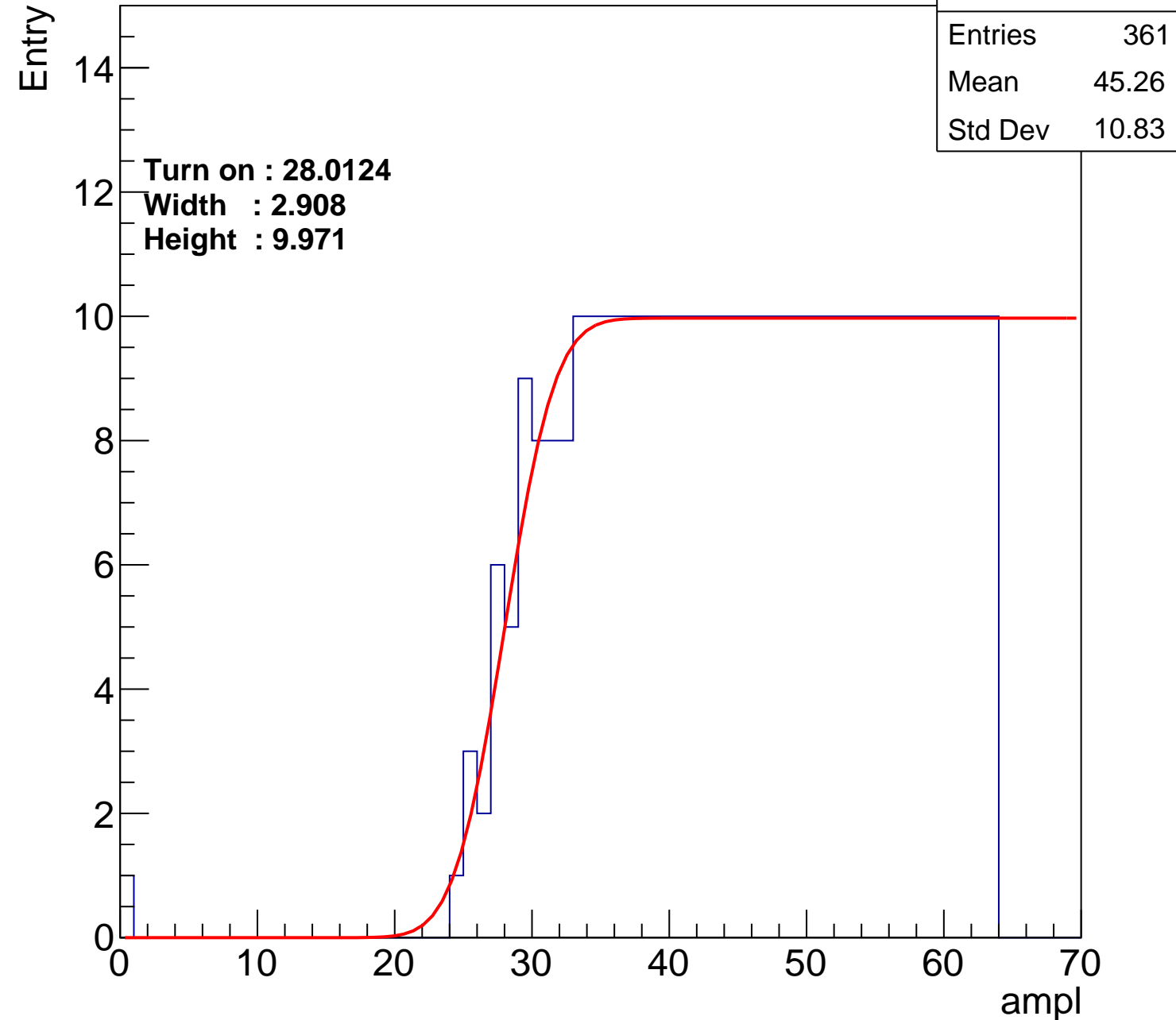
Width : 2.908

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch24

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.14
Std Dev	11.27

Turn on : 28.7821

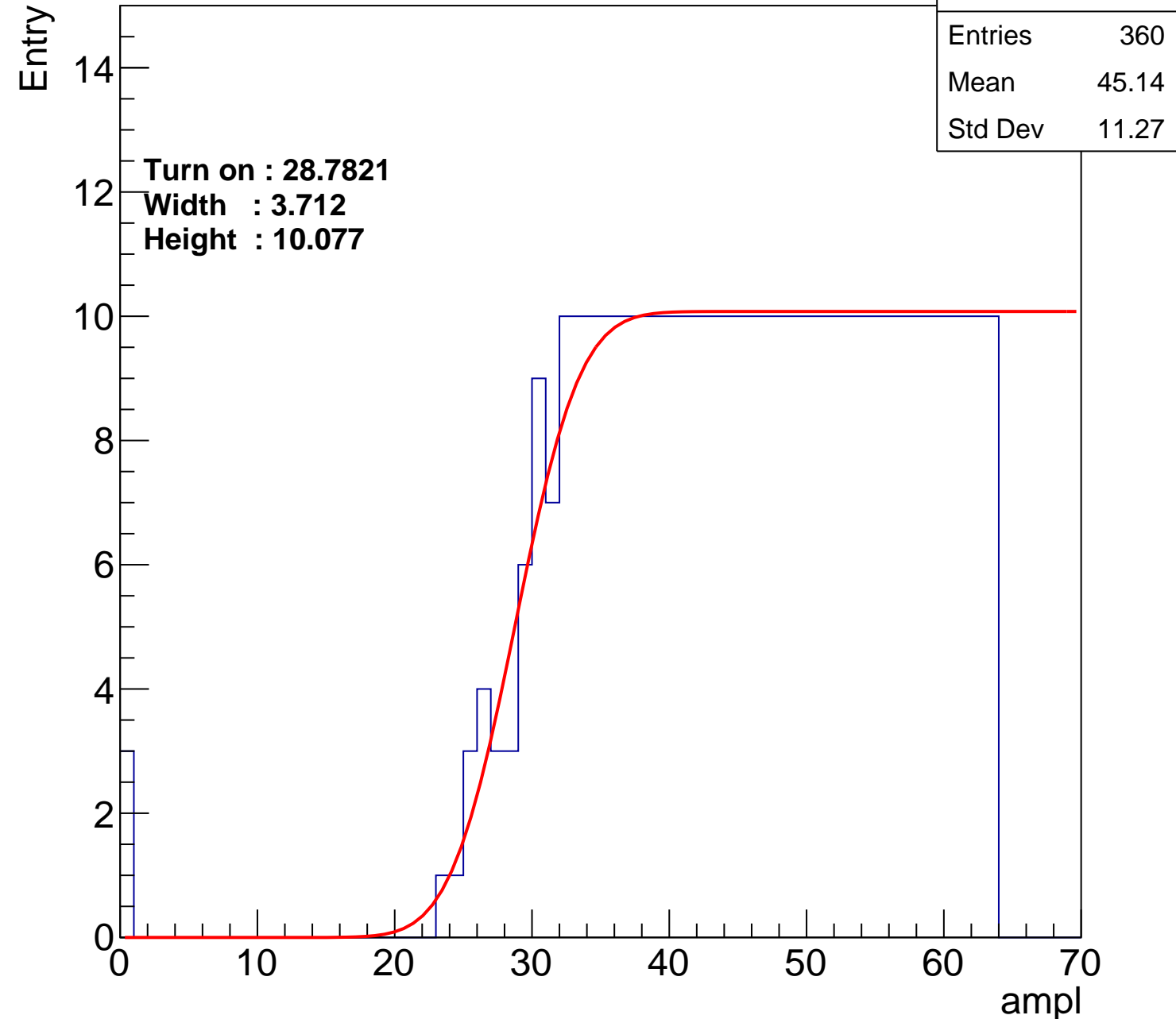
Width : 3.712

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch25

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	352
Mean	45.7
Std Dev	10.59

Turn on : 28.1310

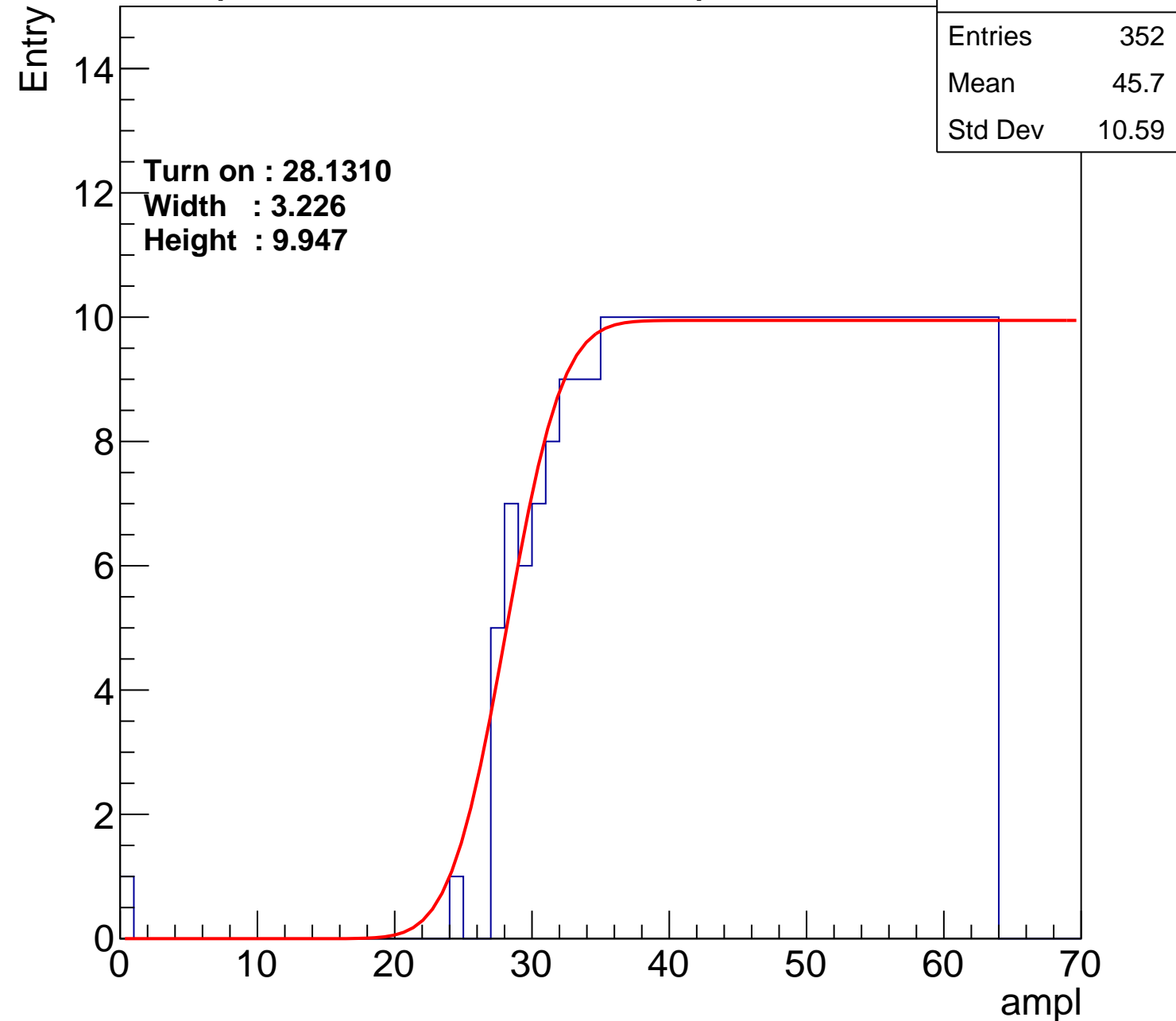
Width : 3.226

Height : 9.947

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch26

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.76
Std Dev	11.39

Turn on : 27.9621

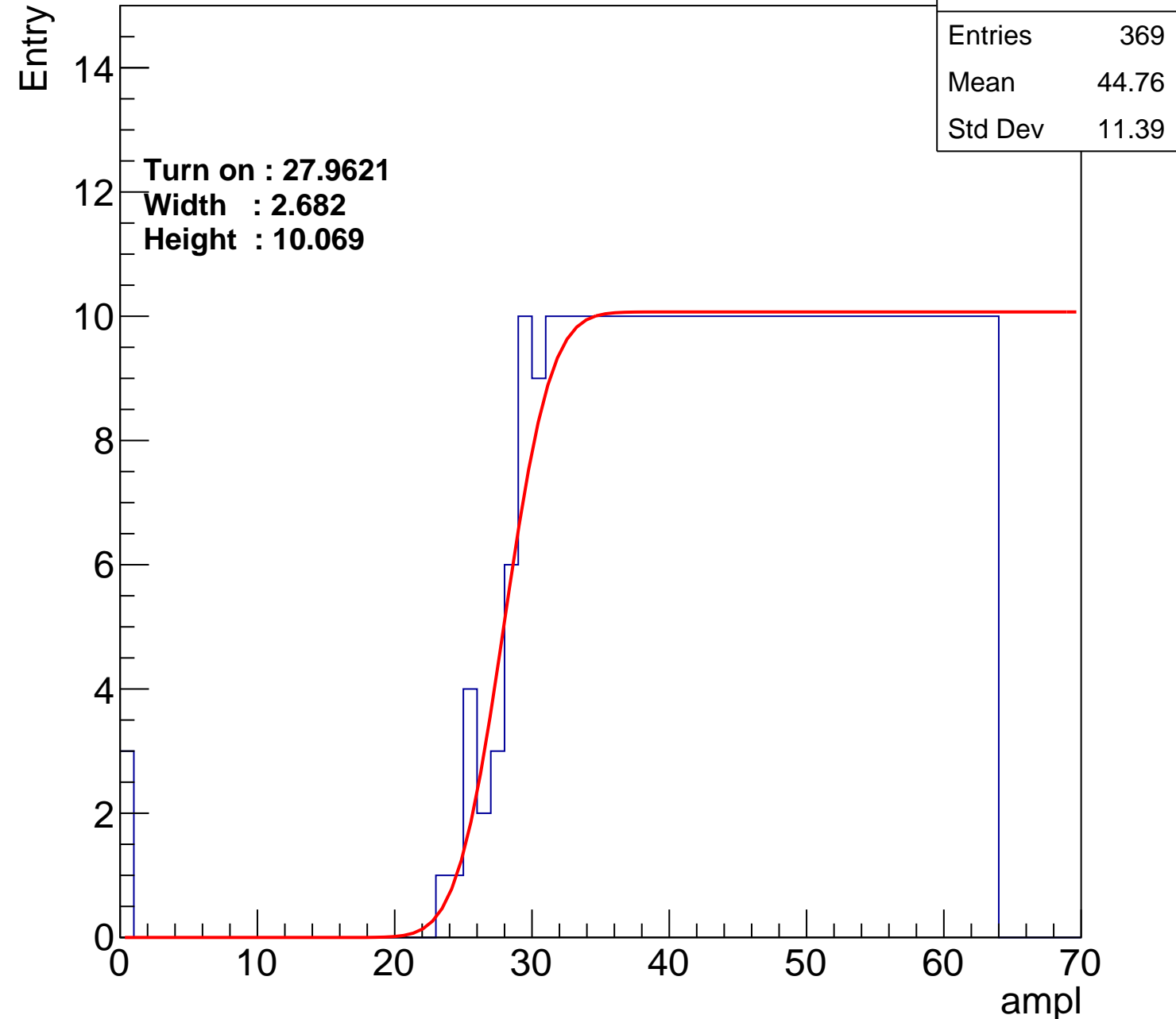
Width : 2.682

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch27

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.98
Std Dev	11.1

**Turn on : 27.1648**

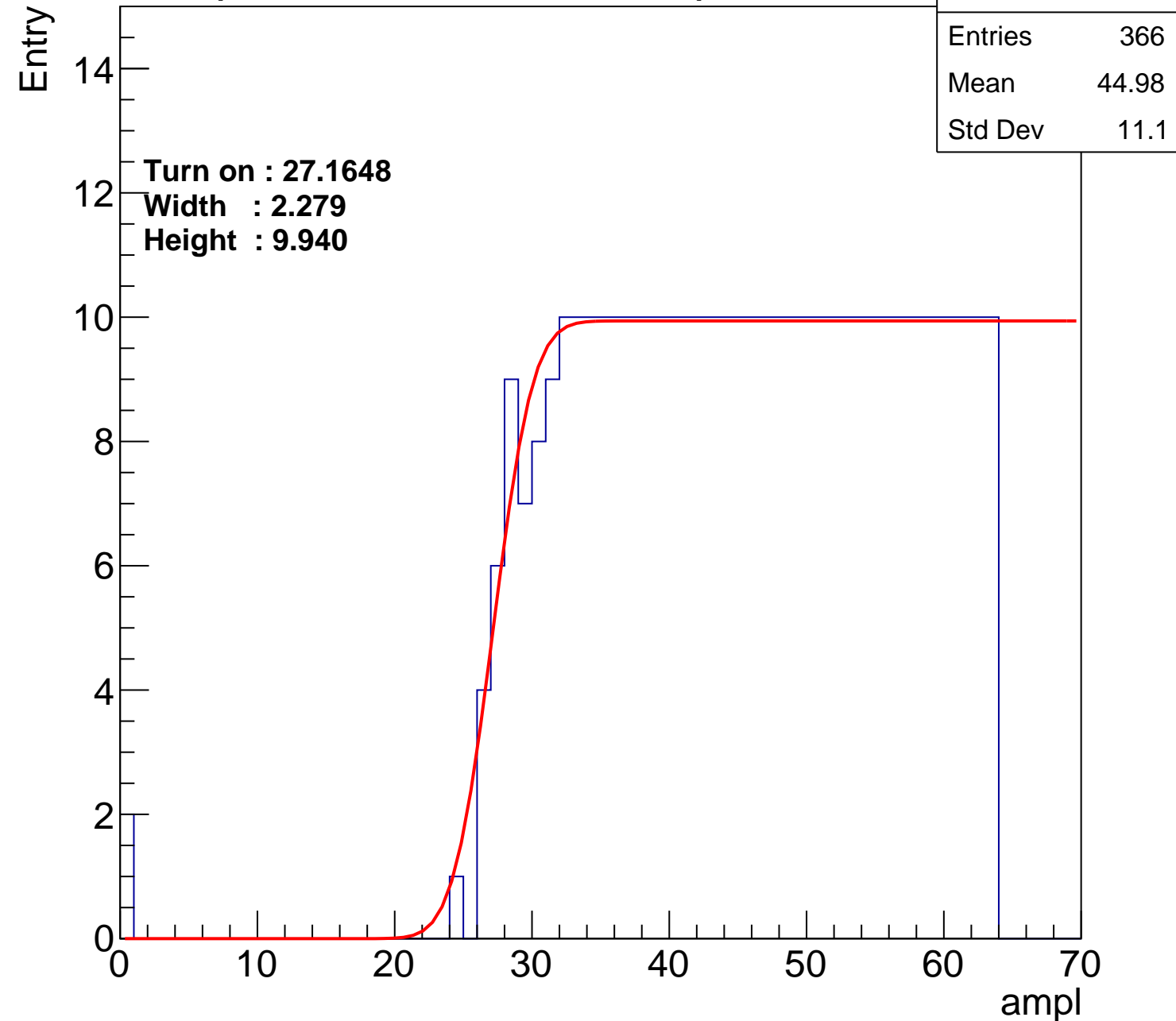
**Width : 2.279**

**Height : 9.940**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch28

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.65
Std Dev	11.18

Turn on : 27.2368

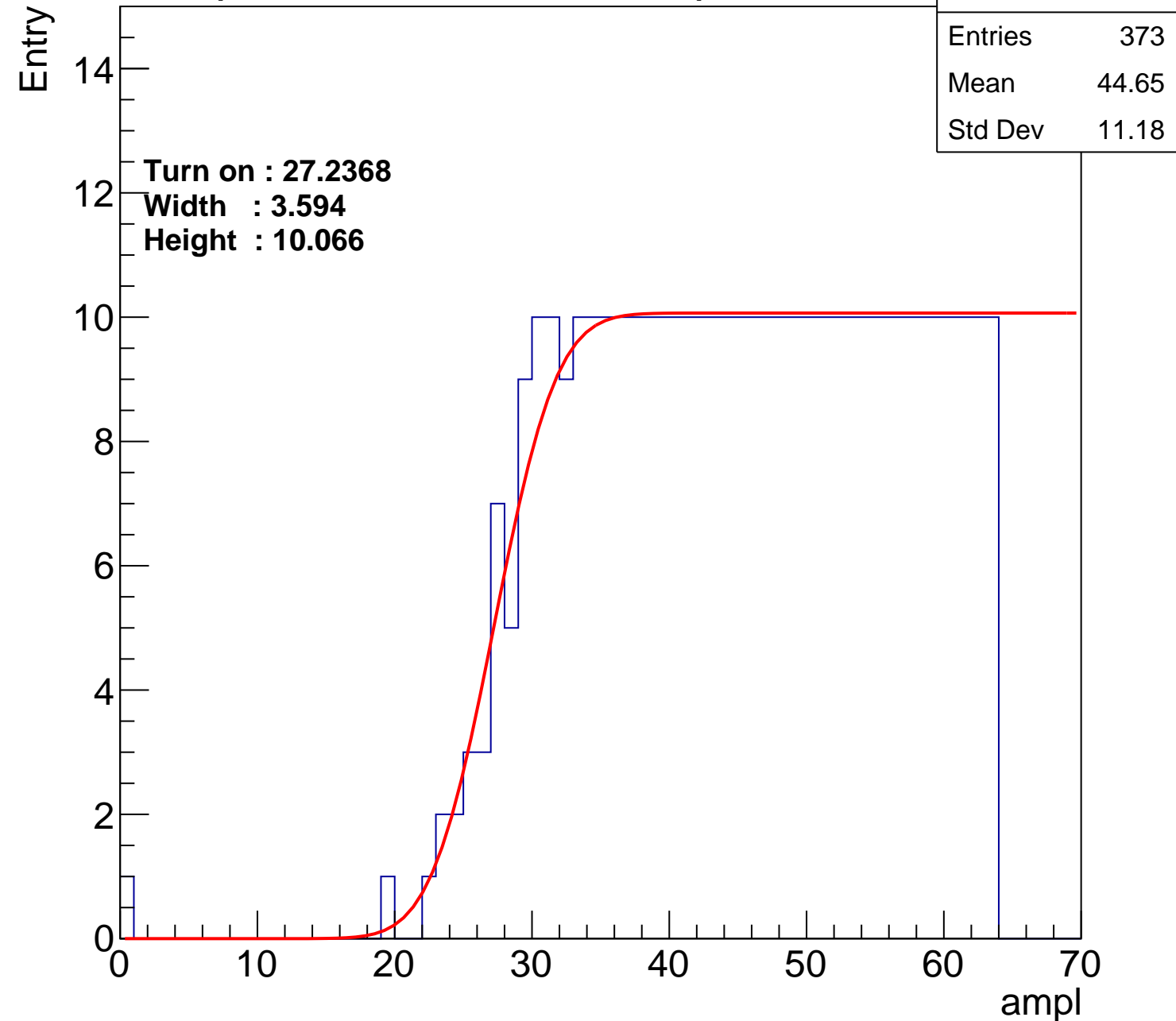
Width : 3.594

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch29

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	348
Mean	45.76
Std Dev	10.94

**Turn on : 29.4828**

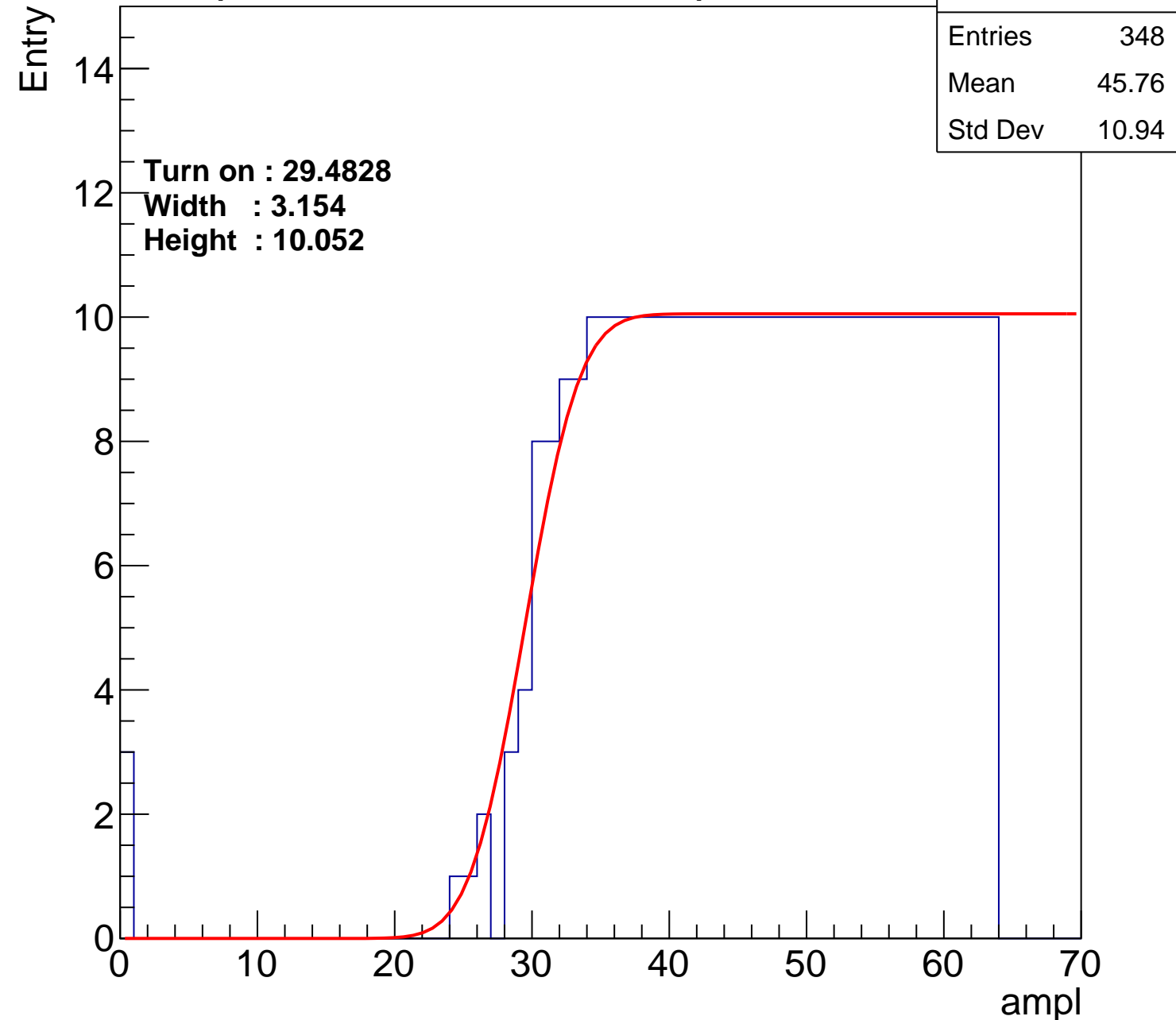
**Width : 3.154**

**Height : 10.052**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch30

calib\_packv5\_042523\_0143.root, FC#8, port C1

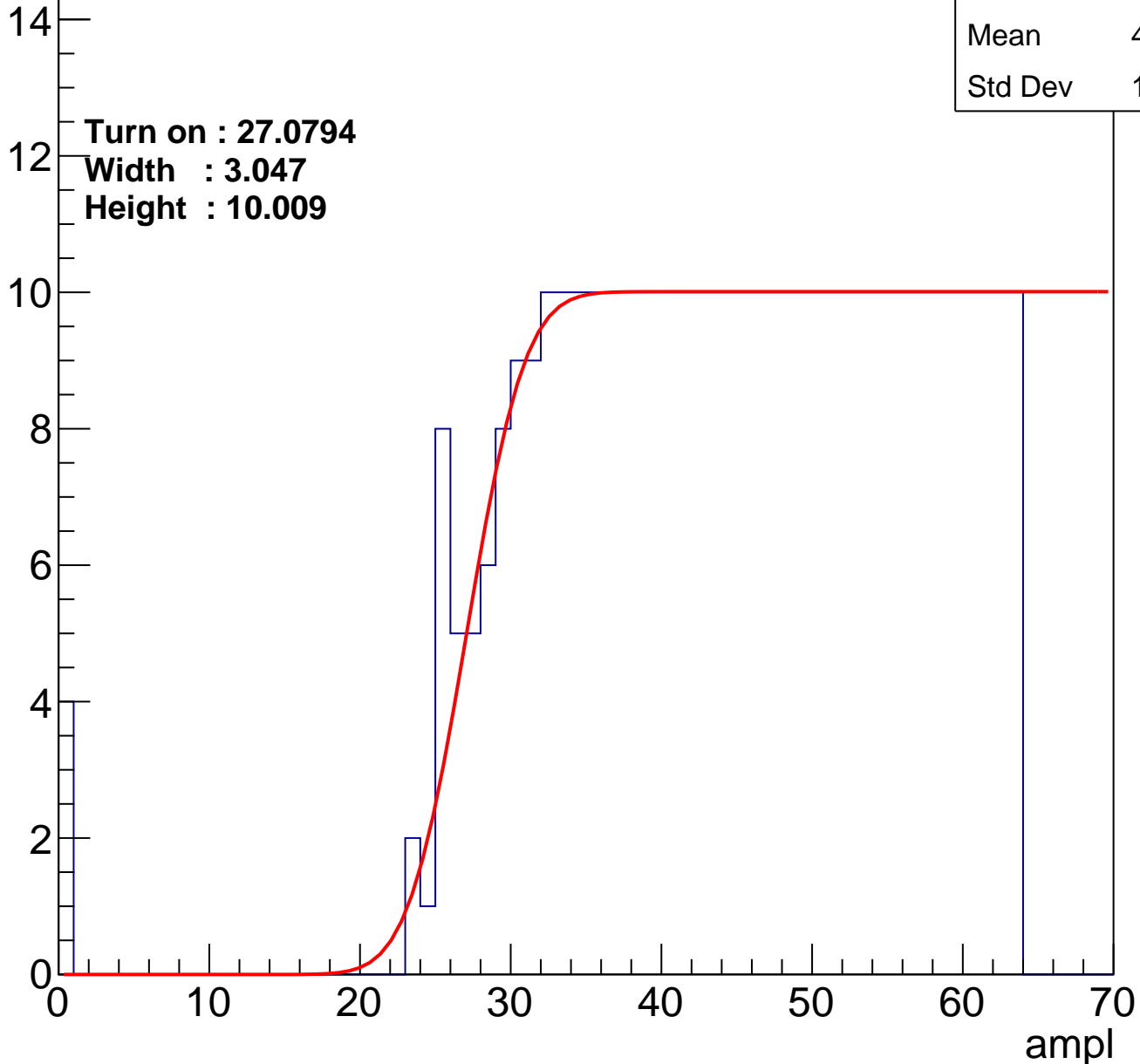
Entries	377
Mean	44.25
Std Dev	11.83

Turn on : 27.0794

Width : 3.047

Height : 10.009

Entry





# B0L002S, U25-ch31

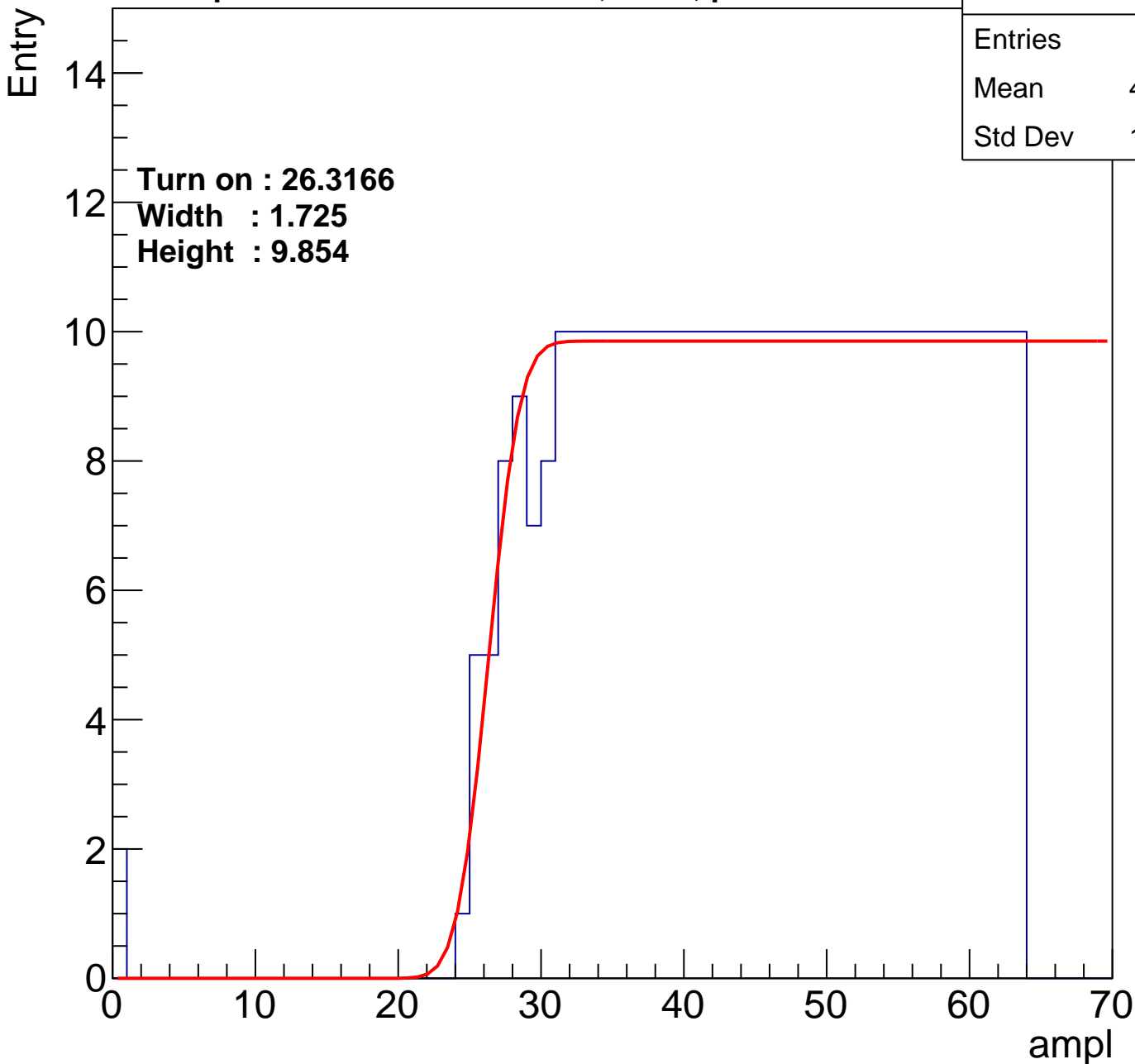
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	375
Mean	44.53
Std Dev	11.34

Turn on : 26.3166

Width : 1.725

Height : 9.854



# B0L002S, U25-ch32

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.67
Std Dev	10.6

Turn on : 29.0069

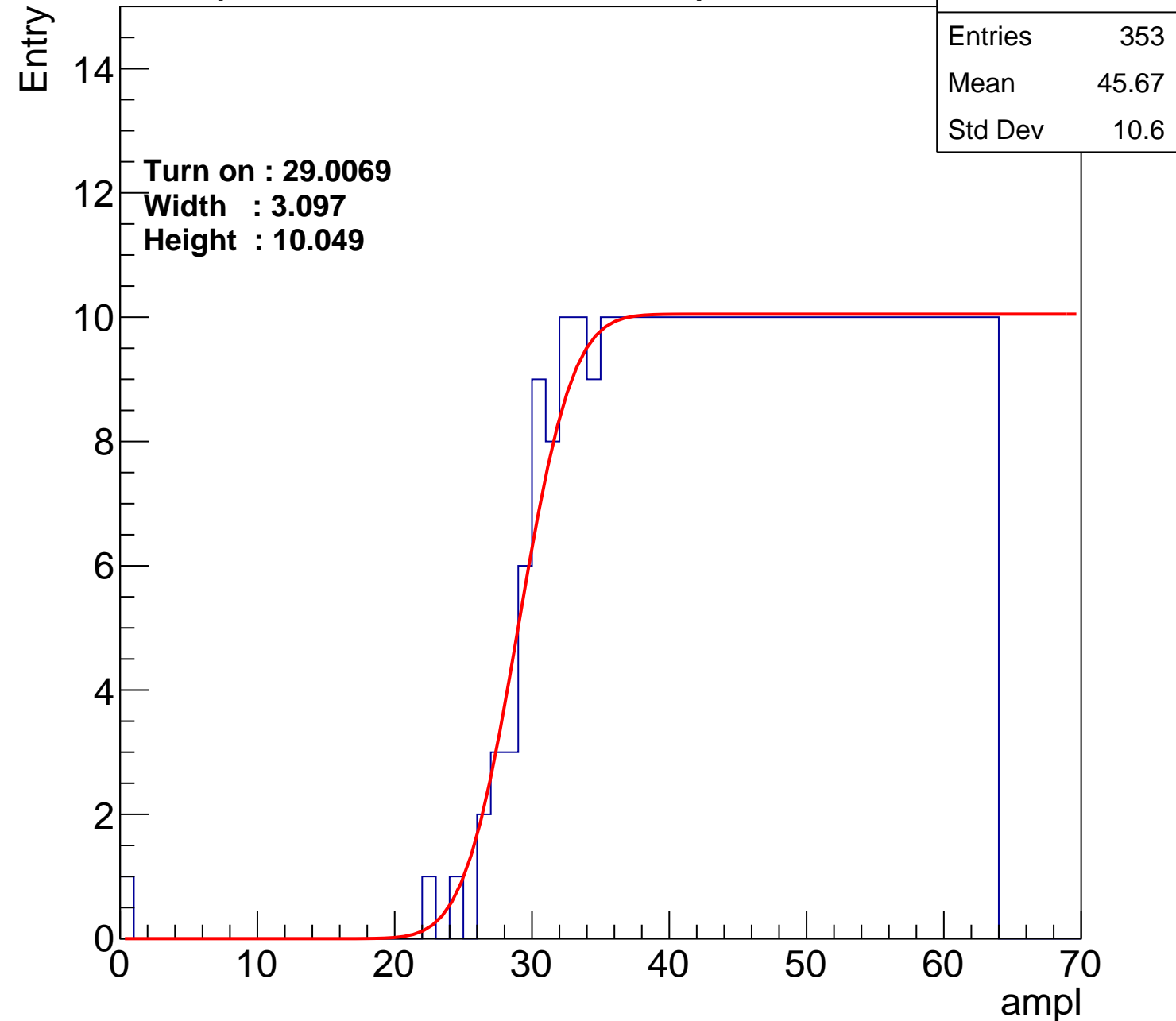
Width : 3.097

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch33

calib\_packv5\_042523\_0143.root, FC#8, port C1

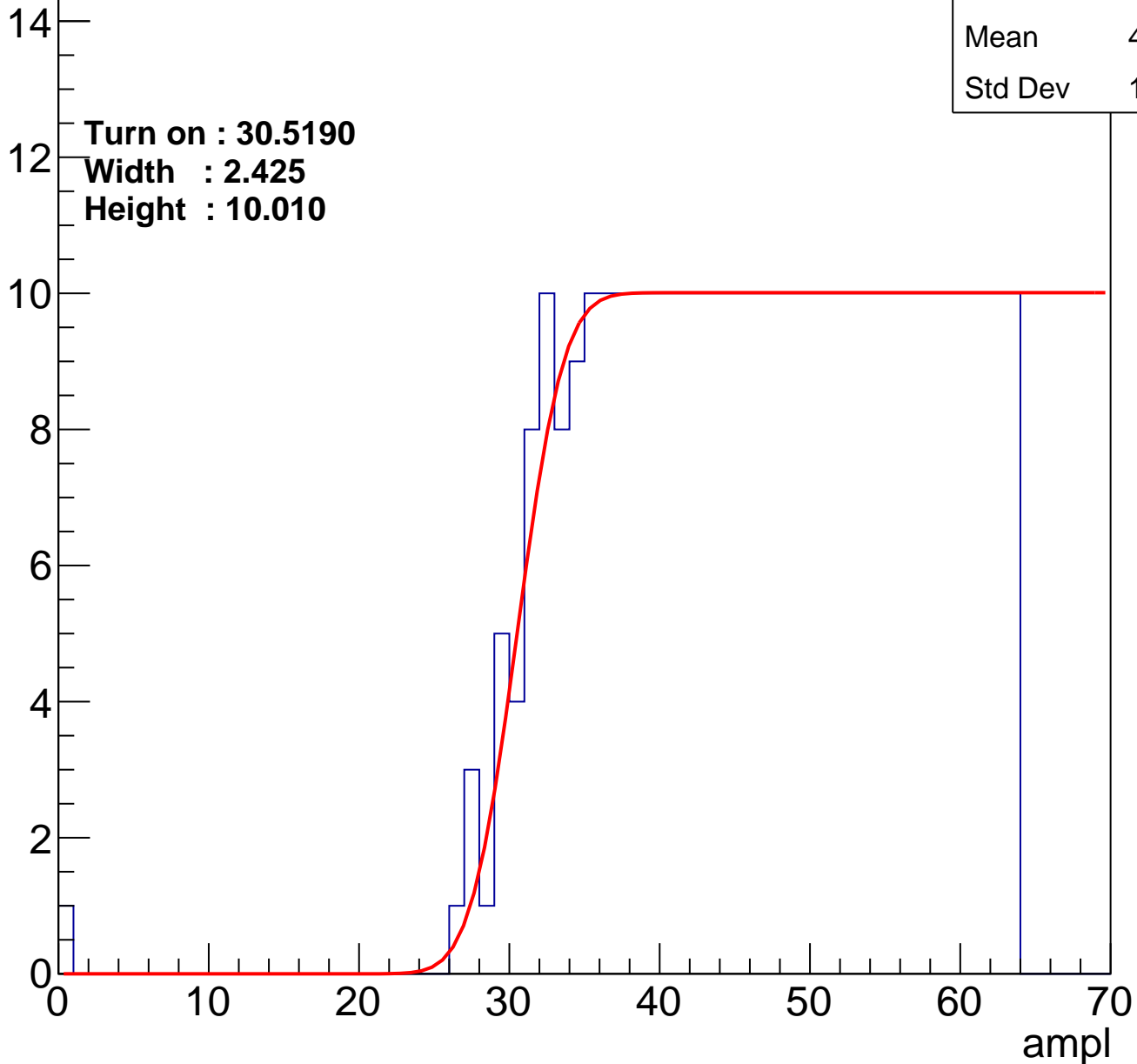
Entries	340
Mean	46.32
Std Dev	10.24

Turn on : 30.5190

Width : 2.425

Height : 10.010

Entry



# B0L002S, U25-ch34

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	341
Mean	46.24
Std Dev	10.32

Turn on : 30.1381

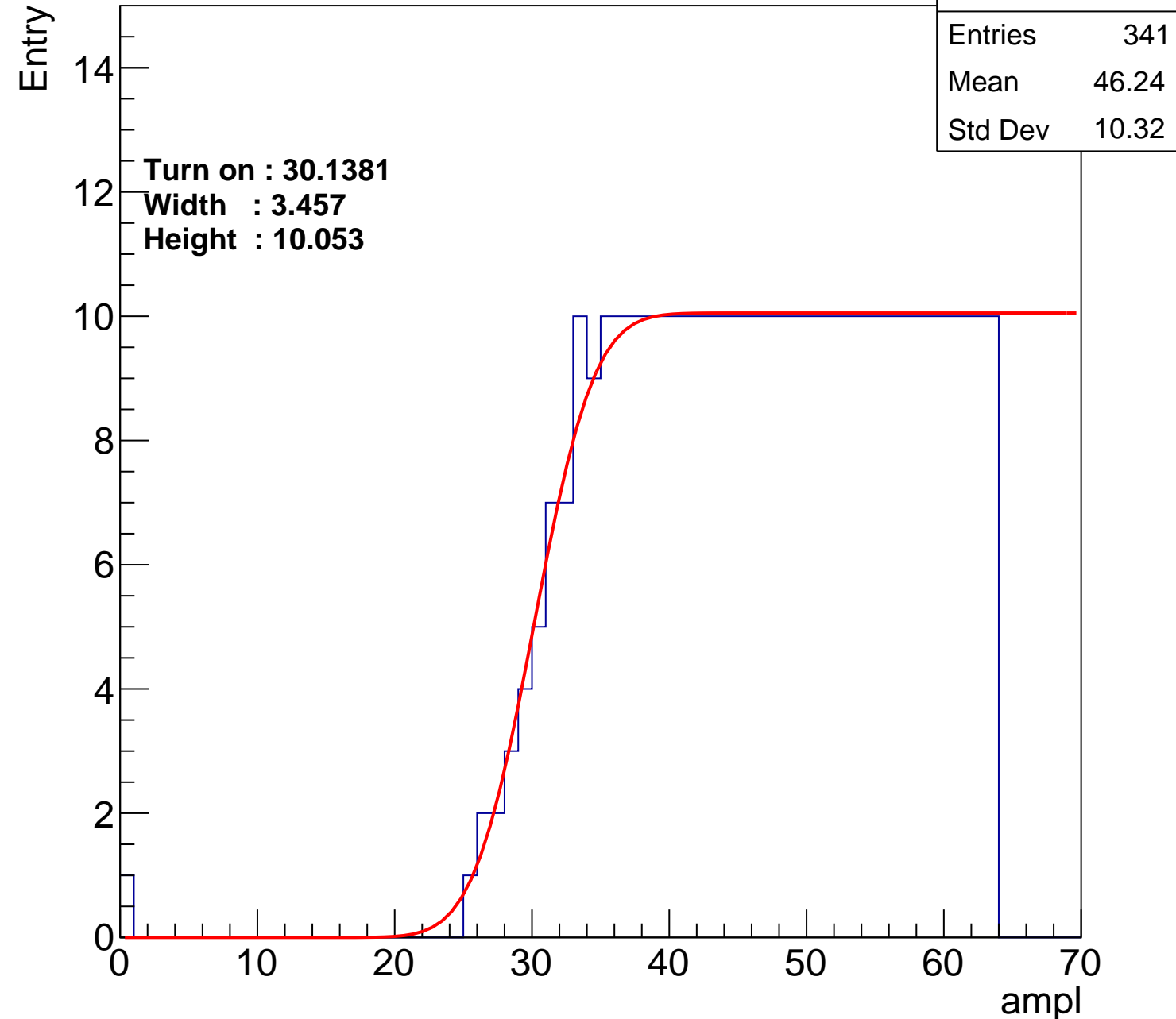
Width : 3.457

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch35

calib\_packv5\_042523\_0143.root, FC#8, port C1

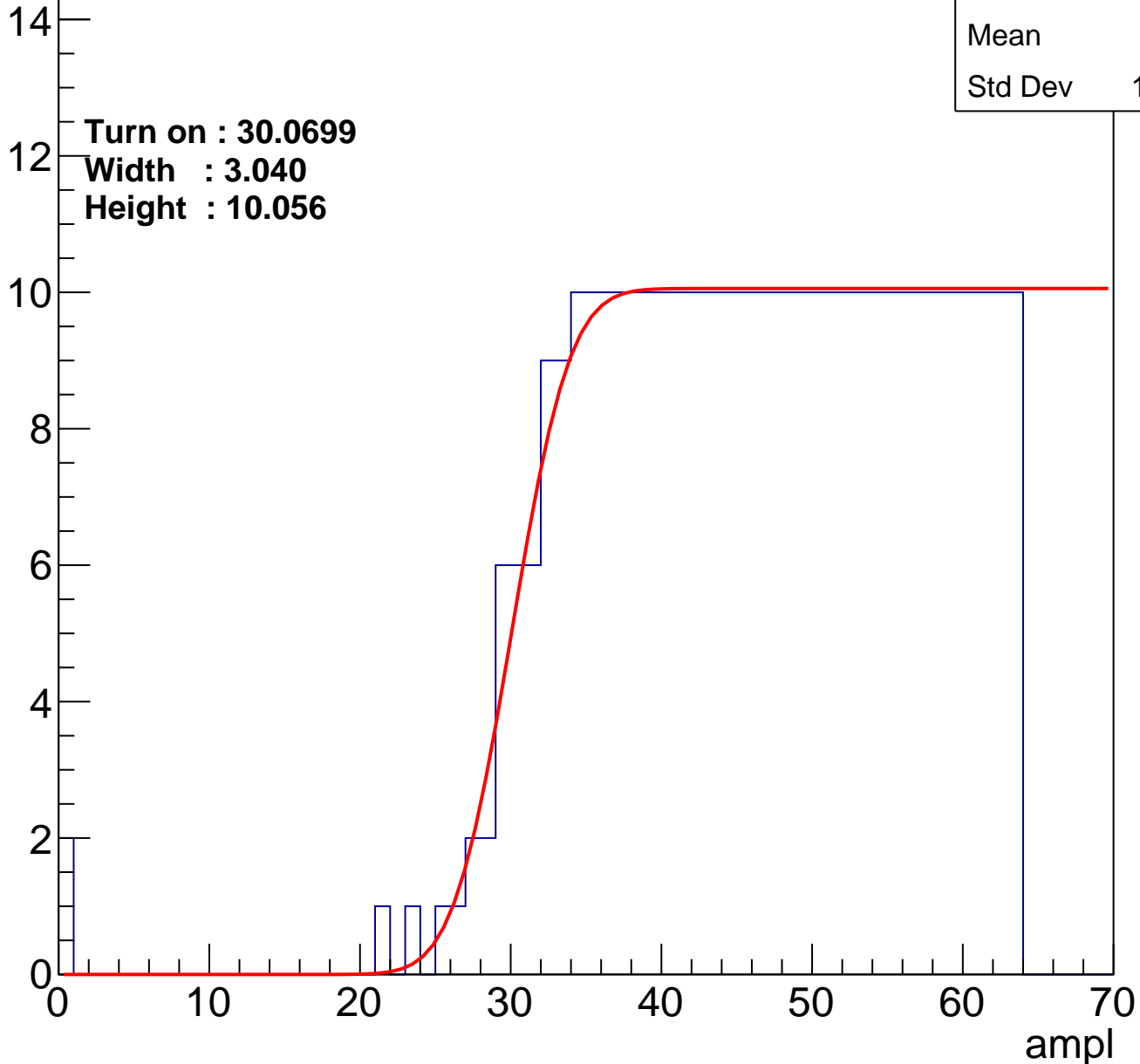
Entries	346
Mean	45.9
Std Dev	10.73

Turn on : 30.0699

Width : 3.040

Height : 10.056

Entry



# B0L002S, U25-ch36

calib\_packv5\_042523\_0143.root, FC#8, port C1

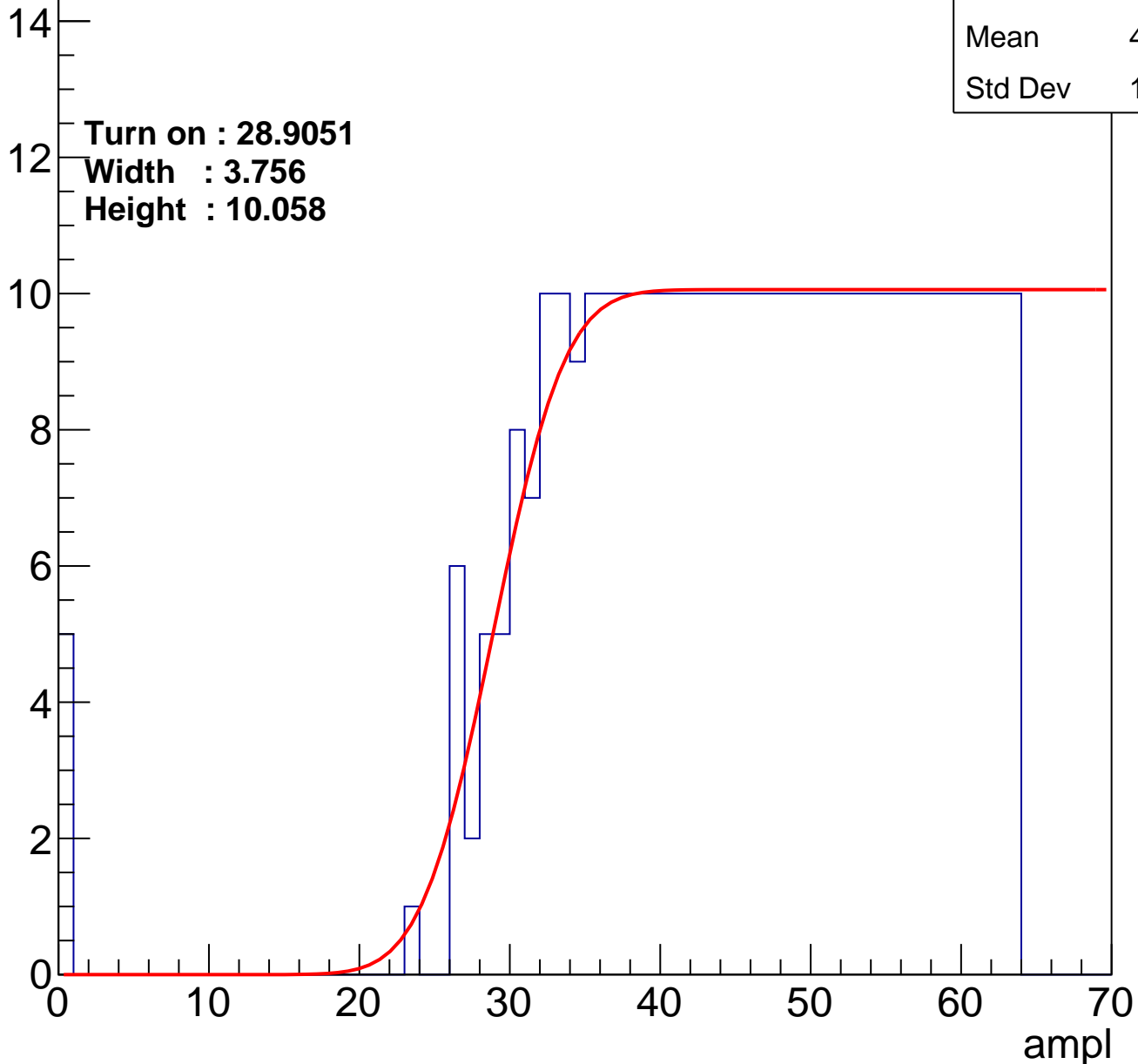
Entries	358
Mean	45.09
Std Dev	11.64

Turn on : 28.9051

Width : 3.756

Height : 10.058

Entry



# B0L002S, U25-ch37

calib\_packv5\_042523\_0143.root, FC#8, port C1

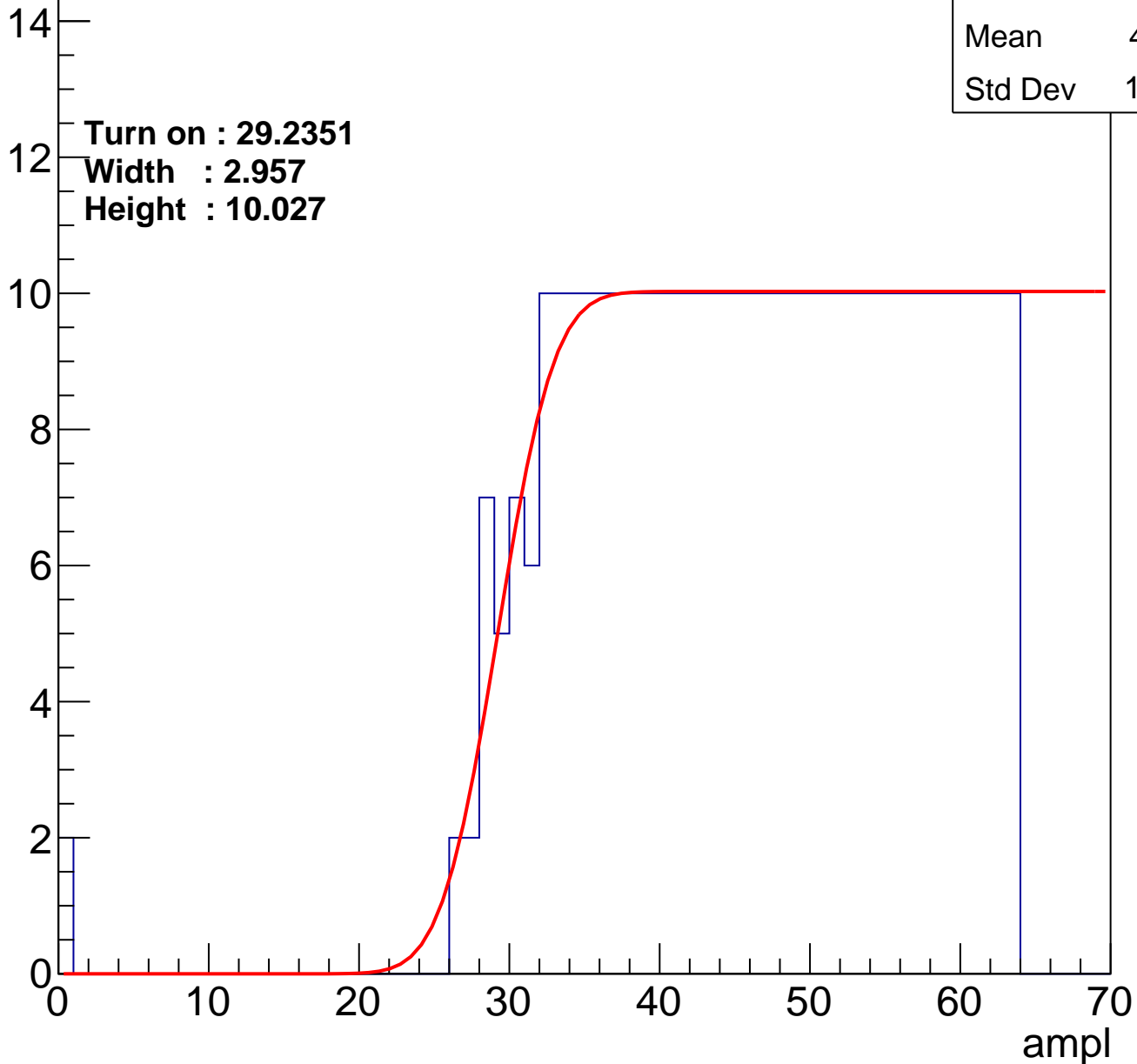
Entries	351
Mean	45.71
Std Dev	10.75

Turn on : 29.2351

Width : 2.957

Height : 10.027

Entry



# B0L002S, U25-ch38

calib\_packv5\_042523\_0143.root, FC#8, port C1

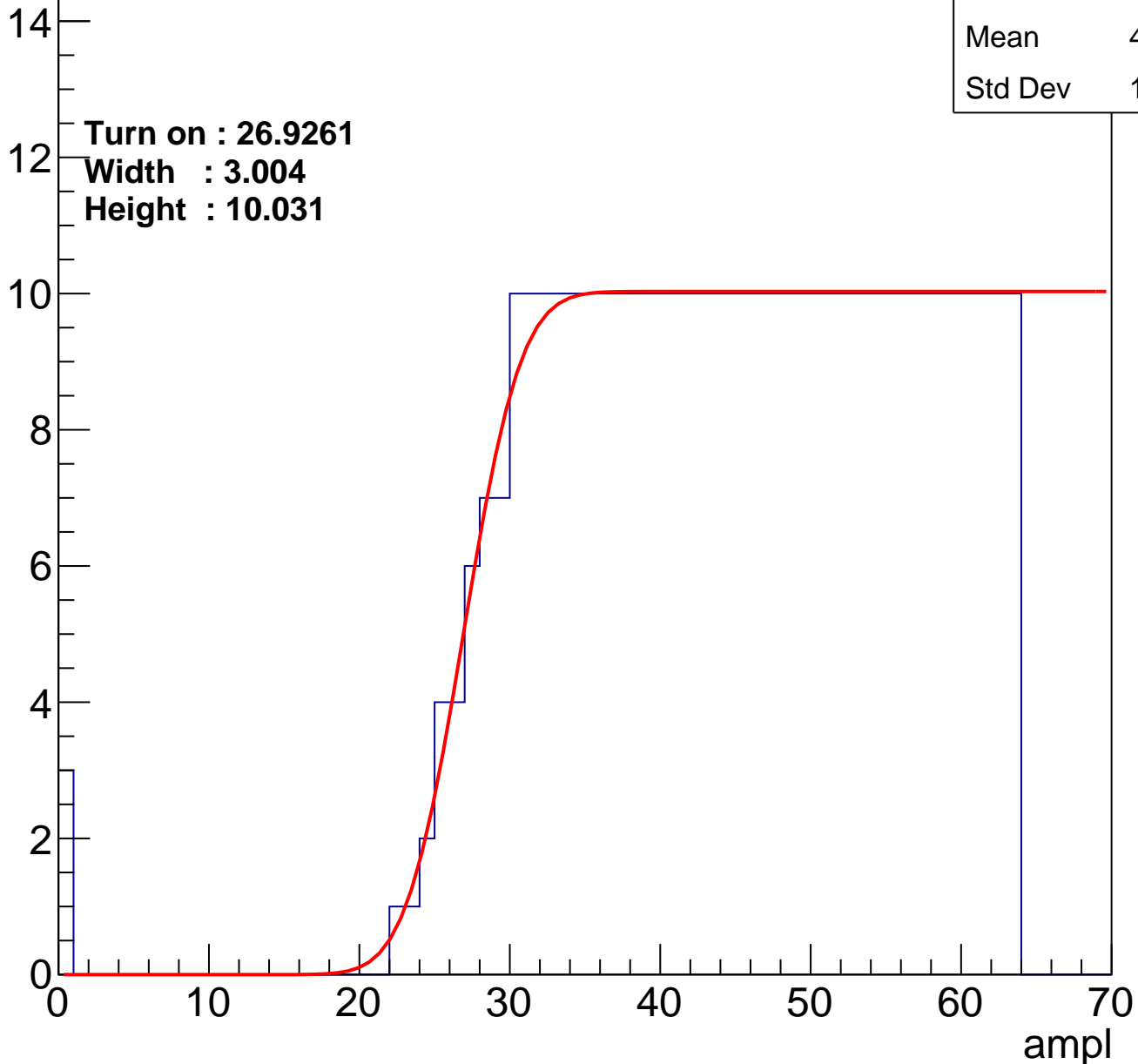
Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 26.9261

Width : 3.004

Height : 10.031

Entry





# B0L002S, U25-ch39

calib\_packv5\_042523\_0143.root, FC#8, port C1

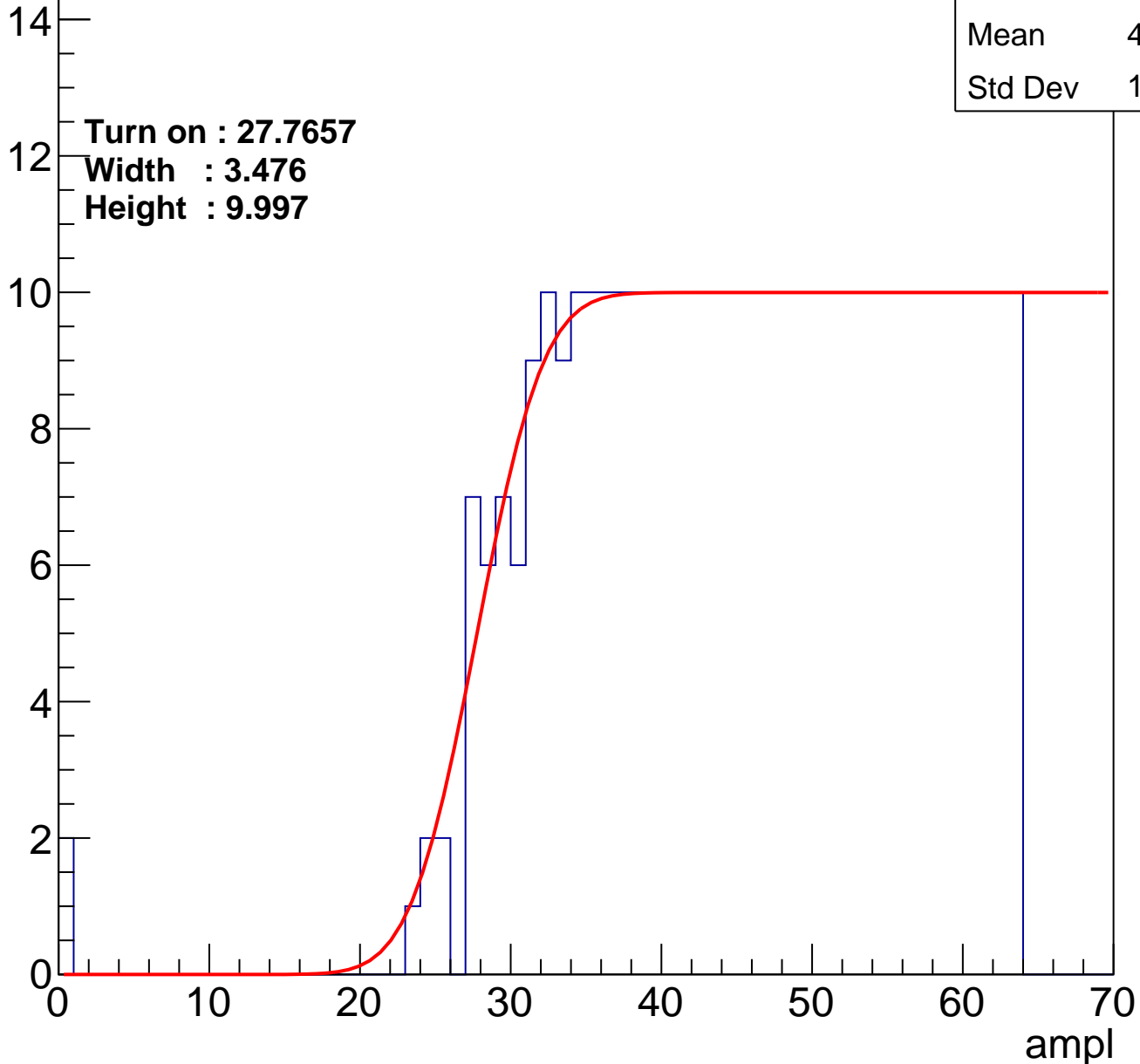
Entries	361
Mean	45.17
Std Dev	11.07

**Turn on : 27.7657**

**Width : 3.476**

**Height : 9.997**

Entry



# B0L002S, U25-ch40

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.39
Std Dev	11.08

Turn on : 28.3914

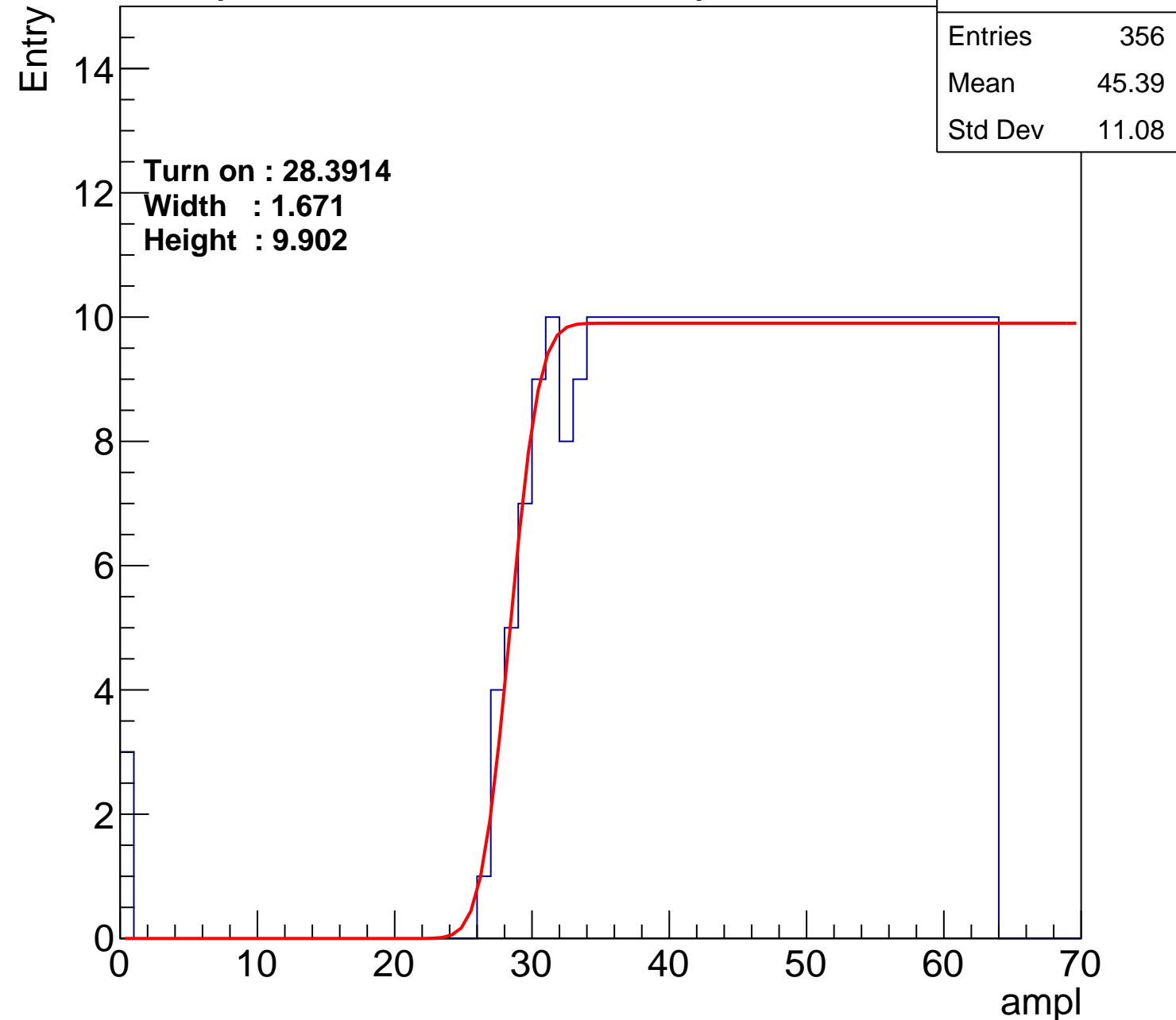
Width : 1.671

Height : 9.902

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch41

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	351
Mean	45.55
Std Dev	11.21

Turn on : 29.2130

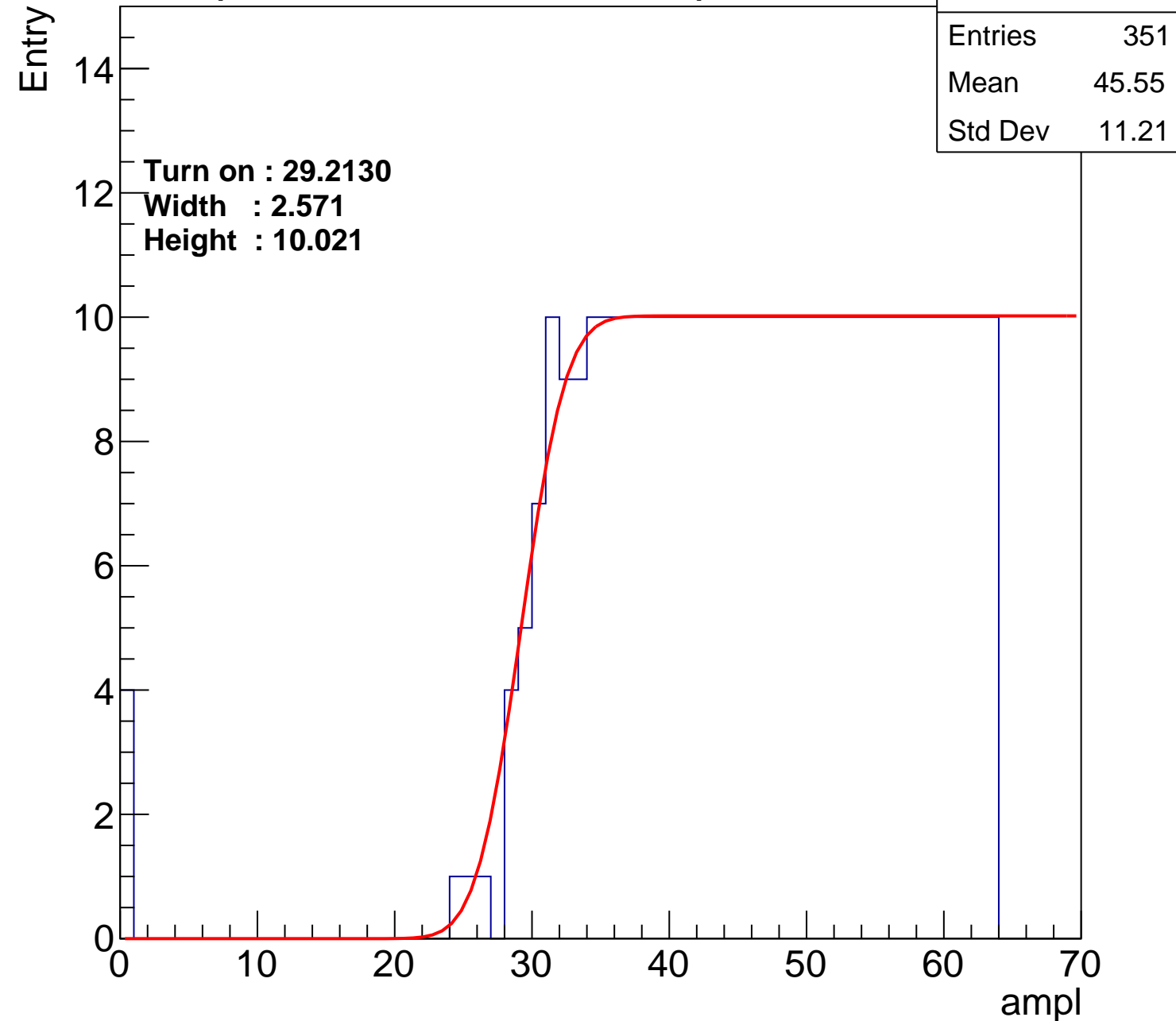
Width : 2.571

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch42

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.91
Std Dev	11.02

Turn on : 27.8333

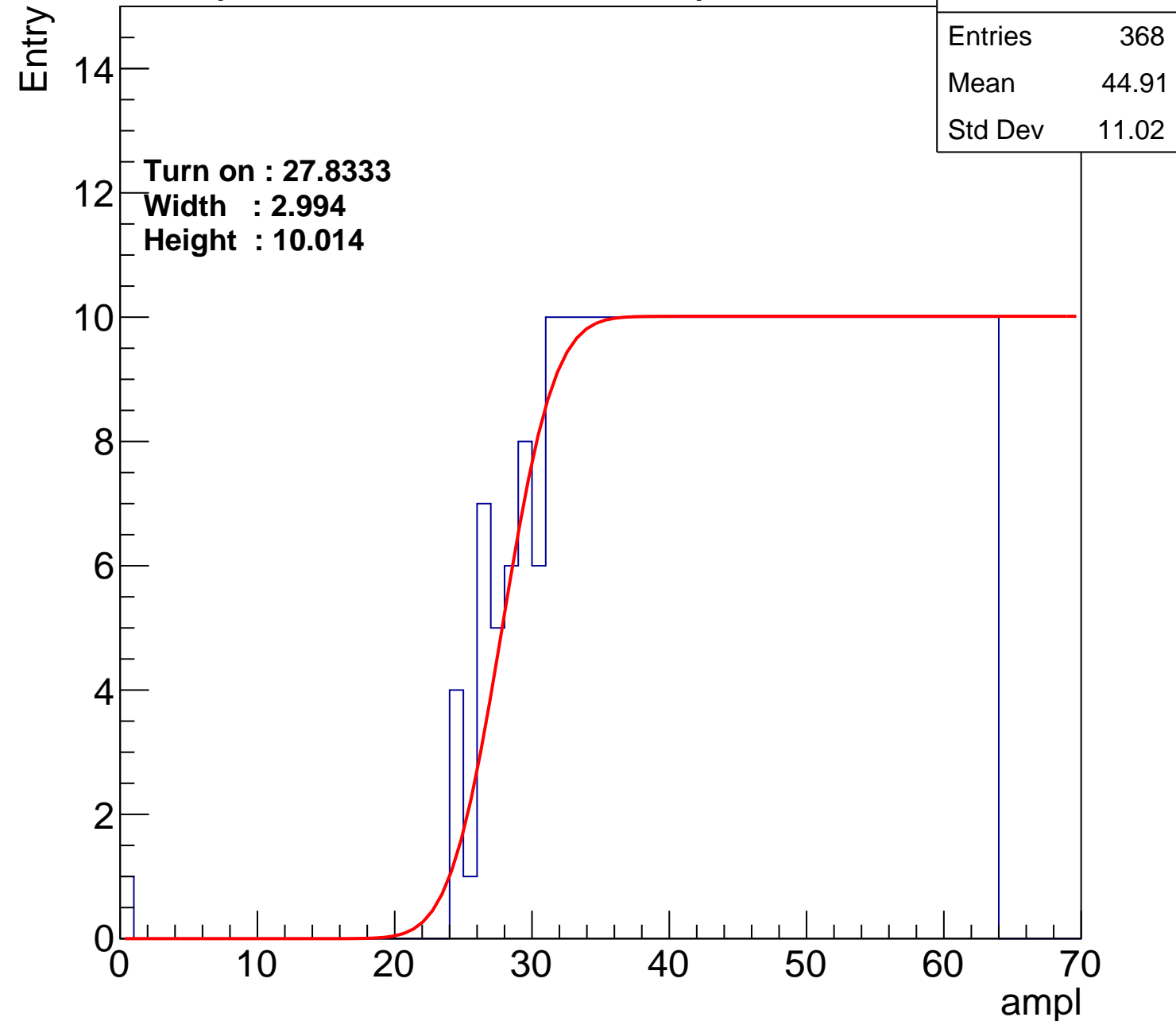
Width : 2.994

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch43

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.17
Std Dev	10.88

Turn on : 27.6720

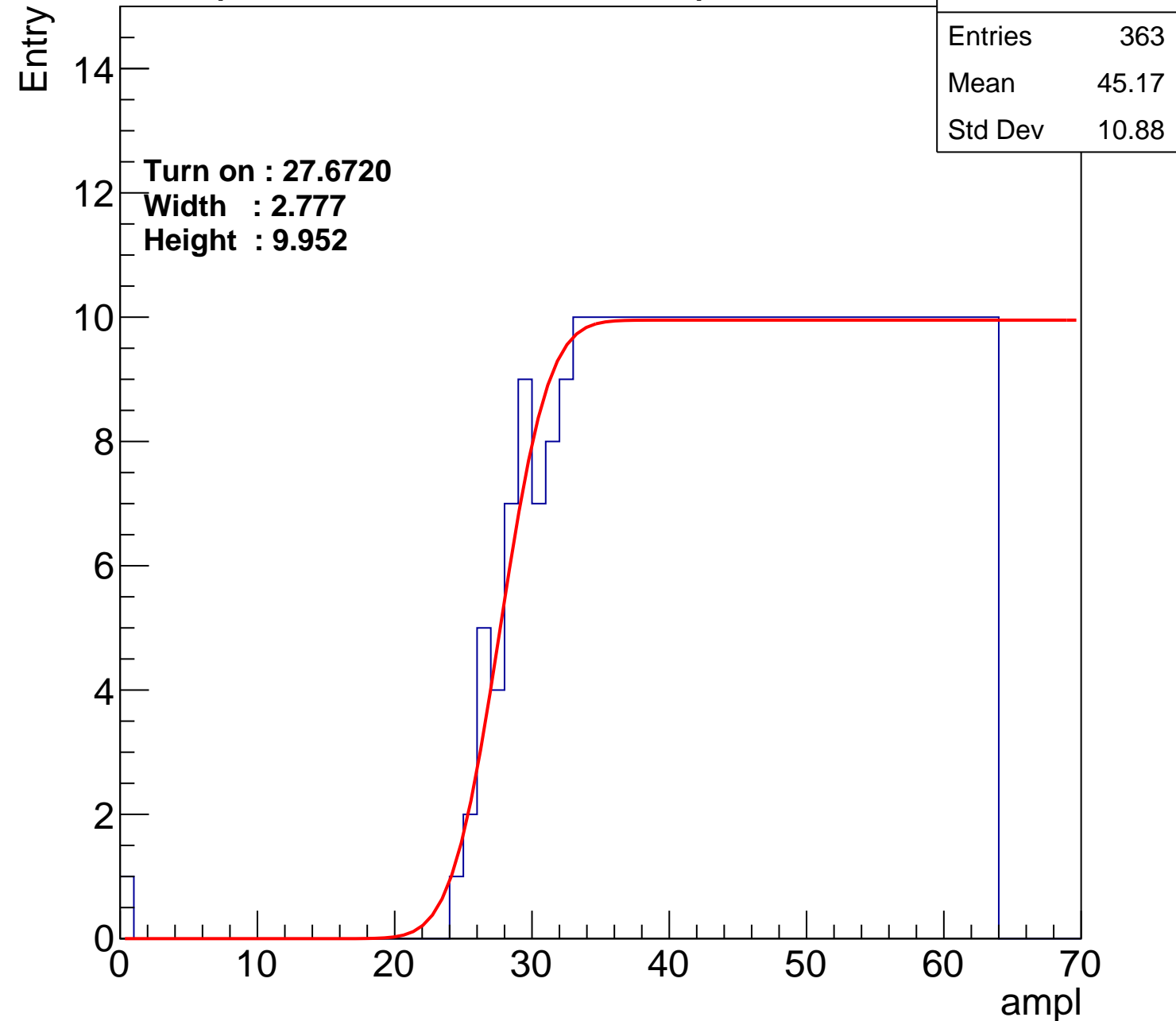
Width : 2.777

Height : 9.952

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch44

calib\_packv5\_042523\_0143.root, FC#8, port C1

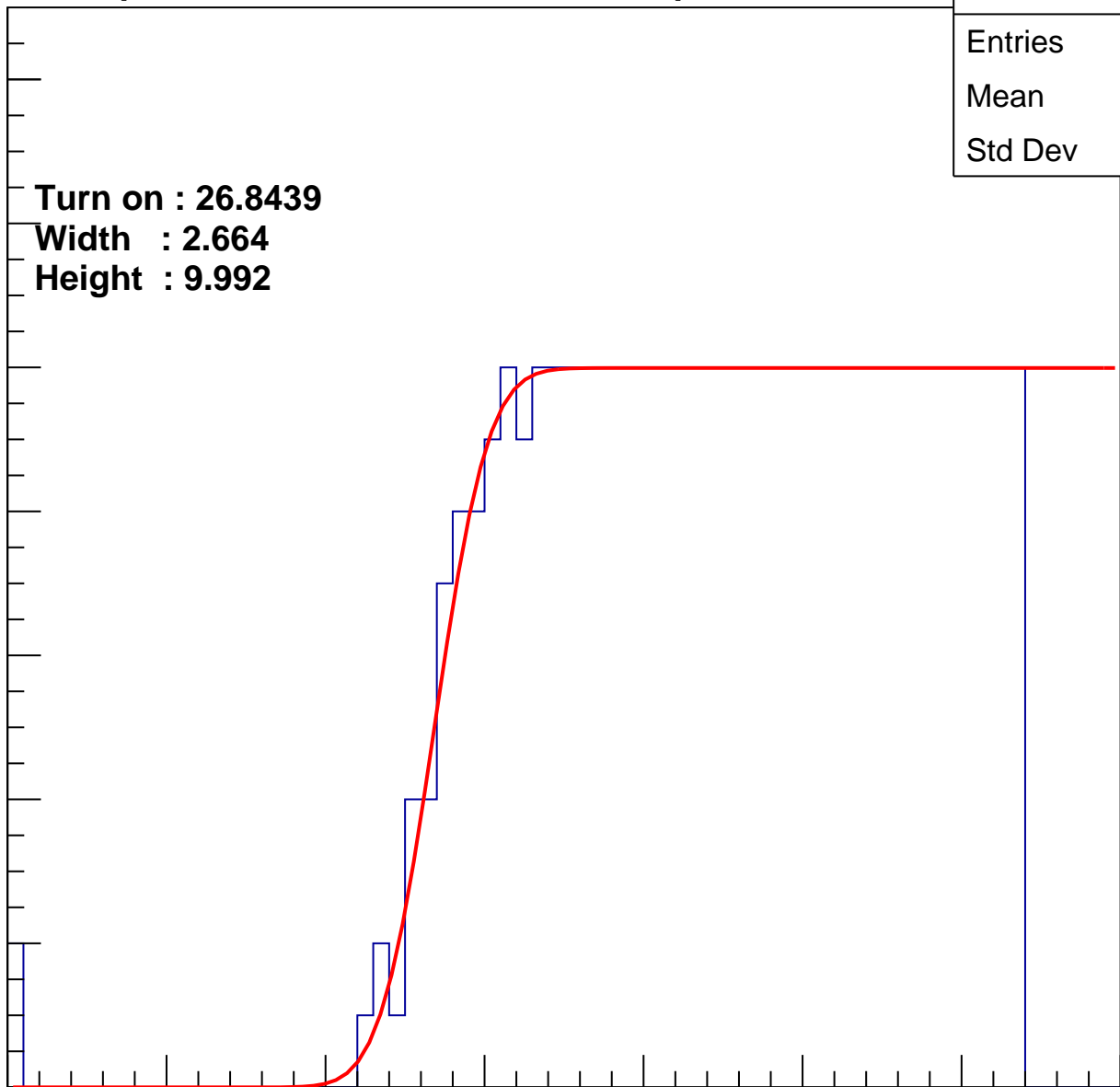
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.8439  
Width : 2.664  
Height : 9.992

Entries	375
Mean	44.5
Std Dev	11.39

ampl



# B0L002S, U25-ch45

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.44
Std Dev	10.91

**Turn on : 28.8489**

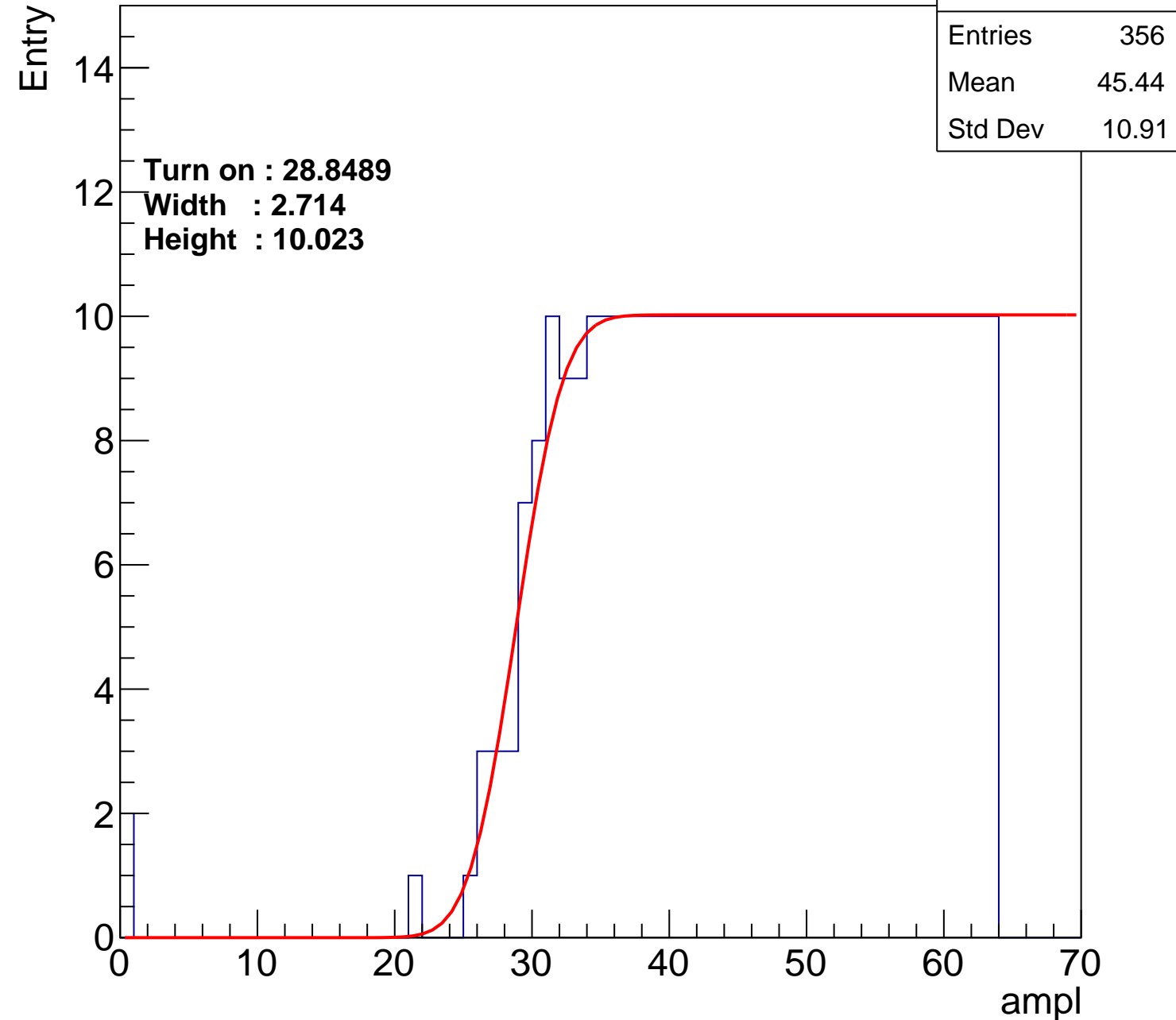
**Width : 2.714**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch46

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.29
Std Dev	10.79

**Turn on : 27.5857**

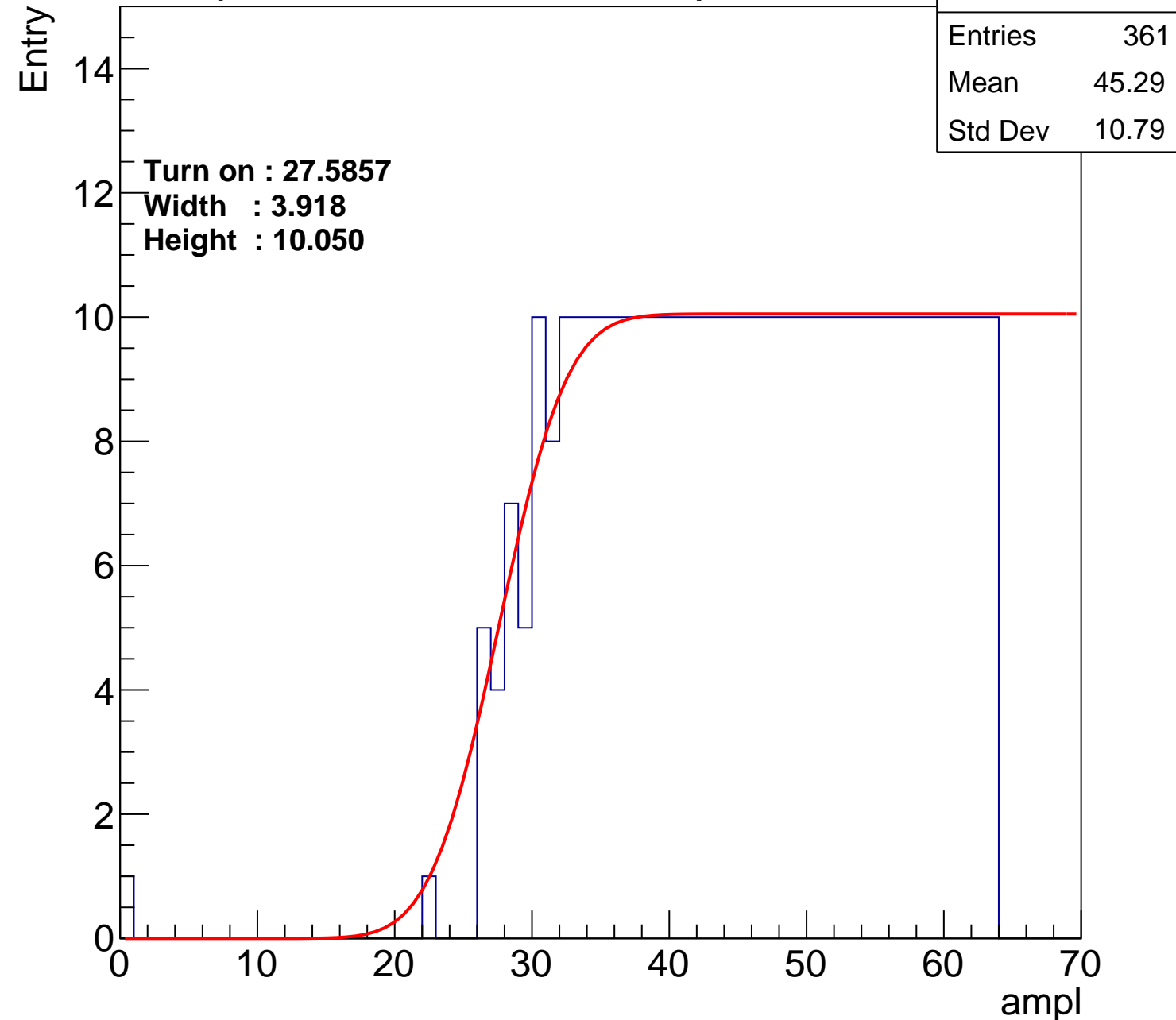
**Width : 3.918**

**Height : 10.050**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch47

calib\_packv5\_042523\_0143.root, FC#8, port C1

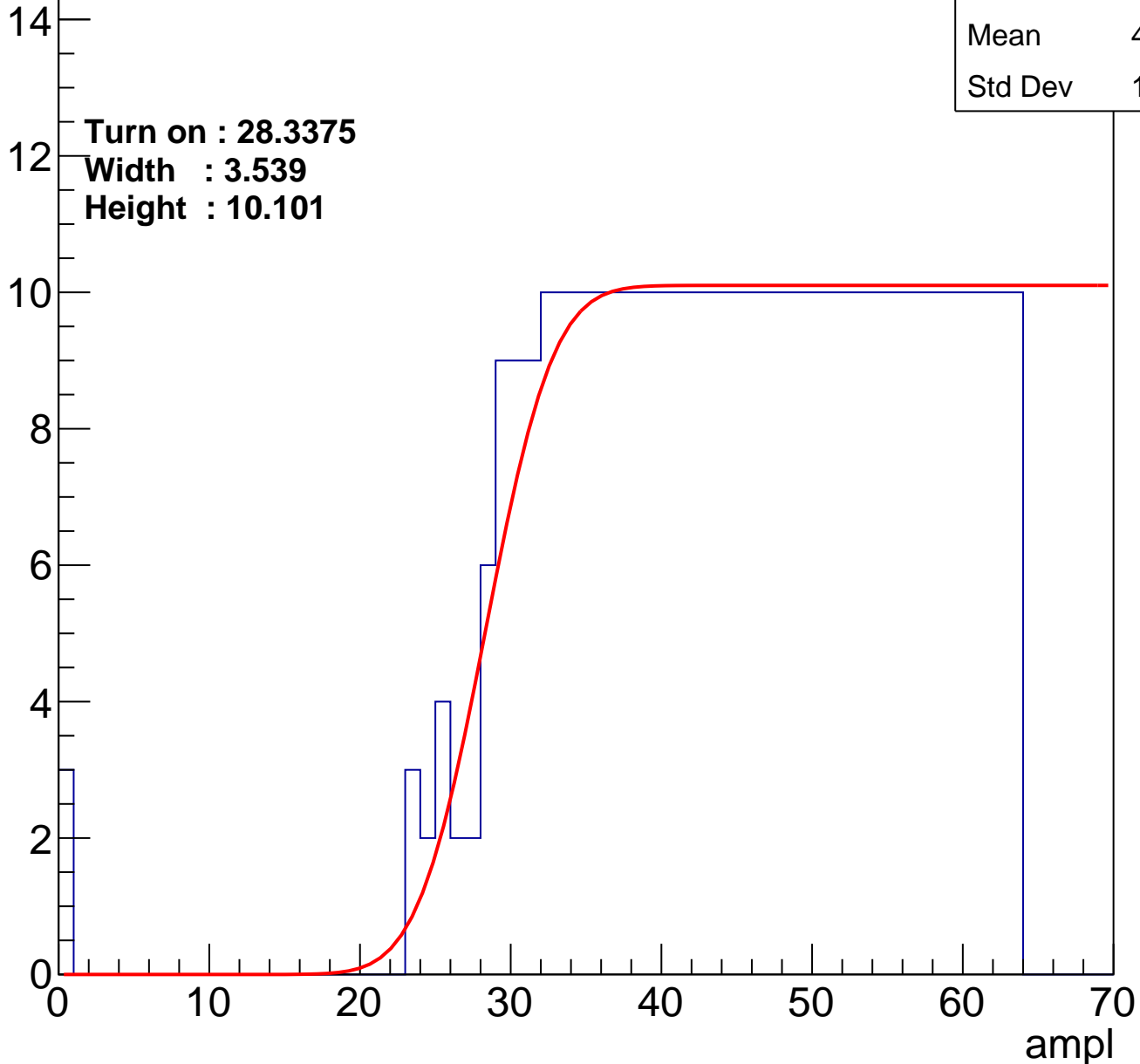
Entries	369
Mean	44.72
Std Dev	11.46

Turn on : 28.3375

Width : 3.539

Height : 10.101

Entry



# B0L002S, U25-ch48

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.49
Std Dev	10.72

Turn on : 28.4036

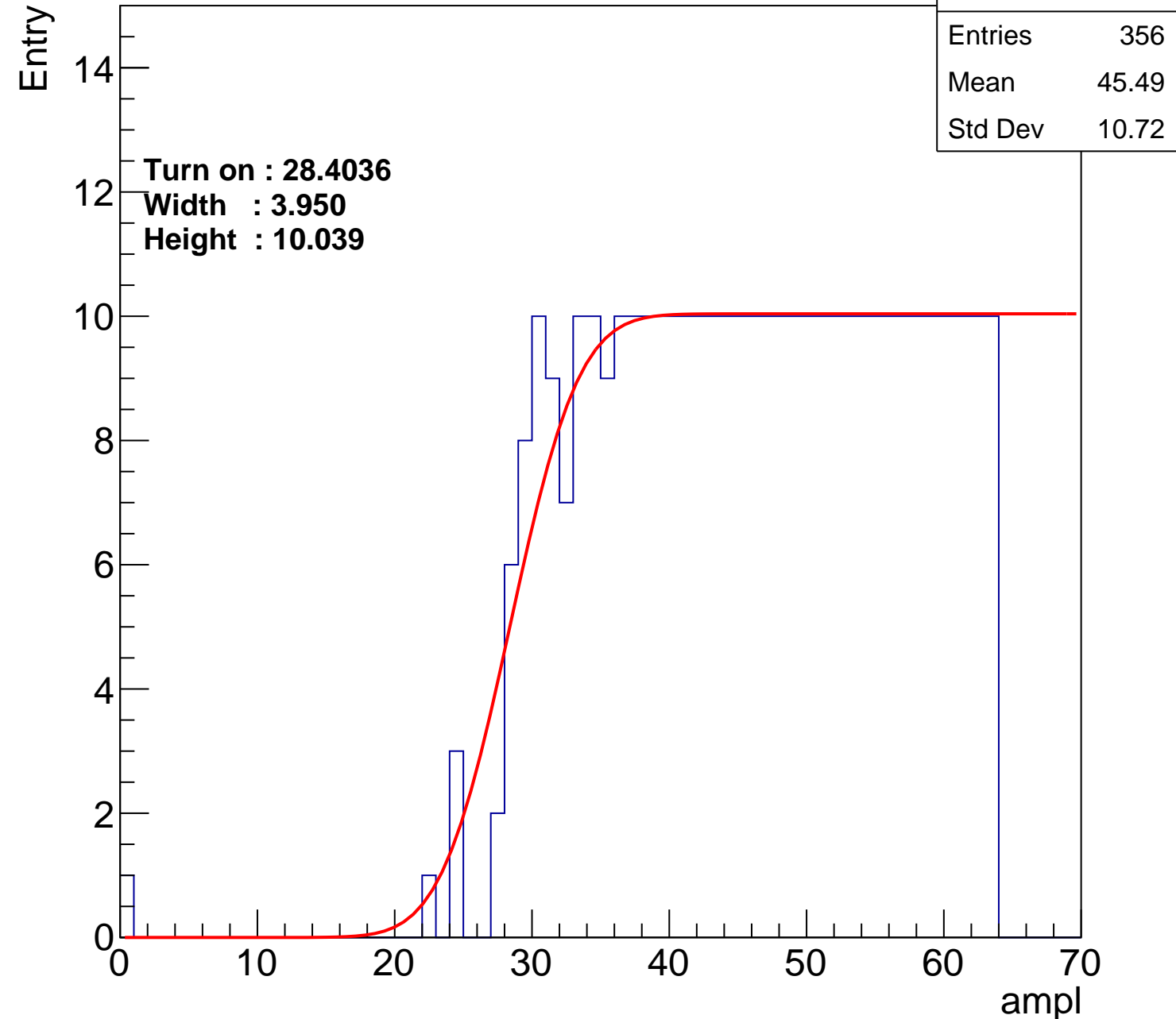
Width : 3.950

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch49

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.65
Std Dev	10.64

Turn on : 29.0339

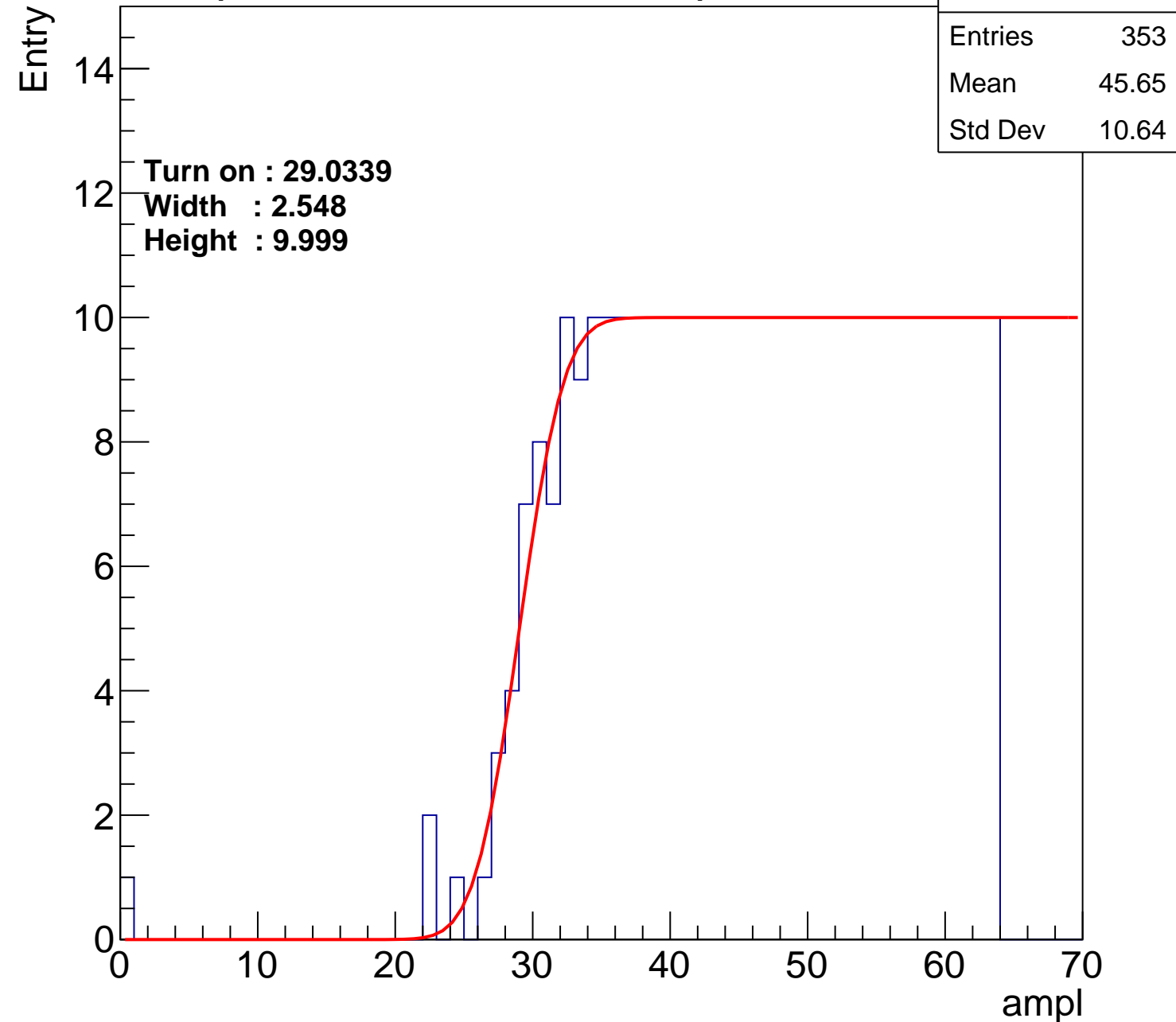
Width : 2.548

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch50

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.47
Std Dev	10.85

Turn on : 28.5824

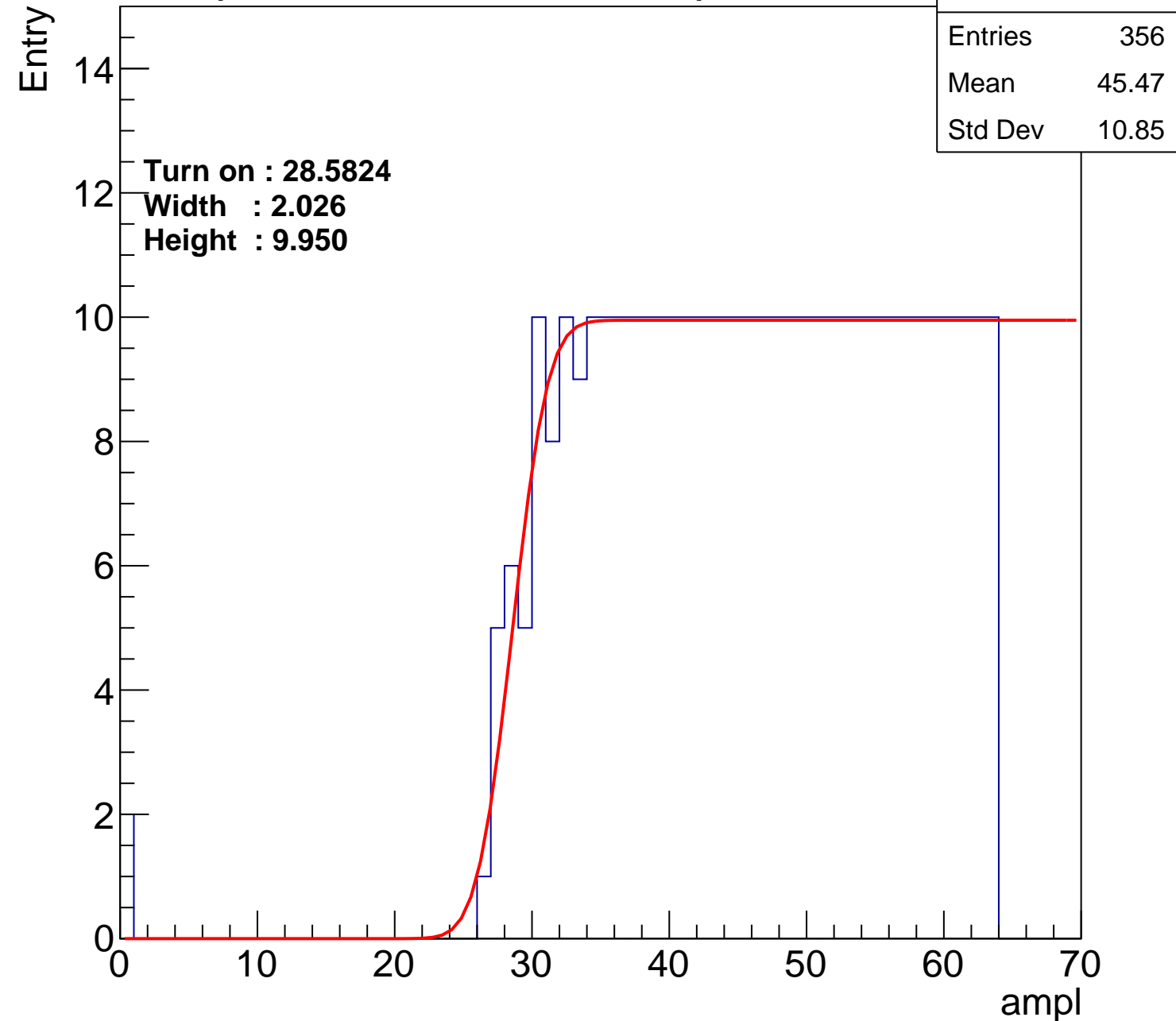
Width : 2.026

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch51

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	387
Mean	43.9
Std Dev	11.72

Turn on : 25.4860

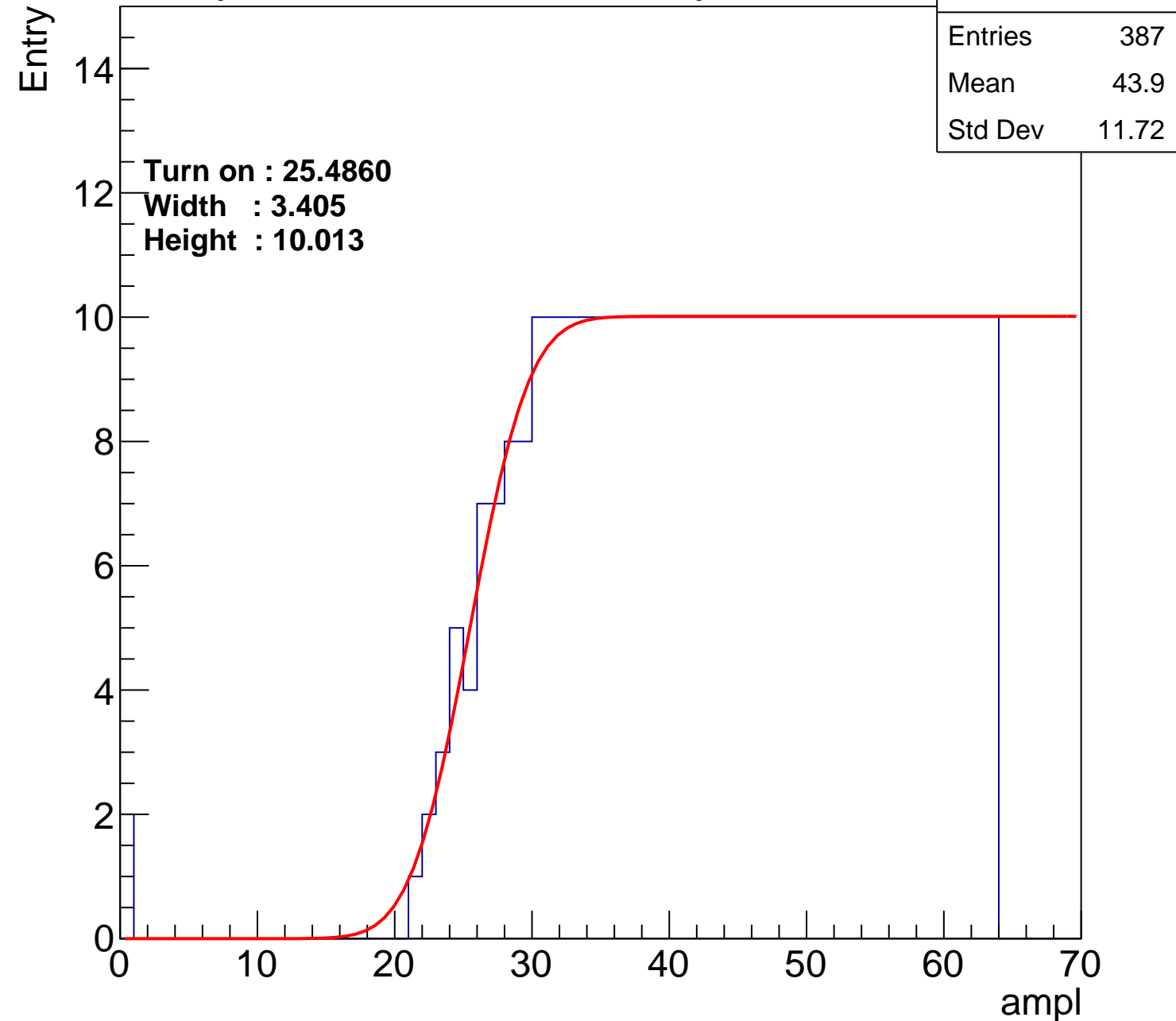
Width : 3.405

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch52

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	382
Mean	44.13
Std Dev	11.69

Turn on : 26.1299

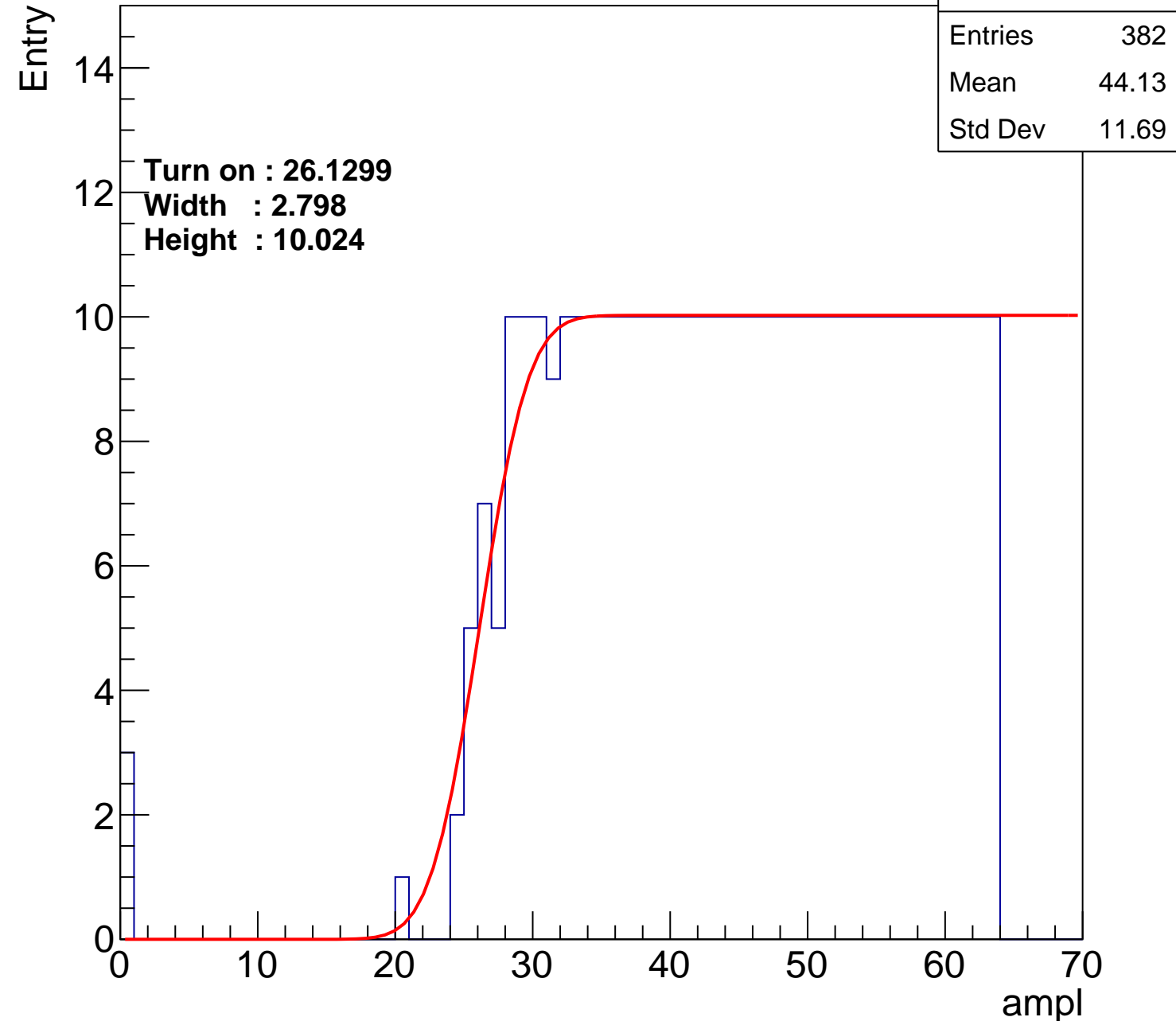
Width : 2.798

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch53

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.17
Std Dev	10.88

Turn on : 27.8759

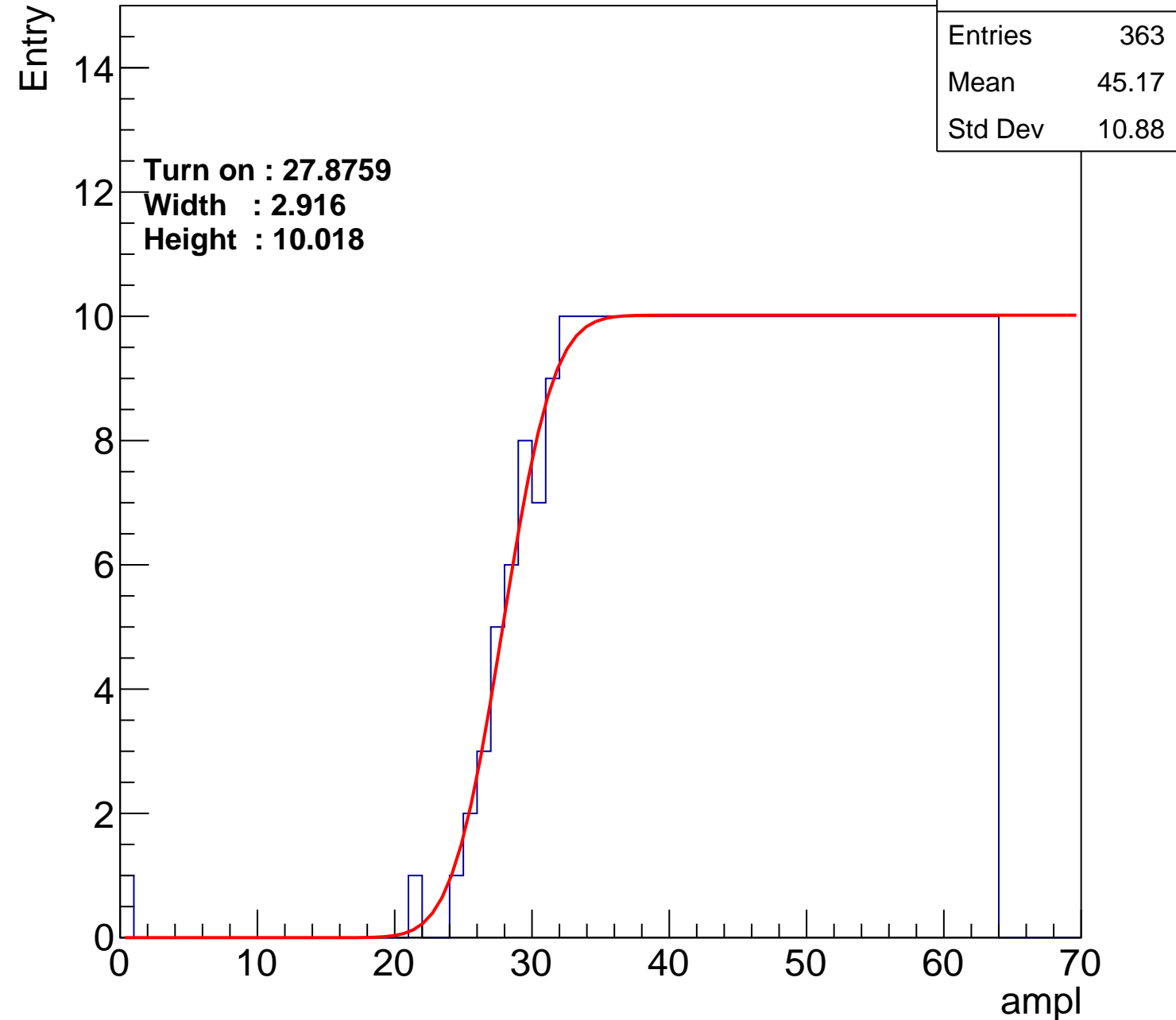
Width : 2.916

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch54

calib\_packv5\_042523\_0143.root, FC#8, port C1

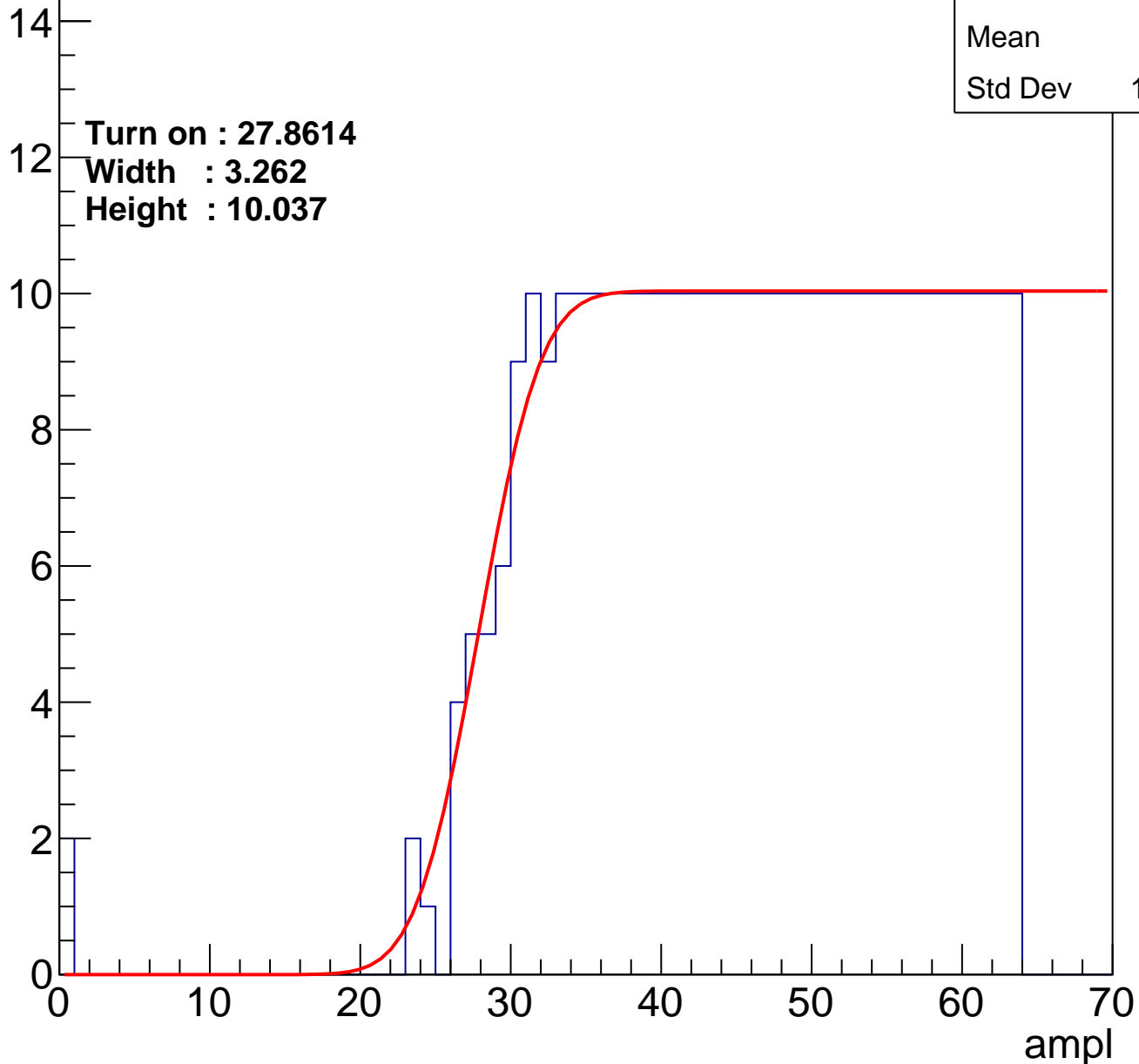
Entries	363
Mean	45.1
Std Dev	11.08

Turn on : 27.8614

Width : 3.262

Height : 10.037

Entry





# B0L002S, U25-ch55

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.3332

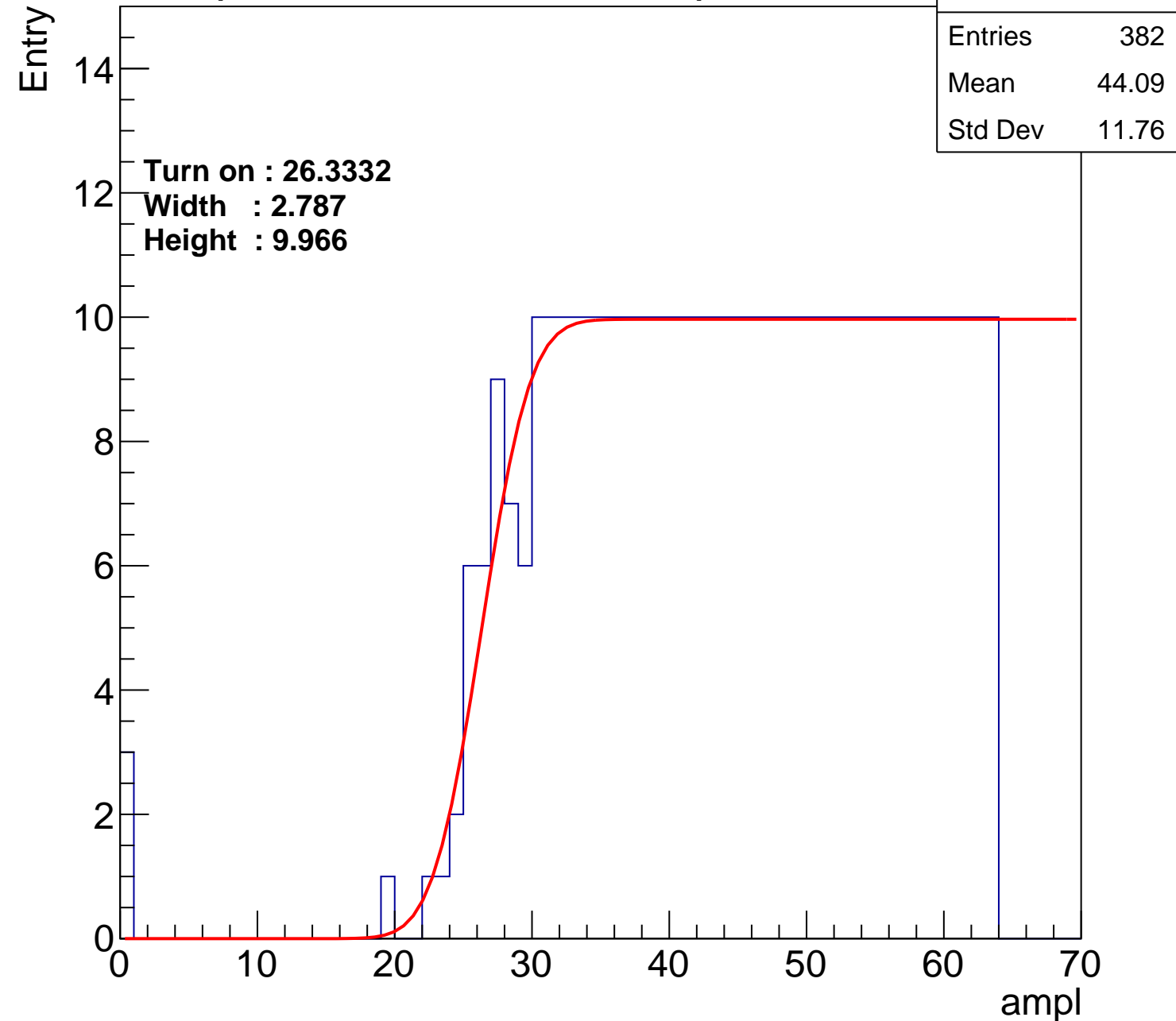
Width : 2.787

Height : 9.966

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch56

calib\_packv5\_042523\_0143.root, FC#8, port C1

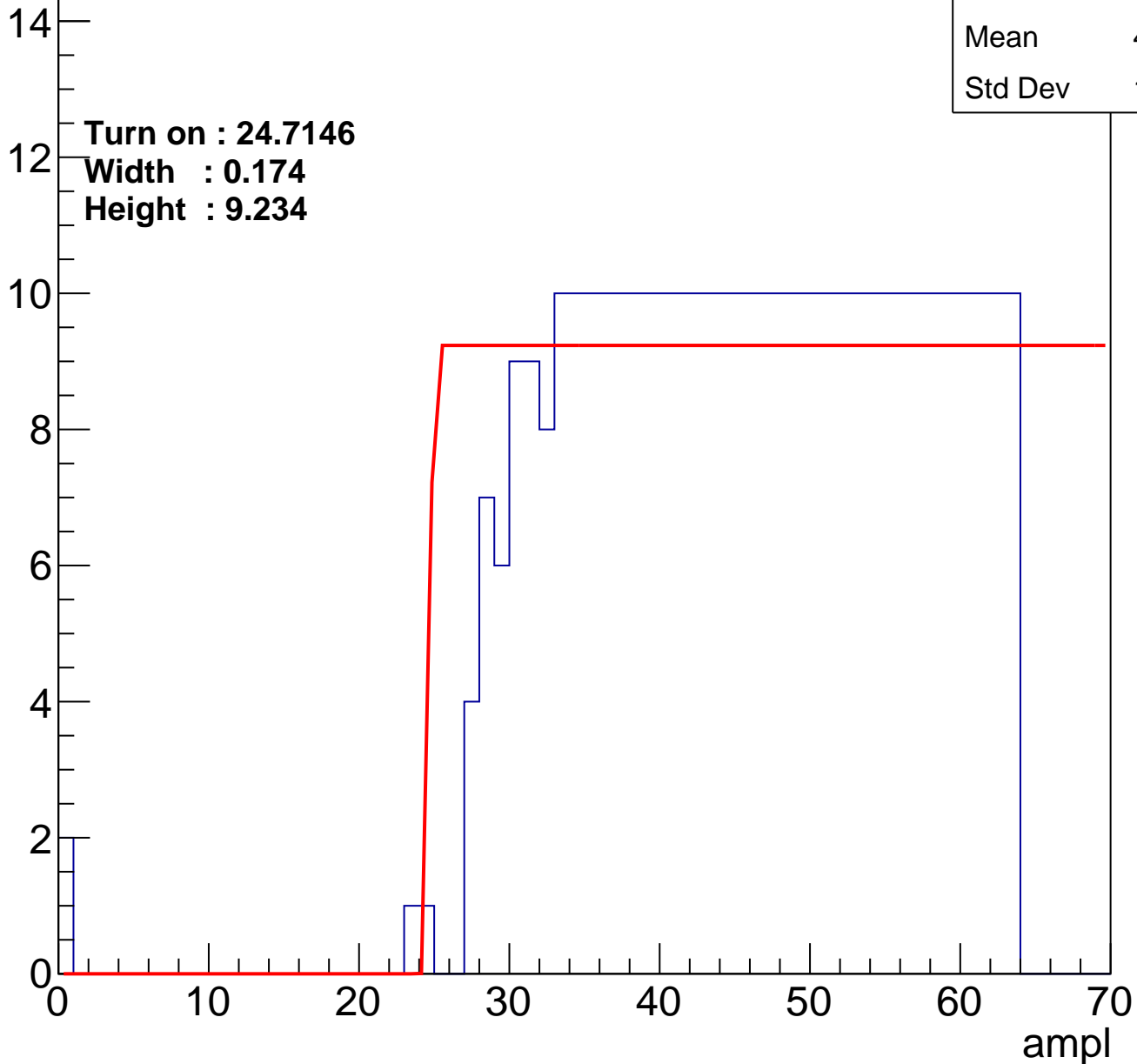
Entries	357
Mean	45.41
Std Dev	10.91

Turn on : 24.7146

Width : 0.174

Height : 9.234

Entry



# B0L002S, U25-ch57

calib\_packv5\_042523\_0143.root, FC#8, port C1

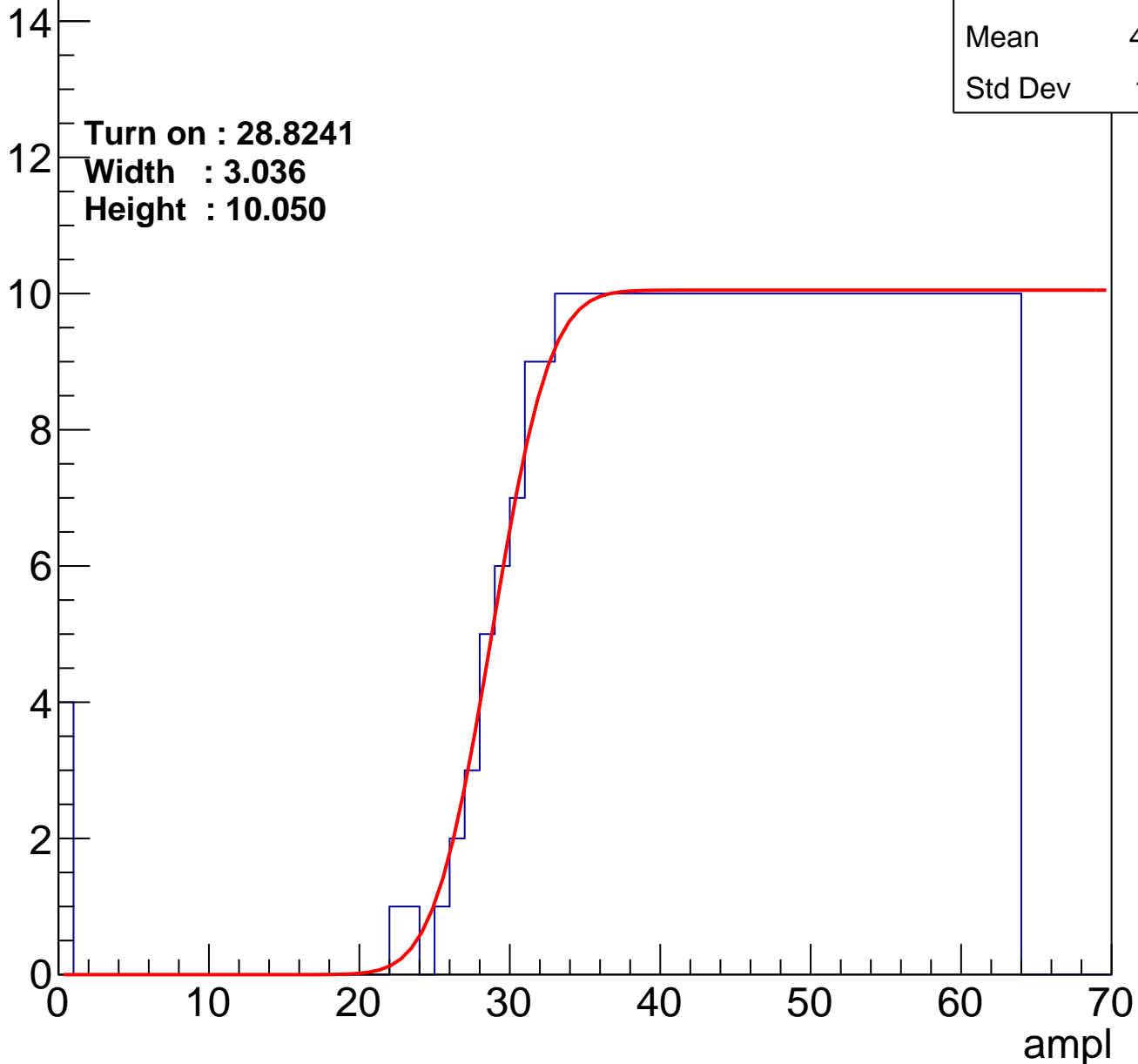
Entries	358
Mean	45.18
Std Dev	11.41

Turn on : 28.8241

Width : 3.036

Height : 10.050

Entry



# B0L002S, U25-ch58

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.17
Std Dev	10.88

Turn on : 28.1433

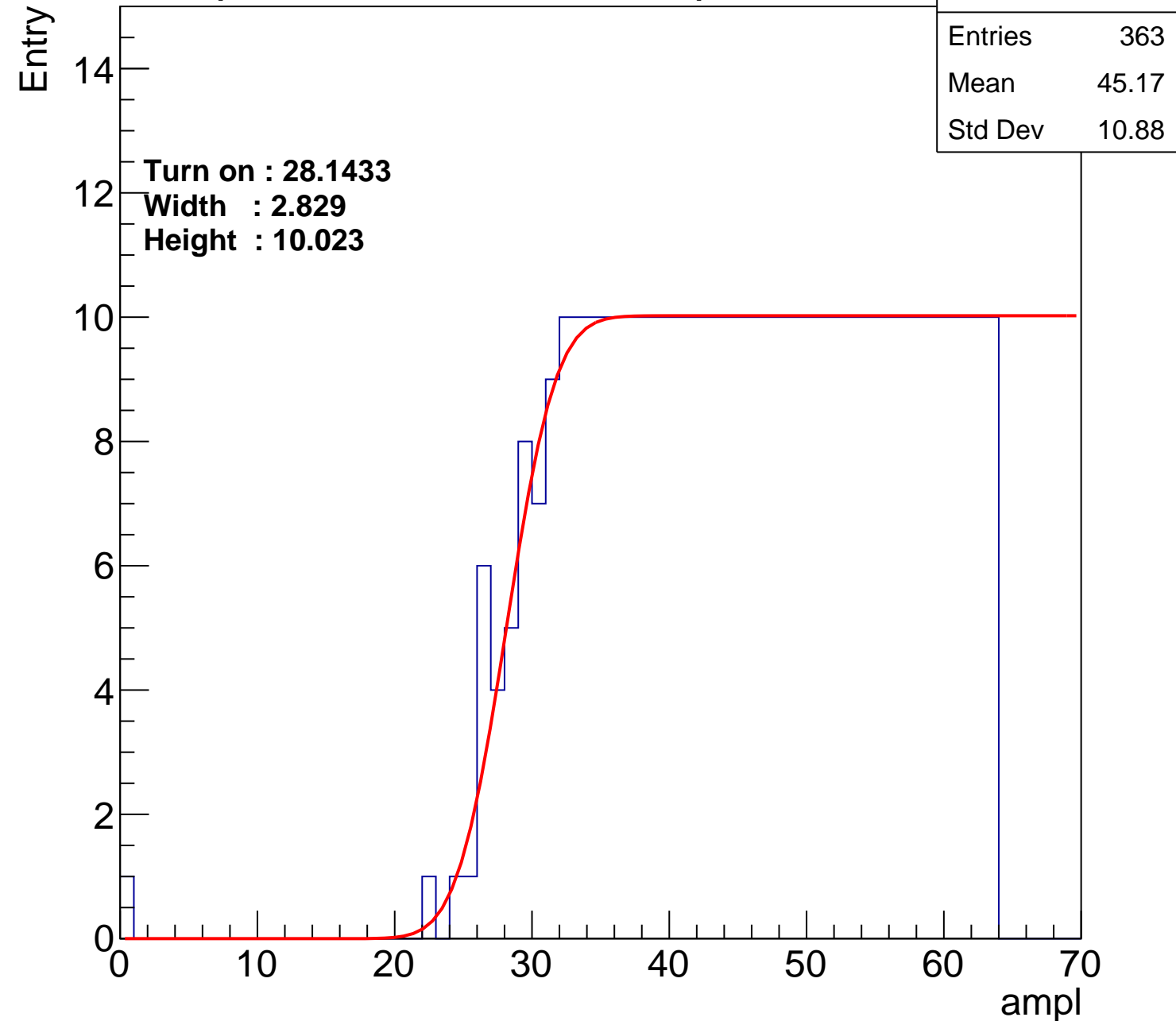
Width : 2.829

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch59

calib\_packv5\_042523\_0143.root, FC#8, port C1

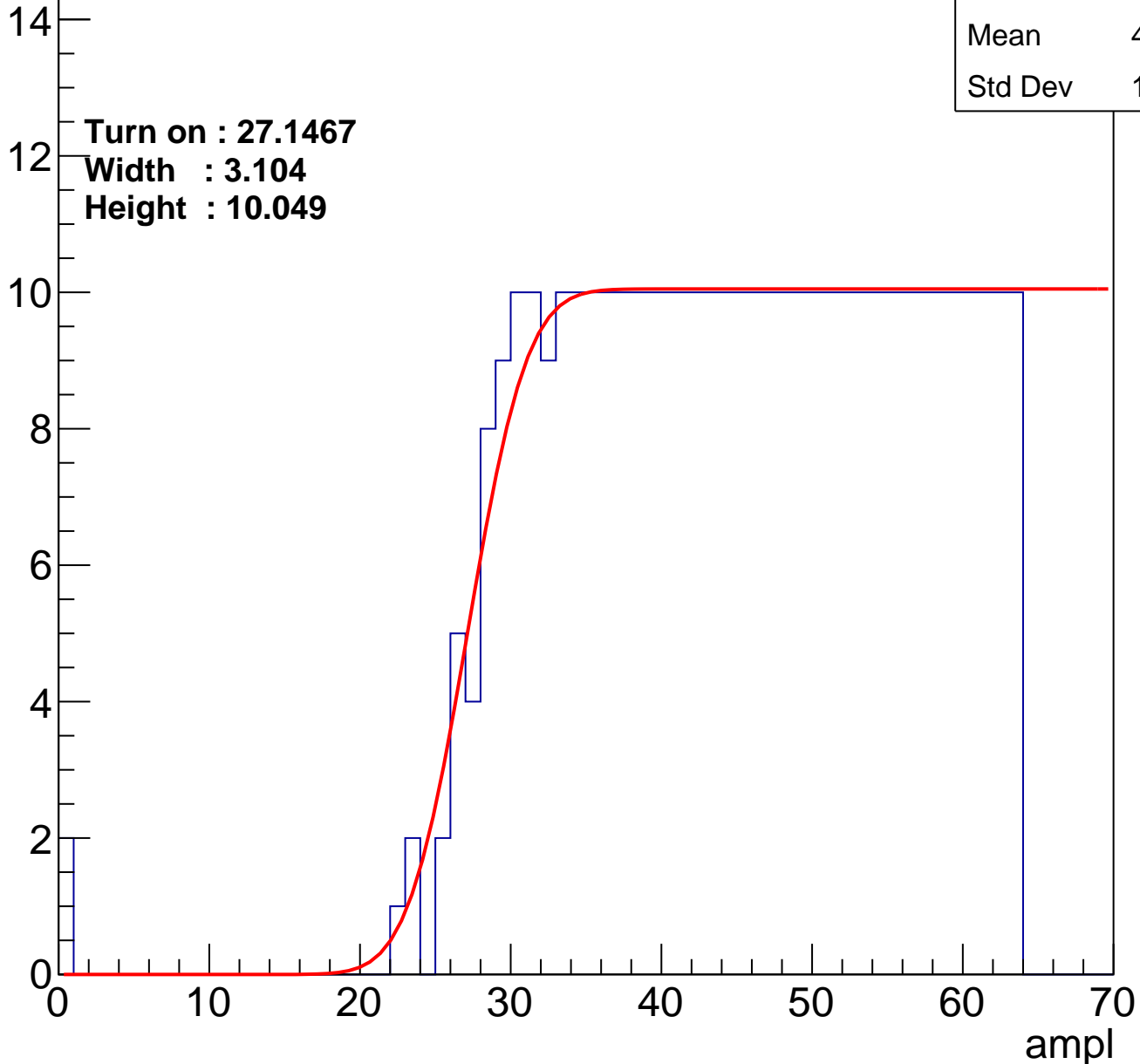
Entries	372
Mean	44.67
Std Dev	11.28

Turn on : 27.1467

Width : 3.104

Height : 10.049

Entry



# B0L002S, U25-ch60

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	345
Mean	45.96
Std Dev	10.67

**Turn on : 29.4437**

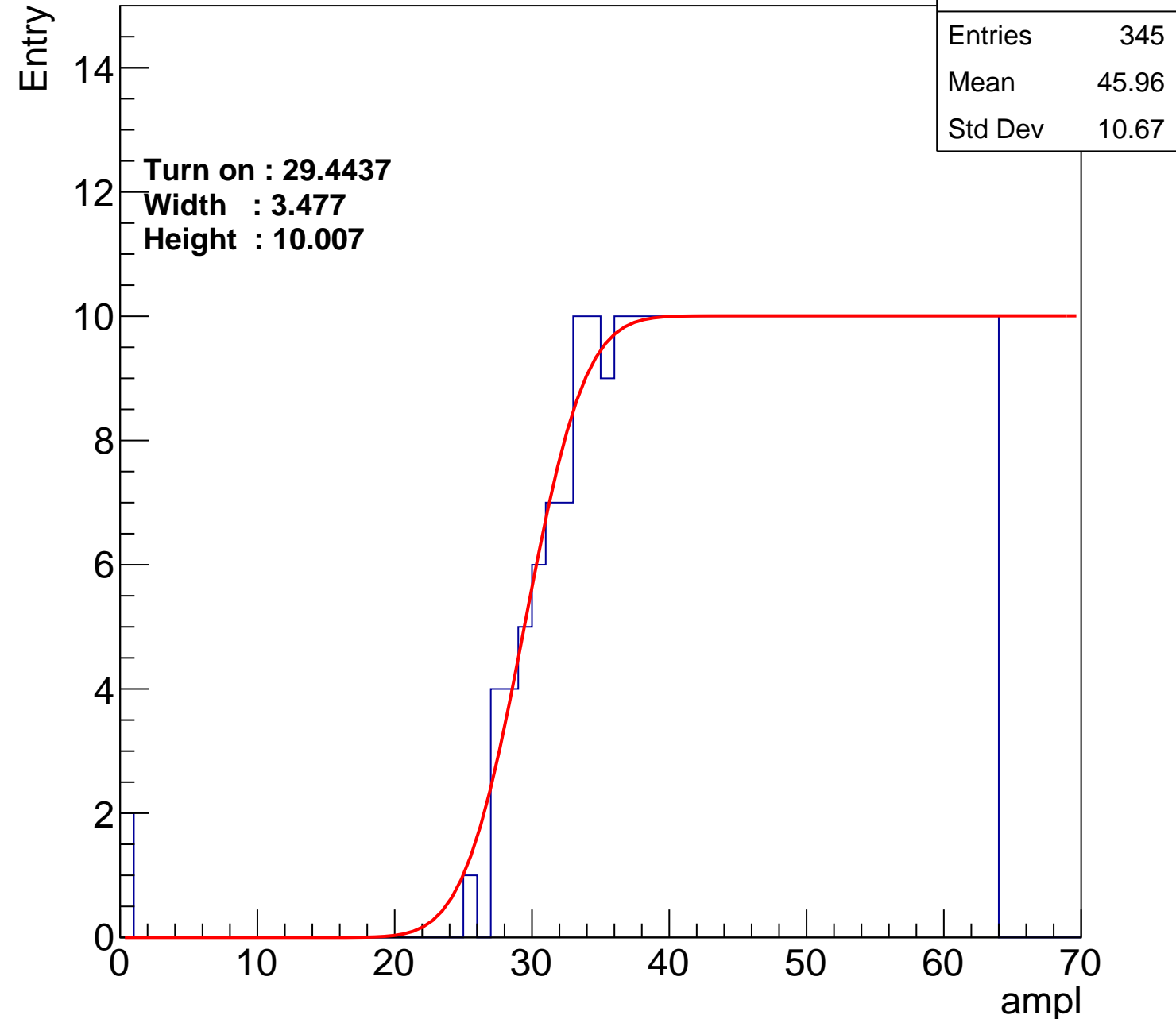
**Width : 3.477**

**Height : 10.007**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch61

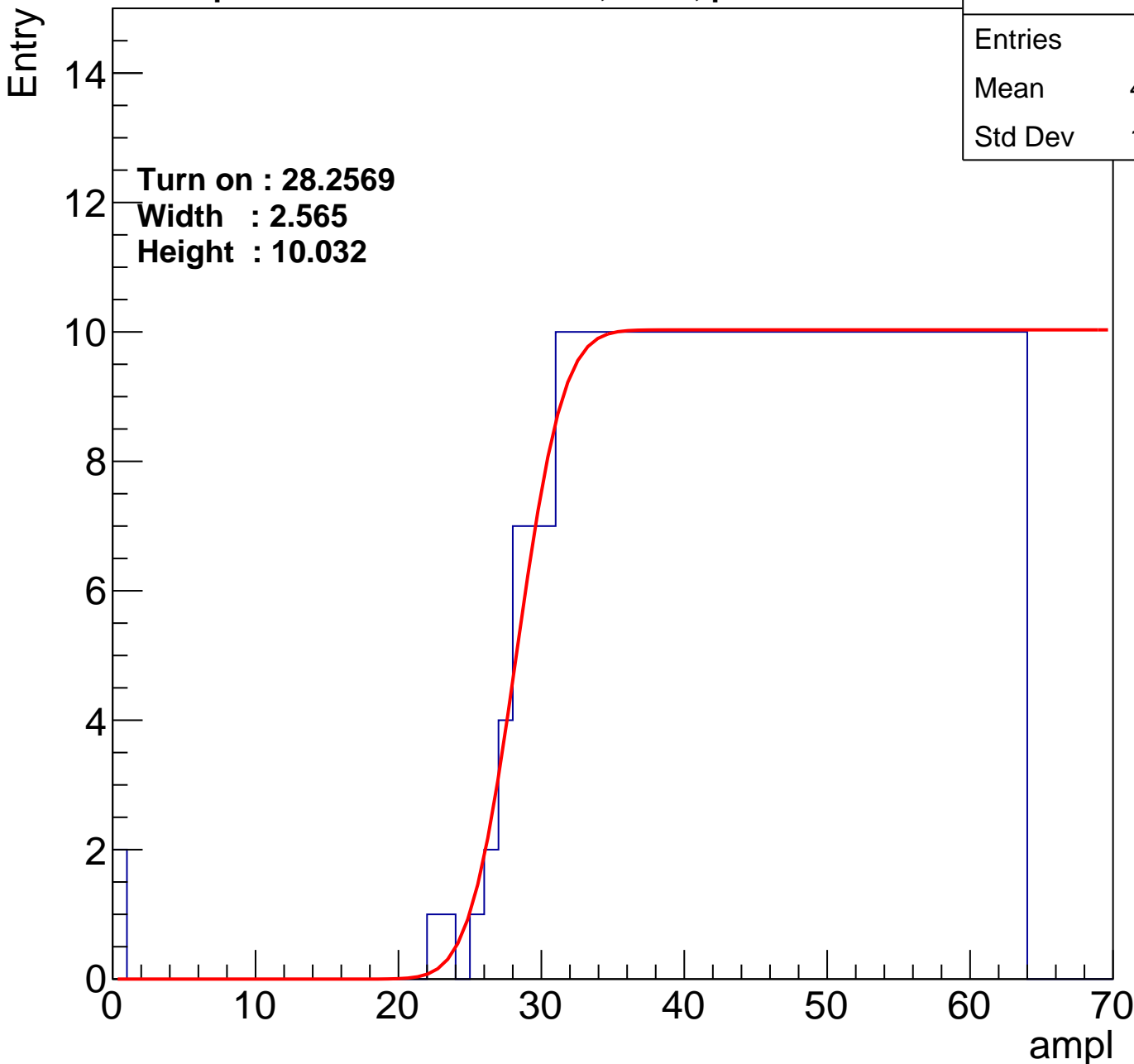
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	362
Mean	45.16
Std Dev	11.04

**Turn on : 28.2569**

**Width : 2.565**

**Height : 10.032**



# B0L002S, U25-ch62

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	386
Mean	43.89
Std Dev	11.86

Turn on : 25.8675

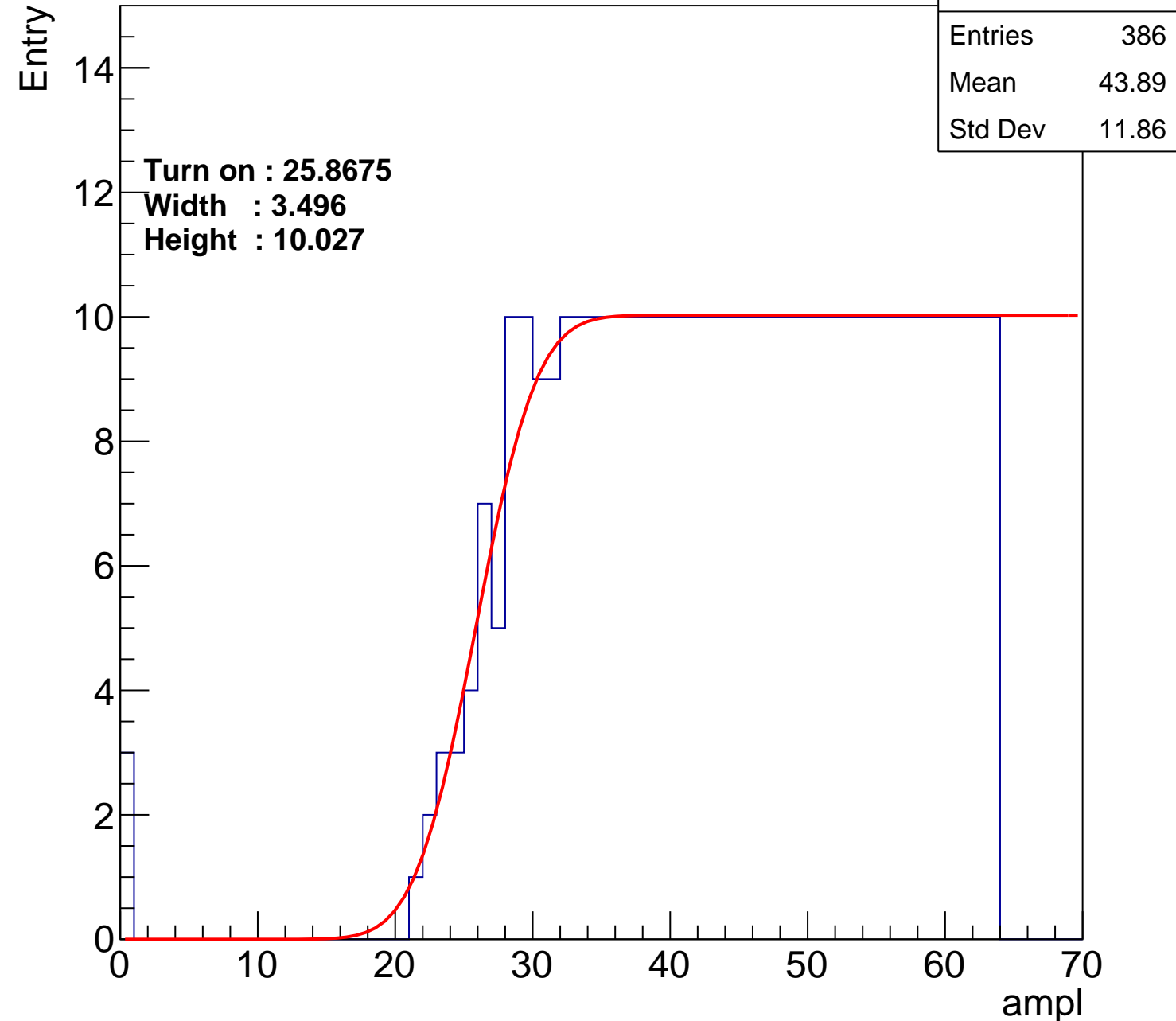
Width : 3.496

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch63

calib\_packv5\_042523\_0143.root, FC#8, port C1

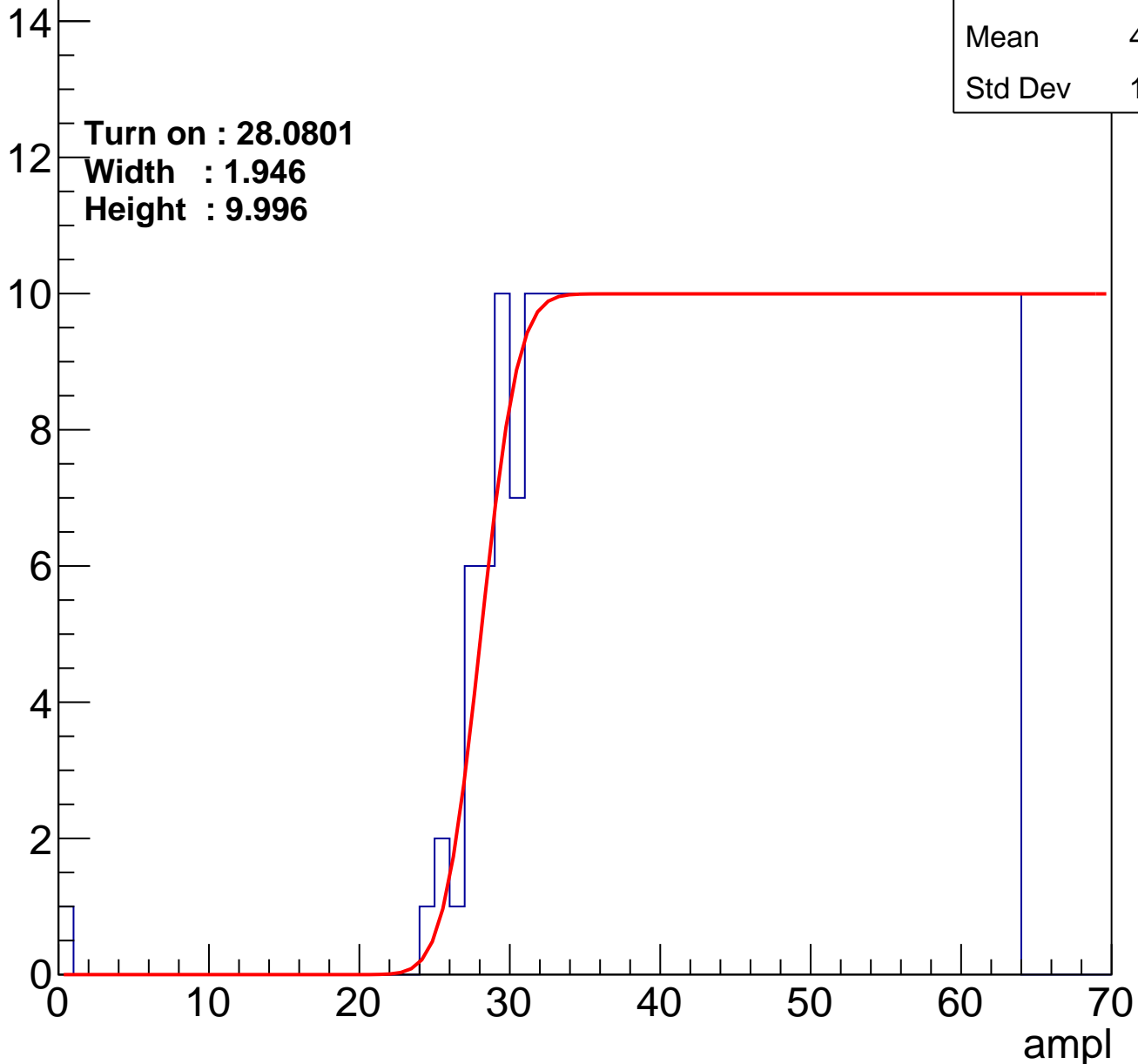
Entries	364
Mean	45.16
Std Dev	10.83

Turn on : 28.0801

Width : 1.946

Height : 9.996

Entry



# B0L002S, U25-ch64

calib\_packv5\_042523\_0143.root, FC#8, port C1

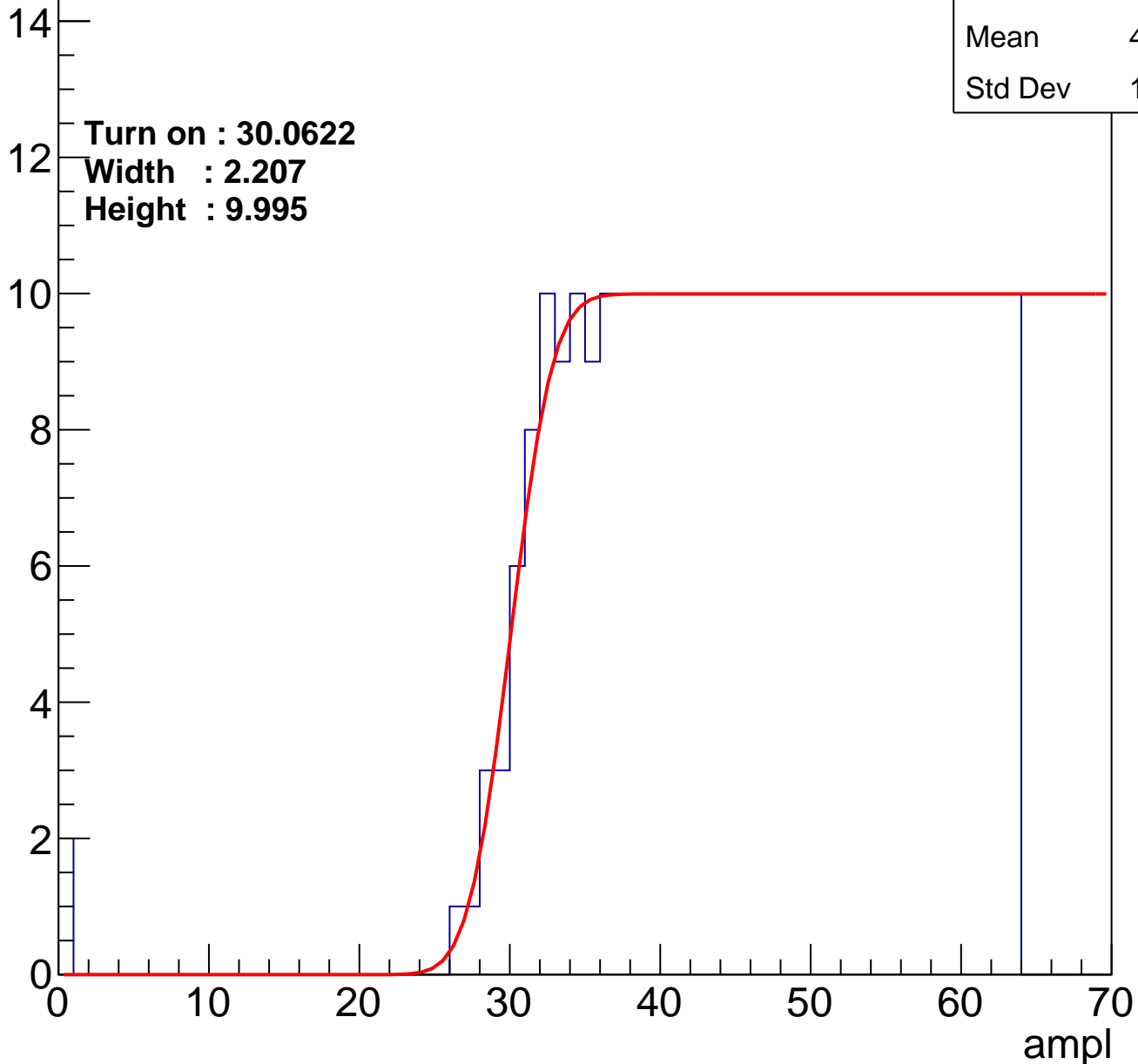
Entries	342
Mean	46.15
Std Dev	10.52

Turn on : 30.0622

Width : 2.207

Height : 9.995

Entry

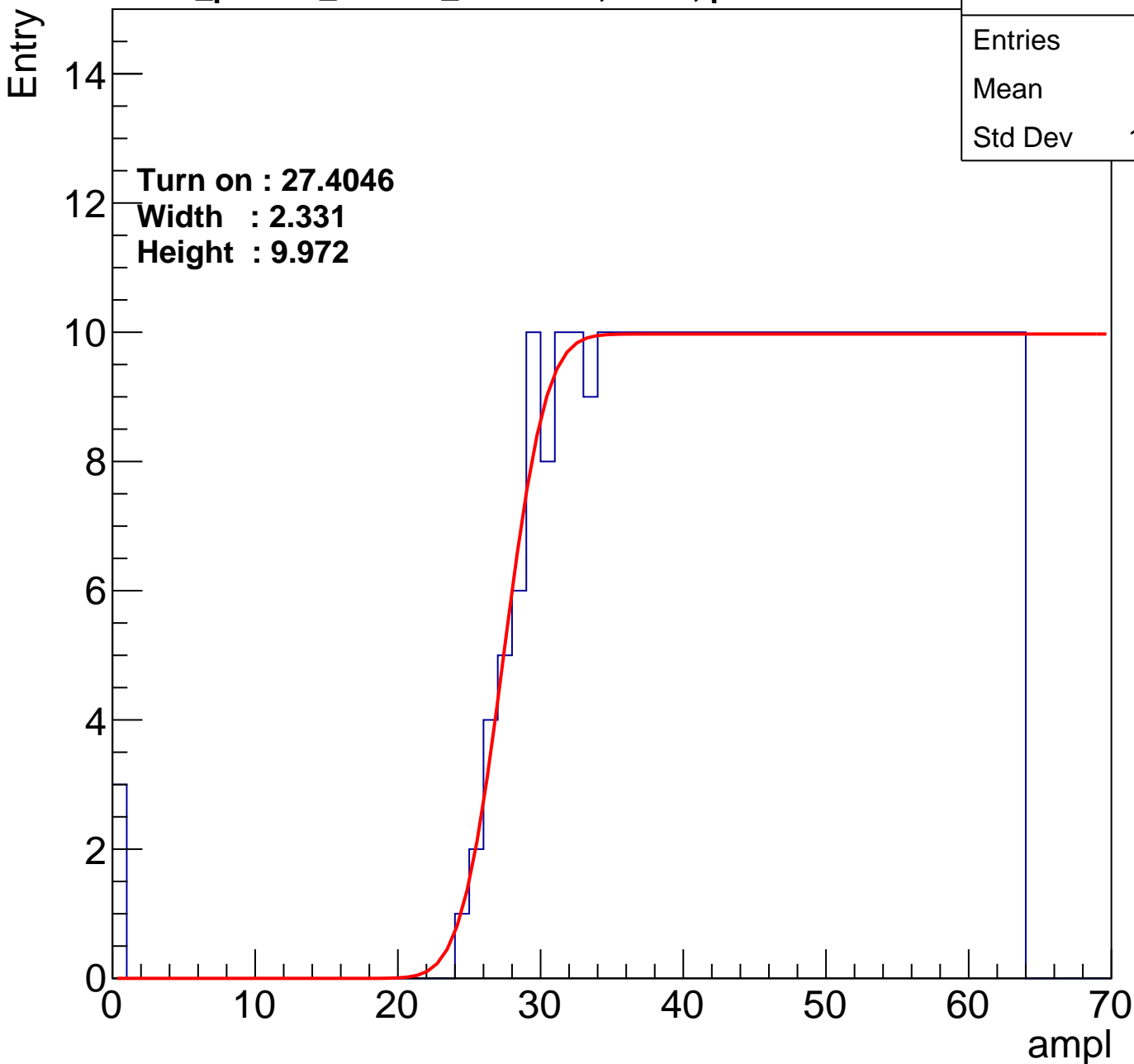


**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	368
Mean	44.8
Std Dev	11.37

**Height : 9.972**



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	338
Mean	46.43
Std Dev	10.18

Mean	46.43
------	-------

Std Dev	10.18
---------	-------

**Width : 2.332**

**Height : 10.003**

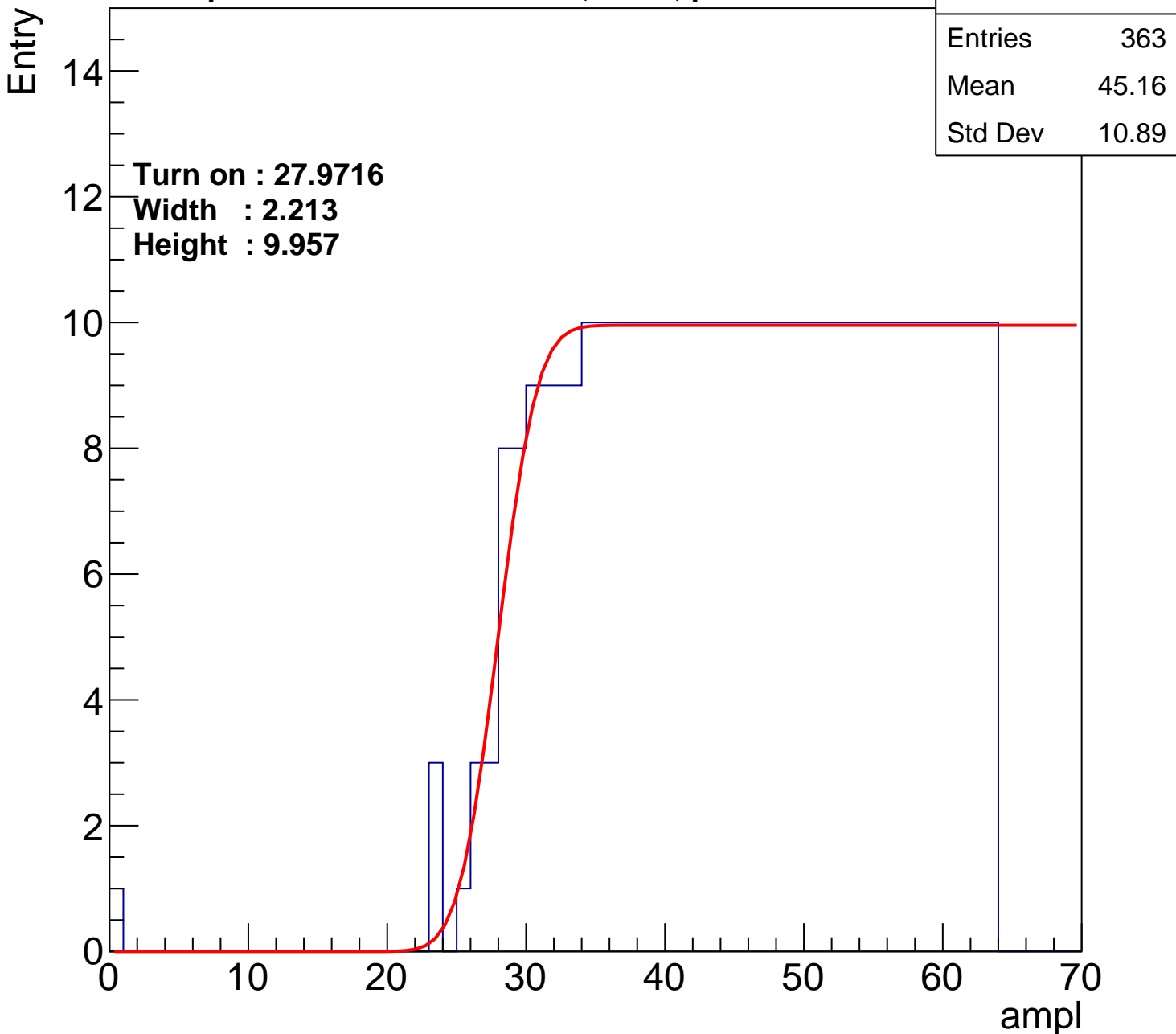


**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	363
Mean	45.16
Std Dev	10.89

**Height : 9.957**



# B0L002S, U25-ch68

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	350
Mean	45.74
Std Dev	10.76

**Turn on : 29.5818**

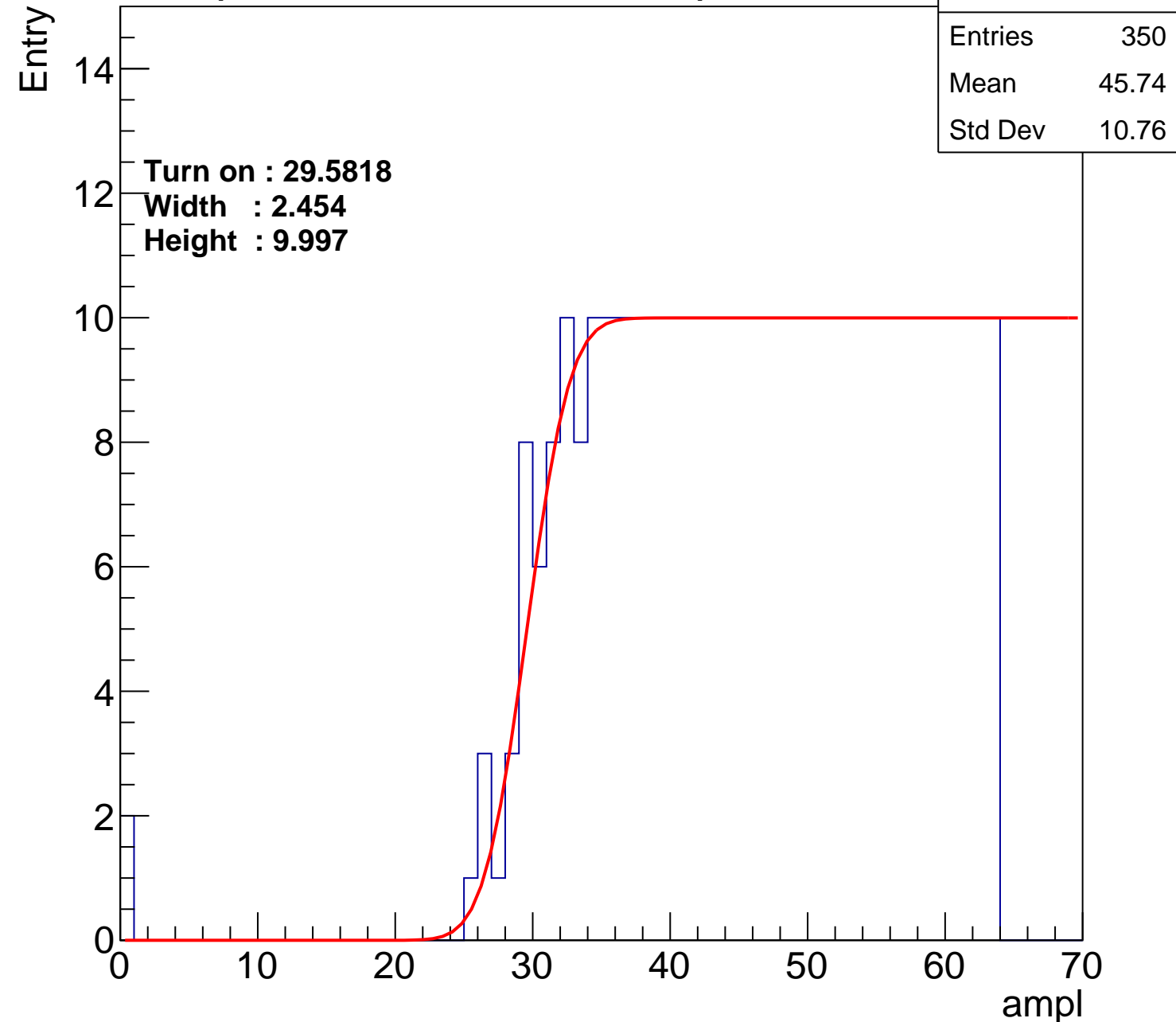
**Width : 2.454**

**Height : 9.997**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch69

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	350
Mean	45.71
Std Dev	10.81

Turn on : 29.8129

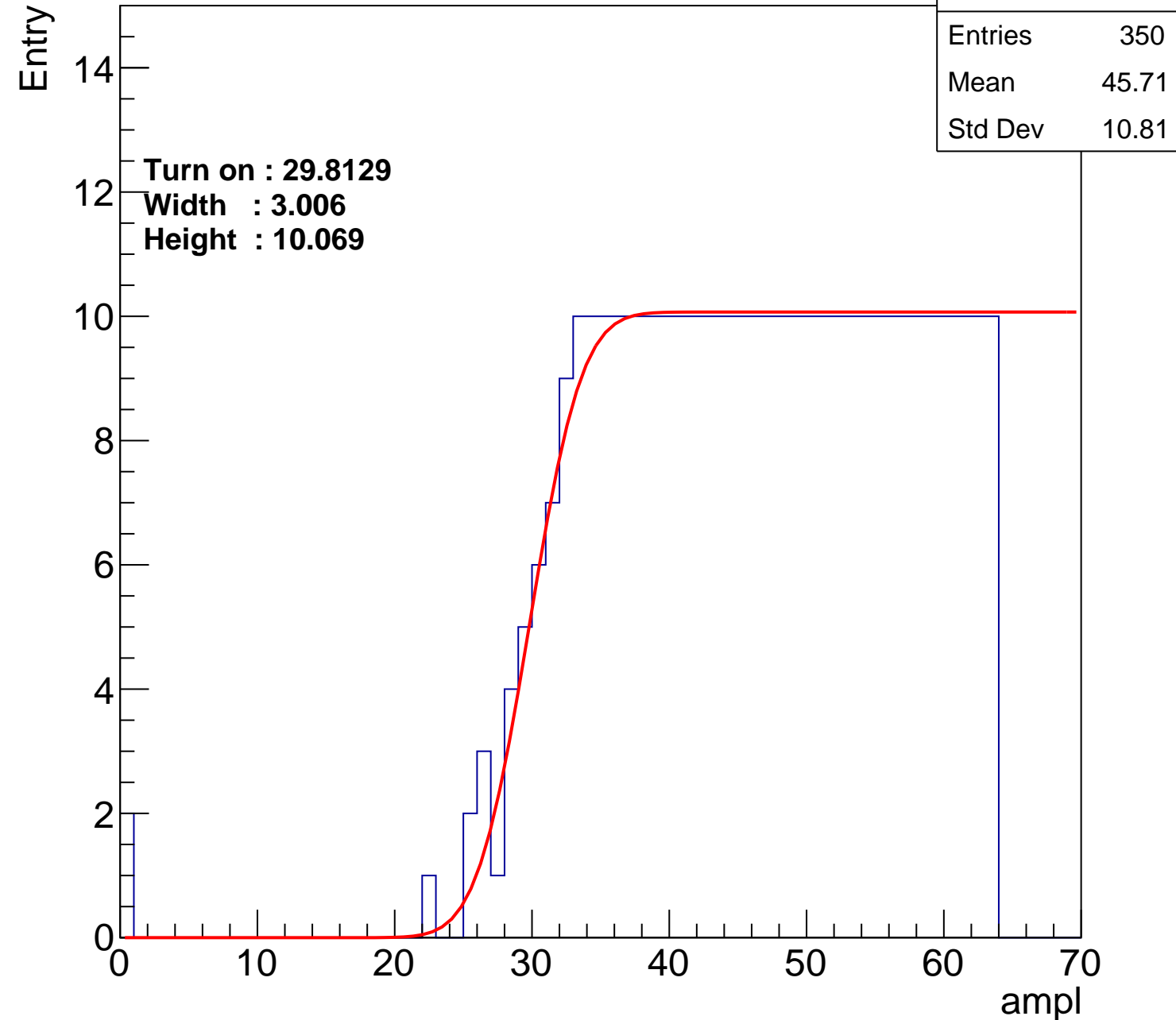
Width : 3.006

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch70

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	392
Mean	43.62
Std Dev	11.96

Turn on : 25.2096

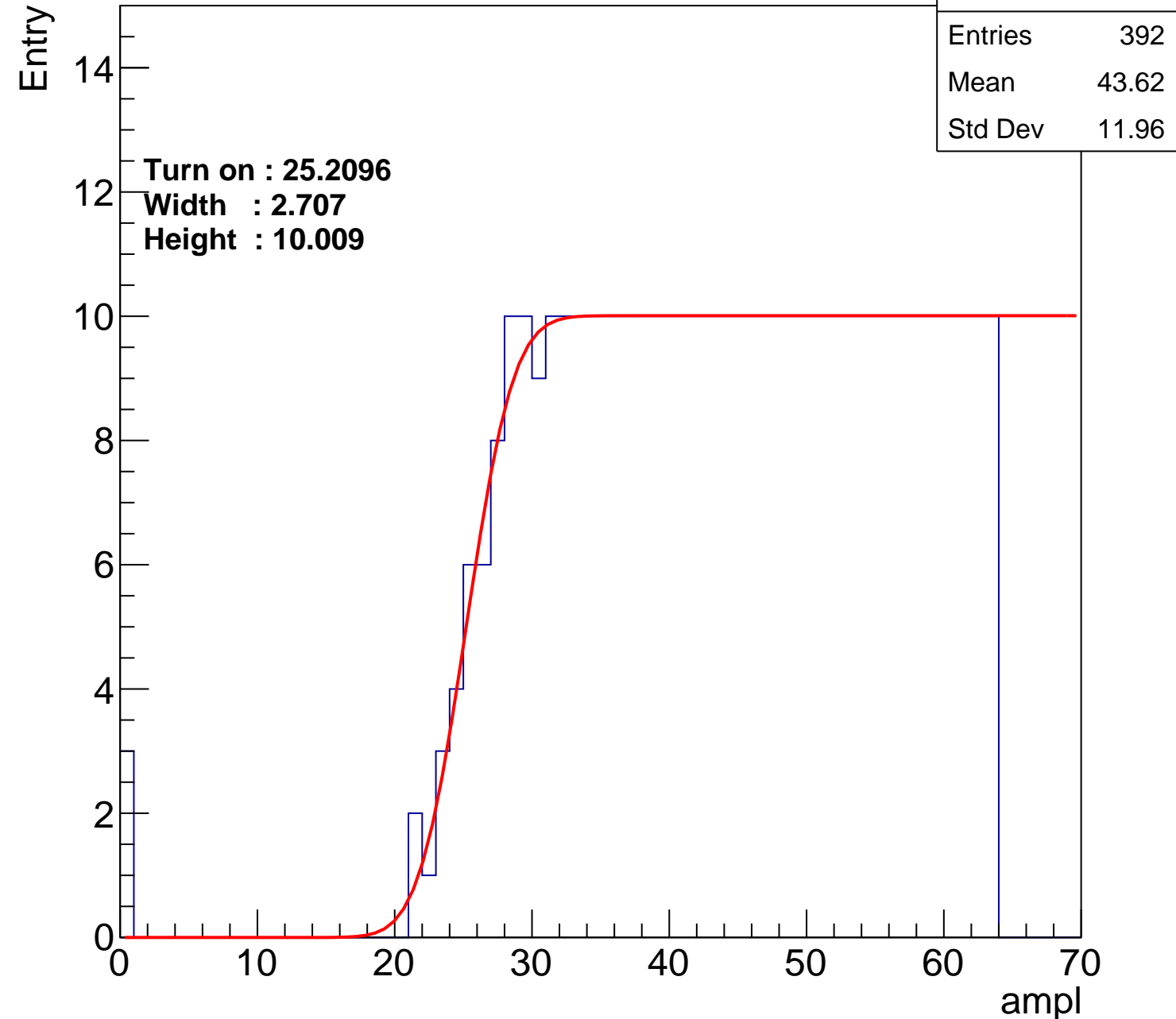
Width : 2.707

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch71

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.24
Std Dev	11.01

Turn on : 28.2736

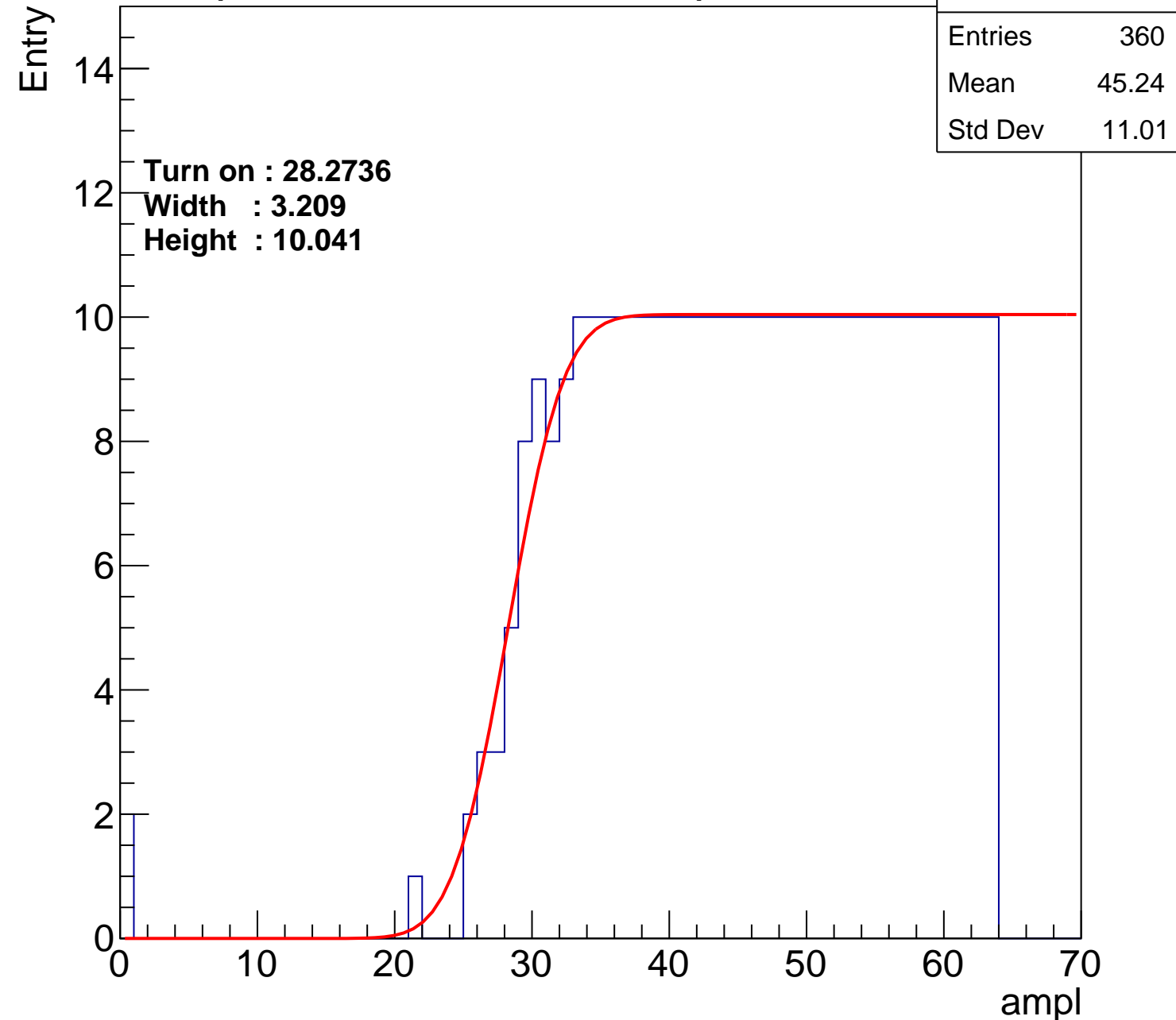
Width : 3.209

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch72

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.32
Std Dev	11

Turn on : 28.4437

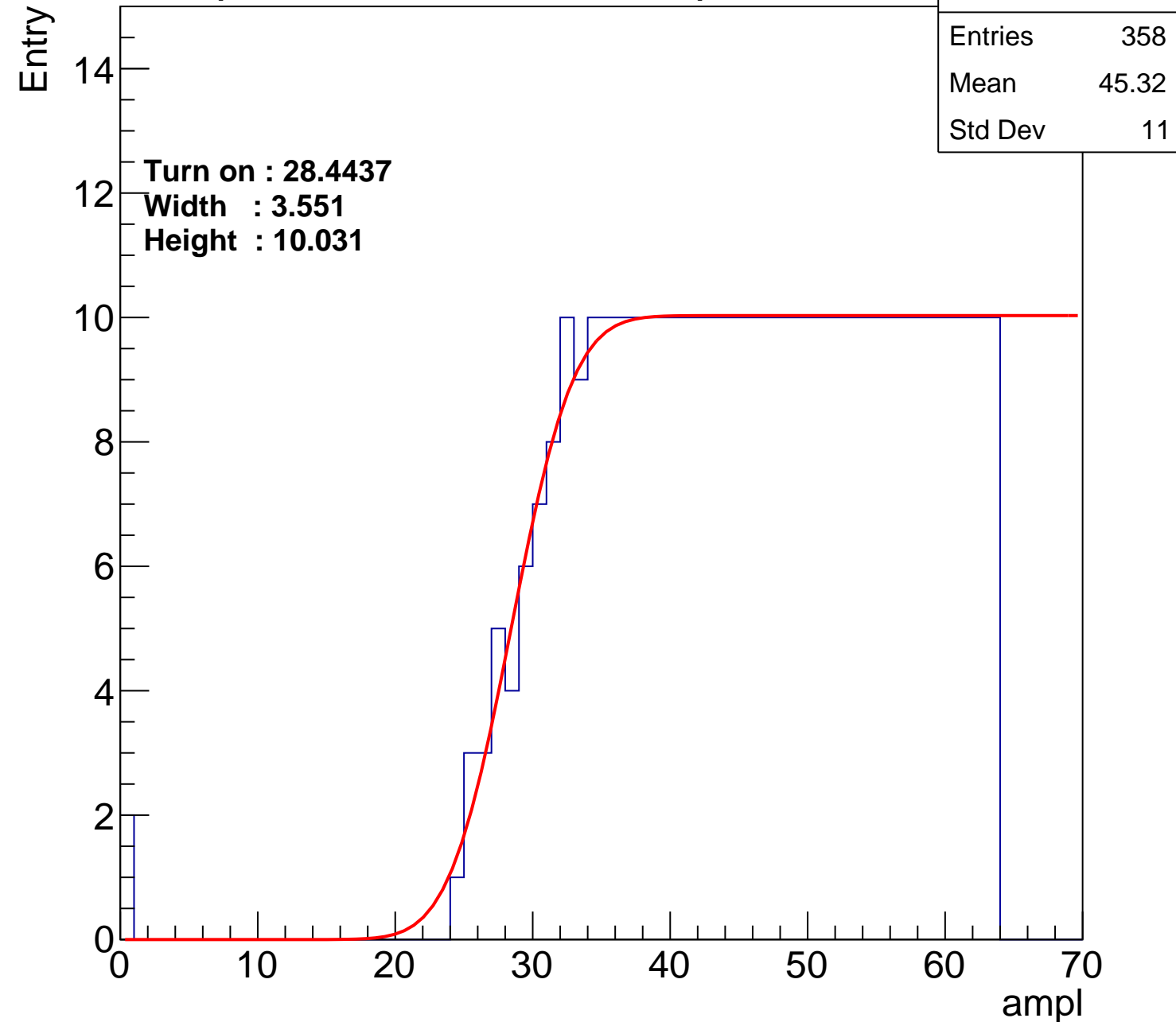
Width : 3.551

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch73

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.2
Std Dev	11.28

Turn on : 29.6915

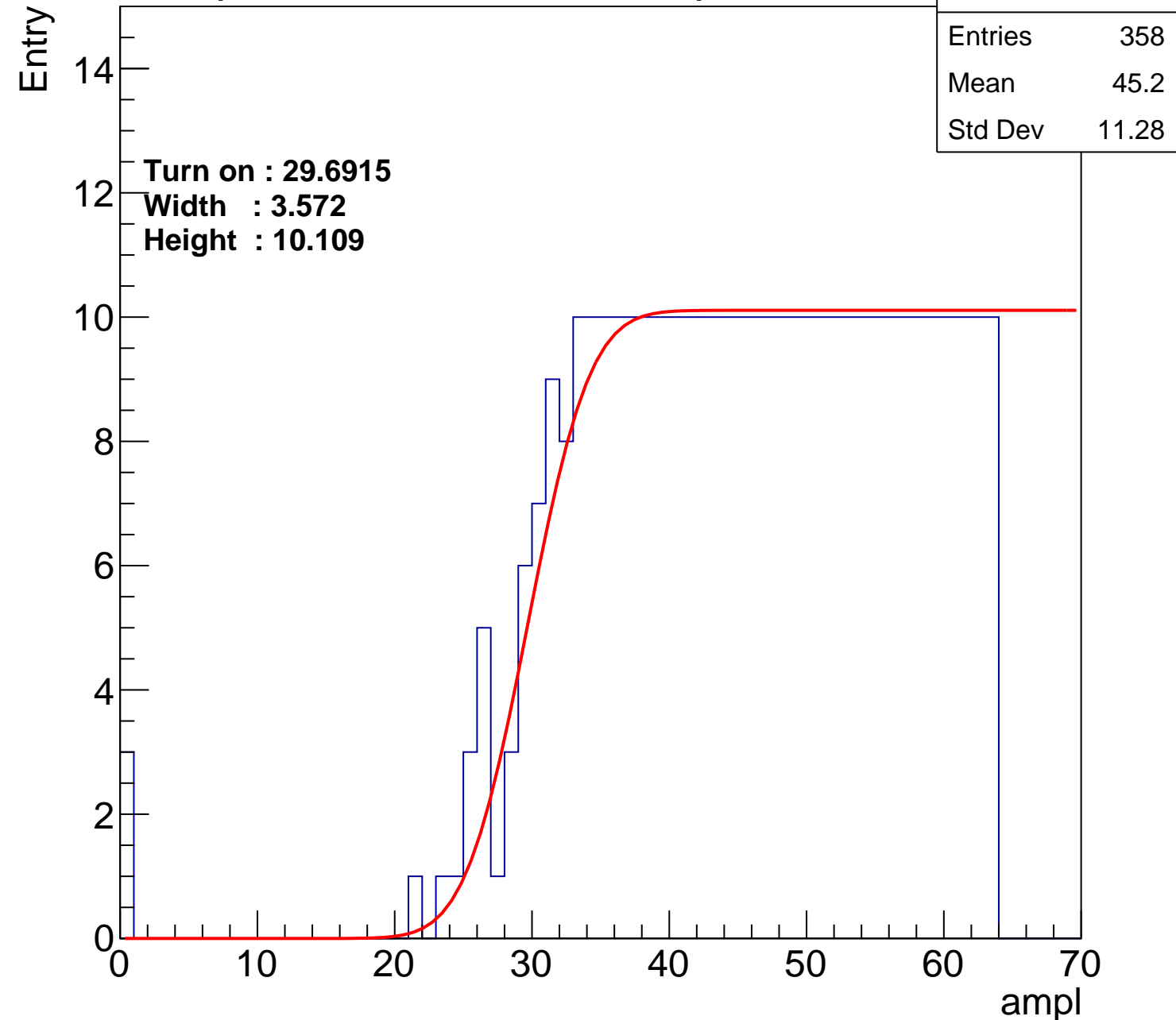
Width : 3.572

Height : 10.109

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch74

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	385
Mean	44.07
Std Dev	11.49

Turn on : 25.9562

Width : 3.441

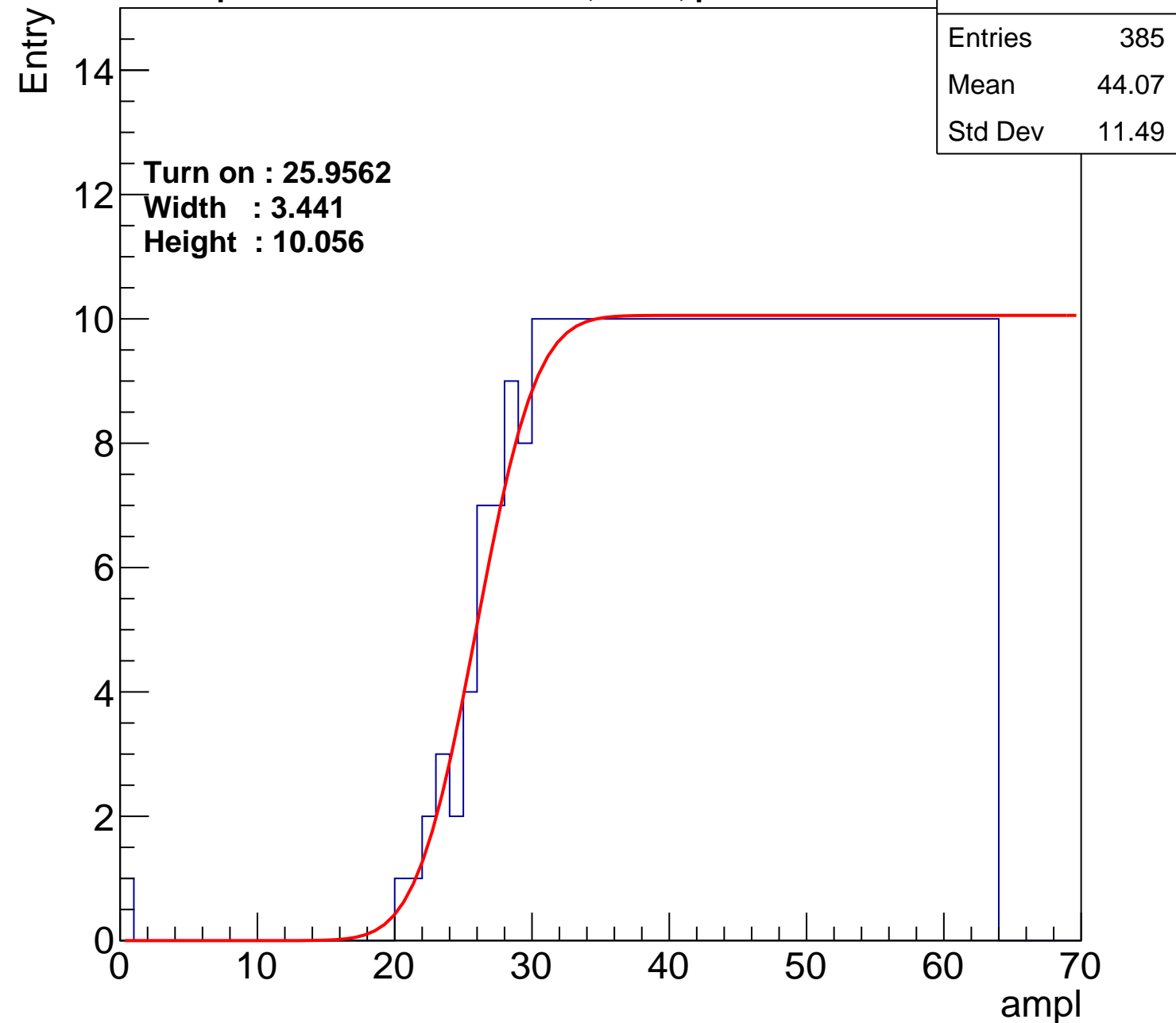
Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U25-ch75

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	341
Mean	46.17
Std Dev	10.55

Turn on : 30.7135

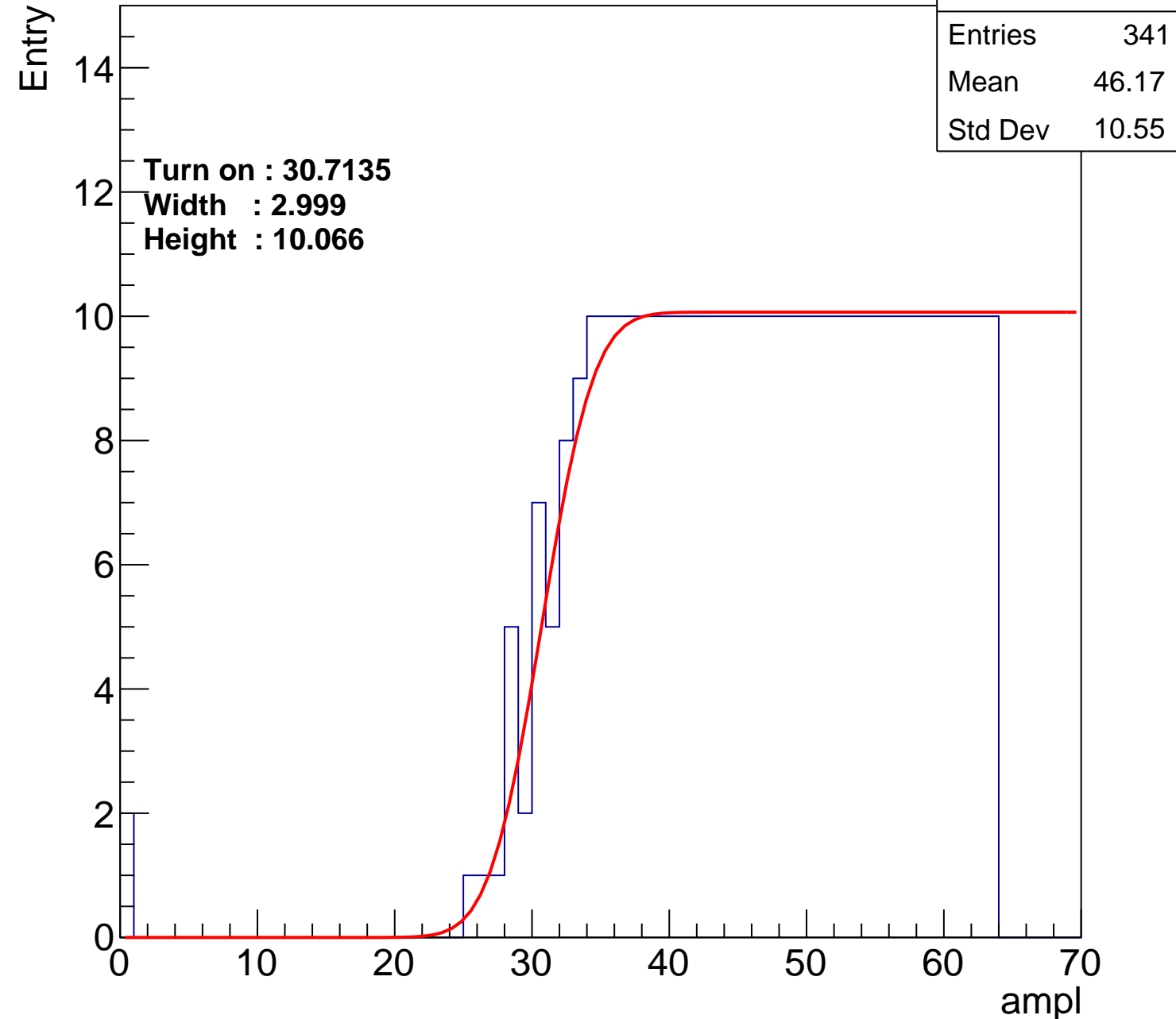
Width : 2.999

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch76

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.05
Std Dev	10.97

**Turn on : 27.9977**

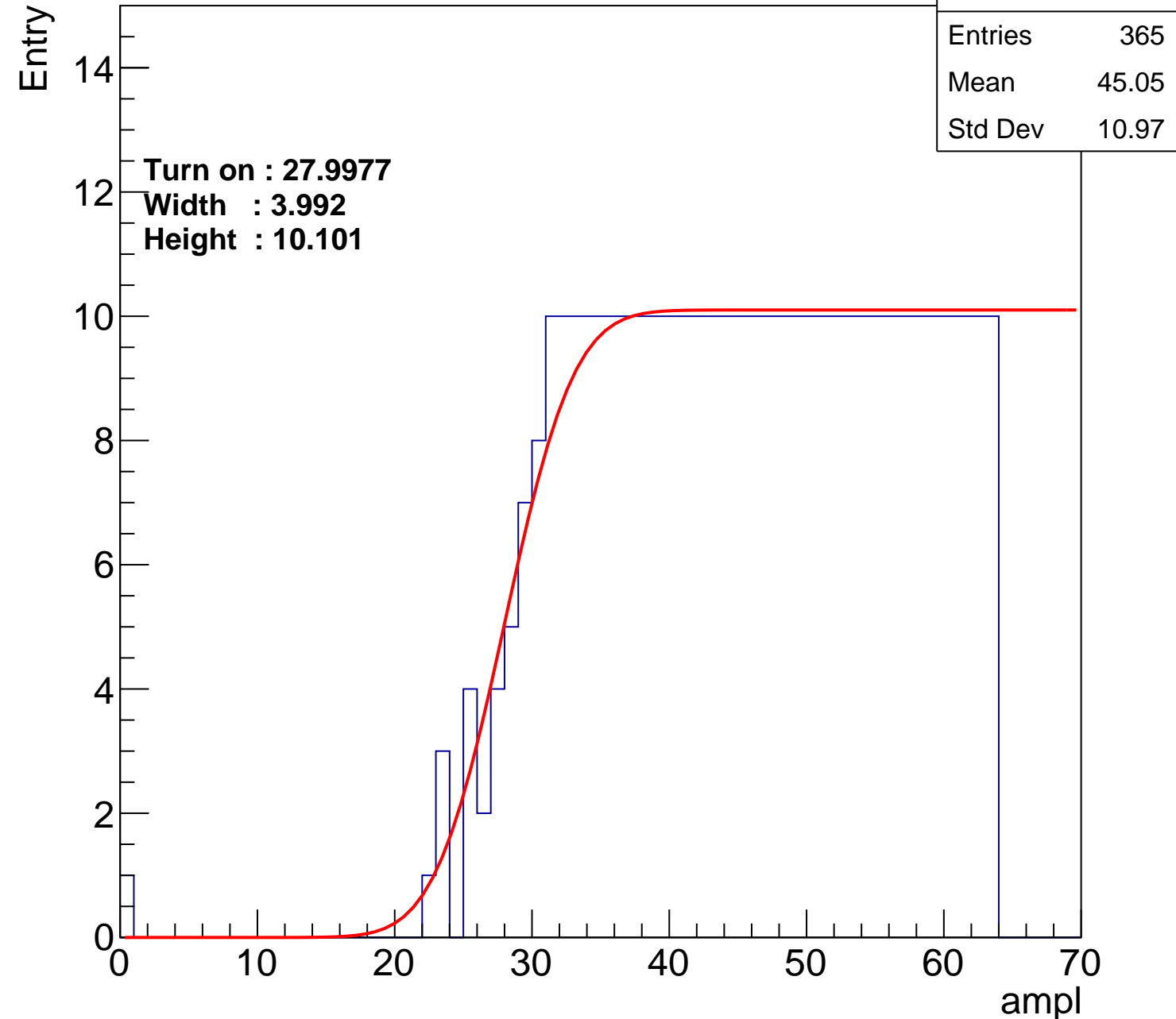
**Width : 3.992**

**Height : 10.101**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch77

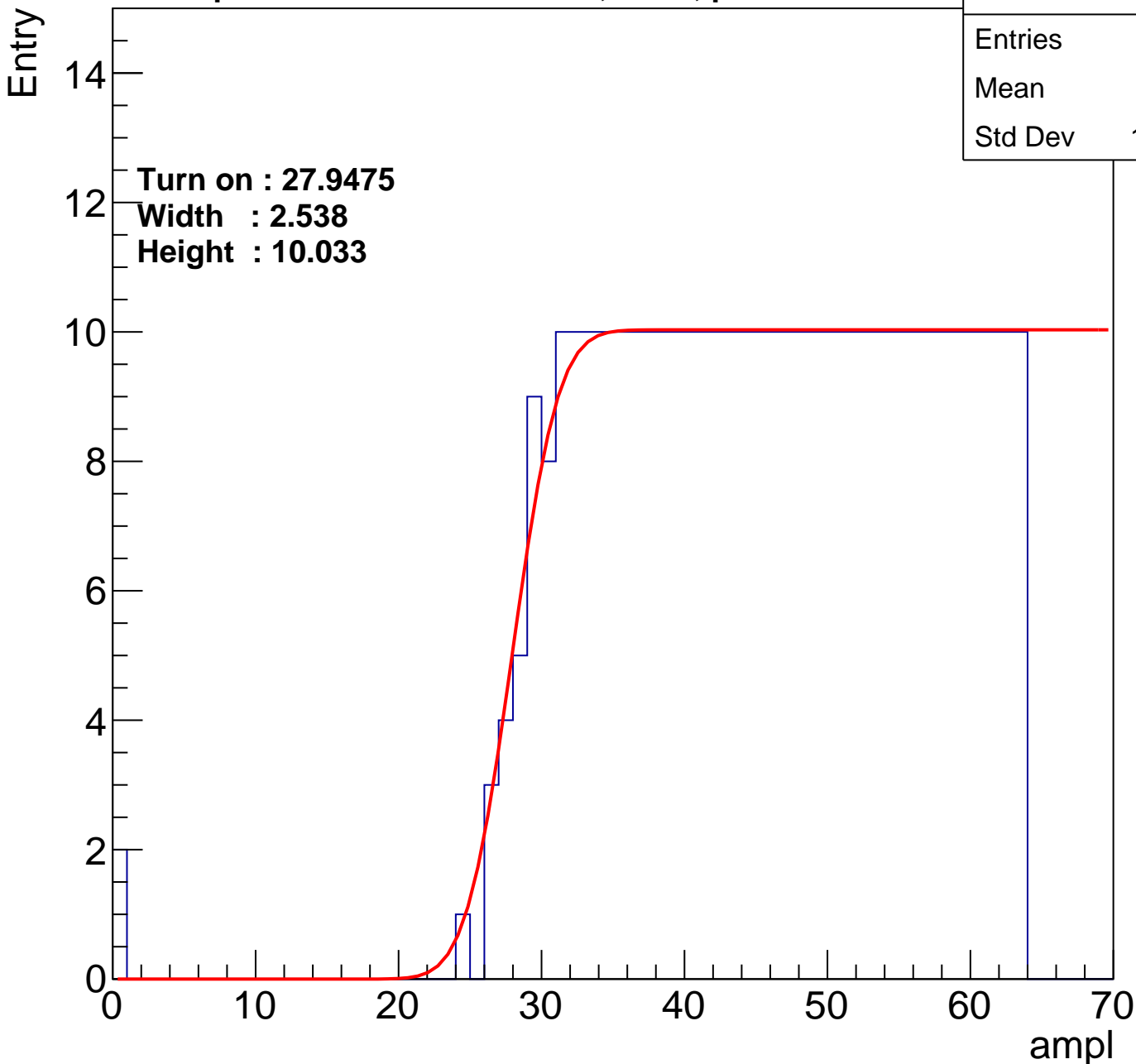
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**Turn on : 27.9475**

**Width : 2.538**

**Height : 10.033**

Entries	362
Mean	45.2
Std Dev	10.98



# B0L002S, U25-ch78

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.84
Std Dev	11.03

Turn on : 27.0895

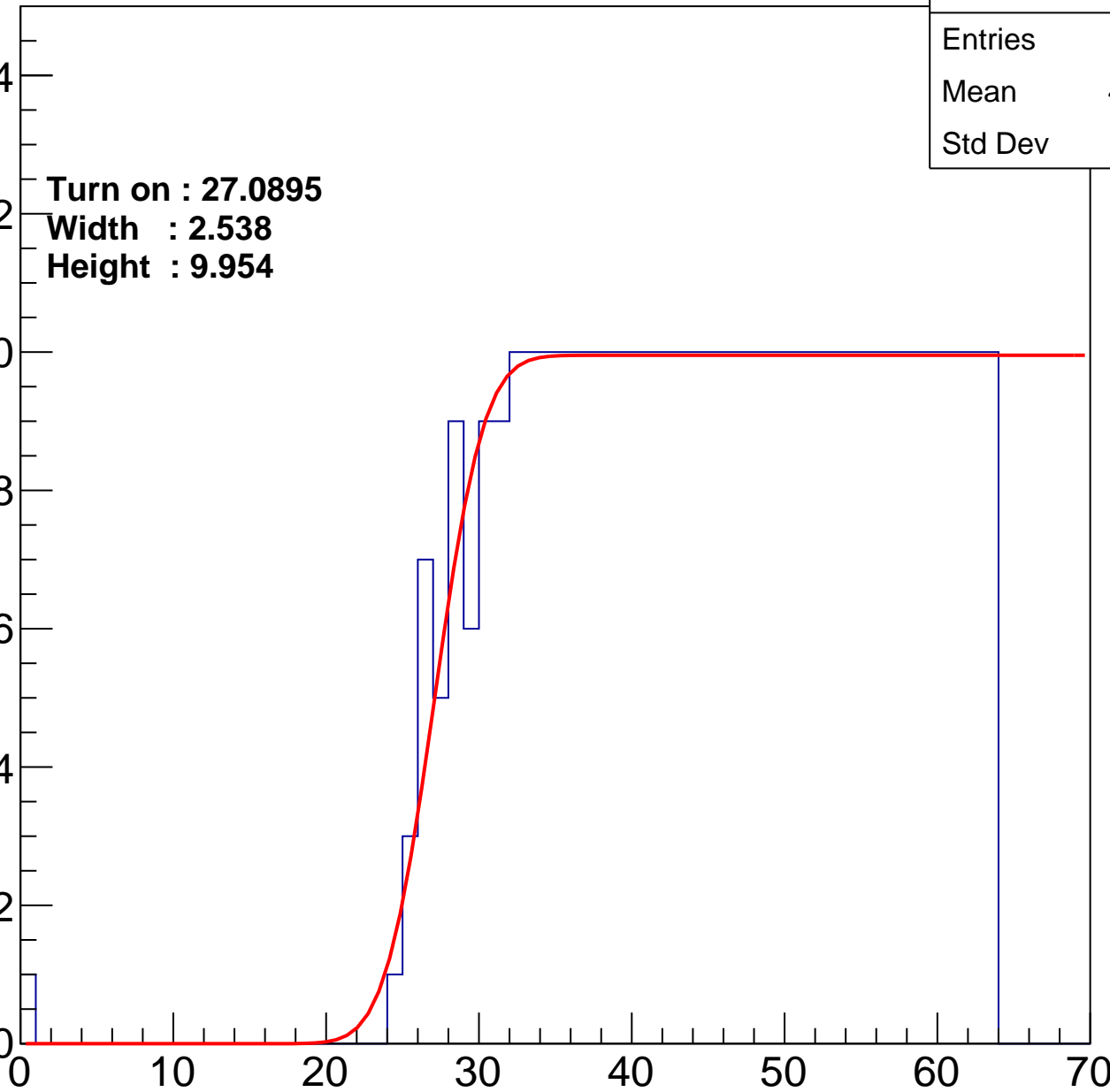
Width : 2.538

Height : 9.954

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch79

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.64
Std Dev	11.34

Turn on : 27.0815

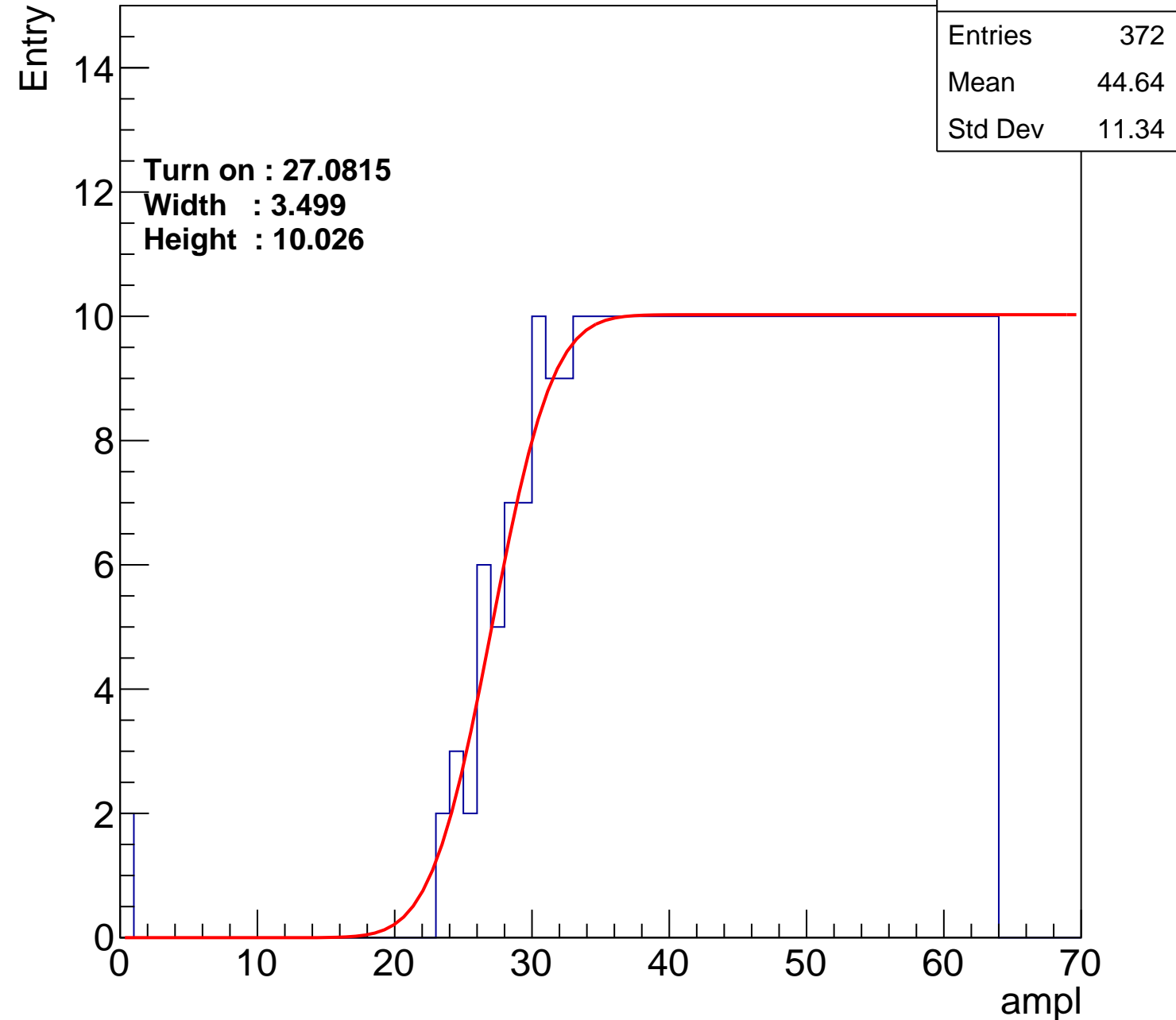
Width : 3.499

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch80

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.03
Std Dev	11.13

Turn on : 28.0523

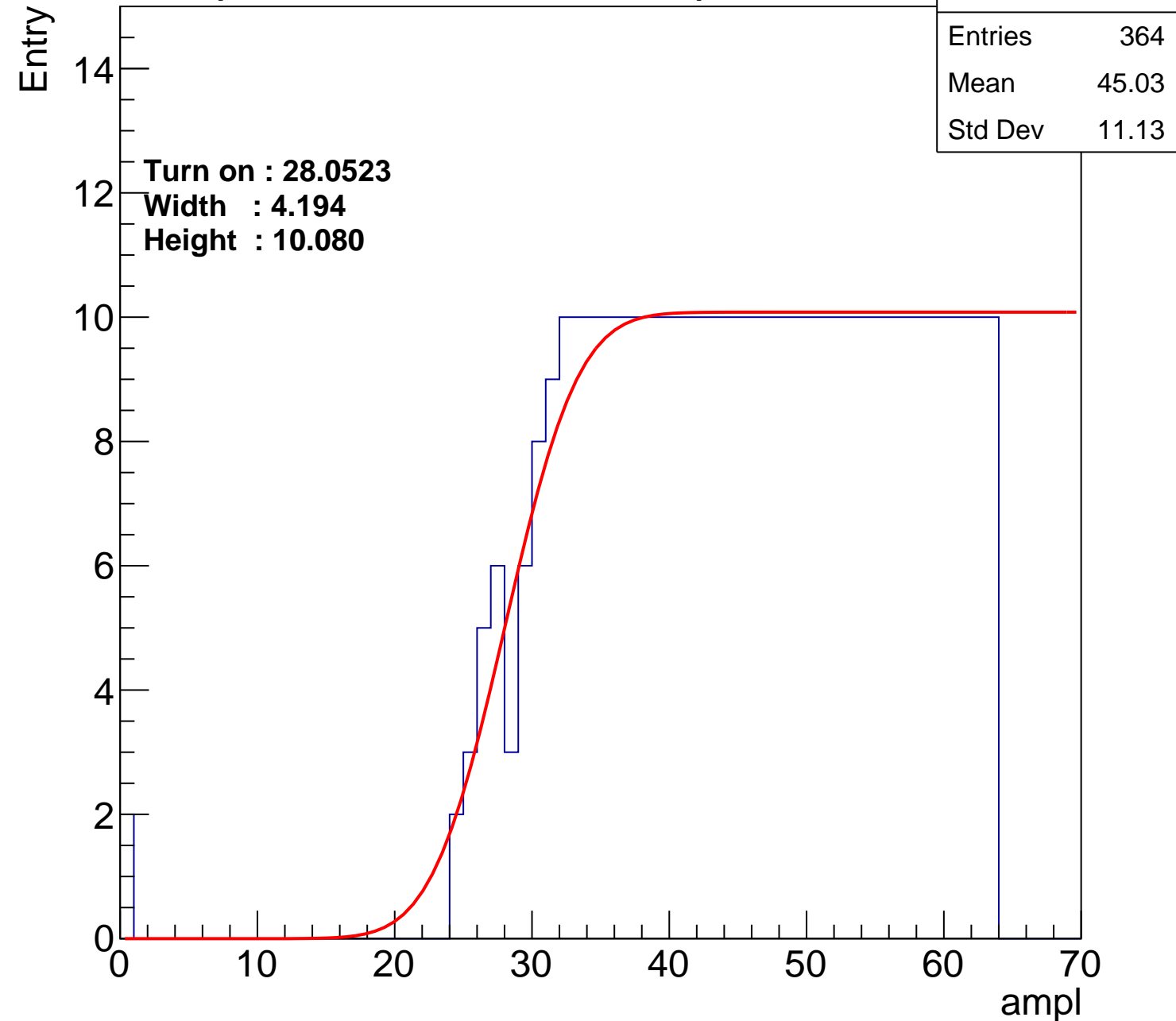
Width : 4.194

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch81

calib\_packv5\_042523\_0143.root, FC#8, port C1

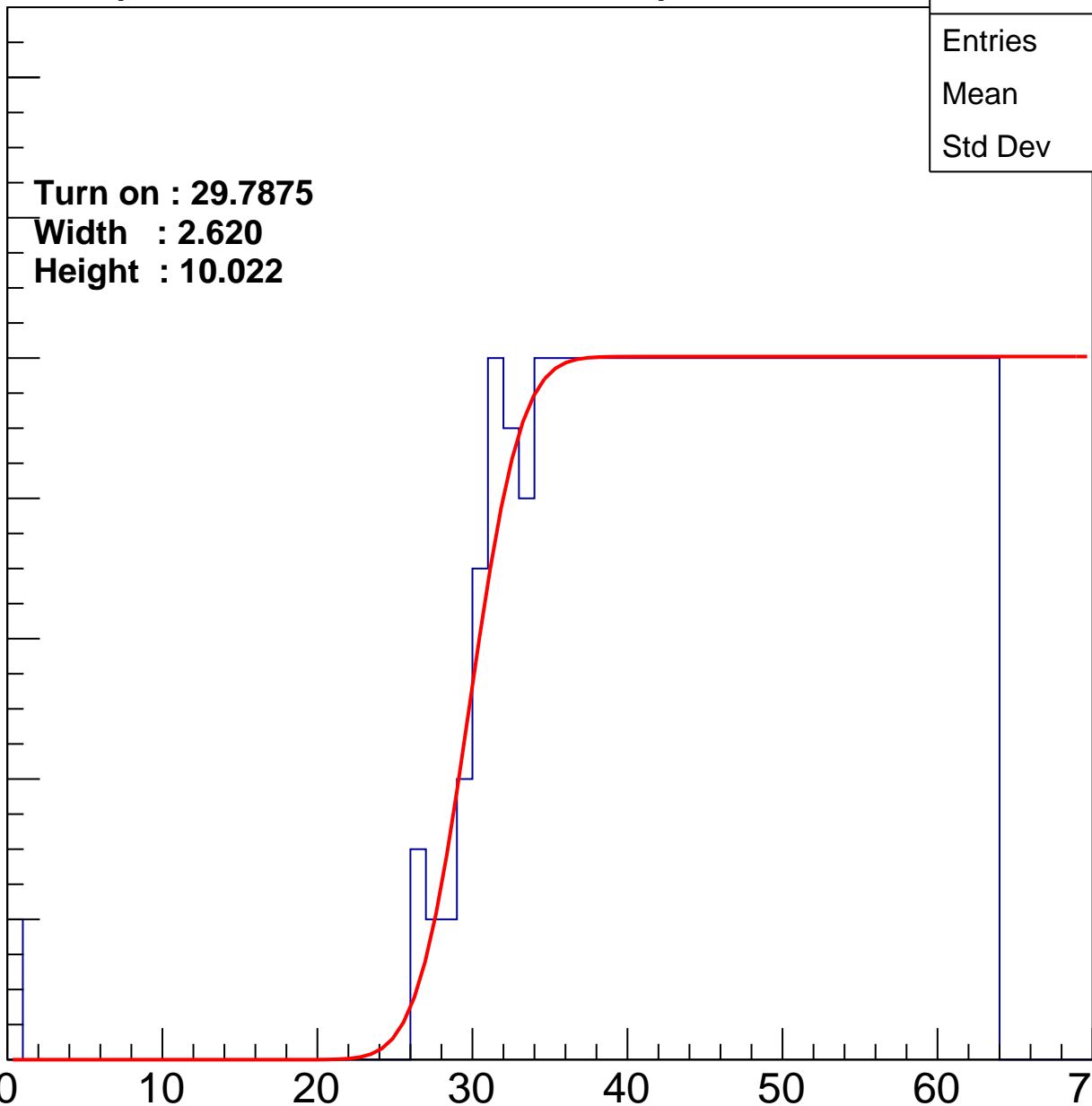
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 29.7875  
Width : 2.620  
Height : 10.022

Entries	347
Mean	45.9
Std Dev	10.66

ampl



# B0L002S, U25-ch82

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.83
Std Dev	11.05

Turn on : 27.4248

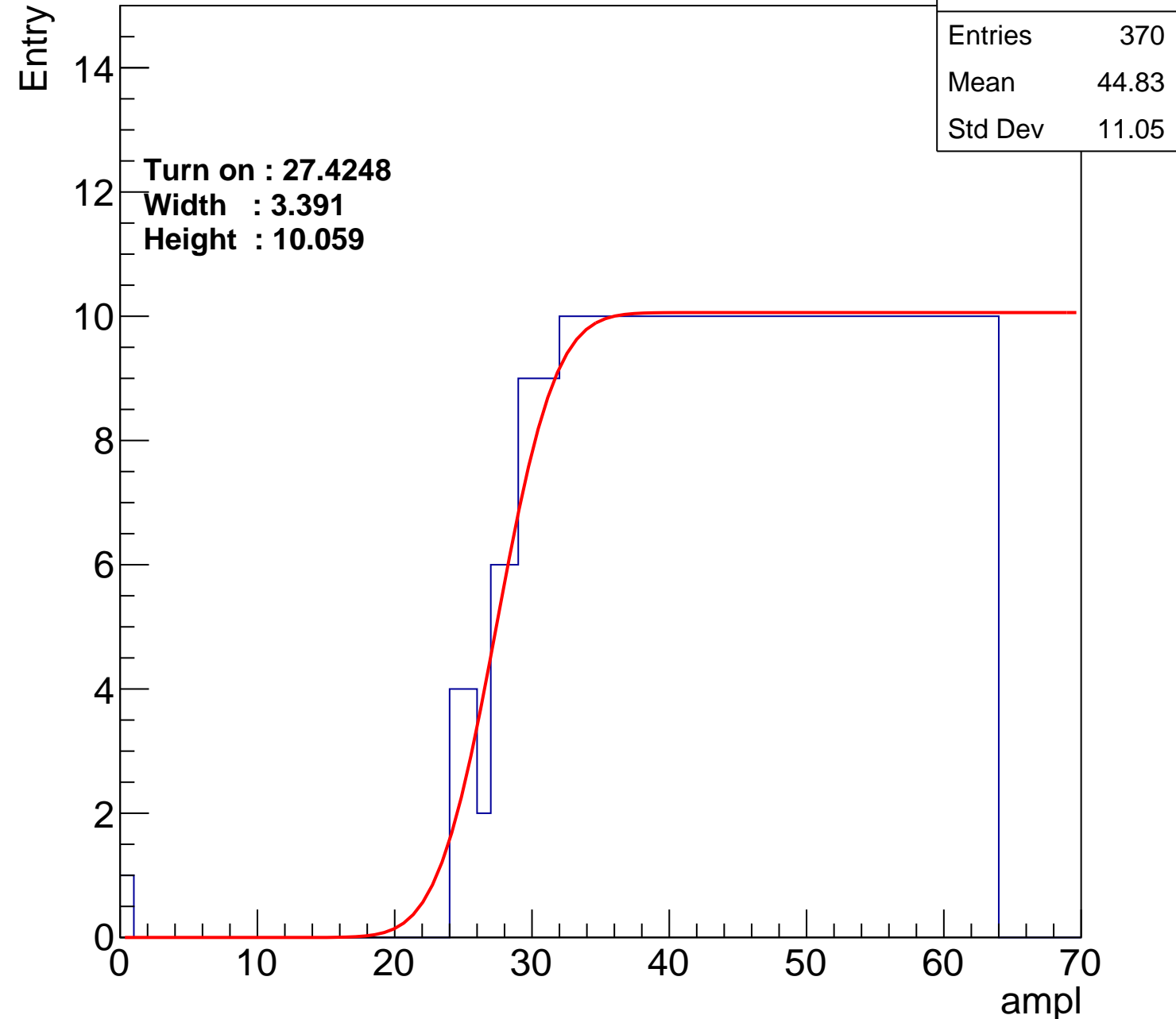
Width : 3.391

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch83

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.63
Std Dev	11.29

Turn on : 26.9908

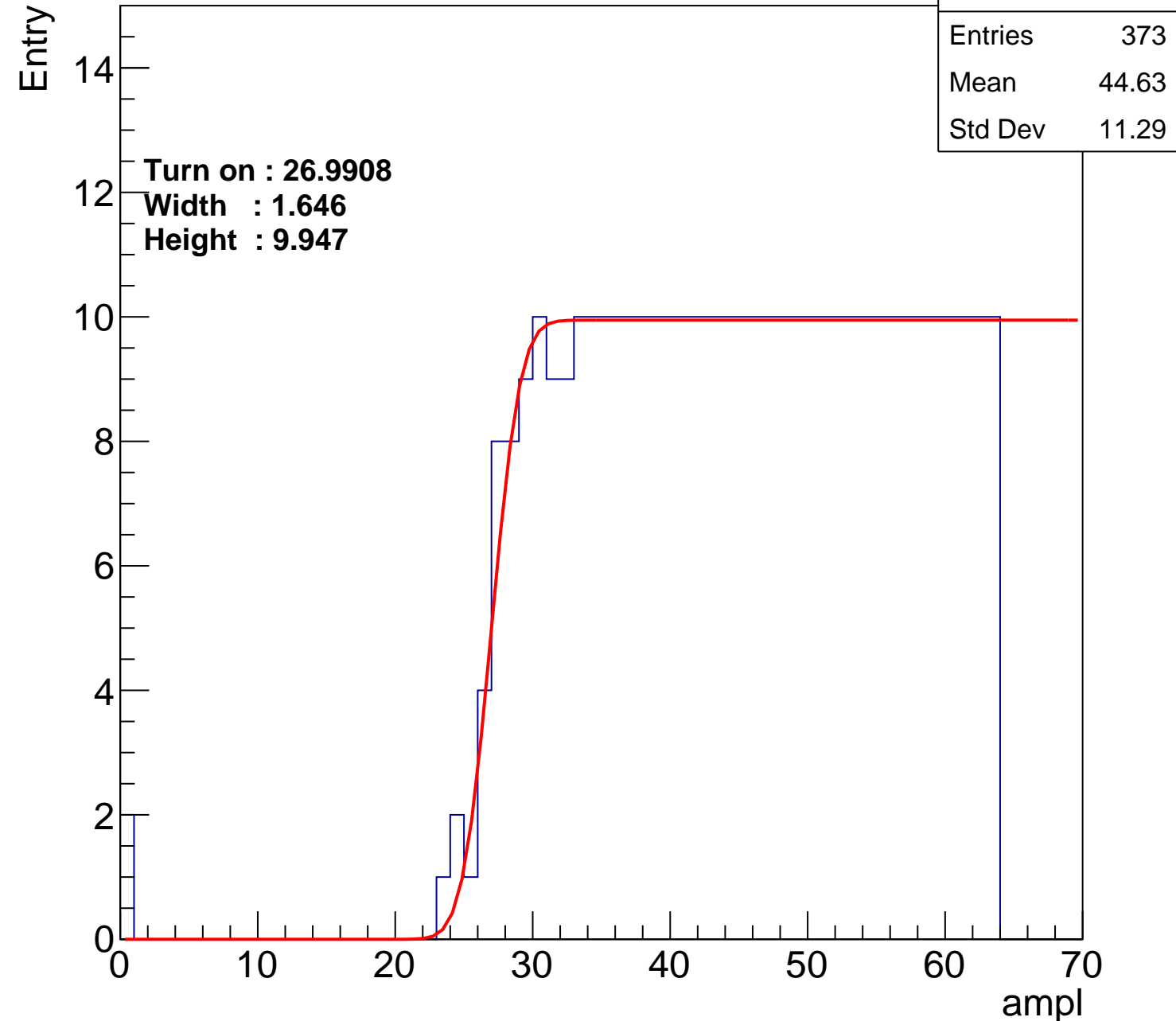
Width : 1.646

Height : 9.947

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch84

calib\_packv5\_042523\_0143.root, FC#8, port C1

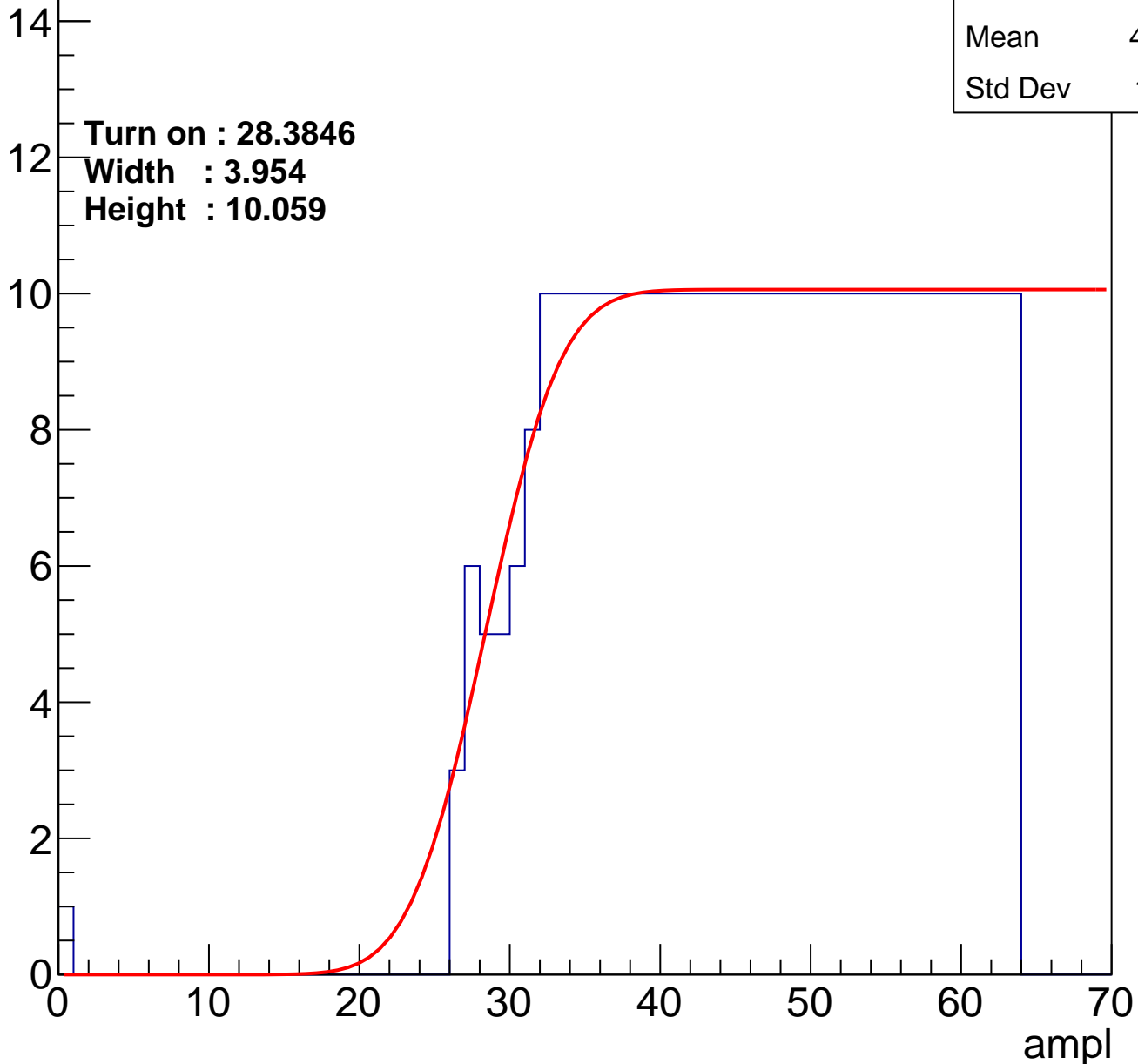
Entries	354
Mean	45.63
Std Dev	10.61

Turn on : 28.3846

Width : 3.954

Height : 10.059

Entry



# B0L002S, U25-ch85

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.06
Std Dev	11.16

Turn on : 28.2490

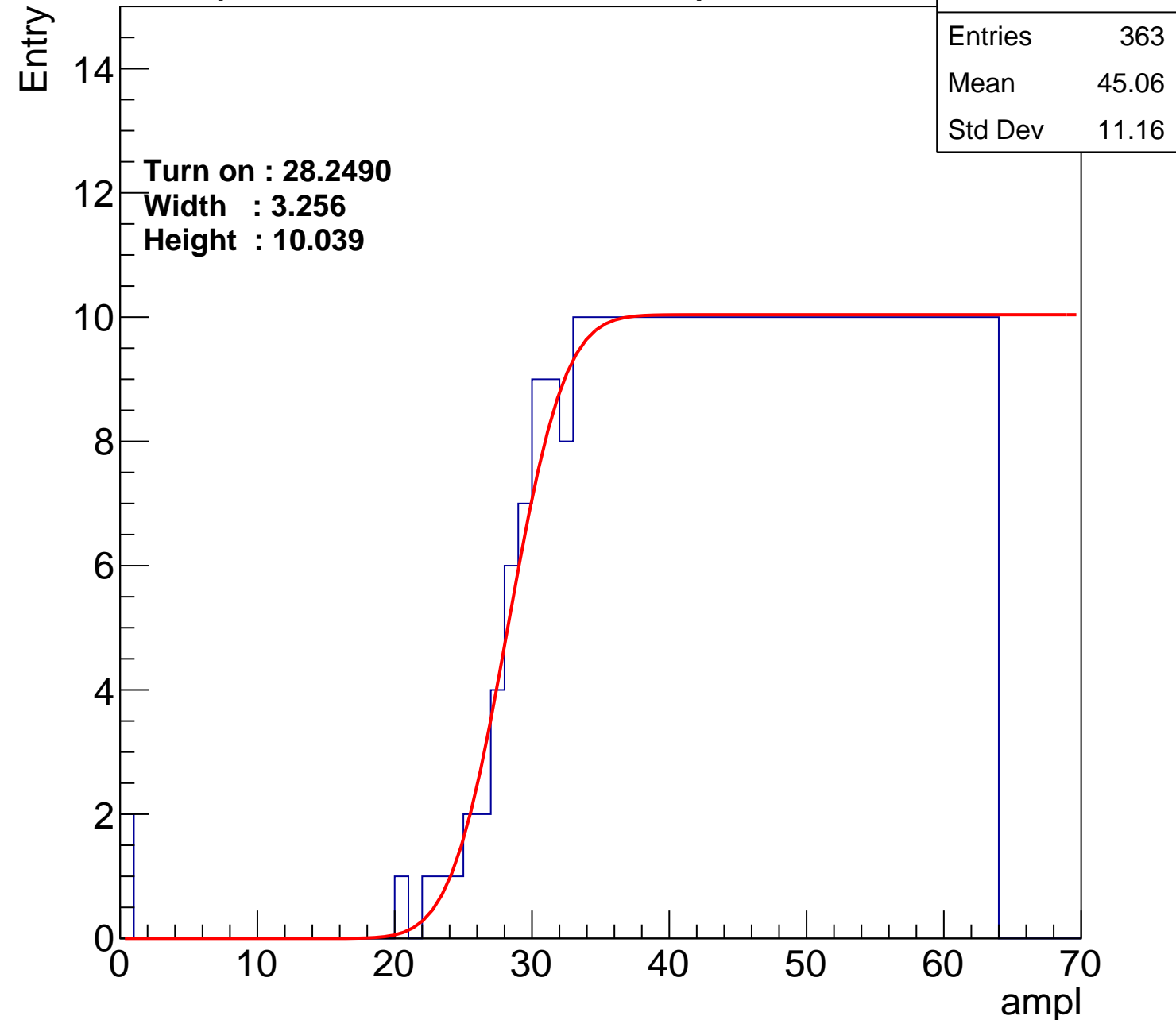
Width : 3.256

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch86

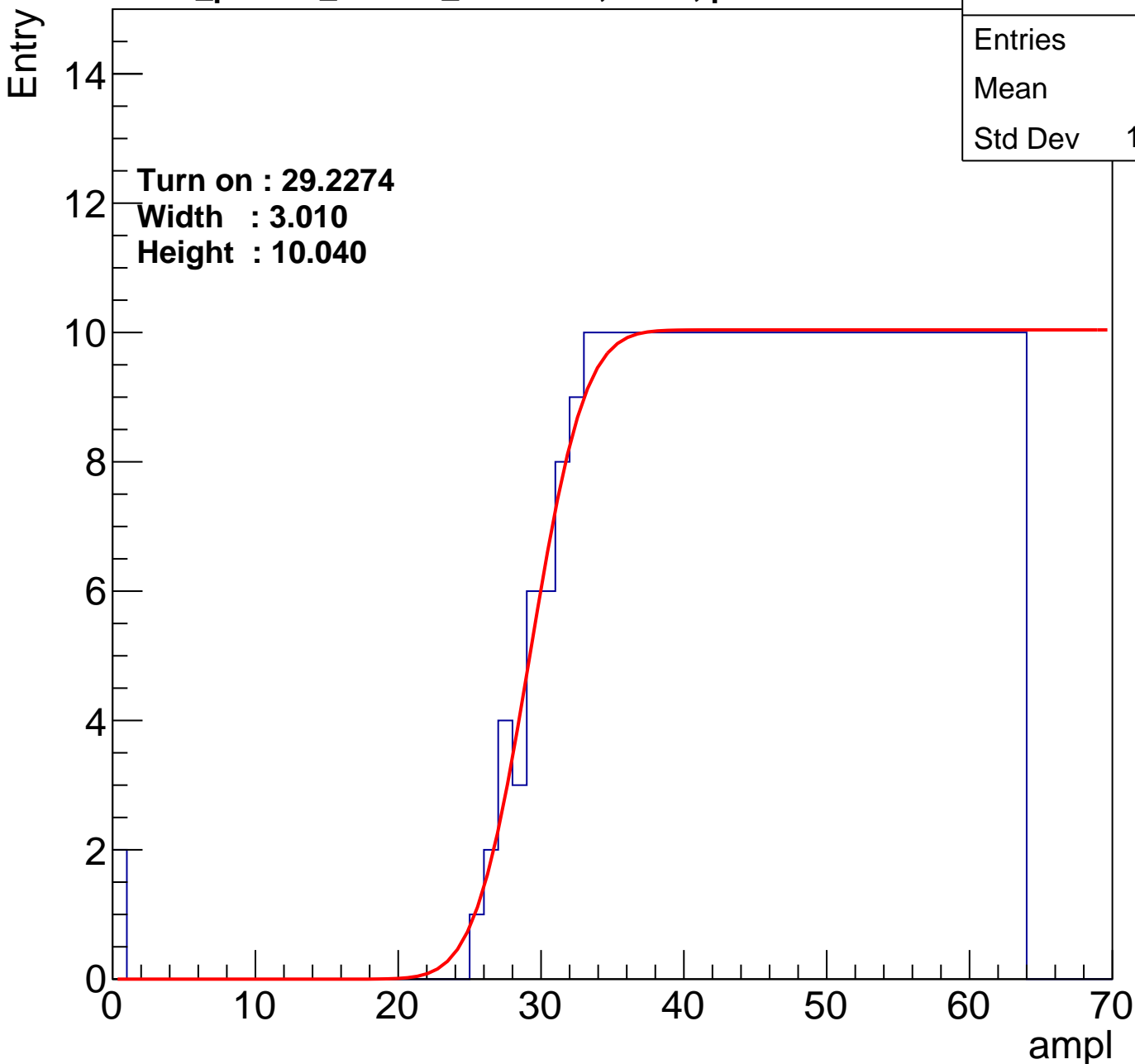
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	351
Mean	45.7
Std Dev	10.77

**Turn on : 29.2274**

**Width : 3.010**

**Height : 10.040**





# B0L002S, U25-ch87

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.33
Std Dev	10.99

Turn on : 28.0885

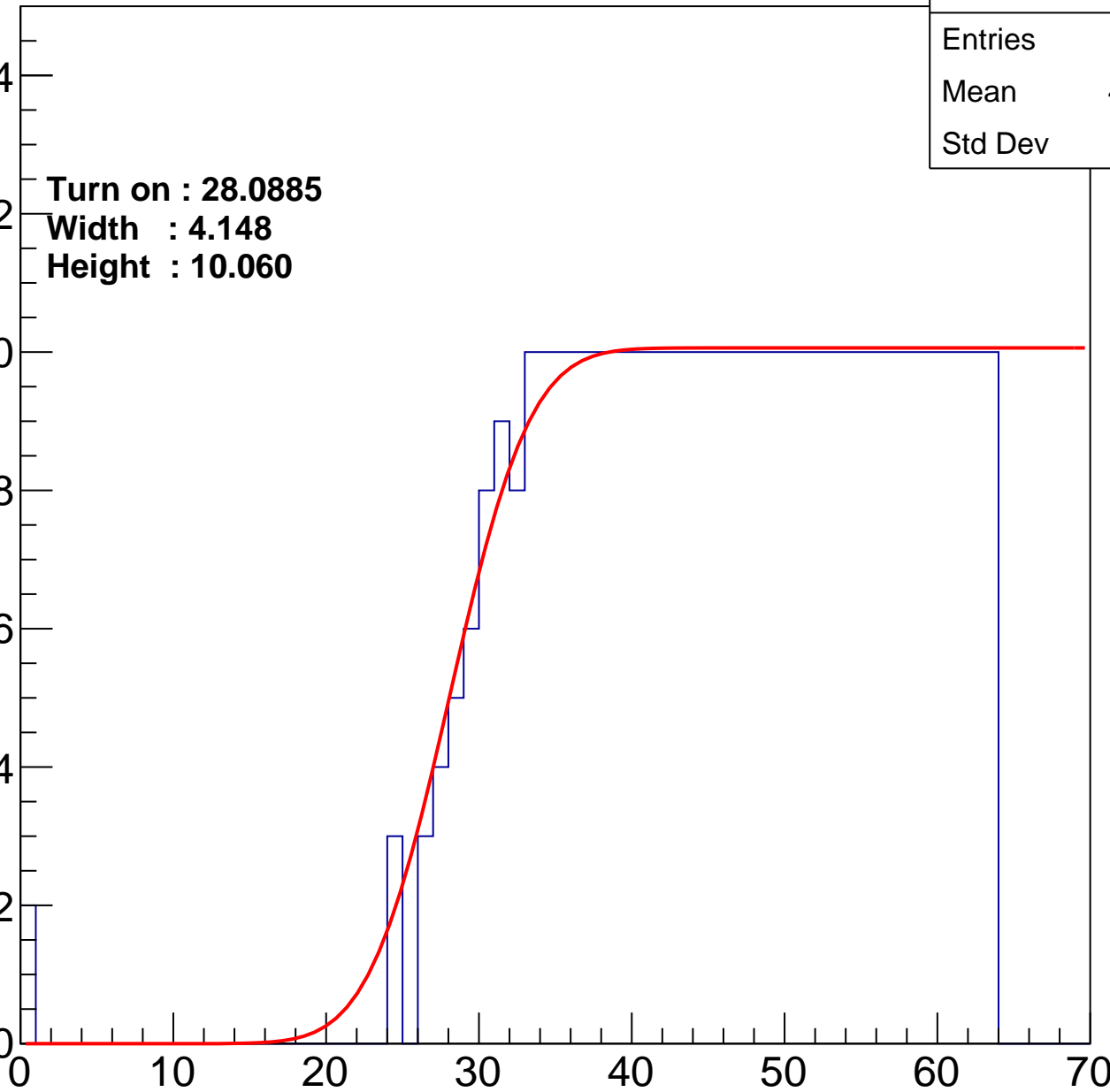
Width : 4.148

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch88

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.36
Std Dev	10.93

Turn on : 28.5451

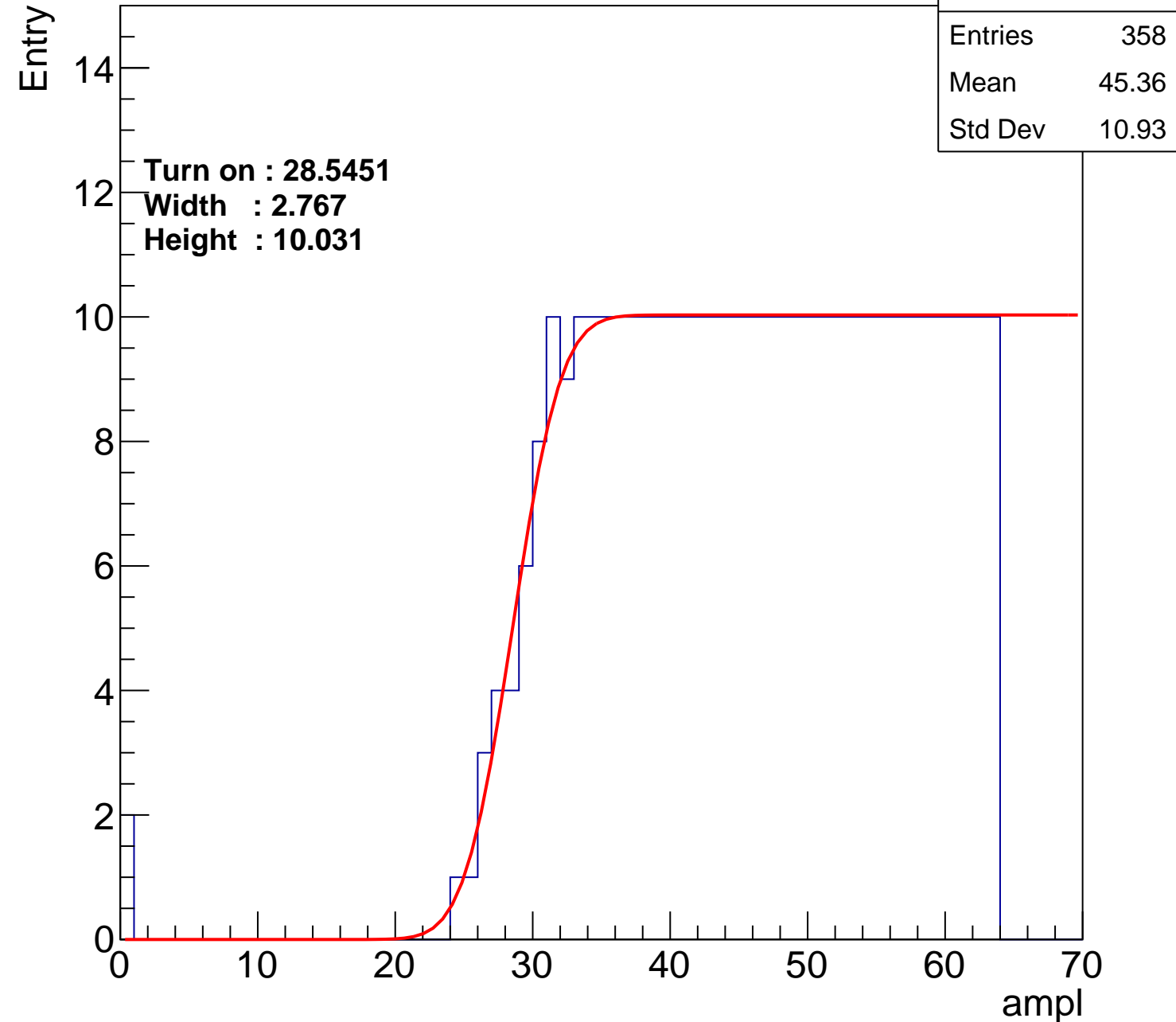
Width : 2.767

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch89

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.88
Std Dev	11.21

**Turn on : 26.7230**

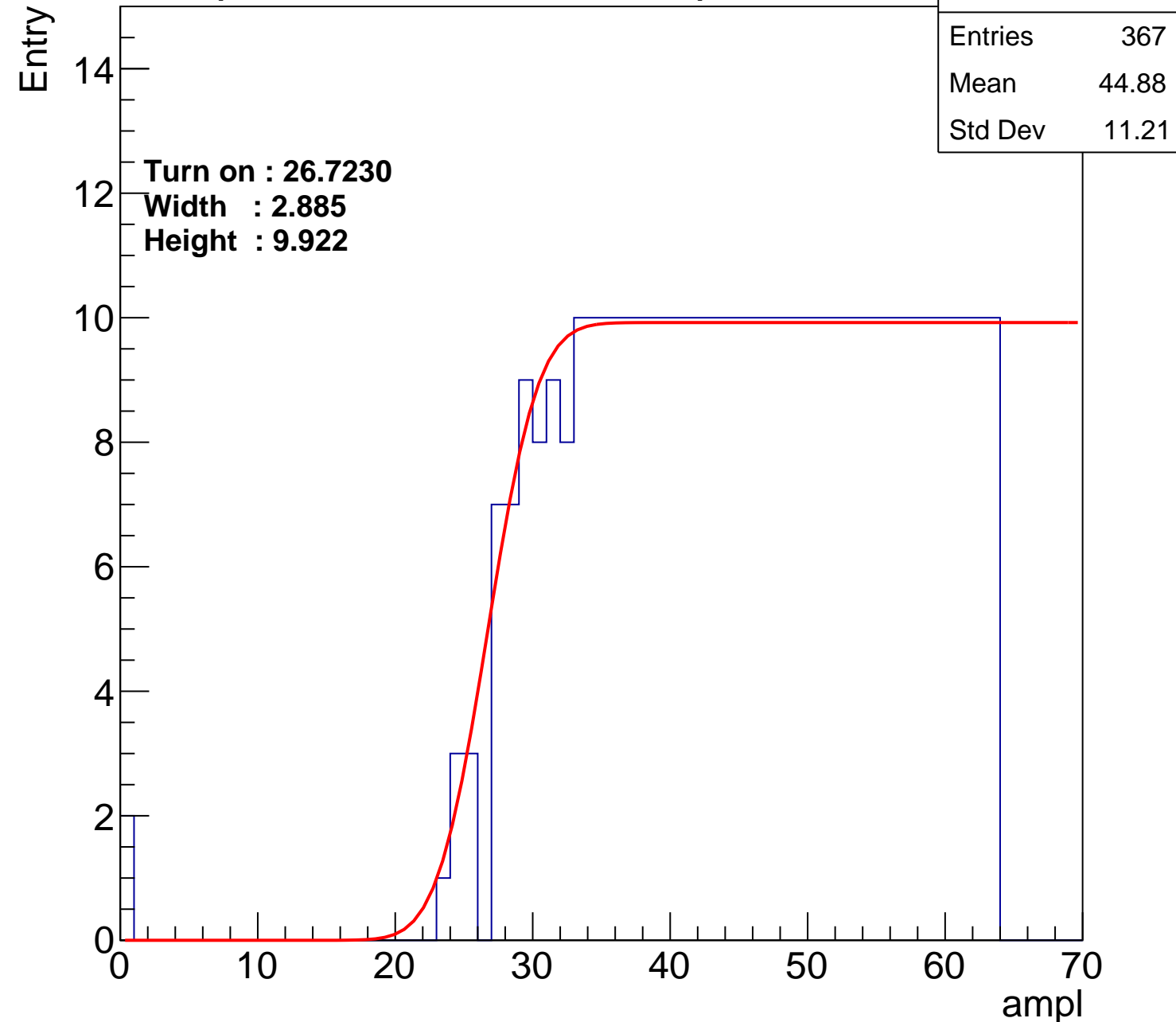
**Width : 2.885**

**Height : 9.922**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch90

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	346
Mean	46.03
Std Dev	10.39

Turn on : 29.5603

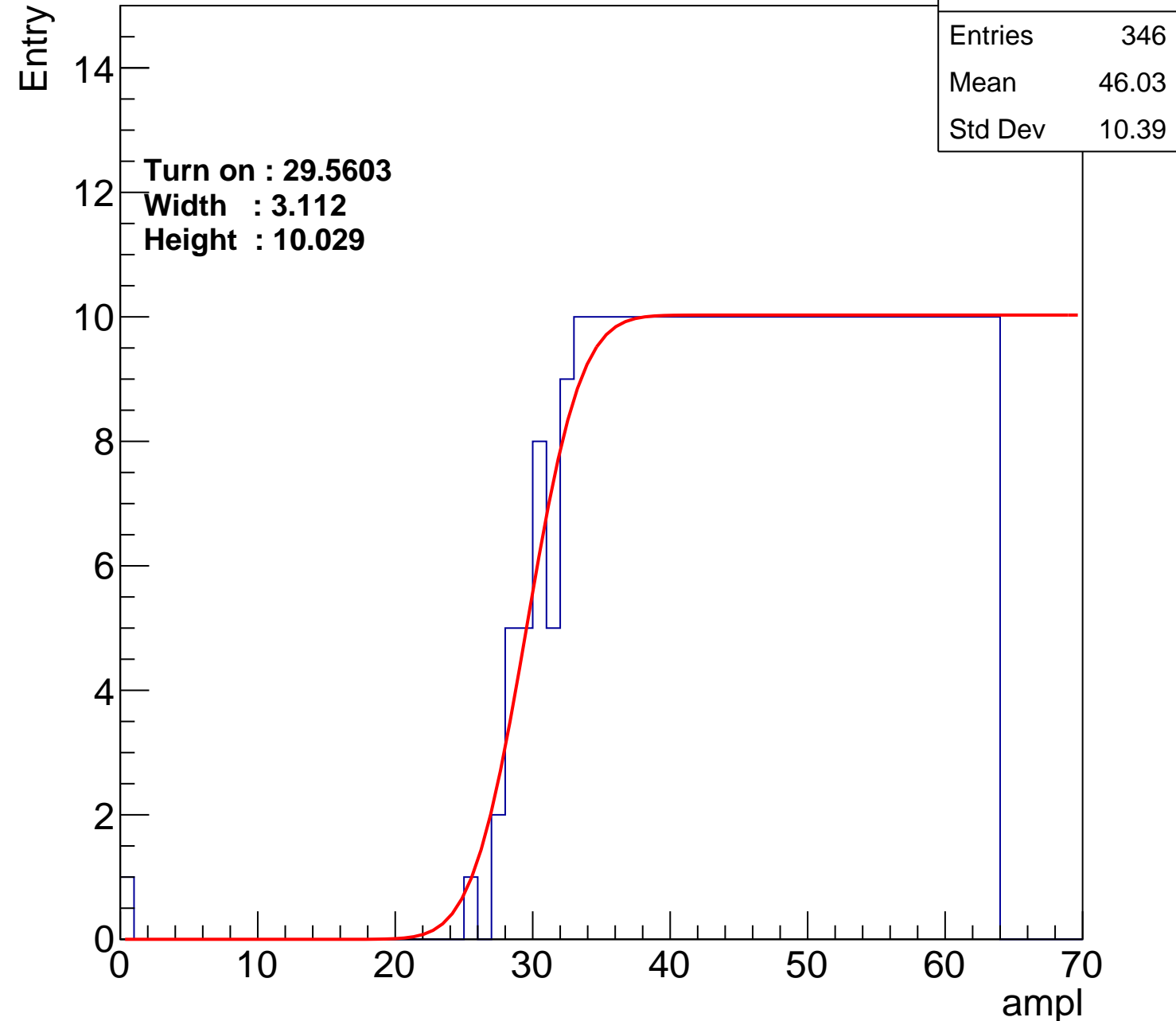
Width : 3.112

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch91

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.39
Std Dev	10.77

Turn on : 28.1285

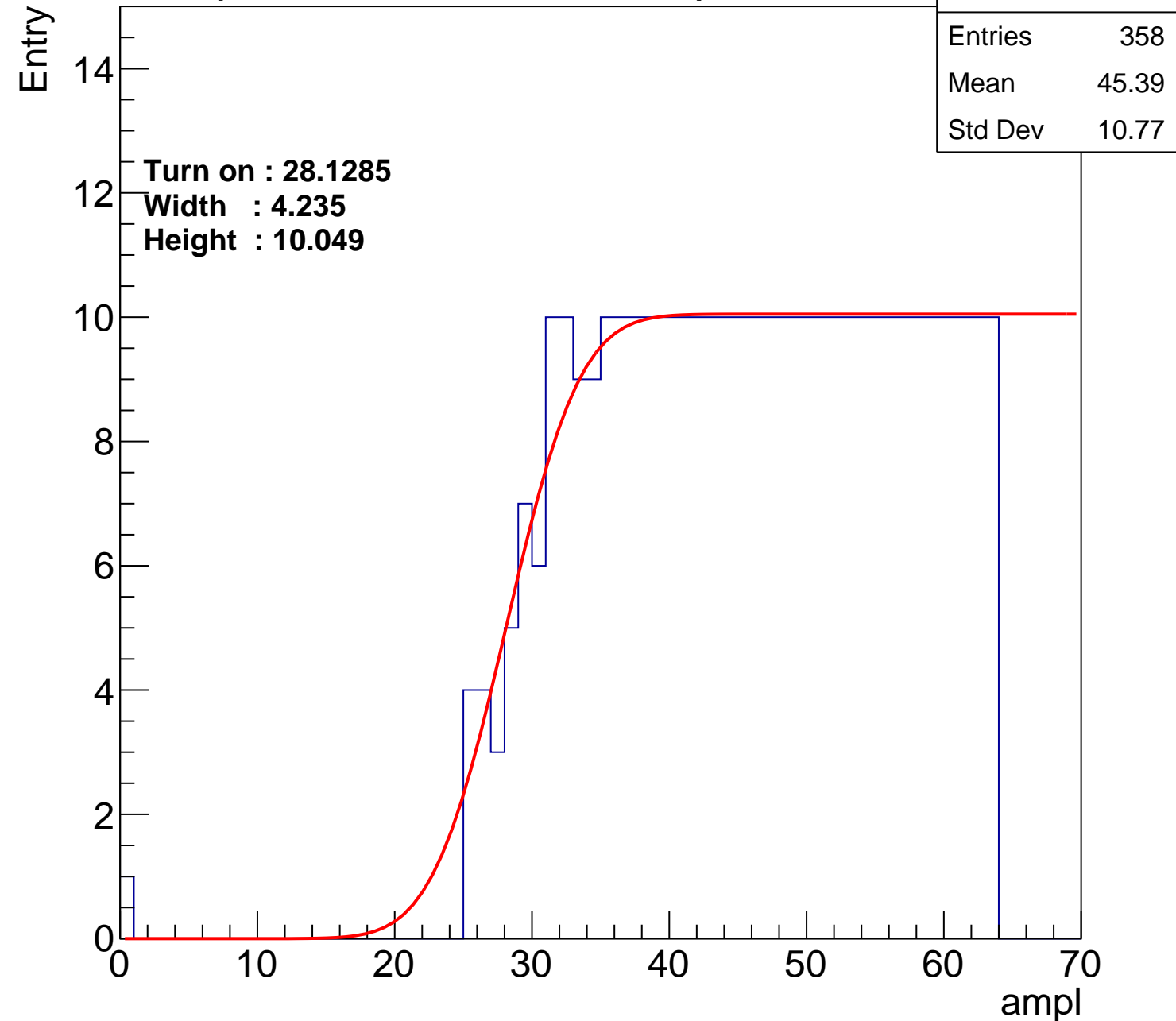
Width : 4.235

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch92

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.39
Std Dev	10.73

Turn on : 28.3496

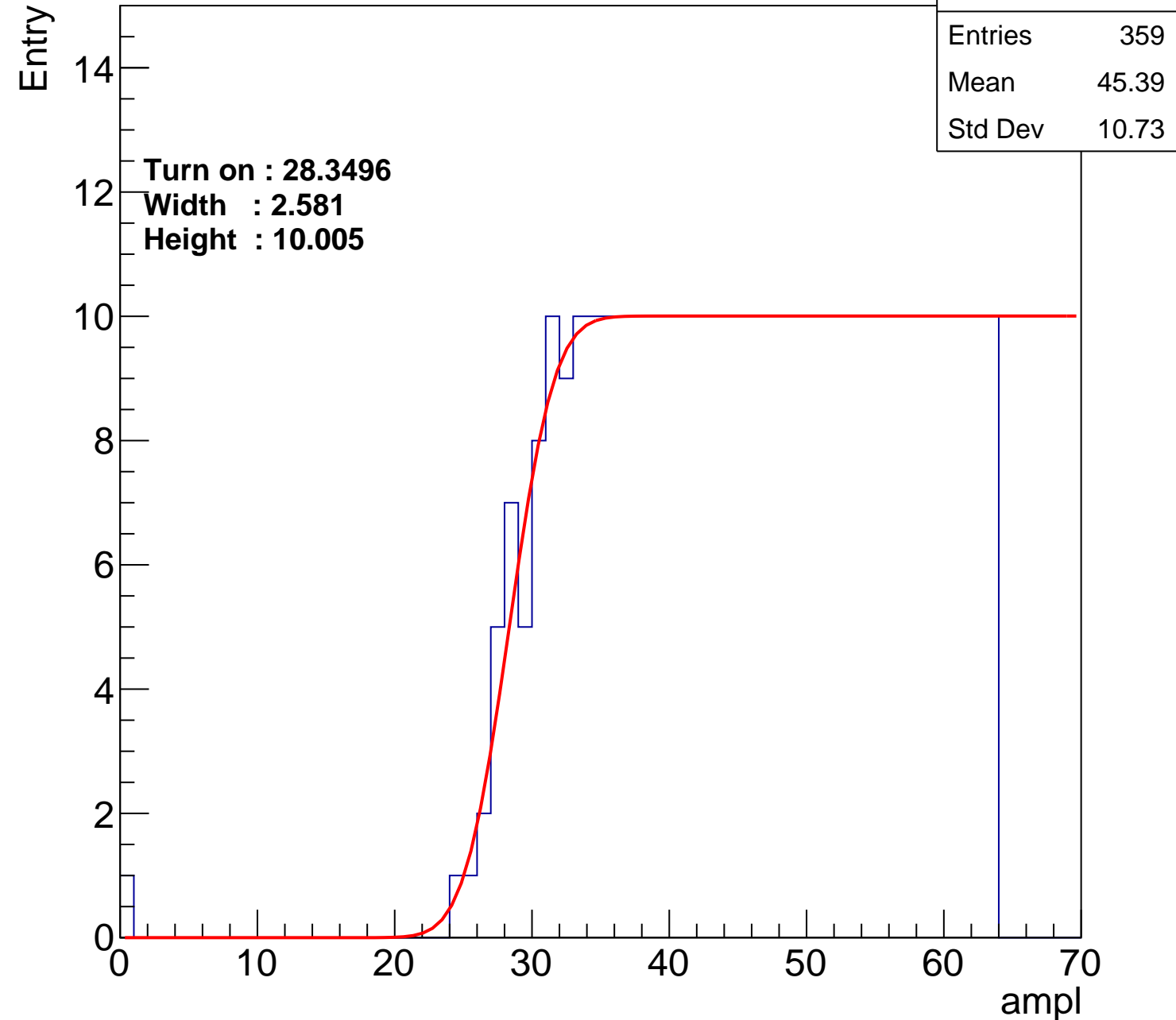
Width : 2.581

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch93

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.24
Std Dev	11.22

Turn on : 28.9390

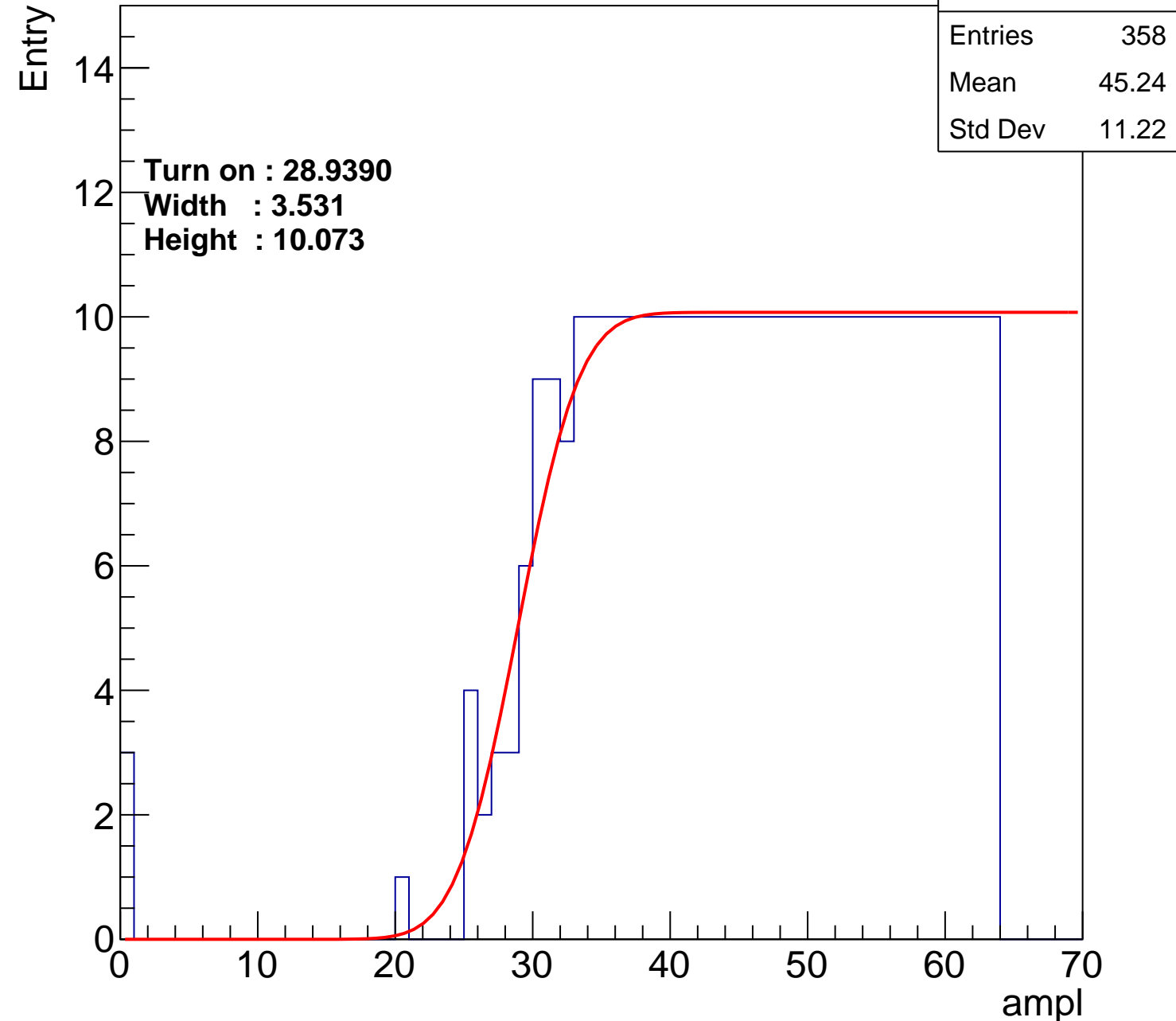
Width : 3.531

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch94

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	375
Mean	44.61
Std Dev	11.14

Turn on : 26.7711

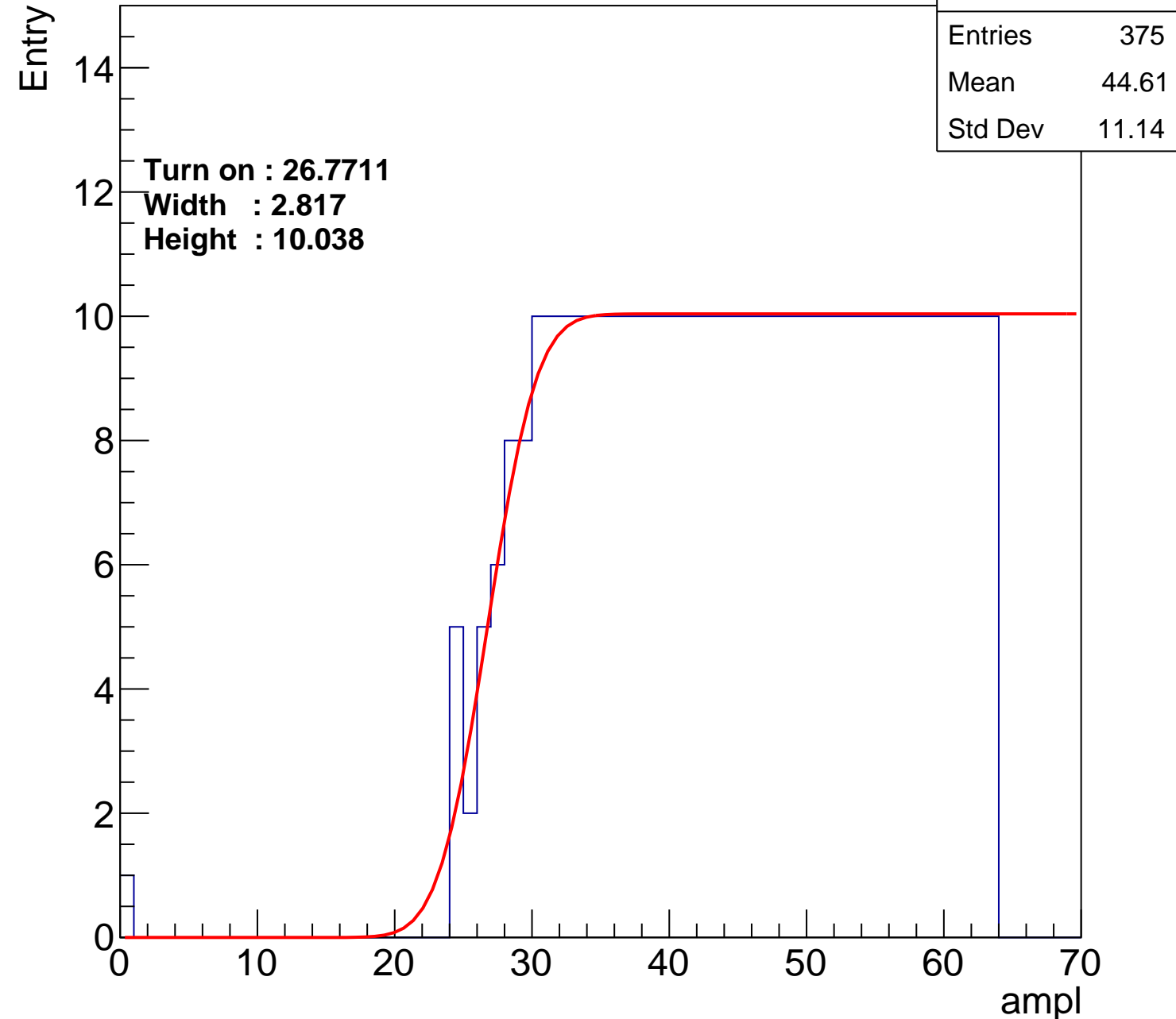
Width : 2.817

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch95

calib\_packv5\_042523\_0143.root, FC#8, port C1

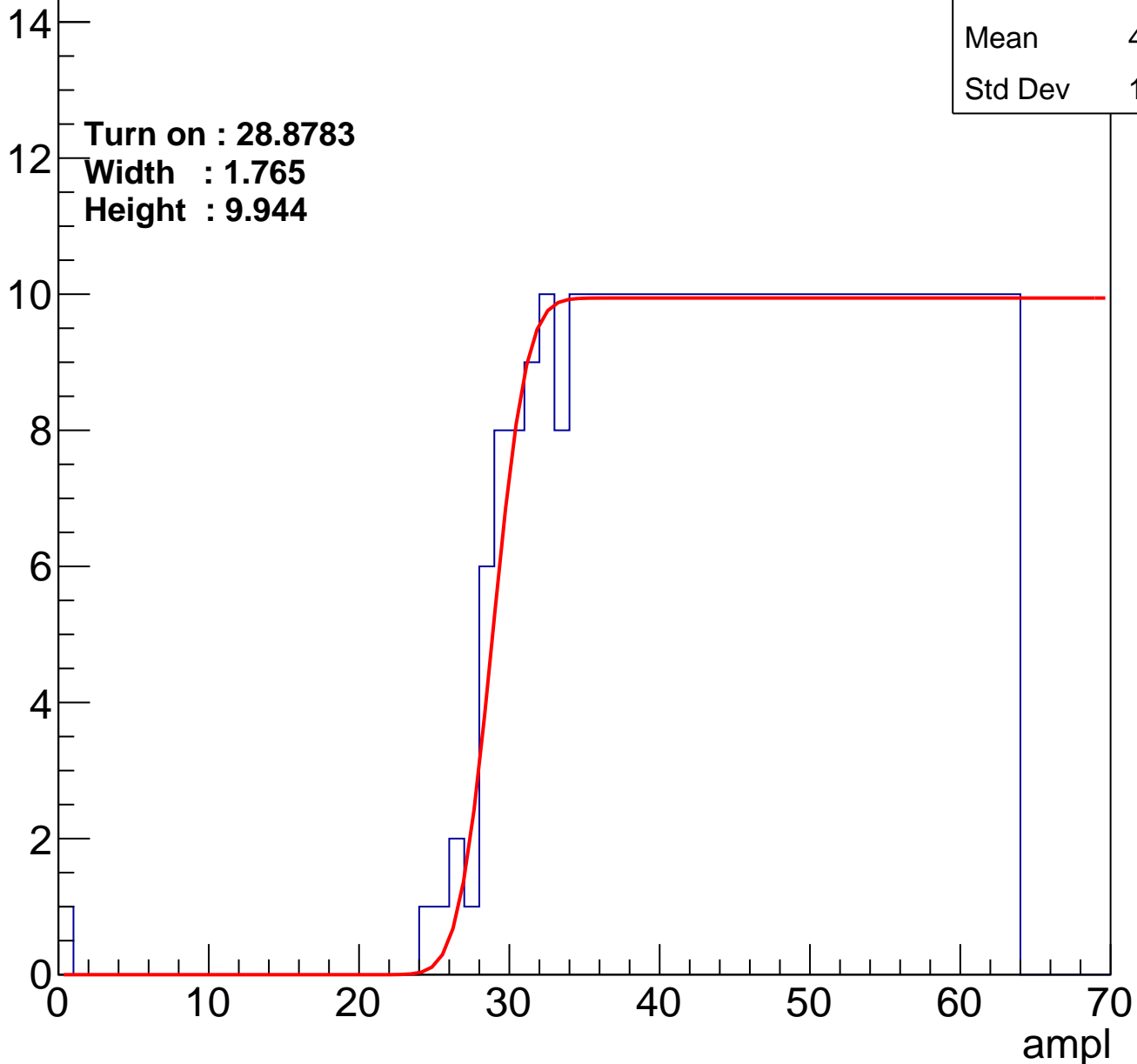
Entries	355
Mean	45.58
Std Dev	10.63

**Turn on : 28.8783**

**Width : 1.765**

**Height : 9.944**

Entry



# B0L002S, U25-ch96

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.32
Std Dev	11.15

**Turn on : 28.8747**

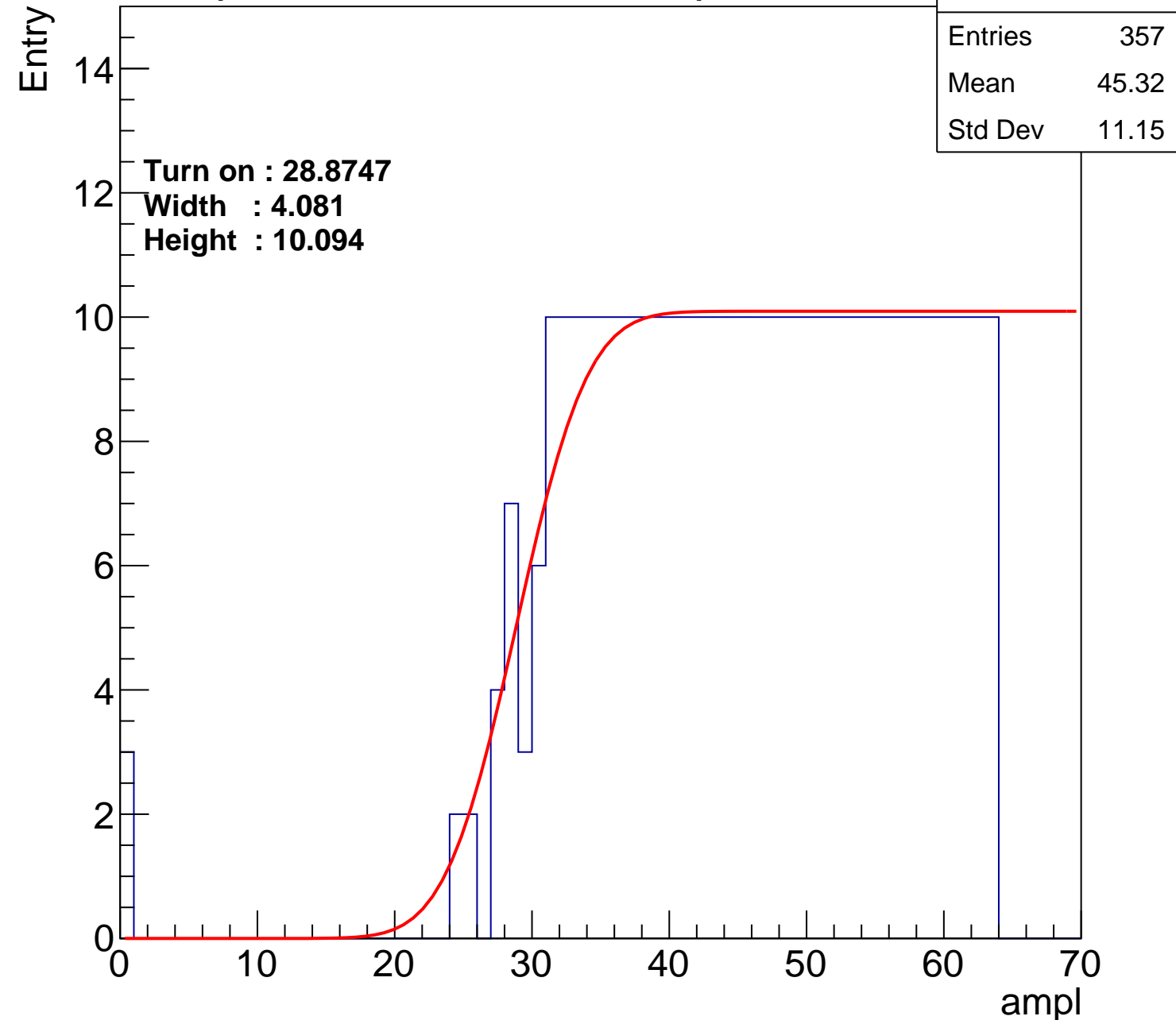
**Width : 4.081**

**Height : 10.094**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch97

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.36
Std Dev	10.98

Turn on : 28.4125

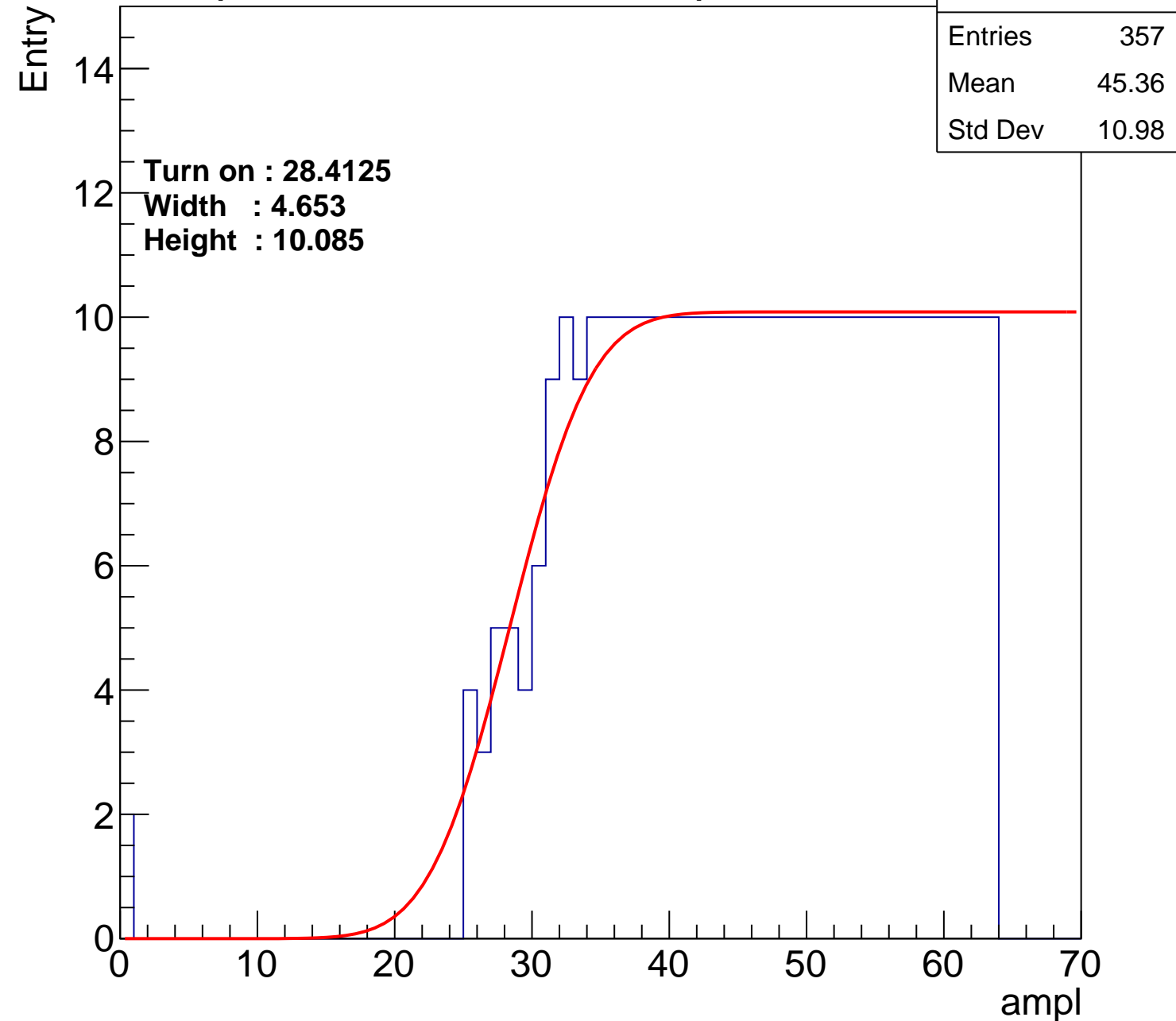
Width : 4.653

Height : 10.085

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch98

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.54
Std Dev	10.91

Turn on : 29.0468

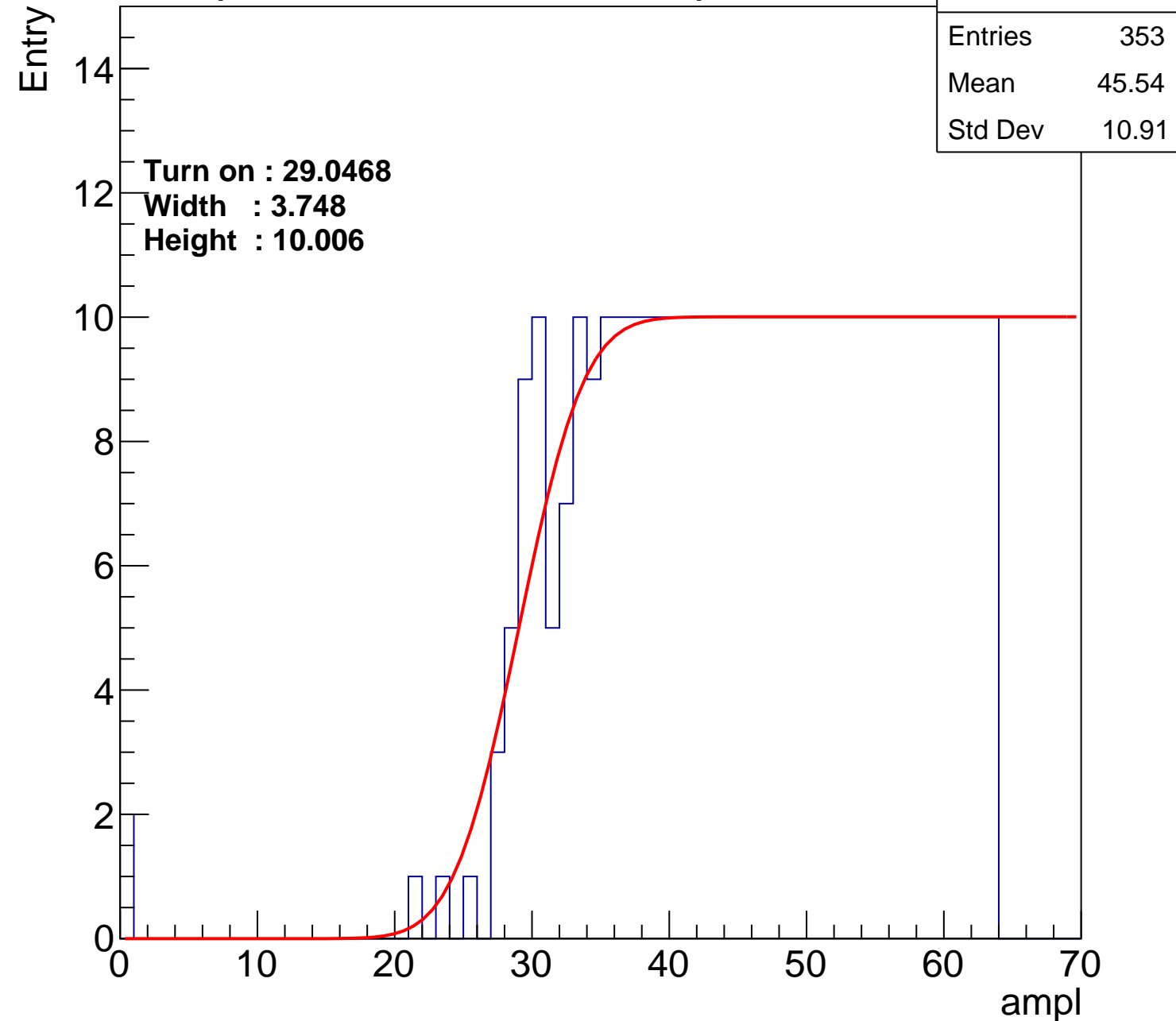
Width : 3.748

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch99

calib\_packv5\_042523\_0143.root, FC#8, port C1

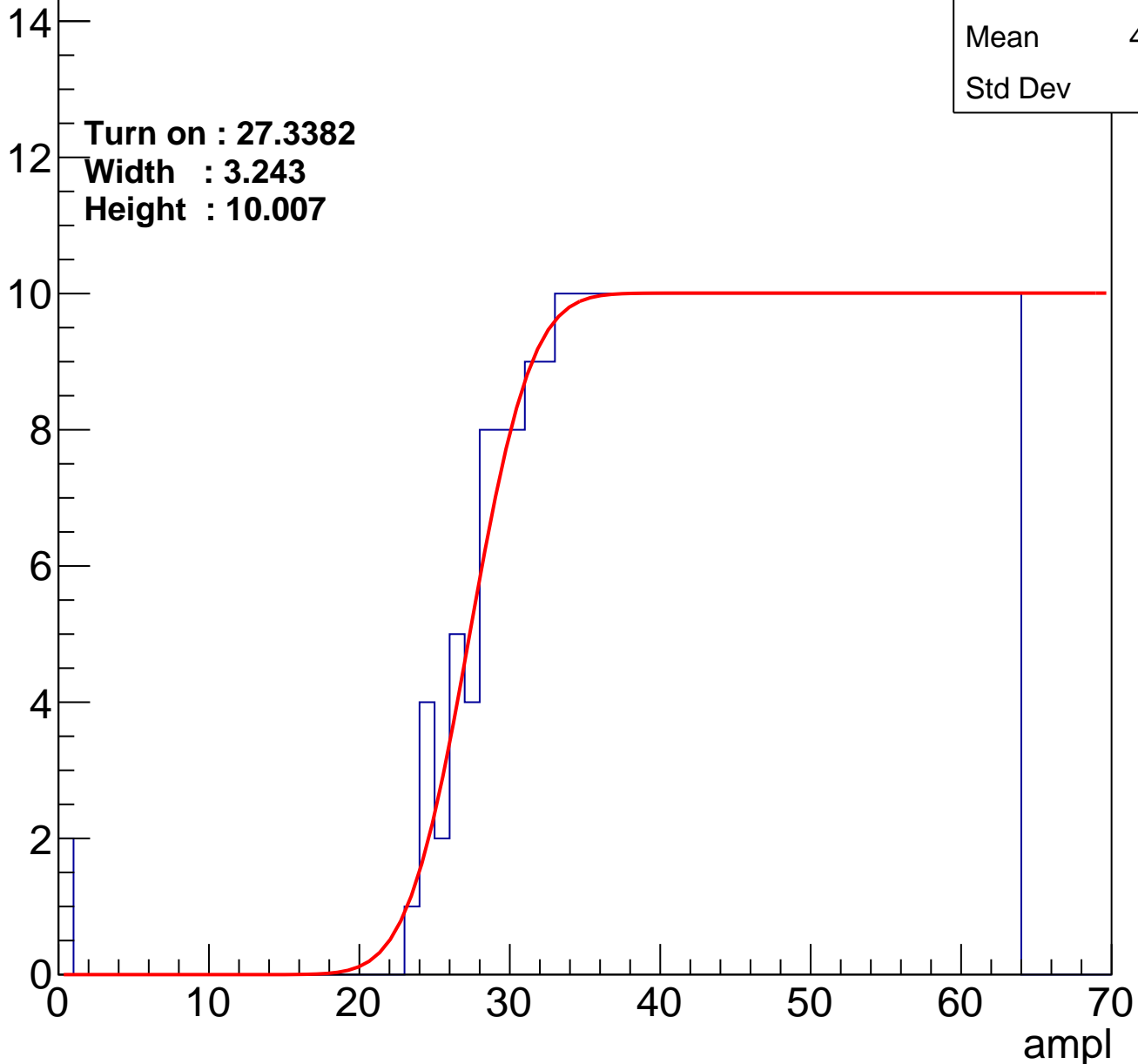
Entries	370
Mean	44.73
Std Dev	11.3

Turn on : 27.3382

Width : 3.243

Height : 10.007

Entry



# B0L002S, U25-ch100

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	381
Mean	44.22
Std Dev	11.53

**Turn on : 25.7687**

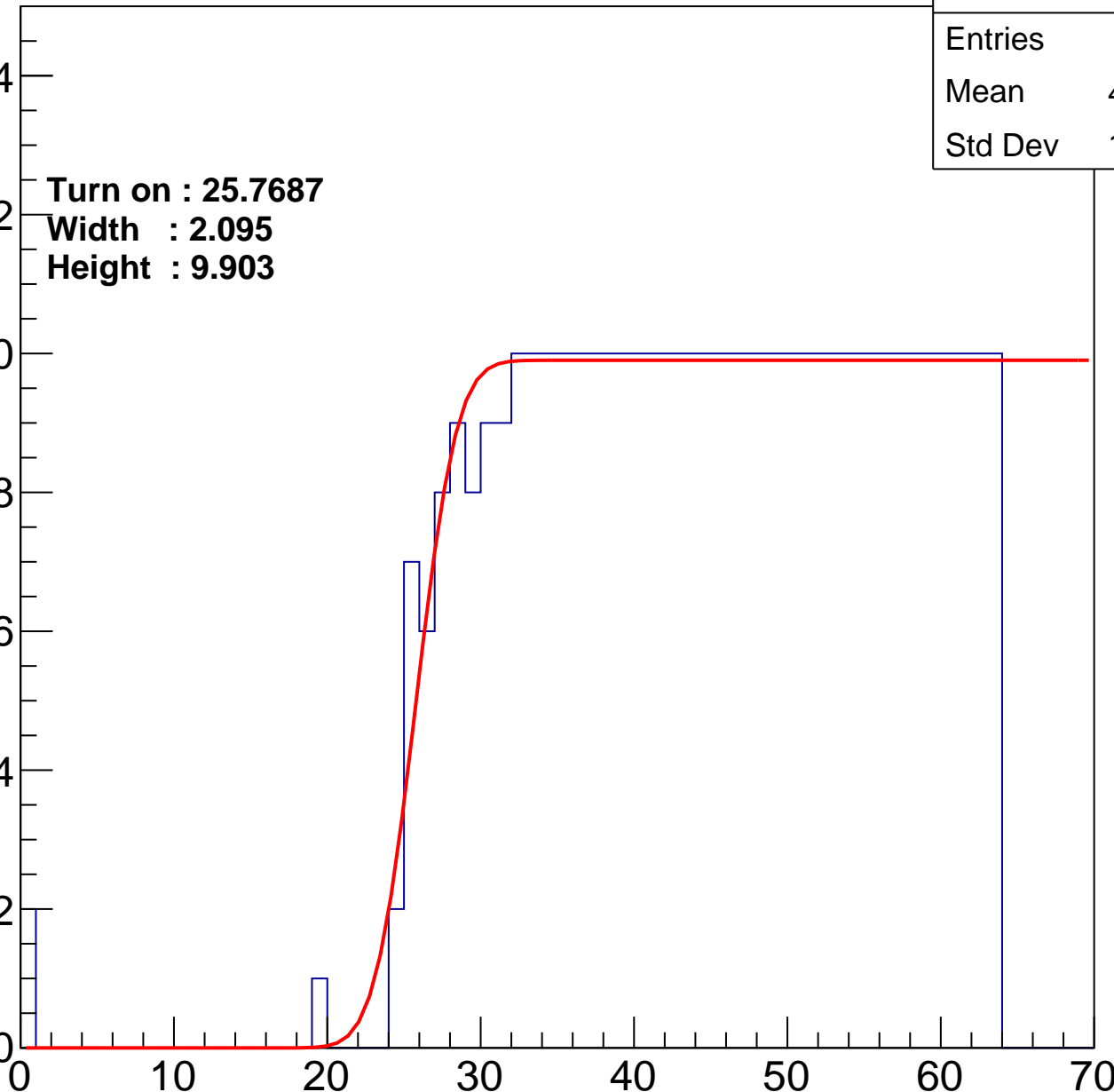
**Width : 2.095**

**Height : 9.903**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch101

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.56
Std Dev	10.88

Turn on : 28.7202

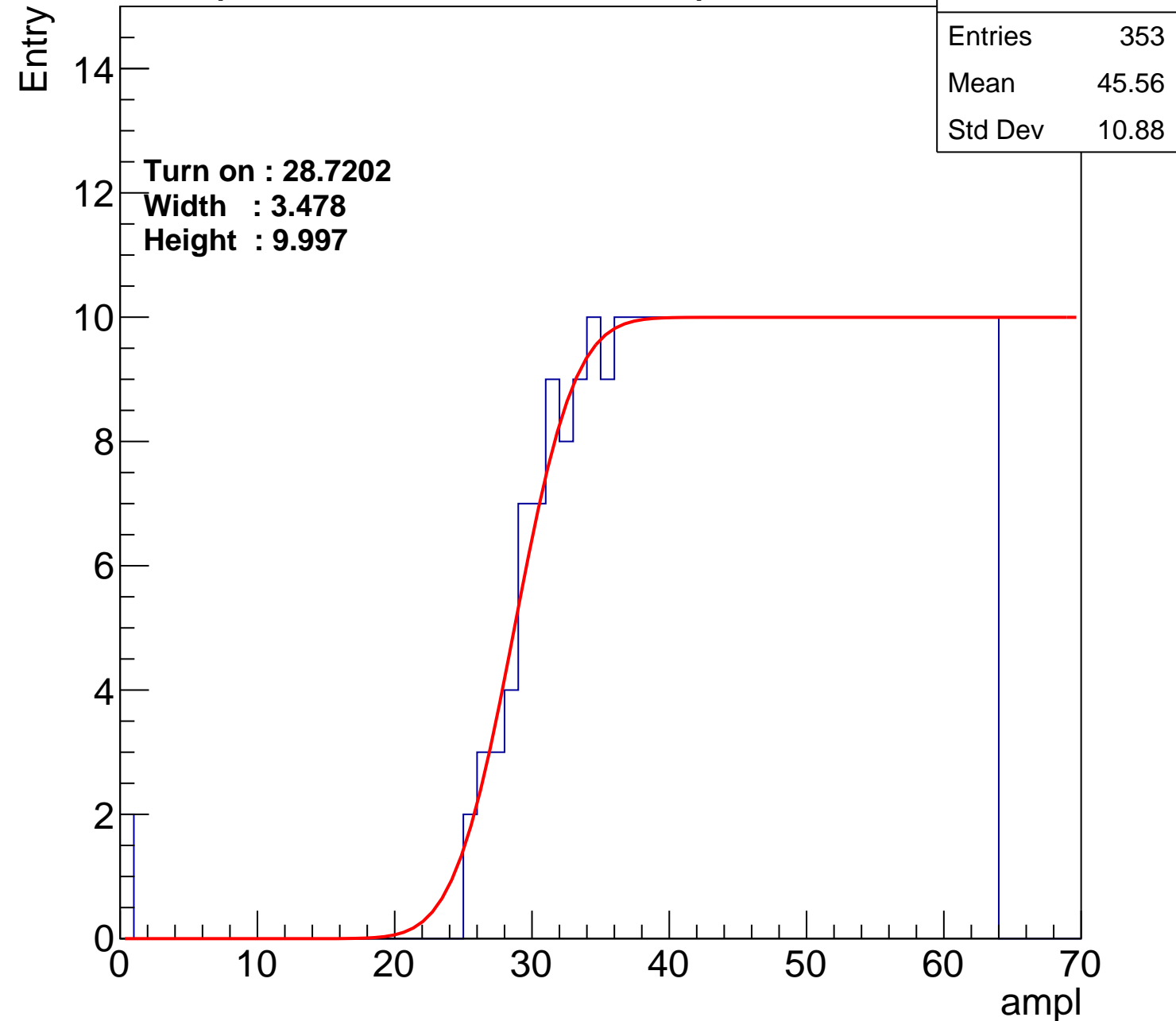
Width : 3.478

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch102

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	341
Mean	46.3
Std Dev	10.21

Turn on : 30.2874

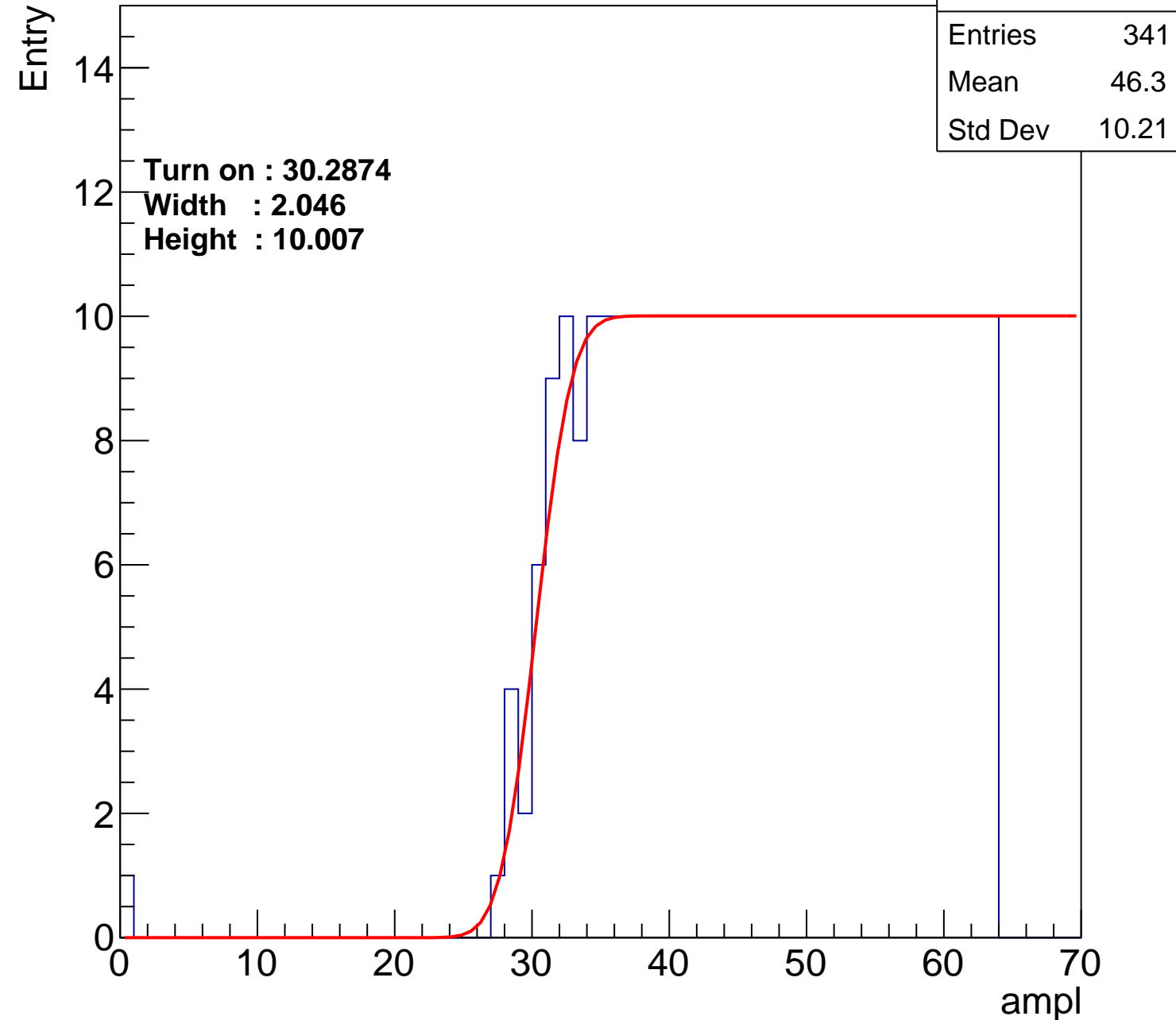
Width : 2.046

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch103

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.96
Std Dev	11.14

**Turn on : 27.8419**

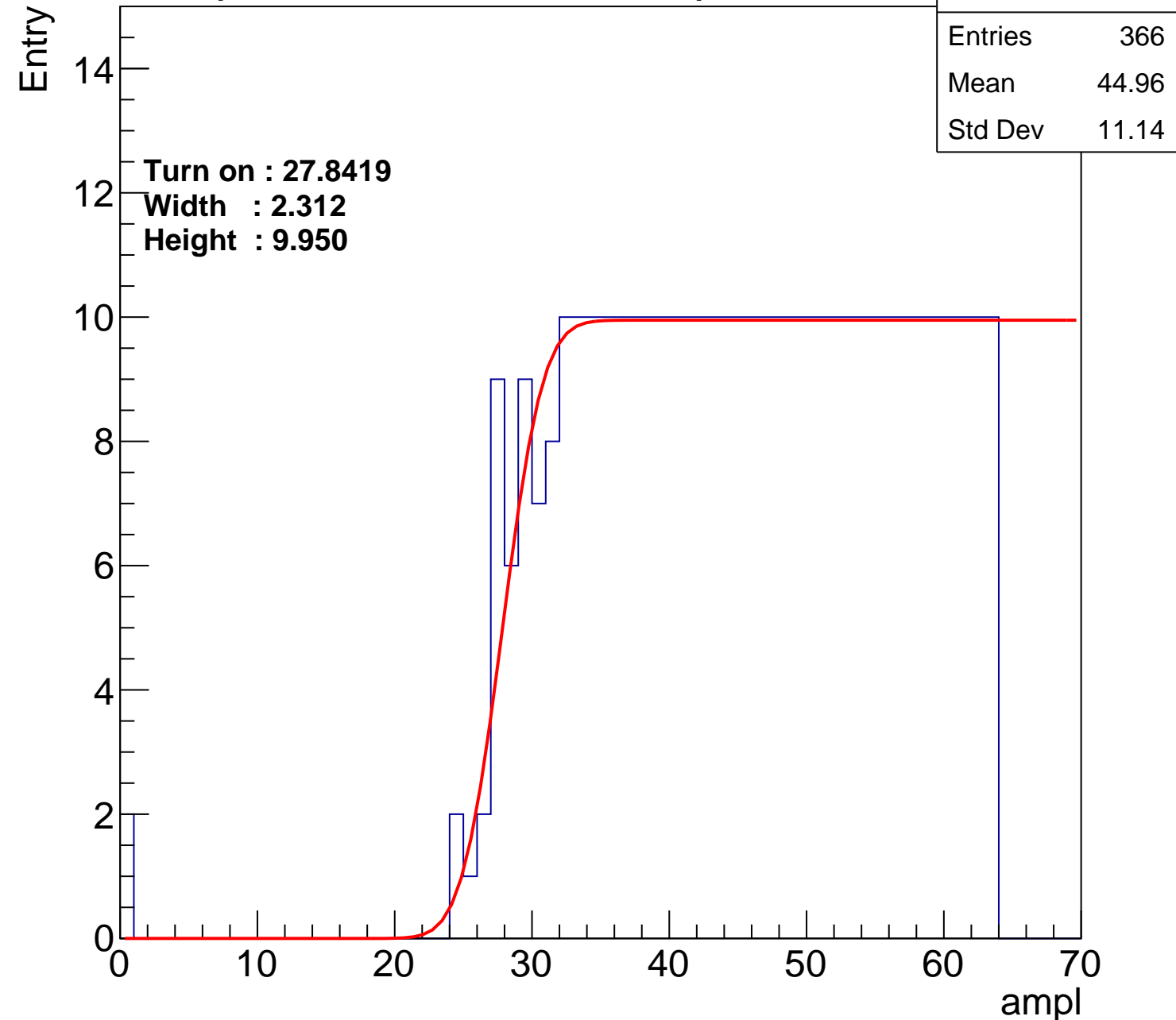
**Width : 2.312**

**Height : 9.950**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch104

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.64
Std Dev	11.38

**Turn on : 26.8982**

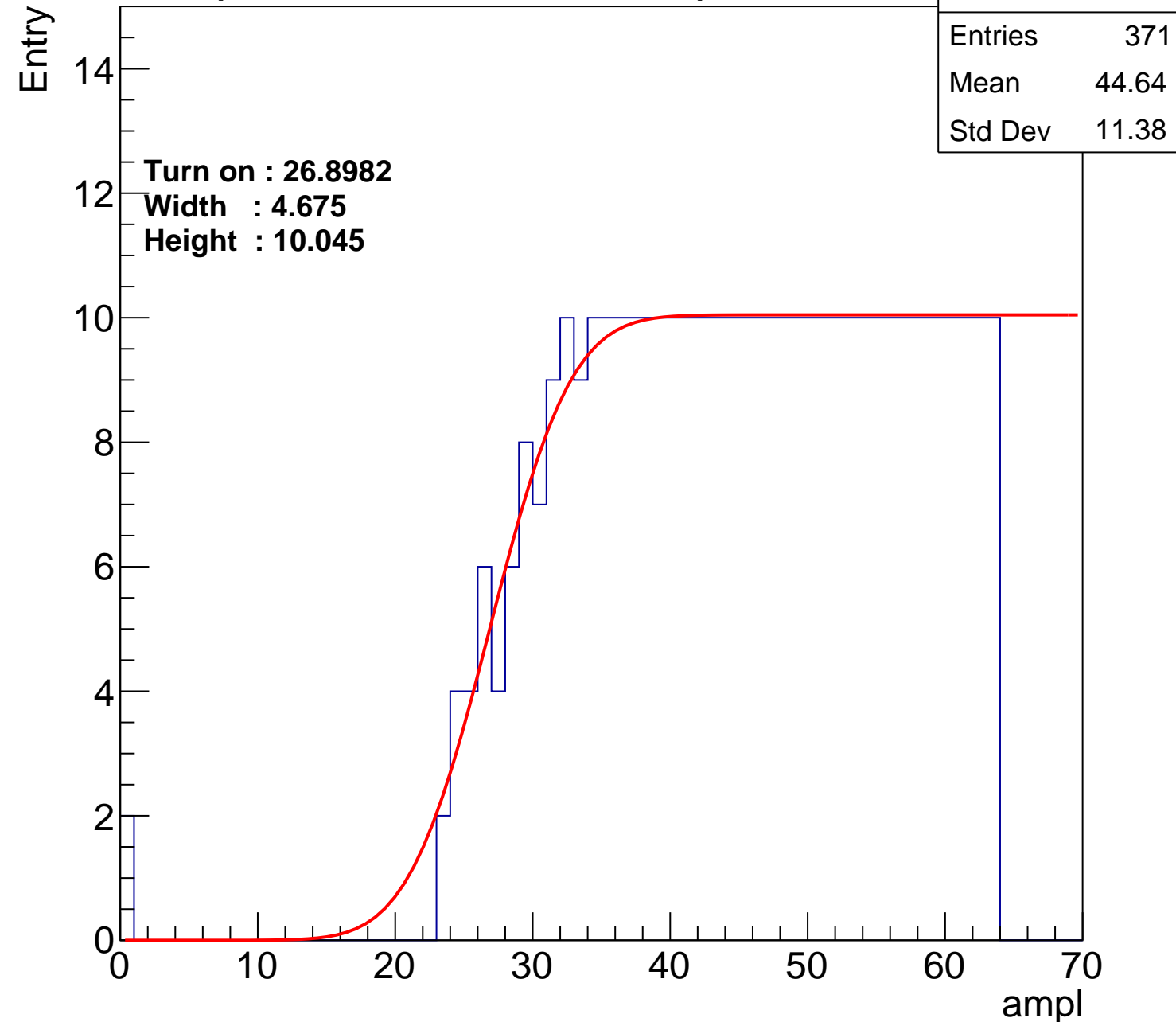
**Width : 4.675**

**Height : 10.045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch105

calib\_packv5\_042523\_0143.root, FC#8, port C1

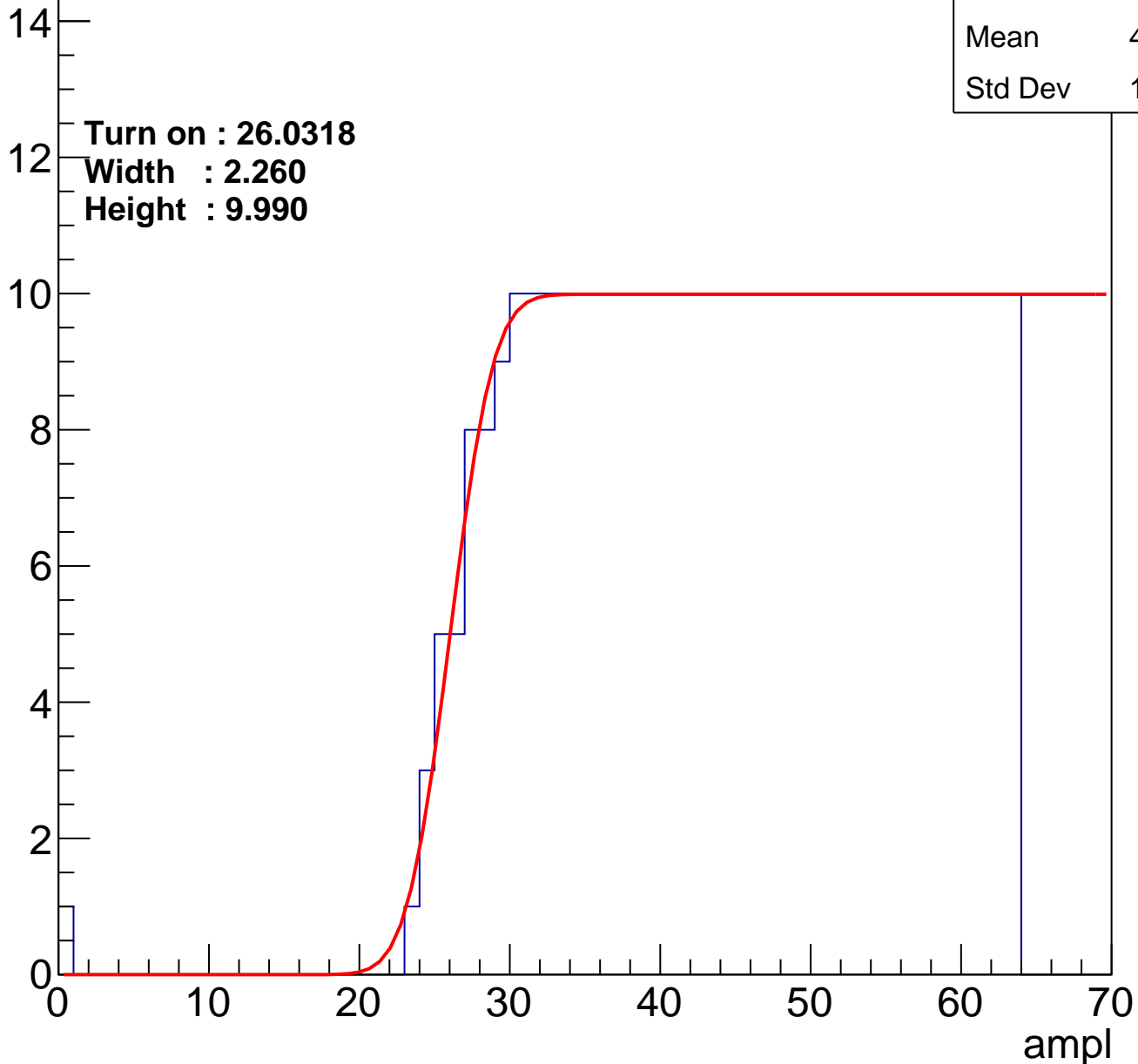
Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 26.0318

Width : 2.260

Height : 9.990

Entry



# B0L002S, U25-ch106

calib\_packv5\_042523\_0143.root, FC#8, port C1

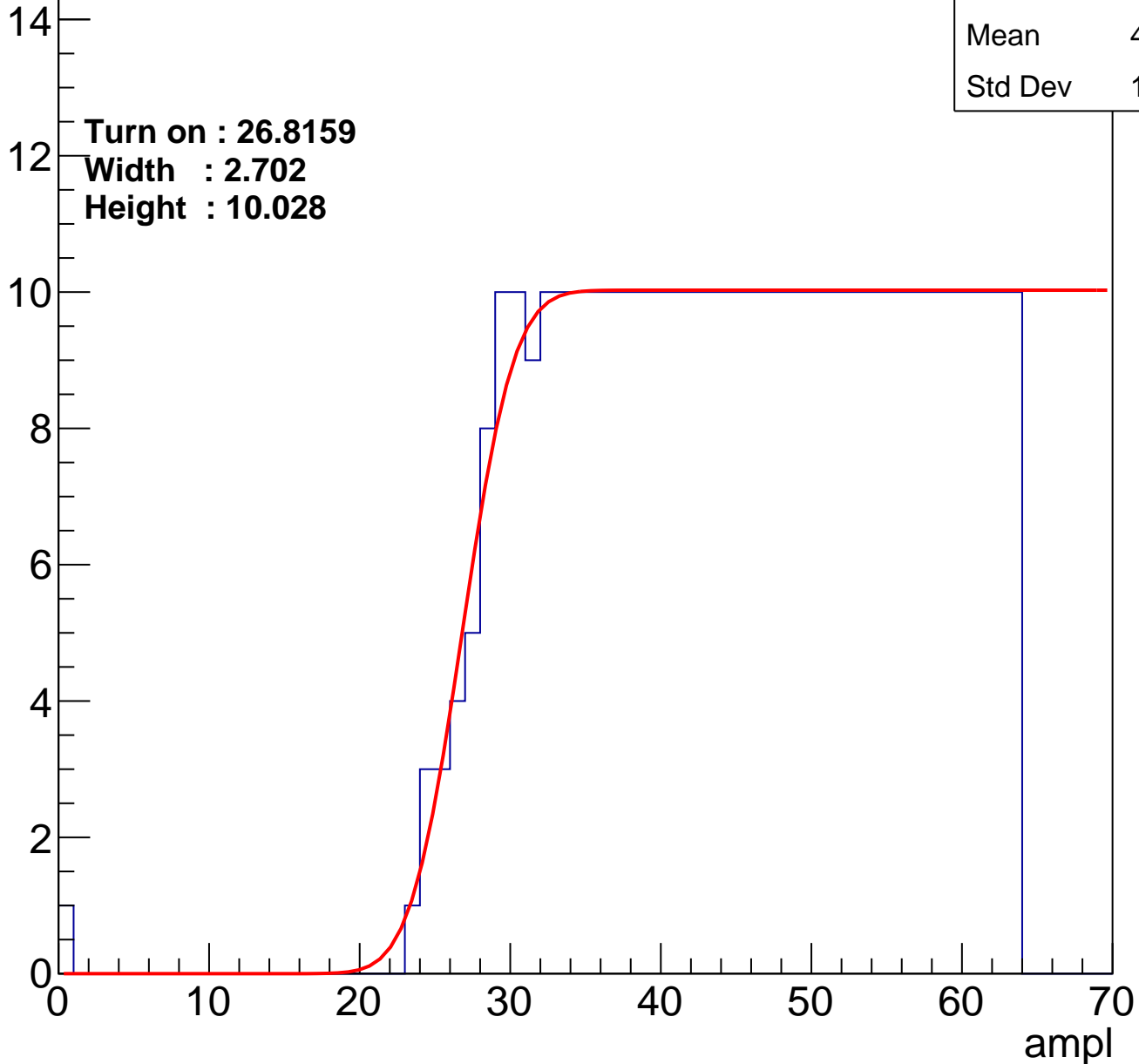
Entries	374
Mean	44.66
Std Dev	11.12

**Turn on : 26.8159**

**Width : 2.702**

**Height : 10.028**

Entry



# B0L002S, U25-ch107

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.06
Std Dev	11.09

**Turn on : 27.9386**

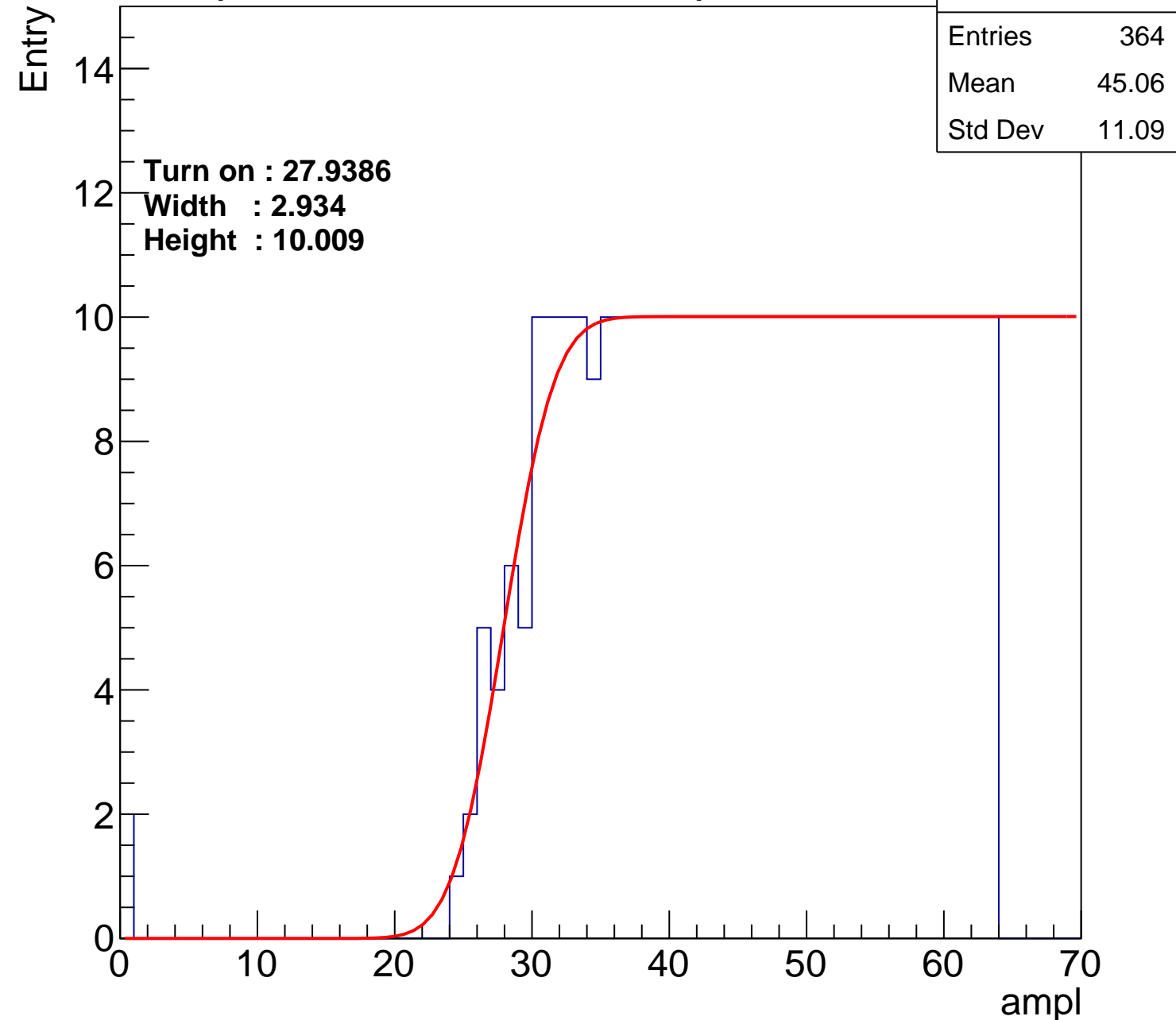
**Width : 2.934**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch108

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.43
Std Dev	10.88

**Turn on : 28.9795**

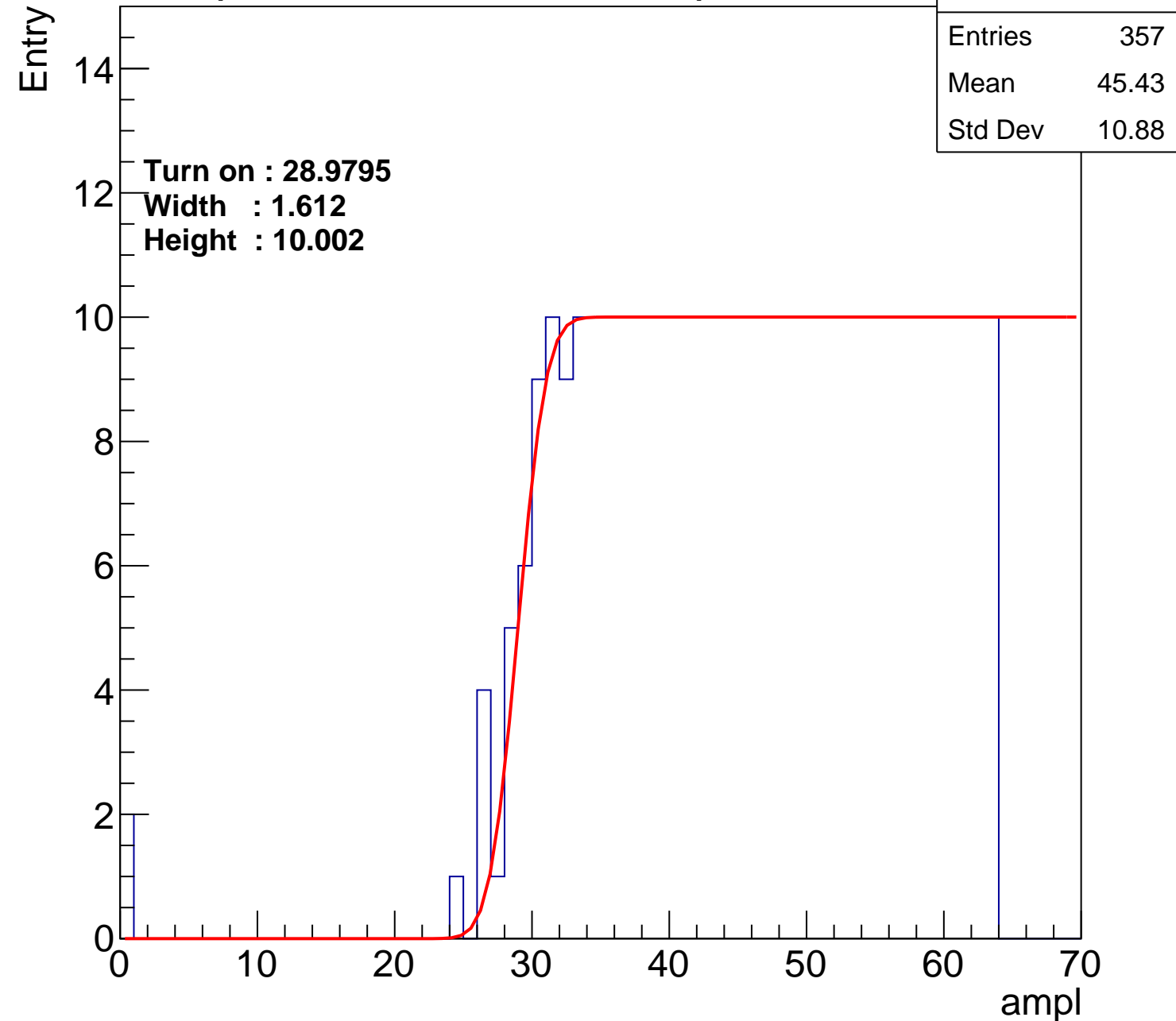
**Width : 1.612**

**Height : 10.002**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch109

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.25
Std Dev	10.8

Turn on : 28.0119

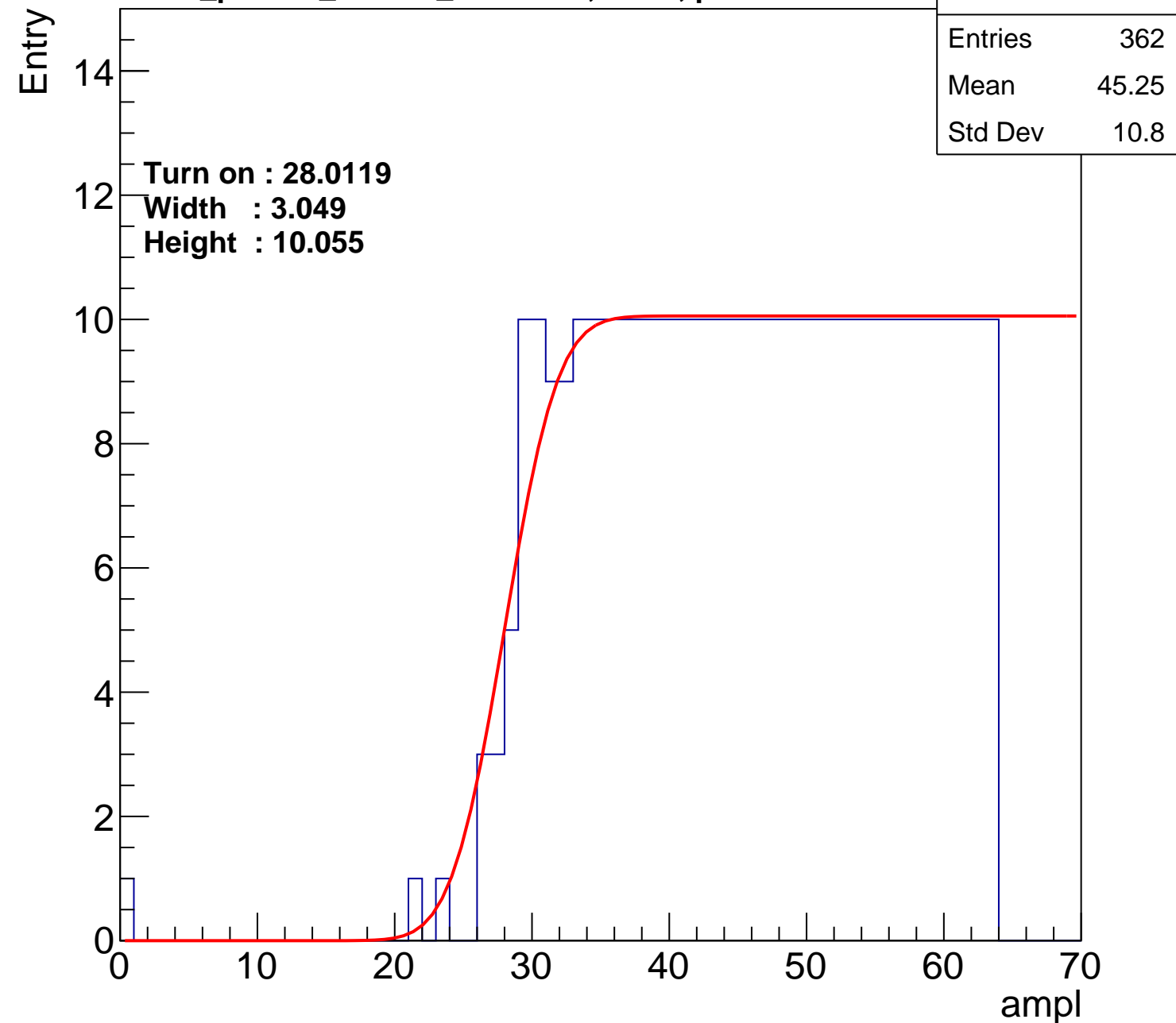
Width : 3.049

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch110

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.86
Std Dev	11.38

Turn on : 27.8079

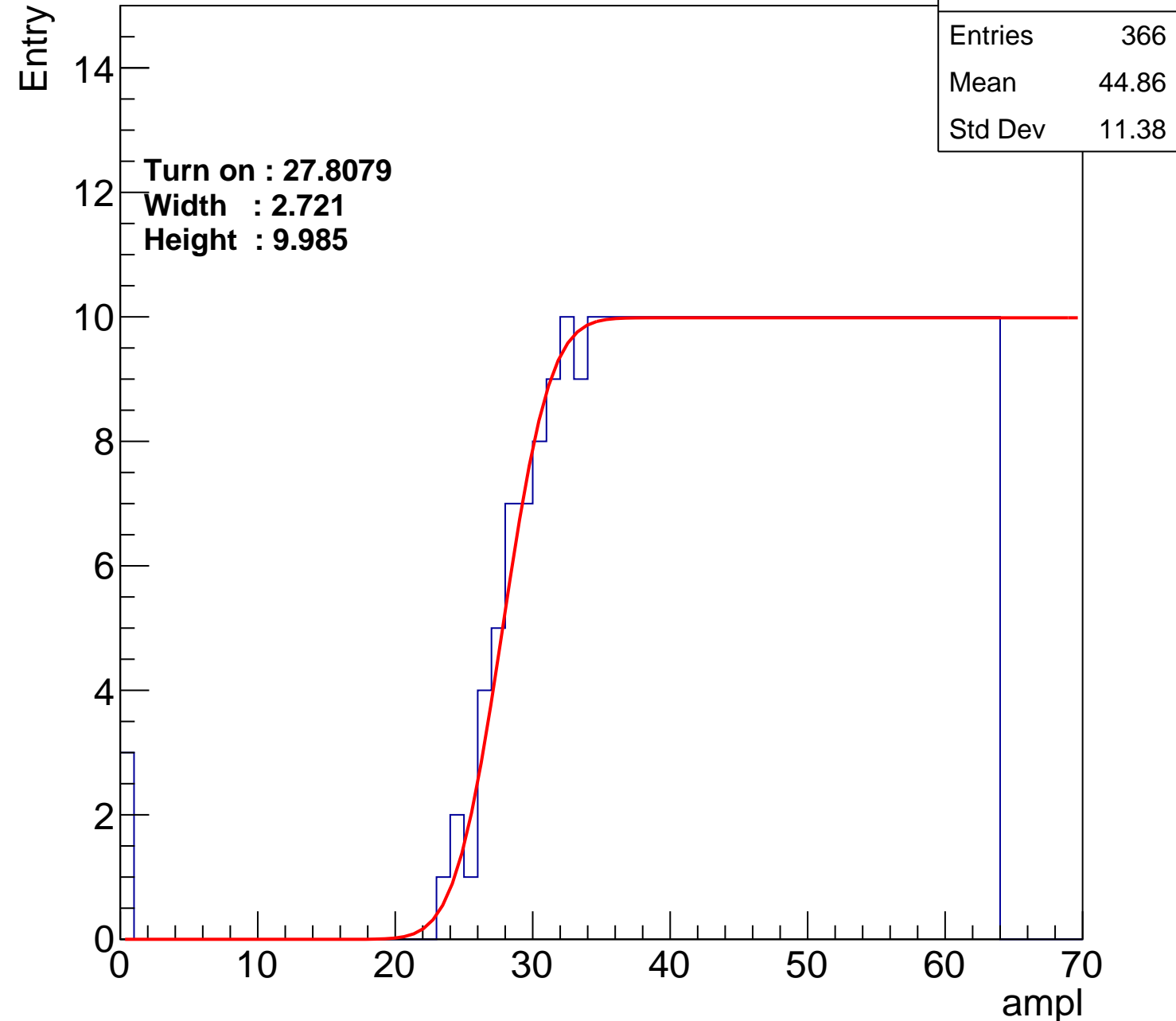
Width : 2.721

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch111

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.14
Std Dev	11.28

Turn on : 28.4512

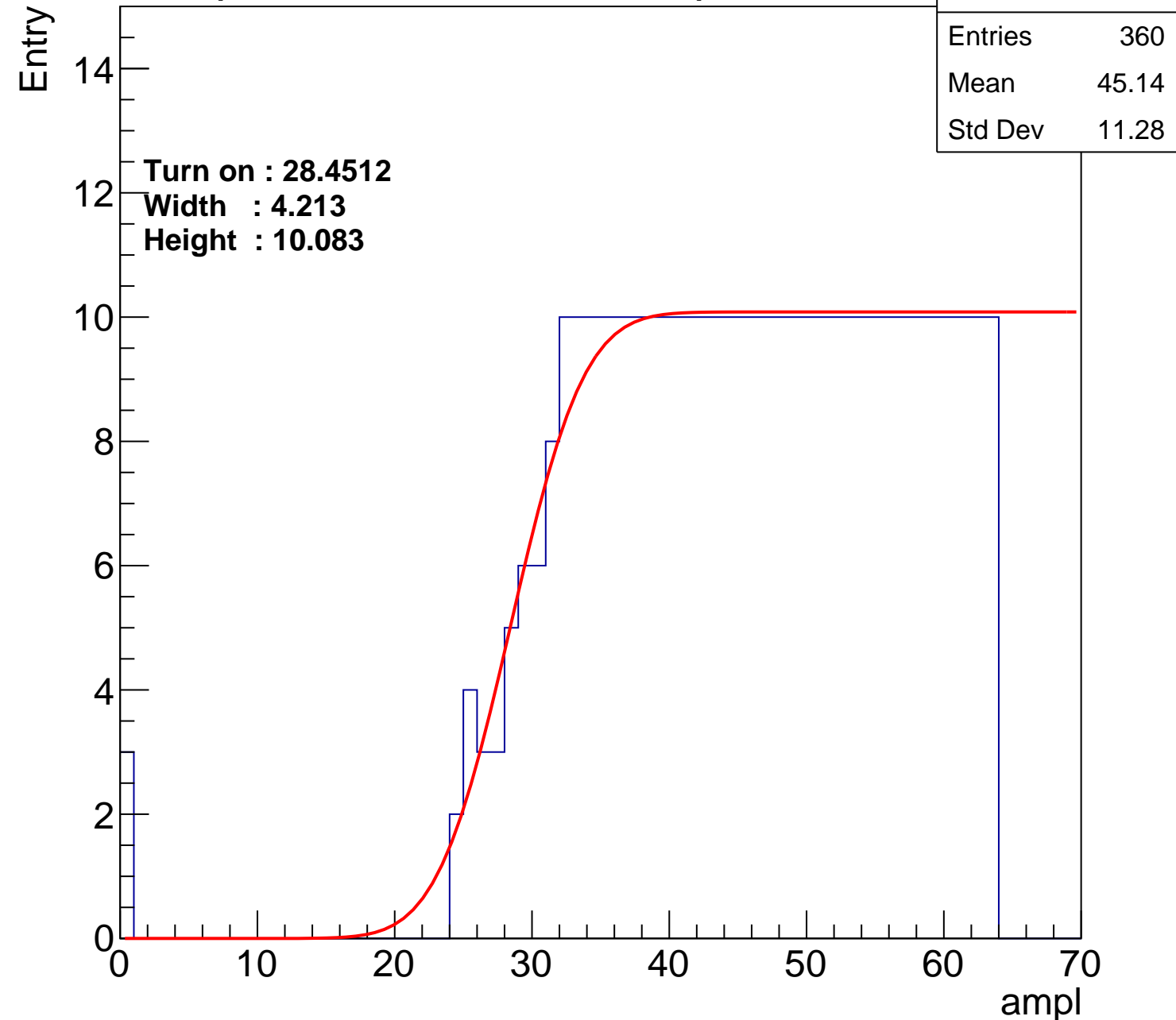
Width : 4.213

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch112

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.95
Std Dev	11.03

Turn on : 27.8292

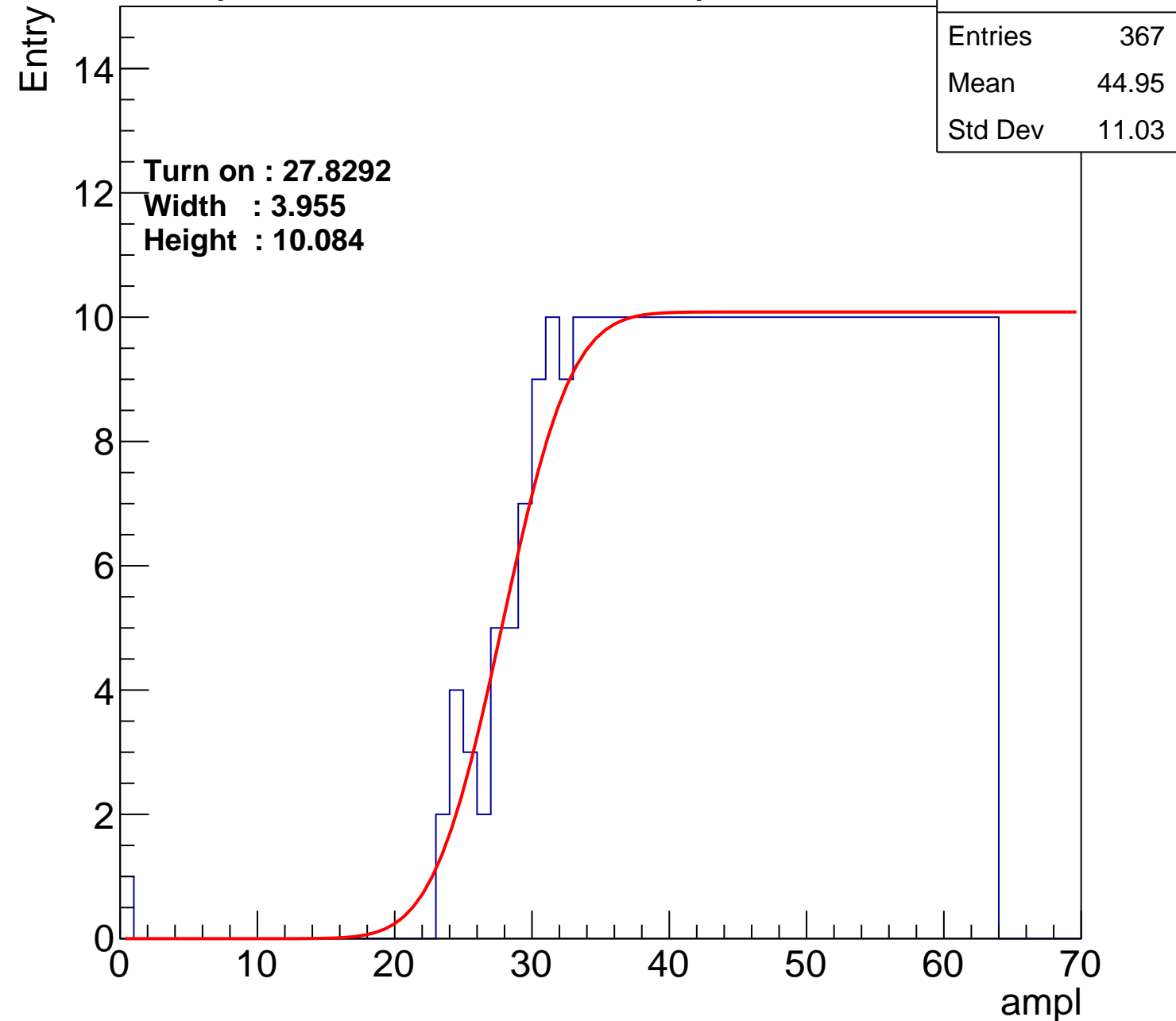
Width : 3.955

Height : 10.084

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch113

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	351
Mean	45.69
Std Dev	10.68

Turn on : 30.4229

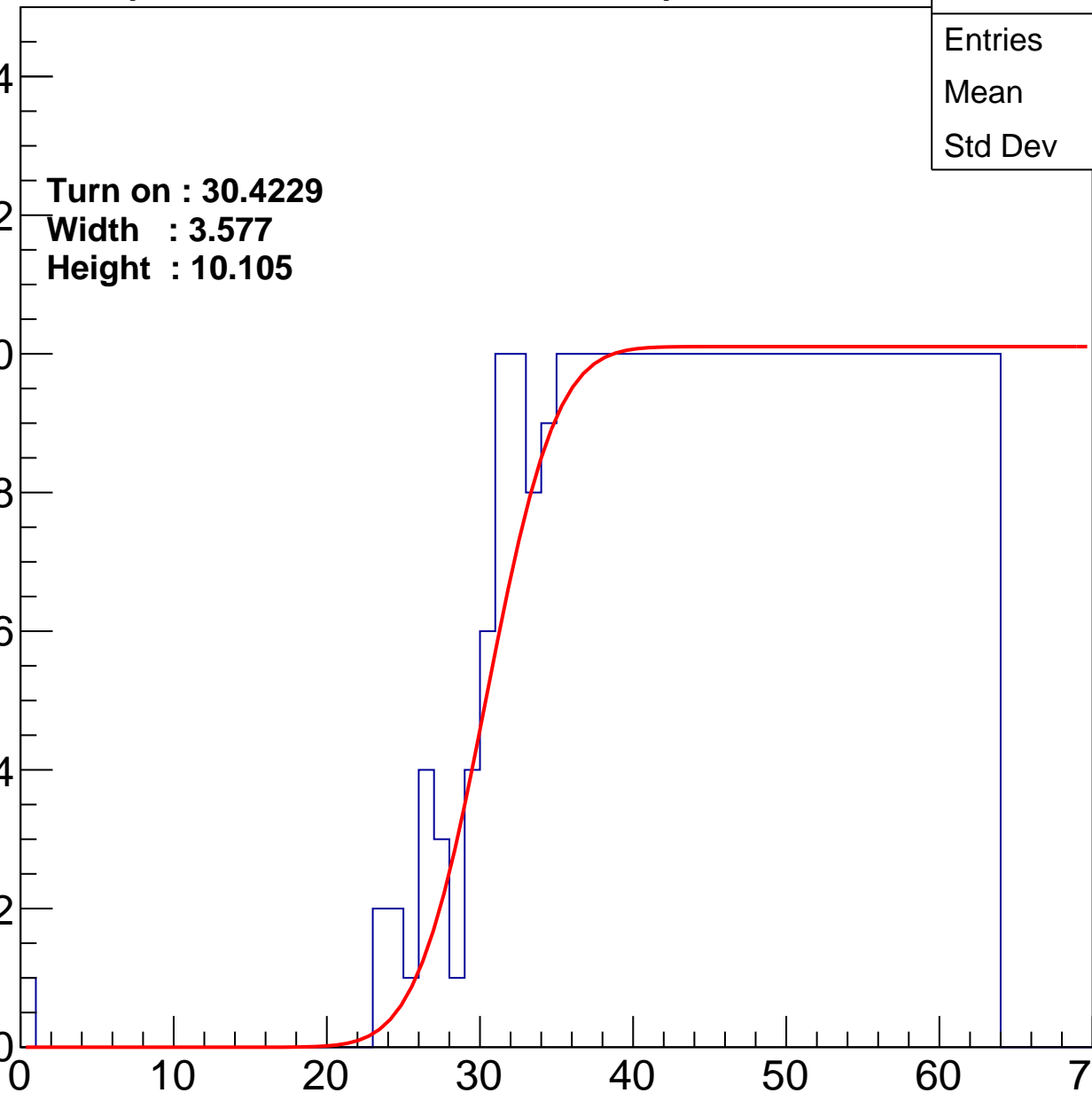
Width : 3.577

Height : 10.105

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch114

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 27.9946

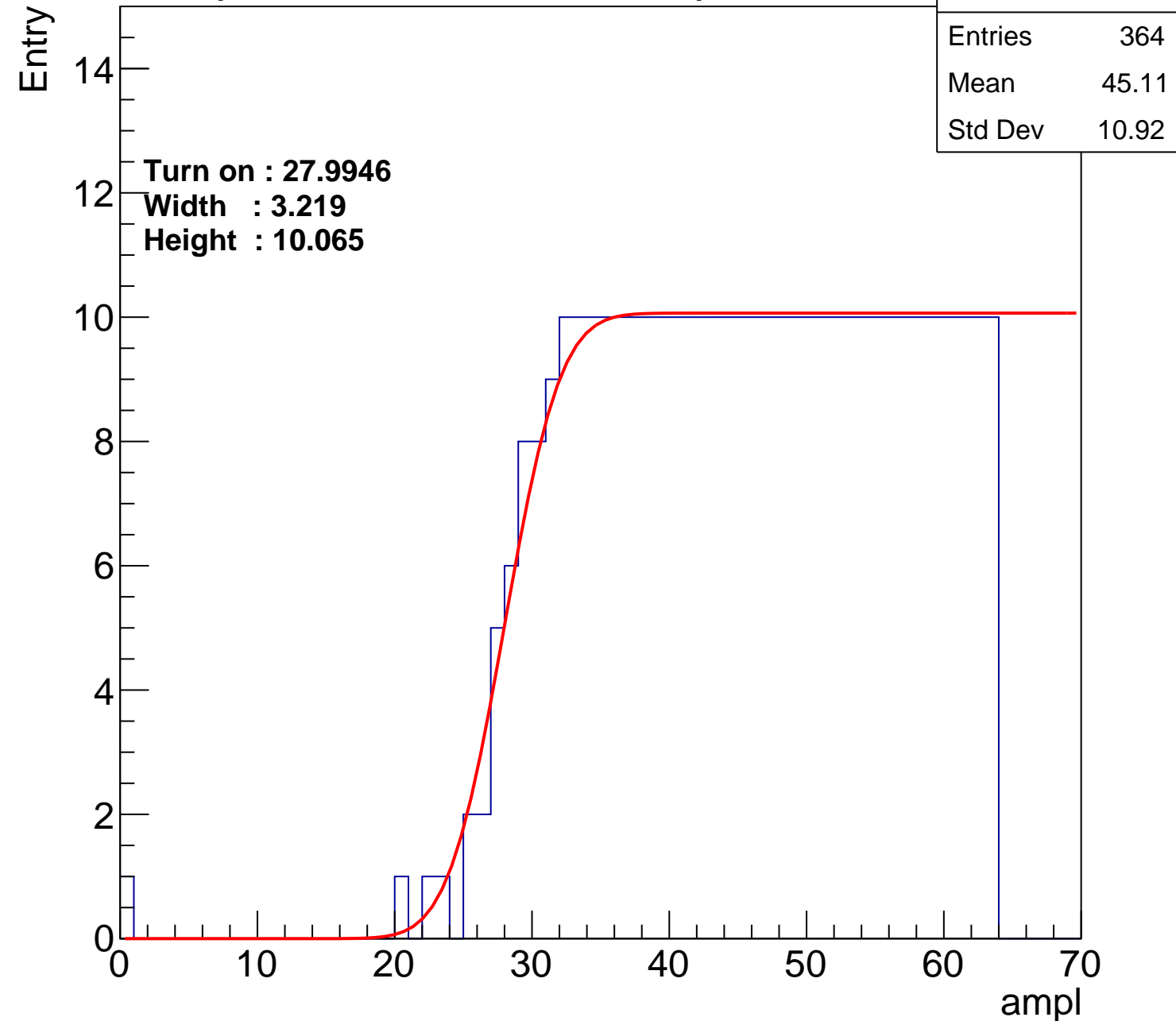
Width : 3.219

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch115

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.01
Std Dev	11.36

Turn on : 28.1487

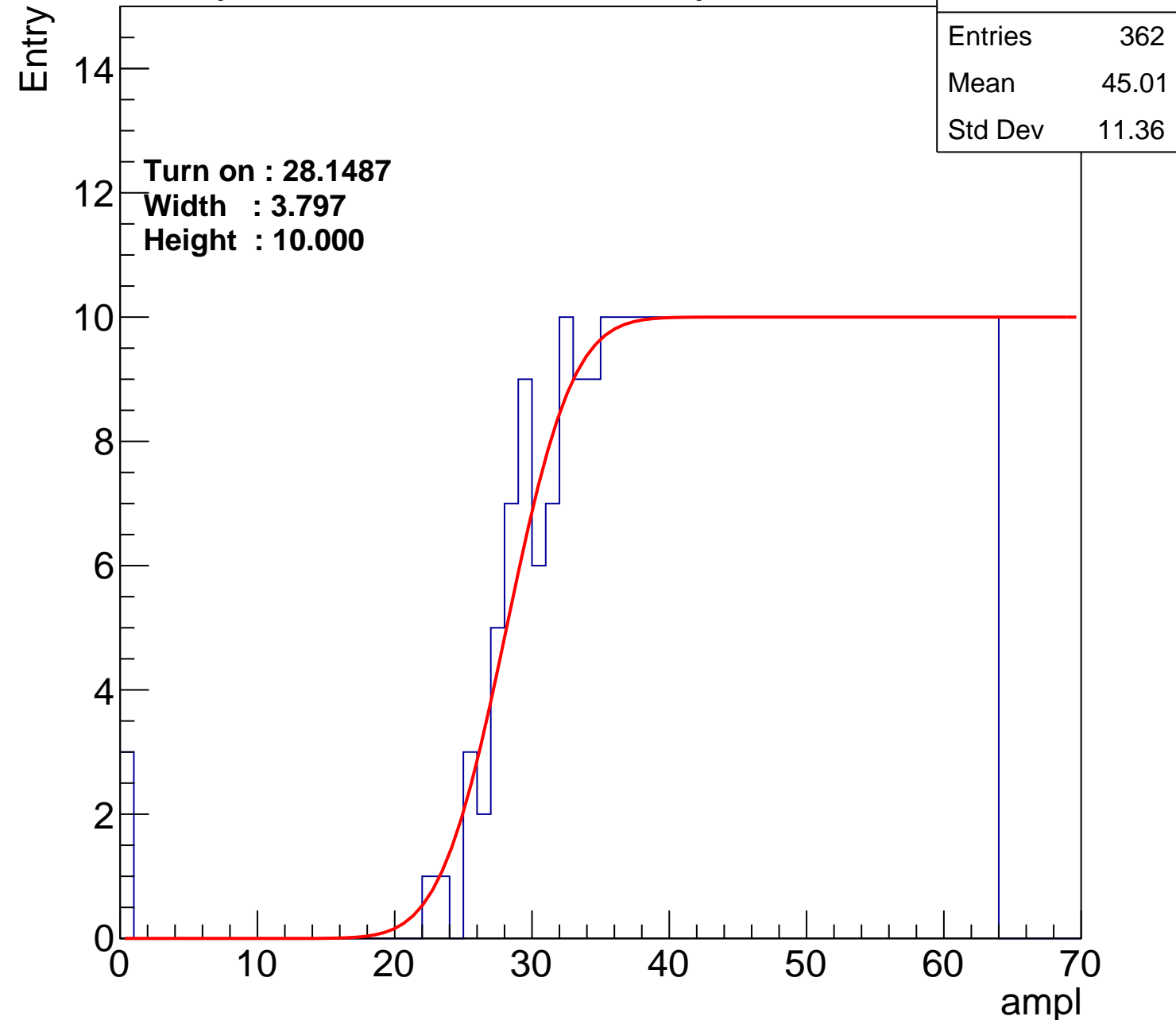
Width : 3.797

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch116

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.18
Std Dev	11.01

Turn on : 28.2799

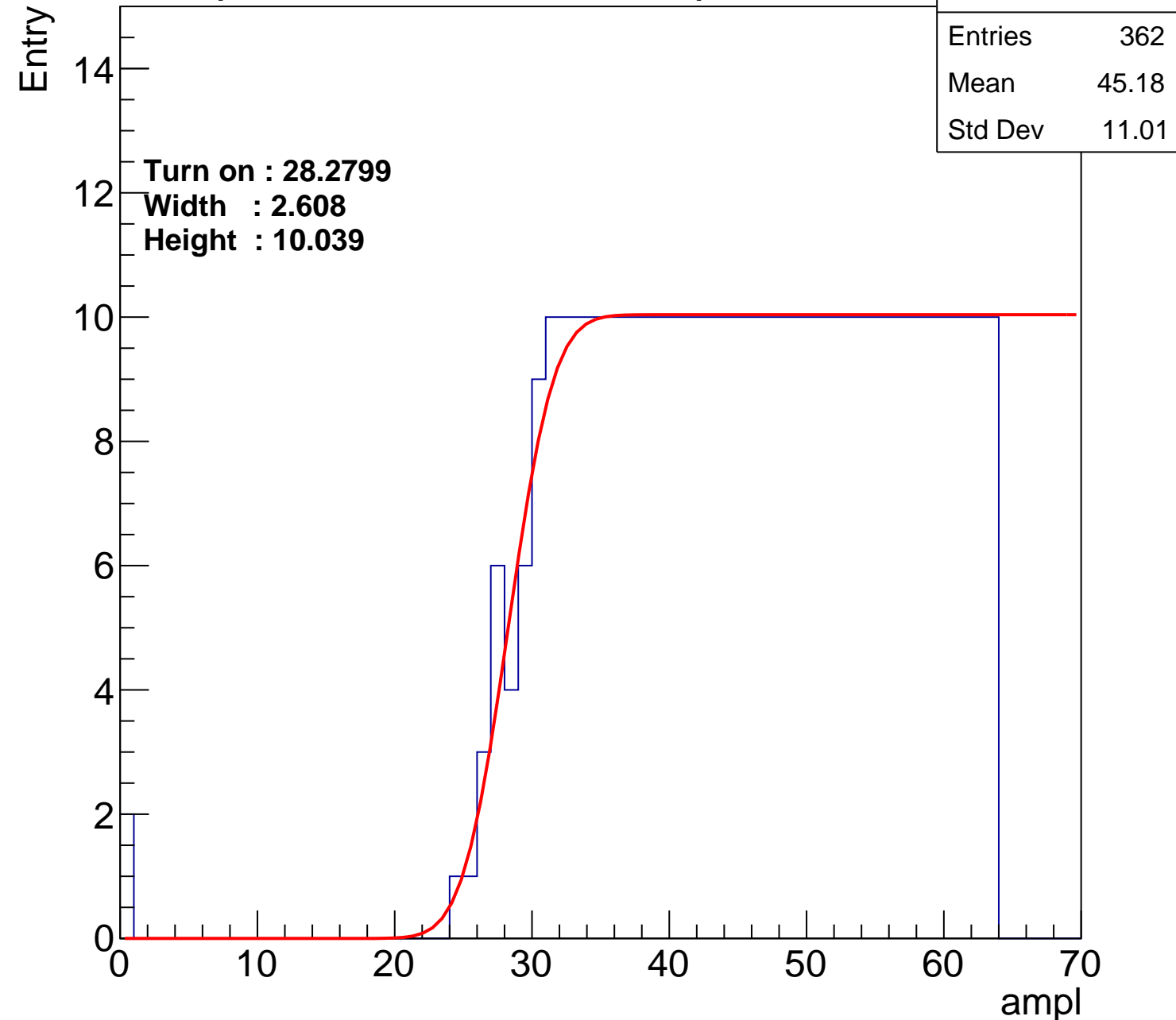
Width : 2.608

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch117

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.26
Std Dev	11.51

**Turn on : 26.2357**

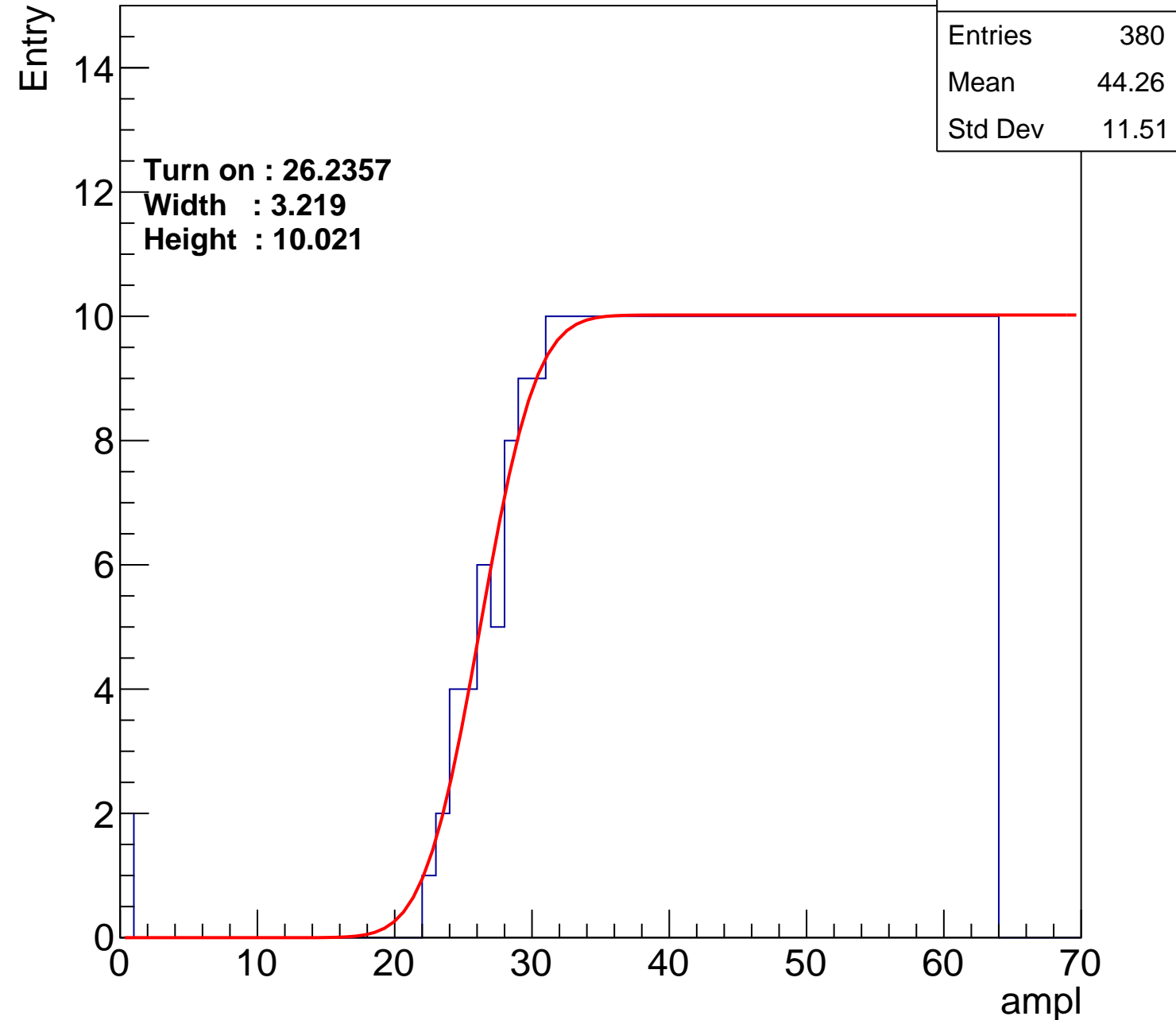
**Width : 3.219**

**Height : 10.021**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch118

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	379
Mean	44.39
Std Dev	11.3

Turn on : 26.7762

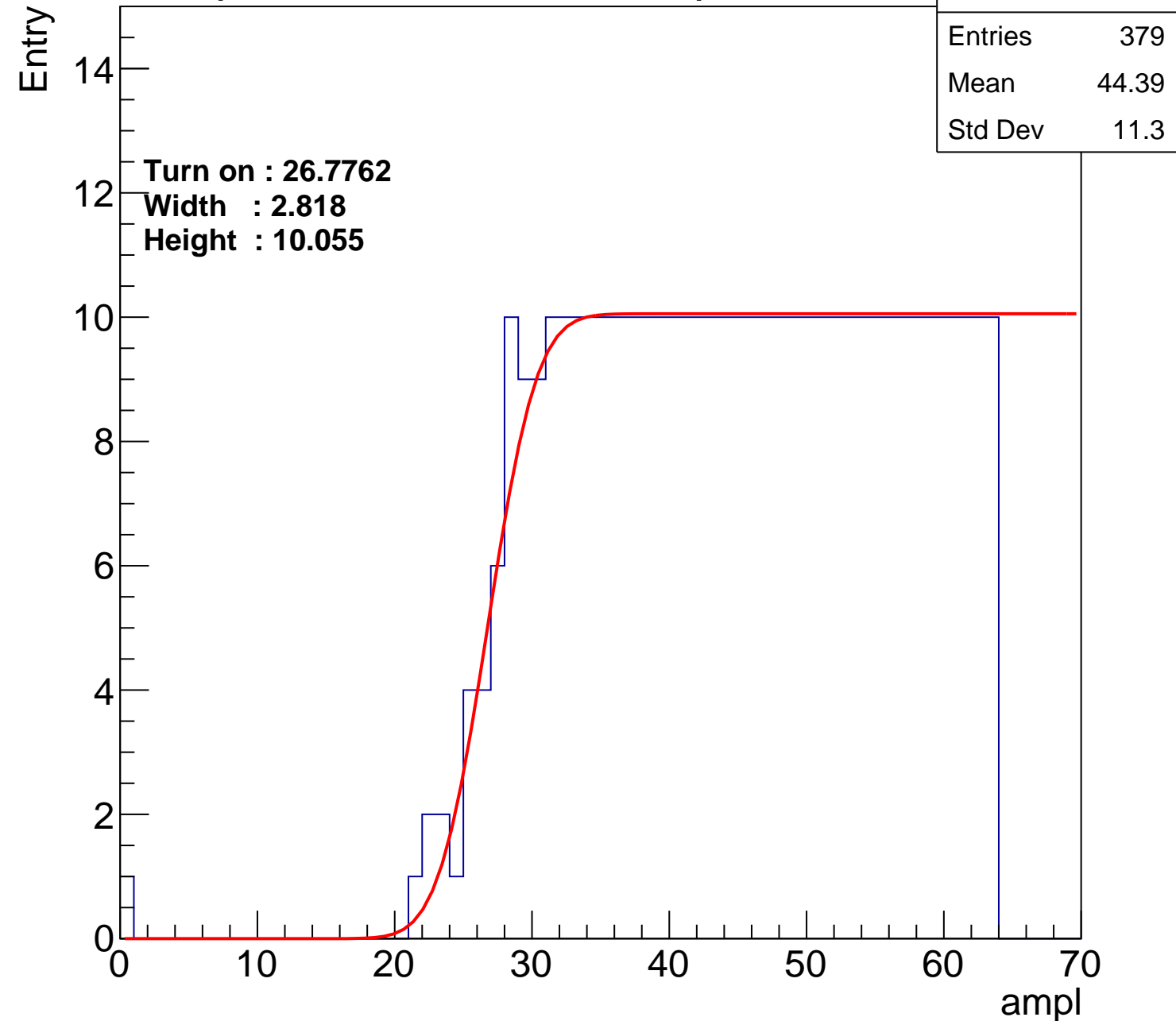
Width : 2.818

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U25-ch119

calib\_packv5\_042523\_0143.root, FC#8, port C1

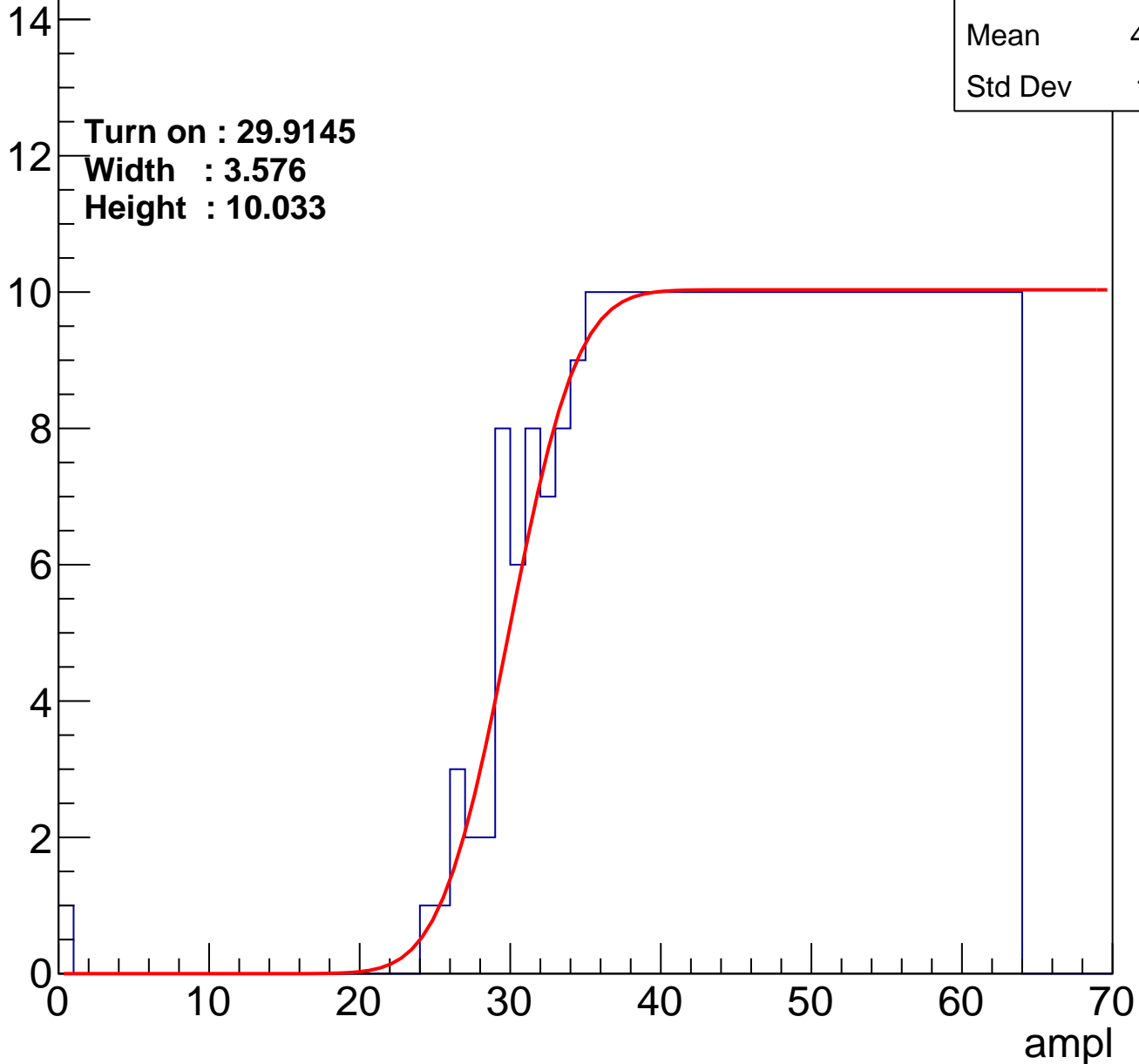
Entries	346
Mean	45.96
Std Dev	10.51

Turn on : 29.9145

Width : 3.576

Height : 10.033

Entry



# B0L002S, U25-ch120

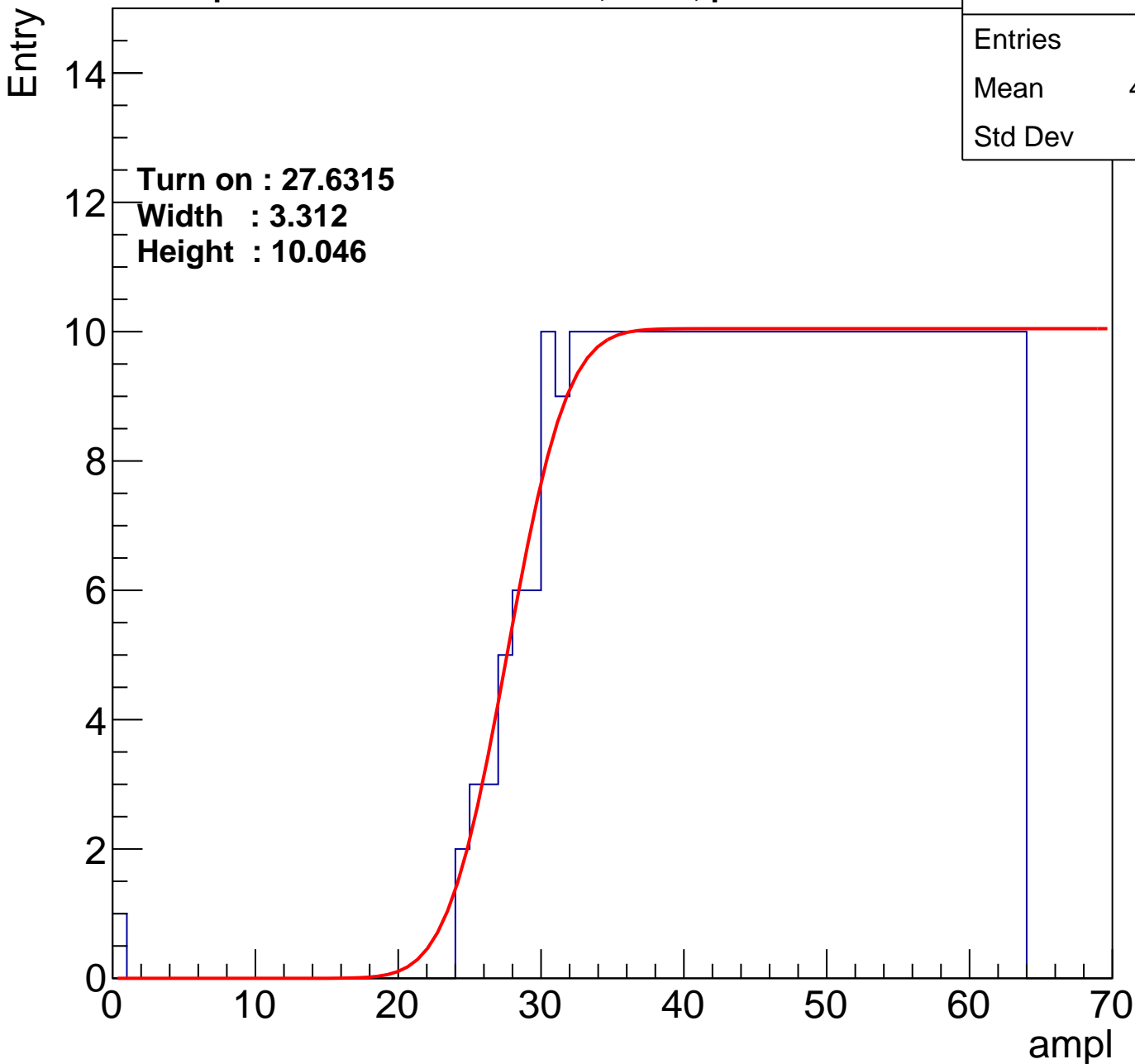
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	365
Mean	45.09
Std Dev	10.9

**Turn on : 27.6315**

**Width : 3.312**

**Height : 10.046**



# B0L002S, U25-ch121

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	344
Mean	46.1
Std Dev	10.38

**Turn on : 29.8466**

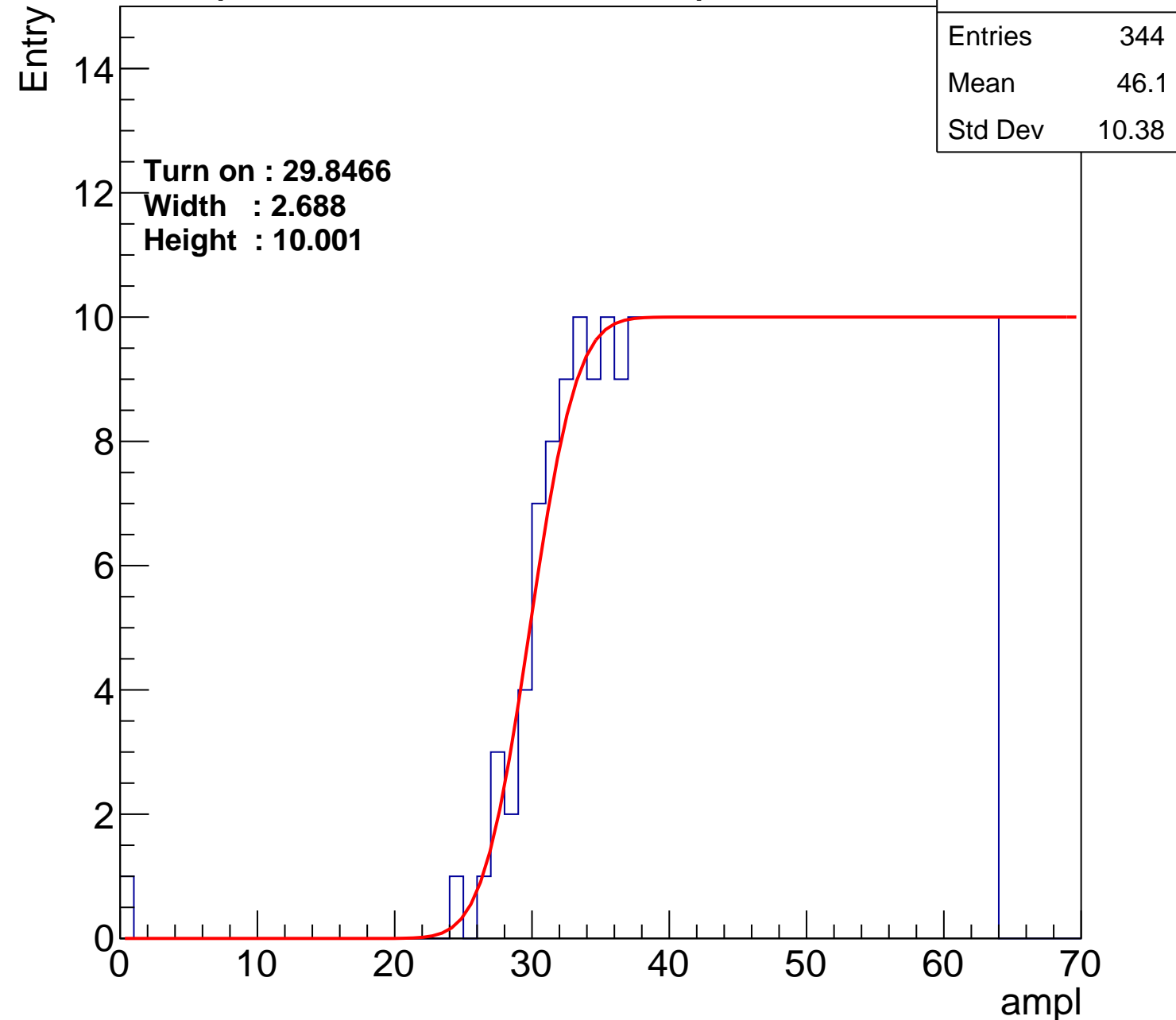
**Width : 2.688**

**Height : 10.001**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch122

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	396
Mean	43.38
Std Dev	12.13

Turn on : 24.8659

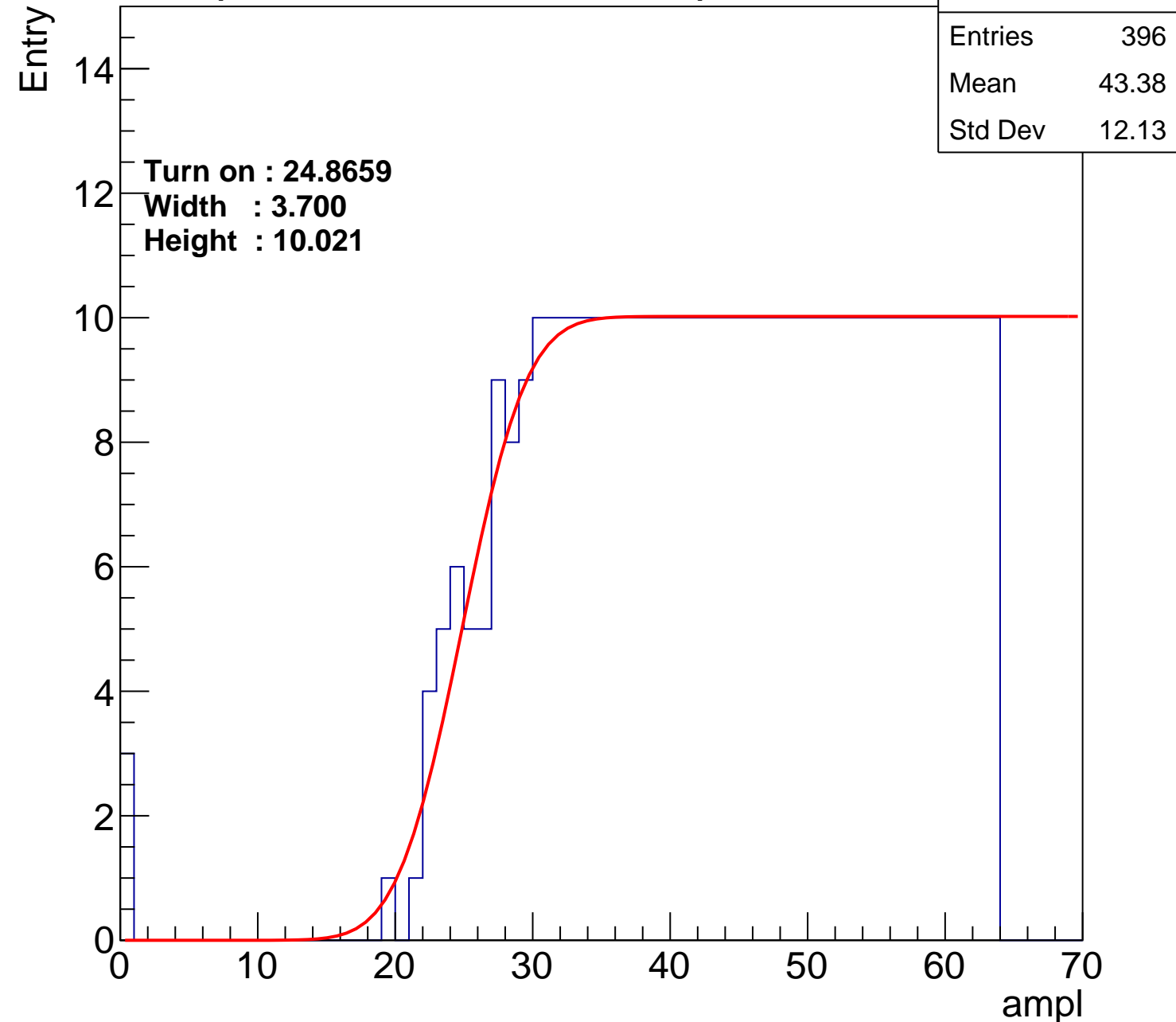
Width : 3.700

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch123

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.08
Std Dev	11.06

**Turn on : 27.6892**

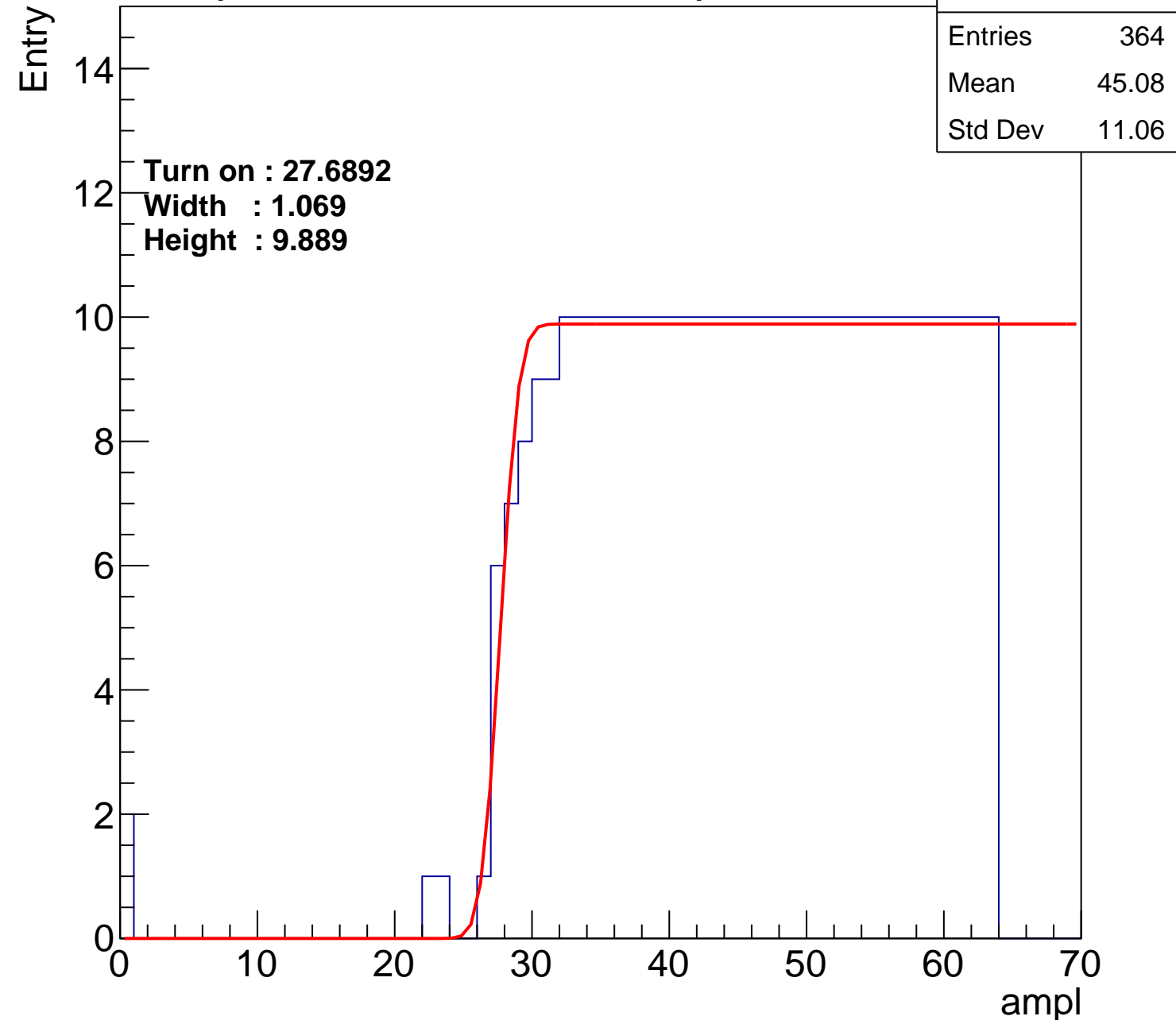
**Width : 1.069**

**Height : 9.889**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch124

calib\_packv5\_042523\_0143.root, FC#8, port C1

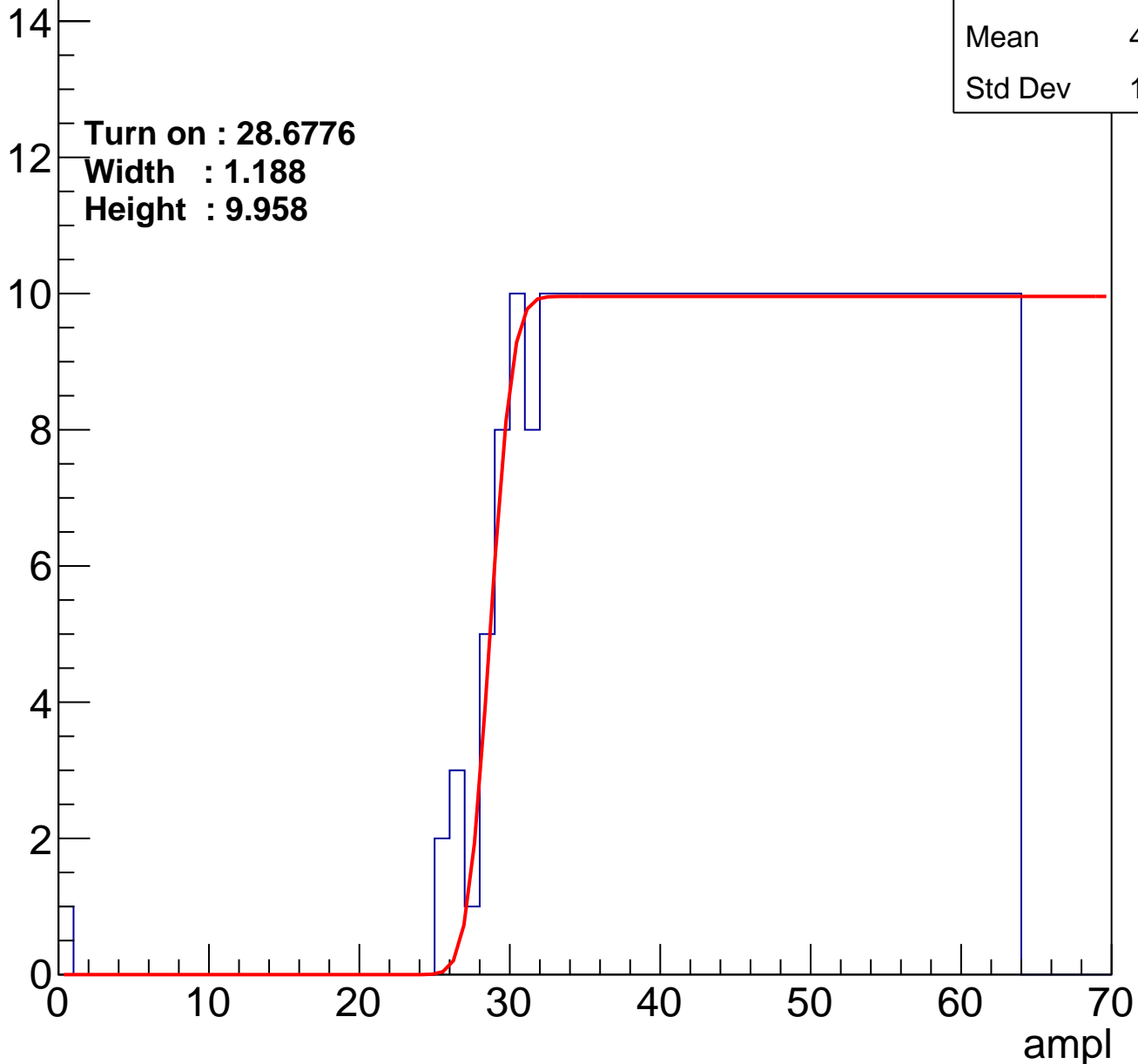
Entries	358
Mean	45.46
Std Dev	10.67

**Turn on : 28.6776**

**Width : 1.188**

**Height : 9.958**

Entry



# B0L002S, U25-ch125

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.59
Std Dev	10.83

**Turn on : 29.6779**

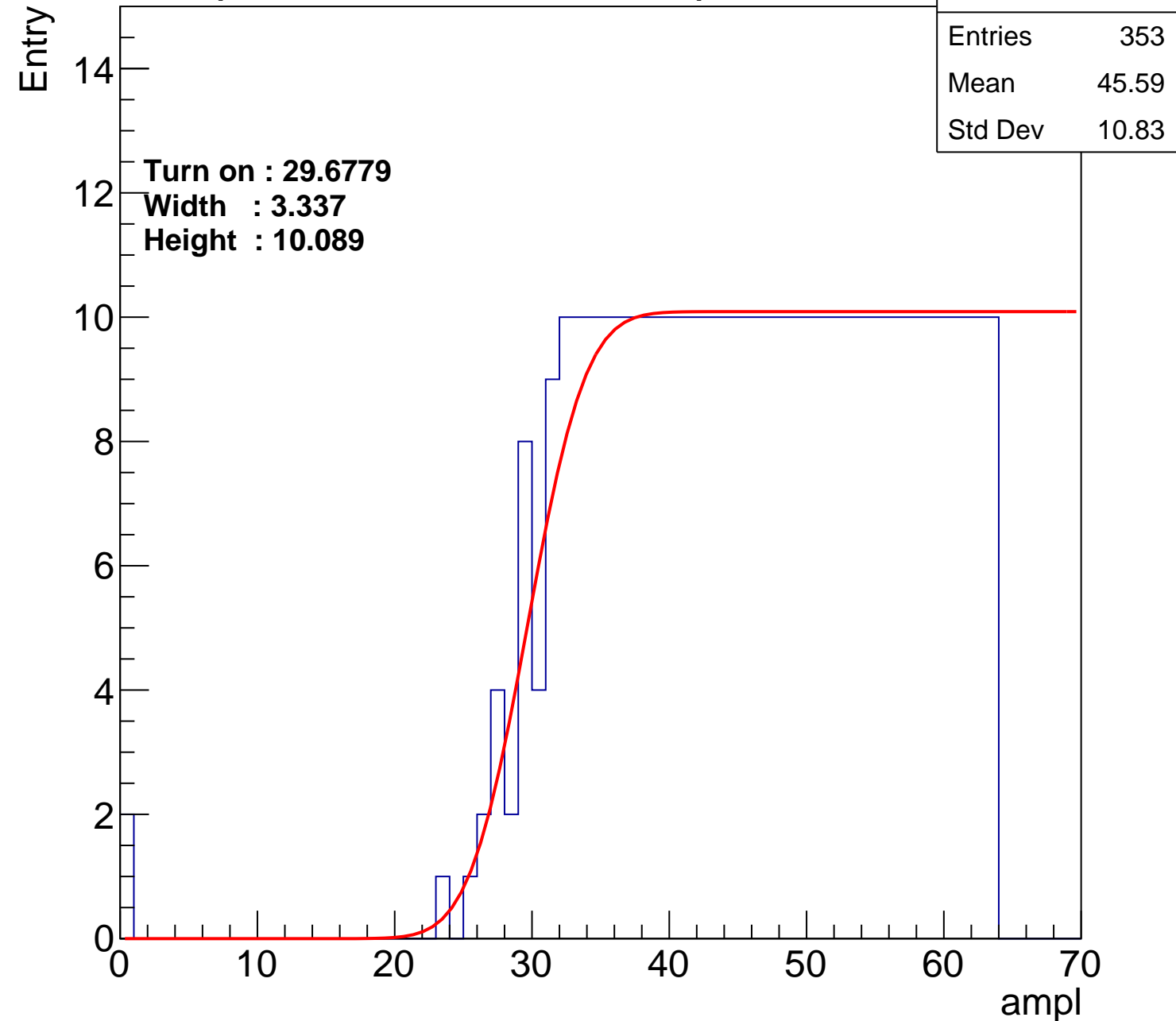
**Width : 3.337**

**Height : 10.089**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch126

calib\_packv5\_042523\_0143.root, FC#8, port C1

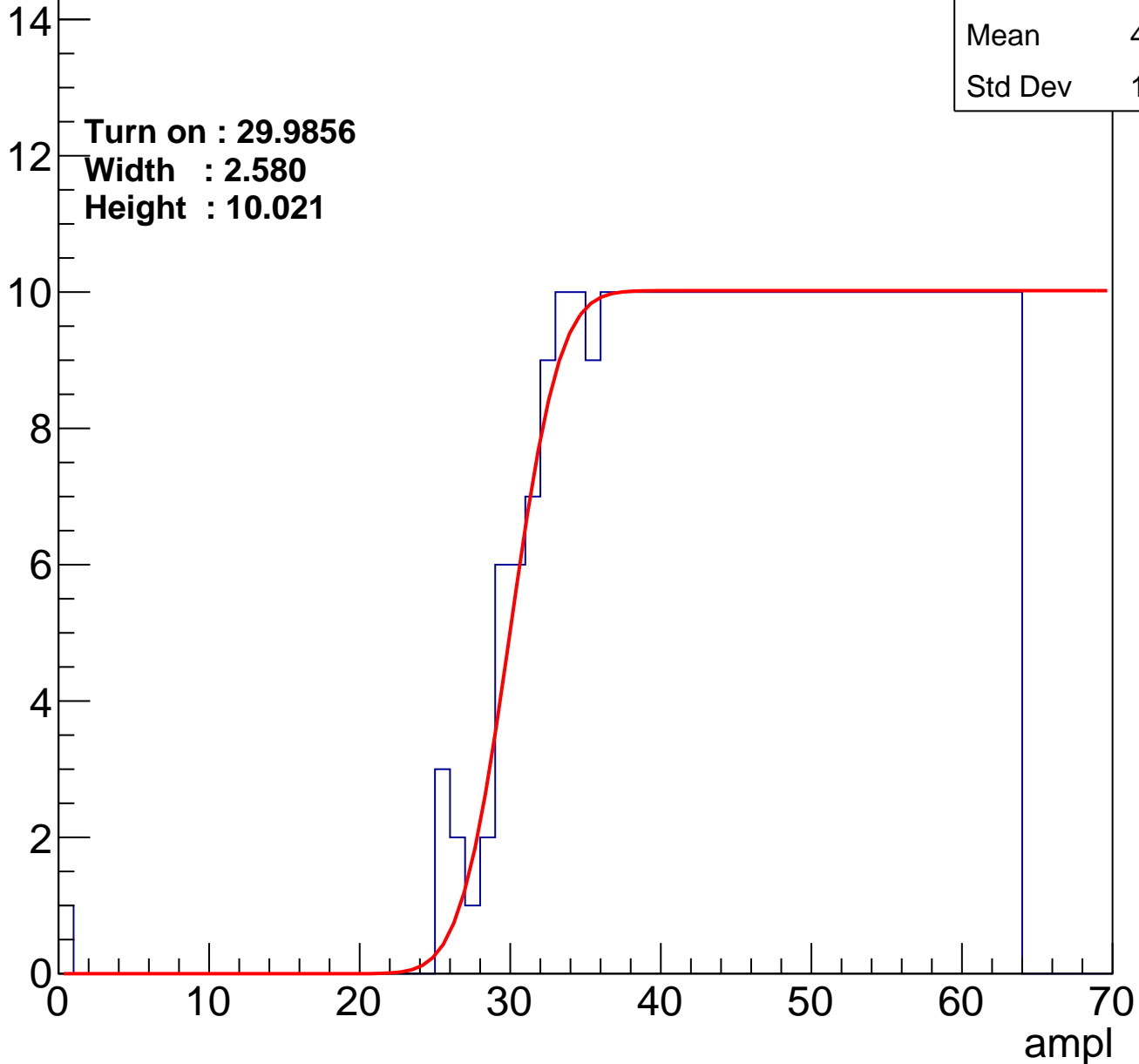
Entries	346
Mean	45.99
Std Dev	10.45

Turn on : 29.9856

Width : 2.580

Height : 10.021

Entry





# B0L002S, U25-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.23
Std Dev	11.08

Turn on : 28.5518

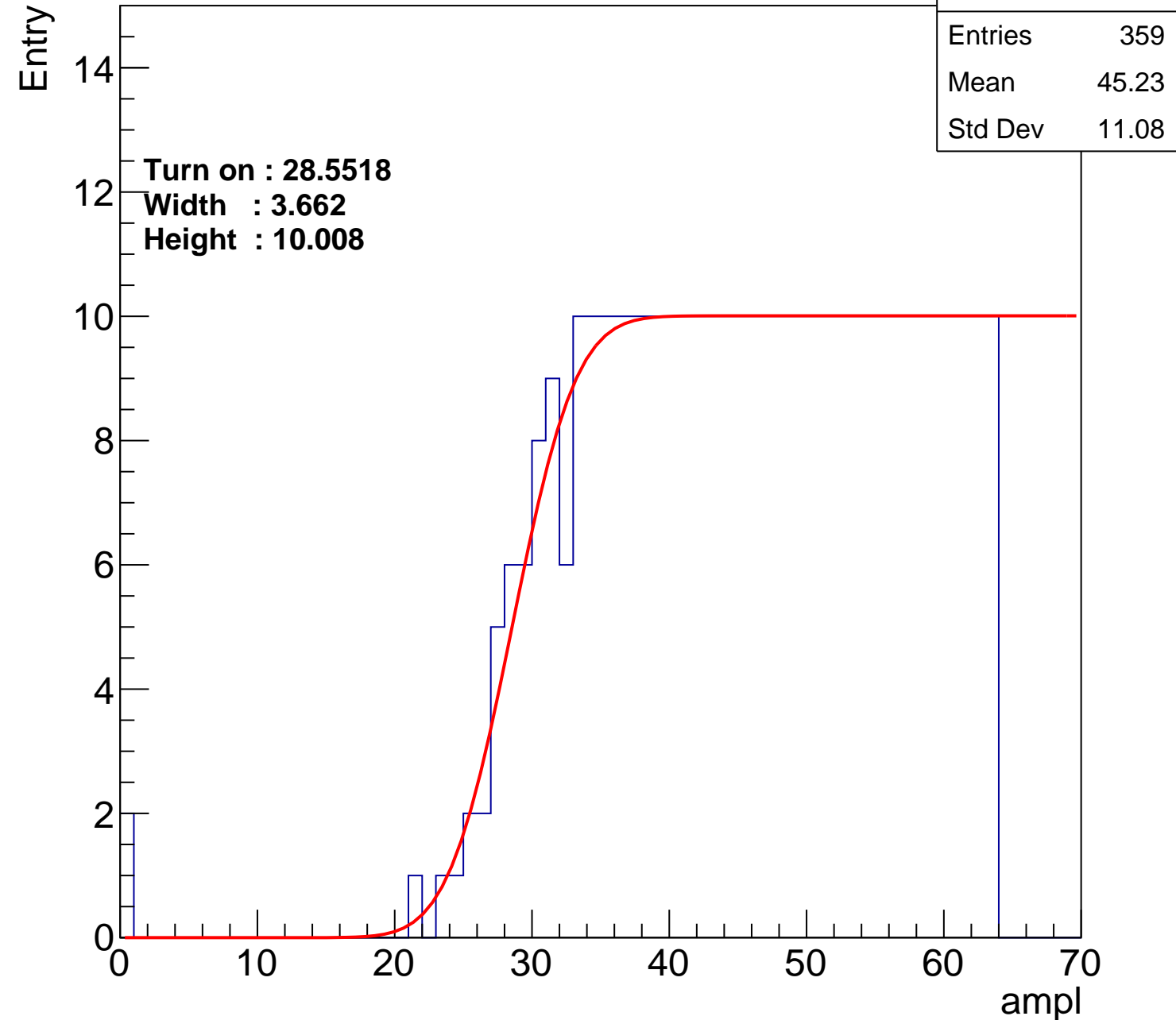
Width : 3.662

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U25-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.23
Std Dev	11.08

Turn on : 28.5518

Width : 3.662

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

