

B0L002S, U3-ch0

calib_packv5_042523_0143.root, FC#8, port C1

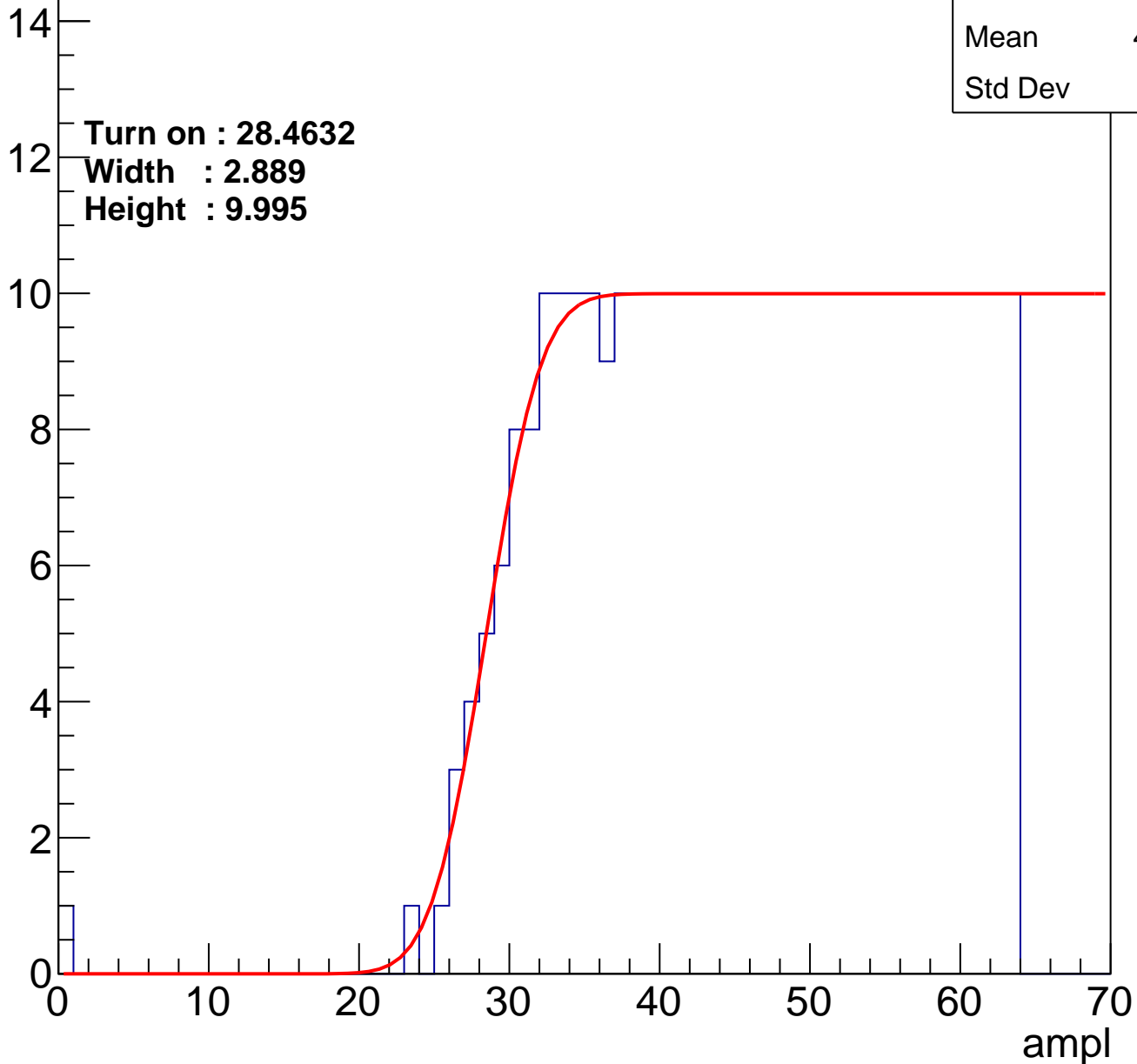
Entries	356
Mean	45.51
Std Dev	10.7

Turn on : 28.4632

Width : 2.889

Height : 9.995

Entry



B0L002S, U3-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.56
Std Dev	11.87

Turn on : 28.4242

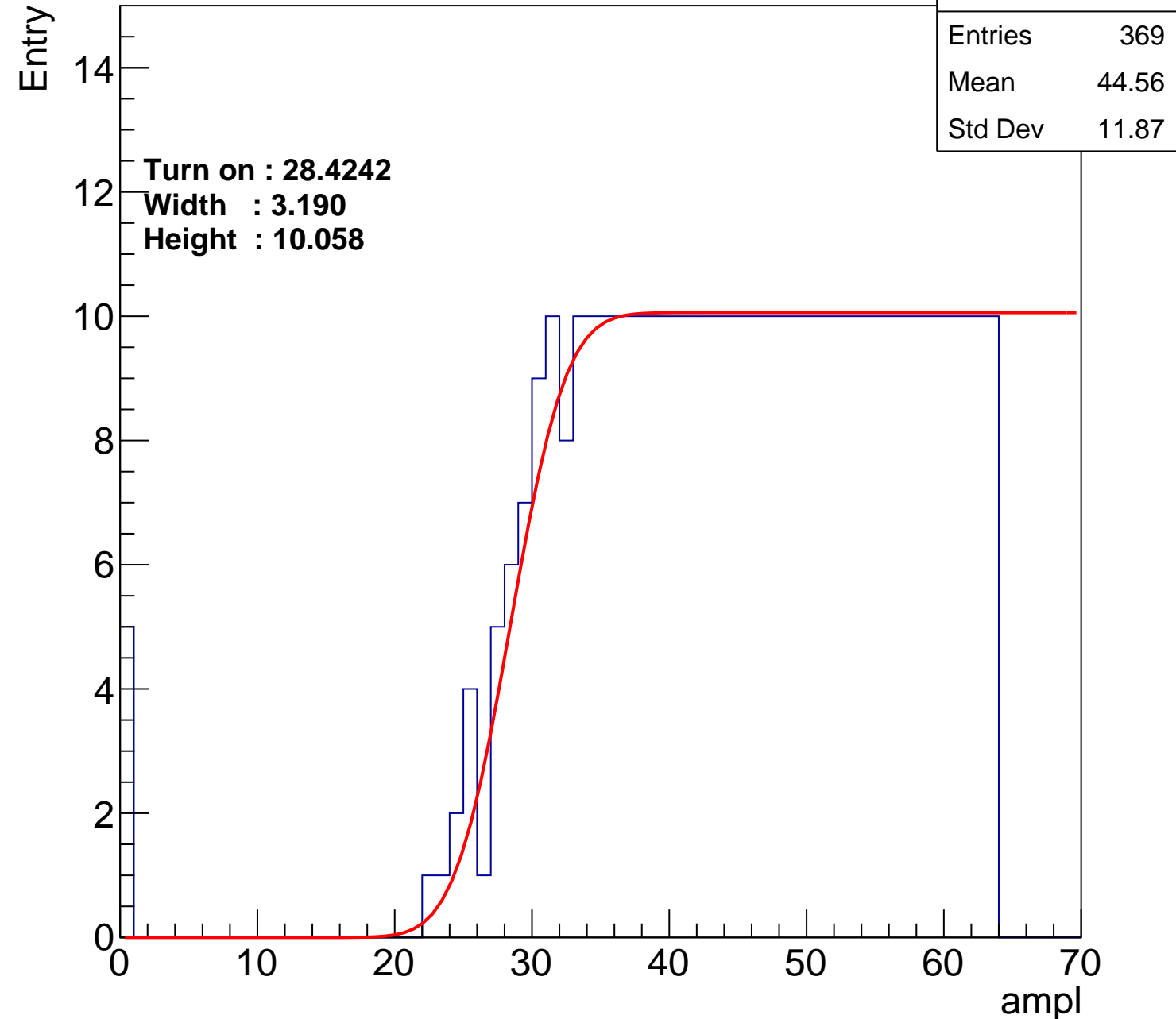
Width : 3.190

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	43.85
Std Dev	12.4

Turn on : 26.9870

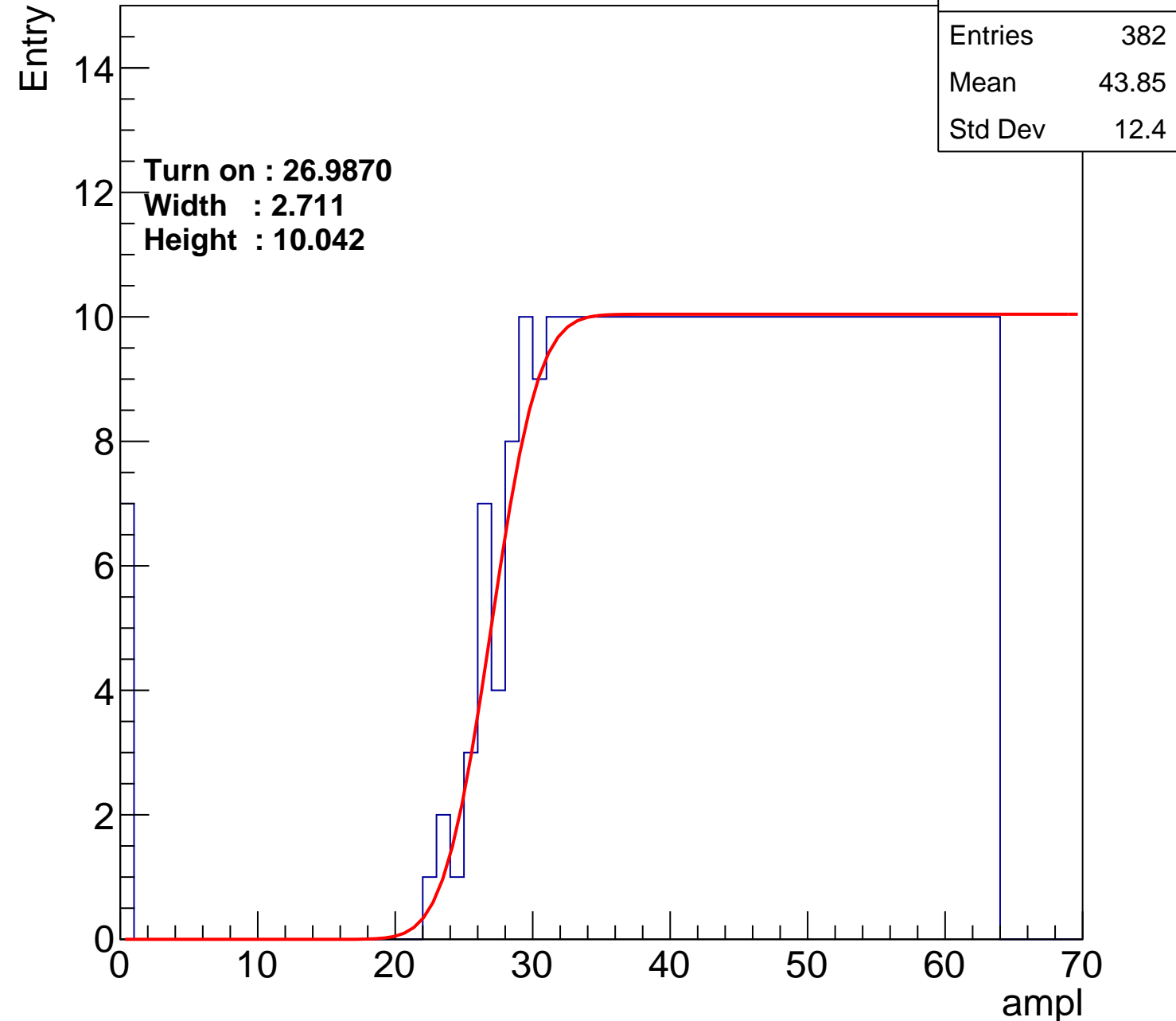
Width : 2.711

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch3

calib_packv5_042523_0143.root, FC#8, port C1

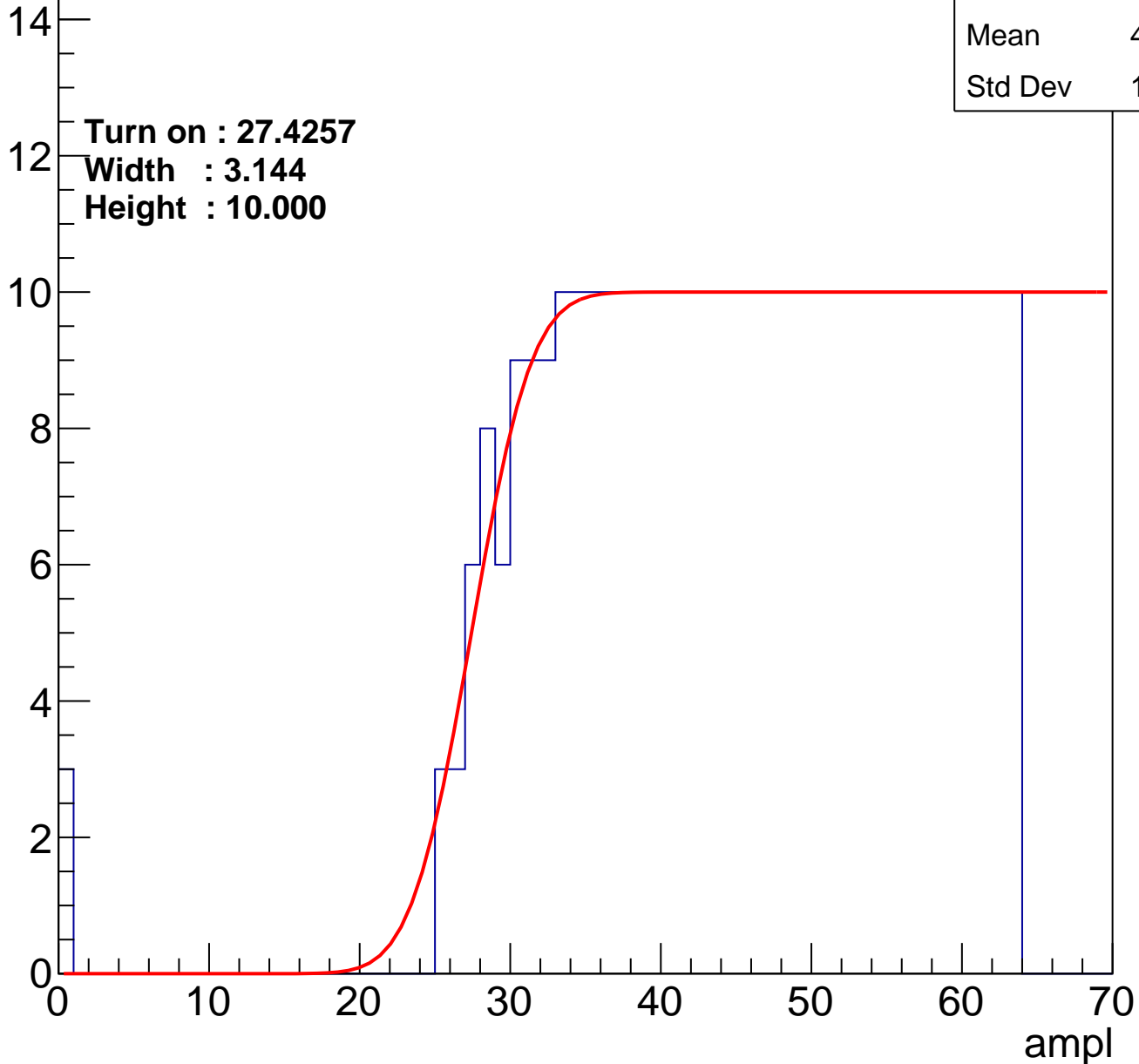
Entries	366
Mean	44.89
Std Dev	11.34

Turn on : 27.4257

Width : 3.144

Height : 10.000

Entry



B0L002S, U3-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.8852

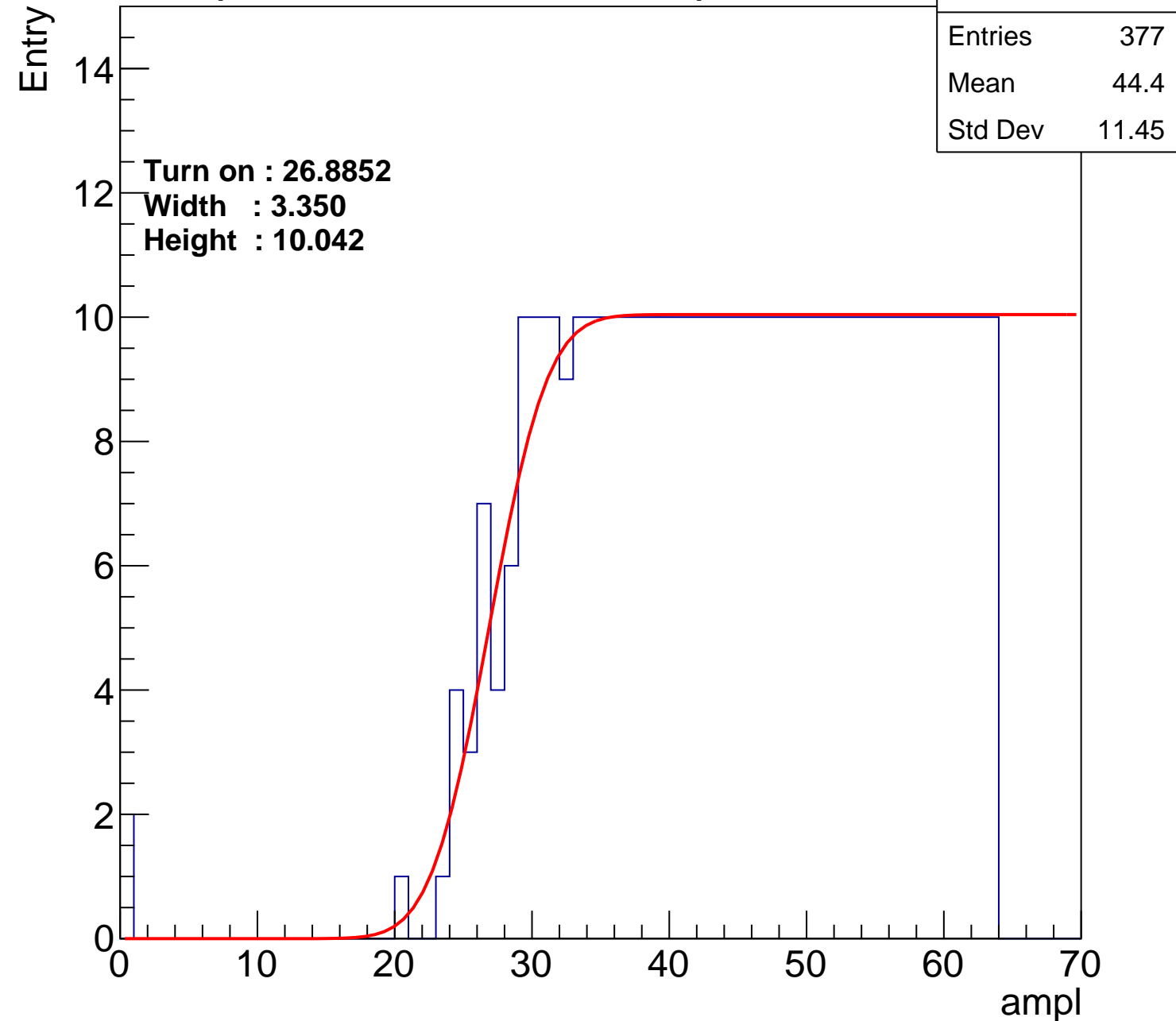
Width : 3.350

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch5

calib_packv5_042523_0143.root, FC#8, port C1

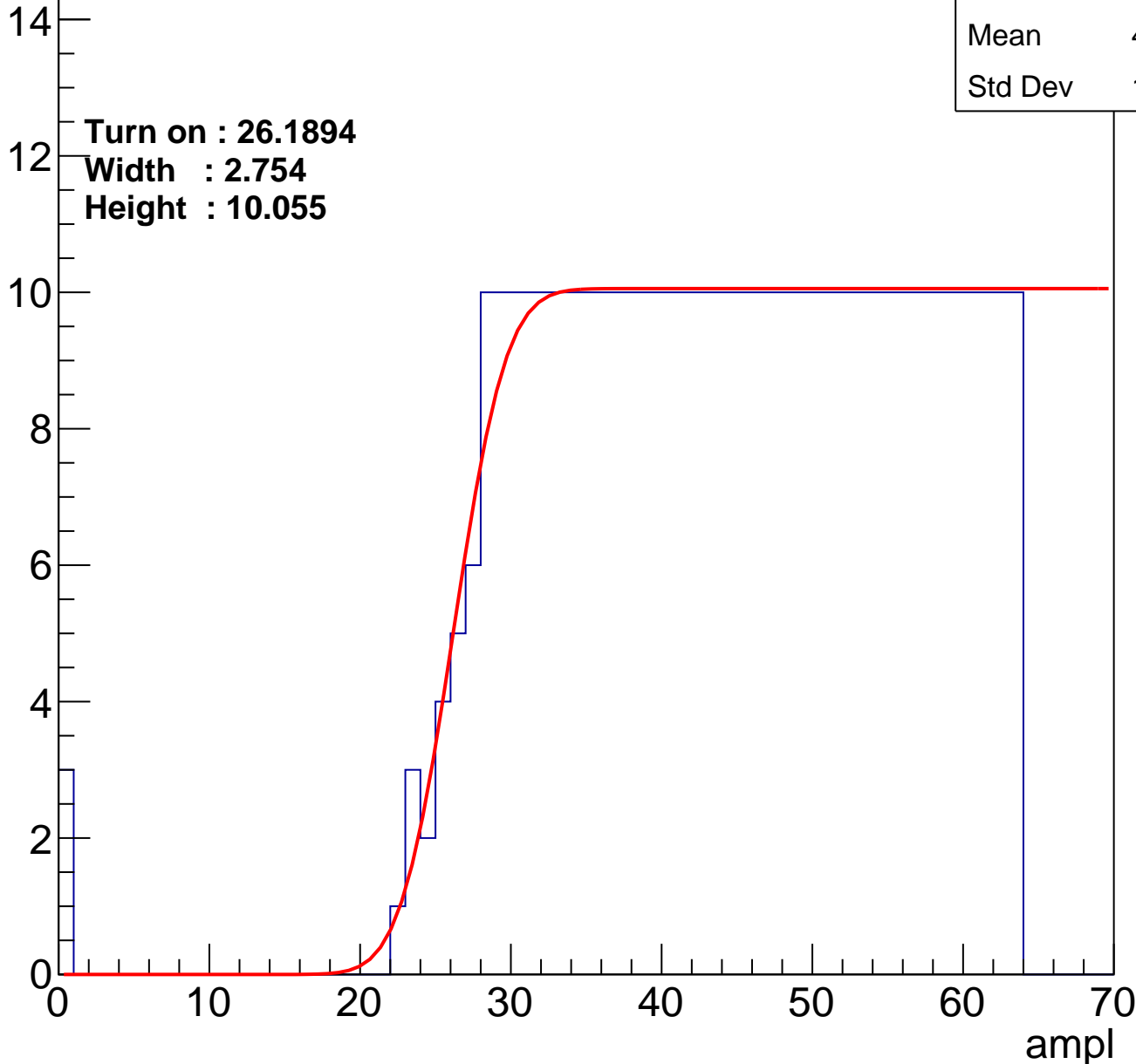
Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 26.1894

Width : 2.754

Height : 10.055

Entry



B0L002S, U3-ch6

calib_packv5_042523_0143.root, FC#8, port C1

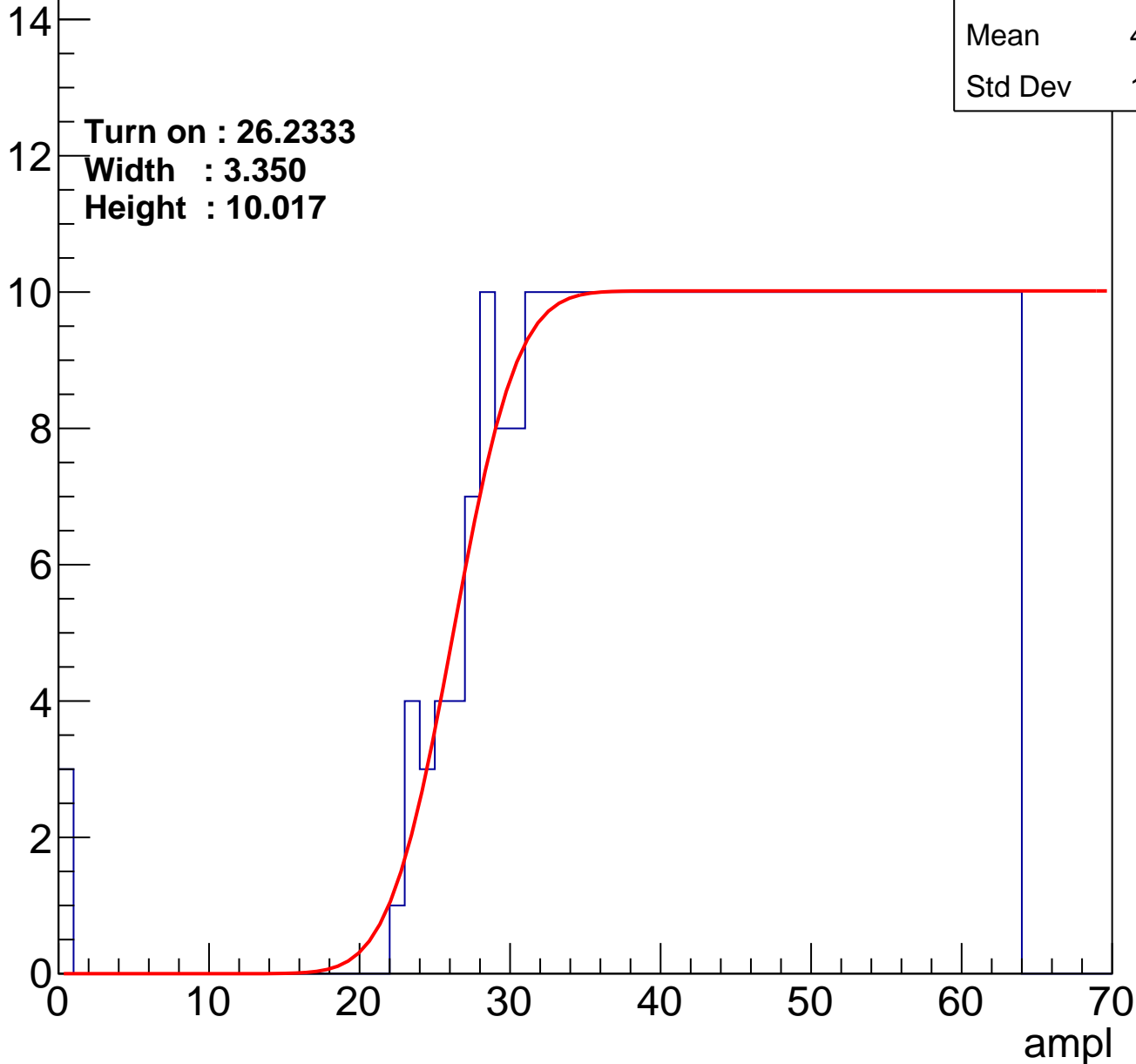
Entry

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.2333

Width : 3.350

Height : 10.017



B0L002S, U3-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.2
Std Dev	10.82

Turn on : 27.9846

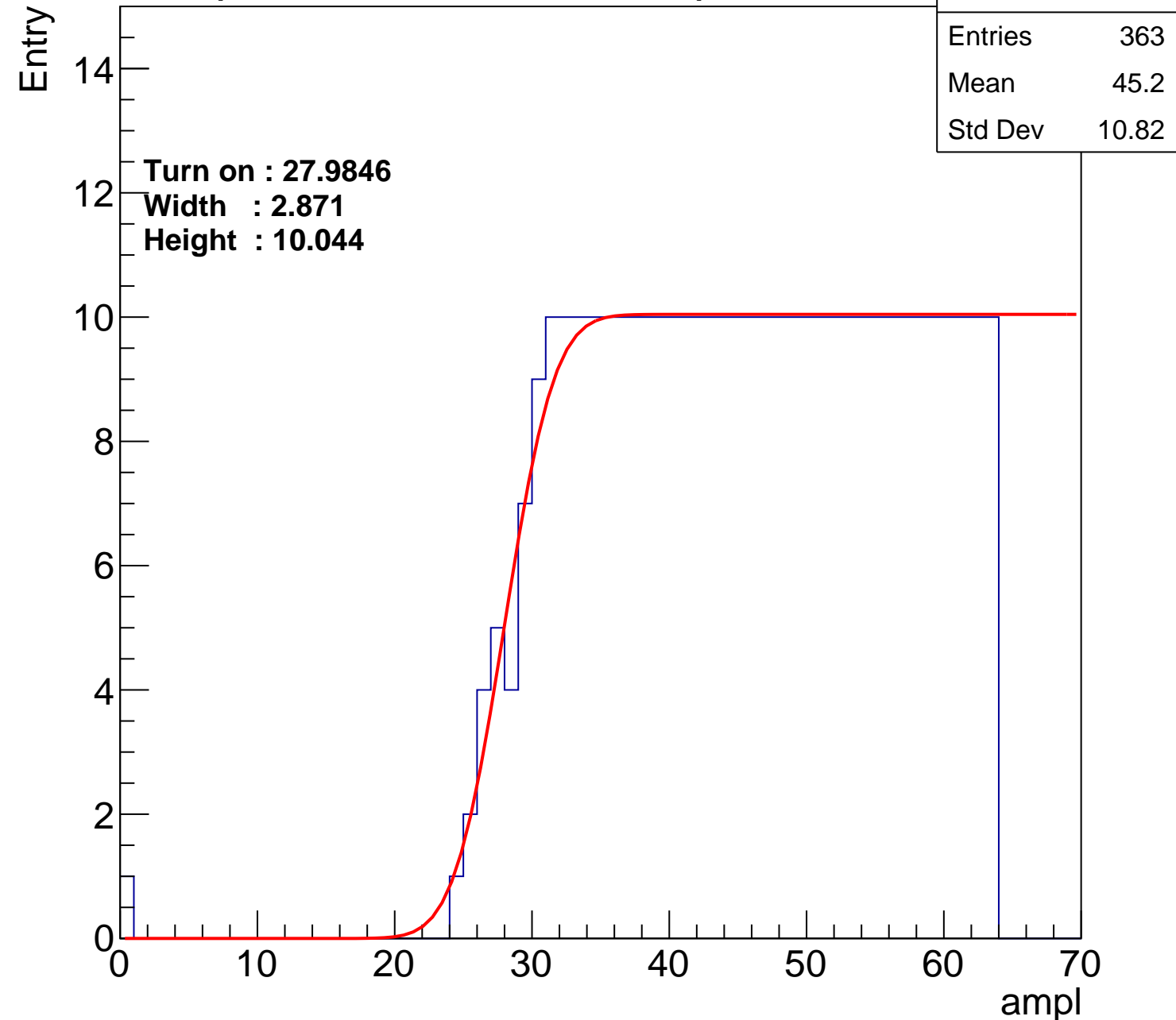
Width : 2.871

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.4
Std Dev	11.82

Turn on : 27.7330

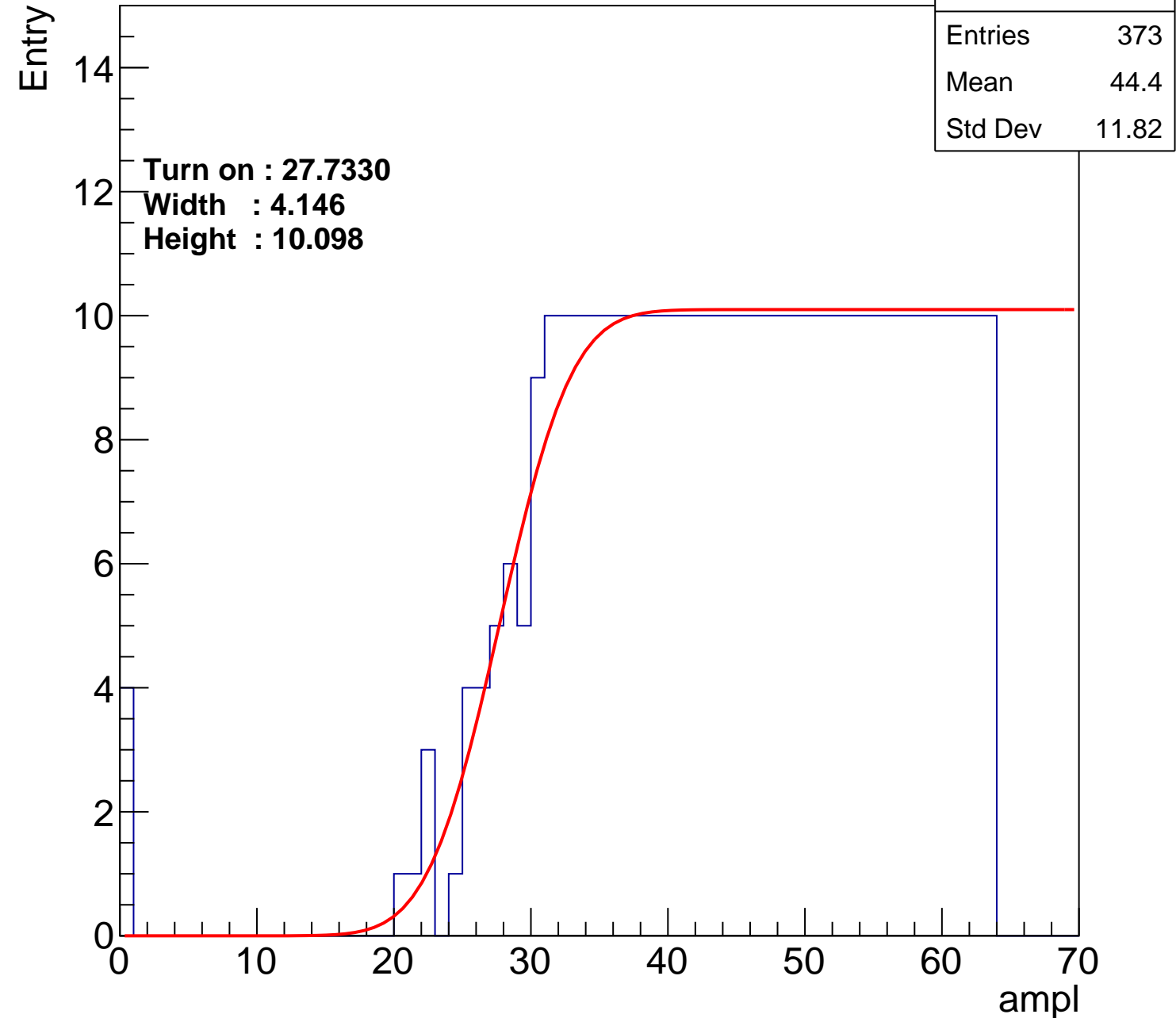
Width : 4.146

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.45
Std Dev	10.74

Turn on : 28.4347

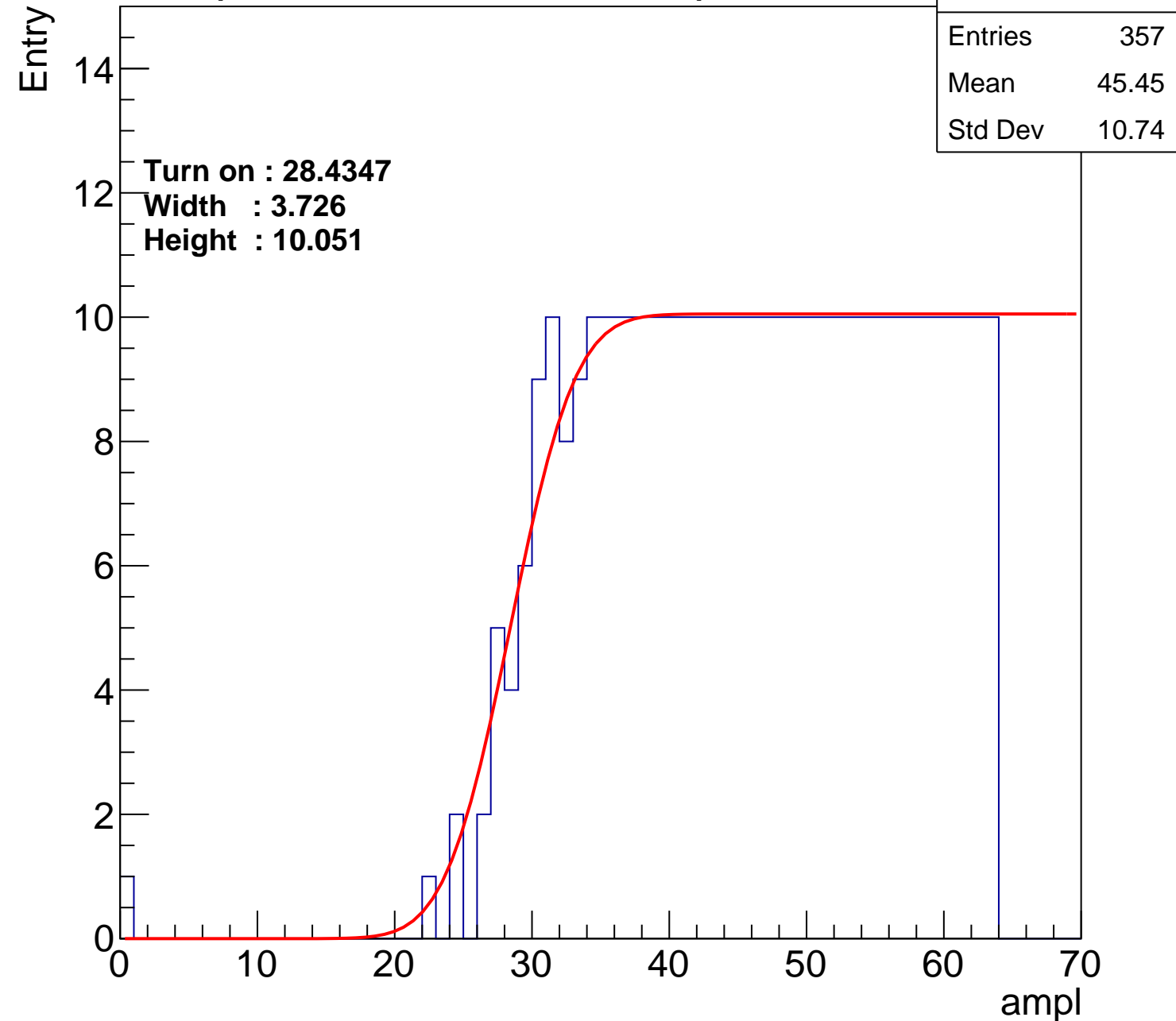
Width : 3.726

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch10

calib_packv5_042523_0143.root, FC#8, port C1

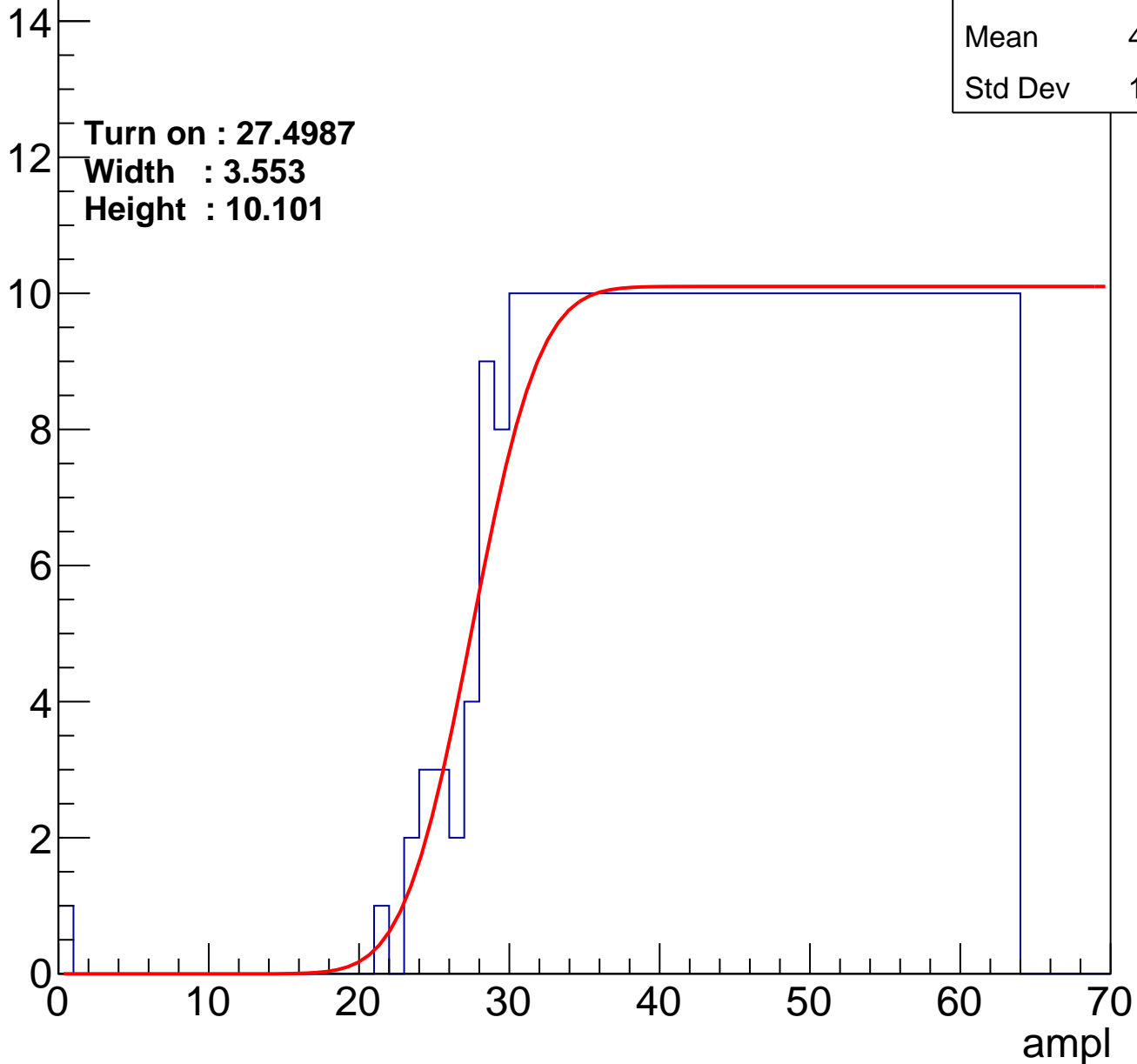
Entry

Entries	373
Mean	44.69
Std Dev	11.13

Turn on : 27.4987

Width : 3.553

Height : 10.101



B0L002S, U3-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 26.9866

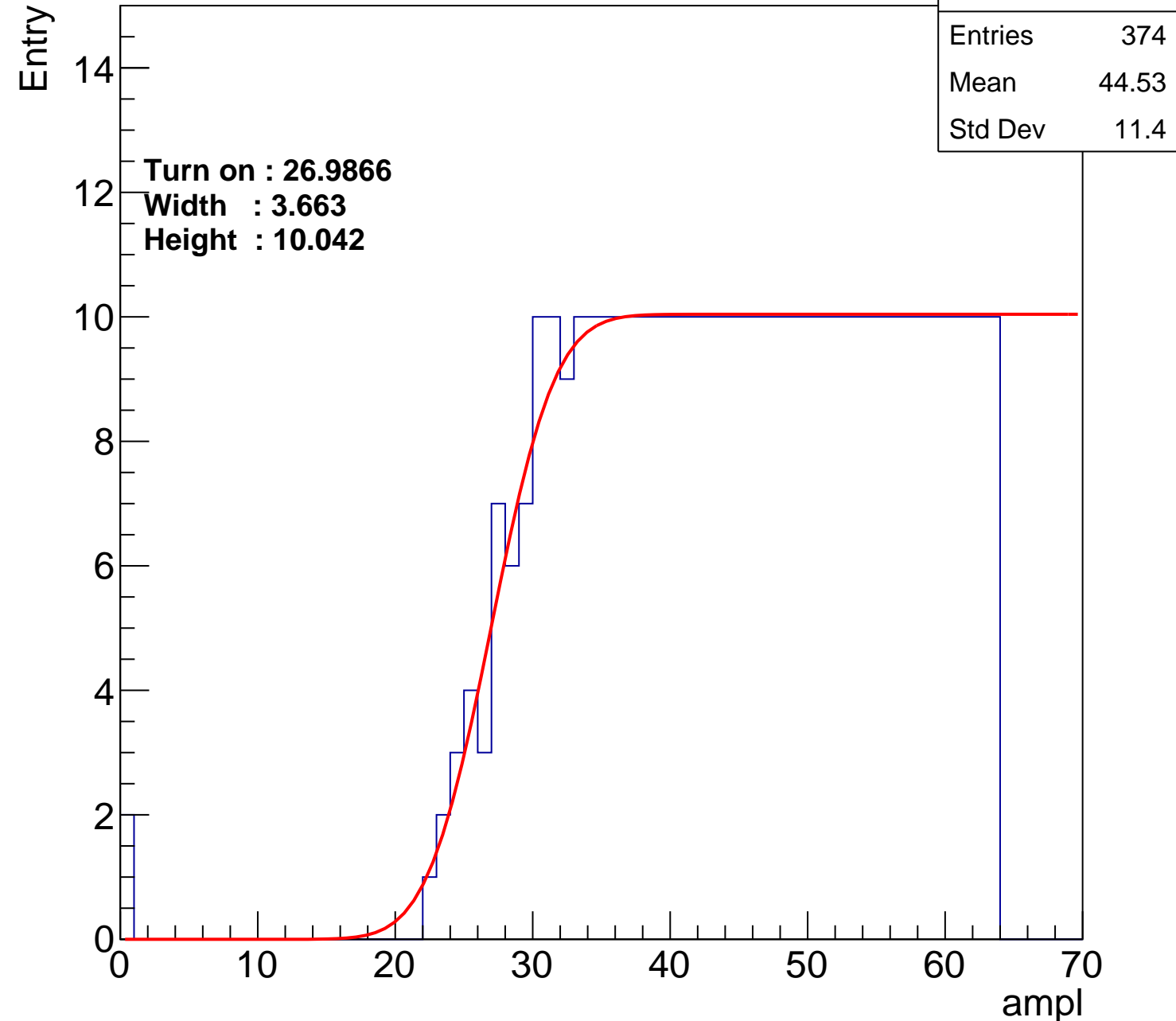
Width : 3.663

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch12

calib_packv5_042523_0143.root, FC#8, port C1

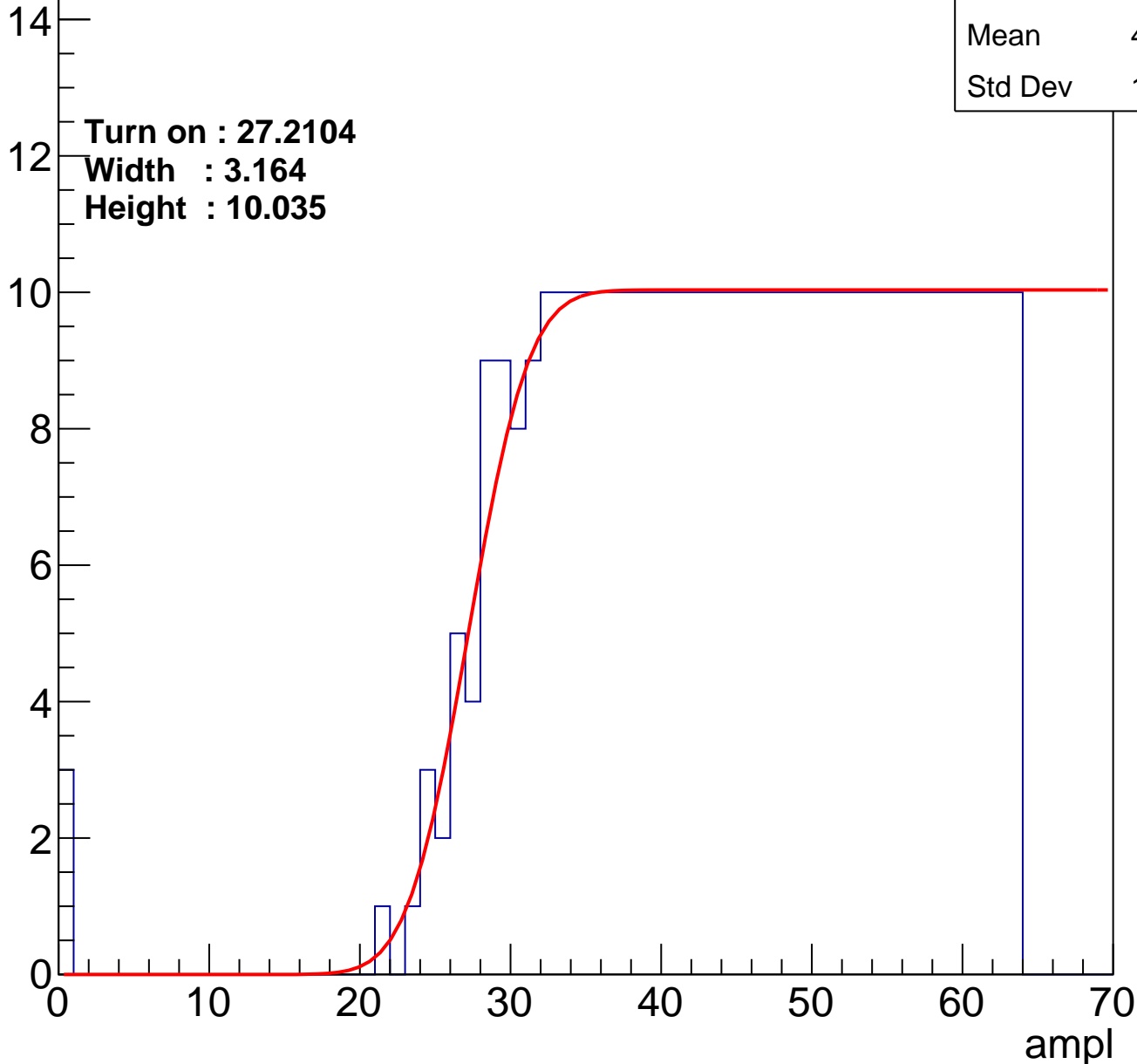
Entry

Entries	374
Mean	44.48
Std Dev	11.56

Turn on : 27.2104

Width : 3.164

Height : 10.035



B0L002S, U3-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.38
Std Dev	11.09

Turn on : 28.3777

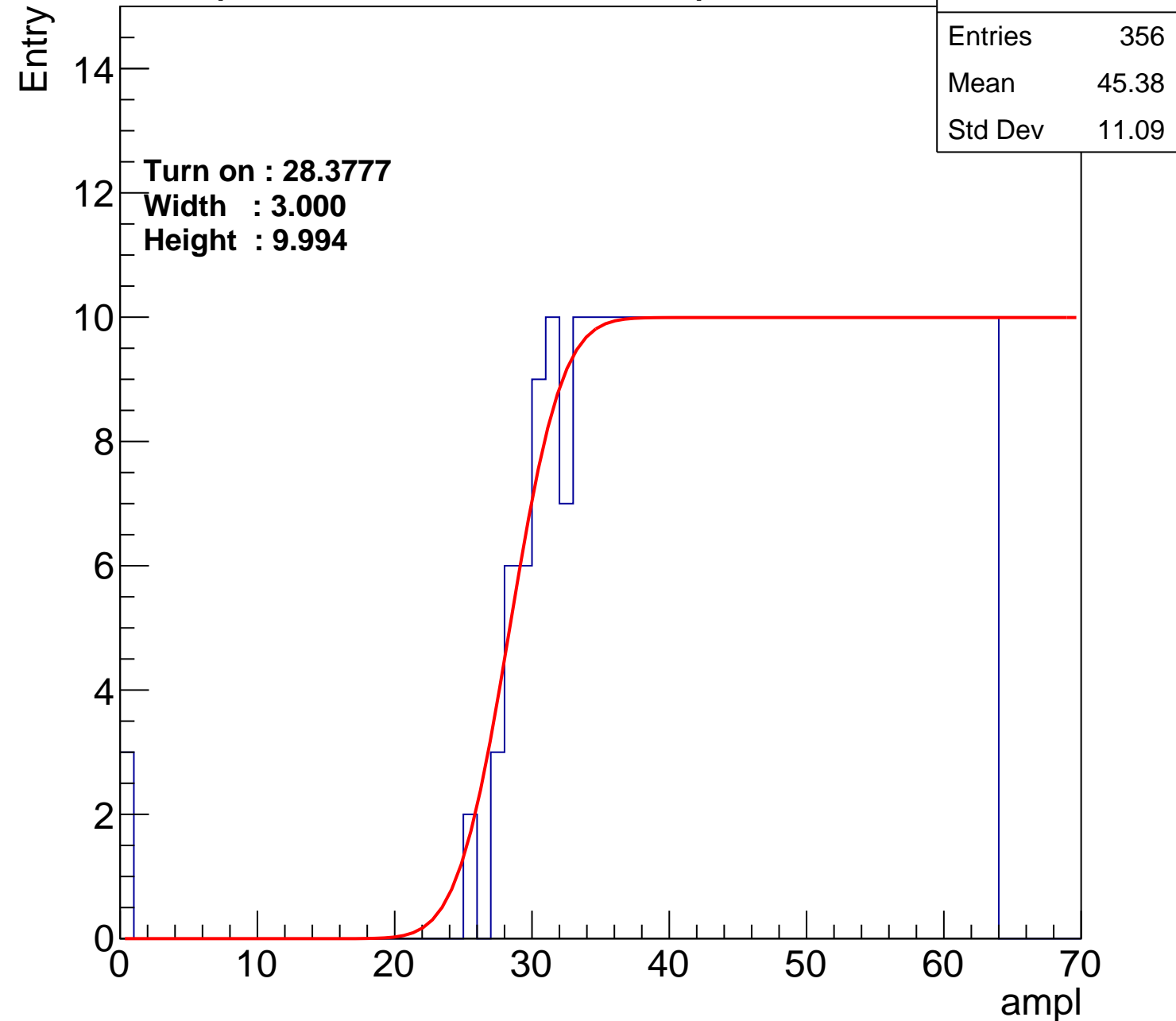
Width : 3.000

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.45
Std Dev	11.25

Turn on : 29.2868

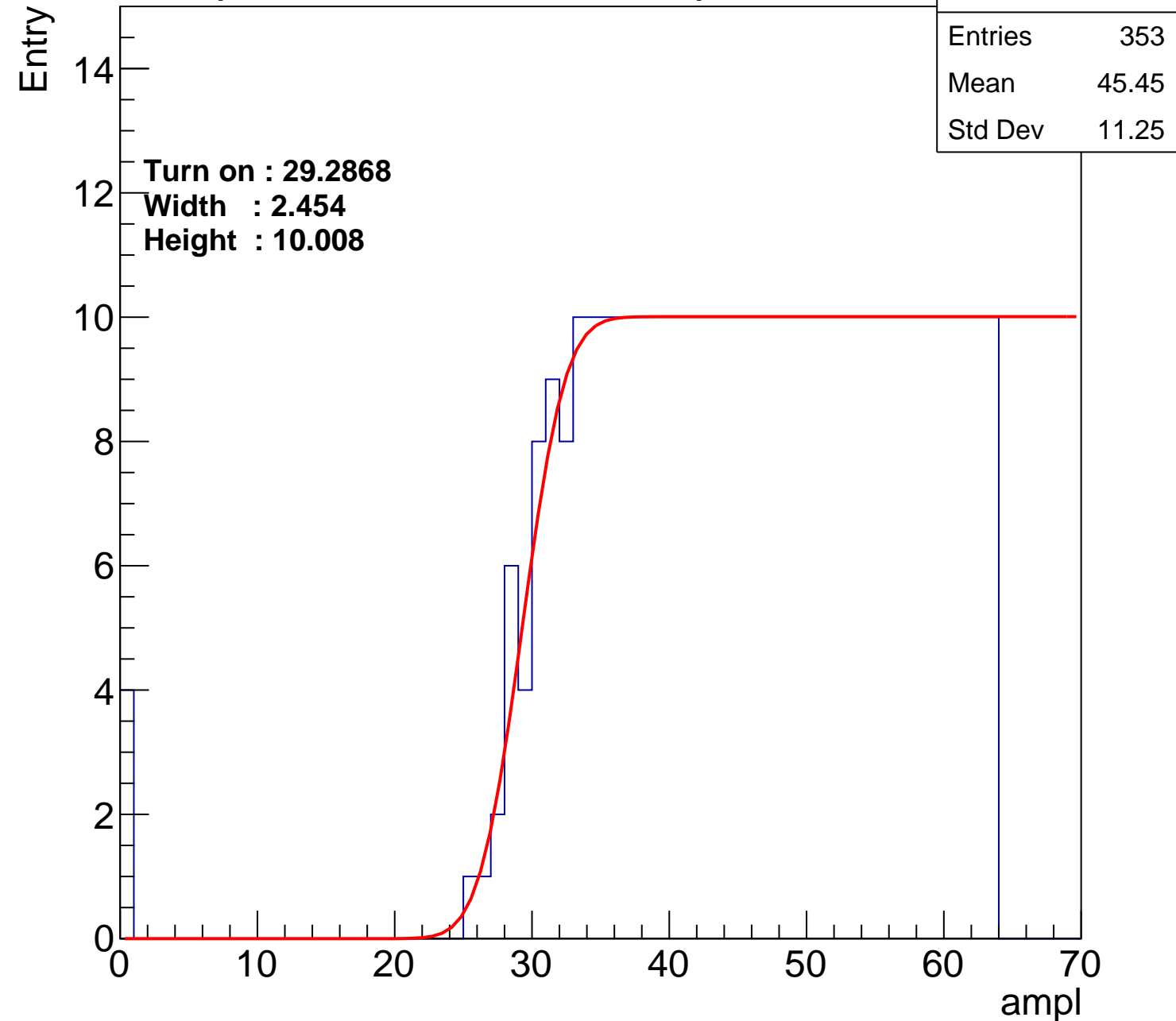
Width : 2.454

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch15

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45
Std Dev	11.13

Turn on : 28.0077

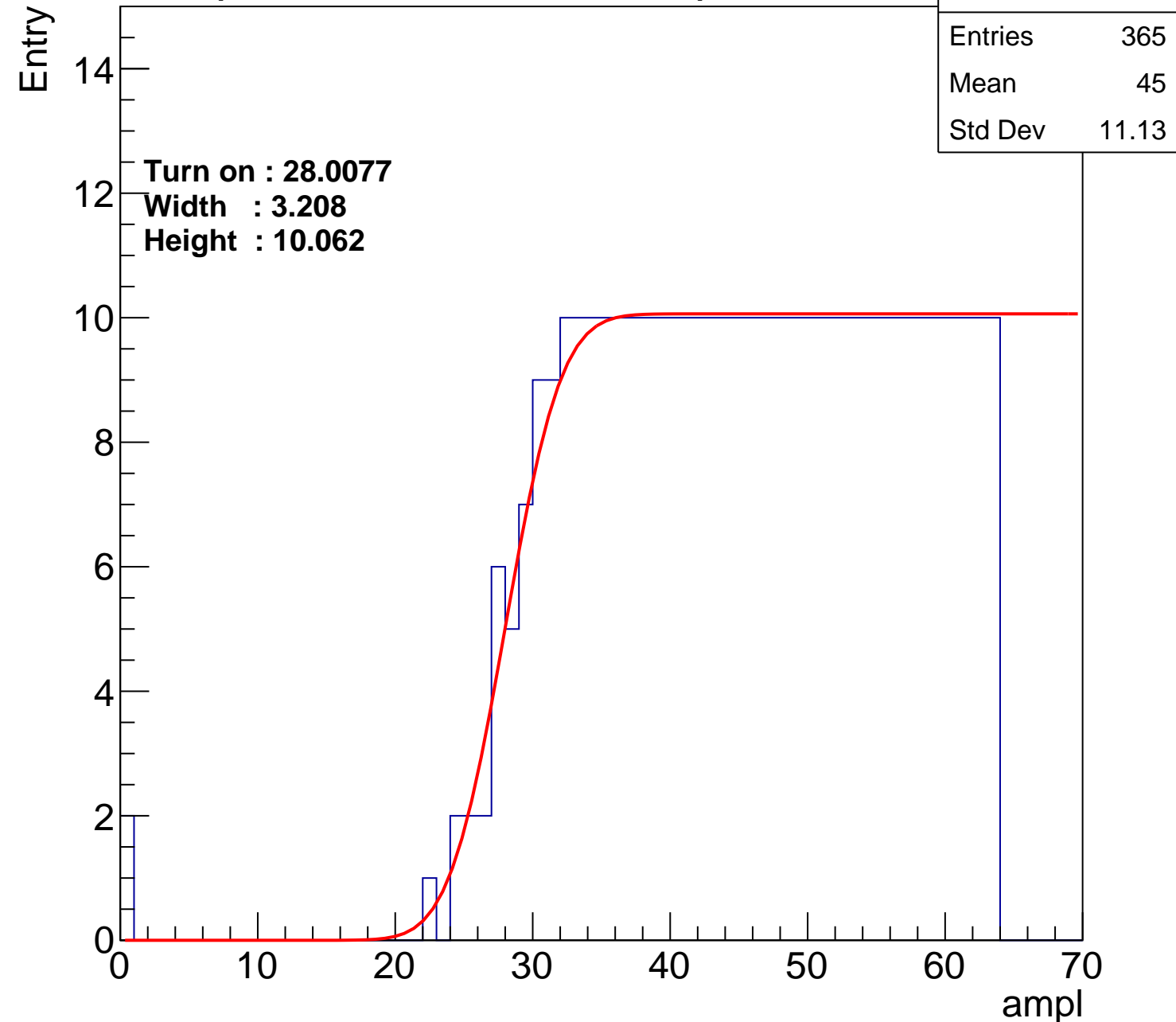
Width : 3.208

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.79
Std Dev	11.4

Turn on : 28.0733

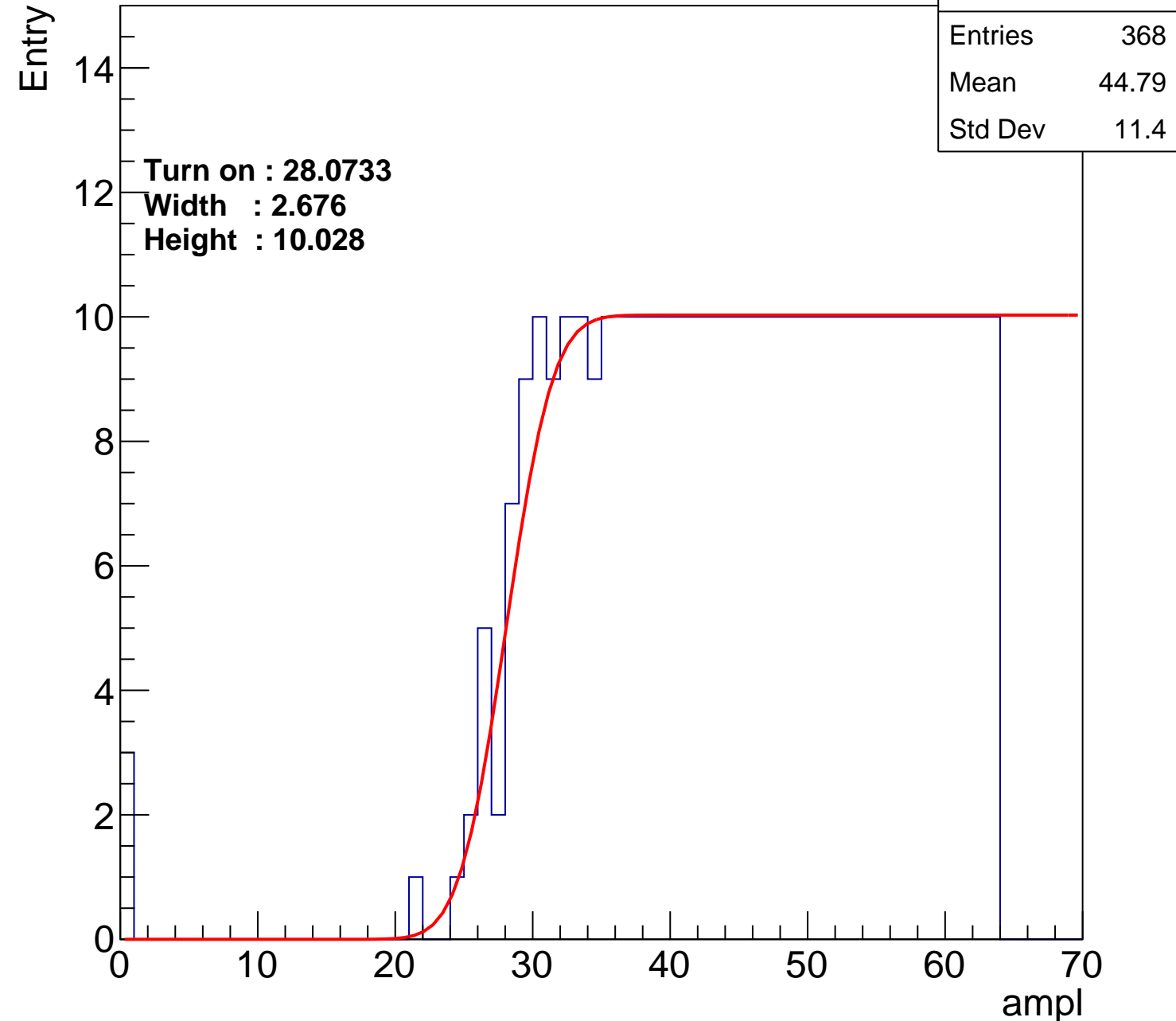
Width : 2.676

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.2553

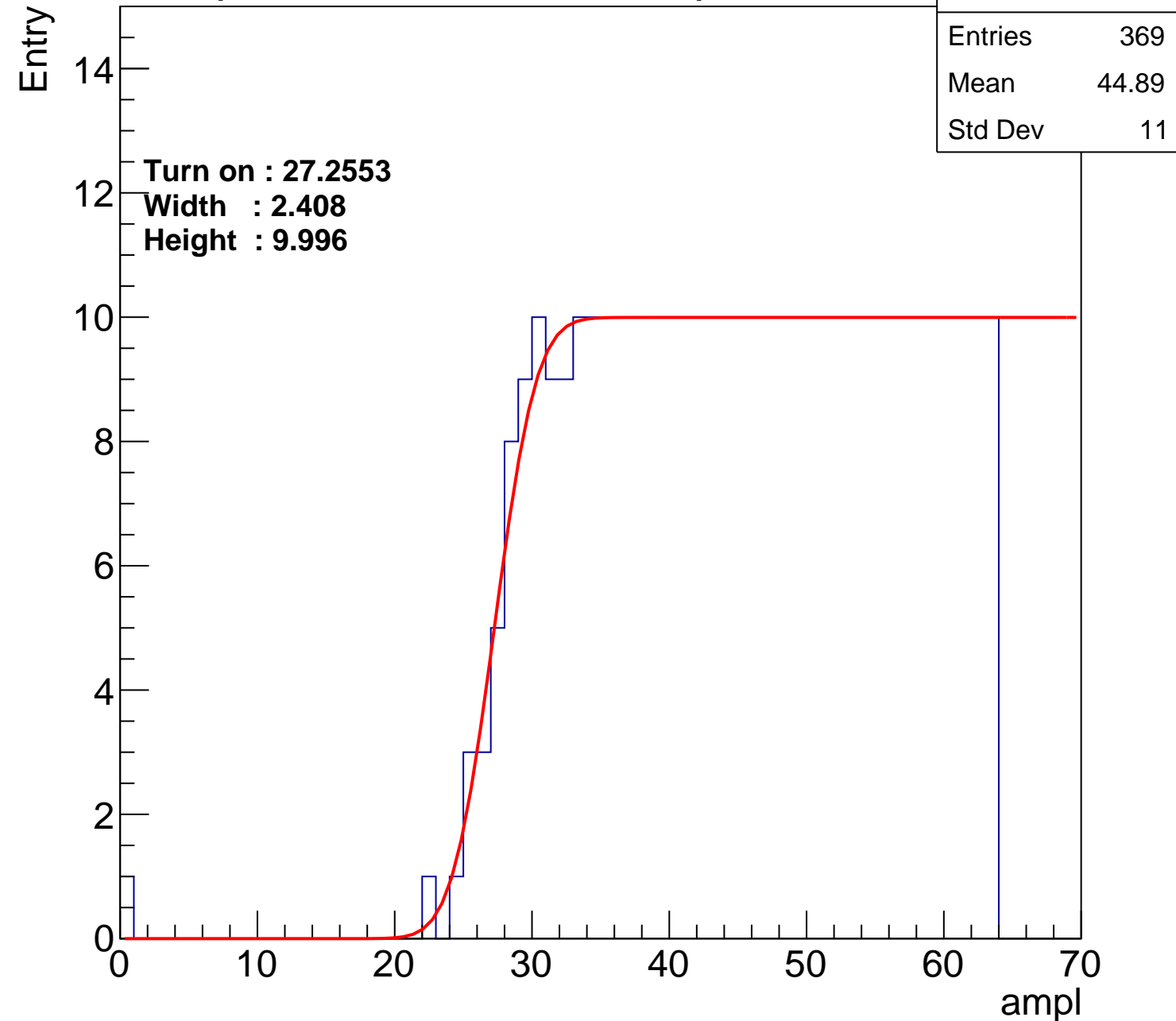
Width : 2.408

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.31
Std Dev	11.17

Turn on : 29.5462

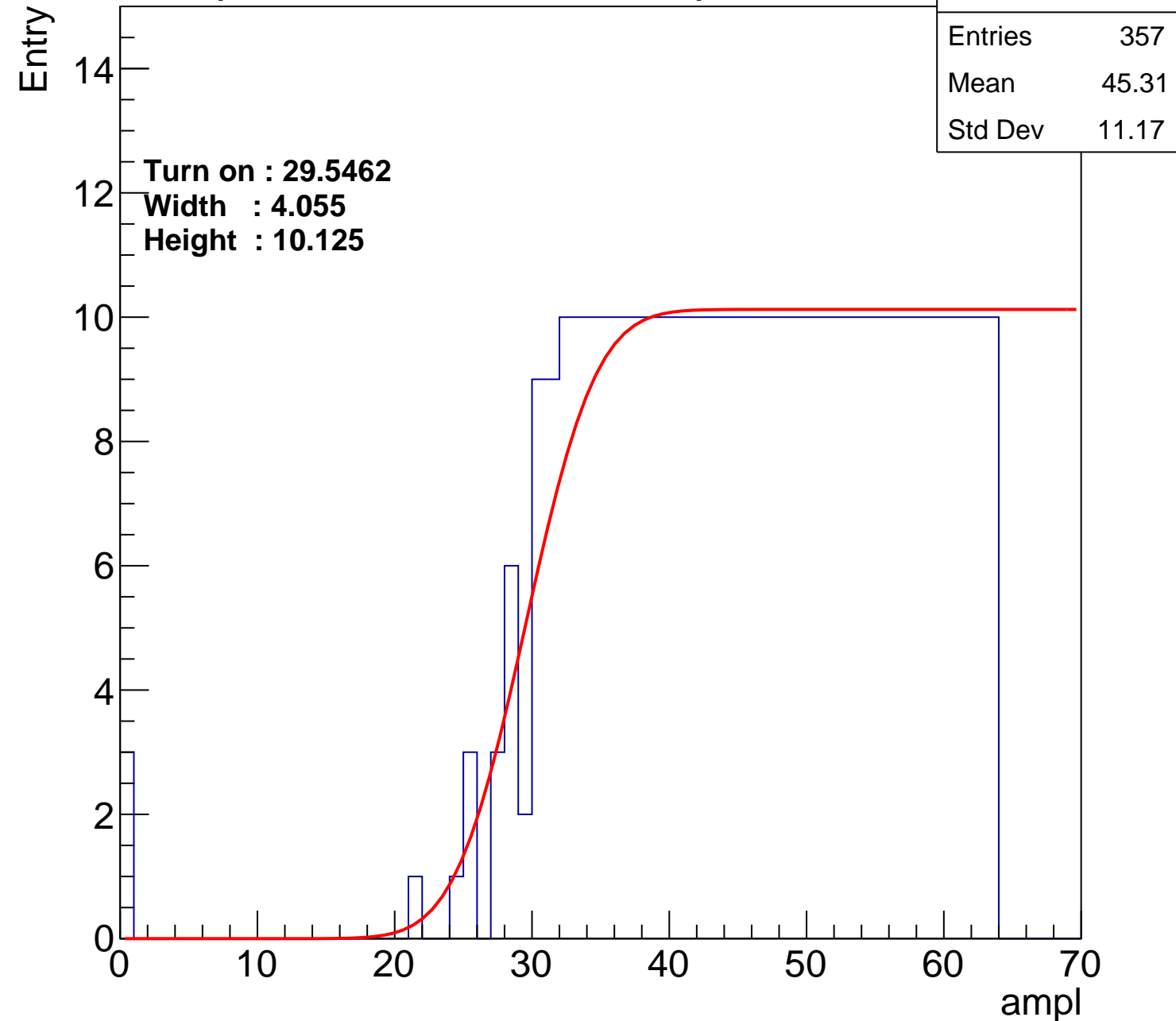
Width : 4.055

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch19

calib_packv5_042523_0143.root, FC#8, port C1

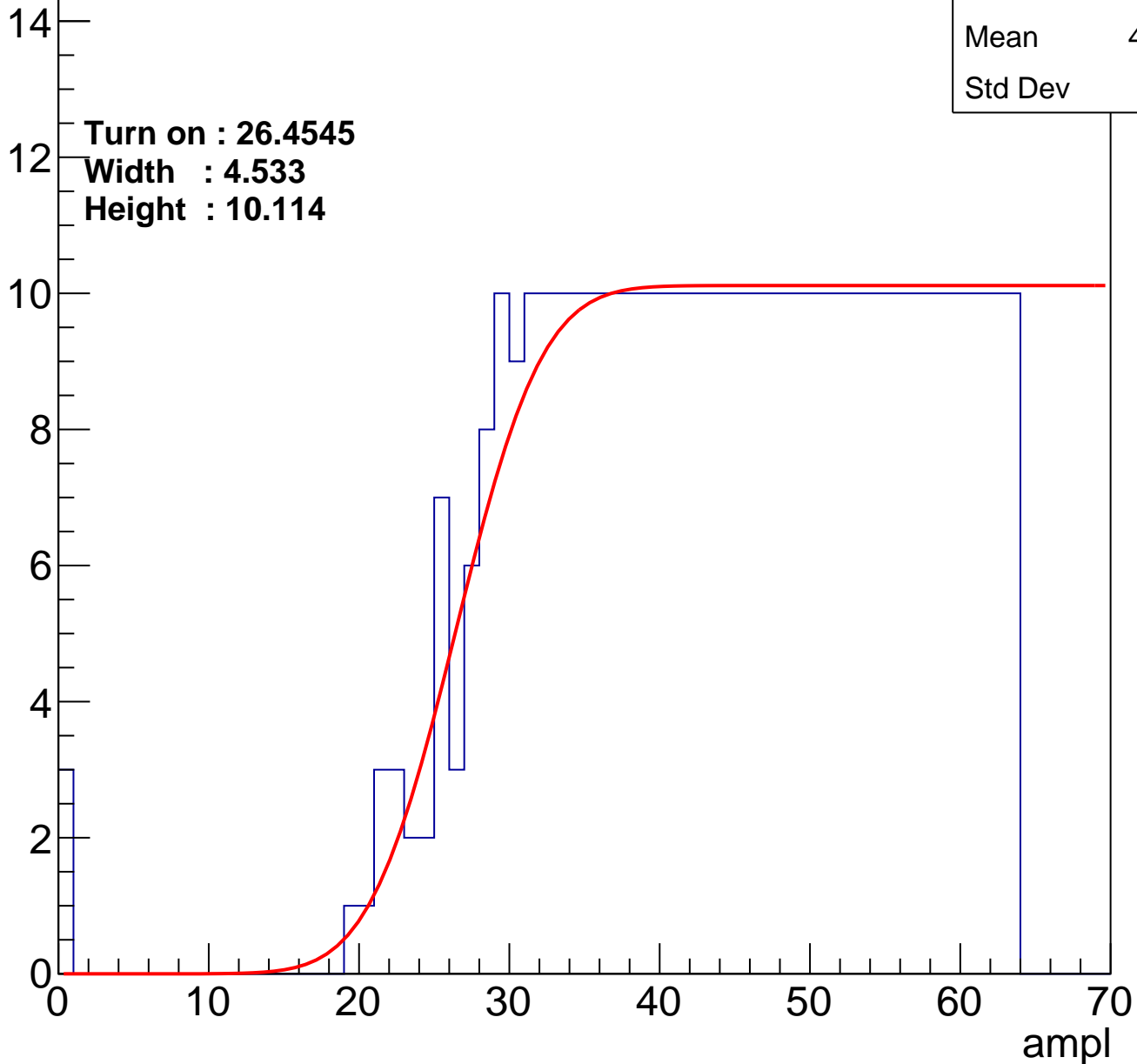
Entries	388
Mean	43.74
Std Dev	12

Turn on : 26.4545

Width : 4.533

Height : 10.114

Entry



B0L002S, U3-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.38
Std Dev	10.74

Turn on : 29.0464

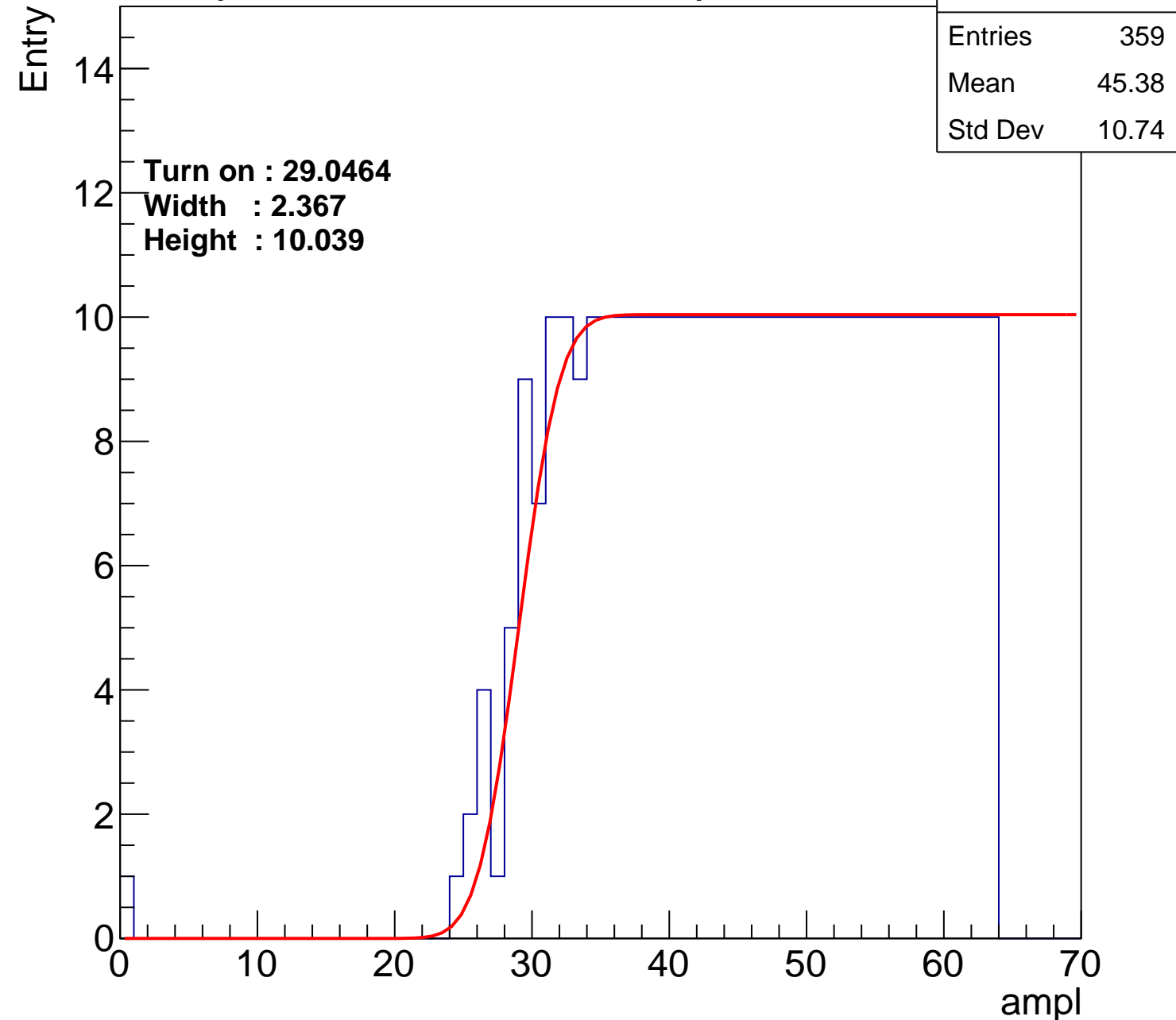
Width : 2.367

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.44
Std Dev	10.7

Turn on : 28.6691

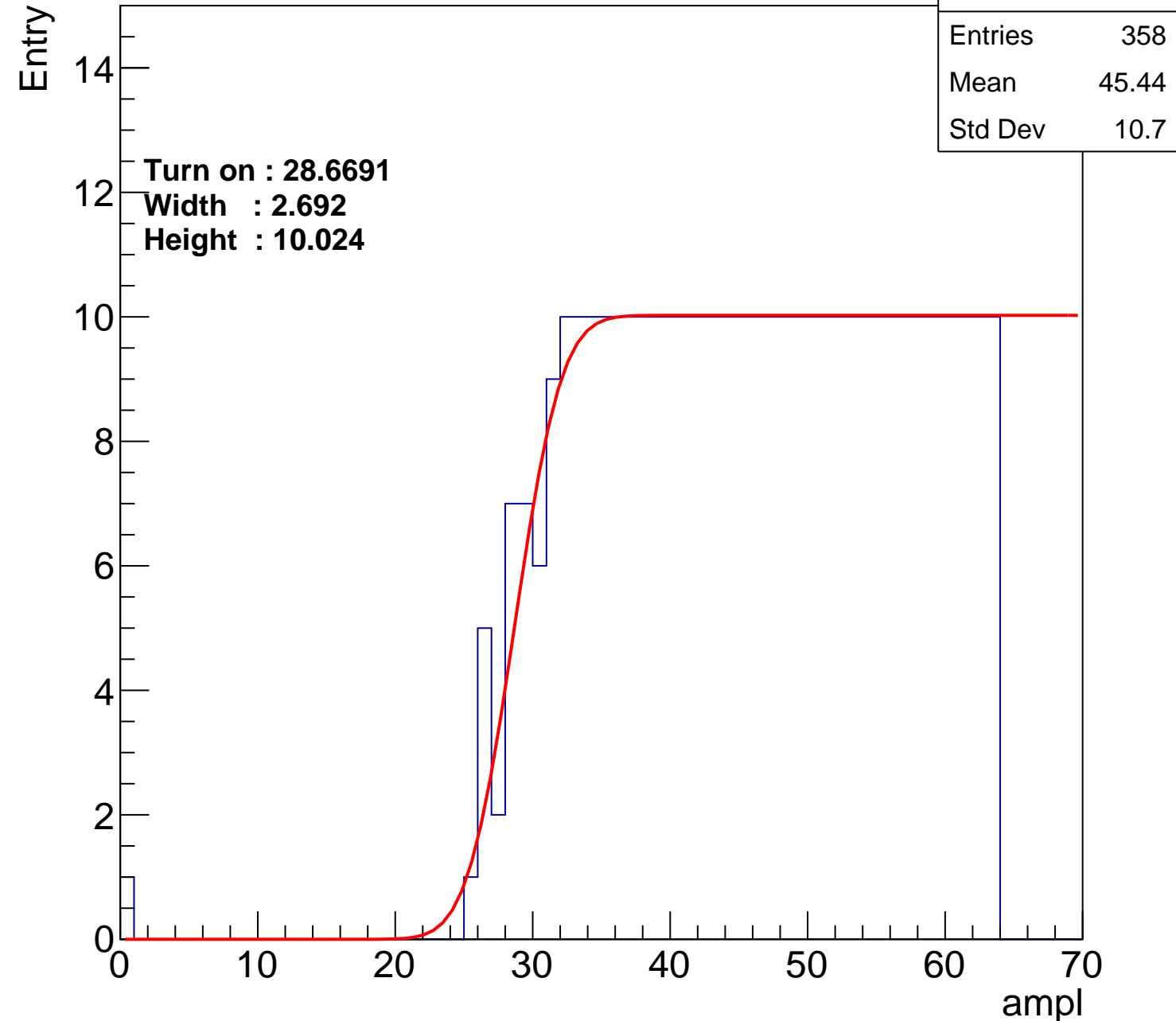
Width : 2.692

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.53
Std Dev	11.72

Turn on : 27.6473

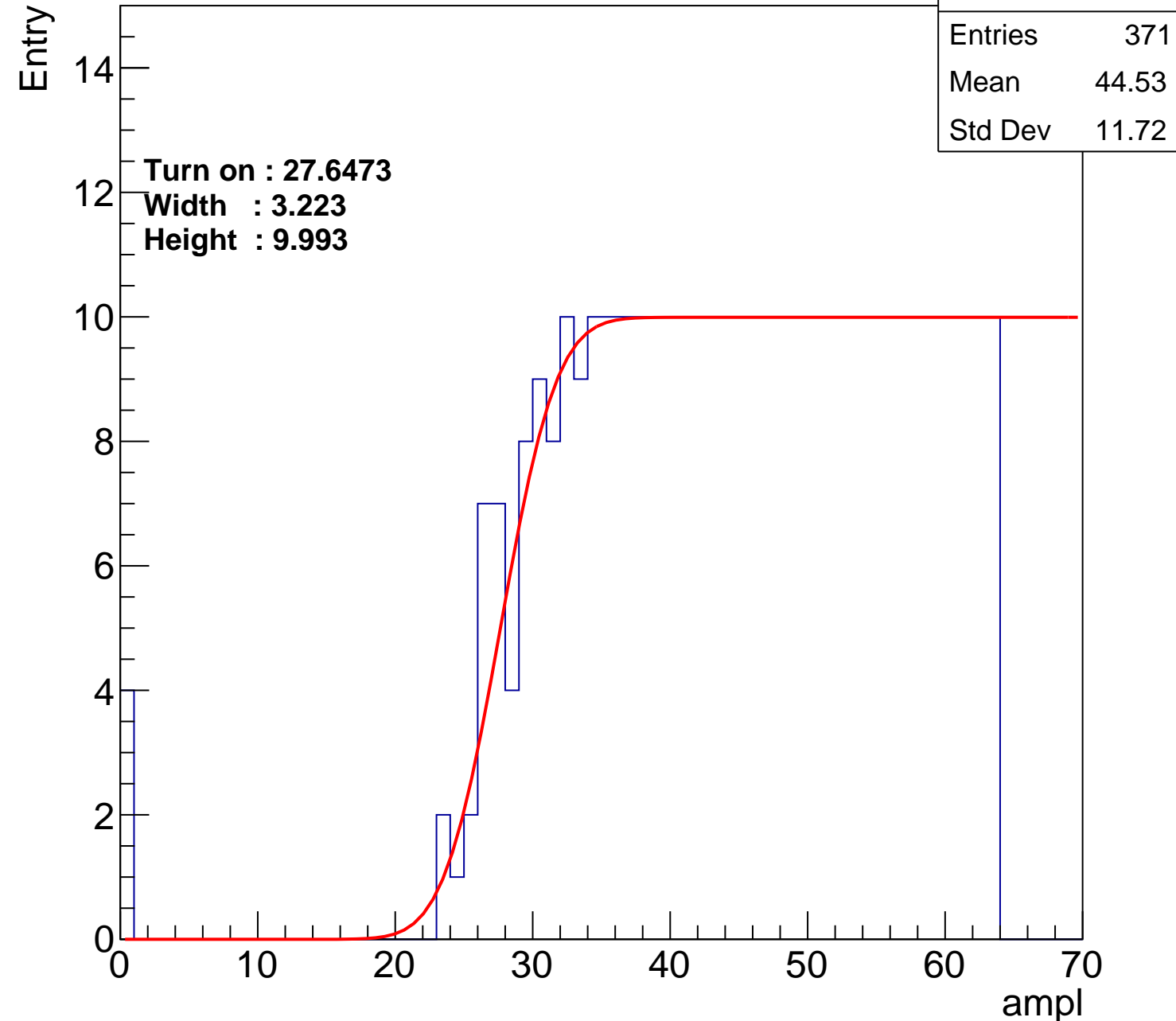
Width : 3.223

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.33
Std Dev	11.14

Turn on : 28.9835

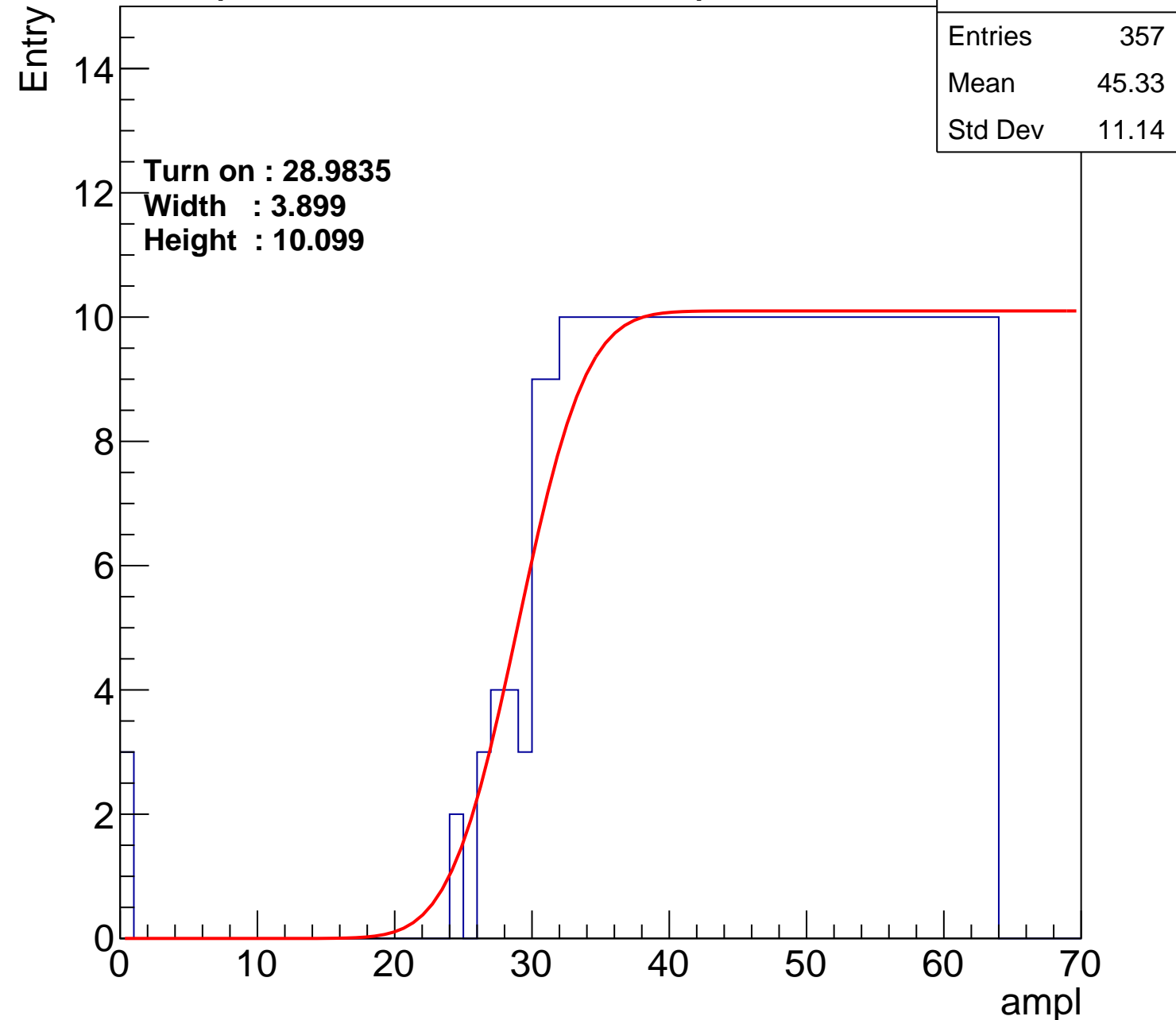
Width : 3.899

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.41
Std Dev	10.91

Turn on : 28.8001

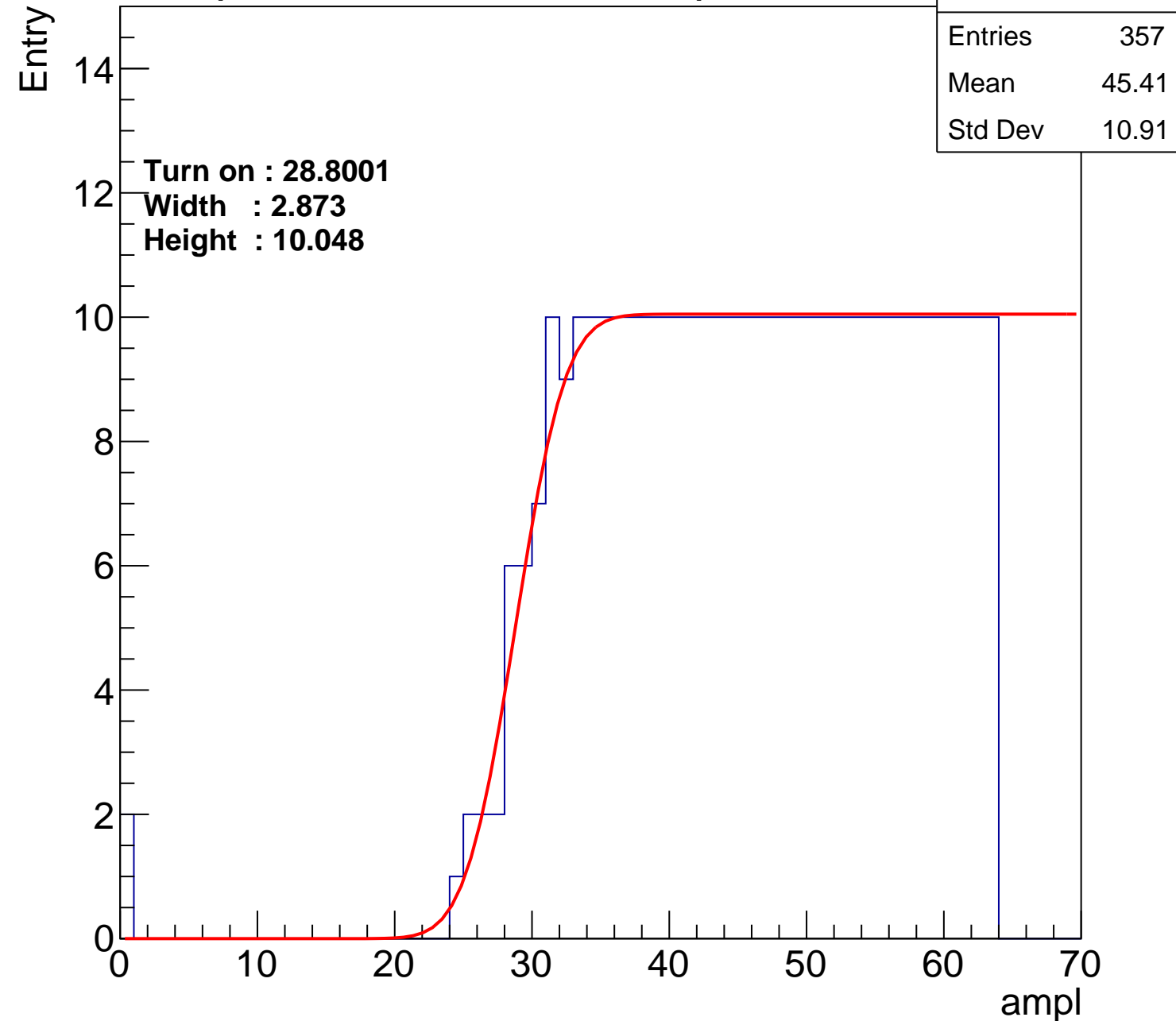
Width : 2.873

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.16
Std Dev	11.1

Turn on : 28.6739

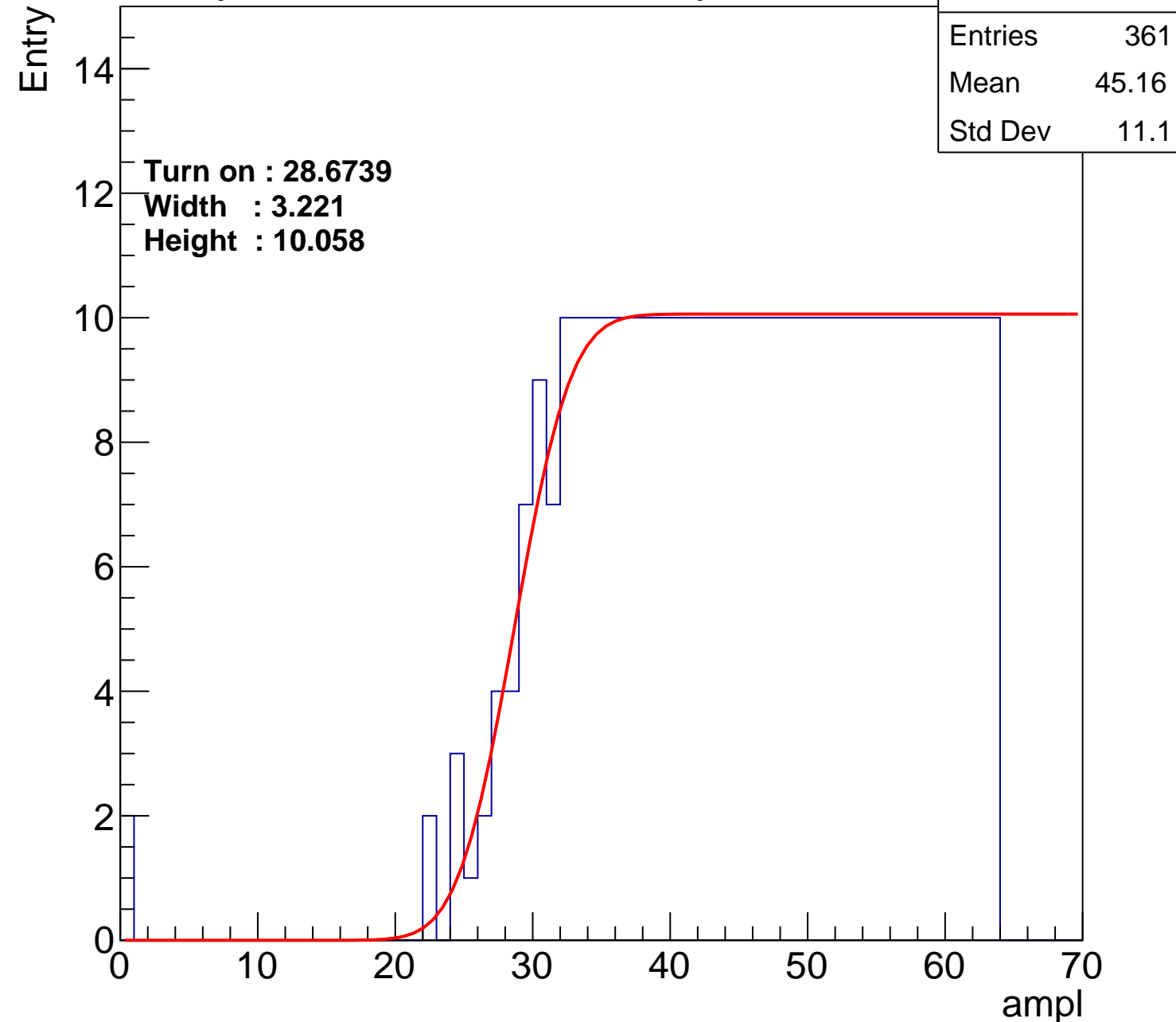
Width : 3.221

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.22
Std Dev	12.45

Turn on : 27.9497

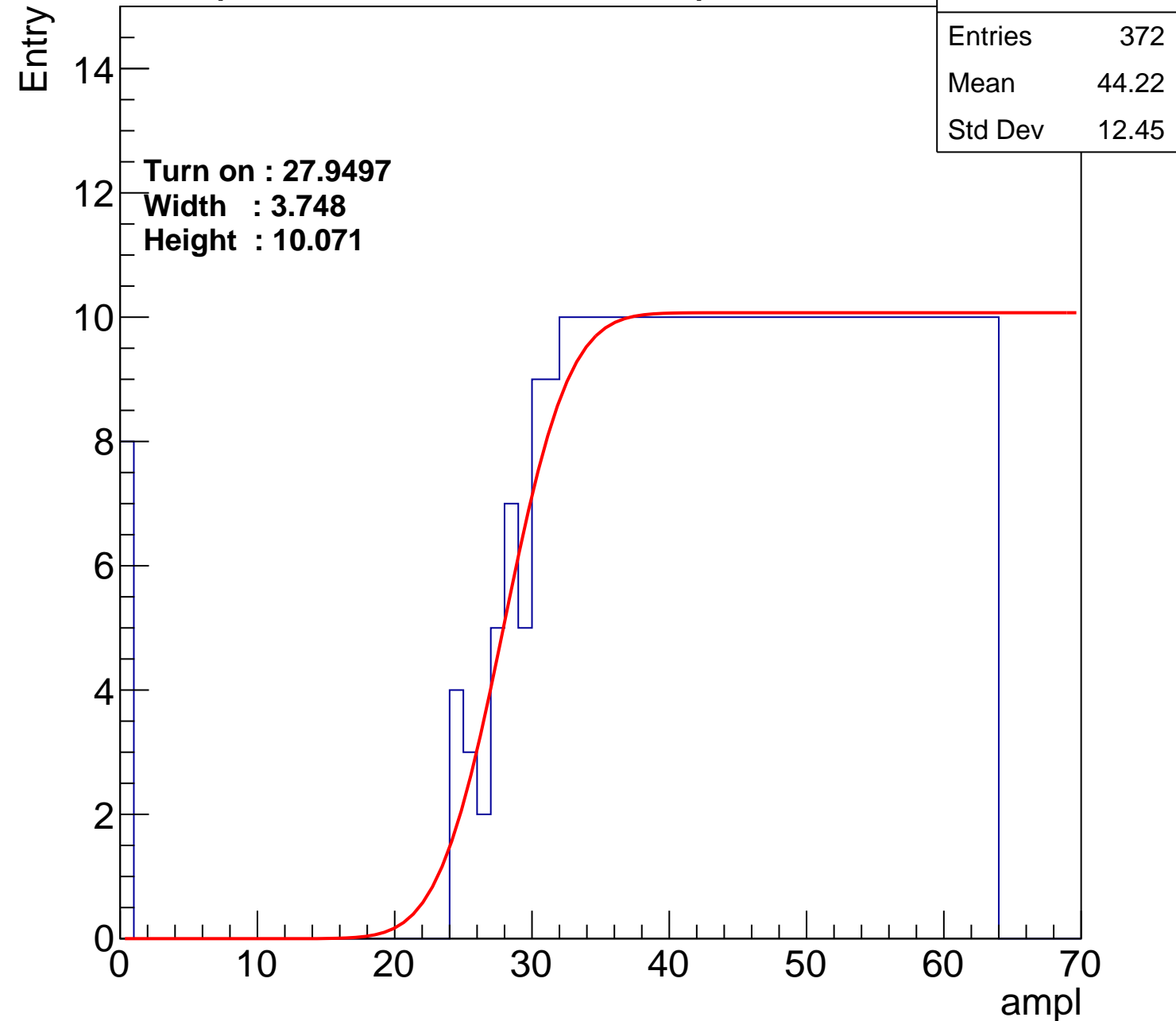
Width : 3.748

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch27

calib_packv5_042523_0143.root, FC#8, port C1

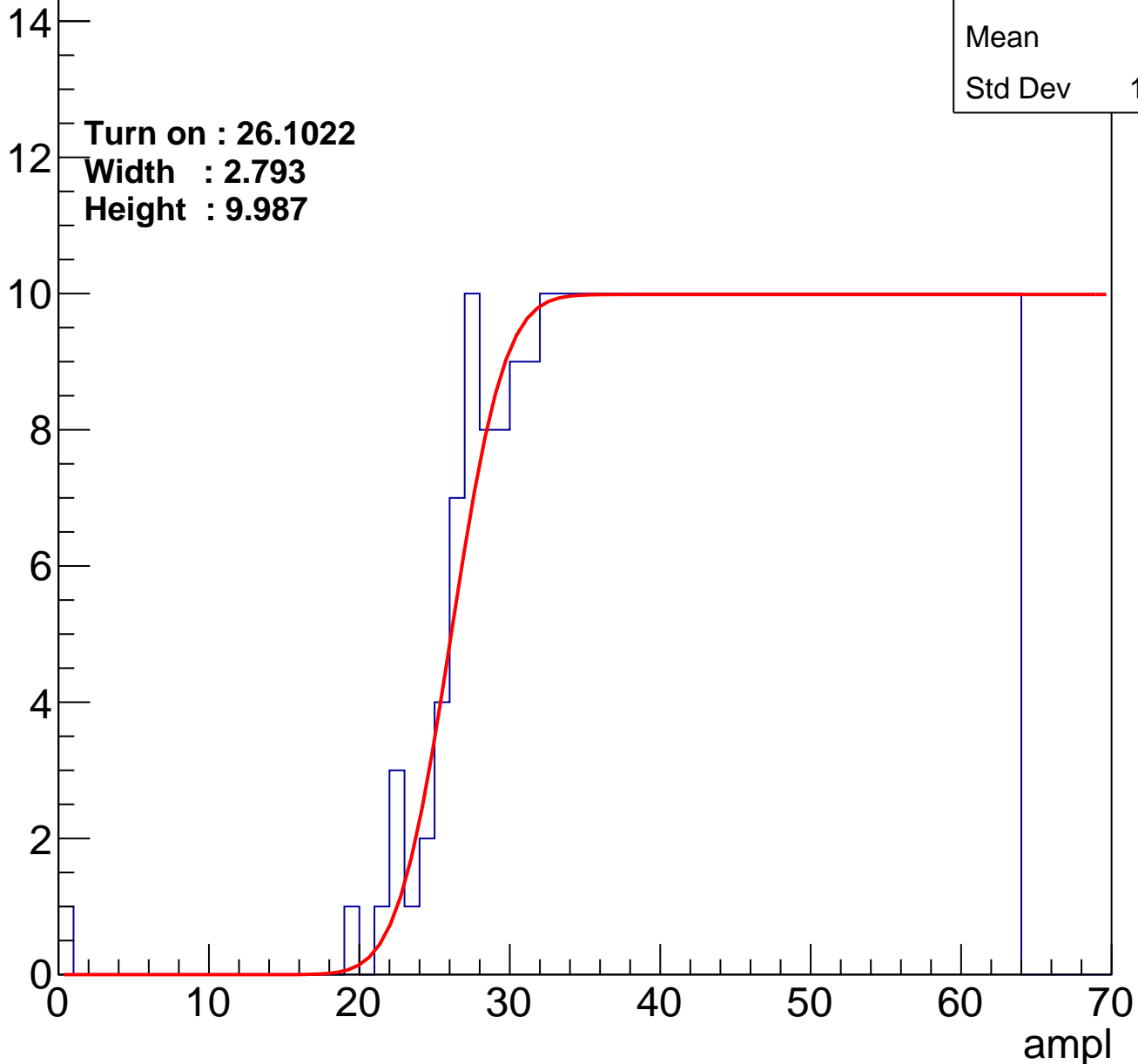
Entry

Entries	384
Mean	44.1
Std Dev	11.49

Turn on : 26.1022

Width : 2.793

Height : 9.987



B0L002S, U3-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.4
Std Dev	10.71

Turn on : 28.5825

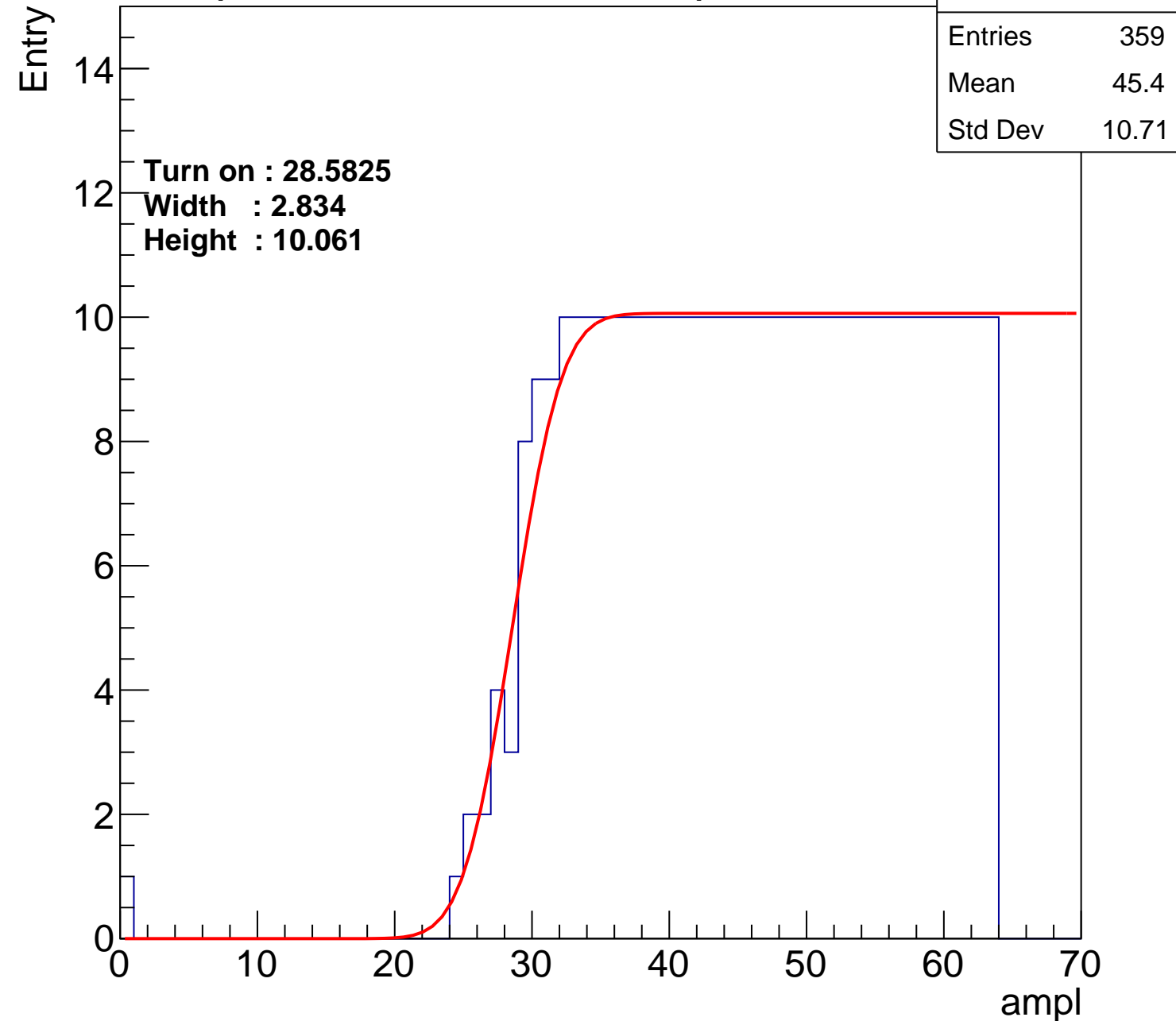
Width : 2.834

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch29

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.14
Std Dev	11.47

Turn on : 28.8272

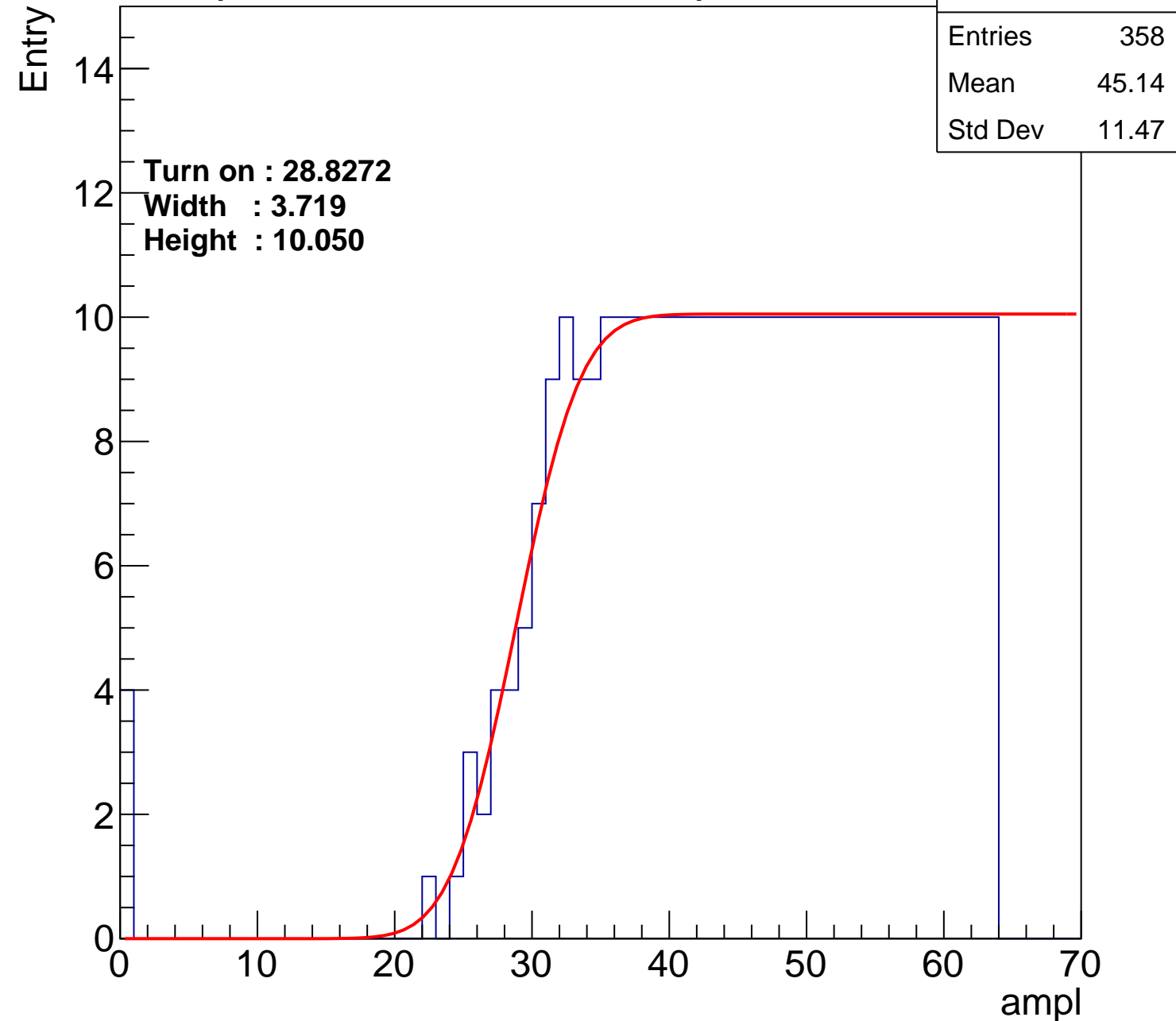
Width : 3.719

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch30

calib_packv5_042523_0143.root, FC#8, port C1

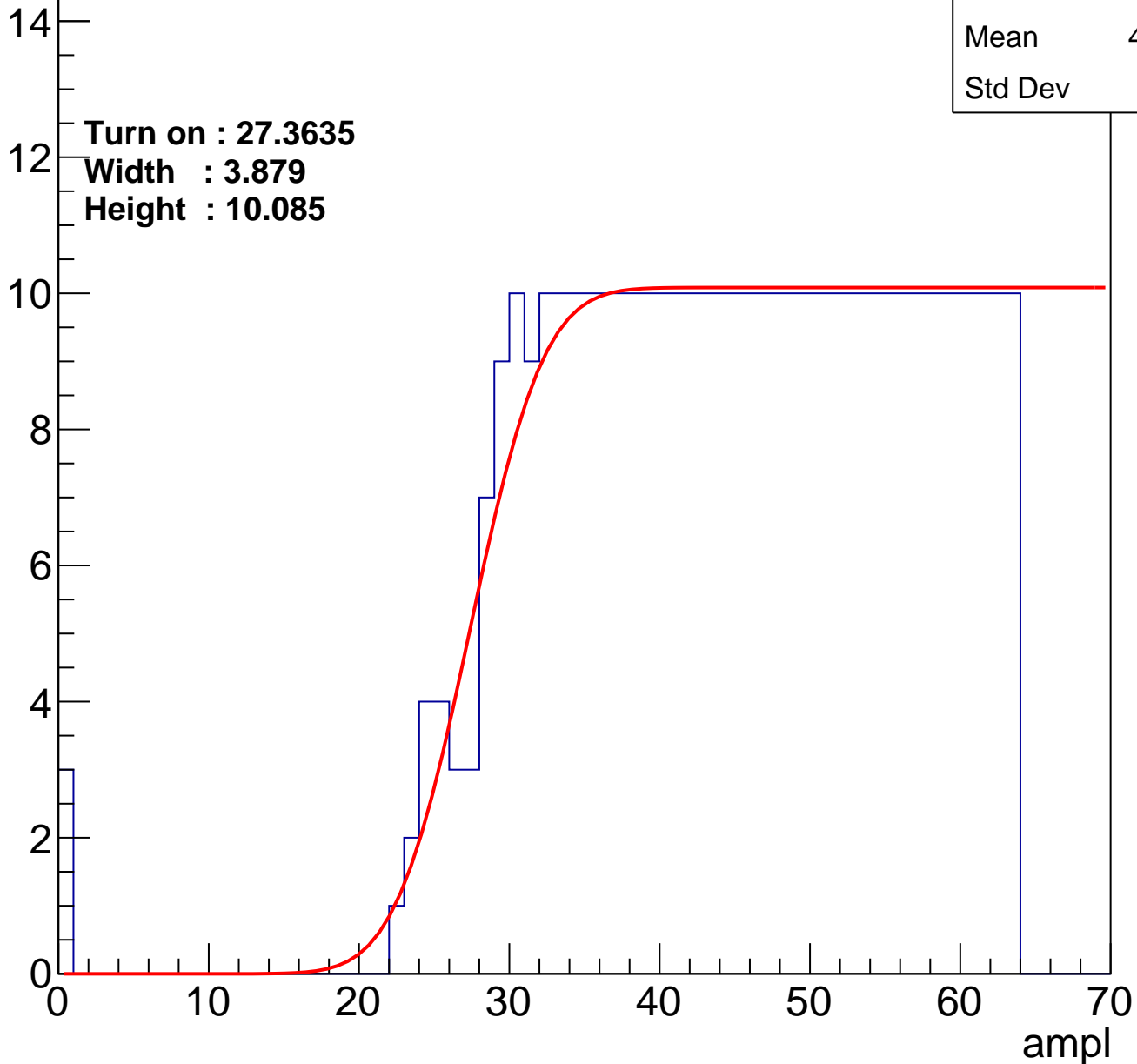
Entry

Entries	375
Mean	44.42
Std Dev	11.6

Turn on : 27.3635

Width : 3.879

Height : 10.085



B0L002S, U3-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	336
Mean	46.5
Std Dev	10.16

Turn on : 30.8109

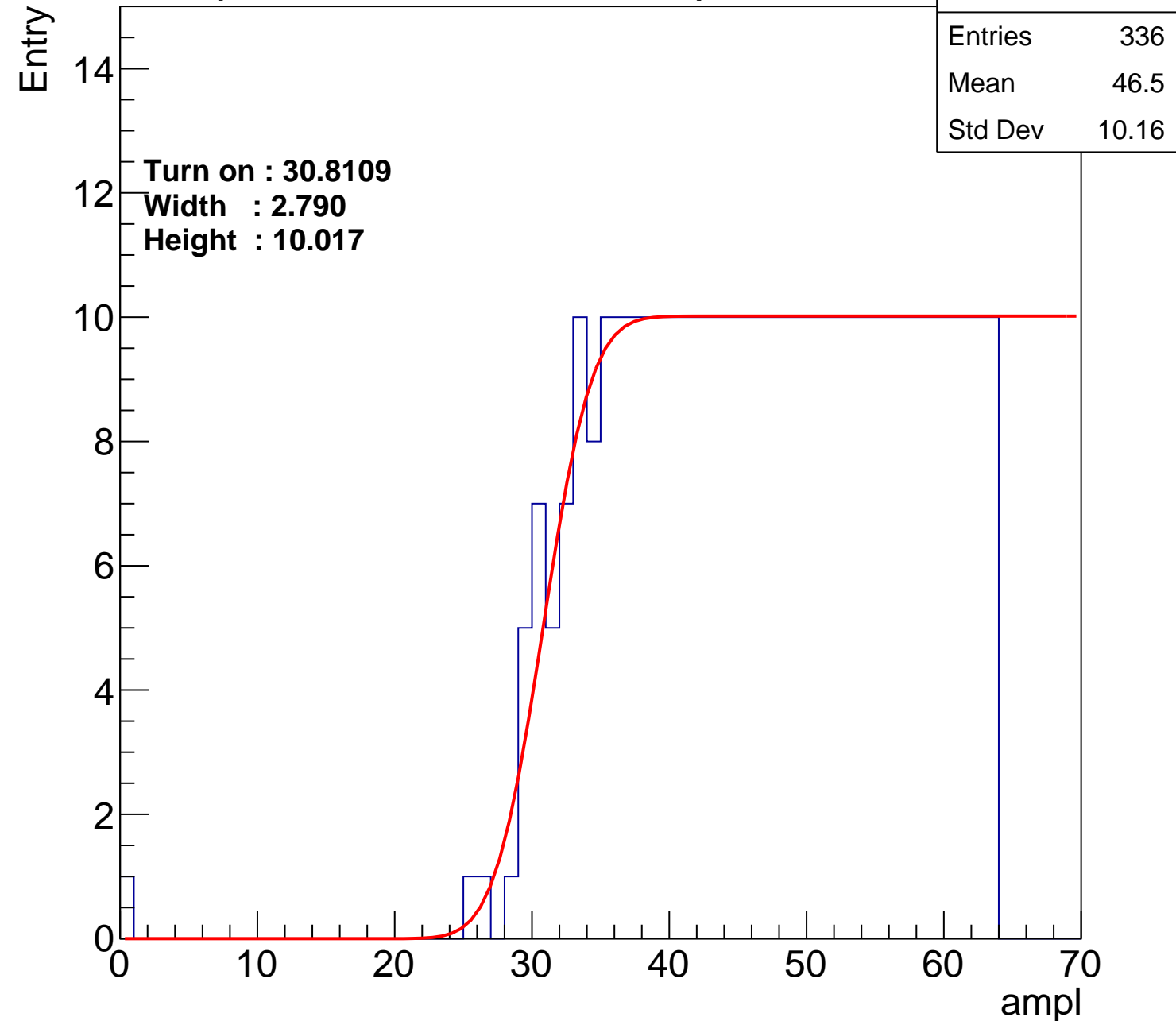
Width : 2.790

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.6
Std Dev	11.53

Turn on : 27.4994

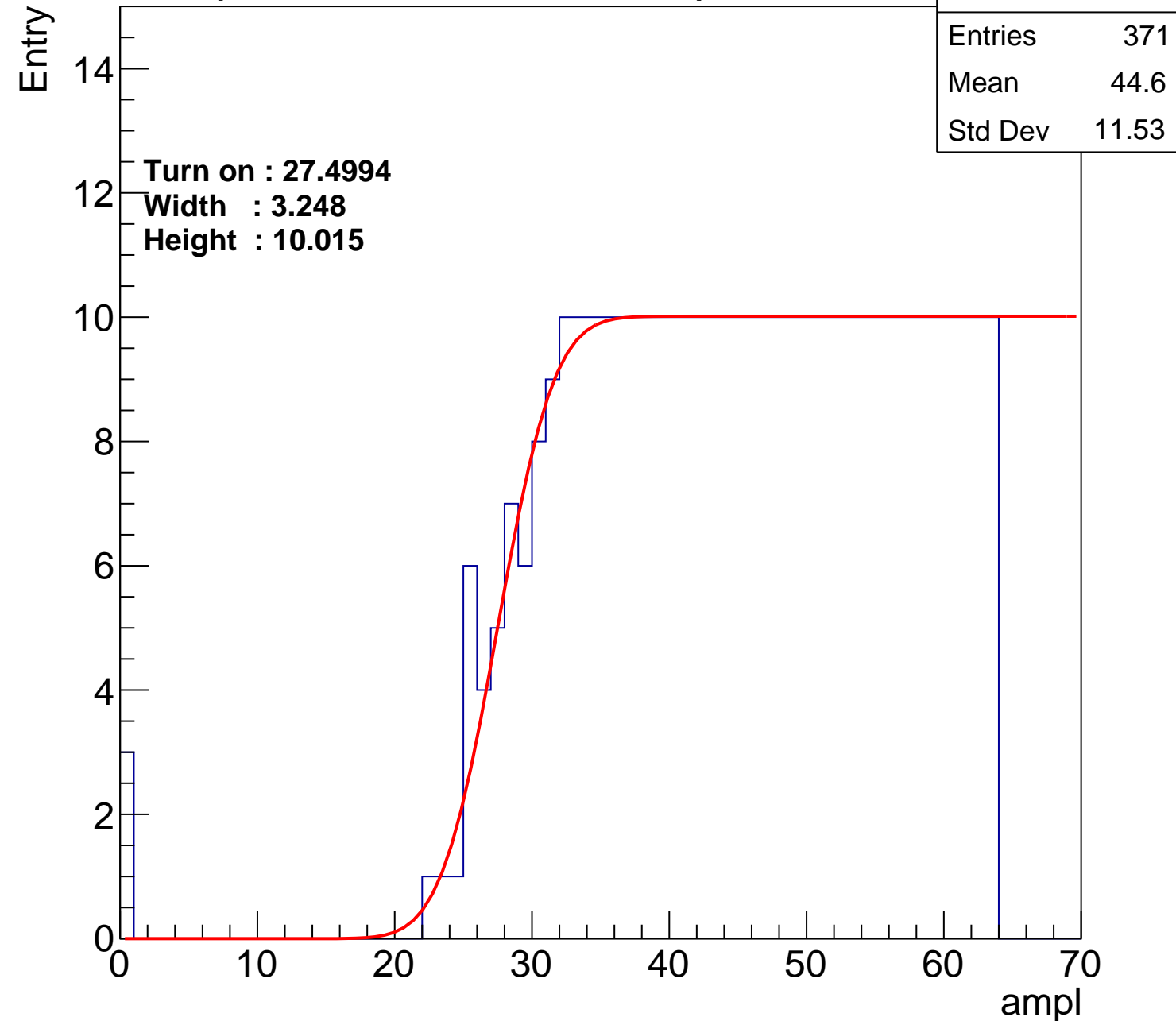
Width : 3.248

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.38
Std Dev	10.74

Turn on : 28.4538

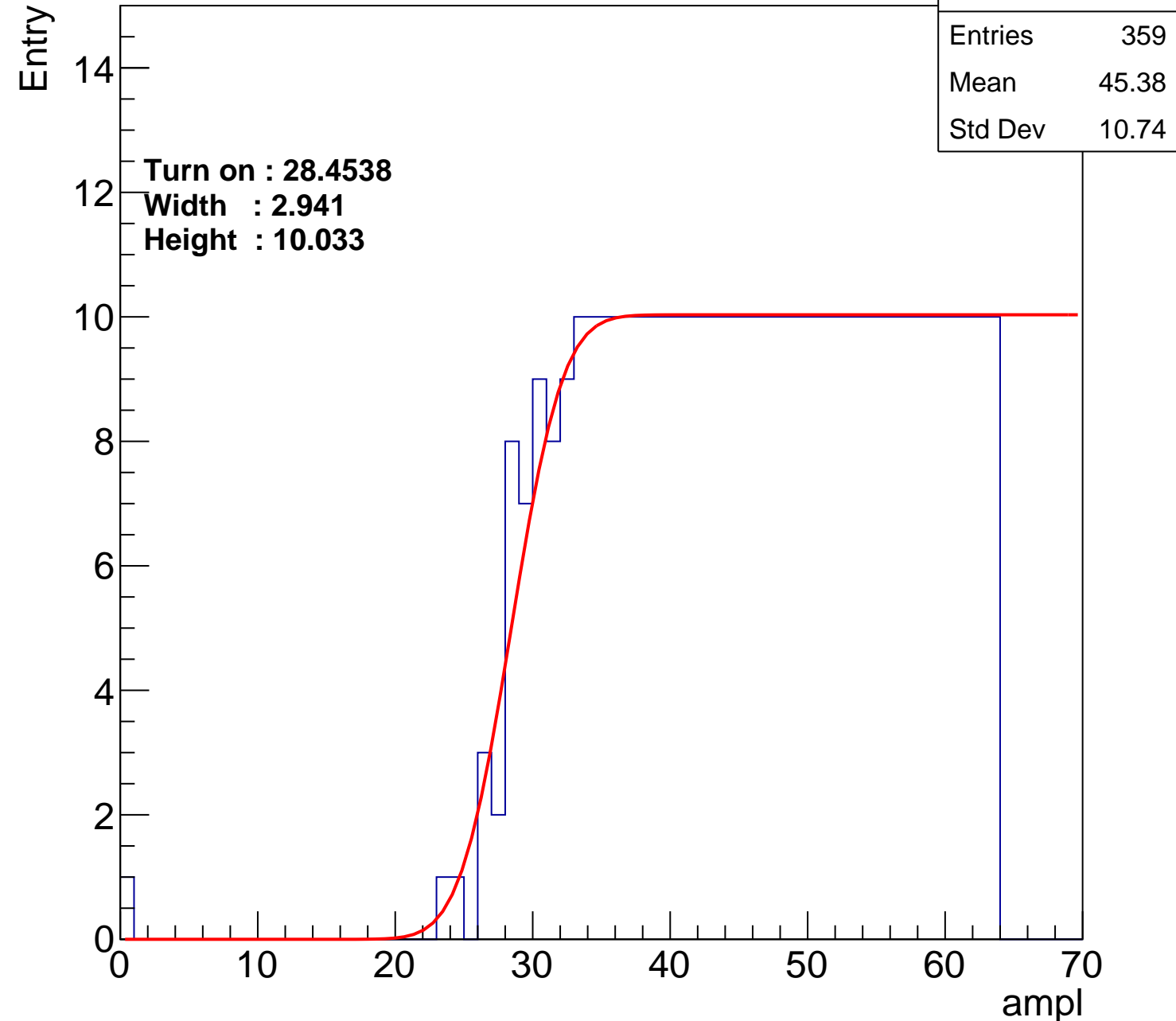
Width : 2.941

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.3234

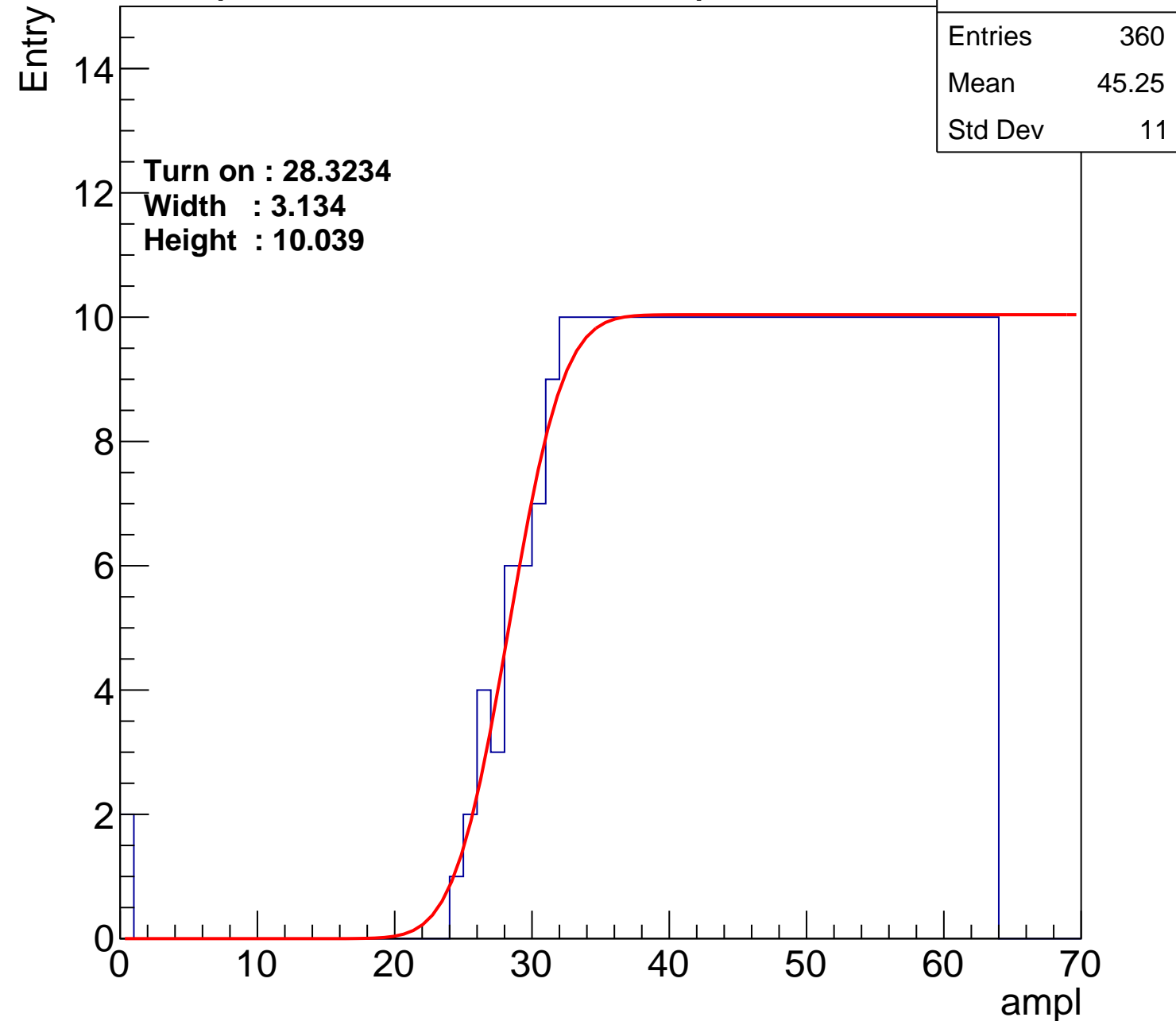
Width : 3.134

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.6267

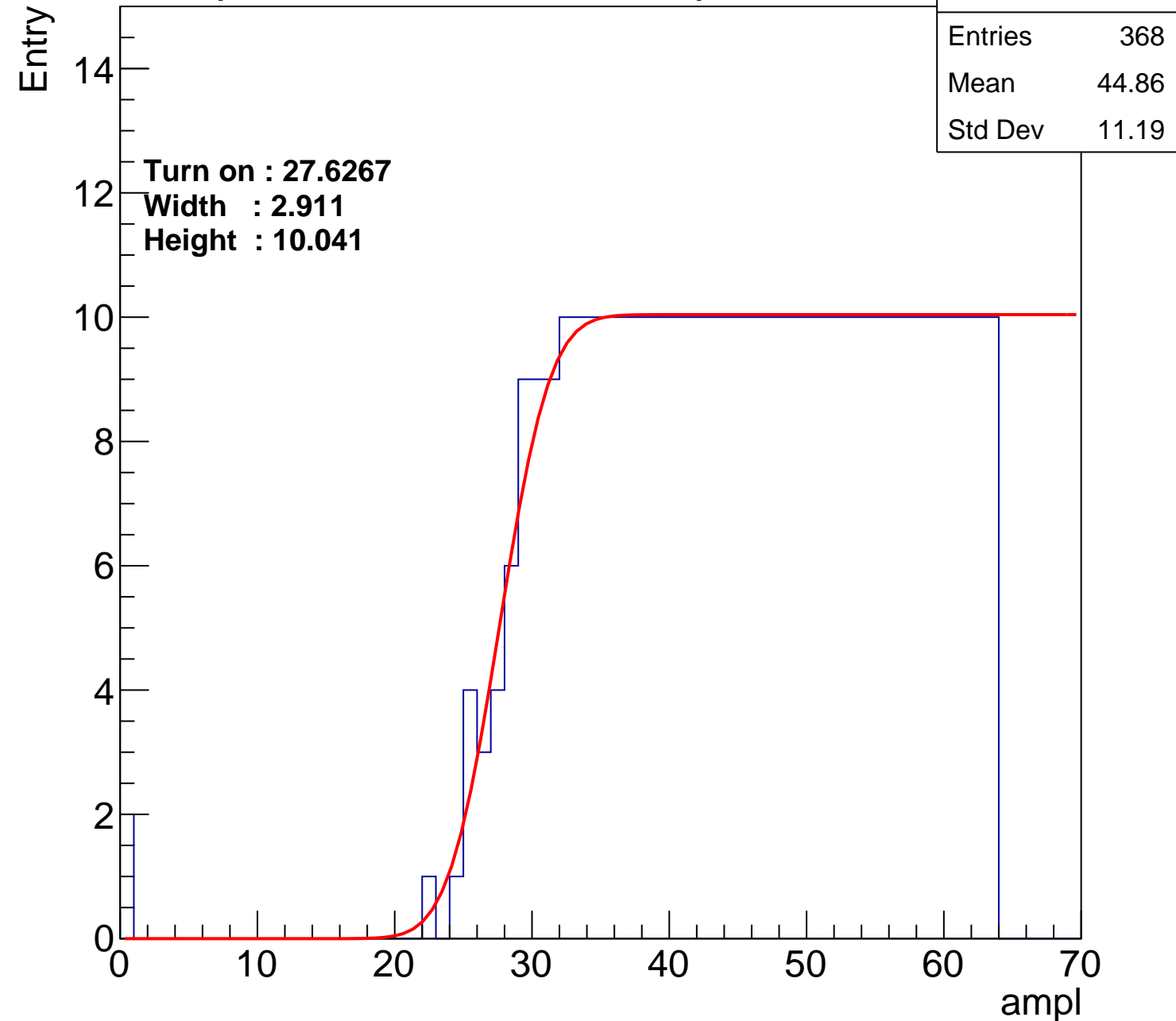
Width : 2.911

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.11
Std Dev	11.73

Turn on : 26.8505

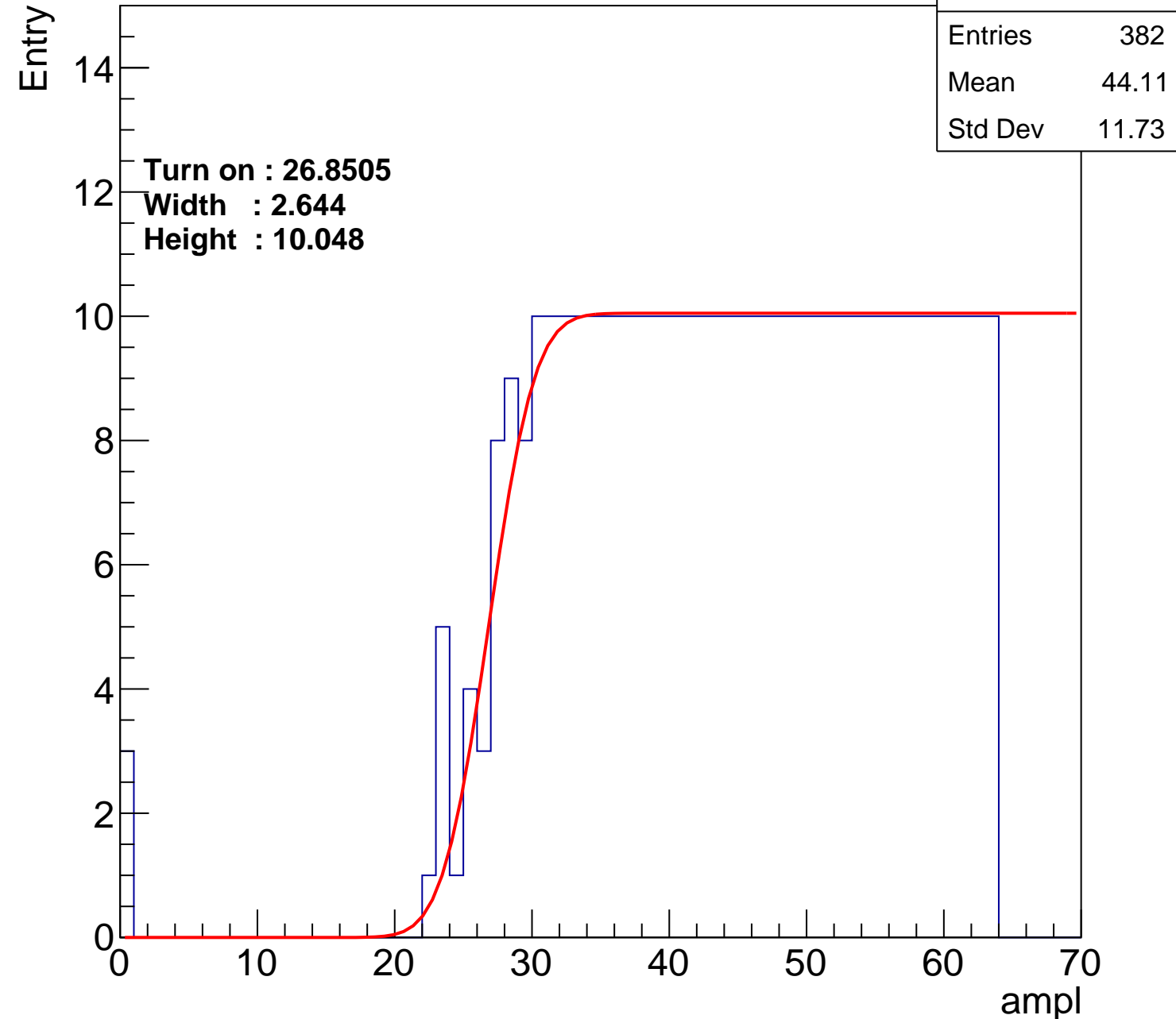
Width : 2.644

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.2404

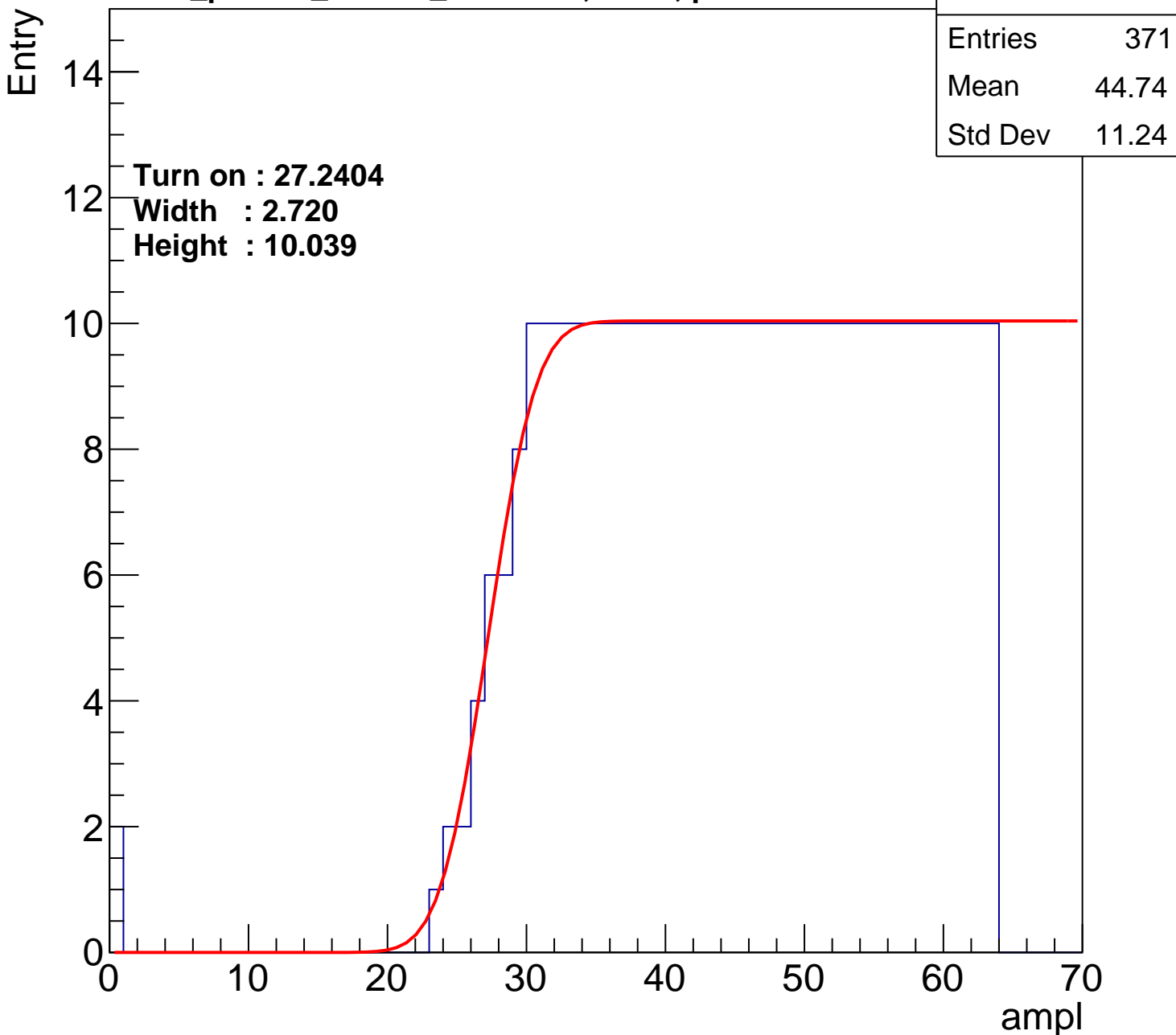
Width : 2.720

Height : 10.039

Entries	371
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Mean	44.74
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Std Dev	11.24
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B0L002S, U3-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.58
Std Dev	10.63

Turn on : 28.9515

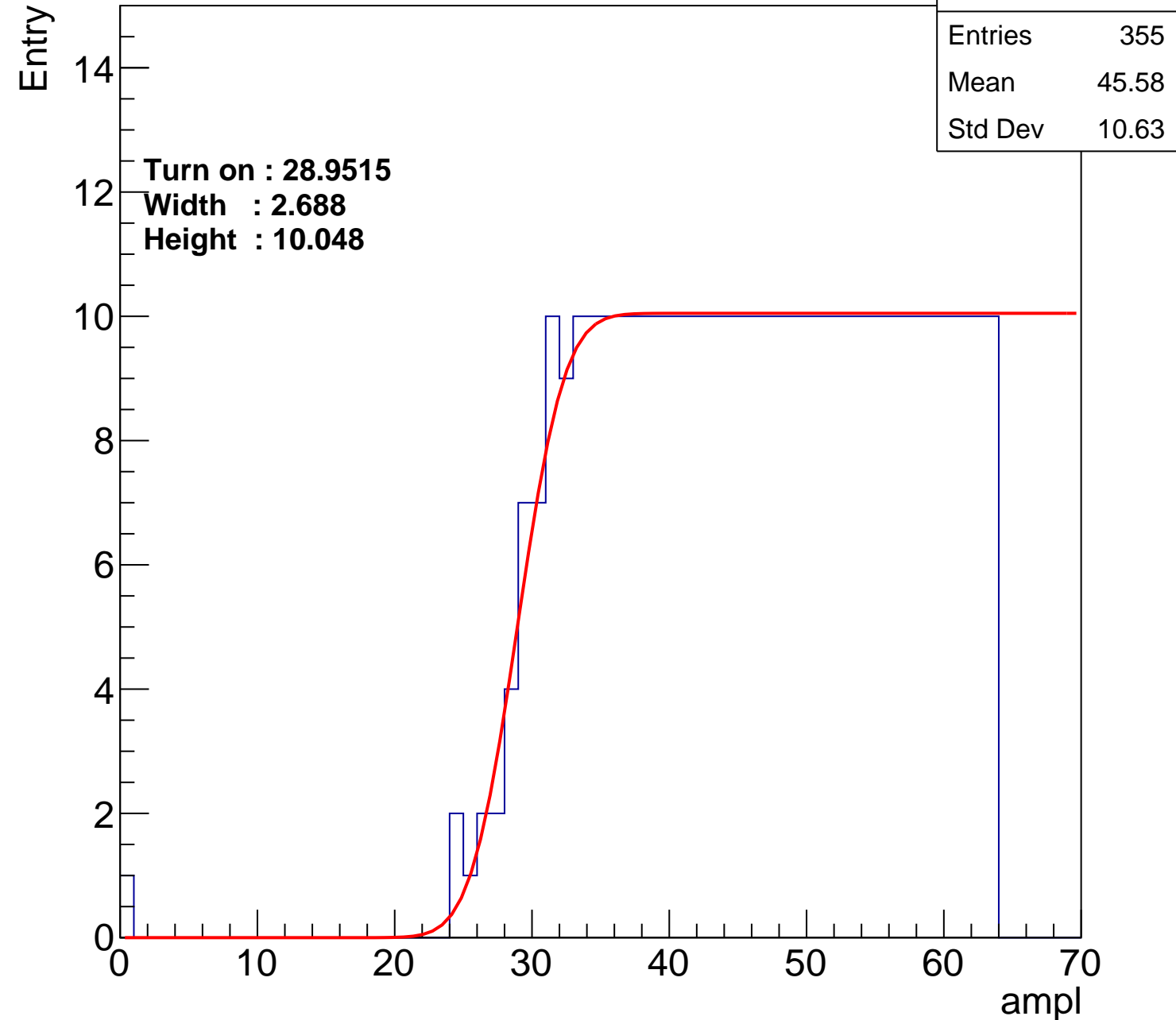
Width : 2.688

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch39

calib_packv5_042523_0143.root, FC#8, port C1

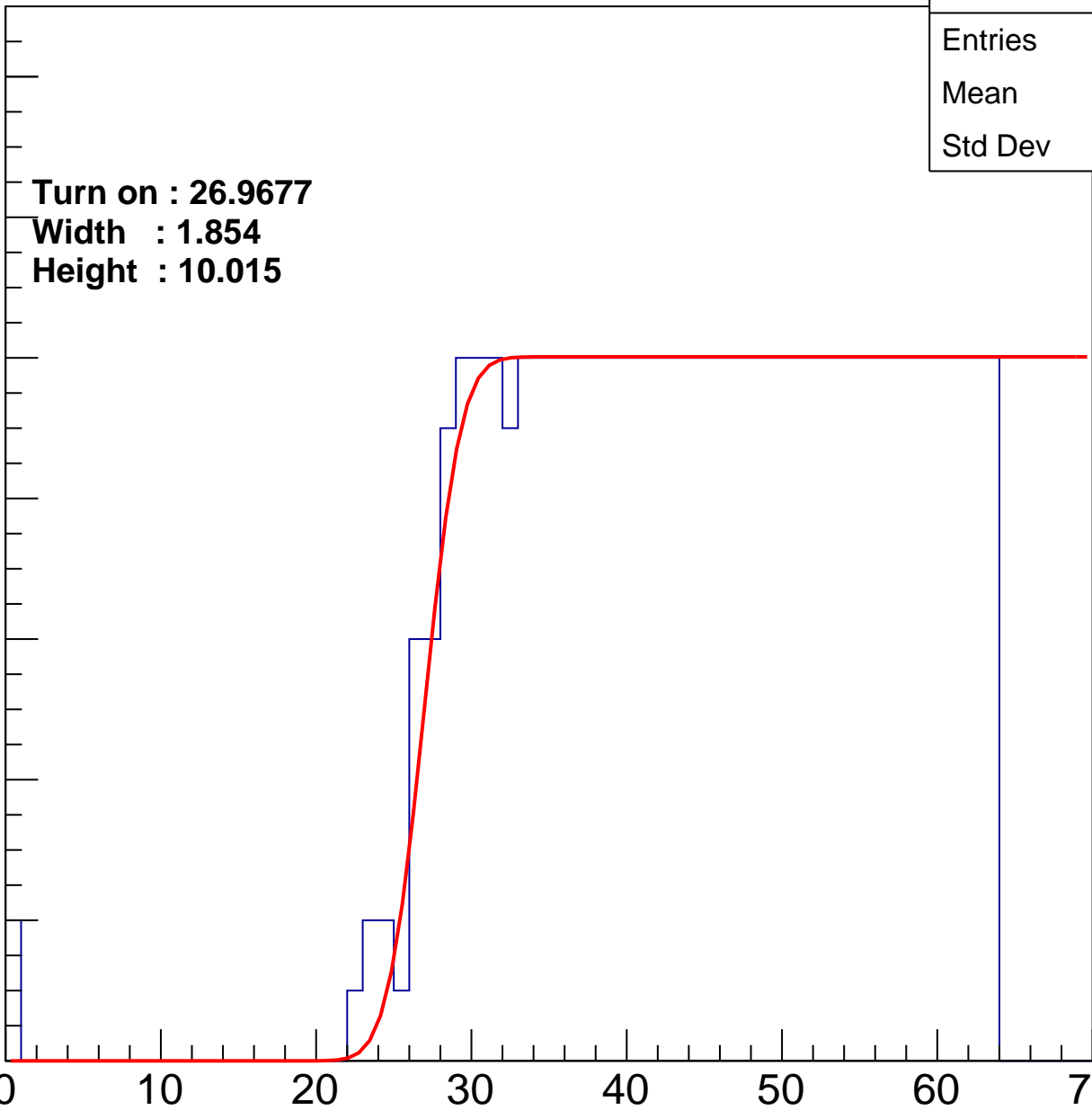
Entry

14
12
10
8
6
4
2
0

Turn on : 26.9677
Width : 1.854
Height : 10.015

Entries	378
Mean	44.39
Std Dev	11.42

ampl



B0L002S, U3-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 25.6404

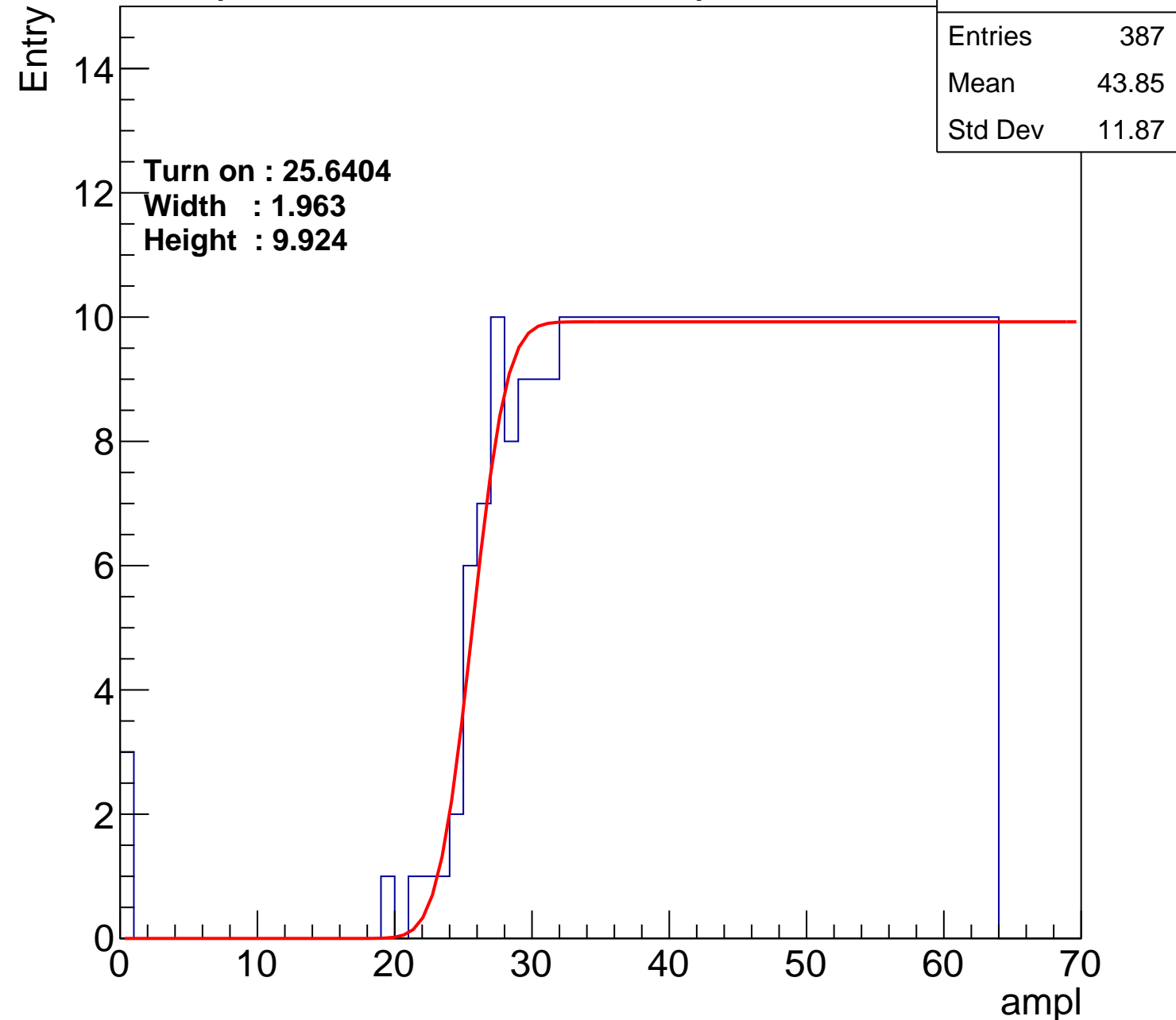
Width : 1.963

Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	391
Mean	43.47
Std Dev	12.44

Turn on : 25.4772

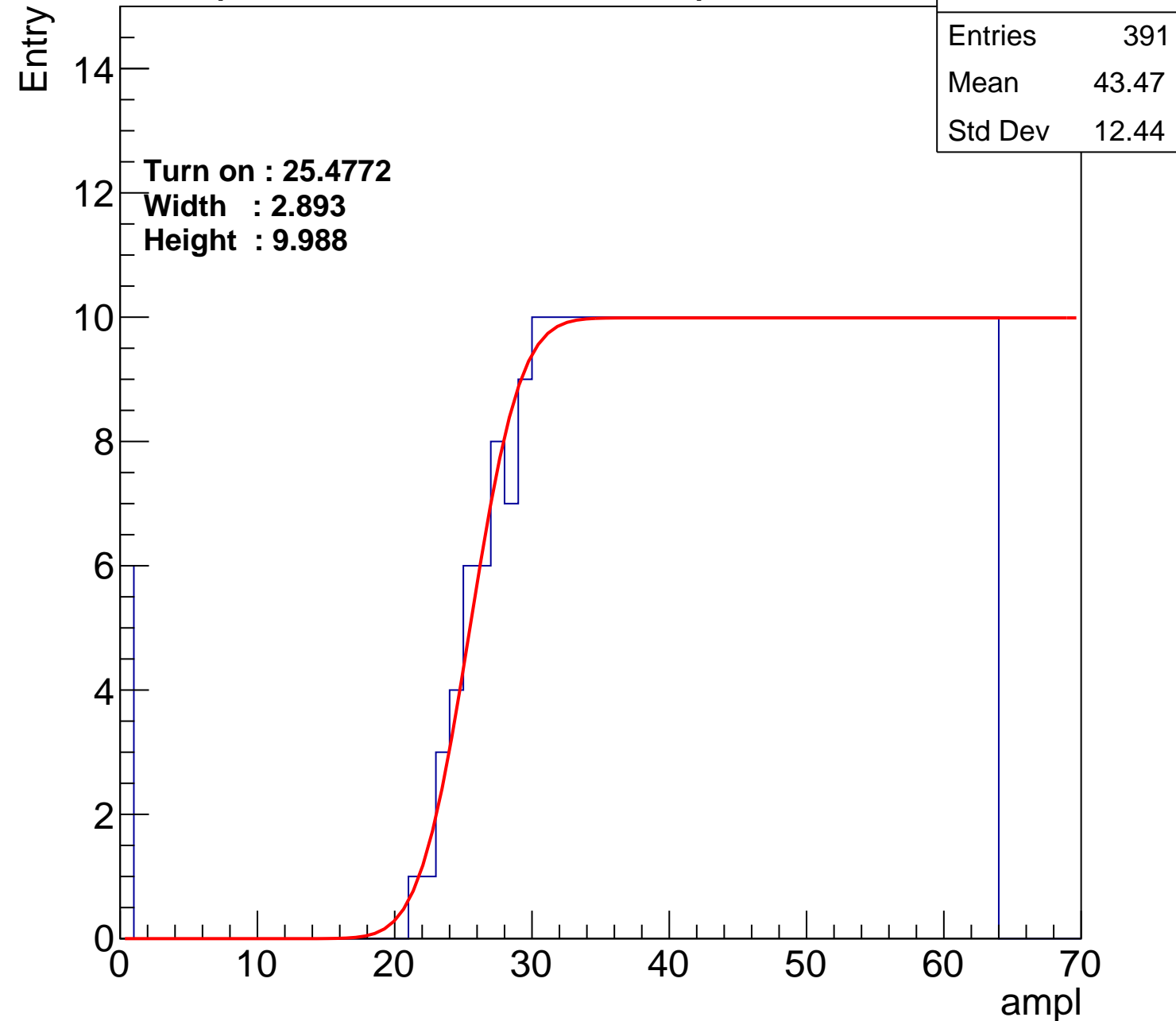
Width : 2.893

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch42

calib_packv5_042523_0143.root, FC#8, port C1

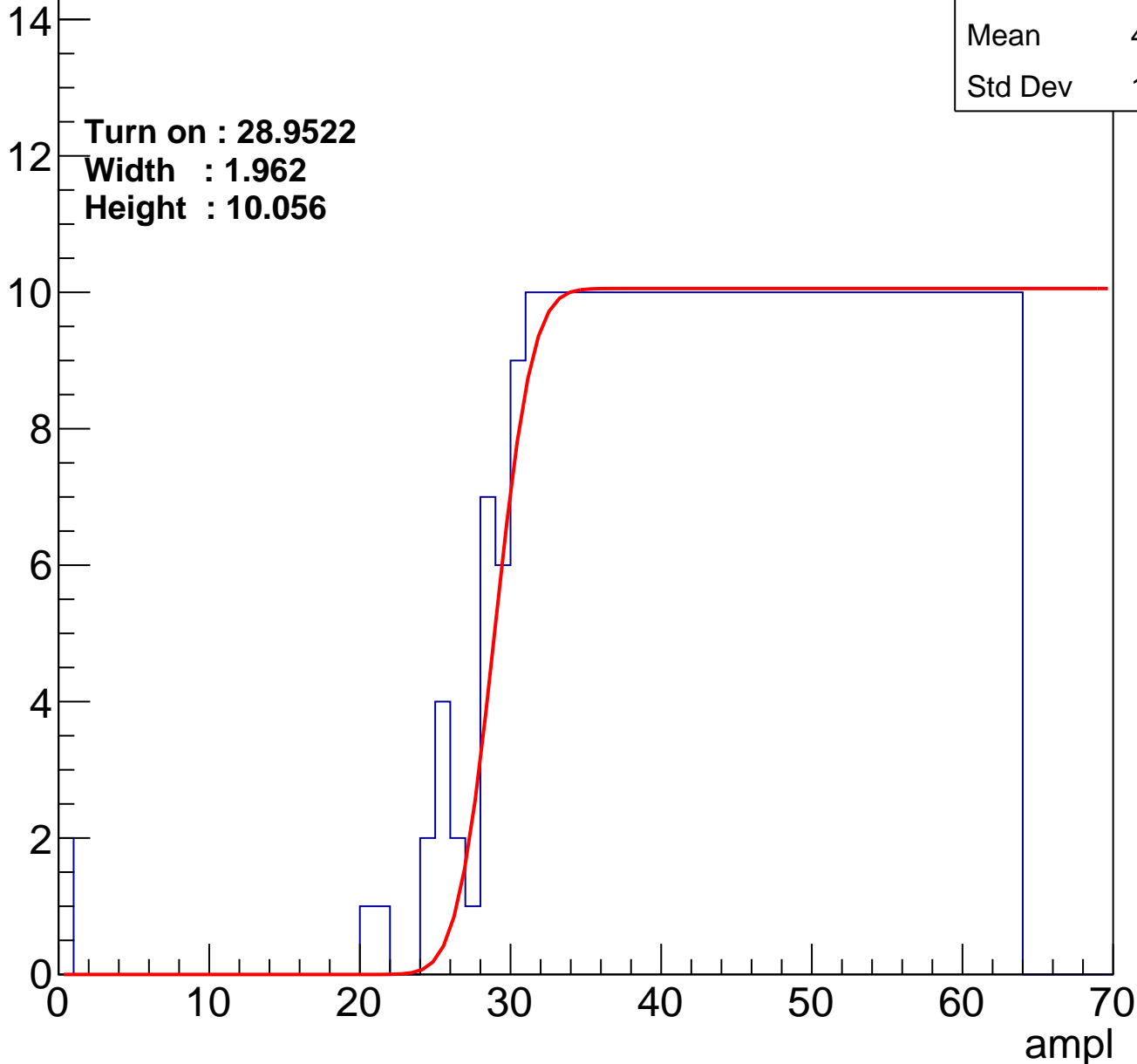
Entry

Entries	365
Mean	44.98
Std Dev	11.18

Turn on : 28.9522

Width : 1.962

Height : 10.056



B0L002S, U3-ch43

calib_packv5_042523_0143.root, FC#8, port C1

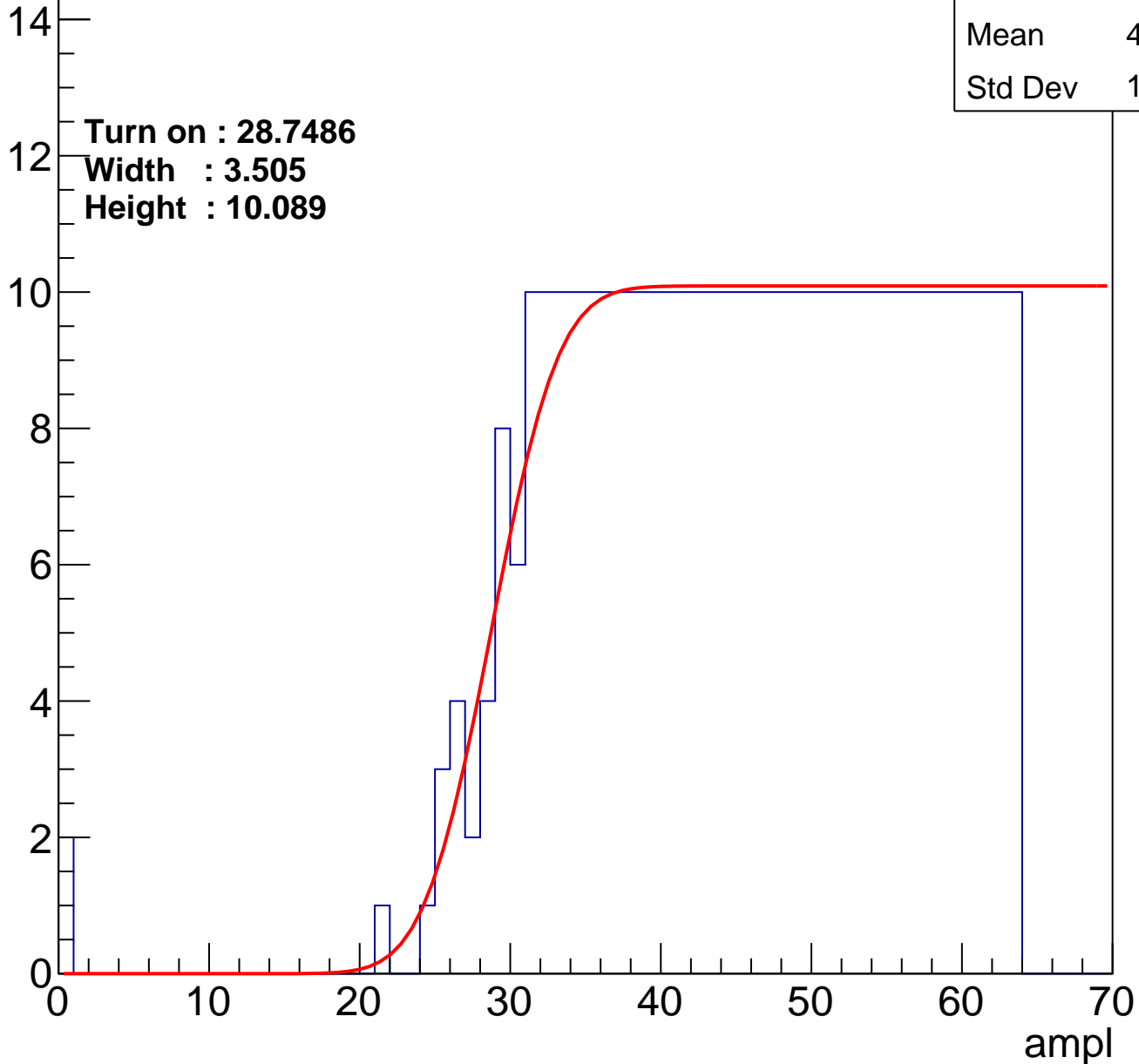
Entries	361
Mean	45.19
Std Dev	11.06

Turn on : 28.7486

Width : 3.505

Height : 10.089

Entry



B0L002S, U3-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.8421

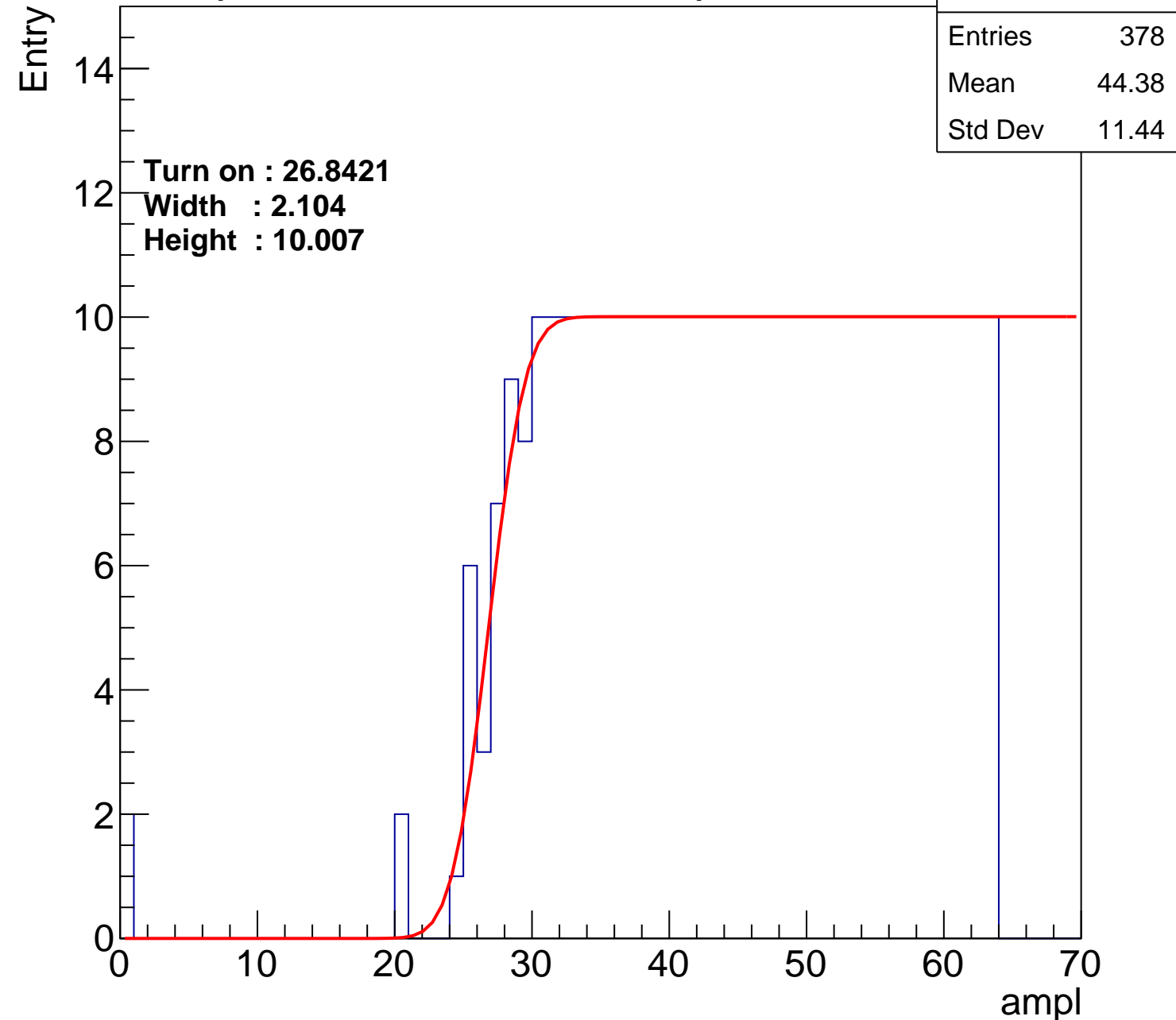
Width : 2.104

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.65
Std Dev	10.99

Turn on : 29.8868

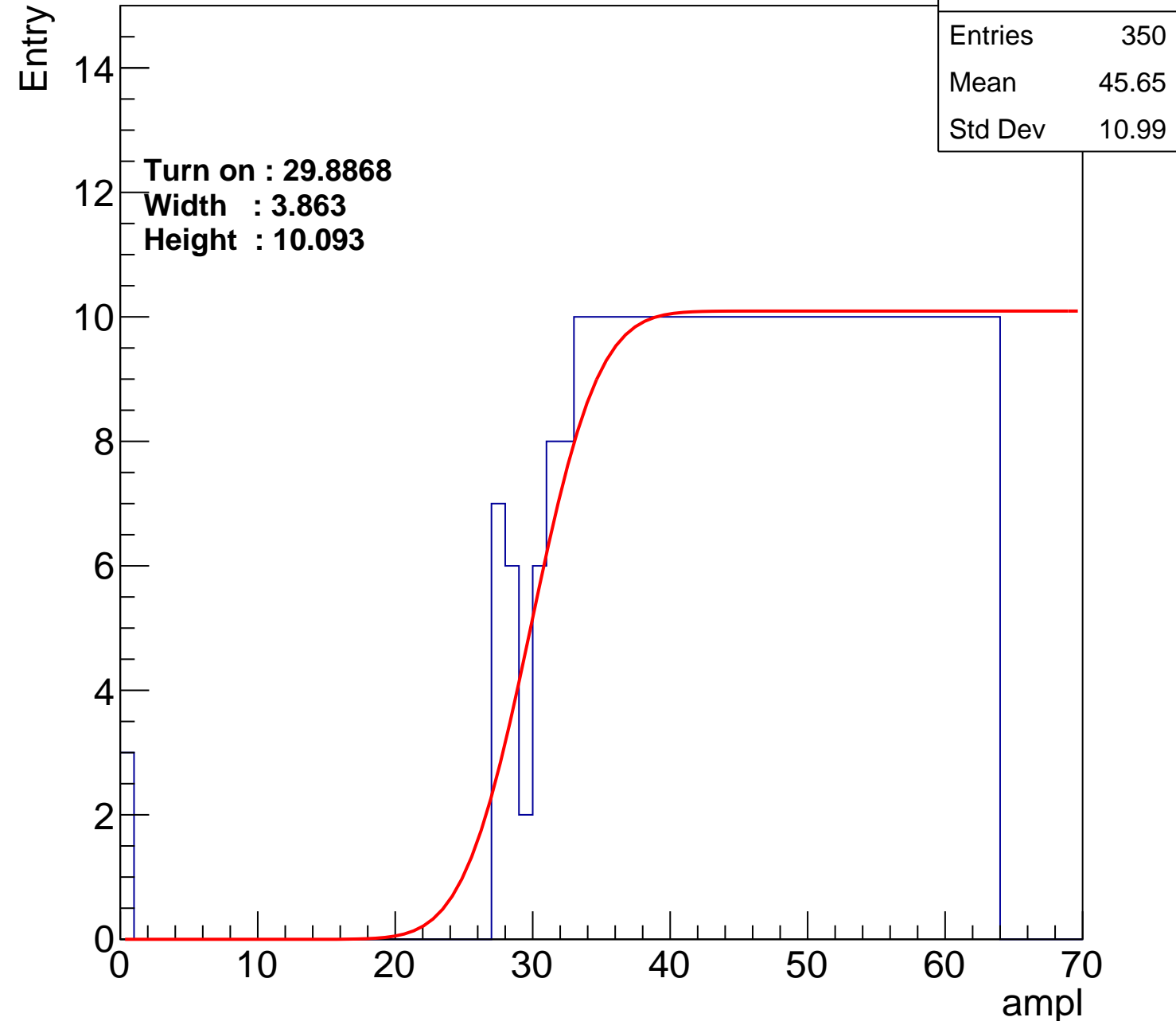
Width : 3.863

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch46

calib_packv5_042523_0143.root, FC#8, port C1

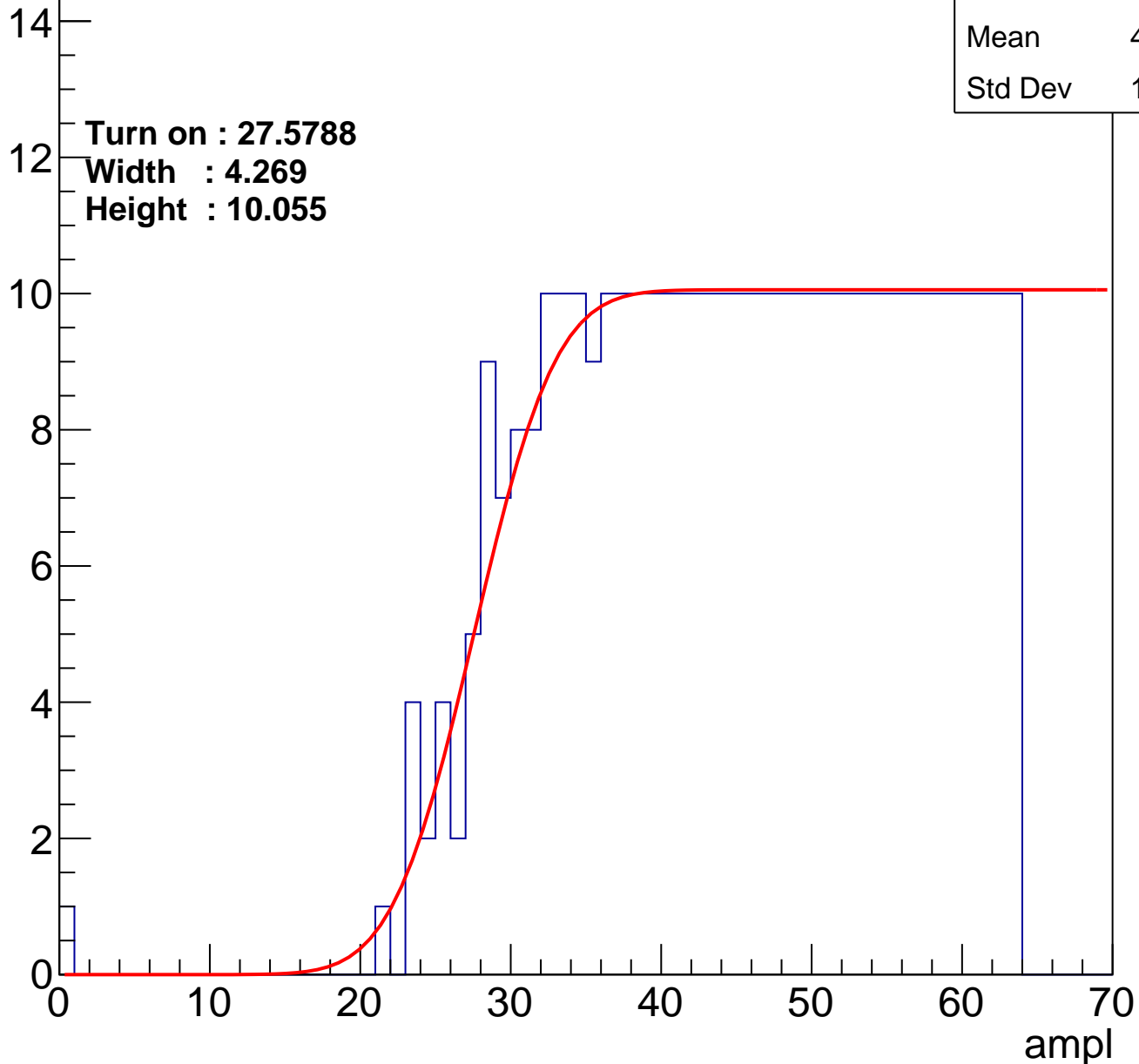
Entries	370
Mean	44.75
Std Dev	11.18

Turn on : 27.5788

Width : 4.269

Height : 10.055

Entry



B0L002S, U3-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 27.7992

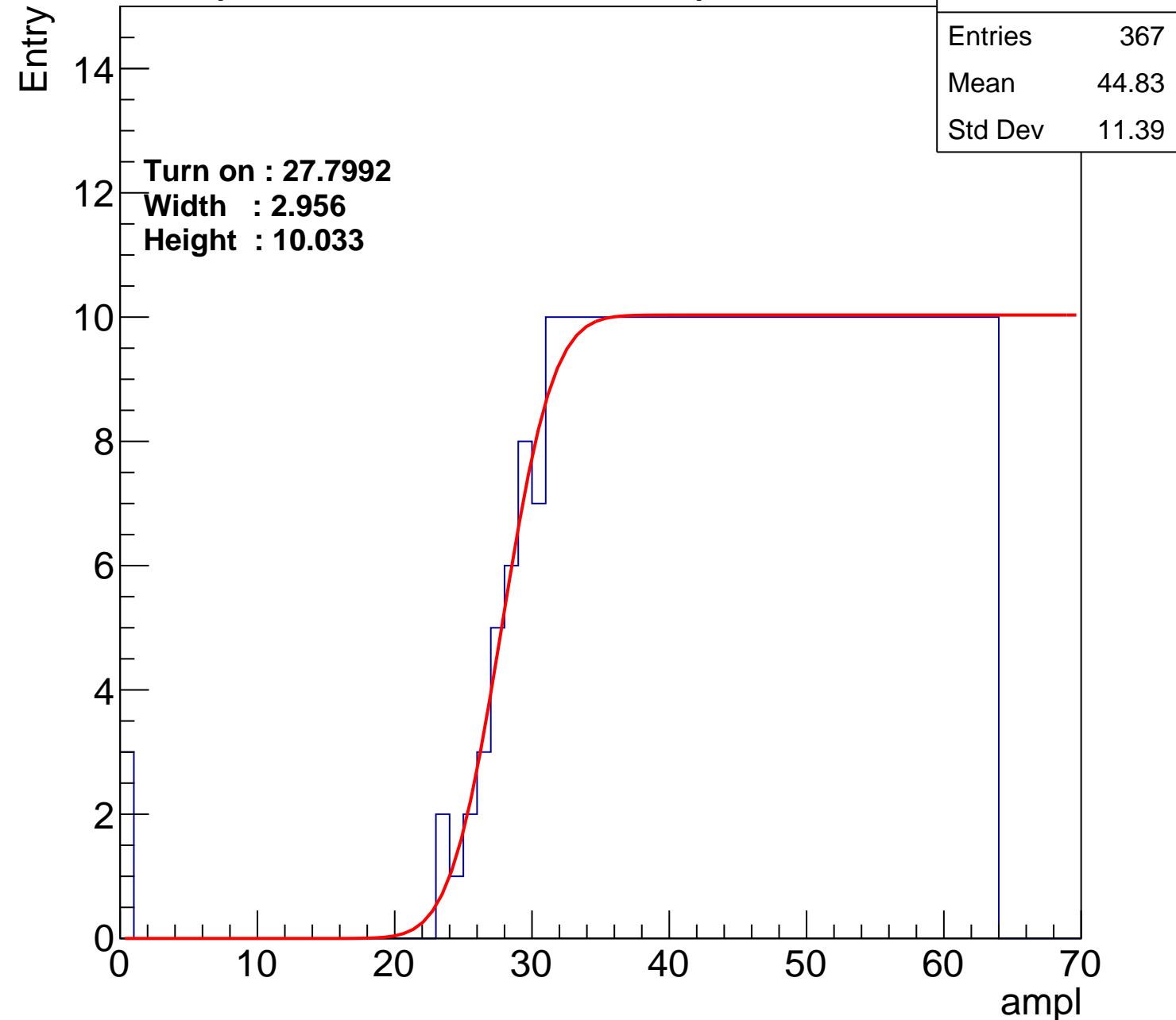
Width : 2.956

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch48

calib_packv5_042523_0143.root, FC#8, port C1

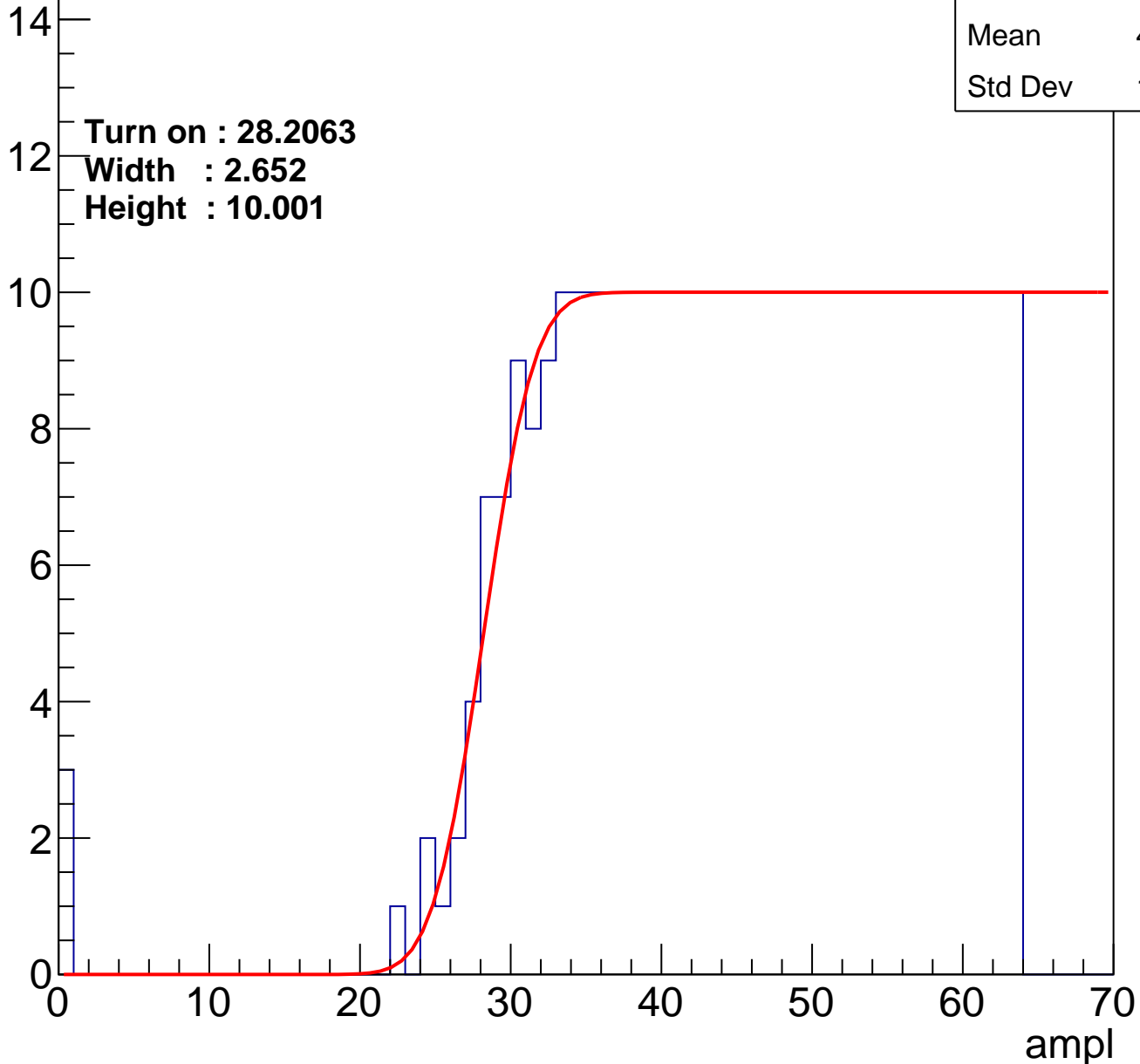
Entries	363
Mean	45.01
Std Dev	11.31

Turn on : 28.2063

Width : 2.652

Height : 10.001

Entry



B0L002S, U3-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.45
Std Dev	11.41

Turn on : 26.5149

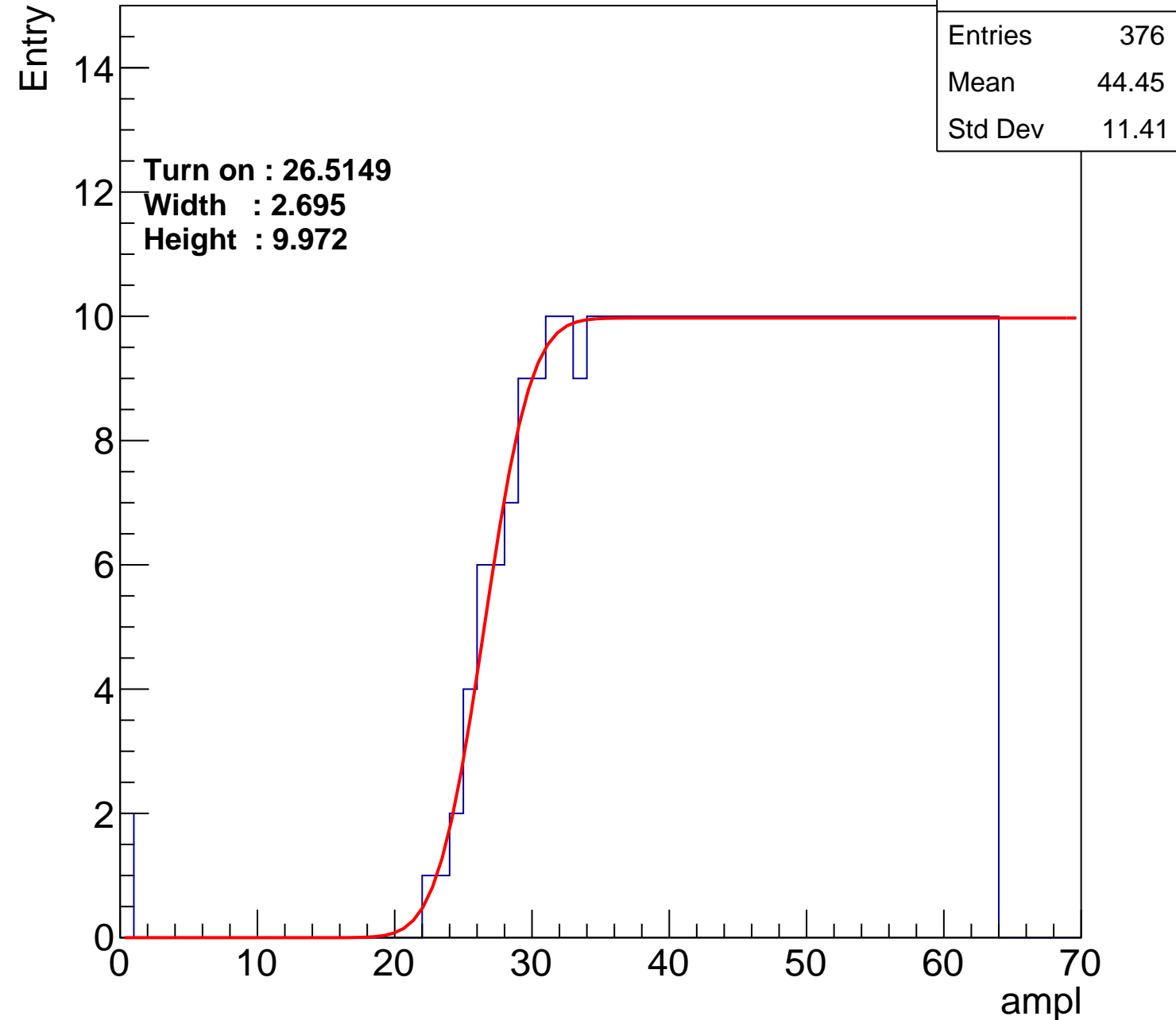
Width : 2.695

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	389
Mean	43.73
Std Dev	12.02

Turn on : 25.0997

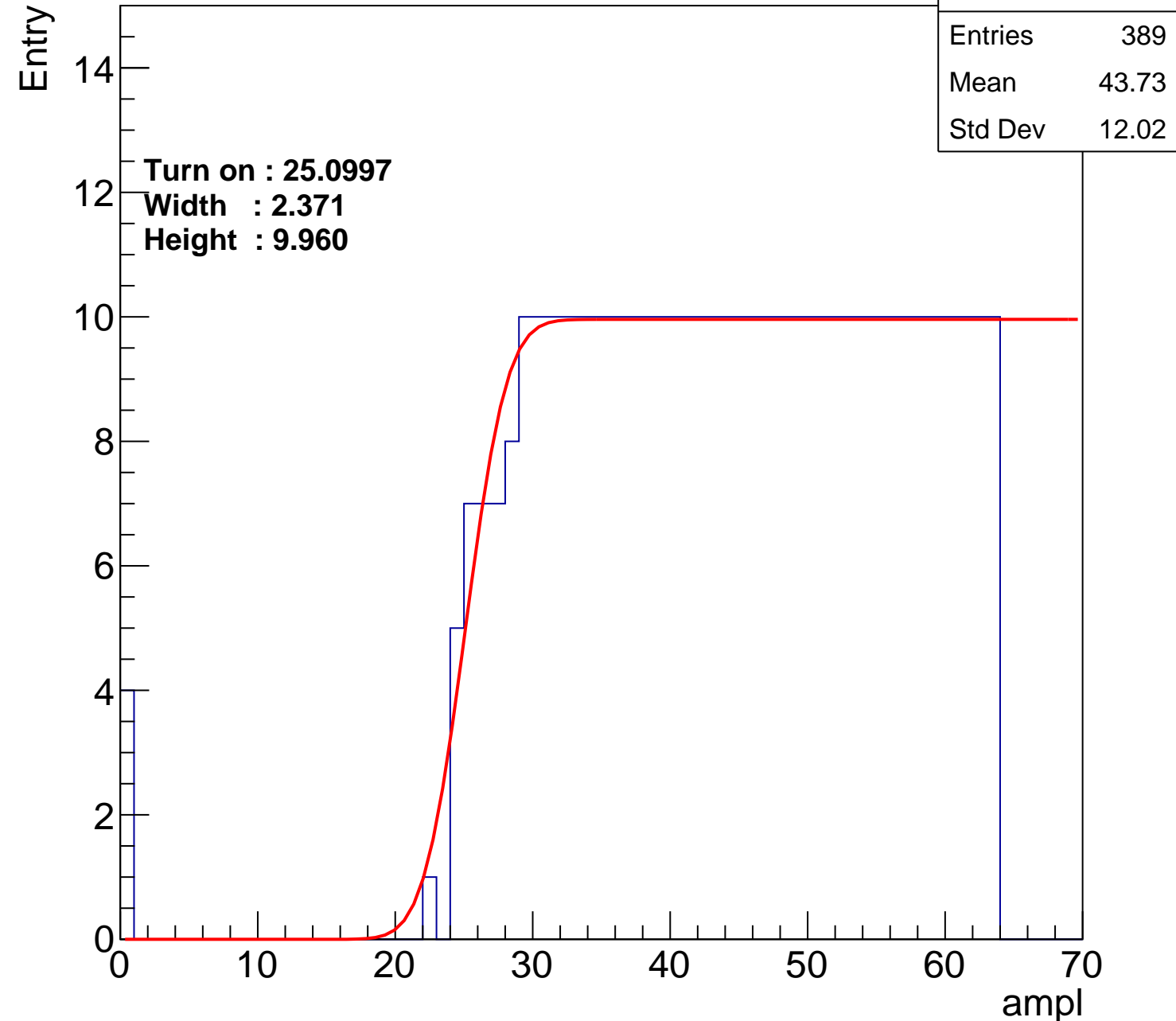
Width : 2.371

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.95
Std Dev	11.3

Turn on : 27.0833

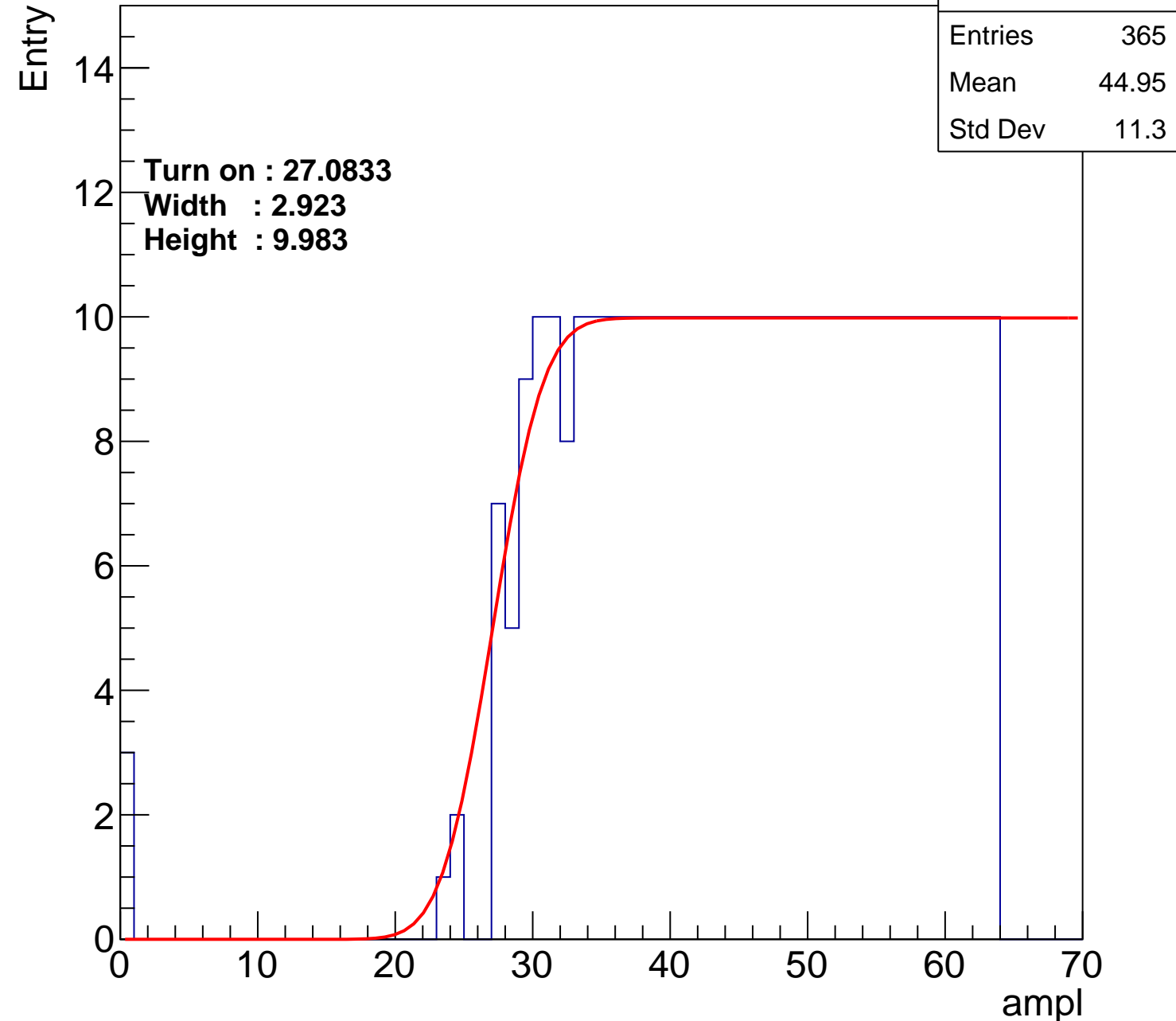
Width : 2.923

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.68
Std Dev	11.4

Turn on : 27.1132

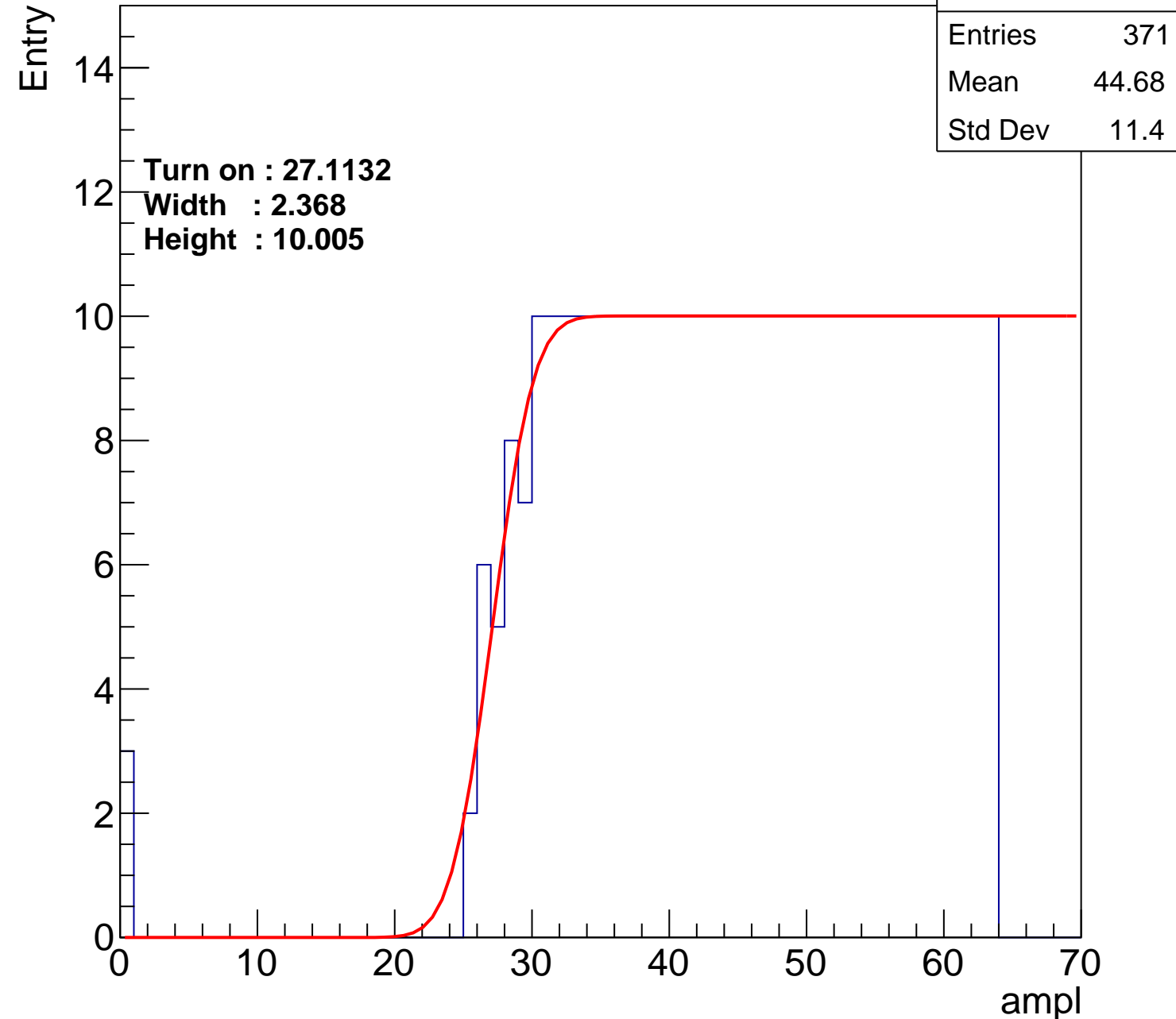
Width : 2.368

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.56
Std Dev	11.49

Turn on : 27.2391

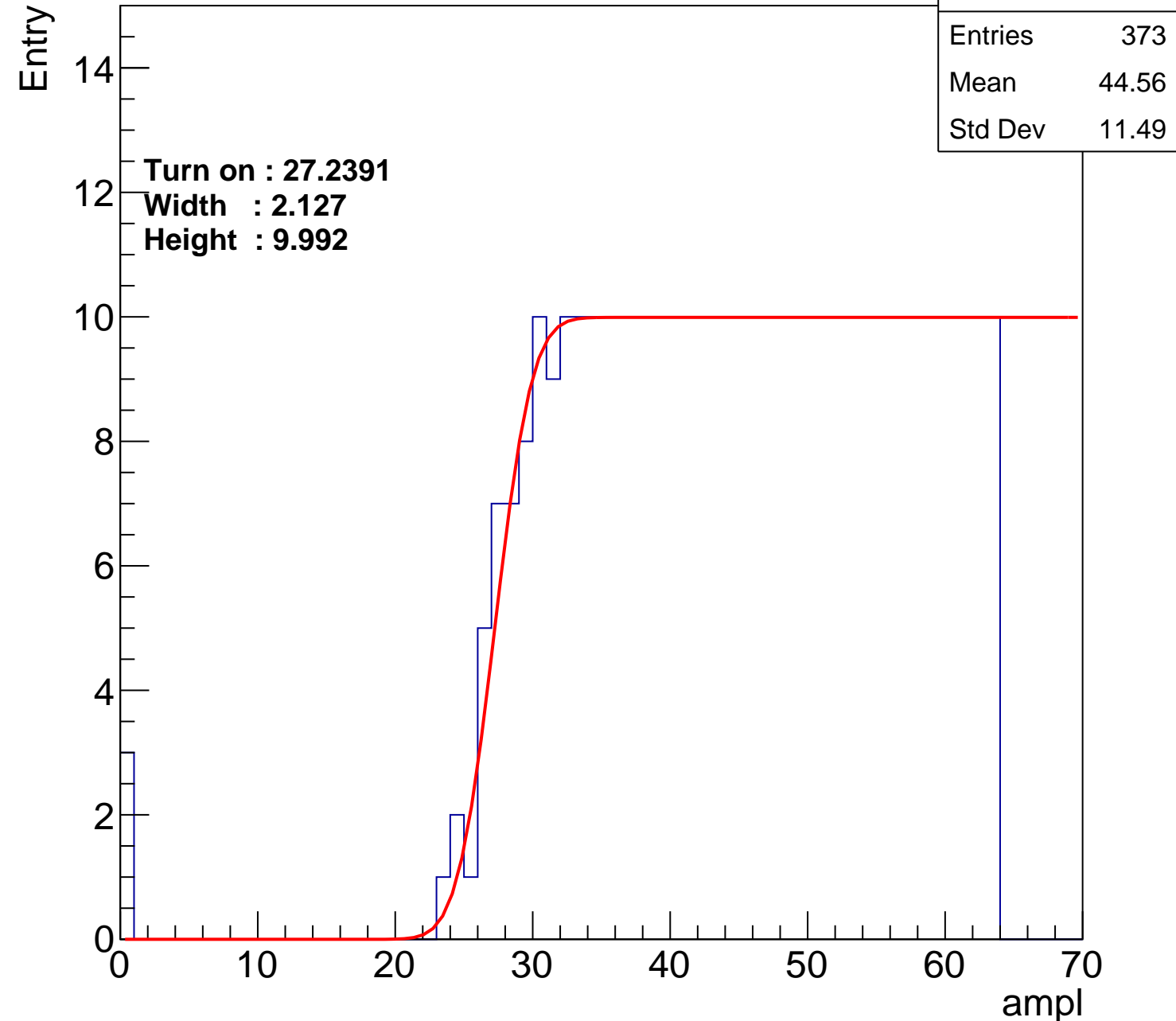
Width : 2.127

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.58
Std Dev	11.32

Turn on : 26.3495

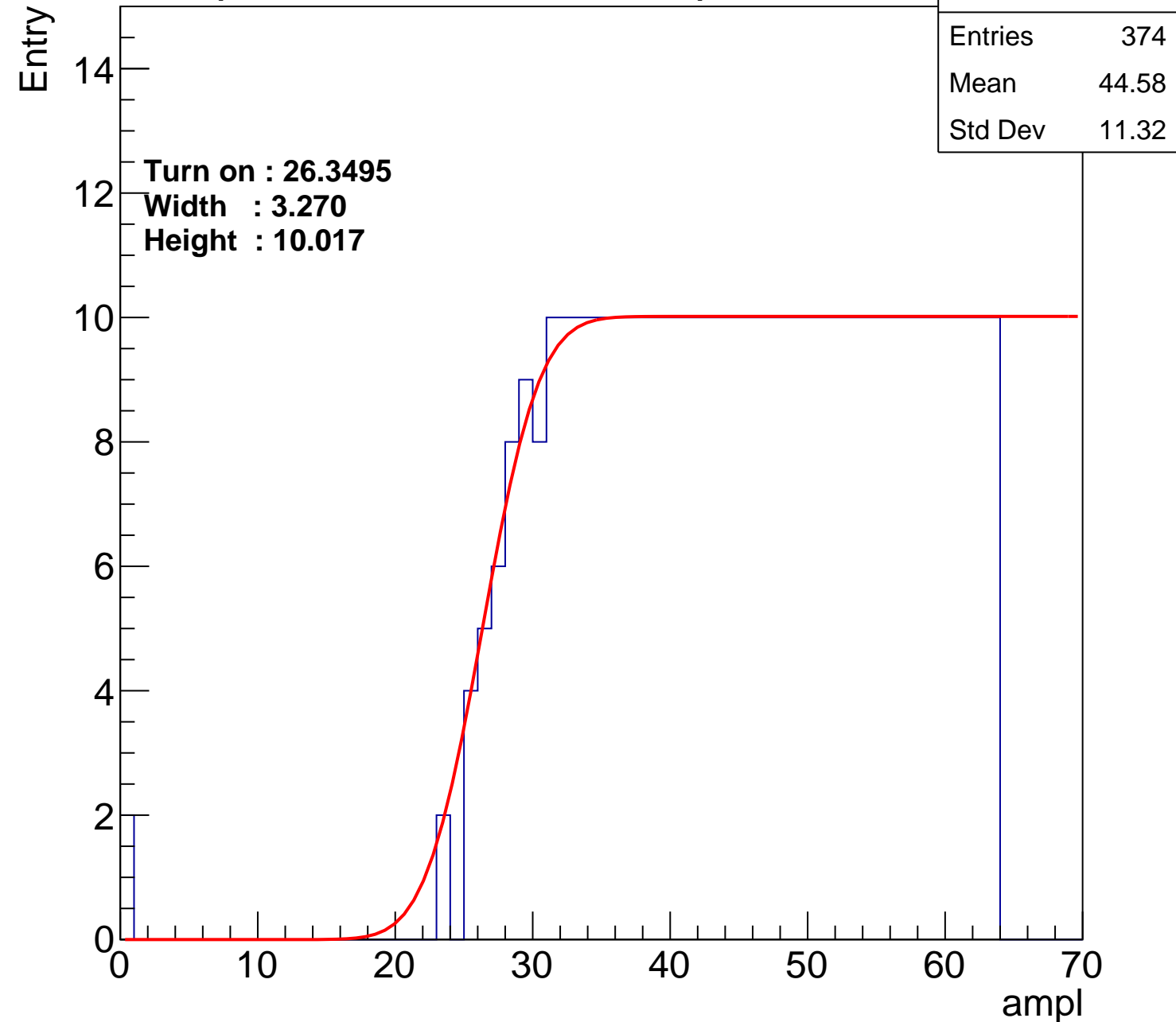
Width : 3.270

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch55

calib_packv5_042523_0143.root, FC#8, port C1

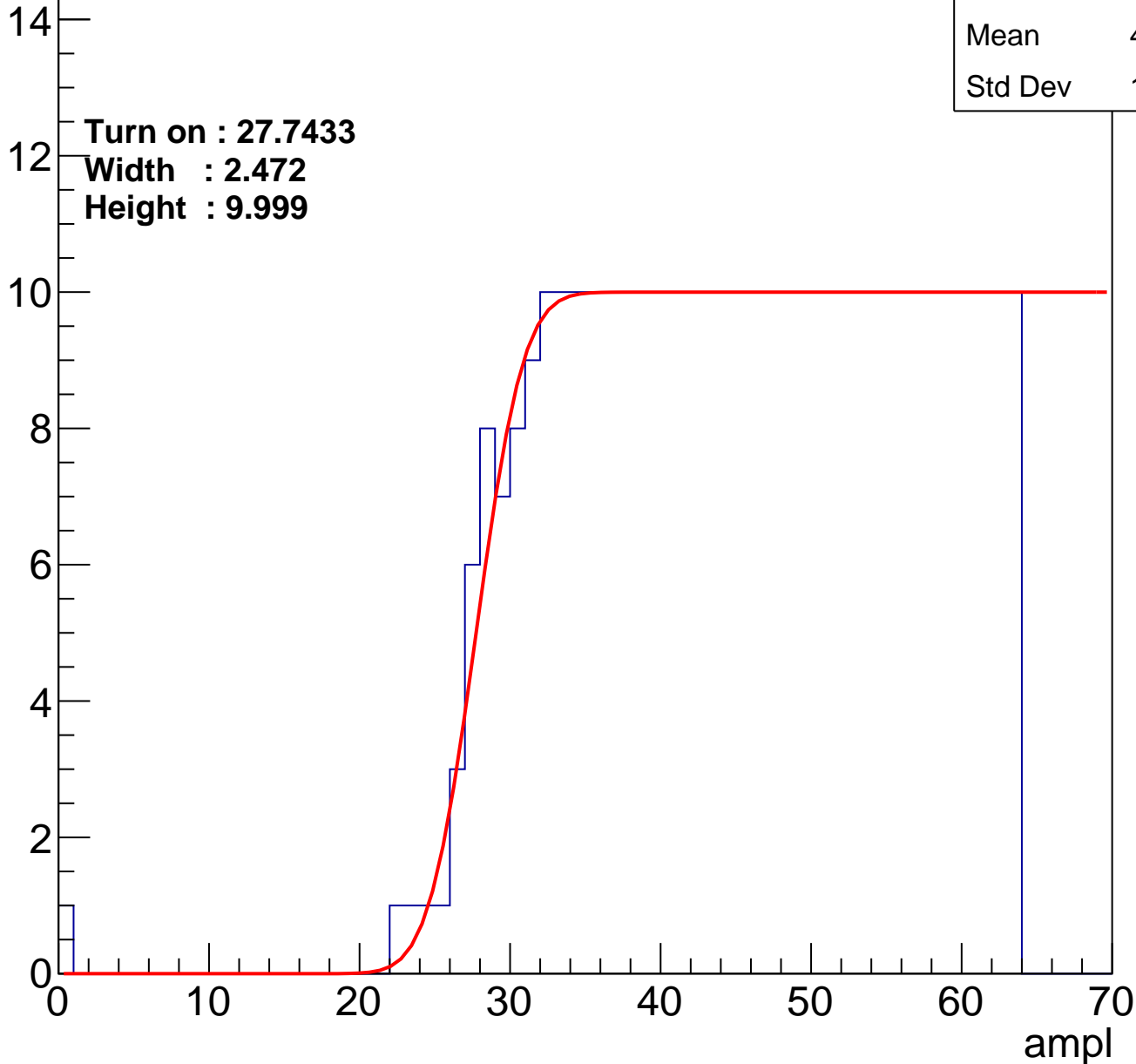
Entry

Entries	366
Mean	45.03
Std Dev	10.95

Turn on : 27.7433

Width : 2.472

Height : 9.999



B0L002S, U3-ch56

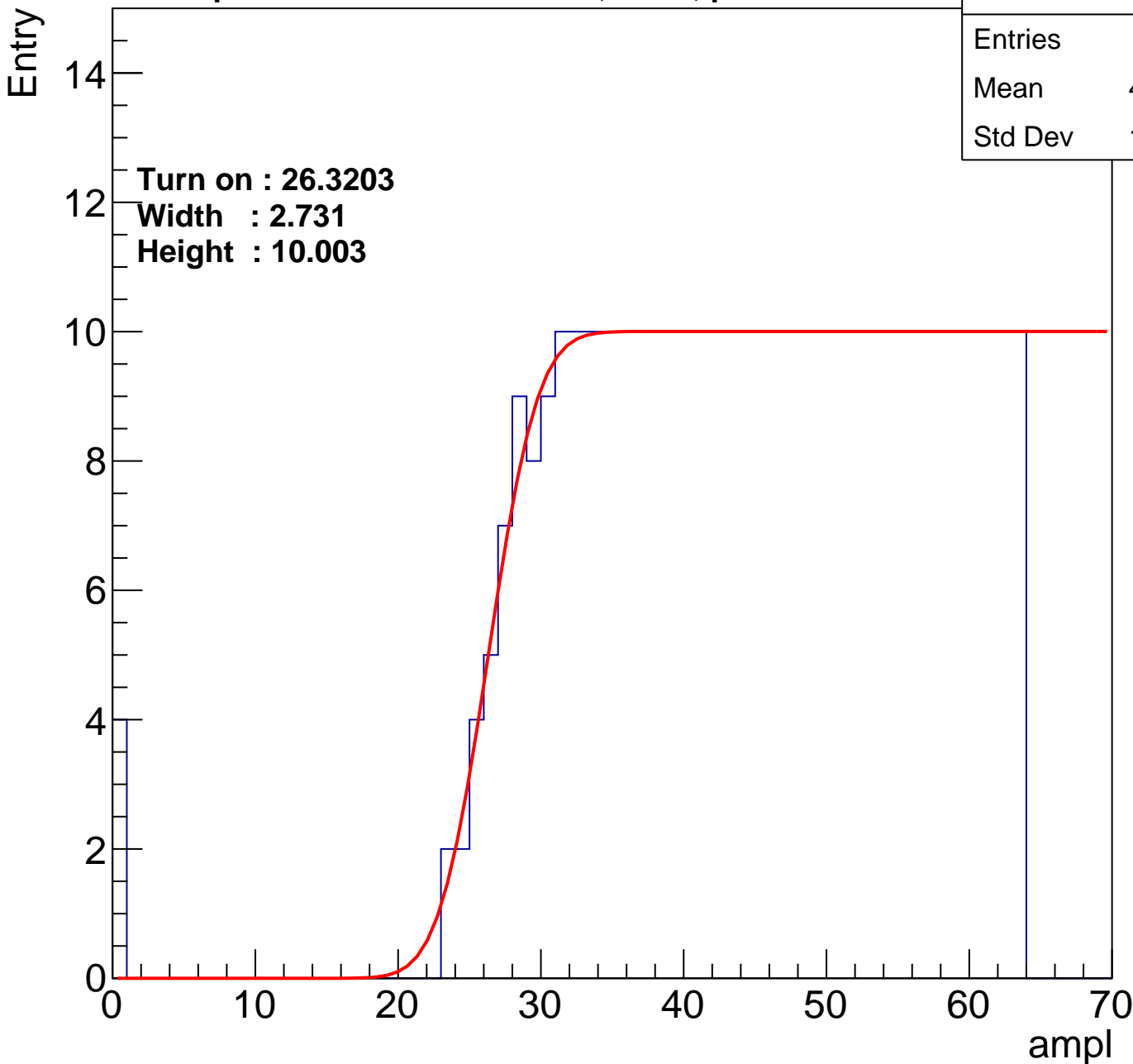
calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.15
Std Dev	11.84

Turn on : 26.3203

Width : 2.731

Height : 10.003



B0L002S, U3-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.39
Std Dev	10.93

Turn on : 28.6097

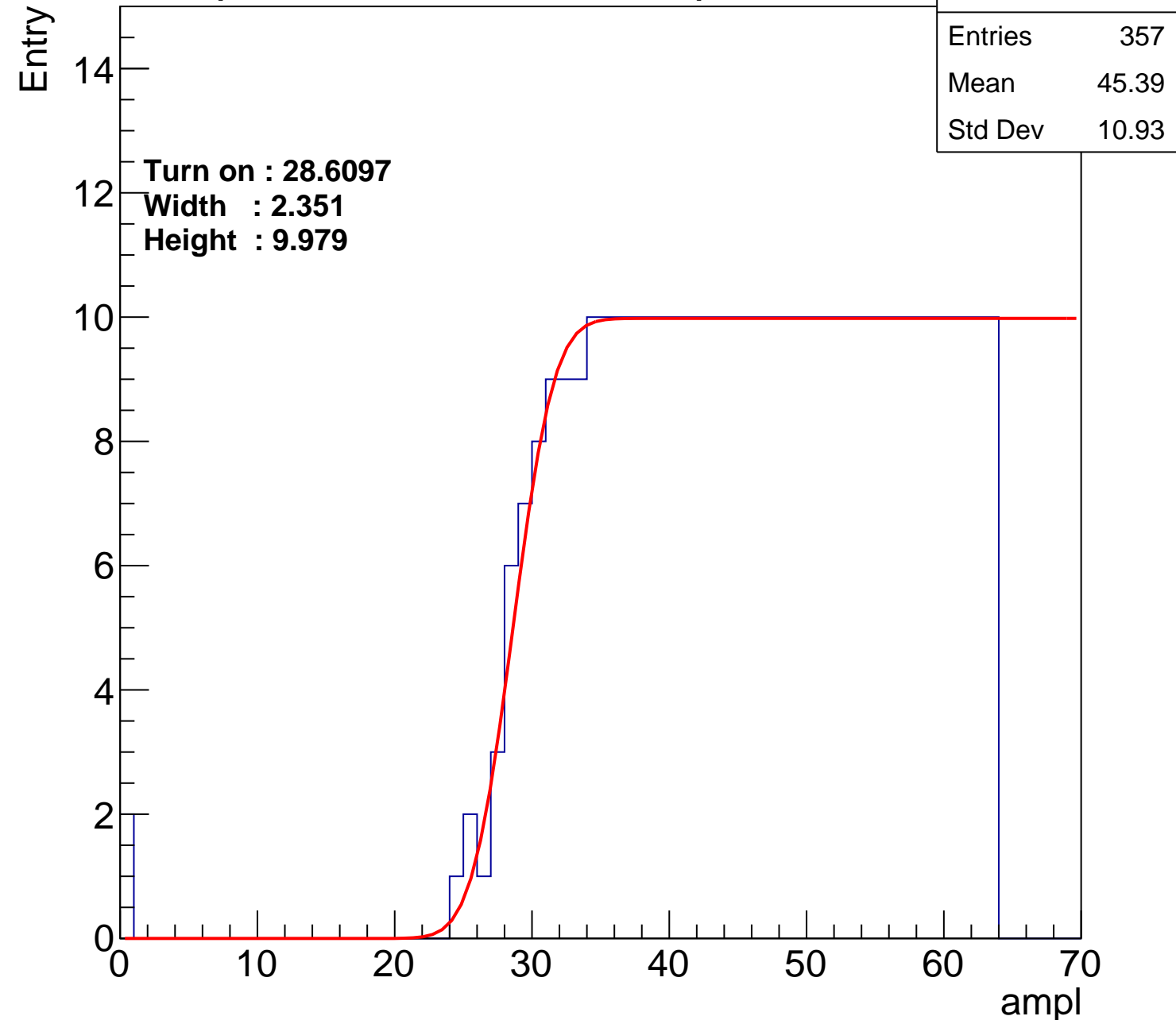
Width : 2.351

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 26.6565

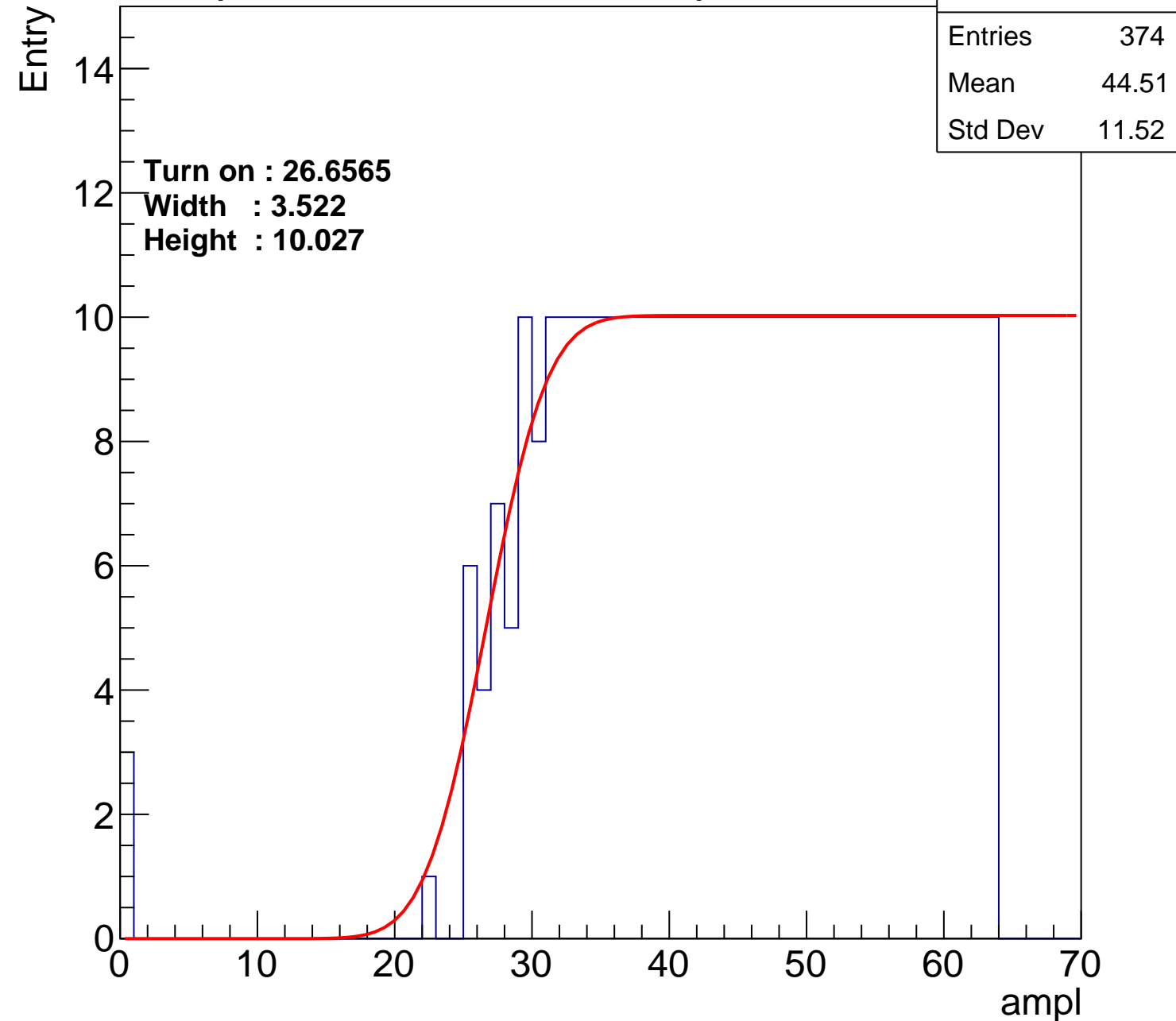
Width : 3.522

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch59

calib_packv5_042523_0143.root, FC#8, port C1

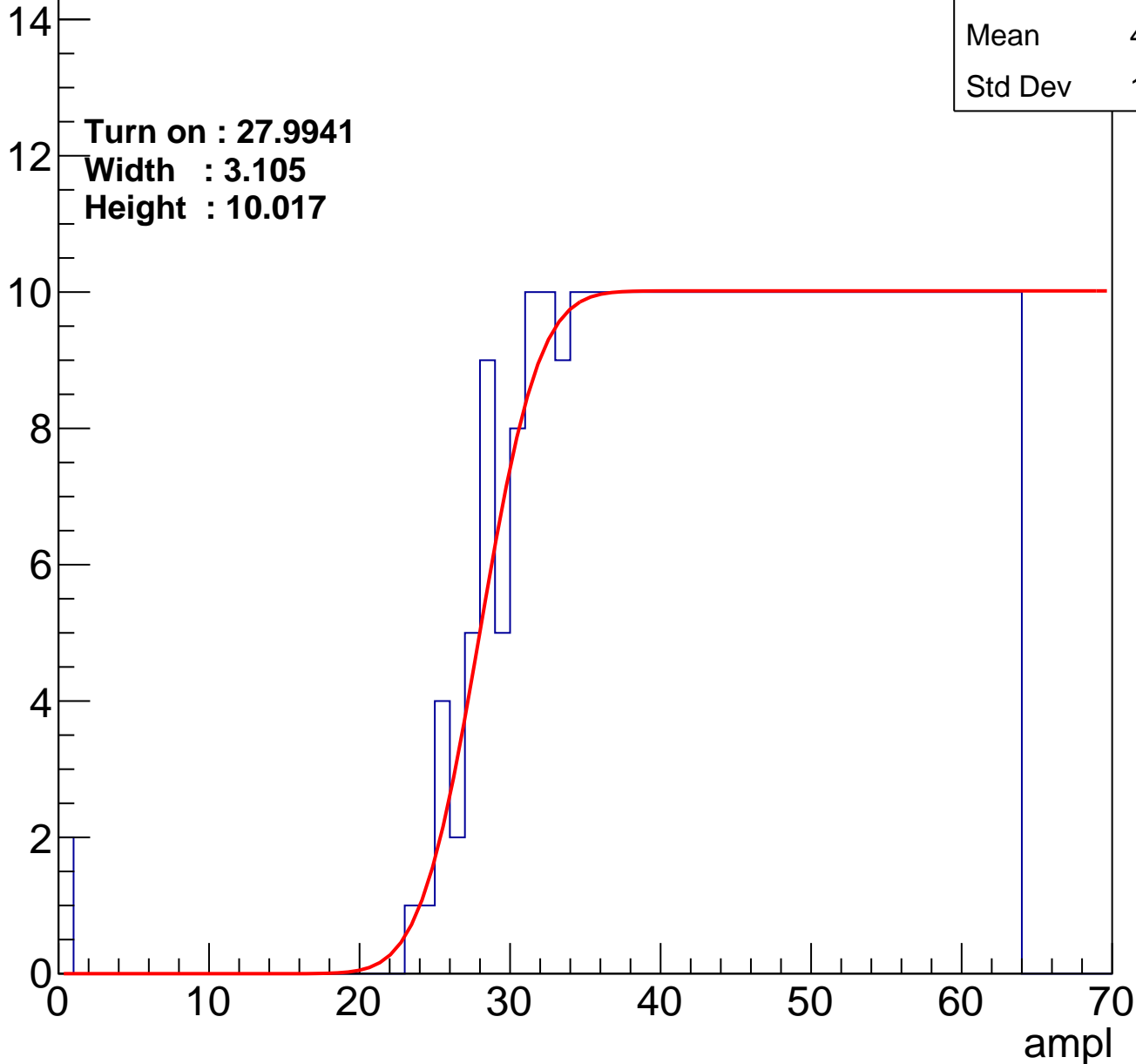
Entry

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.9941

Width : 3.105

Height : 10.017



B0L002S, U3-ch60

calib_packv5_042523_0143.root, FC#8, port C1

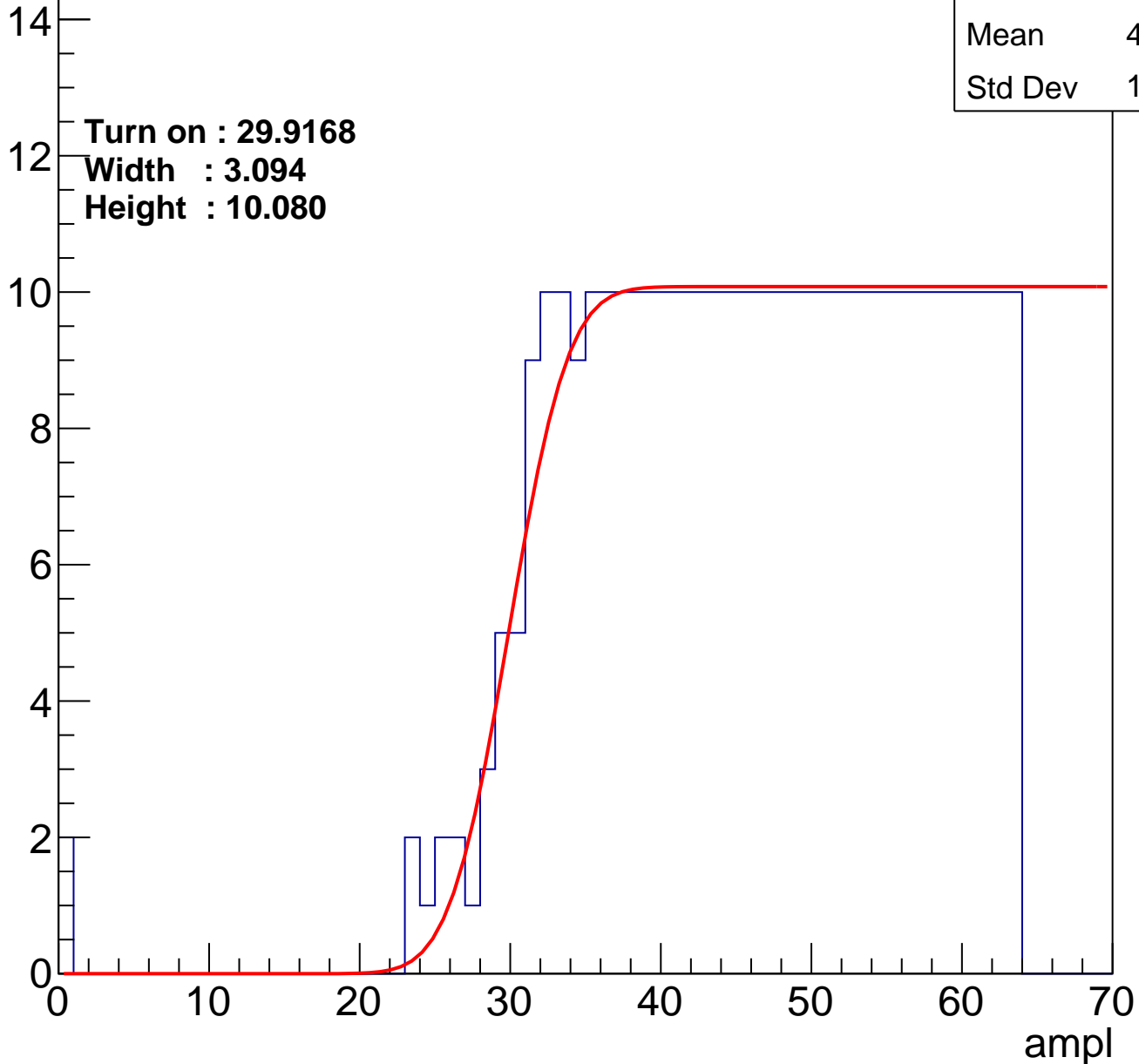
Entries	351
Mean	45.65
Std Dev	10.85

Turn on : 29.9168

Width : 3.094

Height : 10.080

Entry



B0L002S, U3-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.72
Std Dev	11.25

Turn on : 26.7127

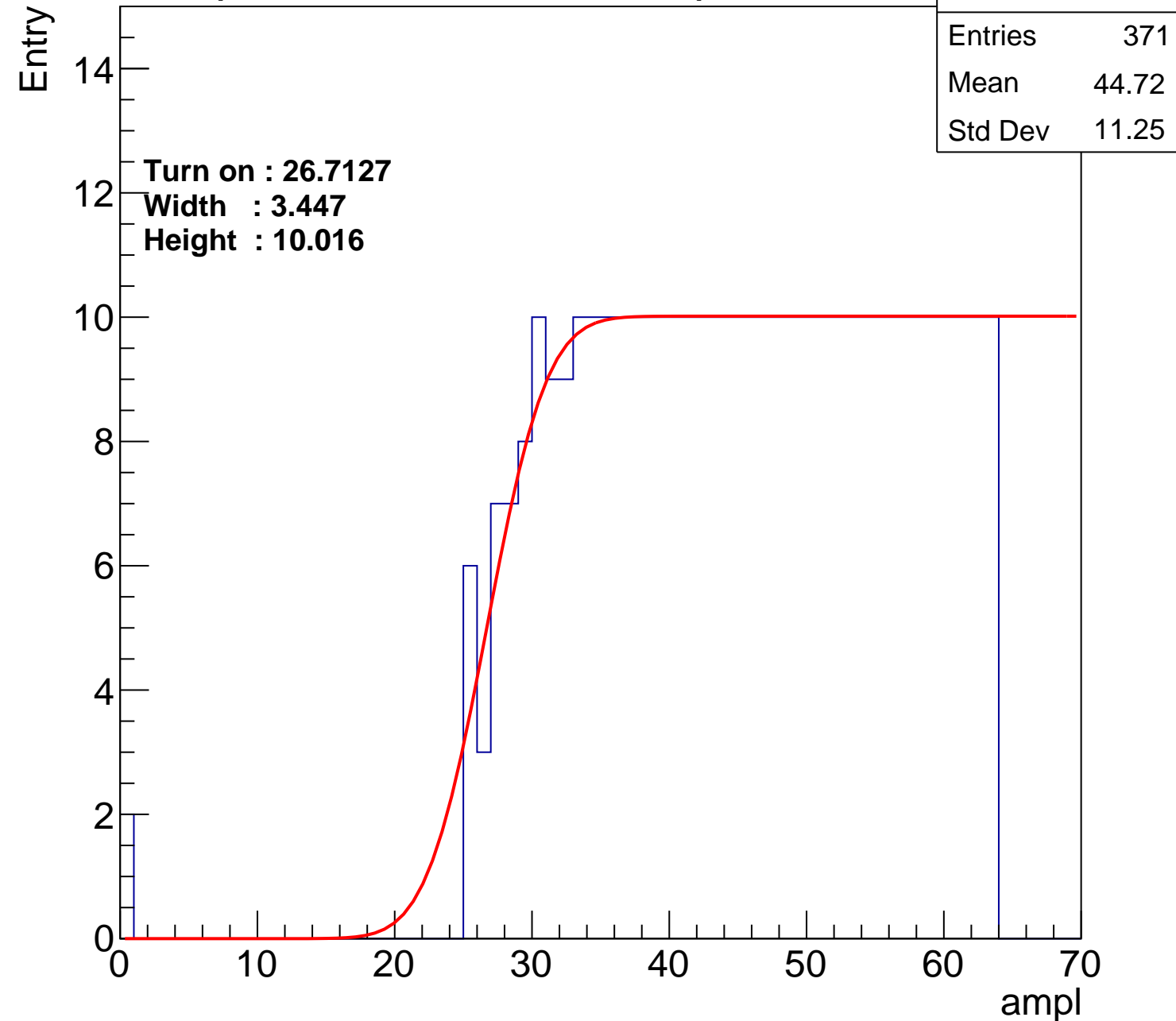
Width : 3.447

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.95
Std Dev	11.16

Turn on : 27.7772

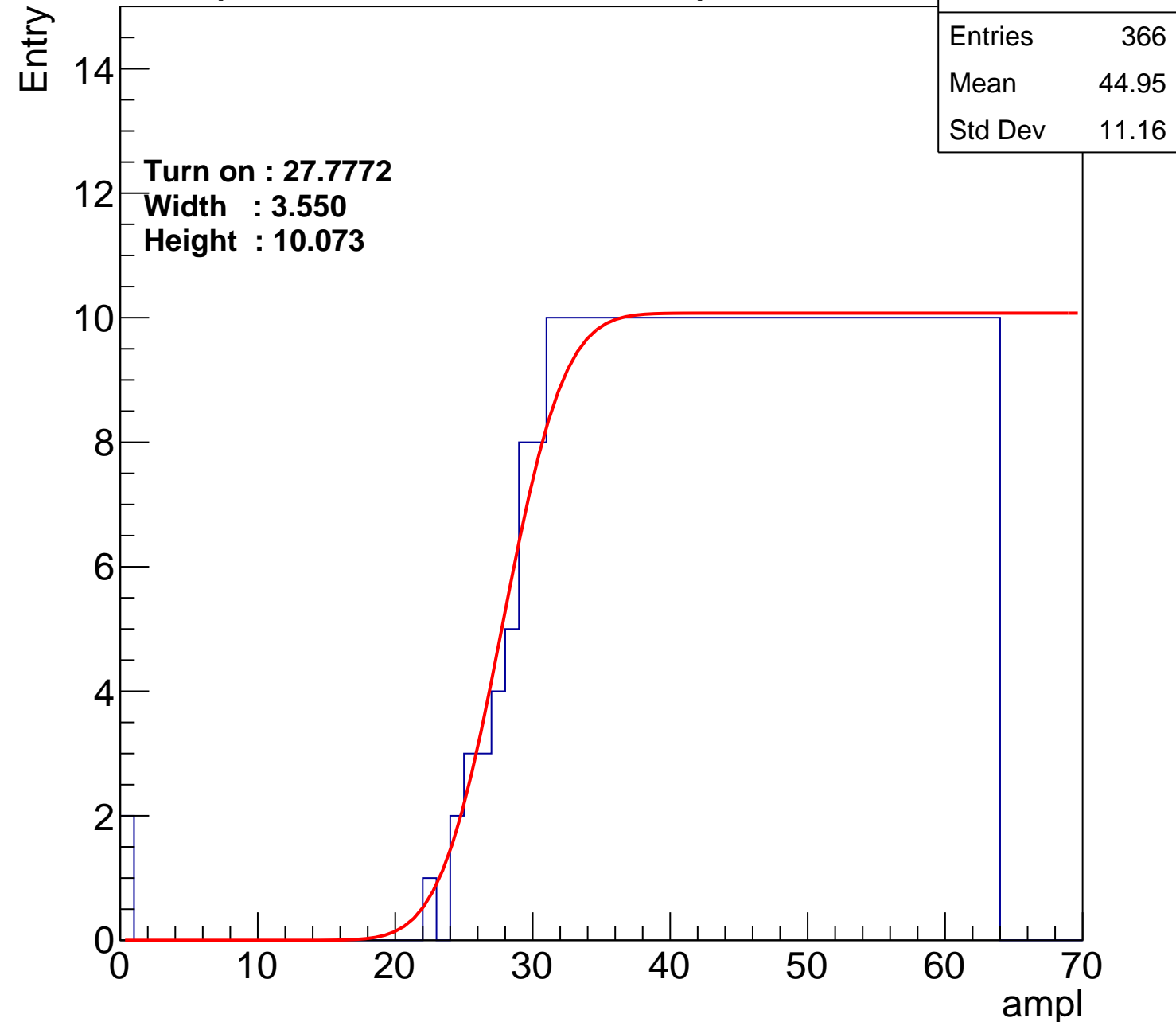
Width : 3.550

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 27.8946

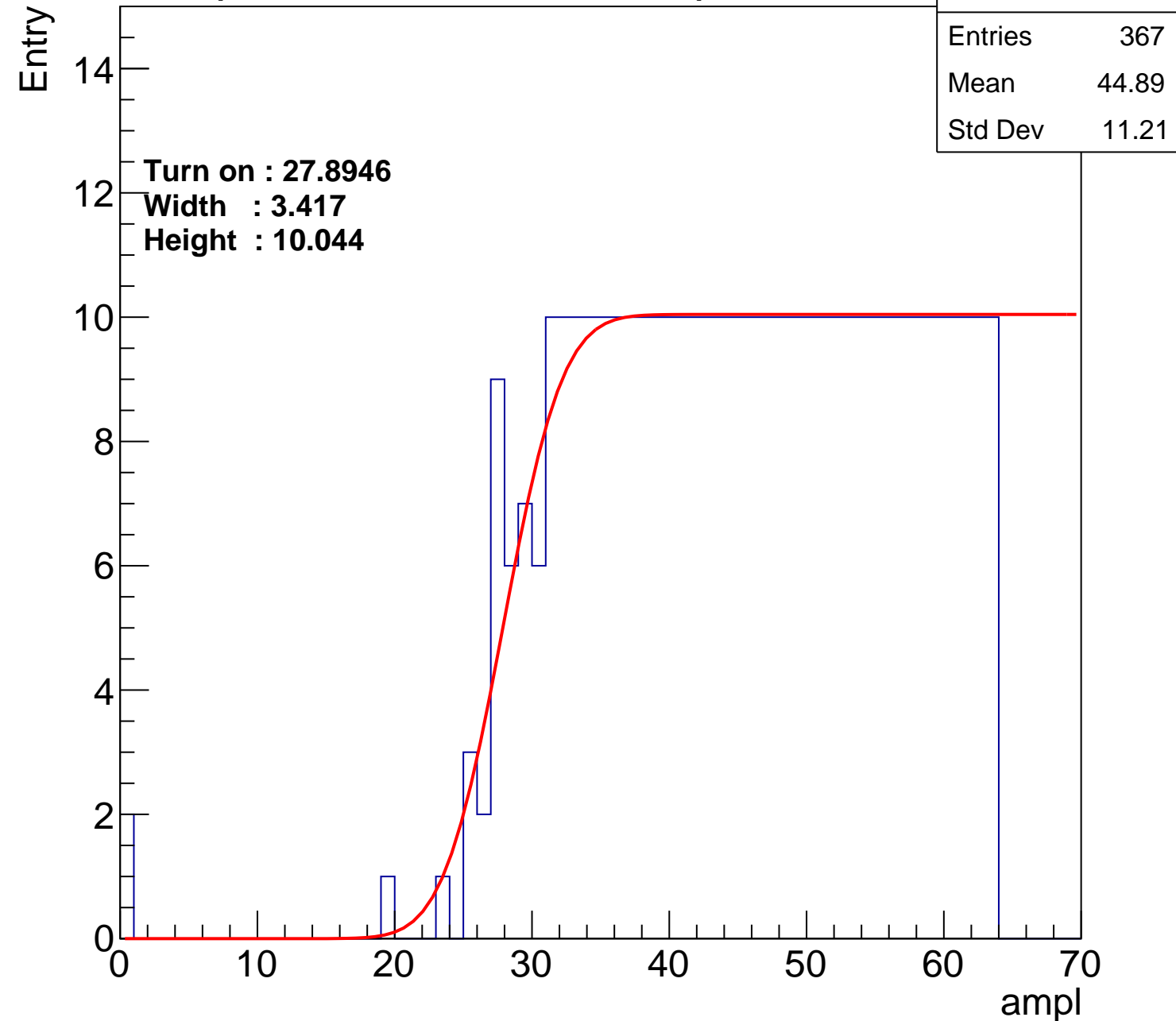
Width : 3.417

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	43.96
Std Dev	11.8

Turn on : 25.8290

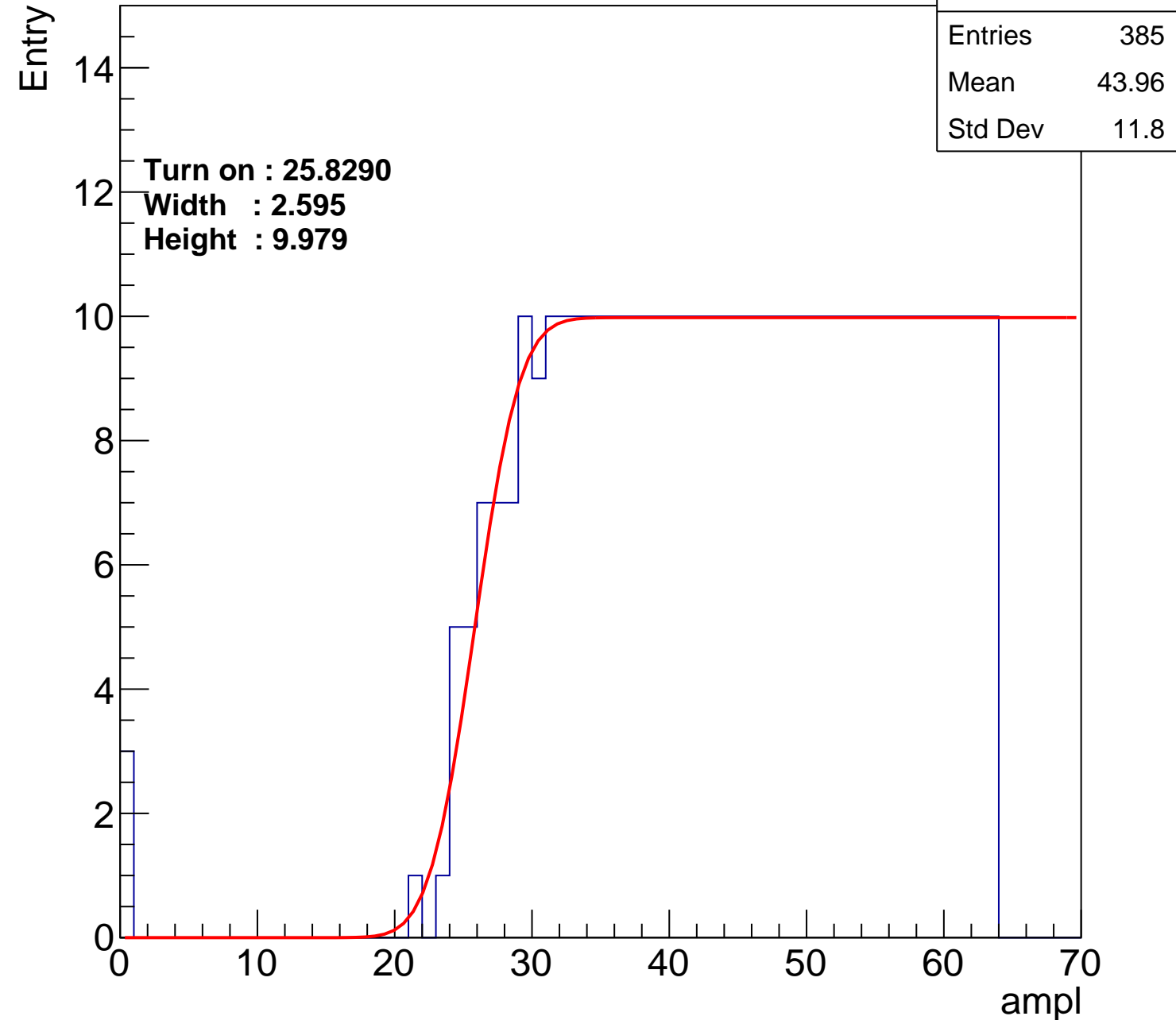
Width : 2.595

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch65

calib_packv5_042523_0143.root, FC#8, port C1

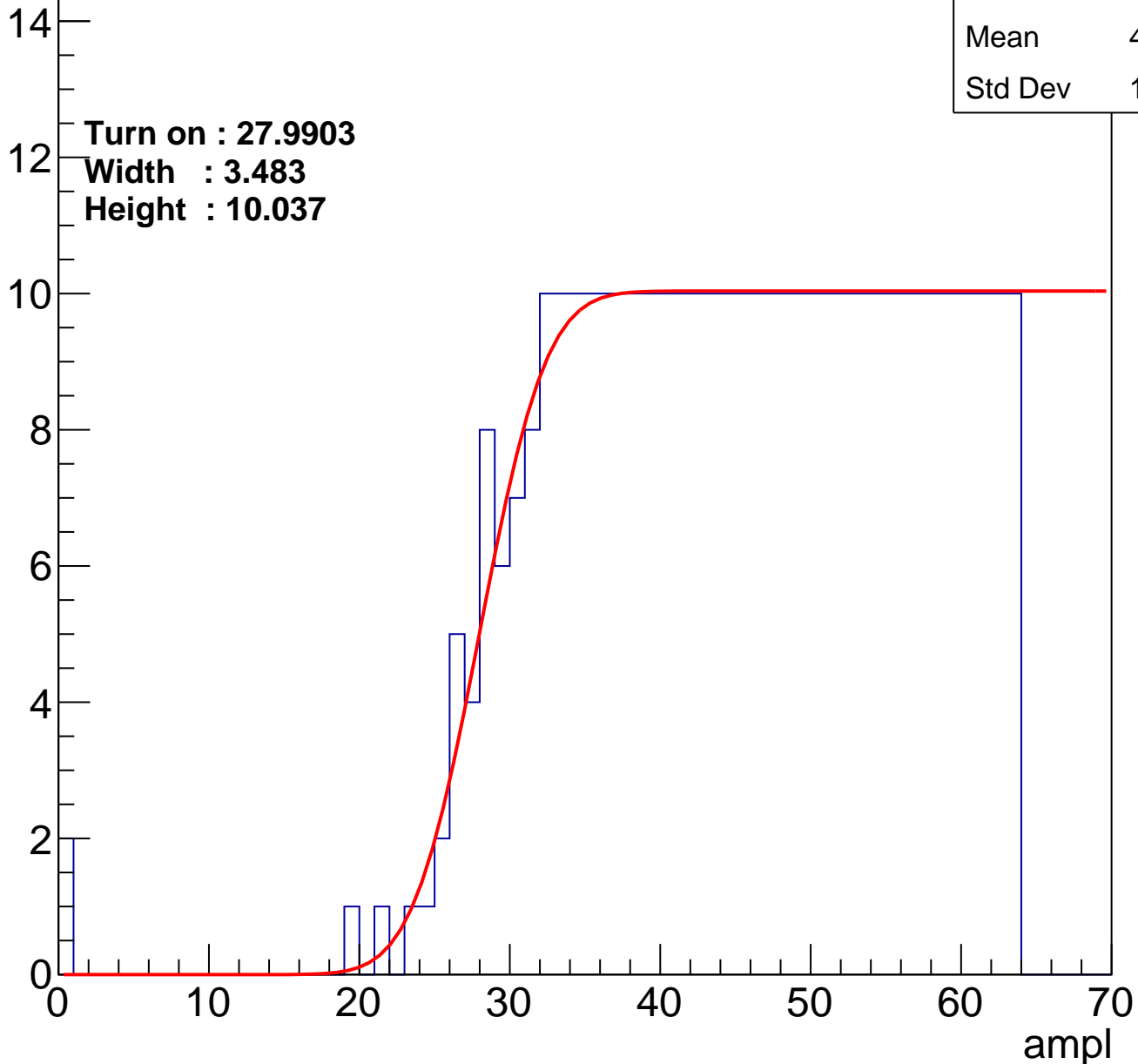
Entries	366
Mean	44.89
Std Dev	11.26

Turn on : 27.9903

Width : 3.483

Height : 10.037

Entry



B0L002S, U3-ch66

calib_packv5_042523_0143.root, FC#8, port C1

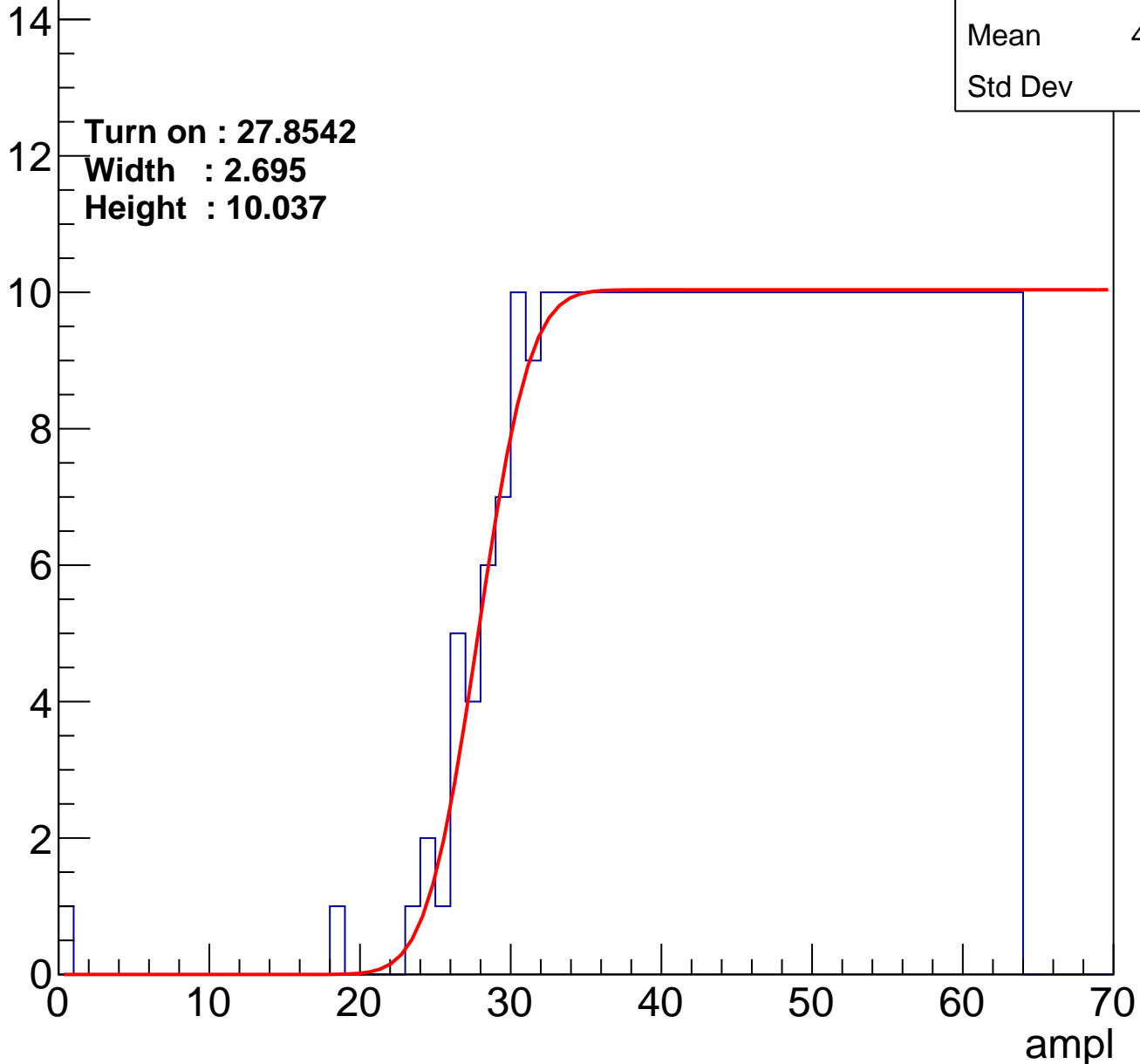
Entries	367
Mean	44.96
Std Dev	11

Turn on : 27.8542

Width : 2.695

Height : 10.037

Entry



B0L002S, U3-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.78
Std Dev	11.28

Turn on : 27.3781

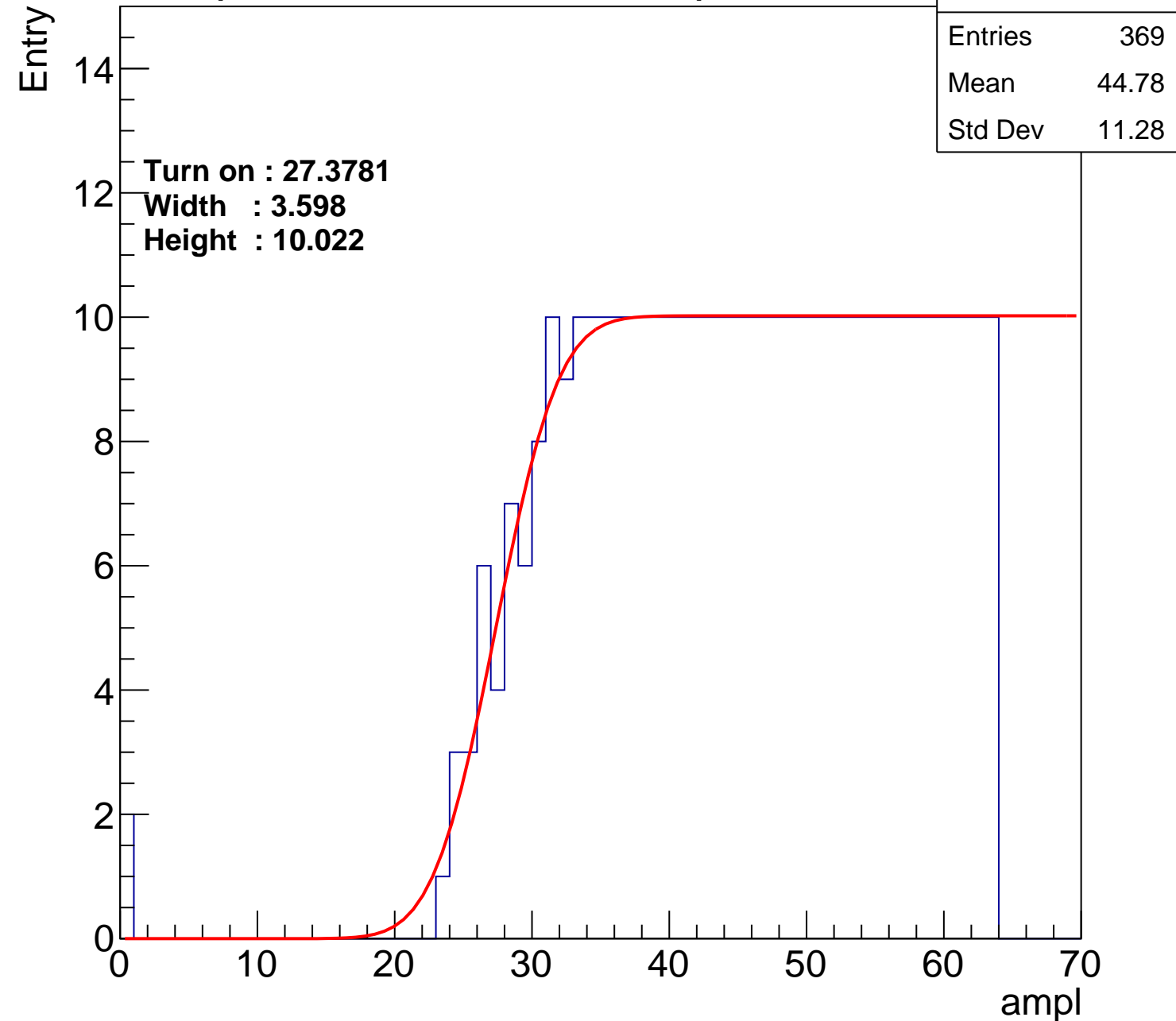
Width : 3.598

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.58
Std Dev	10.87

Turn on : 29.2546

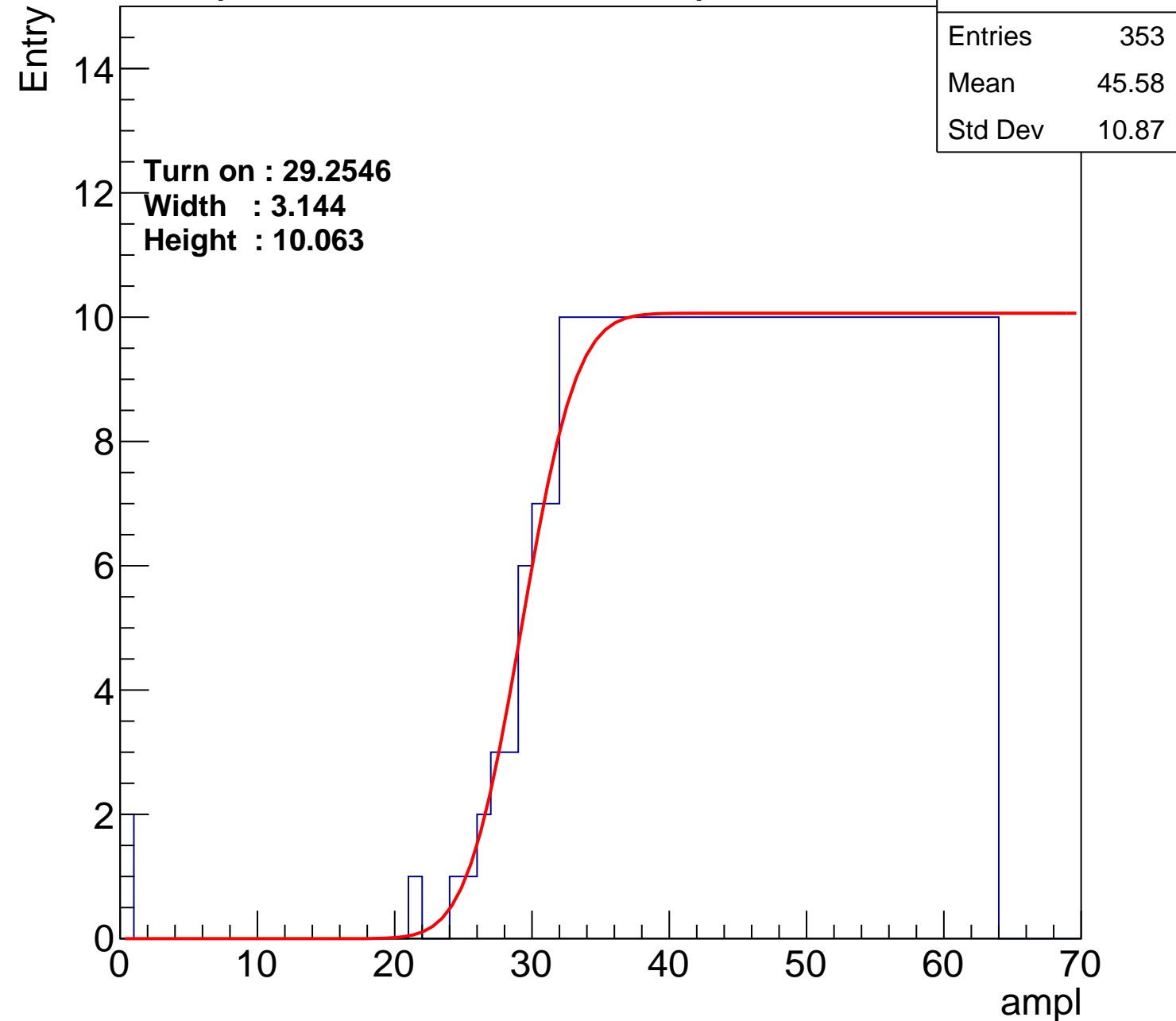
Width : 3.144

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch69

calib_packv5_042523_0143.root, FC#8, port C1

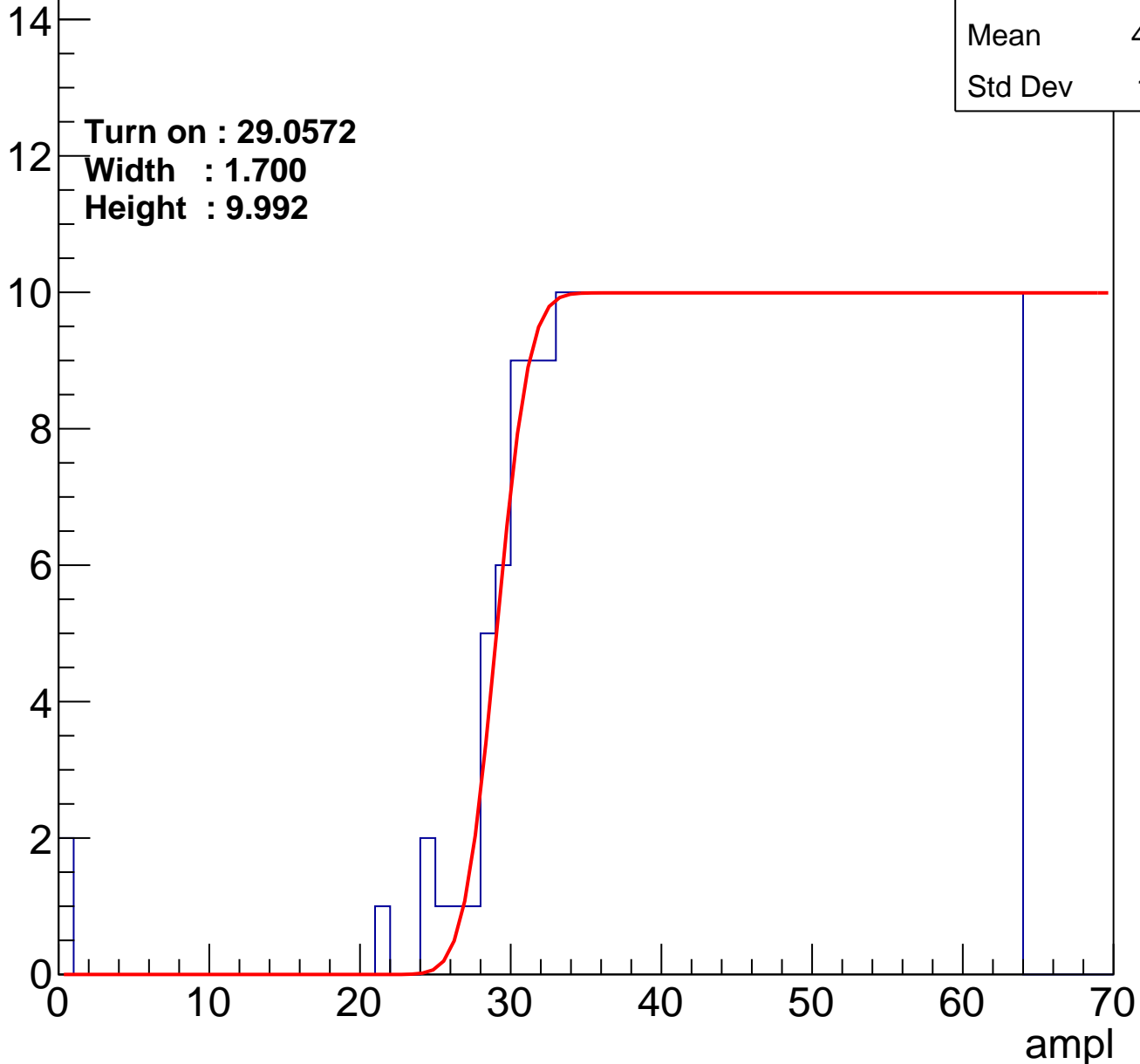
Entries	356
Mean	45.44
Std Dev	10.91

Turn on : 29.0572

Width : 1.700

Height : 9.992

Entry



B0L002S, U3-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.38
Std Dev	11.75

Turn on : 27.1909

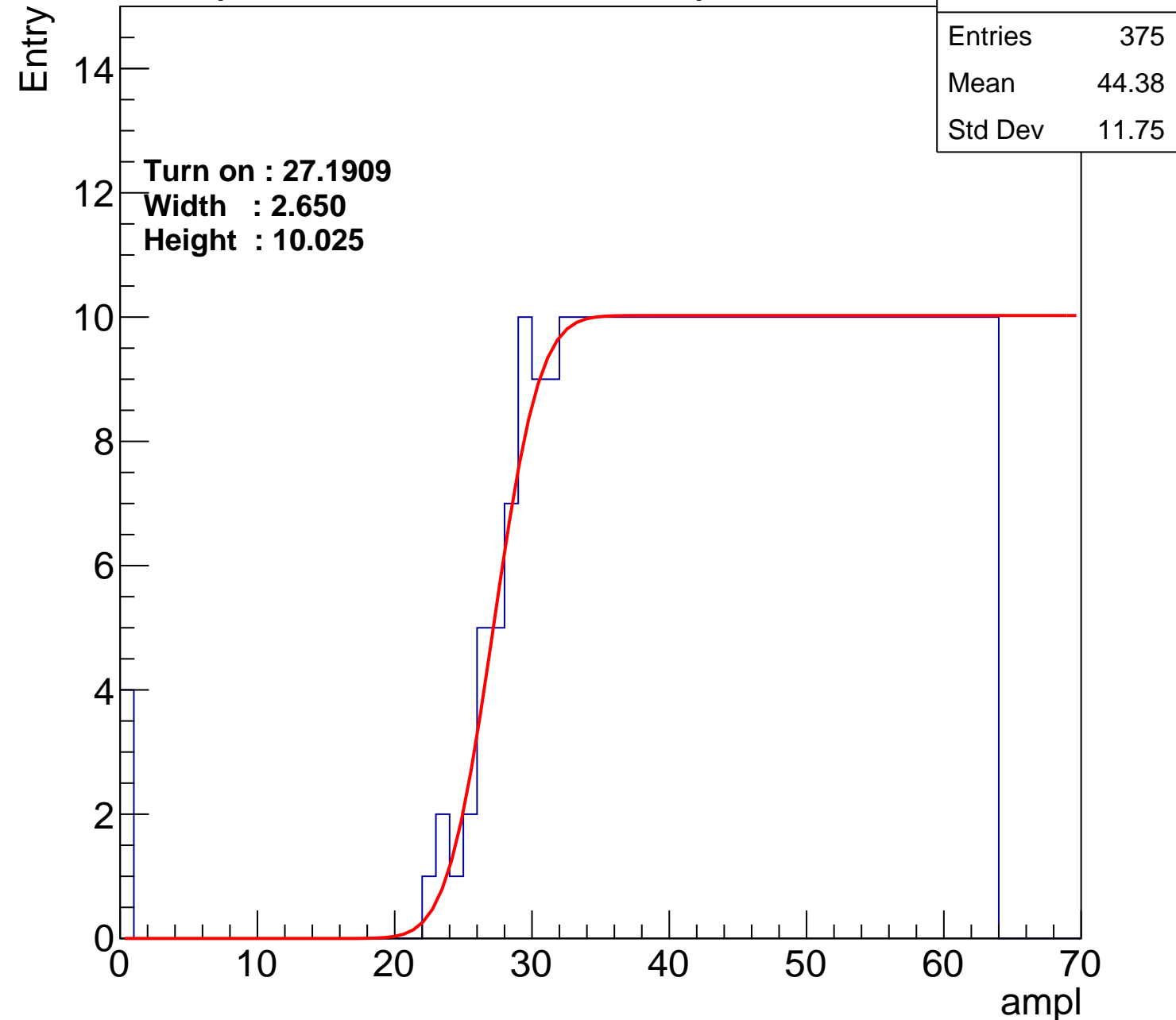
Width : 2.650

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	342
Mean	46.15
Std Dev	10.53

Turn on : 30.2320

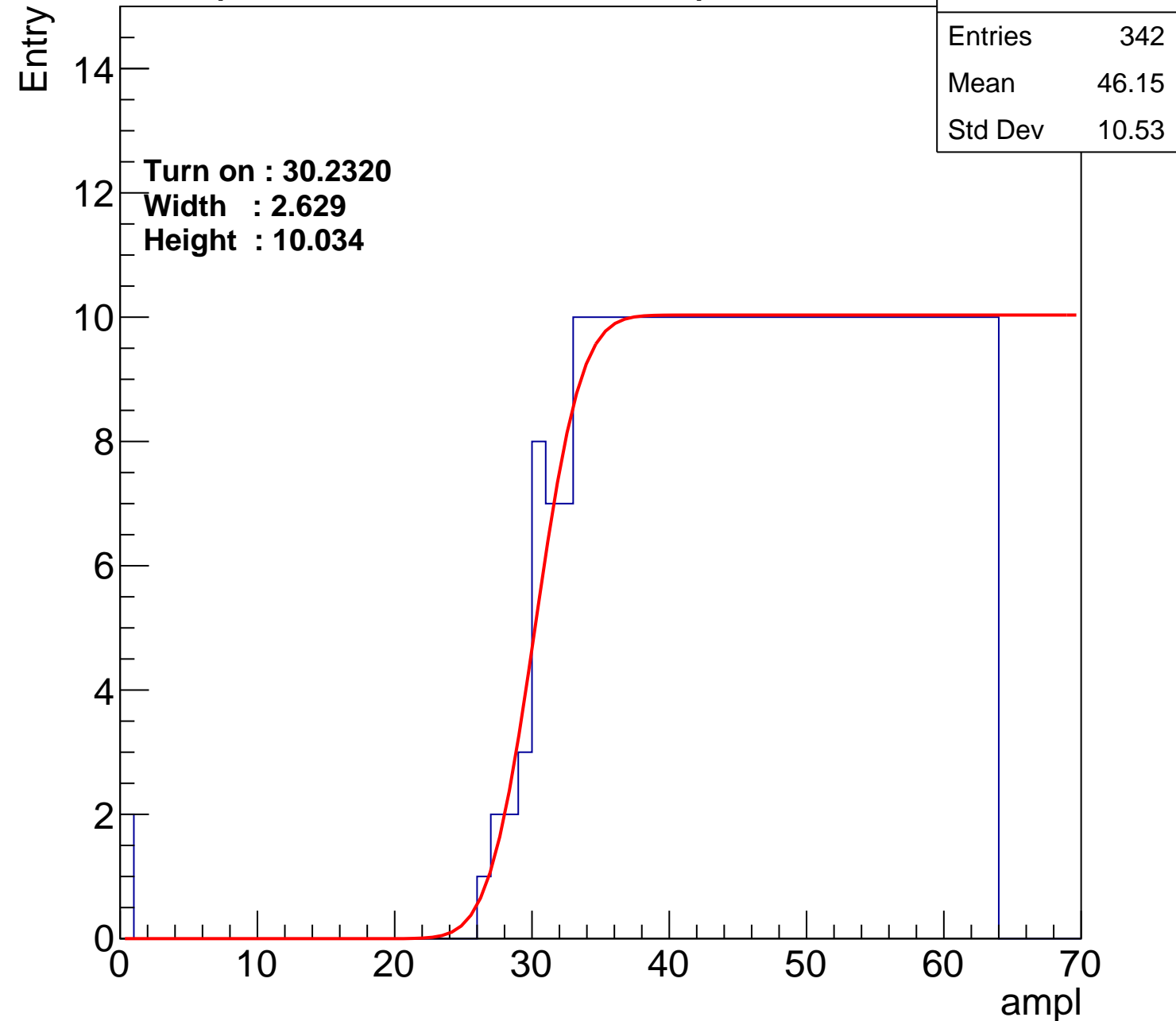
Width : 2.629

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 26.4721

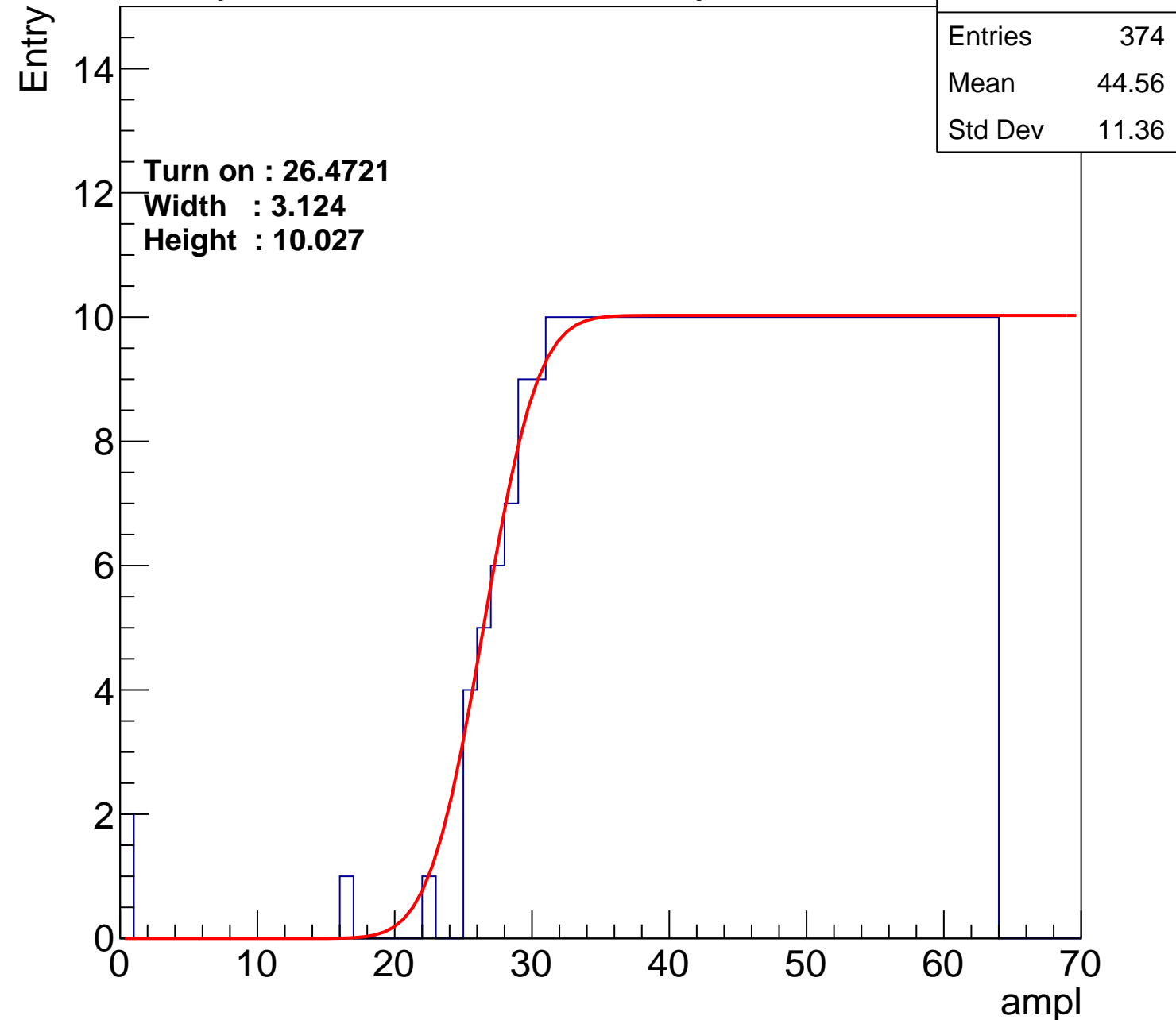
Width : 3.124

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.5
Std Dev	11.41

Turn on : 27.3546

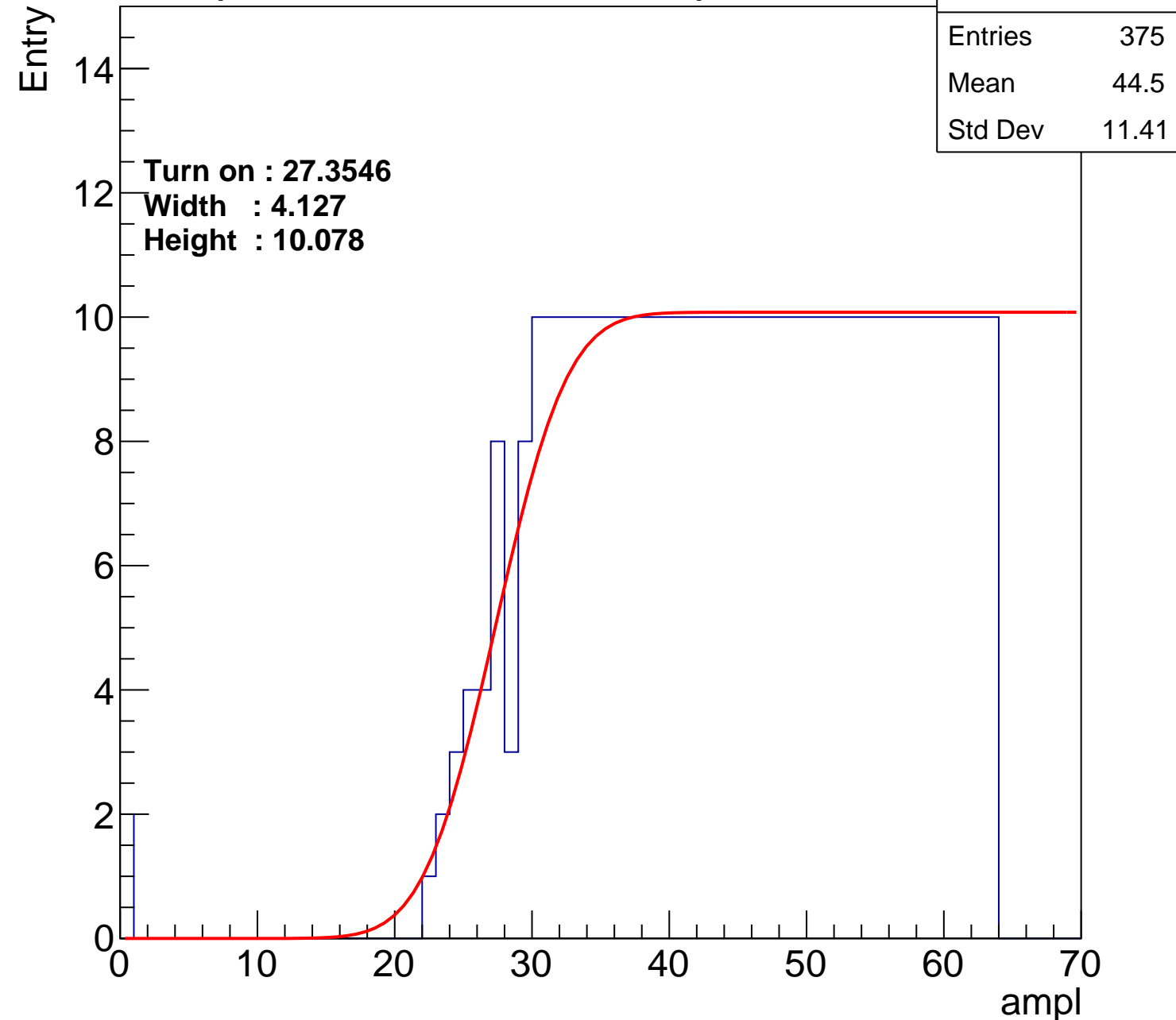
Width : 4.127

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.19
Std Dev	11.65

Turn on : 26.4265

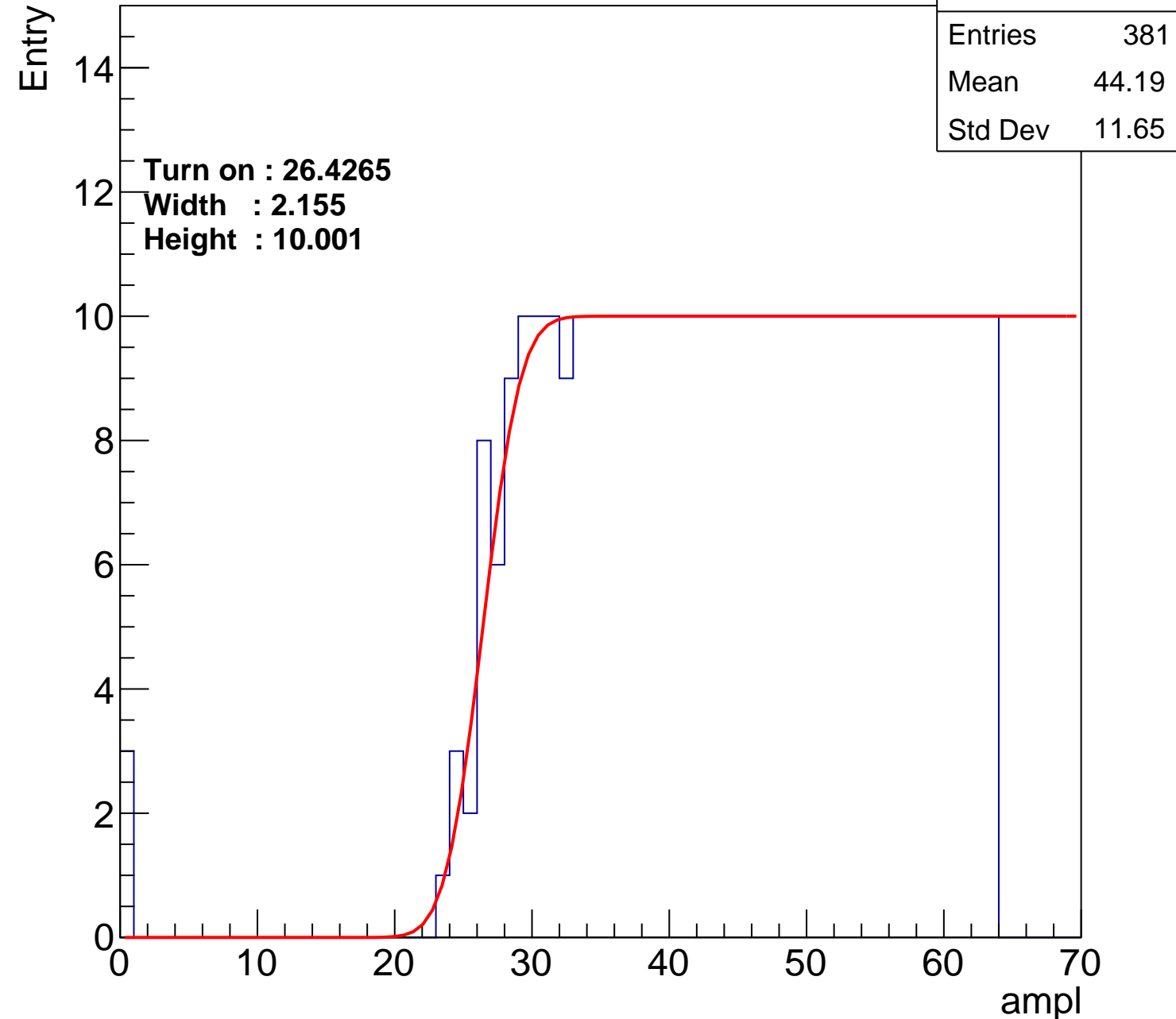
Width : 2.155

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.68
Std Dev	10.99

Turn on : 29.2087

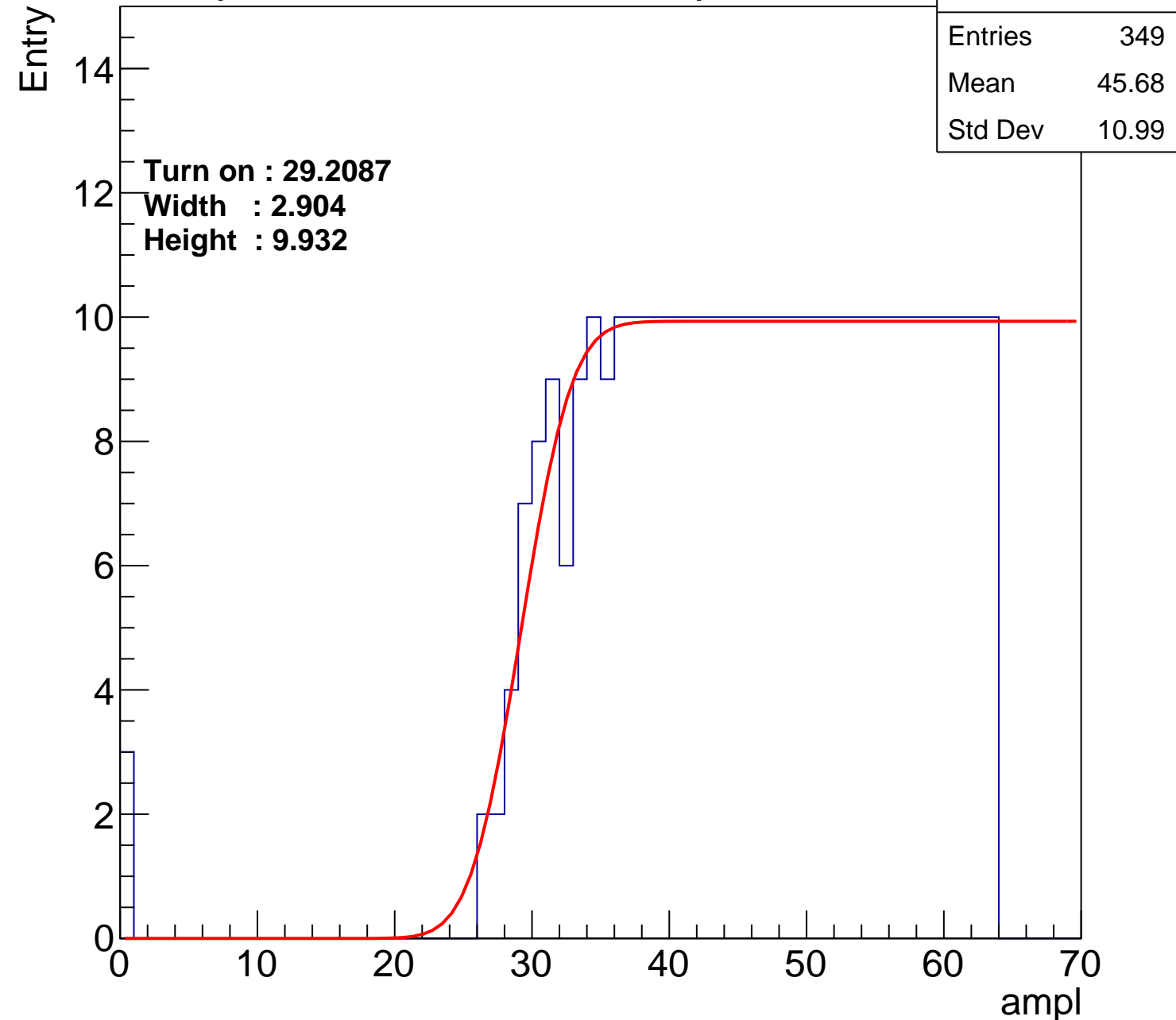
Width : 2.904

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch76

calib_packv5_042523_0143.root, FC#8, port C1

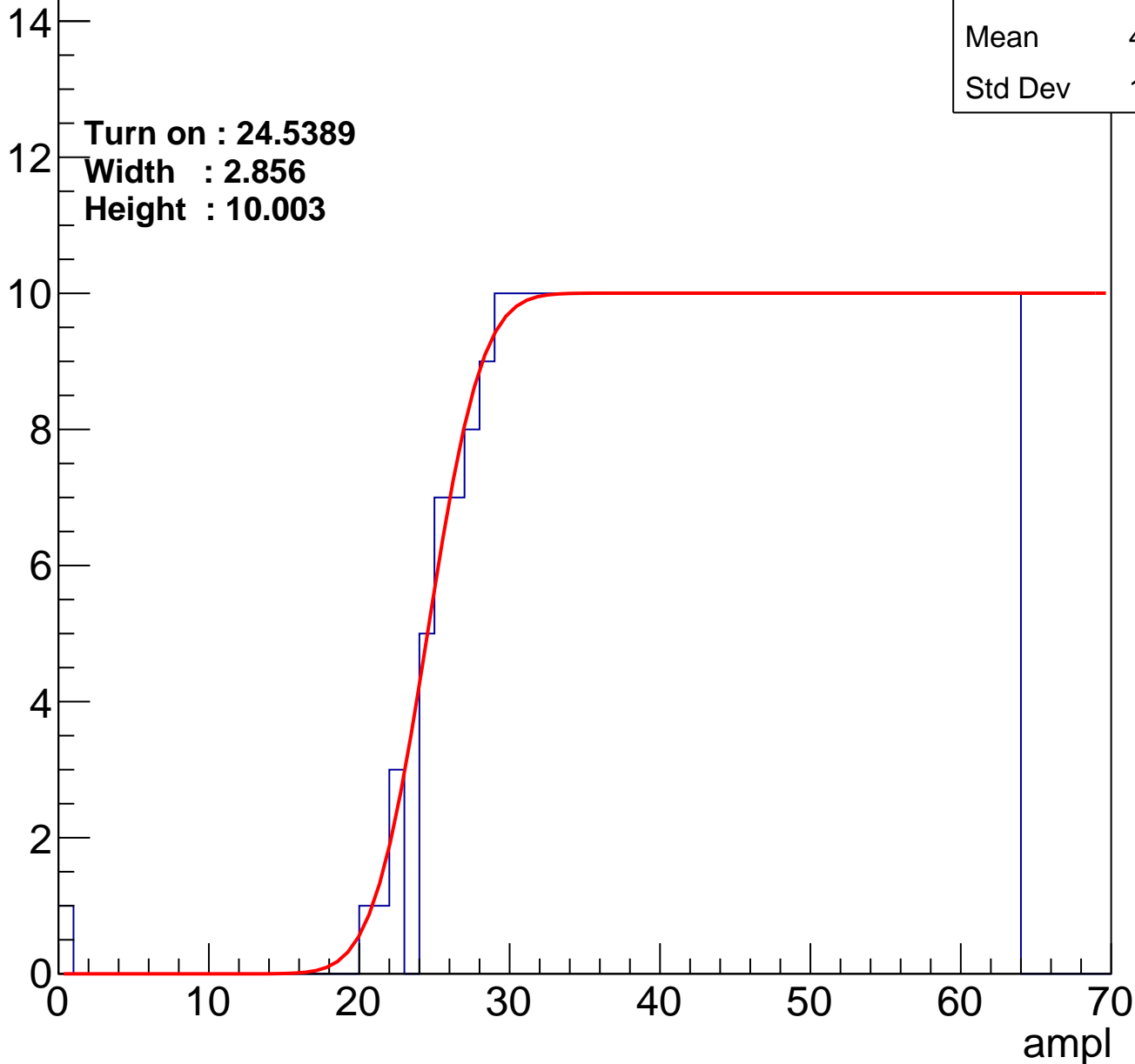
Entry

Entries	392
Mean	43.76
Std Dev	11.63

Turn on : 24.5389

Width : 2.856

Height : 10.003



B0L002S, U3-ch77

calib_packv5_042523_0143.root, FC#8, port C1

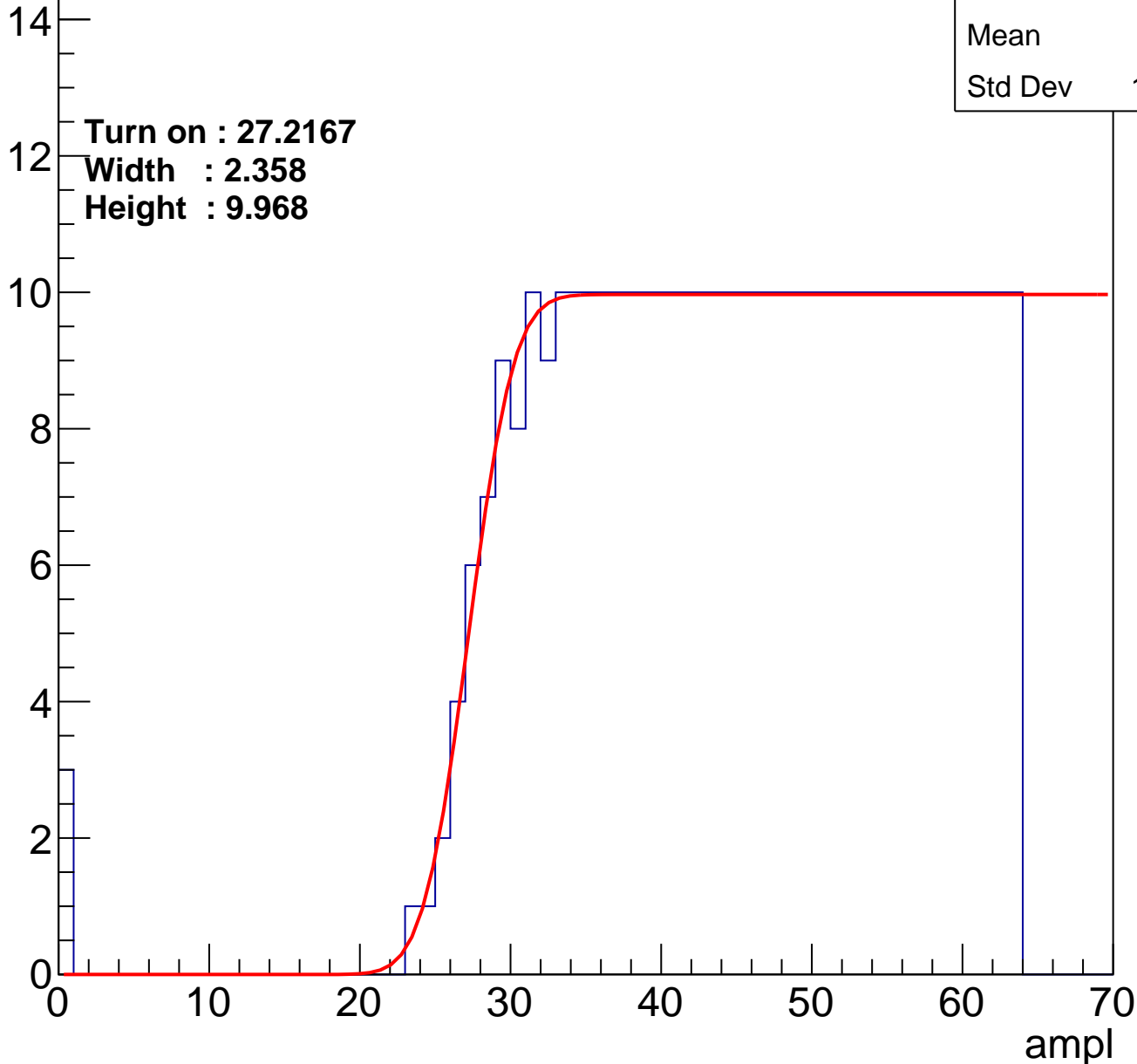
Entry

Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.2167

Width : 2.358

Height : 9.968



B0L002S, U3-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.6589

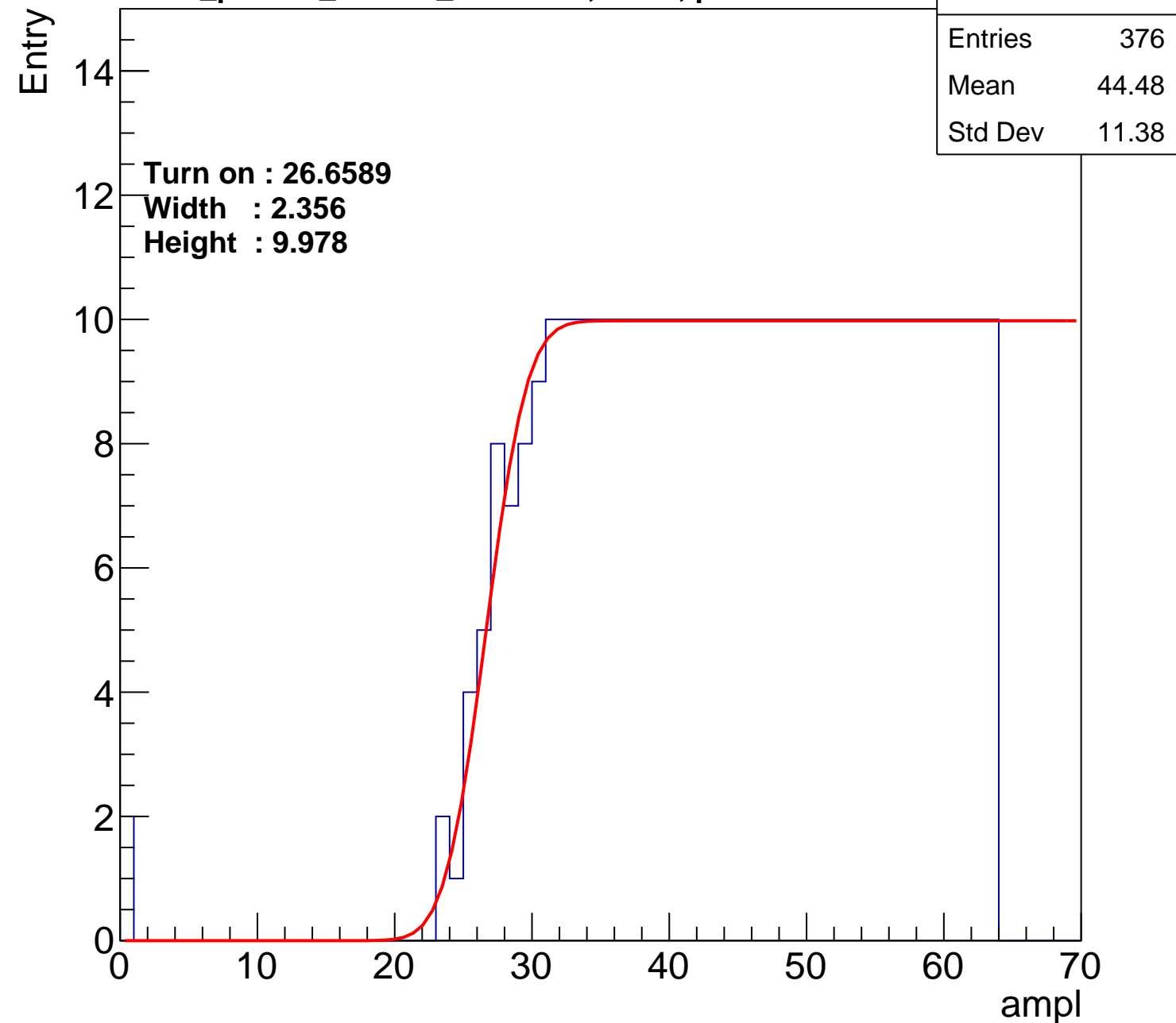
Width : 2.356

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.29
Std Dev	10.79

Turn on : 27.9834

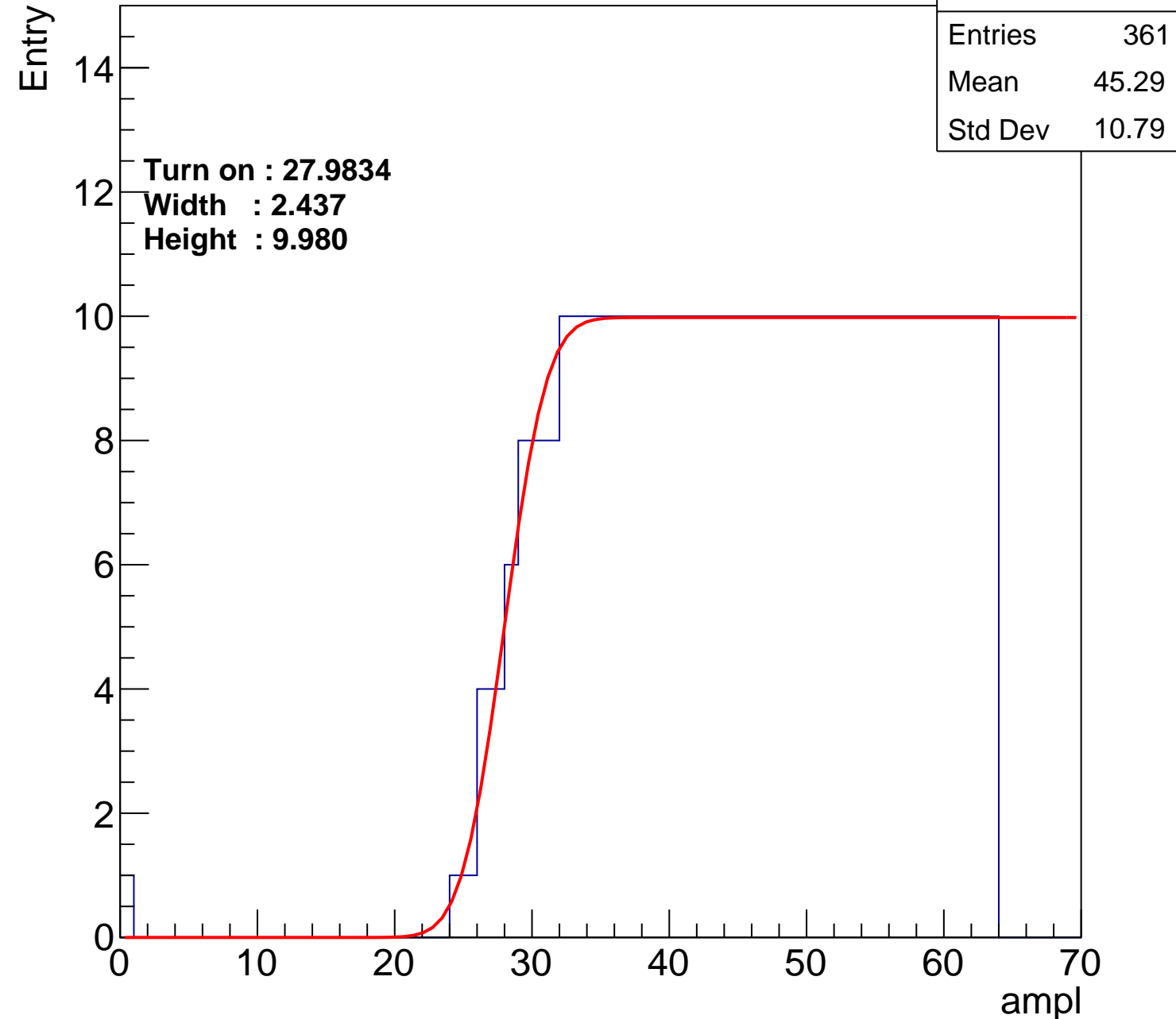
Width : 2.437

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch80

calib_packv5_042523_0143.root, FC#8, port C1

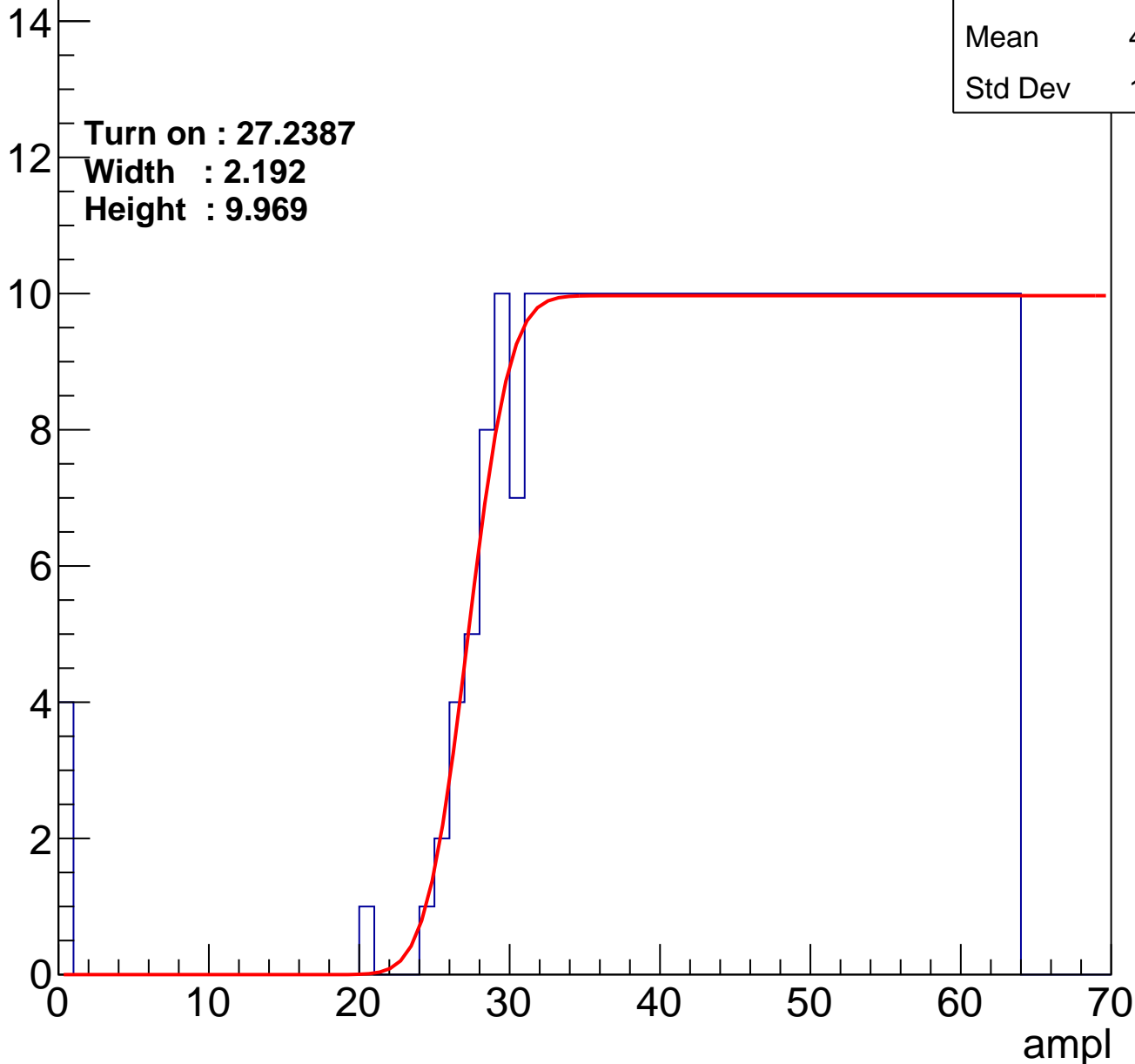
Entry

Entries	372
Mean	44.53
Std Dev	11.67

Turn on : 27.2387

Width : 2.192

Height : 9.969



B0L002S, U3-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.42
Std Dev	11.88

Turn on : 27.3707

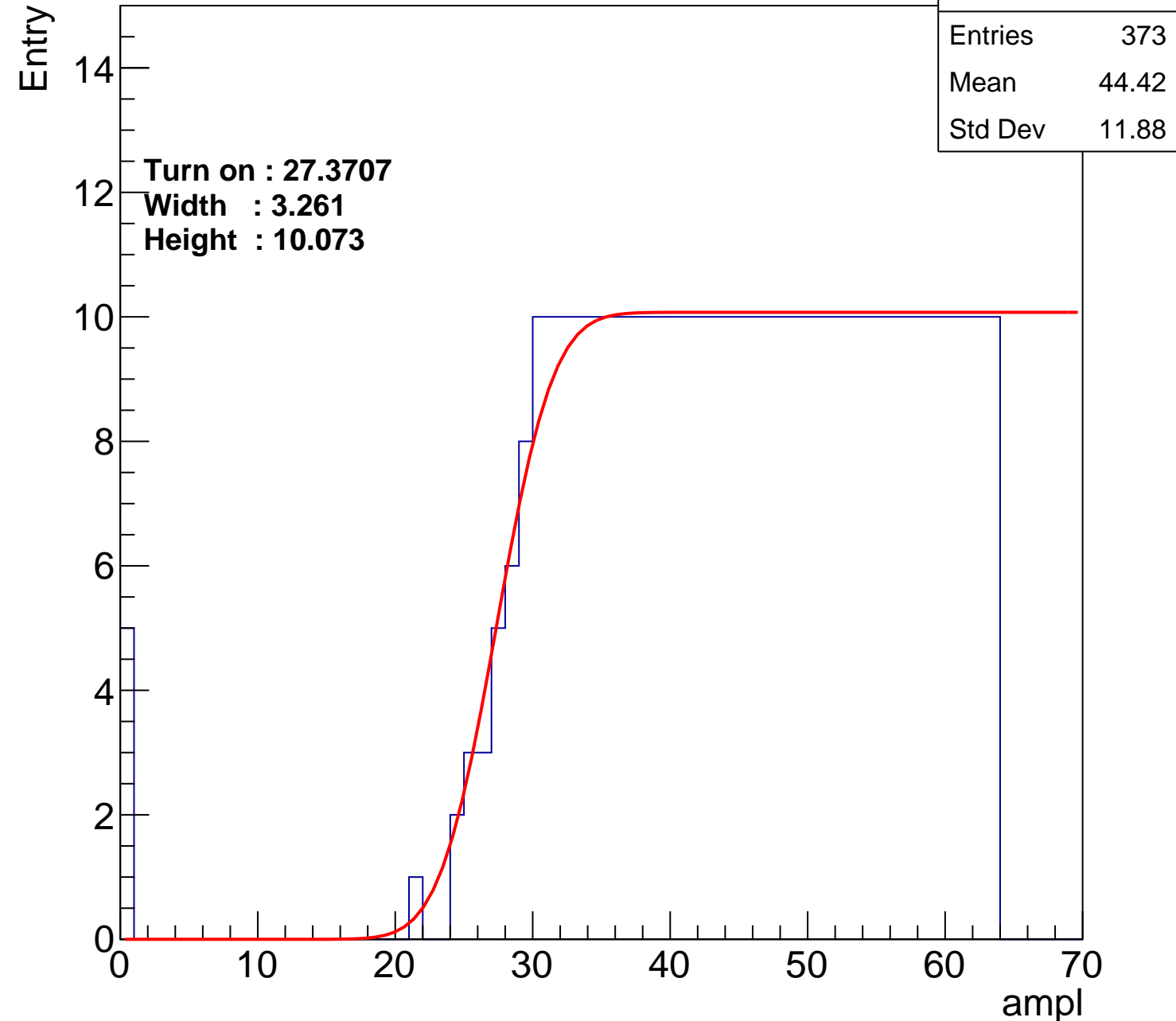
Width : 3.261

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 25.7907

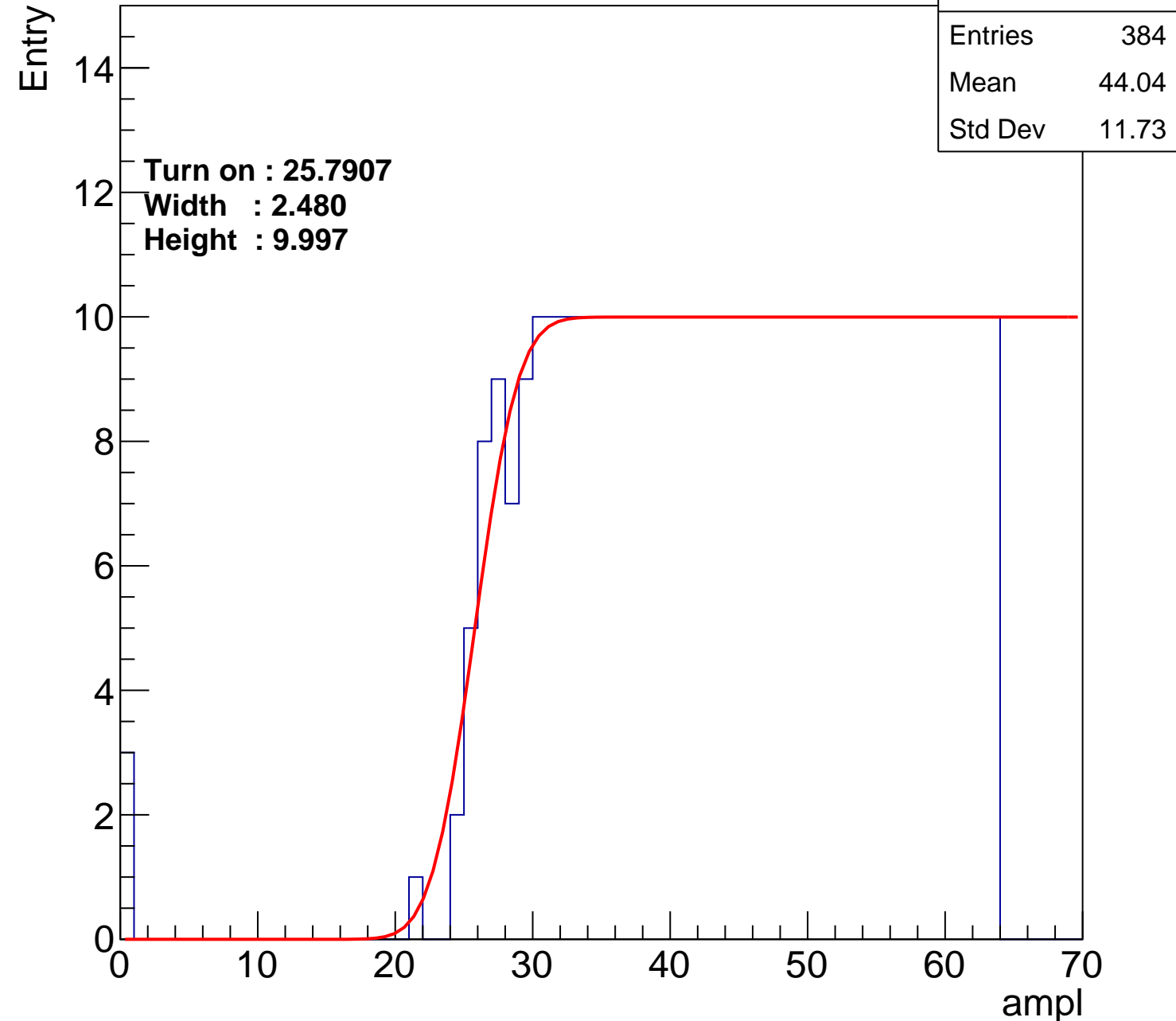
Width : 2.480

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.44
Std Dev	10.9

Turn on : 27.9969

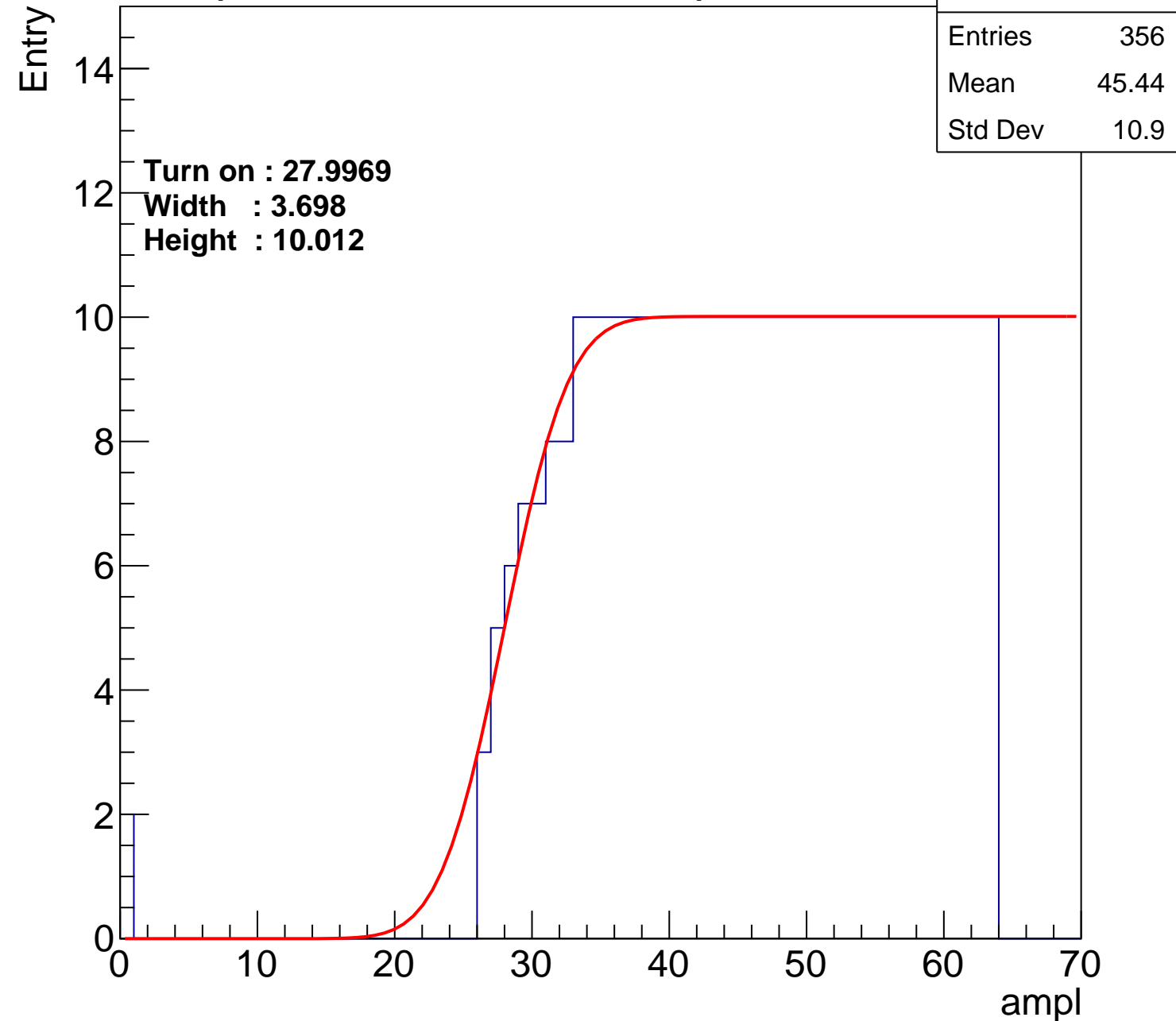
Width : 3.698

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.23
Std Dev	11.96

Turn on : 26.9428

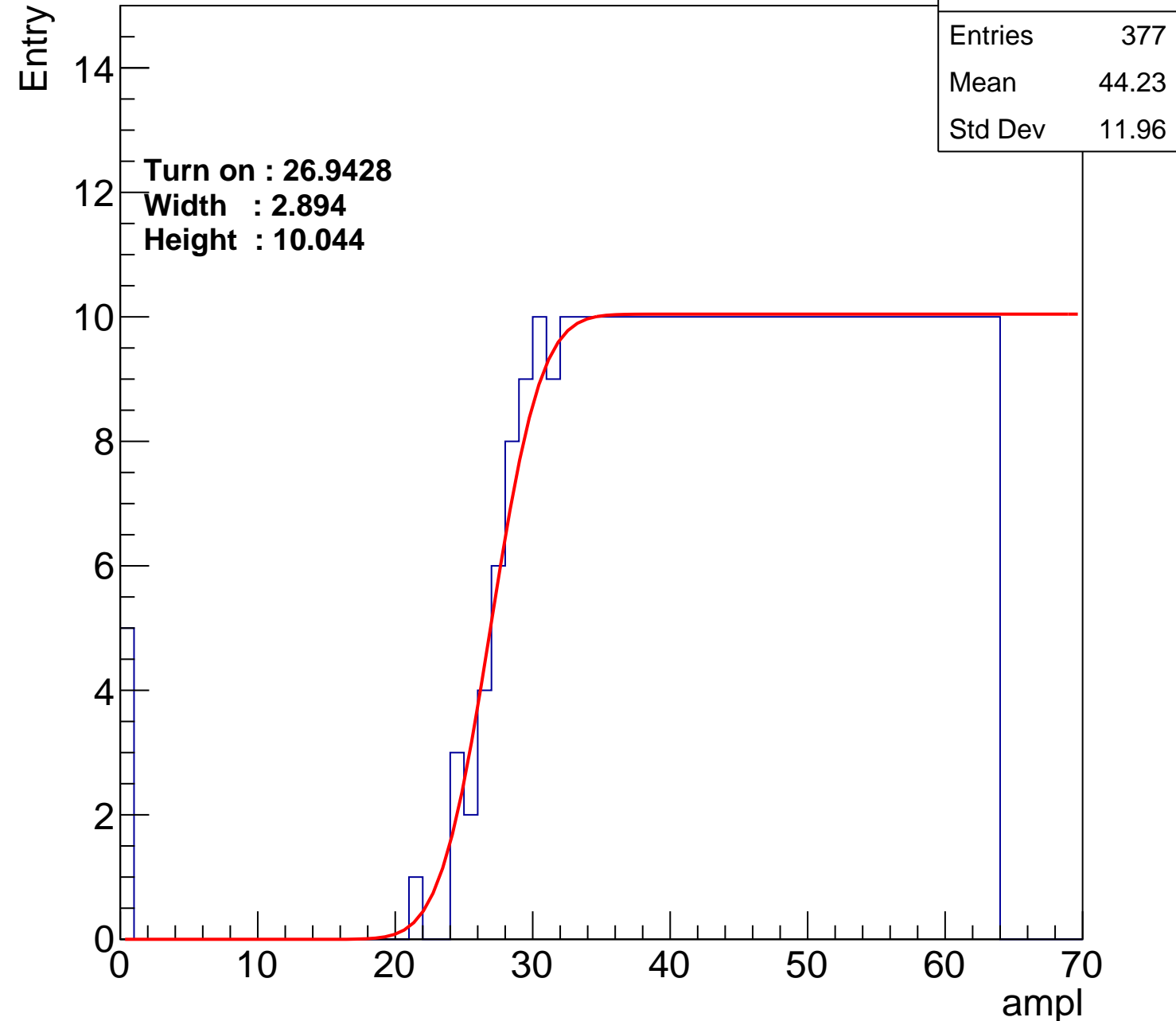
Width : 2.894

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.98
Std Dev	11.03

Turn on : 27.9223

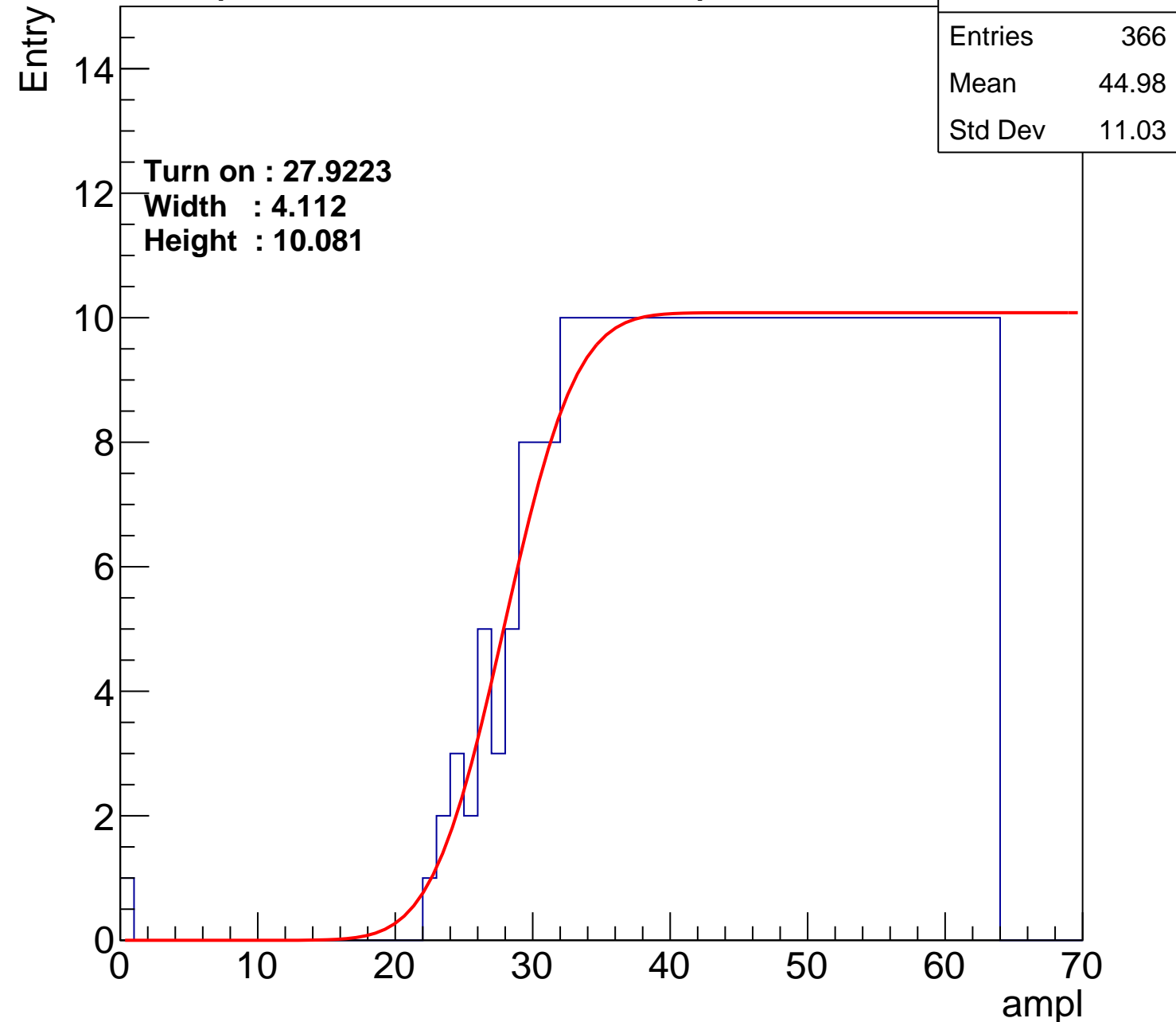
Width : 4.112

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch86

calib_packv5_042523_0143.root, FC#8, port C1

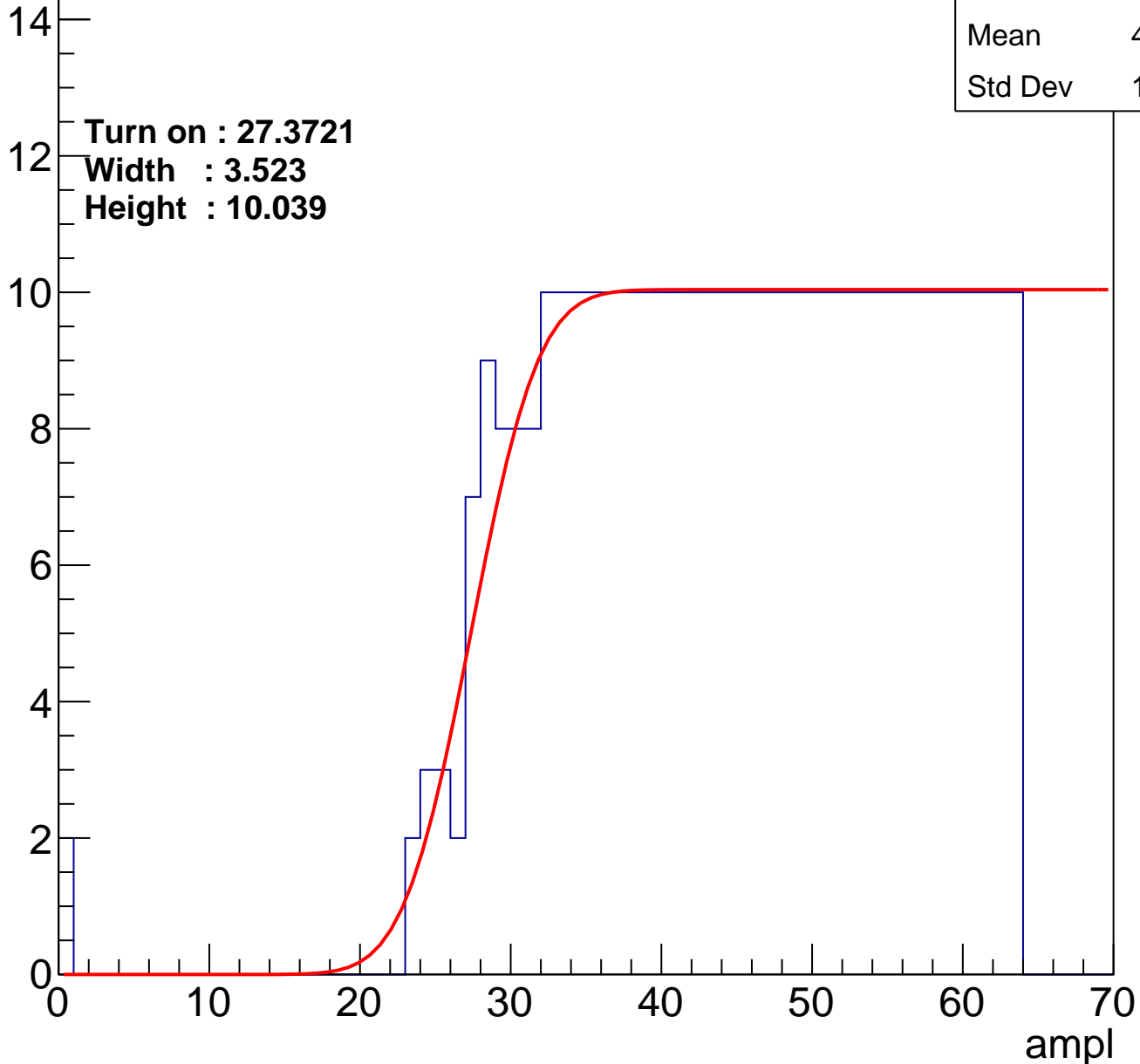
Entries	372
Mean	44.64
Std Dev	11.33

Turn on : 27.3721

Width : 3.523

Height : 10.039

Entry



B0L002S, U3-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.17
Std Dev	10.88

Turn on : 28.2571

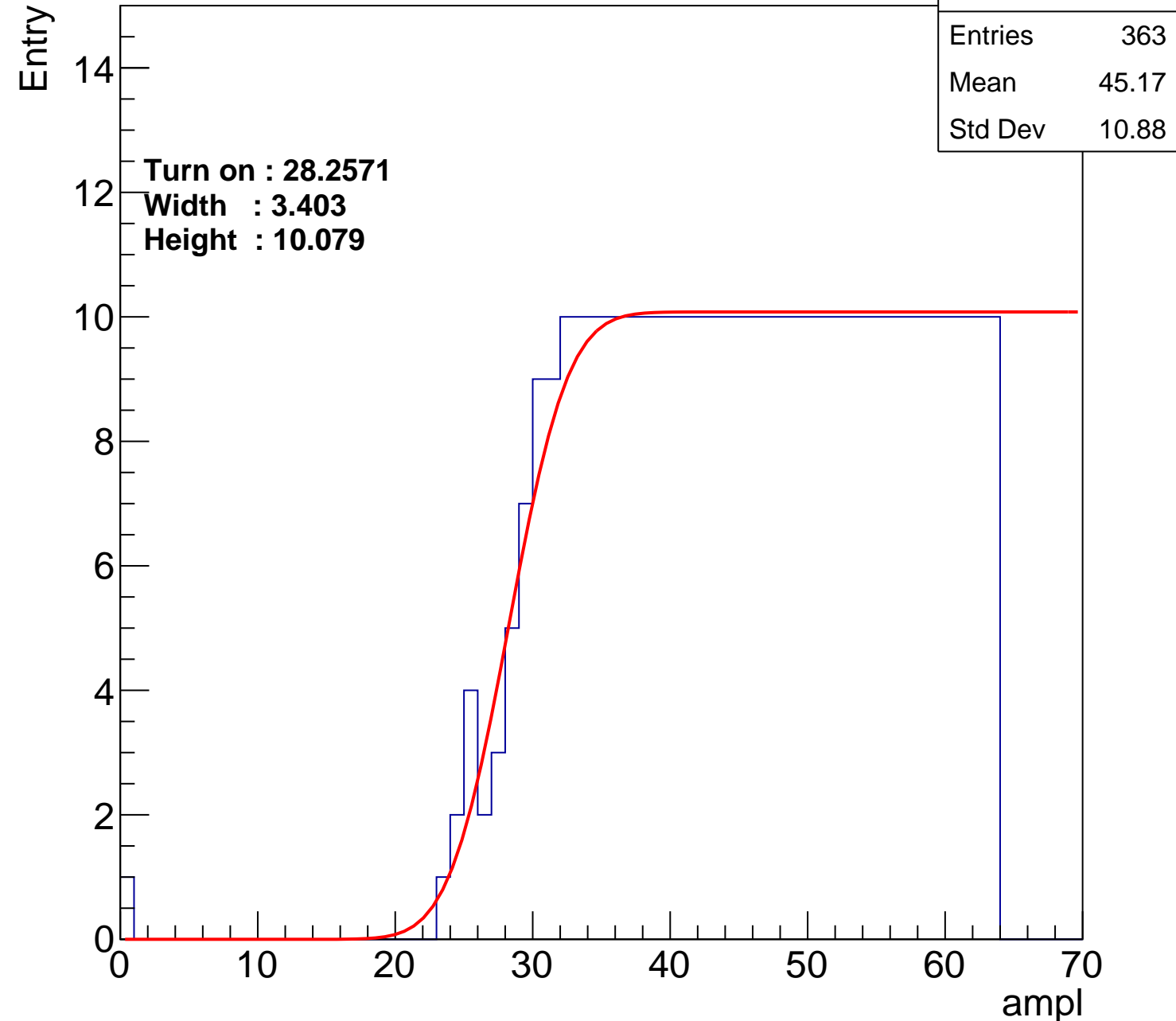
Width : 3.403

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch88

calib_packv5_042523_0143.root, FC#8, port C1

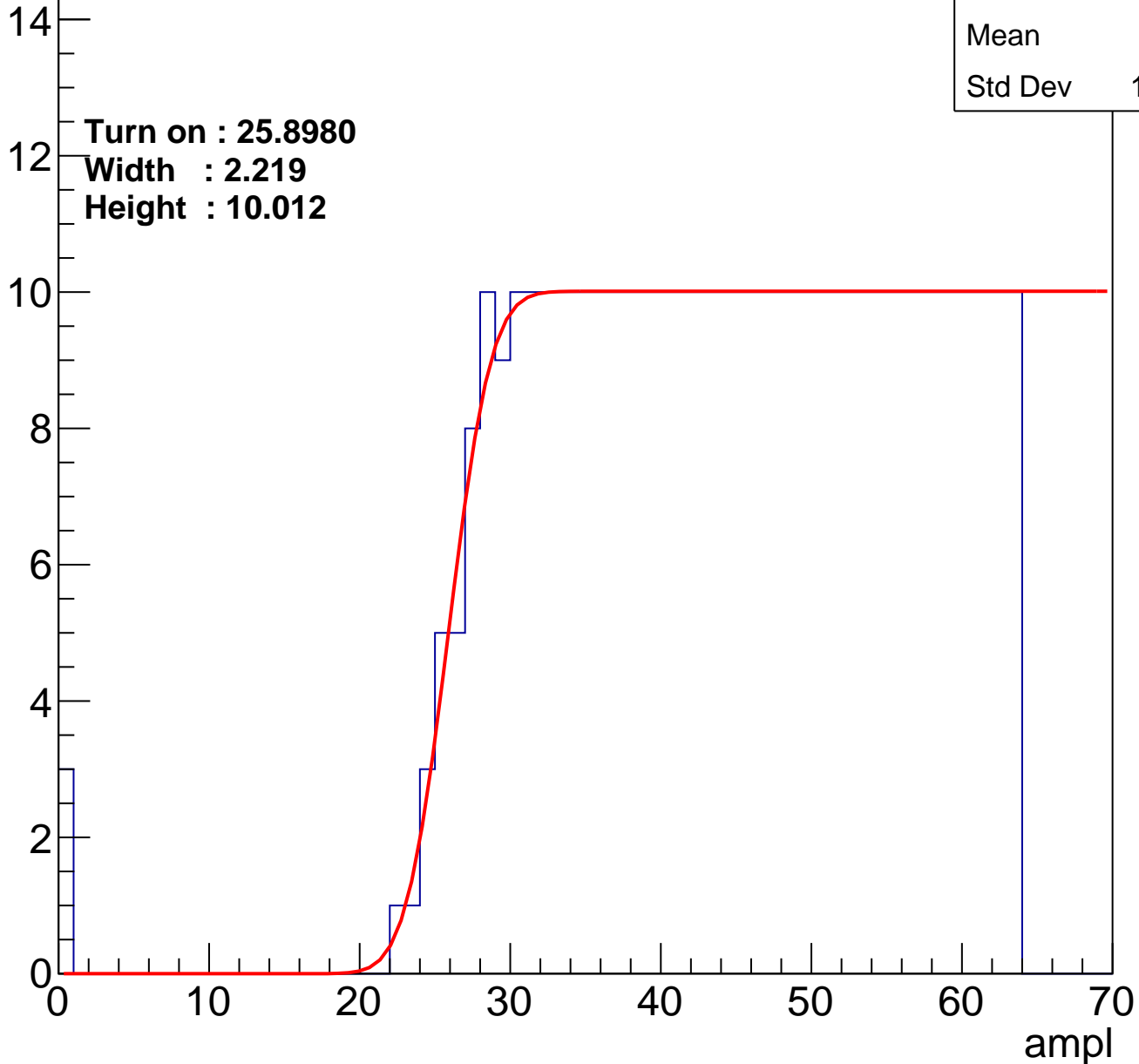
Entry

Entries	385
Mean	44
Std Dev	11.74

Turn on : 25.8980

Width : 2.219

Height : 10.012



B0L002S, U3-ch89

calib_packv5_042523_0143.root, FC#8, port C1

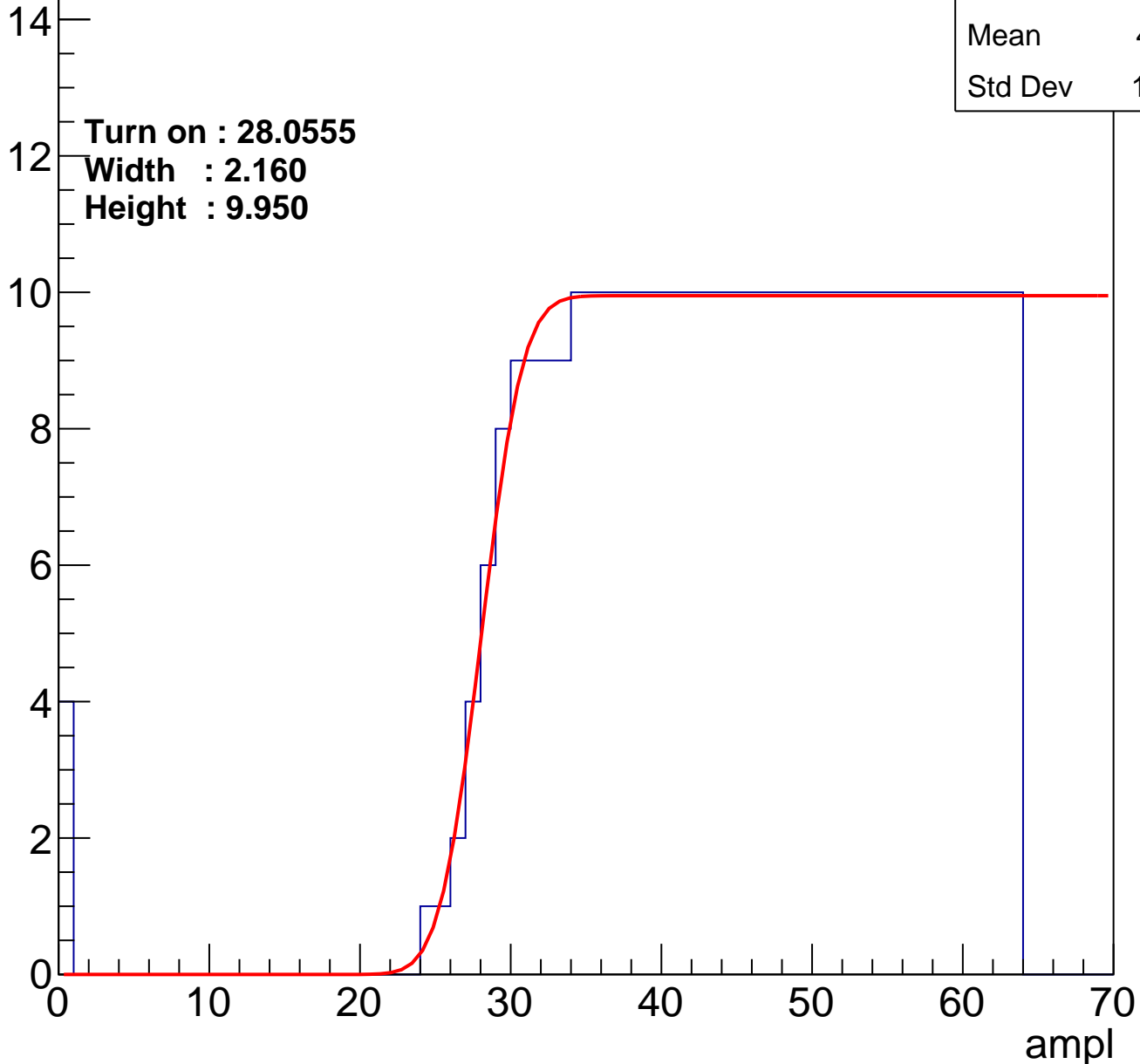
Entries	362
Mean	45.01
Std Dev	11.45

Turn on : 28.0555

Width : 2.160

Height : 9.950

Entry



B0L002S, U3-ch90

calib_packv5_042523_0143.root, FC#8, port C1

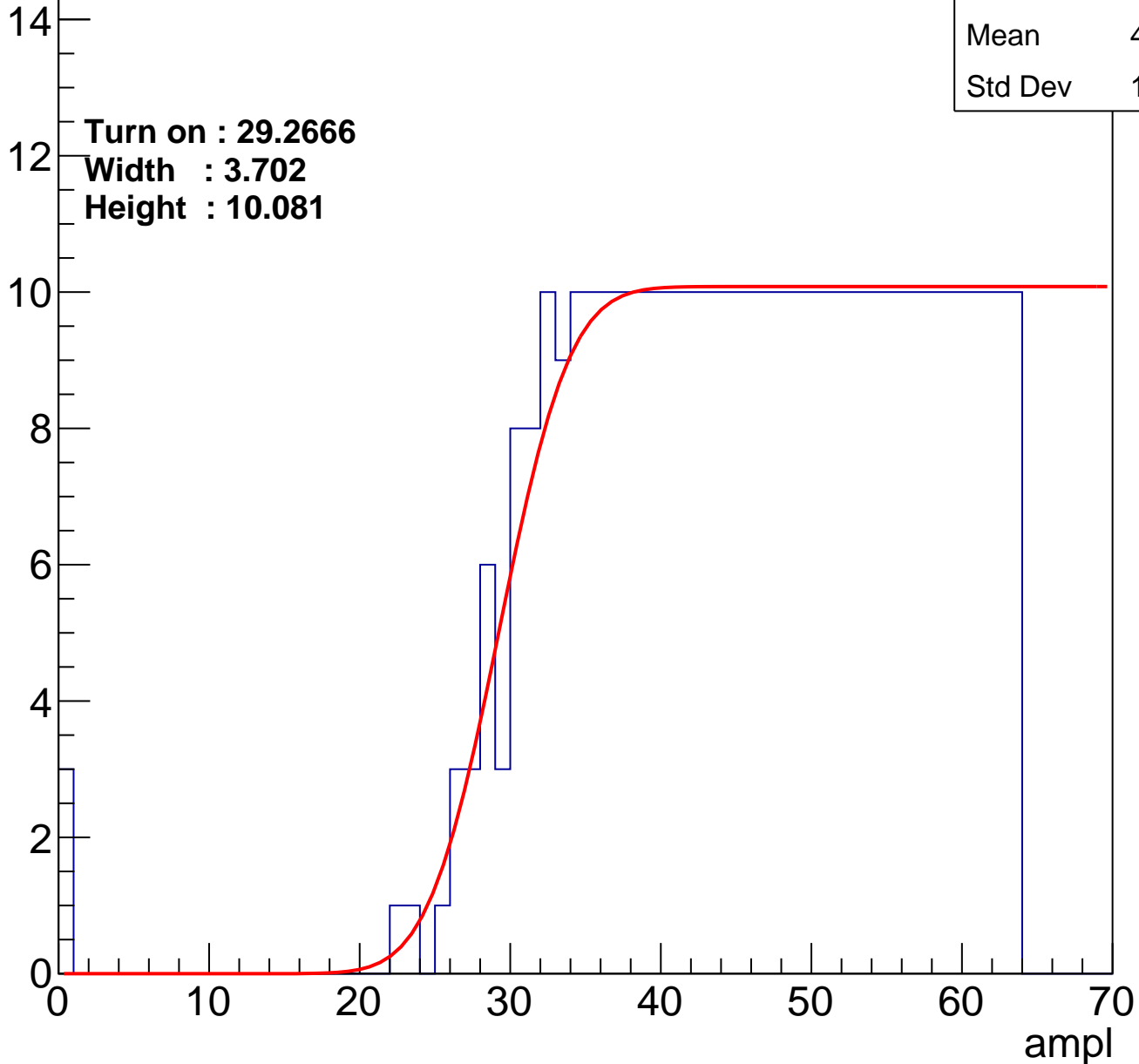
Entries	356
Mean	45.33
Std Dev	11.18

Turn on : 29.2666

Width : 3.702

Height : 10.081

Entry



B0L002S, U3-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.08
Std Dev	11.17

Turn on : 28.7067

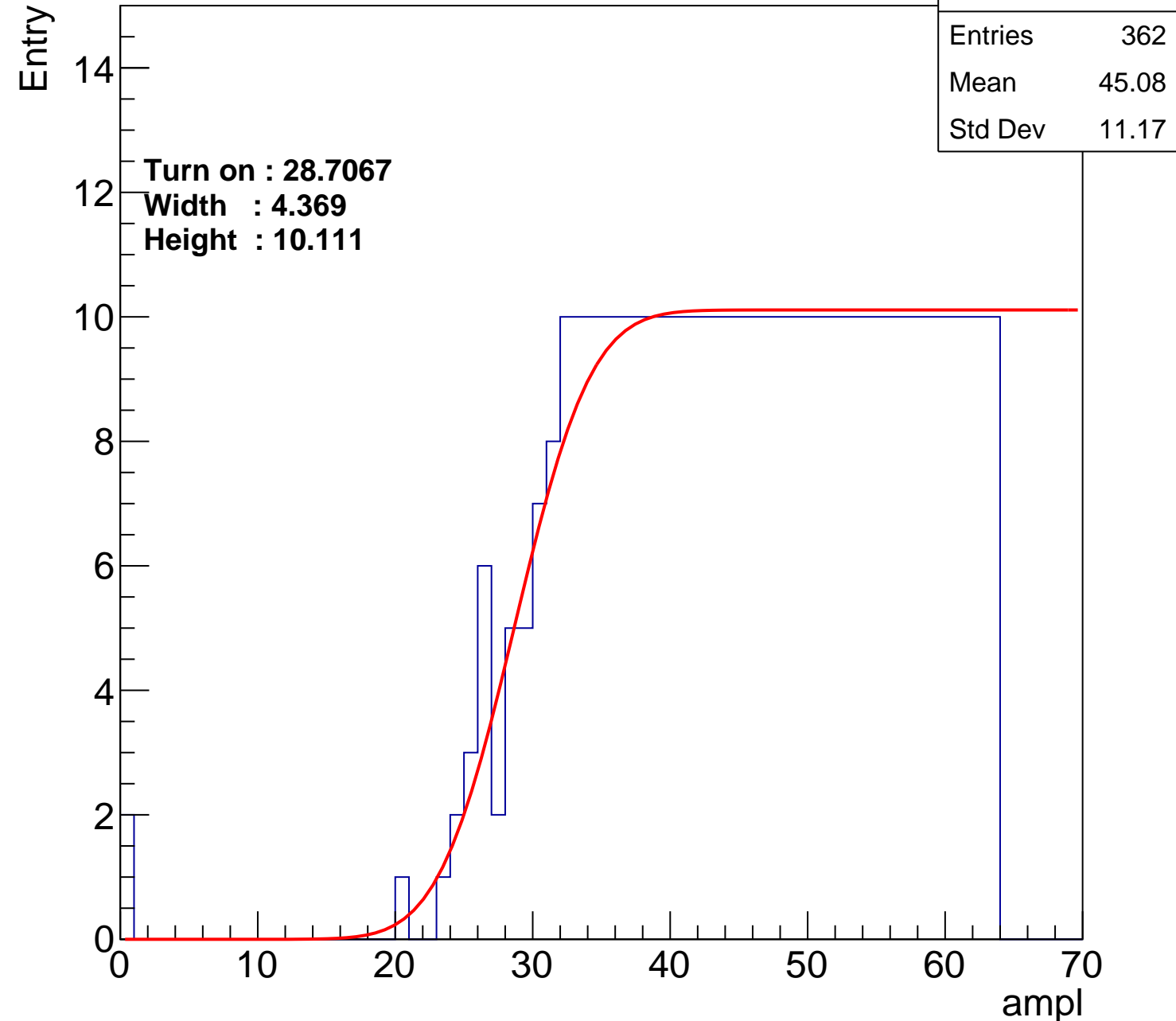
Width : 4.369

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch92

calib_packv5_042523_0143.root, FC#8, port C1

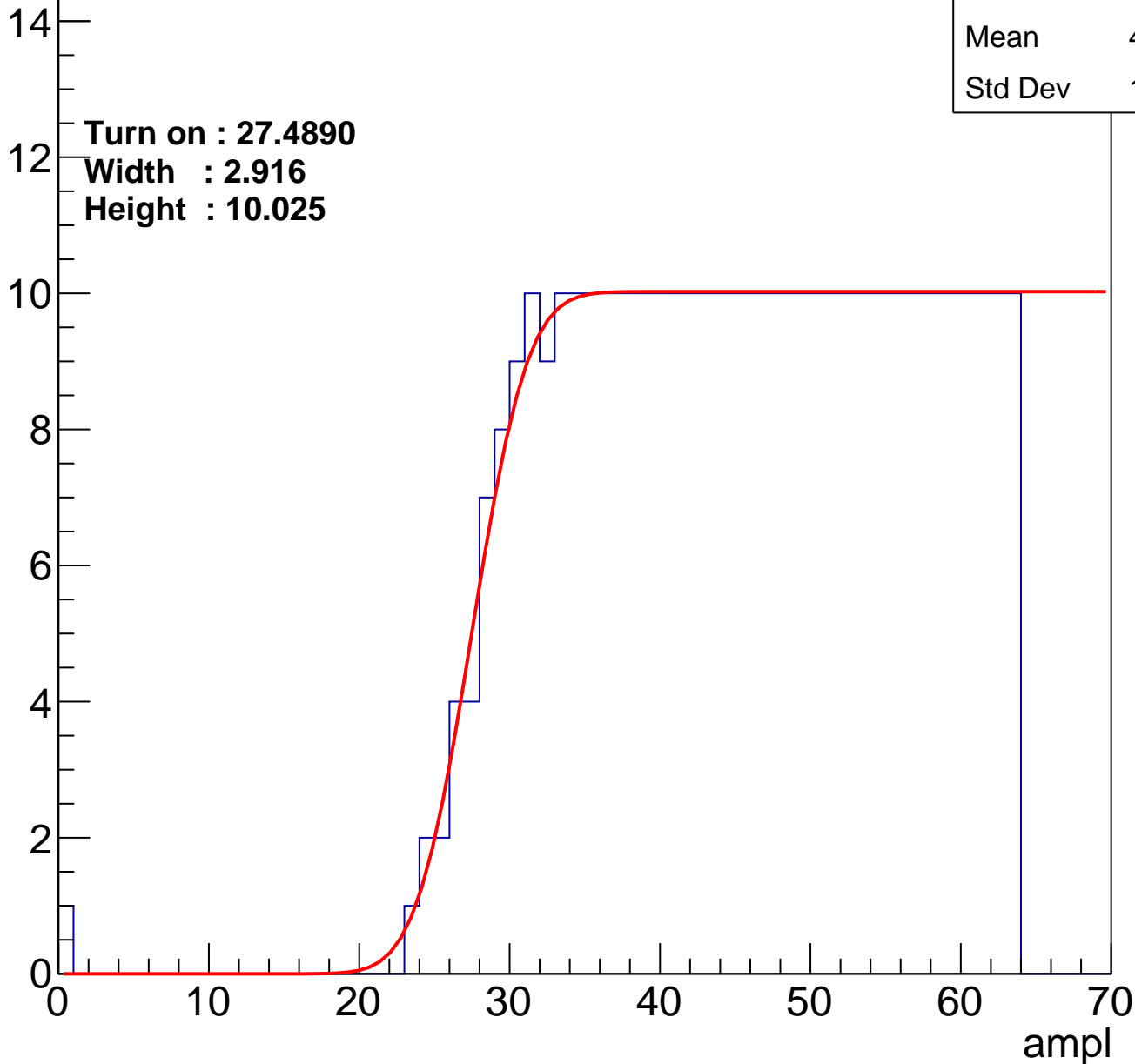
Entry

Entries	367
Mean	44.98
Std Dev	10.96

Turn on : 27.4890

Width : 2.916

Height : 10.025



B0L002S, U3-ch93

calib_packv5_042523_0143.root, FC#8, port C1

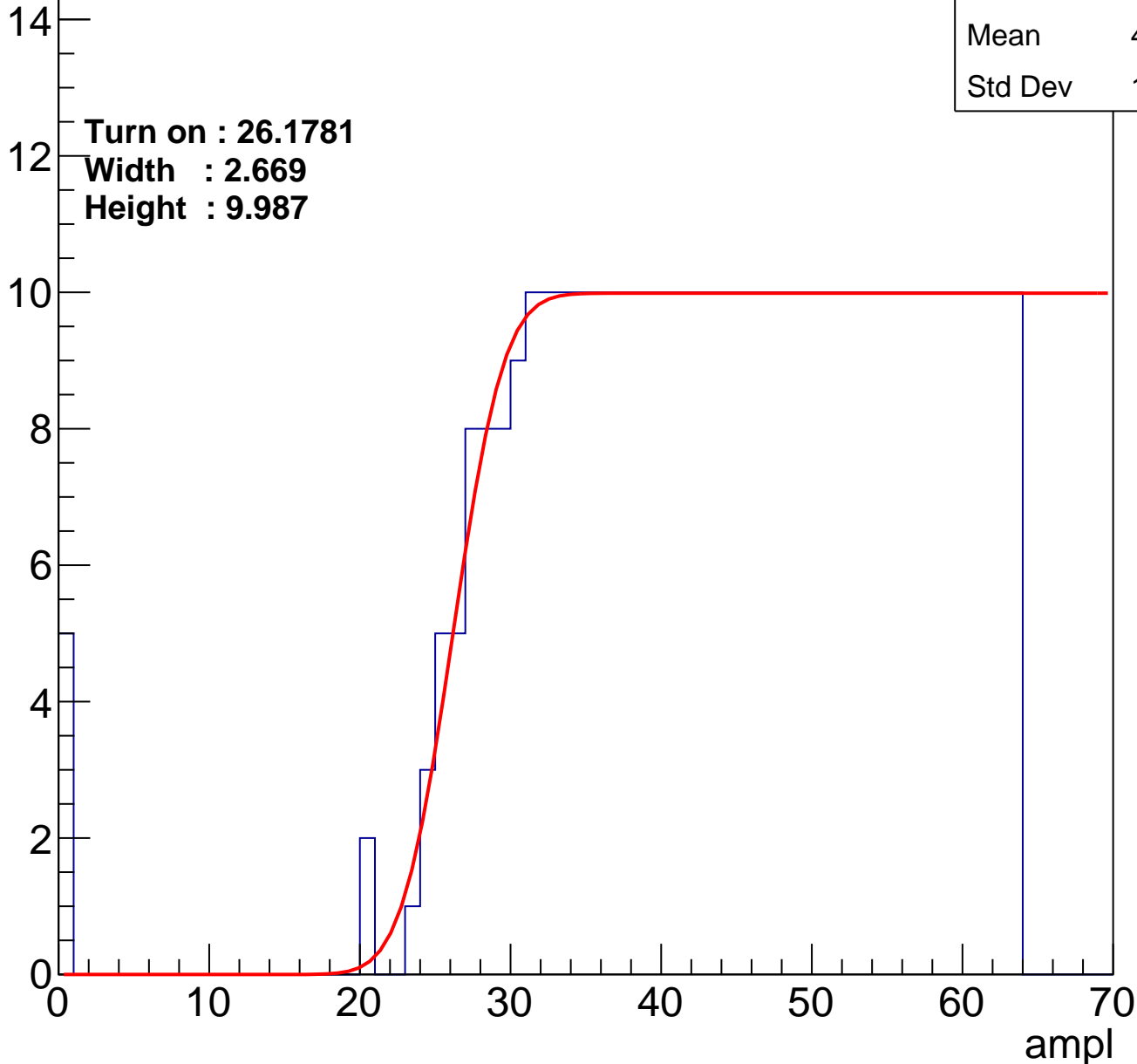
Entry

Entries	384
Mean	43.86
Std Dev	12.15

Turn on : 26.1781

Width : 2.669

Height : 9.987



B0L002S, U3-ch94

calib_packv5_042523_0143.root, FC#8, port C1

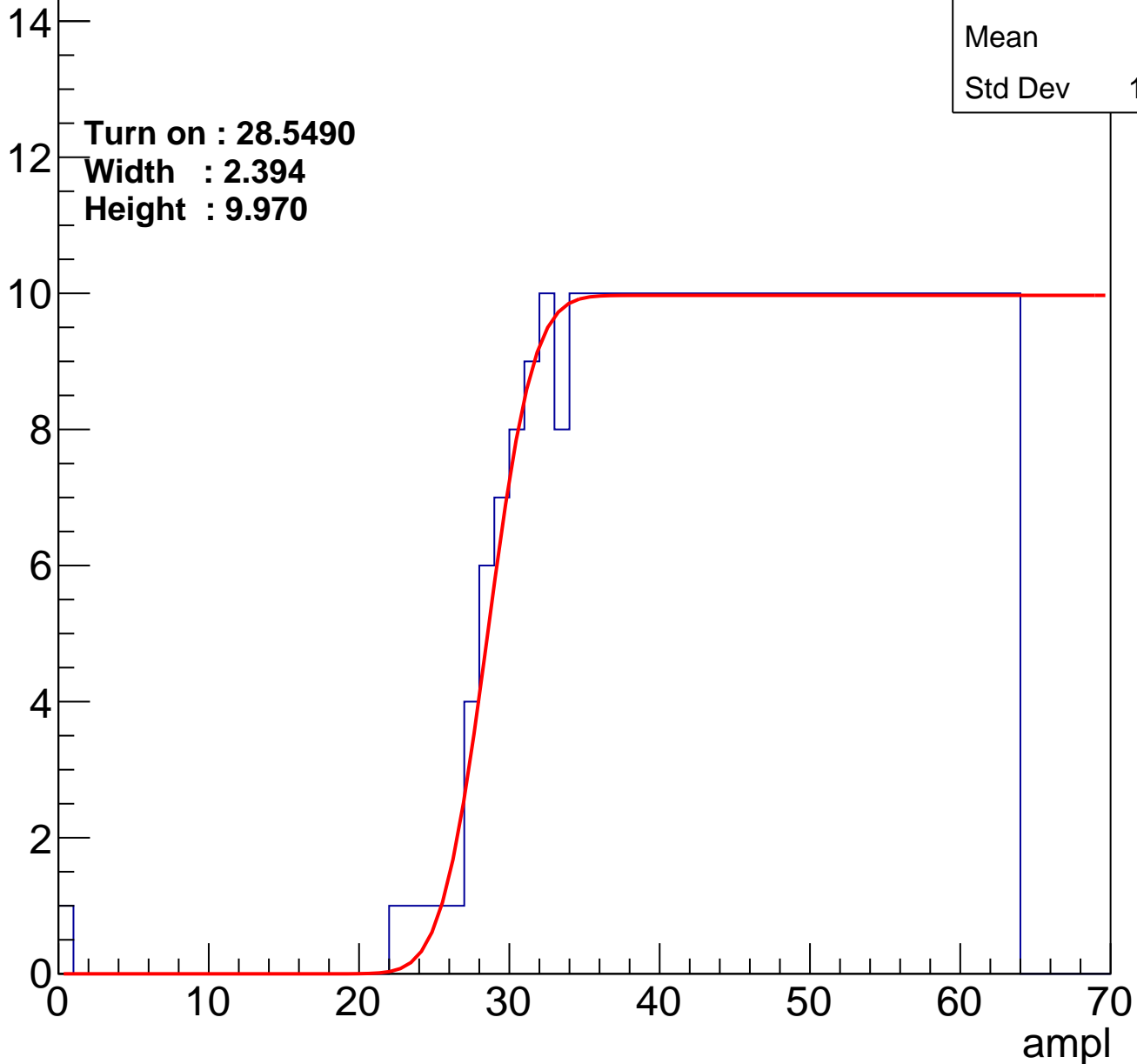
Entry

Entries	358
Mean	45.4
Std Dev	10.77

Turn on : 28.5490

Width : 2.394

Height : 9.970



B0L002S, U3-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.13
Std Dev	11.42

Turn on : 28.2557

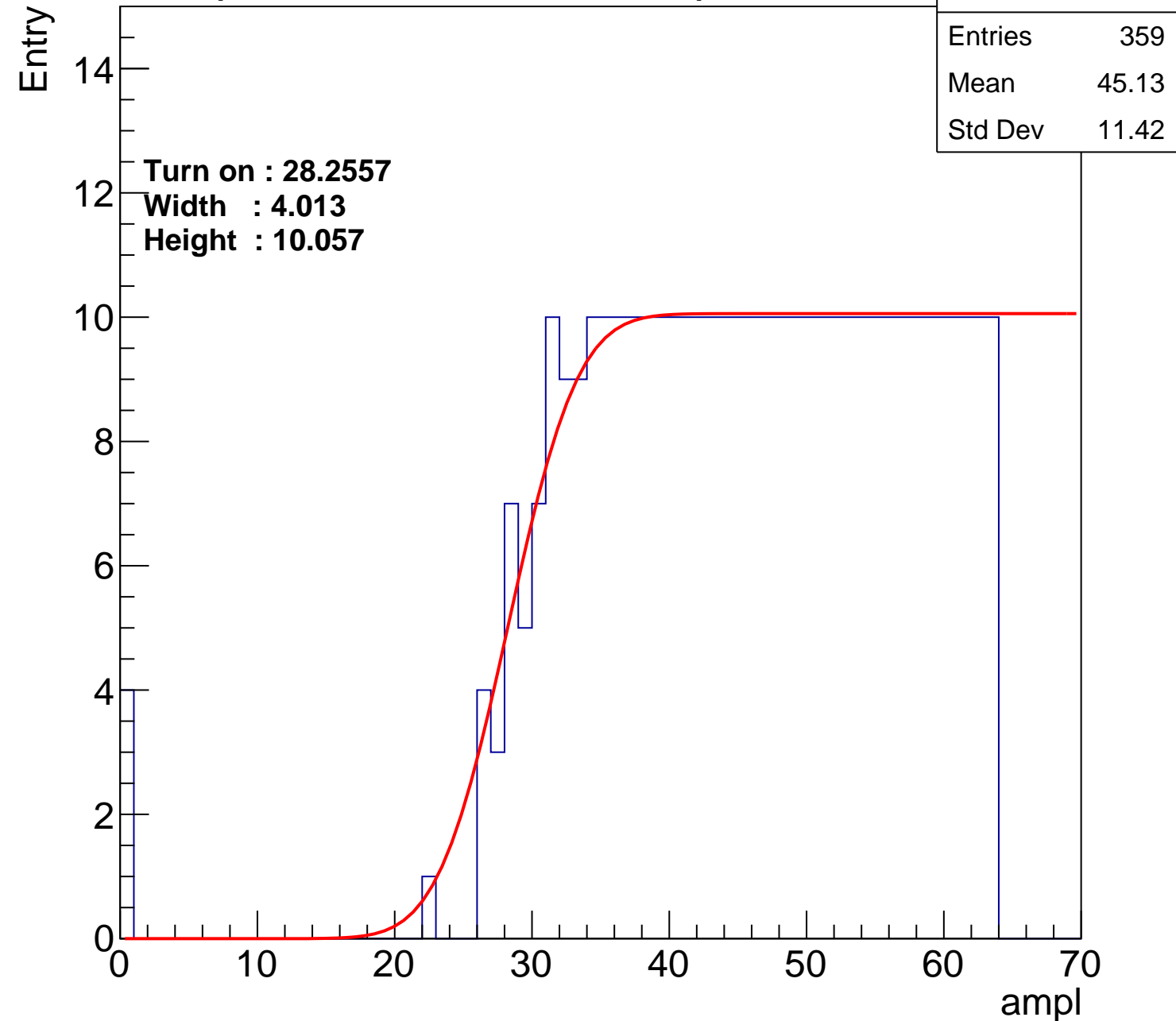
Width : 4.013

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch96

calib_packv5_042523_0143.root, FC#8, port C1

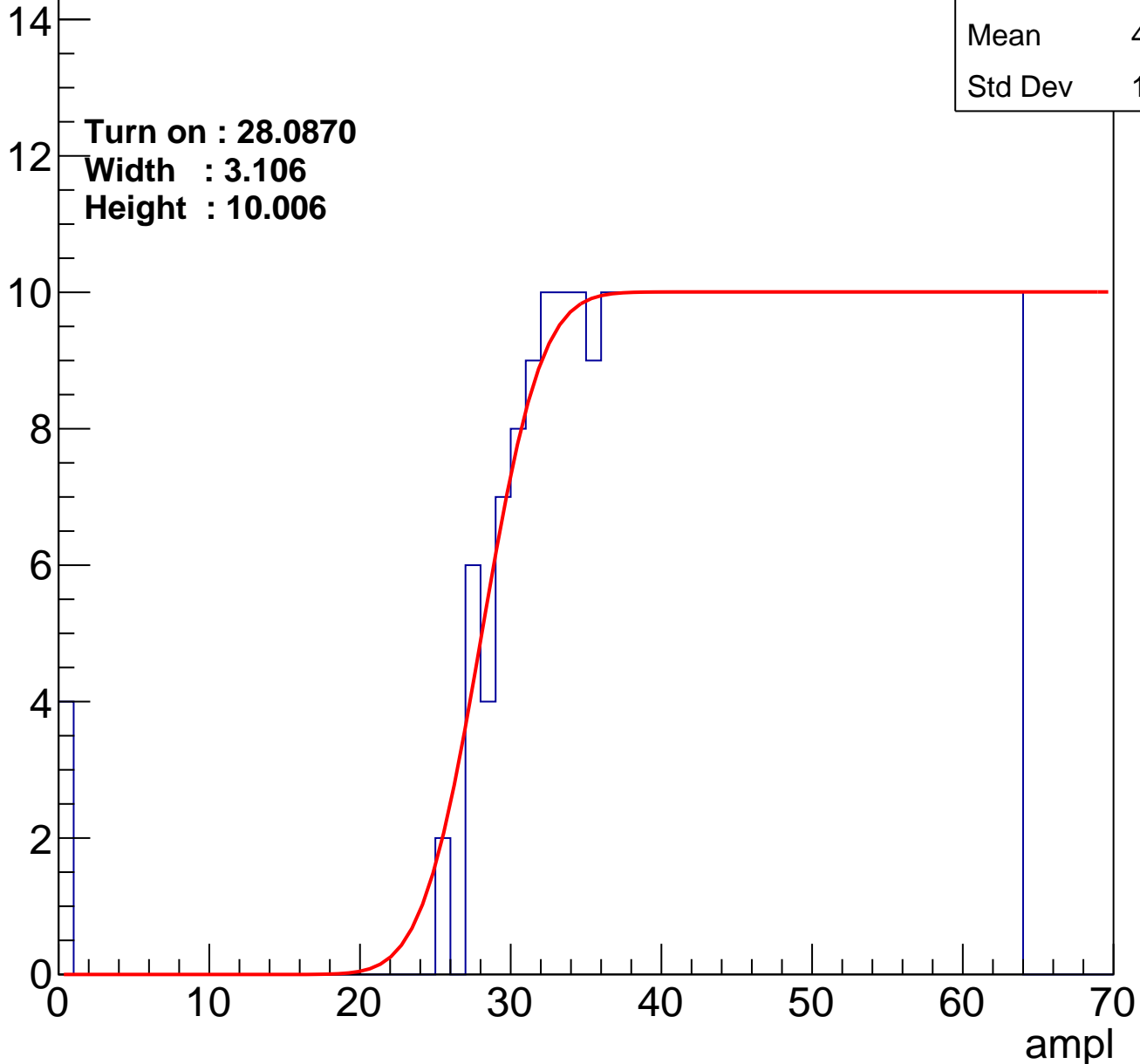
Entries	359
Mean	45.16
Std Dev	11.38

Turn on : 28.0870

Width : 3.106

Height : 10.006

Entry



B0L002S, U3-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.95
Std Dev	11.31

Turn on : 27.3887

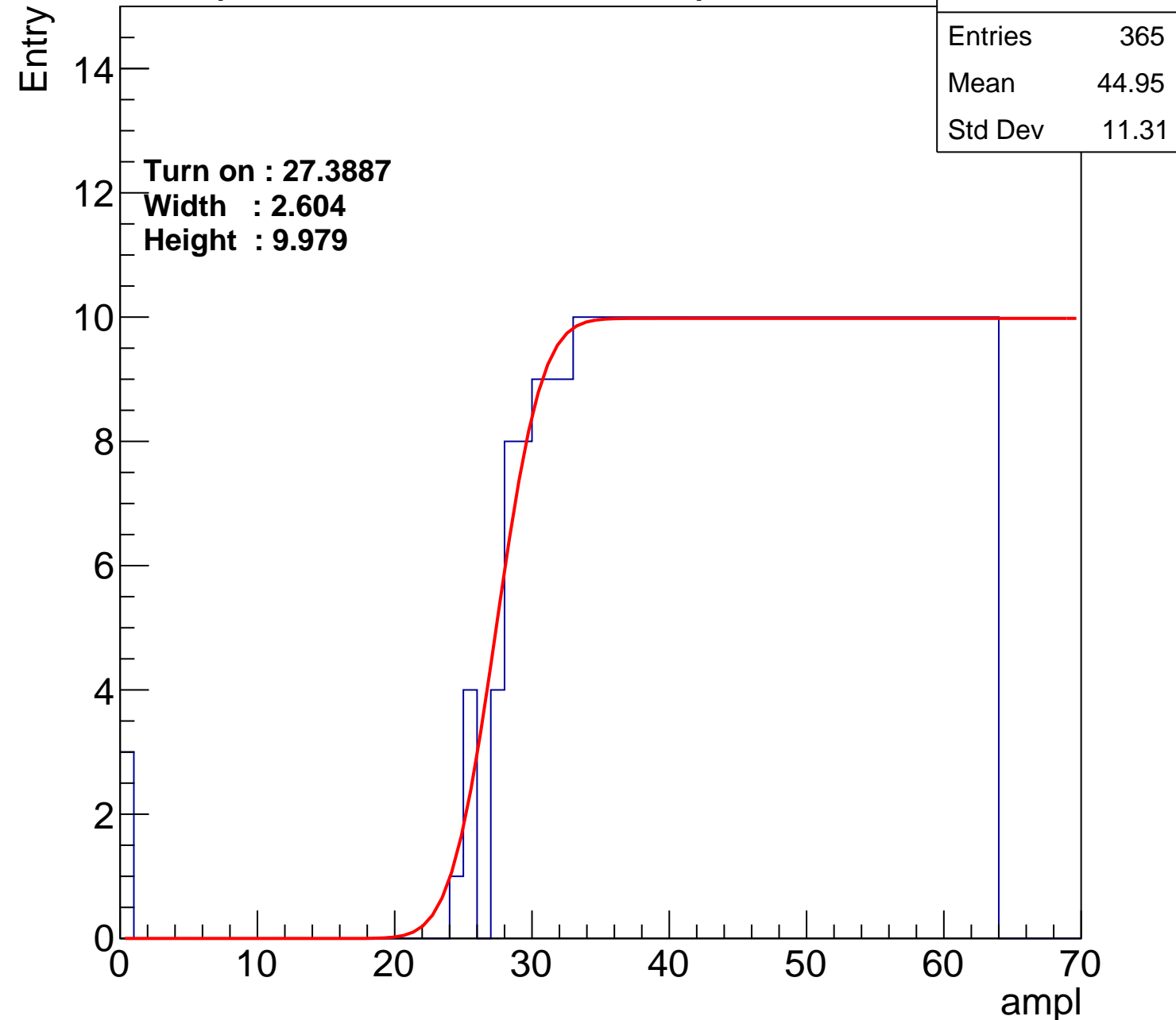
Width : 2.604

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.2680

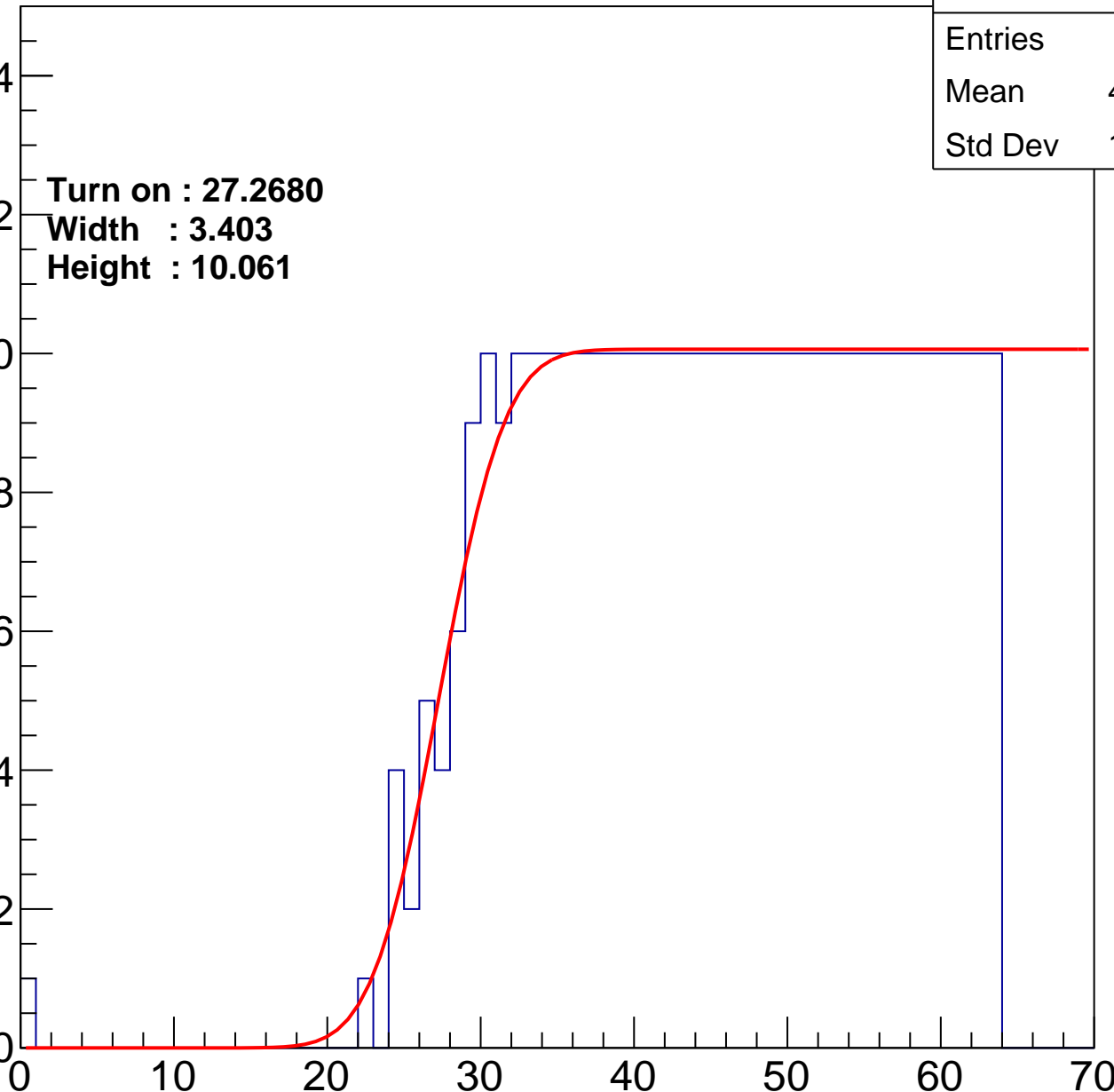
Width : 3.403

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch99

calib_packv5_042523_0143.root, FC#8, port C1

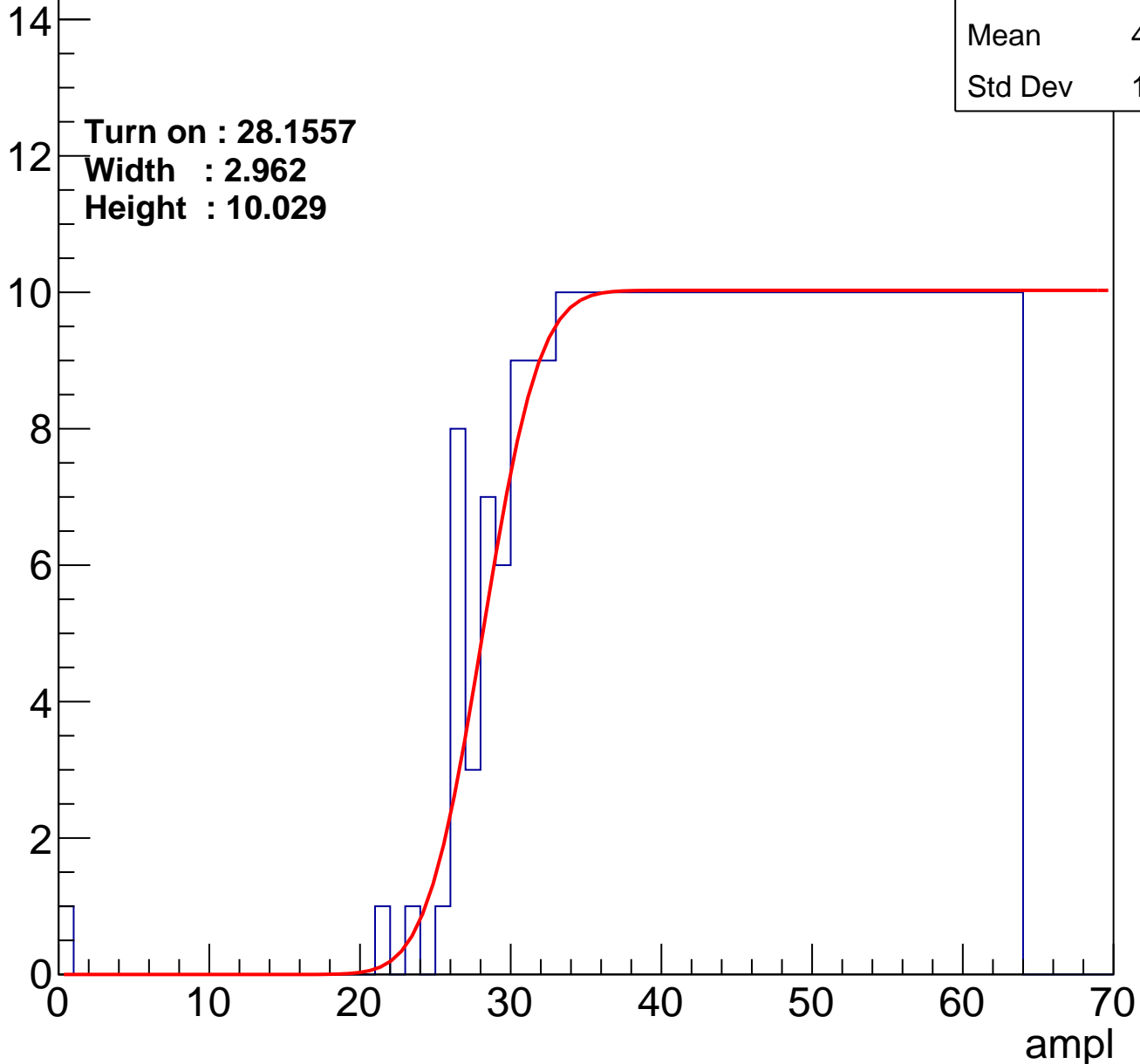
Entries	365
Mean	45.05
Std Dev	10.95

Turn on : 28.1557

Width : 2.962

Height : 10.029

Entry



B0L002S, U3-ch100

calib_packv5_042523_0143.root, FC#8, port C1

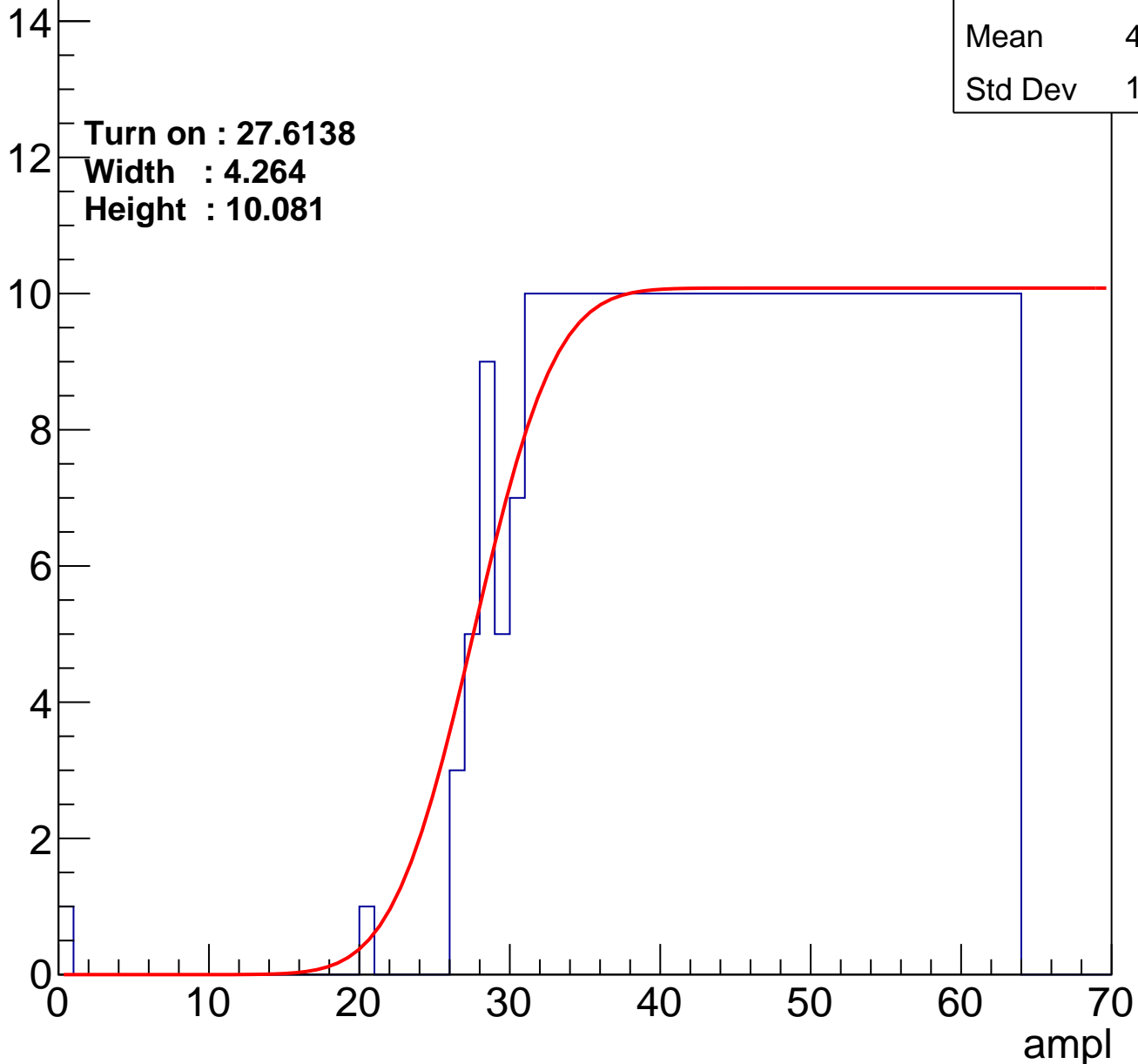
Entries	361
Mean	45.29
Std Dev	10.79

Turn on : 27.6138

Width : 4.264

Height : 10.081

Entry



B0L002S, U3-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.17
Std Dev	11.08

Turn on : 28.6800

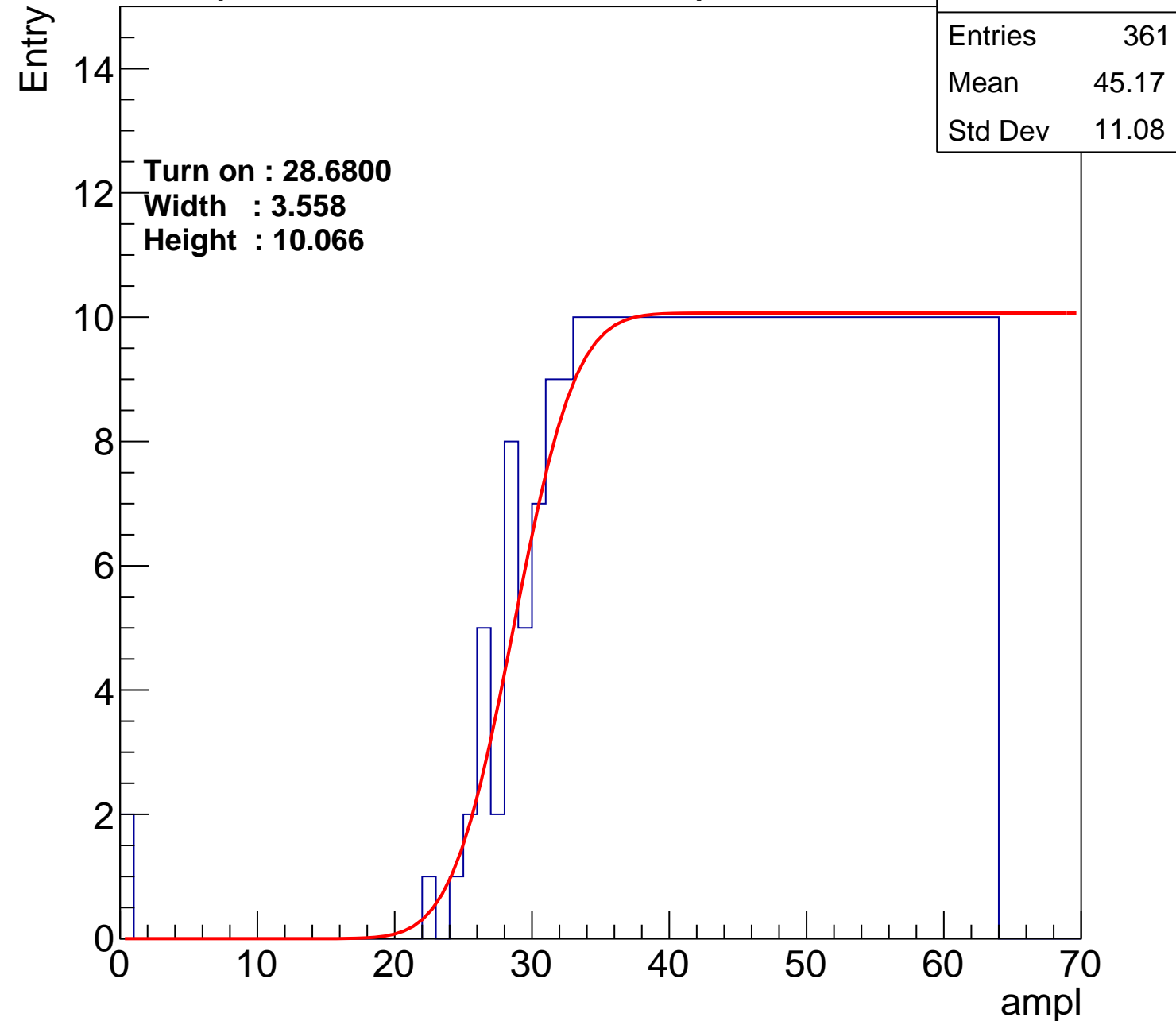
Width : 3.558

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch102

calib_packv5_042523_0143.root, FC#8, port C1

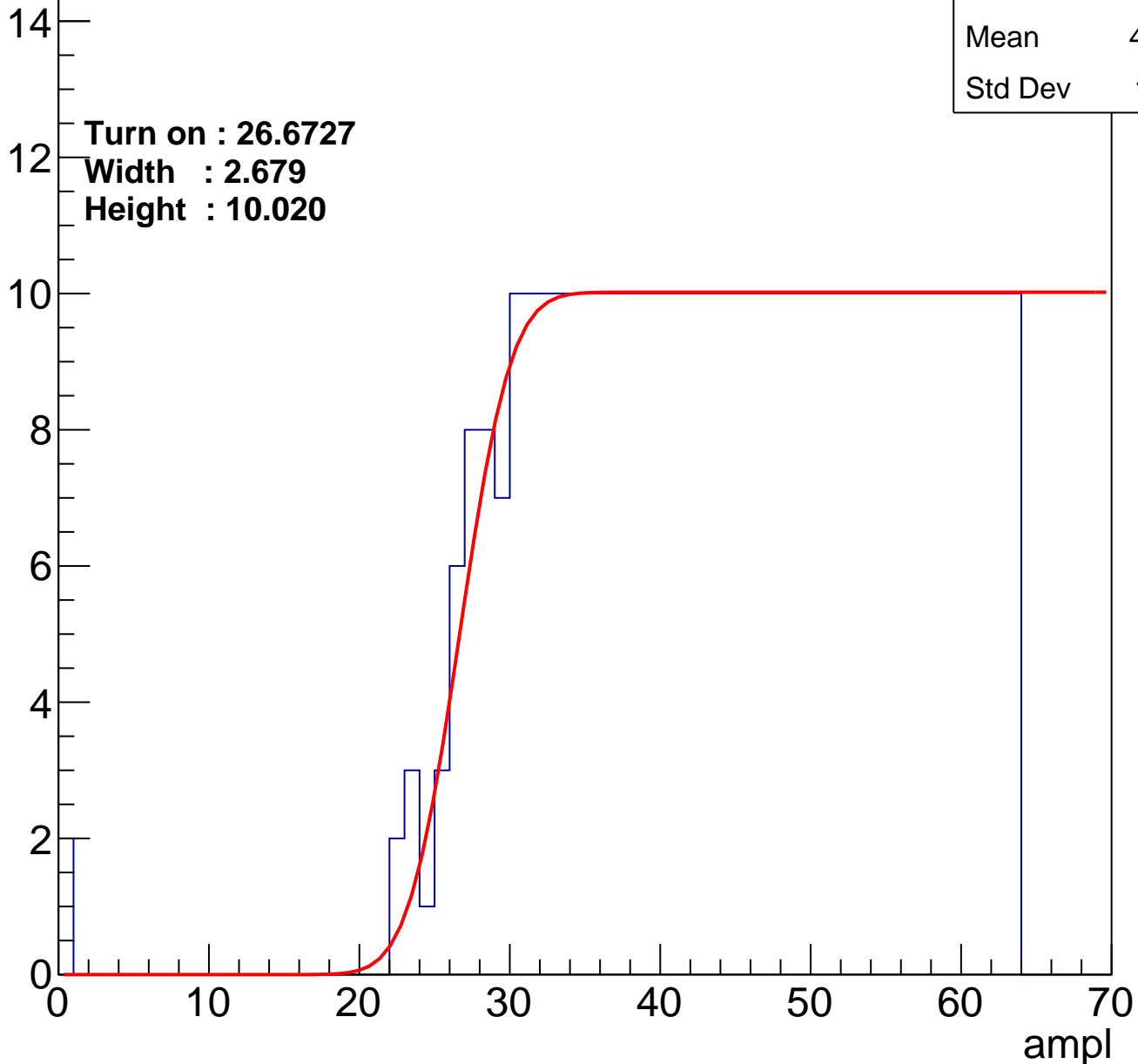
Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.6727

Width : 2.679

Height : 10.020

Entry



B0L002S, U3-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.01
Std Dev	11.11

Turn on : 28.0229

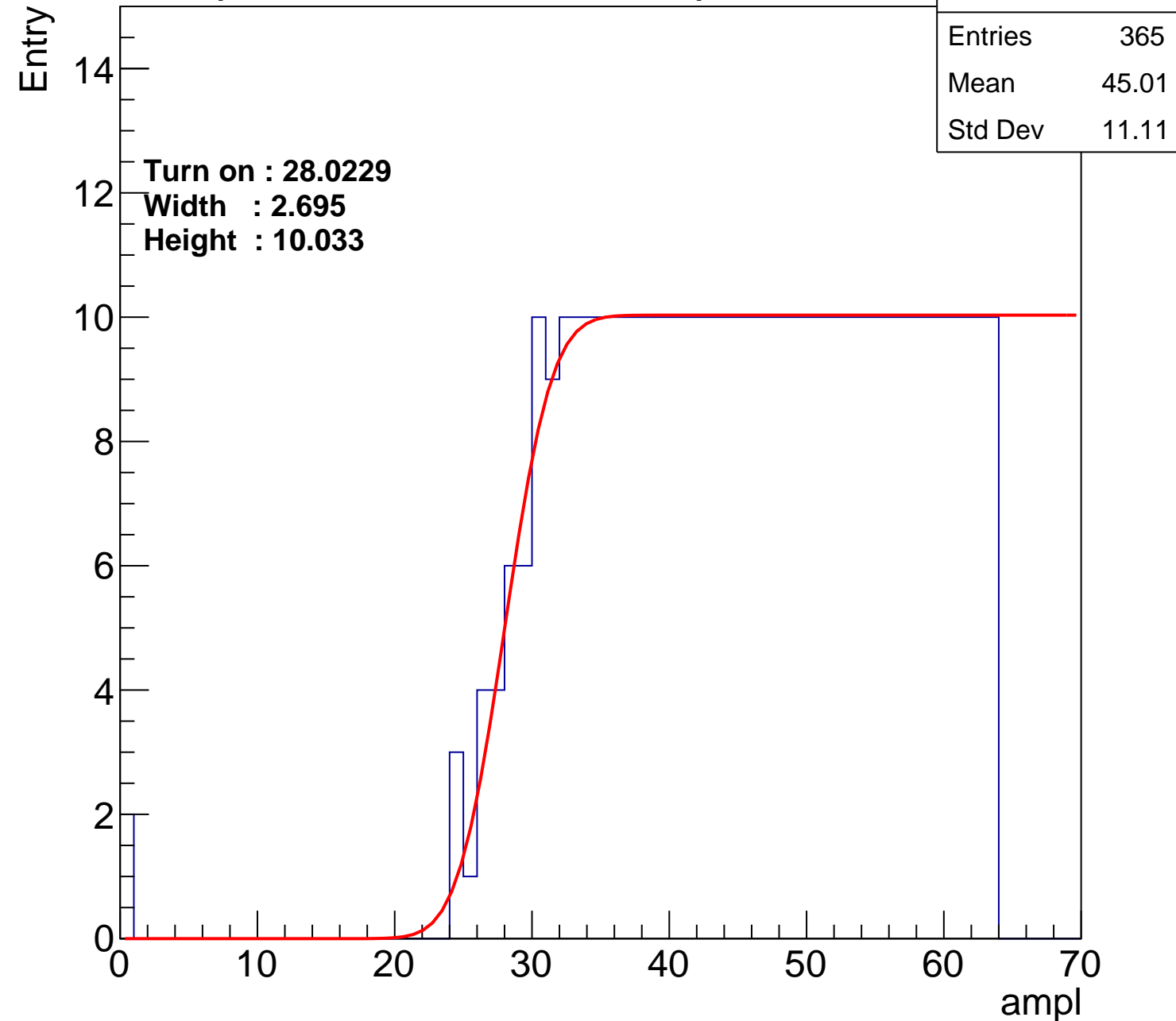
Width : 2.695

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 27.6574

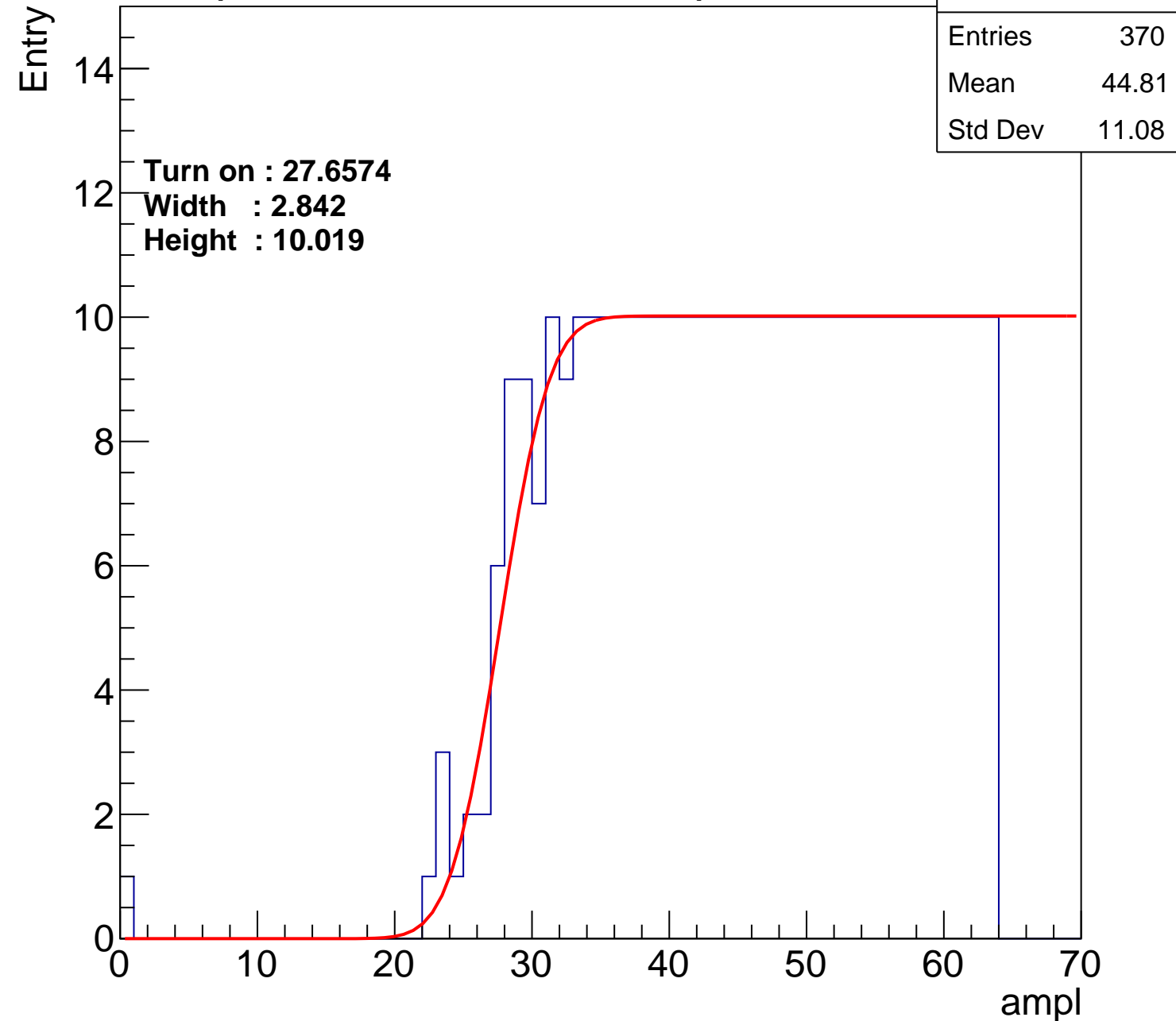
Width : 2.842

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.81
Std Dev	11.23

Turn on : 27.4464

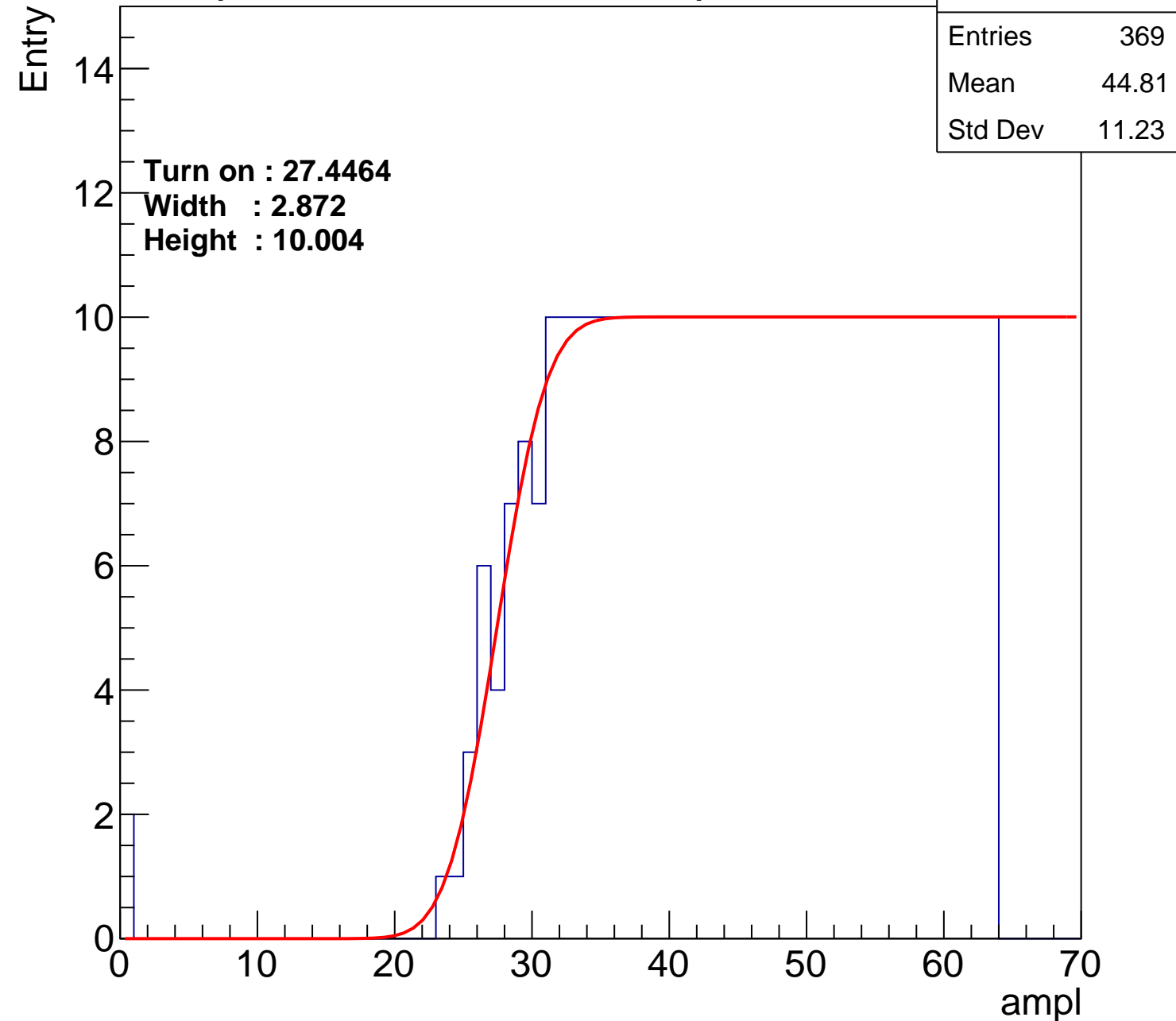
Width : 2.872

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 27.4113

Width : 3.197

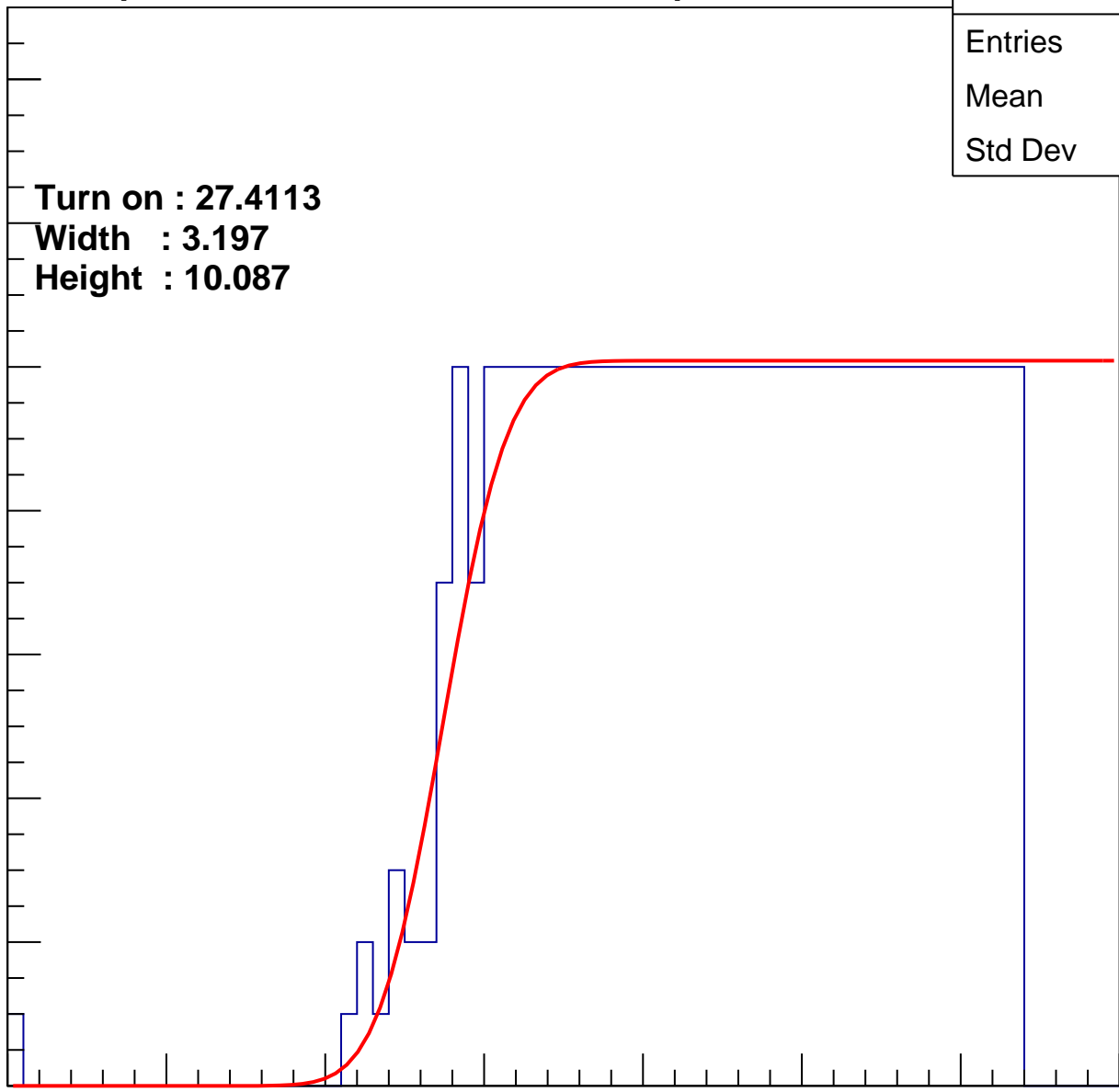
Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U3-ch107

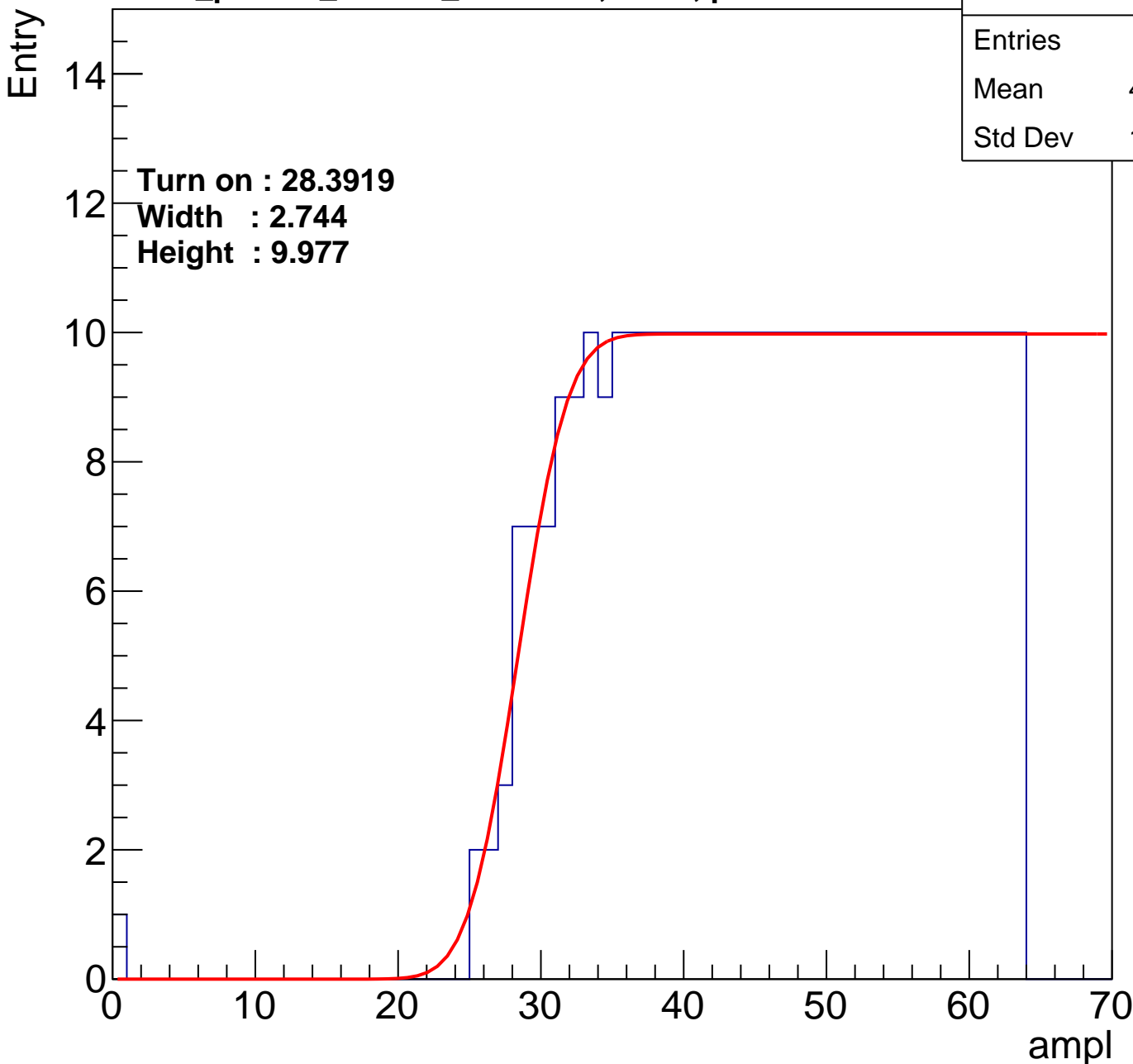
calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.52
Std Dev	10.67

Turn on : 28.3919

Width : 2.744

Height : 9.977



B0L002S, U3-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	44.94
Std Dev	11.85

Turn on : 28.6144

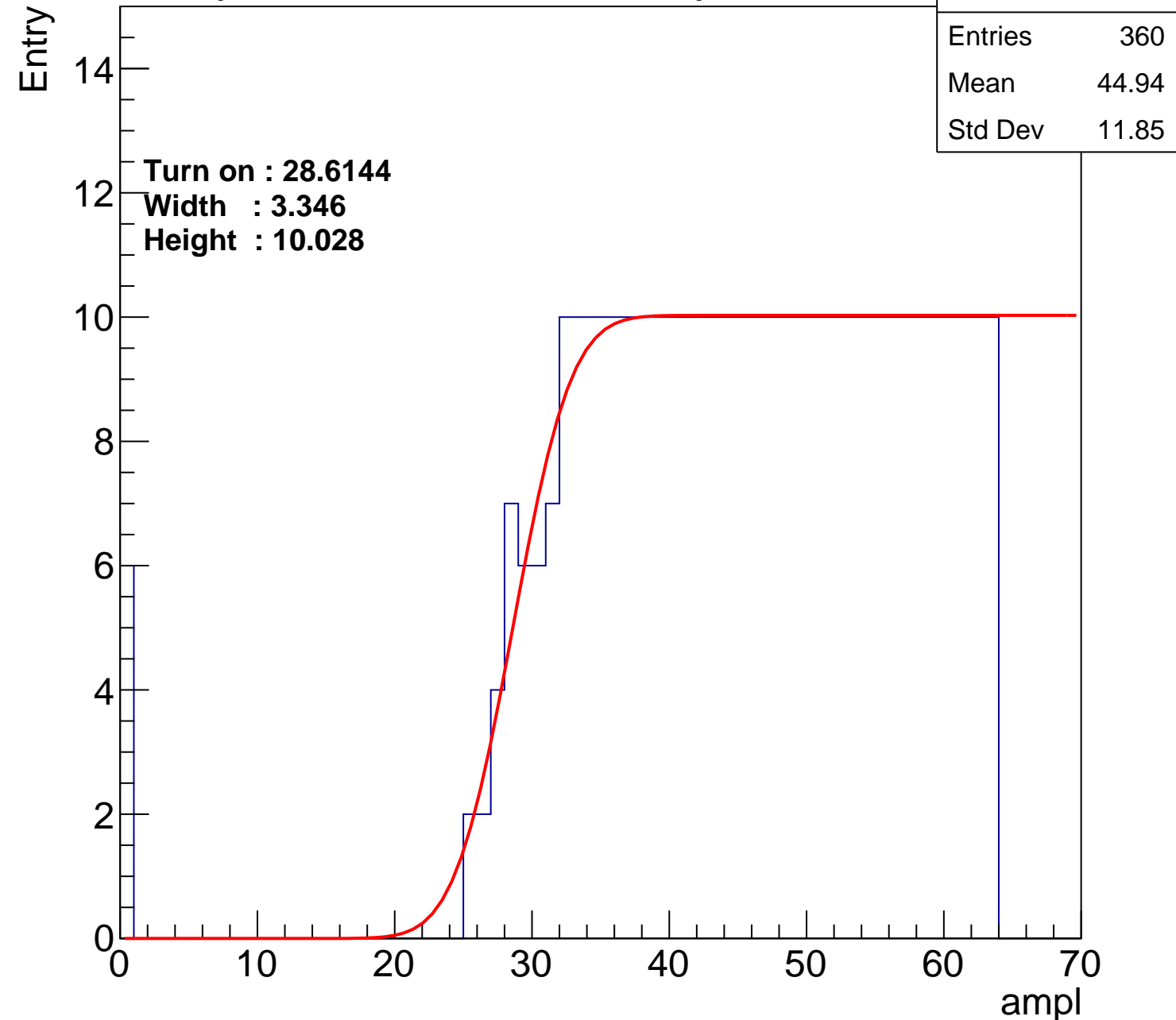
Width : 3.346

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.82
Std Dev	11.59

Turn on : 27.9147

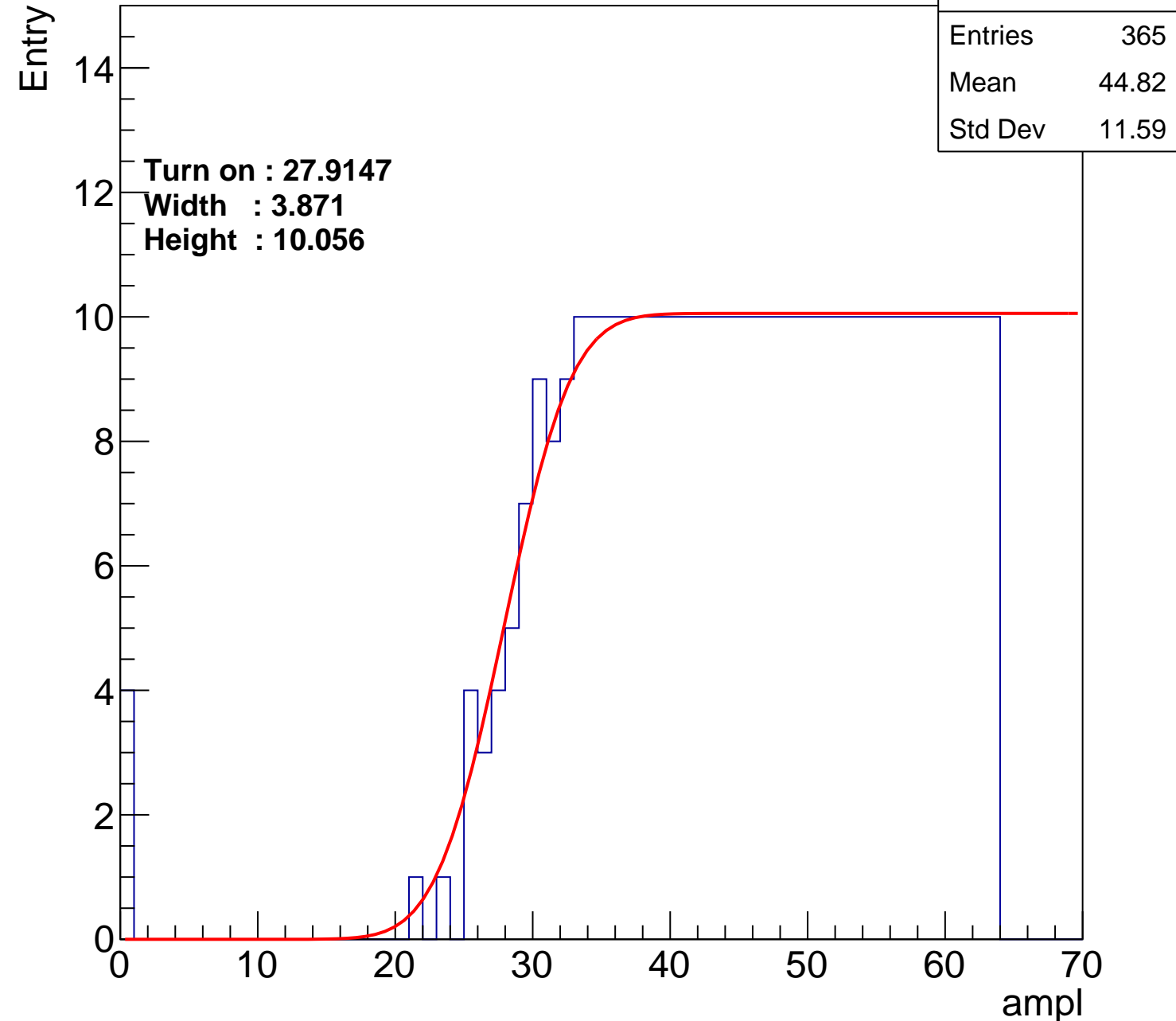
Width : 3.871

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

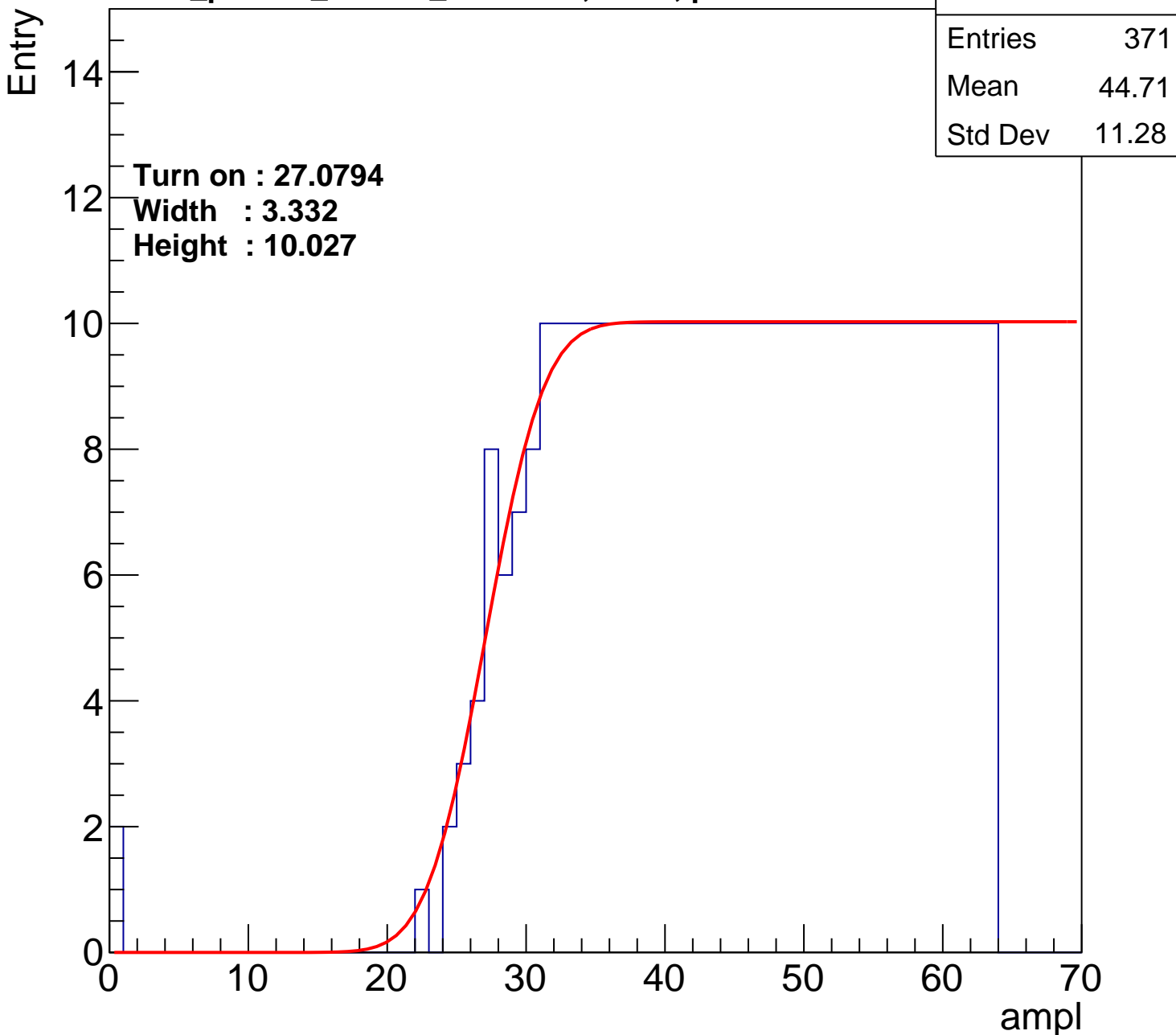


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.71
Std Dev	11.28

Height : 10.027



B0L002S, U3-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.22
Std Dev	12.14

Turn on : 27.6448

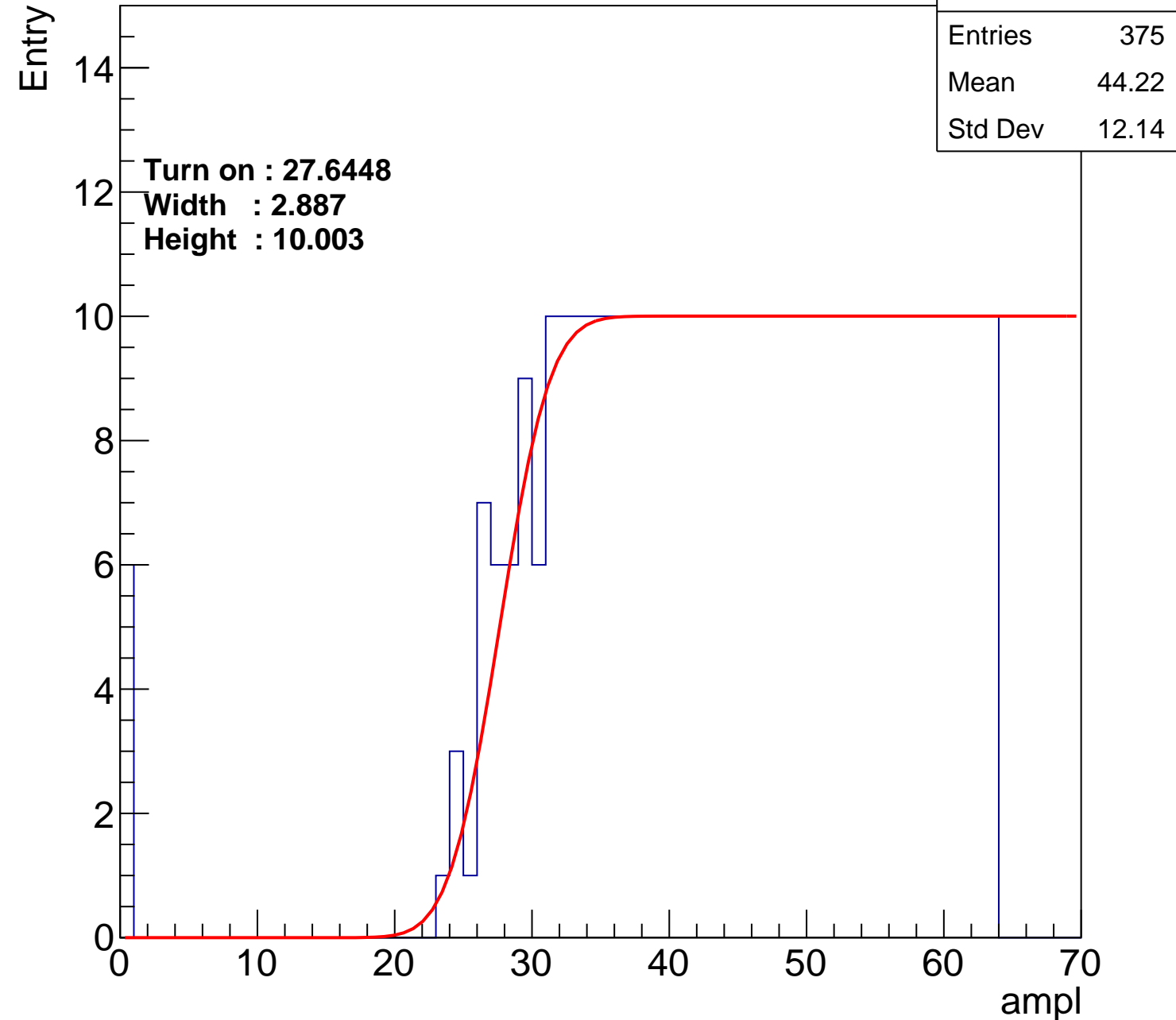
Width : 2.887

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch112

calib_packv5_042523_0143.root, FC#8, port C1

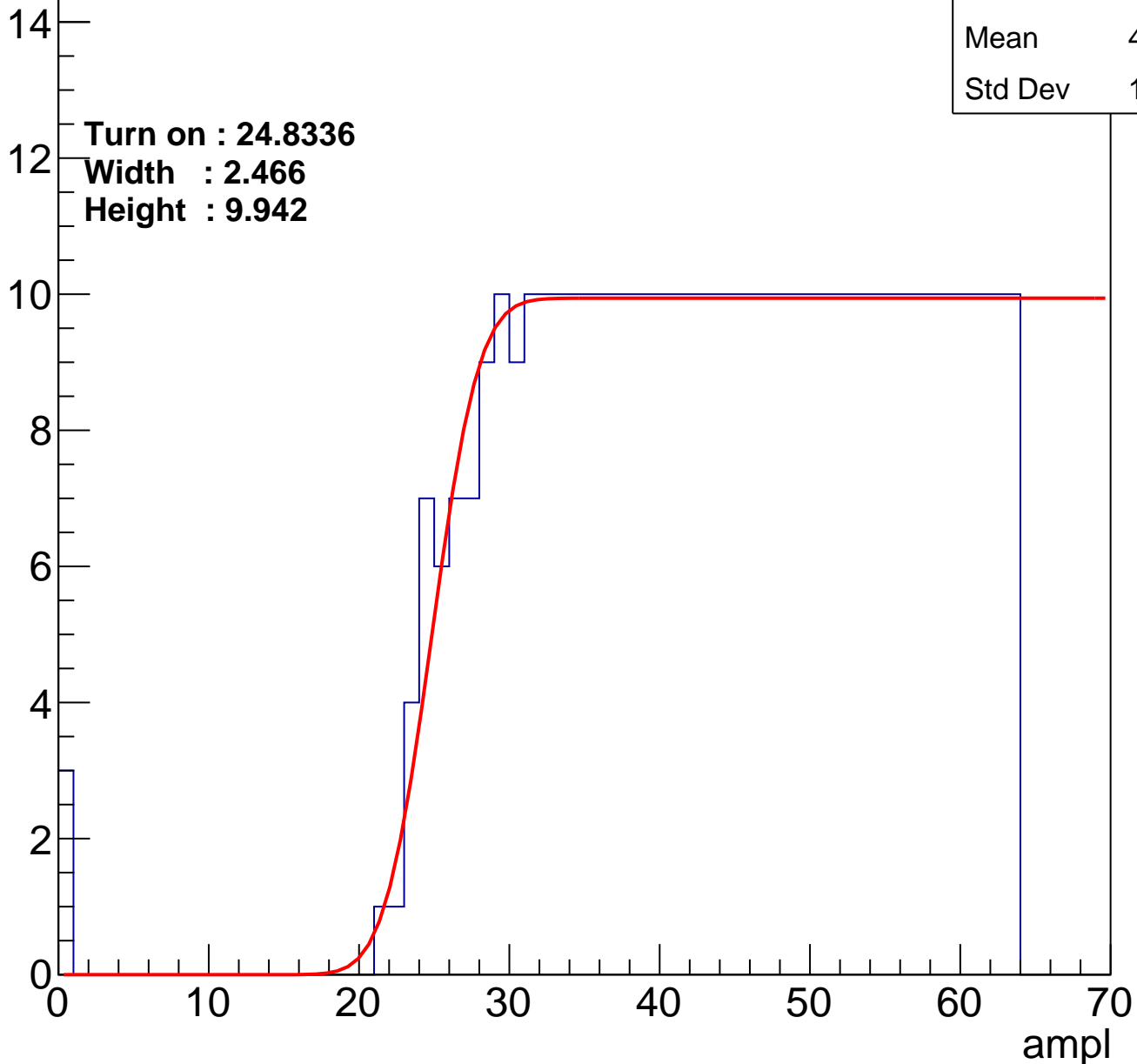
Entries	394
Mean	43.52
Std Dev	12.02

Turn on : 24.8336

Width : 2.466

Height : 9.942

Entry



B0L002S, U3-ch113

calib_packv5_042523_0143.root, FC#8, port C1

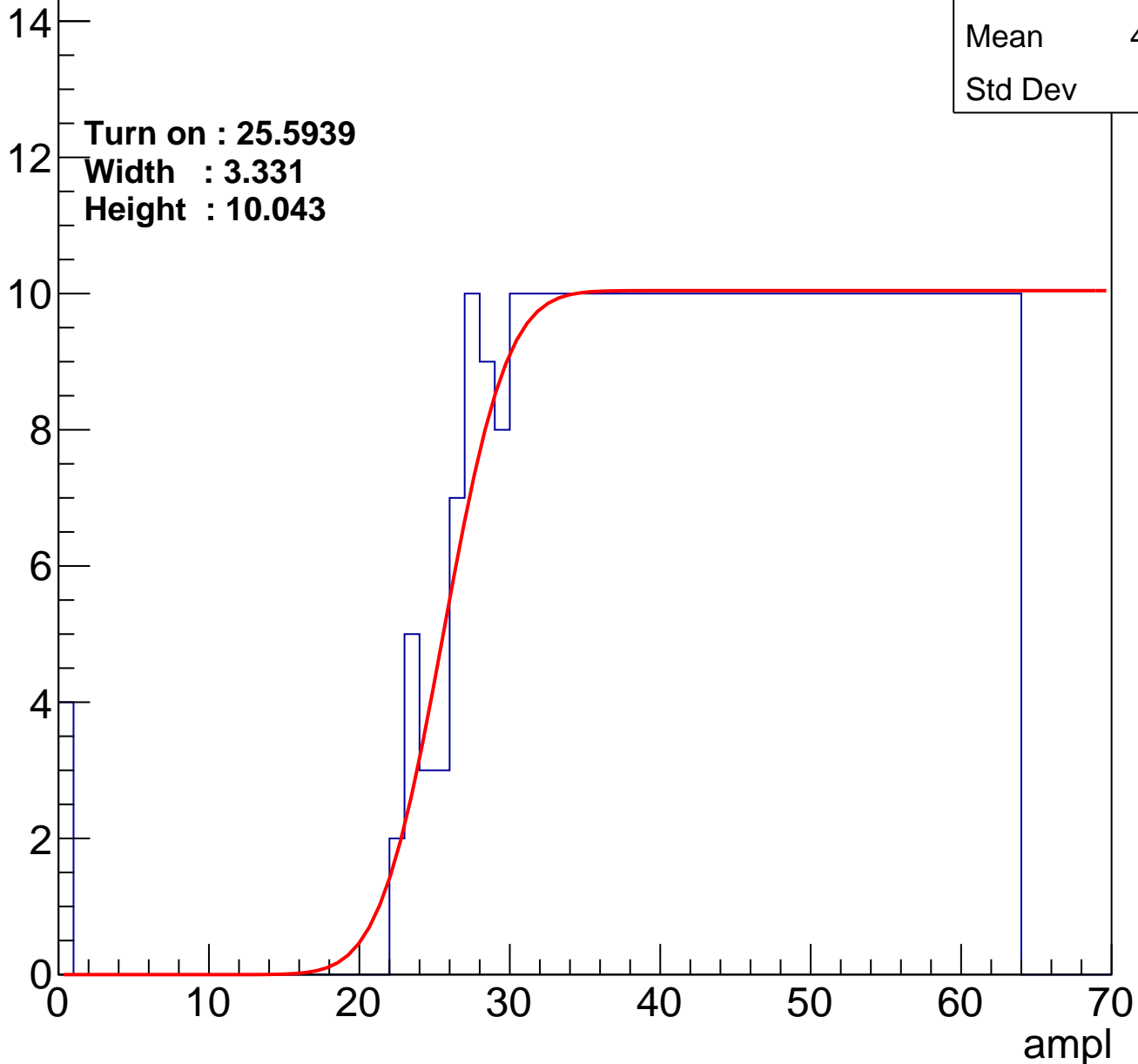
Entries	391
Mean	43.61
Std Dev	12.1

Turn on : 25.5939

Width : 3.331

Height : 10.043

Entry



B0L002S, U3-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.4
Std Dev	11.77

Turn on : 27.4425

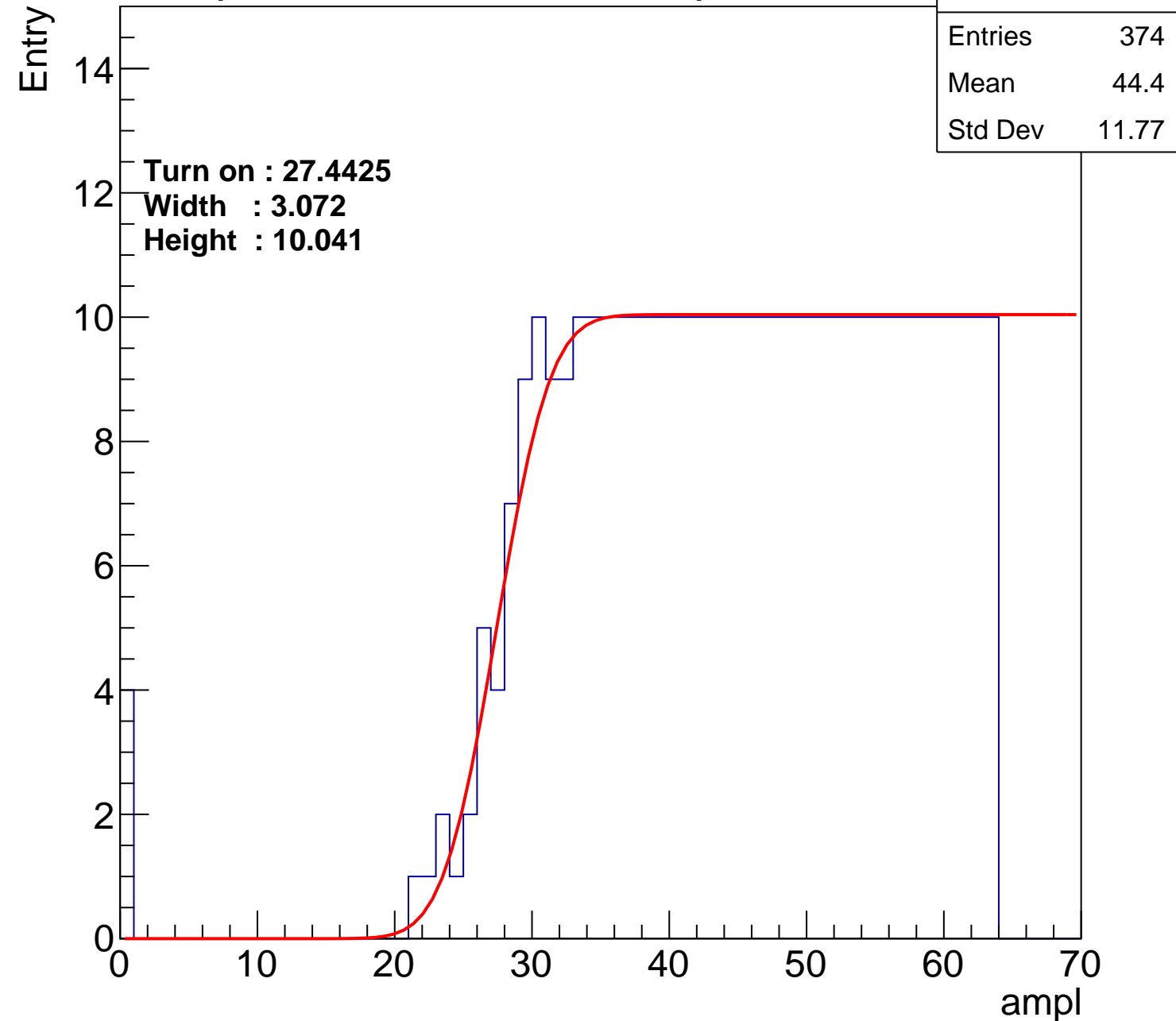
Width : 3.072

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch115

calib_packv5_042523_0143.root, FC#8, port C1

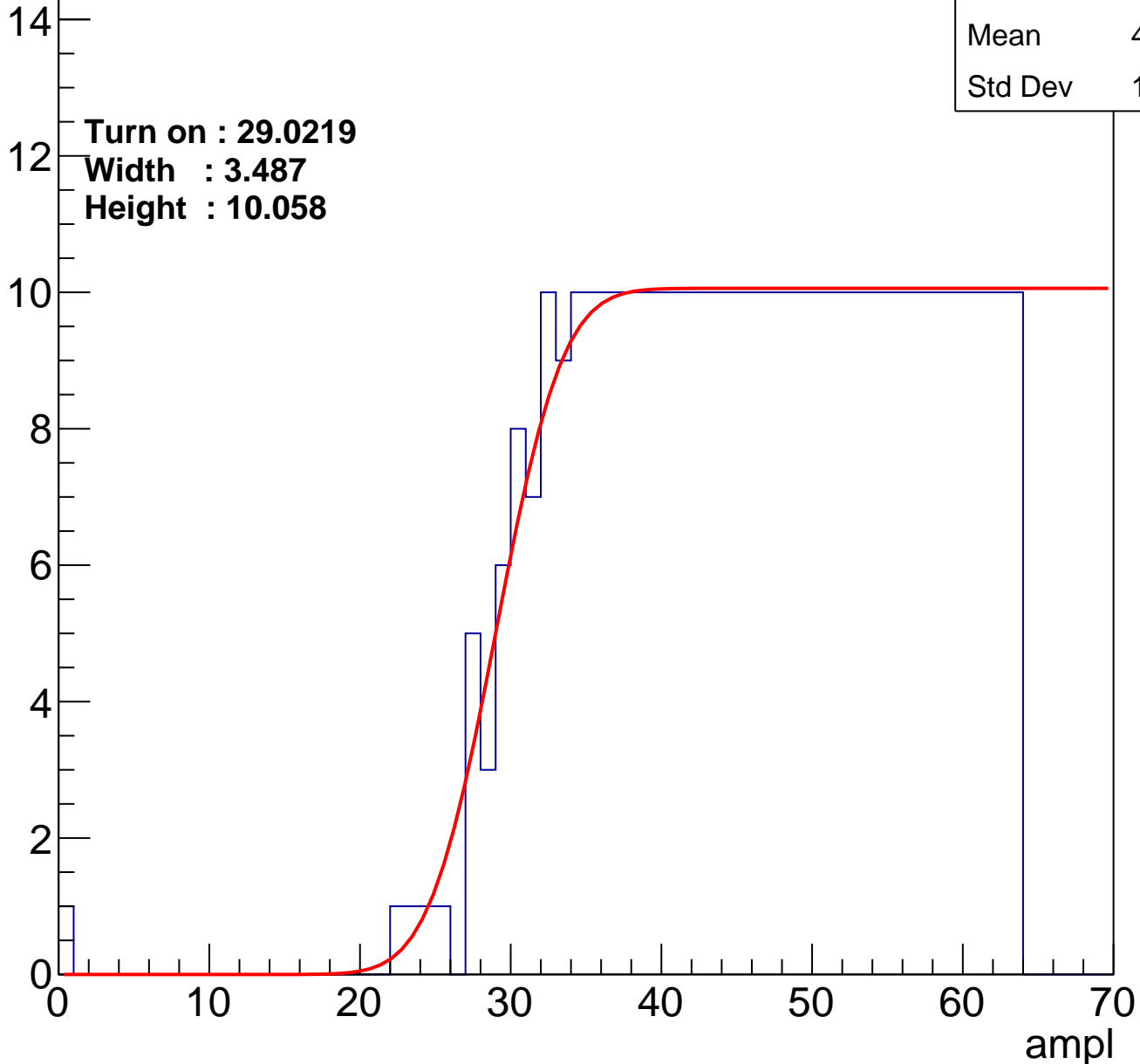
Entries	353
Mean	45.64
Std Dev	10.65

Turn on : 29.0219

Width : 3.487

Height : 10.058

Entry



B0L002S, U3-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.76
Std Dev	11.43

Turn on : 28.0194

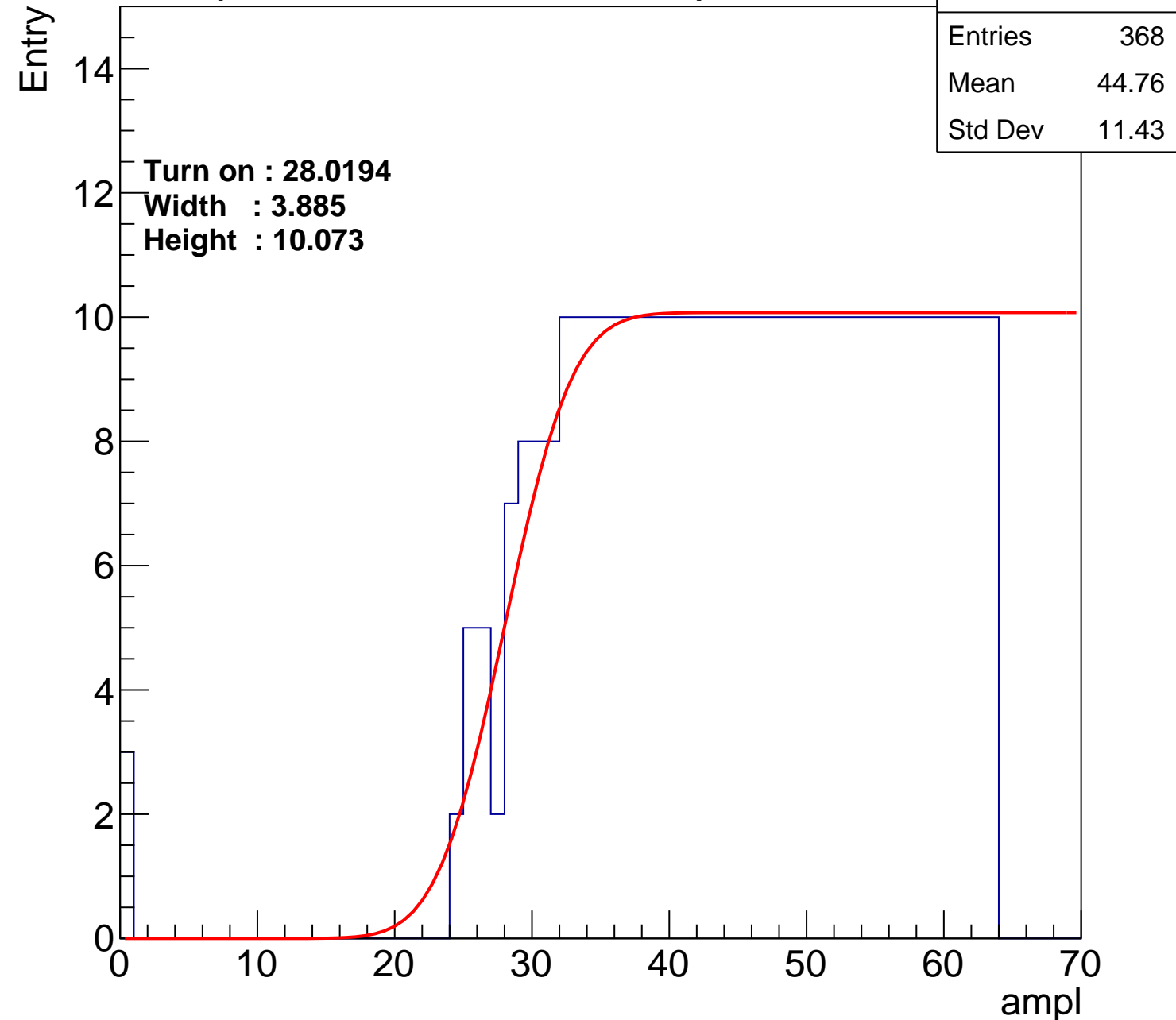
Width : 3.885

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 26.4776

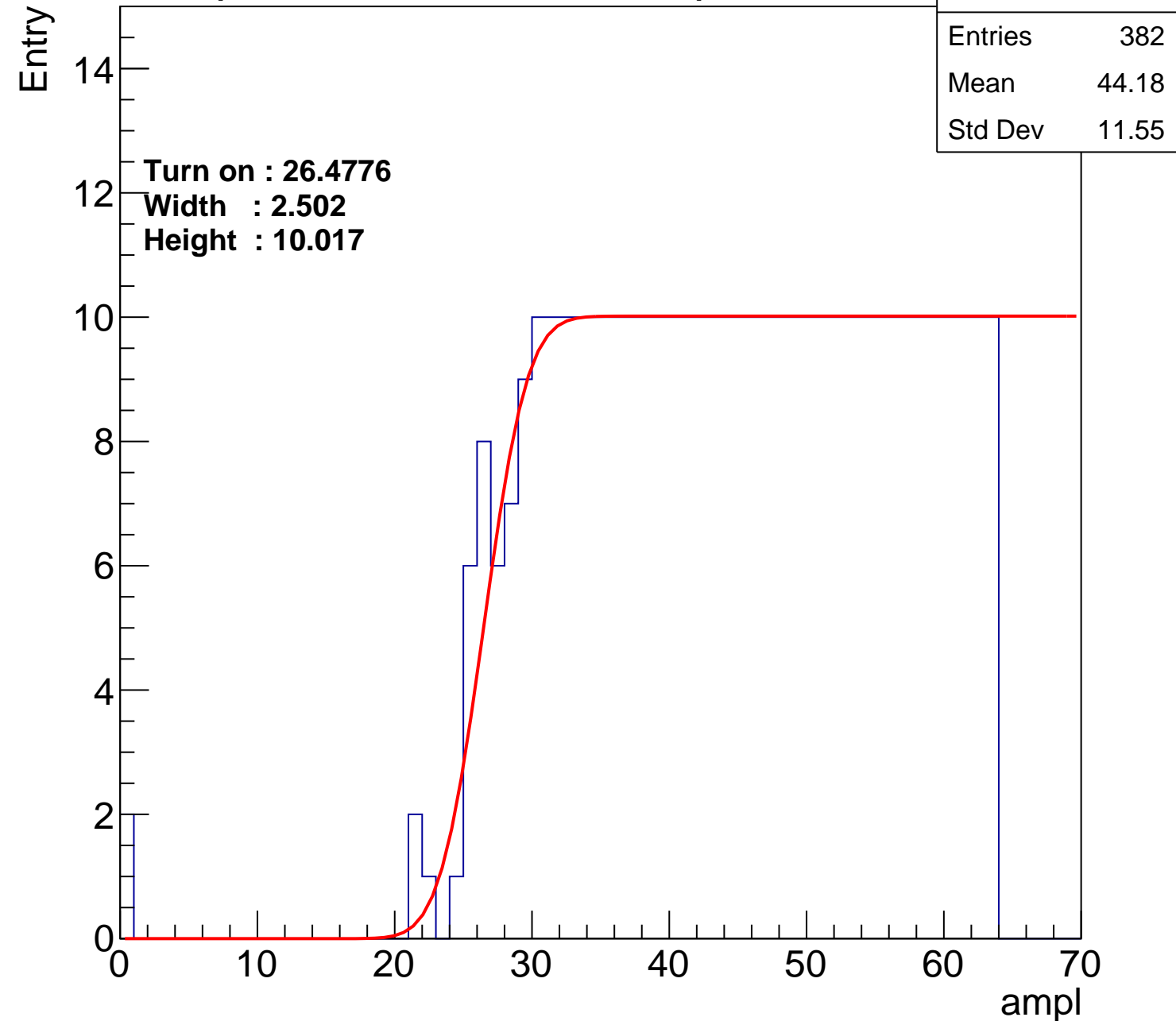
Width : 2.502

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 28.6079

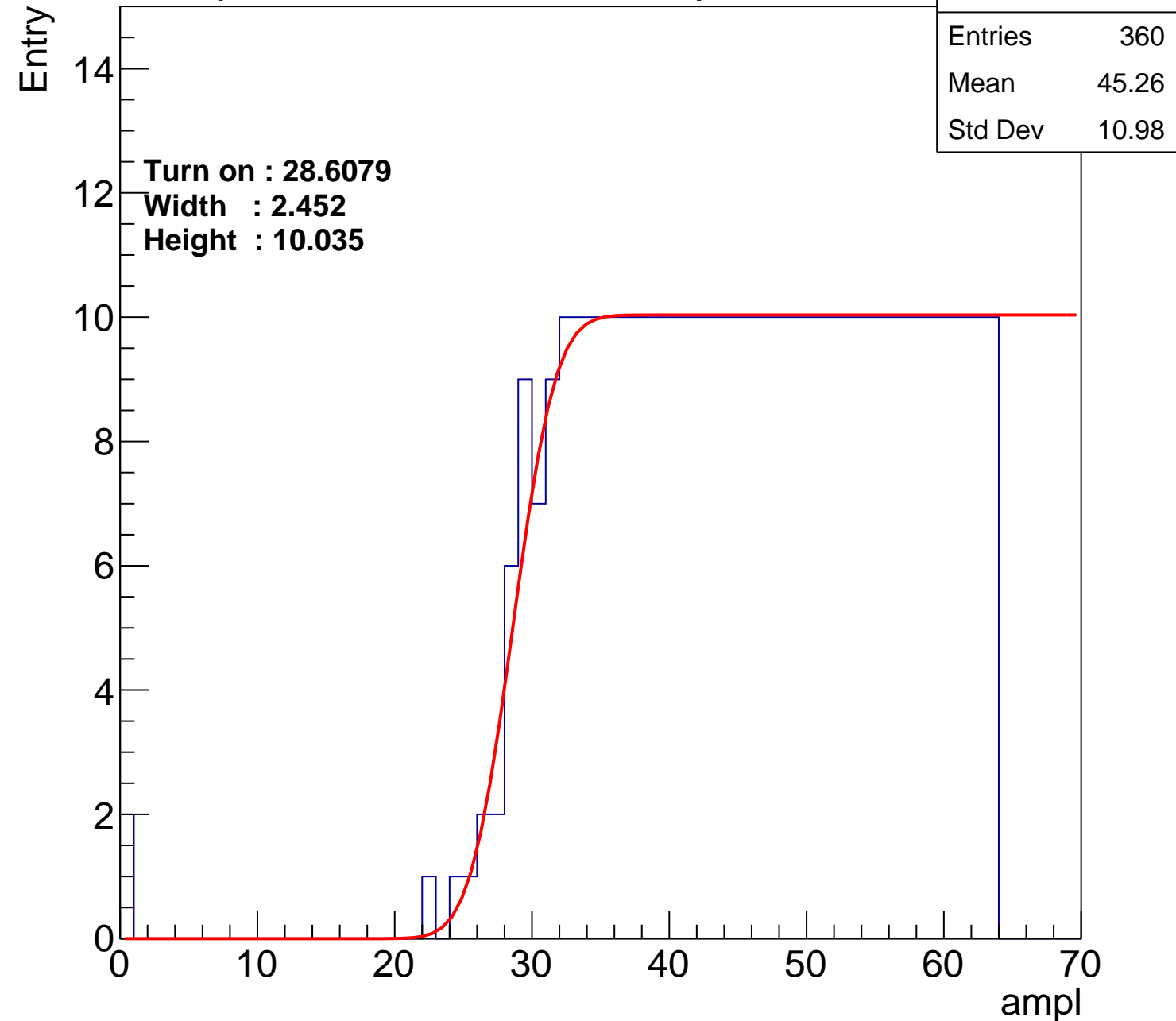
Width : 2.452

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.51
Std Dev	11.27

Turn on : 29.6442

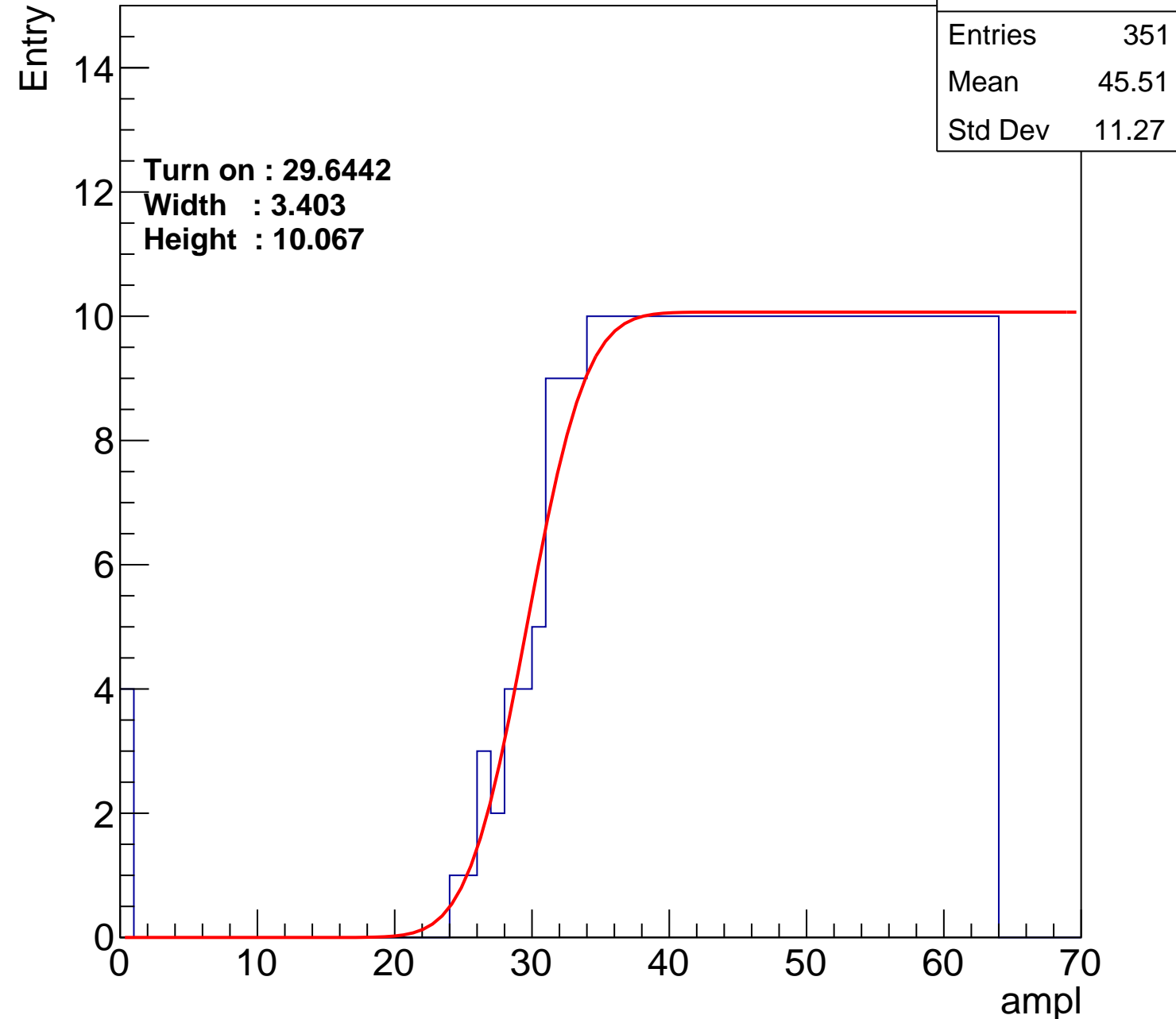
Width : 3.403

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.81
Std Dev	11.62

Turn on : 28.2207

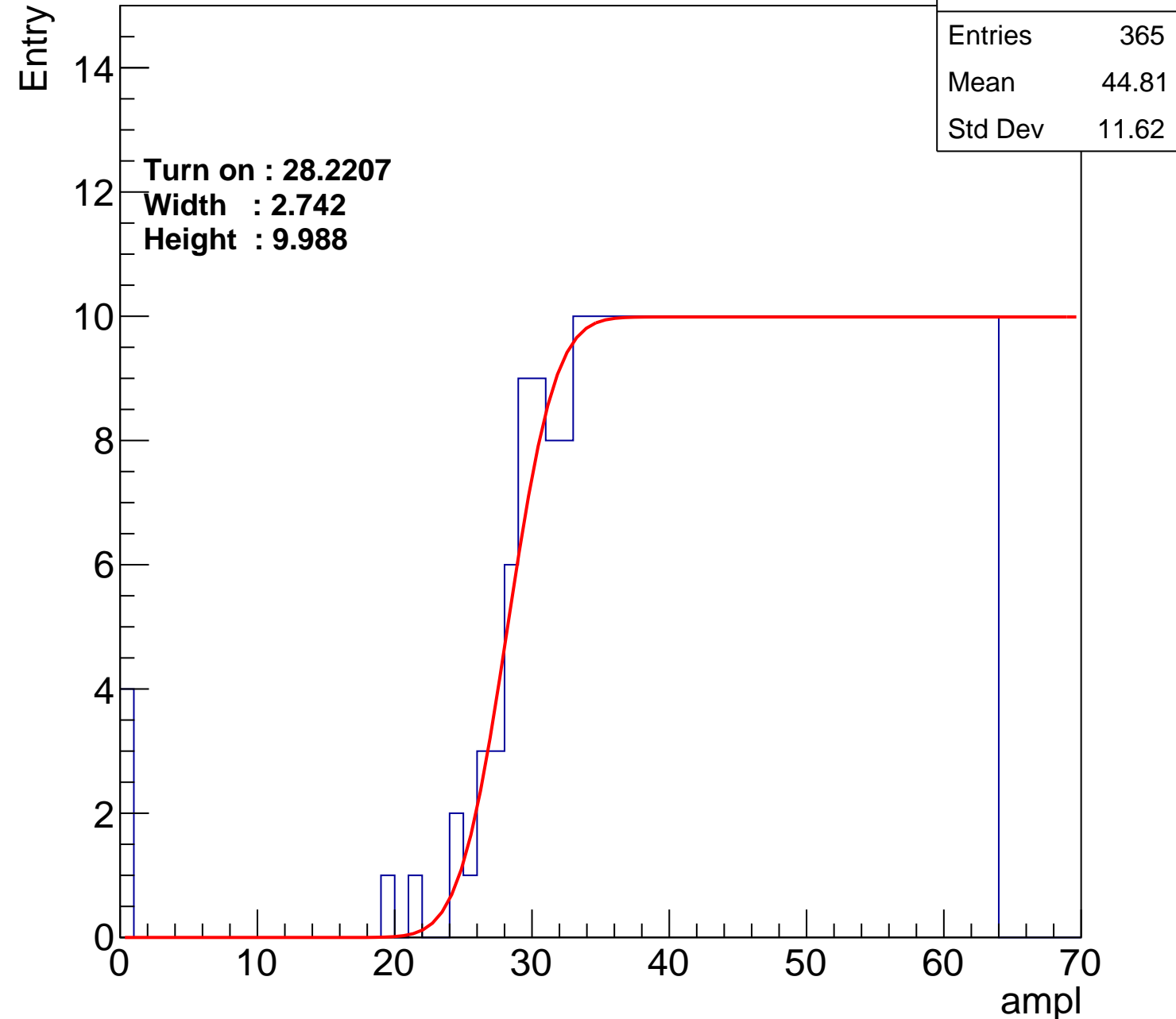
Width : 2.742

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 27.3688

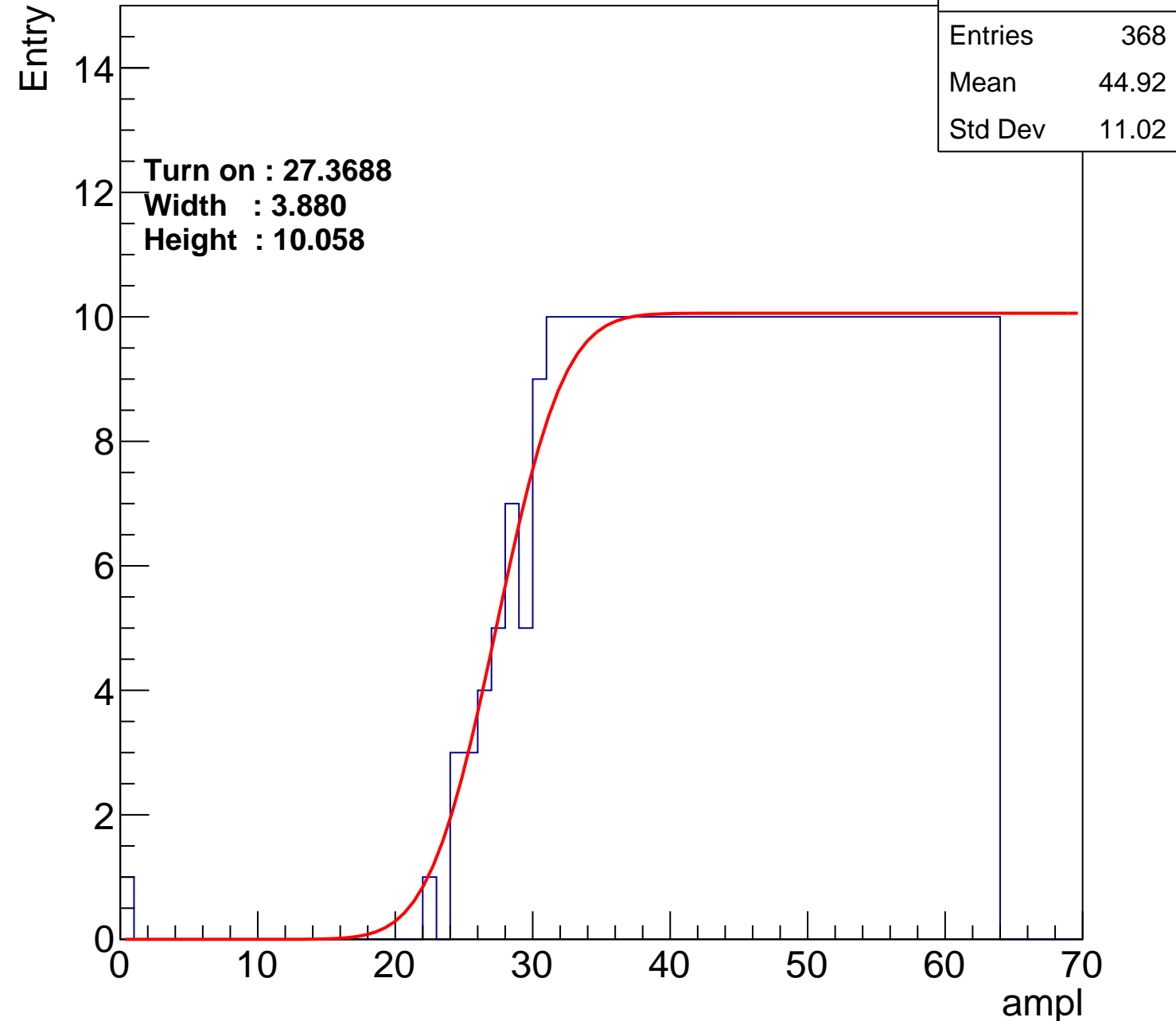
Width : 3.880

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch122

calib_packv5_042523_0143.root, FC#8, port C1

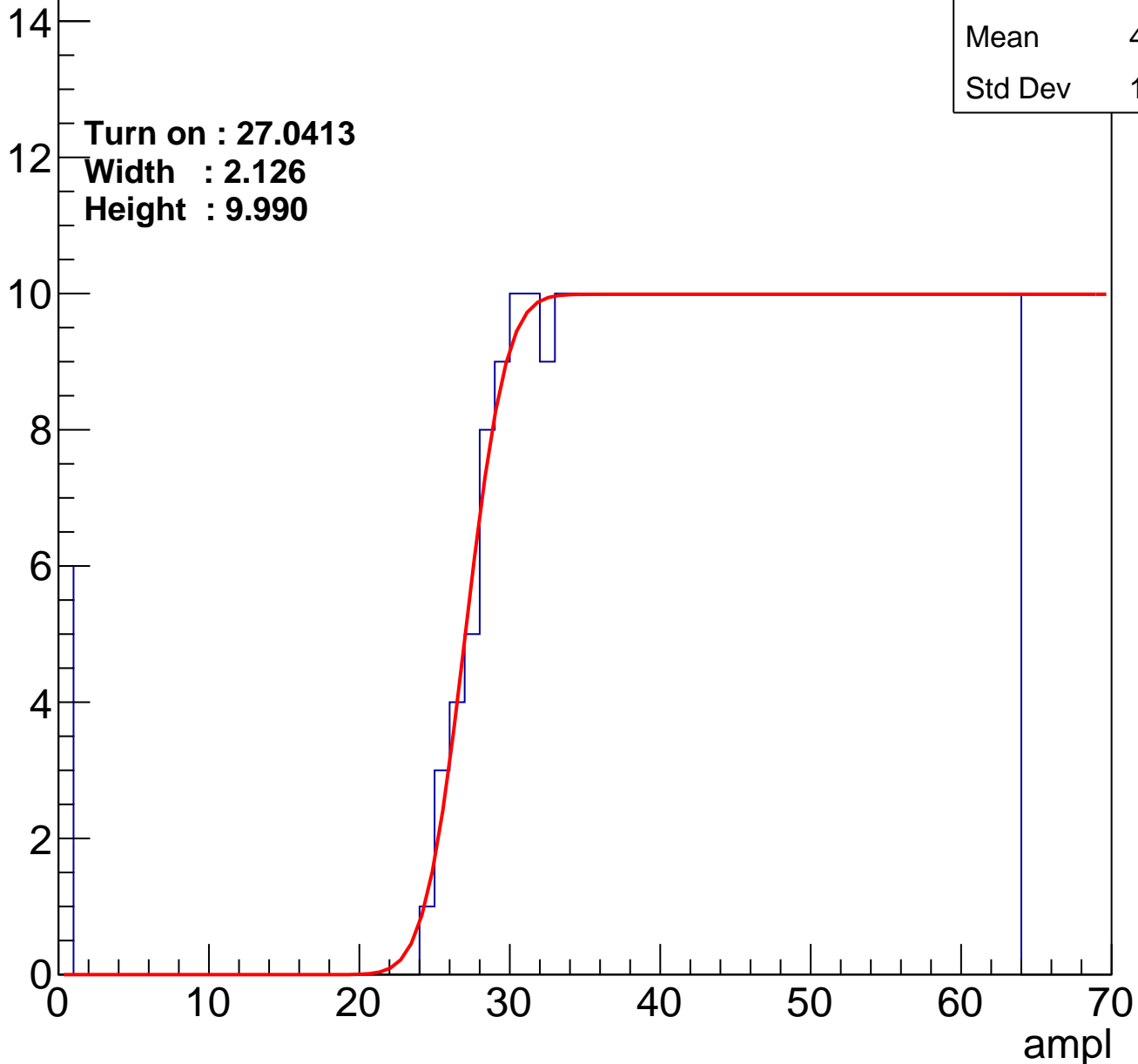
Entries	375
Mean	44.27
Std Dev	12.07

Turn on : 27.0413

Width : 2.126

Height : 9.990

Entry



B0L002S, U3-ch123

calib_packv5_042523_0143.root, FC#8, port C1

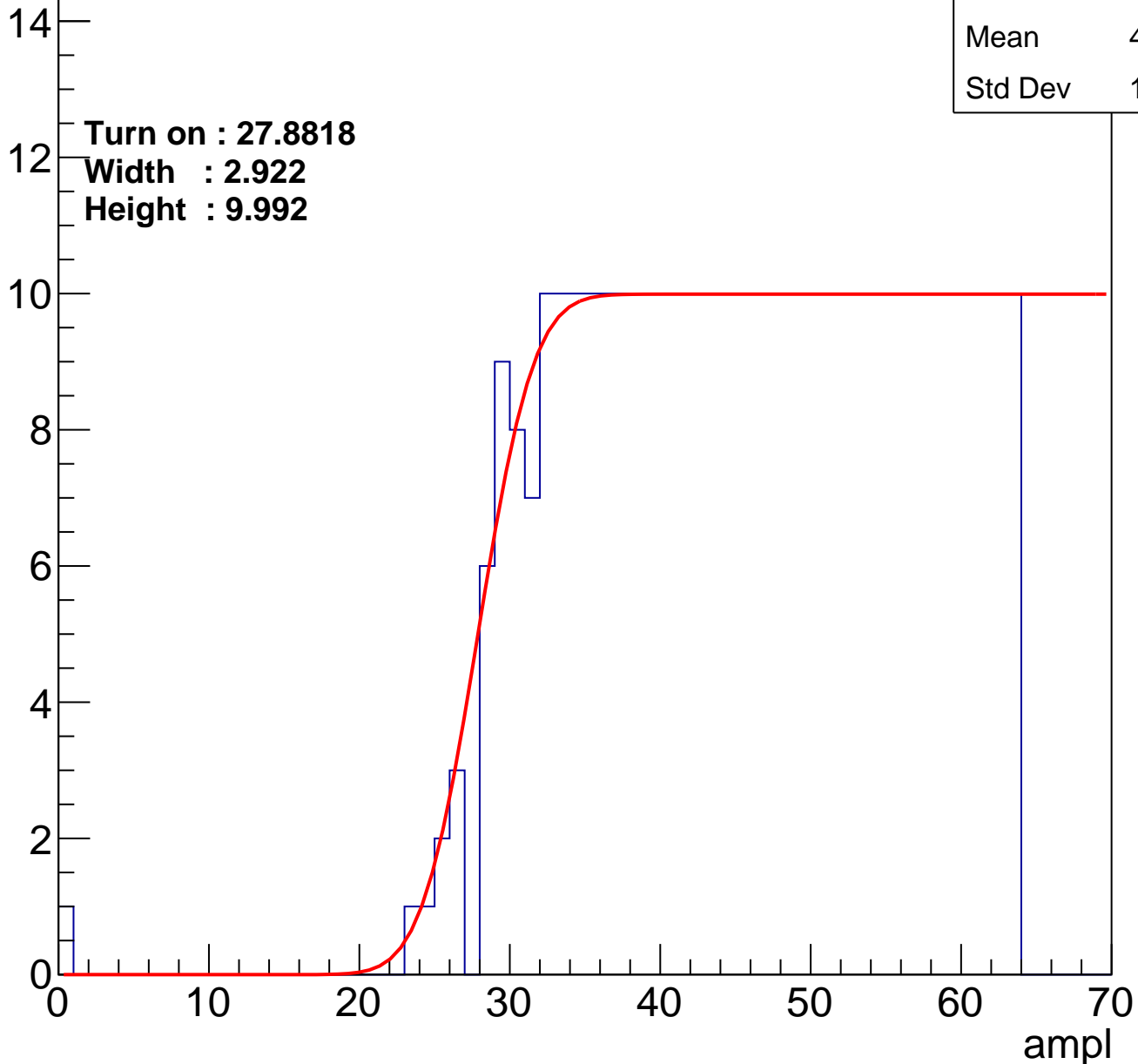
Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 27.8818

Width : 2.922

Height : 9.992

Entry



B0L002S, U3-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.03
Std Dev	11.93

Turn on : 26.3739

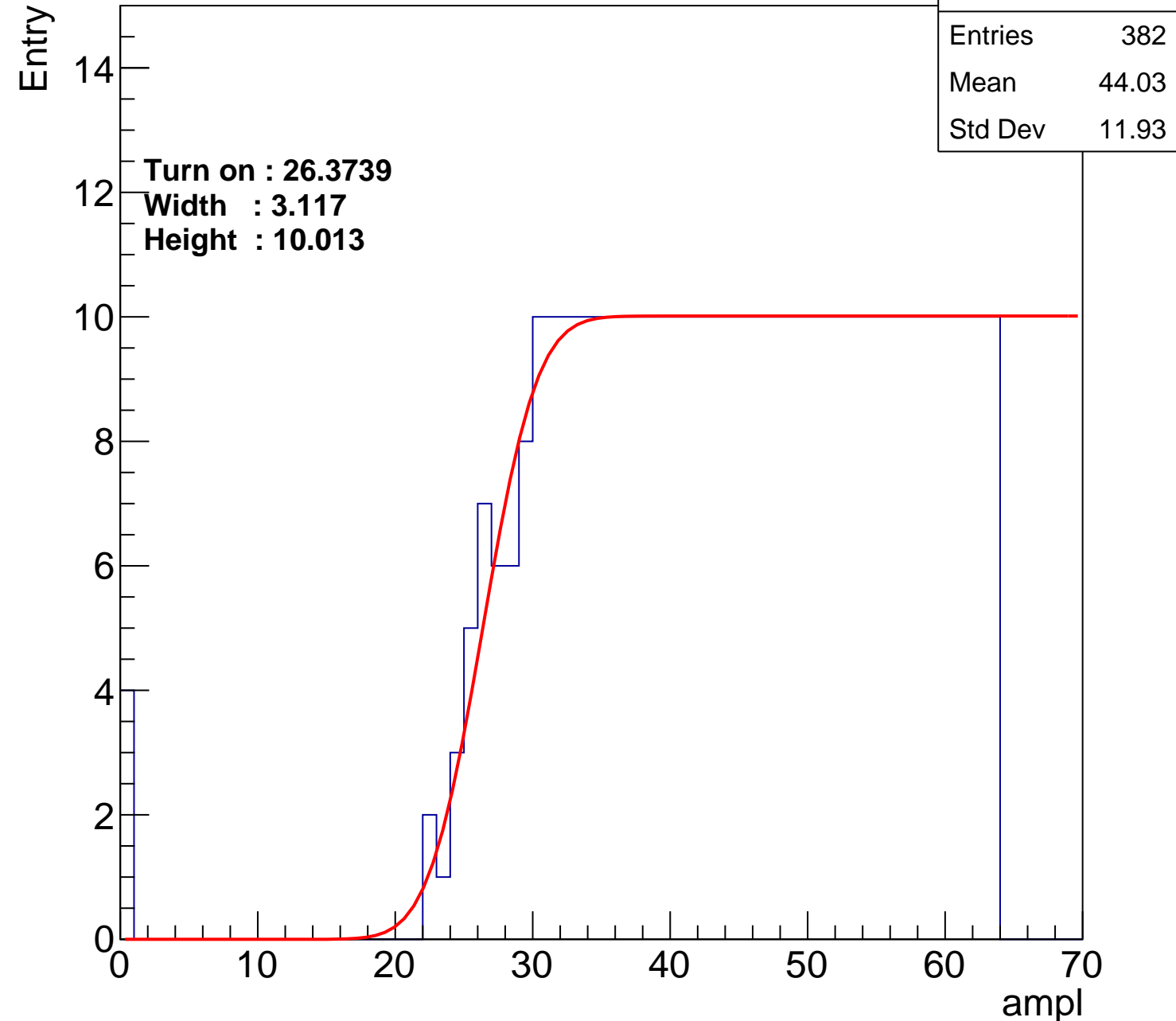
Width : 3.117

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.82
Std Dev	11.2

Turn on : 26.8825

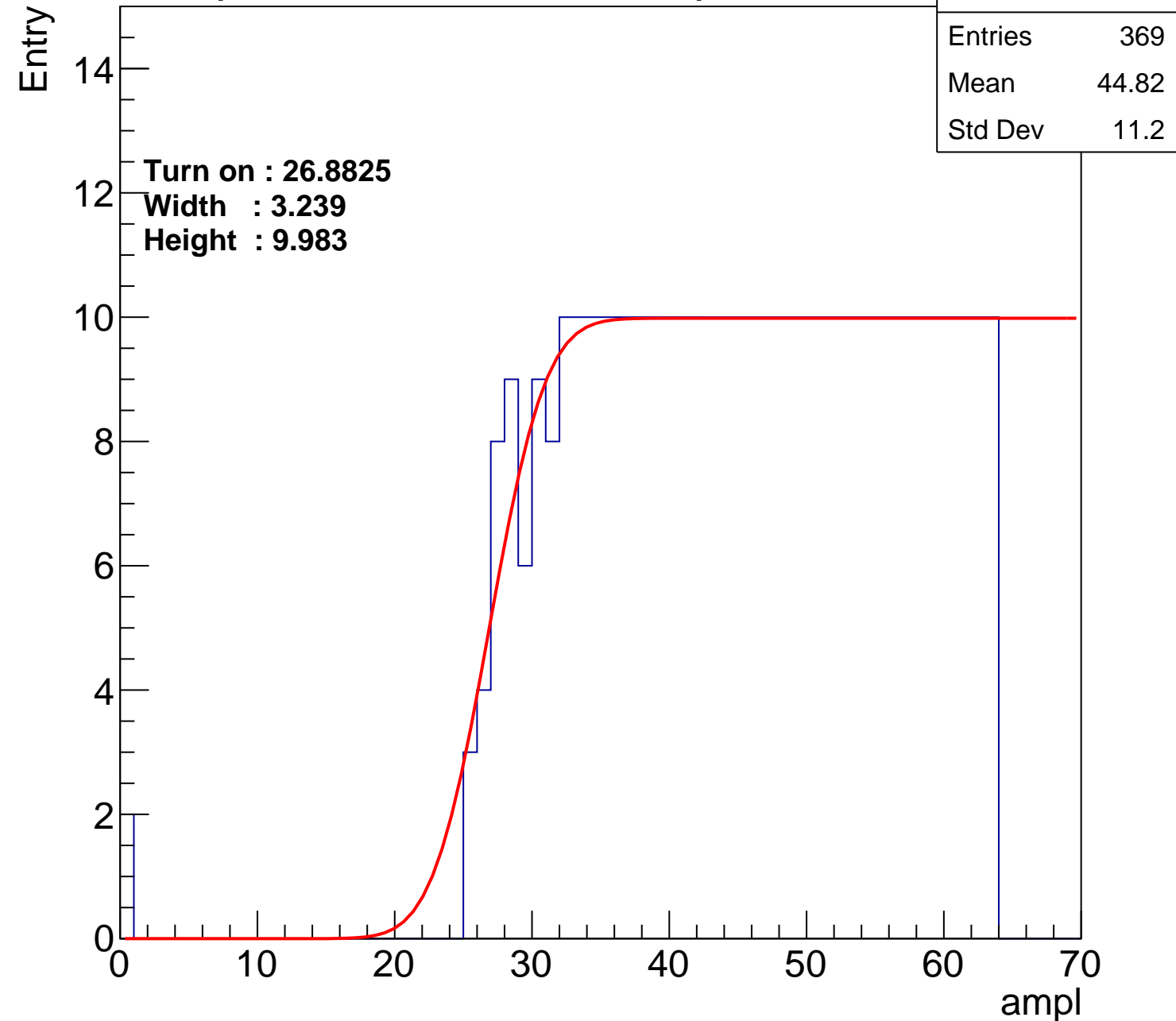
Width : 3.239

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.0457
Width : 4.009
Height : 10.073



B0L002S, U3-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.27
Std Dev	11.16

Turn on : 28.2750

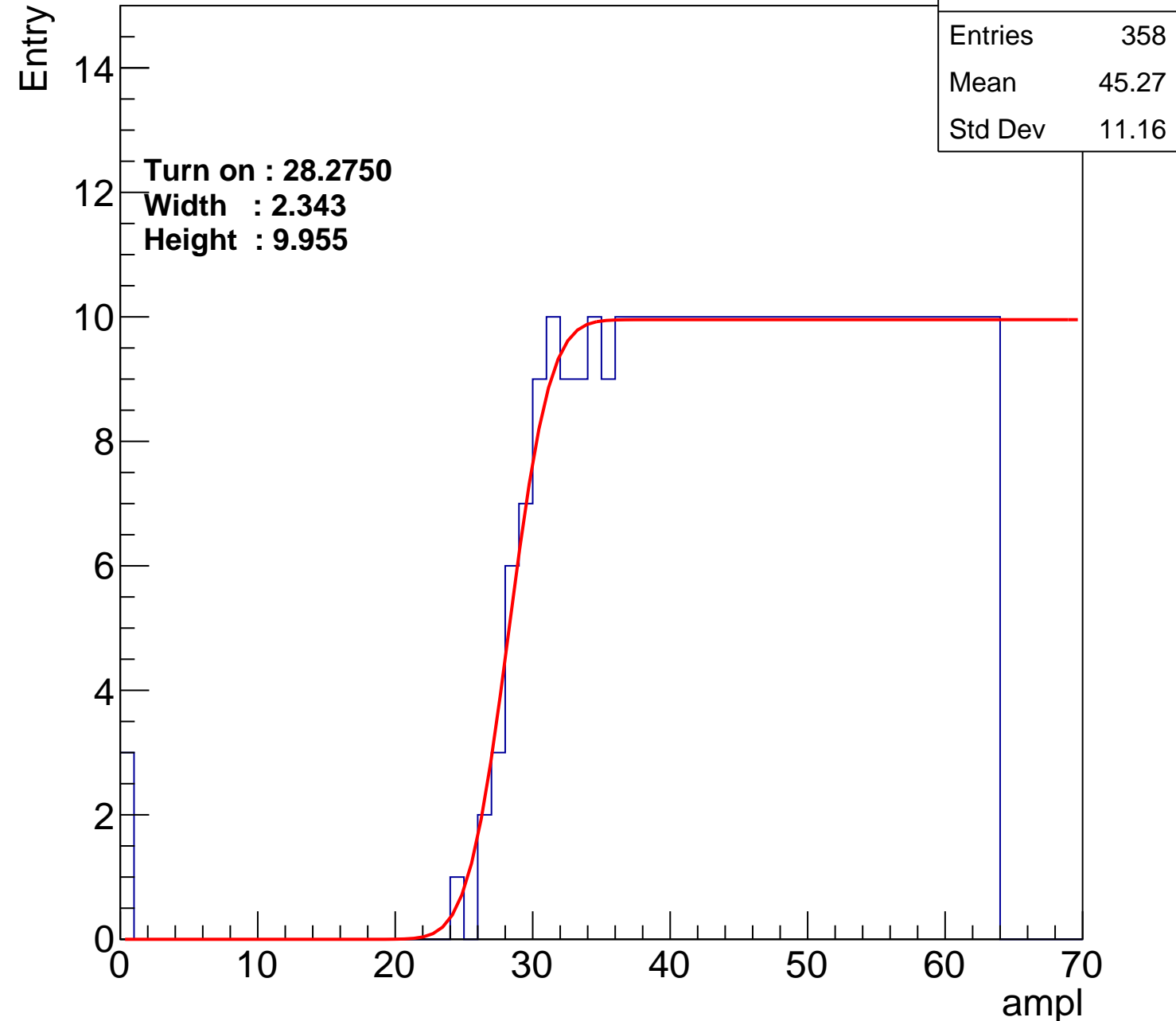
Width : 2.343

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U3-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.27
Std Dev	11.16

Turn on : 28.2750

Width : 2.343

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl

