

B1L103S, U15-ch0

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

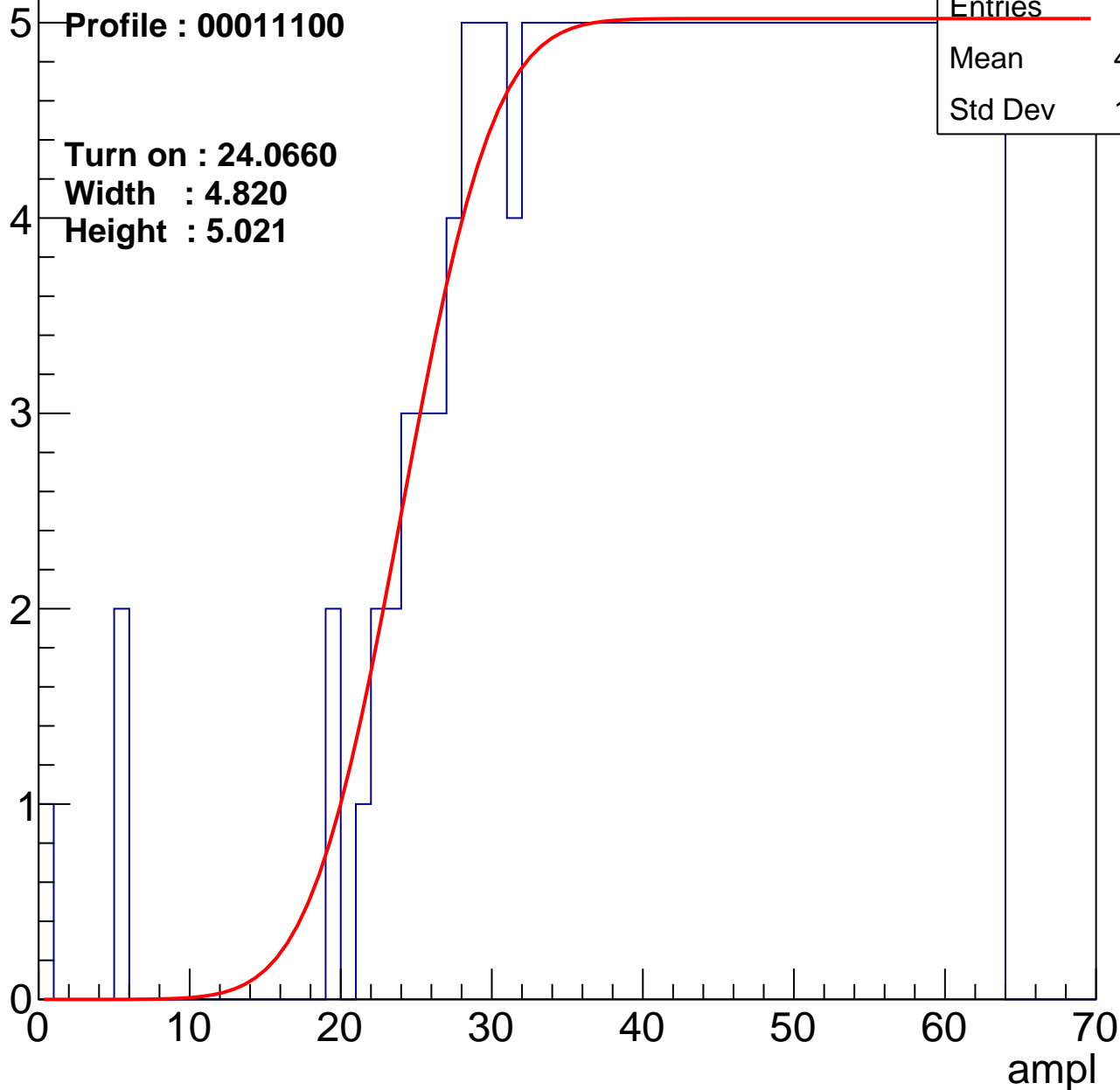
Profile : 00011100

Turn on : 24.0660

Width : 4.820

Height : 5.021

Entries	202
Mean	42.83
Std Dev	12.66



B1L103S, U15-ch1

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.5297

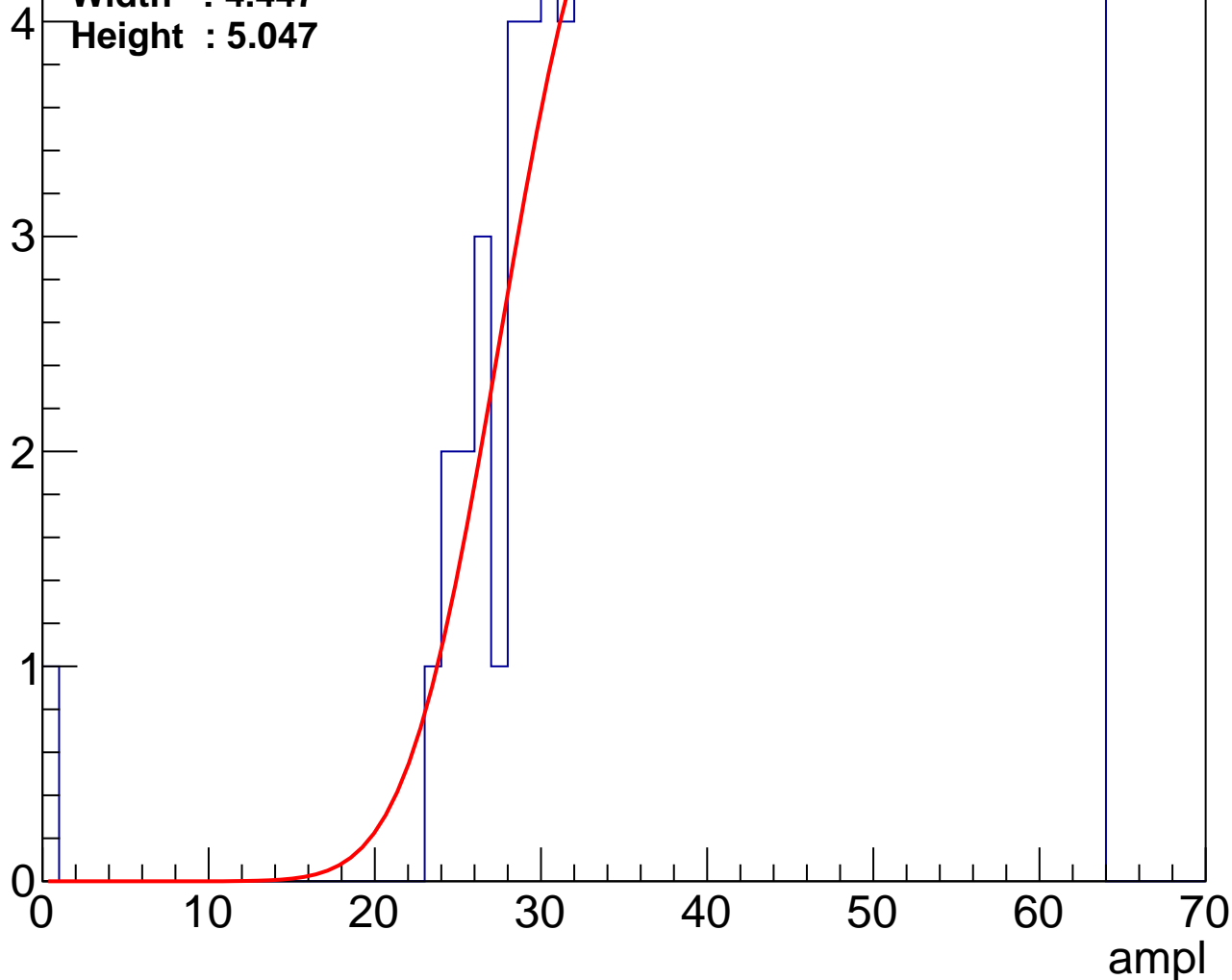
Width : 4.447

Height : 5.047

Entries 187

Mean 44.53

Std Dev 11.4



B1L103S, U15-ch2

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.5970

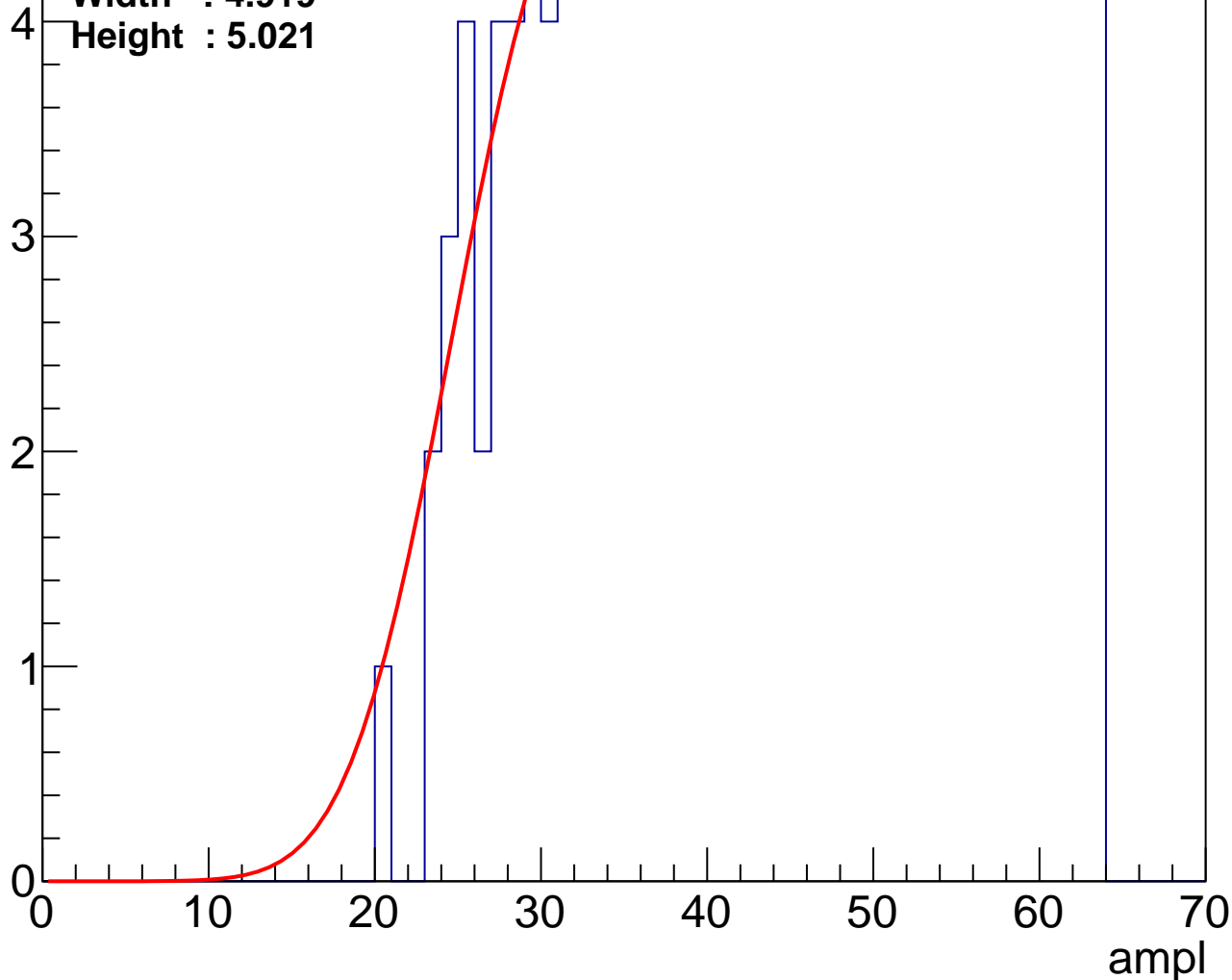
Width : 4.919

Height : 5.021

Entries 194

Mean 43.97

Std Dev 11.42



B1L103S, U15-ch3

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.6545

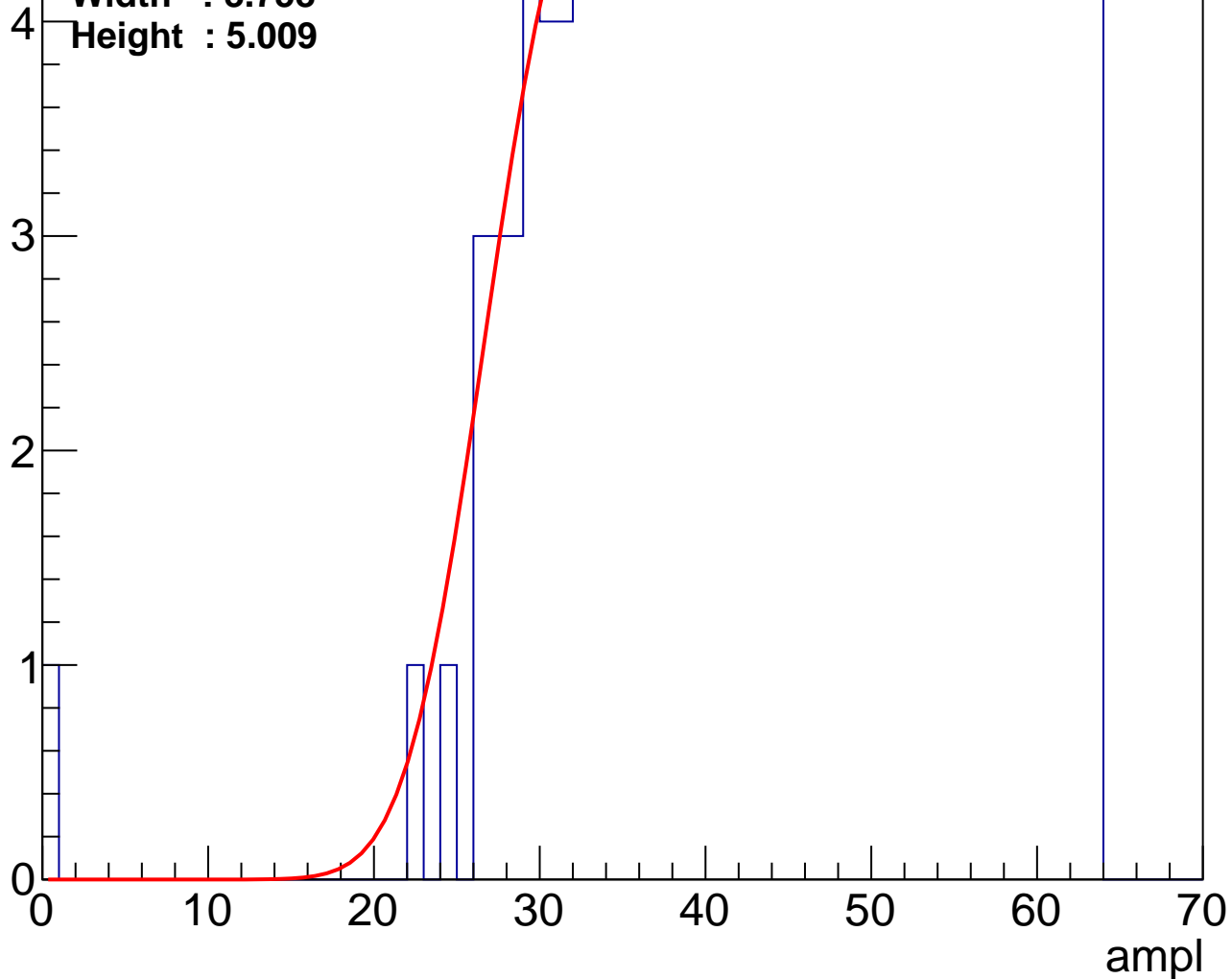
Width : 3.758

Height : 5.009

Entries 185

Mean 44.75

Std Dev 11.27



B1L103S, U15-ch4

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.5735

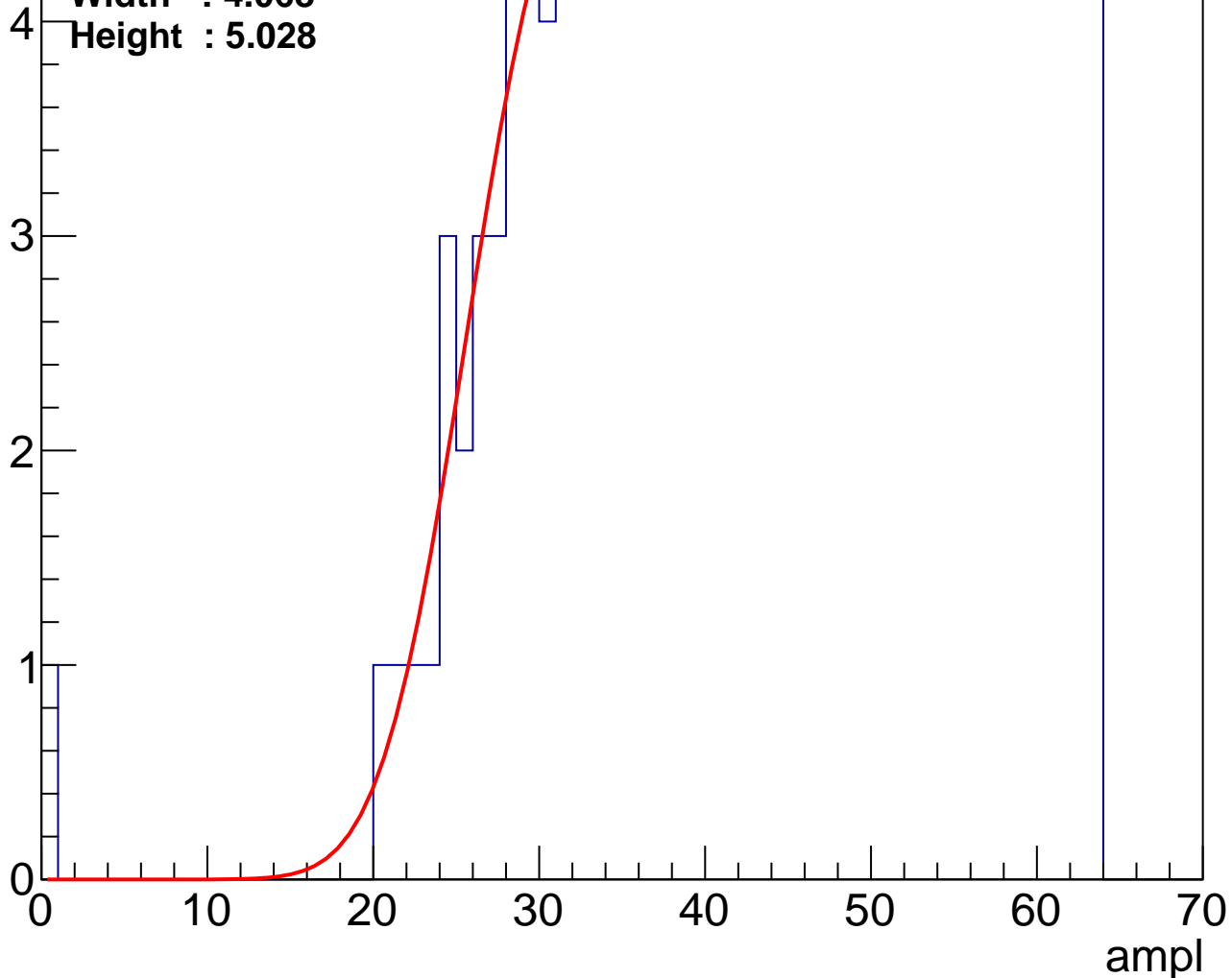
Width : 4.068

Height : 5.028

Entries 195

Mean 43.73

Std Dev 11.84



B1L103S, U15-ch5

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.8168

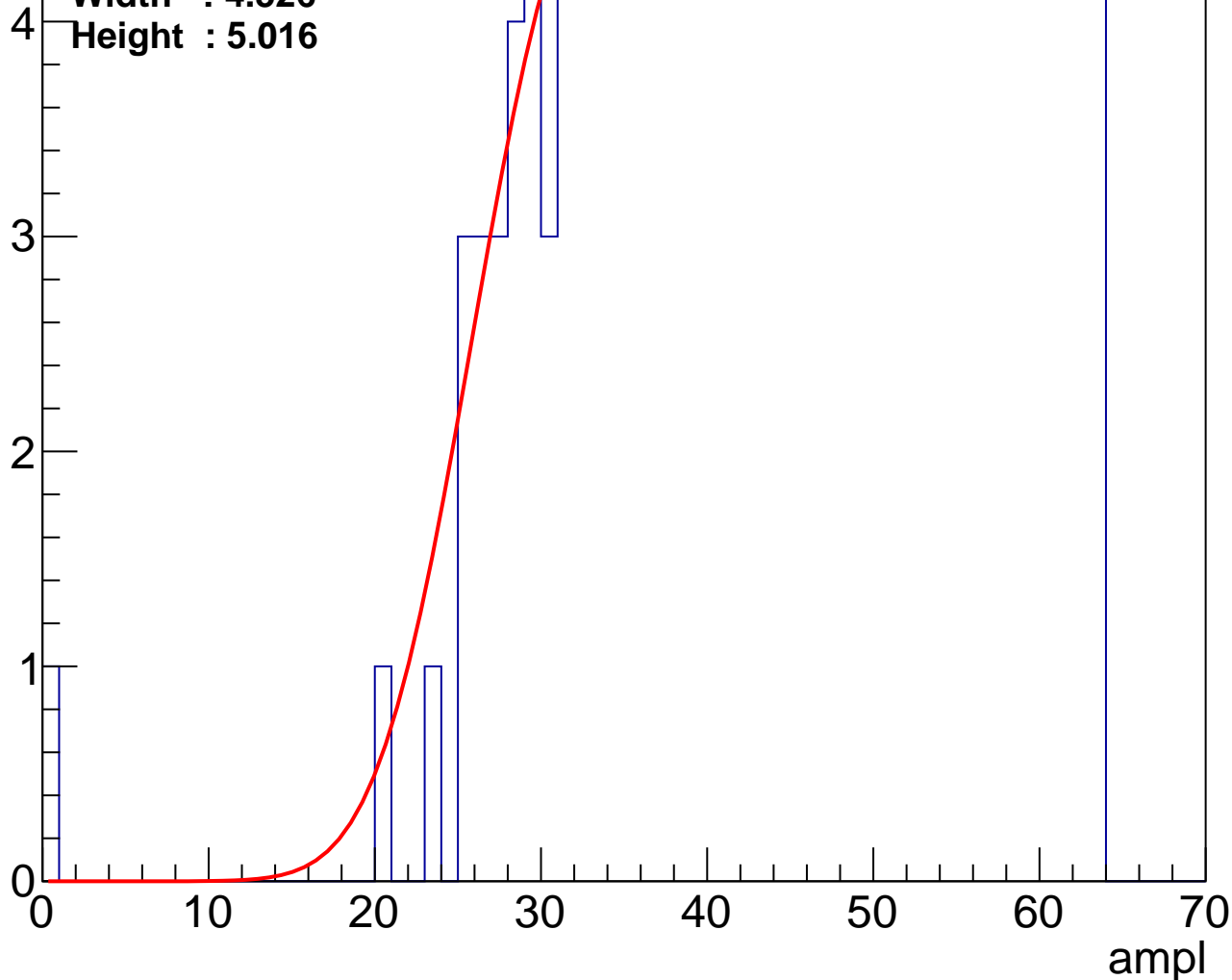
Width : 4.526

Height : 5.016

Entries 189

Mean 44.33

Std Dev 11.51



B1L103S, U15-ch6

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.7149

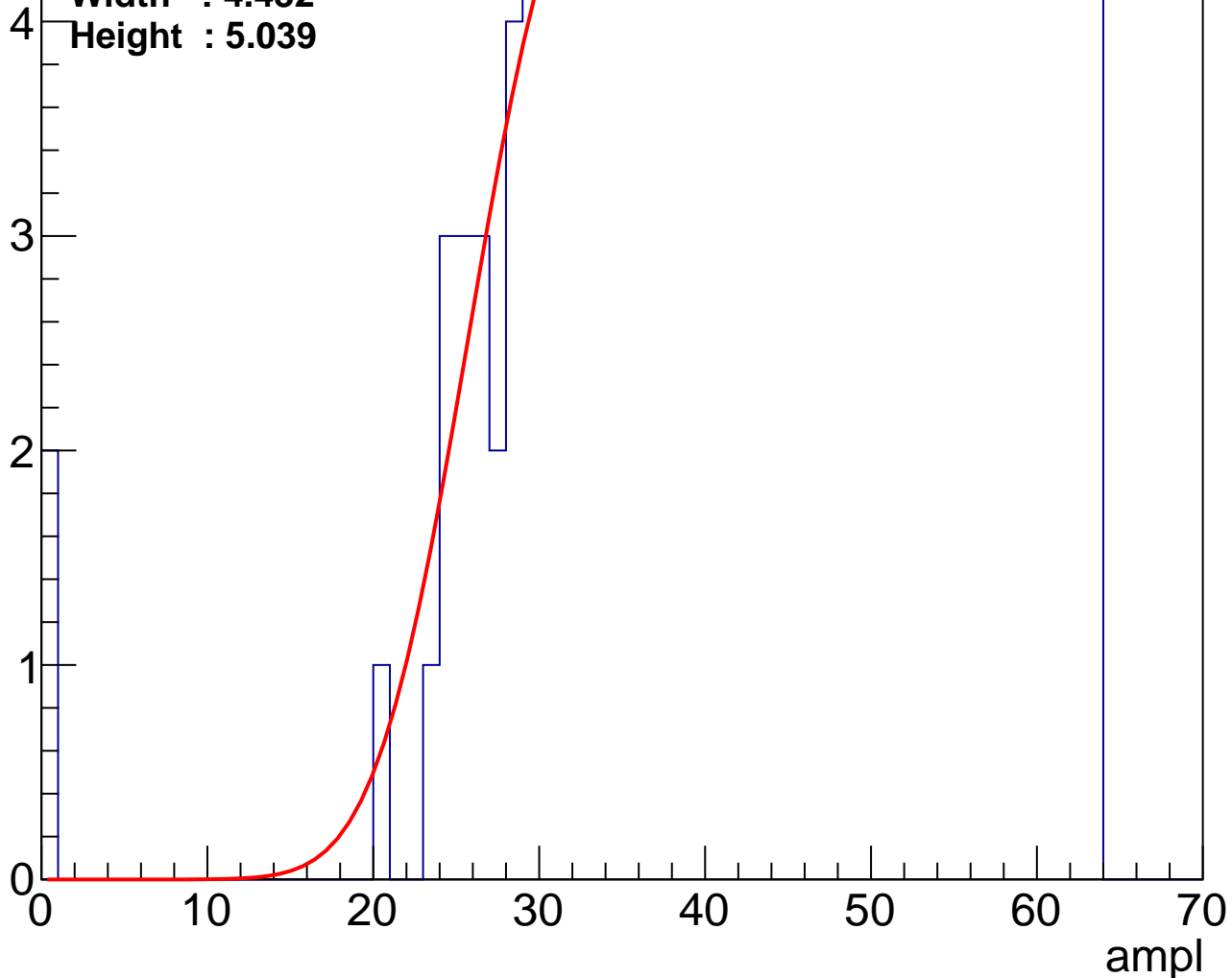
Width : 4.432

Height : 5.039

Entries	194
---------	-----

Mean	43.73
------	-------

Std Dev	12.07
---------	-------



B1L103S, U15-ch7

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.3234

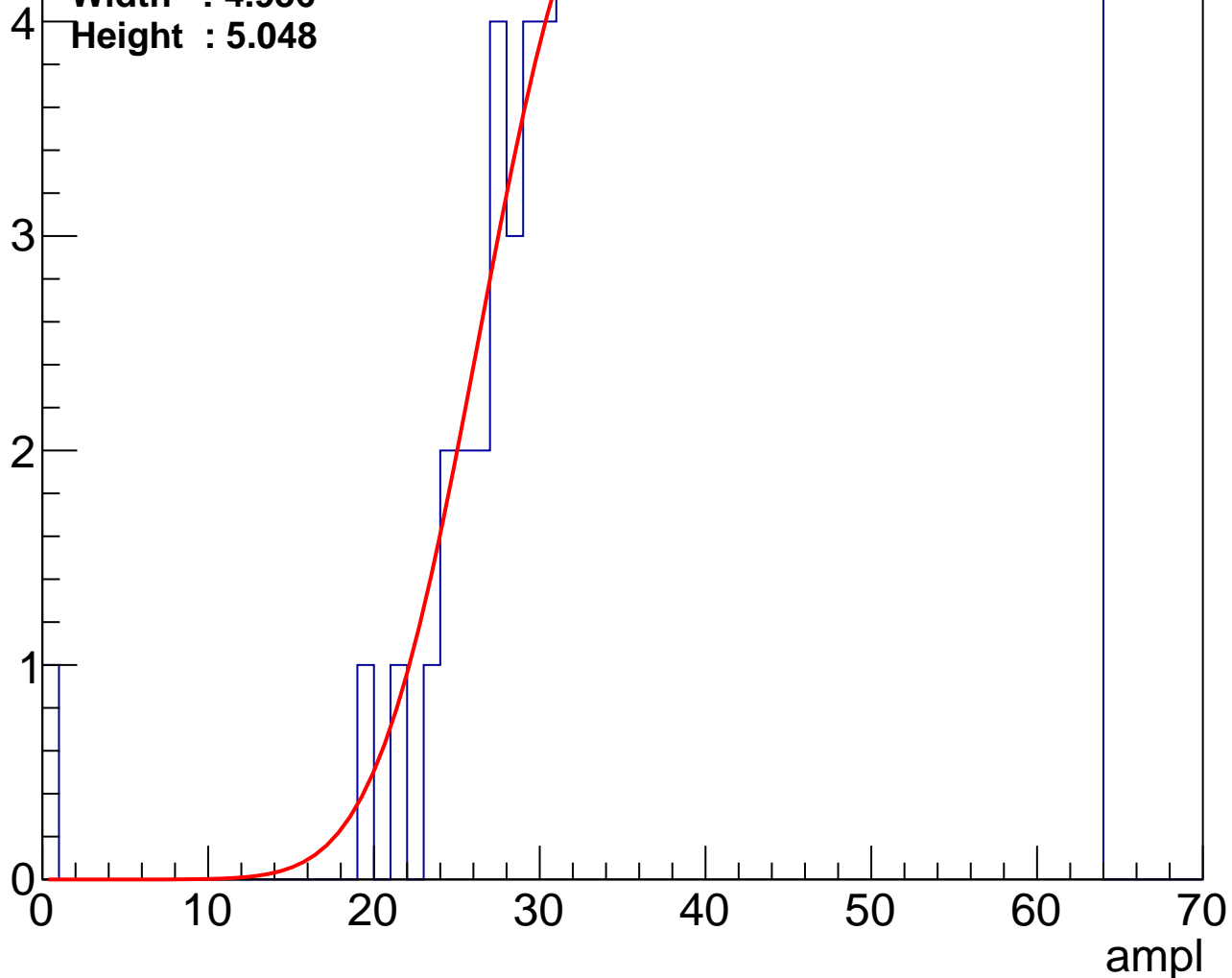
Width : 4.936

Height : 5.048

Entries 190

Mean 44.19

Std Dev 11.64



B1L103S, U15-ch8

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

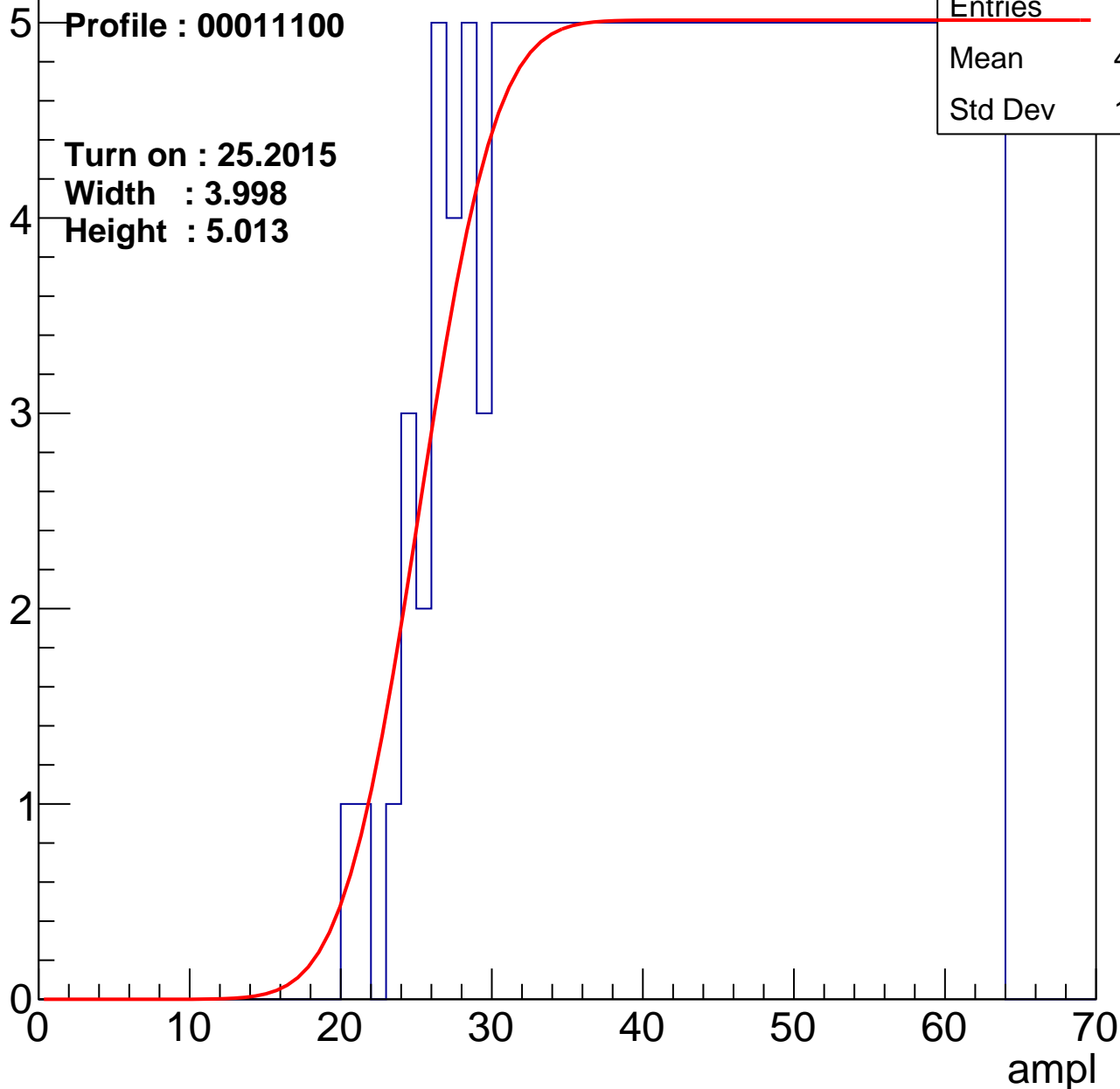
Profile : 00011100

Turn on : 25.2015

Width : 3.998

Height : 5.013

Entries	195
Mean	43.88
Std Dev	11.46



B1L103S, U15-ch9

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.9692

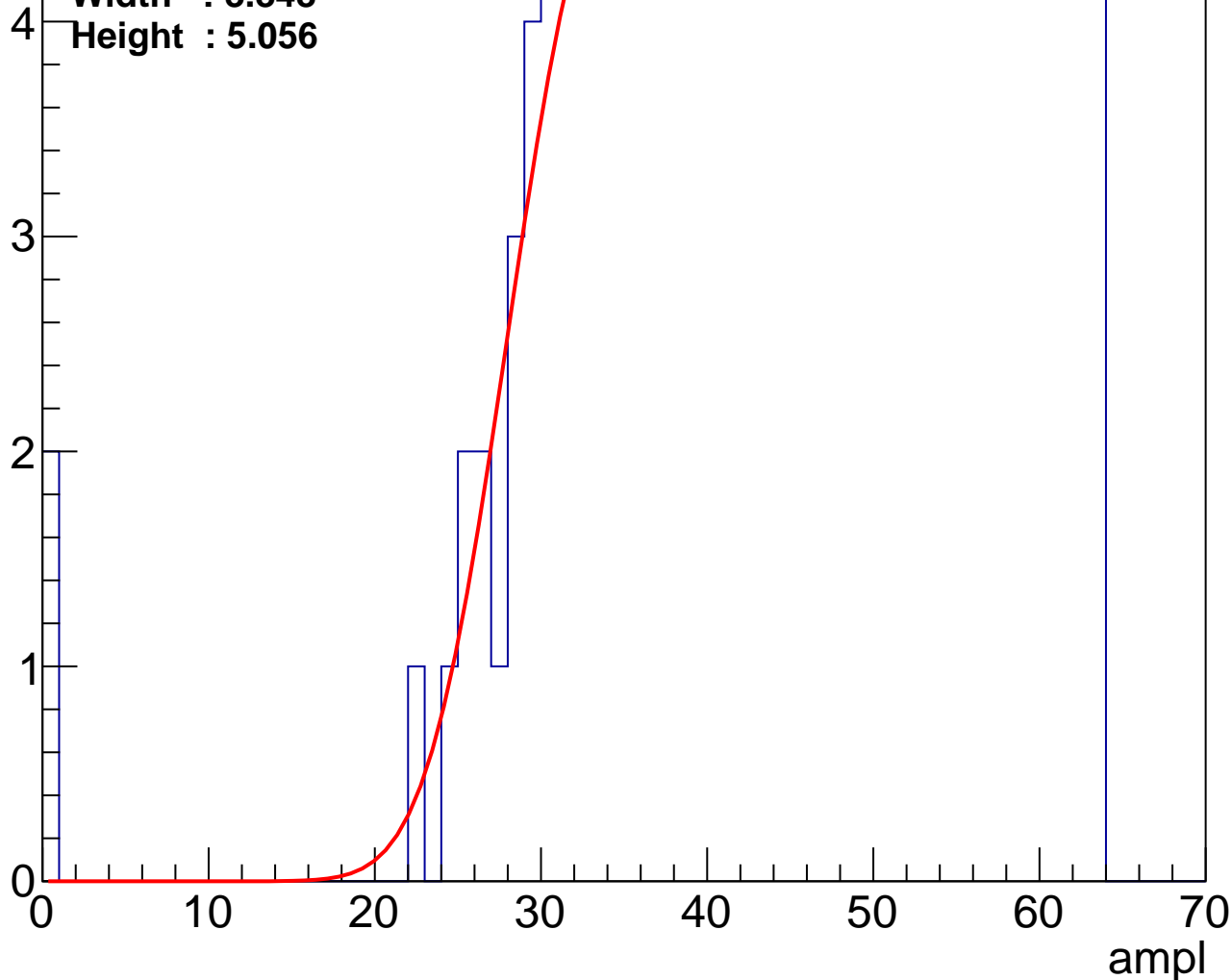
Width : 3.848

Height : 5.056

Entries 186

Mean 44.52

Std Dev 11.7



B1L103S, U15-ch10

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.8138

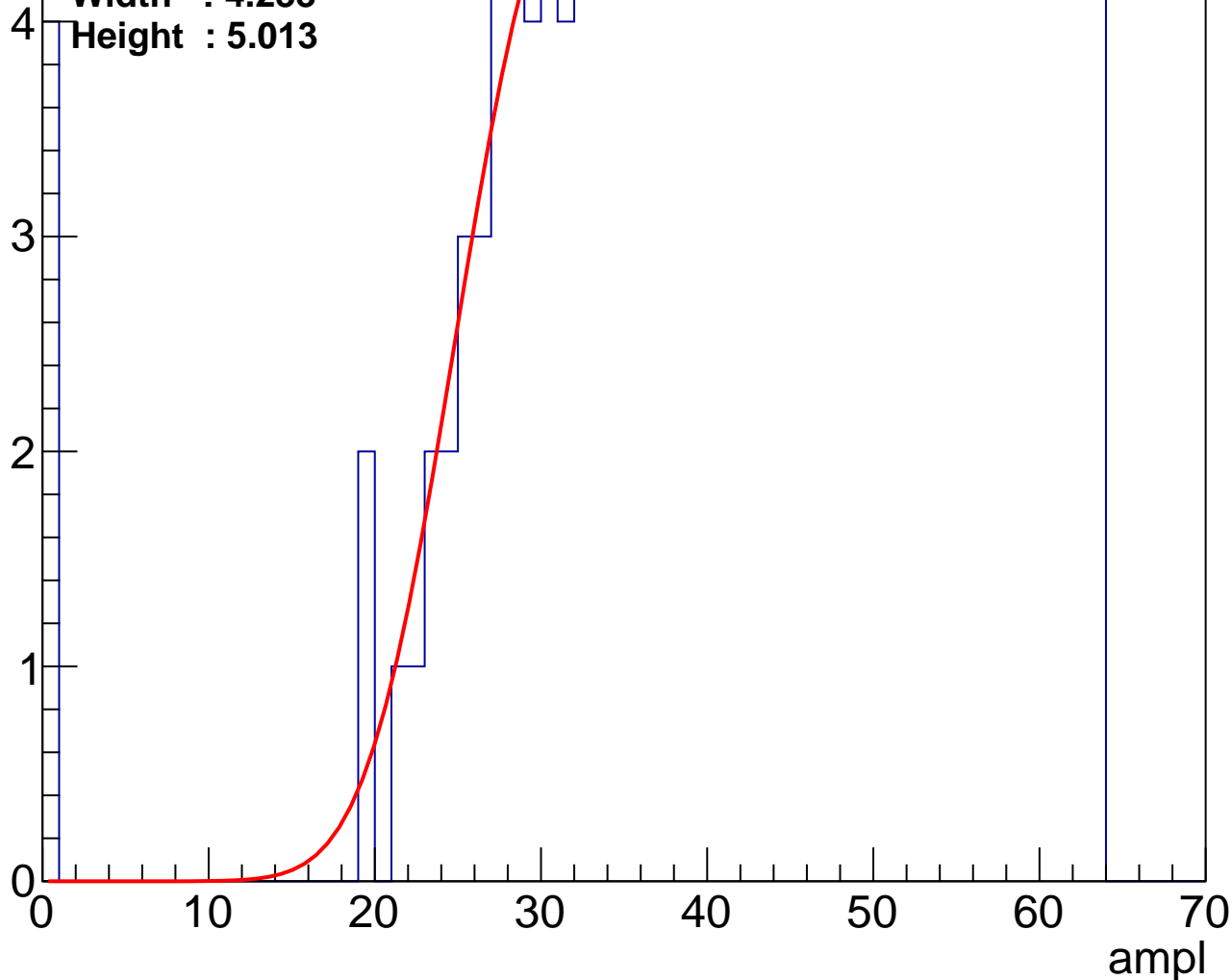
Width : 4.233

Height : 5.013

Entries 201

Mean 42.75

Std Dev 13.07



B1L103S, U15-ch11

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

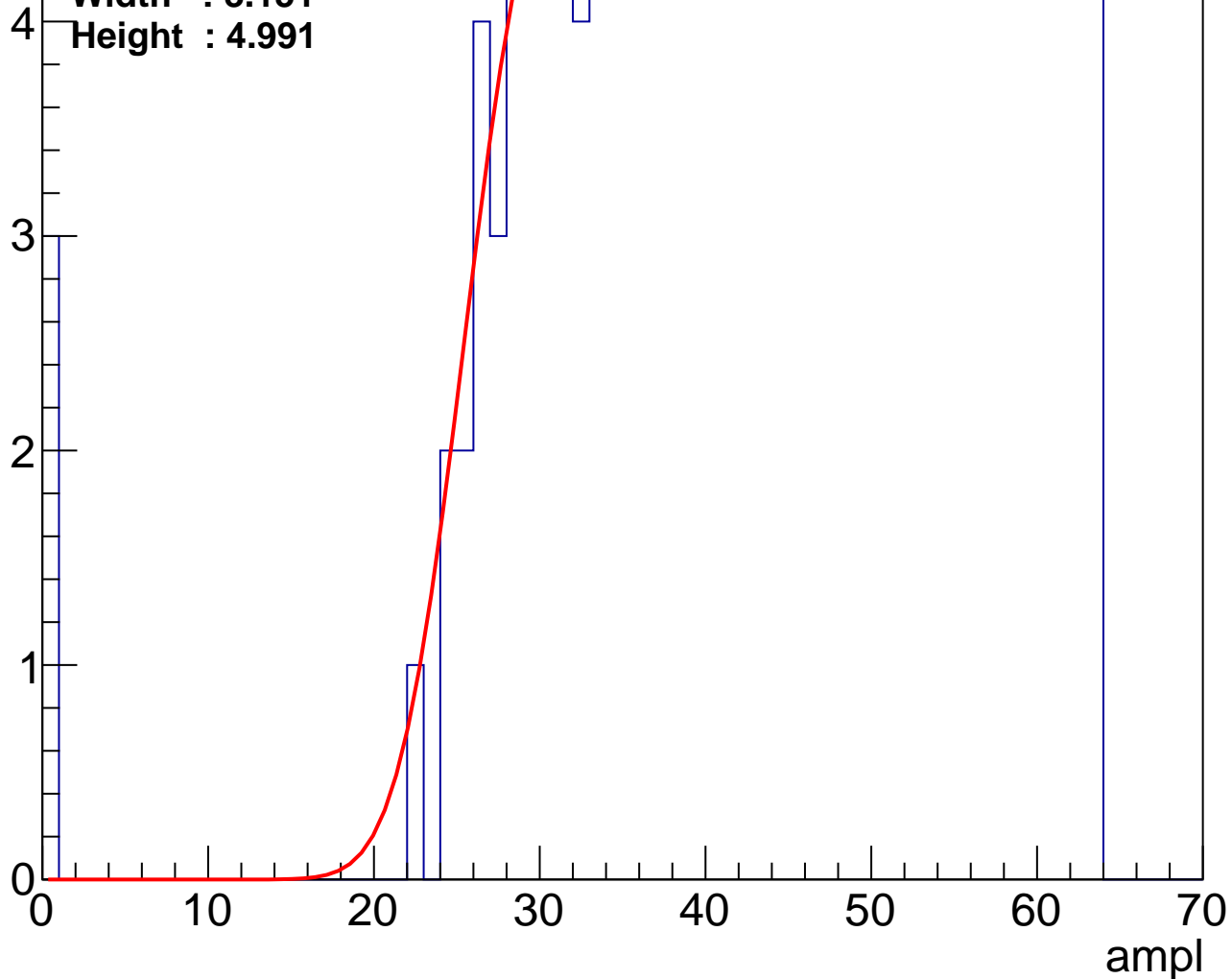
Profile : 00011100

Turn on : 25.4272

Width : 3.151

Height : 4.991

Entries	194
Mean	43.62
Std Dev	12.36



B1L103S, U15-ch12

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.0969

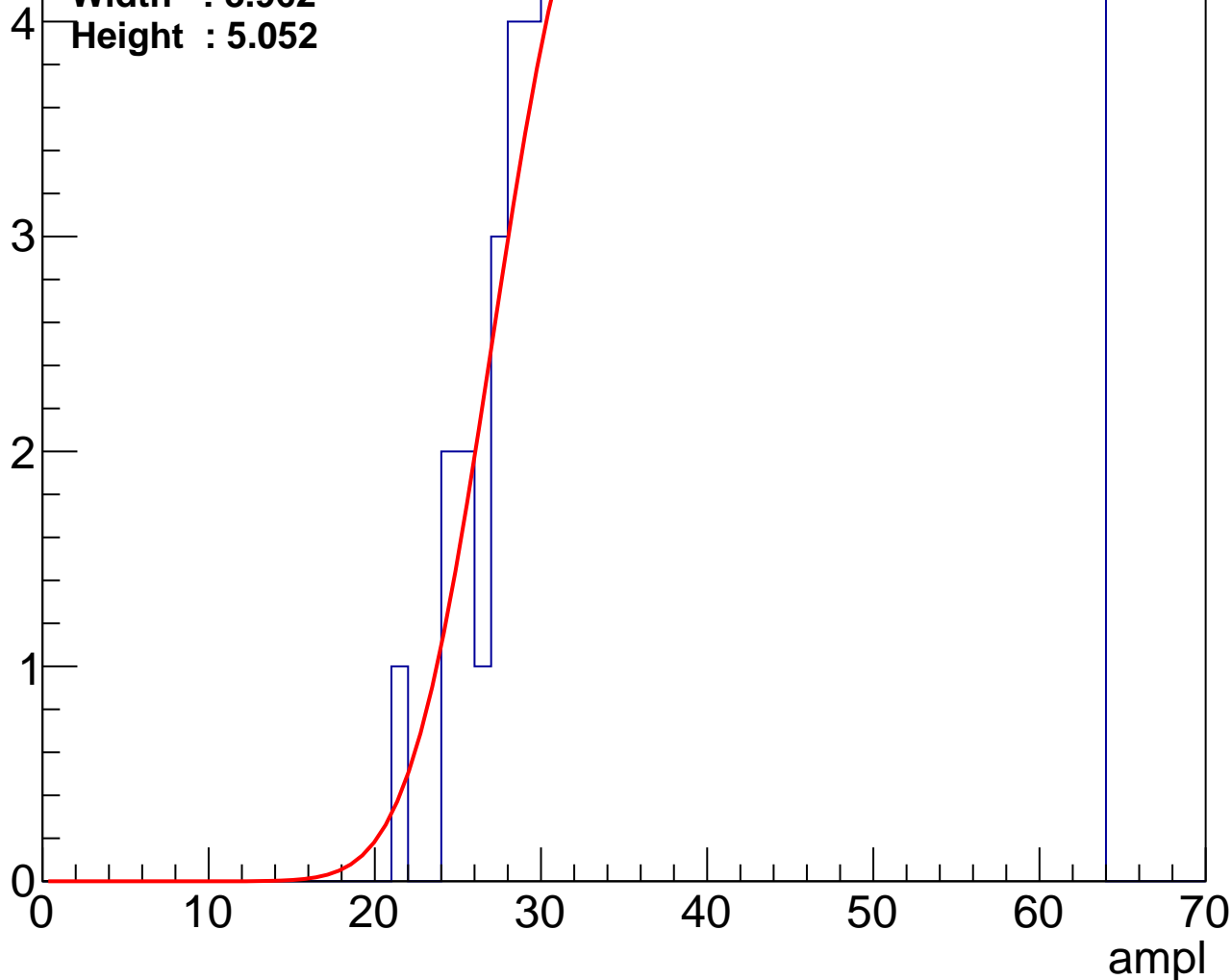
Width : 3.962

Height : 5.052

Entries 187

Mean 44.7

Std Dev 10.97



B1L103S, U15-ch13

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.0541

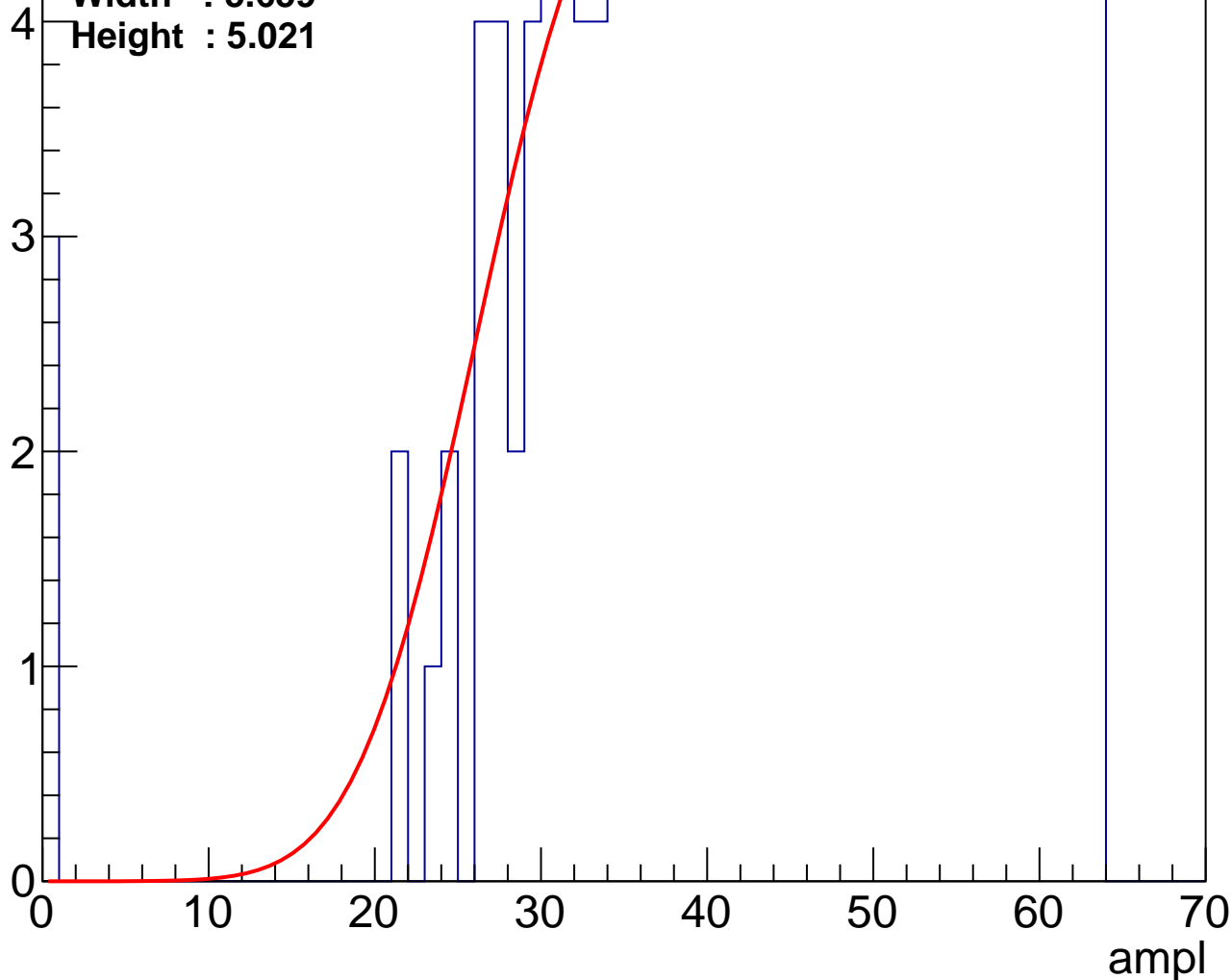
Width : 5.659

Height : 5.021

Entries 190

Mean 43.88

Std Dev 12.38



B1L103S, U15-ch14

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.1580

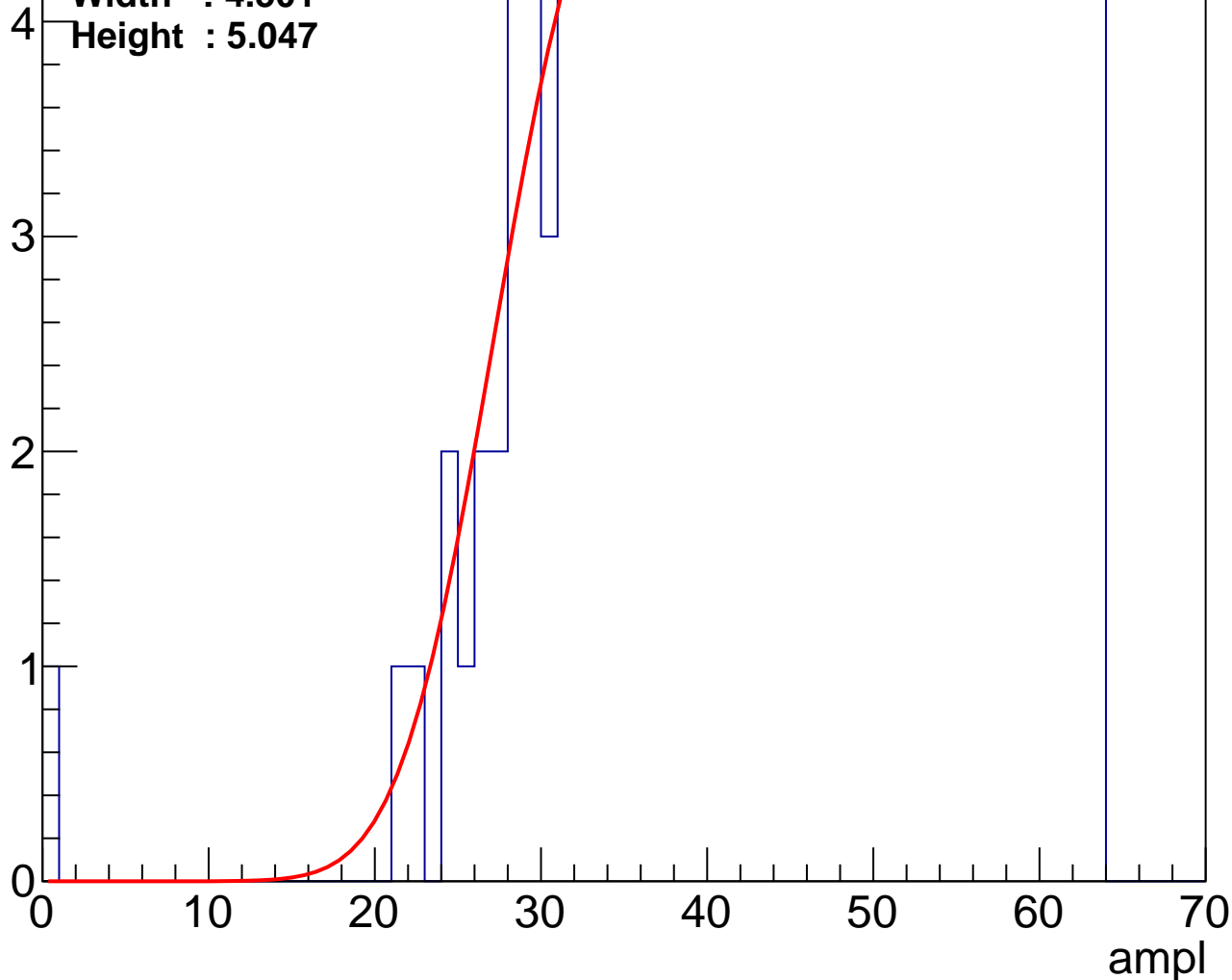
Width : 4.501

Height : 5.047

Entries 188

Mean 44.43

Std Dev 11.47



B1L103S, U15-ch15

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 29.3127

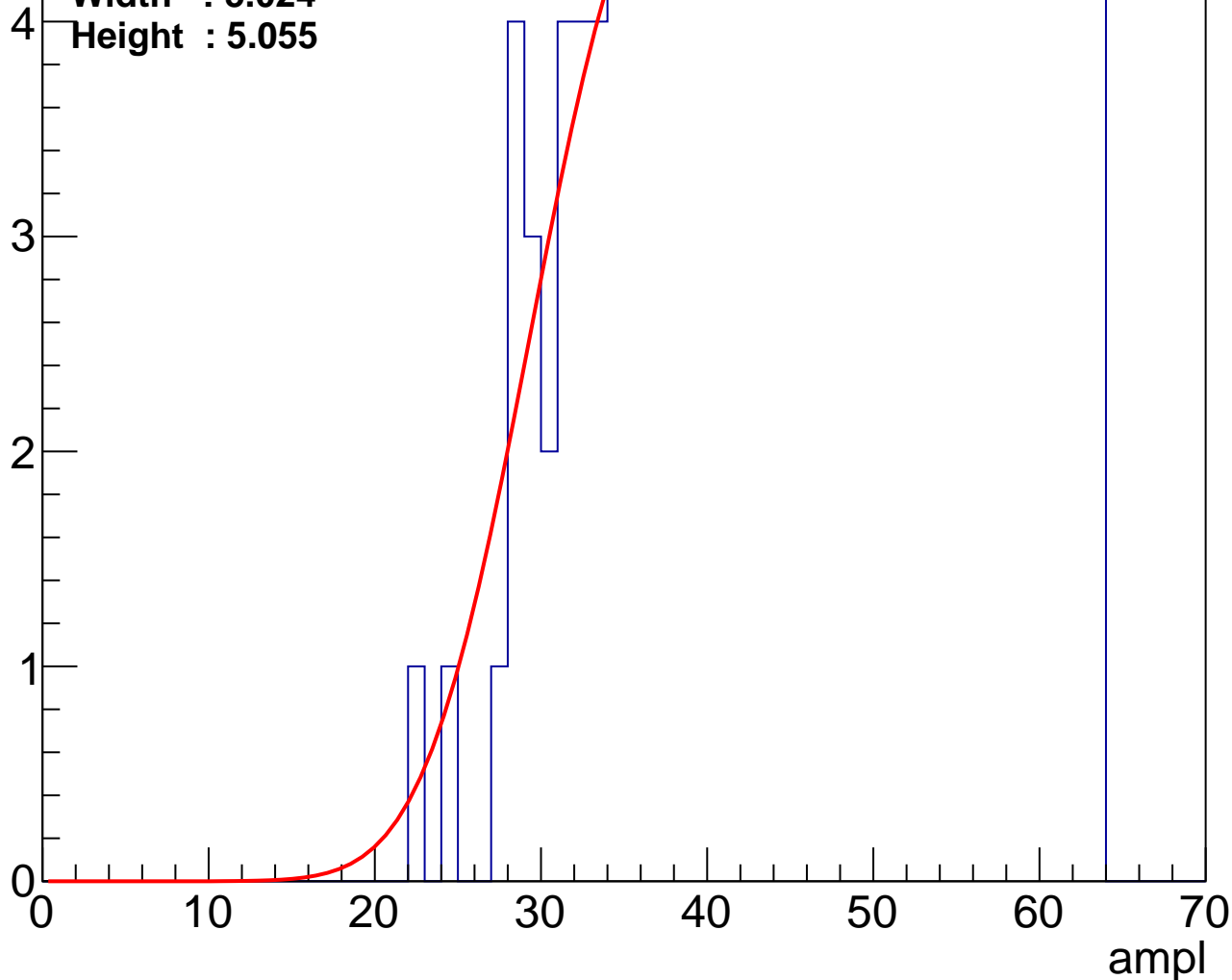
Width : 5.024

Height : 5.055

Entries 174

Mean 45.93

Std Dev 10.35



B1L103S, U15-ch16

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.4908

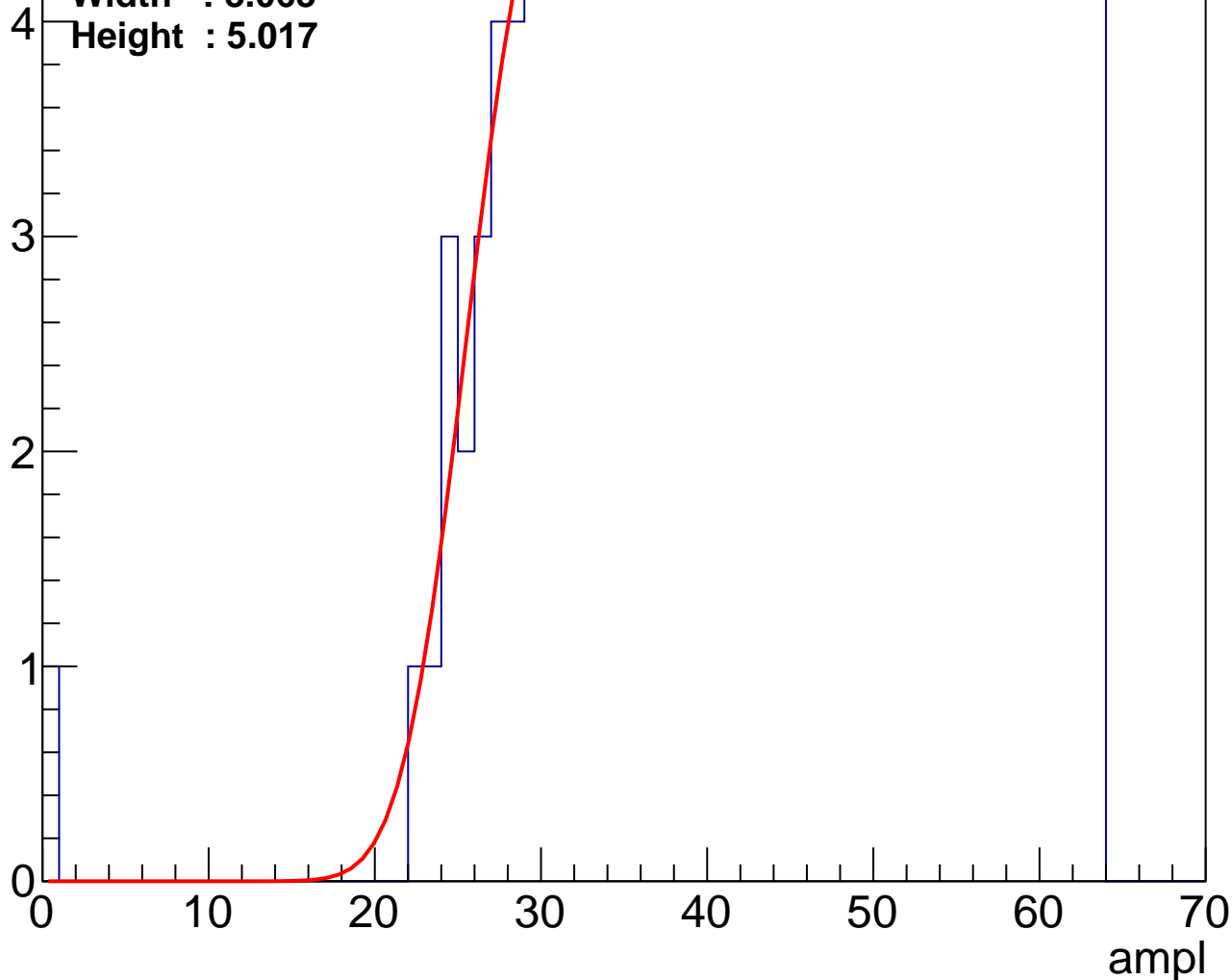
Width : 3.063

Height : 5.017

Entries 194

Mean 43.89

Std Dev 11.68



B1L103S, U15-ch17

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 28.0460

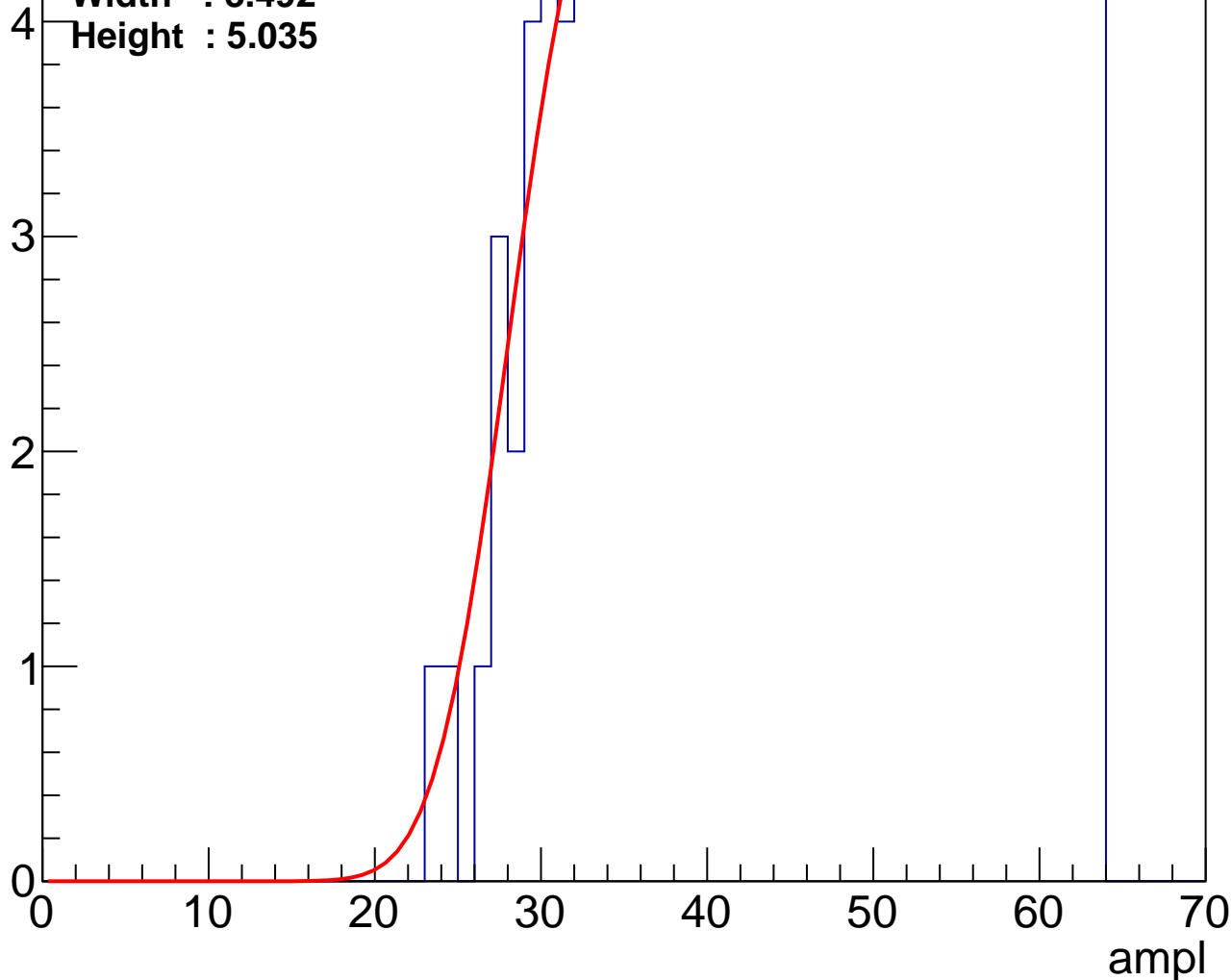
Width : 3.492

Height : 5.035

Entries 181

Mean 45.3

Std Dev 10.61



B1L103S, U15-ch18

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.9005

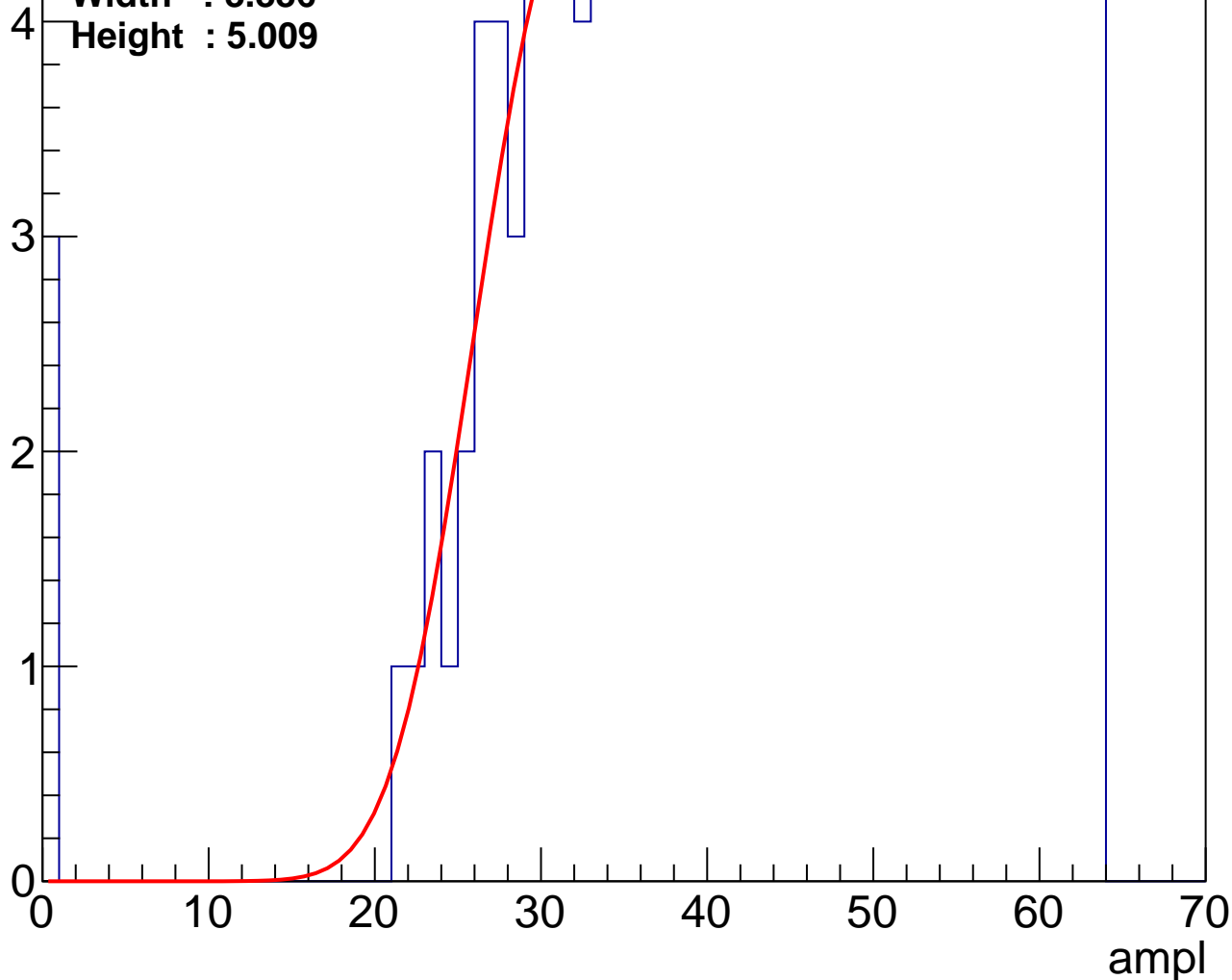
Width : 3.886

Height : 5.009

Entries 195

Mean 43.47

Std Dev 12.49



B1L103S, U15-ch19

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.8468

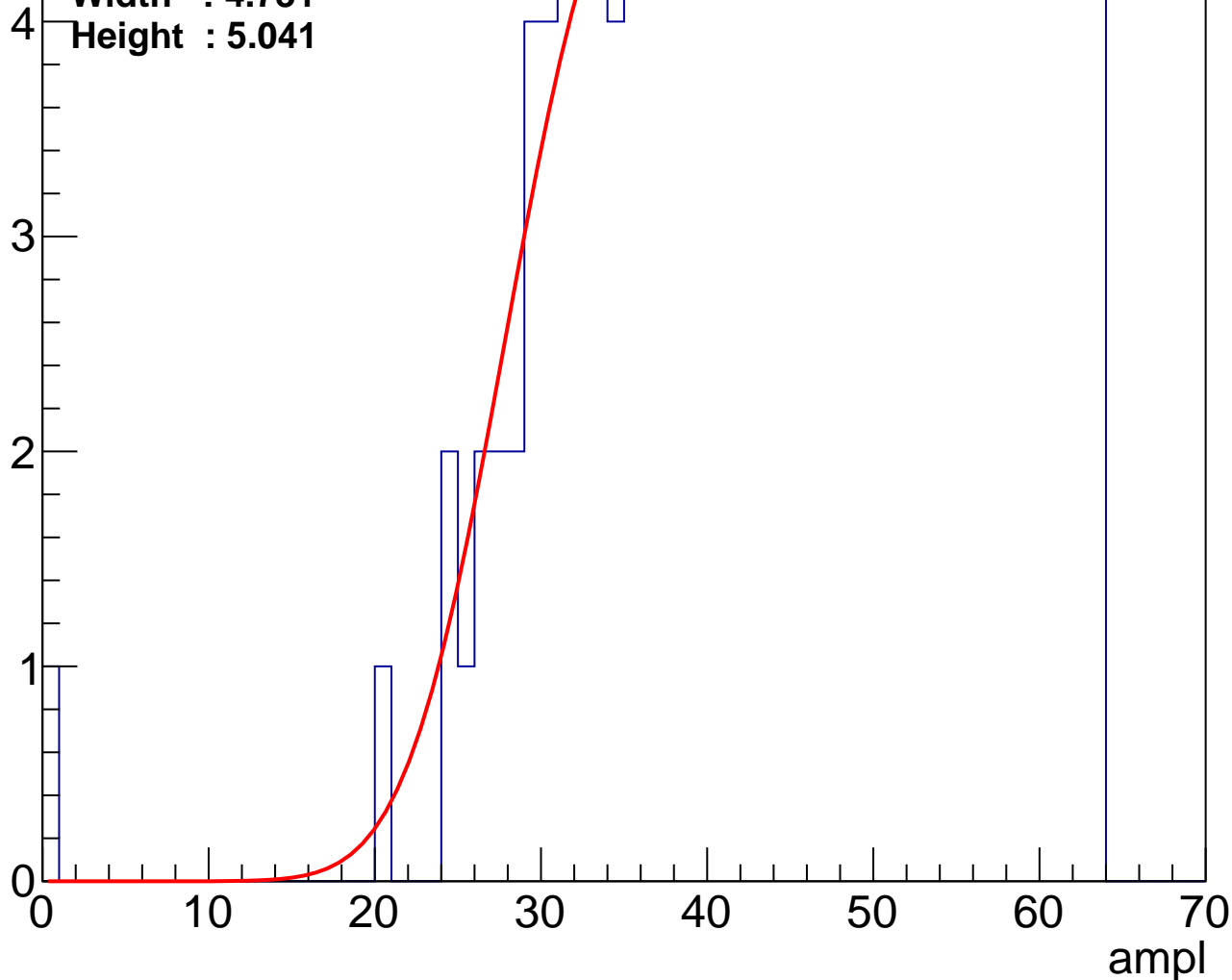
Width : 4.731

Height : 5.041

Entries 183

Mean 44.87

Std Dev 11.28



B1L103S, U15-ch20

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

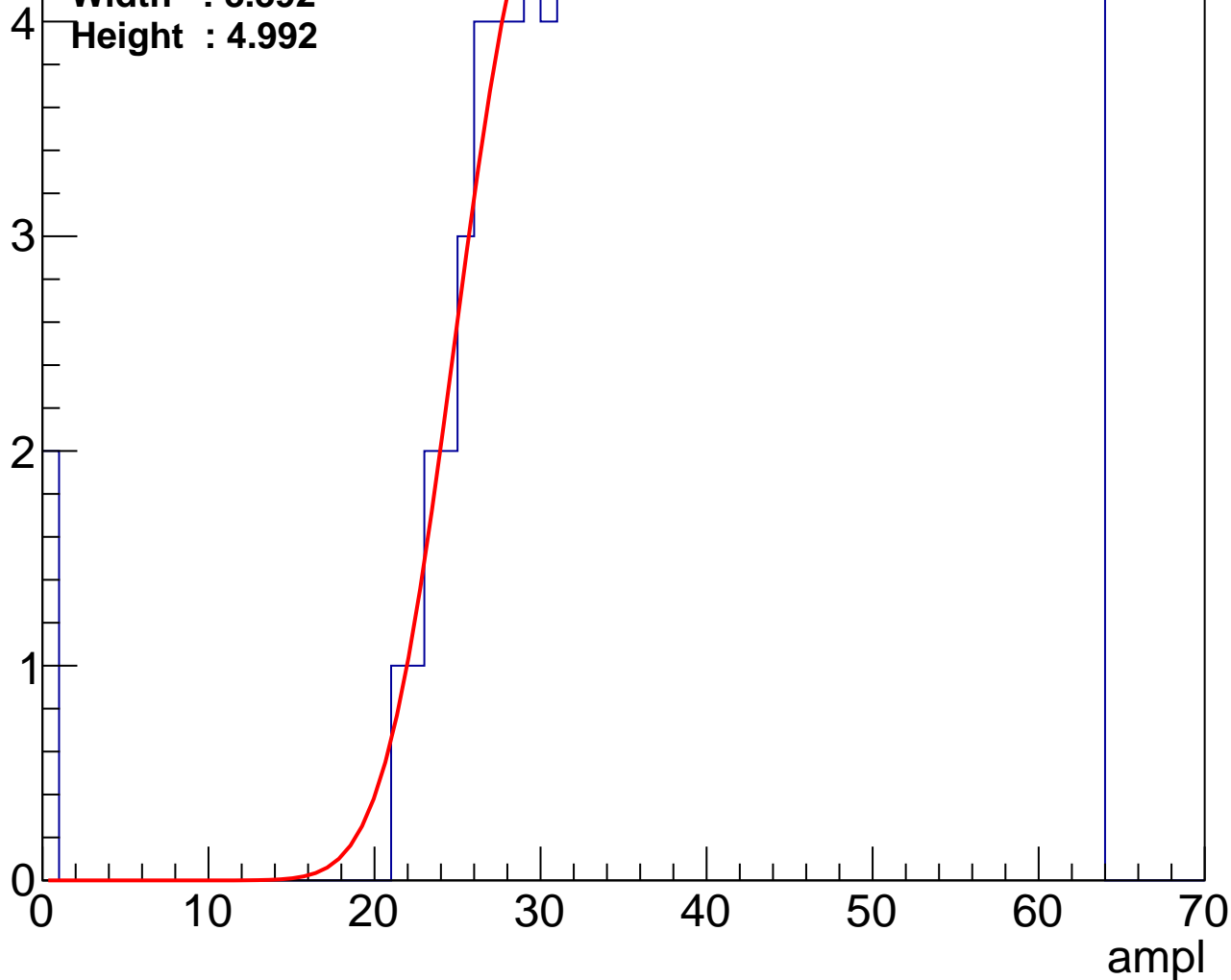
Profile : 00011100

Turn on : 24.8083

Width : 3.392

Height : 4.992

Entries	197
Mean	43.43
Std Dev	12.22



B1L103S, U15-ch21

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

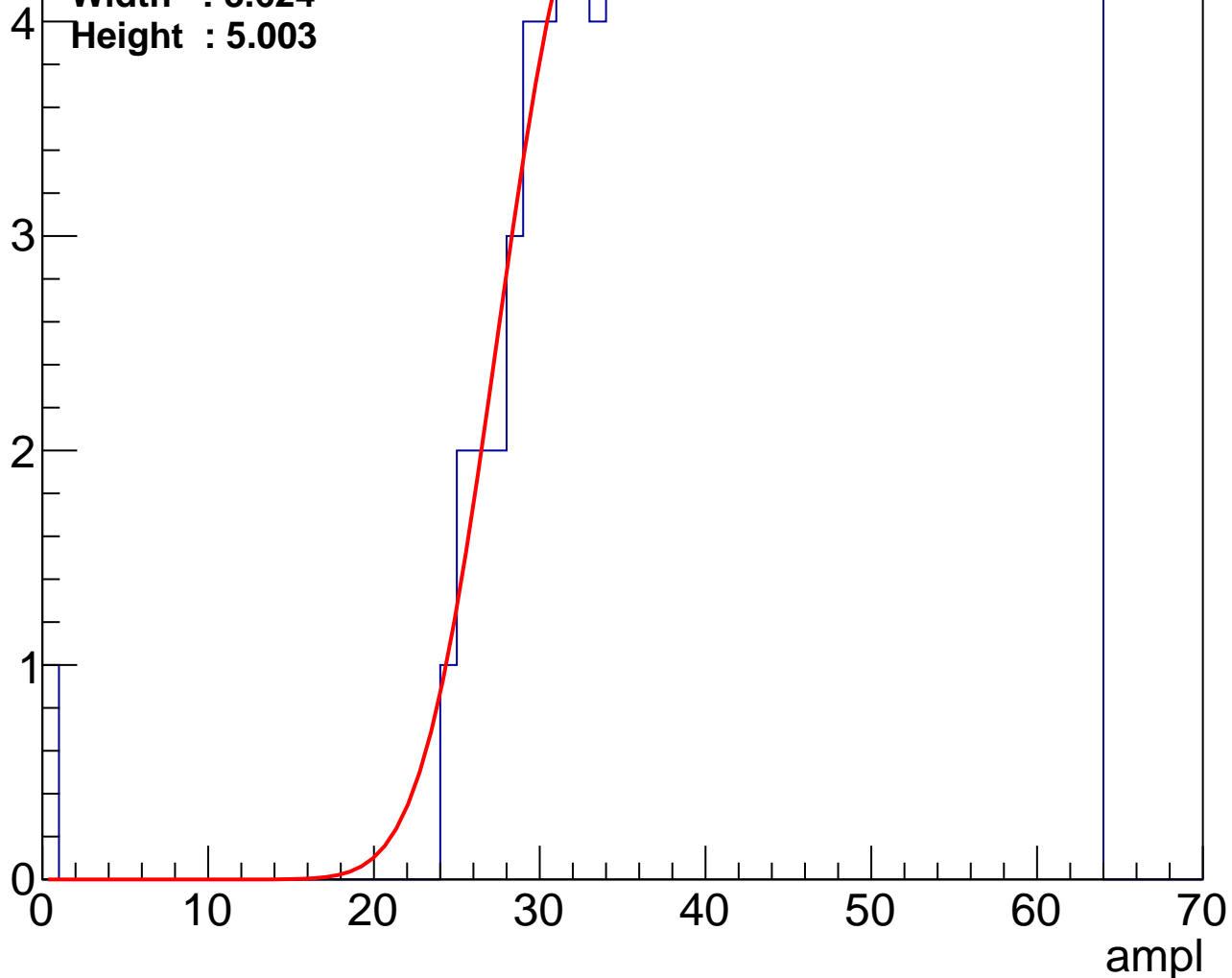
Profile : 00011100

Turn on : 27.4024

Width : 3.624

Height : 5.003

Entries	183
Mean	44.93
Std Dev	11.19



B1L103S, U15-ch22

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.7386

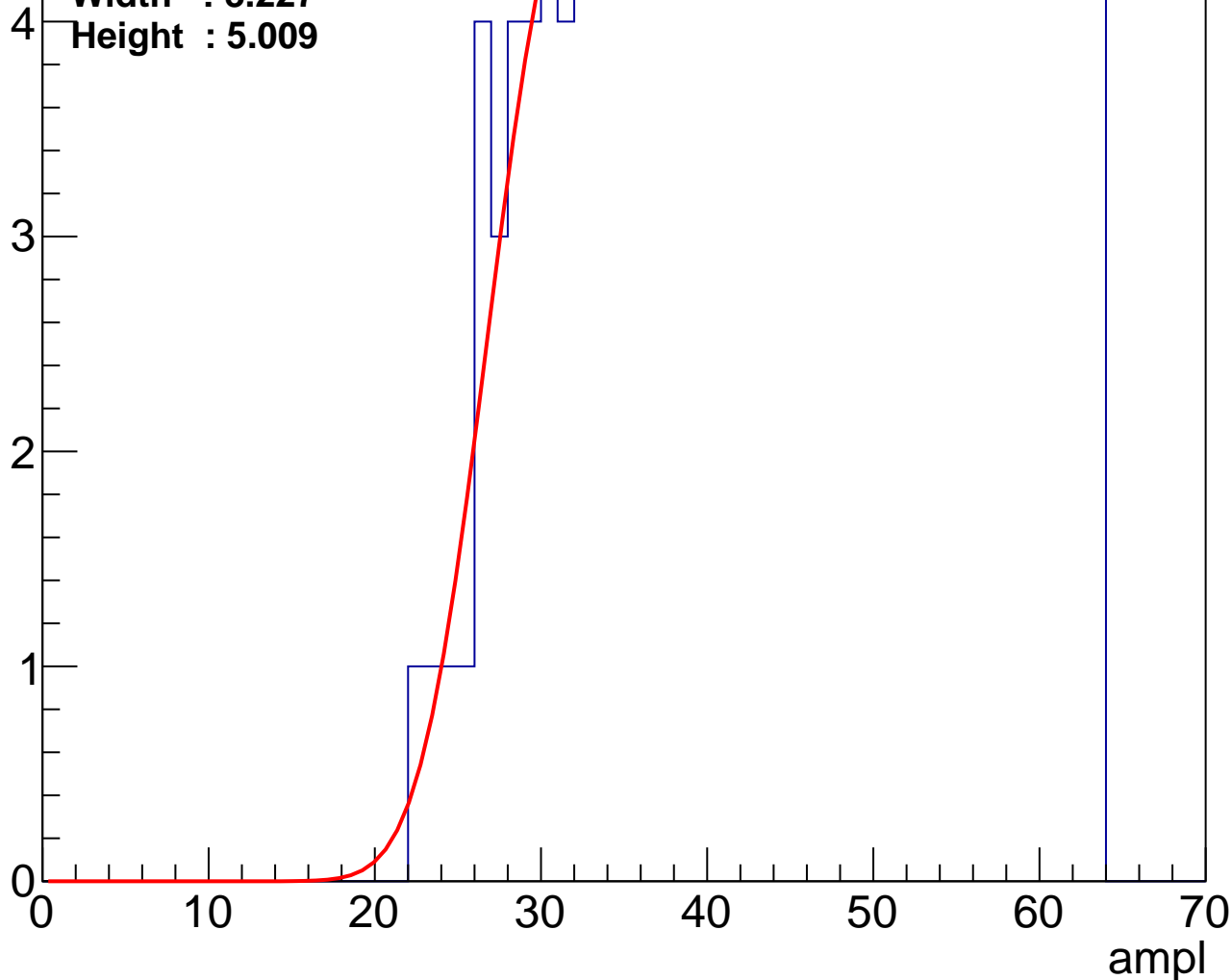
Width : 3.227

Height : 5.009

Entries 188

Mean 44.58

Std Dev 11.05



B1L103S, U15-ch23

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.7535

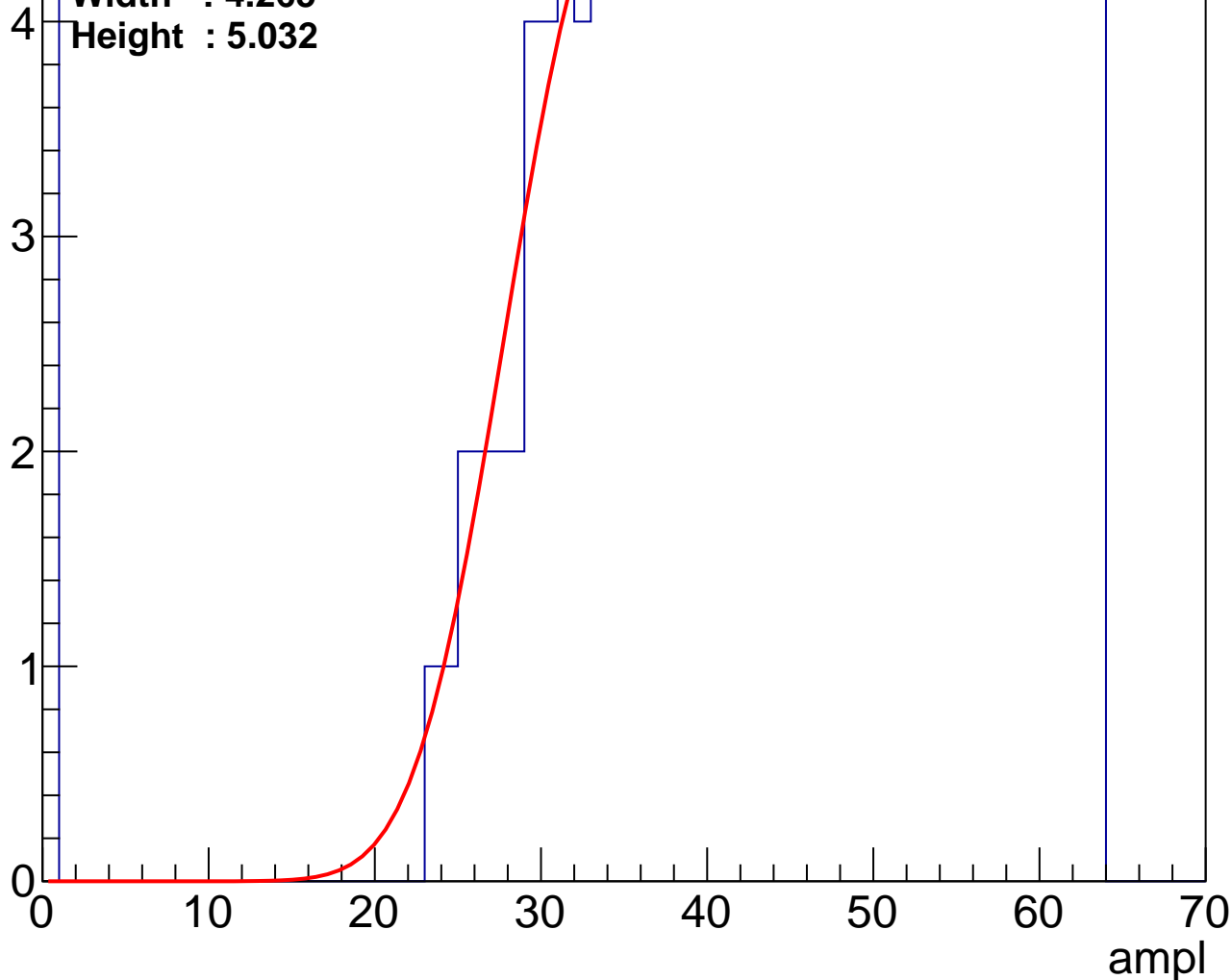
Width : 4.265

Height : 5.032

Entries 187

Mean 43.95

Std Dev 12.87



B1L103S, U15-ch24

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.3093

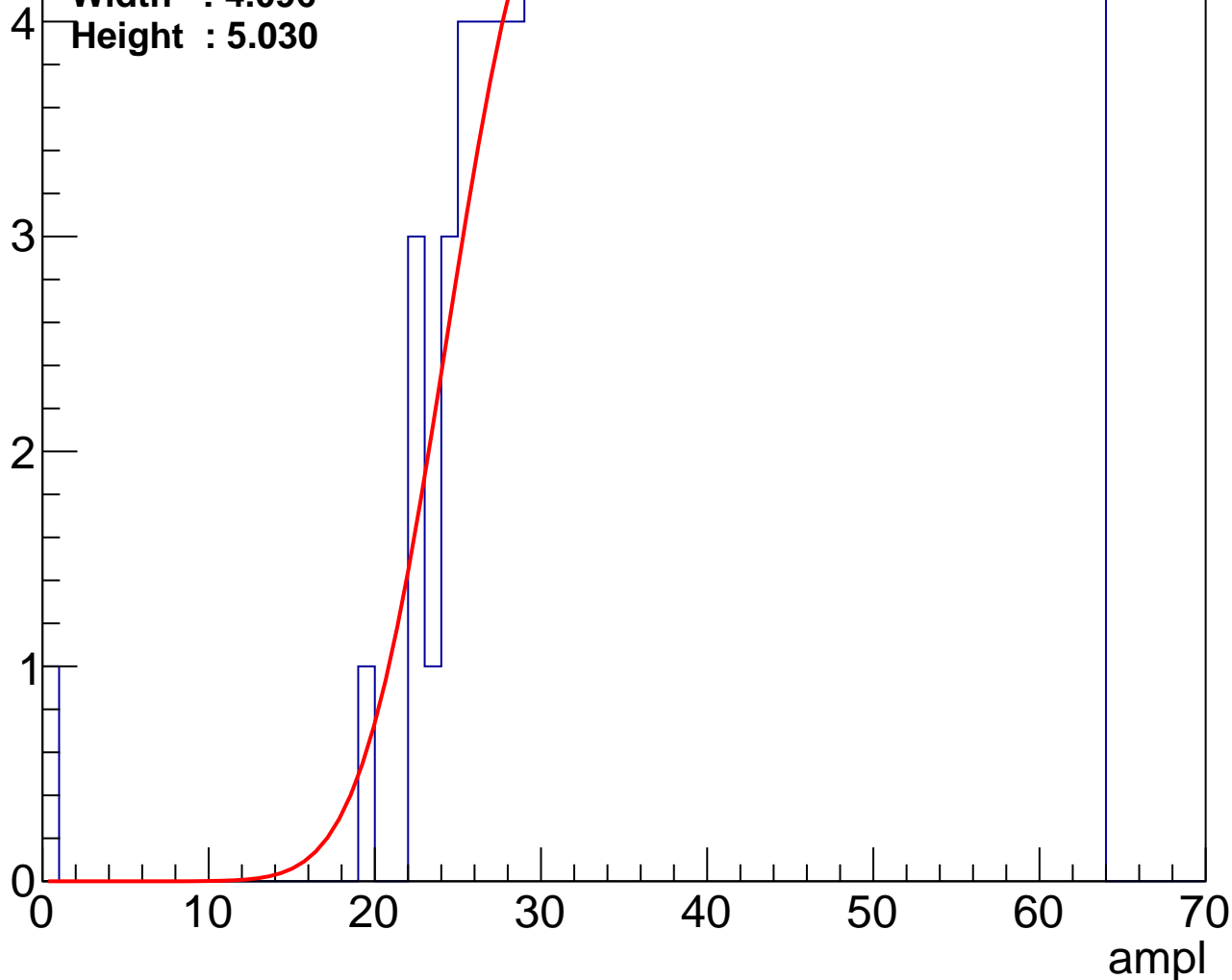
Width : 4.096

Height : 5.030

Entries 200

Mean 43.27

Std Dev 12.05



B1L103S, U15-ch25

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 29.3841

Width : 2.054

Height : 4.996

Entries	178
Mean	45.12
Std Dev	11.78

3

2

1

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U15-ch26

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.3299

Width : 3.322

Height : 5.022

Entries	201
Mean	43.08
Std Dev	12.36

2

1

0

0

10

20

30

40

50

60

70

ampl

3

4

5

B1L103S, U15-ch27

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.4233

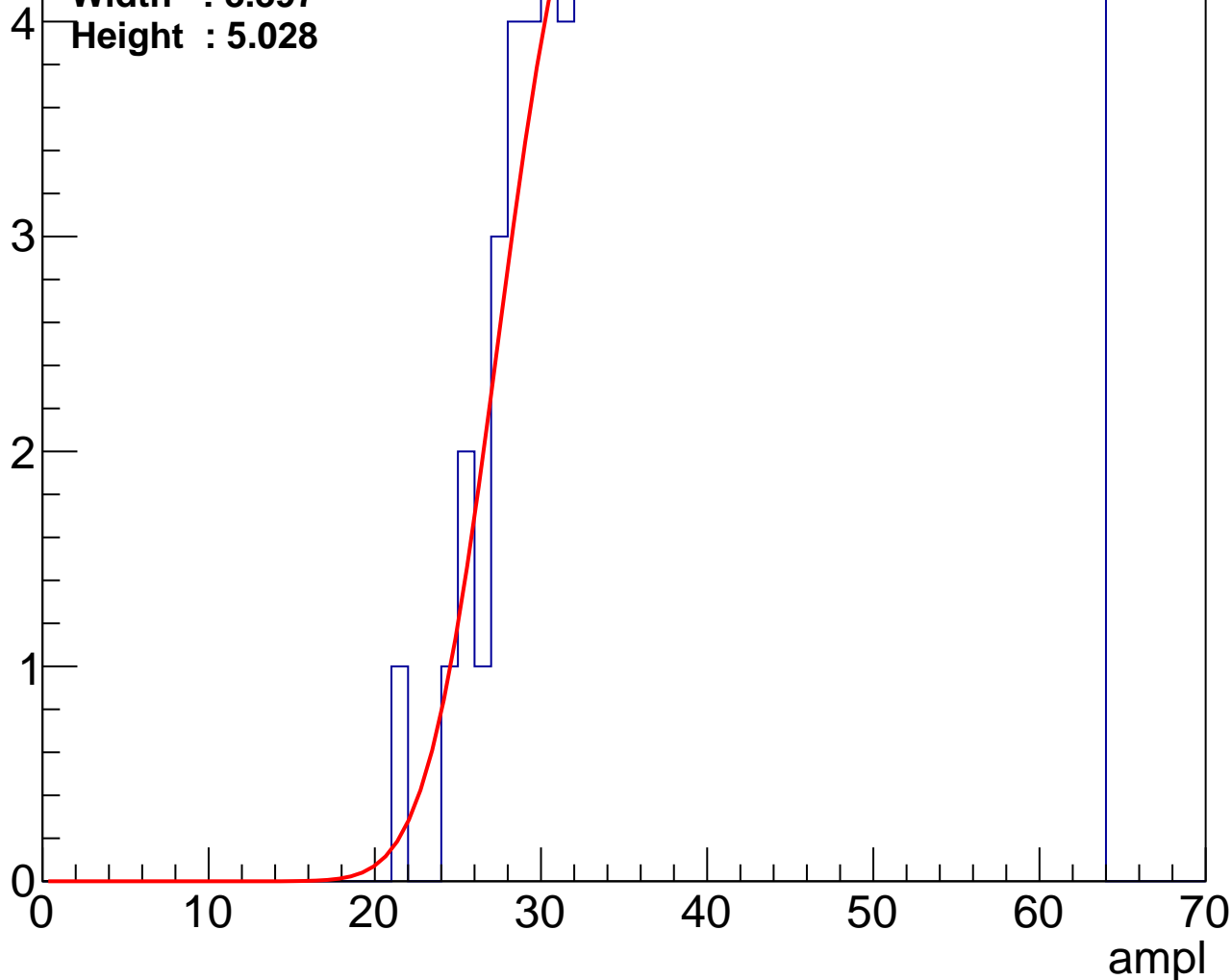
Width : 3.397

Height : 5.028

Entries 185

Mean 44.89

Std Dev 10.87



B1L103S, U15-ch28

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.4262

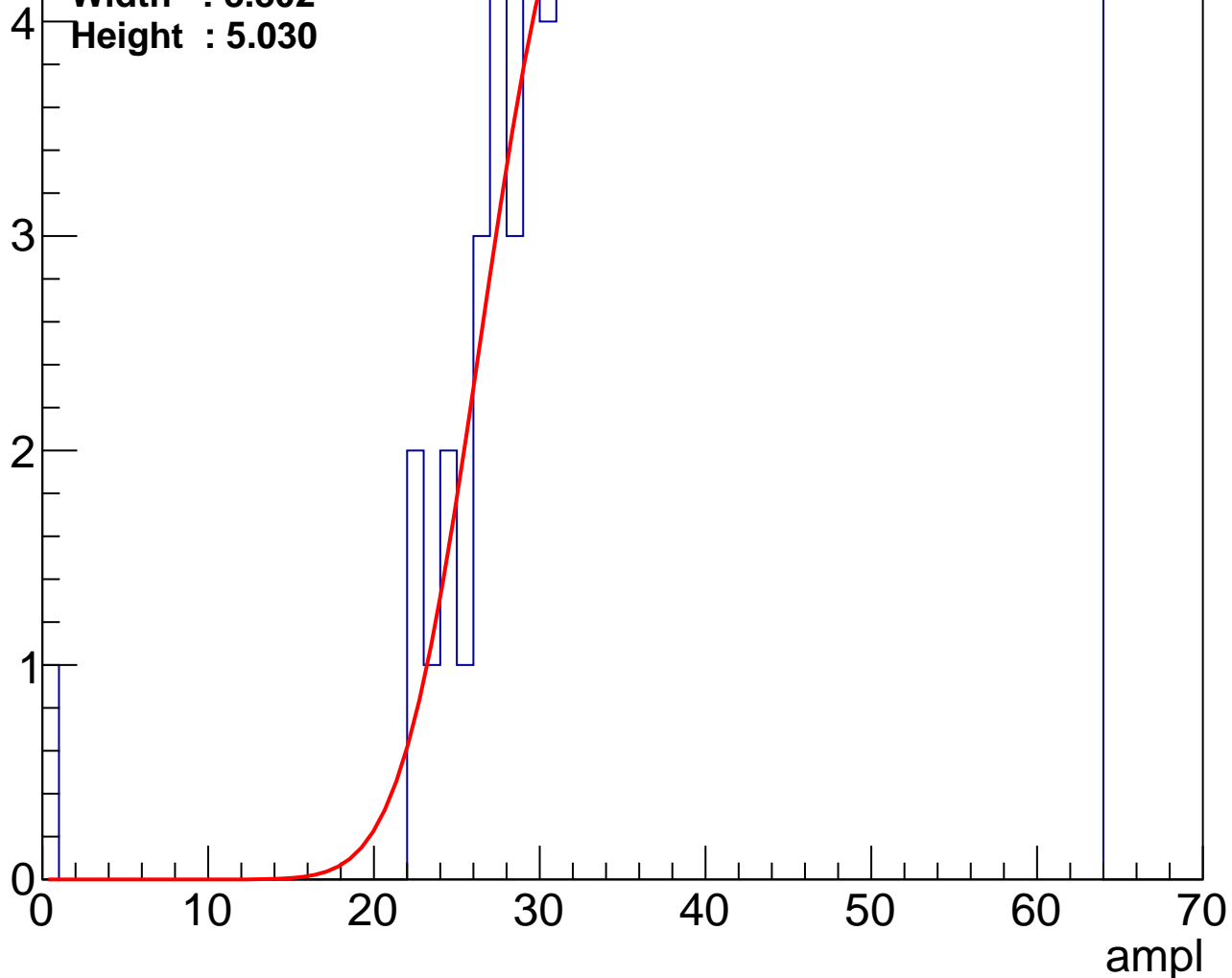
Width : 3.802

Height : 5.030

Entries 192

Mean 44.05

Std Dev 11.65



B1L103S, U15-ch29

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.4416

Width : 3.642

Height : 5.011

Entries	189
Mean	44.06
Std Dev	12.22

3

2

1

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U15-ch30

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.2482

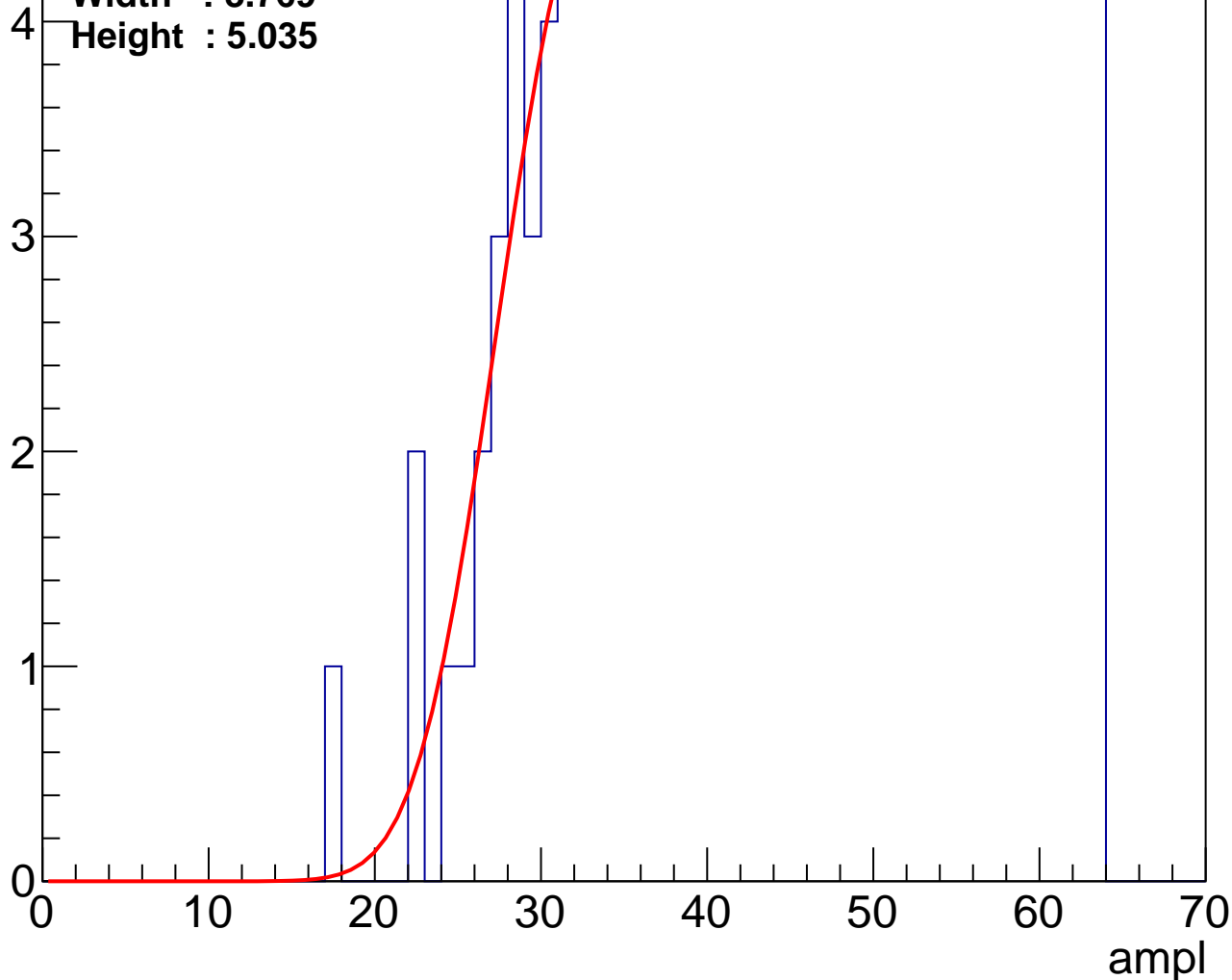
Width : 3.769

Height : 5.035

Entries 187

Mean 44.63

Std Dev 11.11



B1L103S, U15-ch31

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.5241

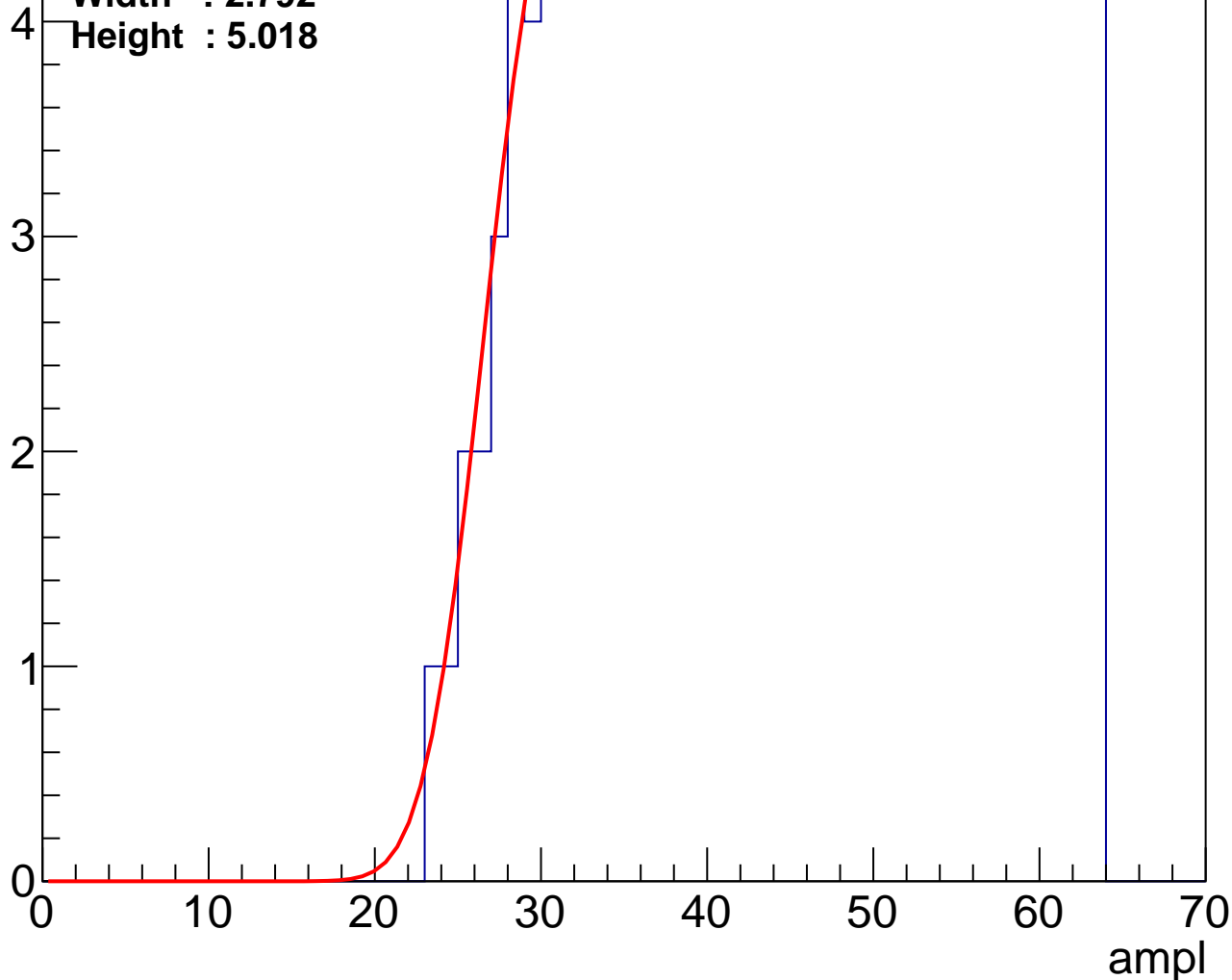
Width : 2.792

Height : 5.018

Entries 188

Mean 44.63

Std Dev 10.97



B1L103S, U15-ch32

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.5077

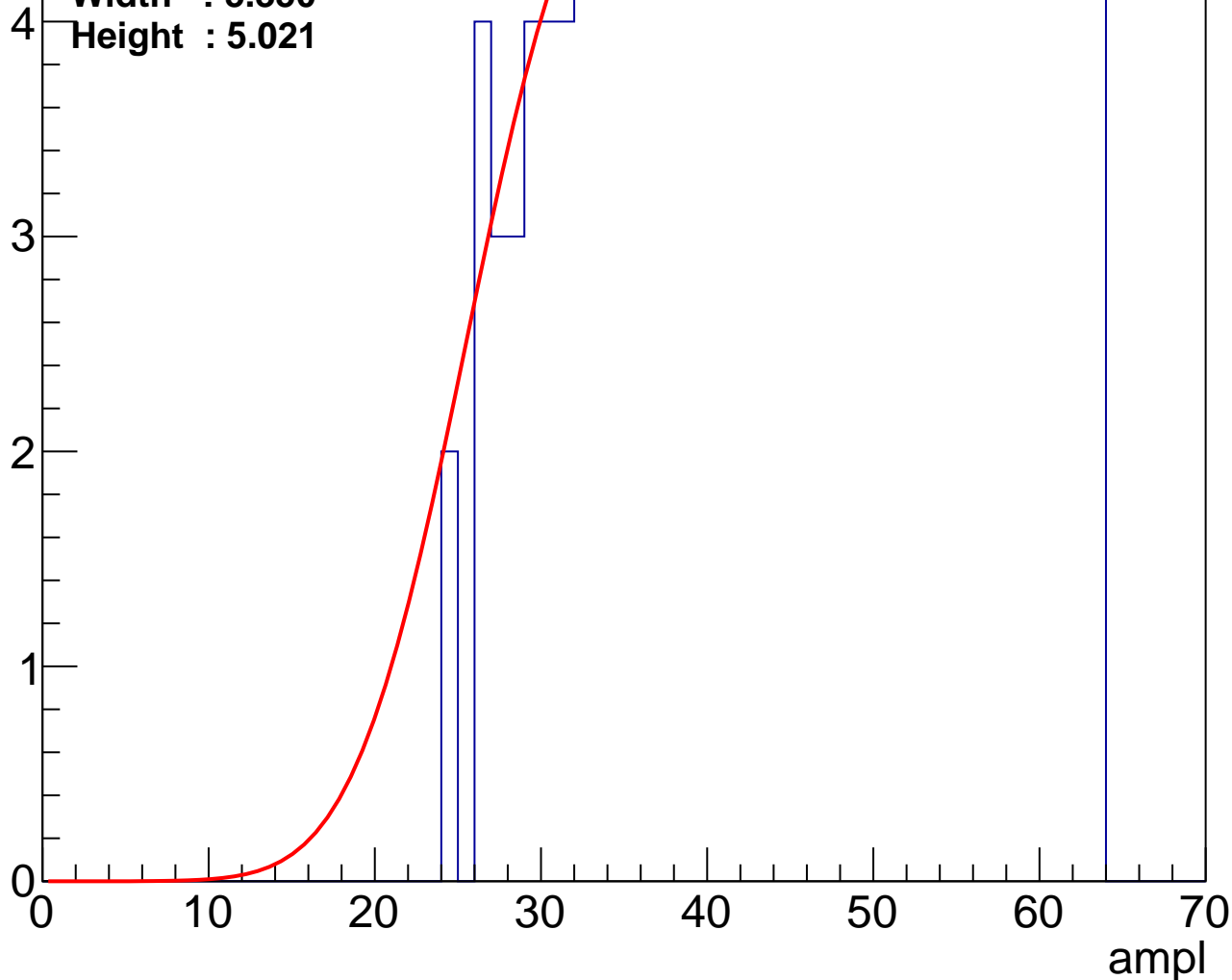
Width : 5.350

Height : 5.021

Entries 184

Mean 44.98

Std Dev 10.81



B1L103S, U15-ch33

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.0205

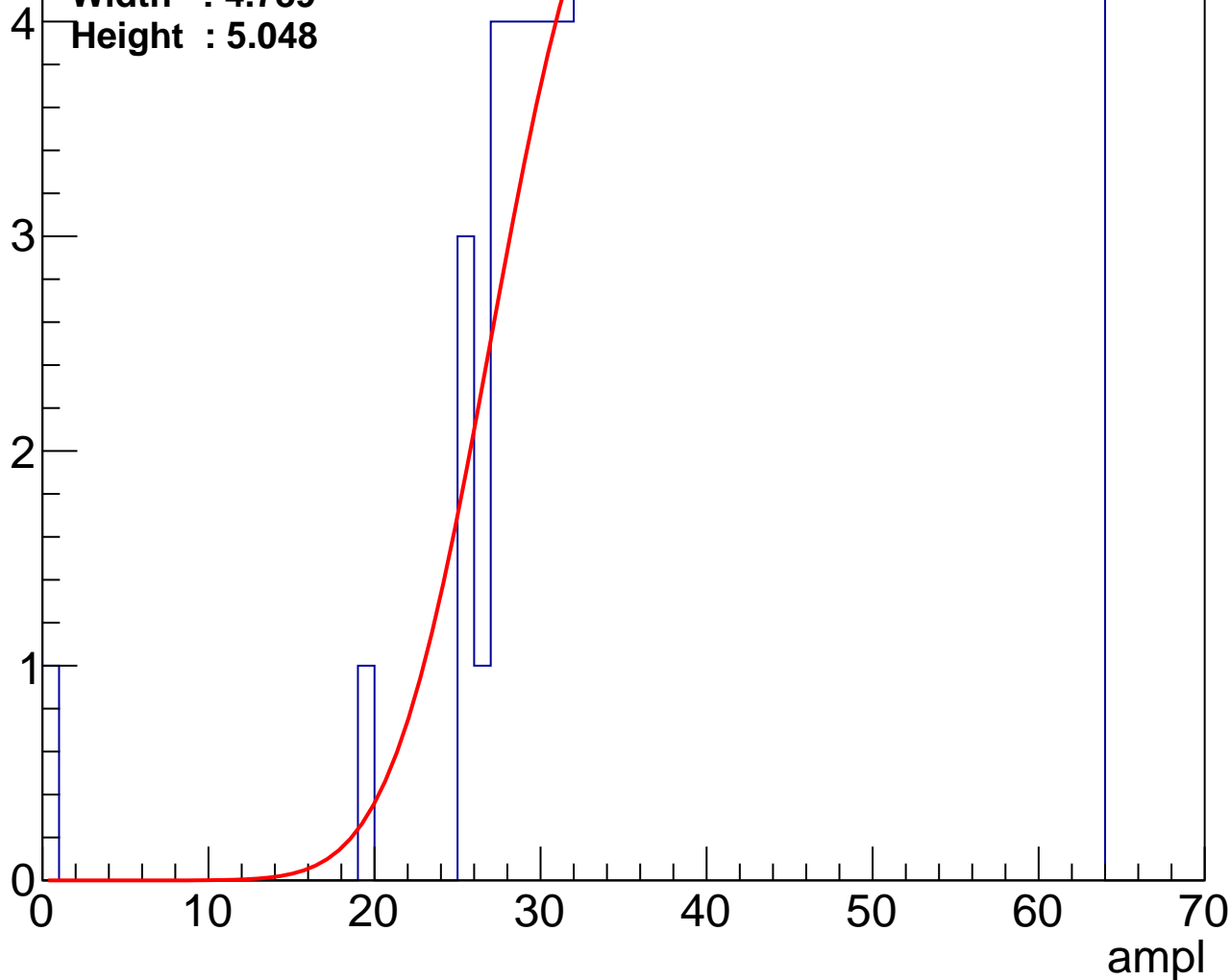
Width : 4.789

Height : 5.048

Entries 186

Mean 44.62

Std Dev 11.37



B1L103S, U15-ch34

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.9110

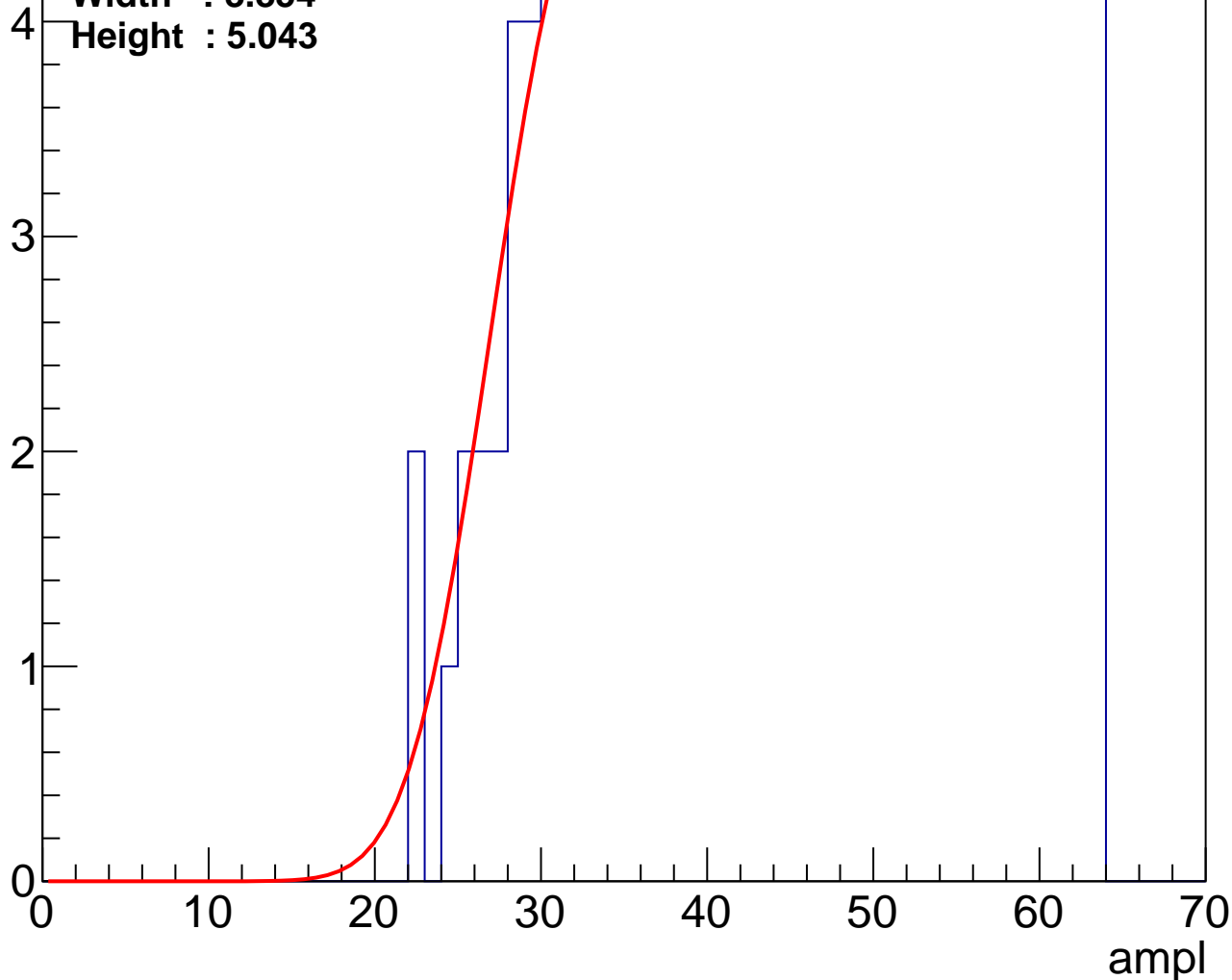
Width : 3.854

Height : 5.043

Entries 187

Mean 44.69

Std Dev 10.99



B1L103S, U15-ch35

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.0104

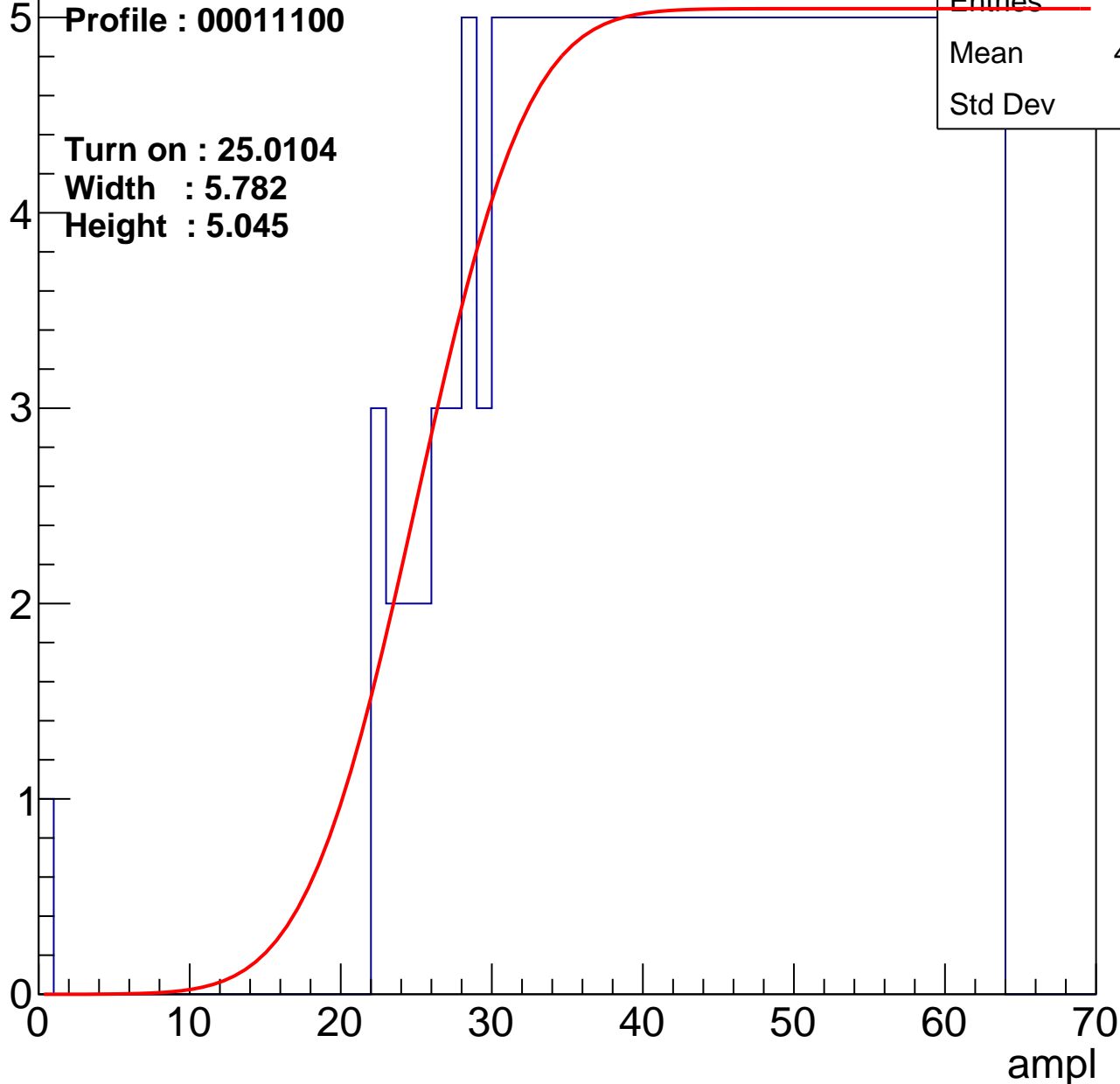
Width : 5.782

Height : 5.045

Entries 194

Mean 43.82

Std Dev 11.8



B1L103S, U15-ch36

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.8615

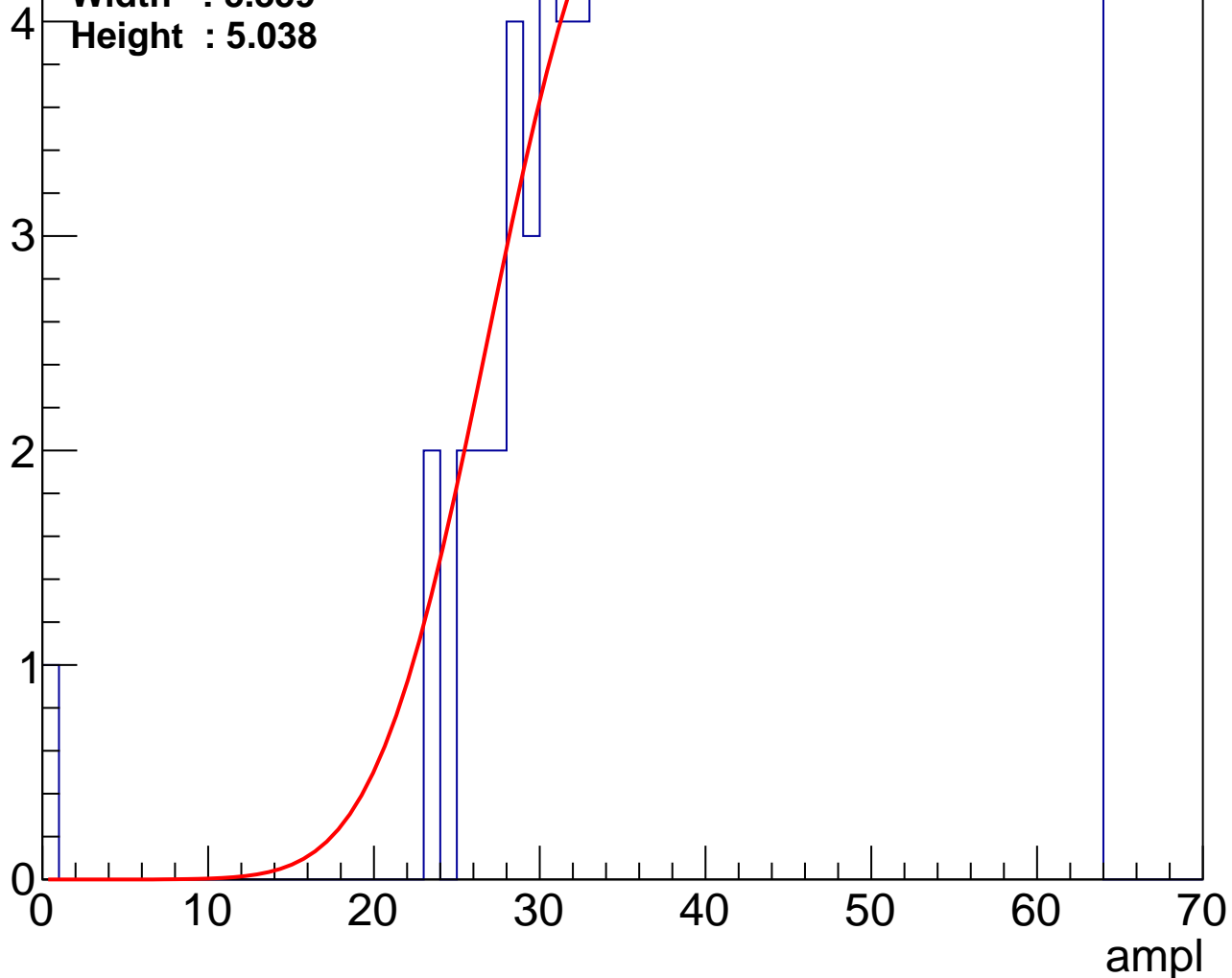
Width : 5.359

Height : 5.038

Entries 184

Mean 44.8

Std Dev 11.29



B1L103S, U15-ch37

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 29.2462

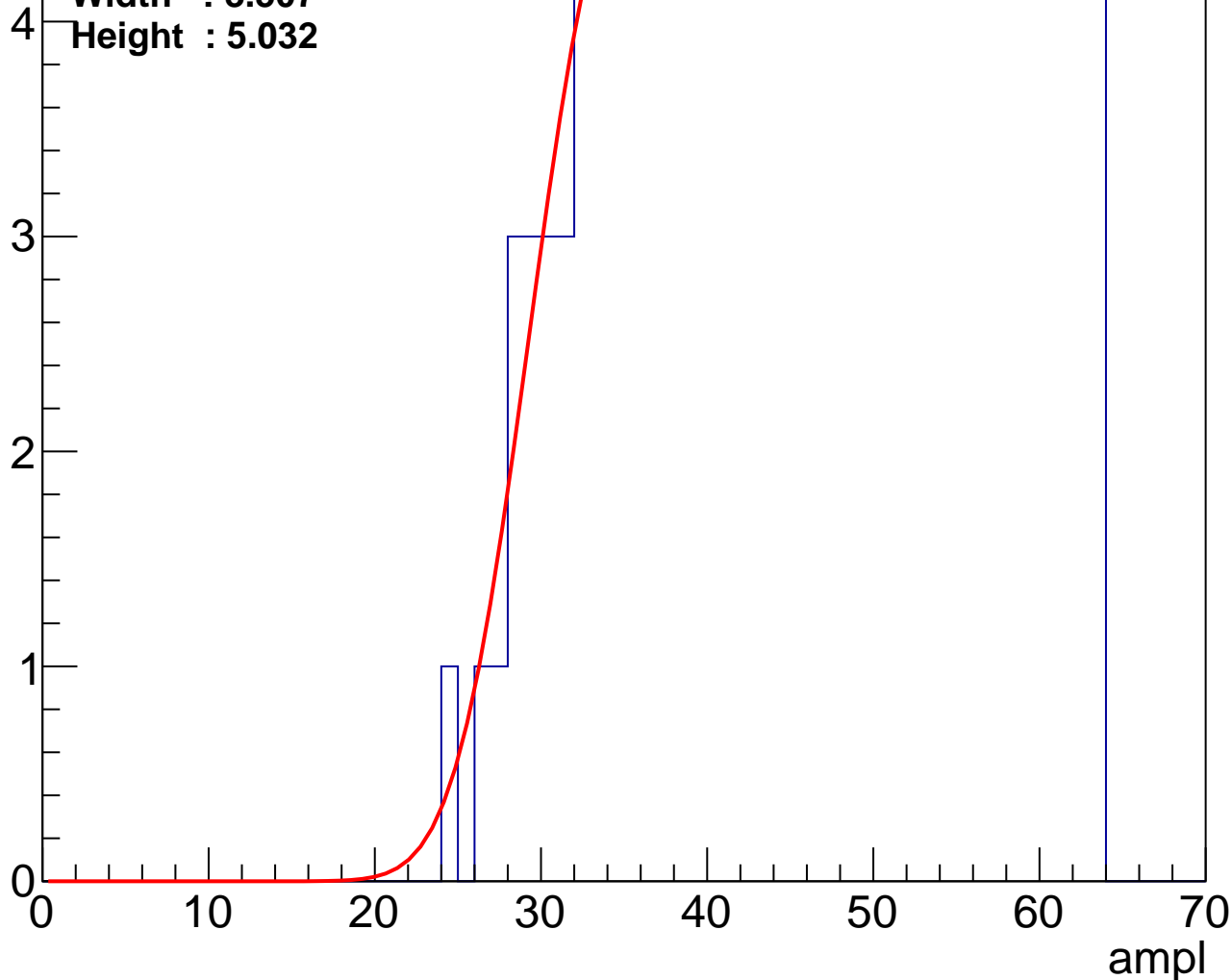
Width : 3.507

Height : 5.032

Entries 175

Mean 45.89

Std Dev 10.29



B1L103S, U15-ch38

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

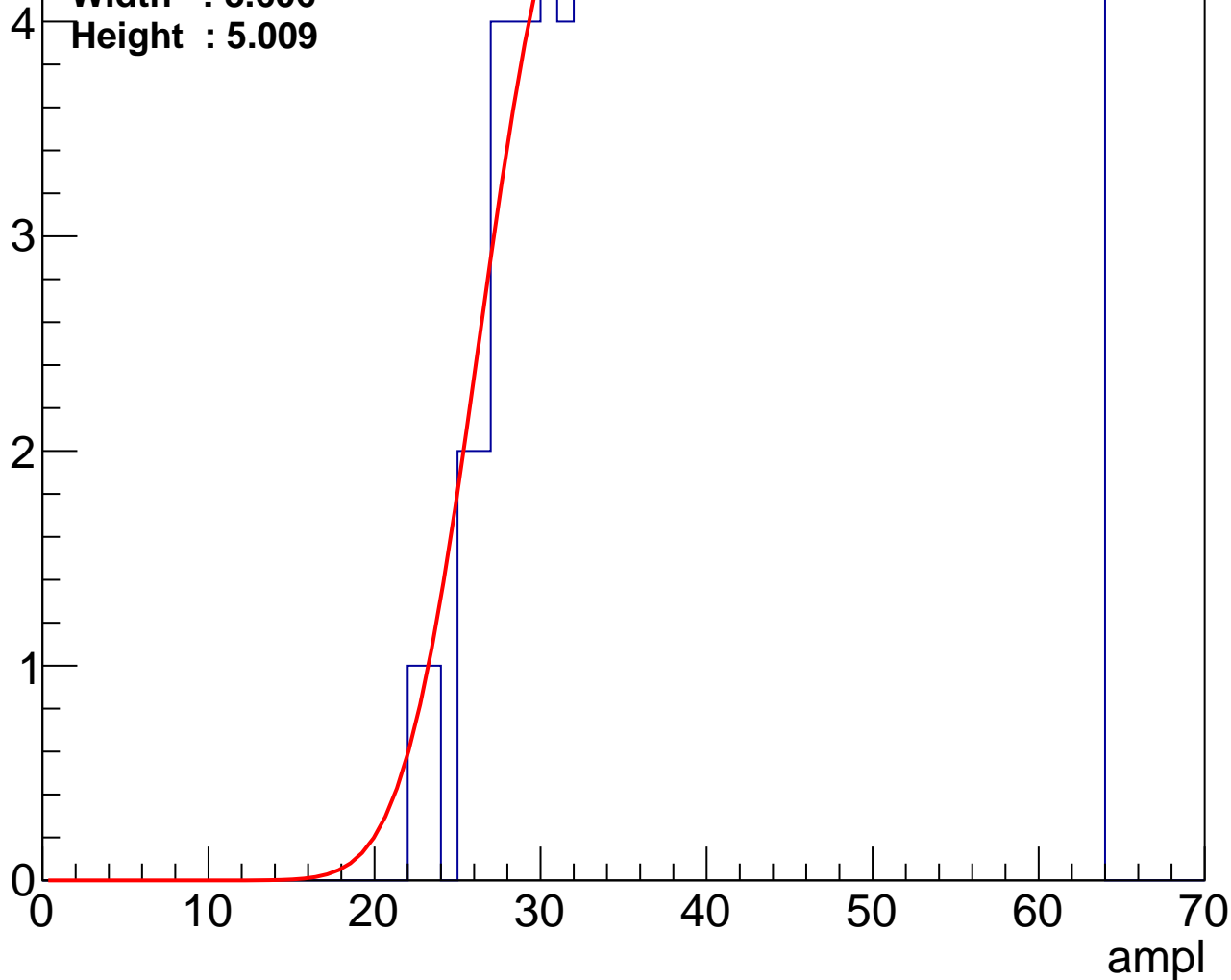
Profile : 00011100

Turn on : 26.2834

Width : 3.606

Height : 5.009

Entries	187
Mean	44.69
Std Dev	10.98



B1L103S, U15-ch39

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.3649

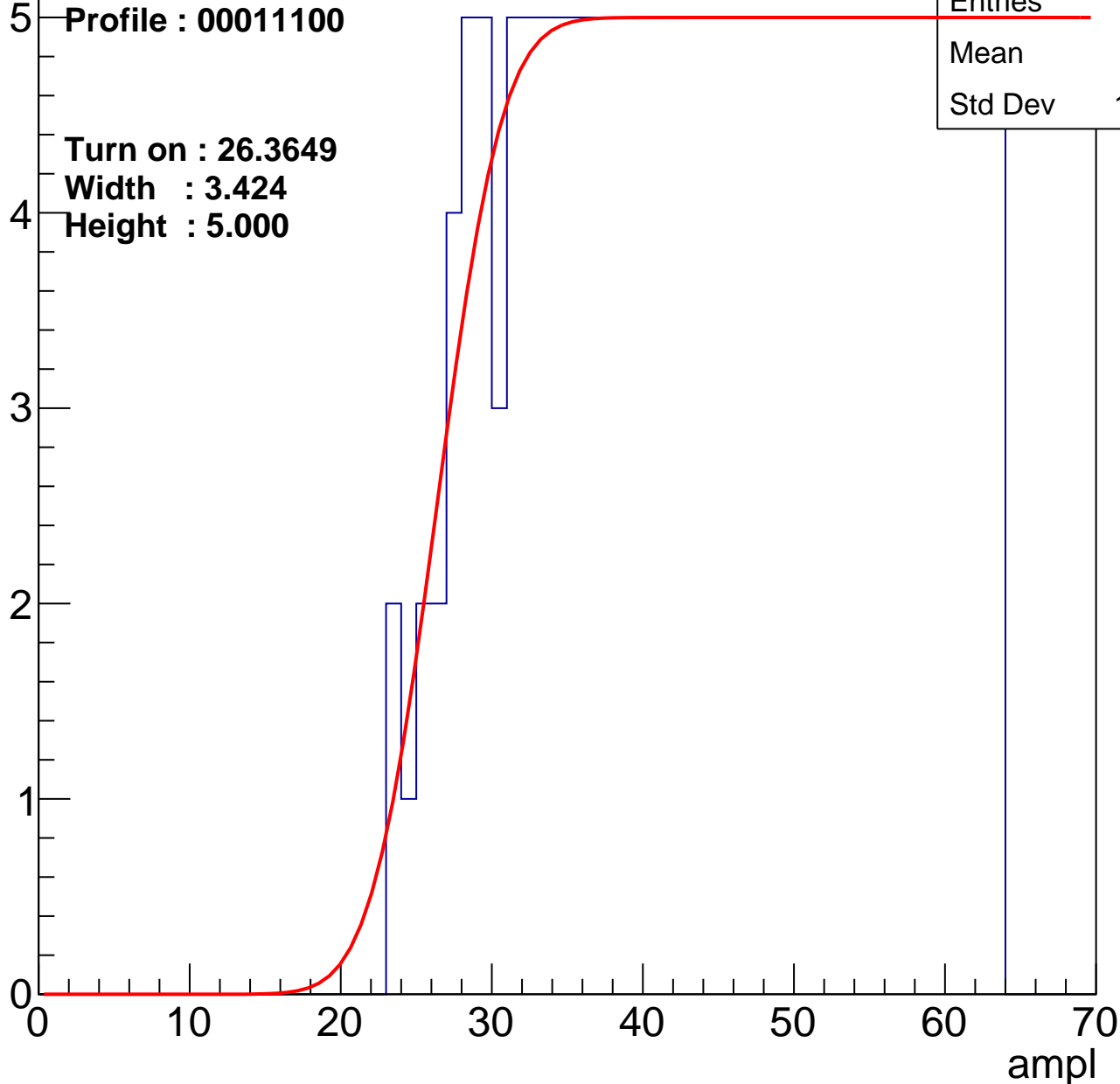
Width : 3.424

Height : 5.000

Entries 189

Mean 44.5

Std Dev 11.08



B1L103S, U15-ch40

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.4035

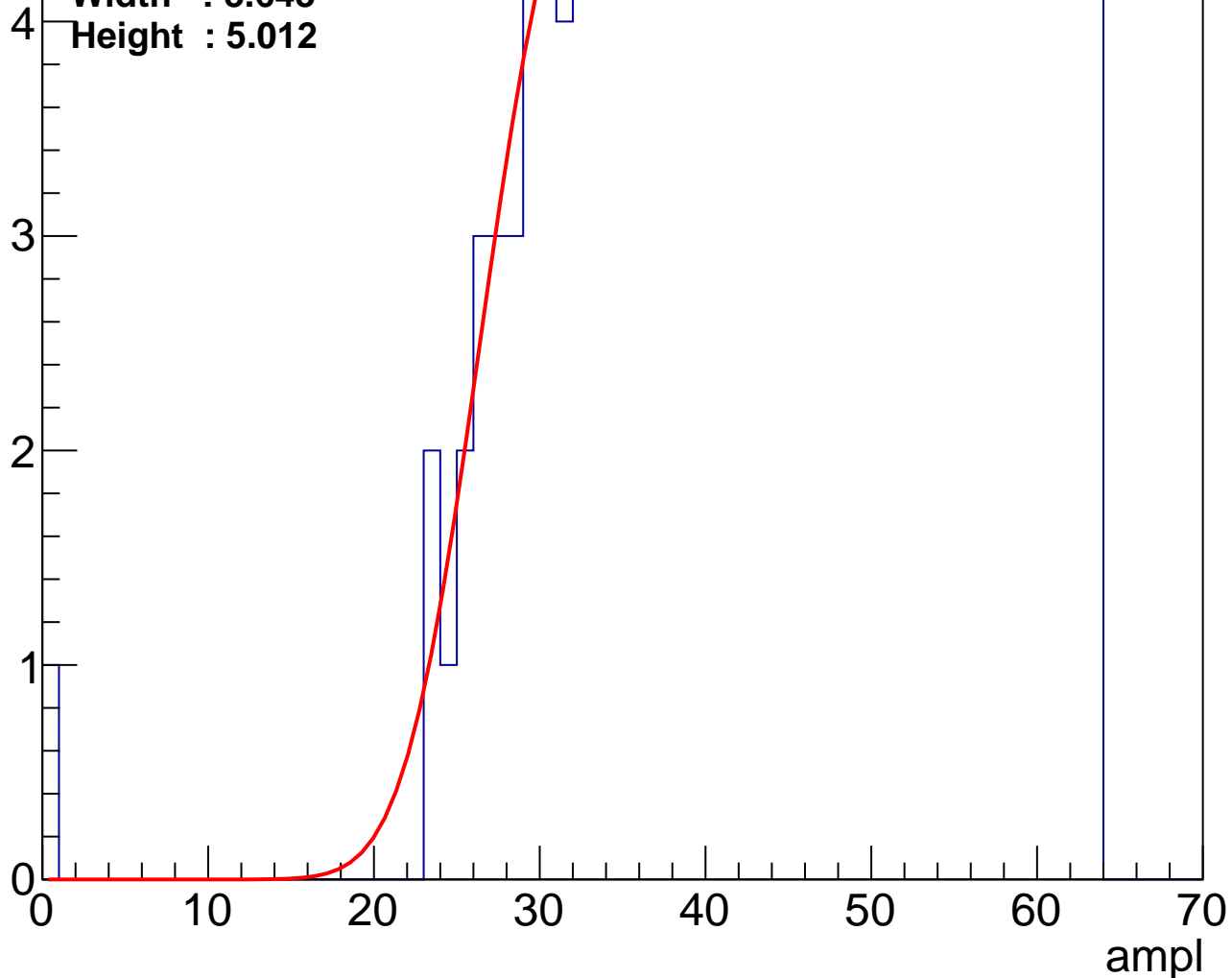
Width : 3.643

Height : 5.012

Entries 189

Mean 44.35

Std Dev 11.48



B1L103S, U15-ch41

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

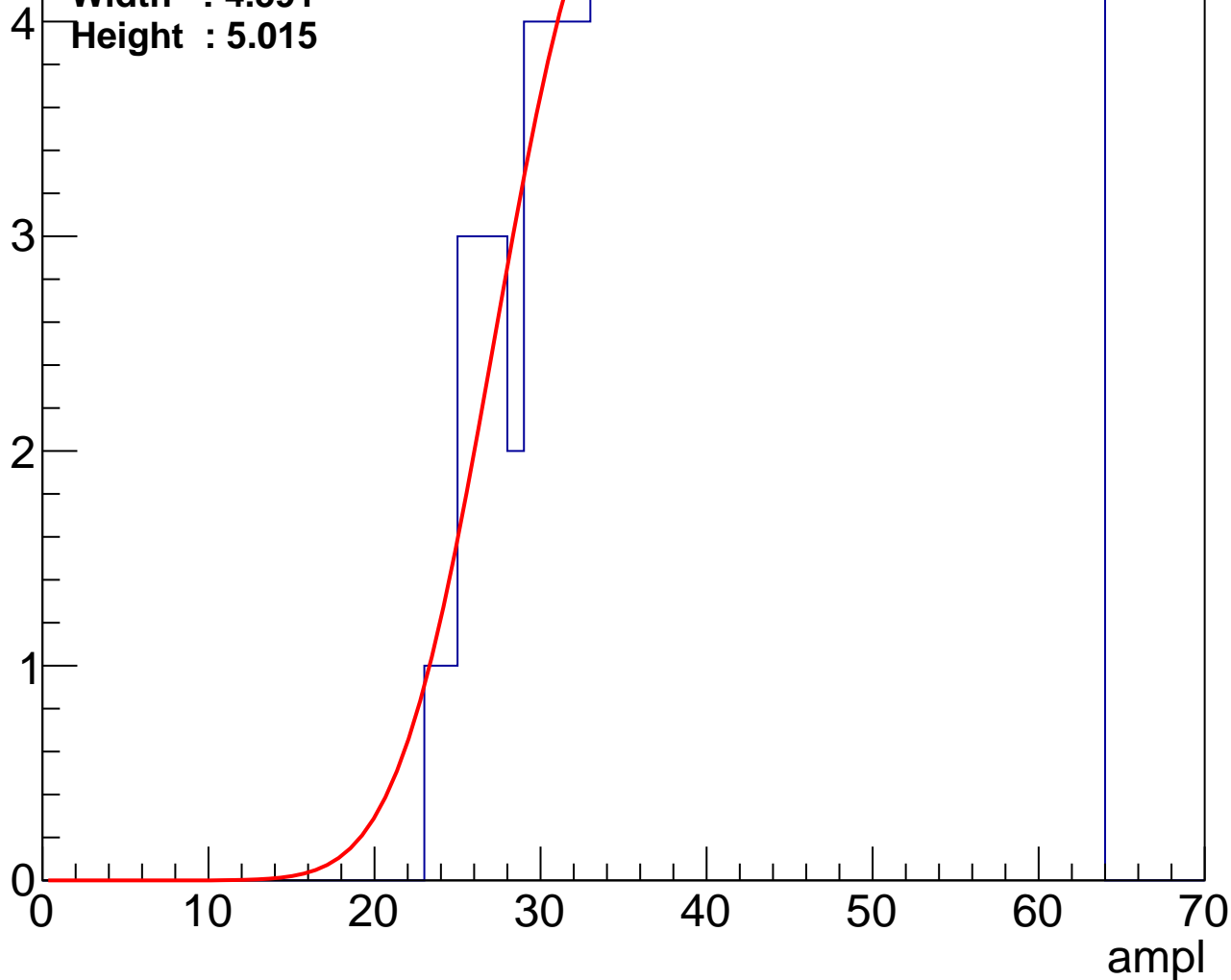
Profile : 00011100

Turn on : 27.1920

Width : 4.591

Height : 5.015

Entries	184
Mean	44.92
Std Dev	10.92



B1L103S, U15-ch42

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.7283

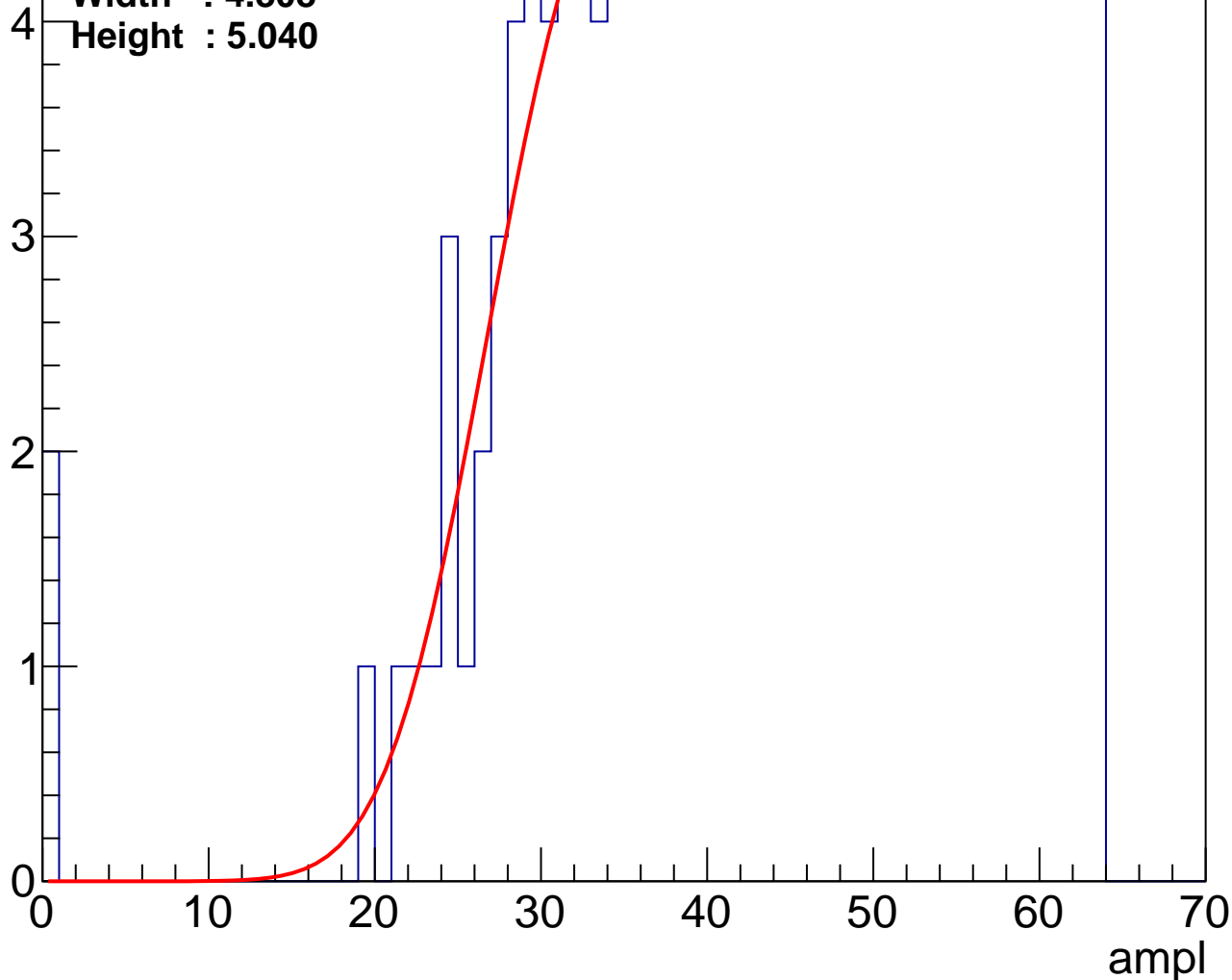
Width : 4.808

Height : 5.040

Entries 192

Mean 43.82

Std Dev 12.14



B1L103S, U15-ch43

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.8501

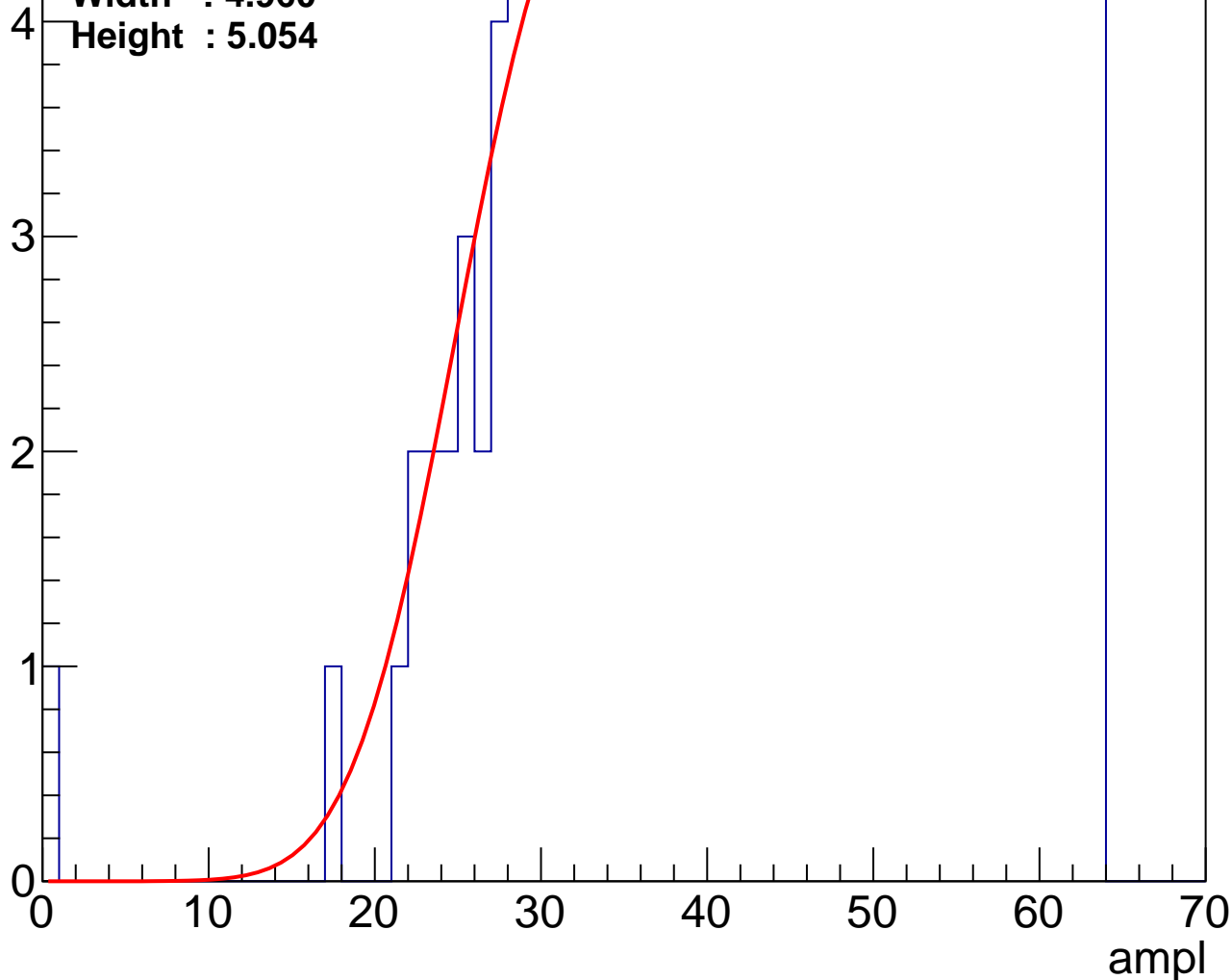
Width : 4.960

Height : 5.054

Entries 198

Mean 43.44

Std Dev 12



B1L103S, U15-ch44

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.3473

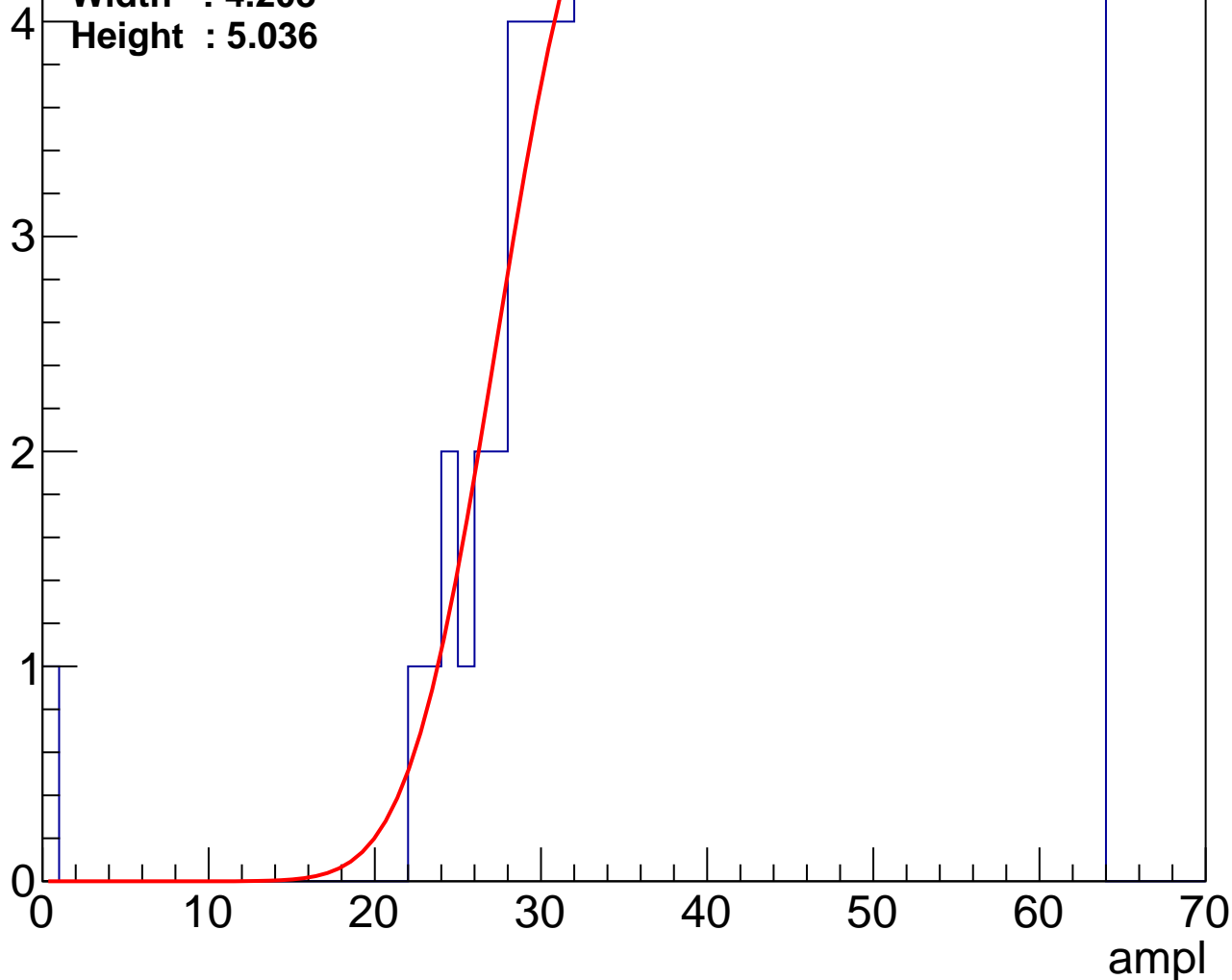
Width : 4.203

Height : 5.036

Entries 186

Mean 44.6

Std Dev 11.4



B1L103S, U15-ch45

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.2407

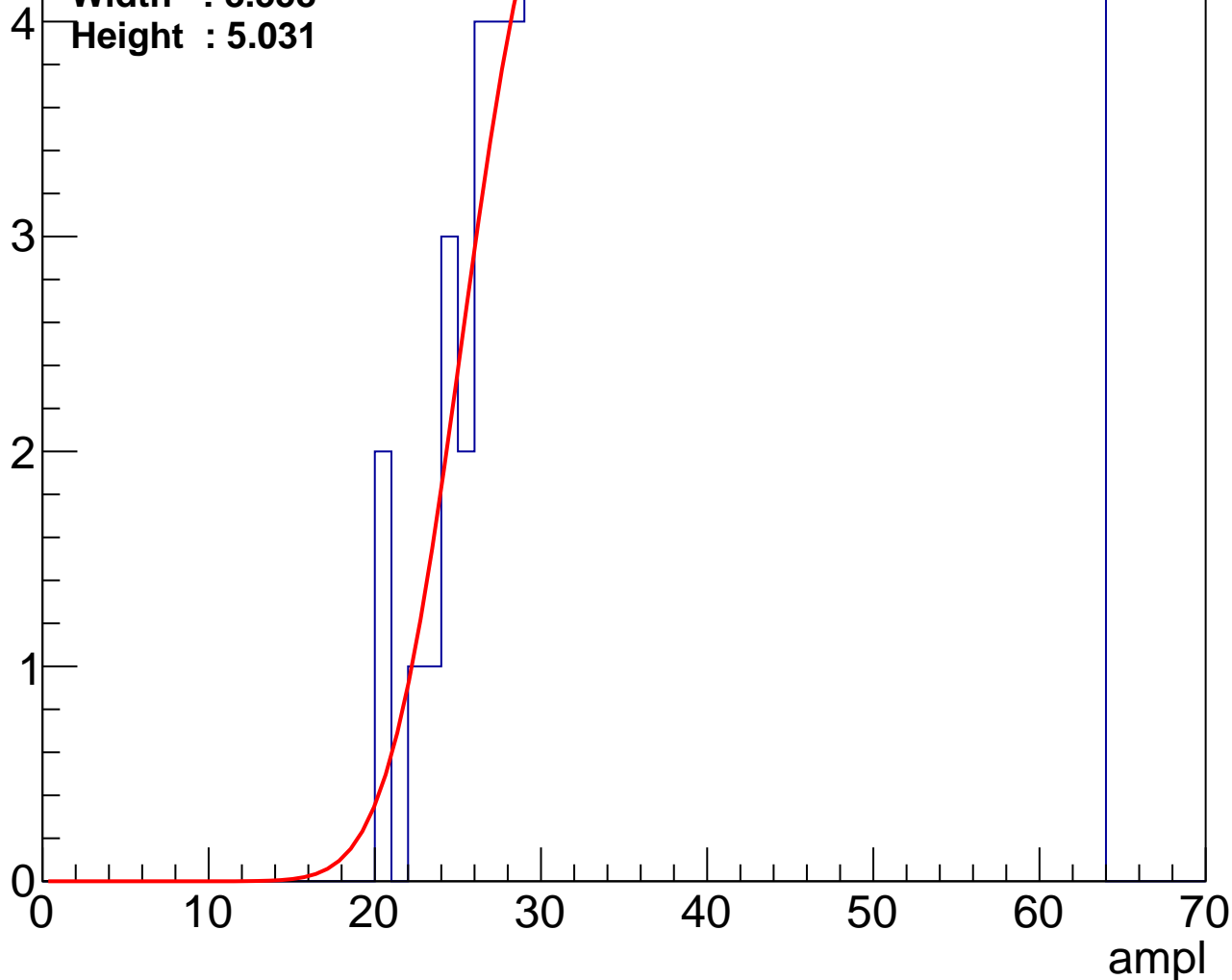
Width : 3.558

Height : 5.031

Entries 196

Mean 43.78

Std Dev 11.52



B1L103S, U15-ch46

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.7866

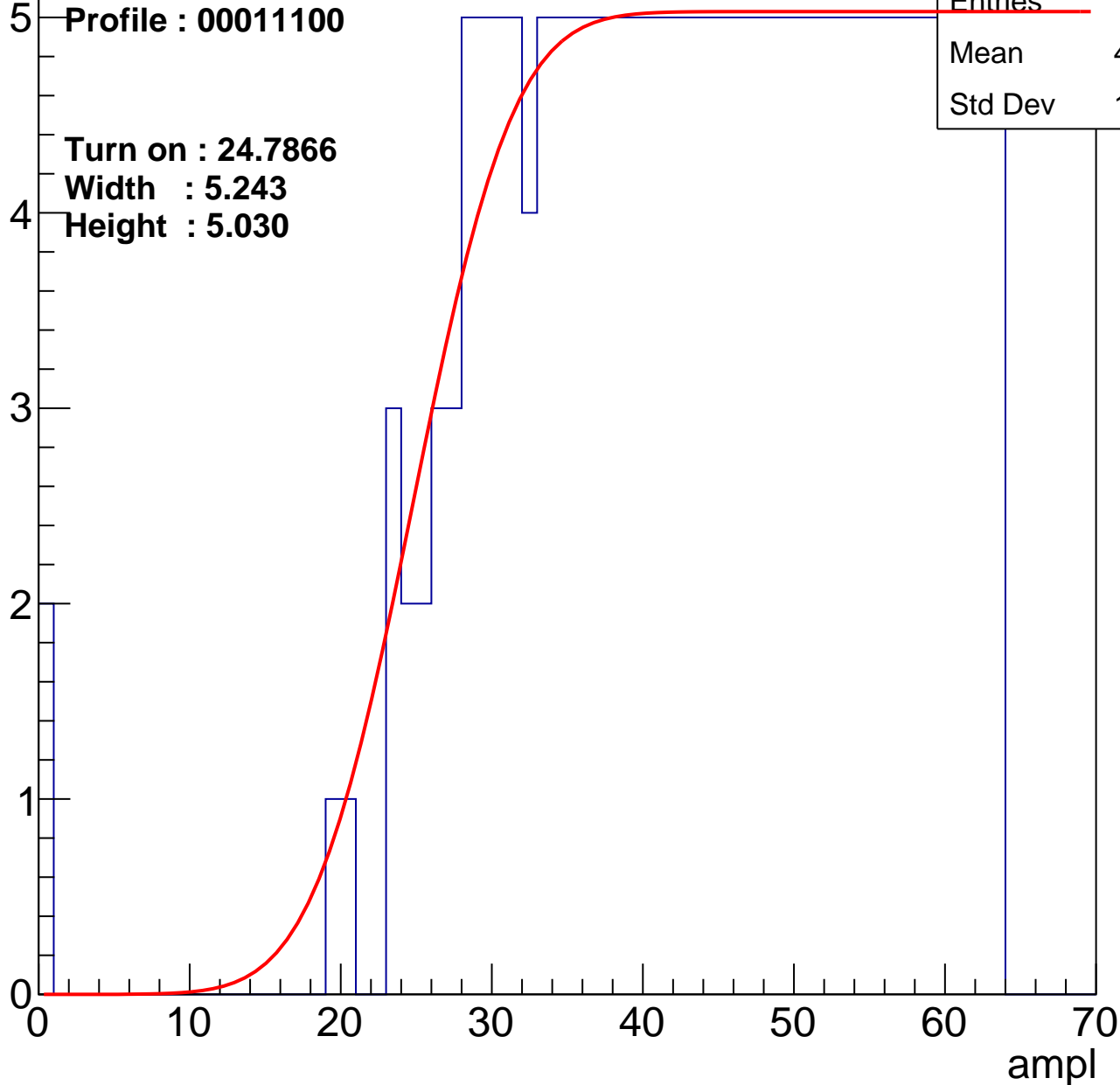
Width : 5.243

Height : 5.030

Entries 196

Mean 43.48

Std Dev 12.25



B1L103S, U15-ch47

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.0704

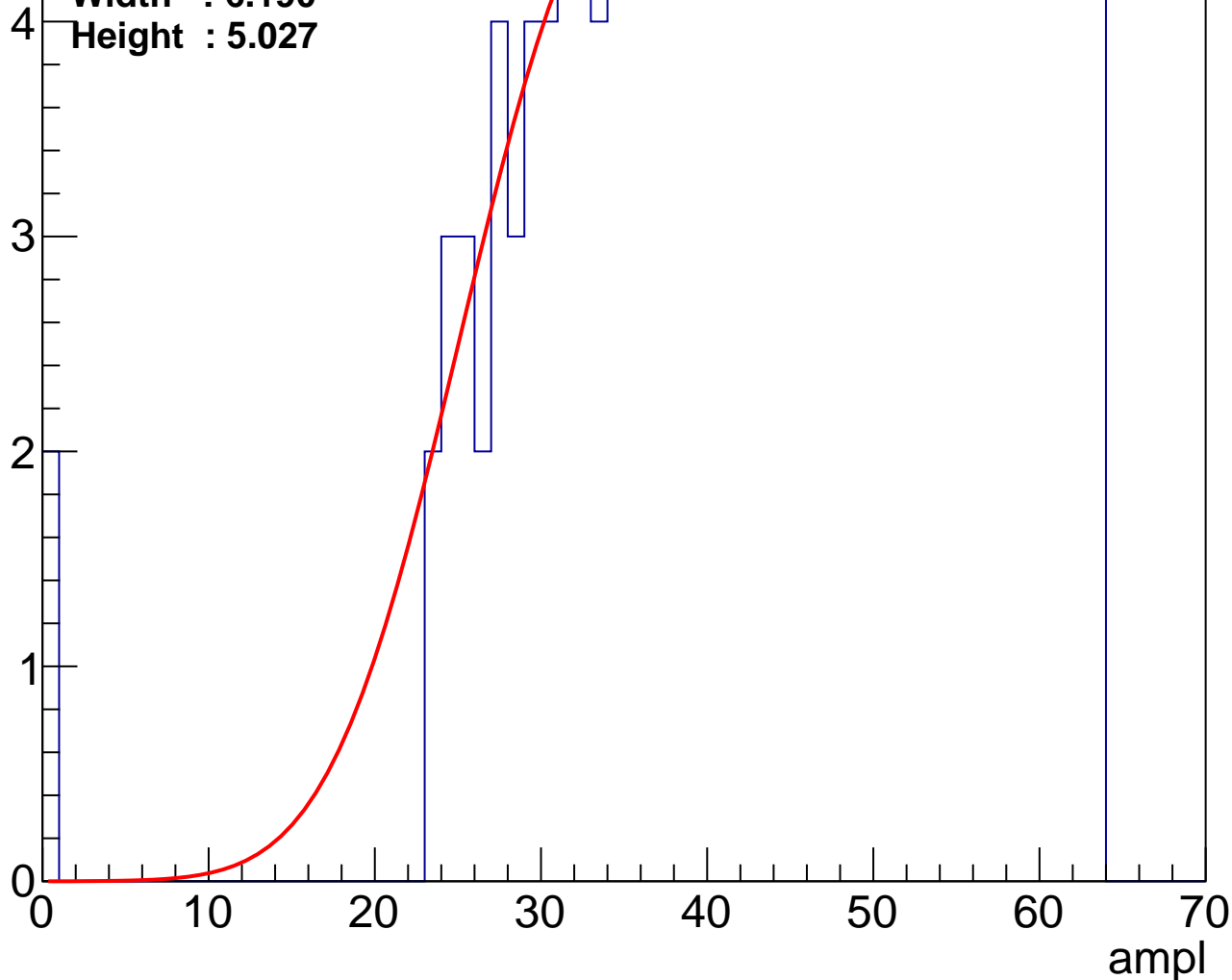
Width : 6.190

Height : 5.027

Entries 191

Mean 43.95

Std Dev 12.02



B1L103S, U15-ch48

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

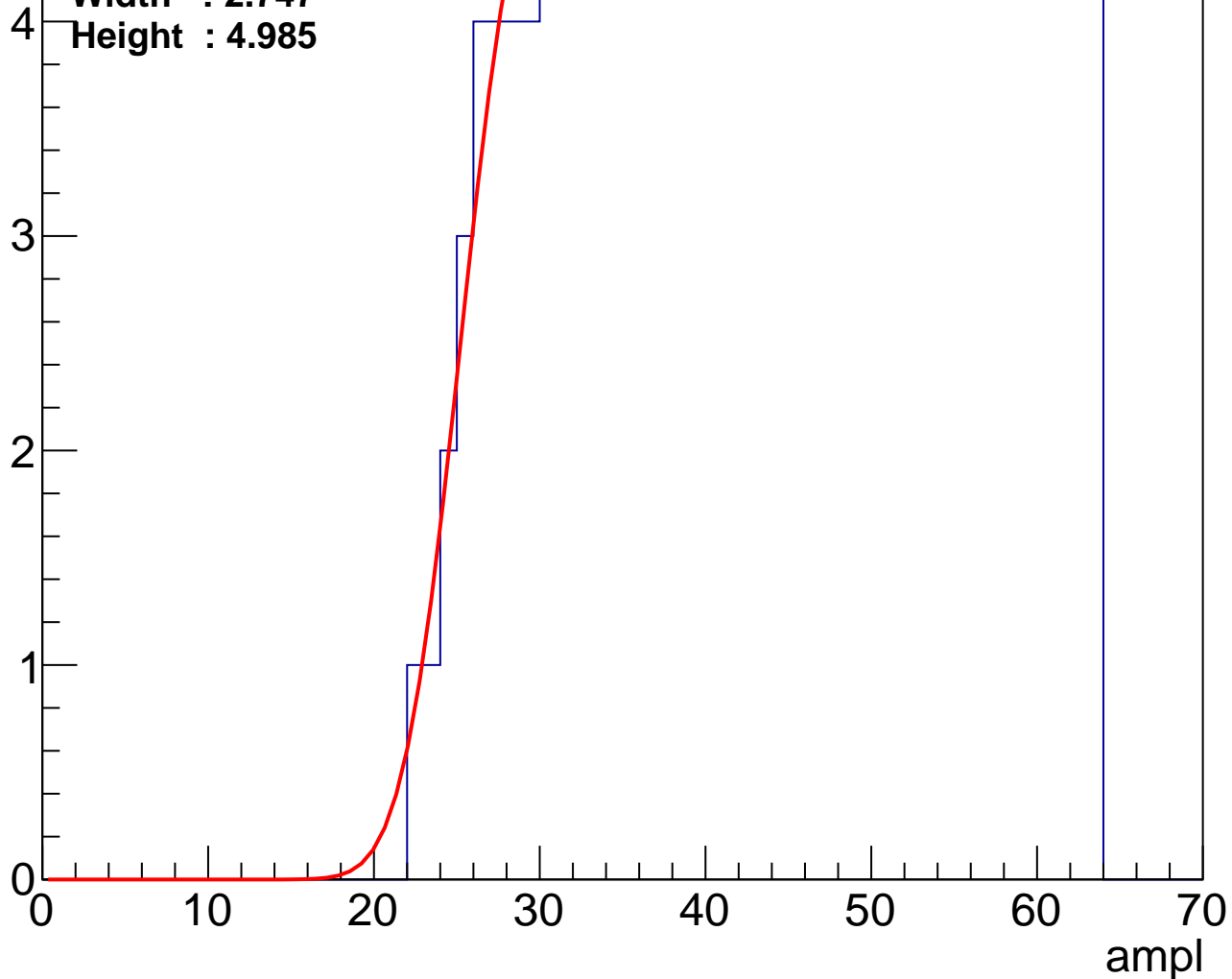
Profile : 00011100

Turn on : 25.2112

Width : 2.747

Height : 4.985

Entries	193
Mean	44.11
Std Dev	11.29



B1L103S, U15-ch49

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.5609

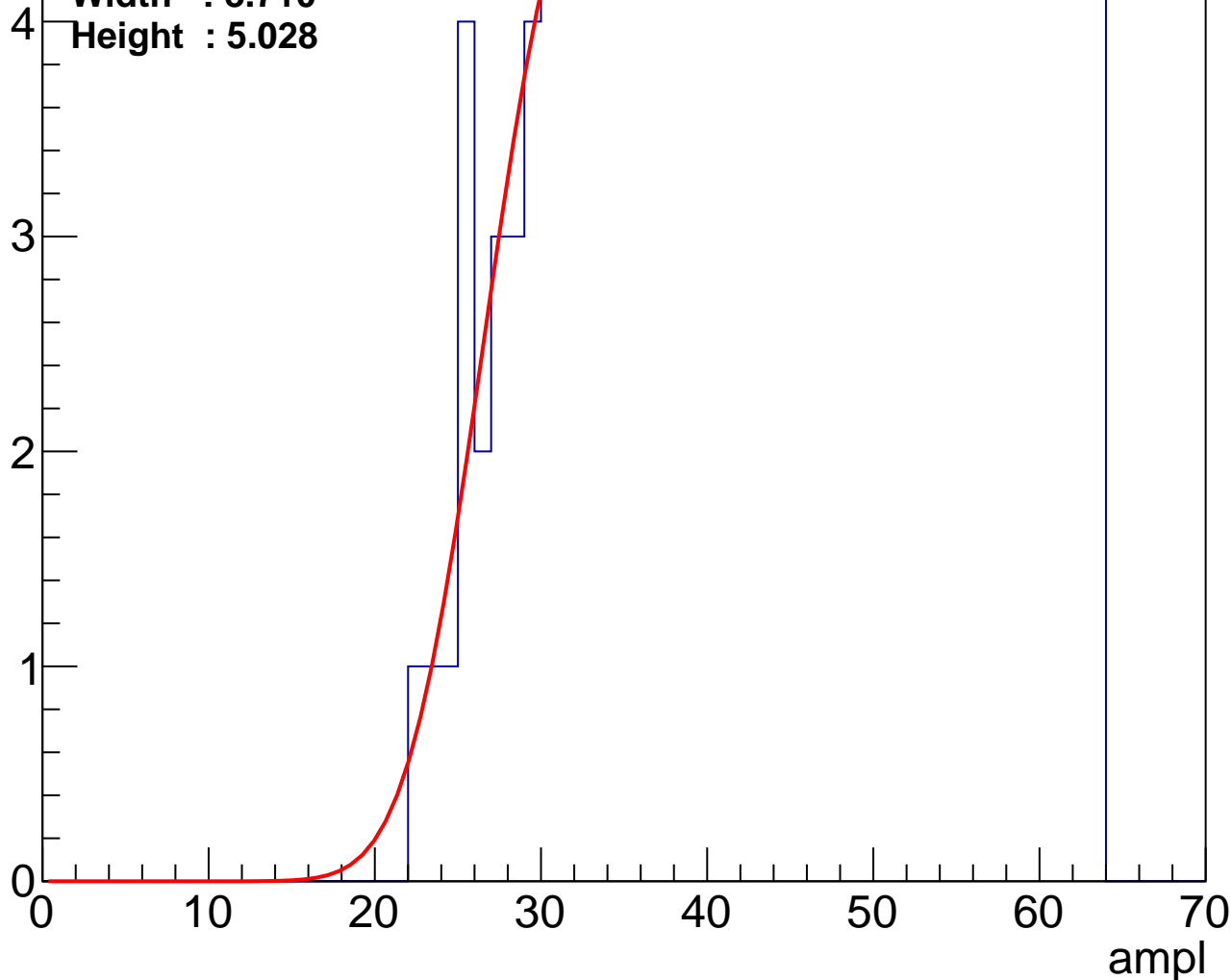
Width : 3.710

Height : 5.028

Entries 189

Mean 44.48

Std Dev 11.11



B1L103S, U15-ch50

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

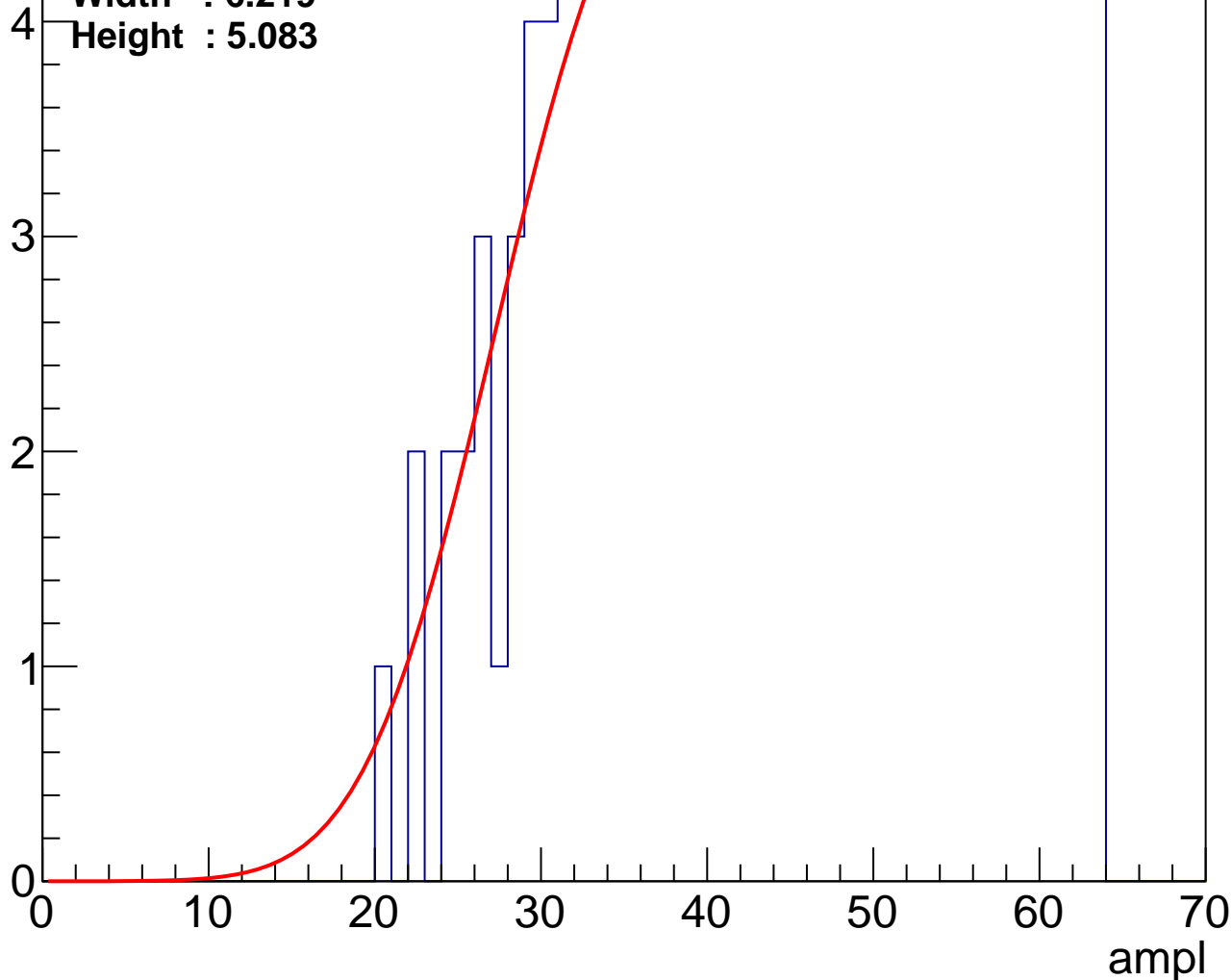
Profile : 00011100

Turn on : 27.2007

Width : 6.219

Height : 5.083

Entries	187
Mean	44.61
Std Dev	11.13



B1L103S, U15-ch51

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 28.4808

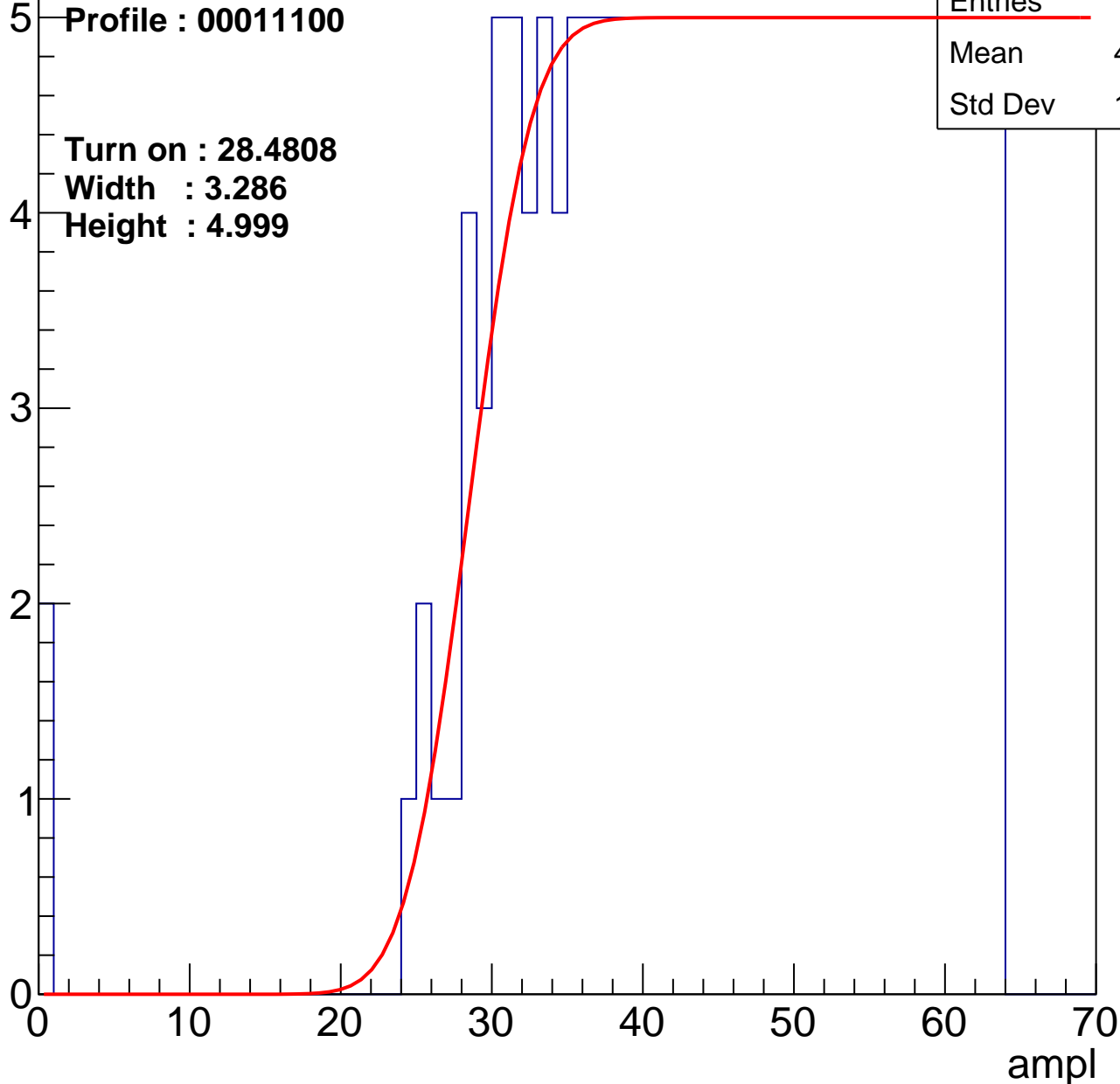
Width : 3.286

Height : 4.999

Entries 182

Mean 44.86

Std Dev 11.57



B1L103S, U15-ch52

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.5815

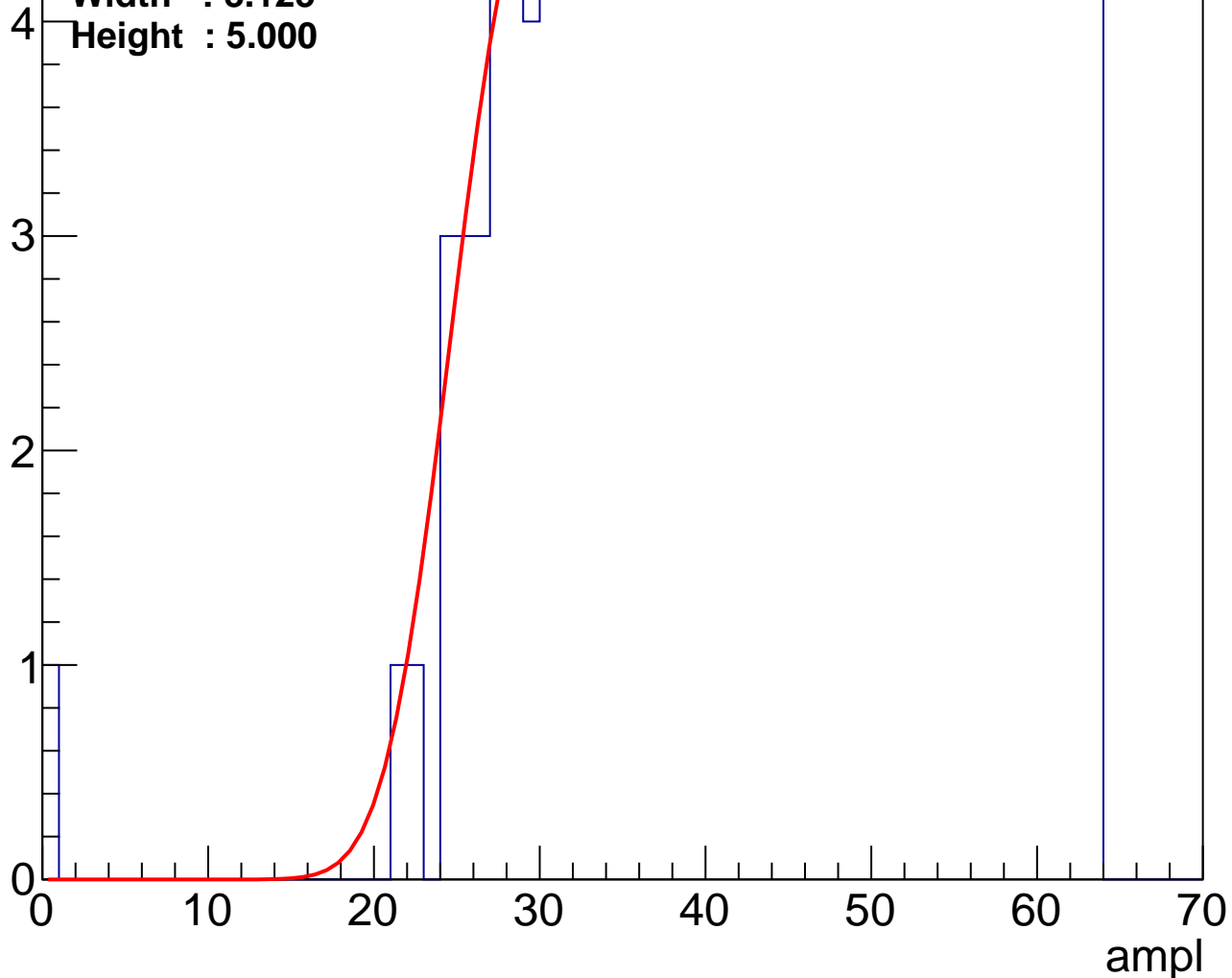
Width : 3.125

Height : 5.000

Entries 196

Mean 43.69

Std Dev 11.79



B1L103S, U15-ch53

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

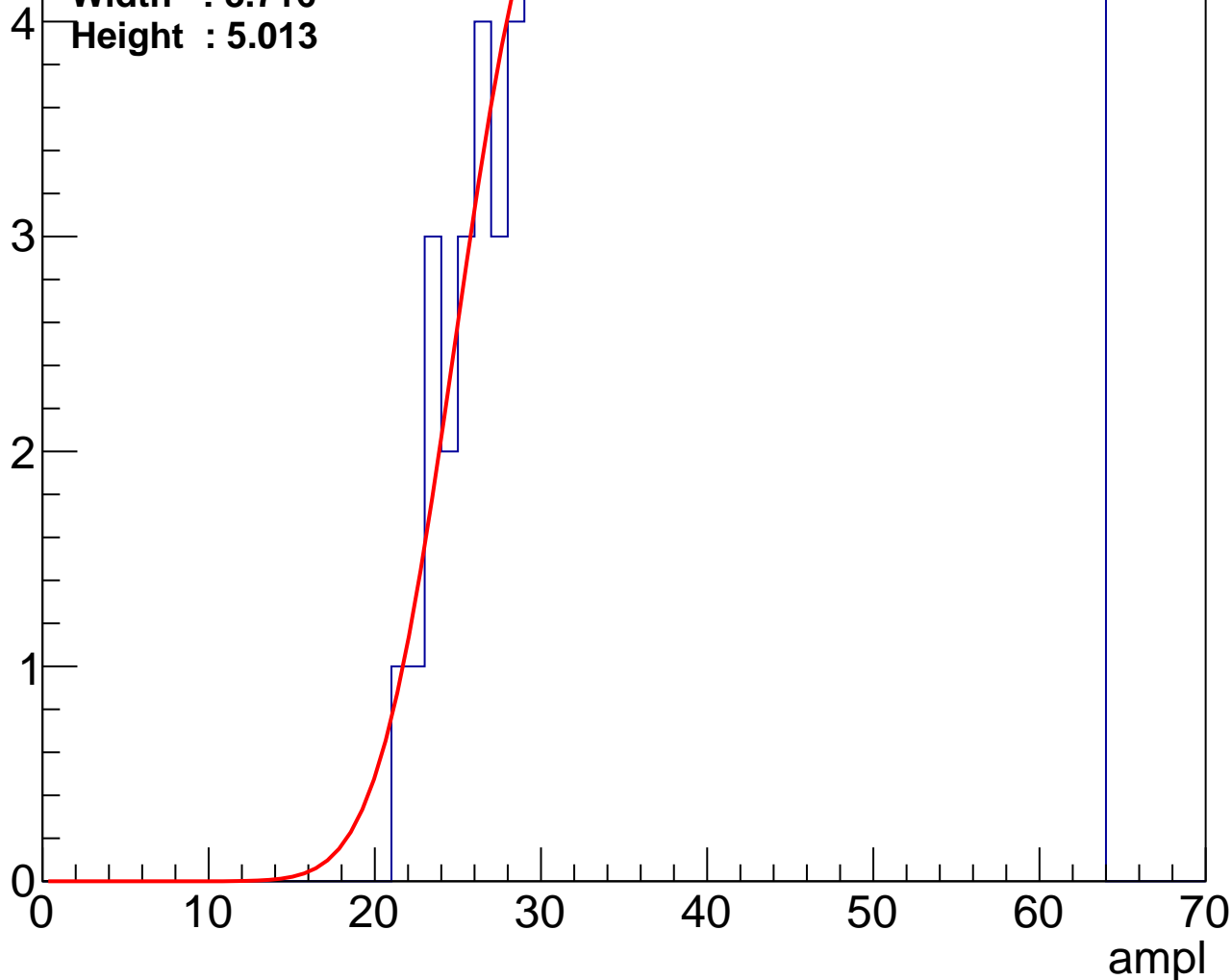
Profile : 00011100

Turn on : 24.8288

Width : 3.716

Height : 5.013

Entries	196
Mean	43.79
Std Dev	11.51



B1L103S, U15-ch54

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.1754

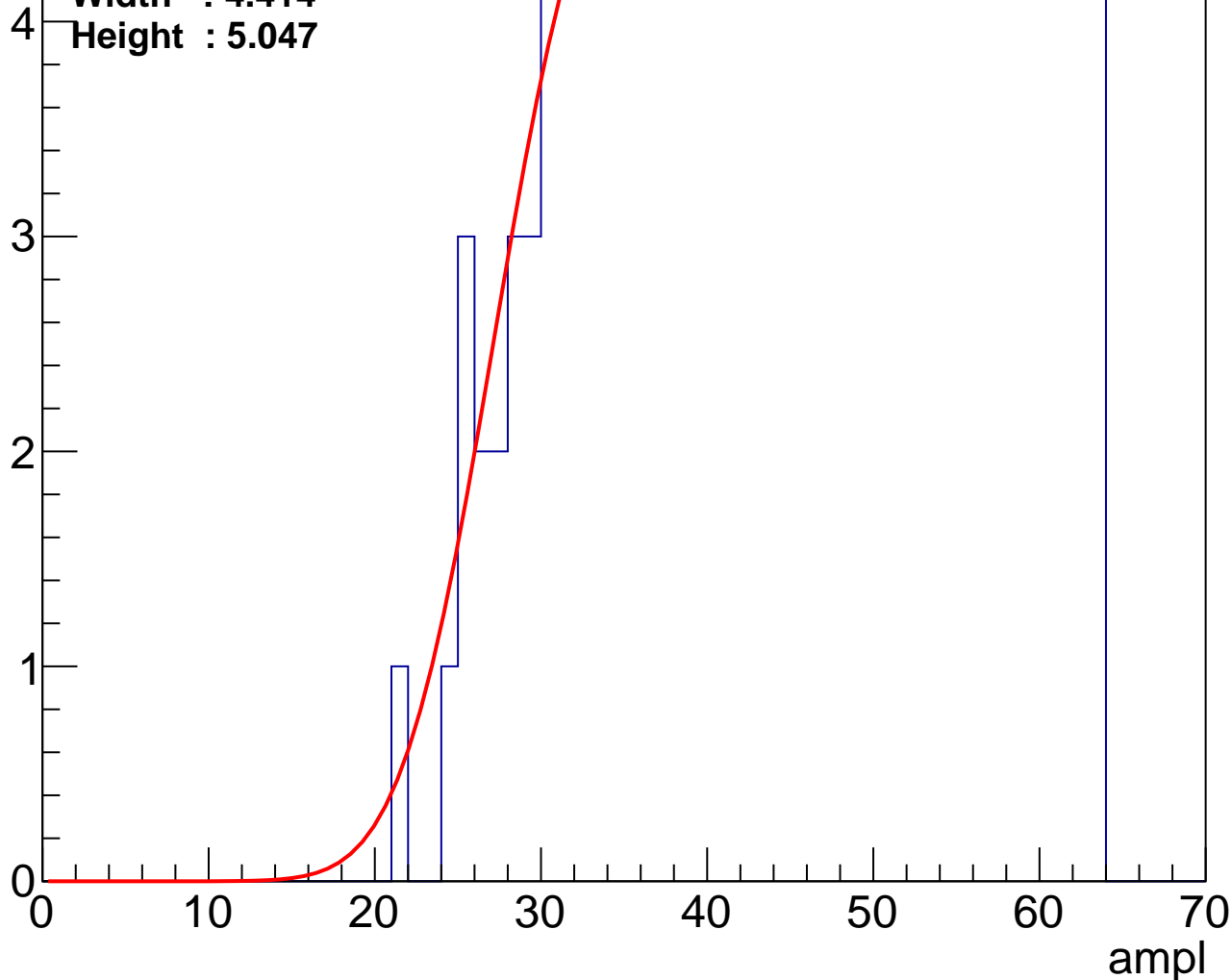
Width : 4.414

Height : 5.047

Entries 185

Mean 44.88

Std Dev 10.9



B1L103S, U15-ch55

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.5153

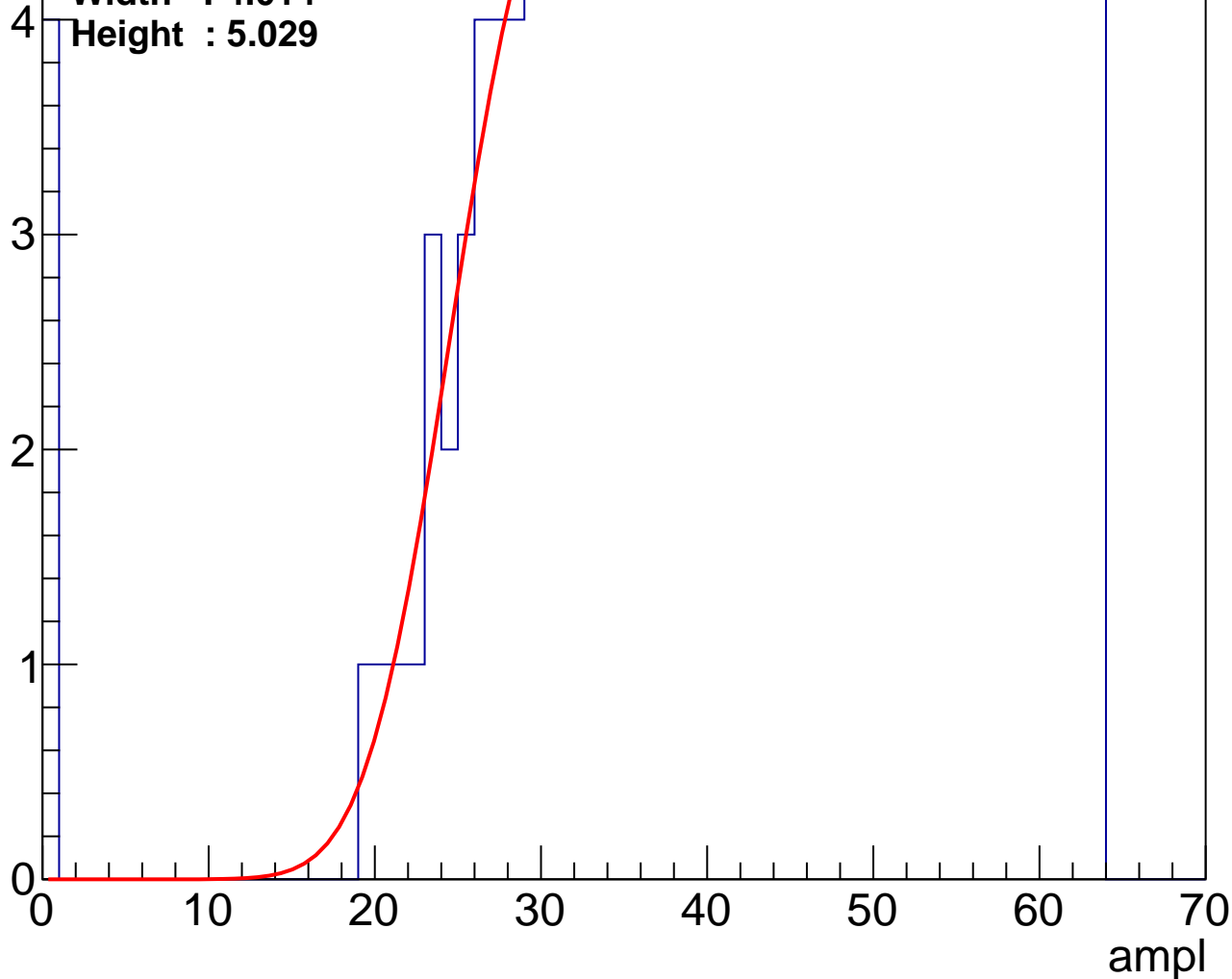
Width : 4.014

Height : 5.029

Entries 203

Mean 42.6

Std Dev 13.09



B1L103S, U15-ch56

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

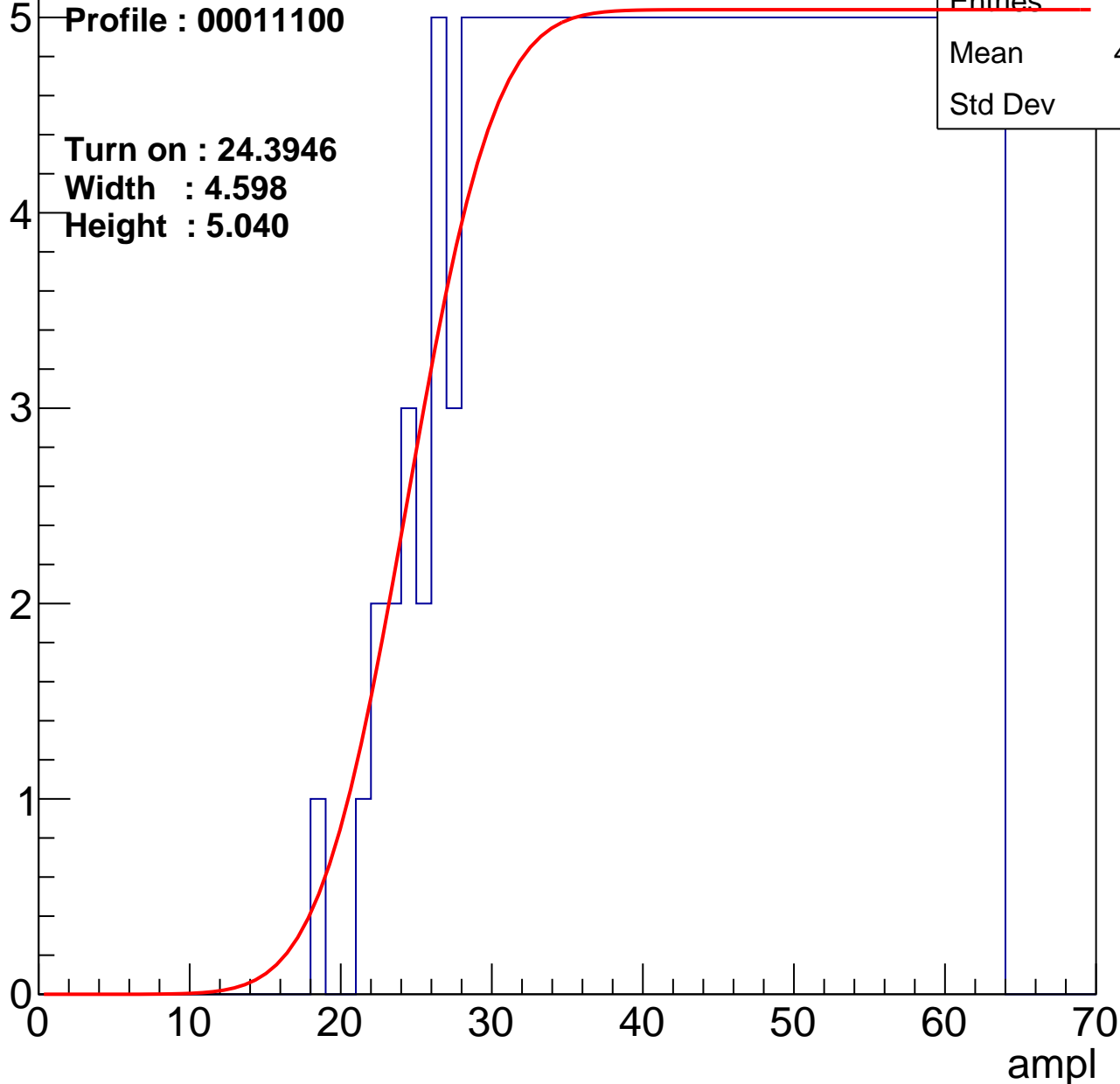
Profile : 00011100

Turn on : 24.3946

Width : 4.598

Height : 5.040

Entries	199
Mean	43.48
Std Dev	11.7



B1L103S, U15-ch57

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

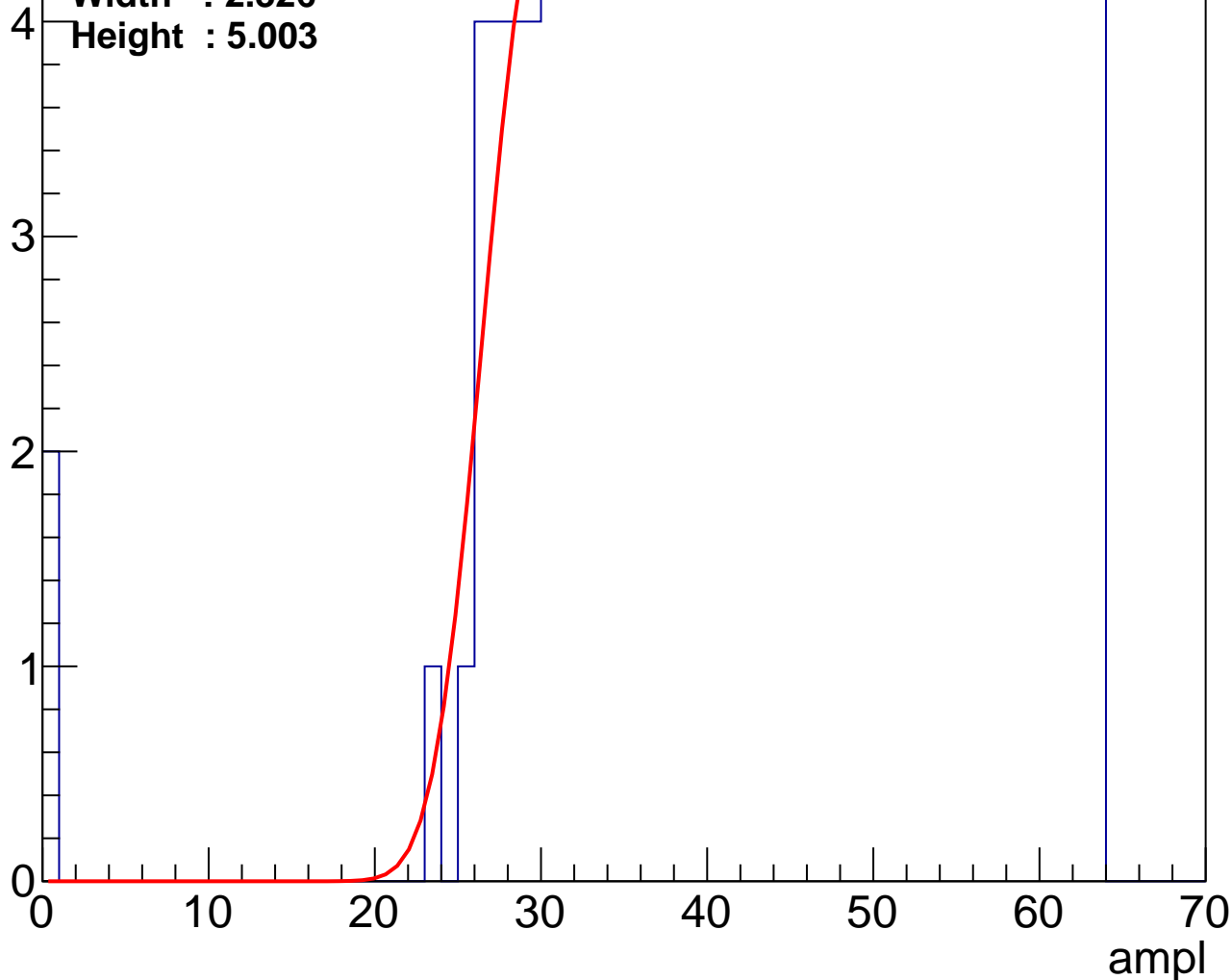
Profile : 00011100

Turn on : 26.4382

Width : 2.326

Height : 5.003

Entries	190
Mean	44.17
Std Dev	11.8



B1L103S, U15-ch58

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

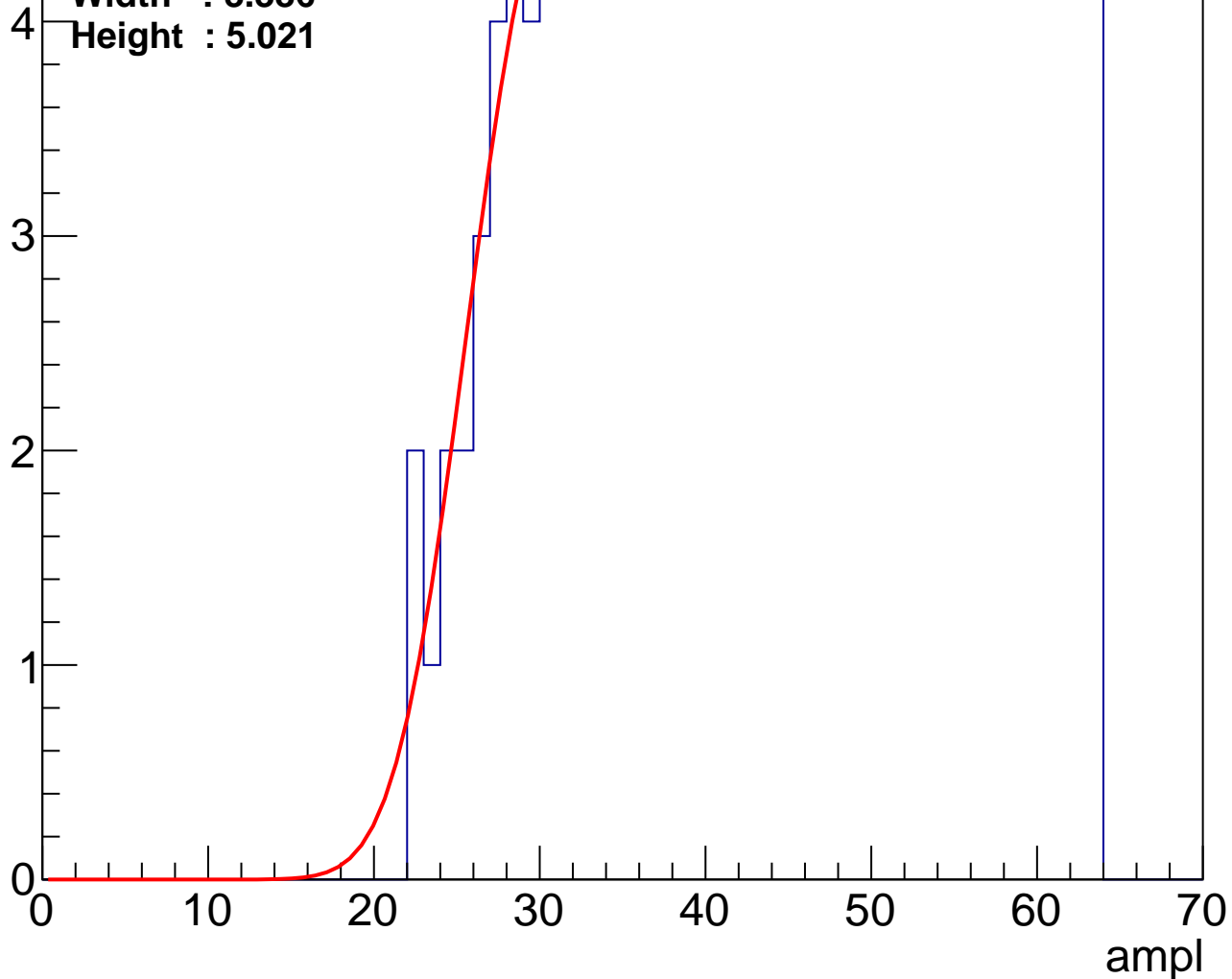
Profile : 00011100

Turn on : 25.5278

Width : 3.386

Height : 5.021

Entries	193
Mean	44.1
Std Dev	11.3



B1L103S, U15-ch59

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

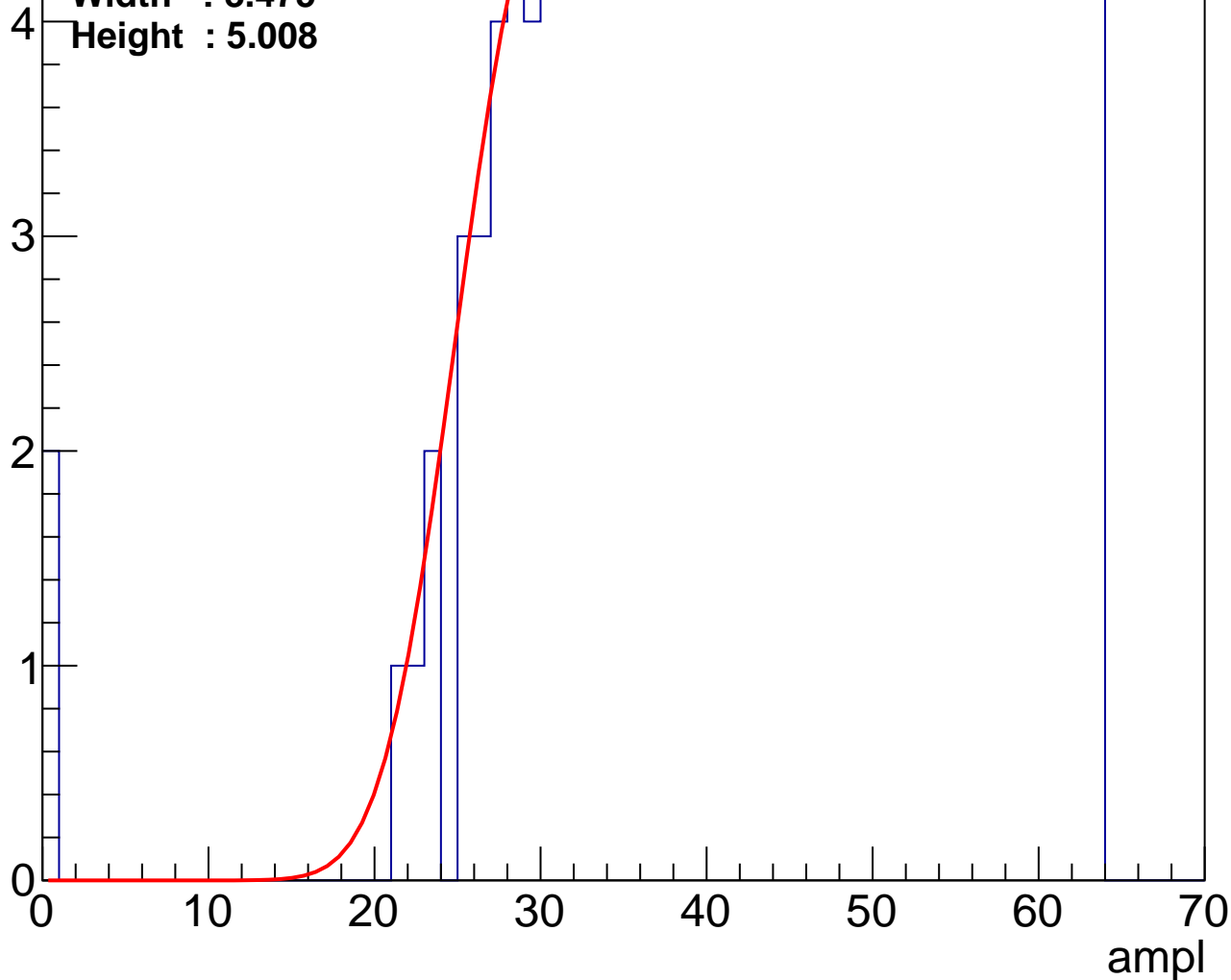
Profile : 00011100

Turn on : 24.8480

Width : 3.475

Height : 5.008

Entries	195
Mean	43.65
Std Dev	12.1



B1L103S, U15-ch60

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

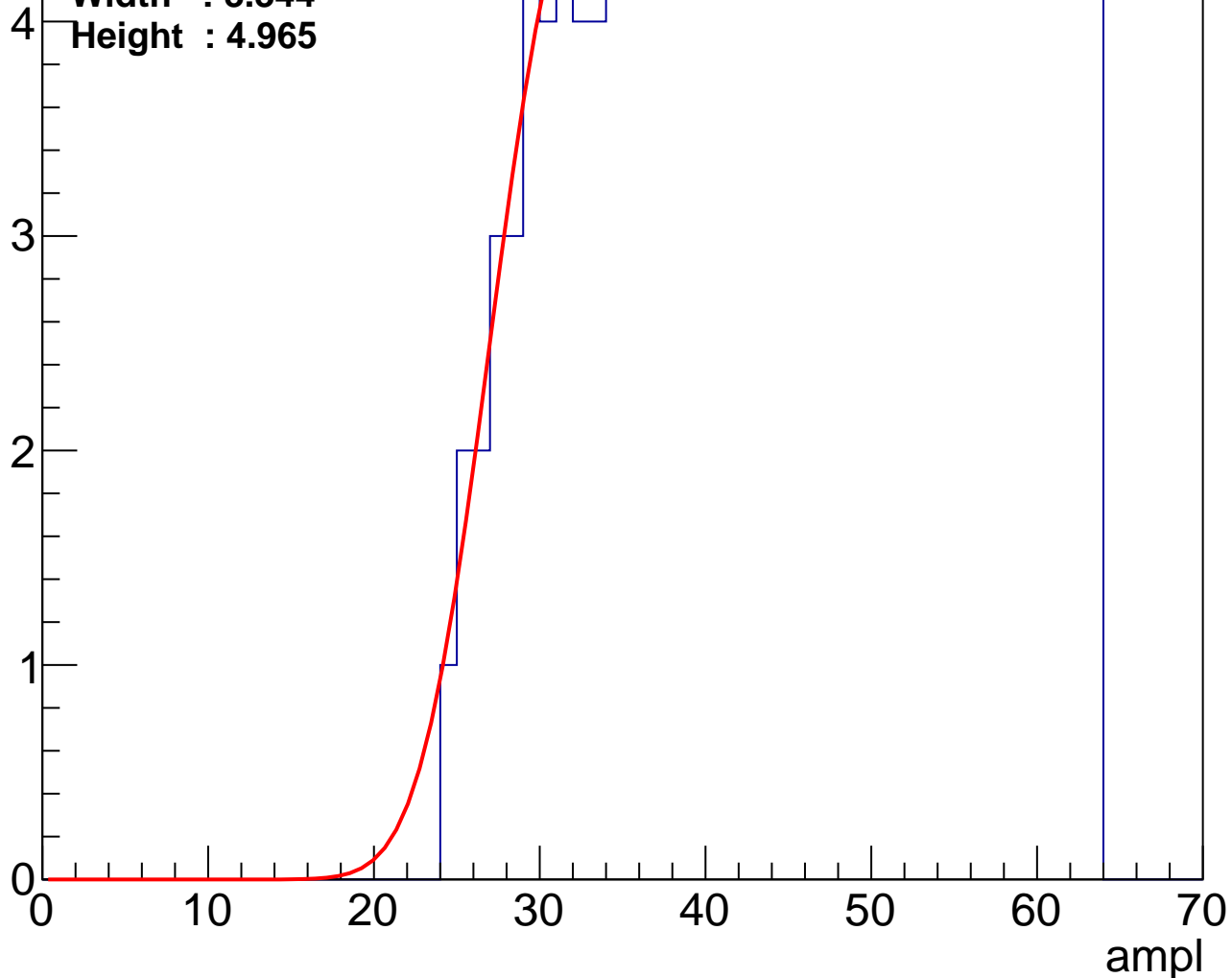
Profile : 00011100

Turn on : 26.9588

Width : 3.344

Height : 4.965

Entries	183
Mean	45.06
Std Dev	10.79



B1L103S, U15-ch61

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

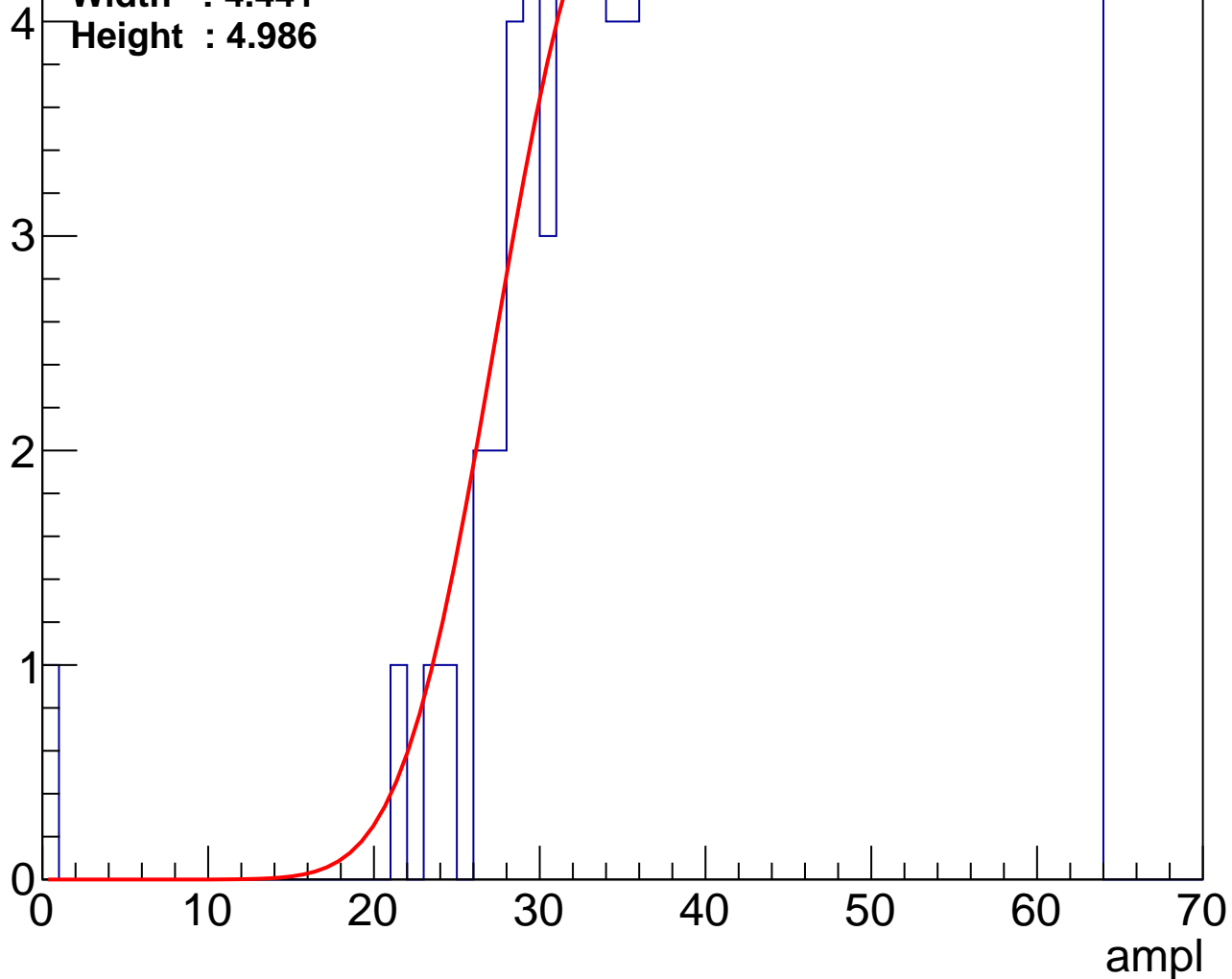
Profile : 00011100

Turn on : 27.2669

Width : 4.441

Height : 4.986

Entries	183
Mean	44.85
Std Dev	11.31



B1L103S, U15-ch62

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.2431

Width : 4.220

Height : 5.059

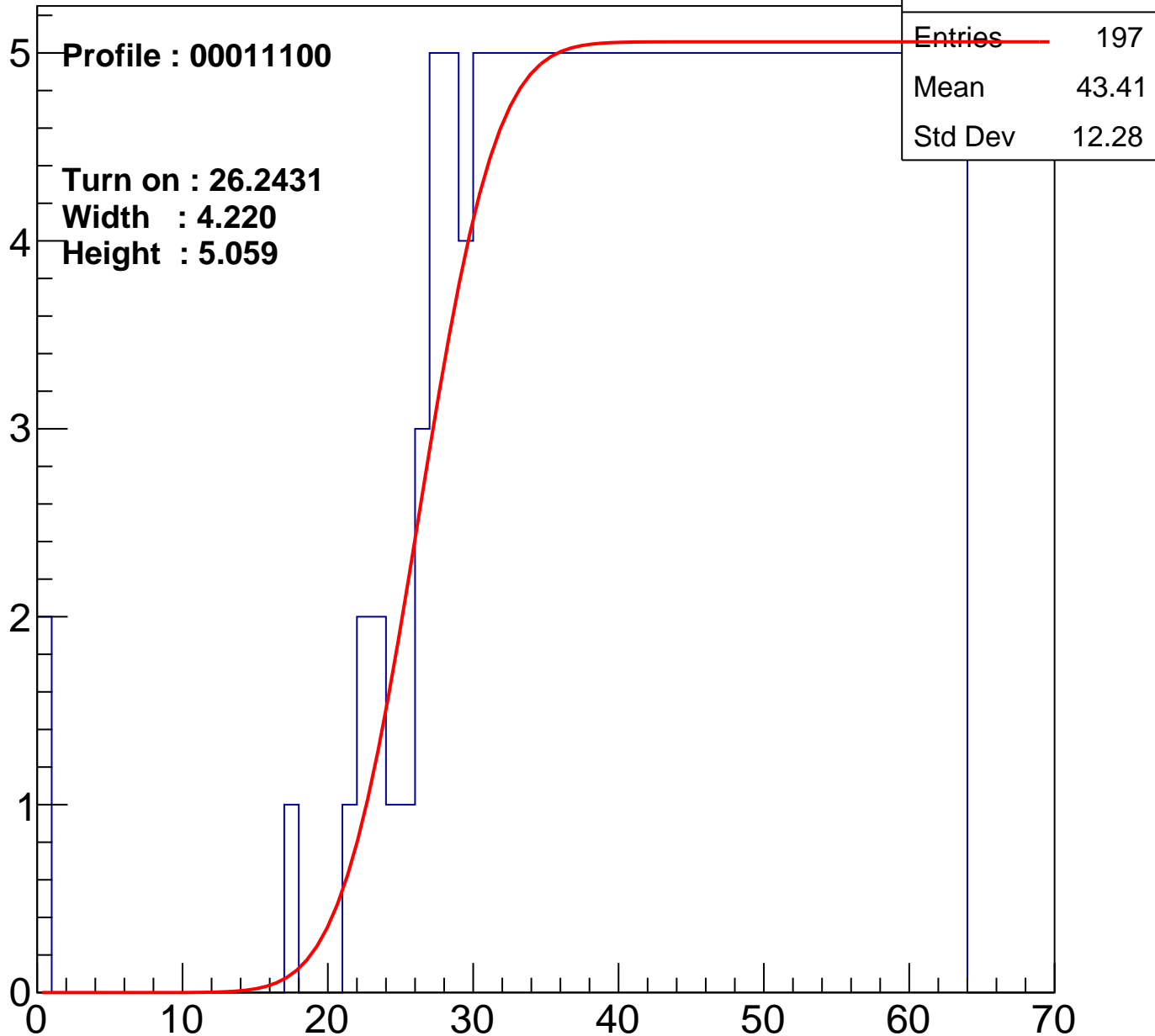
Entries 197

Mean 43.41

Std Dev 12.28

5
4
3
2
1
0

ampl



B1L103S, U15-ch63

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.4216

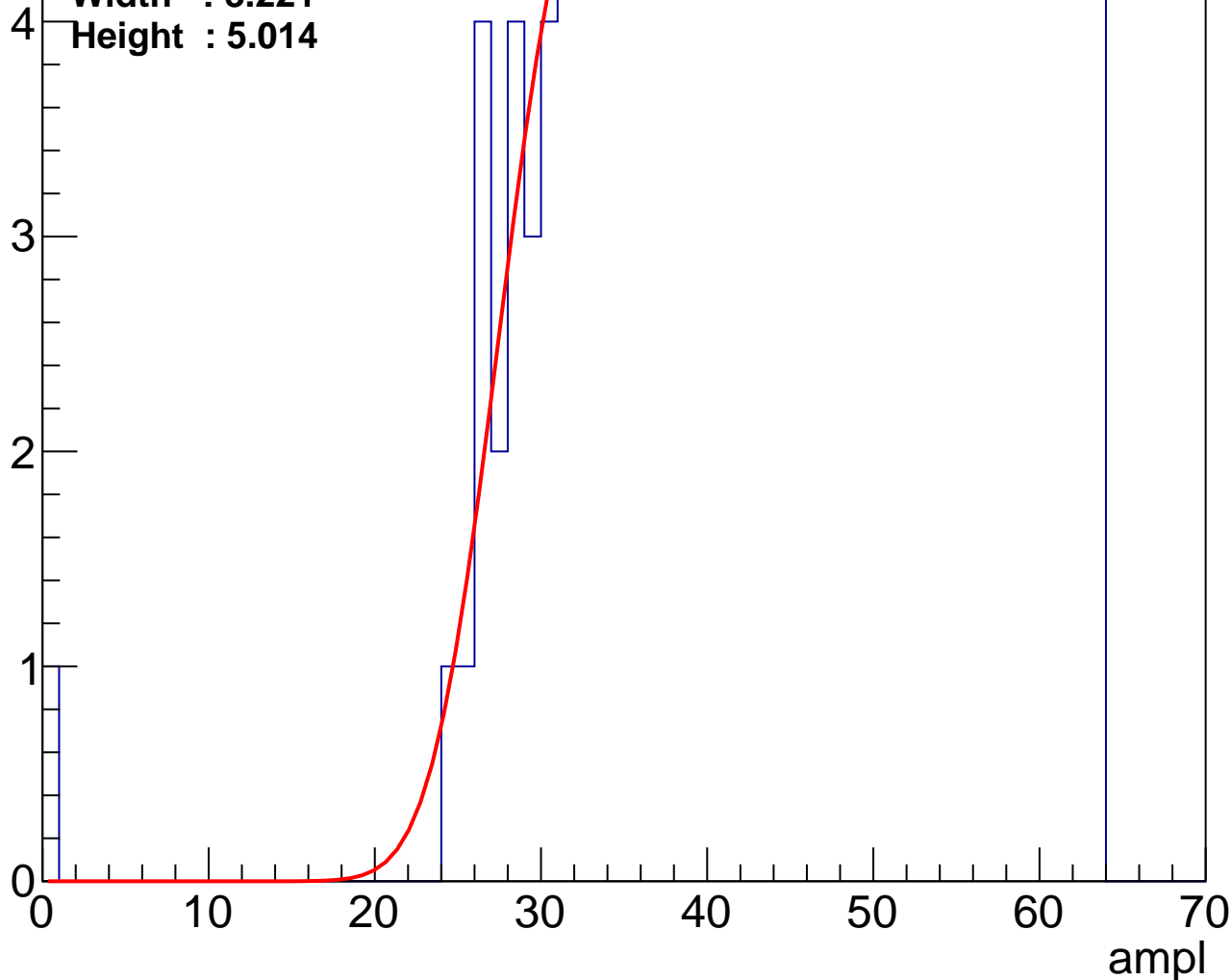
Width : 3.221

Height : 5.014

Entries 185

Mean 44.76

Std Dev 11.24



B1L103S, U15-ch64

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.2934

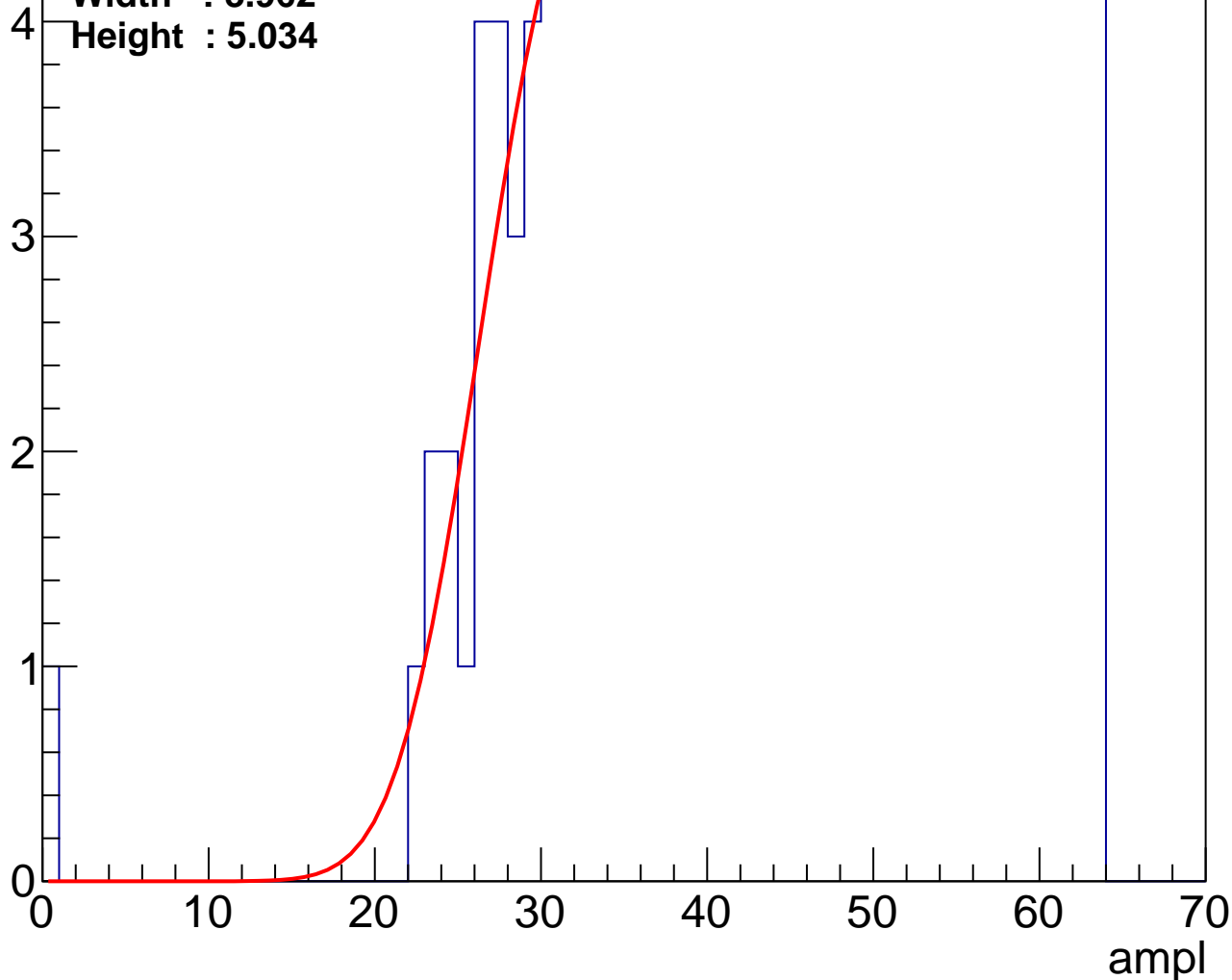
Width : 3.962

Height : 5.034

Entries 192

Mean 44.05

Std Dev 11.64



B1L103S, U15-ch65

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.4864

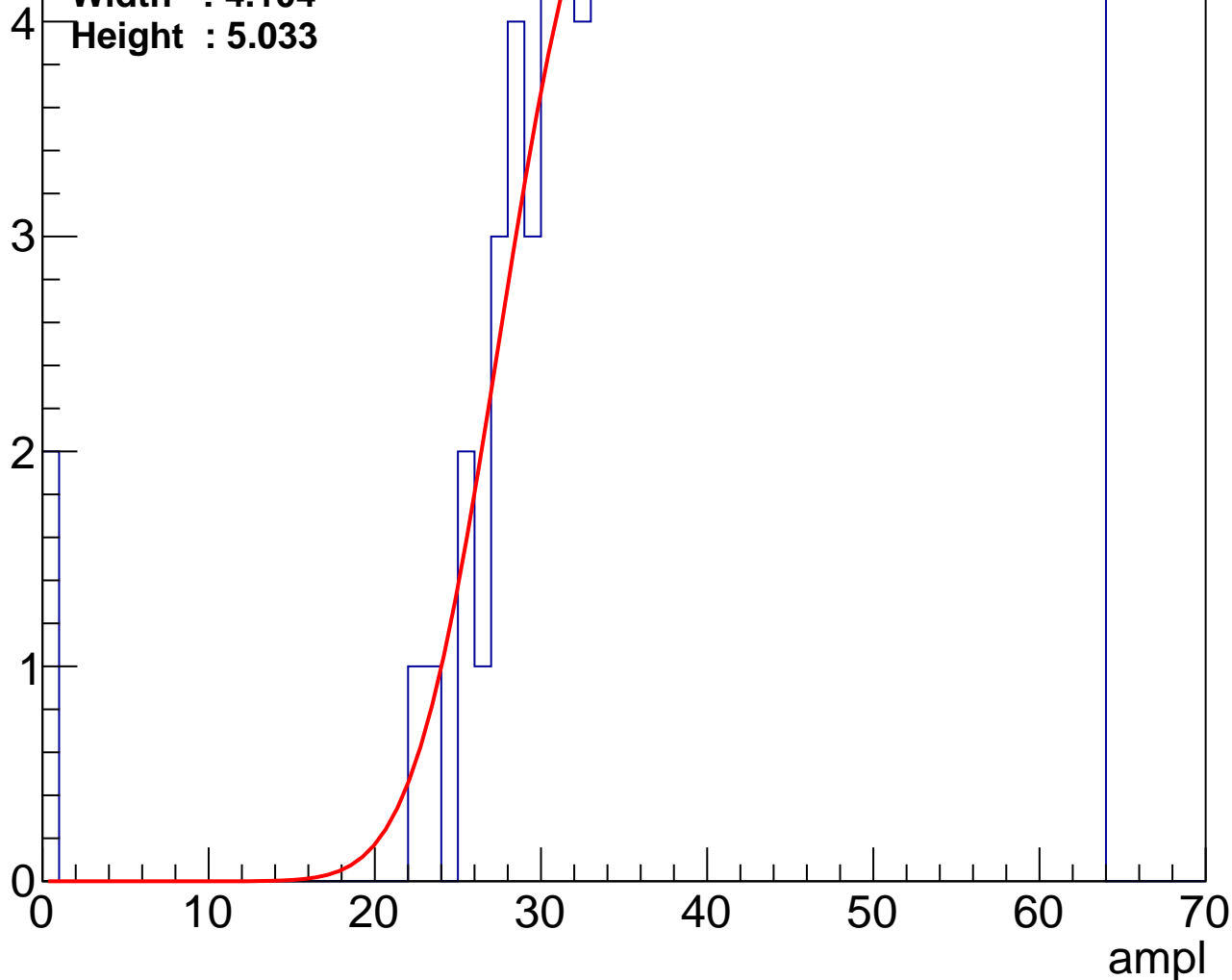
Width : 4.104

Height : 5.033

Entries 186

Mean 44.48

Std Dev 11.74



B1L103S, U15-ch66

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

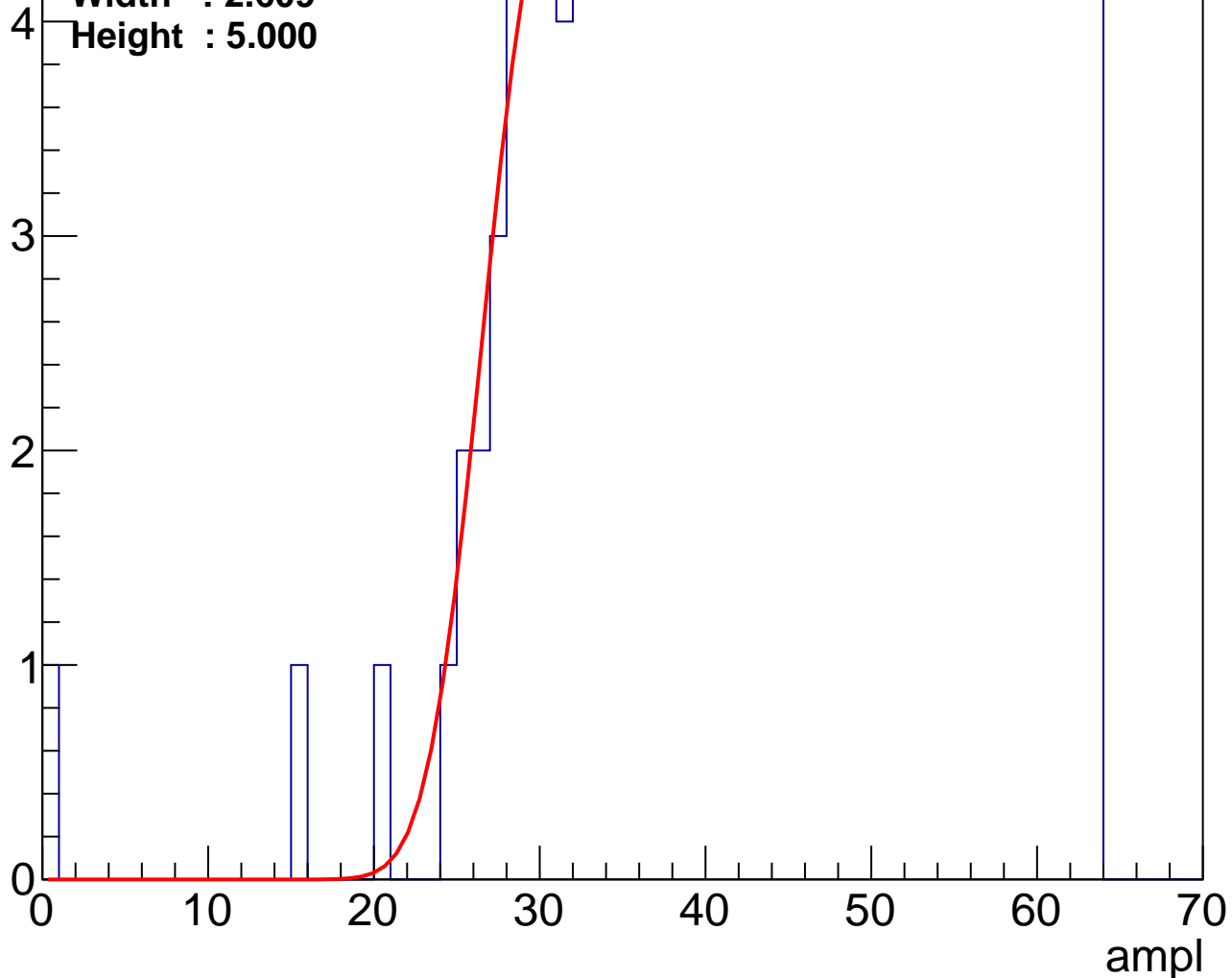
Profile : 00011100

Turn on : 26.5063

Width : 2.609

Height : 5.000

Entries	190
Mean	44.22
Std Dev	11.62



B1L103S, U15-ch67

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 29.9801

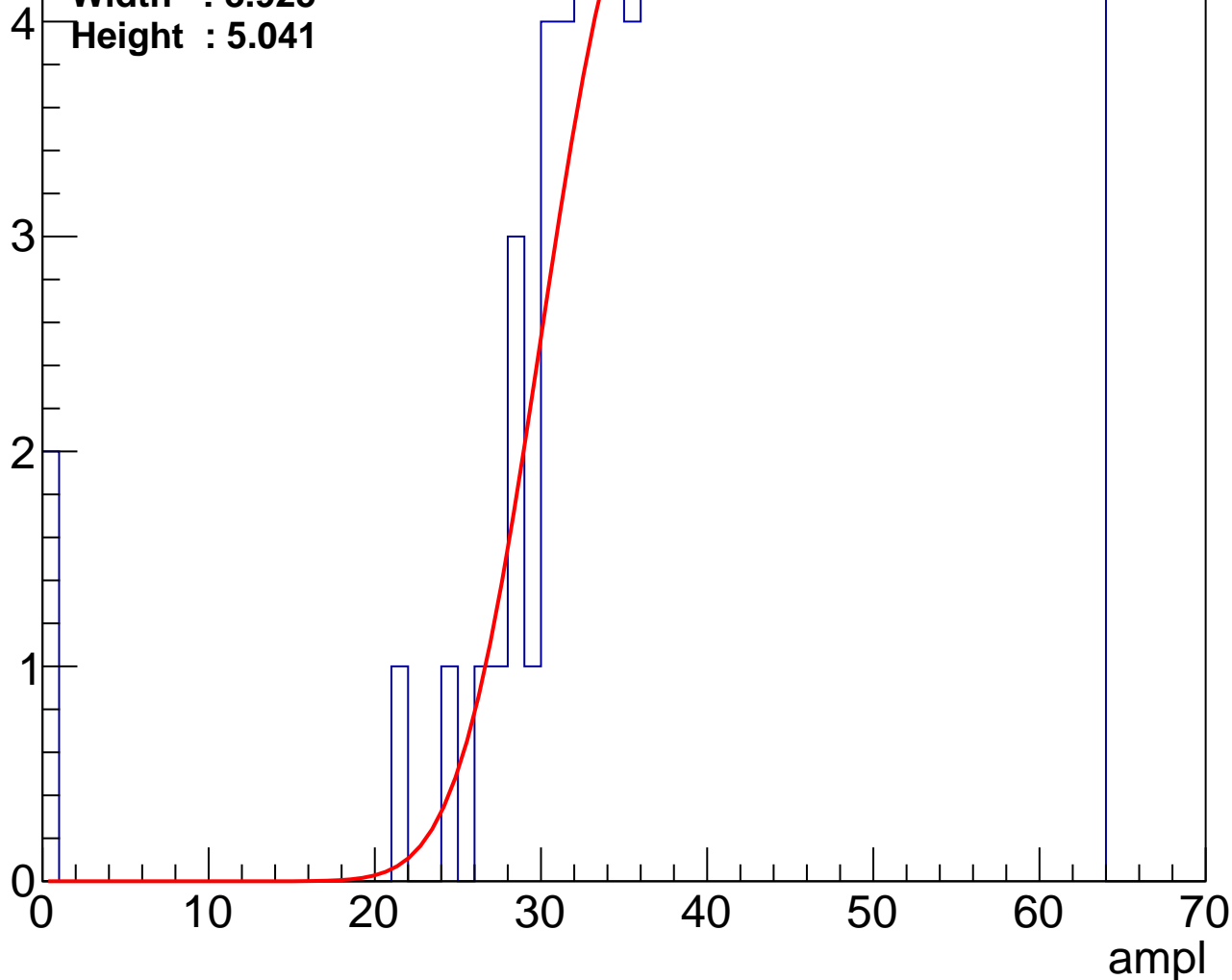
Width : 3.925

Height : 5.041

Entries 177

Mean 45.31

Std Dev 11.42



B1L103S, U15-ch68

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 28.2301

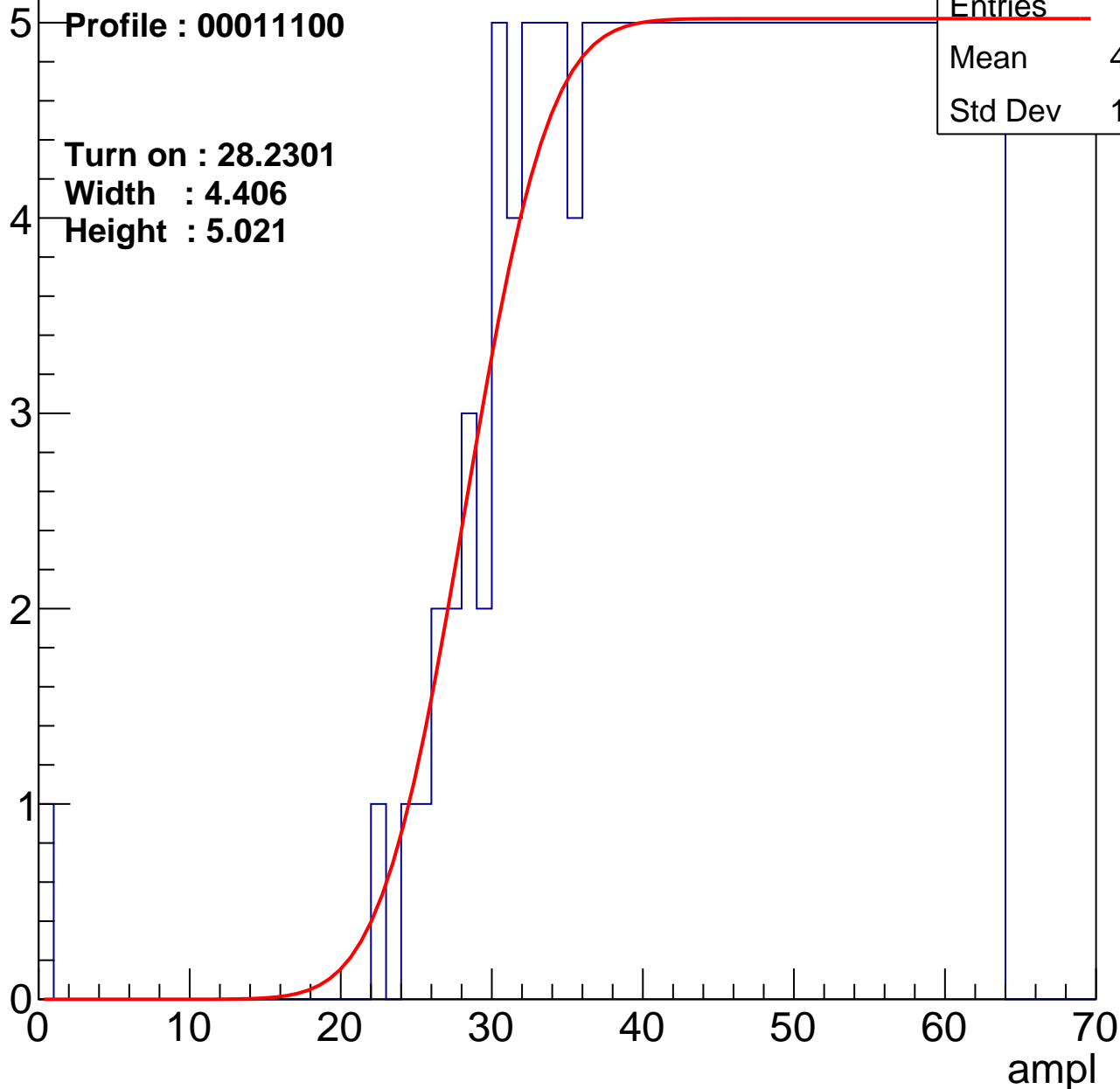
Width : 4.406

Height : 5.021

Entries 181

Mean 45.07

Std Dev 11.17



B1L103S, U15-ch69

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 28.2561

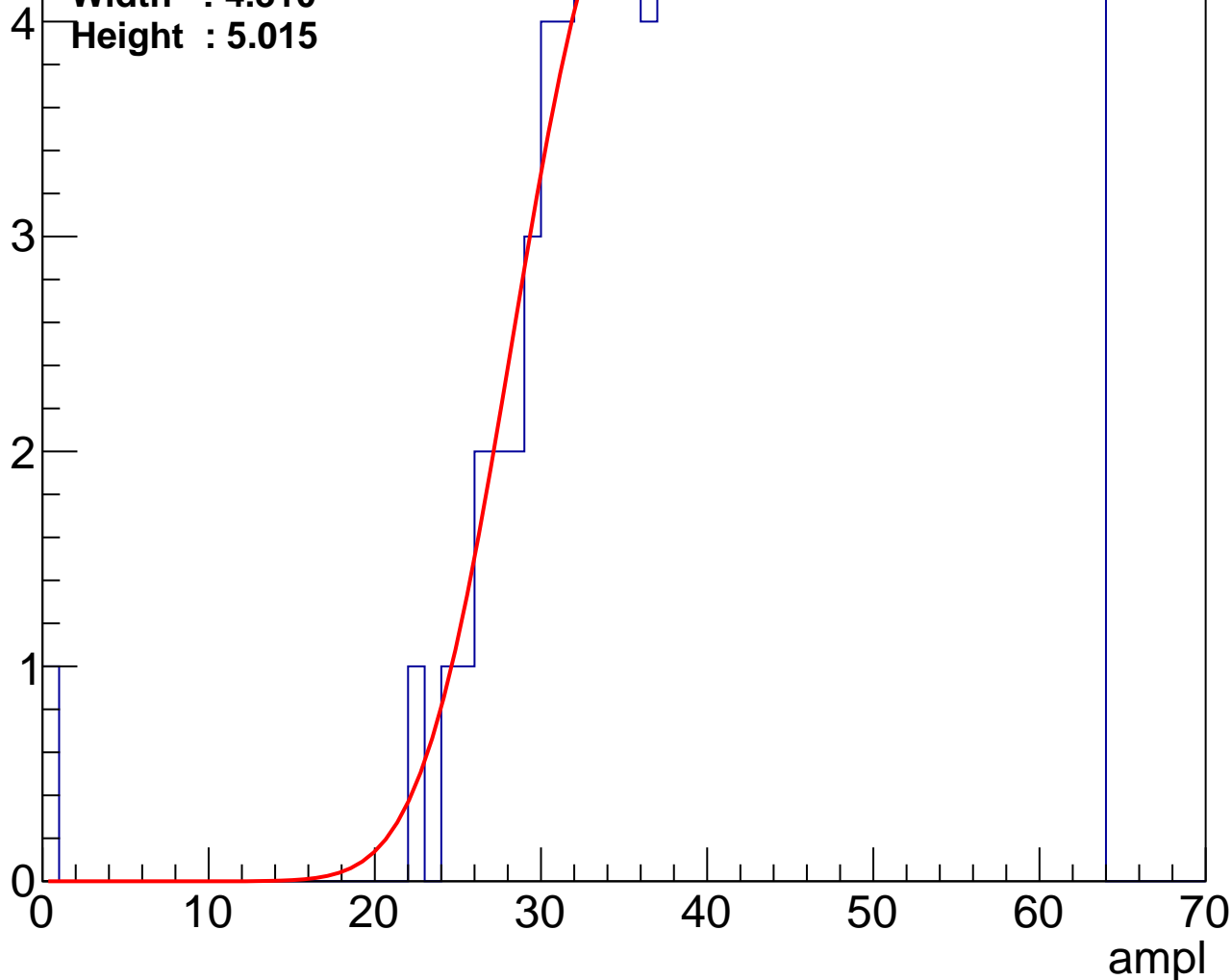
Width : 4.310

Height : 5.015

Entries 180

Mean 45.16

Std Dev 11.14



B1L103S, U15-ch70

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.0108

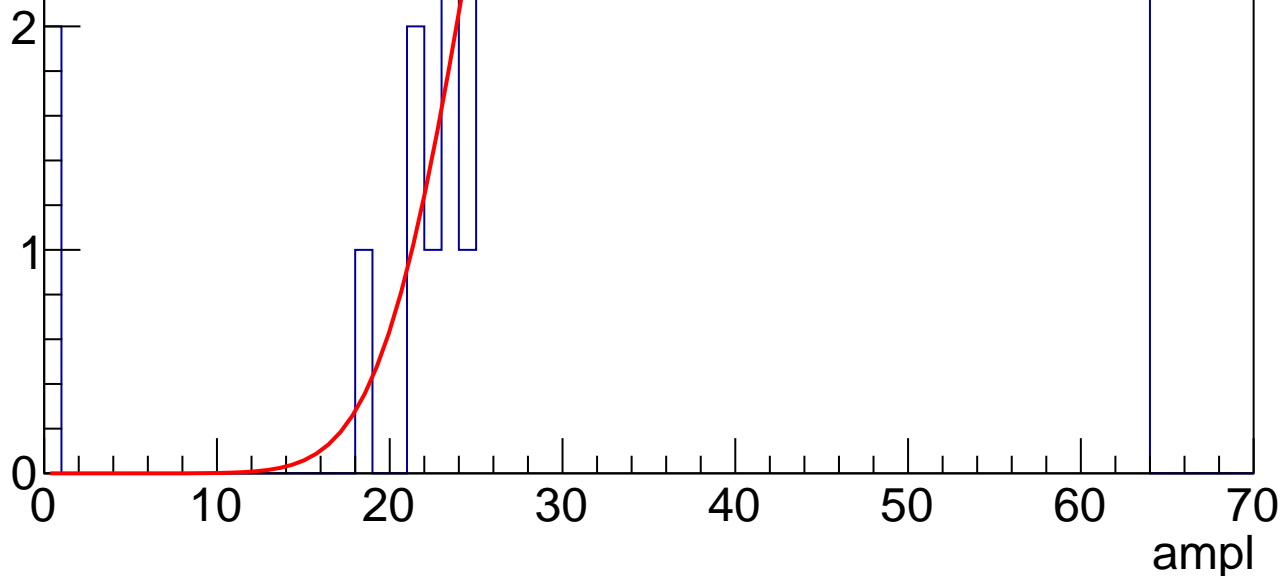
Width : 4.389

Height : 5.040

Entries 201

Mean 43.02

Std Dev 12.44



B1L103S, U15-ch71

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

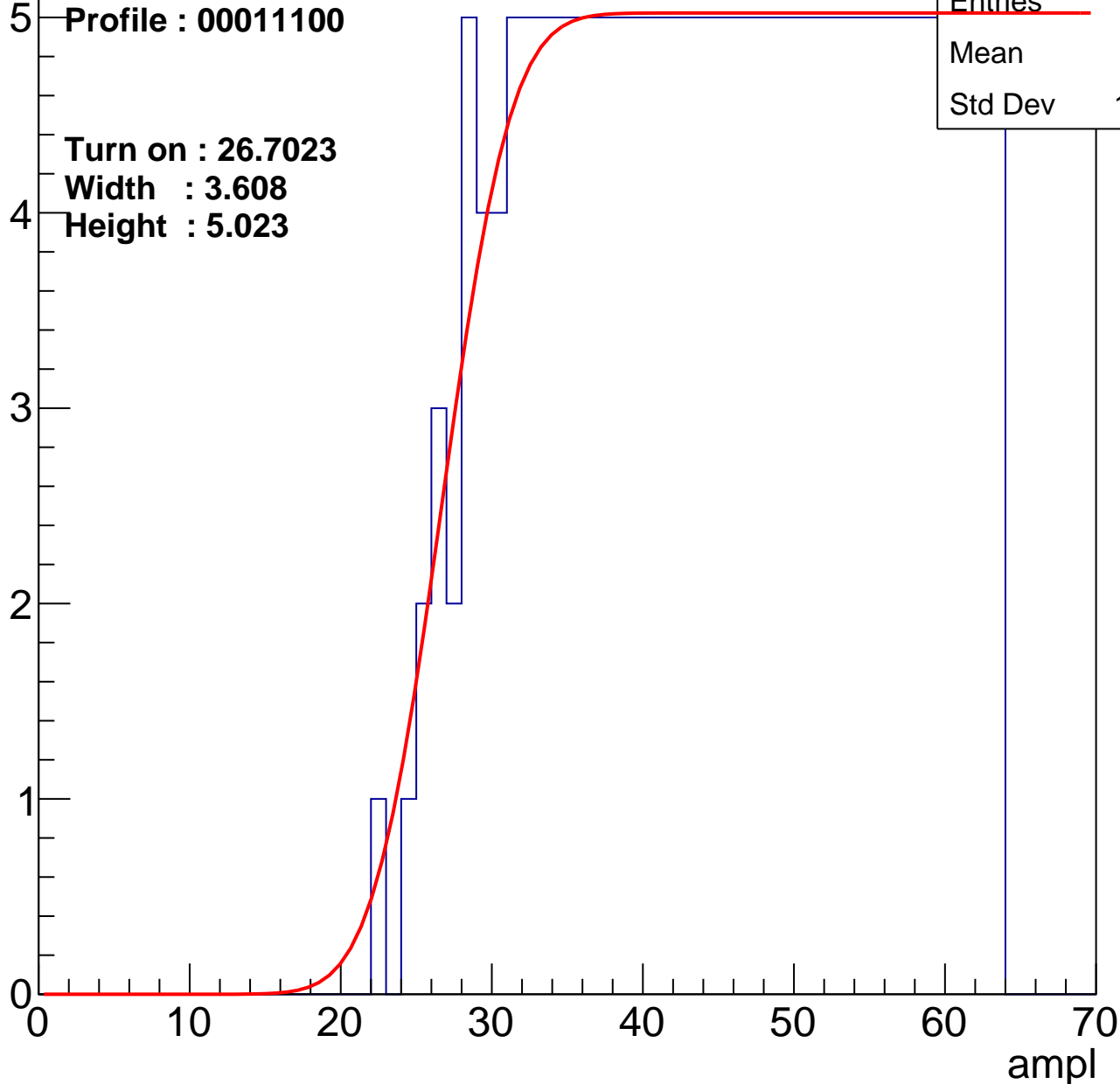
Profile : 00011100

Turn on : 26.7023

Width : 3.608

Height : 5.023

Entries	187
Mean	44.7
Std Dev	10.96



B1L103S, U15-ch72

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

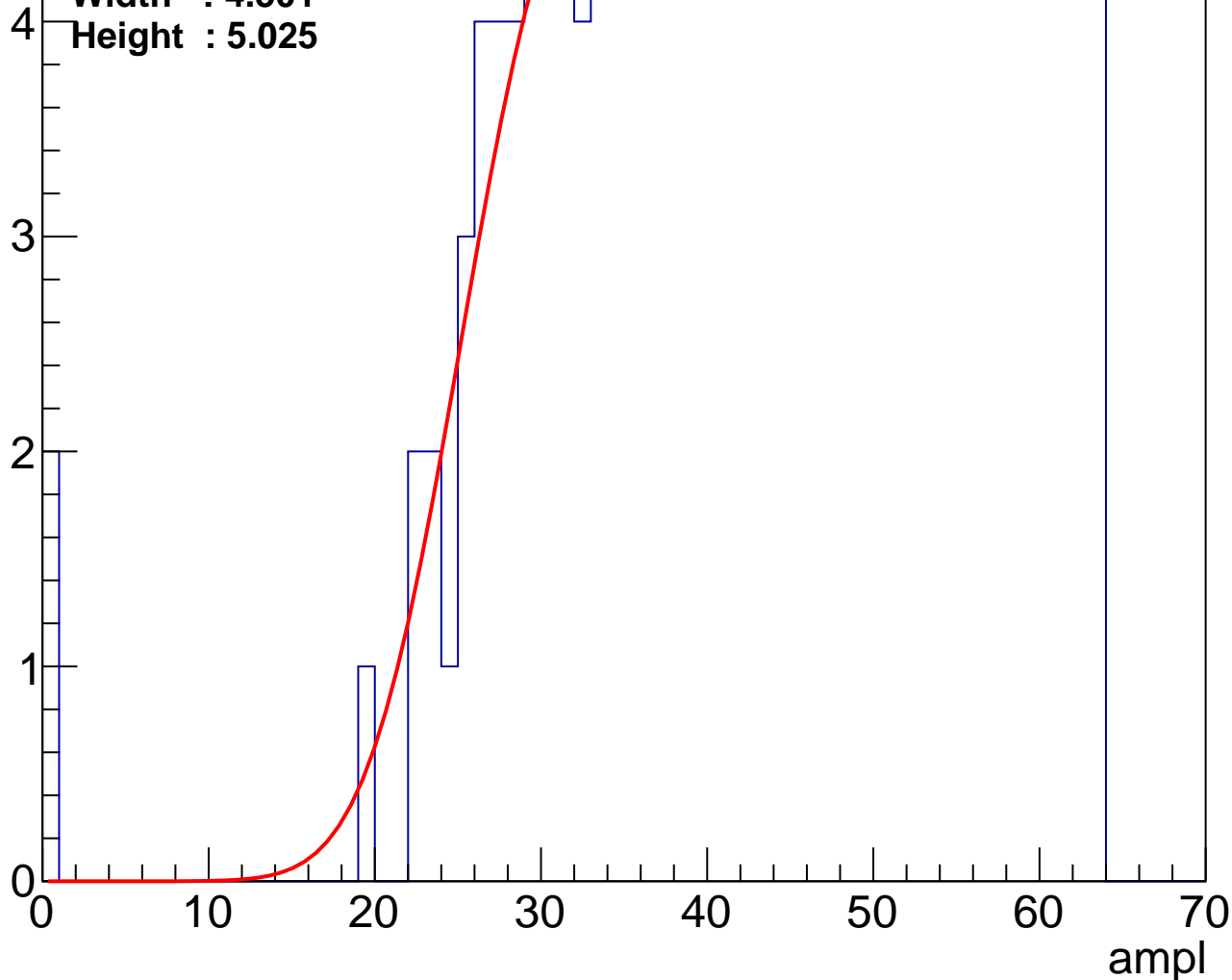
Profile : 00011100

Turn on : 25.1882

Width : 4.501

Height : 5.025

Entries	197
Mean	43.4
Std Dev	12.27



B1L103S, U15-ch73

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

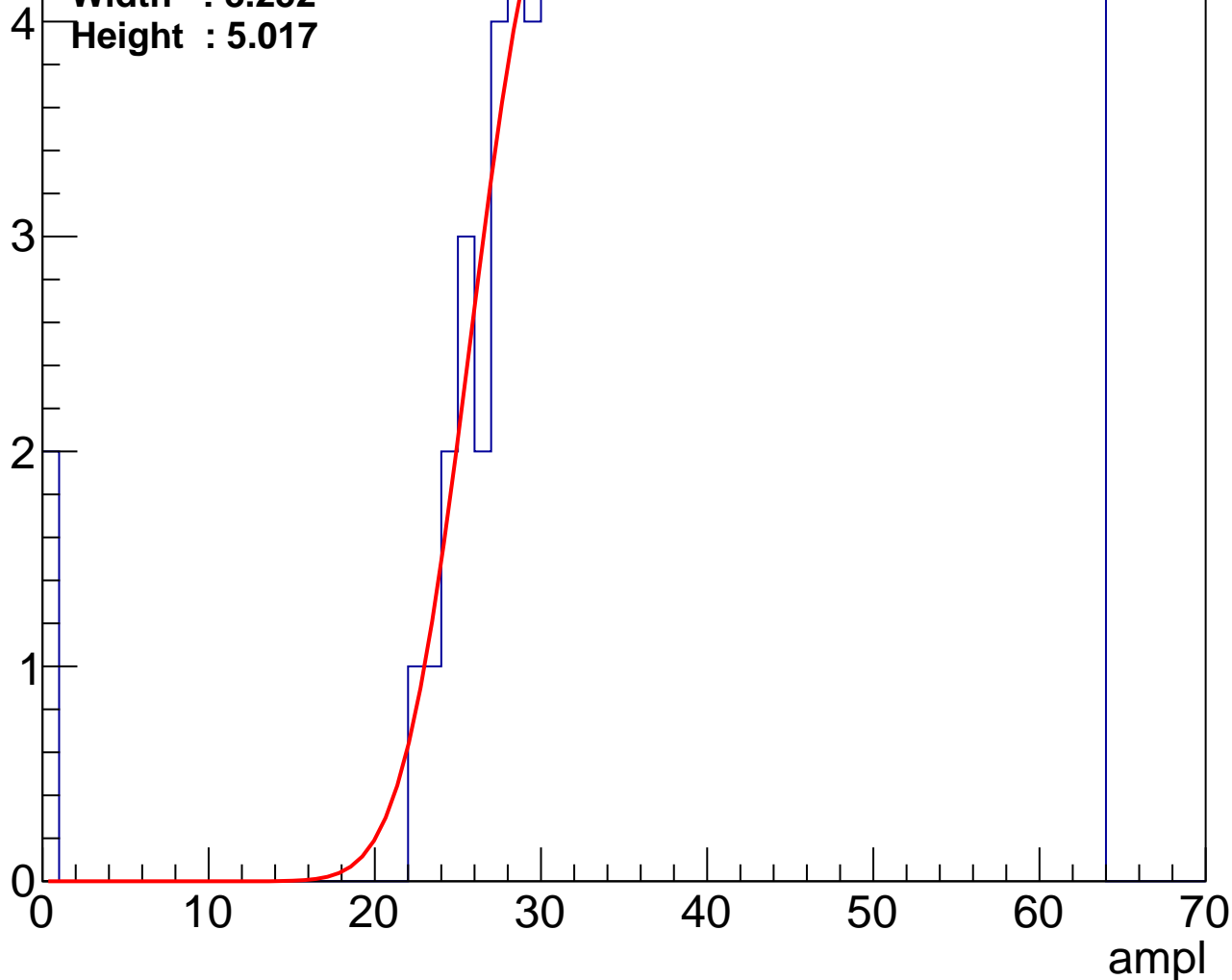
Profile : 00011100

Turn on : 25.7363

Width : 3.252

Height : 5.017

Entries	194
Mean	43.76
Std Dev	12.03



B1L103S, U15-ch74

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

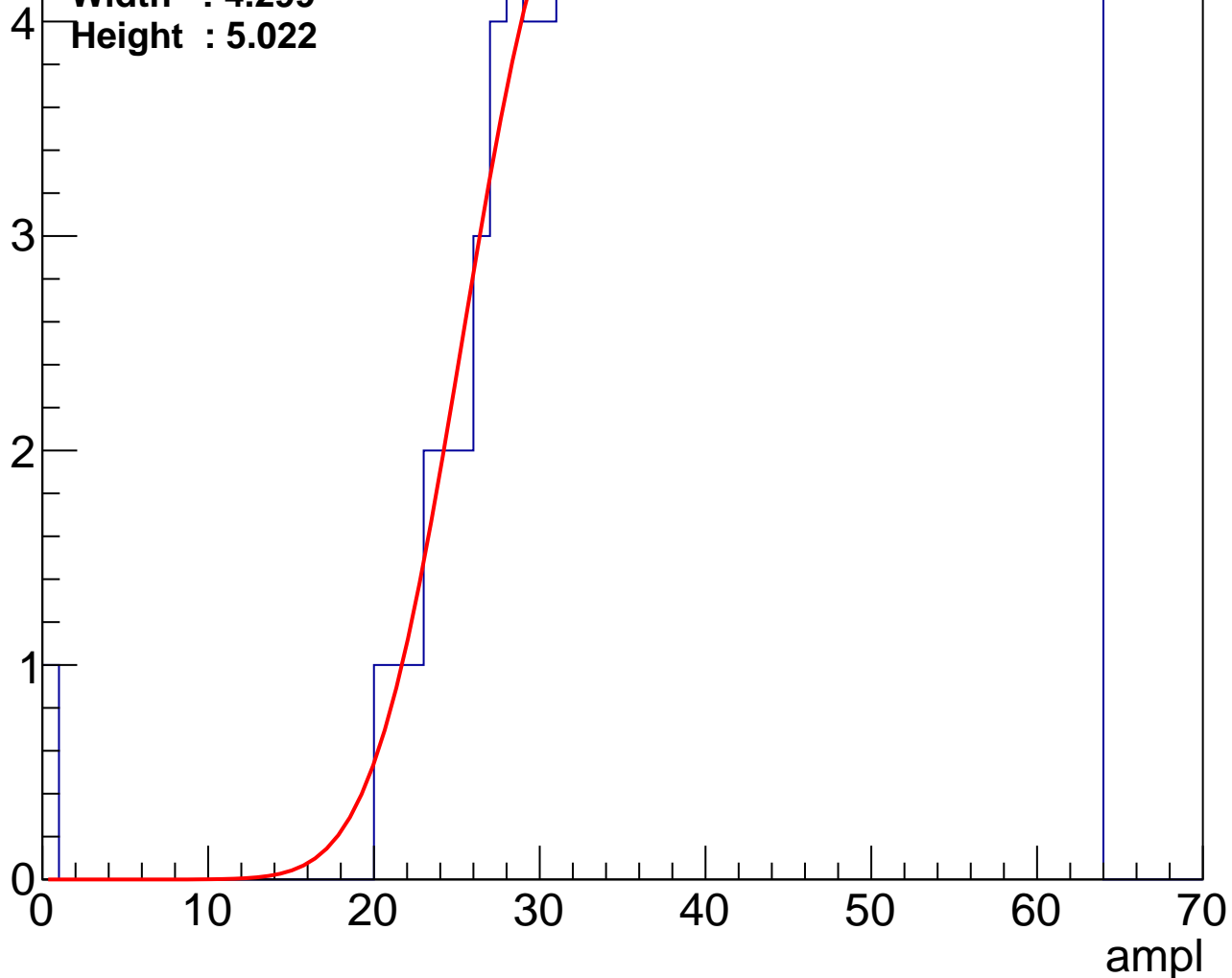
Profile : 00011100

Turn on : 25.3204

Width : 4.299

Height : 5.022

Entries	195
Mean	43.71
Std Dev	11.86



B1L103S, U15-ch75

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

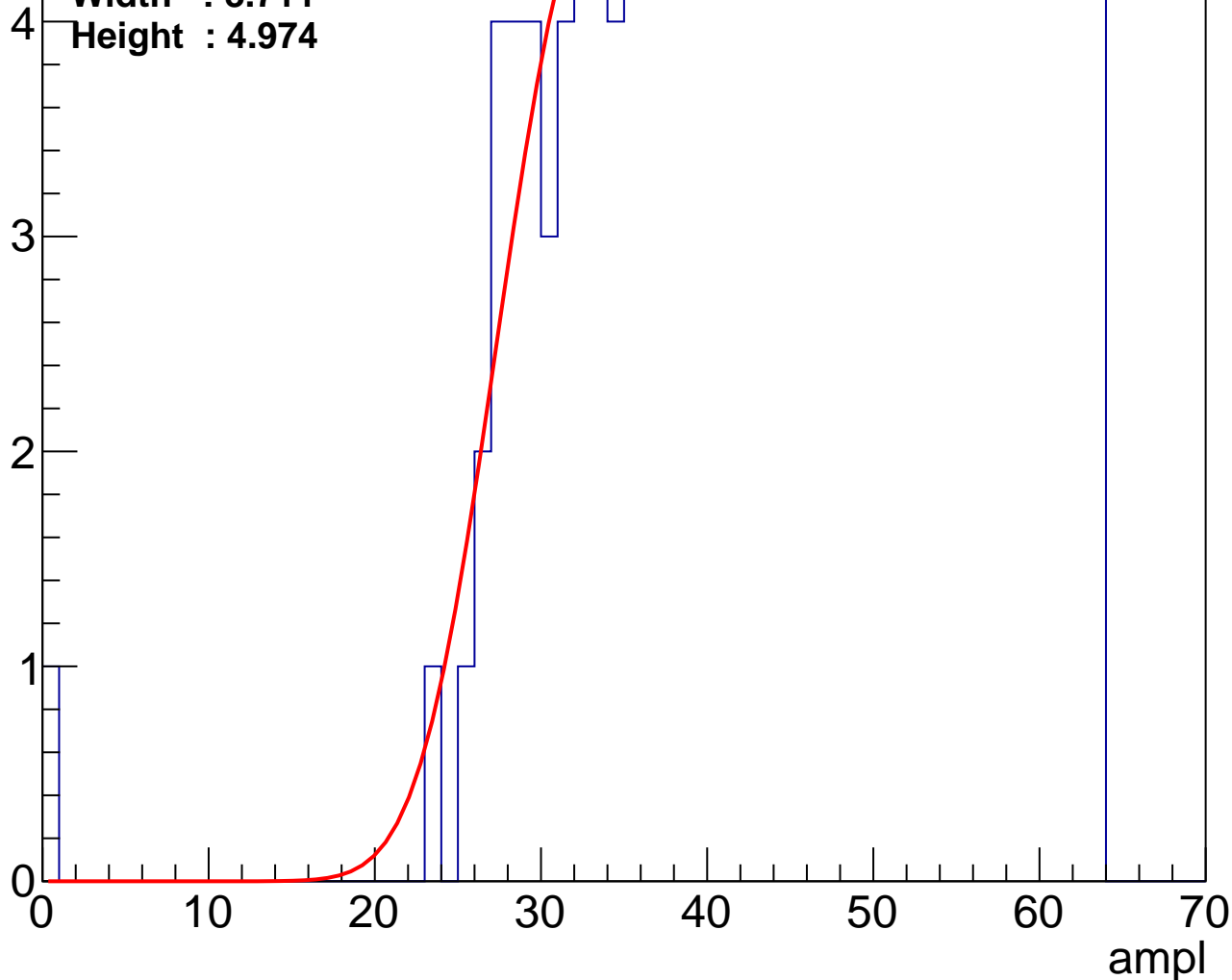
Profile : 00011100

Turn on : 27.3021

Width : 3.711

Height : 4.974

Entries	183
Mean	44.9
Std Dev	11.23



B1L103S, U15-ch76

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 28.2385

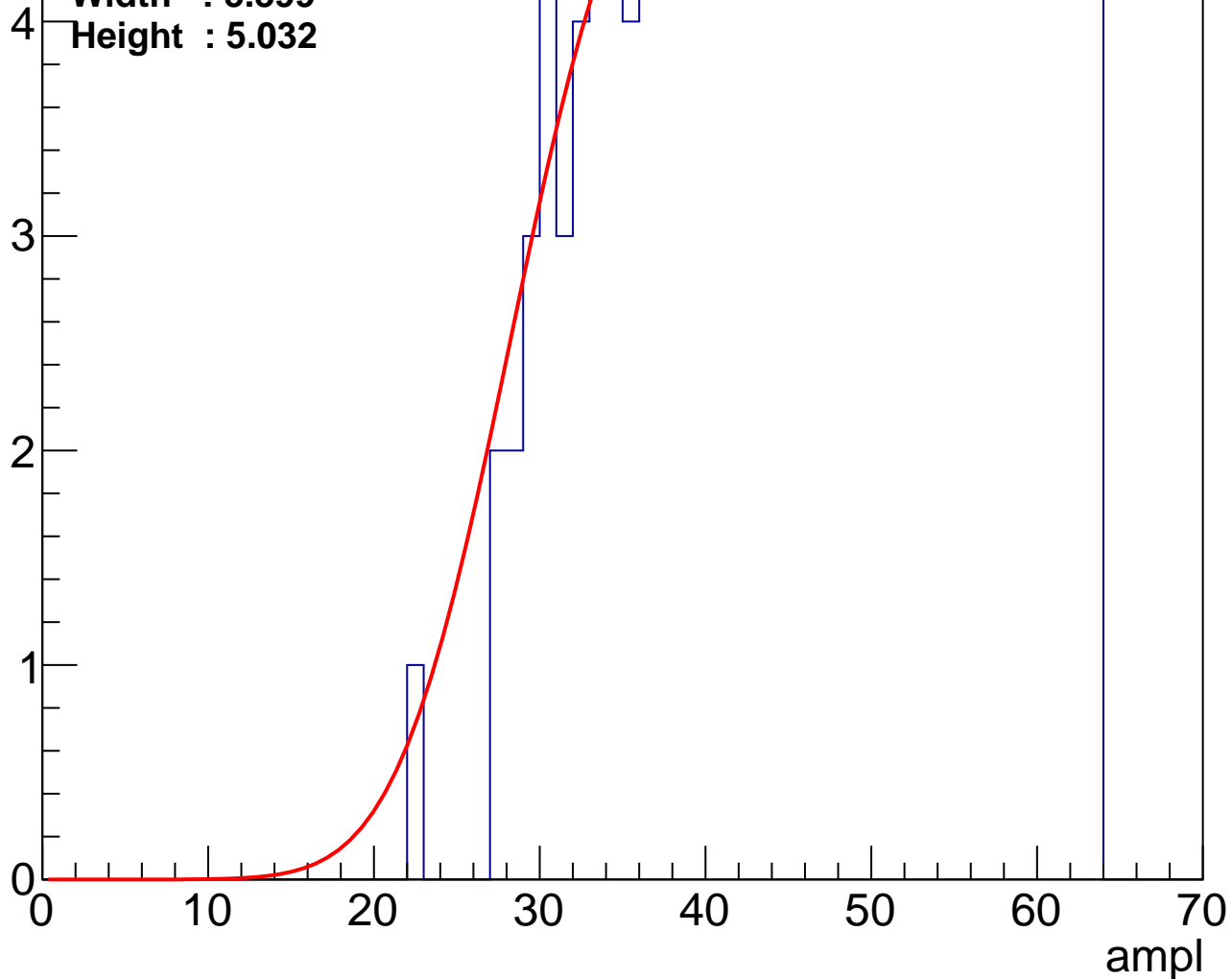
Width : 5.399

Height : 5.032

Entries 174

Mean 45.95

Std Dev 10.3



B1L103S, U15-ch77

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.0938

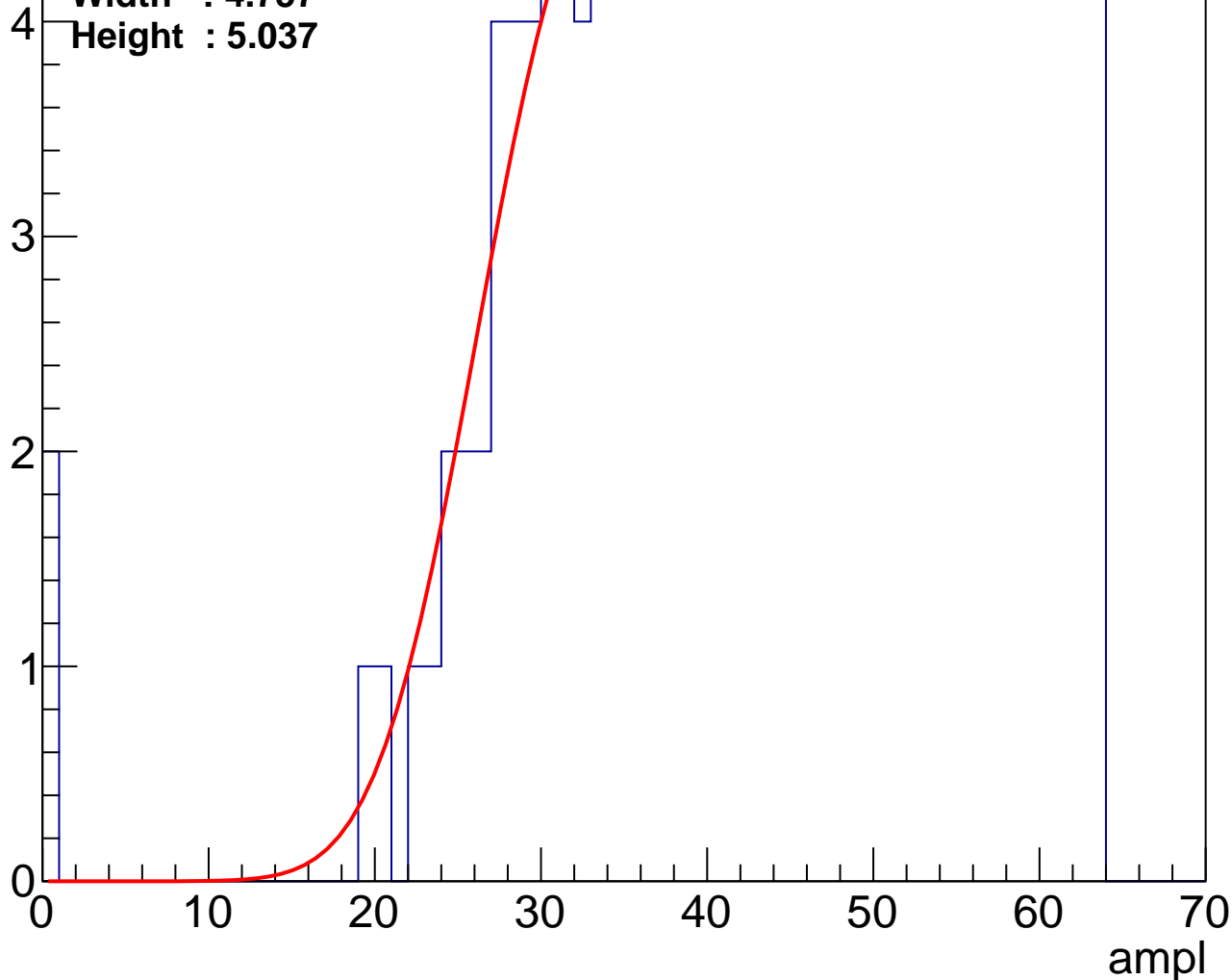
Width : 4.757

Height : 5.037

Entries	193
---------	-----

Mean	43.75
------	-------

Std Dev	12.16
---------	-------



B1L103S, U15-ch78

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.5279

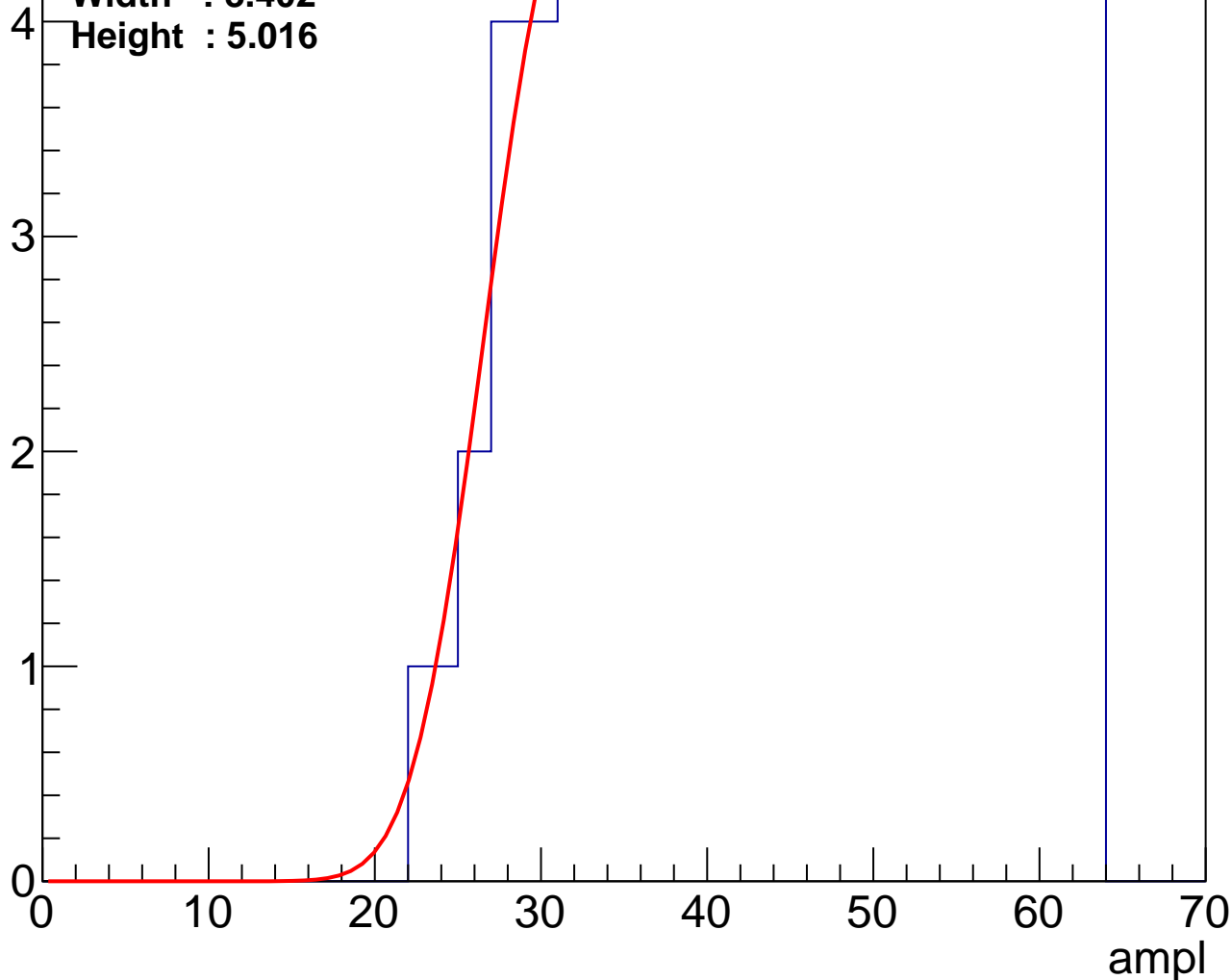
Width : 3.402

Height : 5.016

Entries 188

Mean 44.59

Std Dev 11.05



B1L103S, U15-ch79

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

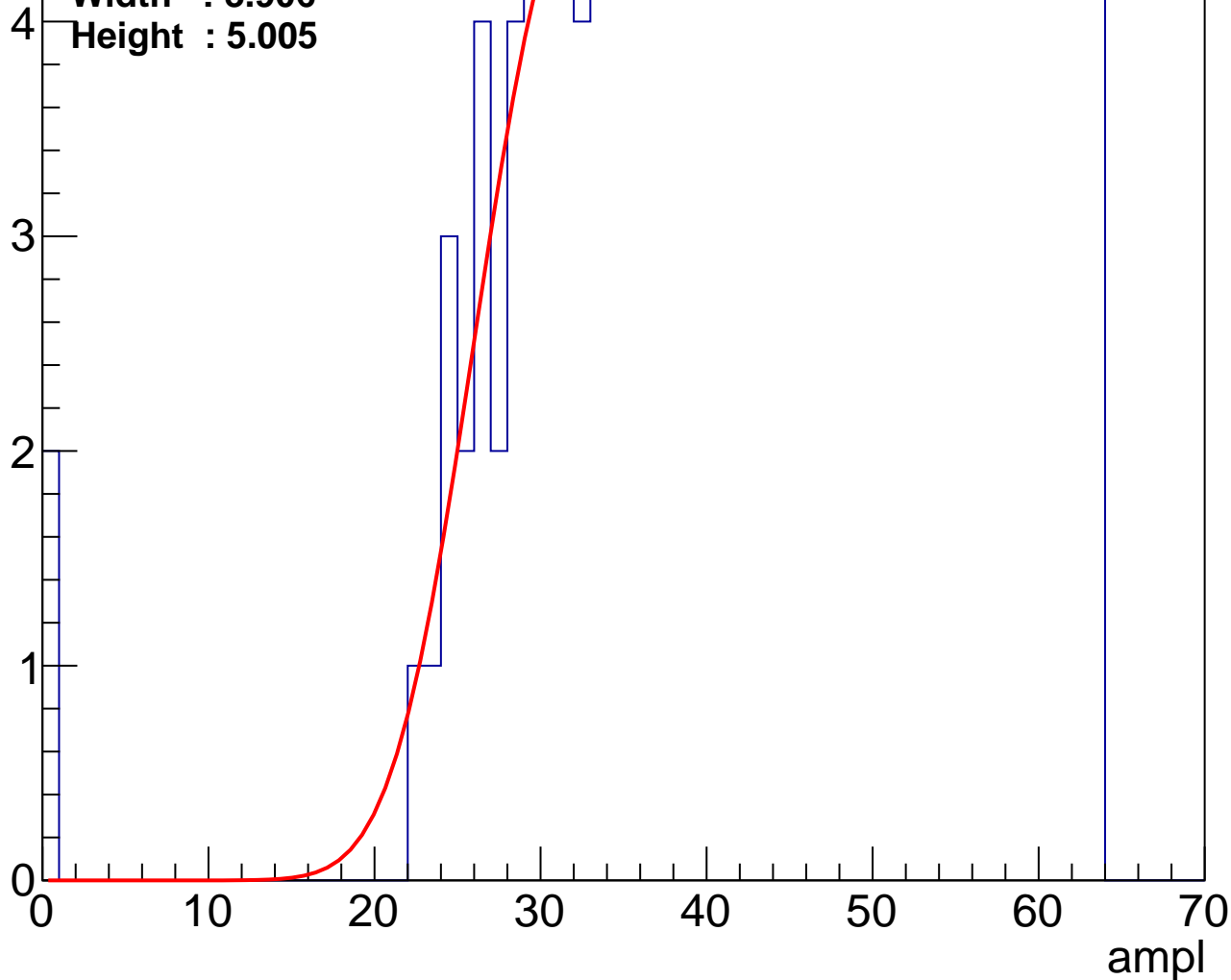
Profile : 00011100

Turn on : 25.9837

Width : 3.906

Height : 5.005

Entries	193
Mean	43.81
Std Dev	12.05



B1L103S, U15-ch80

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.8572

Width : 3.979

Height : 4.995

Entries	200
Mean	42.88
Std Dev	12.97

4

3

2

1

0

ampl

B1L103S, U15-ch81

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.6351

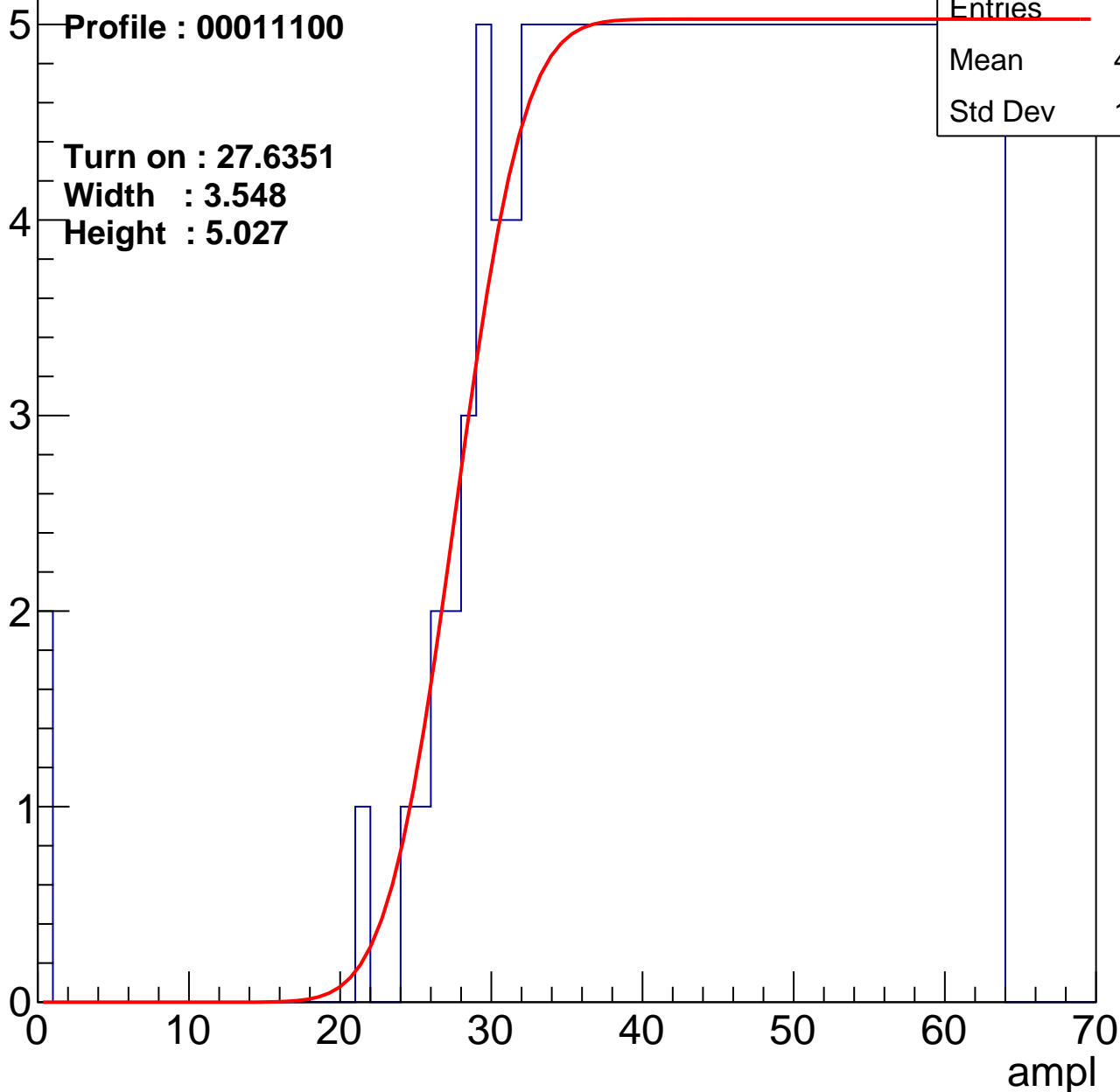
Width : 3.548

Height : 5.027

Entries 185

Mean 44.59

Std Dev 11.69



B1L103S, U15-ch82

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

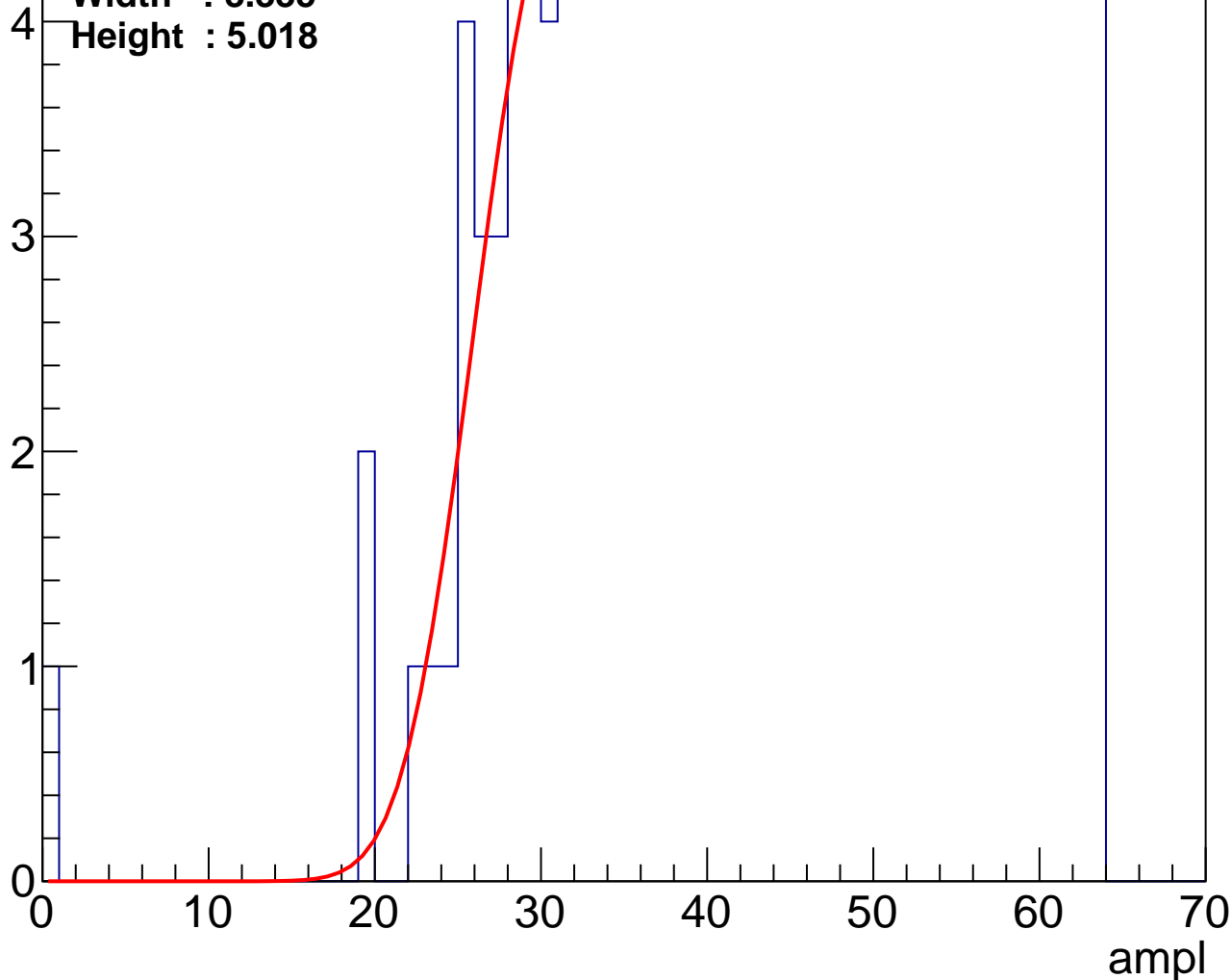
Profile : 00011100

Turn on : 25.8751

Width : 3.335

Height : 5.018

Entries	195
Mean	43.72
Std Dev	11.86



B1L103S, U15-ch83

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

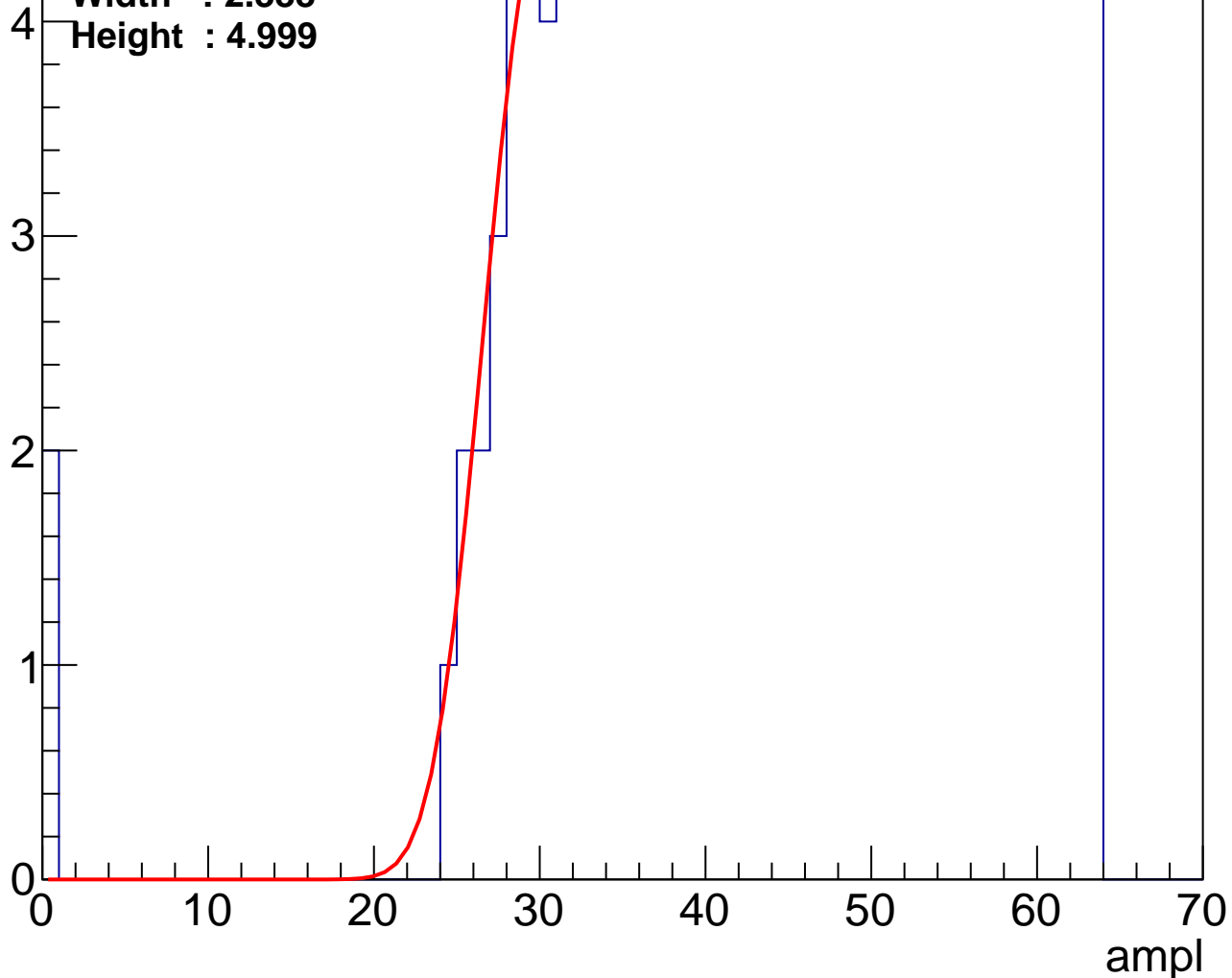
Profile : 00011100

Turn on : 26.5392

Width : 2.388

Height : 4.999

Entries	189
Mean	44.27
Std Dev	11.76



B1L103S, U15-ch84

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.4393

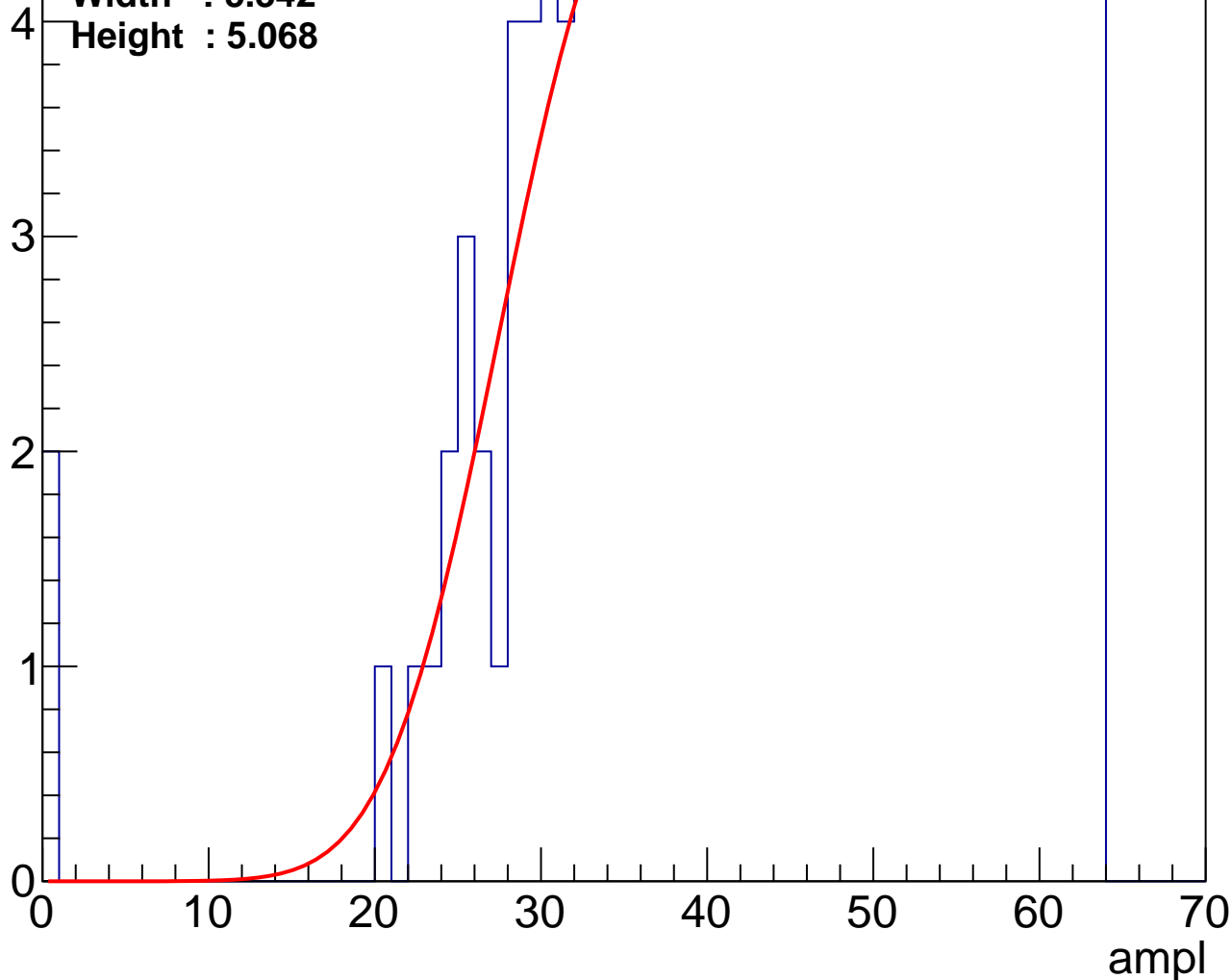
Width : 5.342

Height : 5.068

Entries 190

Mean 44.05

Std Dev 12



B1L103S, U15-ch85

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

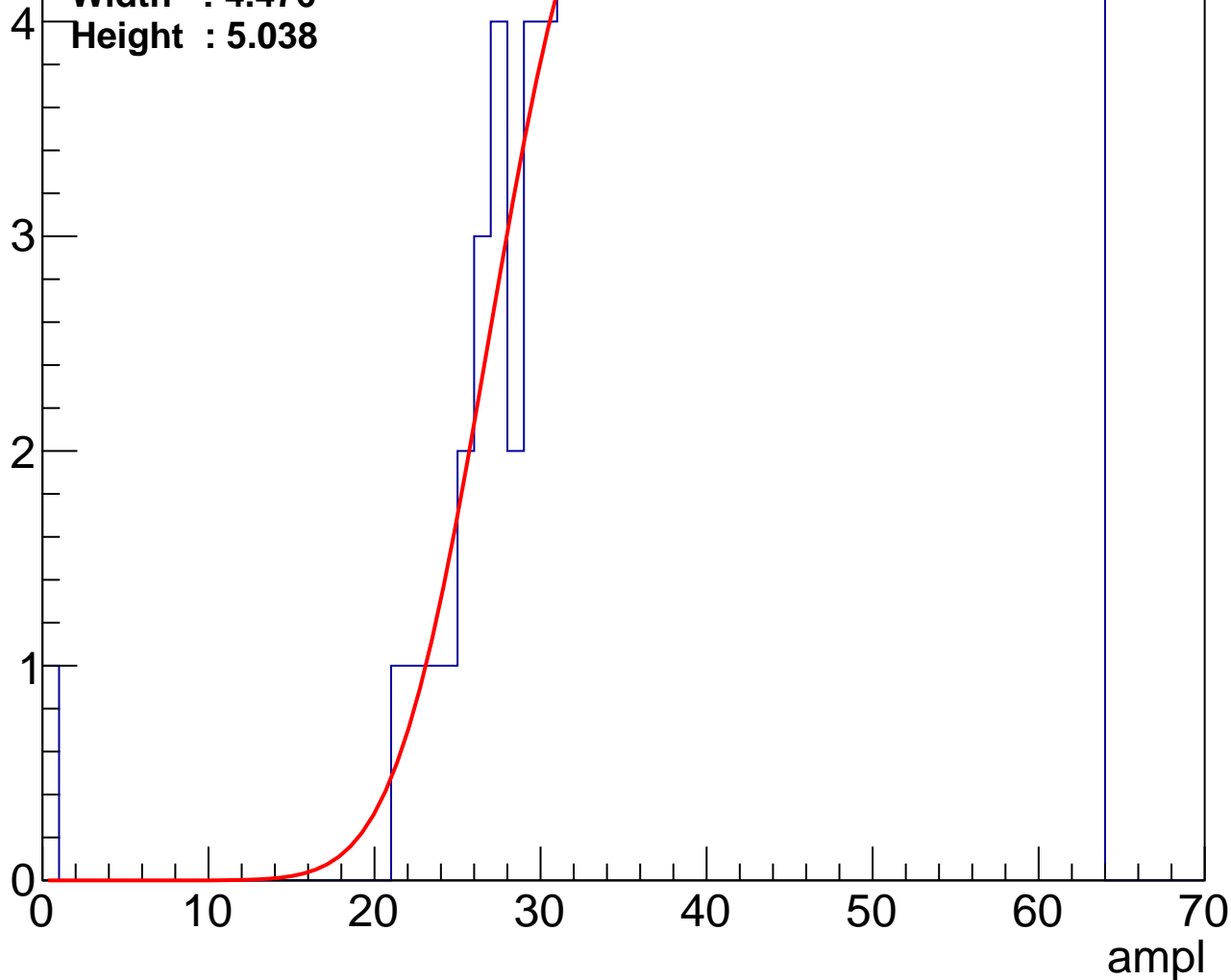
Profile : 00011100

Turn on : 26.8781

Width : 4.476

Height : 5.038

Entries	189
Mean	44.3
Std Dev	11.56



B1L103S, U15-ch86

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

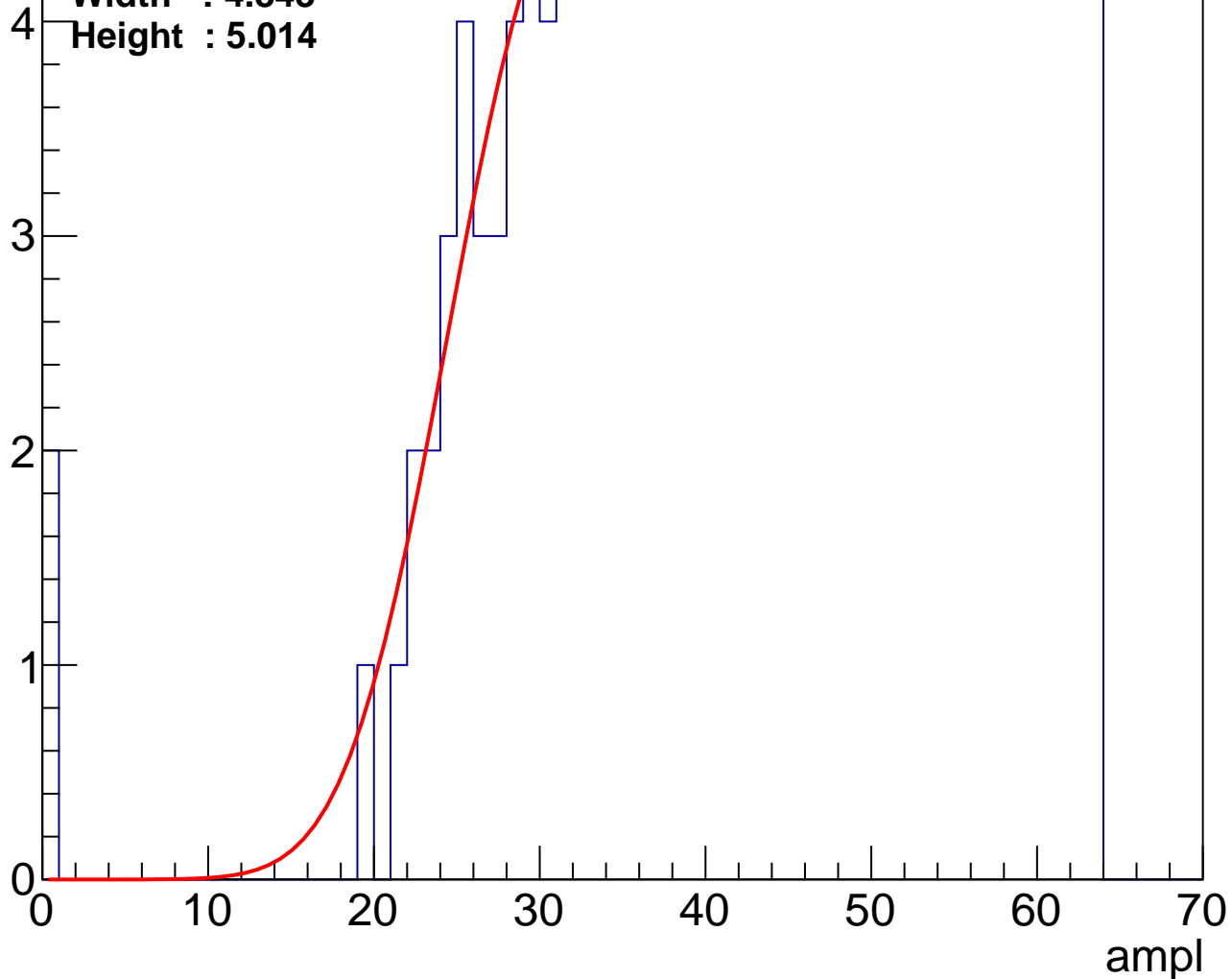
Profile : 00011100

Turn on : 24.3707

Width : 4.843

Height : 5.014

Entries	199
Mean	43.18
Std Dev	12.4



B1L103S, U15-ch87

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.8446

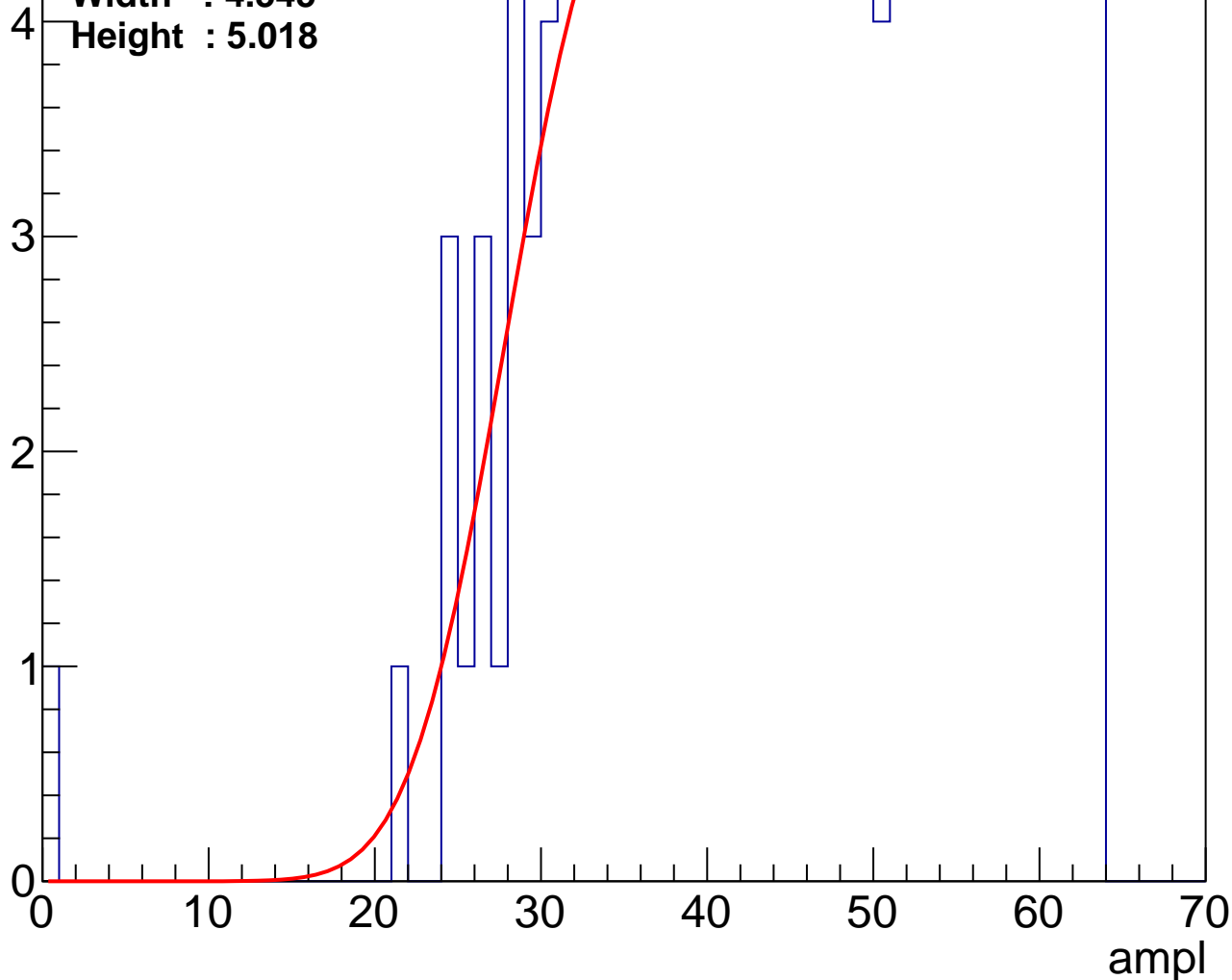
Width : 4.545

Height : 5.018

Entries 186

Mean 44.49

Std Dev 11.45



B1L103S, U15-ch88

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.8269

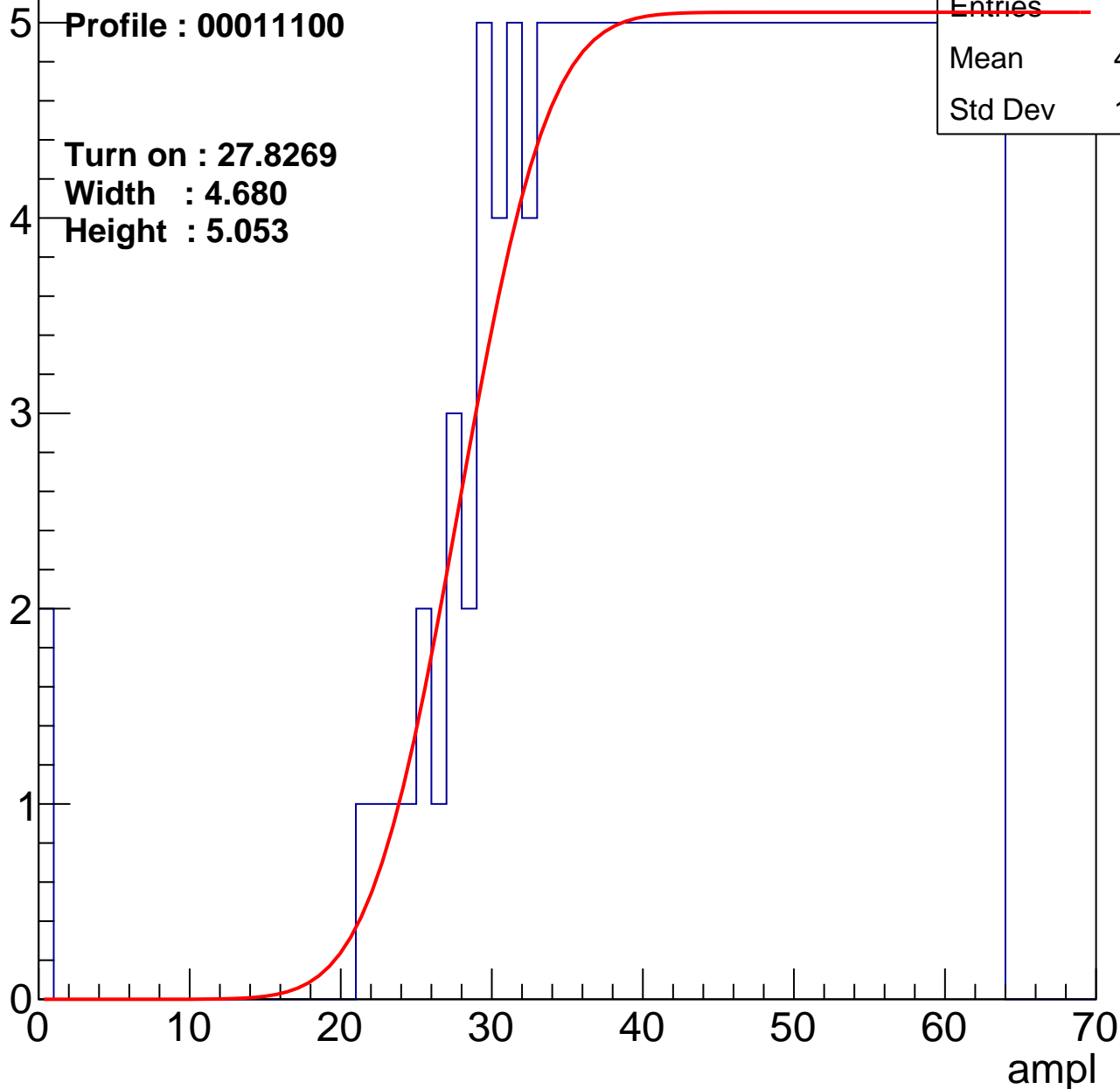
Width : 4.680

Height : 5.053

Entries 187

Mean 44.34

Std Dev 11.87



B1L103S, U15-ch89

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.5181

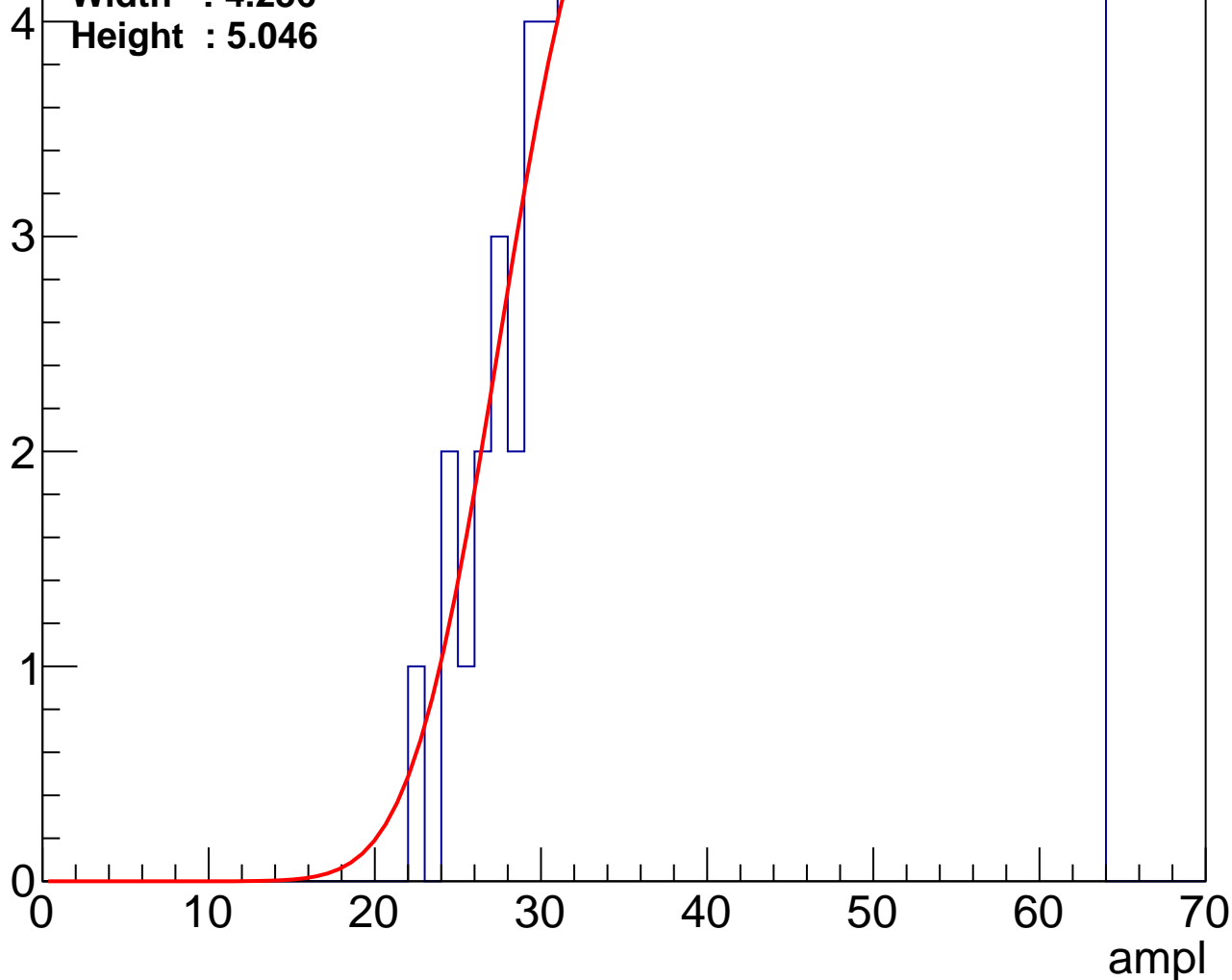
Width : 4.236

Height : 5.046

Entries 184

Mean 44.97

Std Dev 10.84



B1L103S, U15-ch90

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.5207

Width : 4.243

Height : 5.043

Entries 204

Mean 42.7

Std Dev 12.65

2

1

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U15-ch91

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 23.9240

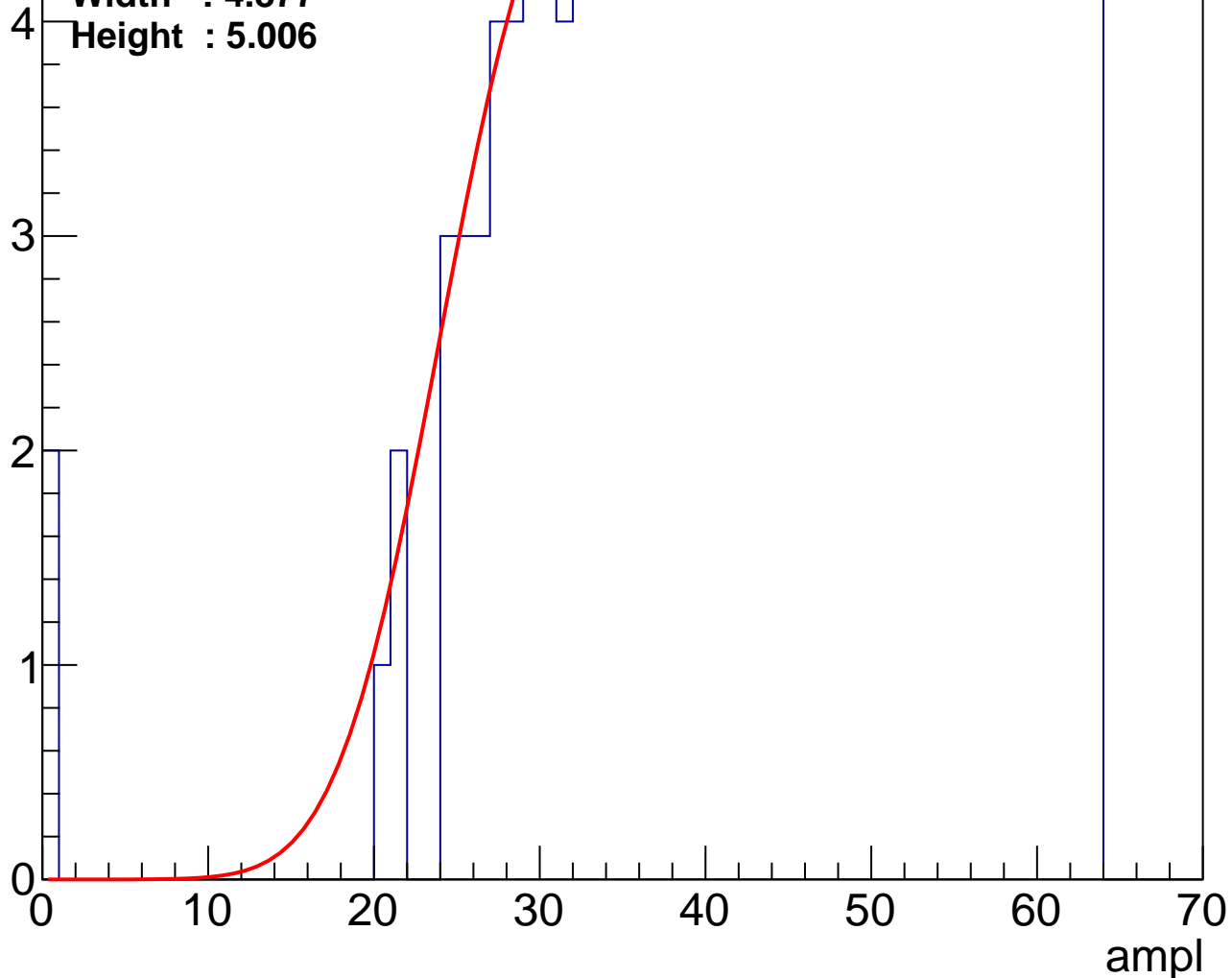
Width : 4.877

Height : 5.006

Entries 196

Mean 43.5

Std Dev 12.22



B1L103S, U15-ch92

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 24.0454

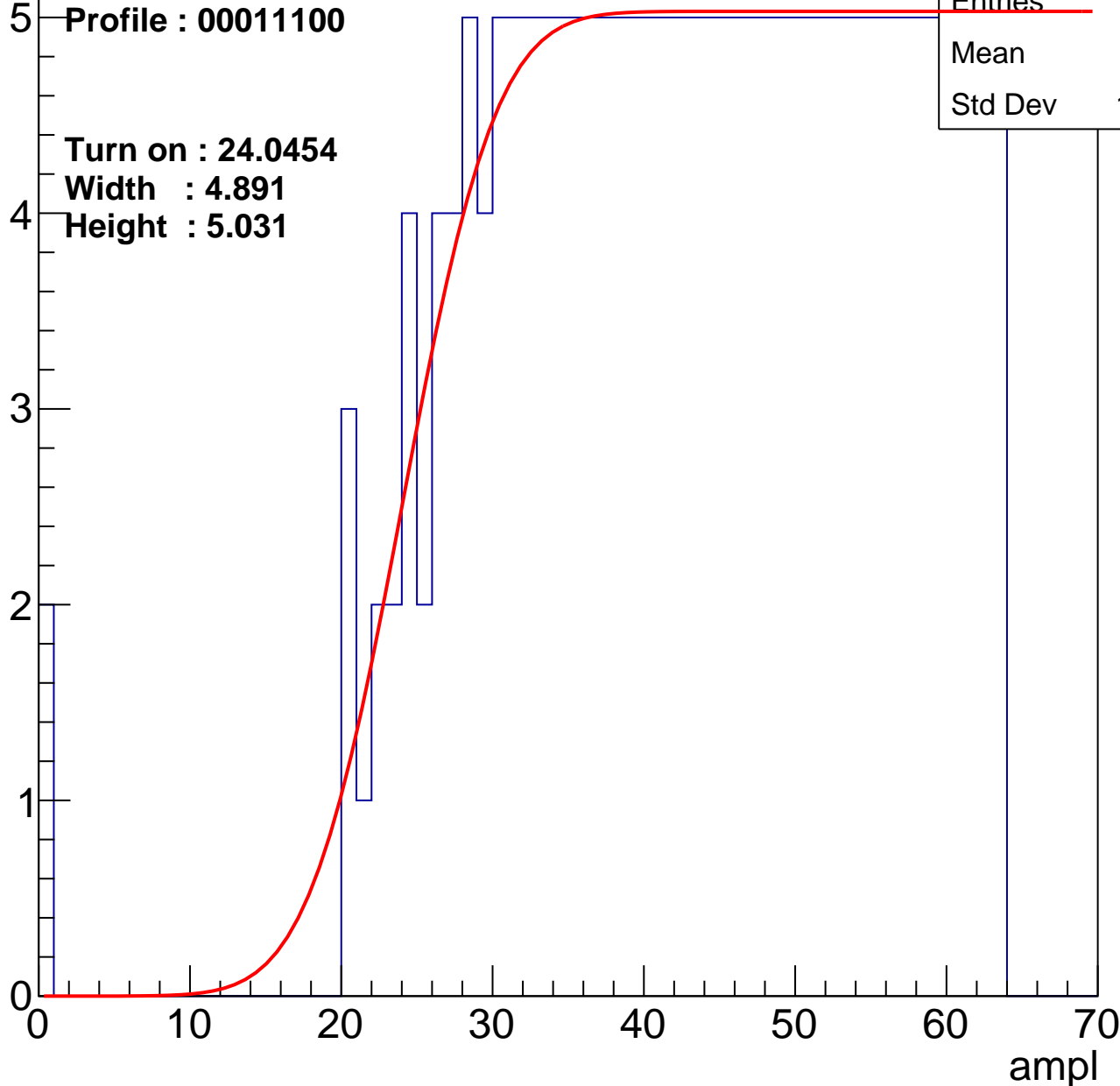
Width : 4.891

Height : 5.031

Entries 203

Mean 42.81

Std Dev 12.57



B1L103S, U15-ch93

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.0315

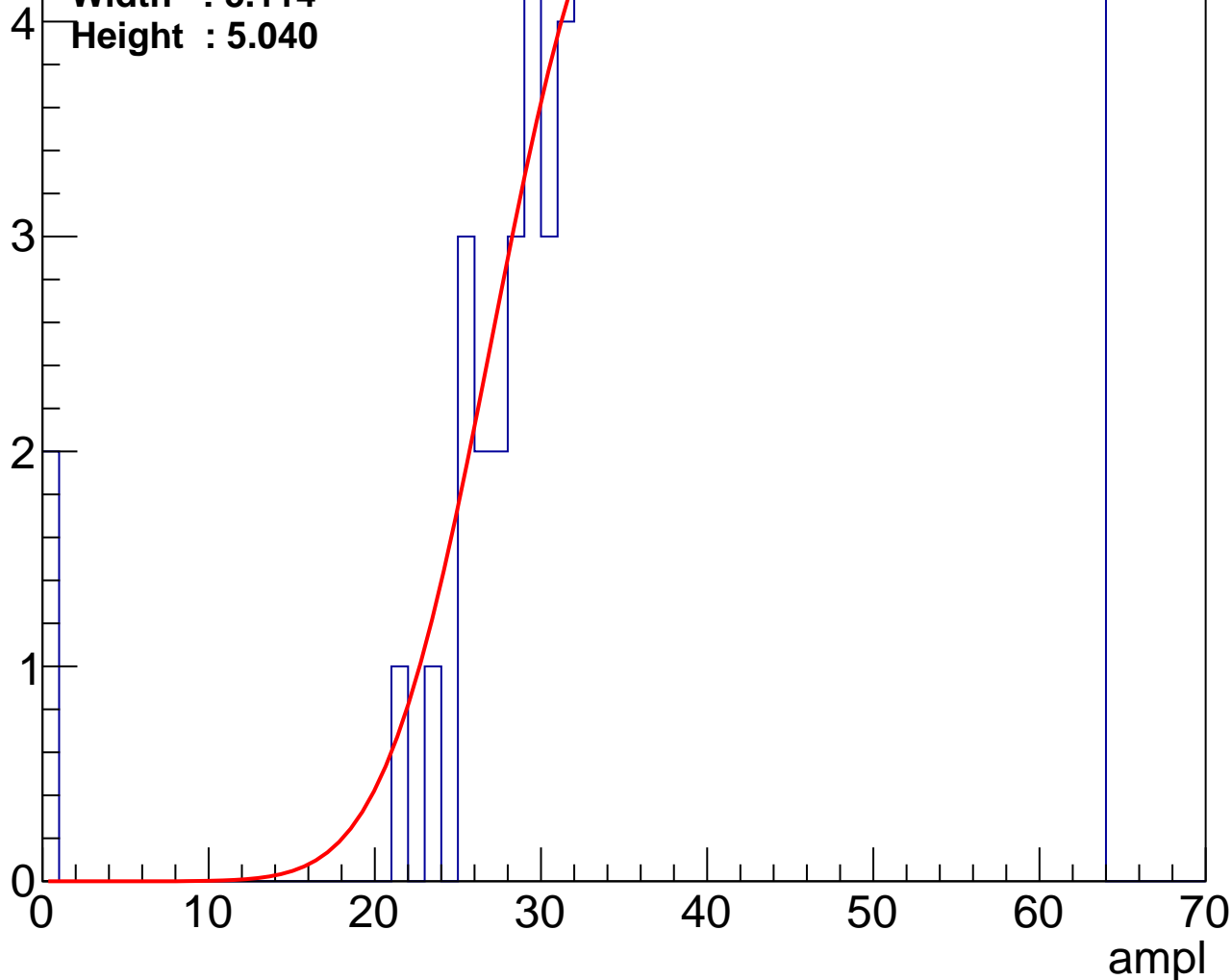
Width : 5.114

Height : 5.040

Entries 186

Mean 44.45

Std Dev 11.79



B1L103S, U15-ch94

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.1962

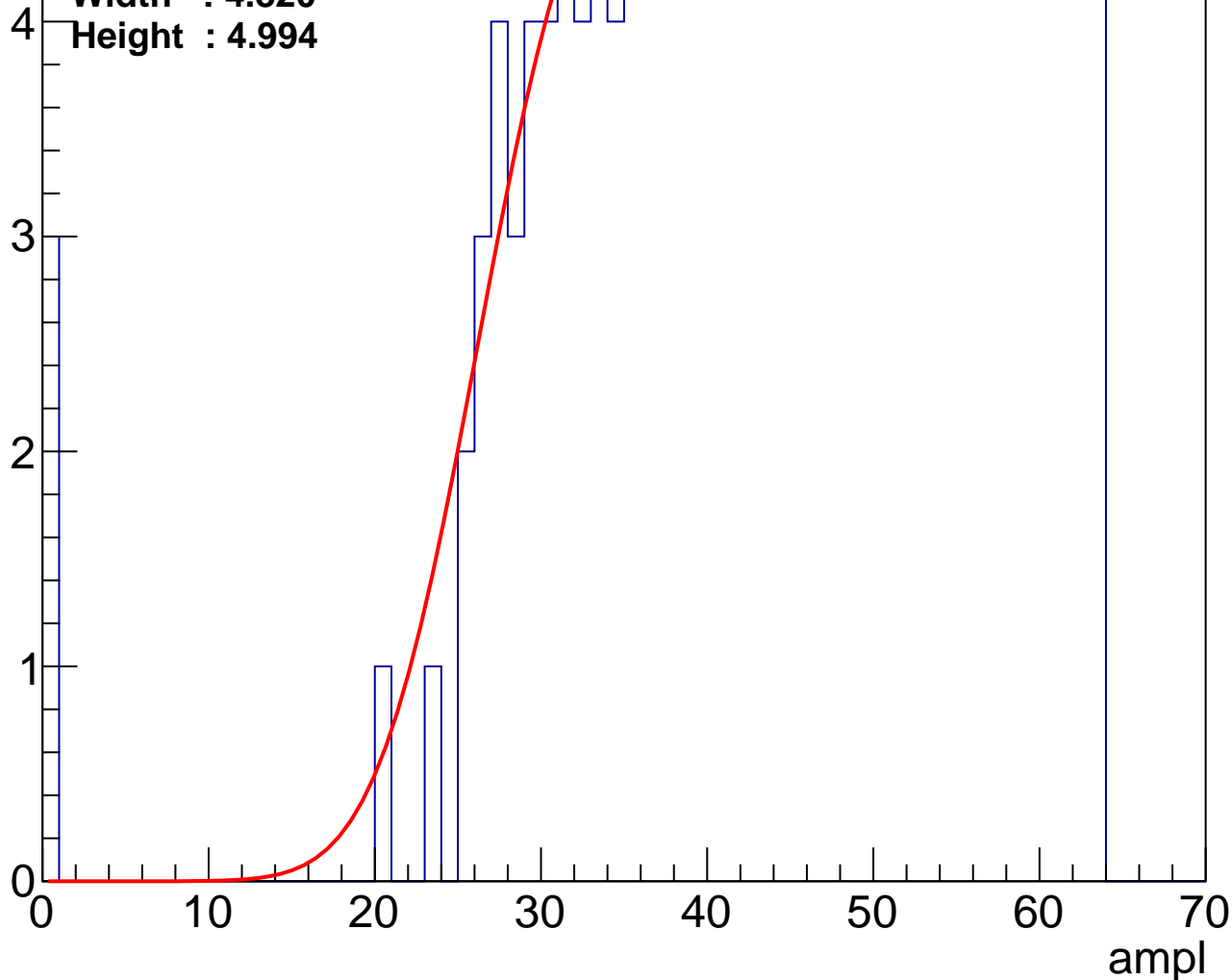
Width : 4.820

Height : 4.994

Entries 188

Mean 44.09

Std Dev 12.27



B1L103S, U15-ch95

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.5332

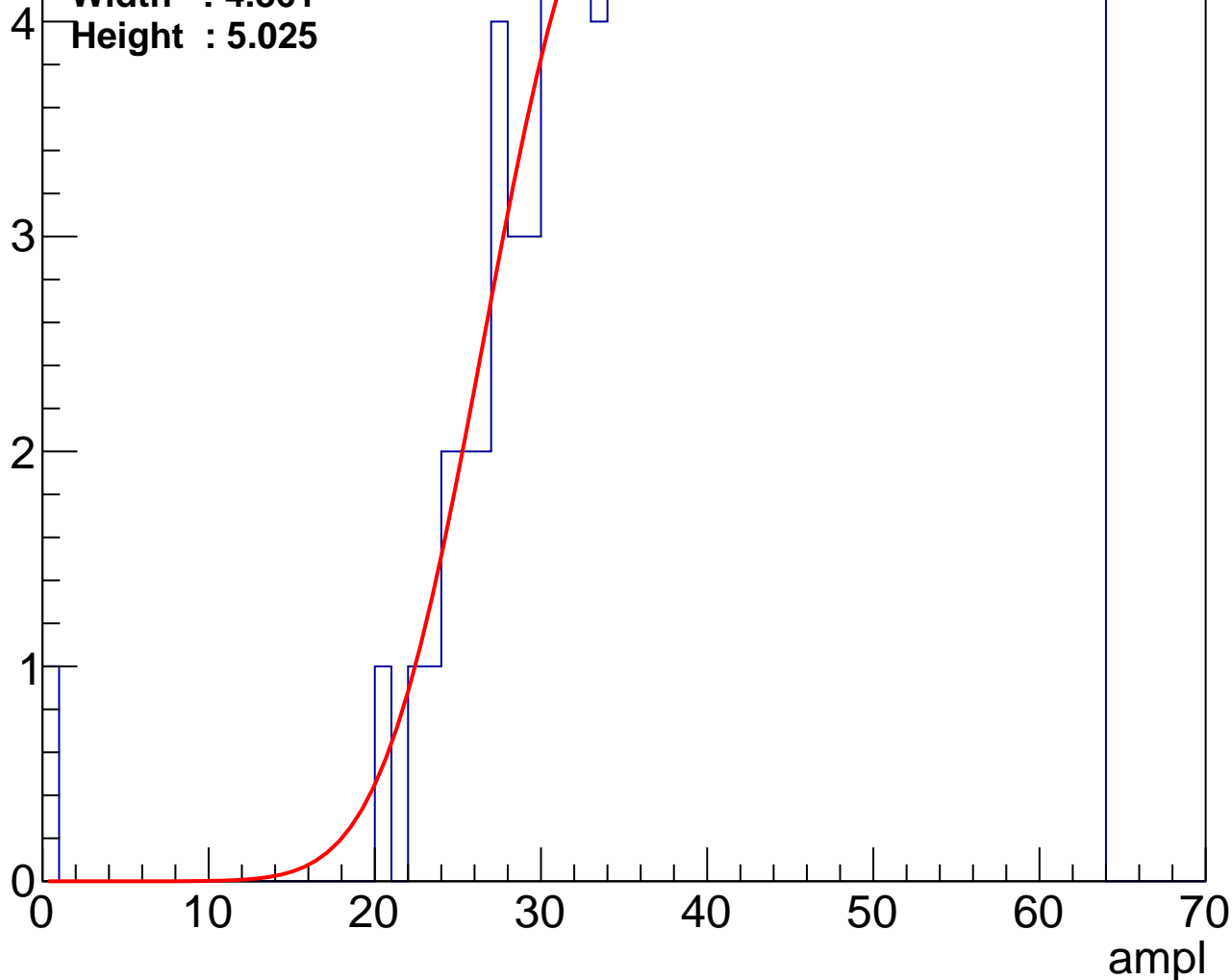
Width : 4.861

Height : 5.025

Entries 189

Mean 44.26

Std Dev 11.61



B1L103S, U15-ch96

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

5

Profile : 00011100

Turn on : 25.0688

Width : 4.481

Height : 5.020

4

3

2

1

0

Entries	196
Mean	43.13
Std Dev	13.13

ampl

0

10

20

30

40

50

60

70

0

B1L103S, U15-ch97

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.9441

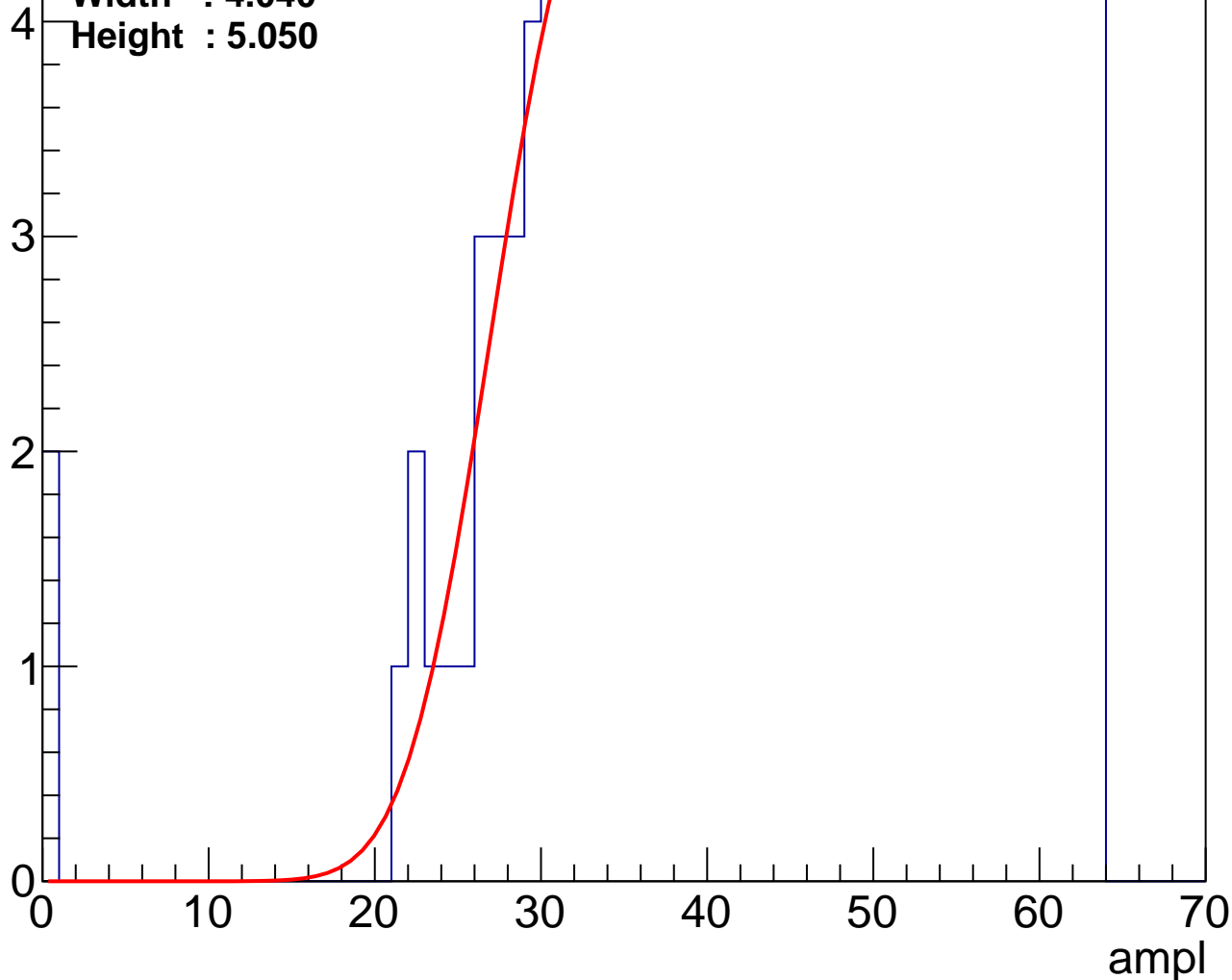
Width : 4.040

Height : 5.050

Entries 191

Mean 43.98

Std Dev 12



B1L103S, U15-ch98

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

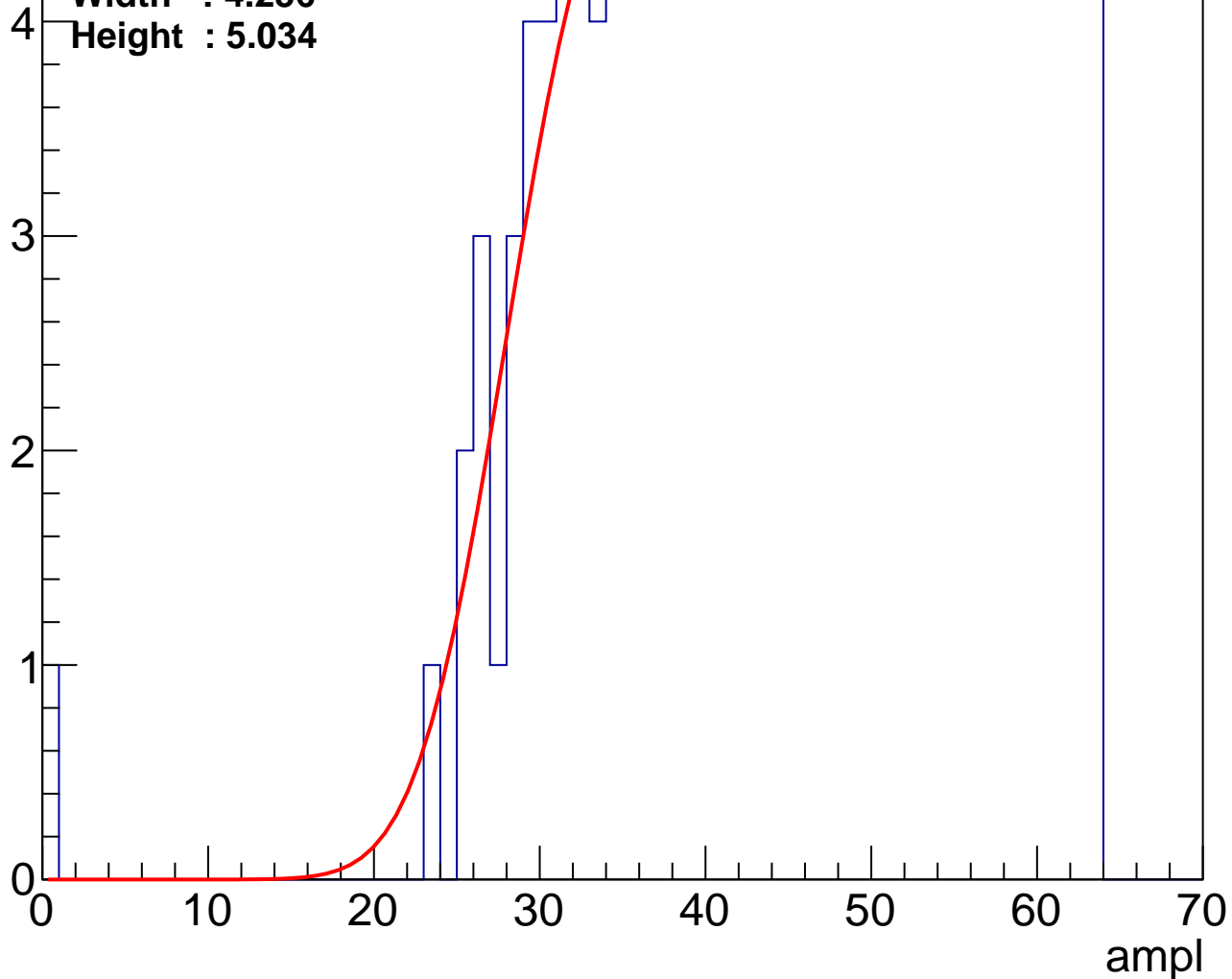
Profile : 00011100

Turn on : 27.9729

Width : 4.256

Height : 5.034

Entries	183
Mean	44.92
Std Dev	11.21



B1L103S, U15-ch99

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.3175

Width : 3.886

Height : 5.042

Entries 194

Mean 43.61

Std Dev 12.39

3

2

1

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U15-ch100

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.5701

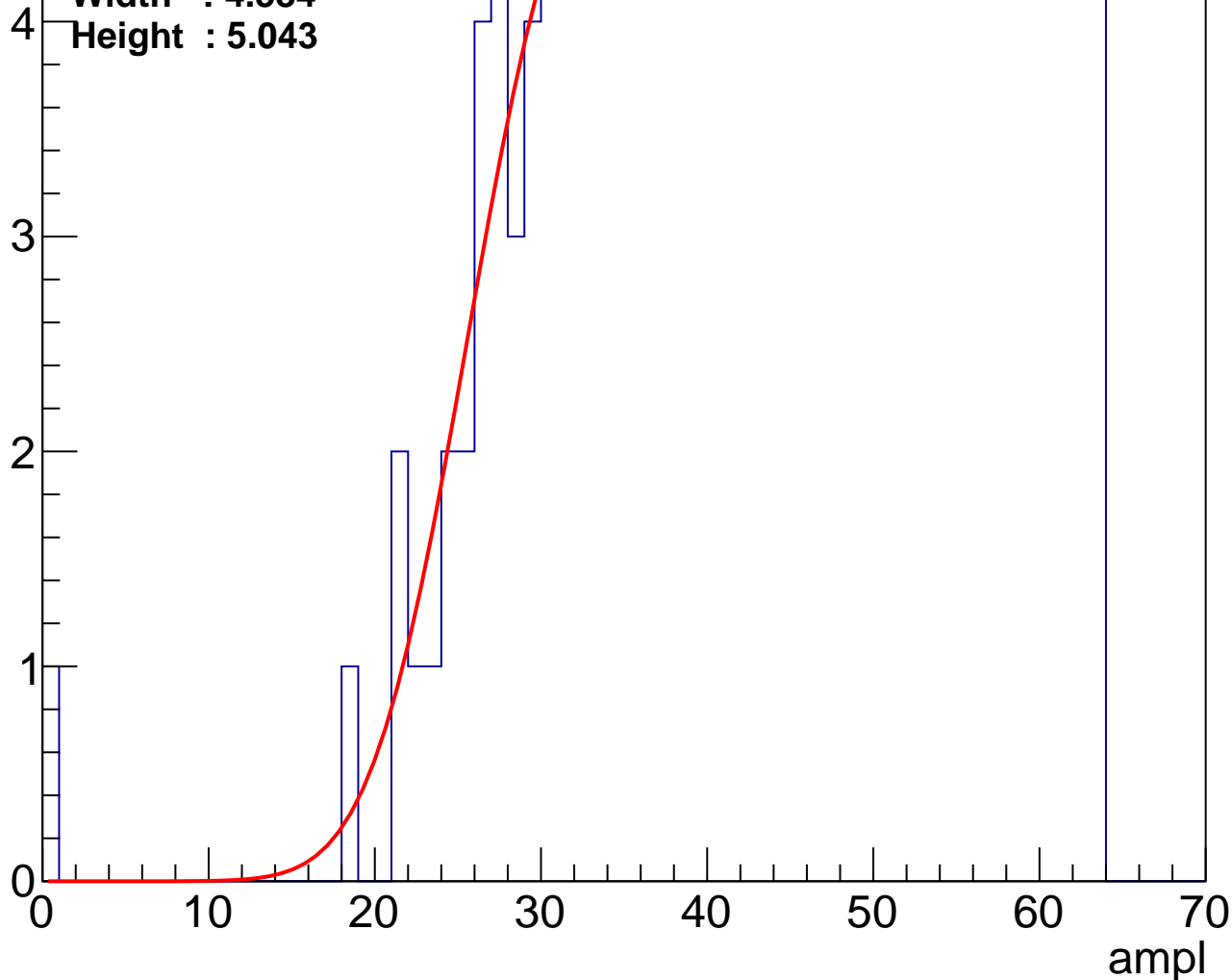
Width : 4.584

Height : 5.043

Entries 196

Mean 43.61

Std Dev 11.94



B1L103S, U15-ch101

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

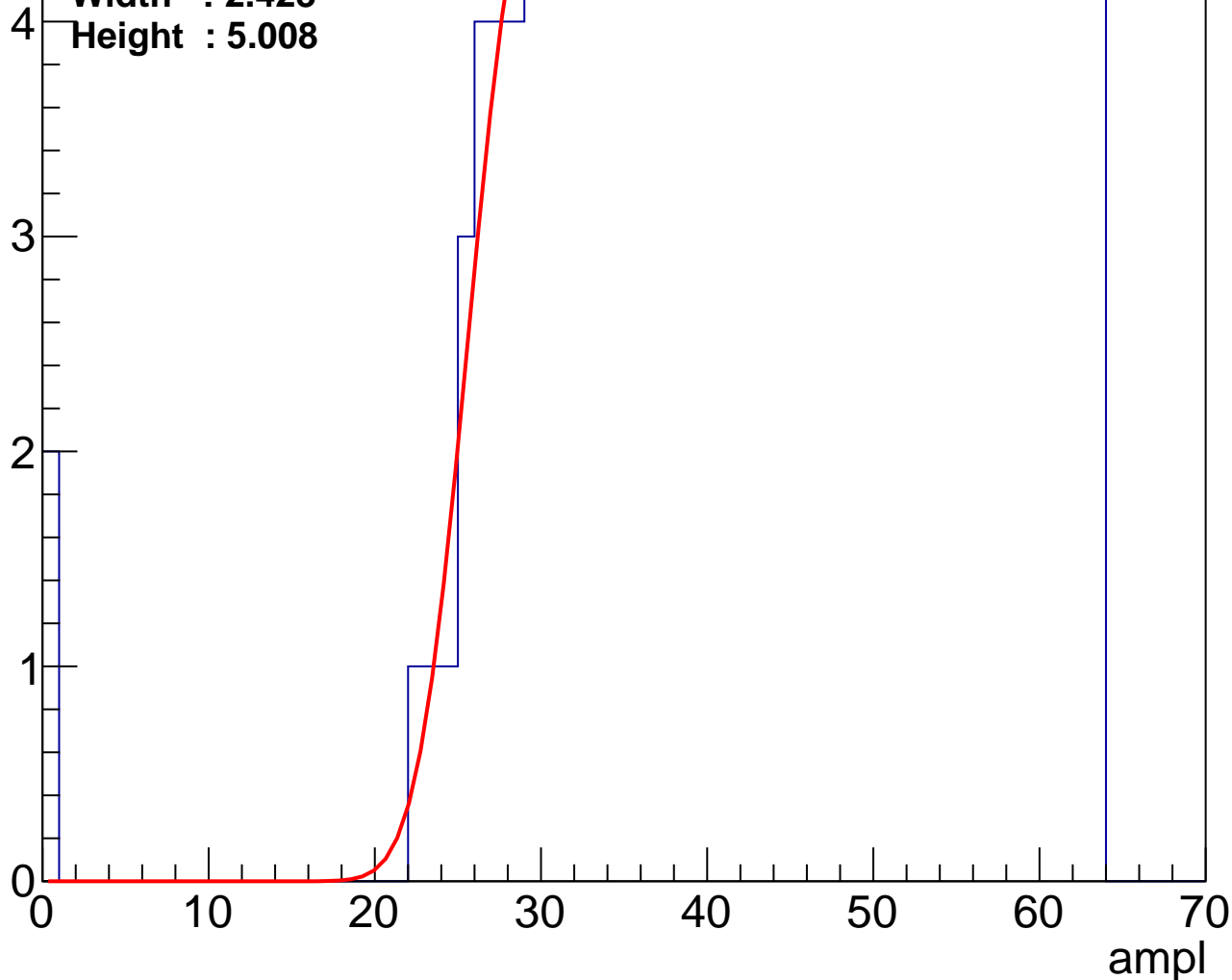
Profile : 00011100

Turn on : 25.5853

Width : 2.423

Height : 5.008

Entries	195
Mean	43.68
Std Dev	12.04



B1L103S, U15-ch102

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 23.9277

Width : 4.097

Height : 4.975

Entries	201
Mean	43
Std Dev	12.46

4

3

2

1

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U15-ch103

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

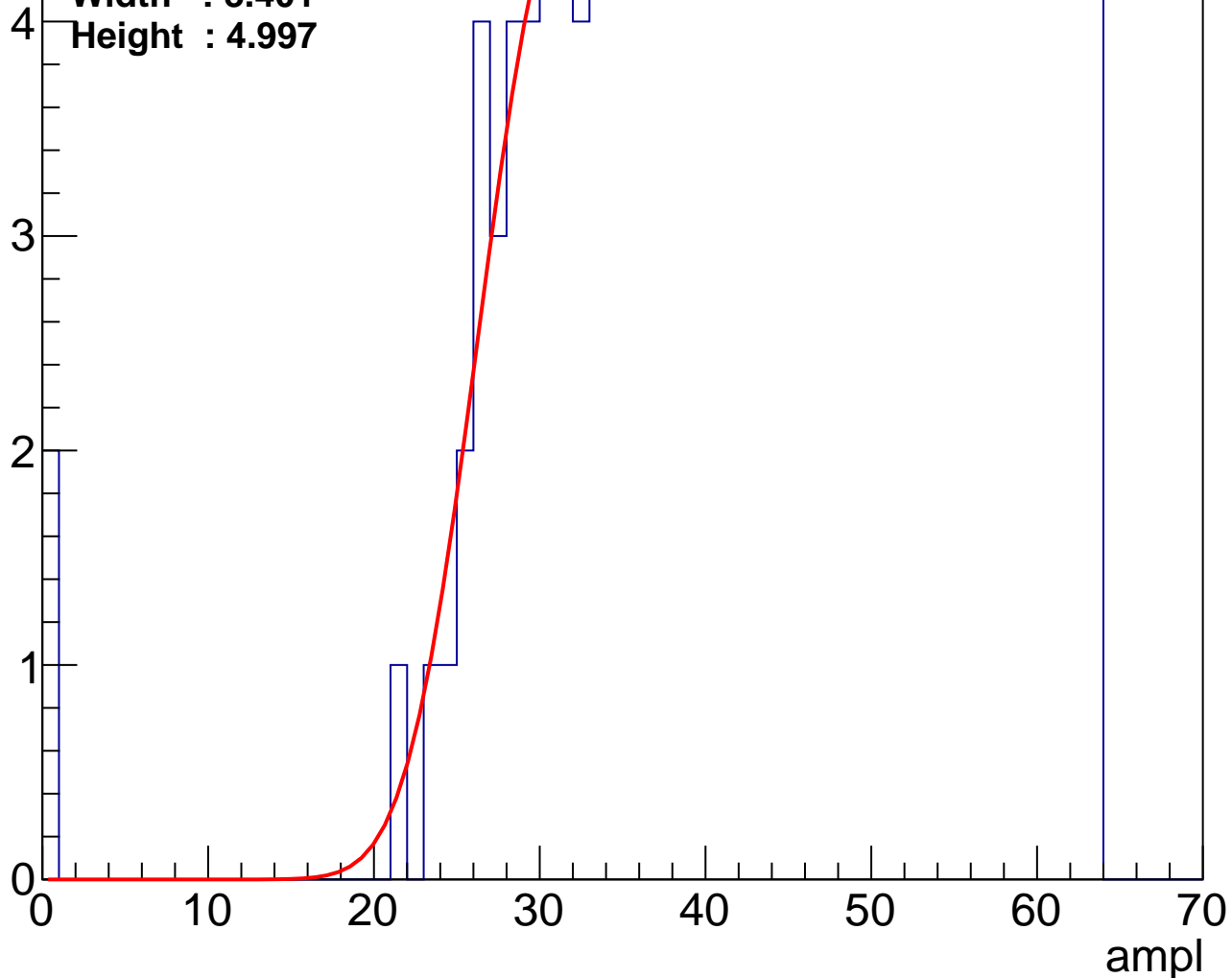
Profile : 00011100

Turn on : 26.2246

Width : 3.401

Height : 4.997

Entries	191
Mean	44
Std Dev	11.96



B1L103S, U15-ch104

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

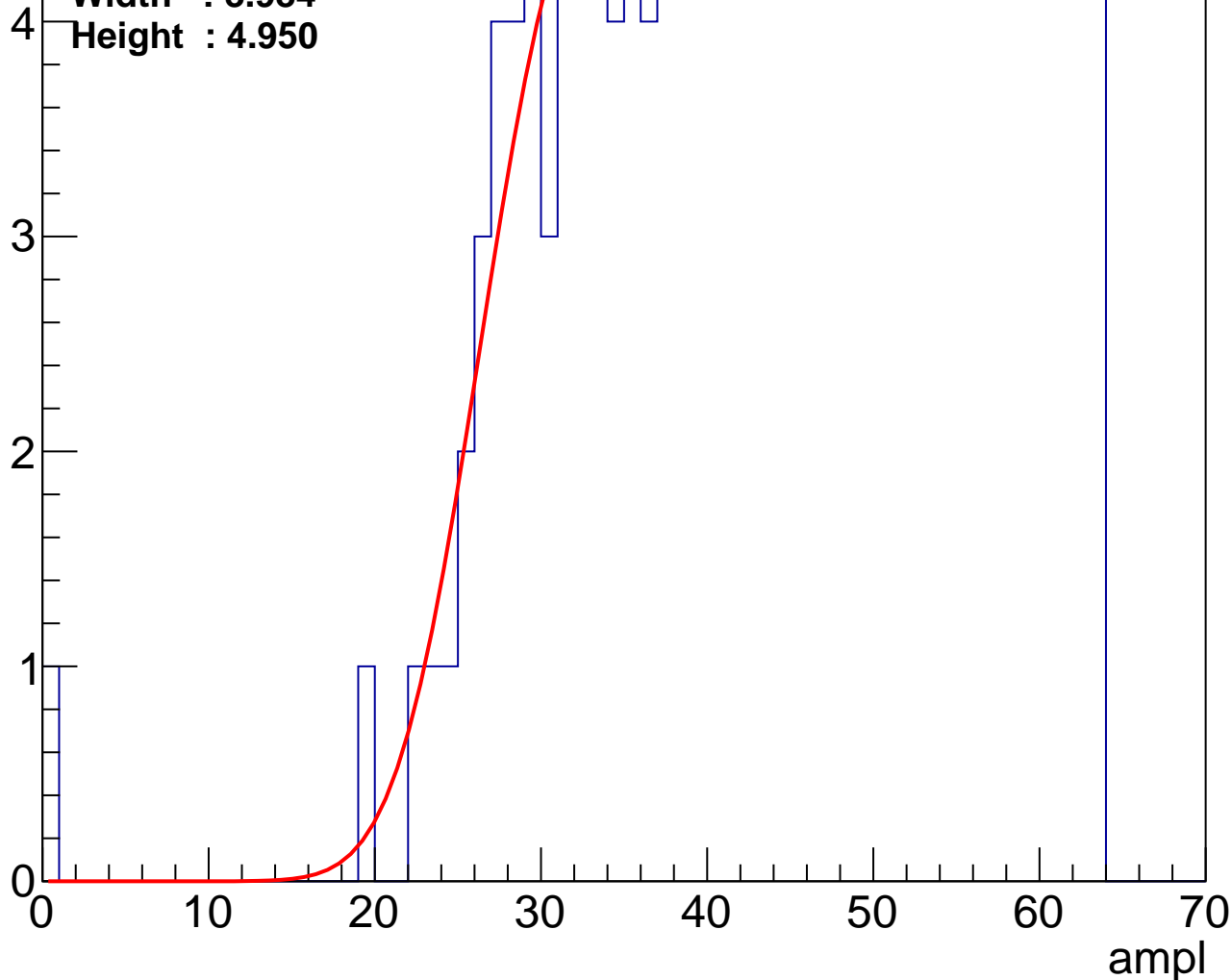
Profile : 00011100

Turn on : 26.3146

Width : 3.984

Height : 4.950

Entries	189
Mean	44.21
Std Dev	11.67



B1L103S, U15-ch105

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

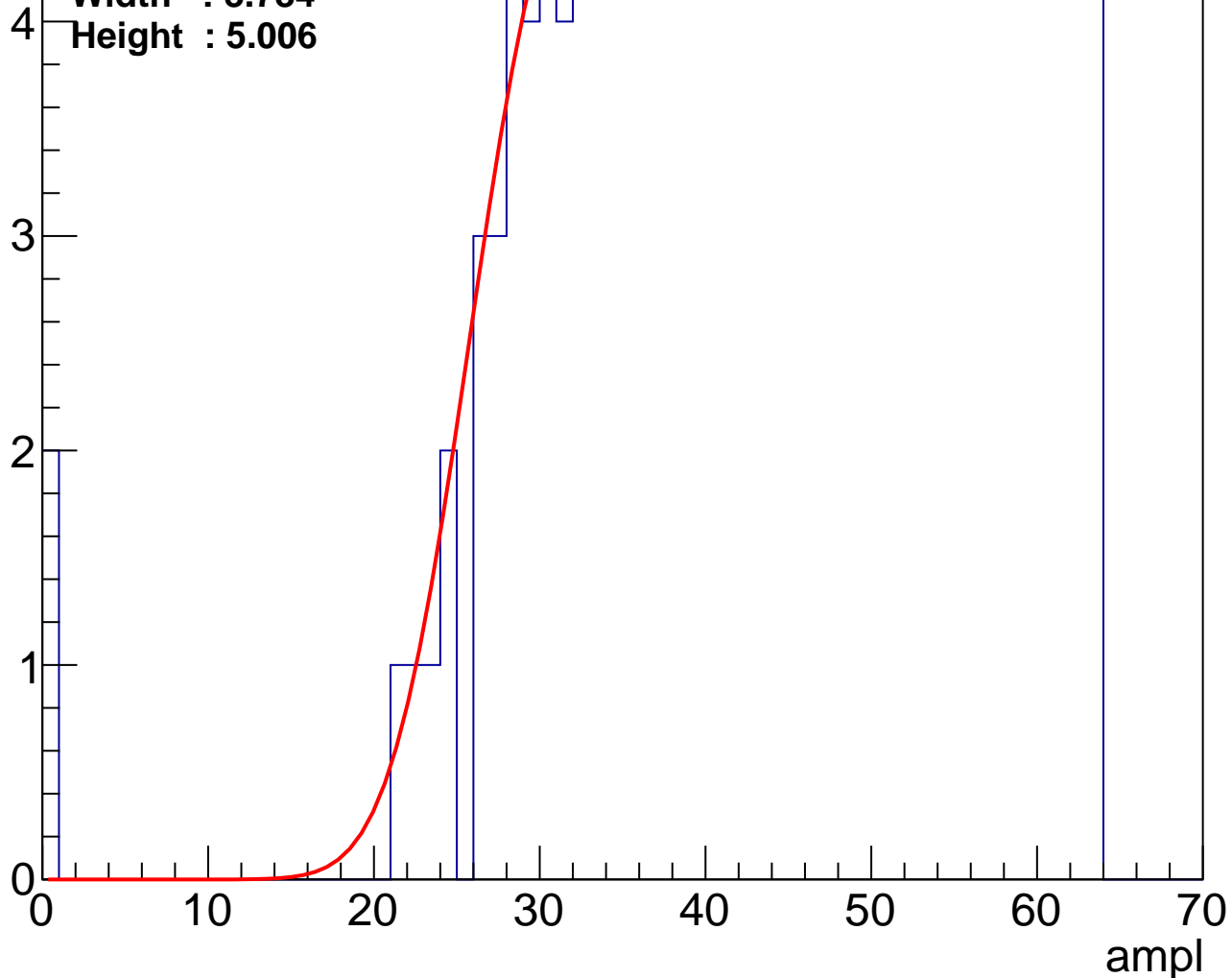
Profile : 00011100

Turn on : 25.7390

Width : 3.784

Height : 5.006

Entries	191
Mean	43.99
Std Dev	11.98



B1L103S, U15-ch106

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.3484

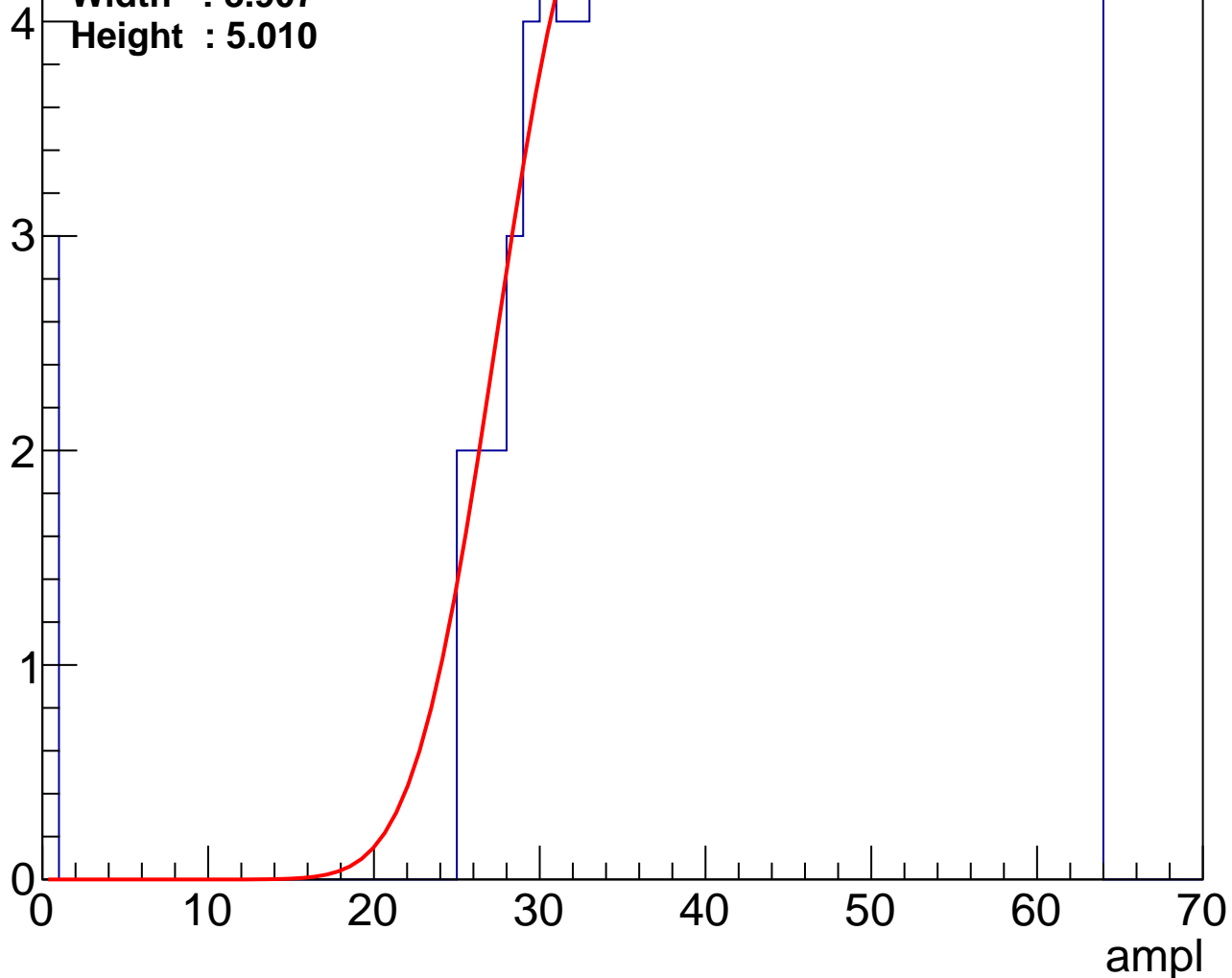
Width : 3.907

Height : 5.010

Entries 184

Mean 44.55

Std Dev 12



B1L103S, U15-ch107

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

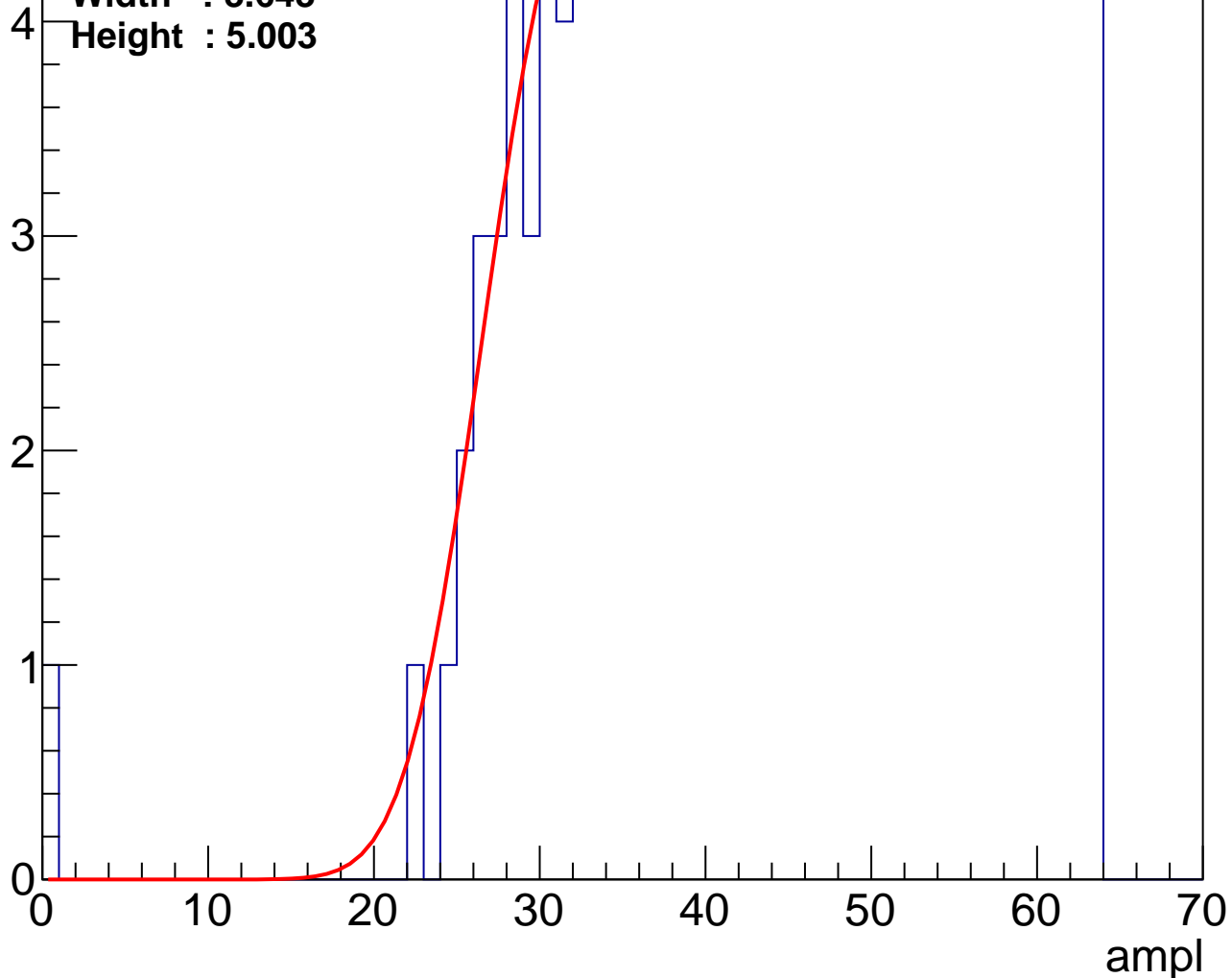
Profile : 00011100

Turn on : 26.4956

Width : 3.643

Height : 5.003

Entries	188
Mean	44.45
Std Dev	11.43



B1L103S, U15-ch108

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.5809

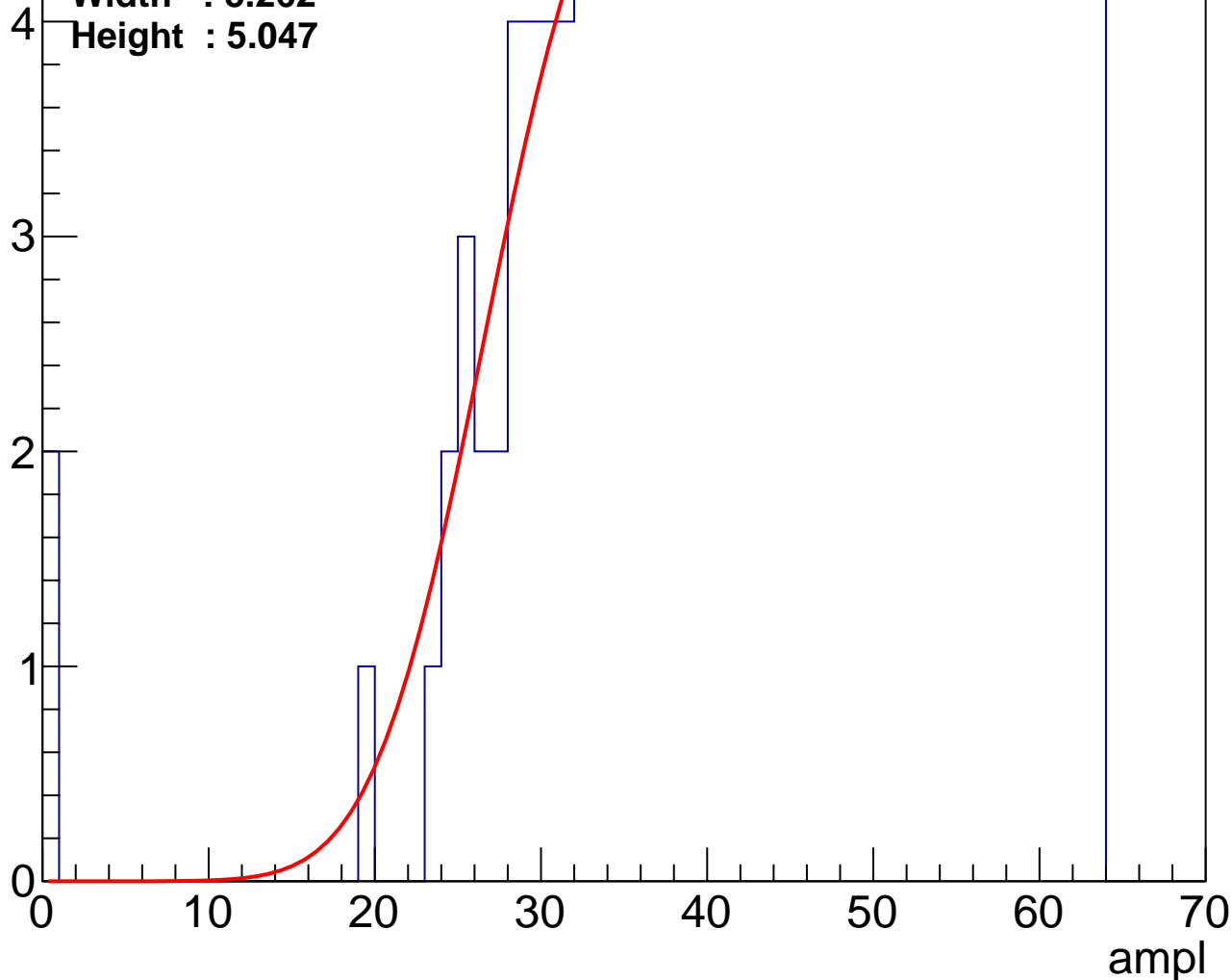
Width : 5.262

Height : 5.047

Entries 189

Mean 44.14

Std Dev 11.96



B1L103S, U15-ch109

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.6661

Width : 3.220

Height : 4.987

Entries 189

Mean 44.05

Std Dev 12.22

3

2

1

0

ampl

0

10

20

30

40

50

60

70

5

4

3

2

1

0

B1L103S, U15-ch110

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.3521

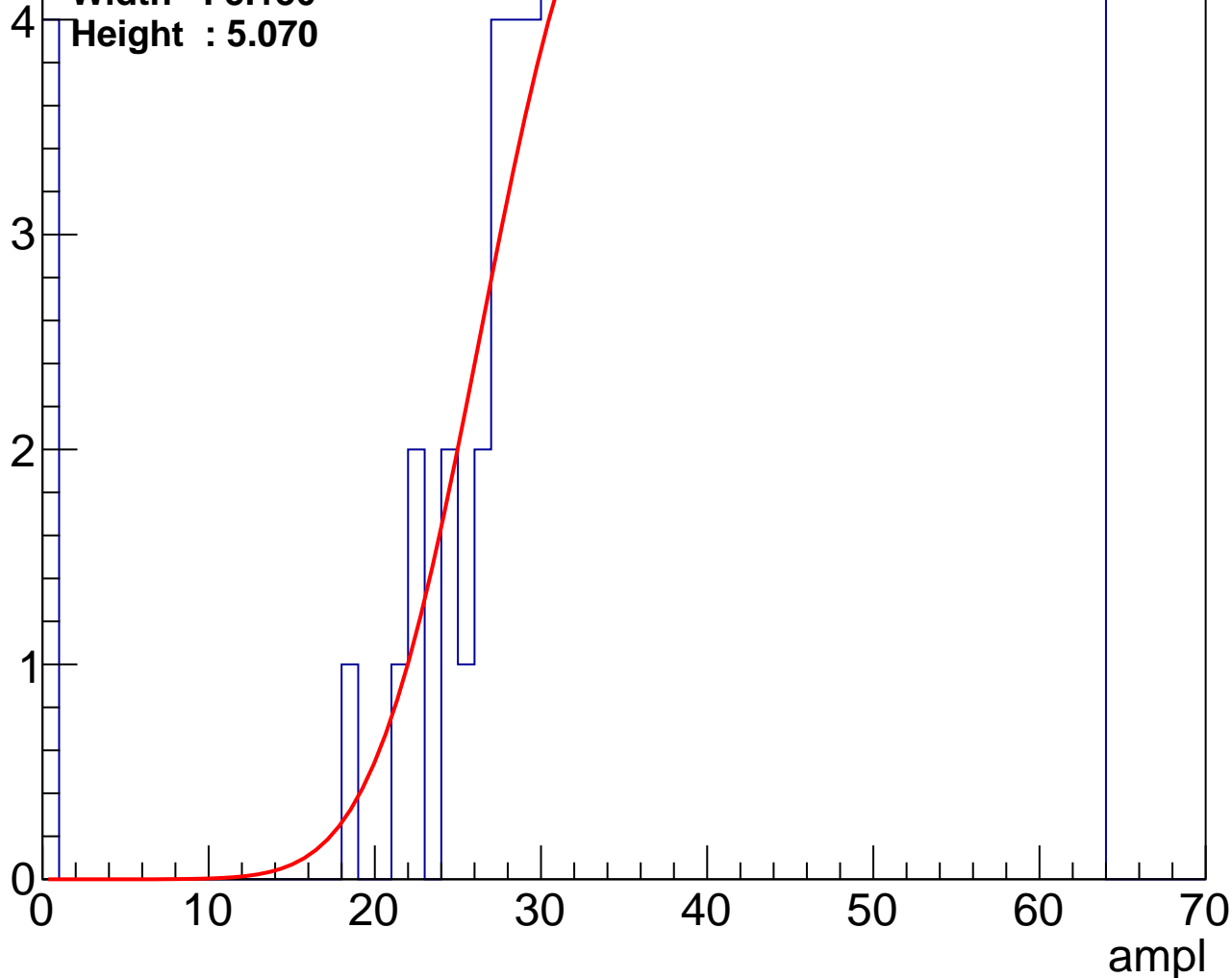
Width : 5.130

Height : 5.070

Entries 195

Mean 43.33

Std Dev 12.84



B1L103S, U15-ch111

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.3243

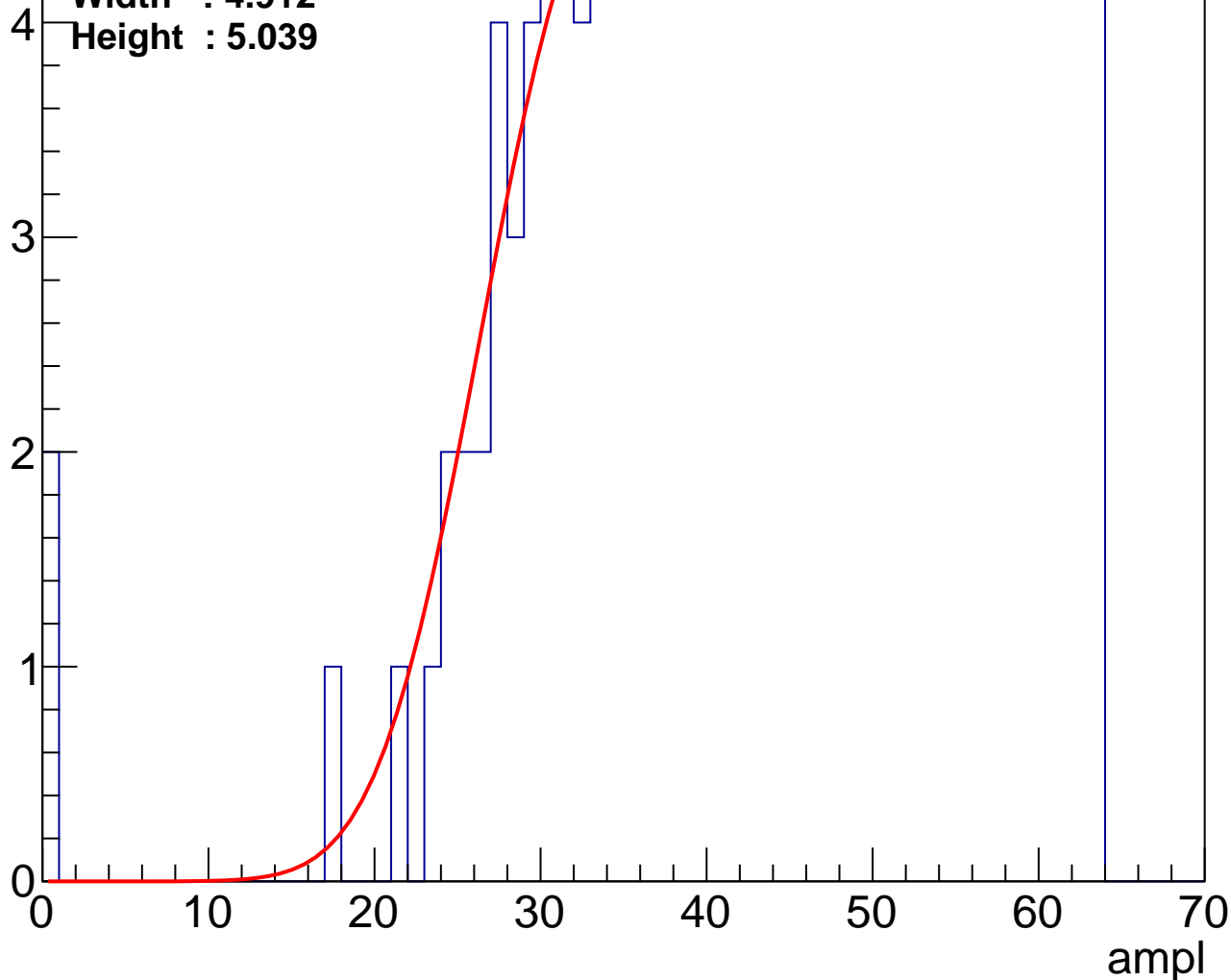
Width : 4.912

Height : 5.039

Entries 191

Mean 43.94

Std Dev 12.07



B1L103S, U15-ch112

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.5357

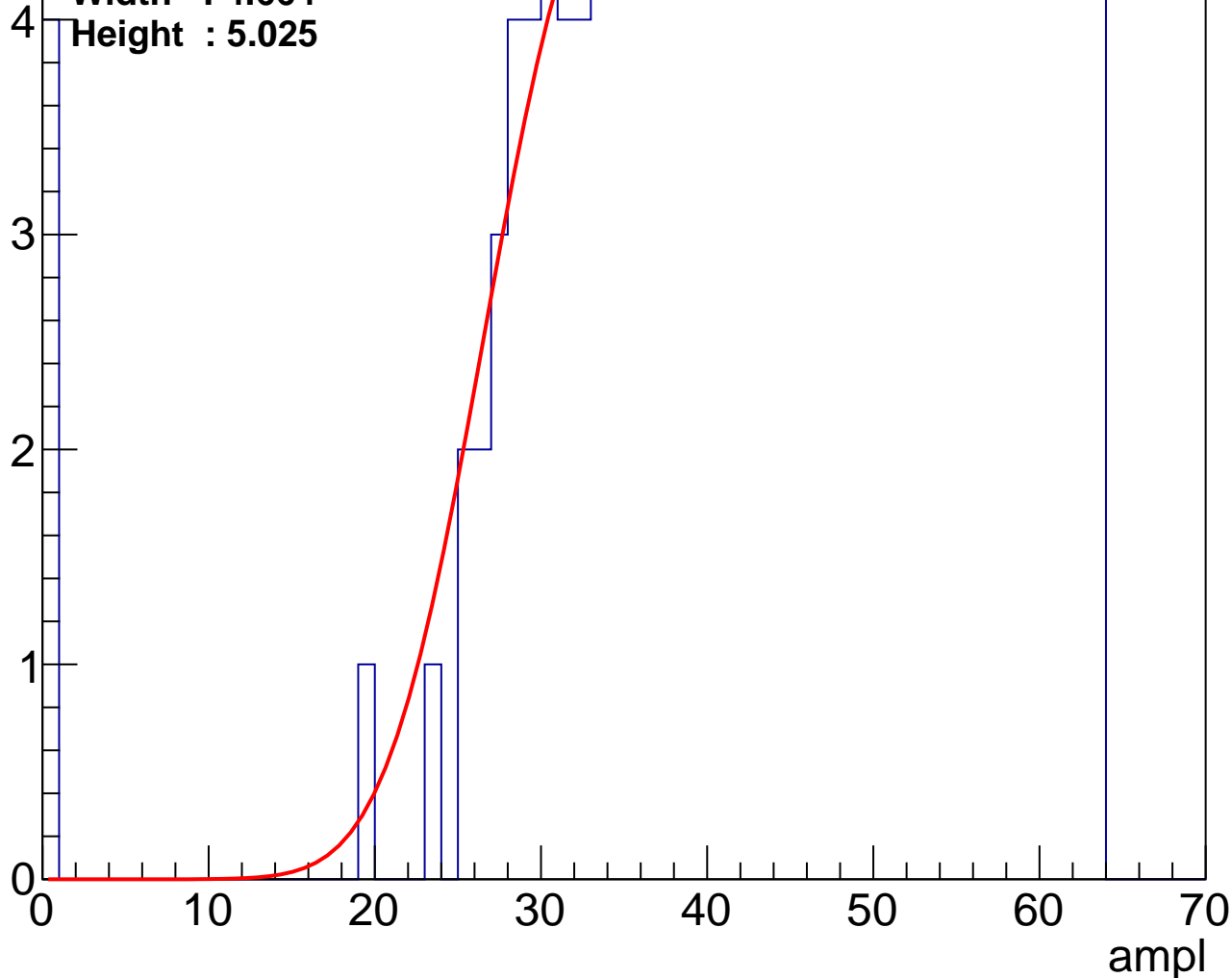
Width : 4.664

Height : 5.025

Entries 189

Mean 43.89

Std Dev 12.62



B1L103S, U15-ch113

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 28.7941

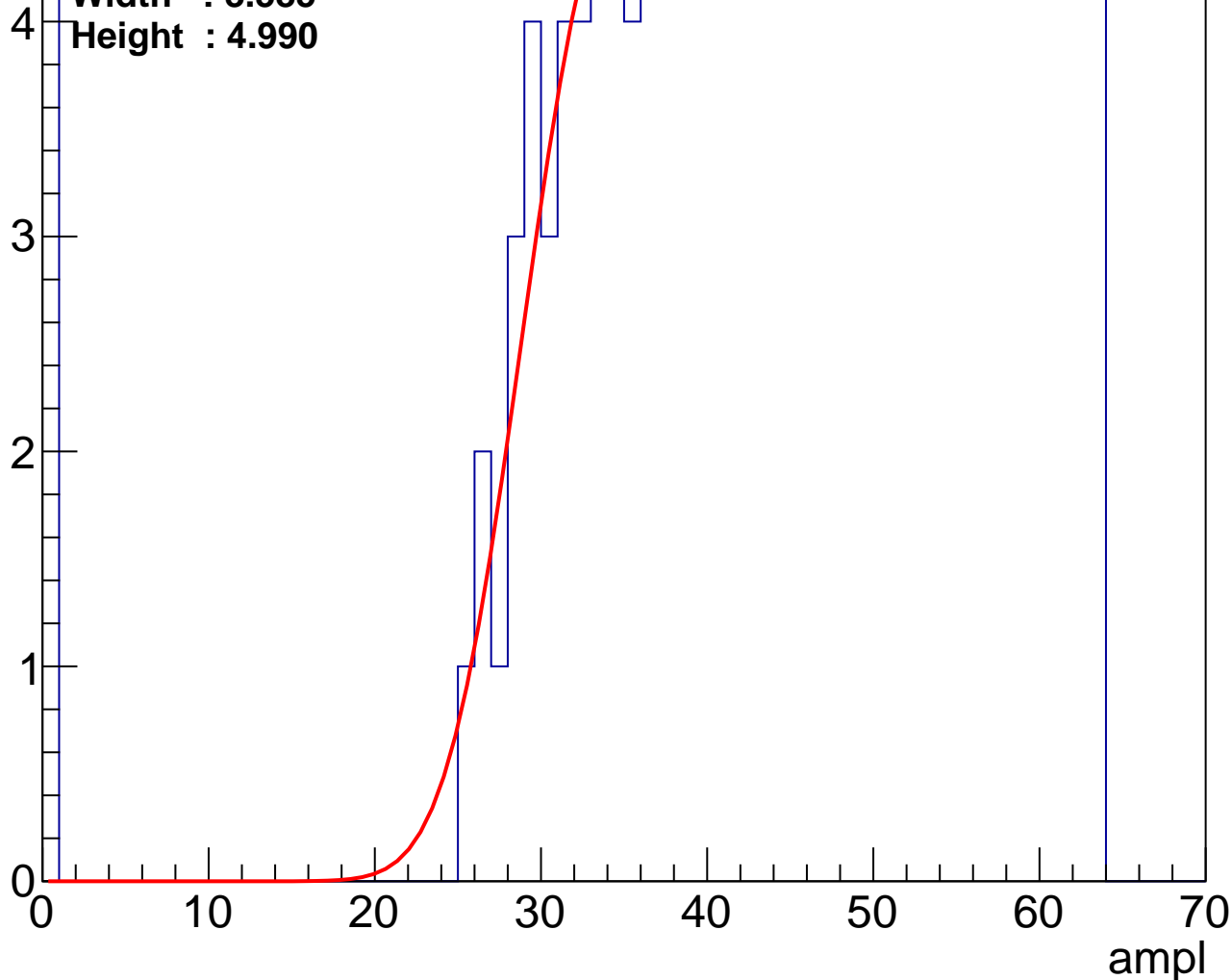
Width : 3.585

Height : 4.990

Entries 181

Mean 44.48

Std Dev 12.71



B1L103S, U15-ch114

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

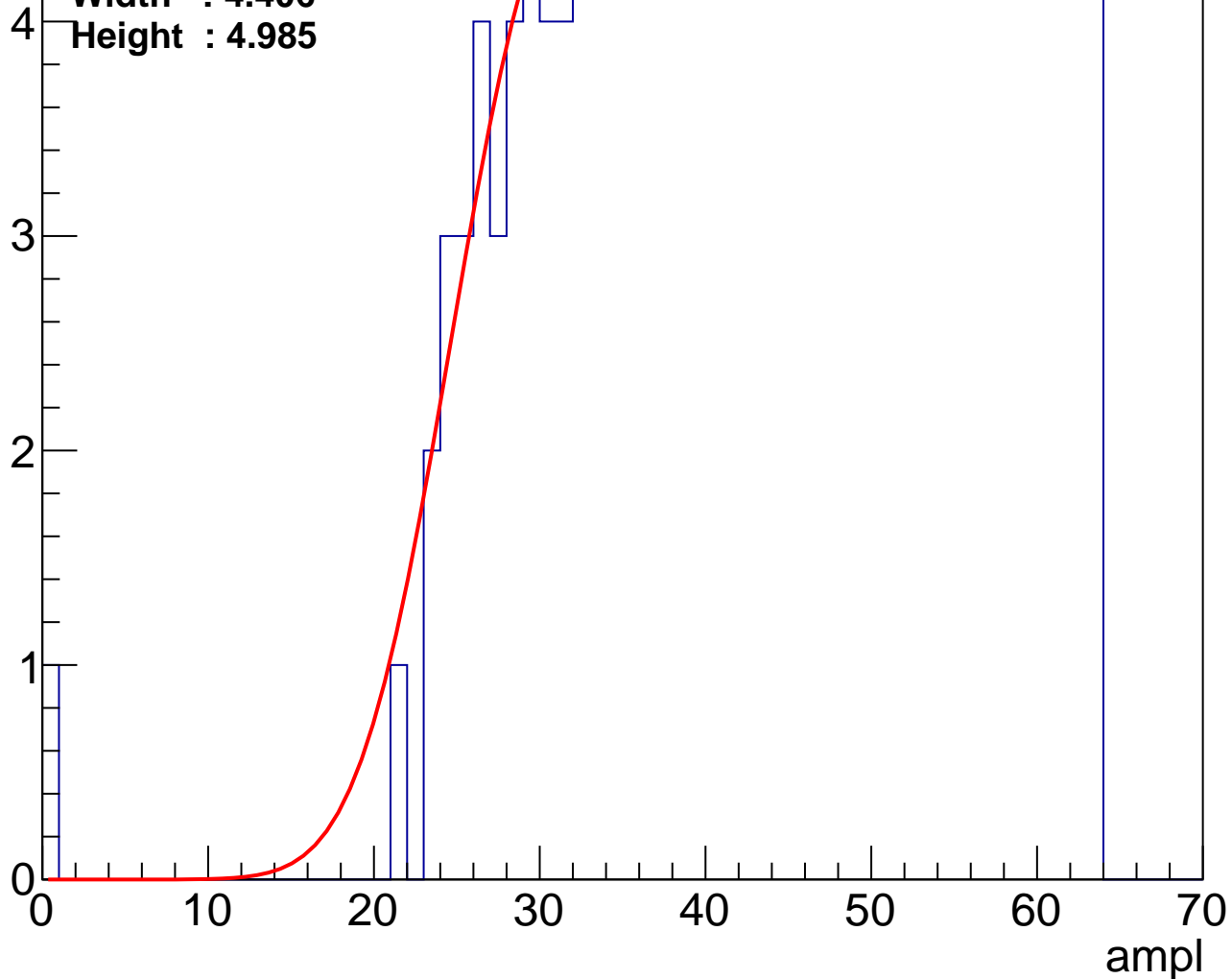
Profile : 00011100

Turn on : 24.6036

Width : 4.406

Height : 4.985

Entries	194
Mean	43.81
Std Dev	11.8



B1L103S, U15-ch115

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.7910

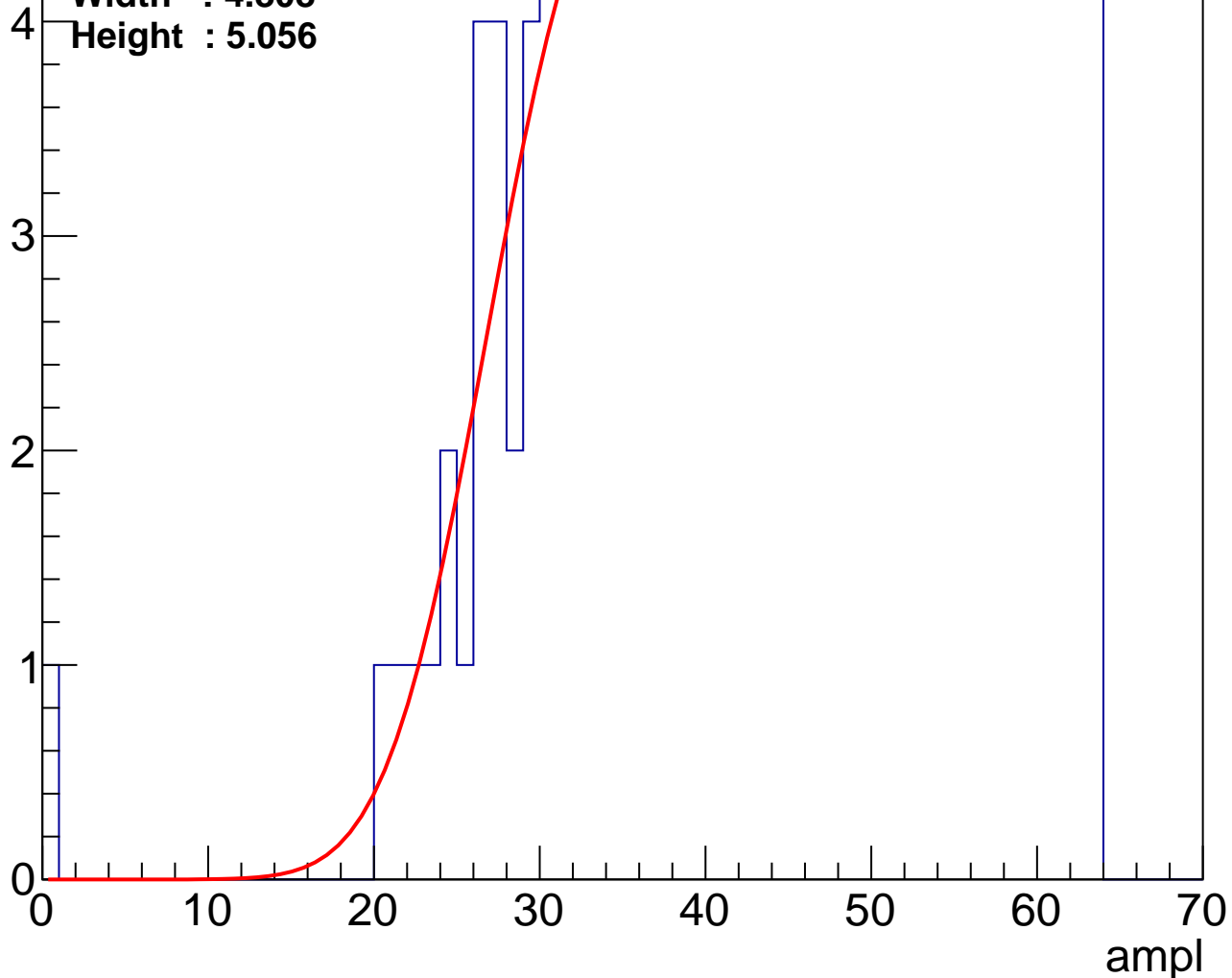
Width : 4.808

Height : 5.056

Entries 192

Mean 44

Std Dev 11.73



B1L103S, U15-ch116

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.4055

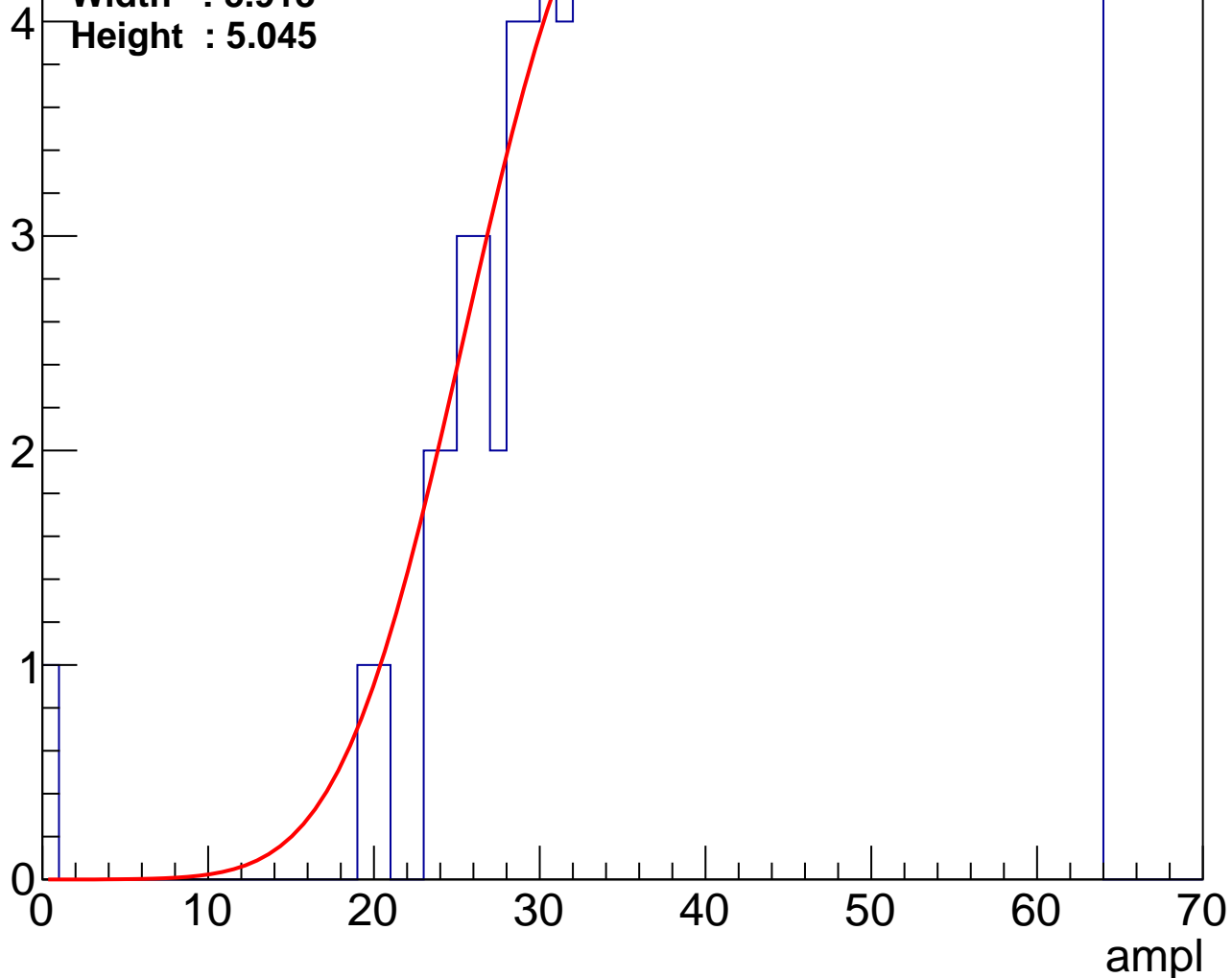
Width : 5.913

Height : 5.045

Entries 192

Mean 43.97

Std Dev 11.78



B1L103S, U15-ch117

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 27.1900

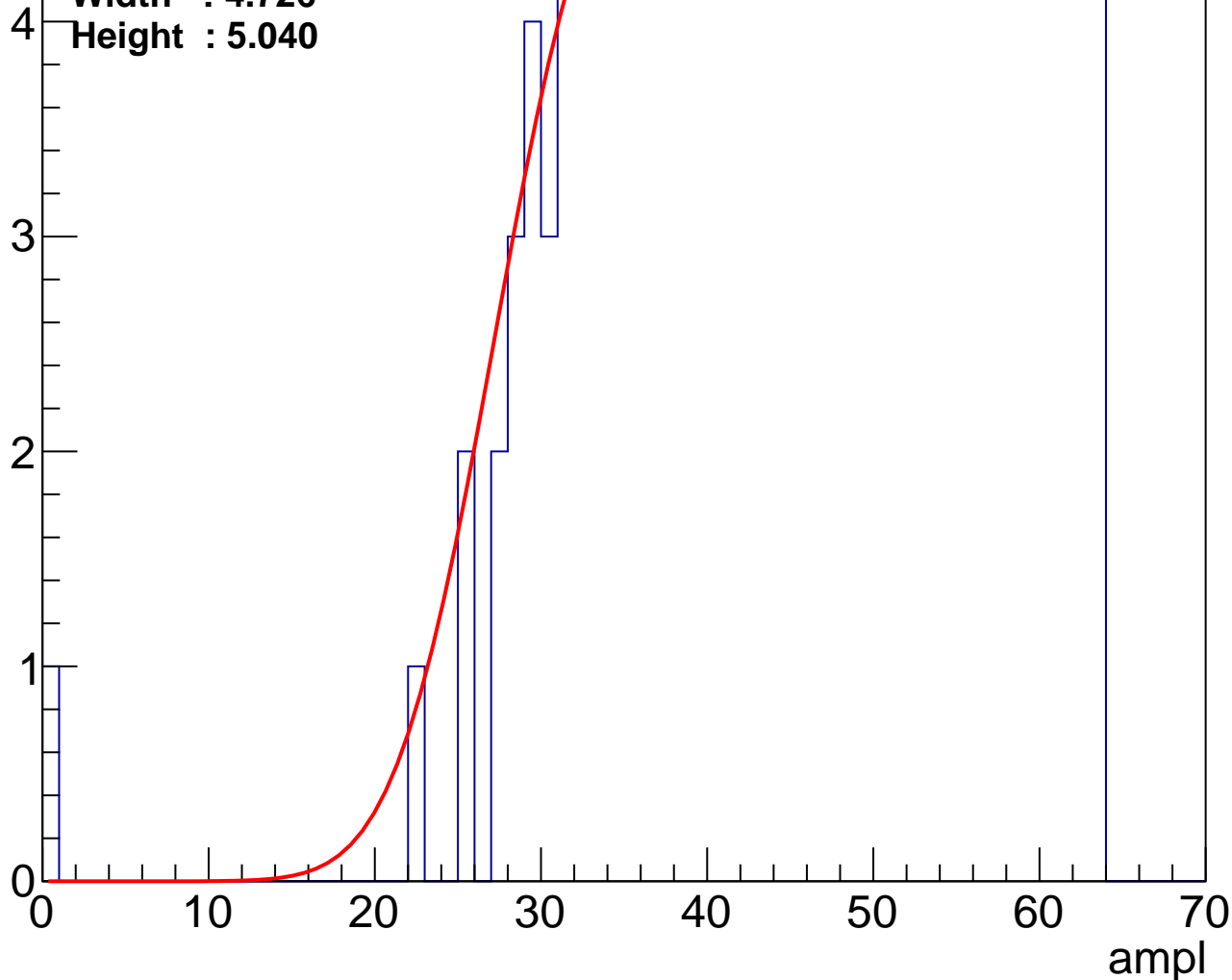
Width : 4.726

Height : 5.040

Entries 181

Mean 45.14

Std Dev 11.07



B1L103S, U15-ch118

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 23.6171

Width : 4.883

Height : 5.026

Entries 201

Mean 43.01

Std Dev 12.47

2

1

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U15-ch119

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.6065

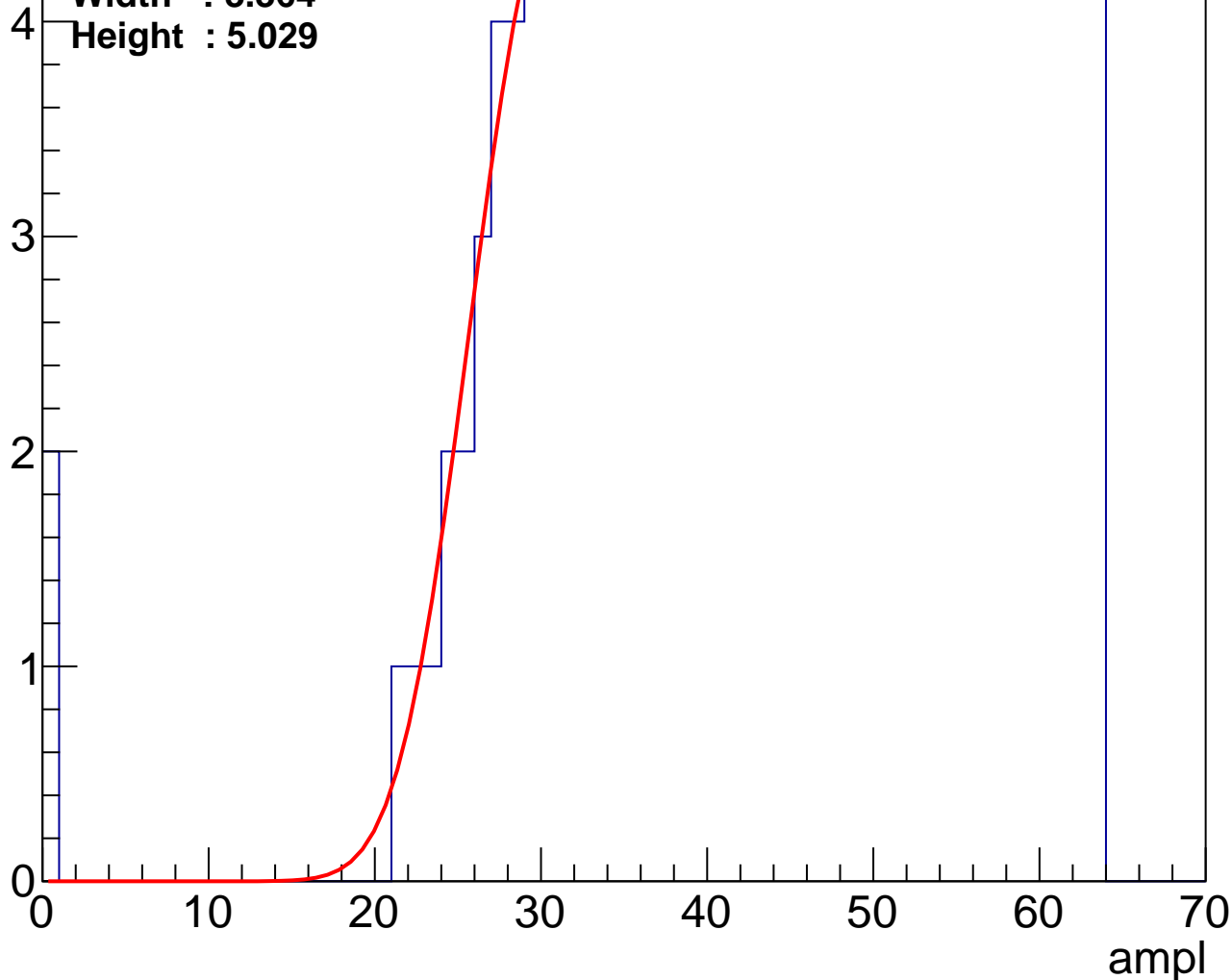
Width : 3.364

Height : 5.029

Entries 195

Mean 43.65

Std Dev 12.09



B1L103S, U15-ch120

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.3333

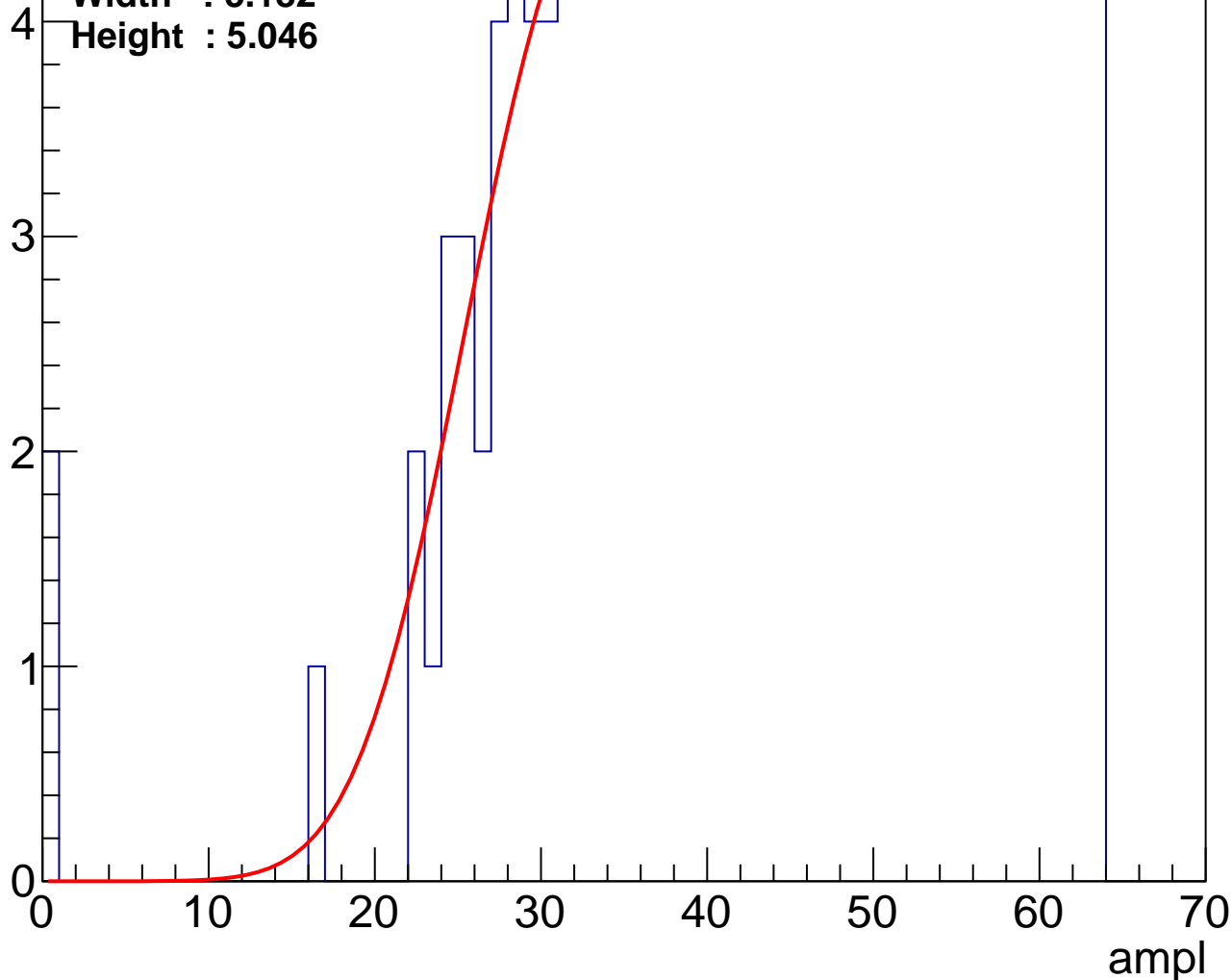
Width : 5.182

Height : 5.046

Entries 196

Mean 43.47

Std Dev 12.27



B1L103S, U15-ch121

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

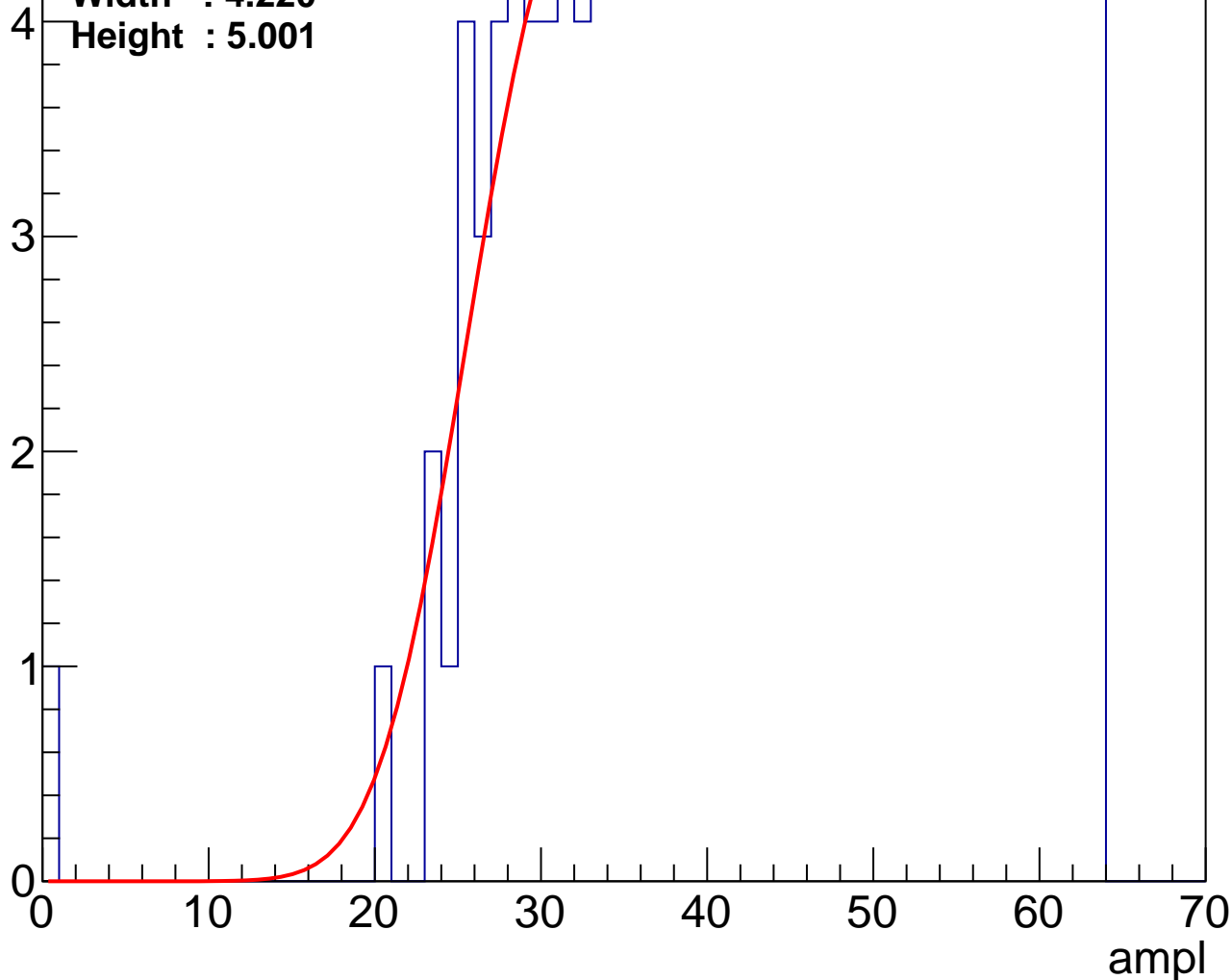
Profile : 00011100

Turn on : 25.5011

Width : 4.220

Height : 5.001

Entries	193
Mean	43.91
Std Dev	11.75



B1L103S, U15-ch122

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.9749

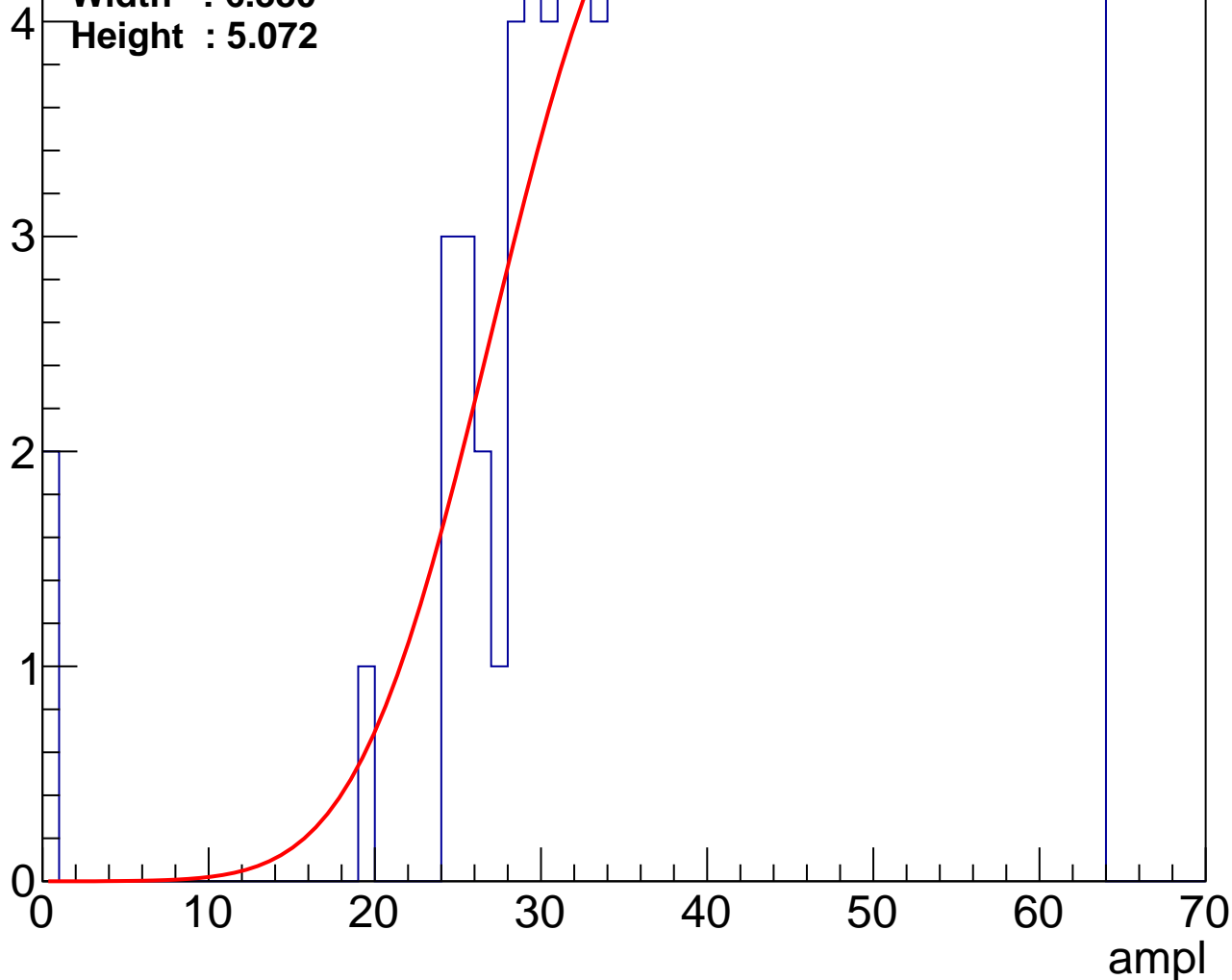
Width : 6.380

Height : 5.072

Entries 189

Mean 44.15

Std Dev 11.95



B1L103S, U15-ch123

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 26.8045

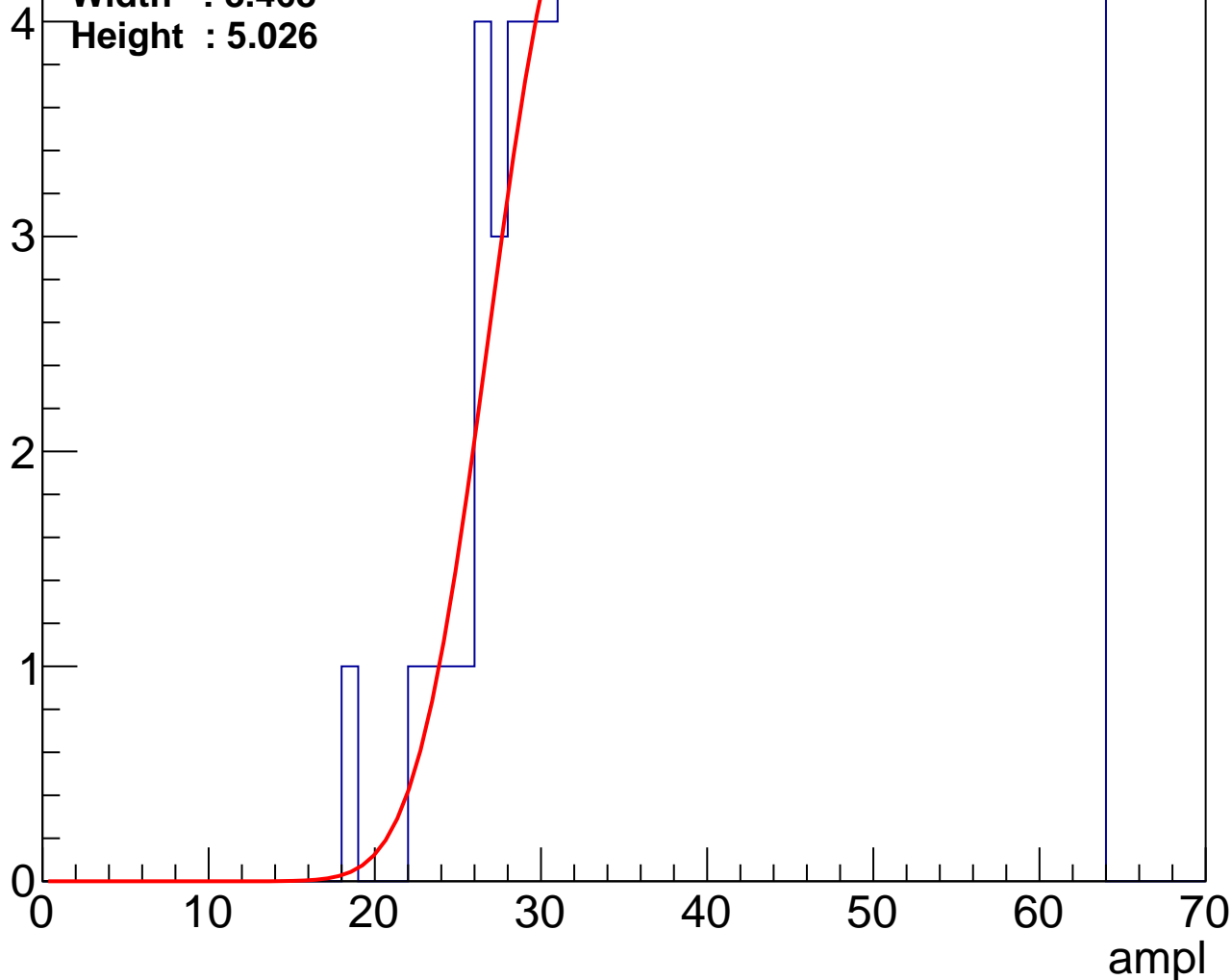
Width : 3.468

Height : 5.026

Entries 189

Mean 44.44

Std Dev 11.18



B1L103S, U15-ch124

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

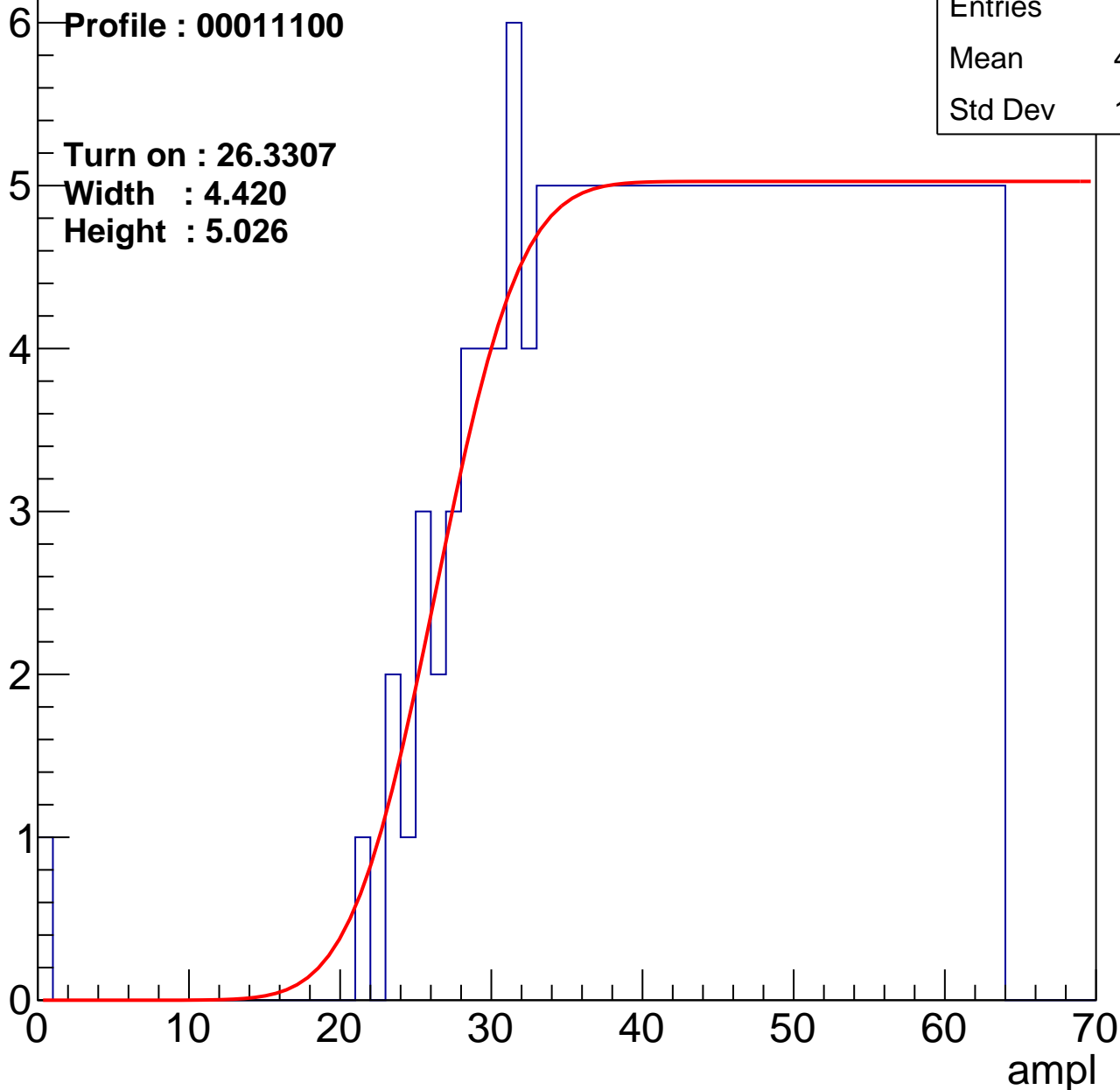
Entries	190
Mean	44.22
Std Dev	11.59

Profile : 00011100

Turn on : 26.3307

Width : 4.420

Height : 5.026



B1L103S, U15-ch125

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 28.3574

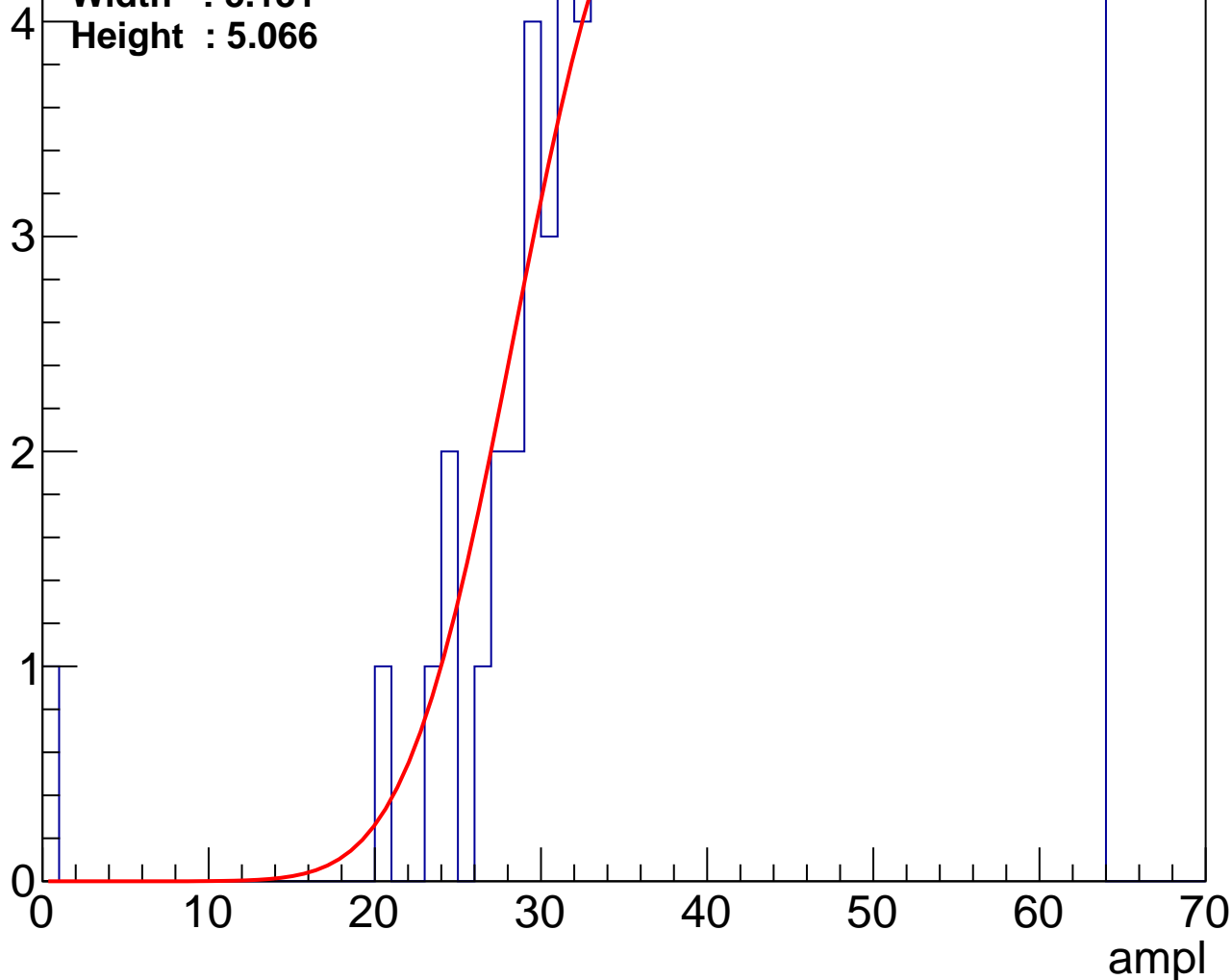
Width : 5.131

Height : 5.066

Entries 181

Mean 45.06

Std Dev 11.21



B1L103S, U15-ch126

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 23.7375

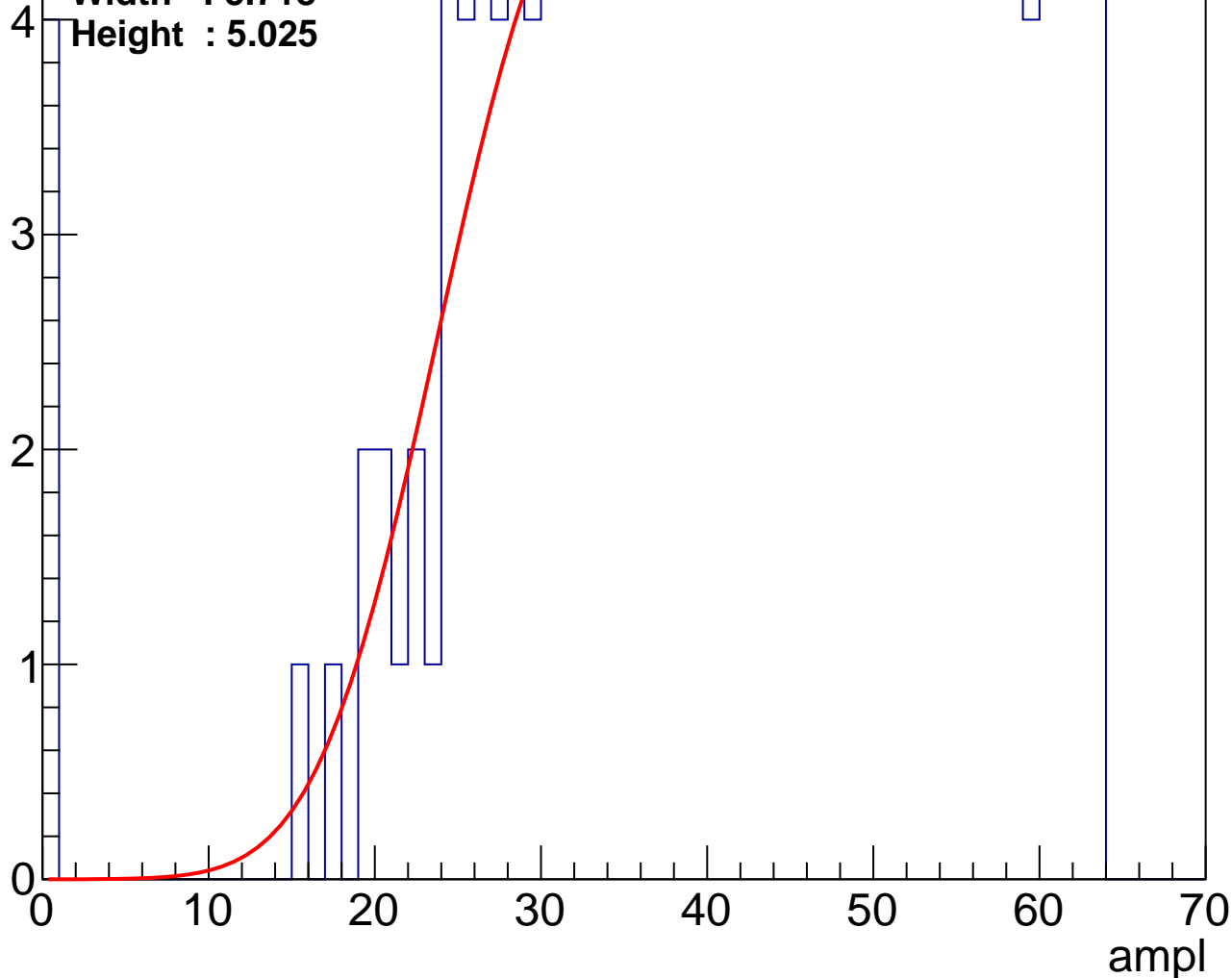
Width : 5.718

Height : 5.025

Entries 210

Mean 41.7

Std Dev 13.48



B1L103S, U15-ch127

calib_packv5_042523_0143.root, FC#7, Port C2

Entry

Profile : 00011100

Turn on : 25.8767

Width : 4.187

Height : 4.952

Entries	189
Mean	44.11
Std Dev	11.97

