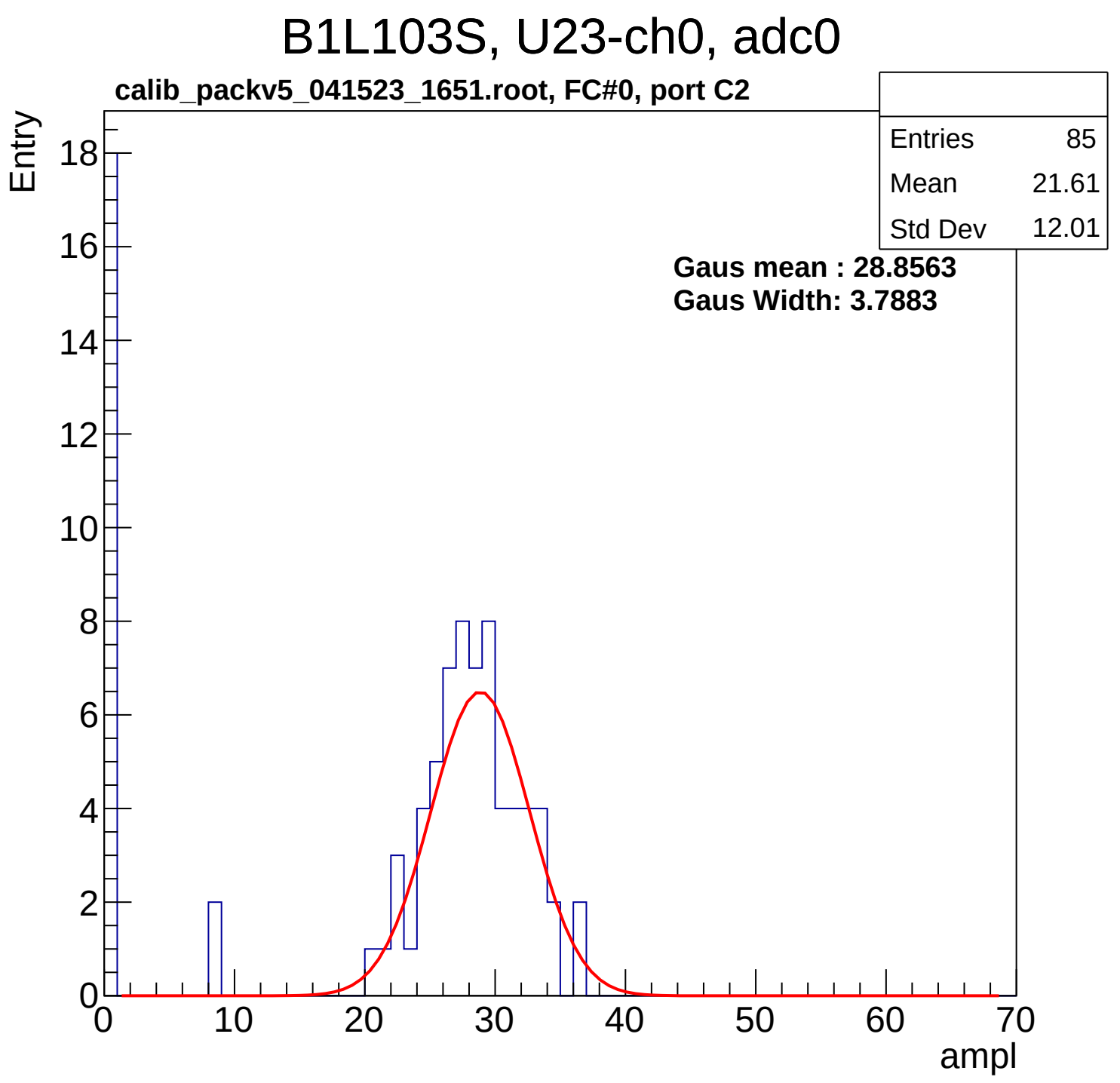
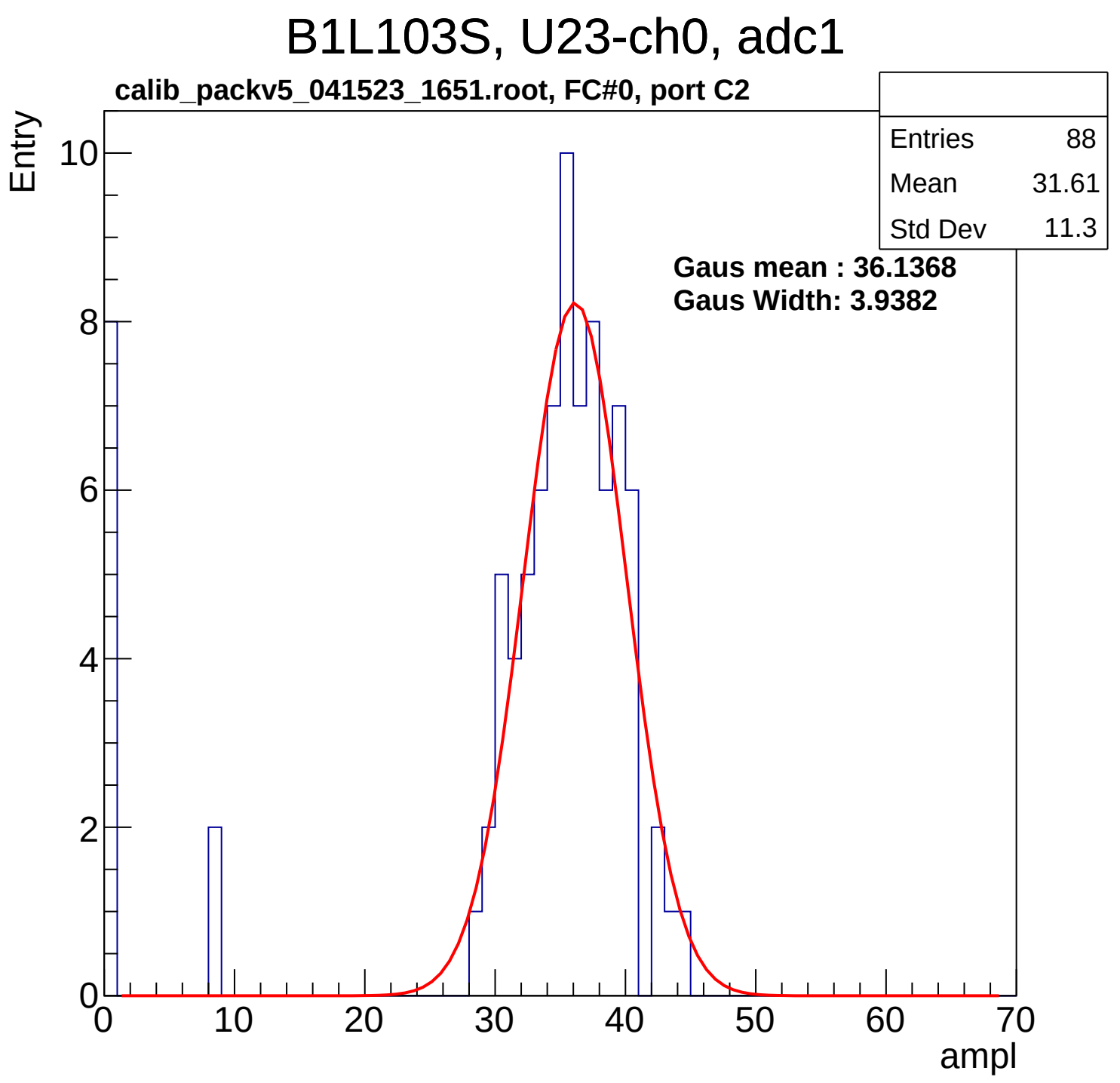
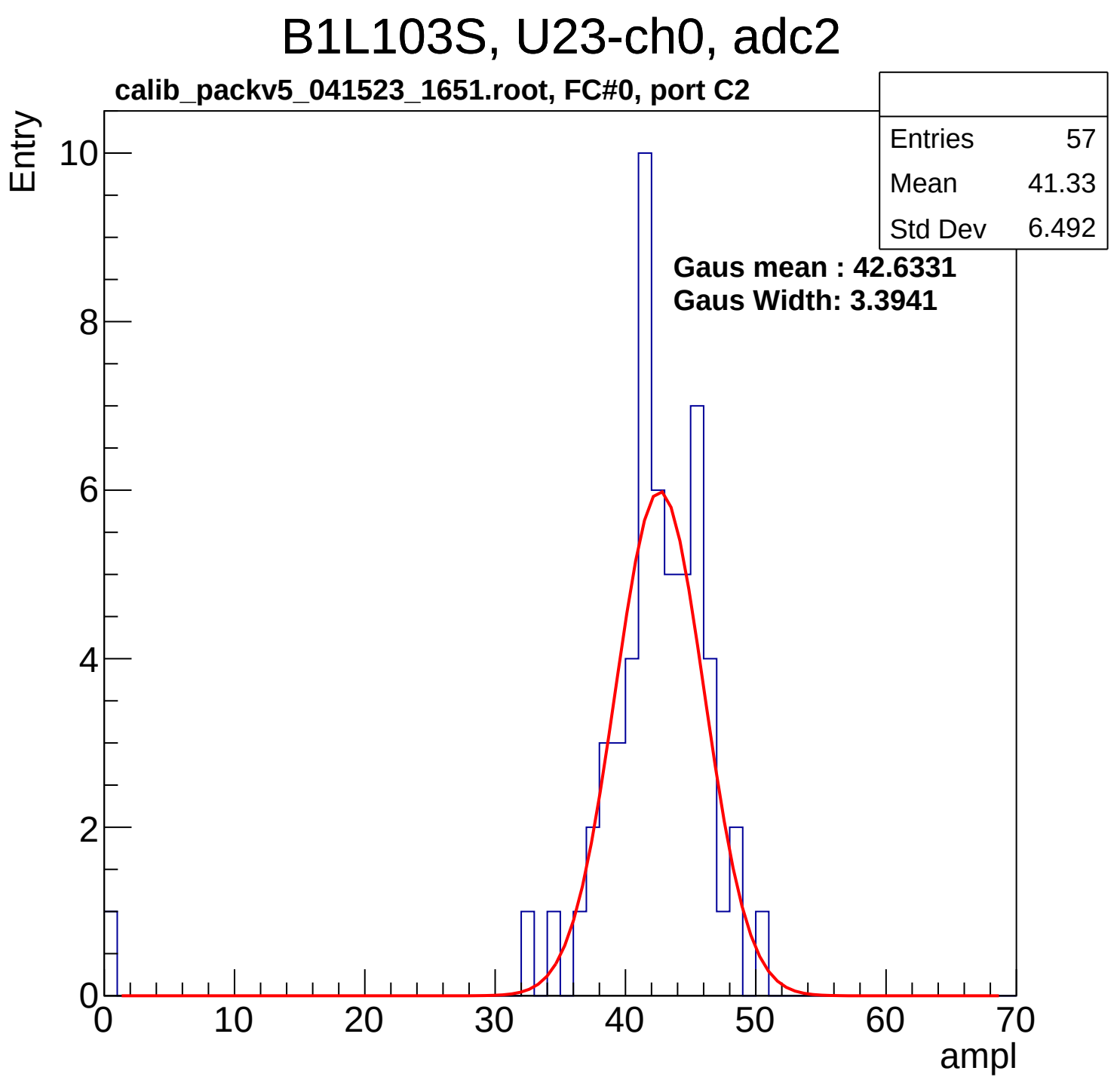
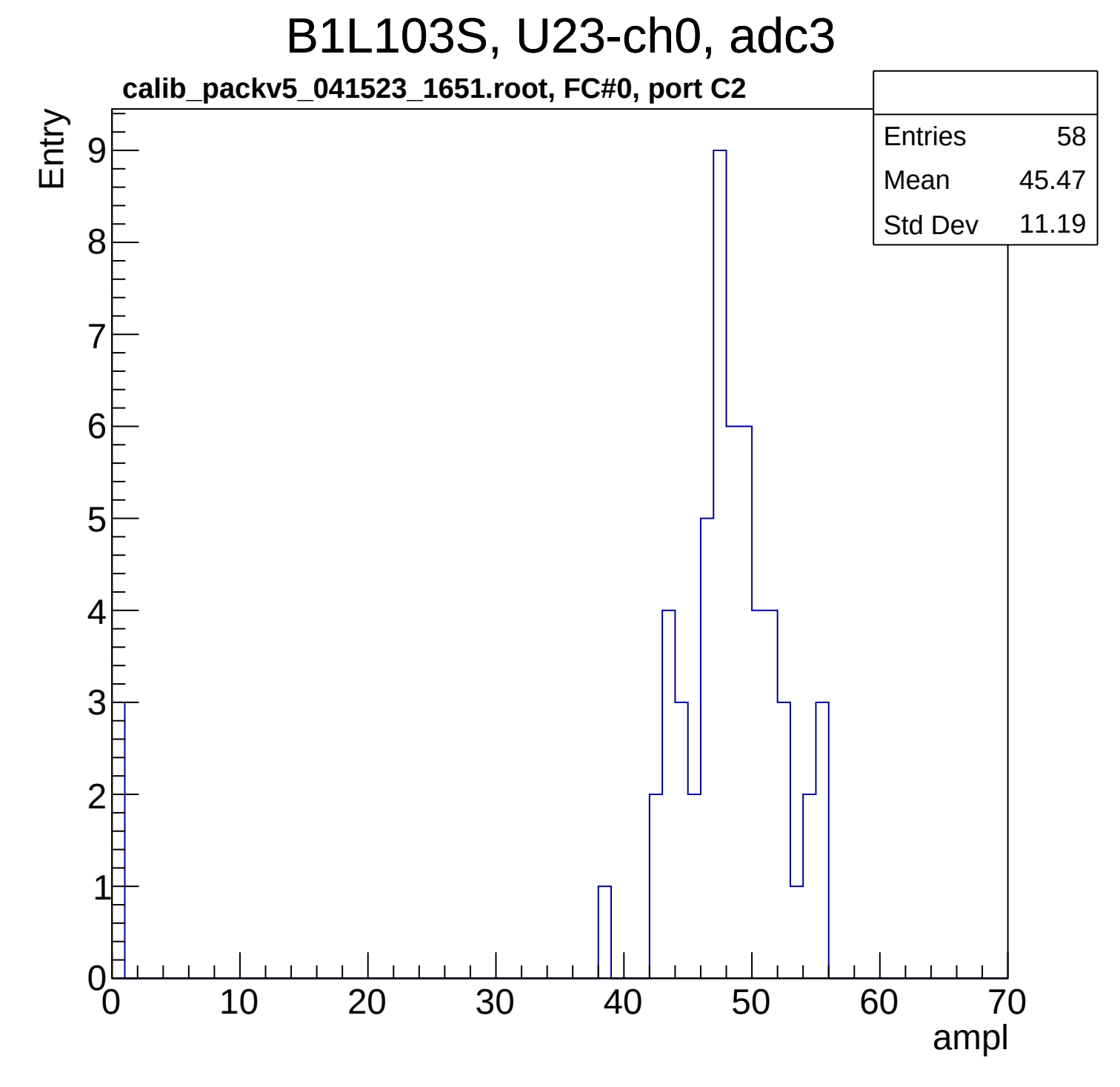
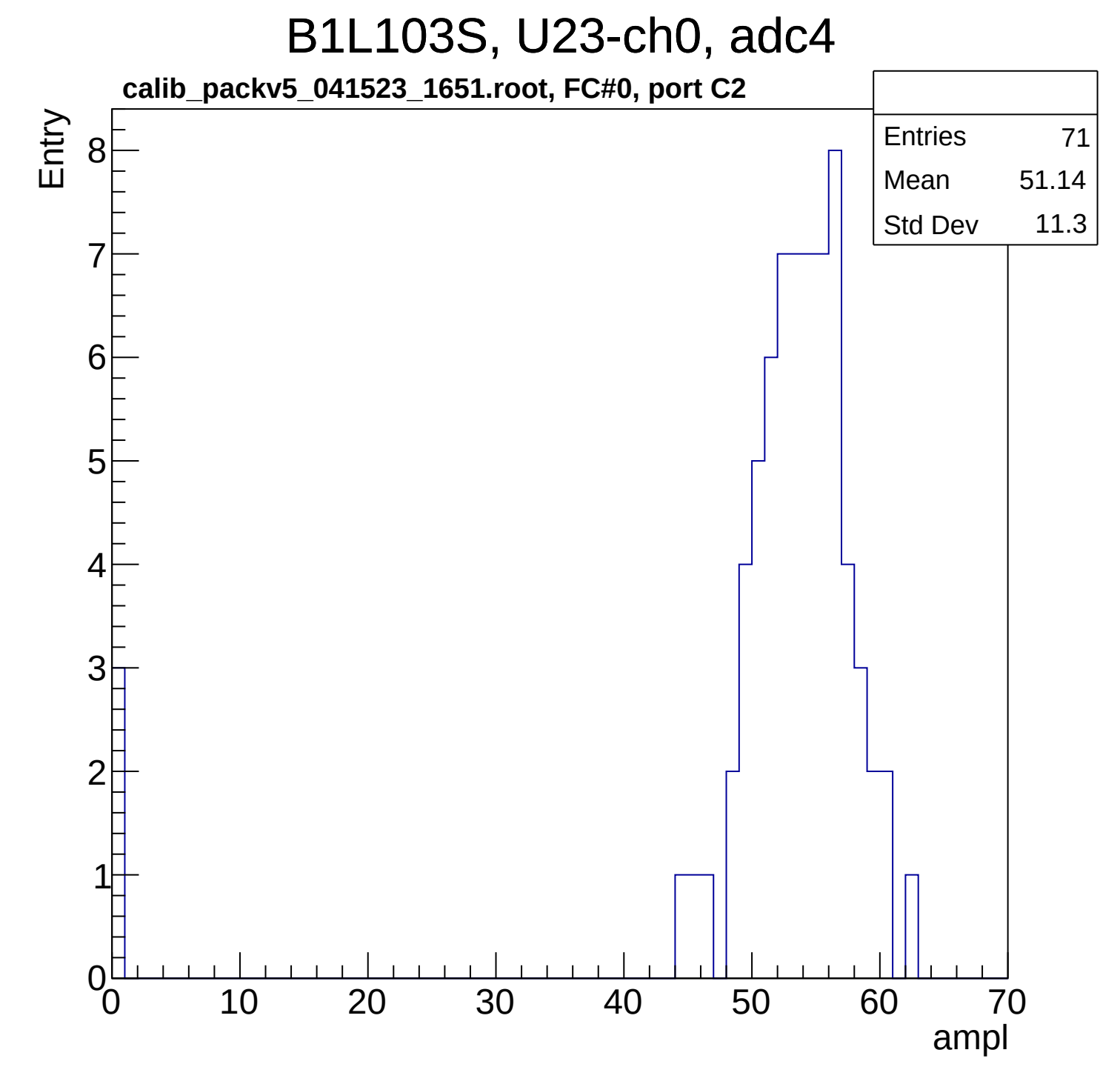
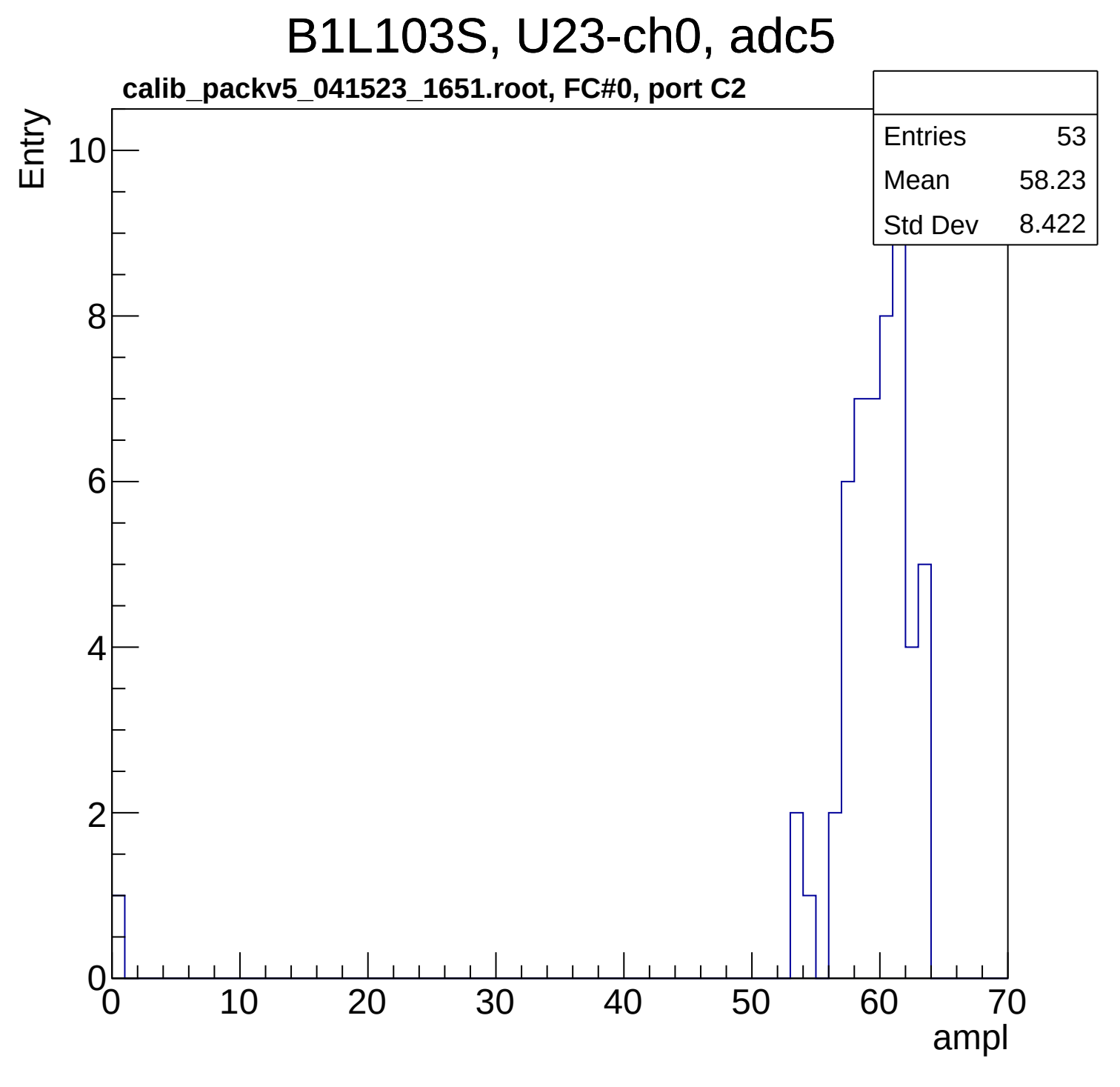

B1L103S, U23-ch0, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 21.61
Std Dev 12.01
Gaus mean : 28.8563
Gaus Width: 3.7883
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

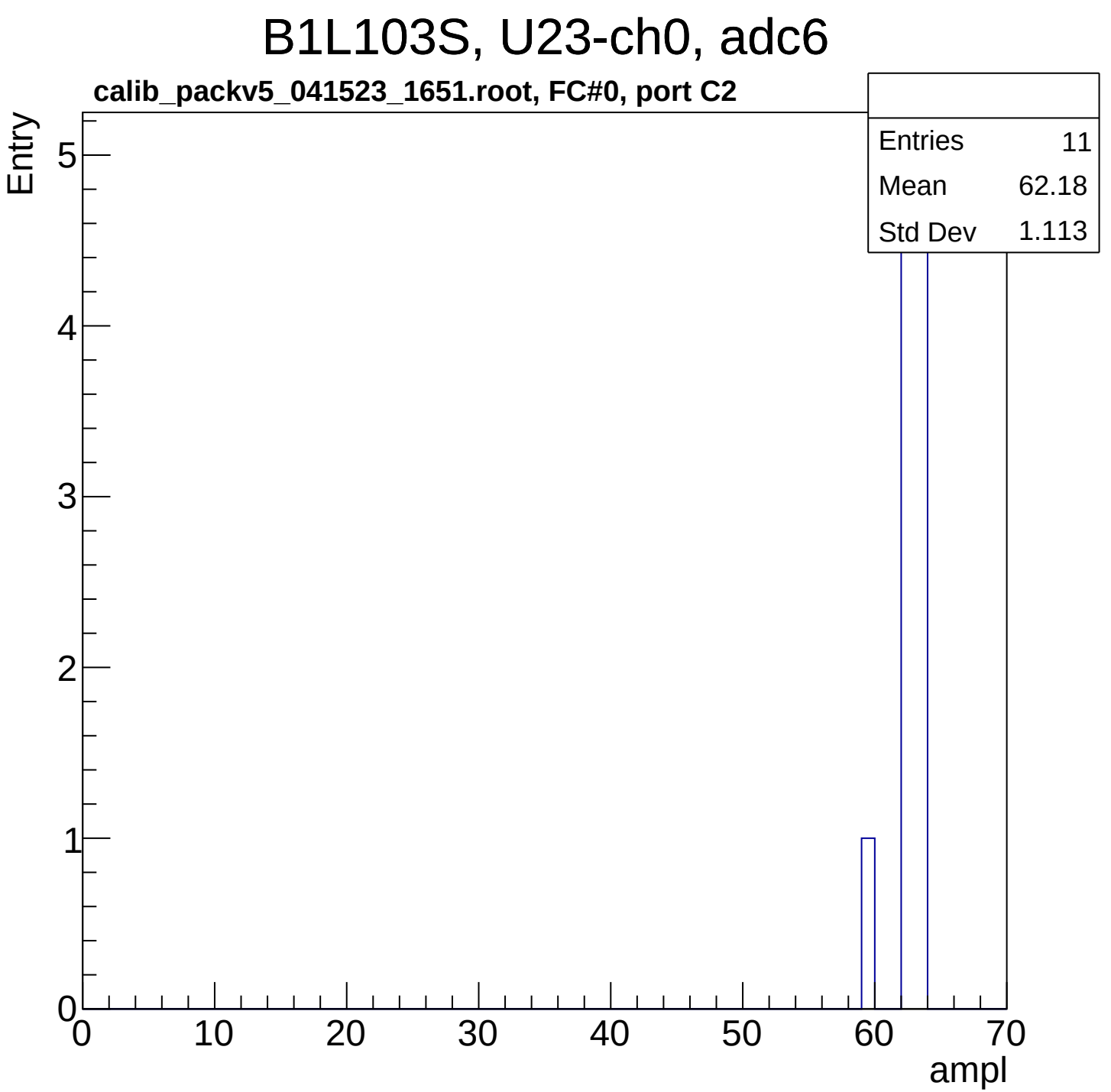

B1L103S, U23-ch0, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 88
Mean 31.61
Std Dev 11.3
Gaus mean : 36.1368
Gaus Width: 3.9382
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

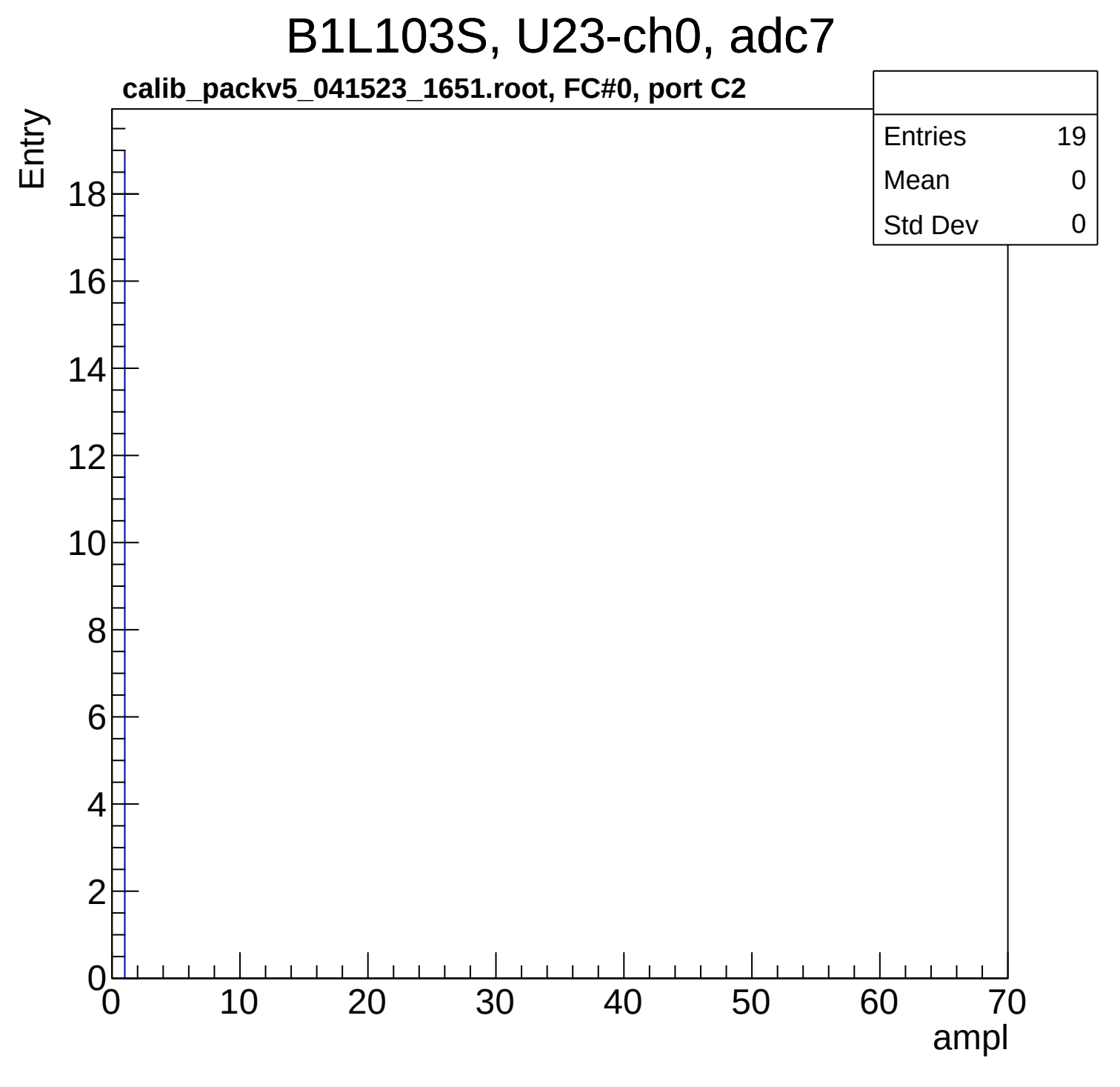

B1L103S, U23-ch0, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 41.33
Std Dev 6.492
Gaus mean : 42.6331
Gaus Width: 3.3941
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

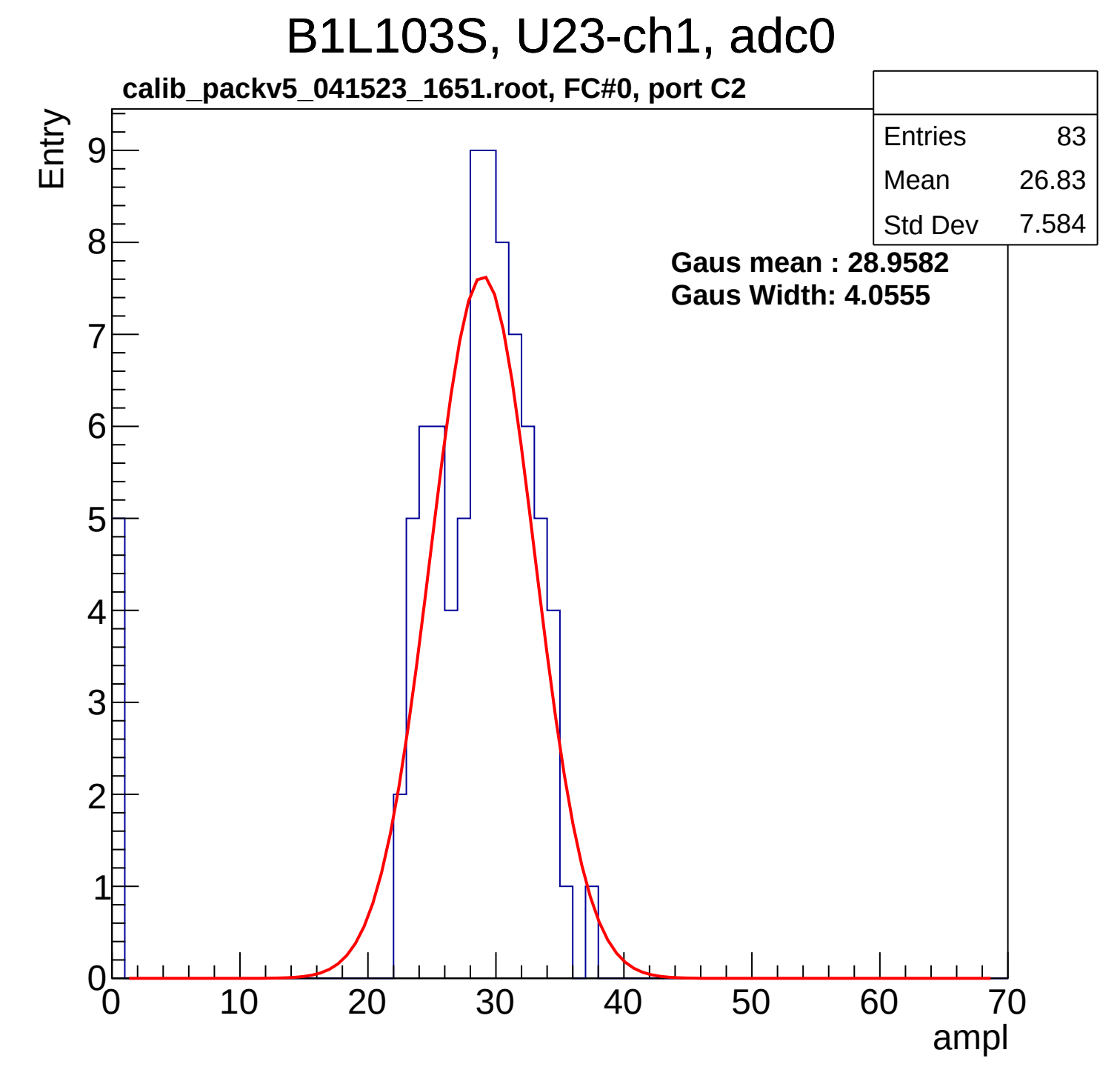

B1L103S, U23-ch0, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 45.47
Std Dev 11.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

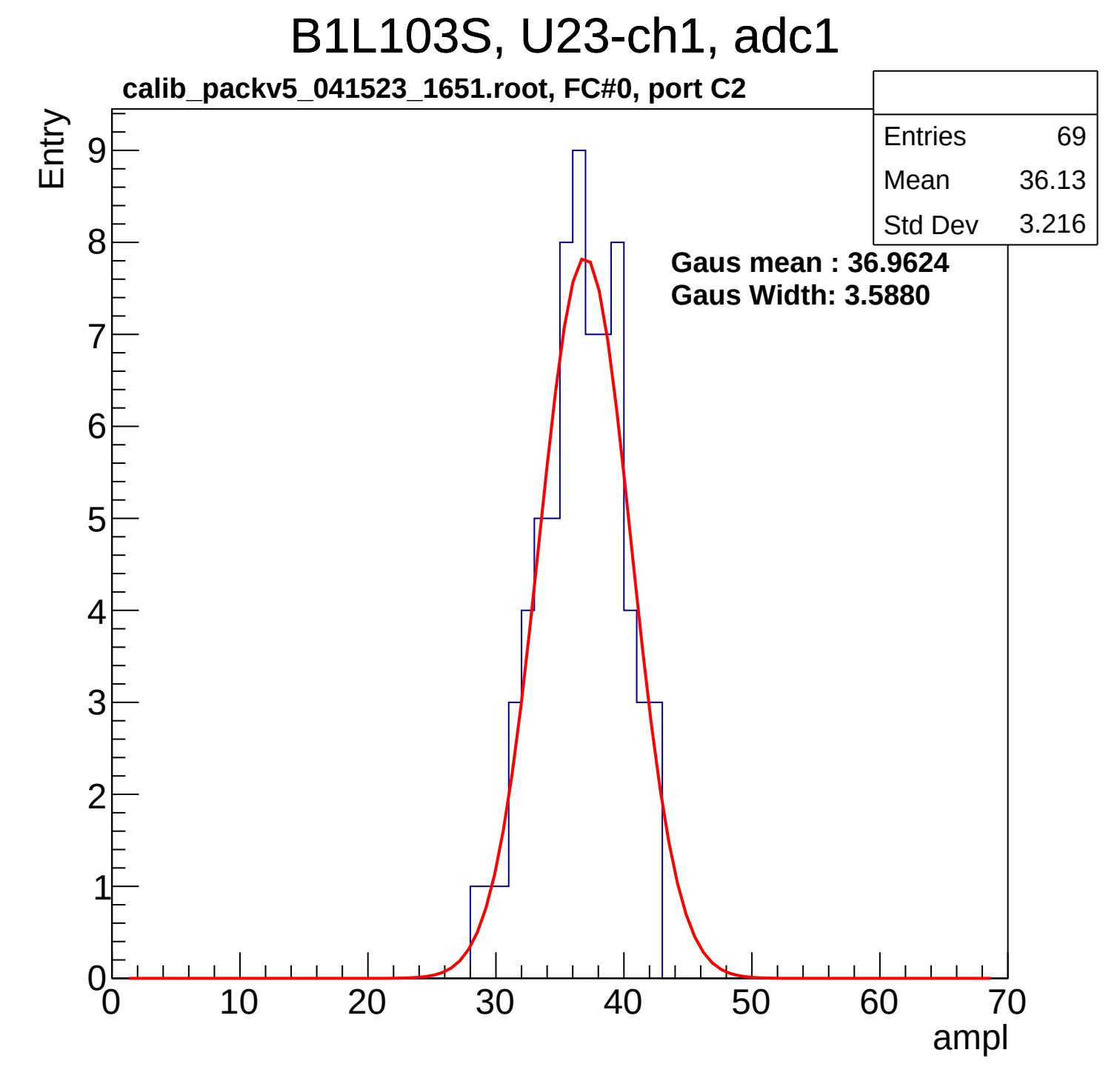

B1L103S, U23-ch0, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 51.14
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

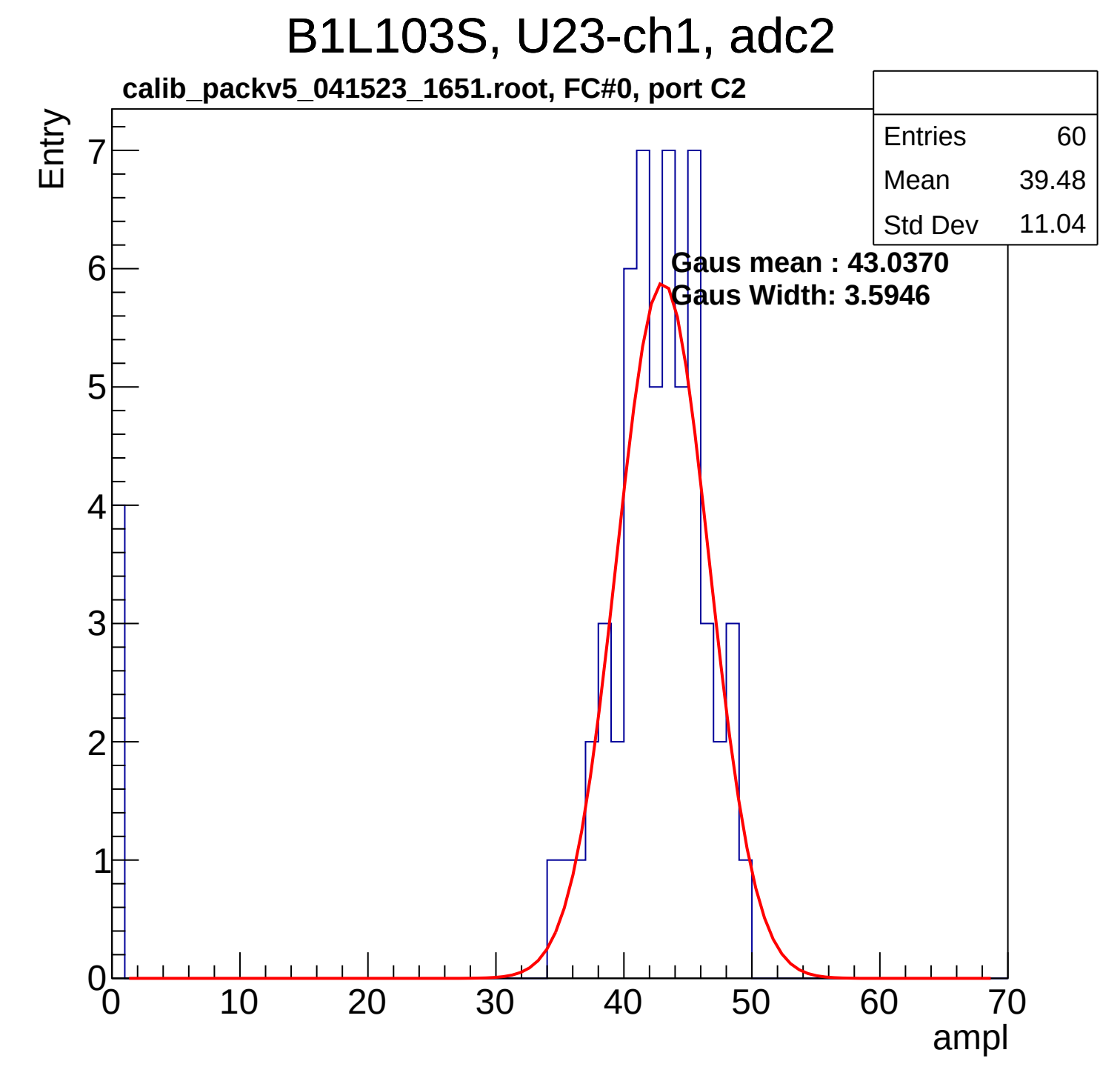

B1L103S, U23-ch0, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 58.23
Std Dev 8.422
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

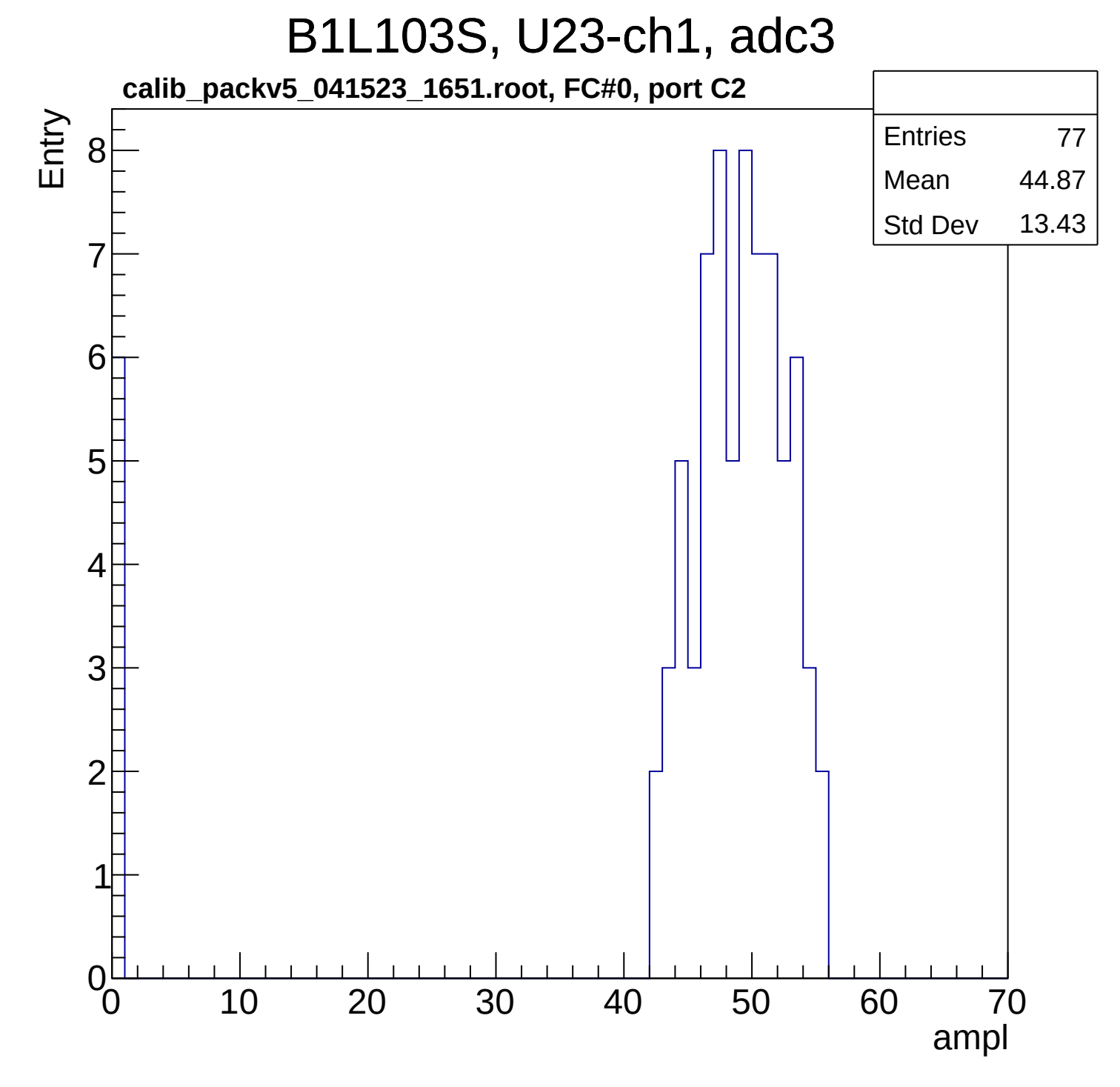

B1L103S, U23-ch0, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 62.18
Std Dev 1.113
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

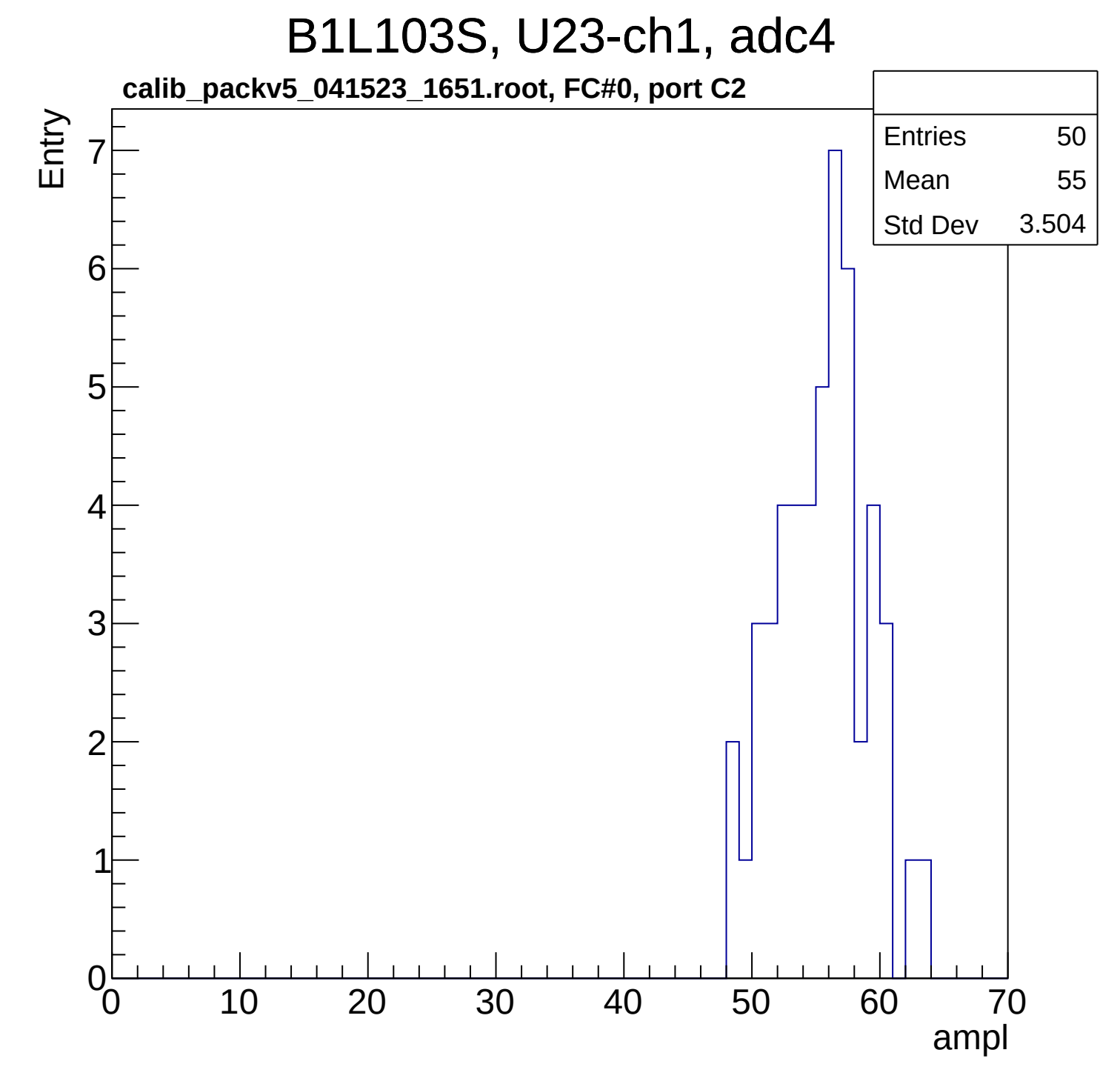

B1L103S, U23-ch0, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

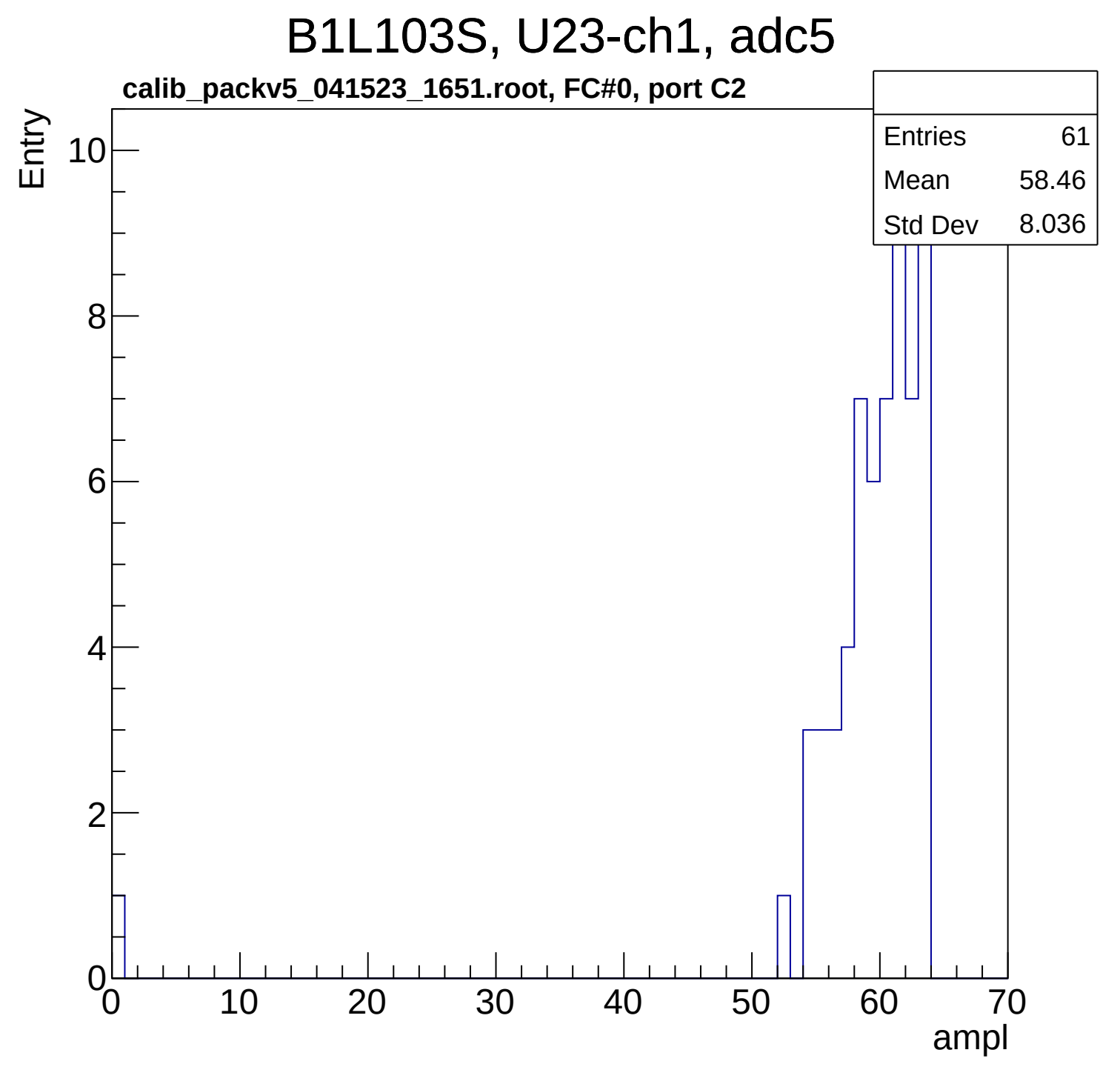

B1L103S, U23-ch1, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 26.83
Std Dev 7.584
Gaus mean : 28.9582
Gaus Width: 4.0555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

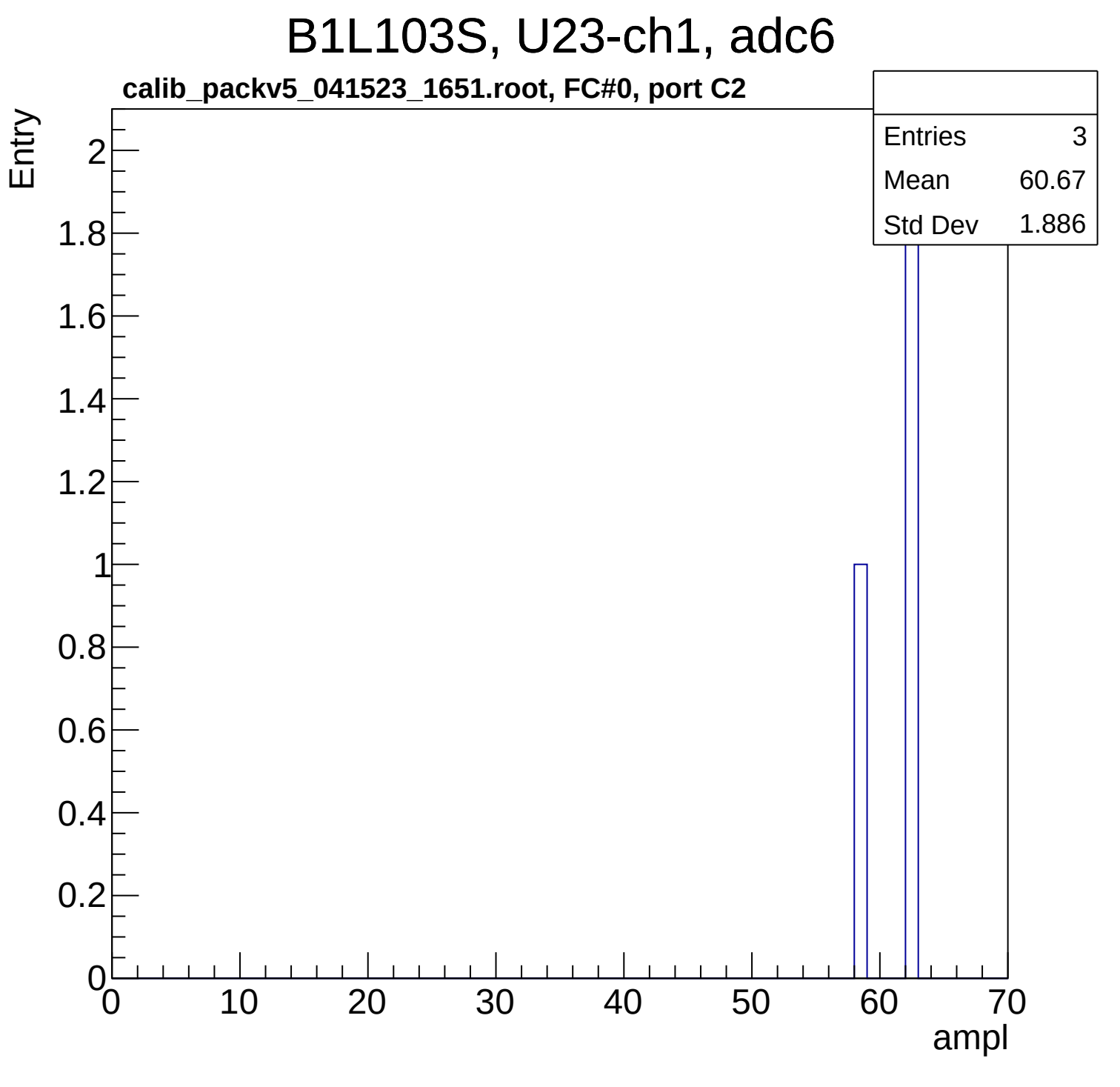

B1L103S, U23-ch1, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 36.13
Std Dev 3.216
Gaus mean : 36.9624
Gaus Width: 3.5880
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

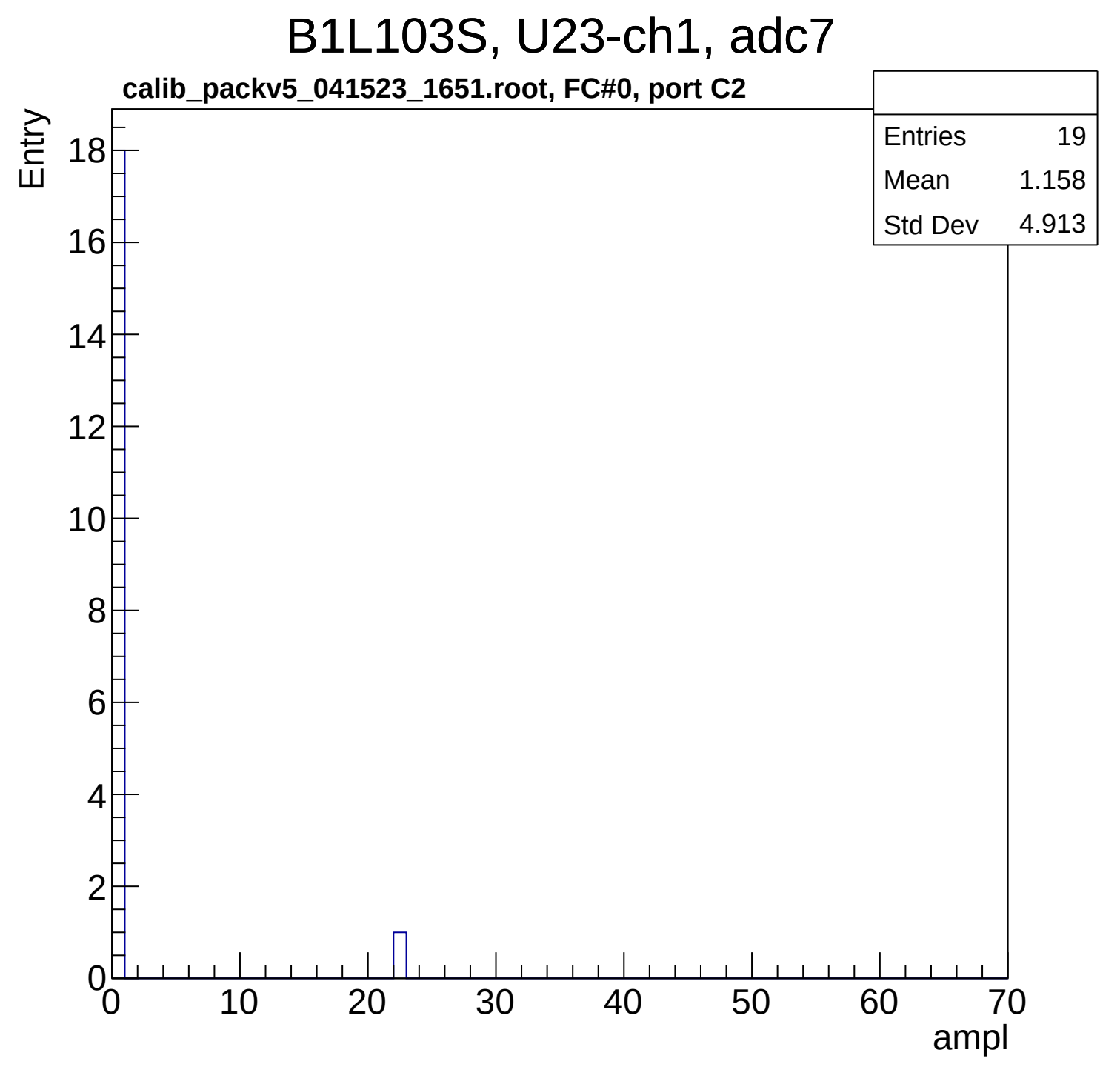

B1L103S, U23-ch1, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 39.48
Std Dev 11.04
Gaus mean : 43.0370
Gaus Width: 3.5946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

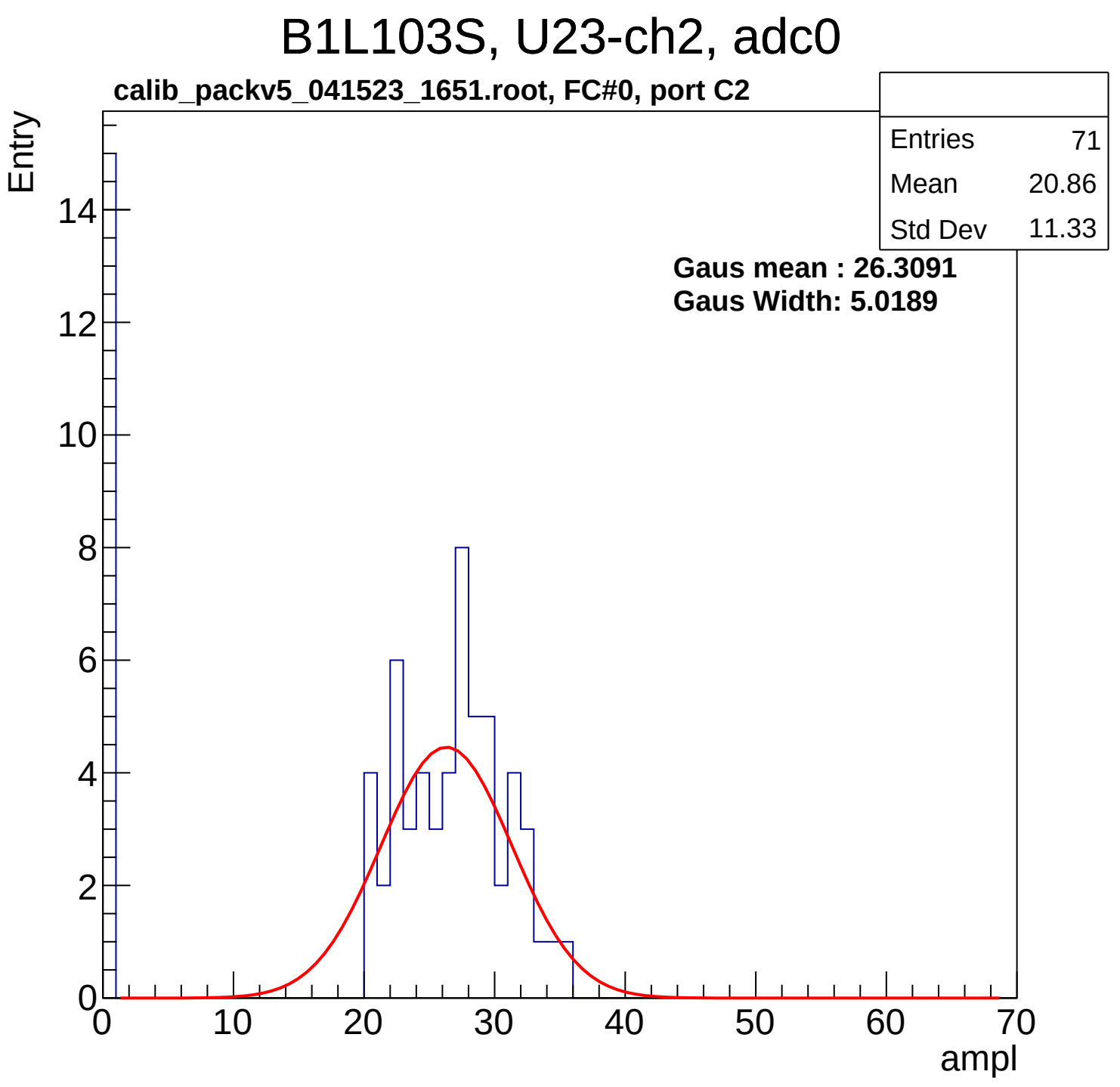

B1L103S, U23-ch1, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 44.87
Std Dev 13.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

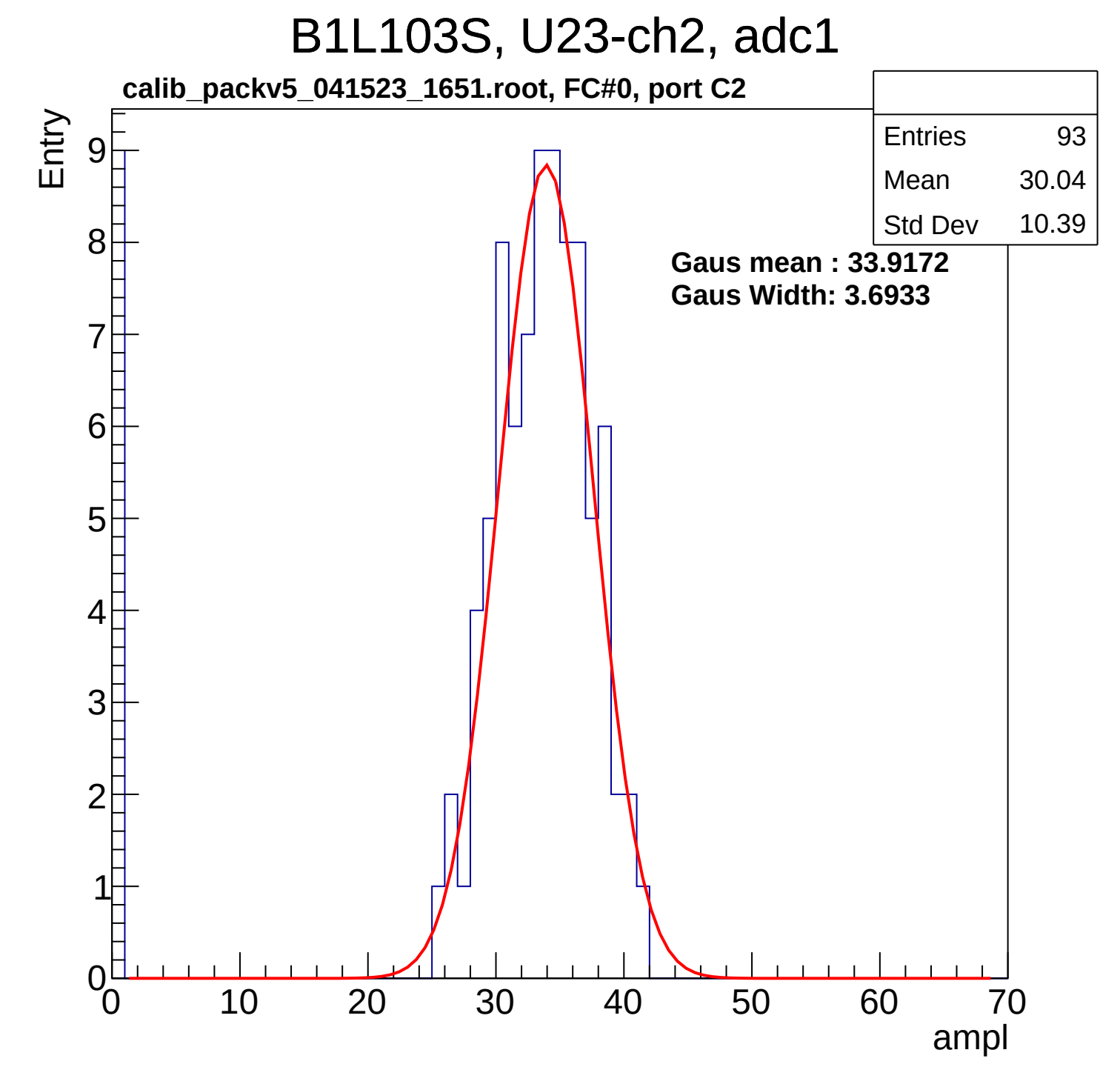

B1L103S, U23-ch1, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 50
Mean 55
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

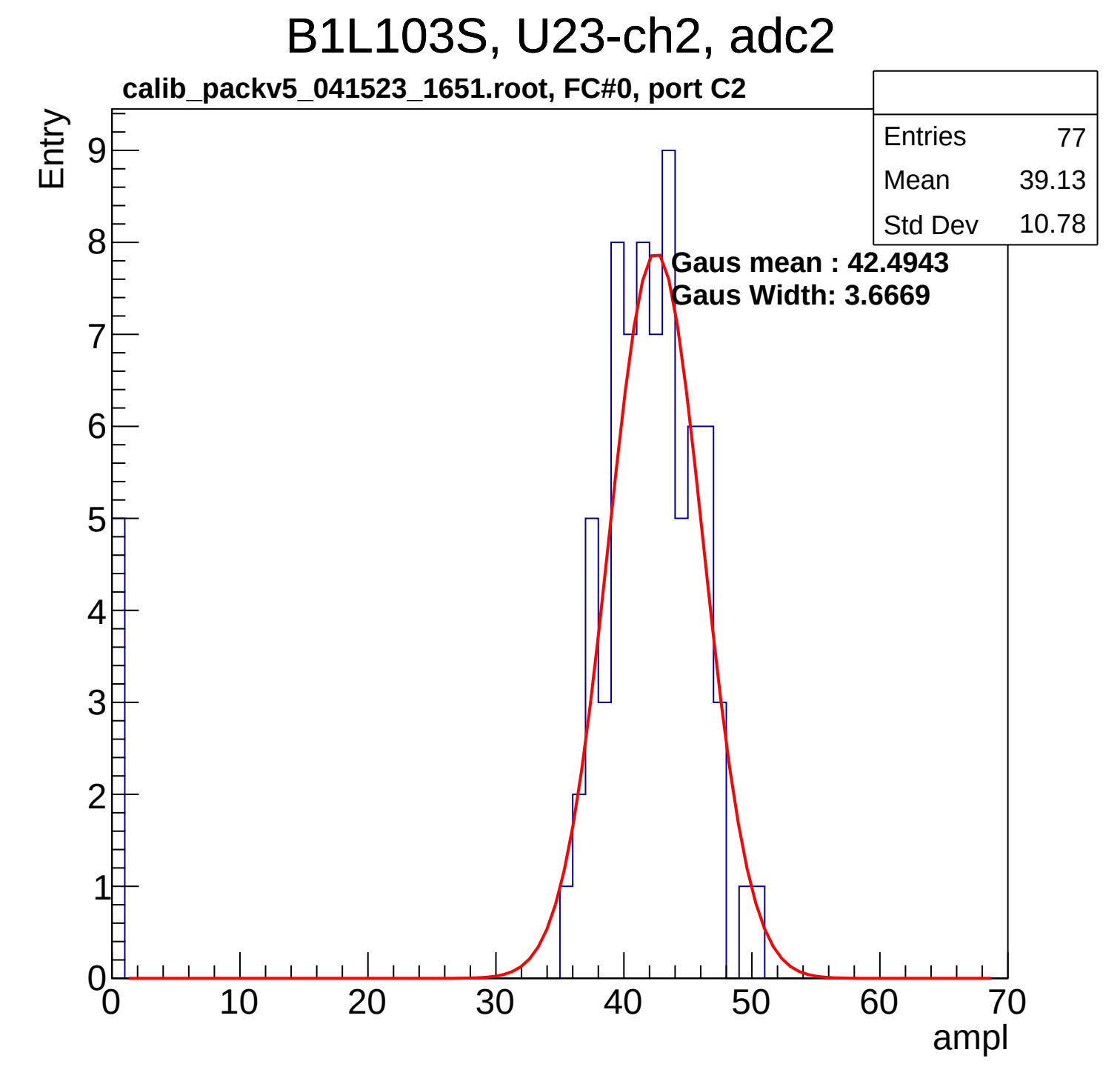

B1L103S, U23-ch1, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 58.46
Std Dev 8.036
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

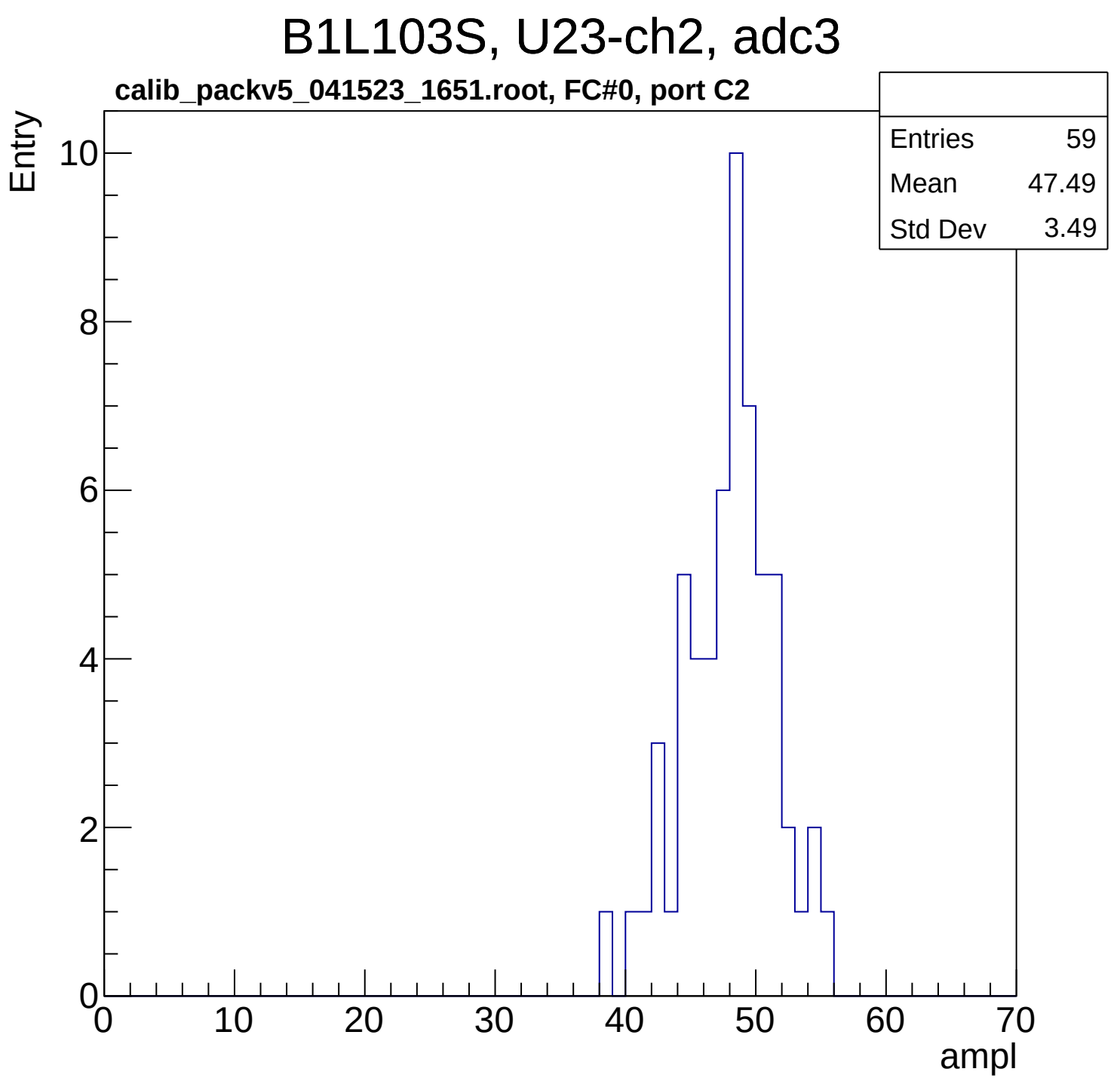

B1L103S, U23-ch1, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 3
Mean 60.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

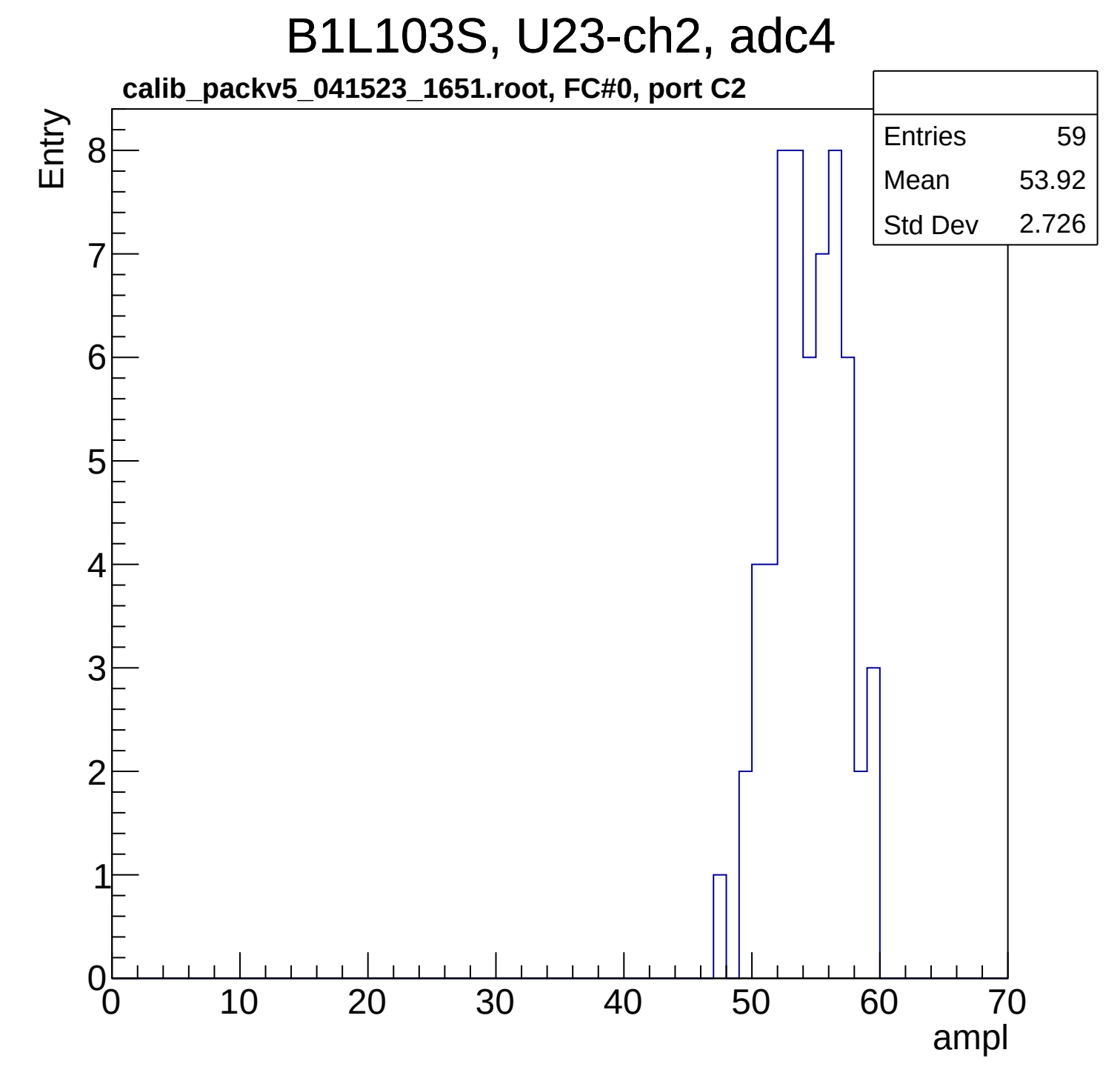

B1L103S, U23-ch1, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.158
Std Dev 4.913
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

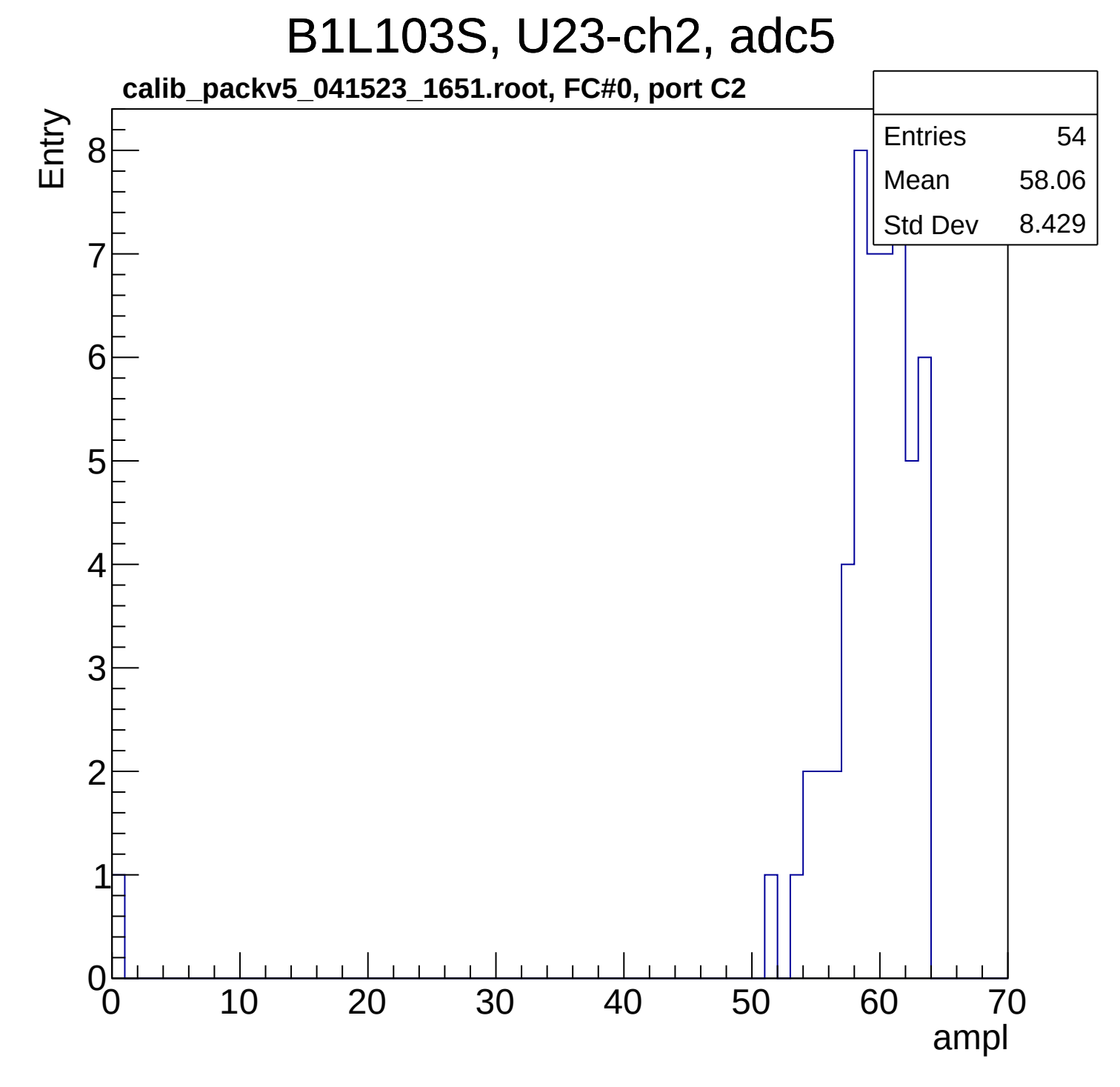

B1L103S, U23-ch2, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 20.86
Std Dev 11.33
Gaus mean : 26.3091
Gaus Width: 5.0189
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

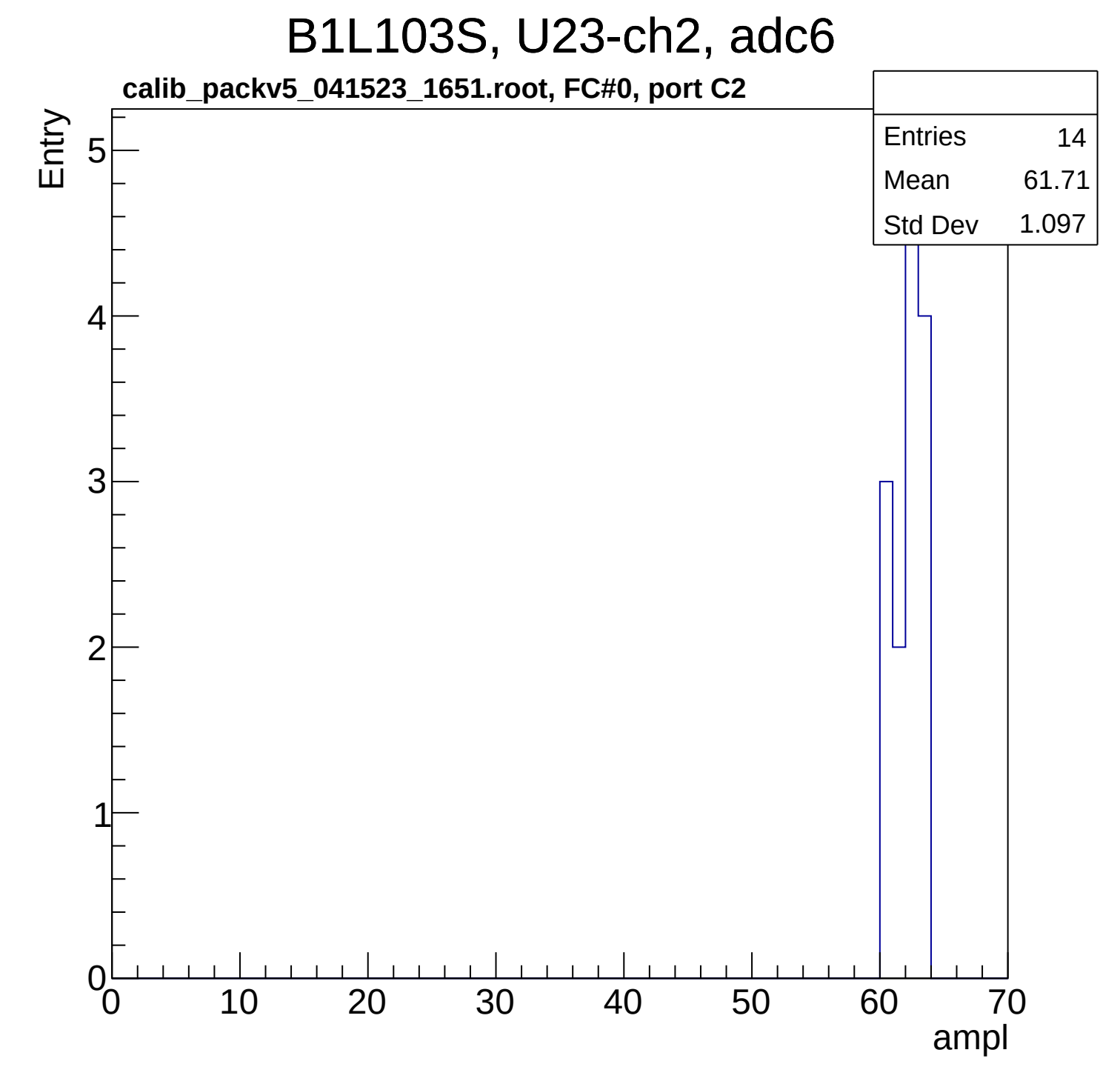

B1L103S, U23-ch2, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 93
Mean 30.04
Std Dev 10.39
Gaus mean : 33.9172
Gaus Width: 3.6933
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

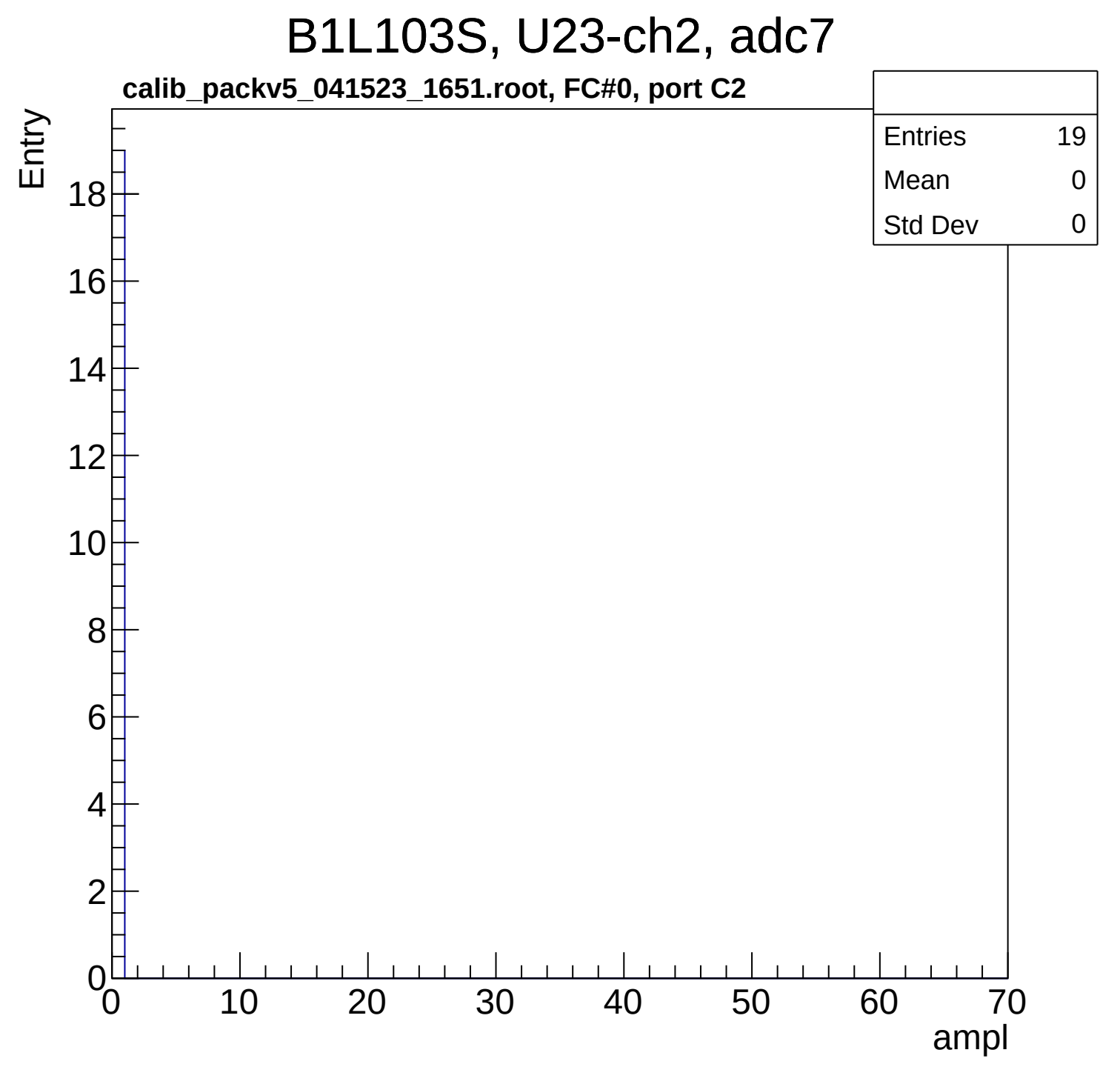

B1L103S, U23-ch2, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 39.13
Std Dev 10.78
Gaus mean : 42.4943
Gaus Width: 3.6669
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

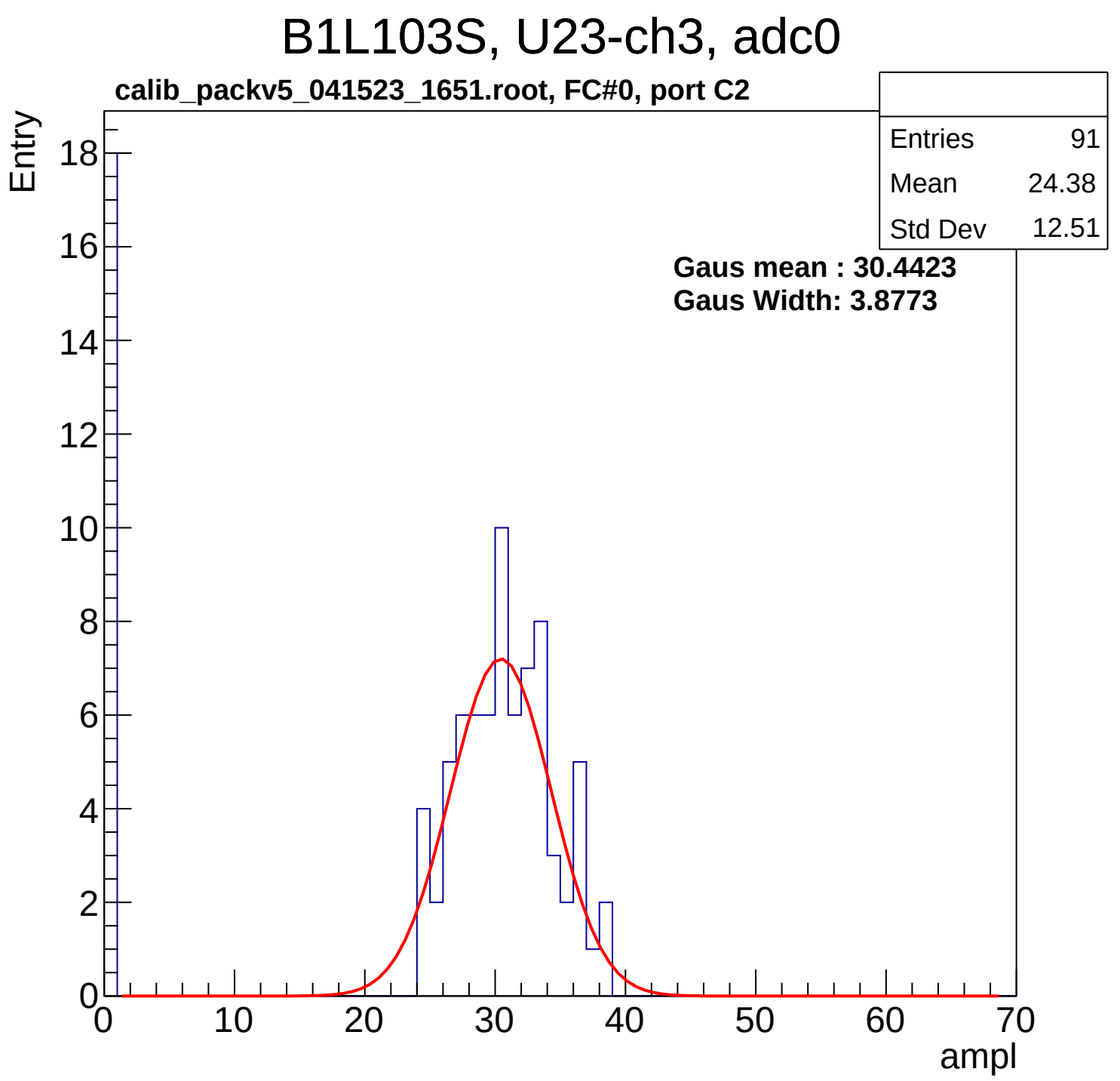

B1L103S, U23-ch2, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 47.49
Std Dev 3.49
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

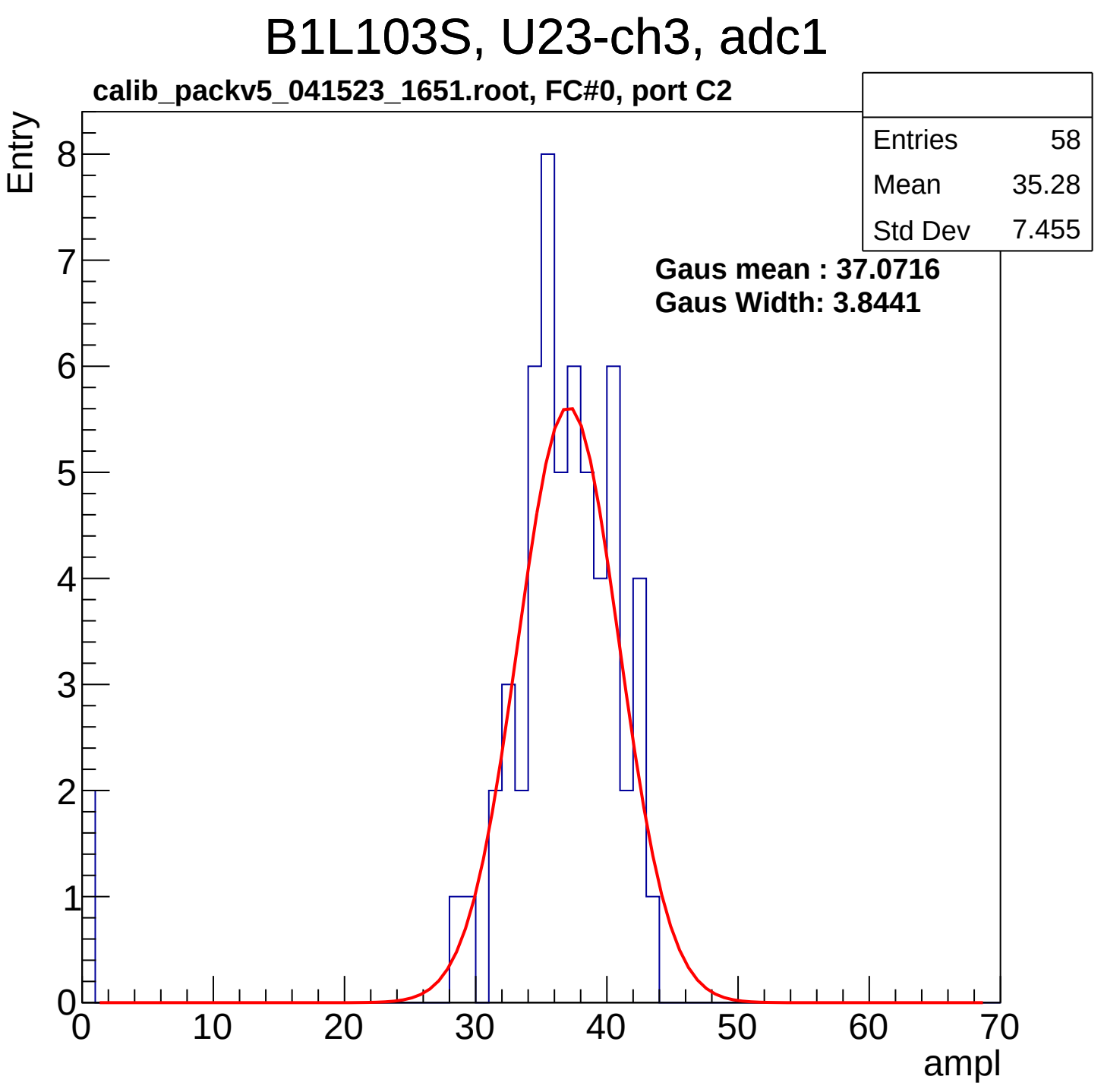

B1L103S, U23-ch2, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 53.92
Std Dev 2.726
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

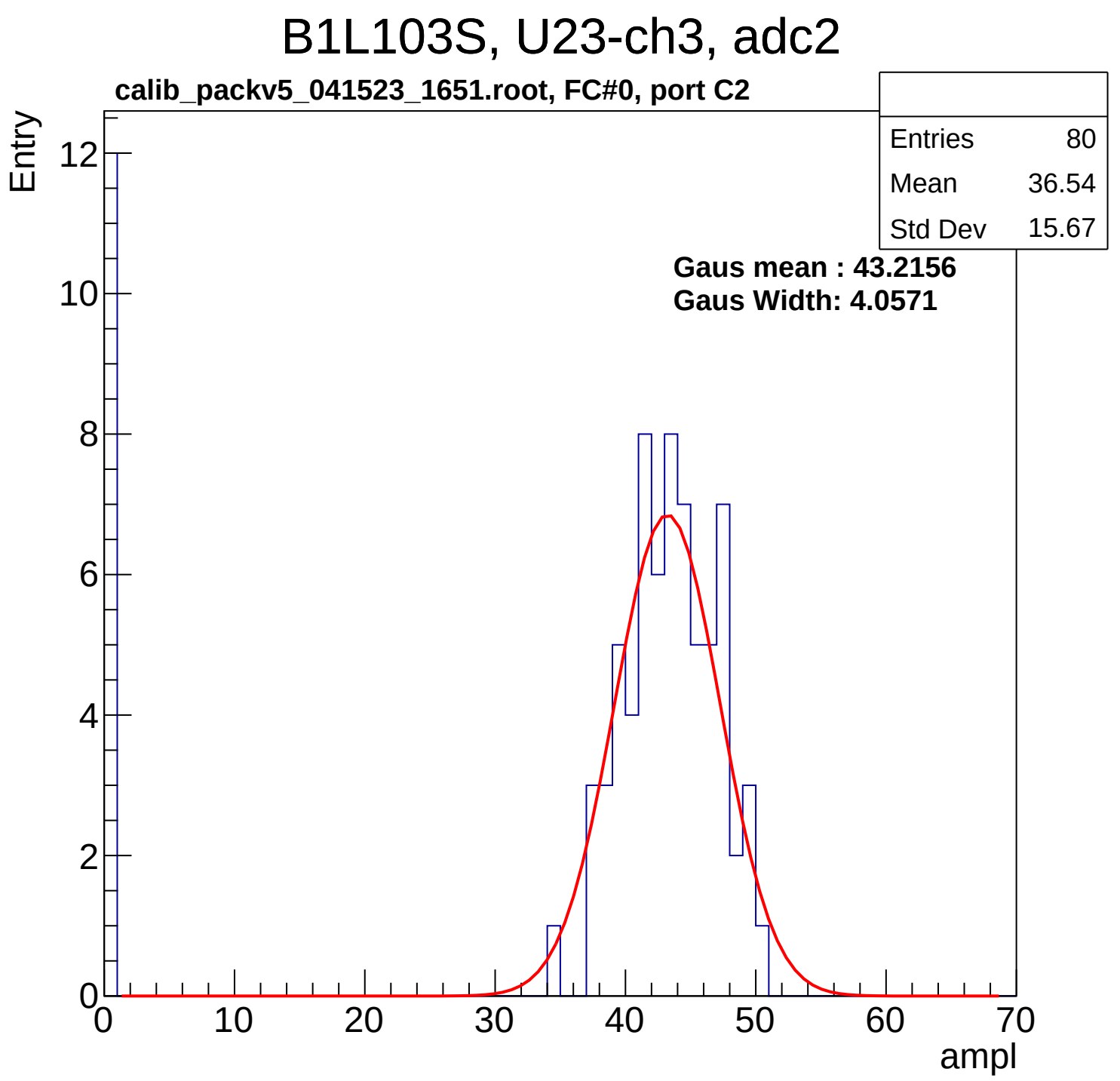

B1L103S, U23-ch2, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 58.06
Std Dev 8.429
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

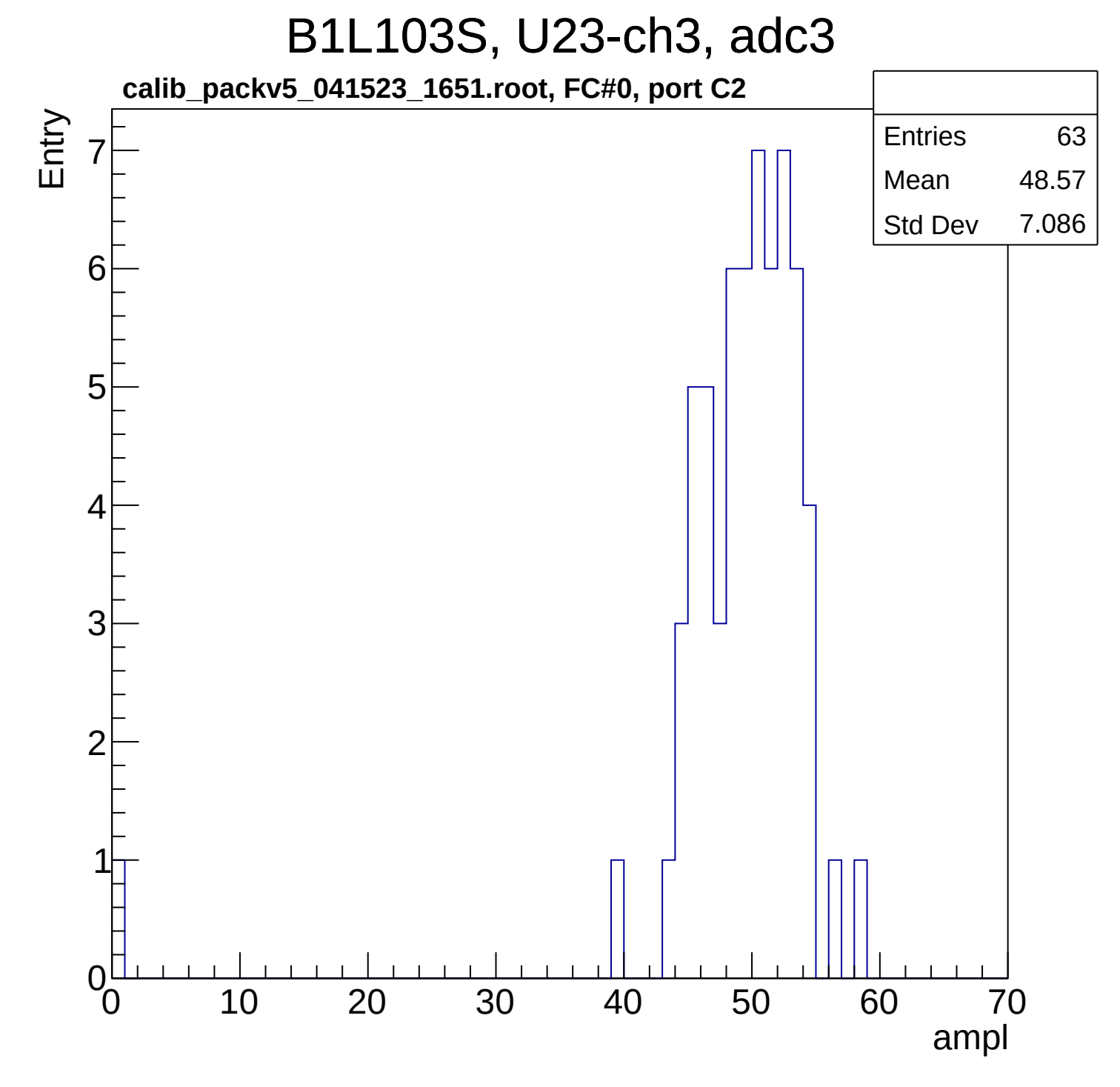

B1L103S, U23-ch2, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 14
Mean 61.71
Std Dev 1.097
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

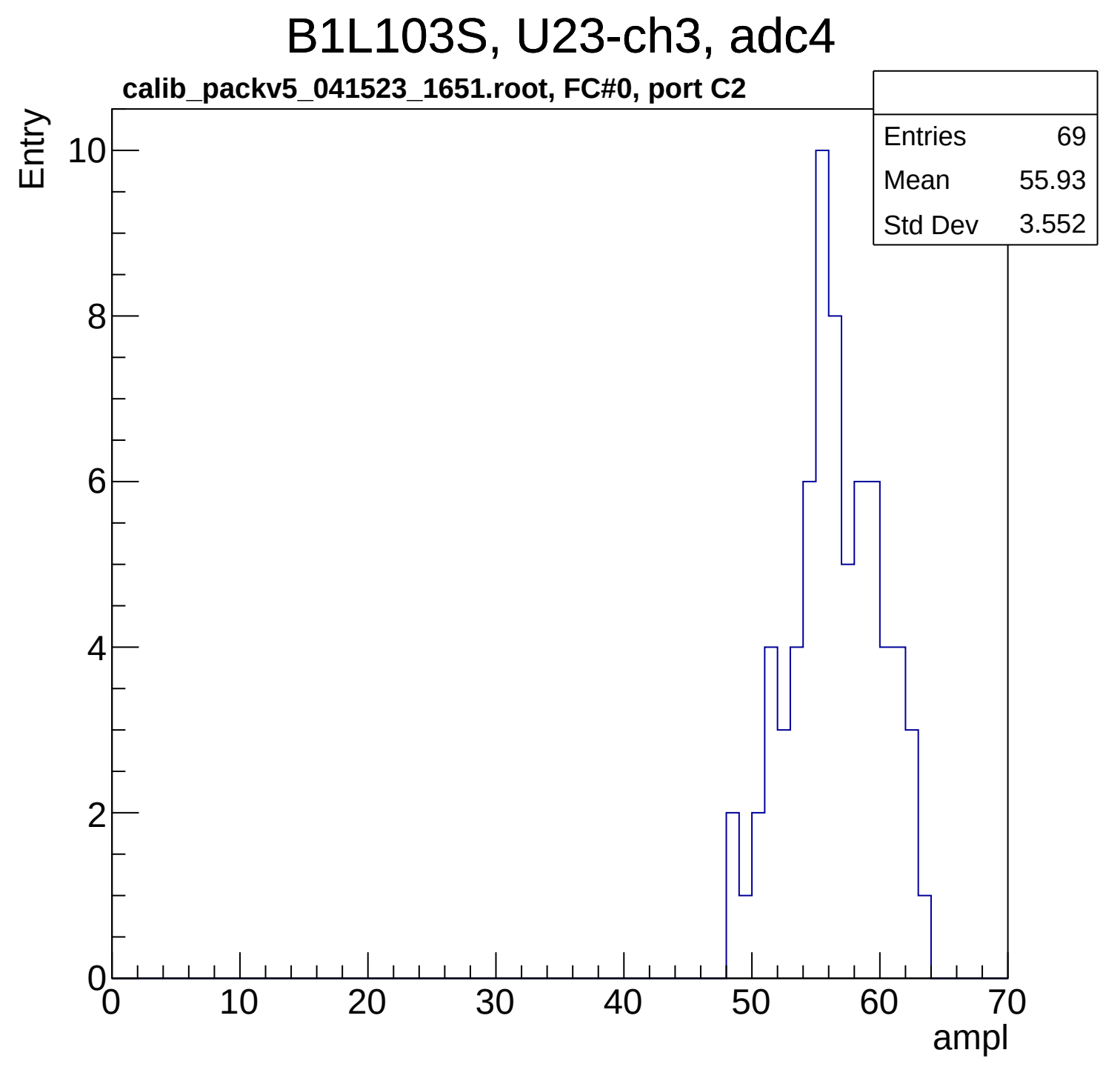

B1L103S, U23-ch2, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

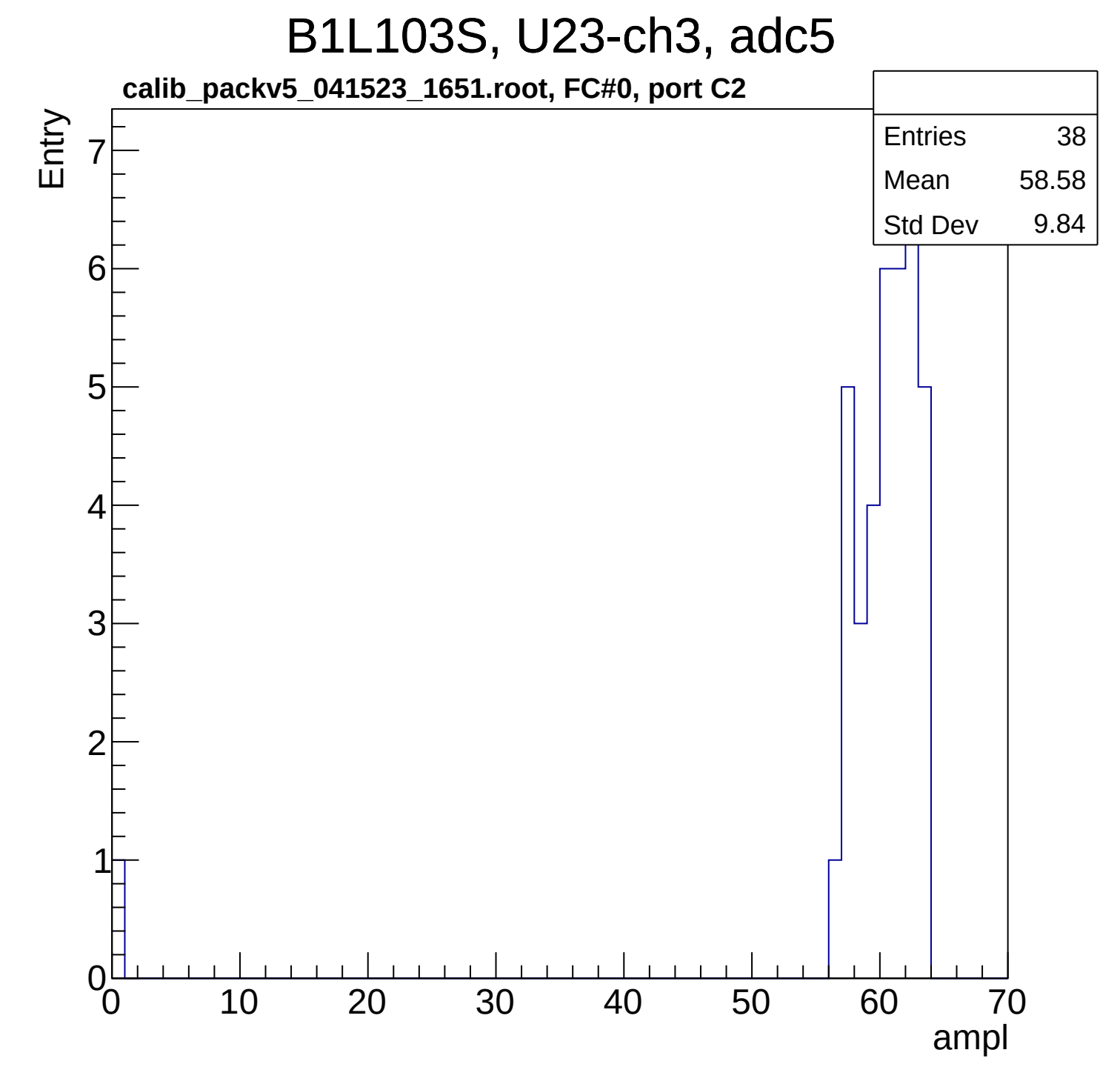

B1L103S, U23-ch3, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 24.38
Std Dev 12.51
Gaus mean : 30.4423
Gaus Width: 3.8773
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

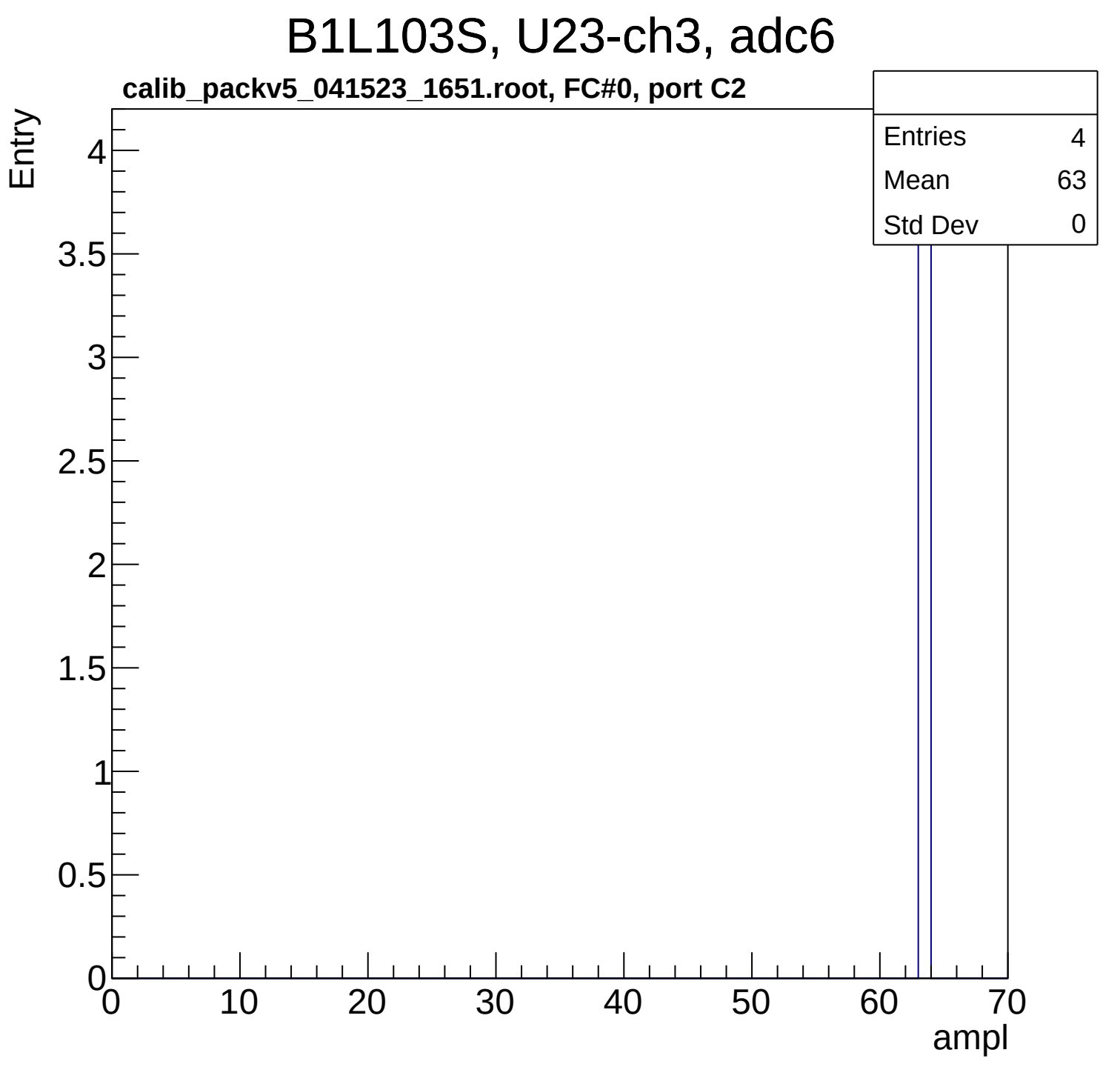

B1L103S, U23-ch3, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 35.28
Std Dev 7.455
Gaus mean : 37.0716
Gaus Width: 3.8441
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

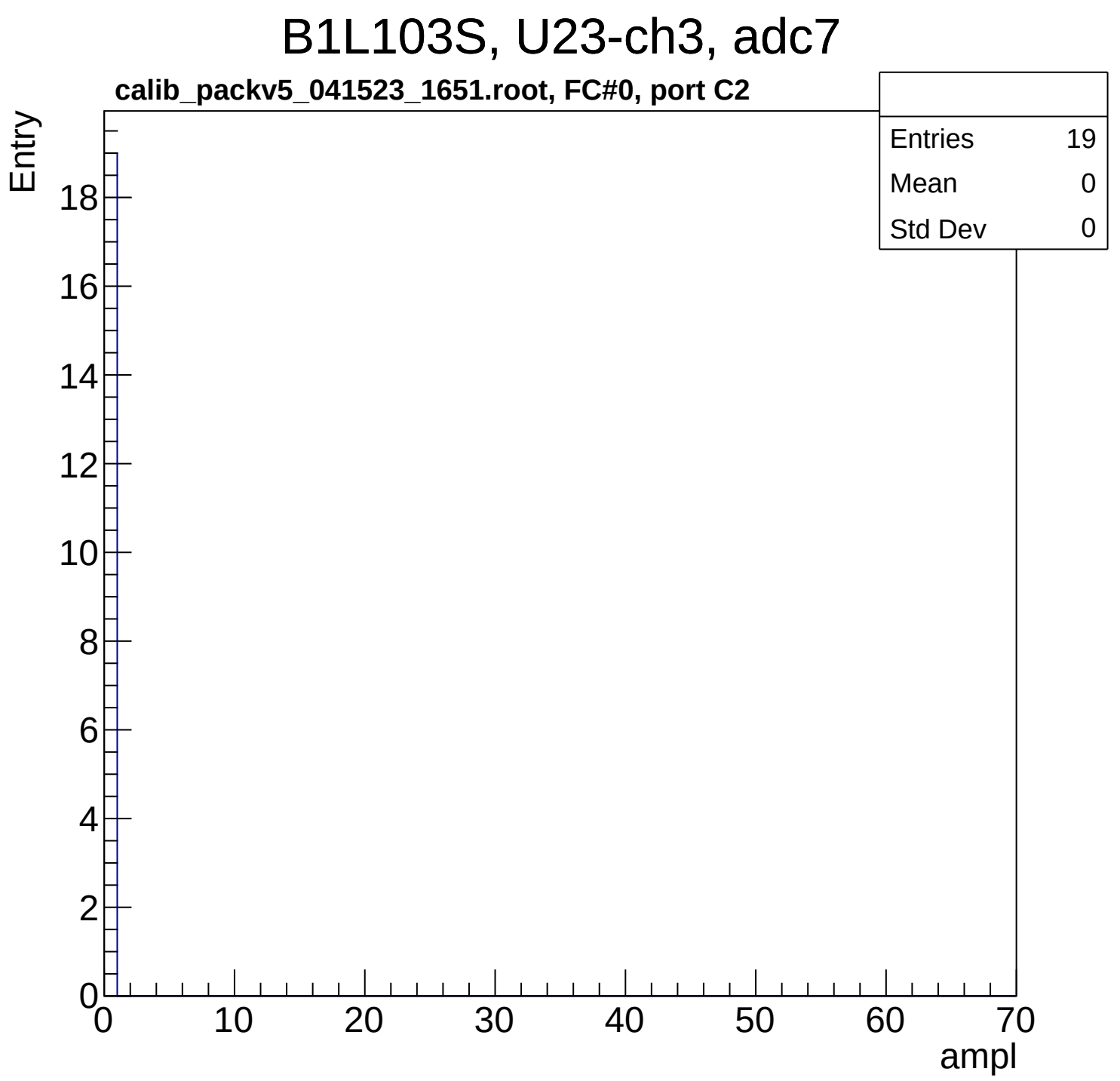

B1L103S, U23-ch3, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 36.54
Std Dev 15.67
Gaus mean : 43.2156
Gaus Width: 4.0571
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

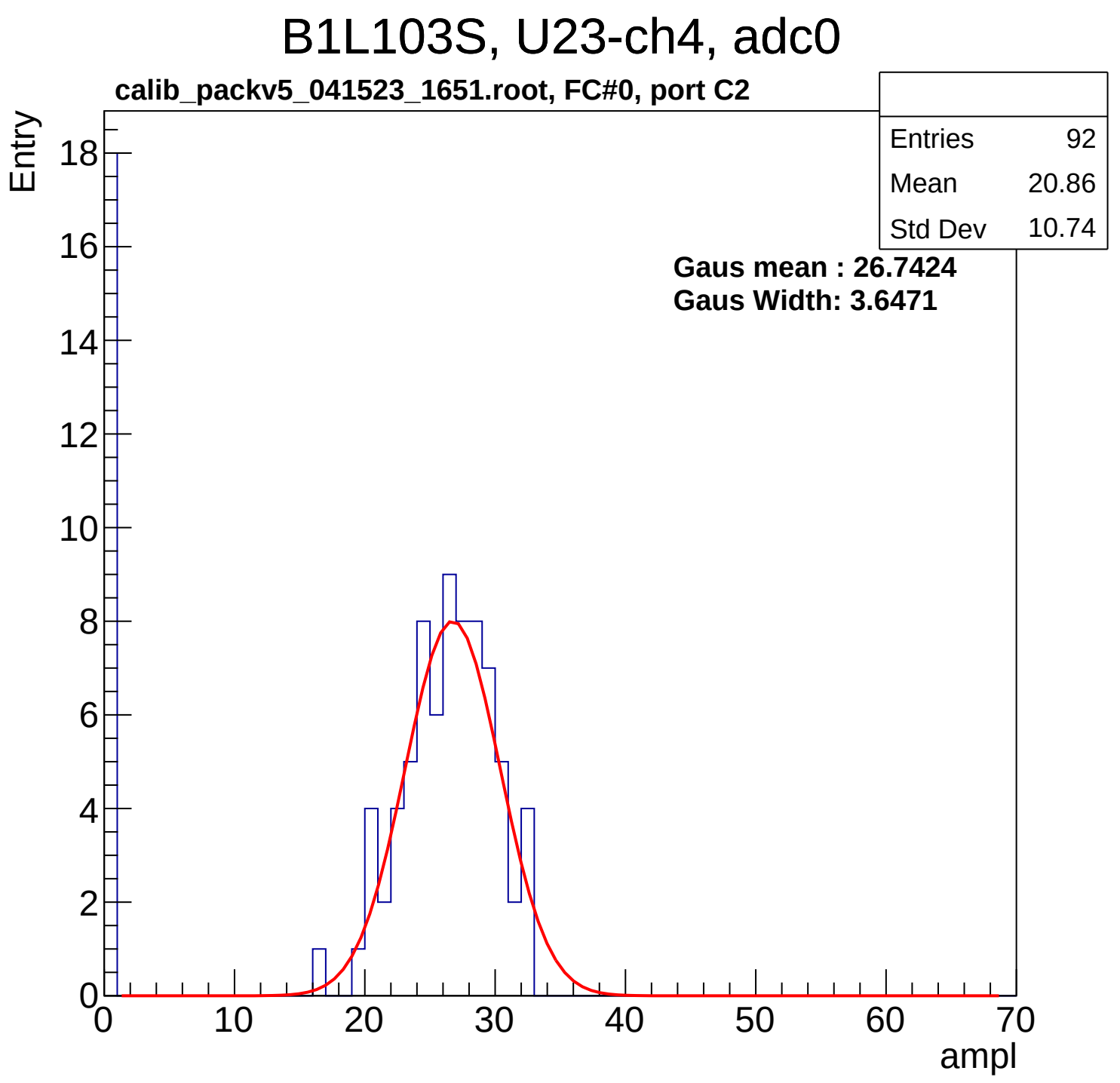

B1L103S, U23-ch3, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 48.57
Std Dev 7.086
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

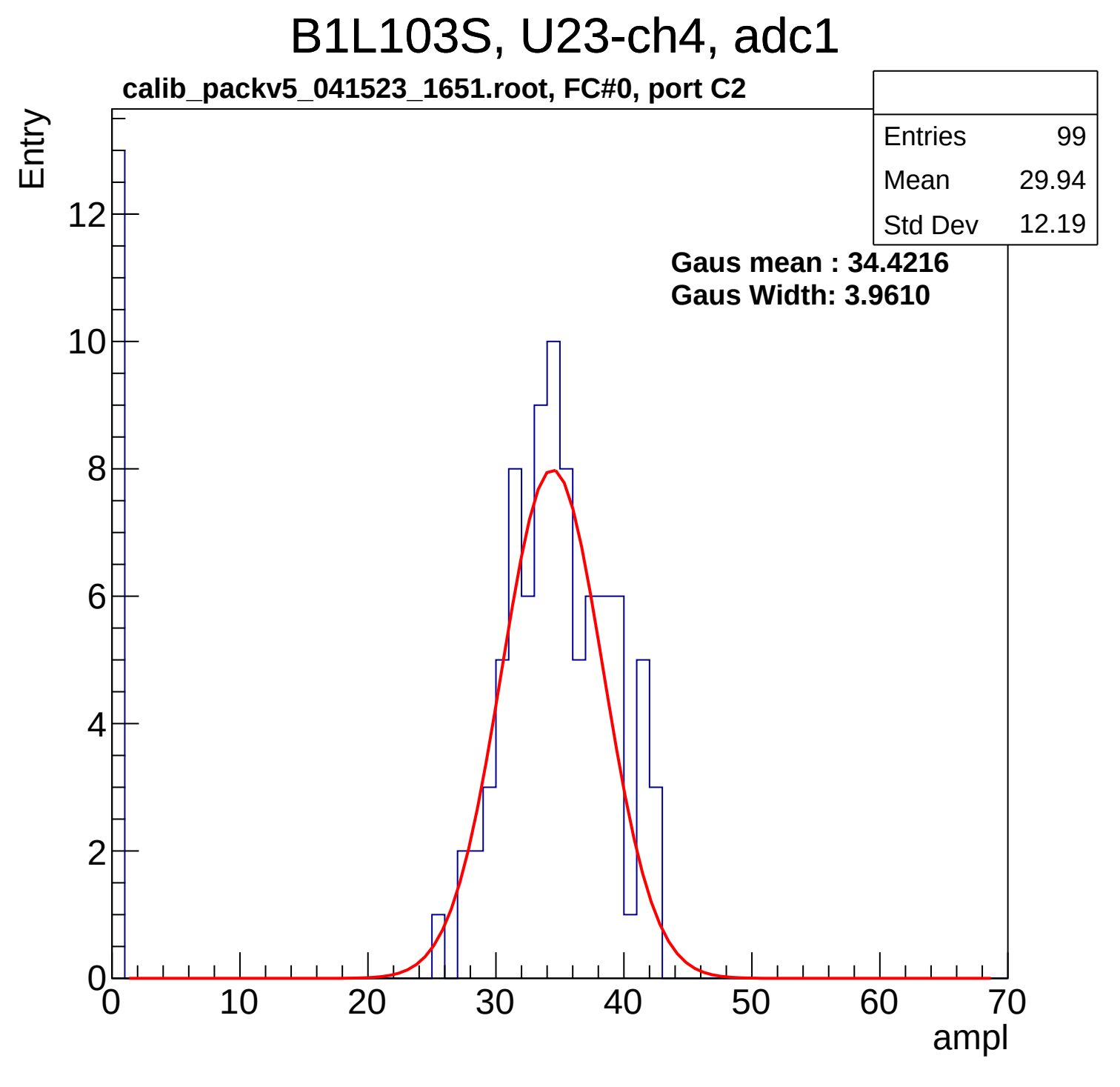

B1L103S, U23-ch3, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 55.93
Std Dev 3.552
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

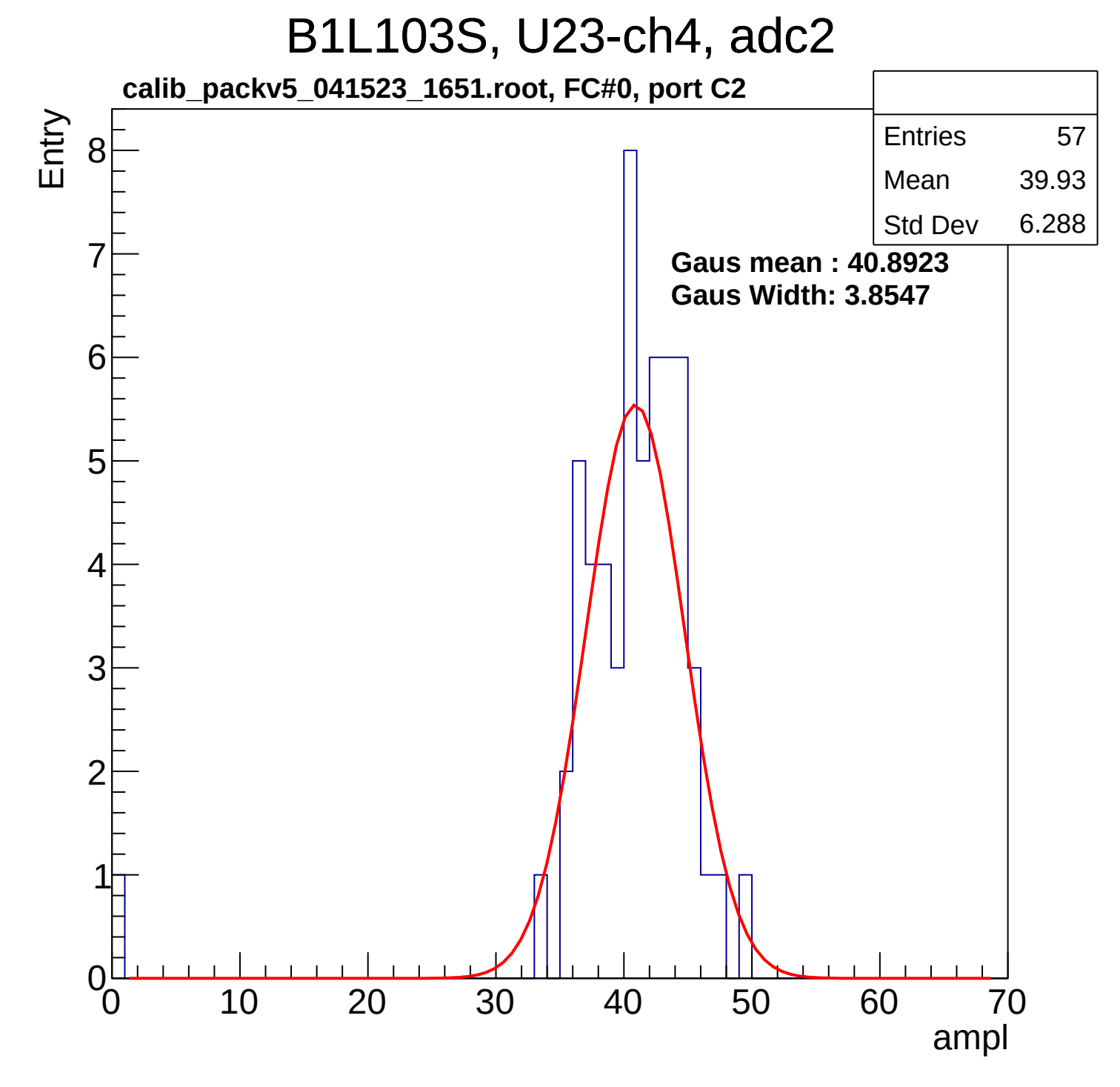

B1L103S, U23-ch3, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 38
Mean 58.58
Std Dev 9.84
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

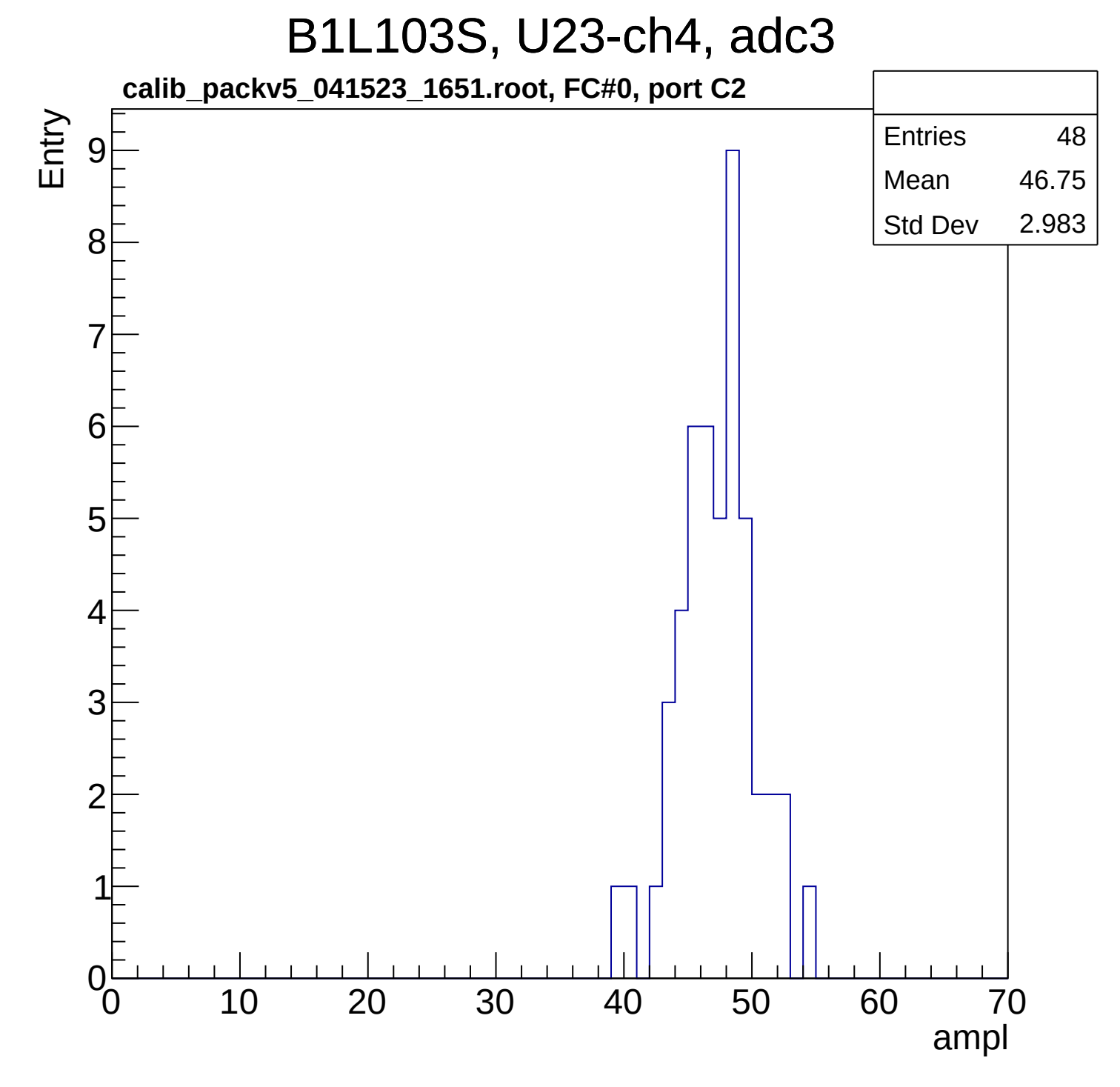

B1L103S, U23-ch3, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

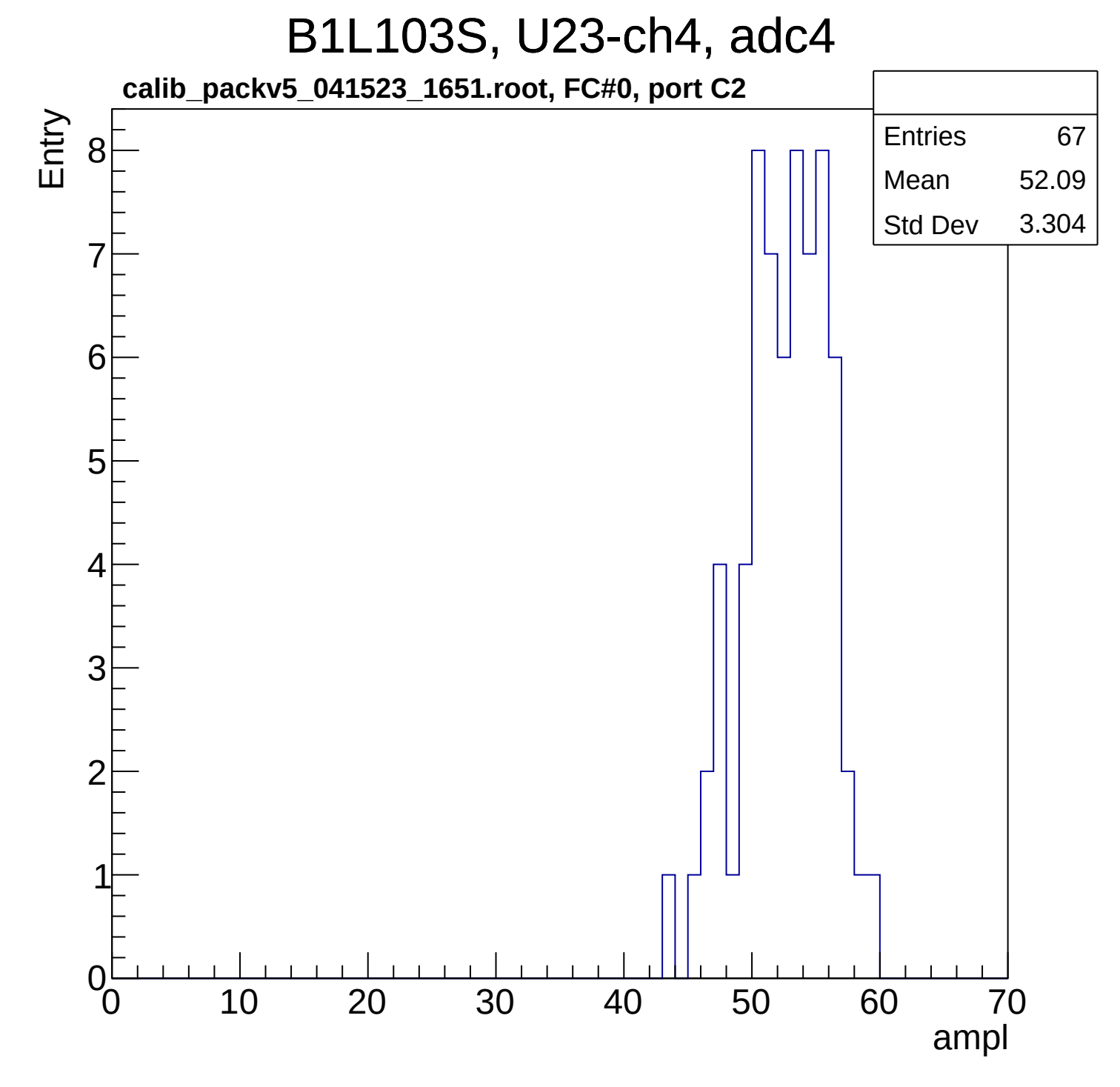

B1L103S, U23-ch3, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

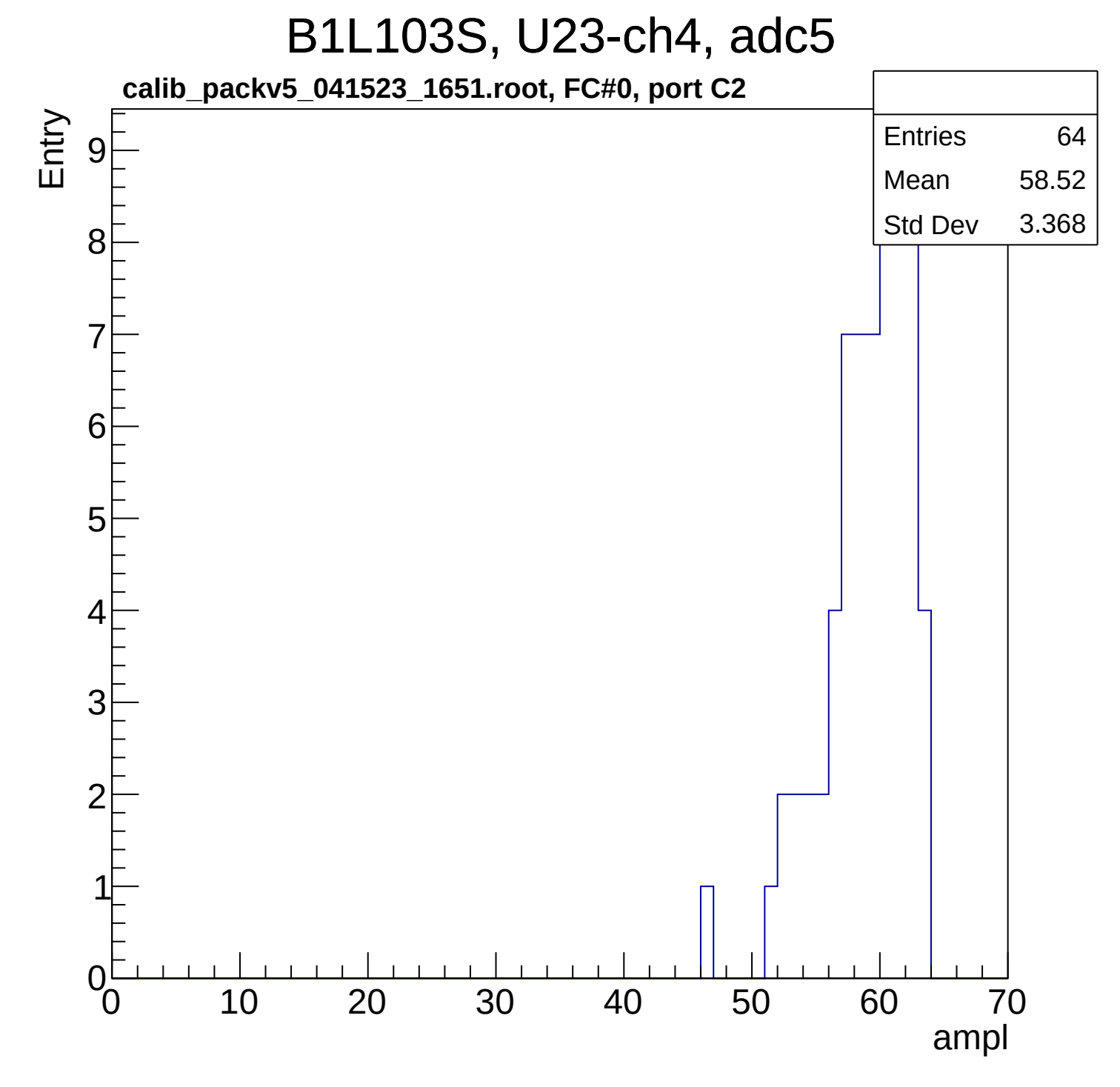

B1L103S, U23-ch4, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 92
Mean 20.86
Std Dev 10.74
Gaus mean : 26.7424
Gaus Width: 3.6471
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

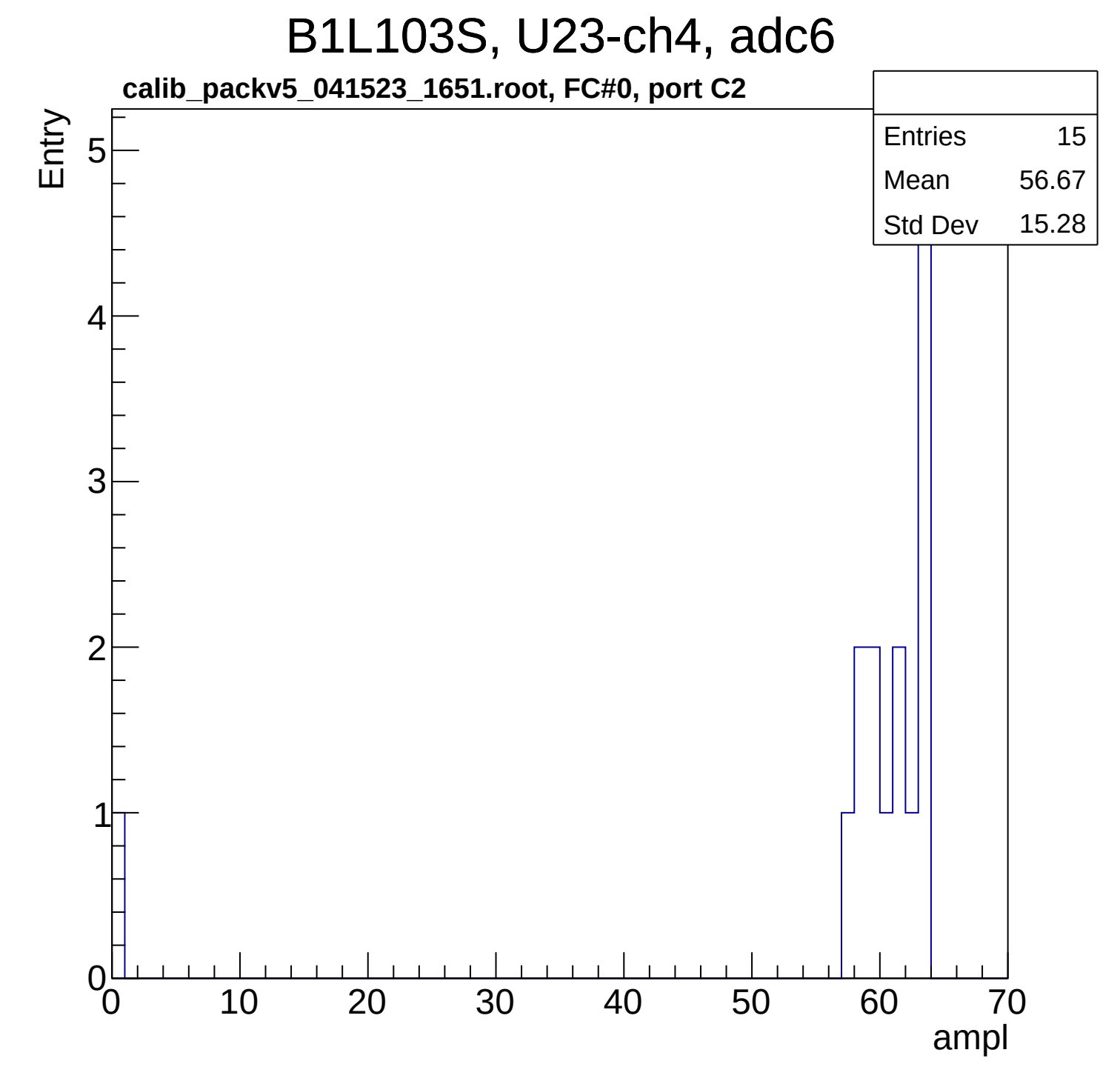

B1L103S, U23-ch4, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 99
Mean 29.94
Std Dev 12.19
Gaus mean : 34.4216
Gaus Width: 3.9610
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

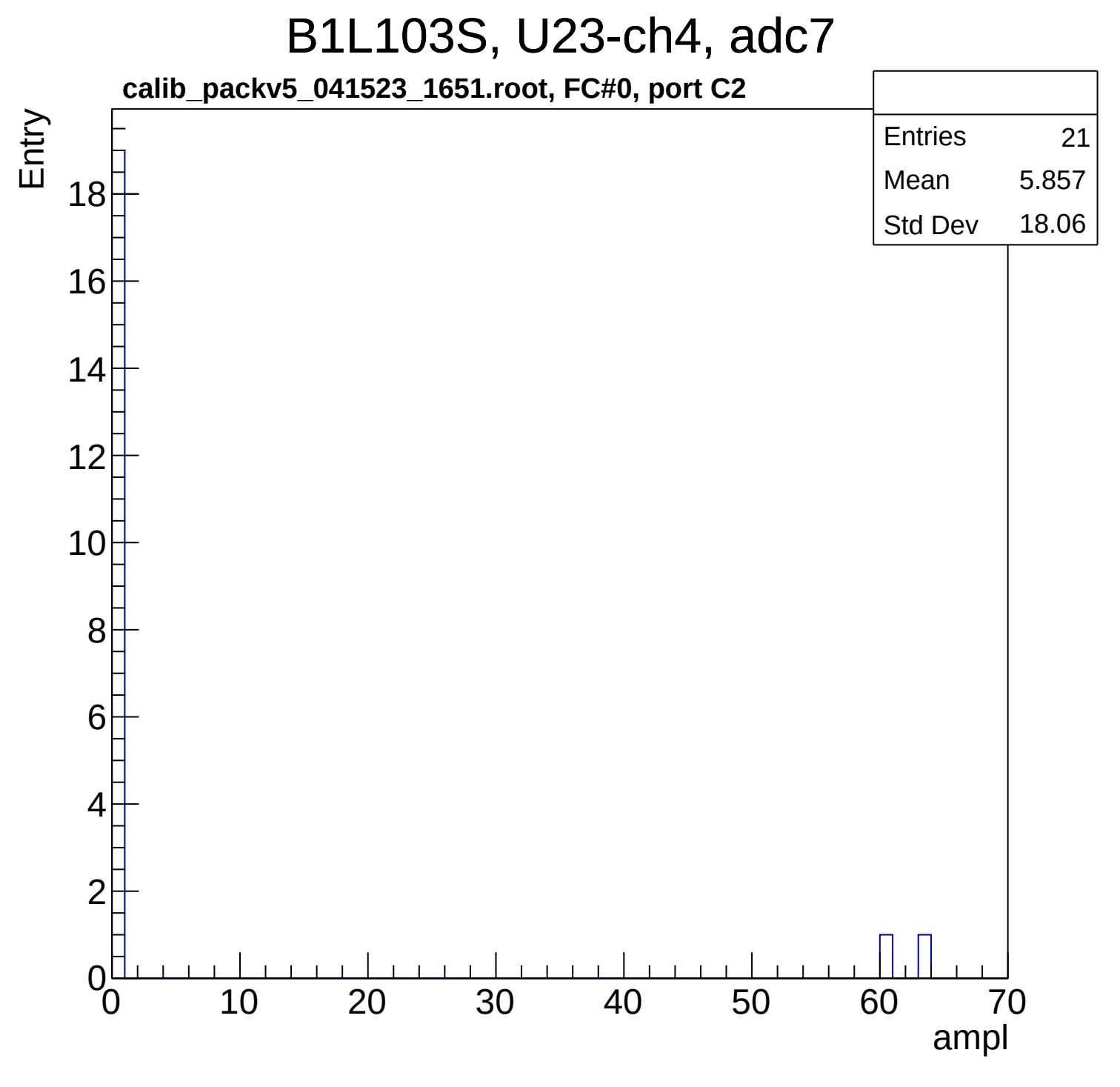

B1L103S, U23-ch4, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 39.93
Std Dev 6.288
Gaus mean : 40.8923
Gaus Width: 3.8547
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

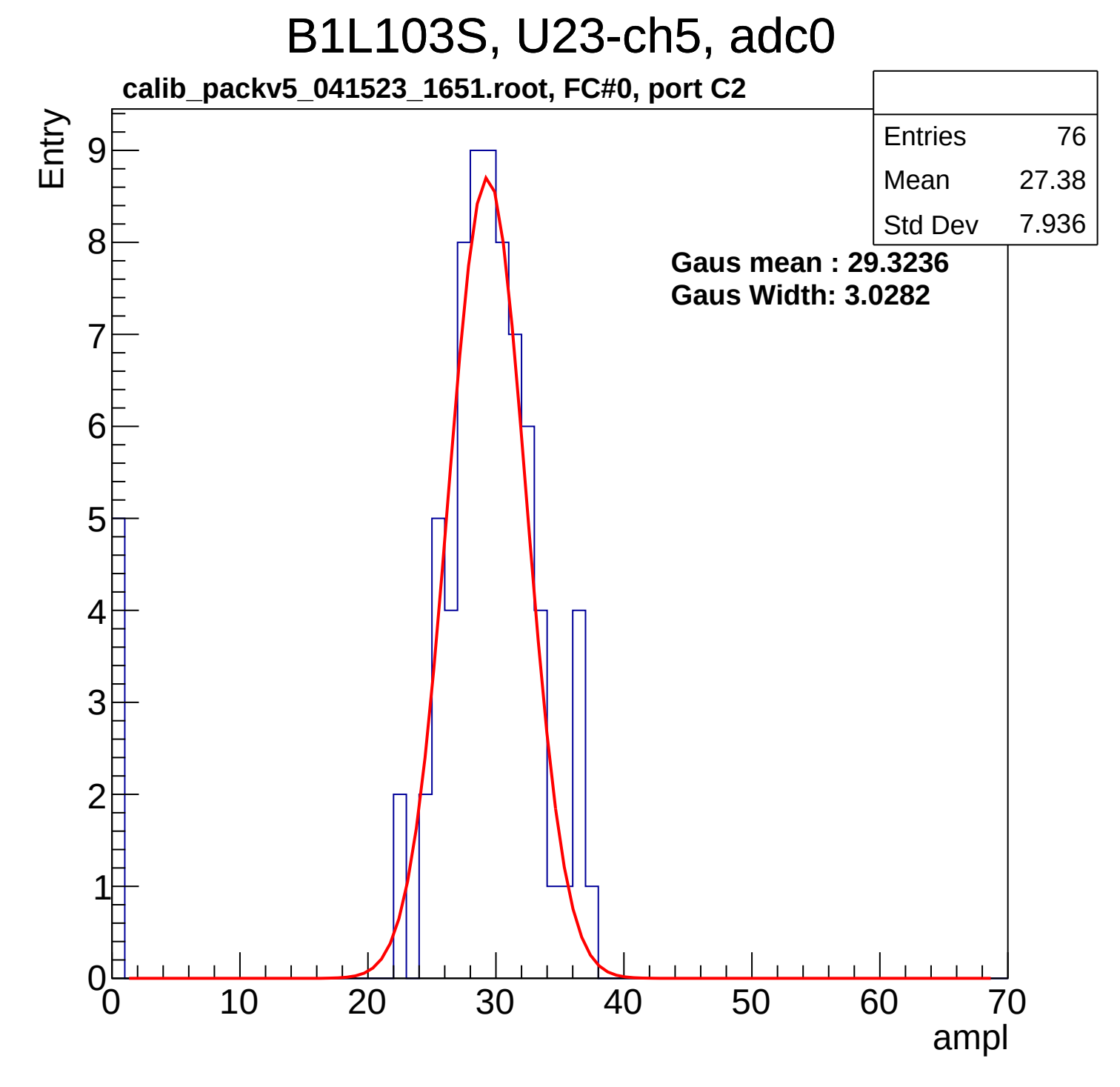

B1L103S, U23-ch4, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 46.75
Std Dev 2.983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

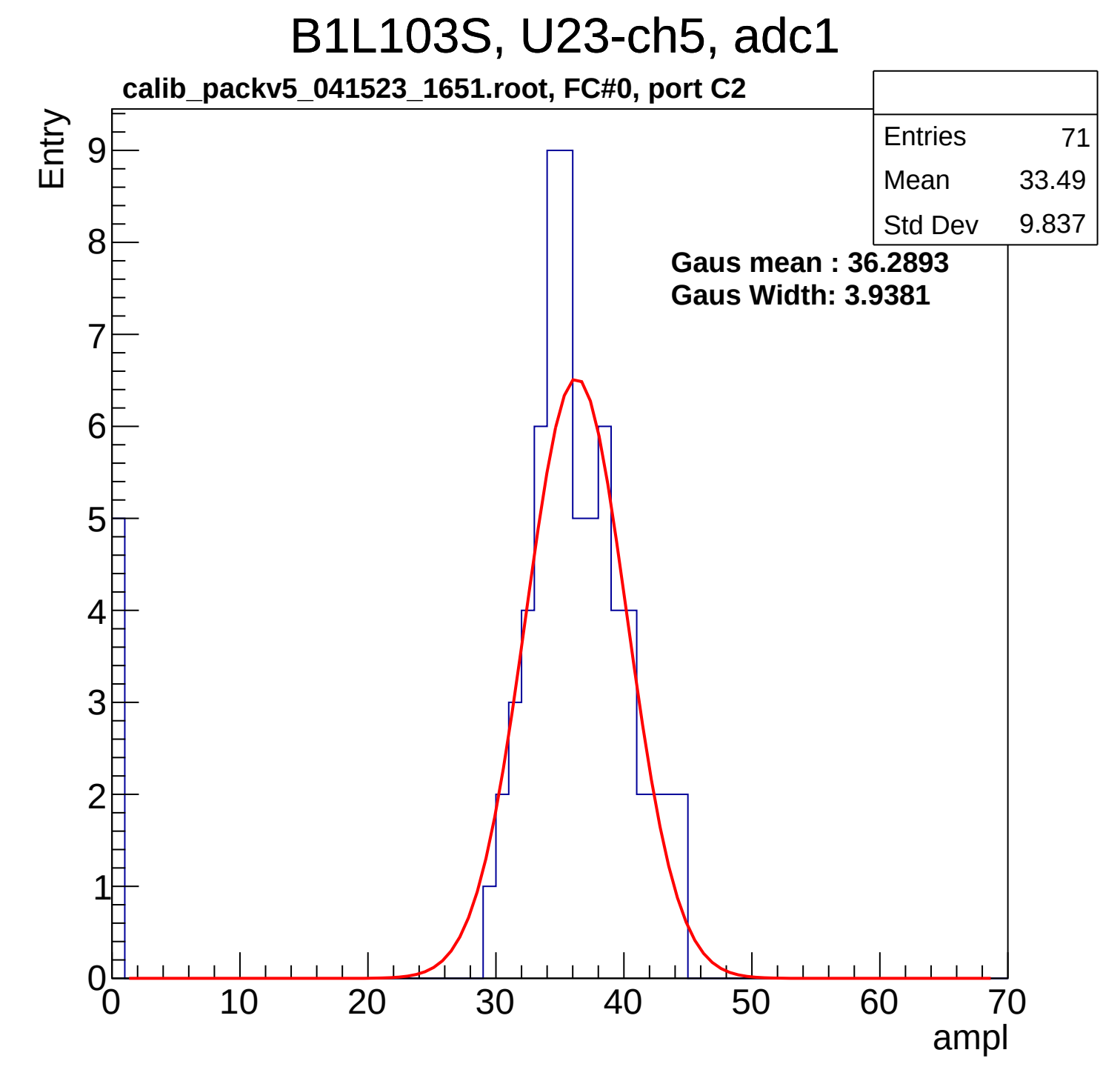

B1L103S, U23-ch4, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 52.09
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

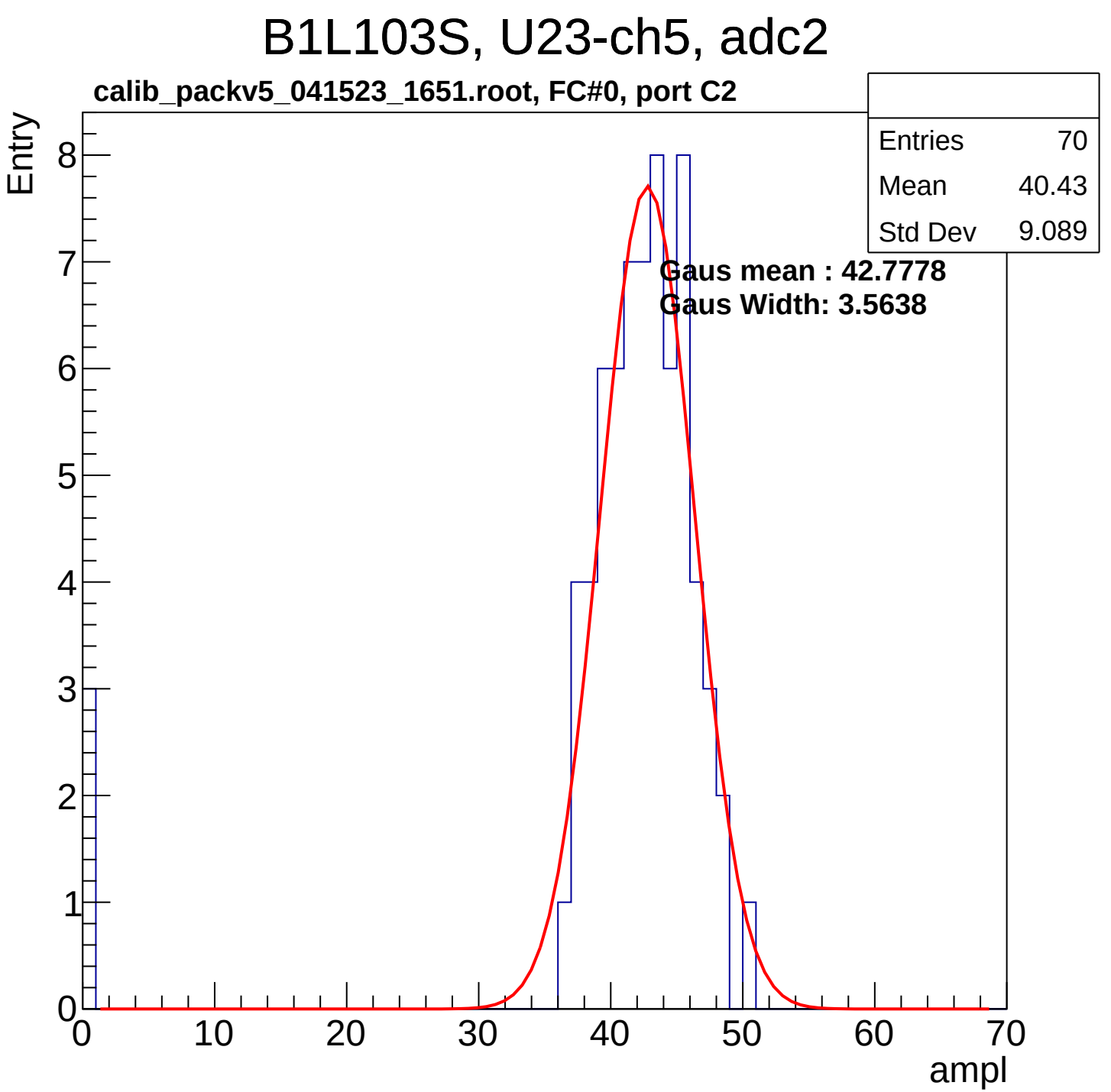

B1L103S, U23-ch4, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 58.52
Std Dev 3.368
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

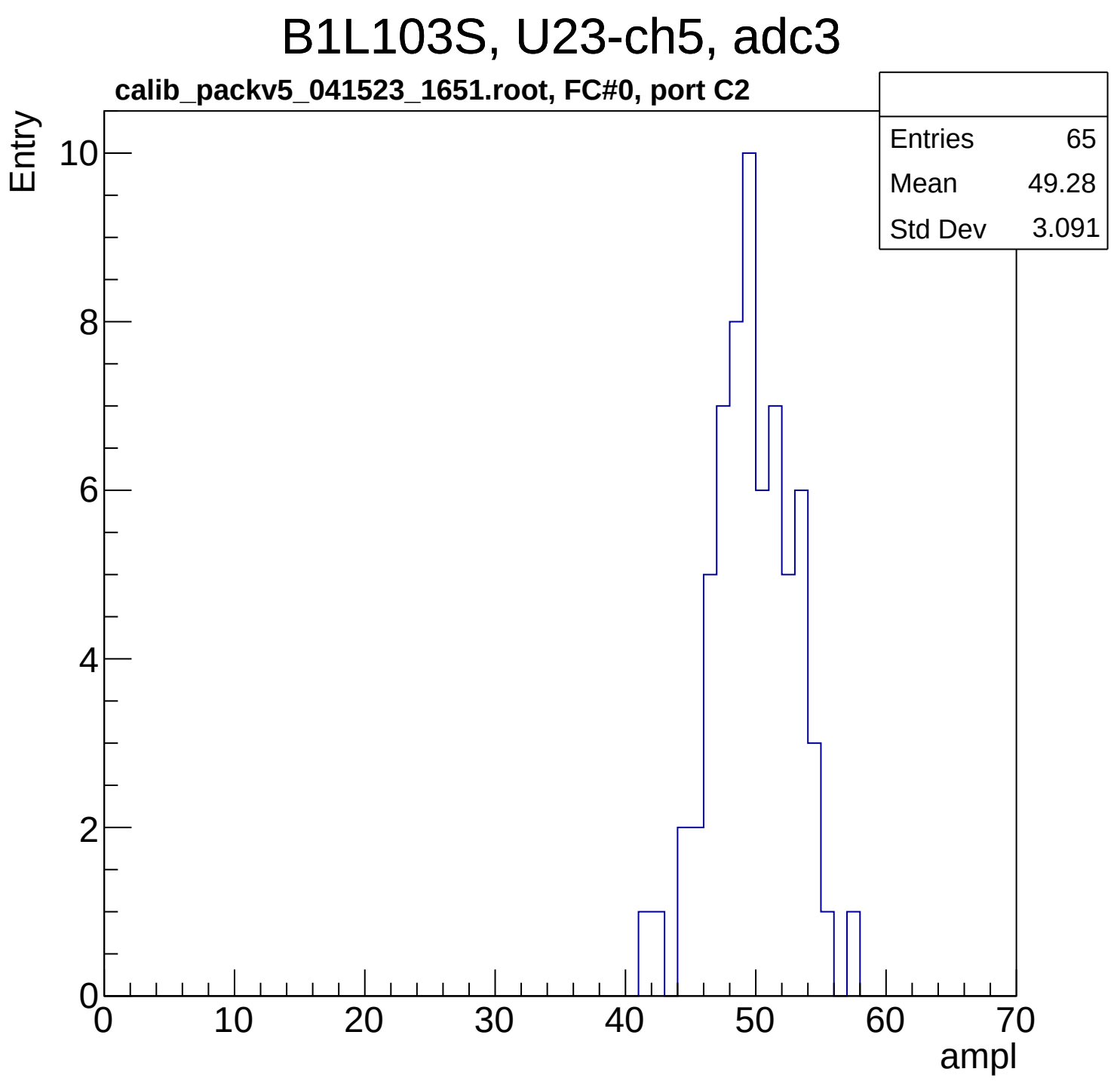

B1L103S, U23-ch4, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 15
Mean 56.67
Std Dev 15.28
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

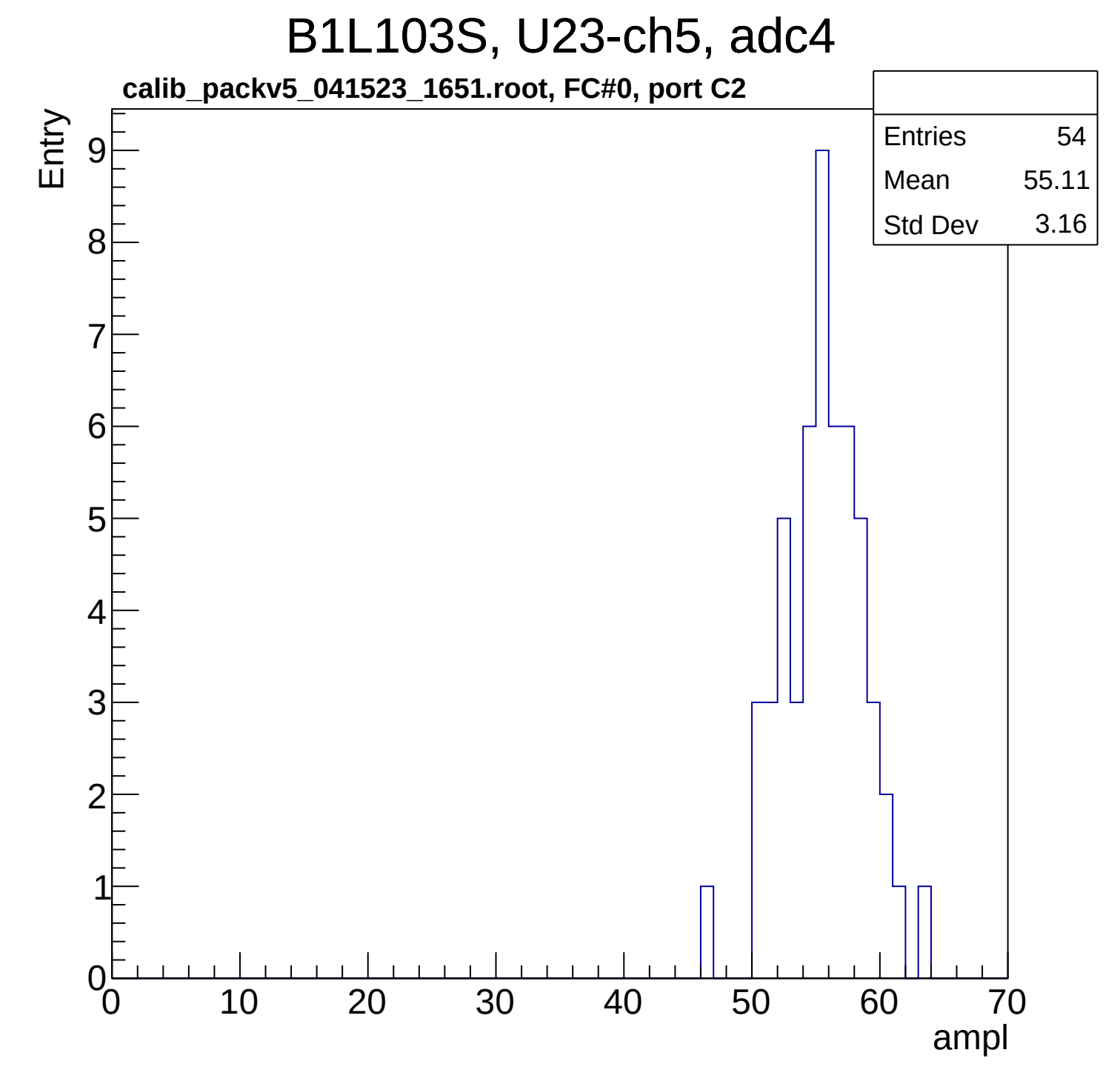

B1L103S, U23-ch4, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.857
Std Dev 18.06
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

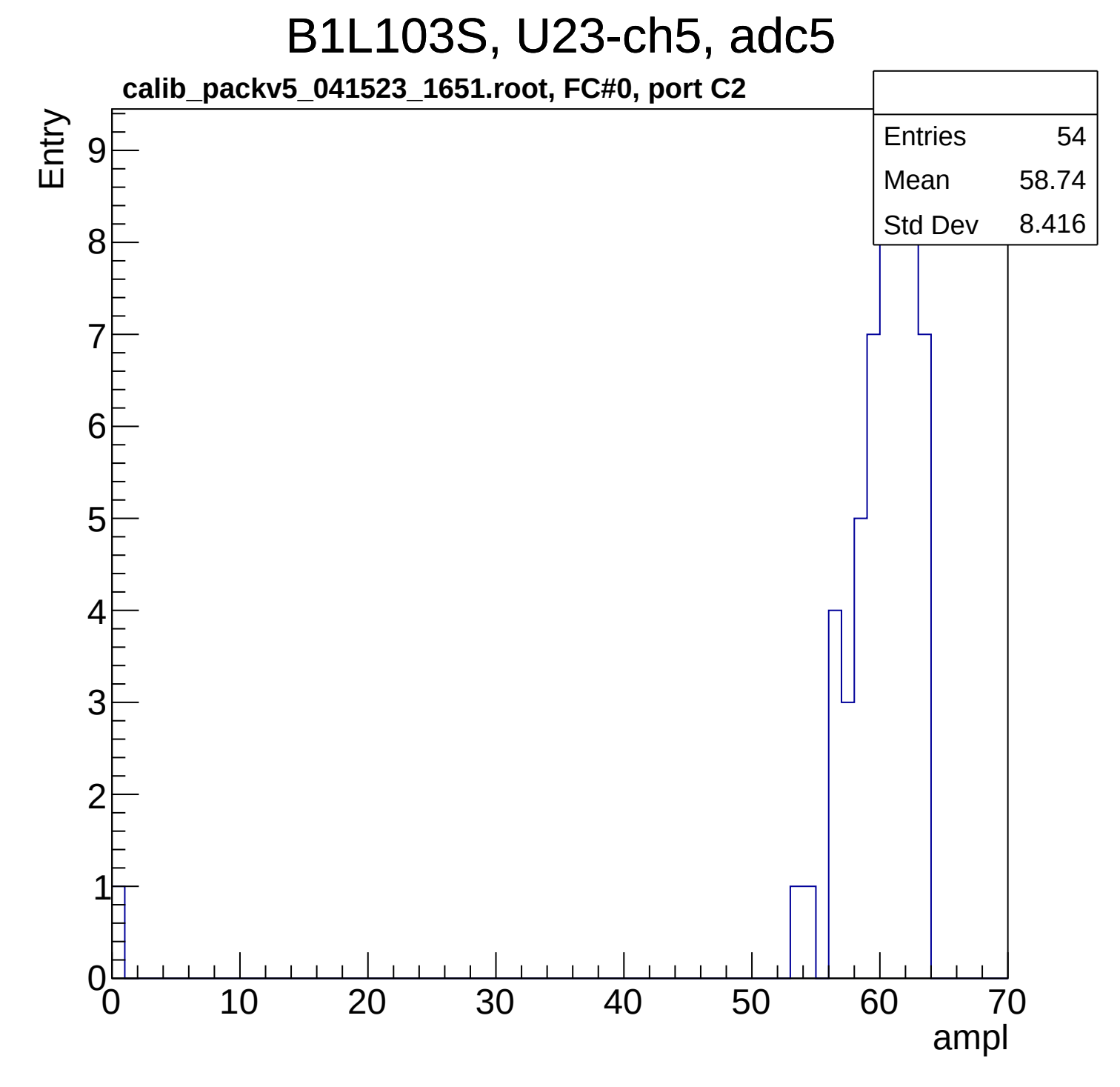

B1L103S, U23-ch5, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 27.38
Std Dev 7.936
Gaus mean : 29.3236
Gaus Width: 3.0282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

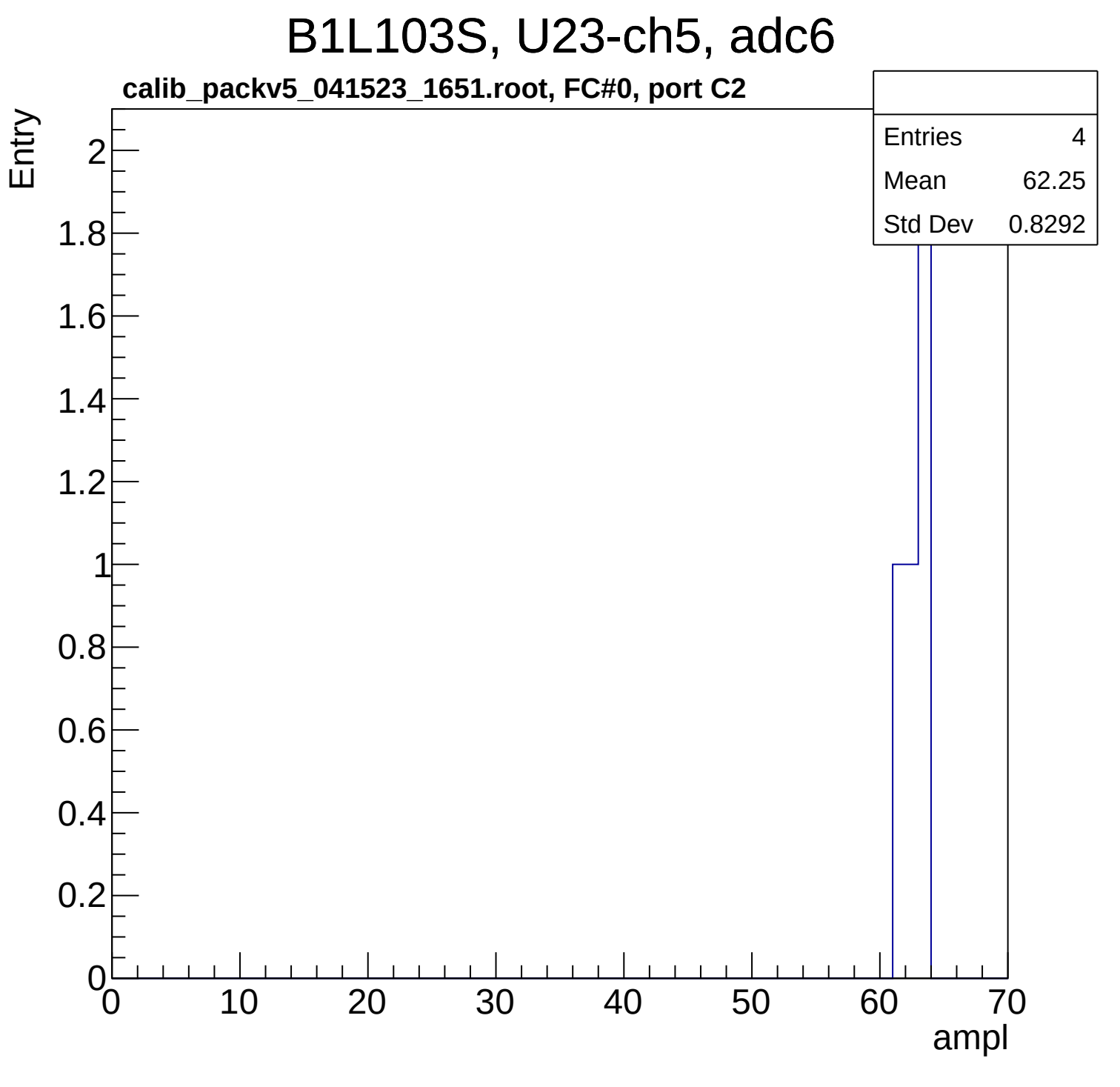

B1L103S, U23-ch5, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 33.49
Std Dev 9.837
Gaus mean : 36.2893
Gaus Width: 3.9381
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

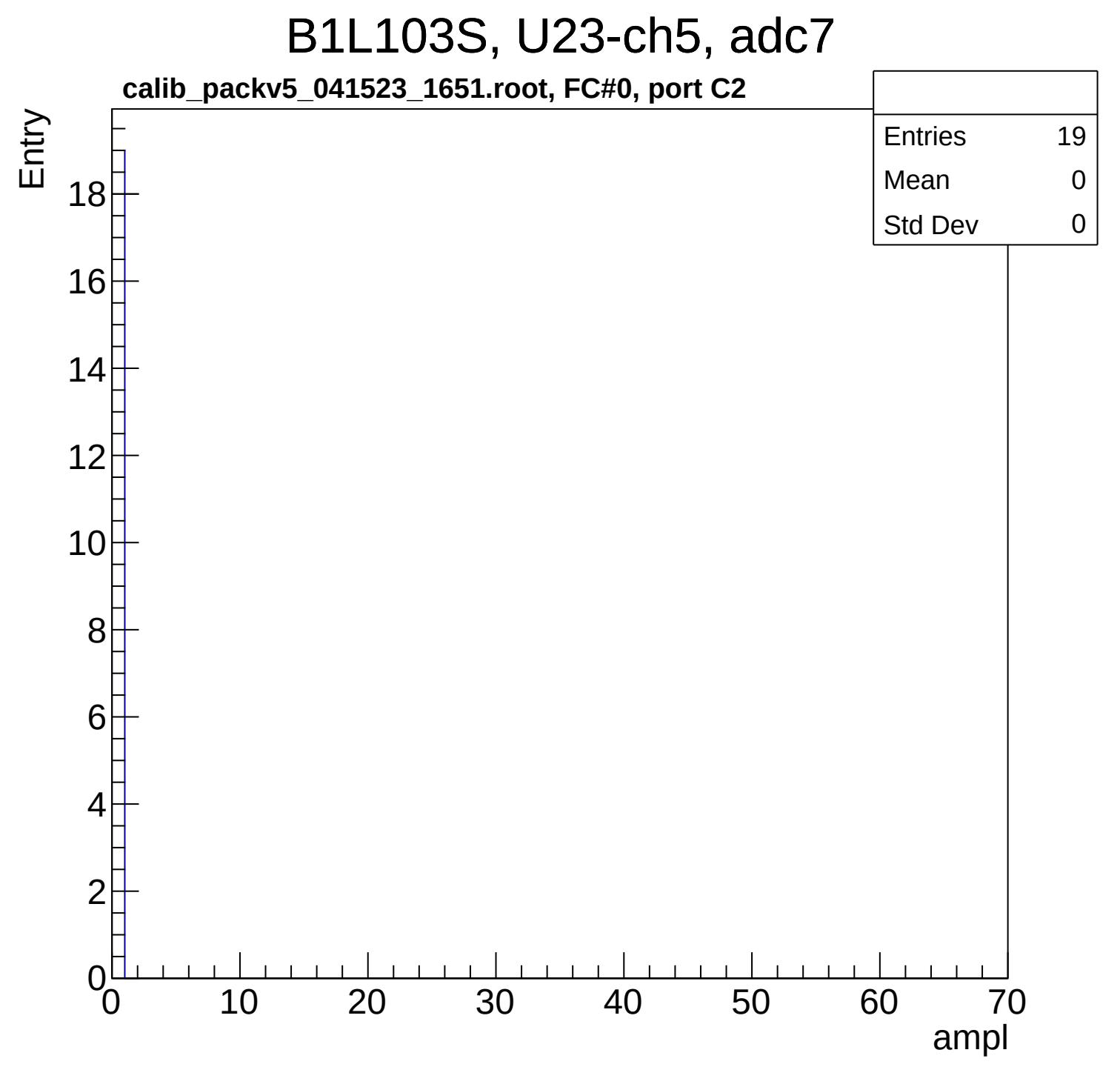

B1L103S, U23-ch5, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 40.43
Std Dev 9.089
Gaus mean : 42.7778
Gaus Width: 3.5638
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

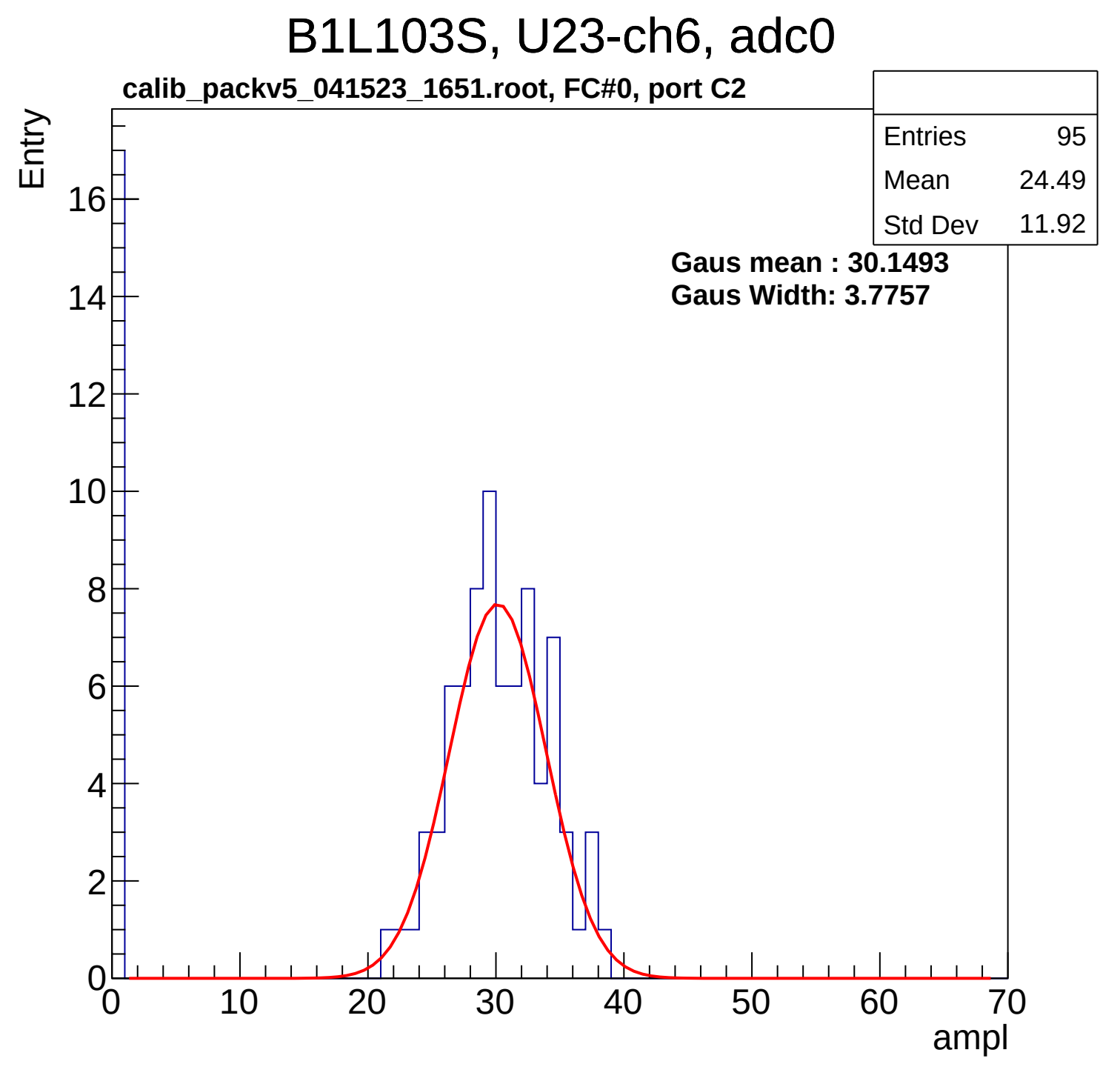

B1L103S, U23-ch5, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 49.28
Std Dev 3.091
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

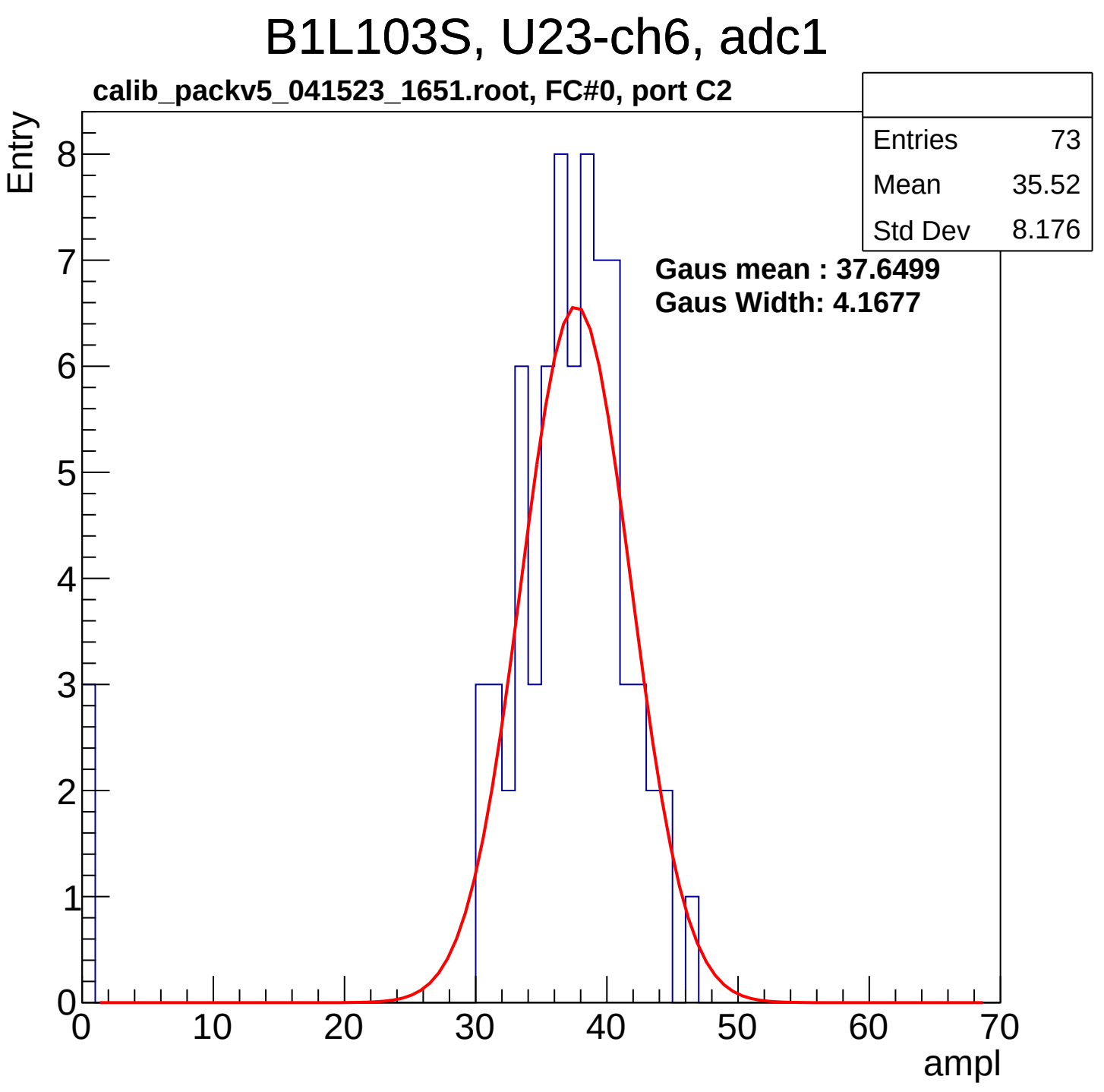

B1L103S, U23-ch5, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 55.11
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

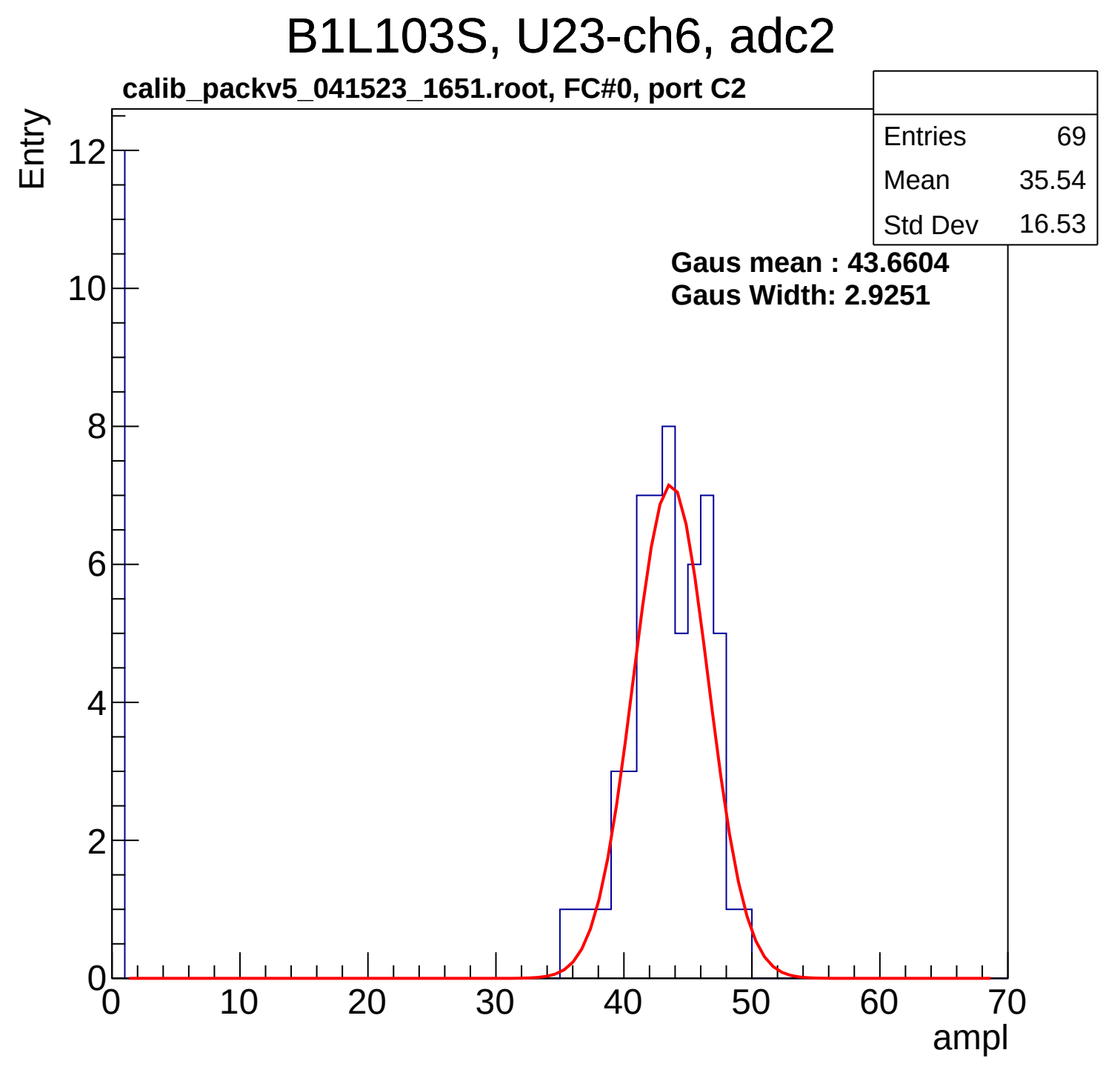

B1L103S, U23-ch5, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 58.74
Std Dev 8.416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

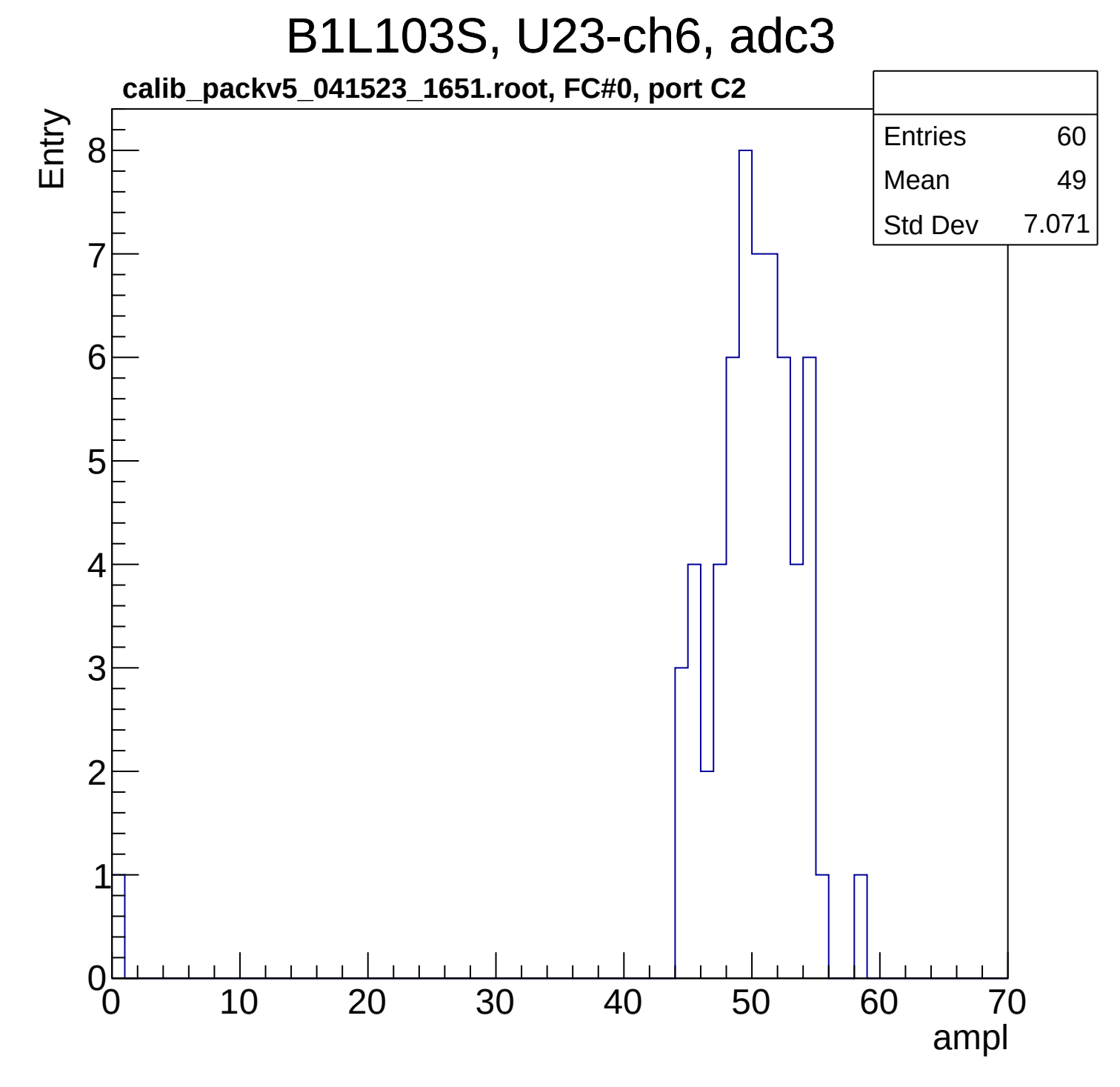

B1L103S, U23-ch5, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

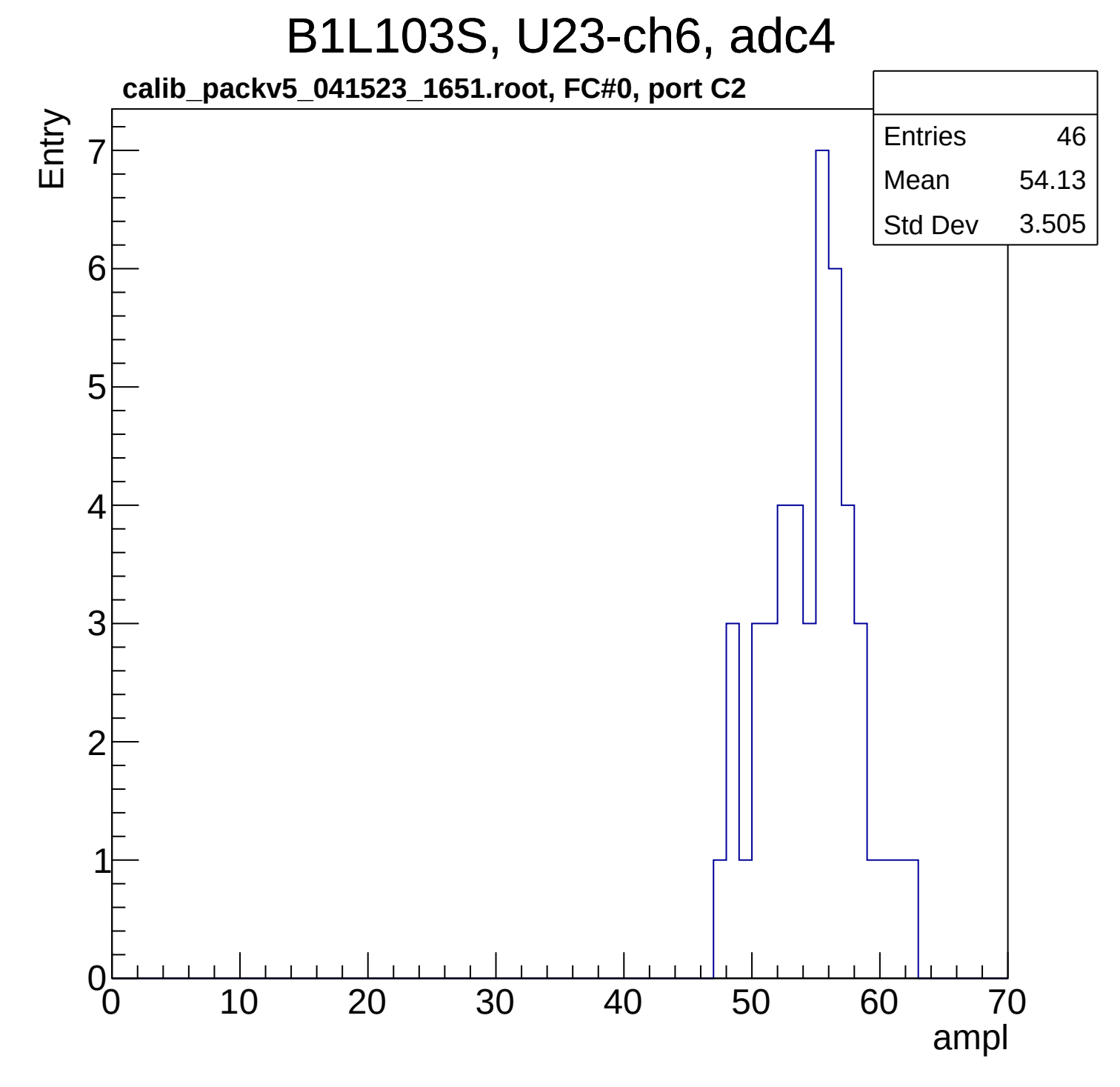

B1L103S, U23-ch5, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

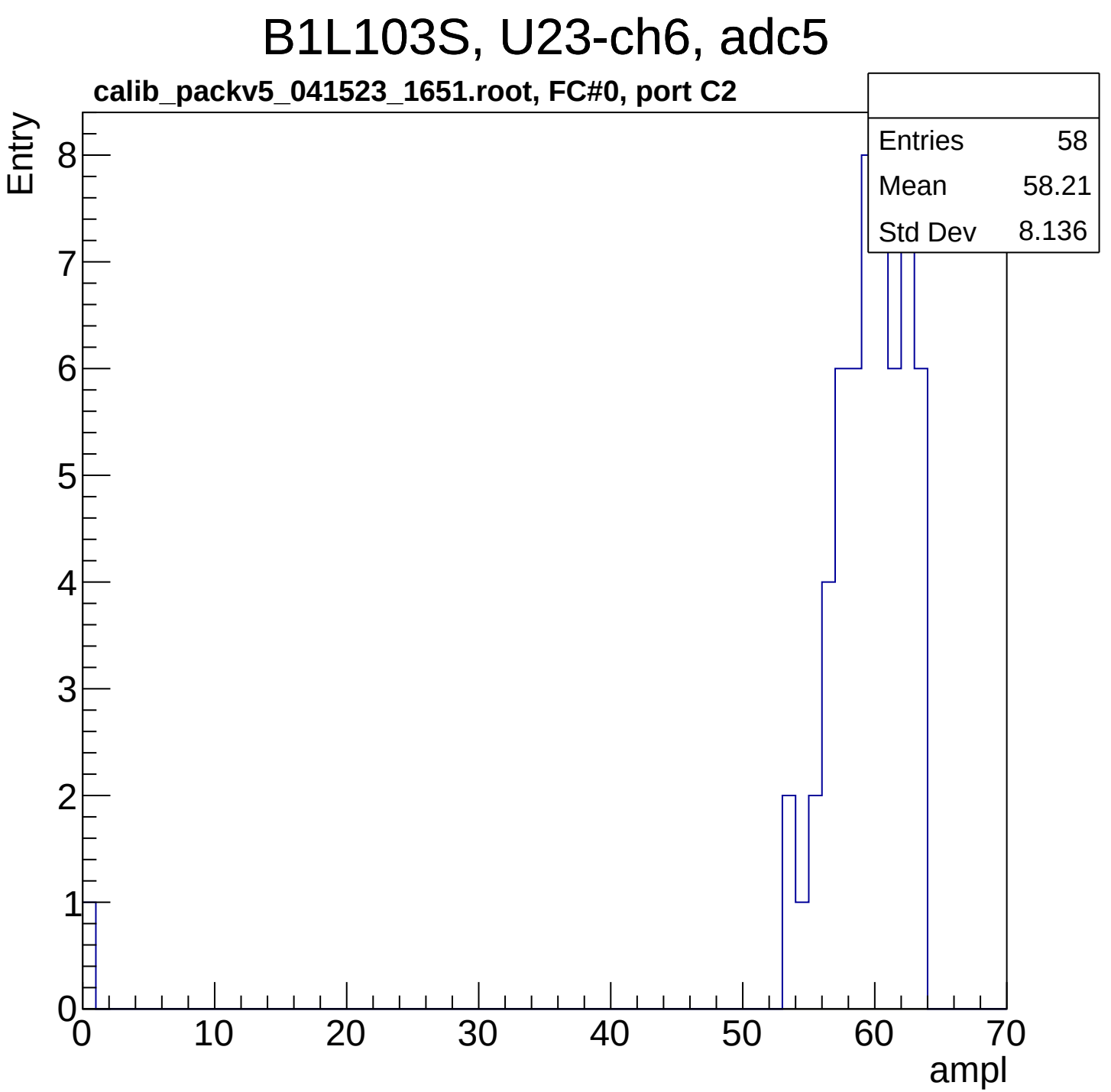

B1L103S, U23-ch6, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 95
Mean 24.49
Std Dev 11.92
Gaus mean : 30.1493
Gaus Width: 3.7757
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

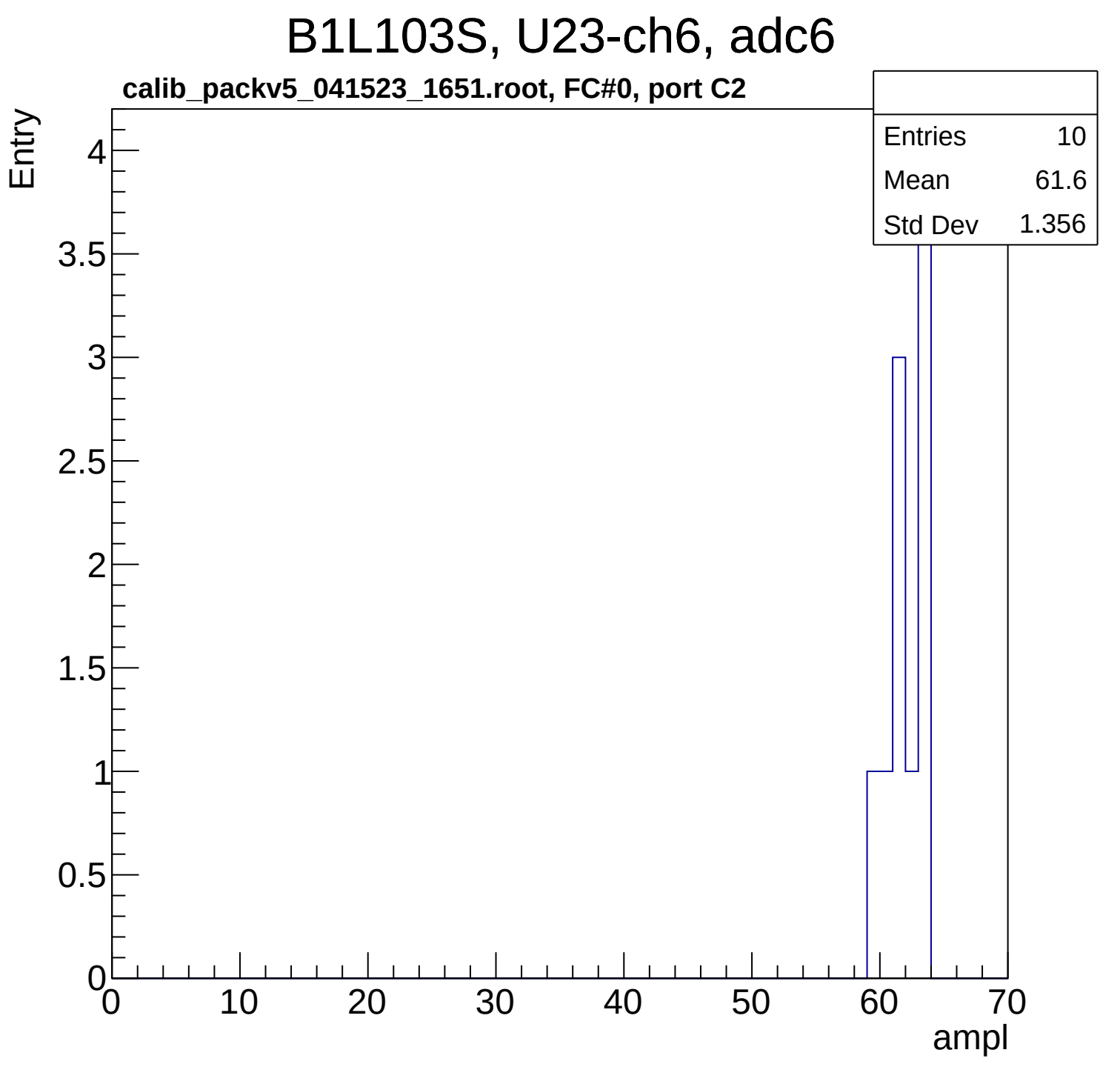

B1L103S, U23-ch6, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 35.52
Std Dev 8.176
Gaus mean : 37.6499
Gaus Width: 4.1677
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

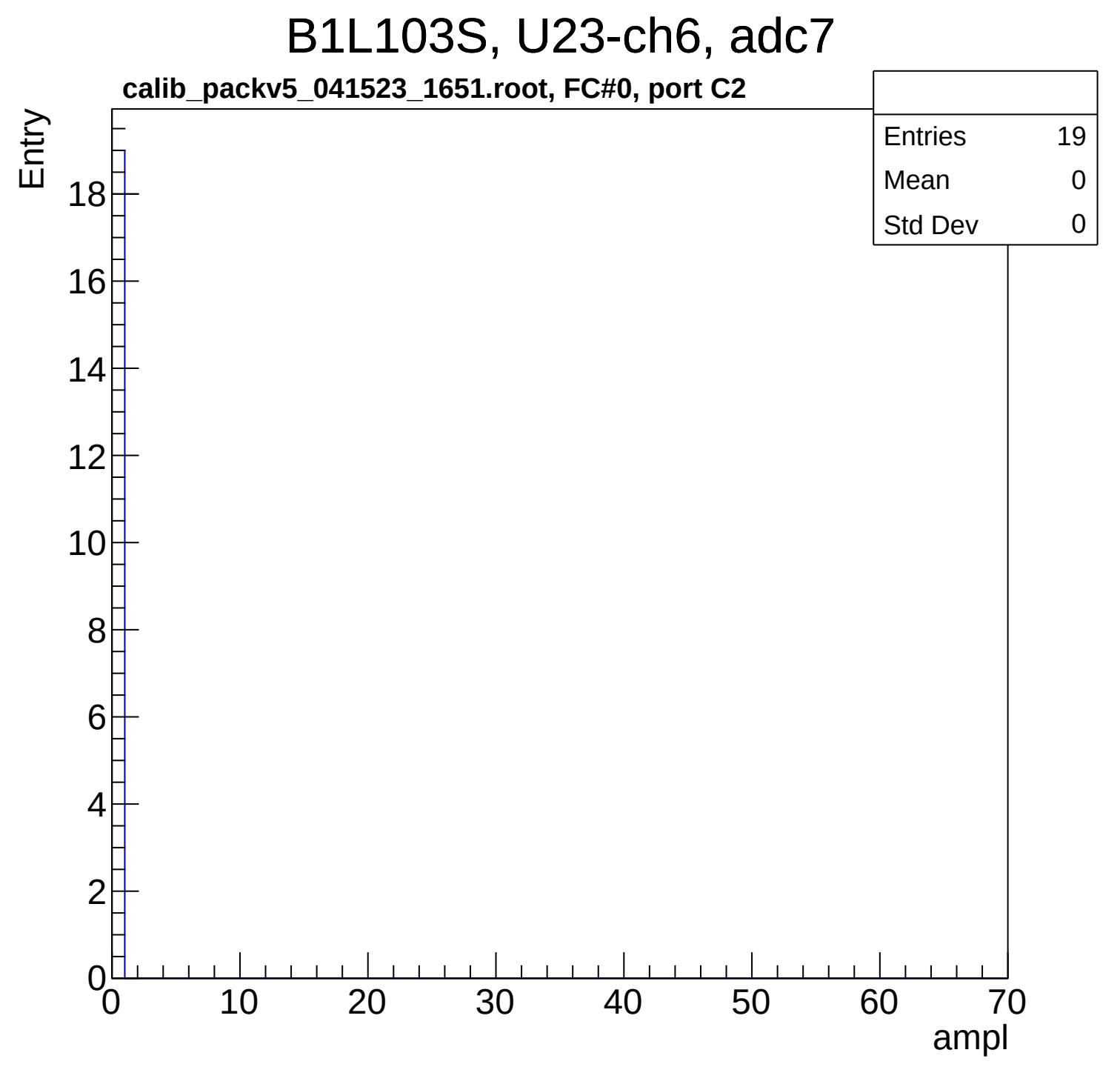

B1L103S, U23-ch6, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 35.54
Std Dev 16.53
Gaus mean : 43.6604
Gaus Width: 2.9251
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

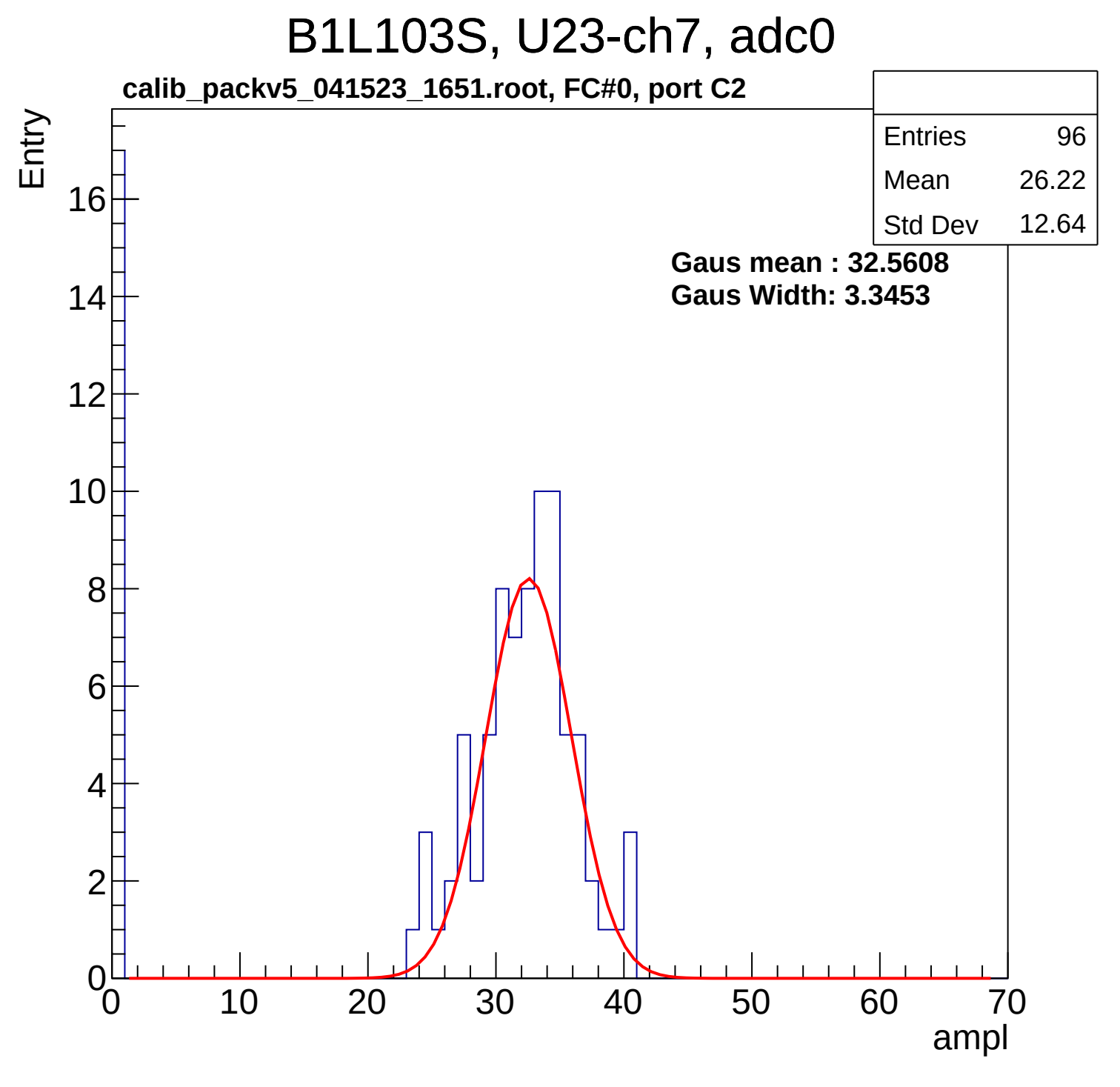

B1L103S, U23-ch6, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 49
Std Dev 7.071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

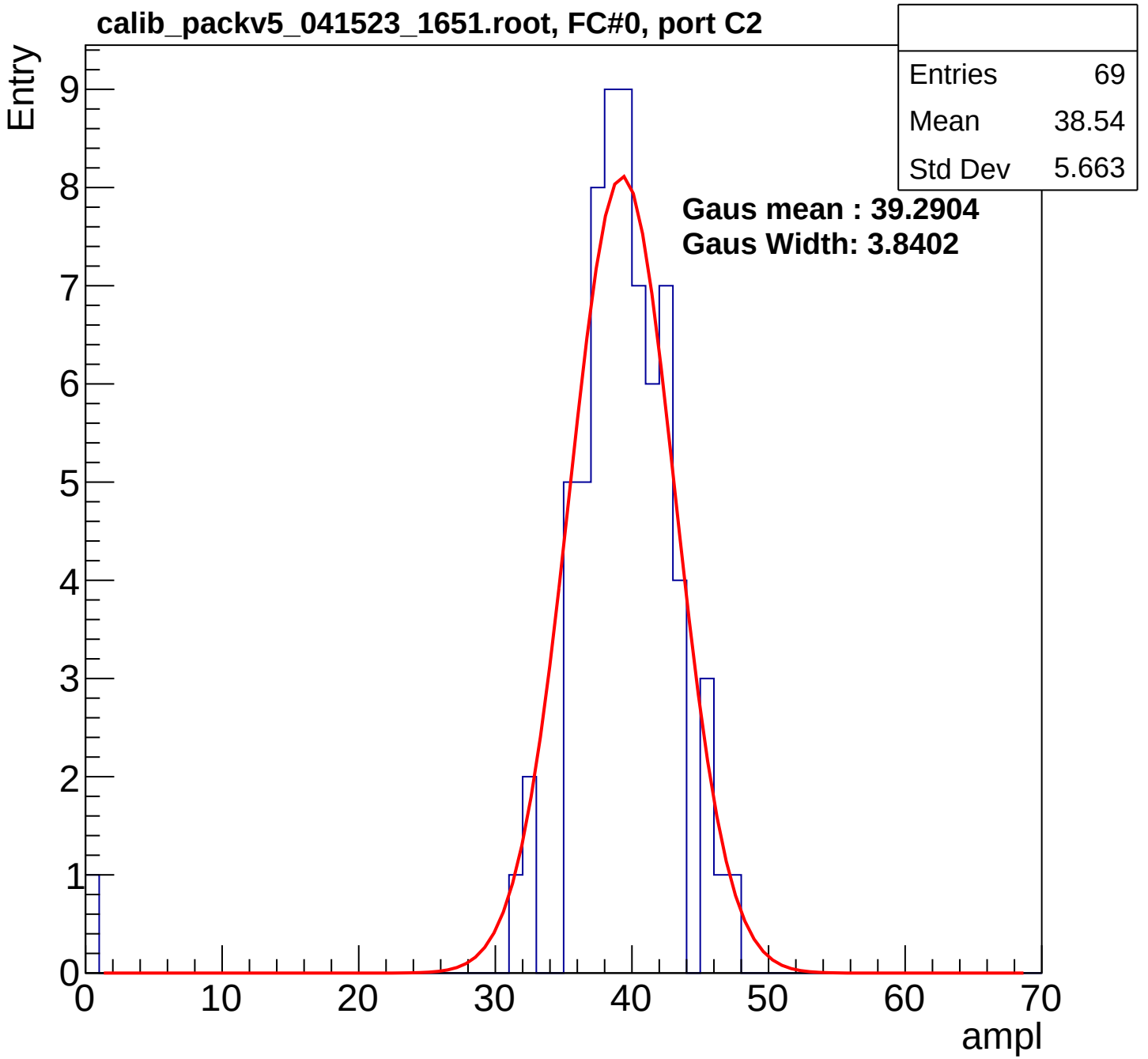

B1L103S, U23-ch6, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 46
Mean 54.13
Std Dev 3.505
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

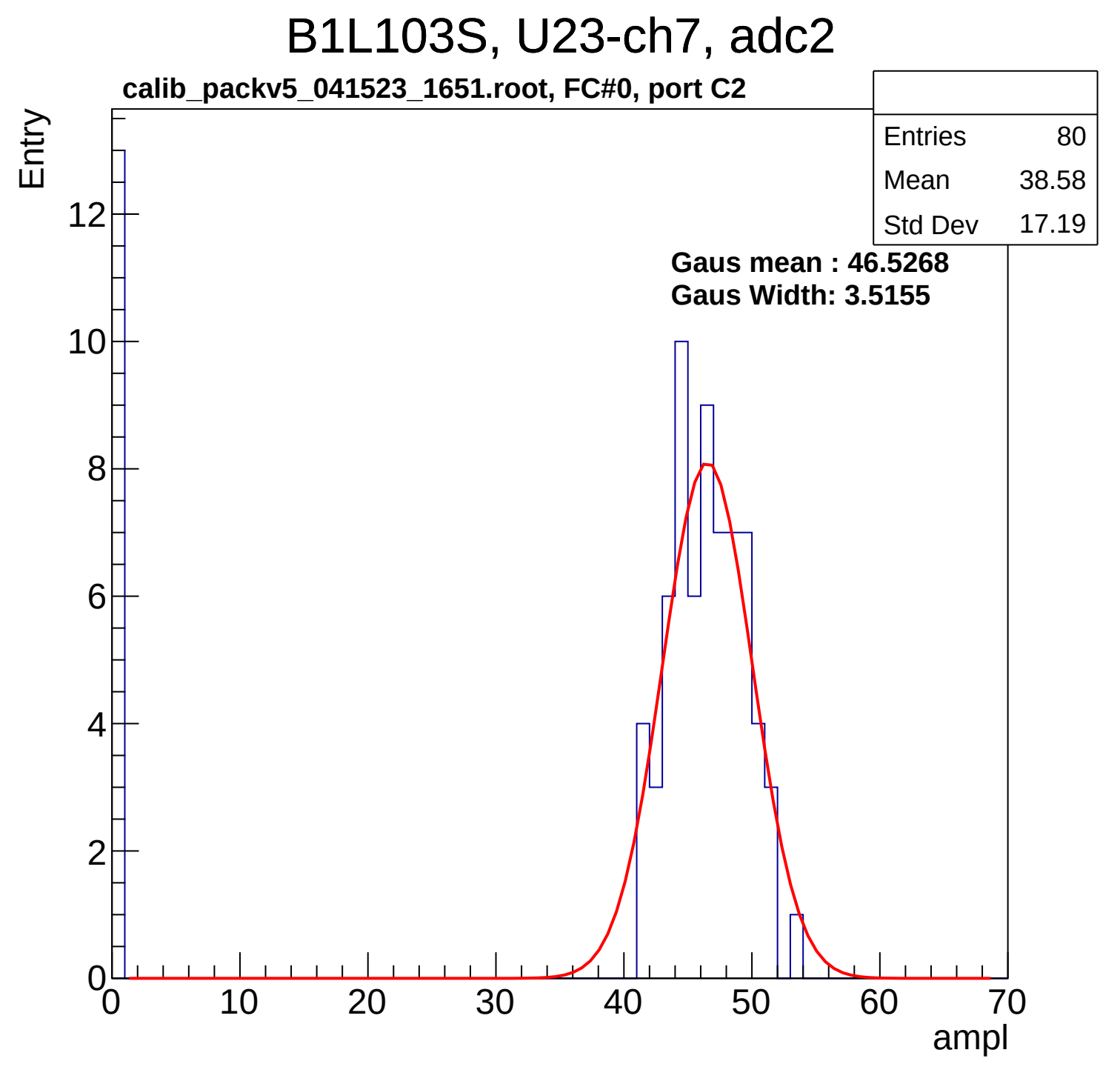

B1L103S, U23-ch6, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 58.21
Std Dev 8.136
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

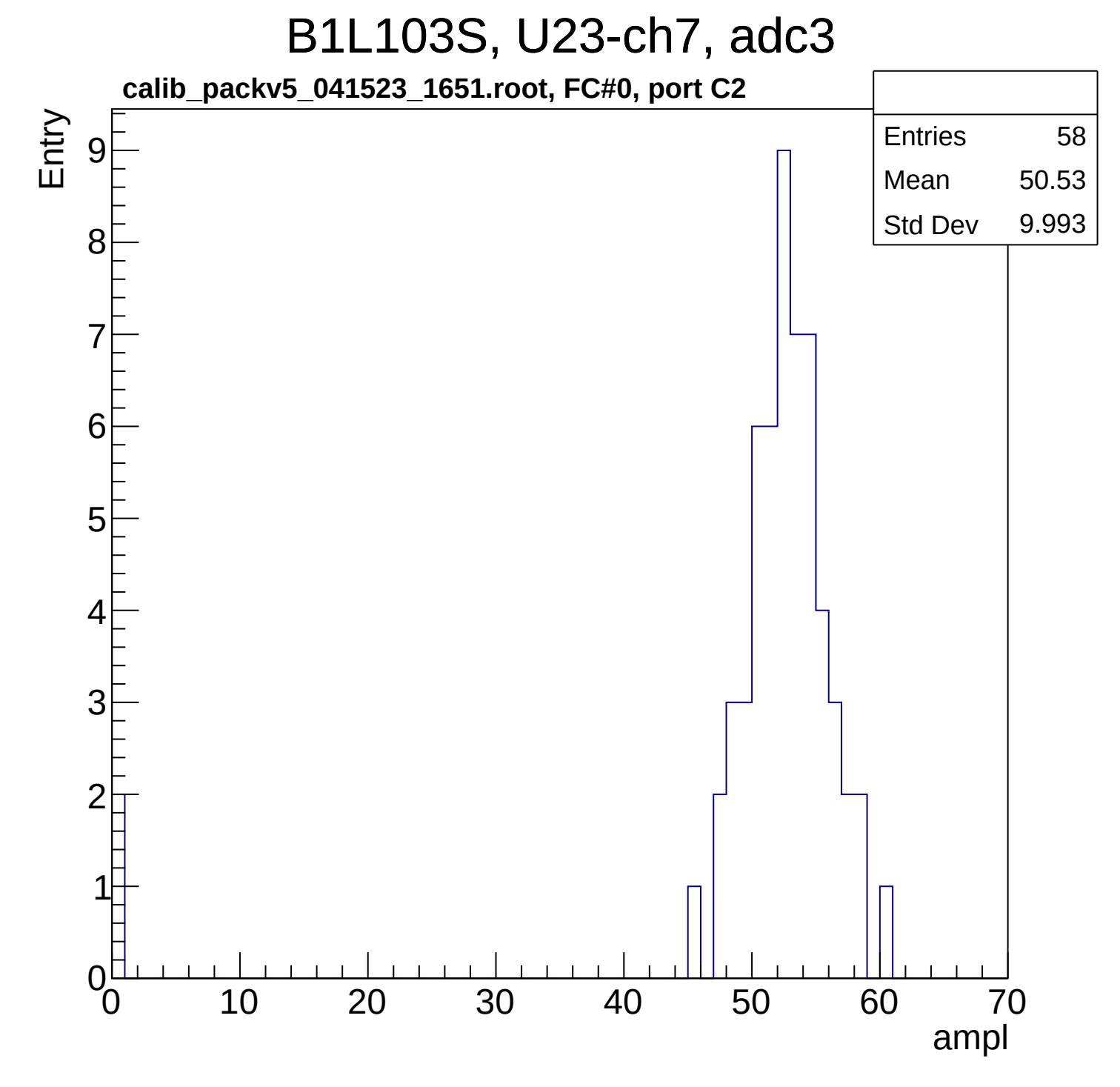

B1L103S, U23-ch6, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 61.6
Std Dev 1.356
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

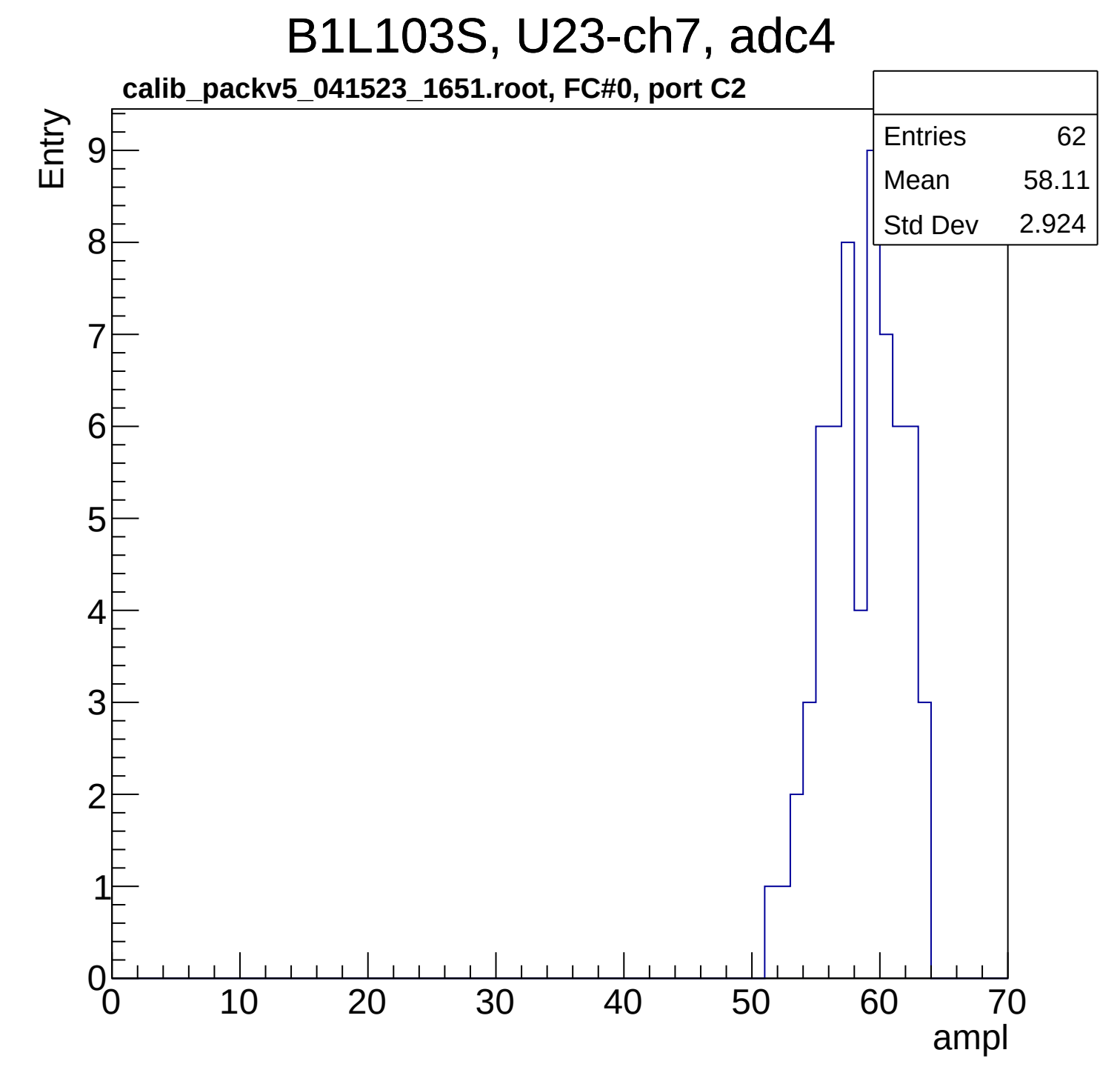

B1L103S, U23-ch6, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

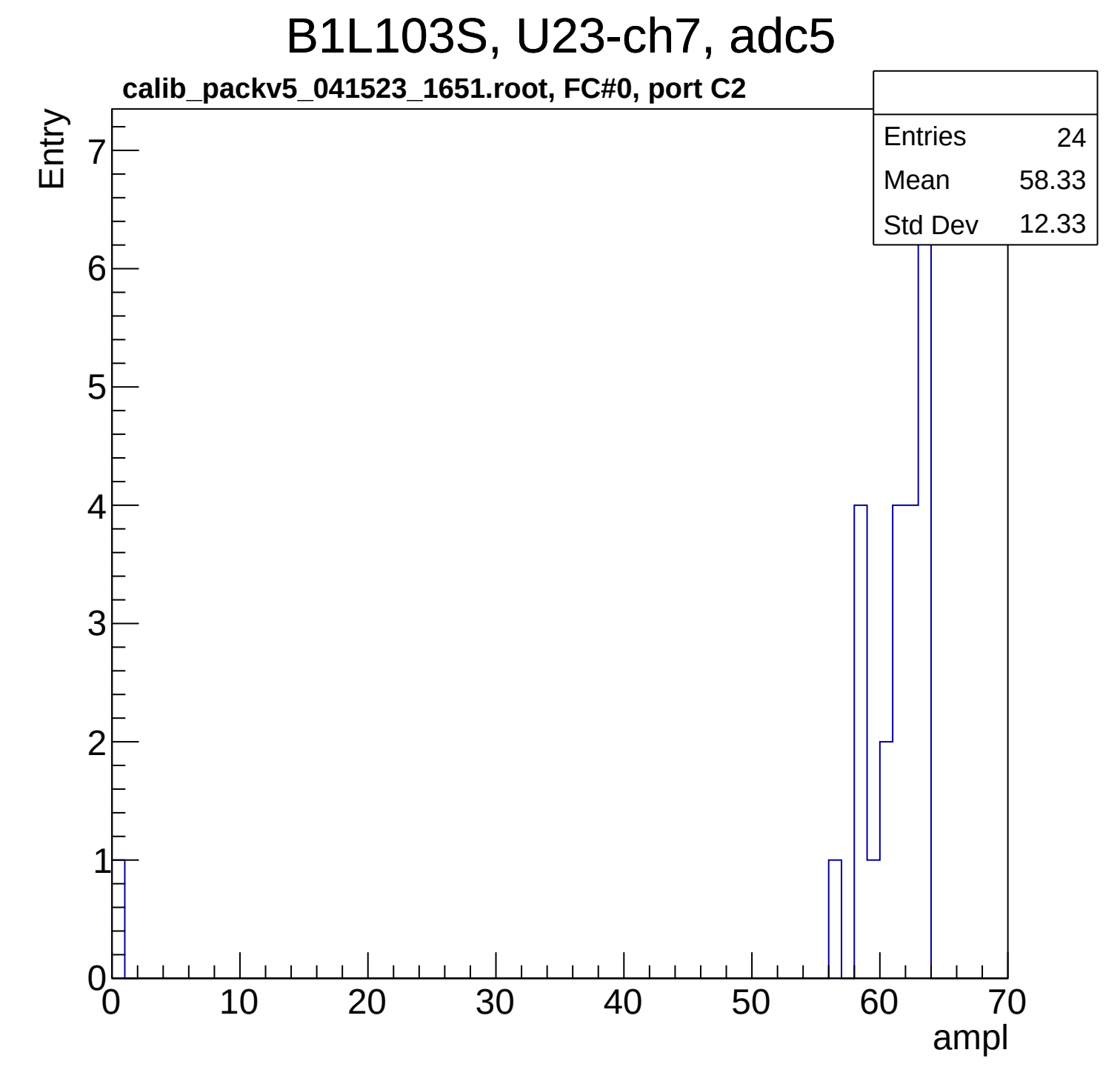

B1L103S, U23-ch7, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 96
Mean 26.22
Std Dev 12.64
Gaus mean : 32.5608
Gaus Width: 3.3453
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

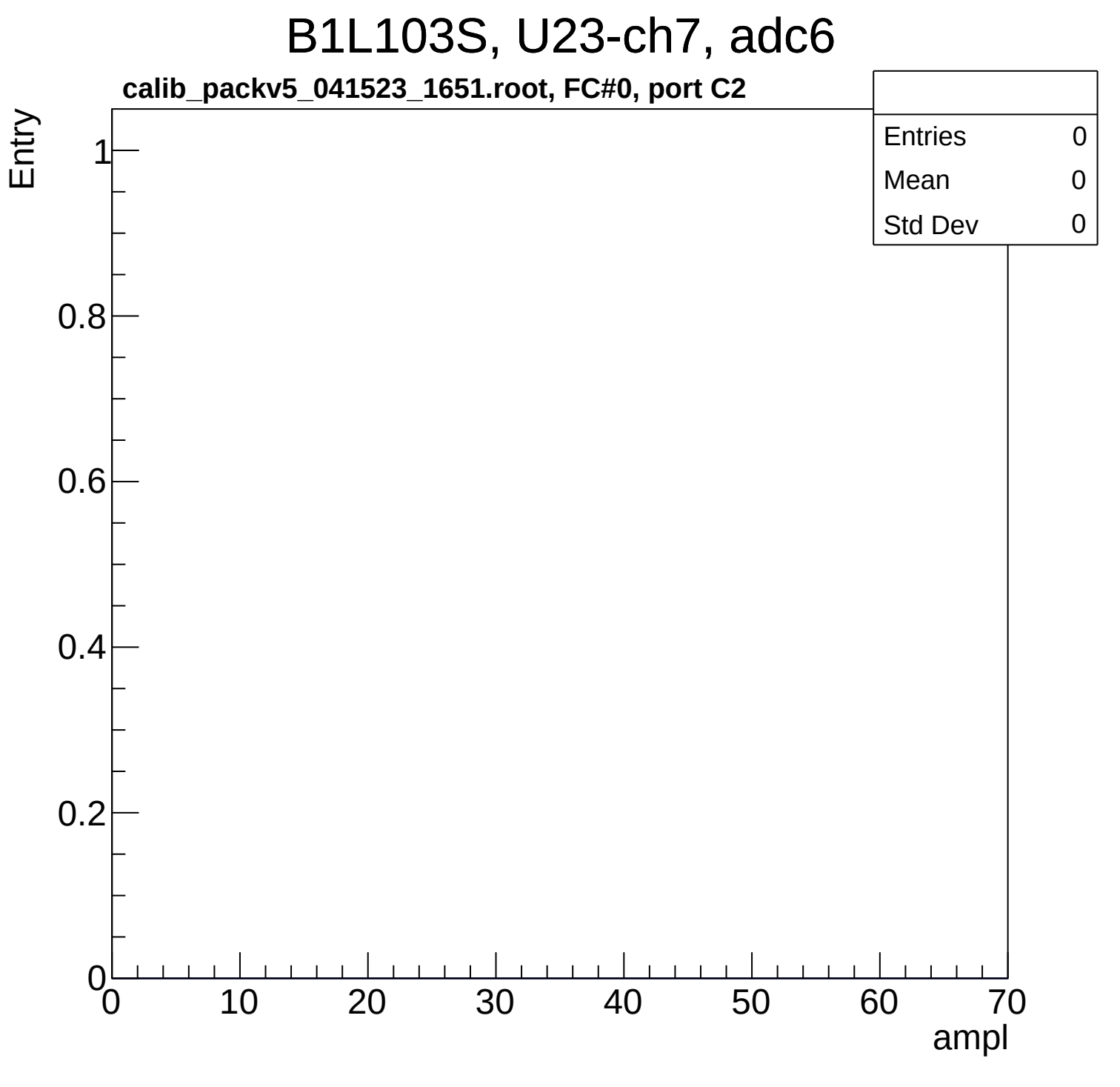

B1L103S, U23-ch7, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 38.54
Std Dev 5.663
Gaus mean : 39.2904
Gaus Width: 3.8402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

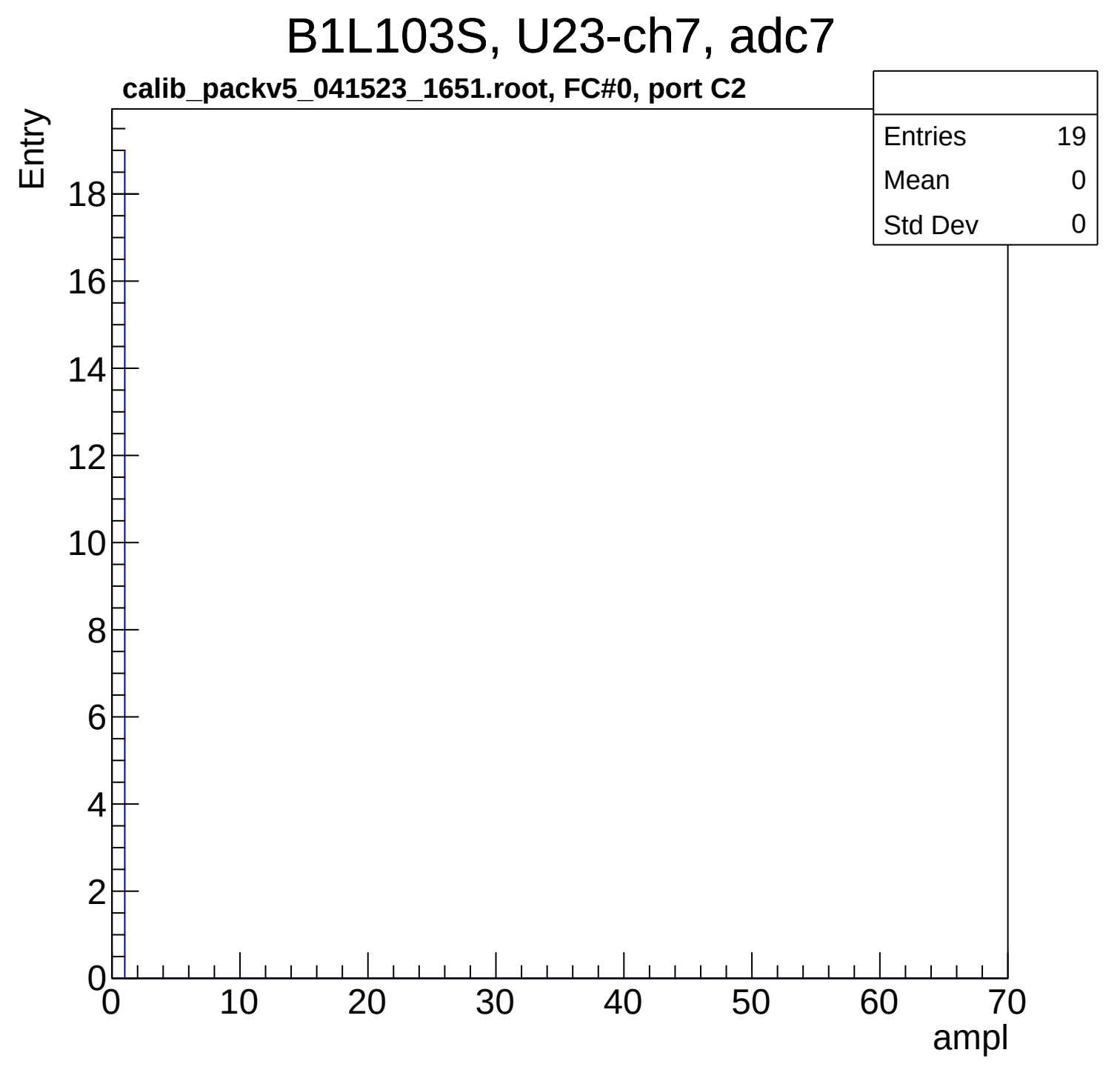

B1L103S, U23-ch7, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 38.58
Std Dev 17.19
Gaus mean : 46.5268
Gaus Width: 3.5155
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

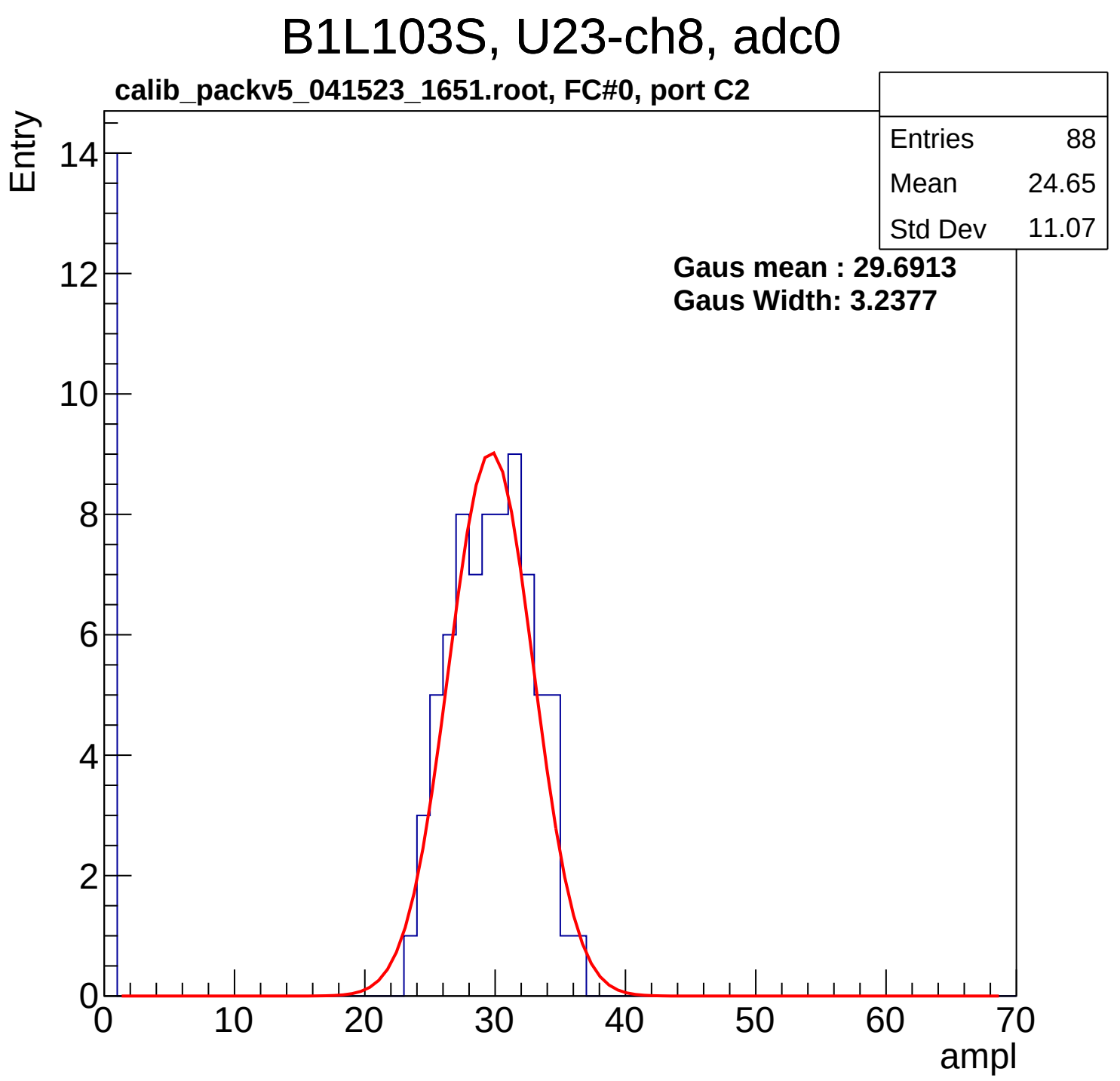

B1L103S, U23-ch7, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 50.53
Std Dev 9.993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

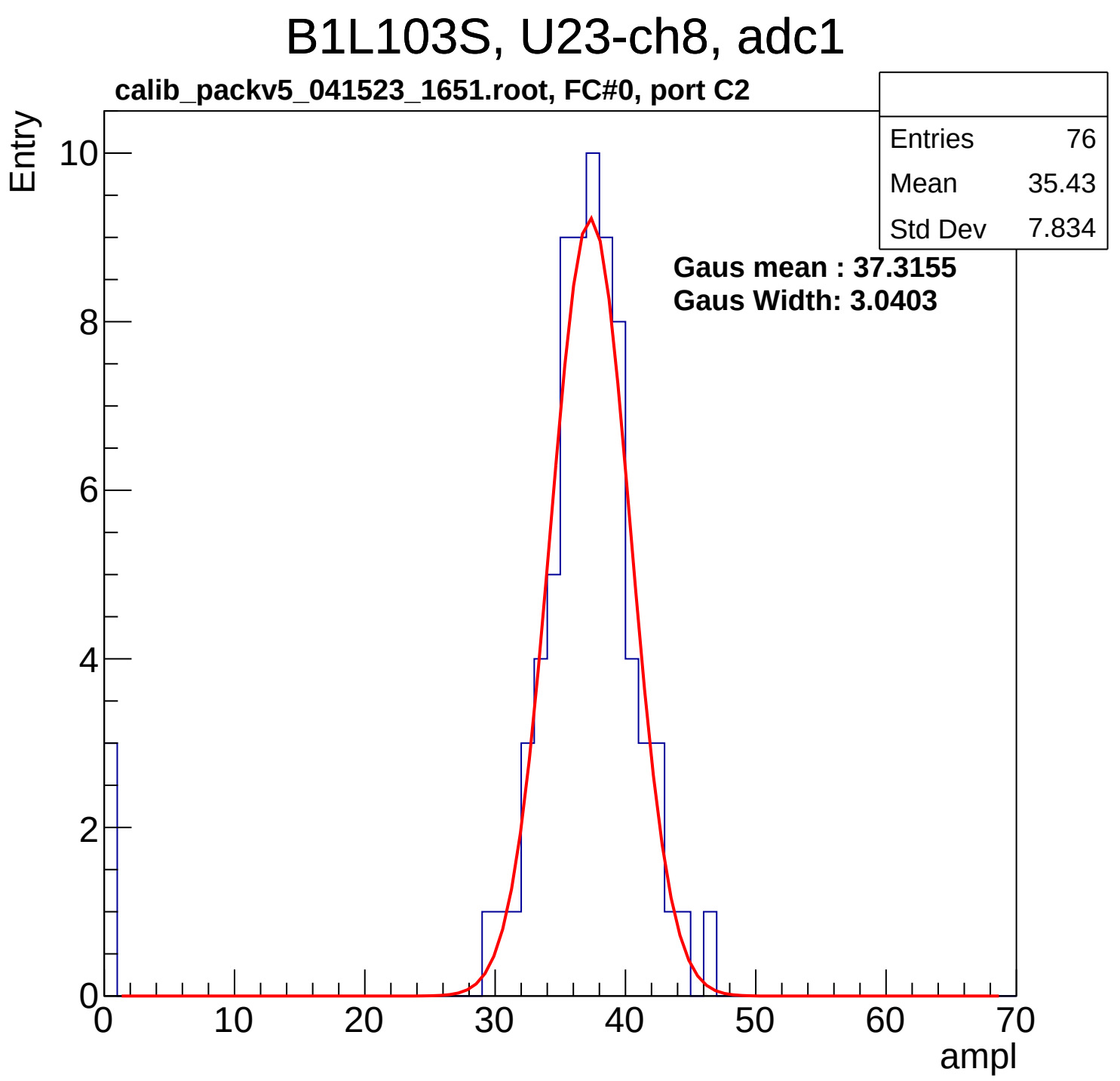

B1L103S, U23-ch7, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 58.11
Std Dev 2.924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

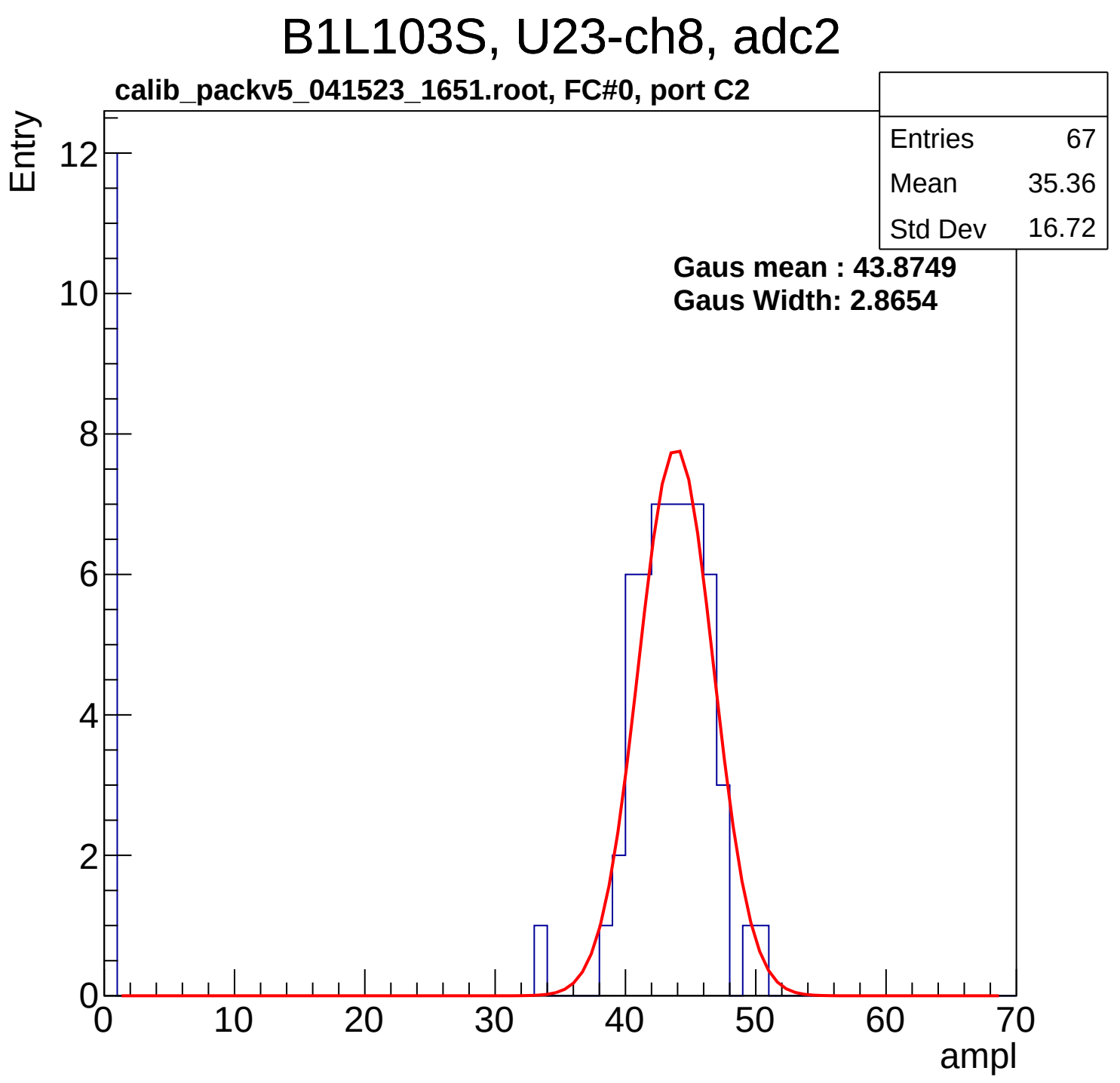

B1L103S, U23-ch7, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 24
Mean 58.33
Std Dev 12.33
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

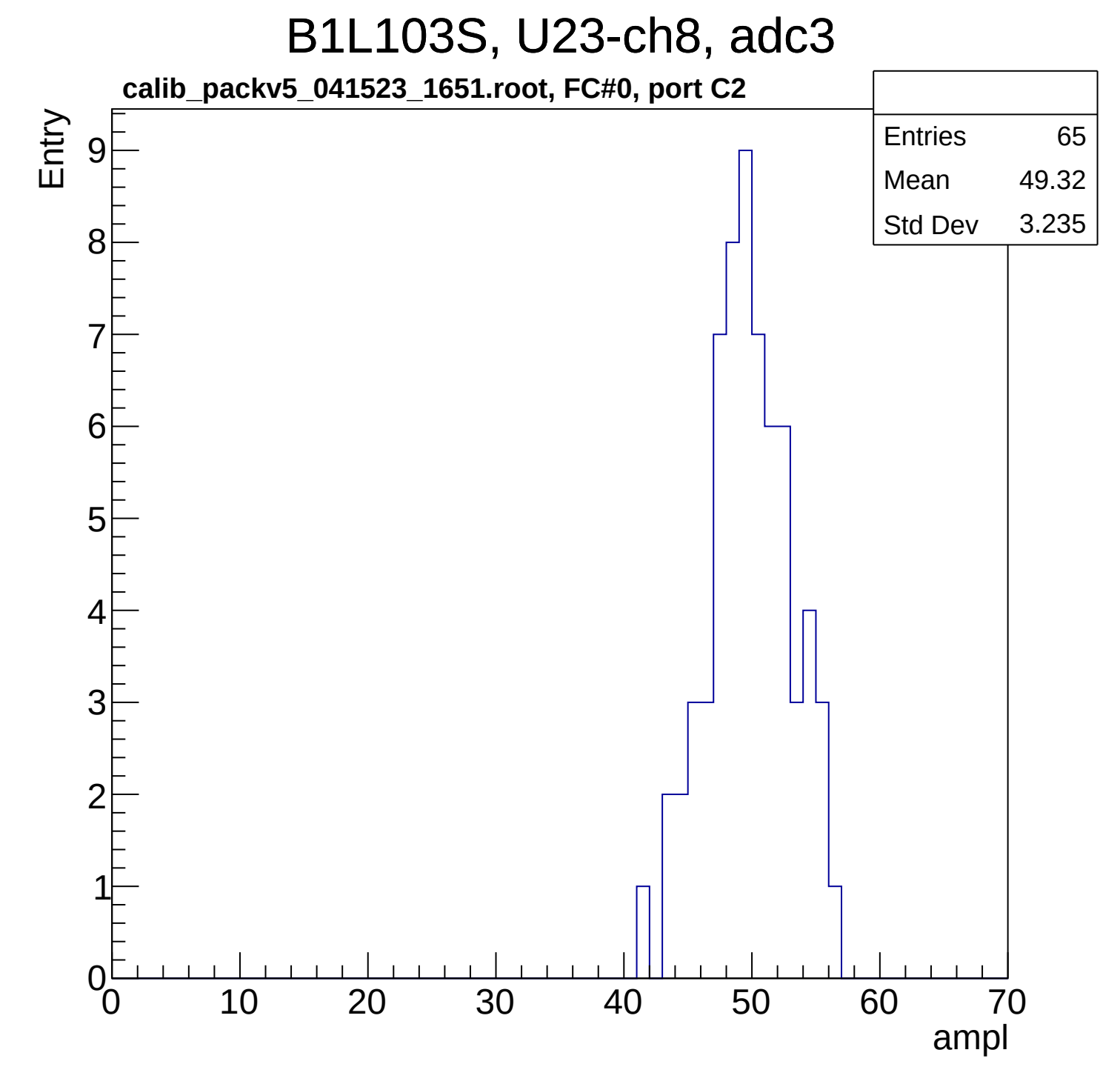

B1L103S, U23-ch7, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

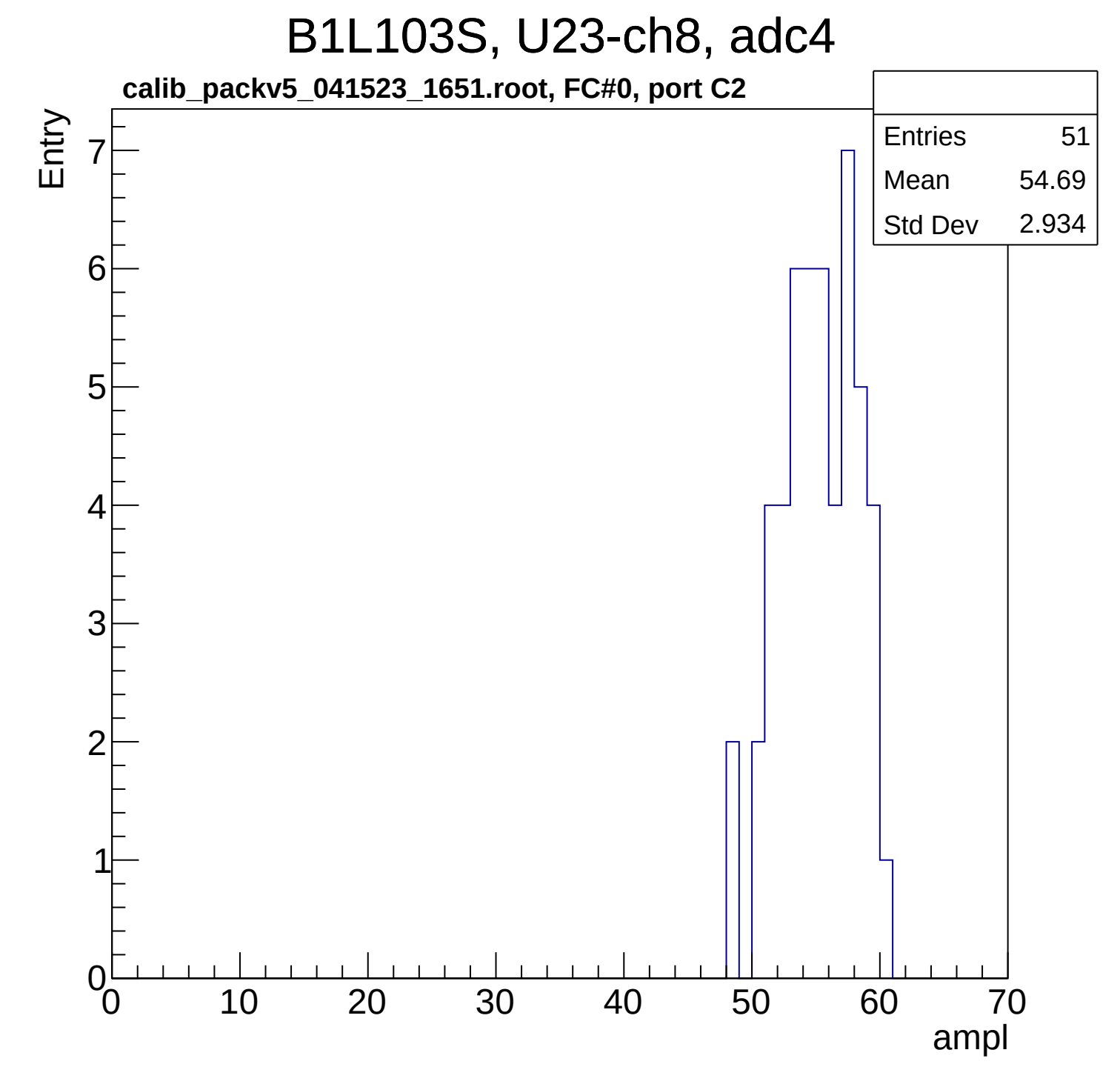

B1L103S, U23-ch7, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

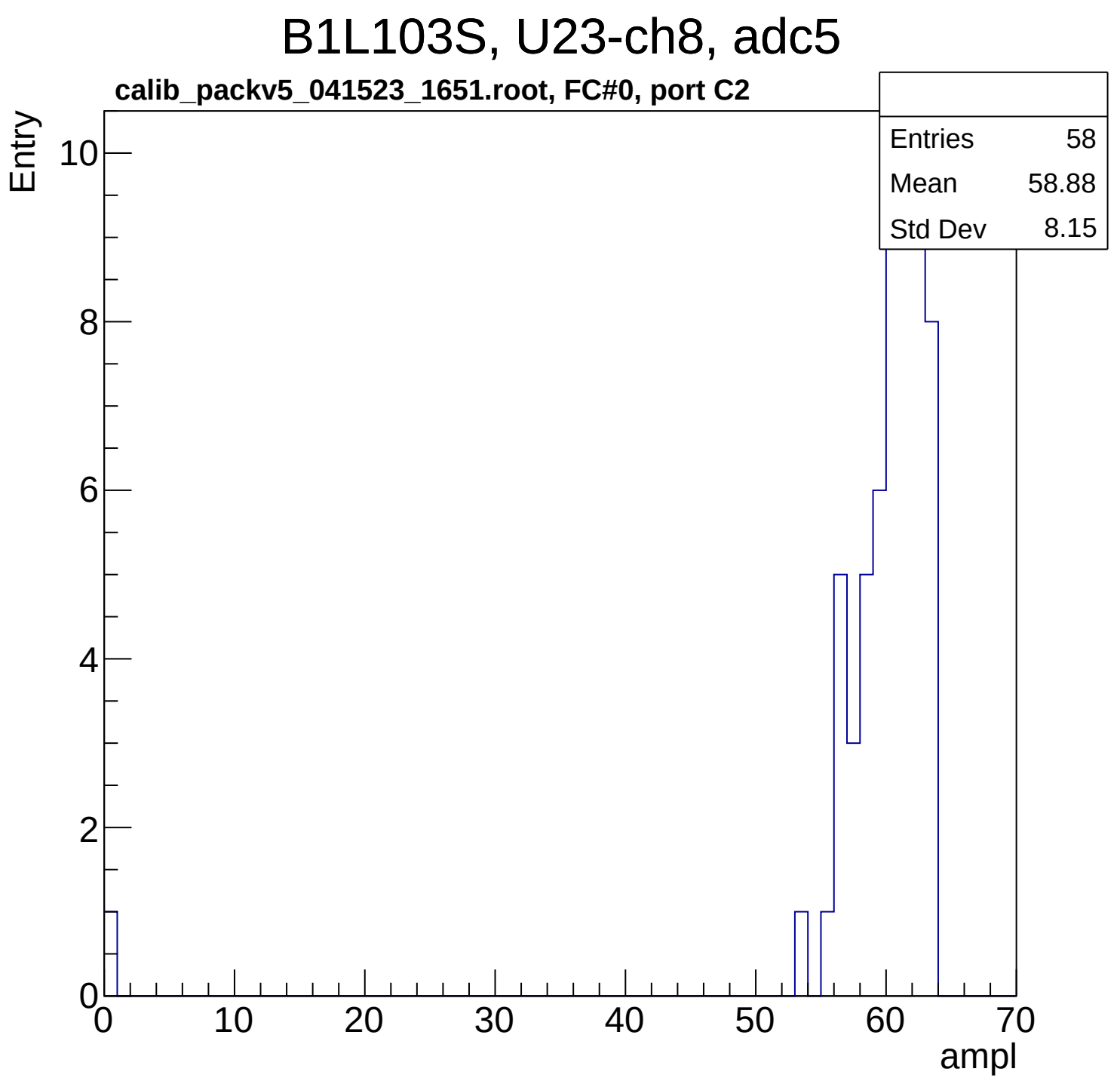

B1L103S, U23-ch8, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 88
Mean 24.65
Std Dev 11.07
Gaus mean : 29.6913
Gaus Width: 3.2377
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

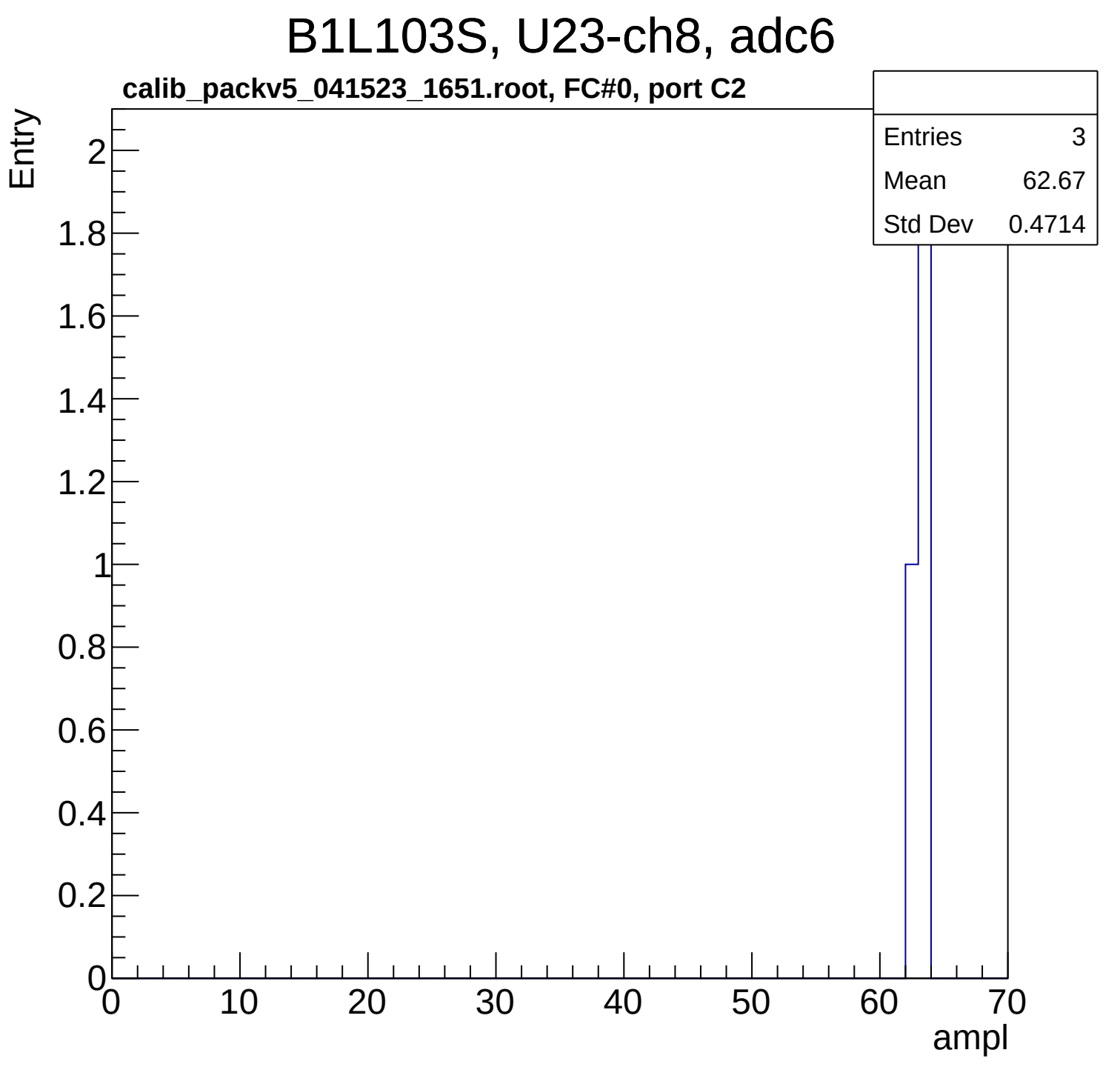

B1L103S, U23-ch8, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 35.43
Std Dev 7.834
Gaus mean : 37.3155
Gaus Width: 3.0403
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

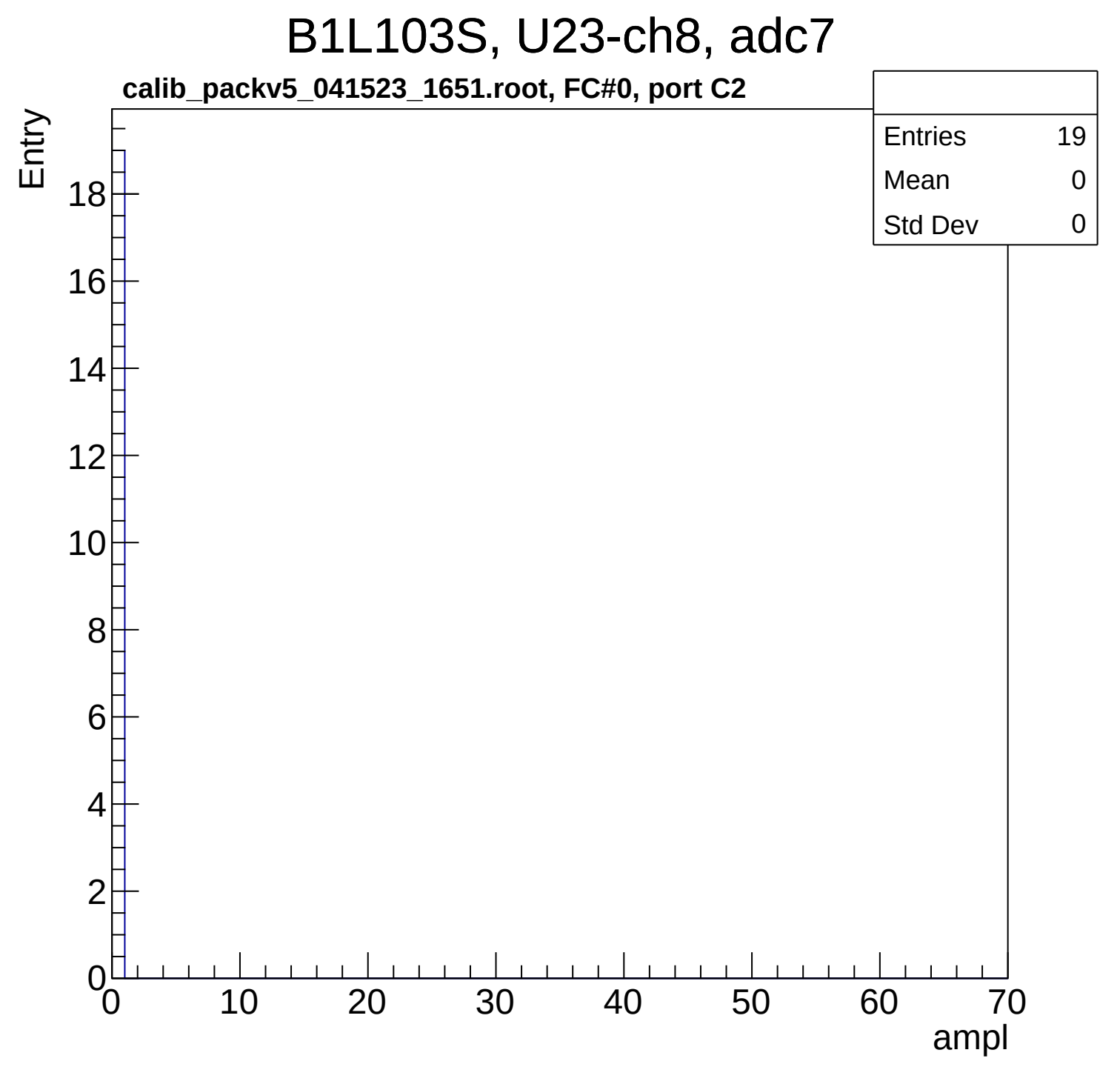

B1L103S, U23-ch8, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 35.36
Std Dev 16.72
Gaus mean : 43.8749
Gaus Width: 2.8654
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

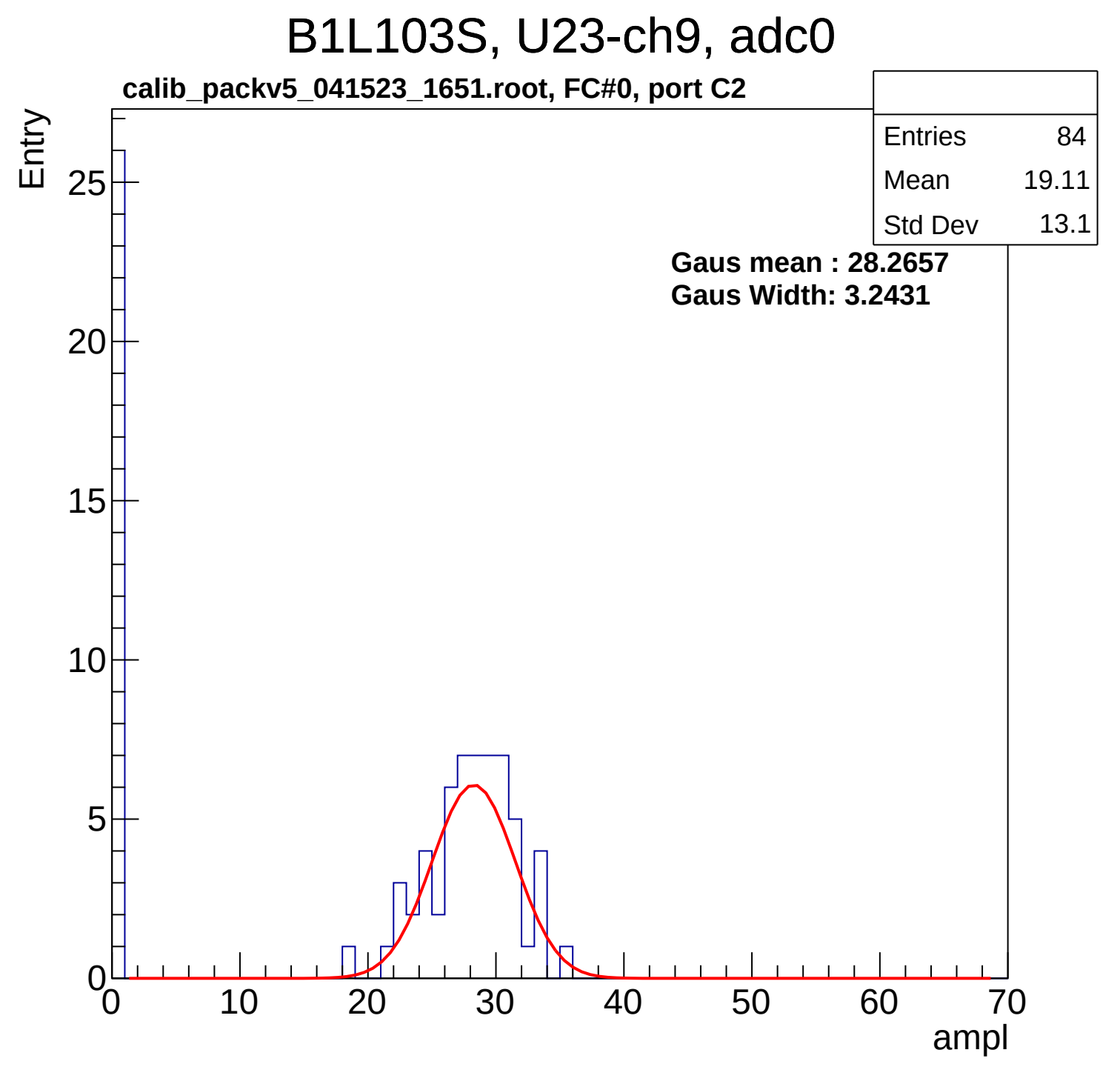

B1L103S, U23-ch8, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 49.32
Std Dev 3.235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

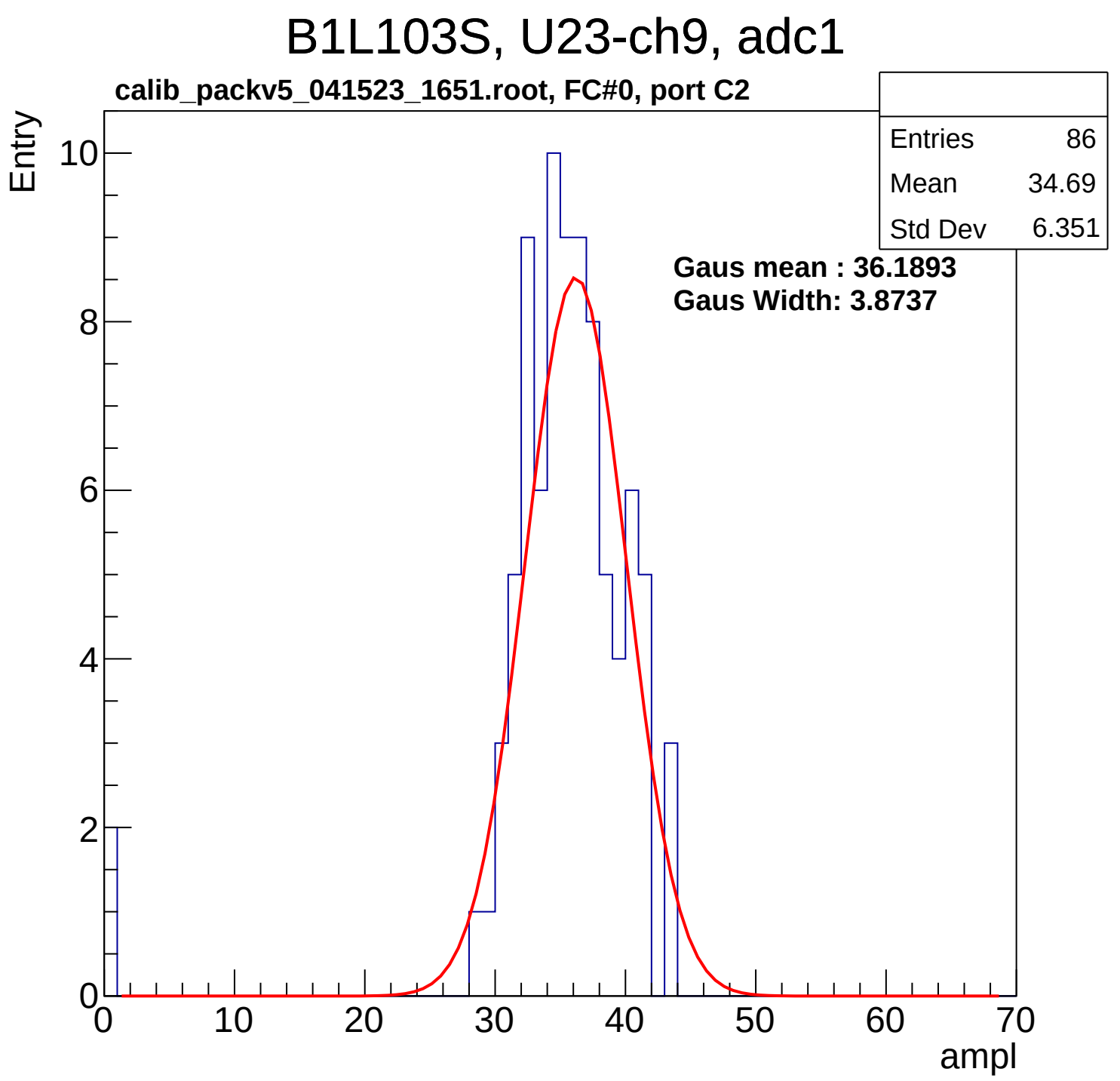

B1L103S, U23-ch8, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 54.69
Std Dev 2.934
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

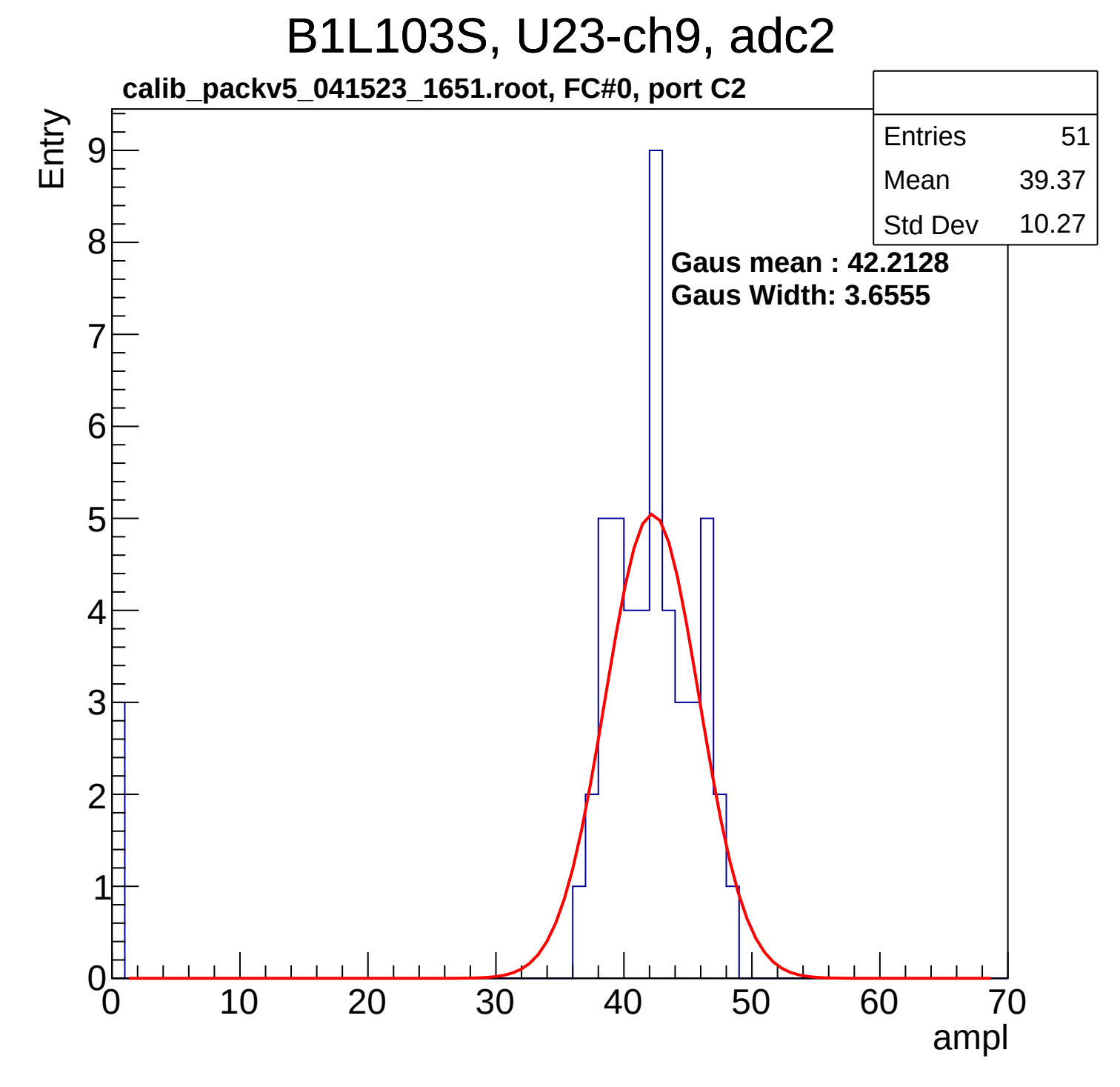

B1L103S, U23-ch8, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 58.88
Std Dev 8.15
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

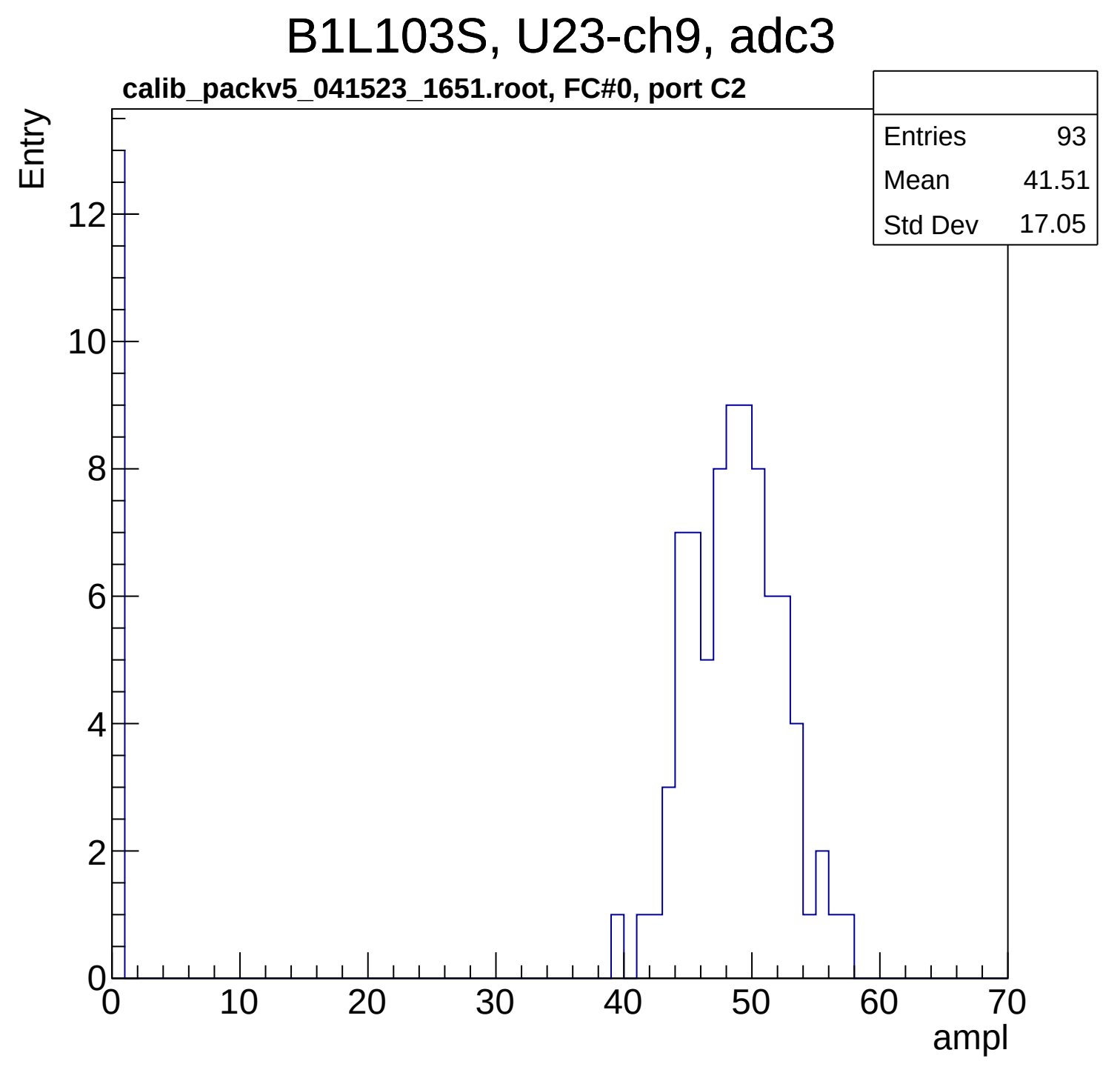

B1L103S, U23-ch8, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

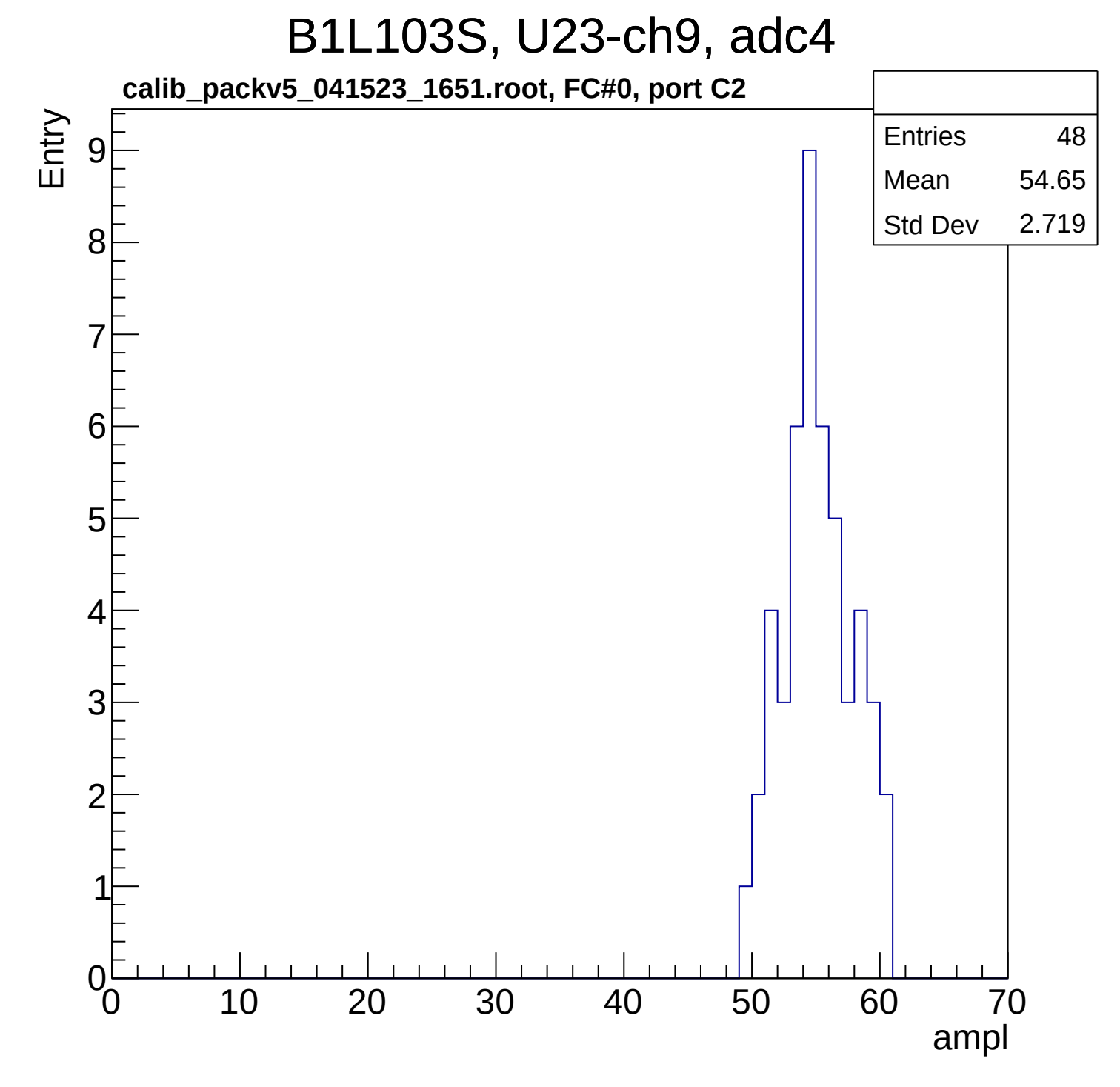

B1L103S, U23-ch8, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

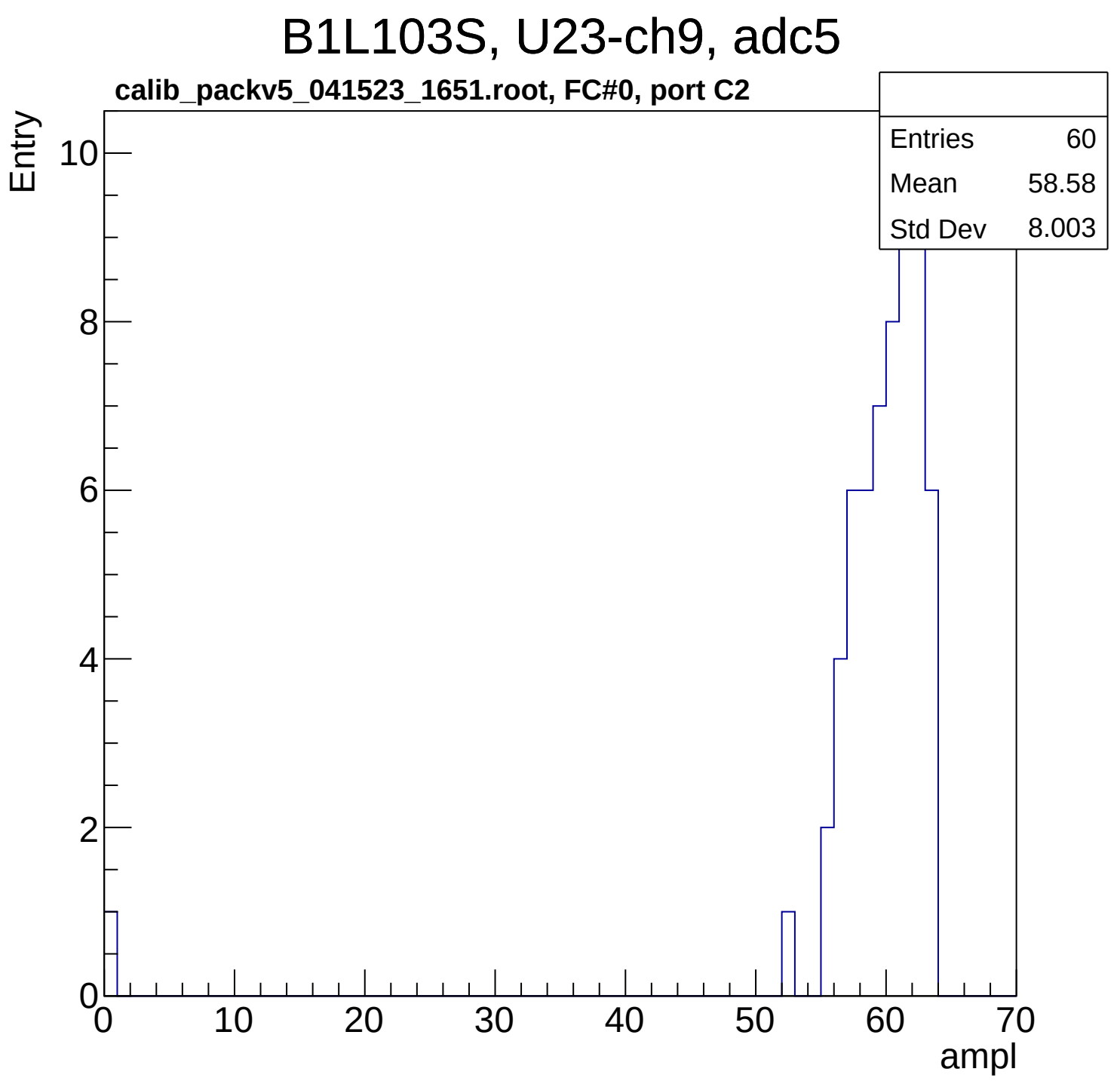

B1L103S, U23-ch9, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 84
Mean 19.11
Std Dev 13.1
Gaus mean : 28.2657
Gaus Width: 3.2431
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

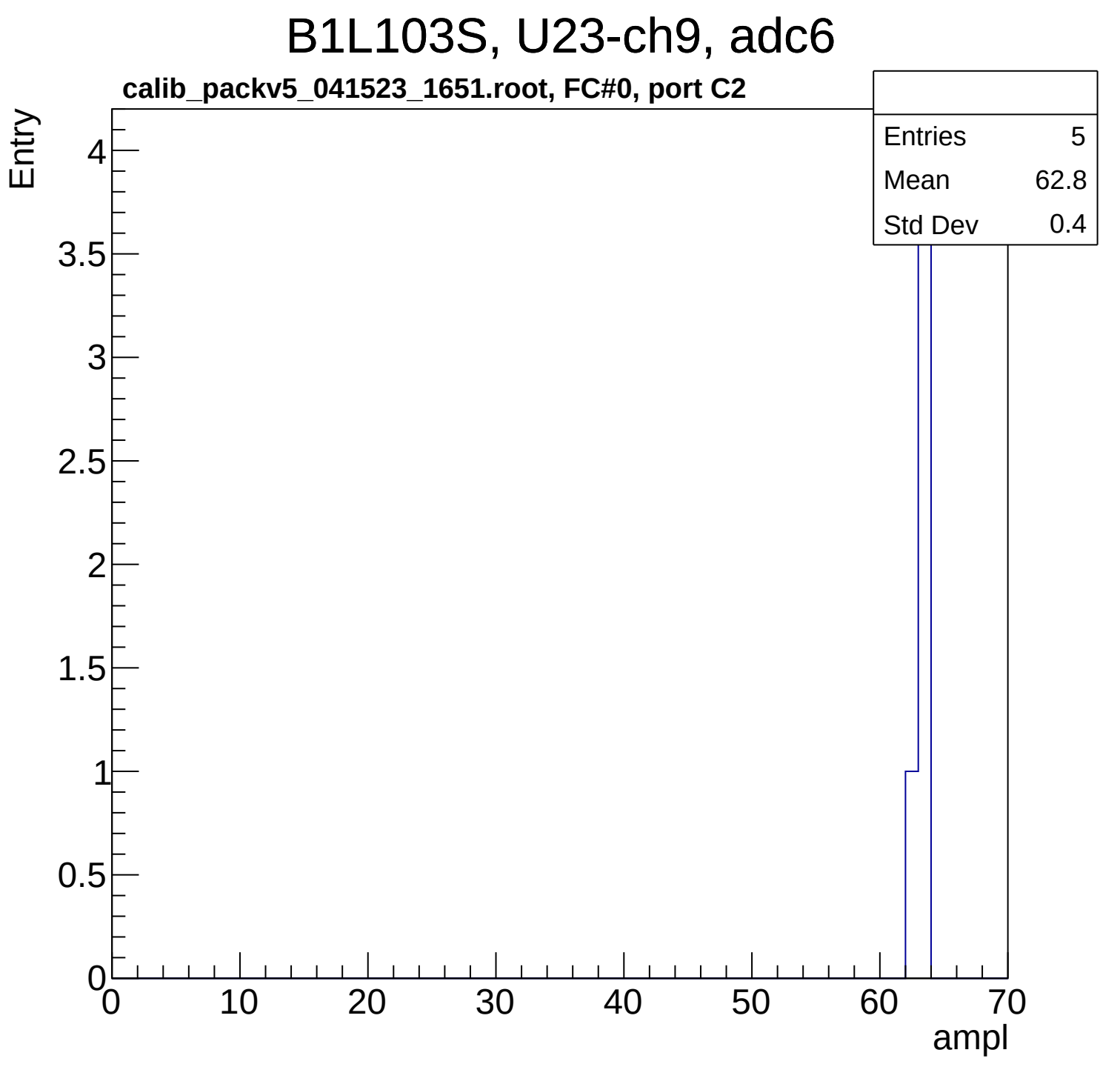

B1L103S, U23-ch9, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 34.69
Std Dev 6.351
Gaus mean : 36.1893
Gaus Width: 3.8737
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

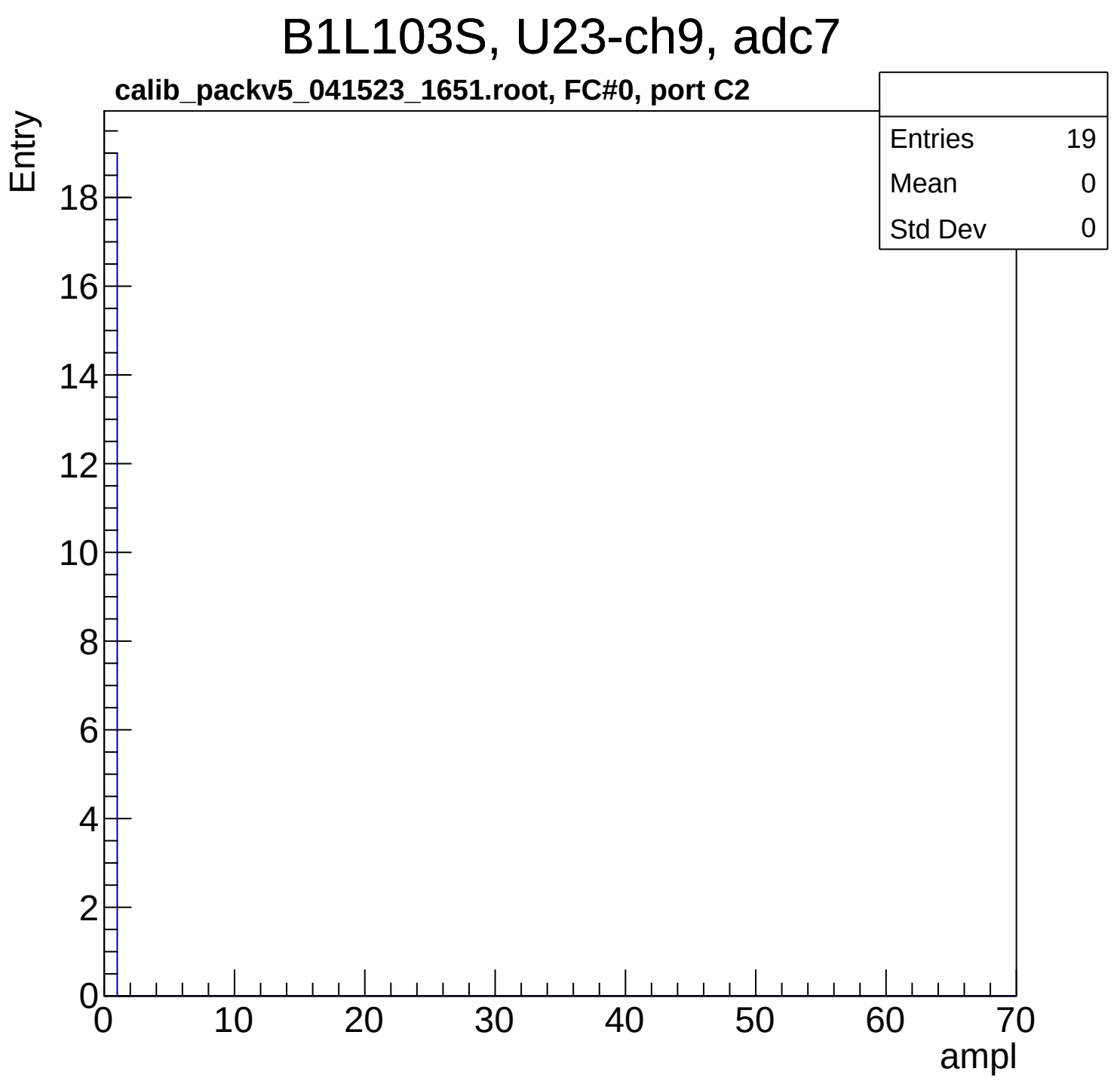

B1L103S, U23-ch9, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 39.37
Std Dev 10.27
Gaus mean : 42.2128
Gaus Width: 3.6555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

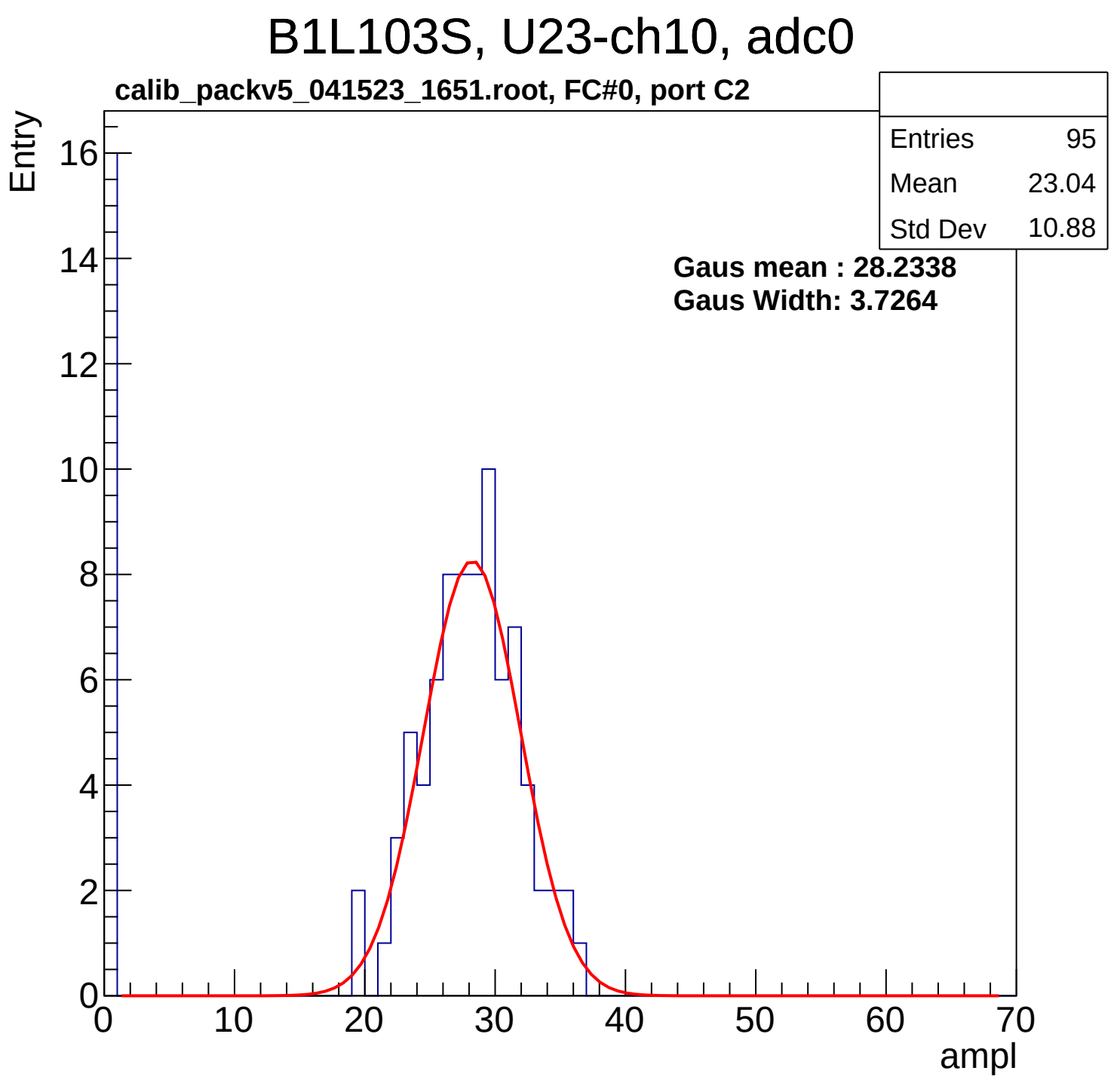

B1L103S, U23-ch9, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 93
Mean 41.51
Std Dev 17.05
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

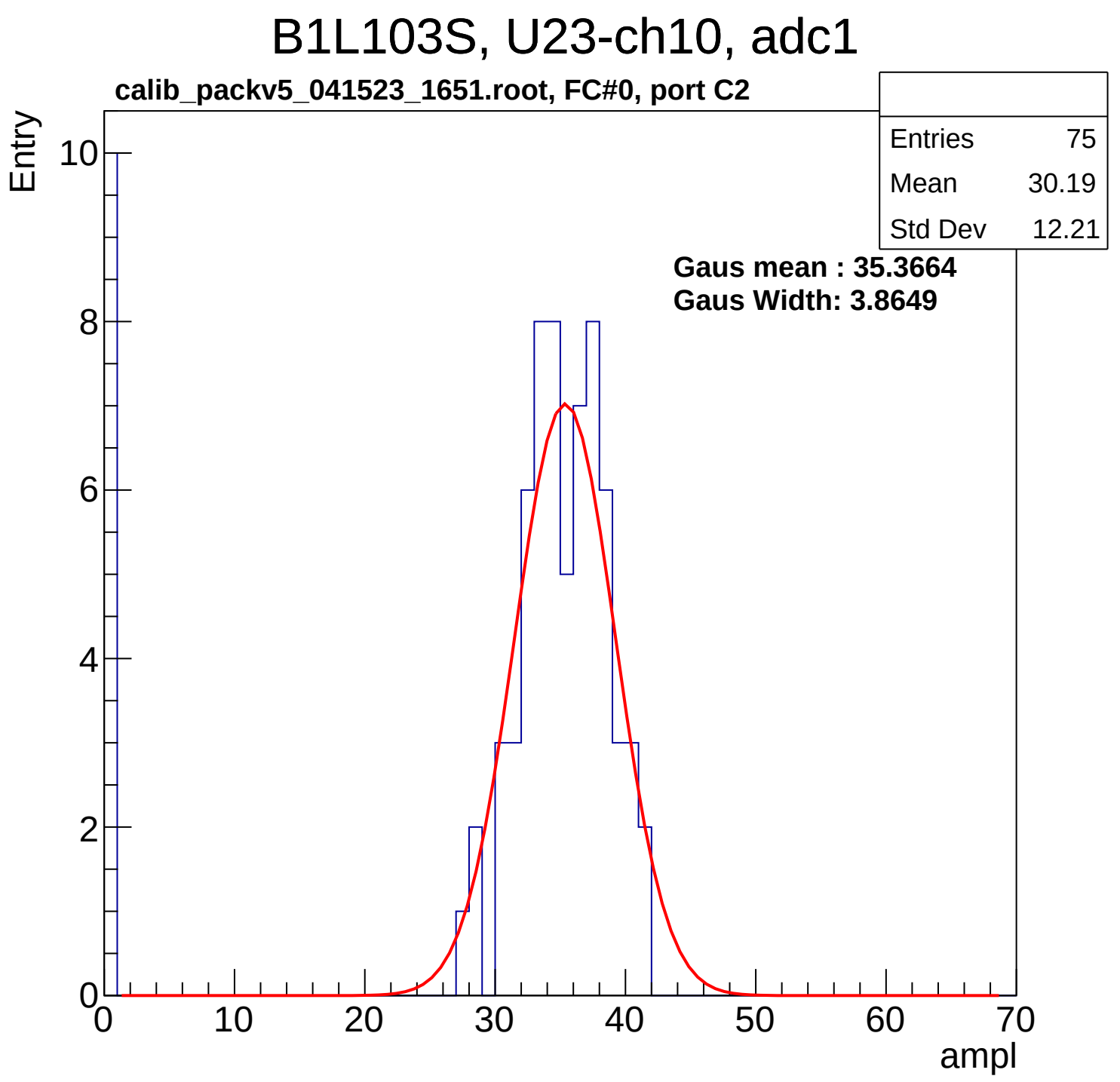

B1L103S, U23-ch9, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 54.65
Std Dev 2.719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

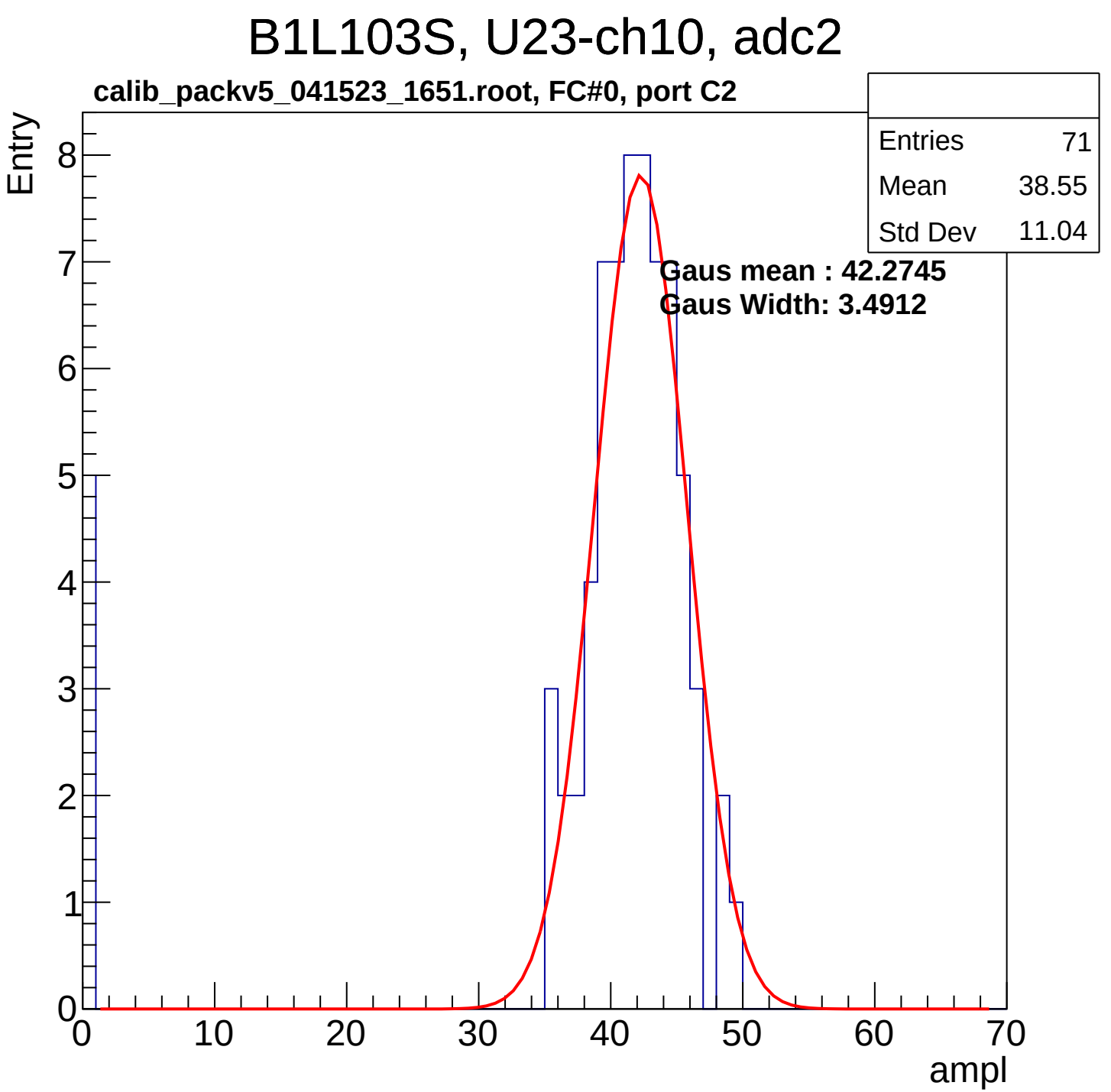

B1L103S, U23-ch9, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 58.58
Std Dev 8.003
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

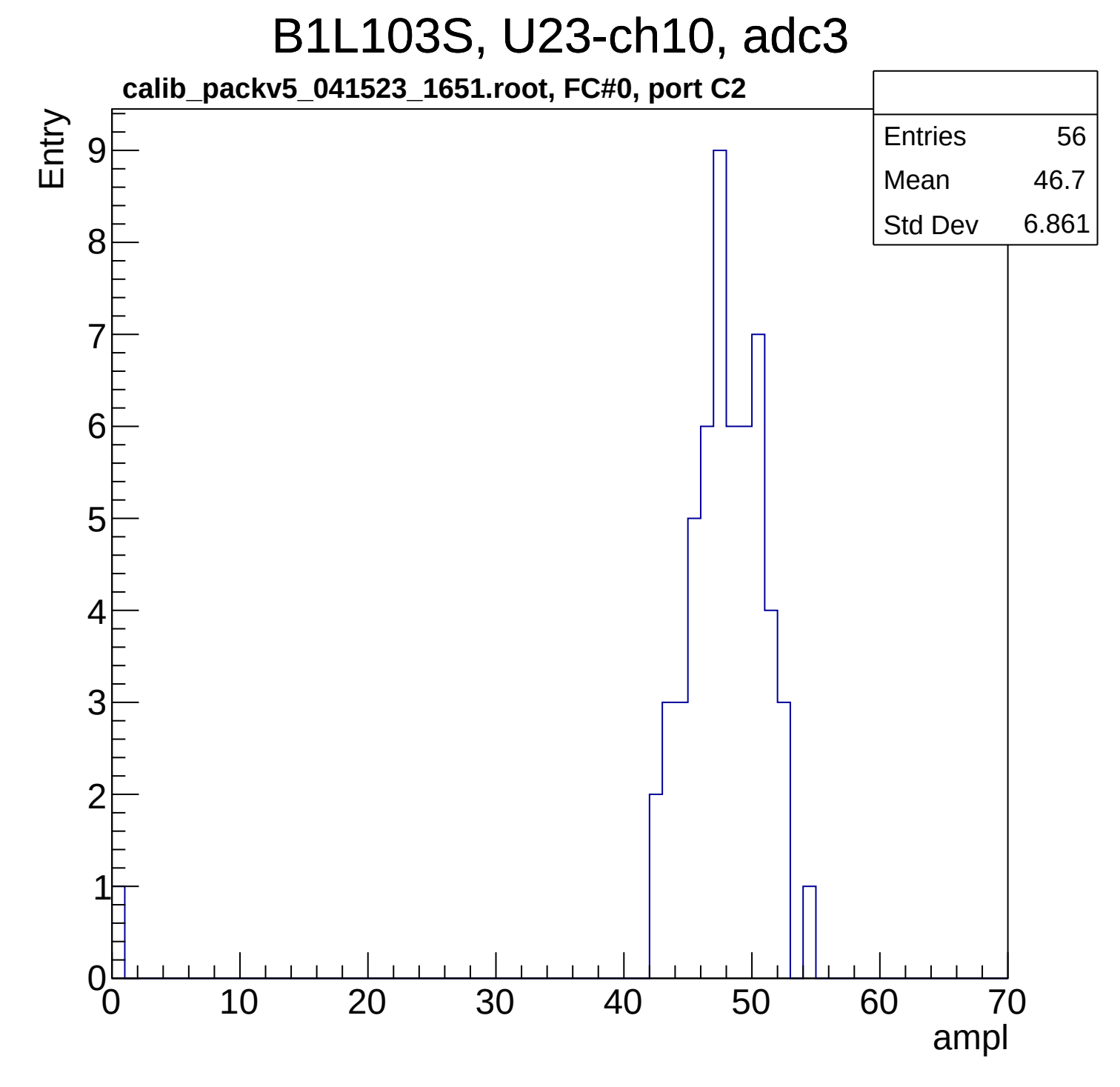

B1L103S, U23-ch9, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

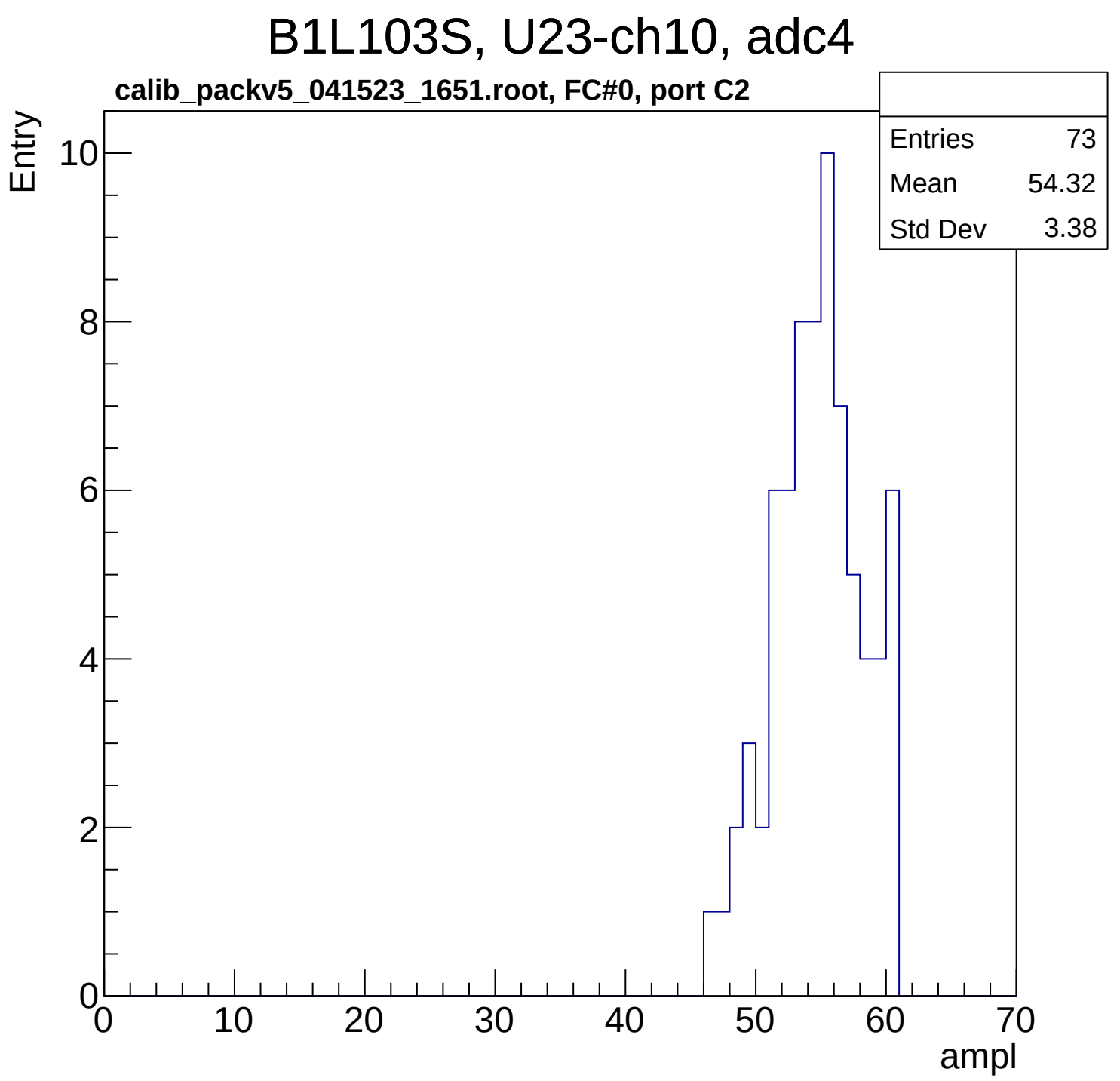

B1L103S, U23-ch9, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

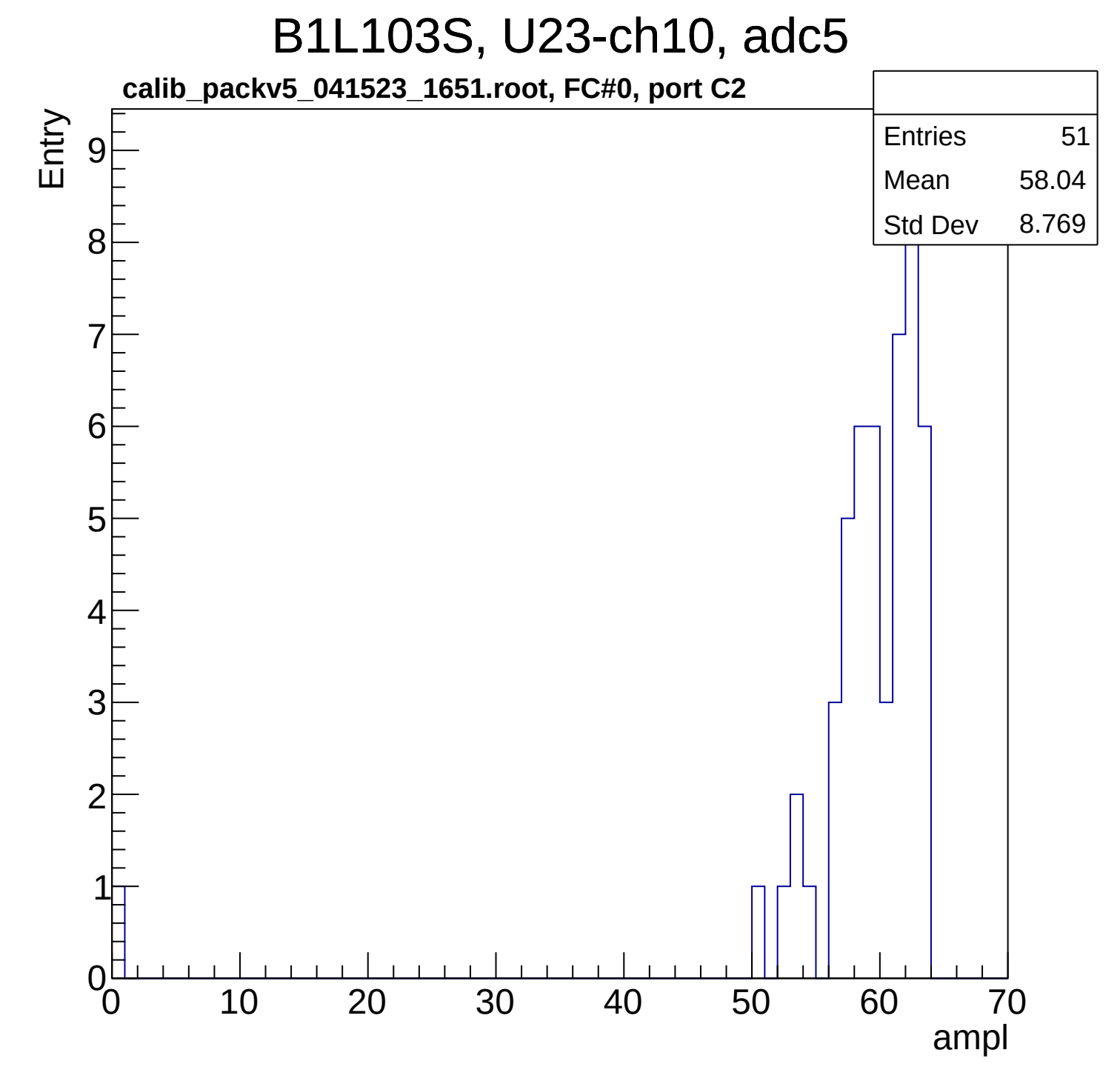

B1L103S, U23-ch10, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 95
Mean 23.04
Std Dev 10.88
Gaus mean : 28.2338
Gaus Width: 3.7264
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

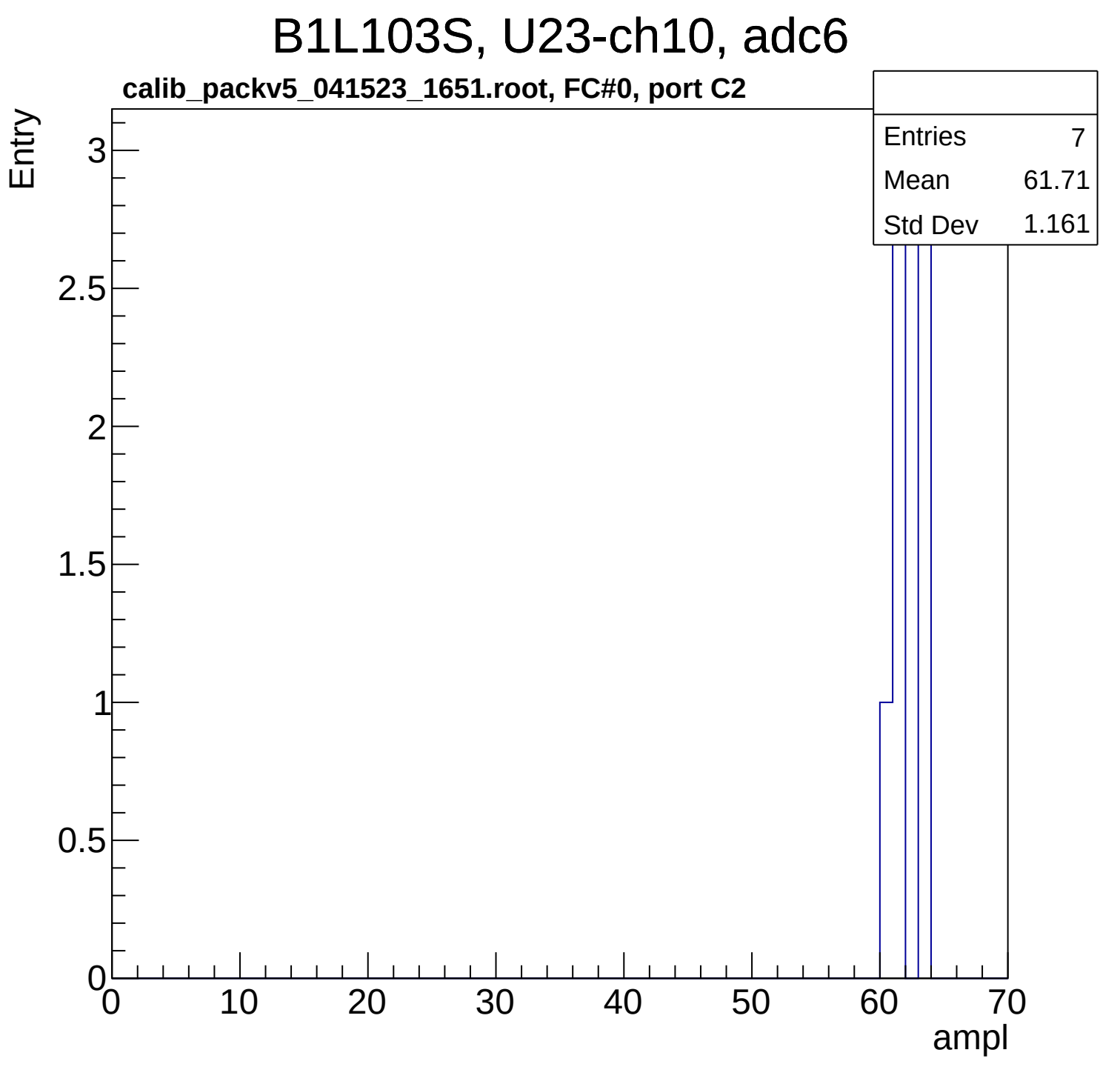

B1L103S, U23-ch10, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 30.19
Std Dev 12.21
Gaus mean : 35.3664
Gaus Width: 3.8649
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

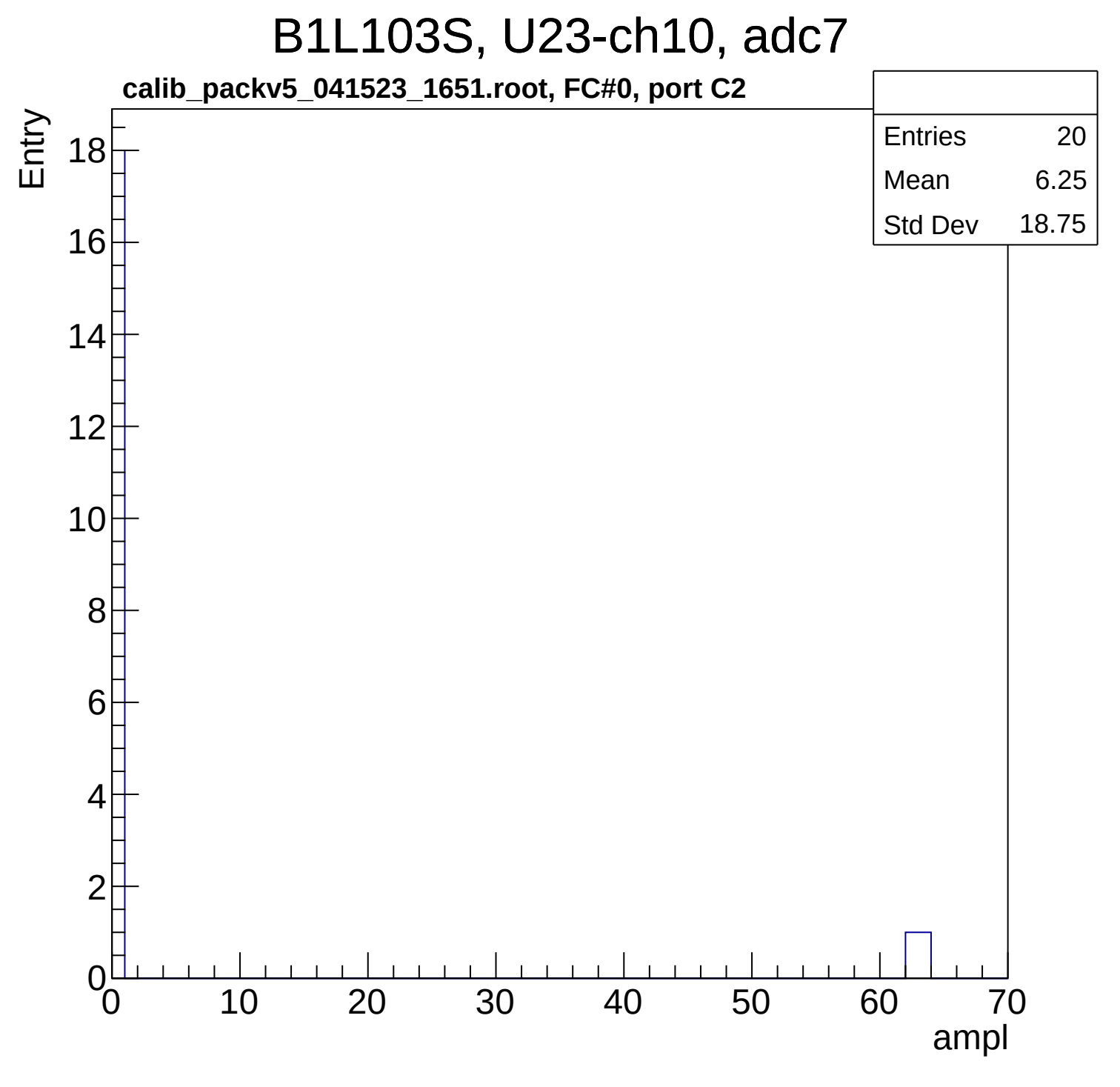

B1L103S, U23-ch10, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 38.55
Std Dev 11.04
Gaus mean : 42.2745
Gaus Width: 3.4912
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

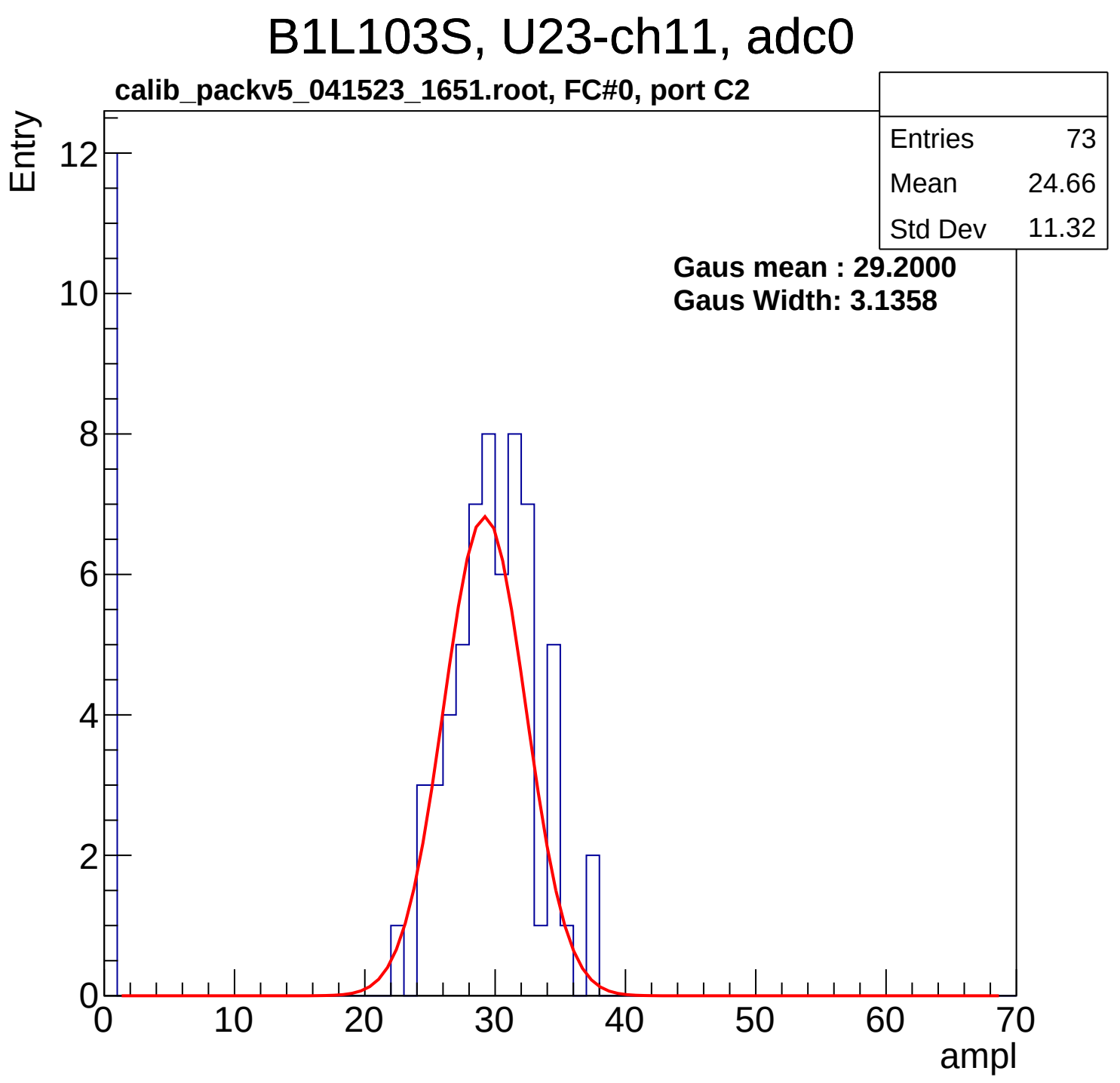

B1L103S, U23-ch10, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 46.7
Std Dev 6.861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

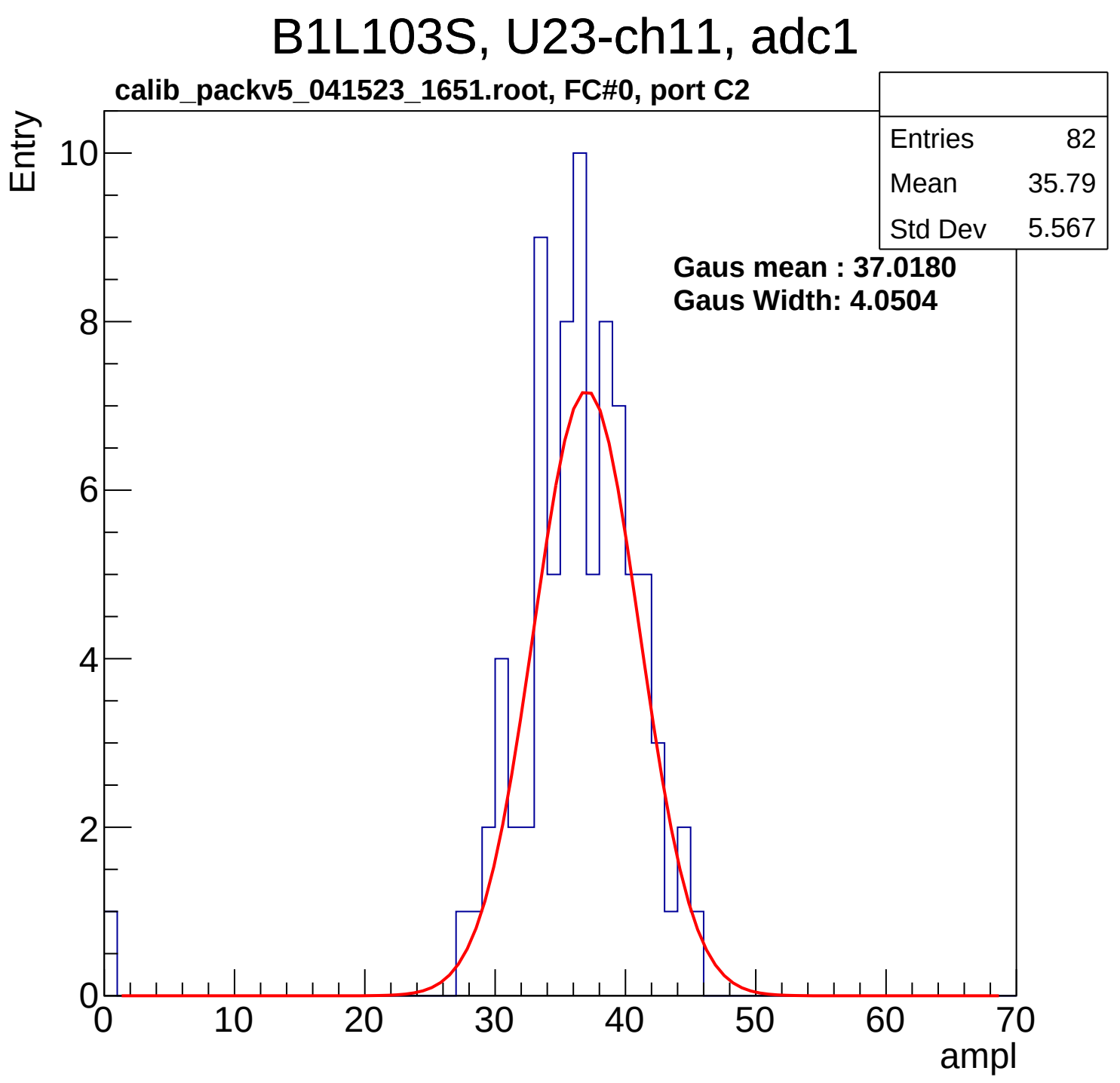

B1L103S, U23-ch10, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 54.32
Std Dev 3.38
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

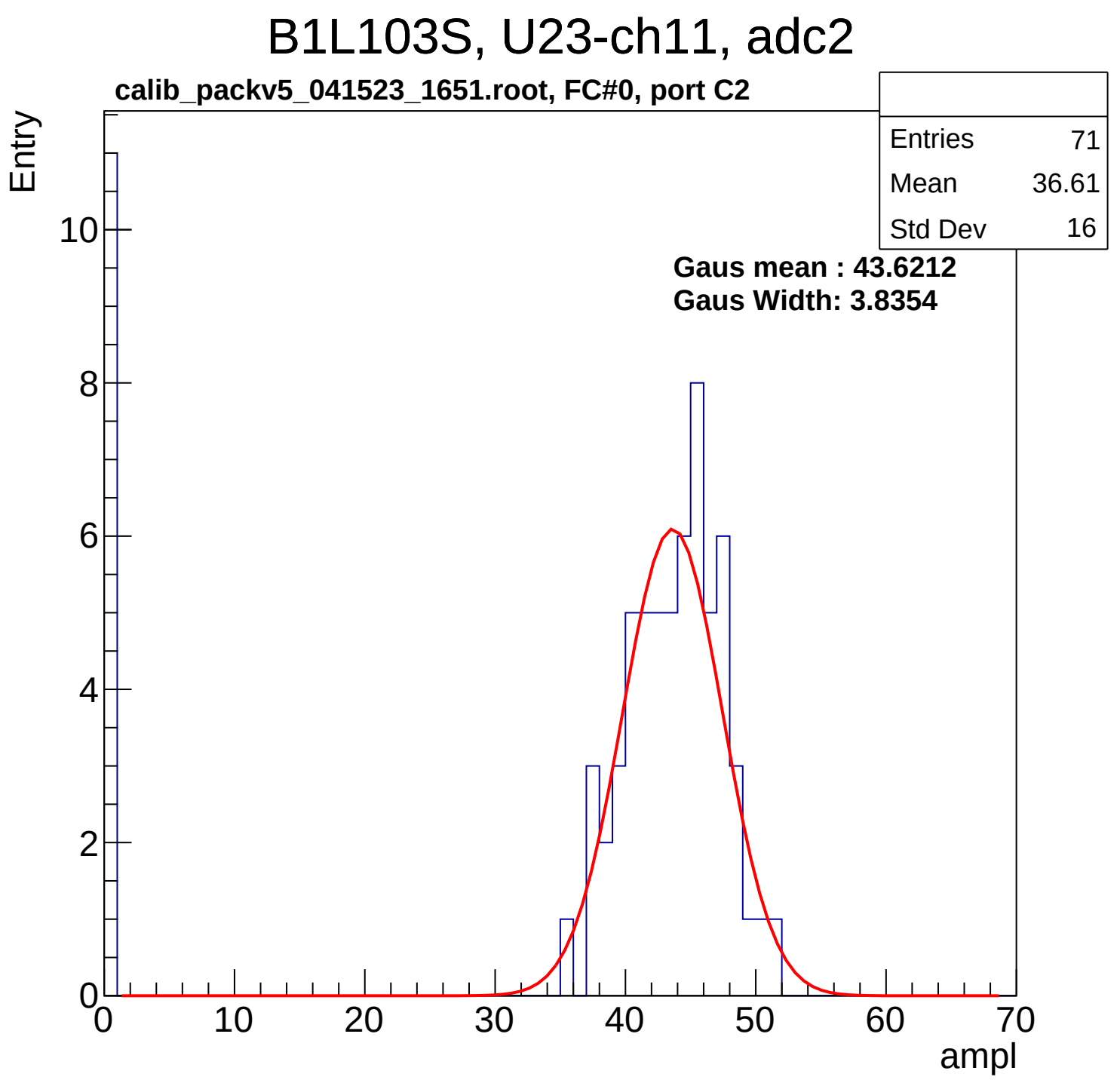

B1L103S, U23-ch10, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 58.04
Std Dev 8.769
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

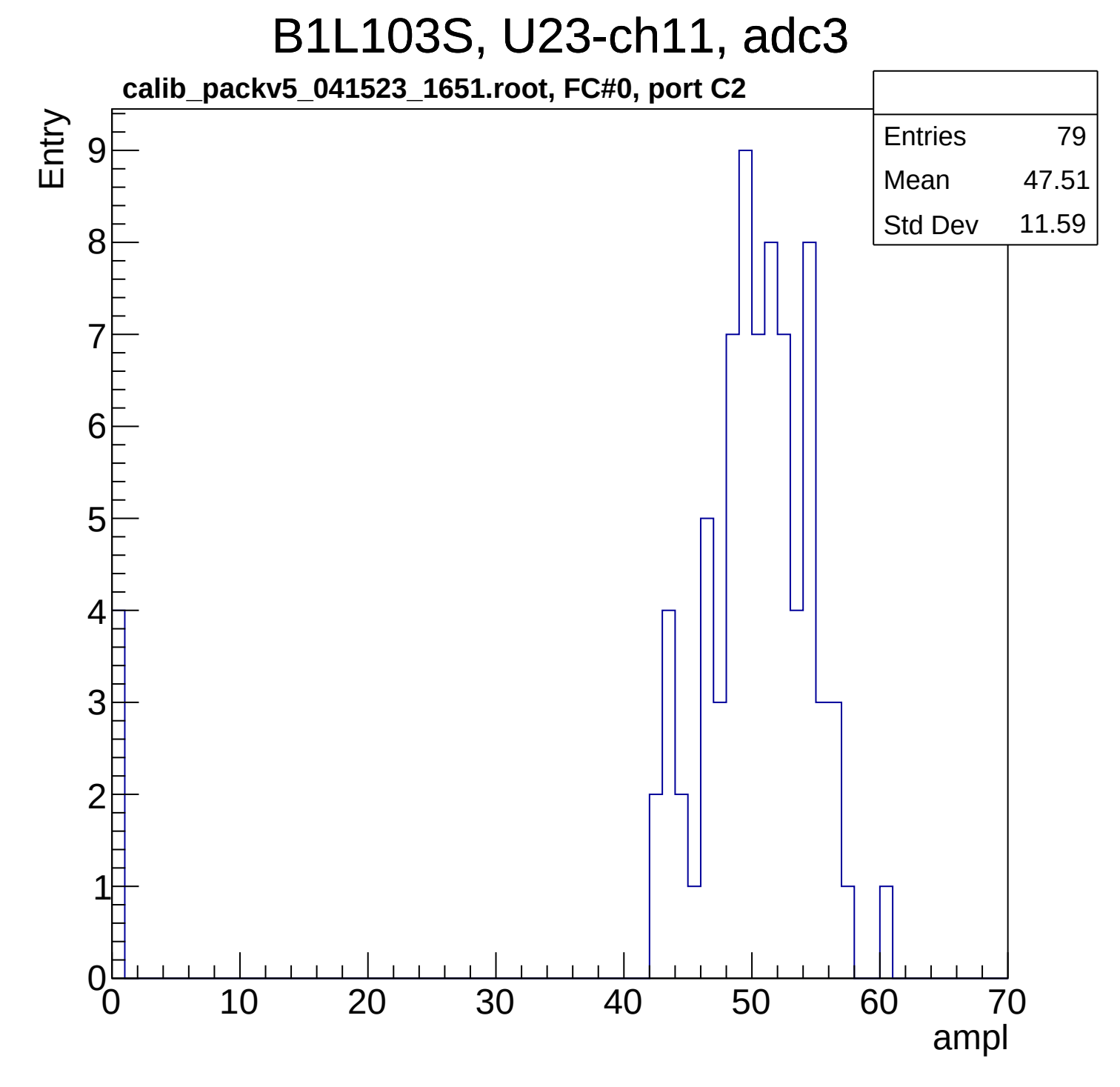

B1L103S, U23-ch10, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 7
Mean 61.71
Std Dev 1.161
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

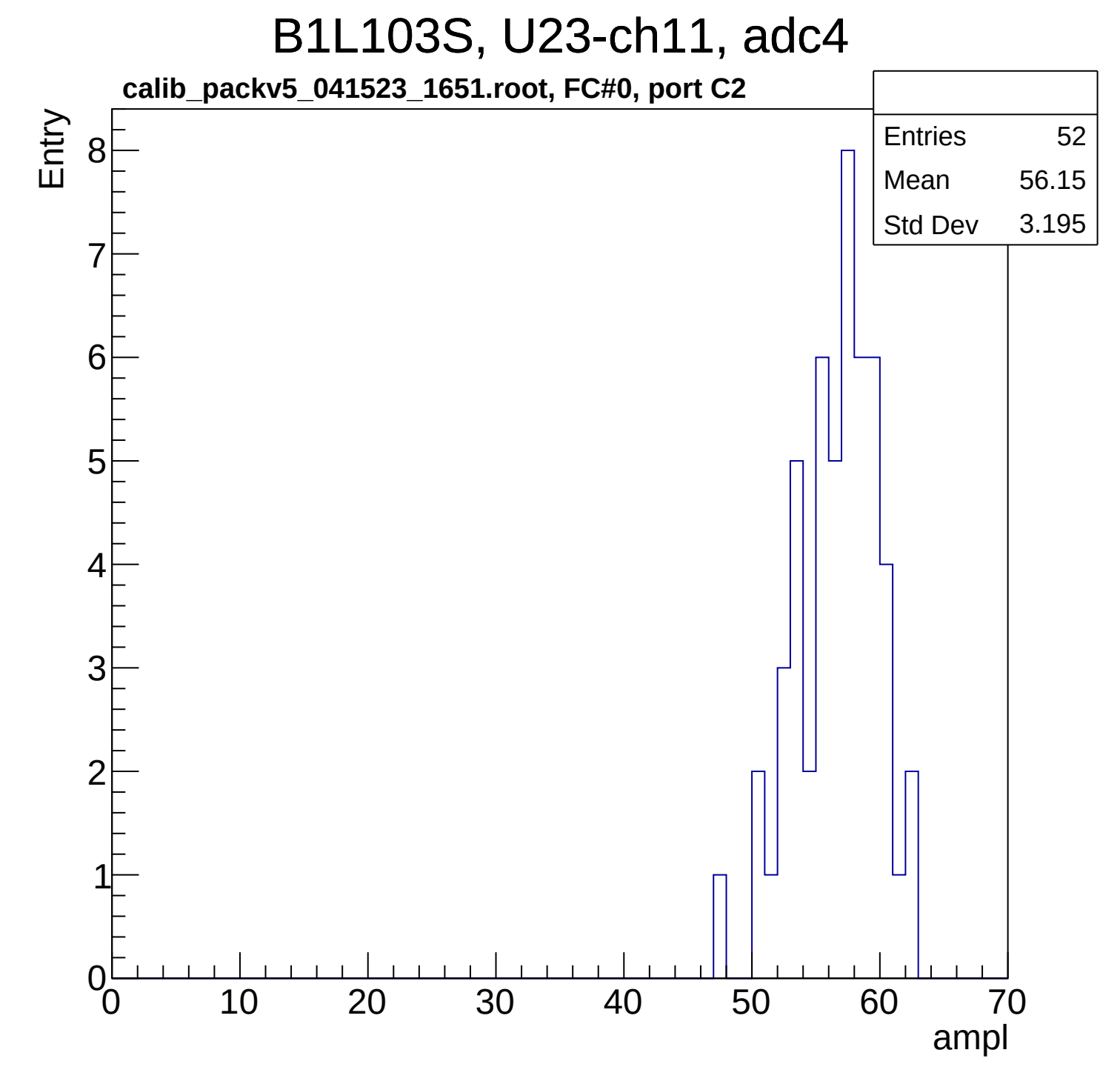

B1L103S, U23-ch10, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 6.25
Std Dev 18.75
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

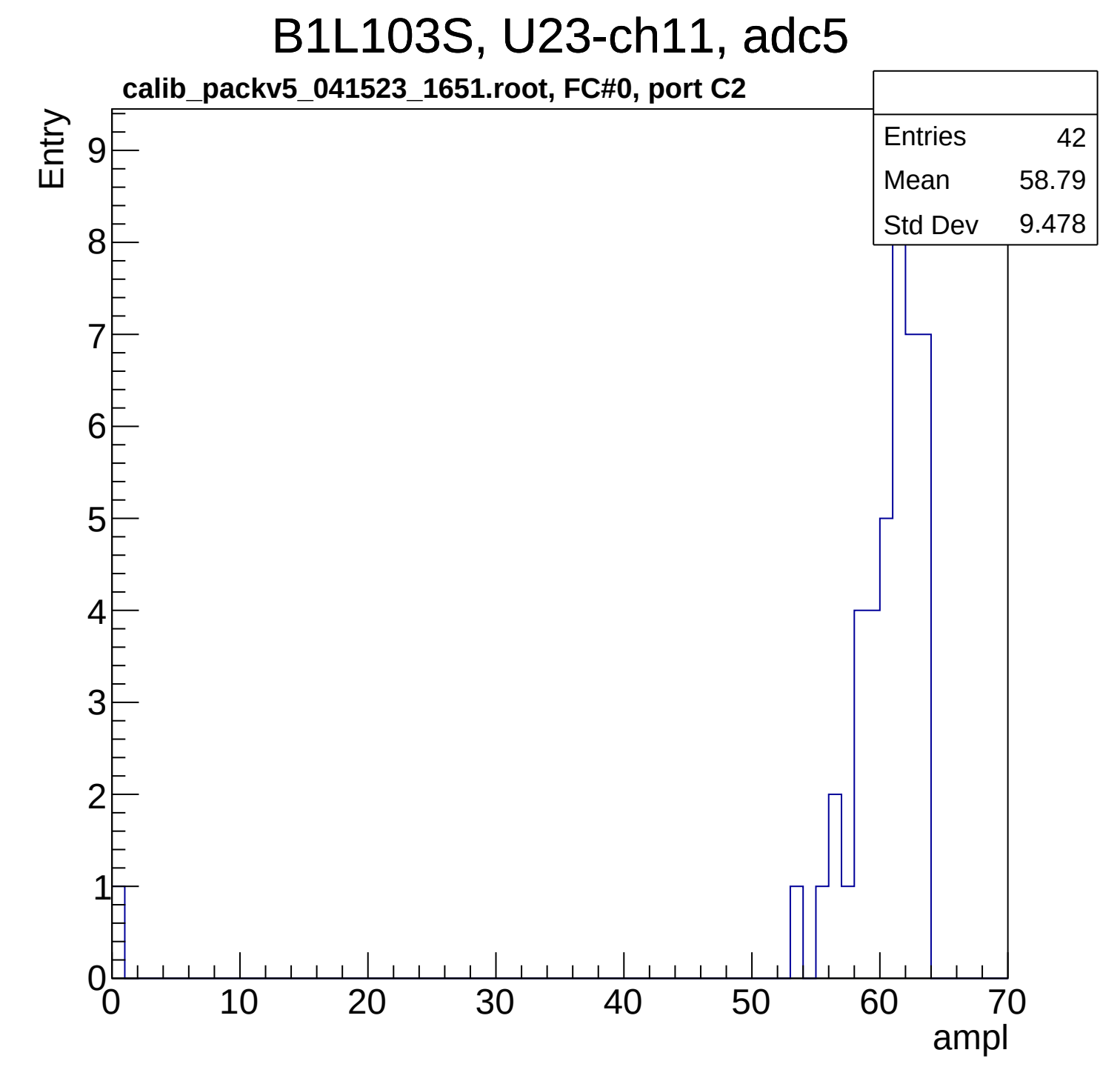

B1L103S, U23-ch11, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 24.66
Std Dev 11.32
Gaus mean : 29.2000
Gaus Width: 3.1358
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

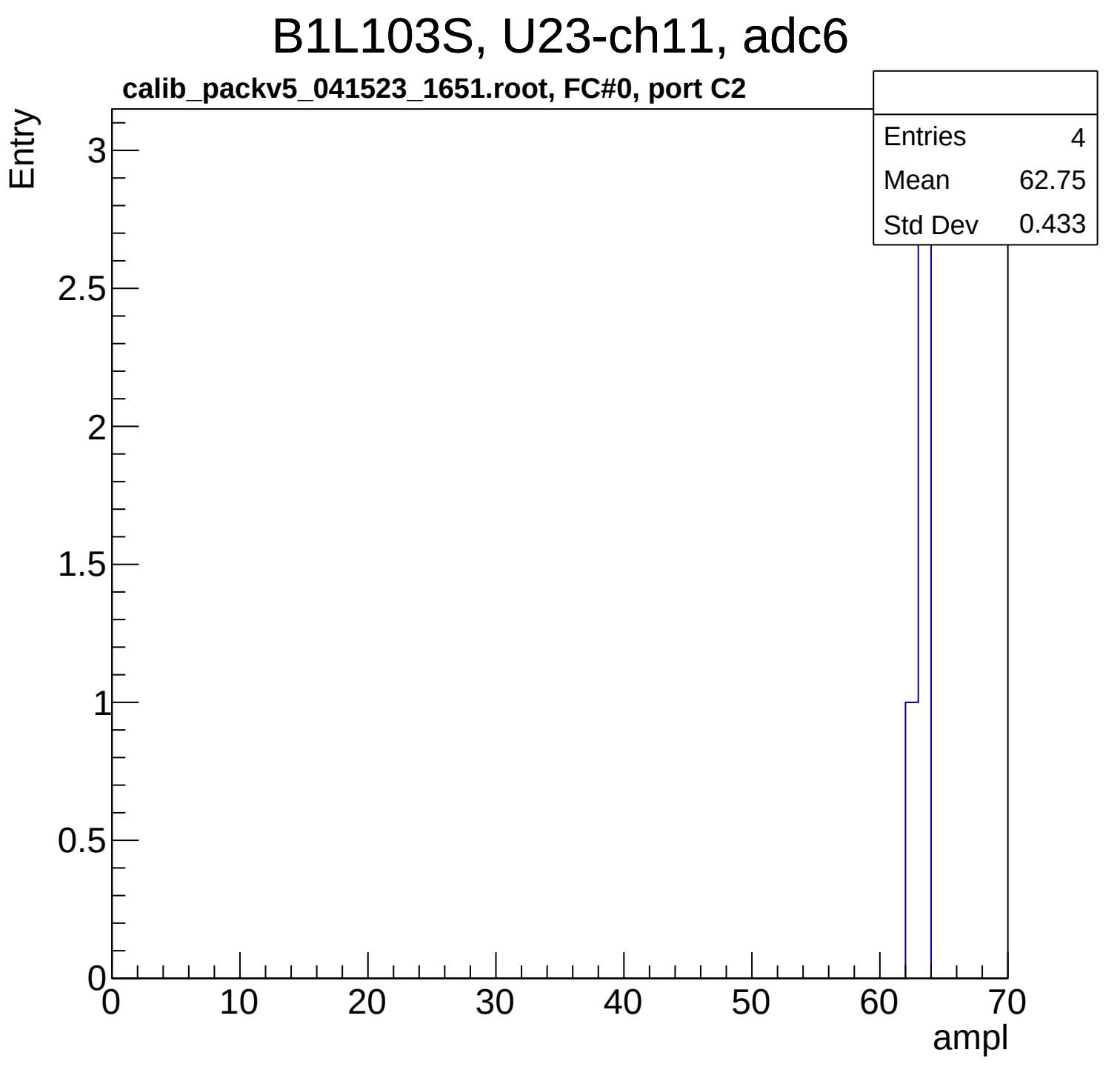

B1L103S, U23-ch11, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 35.79
Std Dev 5.567
Gaus mean : 37.0180
Gaus Width: 4.0504
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

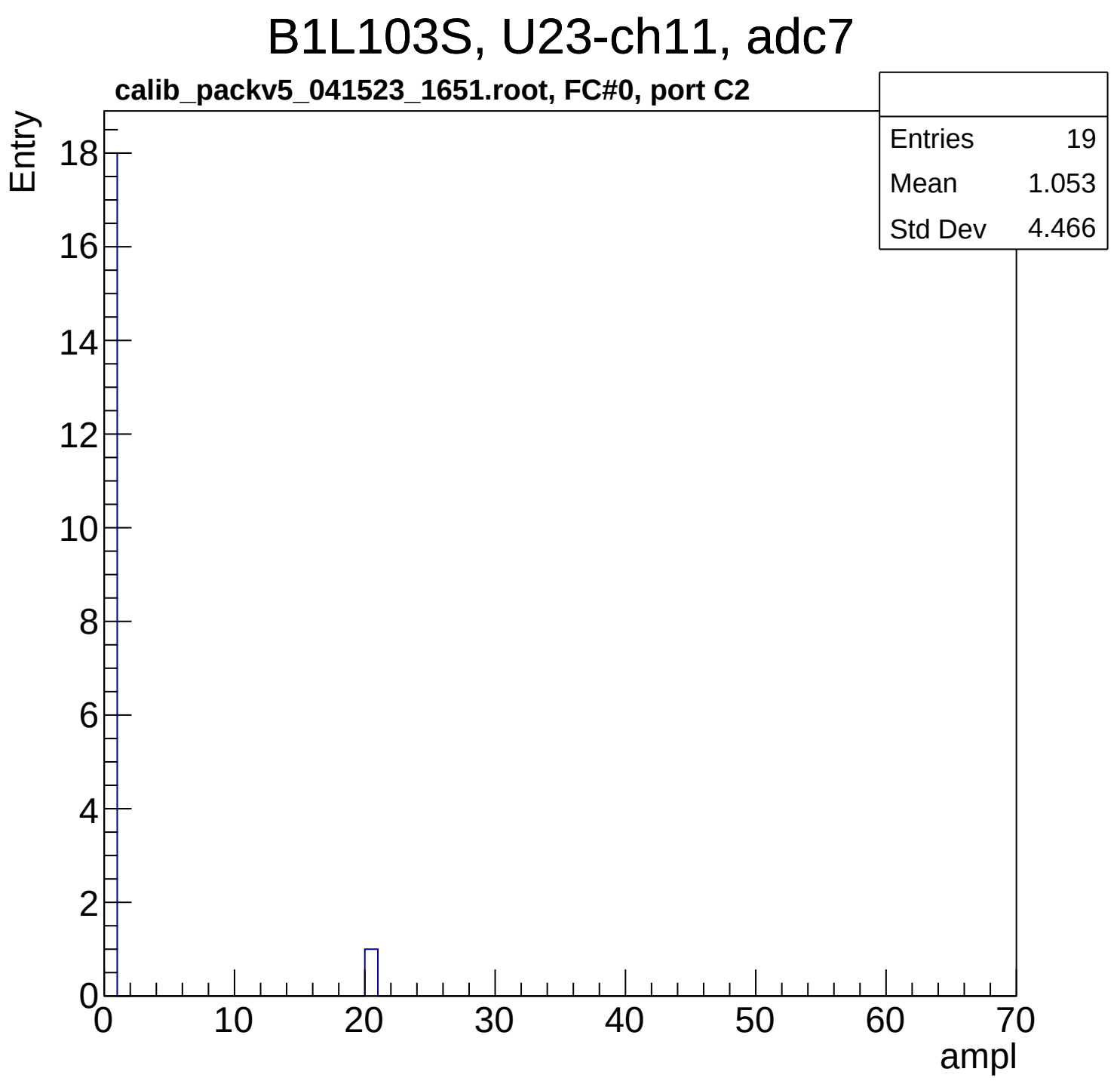

B1L103S, U23-ch11, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 36.61
Std Dev 16
Gaus mean : 43.6212
Gaus Width: 3.8354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

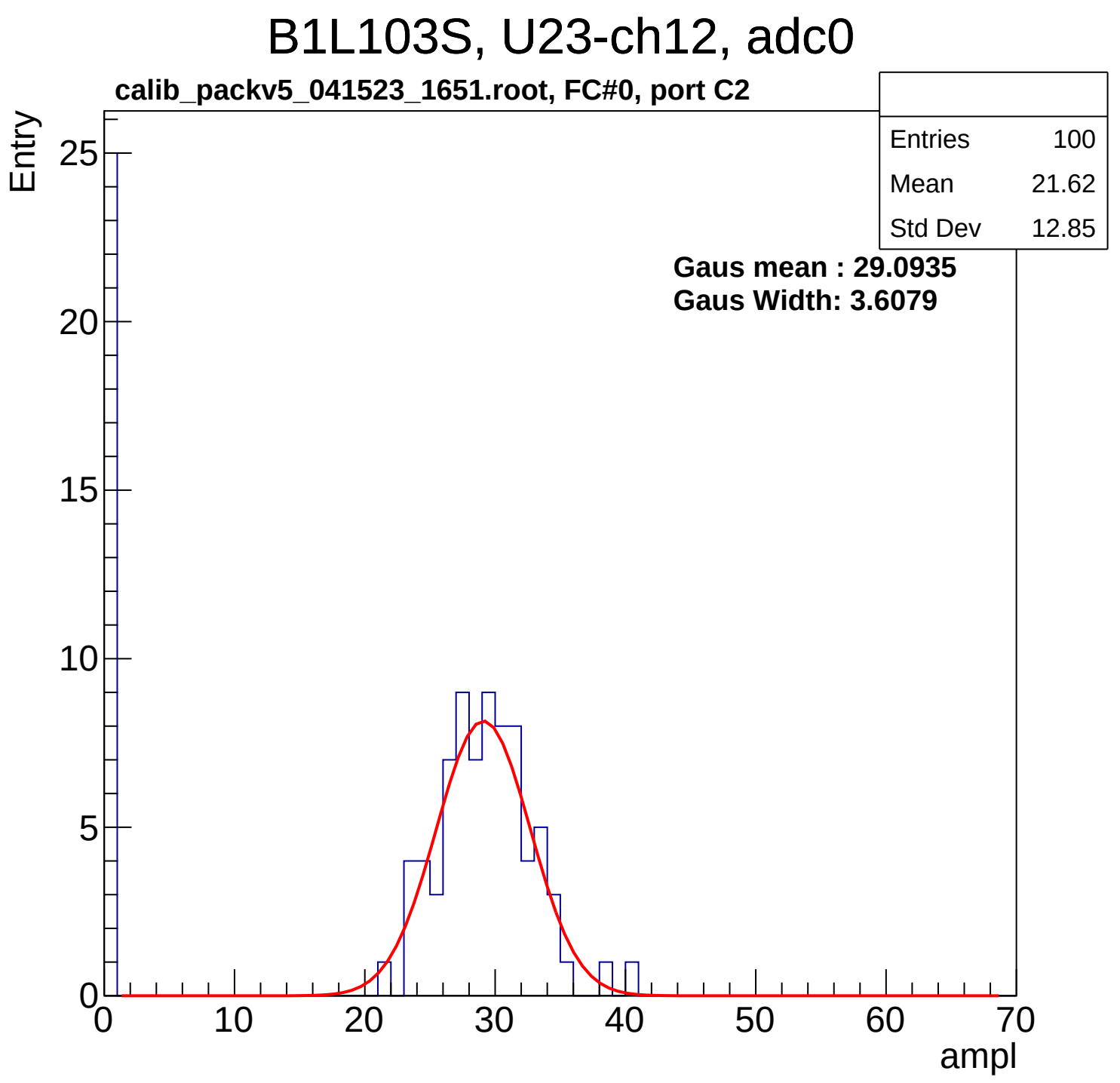

B1L103S, U23-ch11, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 47.51
Std Dev 11.59
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

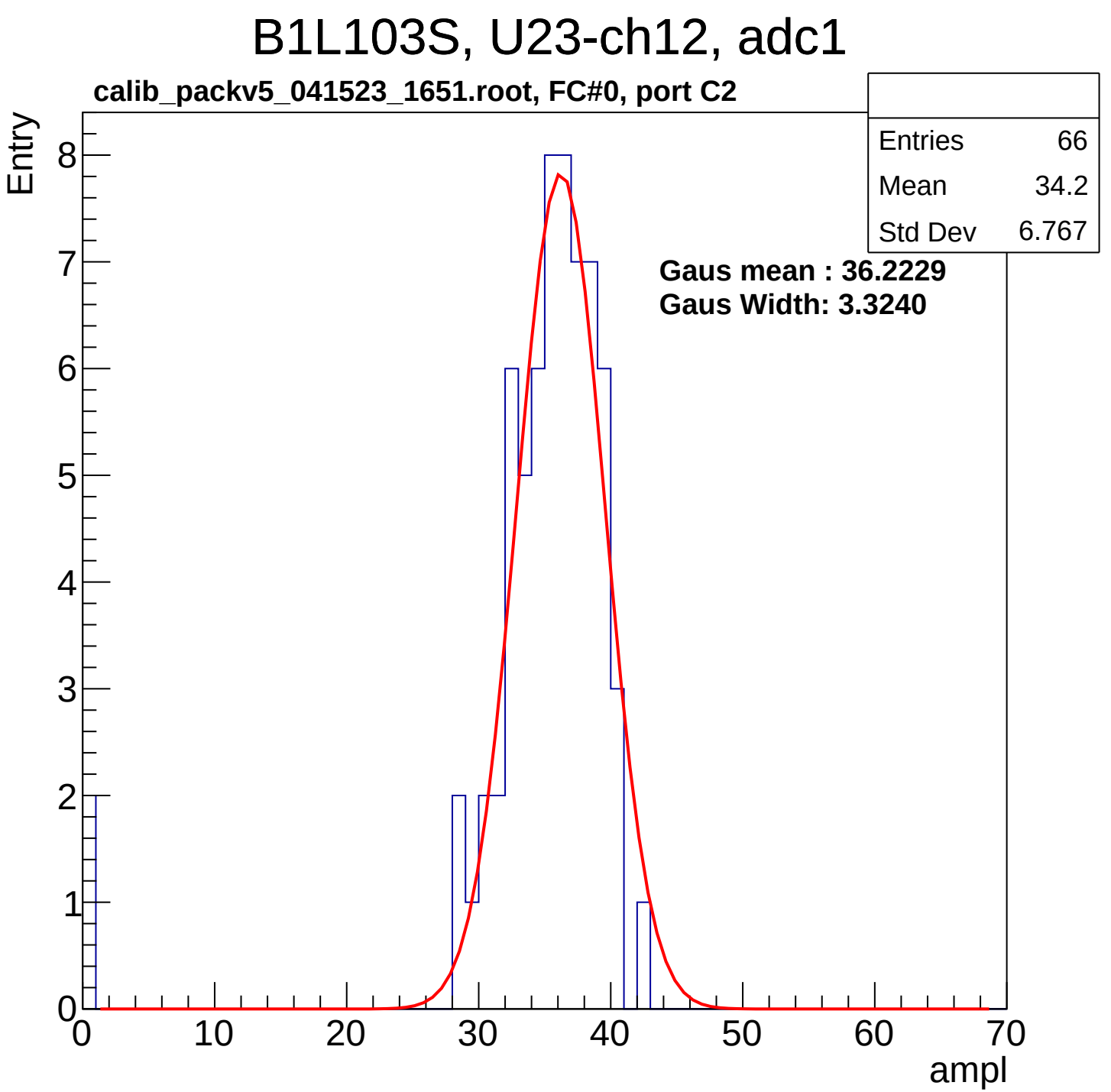

B1L103S, U23-ch11, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 56.15
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

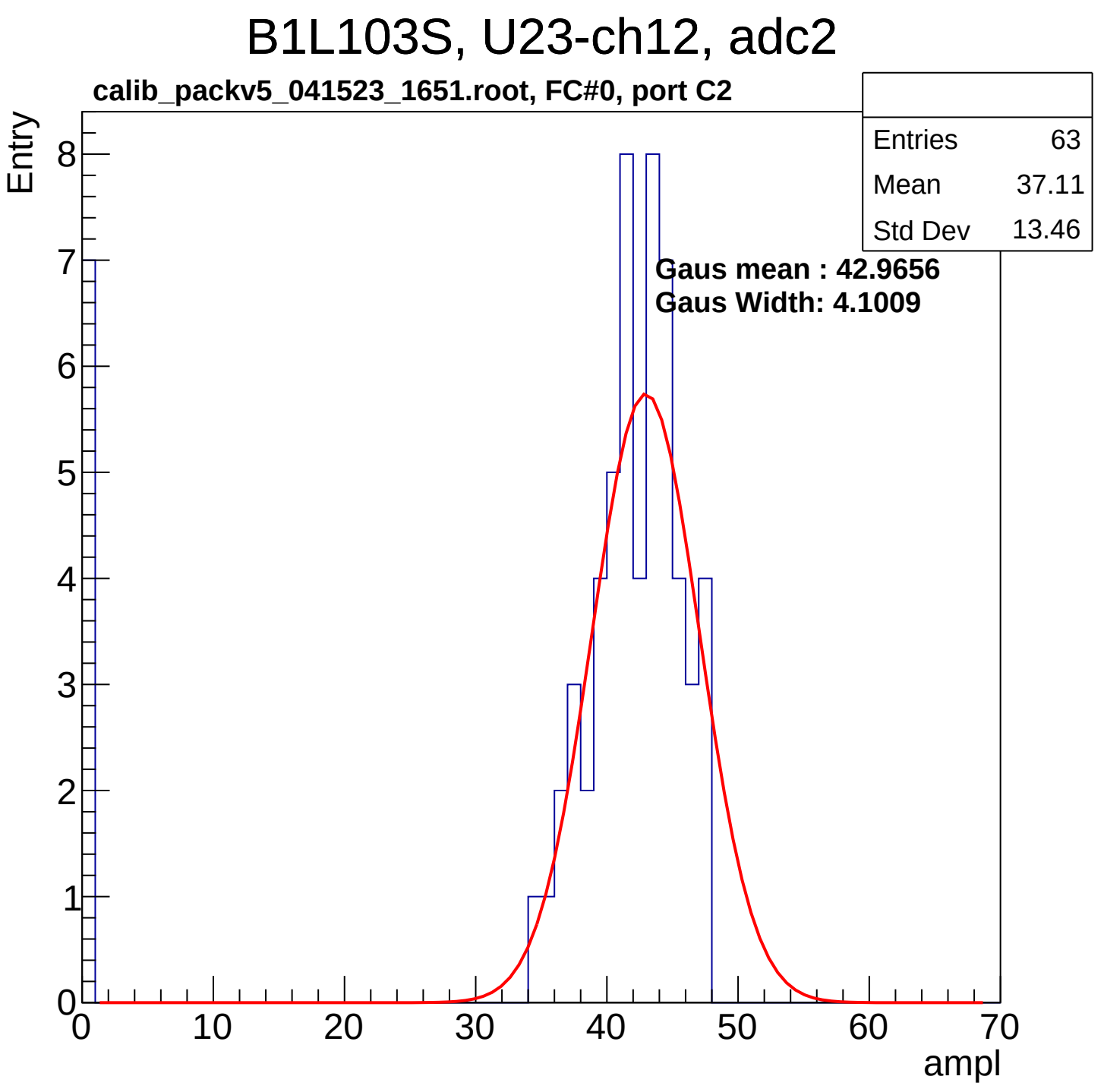

B1L103S, U23-ch11, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 42
Mean 58.79
Std Dev 9.478
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

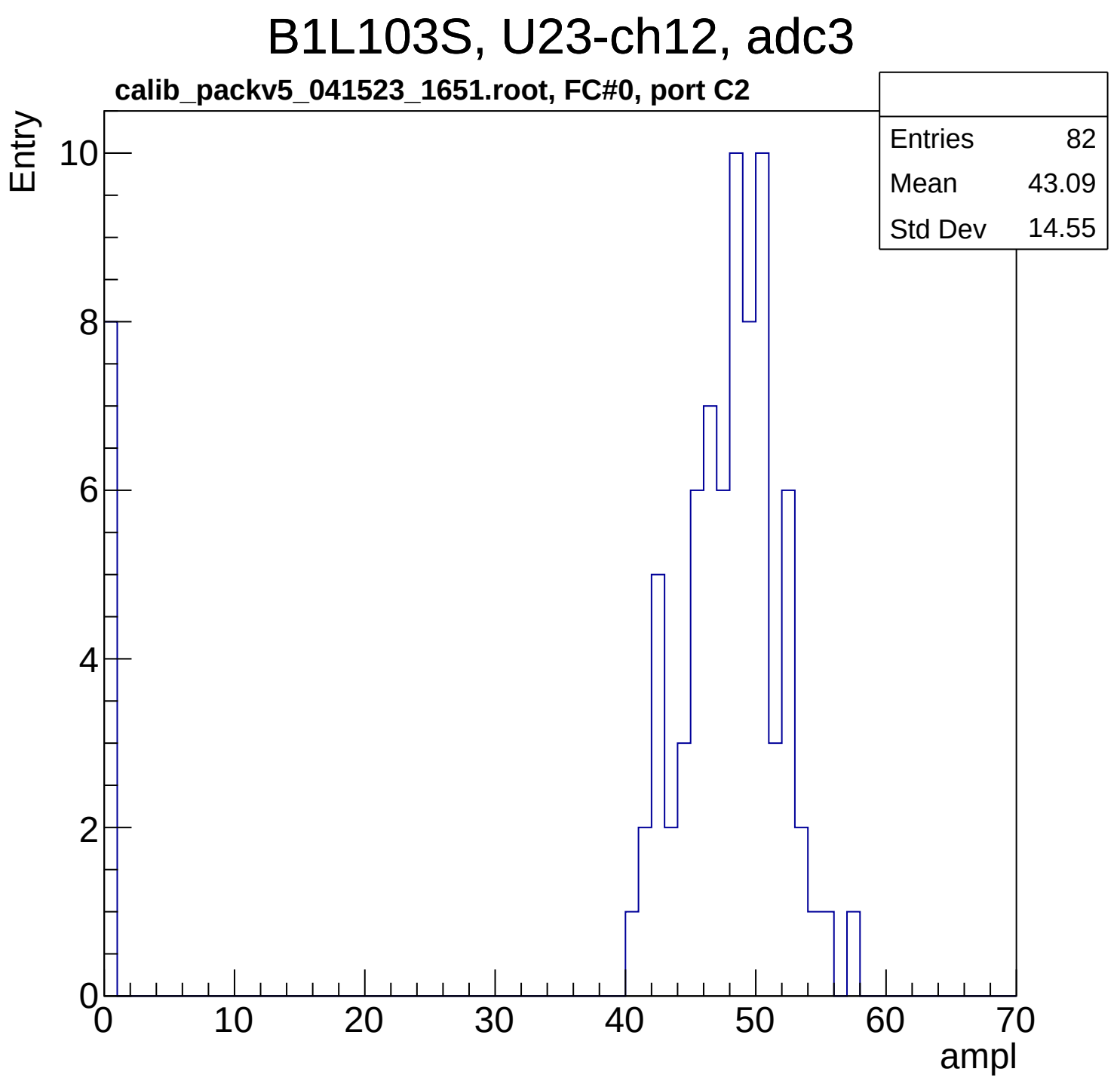

B1L103S, U23-ch11, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

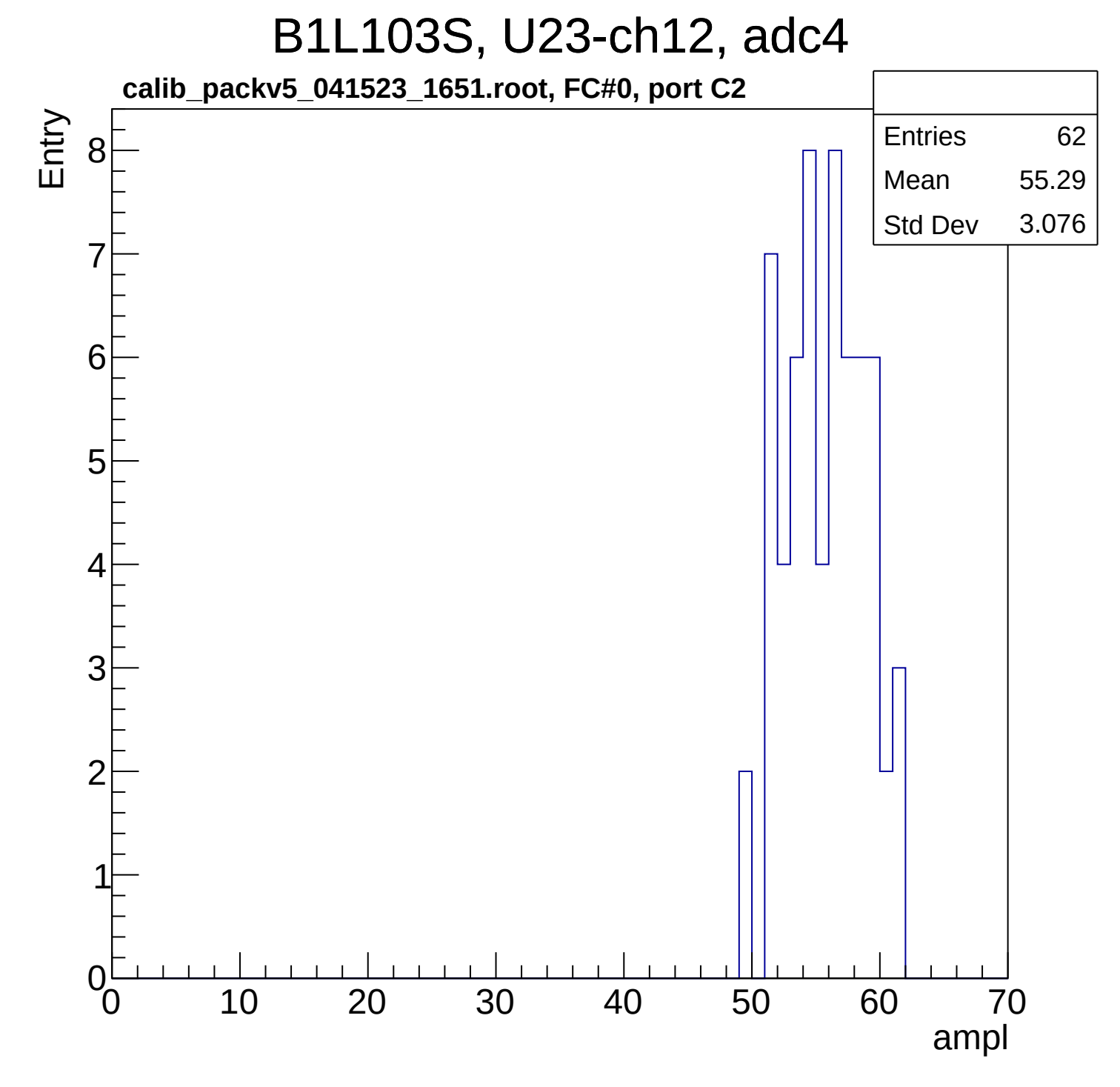

B1L103S, U23-ch11, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.053
Std Dev 4.466
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

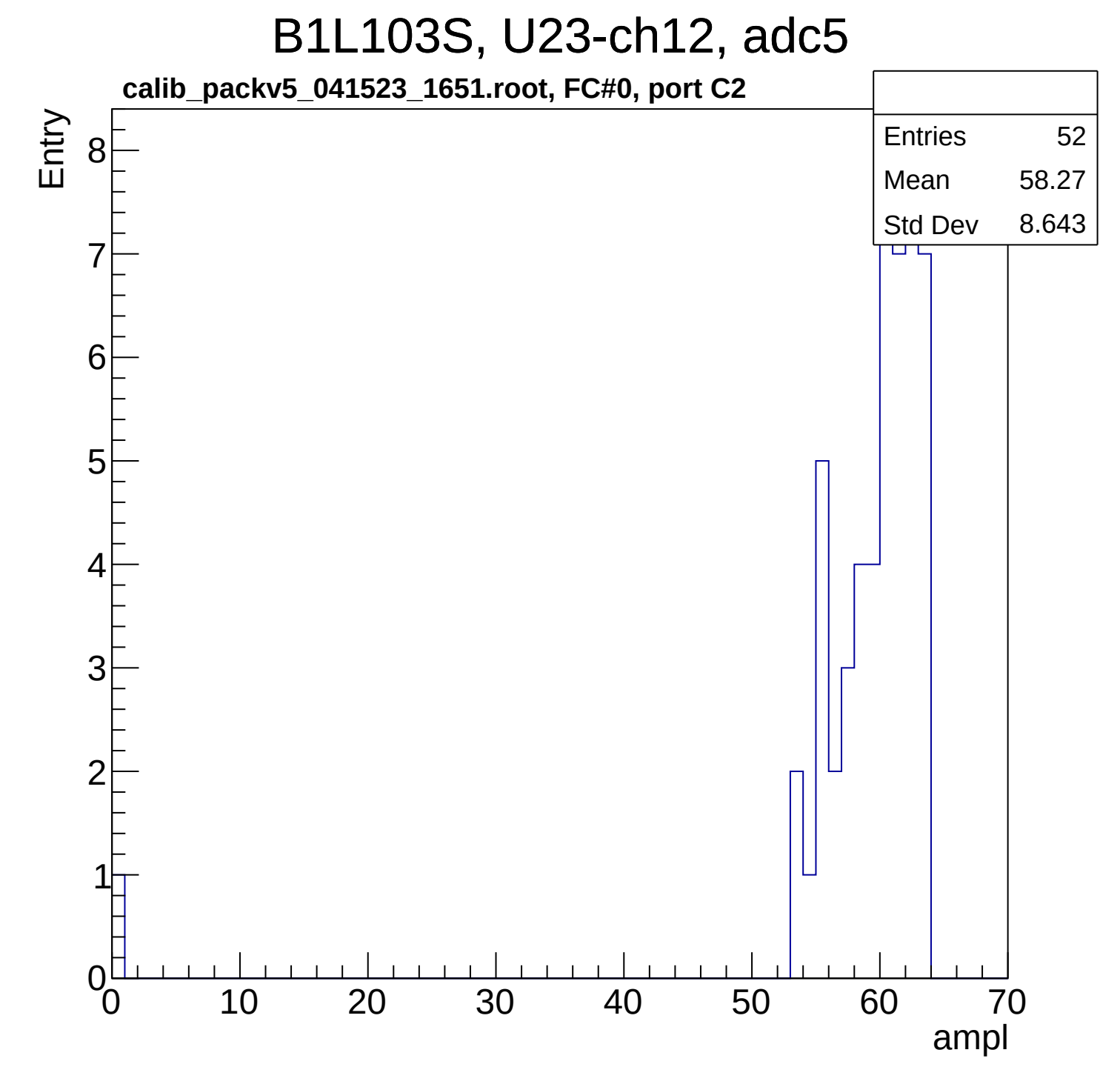

B1L103S, U23-ch12, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 21.62
Std Dev 12.85
Gaus mean : 29.0935
Gaus Width: 3.6079
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

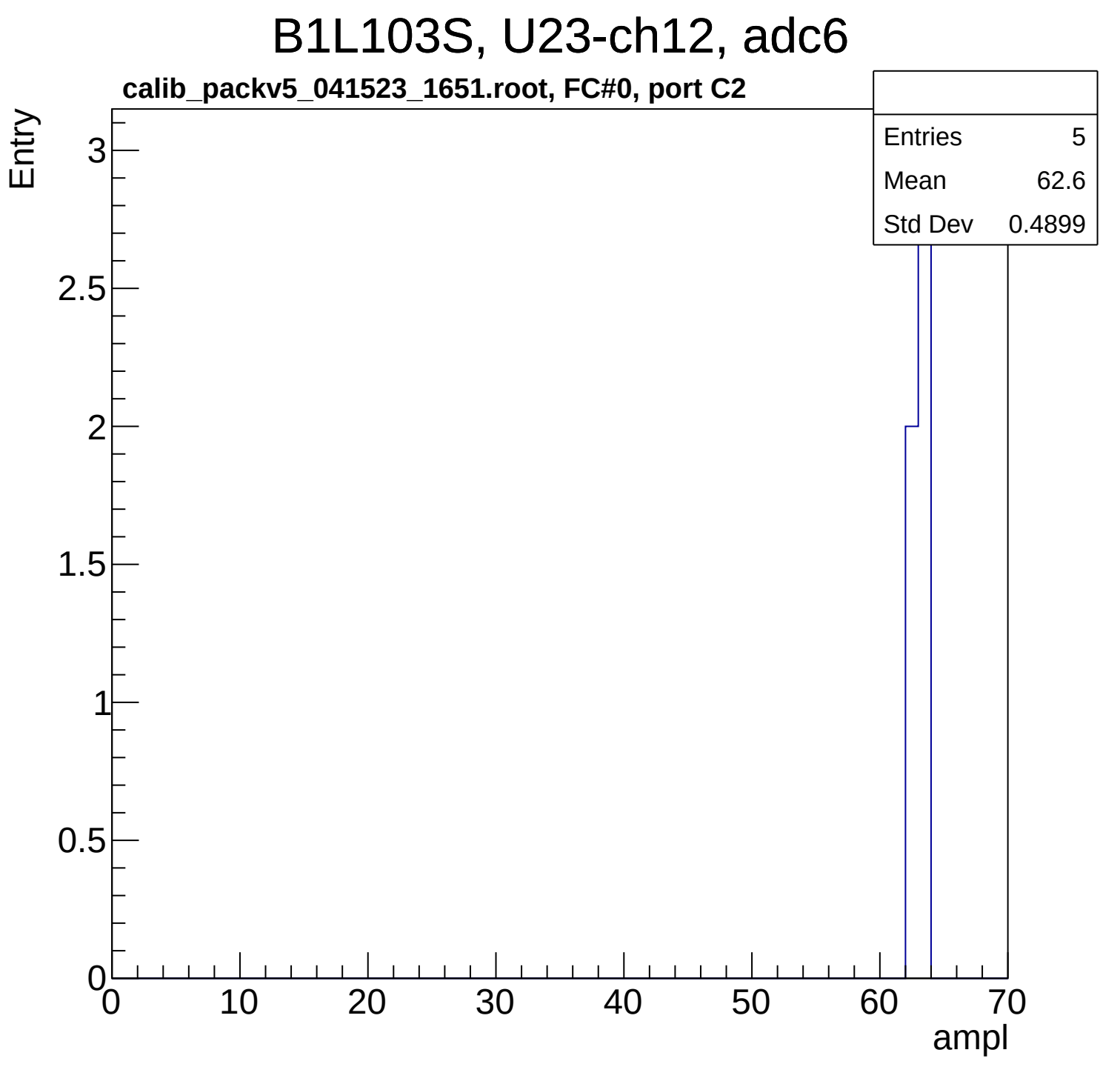

B1L103S, U23-ch12, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 34.2
Std Dev 6.767
Gaus mean : 36.2229
Gaus Width: 3.3240
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

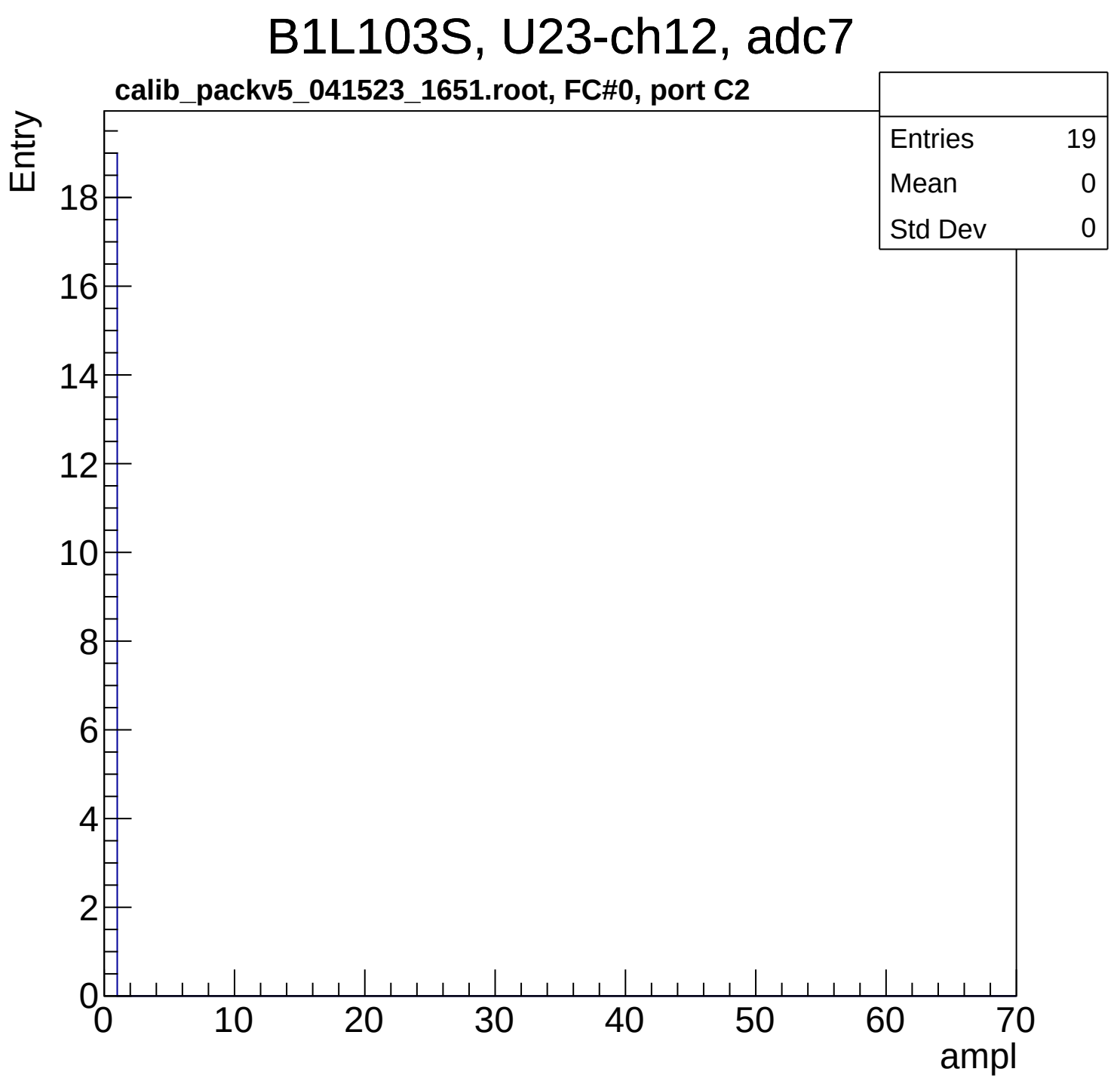

B1L103S, U23-ch12, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 37.11
Std Dev 13.46
Gaus mean : 42.9656
Gaus Width: 4.1009
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

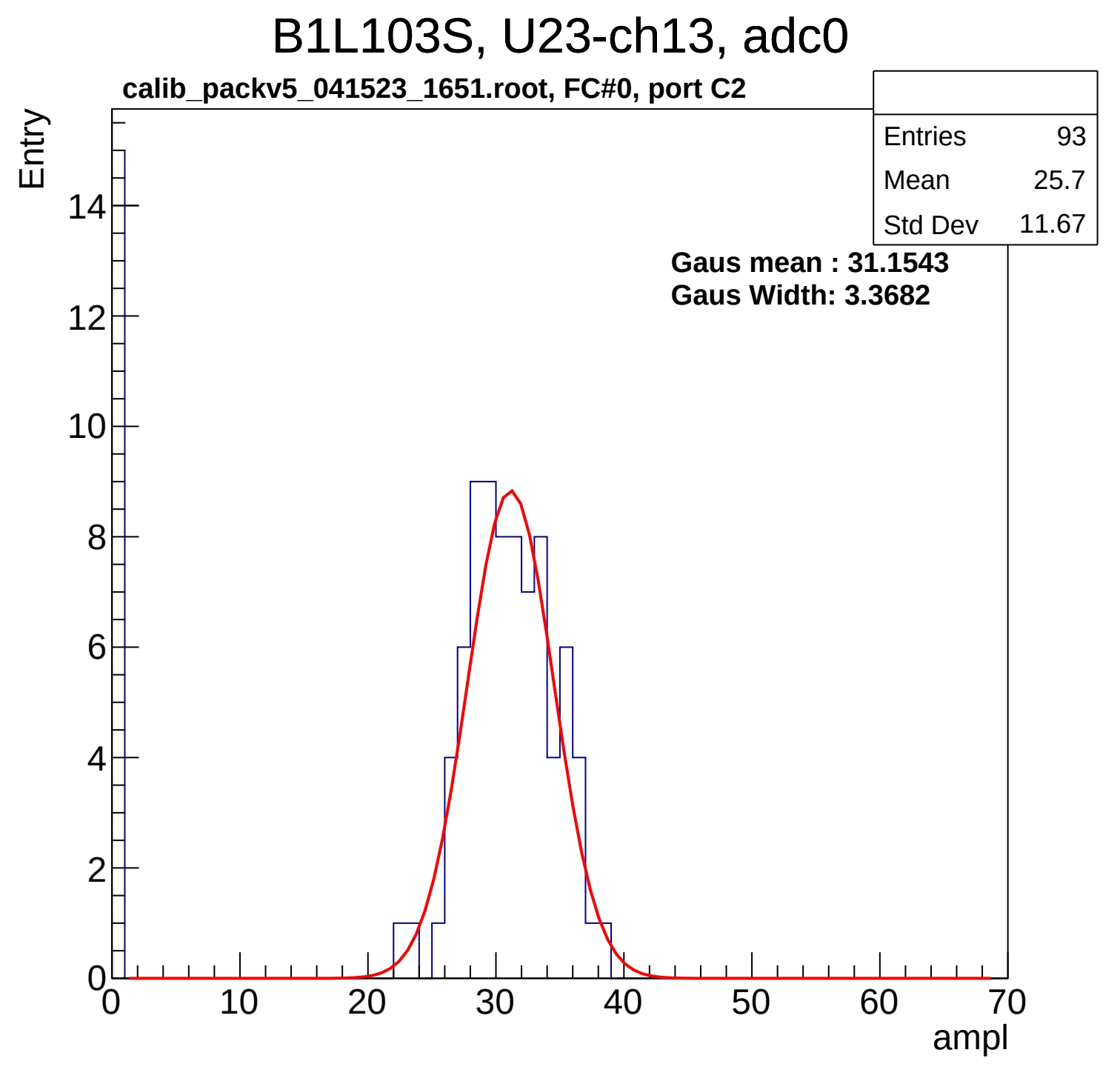

B1L103S, U23-ch12, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 43.09
Std Dev 14.55
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

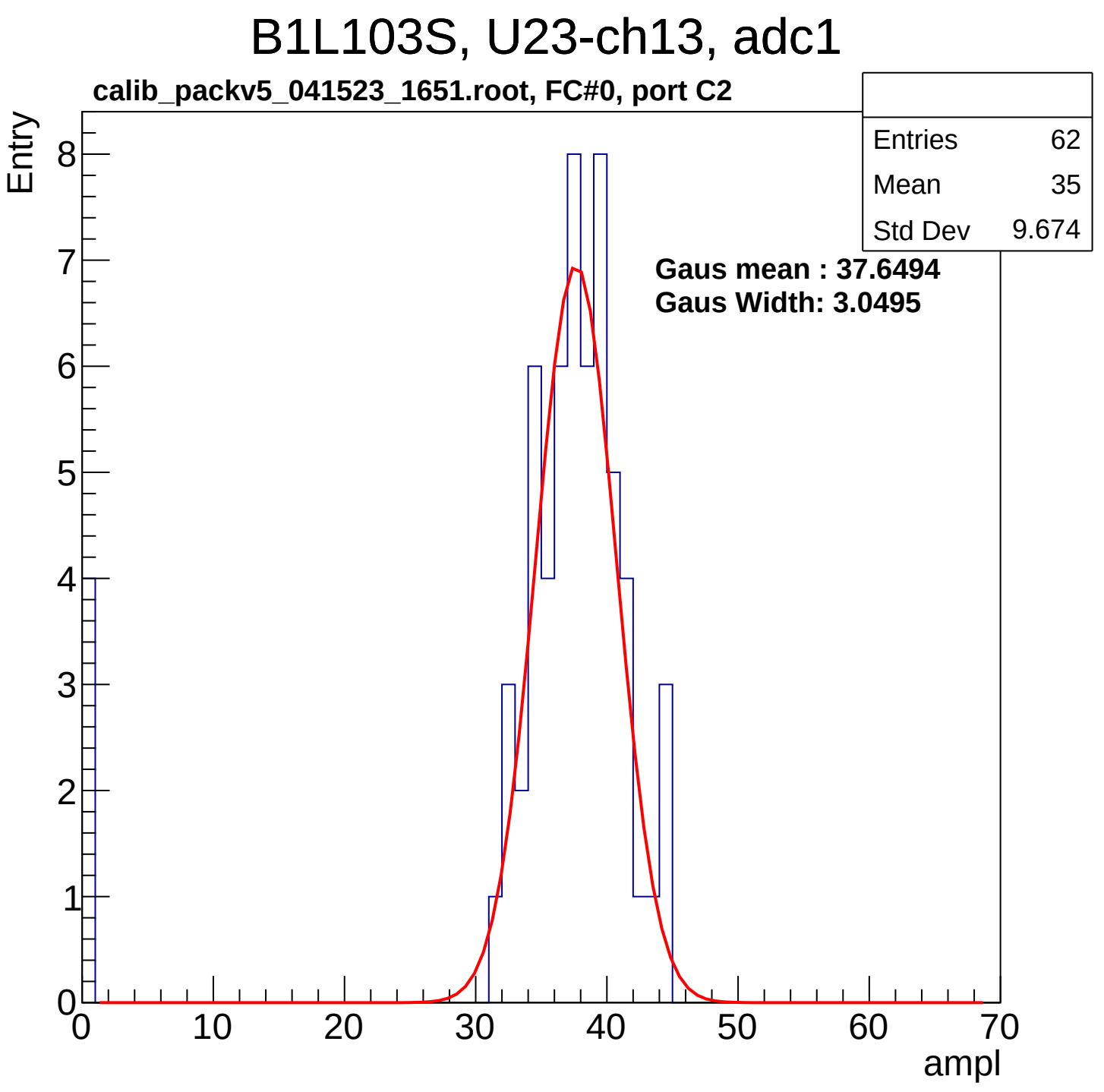

B1L103S, U23-ch12, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 55.29
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

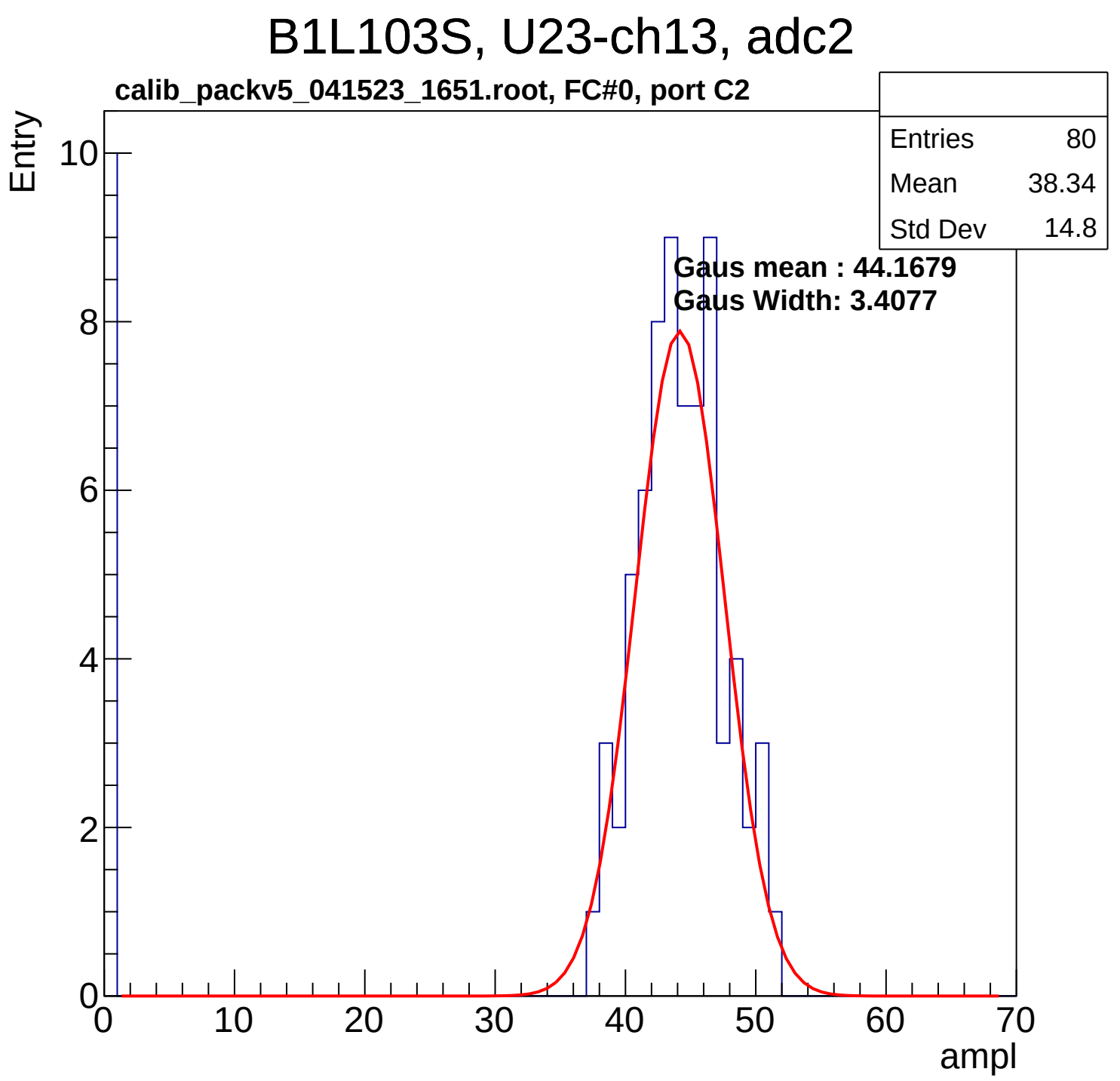

B1L103S, U23-ch12, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 58.27
Std Dev 8.643
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

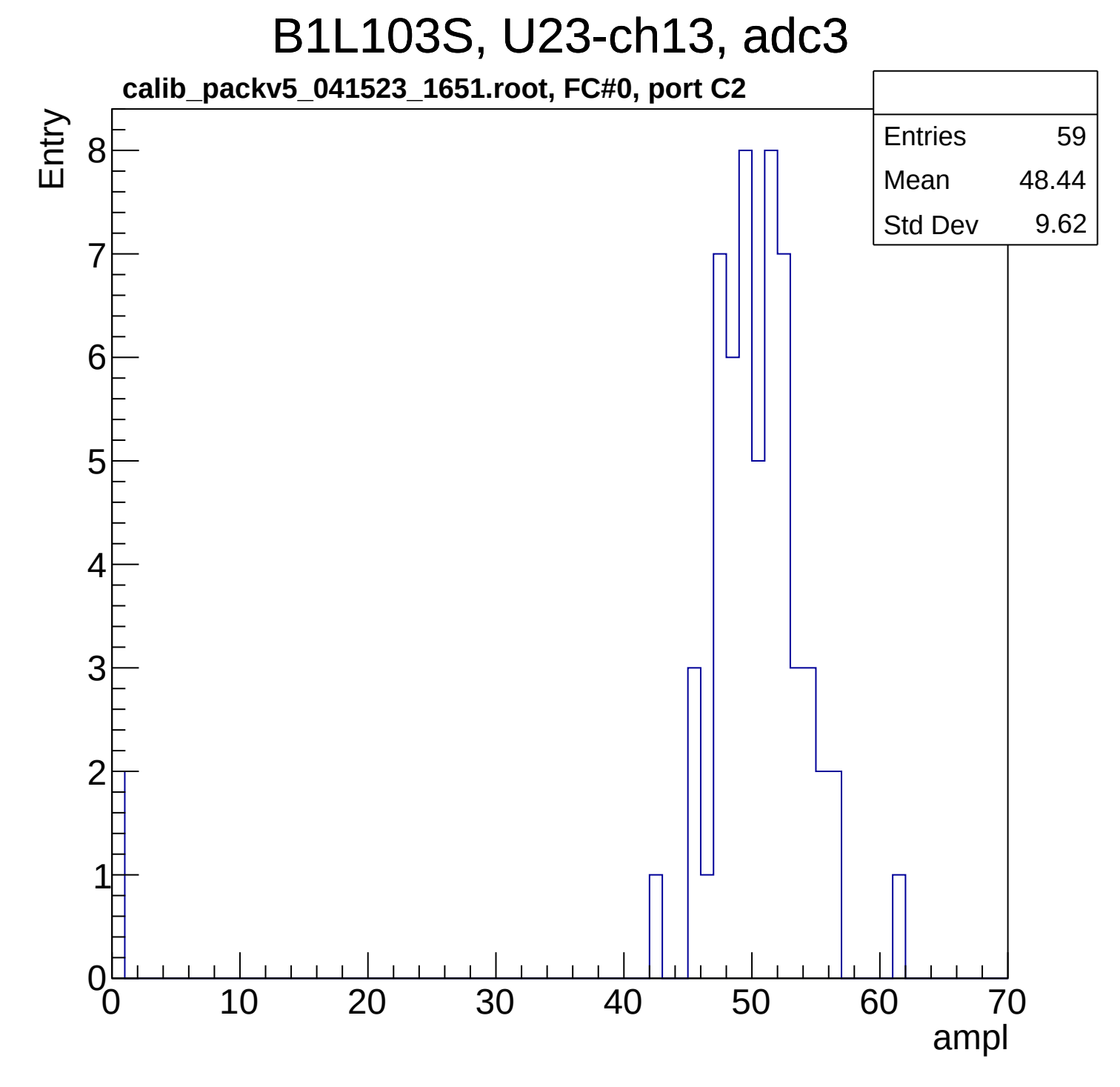

B1L103S, U23-ch12, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

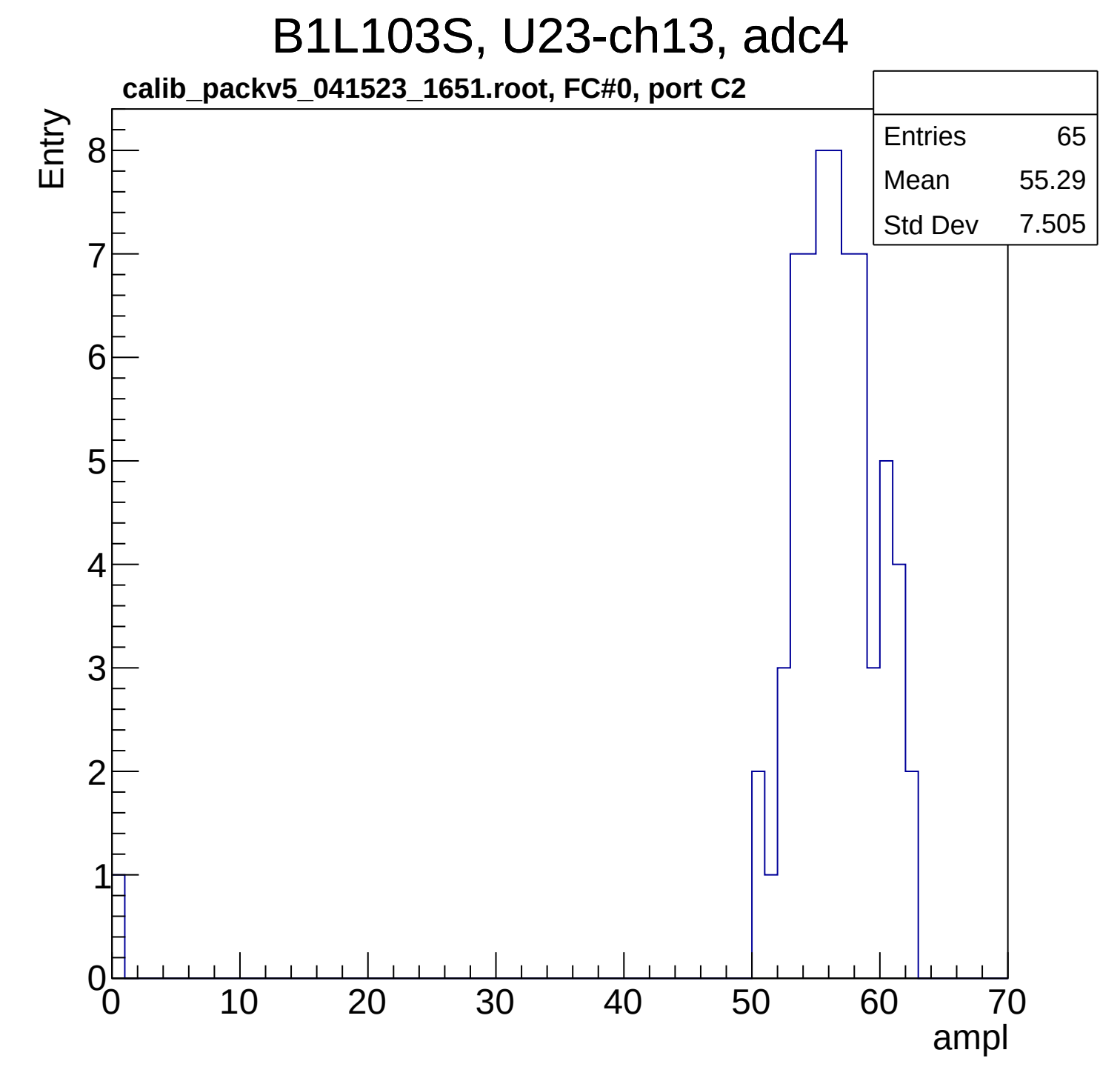

B1L103S, U23-ch12, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

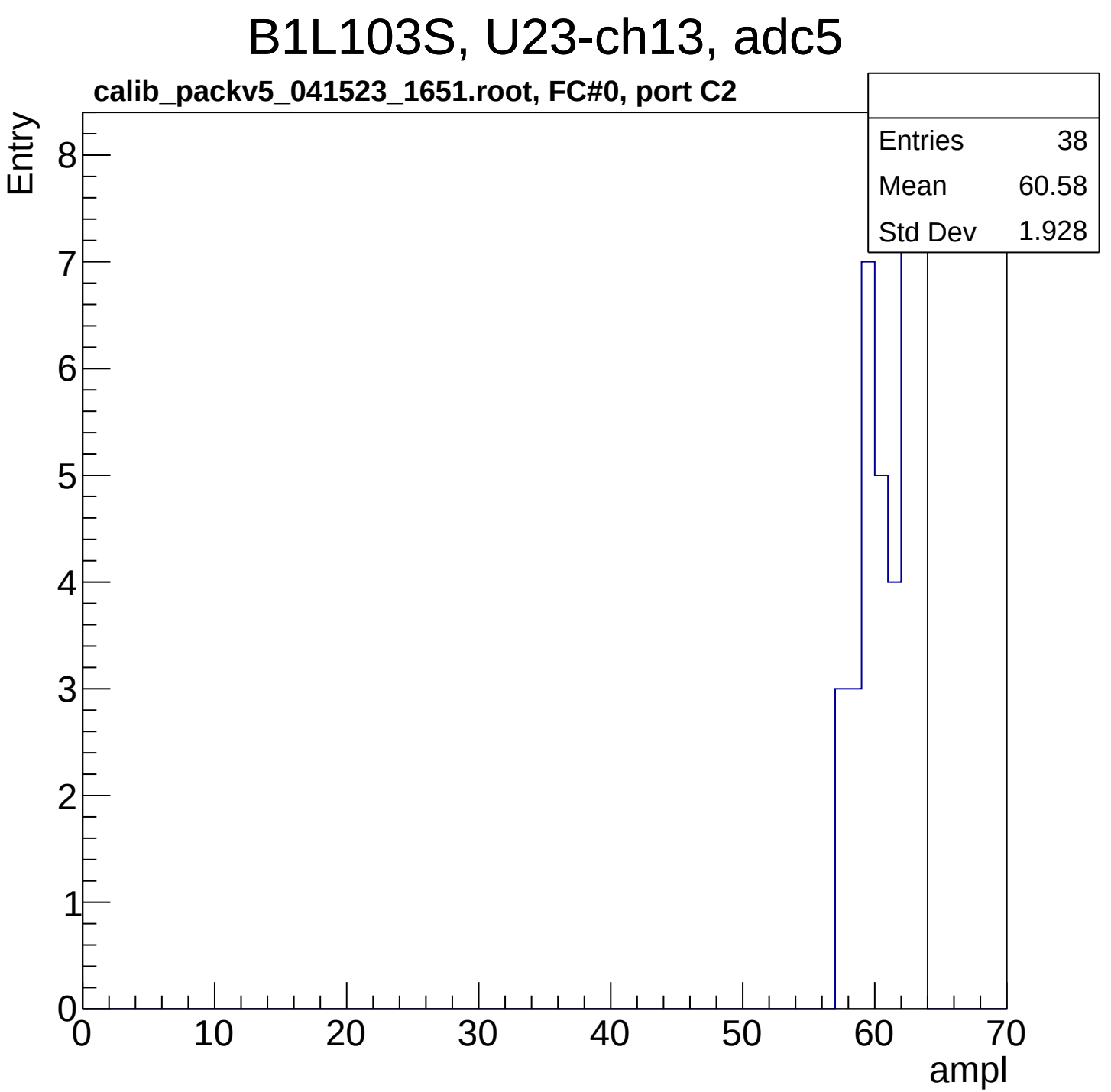

B1L103S, U23-ch13, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 93
Mean 25.7
Std Dev 11.67
Gaus mean : 31.1543
Gaus Width: 3.3682
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

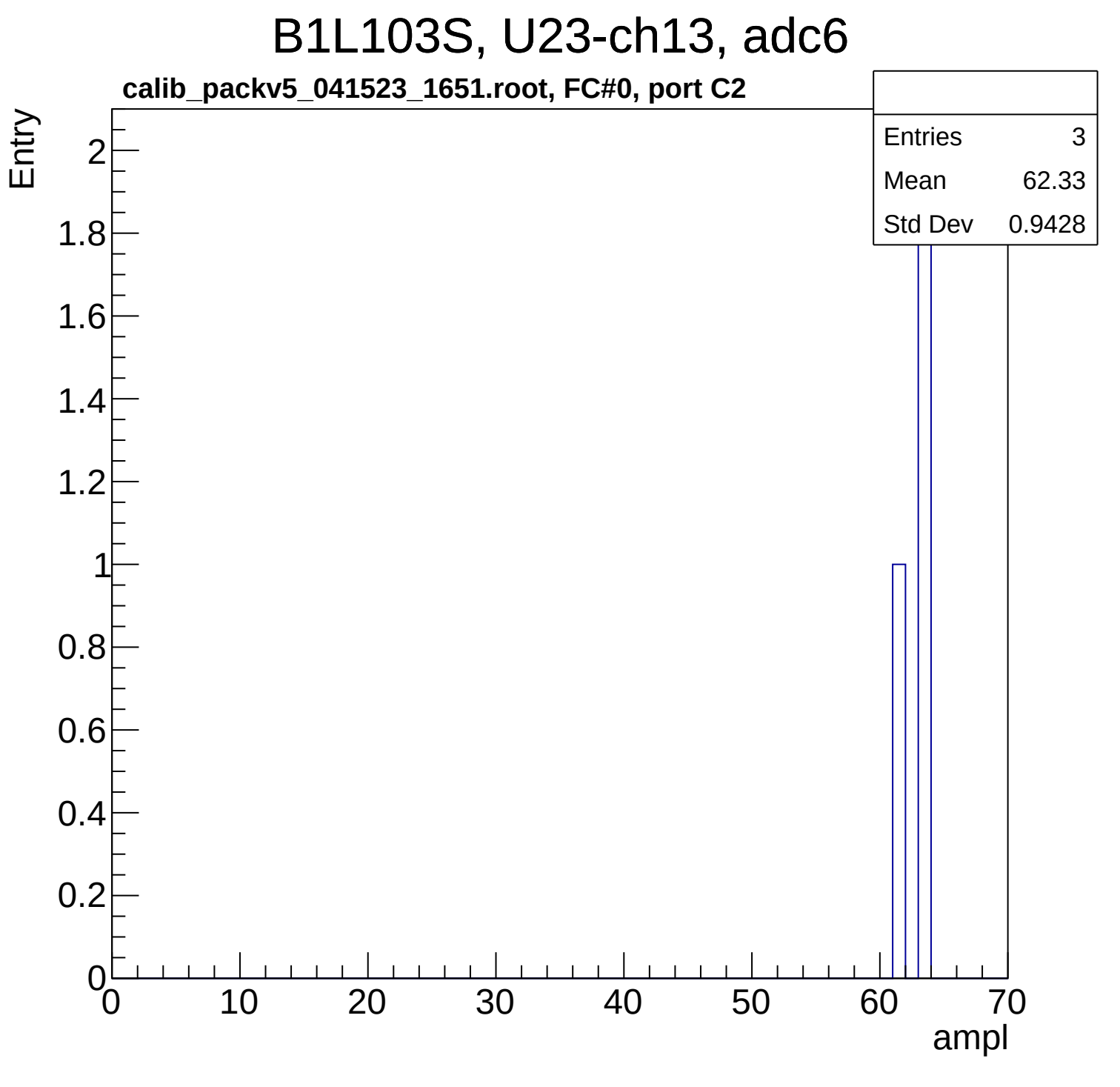

B1L103S, U23-ch13, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 35
Std Dev 9.674
Gaus mean : 37.6494
Gaus Width: 3.0495
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

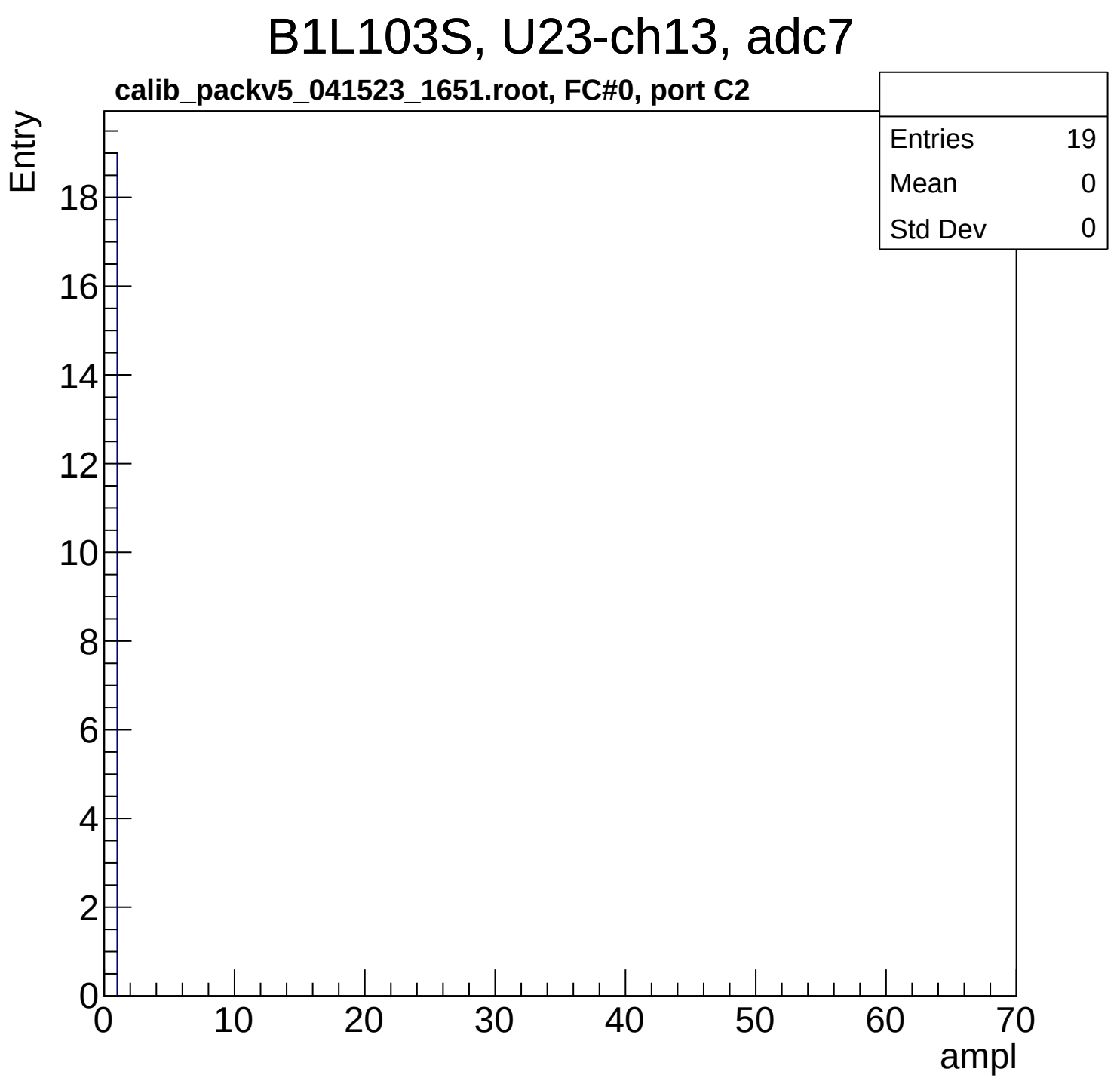

B1L103S, U23-ch13, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 38.34
Std Dev 14.8
Gaus mean : 44.1679
Gaus Width: 3.4077
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

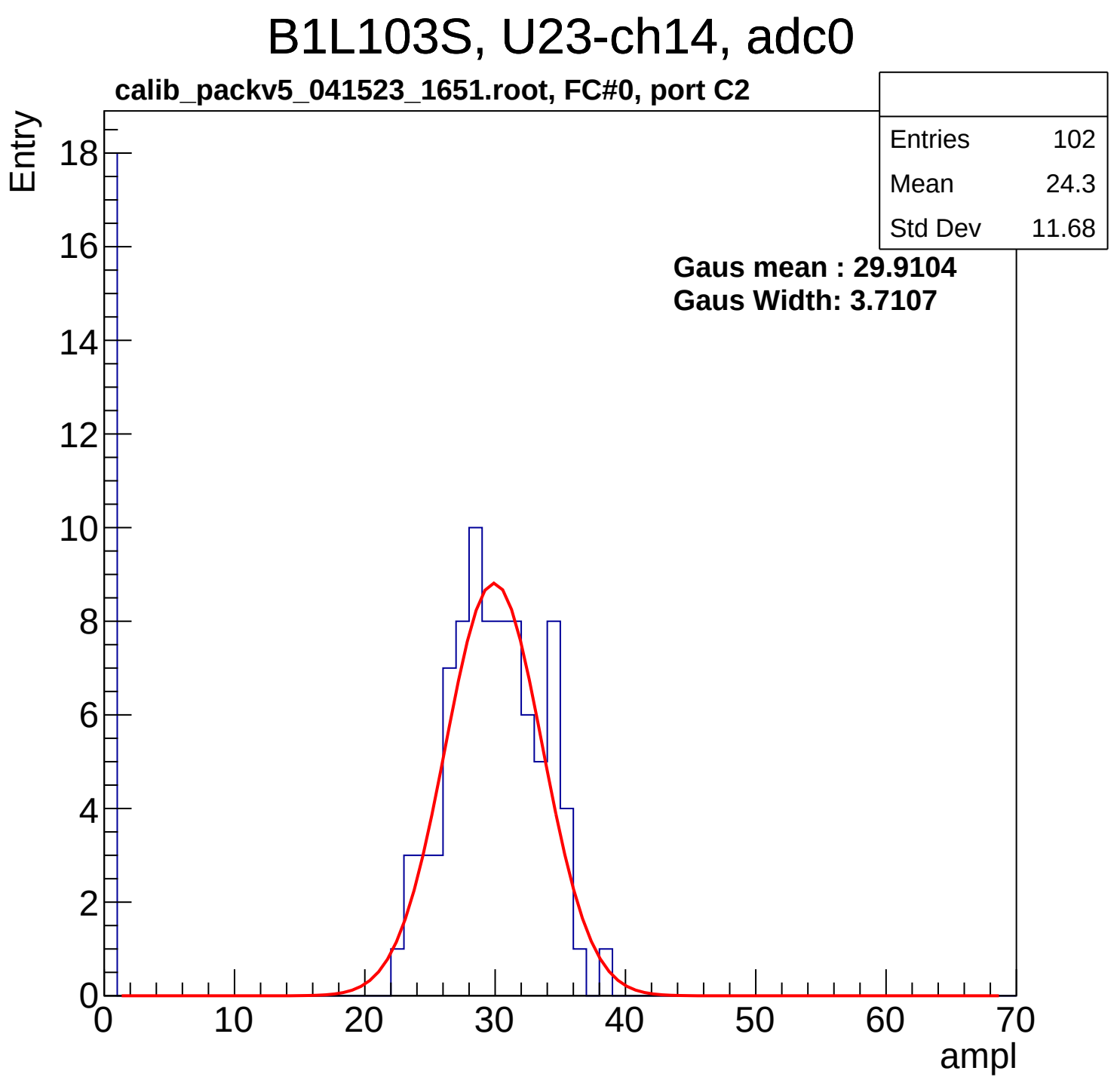

B1L103S, U23-ch13, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 48.44
Std Dev 9.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

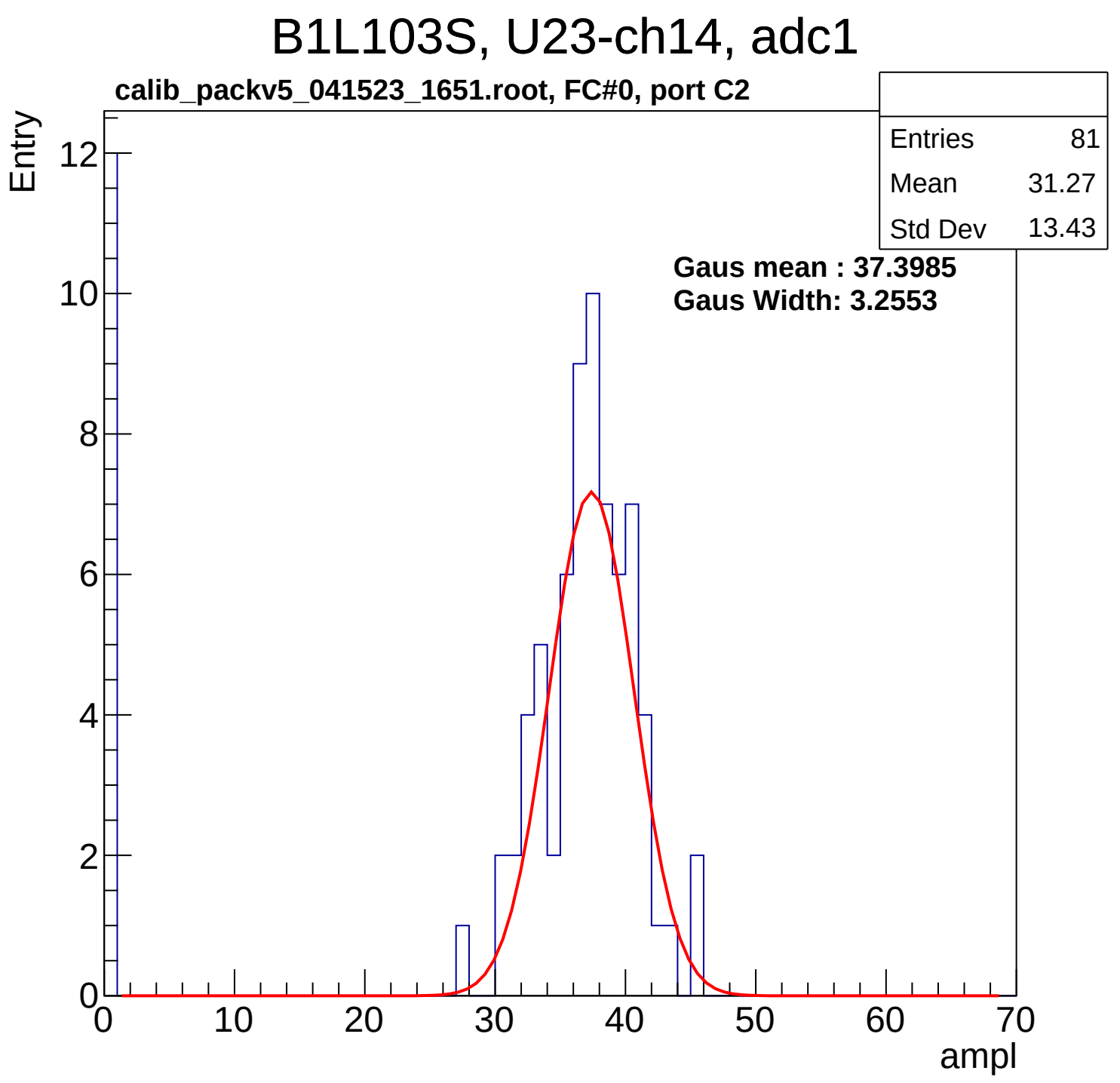

B1L103S, U23-ch13, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 55.29
Std Dev 7.505
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

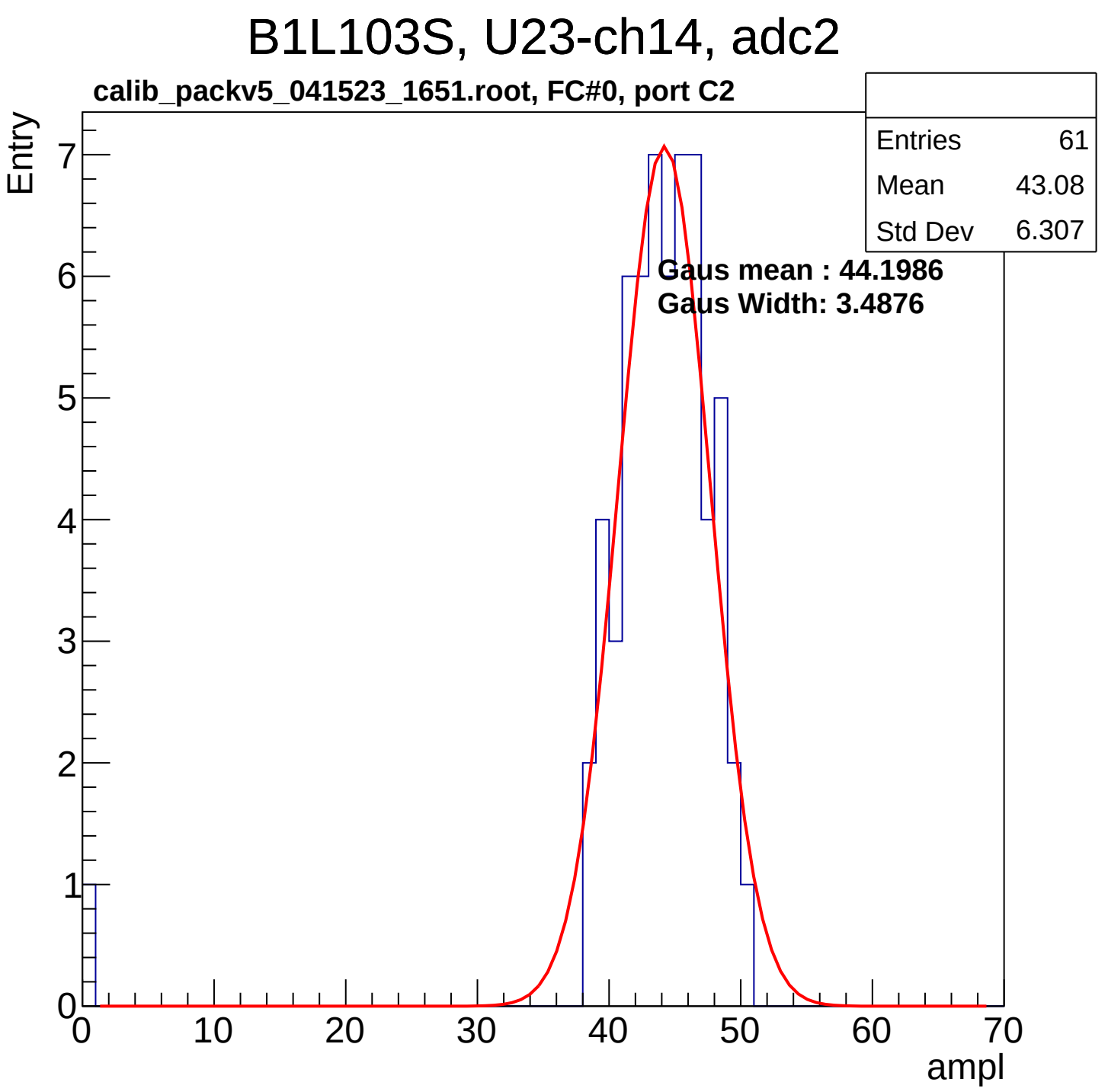

B1L103S, U23-ch13, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 38
Mean 60.58
Std Dev 1.928
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

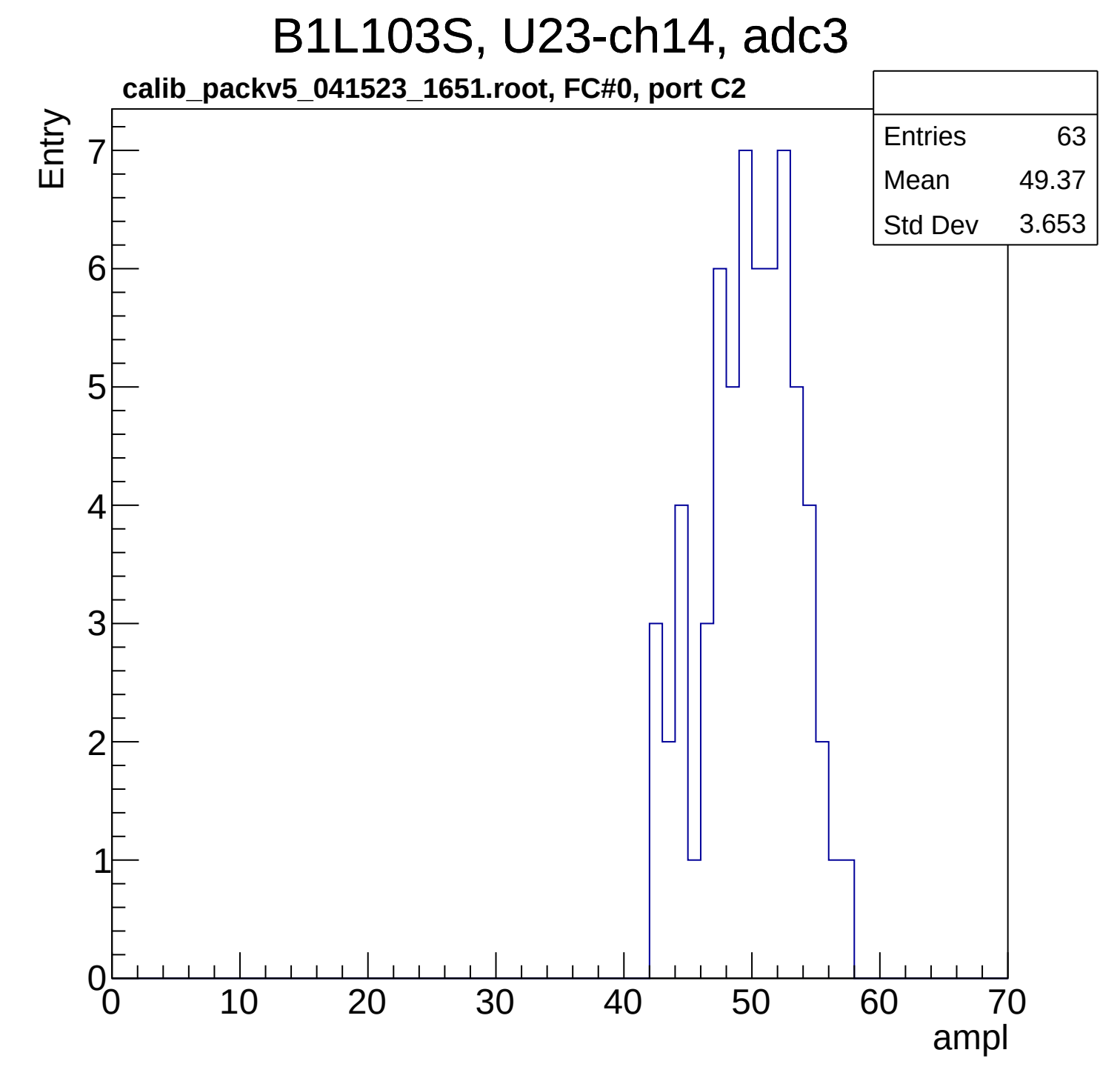

B1L103S, U23-ch13, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

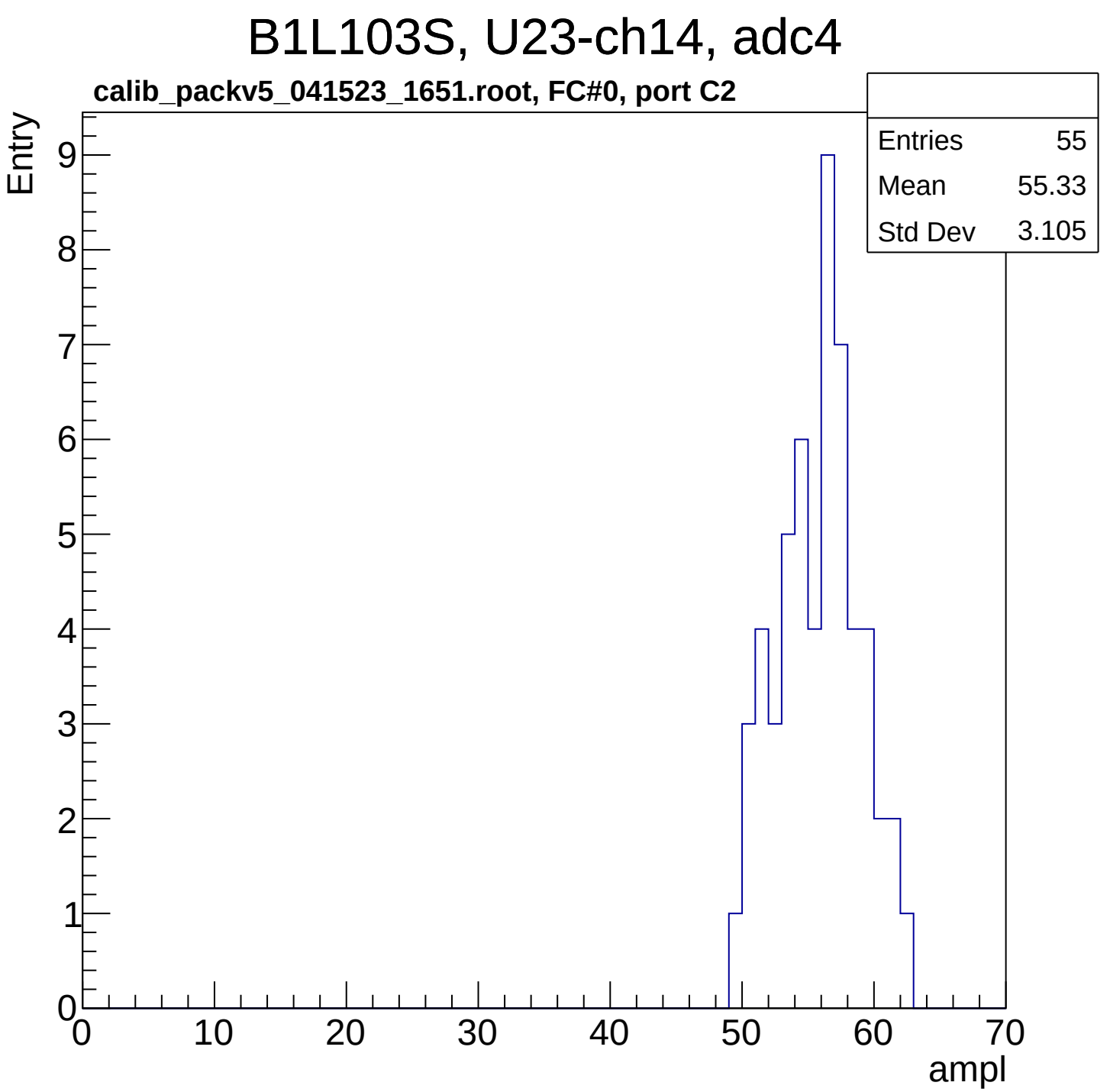

B1L103S, U23-ch13, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

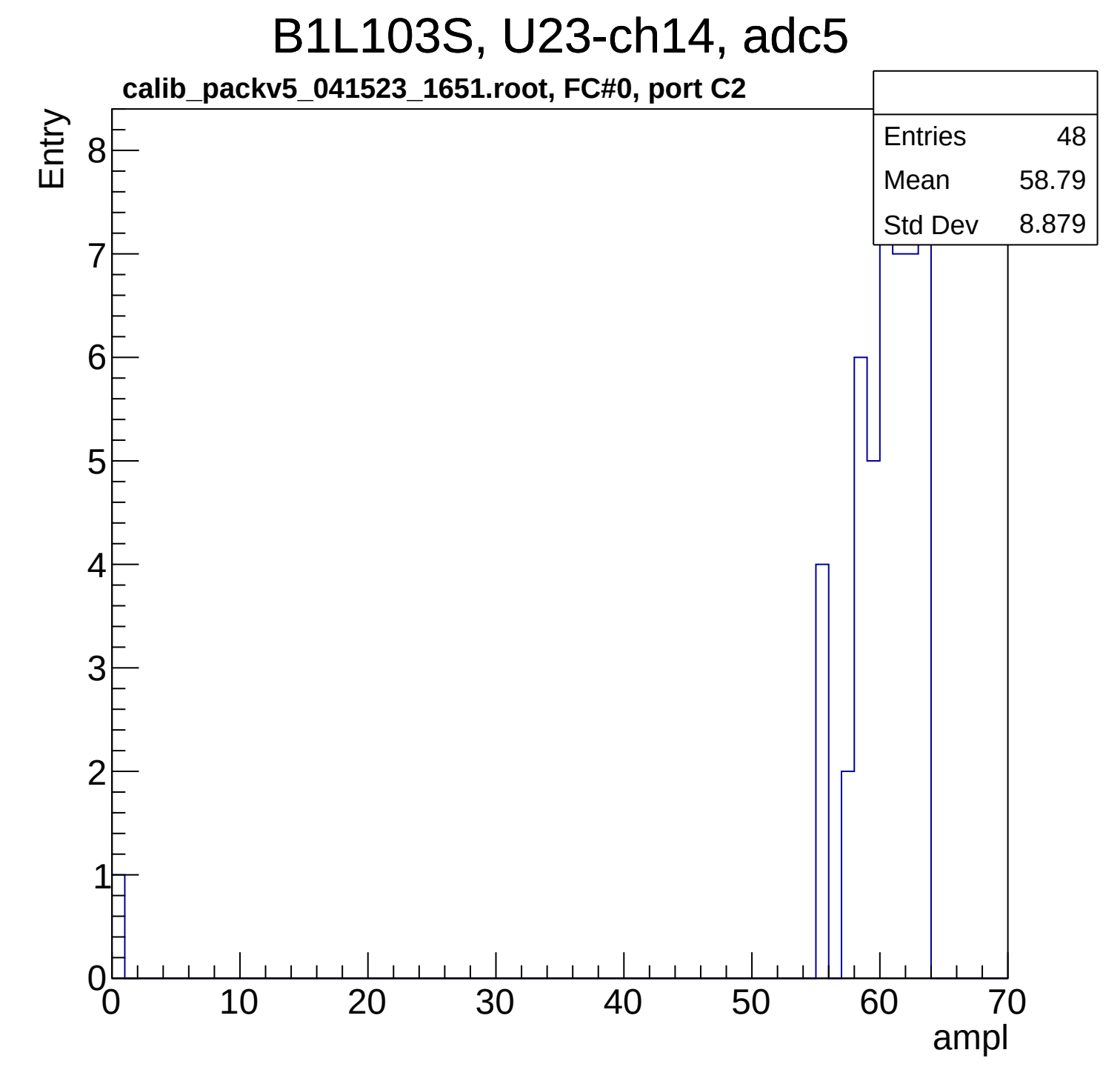

B1L103S, U23-ch14, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 102
Mean 24.3
Std Dev 11.68
Gaus mean : 29.9104
Gaus Width: 3.7107
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

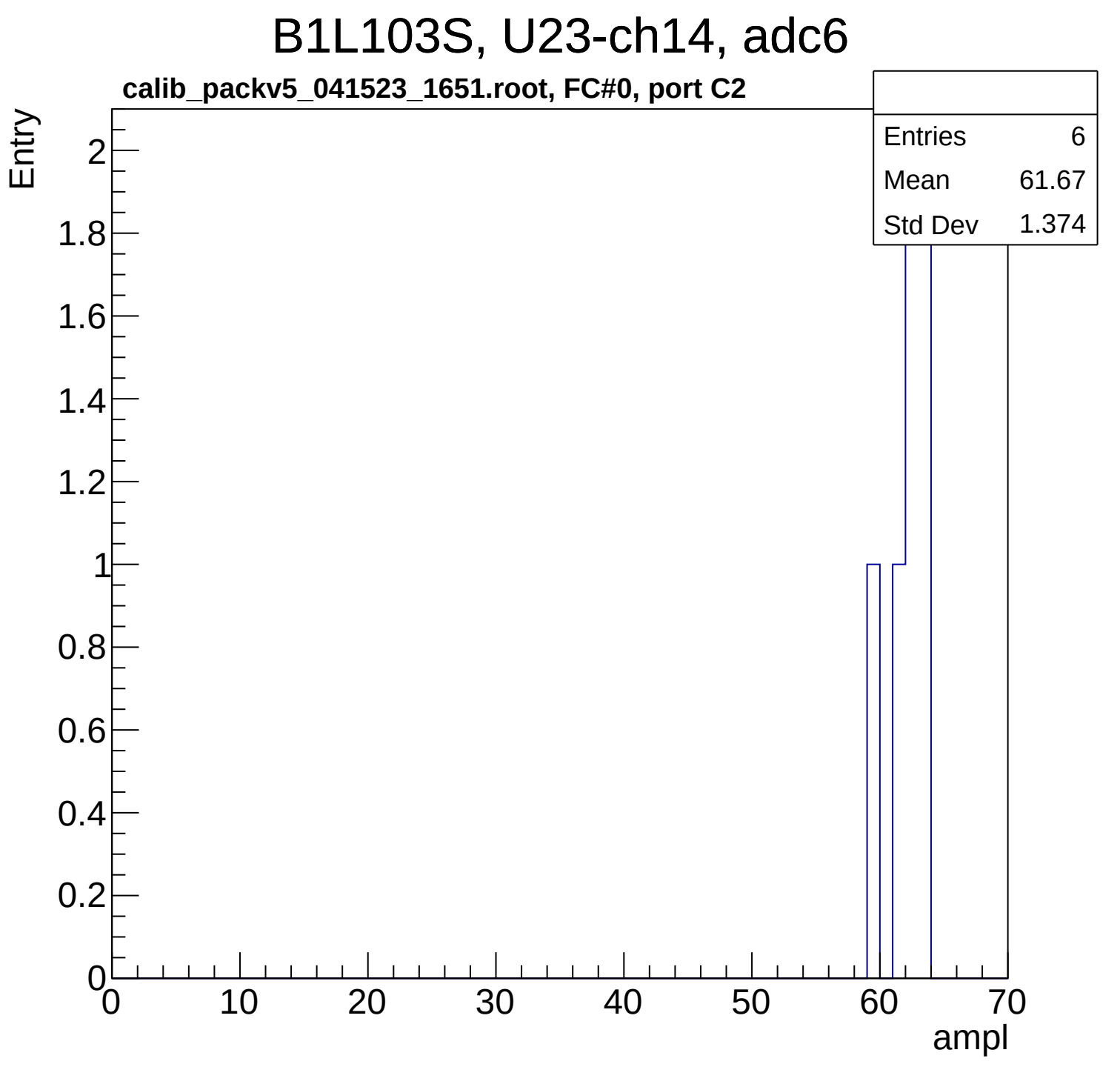

B1L103S, U23-ch14, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 31.27
Std Dev 13.43
Gaus mean : 37.3985
Gaus Width: 3.2553
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

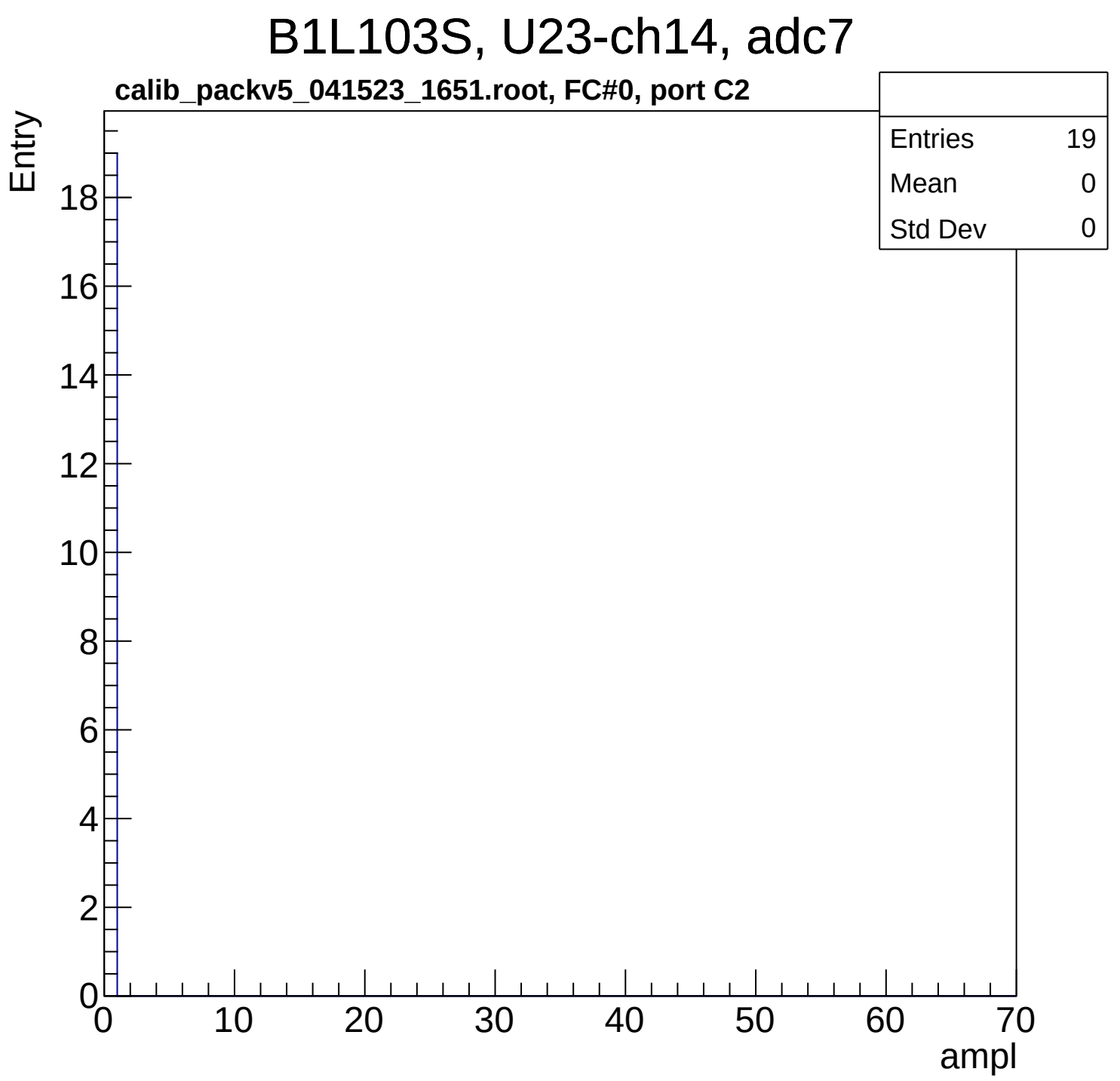

B1L103S, U23-ch14, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 43.08
Std Dev 6.307
Gaus mean : 44.1986
Gaus Width: 3.4876
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

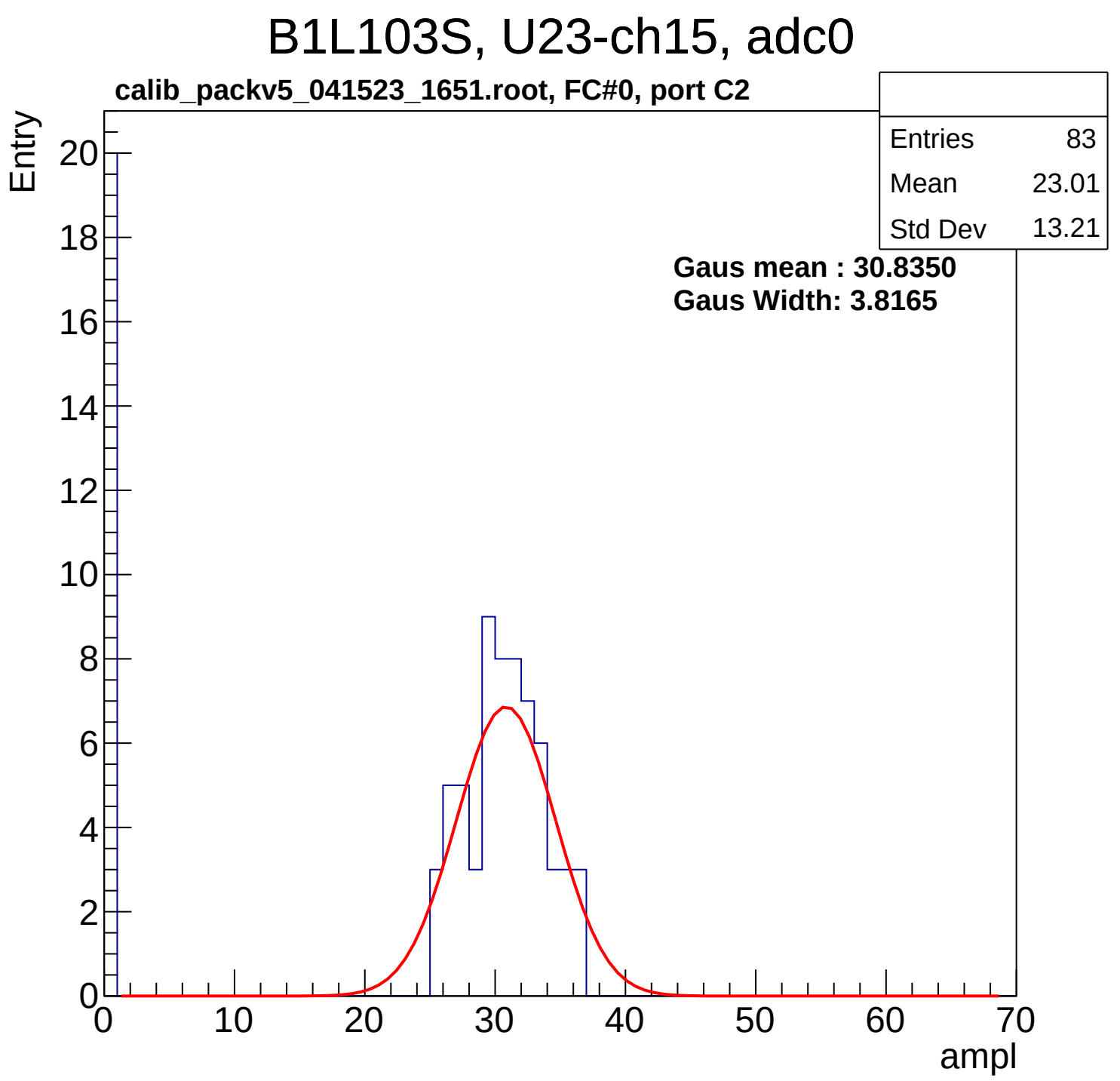

B1L103S, U23-ch14, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 49.37
Std Dev 3.653
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

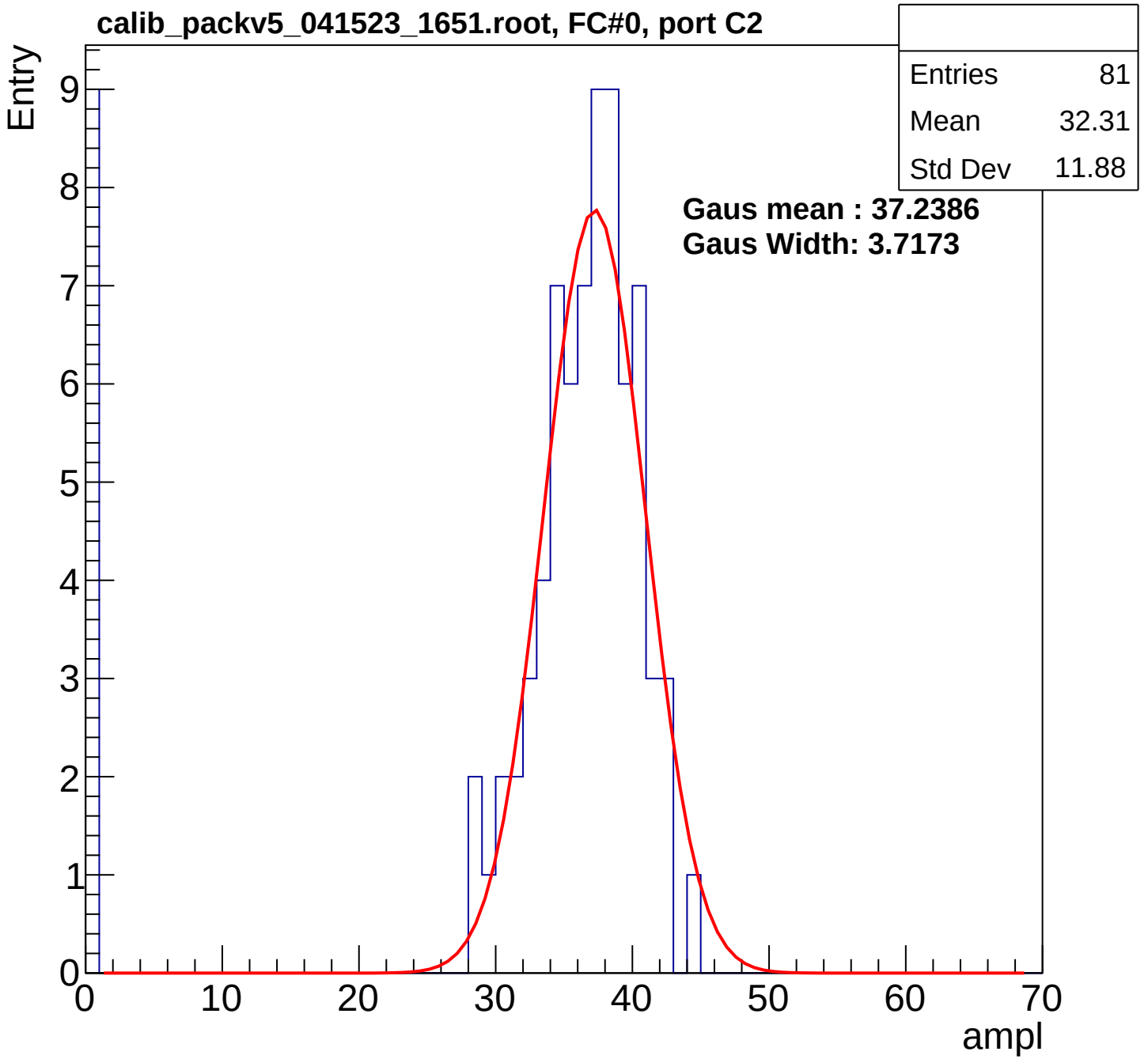

B1L103S, U23-ch14, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 55
Mean 55.33
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

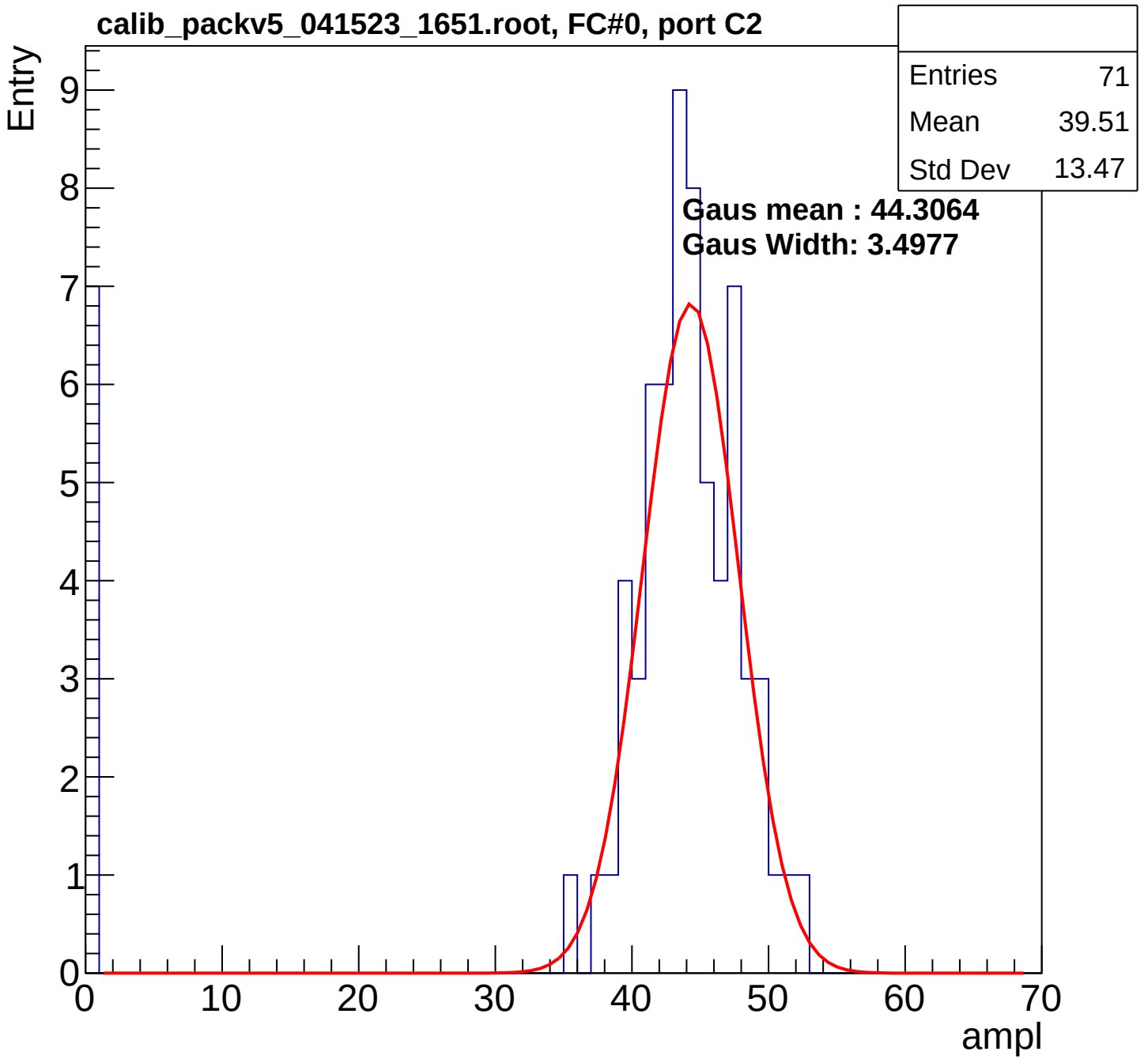

B1L103S, U23-ch14, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 58.79
Std Dev 8.879
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

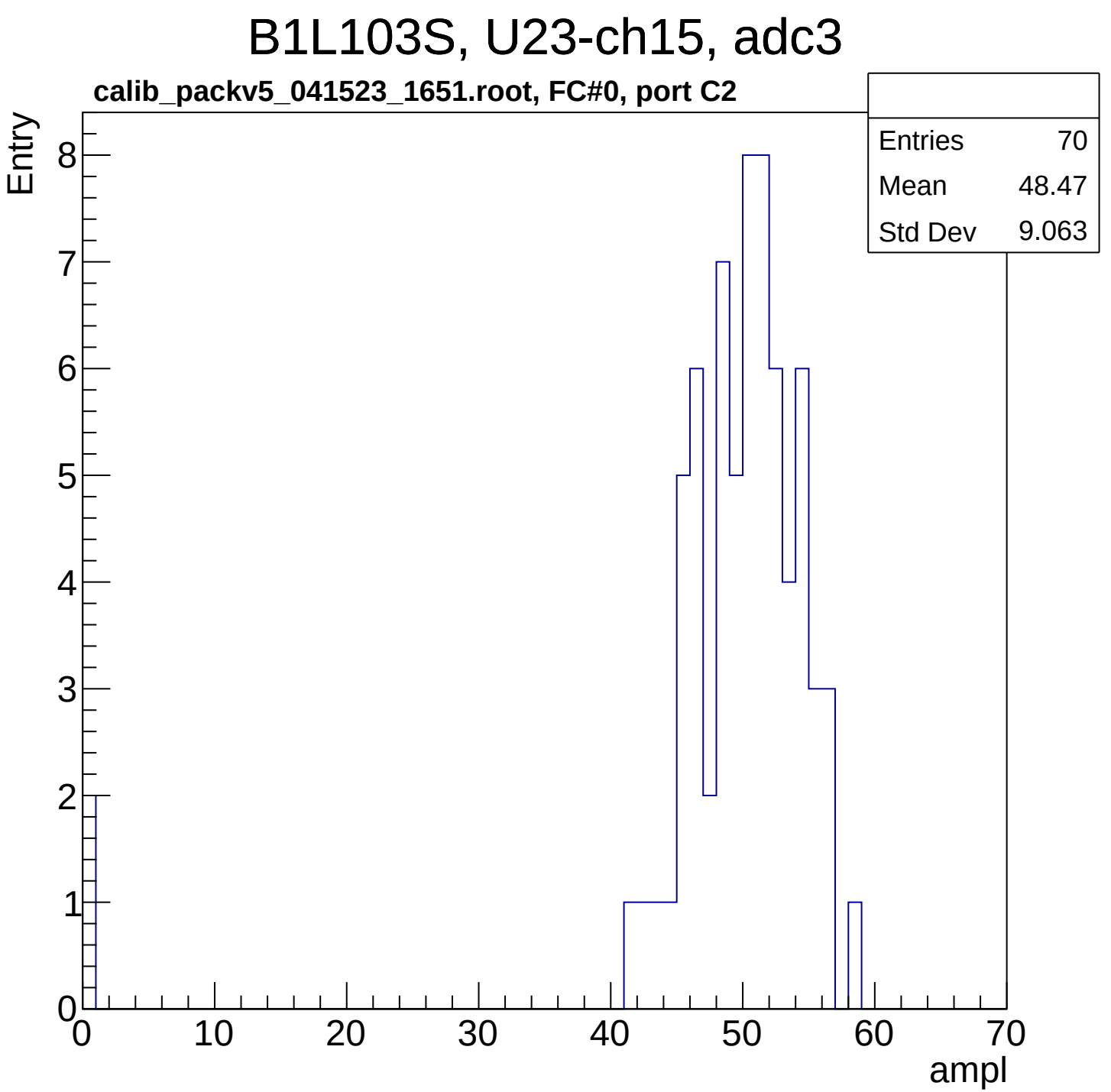

B1L103S, U23-ch14, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 61.67
Std Dev 1.374
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

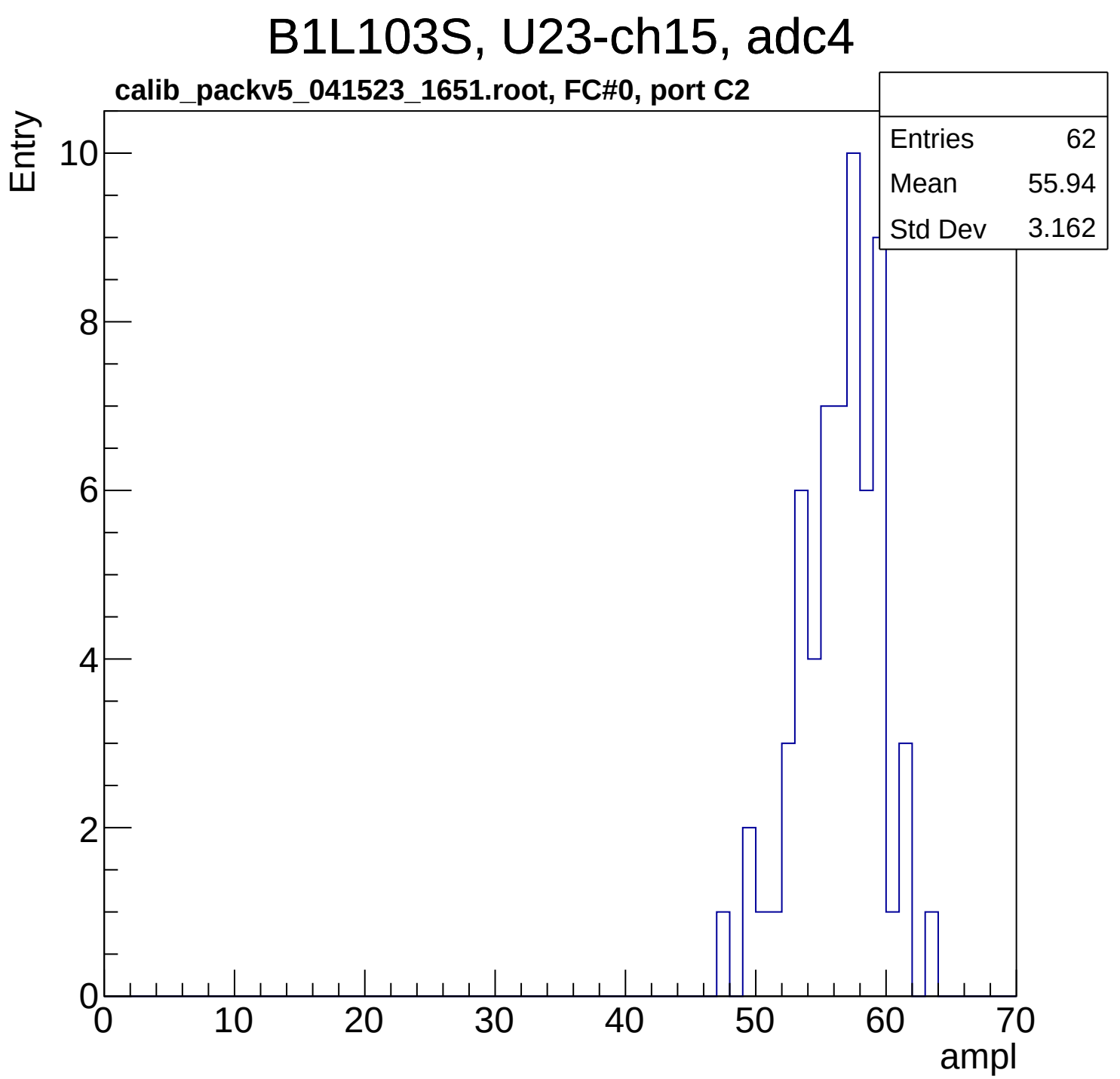

B1L103S, U23-ch14, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

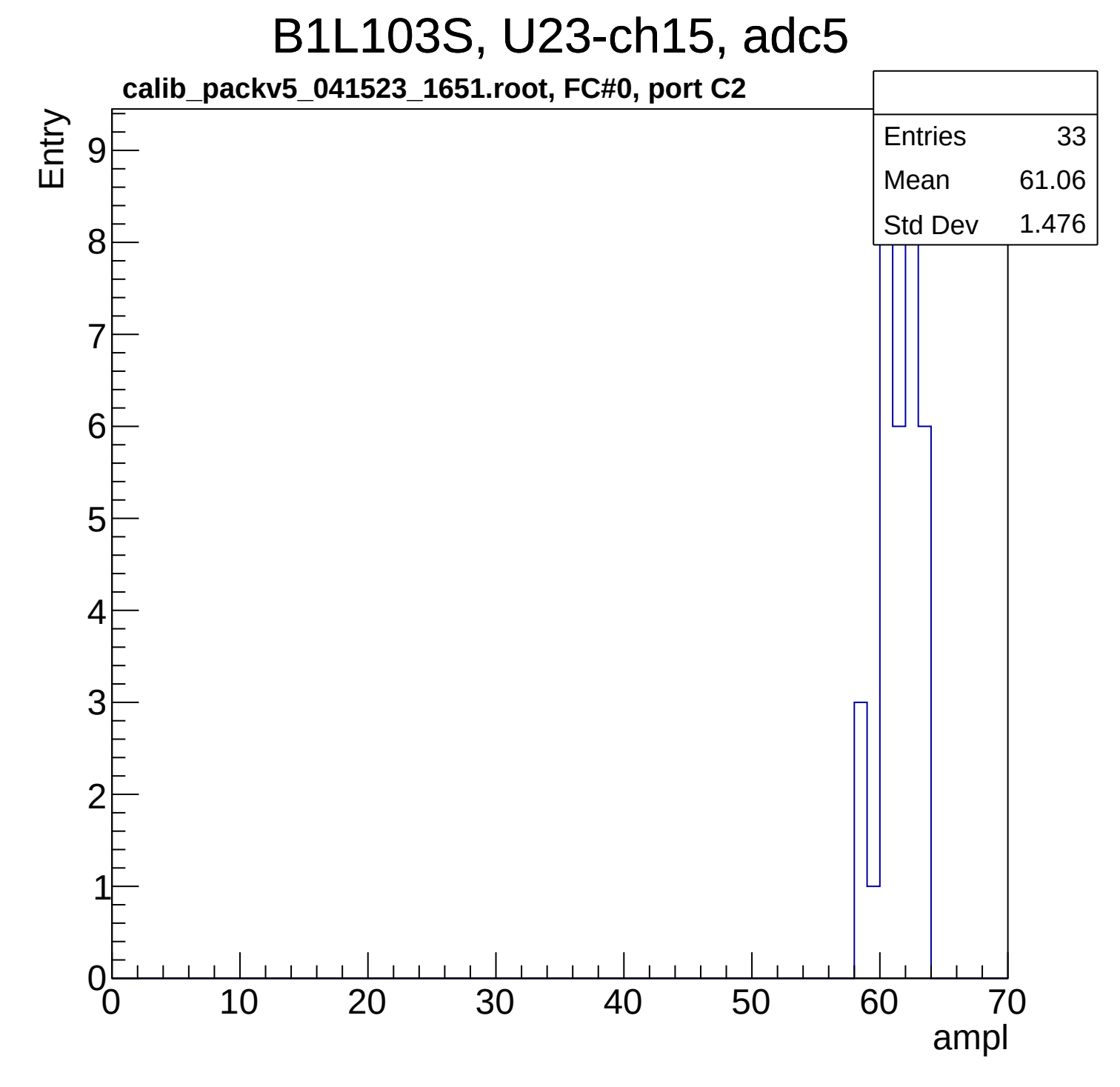

B1L103S, U23-ch15, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 23.01
Std Dev 13.21
Gaus mean : 30.8350
Gaus Width: 3.8165
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

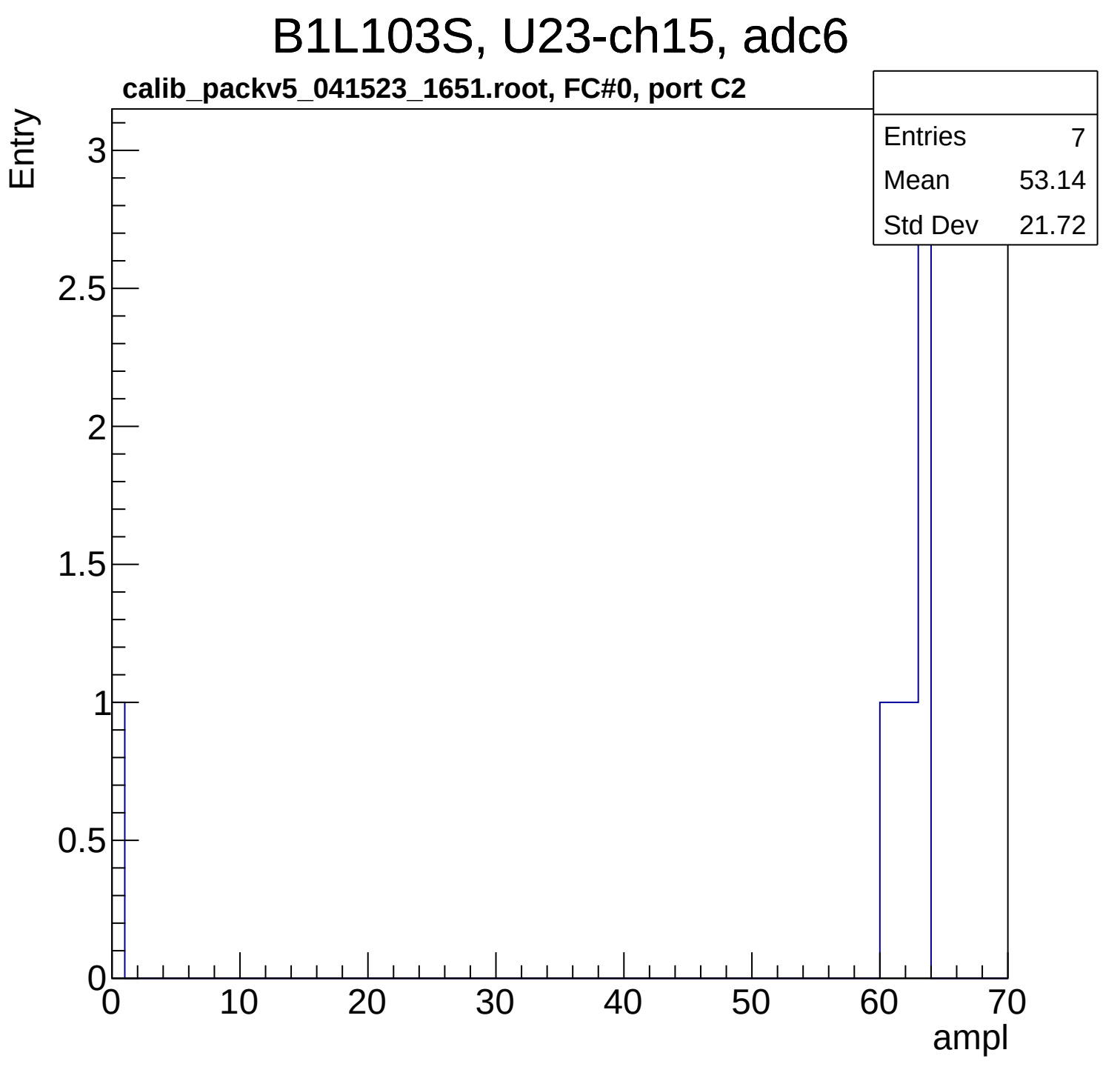

B1L103S, U23-ch15, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 32.31
Std Dev 11.88
Gaus mean : 37.2386
Gaus Width: 3.7173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

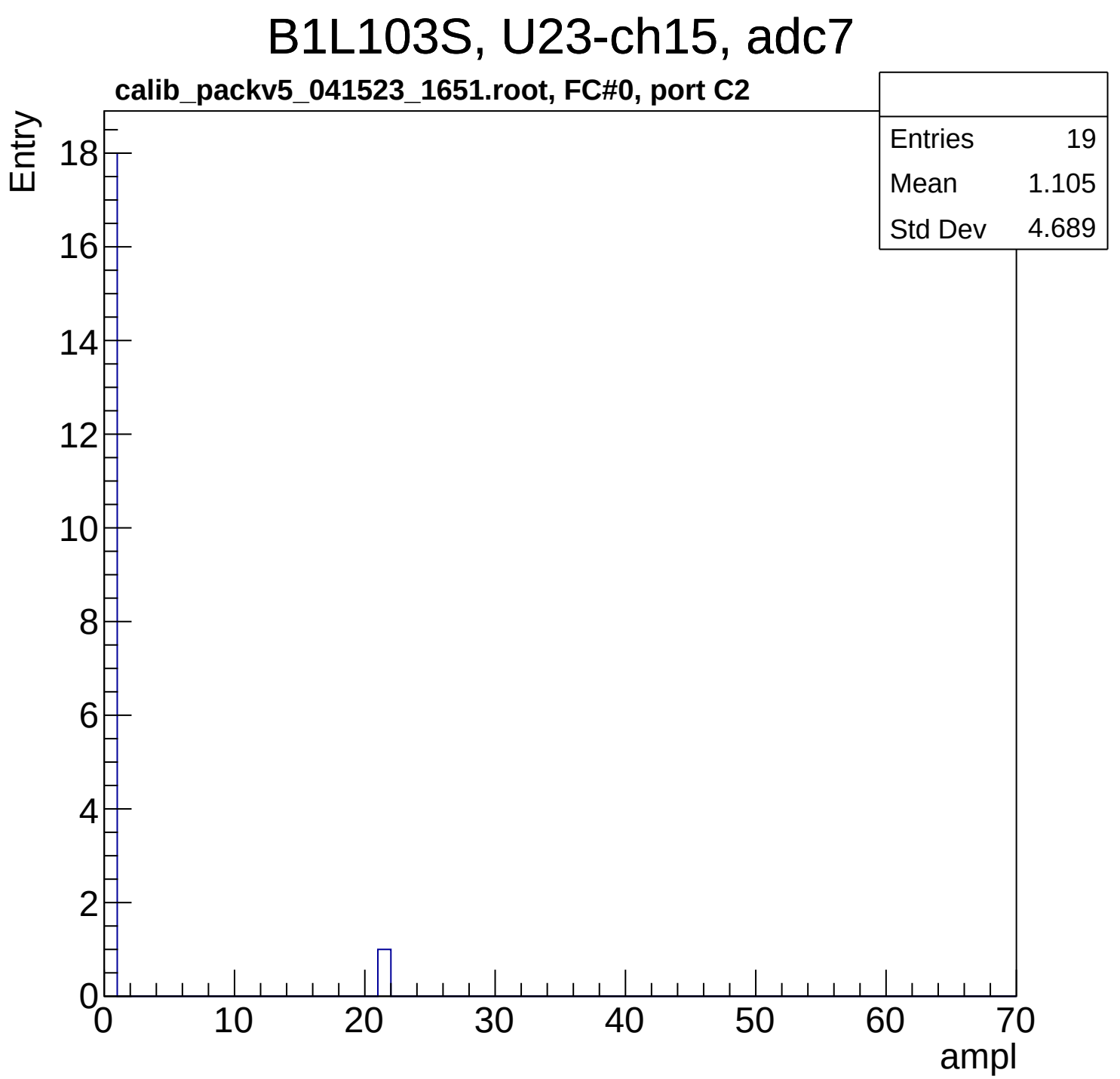

B1L103S, U23-ch15, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 39.51
Std Dev 13.47
Gaus mean : 44.3064
Gaus Width: 3.4977
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

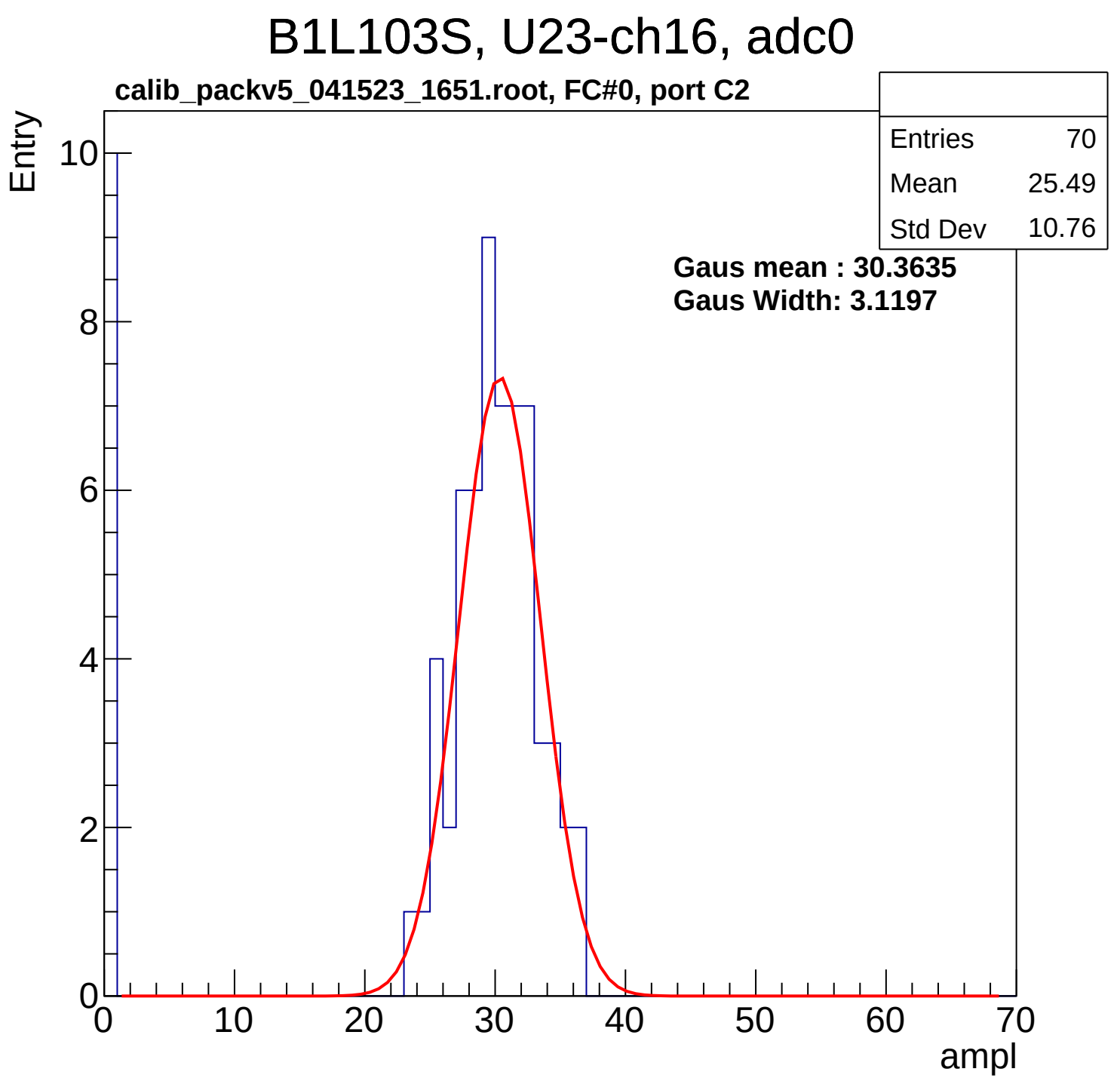

B1L103S, U23-ch15, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 48.47
Std Dev 9.063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

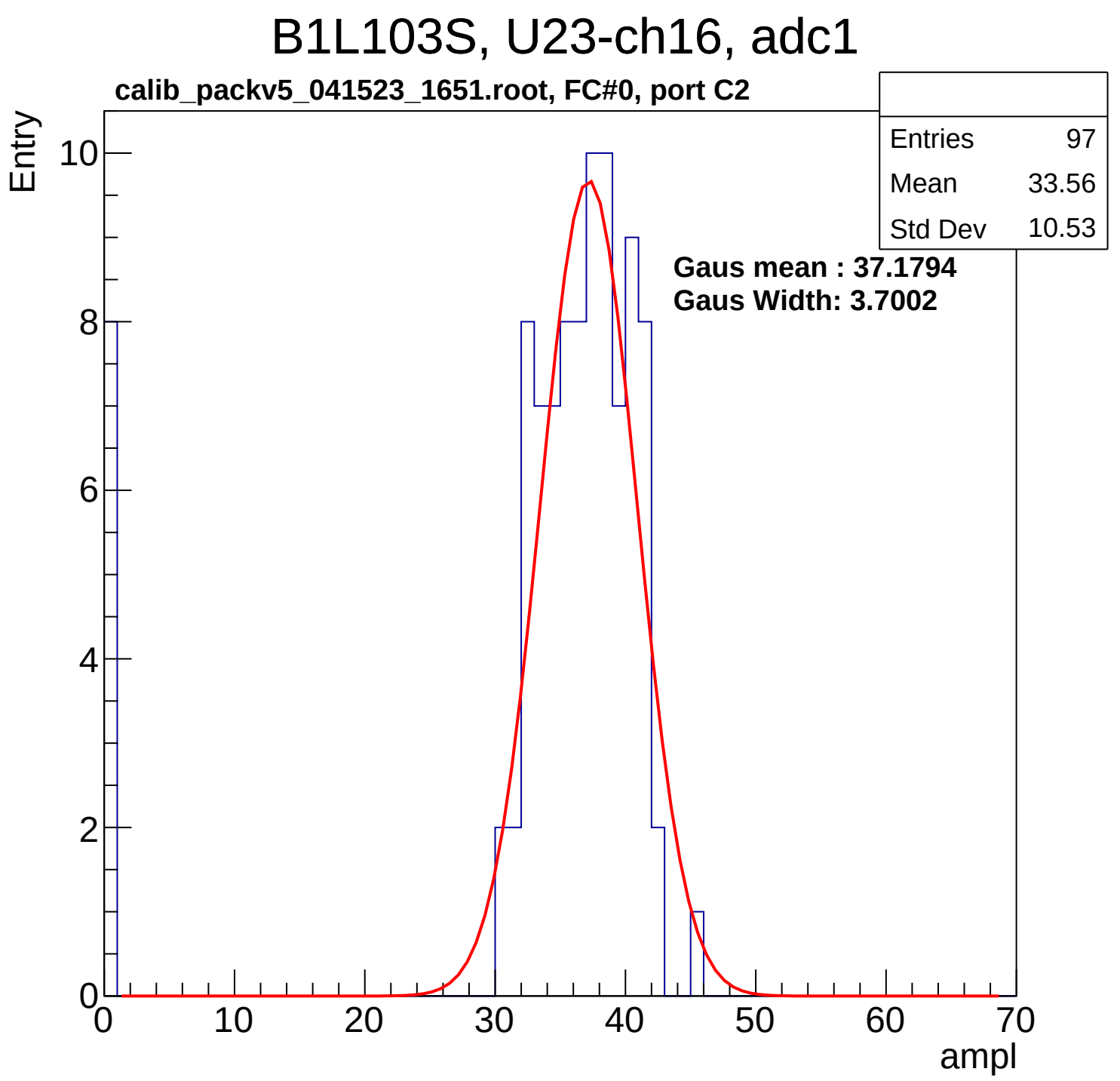

B1L103S, U23-ch15, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 55.94
Std Dev 3.162
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

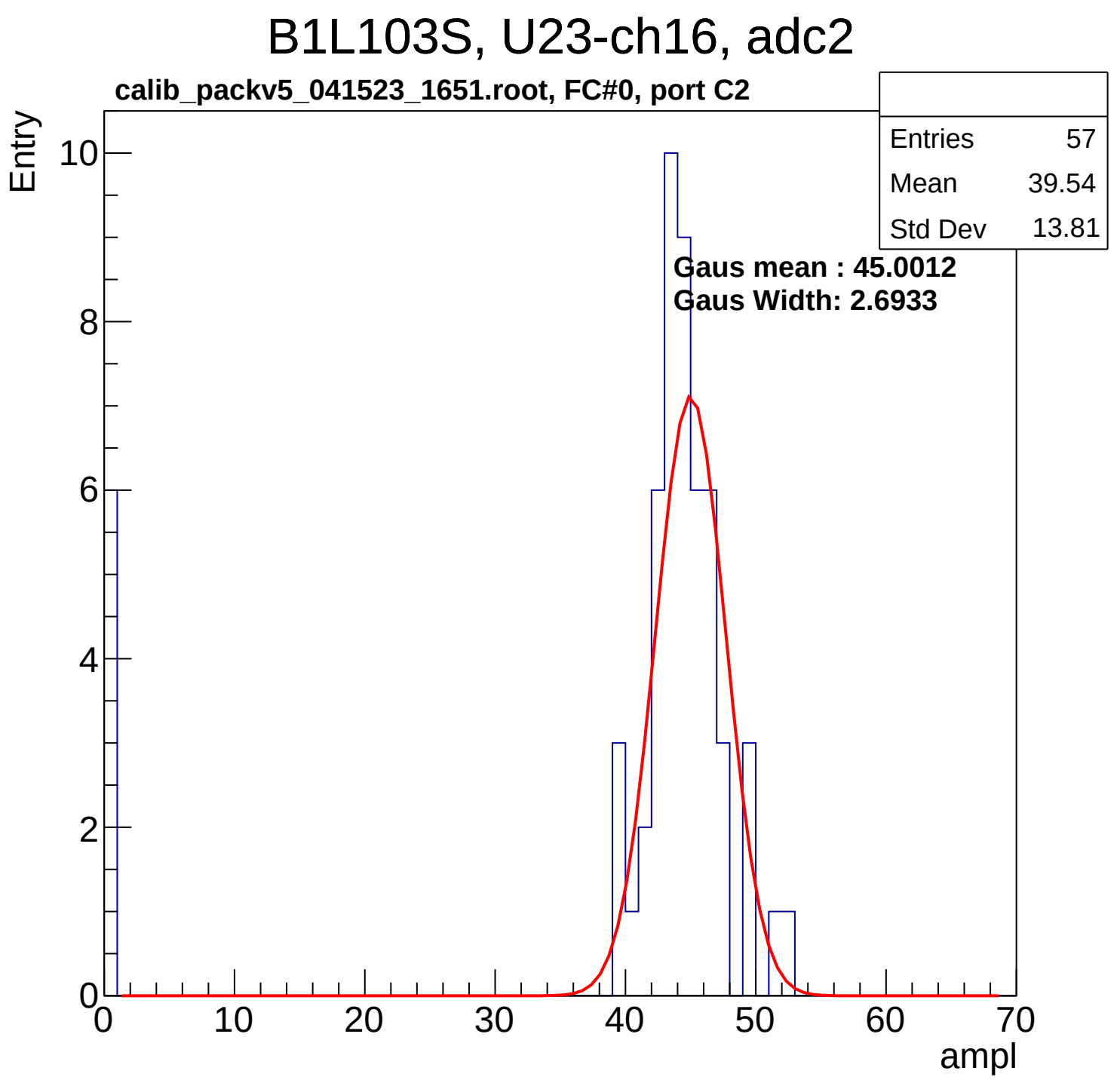

B1L103S, U23-ch15, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 33
Mean 61.06
Std Dev 1.476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

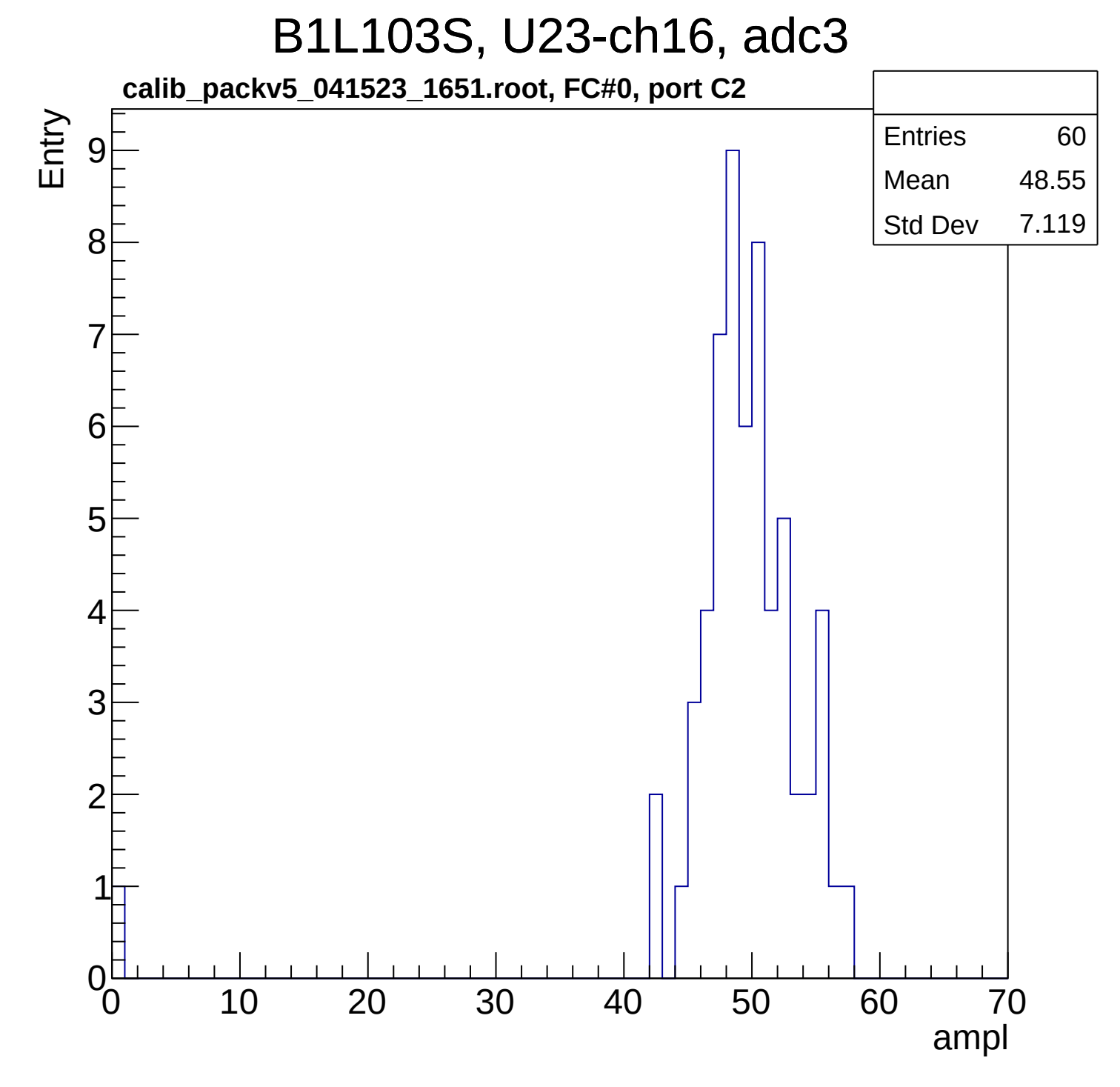

B1L103S, U23-ch15, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 7
Mean 53.14
Std Dev 21.72
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

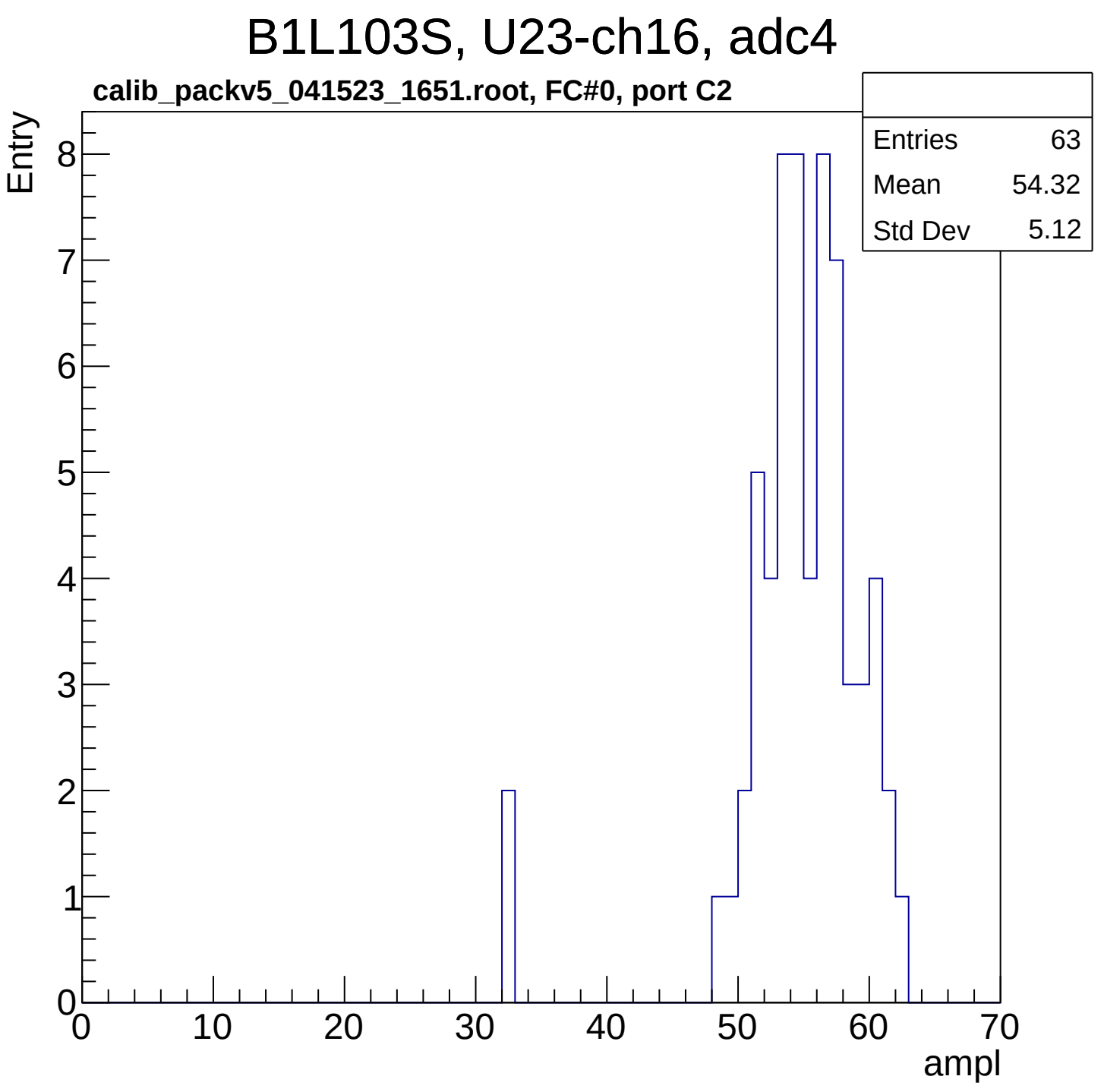

B1L103S, U23-ch15, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.105
Std Dev 4.689
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

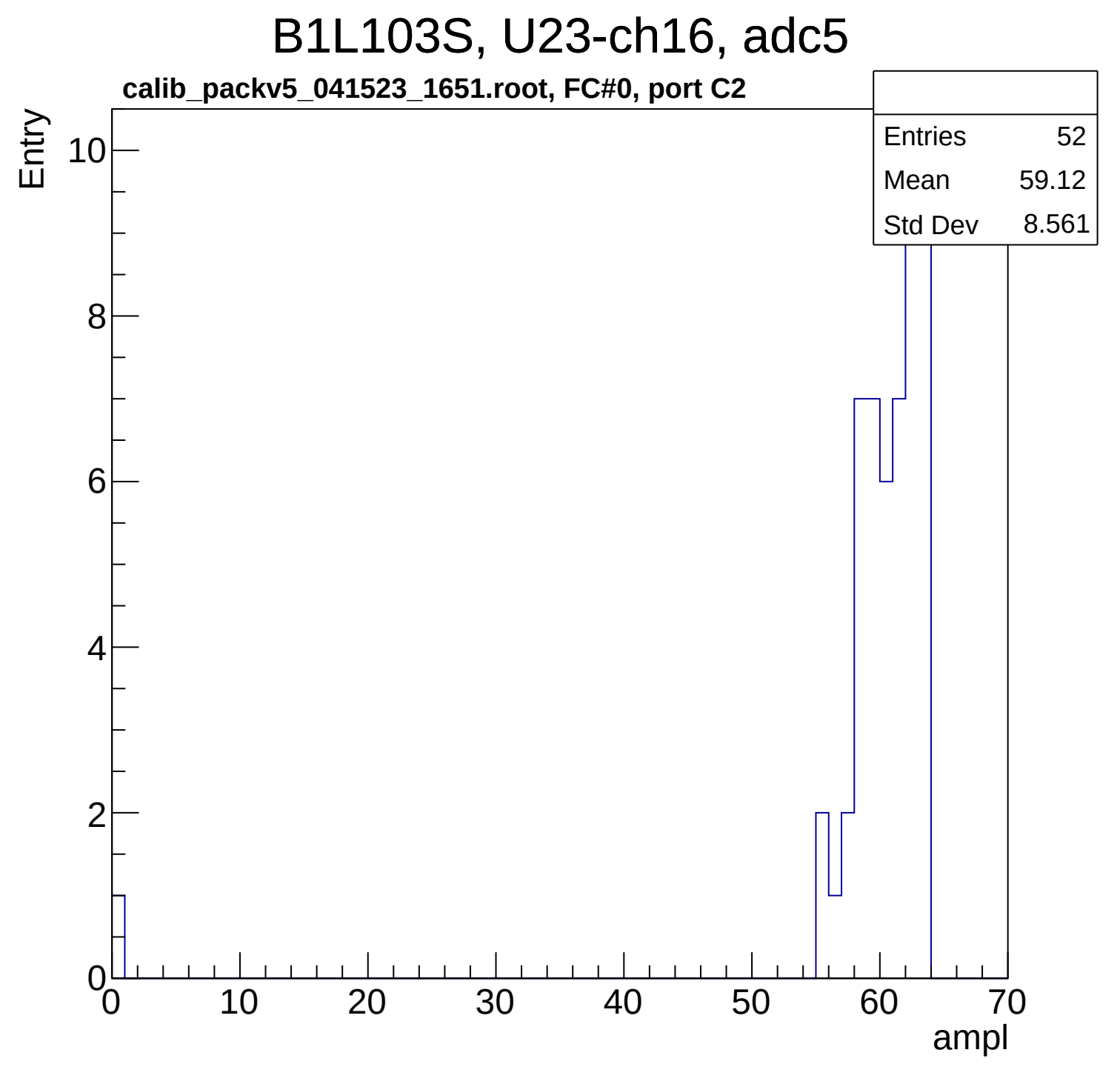

B1L103S, U23-ch16, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 25.49
Std Dev 10.76
Gaus mean : 30.3635
Gaus Width: 3.1197
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

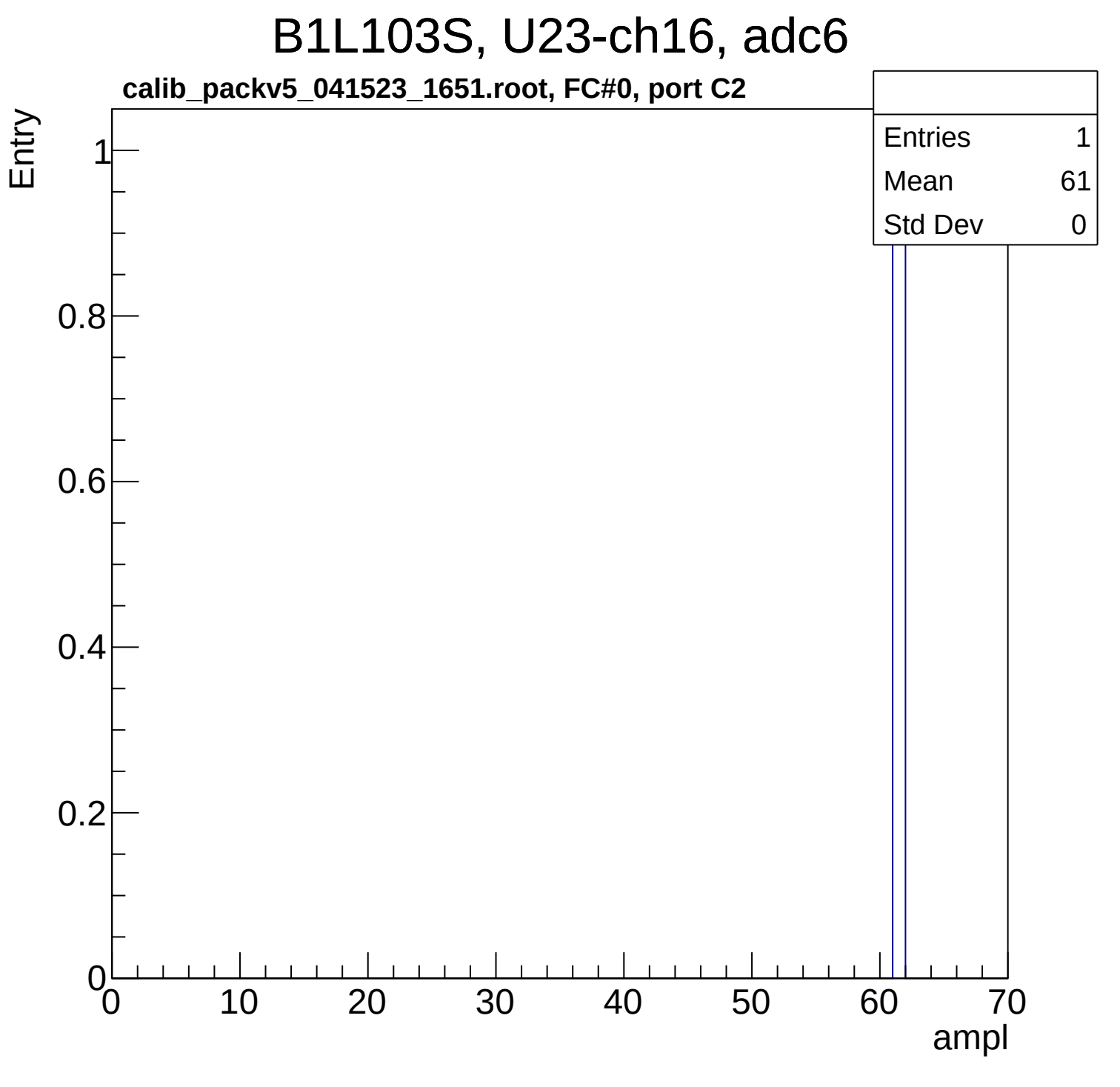

B1L103S, U23-ch16, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 97
Mean 33.56
Std Dev 10.53
Gaus mean : 37.1794
Gaus Width: 3.7002
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

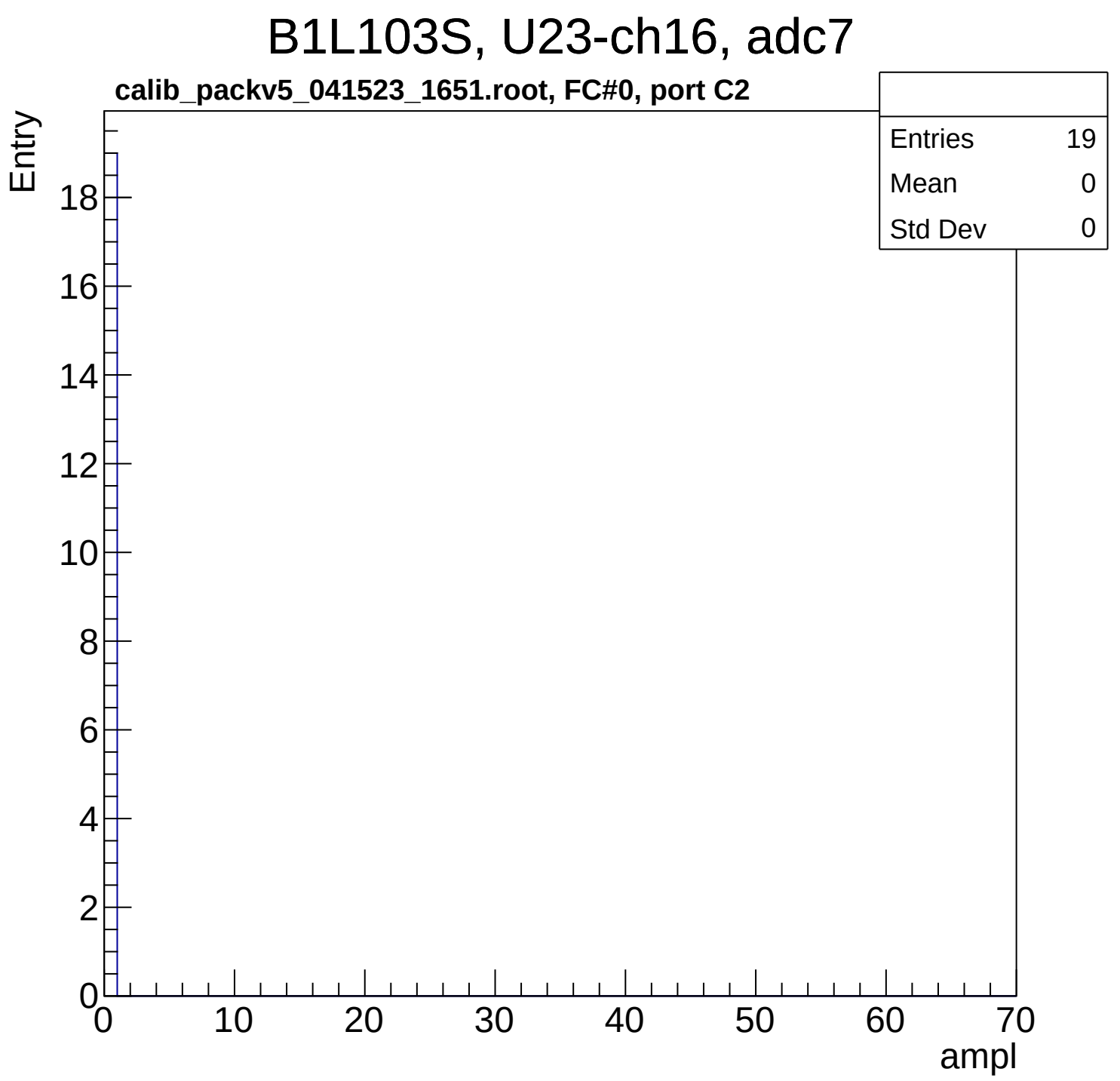

B1L103S, U23-ch16, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 39.54
Std Dev 13.81
Gaus mean : 45.0012
Gaus Width: 2.6933
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

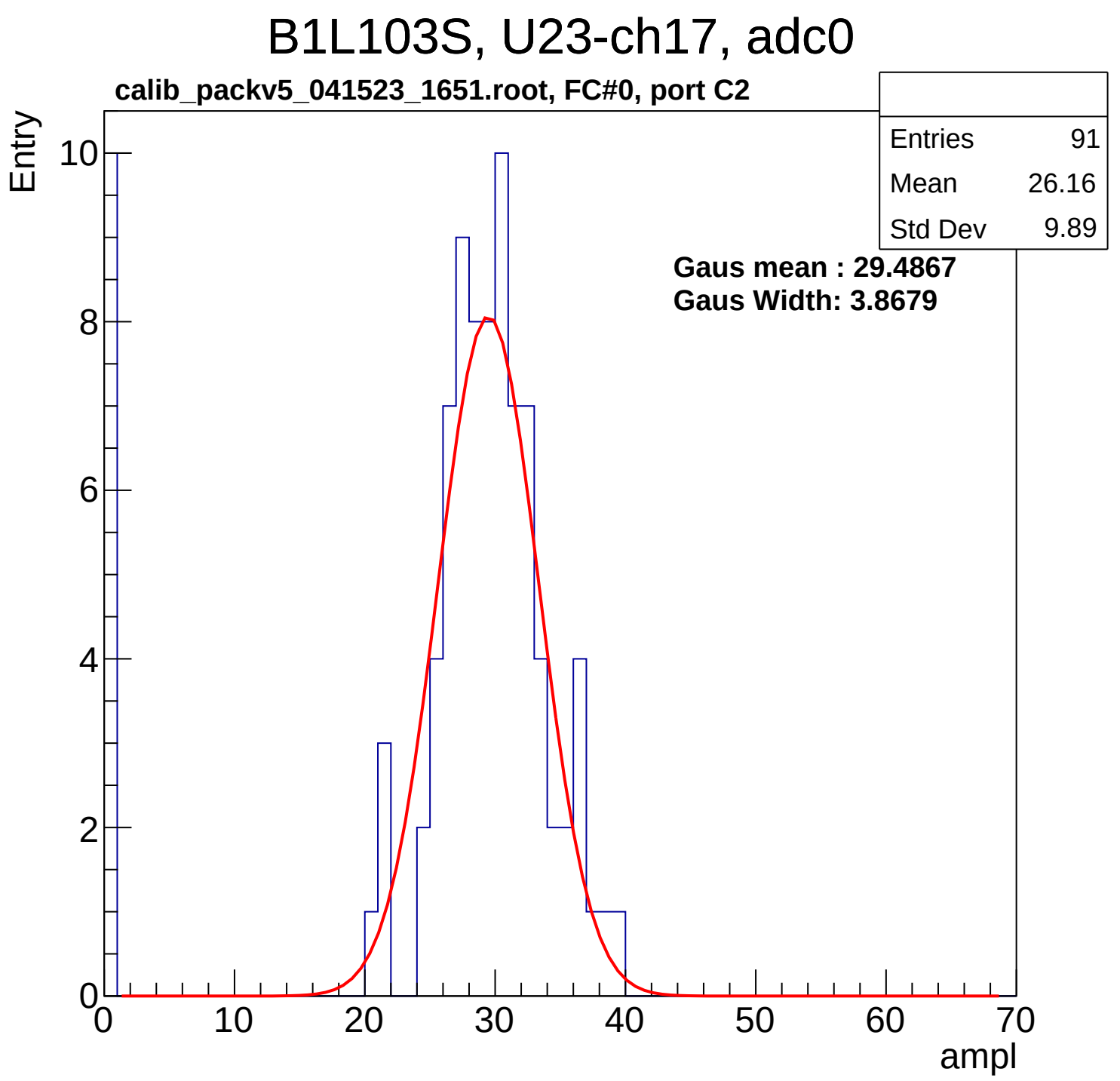

B1L103S, U23-ch16, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 48.55
Std Dev 7.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

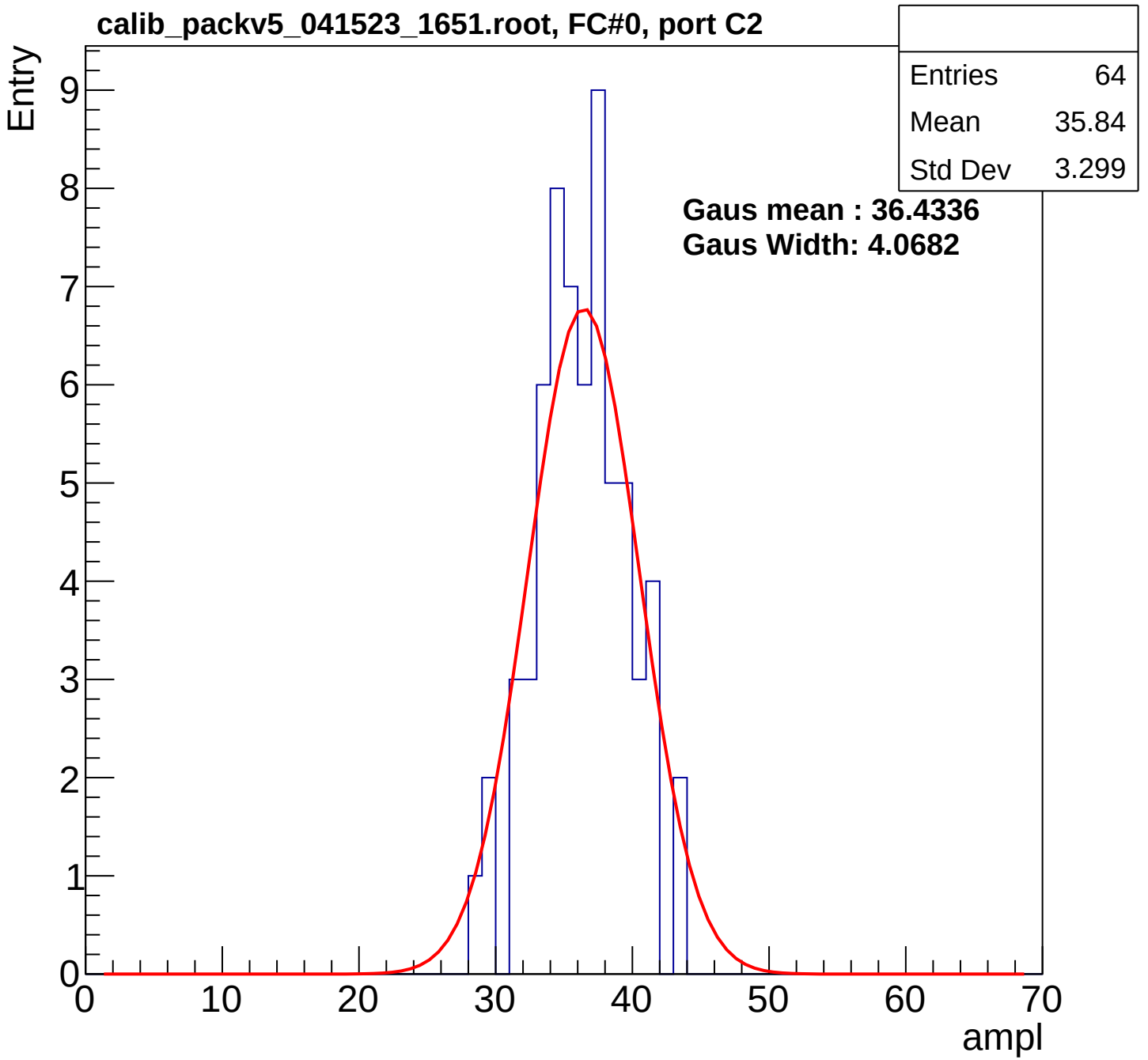

B1L103S, U23-ch16, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 54.32
Std Dev 5.12
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

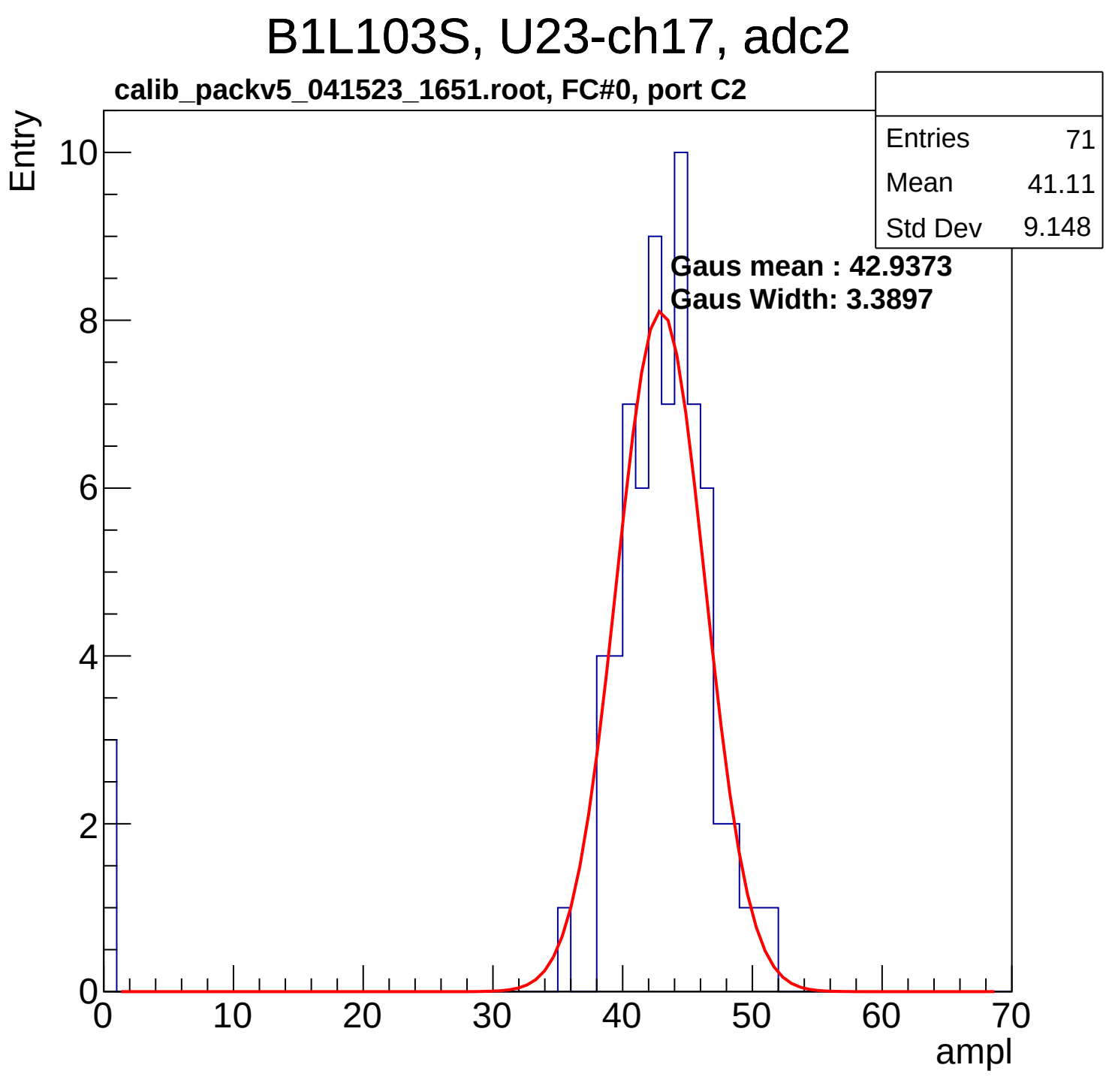

B1L103S, U23-ch16, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 59.12
Std Dev 8.561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

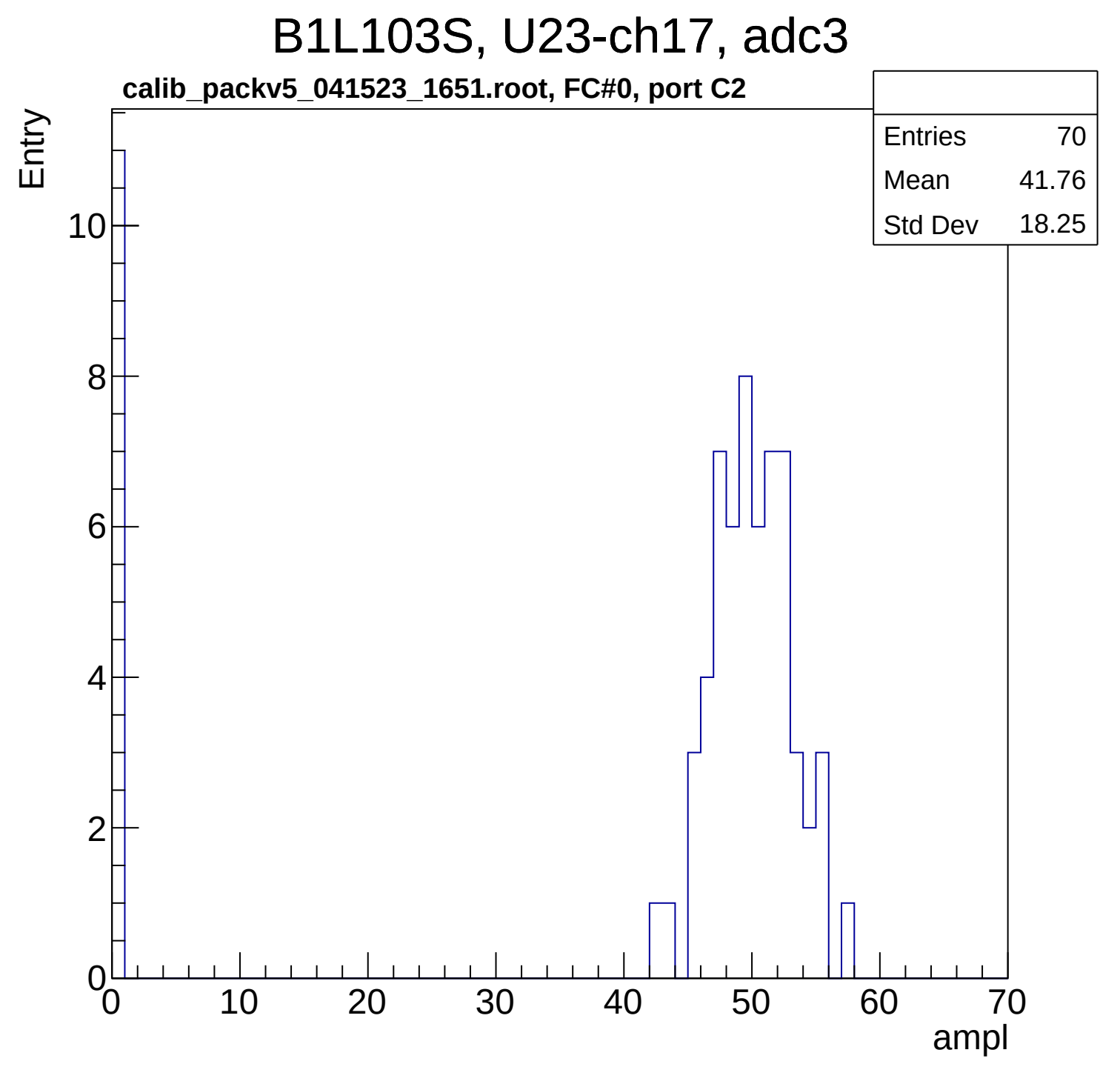

B1L103S, U23-ch16, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 1
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

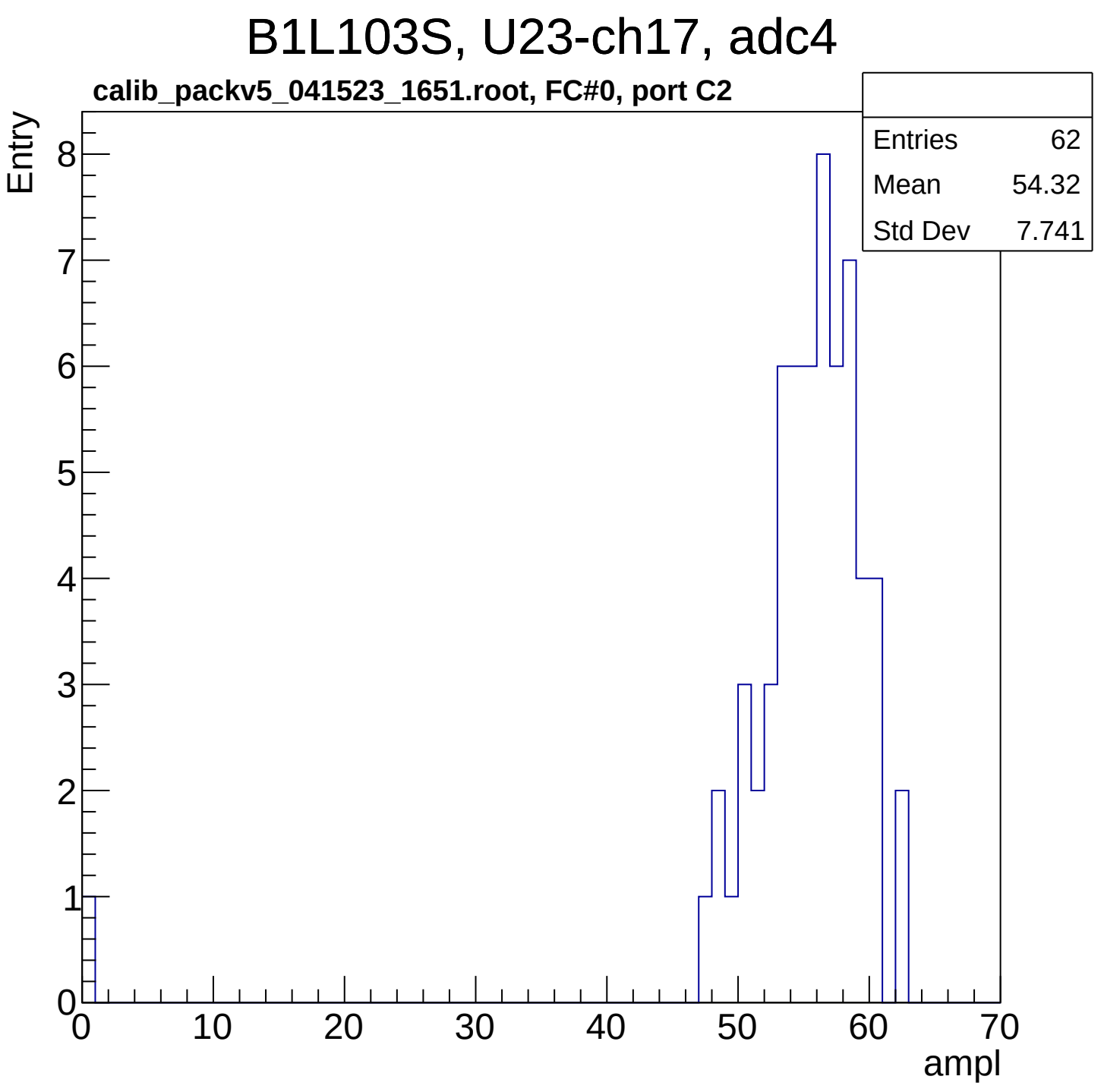

B1L103S, U23-ch16, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

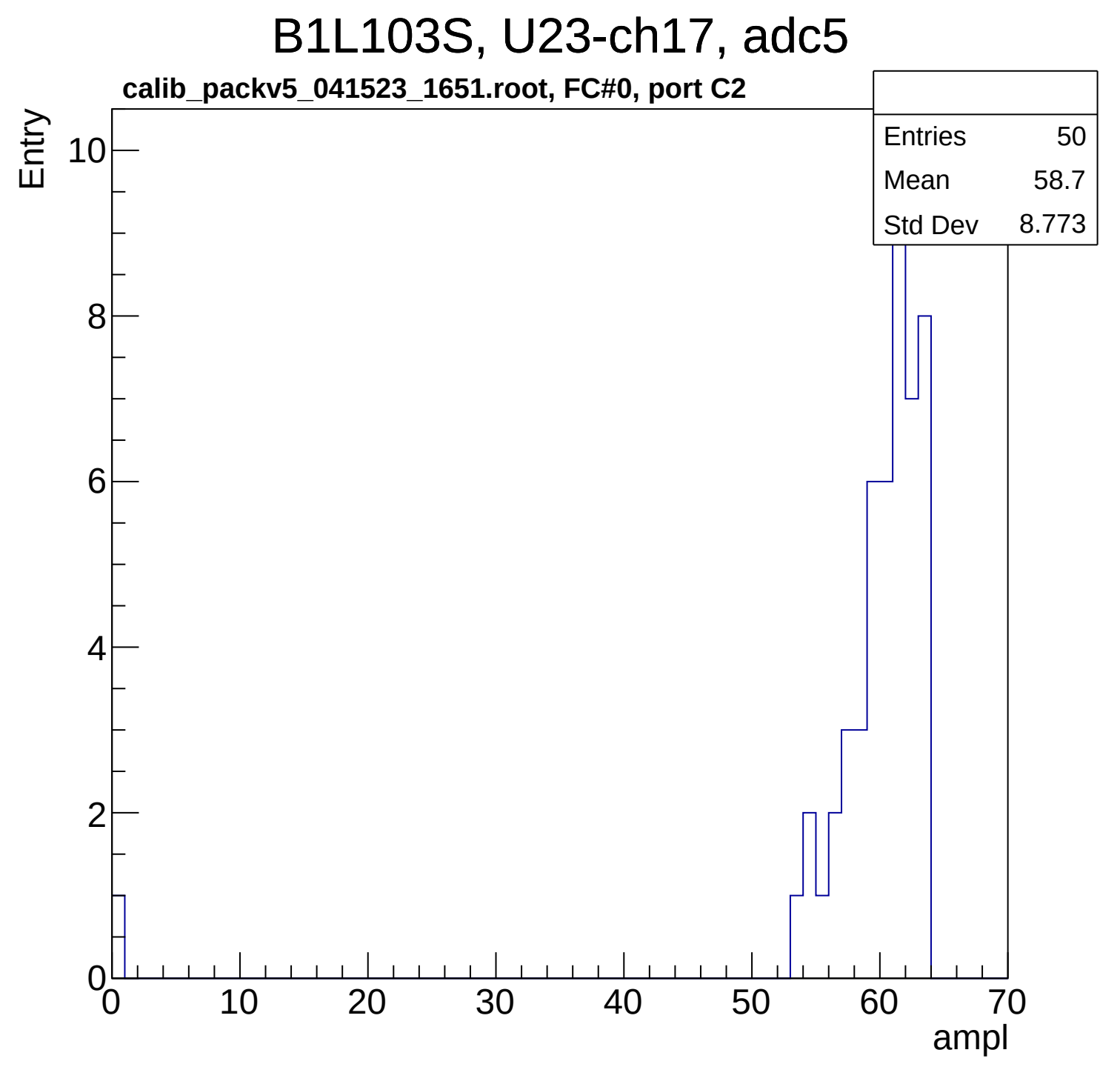

B1L103S, U23-ch17, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 26.16
Std Dev 9.89
Gaus mean : 29.4867
Gaus Width: 3.8679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

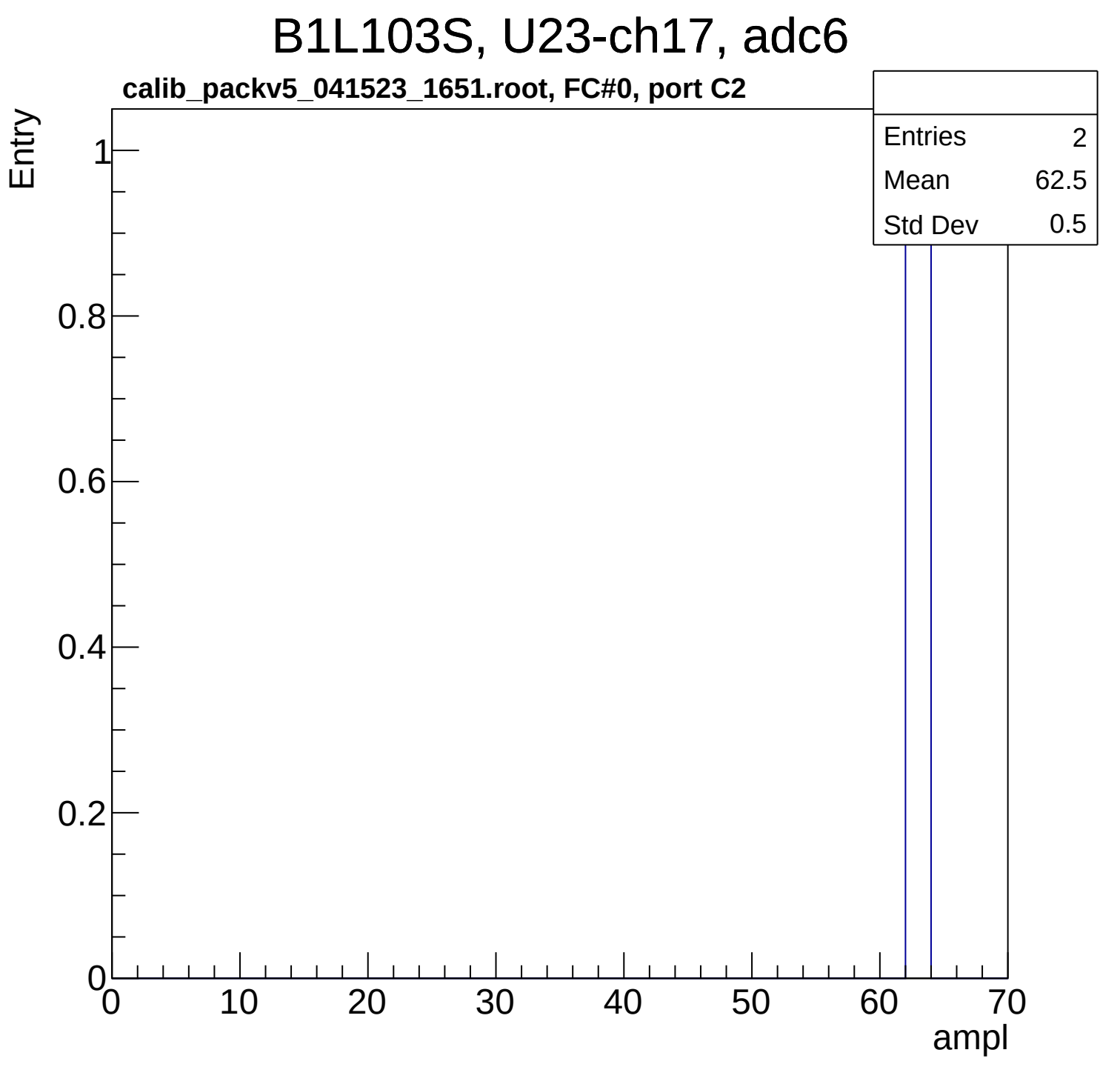

B1L103S, U23-ch17, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 35.84
Std Dev 3.299
Gaus mean : 36.4336
Gaus Width: 4.0682
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

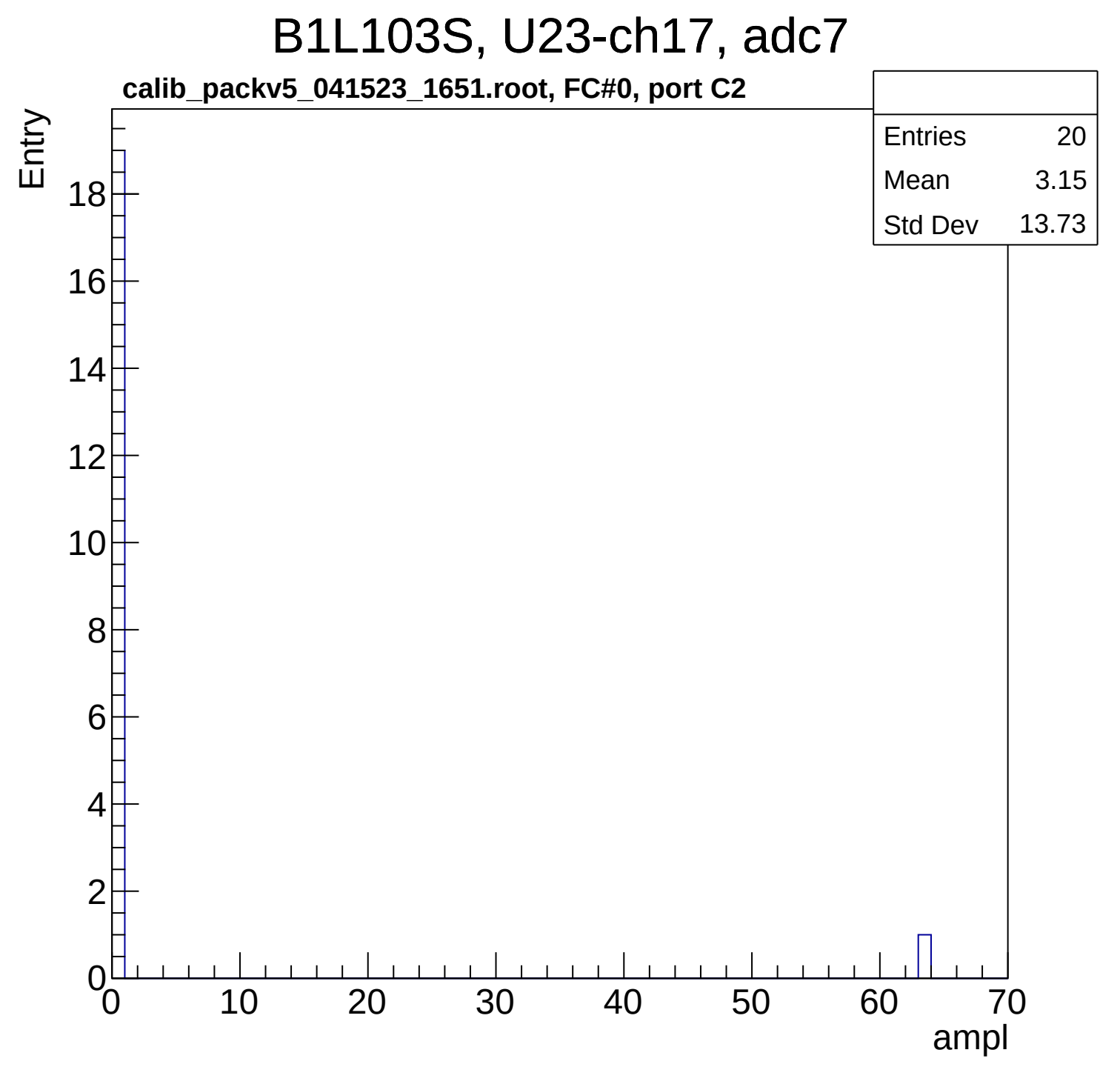

B1L103S, U23-ch17, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 41.11
Std Dev 9.148
Gaus mean : 42.9373
Gaus Width: 3.3897
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

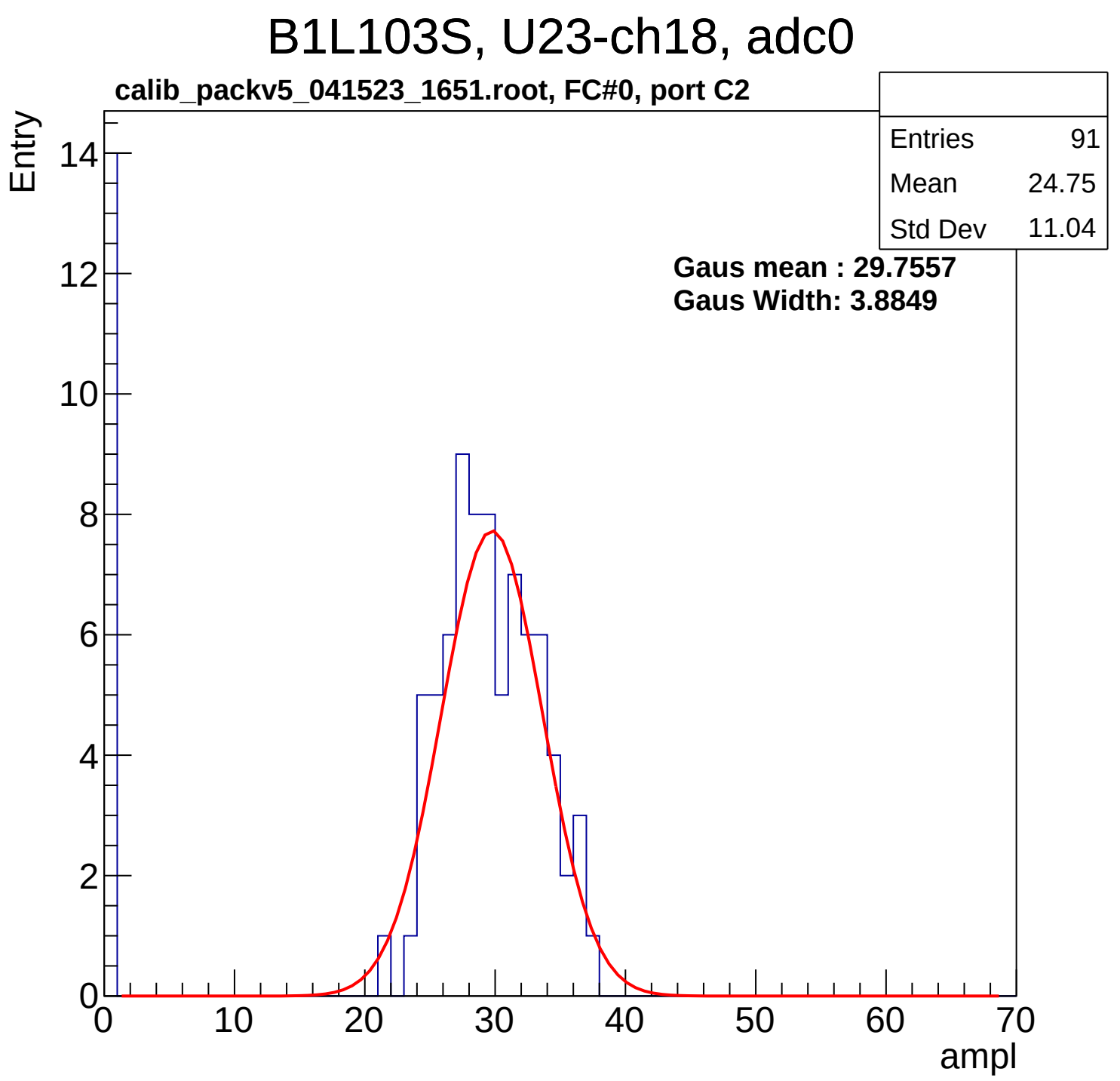

B1L103S, U23-ch17, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 41.76
Std Dev 18.25
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

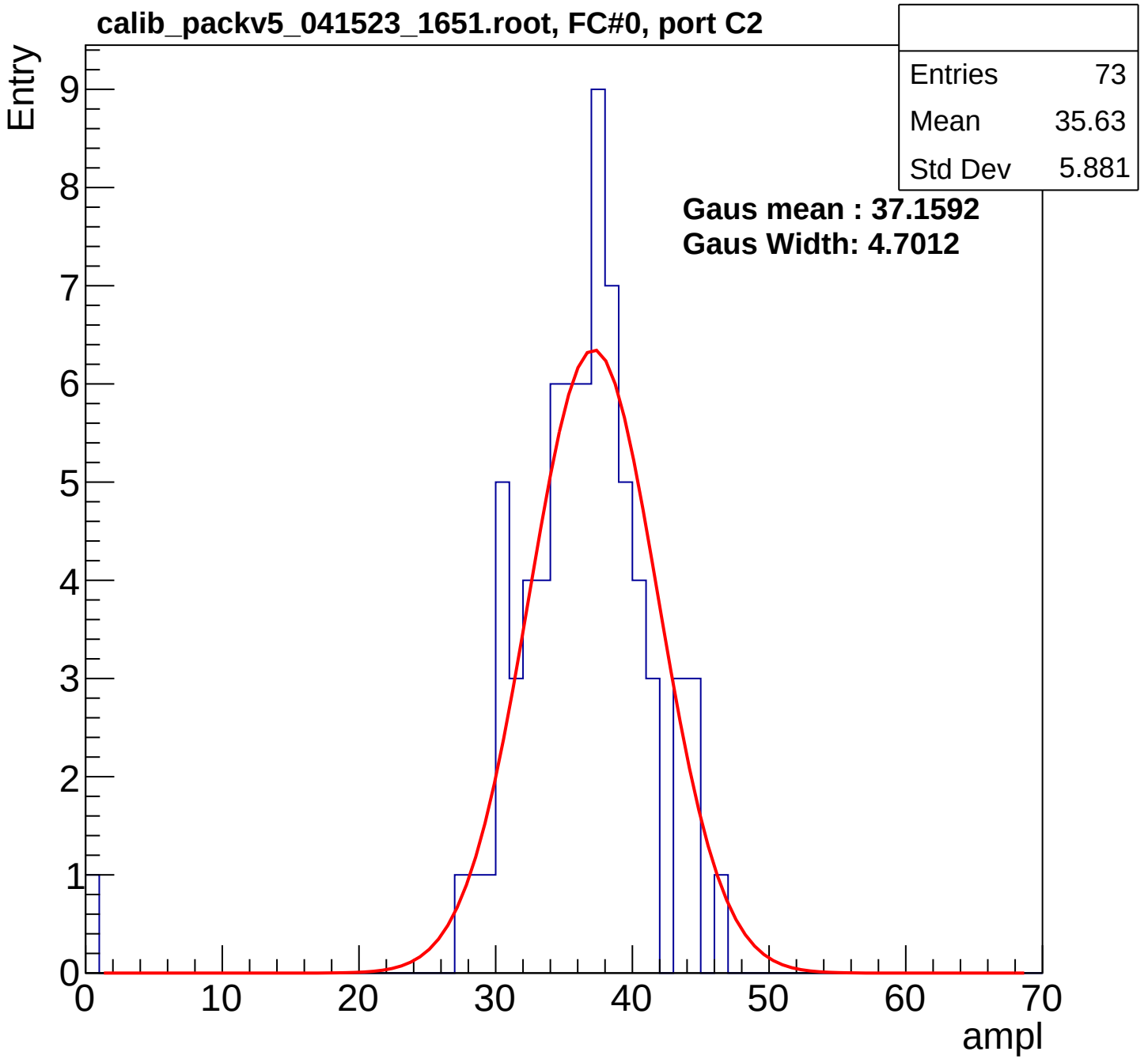

B1L103S, U23-ch17, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 54.32
Std Dev 7.741
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

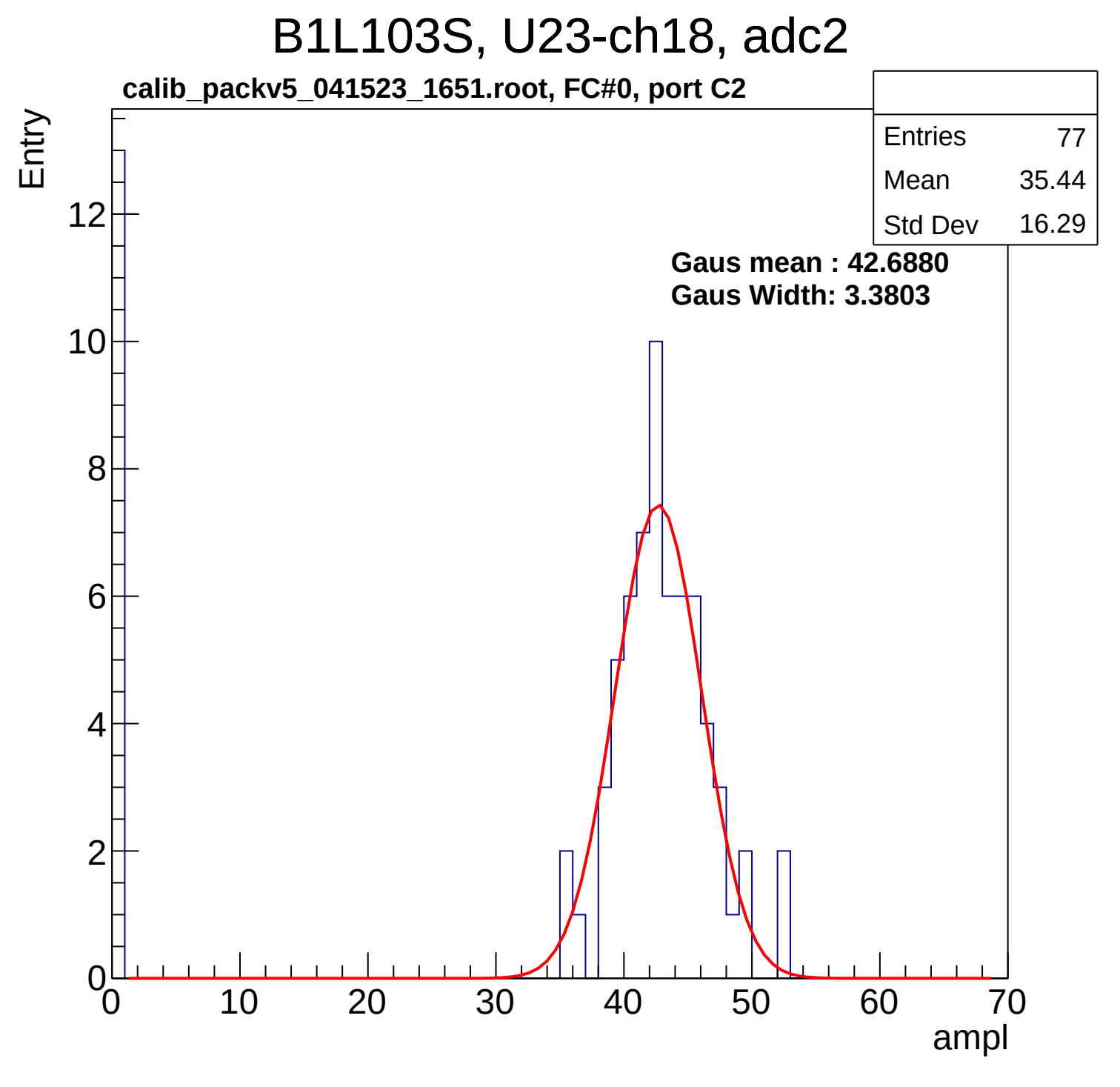

B1L103S, U23-ch17, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 50
Mean 58.7
Std Dev 8.773
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

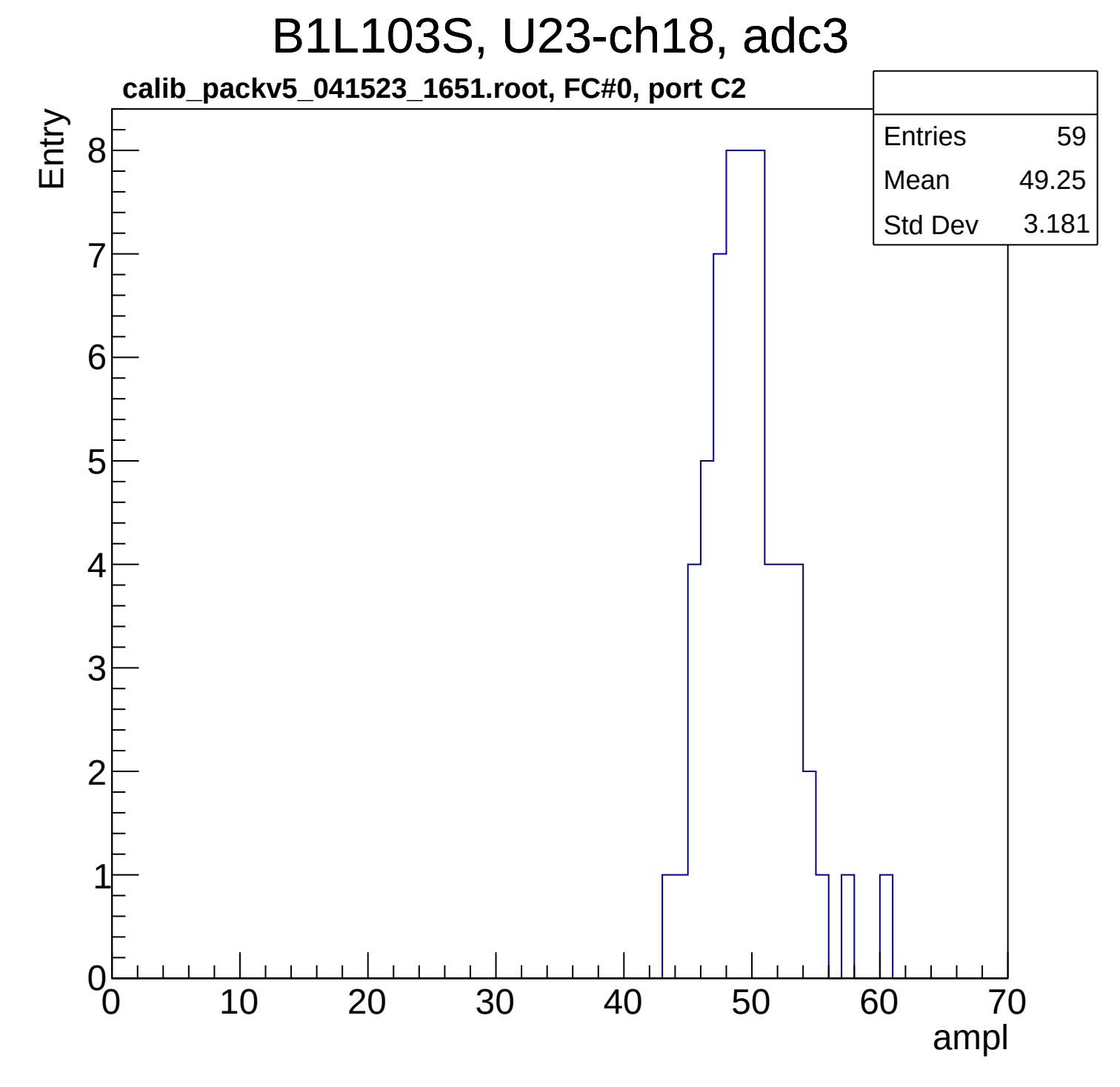

B1L103S, U23-ch17, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

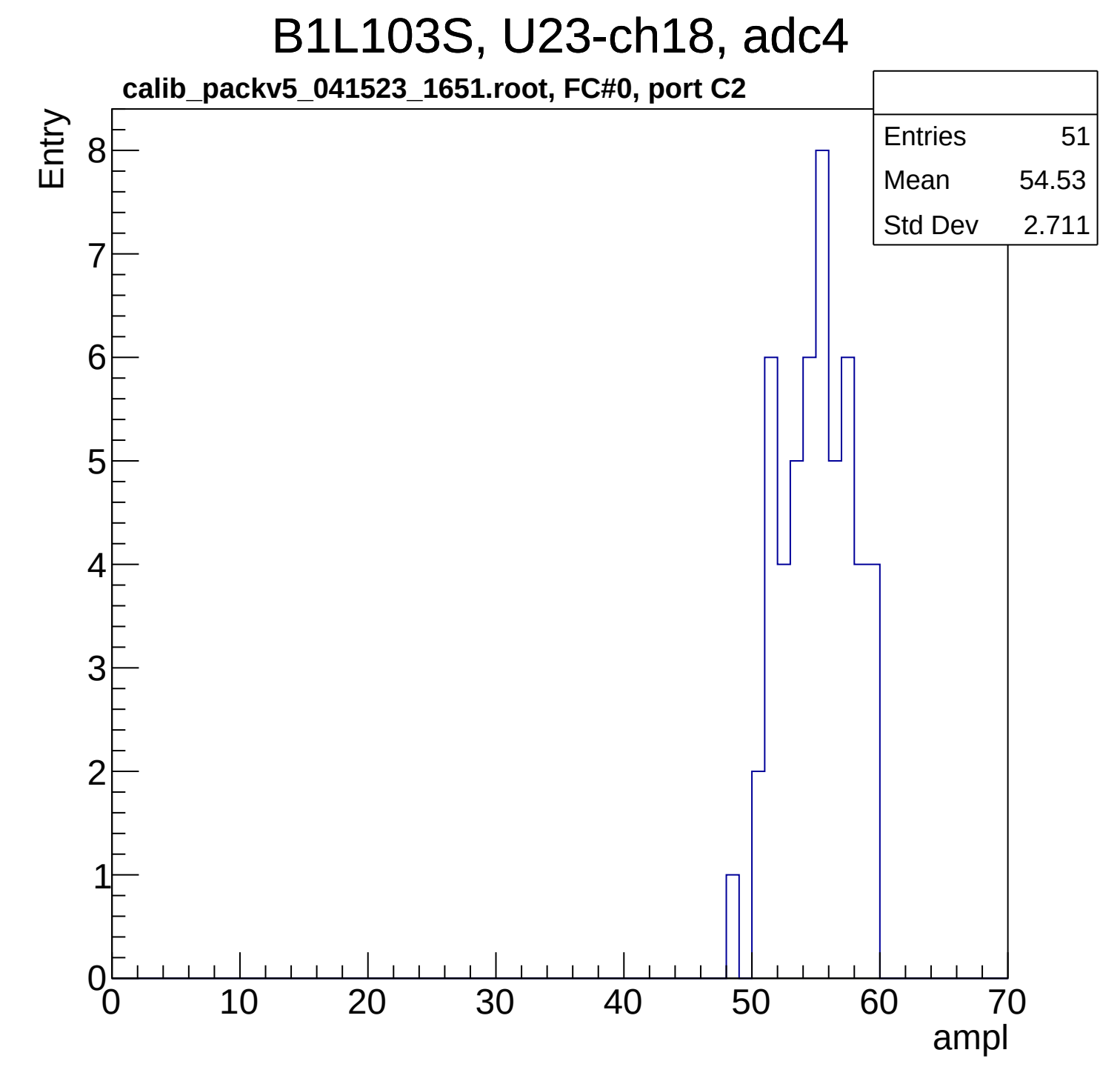

B1L103S, U23-ch17, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.15
Std Dev 13.73
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

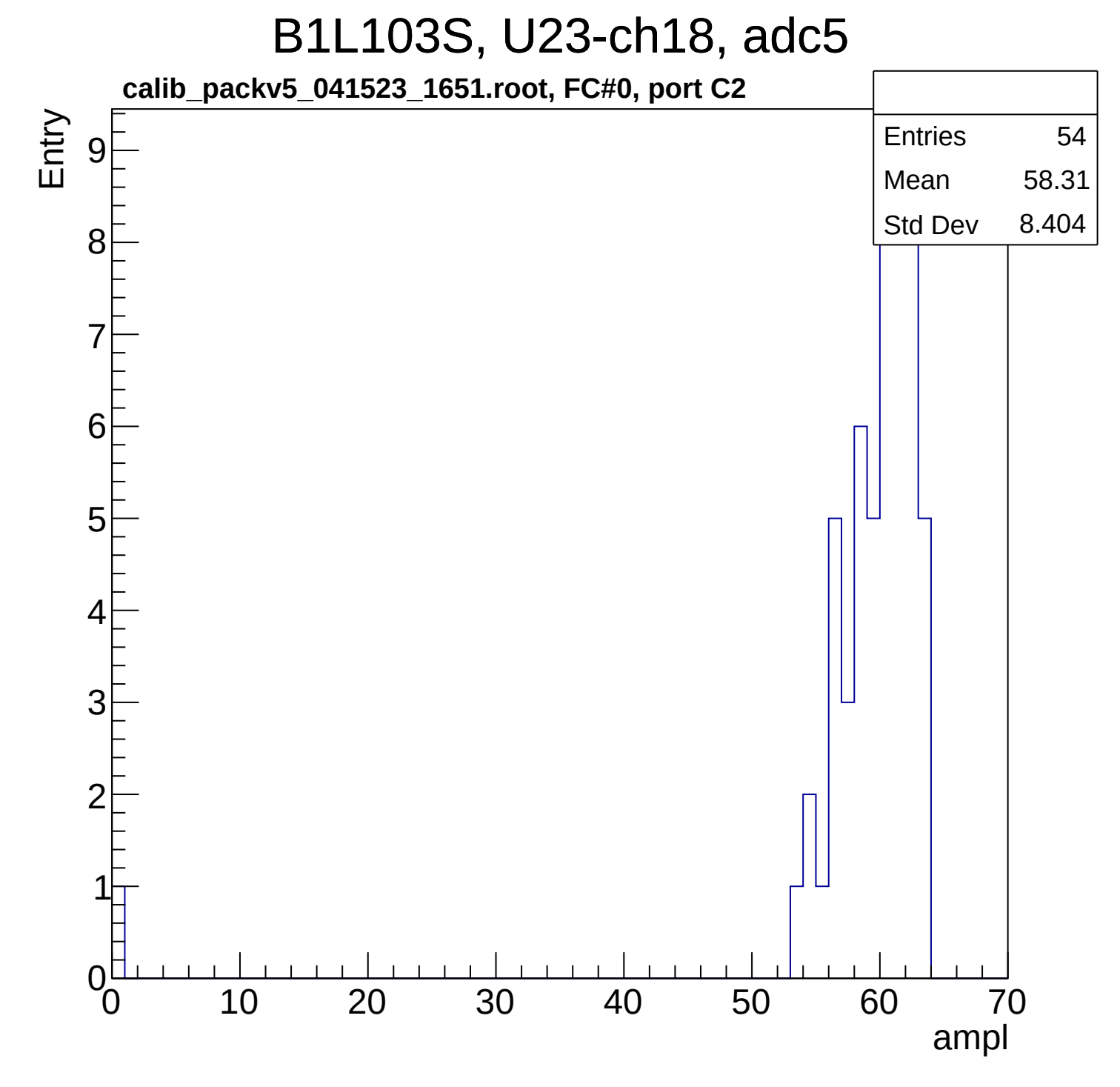

B1L103S, U23-ch18, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 24.75
Std Dev 11.04
Gaus mean : 29.7557
Gaus Width: 3.8849
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

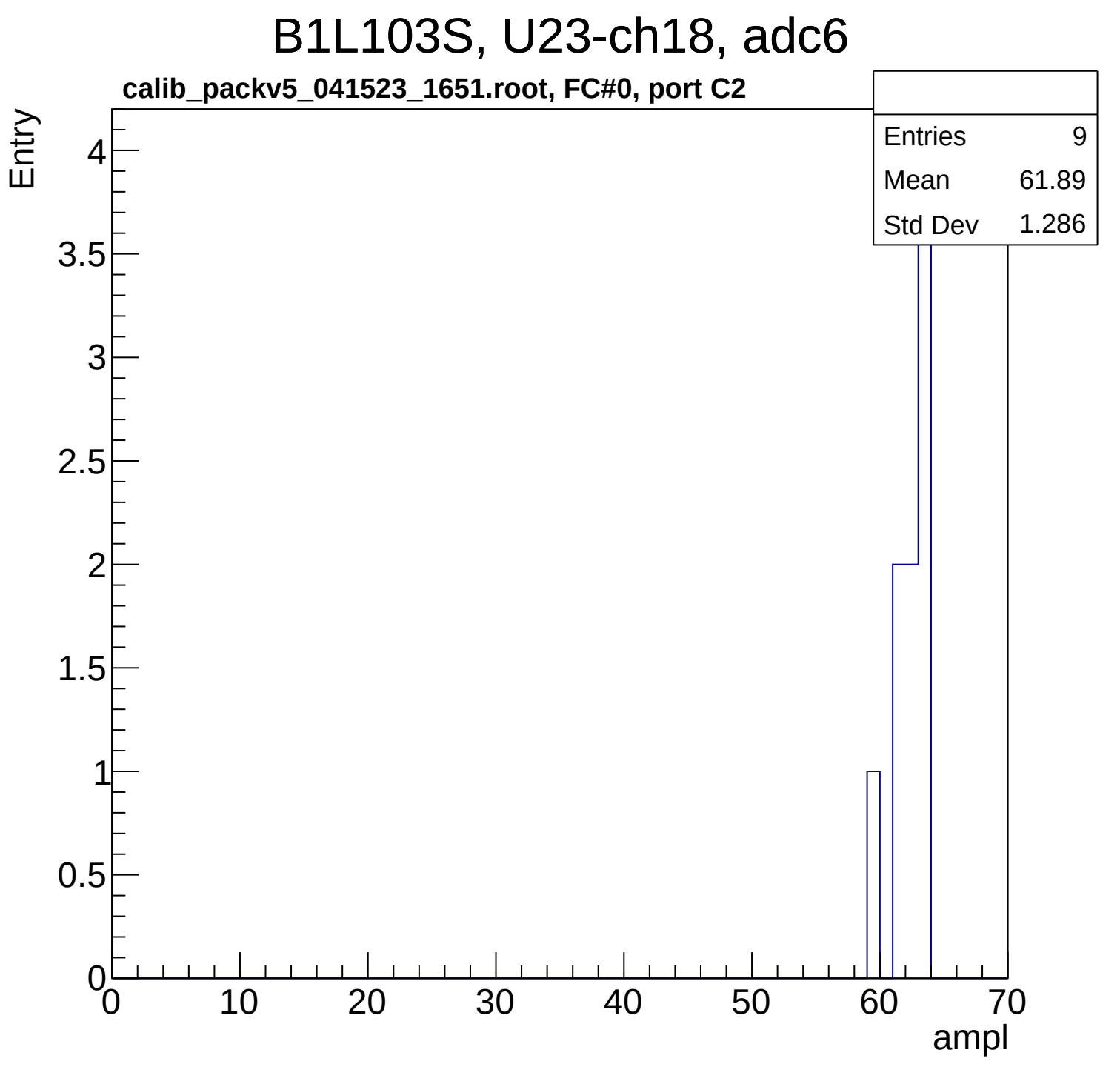

B1L103S, U23-ch18, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 35.63
Std Dev 5.881
Gaus mean : 37.1592
Gaus Width: 4.7012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

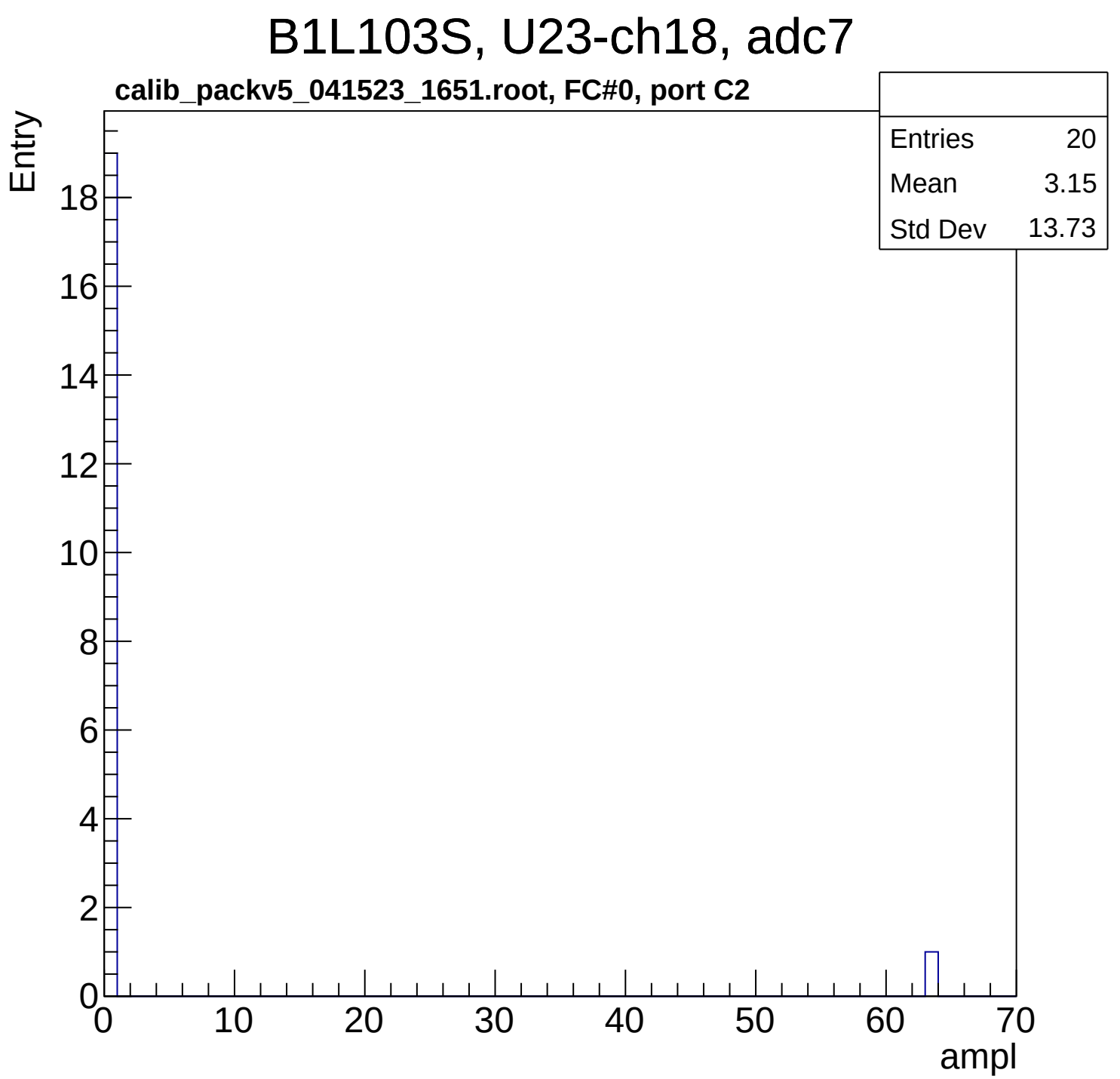

B1L103S, U23-ch18, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 35.44
Std Dev 16.29
Gaus mean : 42.6880
Gaus Width: 3.3803
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

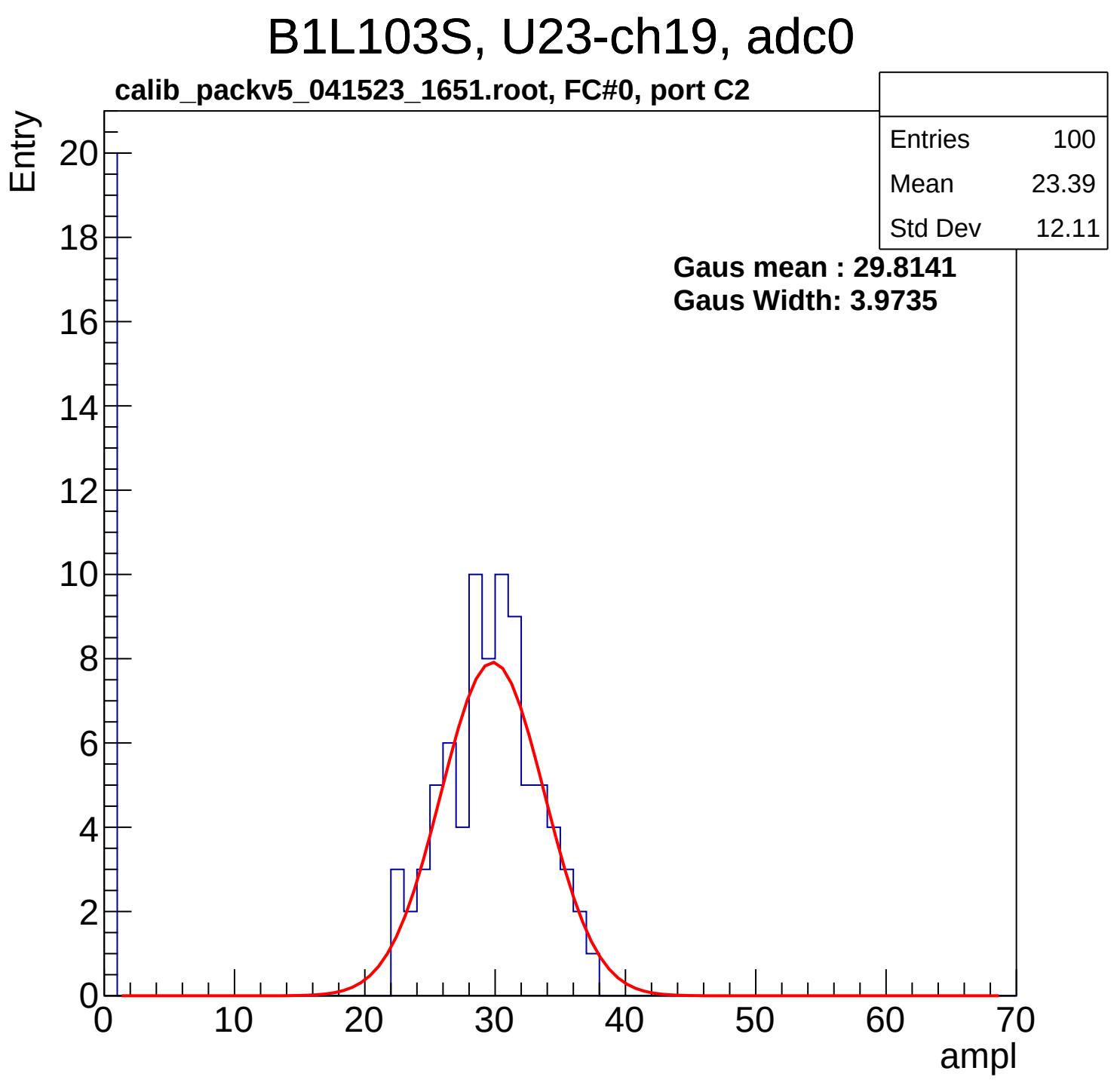

B1L103S, U23-ch18, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 49.25
Std Dev 3.181
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

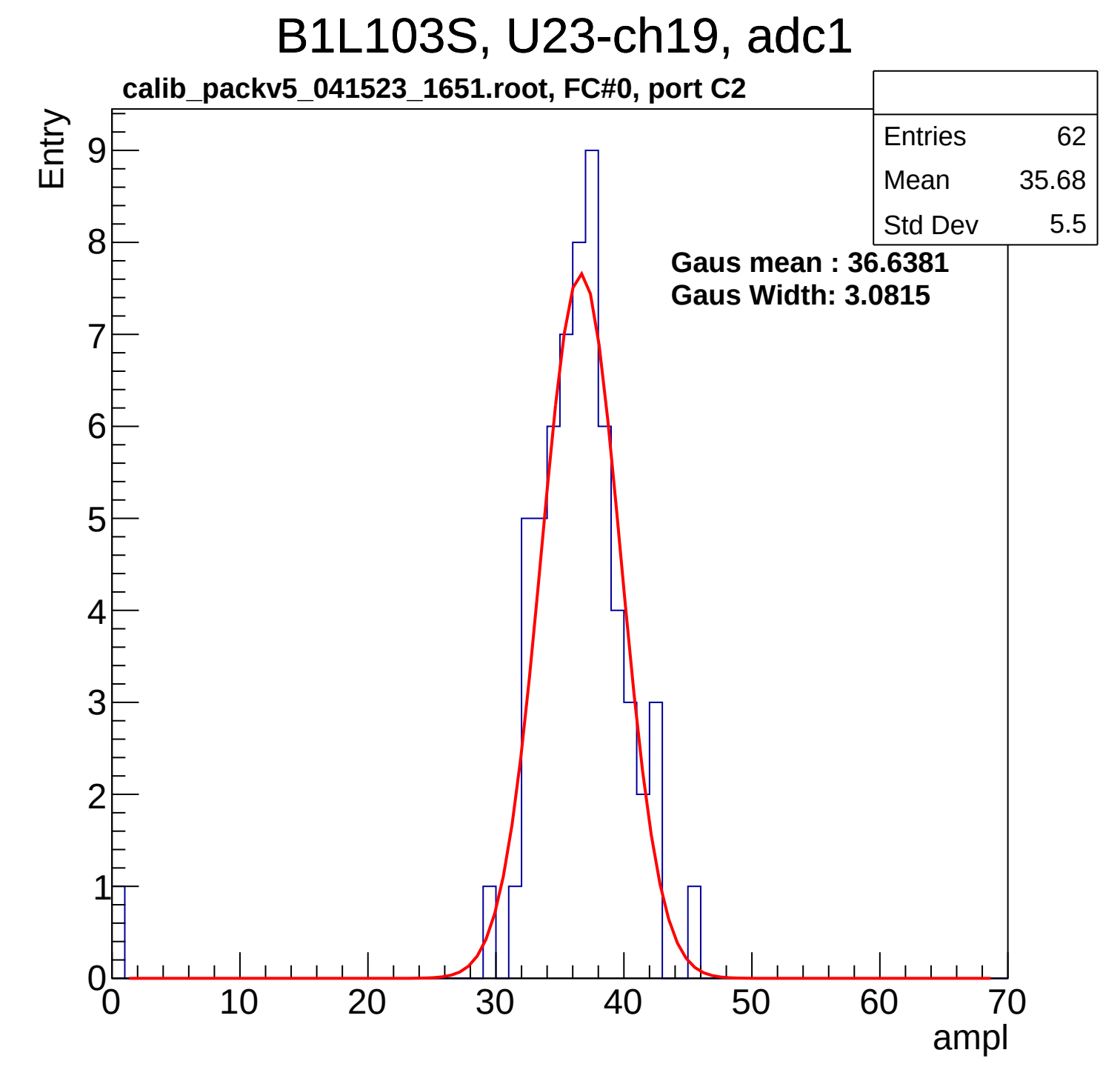

B1L103S, U23-ch18, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 54.53
Std Dev 2.711
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

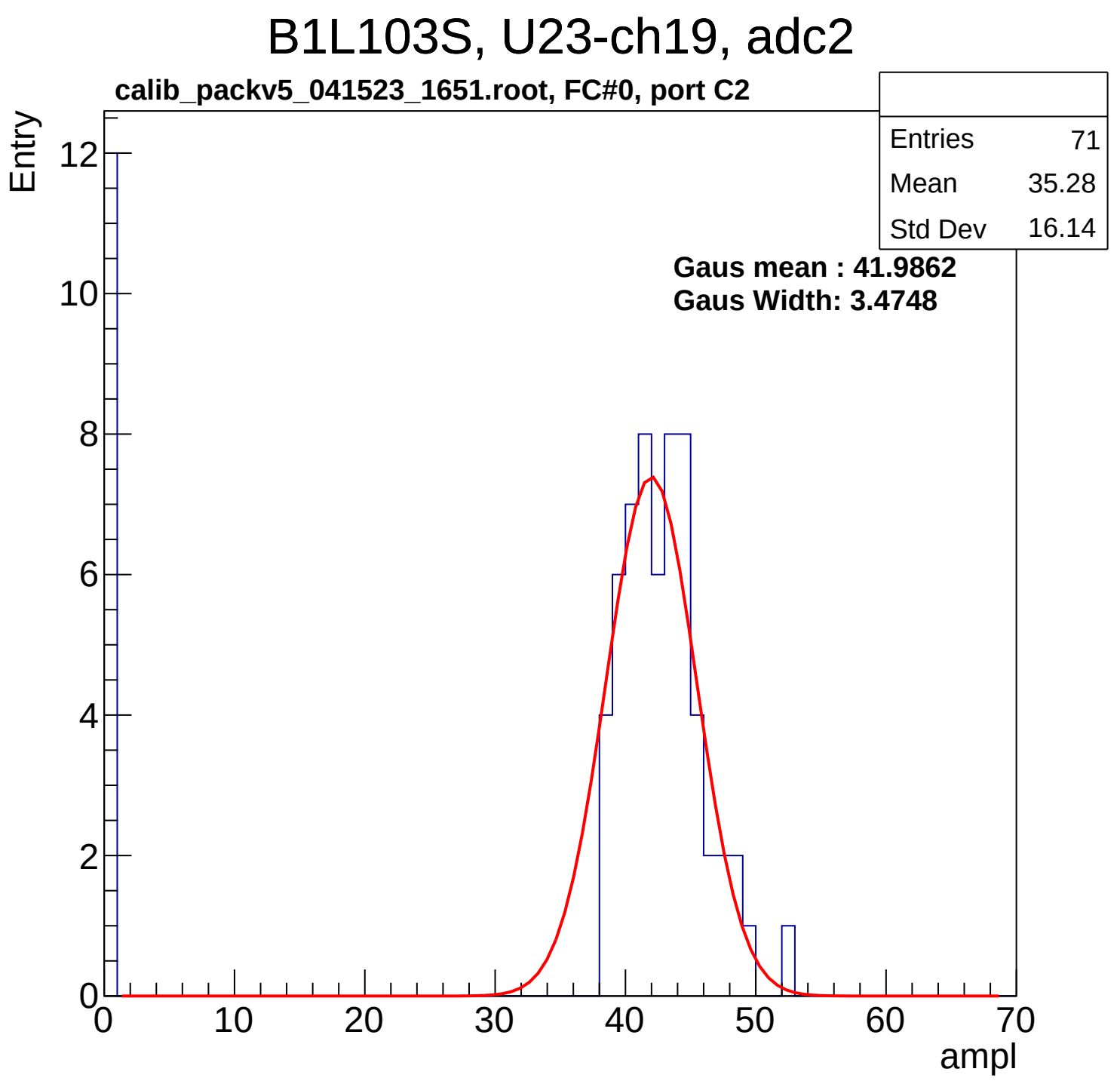

B1L103S, U23-ch18, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 58.31
Std Dev 8.404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

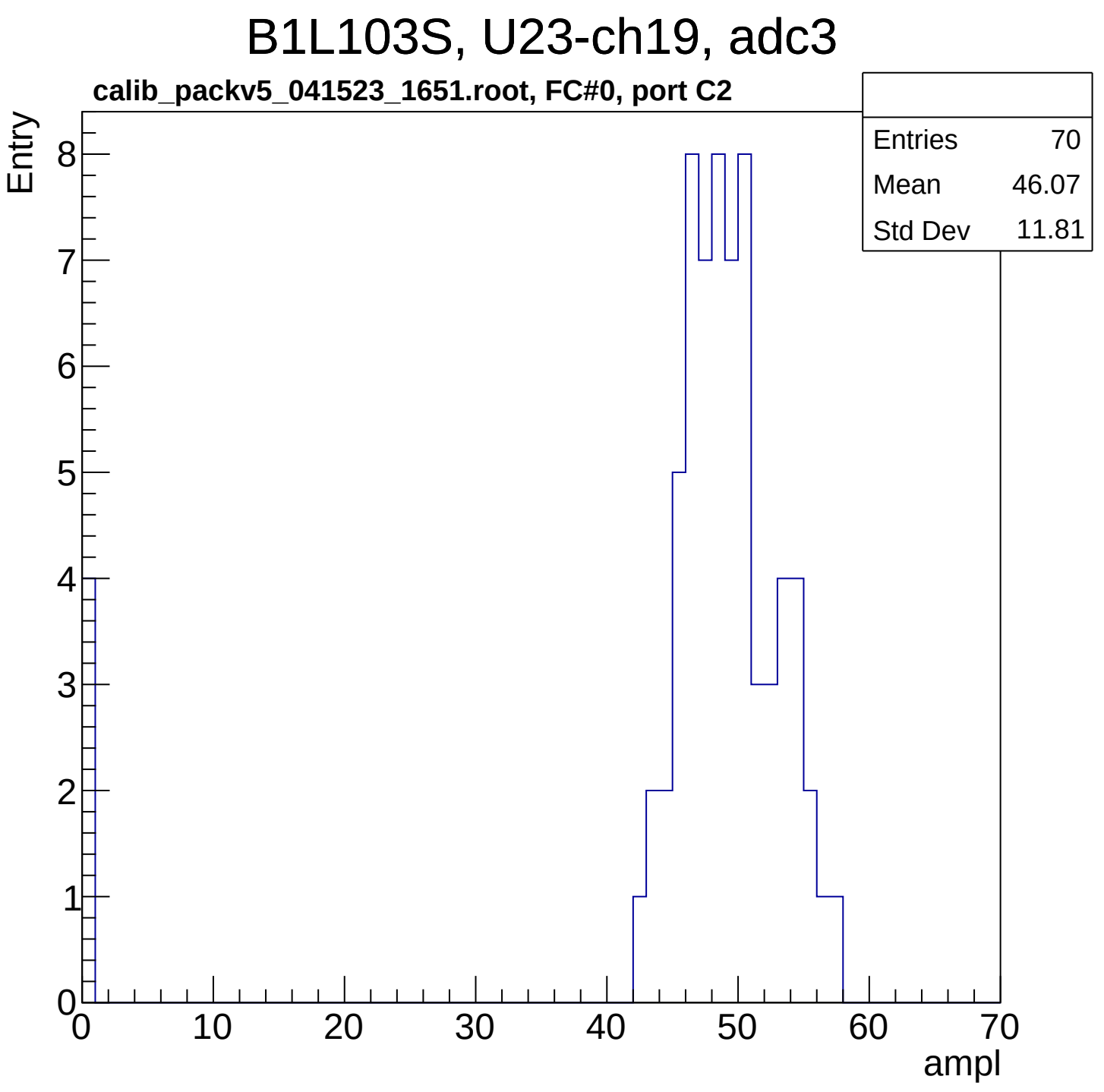

B1L103S, U23-ch18, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 61.89
Std Dev 1.286
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

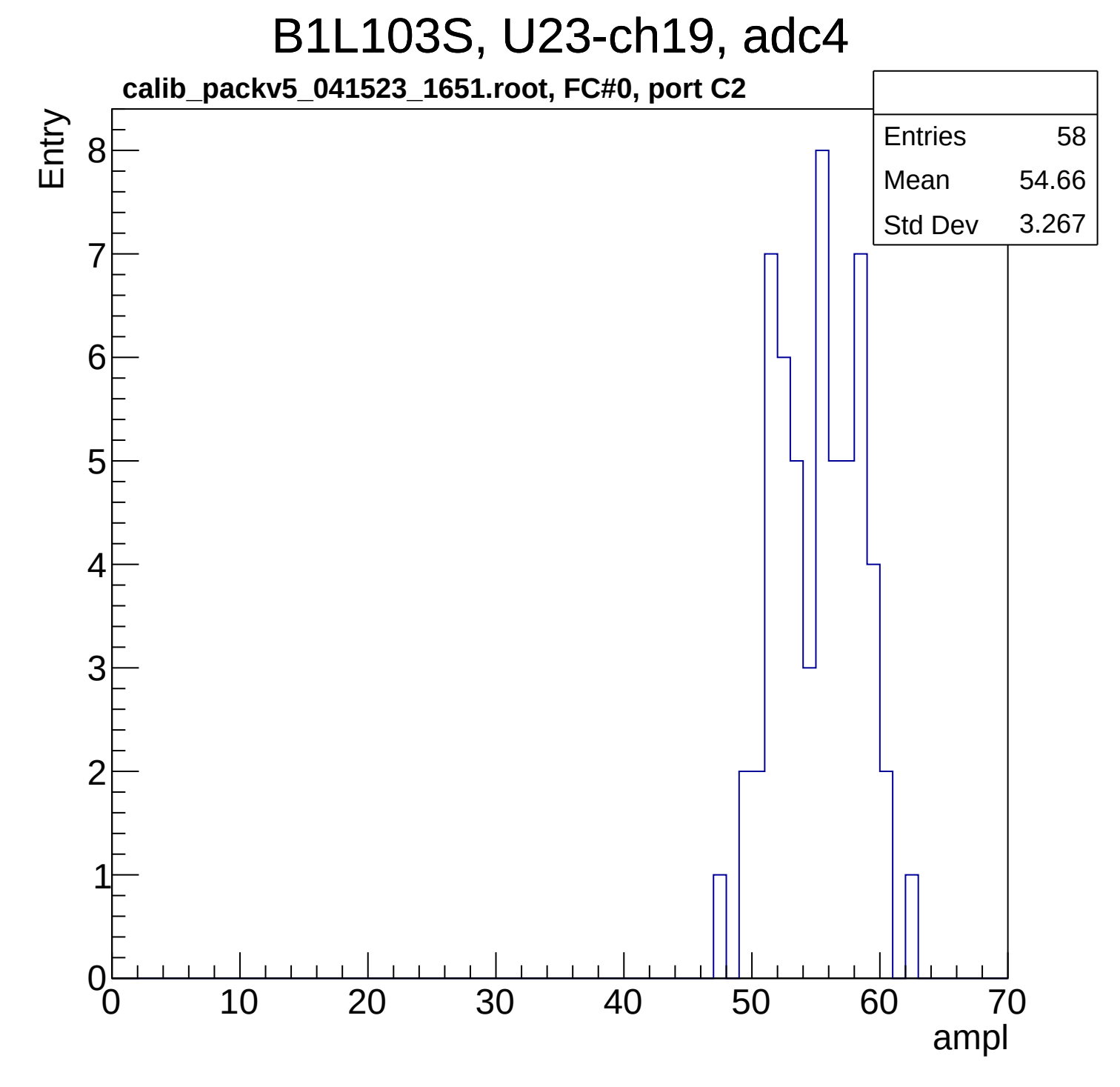

B1L103S, U23-ch18, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.15
Std Dev 13.73
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

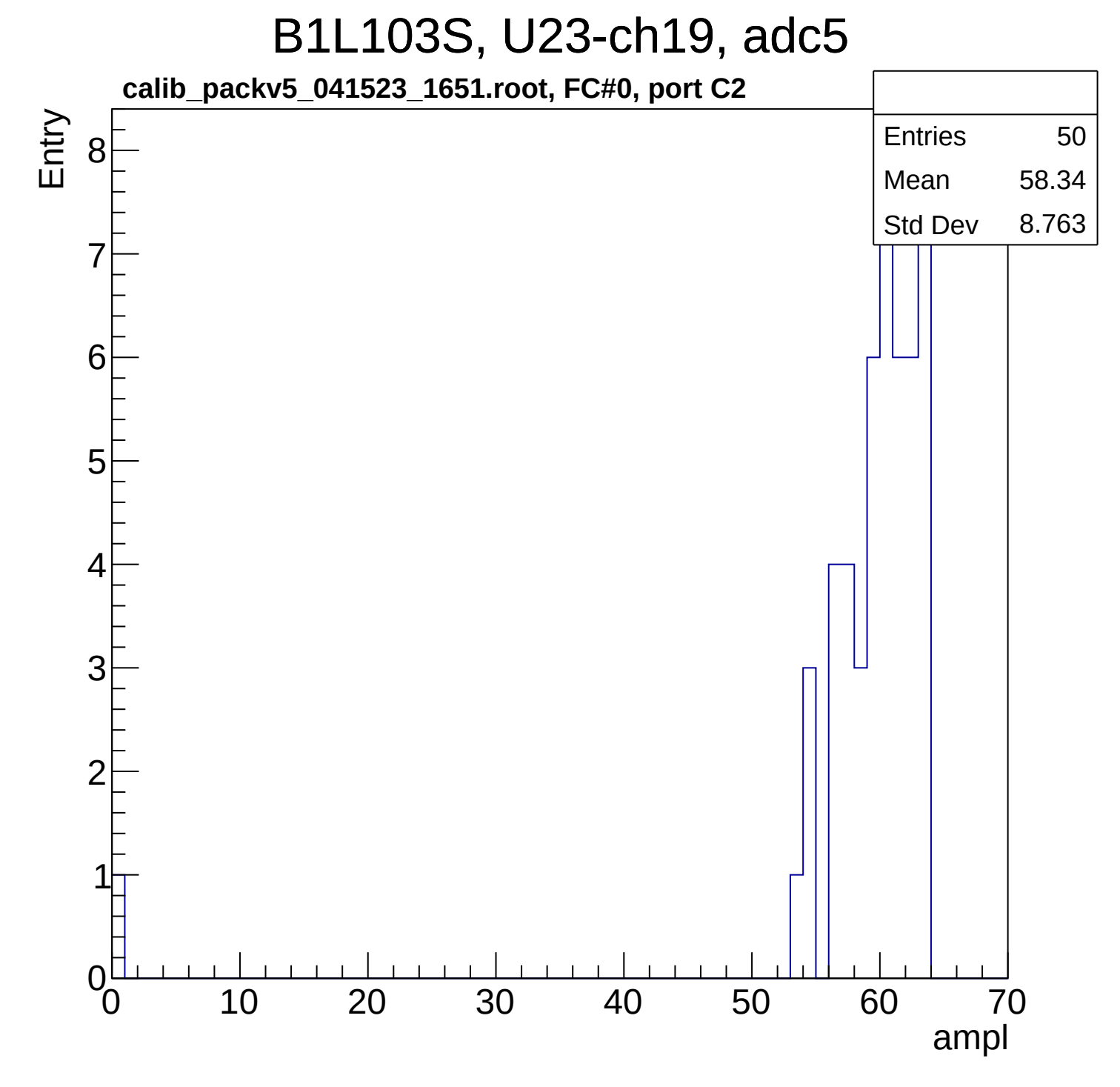

B1L103S, U23-ch19, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 23.39
Std Dev 12.11
Gaus mean : 29.8141
Gaus Width: 3.9735
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

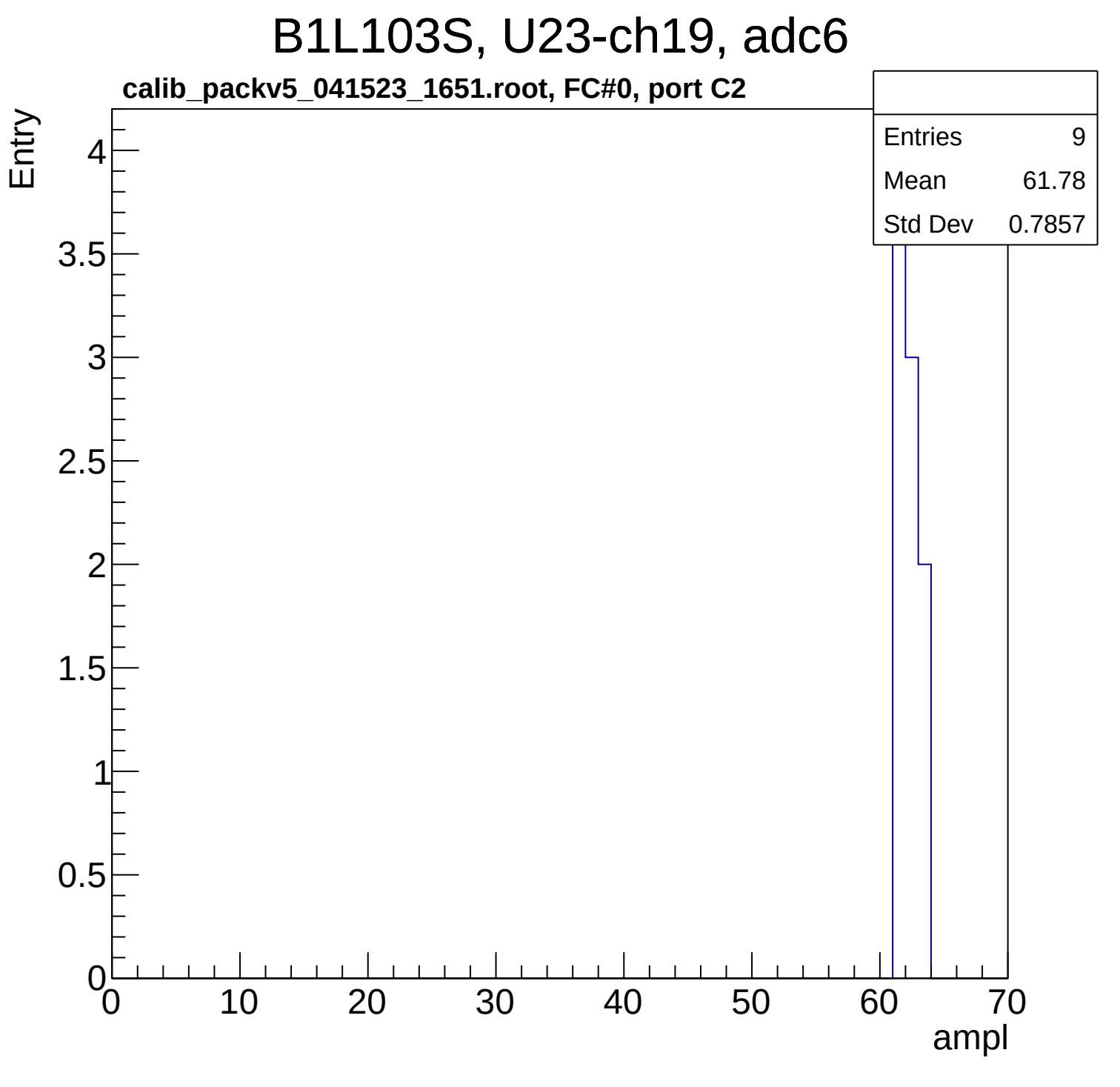

B1L103S, U23-ch19, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 35.68
Std Dev 5.5
Gaus mean : 36.6381
Gaus Width: 3.0815
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

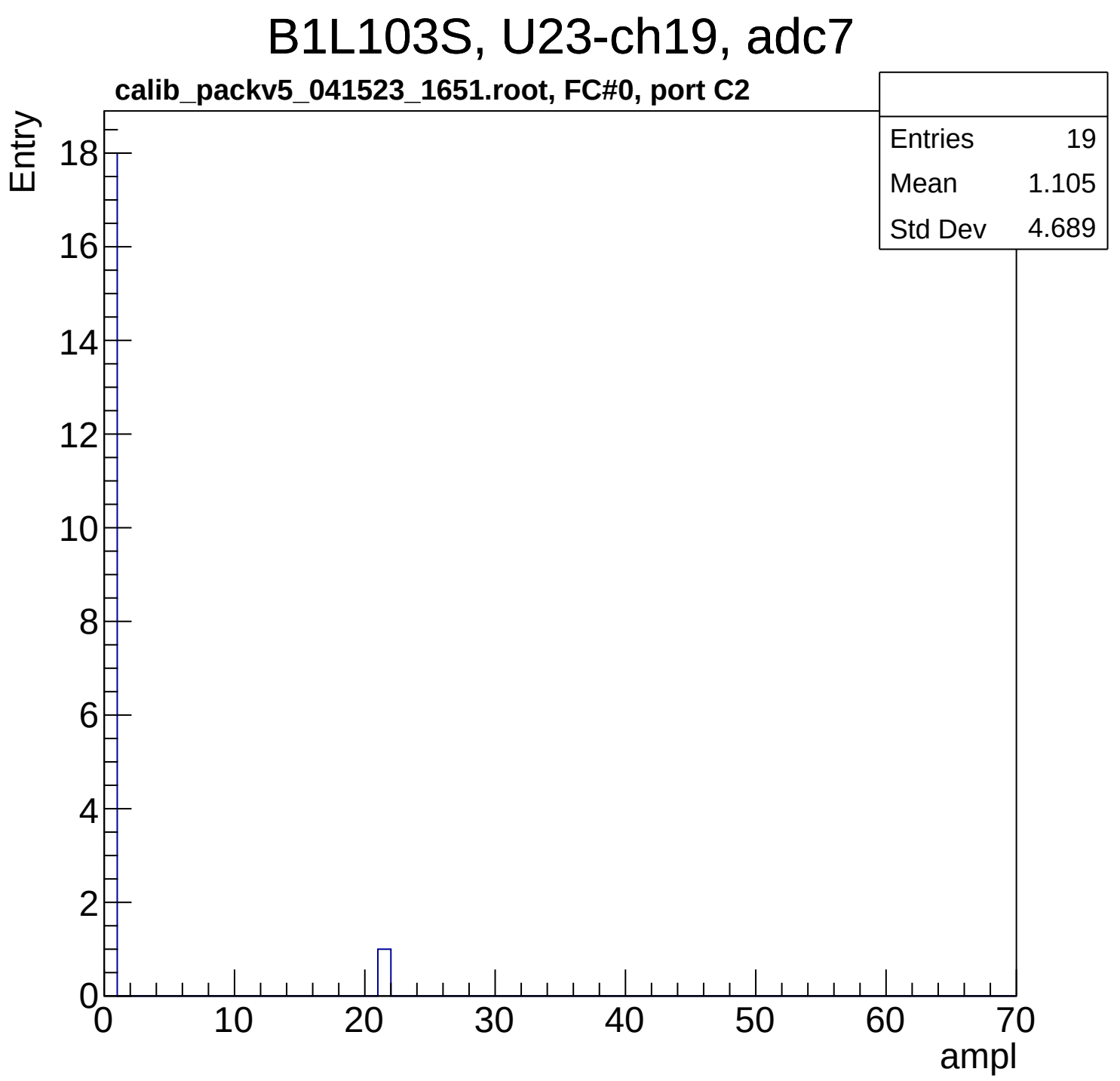

B1L103S, U23-ch19, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 35.28
Std Dev 16.14
Gaus mean : 41.9862
Gaus Width: 3.4748
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

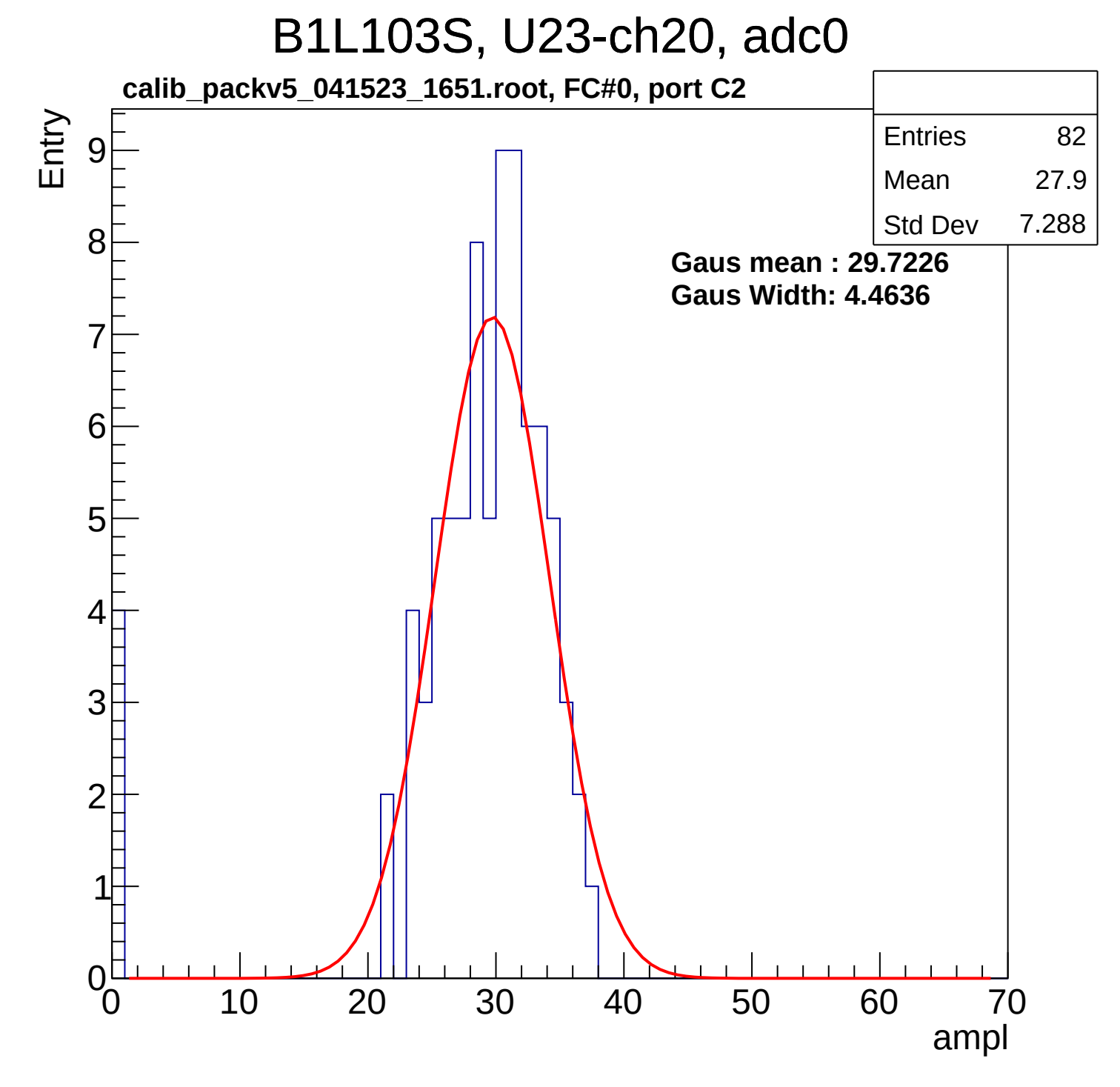

B1L103S, U23-ch19, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 46.07
Std Dev 11.81
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

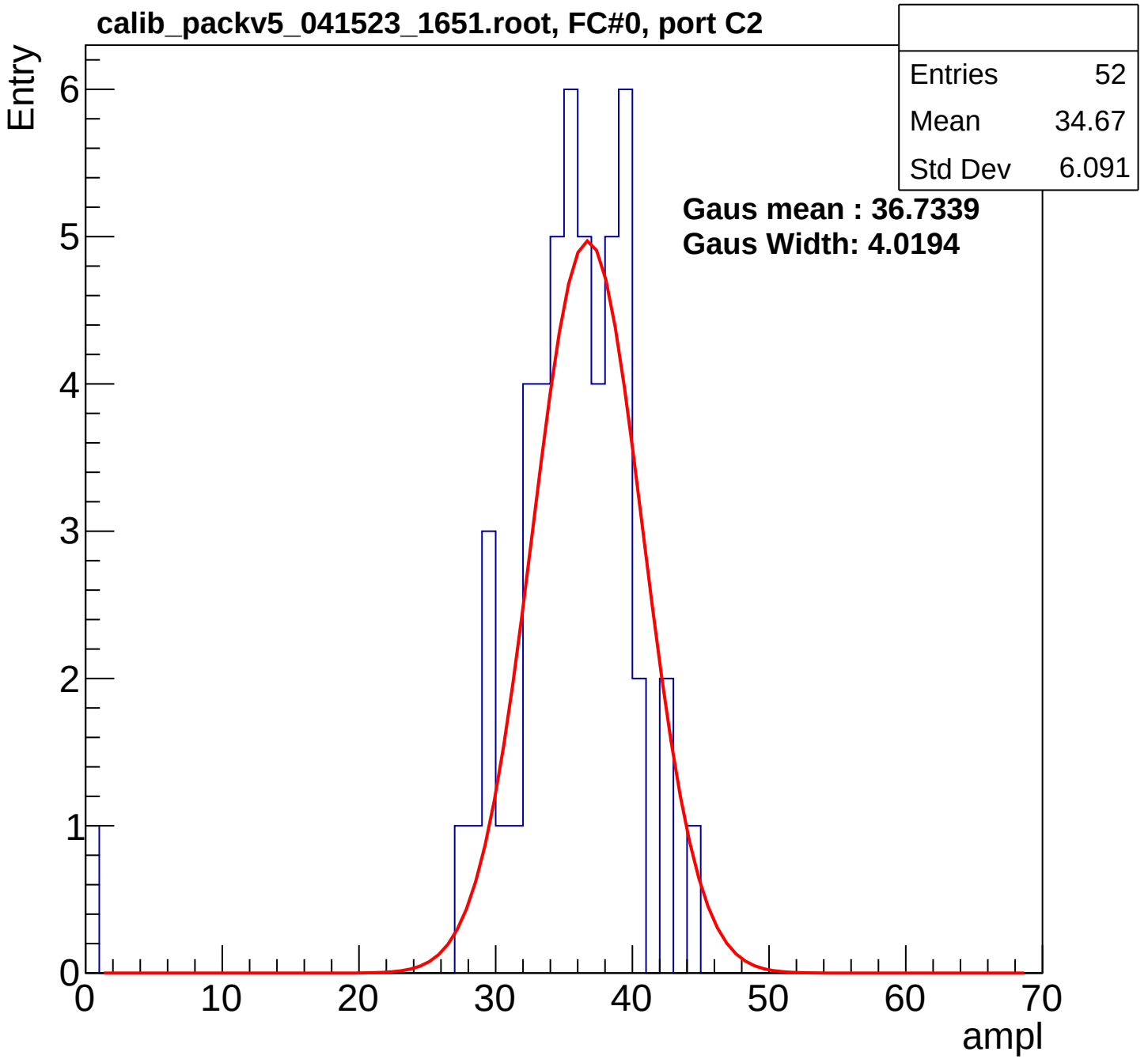

B1L103S, U23-ch19, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 54.66
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

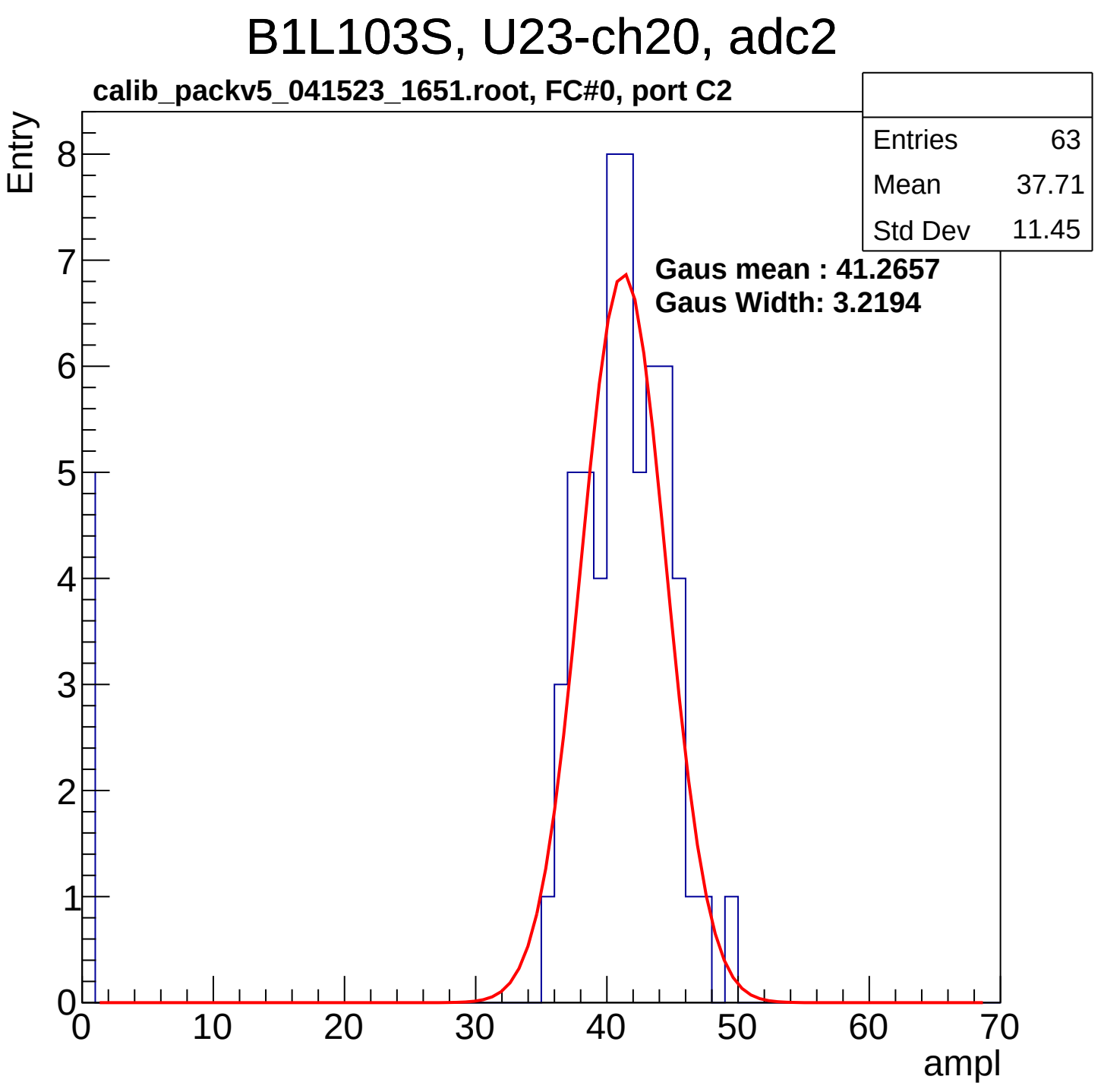

B1L103S, U23-ch19, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 50
Mean 58.34
Std Dev 8.763
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

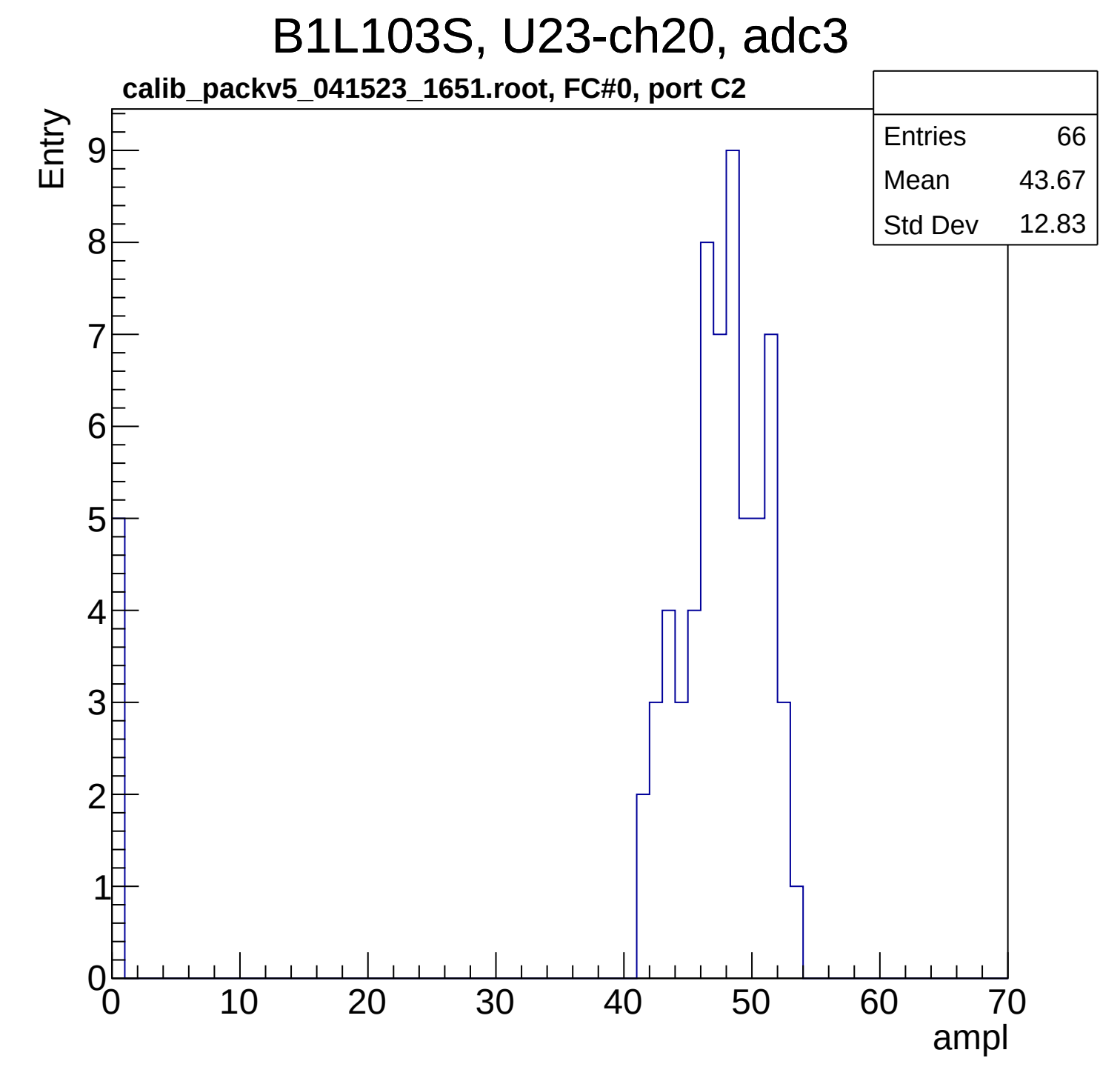

B1L103S, U23-ch19, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 61.78
Std Dev 0.7857
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

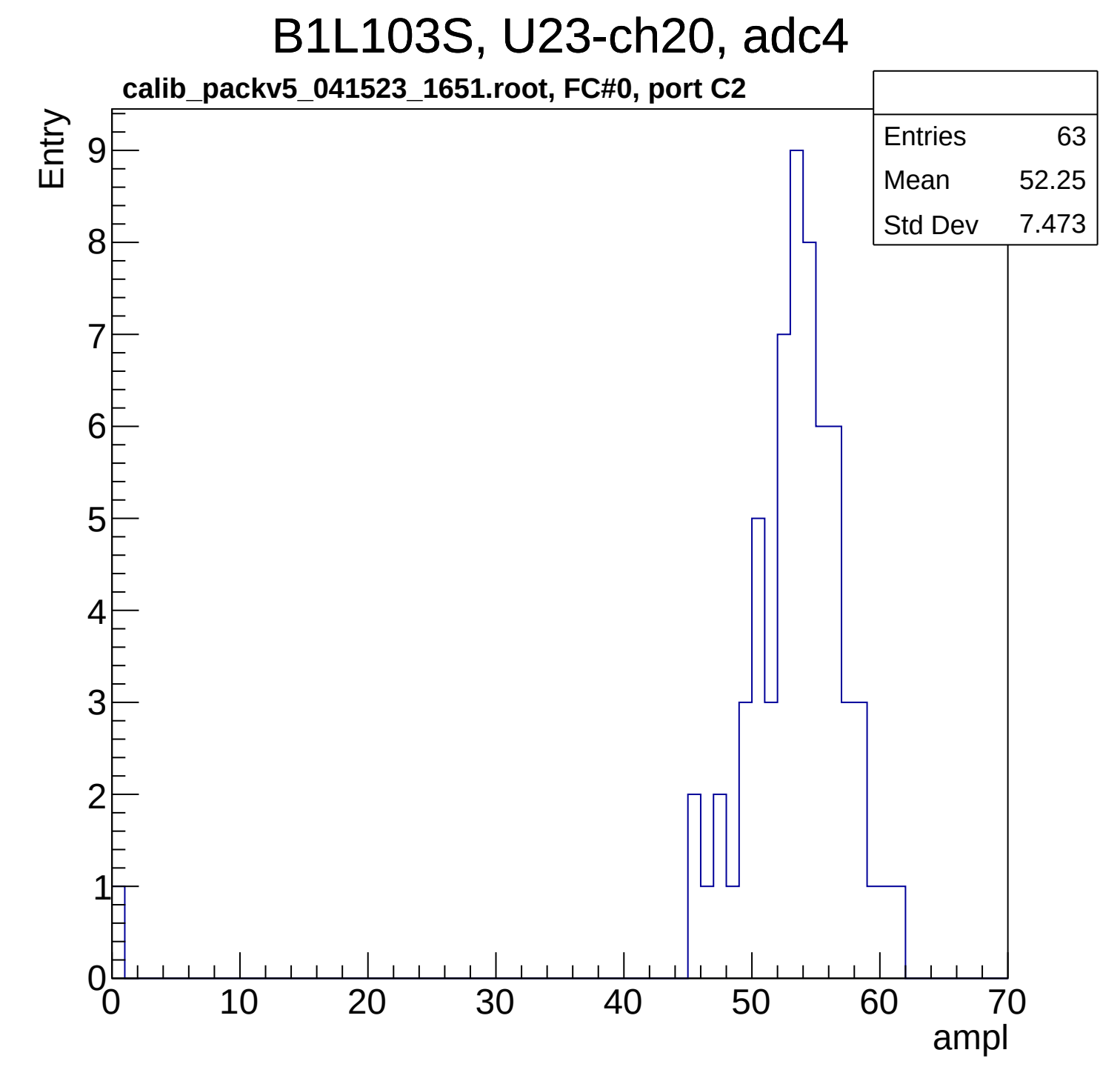

B1L103S, U23-ch19, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.105
Std Dev 4.689
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

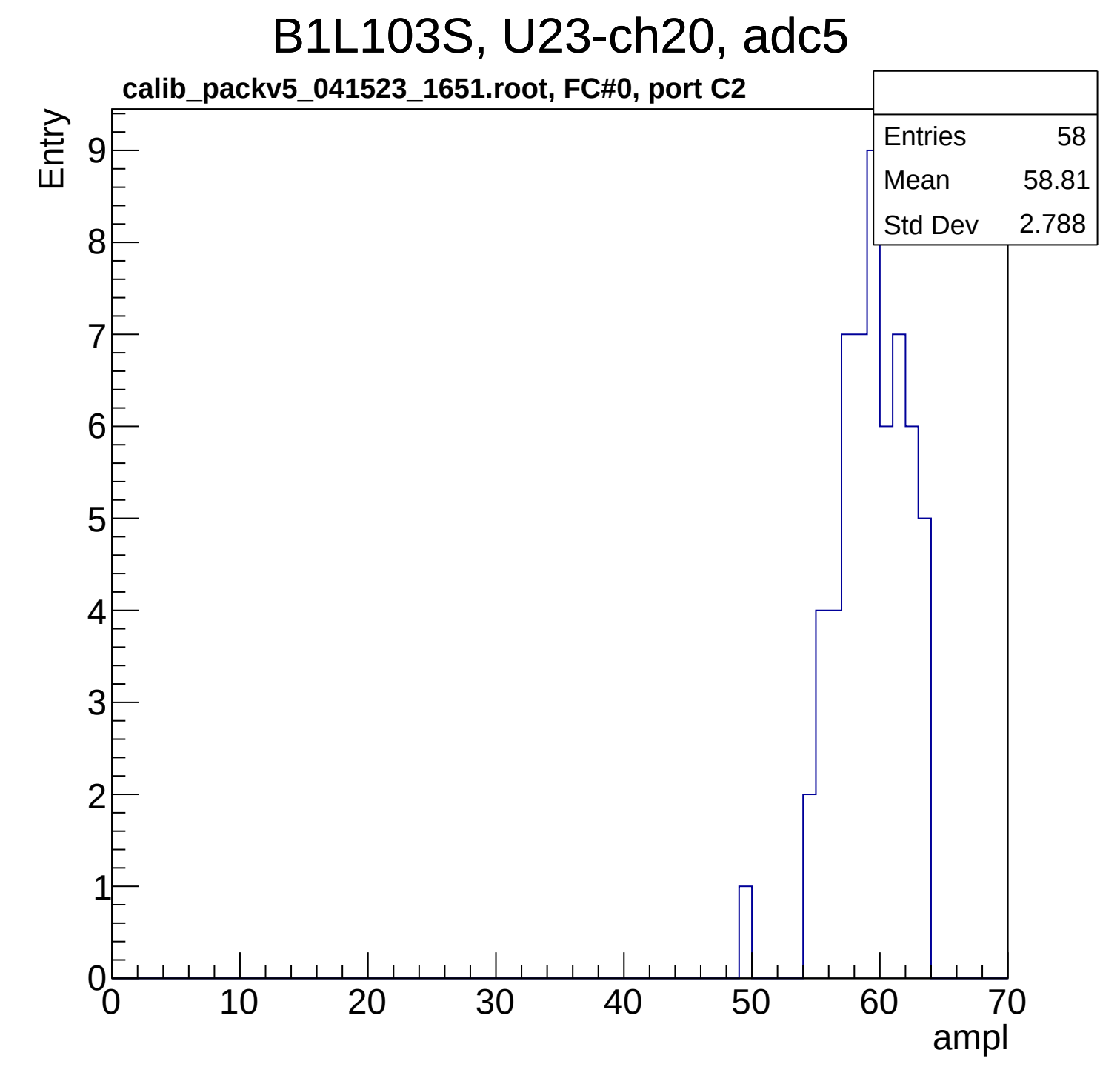

B1L103S, U23-ch20, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 27.9
Std Dev 7.288
Gaus mean : 29.7226
Gaus Width: 4.4636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

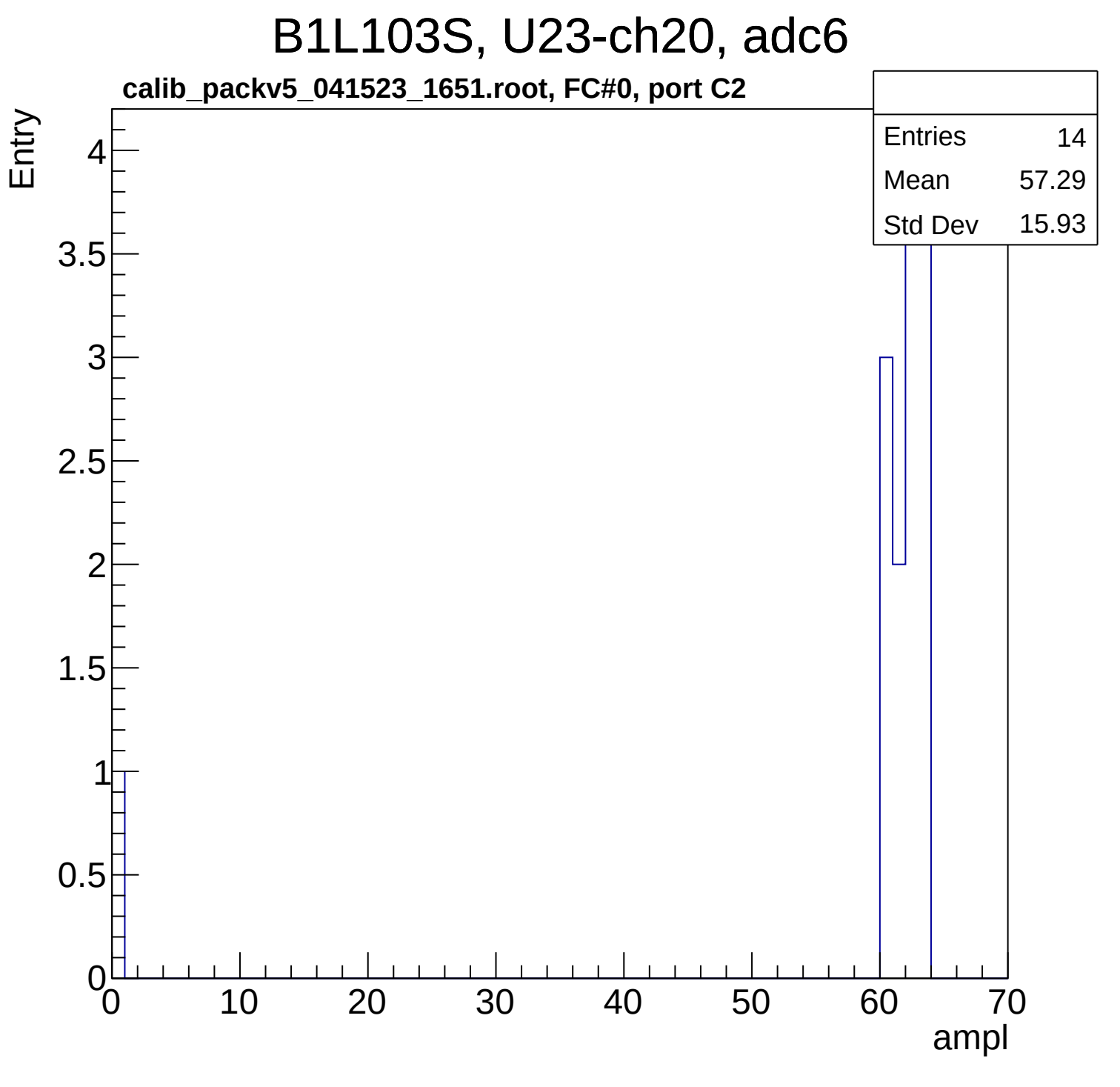

B1L103S, U23-ch20, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 34.67
Std Dev 6.091
Gaus mean : 36.7339
Gaus Width: 4.0194
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

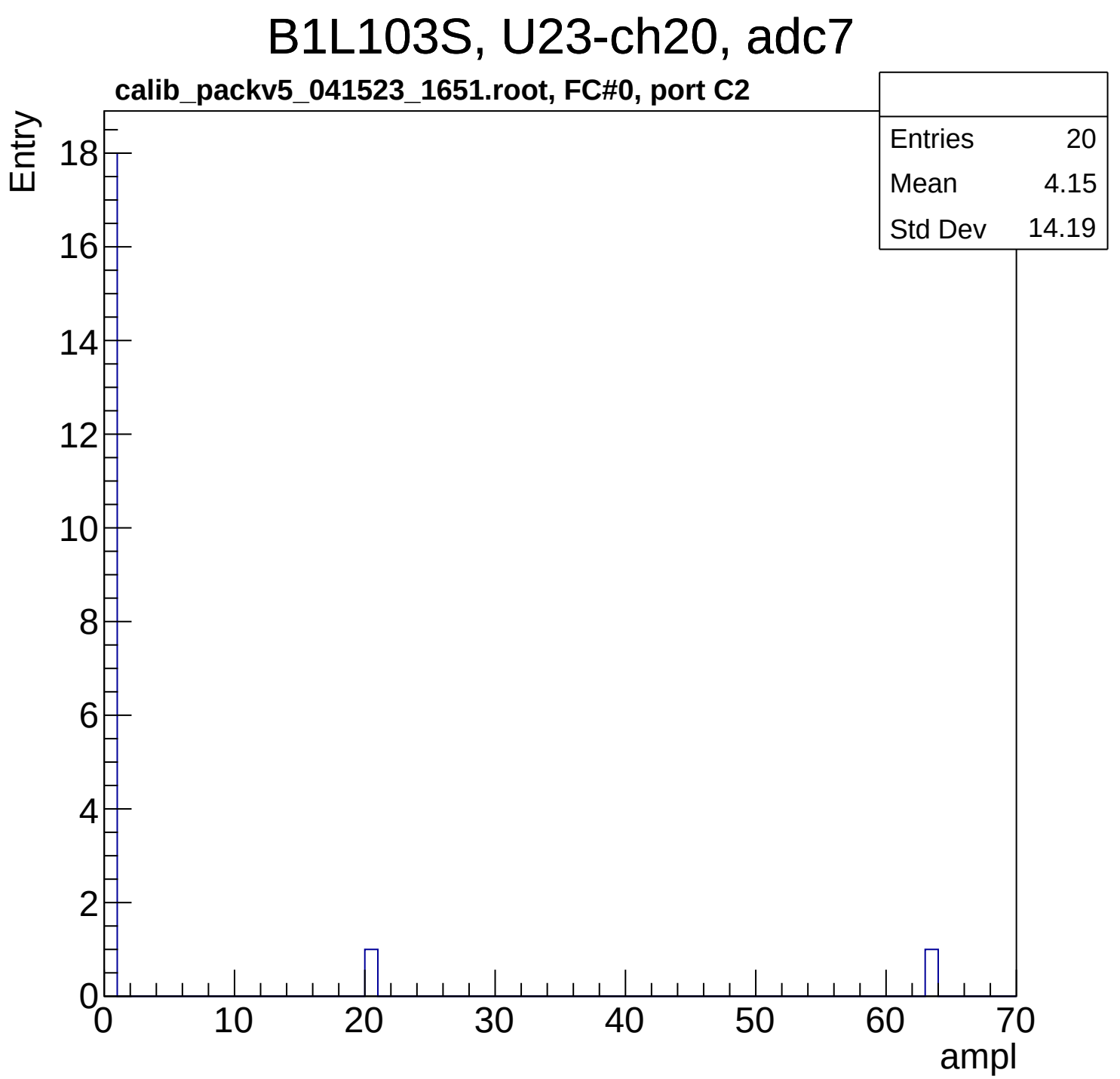

B1L103S, U23-ch20, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 37.71
Std Dev 11.45
Gaus mean : 41.2657
Gaus Width: 3.2194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

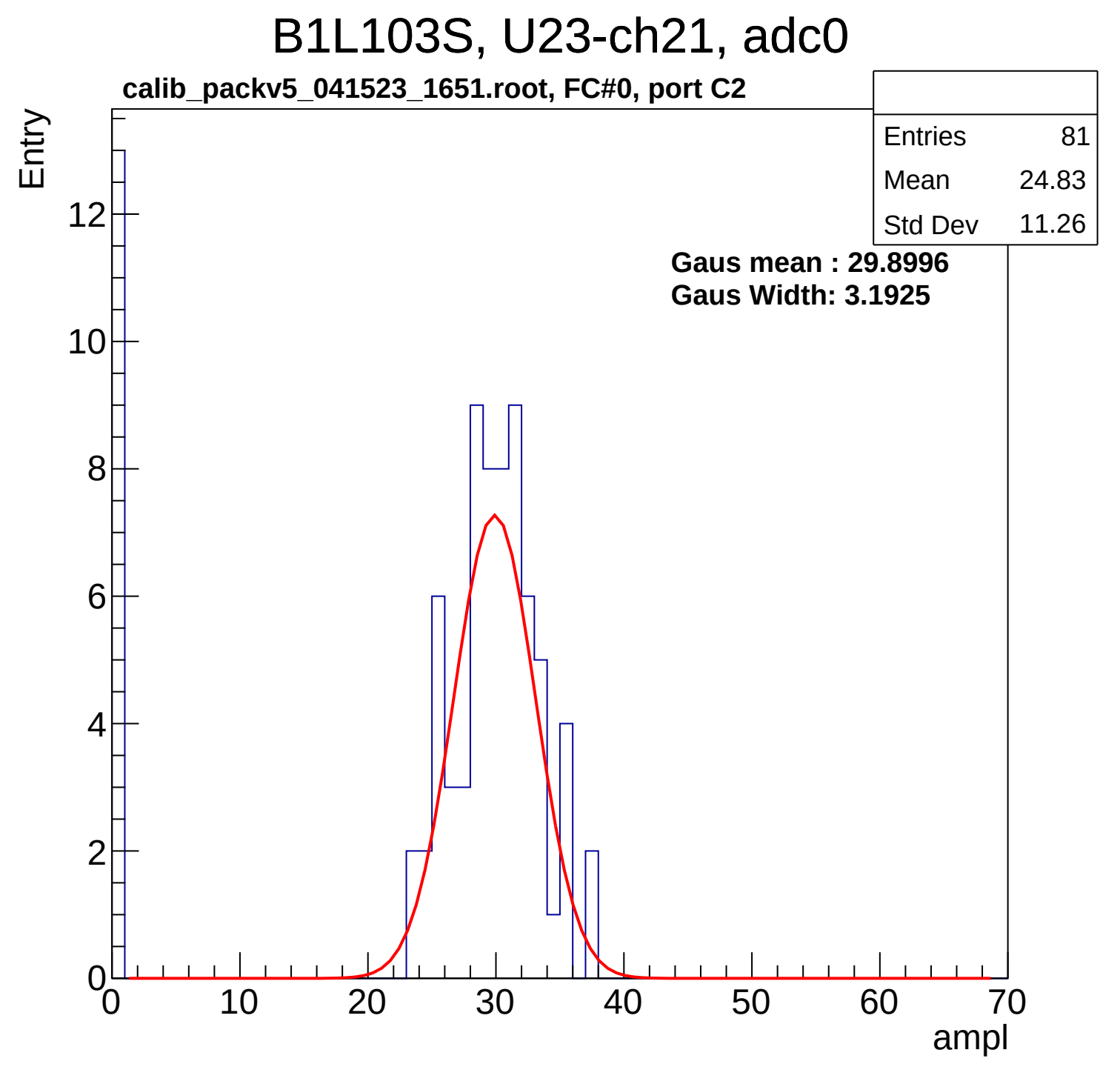

B1L103S, U23-ch20, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 43.67
Std Dev 12.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

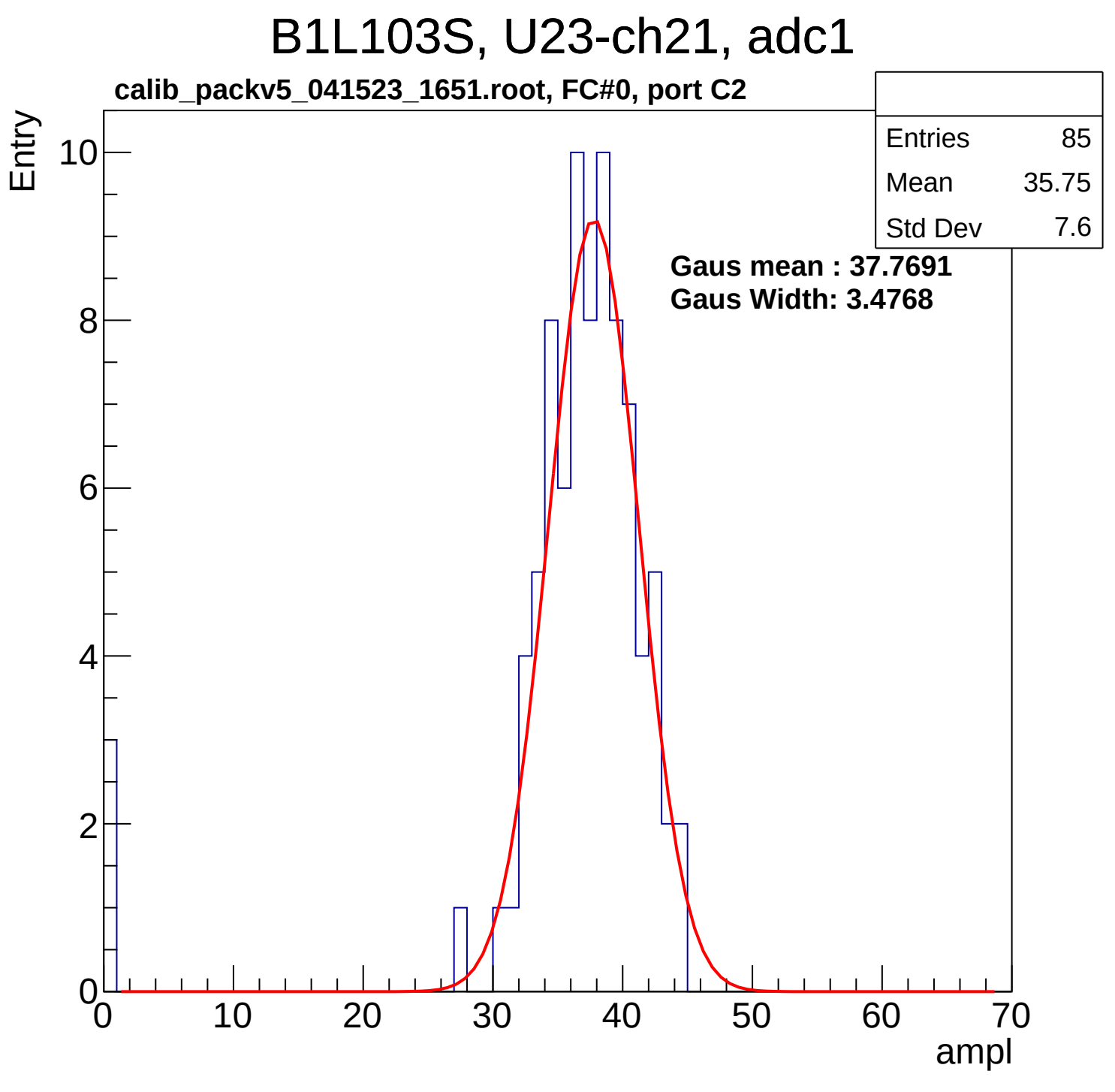

B1L103S, U23-ch20, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 52.25
Std Dev 7.473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

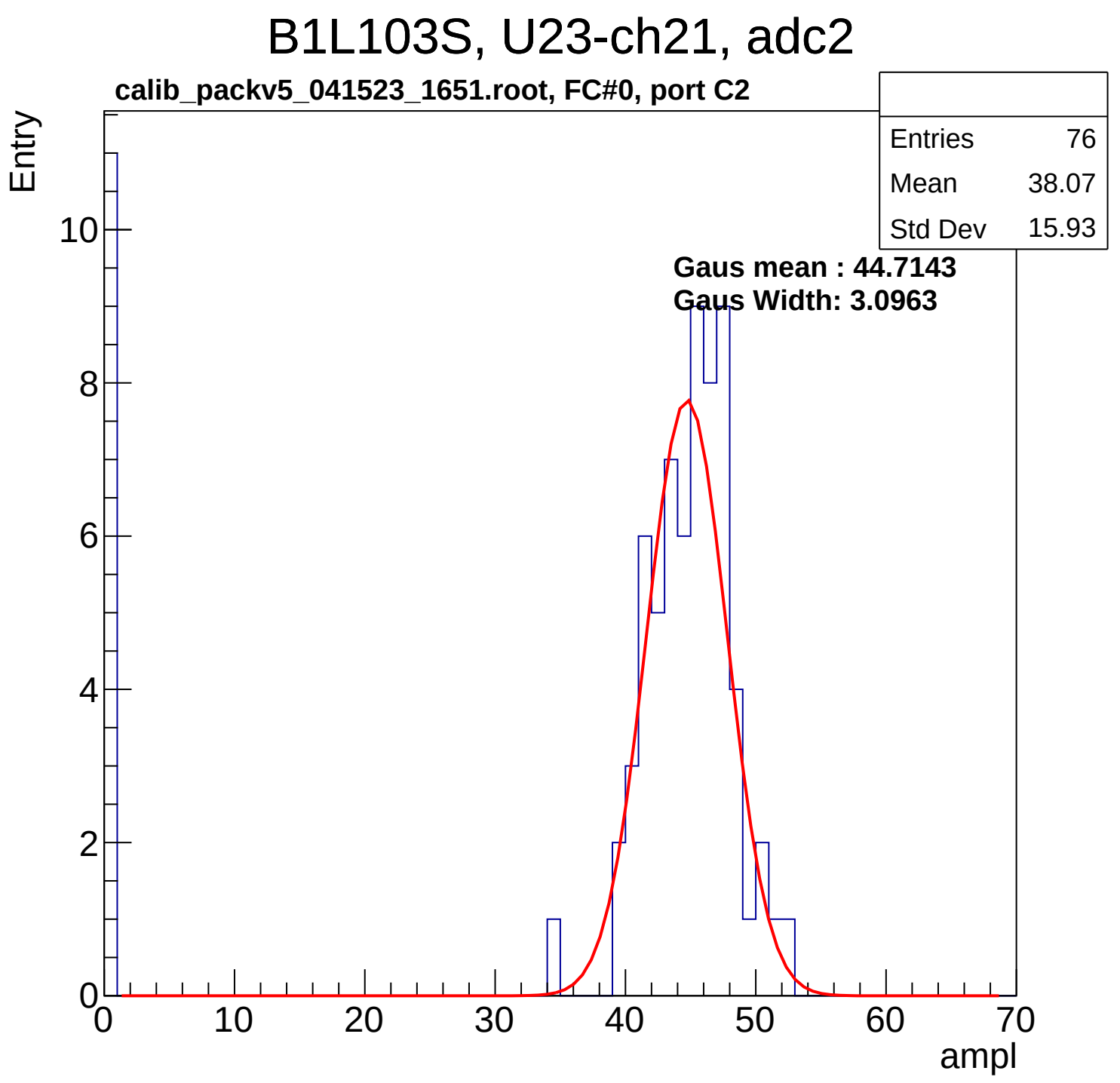

B1L103S, U23-ch20, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 58.81
Std Dev 2.788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

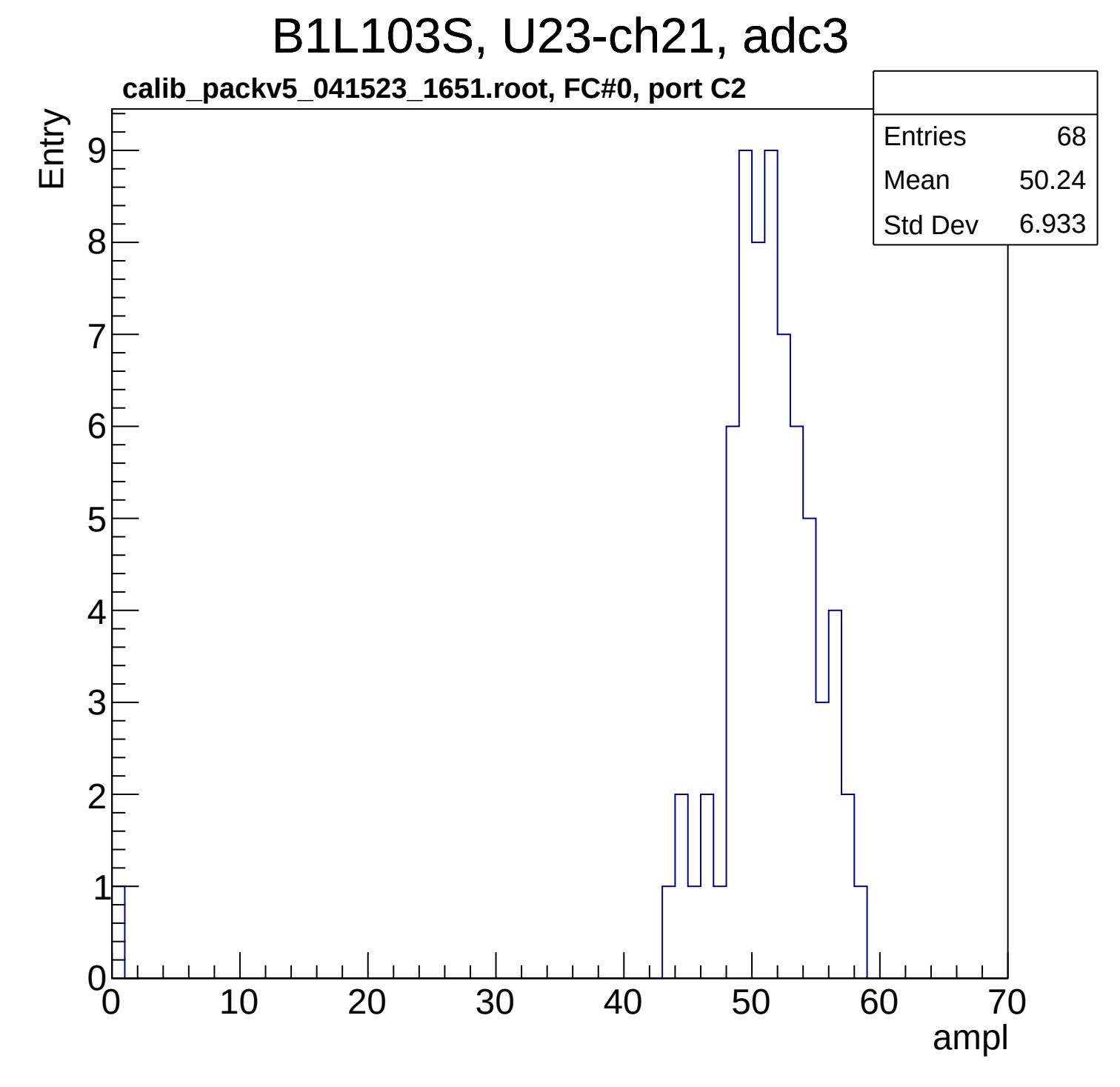

B1L103S, U23-ch20, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 14
Mean 57.29
Std Dev 15.93
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

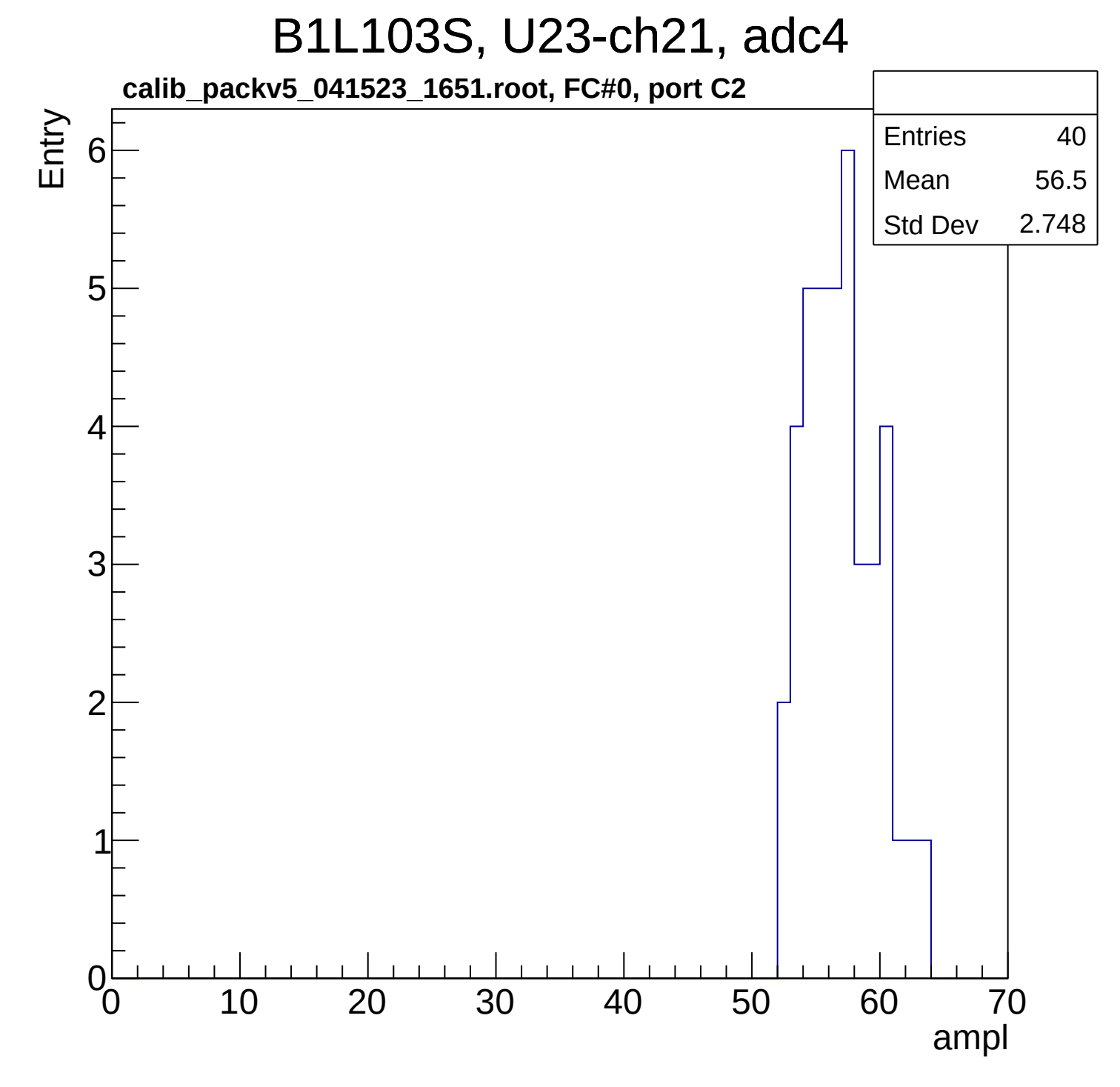

B1L103S, U23-ch20, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 4.15
Std Dev 14.19
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

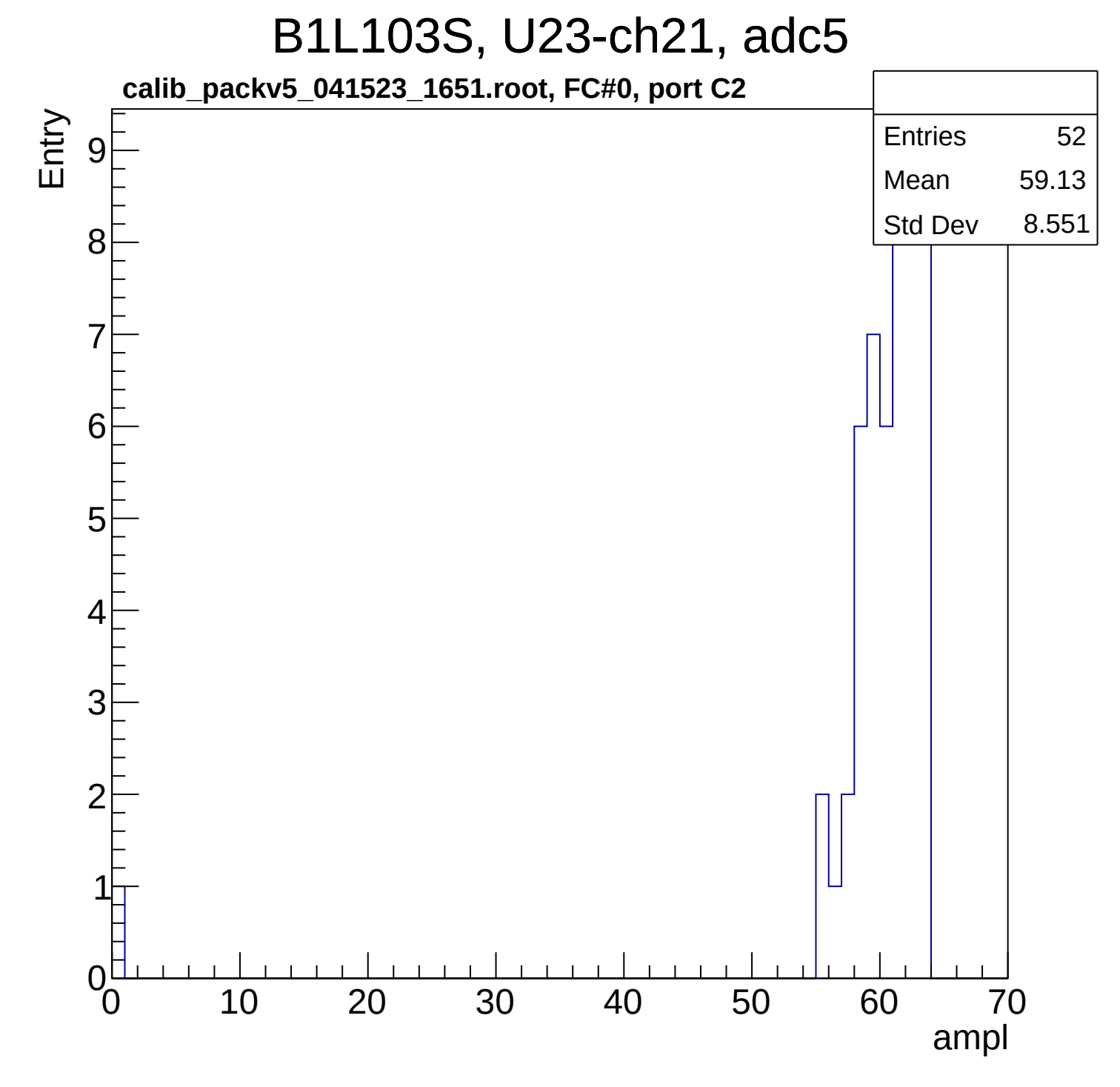

B1L103S, U23-ch21, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 24.83
Std Dev 11.26
Gaus mean : 29.8996
Gaus Width: 3.1925
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

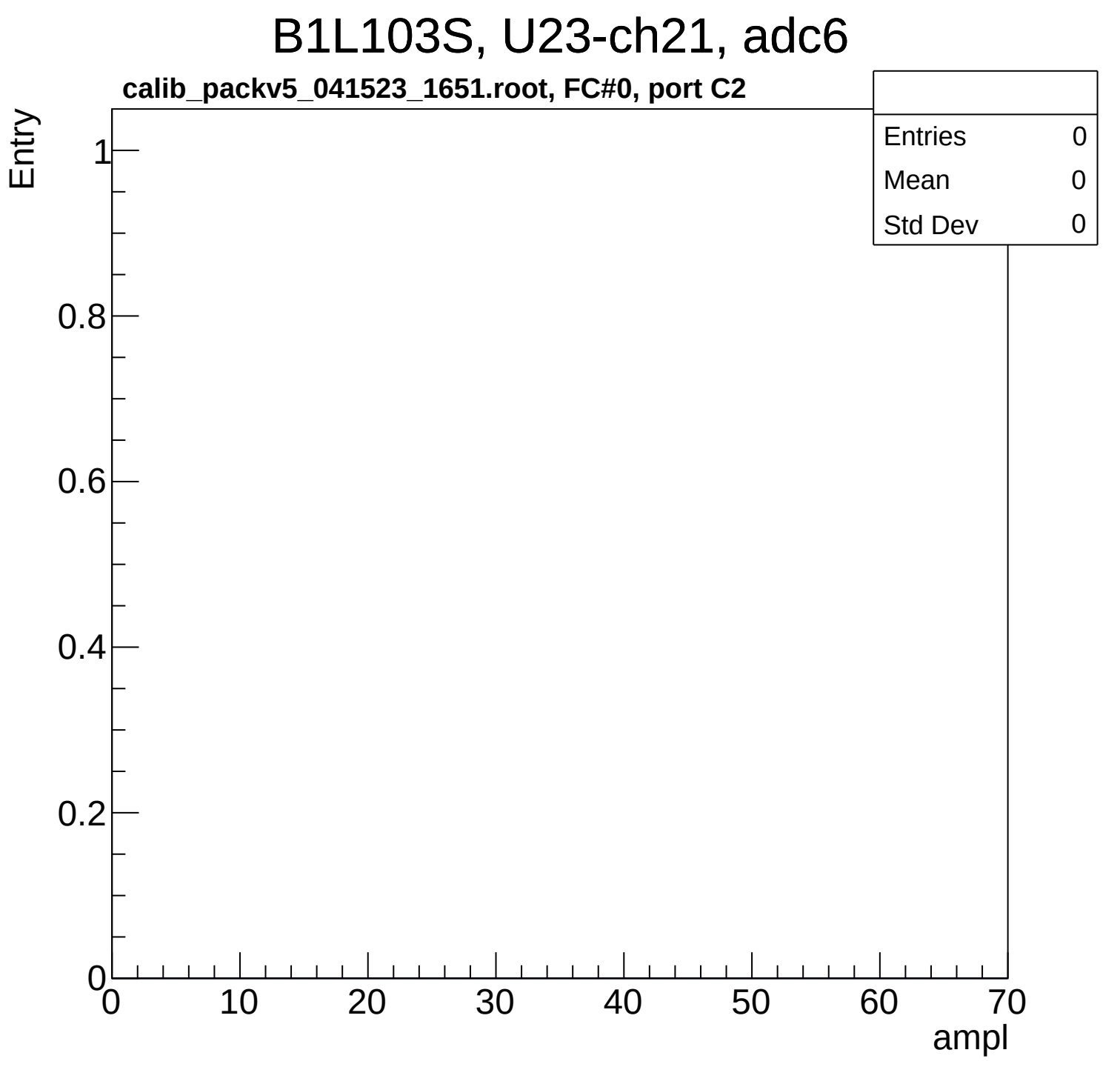

B1L103S, U23-ch21, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 35.75
Std Dev 7.6
Gaus mean : 37.7691
Gaus Width: 3.4768
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

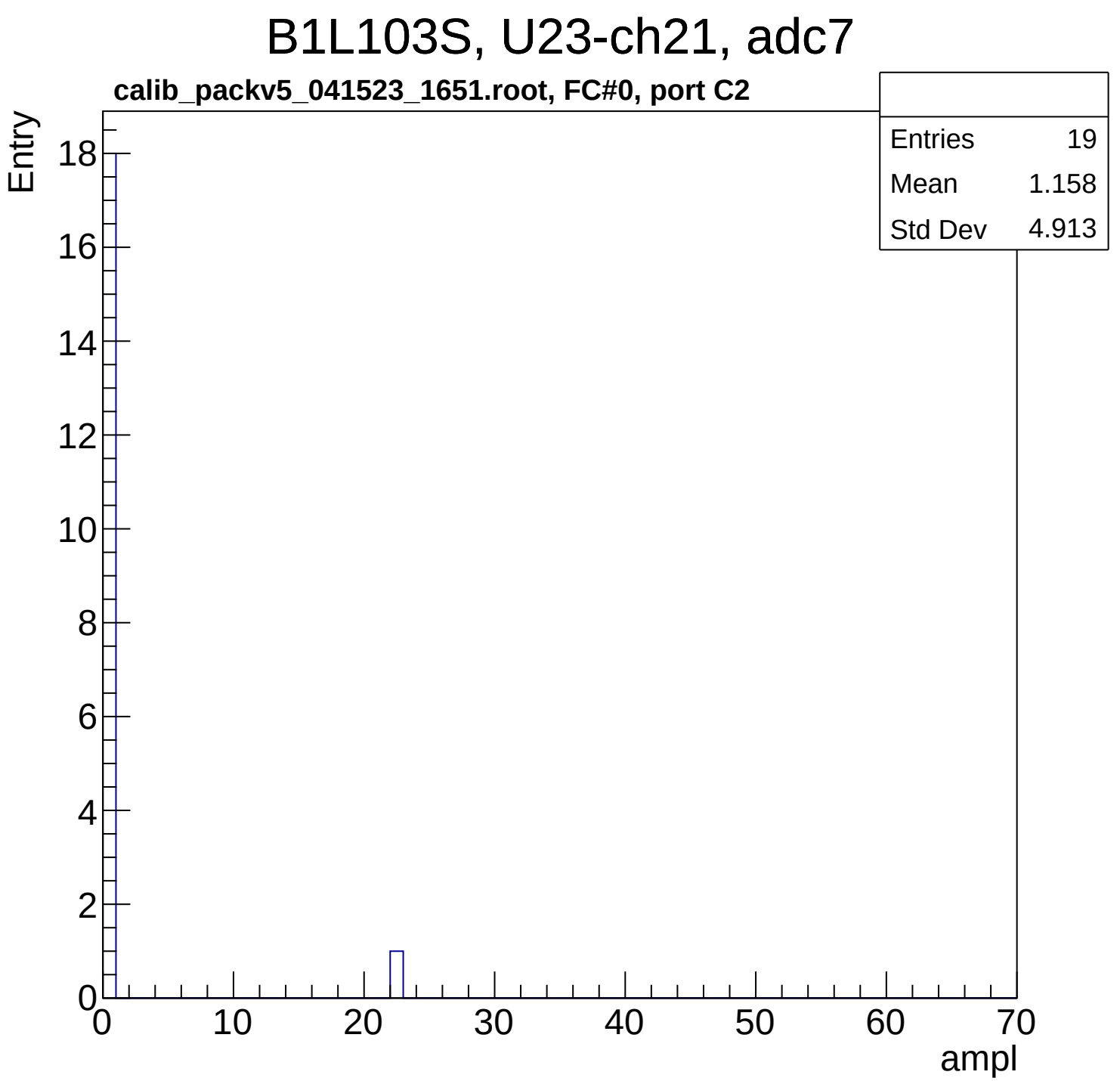

B1L103S, U23-ch21, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 38.07
Std Dev 15.93
Gaus mean : 44.7143
Gaus Width: 3.0963
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

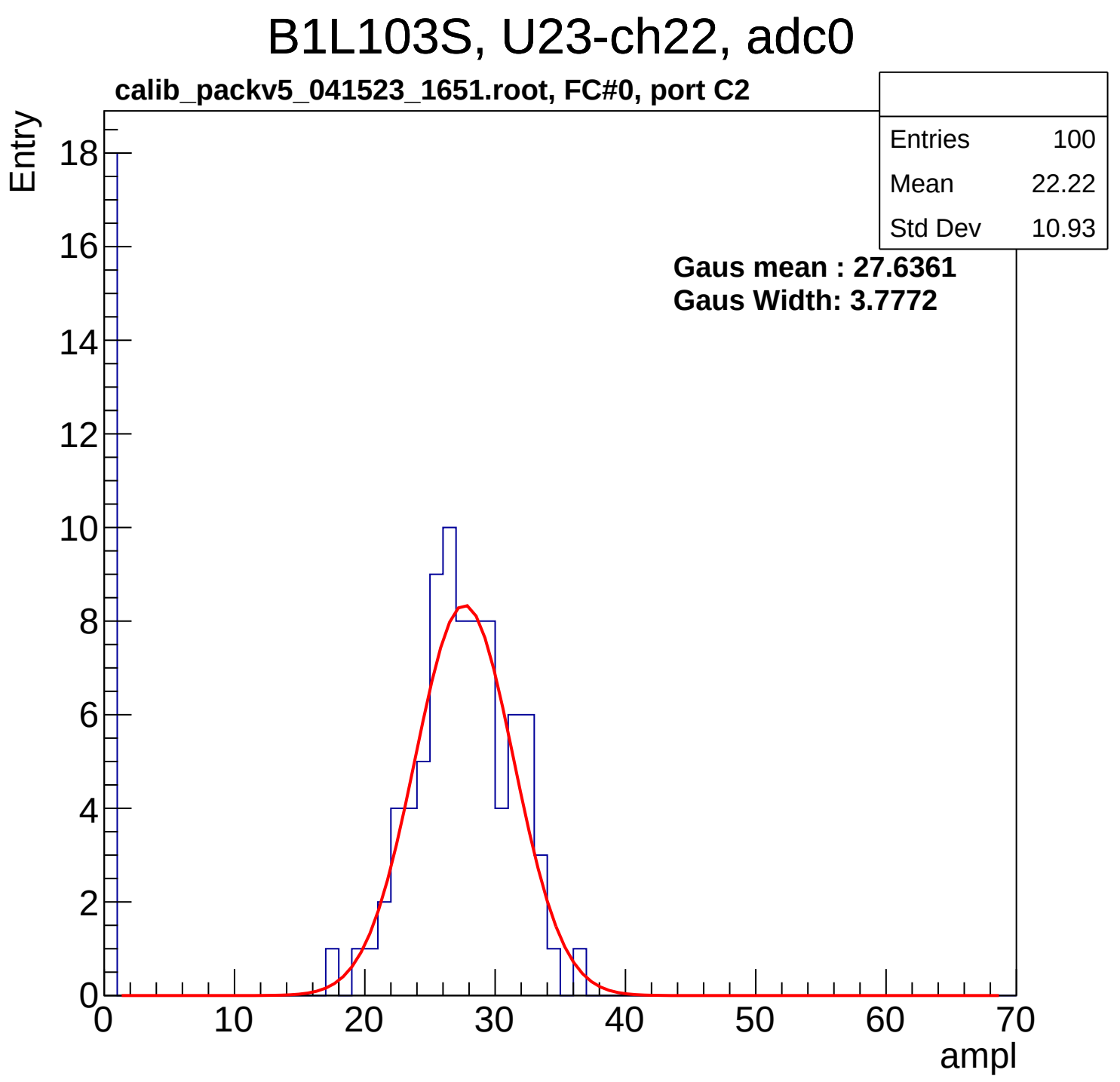

B1L103S, U23-ch21, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 50.24
Std Dev 6.933
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

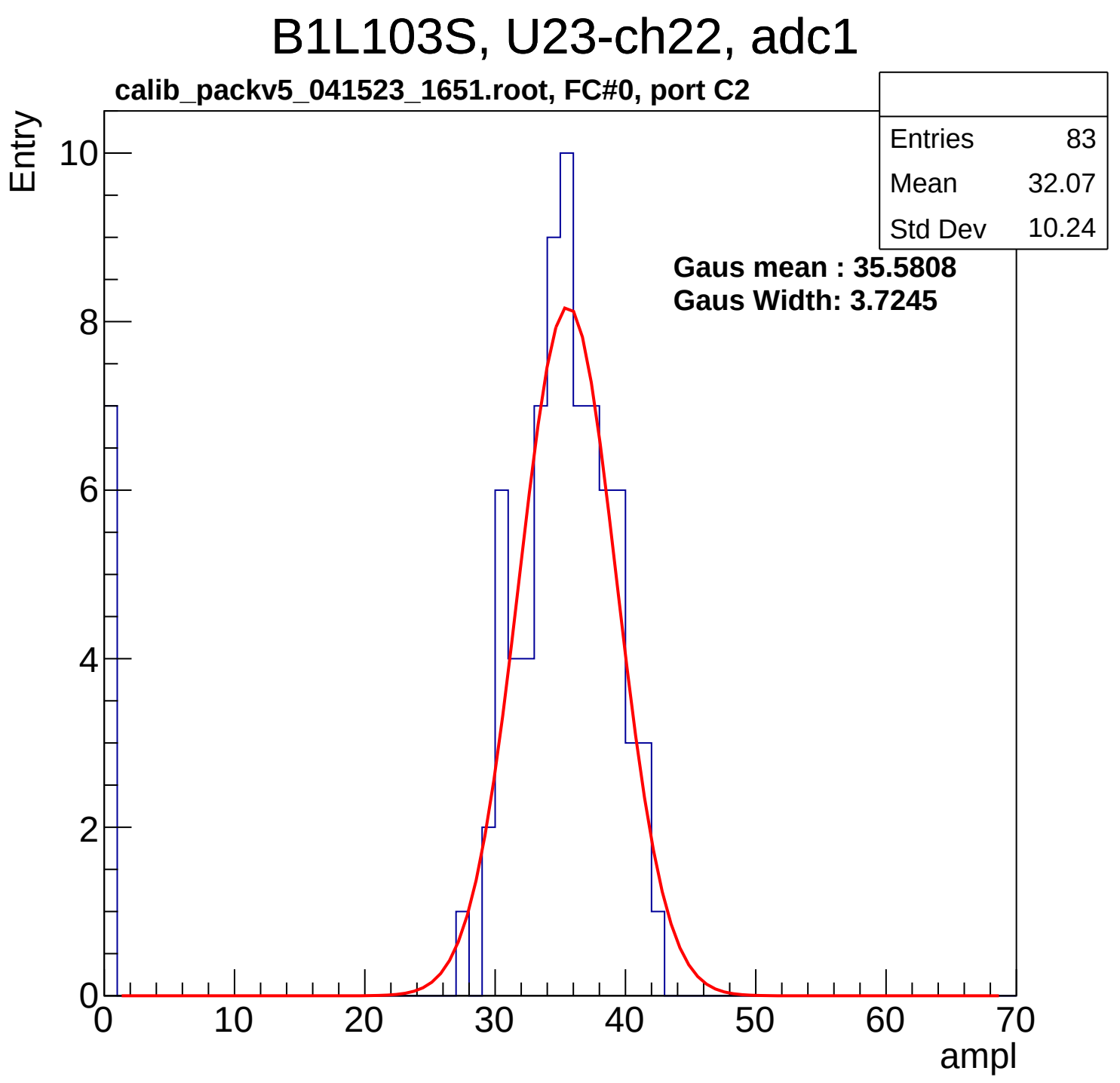

B1L103S, U23-ch21, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 40
Mean 56.5
Std Dev 2.748
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

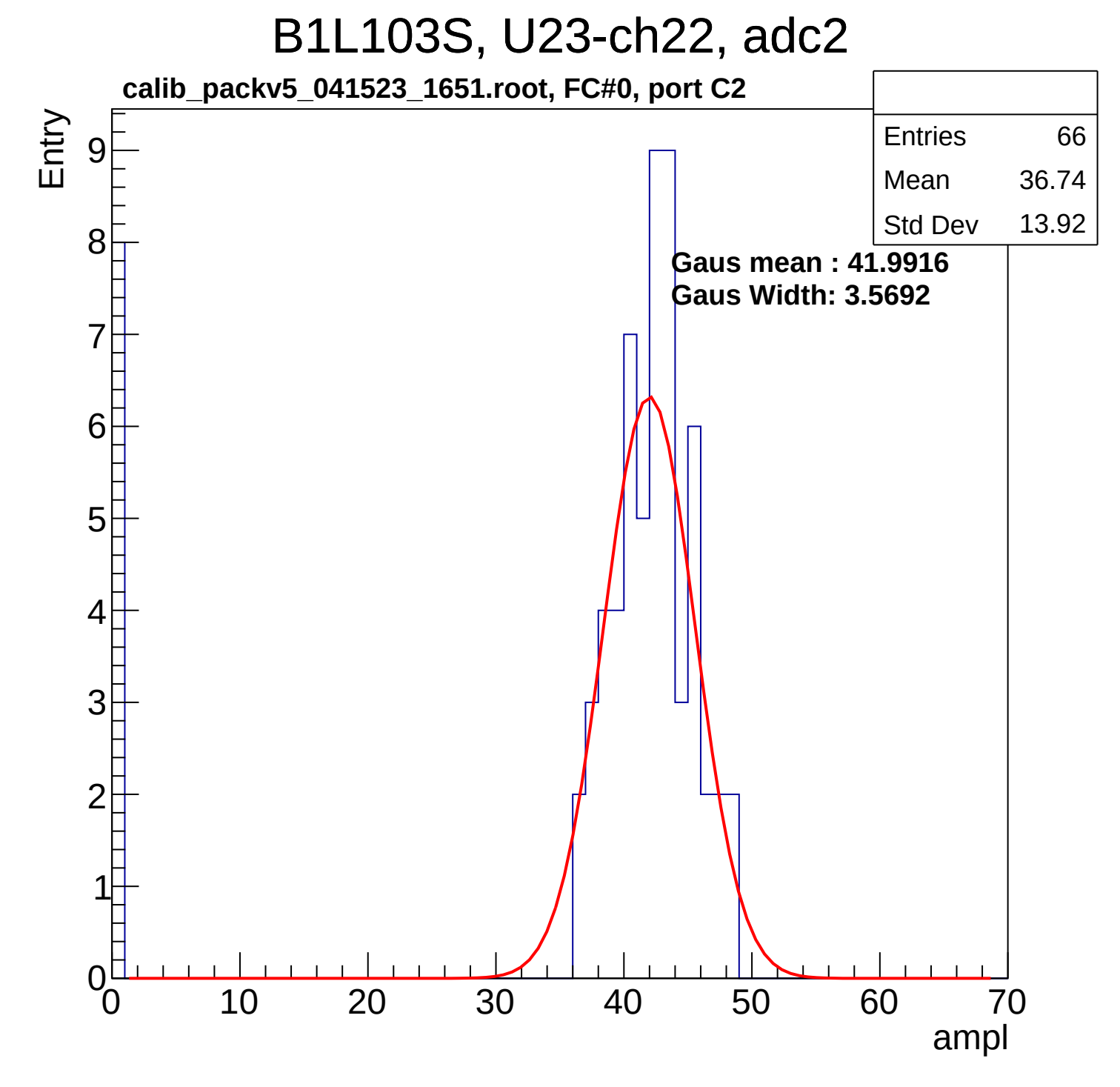

B1L103S, U23-ch21, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 59.13
Std Dev 8.551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

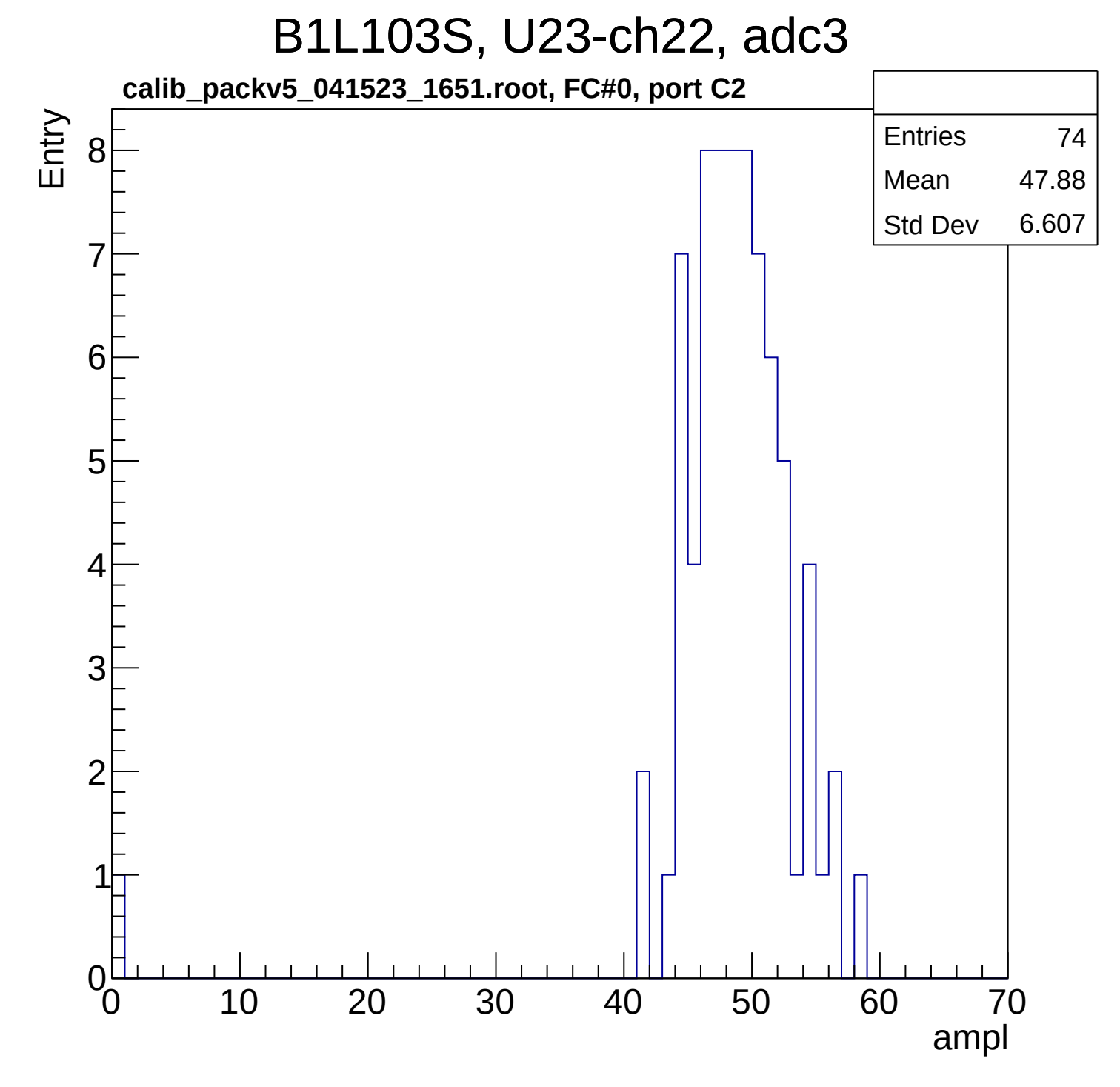

B1L103S, U23-ch21, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

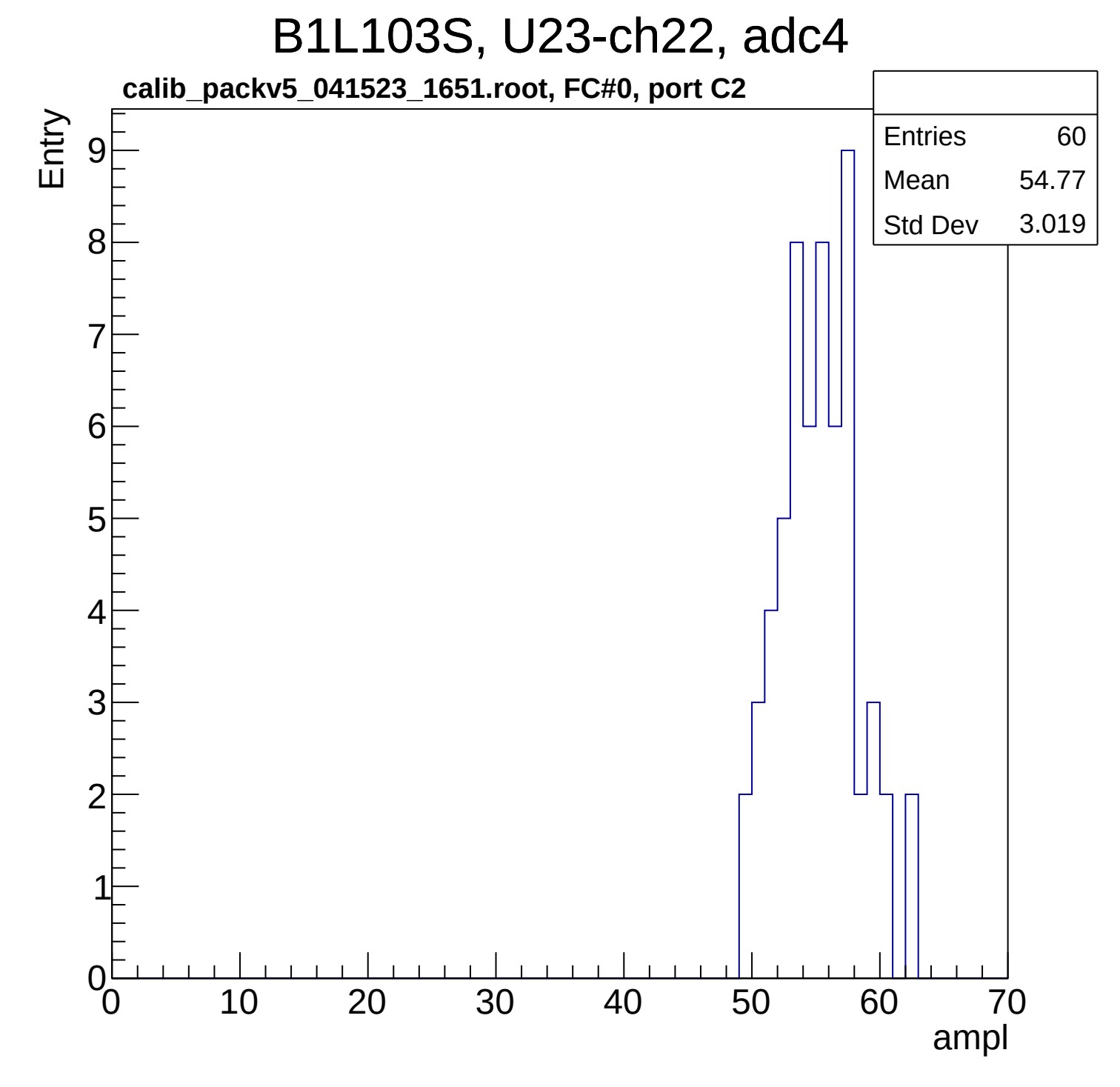

B1L103S, U23-ch21, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.158
Std Dev 4.913
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

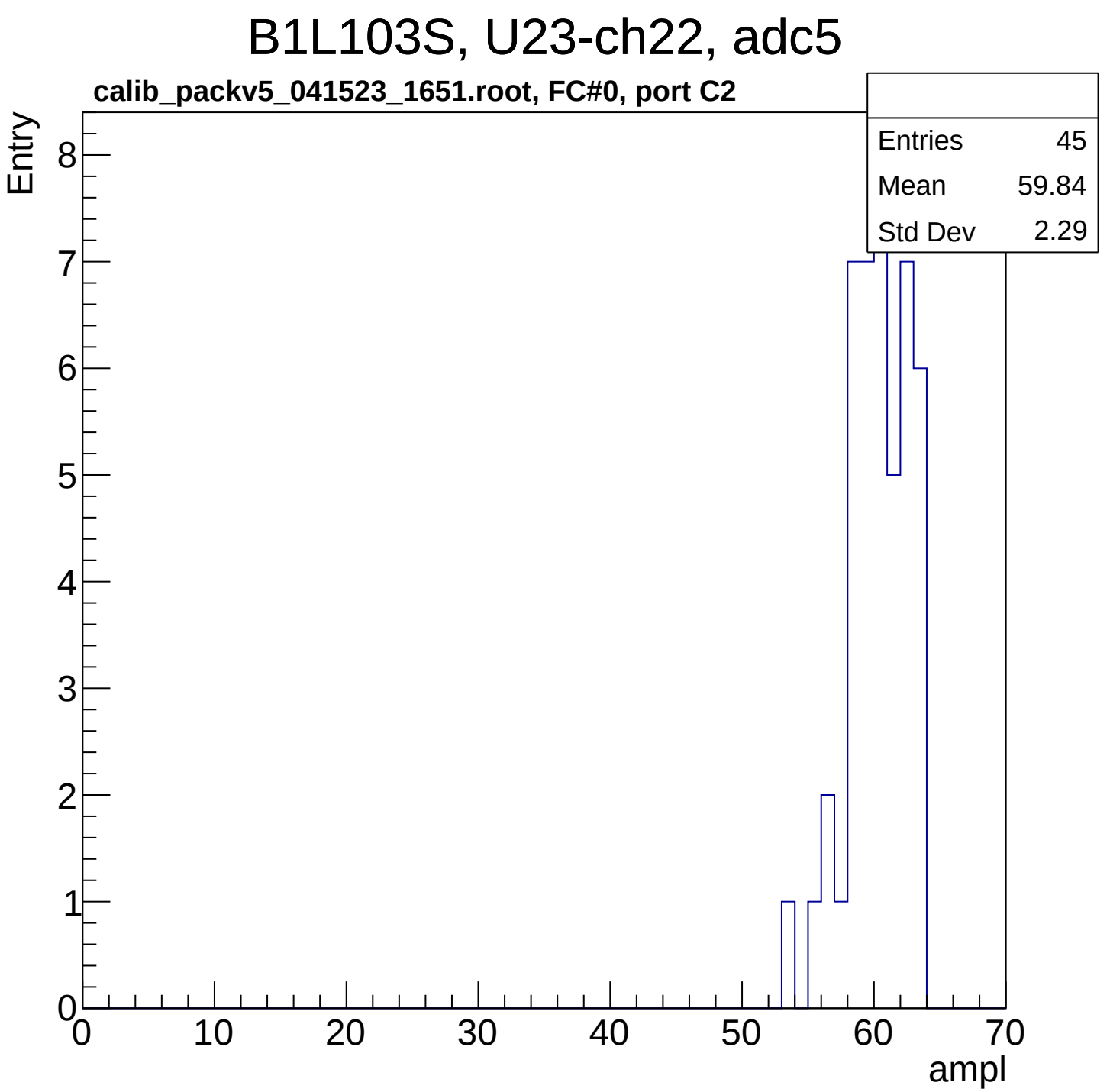

B1L103S, U23-ch22, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 22.22
Std Dev 10.93
Gaus mean : 27.6361
Gaus Width: 3.7772
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

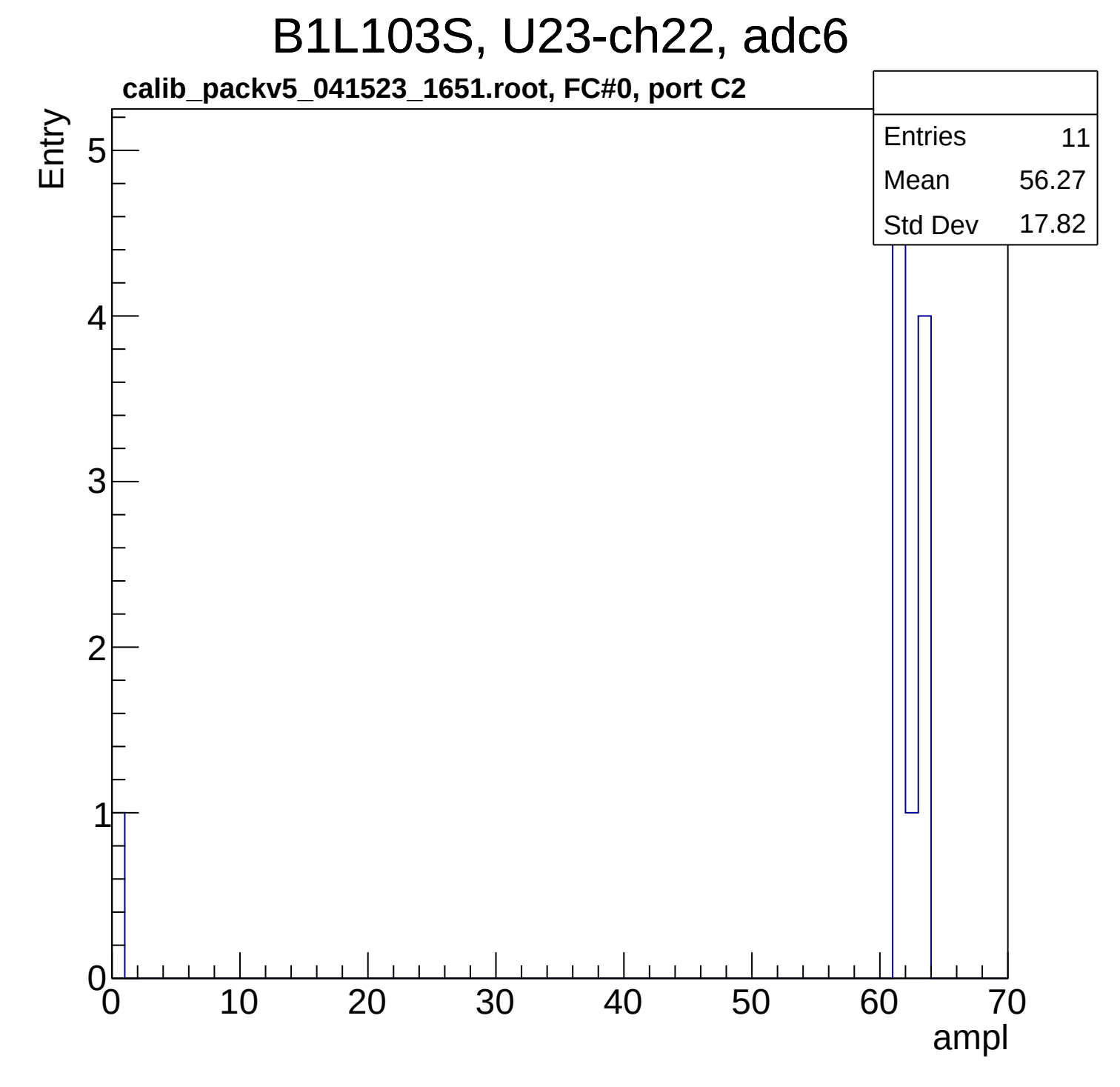

B1L103S, U23-ch22, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 32.07
Std Dev 10.24
Gaus mean : 35.5808
Gaus Width: 3.7245
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

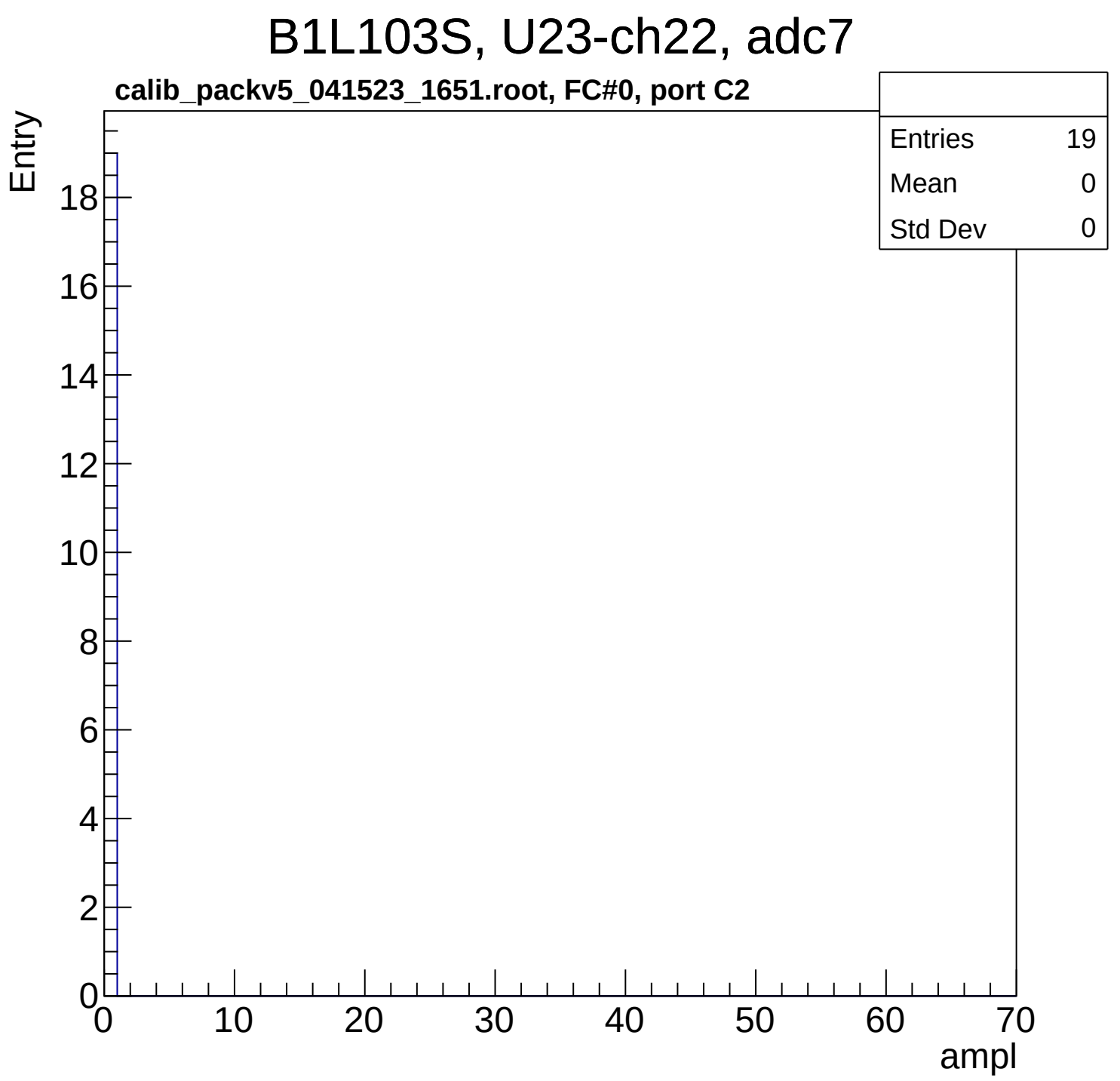

B1L103S, U23-ch22, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 36.74
Std Dev 13.92
Gaus mean : 41.9916
Gaus Width: 3.5692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

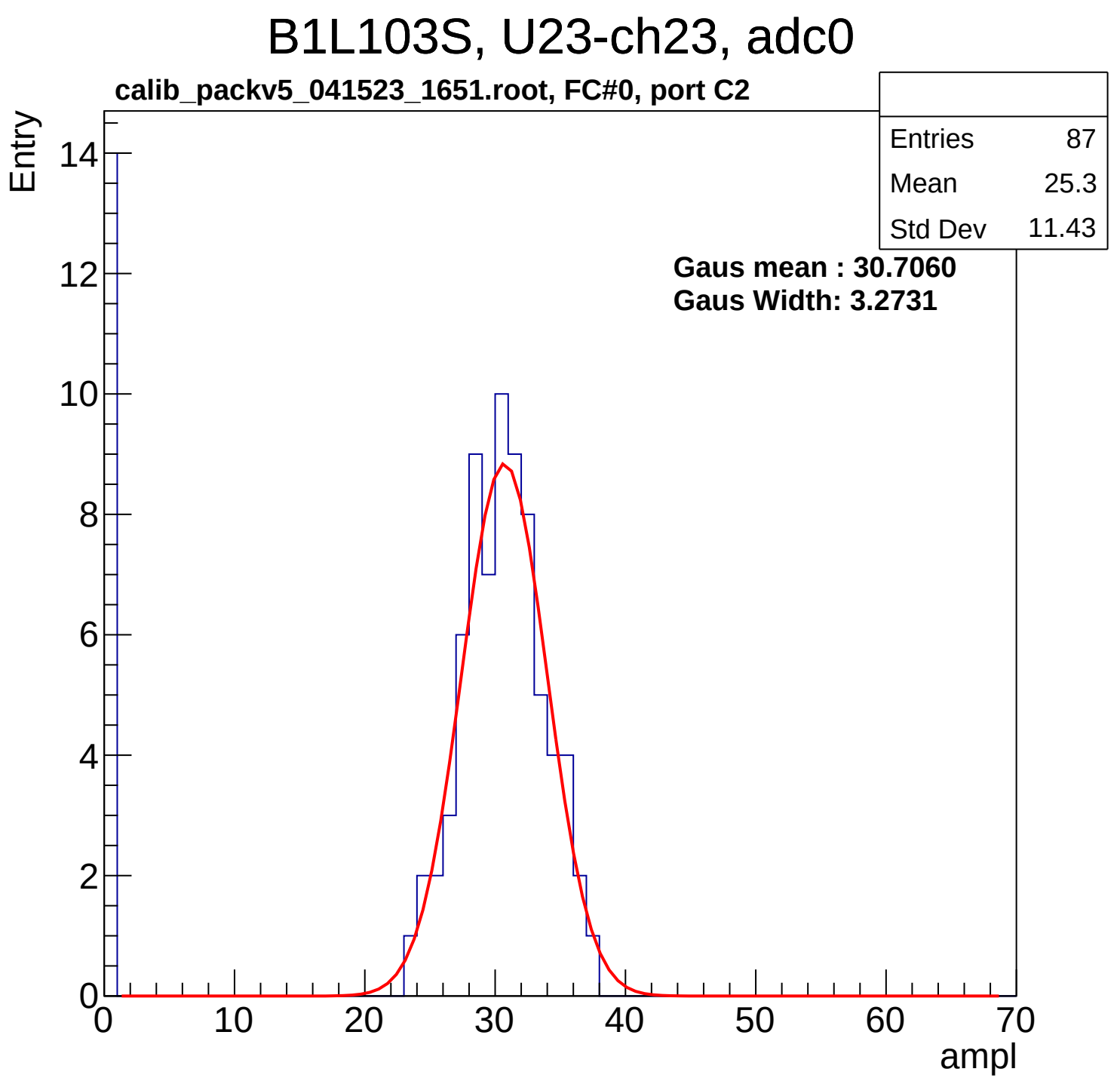

B1L103S, U23-ch22, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 47.88
Std Dev 6.607
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

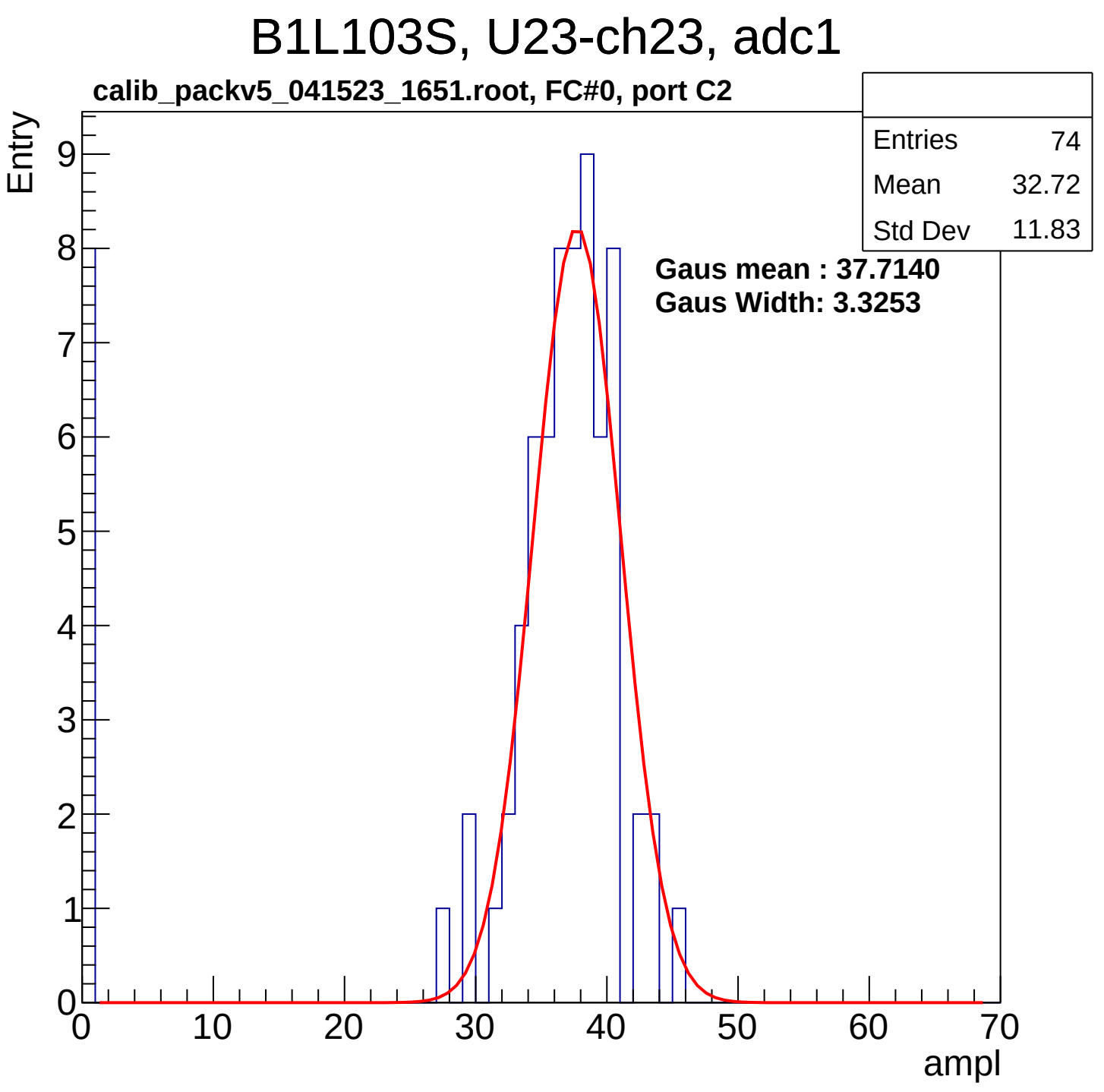

B1L103S, U23-ch22, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 54.77
Std Dev 3.019
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

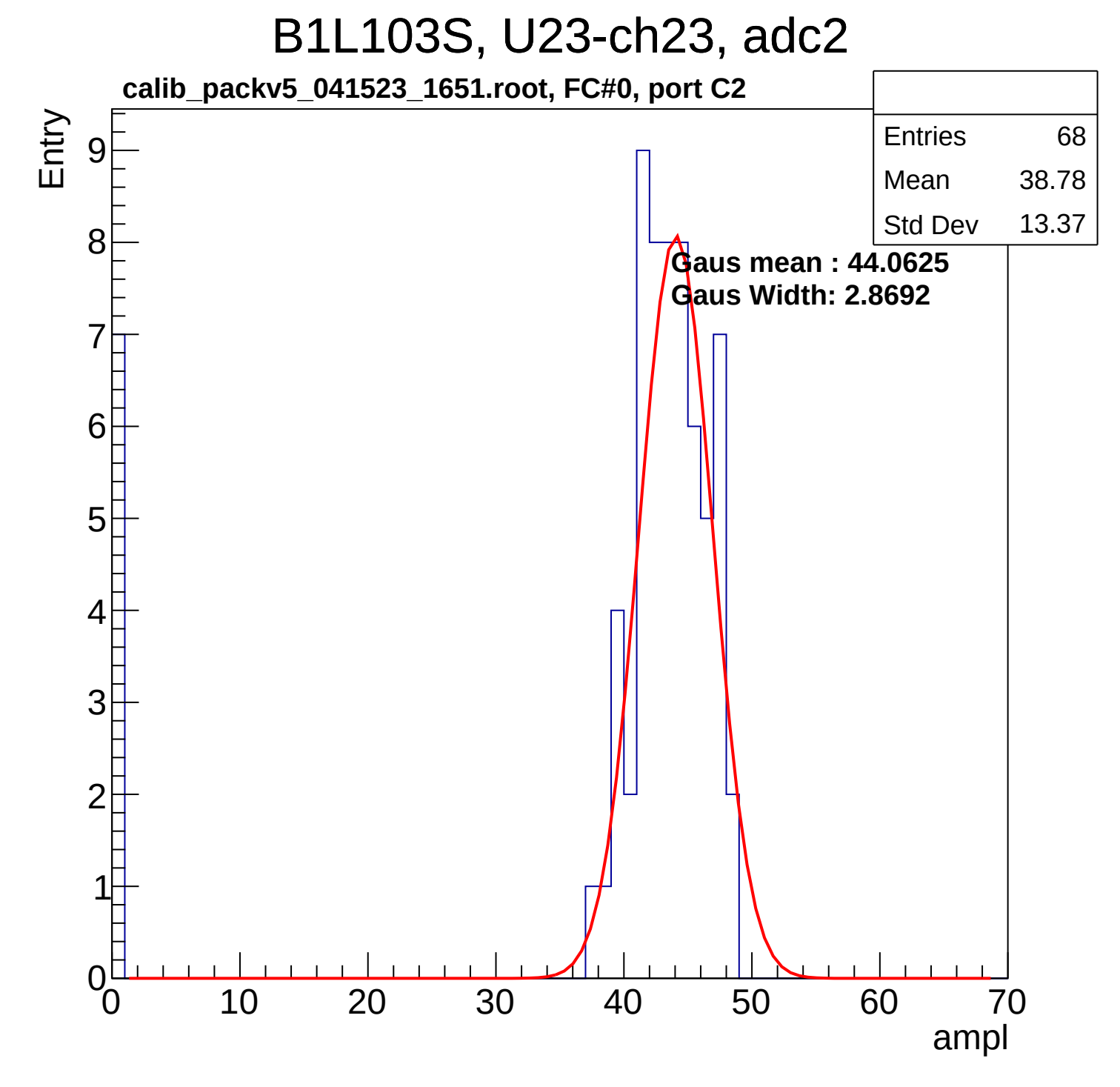

B1L103S, U23-ch22, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 45
Mean 59.84
Std Dev 2.29
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

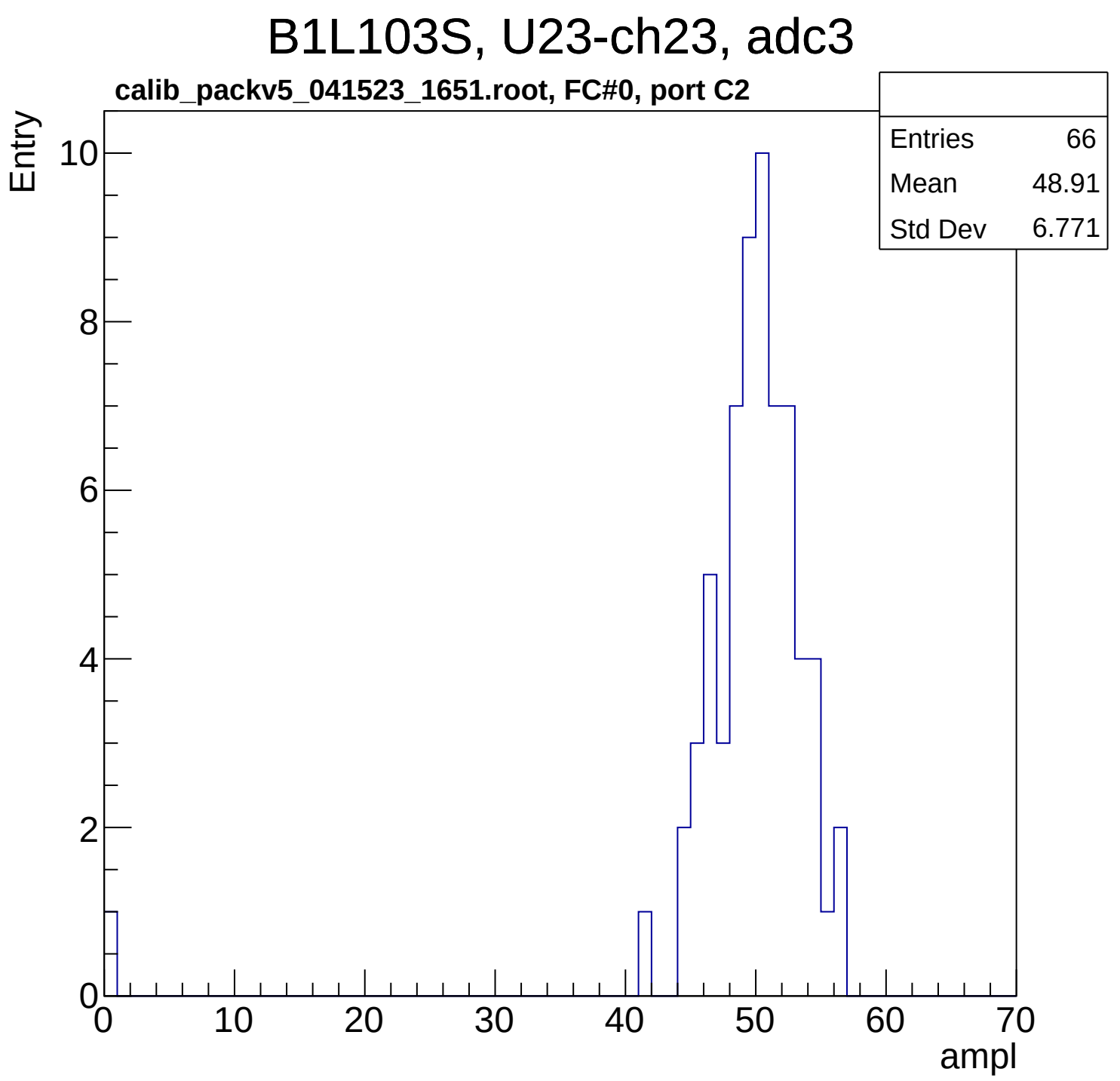

B1L103S, U23-ch22, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 56.27
Std Dev 17.82
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

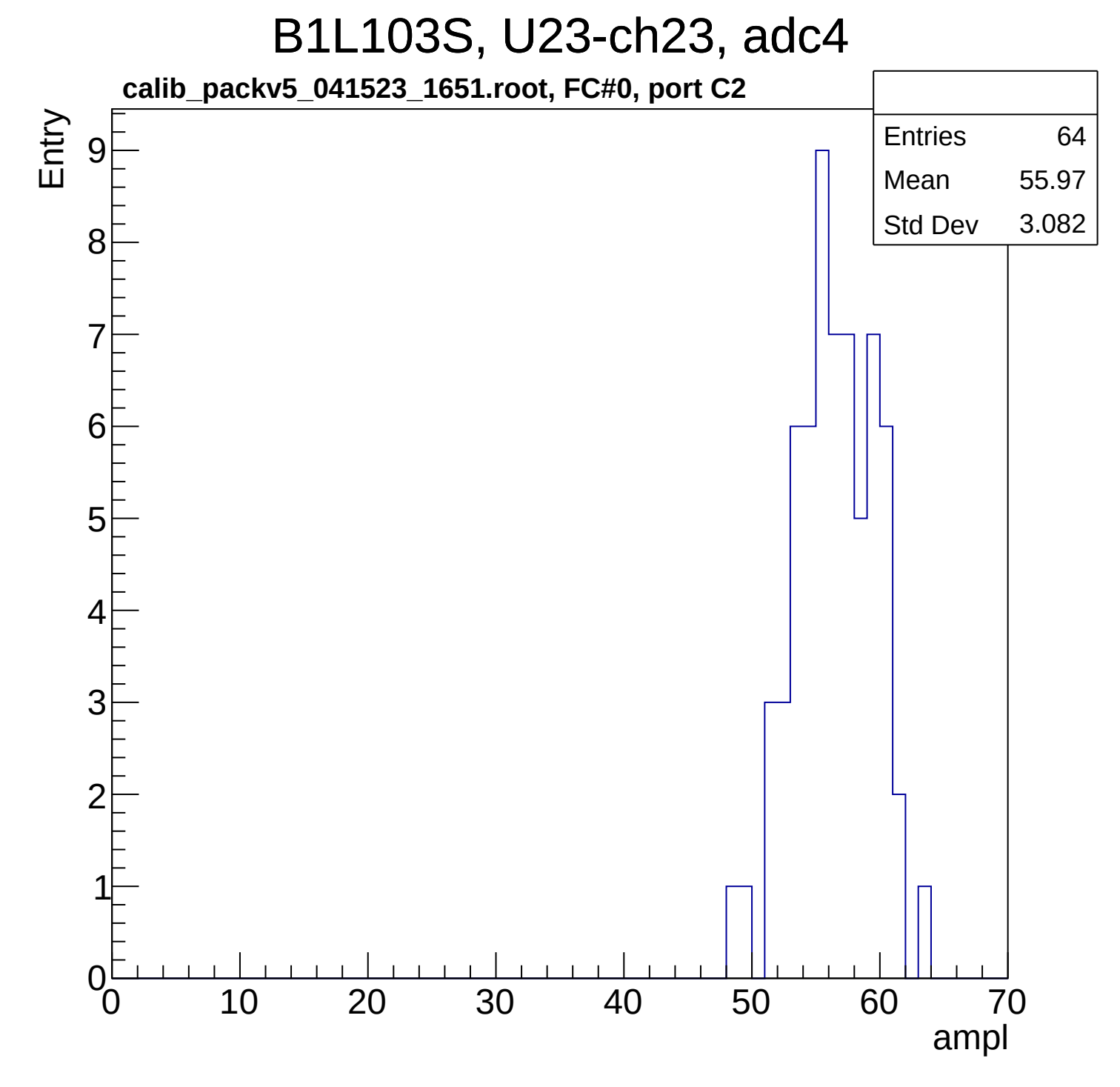

B1L103S, U23-ch22, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

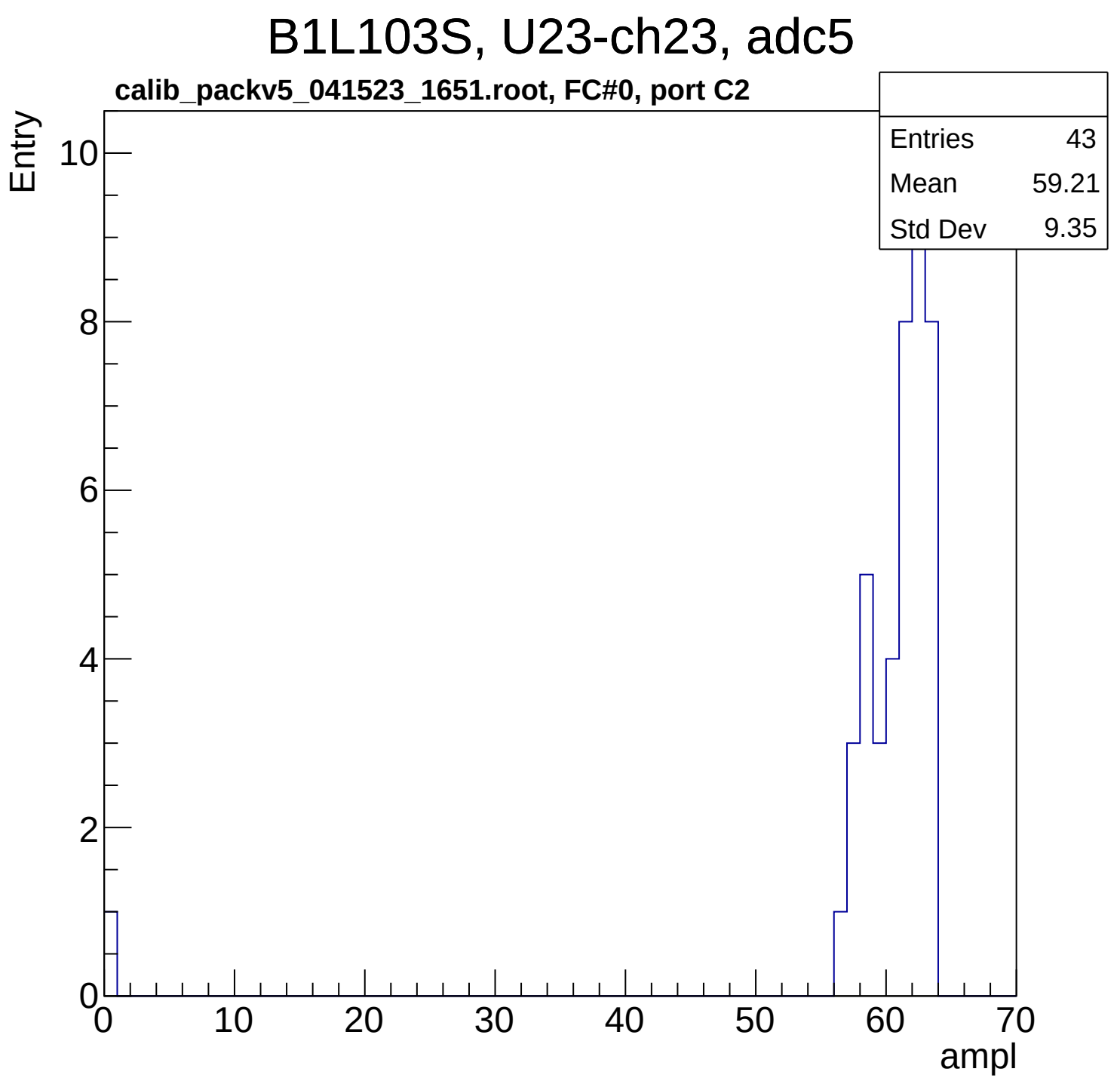

B1L103S, U23-ch23, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 87
Mean 25.3
Std Dev 11.43
Gaus mean : 30.7060
Gaus Width: 3.2731
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

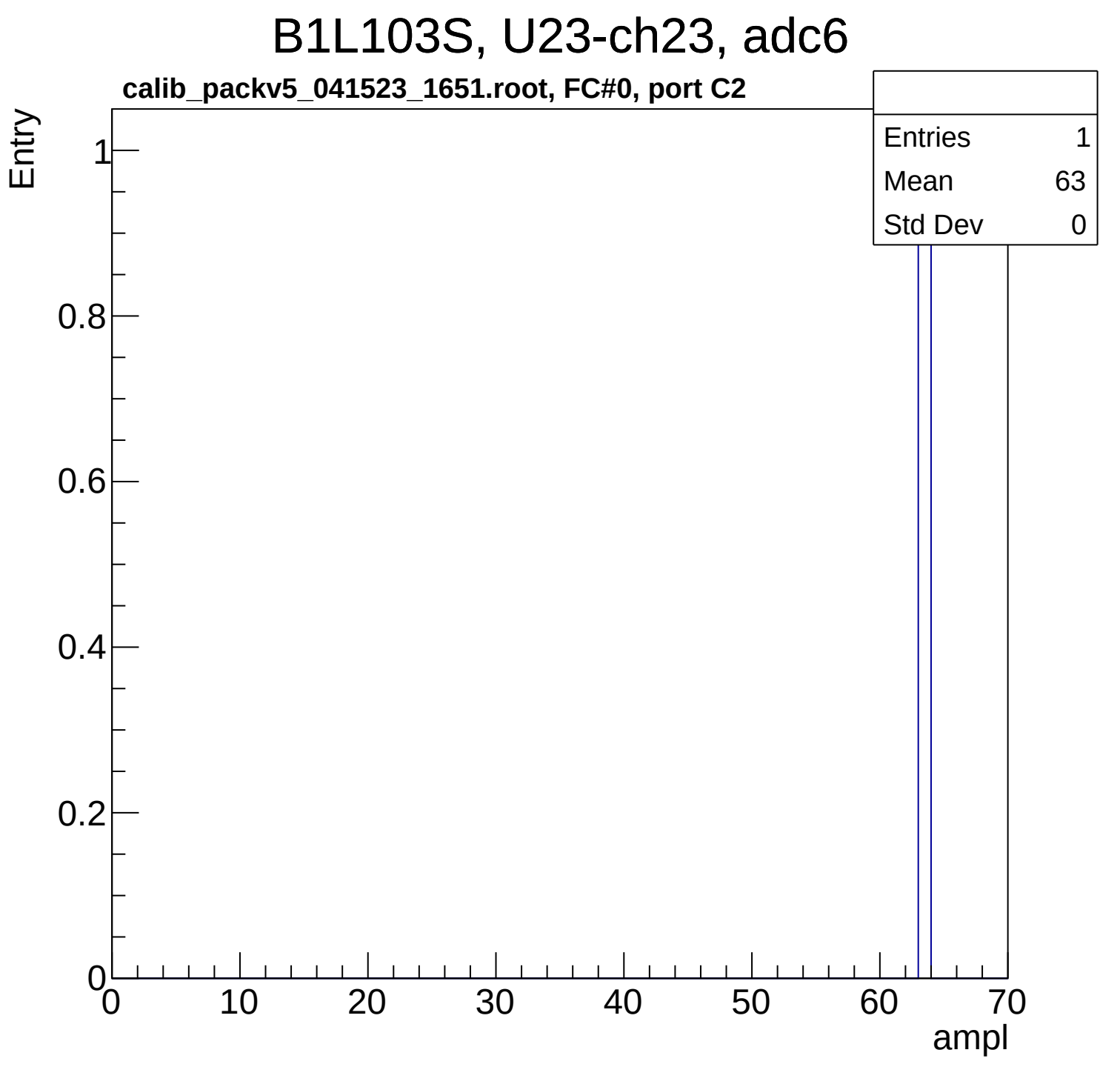

B1L103S, U23-ch23, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 32.72
Std Dev 11.83
Gaus mean : 37.7140
Gaus Width: 3.3253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

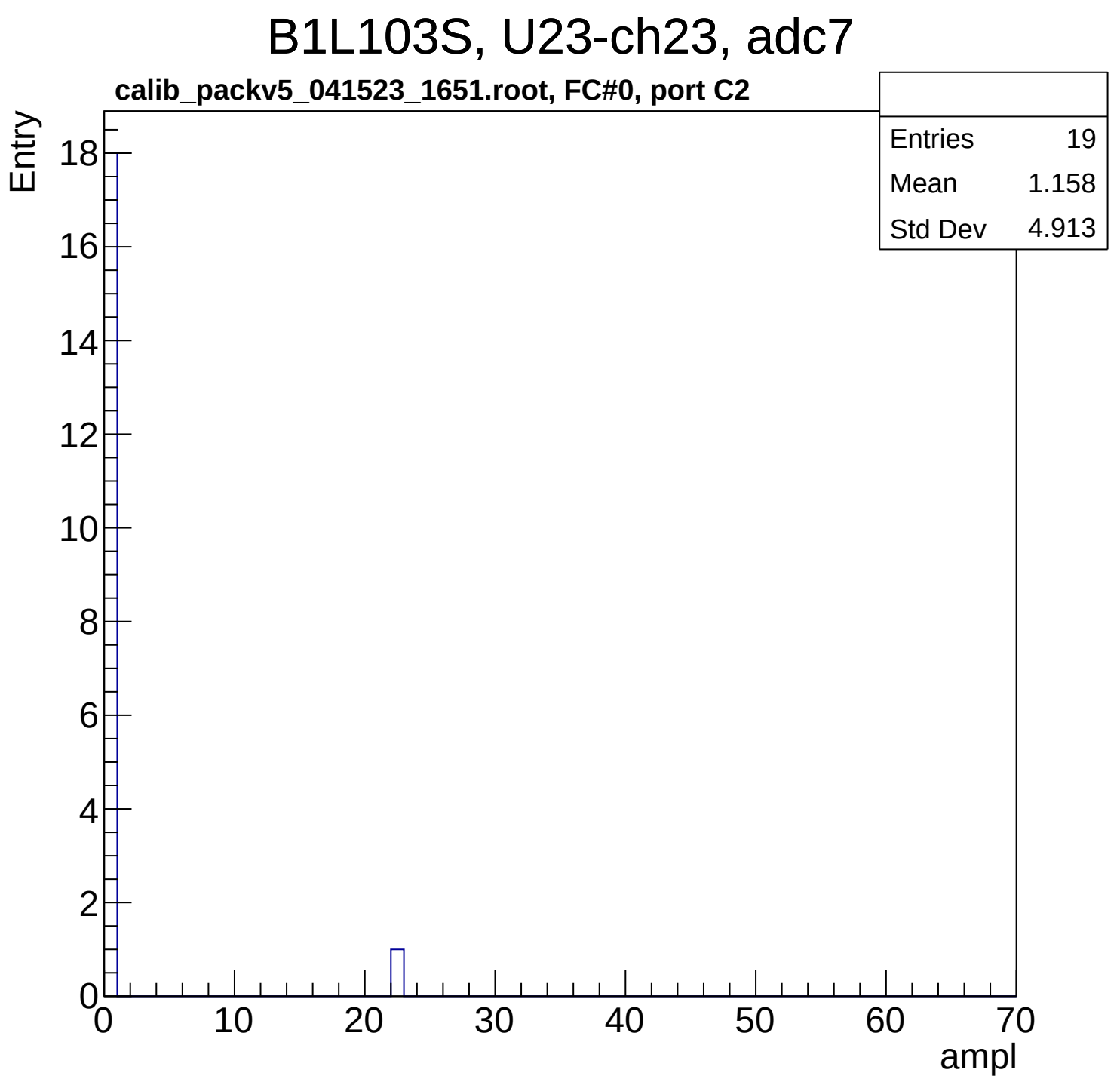

B1L103S, U23-ch23, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 38.78
Std Dev 13.37
Gaus mean : 44.0625
Gaus Width: 2.8692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

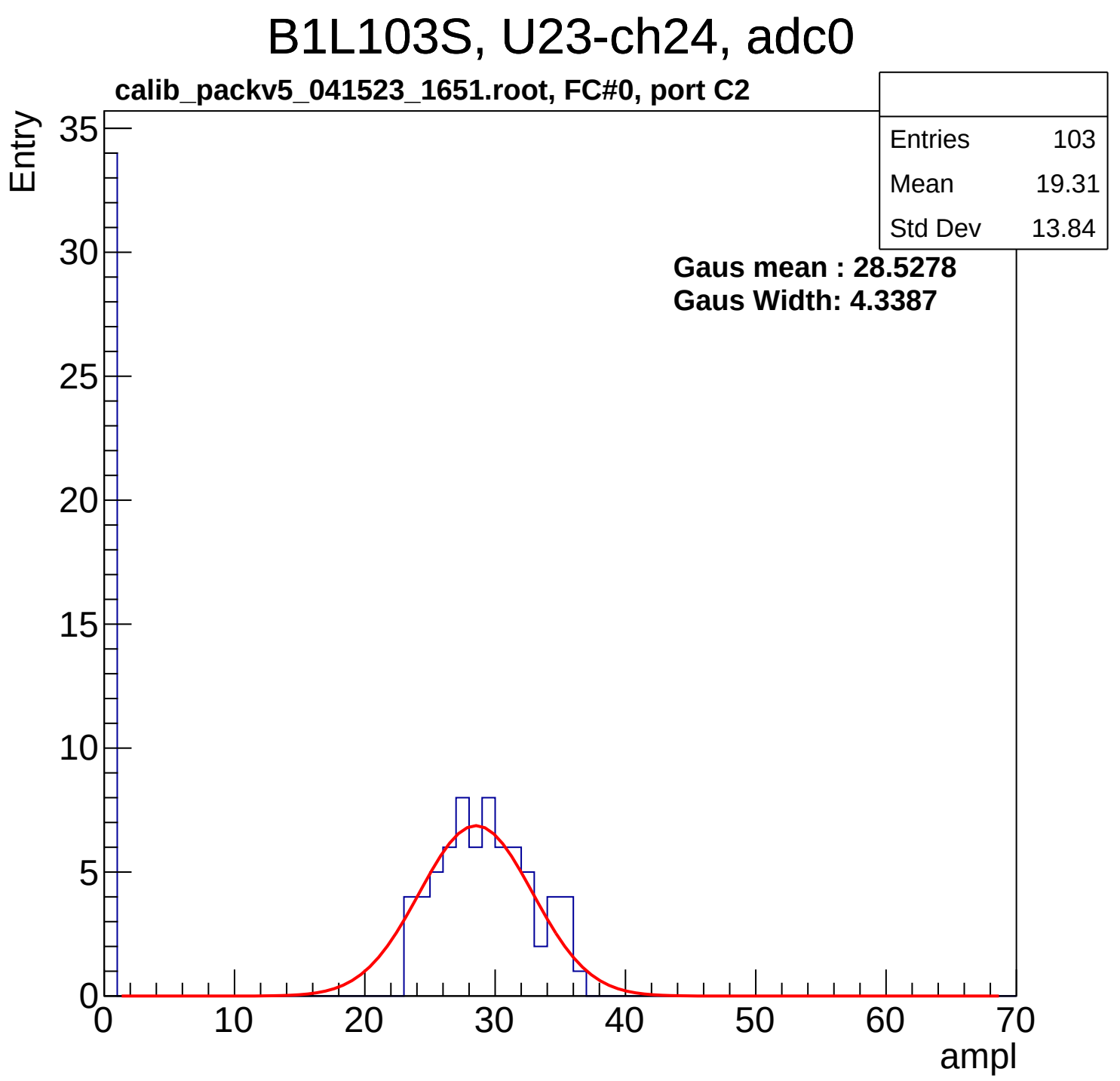

B1L103S, U23-ch23, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 48.91
Std Dev 6.771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

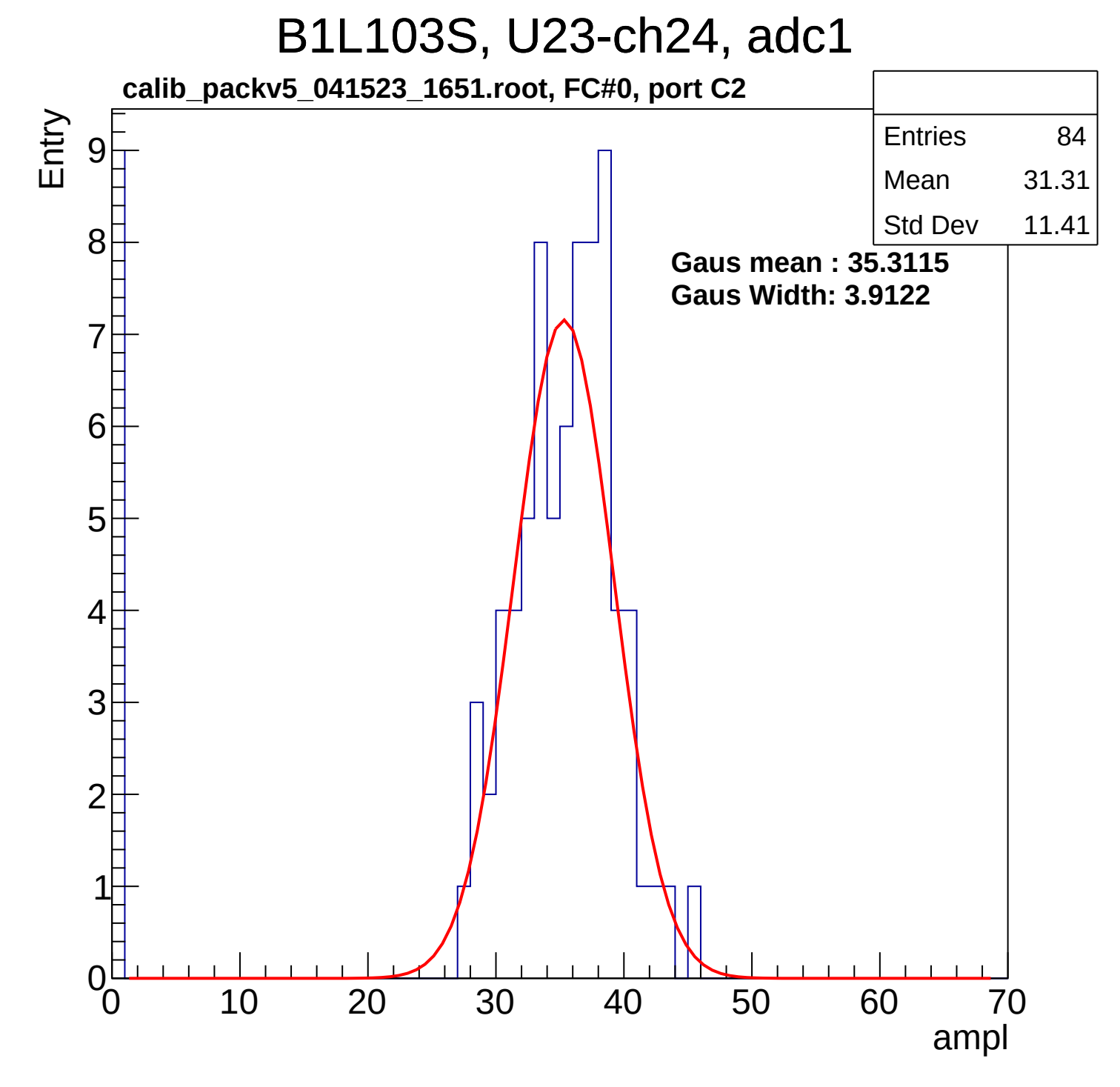

B1L103S, U23-ch23, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 55.97
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

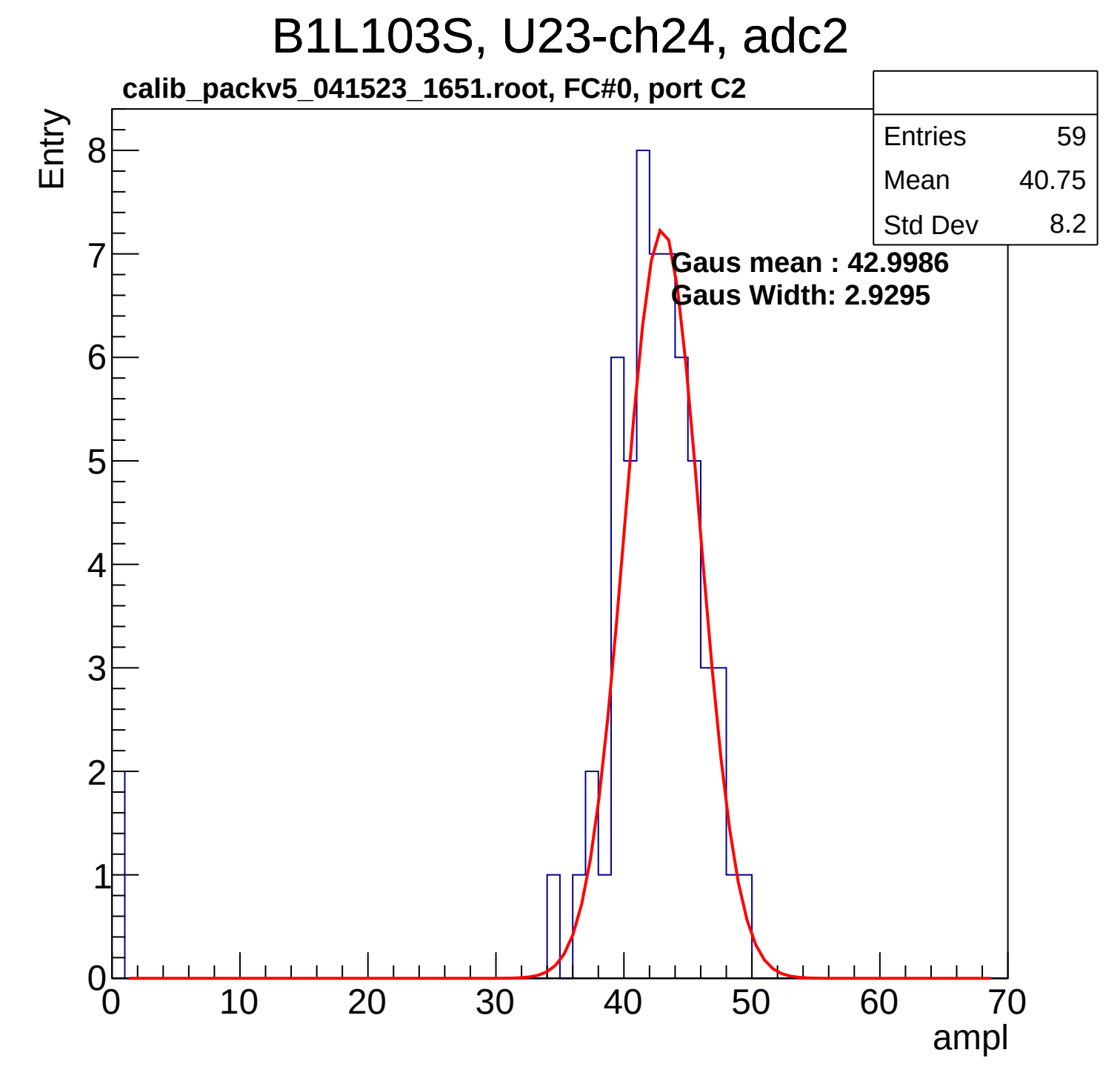

B1L103S, U23-ch23, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 43
Mean 59.21
Std Dev 9.35
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

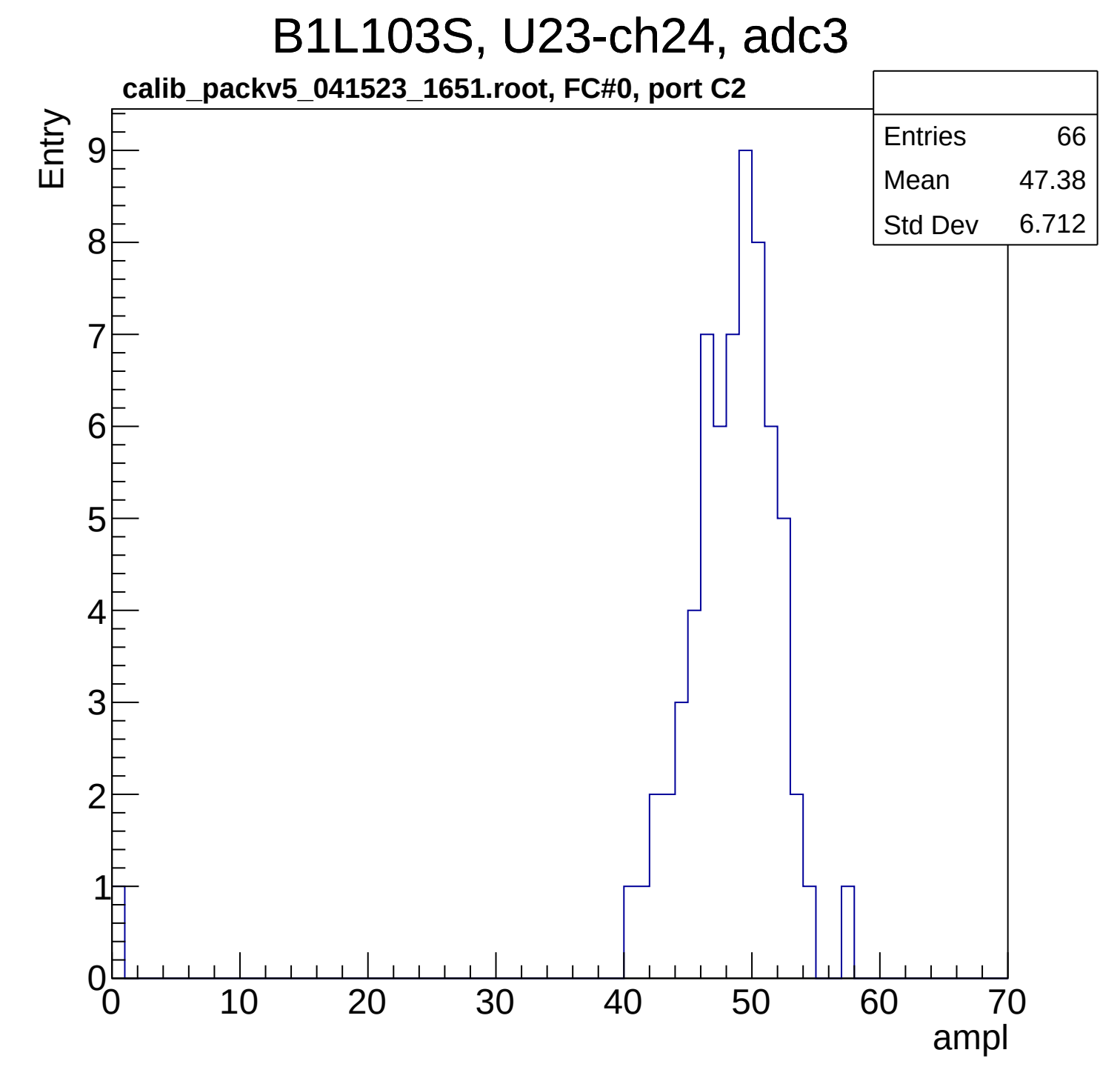

B1L103S, U23-ch23, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

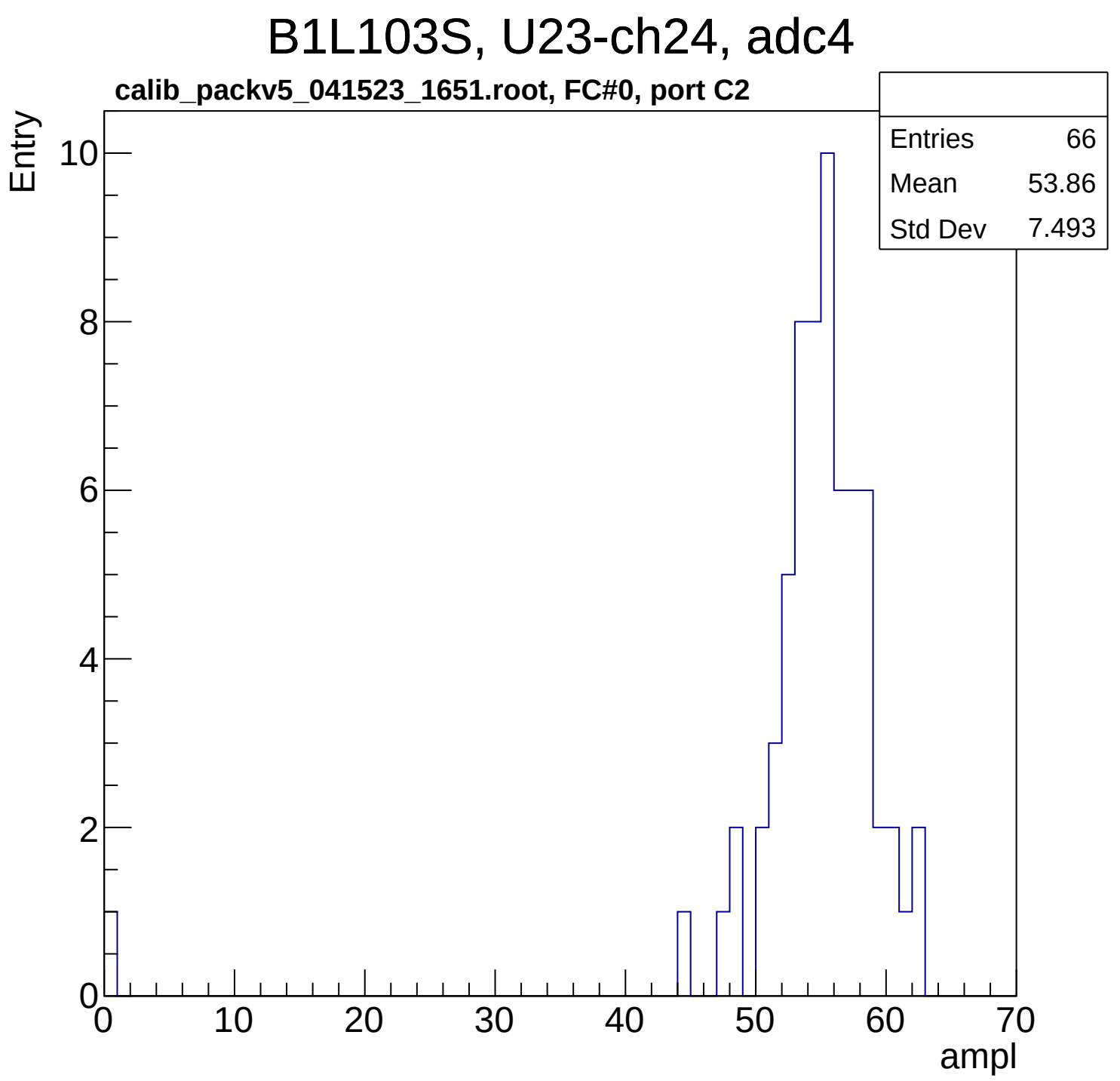

B1L103S, U23-ch23, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.158
Std Dev 4.913
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

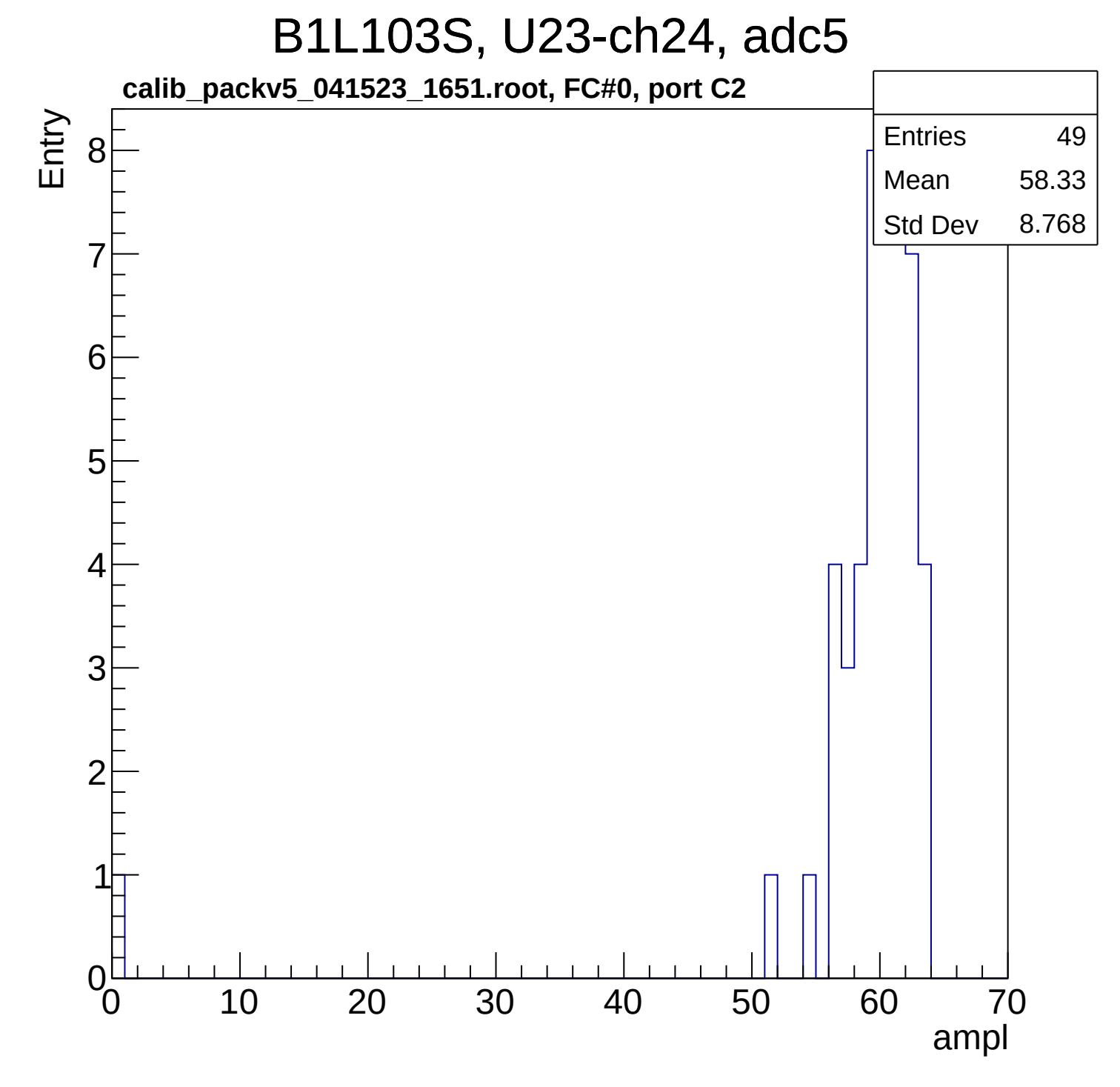

B1L103S, U23-ch24, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 103
Mean 19.31
Std Dev 13.84
Gaus mean : 28.5278
Gaus Width: 4.3387
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

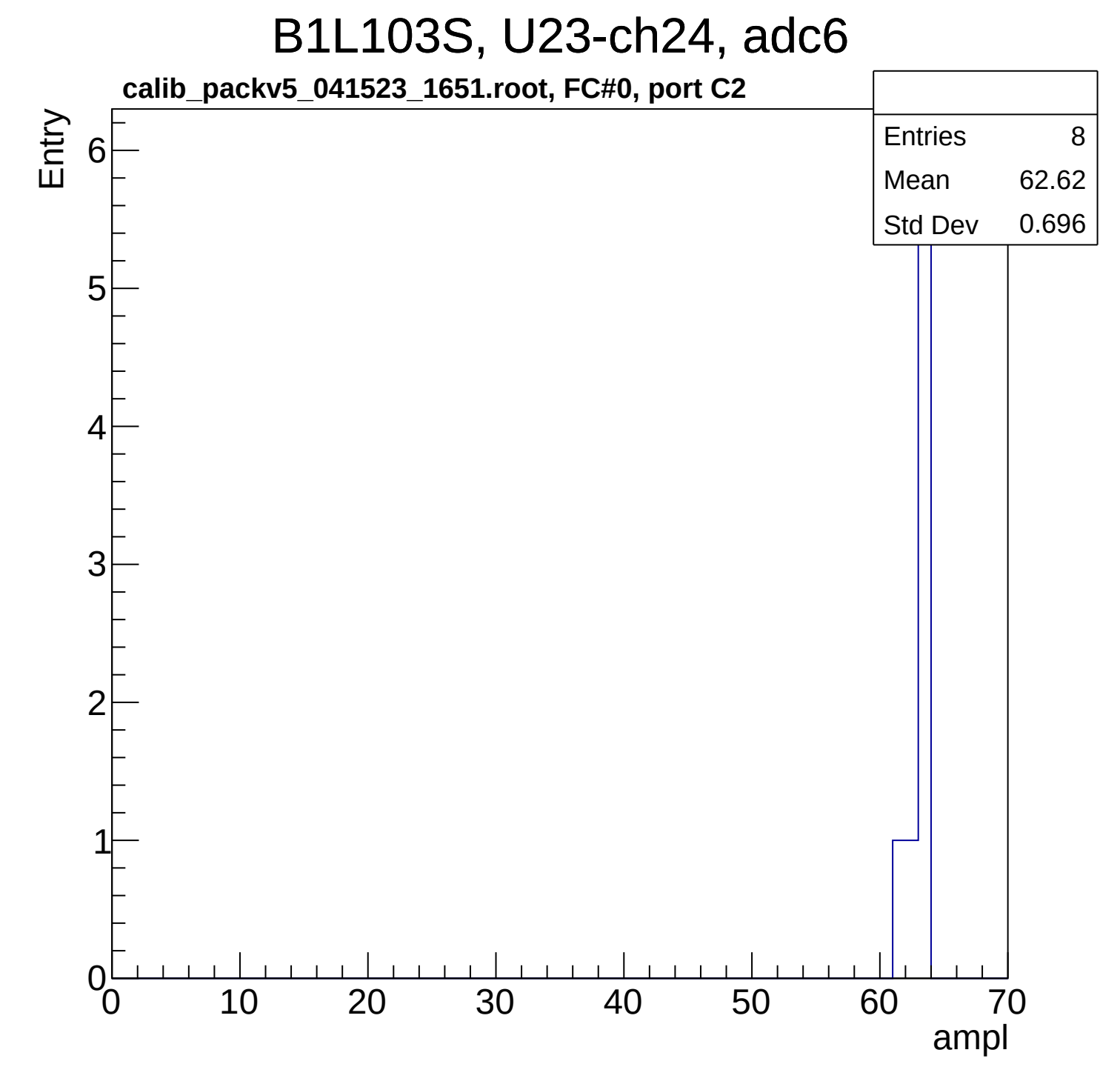

B1L103S, U23-ch24, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 84
Mean 31.31
Std Dev 11.41
Gaus mean : 35.3115
Gaus Width: 3.9122
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

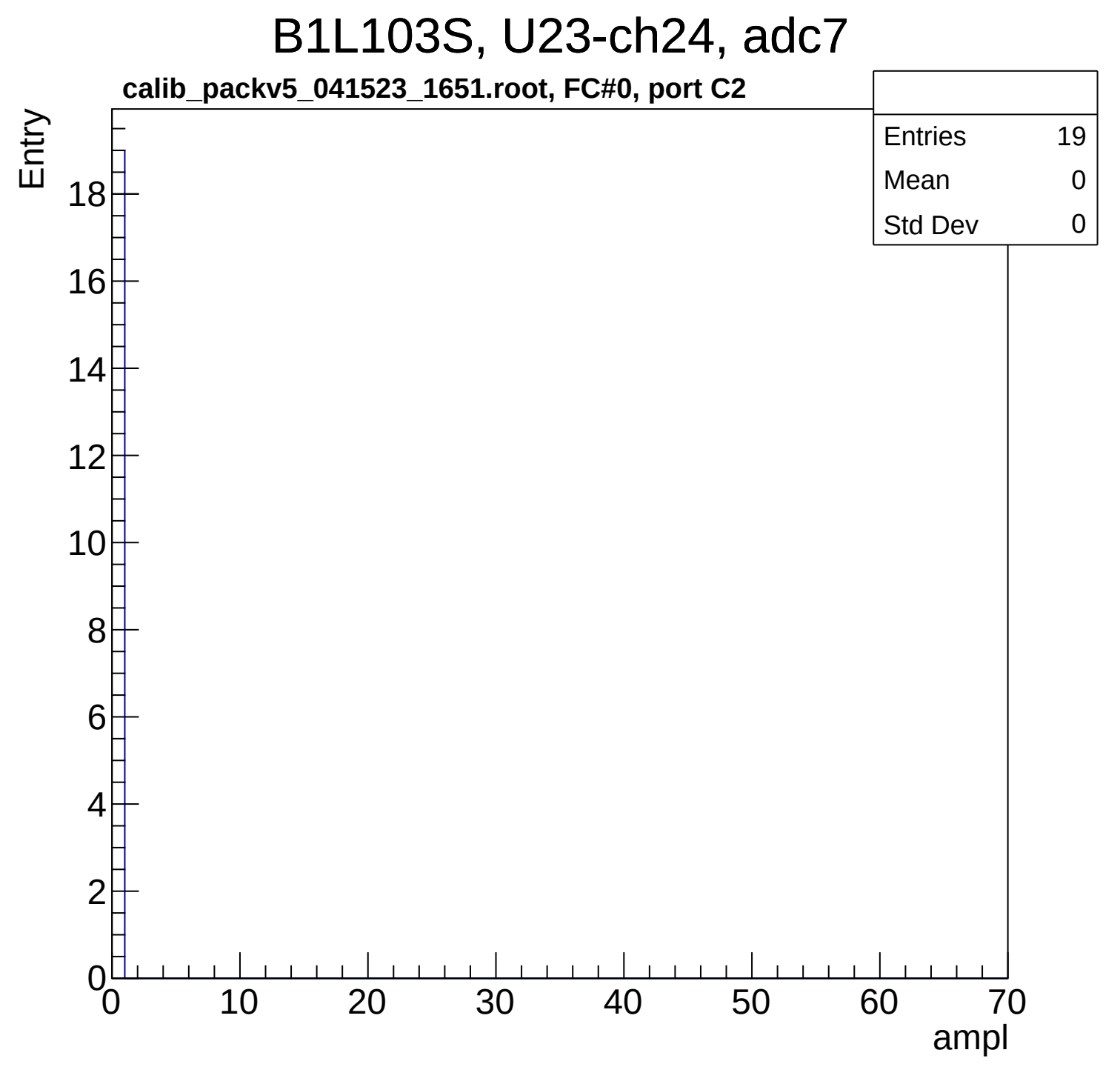

B1L103S, U23-ch24, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 40.75
Std Dev 8.2
Gaus mean : 42.9986
Gaus Width: 2.9295
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

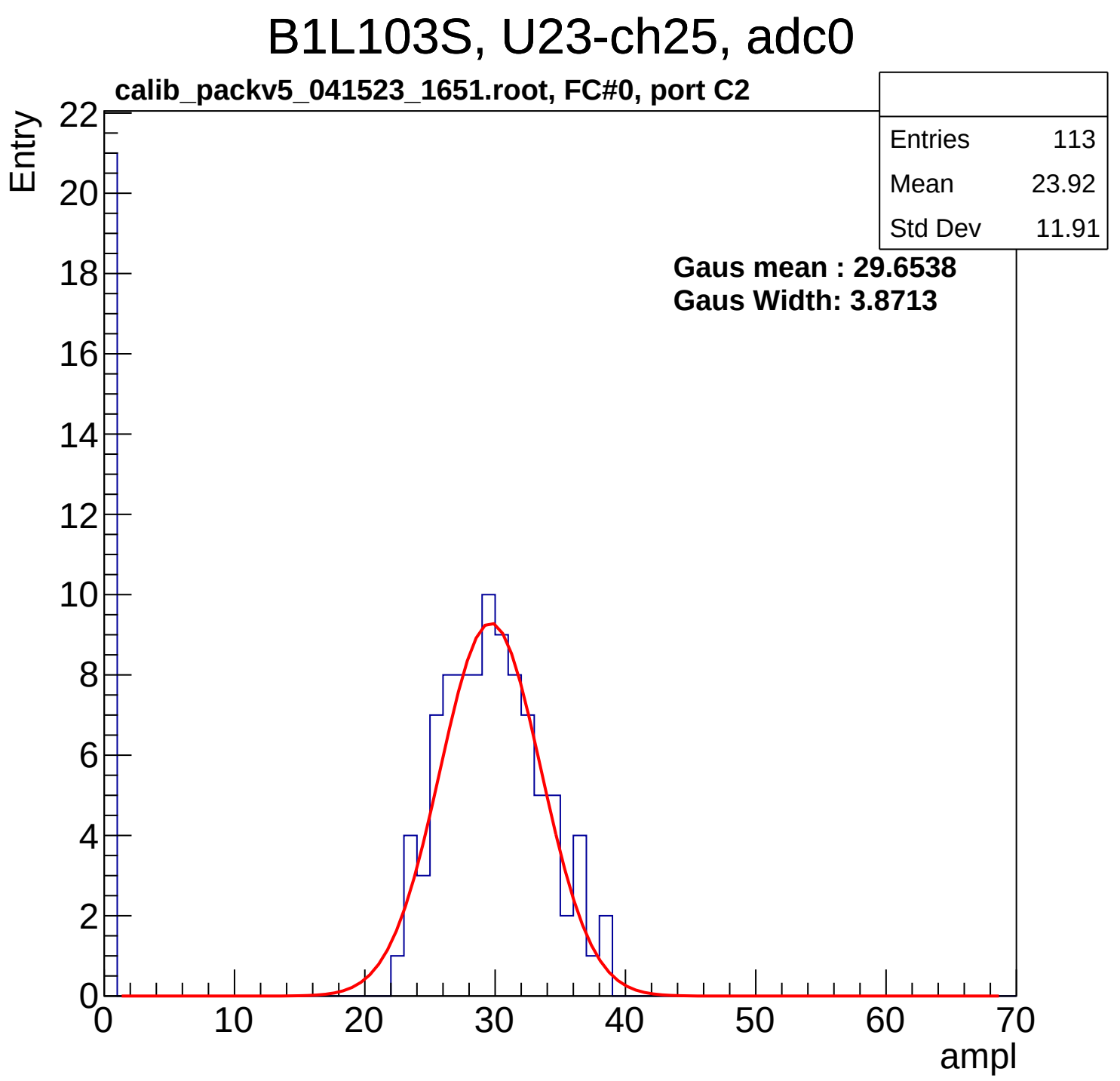

B1L103S, U23-ch24, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 47.38
Std Dev 6.712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

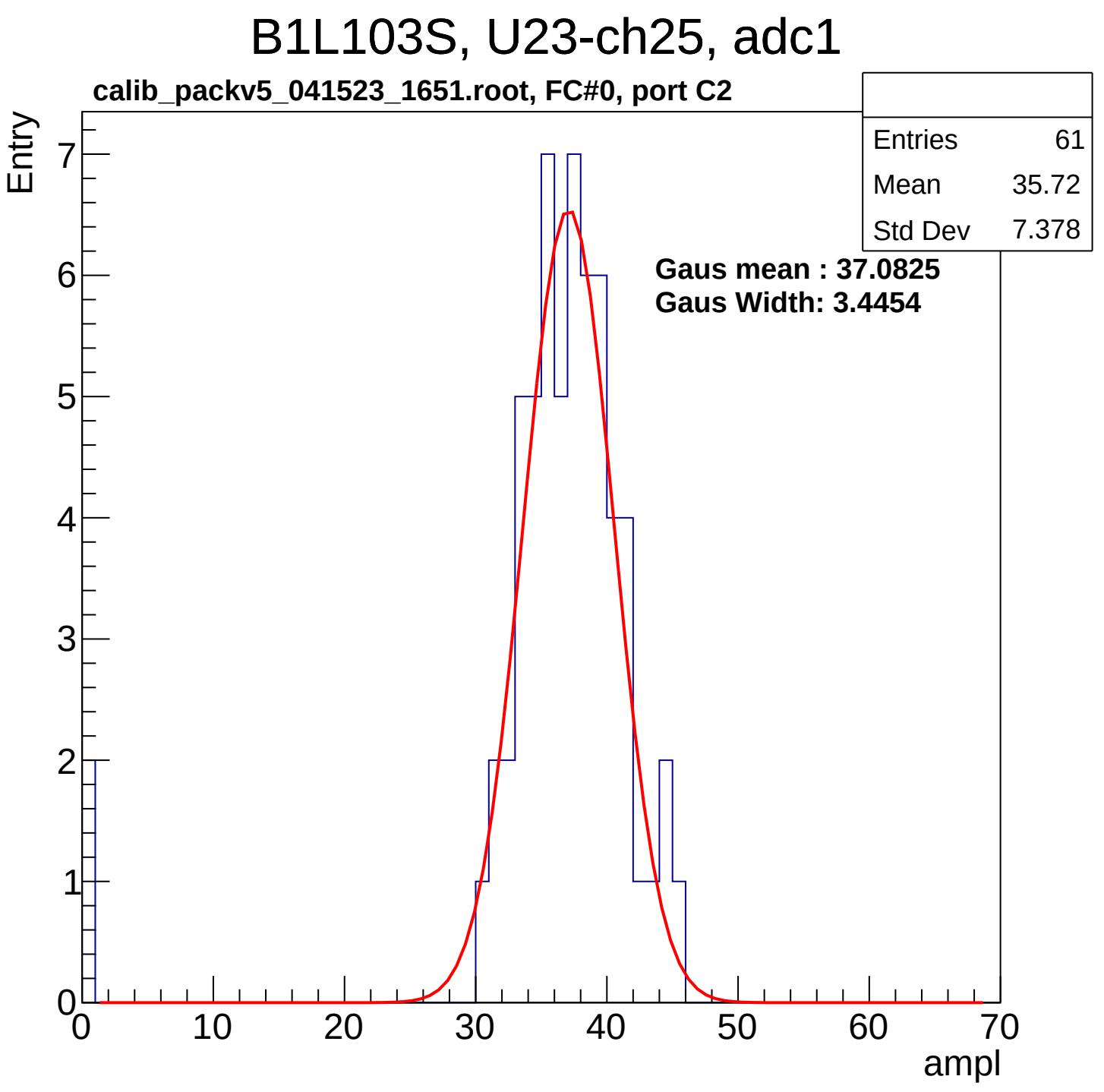

B1L103S, U23-ch24, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 53.86
Std Dev 7.493
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

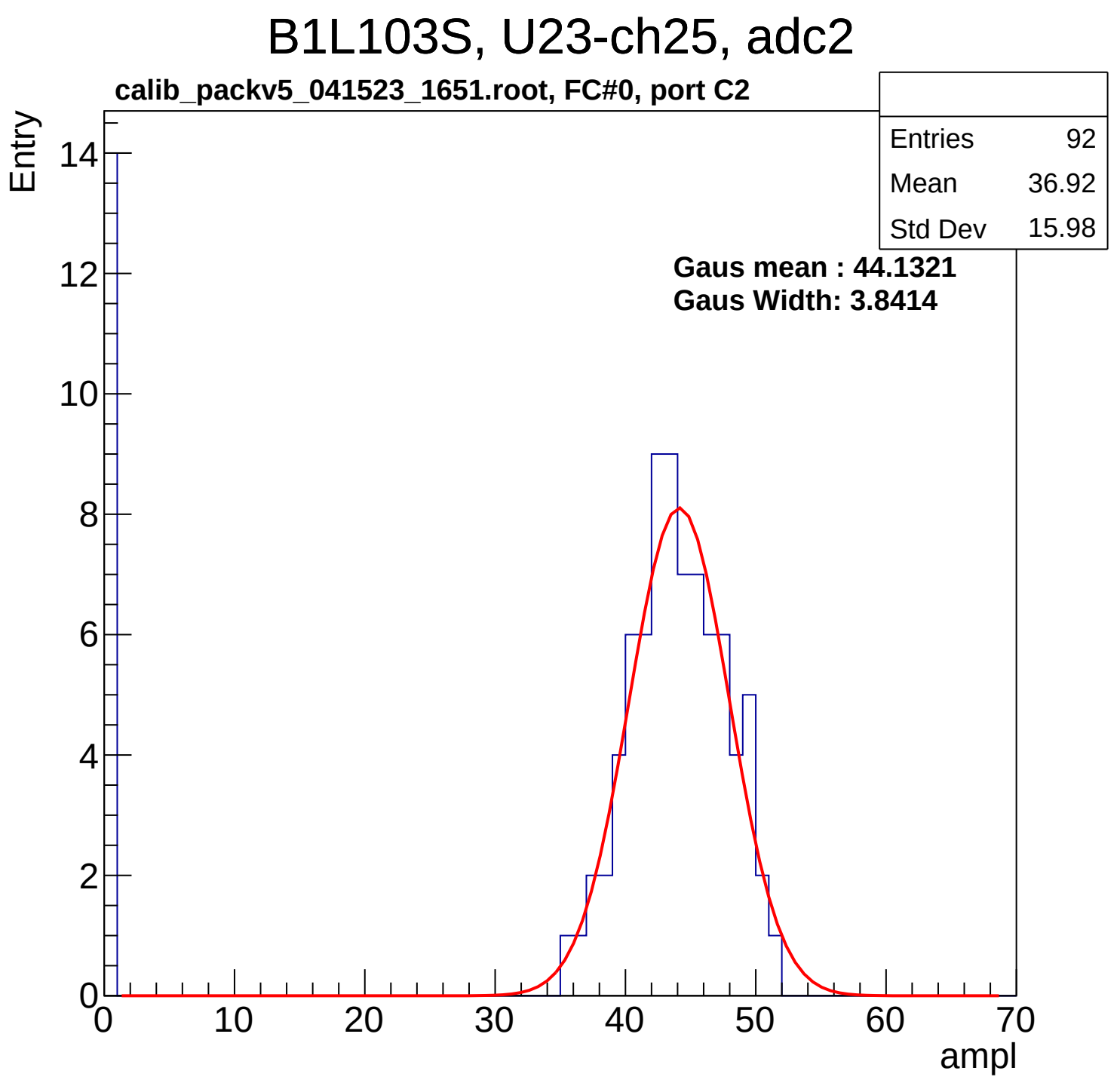

B1L103S, U23-ch24, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 58.33
Std Dev 8.768
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

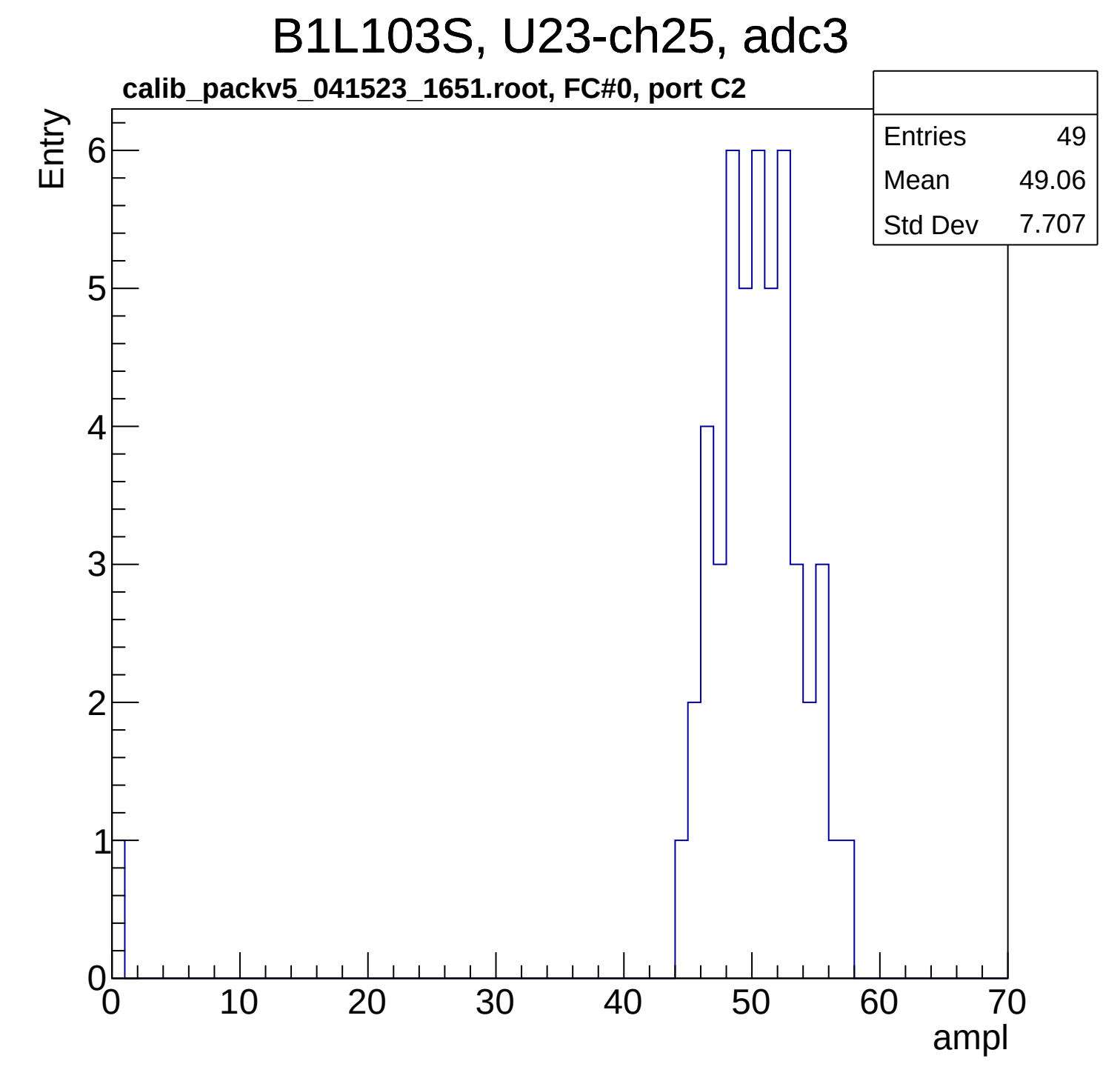

B1L103S, U23-ch24, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 62.62
Std Dev 0.696
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

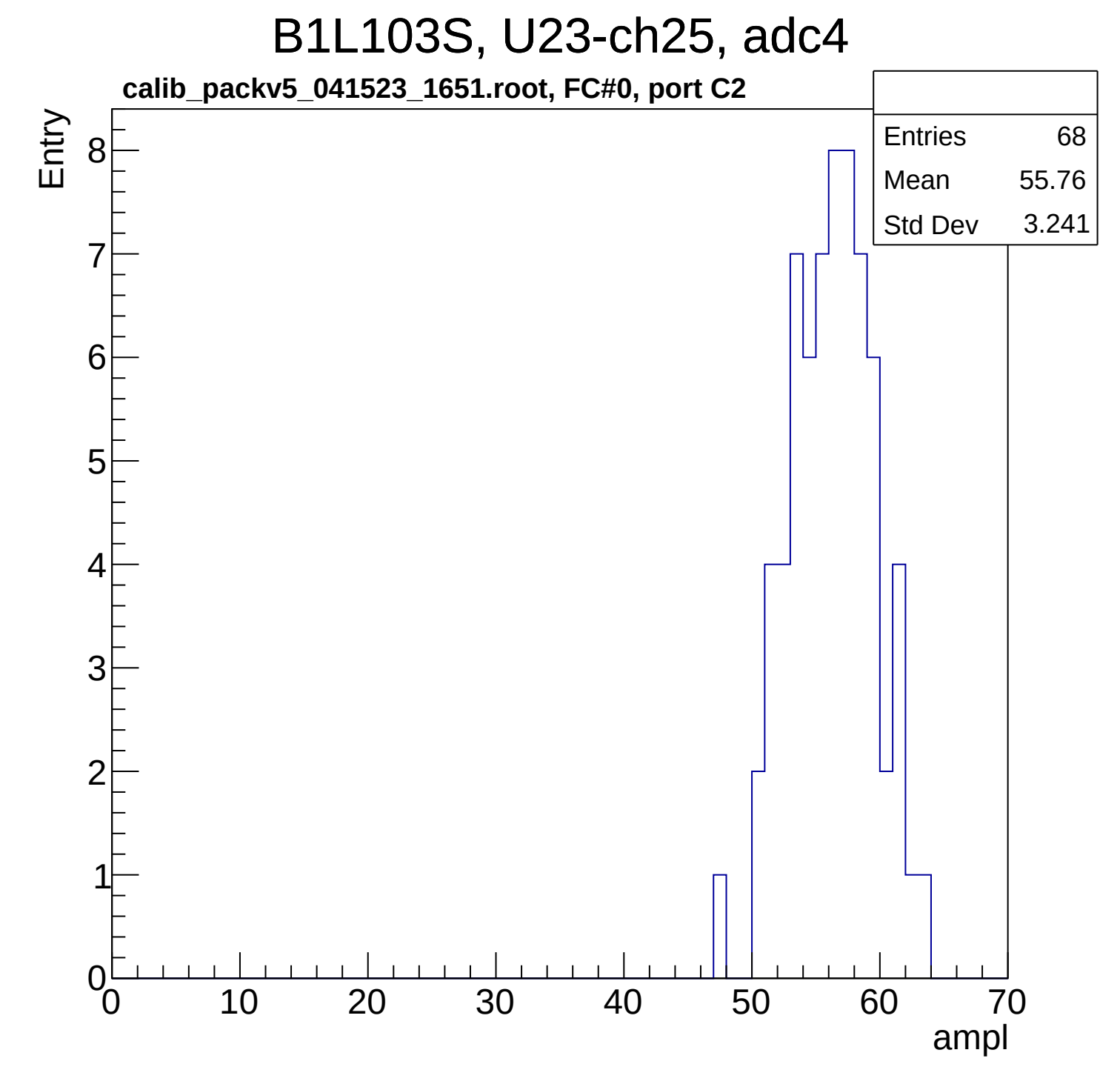

B1L103S, U23-ch24, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

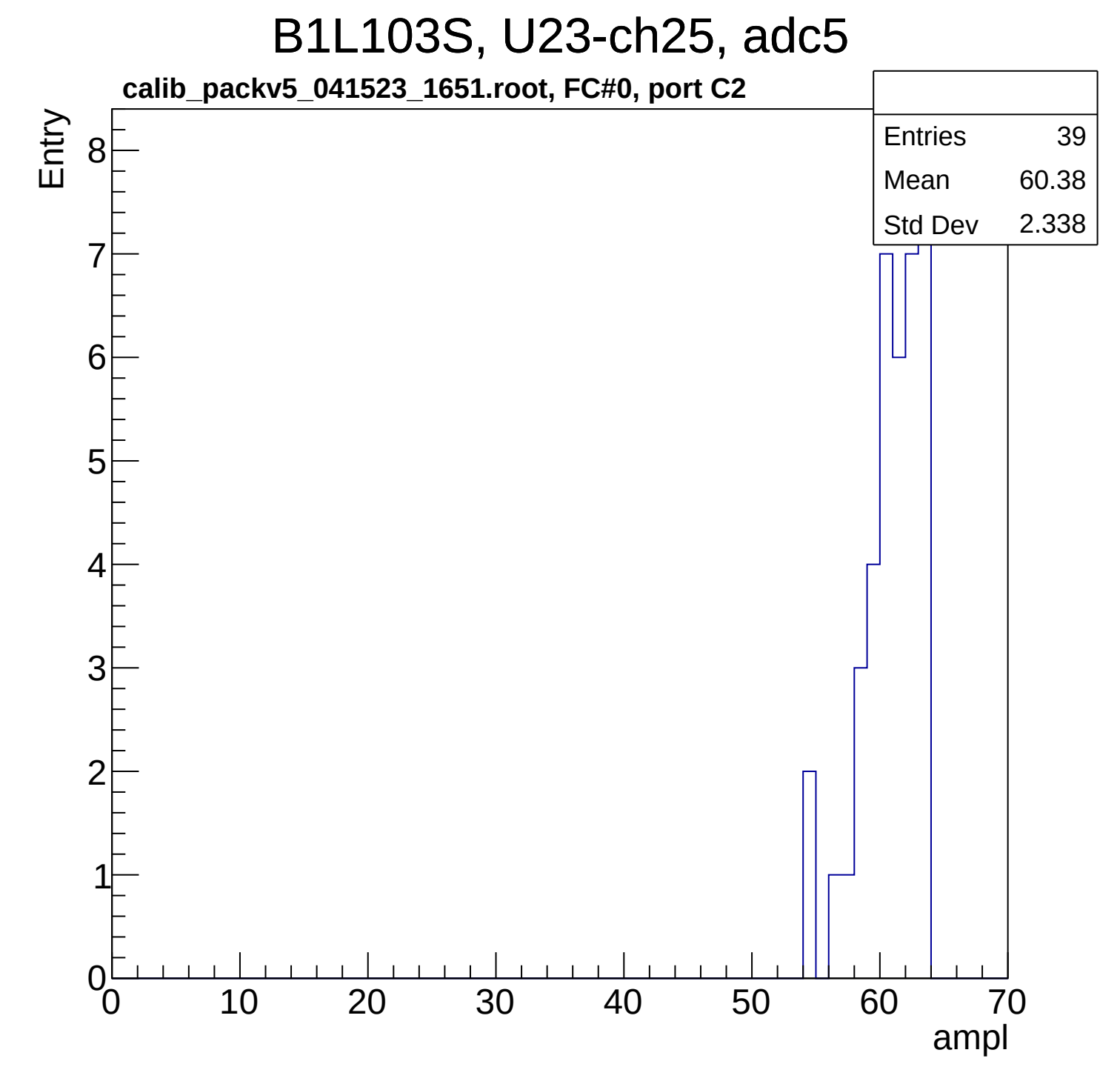

B1L103S, U23-ch25, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 113
Mean 23.92
Std Dev 11.91
Gaus mean : 29.6538
Gaus Width: 3.8713
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

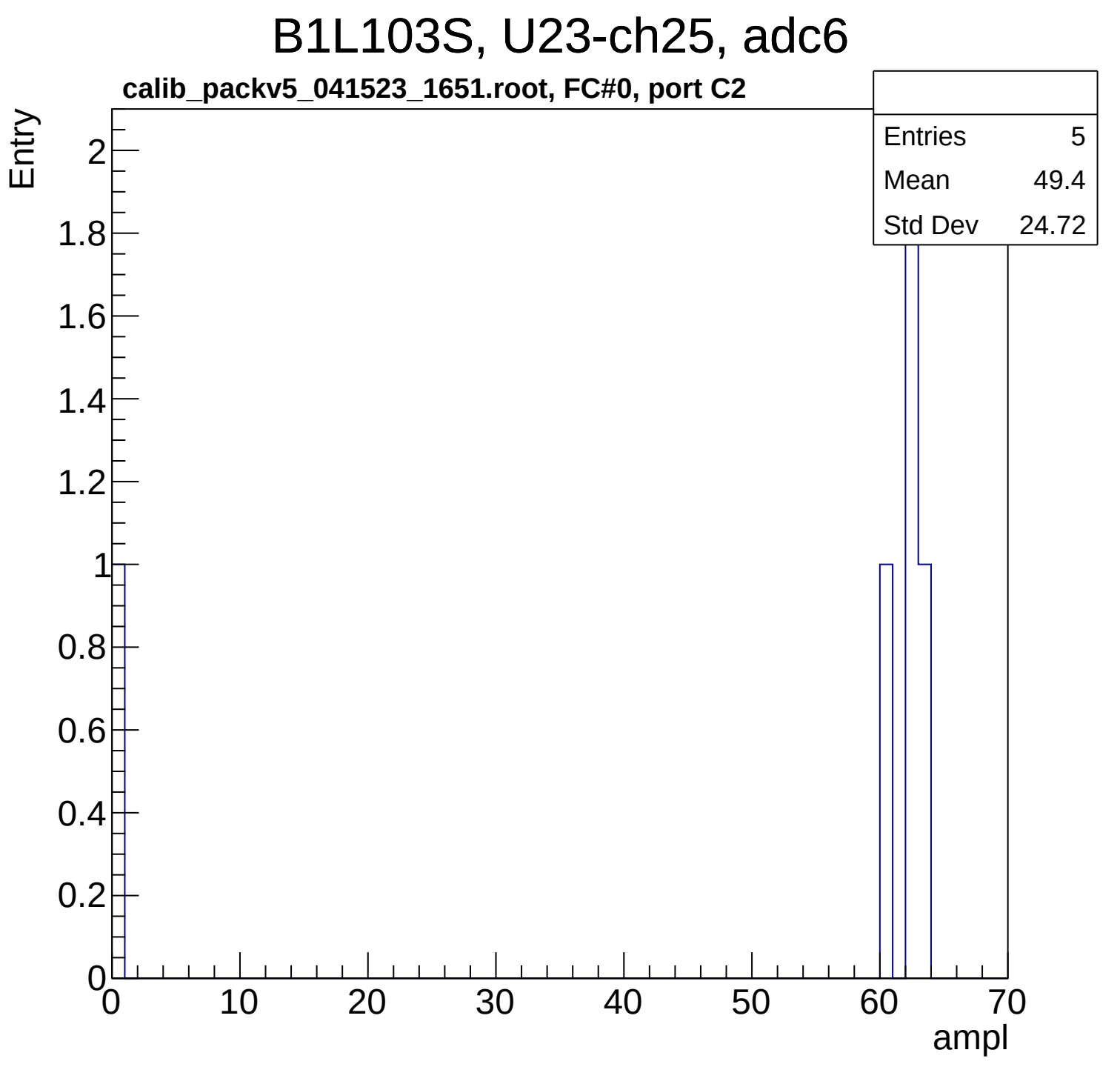

B1L103S, U23-ch25, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 35.72
Std Dev 7.378
Gaus mean : 37.0825
Gaus Width: 3.4454
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

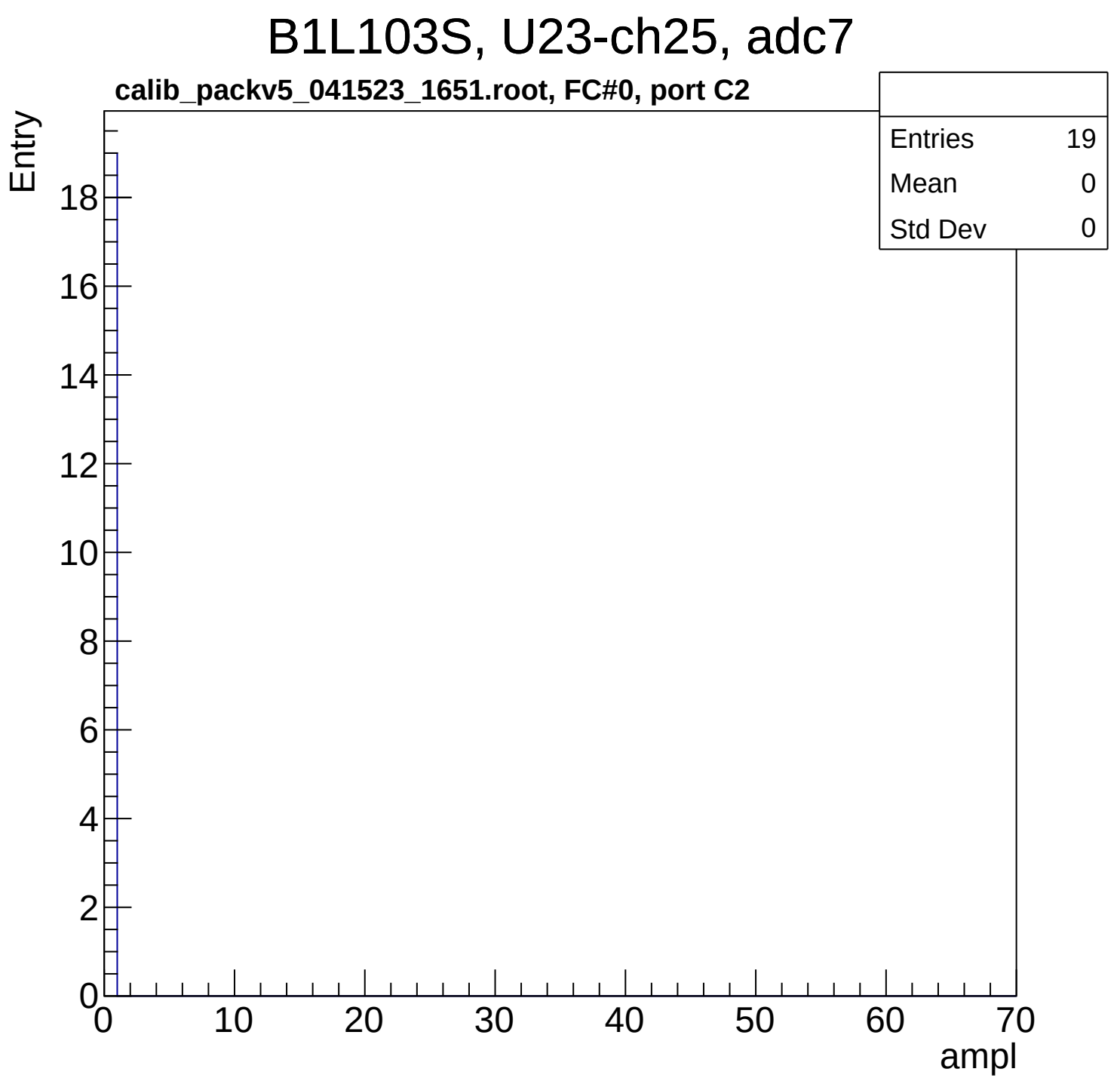

B1L103S, U23-ch25, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 92
Mean 36.92
Std Dev 15.98
Gaus mean : 44.1321
Gaus Width: 3.8414
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

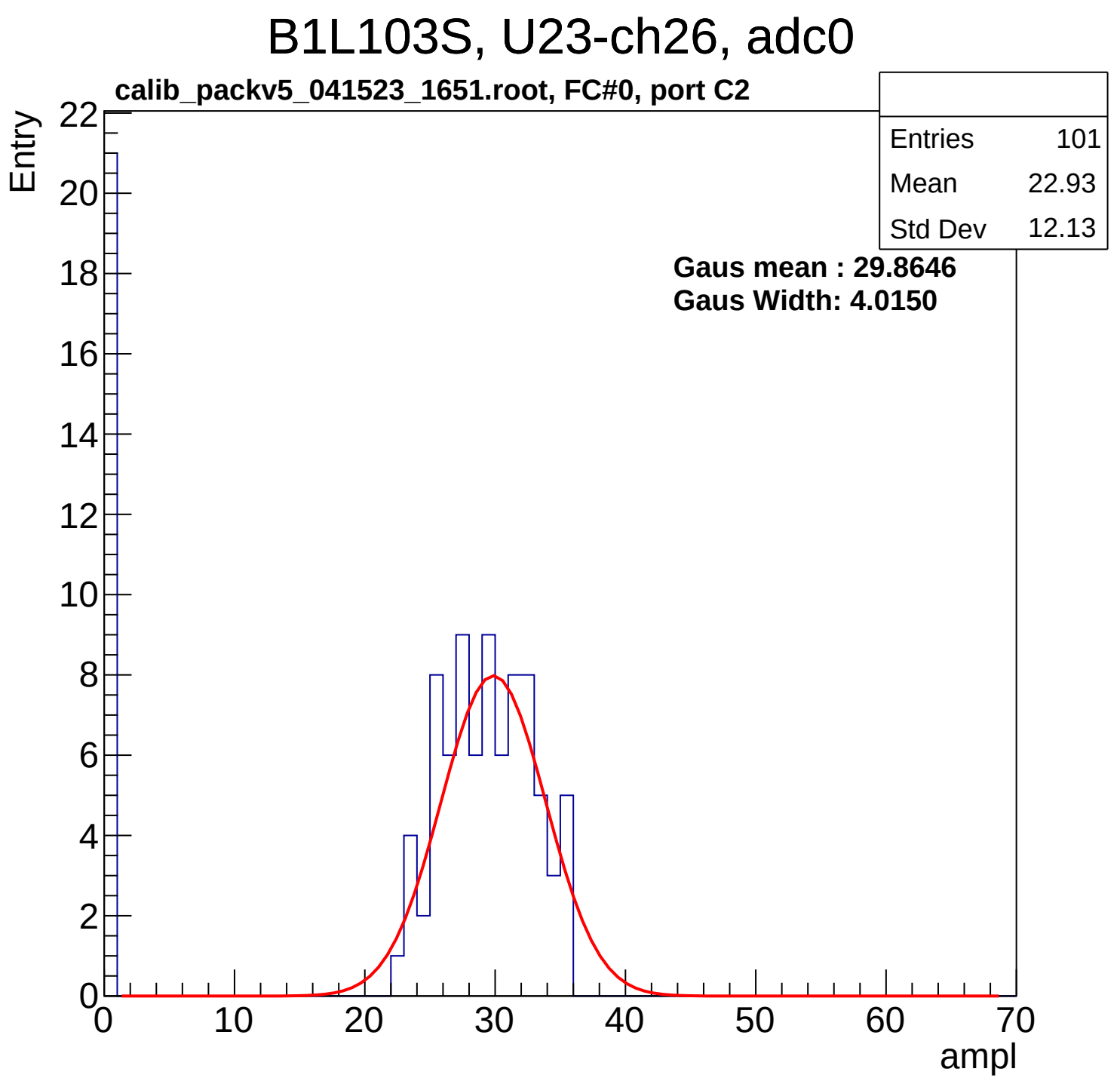

B1L103S, U23-ch25, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 49.06
Std Dev 7.707
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

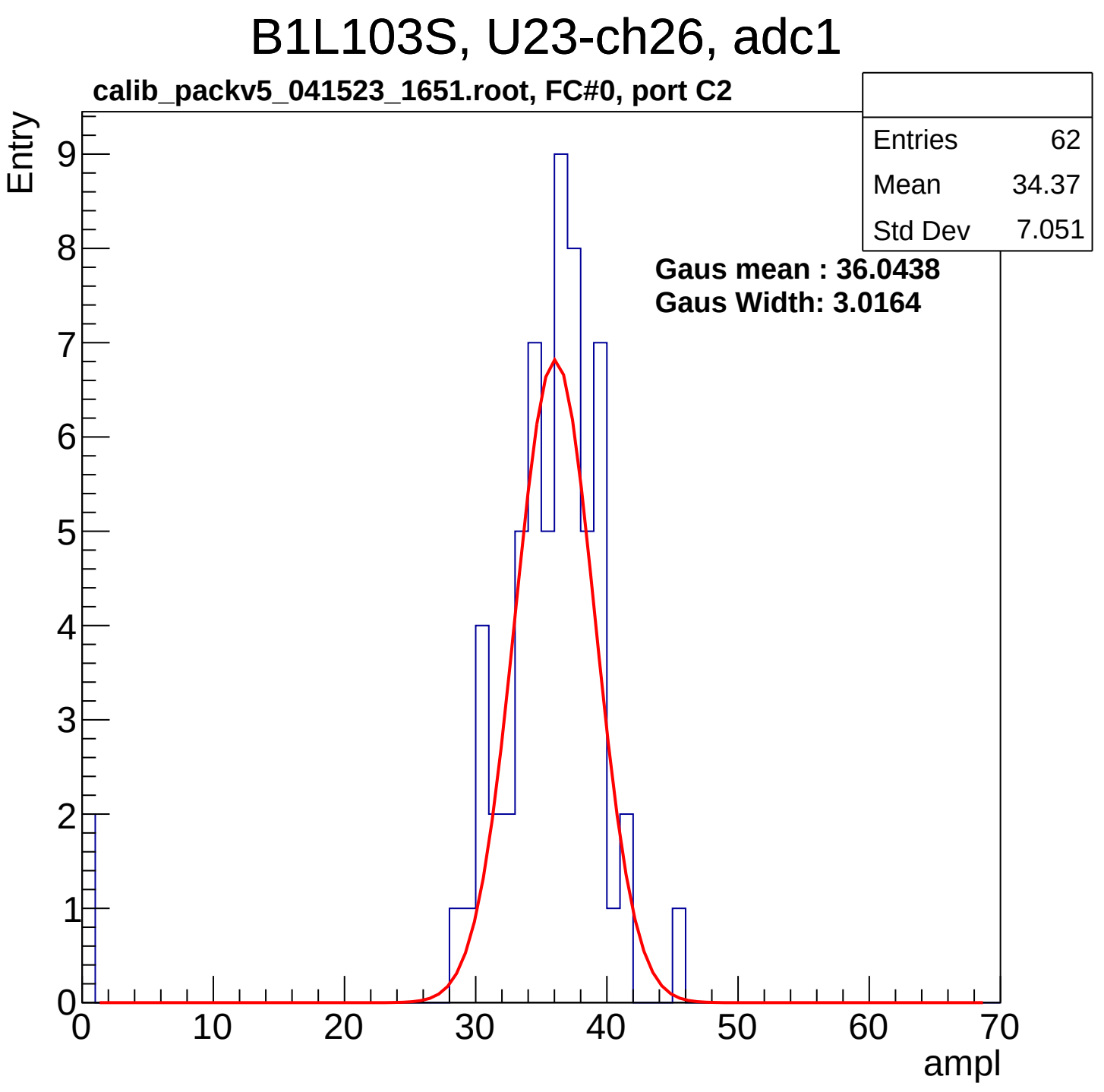

B1L103S, U23-ch25, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 55.76
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

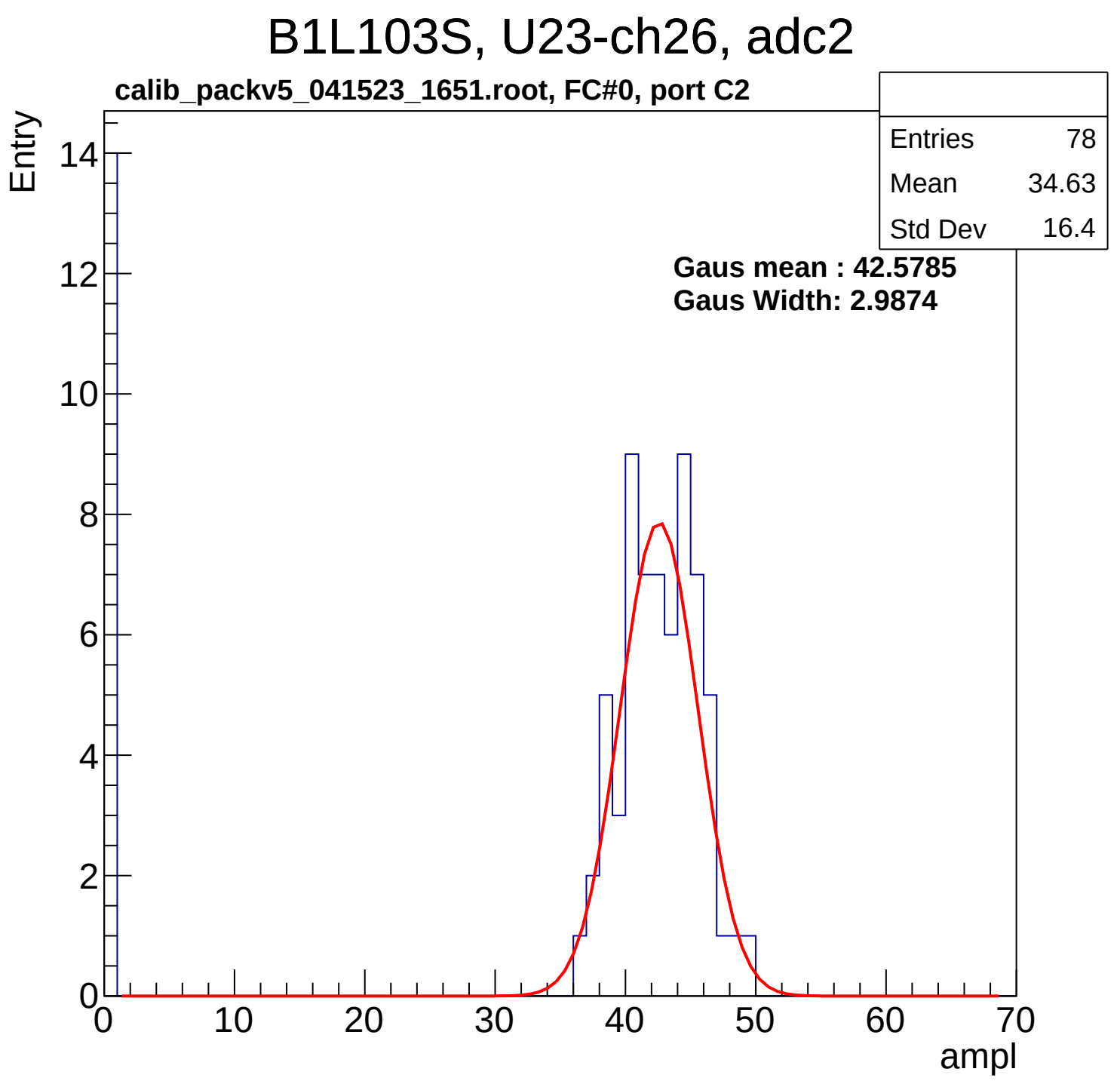

B1L103S, U23-ch25, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 39
Mean 60.38
Std Dev 2.338
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

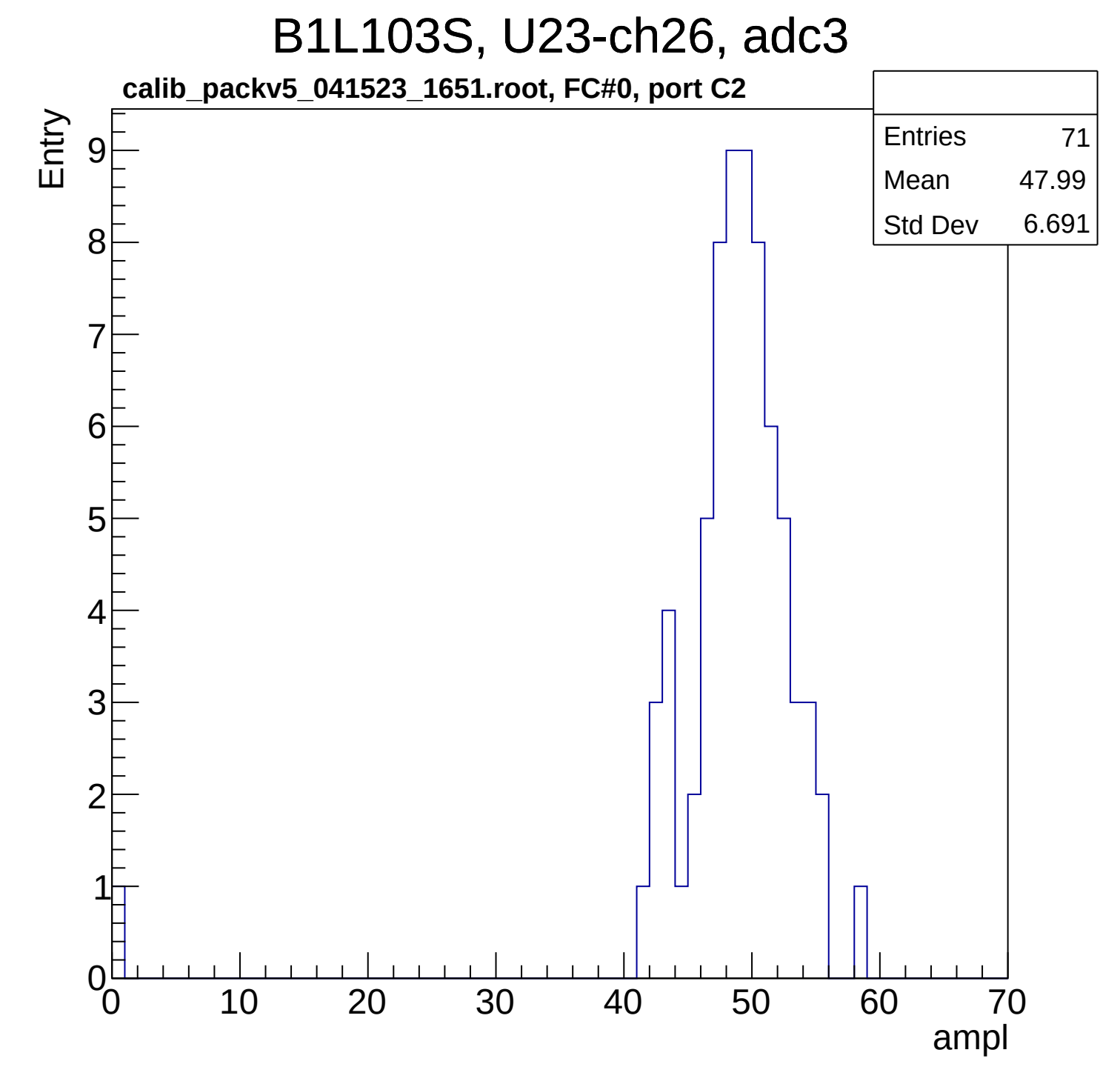

B1L103S, U23-ch25, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 49.4
Std Dev 24.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

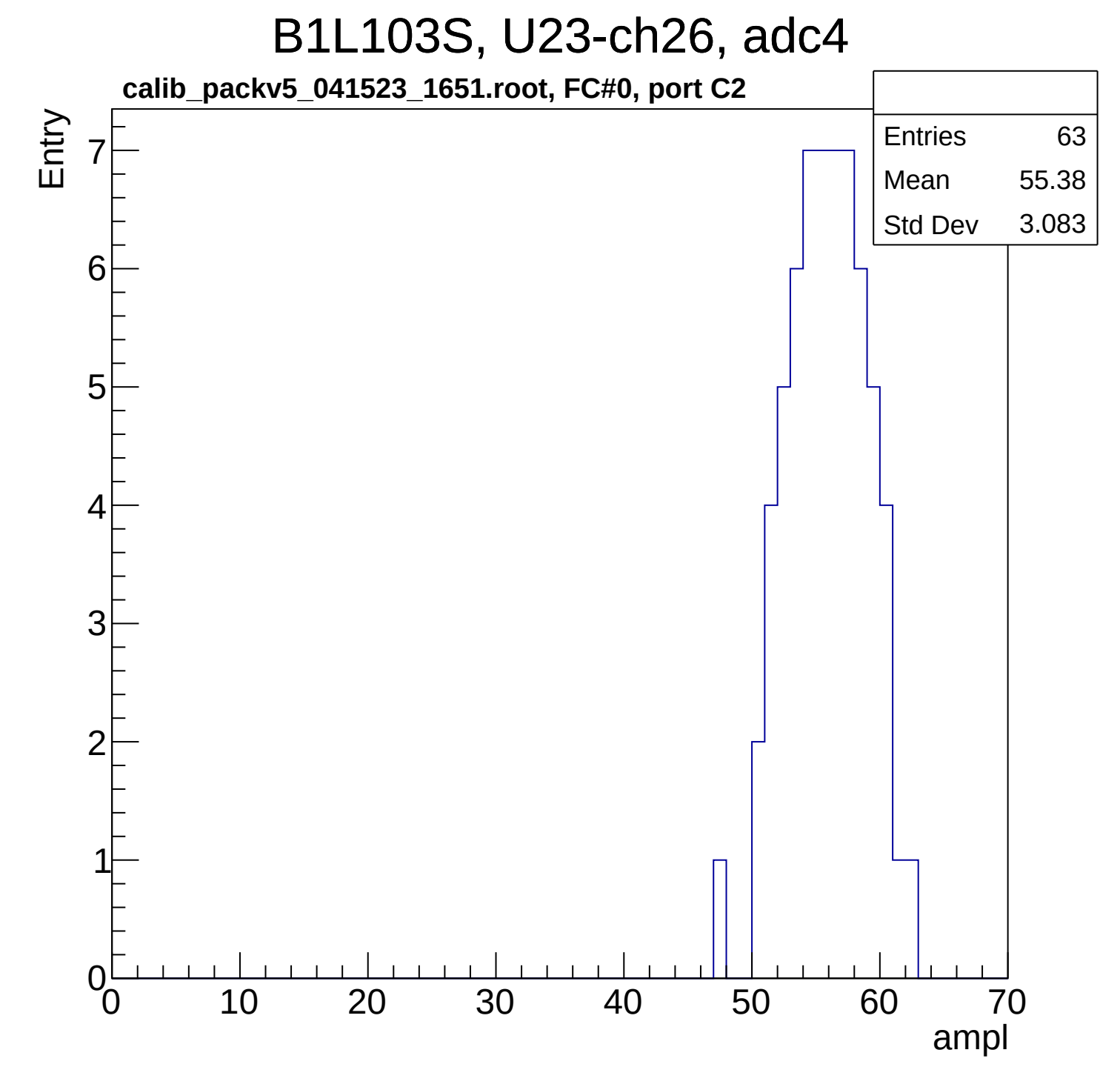

B1L103S, U23-ch25, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

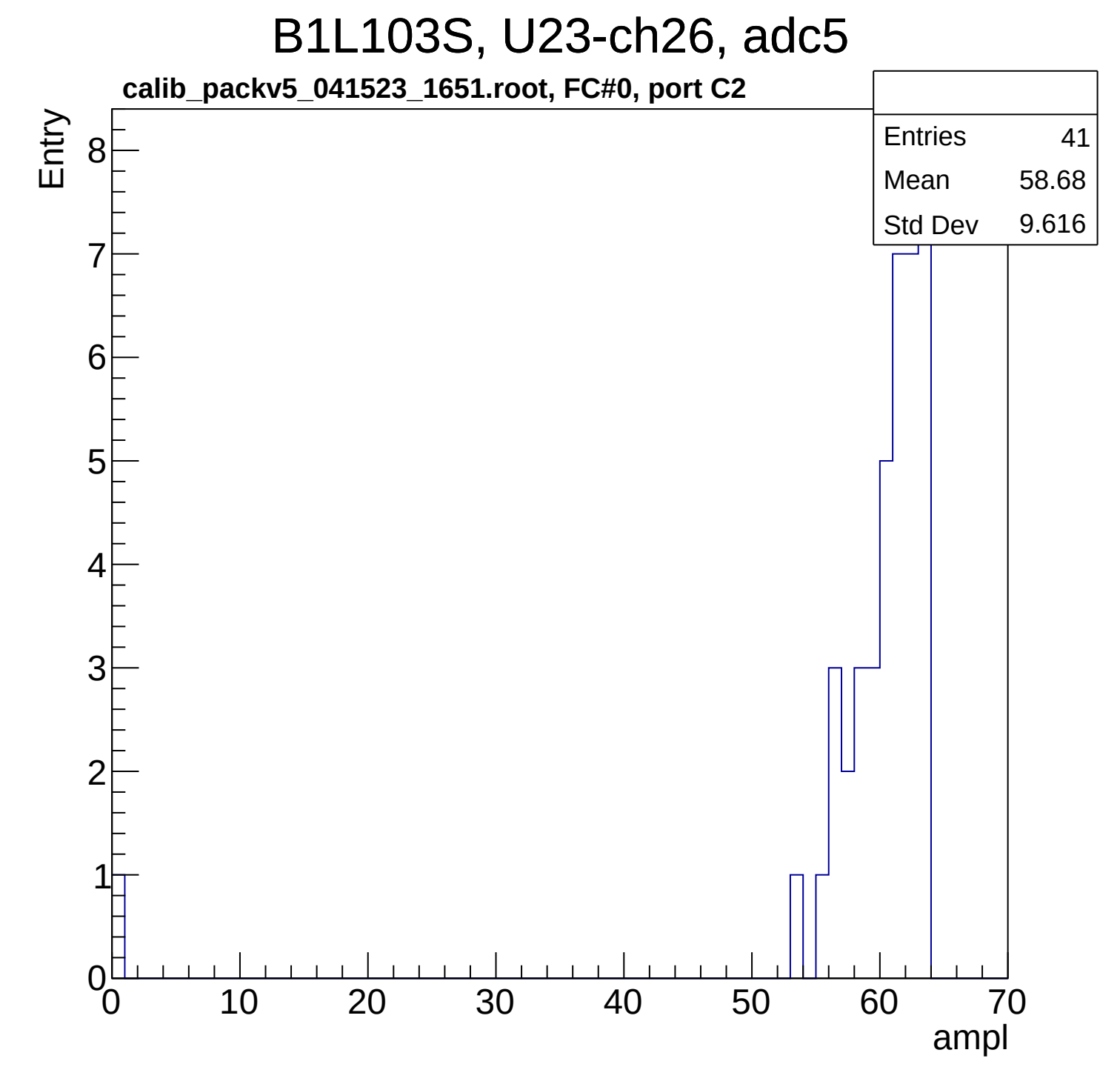

B1L103S, U23-ch26, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 101
Mean 22.93
Std Dev 12.13
Gaus mean : 29.8646
Gaus Width: 4.0150
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

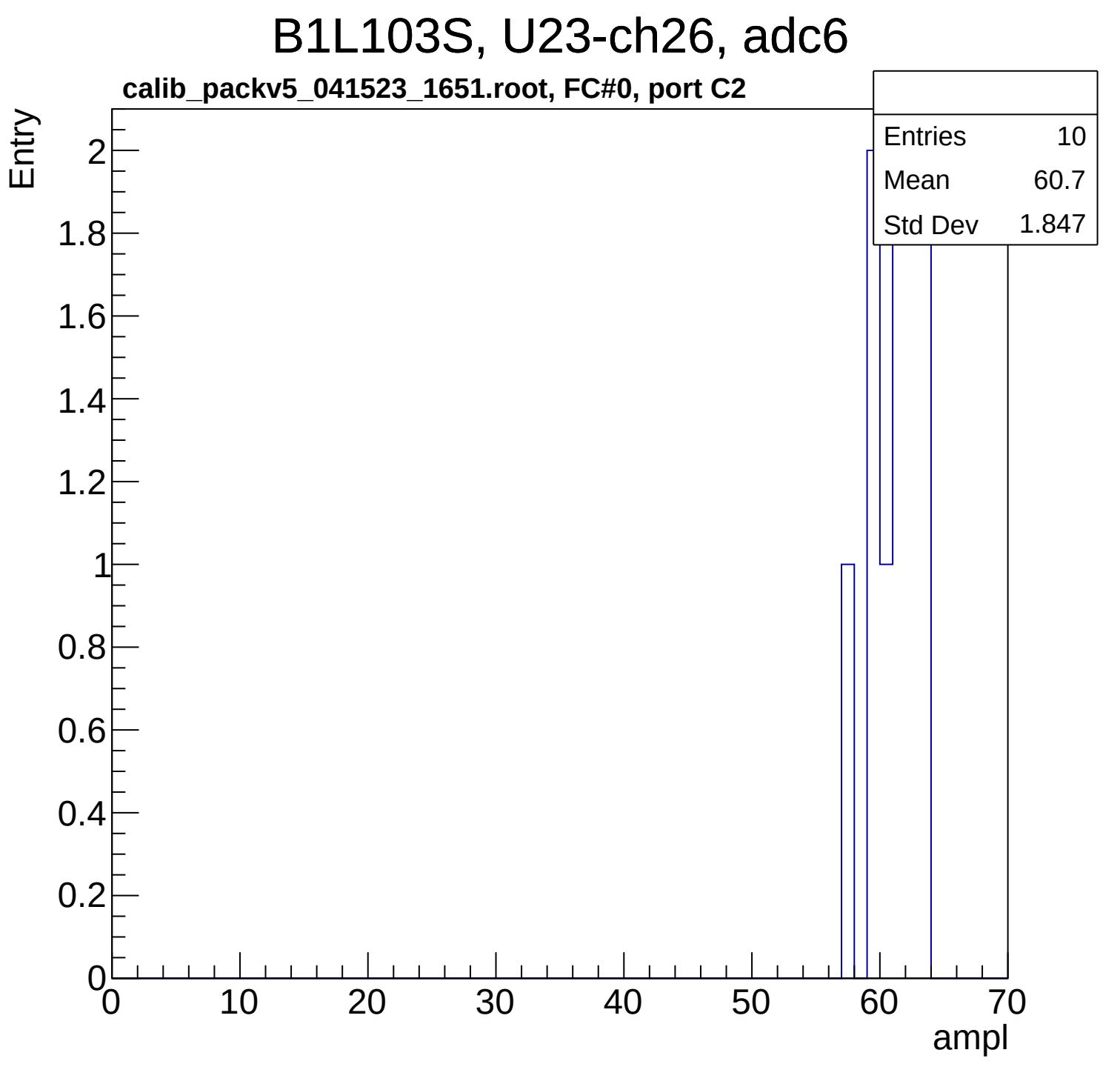

B1L103S, U23-ch26, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 34.37
Std Dev 7.051
Gaus mean : 36.0438
Gaus Width: 3.0164
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

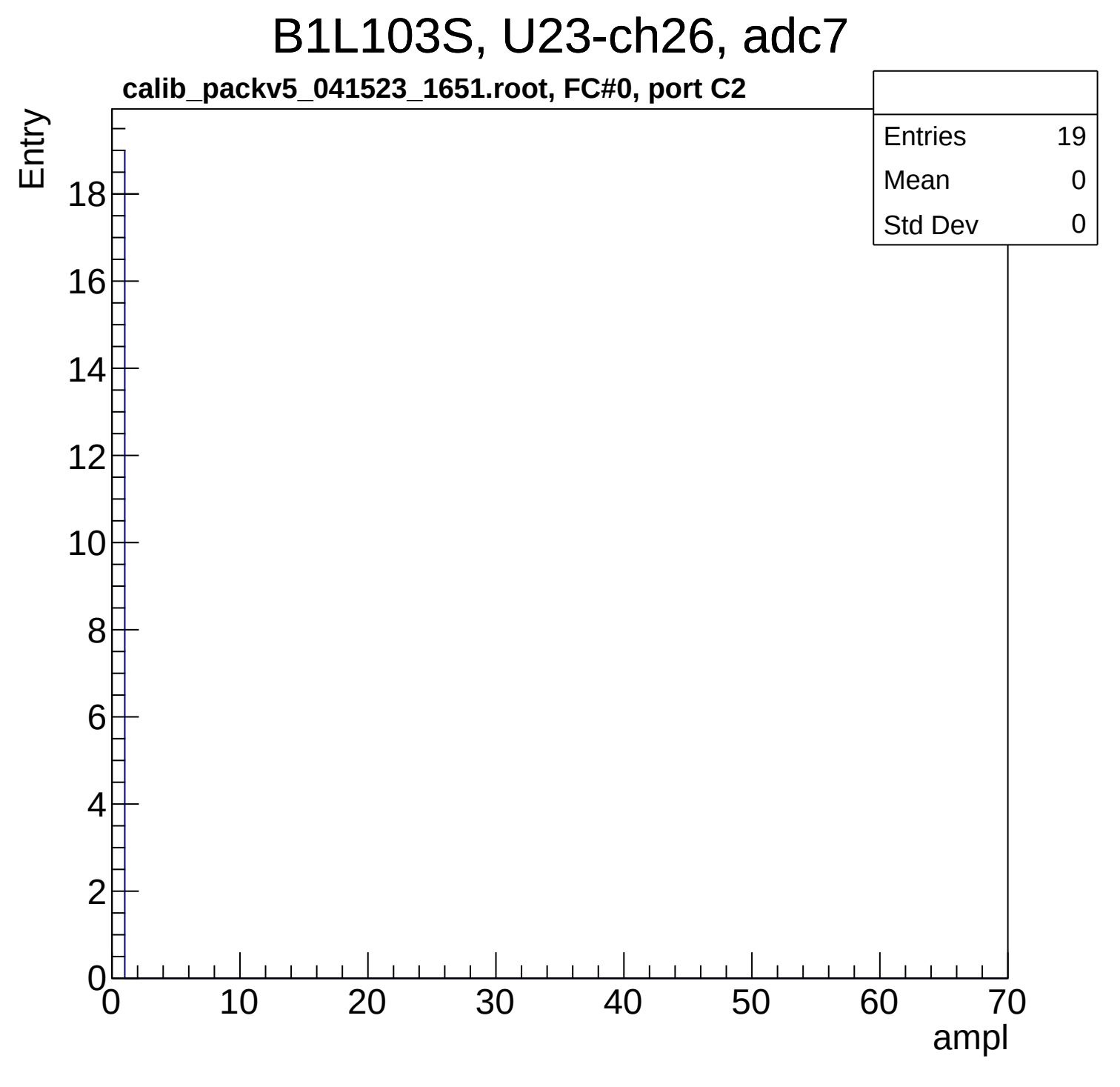

B1L103S, U23-ch26, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 34.63
Std Dev 16.4
Gaus mean : 42.5785
Gaus Width: 2.9874
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

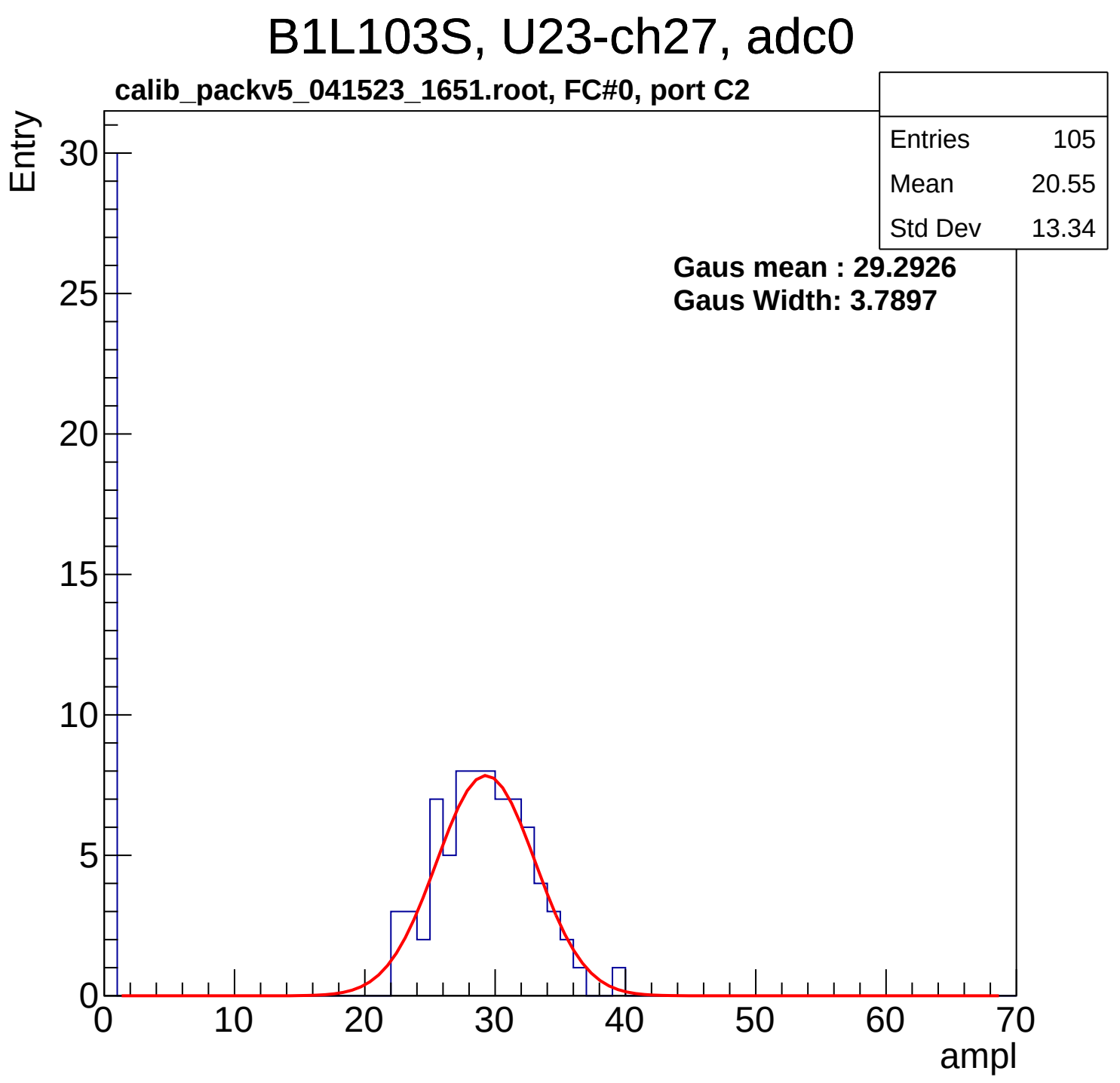

B1L103S, U23-ch26, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 47.99
Std Dev 6.691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

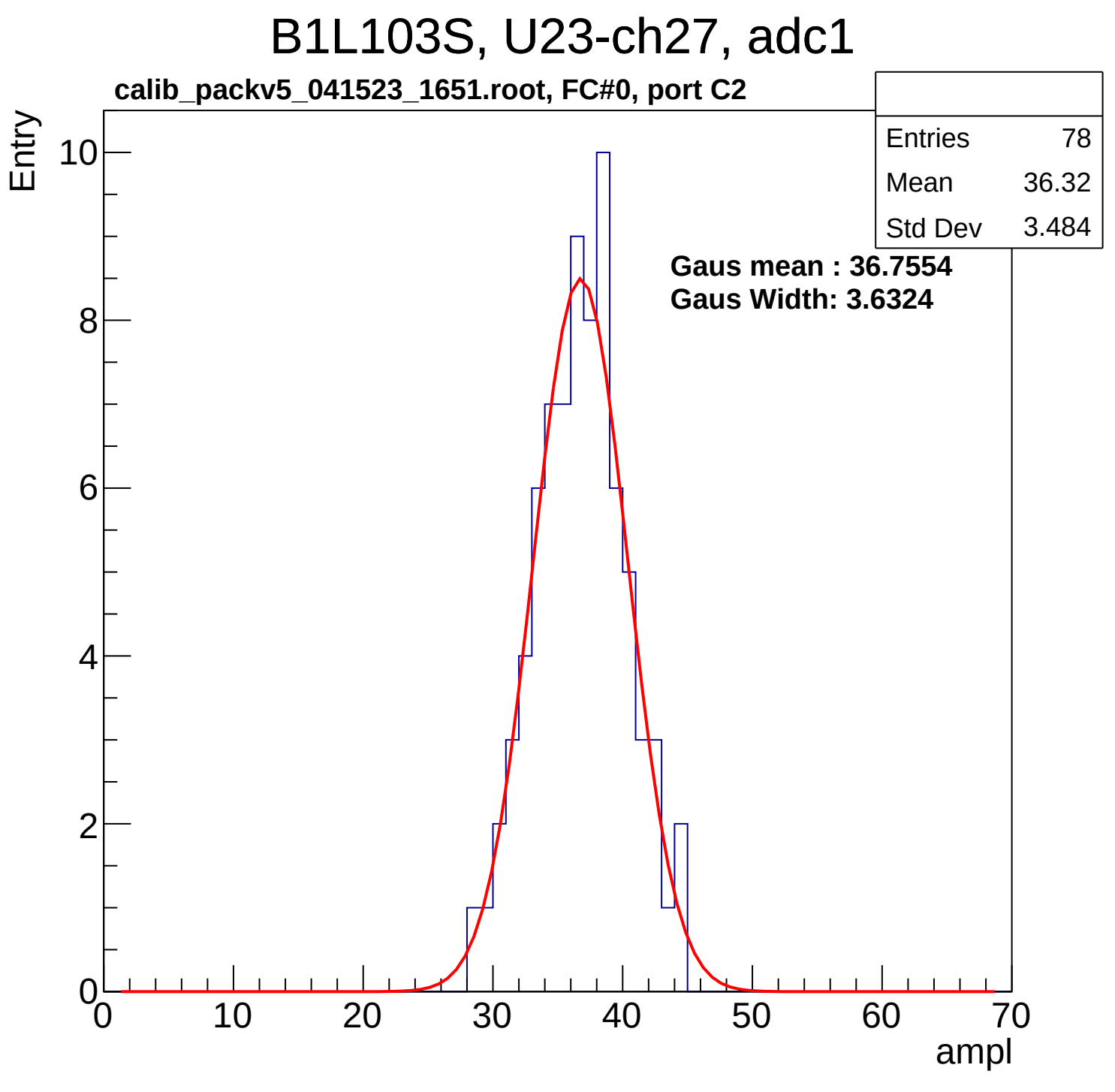

B1L103S, U23-ch26, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 55.38
Std Dev 3.083
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

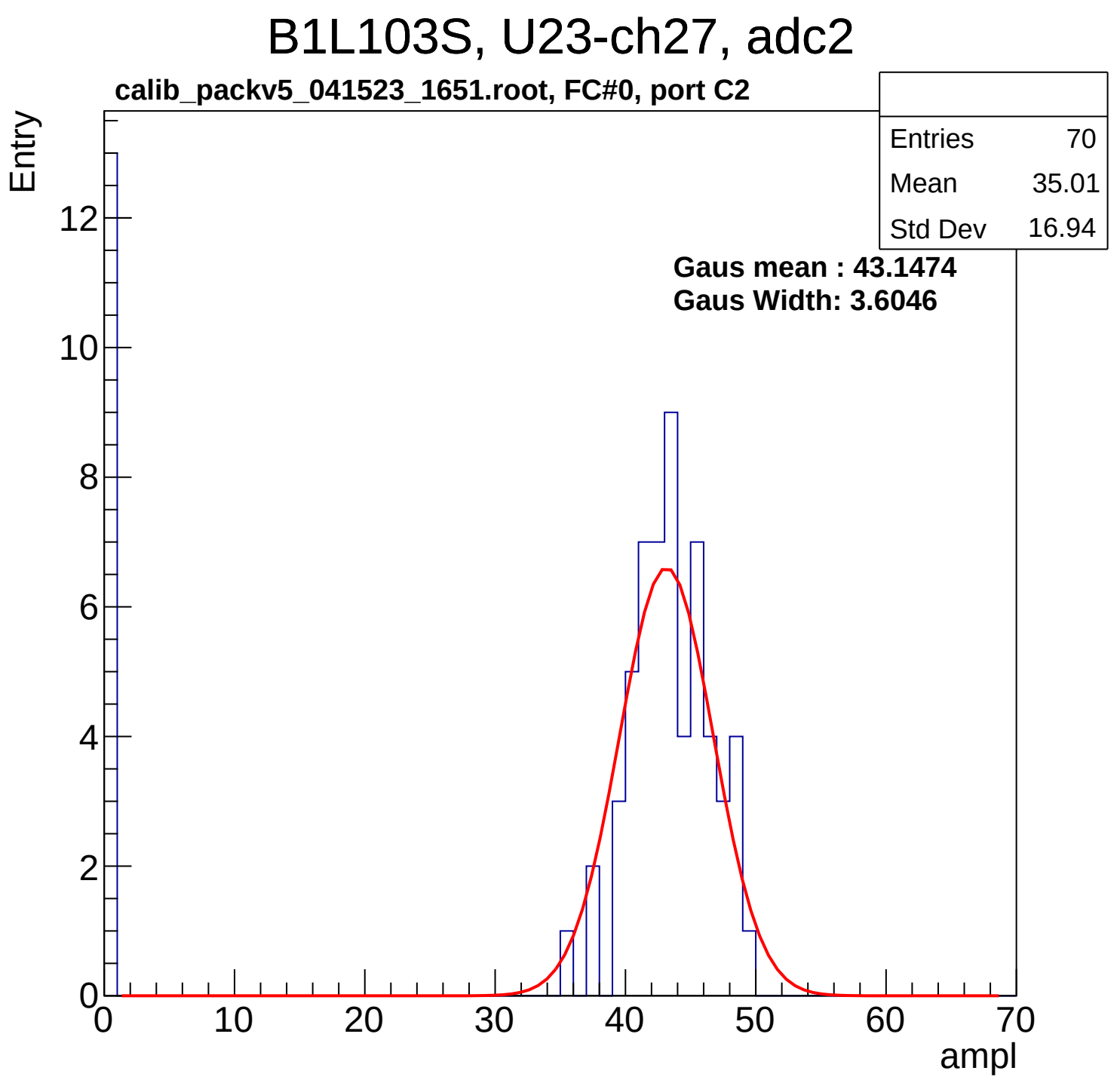

B1L103S, U23-ch26, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 41
Mean 58.68
Std Dev 9.616
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

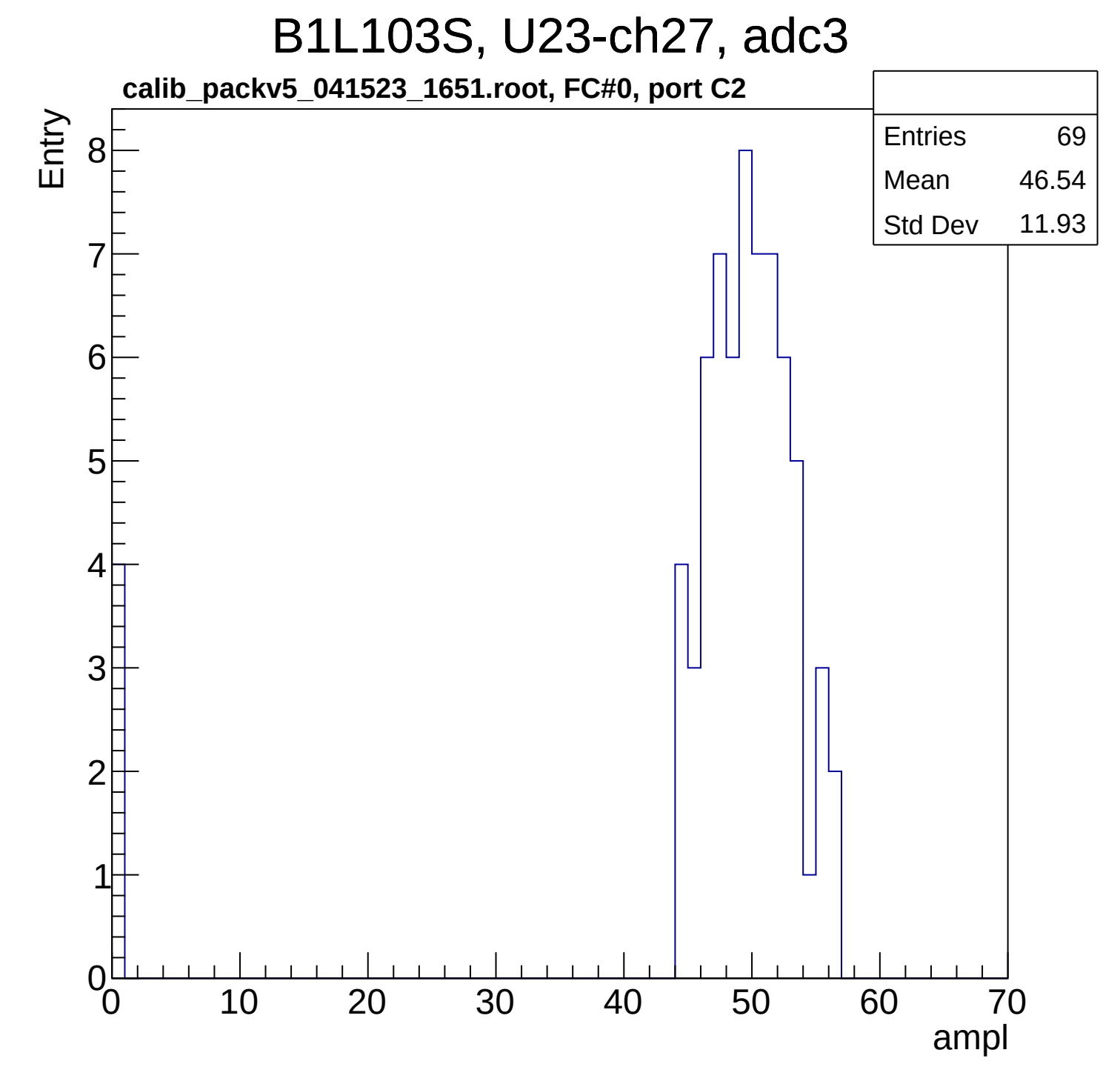

B1L103S, U23-ch26, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 60.7
Std Dev 1.847
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

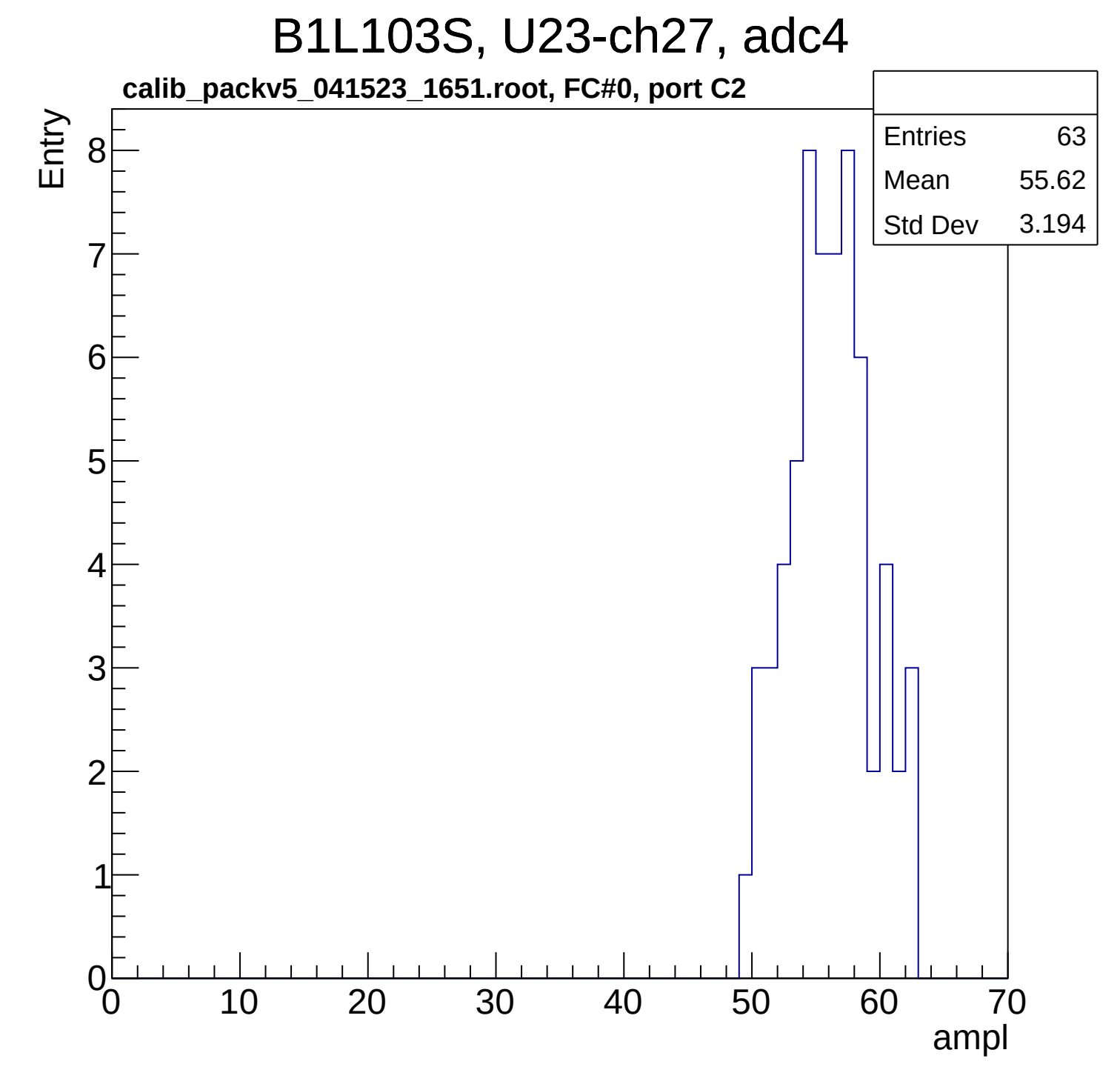

B1L103S, U23-ch26, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

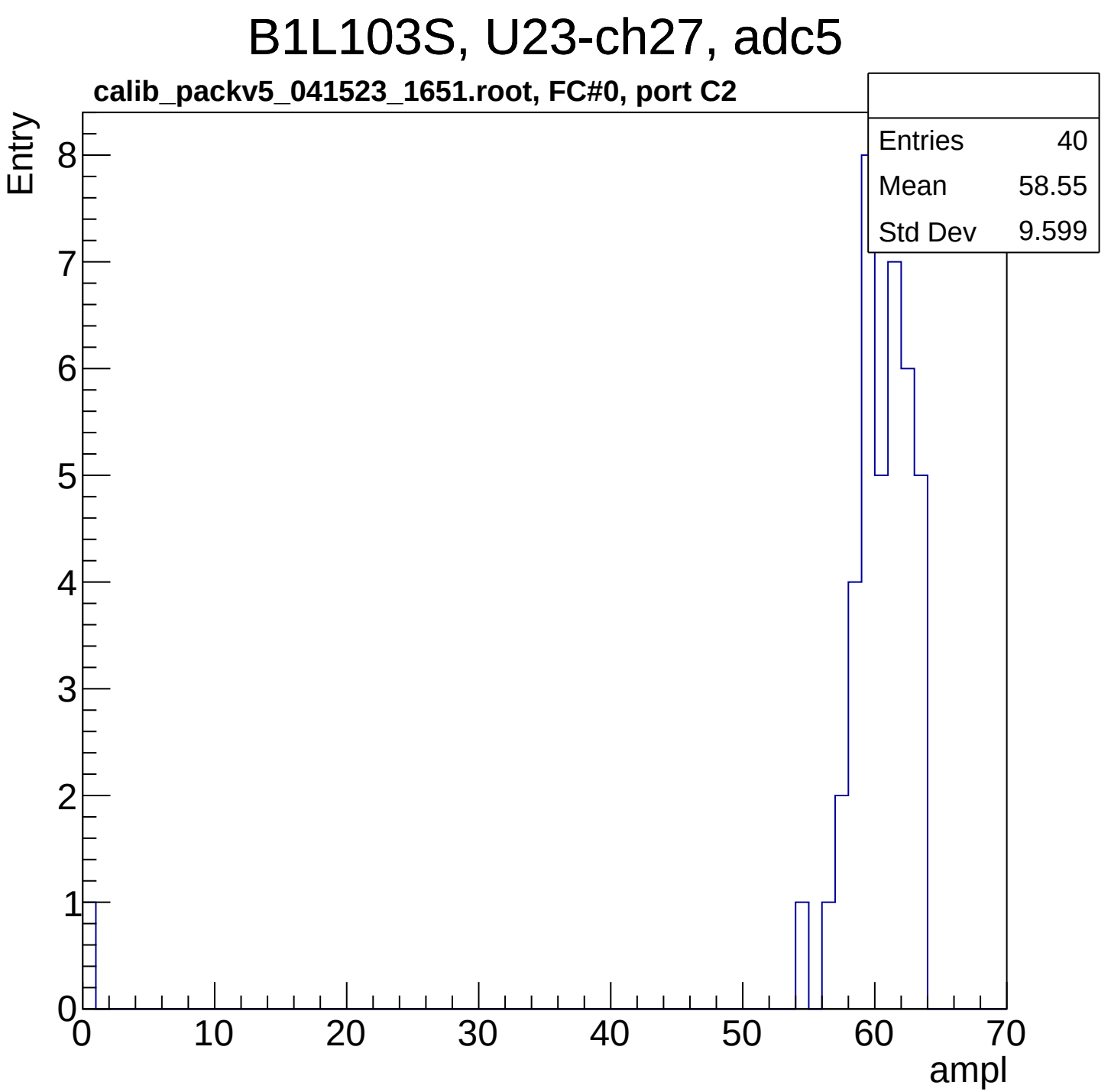

B1L103S, U23-ch27, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 105
Mean 20.55
Std Dev 13.34
Gaus mean : 29.2926
Gaus Width: 3.7897
Entry
ampl
0
5
10
15
20
25
30
0
10
20
30
40
50
60
70

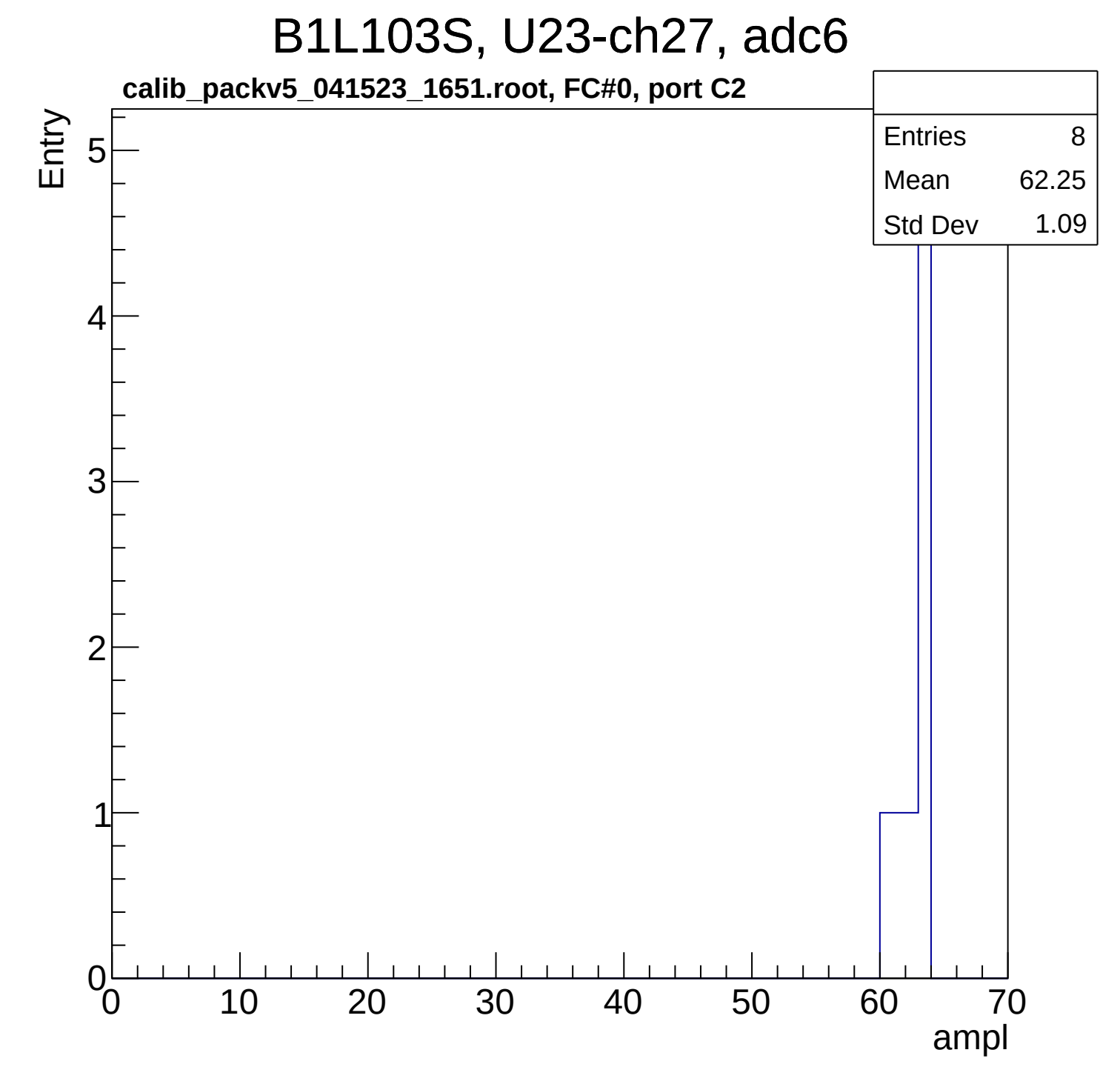

B1L103S, U23-ch27, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 36.32
Std Dev 3.484
Gaus mean : 36.7554
Gaus Width: 3.6324
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

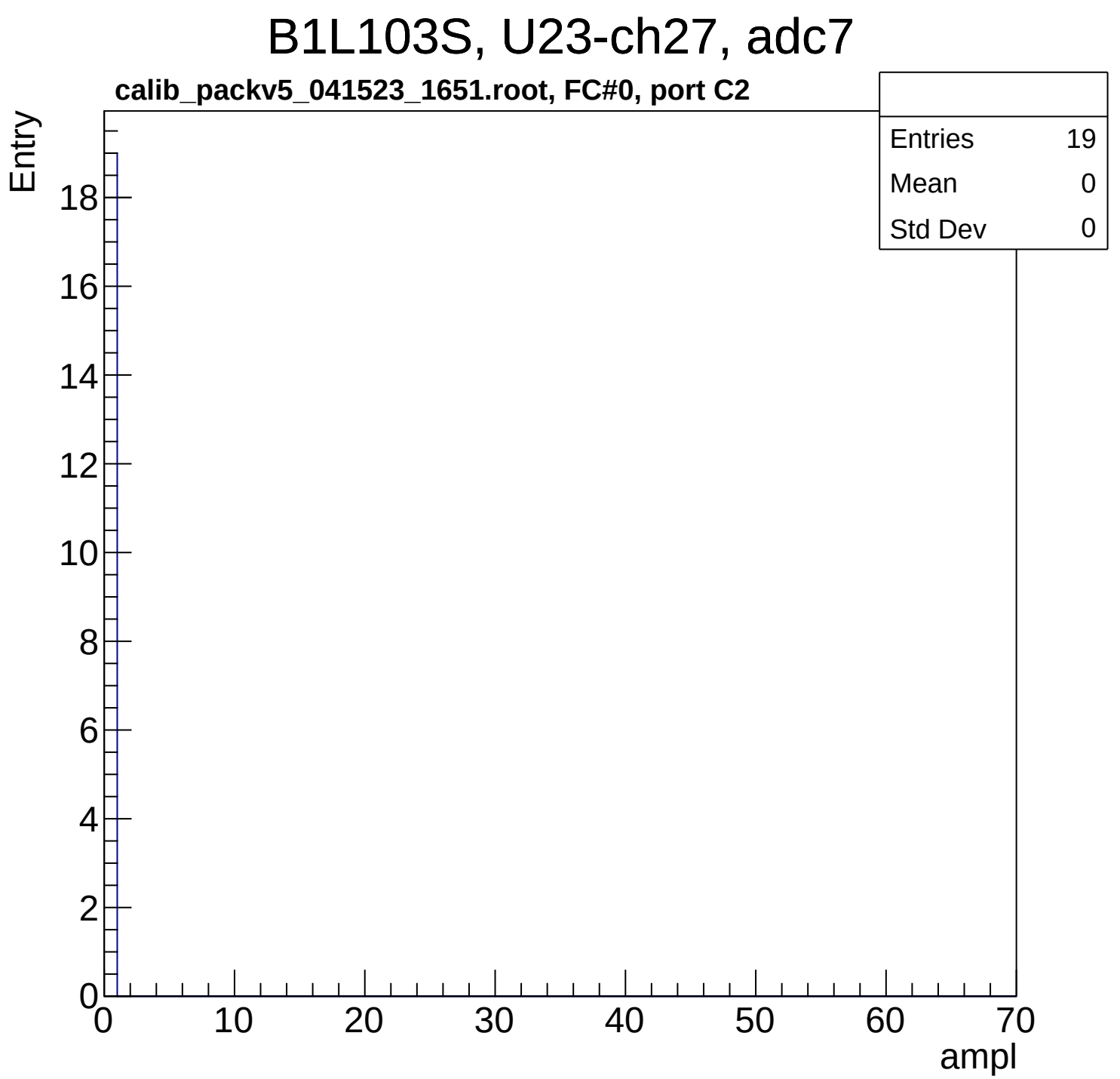

B1L103S, U23-ch27, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 35.01
Std Dev 16.94
Gaus mean : 43.1474
Gaus Width: 3.6046
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

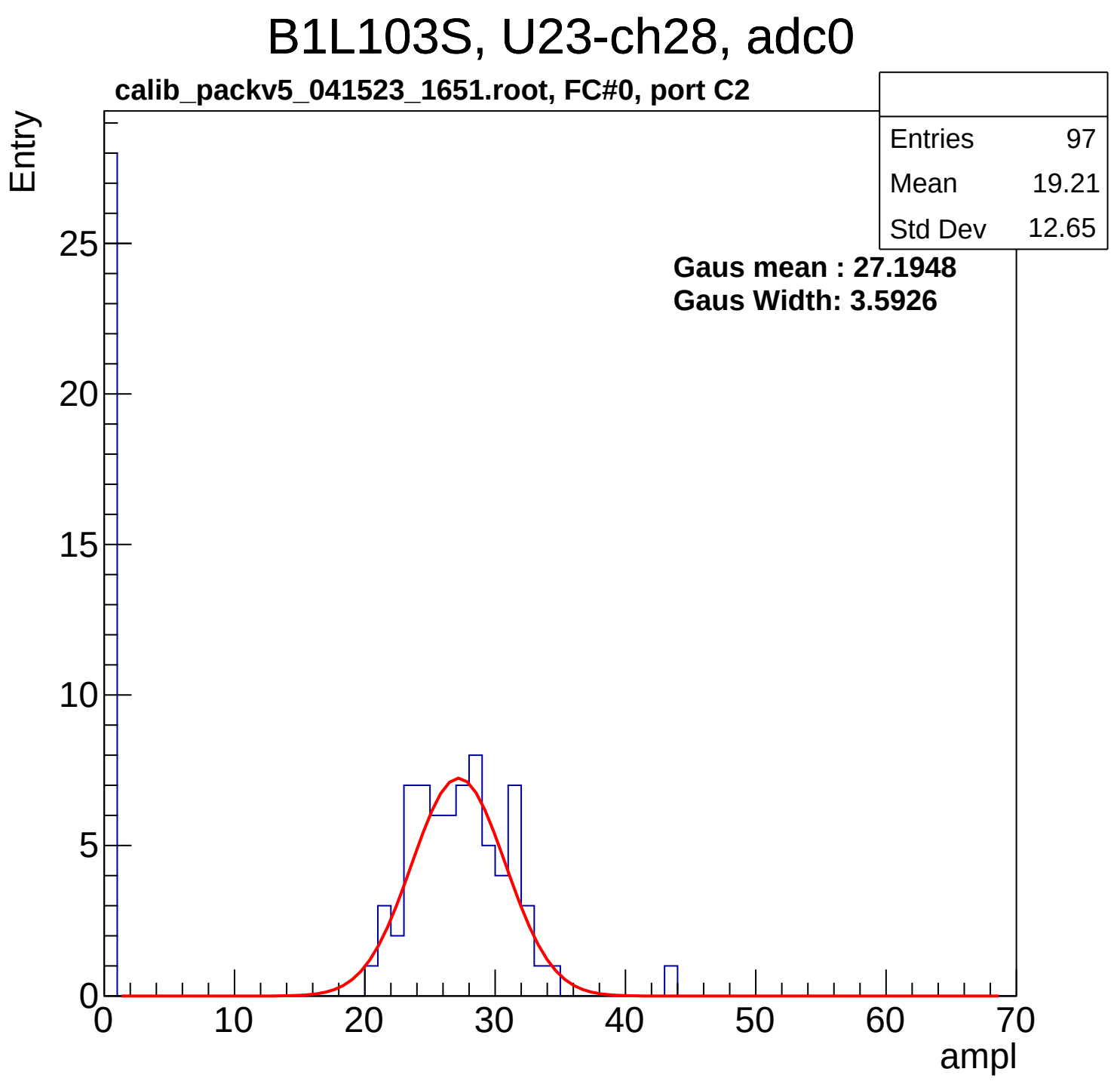

B1L103S, U23-ch27, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 46.54
Std Dev 11.93
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

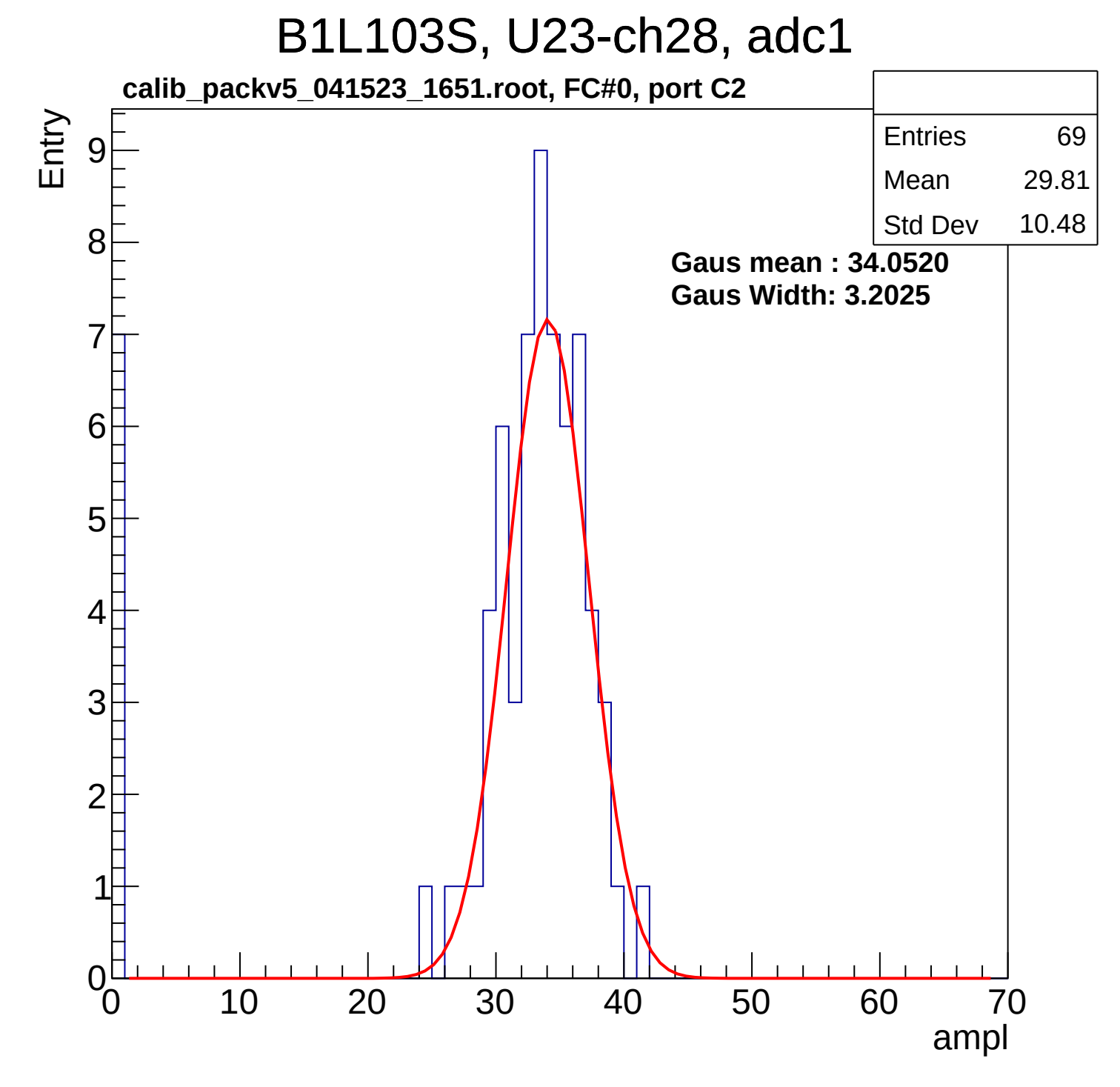

B1L103S, U23-ch27, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 55.62
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

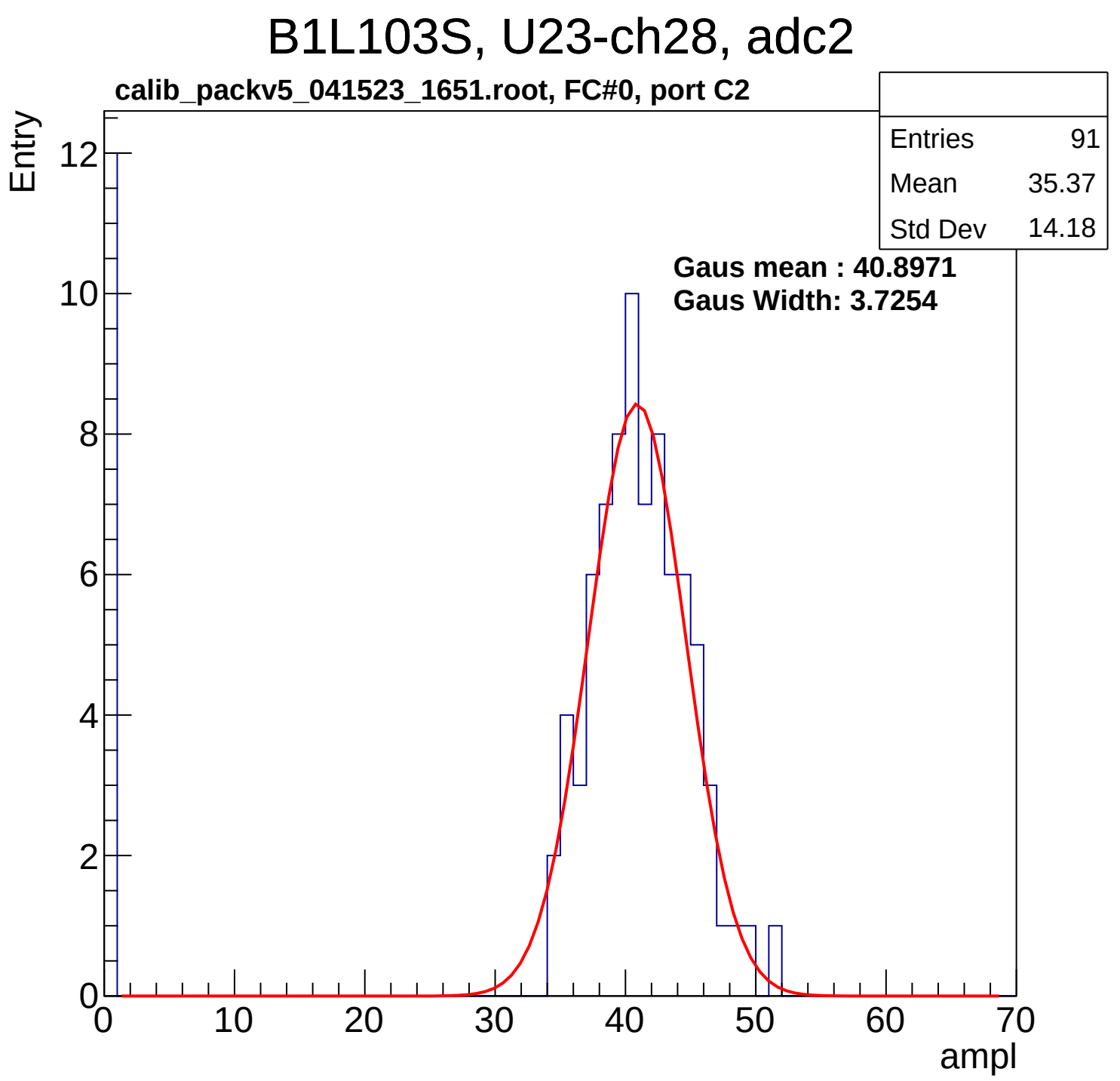

B1L103S, U23-ch27, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 40
Mean 58.55
Std Dev 9.599
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

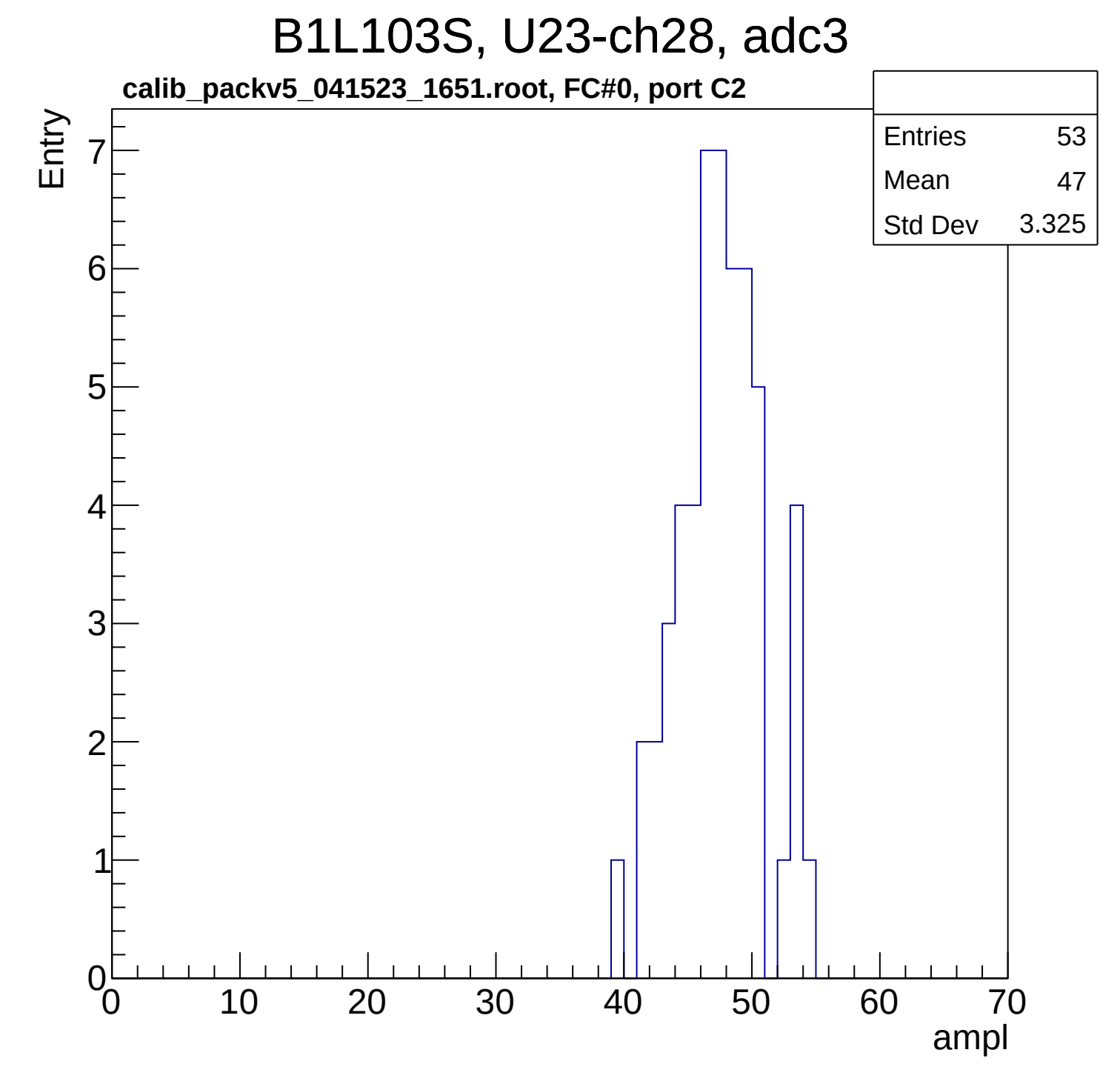

B1L103S, U23-ch27, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 62.25
Std Dev 1.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

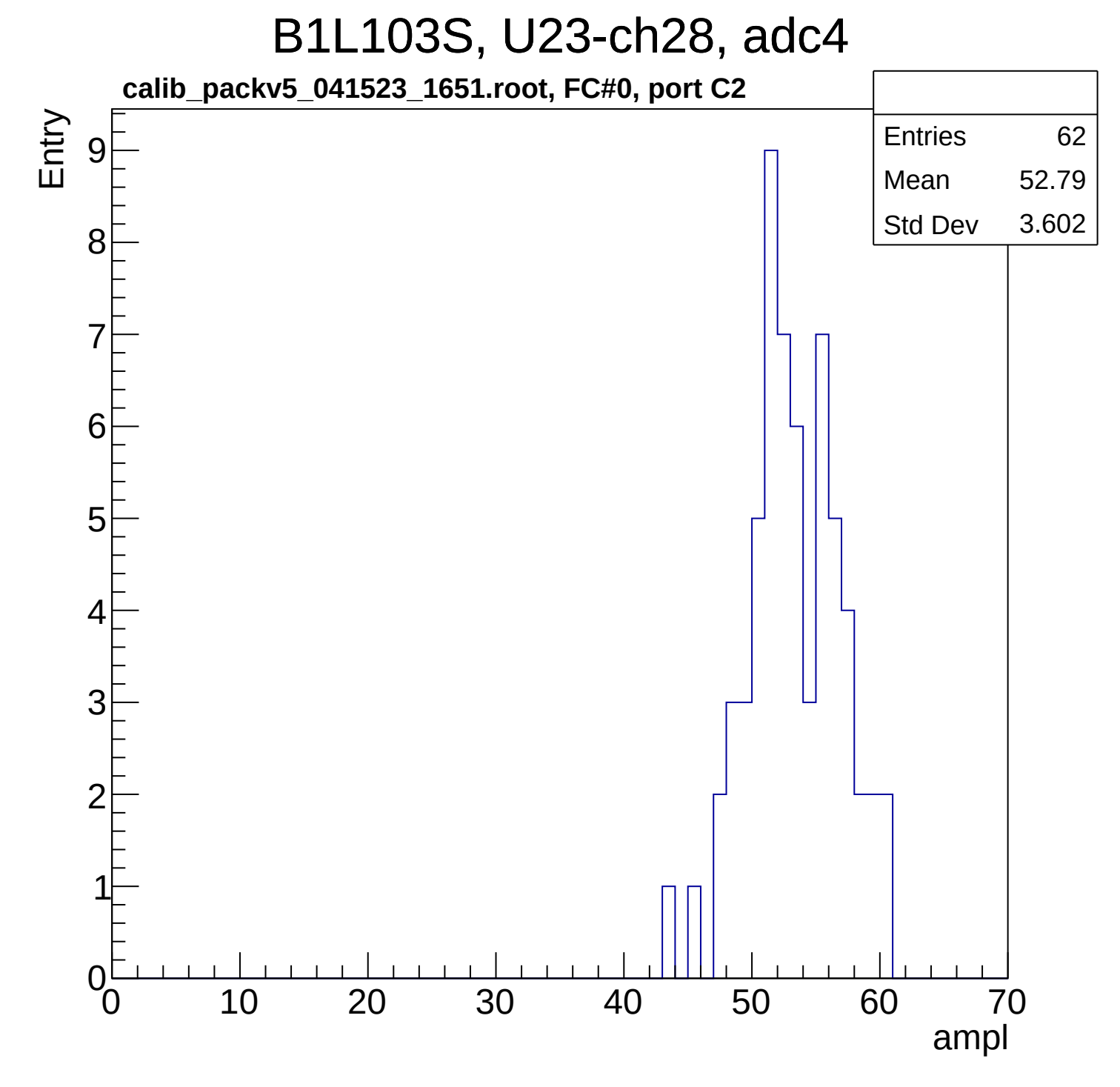

B1L103S, U23-ch27, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

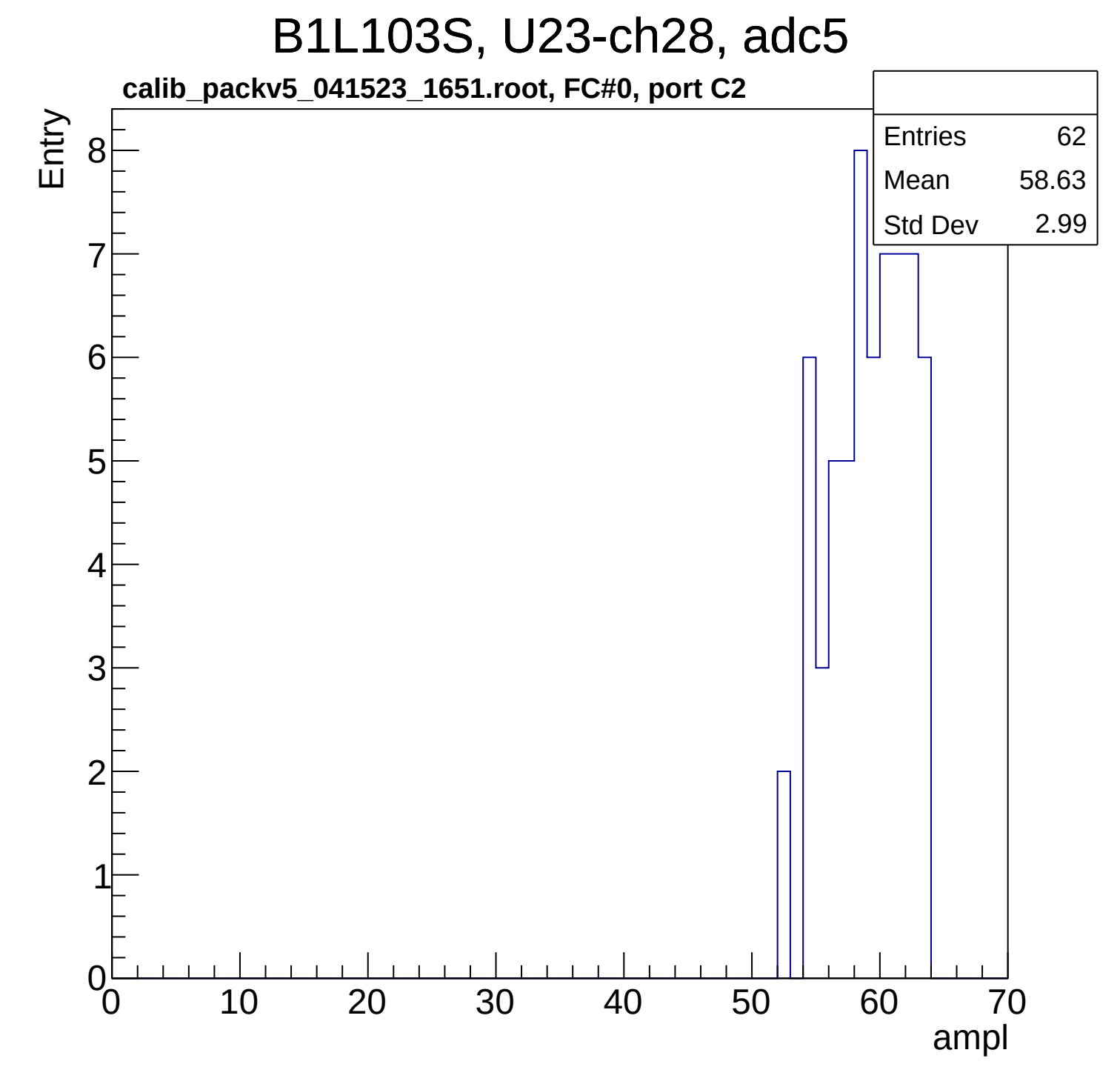

B1L103S, U23-ch28, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 97
Mean 19.21
Std Dev 12.65
Gaus mean : 27.1948
Gaus Width: 3.5926
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

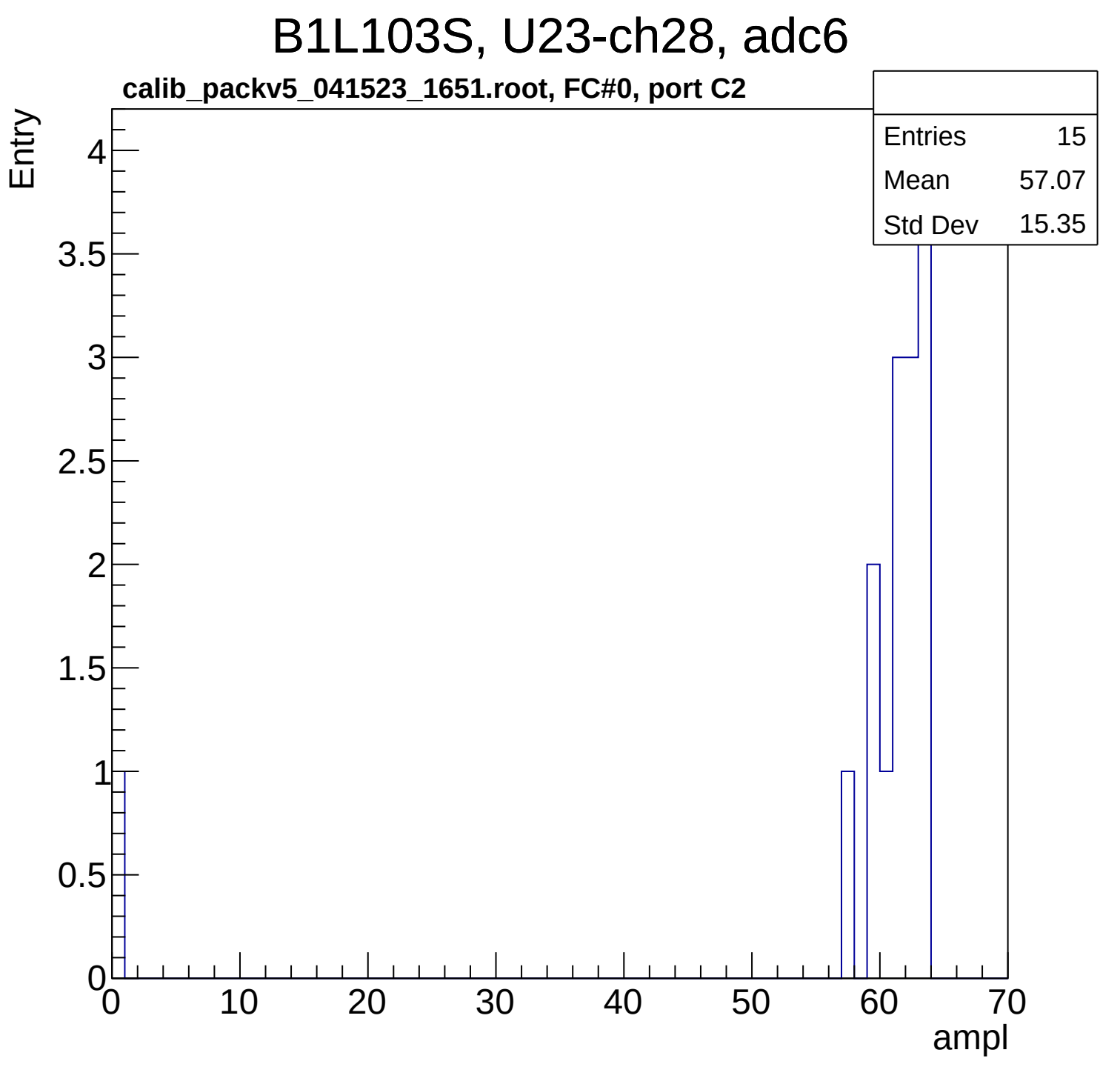

B1L103S, U23-ch28, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 29.81
Std Dev 10.48
Gaus mean : 34.0520
Gaus Width: 3.2025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

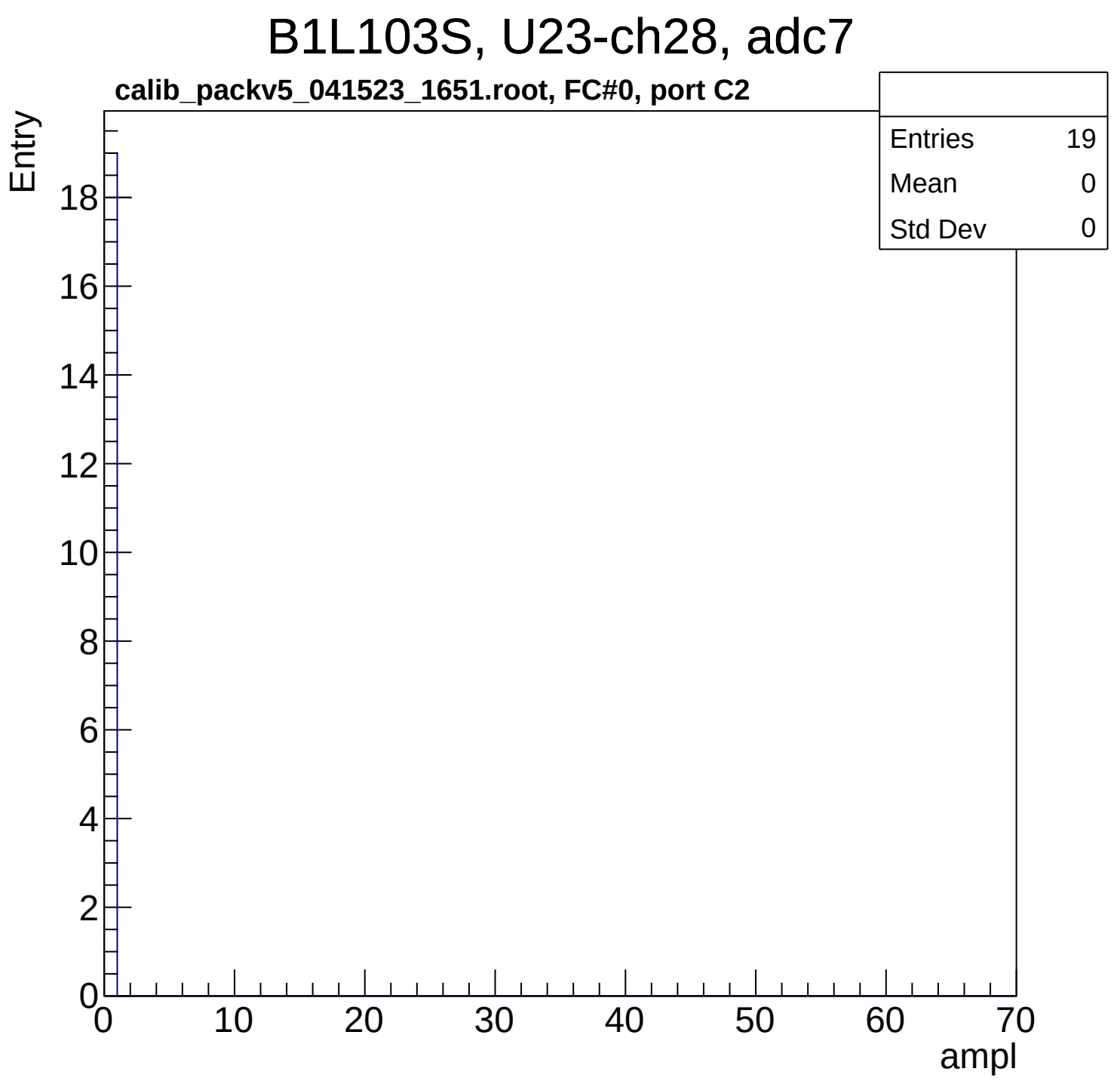

B1L103S, U23-ch28, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 35.37
Std Dev 14.18
Gaus mean : 40.8971
Gaus Width: 3.7254
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

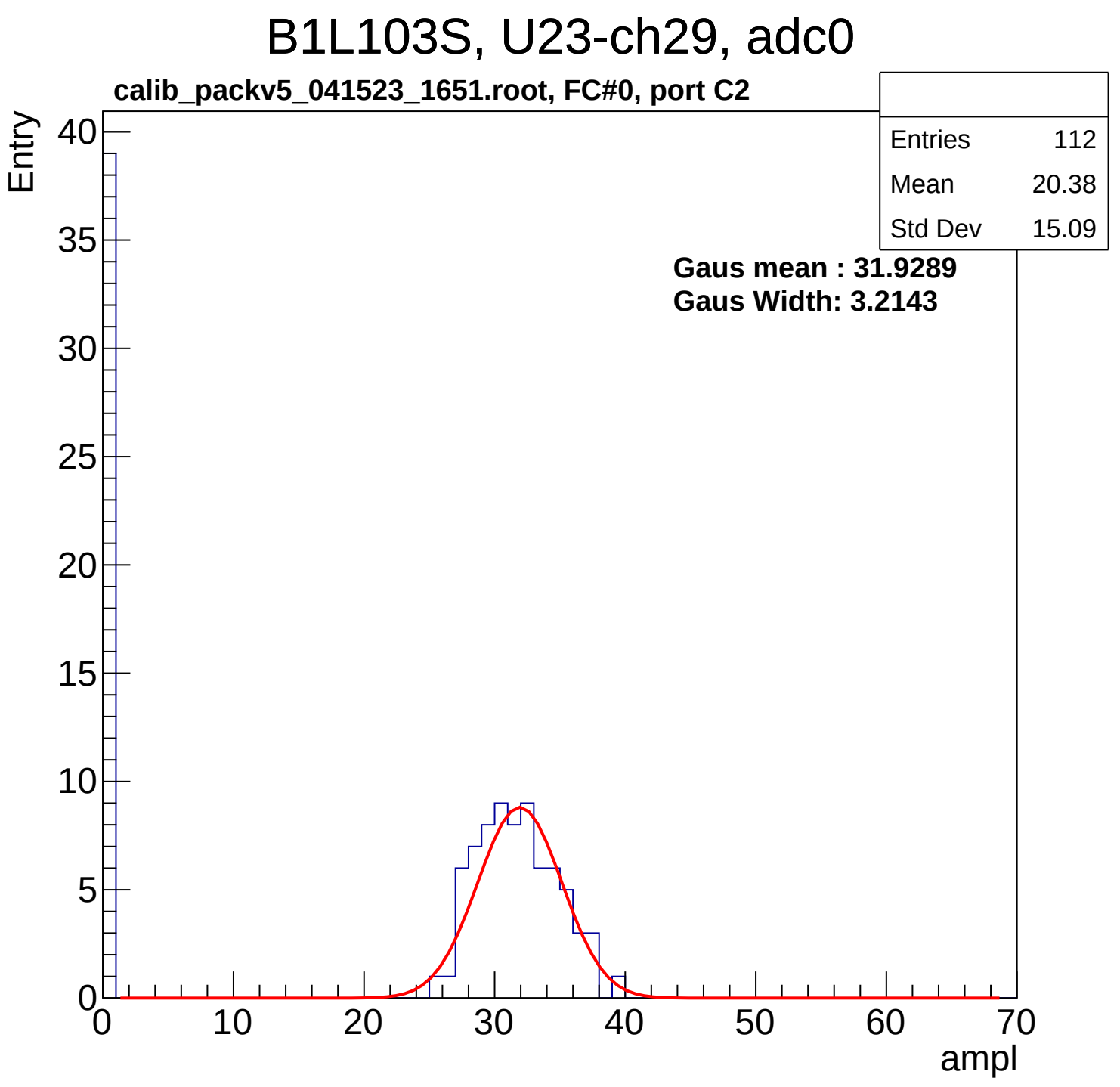

B1L103S, U23-ch28, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 47
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

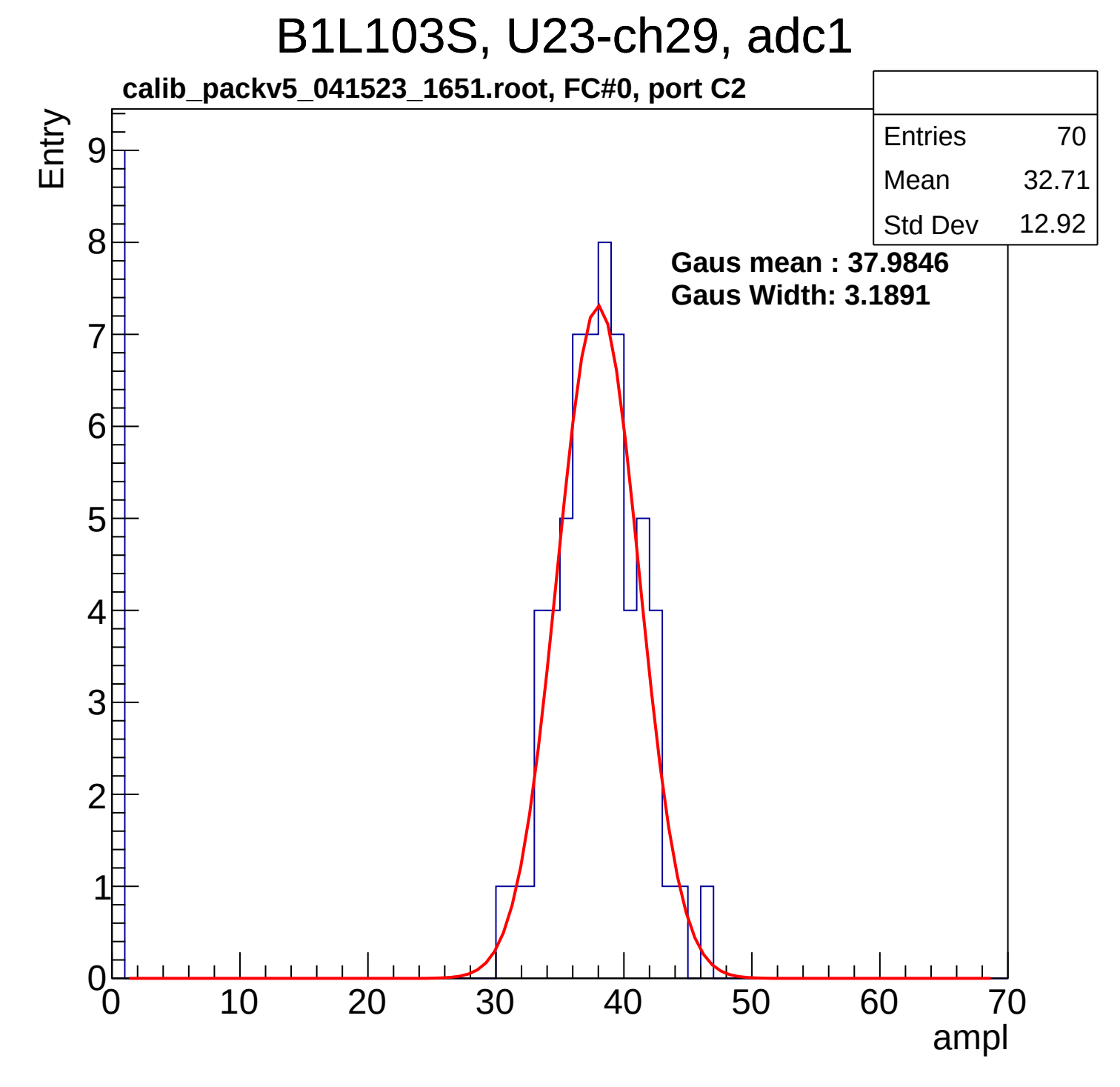

B1L103S, U23-ch28, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 52.79
Std Dev 3.602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

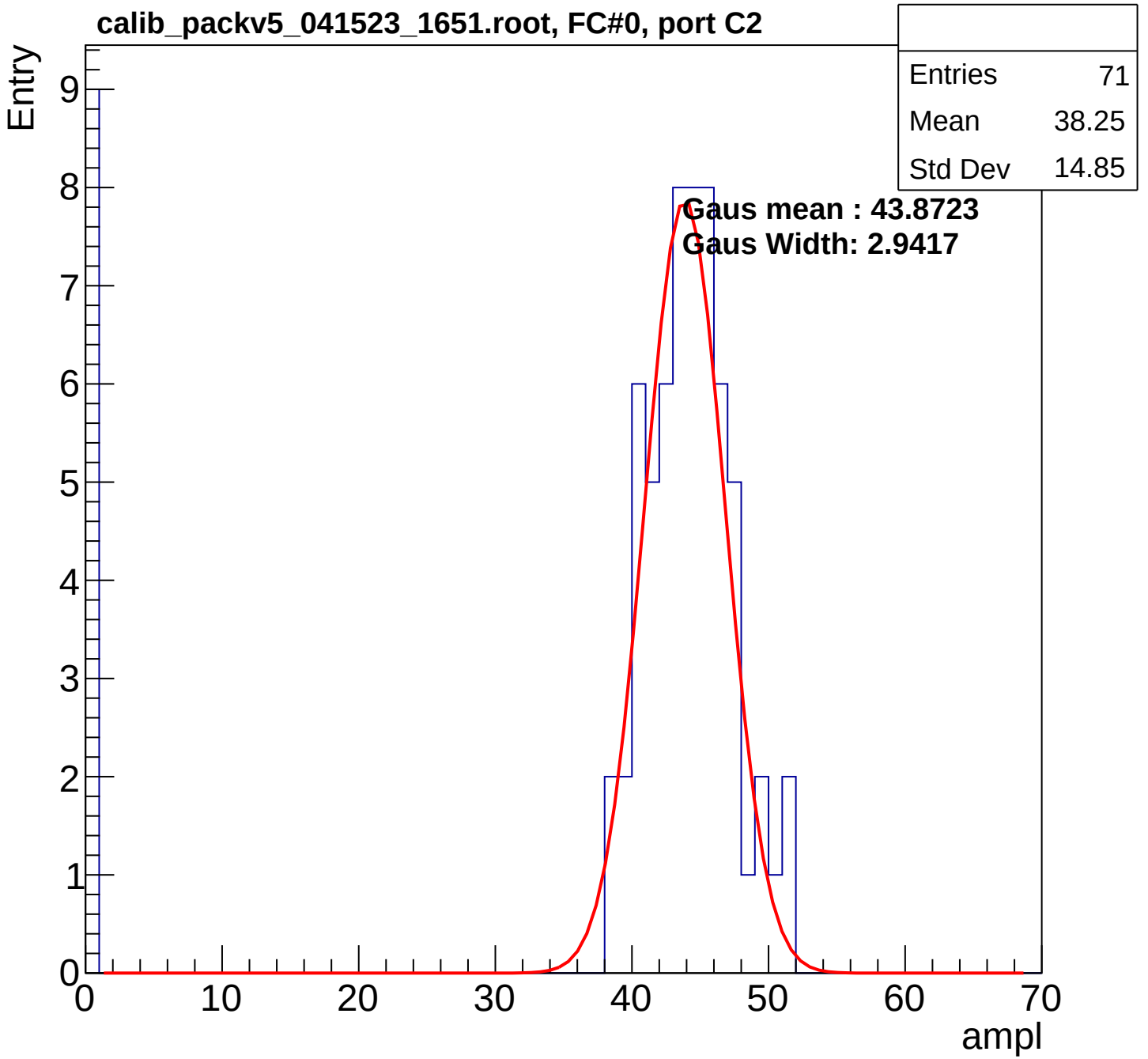

B1L103S, U23-ch28, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 58.63
Std Dev 2.99
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

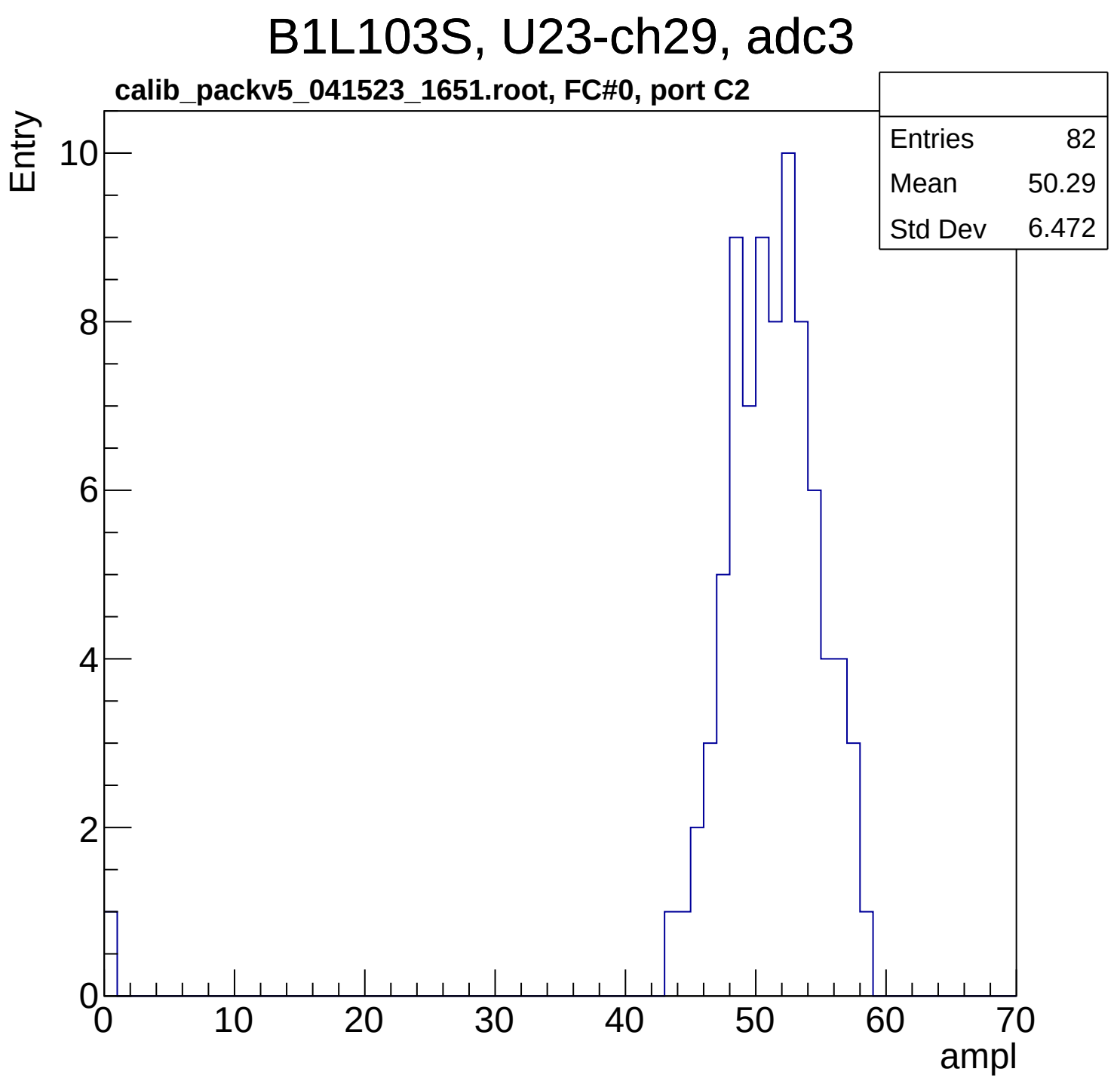

B1L103S, U23-ch28, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 15
Mean 57.07
Std Dev 15.35
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

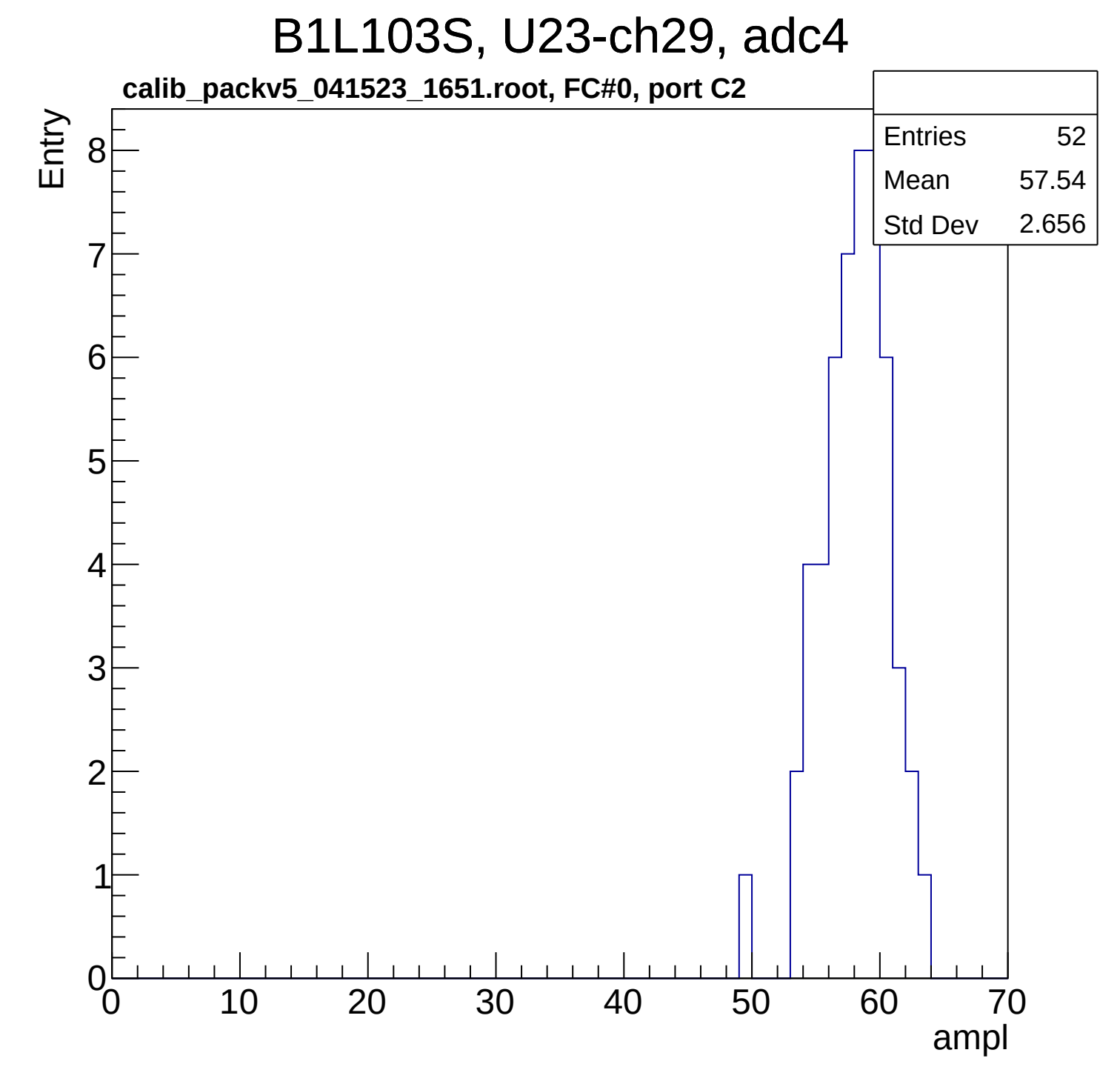

B1L103S, U23-ch28, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

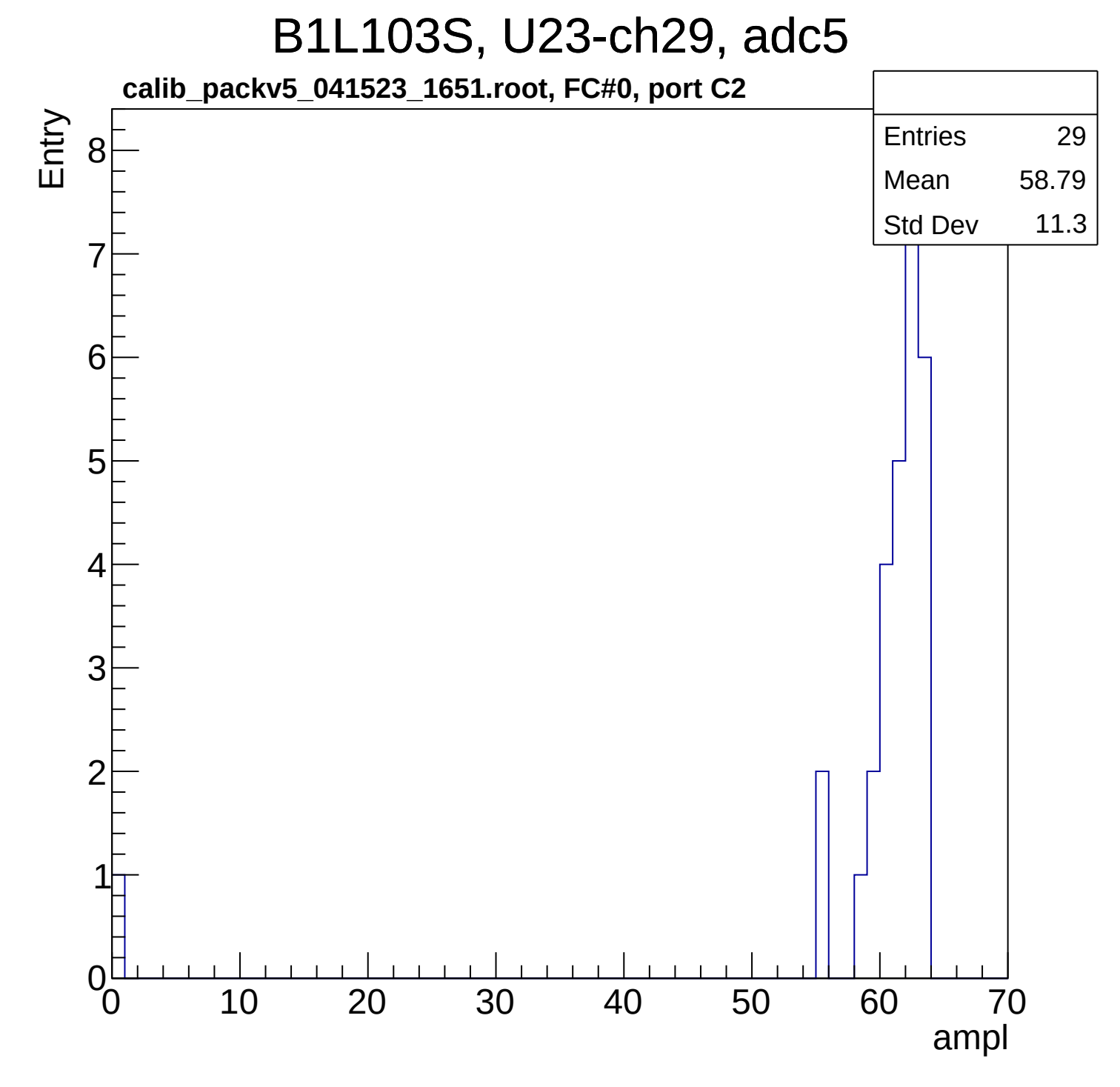

B1L103S, U23-ch29, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 112
Mean 20.38
Std Dev 15.09
Gaus mean : 31.9289
Gaus Width: 3.2143
Entry
ampl
0
5
10
15
20
25
30
35
40
0
10
20
30
40
50
60
70

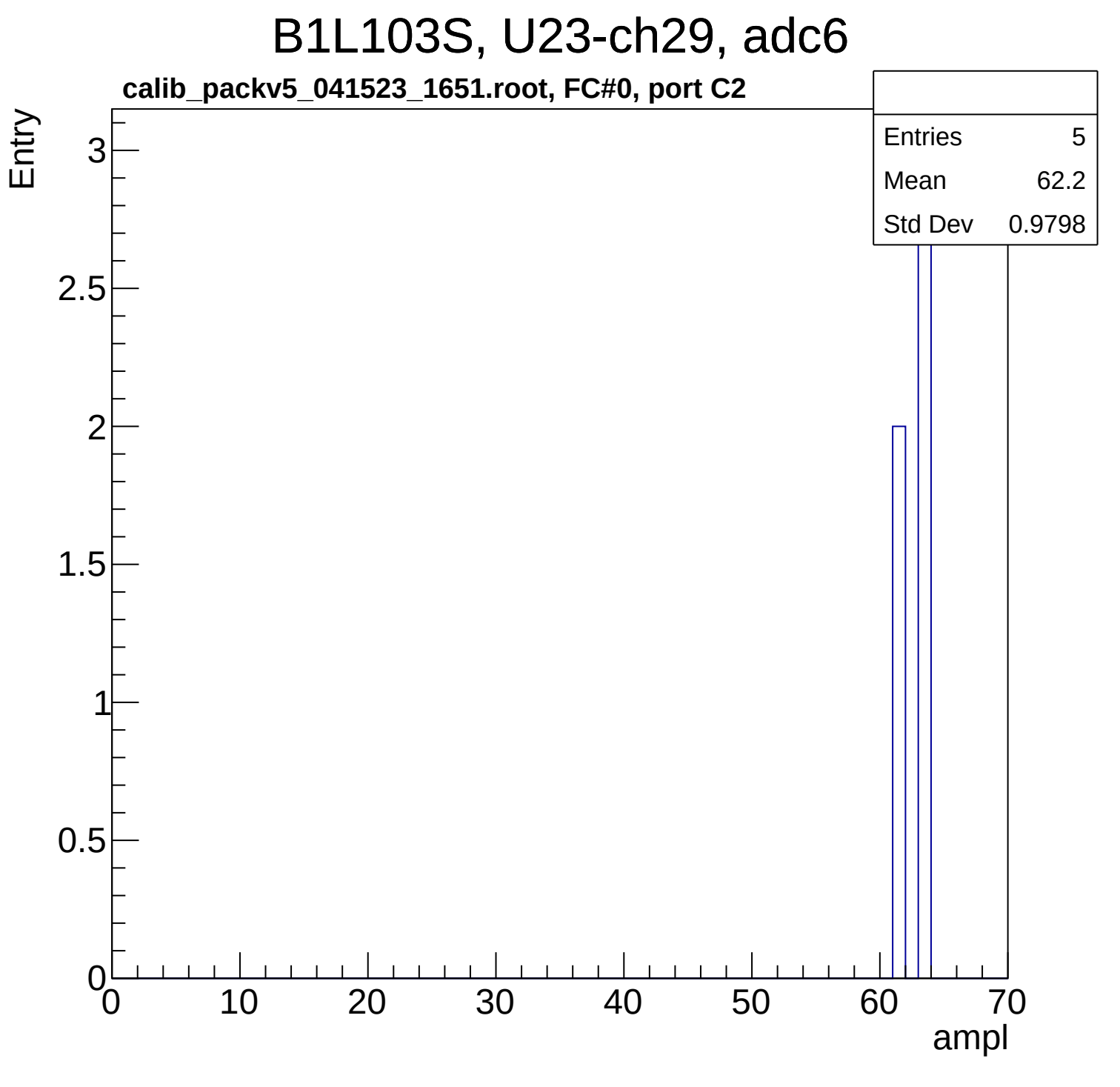

B1L103S, U23-ch29, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 32.71
Std Dev 12.92
Gaus mean : 37.9846
Gaus Width: 3.1891
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

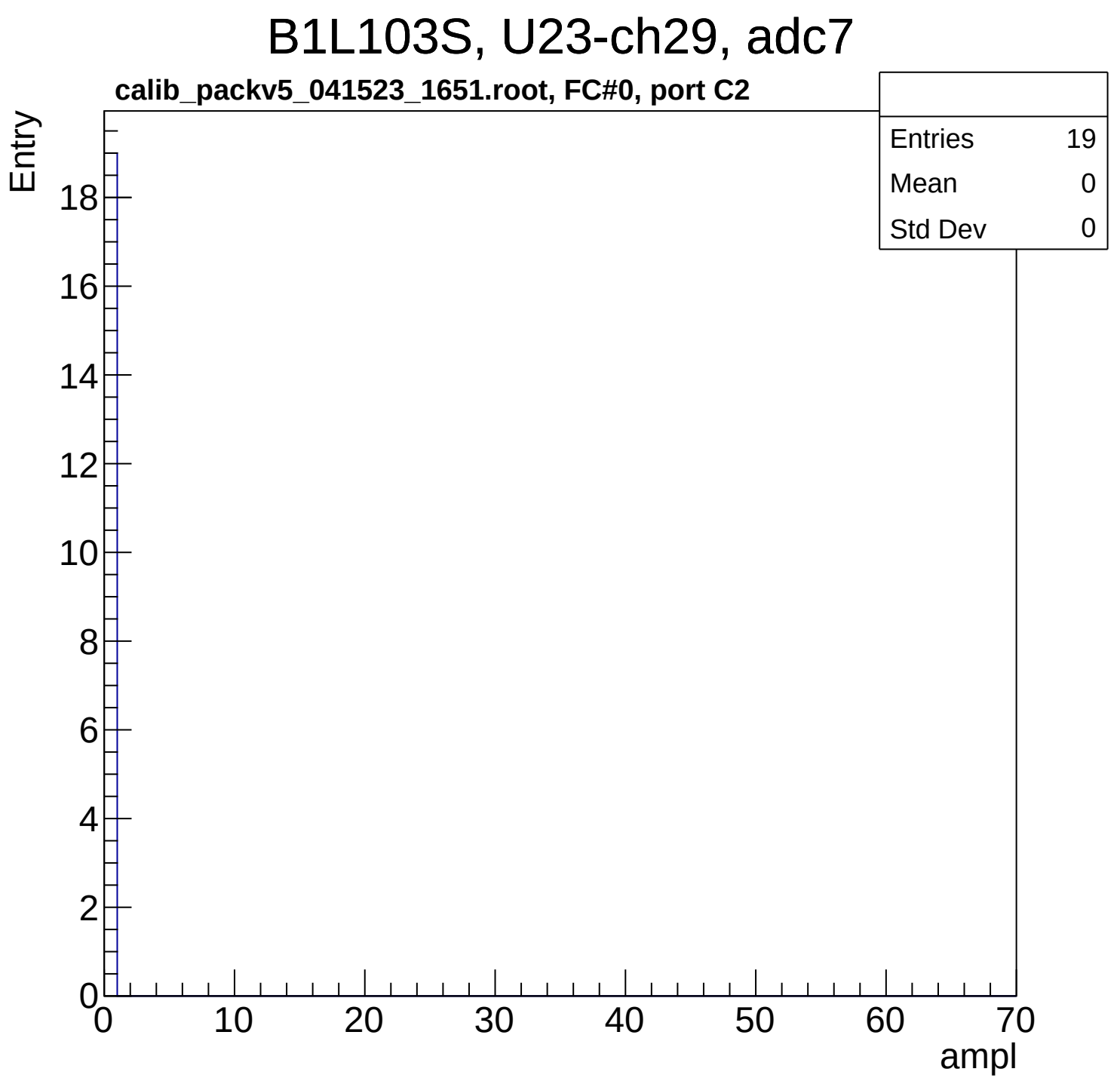

B1L103S, U23-ch29, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 38.25
Std Dev 14.85
Gaus mean : 43.8723
Gaus Width: 2.9417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

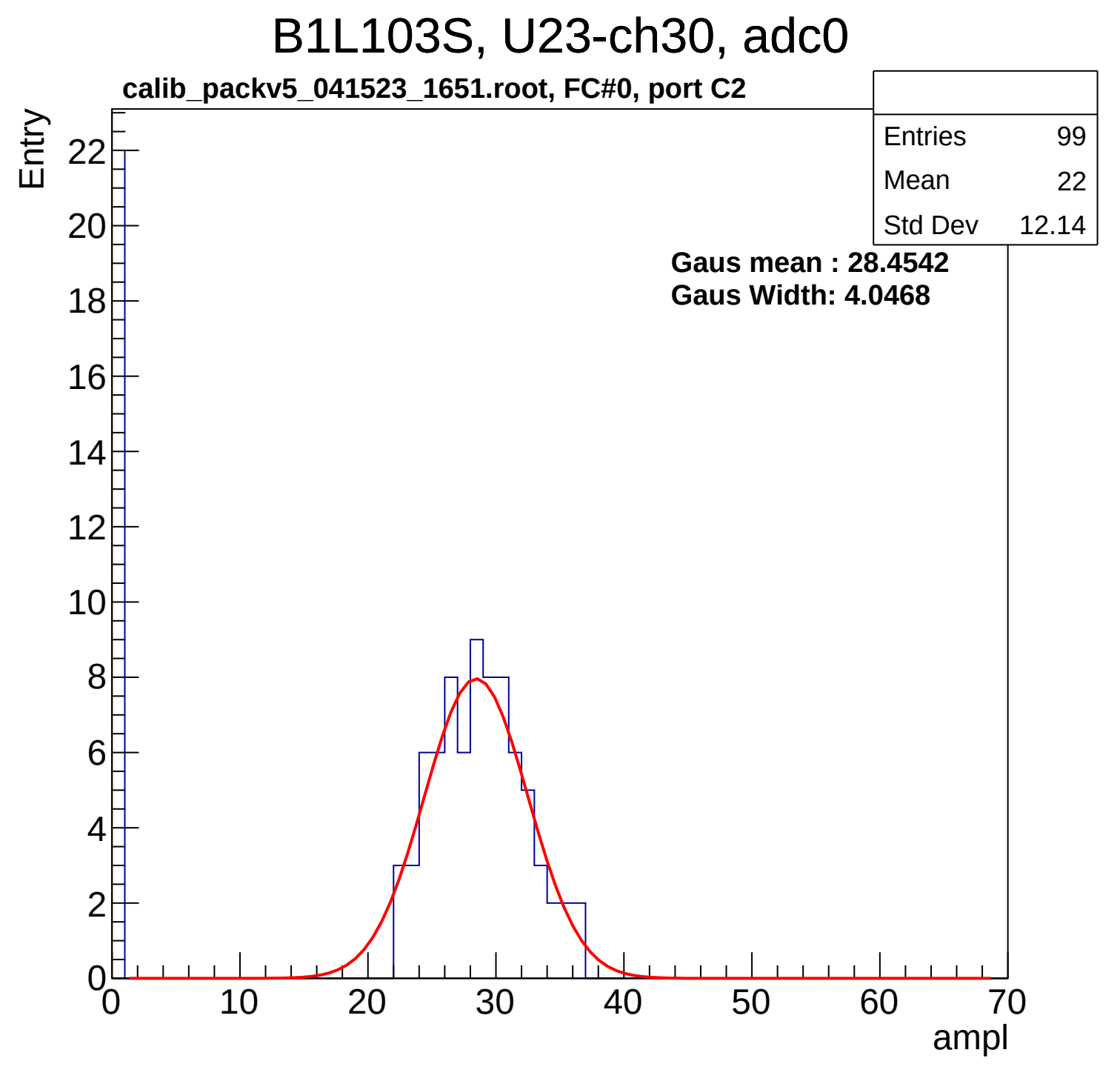

B1L103S, U23-ch29, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 50.29
Std Dev 6.472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

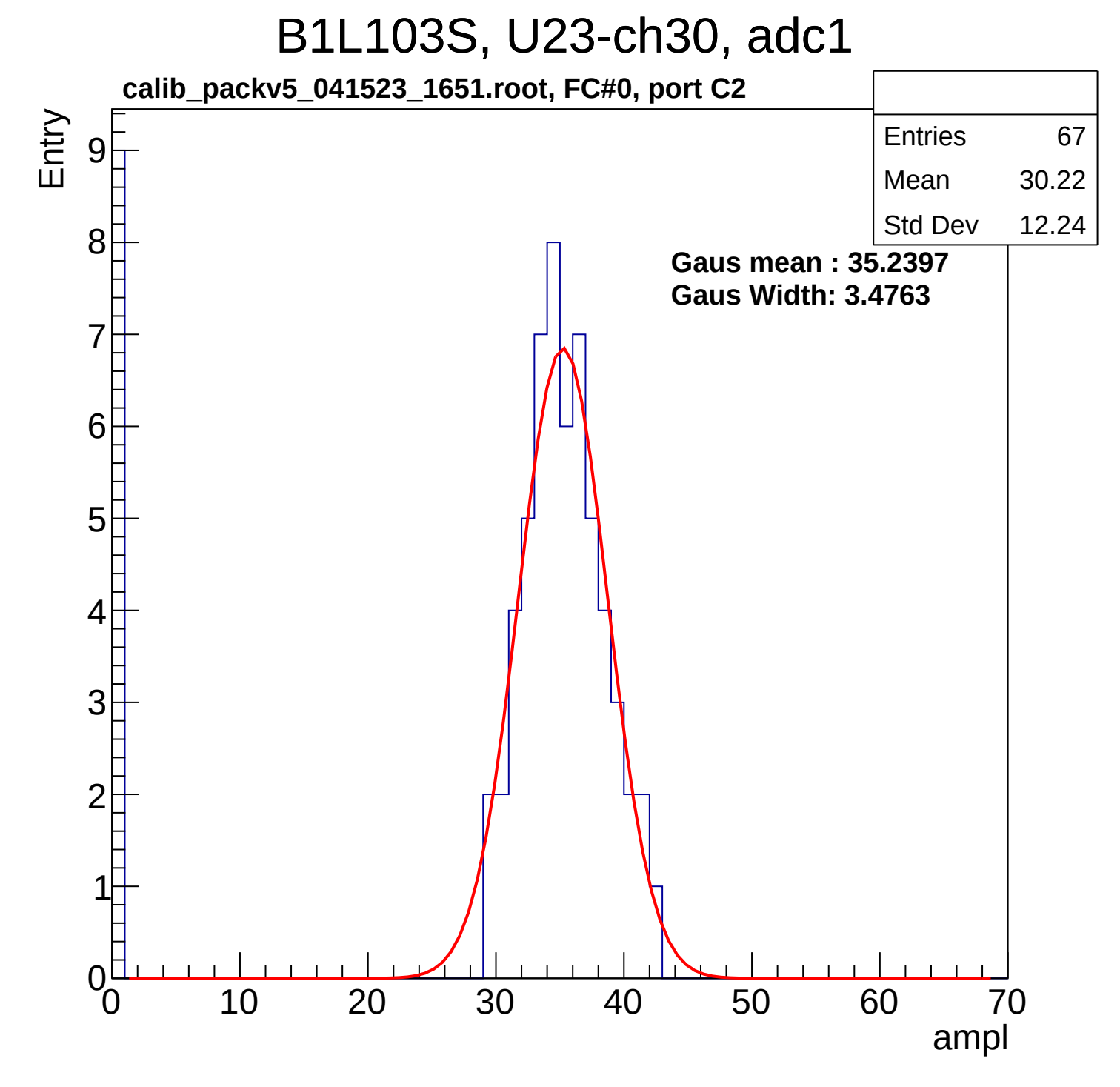

B1L103S, U23-ch29, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 57.54
Std Dev 2.656
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

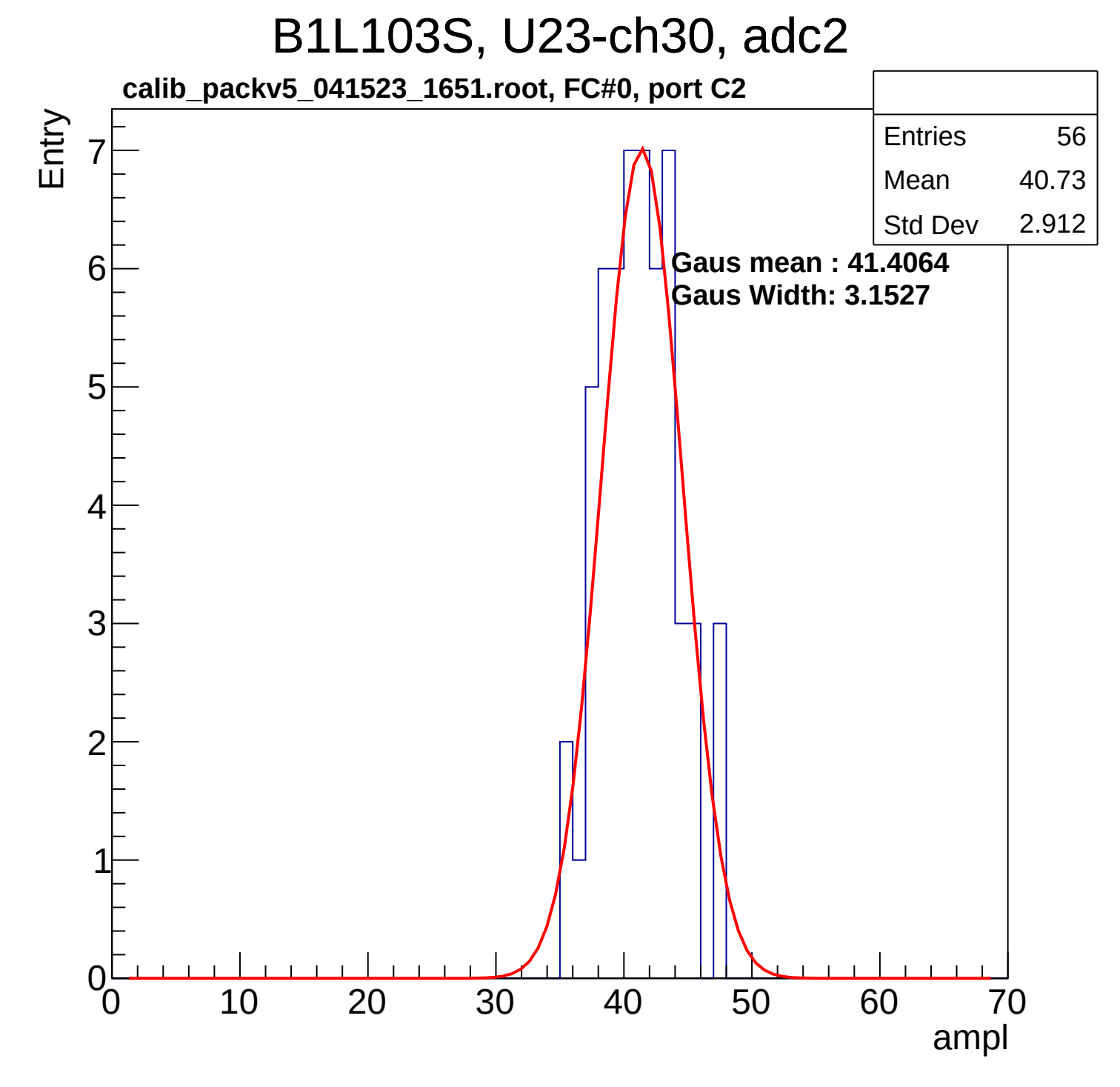

B1L103S, U23-ch29, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 29
Mean 58.79
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

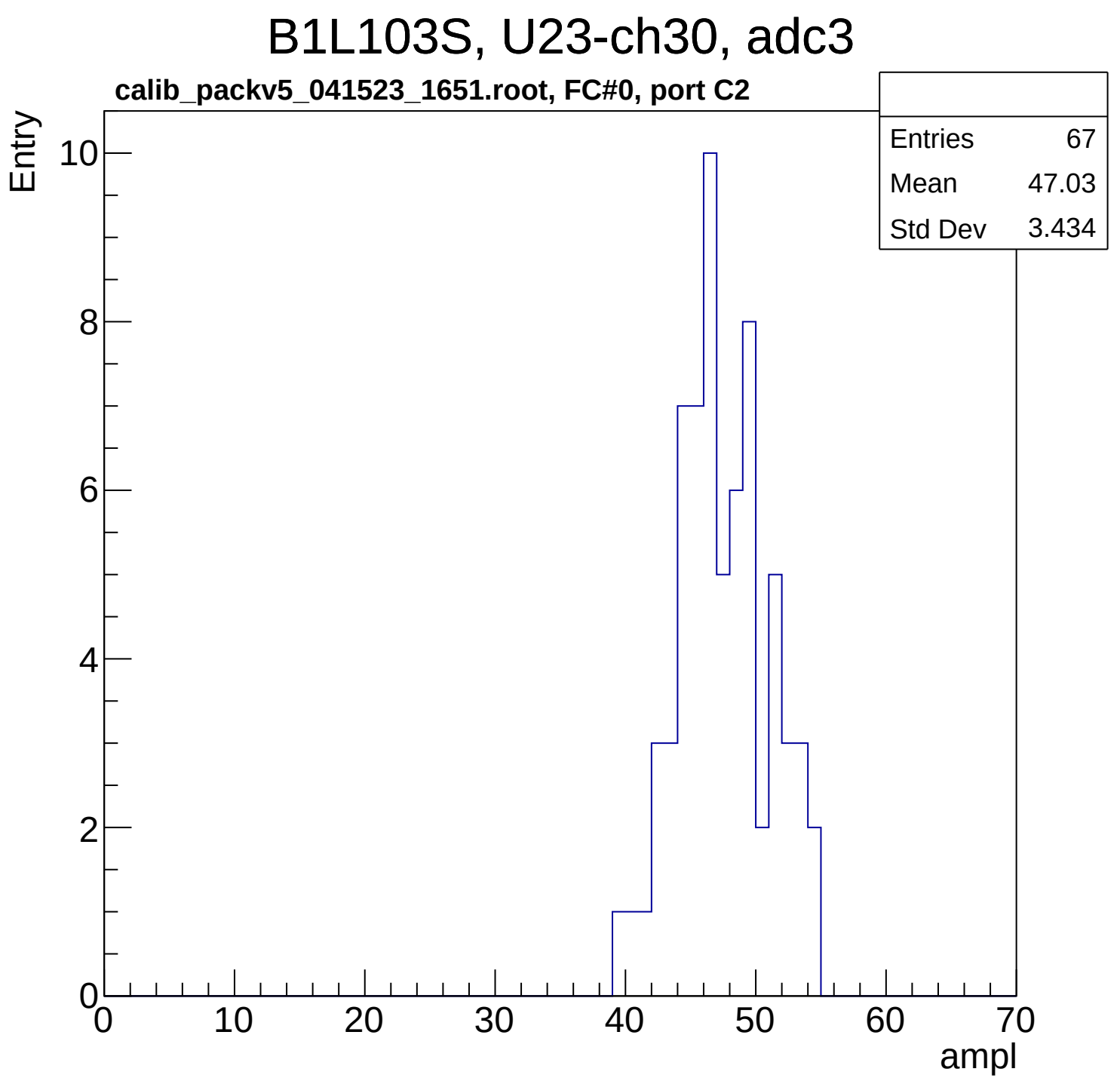

B1L103S, U23-ch29, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 62.2
Std Dev 0.9798
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

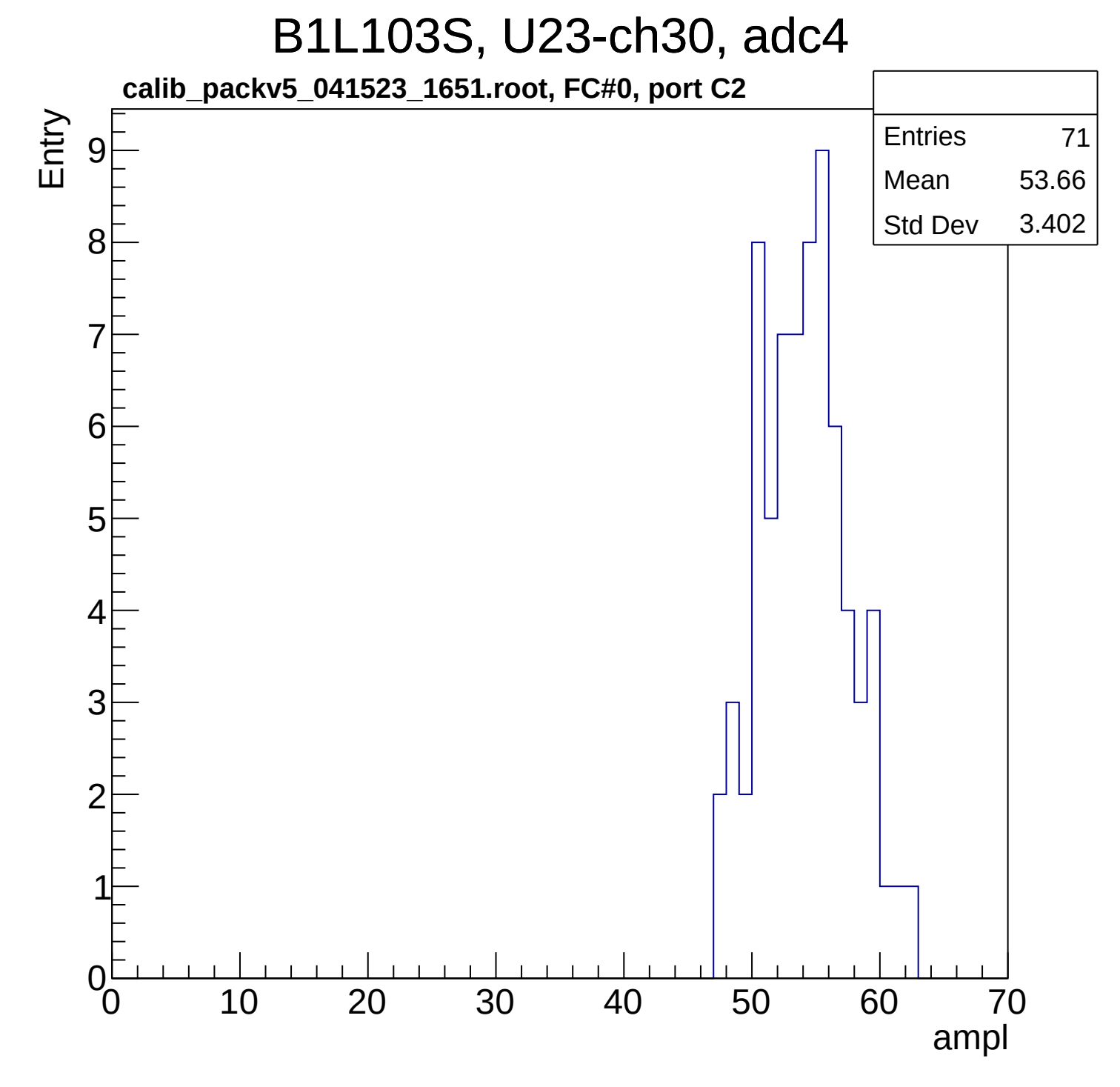

B1L103S, U23-ch29, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

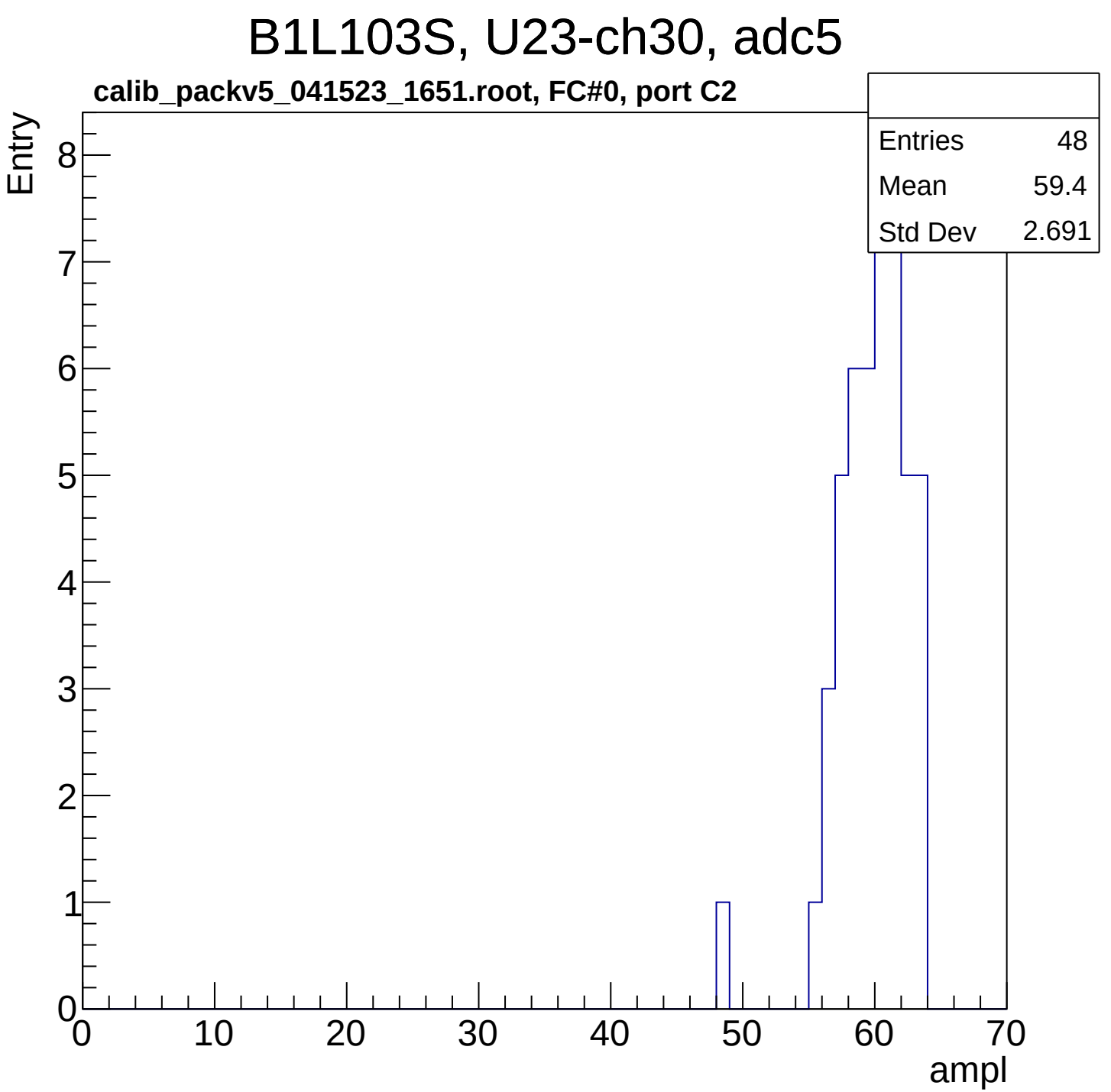

B1L103S, U23-ch30, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 99
Mean 22
Std Dev 12.14
Gaus mean : 28.4542
Gaus Width: 4.0468
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

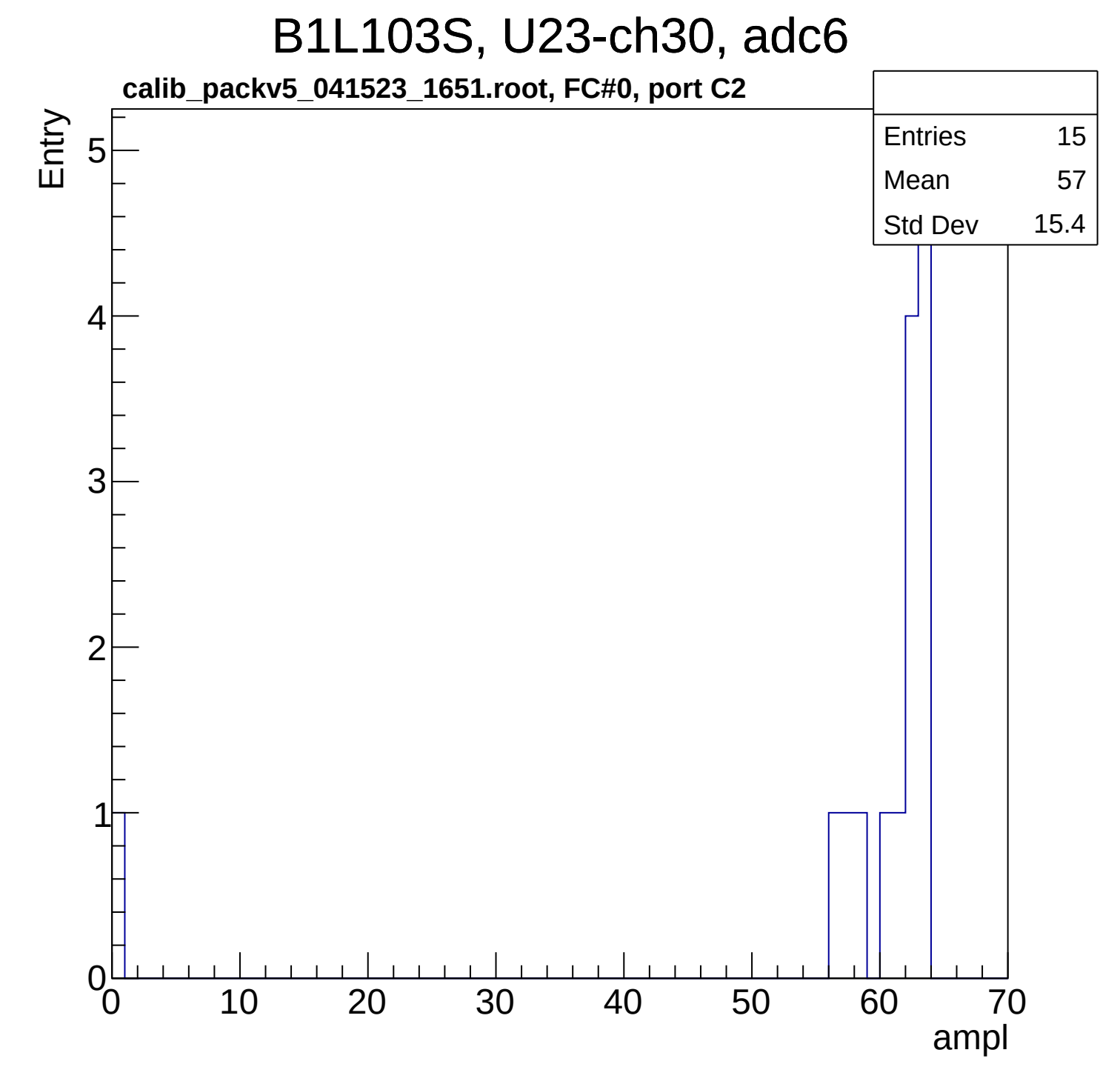

B1L103S, U23-ch30, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 30.22
Std Dev 12.24
Gaus mean : 35.2397
Gaus Width: 3.4763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

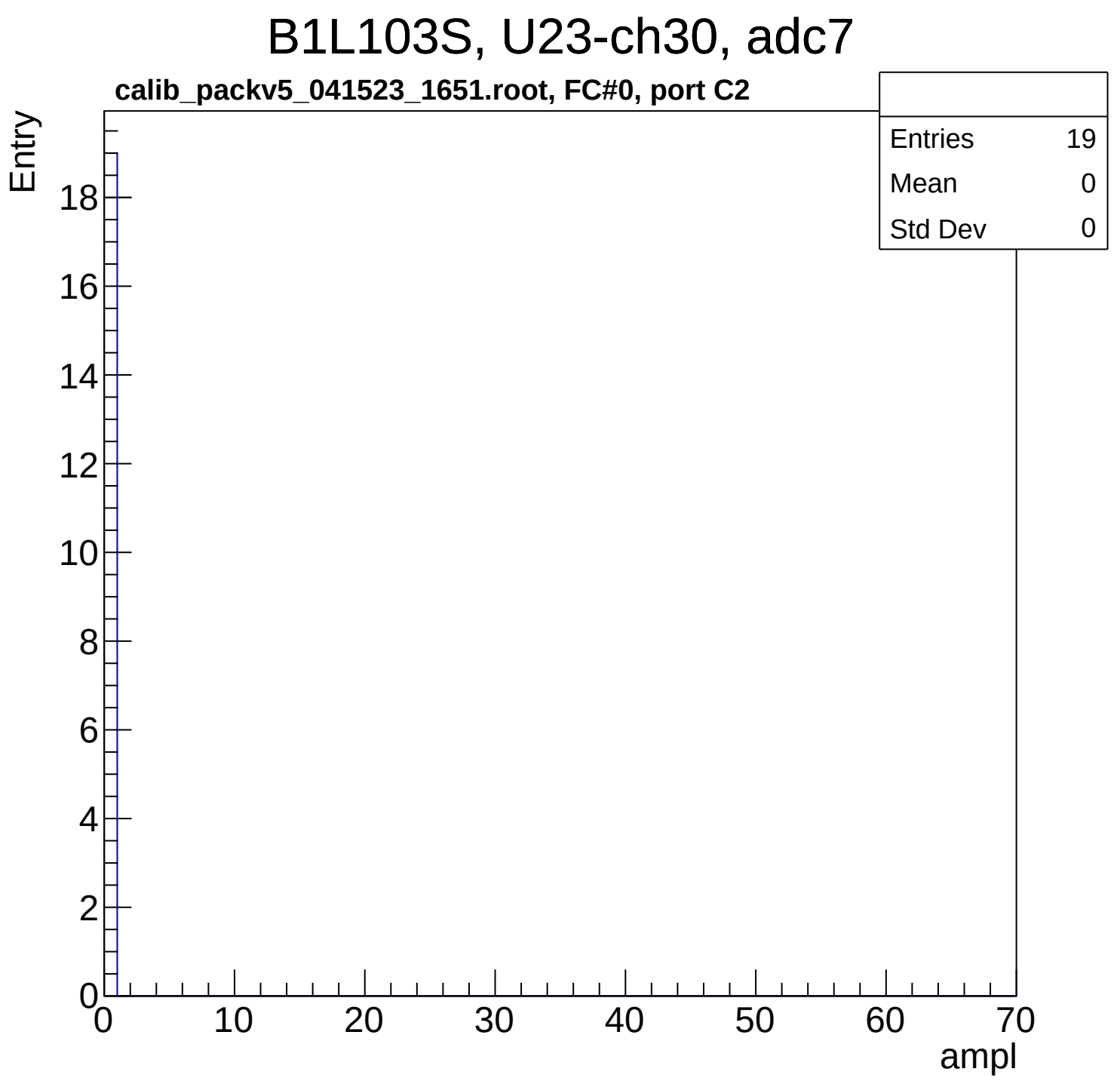

B1L103S, U23-ch30, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 40.73
Std Dev 2.912
Gaus mean : 41.4064
Gaus Width: 3.1527
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

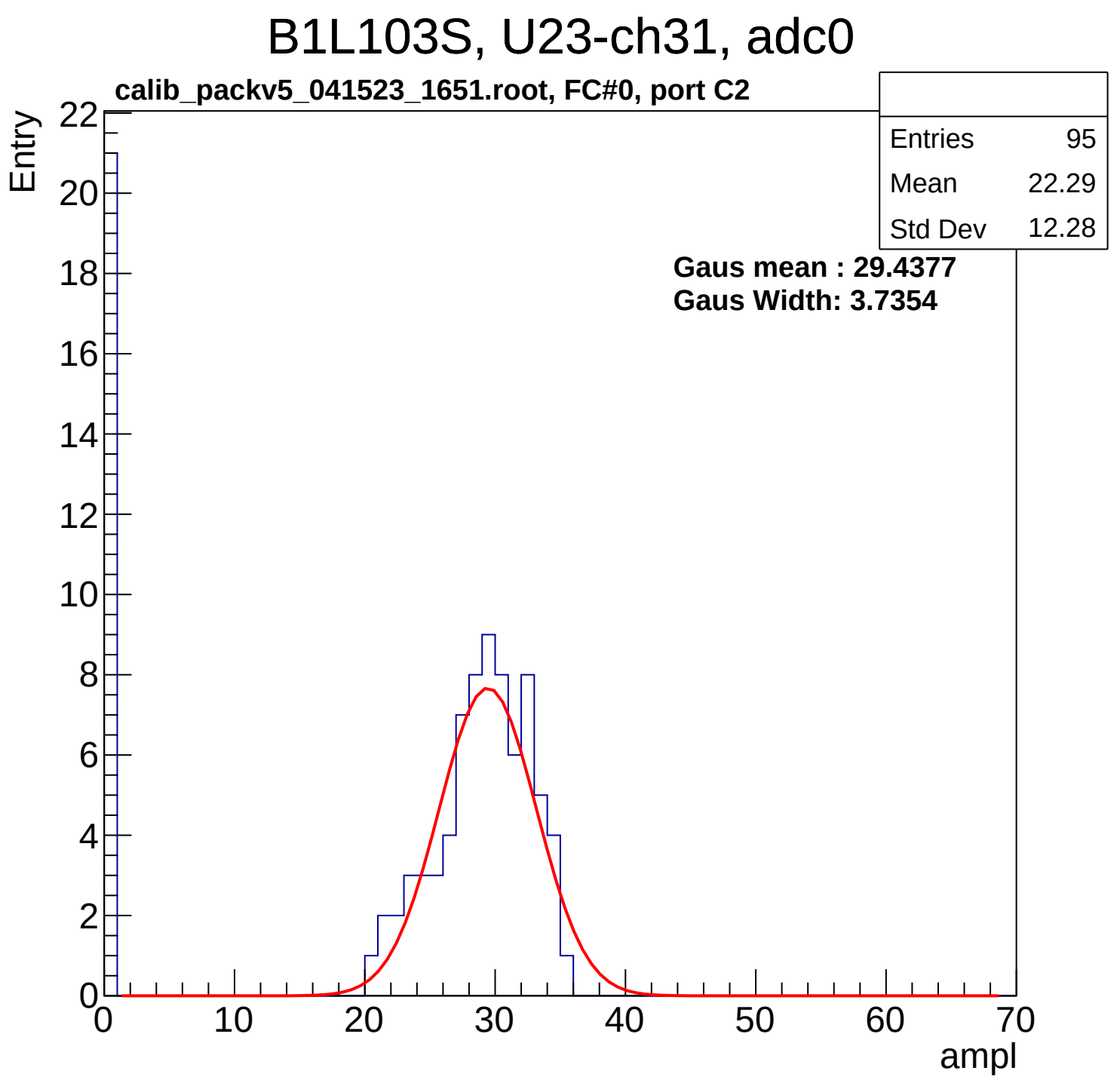

B1L103S, U23-ch30, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 47.03
Std Dev 3.434
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

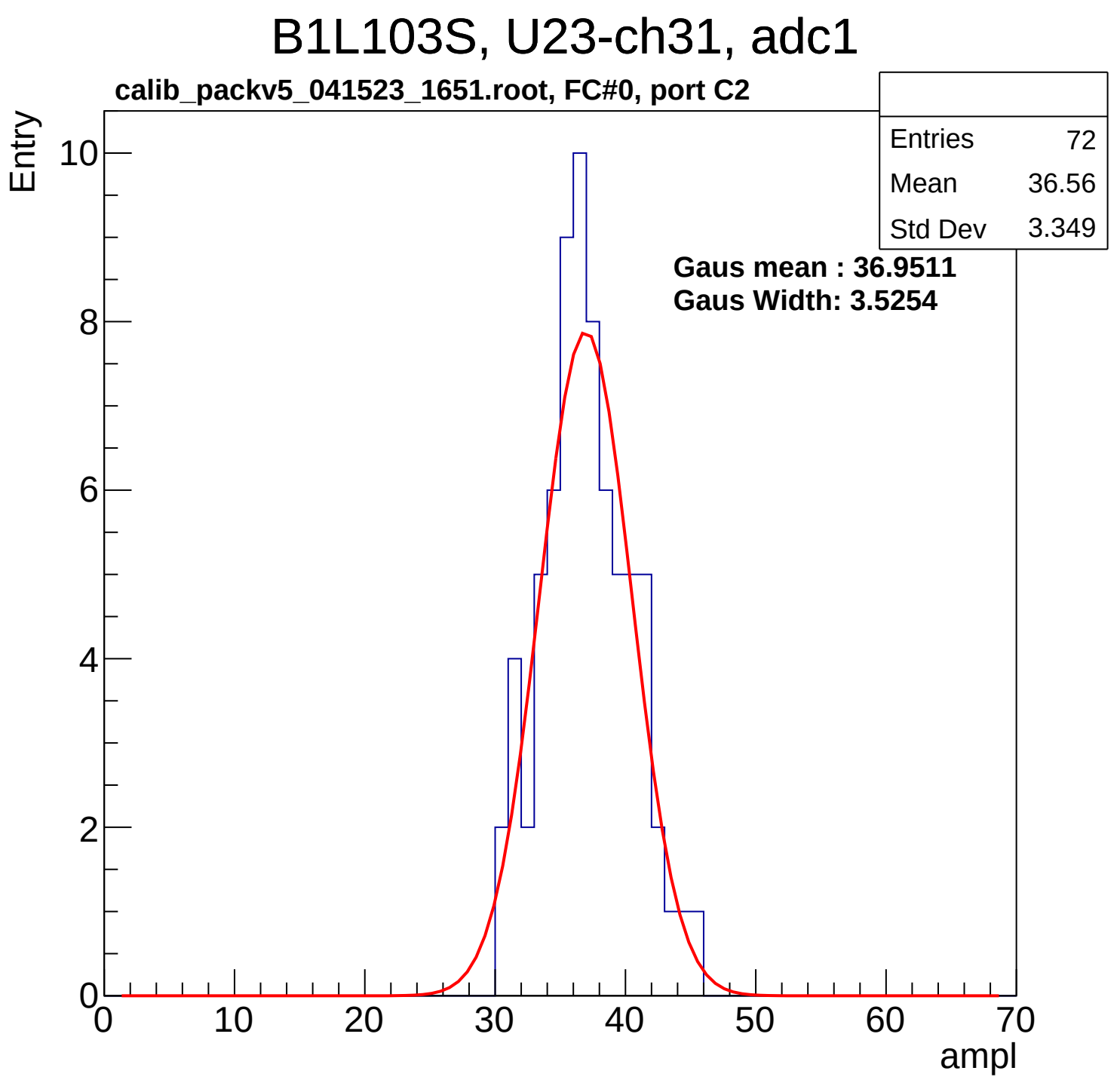

B1L103S, U23-ch30, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 53.66
Std Dev 3.402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

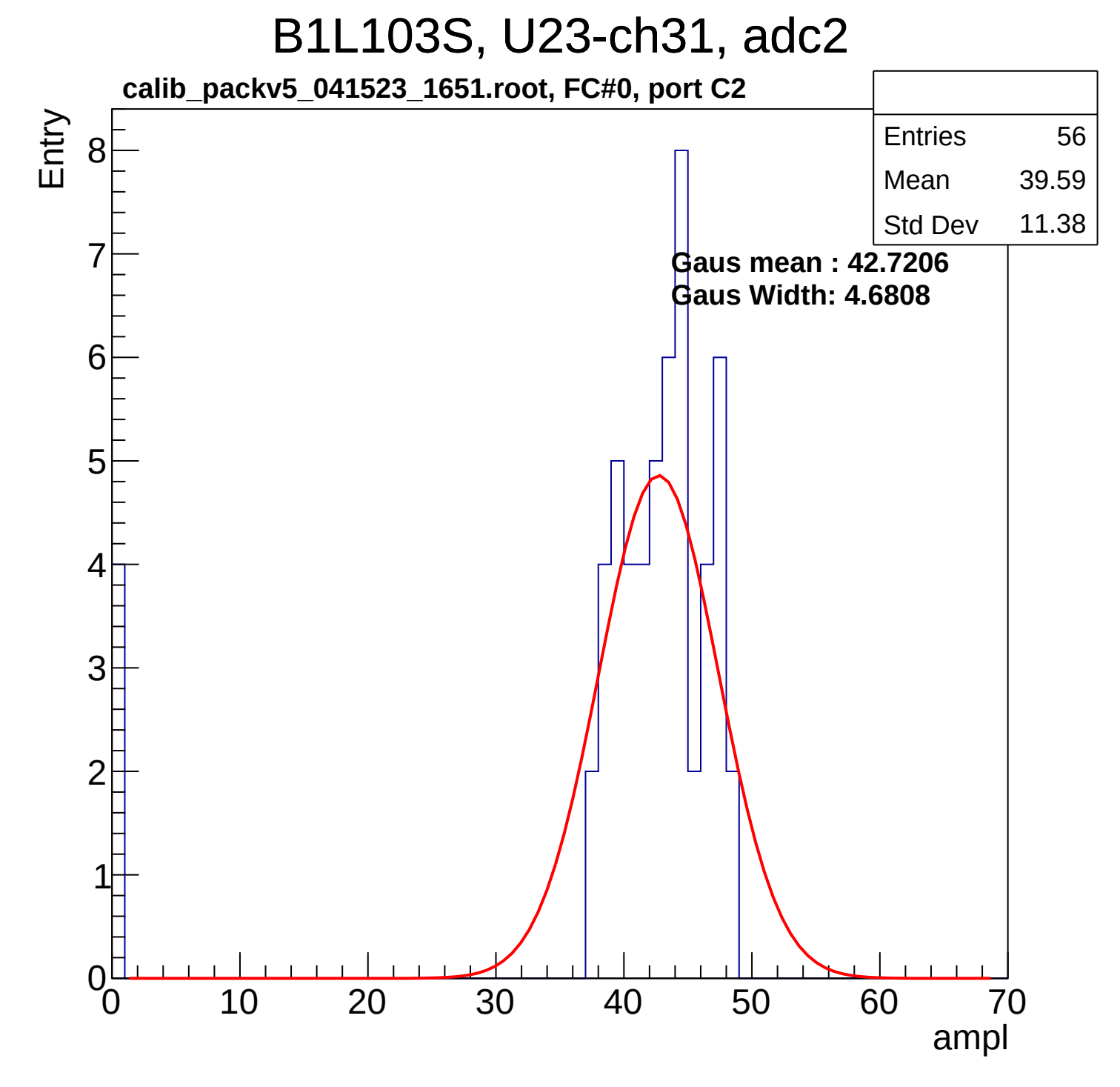

B1L103S, U23-ch30, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 59.4
Std Dev 2.691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

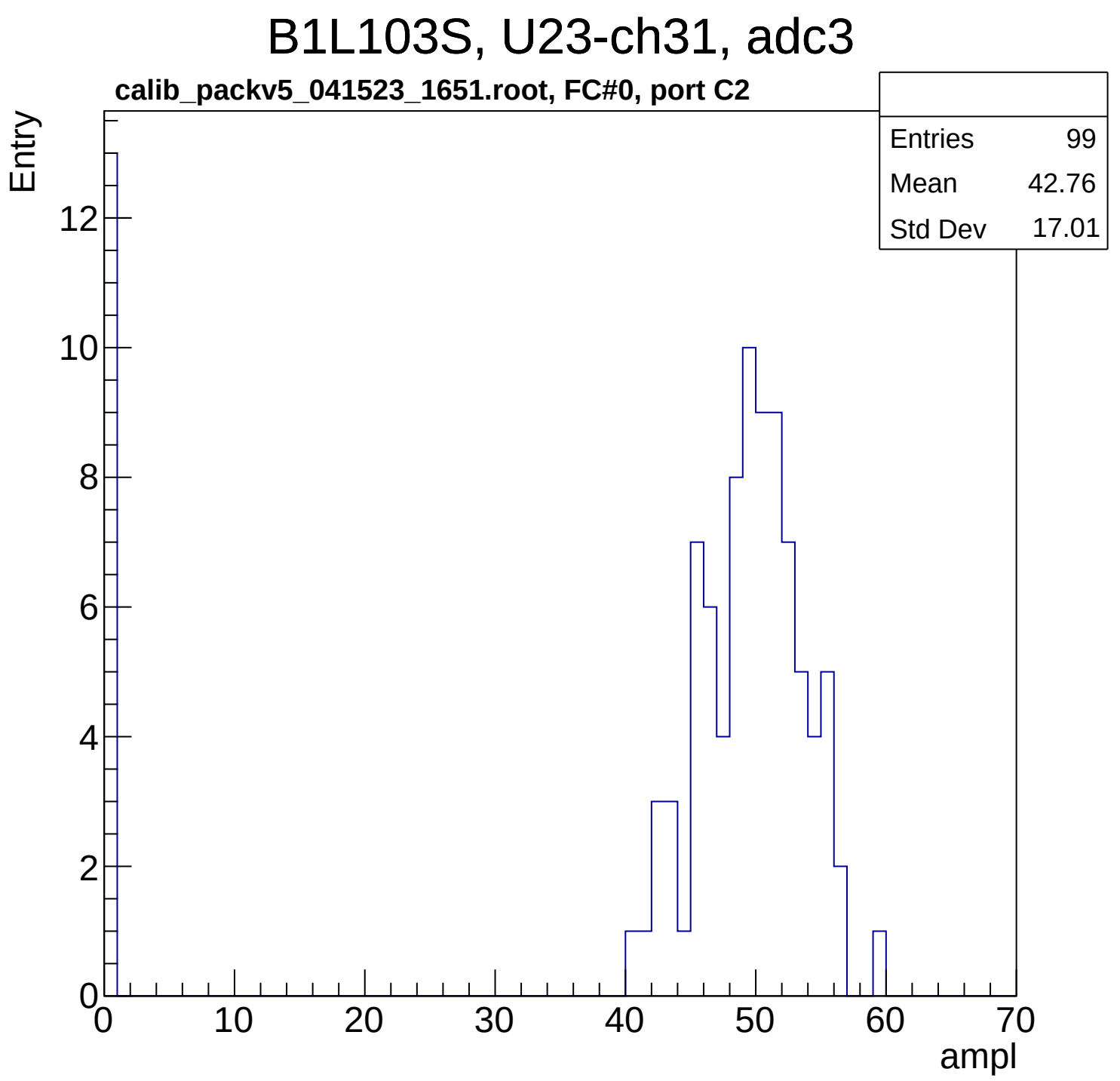

B1L103S, U23-ch30, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 15
Mean 57
Std Dev 15.4
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

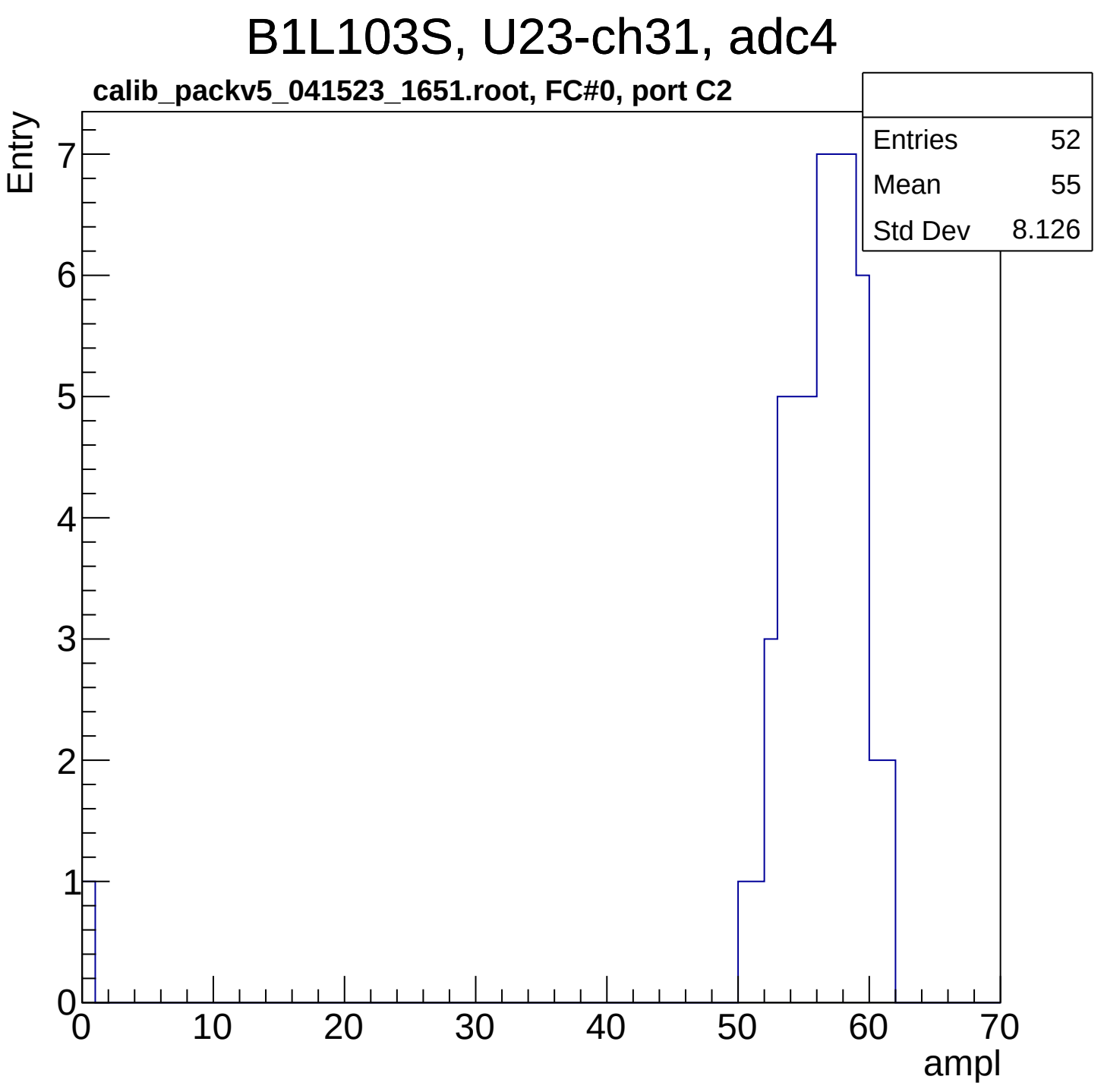

B1L103S, U23-ch30, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

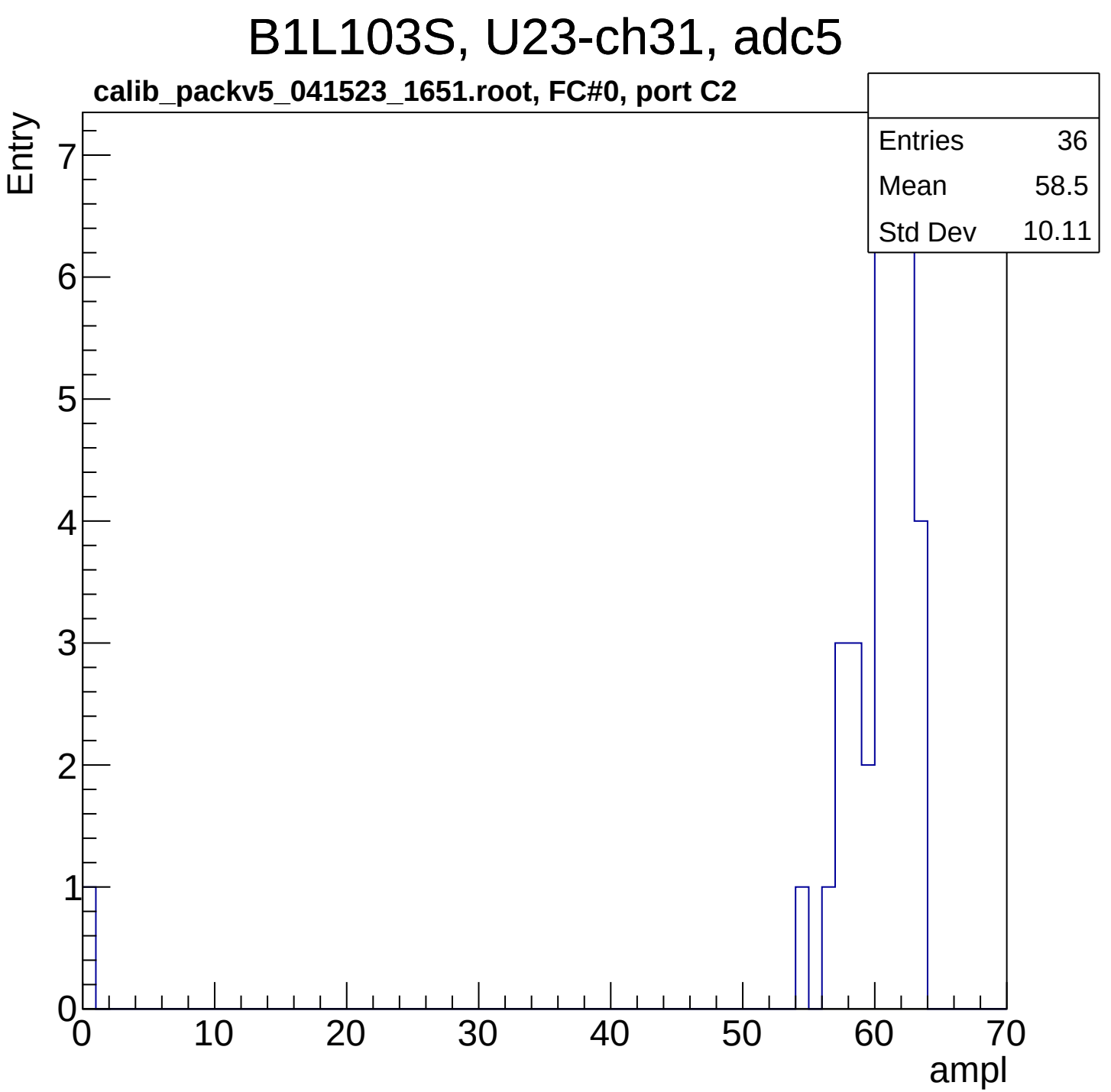

B1L103S, U23-ch31, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 95
Mean 22.29
Std Dev 12.28
Gaus mean : 29.4377
Gaus Width: 3.7354
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

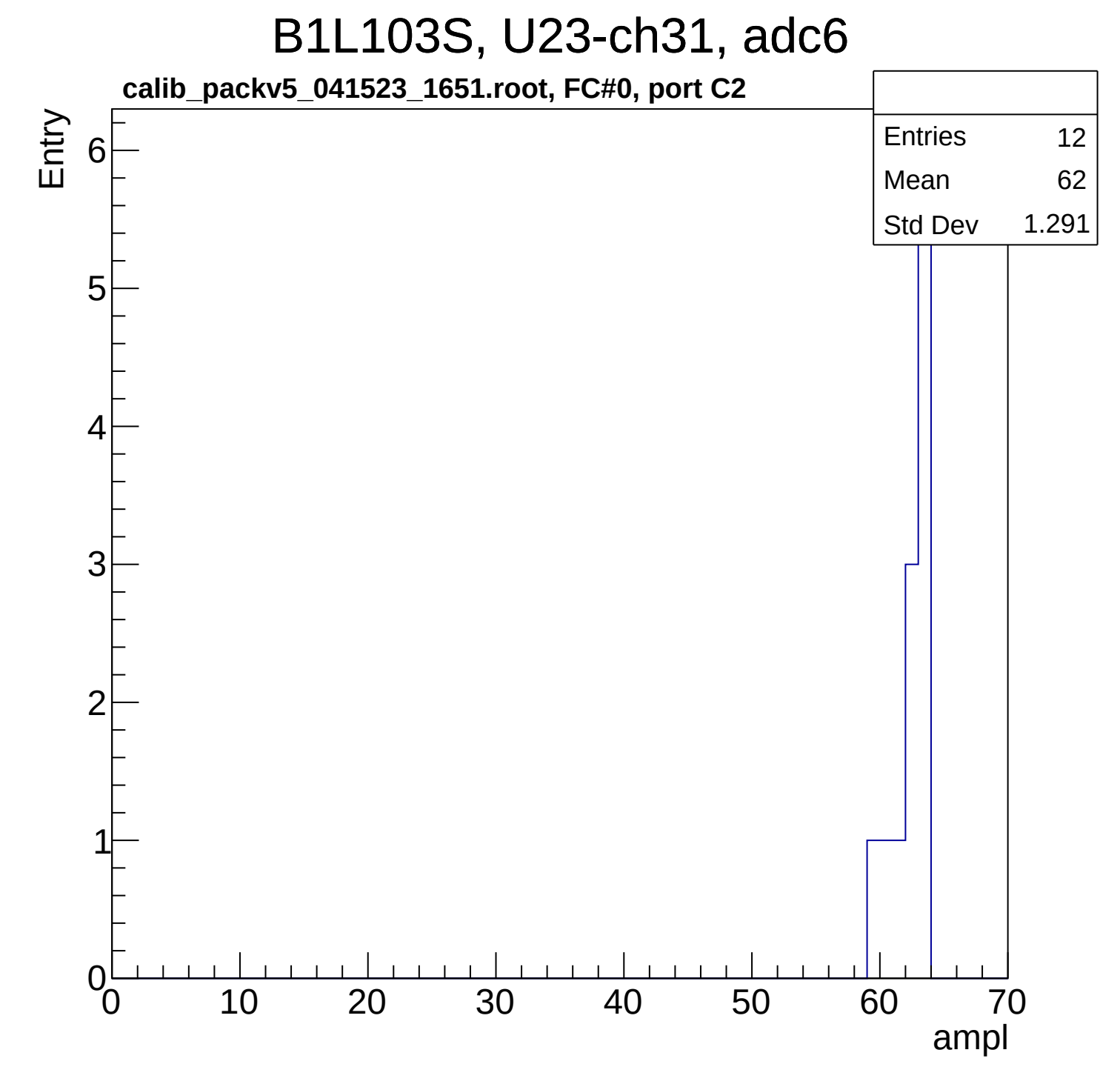

B1L103S, U23-ch31, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 36.56
Std Dev 3.349
Gaus mean : 36.9511
Gaus Width: 3.5254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

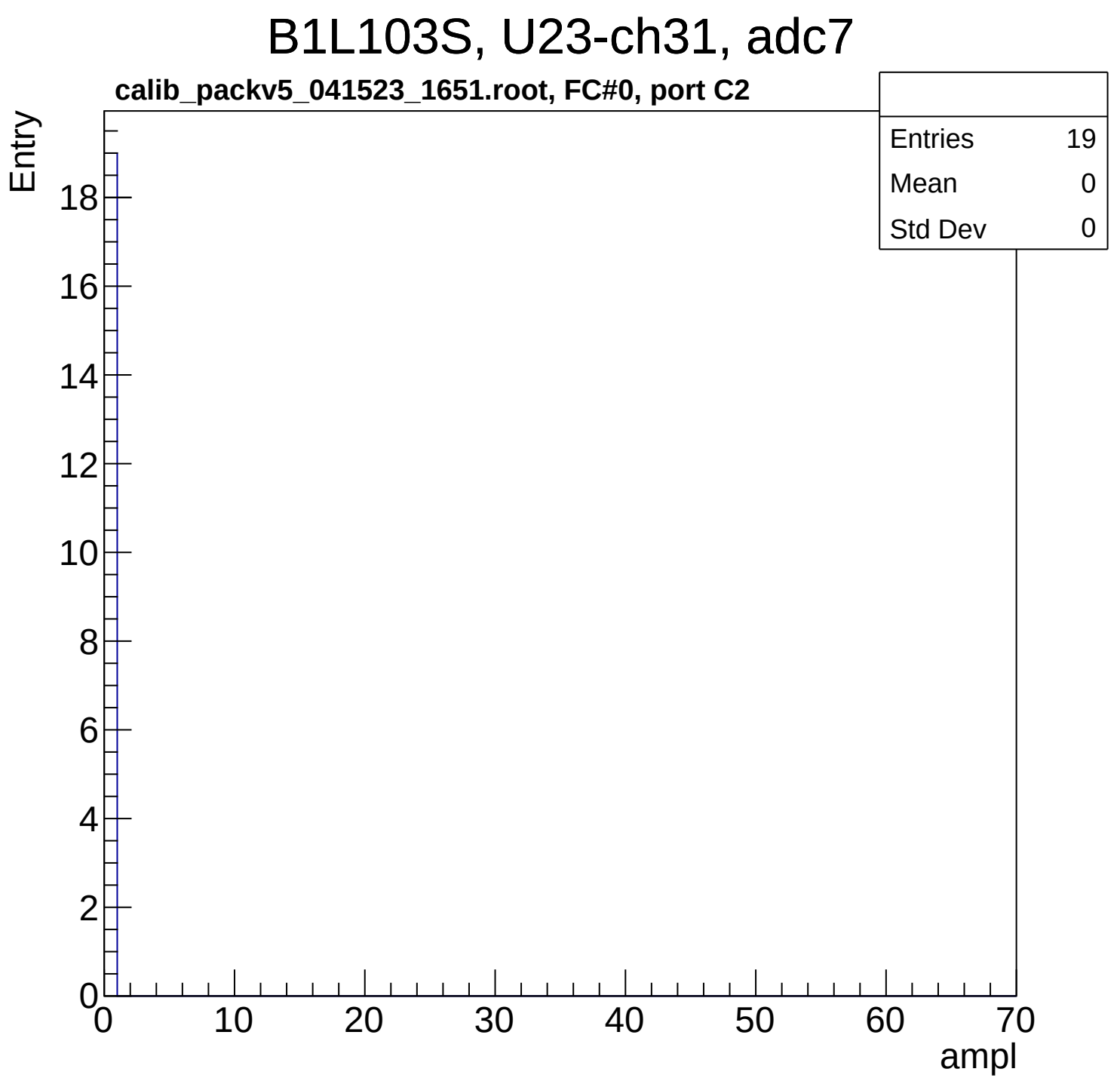

B1L103S, U23-ch31, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 39.59
Std Dev 11.38
Gaus mean : 42.7206
Gaus Width: 4.6808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

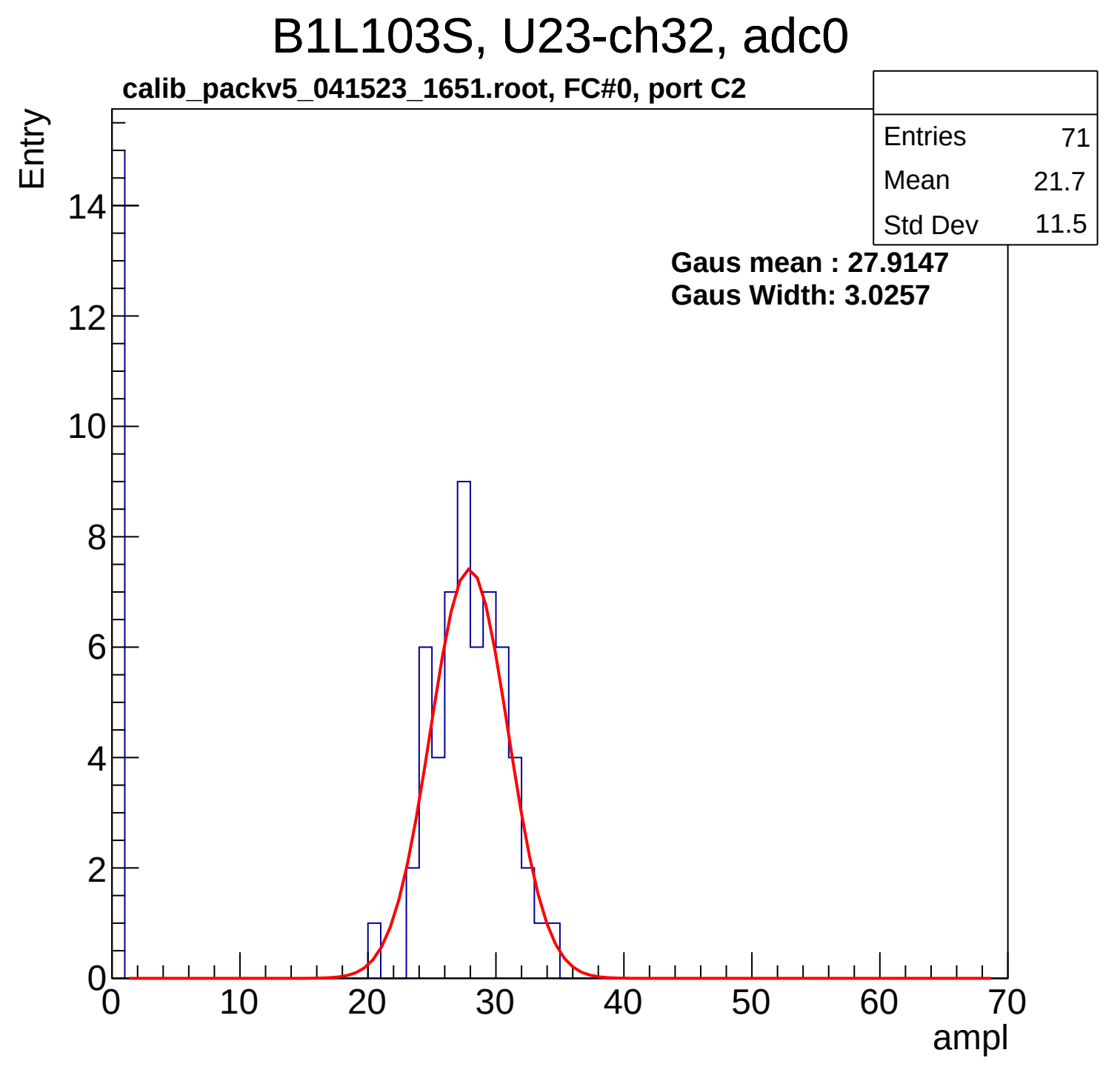

B1L103S, U23-ch31, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 99
Mean 42.76
Std Dev 17.01
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

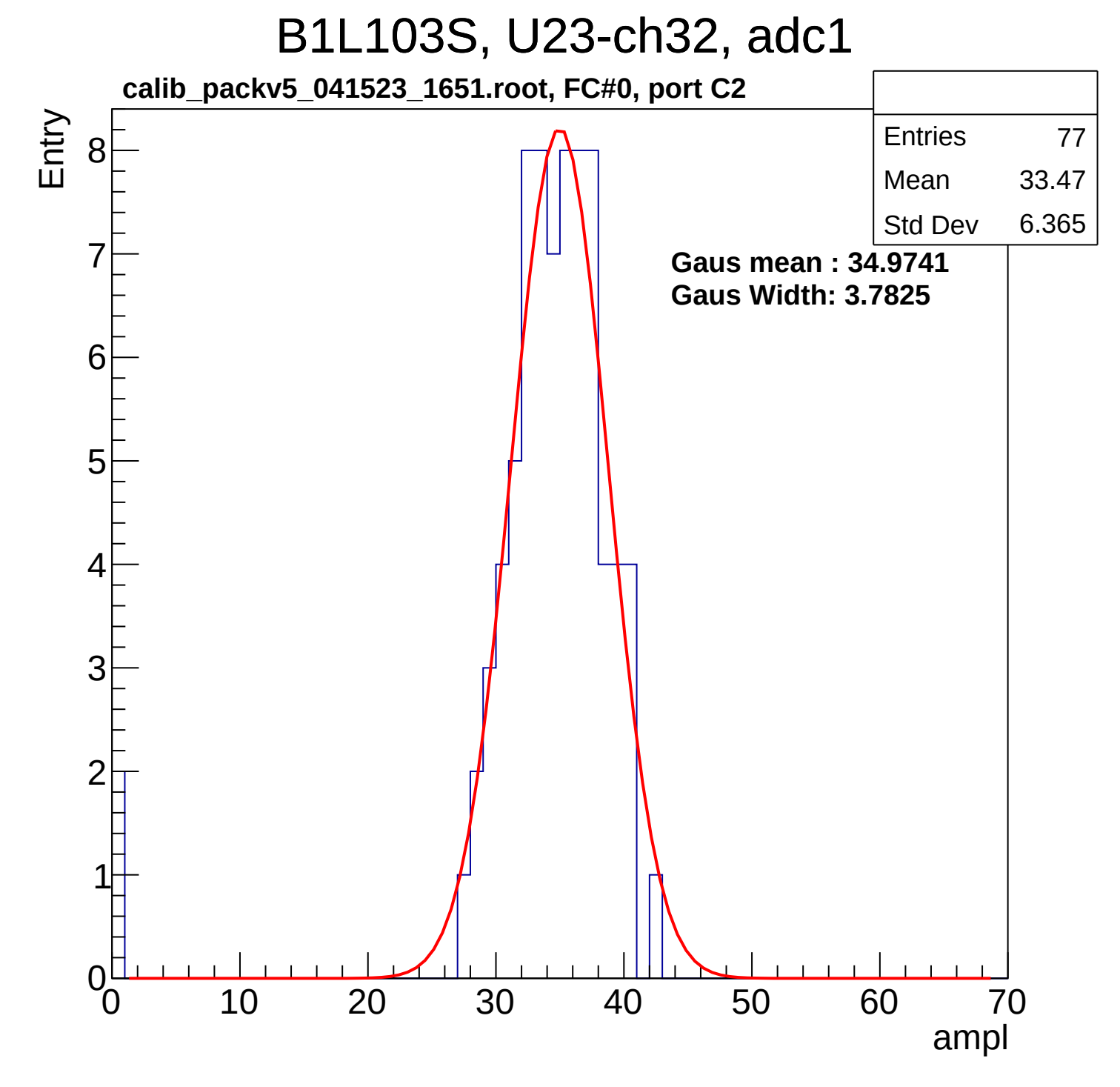

B1L103S, U23-ch31, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 55
Std Dev 8.126
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

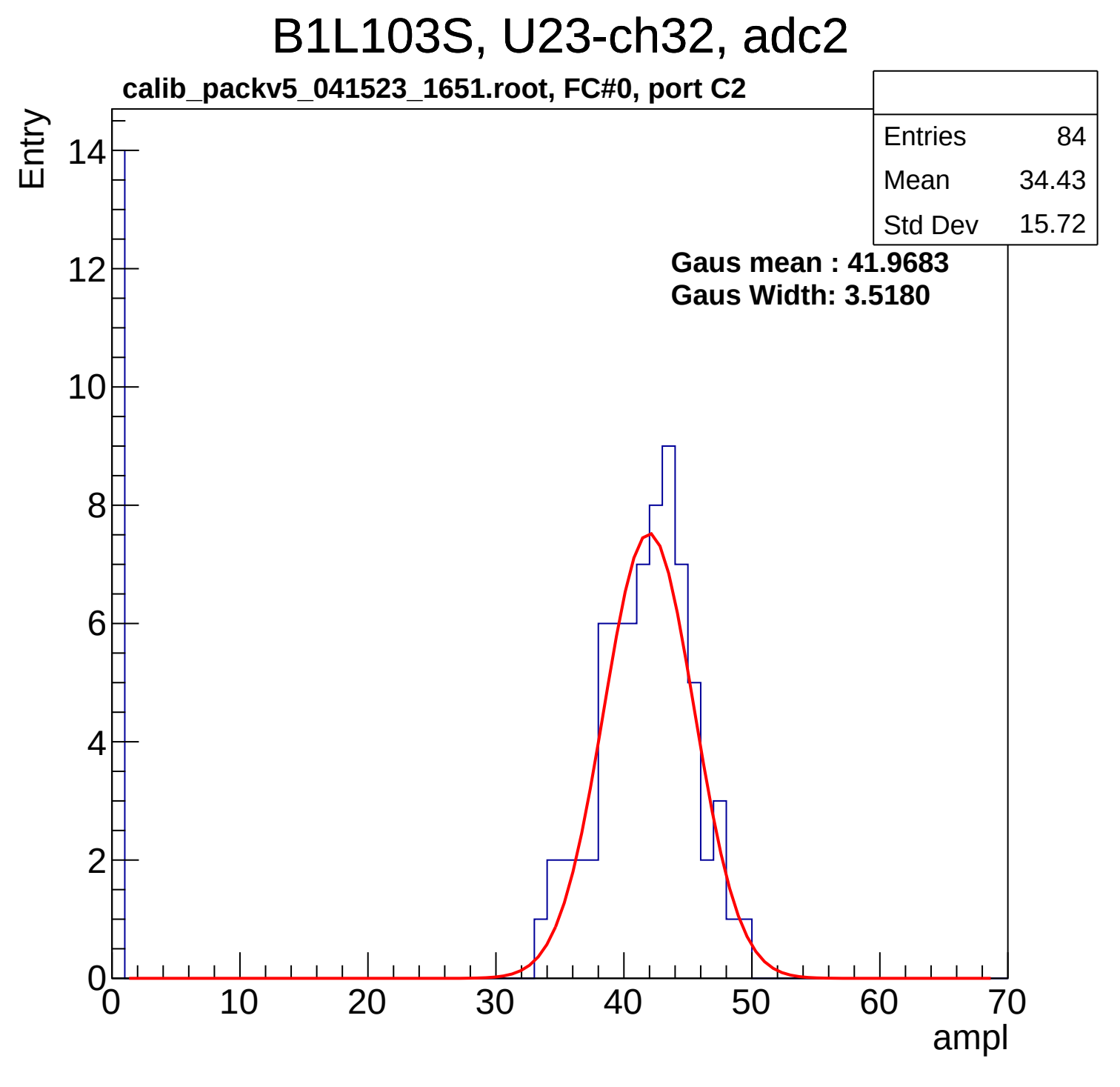

B1L103S, U23-ch31, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 36
Mean 58.5
Std Dev 10.11
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

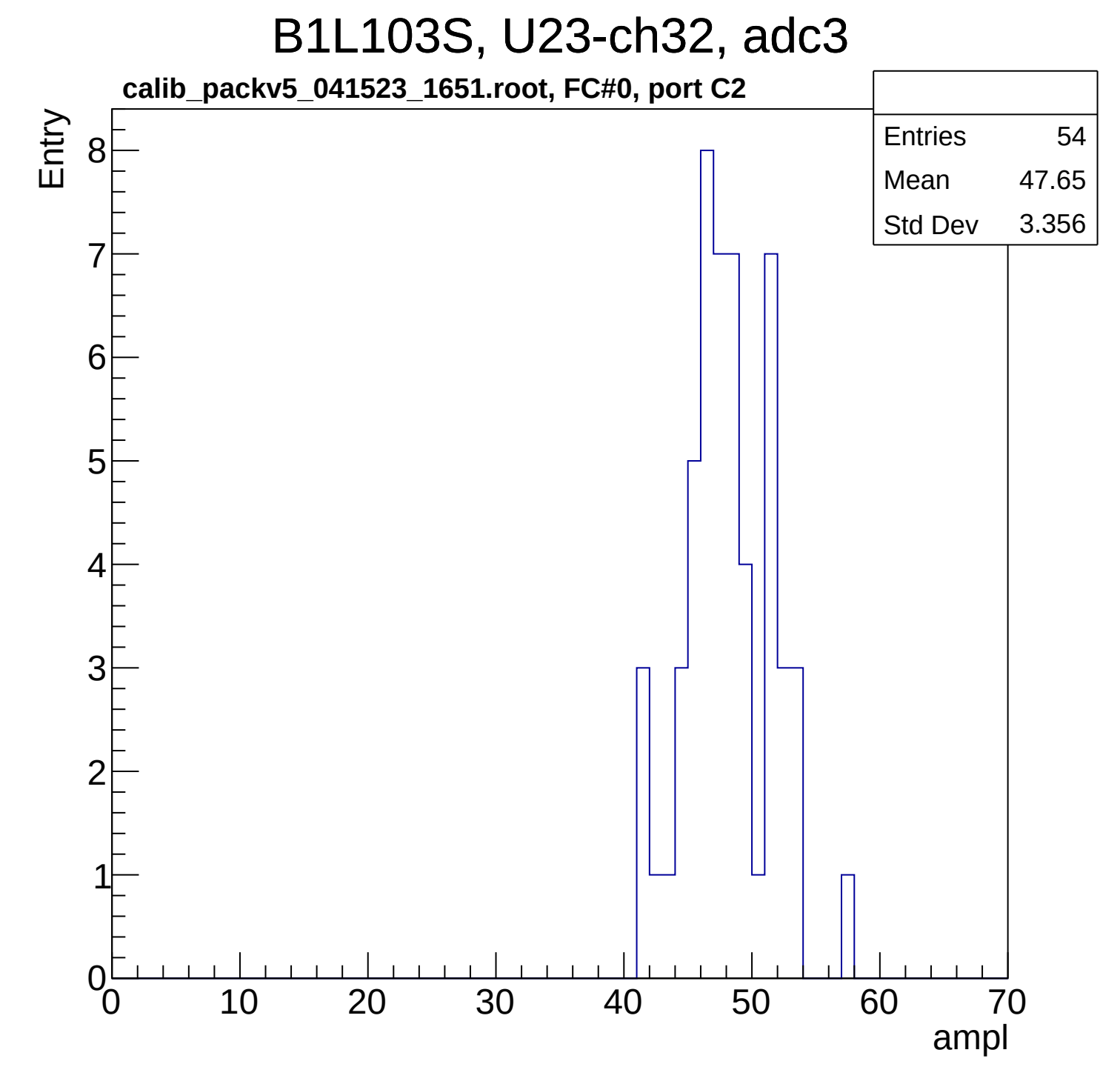

B1L103S, U23-ch31, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 12
Mean 62
Std Dev 1.291
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

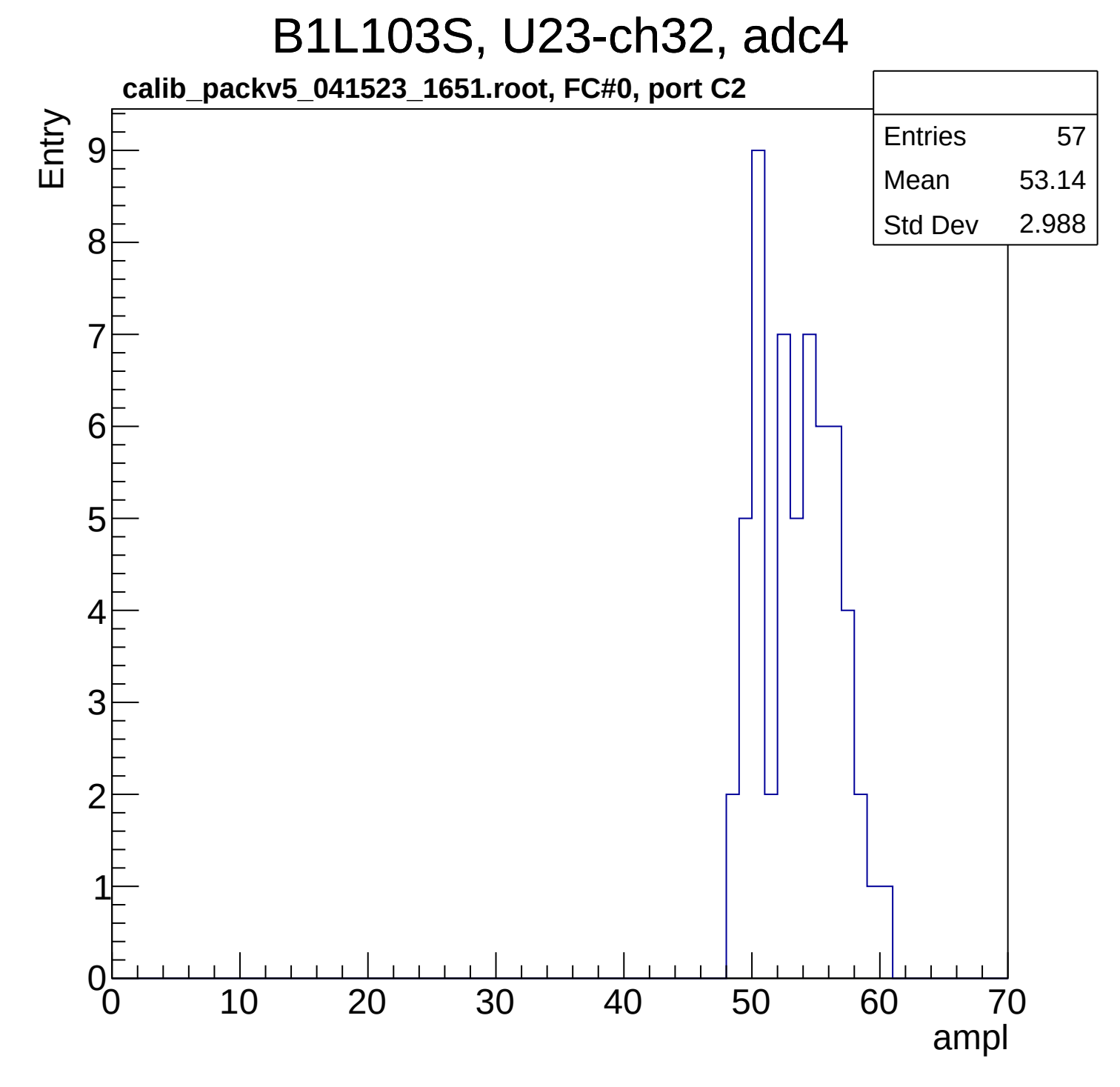

B1L103S, U23-ch31, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

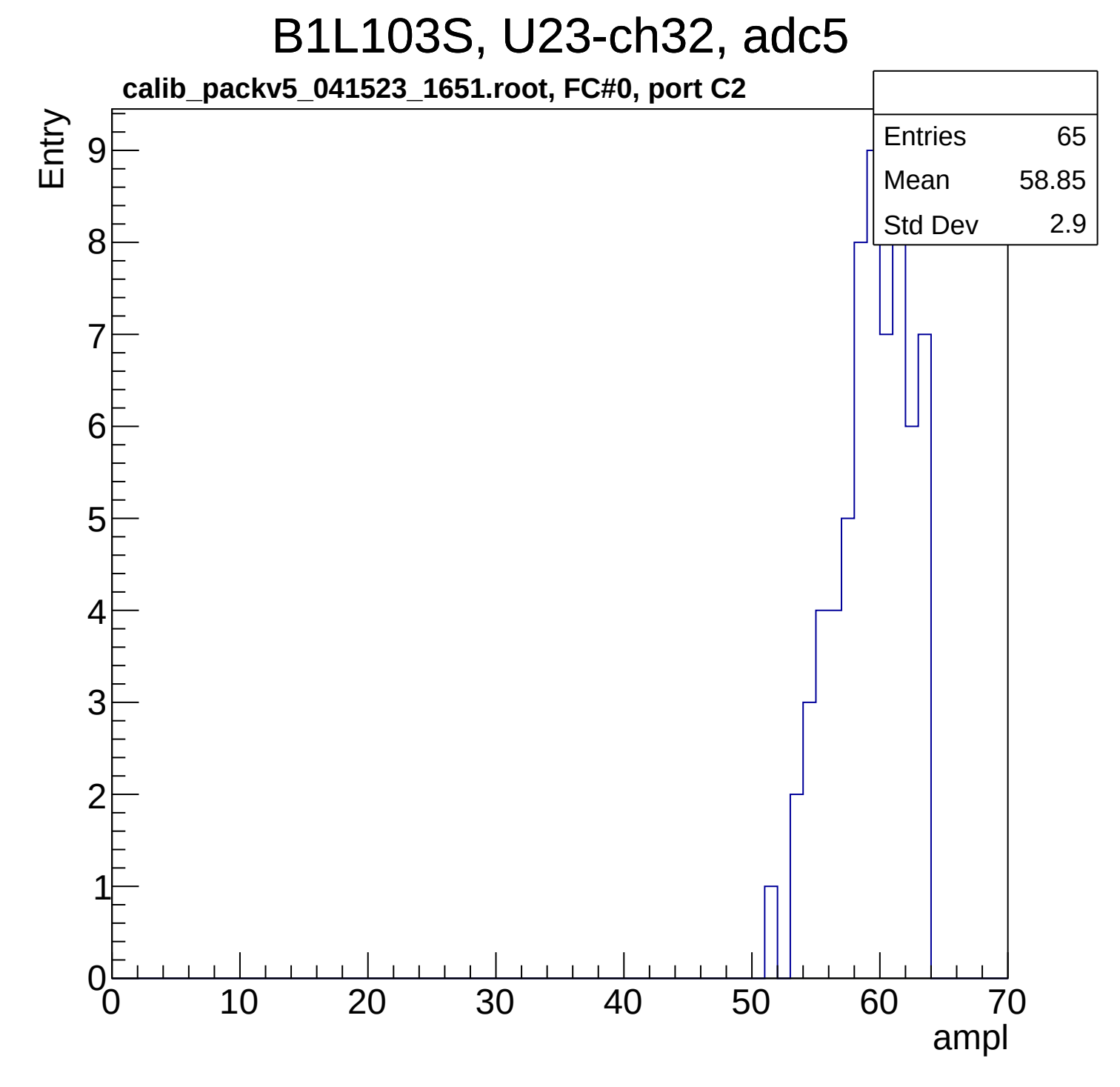

B1L103S, U23-ch32, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 21.7
Std Dev 11.5
Gaus mean : 27.9147
Gaus Width: 3.0257
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

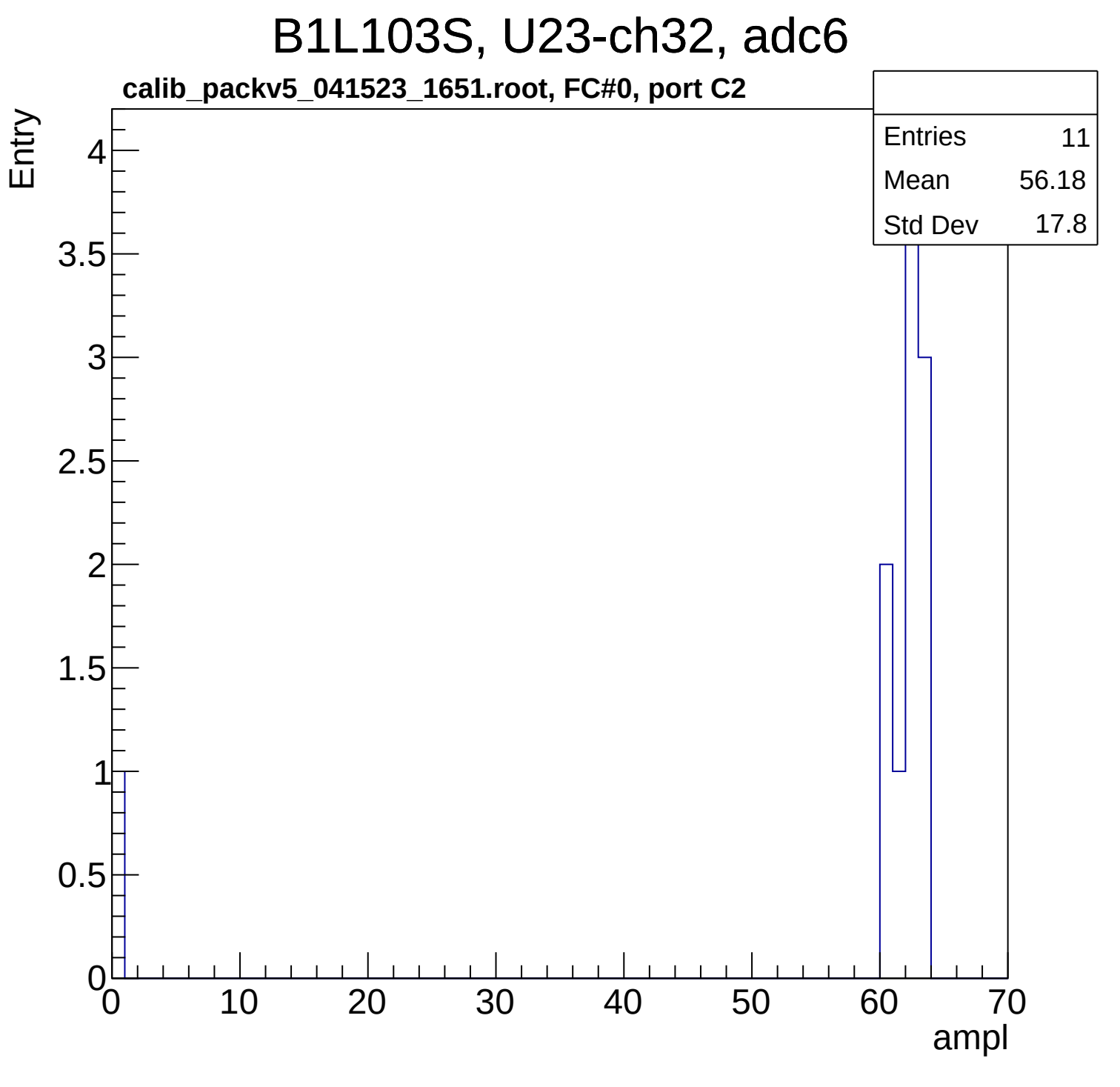

B1L103S, U23-ch32, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 33.47
Std Dev 6.365
Gaus mean : 34.9741
Gaus Width: 3.7825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

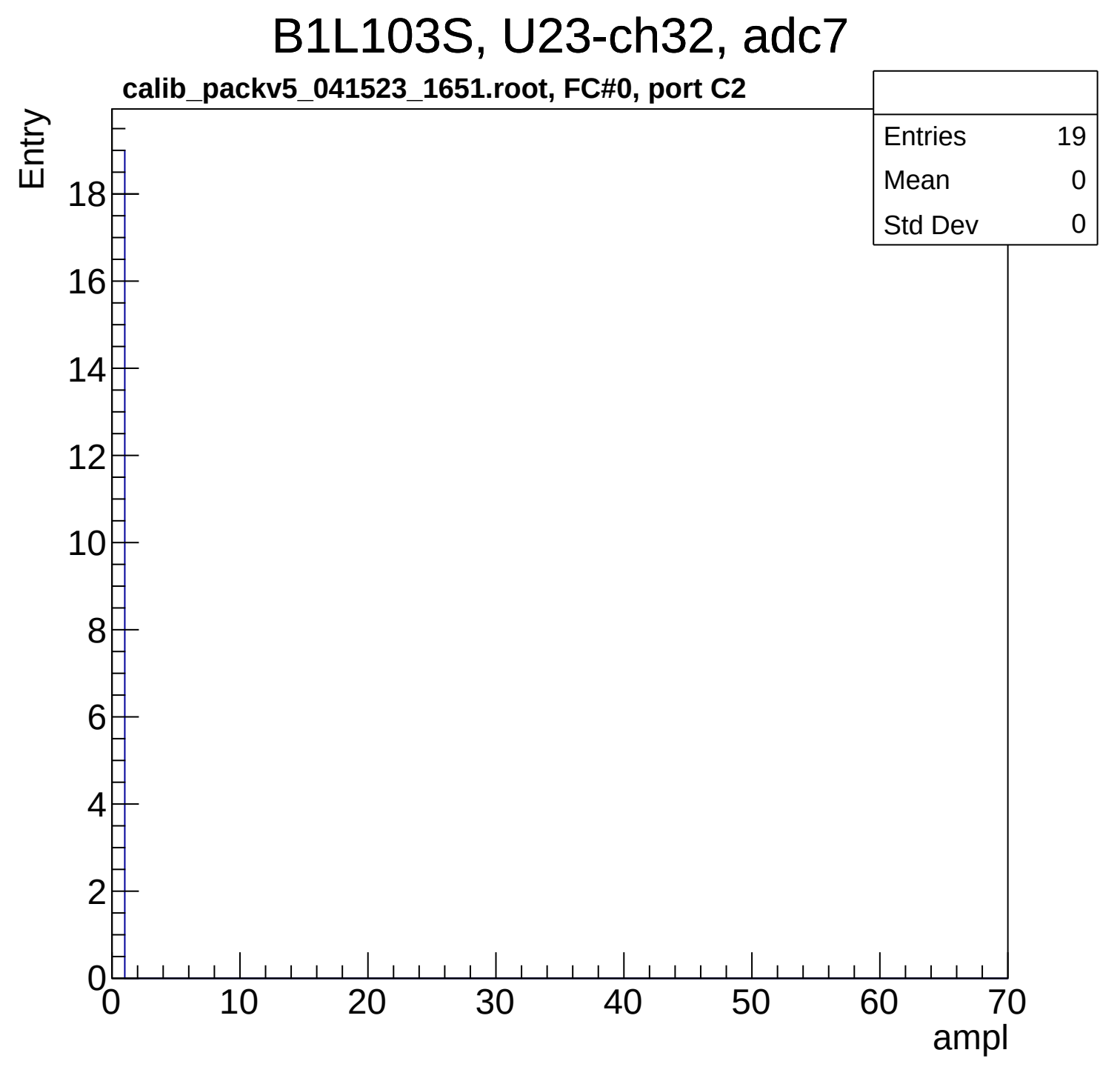

B1L103S, U23-ch32, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 84
Mean 34.43
Std Dev 15.72
Gaus mean : 41.9683
Gaus Width: 3.5180
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

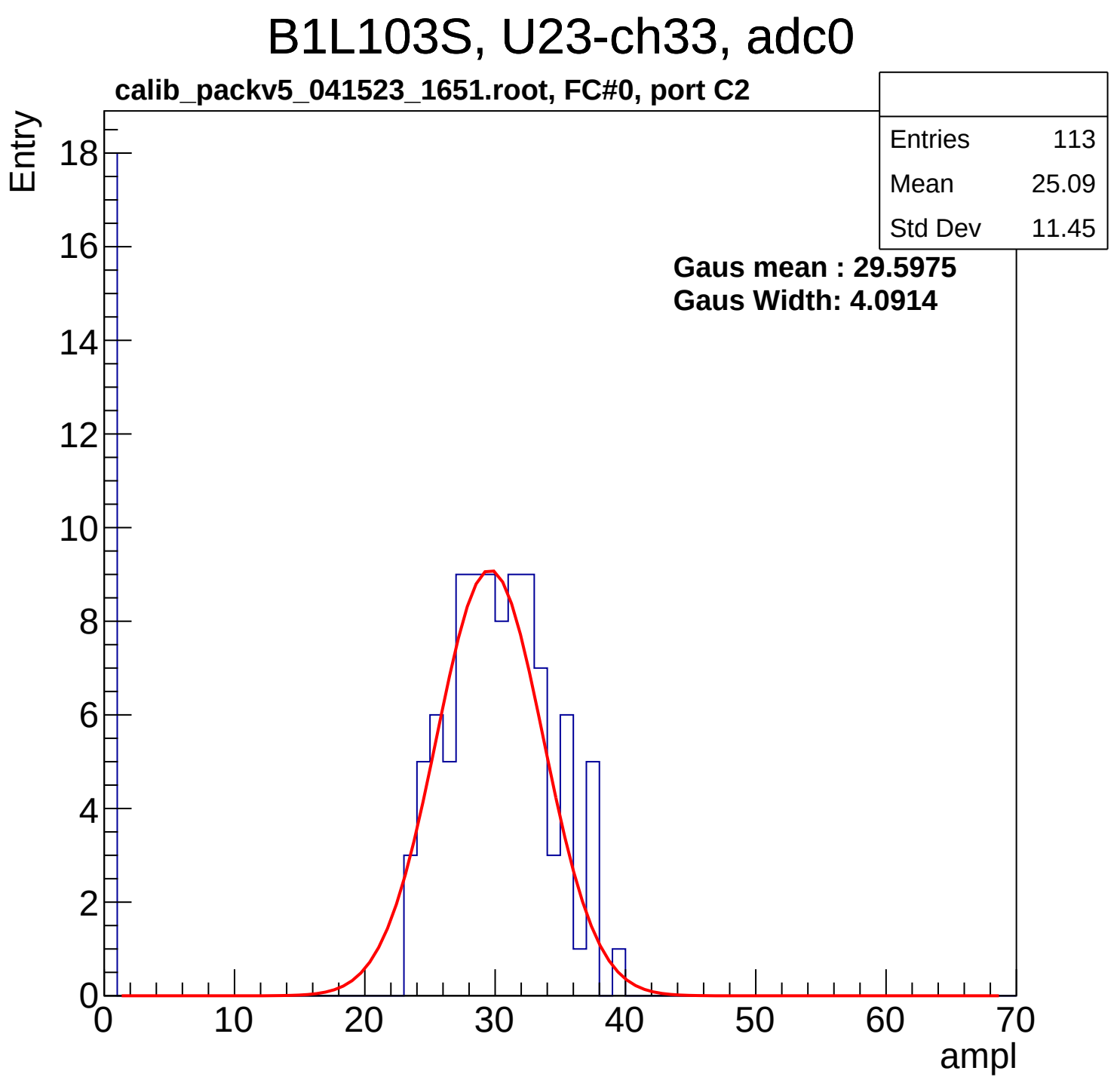

B1L103S, U23-ch32, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 47.65
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

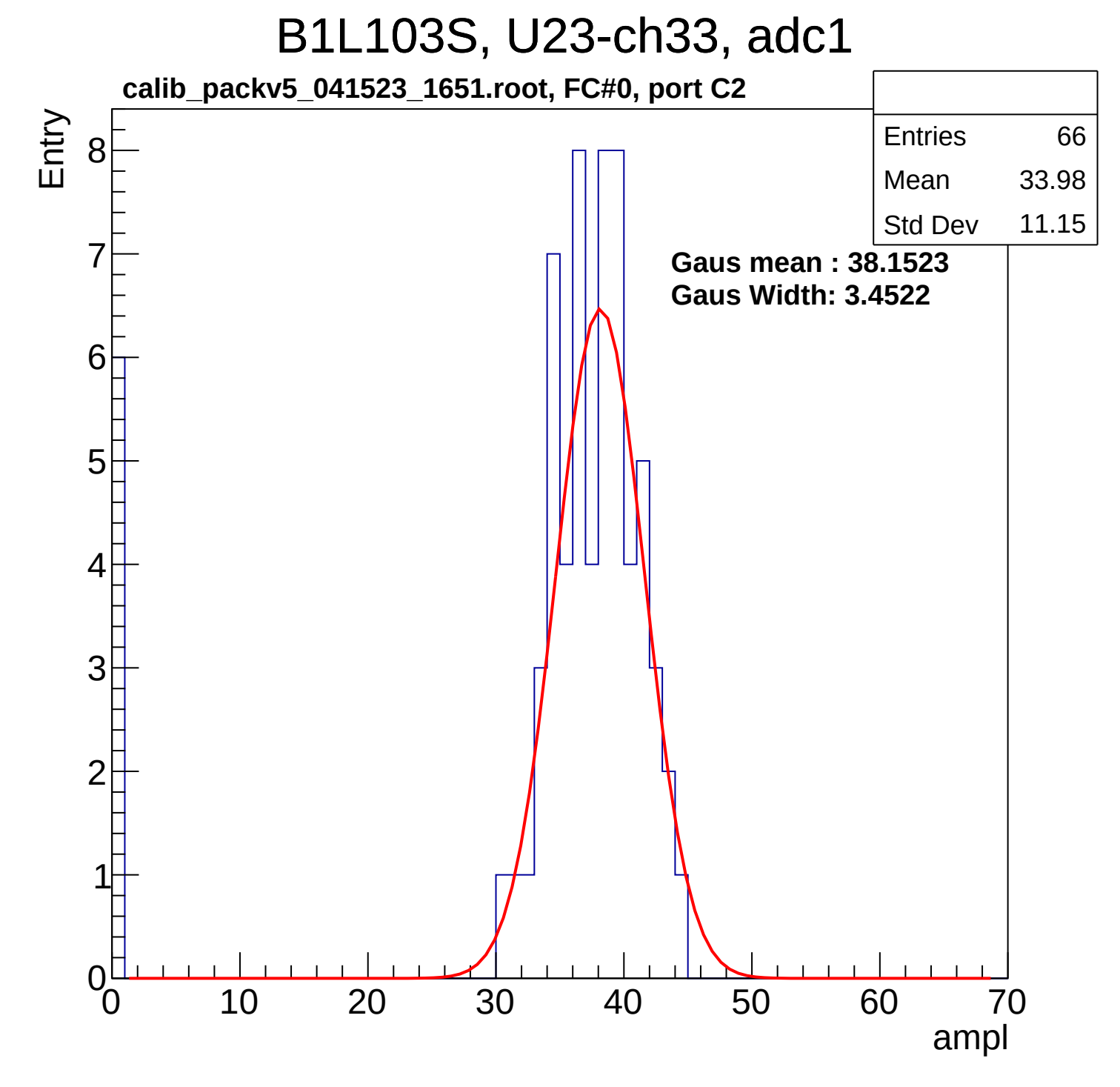

B1L103S, U23-ch32, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 53.14
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

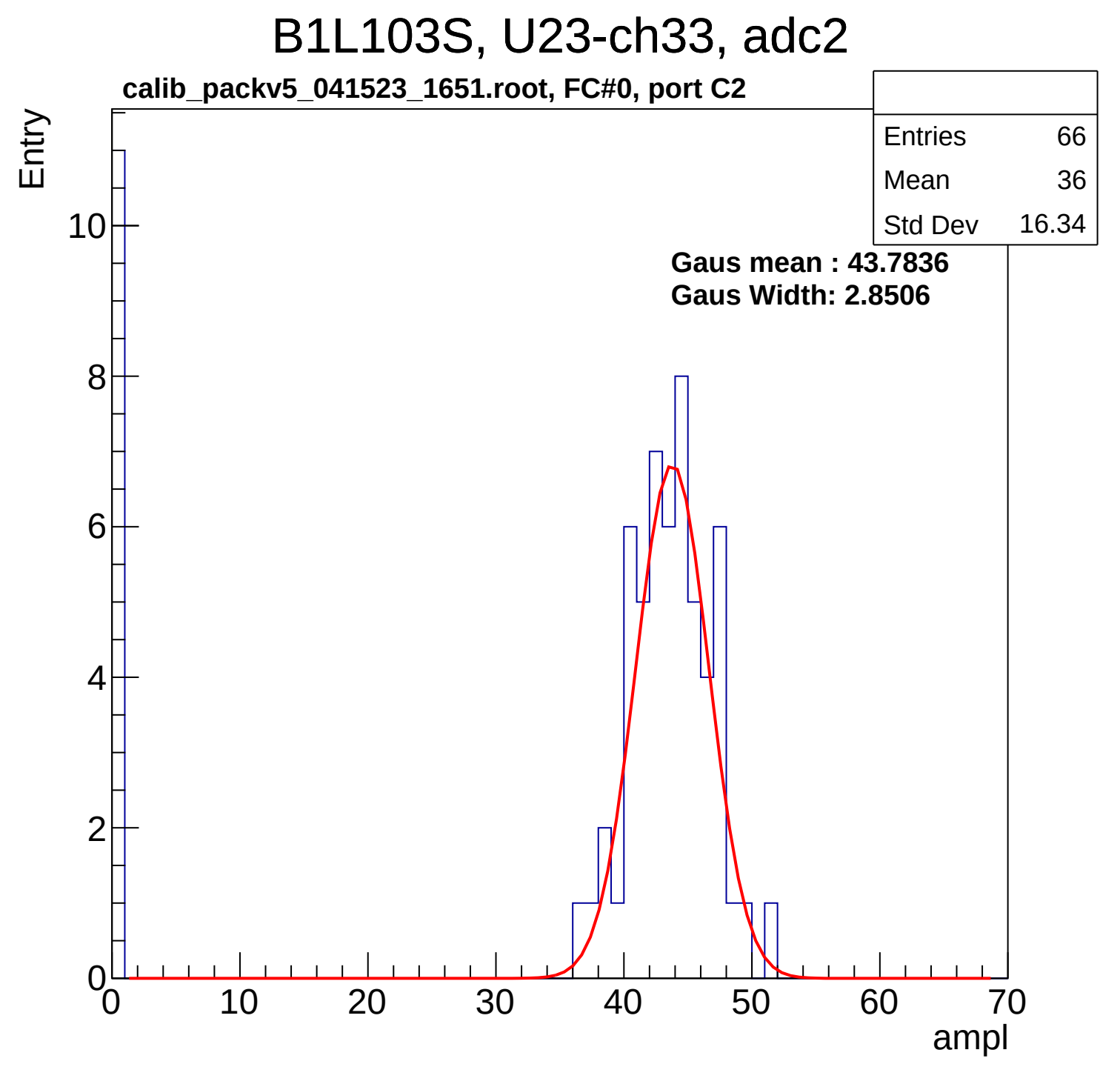

B1L103S, U23-ch32, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 58.85
Std Dev 2.9
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

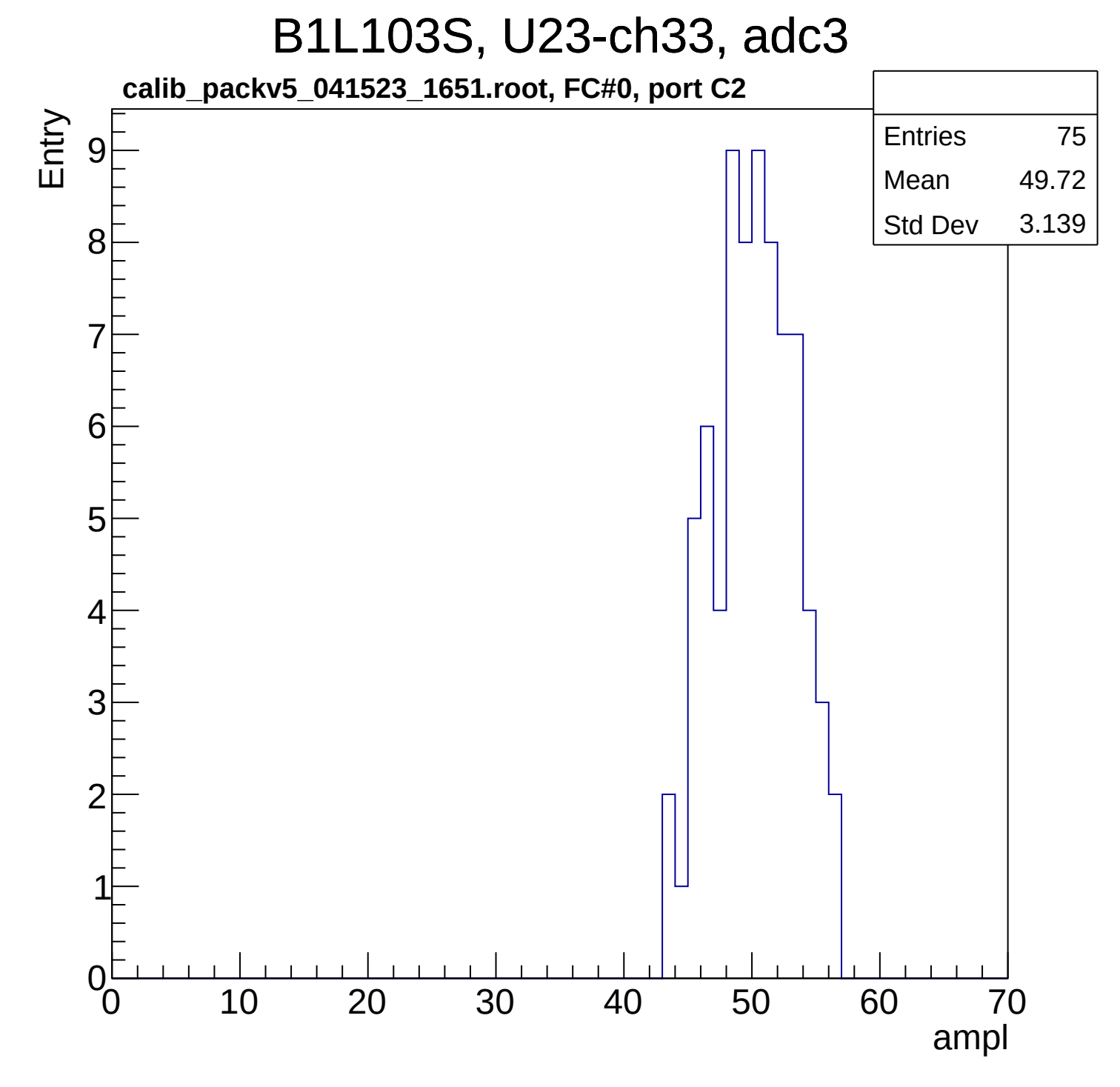

B1L103S, U23-ch32, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 56.18
Std Dev 17.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

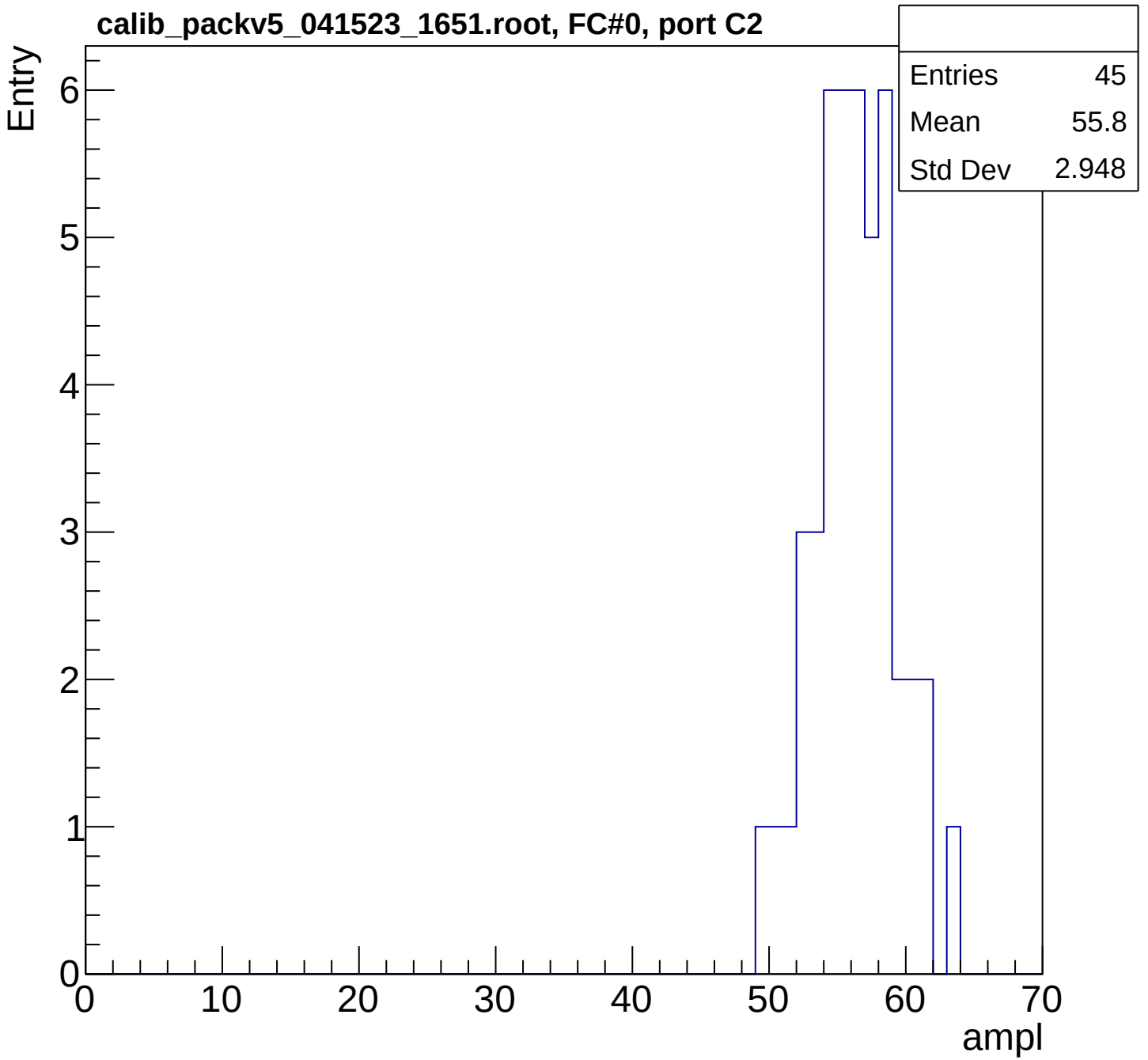

B1L103S, U23-ch32, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

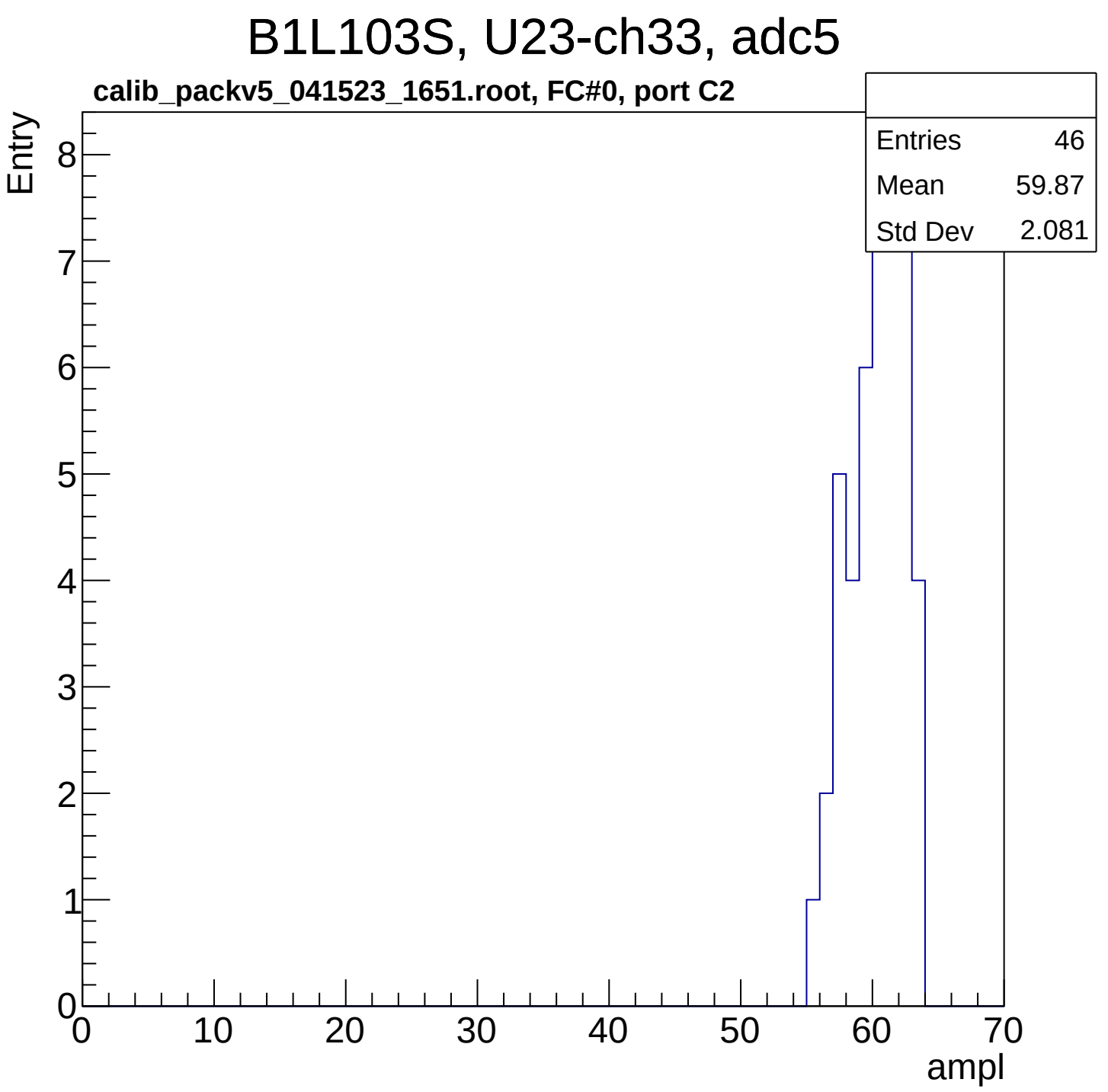

B1L103S, U23-ch33, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 113
Mean 25.09
Std Dev 11.45
Gaus mean : 29.5975
Gaus Width: 4.0914
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

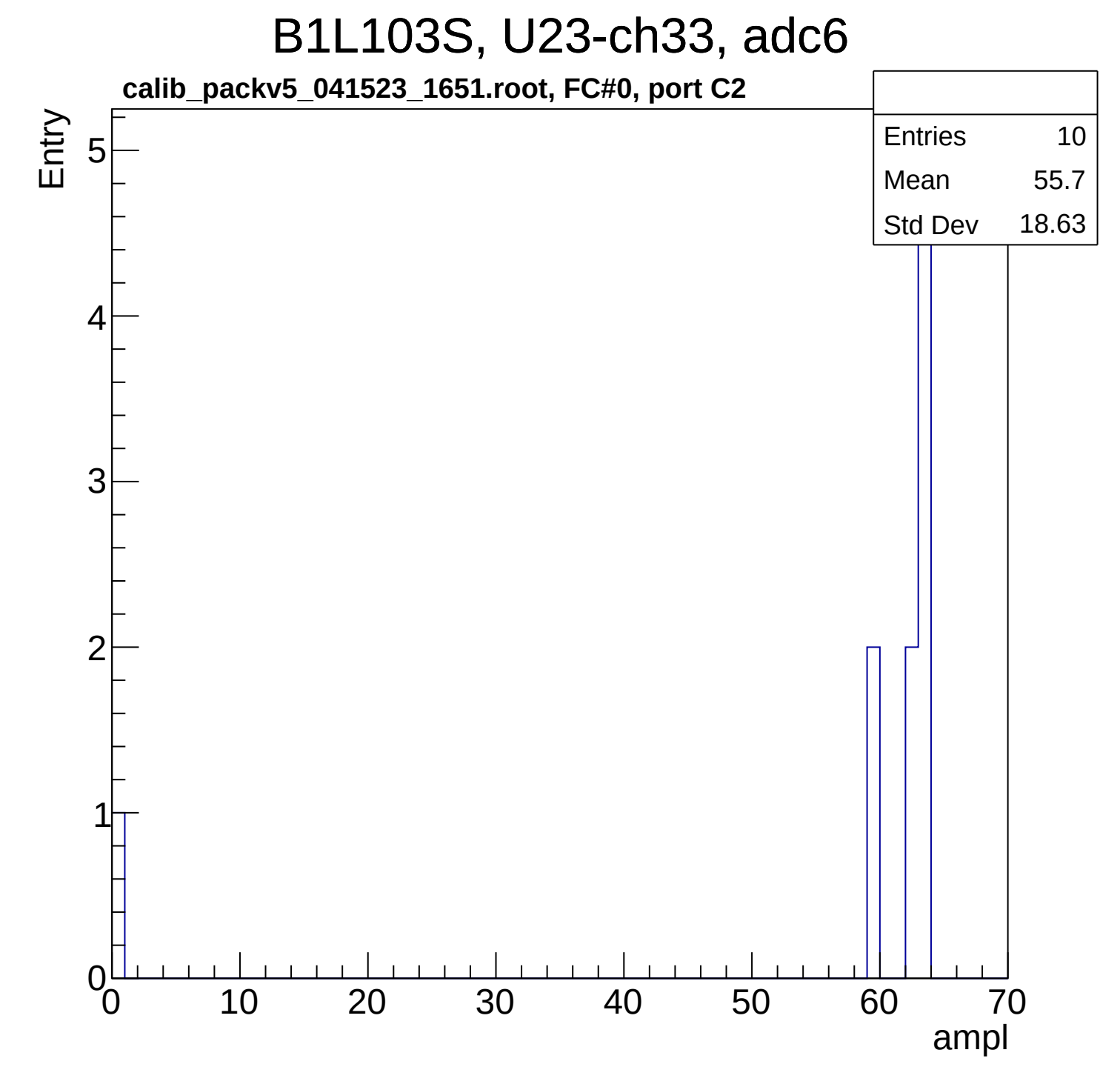

B1L103S, U23-ch33, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 33.98
Std Dev 11.15
Gaus mean : 38.1523
Gaus Width: 3.4522
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

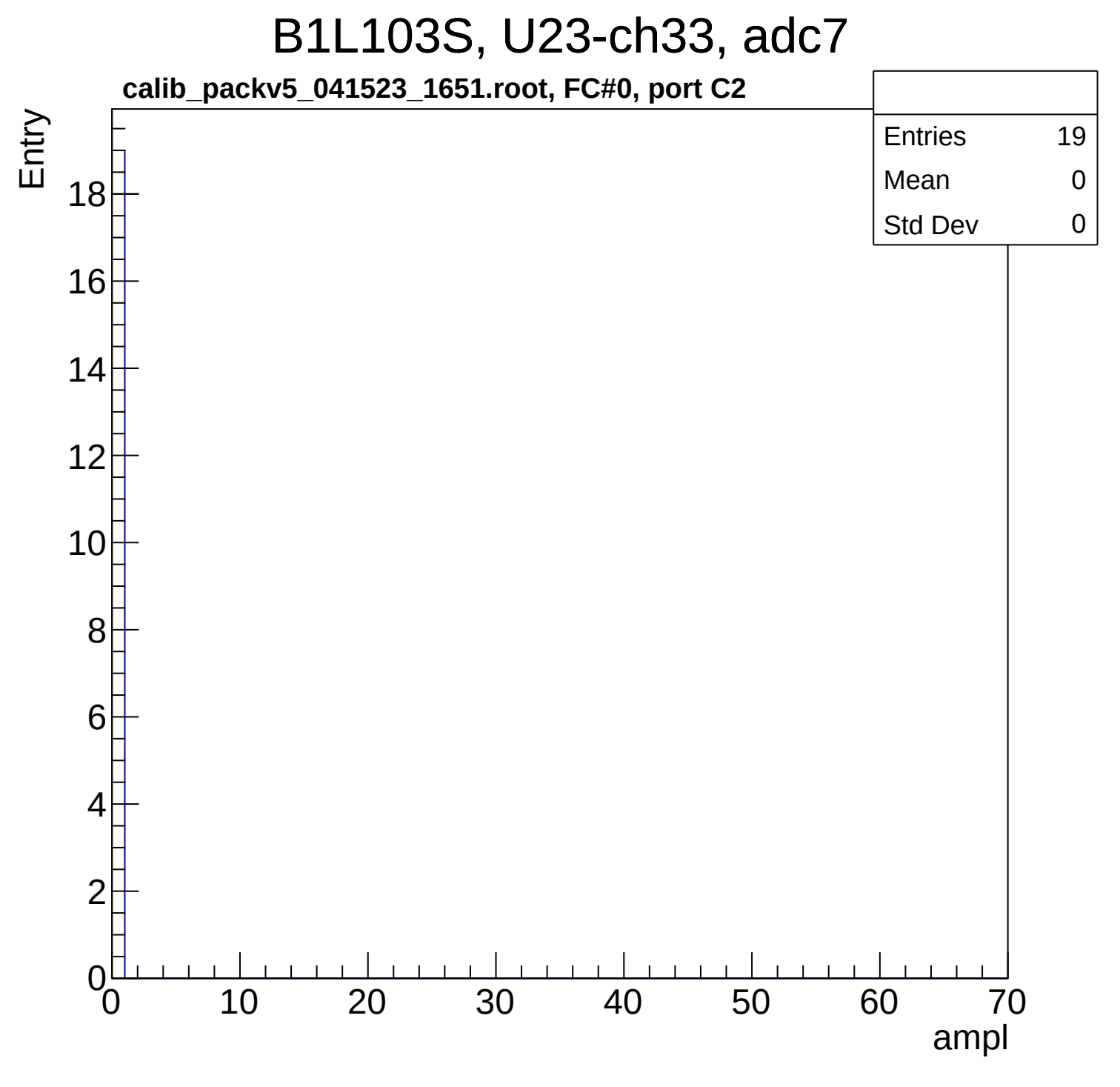

B1L103S, U23-ch33, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 36
Std Dev 16.34
Gaus mean : 43.7836
Gaus Width: 2.8506
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

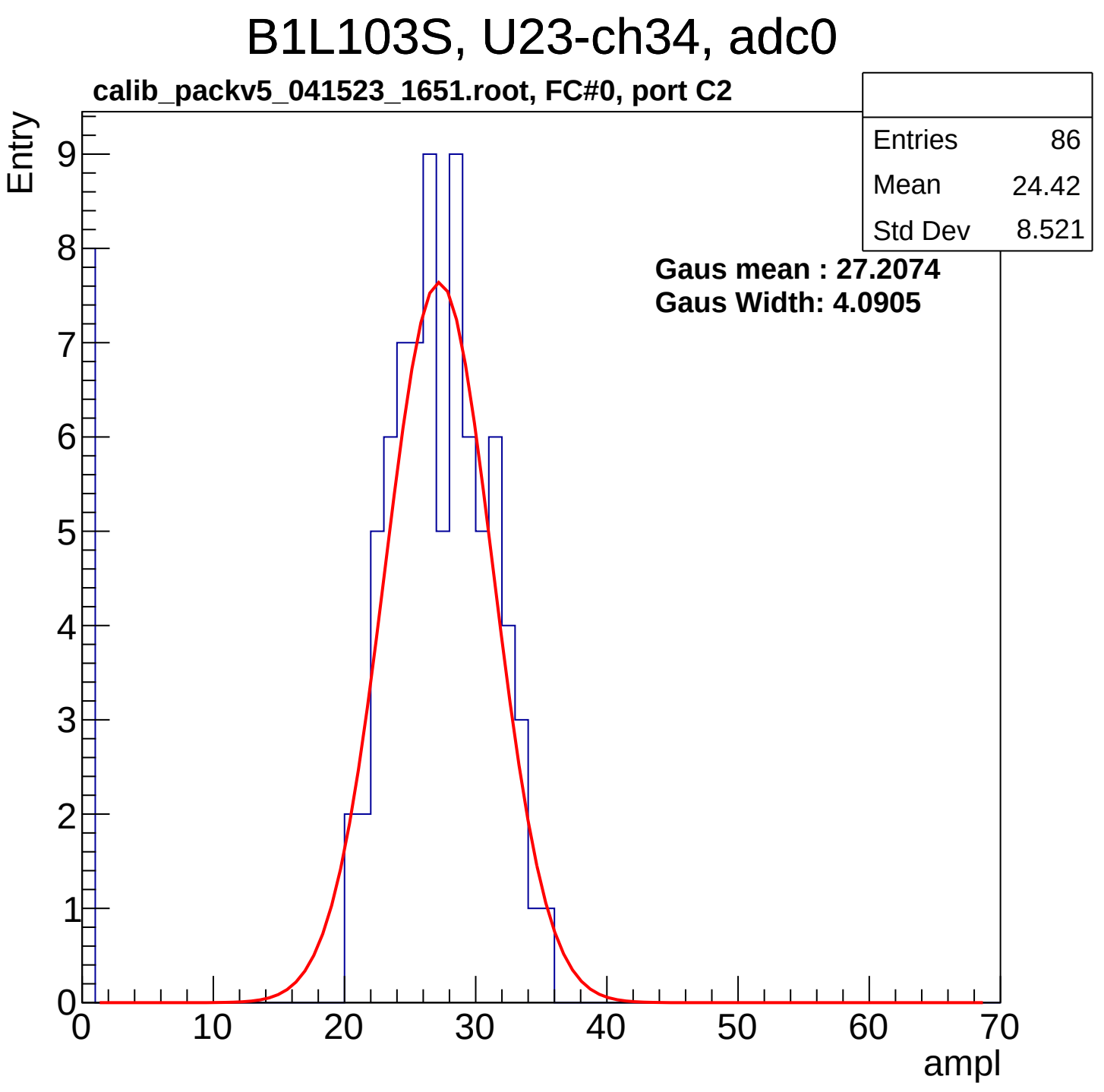

B1L103S, U23-ch33, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 49.72
Std Dev 3.139
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

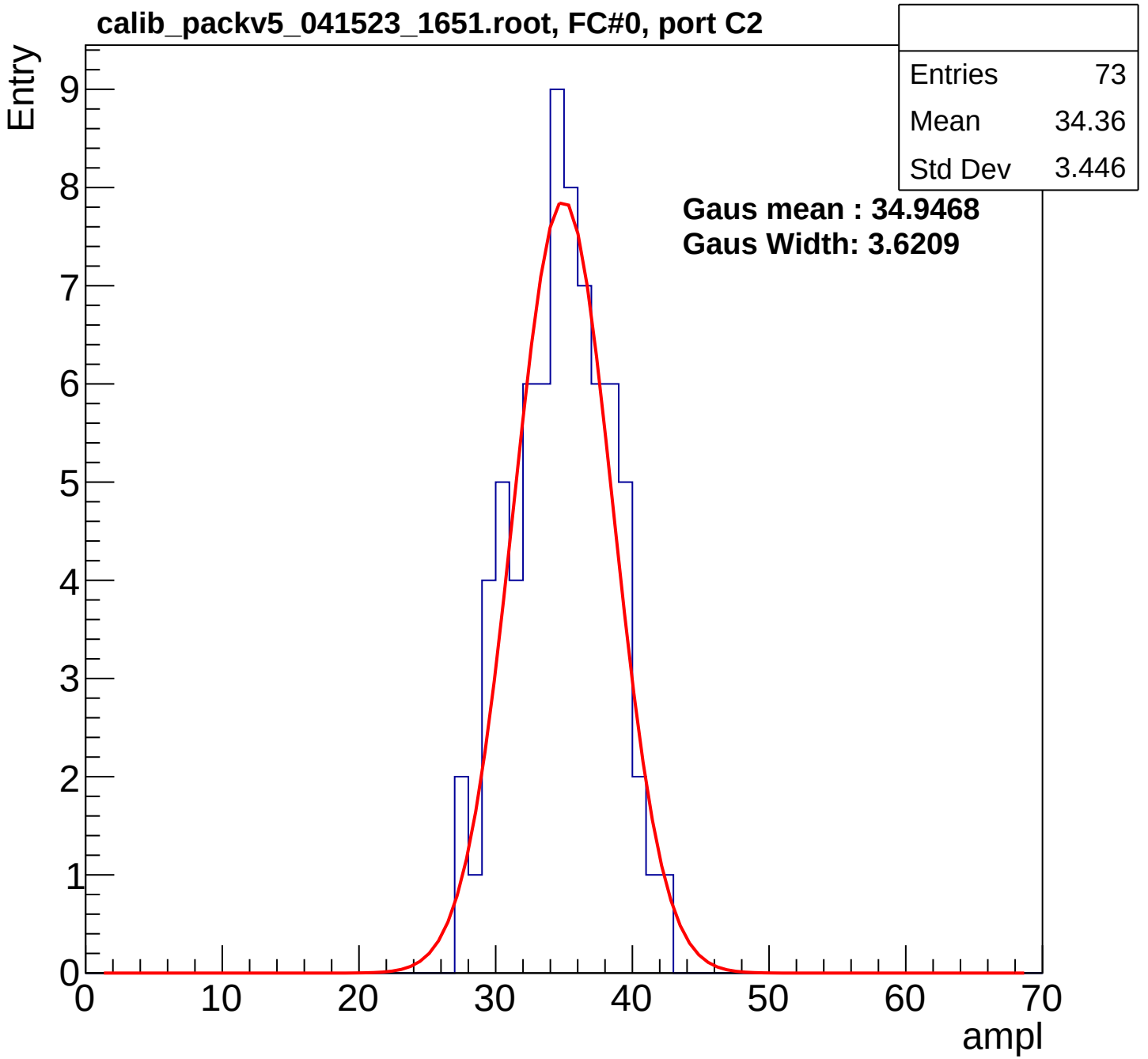

B1L103S, U23-ch33, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 45
Mean 55.8
Std Dev 2.948
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

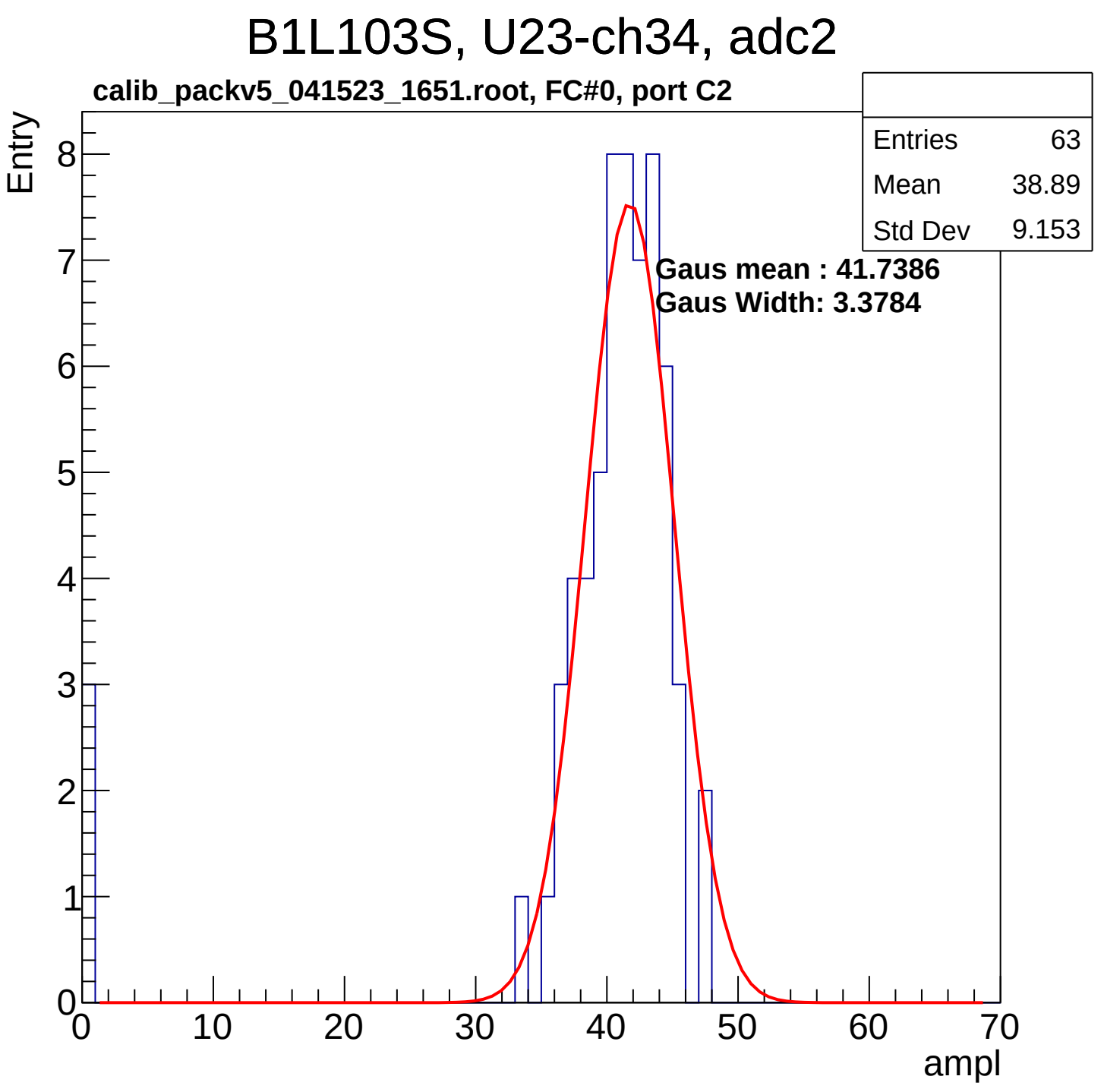

B1L103S, U23-ch33, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 46
Mean 59.87
Std Dev 2.081
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

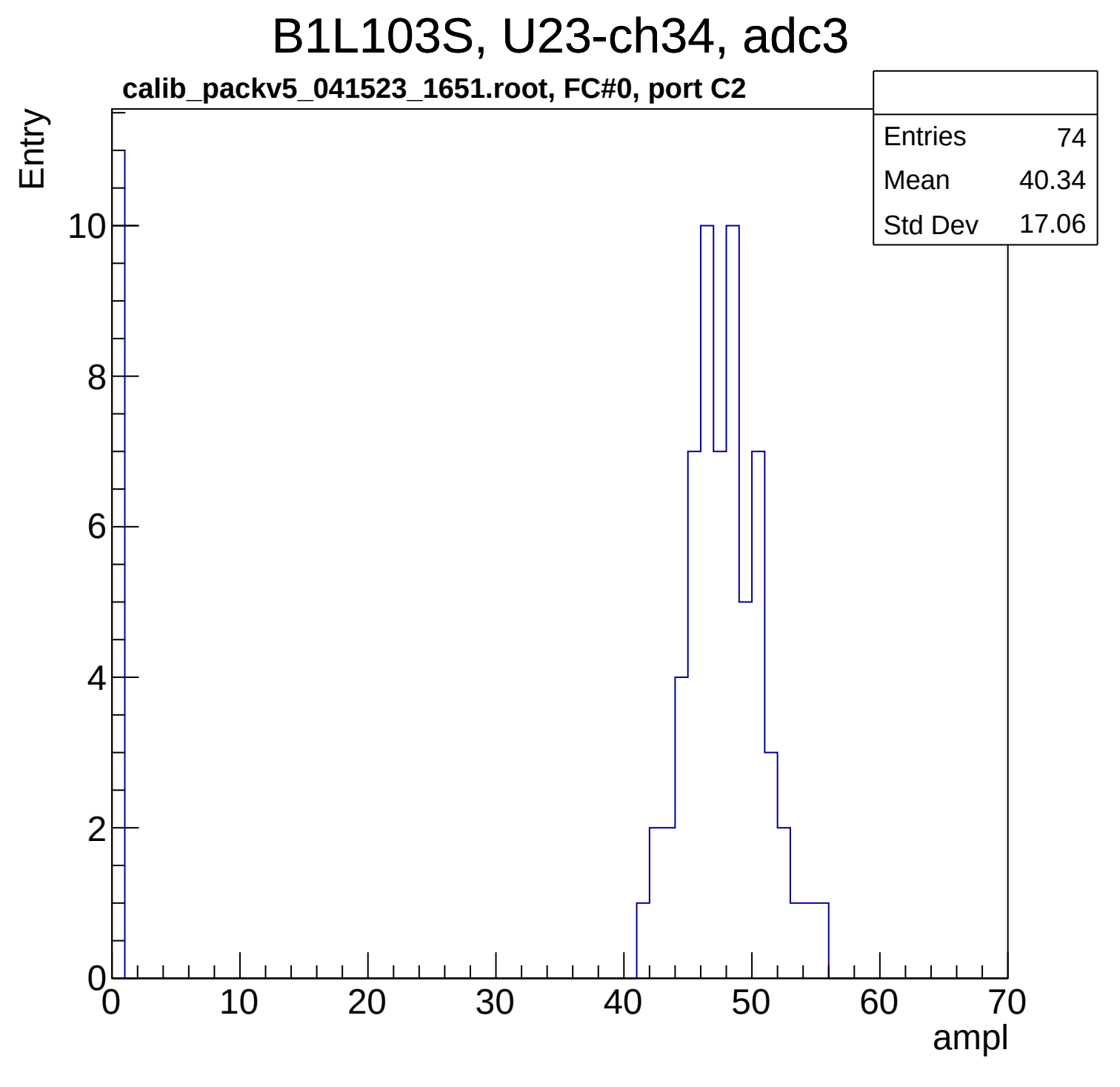

B1L103S, U23-ch33, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 55.7
Std Dev 18.63
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

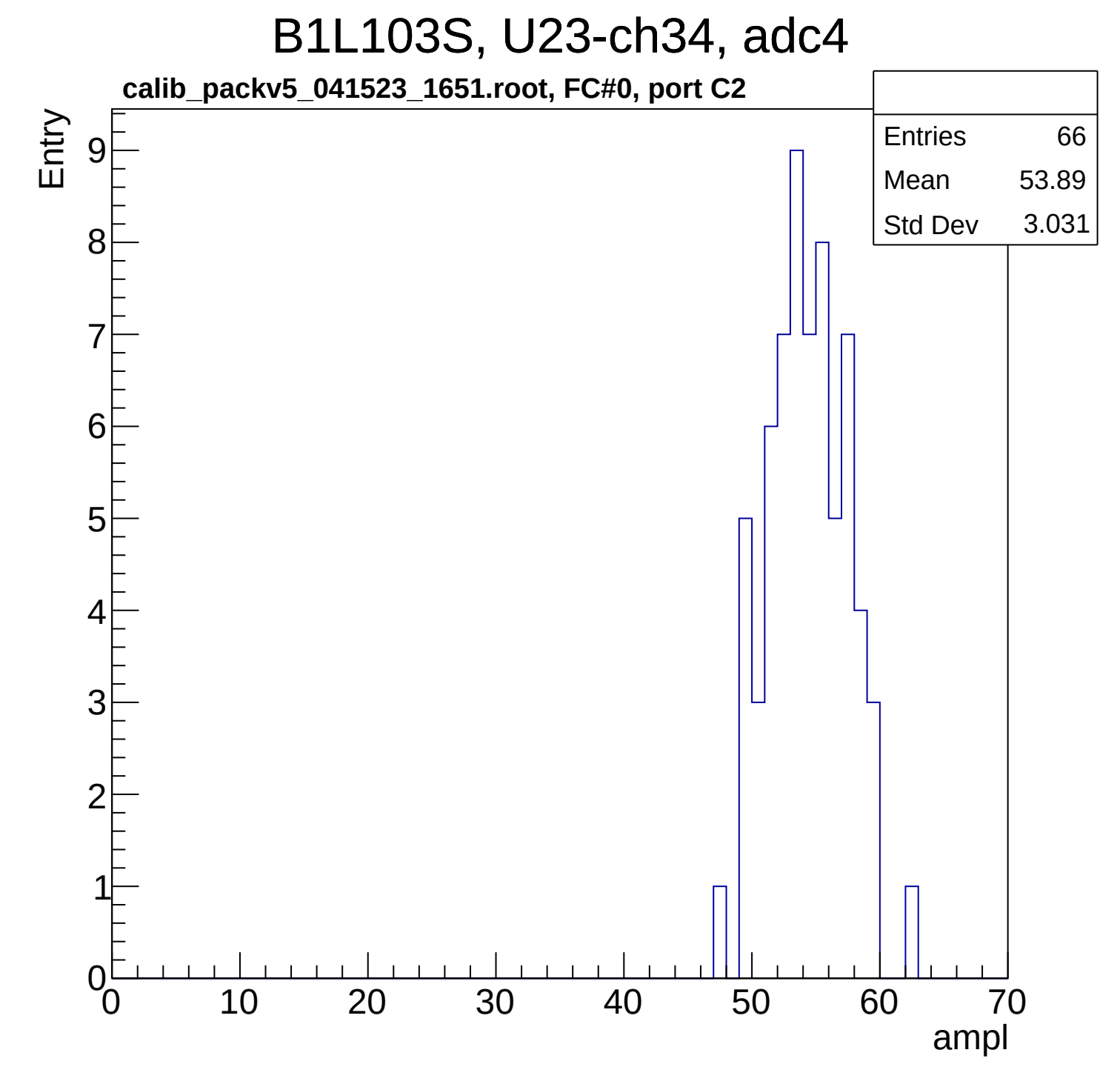

B1L103S, U23-ch33, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

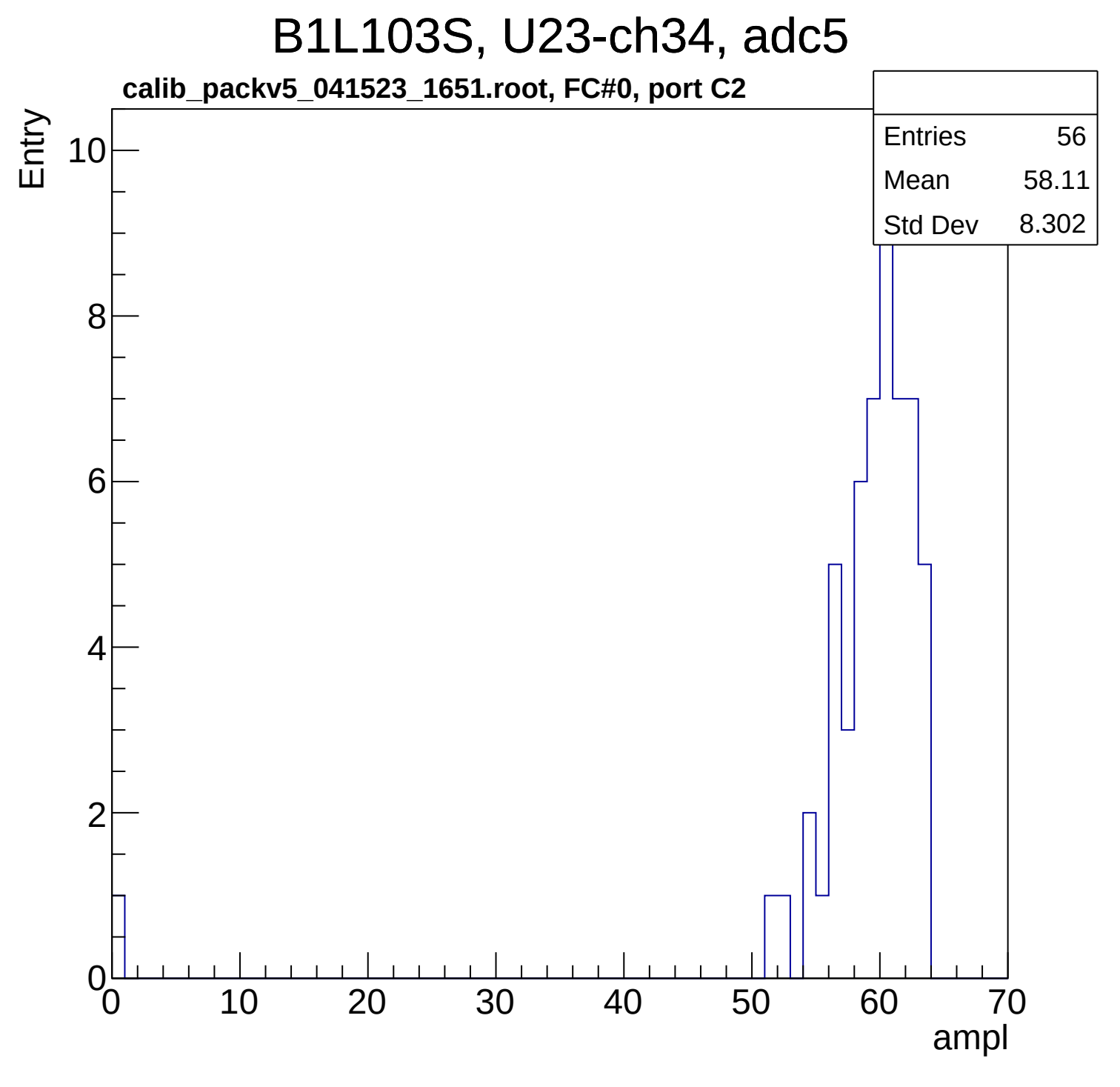

B1L103S, U23-ch34, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 24.42
Std Dev 8.521
Gaus mean : 27.2074
Gaus Width: 4.0905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

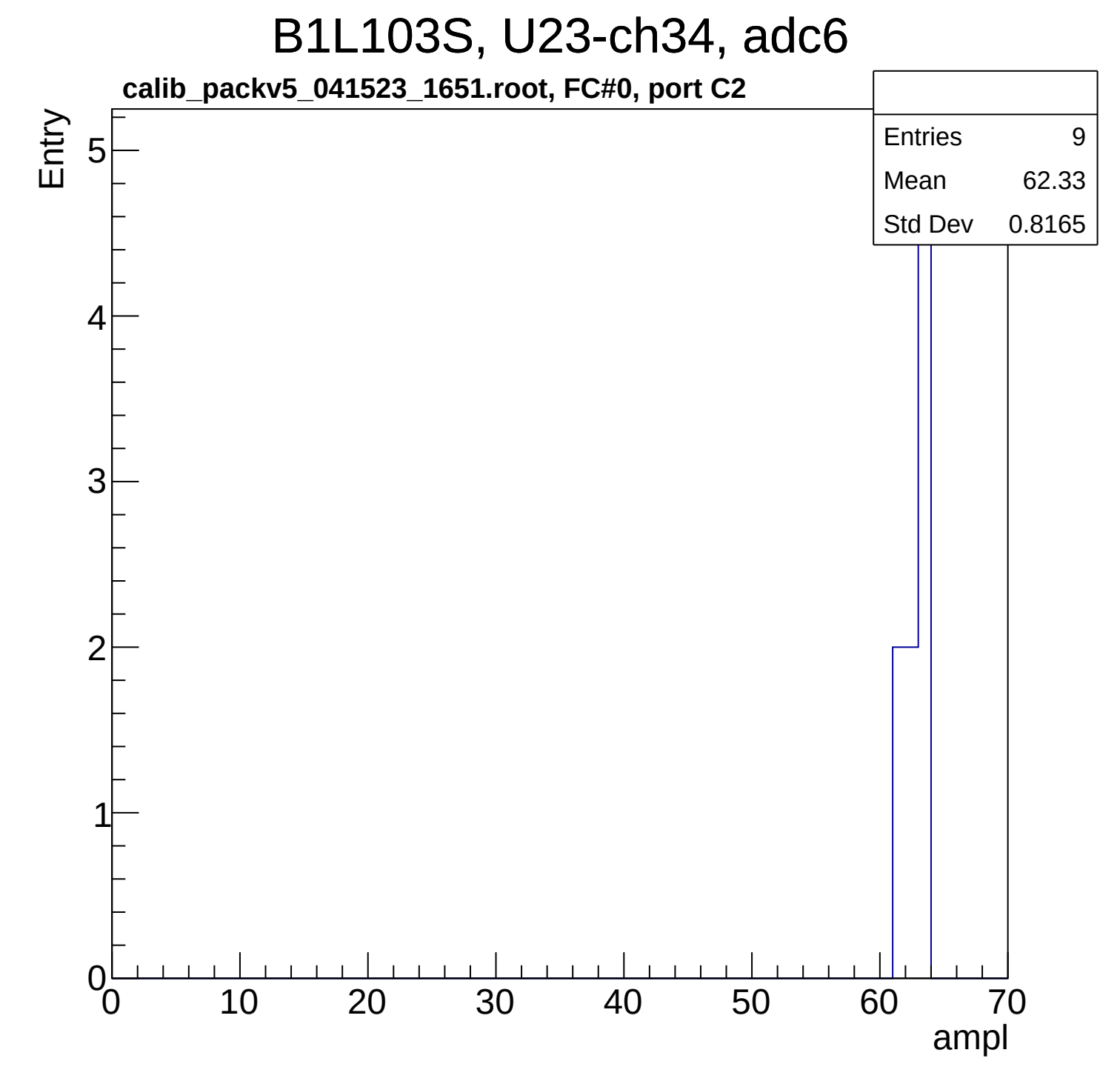

B1L103S, U23-ch34, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 34.36
Std Dev 3.446
Gaus mean : 34.9468
Gaus Width: 3.6209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

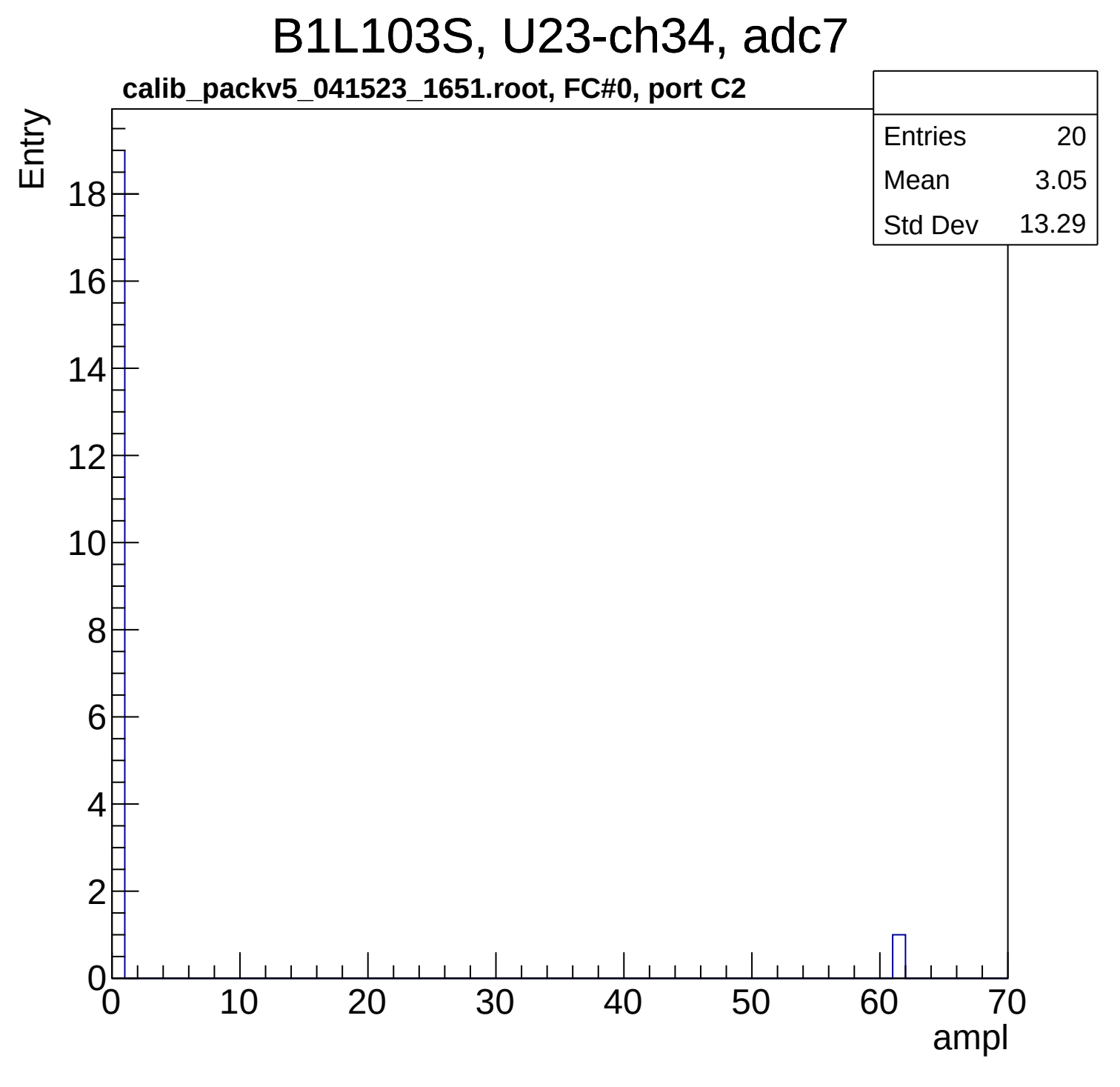

B1L103S, U23-ch34, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 38.89
Std Dev 9.153
Gaus mean : 41.7386
Gaus Width: 3.3784
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

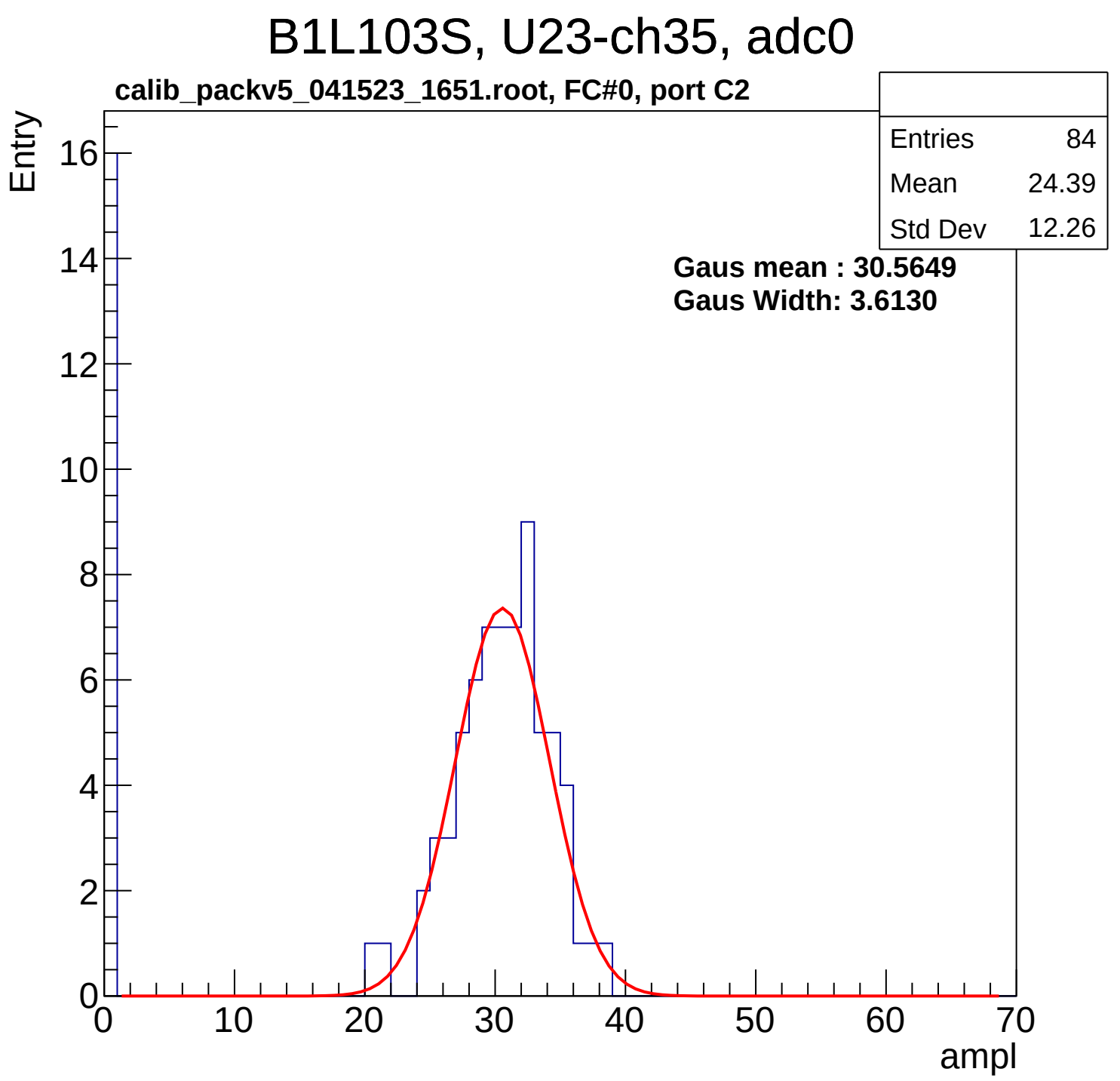

B1L103S, U23-ch34, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 40.34
Std Dev 17.06
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

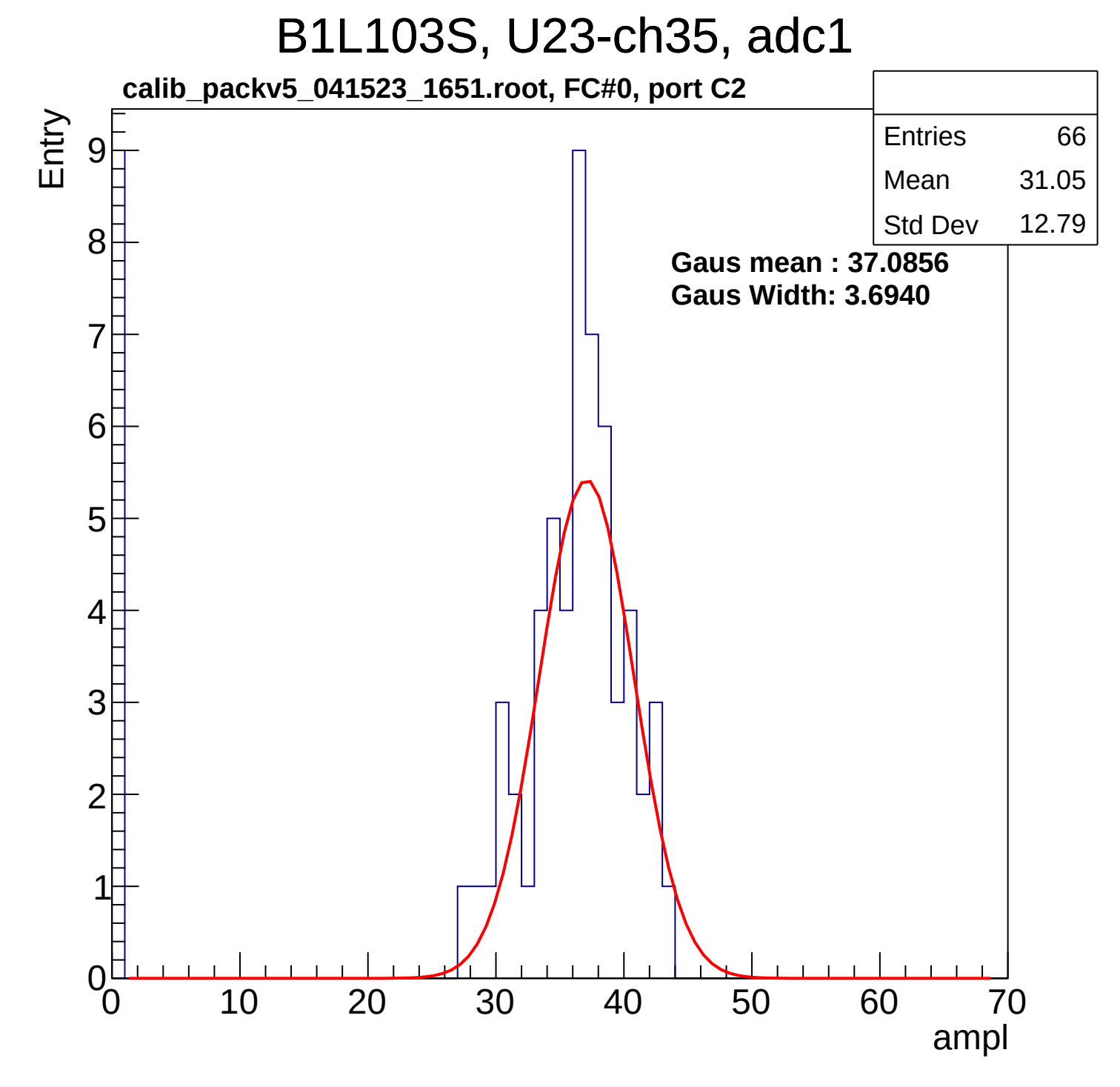

B1L103S, U23-ch34, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 53.89
Std Dev 3.031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

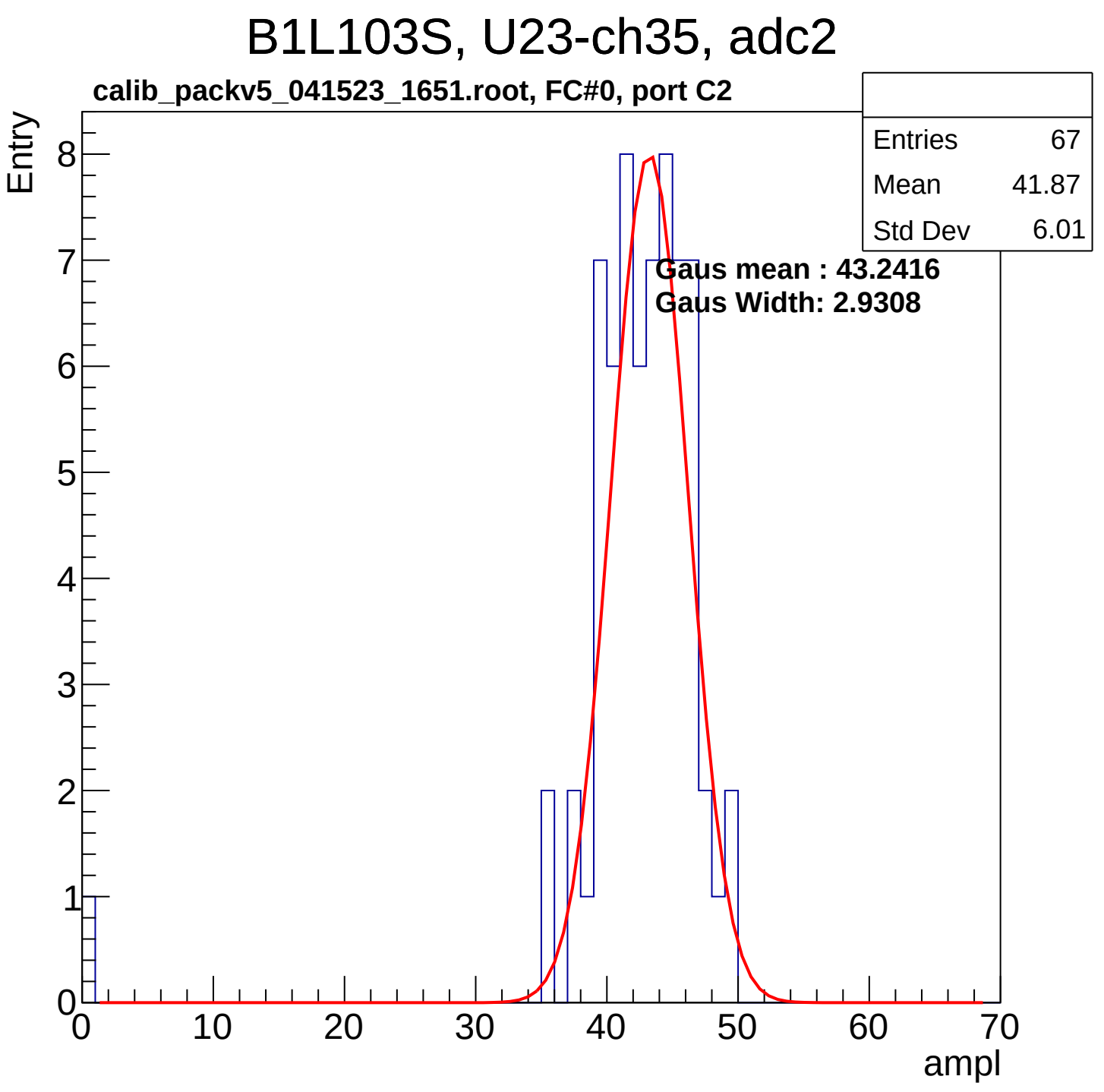

B1L103S, U23-ch34, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 58.11
Std Dev 8.302
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

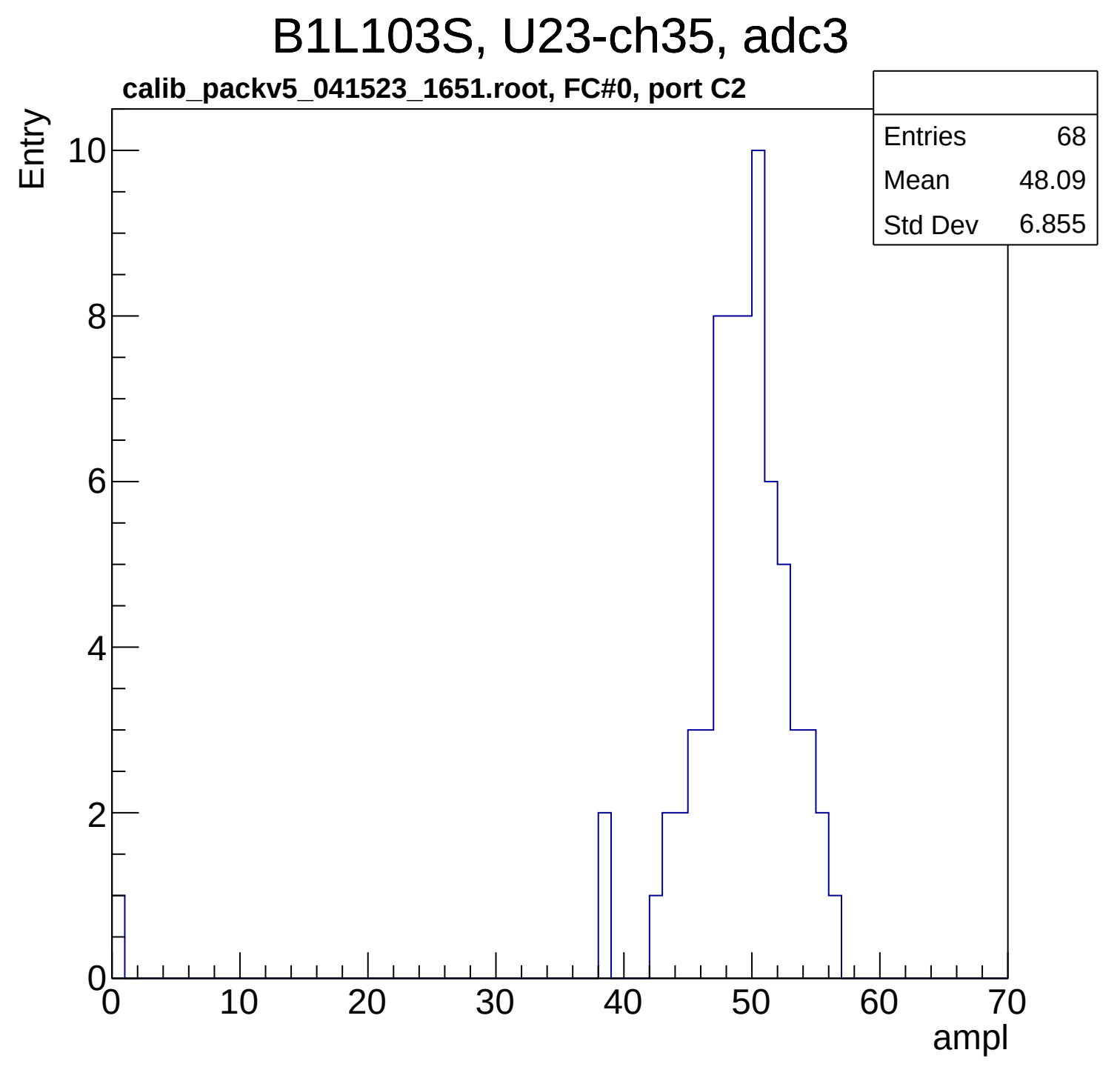

B1L103S, U23-ch34, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 62.33
Std Dev 0.8165
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

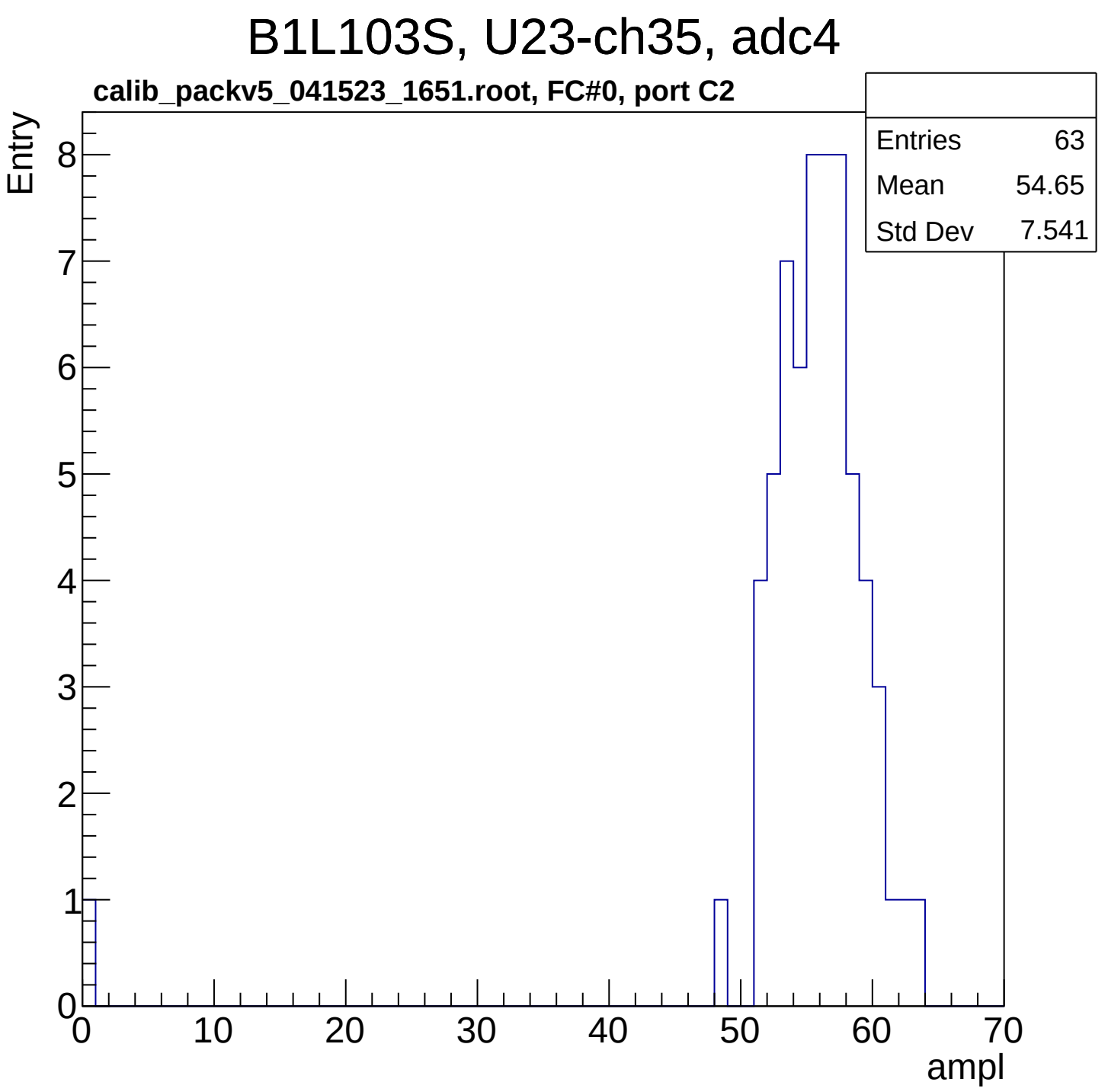

B1L103S, U23-ch34, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.05
Std Dev 13.29
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

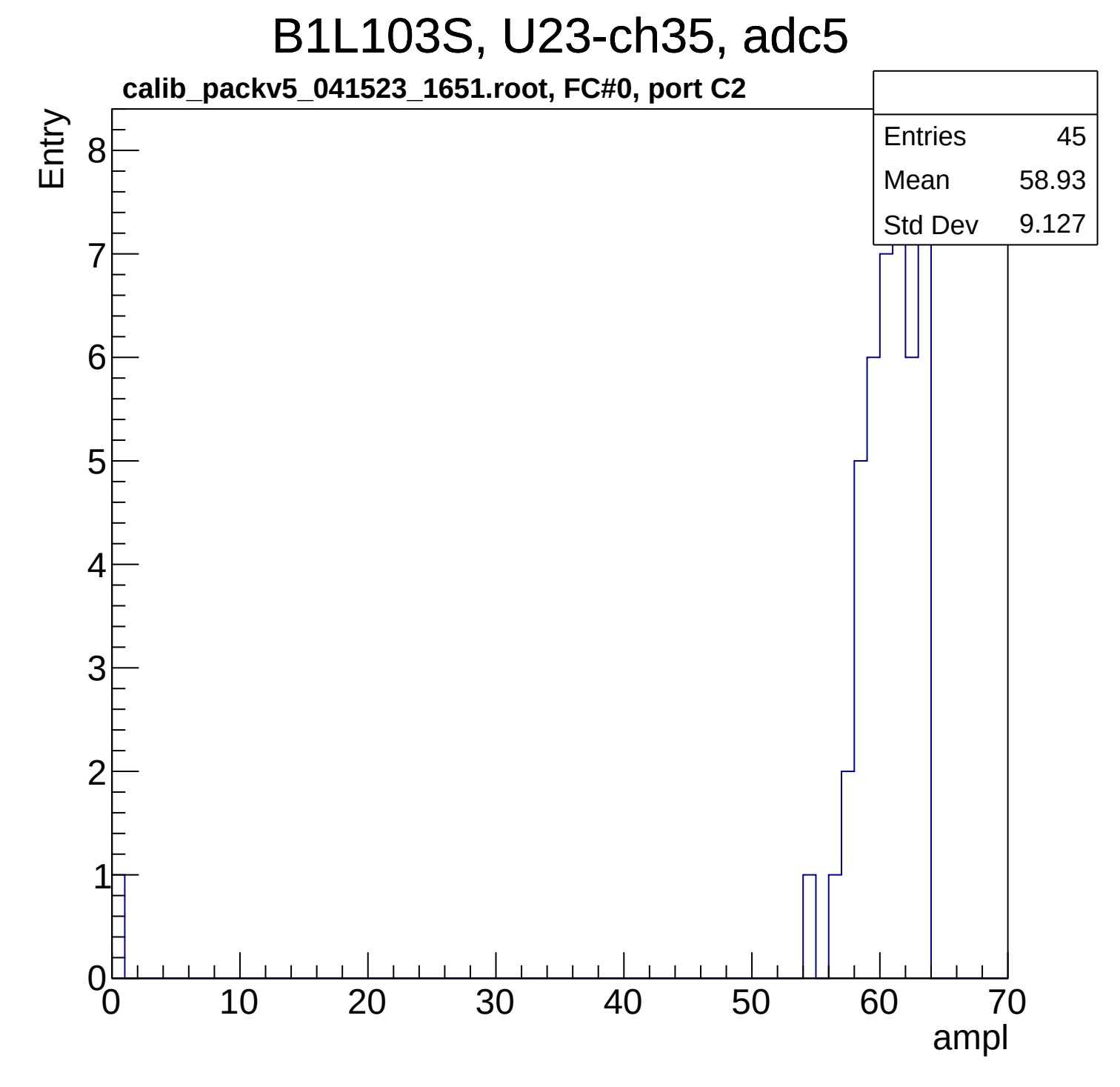

B1L103S, U23-ch35, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 84
Mean 24.39
Std Dev 12.26
Gaus mean : 30.5649
Gaus Width: 3.6130
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

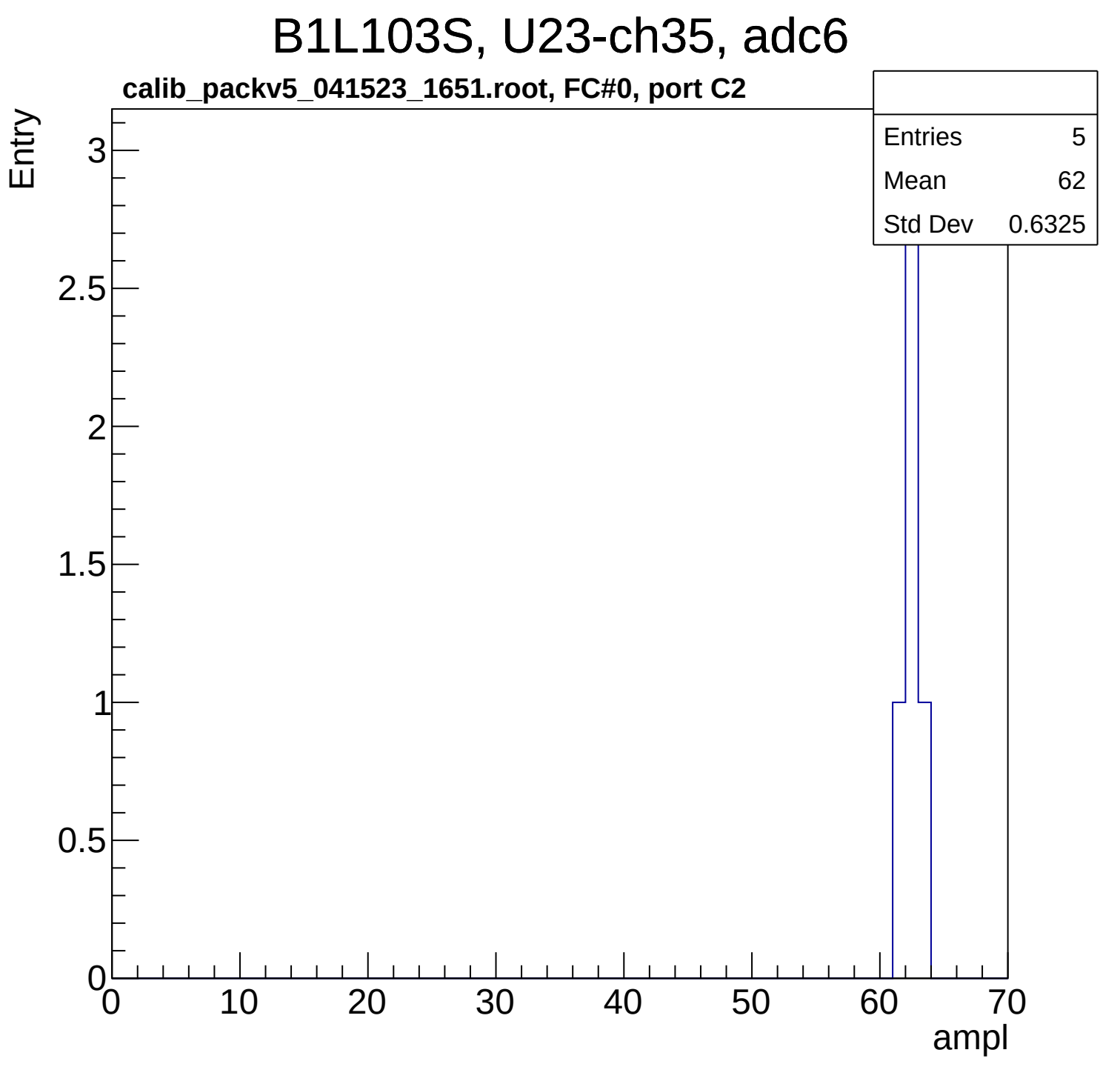

B1L103S, U23-ch35, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 31.05
Std Dev 12.79
Gaus mean : 37.0856
Gaus Width: 3.6940
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

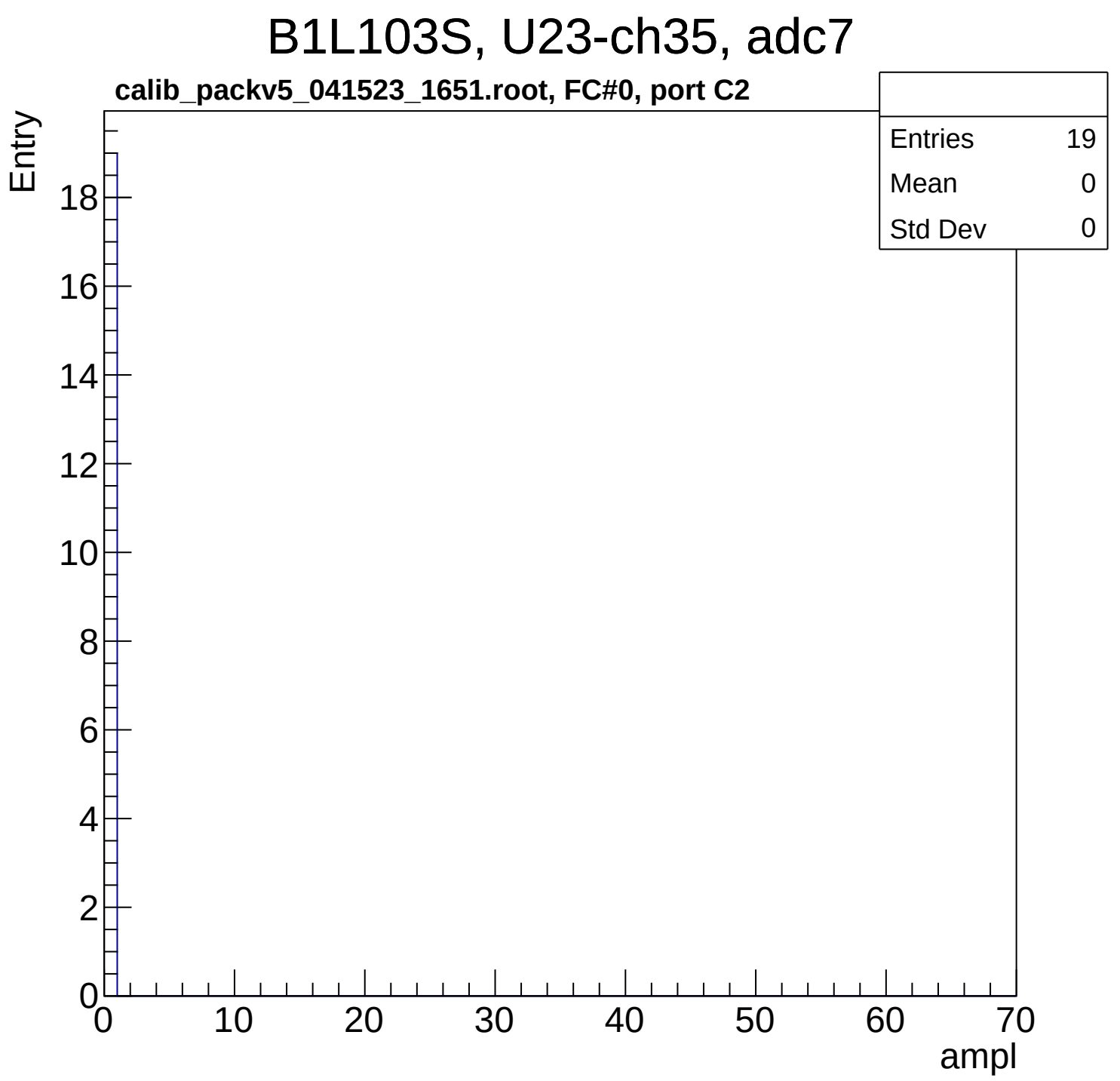

B1L103S, U23-ch35, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 41.87
Std Dev 6.01
Gaus mean : 43.2416
Gaus Width: 2.9308
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

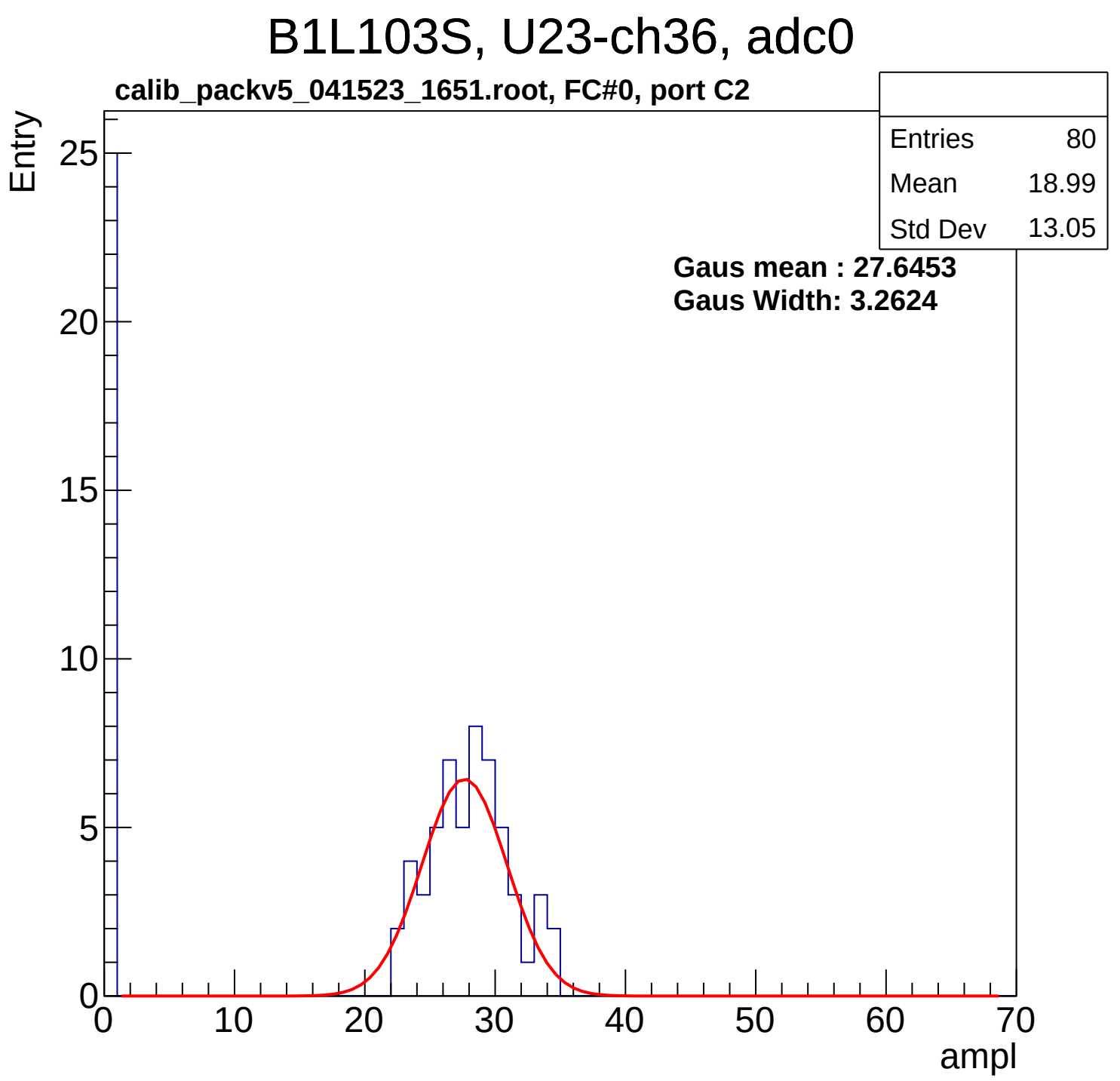

B1L103S, U23-ch35, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 48.09
Std Dev 6.855
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

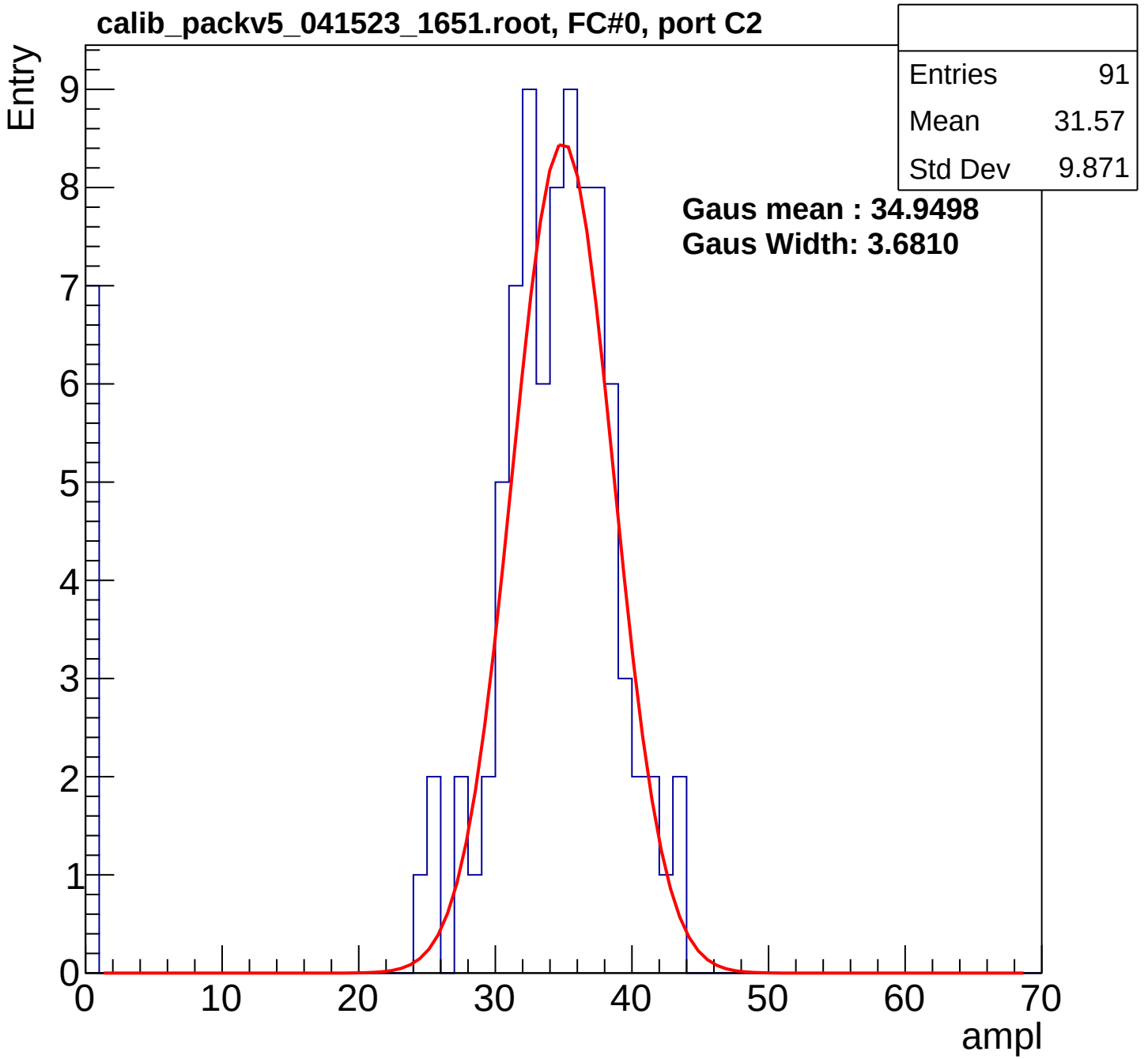

B1L103S, U23-ch35, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 54.65
Std Dev 7.541
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

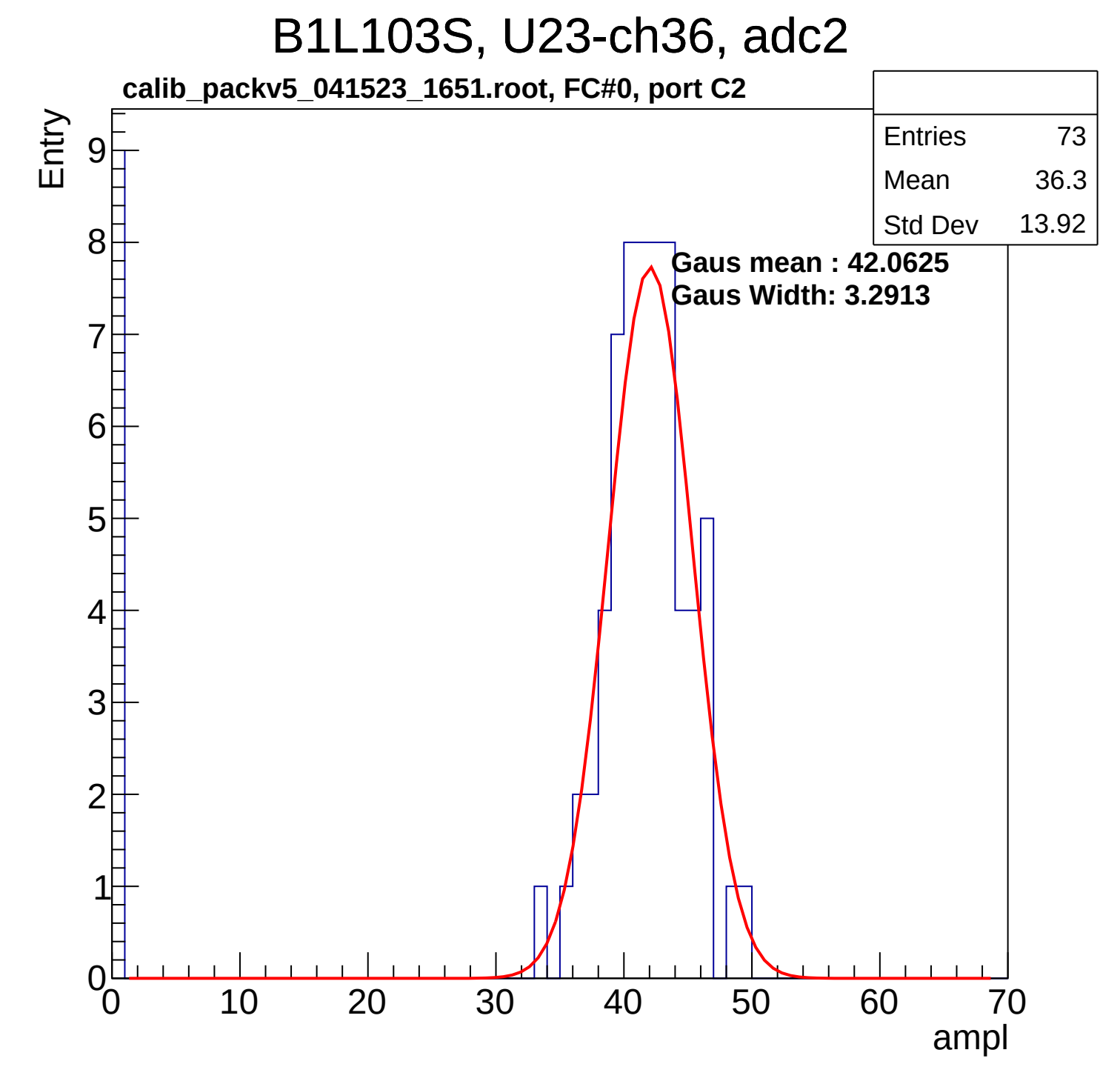

B1L103S, U23-ch35, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 45
Mean 58.93
Std Dev 9.127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

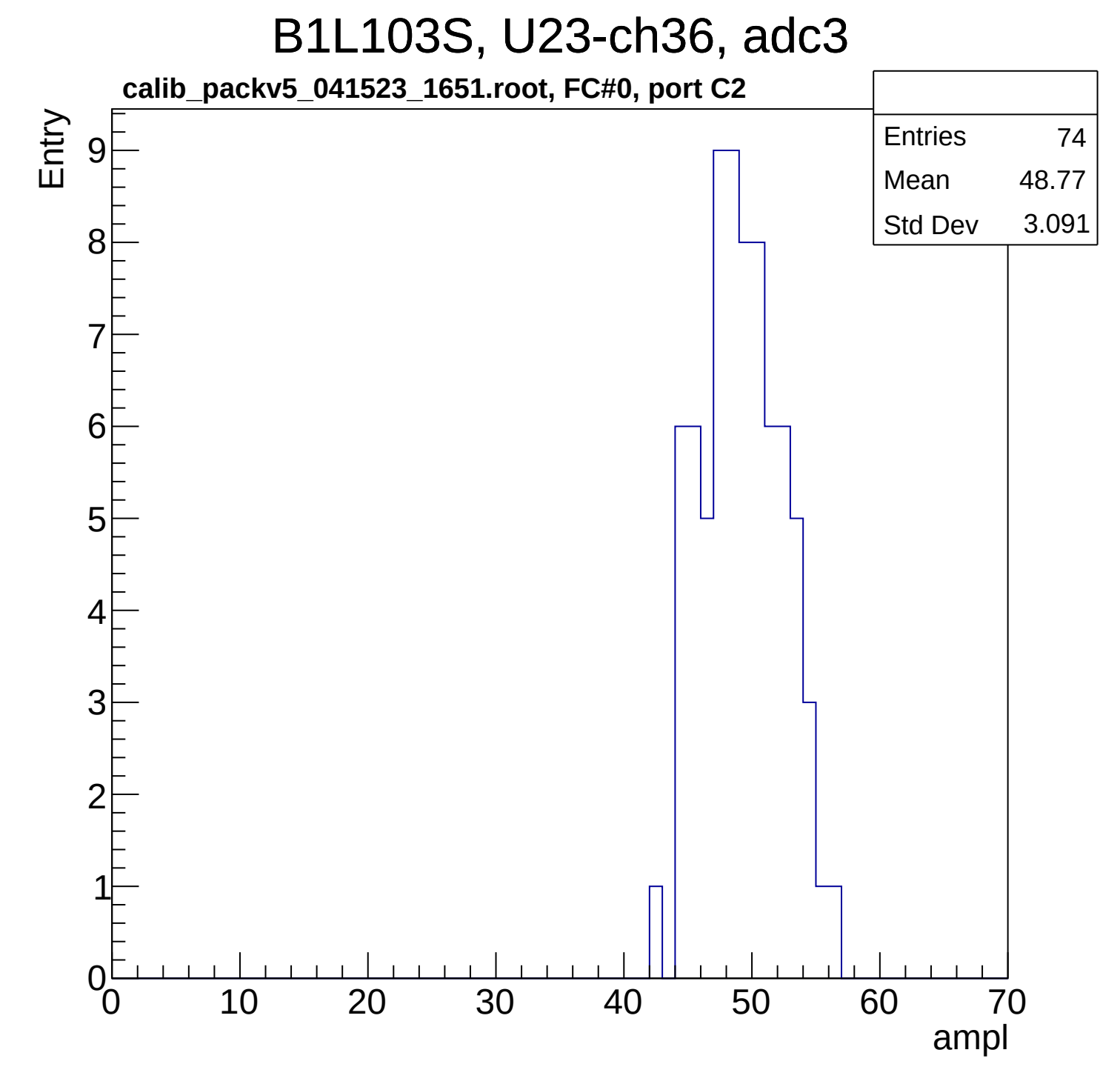

B1L103S, U23-ch35, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 62
Std Dev 0.6325
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

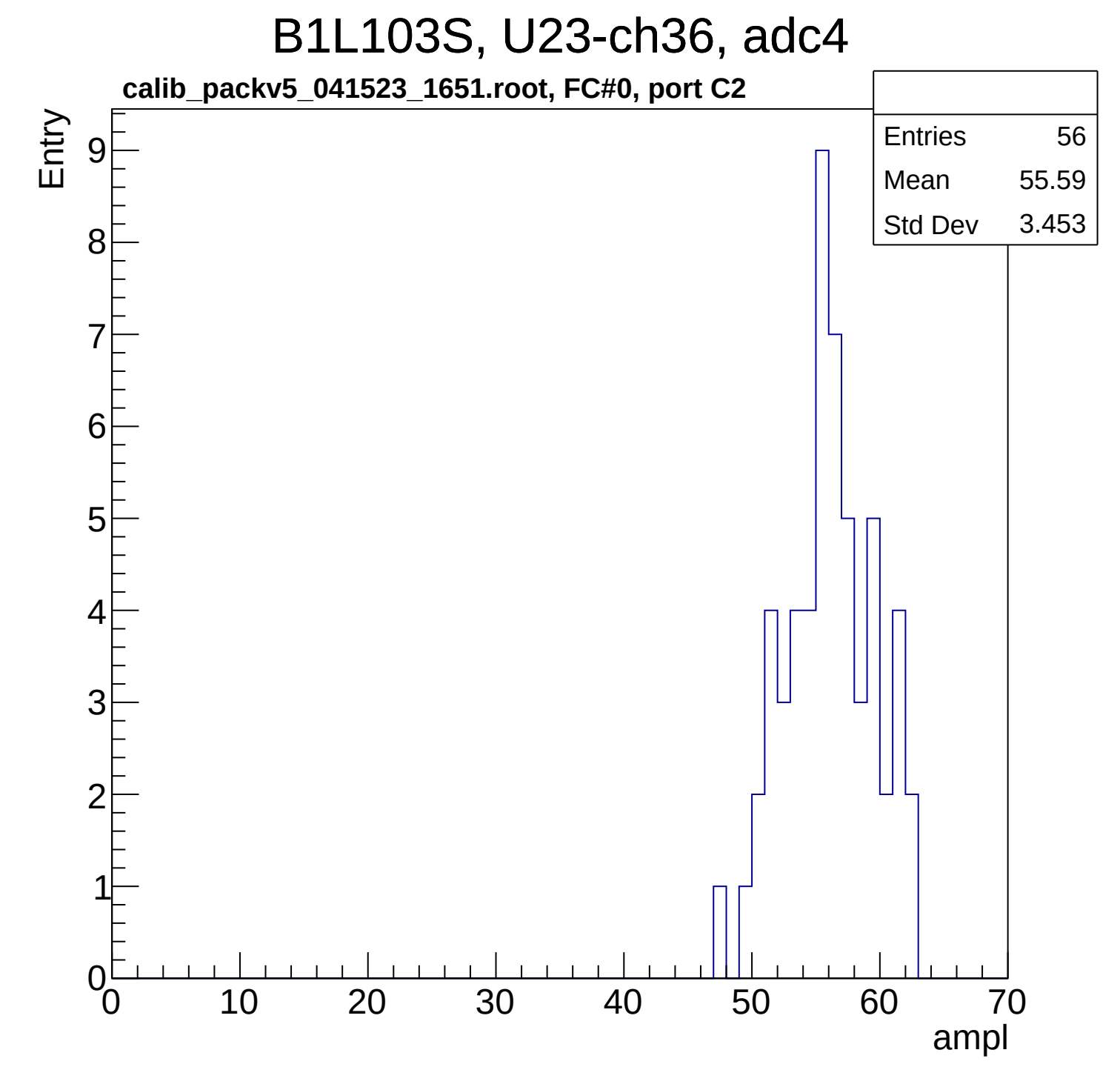

B1L103S, U23-ch35, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

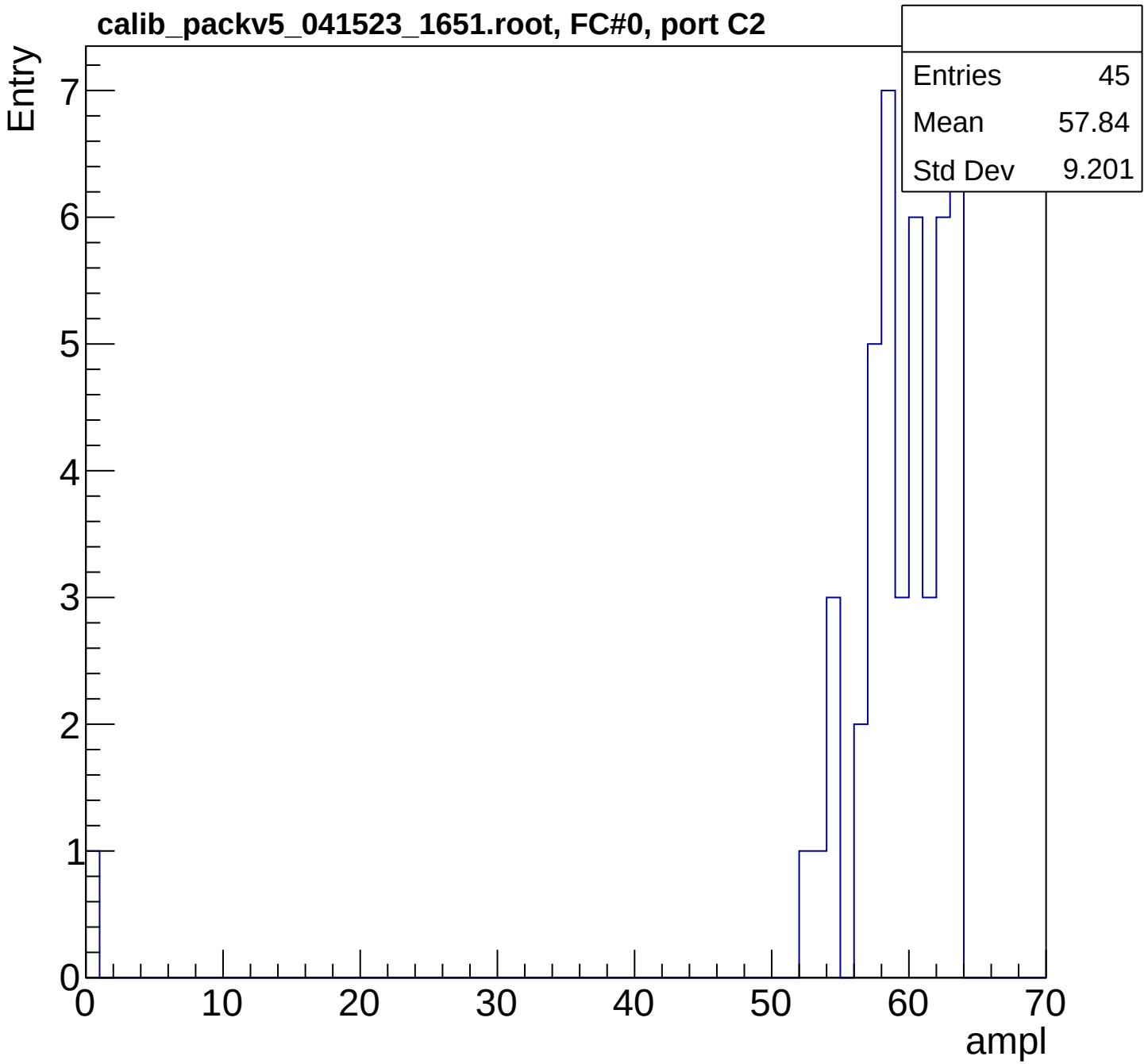

B1L103S, U23-ch36, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 18.99
Std Dev 13.05
Gaus mean : 27.6453
Gaus Width: 3.2624
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

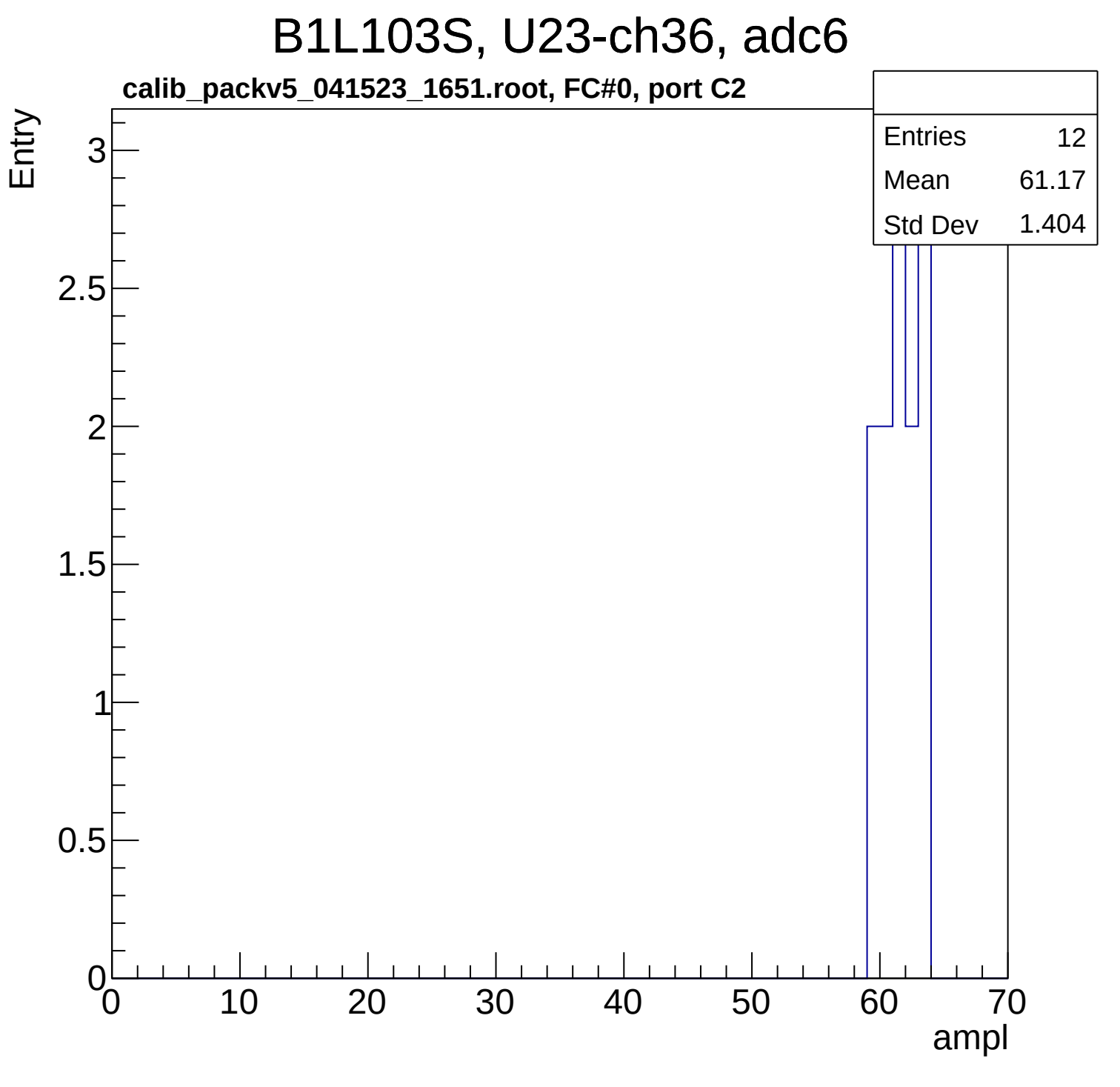

B1L103S, U23-ch36, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 31.57
Std Dev 9.871
Gaus mean : 34.9498
Gaus Width: 3.6810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

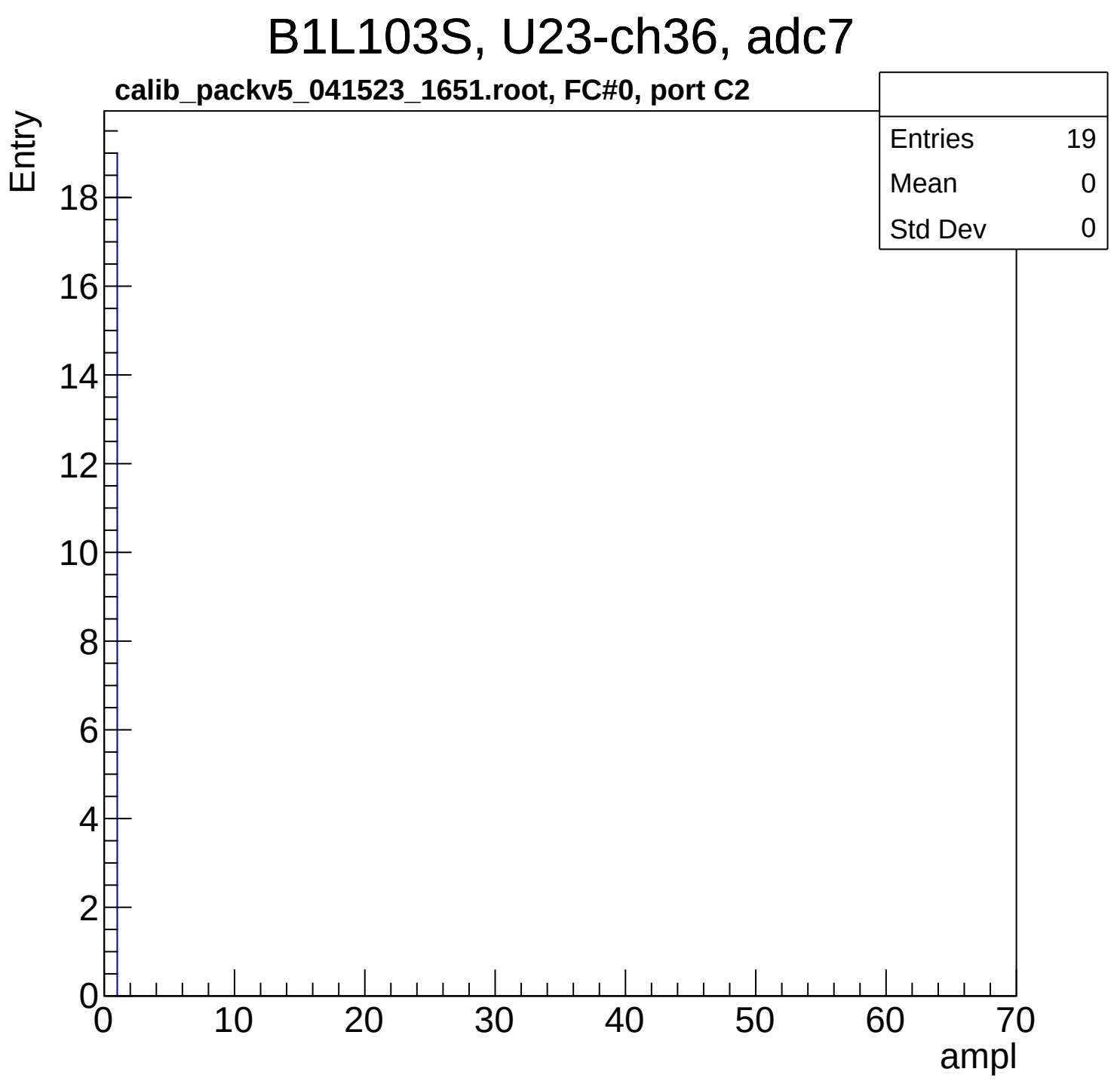

B1L103S, U23-ch36, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 36.3
Std Dev 13.92
Gaus mean : 42.0625
Gaus Width: 3.2913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

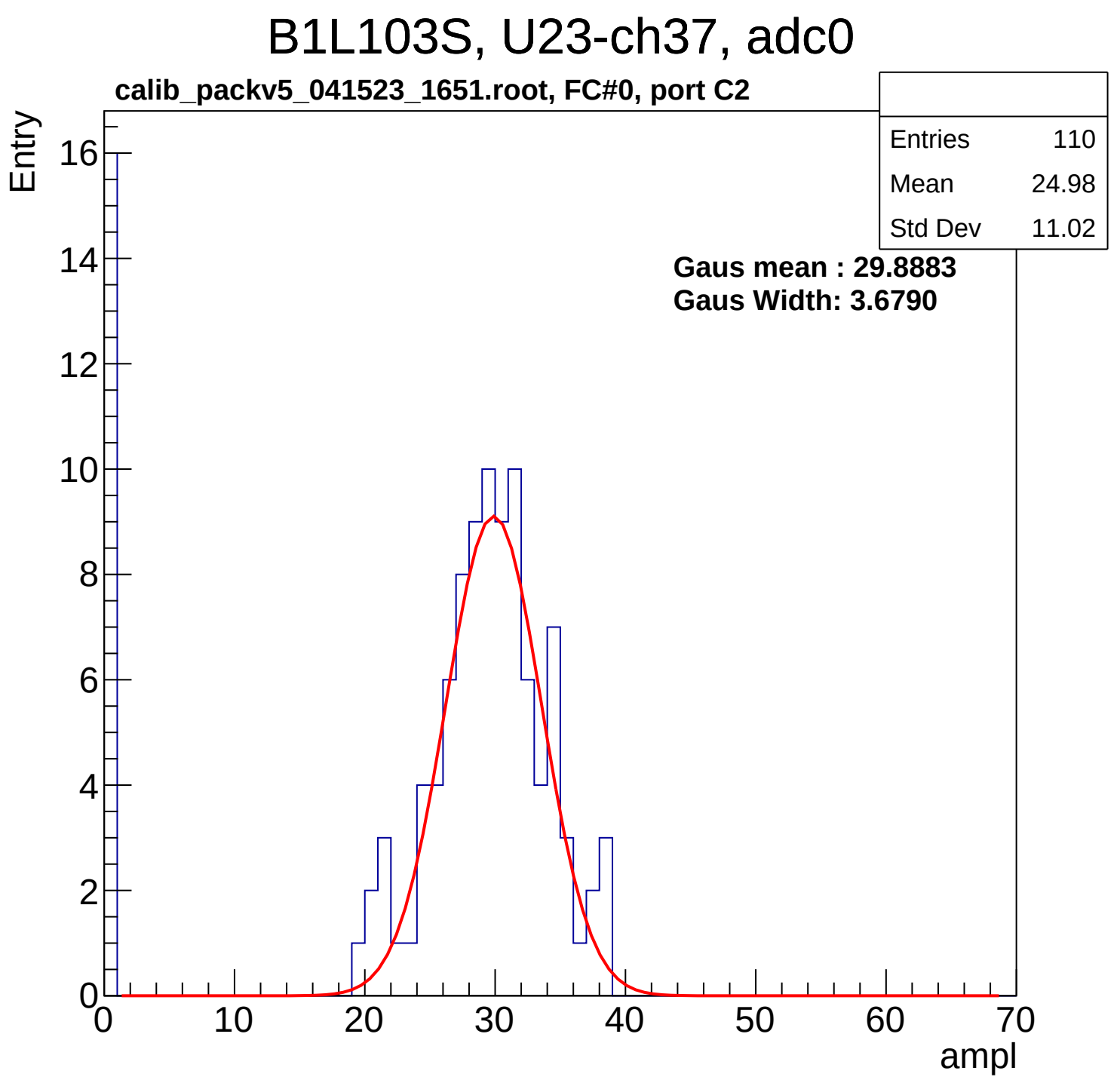

B1L103S, U23-ch36, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 48.77
Std Dev 3.091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

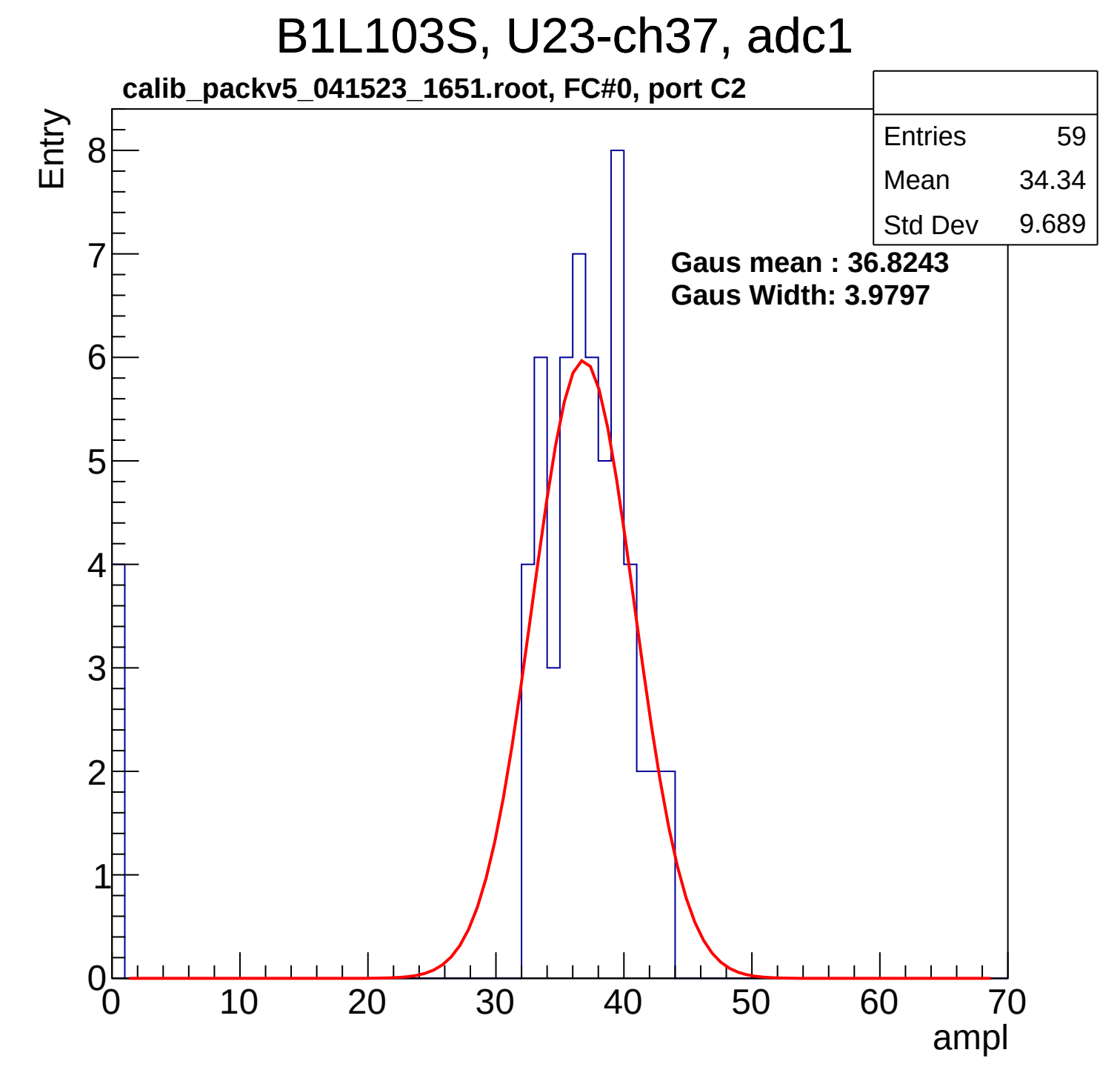

B1L103S, U23-ch36, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 55.59
Std Dev 3.453
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

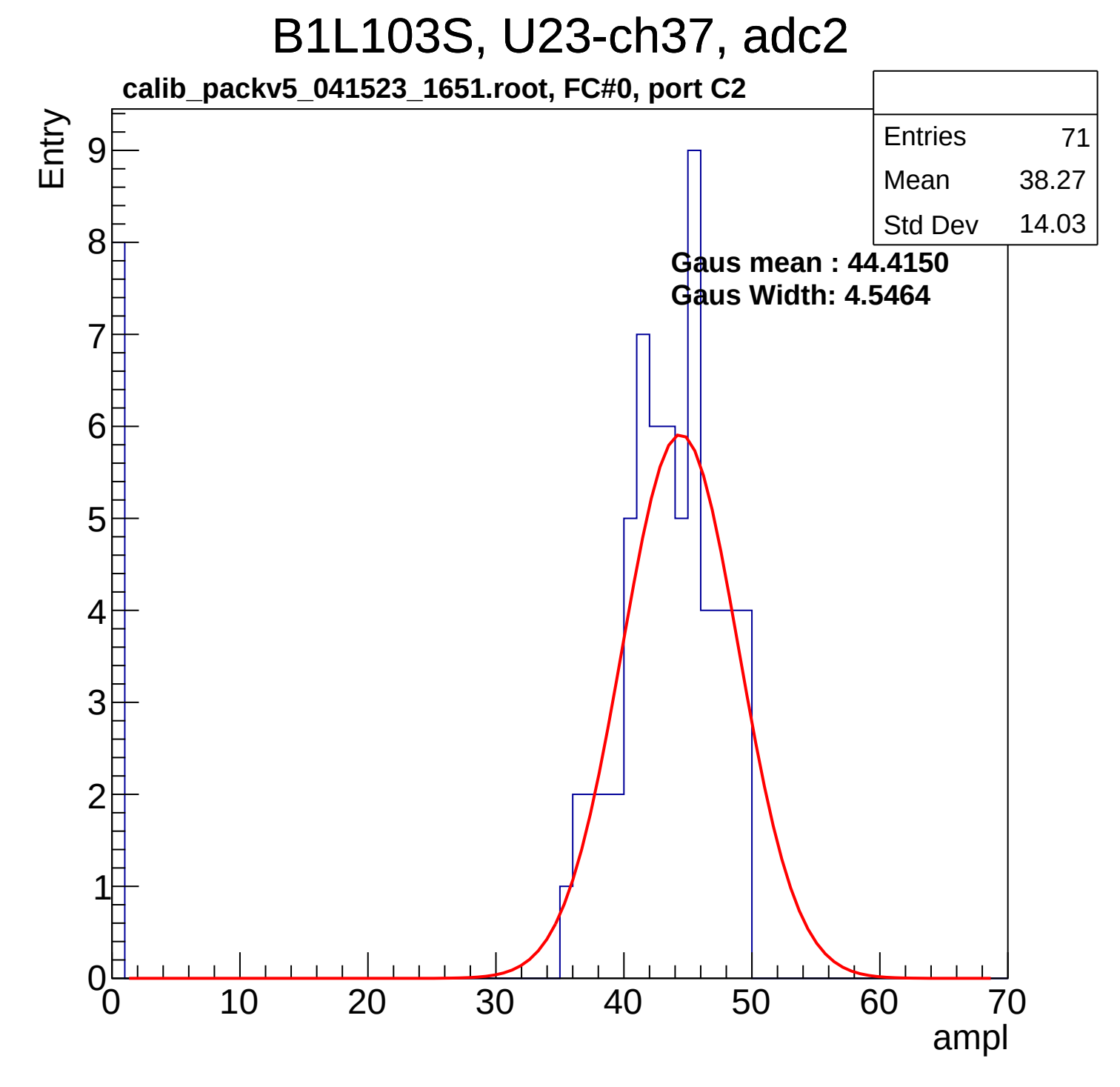

B1L103S, U23-ch36, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 45
Mean 57.84
Std Dev 9.201
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

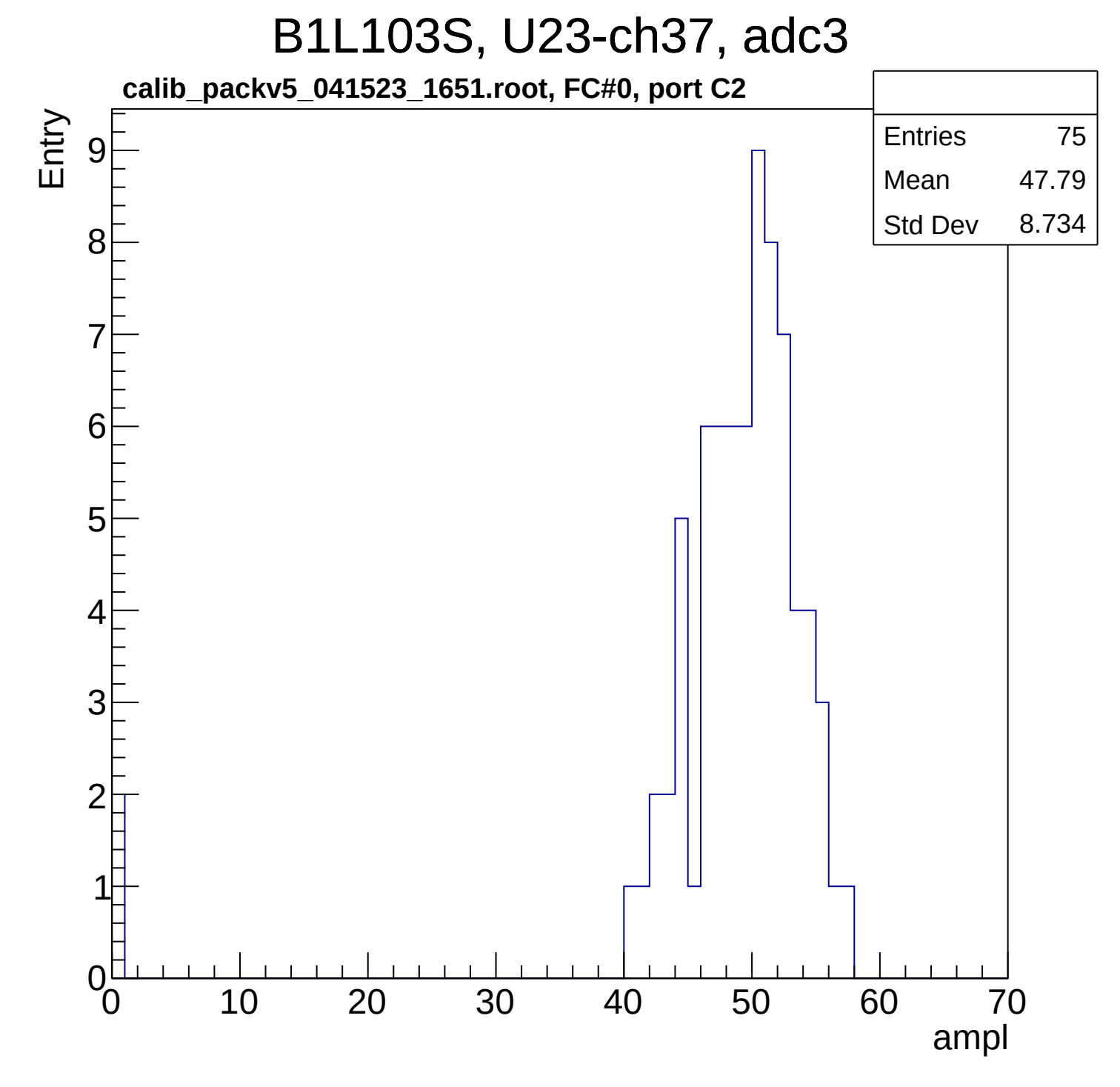

B1L103S, U23-ch36, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 12
Mean 61.17
Std Dev 1.404
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

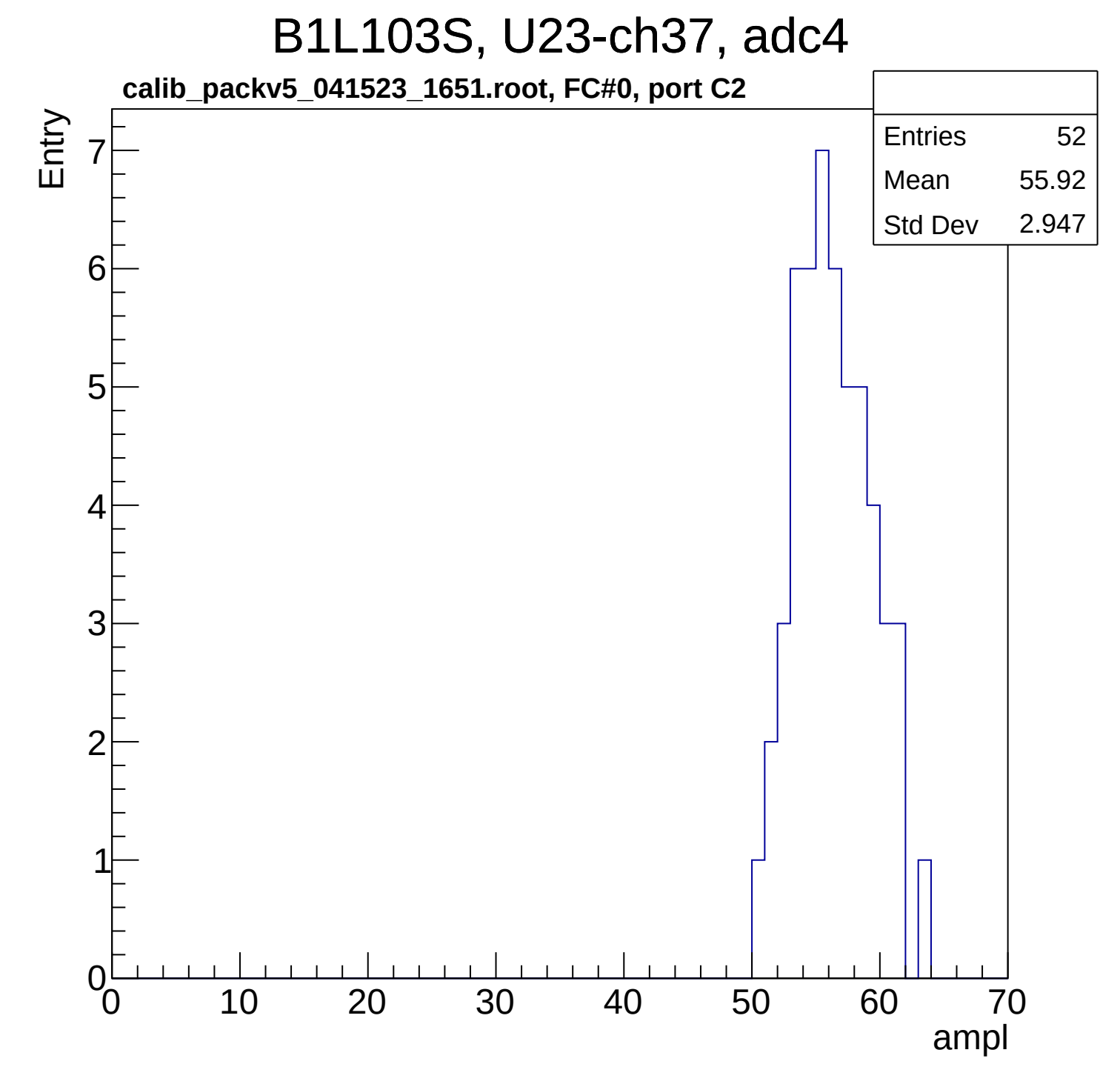

B1L103S, U23-ch36, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

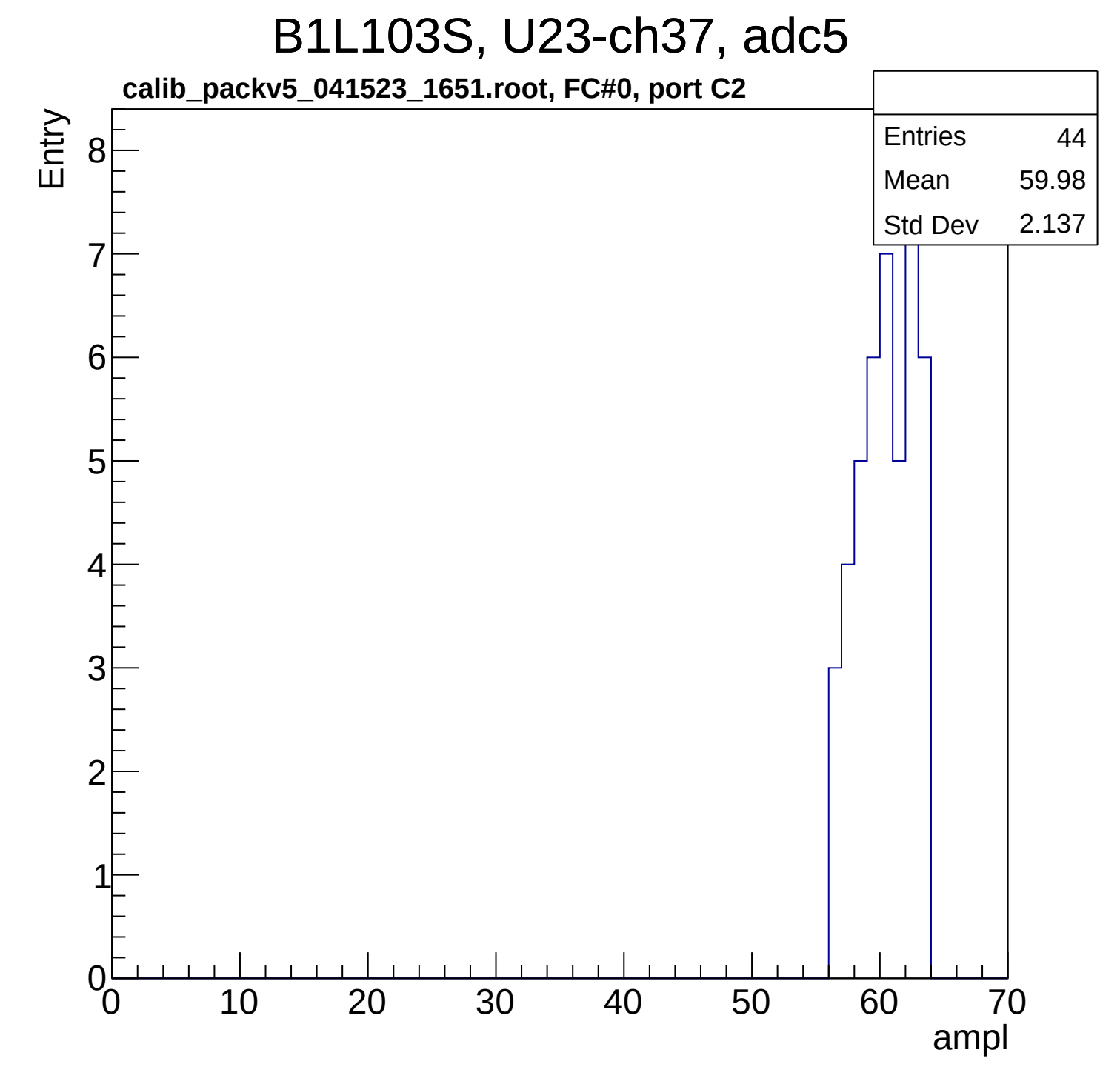

B1L103S, U23-ch37, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 110
Mean 24.98
Std Dev 11.02
Gaus mean : 29.8883
Gaus Width: 3.6790
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

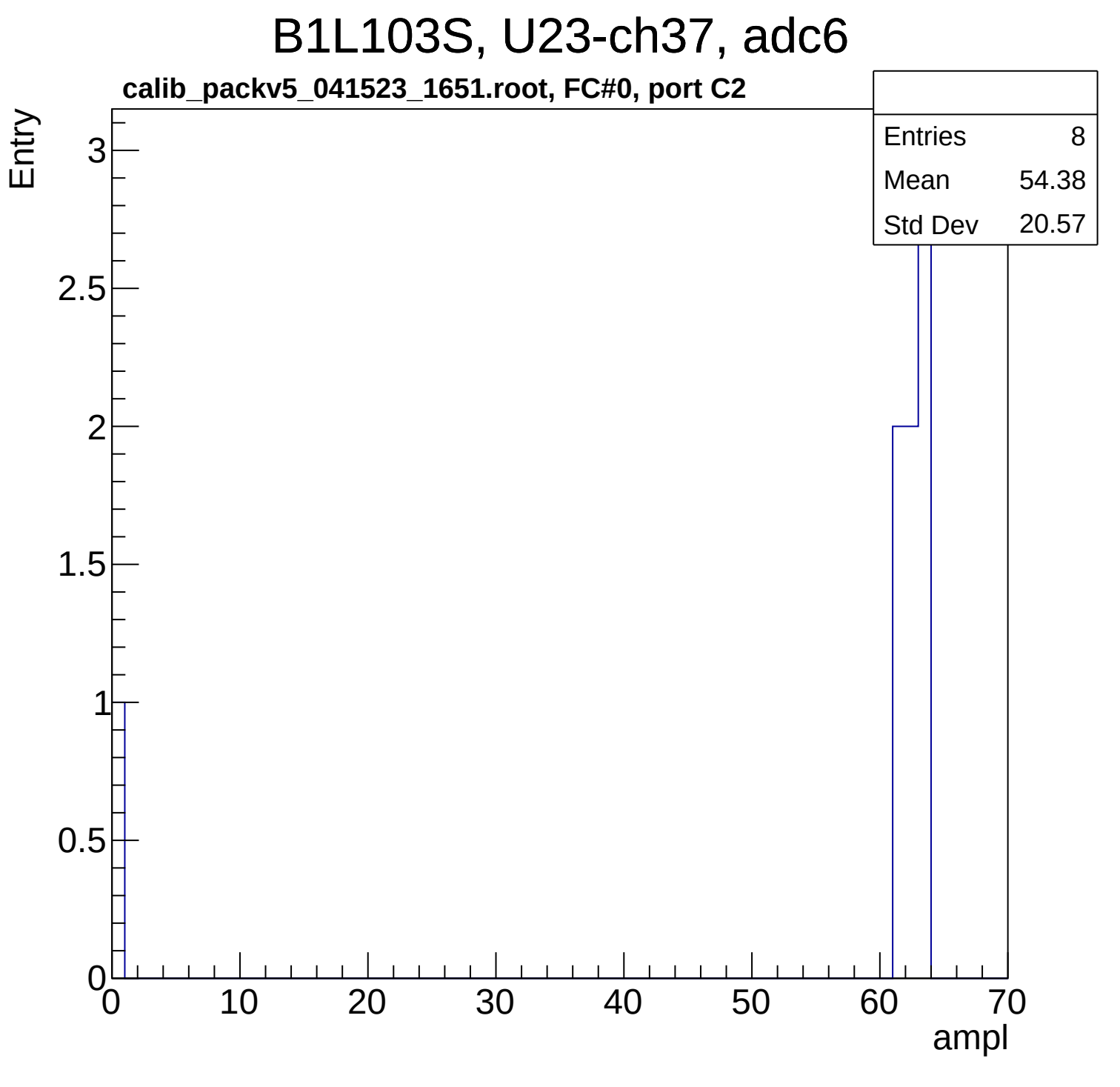

B1L103S, U23-ch37, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 34.34
Std Dev 9.689
Gaus mean : 36.8243
Gaus Width: 3.9797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

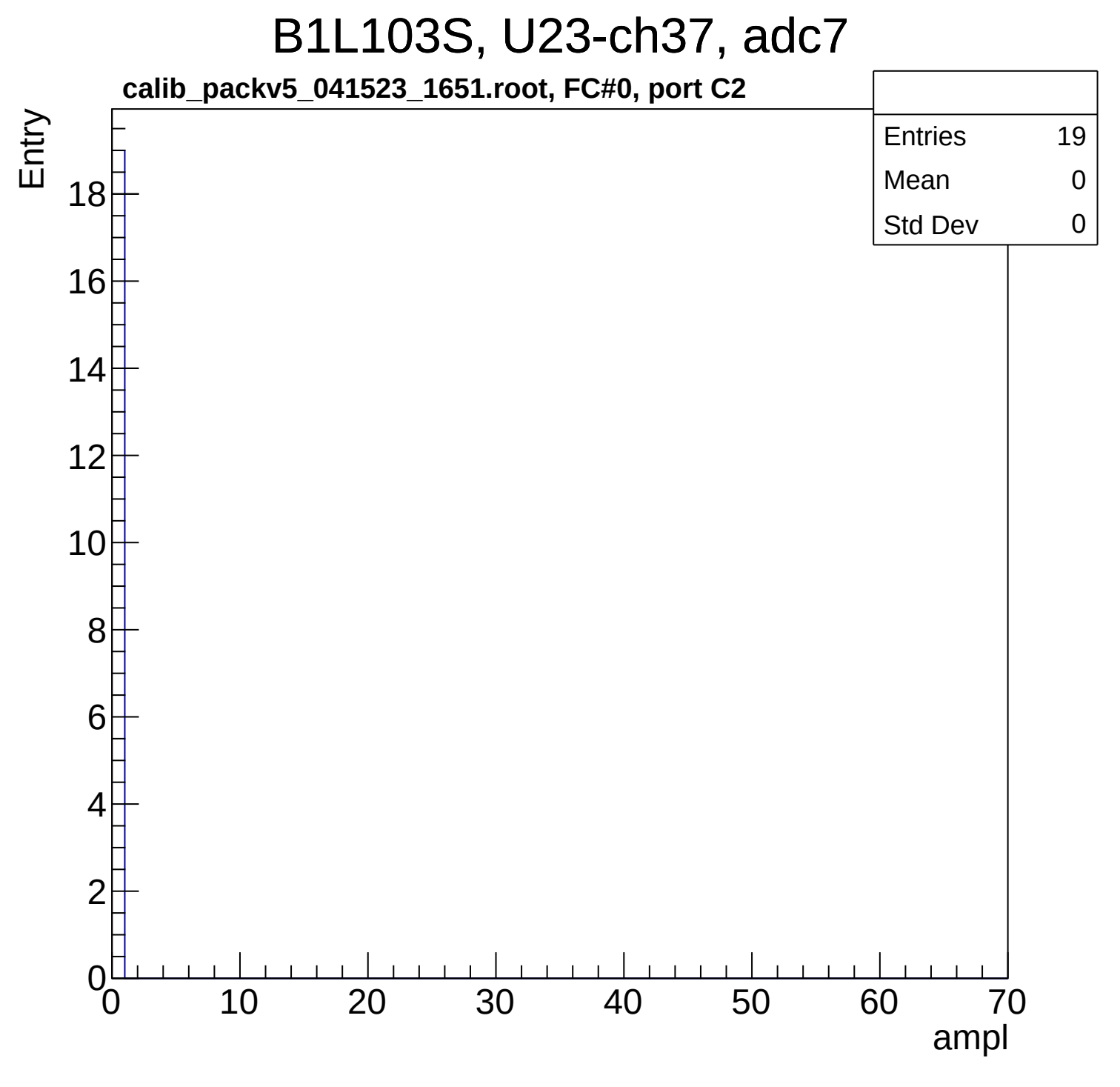

B1L103S, U23-ch37, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 38.27
Std Dev 14.03
Gaus mean : 44.4150
Gaus Width: 4.5464
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

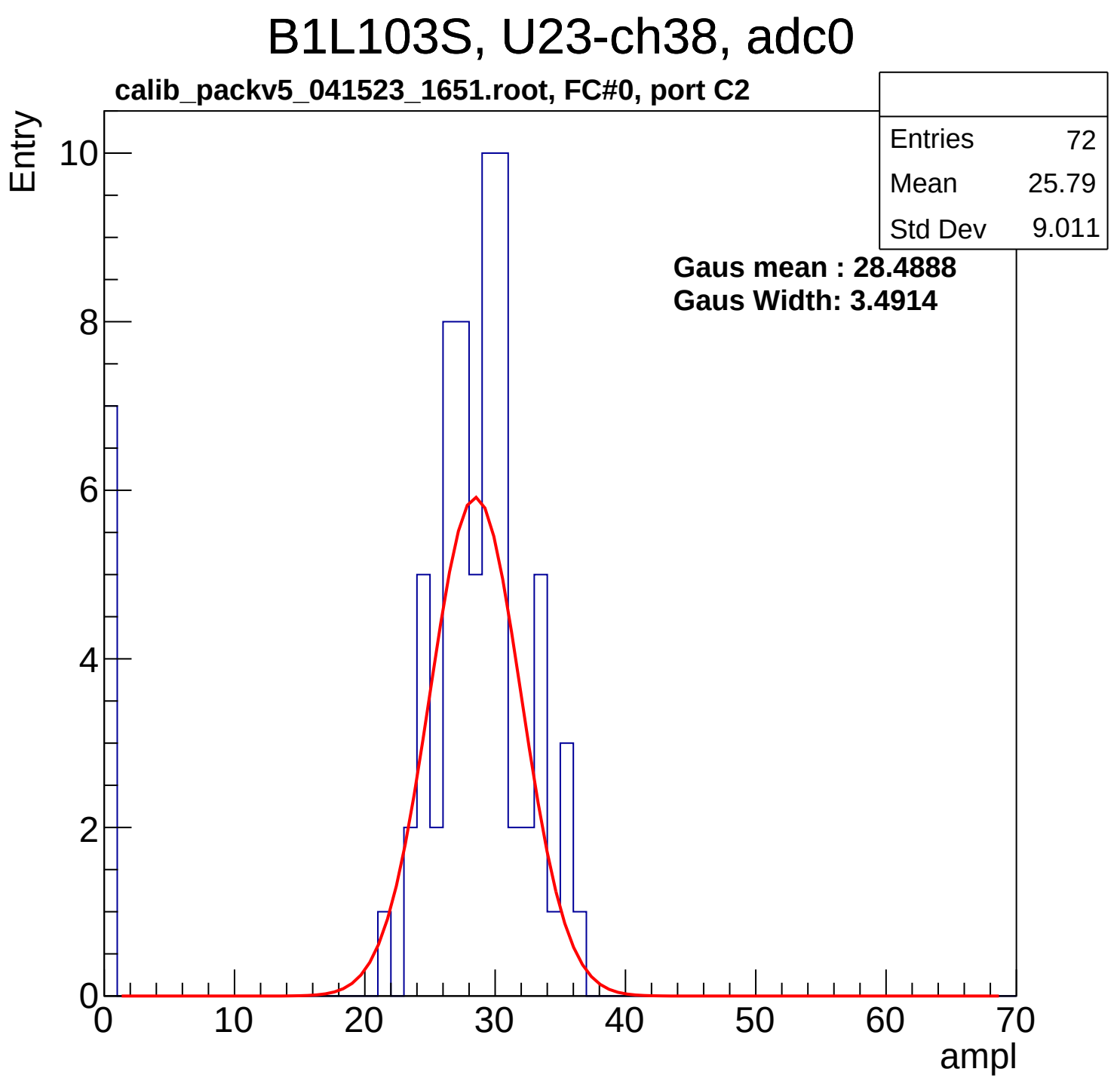

B1L103S, U23-ch37, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 47.79
Std Dev 8.734
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

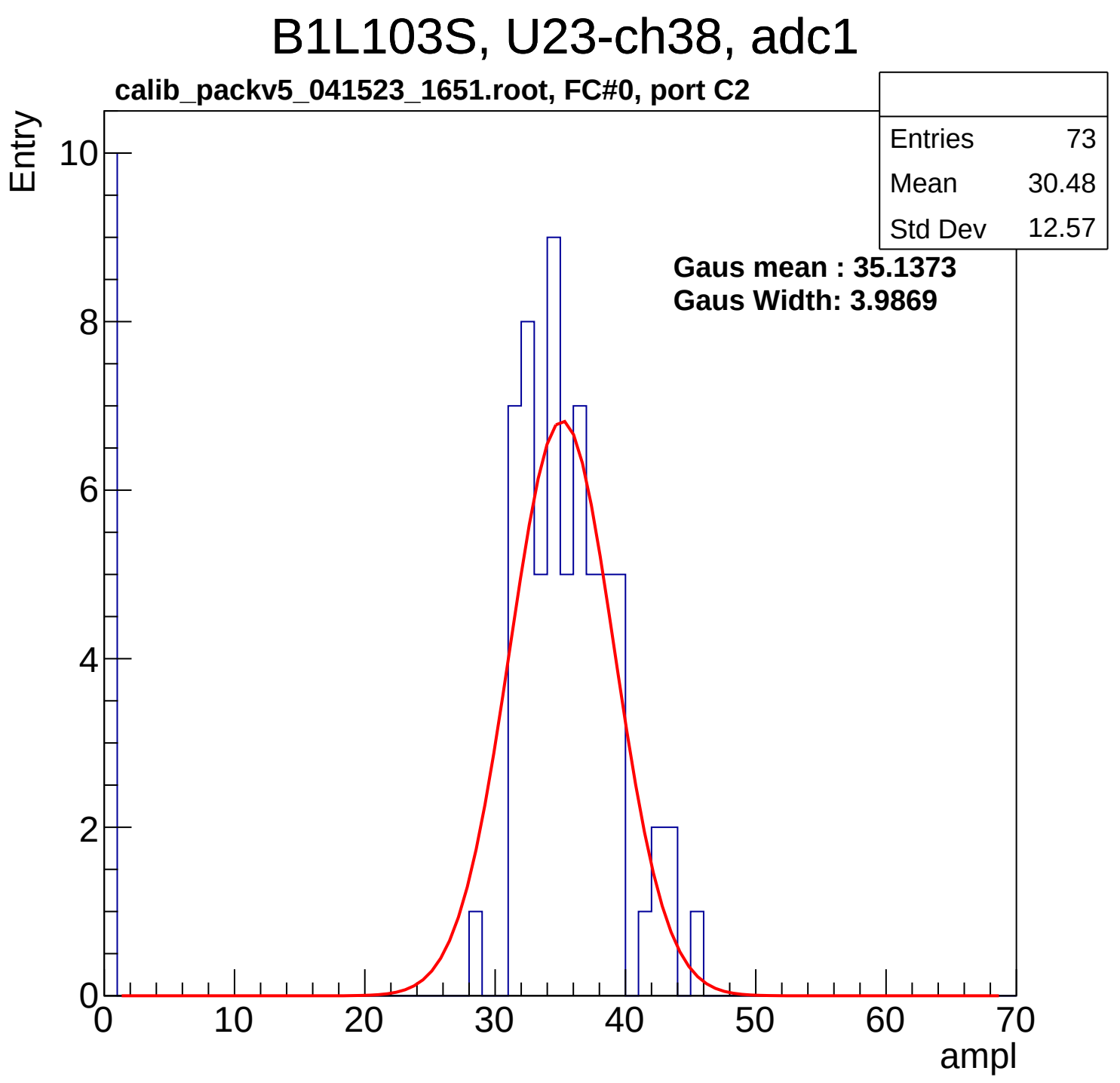

B1L103S, U23-ch37, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 55.92
Std Dev 2.947
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

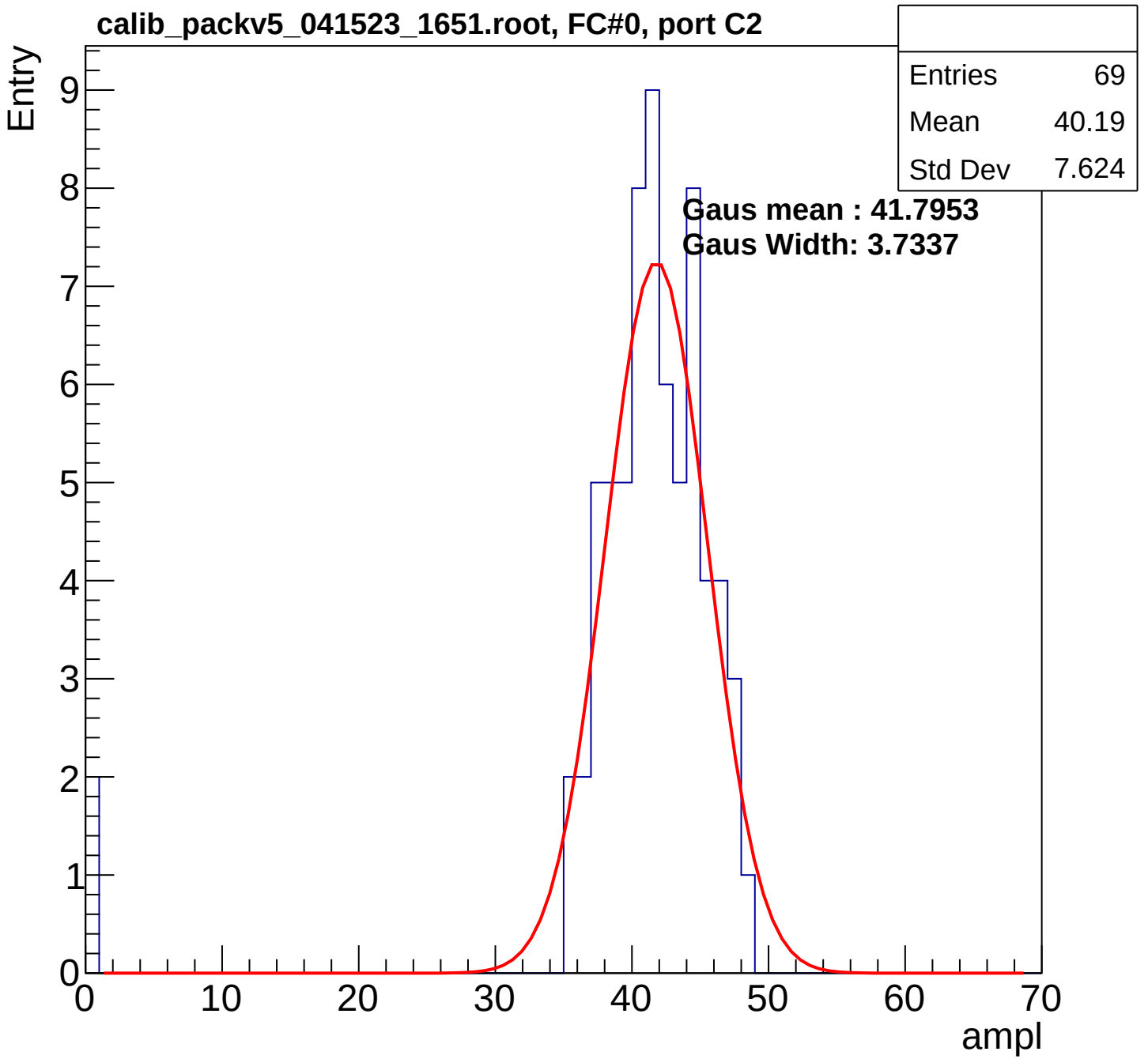

B1L103S, U23-ch37, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 44
Mean 59.98
Std Dev 2.137
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

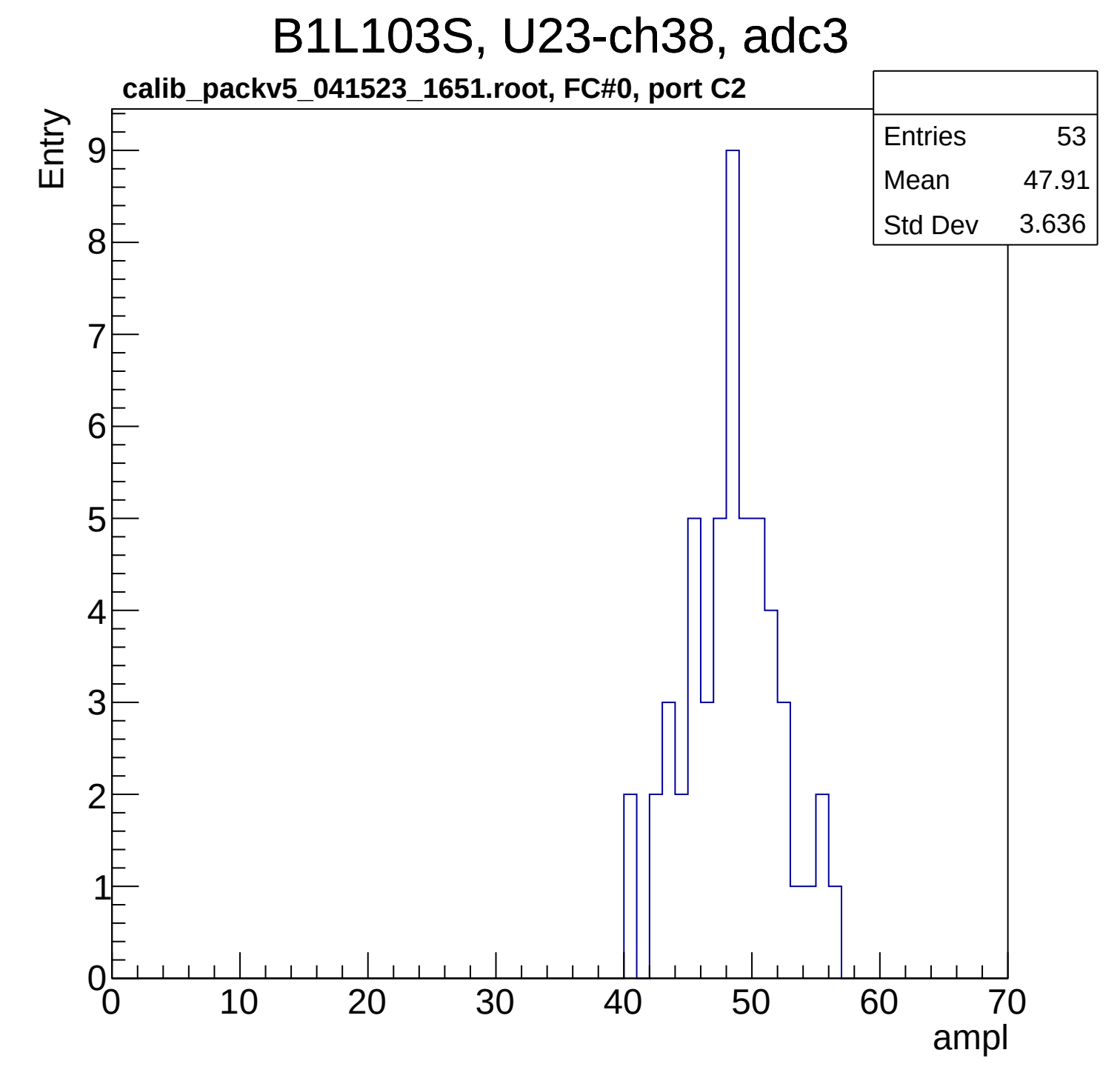

B1L103S, U23-ch37, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

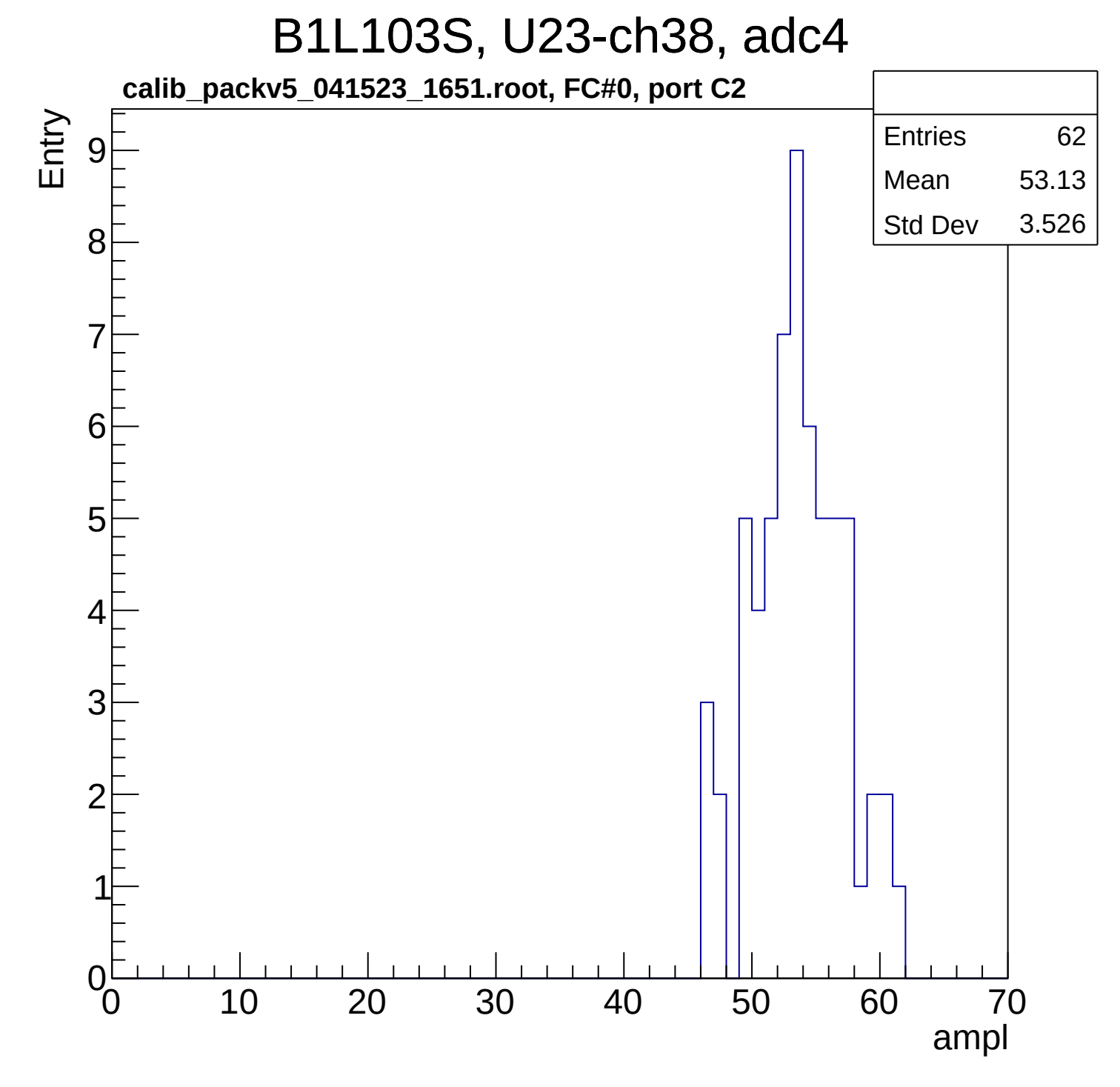

B1L103S, U23-ch37, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

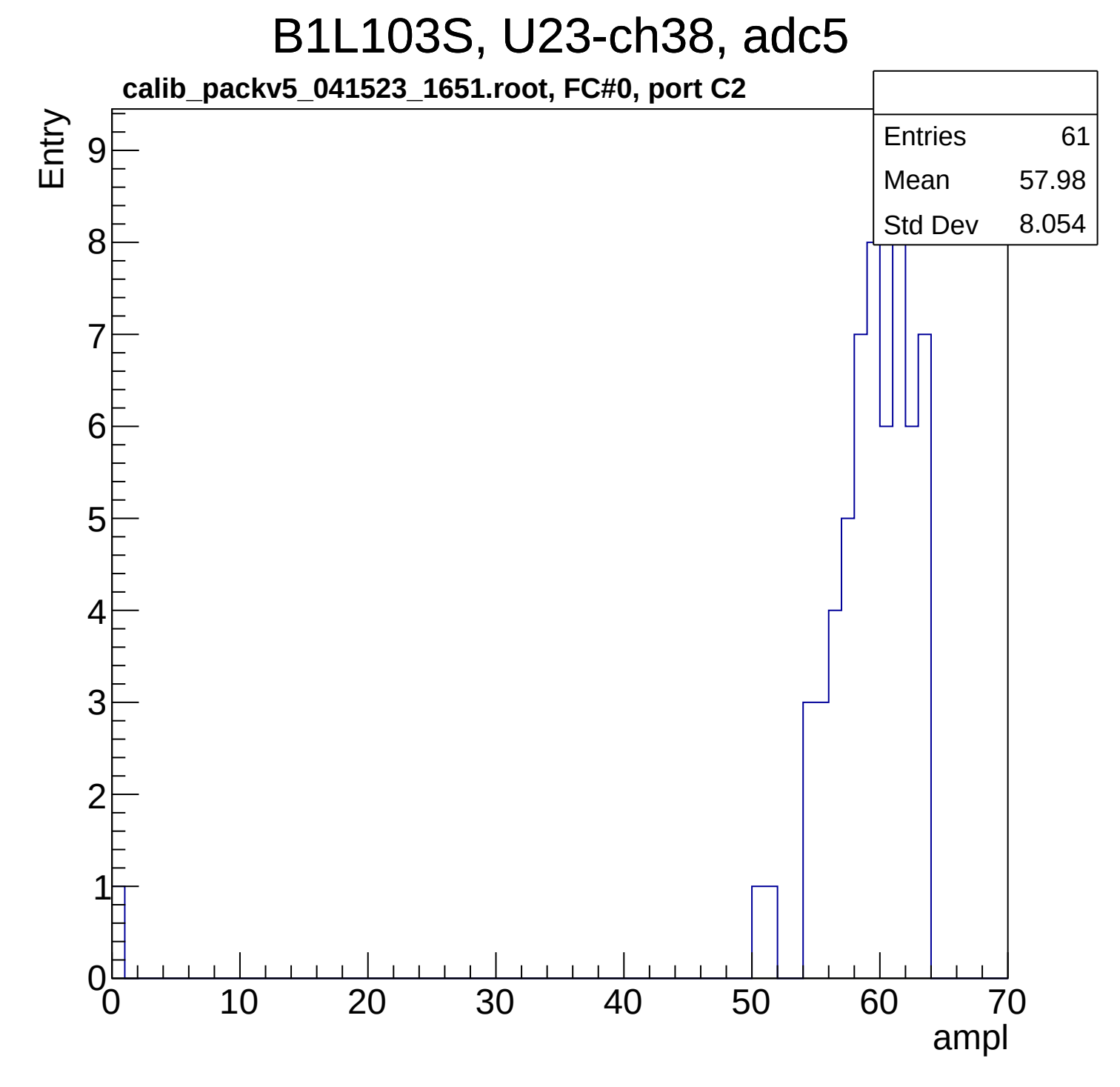

B1L103S, U23-ch38, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 25.79
Std Dev 9.011
Gaus mean : 28.4888
Gaus Width: 3.4914
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

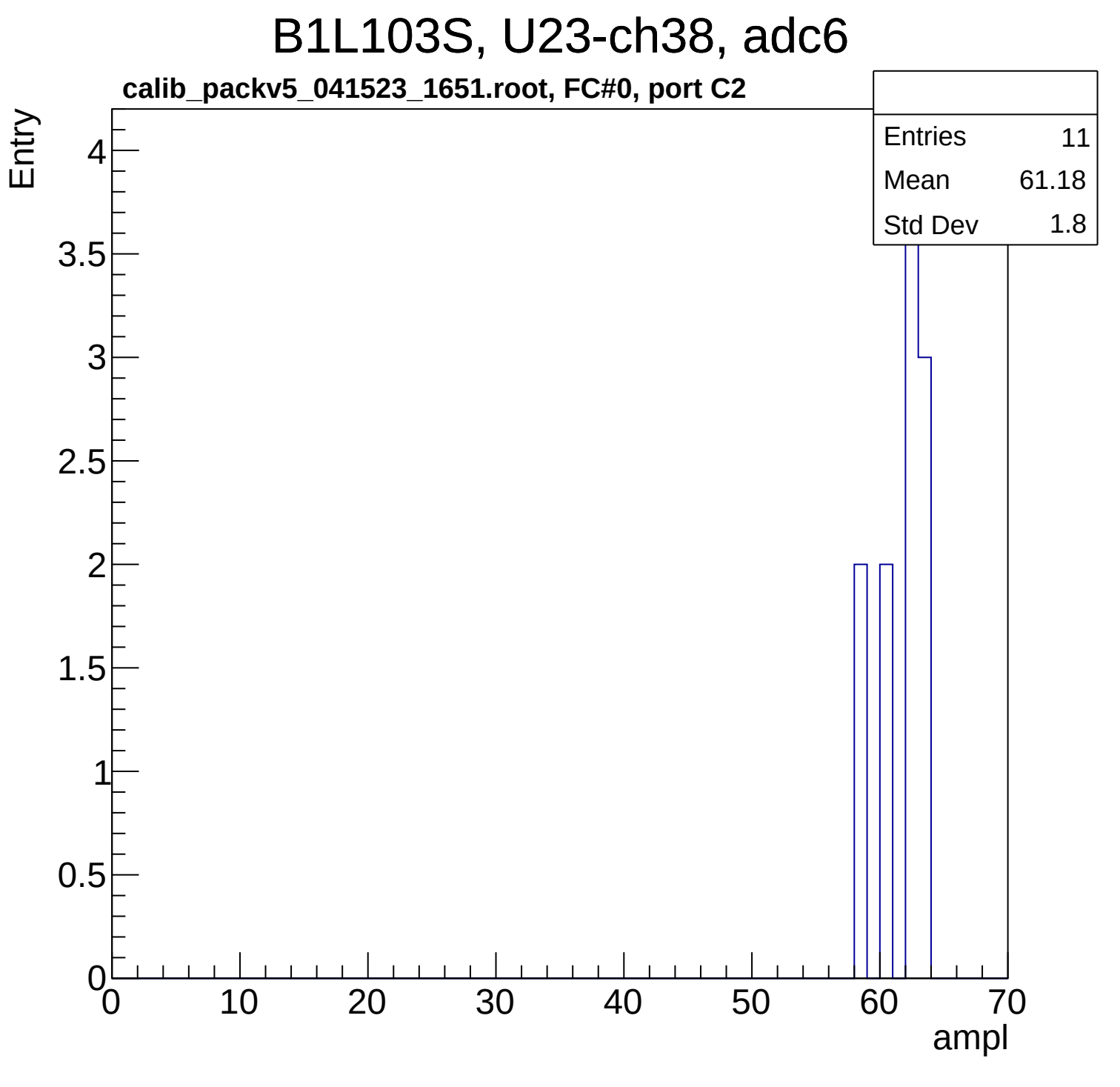

B1L103S, U23-ch38, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 30.48
Std Dev 12.57
Gaus mean : 35.1373
Gaus Width: 3.9869
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

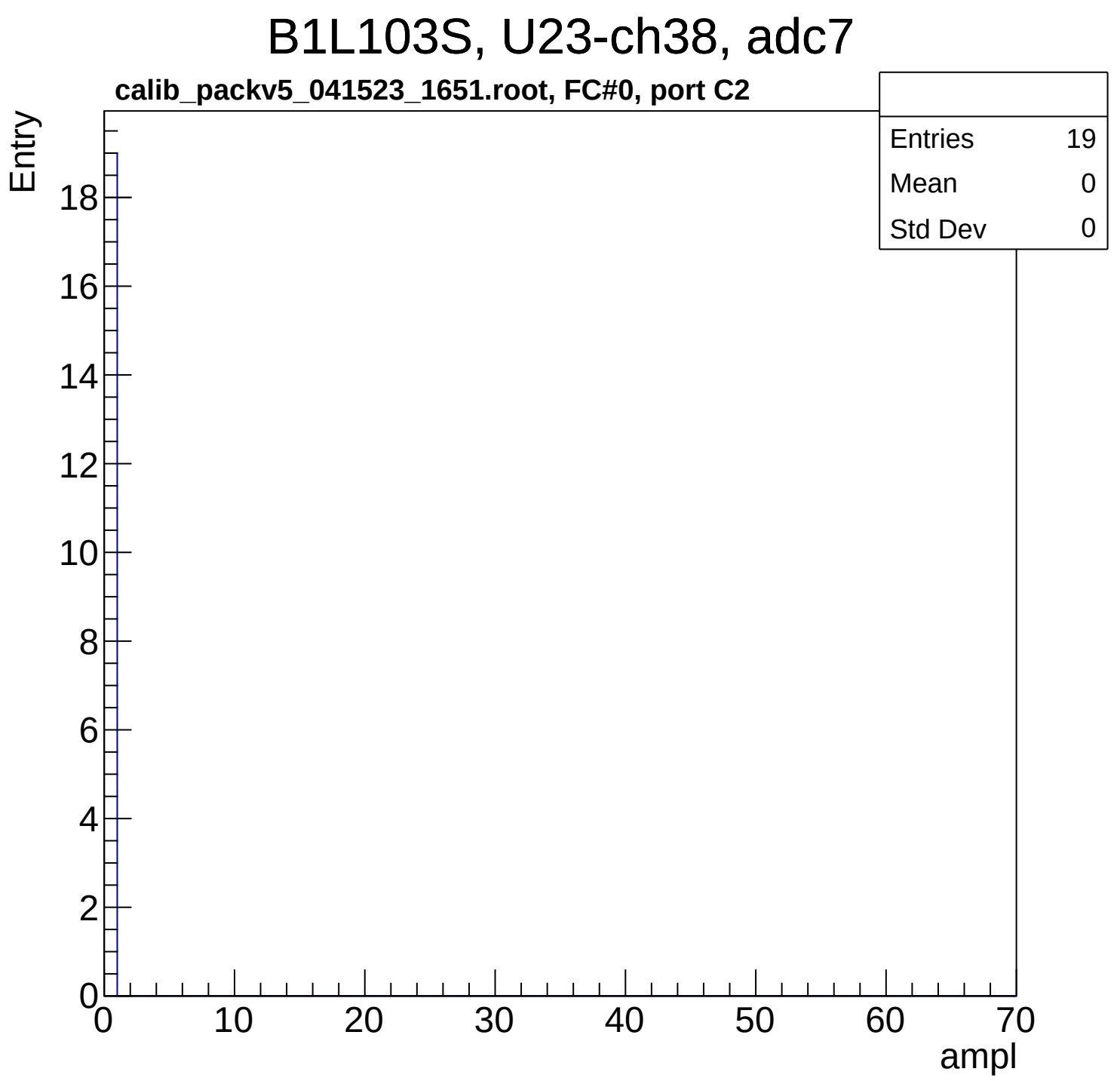

B1L103S, U23-ch38, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 40.19
Std Dev 7.624
Gaus mean : 41.7953
Gaus Width: 3.7337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

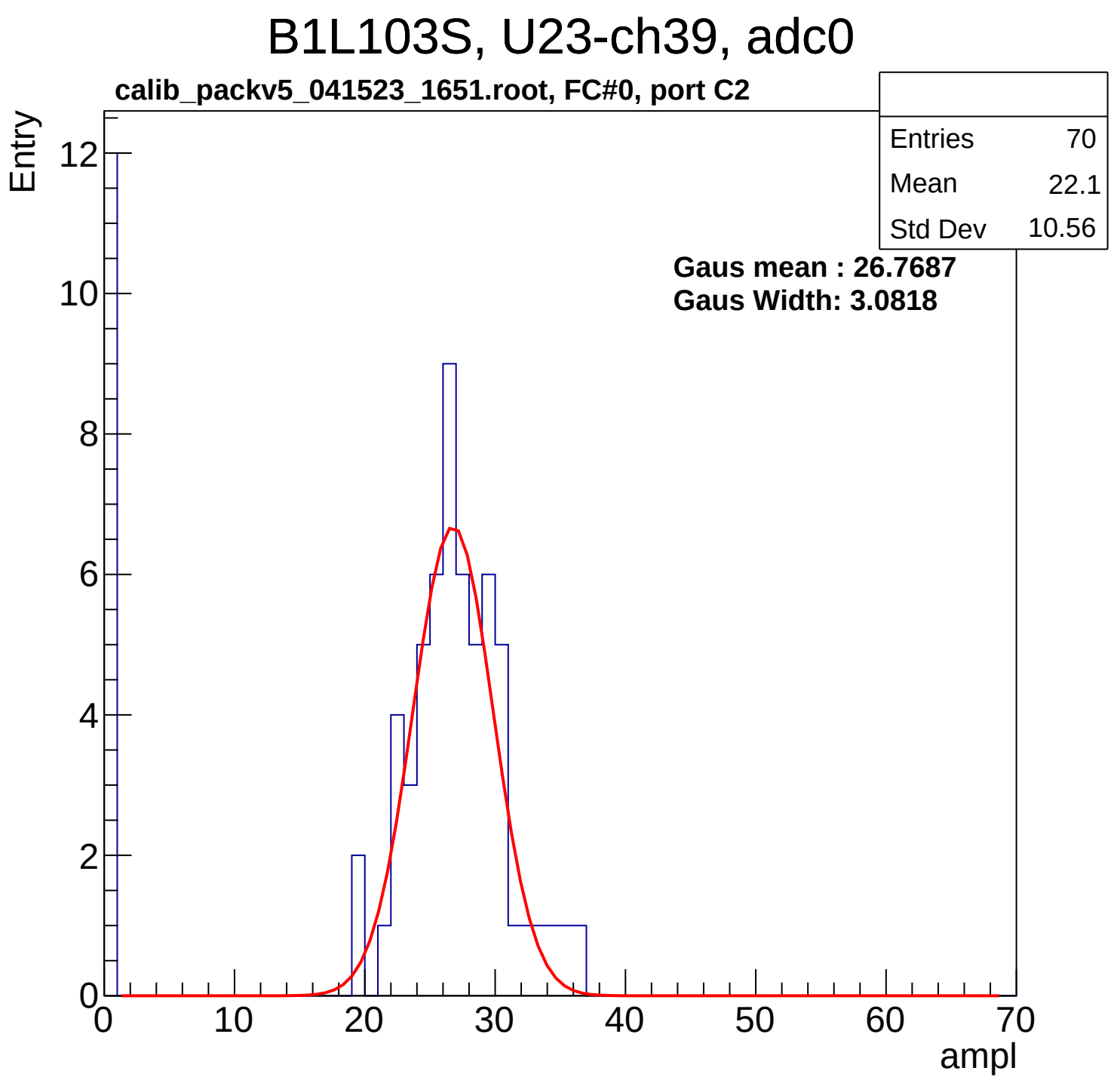

B1L103S, U23-ch38, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 47.91
Std Dev 3.636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

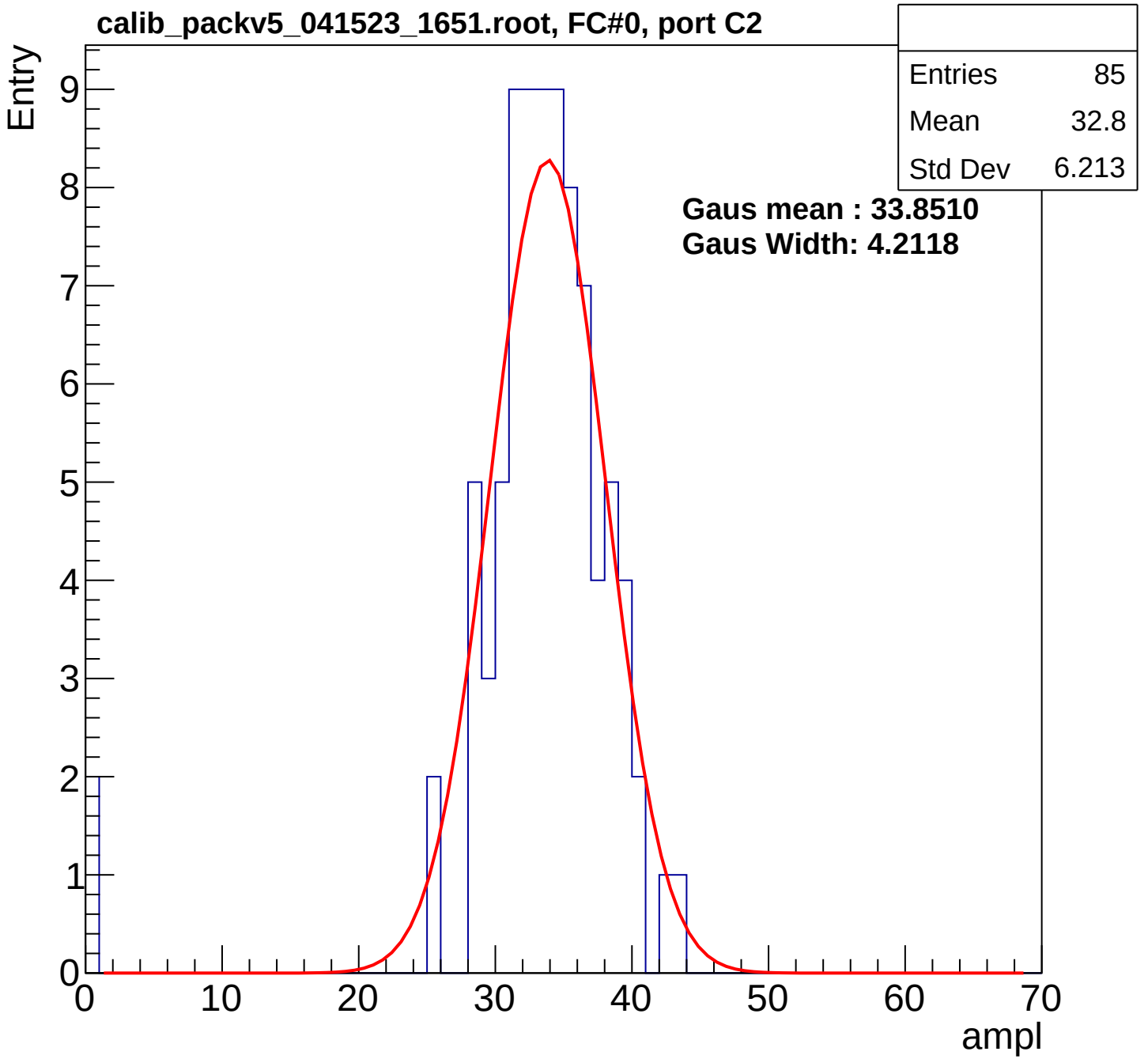

B1L103S, U23-ch38, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 53.13
Std Dev 3.526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

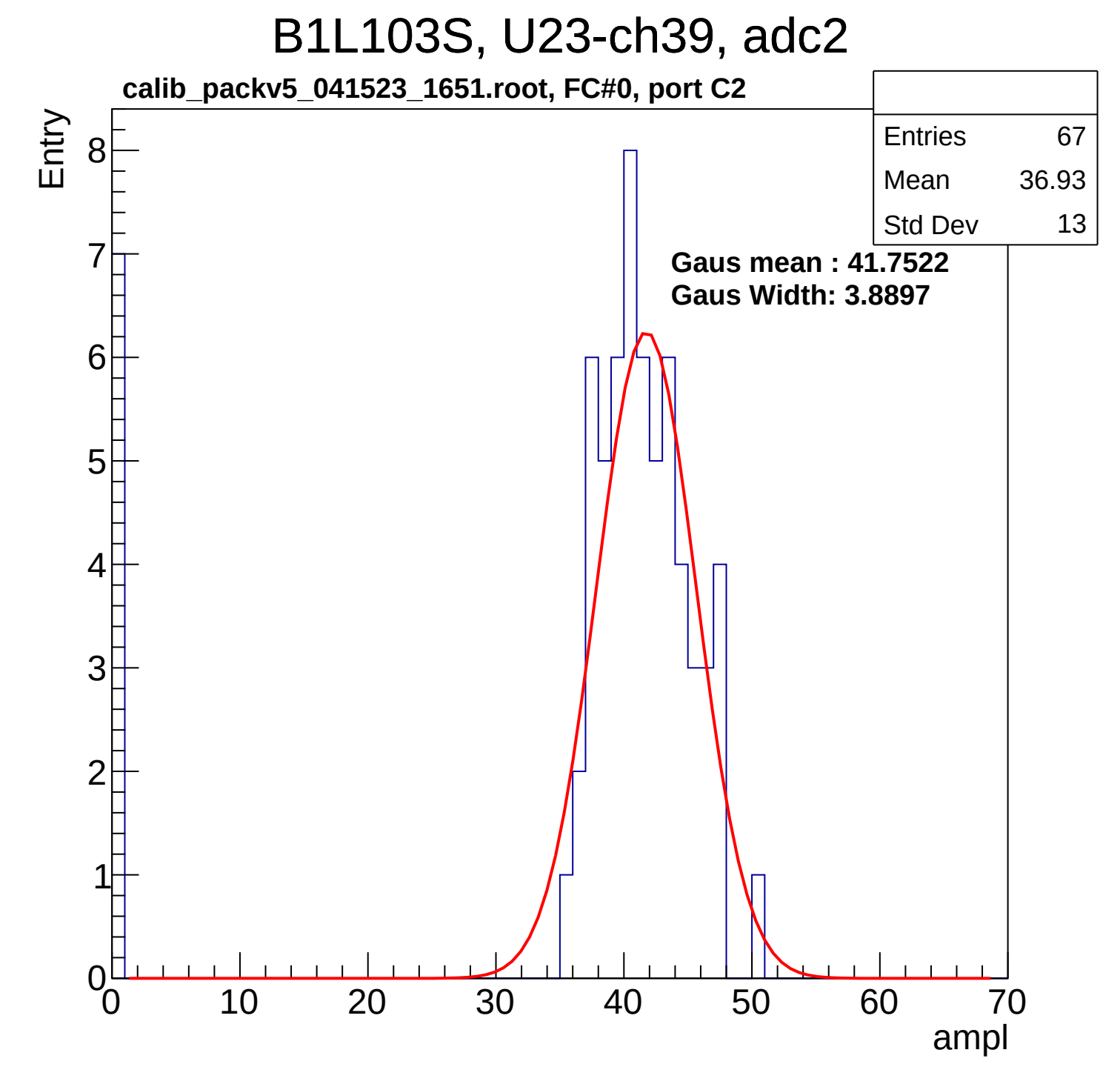

B1L103S, U23-ch38, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 57.98
Std Dev 8.054
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

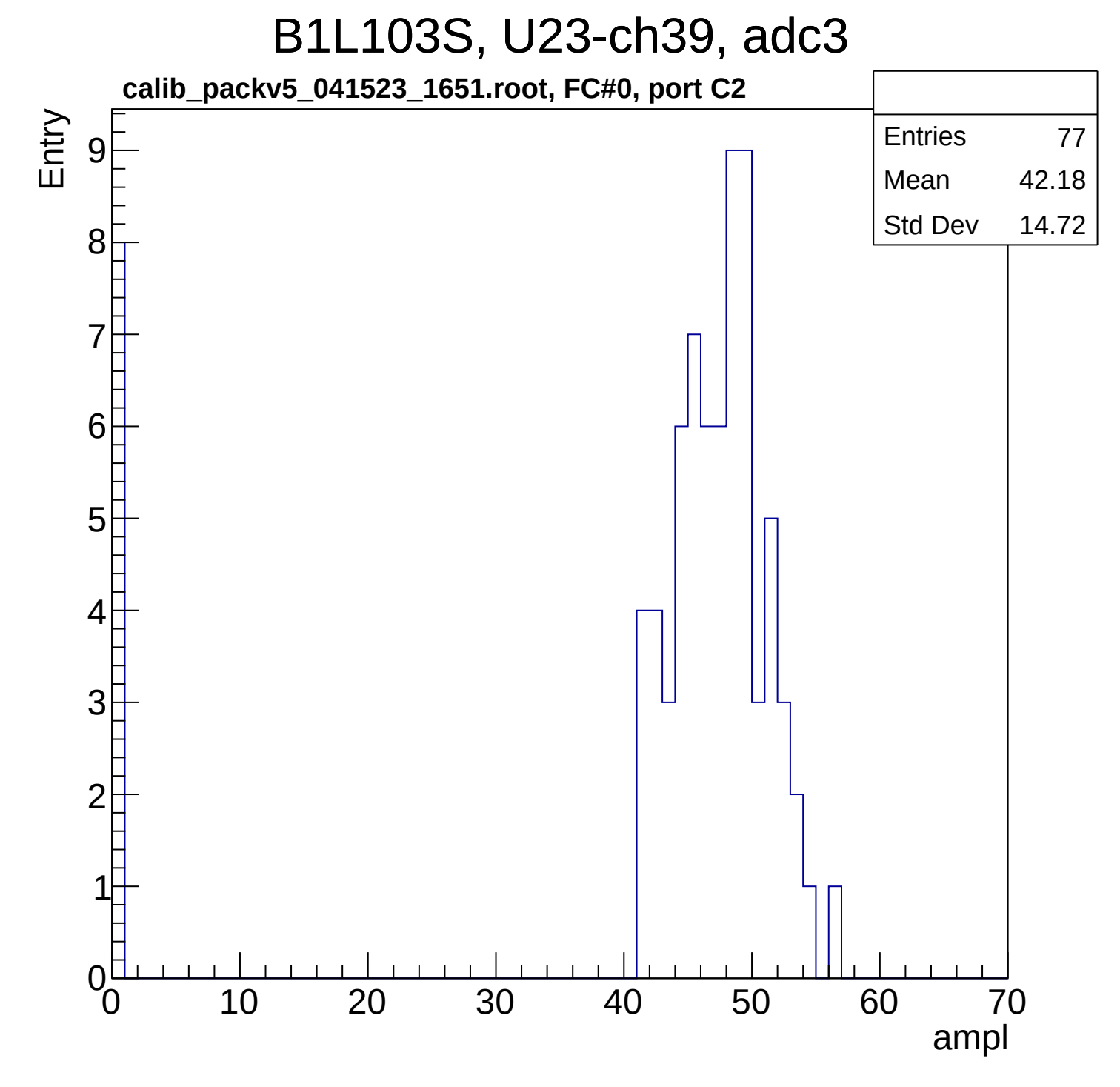

B1L103S, U23-ch38, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 61.18
Std Dev 1.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

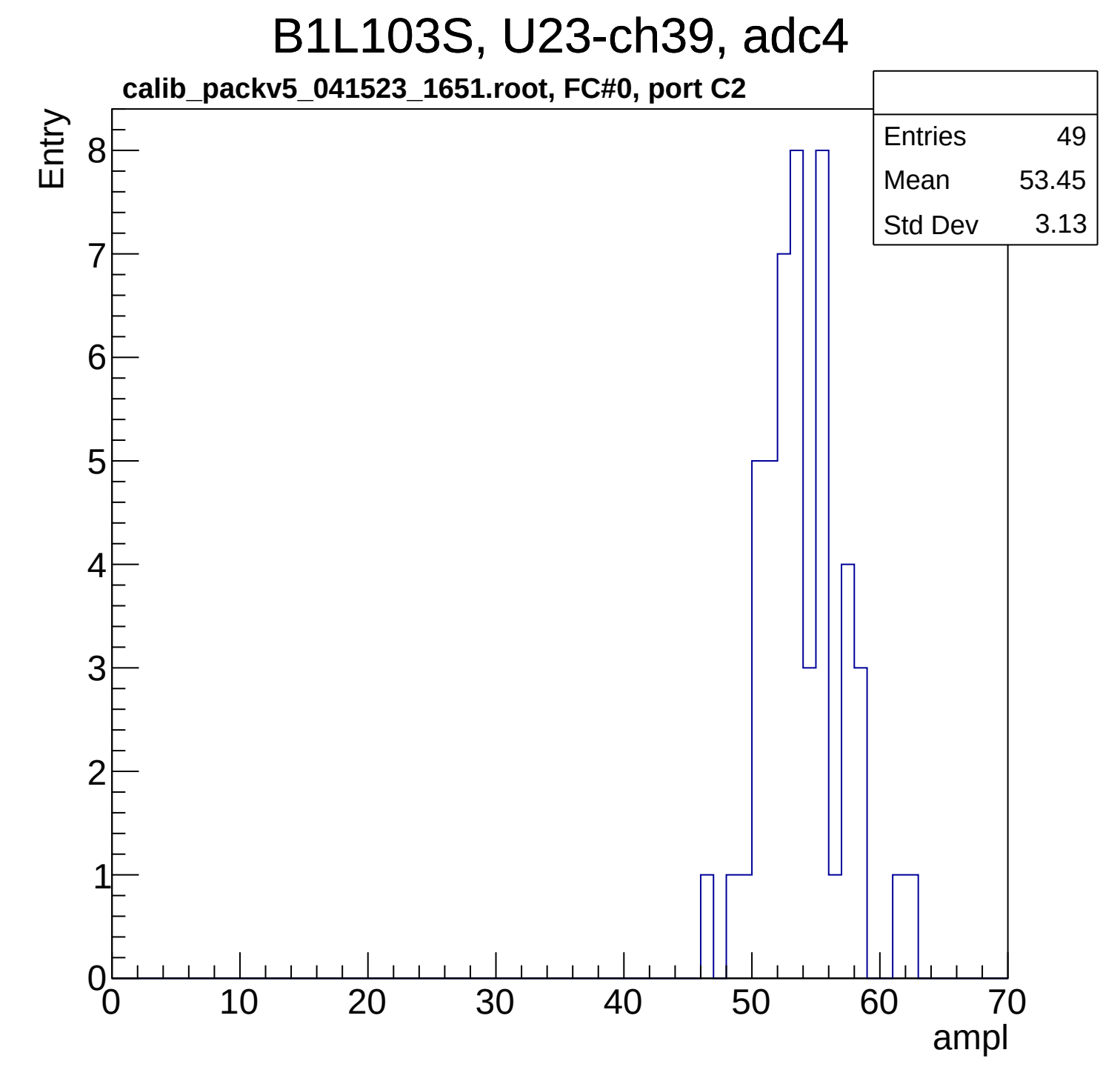

B1L103S, U23-ch38, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

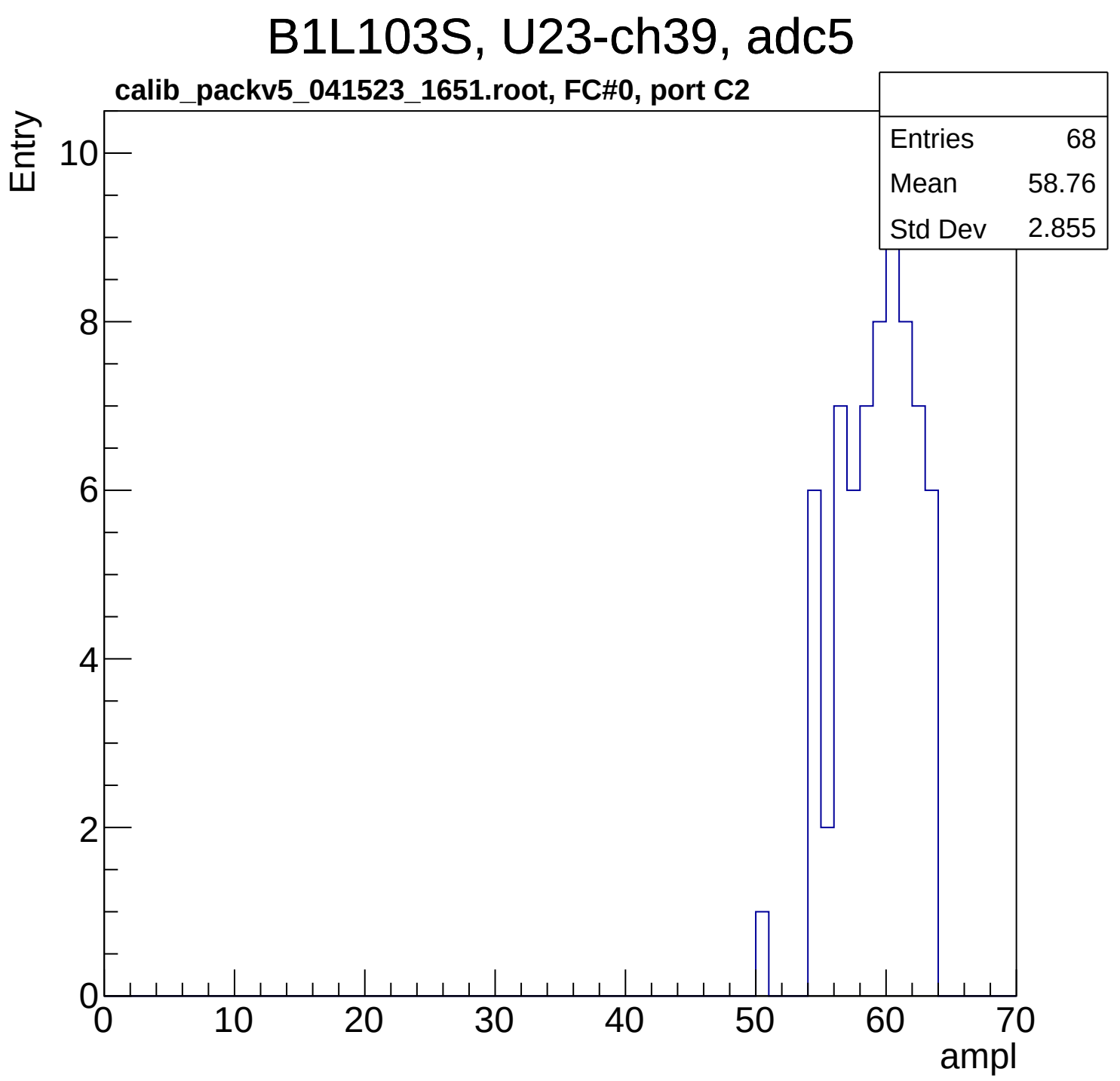

B1L103S, U23-ch39, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 22.1
Std Dev 10.56
Gaus mean : 26.7687
Gaus Width: 3.0818
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

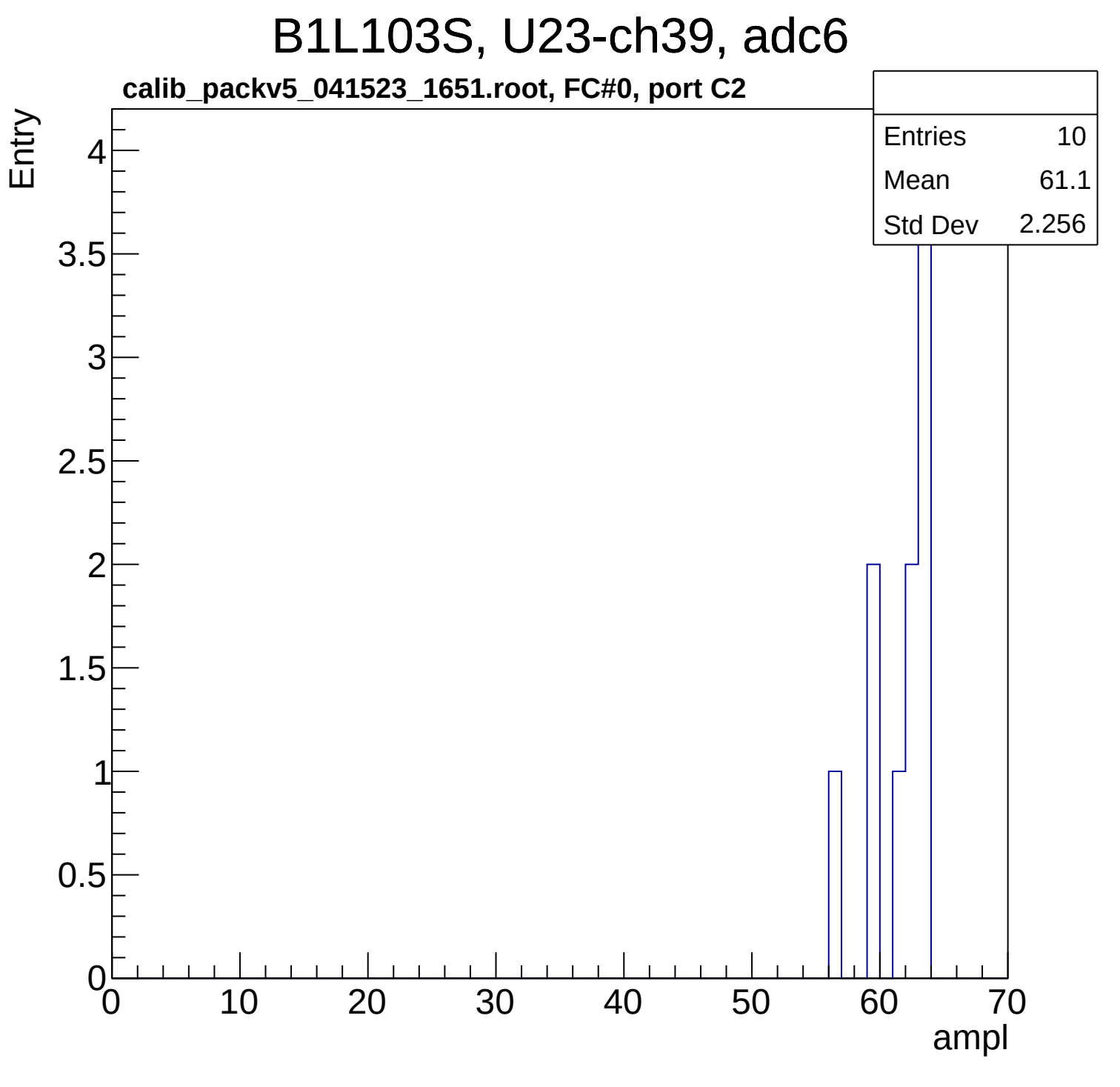

B1L103S, U23-ch39, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 32.8
Std Dev 6.213
Gaus mean : 33.8510
Gaus Width: 4.2118
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

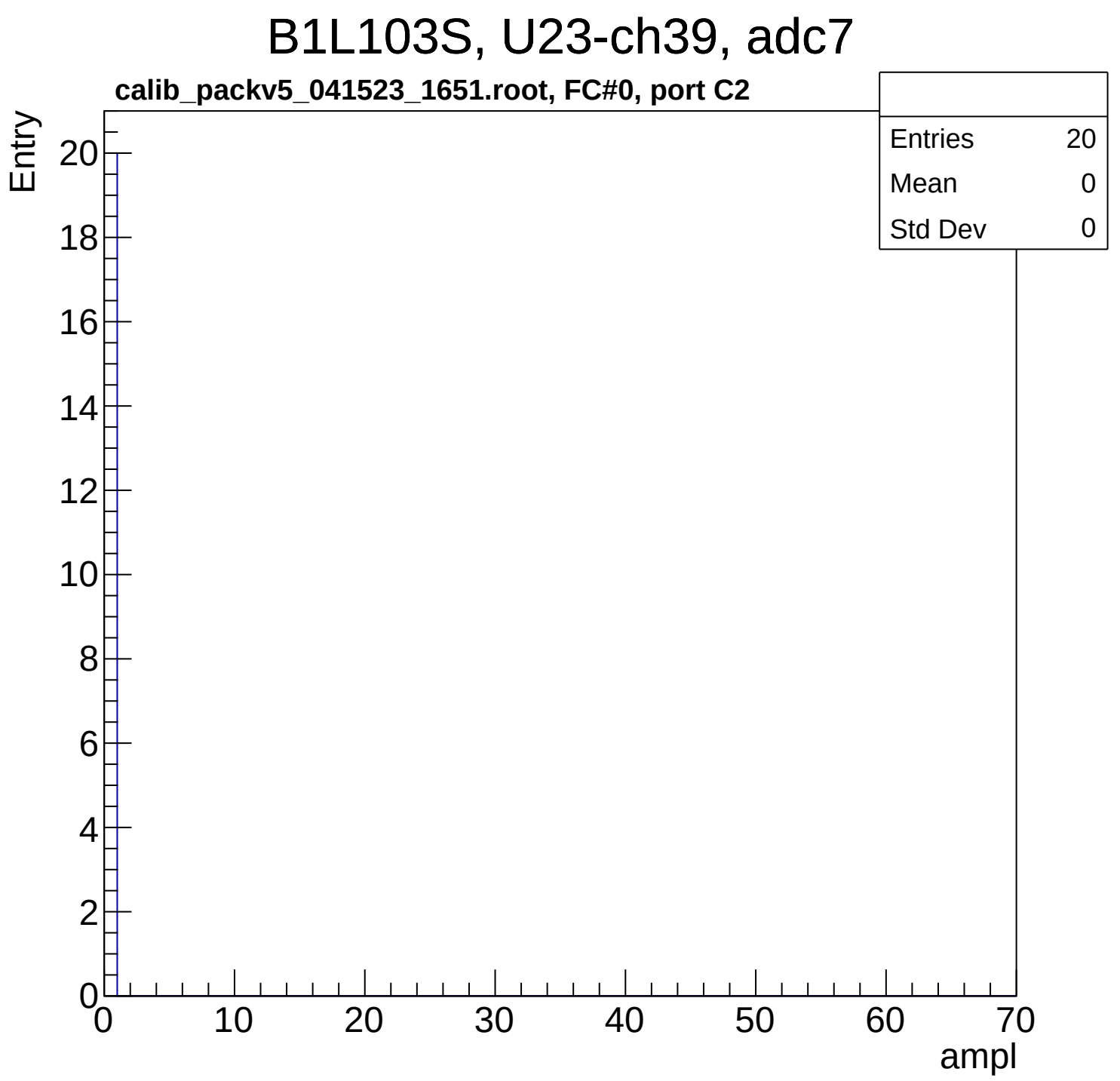

B1L103S, U23-ch39, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 36.93
Std Dev 13
Gaus mean : 41.7522
Gaus Width: 3.8897
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

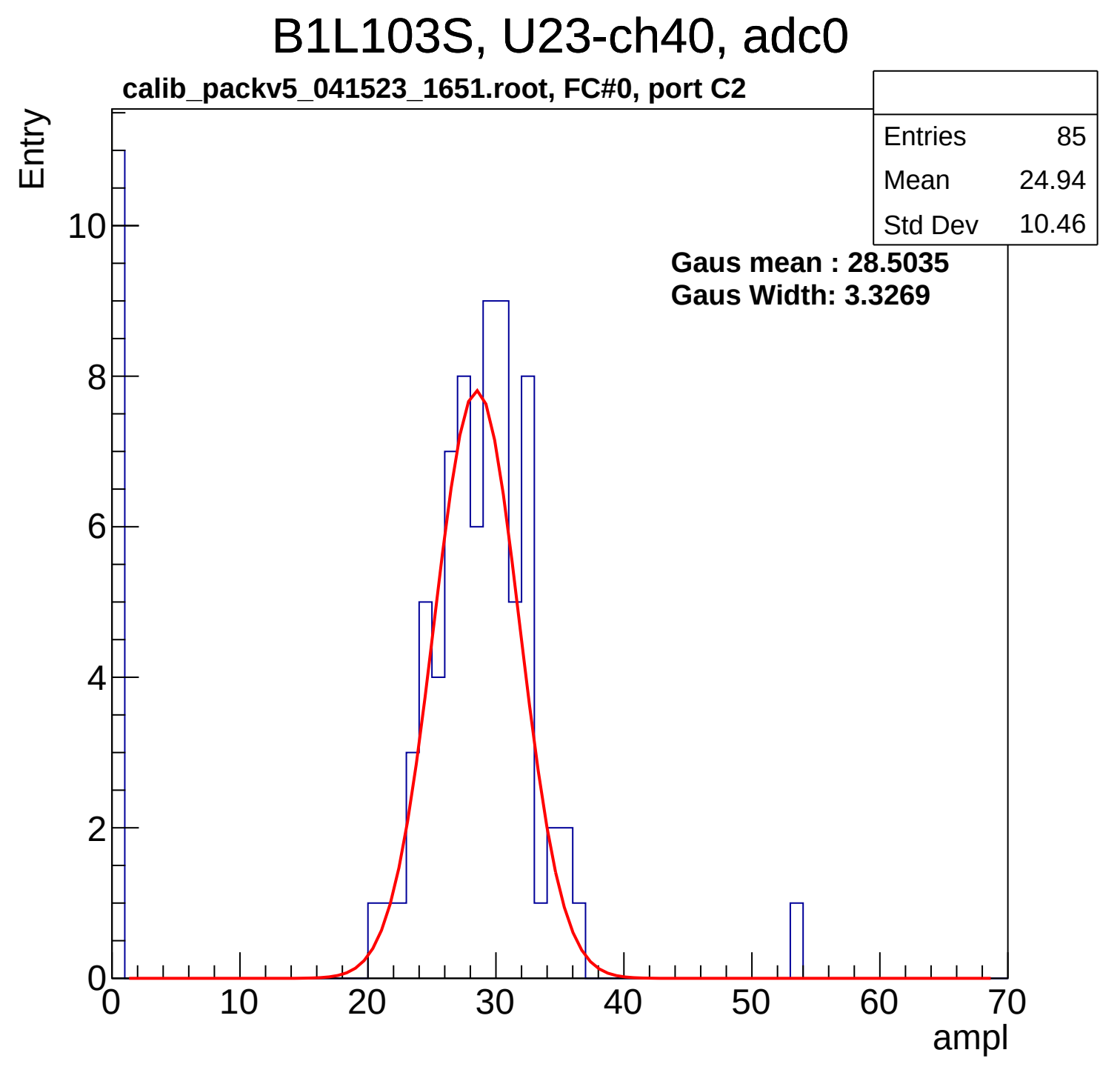

B1L103S, U23-ch39, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 42.18
Std Dev 14.72
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

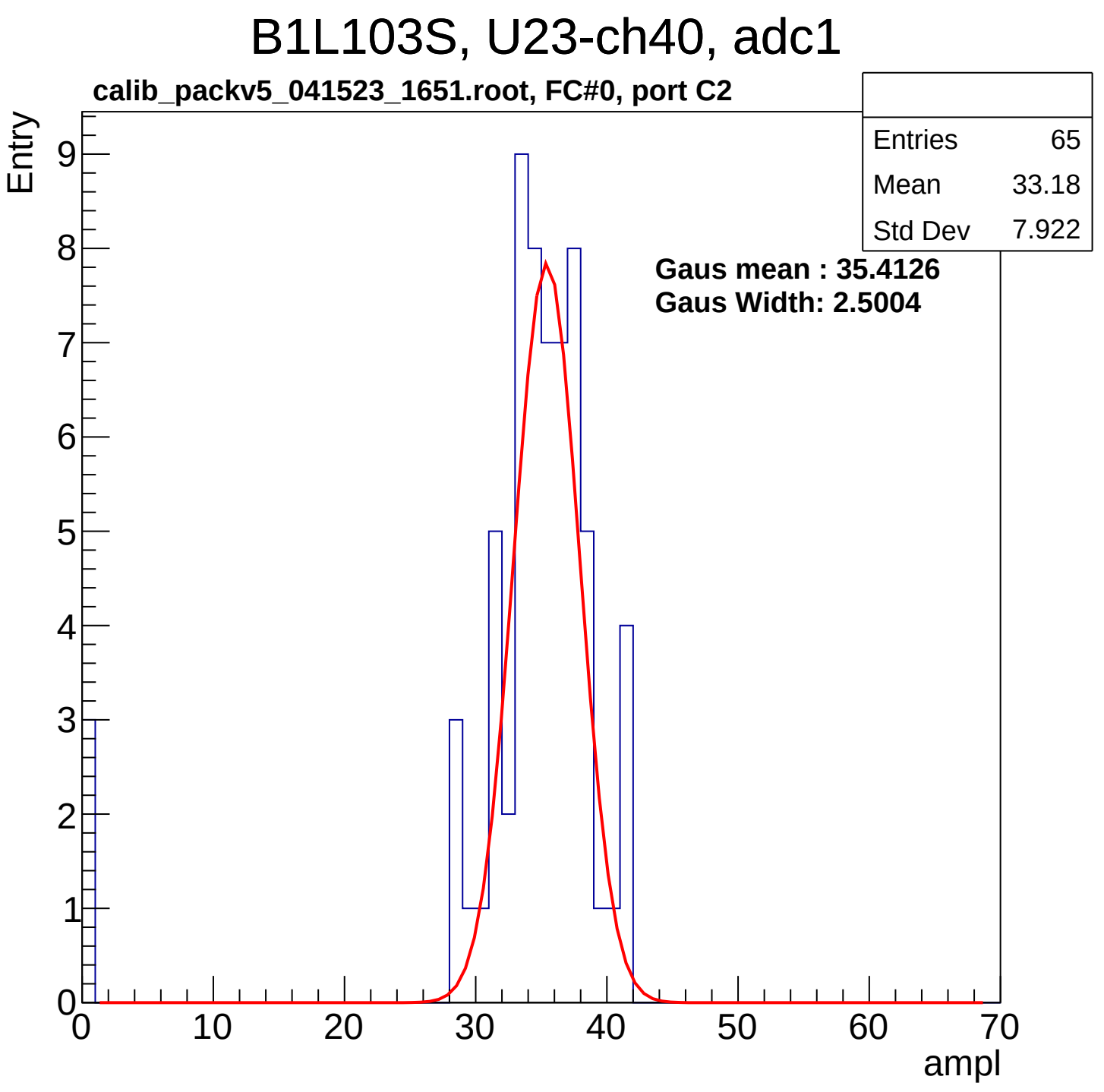

B1L103S, U23-ch39, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 53.45
Std Dev 3.13
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

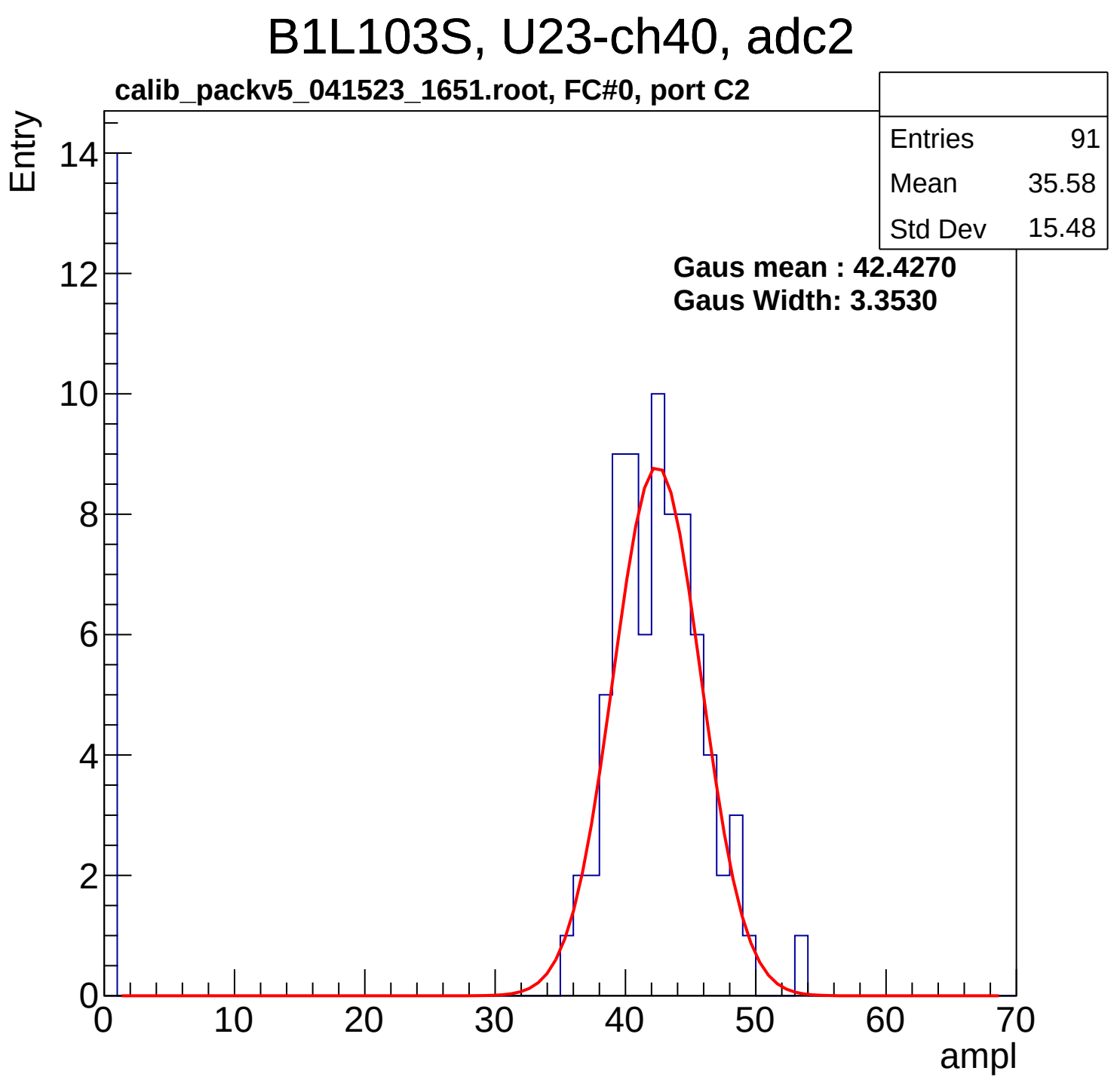

B1L103S, U23-ch39, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 58.76
Std Dev 2.855
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

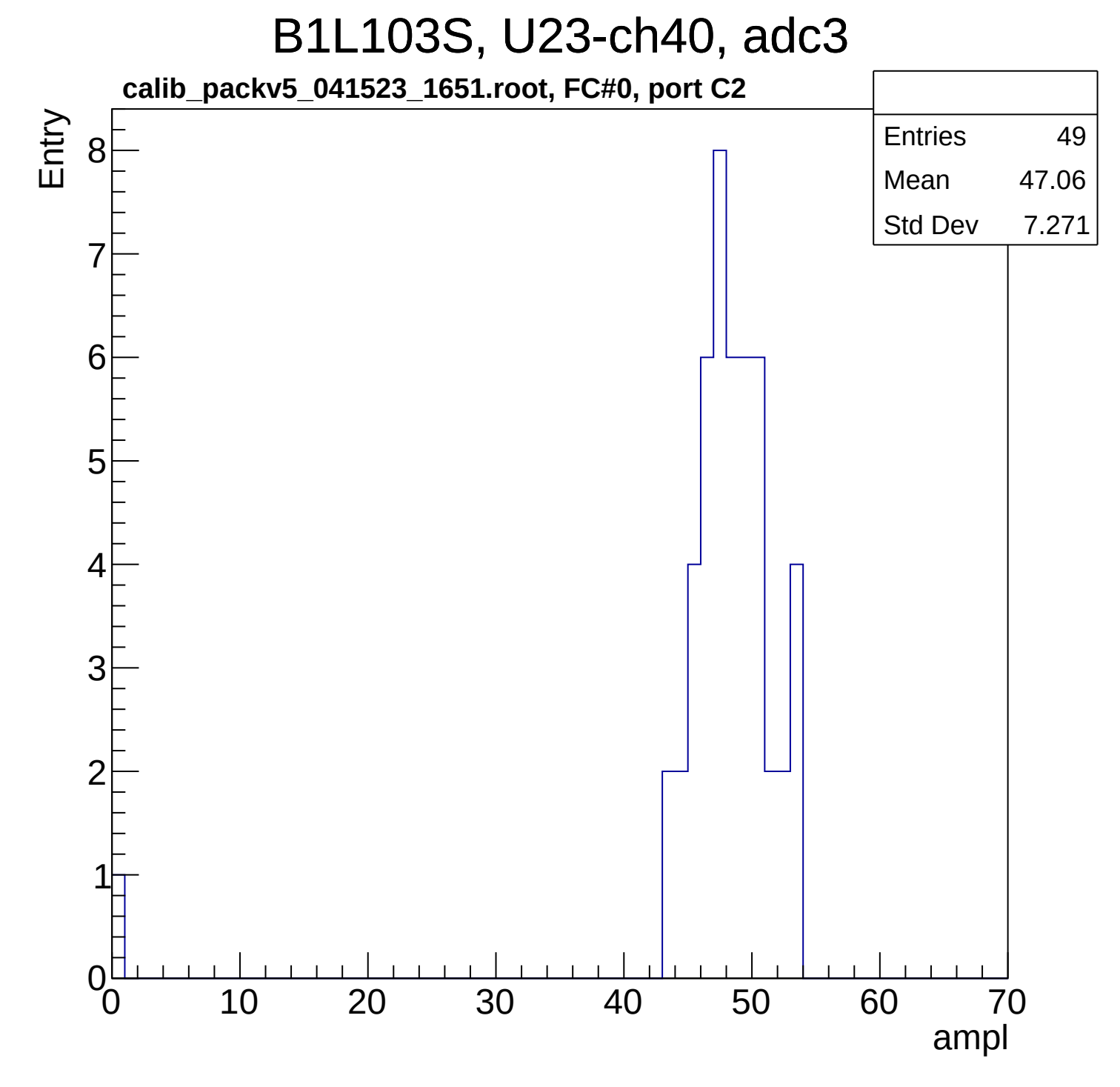

B1L103S, U23-ch39, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 61.1
Std Dev 2.256
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

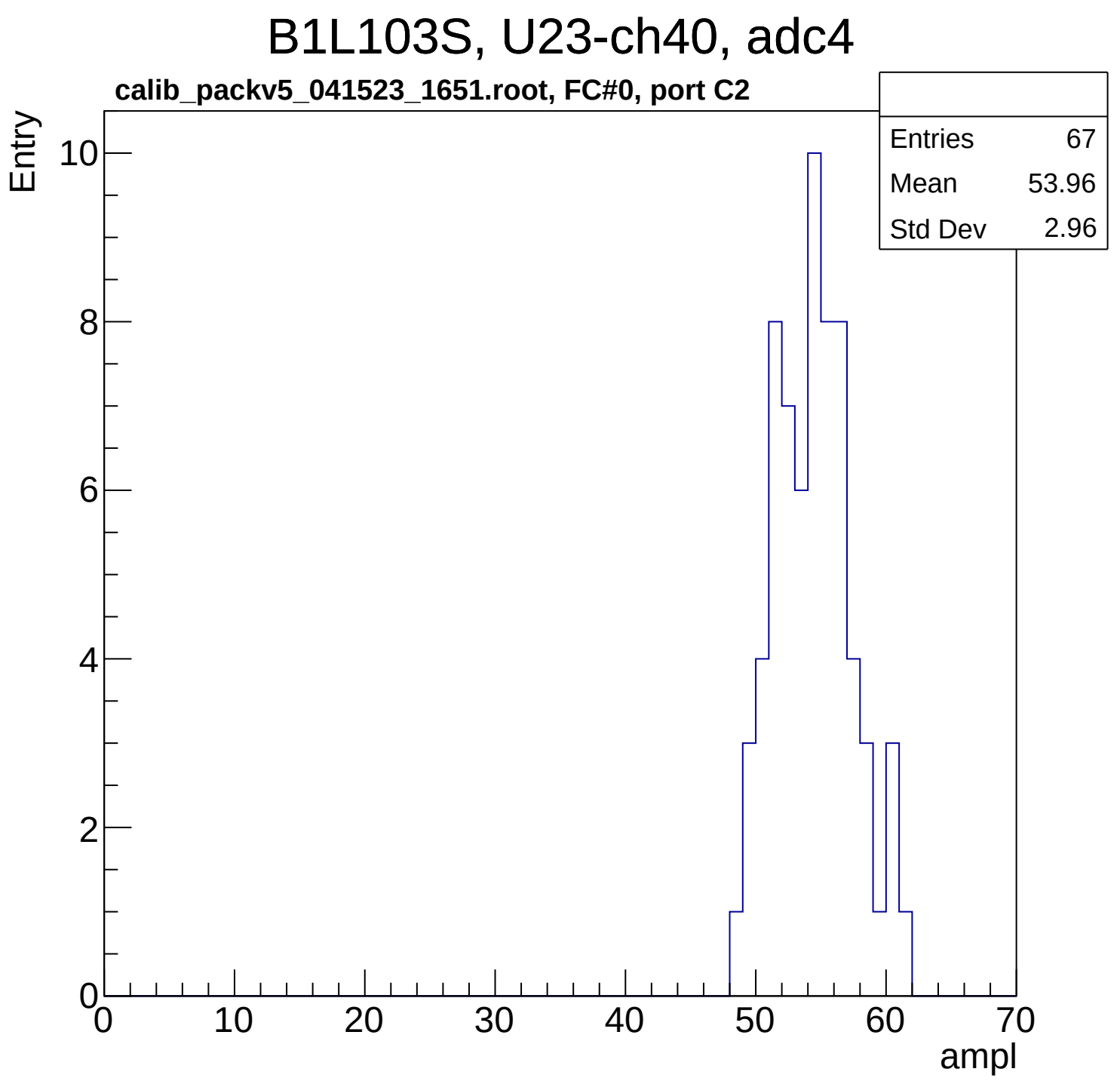

B1L103S, U23-ch39, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70
ampl

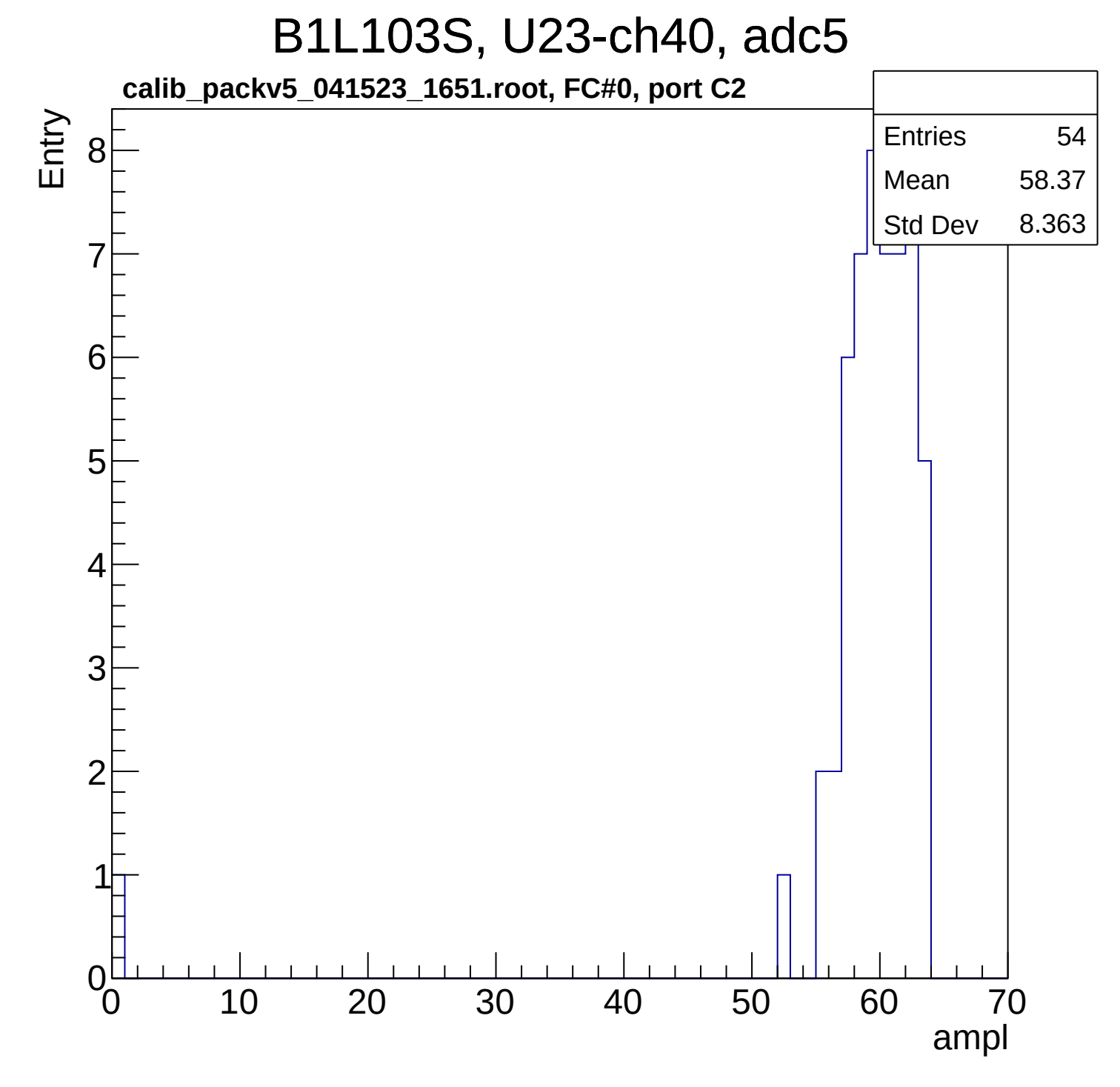

B1L103S, U23-ch40, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 24.94
Std Dev 10.46
Gaus mean : 28.5035
Gaus Width: 3.3269
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

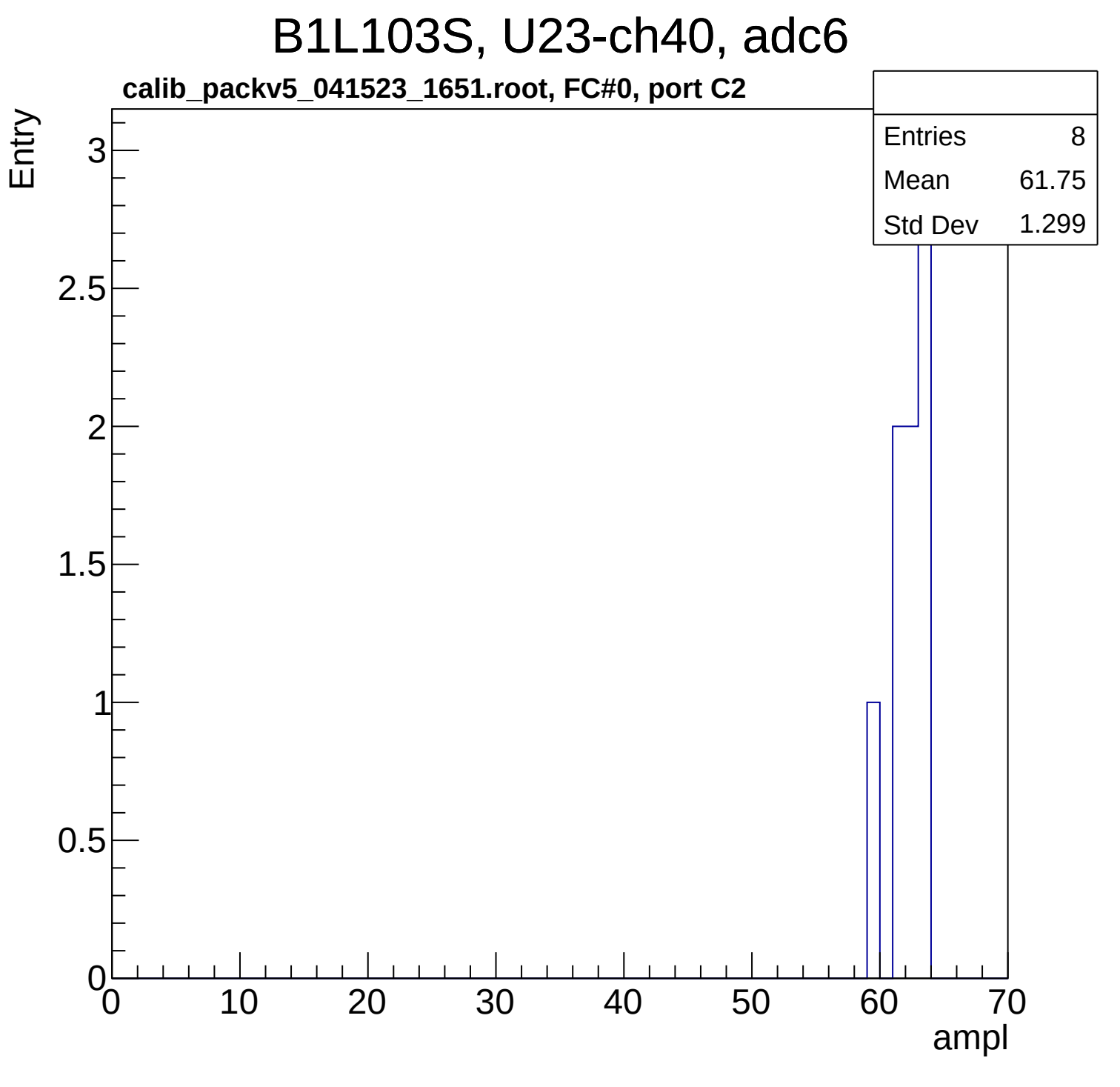

B1L103S, U23-ch40, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 33.18
Std Dev 7.922
Gaus mean : 35.4126
Gaus Width: 2.5004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

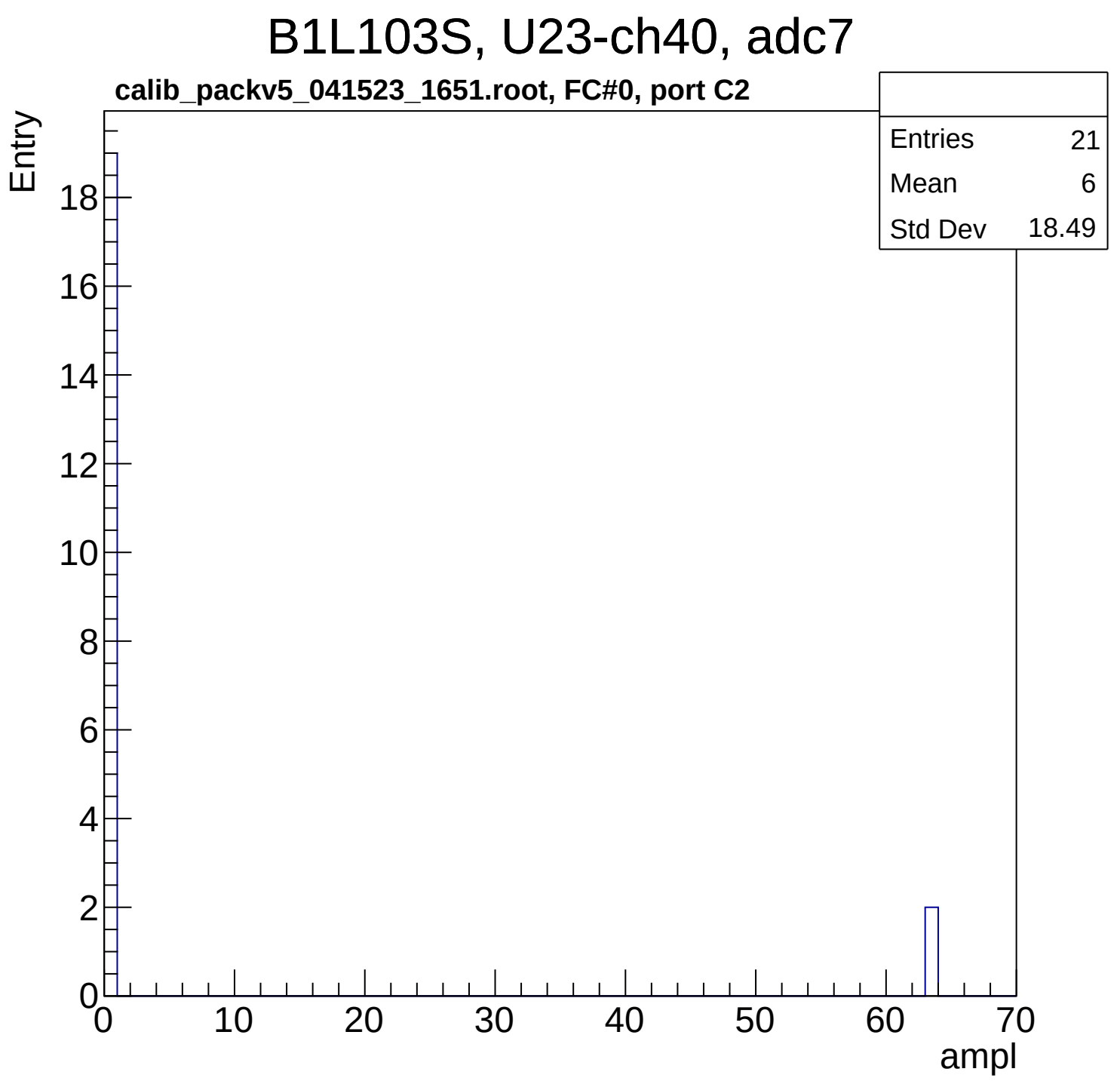

B1L103S, U23-ch40, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 35.58
Std Dev 15.48
Gaus mean : 42.4270
Gaus Width: 3.3530
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

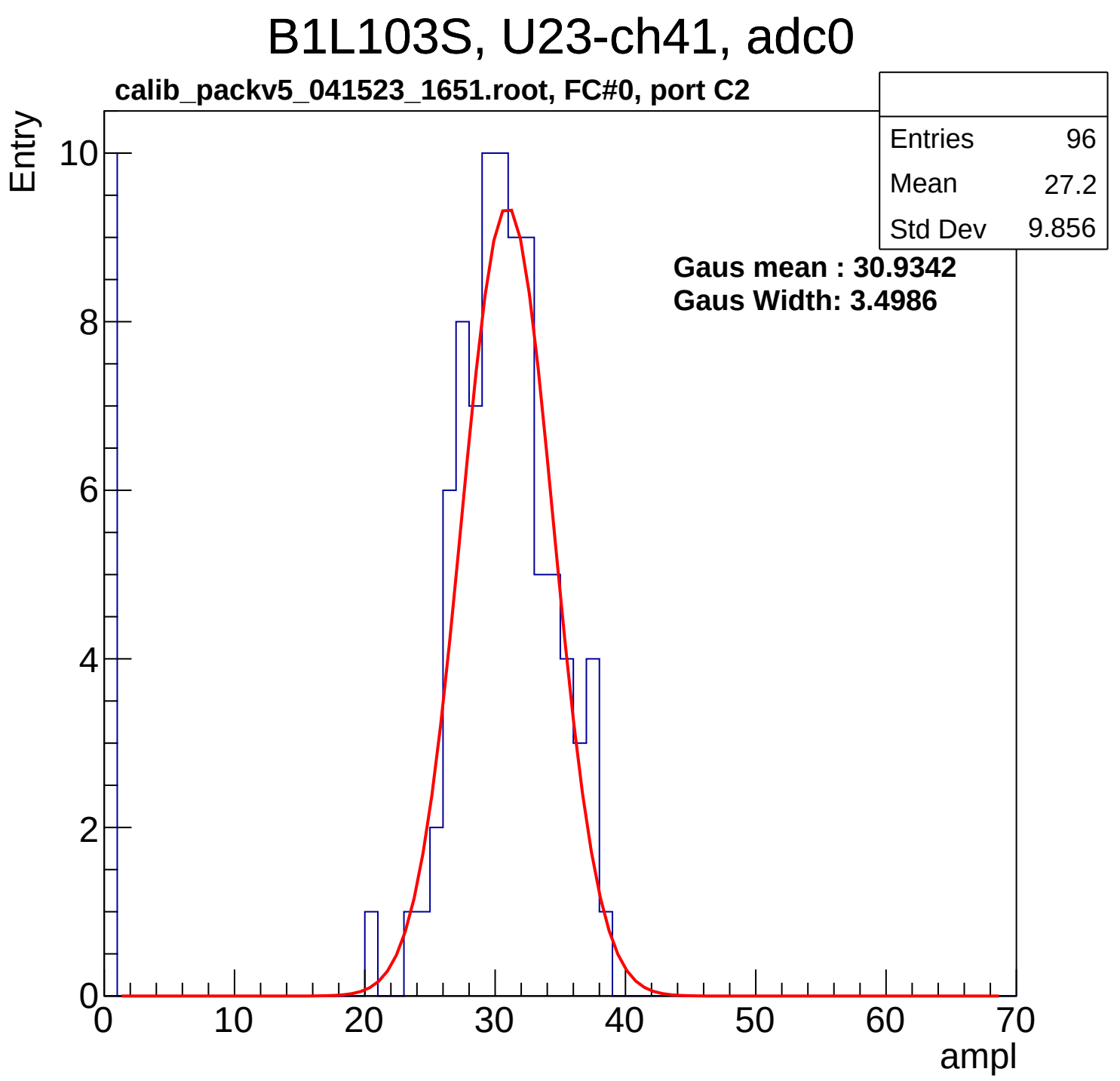

B1L103S, U23-ch40, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 47.06
Std Dev 7.271
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

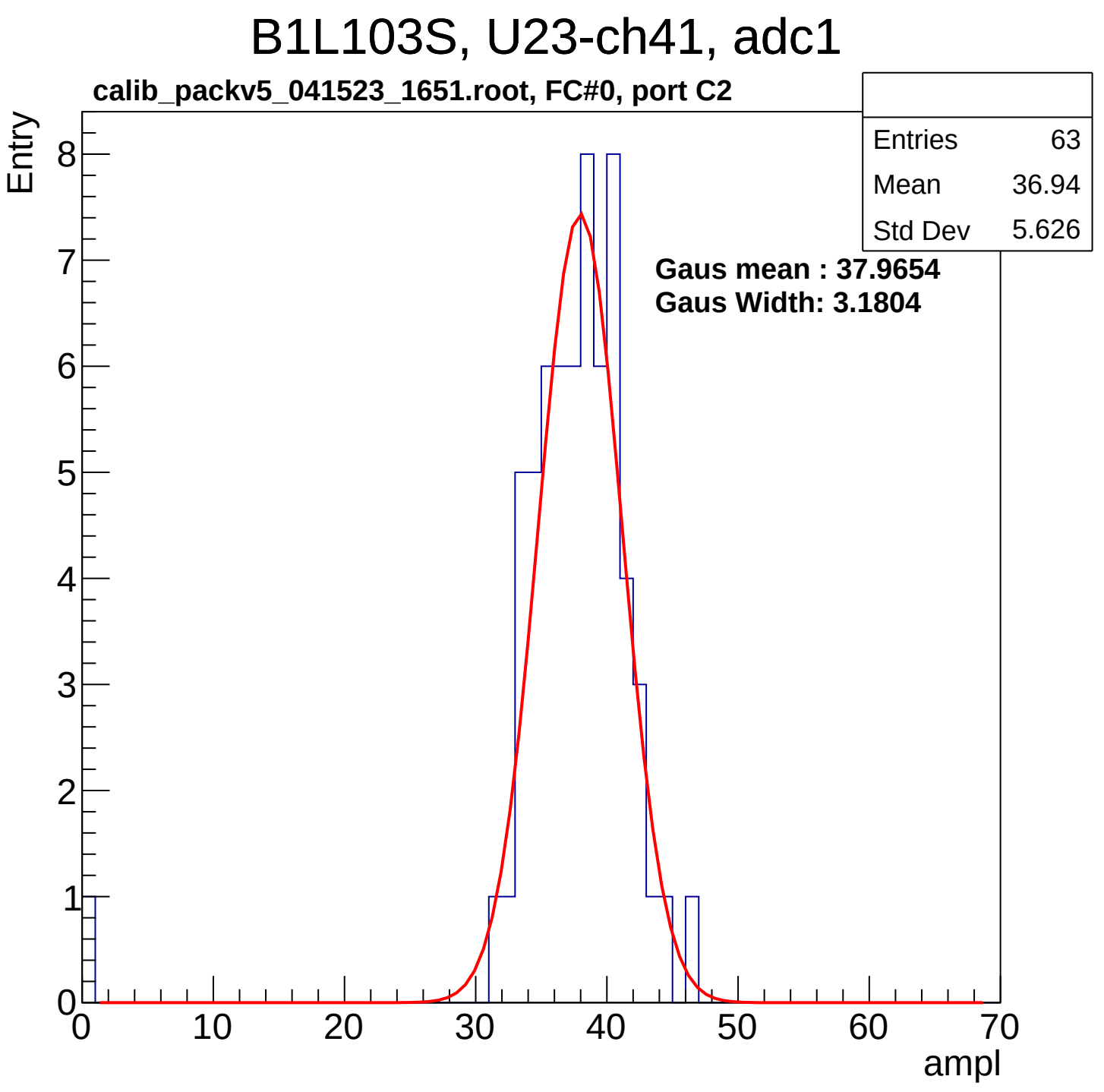

B1L103S, U23-ch40, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 53.96
Std Dev 2.96
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

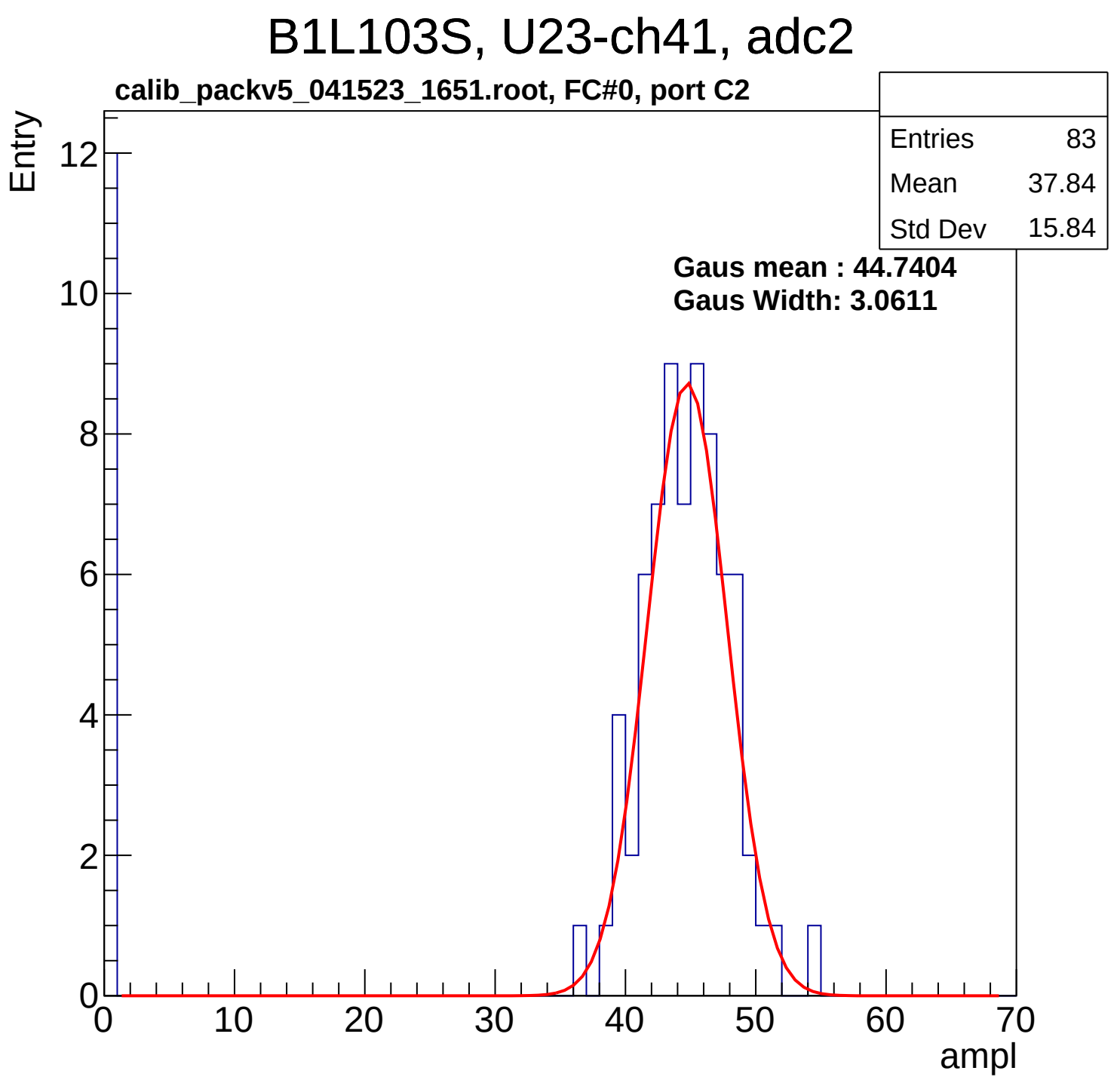

B1L103S, U23-ch40, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 58.37
Std Dev 8.363
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

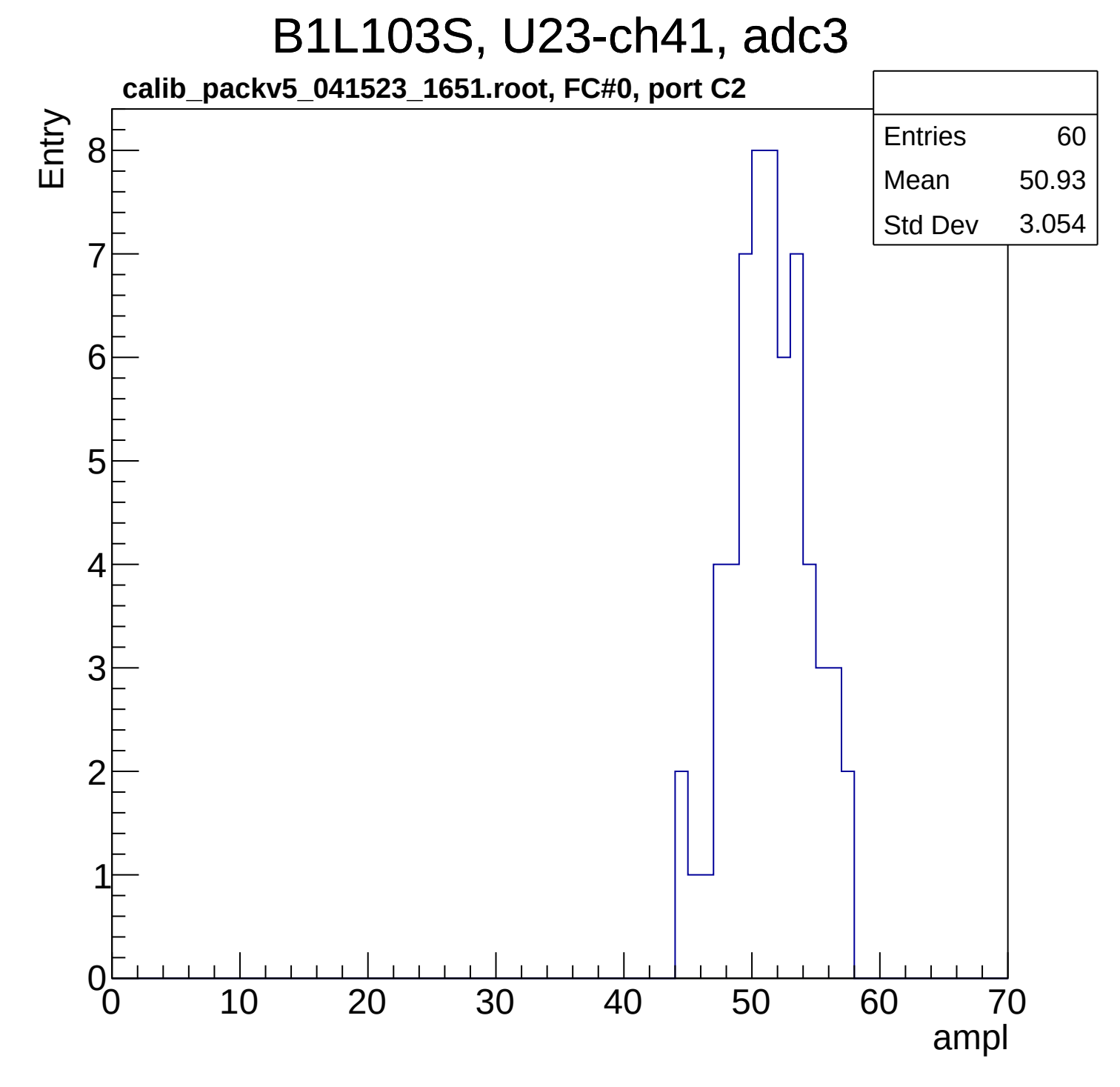

B1L103S, U23-ch40, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

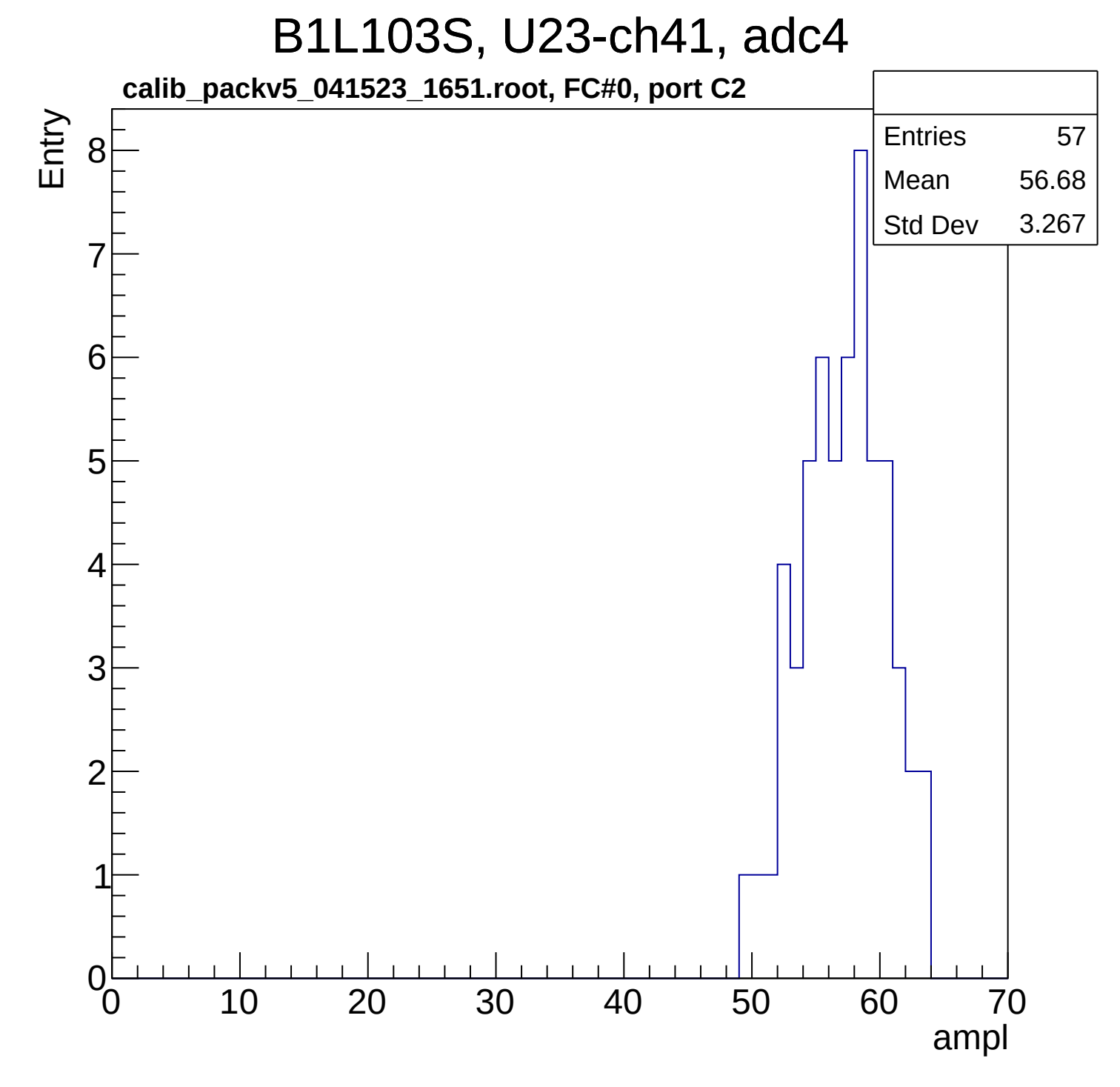

B1L103S, U23-ch40, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 6
Std Dev 18.49
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

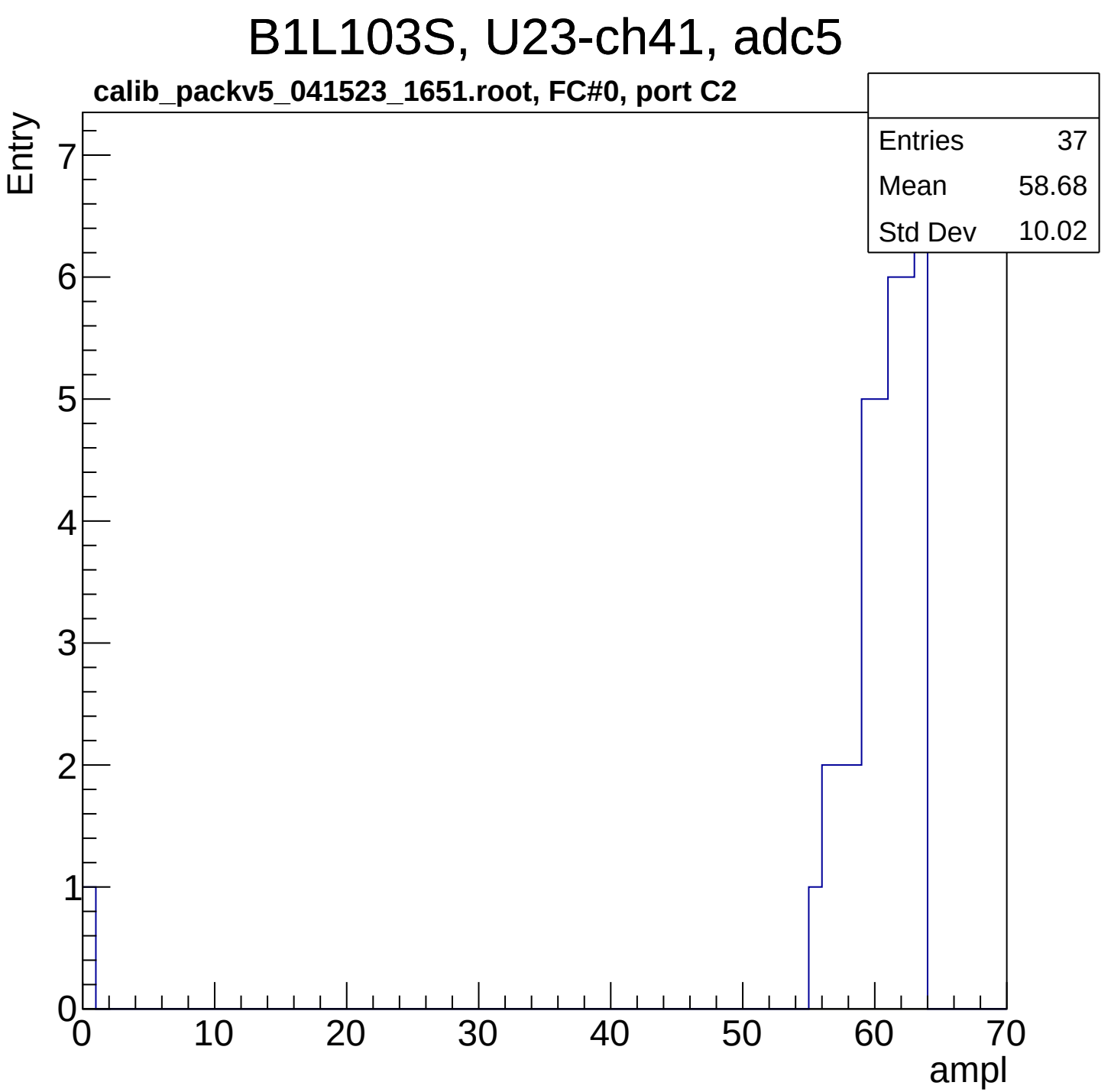

B1L103S, U23-ch41, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 96
Mean 27.2
Std Dev 9.856
Gaus mean : 30.9342
Gaus Width: 3.4986
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

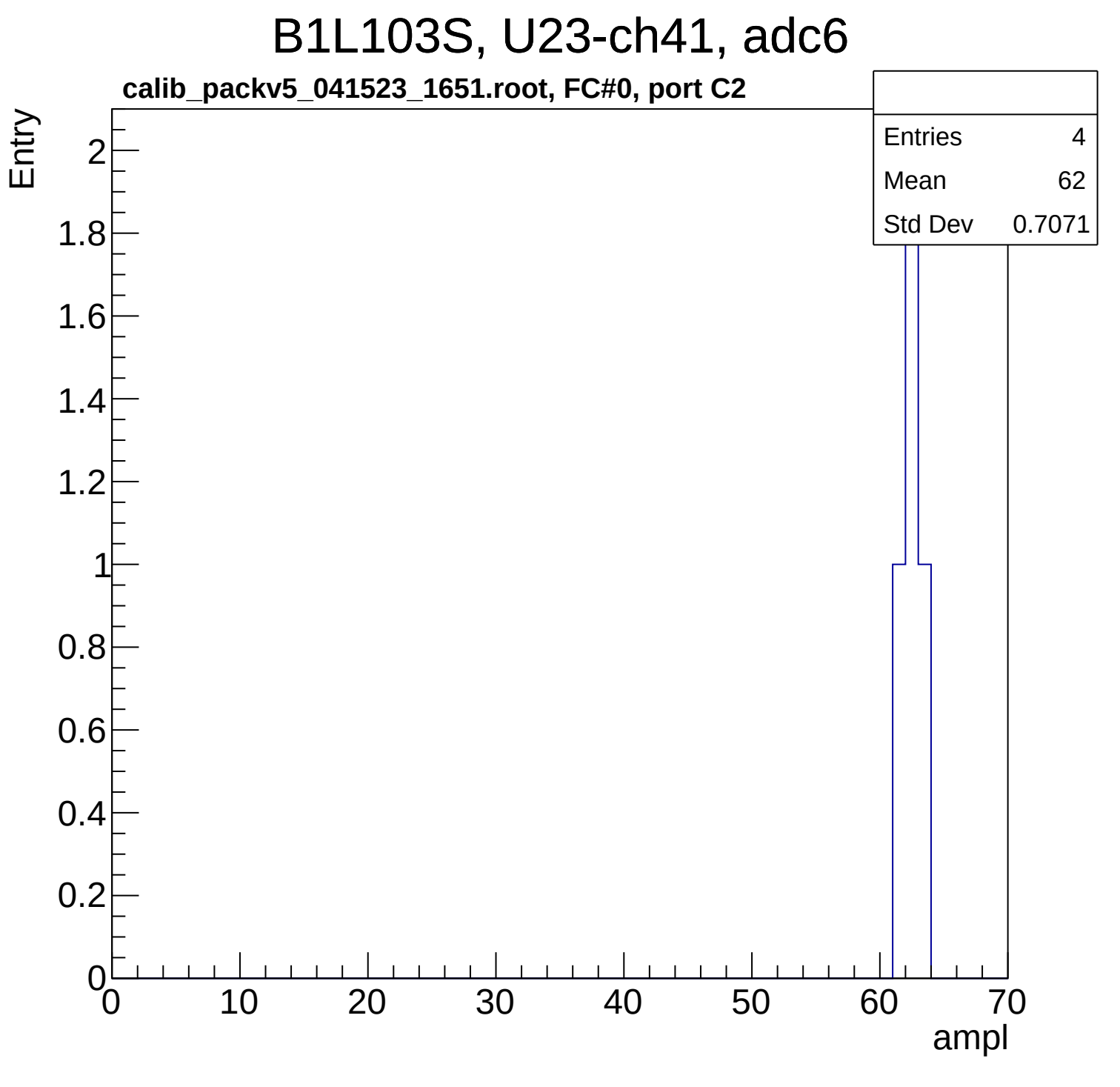

B1L103S, U23-ch41, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 36.94
Std Dev 5.626
Gaus mean : 37.9654
Gaus Width: 3.1804
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

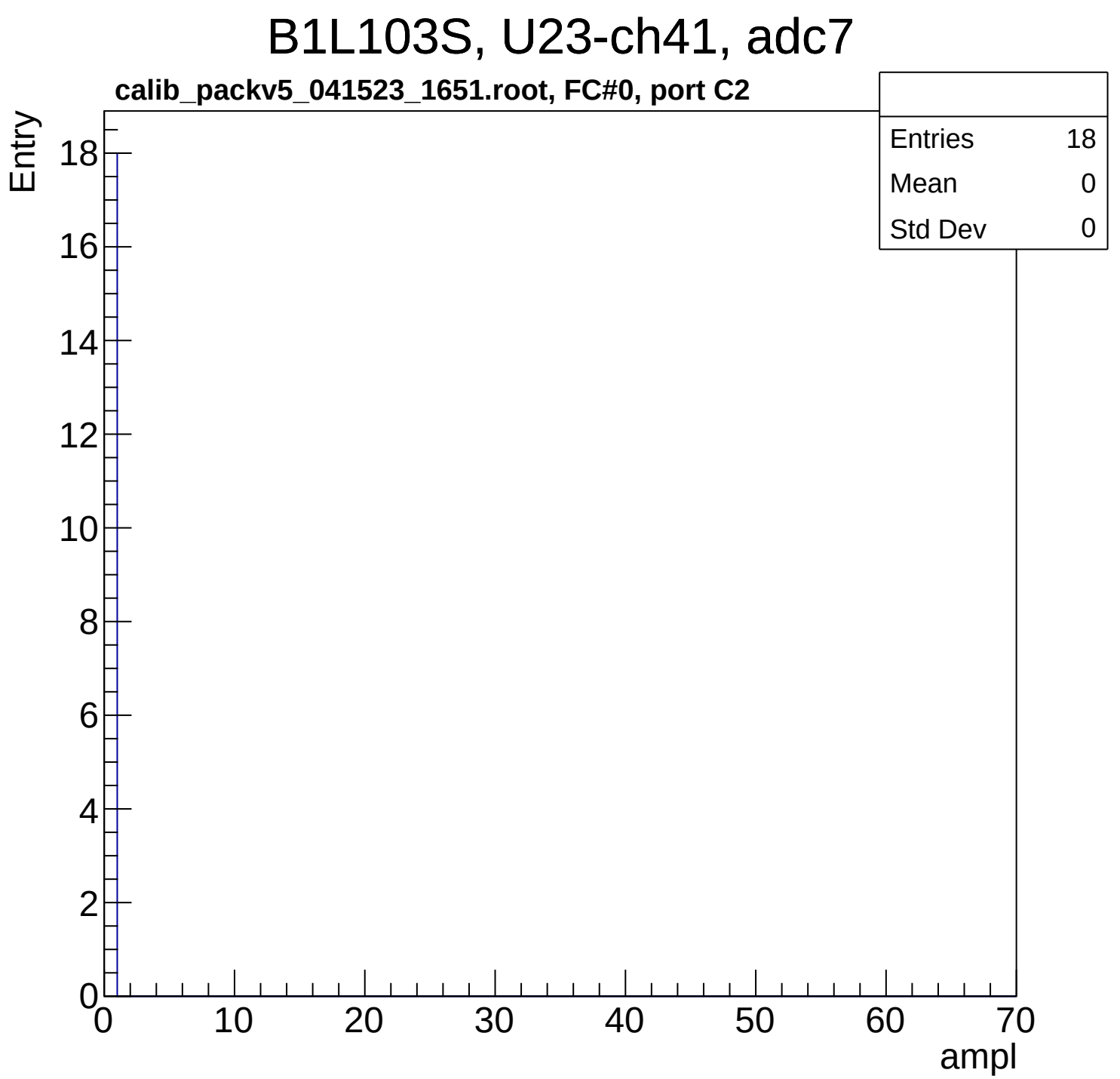

B1L103S, U23-ch41, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 37.84
Std Dev 15.84
Gaus mean : 44.7404
Gaus Width: 3.0611
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

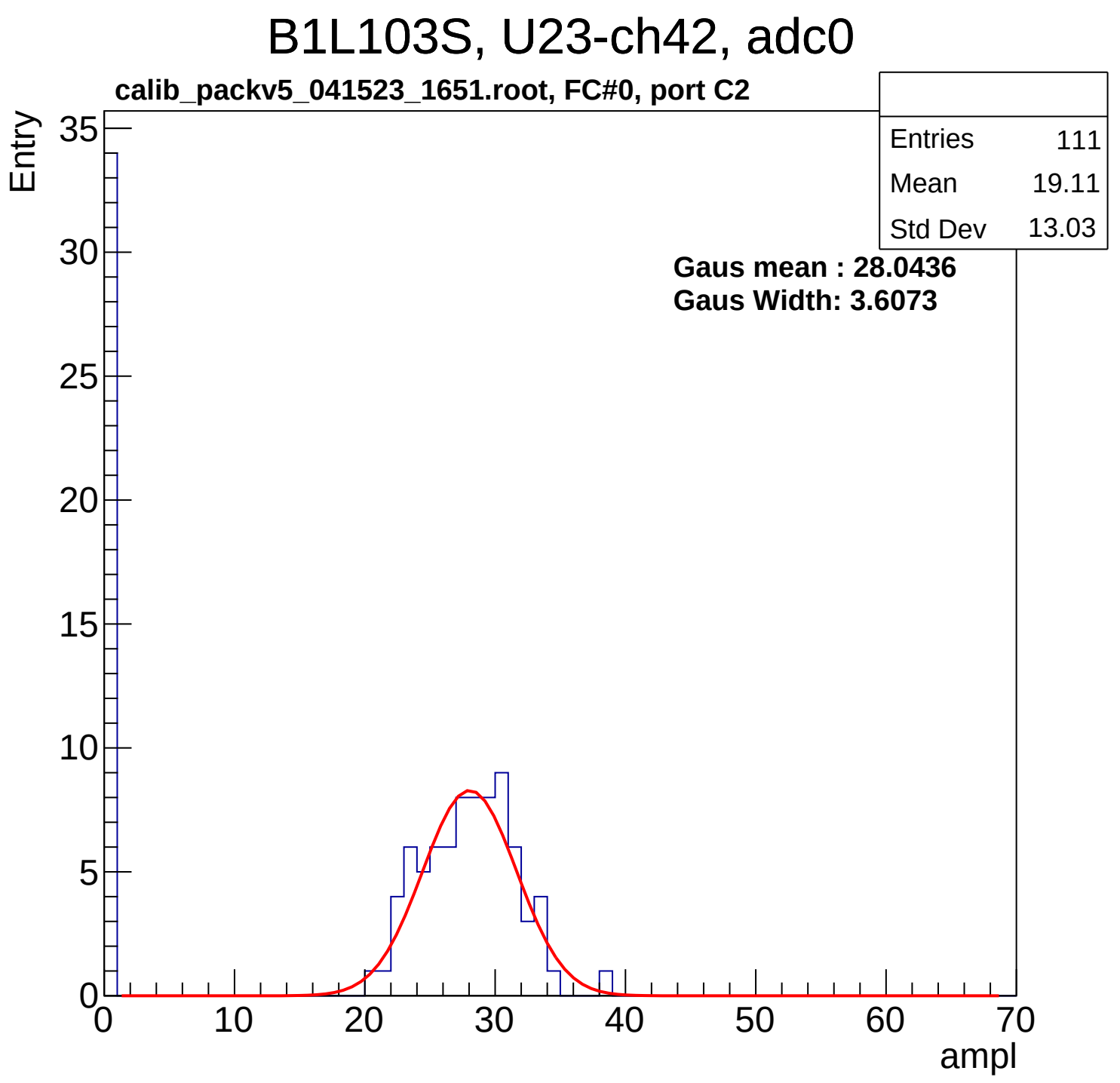

B1L103S, U23-ch41, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 50.93
Std Dev 3.054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

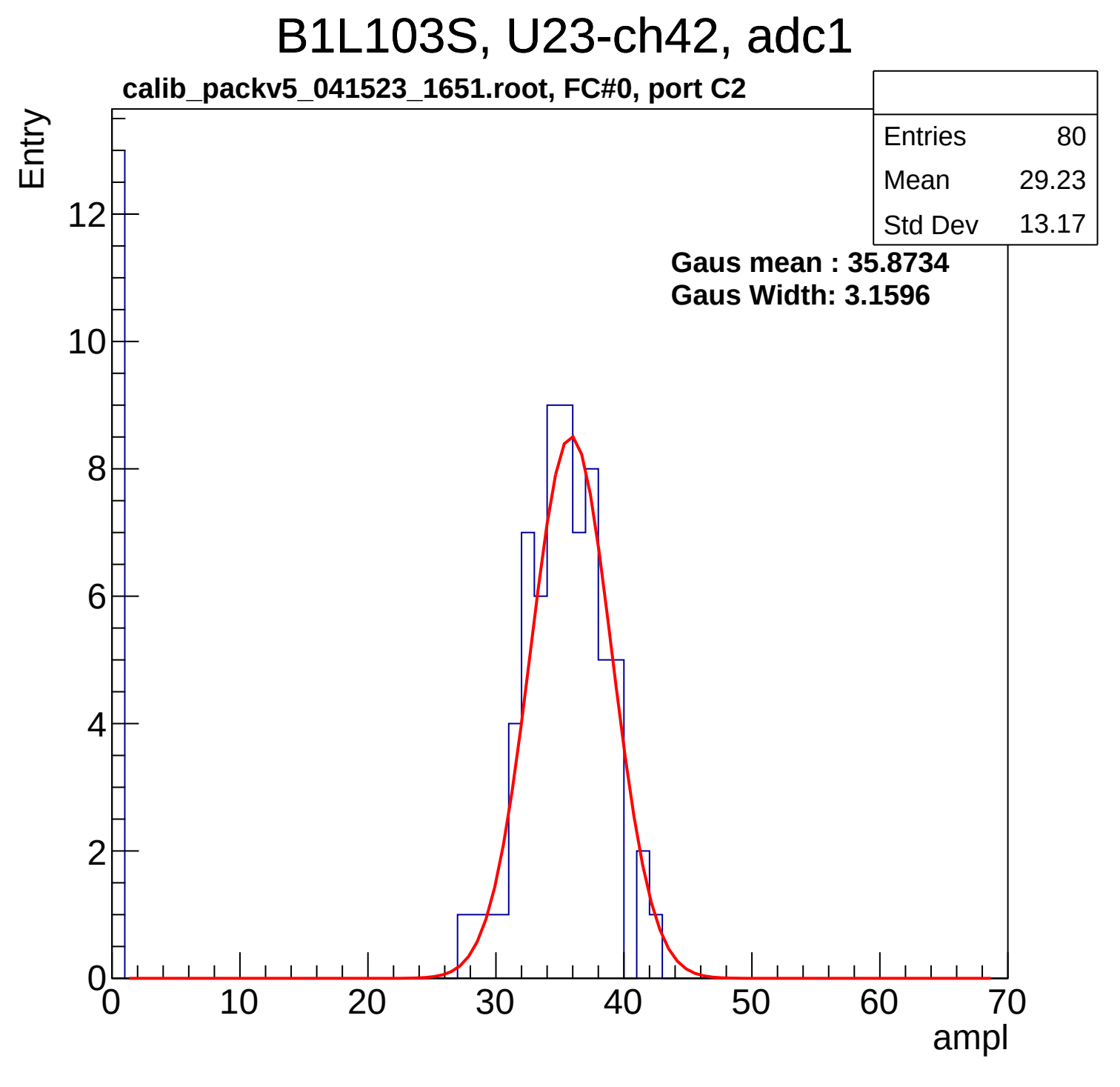

B1L103S, U23-ch41, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 56.68
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

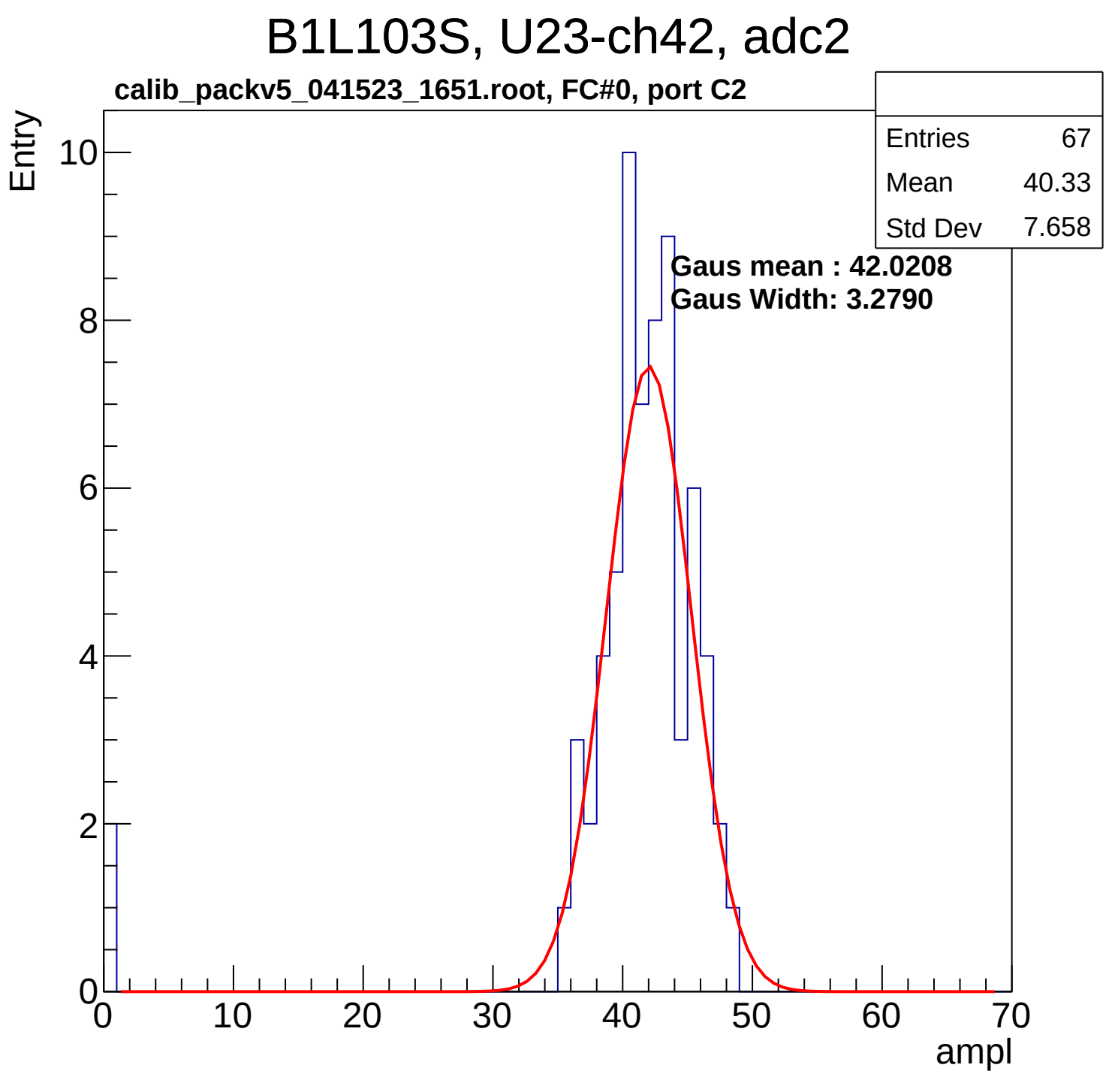

B1L103S, U23-ch41, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 37
Mean 58.68
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

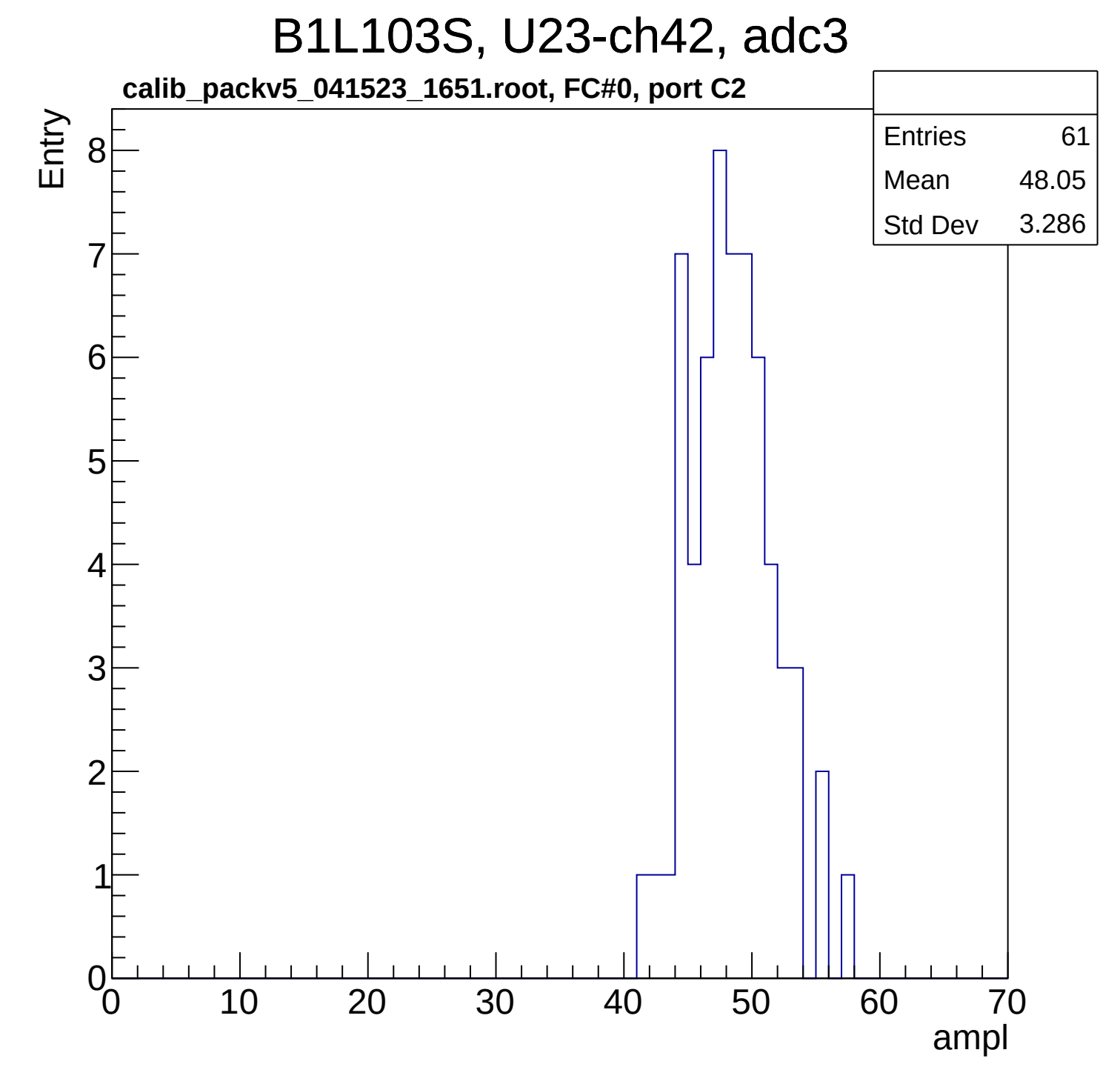

B1L103S, U23-ch41, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

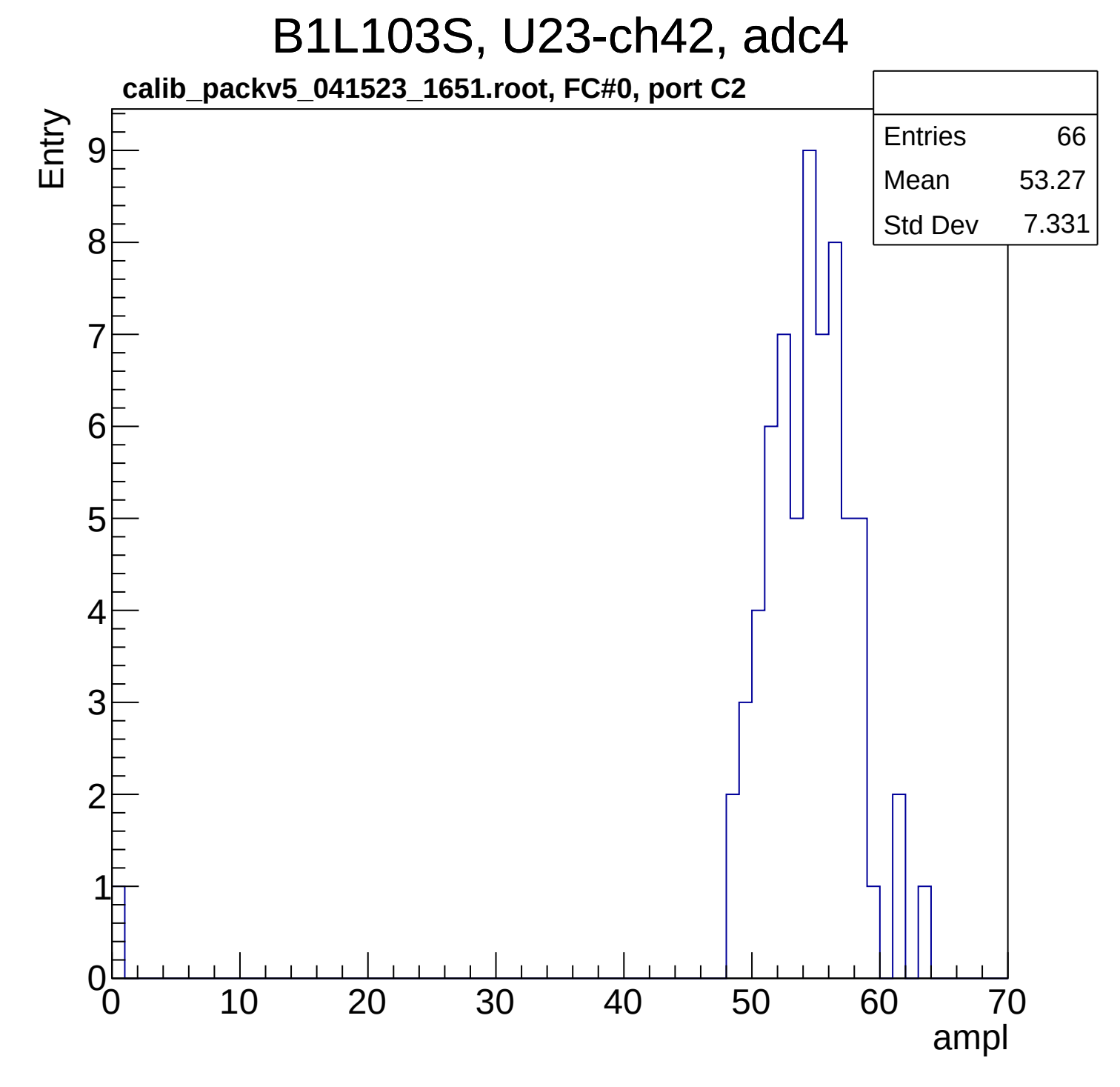

B1L103S, U23-ch41, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

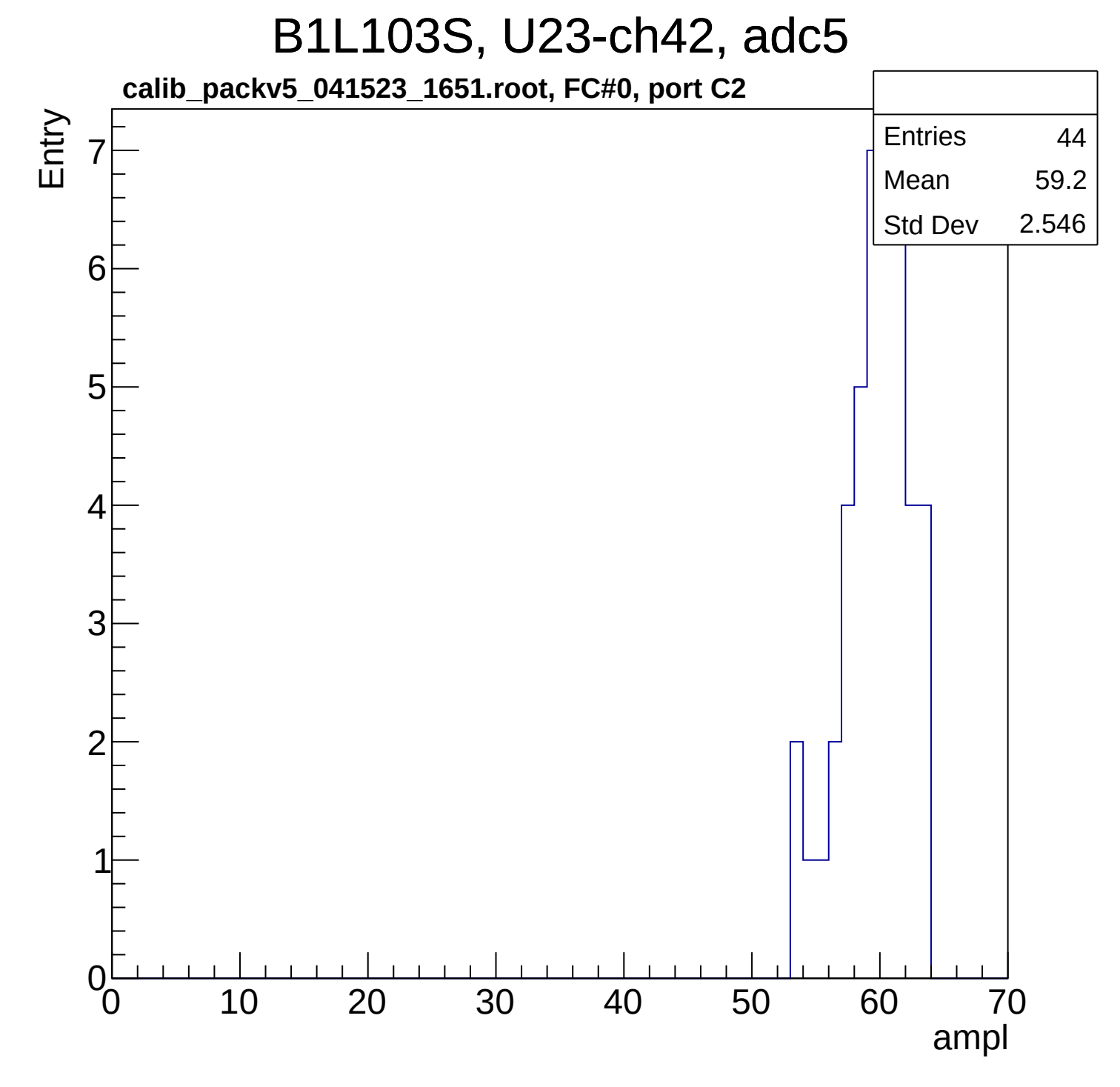

B1L103S, U23-ch42, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 111
Mean 19.11
Std Dev 13.03
Gaus mean : 28.0436
Gaus Width: 3.6073
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

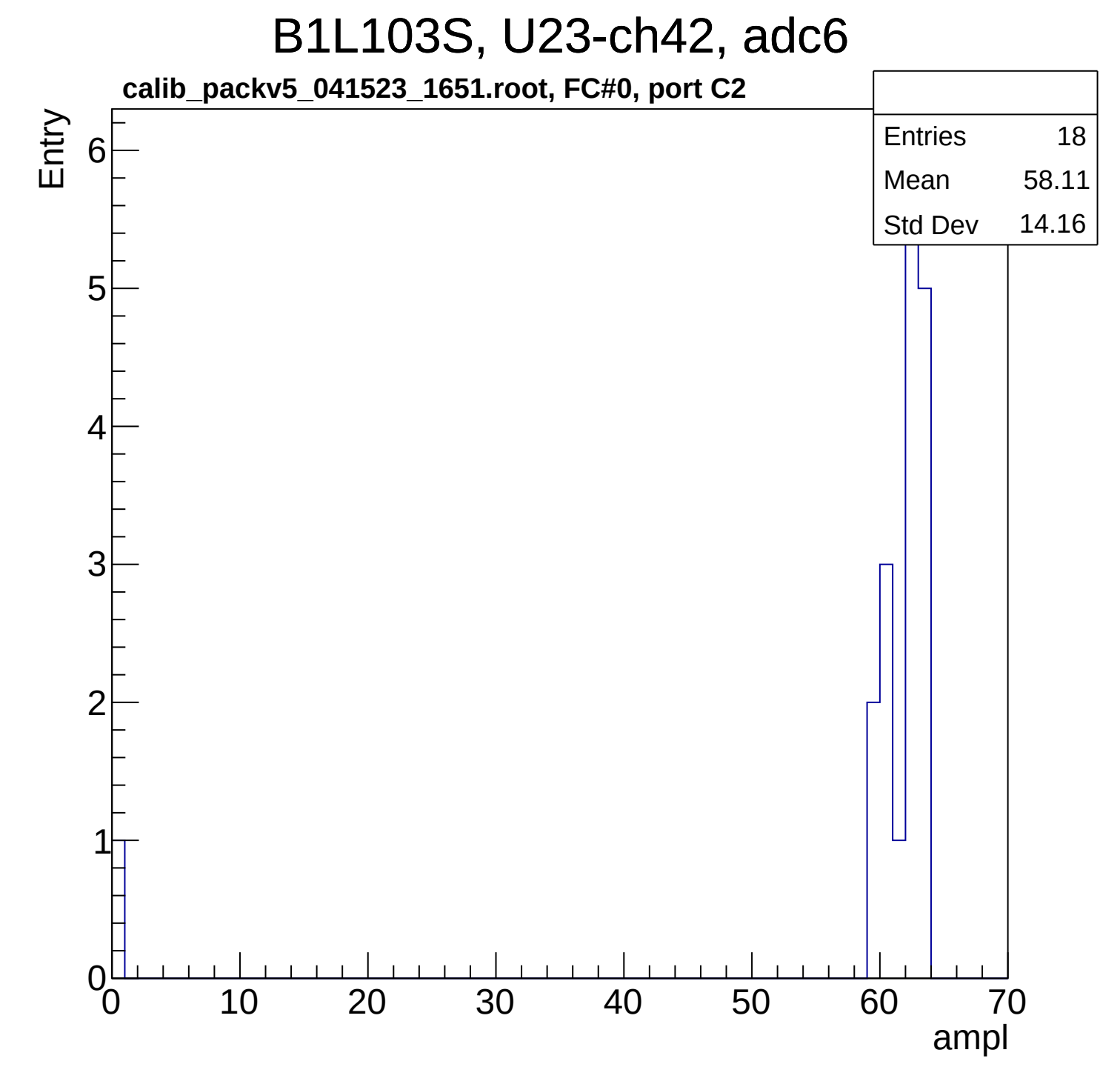

B1L103S, U23-ch42, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 29.23
Std Dev 13.17
Gaus mean : 35.8734
Gaus Width: 3.1596
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

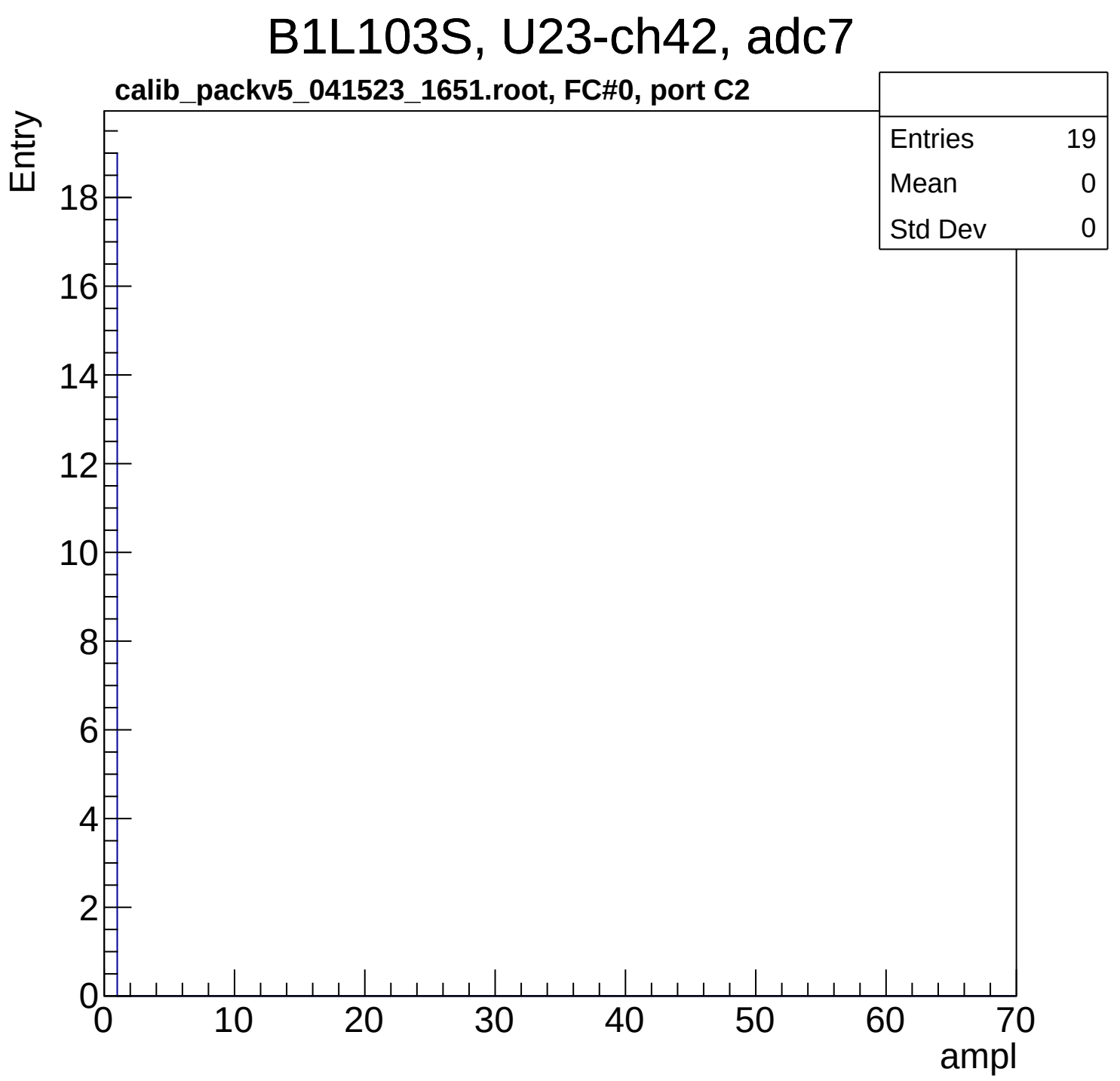

B1L103S, U23-ch42, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 40.33
Std Dev 7.658
Gaus mean : 42.0208
Gaus Width: 3.2790
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

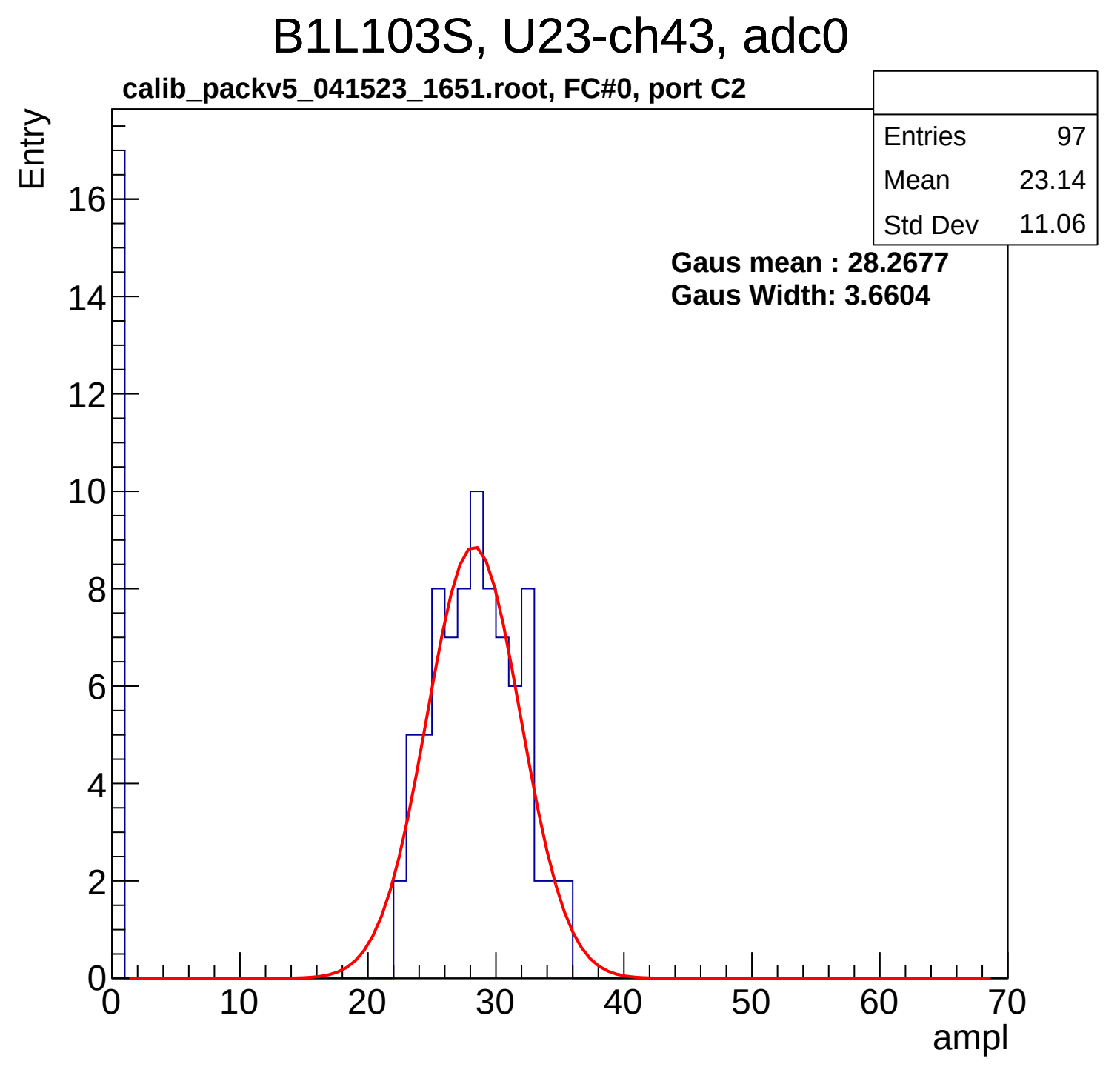

B1L103S, U23-ch42, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 48.05
Std Dev 3.286
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

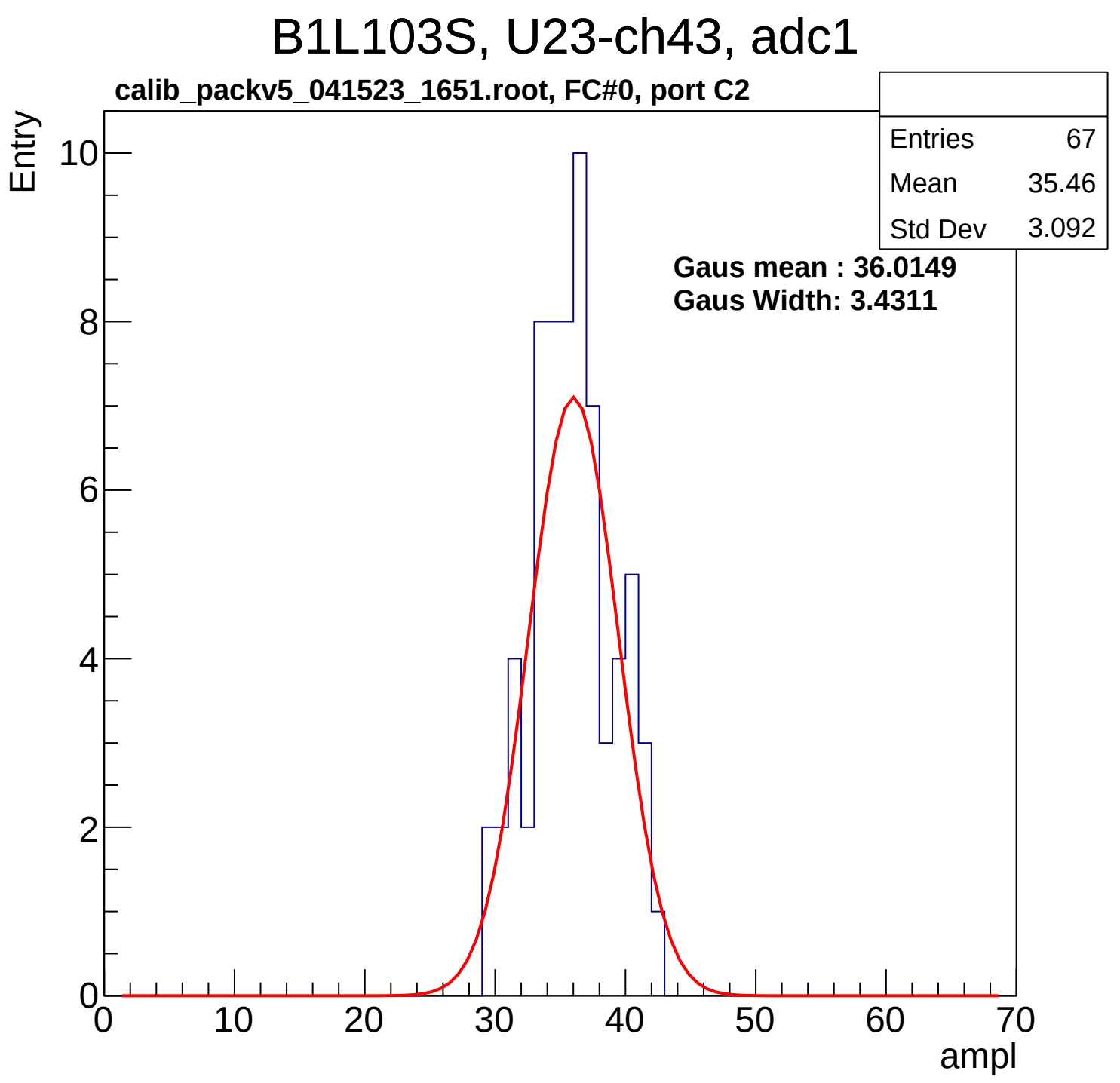

B1L103S, U23-ch42, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 53.27
Std Dev 7.331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

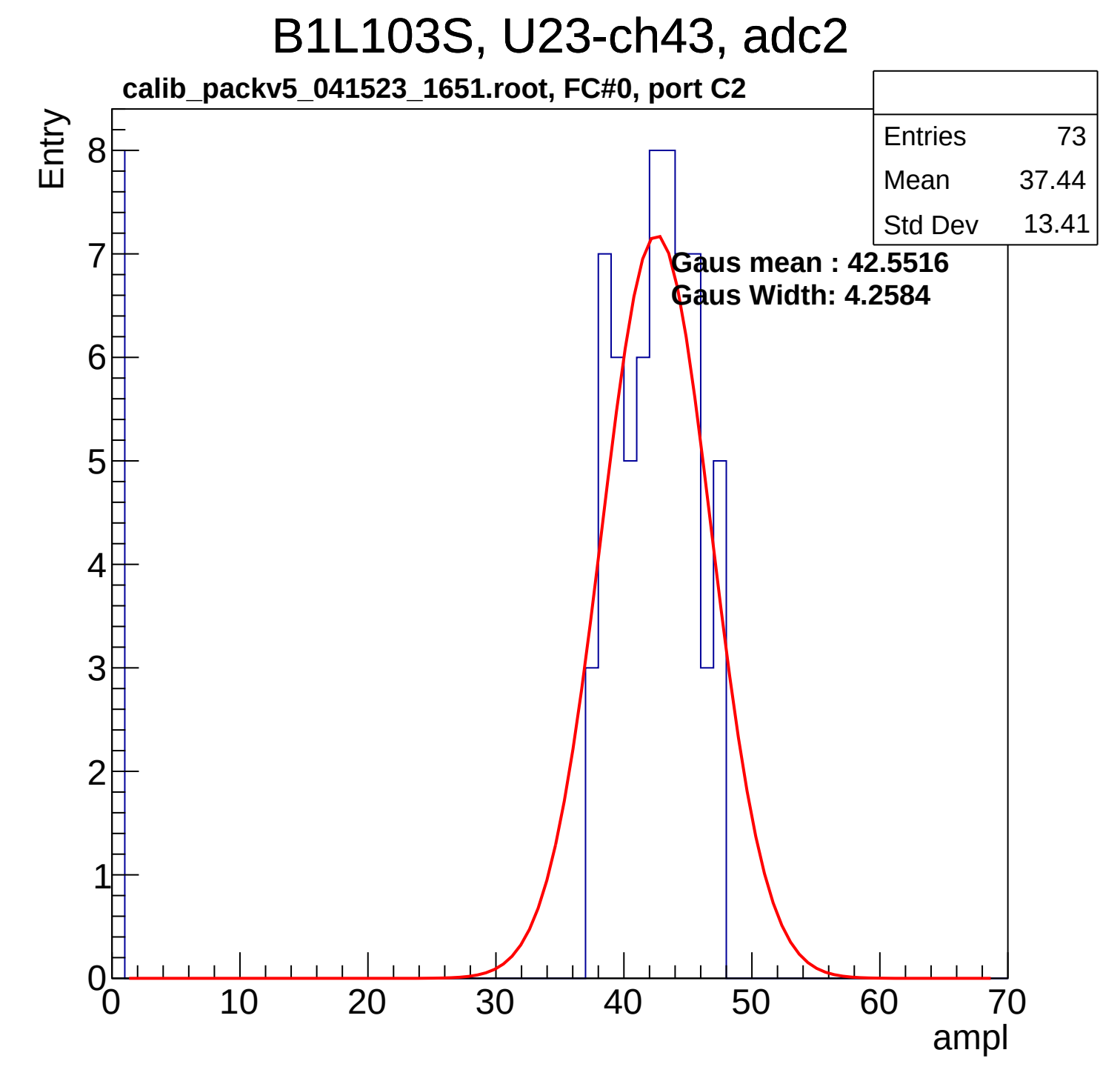

B1L103S, U23-ch42, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 44
Mean 59.2
Std Dev 2.546
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

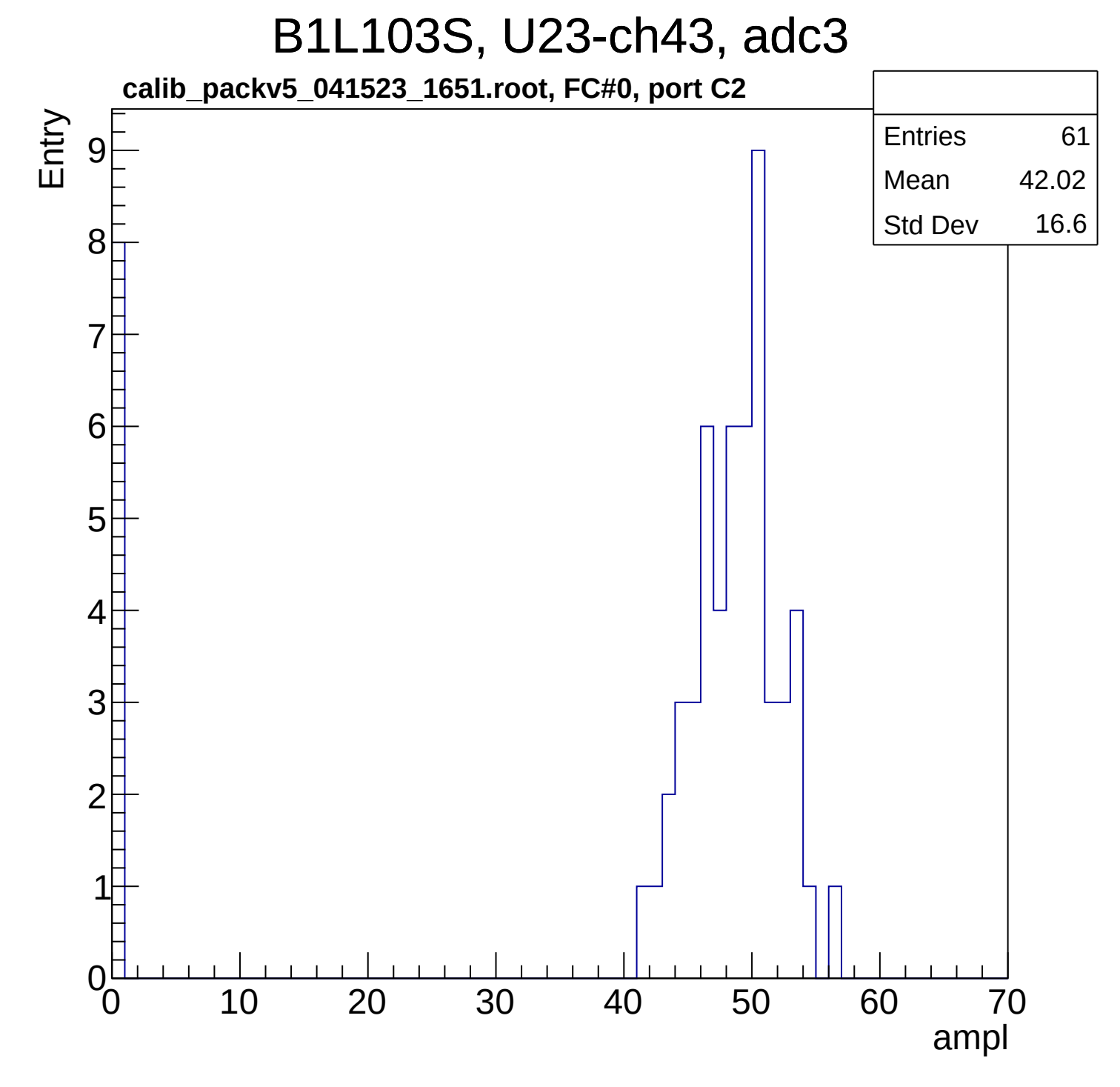

B1L103S, U23-ch42, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 58.11
Std Dev 14.16
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

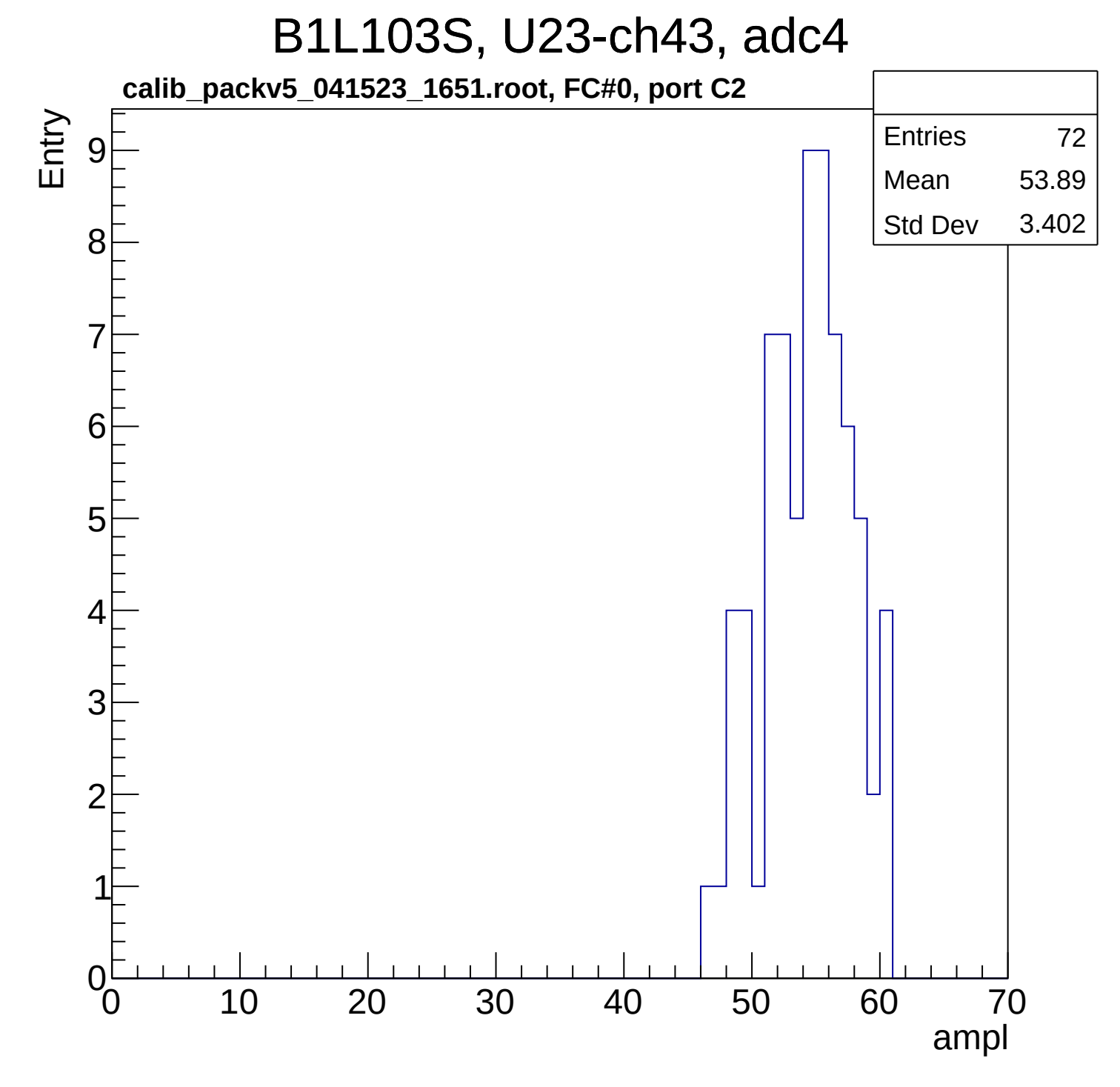

B1L103S, U23-ch42, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

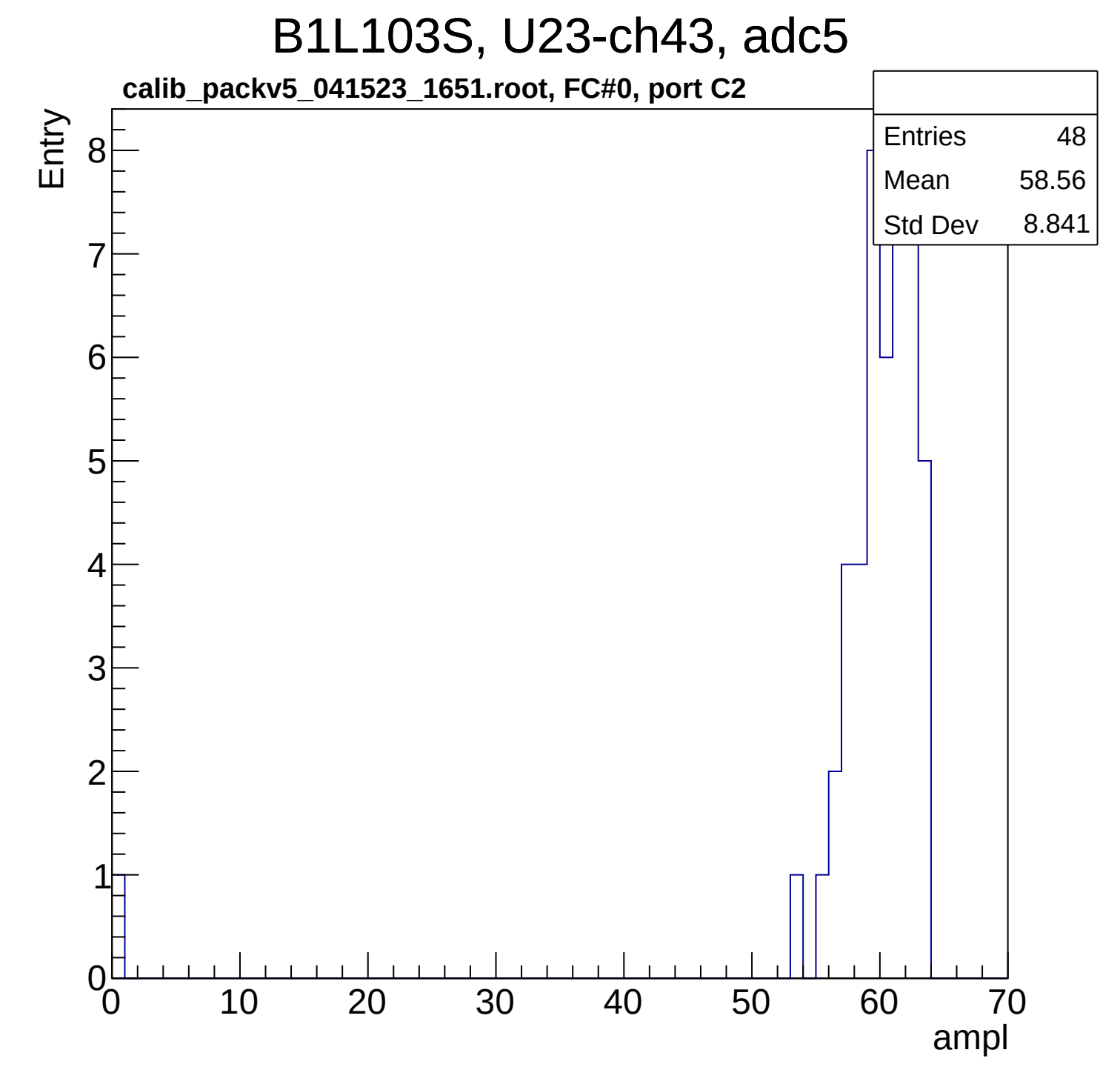

B1L103S, U23-ch43, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 97
Mean 23.14
Std Dev 11.06
Gaus mean : 28.2677
Gaus Width: 3.6604
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

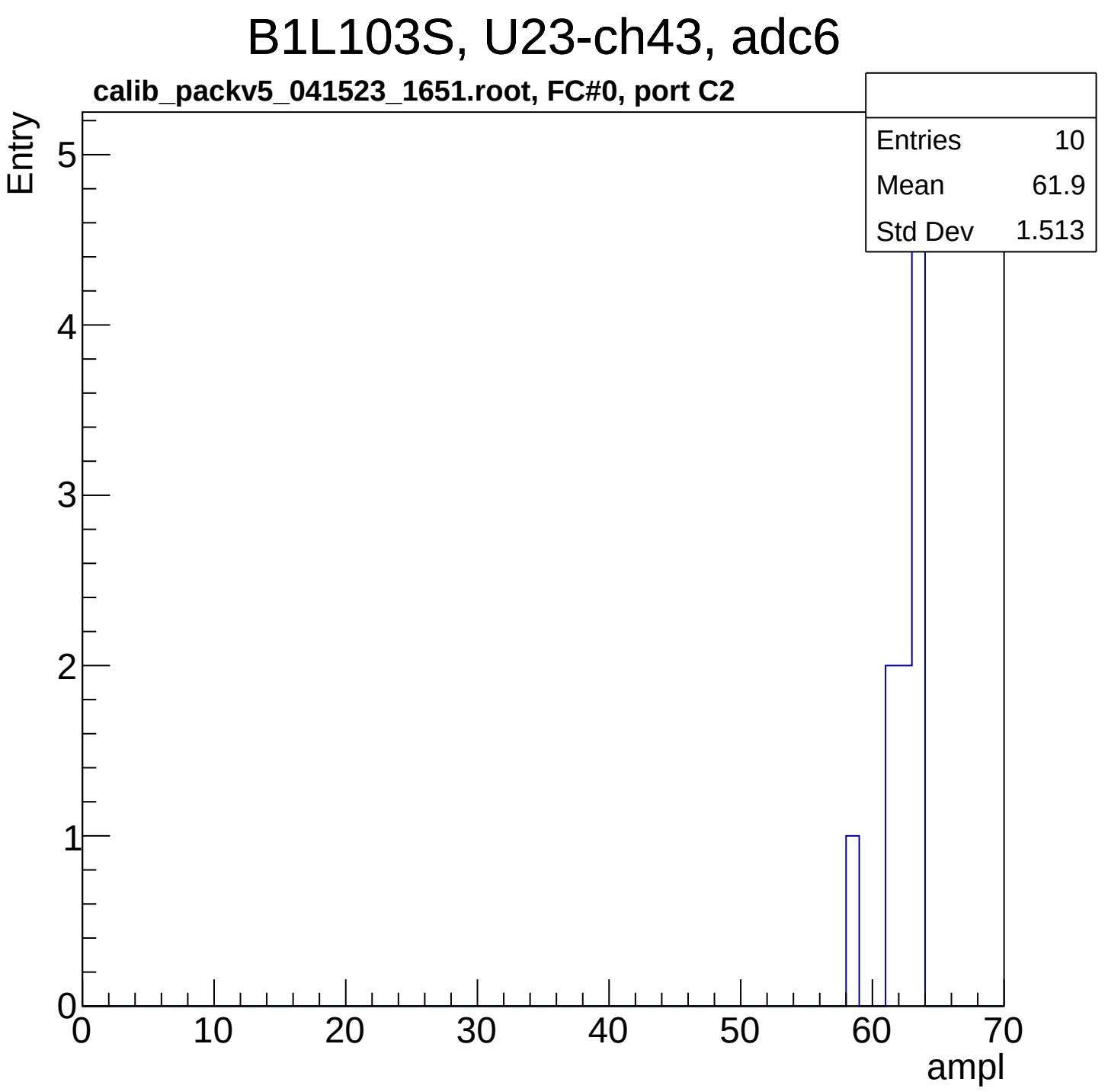

B1L103S, U23-ch43, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 35.46
Std Dev 3.092
Gaus mean : 36.0149
Gaus Width: 3.4311
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

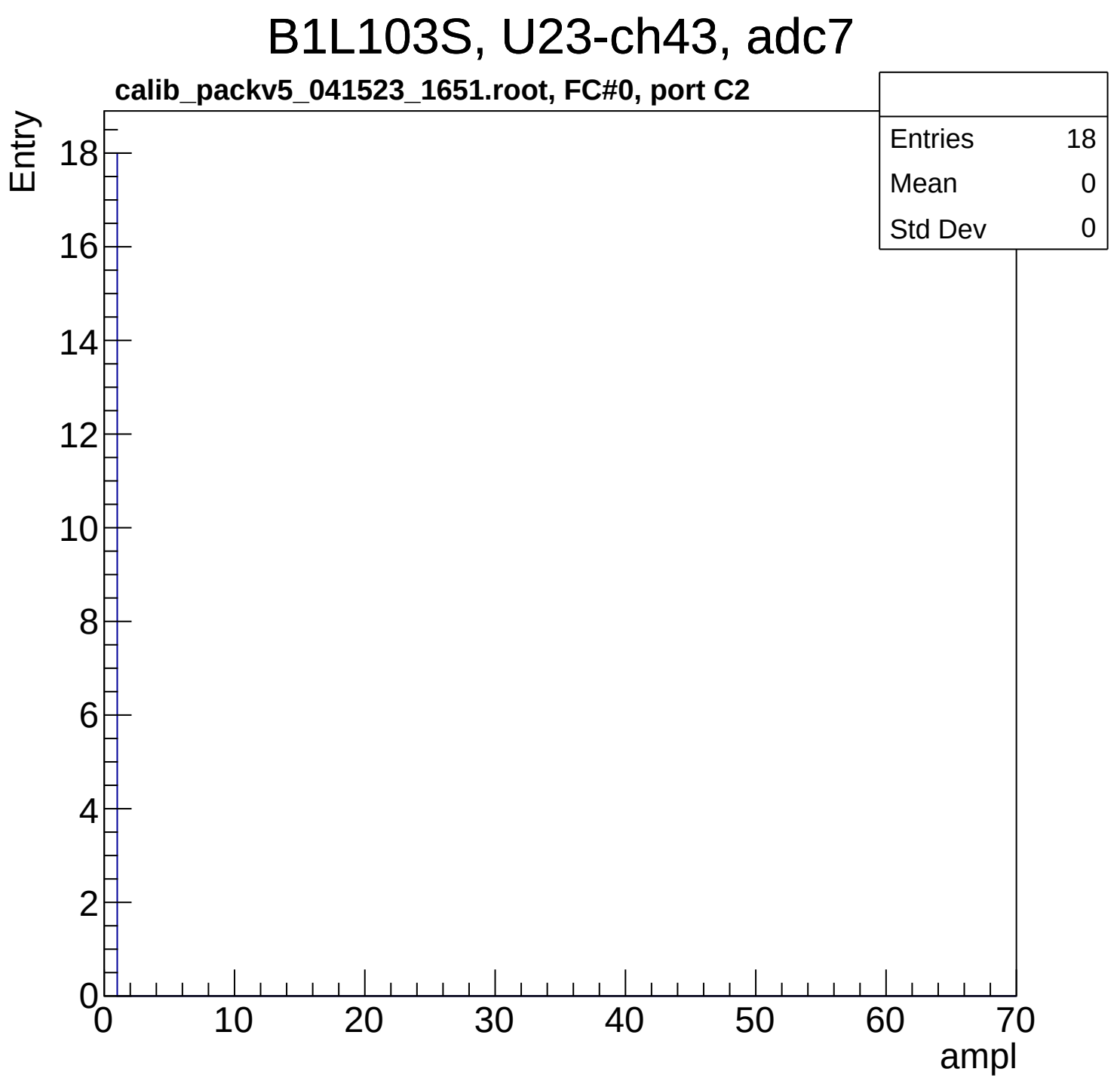

B1L103S, U23-ch43, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 37.44
Std Dev 13.41
Gaus mean : 42.5516
Gaus Width: 4.2584
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

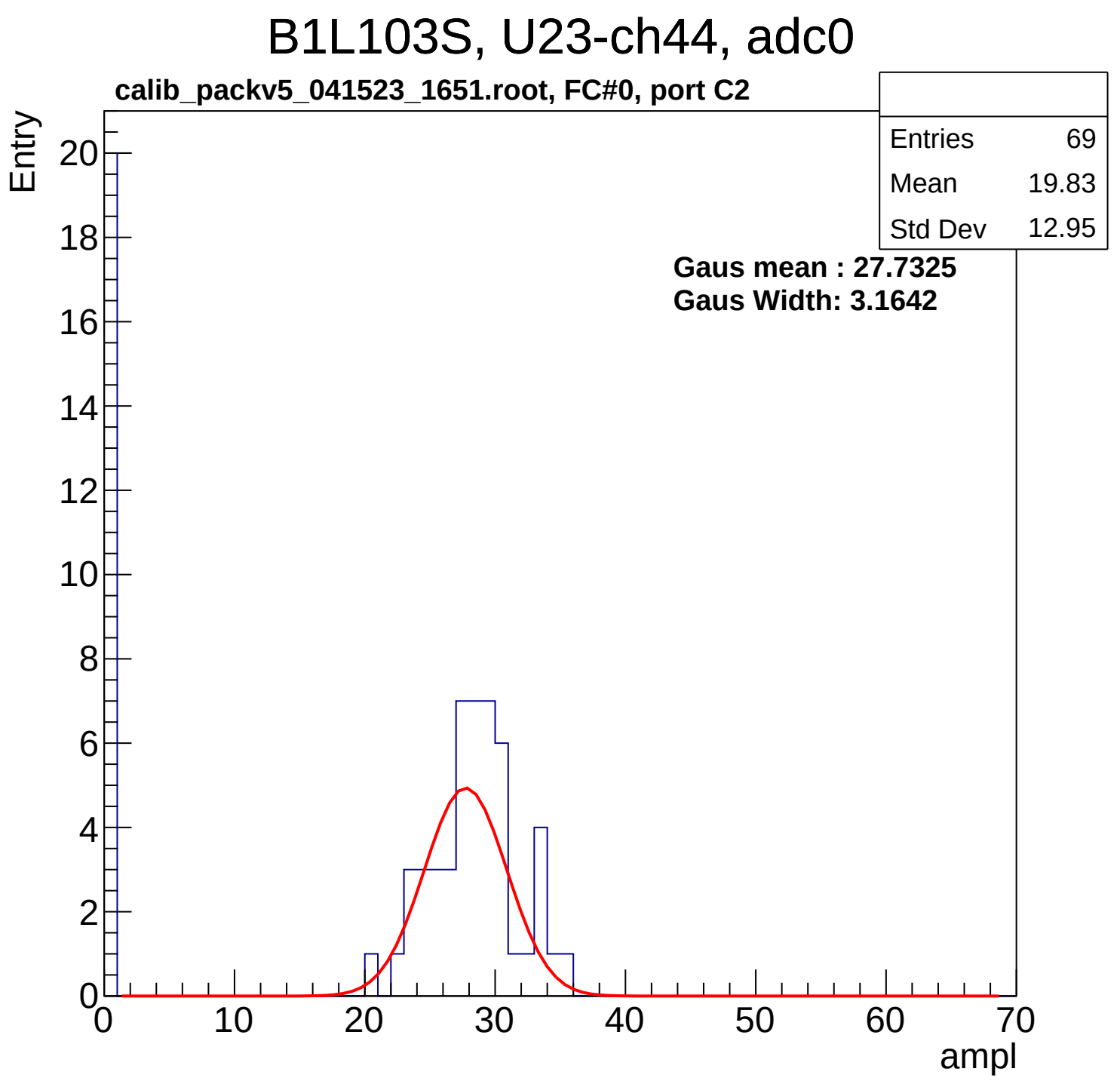

B1L103S, U23-ch43, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 42.02
Std Dev 16.6
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

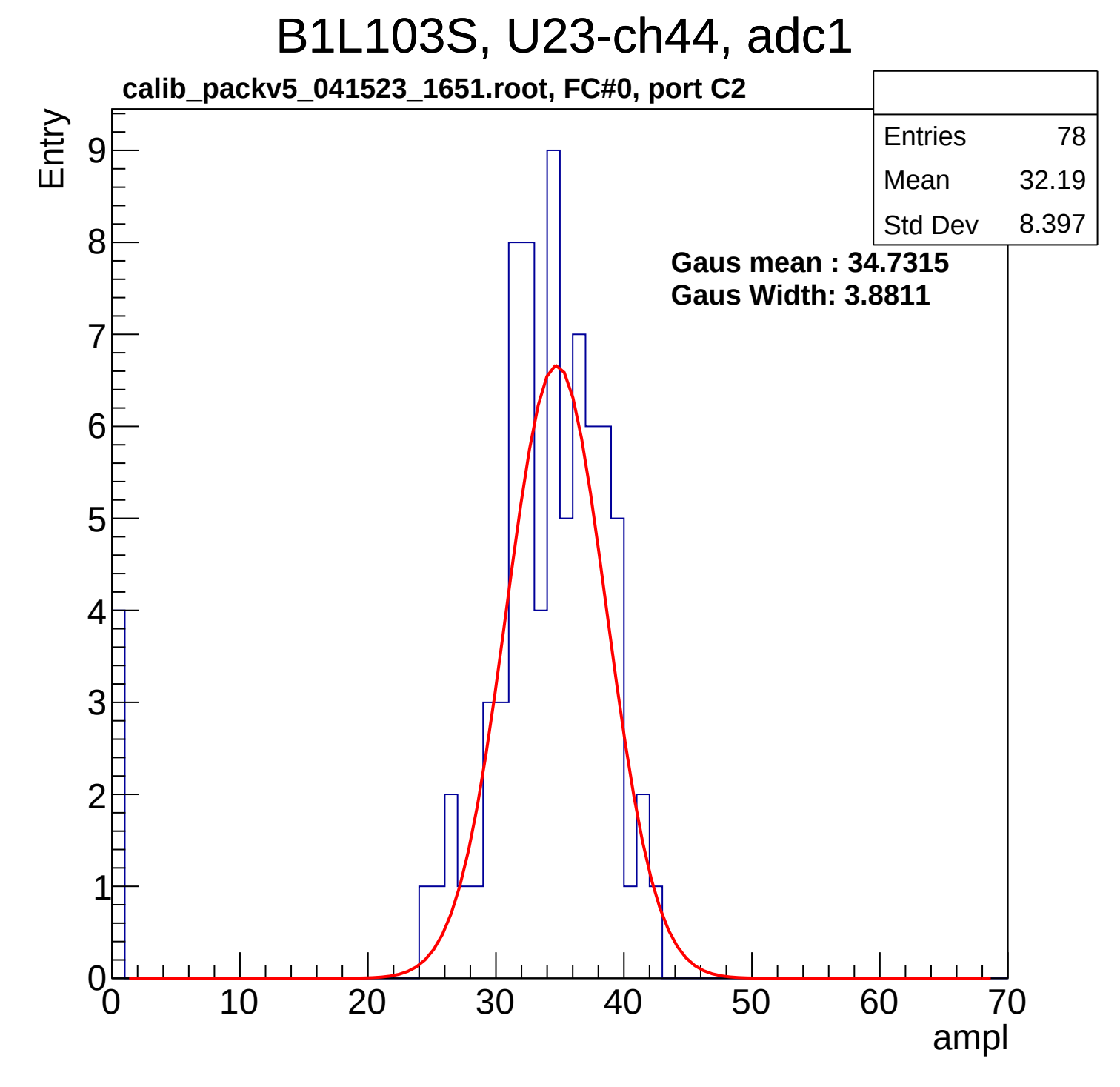

B1L103S, U23-ch43, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 53.89
Std Dev 3.402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

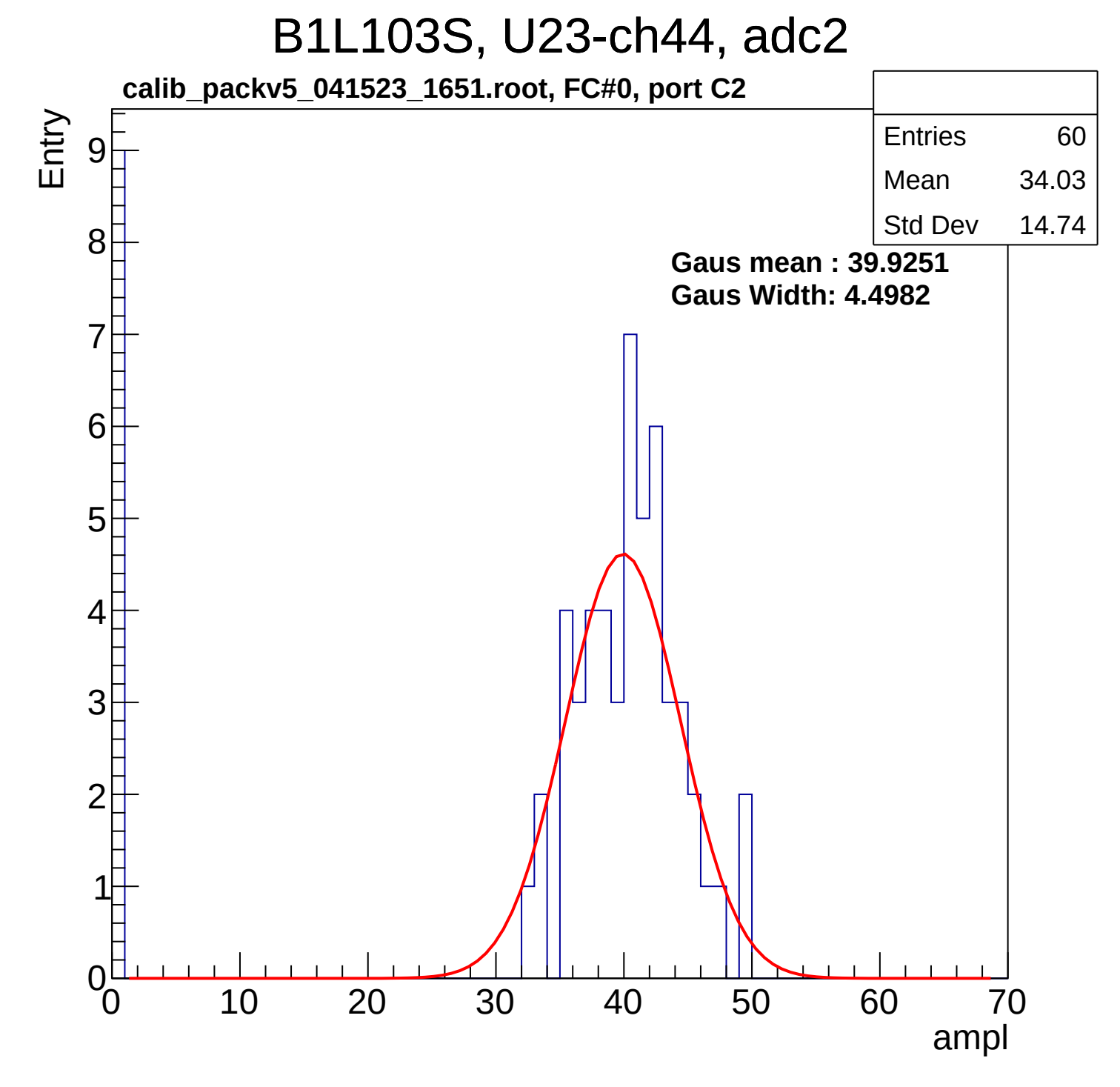

B1L103S, U23-ch43, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 58.56
Std Dev 8.841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

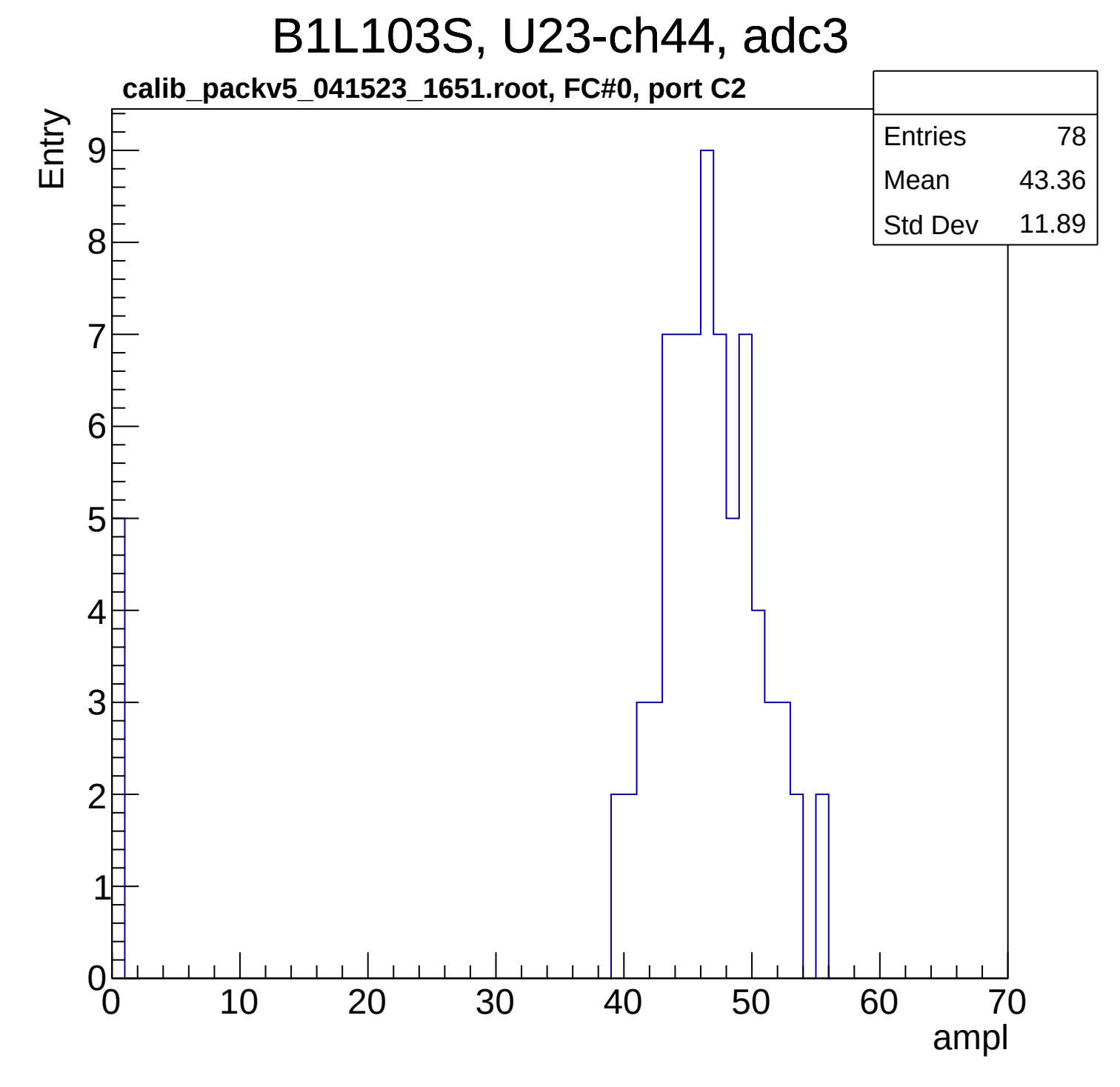

B1L103S, U23-ch43, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 61.9
Std Dev 1.513
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

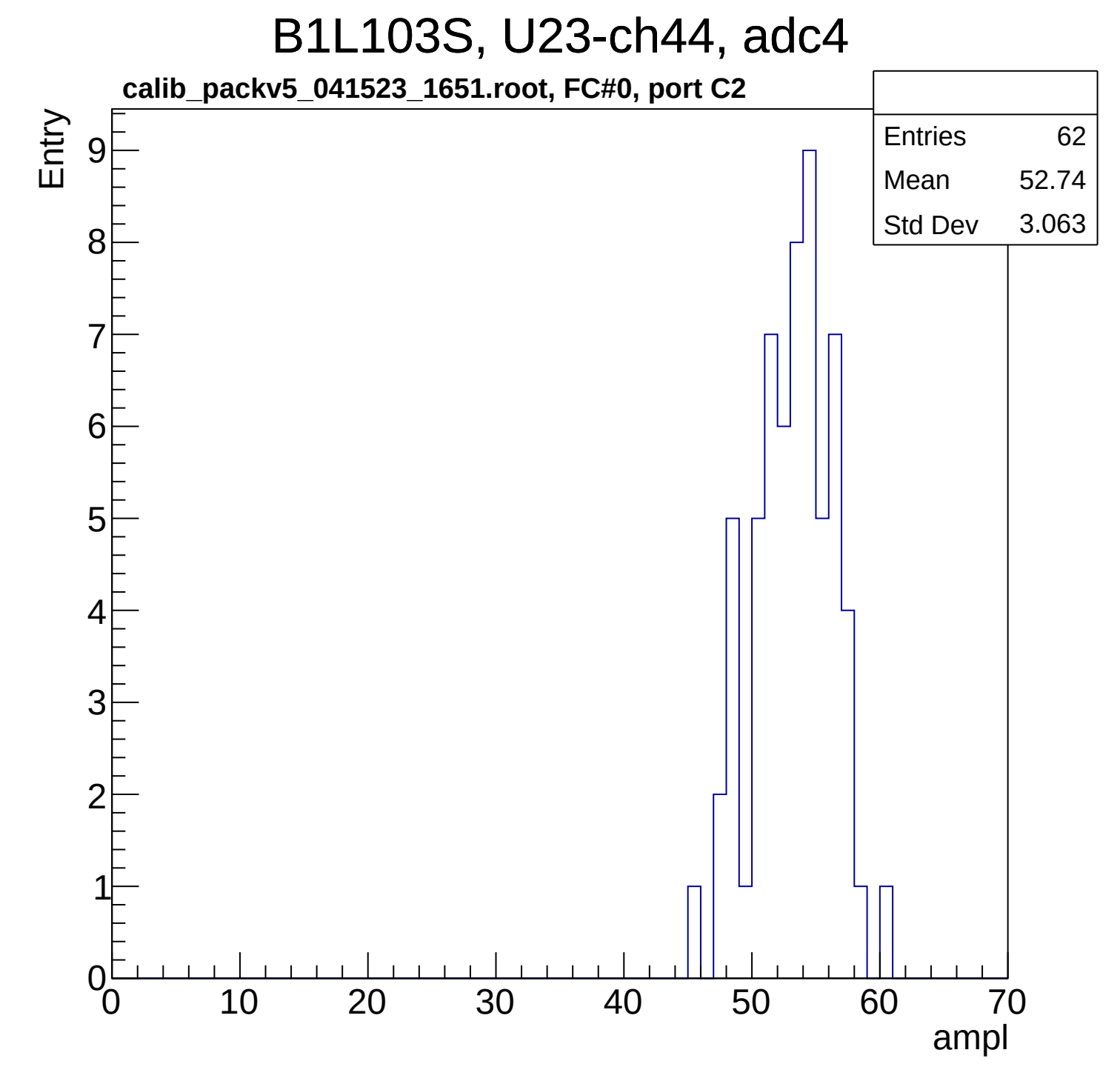

B1L103S, U23-ch43, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

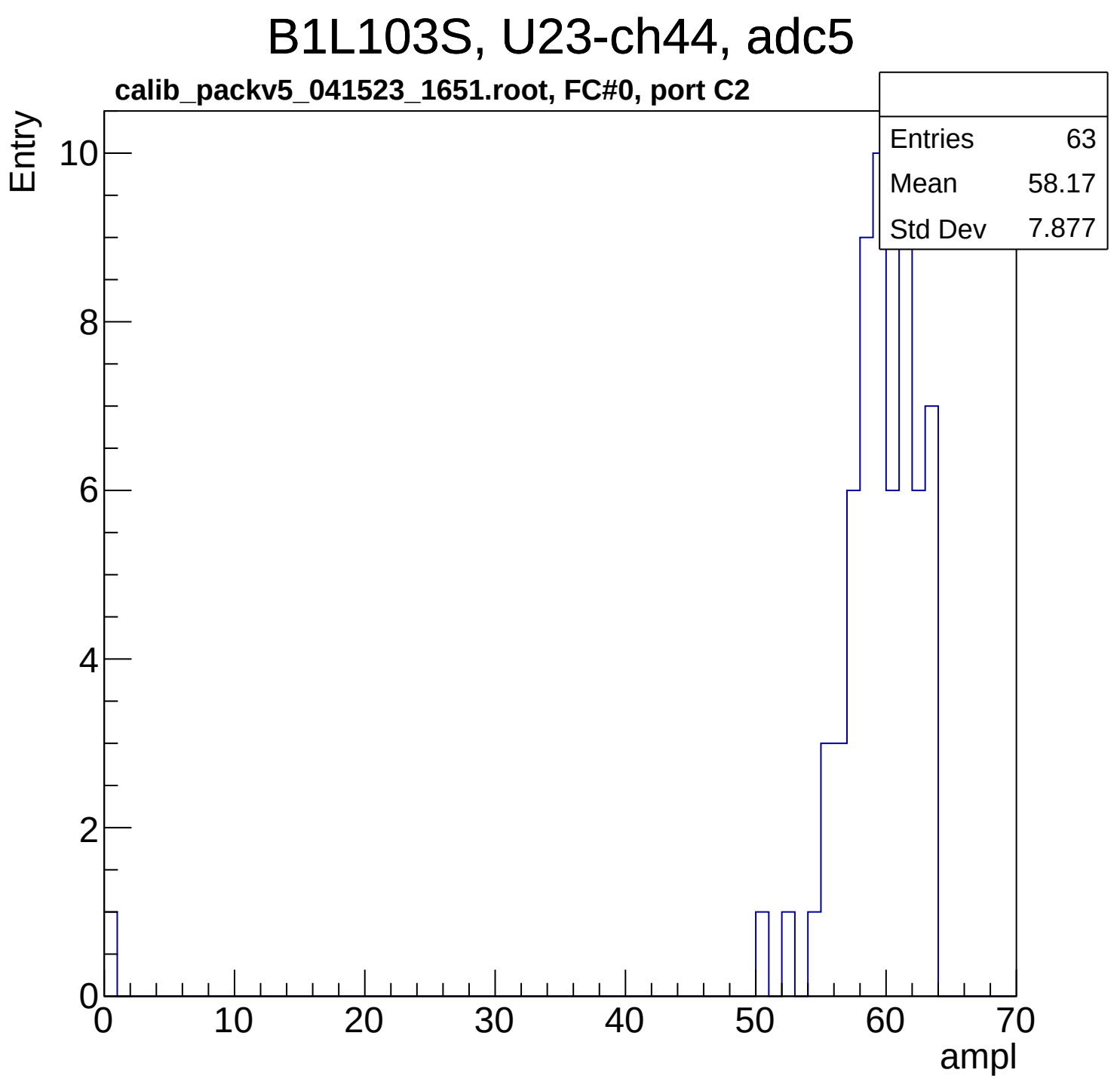

B1L103S, U23-ch44, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 19.83
Std Dev 12.95
Gaus mean : 27.7325
Gaus Width: 3.1642
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

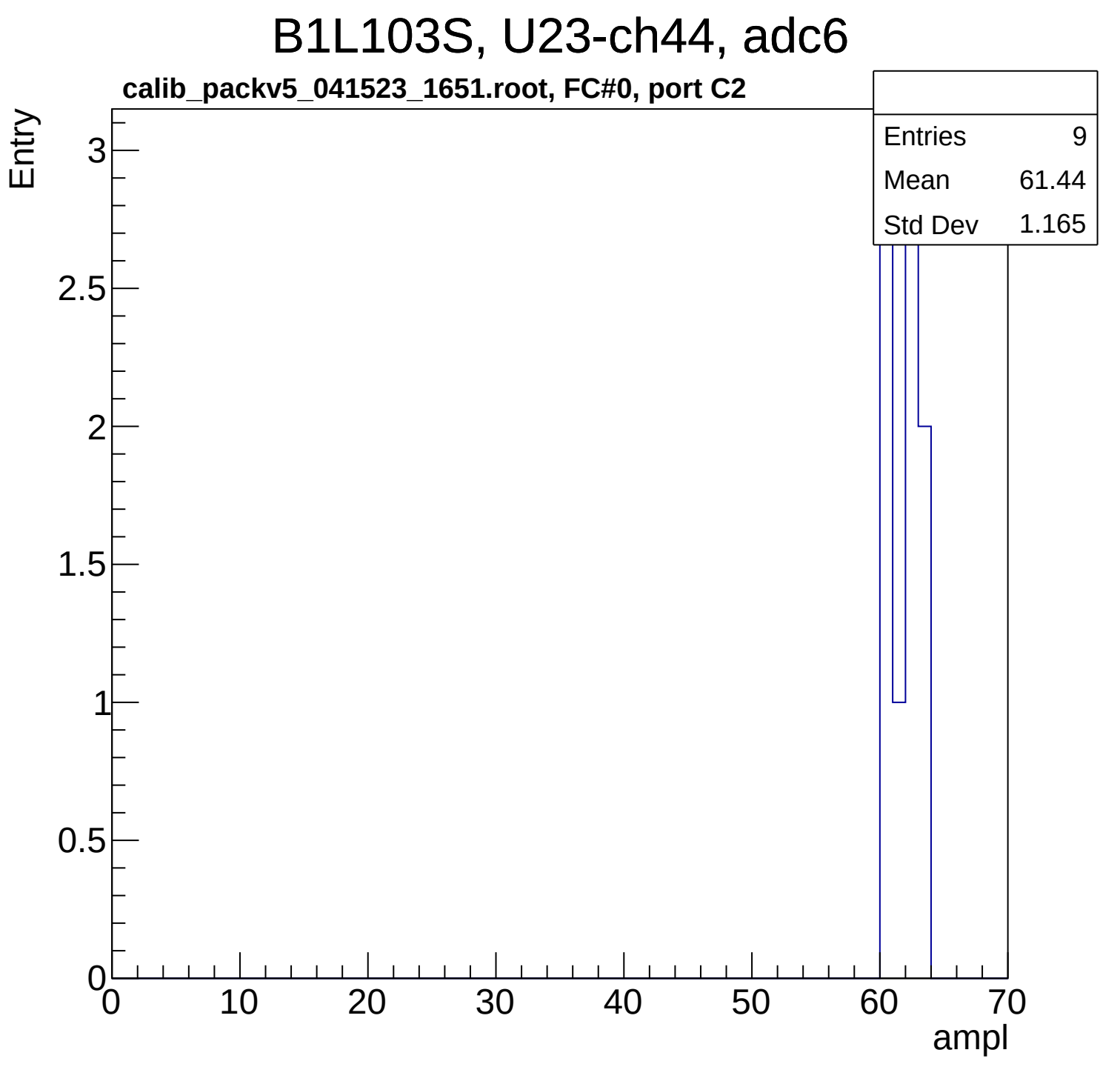

B1L103S, U23-ch44, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 32.19
Std Dev 8.397
Gaus mean : 34.7315
Gaus Width: 3.8811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

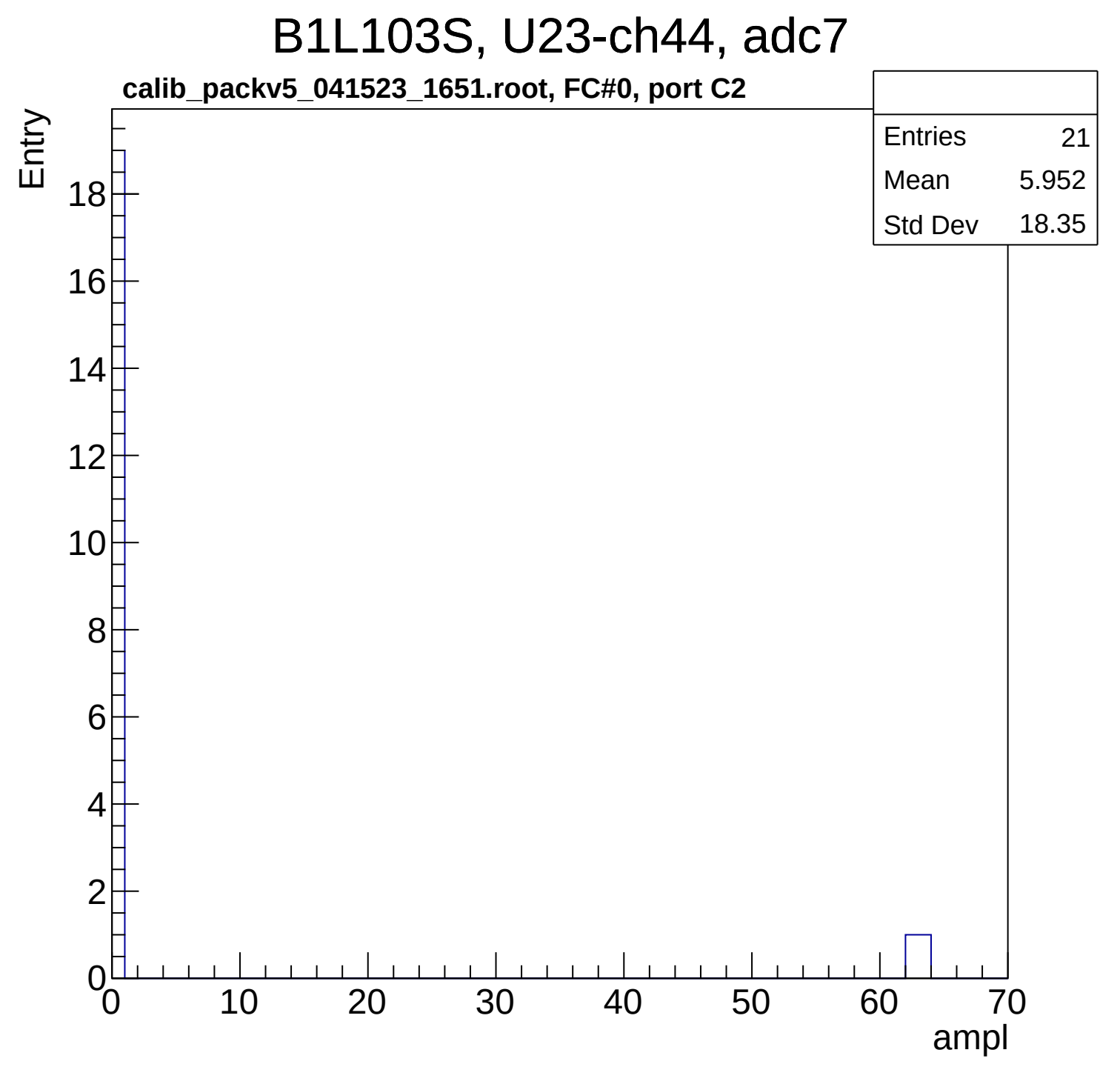

B1L103S, U23-ch44, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 34.03
Std Dev 14.74
Gaus mean : 39.9251
Gaus Width: 4.4982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

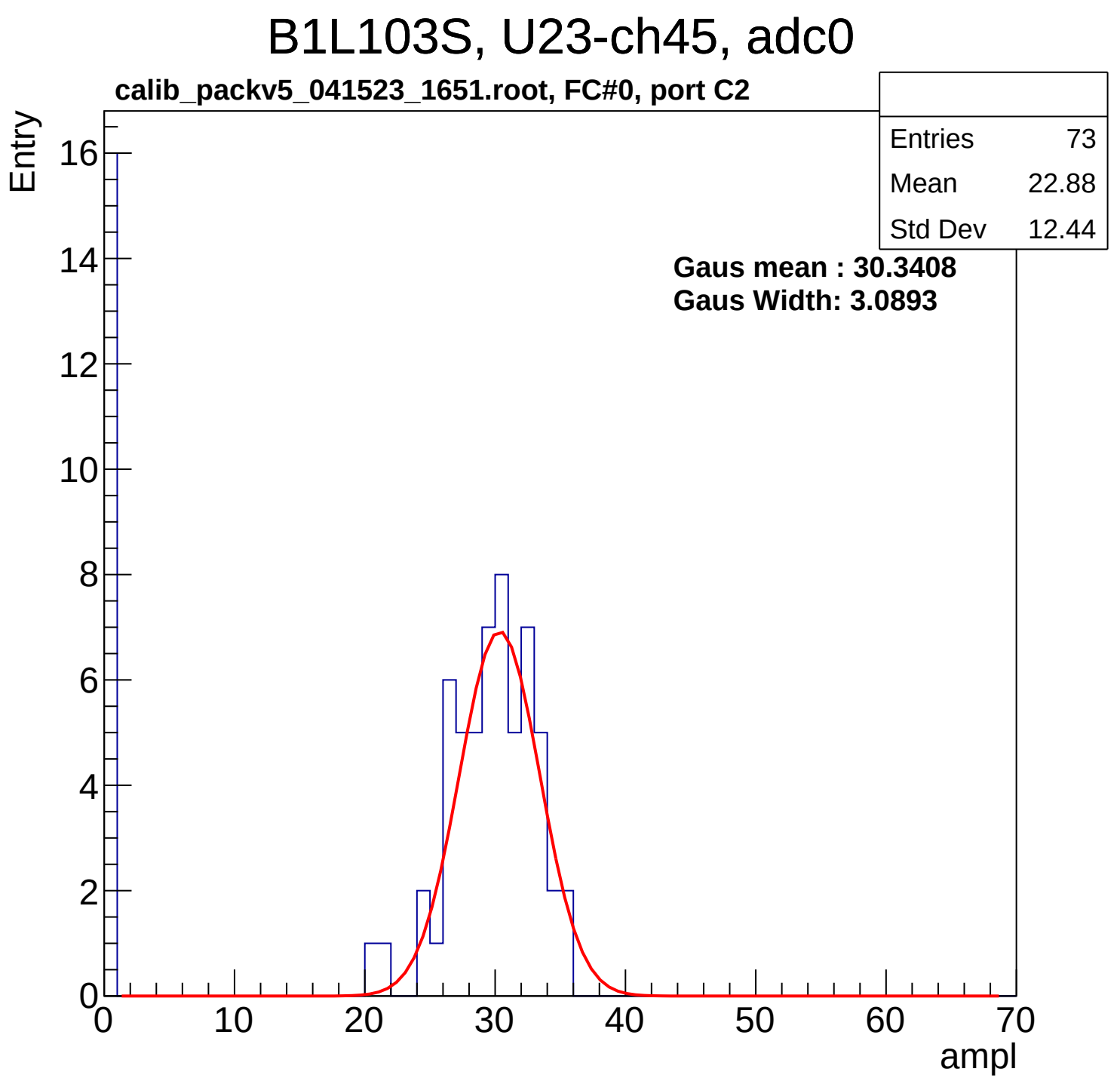

B1L103S, U23-ch44, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 43.36
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

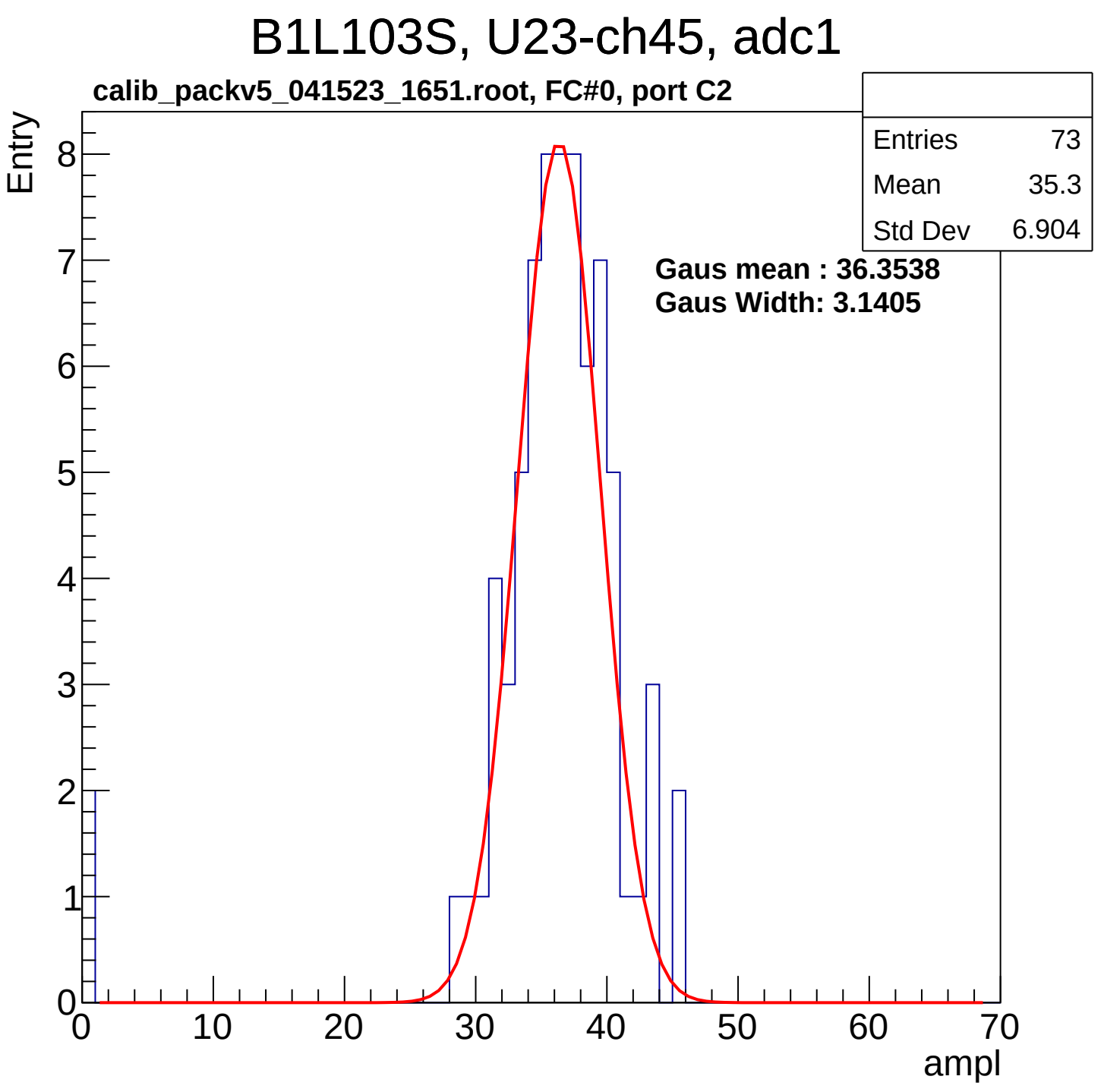

B1L103S, U23-ch44, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 52.74
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

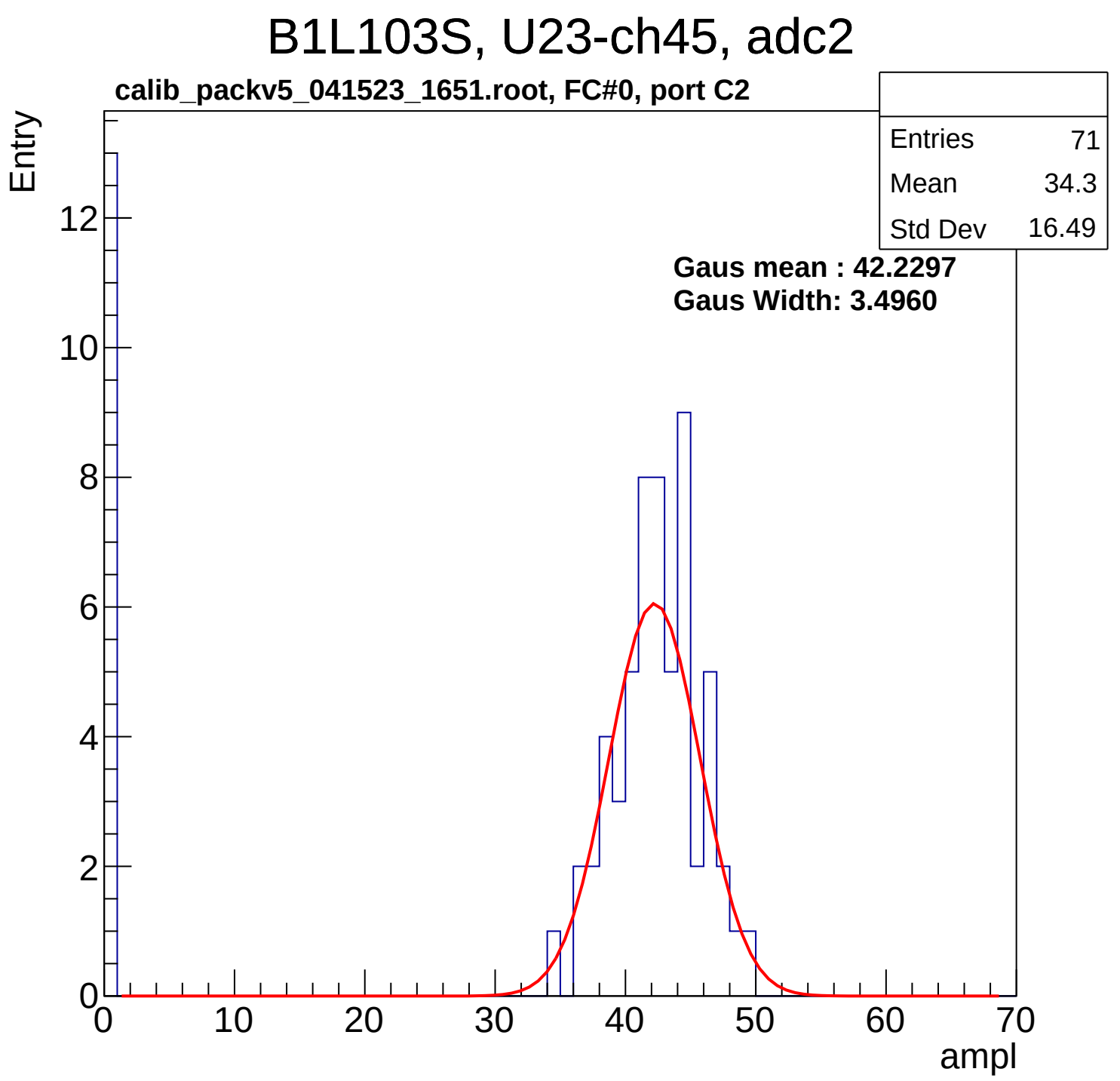

B1L103S, U23-ch44, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 58.17
Std Dev 7.877
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

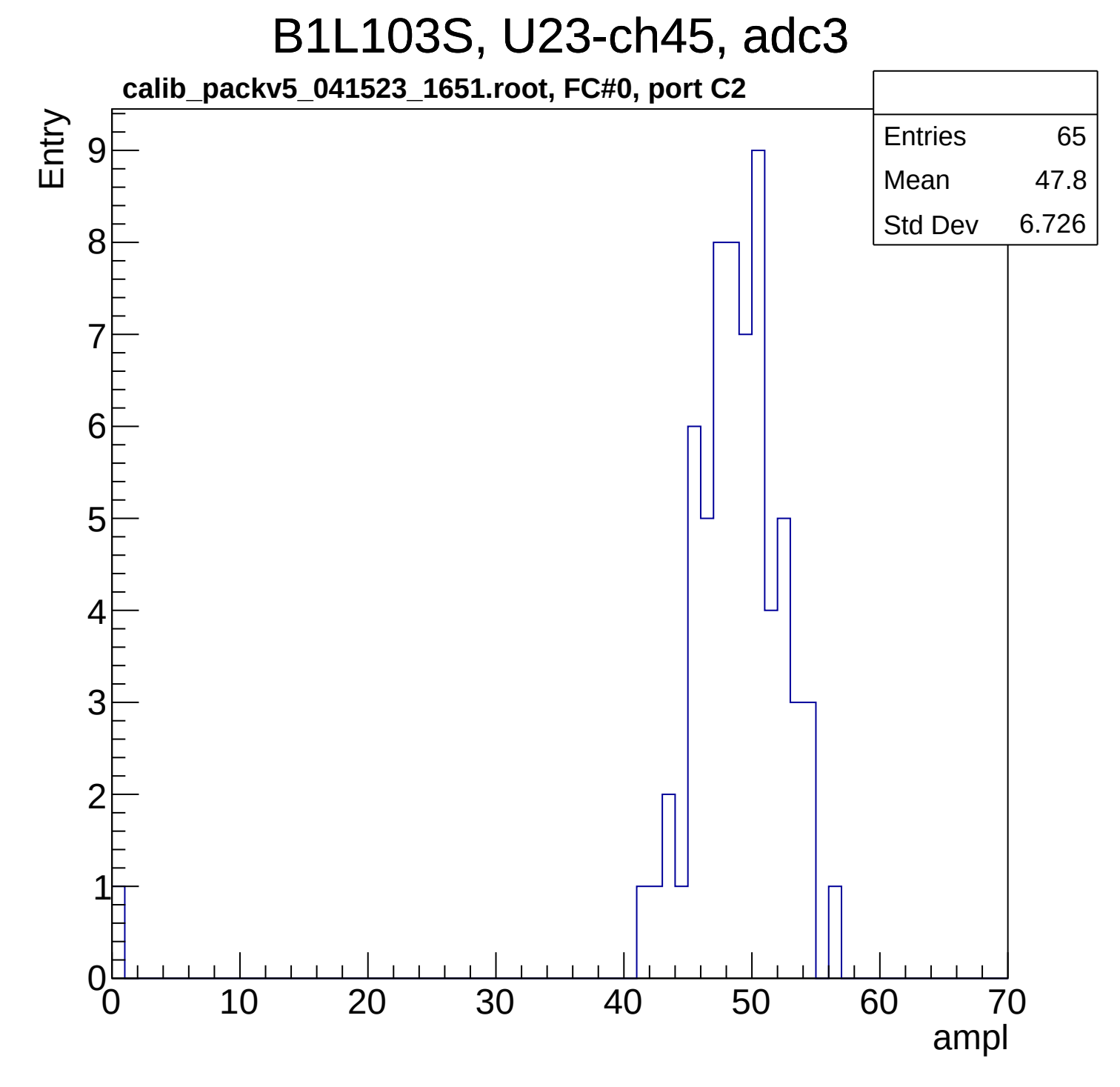

B1L103S, U23-ch44, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 61.44
Std Dev 1.165
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

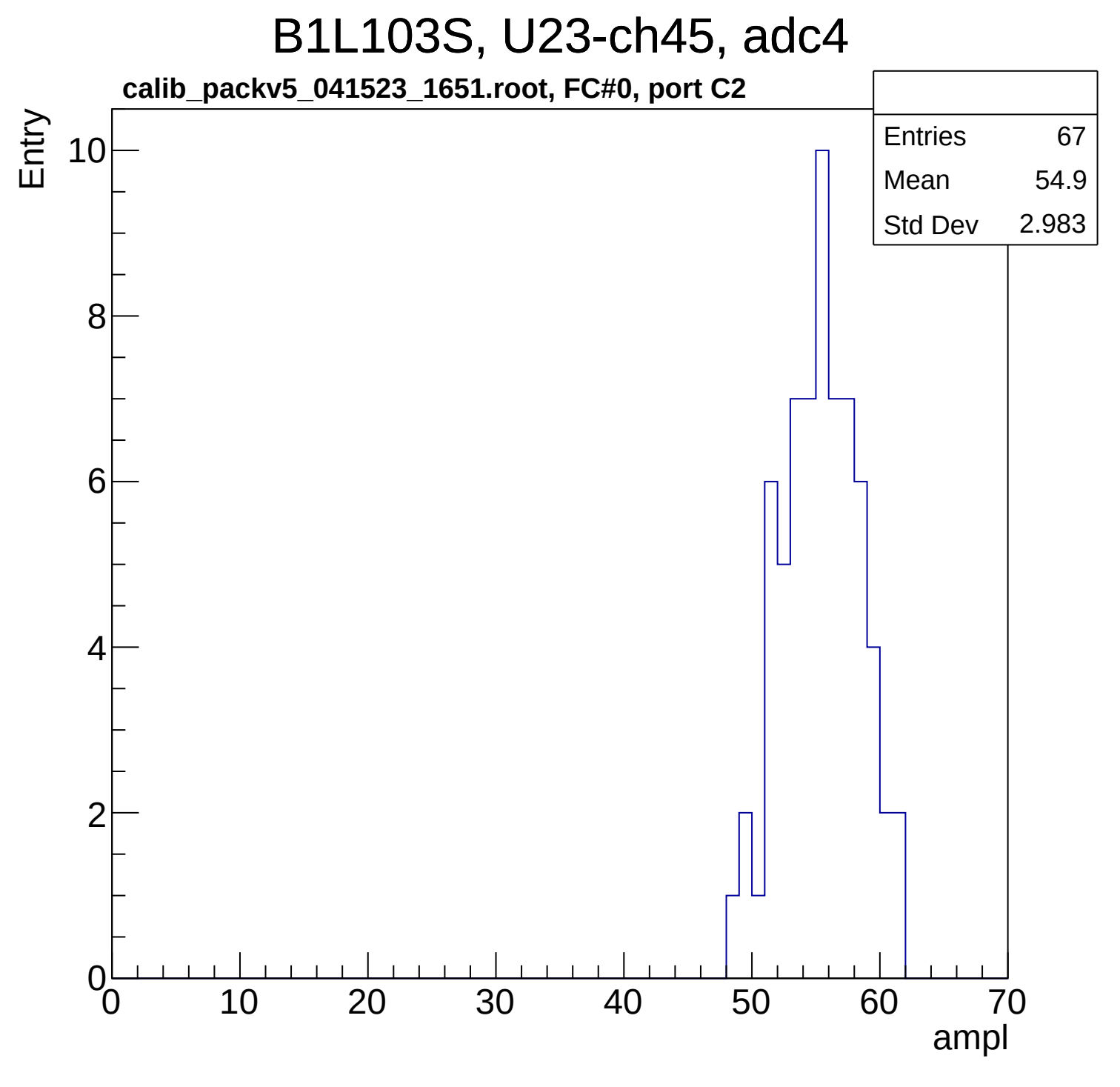

B1L103S, U23-ch44, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.952
Std Dev 18.35
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

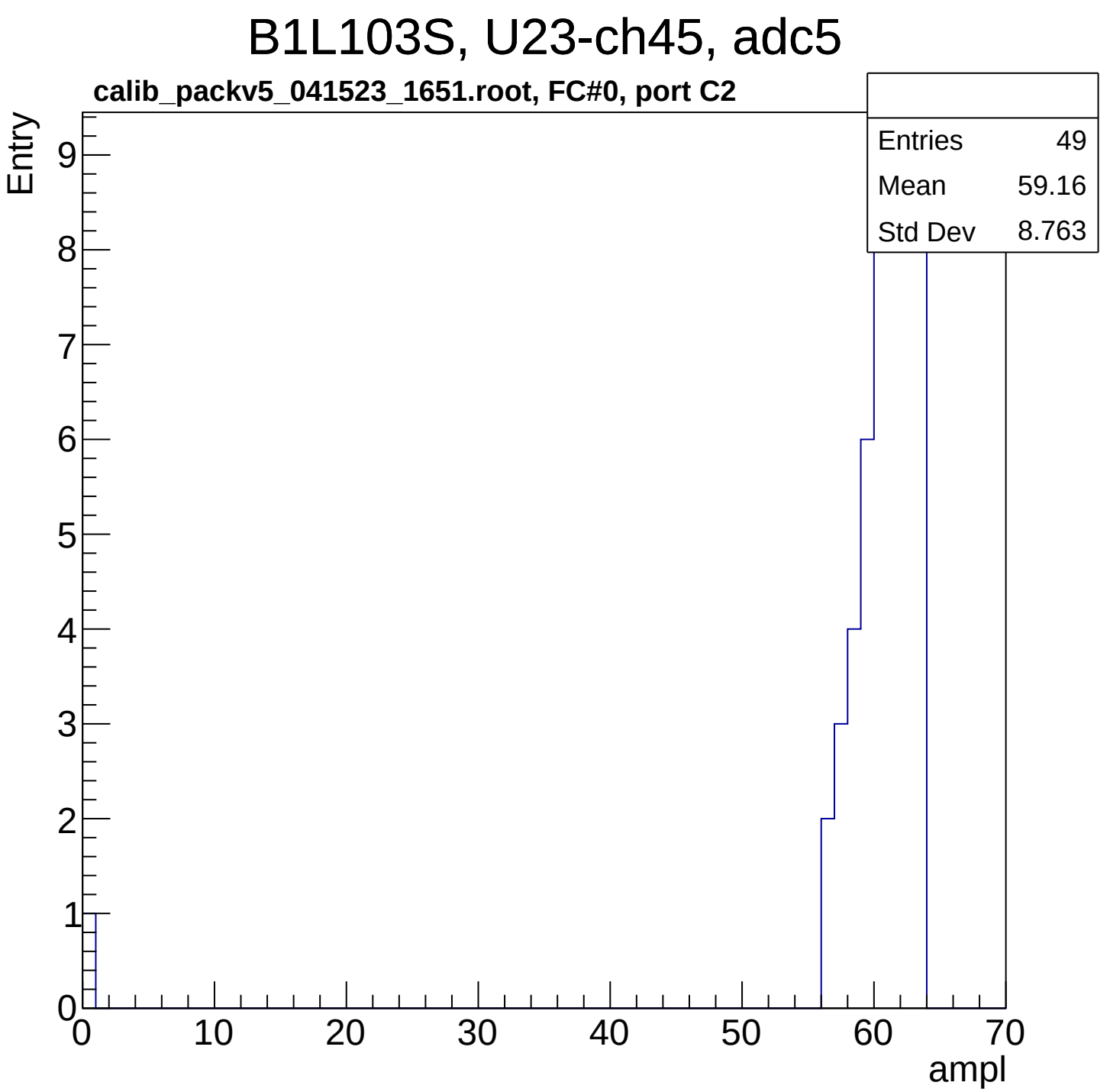

B1L103S, U23-ch45, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 22.88
Std Dev 12.44
Gaus mean : 30.3408
Gaus Width: 3.0893
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

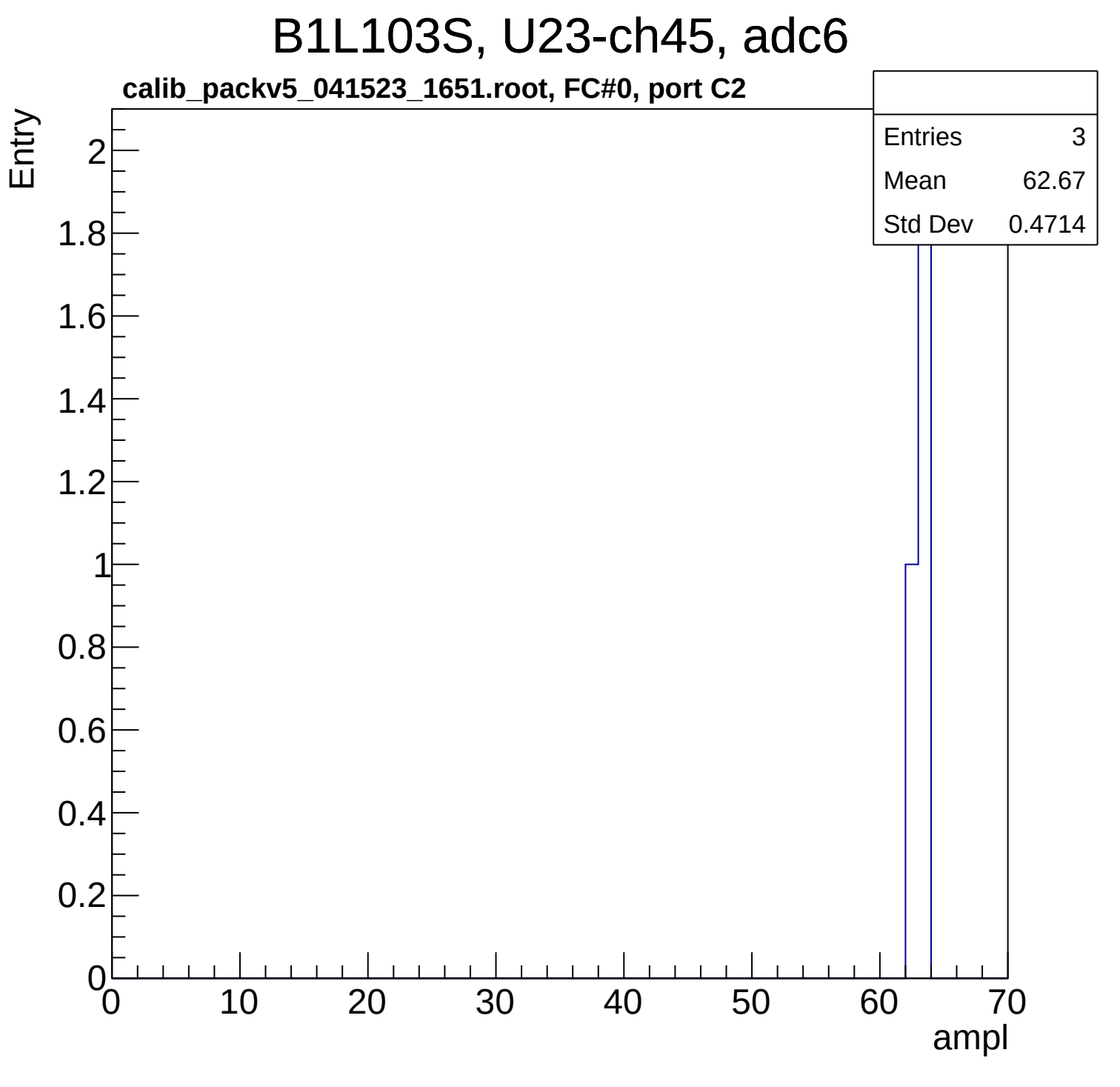

B1L103S, U23-ch45, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 35.3
Std Dev 6.904
Gaus mean : 36.3538
Gaus Width: 3.1405
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

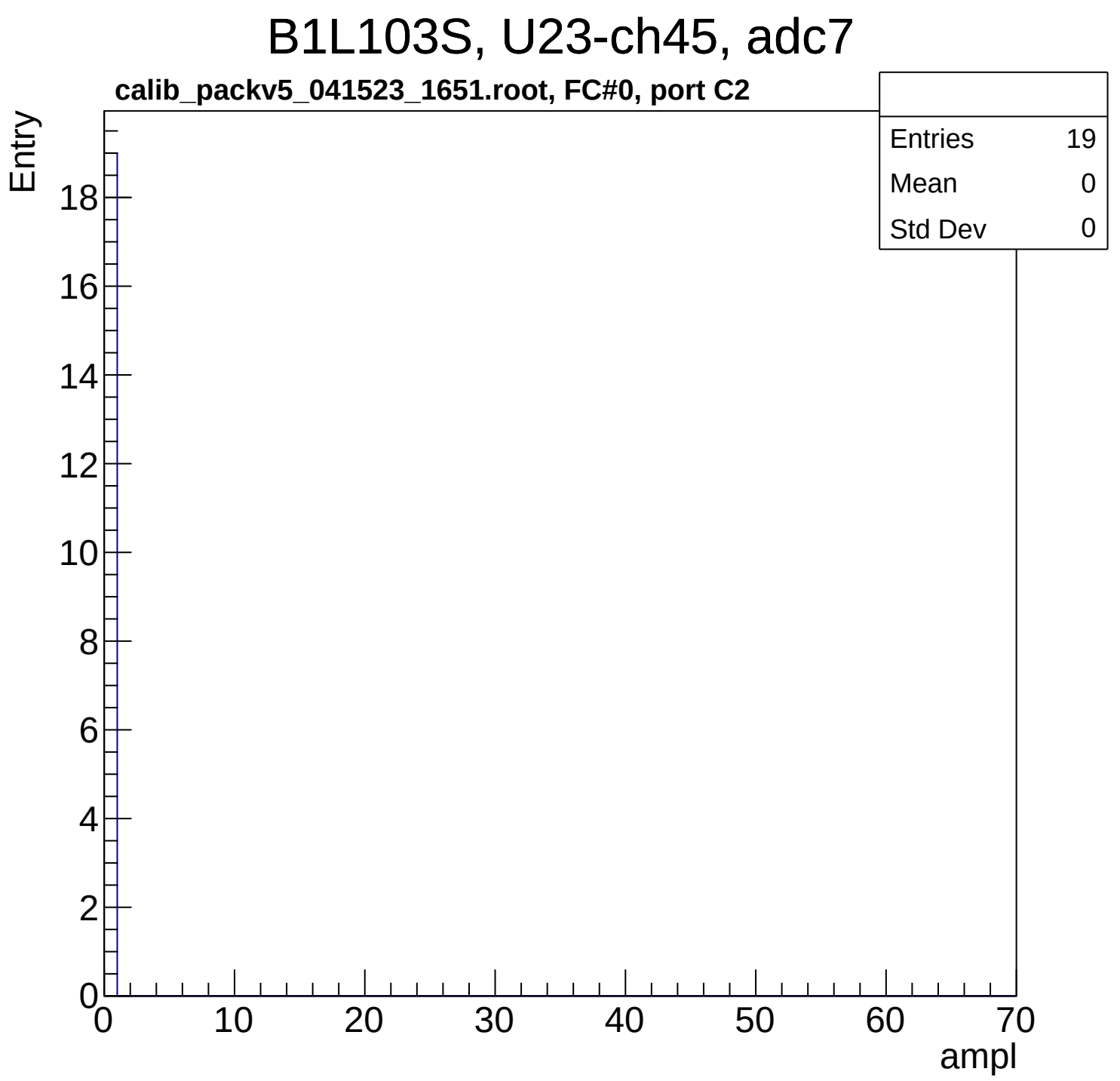

B1L103S, U23-ch45, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 34.3
Std Dev 16.49
Gaus mean : 42.2297
Gaus Width: 3.4960
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

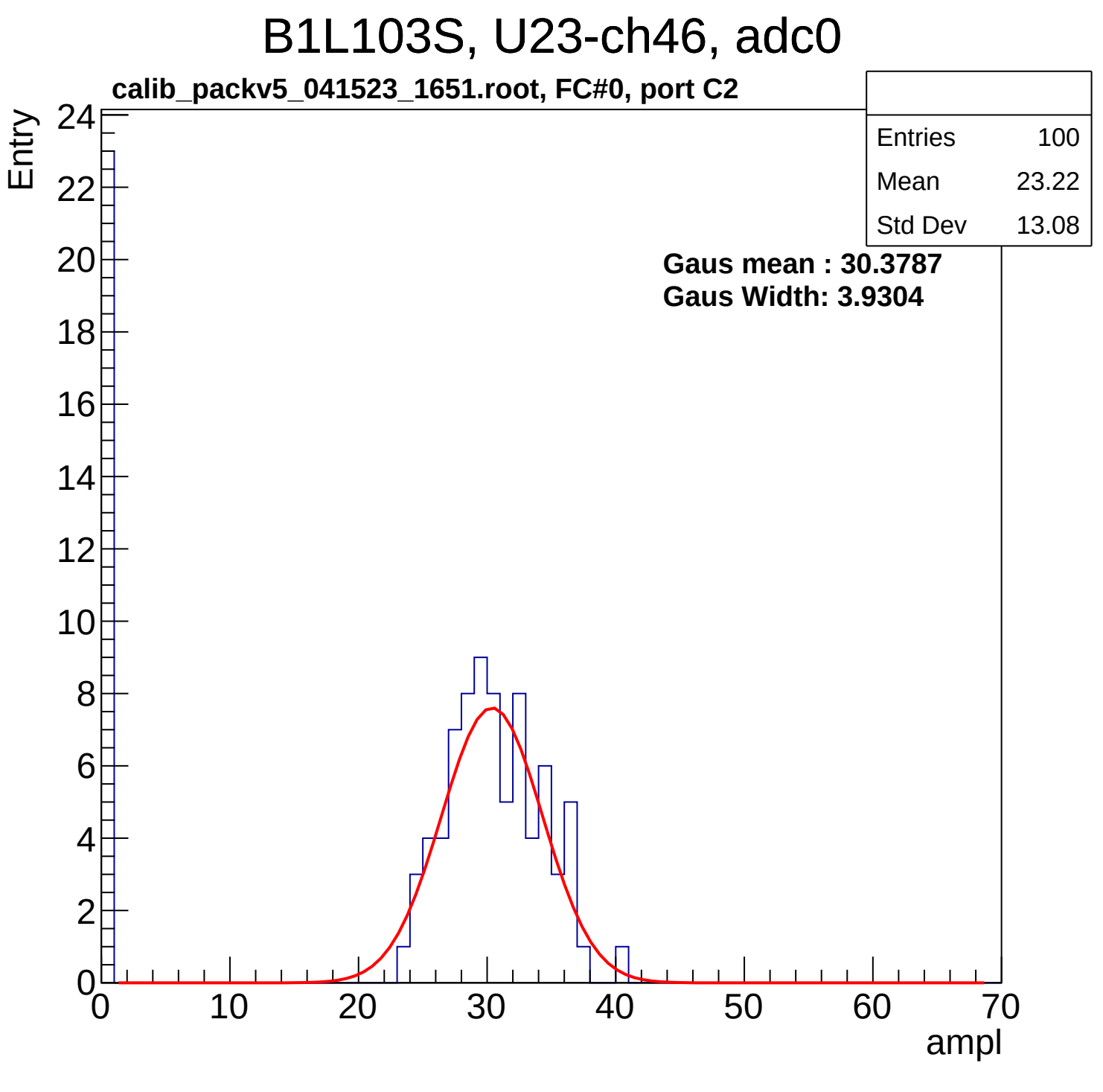

B1L103S, U23-ch45, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 47.8
Std Dev 6.726
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

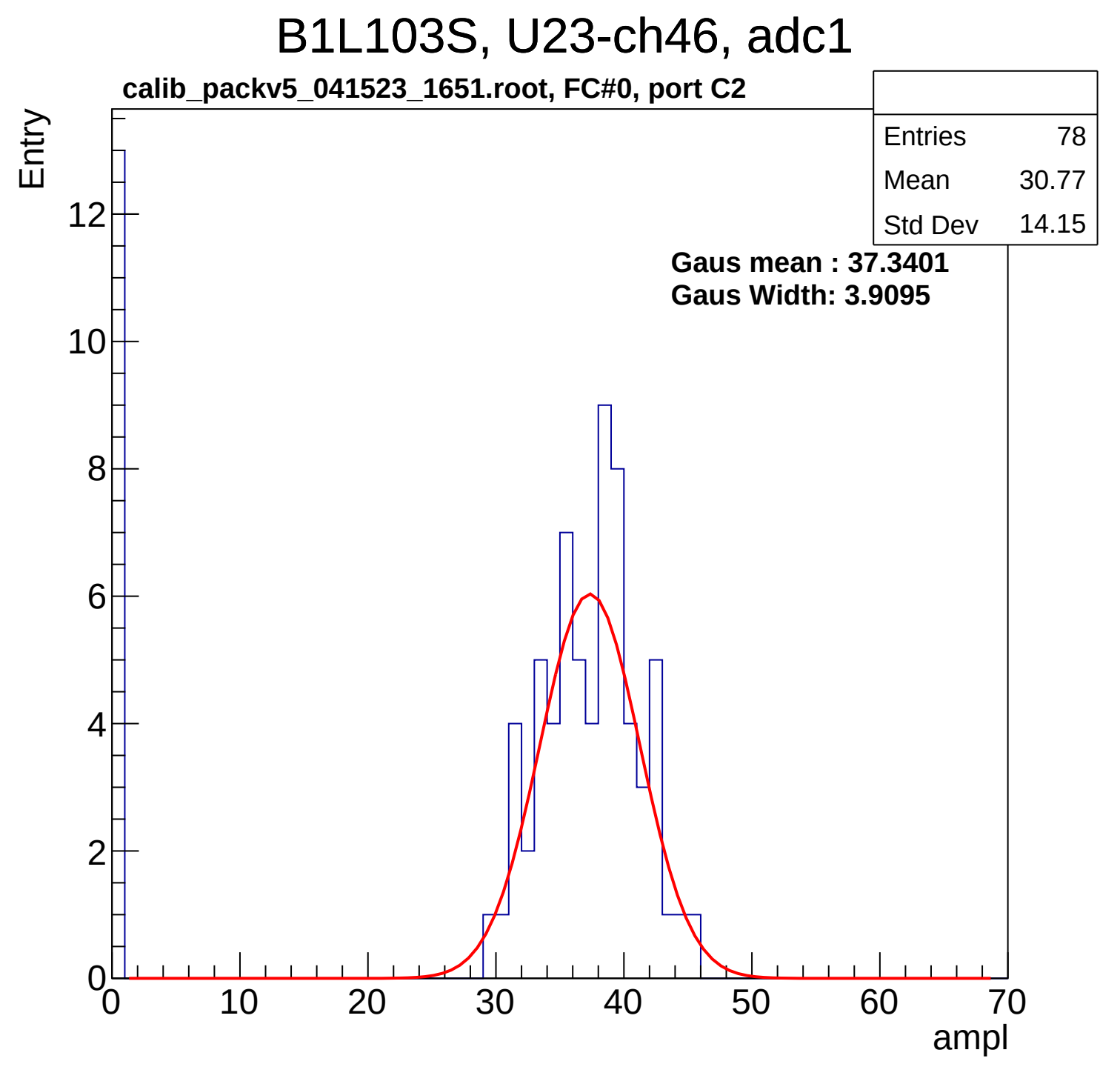

B1L103S, U23-ch45, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 54.9
Std Dev 2.983
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

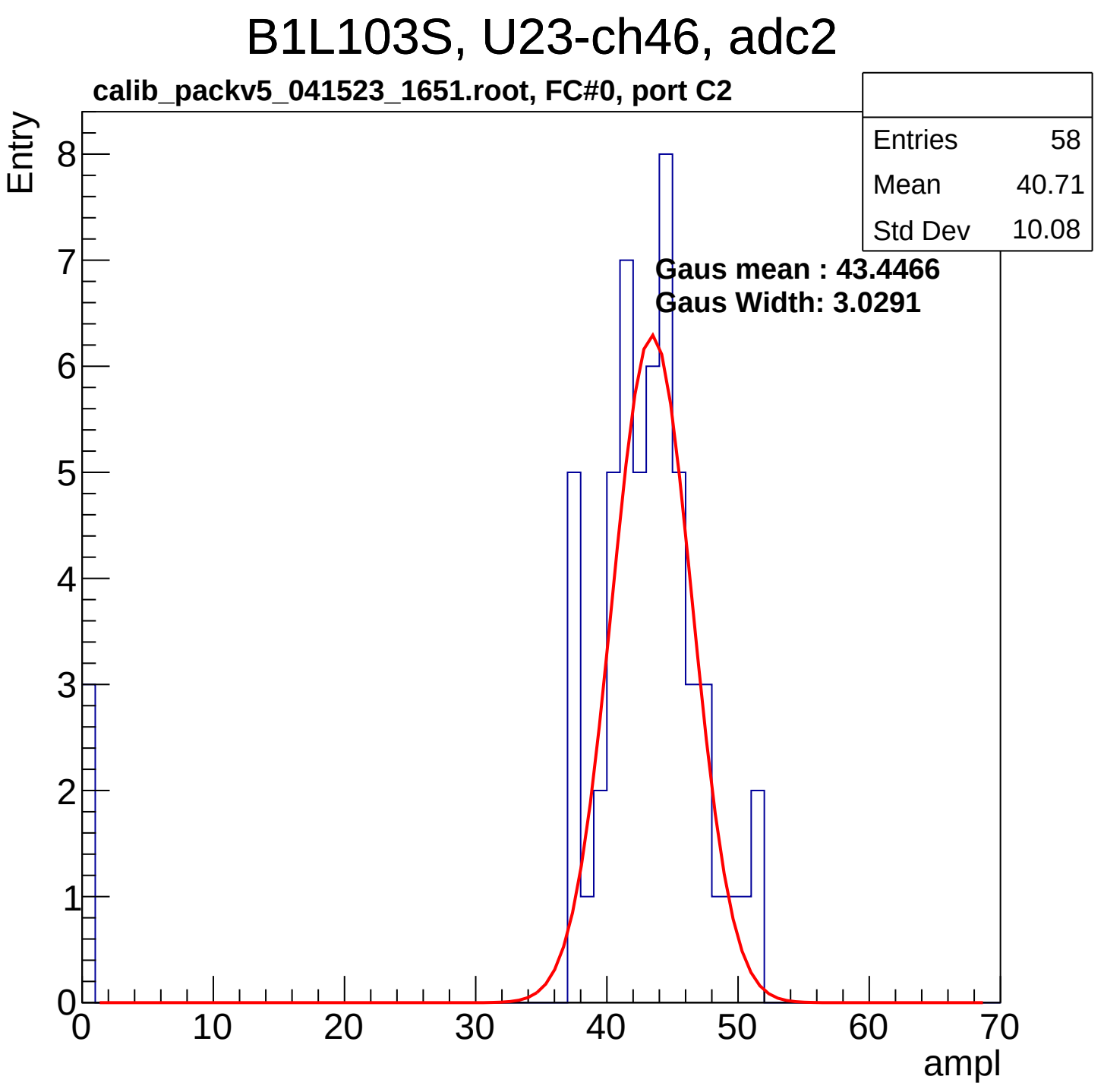

B1L103S, U23-ch45, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 59.16
Std Dev 8.763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

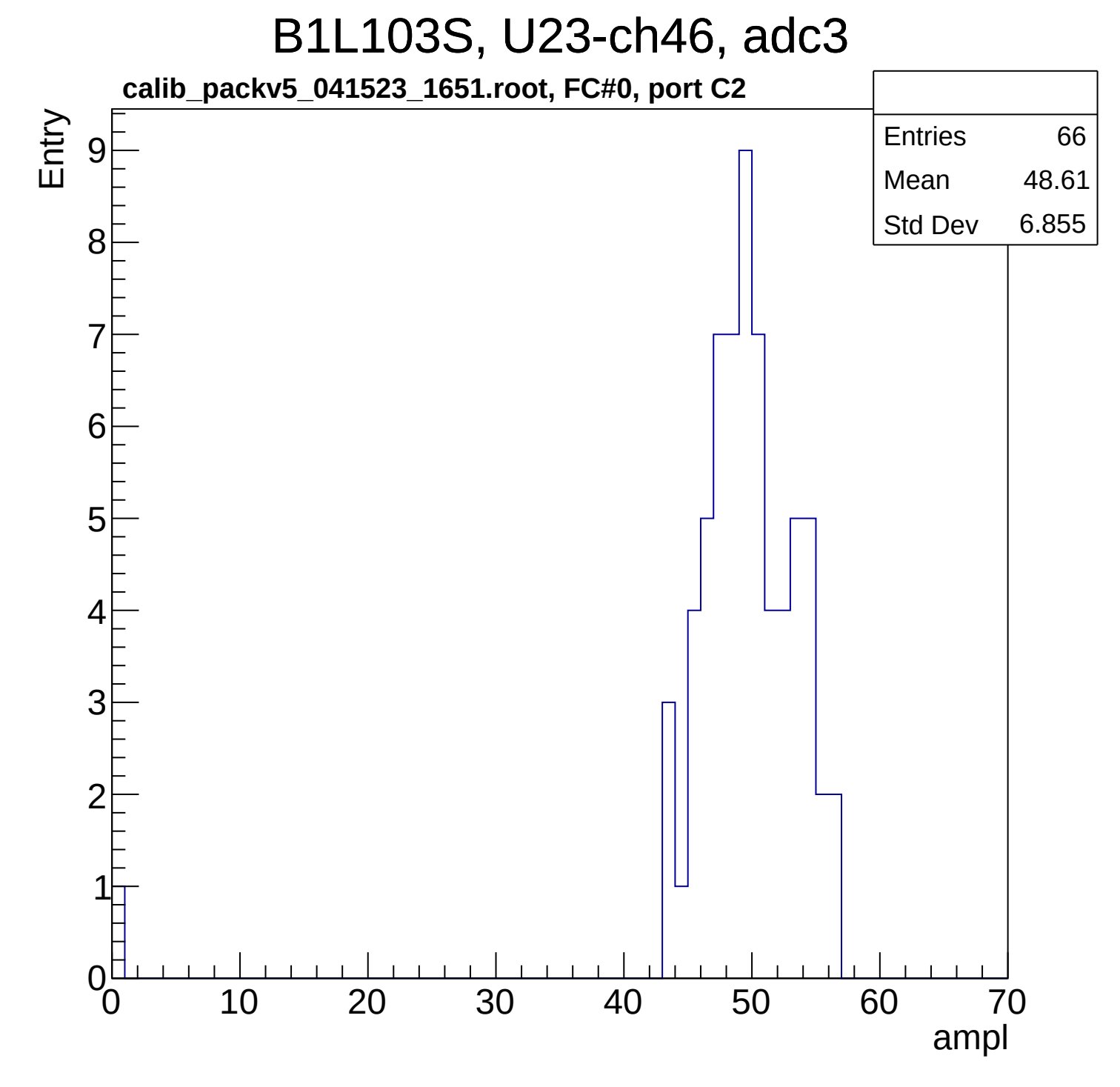

B1L103S, U23-ch45, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

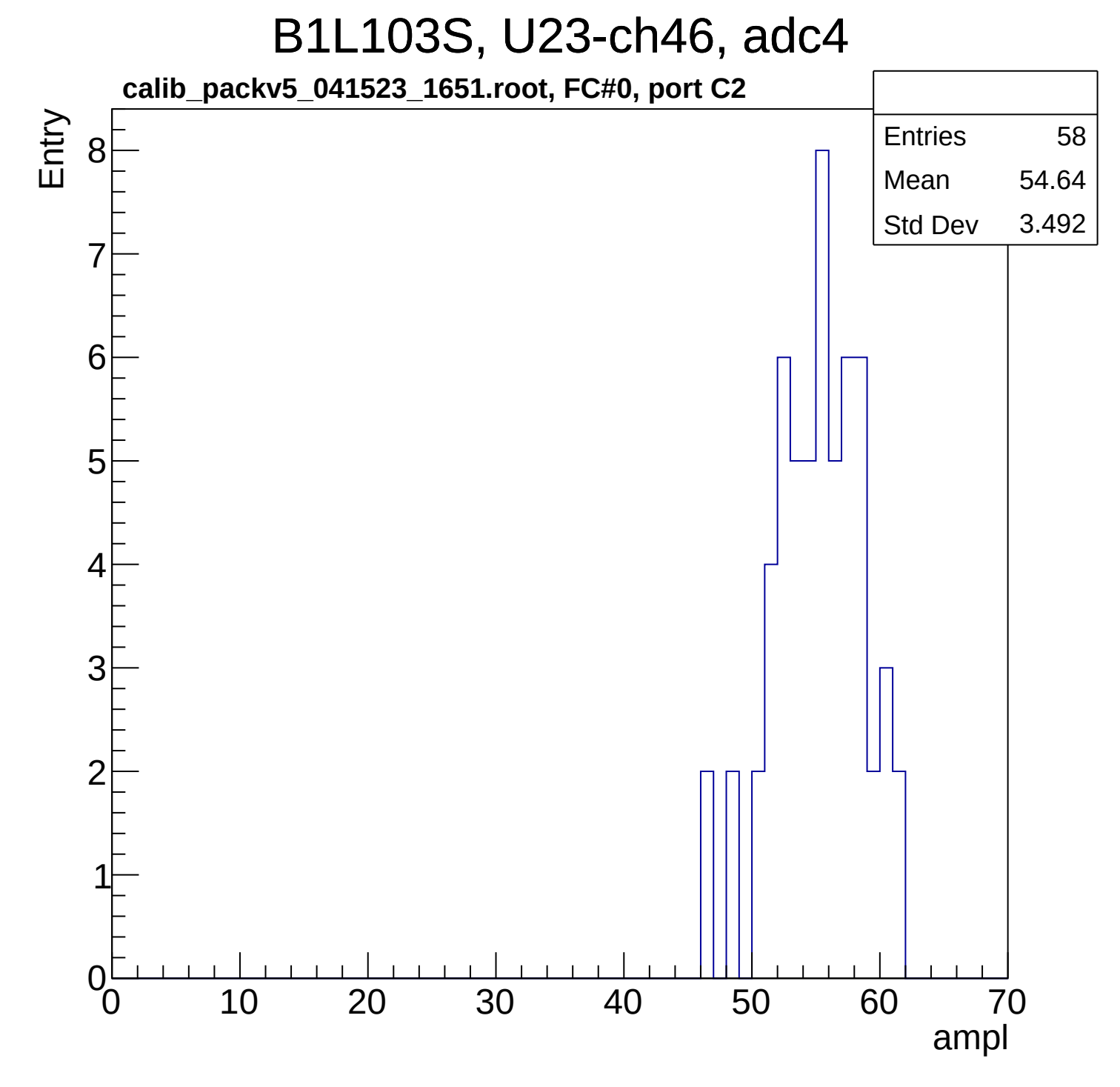

B1L103S, U23-ch45, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

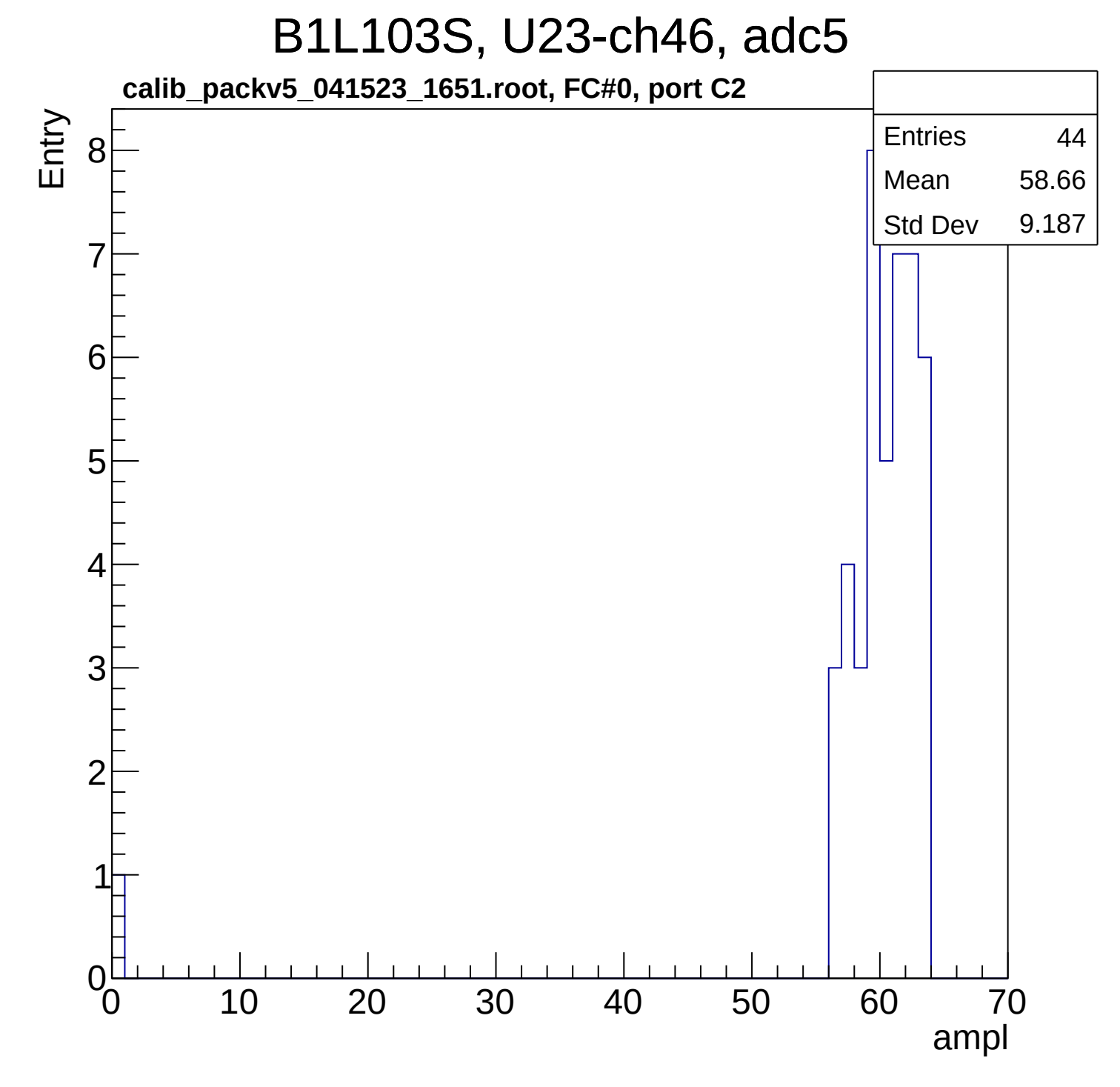

B1L103S, U23-ch46, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 23.22
Std Dev 13.08
Gaus mean : 30.3787
Gaus Width: 3.9304
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
24
0
10
20
30
40
50
60
70

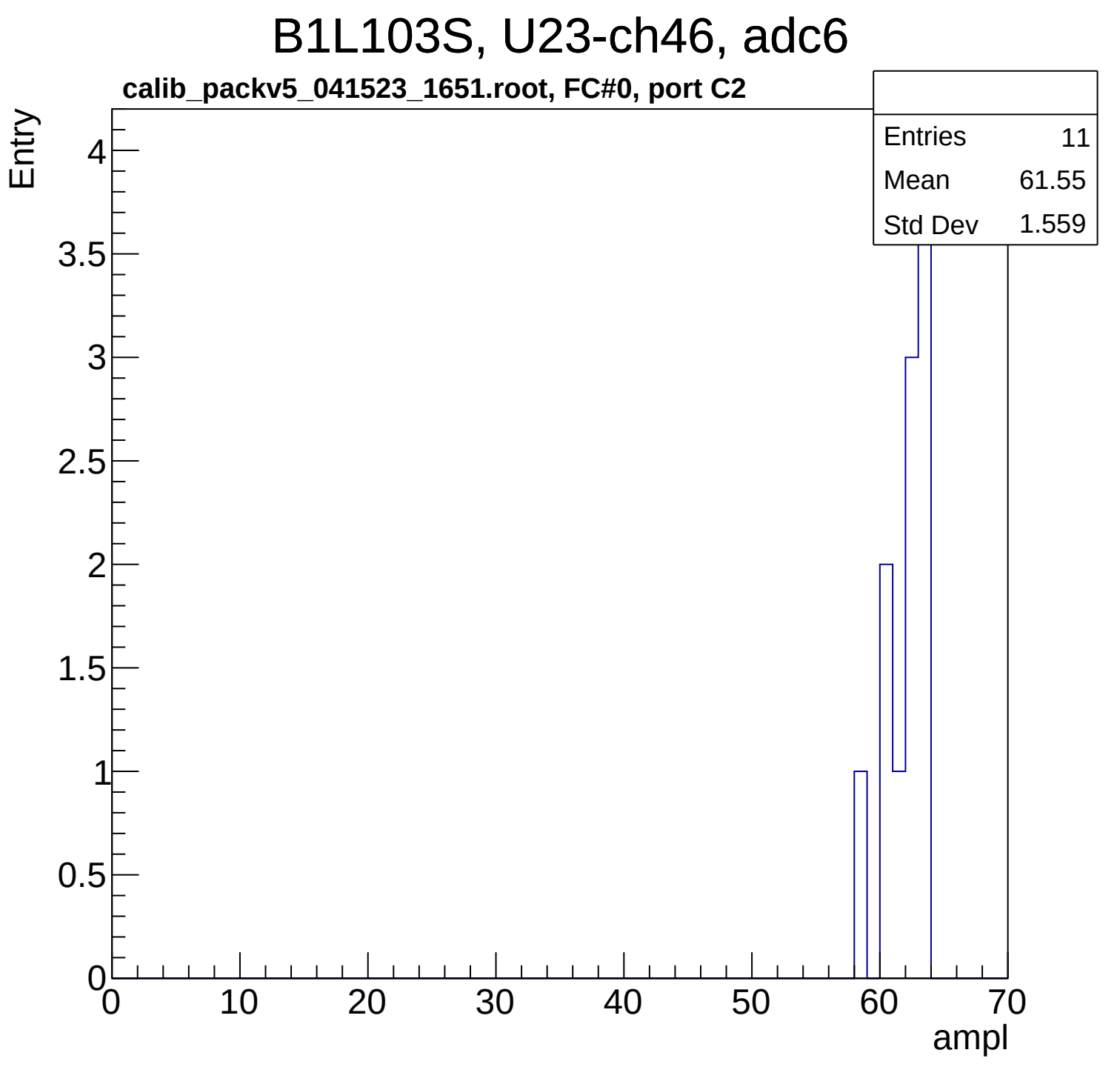

B1L103S, U23-ch46, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 30.77
Std Dev 14.15
Gaus mean : 37.3401
Gaus Width: 3.9095
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

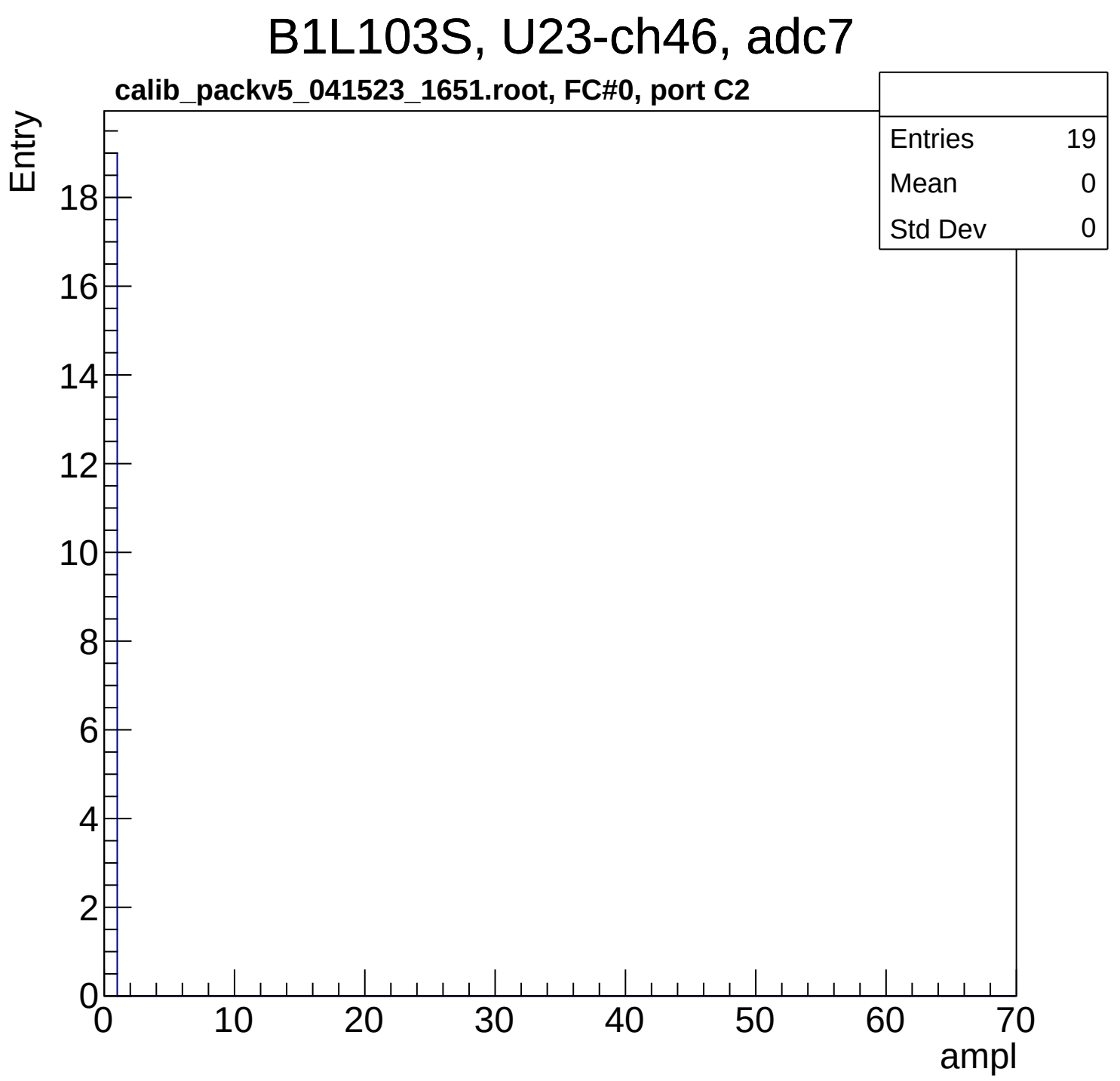

B1L103S, U23-ch46, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 40.71
Std Dev 10.08
Gaus mean : 43.4466
Gaus Width: 3.0291
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

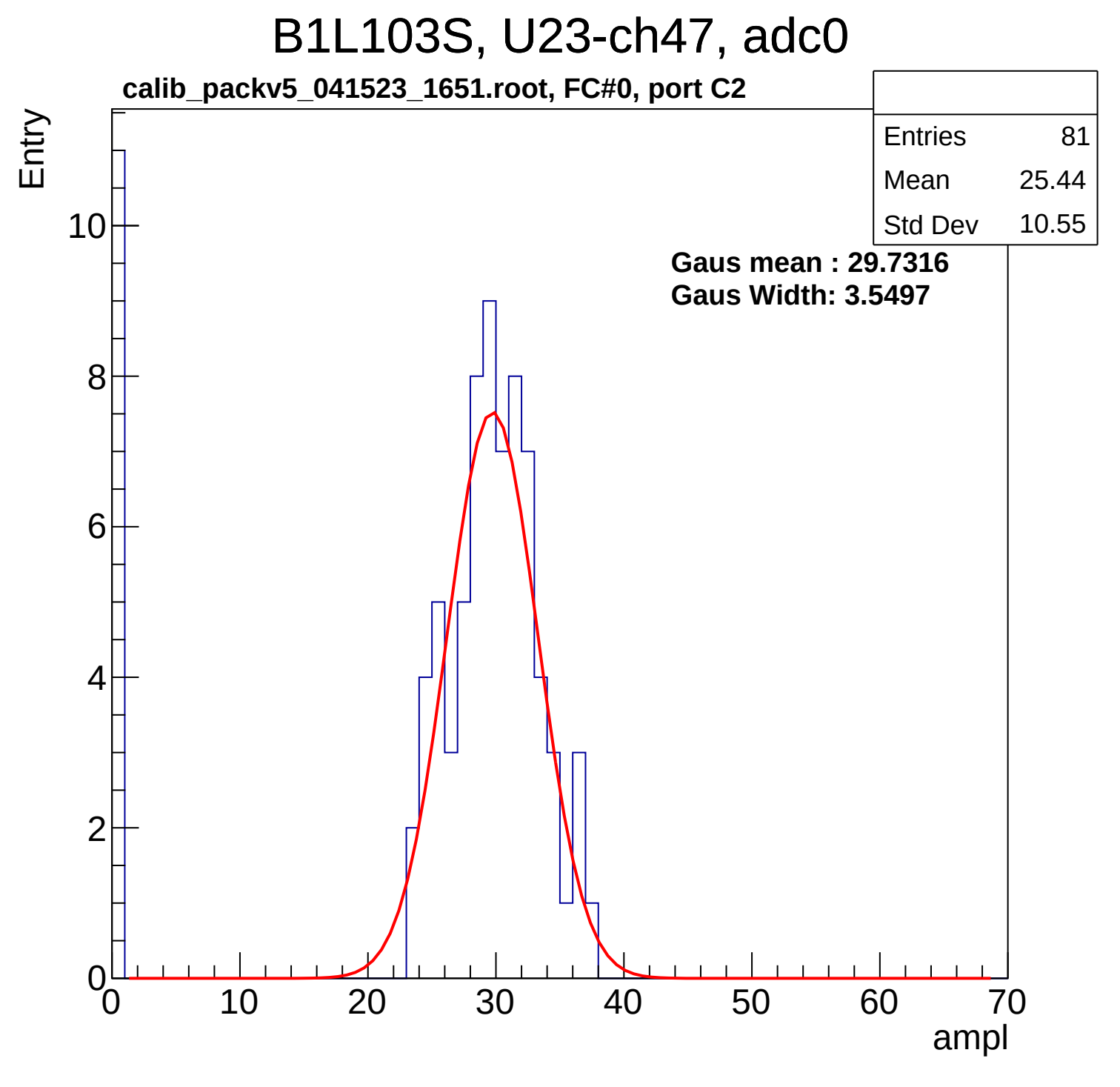

B1L103S, U23-ch46, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 48.61
Std Dev 6.855
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

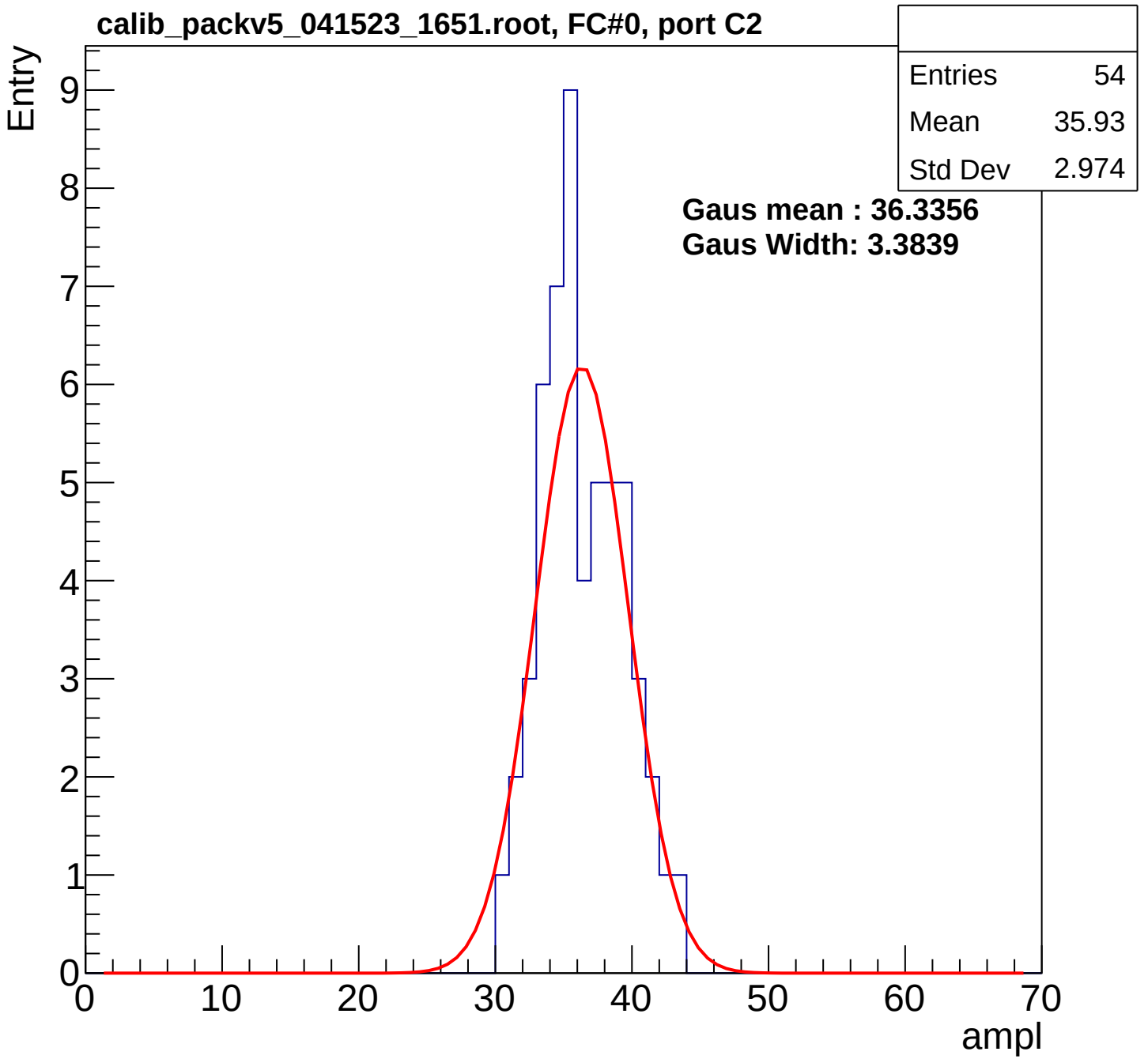

B1L103S, U23-ch46, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 54.64
Std Dev 3.492
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

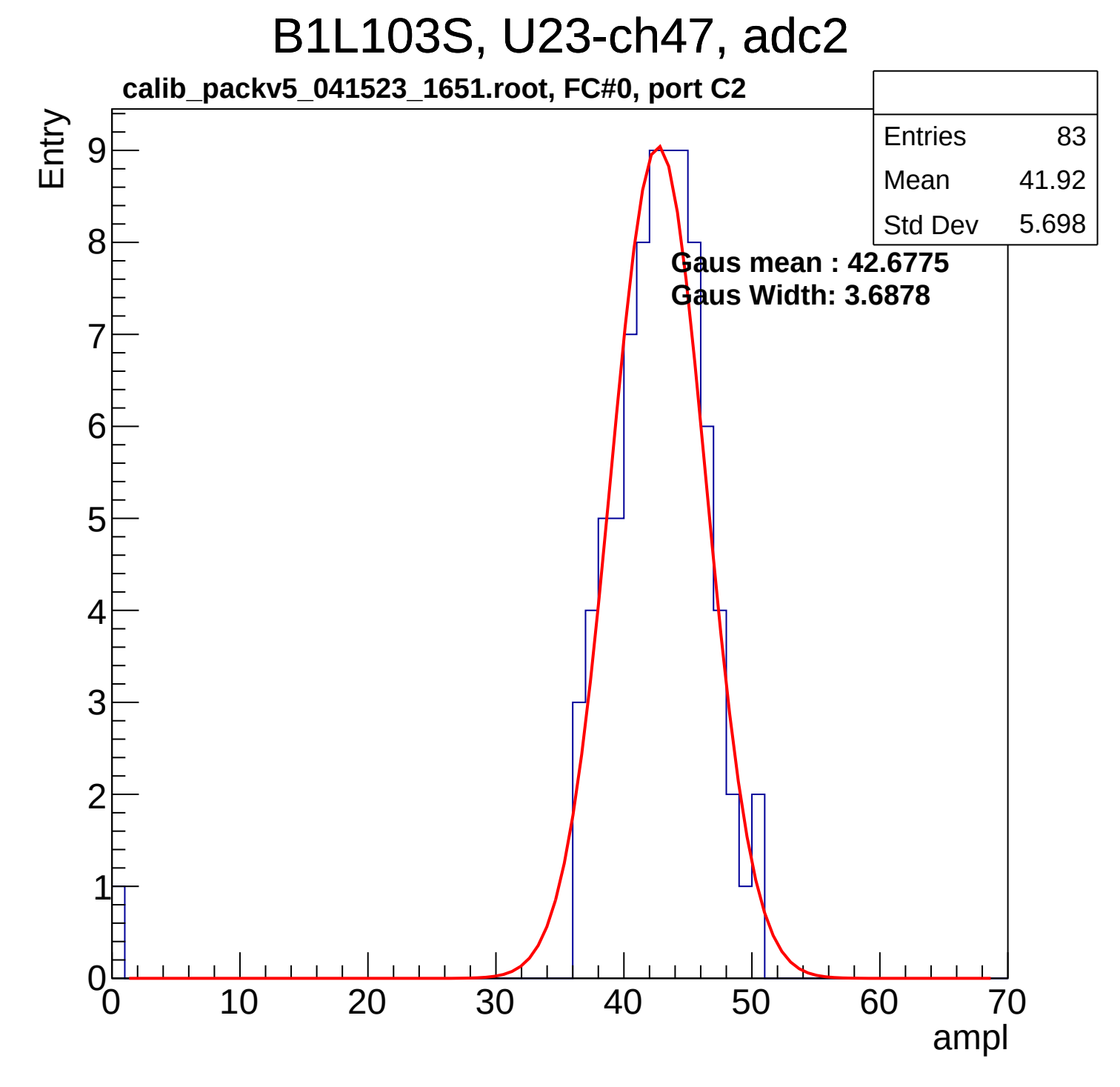

B1L103S, U23-ch46, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 44
Mean 58.66
Std Dev 9.187
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

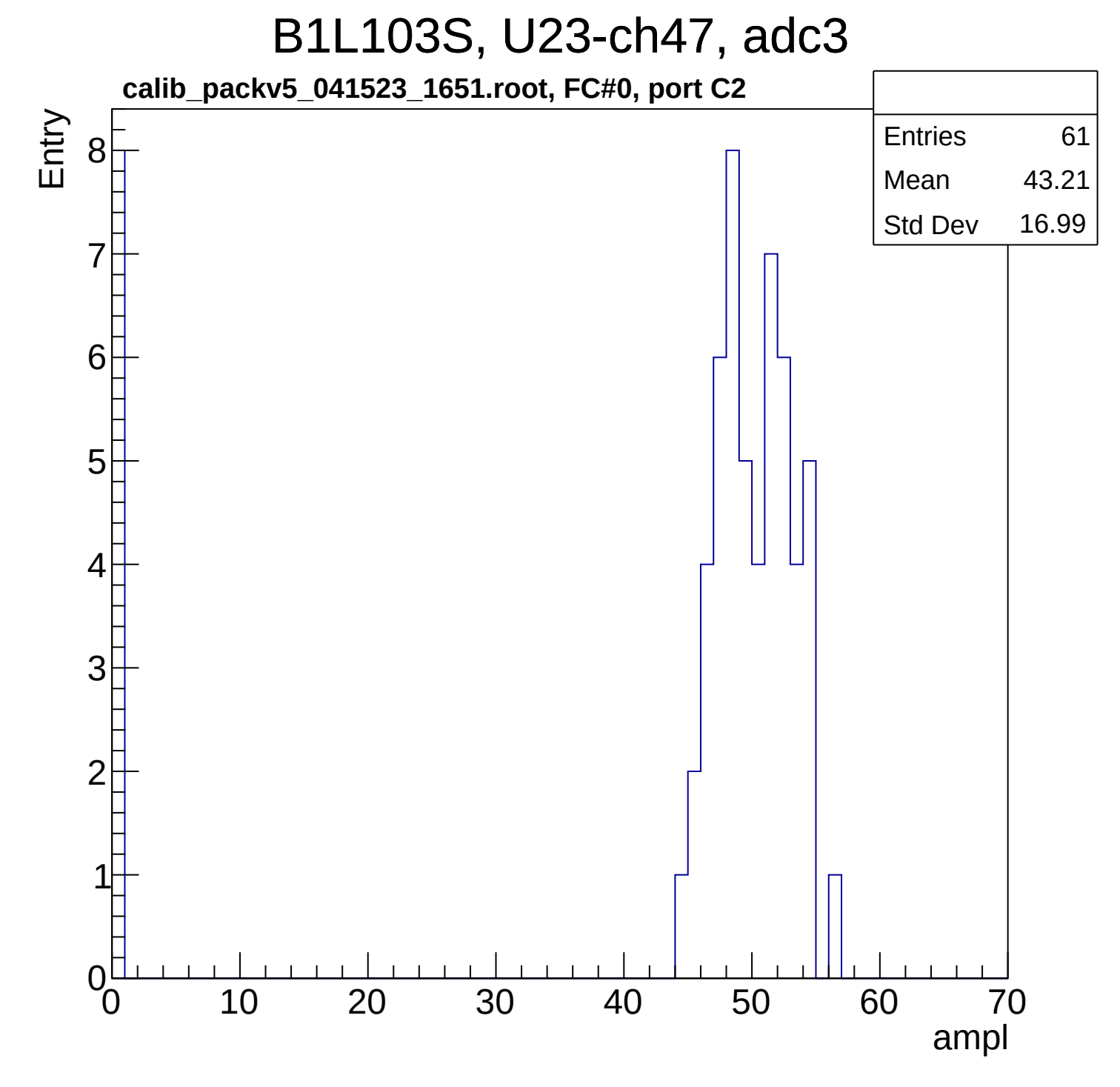

B1L103S, U23-ch46, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 61.55
Std Dev 1.559
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

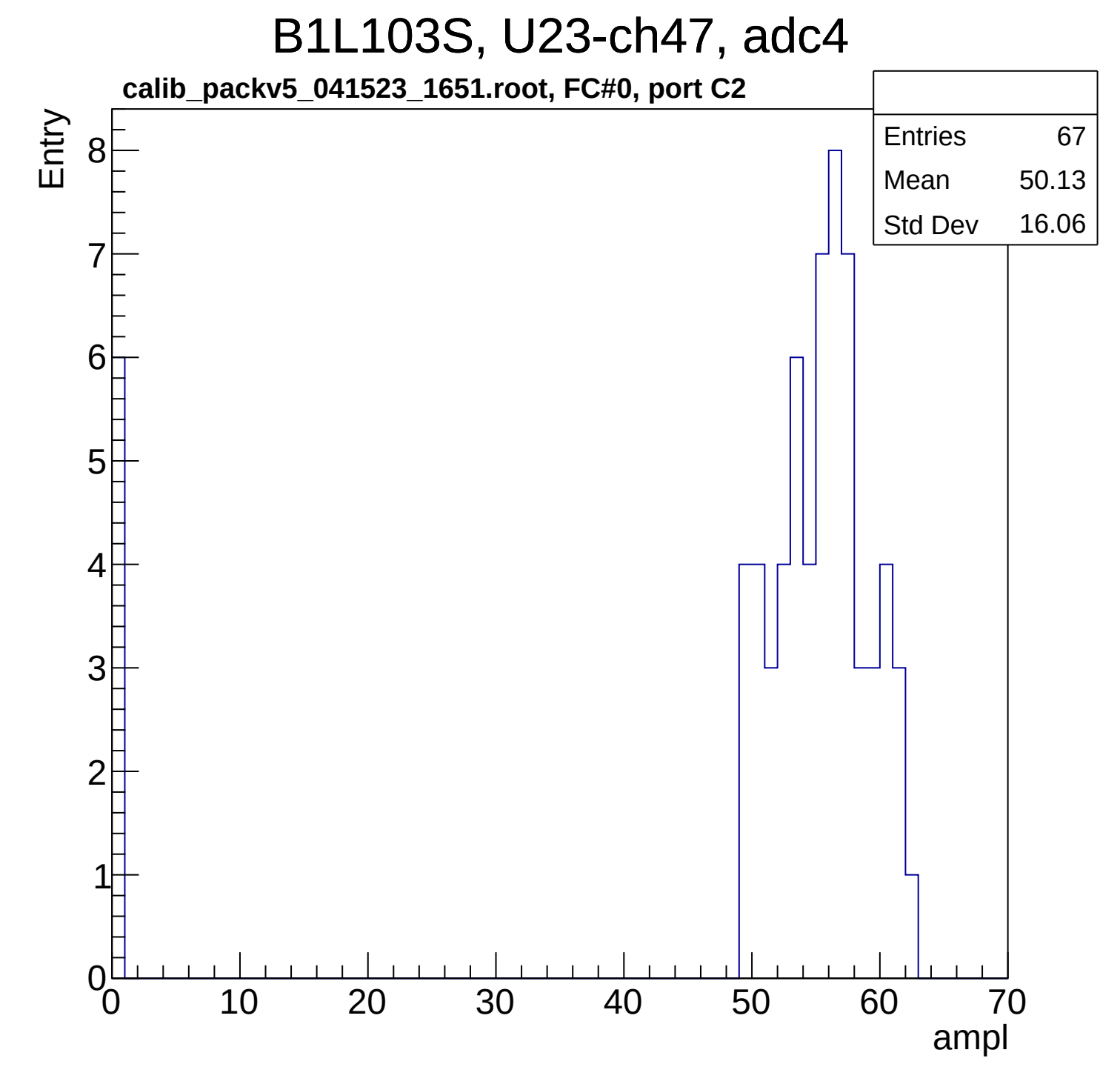

B1L103S, U23-ch46, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

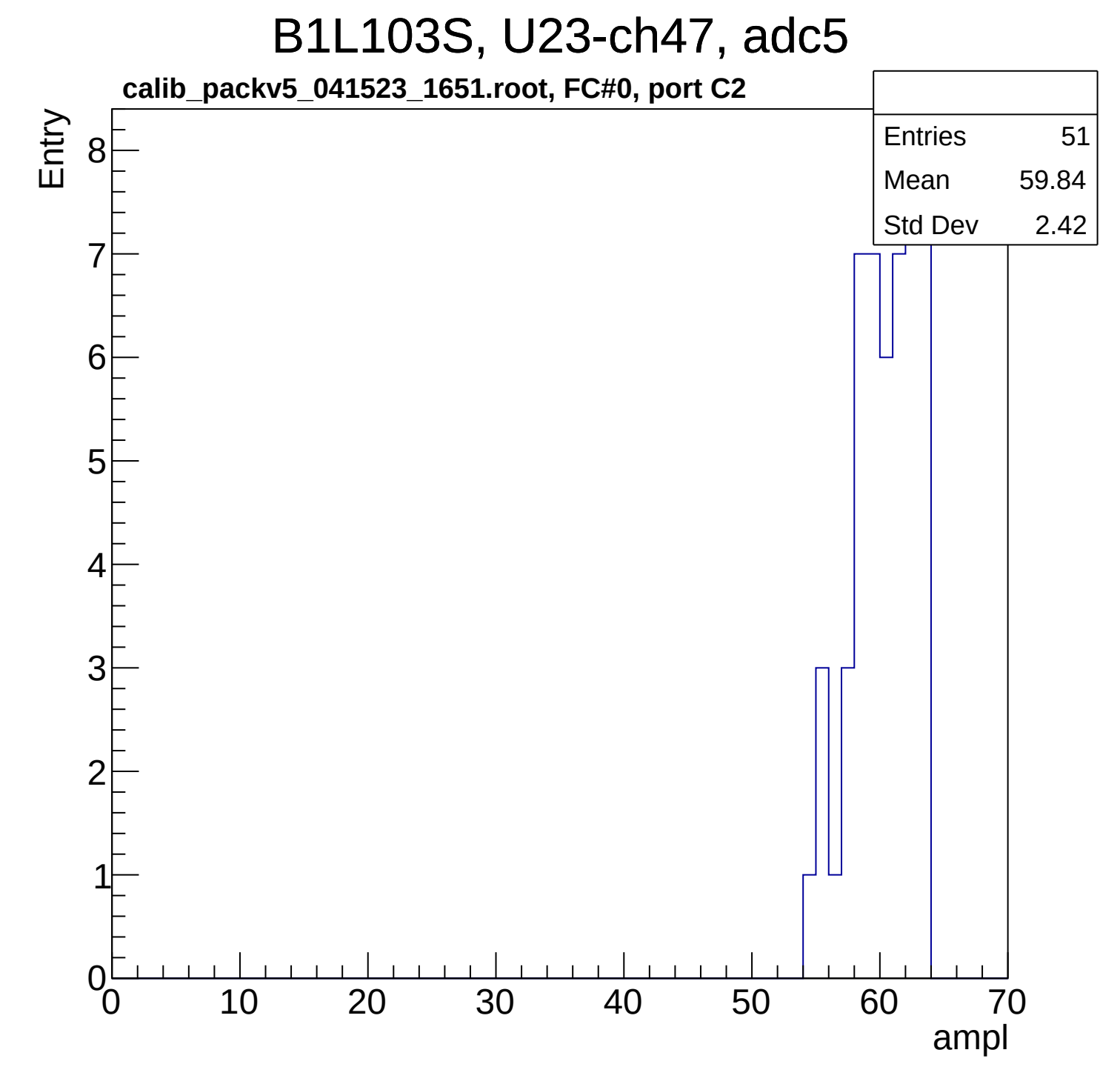

B1L103S, U23-ch47, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 25.44
Std Dev 10.55
Gaus mean : 29.7316
Gaus Width: 3.5497
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

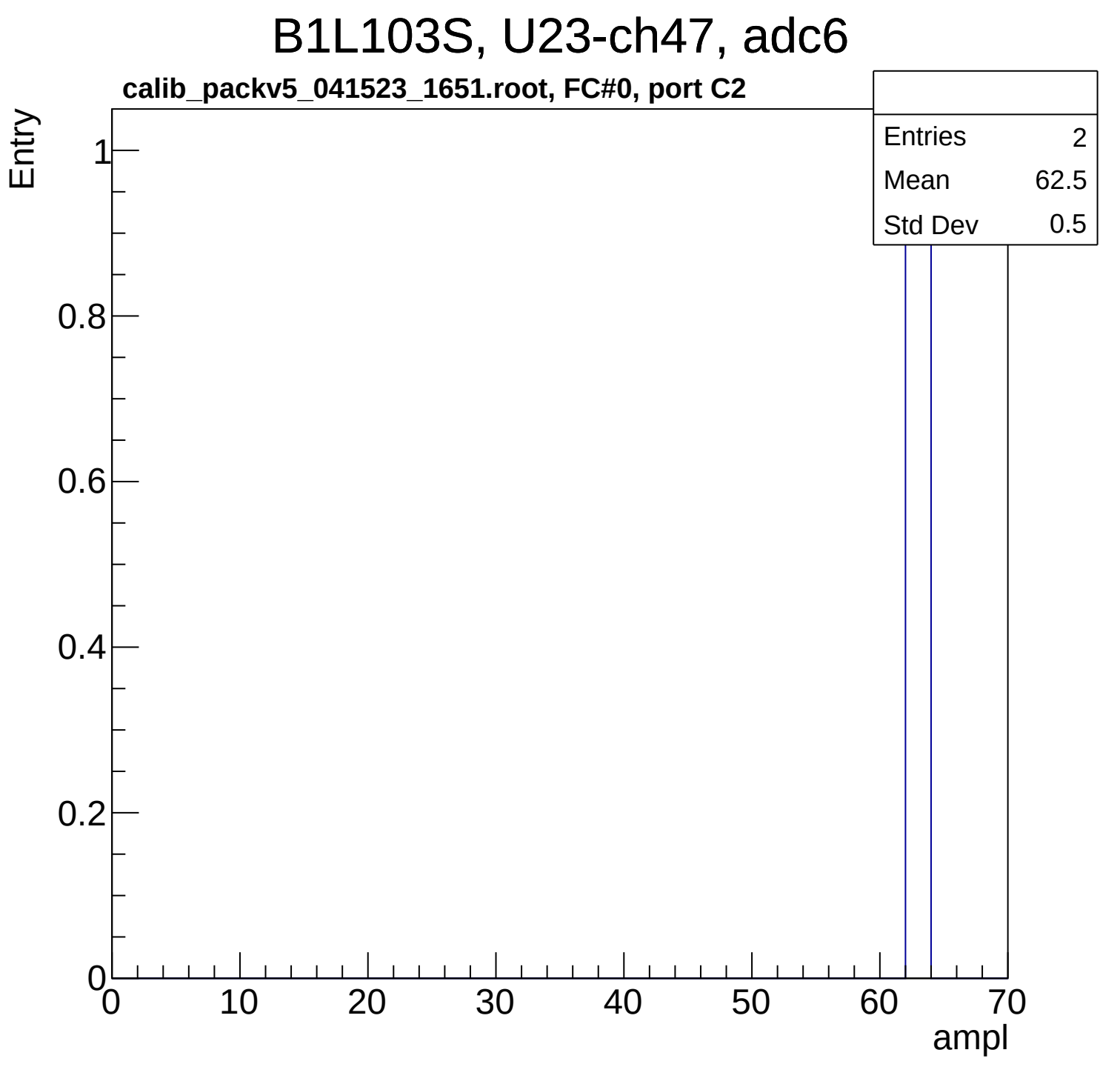

B1L103S, U23-ch47, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 35.93
Std Dev 2.974
Gaus mean : 36.3356
Gaus Width: 3.3839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

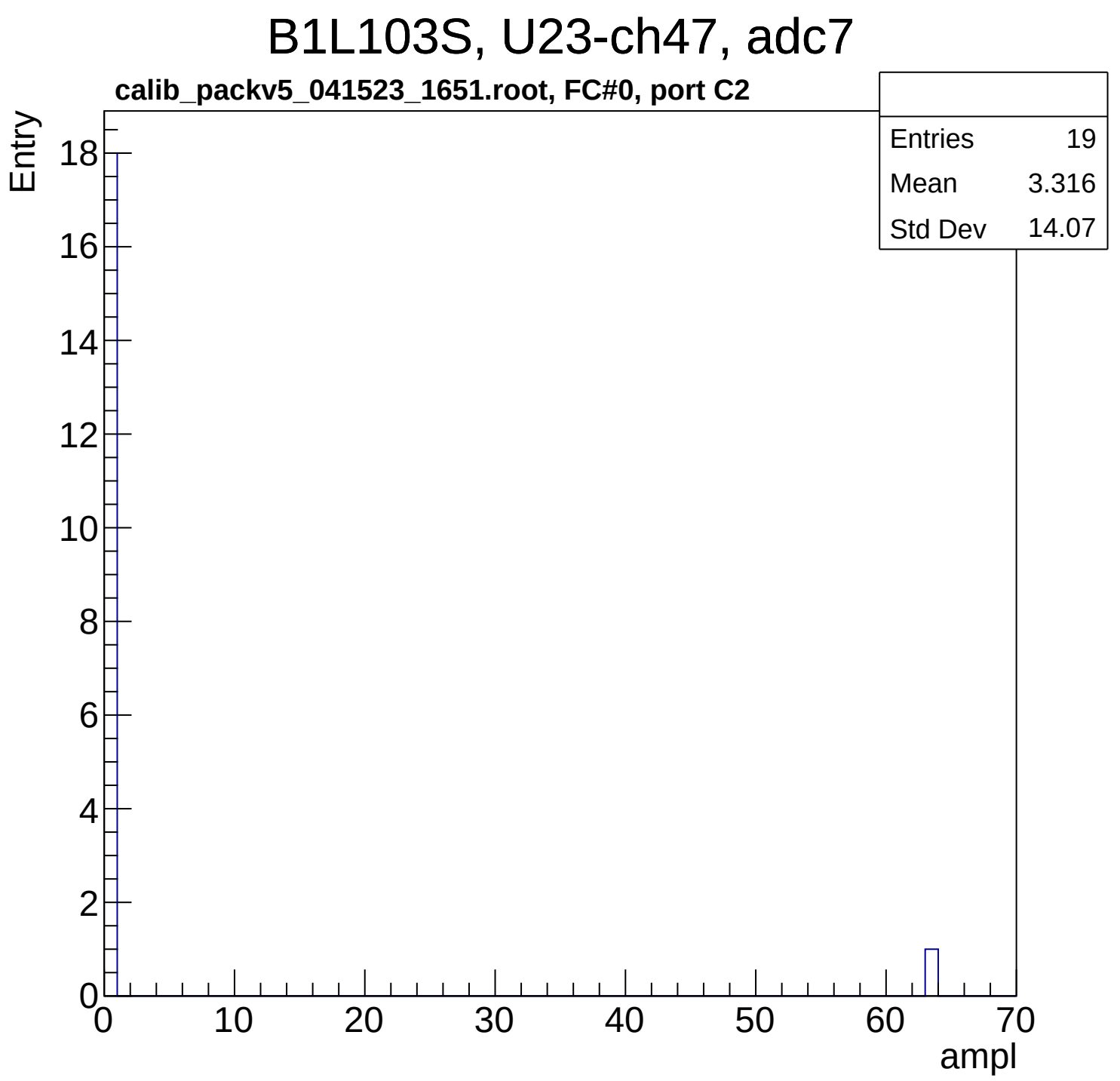

B1L103S, U23-ch47, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 41.92
Std Dev 5.698
Gaus mean : 42.6775
Gaus Width: 3.6878
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

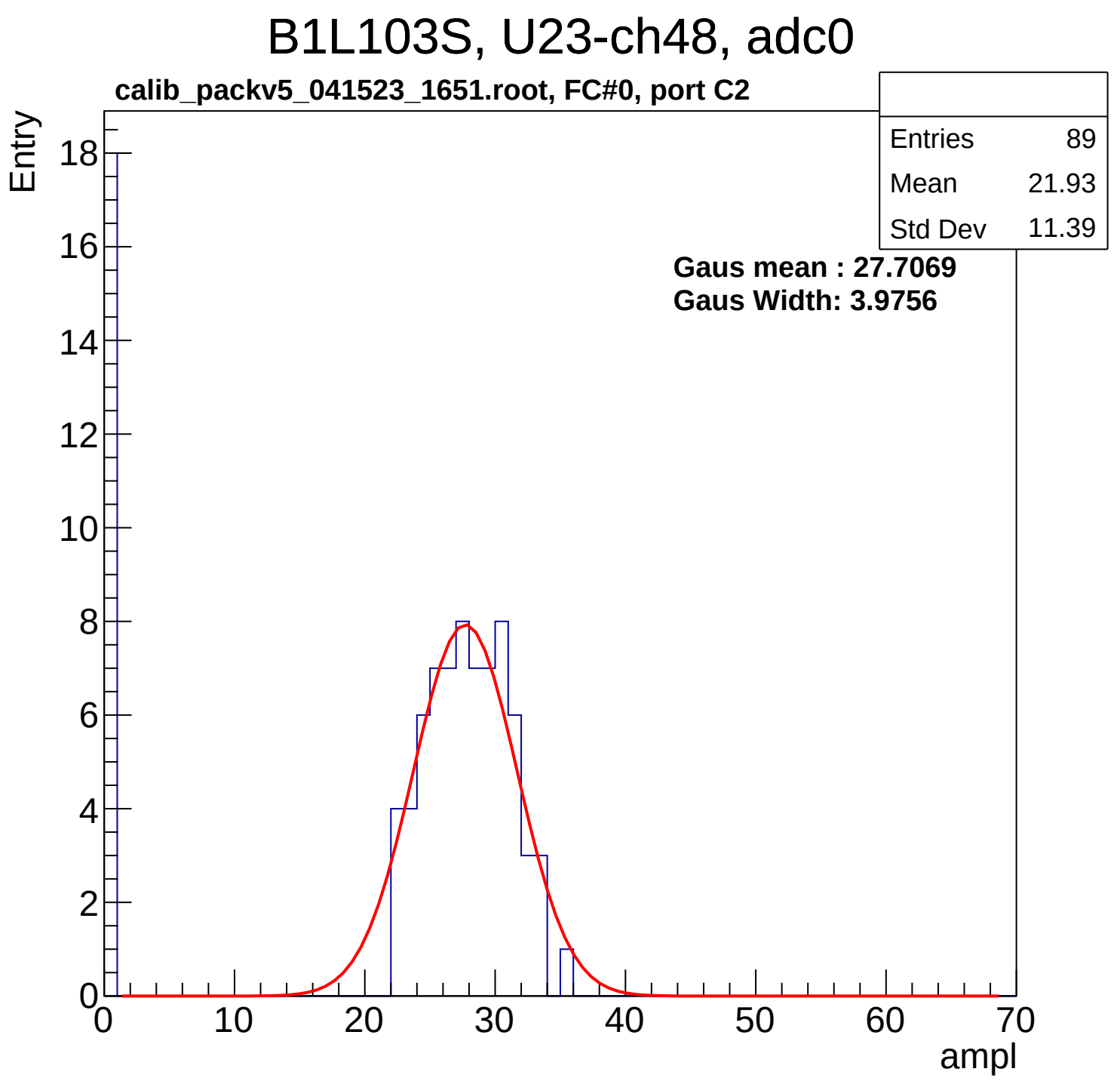

B1L103S, U23-ch47, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 43.21
Std Dev 16.99
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

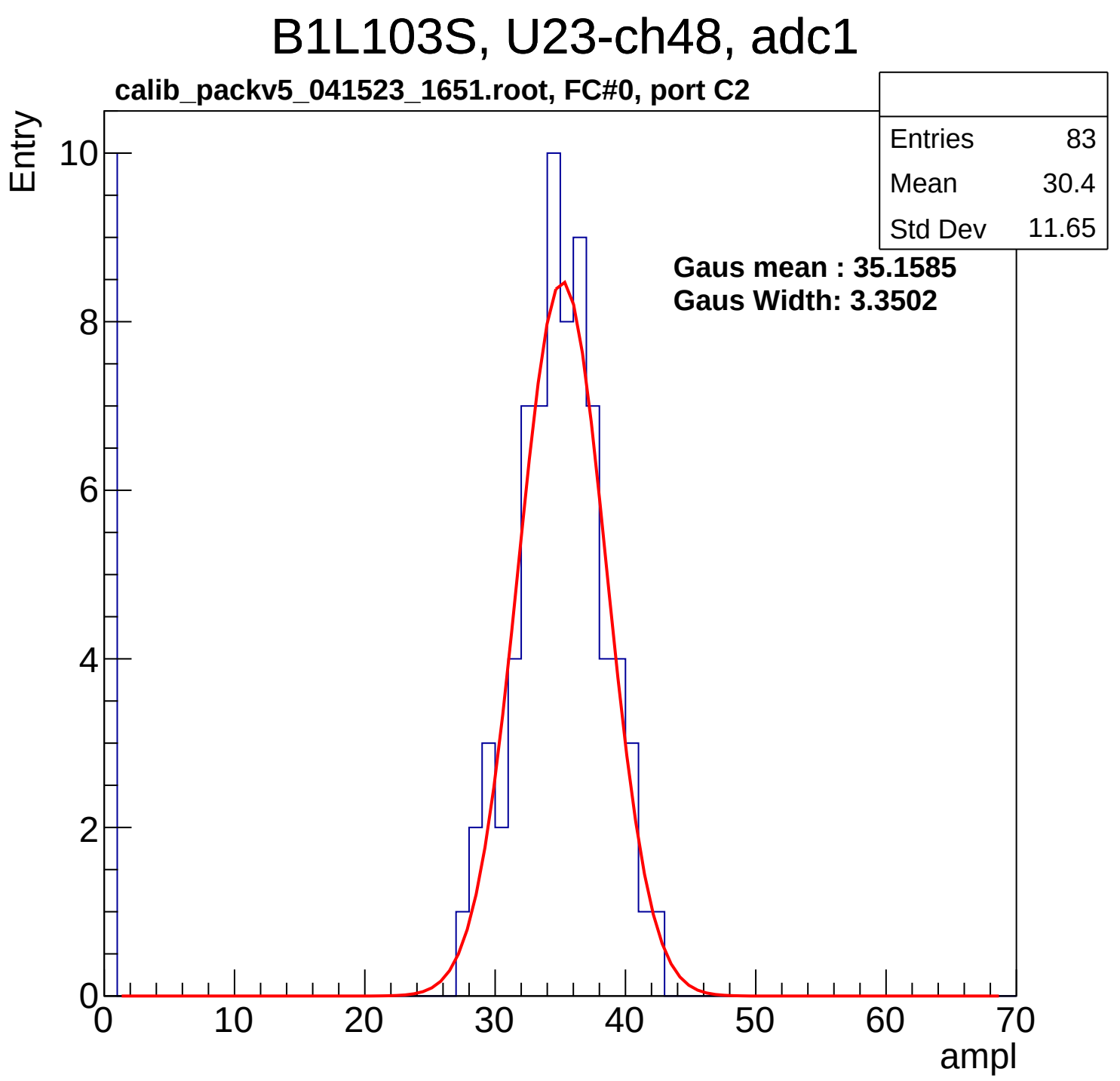

B1L103S, U23-ch47, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 50.13
Std Dev 16.06
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

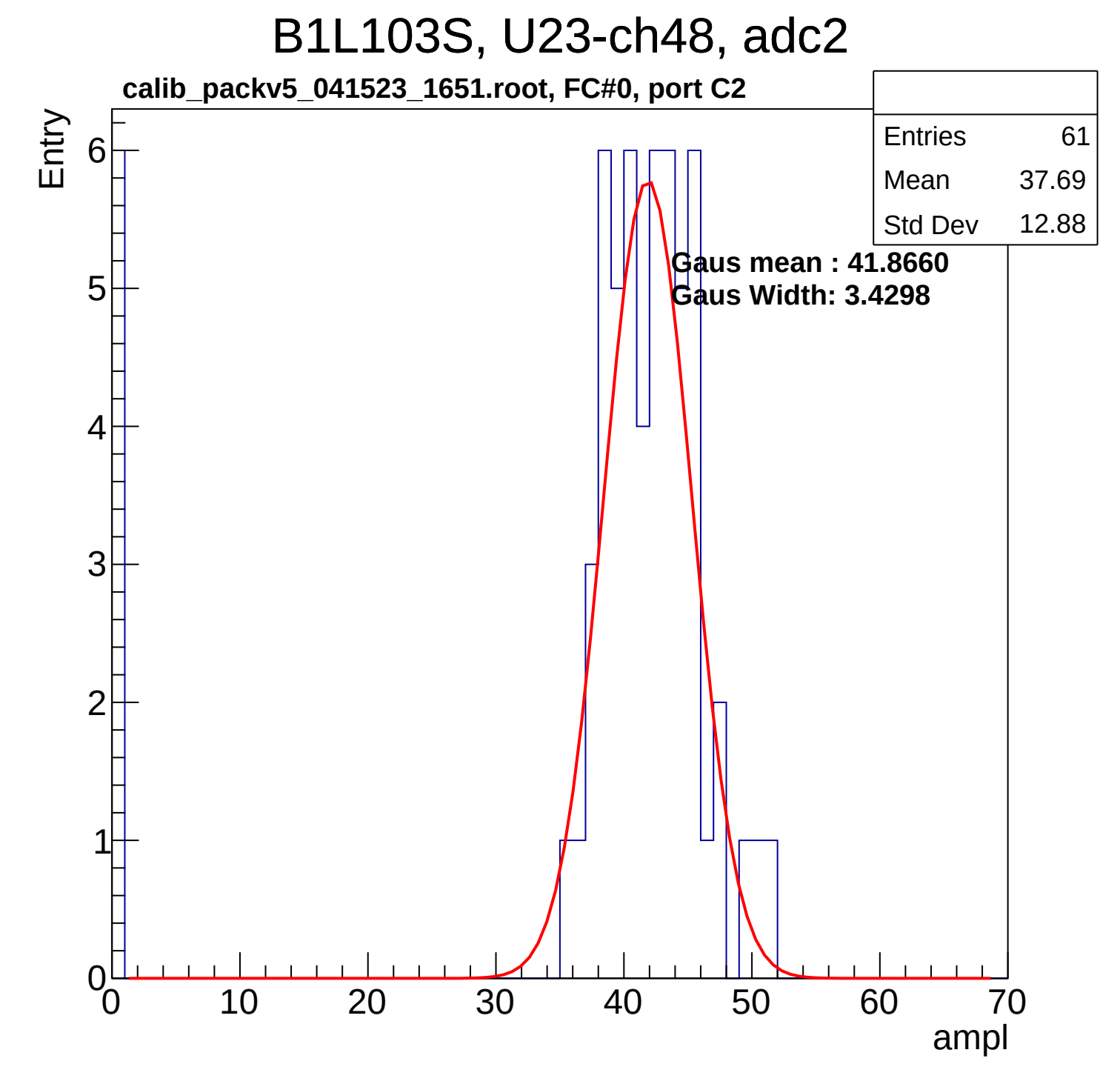

B1L103S, U23-ch47, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 59.84
Std Dev 2.42
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

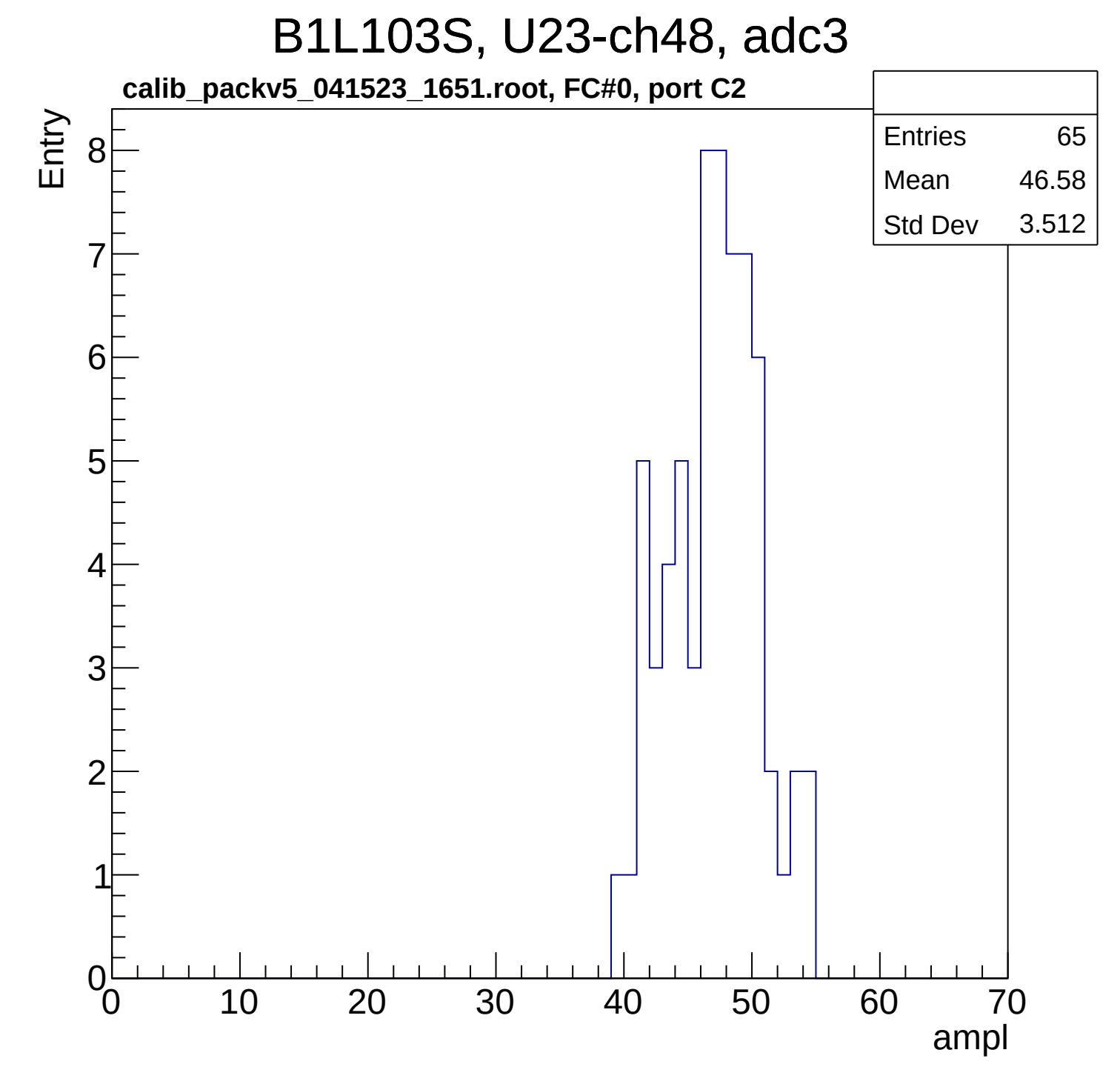

B1L103S, U23-ch47, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

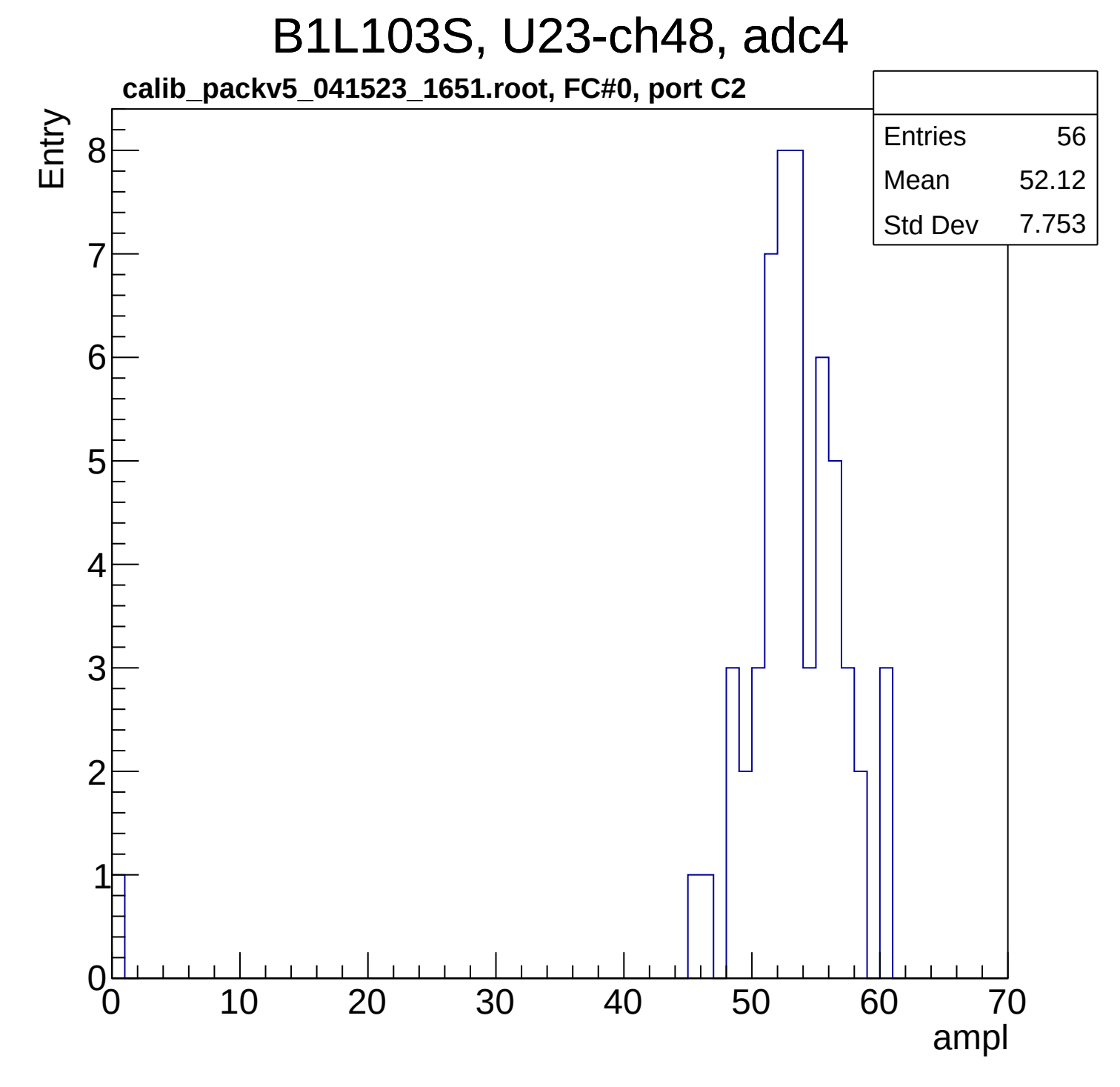

B1L103S, U23-ch47, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 3.316
Std Dev 14.07
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

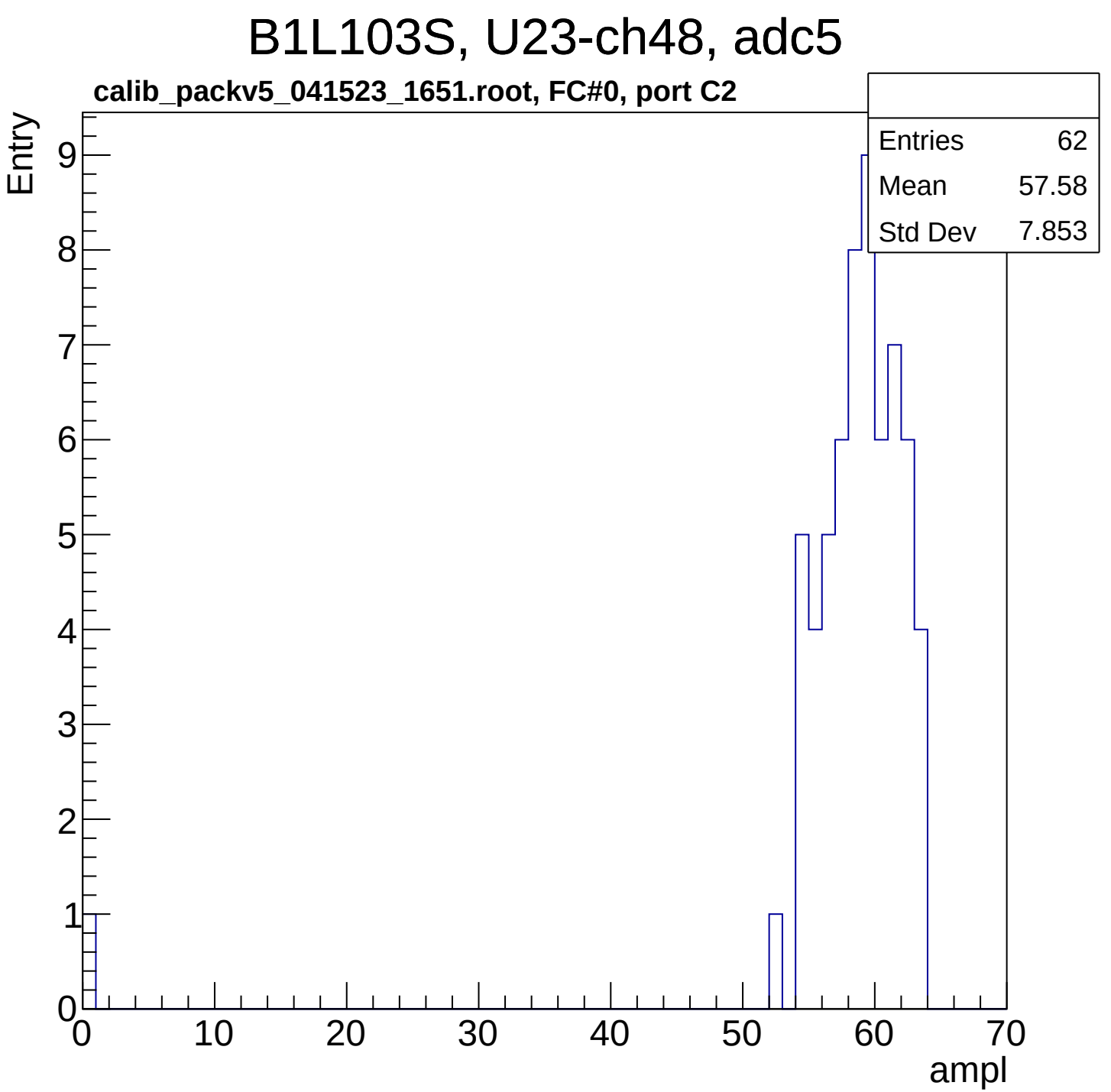

B1L103S, U23-ch48, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 21.93
Std Dev 11.39
Gaus mean : 27.7069
Gaus Width: 3.9756
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

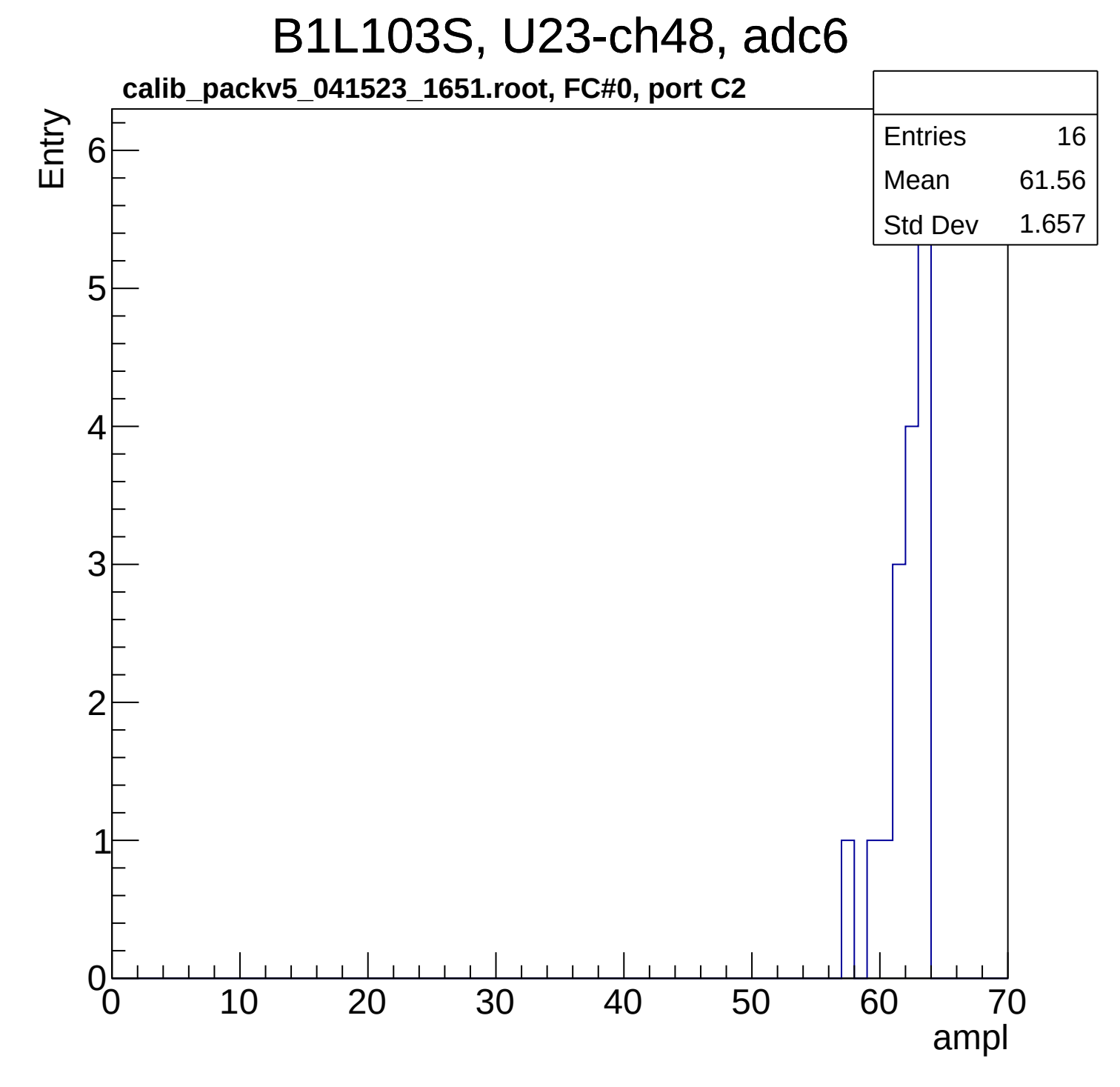

B1L103S, U23-ch48, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 30.4
Std Dev 11.65
Gaus mean : 35.1585
Gaus Width: 3.3502
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

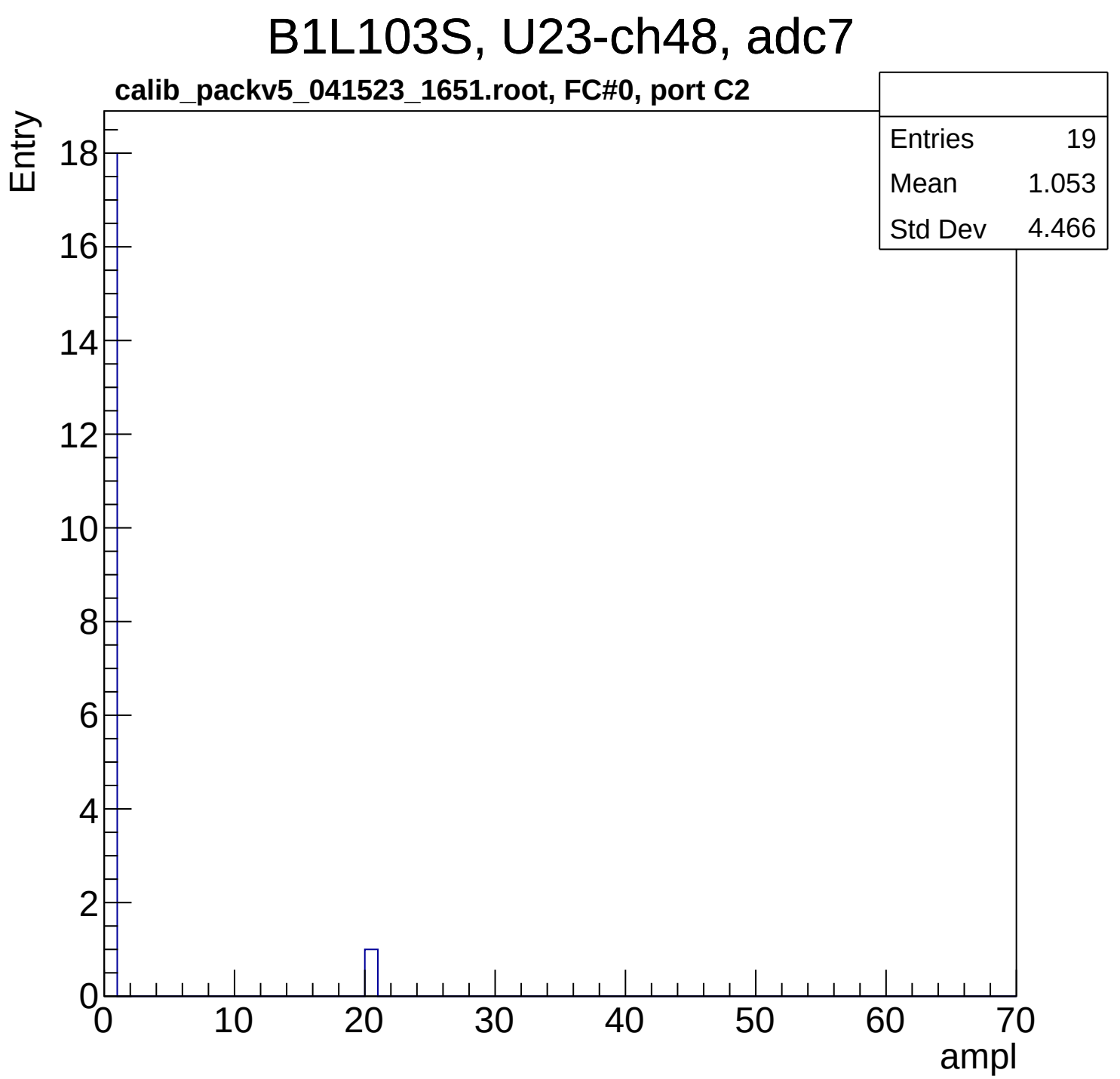

B1L103S, U23-ch48, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 37.69
Std Dev 12.88
Gaus mean : 41.8660
Gaus Width: 3.4298
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

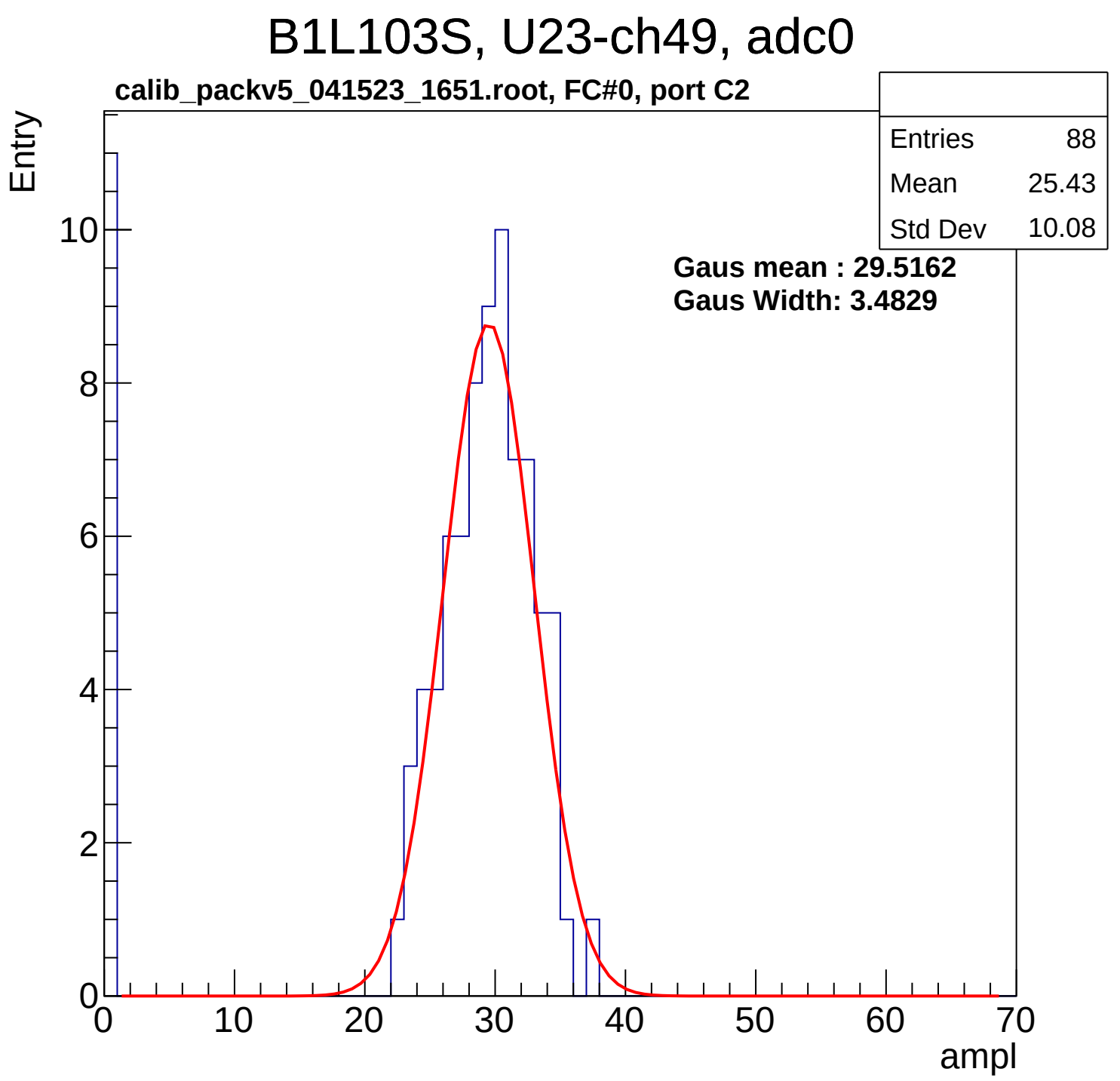

B1L103S, U23-ch48, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 46.58
Std Dev 3.512
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

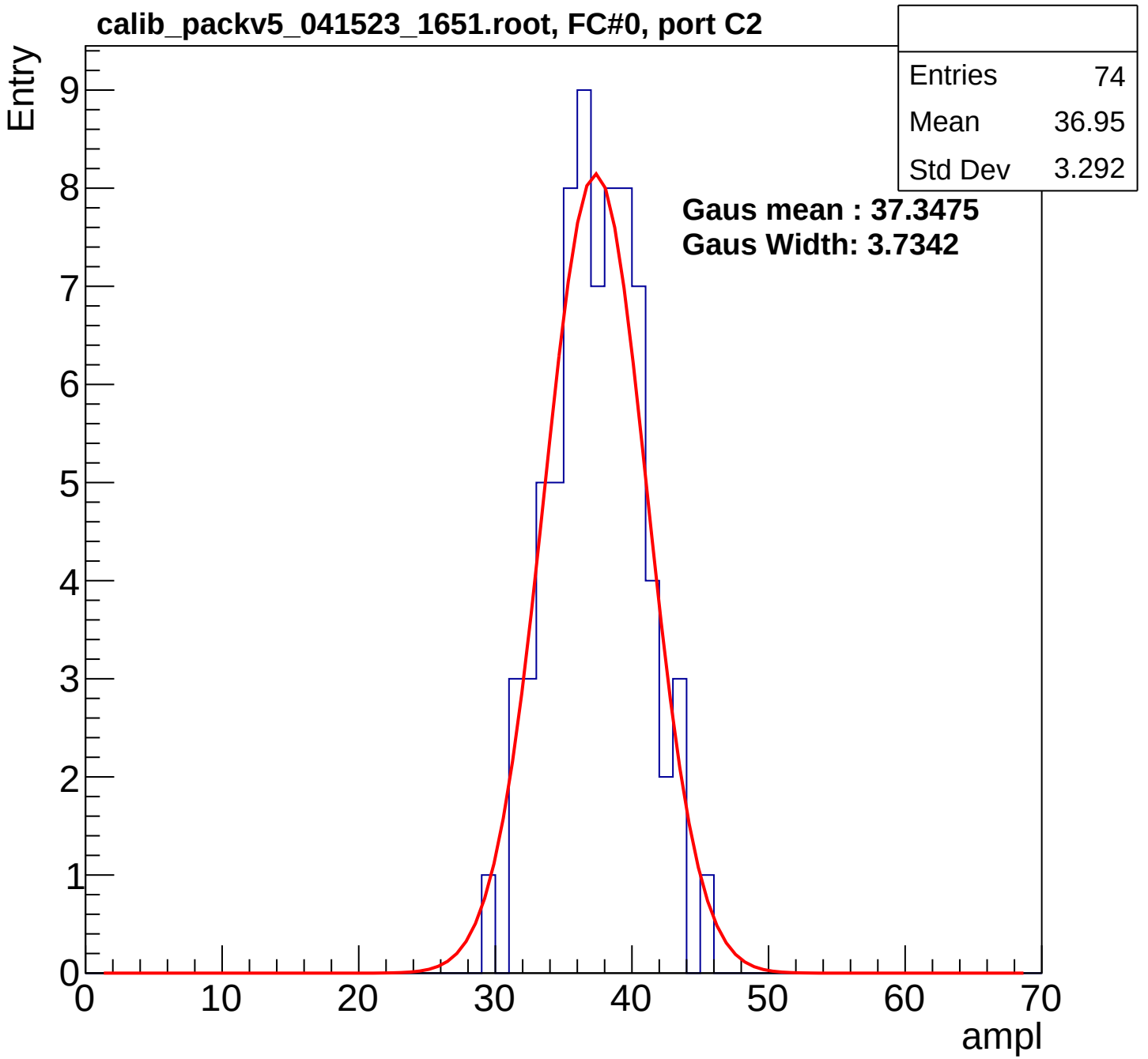

B1L103S, U23-ch48, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 52.12
Std Dev 7.753
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

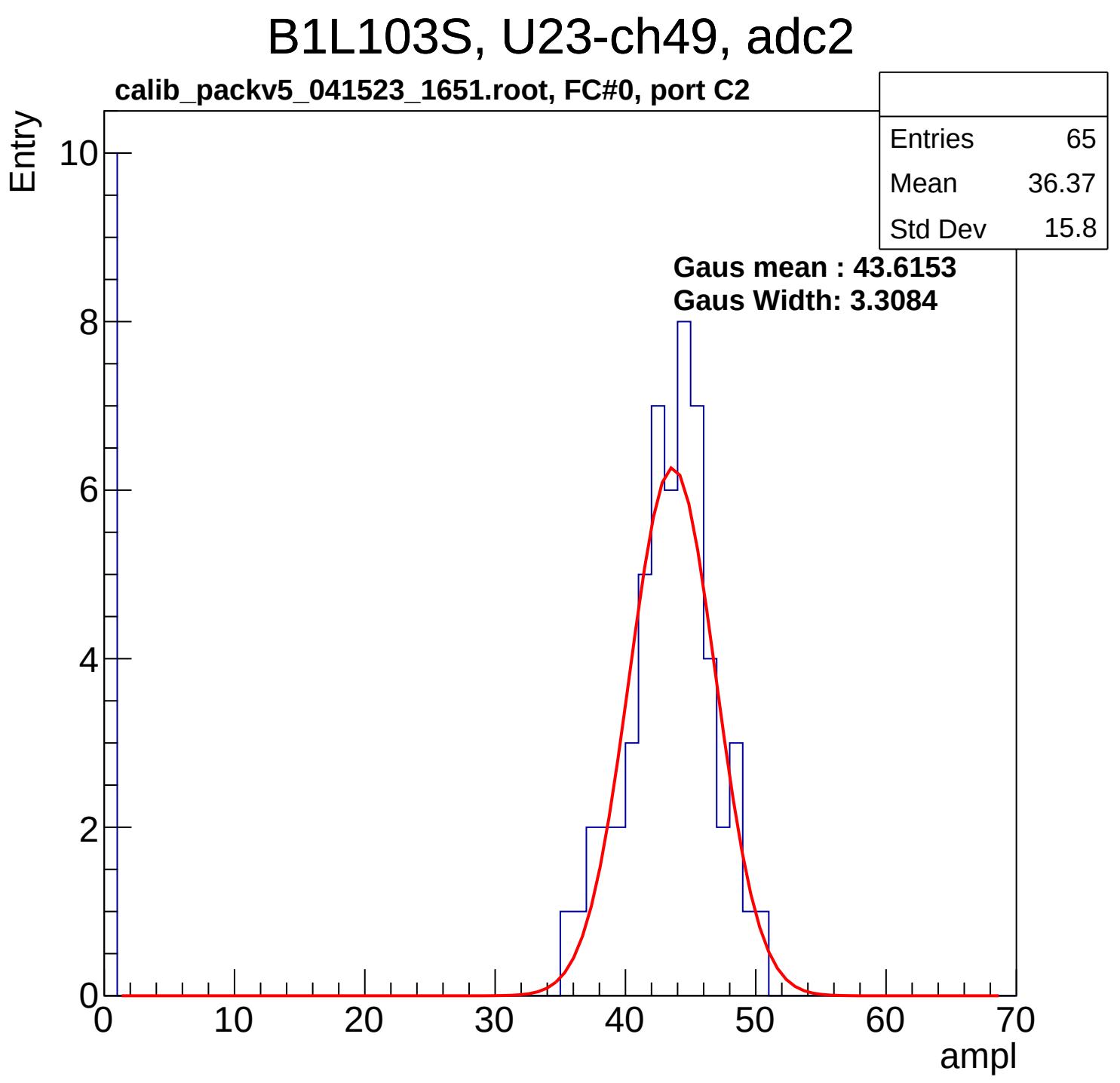

B1L103S, U23-ch48, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 57.58
Std Dev 7.853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

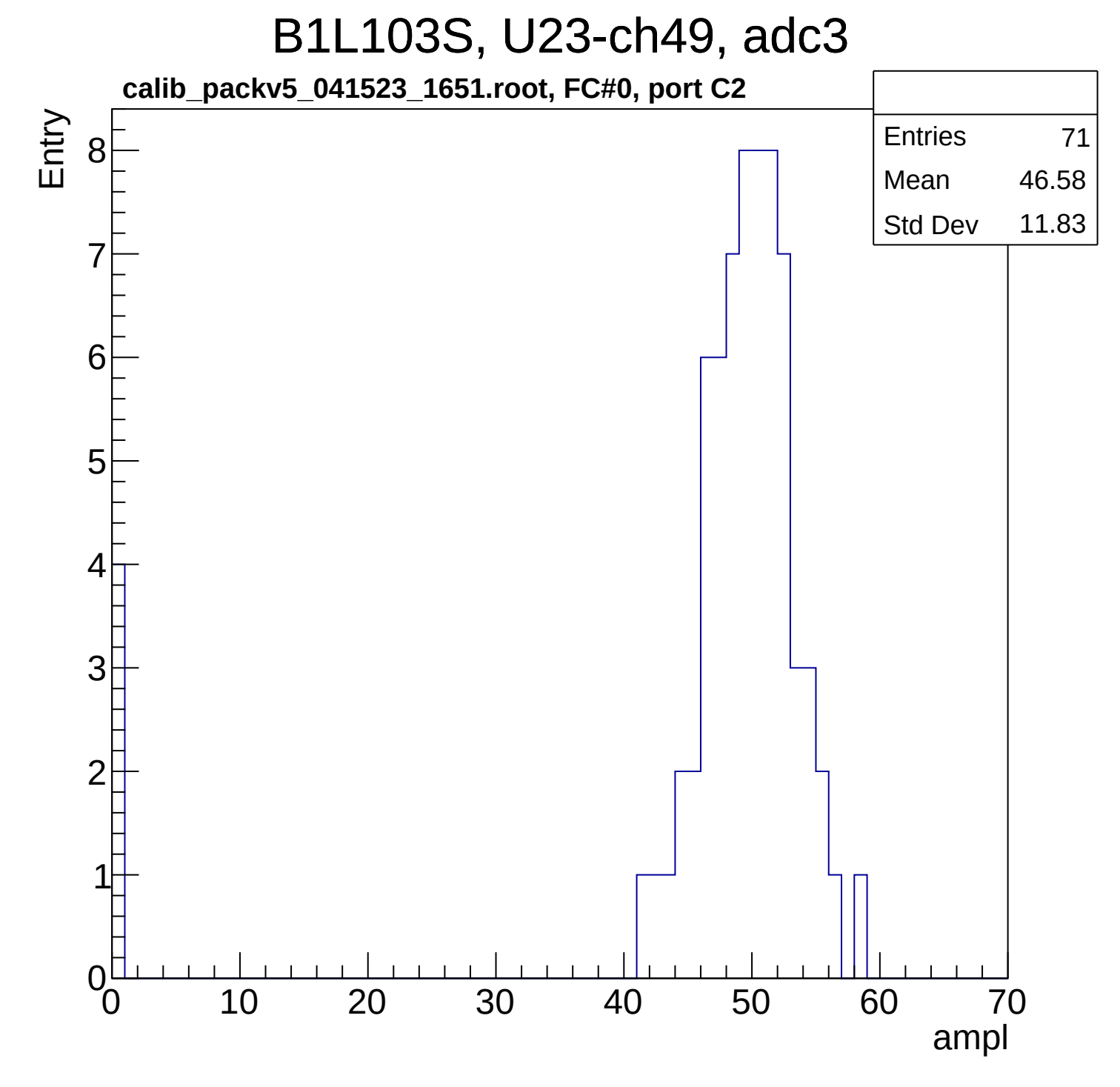

B1L103S, U23-ch48, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 16
Mean 61.56
Std Dev 1.657
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

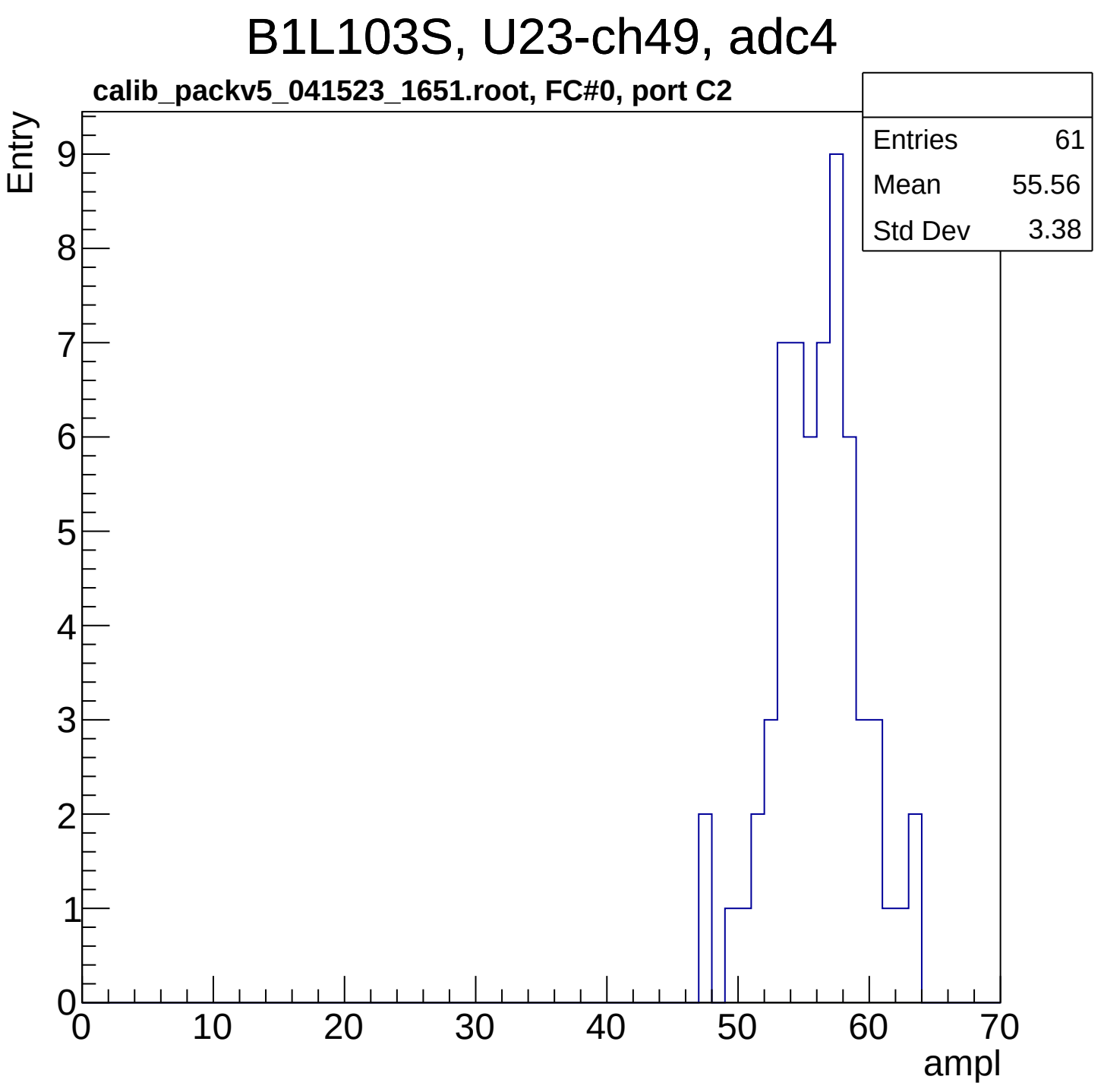

B1L103S, U23-ch48, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.053
Std Dev 4.466
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

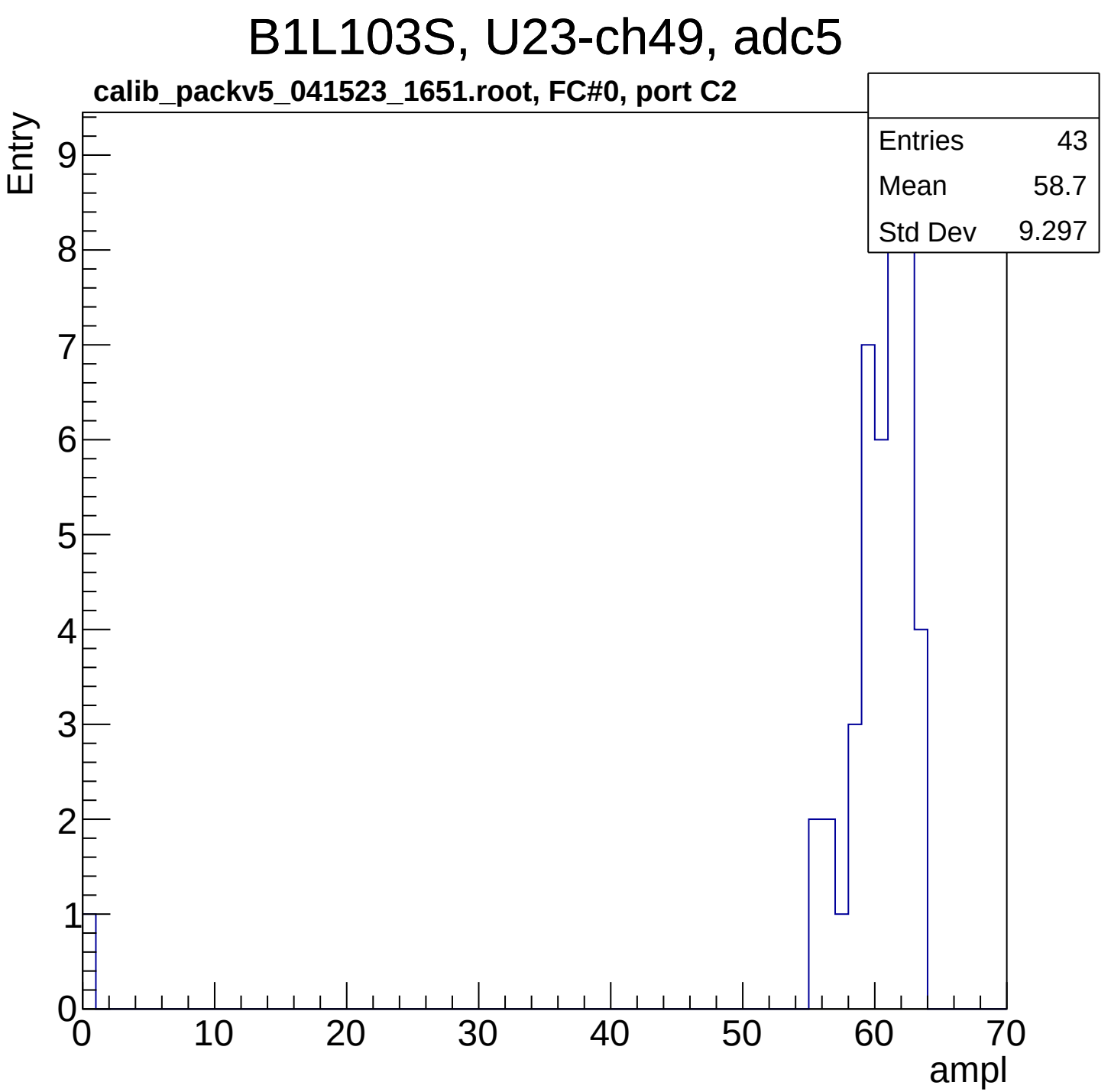

B1L103S, U23-ch49, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 88
Mean 25.43
Std Dev 10.08
Gaus mean : 29.5162
Gaus Width: 3.4829
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

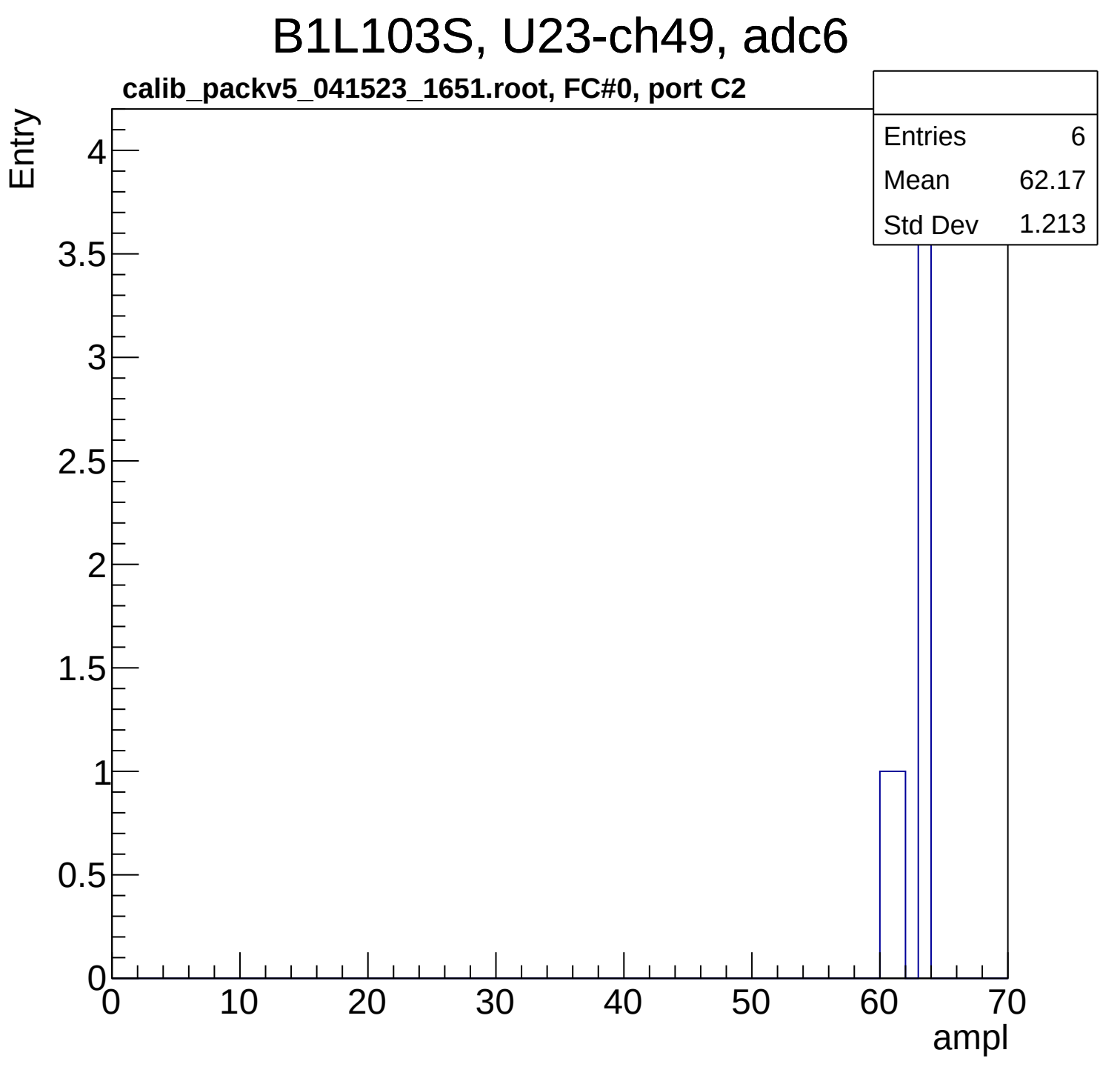

B1L103S, U23-ch49, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 36.95
Std Dev 3.292
Gaus mean : 37.3475
Gaus Width: 3.7342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

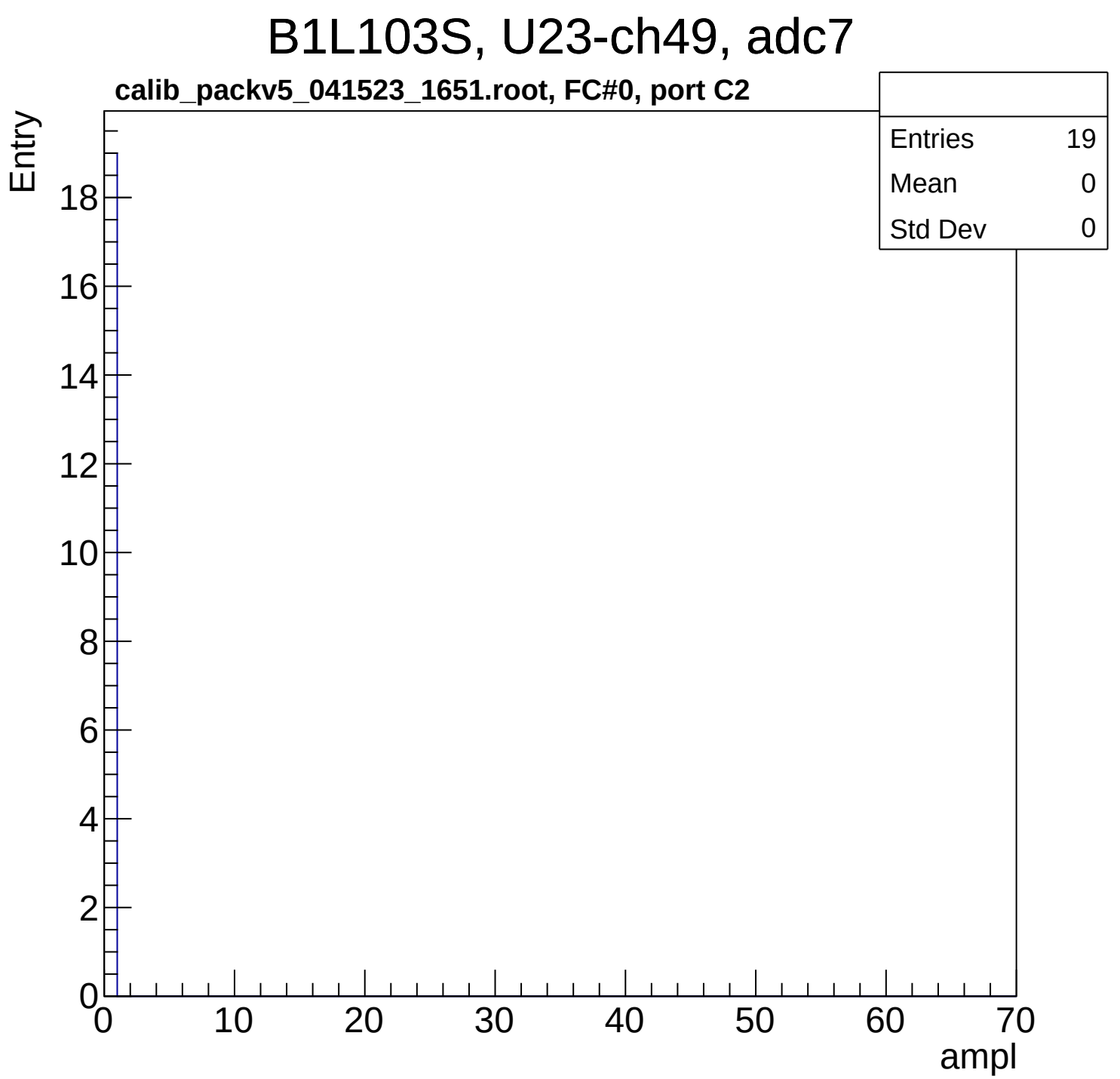

B1L103S, U23-ch49, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 36.37
Std Dev 15.8
Gaus mean : 43.6153
Gaus Width: 3.3084
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

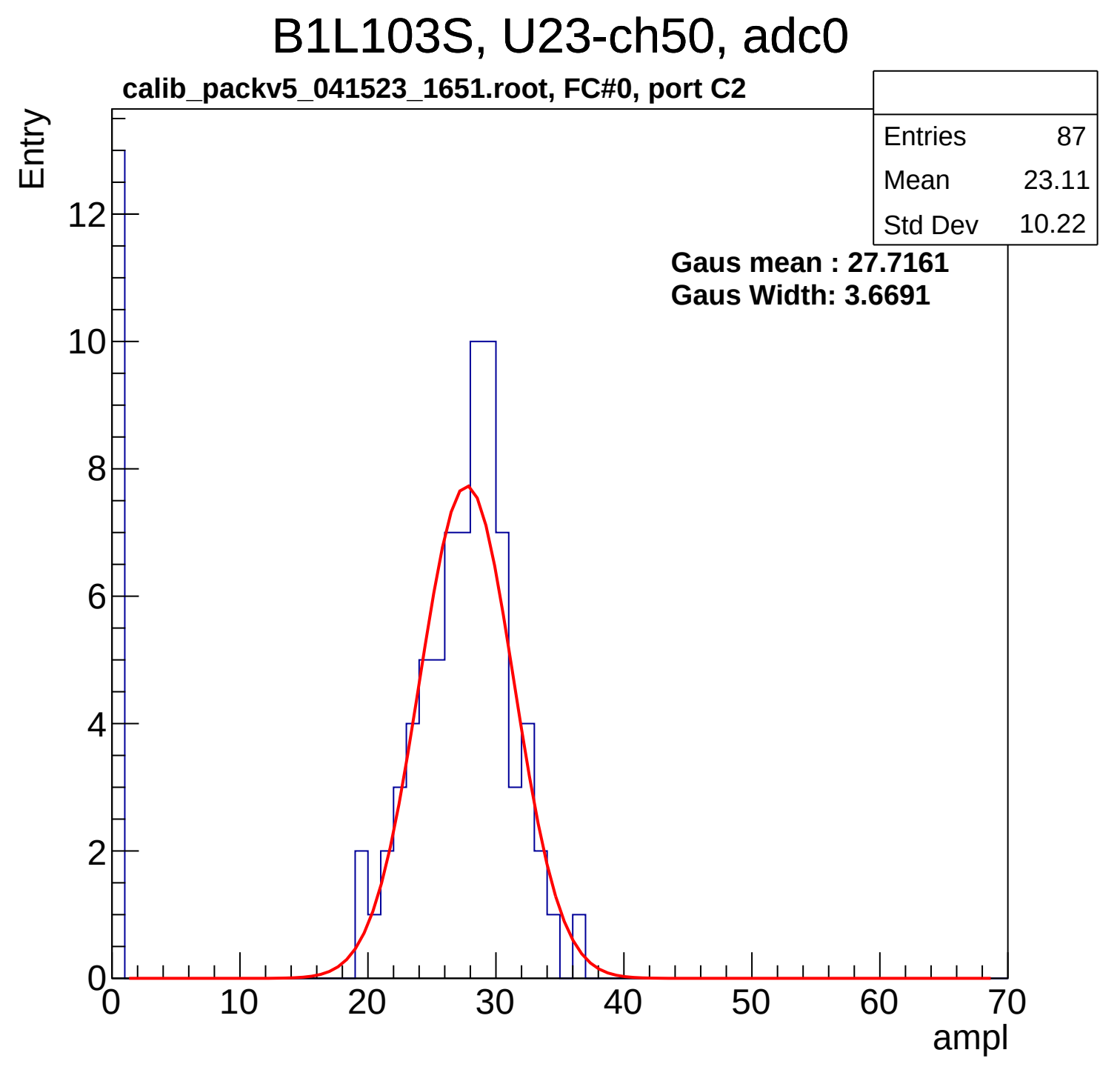

B1L103S, U23-ch49, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 46.58
Std Dev 11.83
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

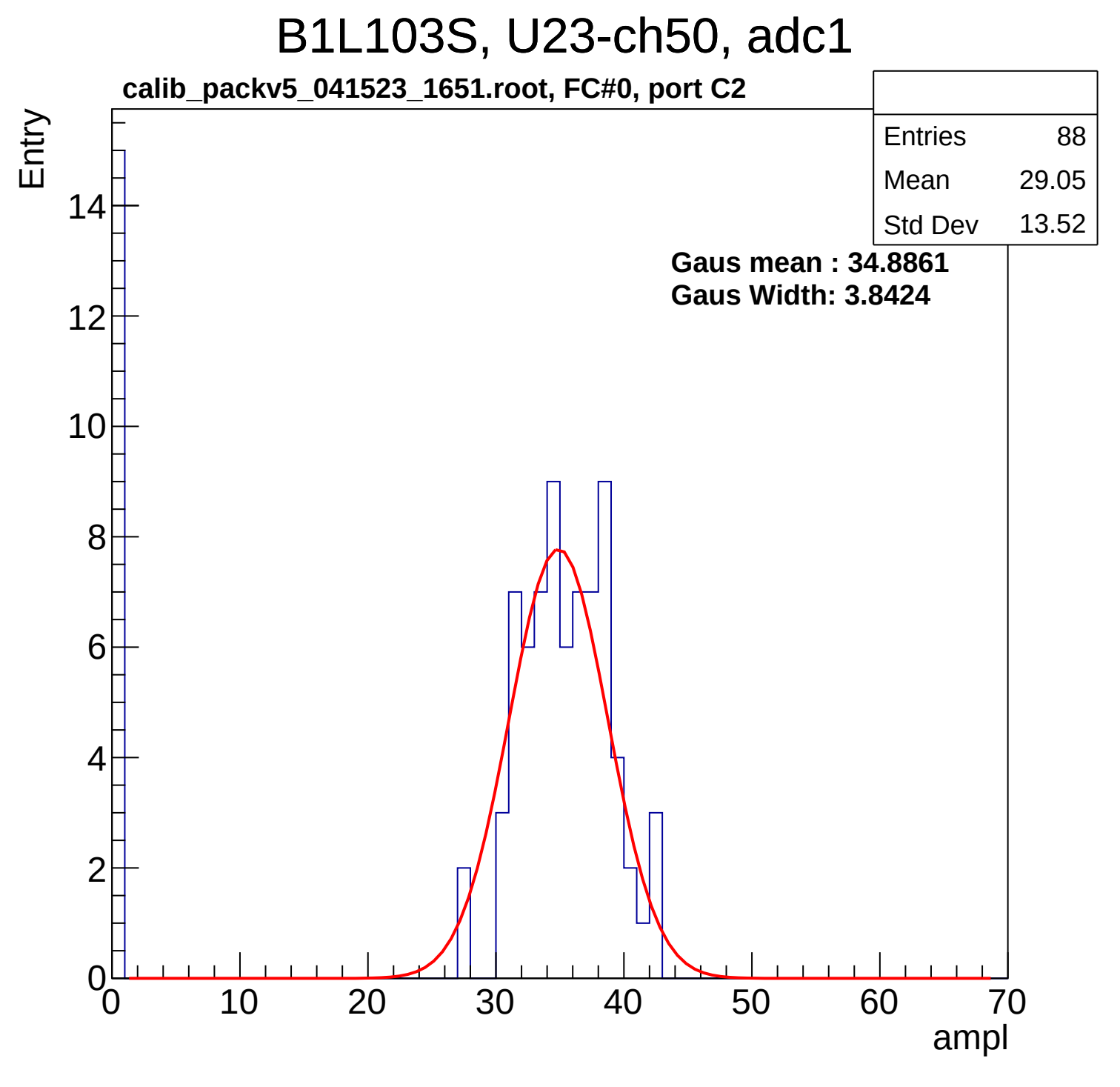

B1L103S, U23-ch49, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 55.56
Std Dev 3.38
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

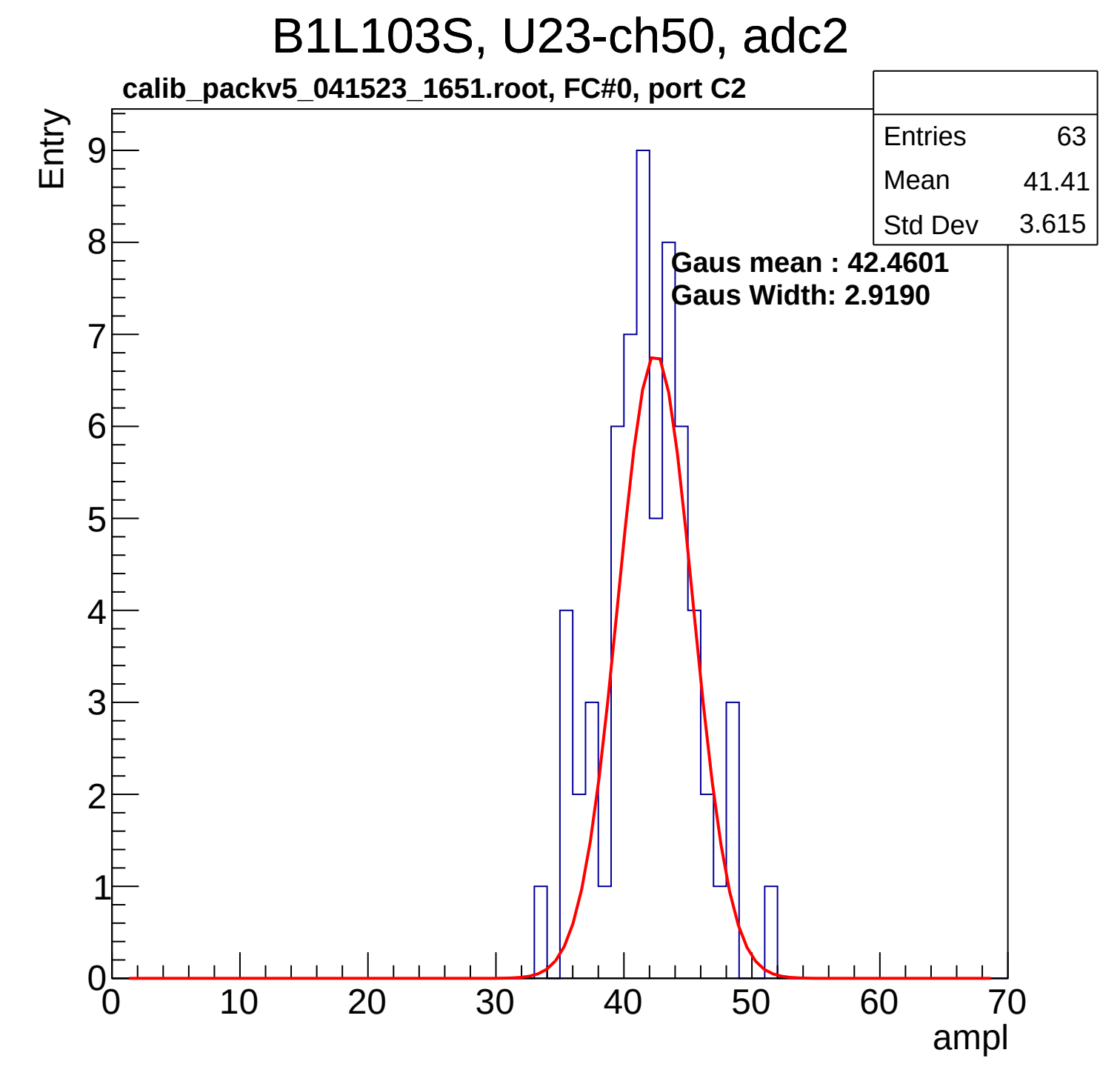

B1L103S, U23-ch49, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 43
Mean 58.7
Std Dev 9.297
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

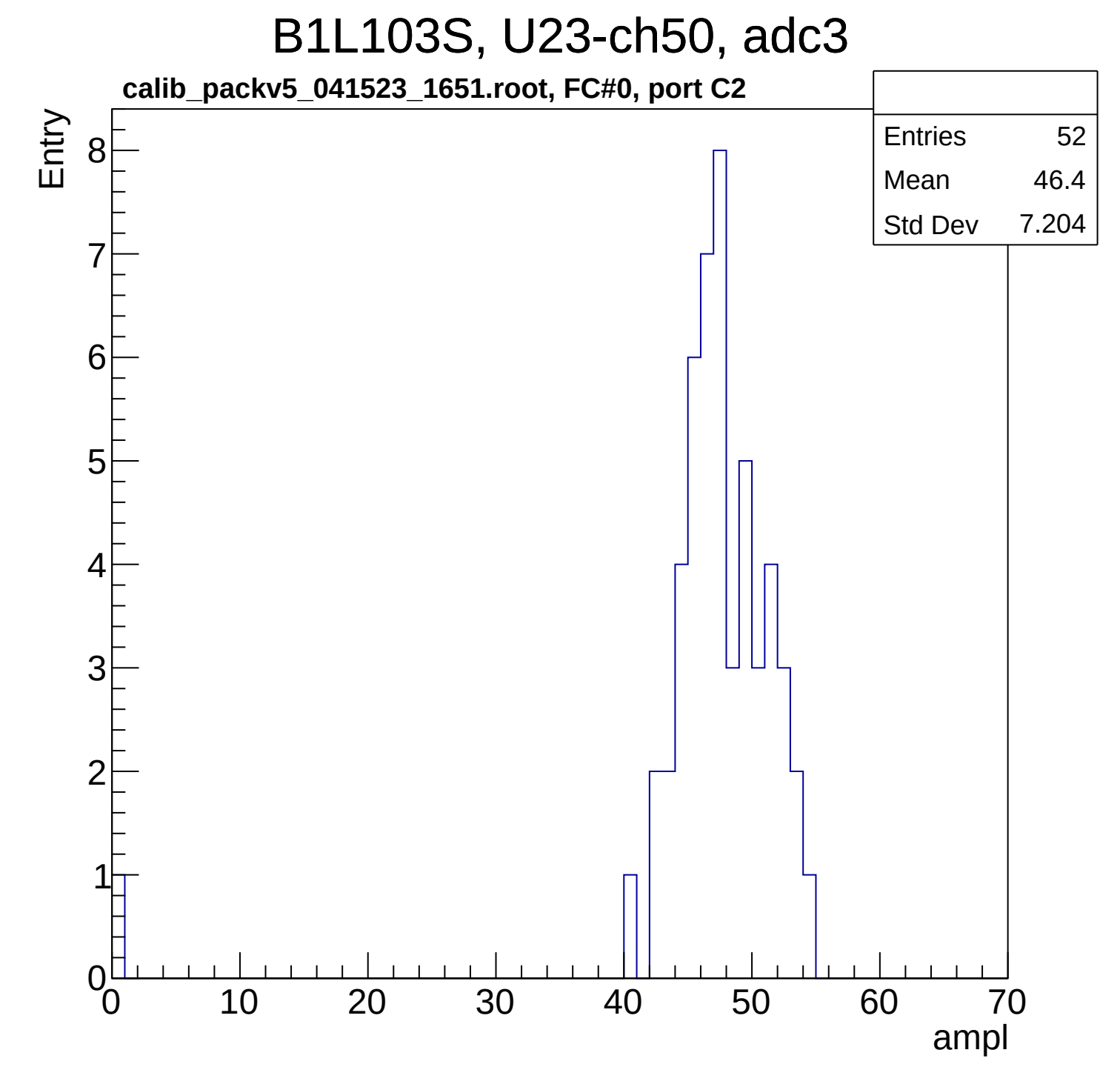

B1L103S, U23-ch49, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

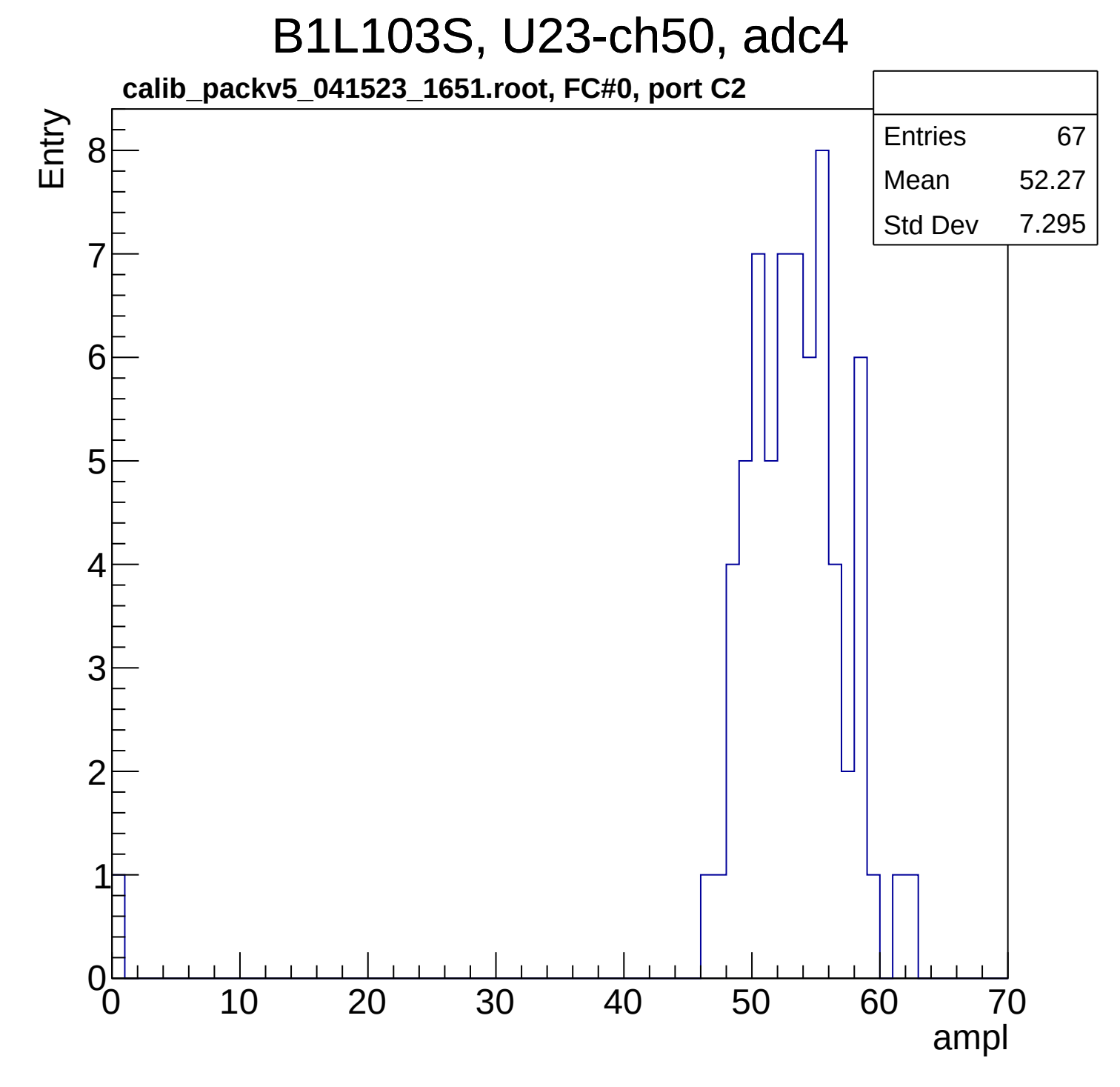

B1L103S, U23-ch49, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

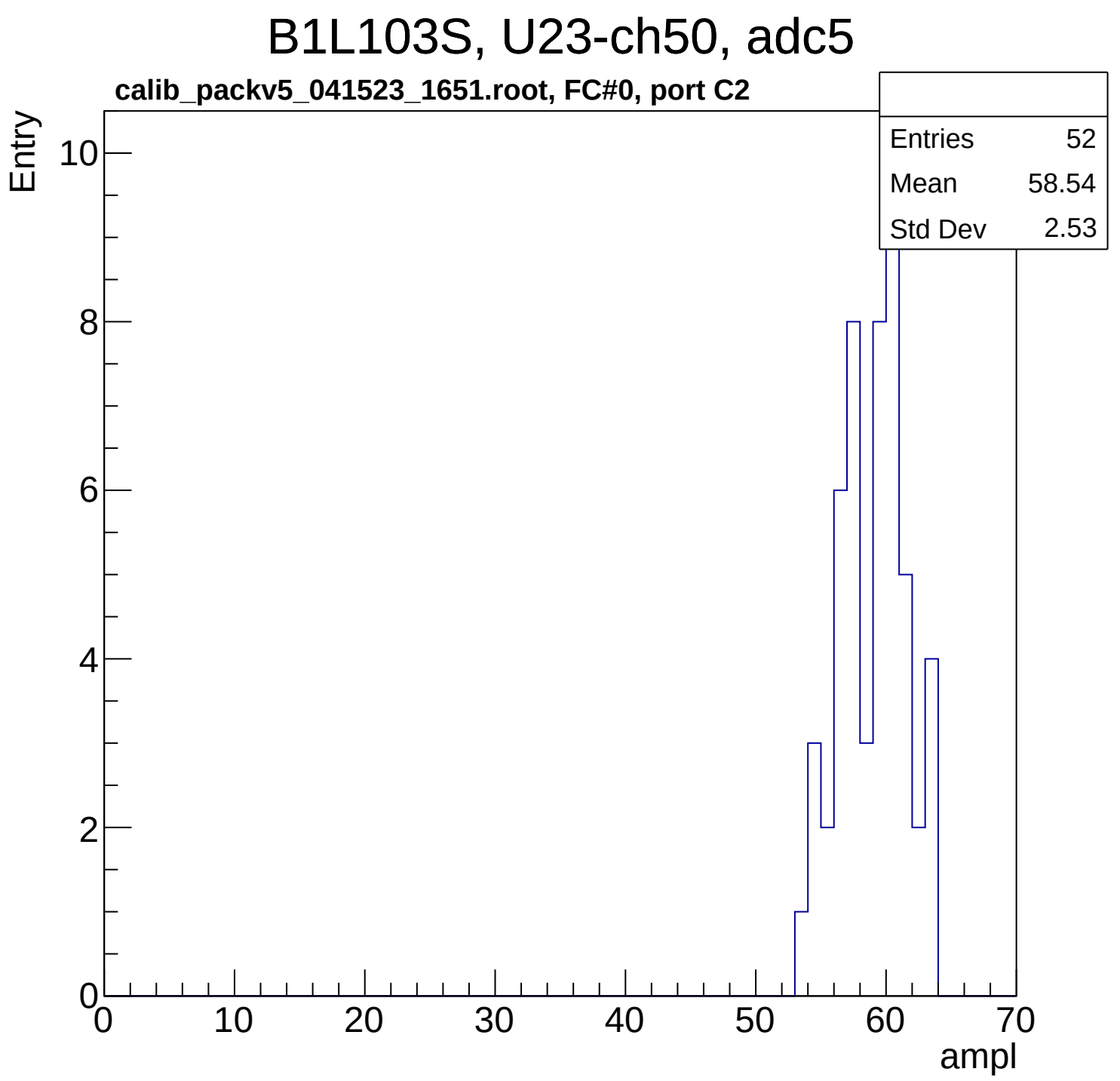

B1L103S, U23-ch50, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 87
Mean 23.11
Std Dev 10.22
Gaus mean : 27.7161
Gaus Width: 3.6691
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

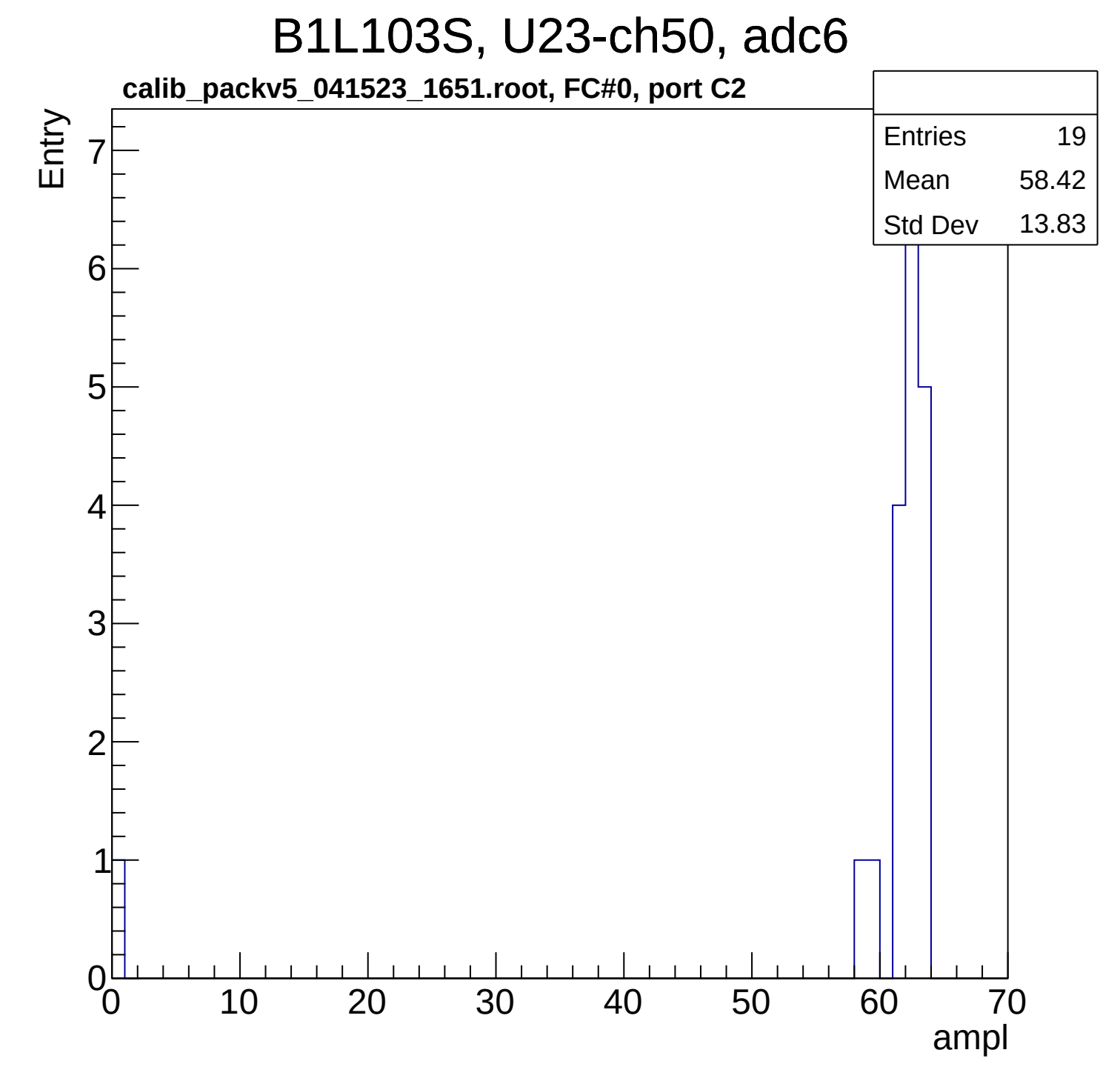

B1L103S, U23-ch50, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 88
Mean 29.05
Std Dev 13.52
Gaus mean : 34.8861
Gaus Width: 3.8424
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

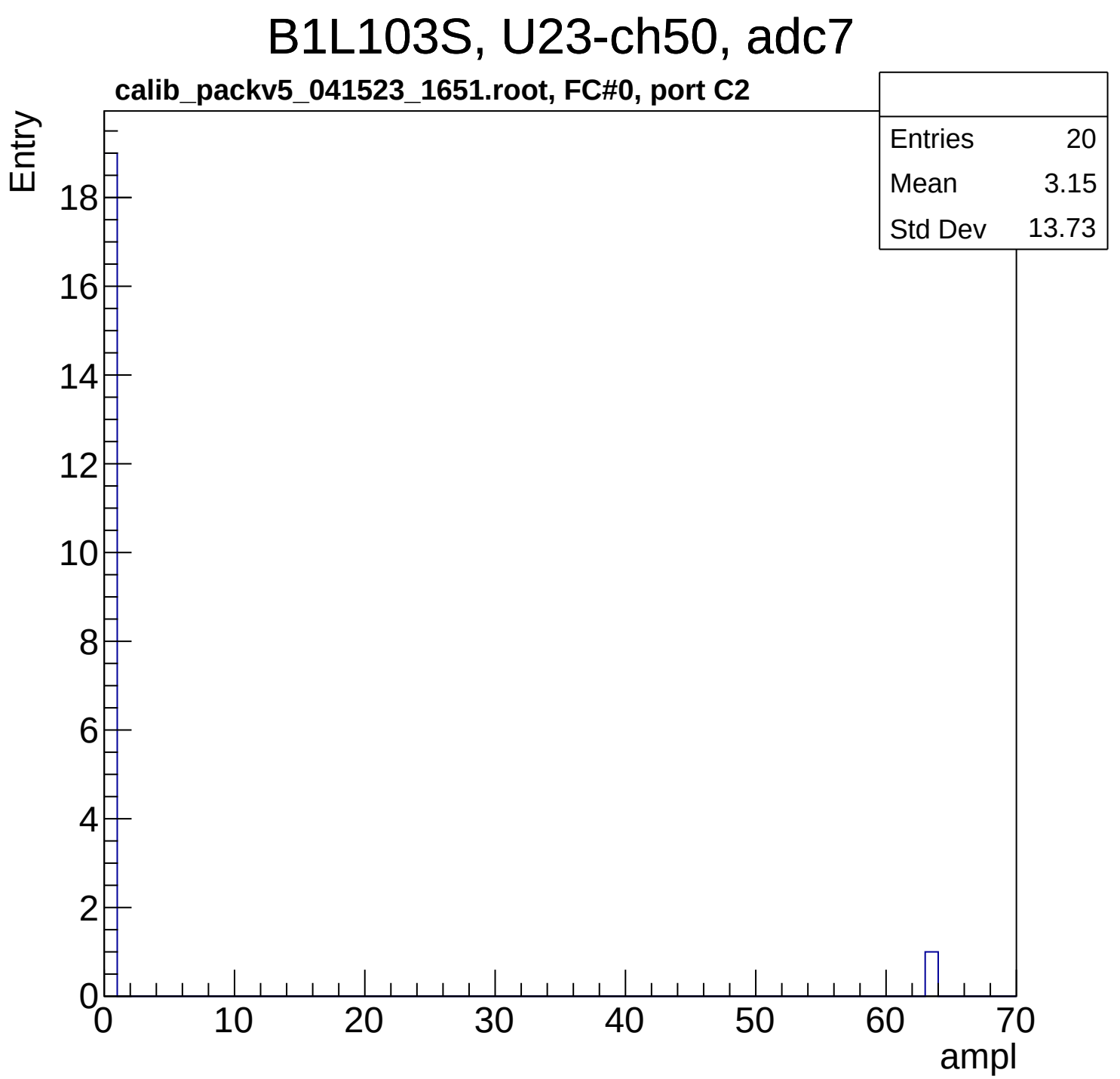

B1L103S, U23-ch50, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 41.41
Std Dev 3.615
Gaus mean : 42.4601
Gaus Width: 2.9190
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

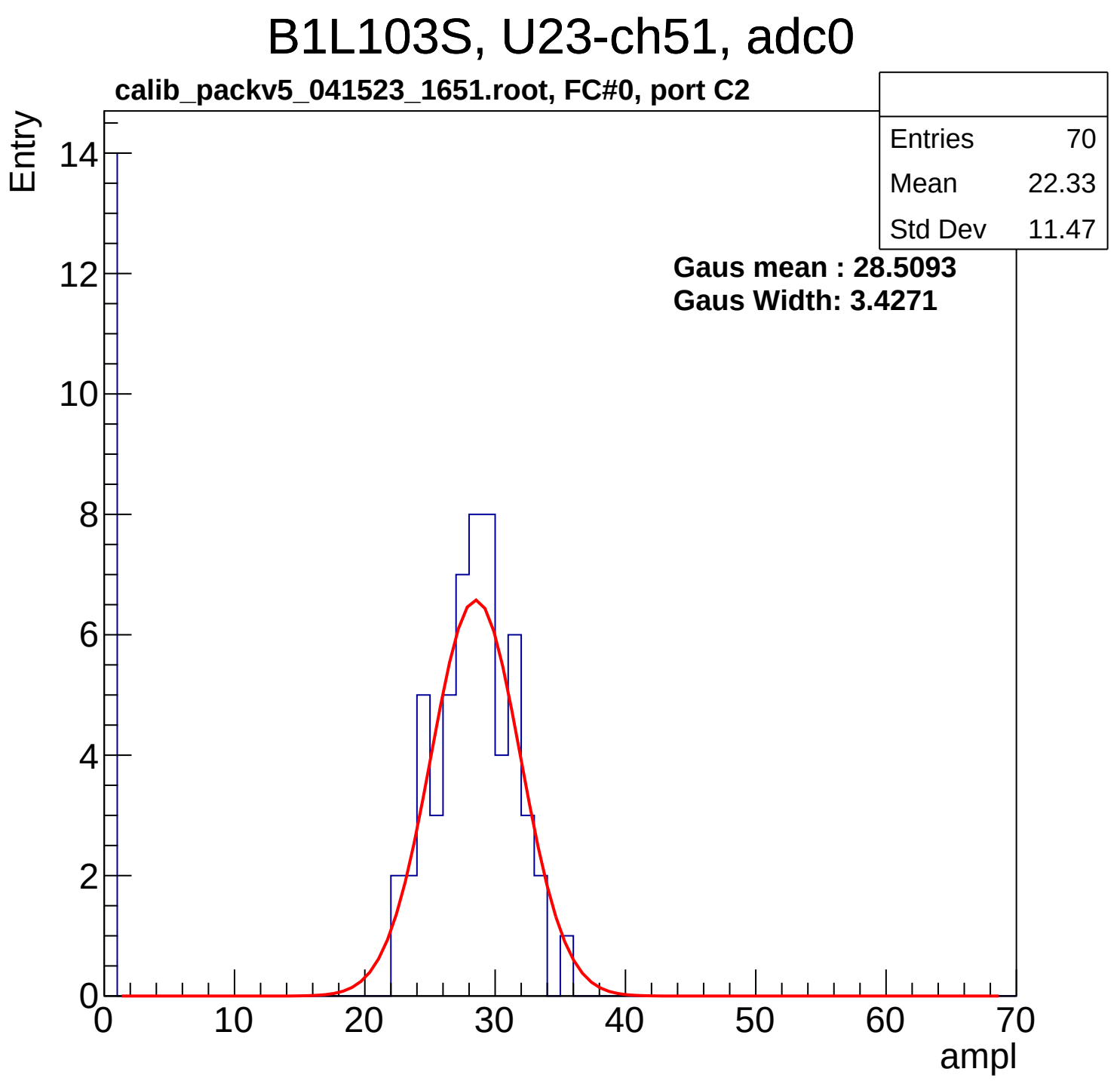

B1L103S, U23-ch50, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 46.4
Std Dev 7.204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

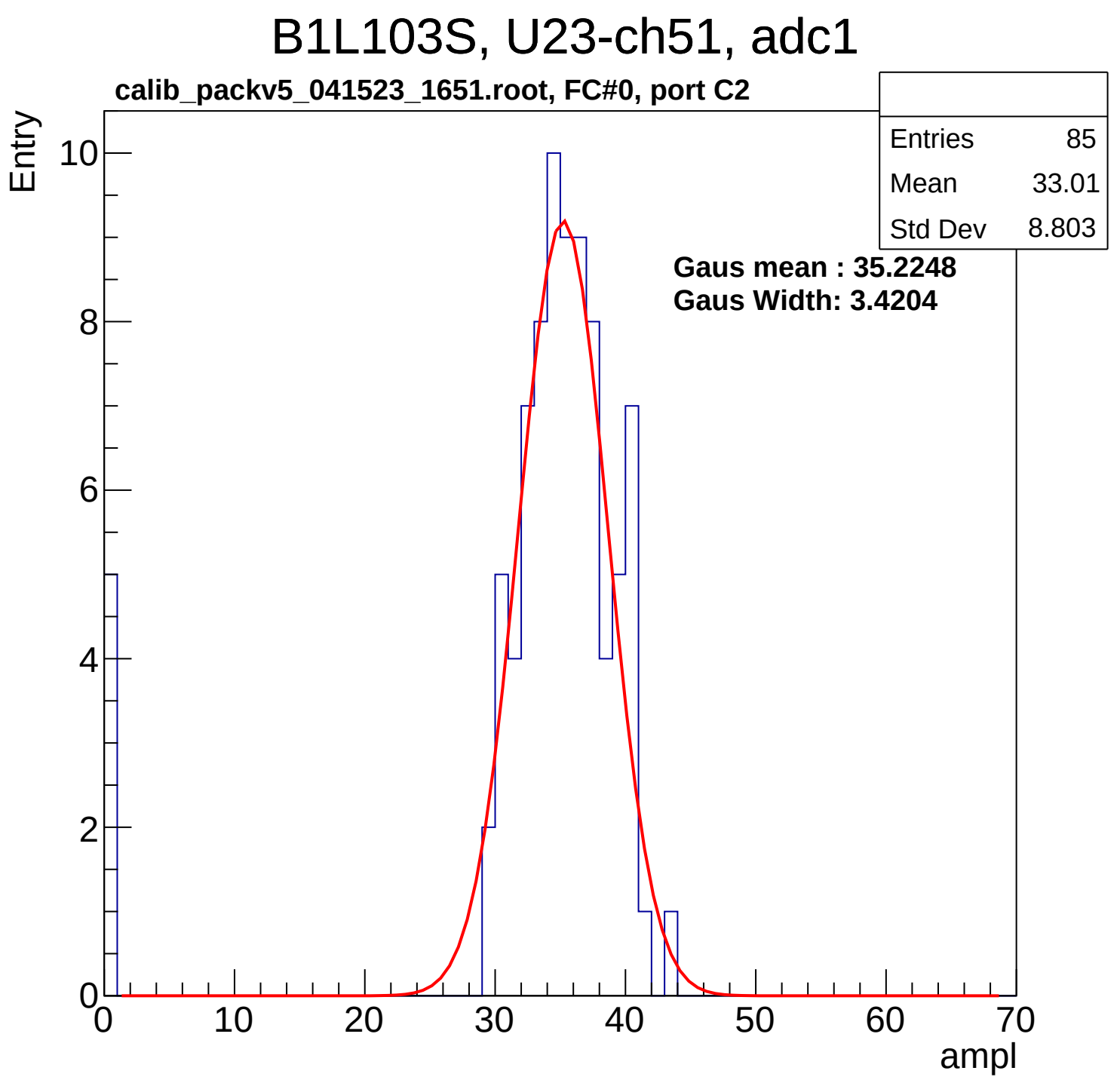

B1L103S, U23-ch50, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 52.27
Std Dev 7.295
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

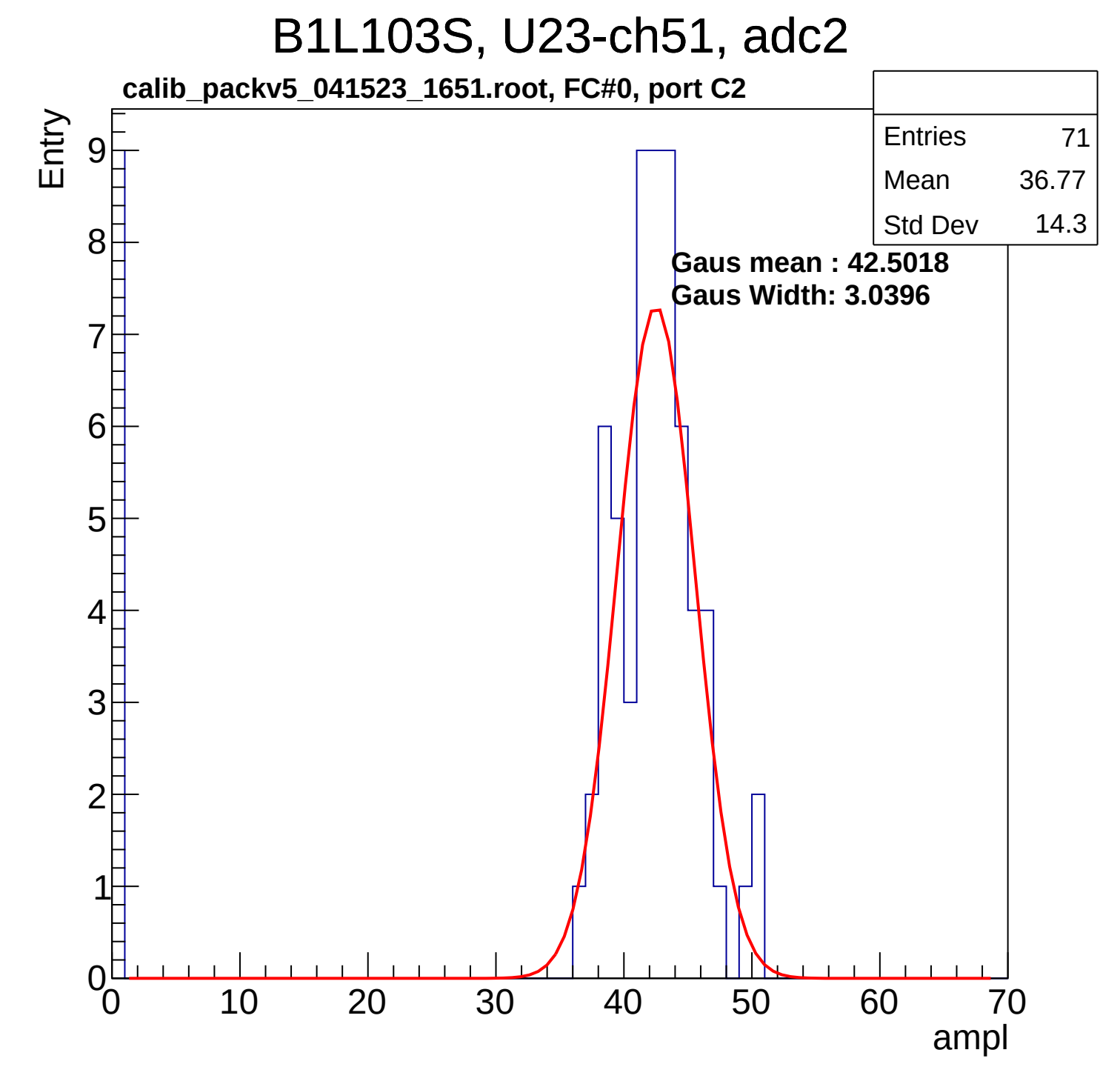

B1L103S, U23-ch50, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 58.54
Std Dev 2.53
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

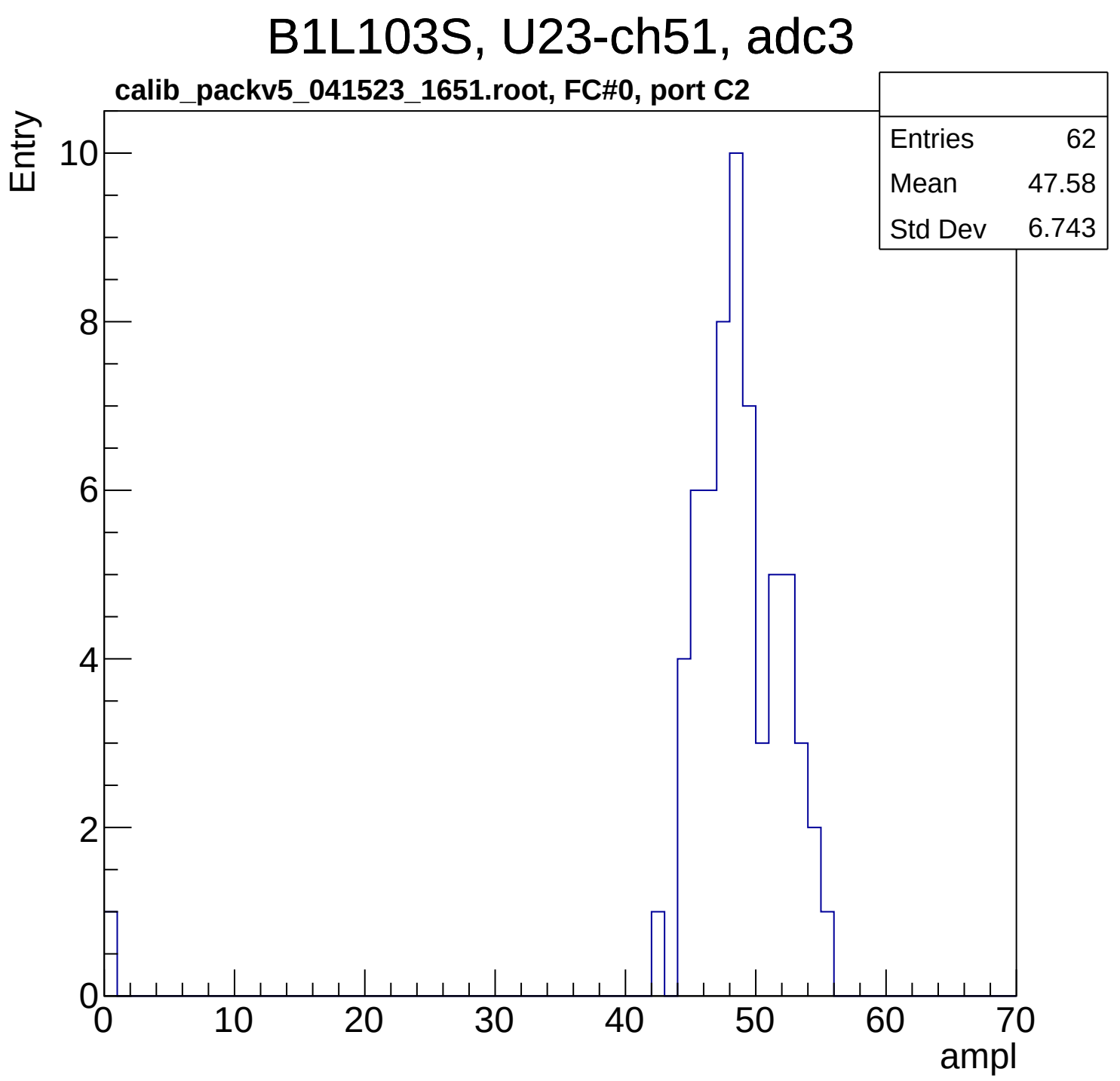

B1L103S, U23-ch50, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 58.42
Std Dev 13.83
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

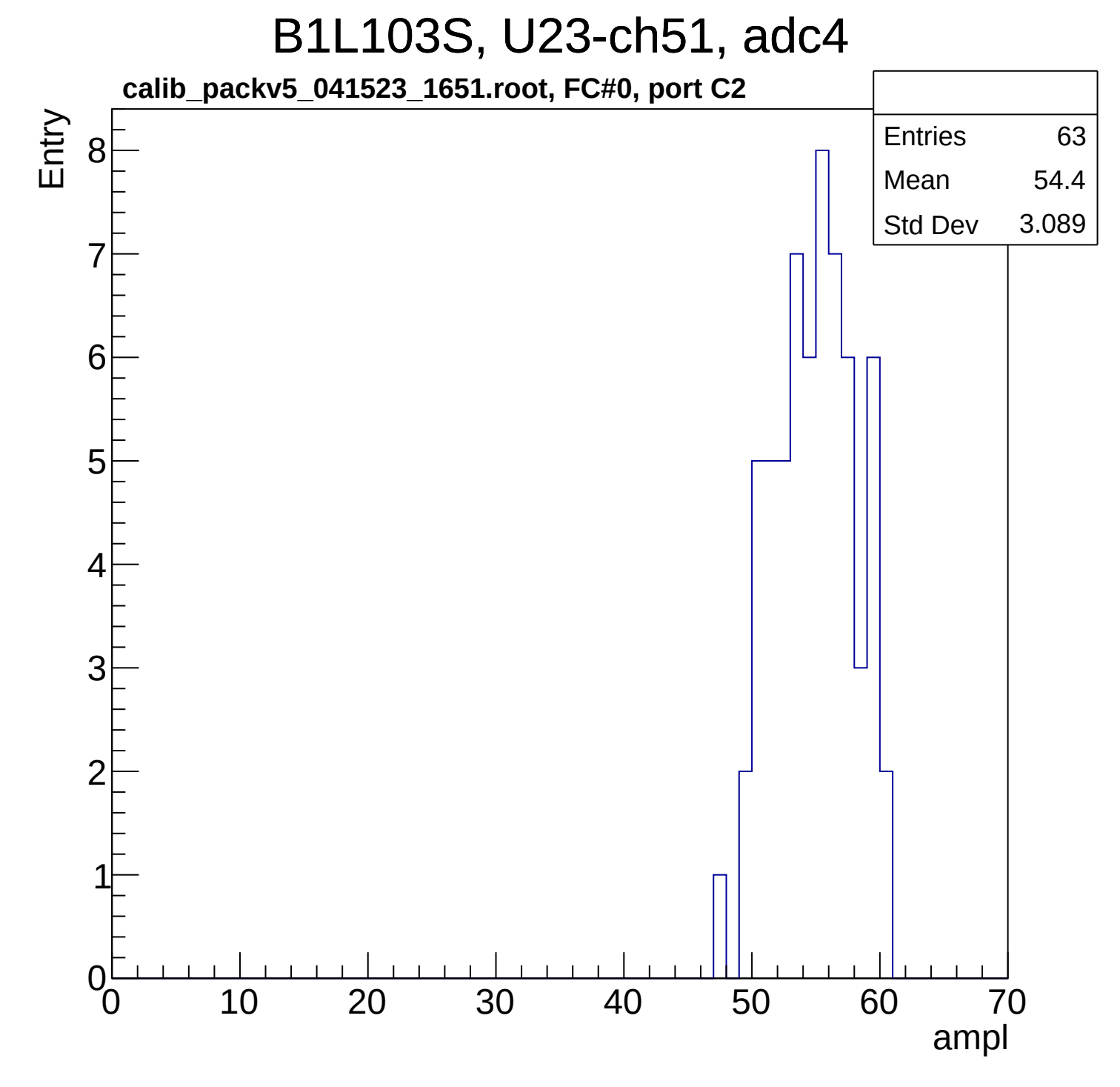

B1L103S, U23-ch50, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.15
Std Dev 13.73
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

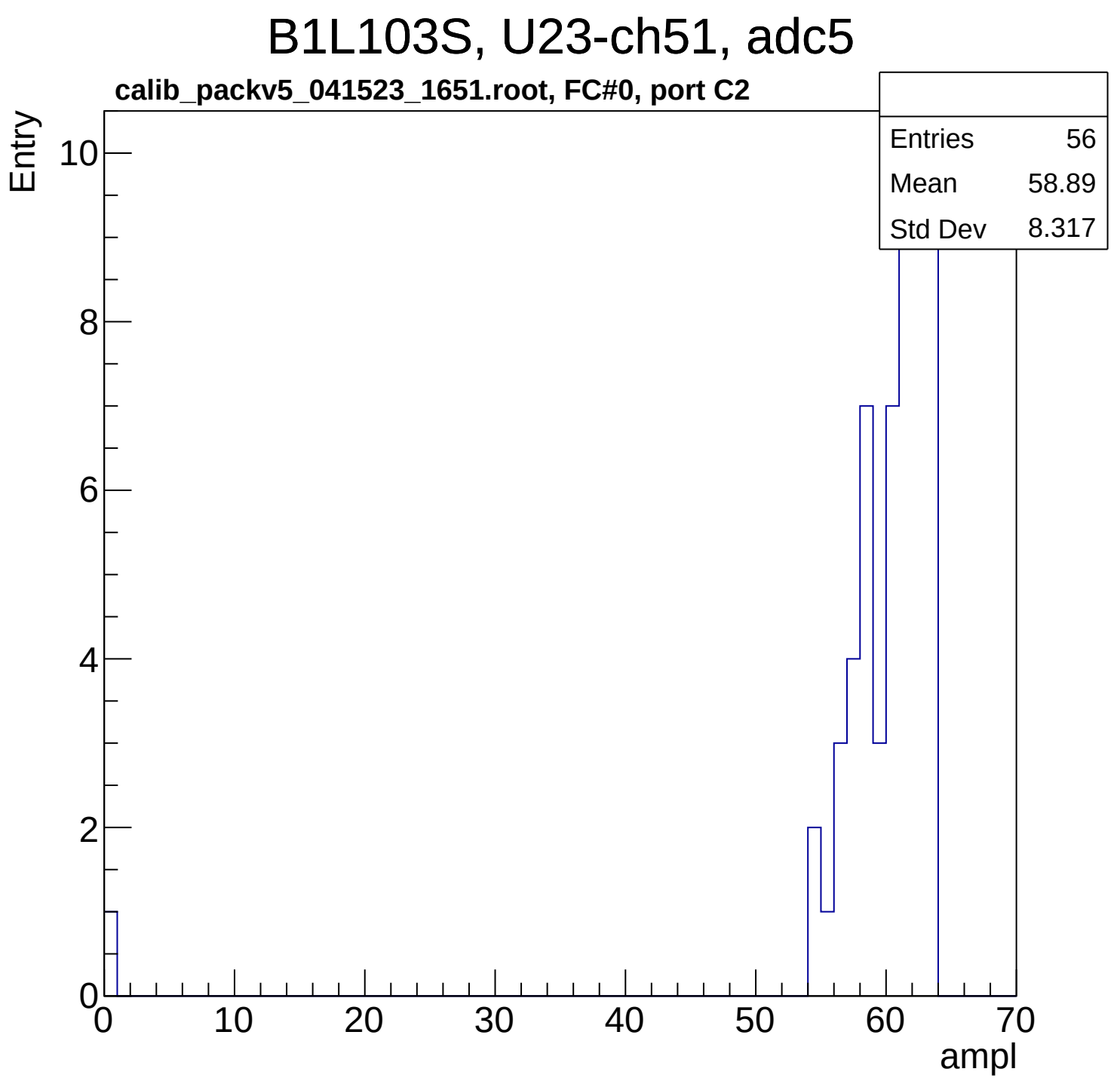

B1L103S, U23-ch51, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 22.33
Std Dev 11.47
Gaus mean : 28.5093
Gaus Width: 3.4271
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

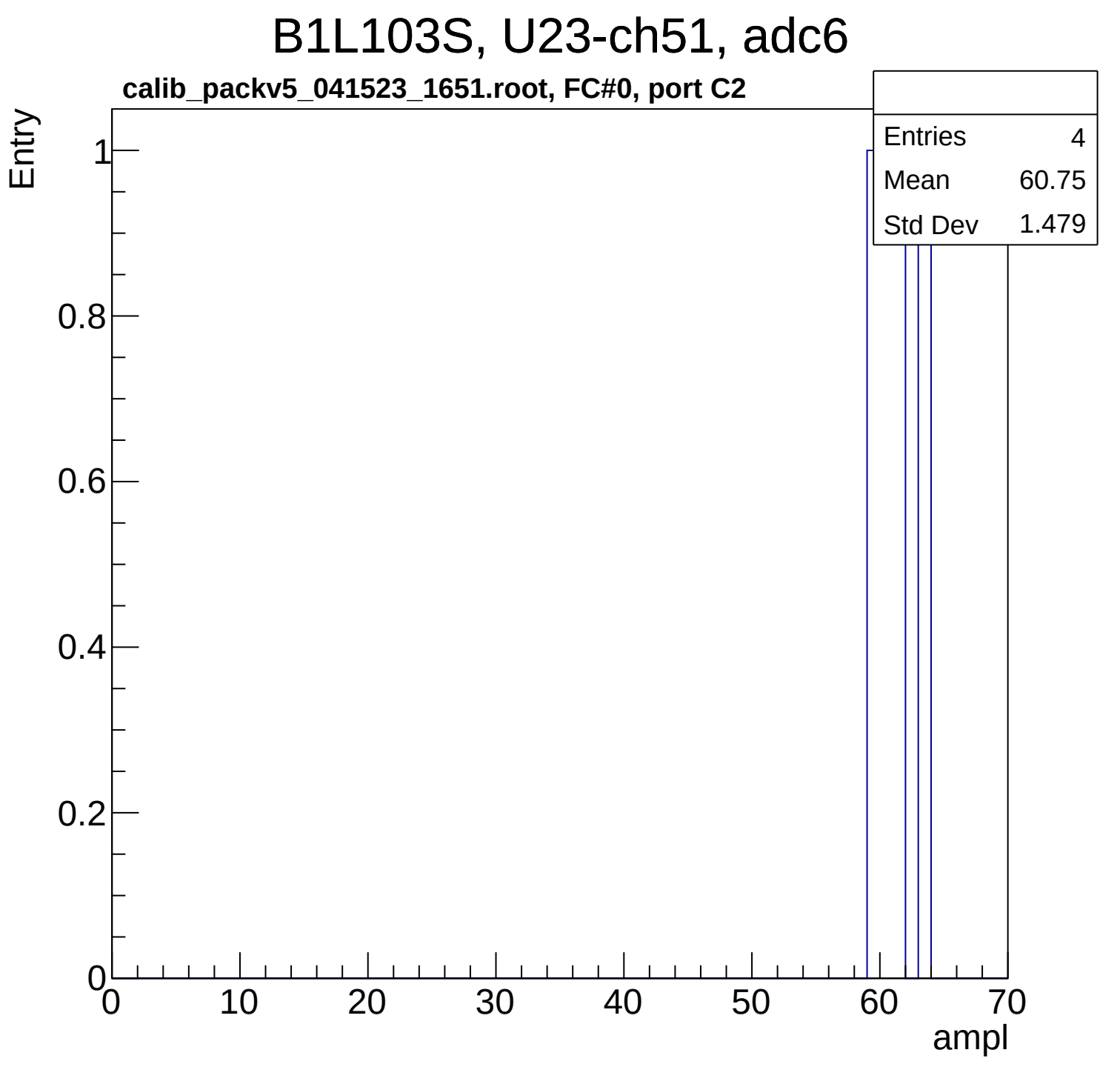

B1L103S, U23-ch51, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 33.01
Std Dev 8.803
Gaus mean : 35.2248
Gaus Width: 3.4204
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

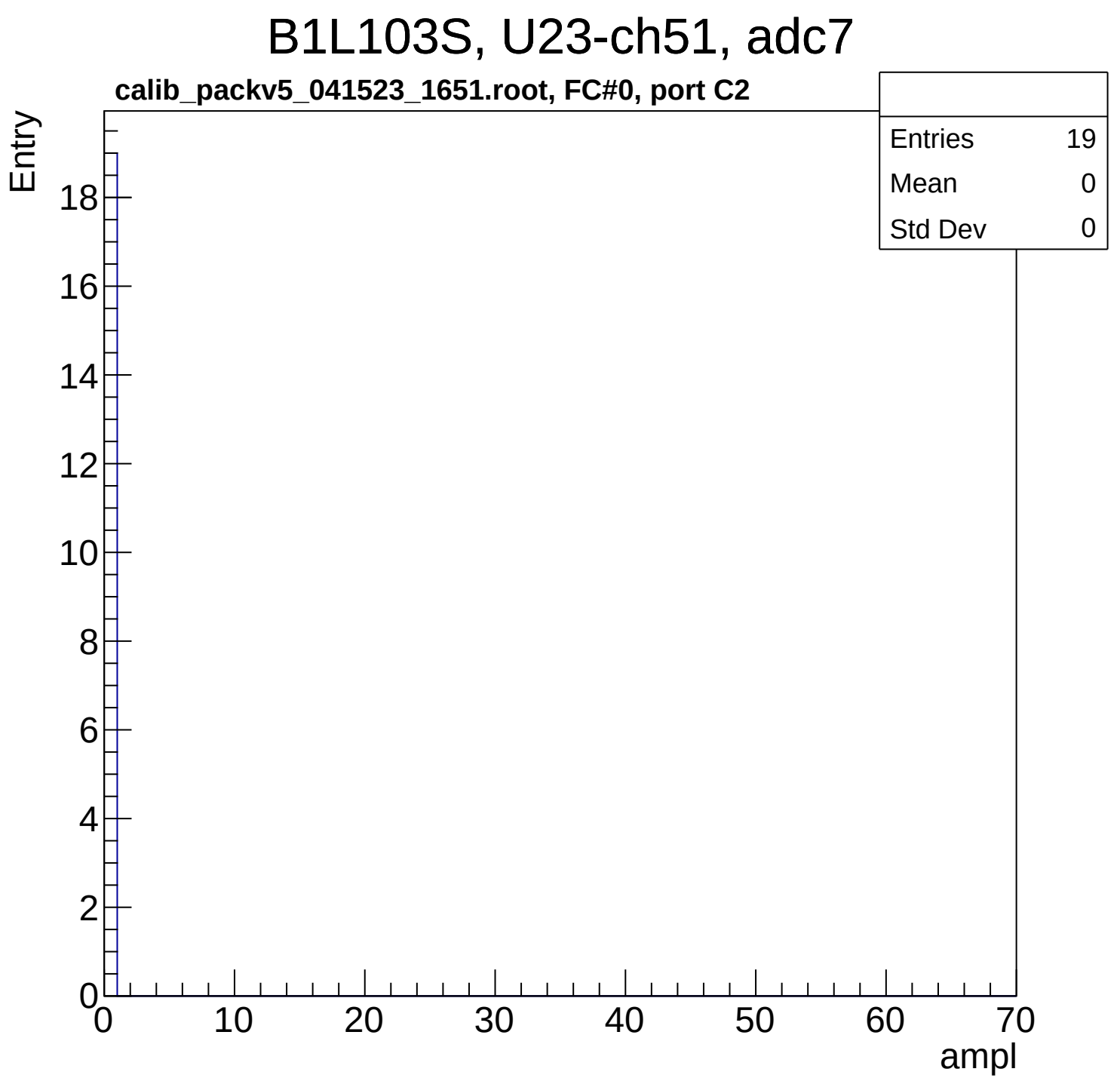

B1L103S, U23-ch51, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 36.77
Std Dev 14.3
Gaus mean : 42.5018
Gaus Width: 3.0396
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

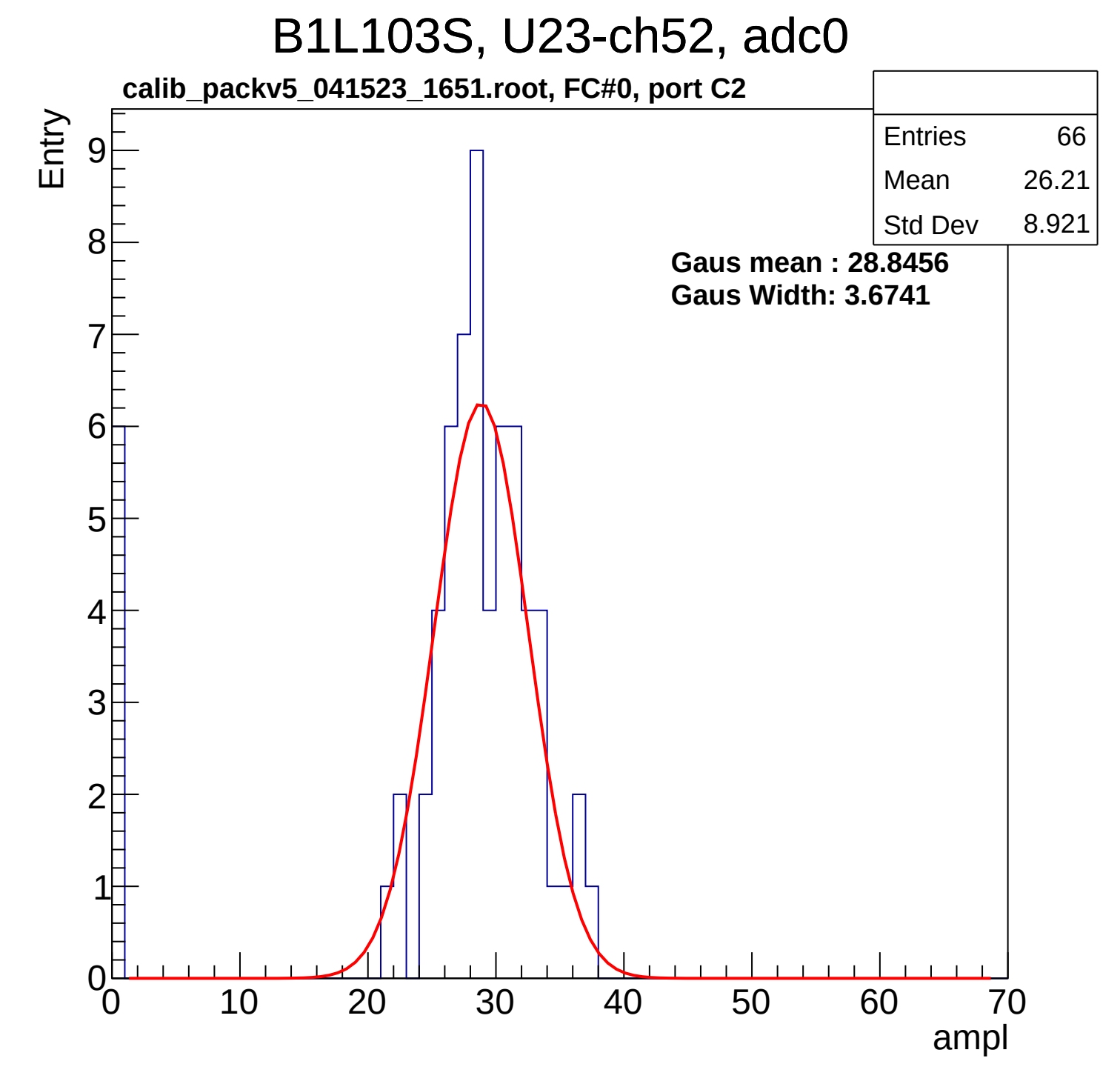

B1L103S, U23-ch51, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 47.58
Std Dev 6.743
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

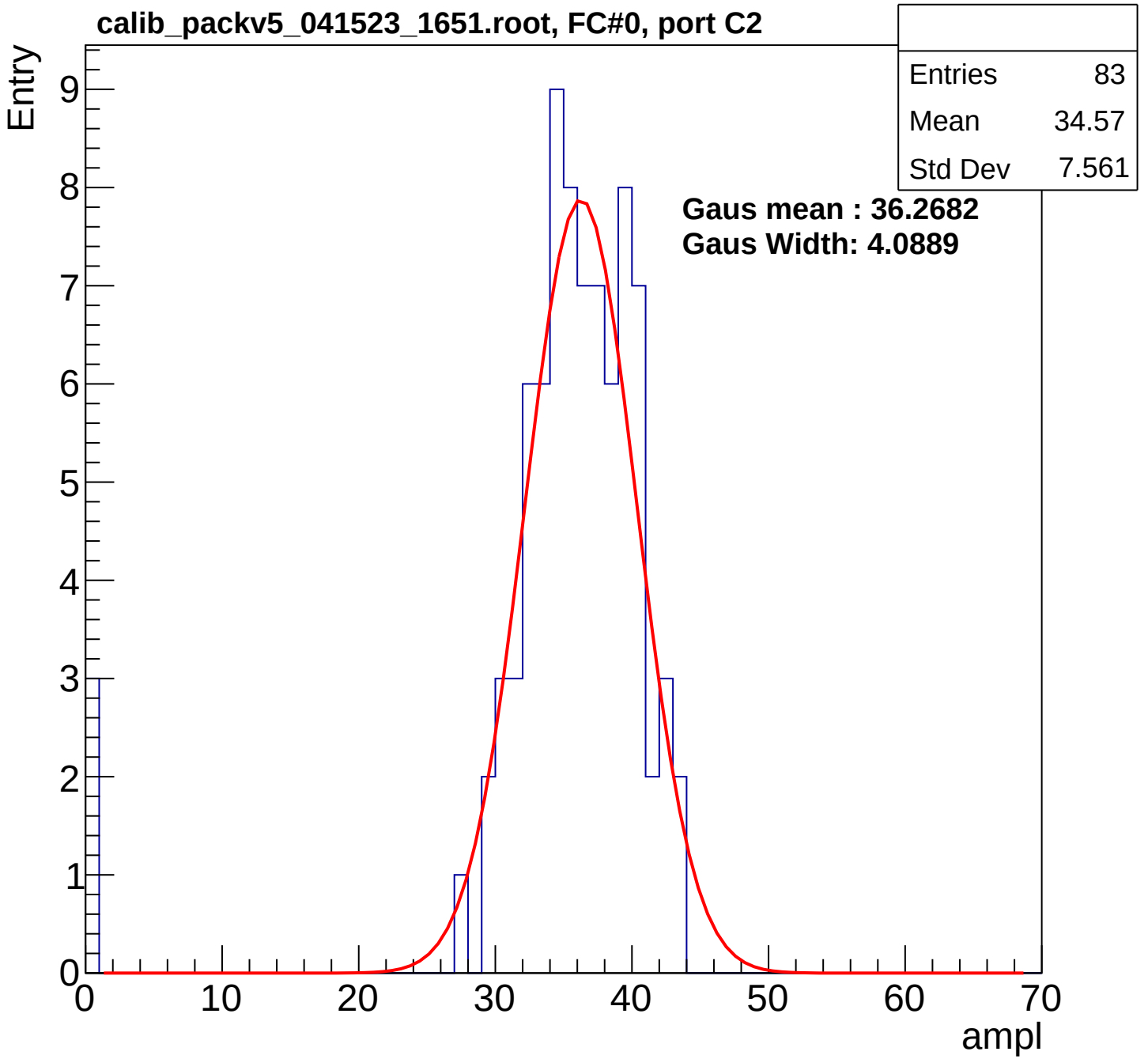

B1L103S, U23-ch51, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 54.4
Std Dev 3.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

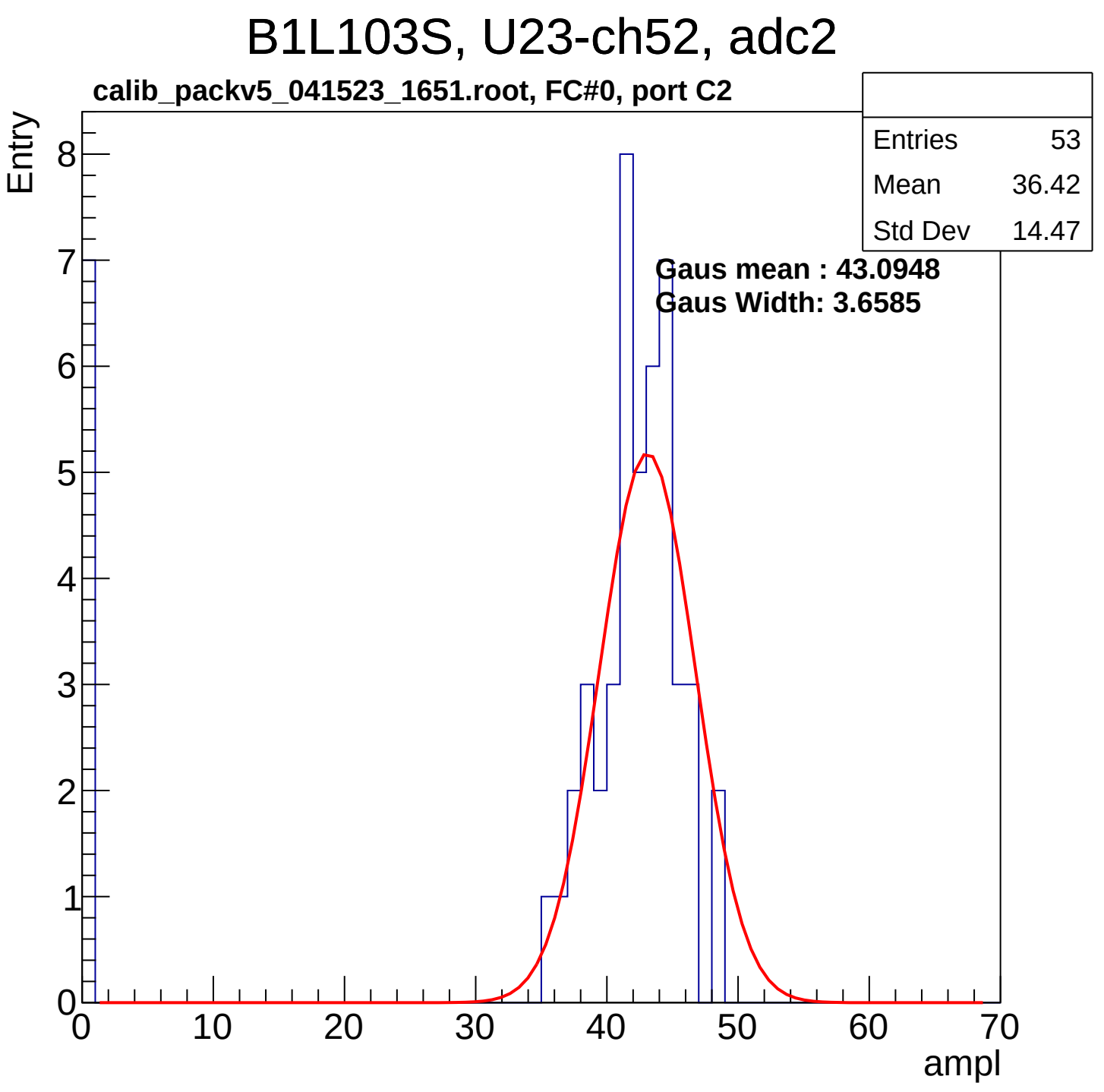

B1L103S, U23-ch51, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 58.89
Std Dev 8.317
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

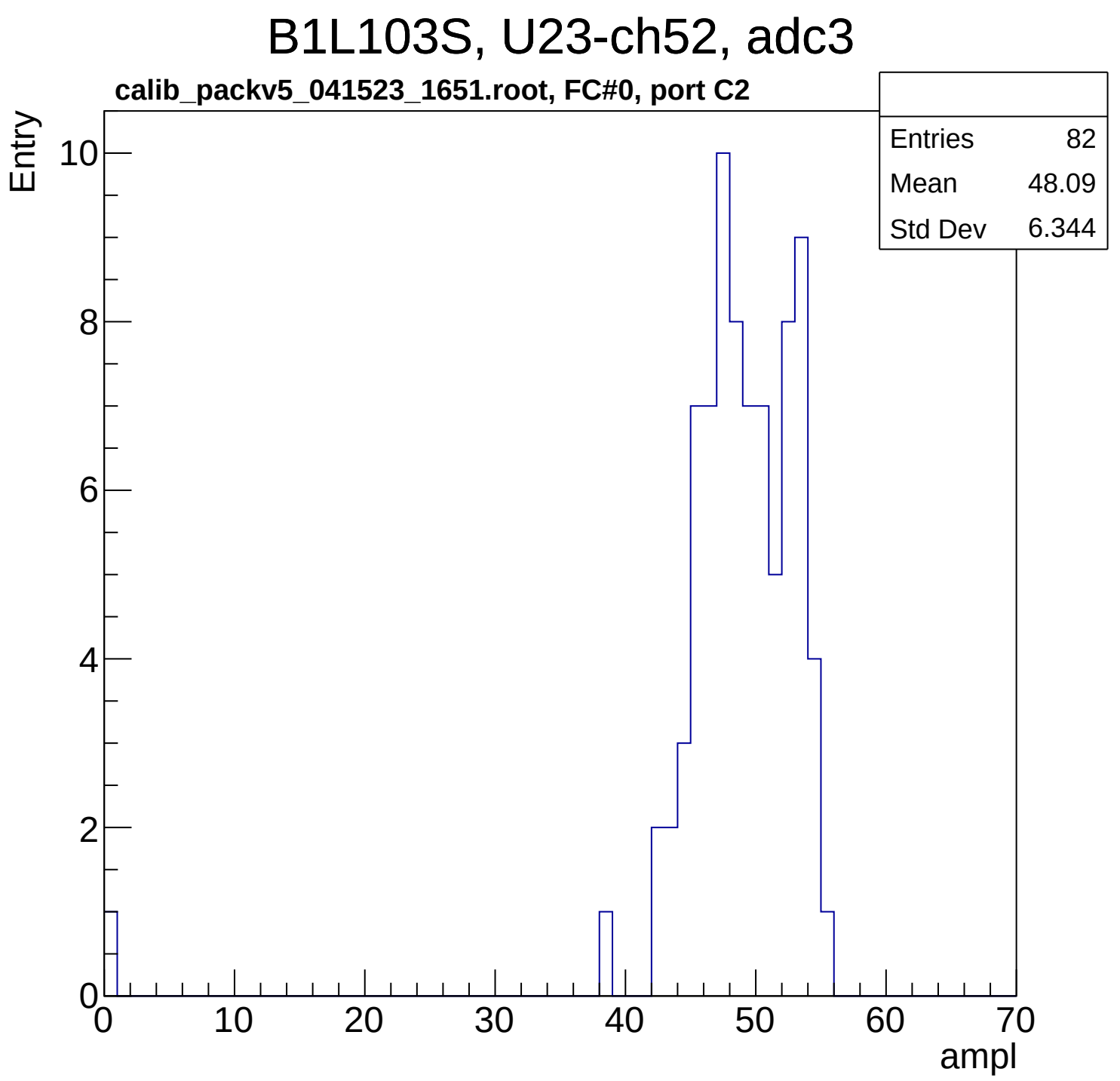

B1L103S, U23-ch51, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 4
Mean 60.75
Std Dev 1.479
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

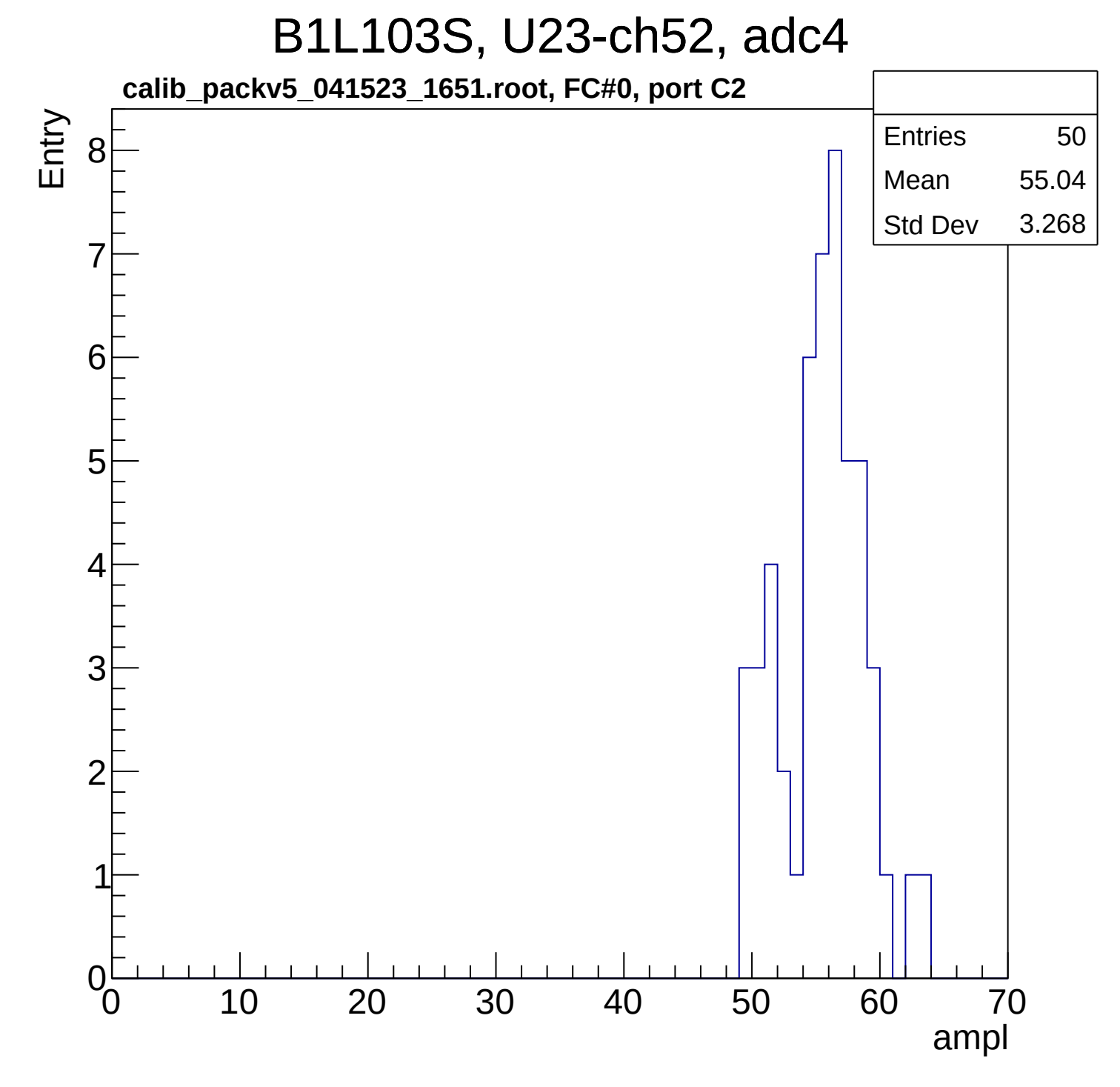

B1L103S, U23-ch51, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

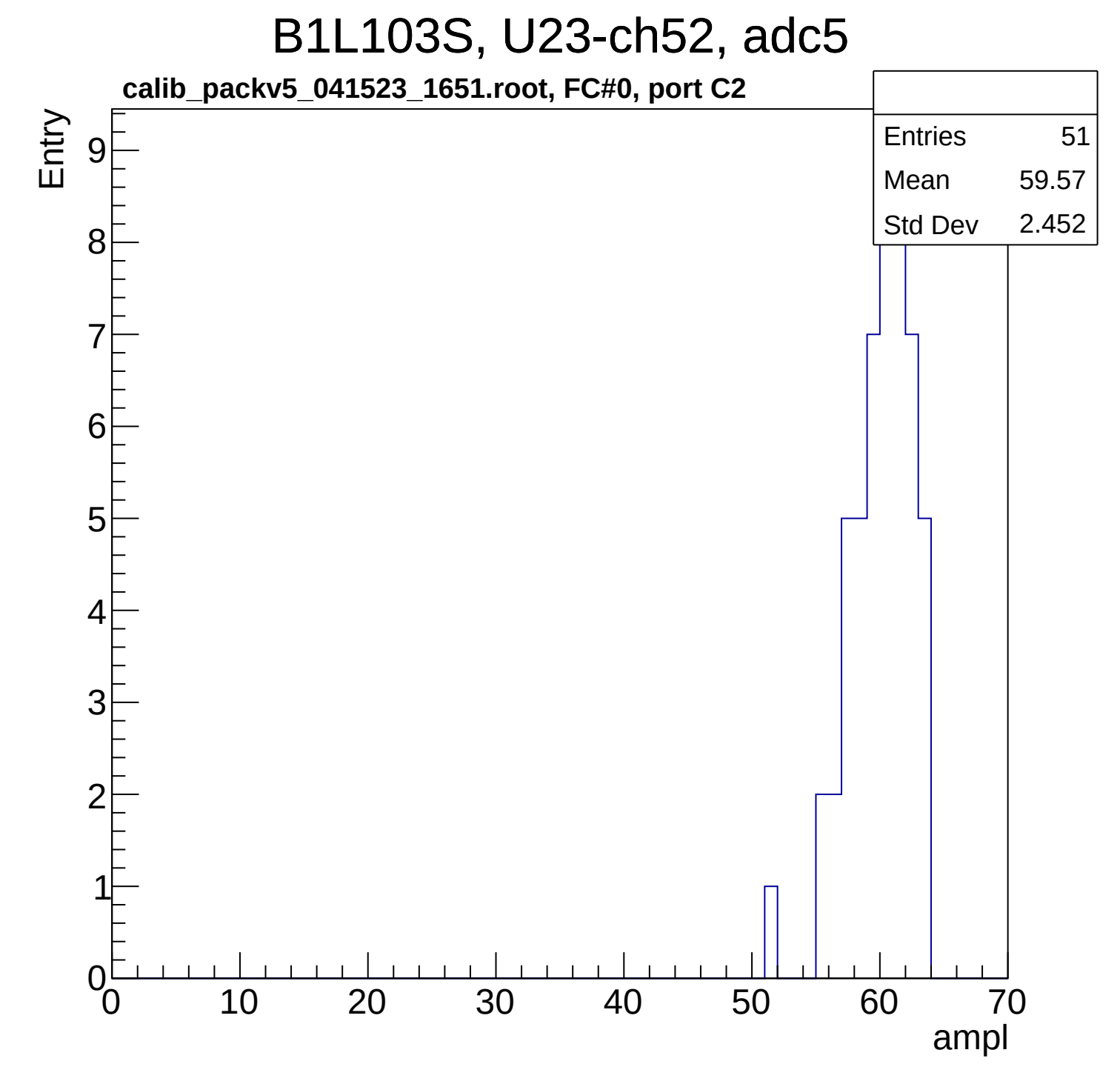

B1L103S, U23-ch52, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 26.21
Std Dev 8.921
Gaus mean : 28.8456
Gaus Width: 3.6741
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

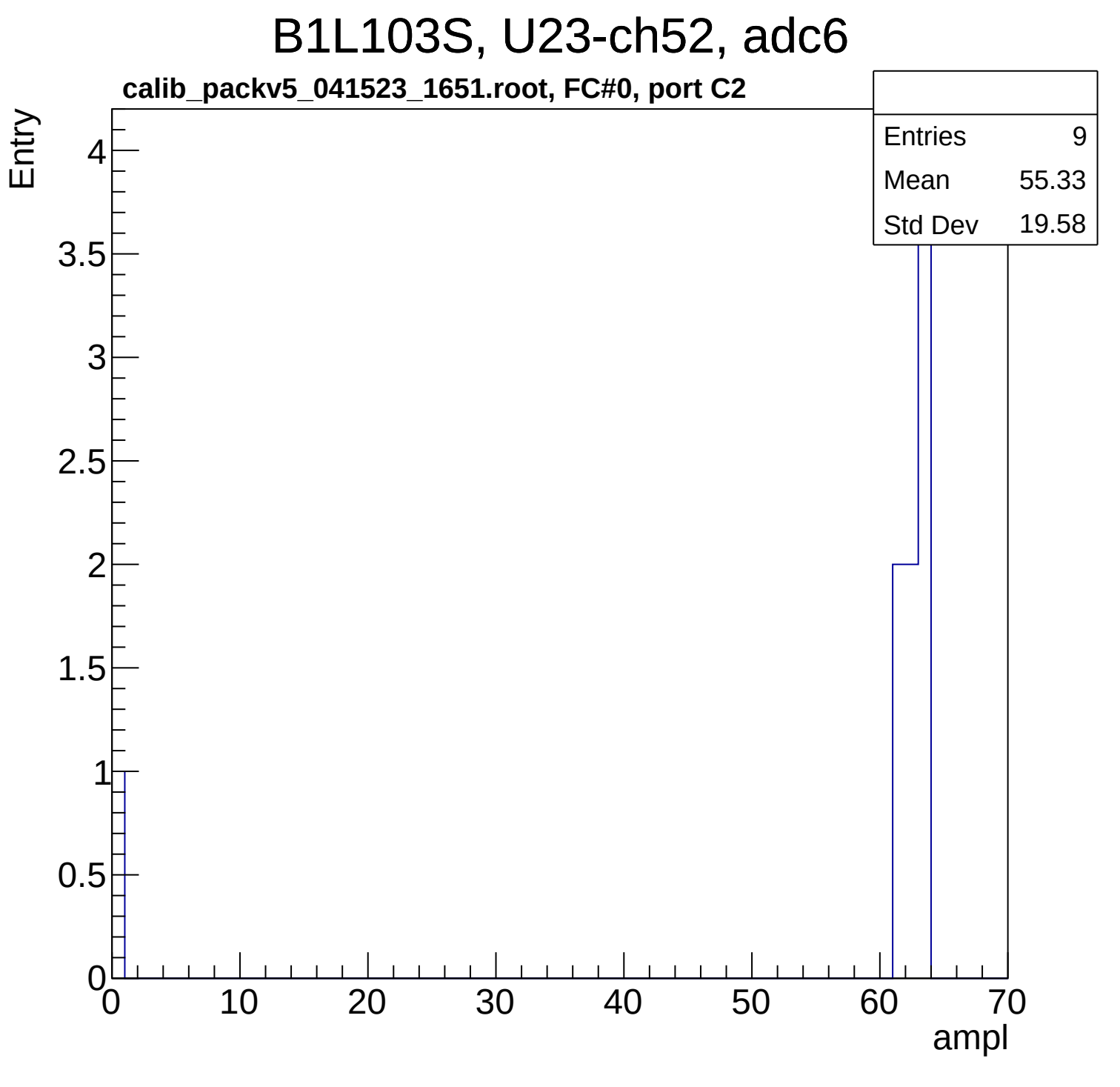

B1L103S, U23-ch52, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 34.57
Std Dev 7.561
Gaus mean : 36.2682
Gaus Width: 4.0889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

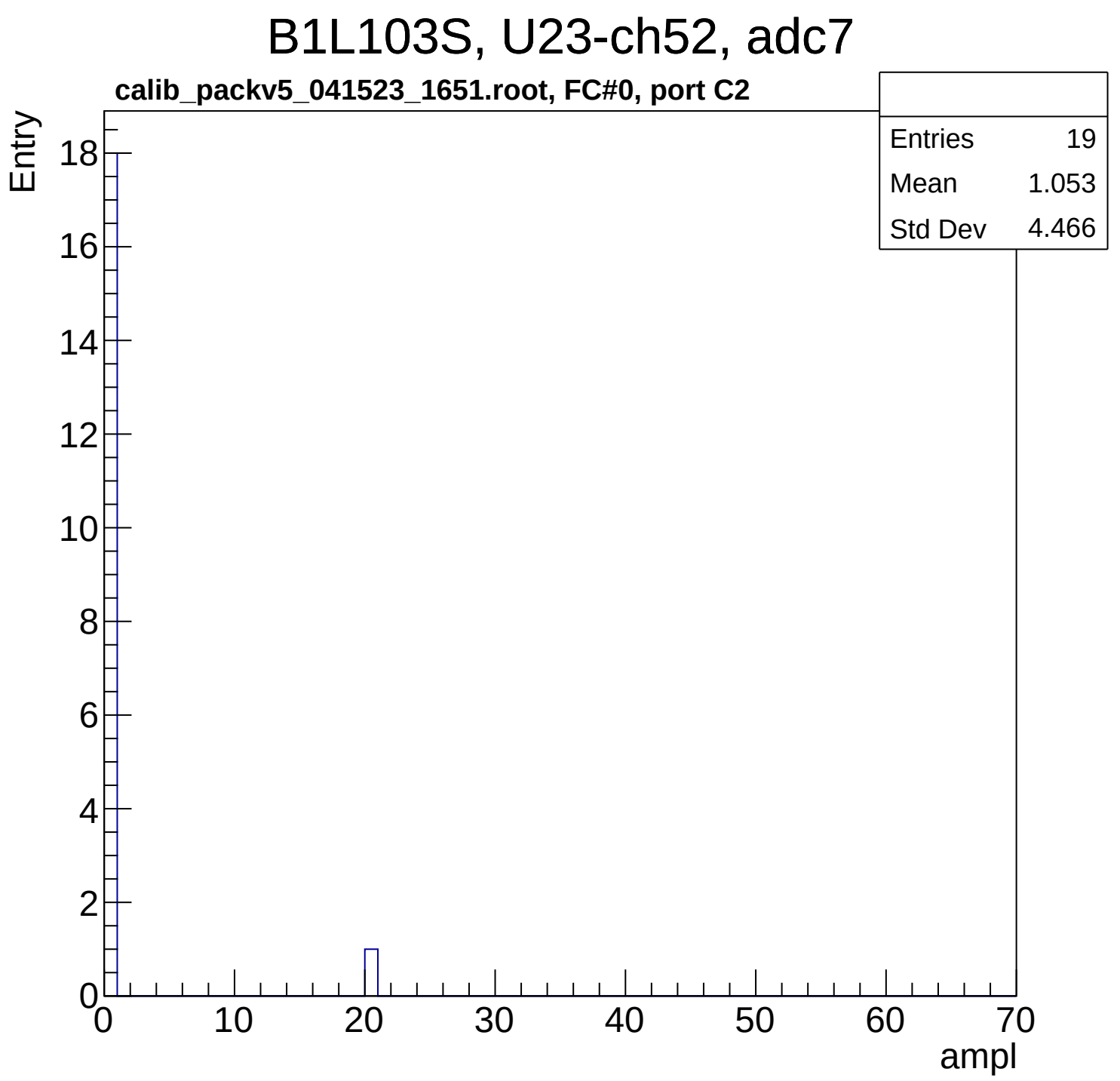

B1L103S, U23-ch52, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 36.42
Std Dev 14.47
Gaus mean : 43.0948
Gaus Width: 3.6585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

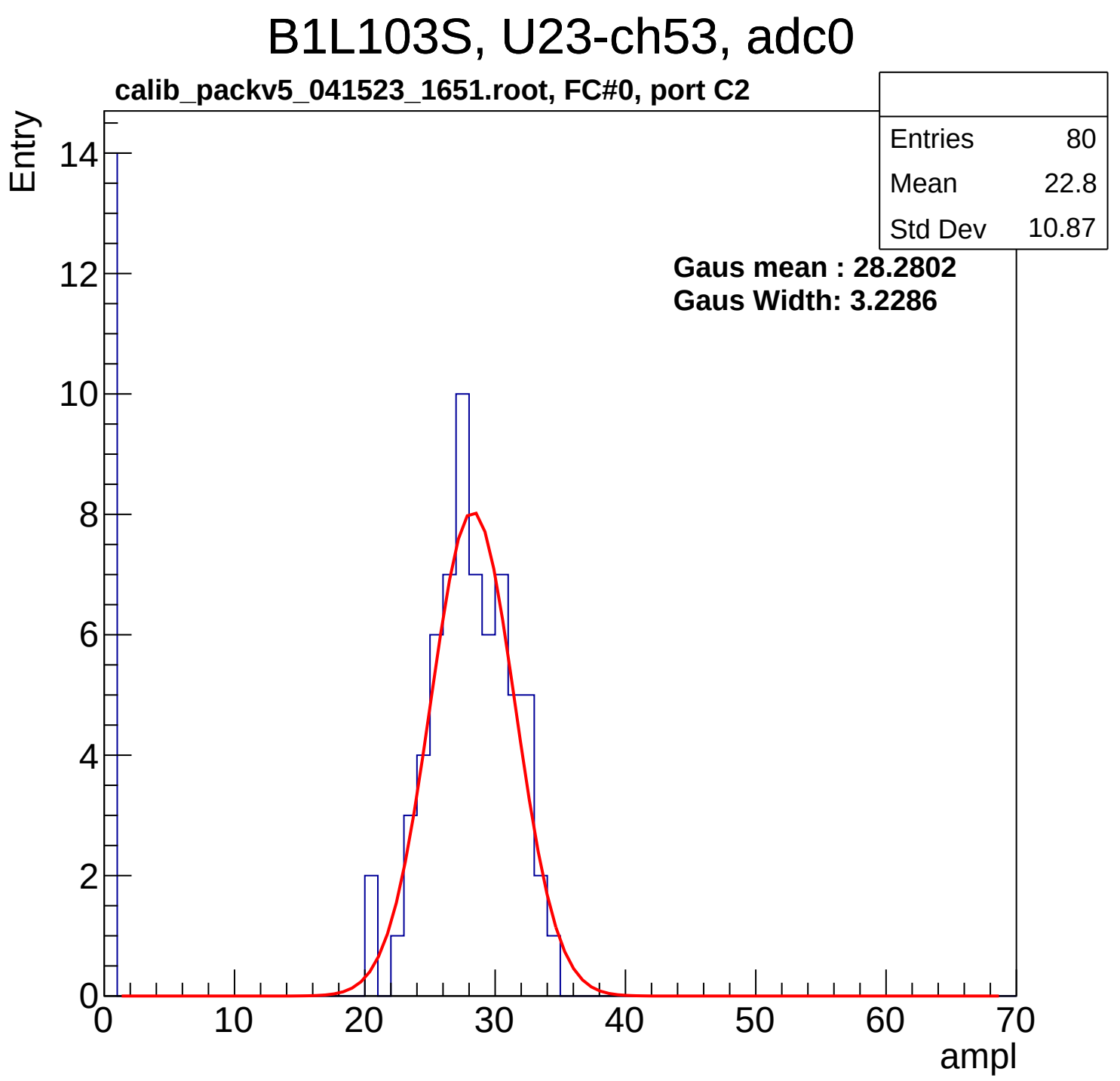

B1L103S, U23-ch52, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 48.09
Std Dev 6.344
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

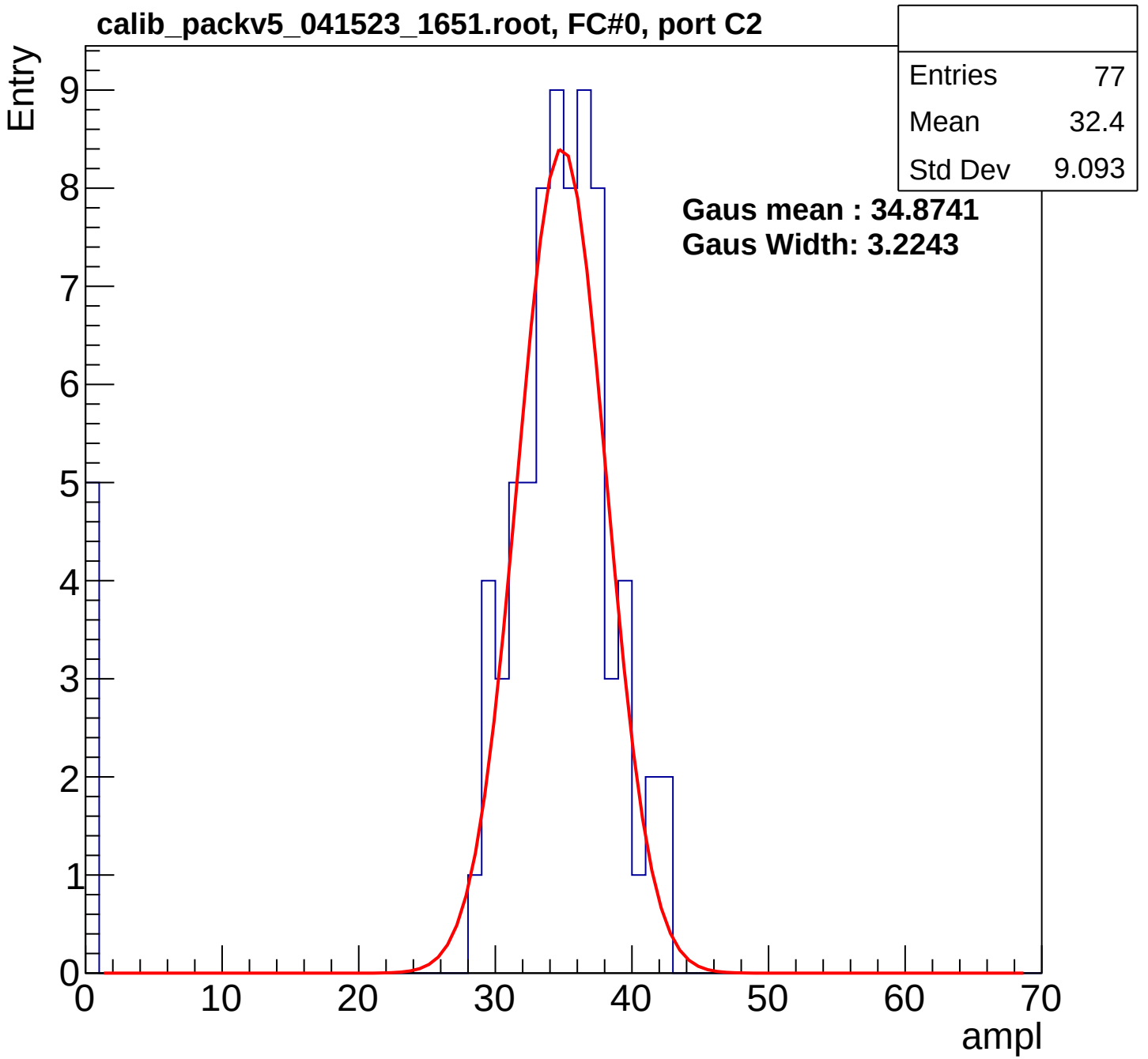

B1L103S, U23-ch52, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 50
Mean 55.04
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

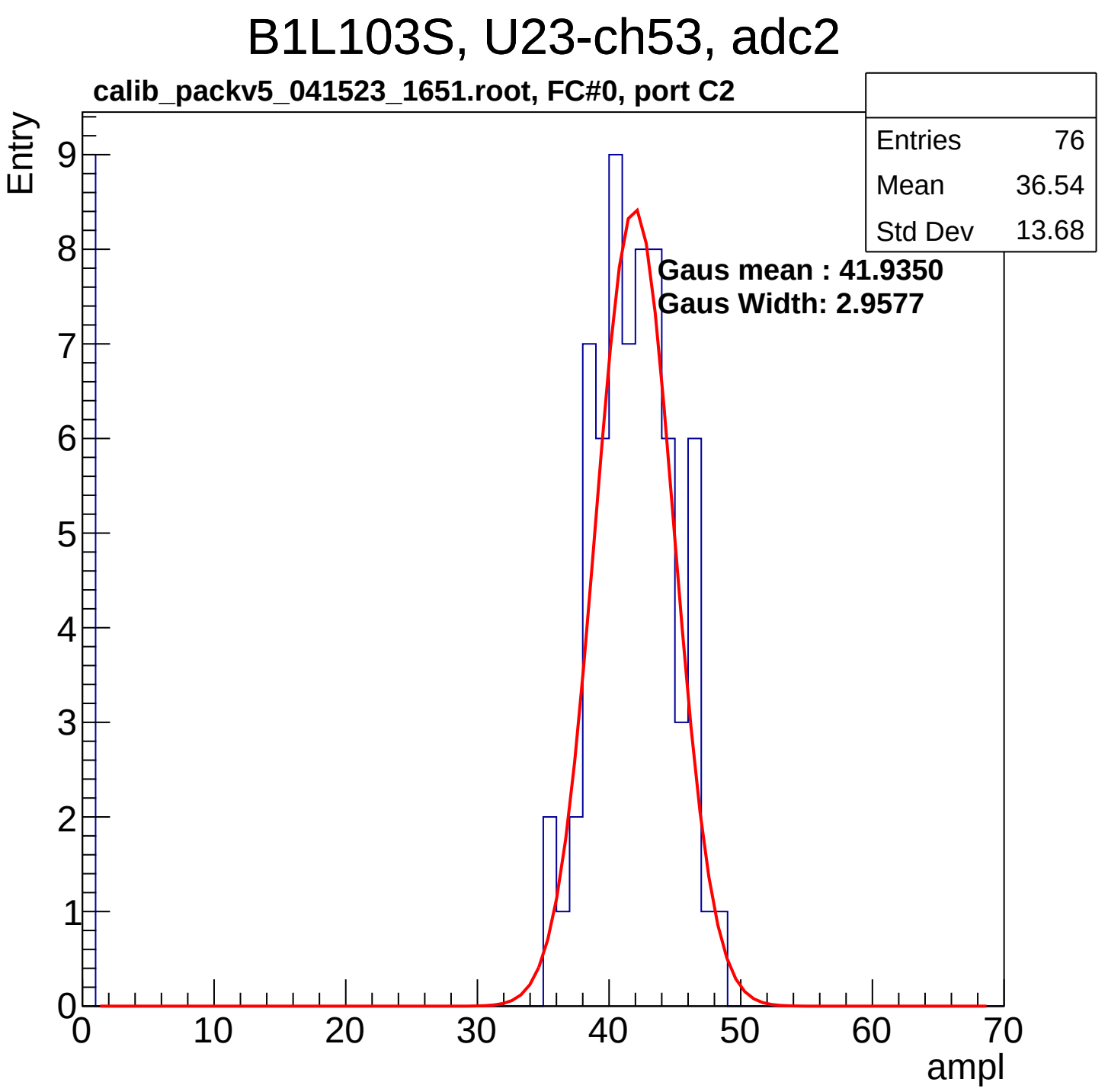

B1L103S, U23-ch52, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 59.57
Std Dev 2.452
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

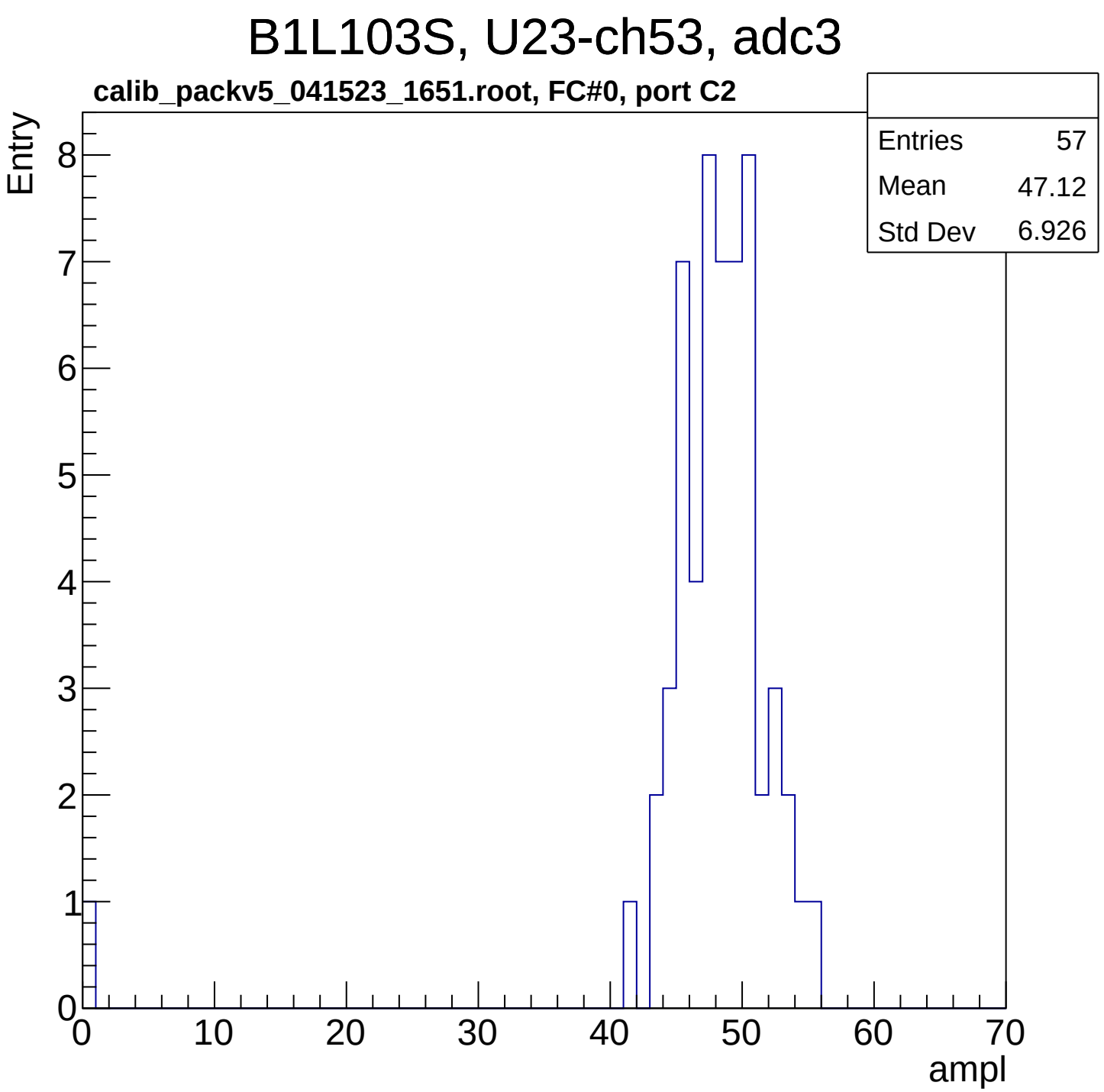

B1L103S, U23-ch52, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

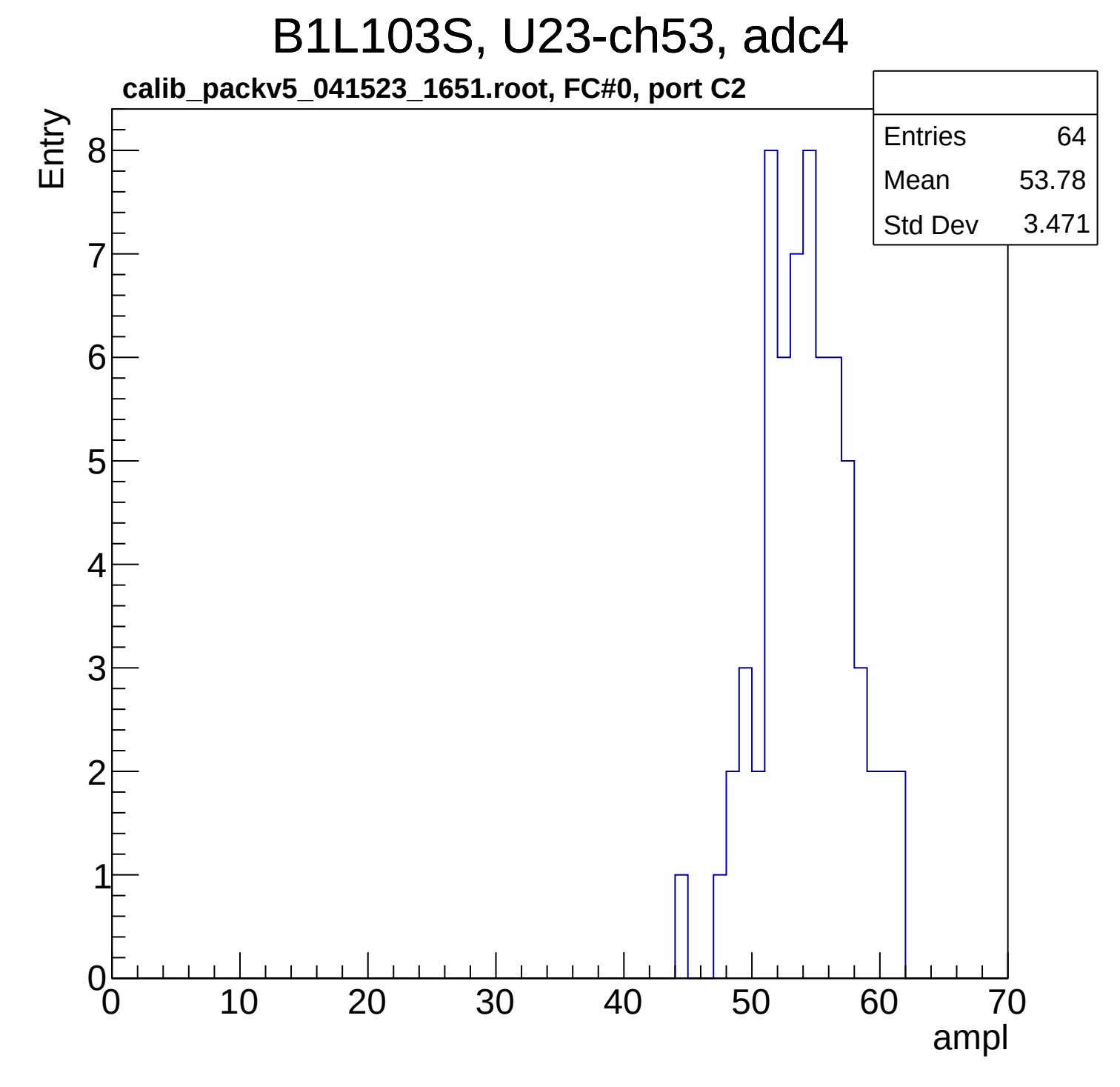

B1L103S, U23-ch52, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.053
Std Dev 4.466
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

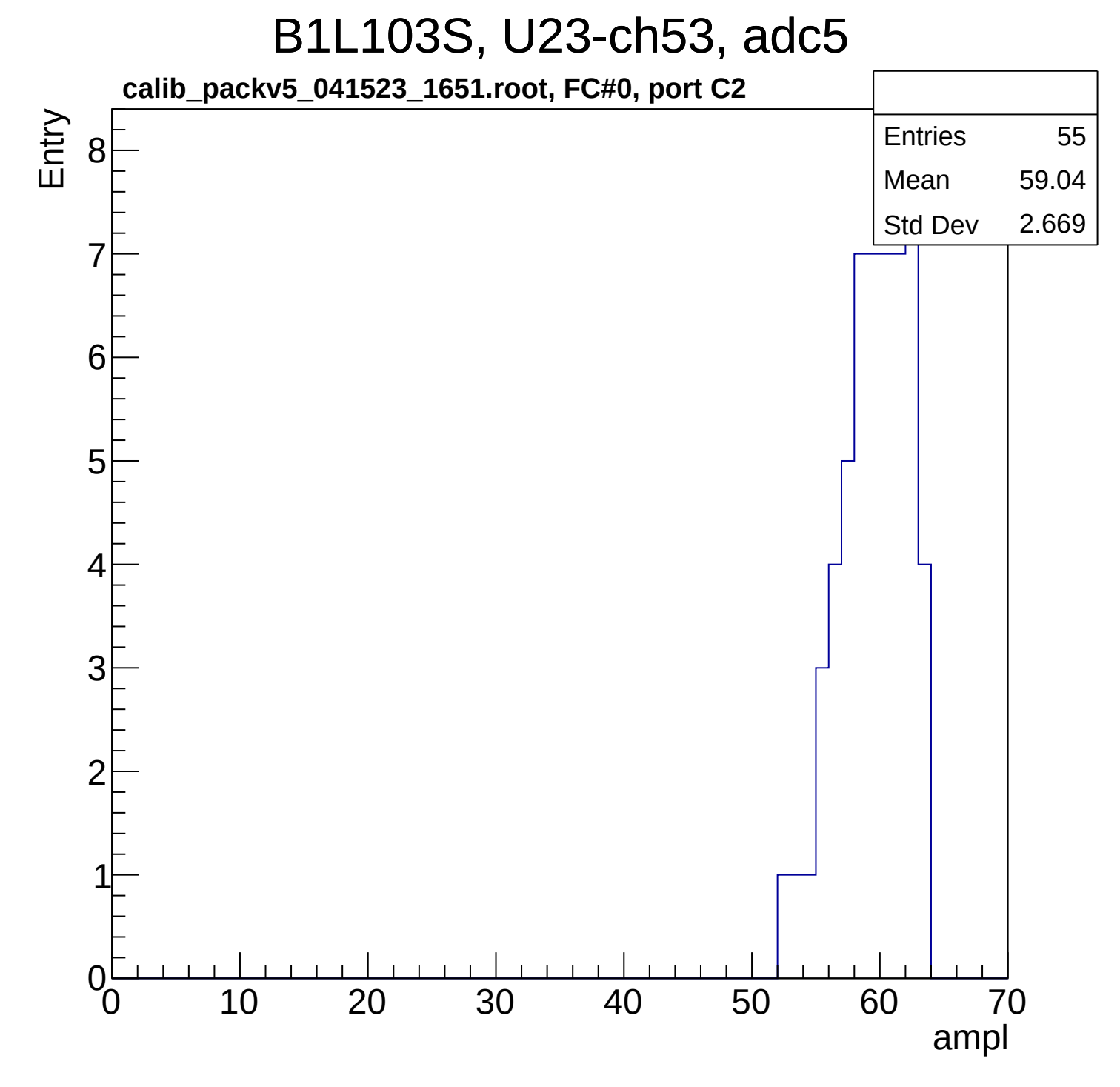

B1L103S, U23-ch53, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 22.8
Std Dev 10.87
Gaus mean : 28.2802
Gaus Width: 3.2286
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

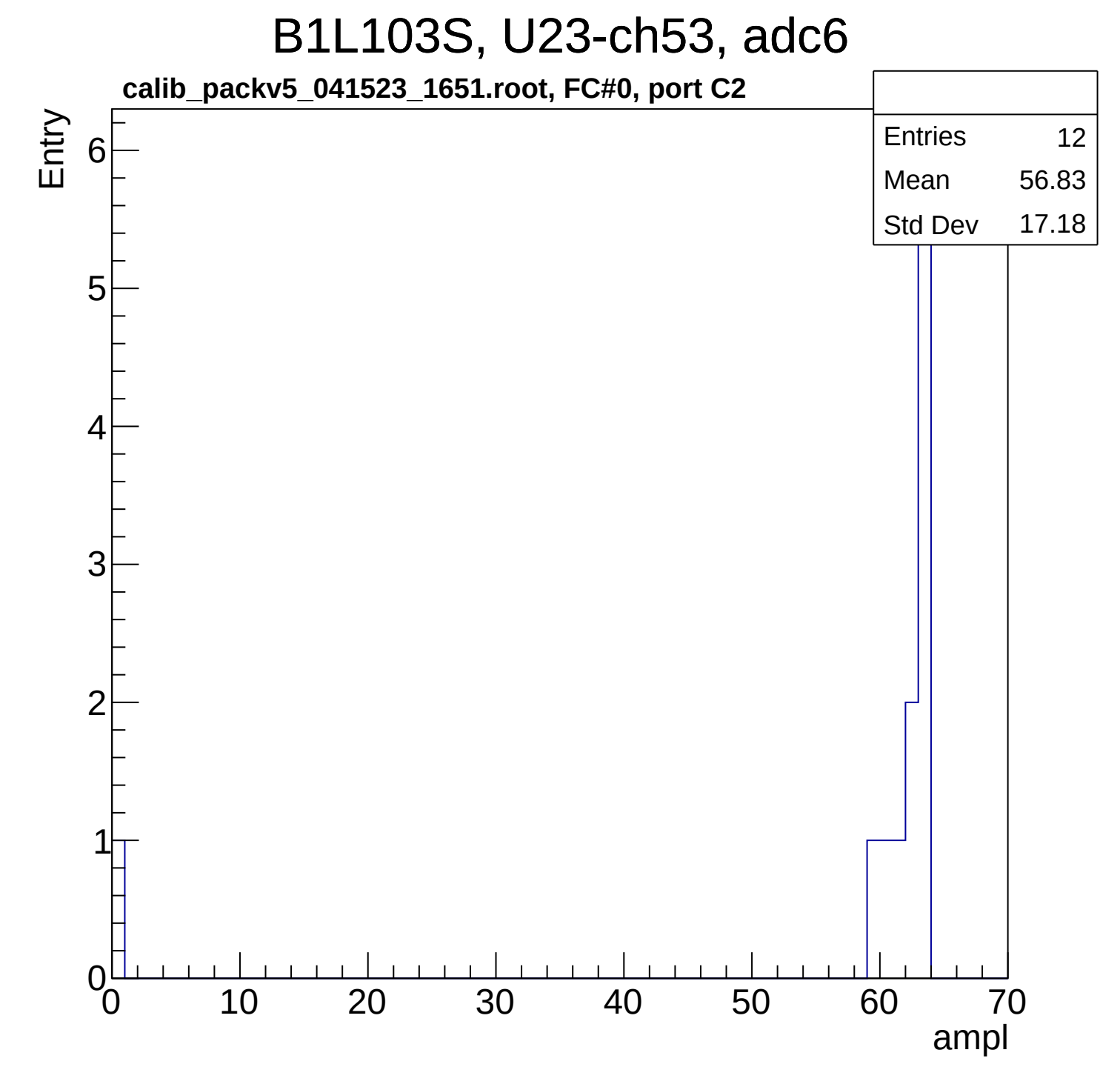

B1L103S, U23-ch53, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 32.4
Std Dev 9.093
Gaus mean : 34.8741
Gaus Width: 3.2243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

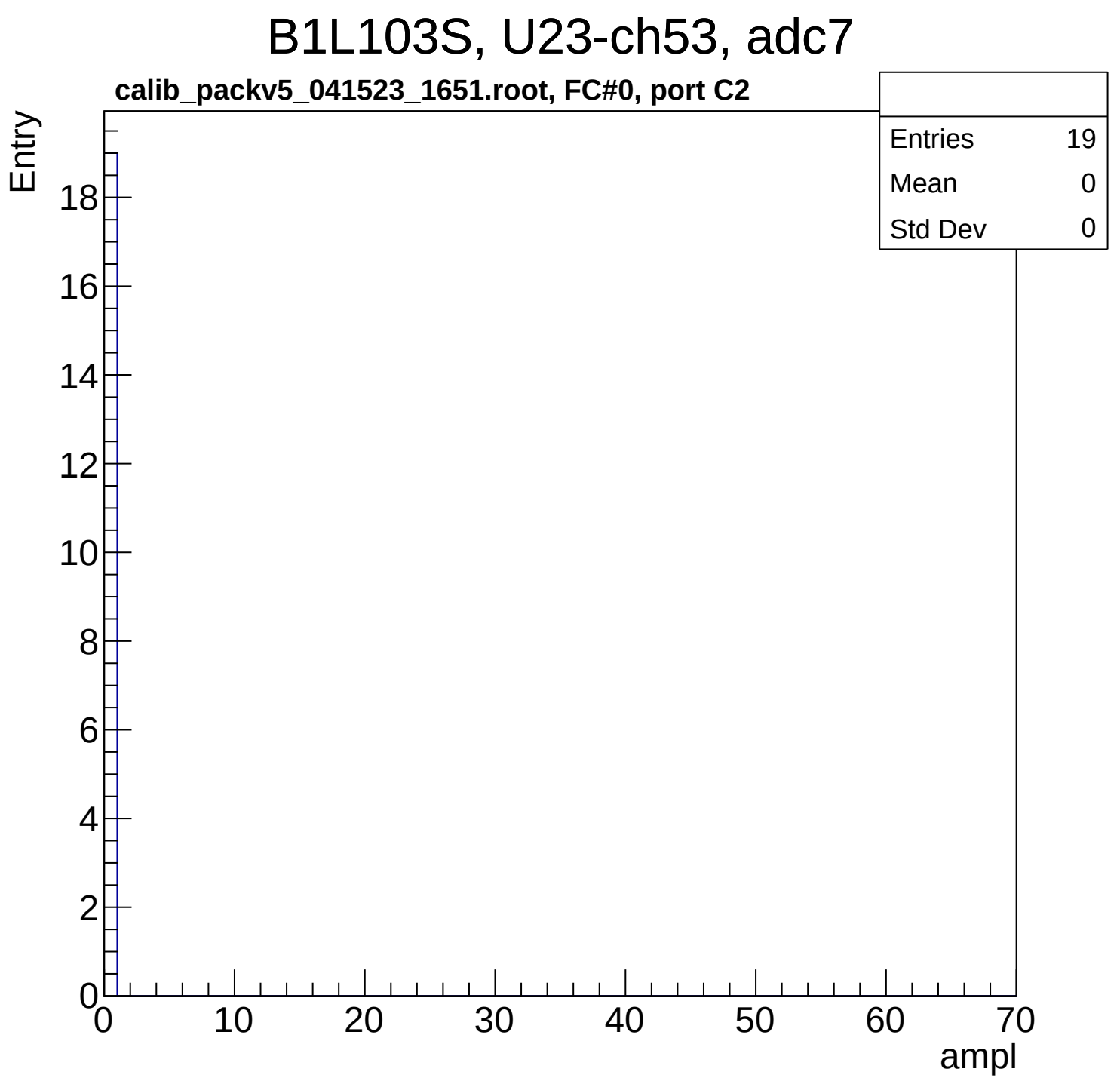

B1L103S, U23-ch53, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 36.54
Std Dev 13.68
Gaus mean : 41.9350
Gaus Width: 2.9577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

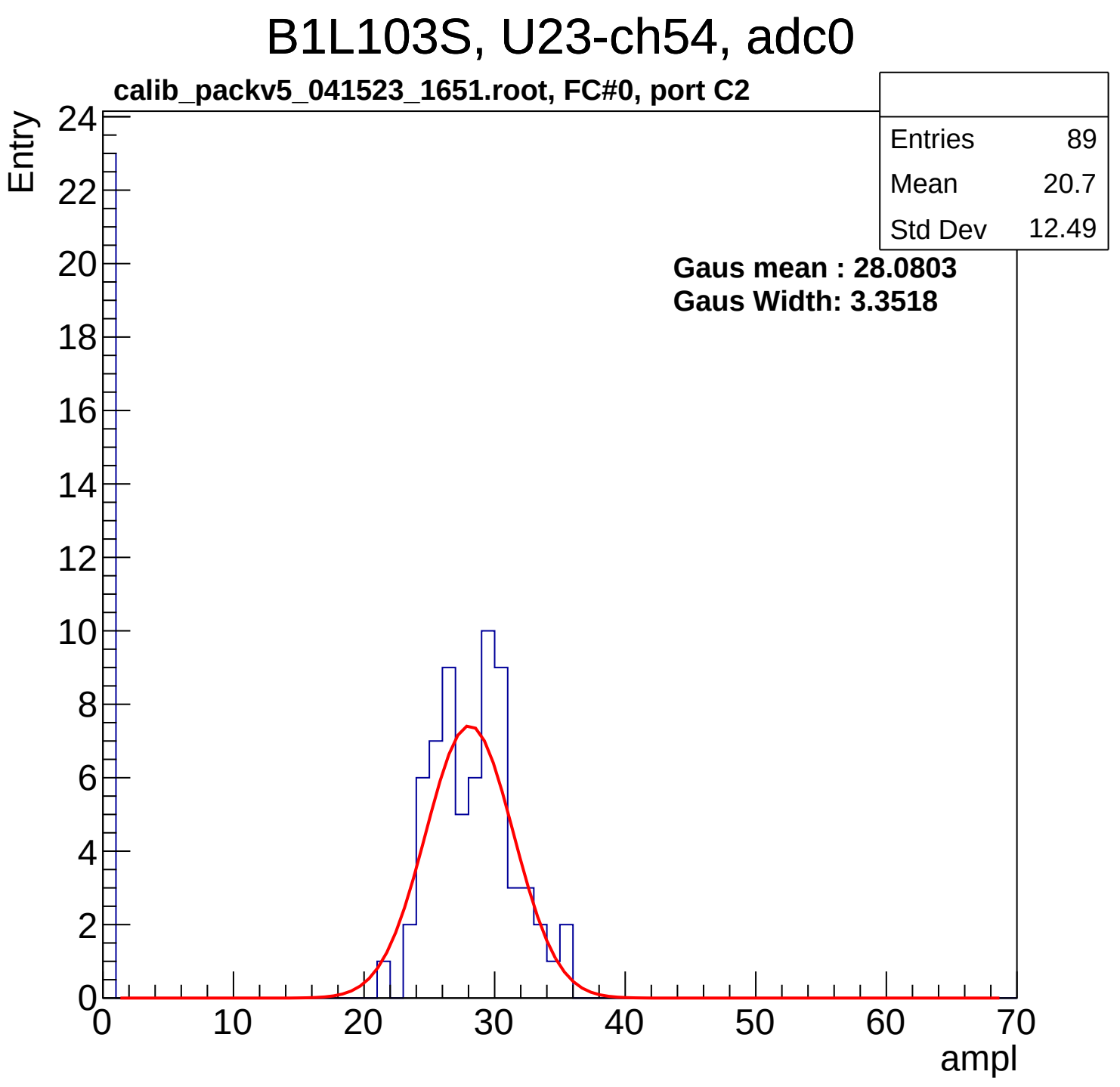

B1L103S, U23-ch53, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 47.12
Std Dev 6.926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

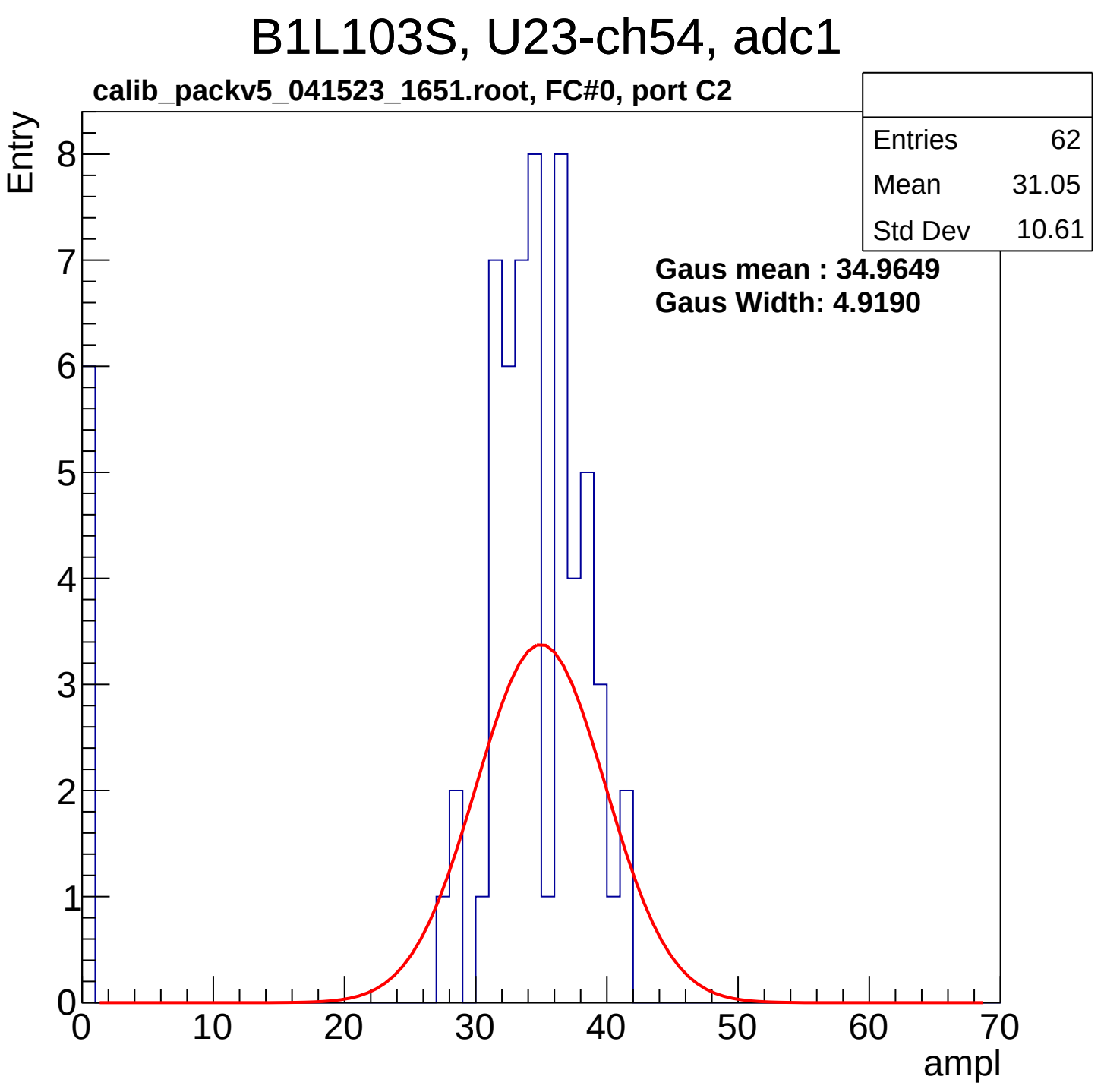

B1L103S, U23-ch53, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 53.78
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

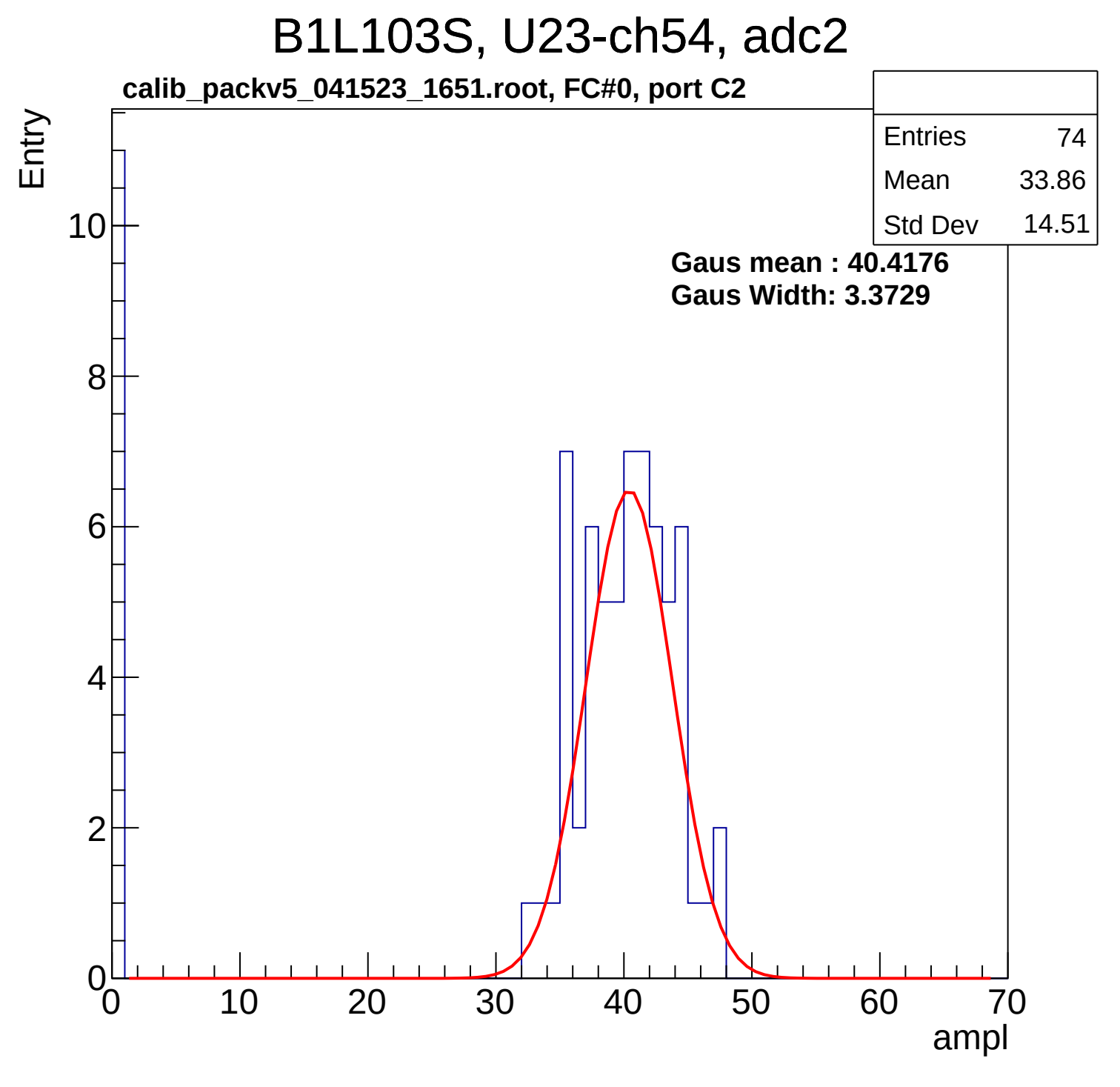

B1L103S, U23-ch53, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 55
Mean 59.04
Std Dev 2.669
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

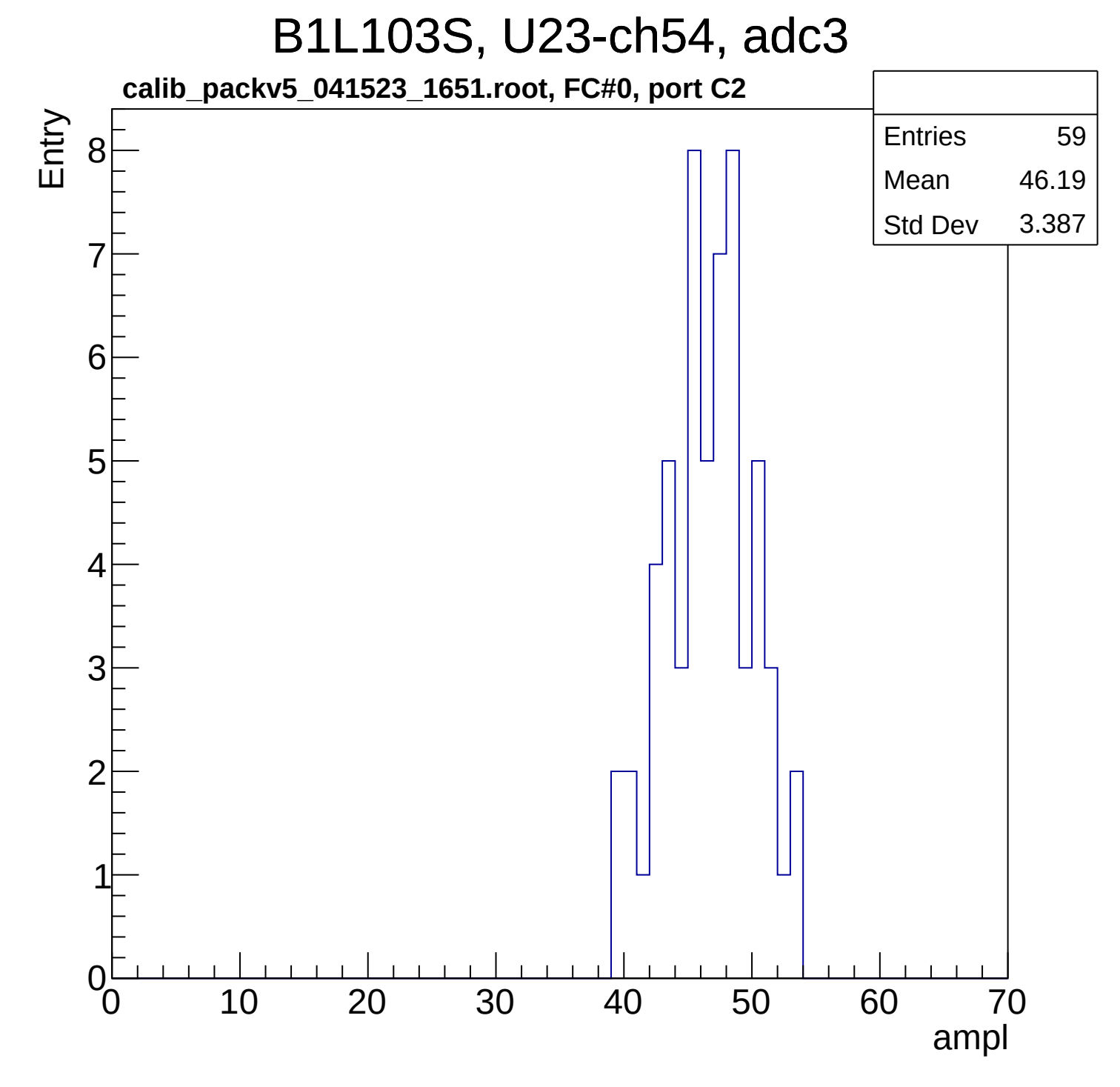

B1L103S, U23-ch53, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 12
Mean 56.83
Std Dev 17.18
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

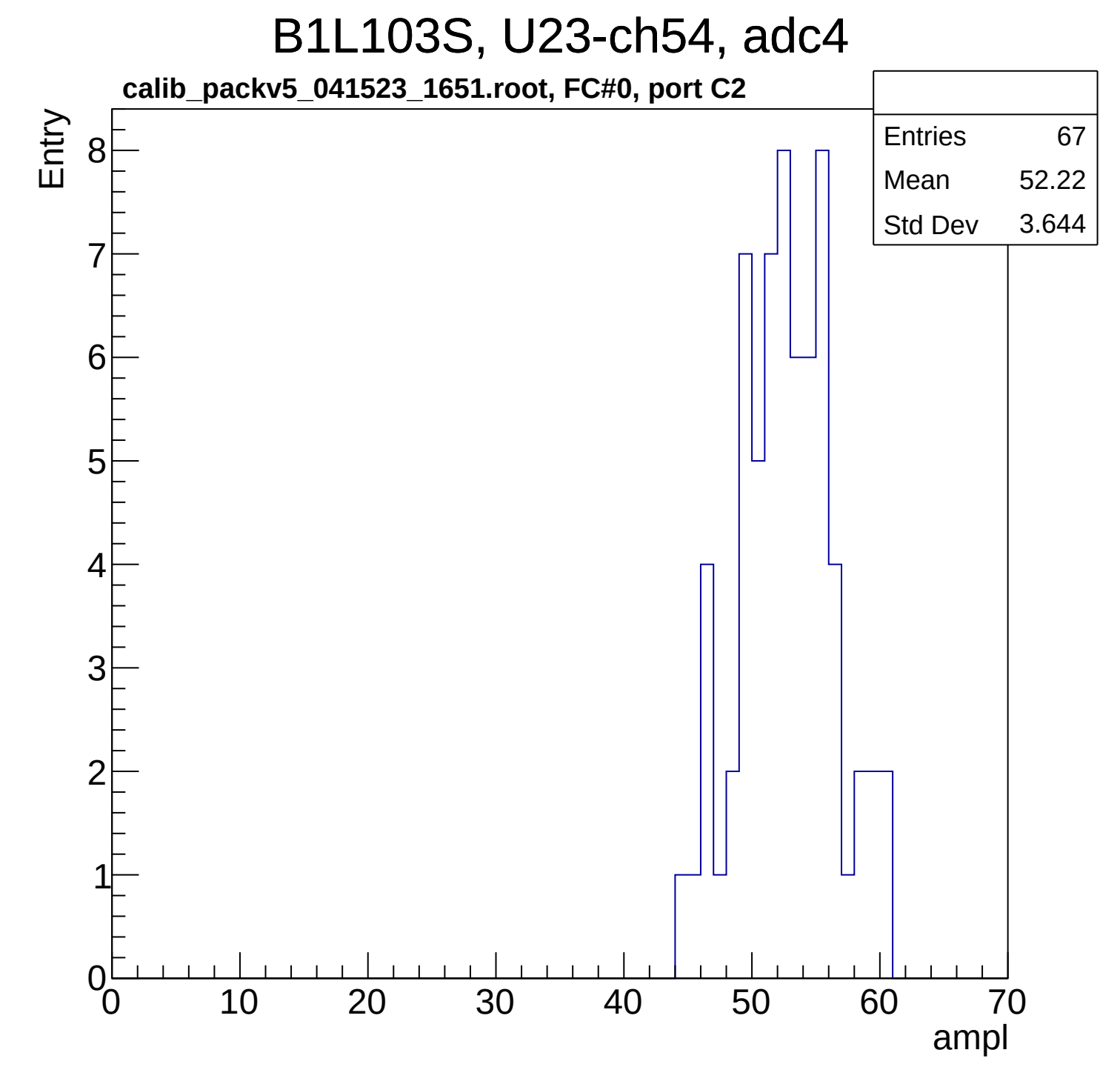

B1L103S, U23-ch53, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

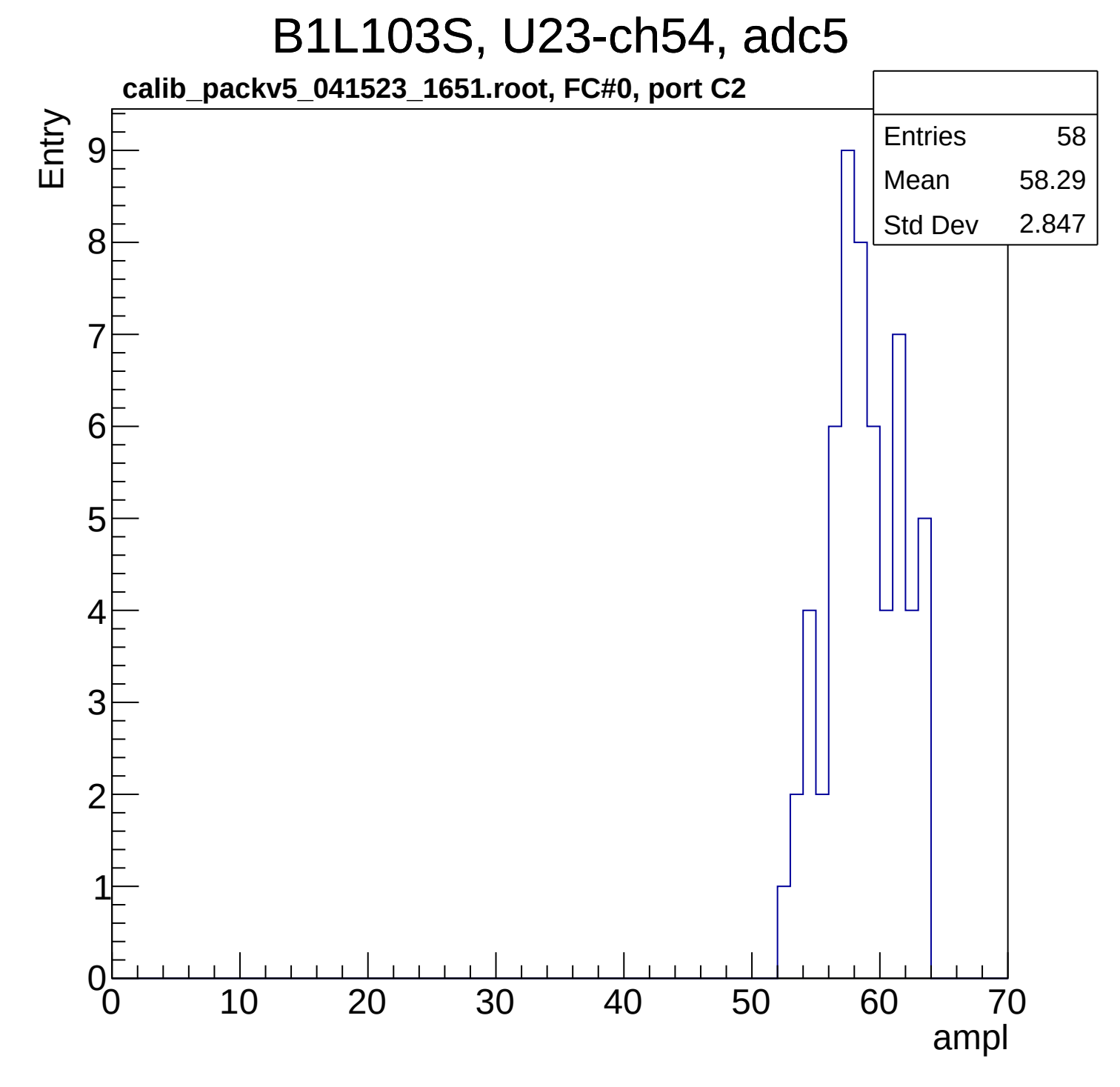

B1L103S, U23-ch54, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 20.7
Std Dev 12.49
Gaus mean : 28.0803
Gaus Width: 3.3518
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
24
0
10
20
30
40
50
60
70

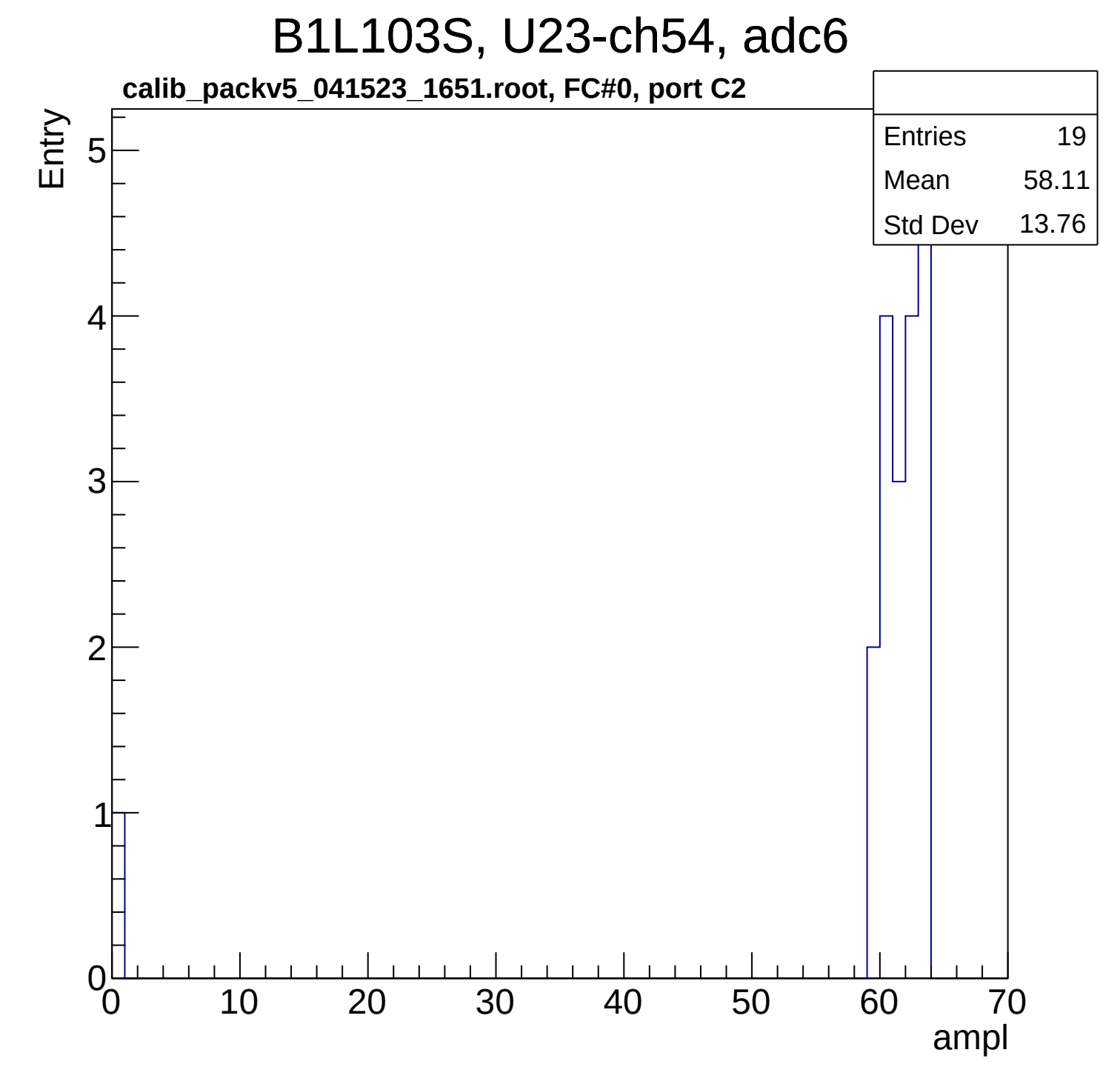

B1L103S, U23-ch54, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 31.05
Std Dev 10.61
Gaus mean : 34.9649
Gaus Width: 4.9190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

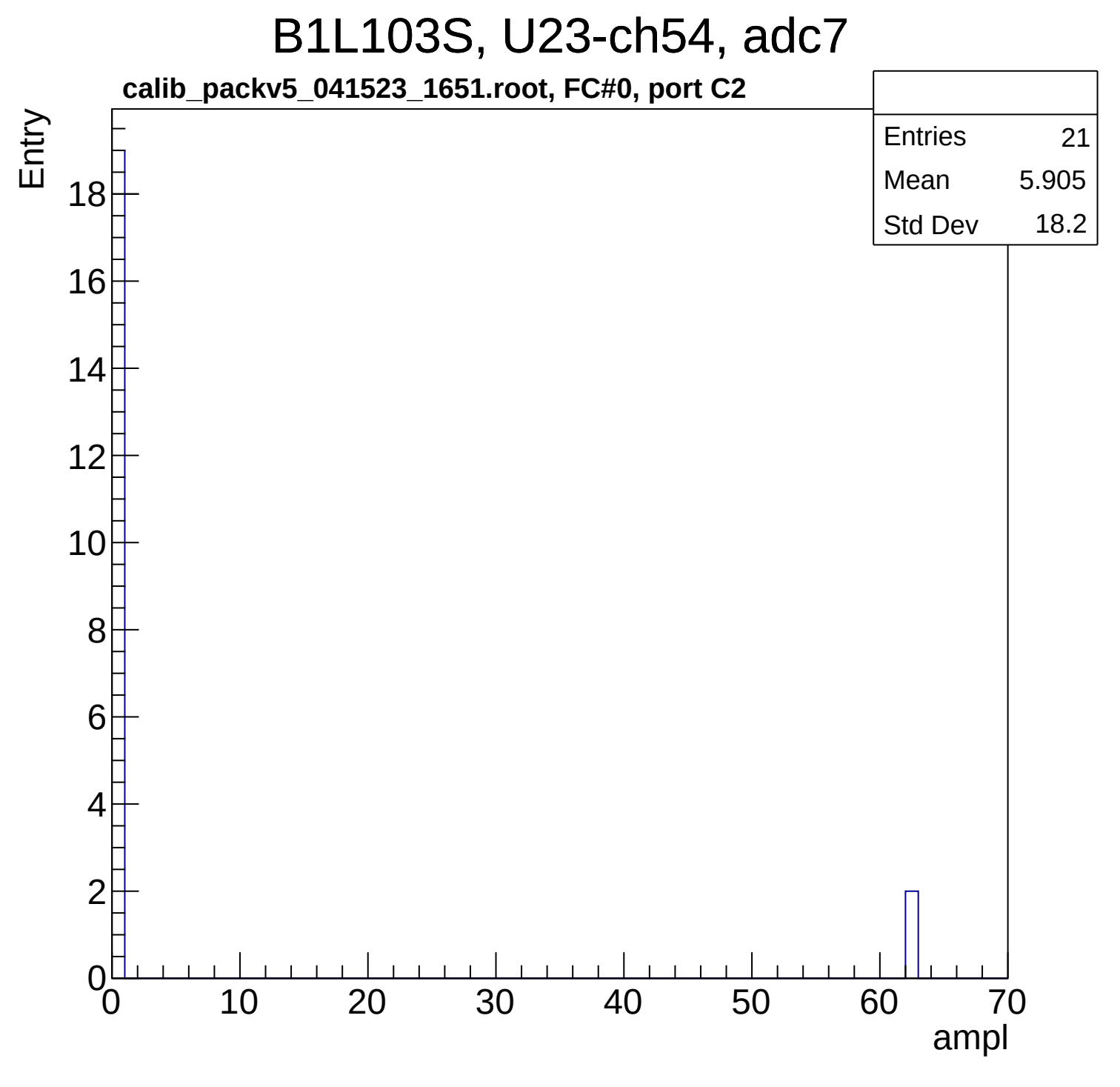

B1L103S, U23-ch54, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 33.86
Std Dev 14.51
Gaus mean : 40.4176
Gaus Width: 3.3729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

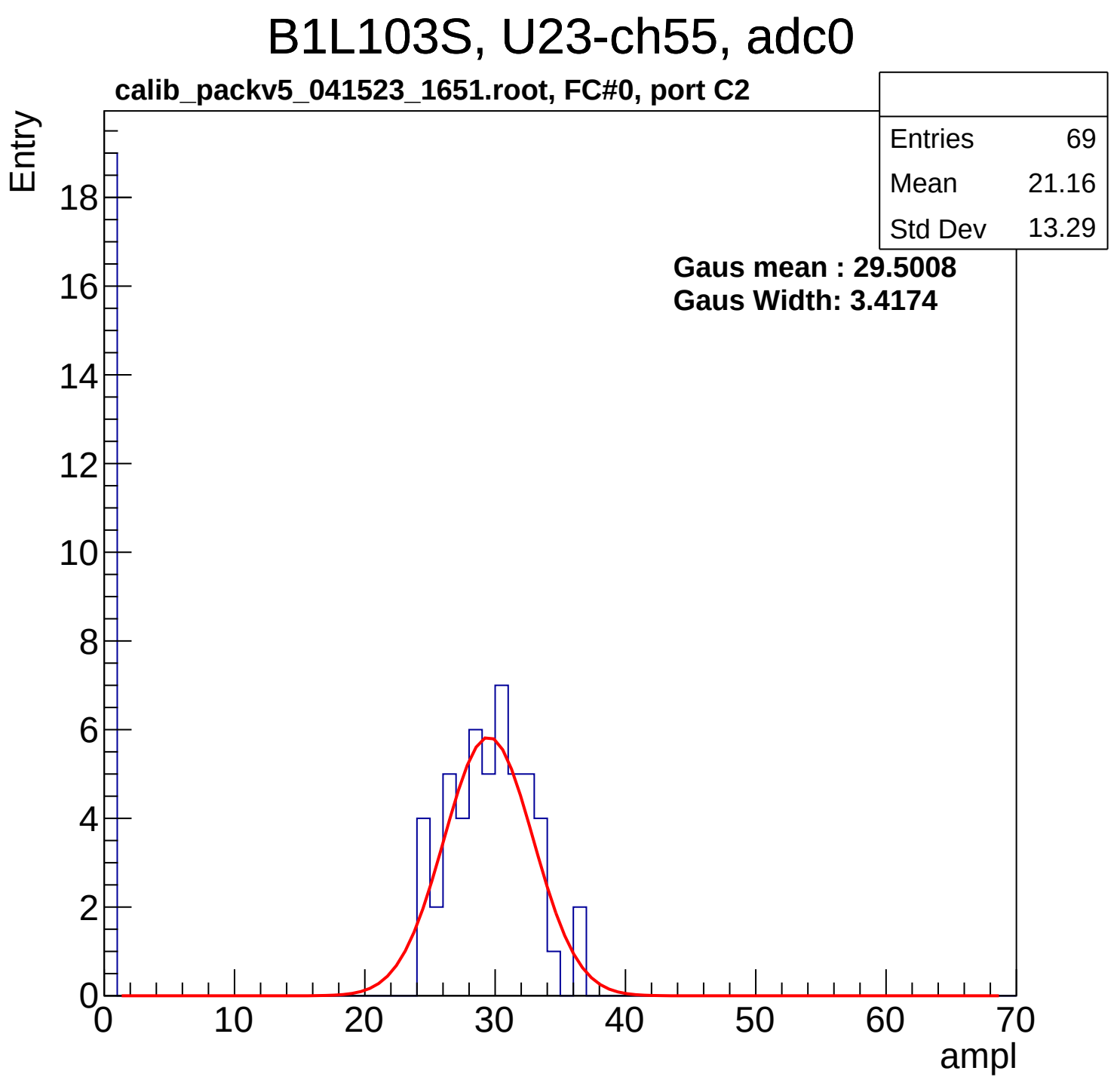

B1L103S, U23-ch54, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 46.19
Std Dev 3.387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

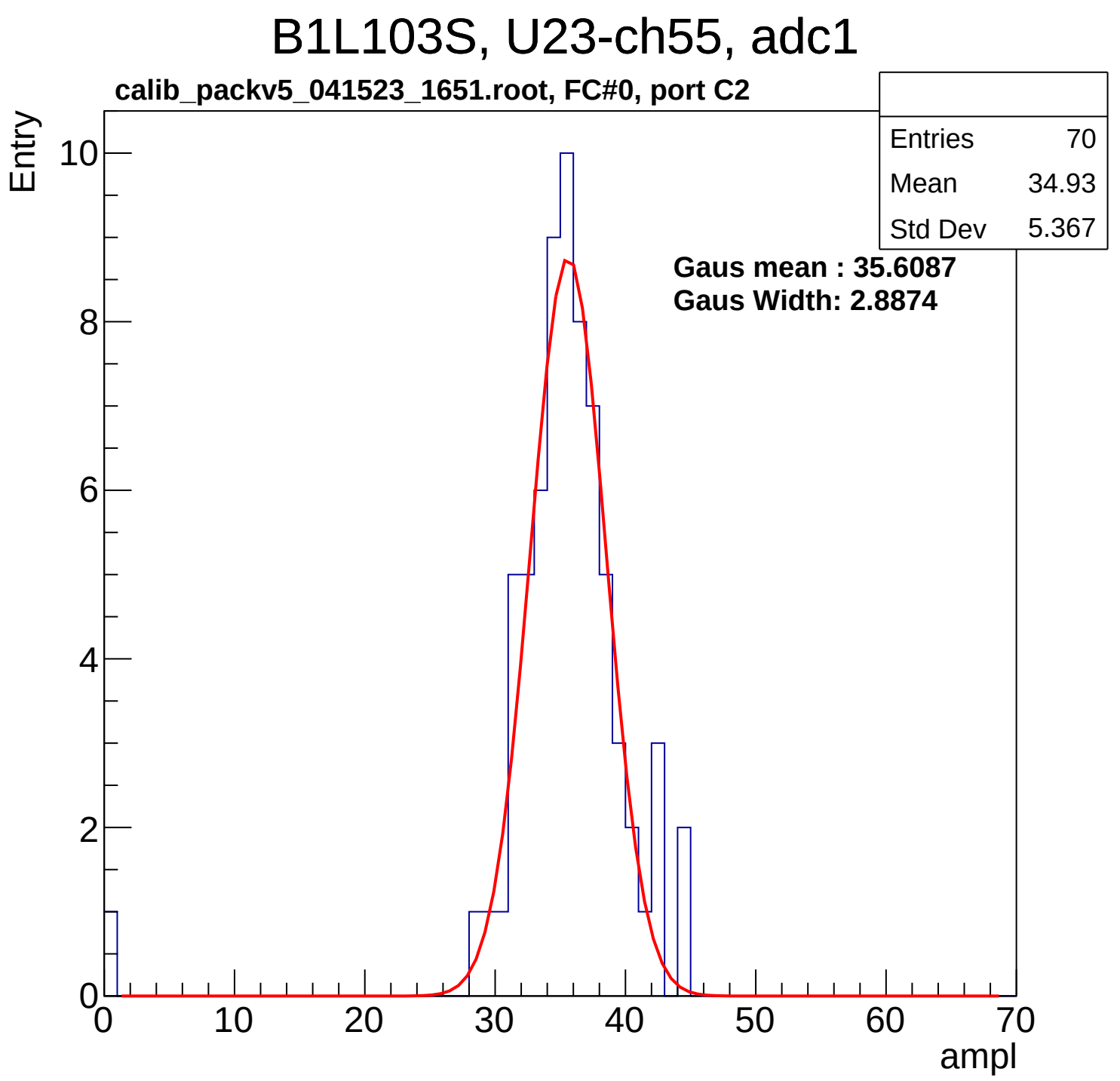

B1L103S, U23-ch54, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 52.22
Std Dev 3.644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

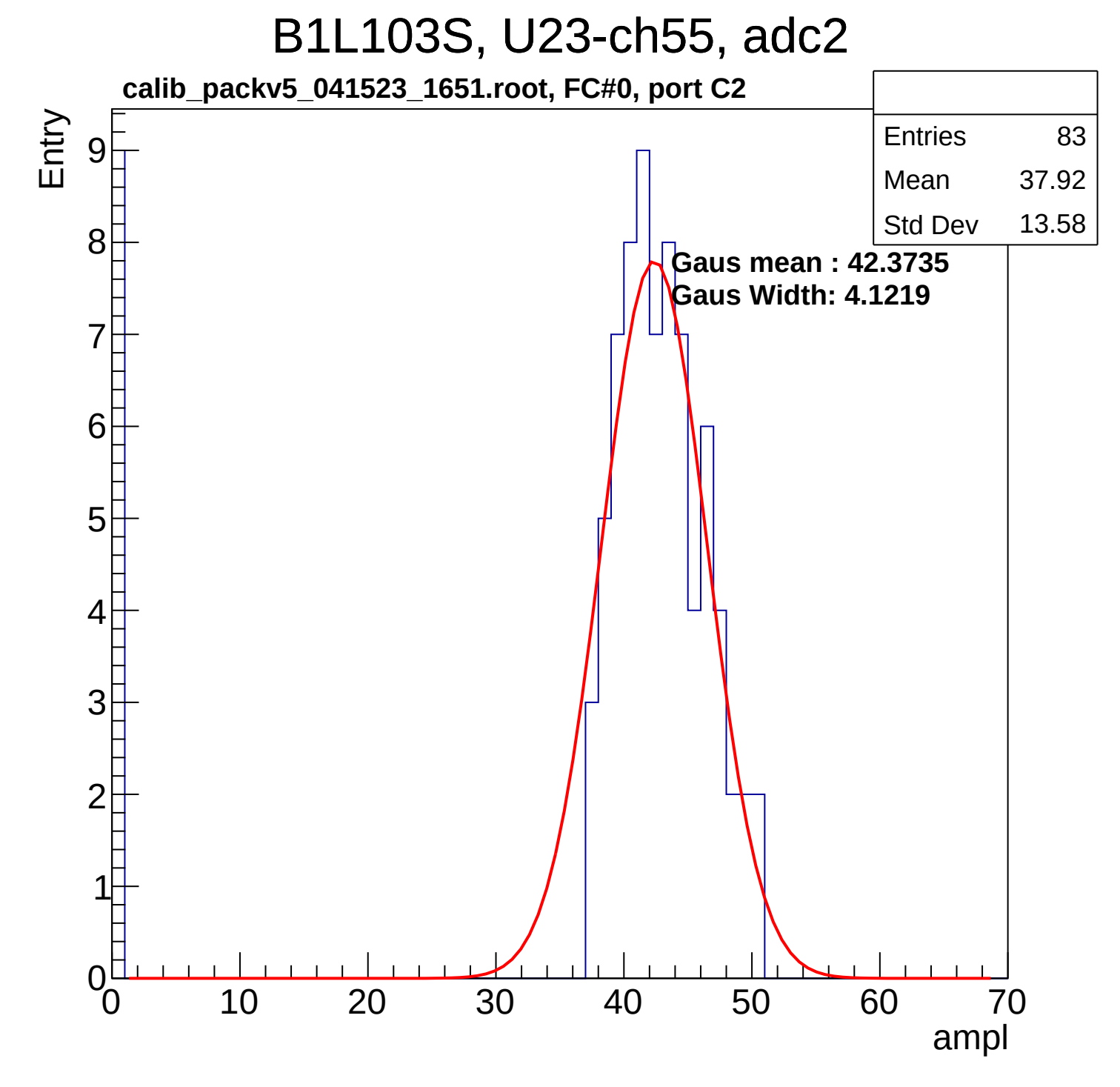

B1L103S, U23-ch54, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 58.29
Std Dev 2.847
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

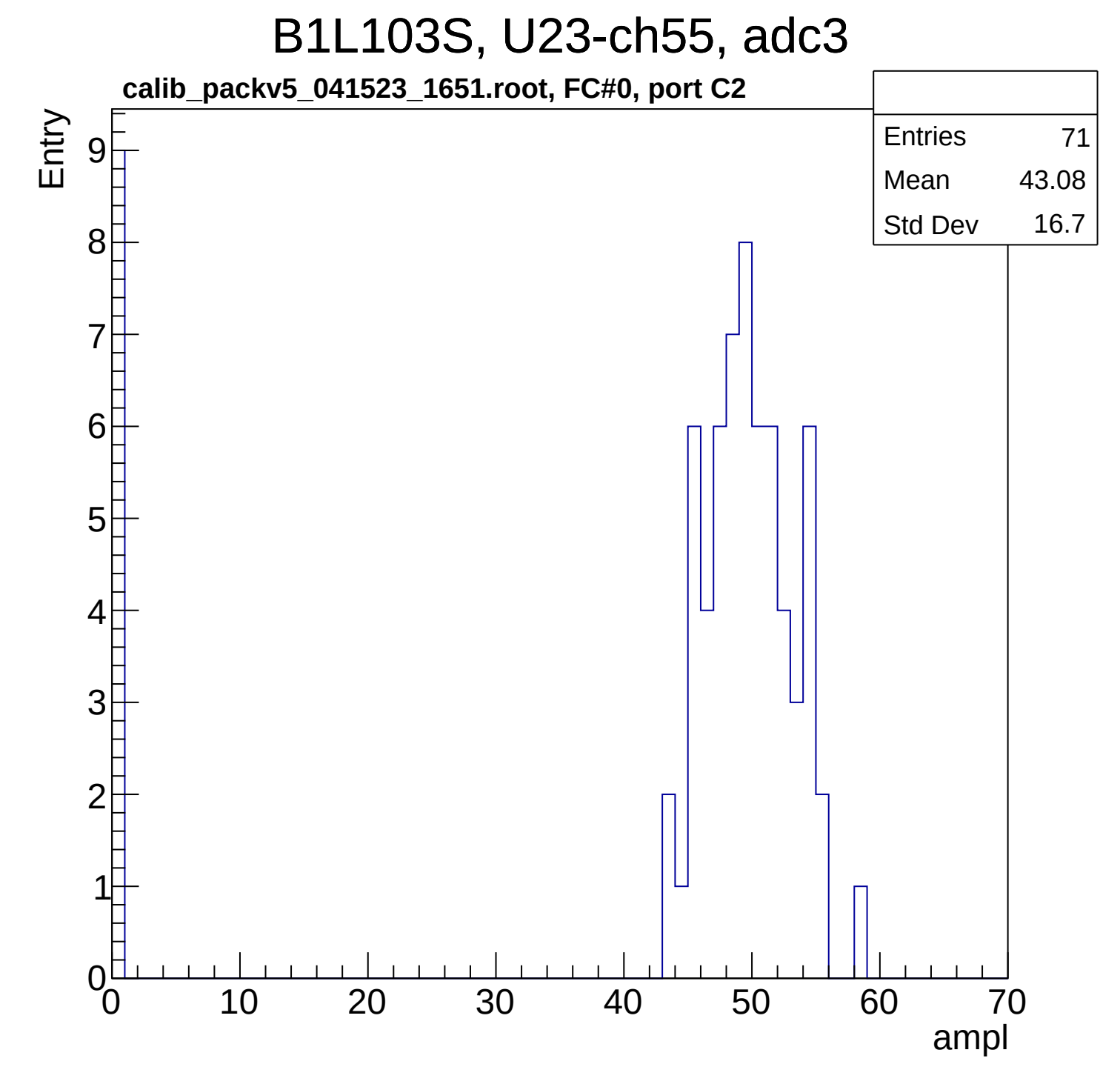

B1L103S, U23-ch54, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 58.11
Std Dev 13.76
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

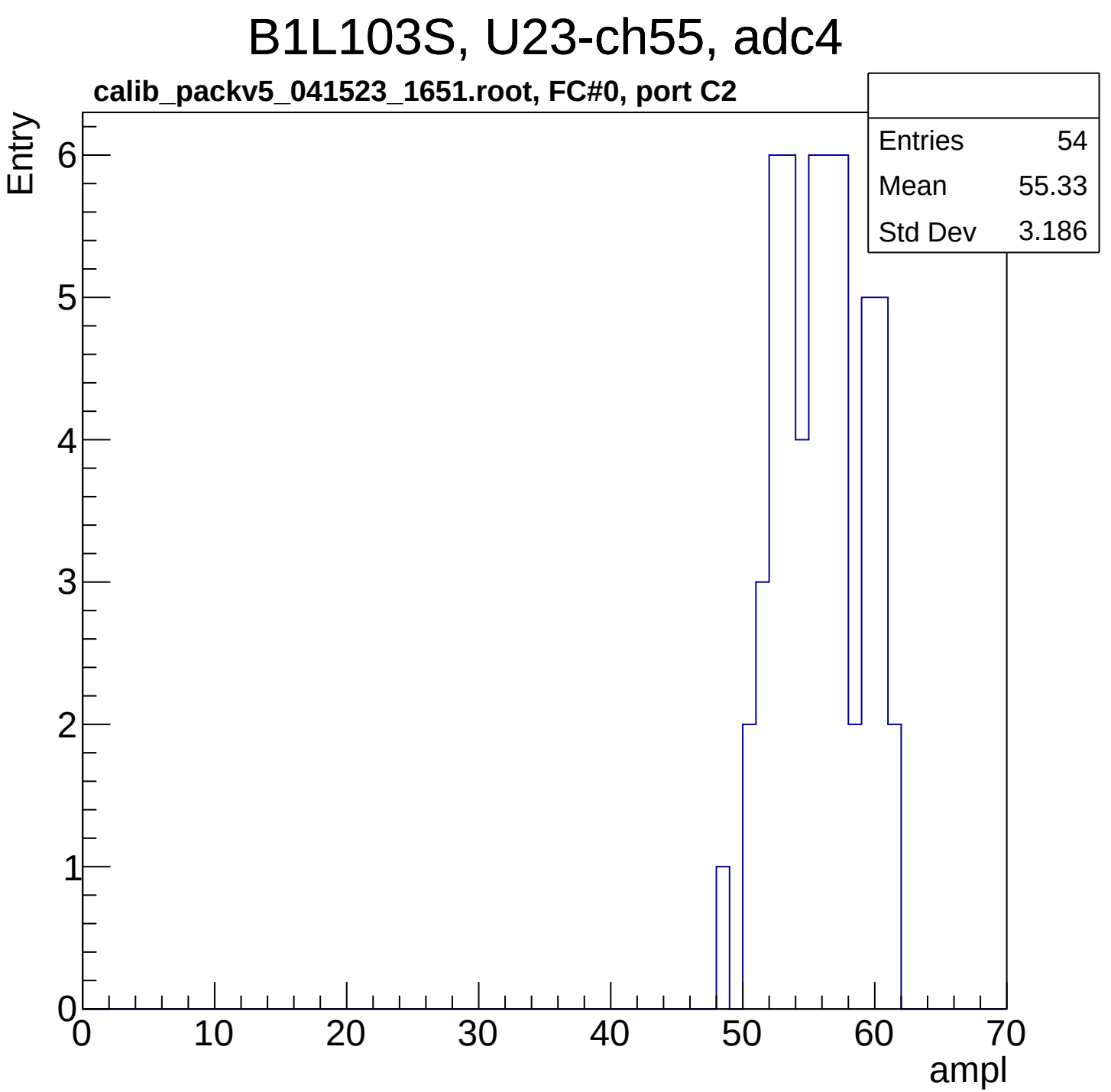

B1L103S, U23-ch54, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.905
Std Dev 18.2
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

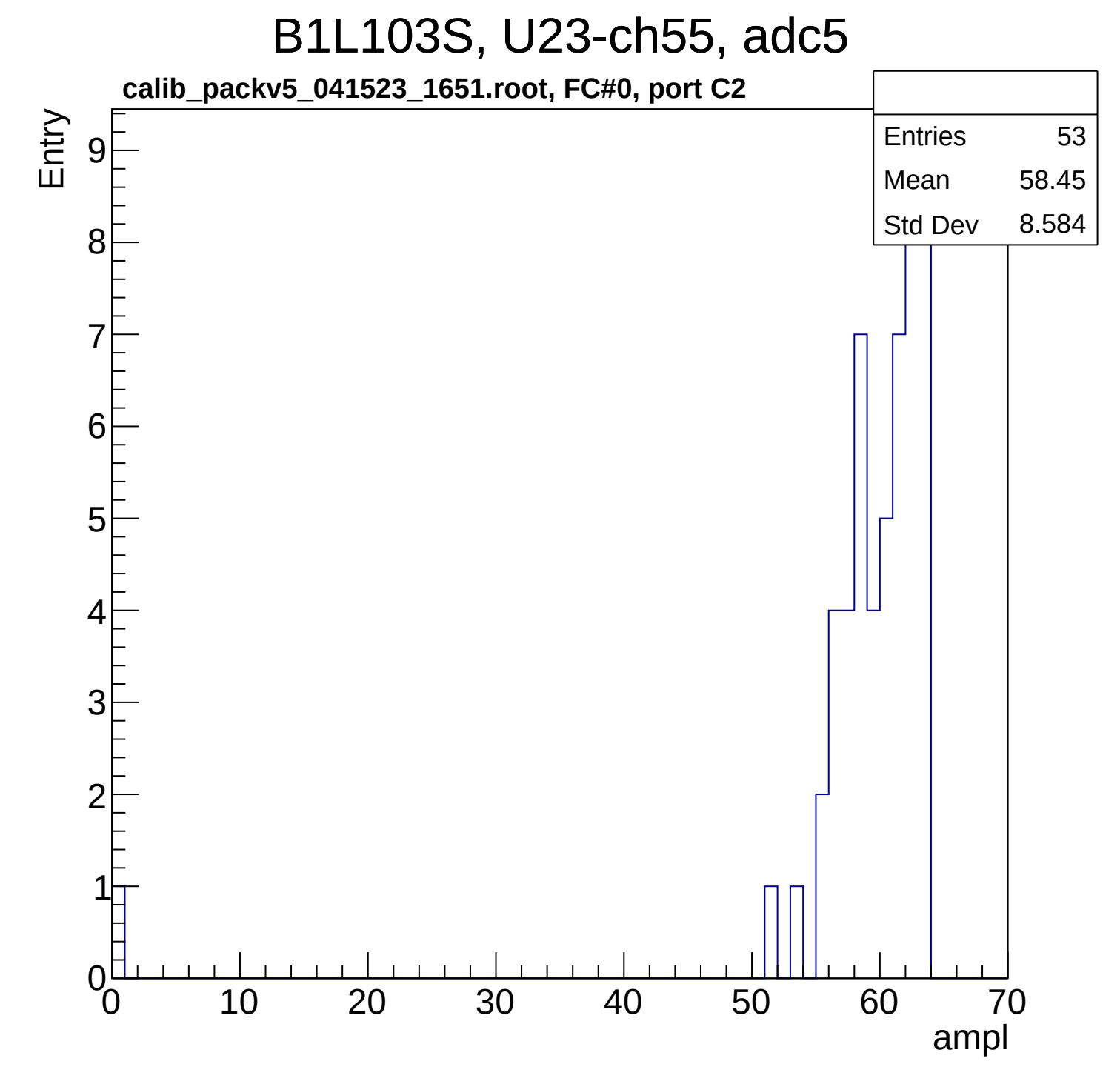

B1L103S, U23-ch55, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 21.16
Std Dev 13.29
Gaus mean : 29.5008
Gaus Width: 3.4174
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

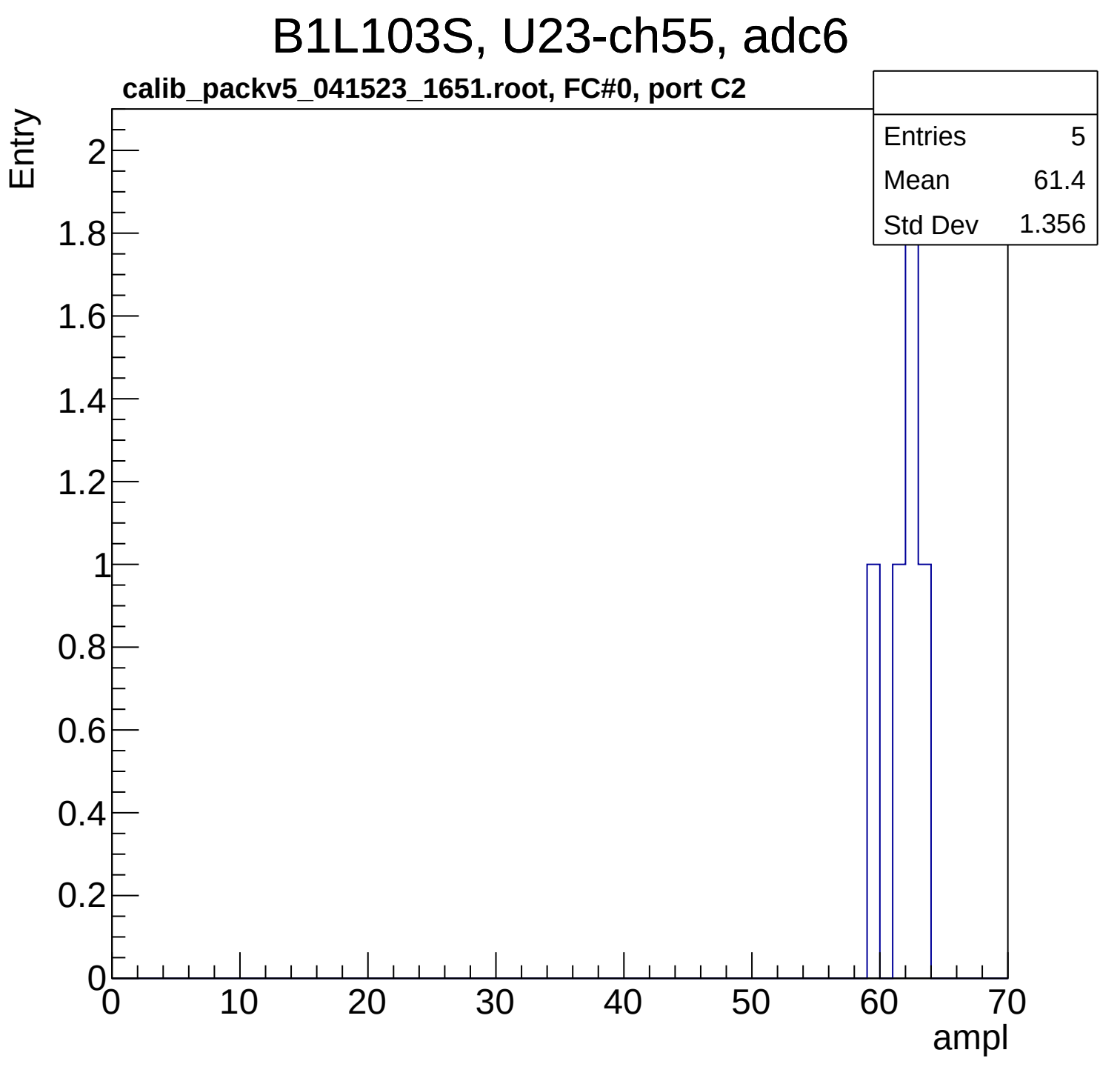

B1L103S, U23-ch55, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 34.93
Std Dev 5.367
Gaus mean : 35.6087
Gaus Width: 2.8874
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

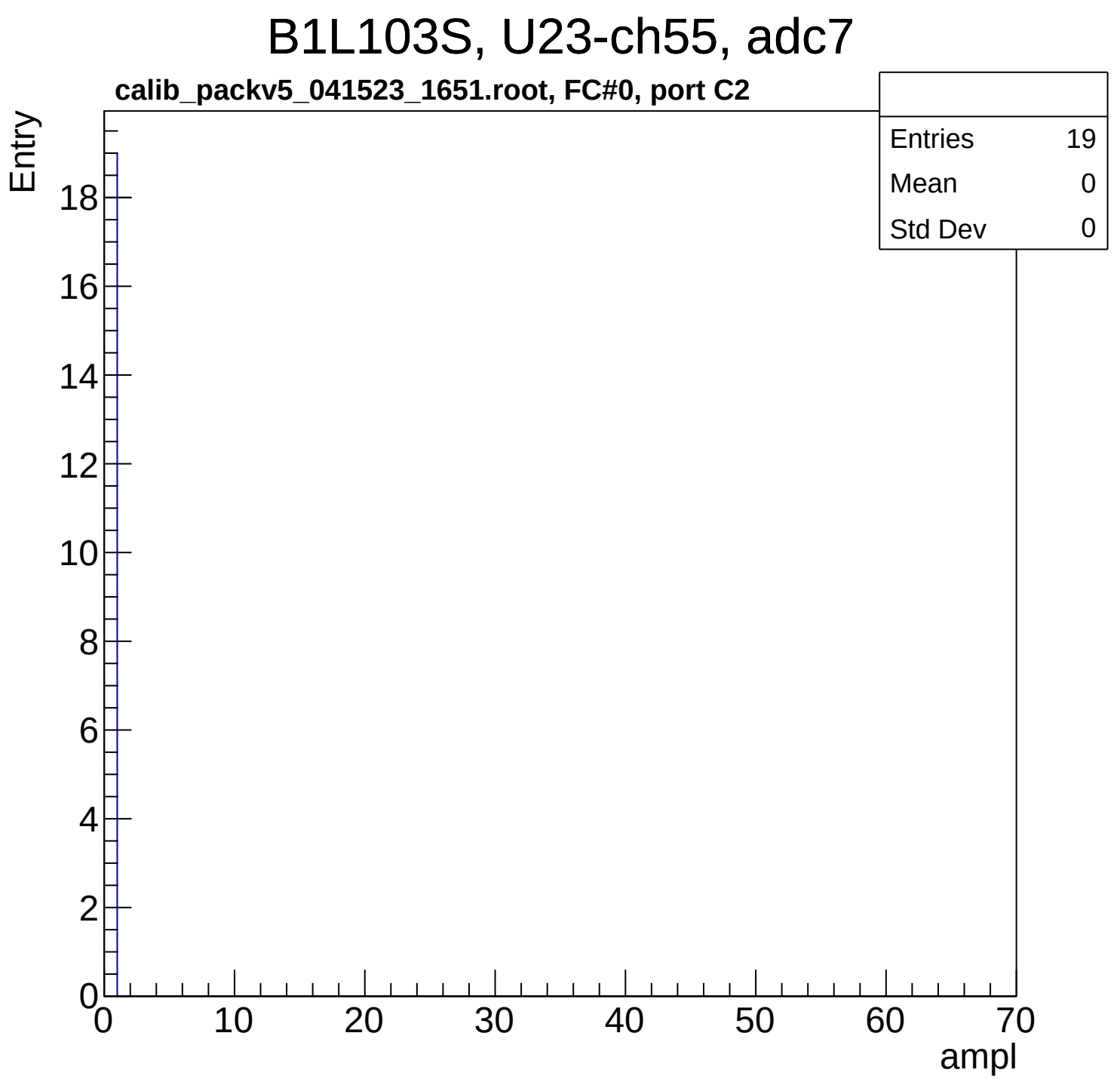

B1L103S, U23-ch55, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 37.92
Std Dev 13.58
Gaus mean : 42.3735
Gaus Width: 4.1219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

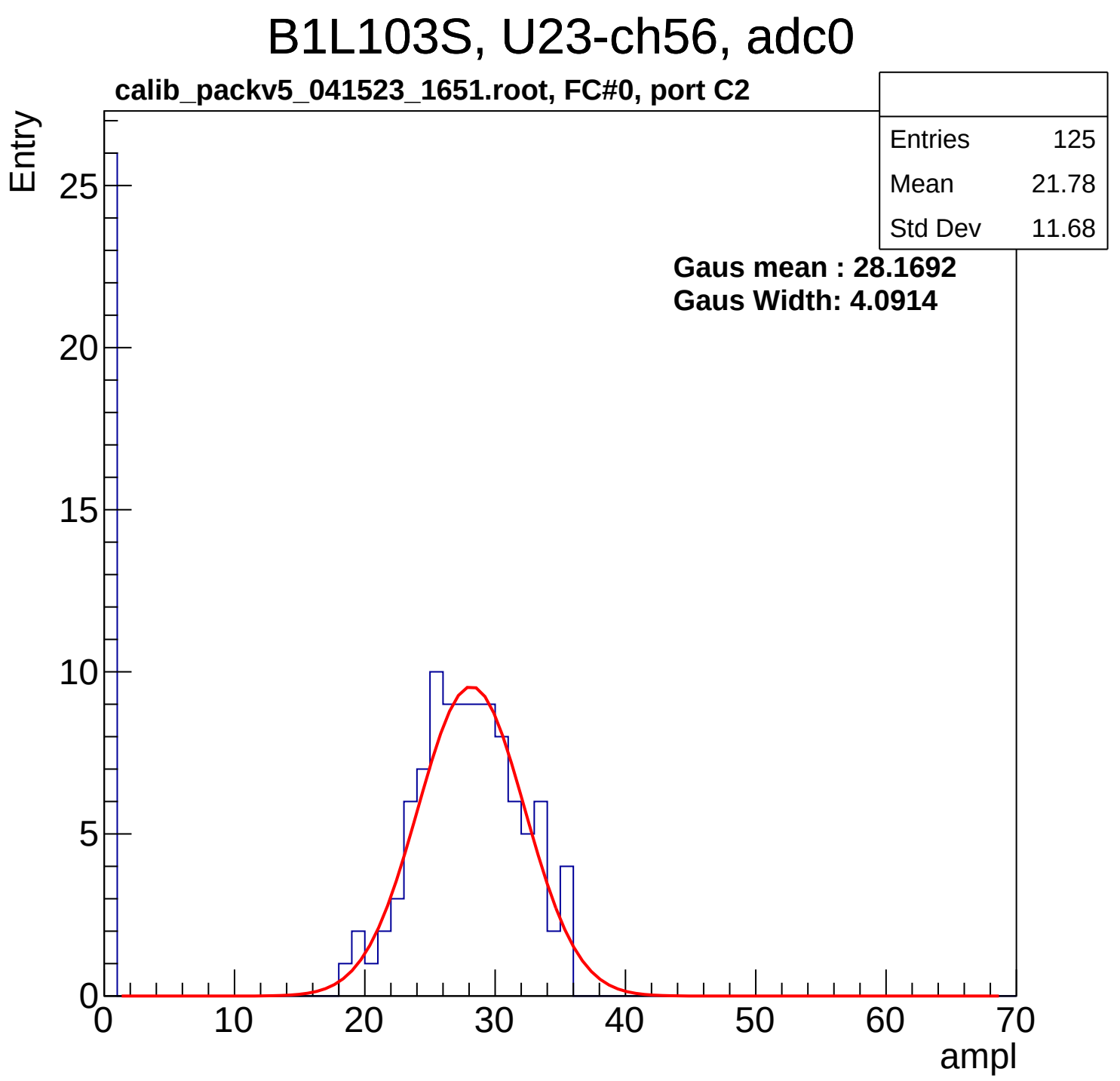

B1L103S, U23-ch55, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 43.08
Std Dev 16.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

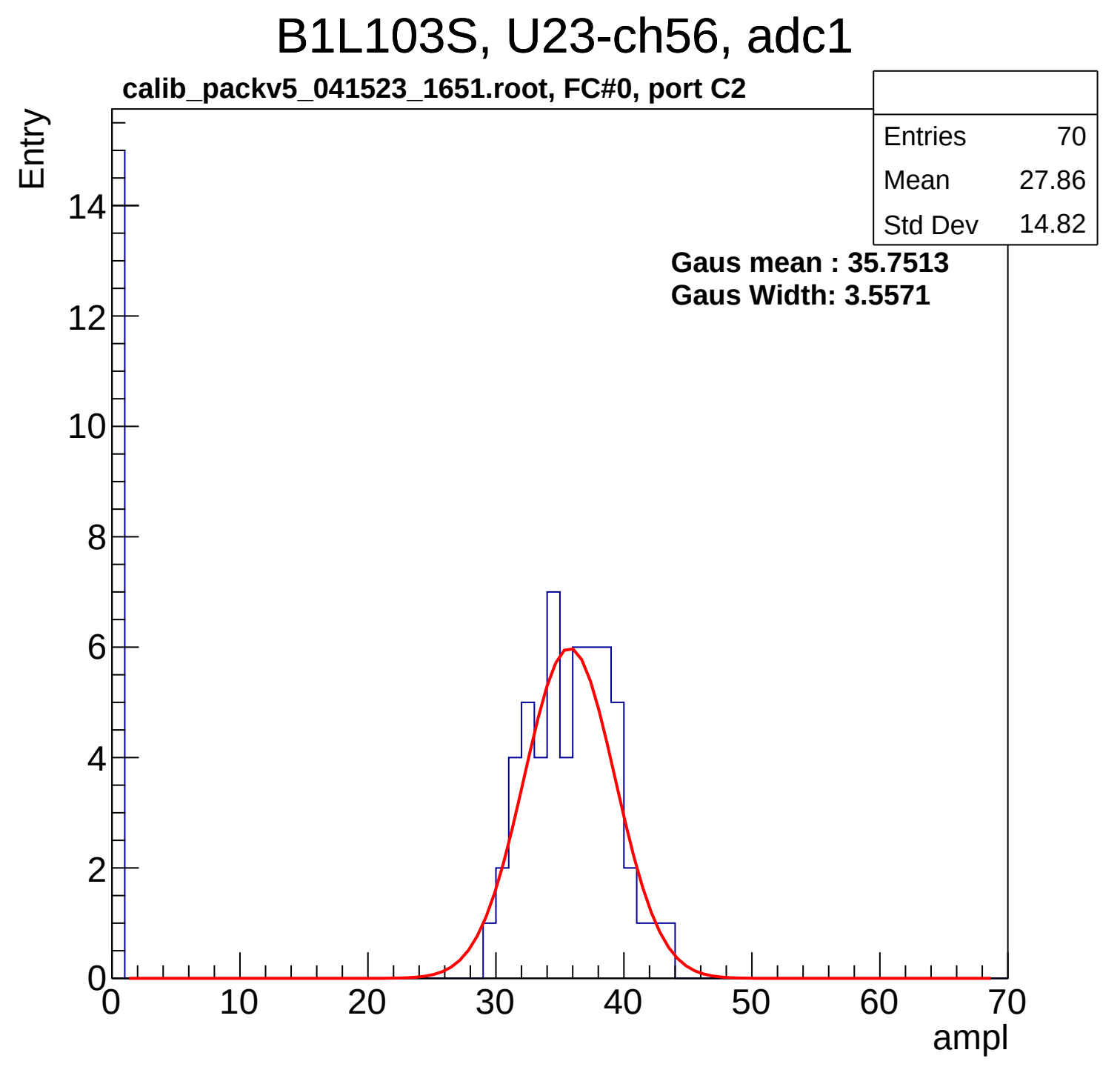

B1L103S, U23-ch55, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 55.33
Std Dev 3.186
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

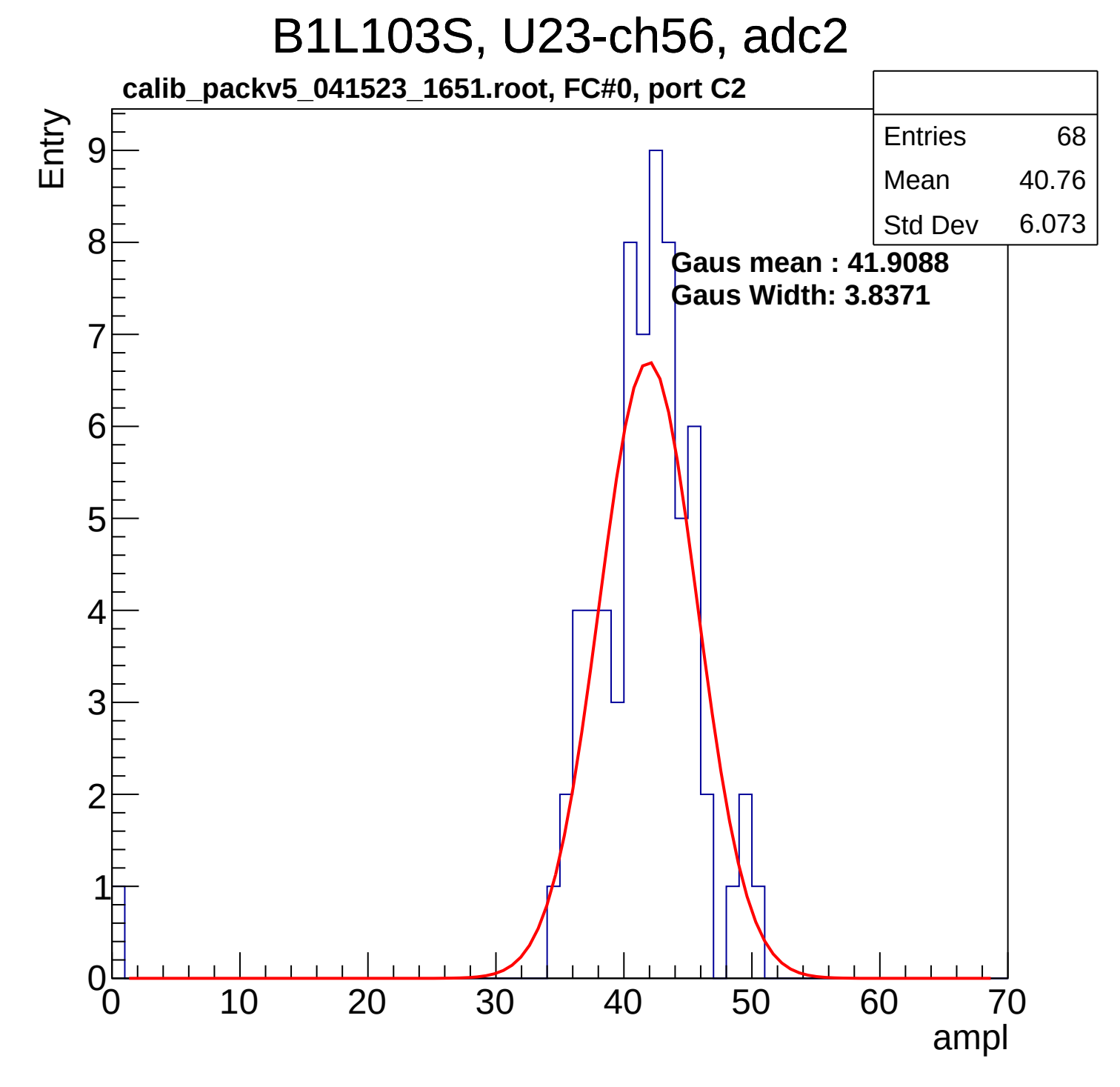

B1L103S, U23-ch55, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 58.45
Std Dev 8.584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

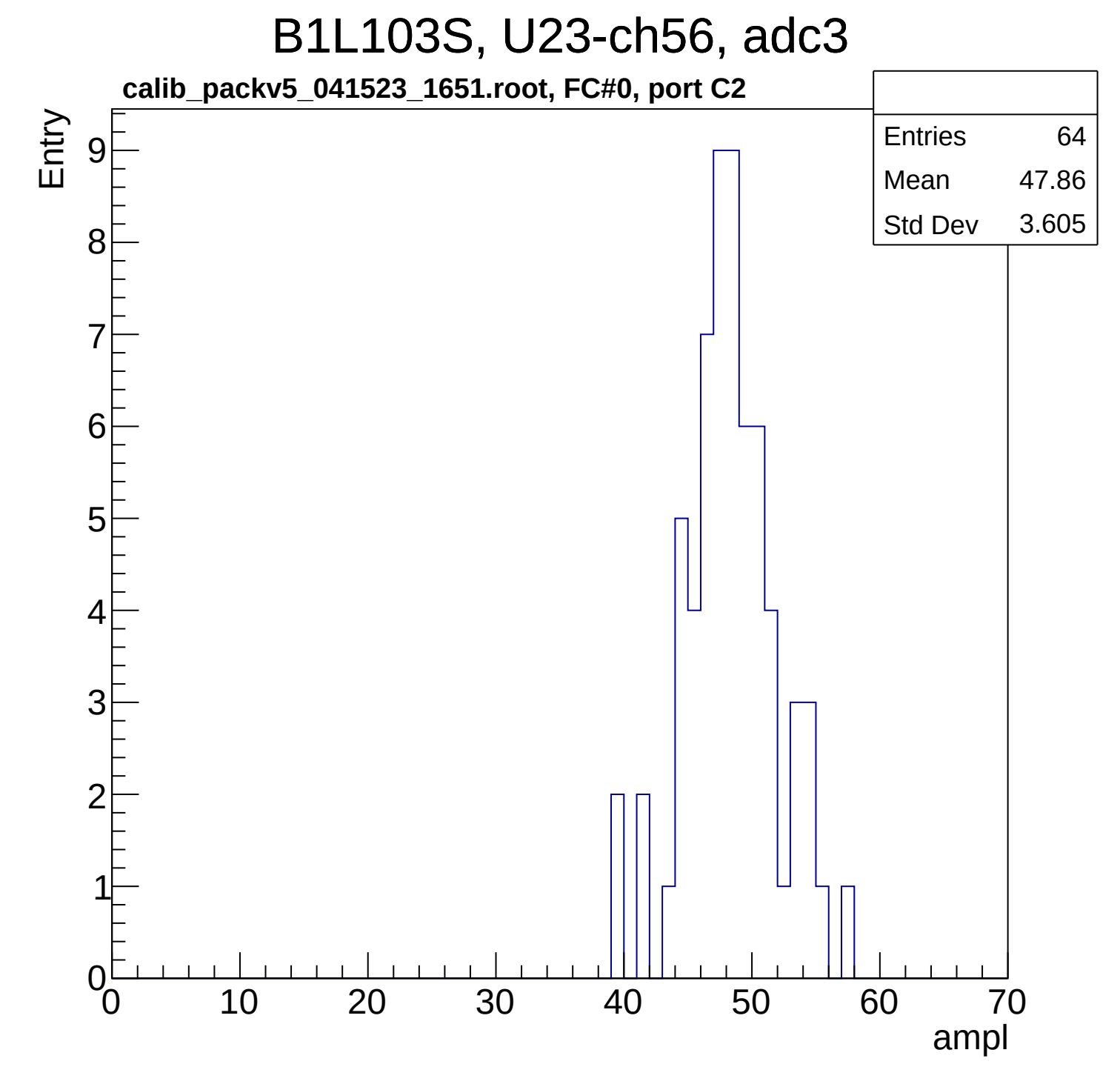

B1L103S, U23-ch55, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

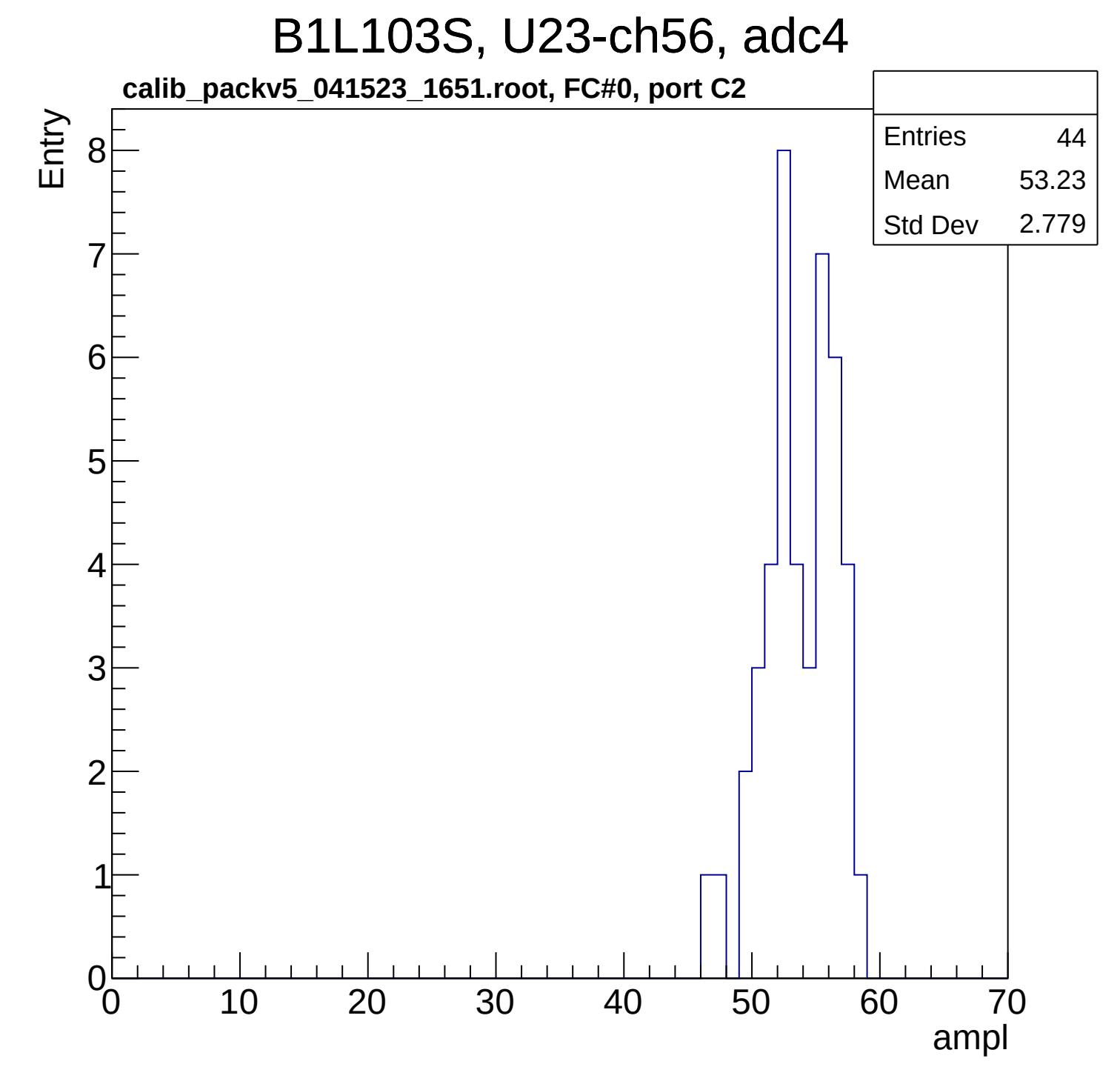

B1L103S, U23-ch55, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

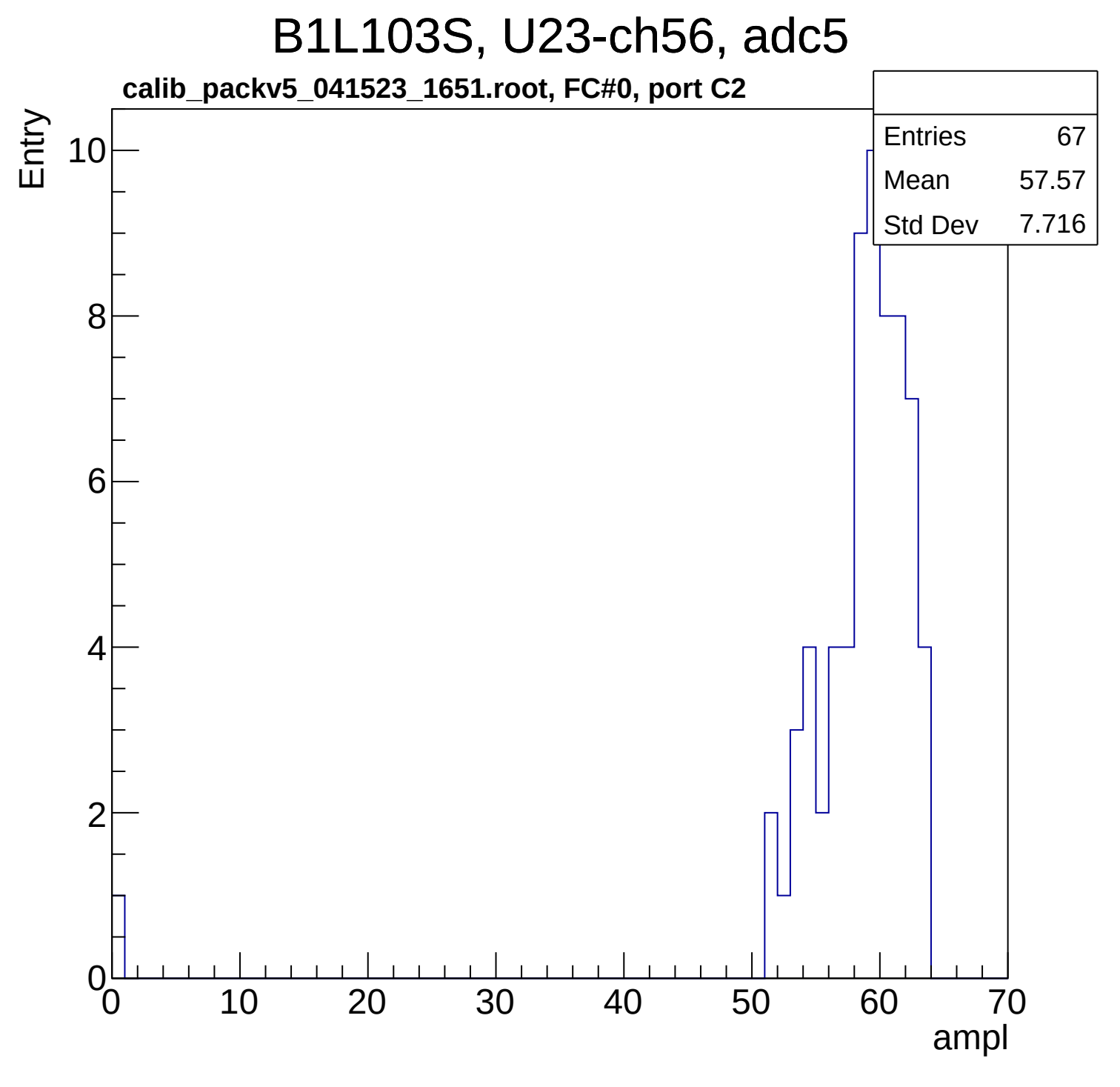

B1L103S, U23-ch56, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 125
Mean 21.78
Std Dev 11.68
Gaus mean : 28.1692
Gaus Width: 4.0914
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

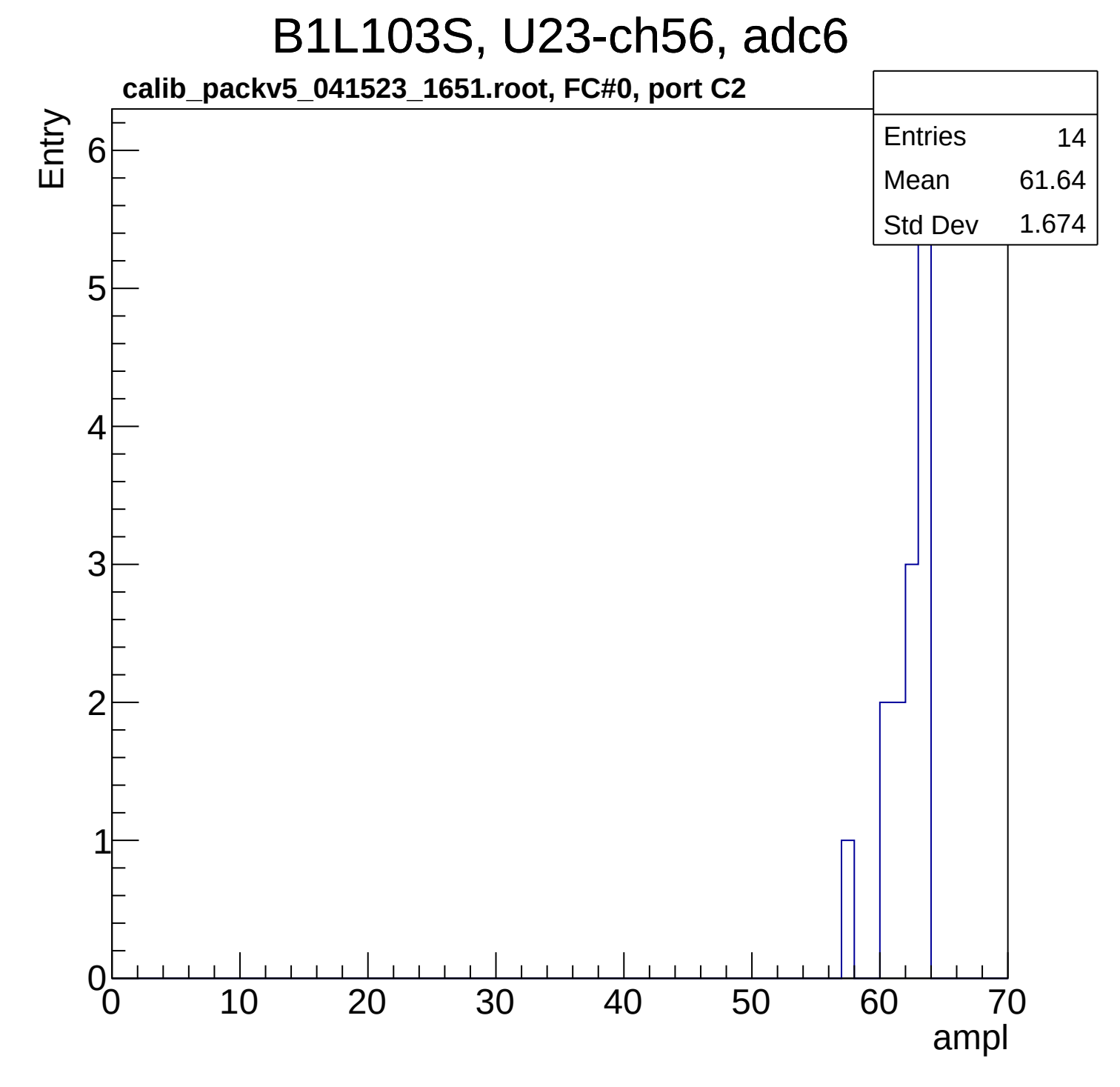

B1L103S, U23-ch56, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 27.86
Std Dev 14.82
Gaus mean : 35.7513
Gaus Width: 3.5571
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

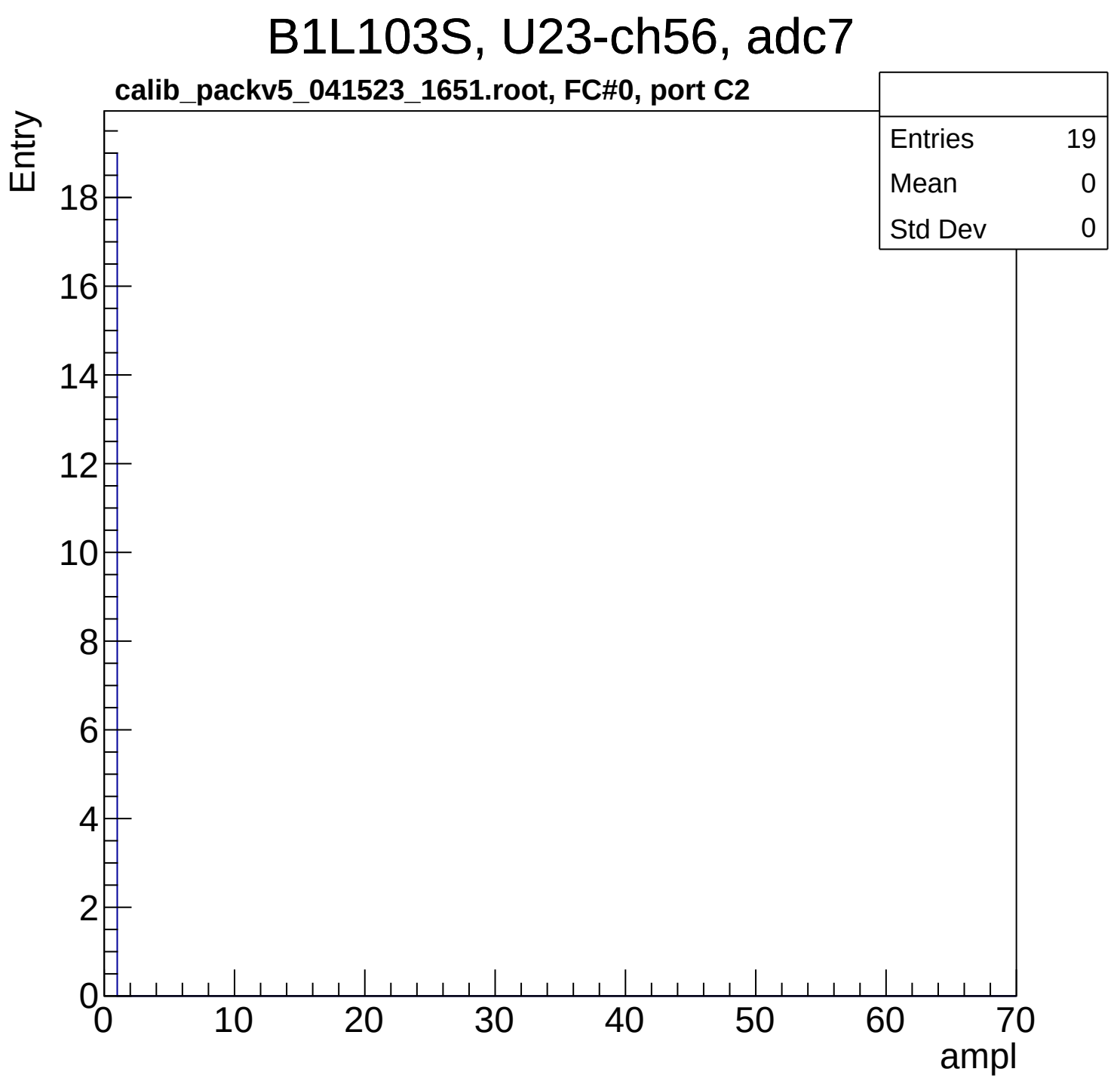

B1L103S, U23-ch56, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 40.76
Std Dev 6.073
Gaus mean : 41.9088
Gaus Width: 3.8371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

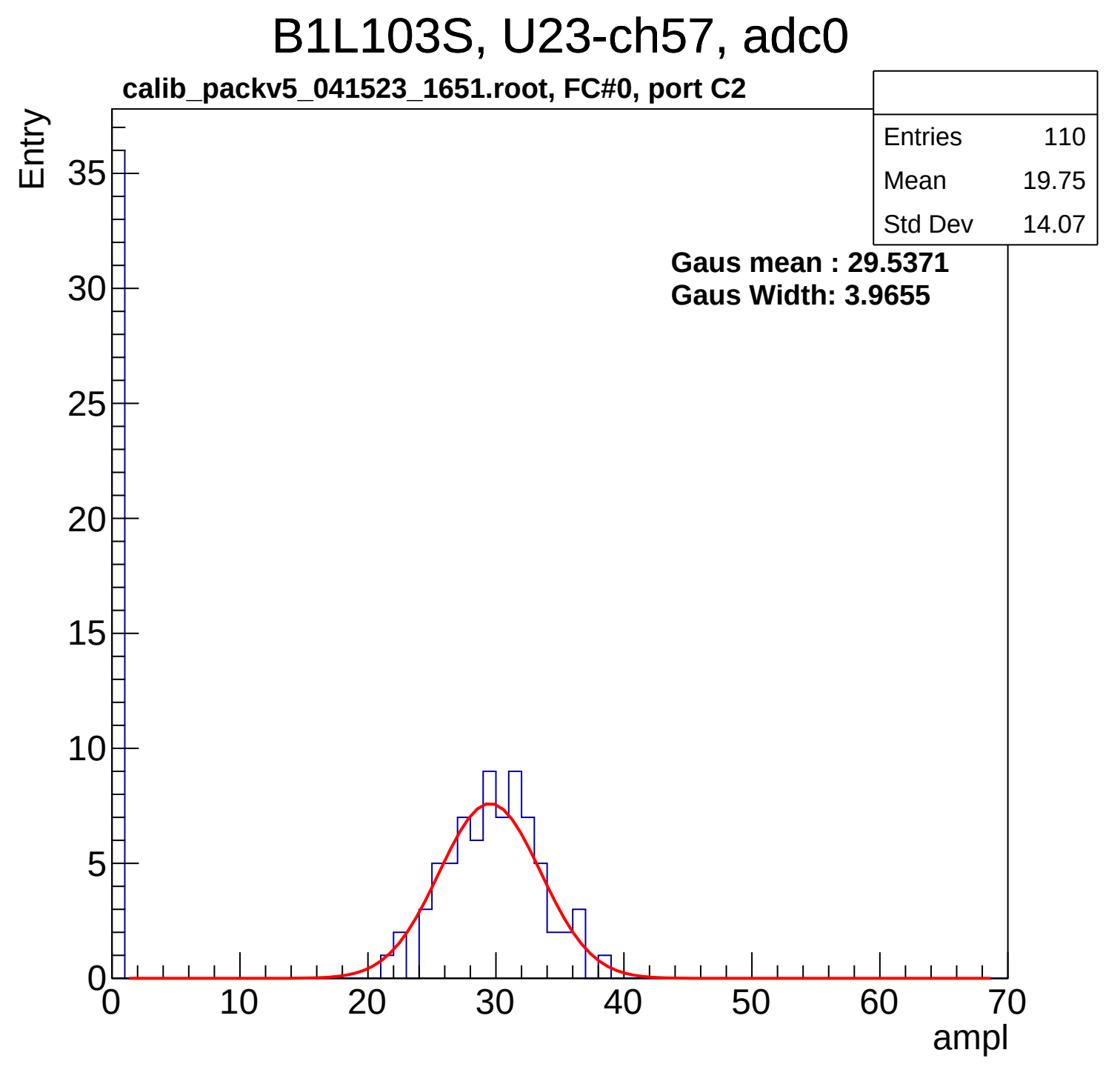

B1L103S, U23-ch56, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 47.86
Std Dev 3.605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

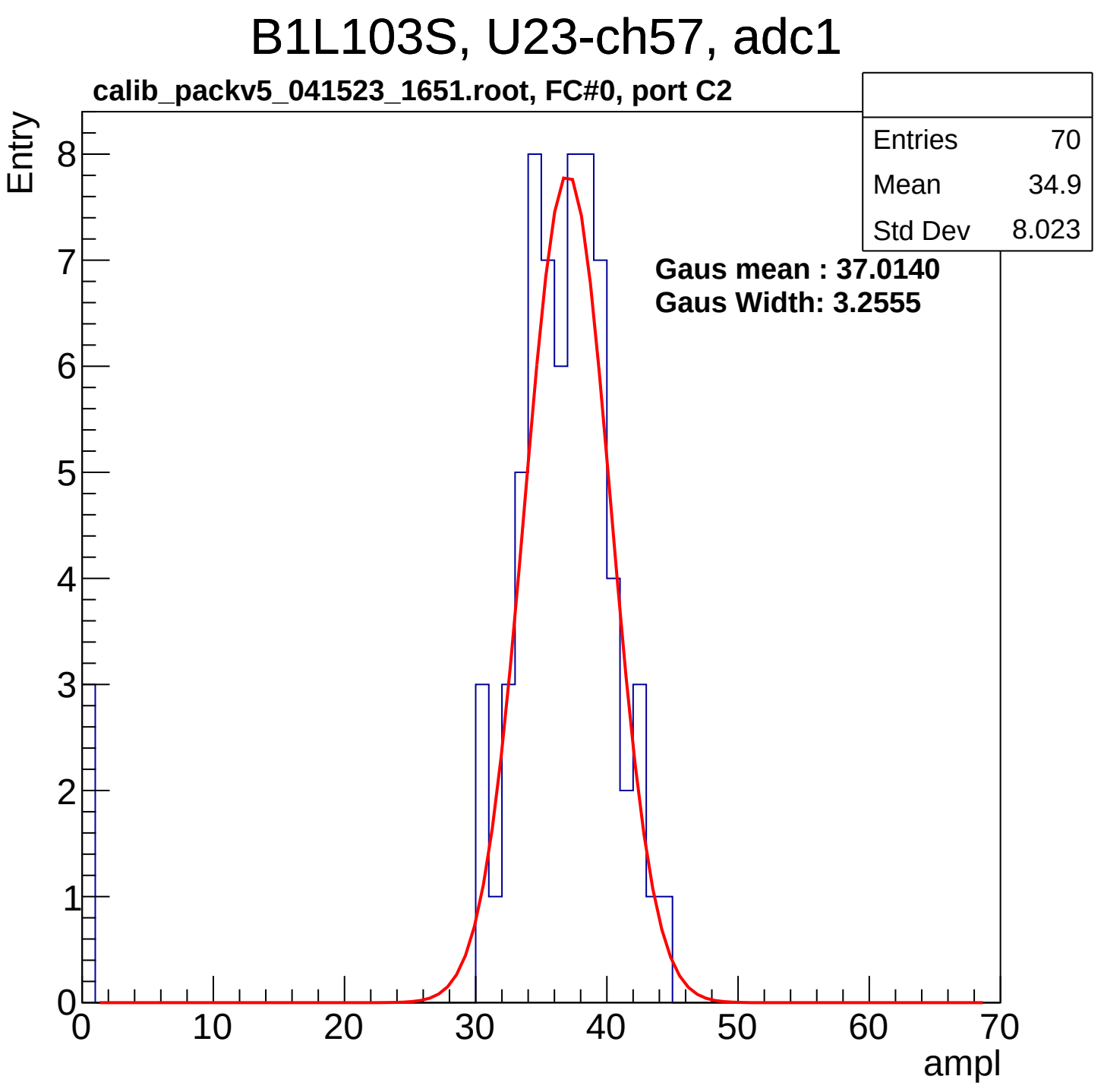

B1L103S, U23-ch56, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 44
Mean 53.23
Std Dev 2.779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

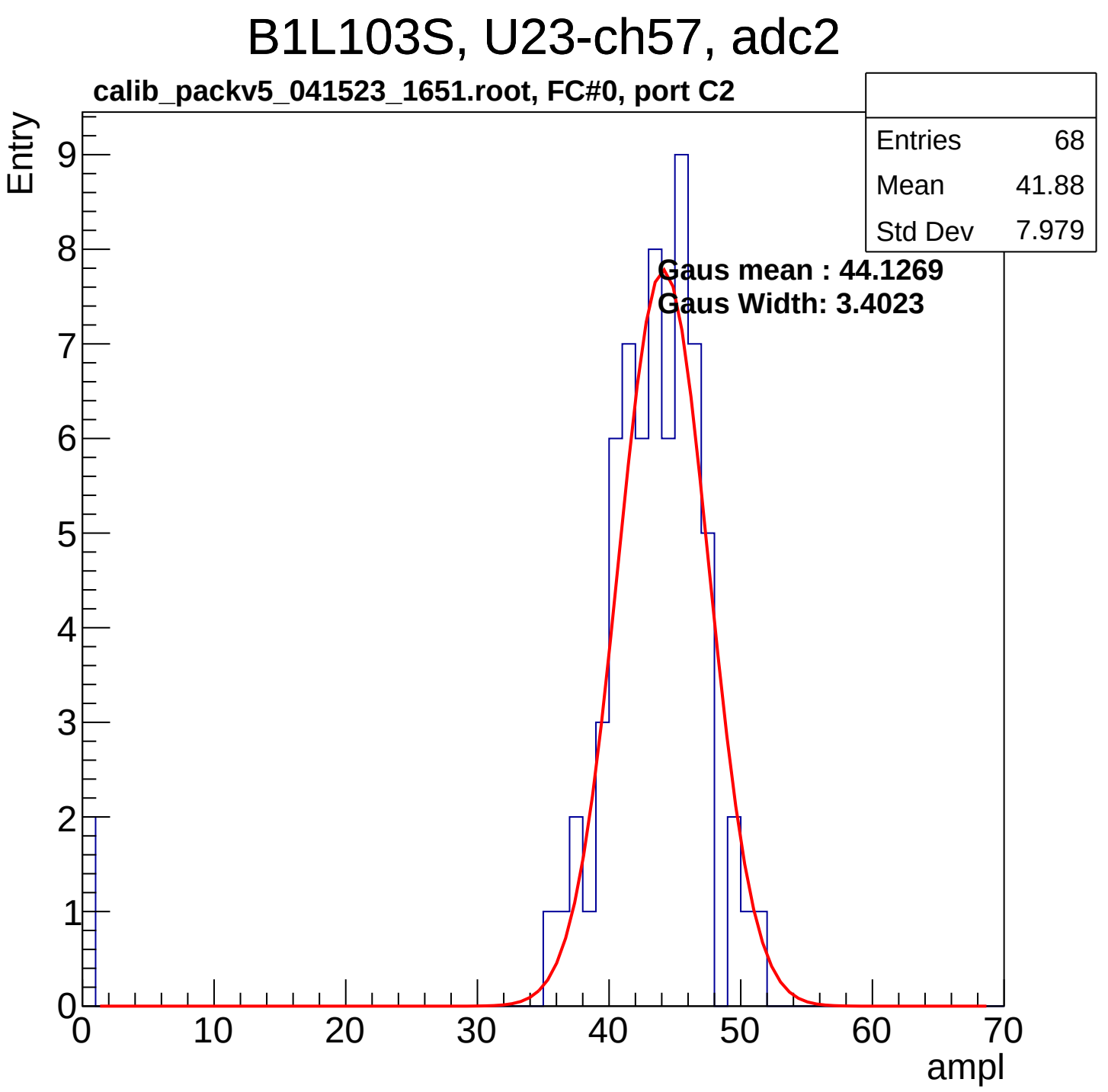

B1L103S, U23-ch56, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 57.57
Std Dev 7.716
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

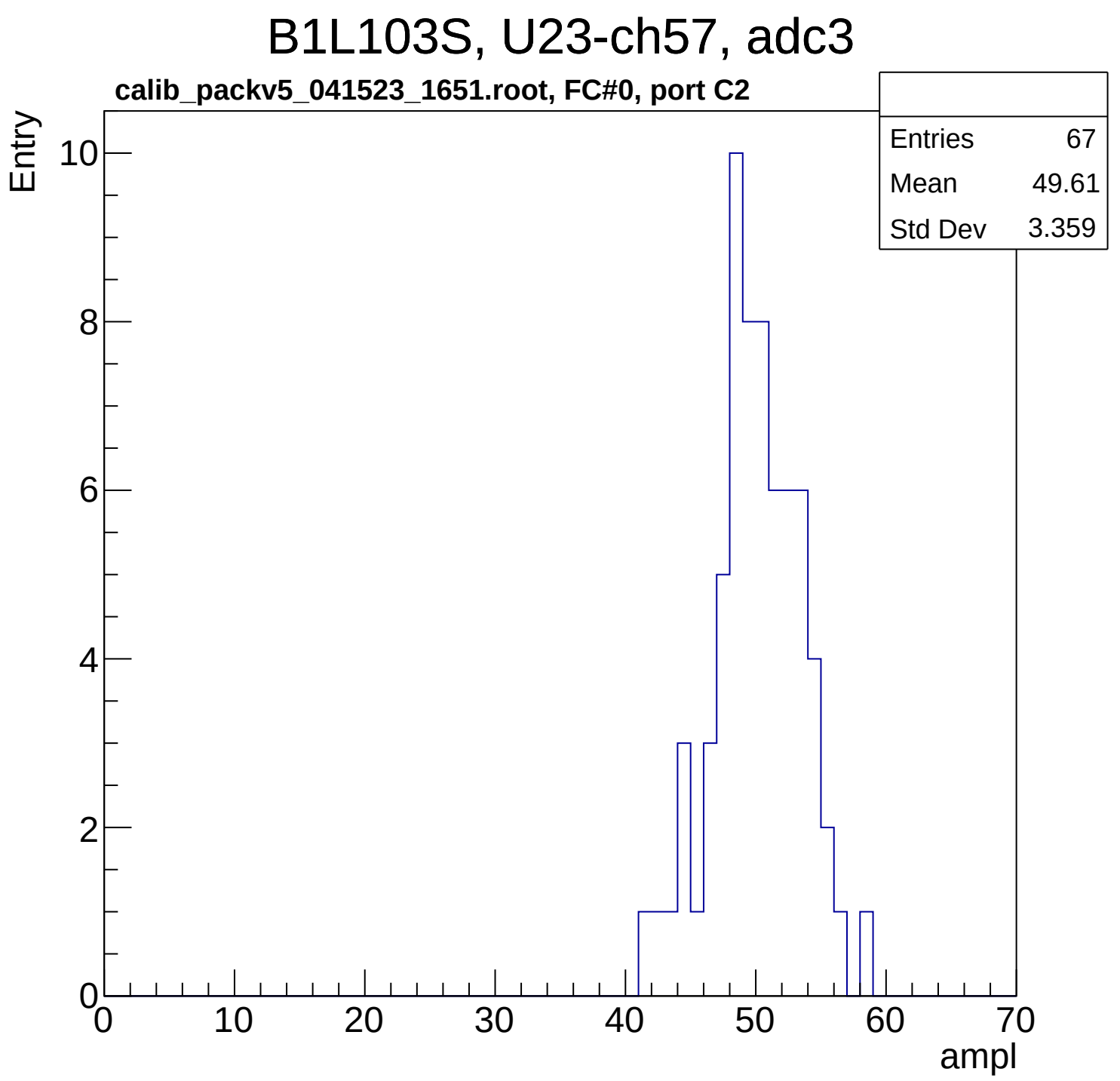

B1L103S, U23-ch56, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 14
Mean 61.64
Std Dev 1.674
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

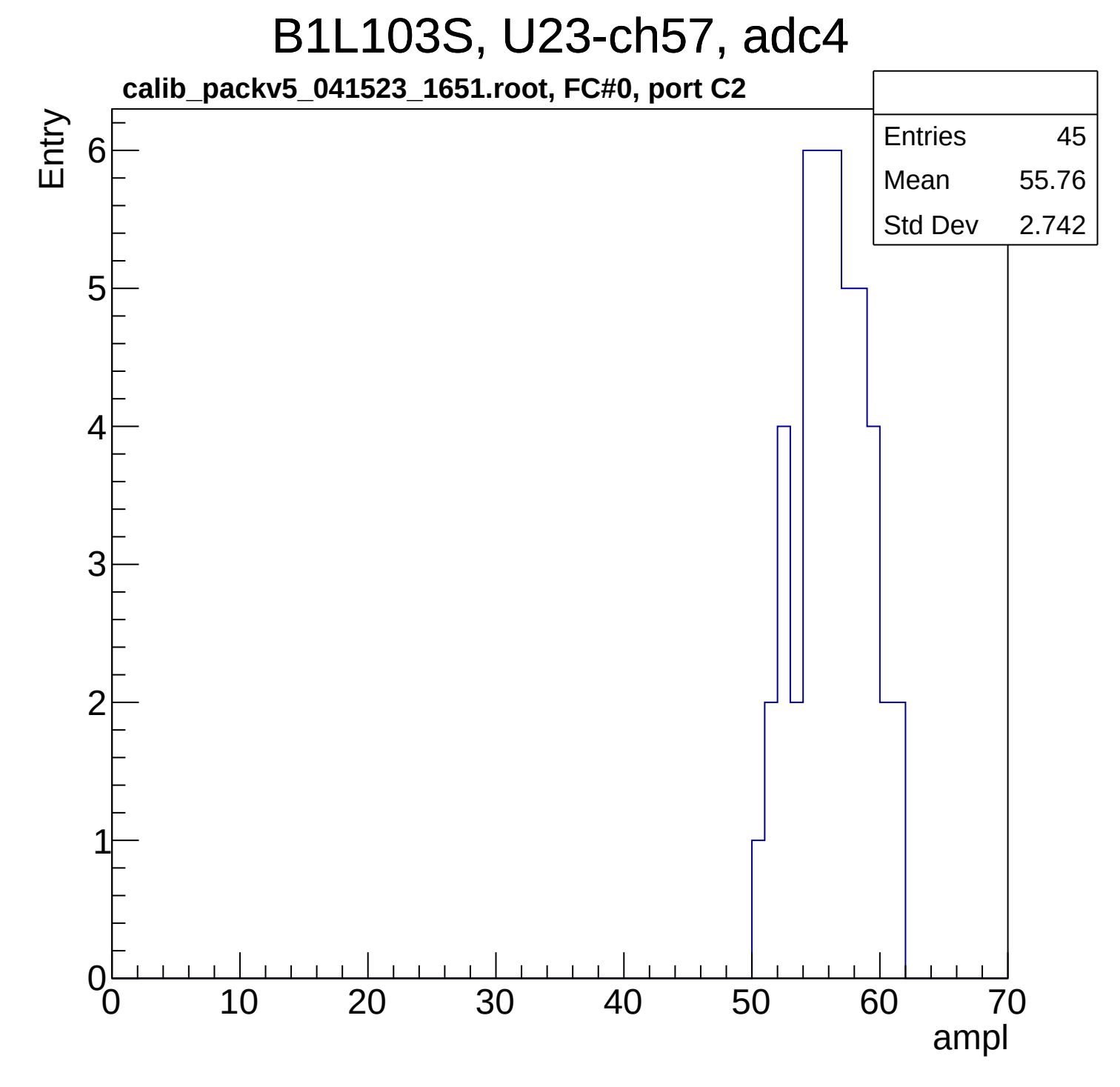

B1L103S, U23-ch56, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

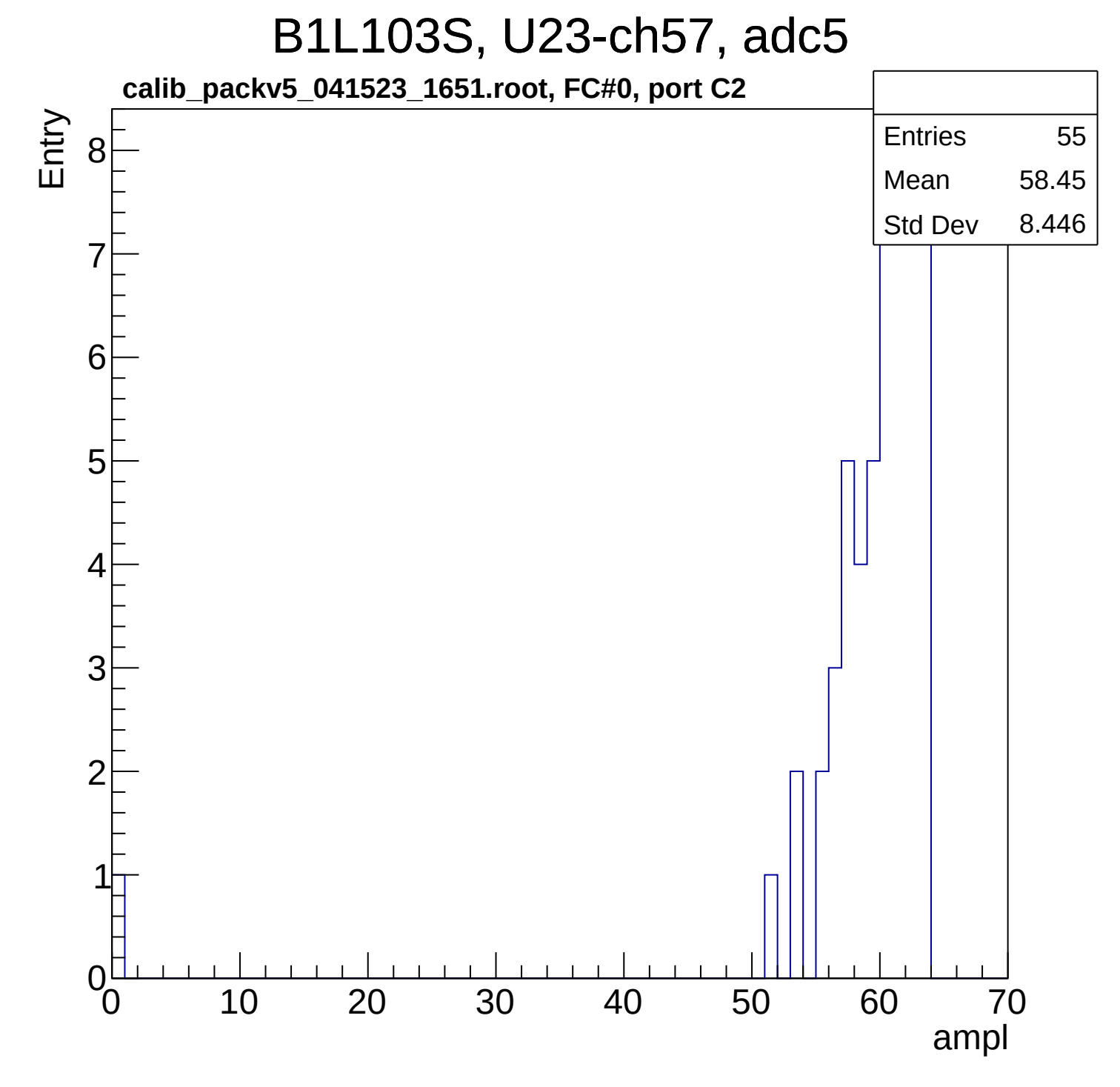

B1L103S, U23-ch57, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 110
Mean 19.75
Std Dev 14.07
Gaus mean : 29.5371
Gaus Width: 3.9655
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

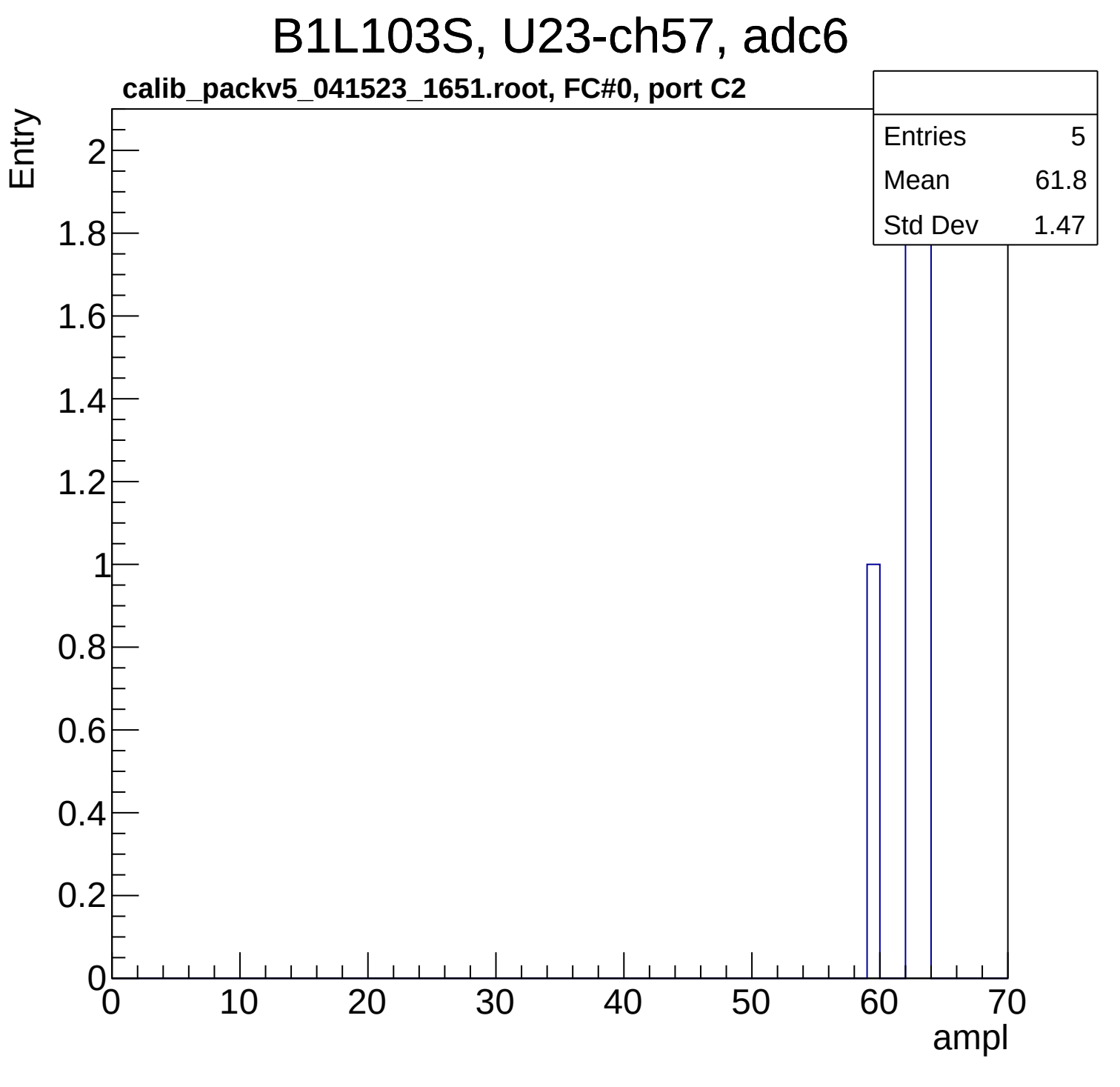

B1L103S, U23-ch57, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 34.9
Std Dev 8.023
Gaus mean : 37.0140
Gaus Width: 3.2555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

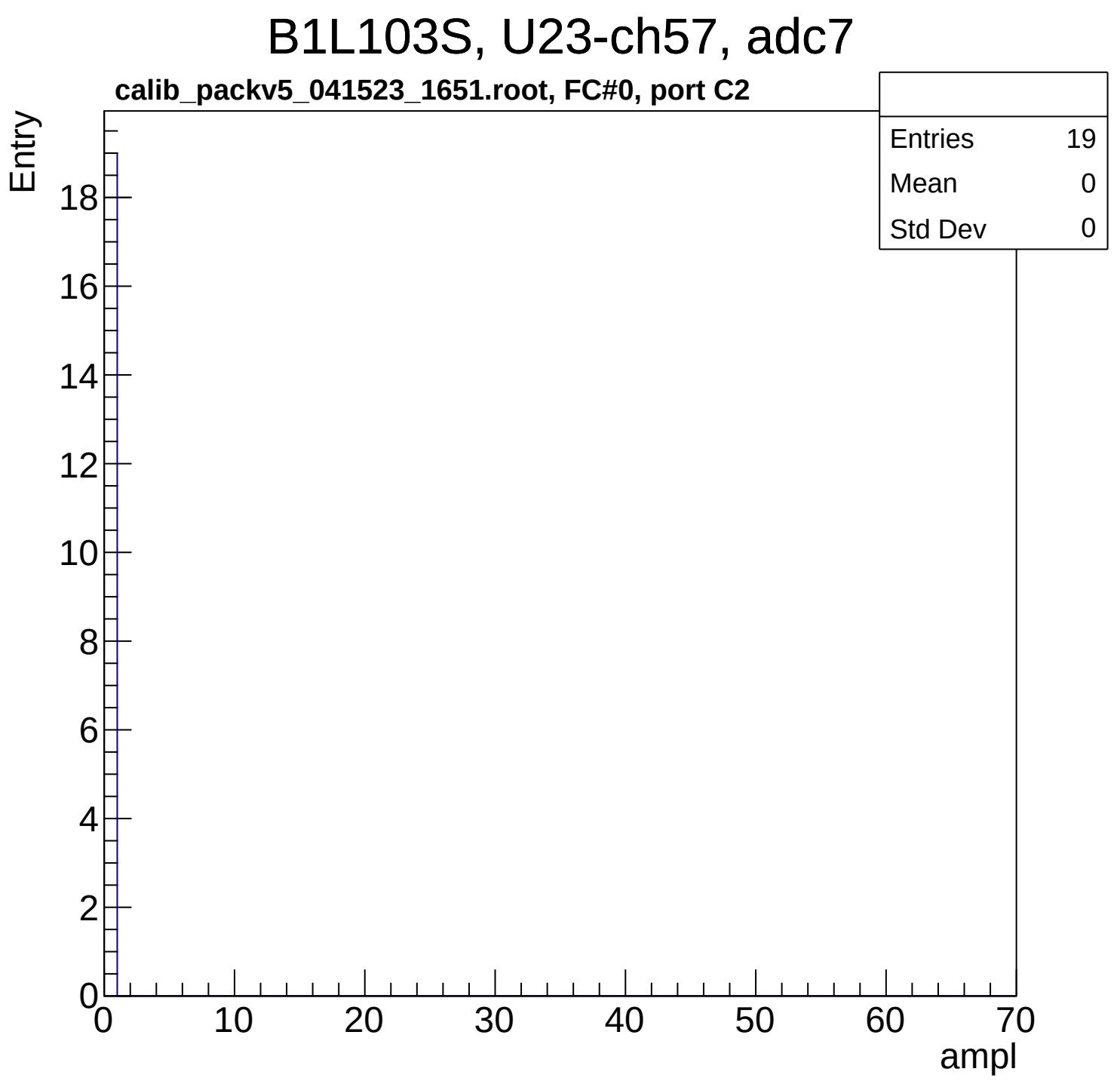

B1L103S, U23-ch57, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 41.88
Std Dev 7.979
Gaus mean : 44.1269
Gaus Width: 3.4023
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

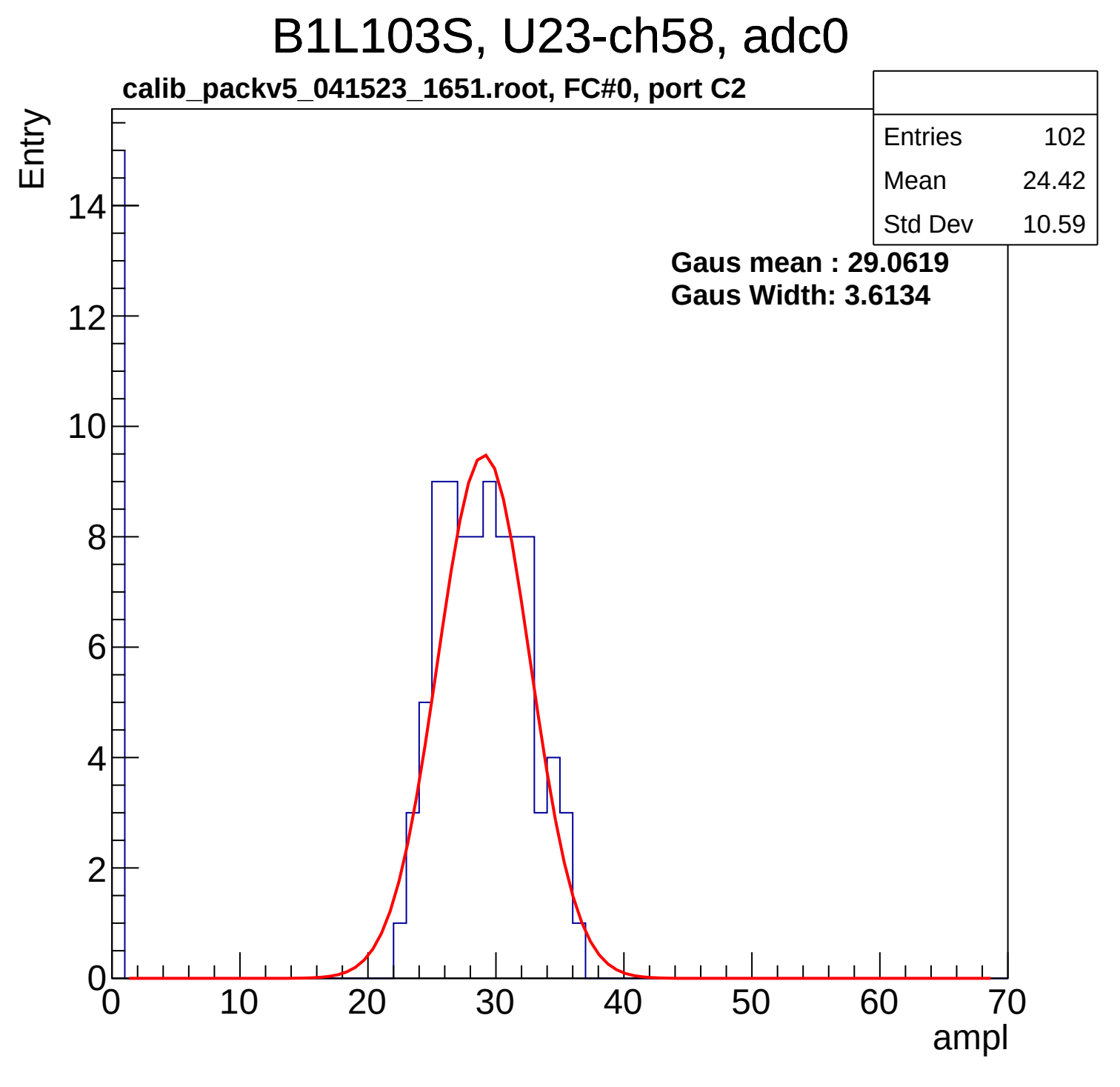

B1L103S, U23-ch57, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 49.61
Std Dev 3.359
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

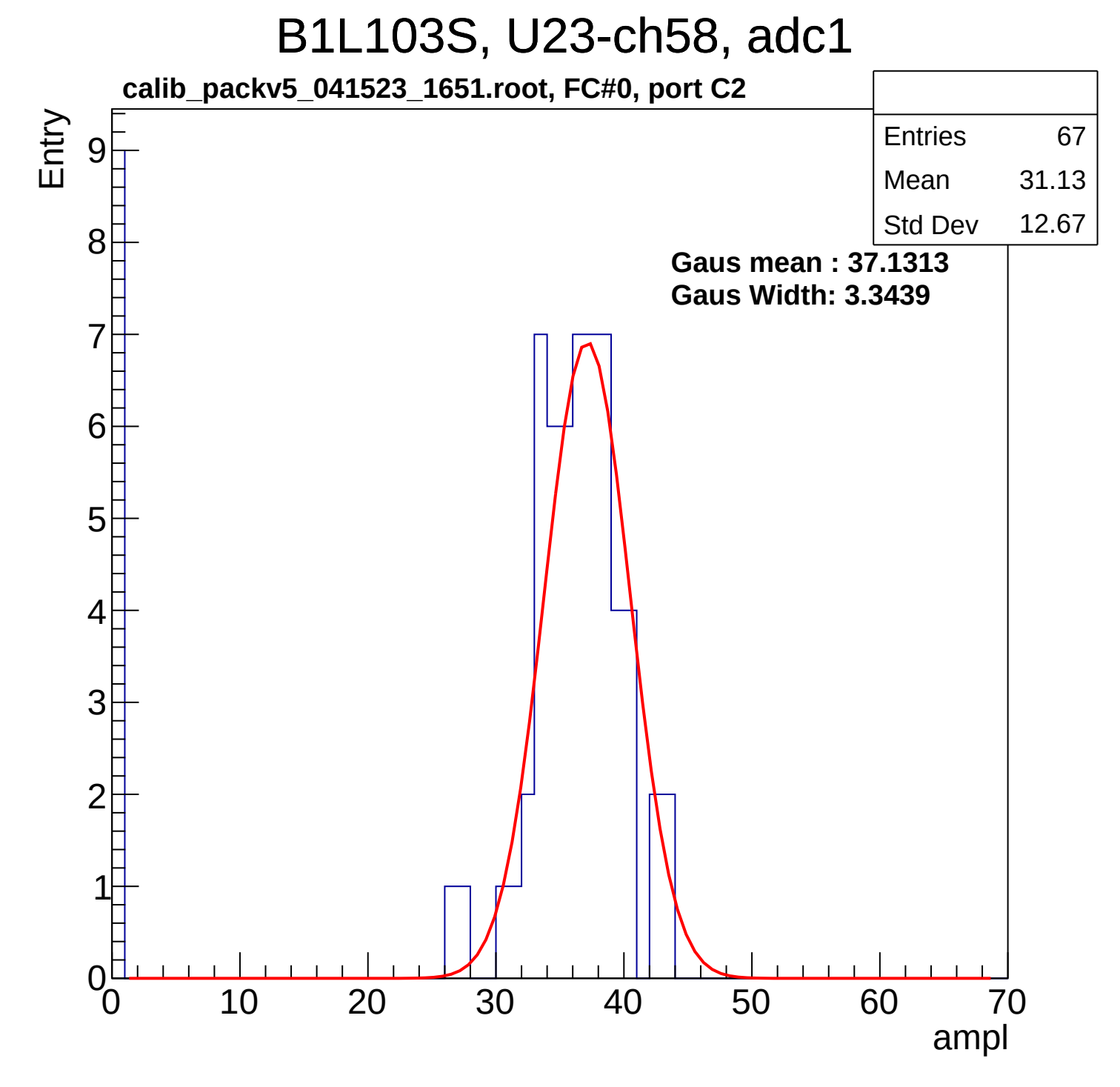

B1L103S, U23-ch57, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 45
Mean 55.76
Std Dev 2.742
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

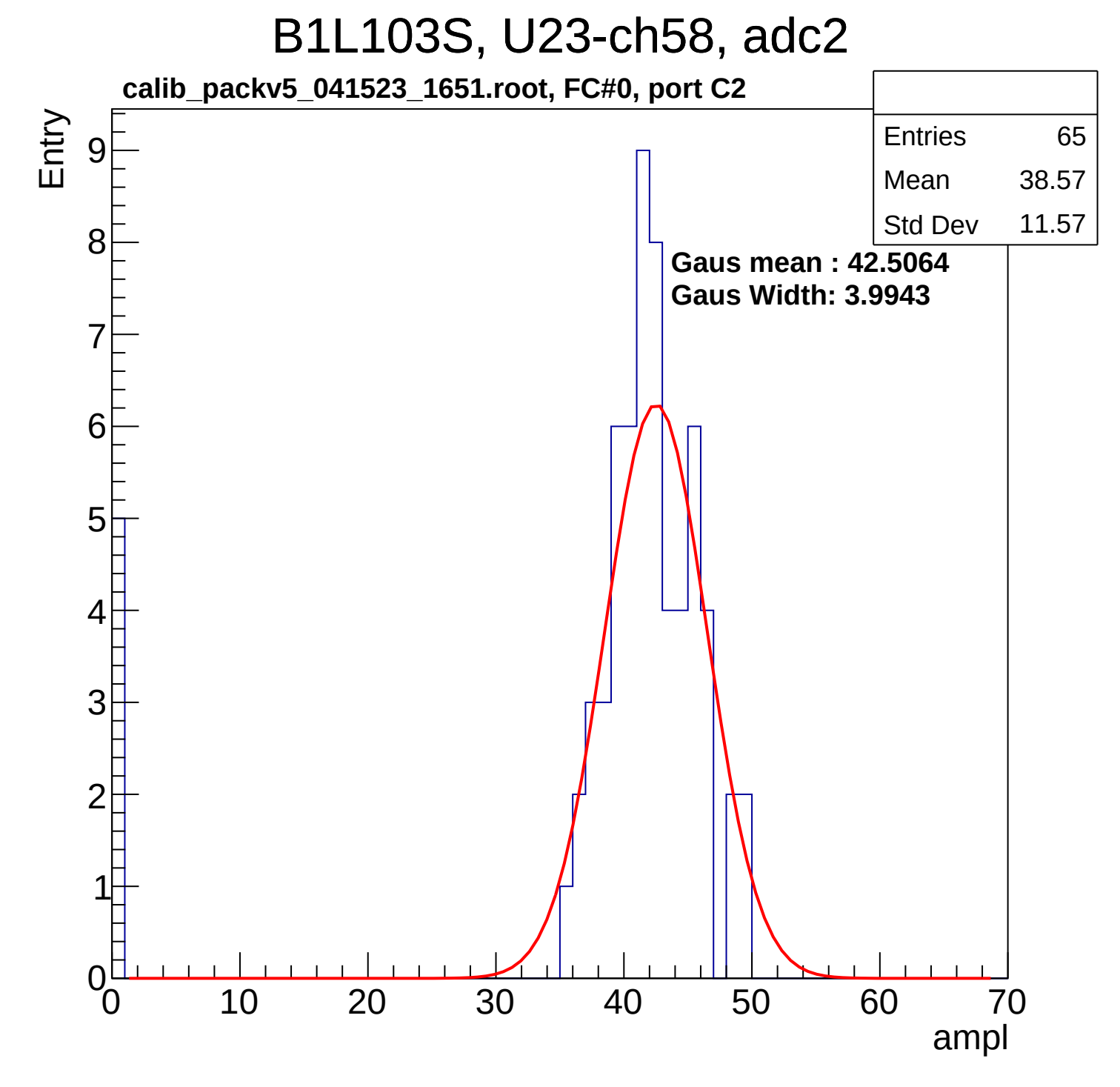

B1L103S, U23-ch57, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 55
Mean 58.45
Std Dev 8.446
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

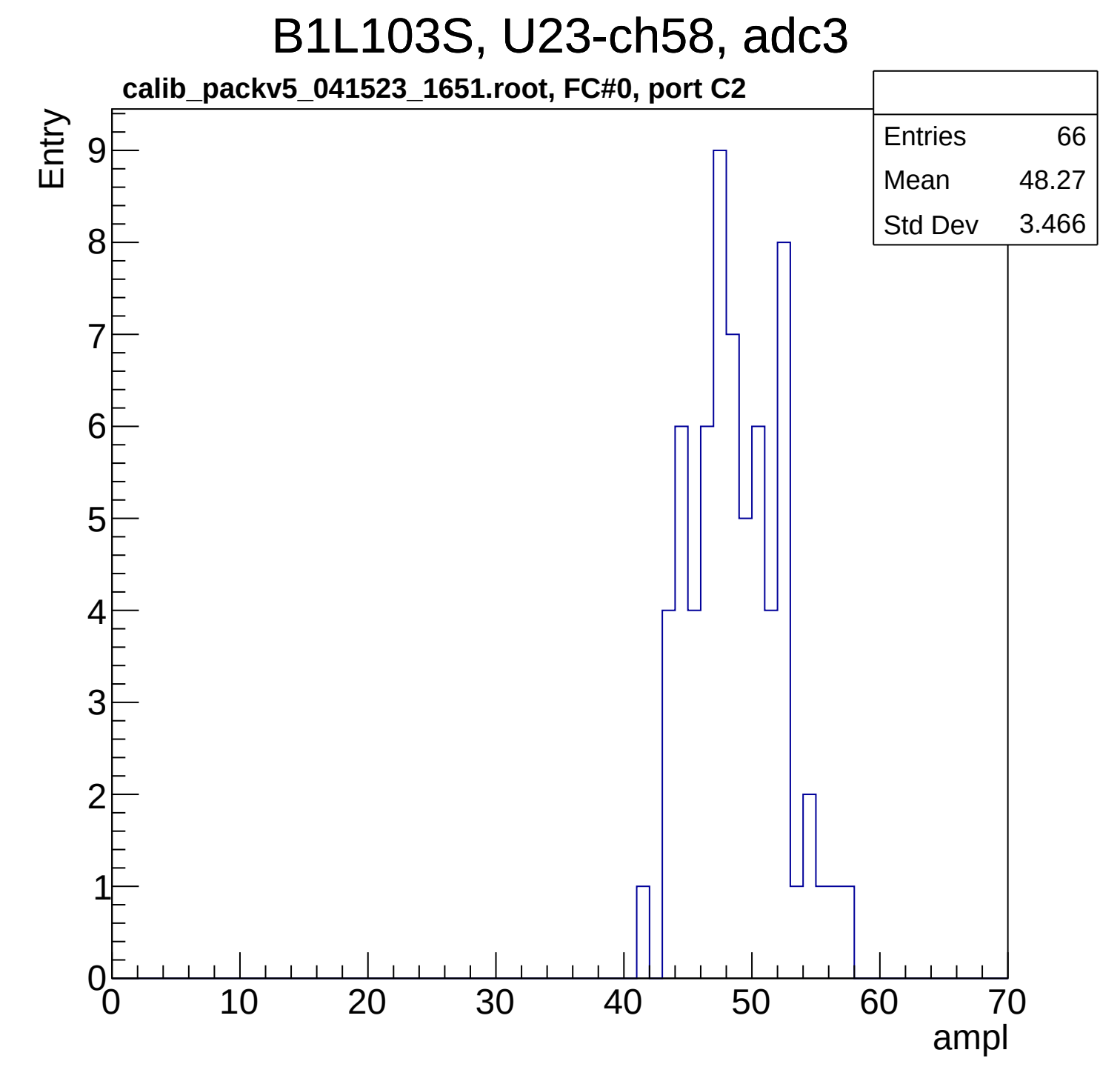

B1L103S, U23-ch57, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 61.8
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

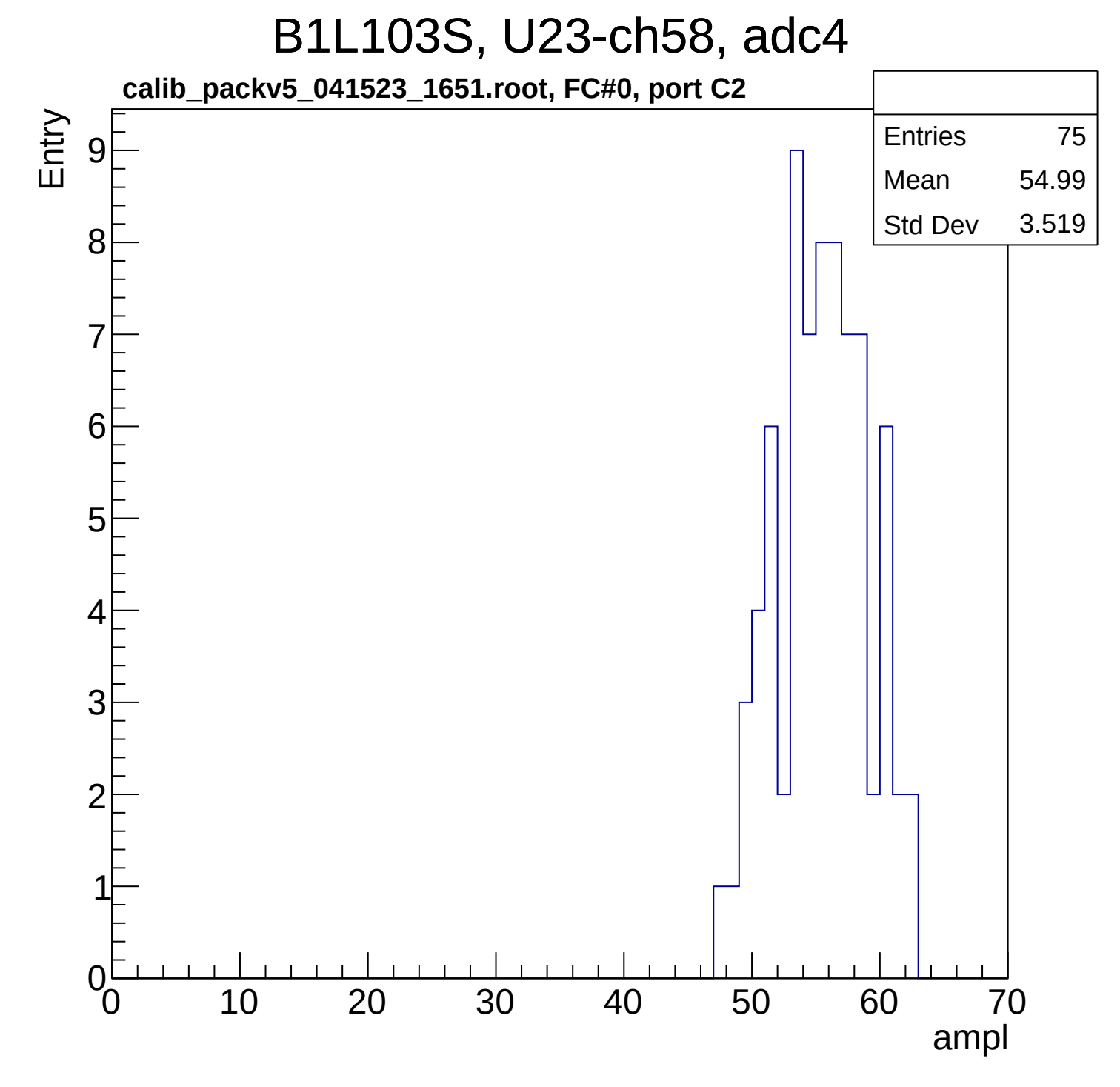

B1L103S, U23-ch57, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

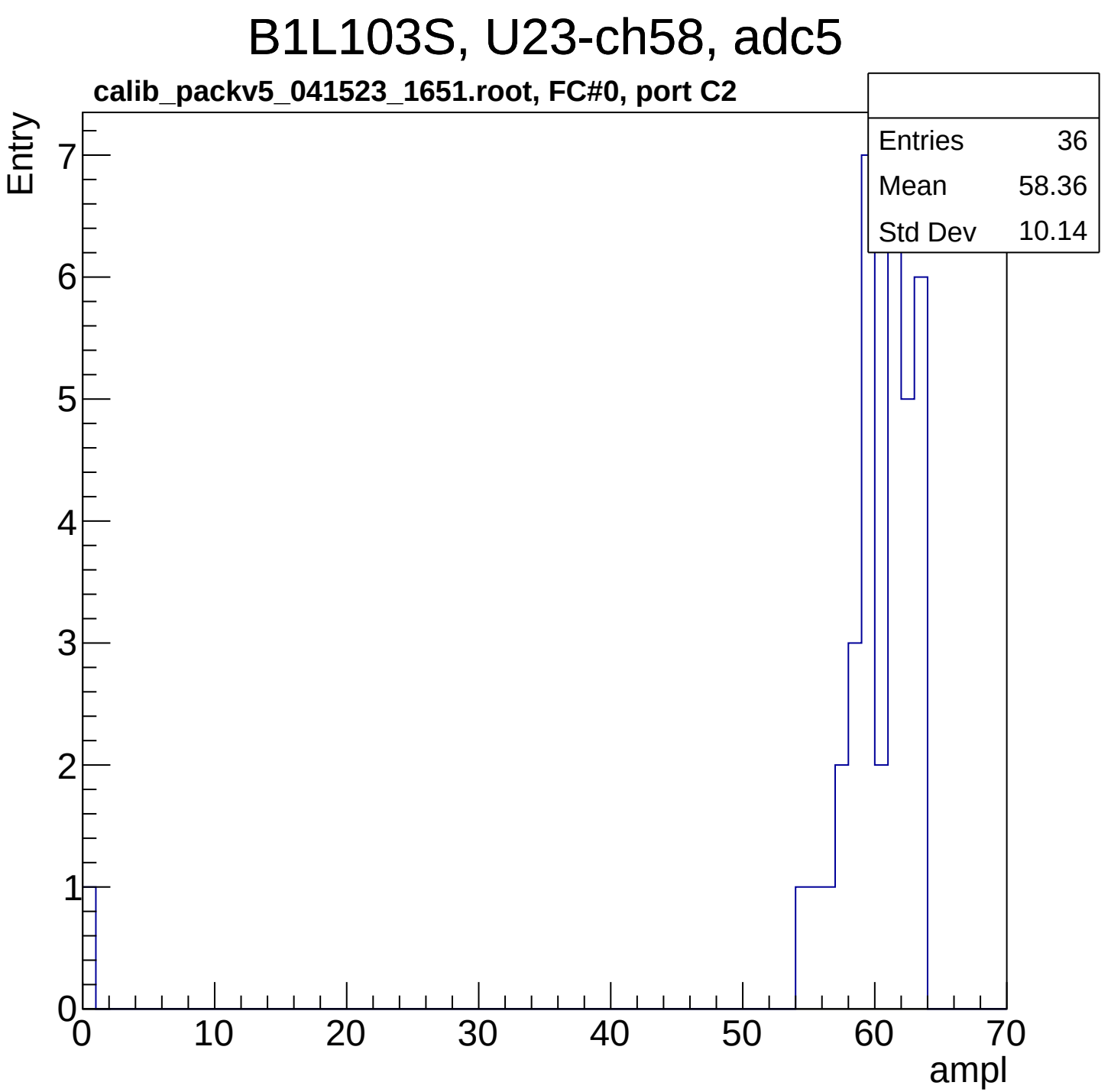

B1L103S, U23-ch58, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 102
Mean 24.42
Std Dev 10.59
Gaus mean : 29.0619
Gaus Width: 3.6134
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

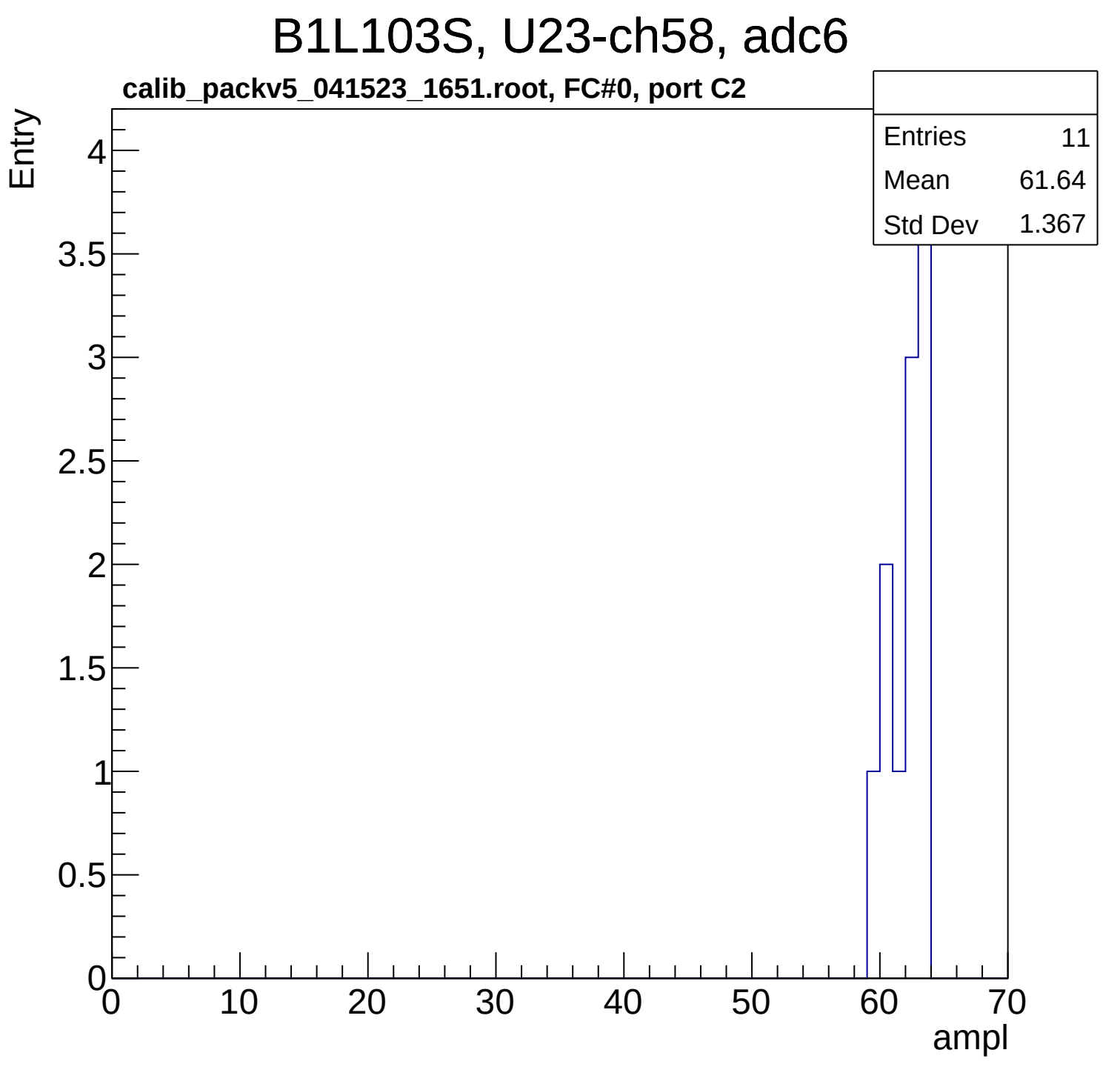

B1L103S, U23-ch58, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 31.13
Std Dev 12.67
Gaus mean : 37.1313
Gaus Width: 3.3439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

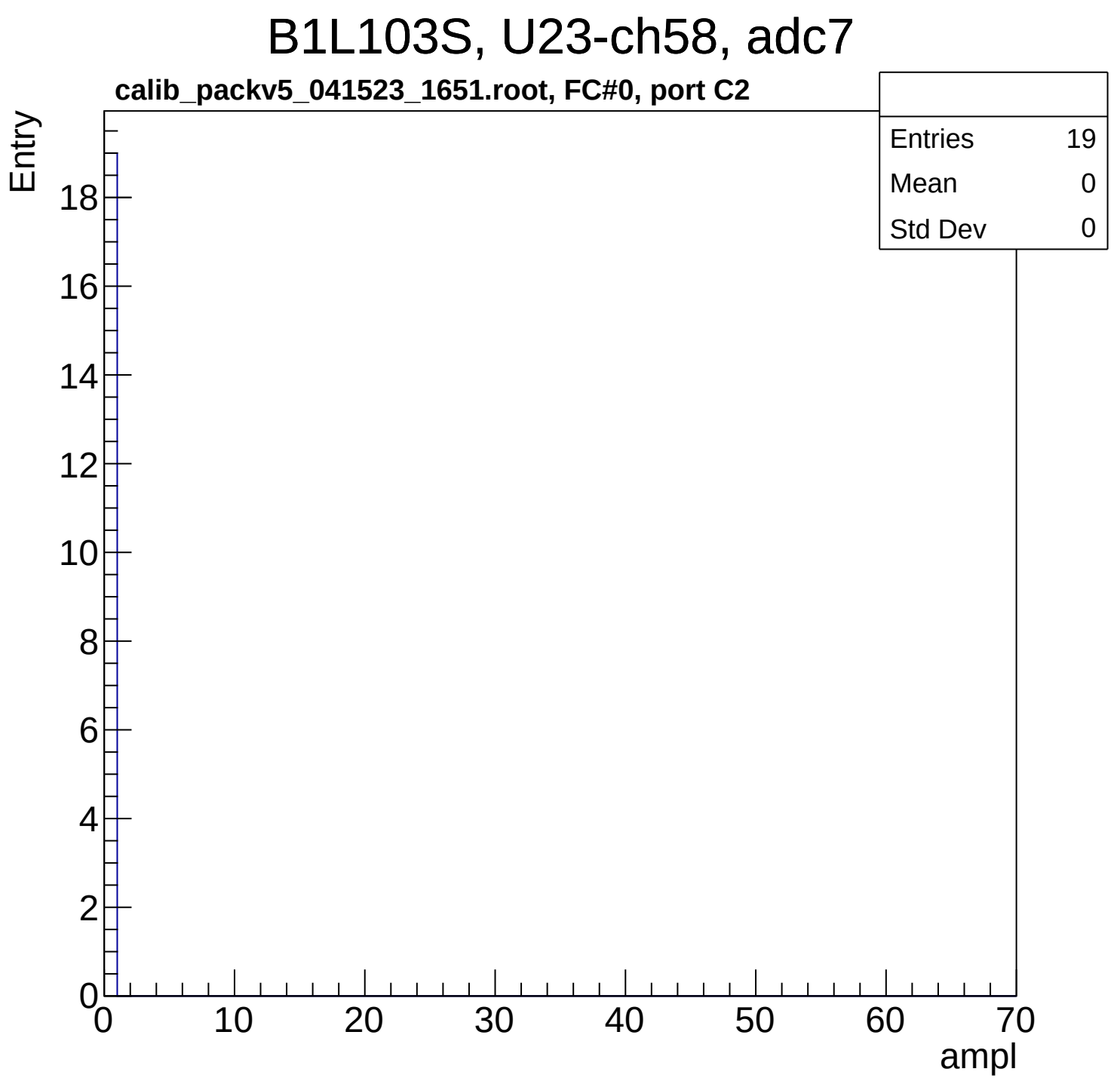

B1L103S, U23-ch58, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 38.57
Std Dev 11.57
Gaus mean : 42.5064
Gaus Width: 3.9943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

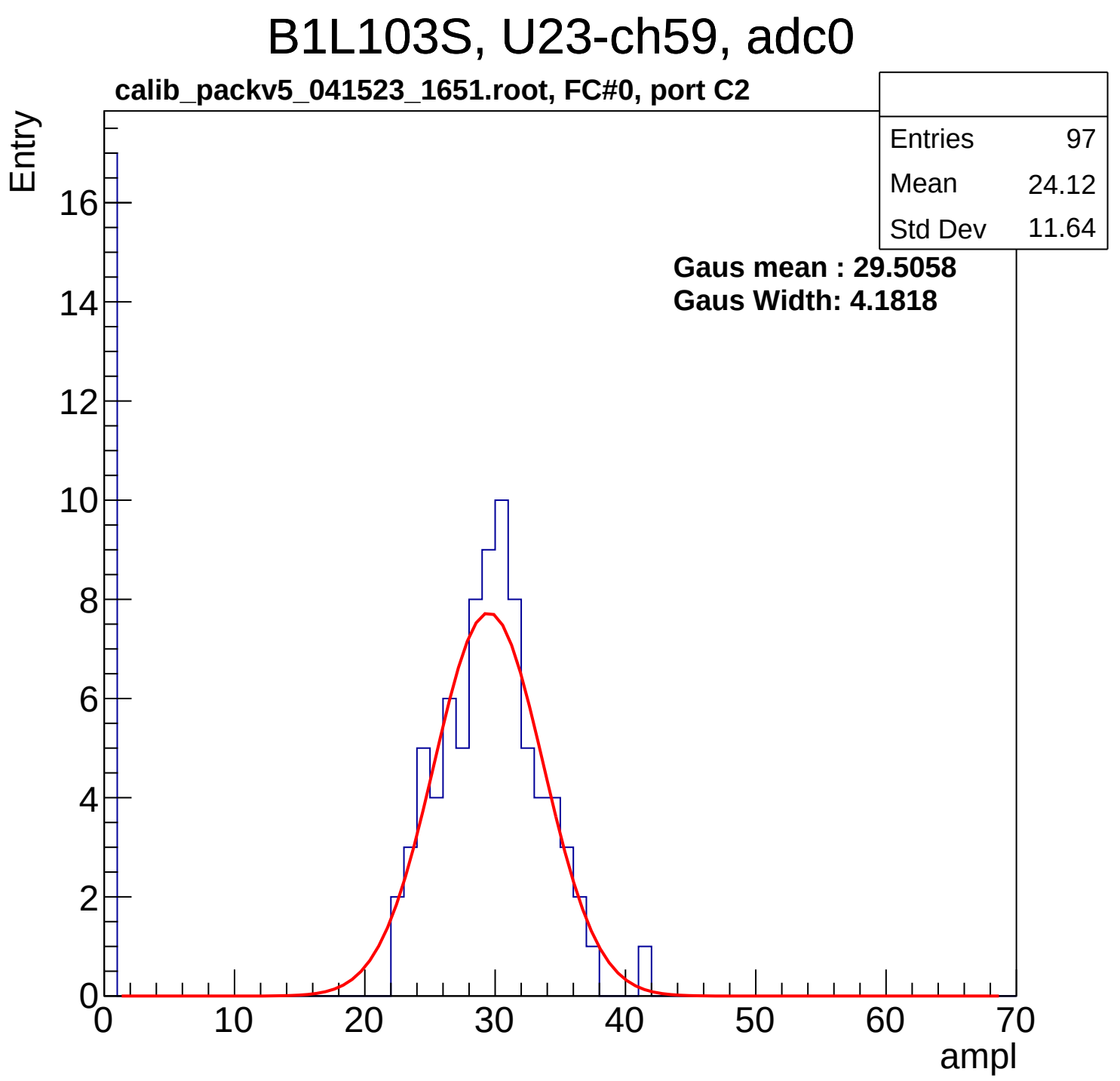

B1L103S, U23-ch58, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 48.27
Std Dev 3.466
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

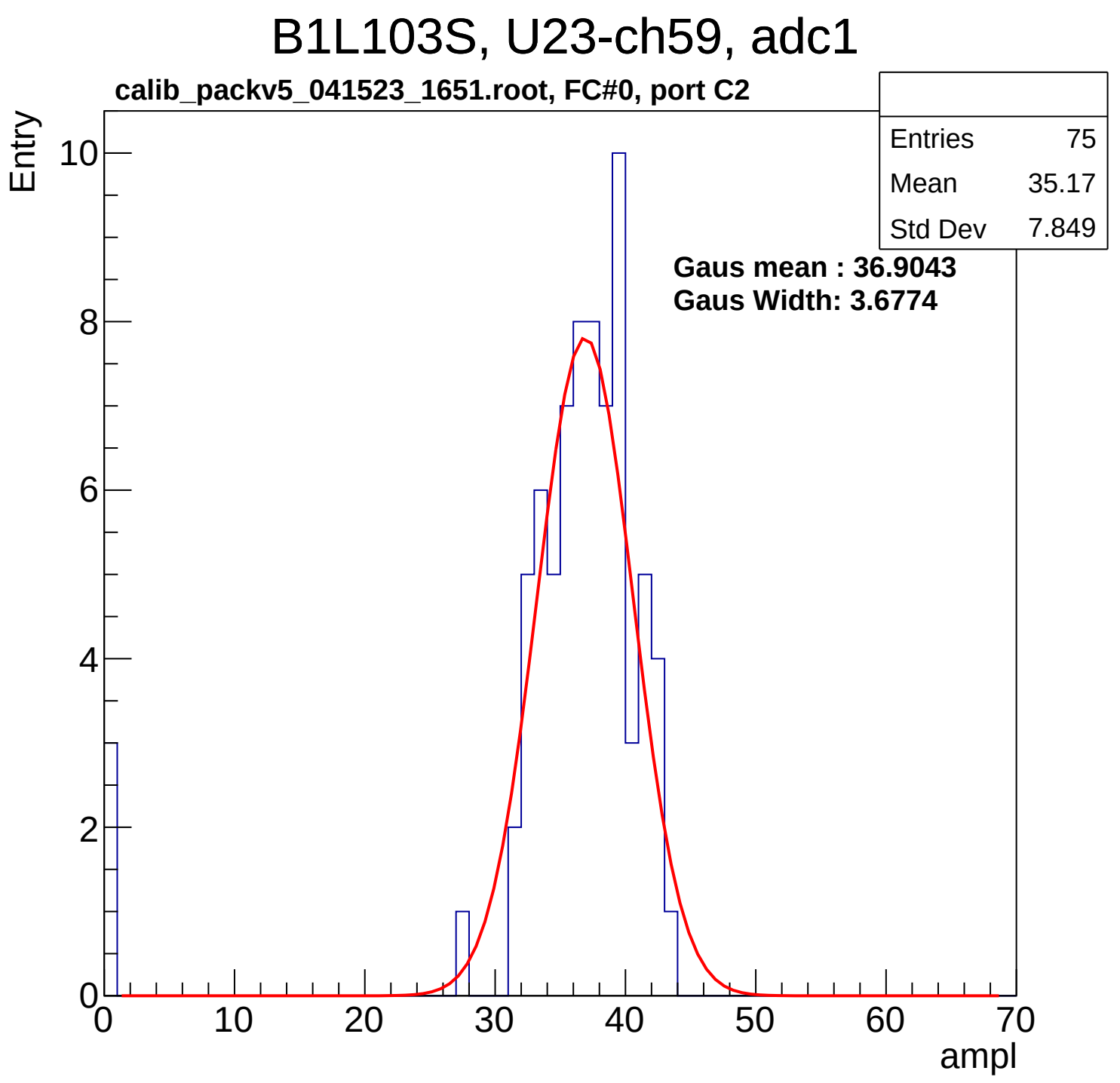

B1L103S, U23-ch58, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 54.99
Std Dev 3.519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

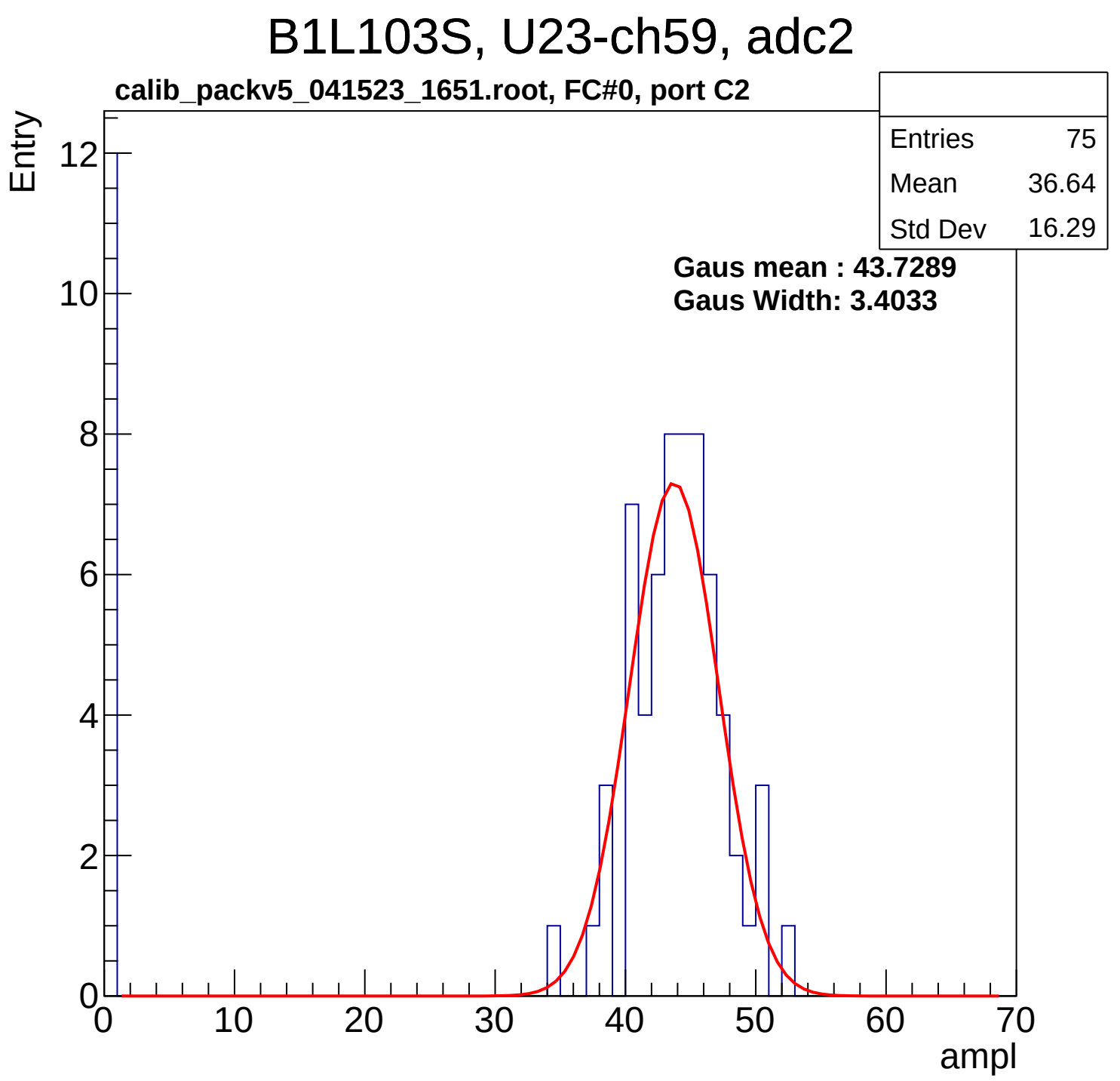

B1L103S, U23-ch58, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 36
Mean 58.36
Std Dev 10.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

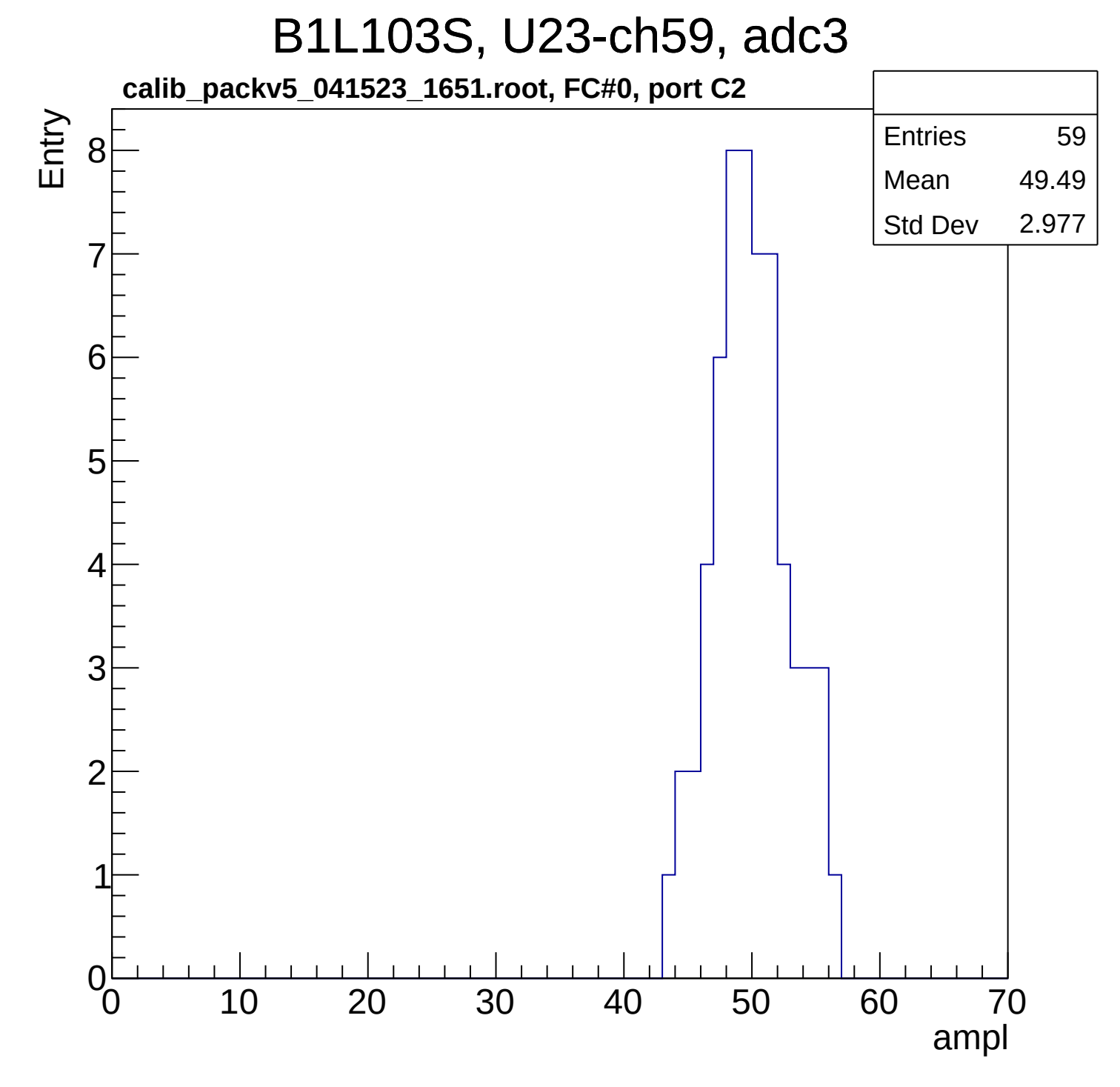

B1L103S, U23-ch58, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 61.64
Std Dev 1.367
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

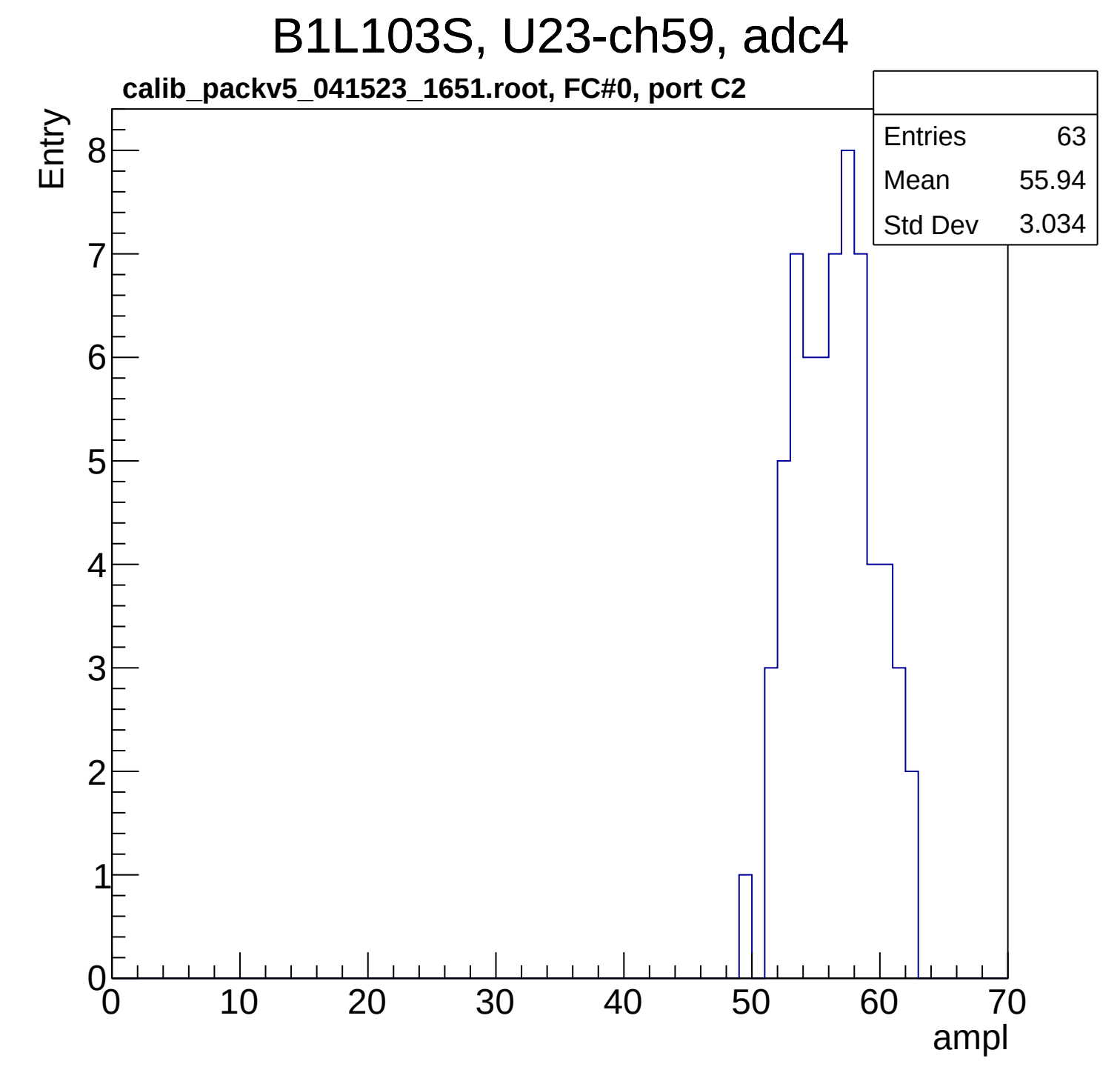

B1L103S, U23-ch58, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

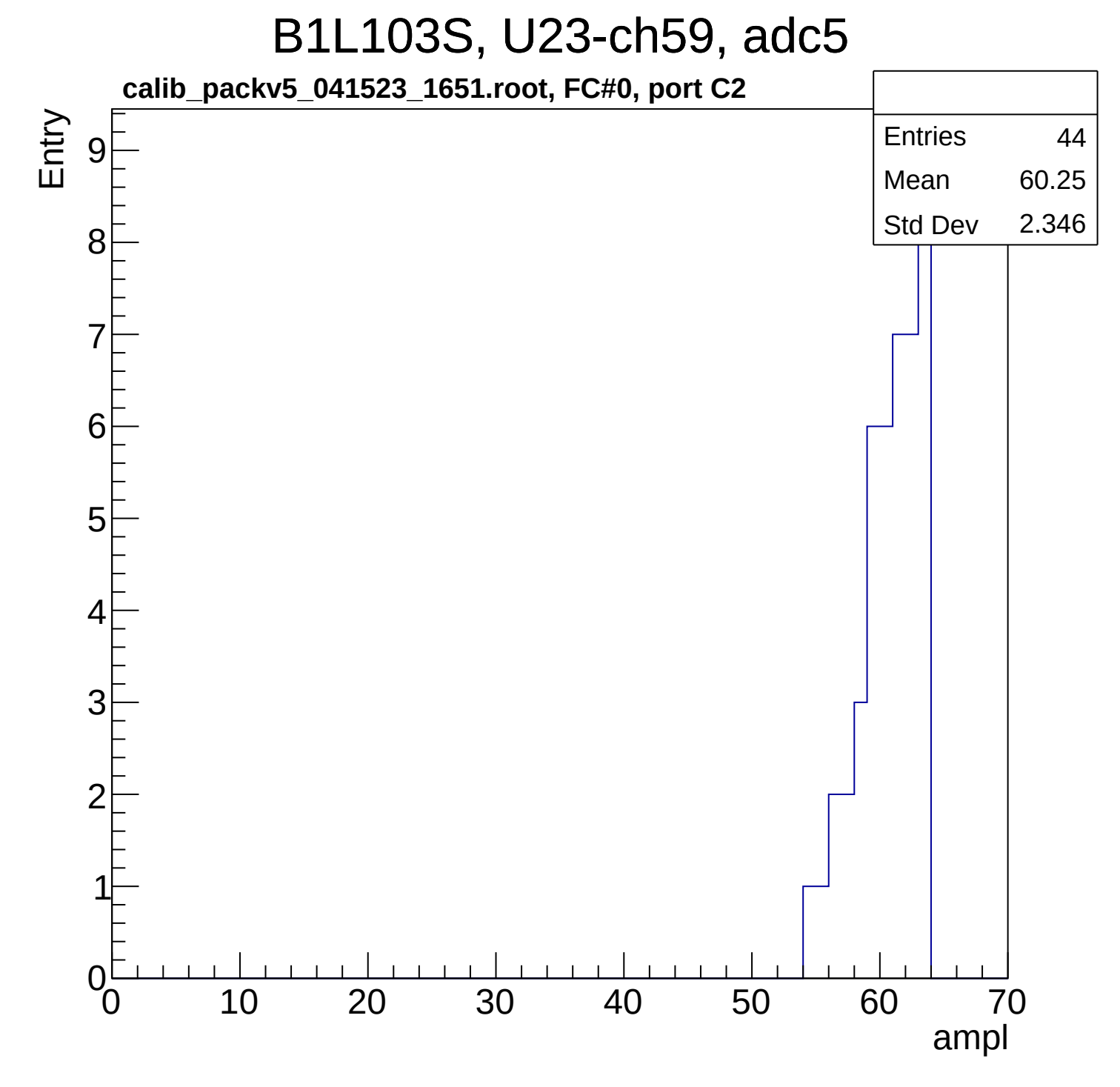

B1L103S, U23-ch59, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 97
Mean 24.12
Std Dev 11.64
Gaus mean : 29.5058
Gaus Width: 4.1818
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

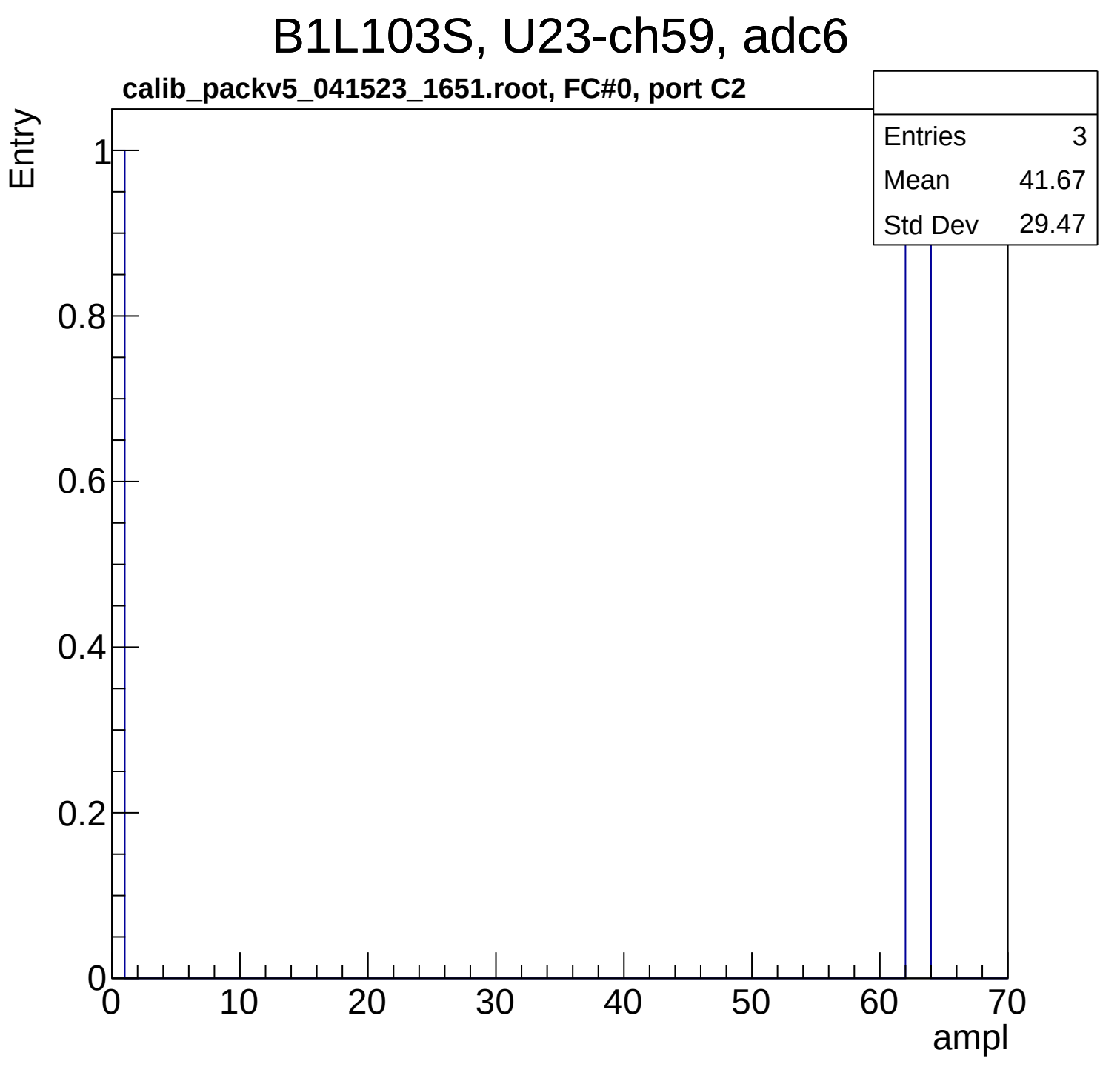

B1L103S, U23-ch59, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 35.17
Std Dev 7.849
Gaus mean : 36.9043
Gaus Width: 3.6774
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

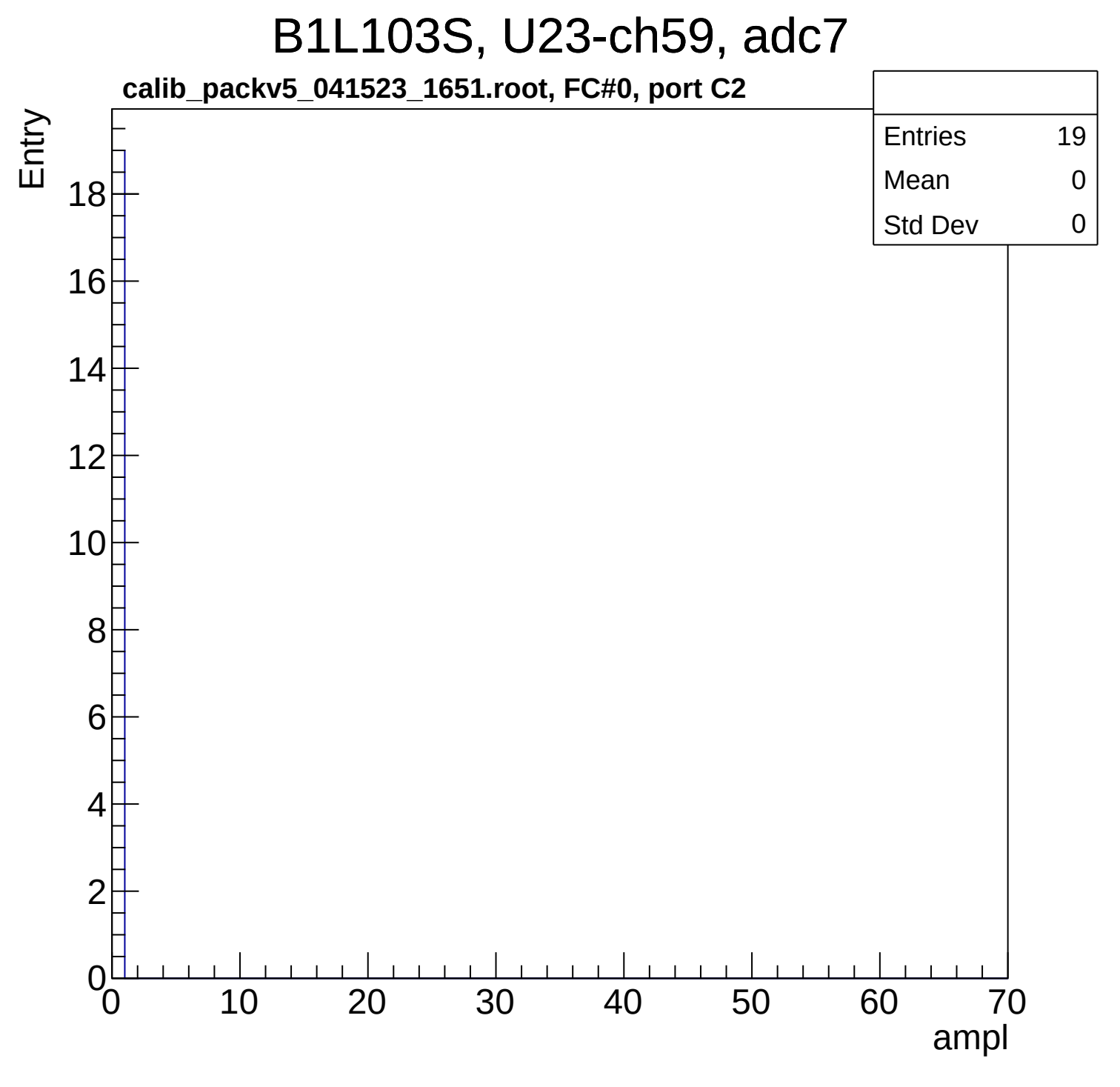

B1L103S, U23-ch59, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 36.64
Std Dev 16.29
Gaus mean : 43.7289
Gaus Width: 3.4033
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

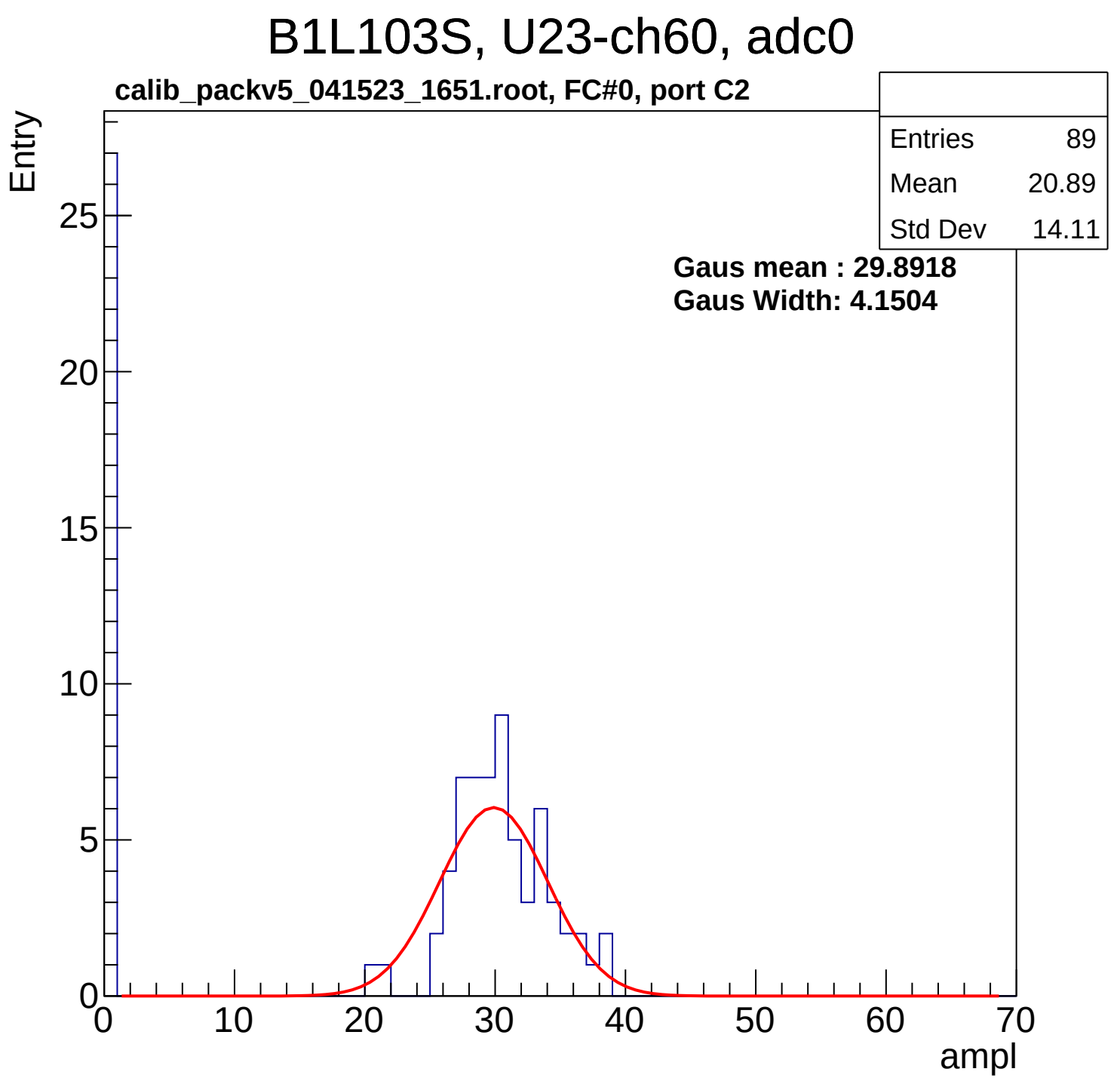

B1L103S, U23-ch59, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 49.49
Std Dev 2.977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

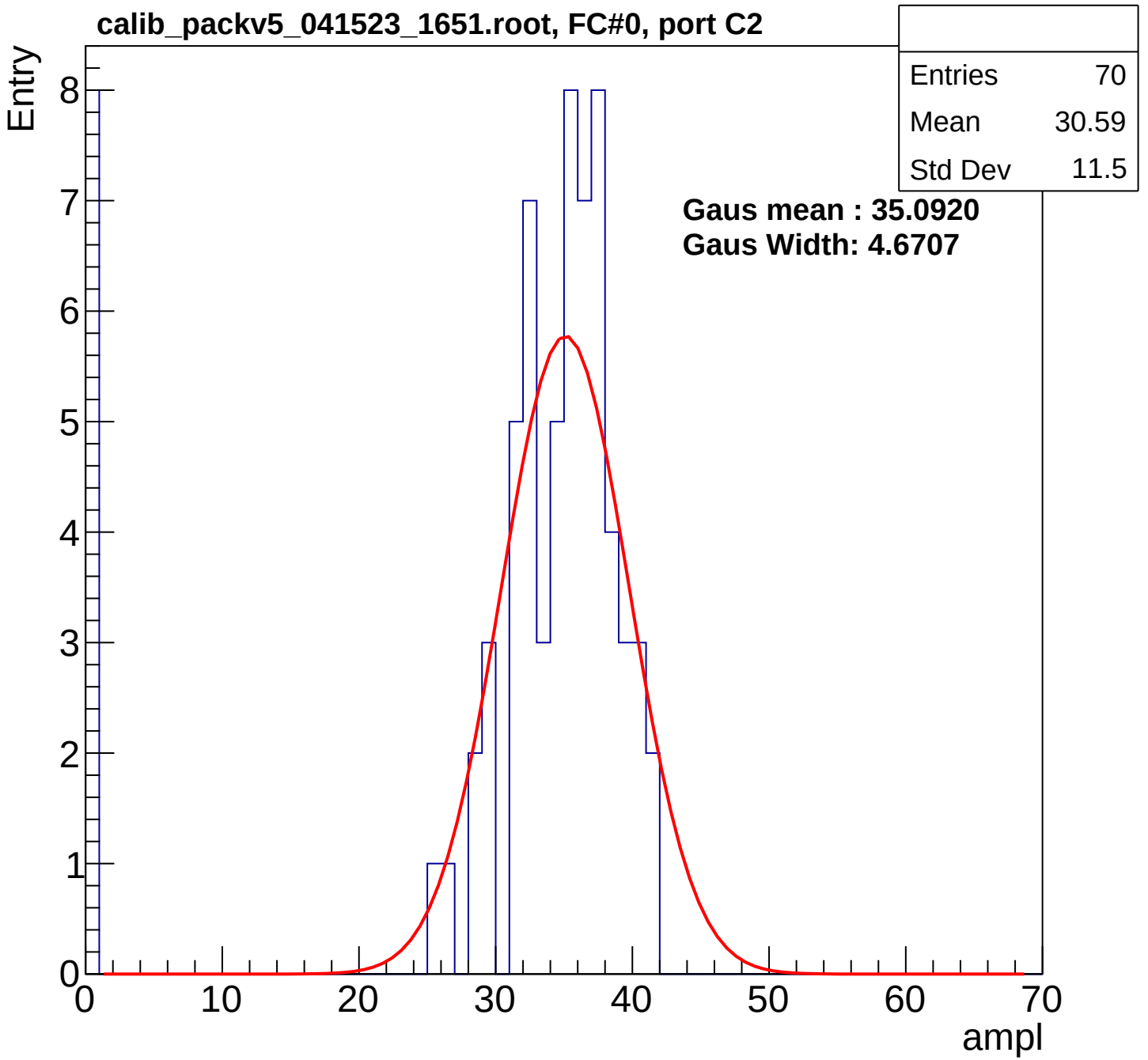

B1L103S, U23-ch59, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 55.94
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

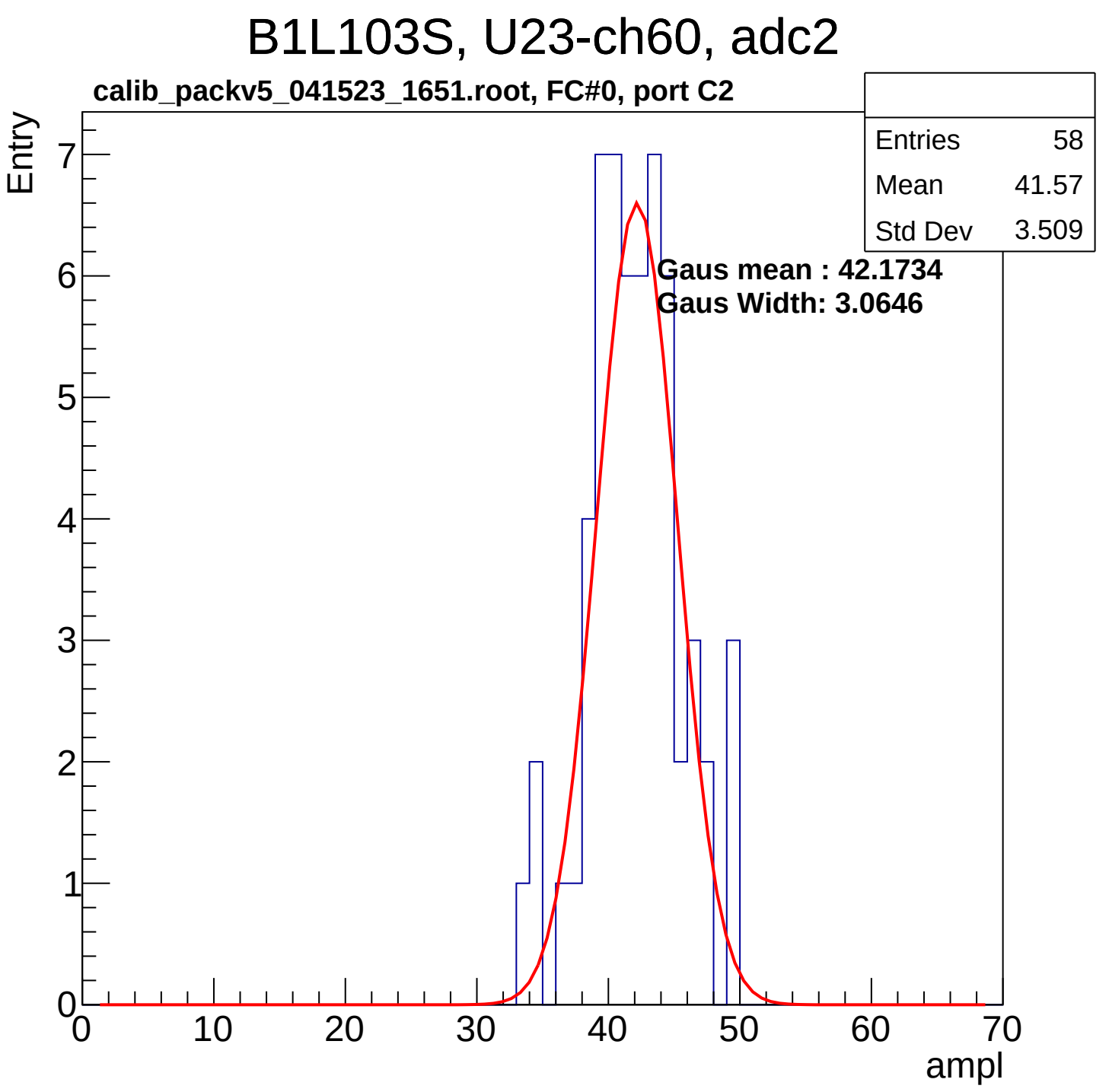

B1L103S, U23-ch59, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 44
Mean 60.25
Std Dev 2.346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

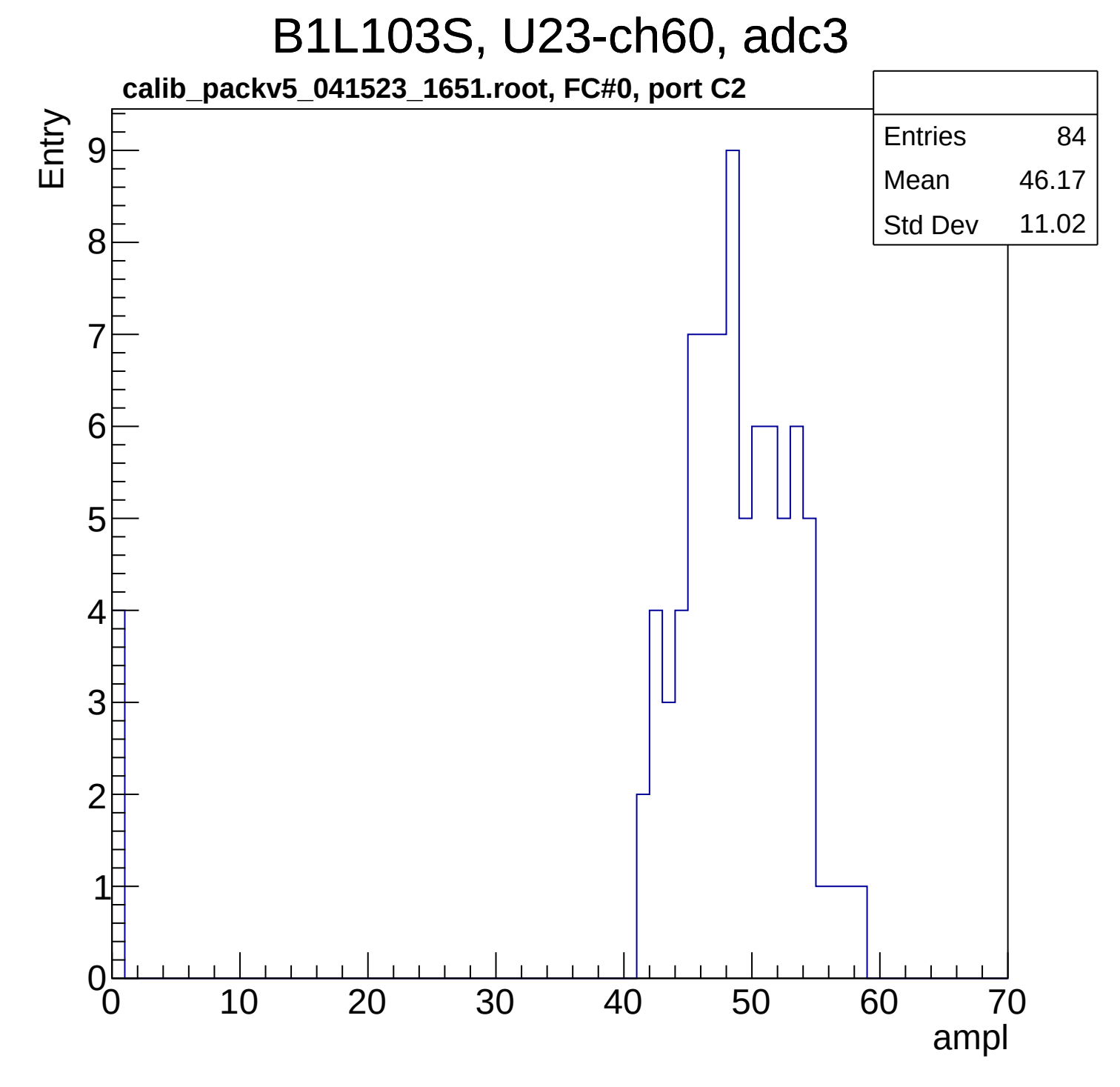

B1L103S, U23-ch59, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

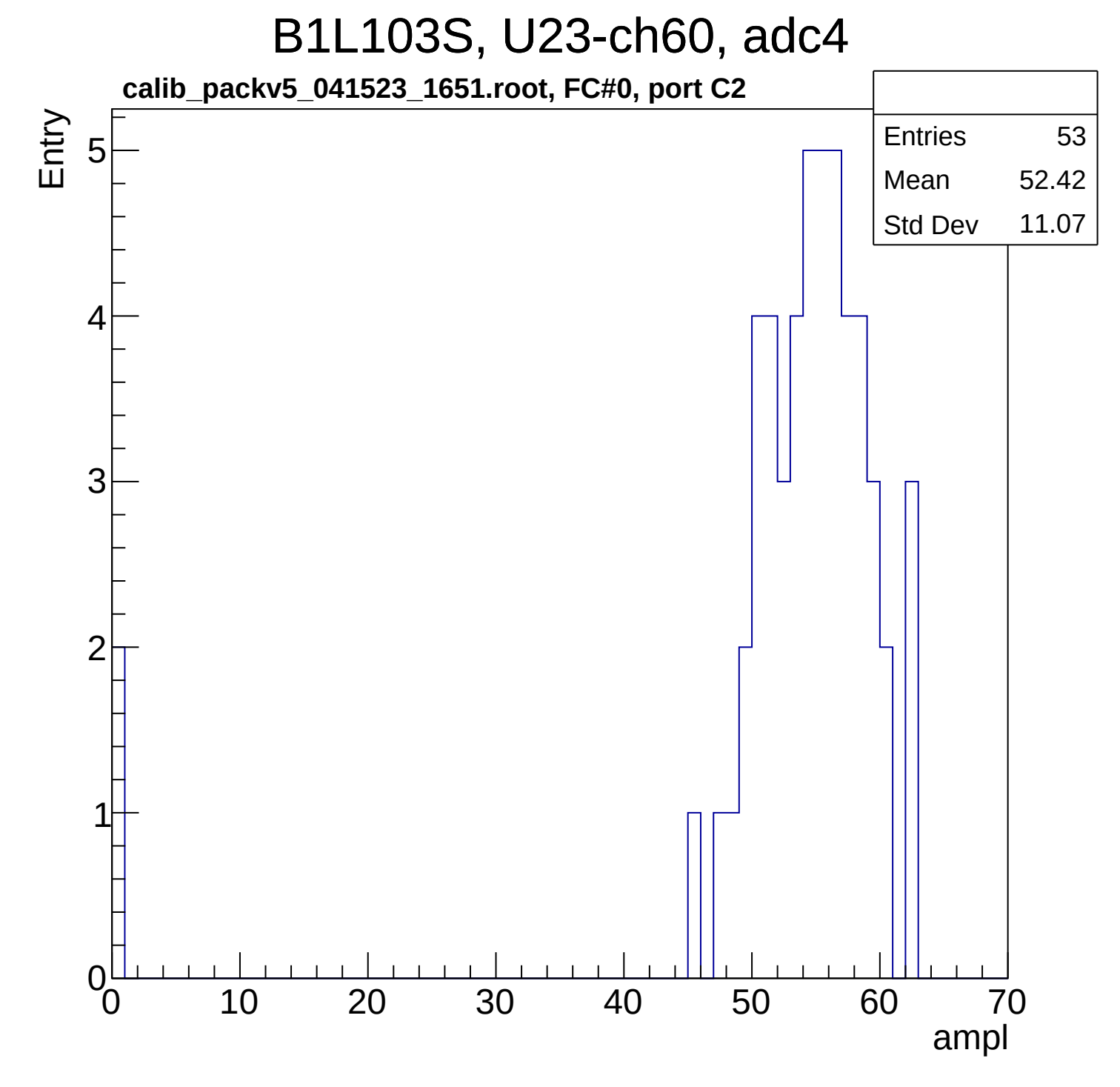

B1L103S, U23-ch59, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

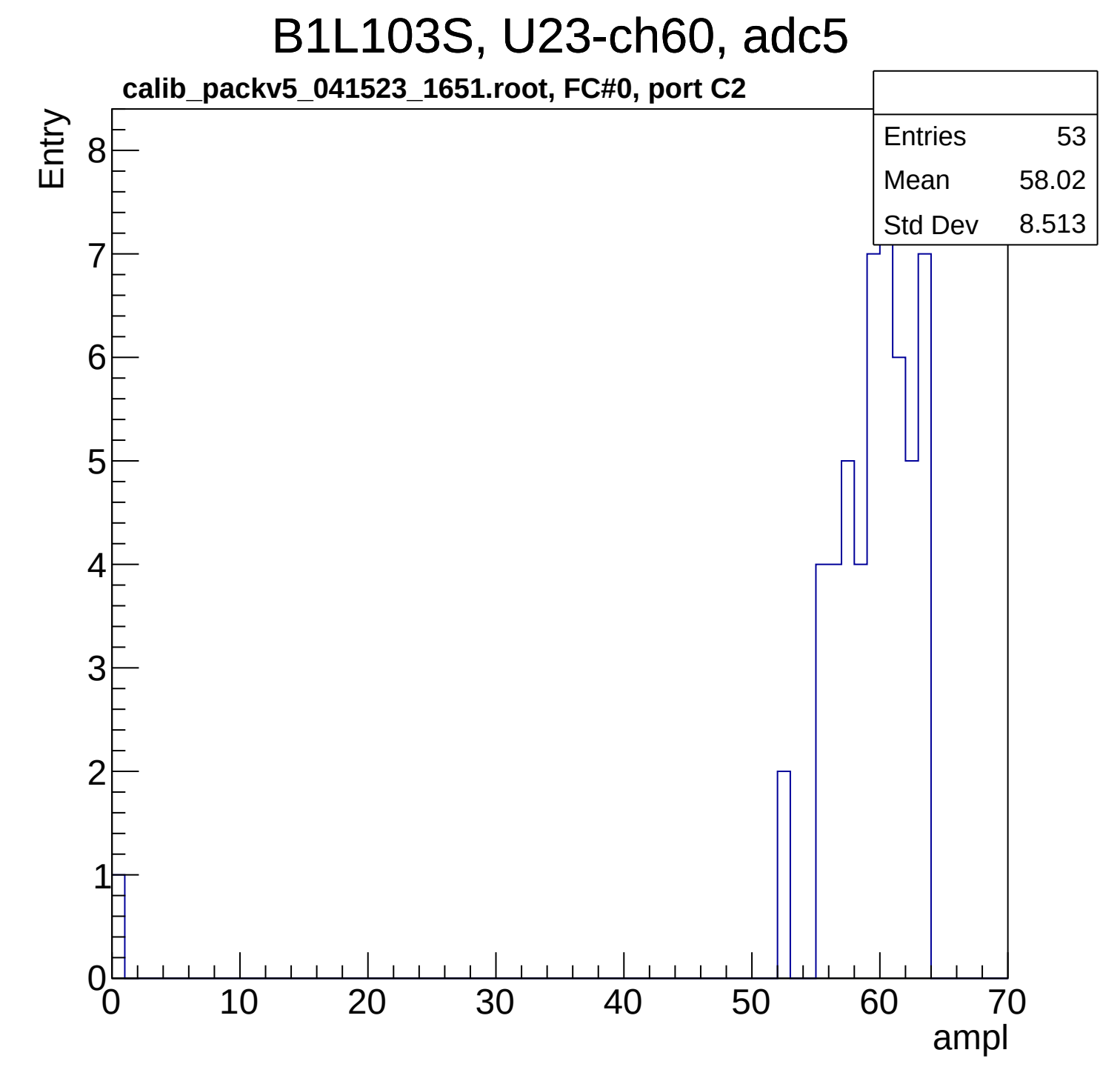

B1L103S, U23-ch60, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 20.89
Std Dev 14.11
Gaus mean : 29.8918
Gaus Width: 4.1504
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

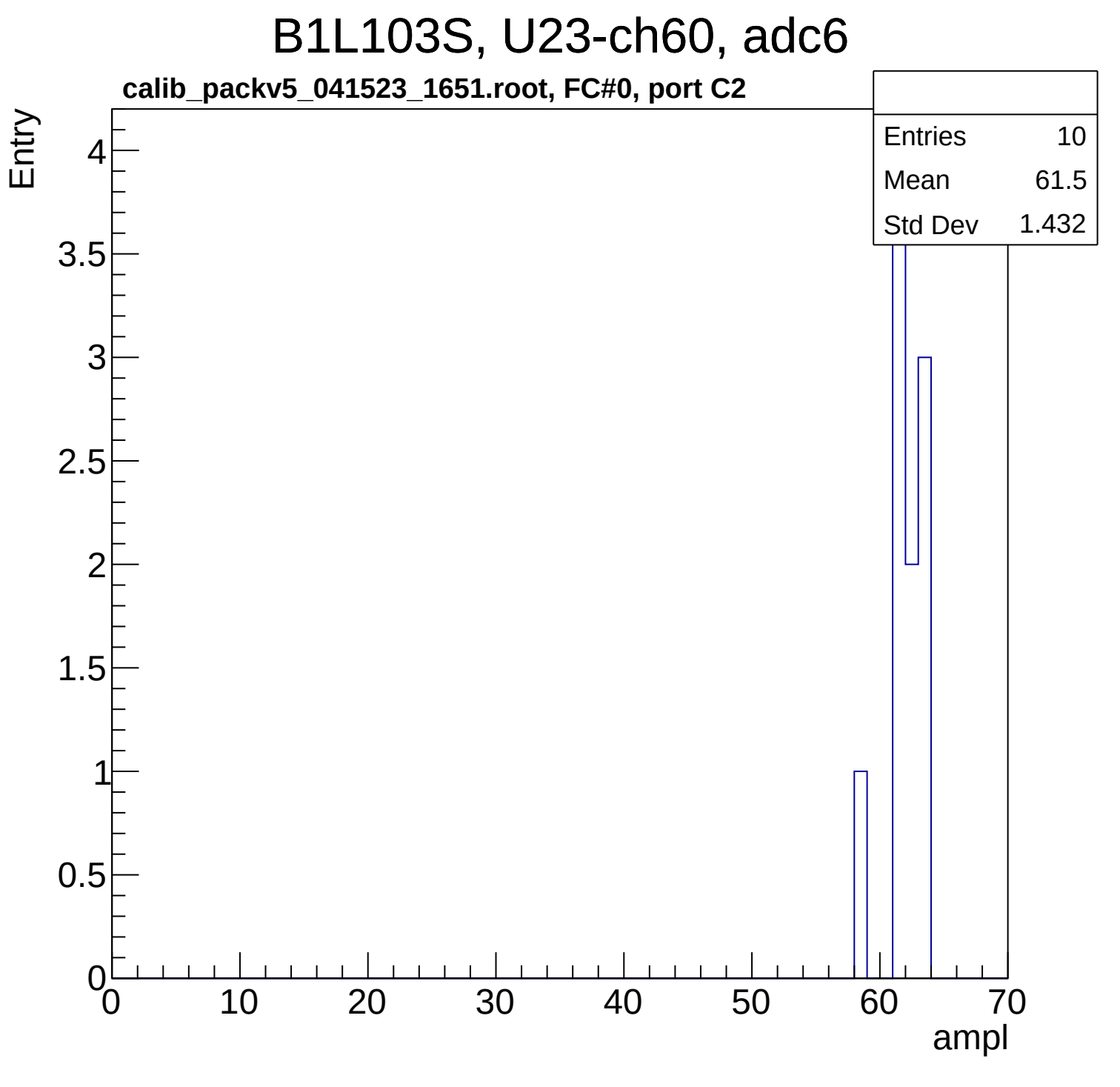

B1L103S, U23-ch60, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 30.59
Std Dev 11.5
Gaus mean : 35.0920
Gaus Width: 4.6707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

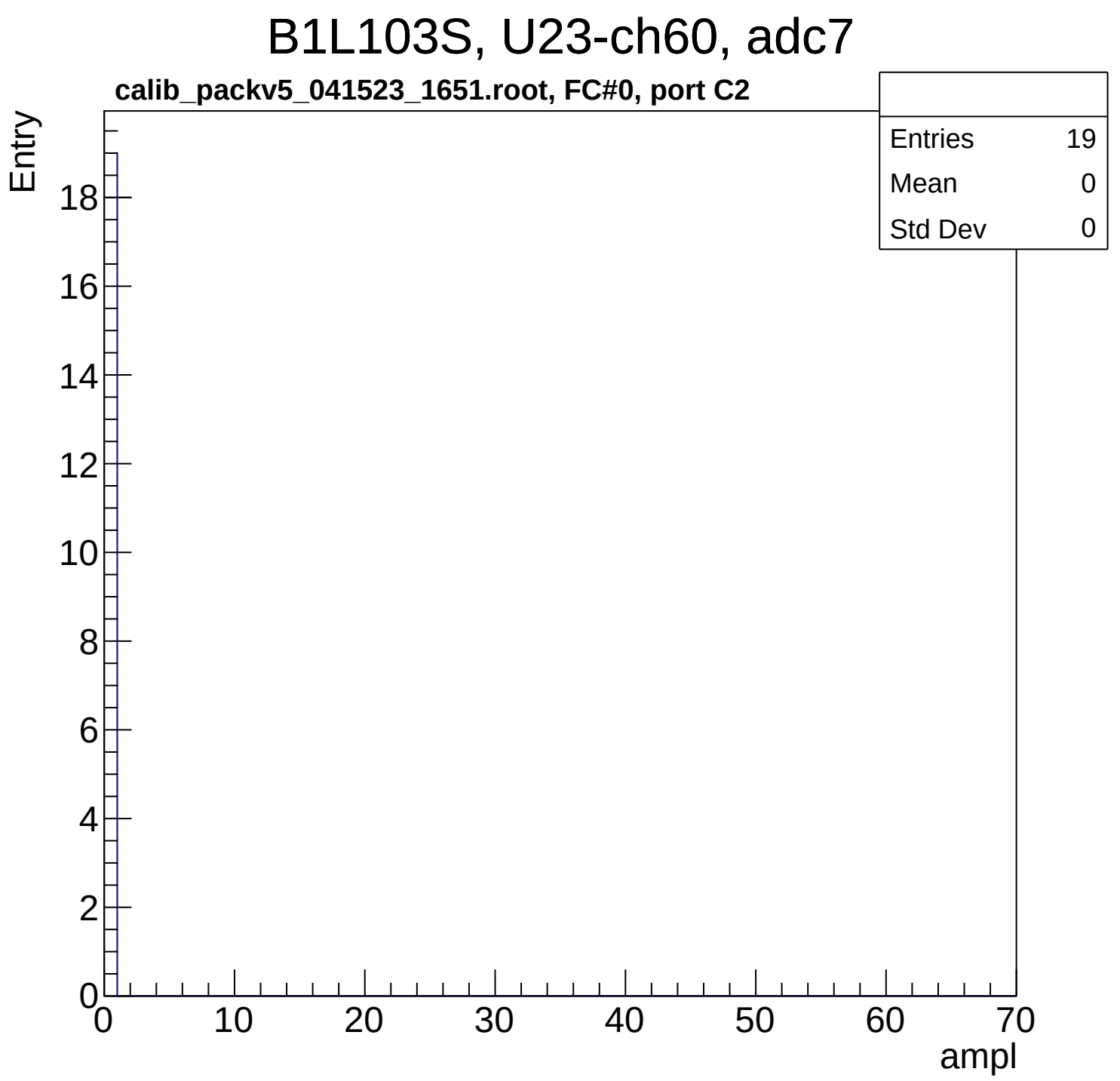

B1L103S, U23-ch60, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 41.57
Std Dev 3.509
Gaus mean : 42.1734
Gaus Width: 3.0646
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

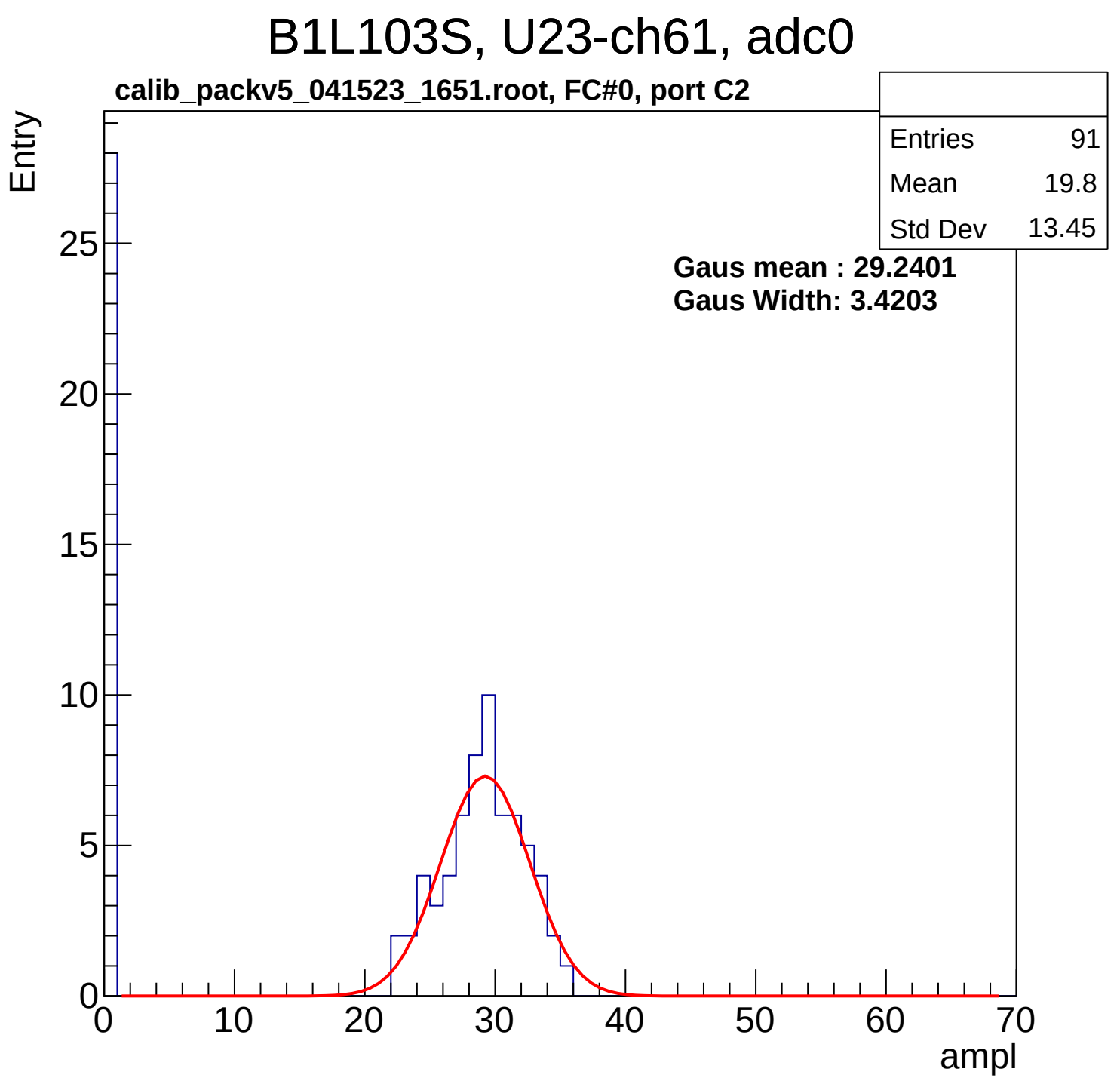

B1L103S, U23-ch60, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 84
Mean 46.17
Std Dev 11.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

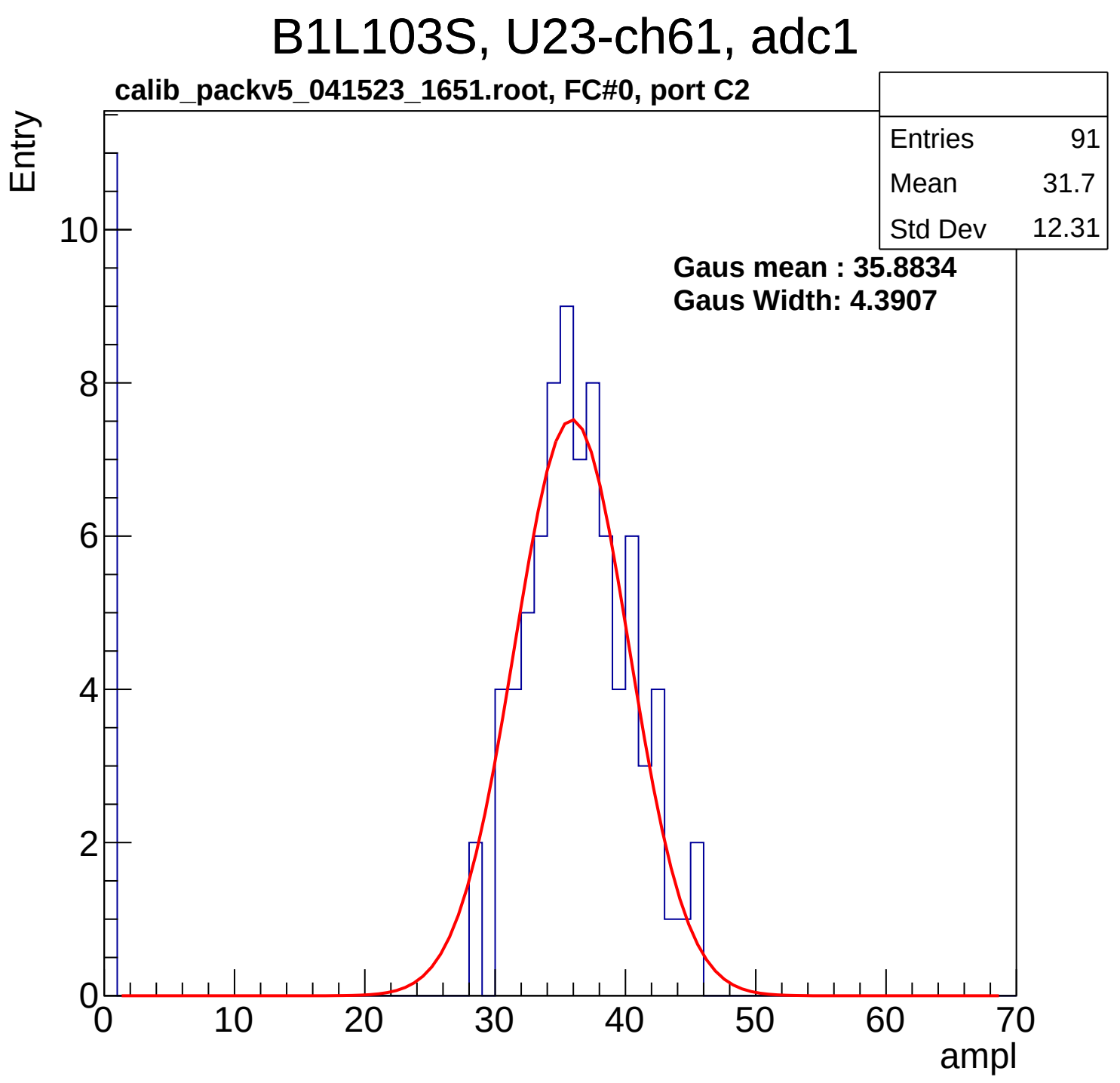

B1L103S, U23-ch60, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 52.42
Std Dev 11.07
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

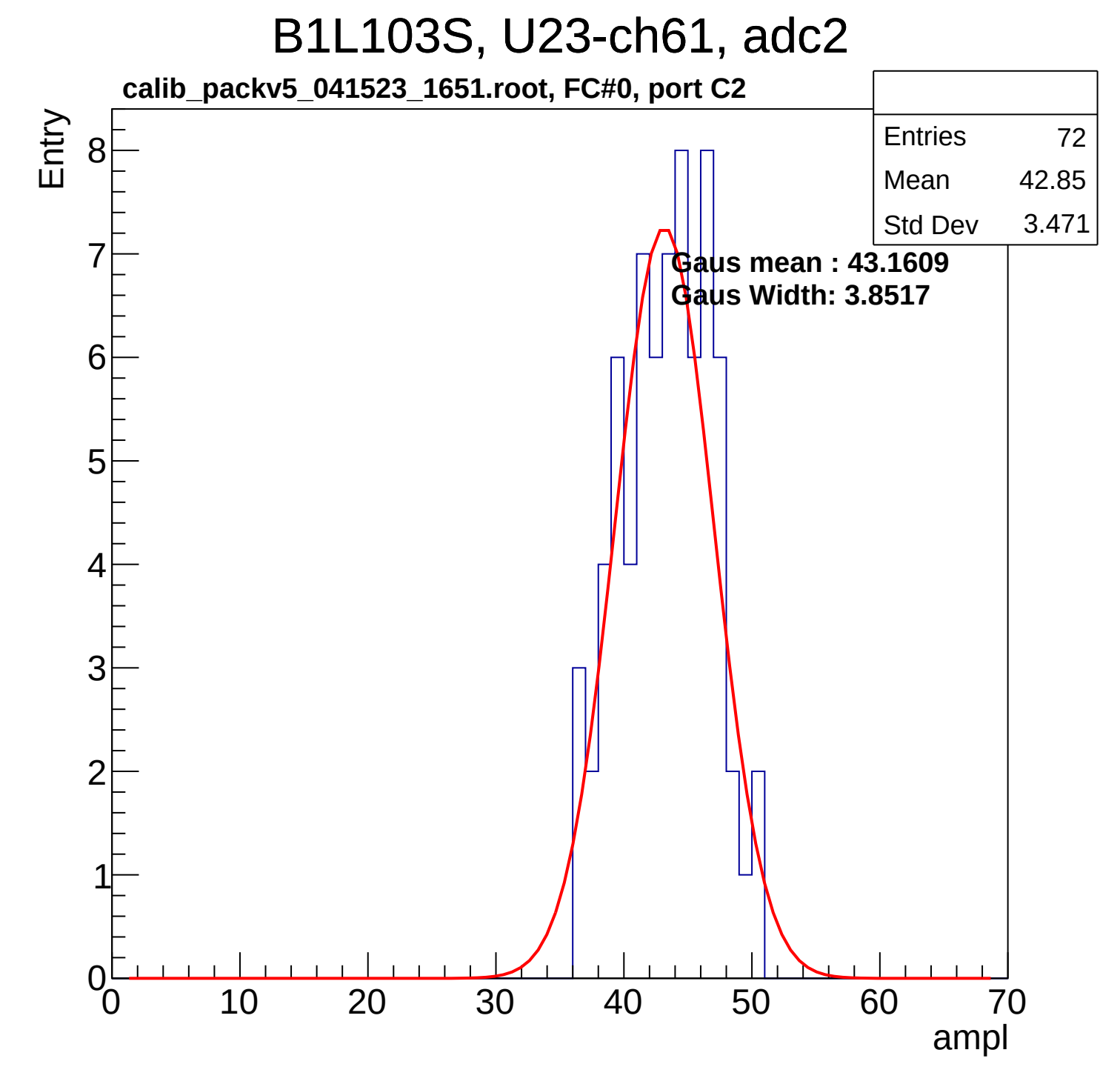

B1L103S, U23-ch60, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 58.02
Std Dev 8.513
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

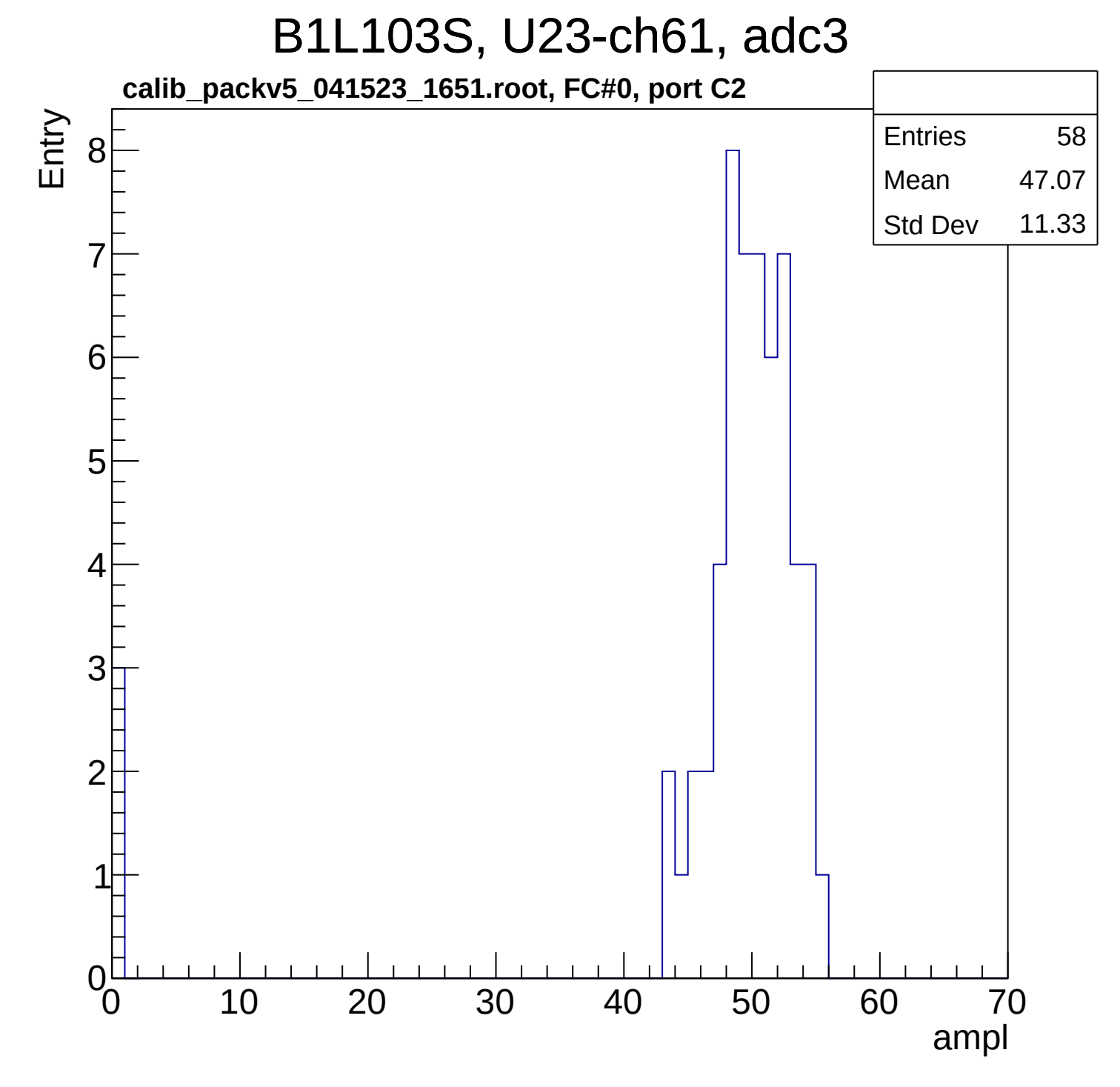

B1L103S, U23-ch60, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 61.5
Std Dev 1.432
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

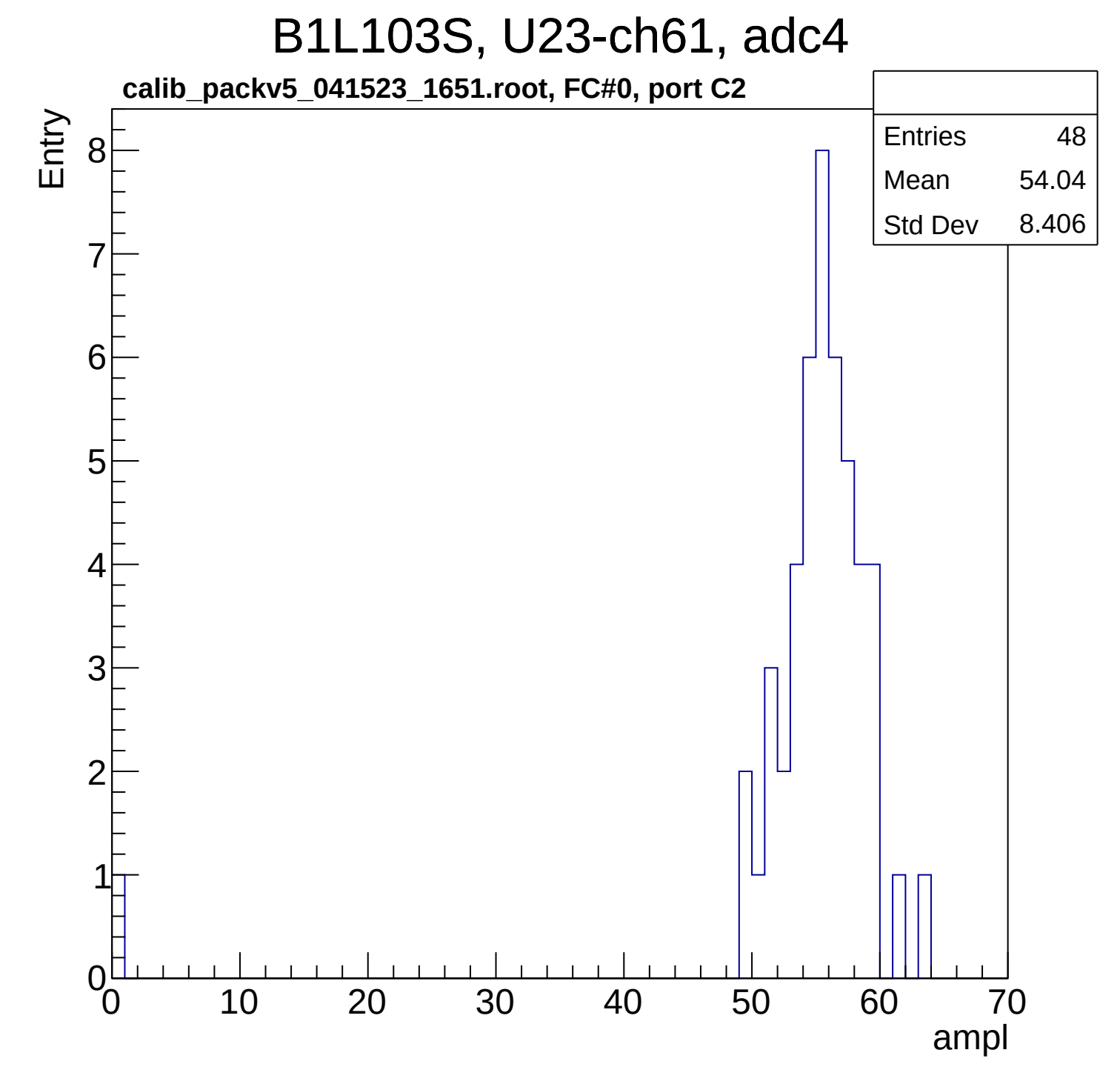

B1L103S, U23-ch60, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

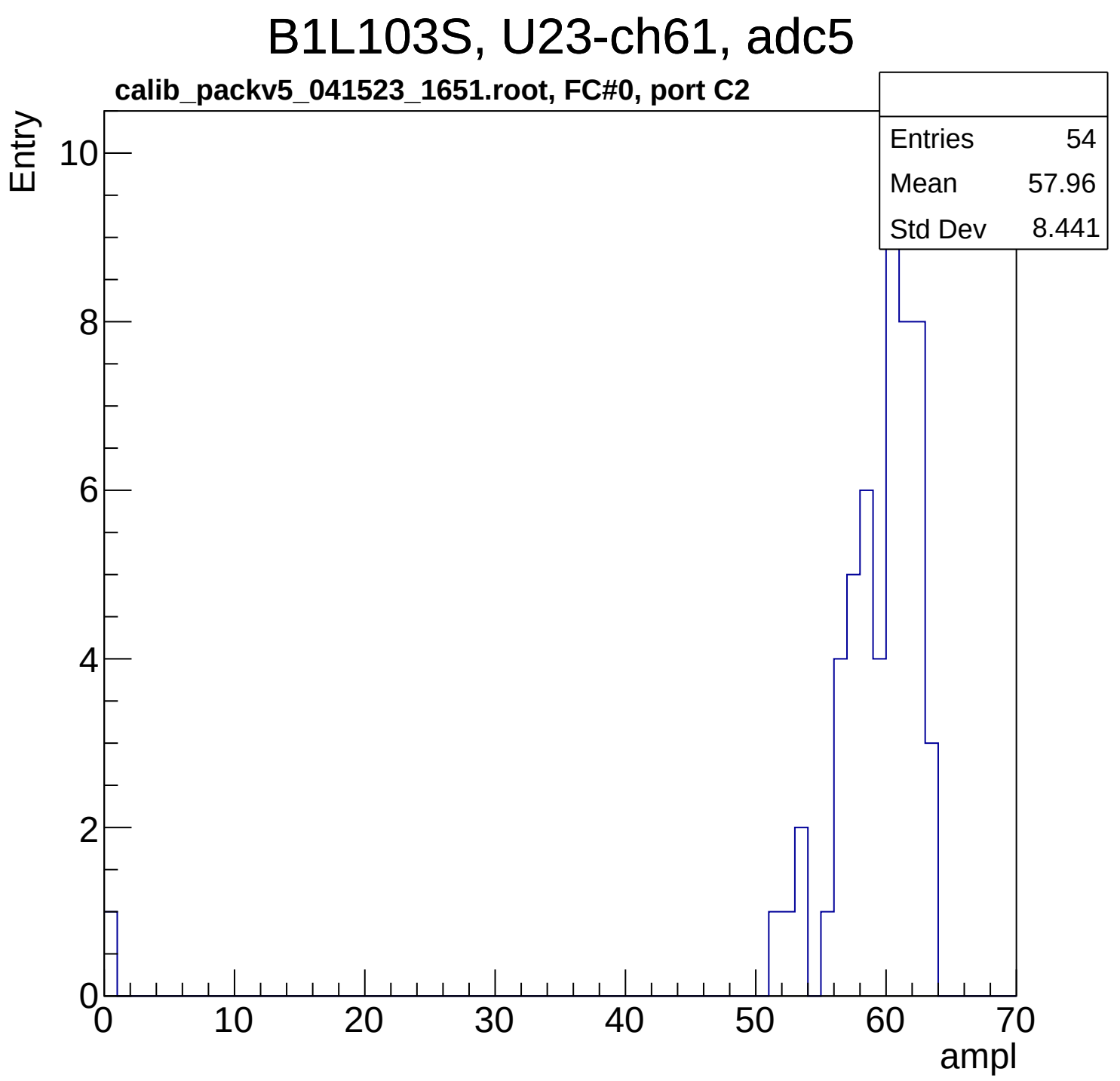

B1L103S, U23-ch61, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 19.8
Std Dev 13.45
Gaus mean : 29.2401
Gaus Width: 3.4203
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

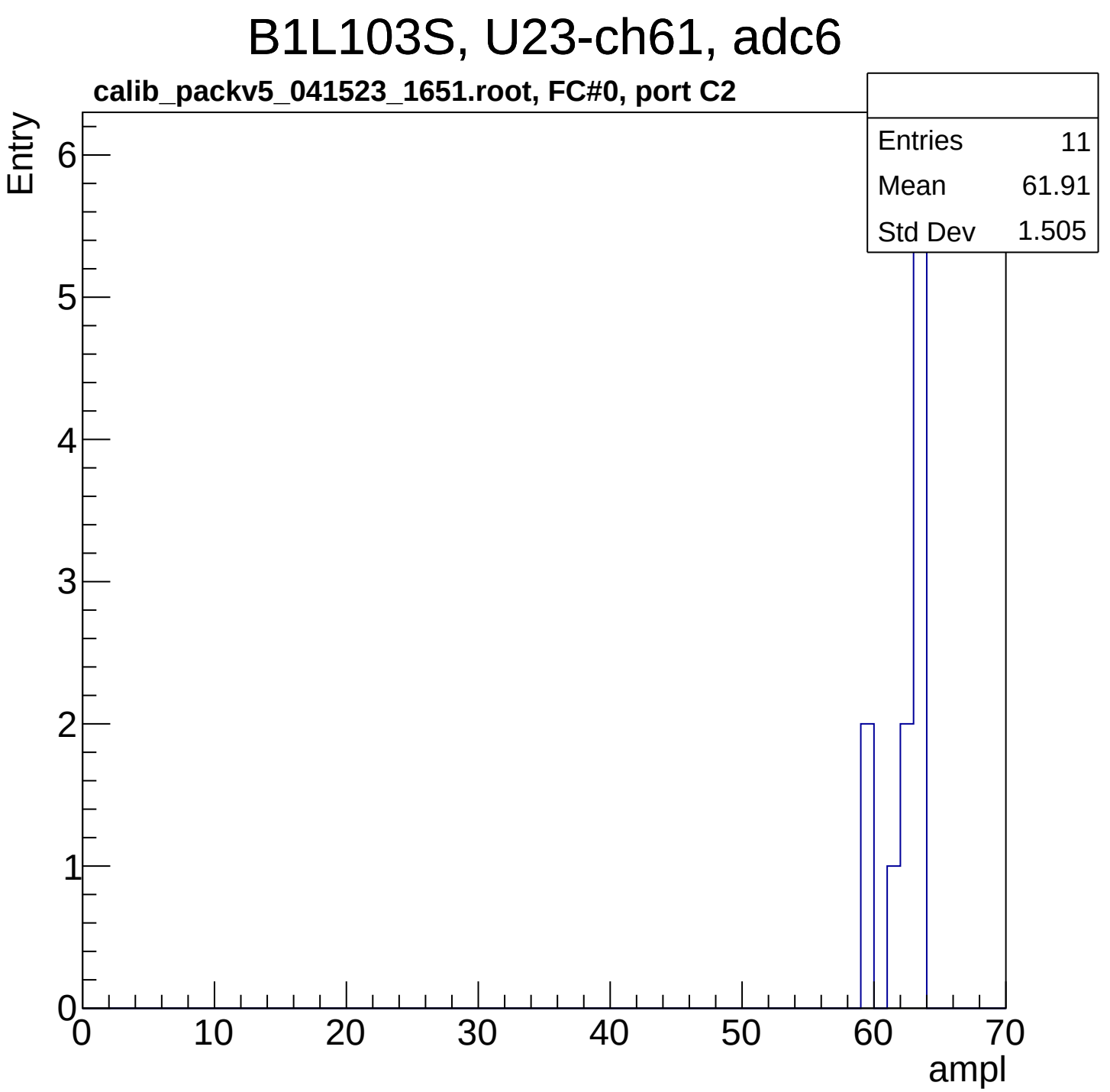

B1L103S, U23-ch61, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 31.7
Std Dev 12.31
Gaus mean : 35.8834
Gaus Width: 4.3907
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

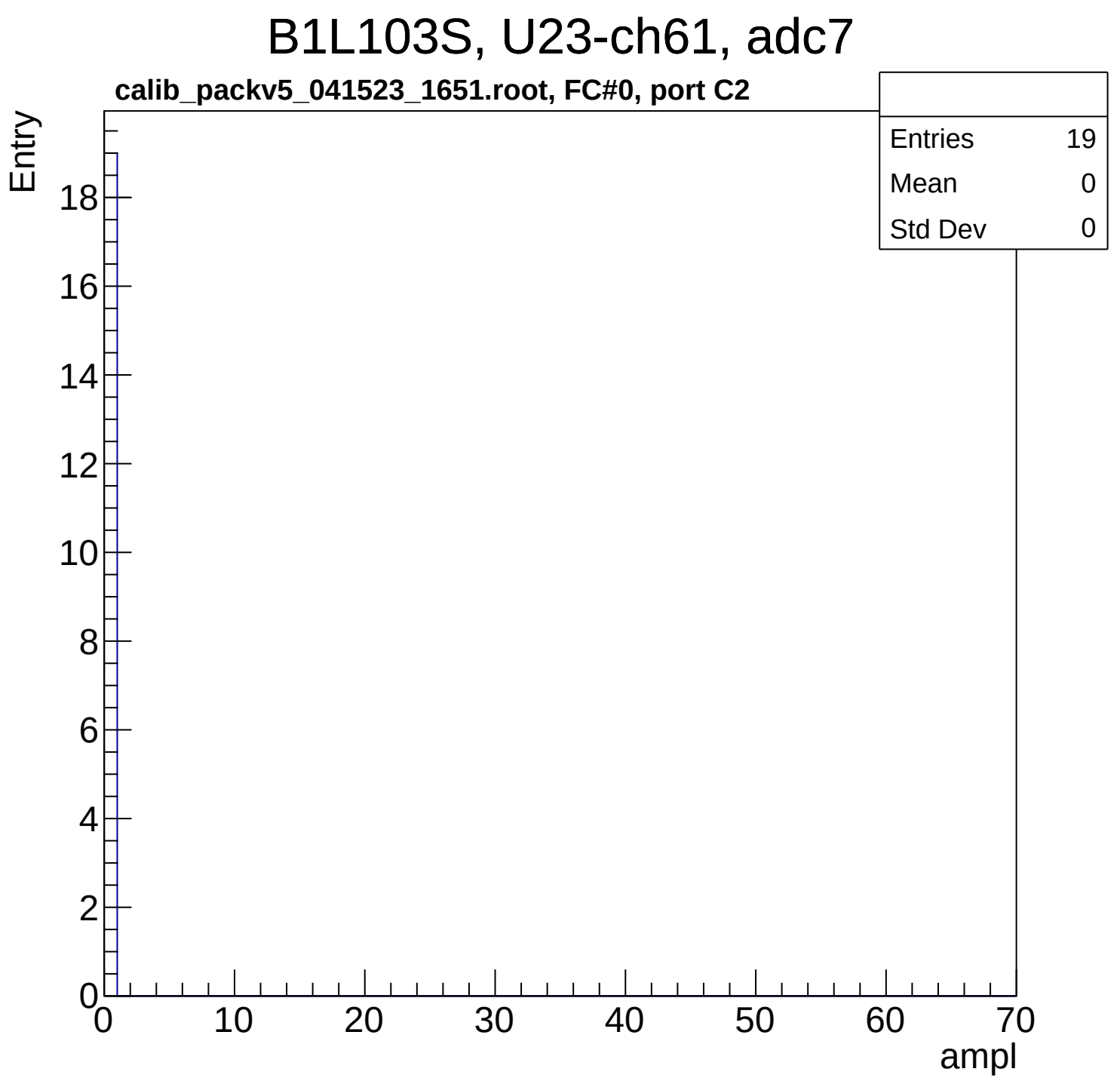

B1L103S, U23-ch61, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 42.85
Std Dev 3.471
Gaus mean : 43.1609
Gaus Width: 3.8517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

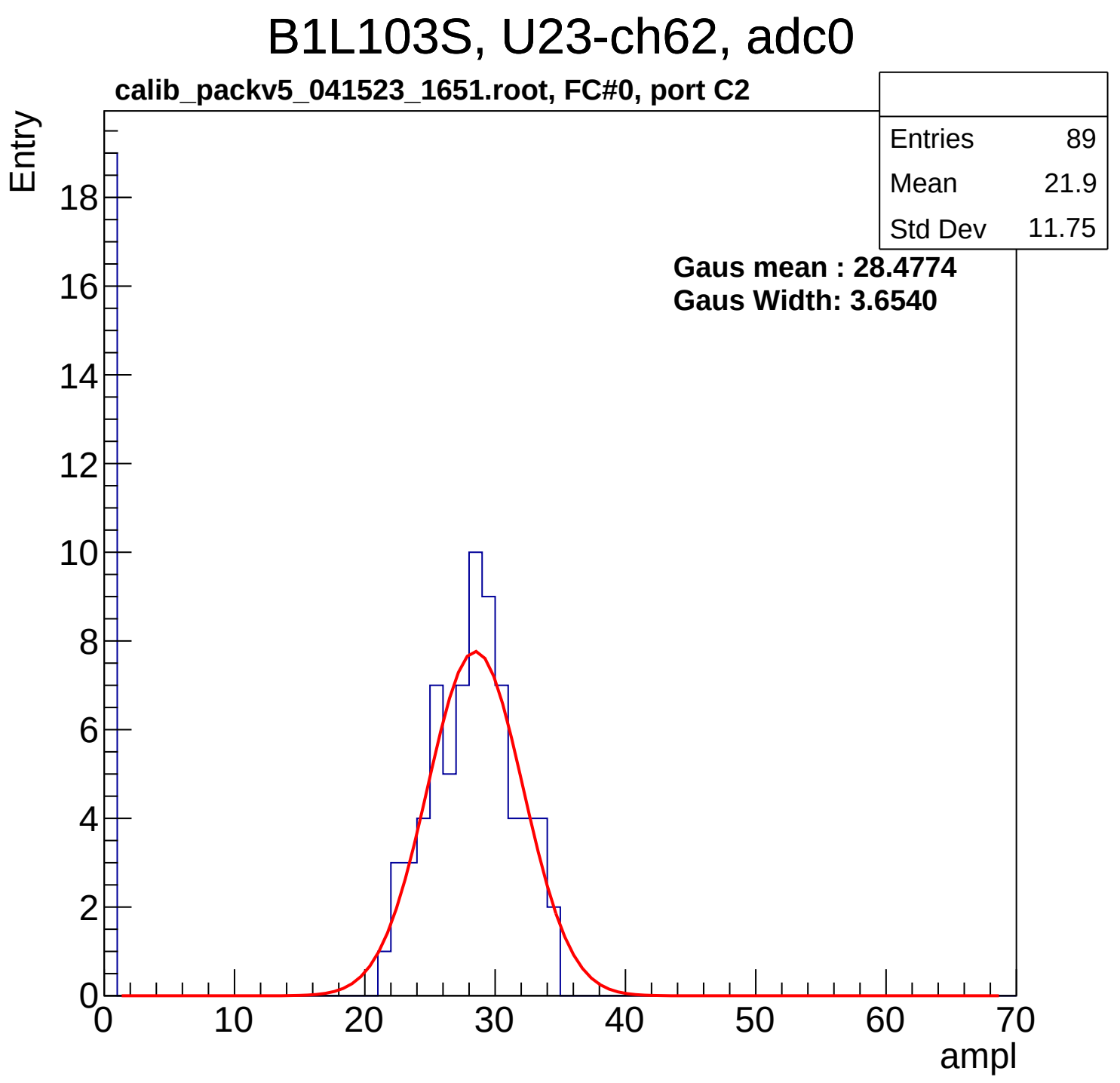

B1L103S, U23-ch61, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 47.07
Std Dev 11.33
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

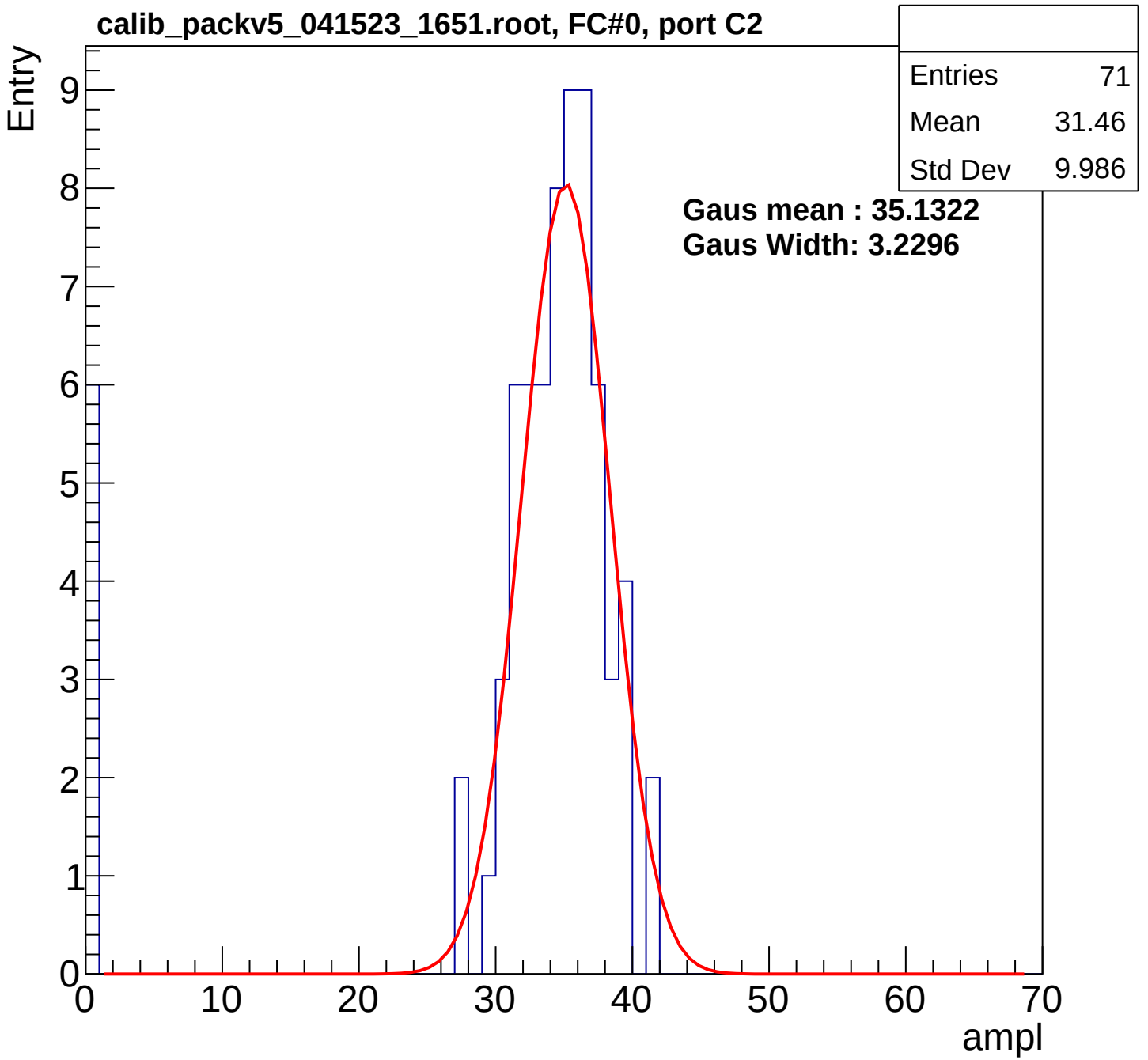

B1L103S, U23-ch61, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 54.04
Std Dev 8.406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

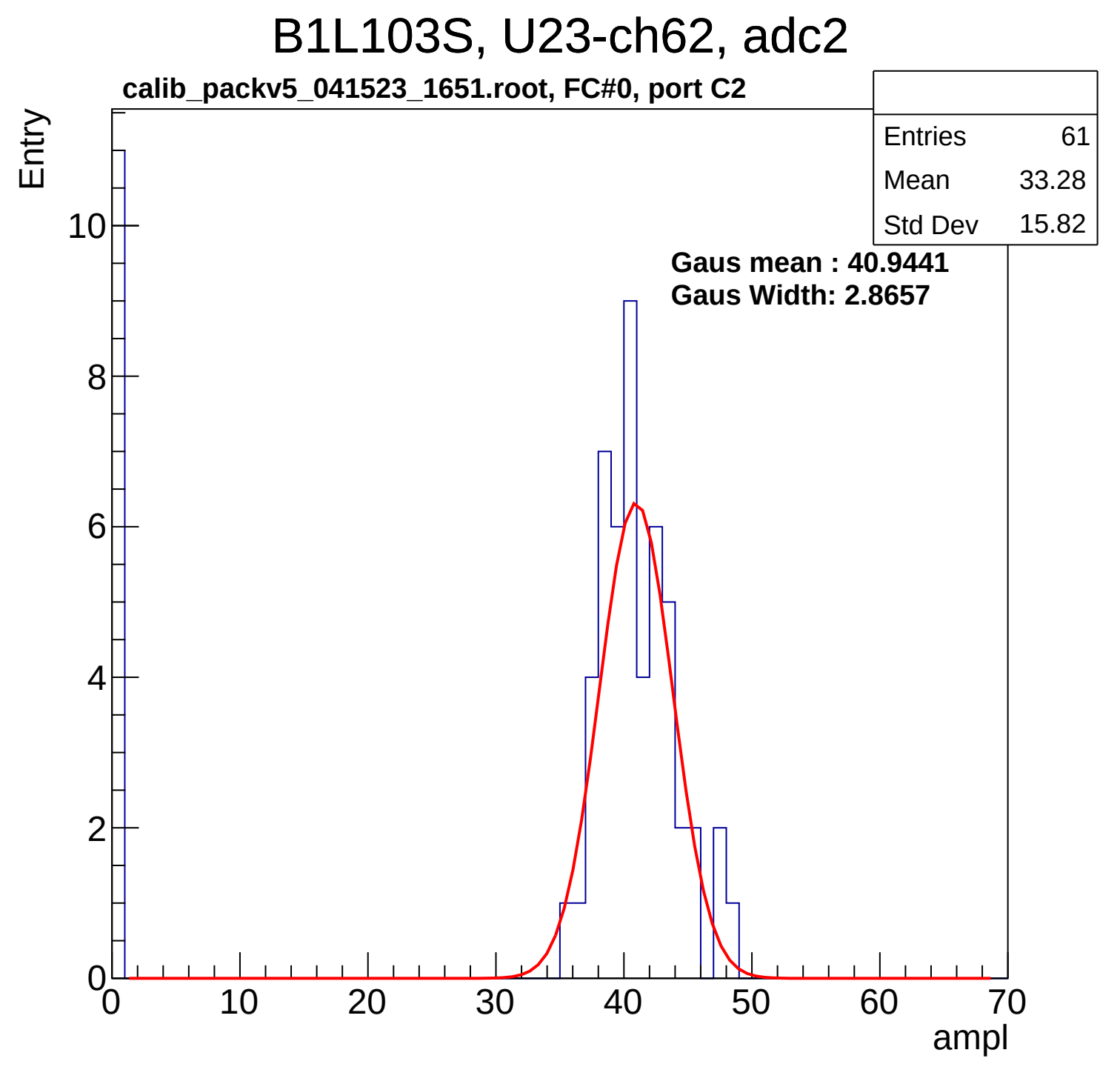

B1L103S, U23-ch61, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 57.96
Std Dev 8.441
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

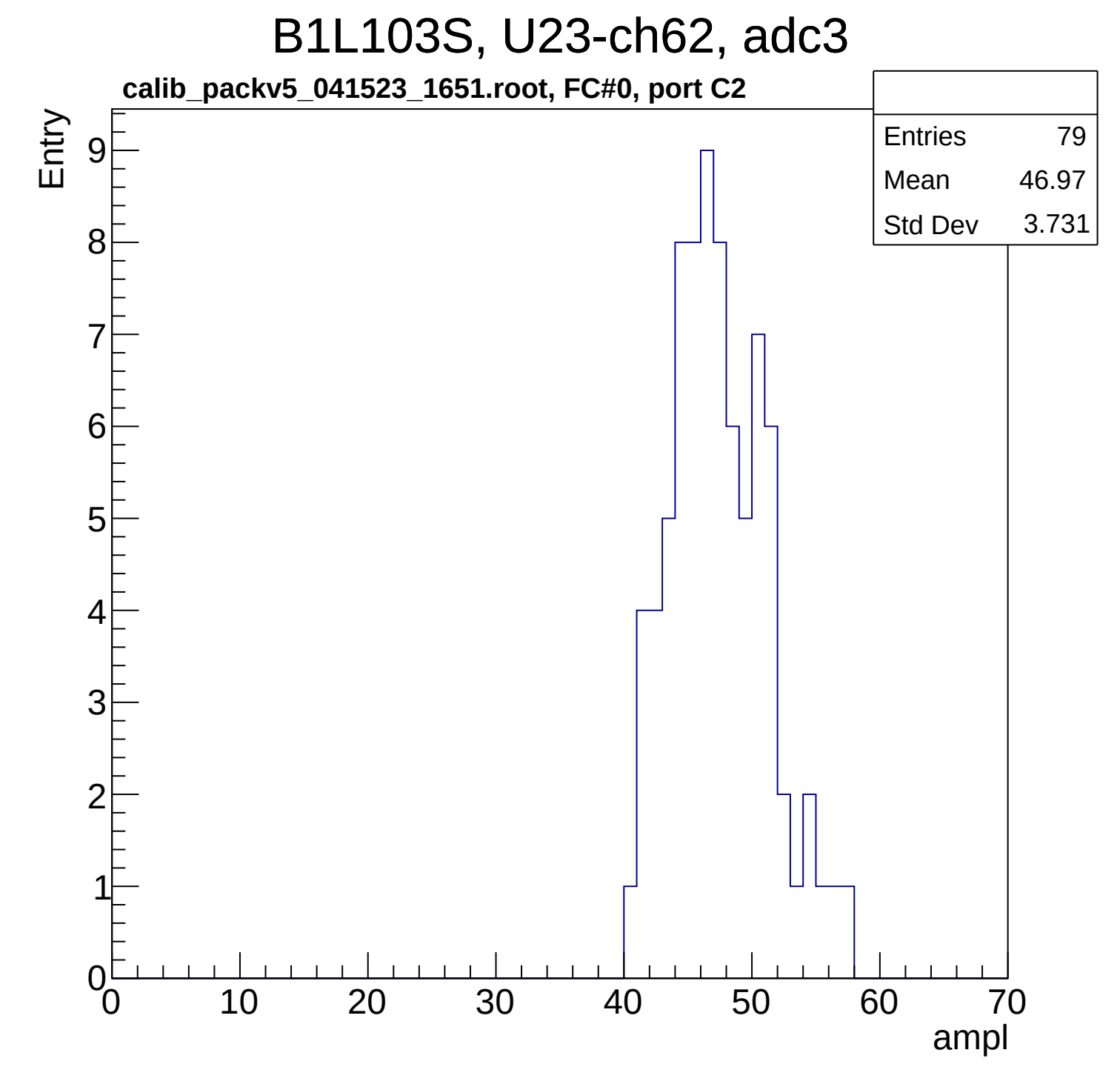

B1L103S, U23-ch61, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 61.91
Std Dev 1.505
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

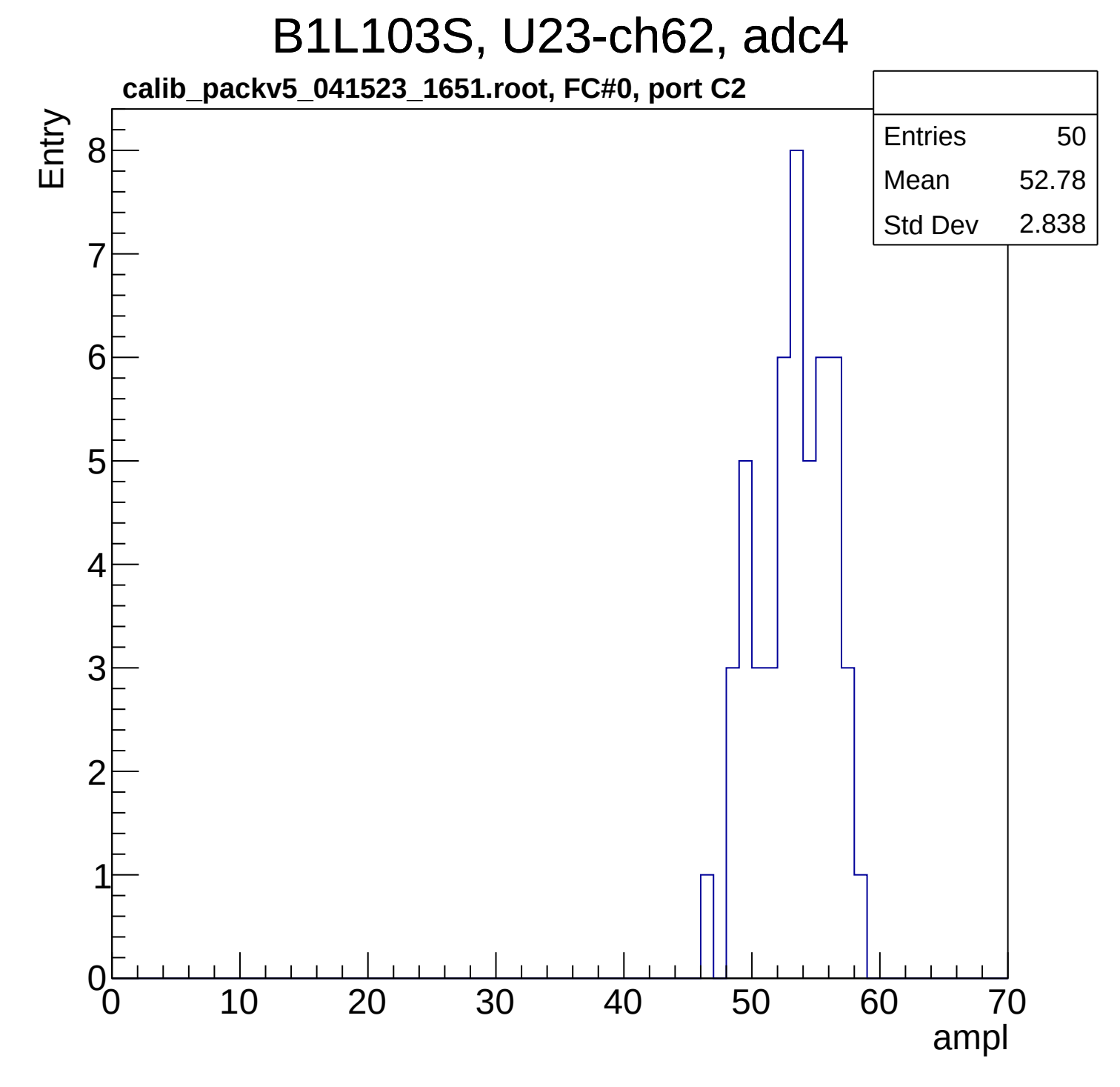

B1L103S, U23-ch61, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

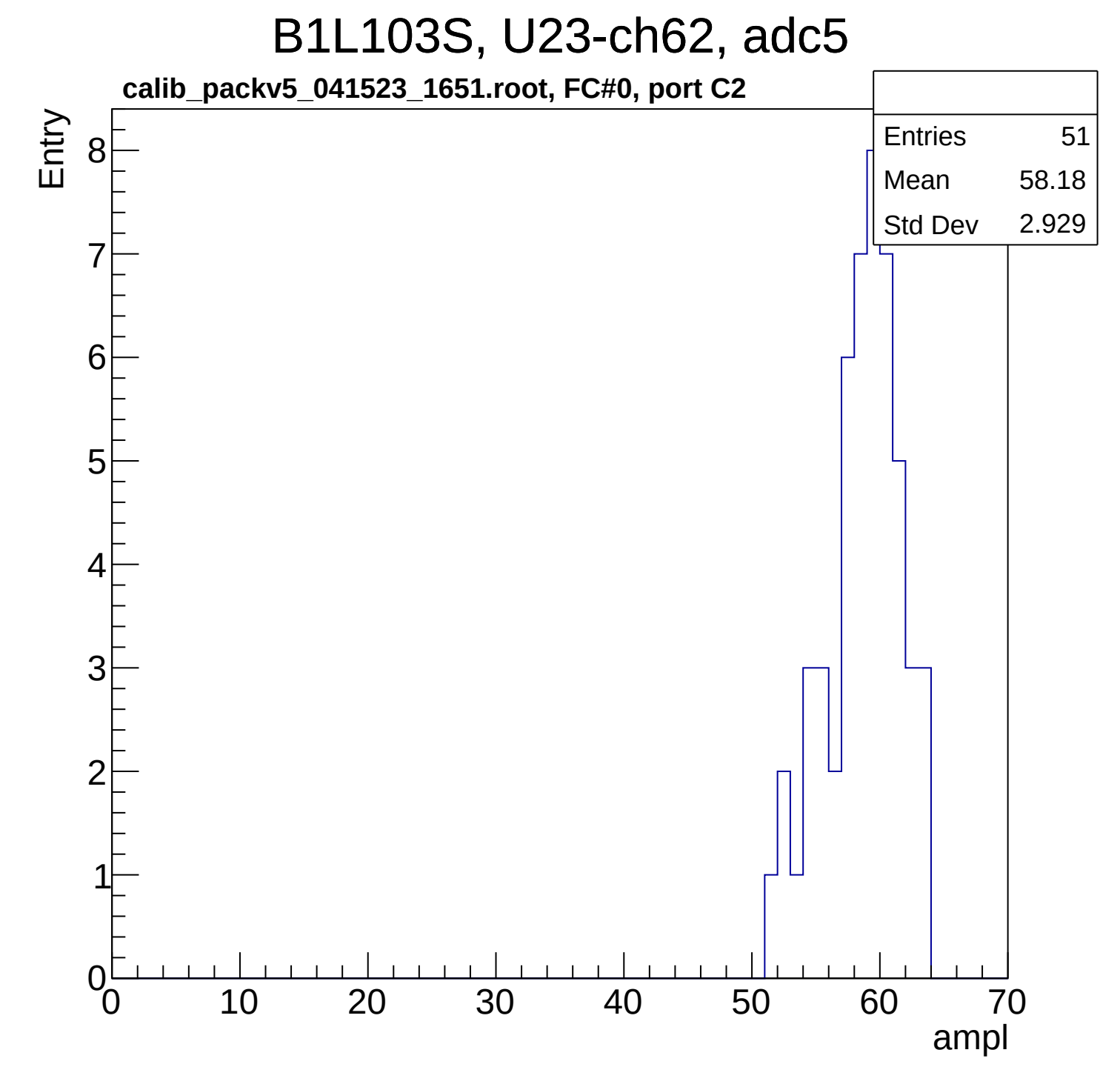

B1L103S, U23-ch62, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 21.9
Std Dev 11.75
Gaus mean : 28.4774
Gaus Width: 3.6540
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

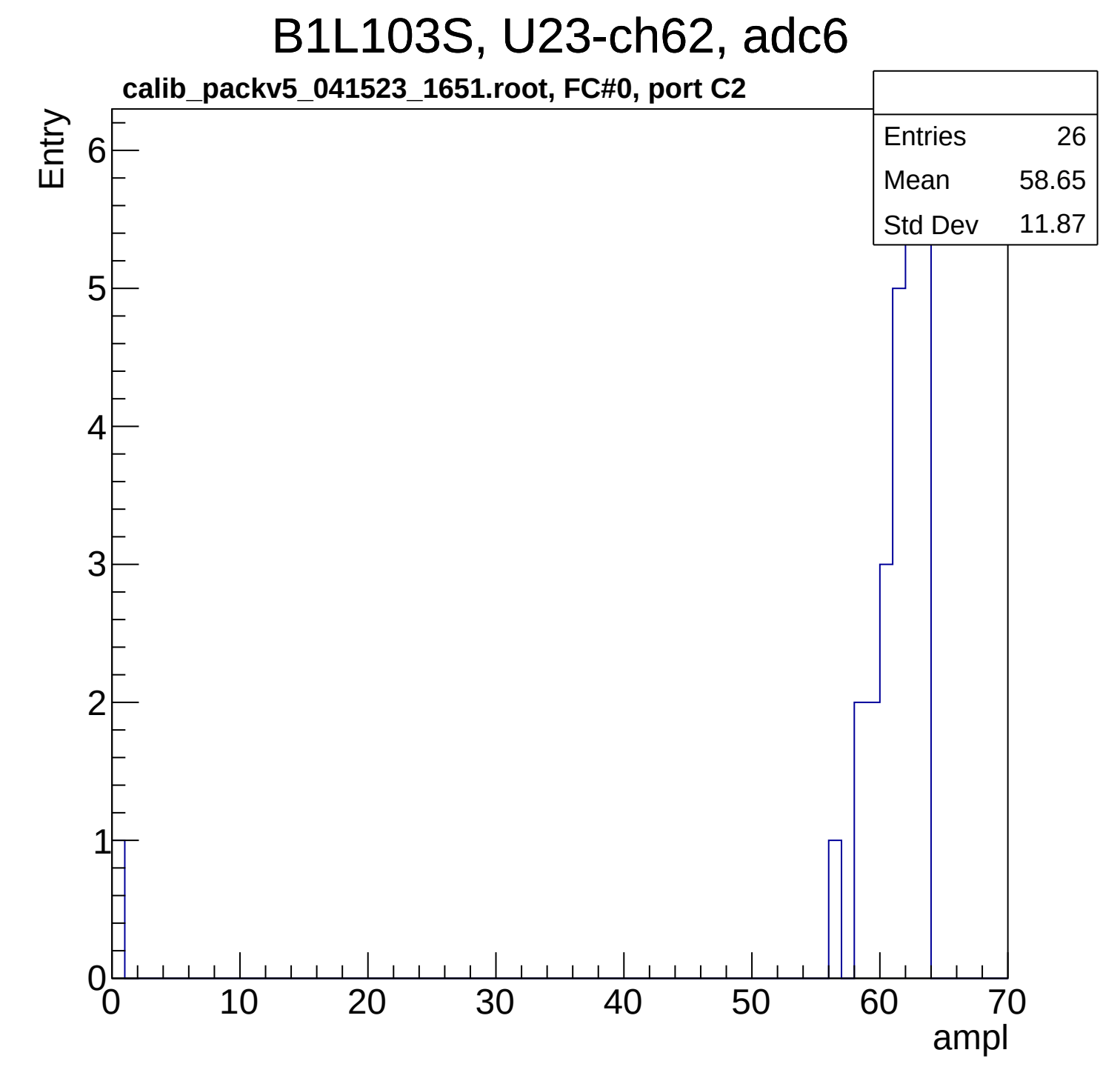

B1L103S, U23-ch62, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 31.46
Std Dev 9.986
Gaus mean : 35.1322
Gaus Width: 3.2296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

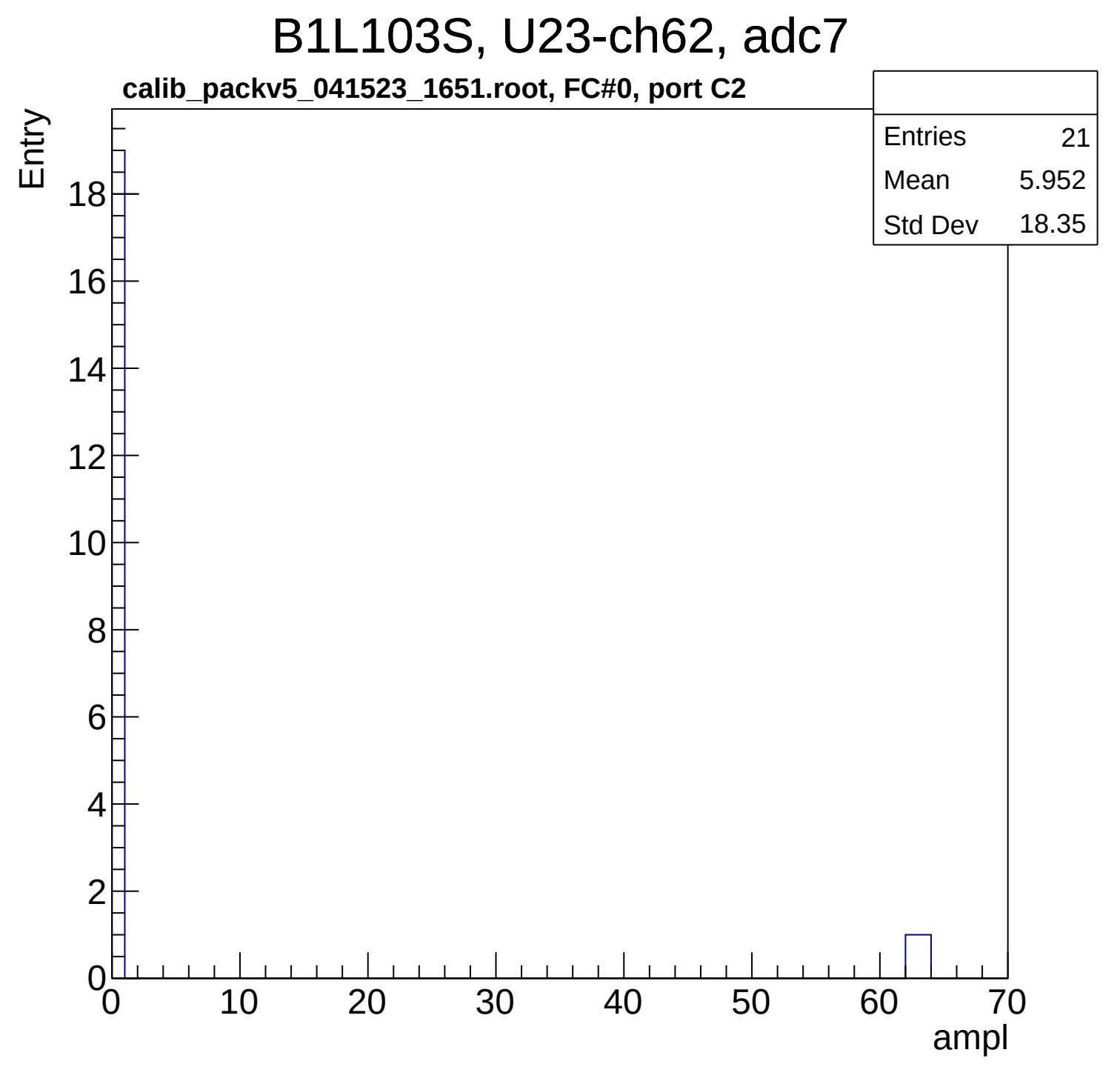

B1L103S, U23-ch62, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 33.28
Std Dev 15.82
Gaus mean : 40.9441
Gaus Width: 2.8657
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

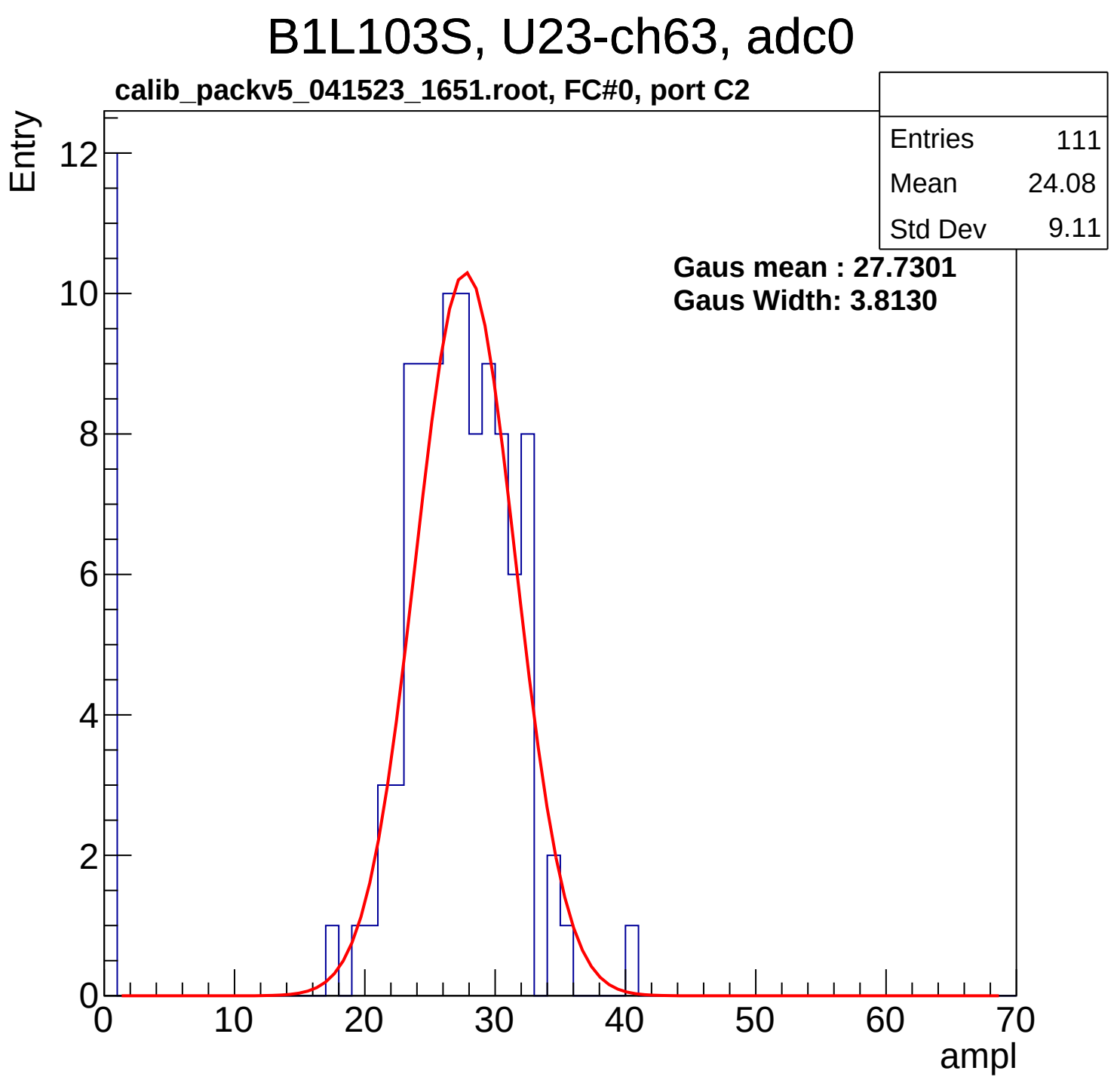

B1L103S, U23-ch62, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 46.97
Std Dev 3.731
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

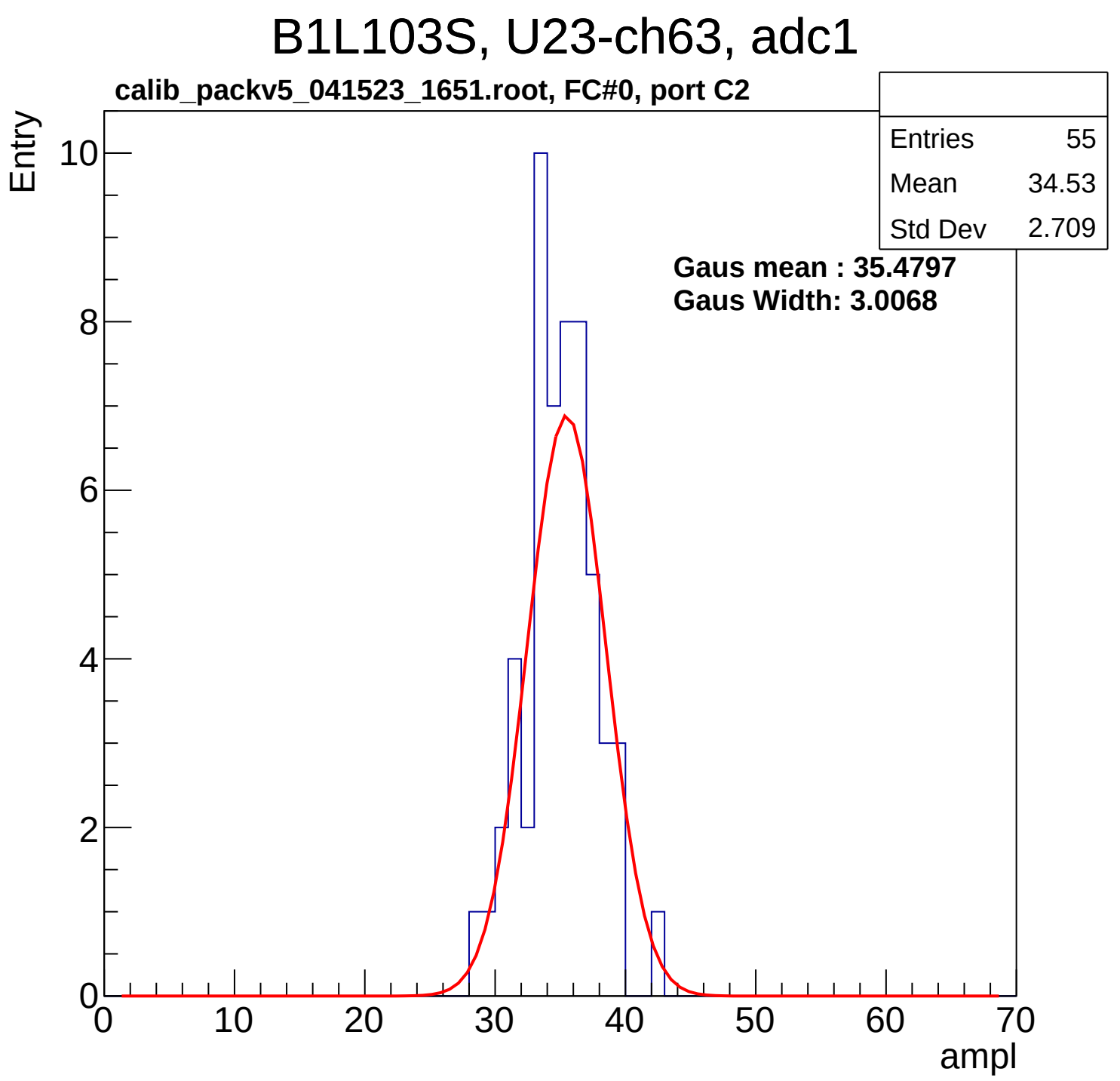

B1L103S, U23-ch62, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 50
Mean 52.78
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

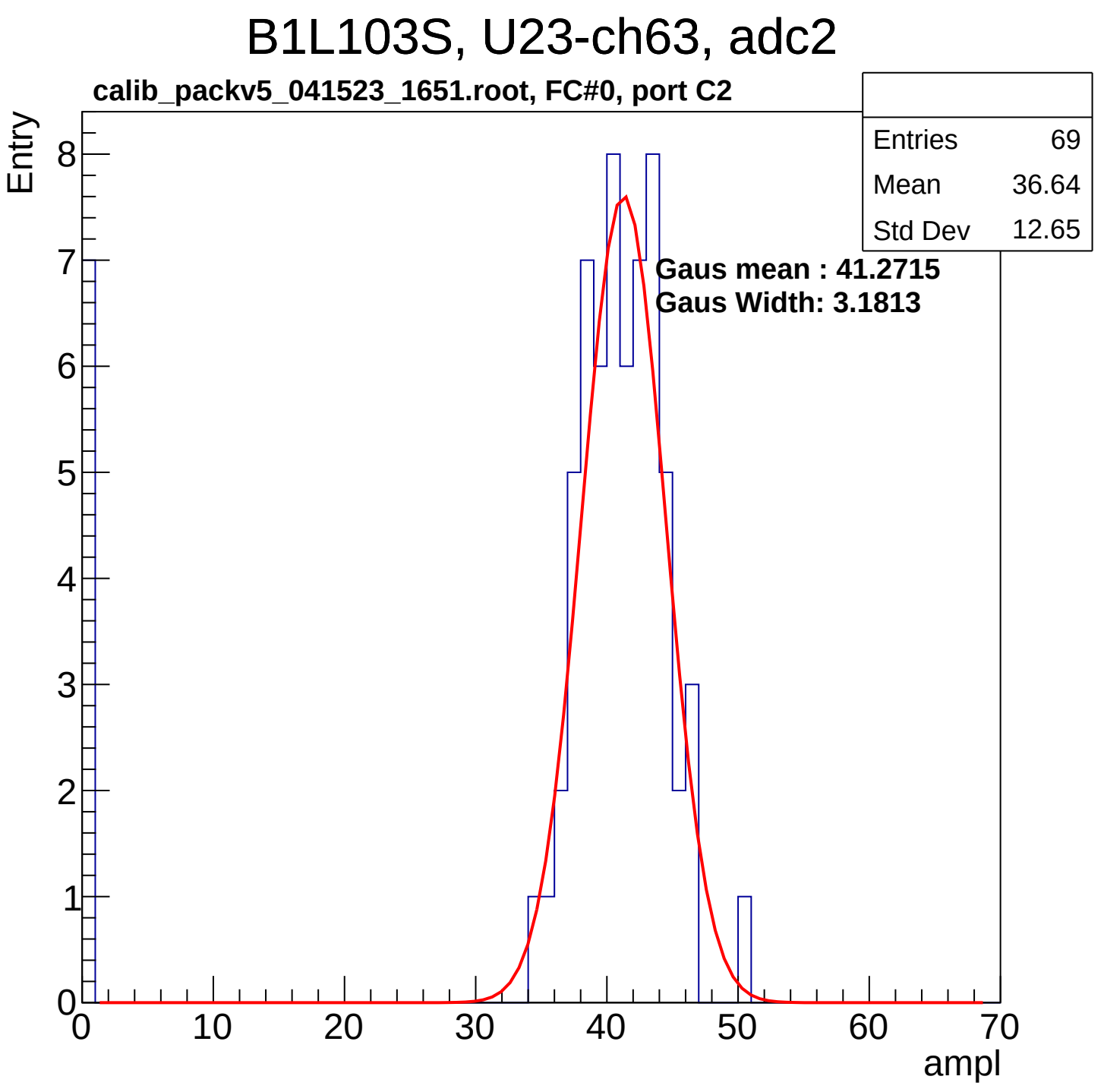

B1L103S, U23-ch62, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 58.18
Std Dev 2.929
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

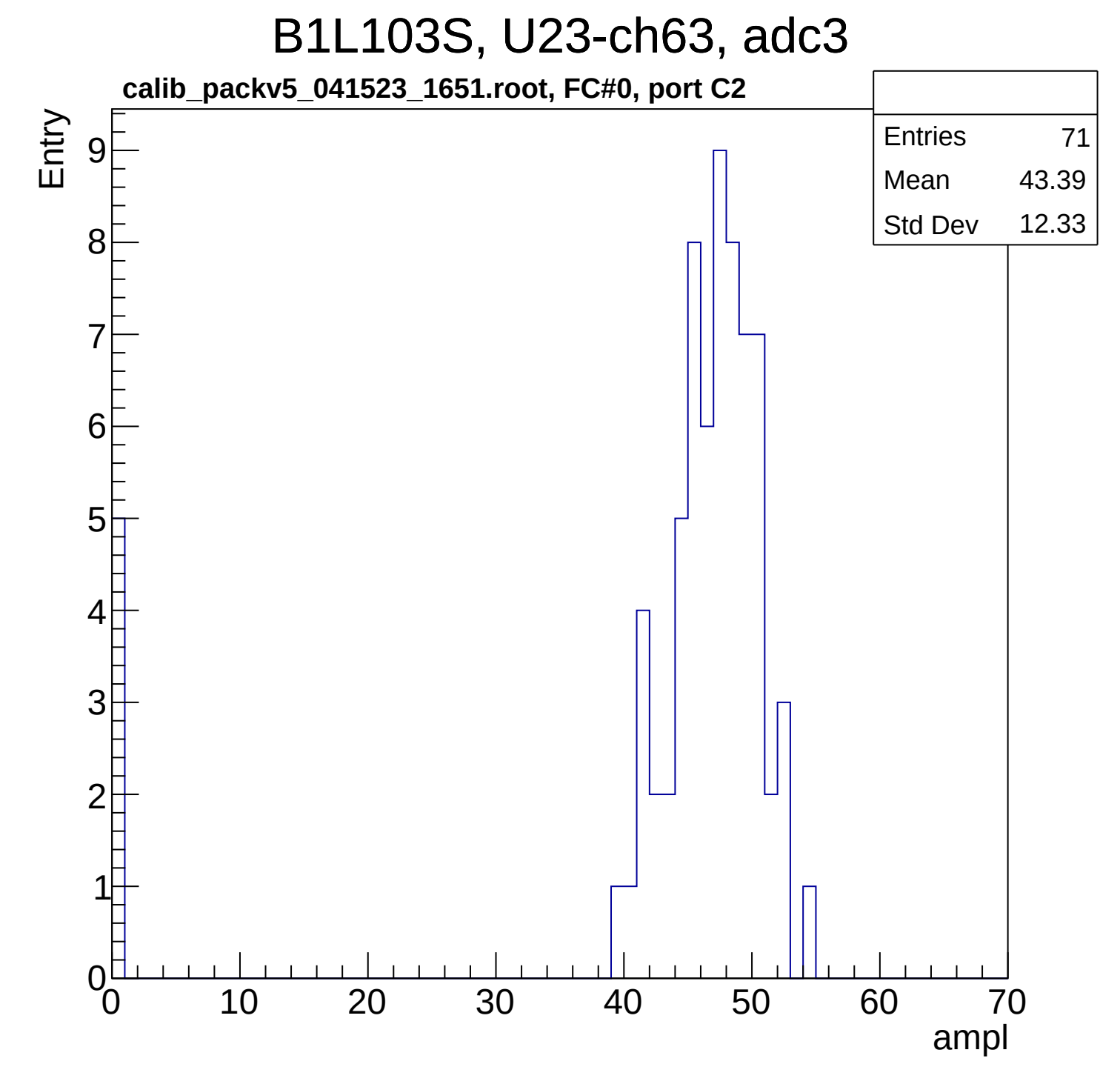

B1L103S, U23-ch62, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 26
Mean 58.65
Std Dev 11.87
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

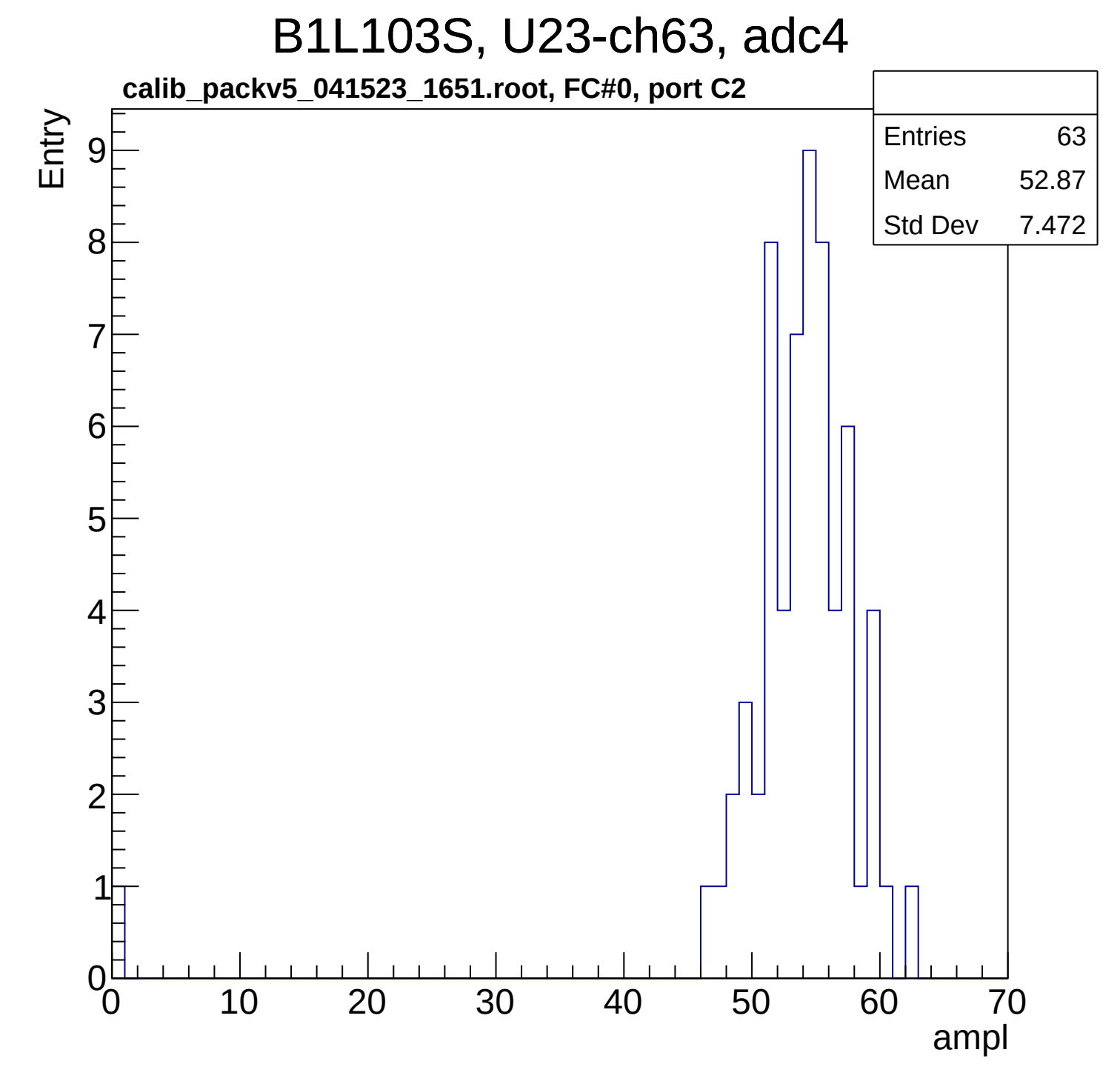

B1L103S, U23-ch62, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.952
Std Dev 18.35
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

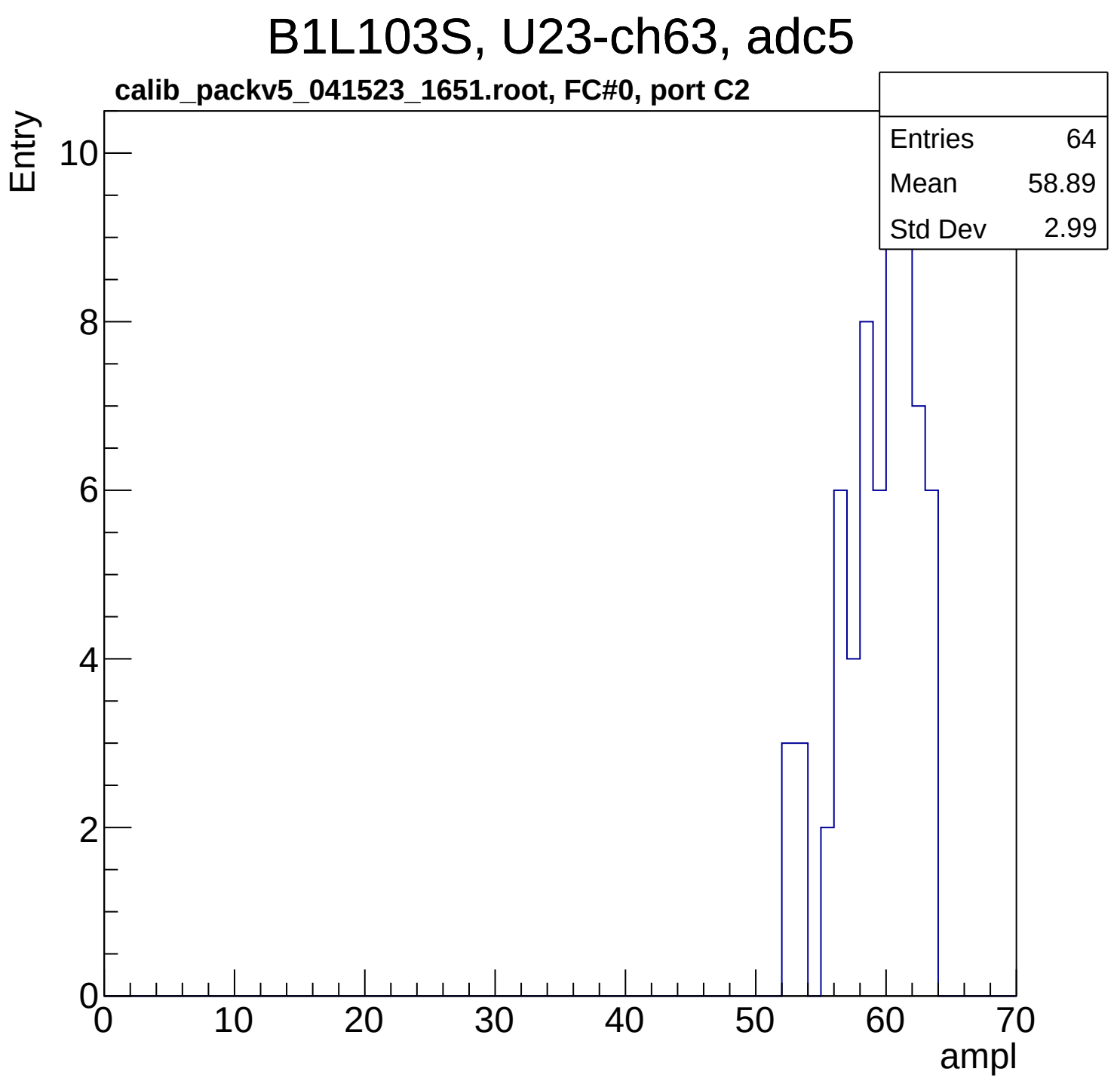

B1L103S, U23-ch63, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 111
Mean 24.08
Std Dev 9.11
Gaus mean : 27.7301
Gaus Width: 3.8130
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

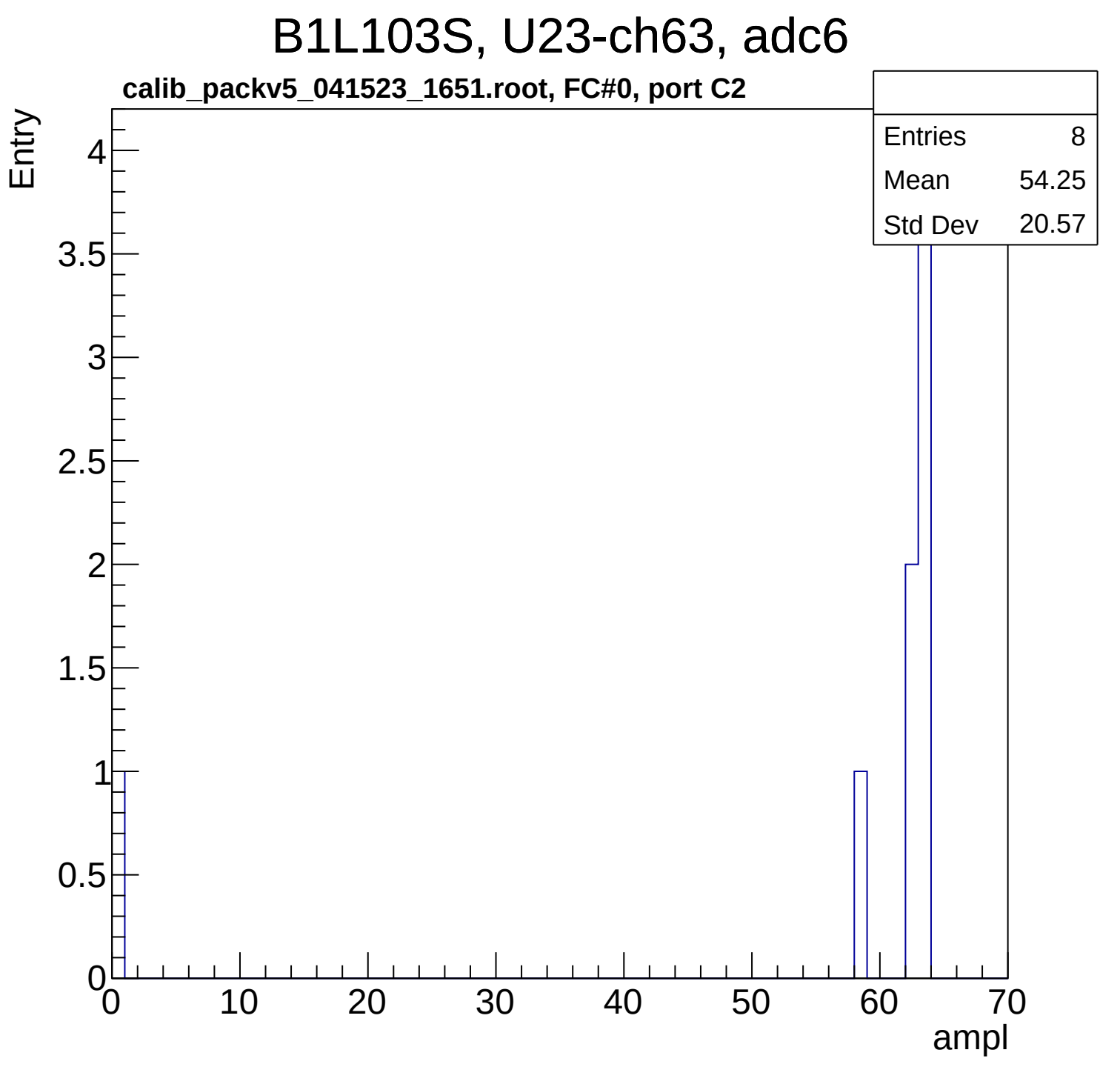

B1L103S, U23-ch63, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 55
Mean 34.53
Std Dev 2.709
Gaus mean : 35.4797
Gaus Width: 3.0068
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

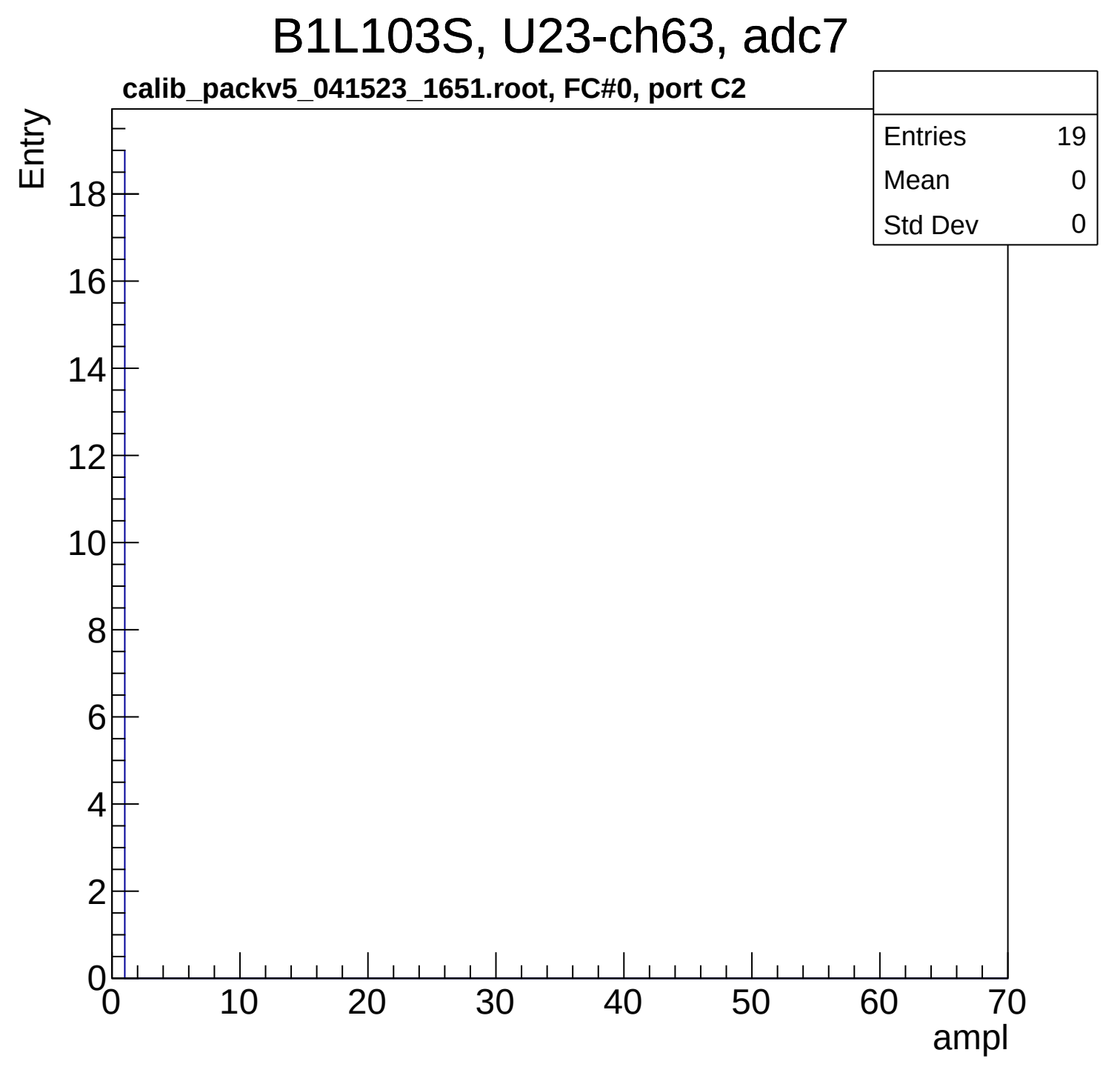

B1L103S, U23-ch63, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 36.64
Std Dev 12.65
Gaus mean : 41.2715
Gaus Width: 3.1813
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

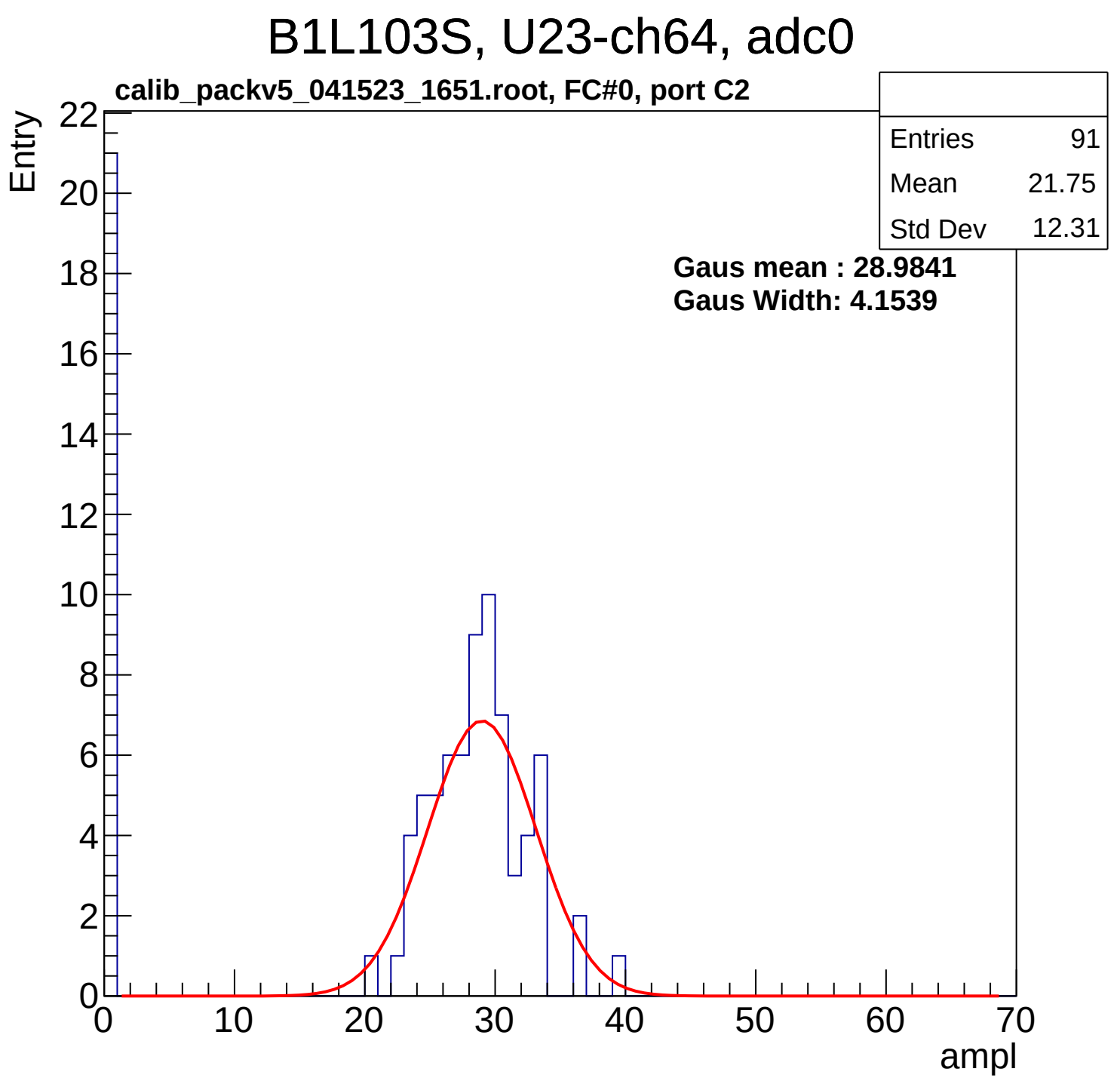

B1L103S, U23-ch63, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 43.39
Std Dev 12.33
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

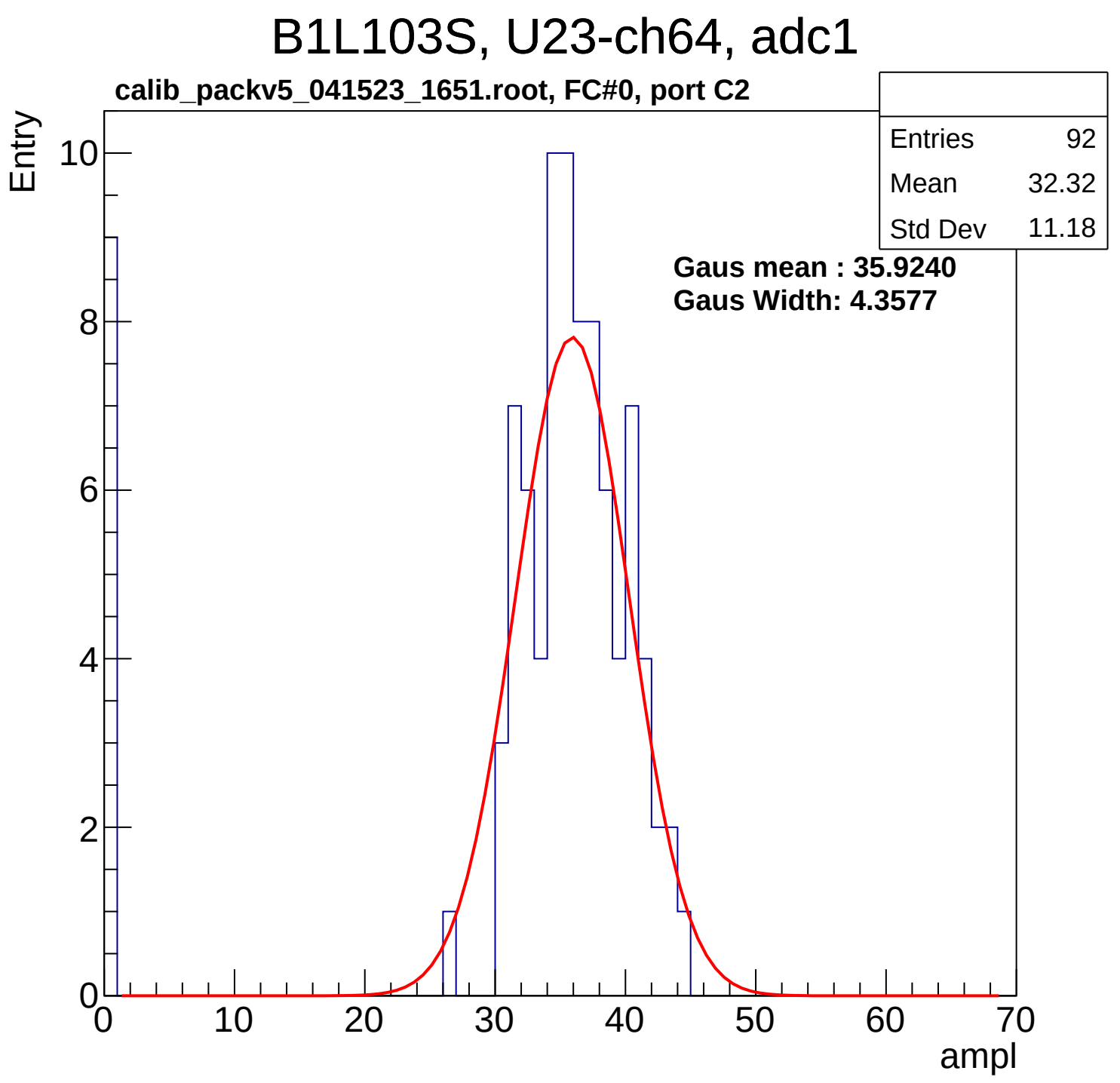

B1L103S, U23-ch63, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 52.87
Std Dev 7.472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

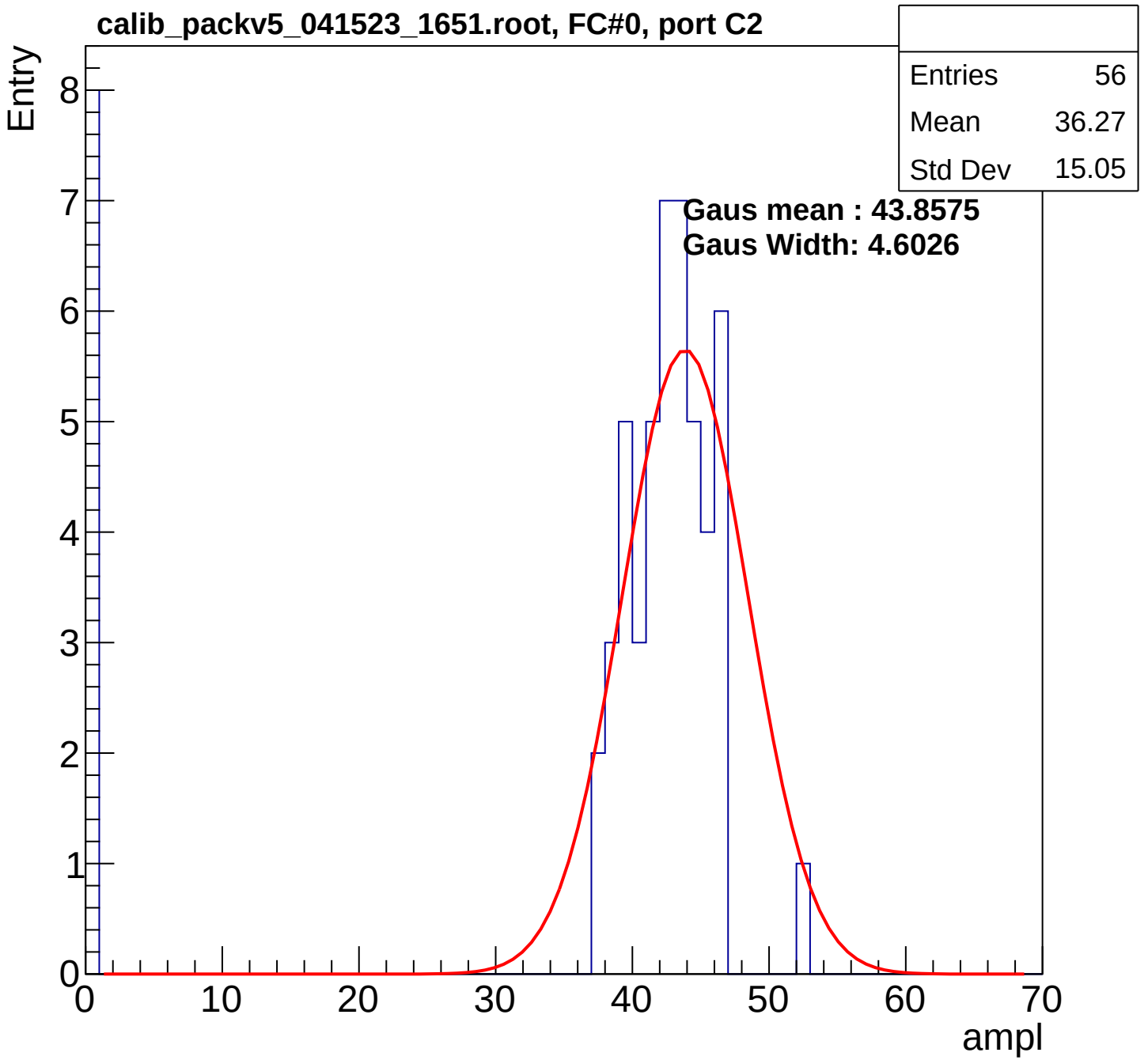

B1L103S, U23-ch63, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 58.89
Std Dev 2.99
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

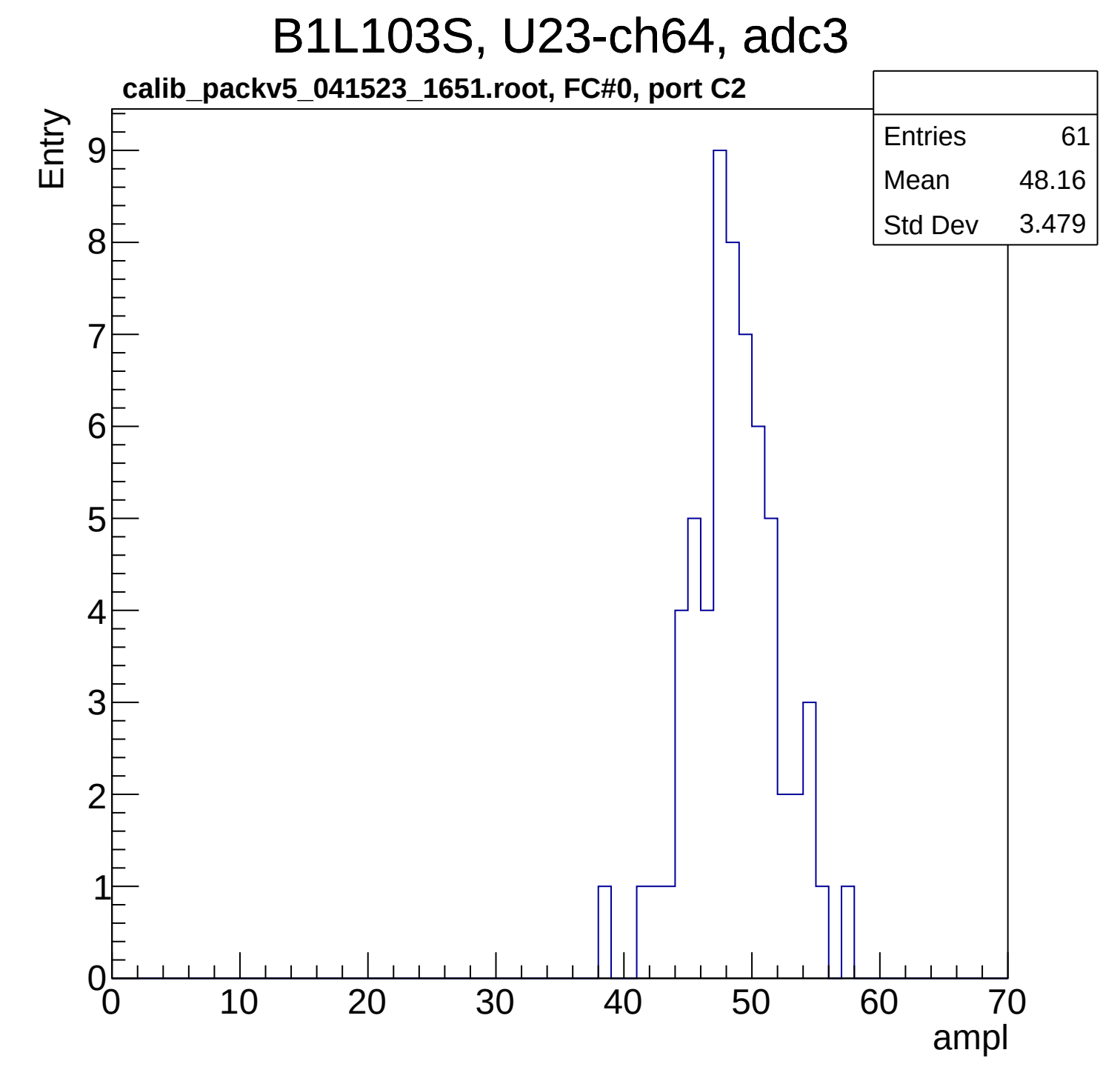

B1L103S, U23-ch63, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 54.25
Std Dev 20.57
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

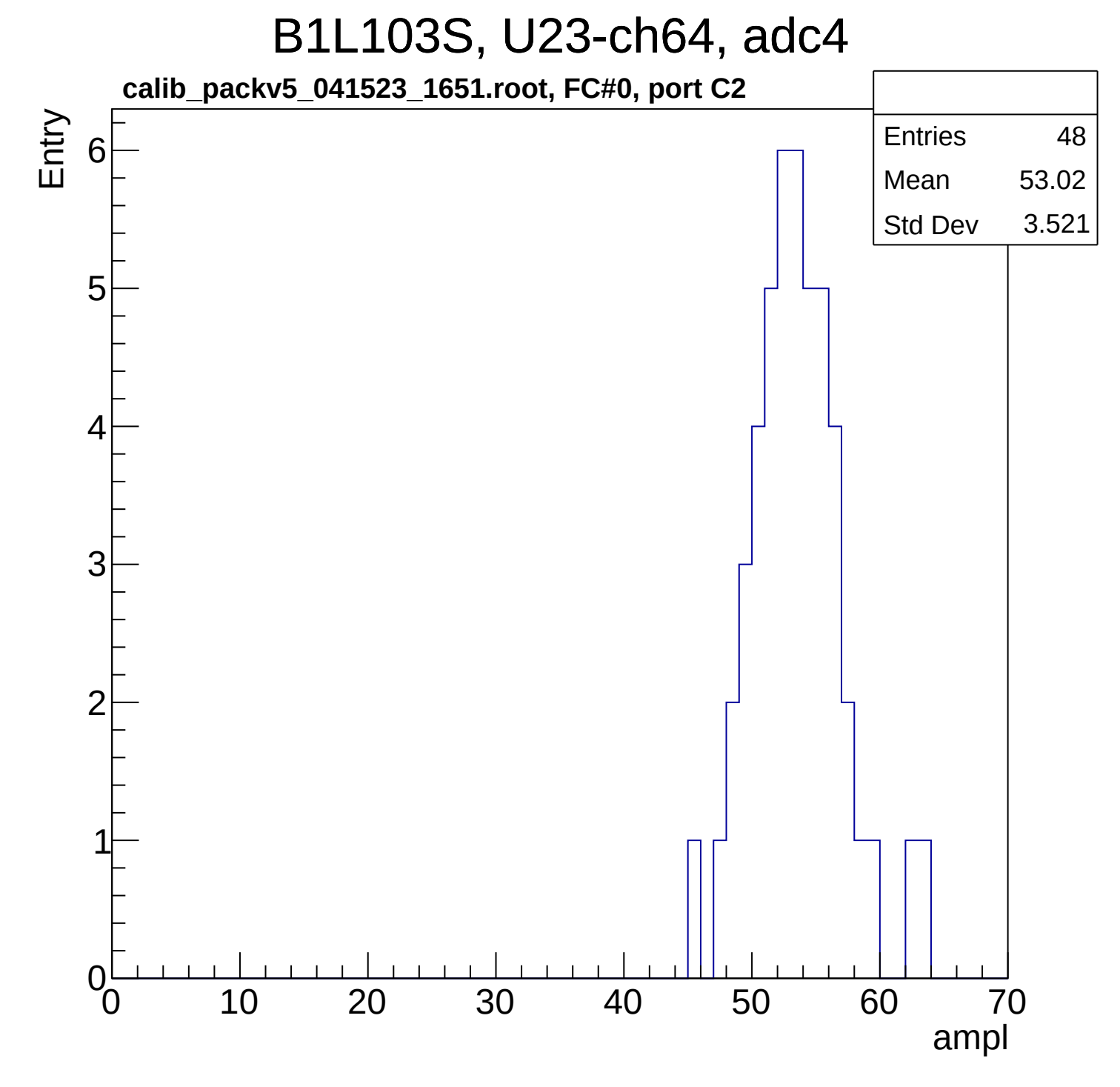

B1L103S, U23-ch63, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

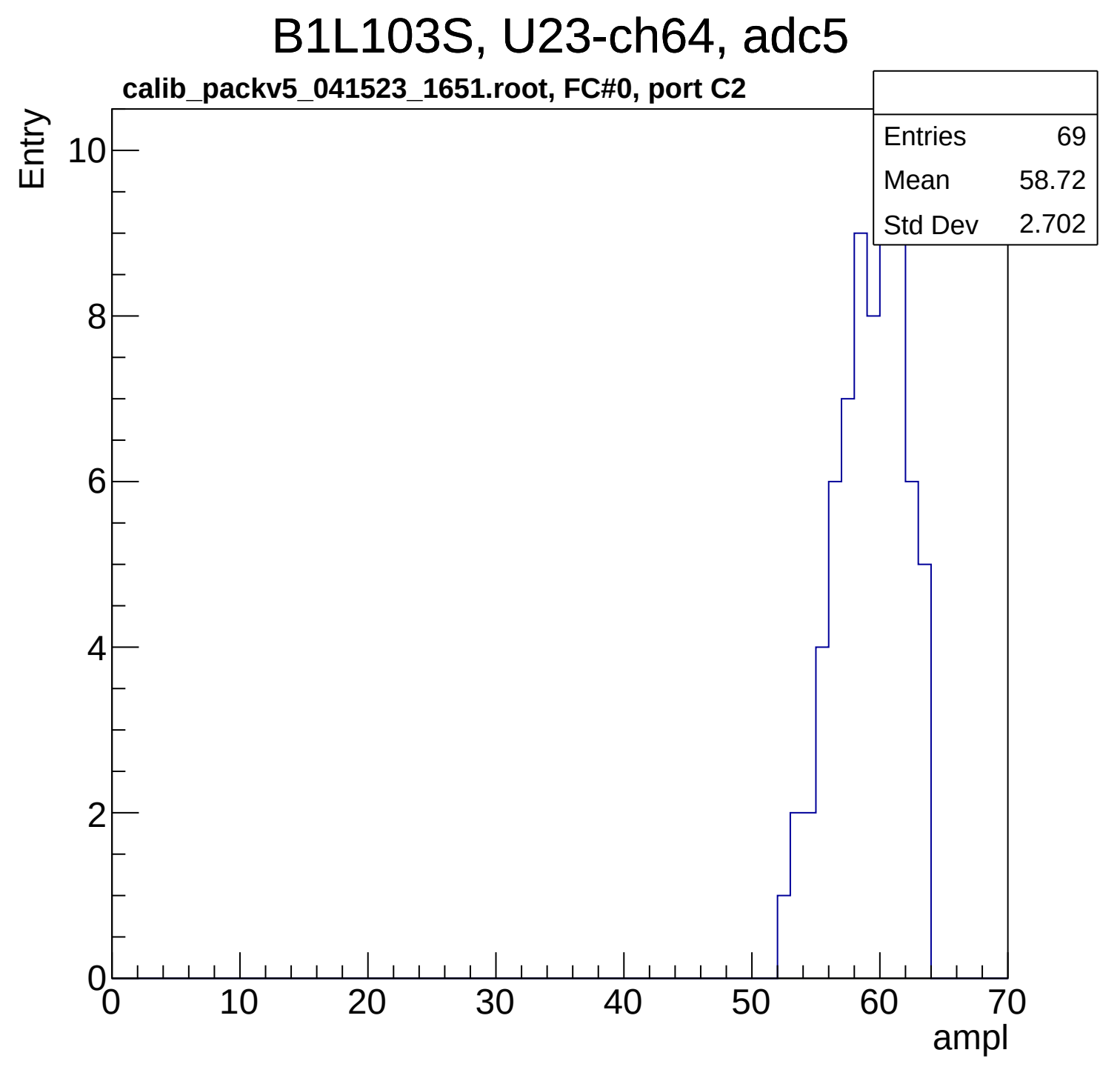

B1L103S, U23-ch64, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 21.75
Std Dev 12.31
Gaus mean : 28.9841
Gaus Width: 4.1539
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

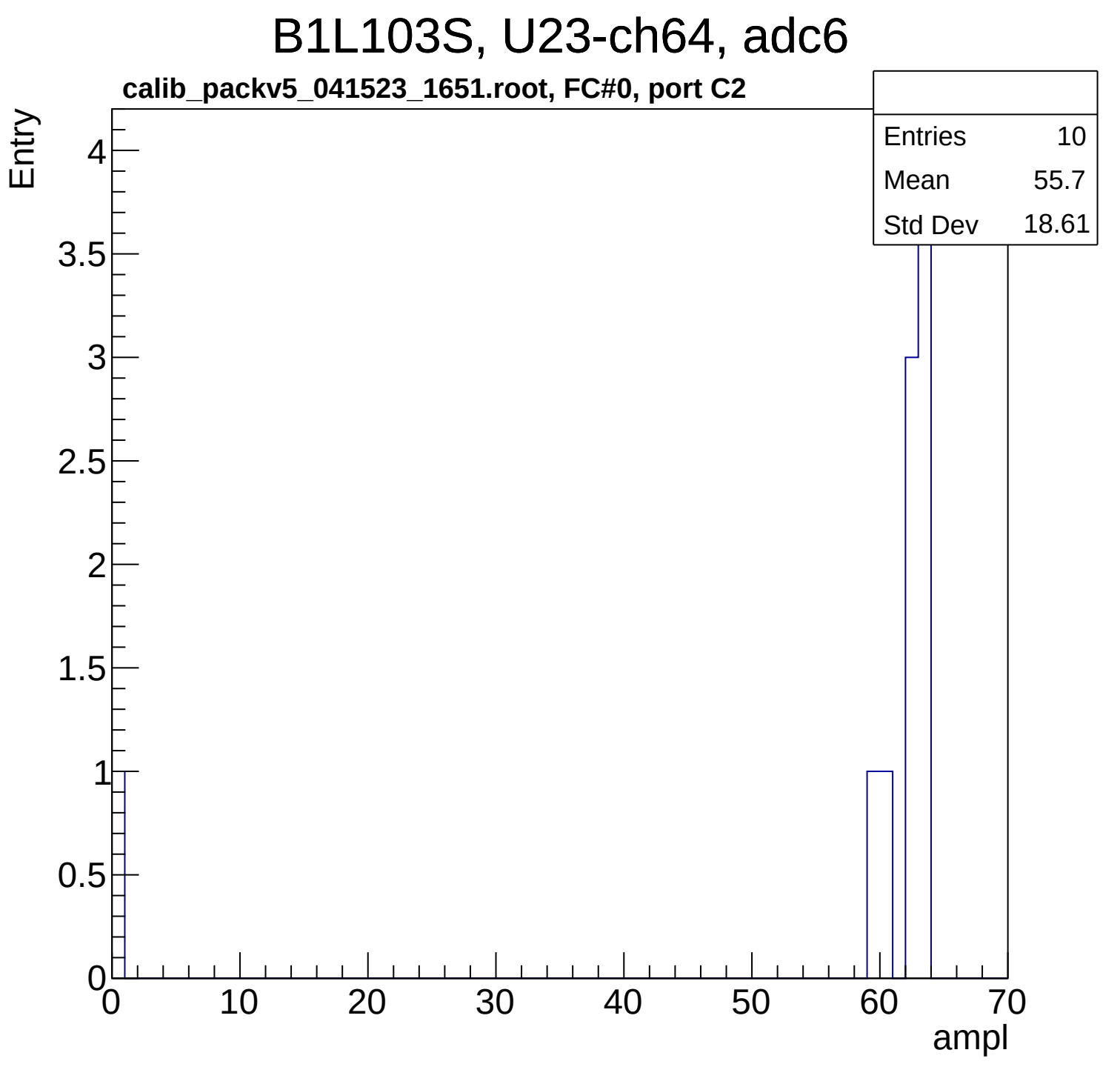

B1L103S, U23-ch64, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 92
Mean 32.32
Std Dev 11.18
Gaus mean : 35.9240
Gaus Width: 4.3577
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

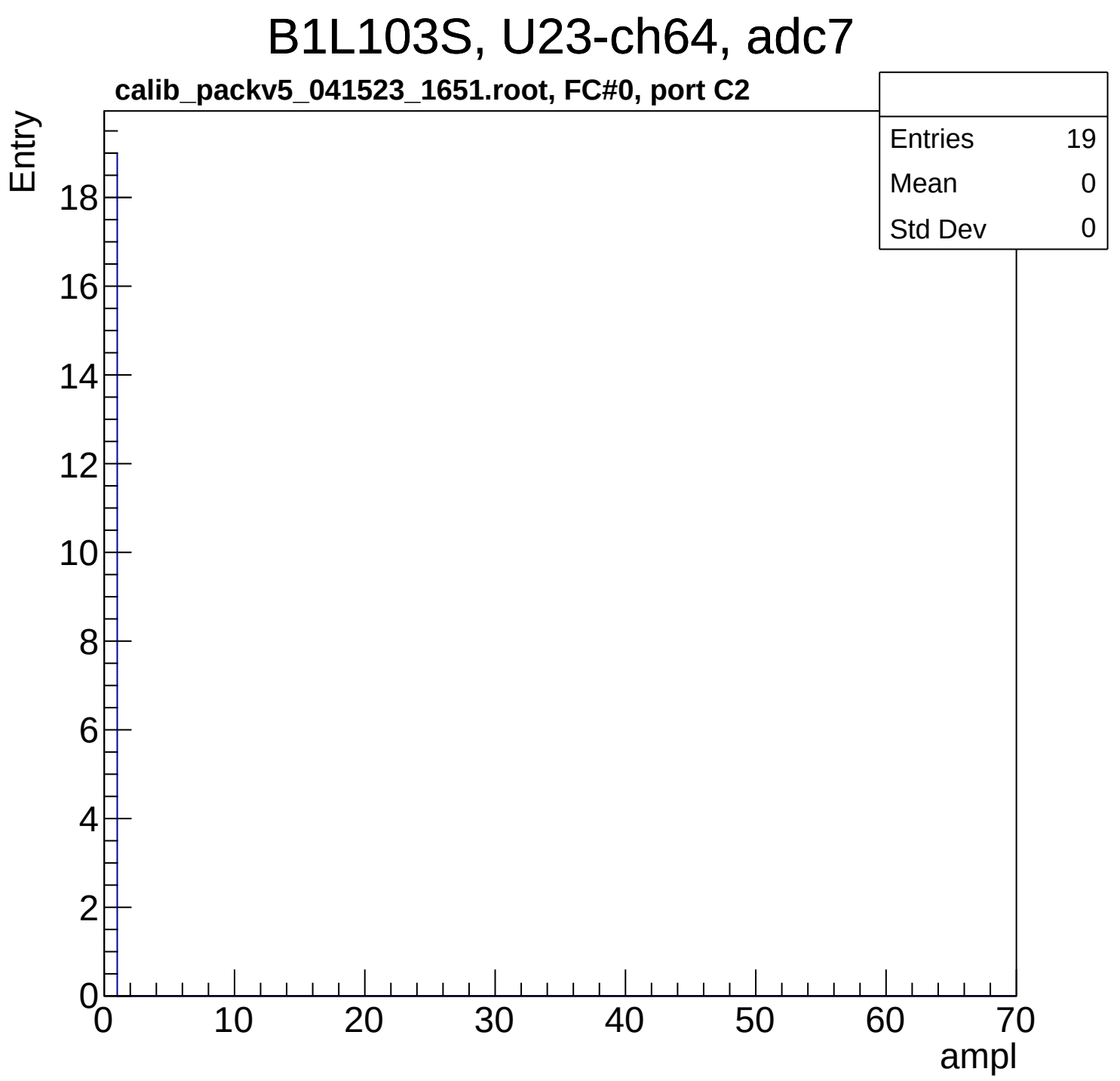

B1L103S, U23-ch64, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 36.27
Std Dev 15.05
Gaus mean : 43.8575
Gaus Width: 4.6026
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

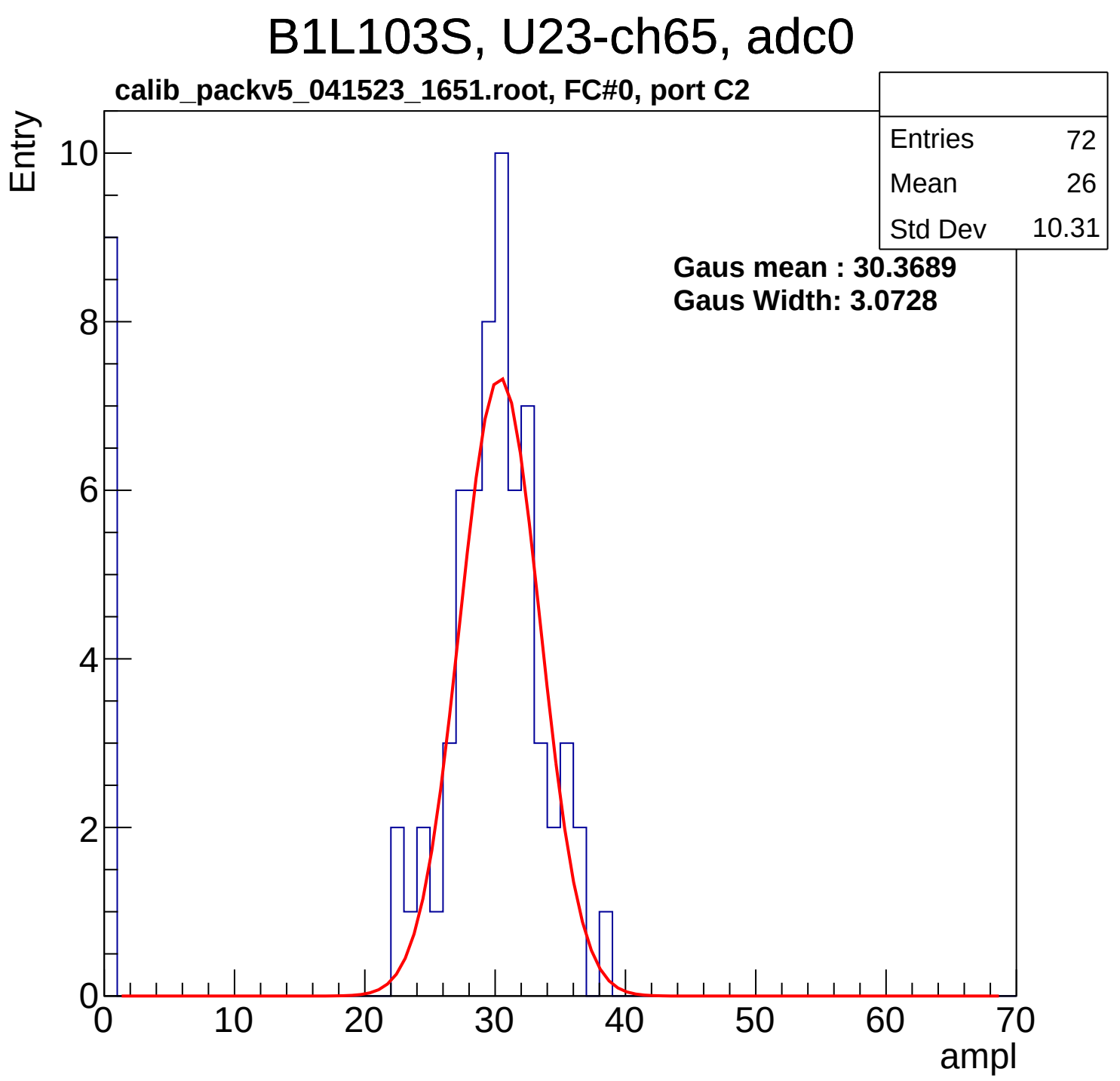

B1L103S, U23-ch64, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 48.16
Std Dev 3.479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

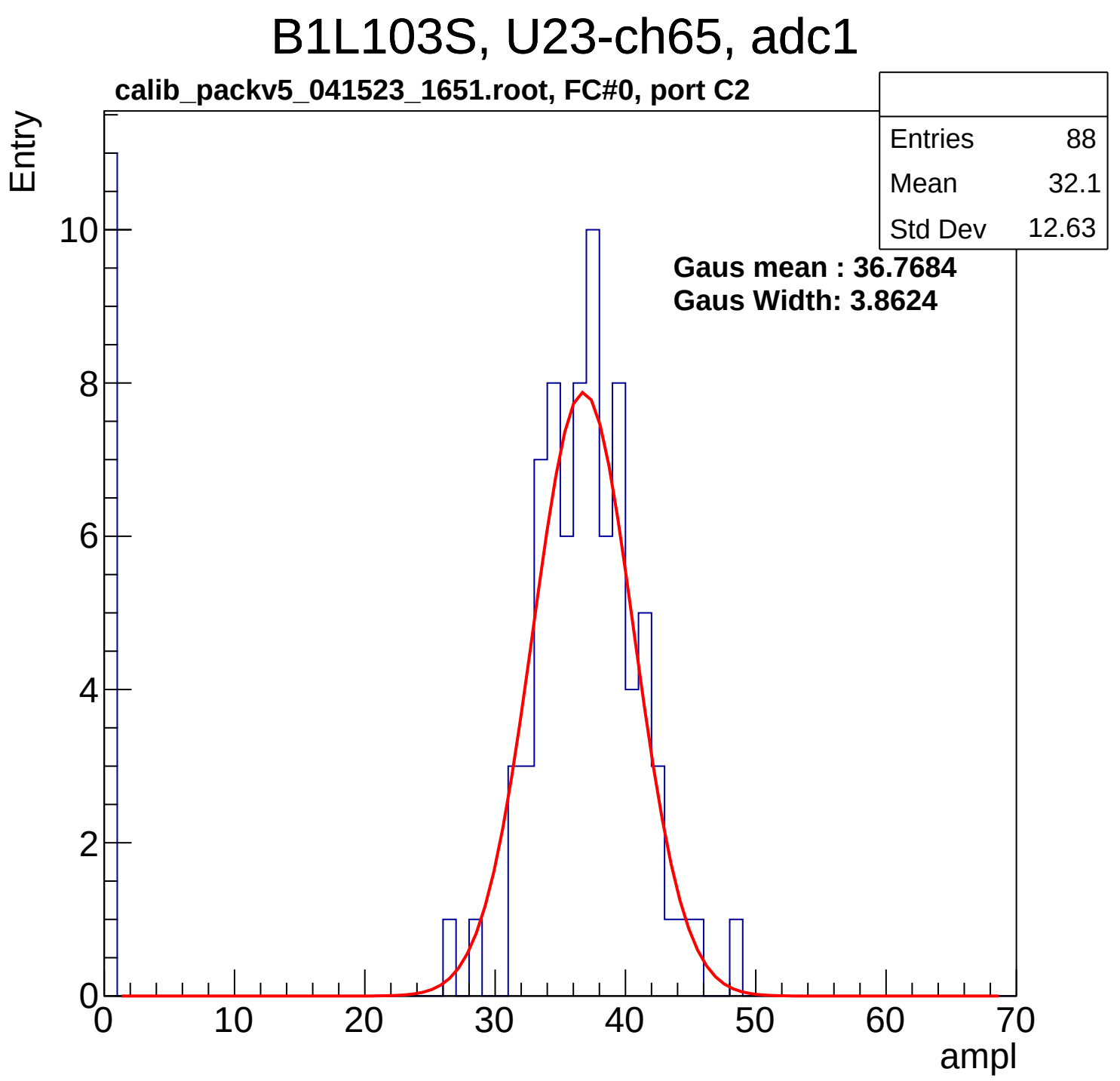

B1L103S, U23-ch64, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 53.02
Std Dev 3.521
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

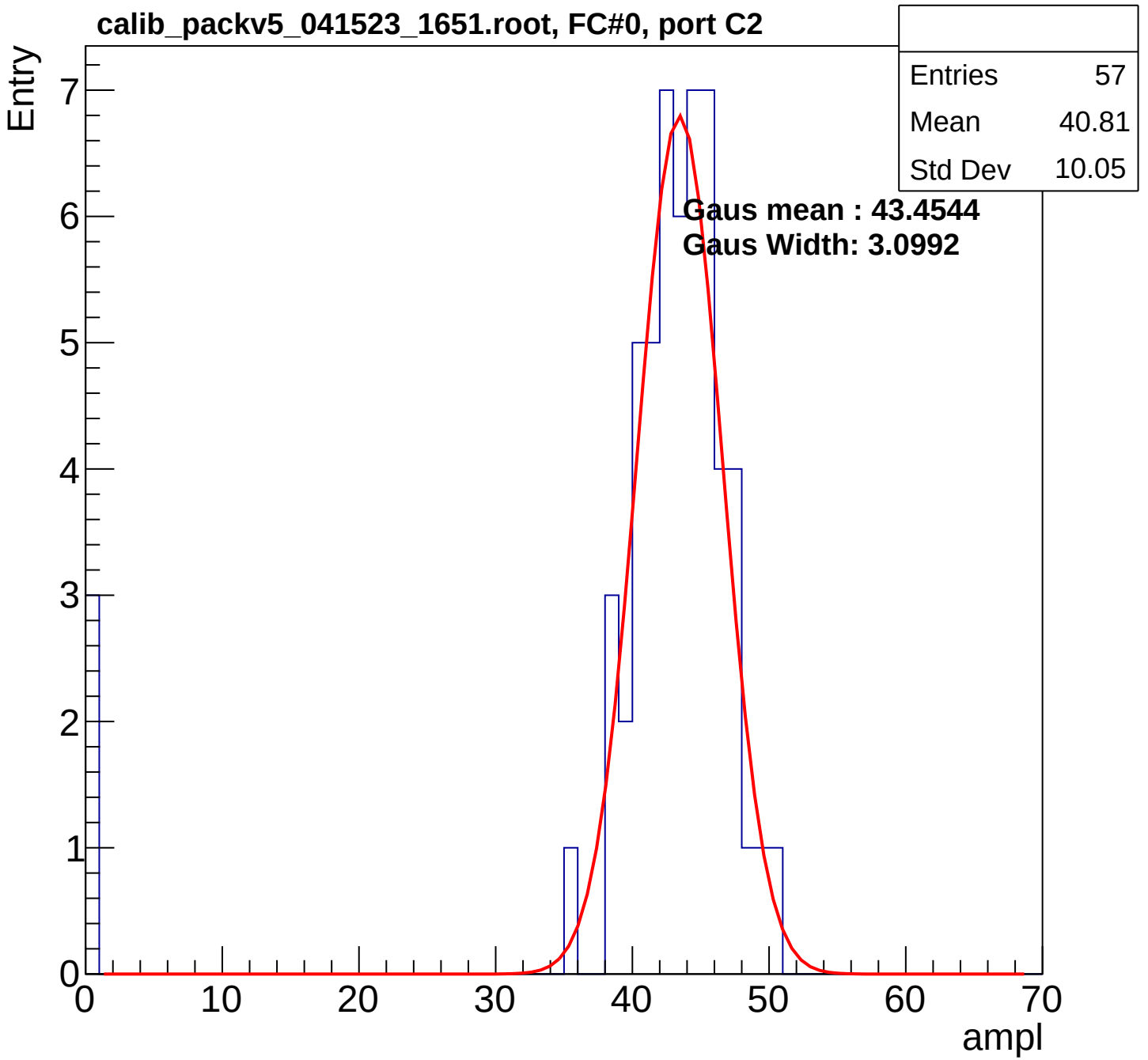

B1L103S, U23-ch64, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 58.72
Std Dev 2.702
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

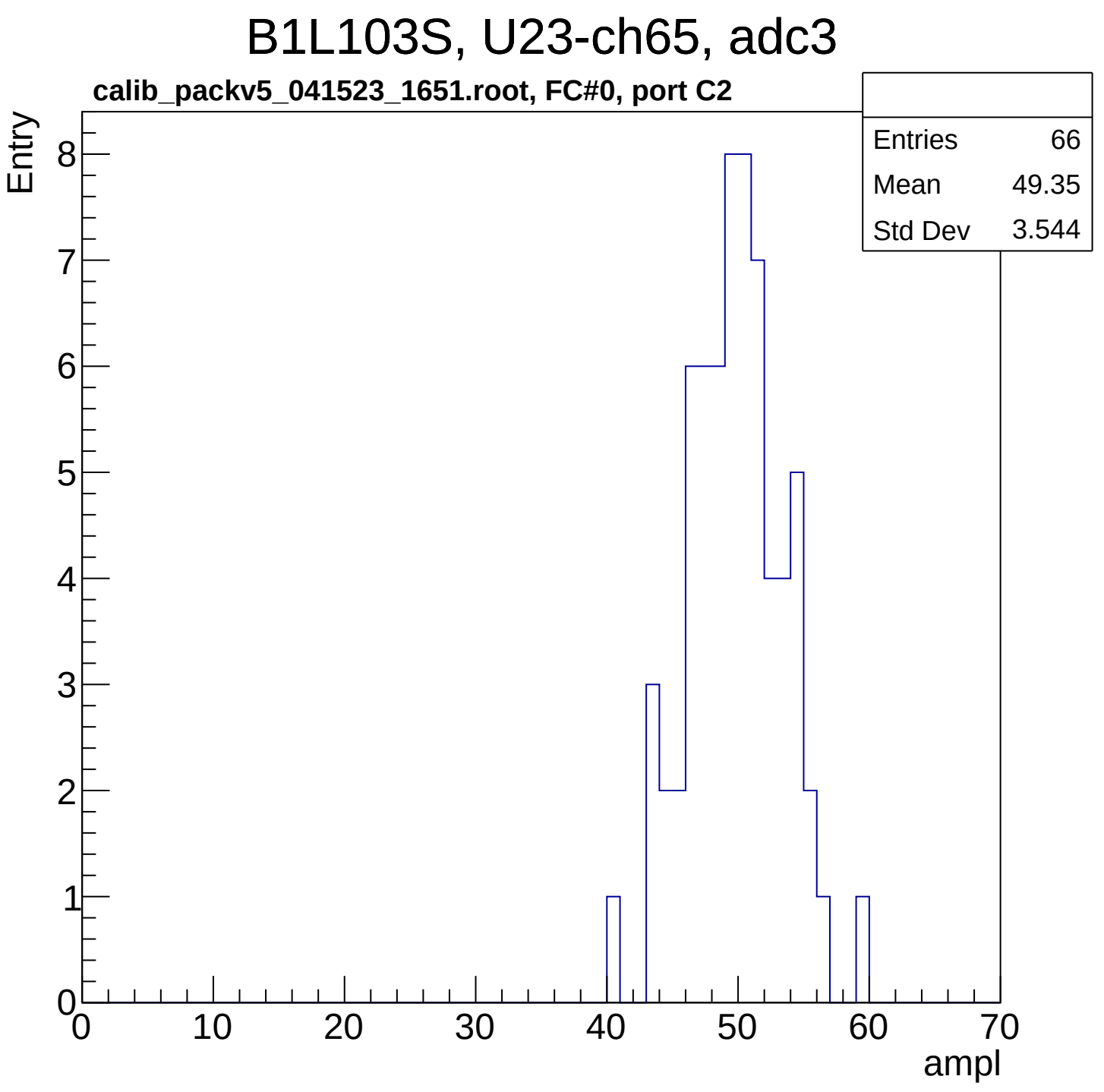

B1L103S, U23-ch64, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 55.7
Std Dev 18.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

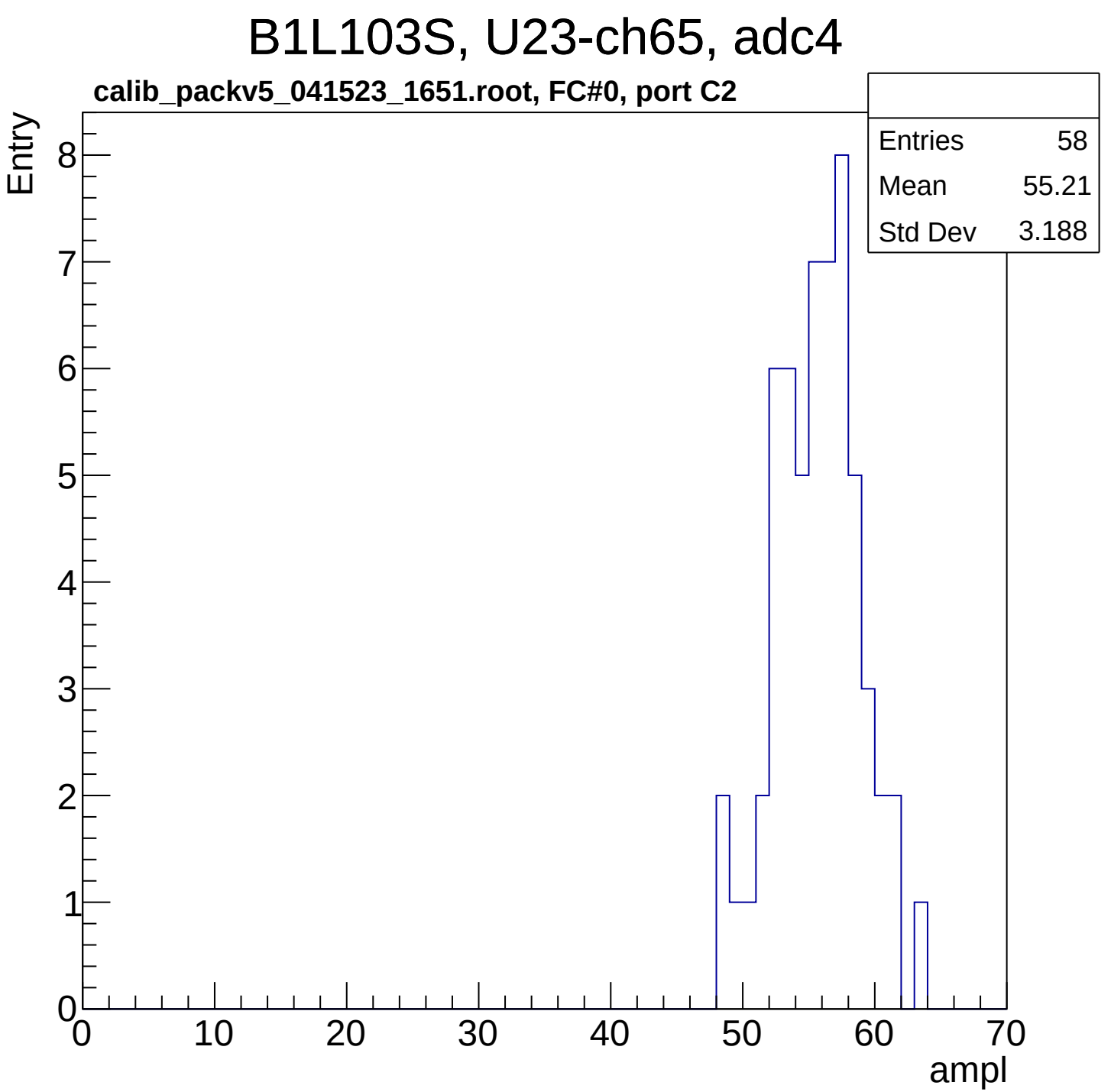

B1L103S, U23-ch64, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

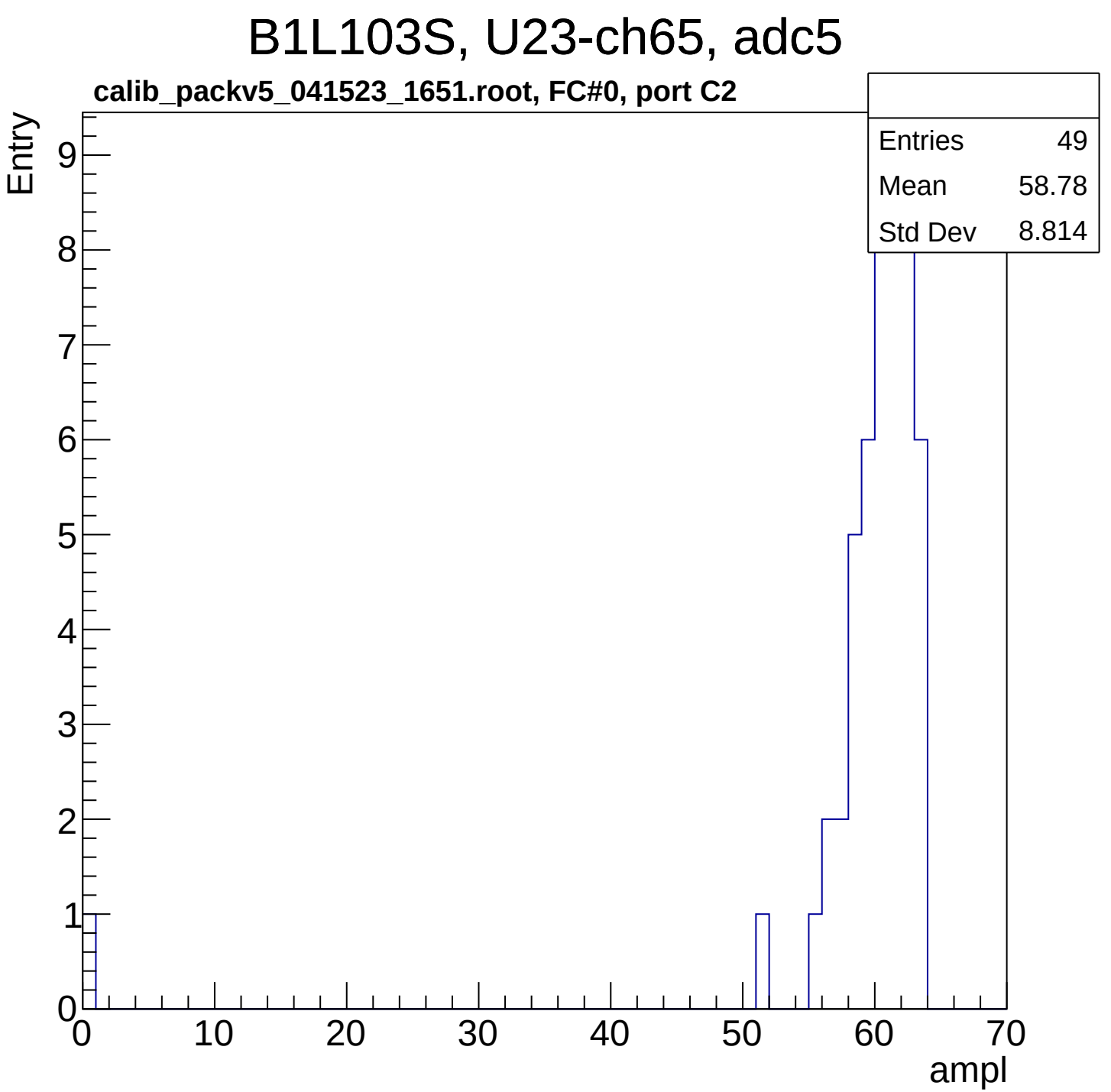

B1L103S, U23-ch65, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 26
Std Dev 10.31
Gaus mean : 30.3689
Gaus Width: 3.0728
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

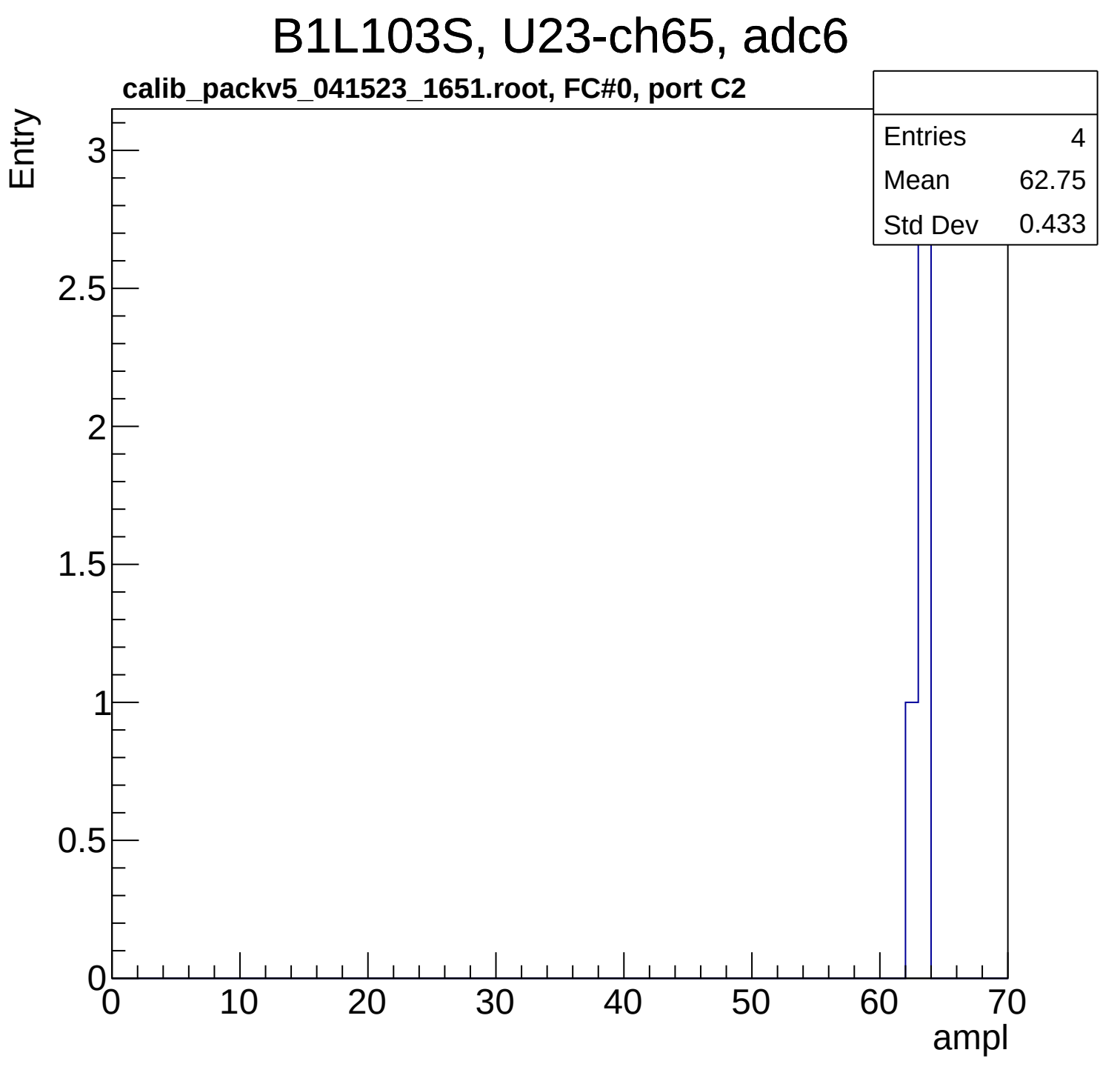

B1L103S, U23-ch65, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 88
Mean 32.1
Std Dev 12.63
Gaus mean : 36.7684
Gaus Width: 3.8624
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

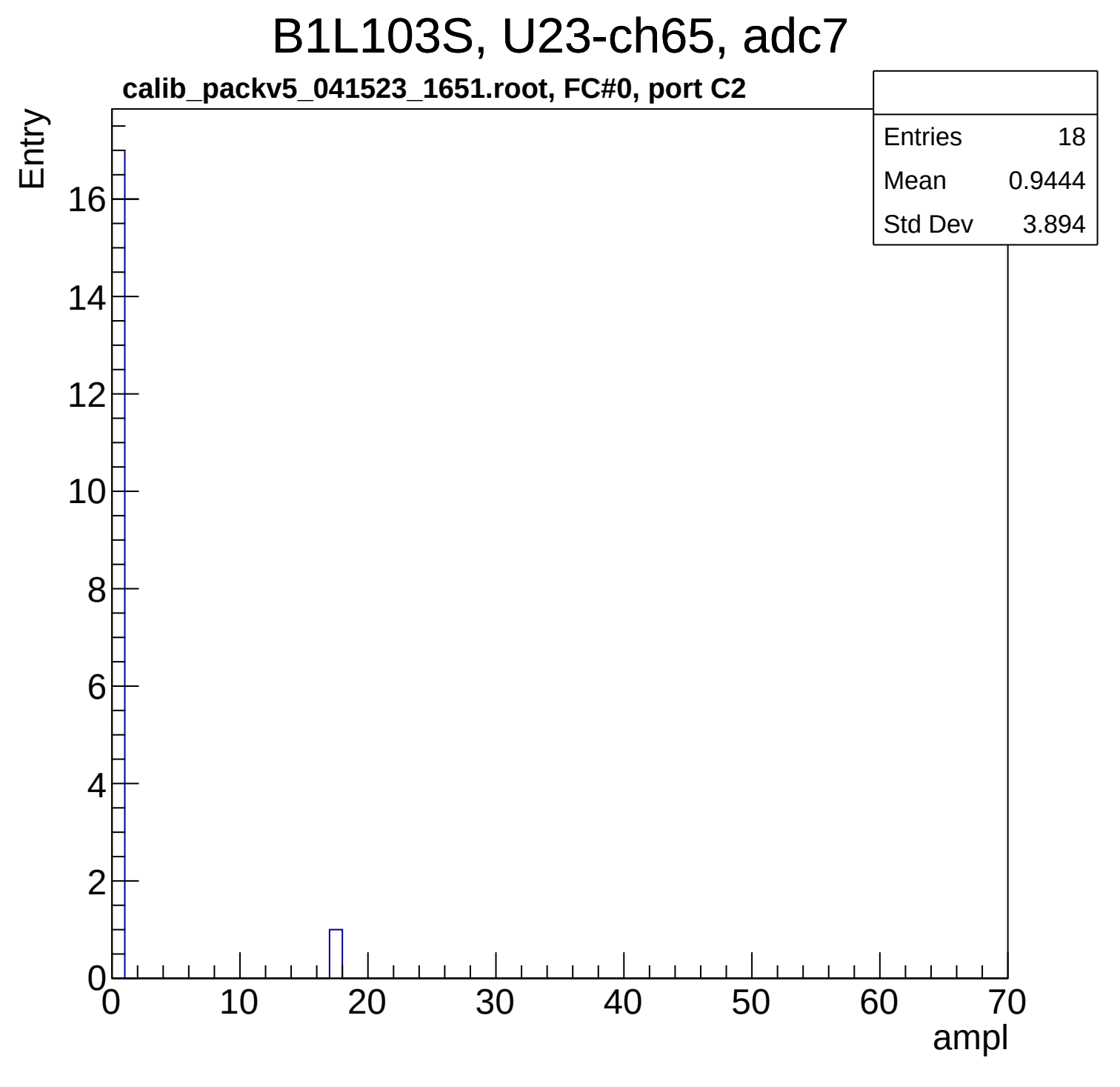

B1L103S, U23-ch65, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 40.81
Std Dev 10.05
Gaus mean : 43.4544
Gaus Width: 3.0992
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

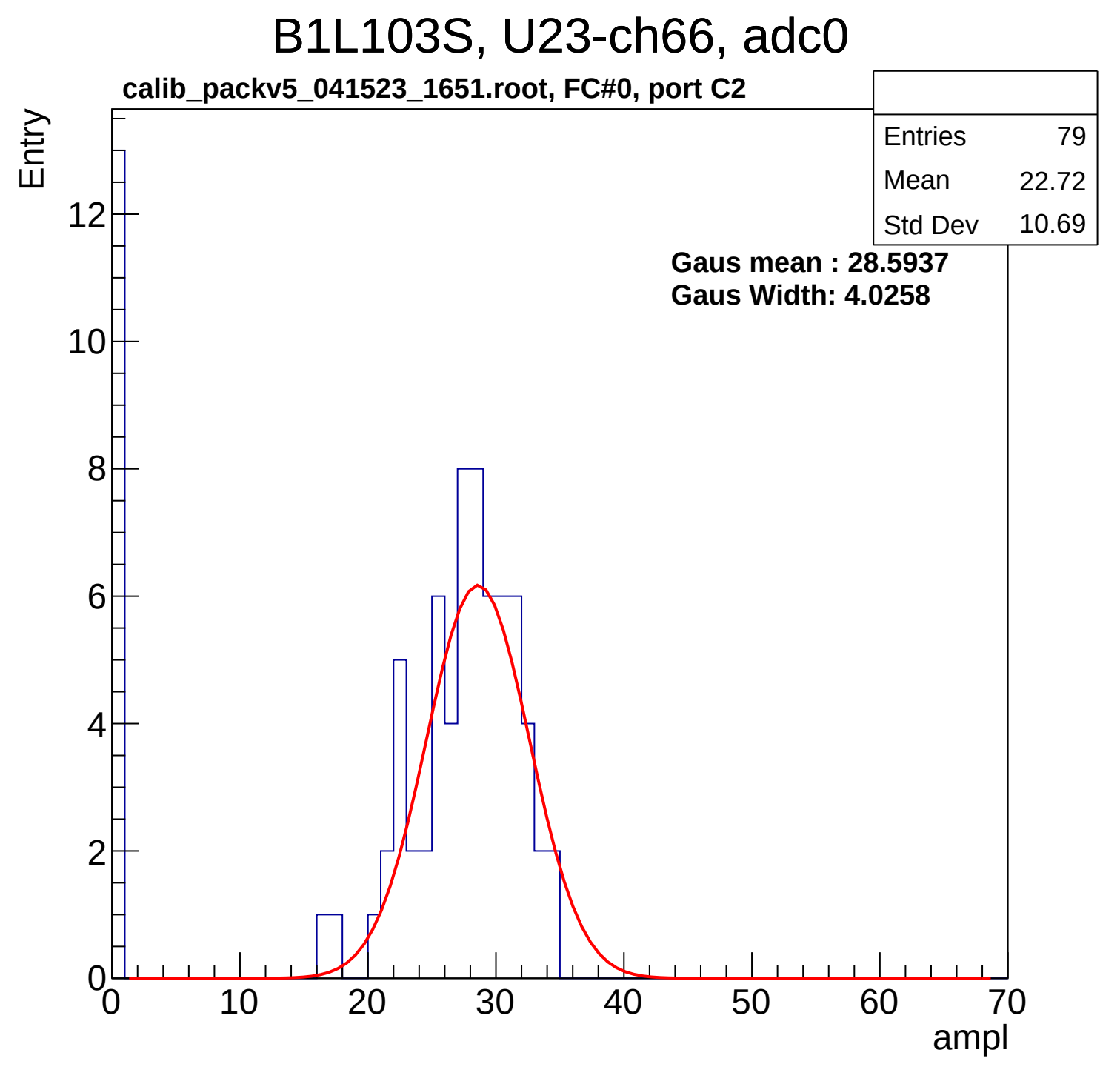

B1L103S, U23-ch65, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 49.35
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

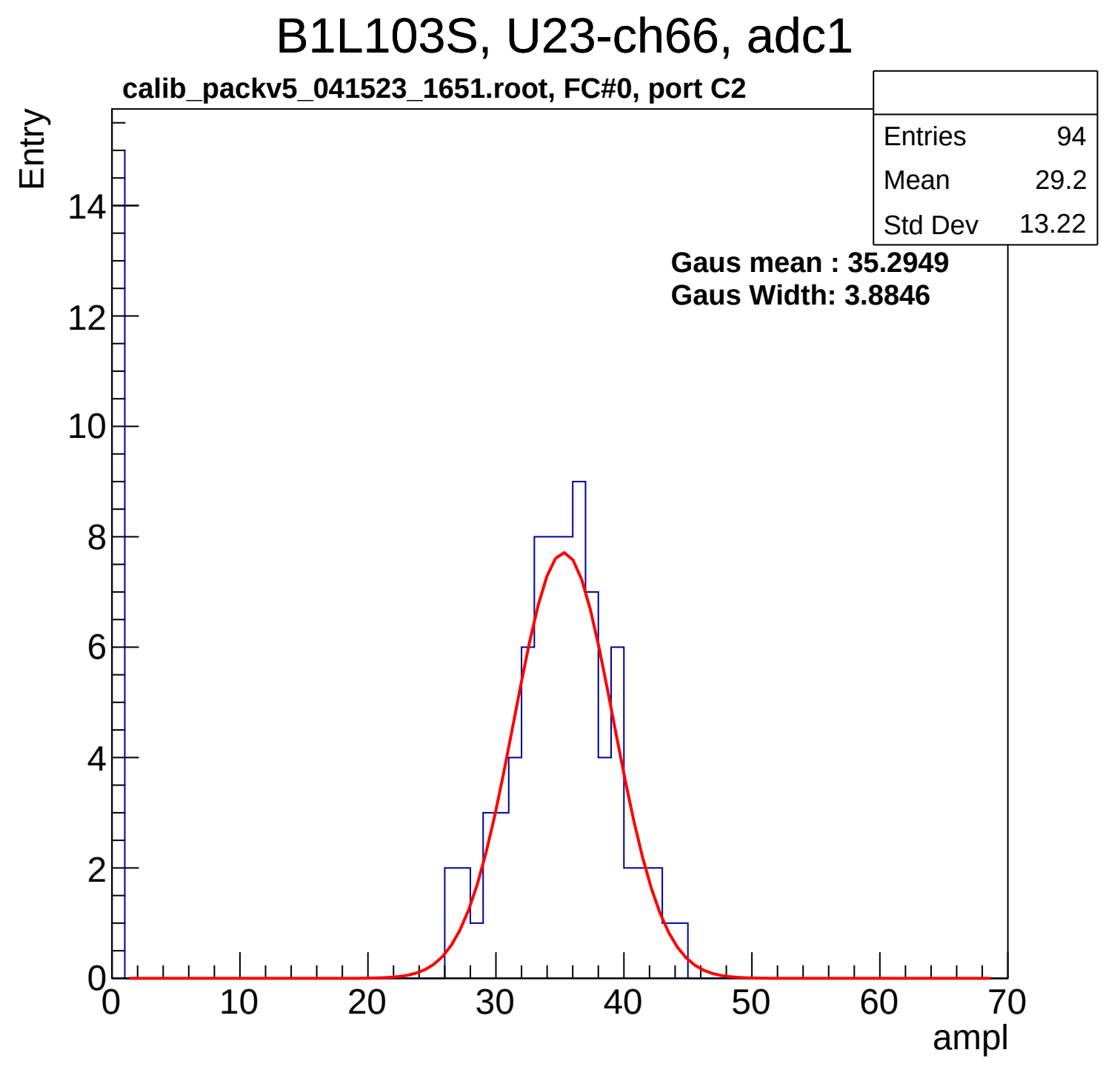

B1L103S, U23-ch65, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 55.21
Std Dev 3.188
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

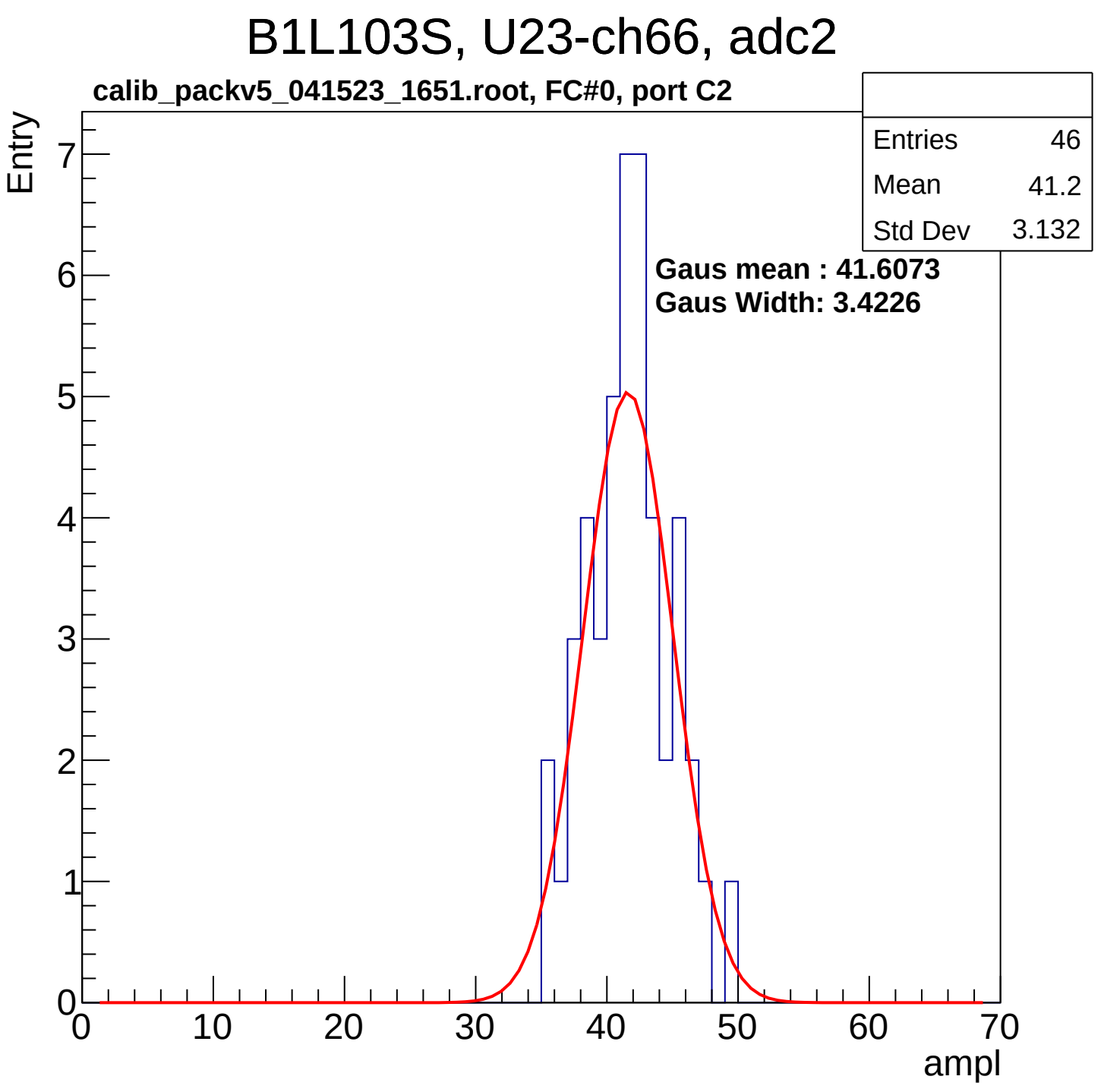

B1L103S, U23-ch65, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 58.78
Std Dev 8.814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

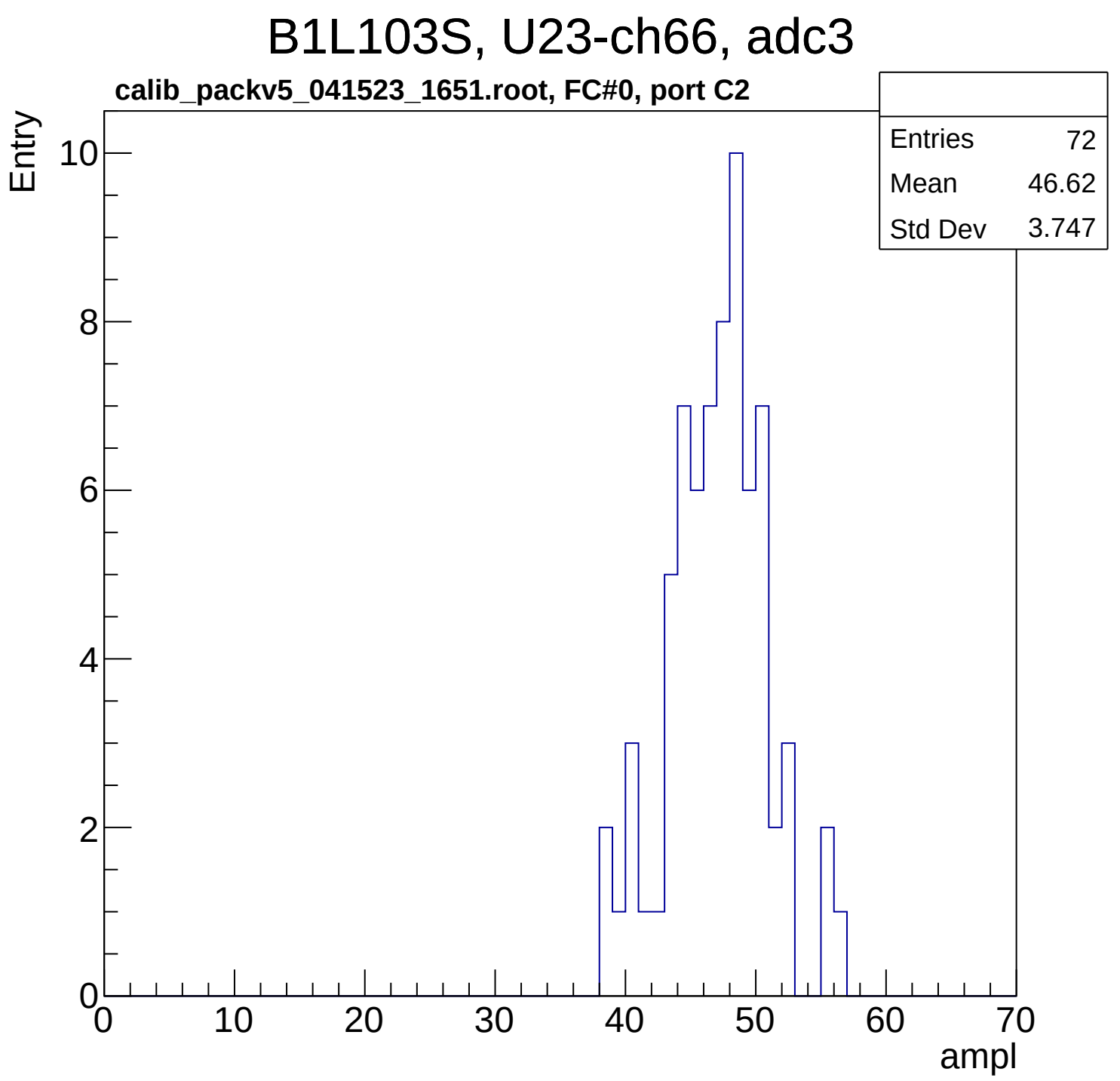

B1L103S, U23-ch65, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

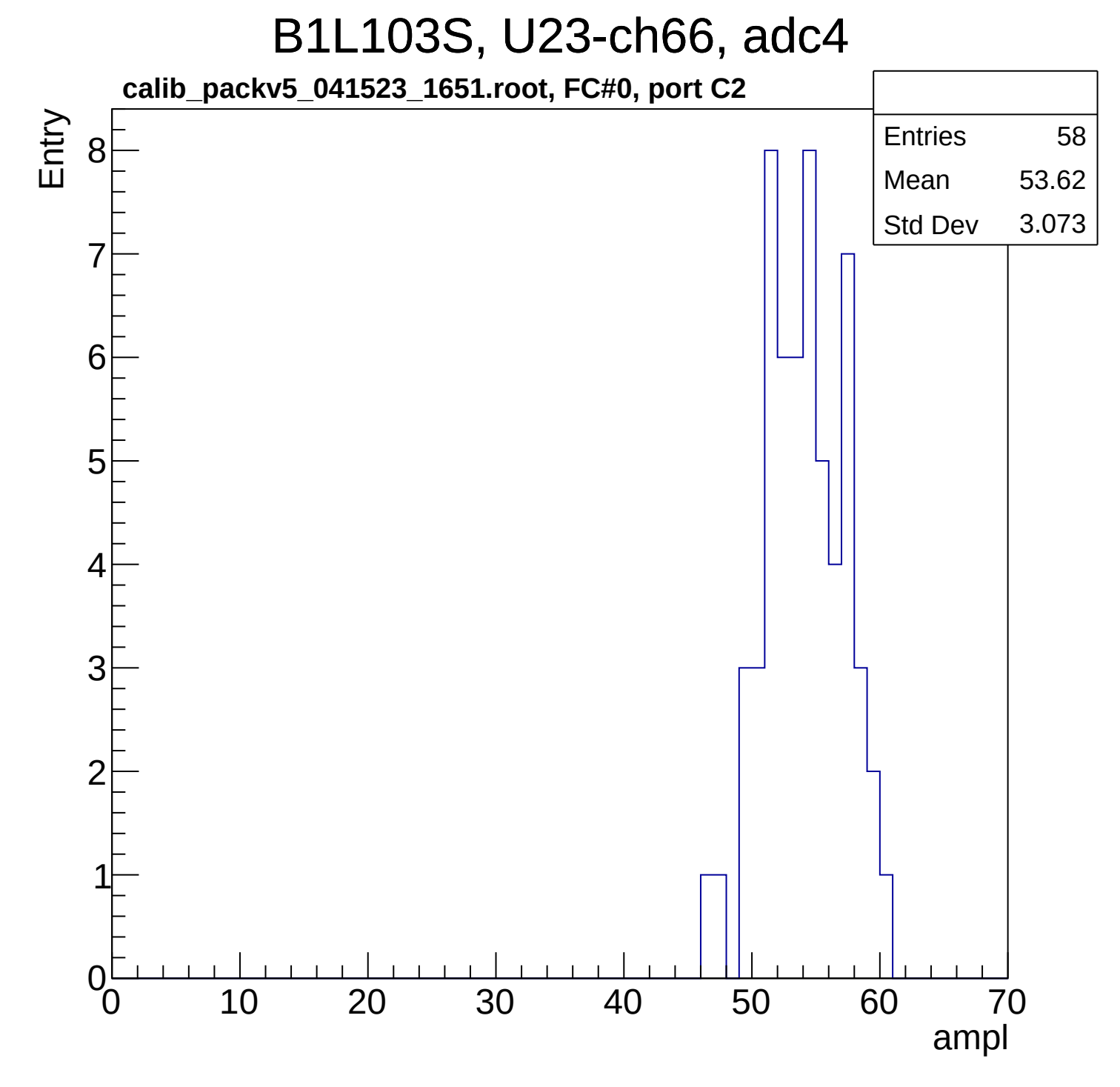

B1L103S, U23-ch65, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0.9444
Std Dev 3.894
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

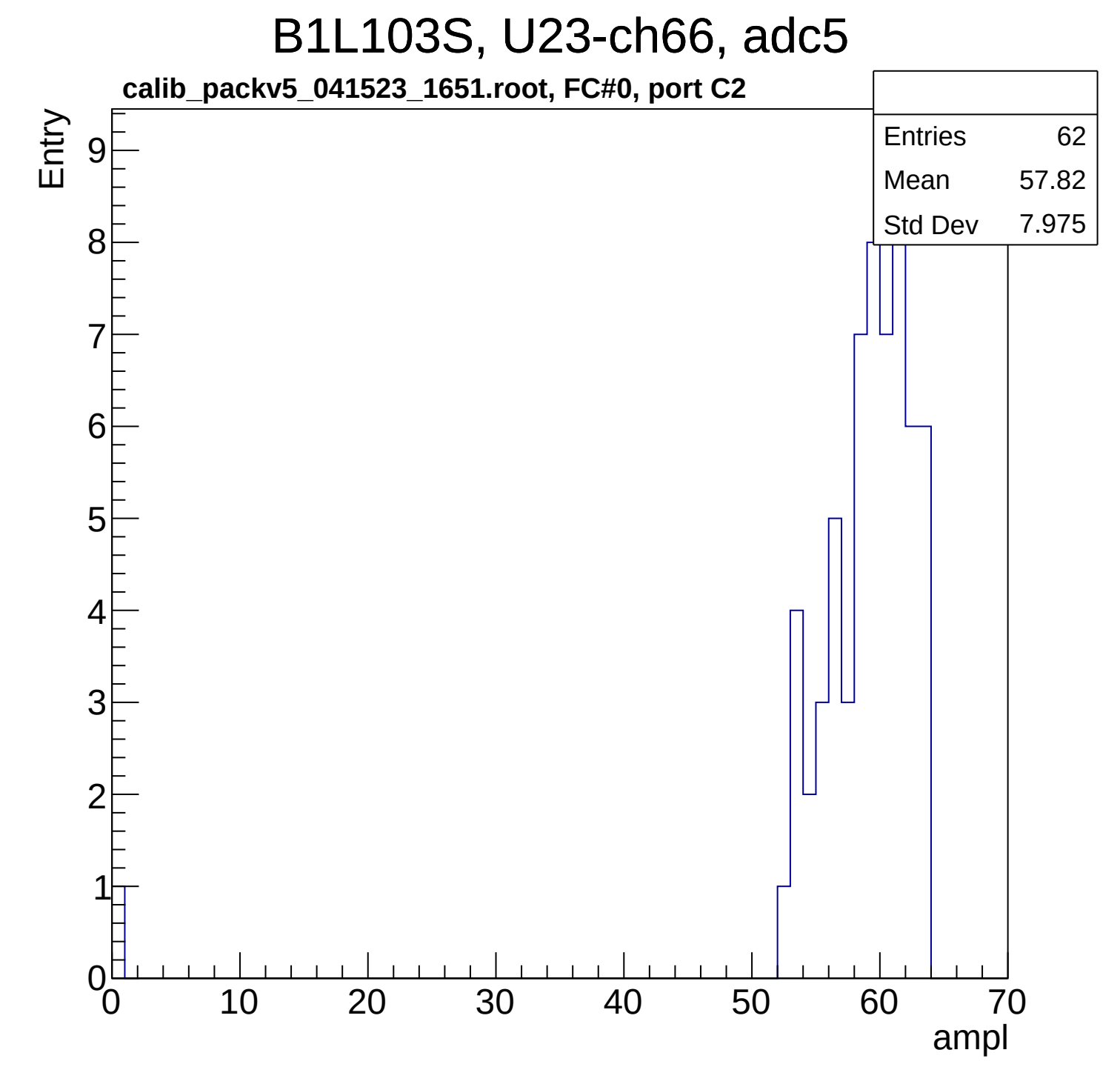

B1L103S, U23-ch66, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 22.72
Std Dev 10.69
Gaus mean : 28.5937
Gaus Width: 4.0258
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

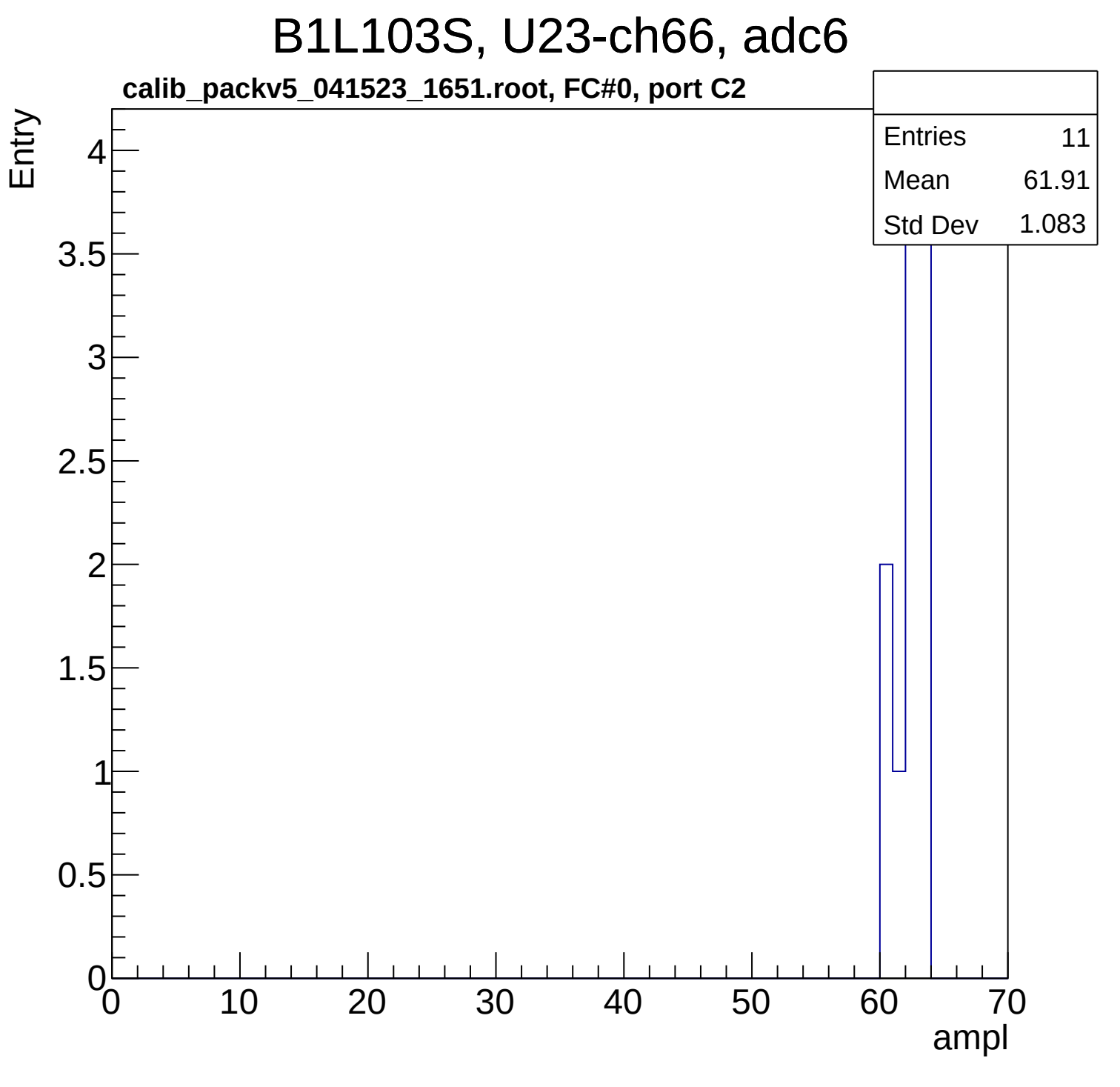

B1L103S, U23-ch66, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 94
Mean 29.2
Std Dev 13.22
Gaus mean : 35.2949
Gaus Width: 3.8846
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

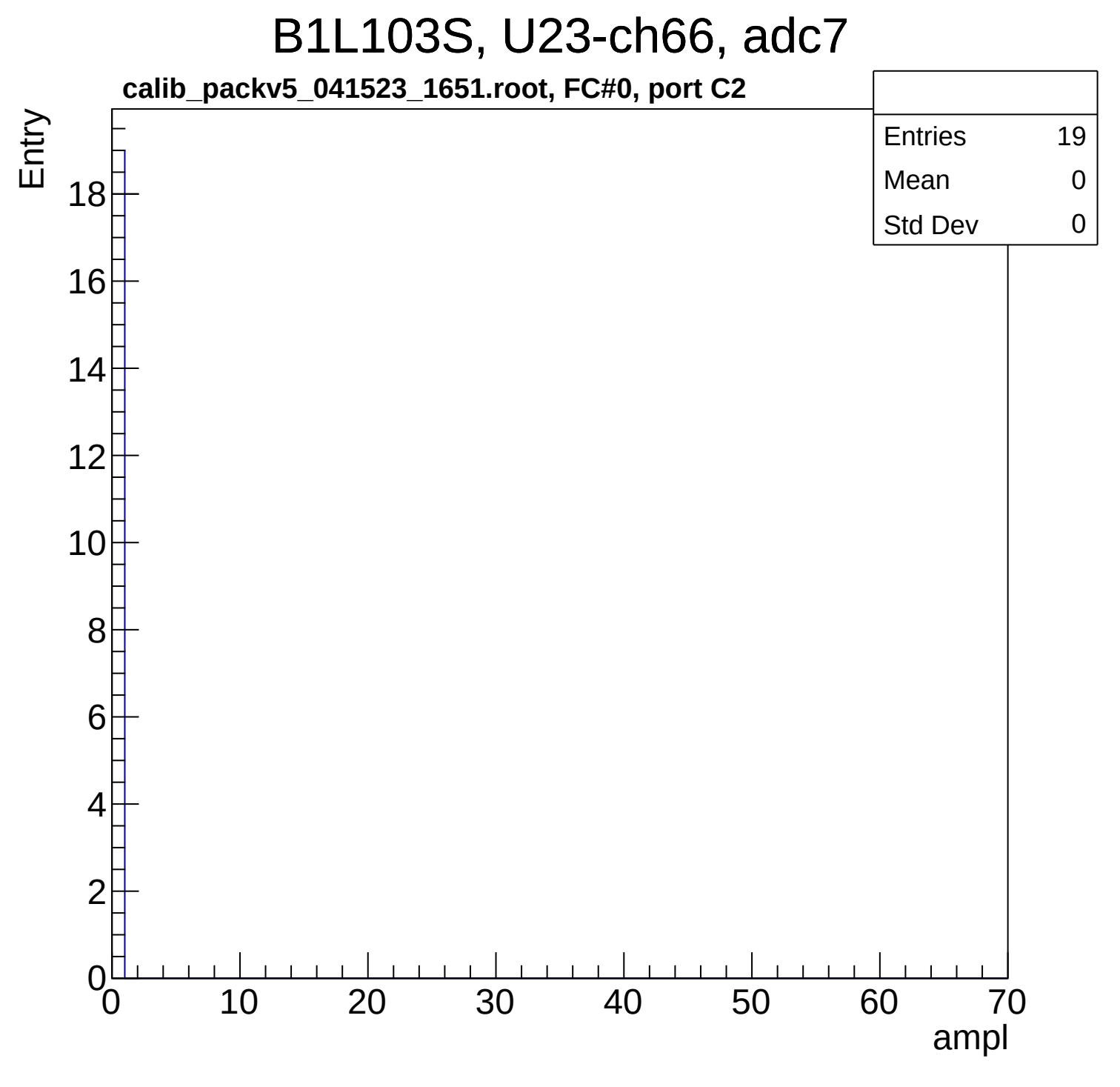

B1L103S, U23-ch66, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 46
Mean 41.2
Std Dev 3.132
Gaus mean : 41.6073
Gaus Width: 3.4226
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

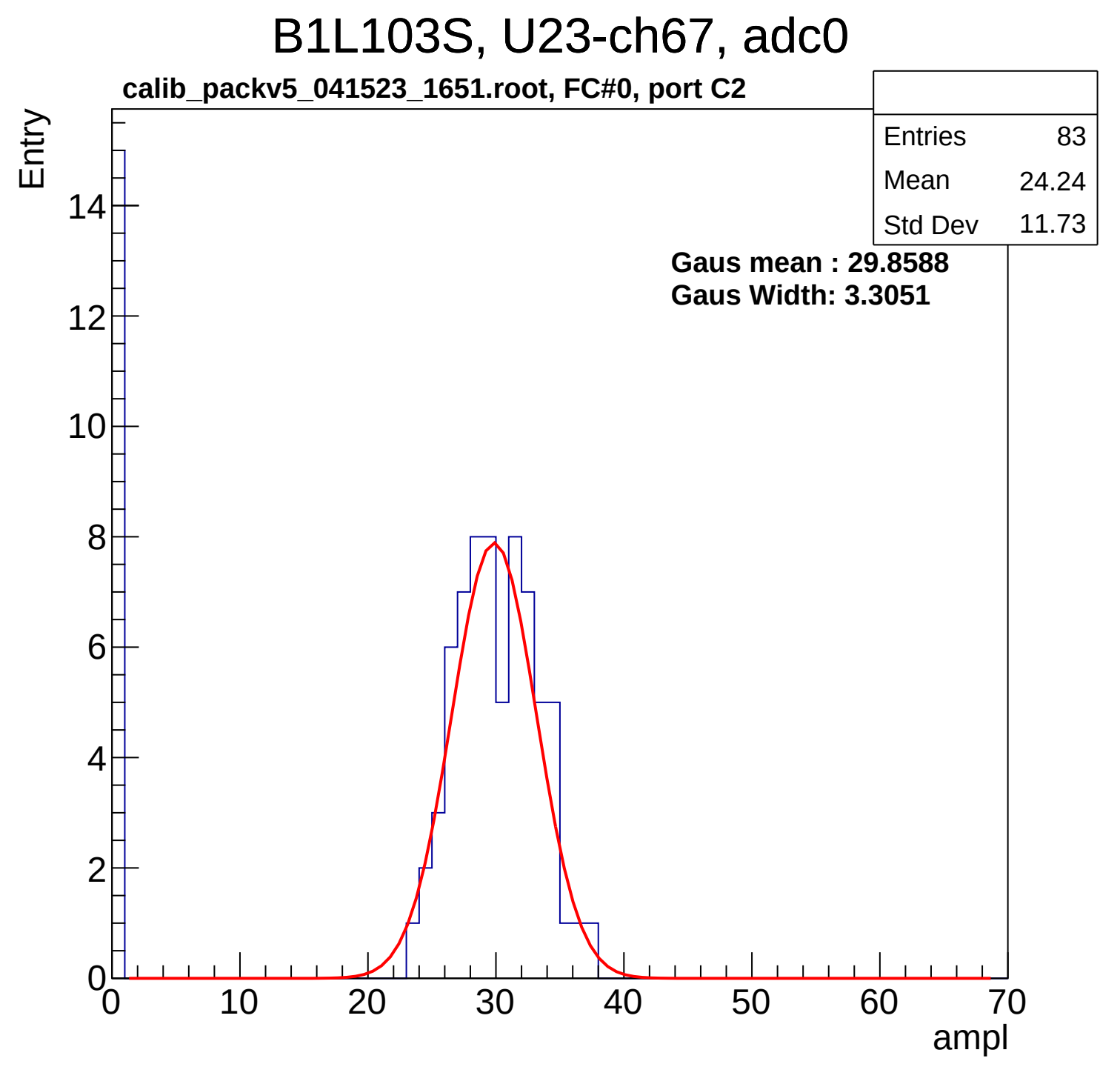

B1L103S, U23-ch66, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 46.62
Std Dev 3.747
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

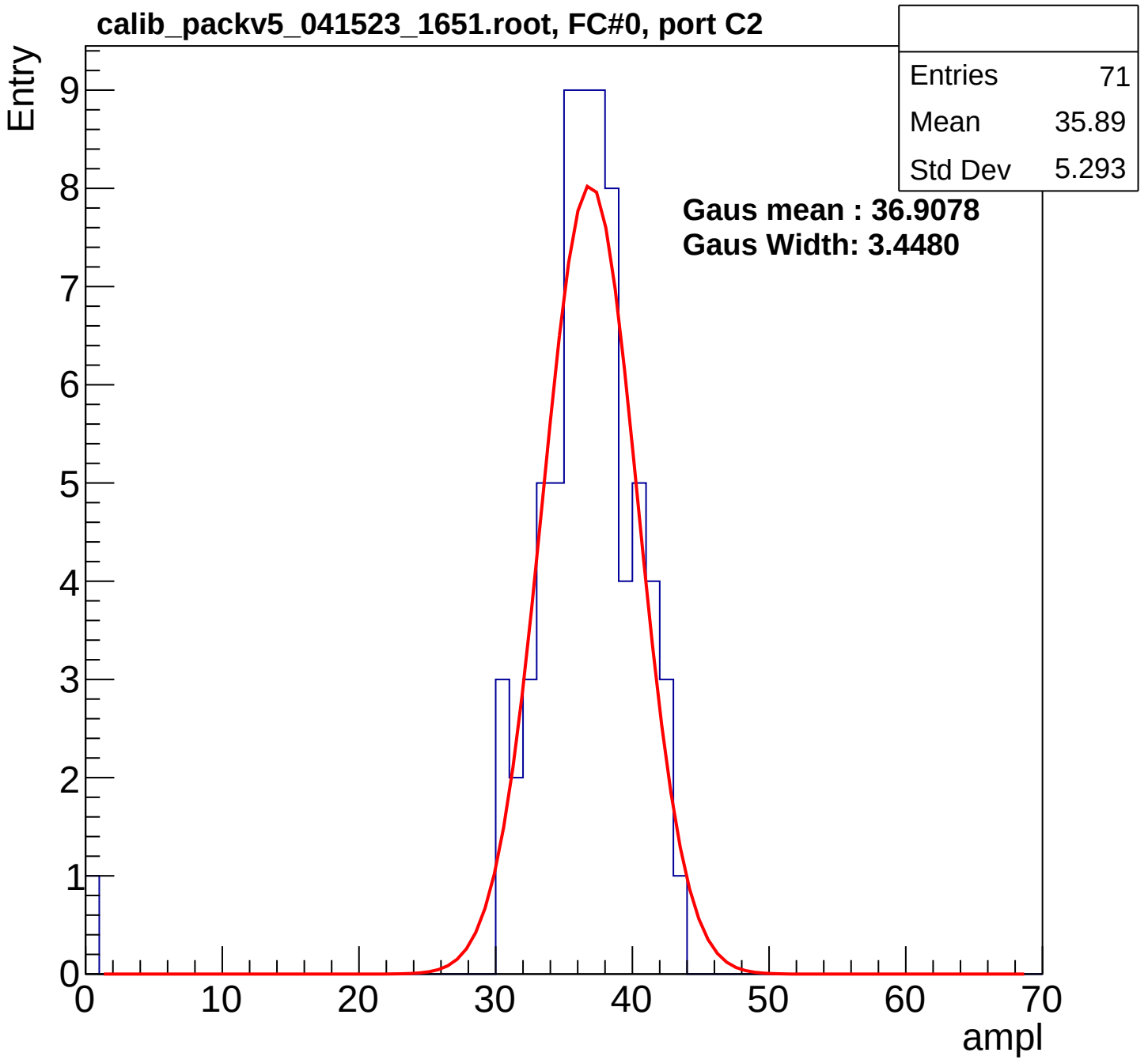

B1L103S, U23-ch66, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 53.62
Std Dev 3.073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

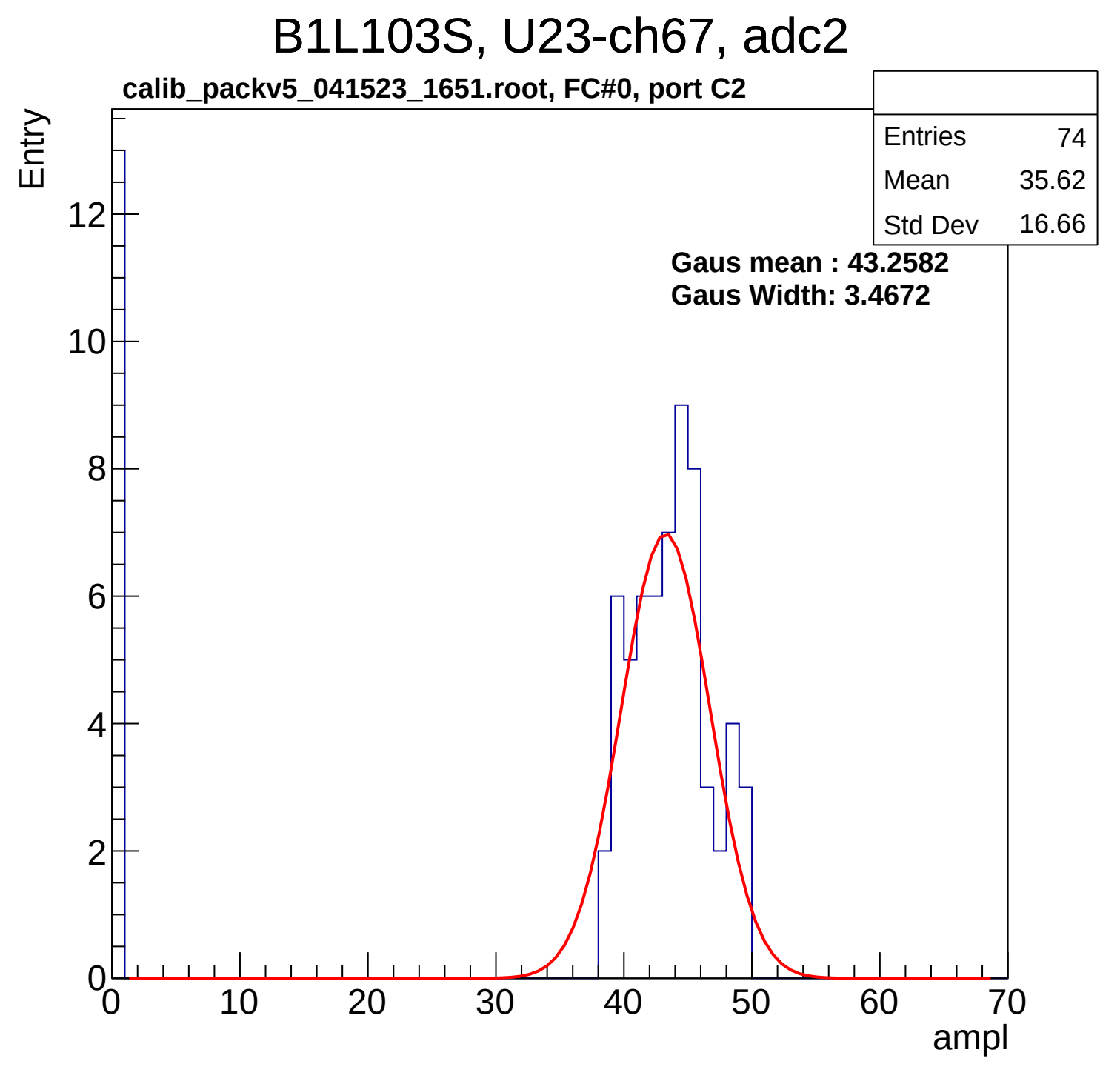

B1L103S, U23-ch66, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 57.82
Std Dev 7.975
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

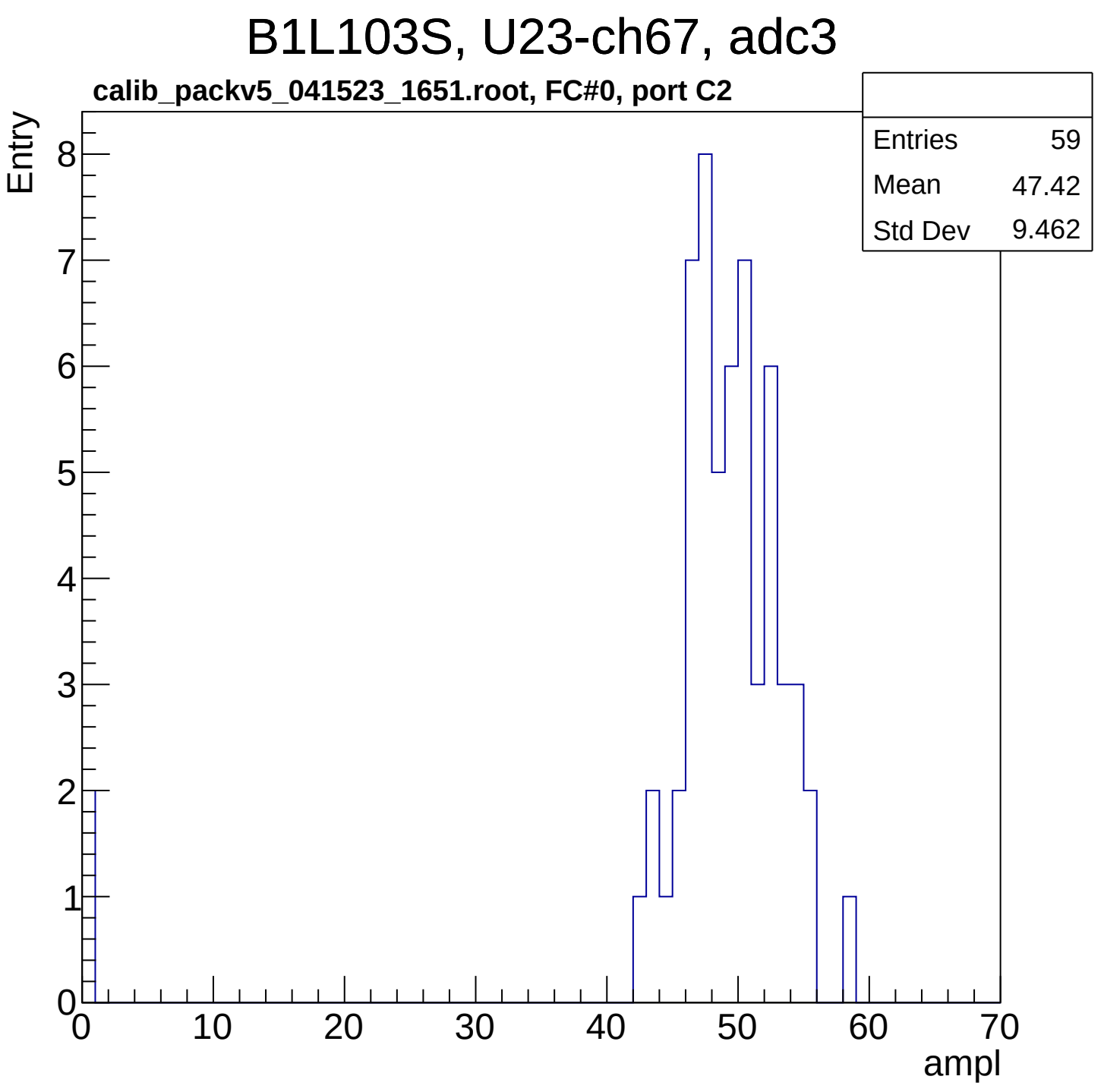

B1L103S, U23-ch66, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 61.91
Std Dev 1.083
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

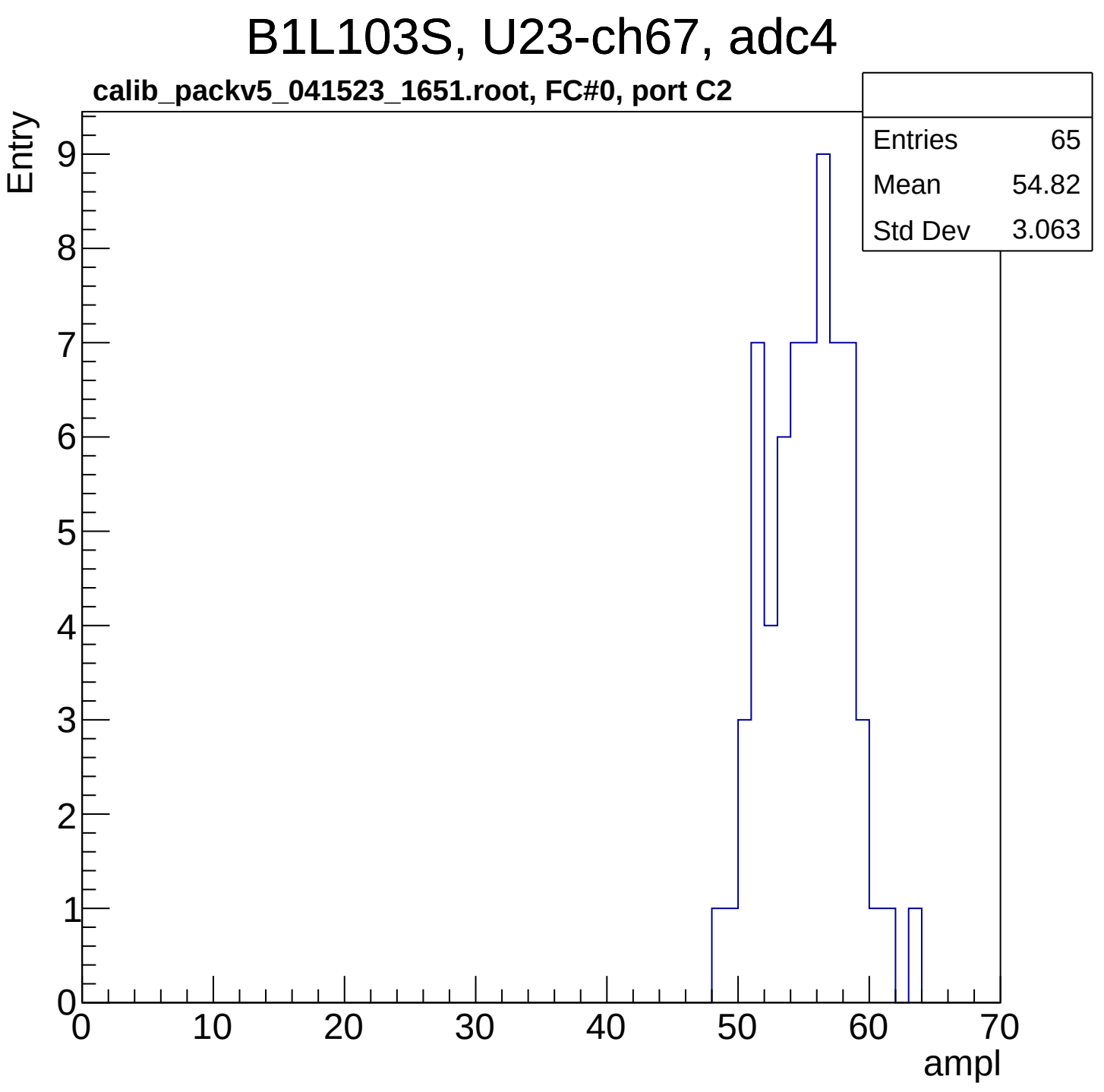

B1L103S, U23-ch66, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

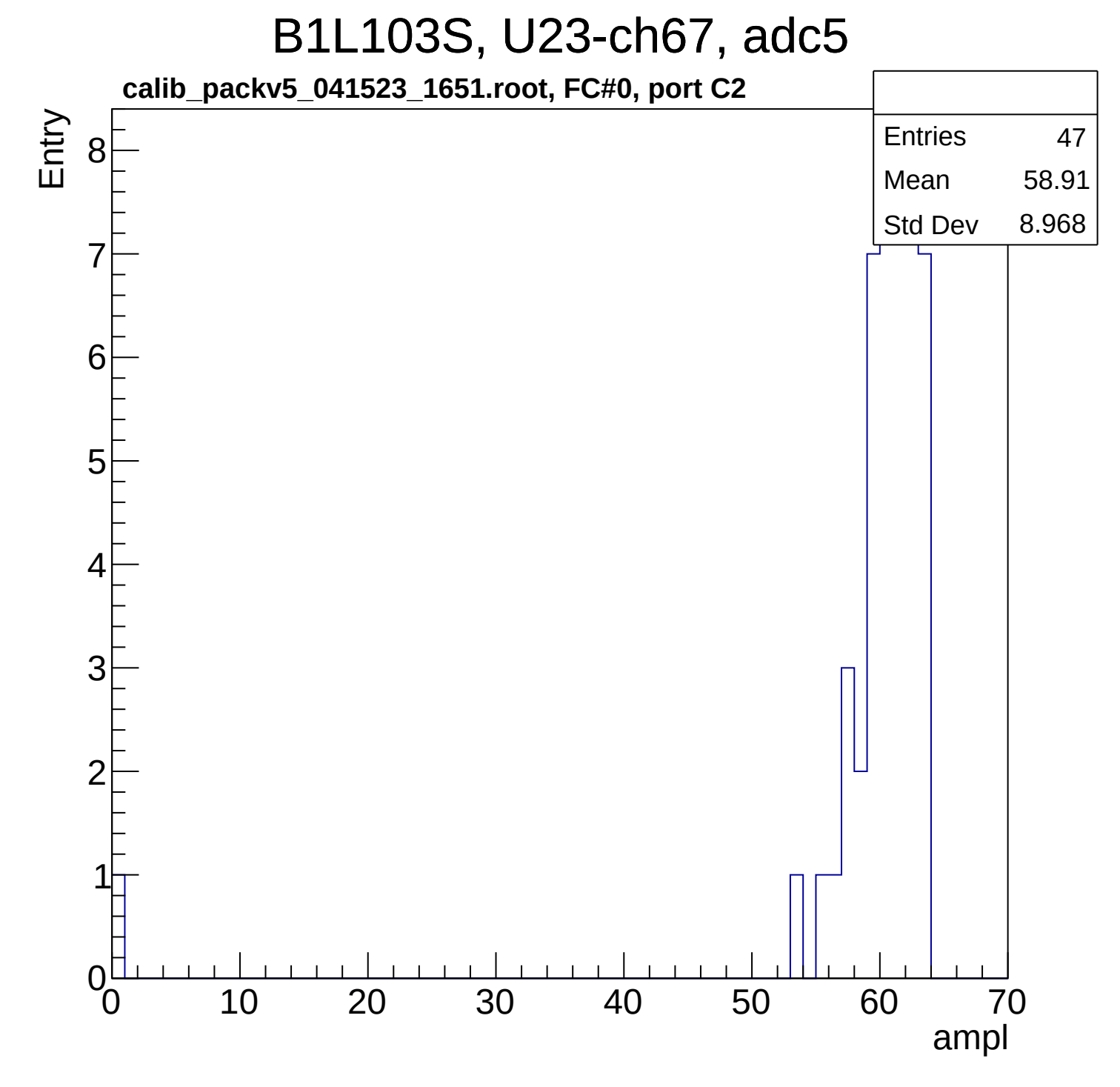

B1L103S, U23-ch67, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 24.24
Std Dev 11.73
Gaus mean : 29.8588
Gaus Width: 3.3051
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

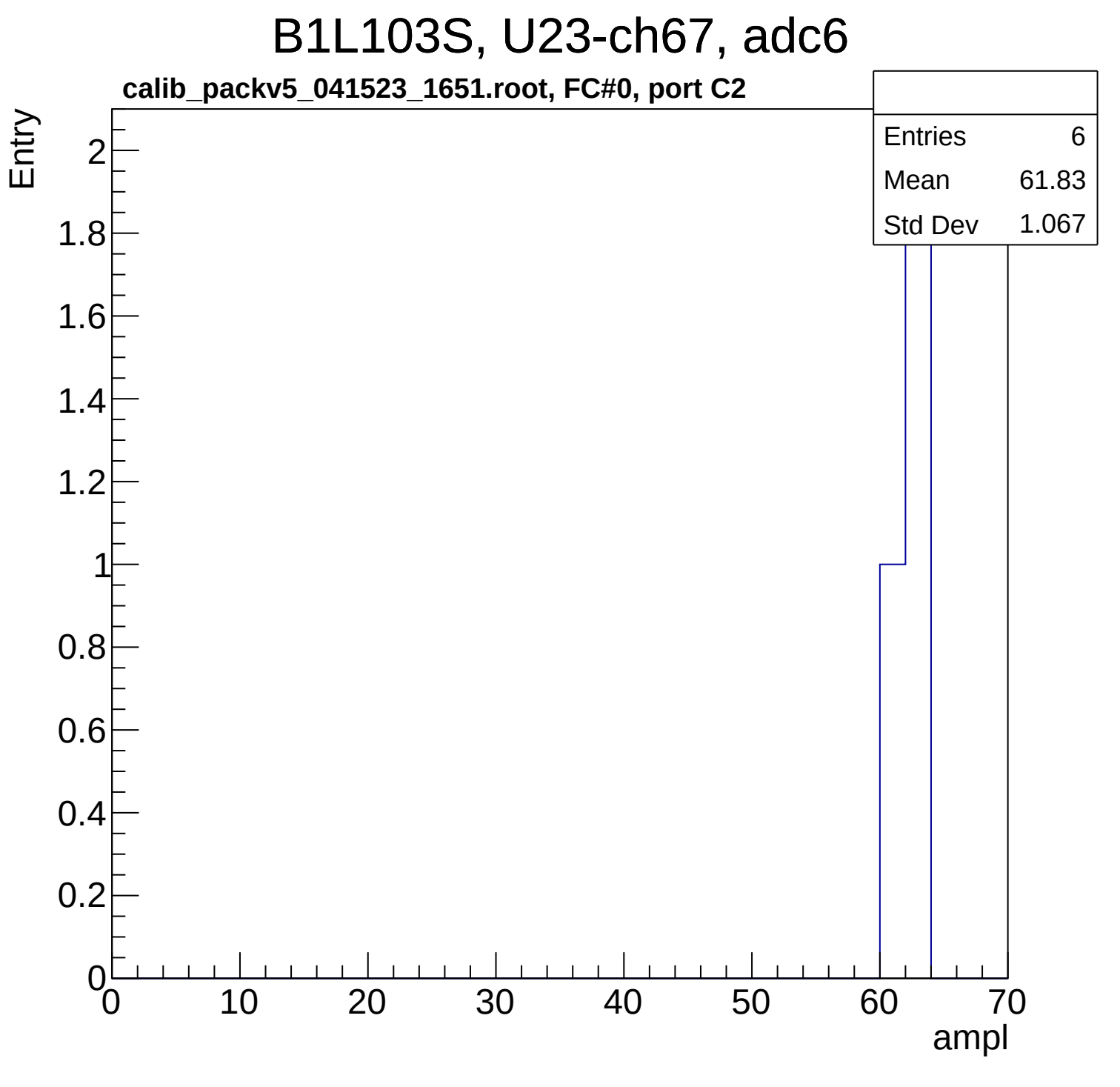

B1L103S, U23-ch67, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 35.89
Std Dev 5.293
Gaus mean : 36.9078
Gaus Width: 3.4480
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

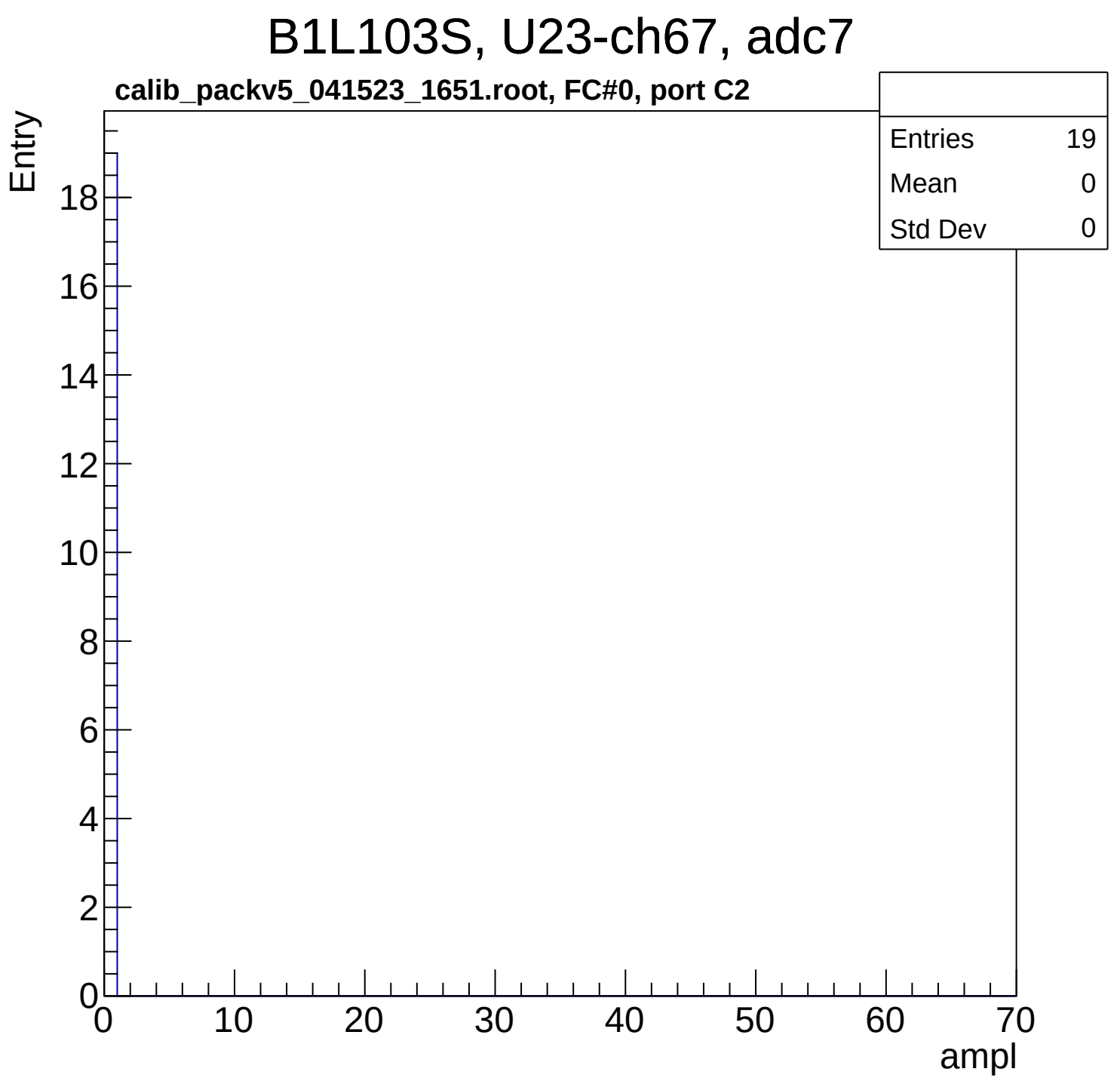

B1L103S, U23-ch67, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 35.62
Std Dev 16.66
Gaus mean : 43.2582
Gaus Width: 3.4672
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

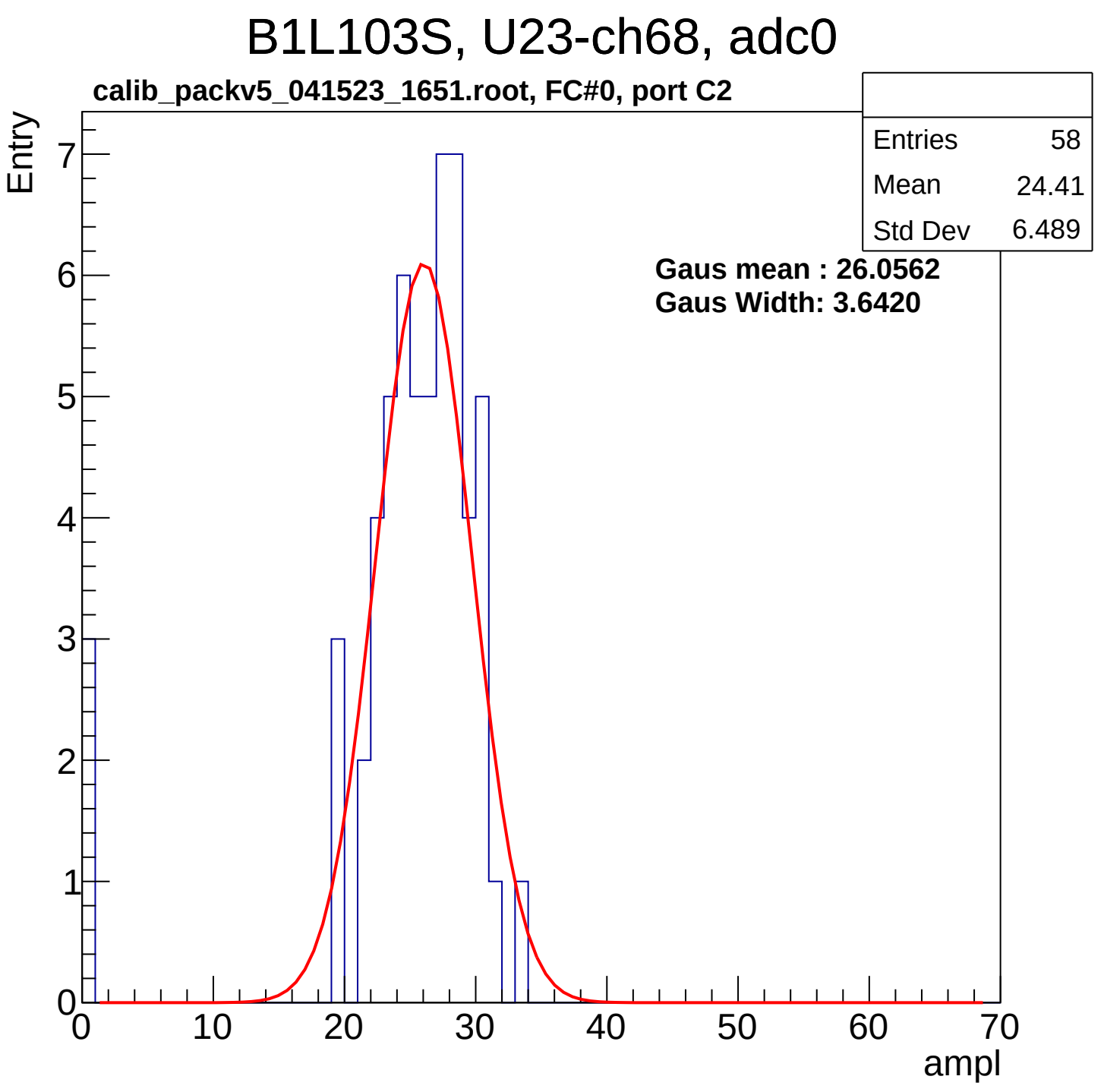

B1L103S, U23-ch67, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 47.42
Std Dev 9.462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

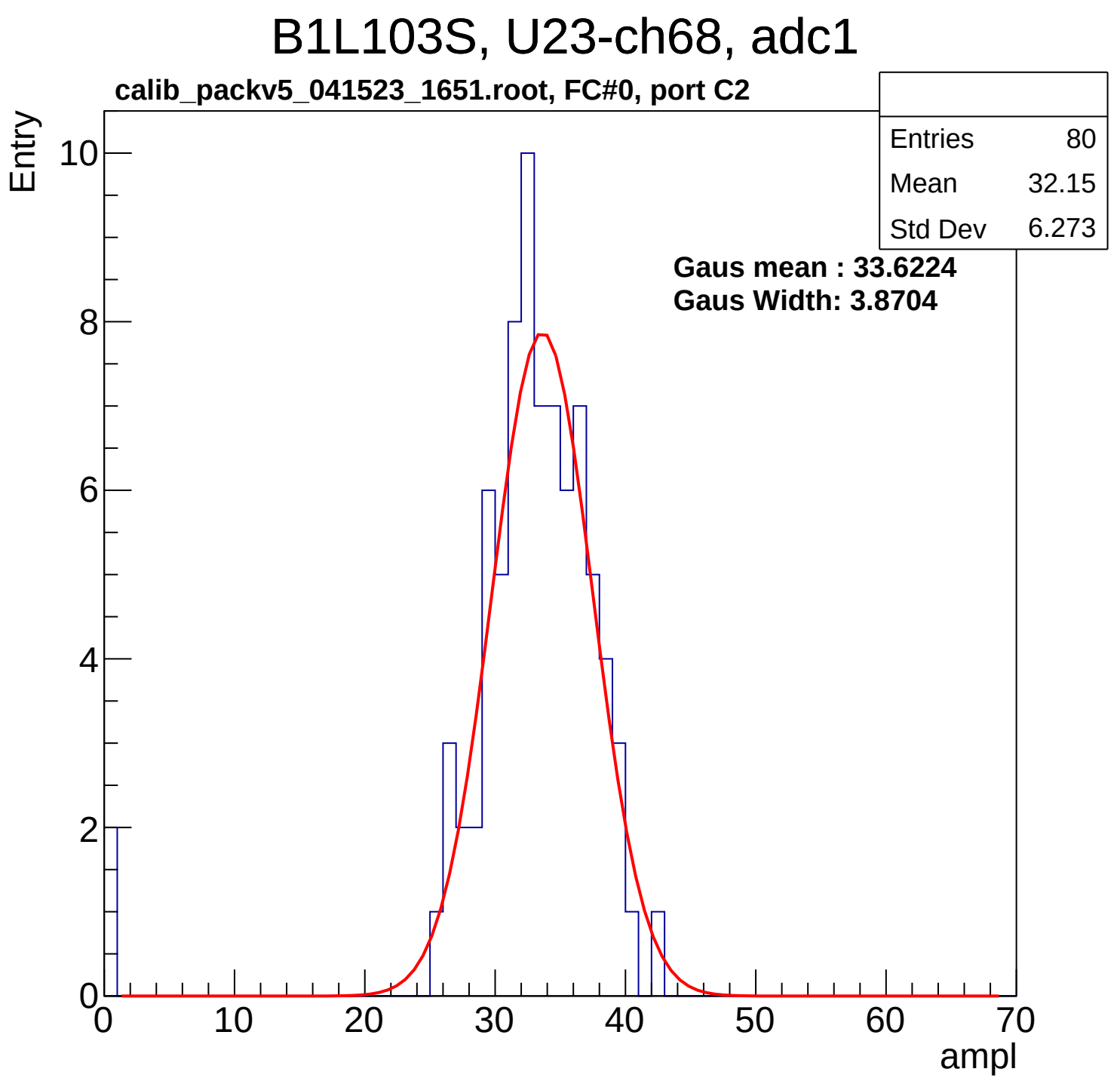

B1L103S, U23-ch67, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 54.82
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

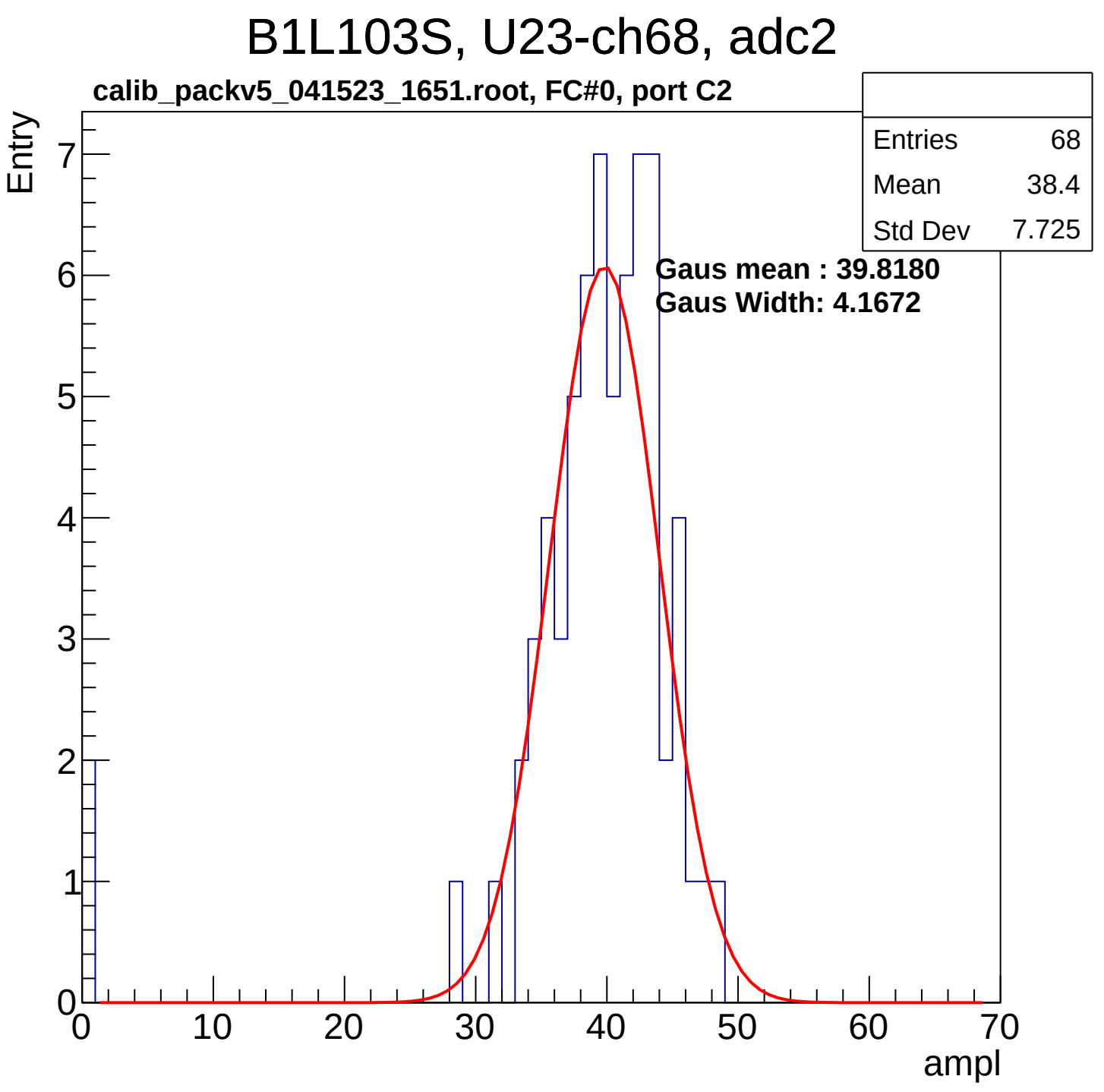

B1L103S, U23-ch67, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 47
Mean 58.91
Std Dev 8.968
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

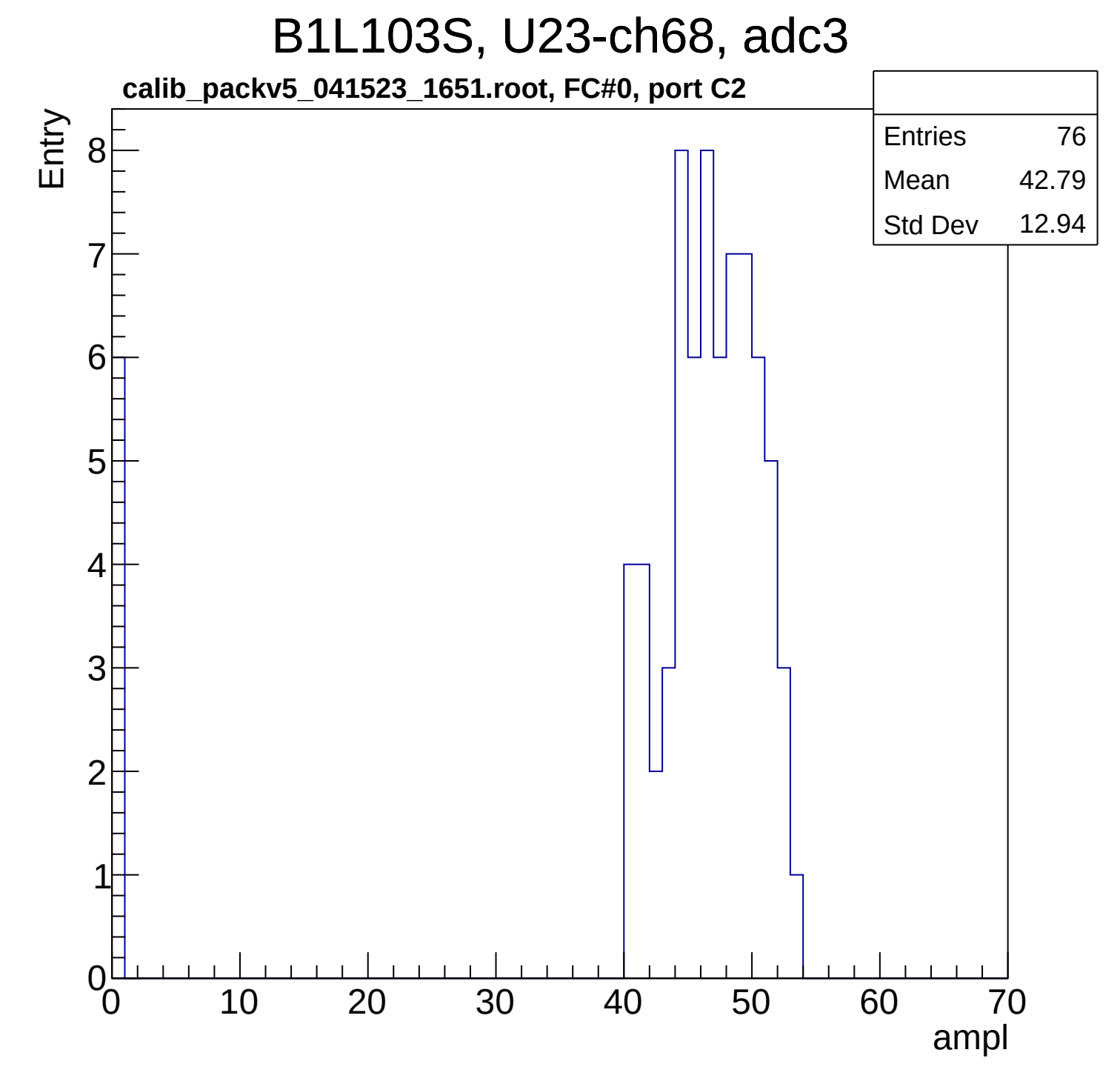

B1L103S, U23-ch67, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

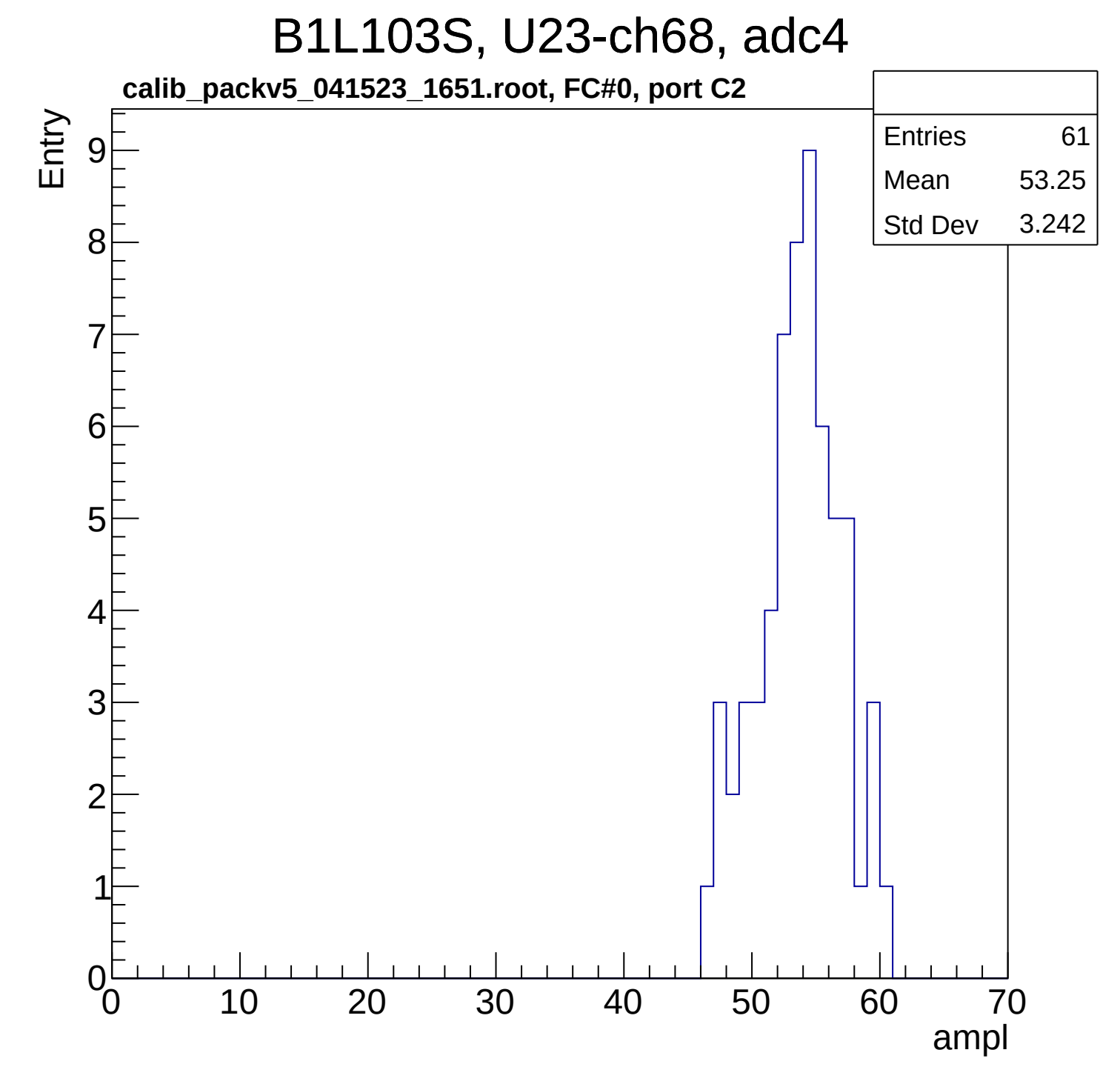

B1L103S, U23-ch67, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

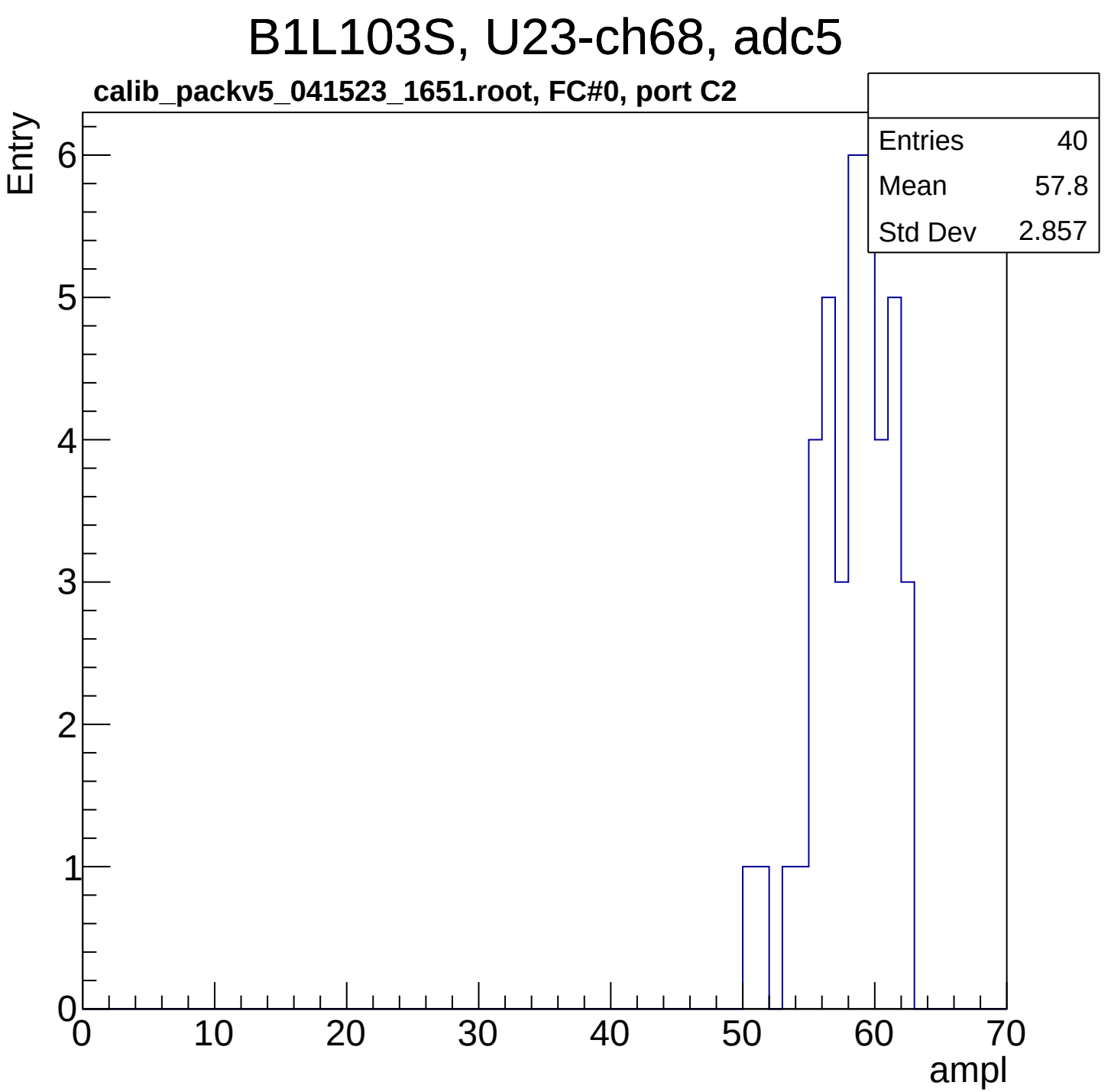

B1L103S, U23-ch68, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 24.41
Std Dev 6.489
Gaus mean : 26.0562
Gaus Width: 3.6420
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

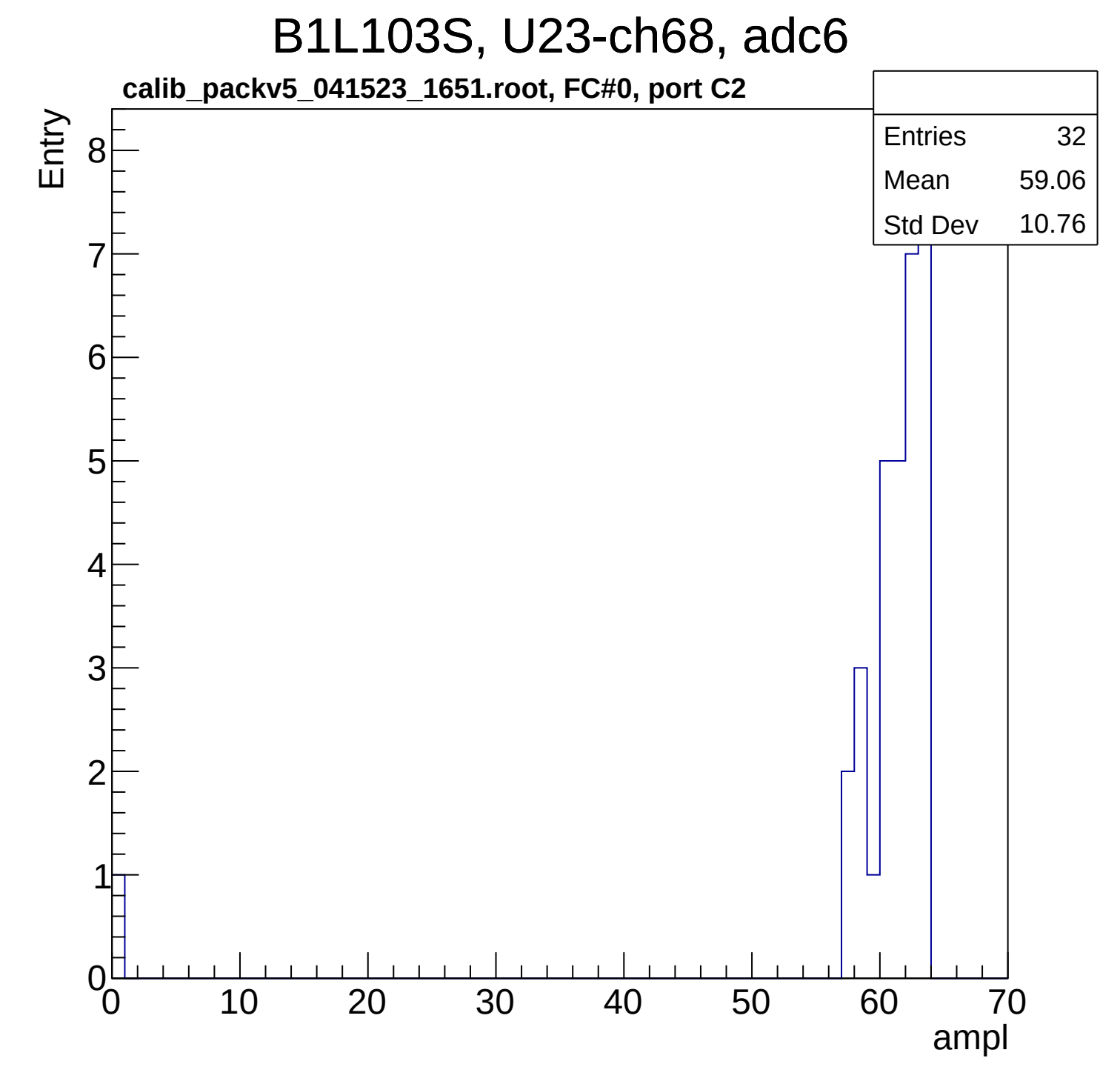

B1L103S, U23-ch68, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 32.15
Std Dev 6.273
Gaus mean : 33.6224
Gaus Width: 3.8704
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

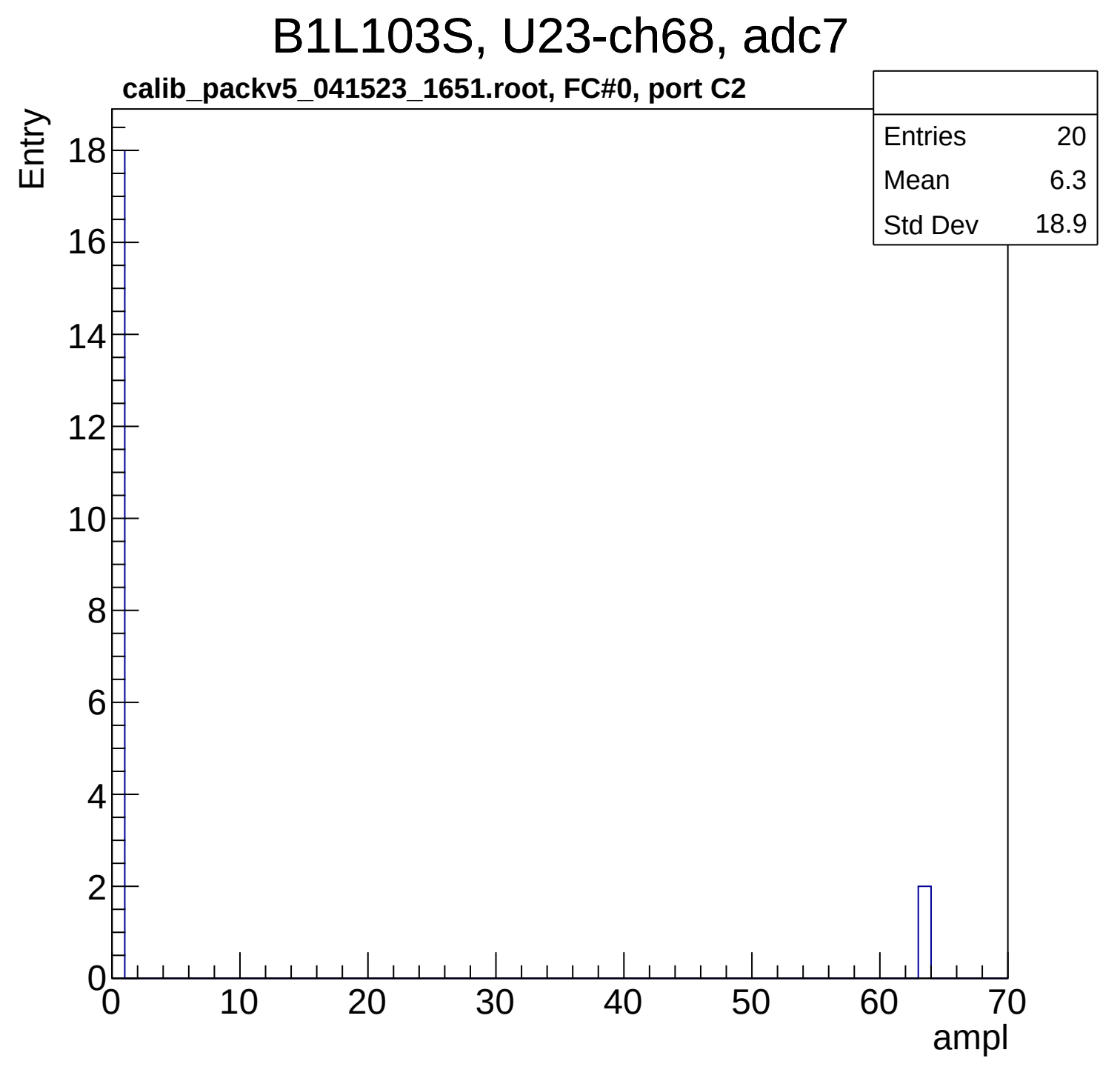

B1L103S, U23-ch68, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 38.4
Std Dev 7.725
Gaus mean : 39.8180
Gaus Width: 4.1672
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

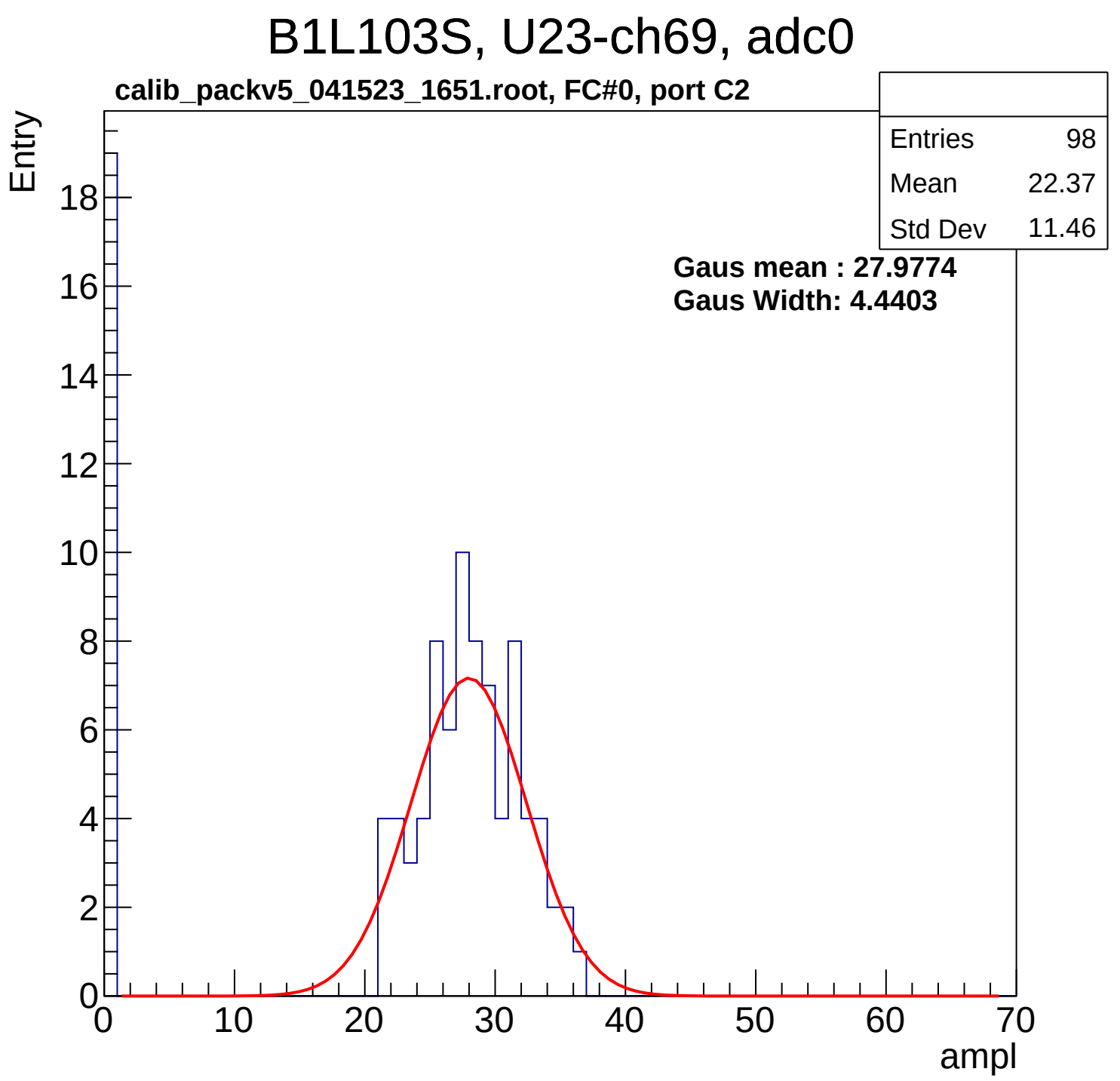

B1L103S, U23-ch68, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 42.79
Std Dev 12.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

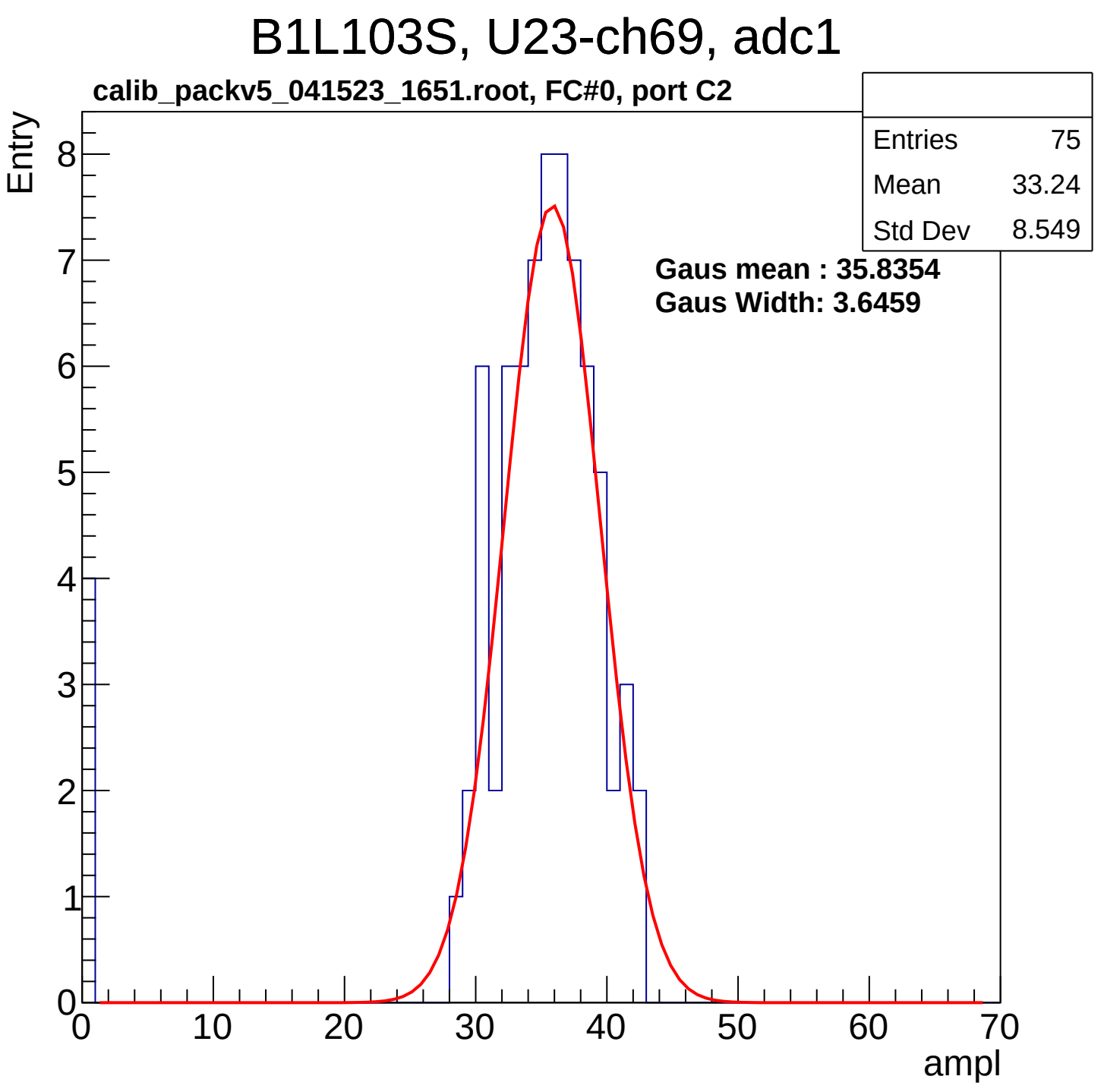

B1L103S, U23-ch68, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 53.25
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

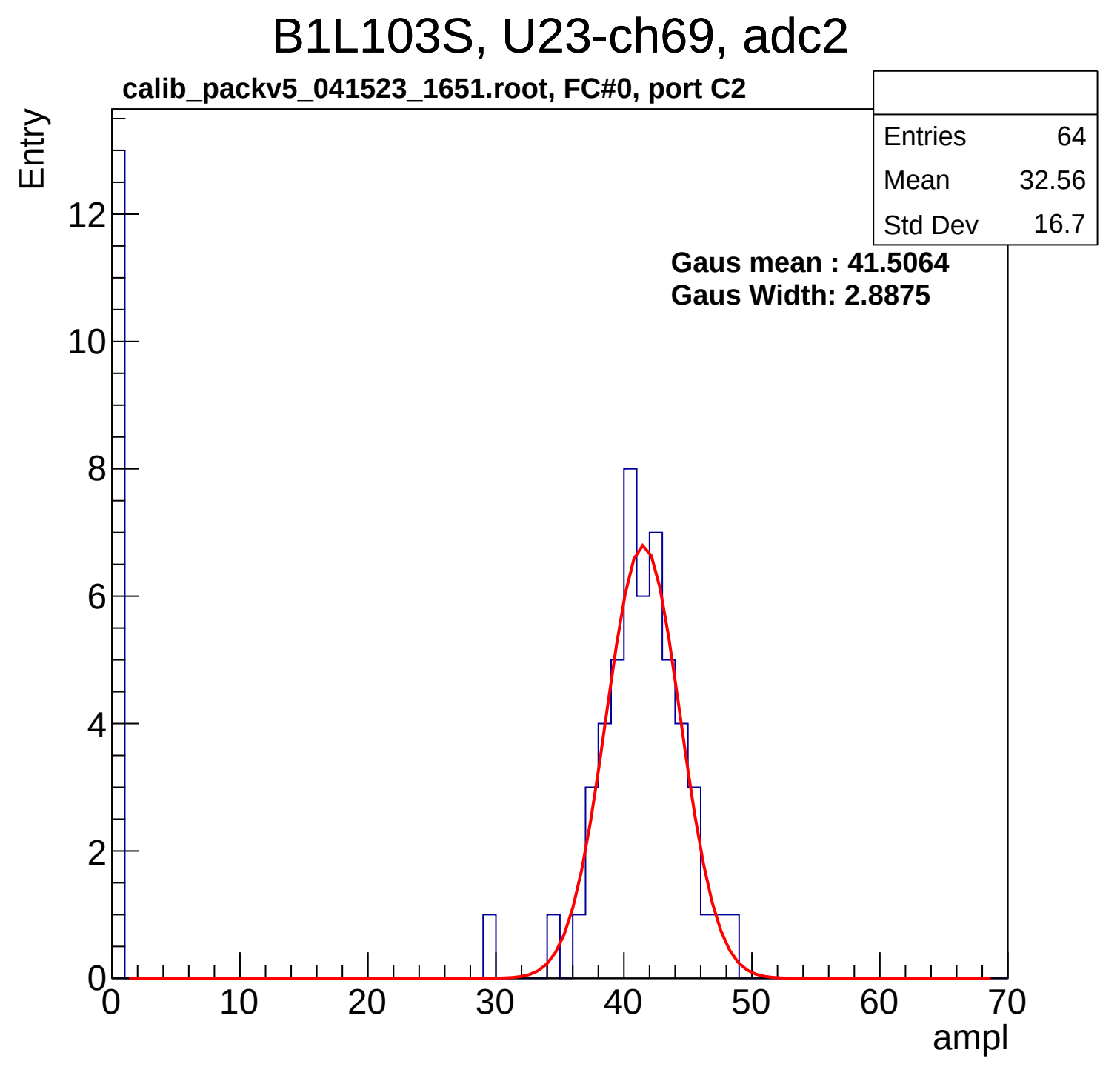

B1L103S, U23-ch68, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 40
Mean 57.8
Std Dev 2.857
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

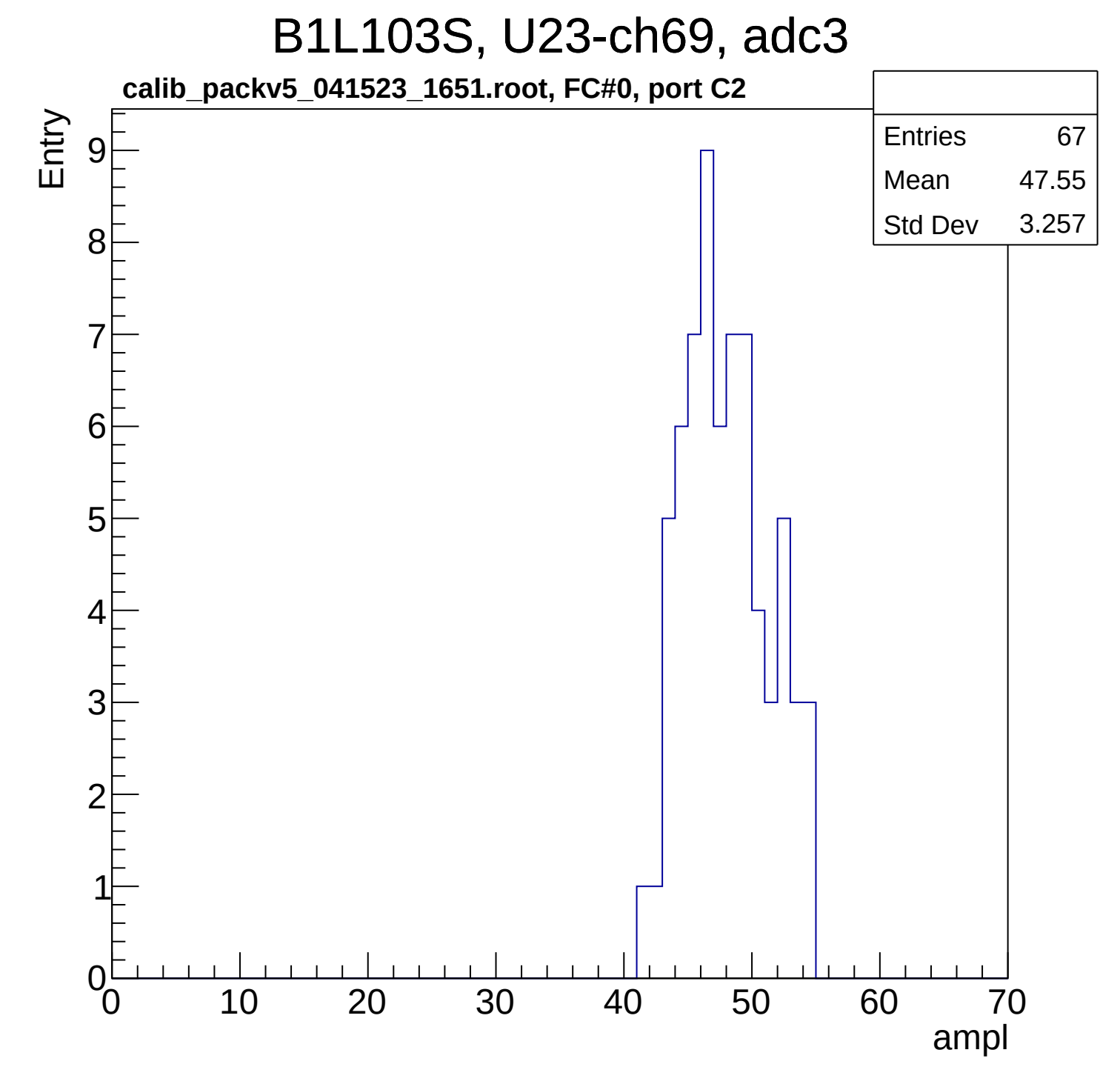

B1L103S, U23-ch68, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 32
Mean 59.06
Std Dev 10.76
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

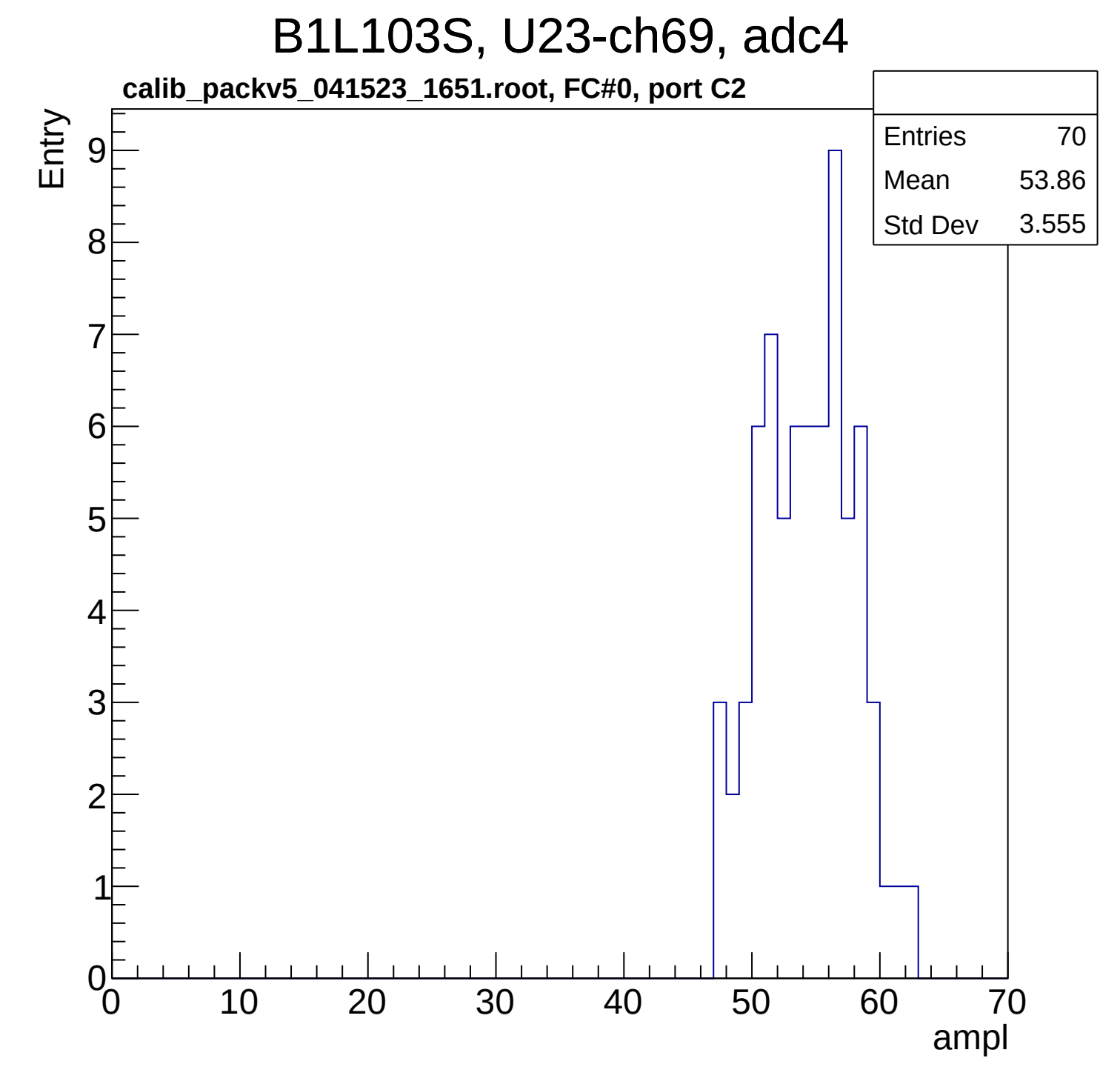

B1L103S, U23-ch68, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 6.3
Std Dev 18.9
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

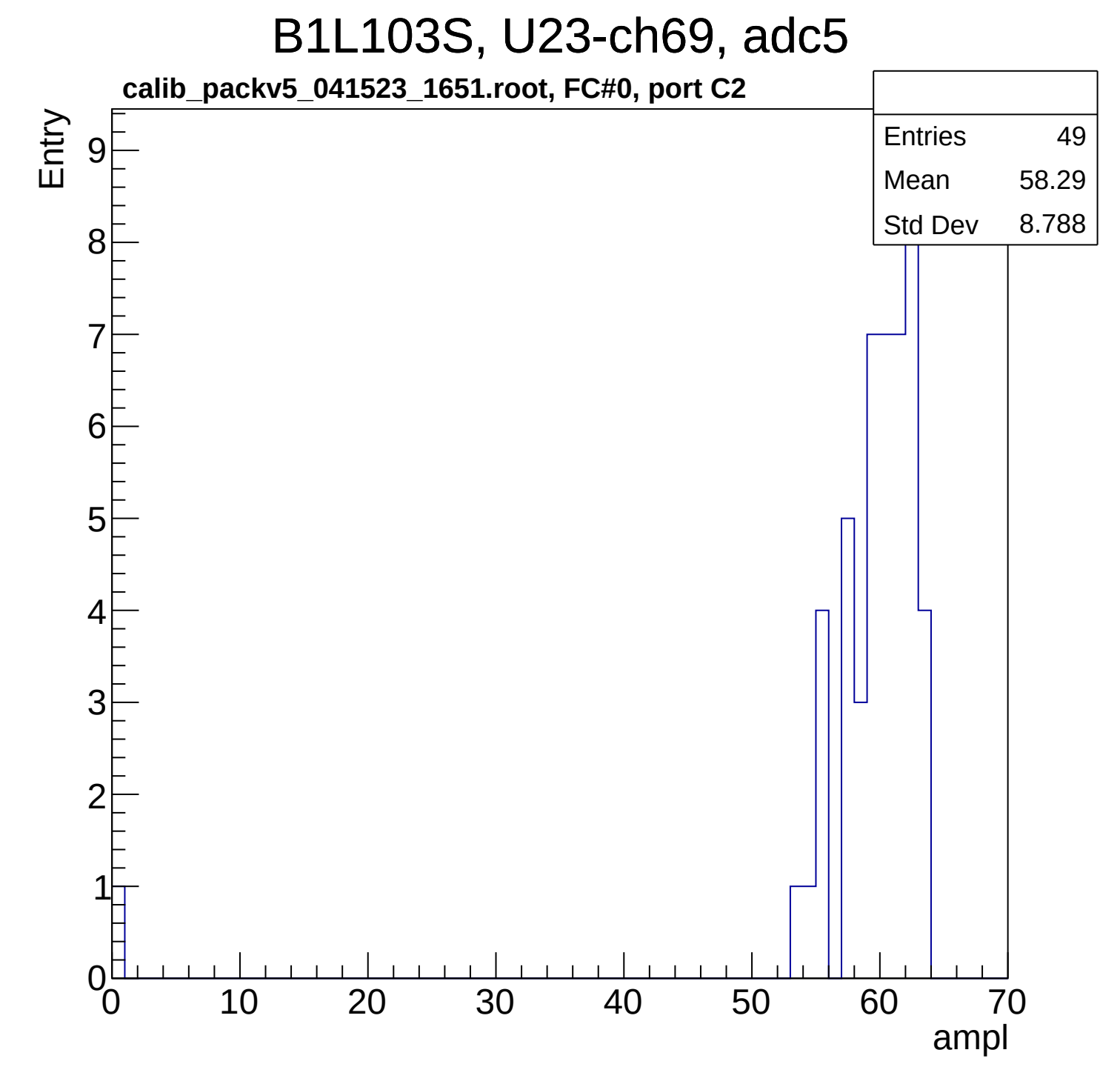

B1L103S, U23-ch69, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 98
Mean 22.37
Std Dev 11.46
Gaus mean : 27.9774
Gaus Width: 4.4403
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

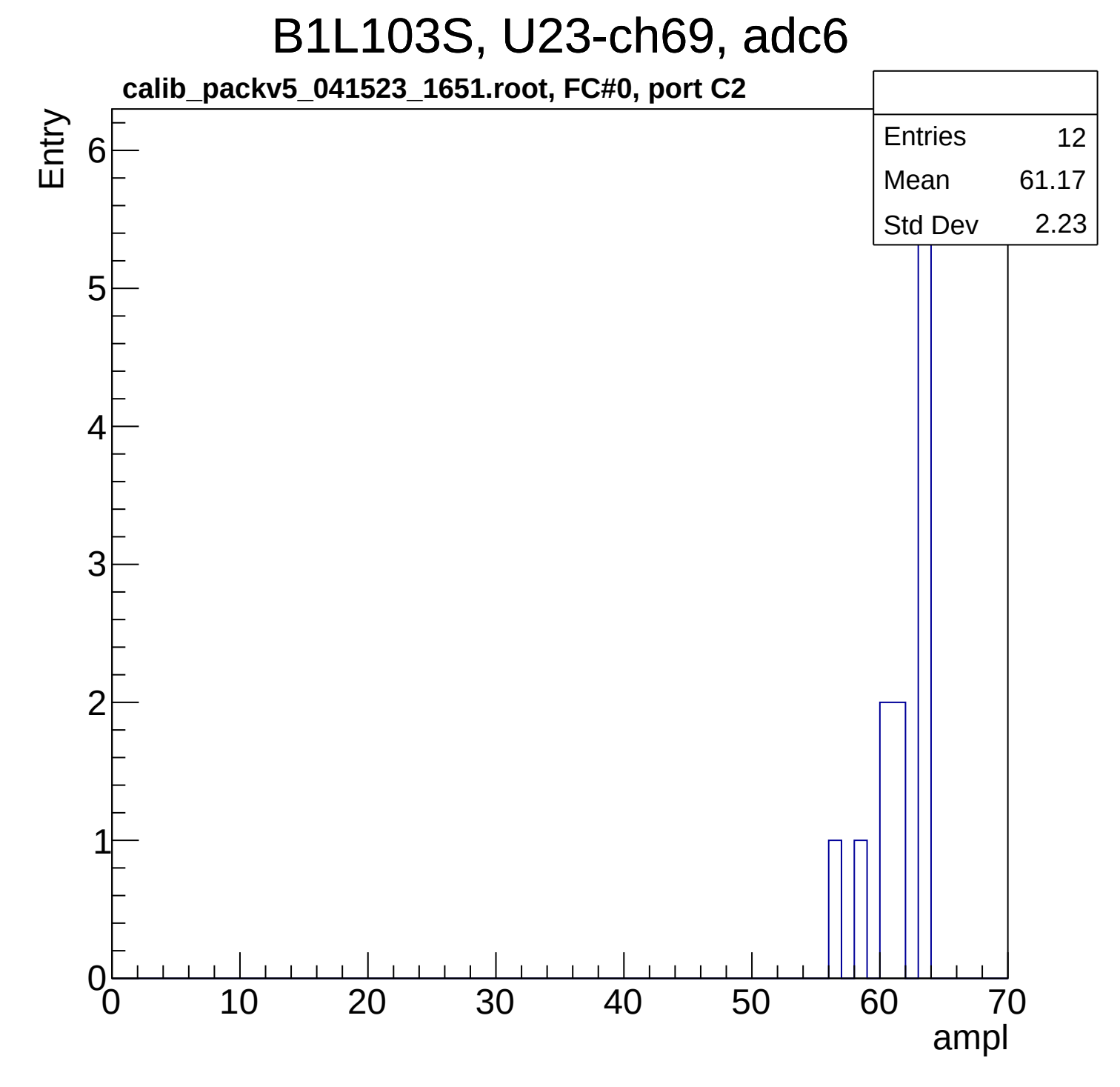

B1L103S, U23-ch69, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 33.24
Std Dev 8.549
Gaus mean : 35.8354
Gaus Width: 3.6459
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

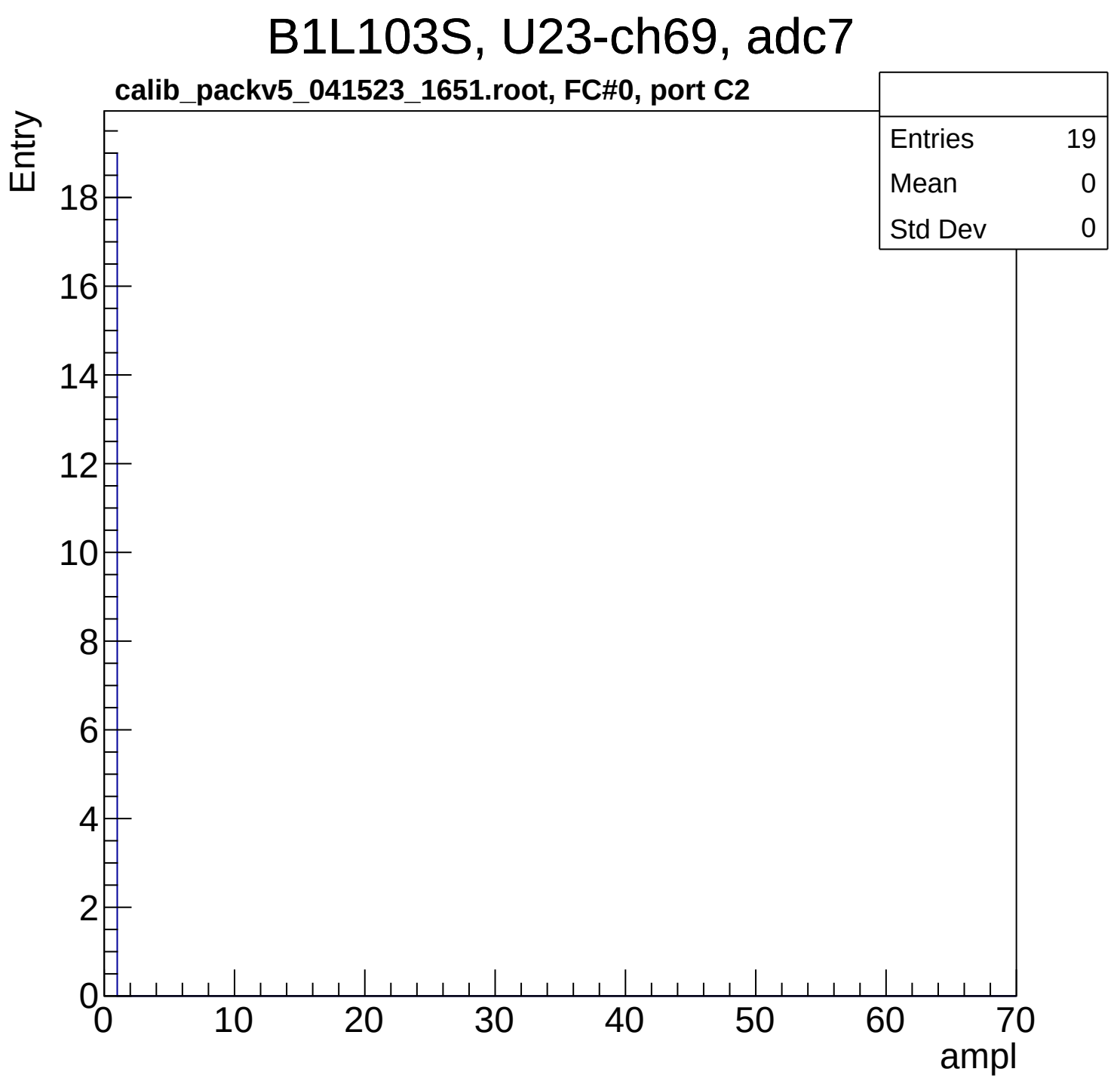

B1L103S, U23-ch69, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 32.56
Std Dev 16.7
Gaus mean : 41.5064
Gaus Width: 2.8875
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

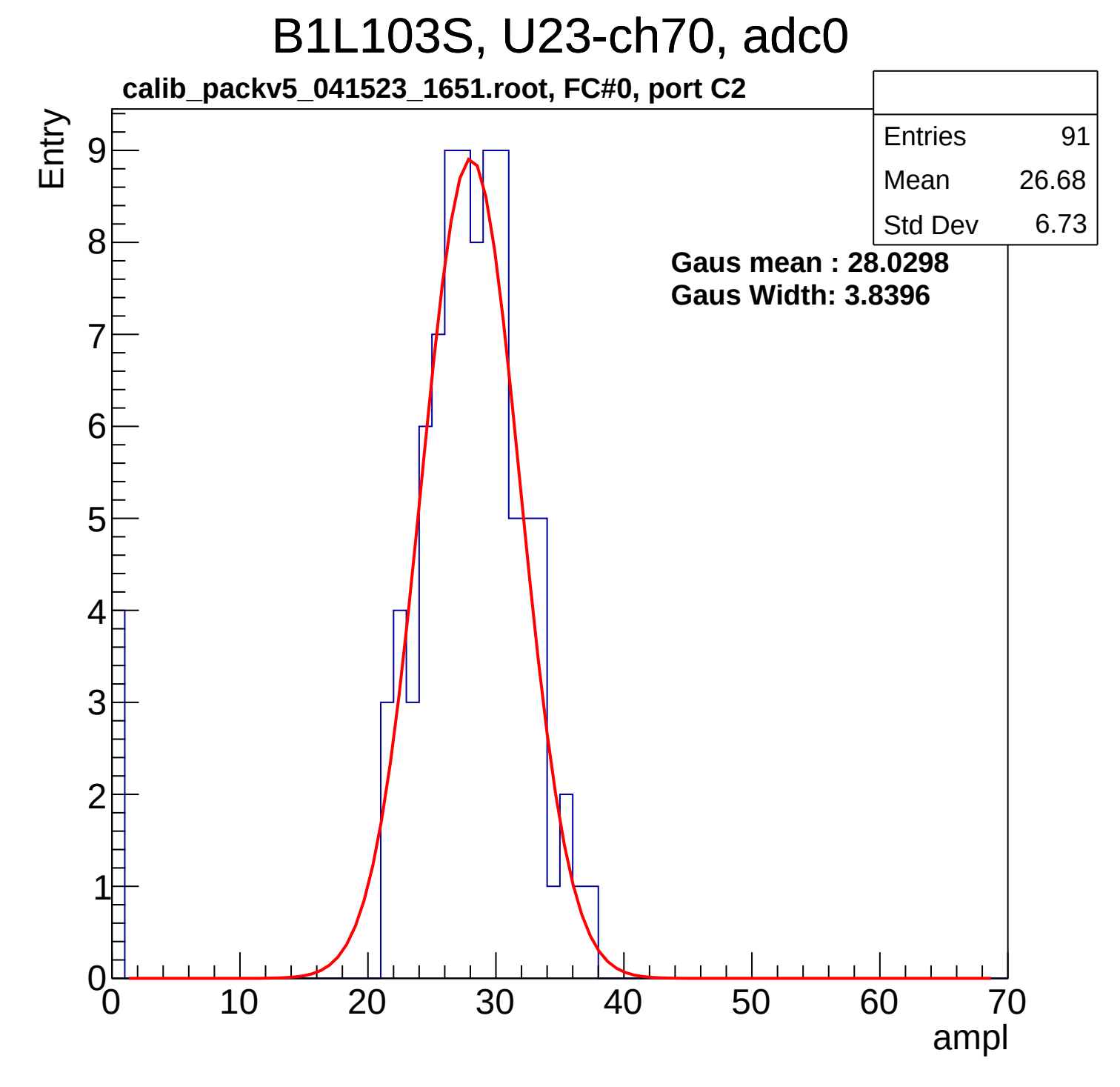

B1L103S, U23-ch69, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 47.55
Std Dev 3.257
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

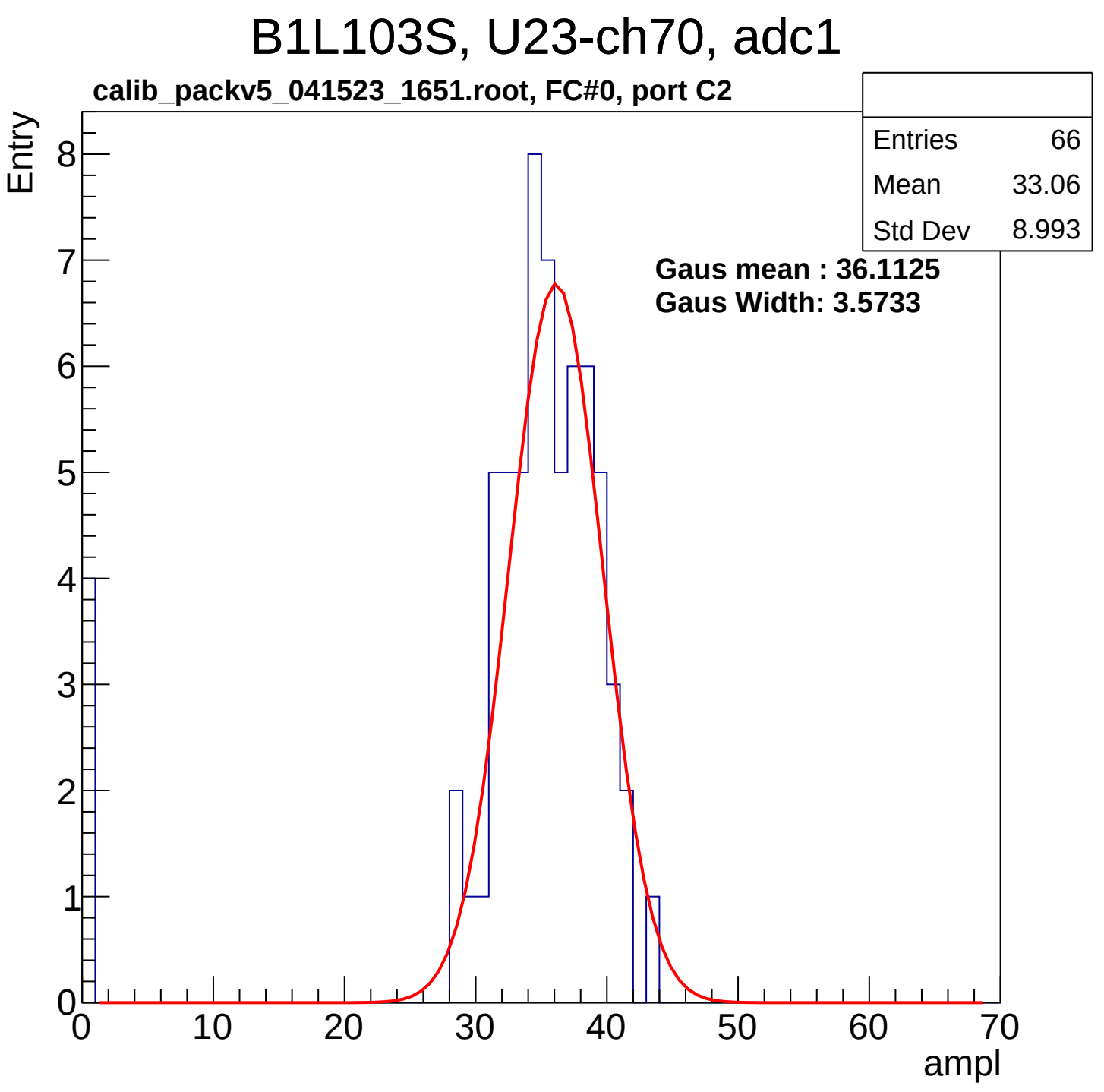

B1L103S, U23-ch69, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 53.86
Std Dev 3.555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

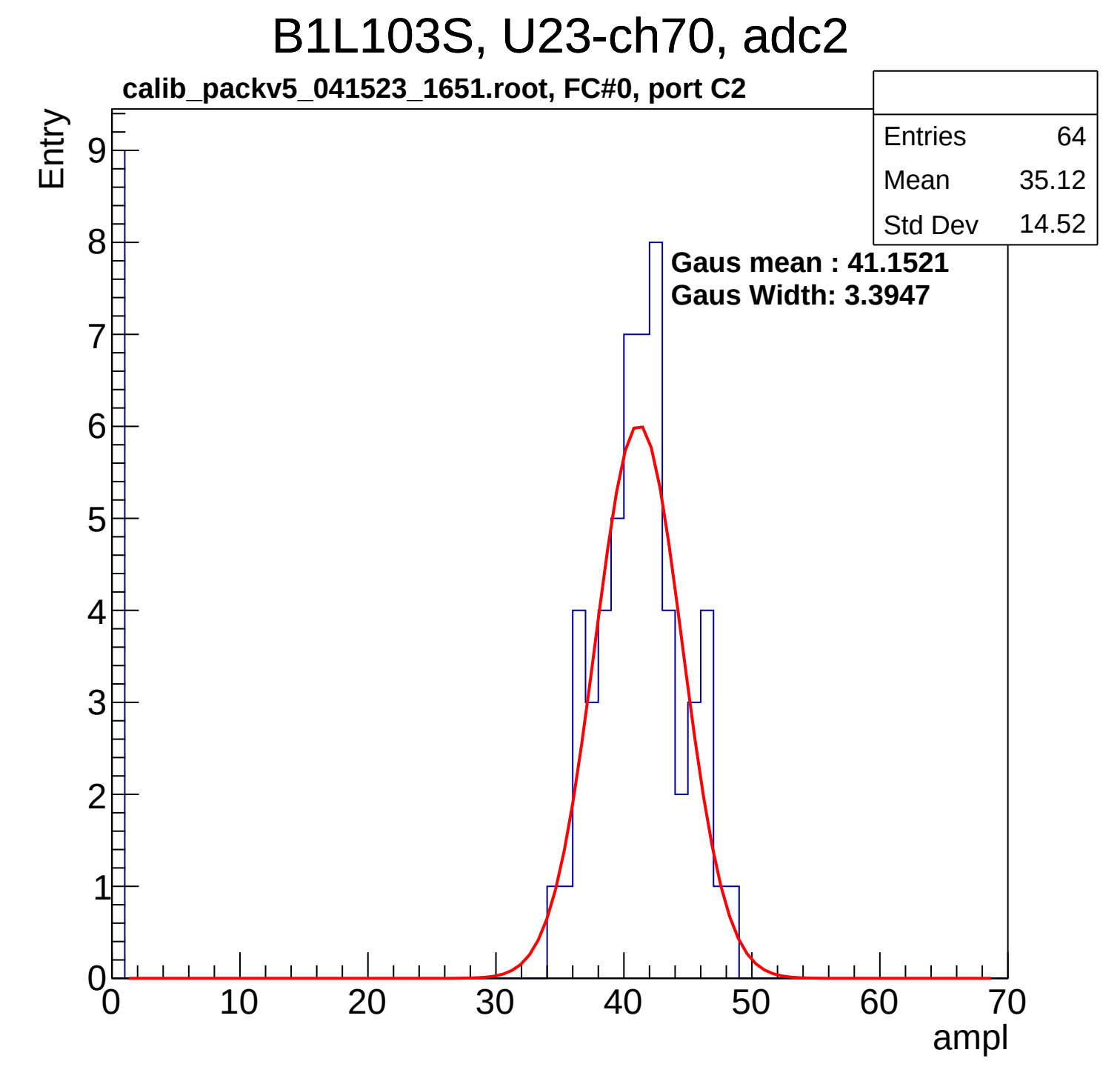

B1L103S, U23-ch69, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 58.29
Std Dev 8.788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

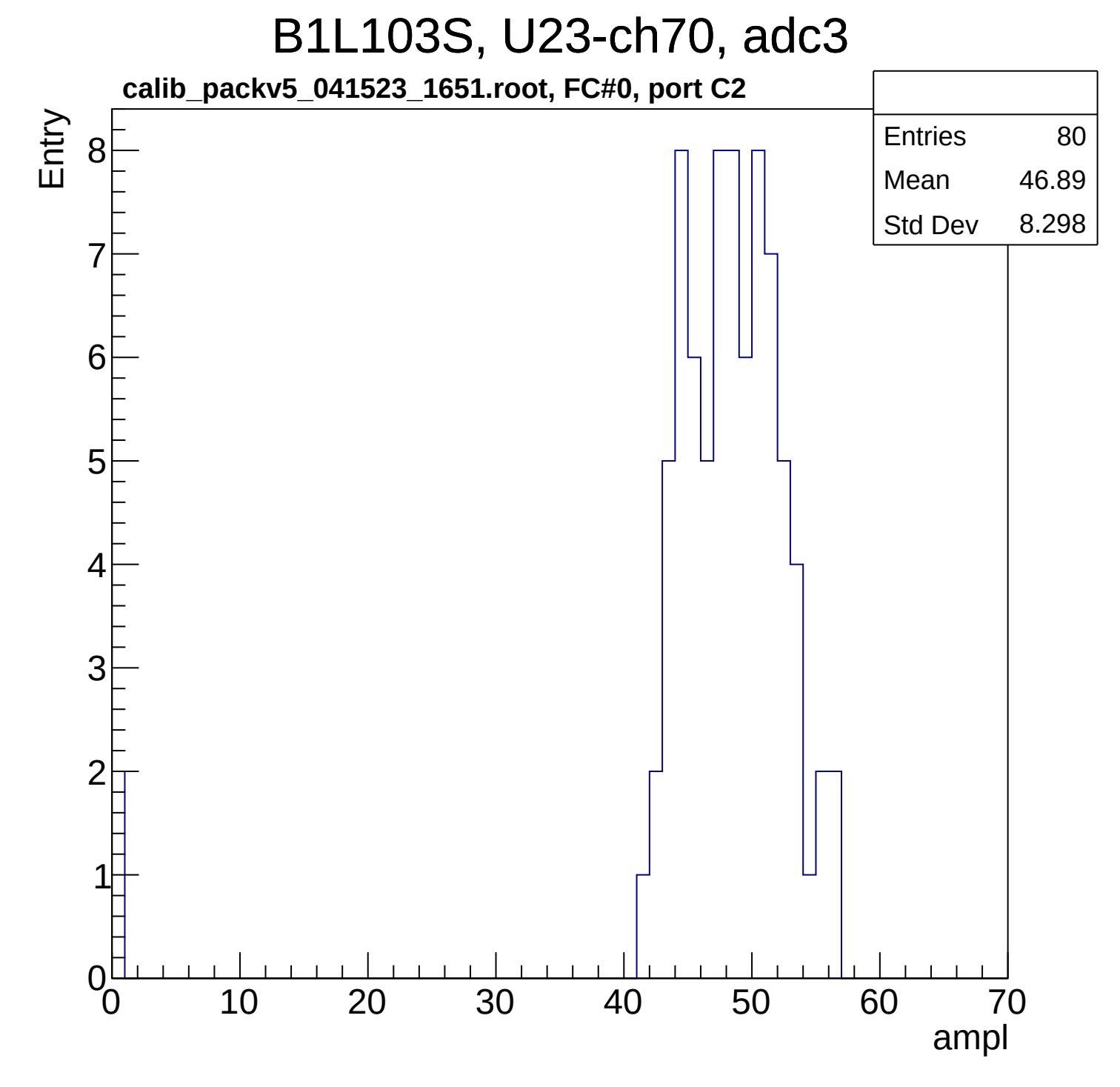

B1L103S, U23-ch69, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 12
Mean 61.17
Std Dev 2.23
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

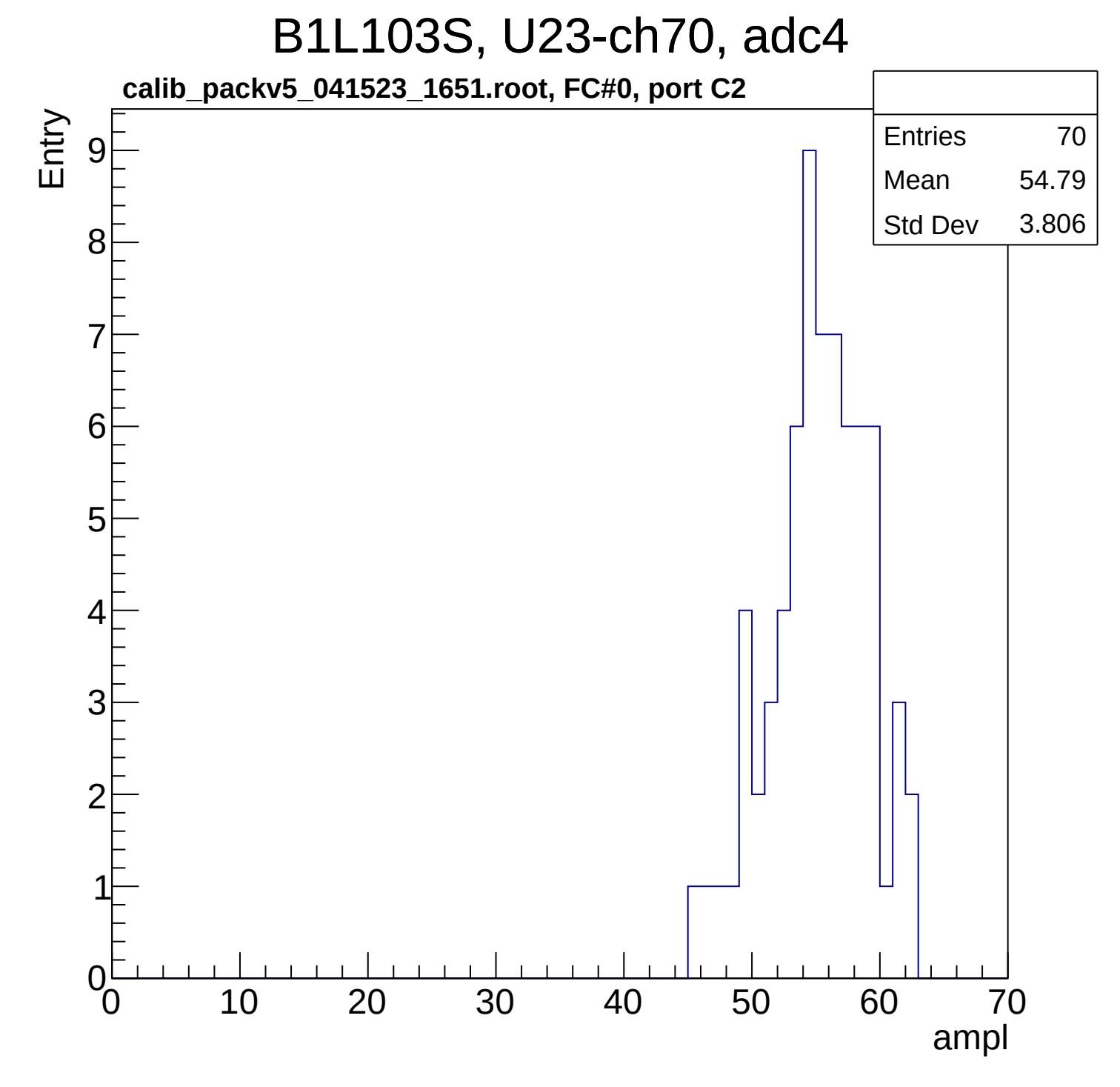

B1L103S, U23-ch69, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

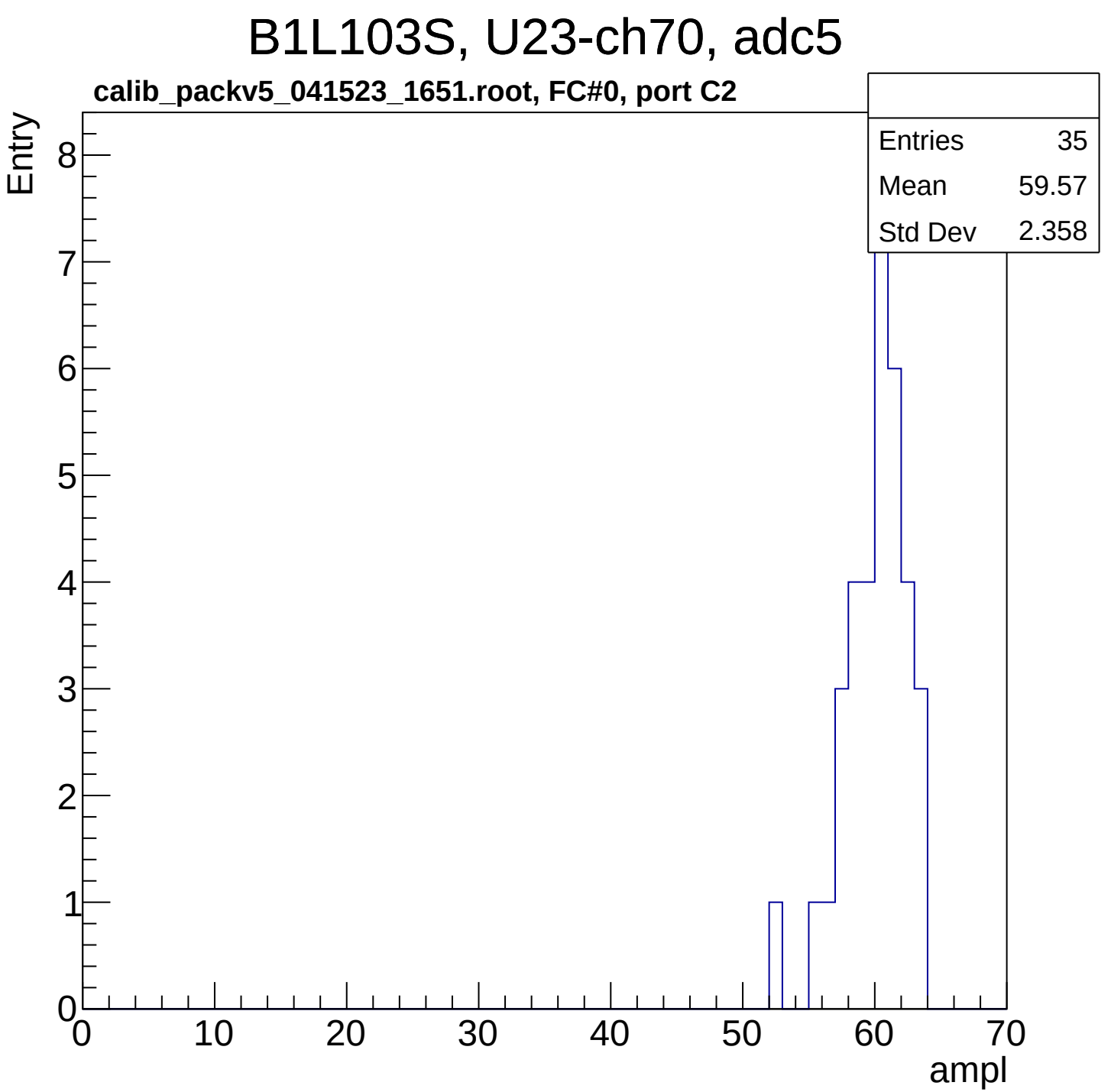

B1L103S, U23-ch70, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 26.68
Std Dev 6.73
Gaus mean : 28.0298
Gaus Width: 3.8396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

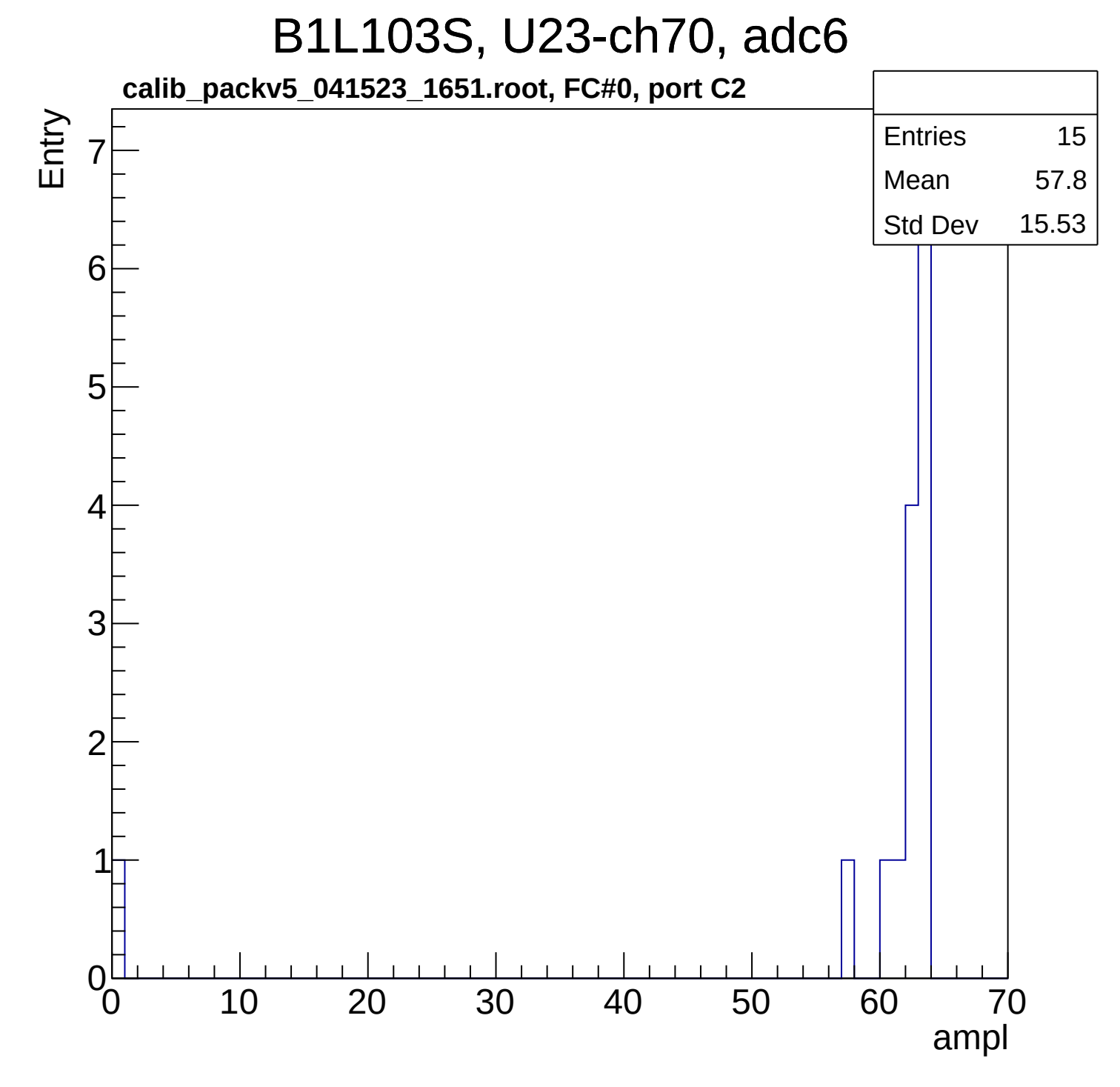

B1L103S, U23-ch70, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 33.06
Std Dev 8.993
Gaus mean : 36.1125
Gaus Width: 3.5733
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

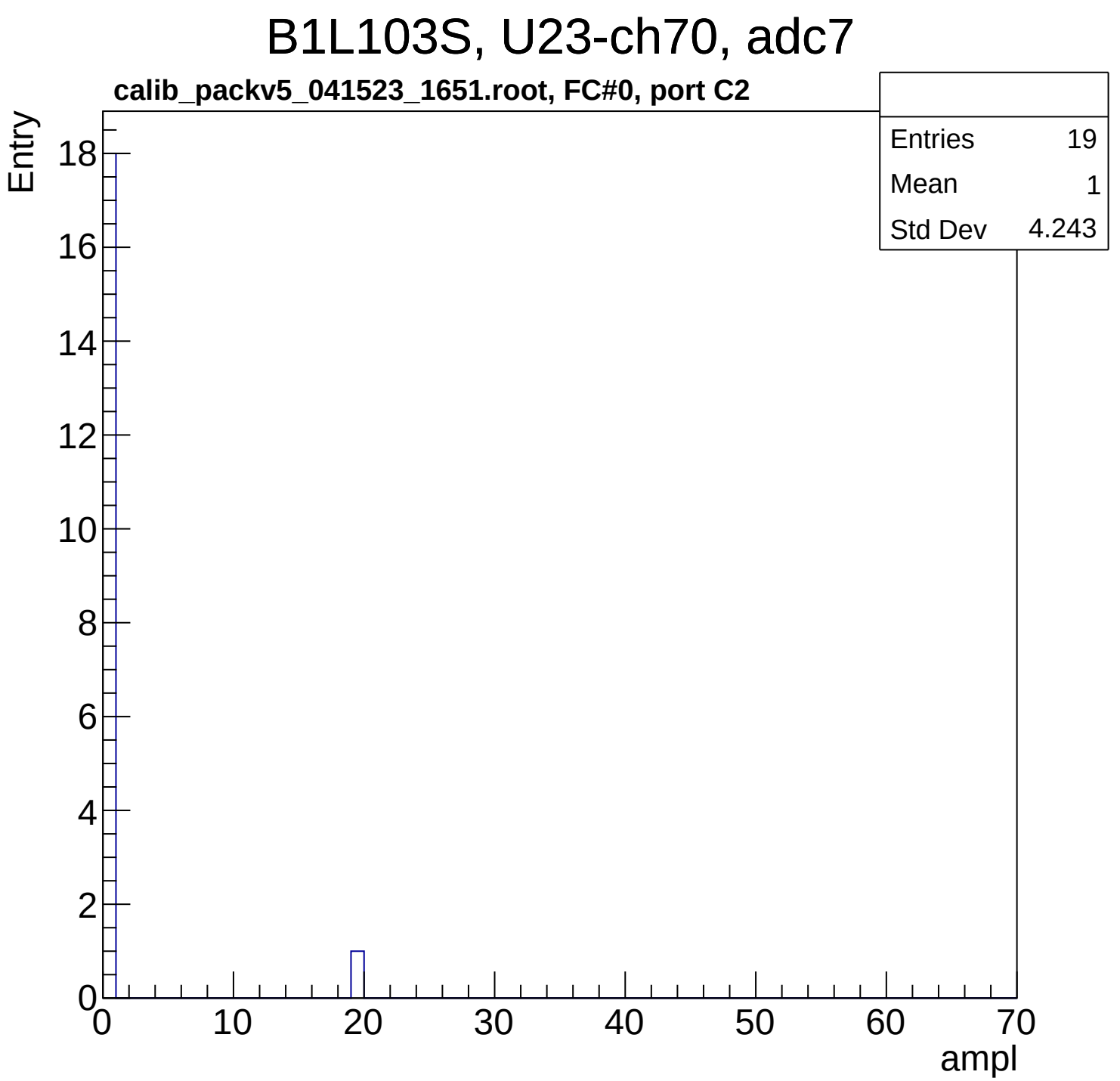

B1L103S, U23-ch70, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 35.12
Std Dev 14.52
Gaus mean : 41.1521
Gaus Width: 3.3947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

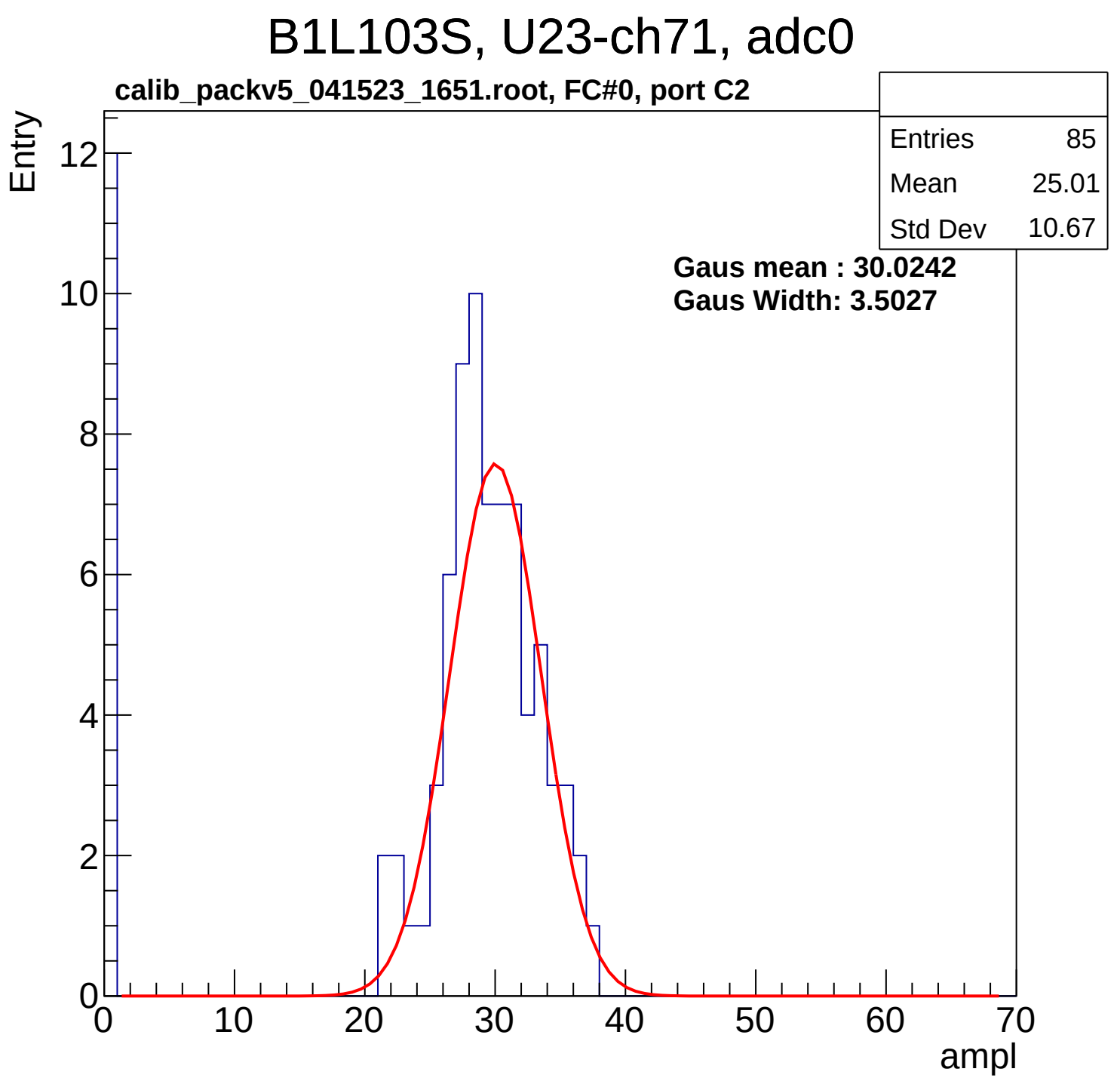

B1L103S, U23-ch70, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 46.89
Std Dev 8.298
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

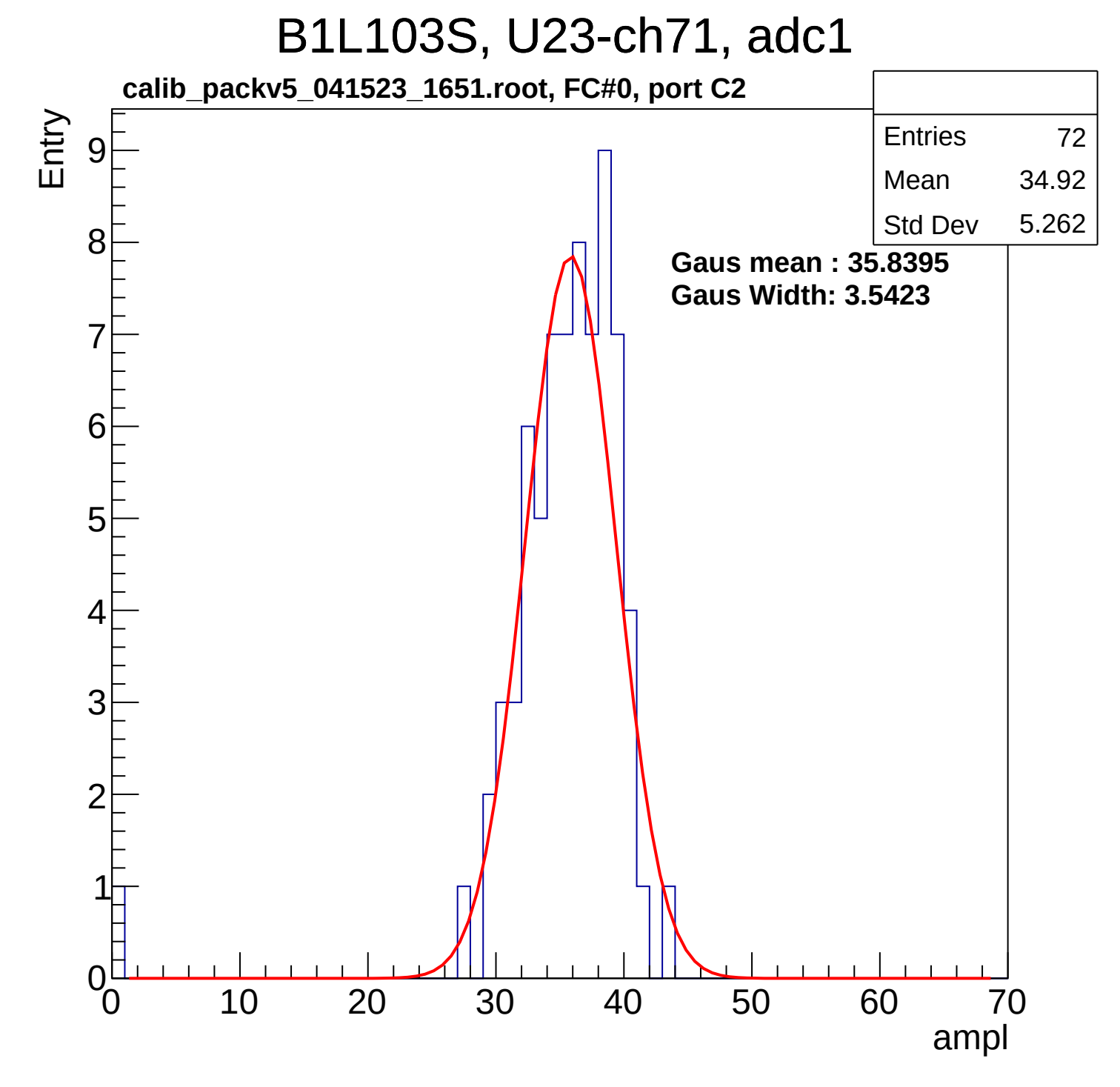

B1L103S, U23-ch70, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 54.79
Std Dev 3.806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

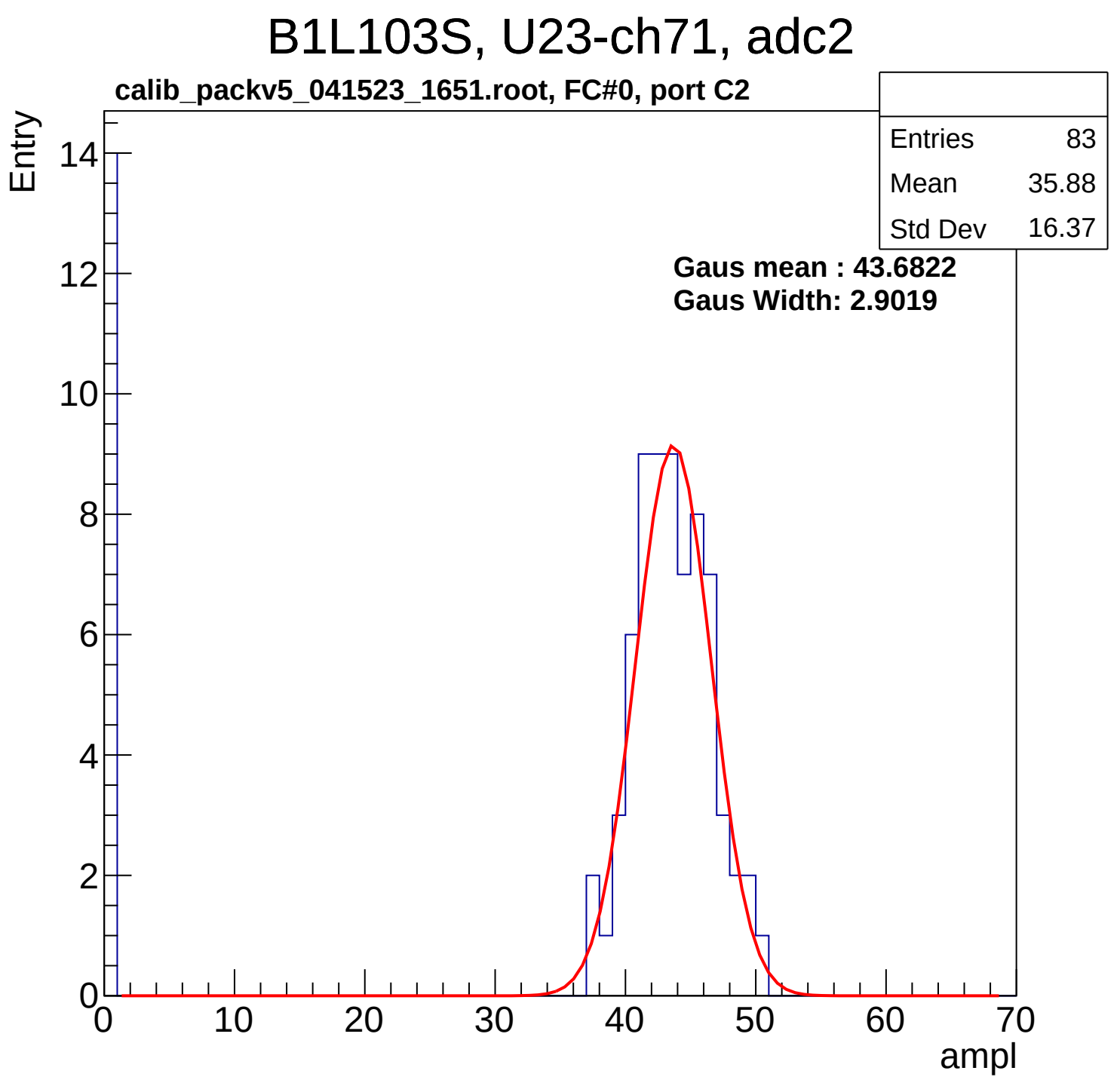

B1L103S, U23-ch70, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 35
Mean 59.57
Std Dev 2.358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

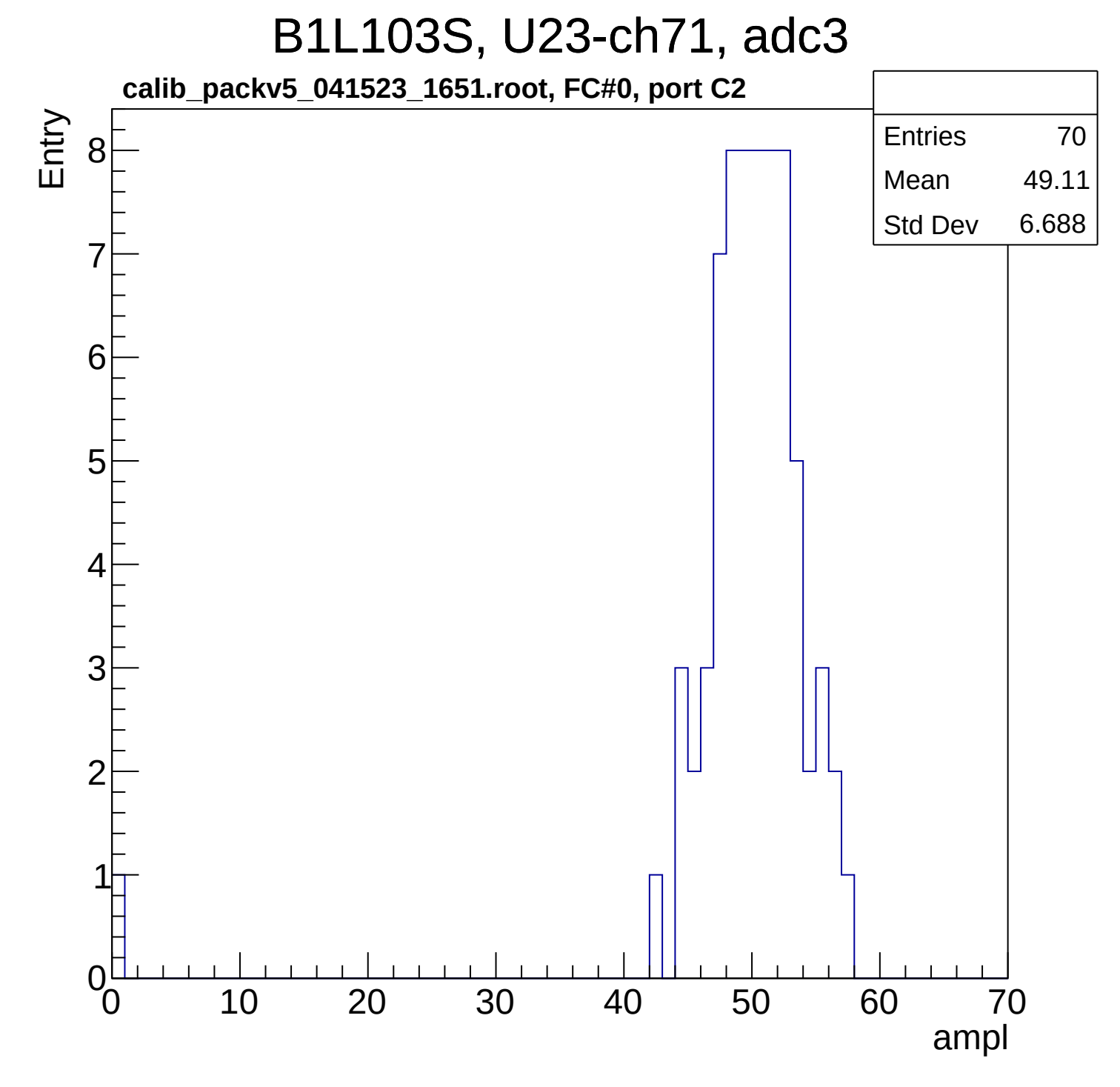

B1L103S, U23-ch70, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 15
Mean 57.8
Std Dev 15.53
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

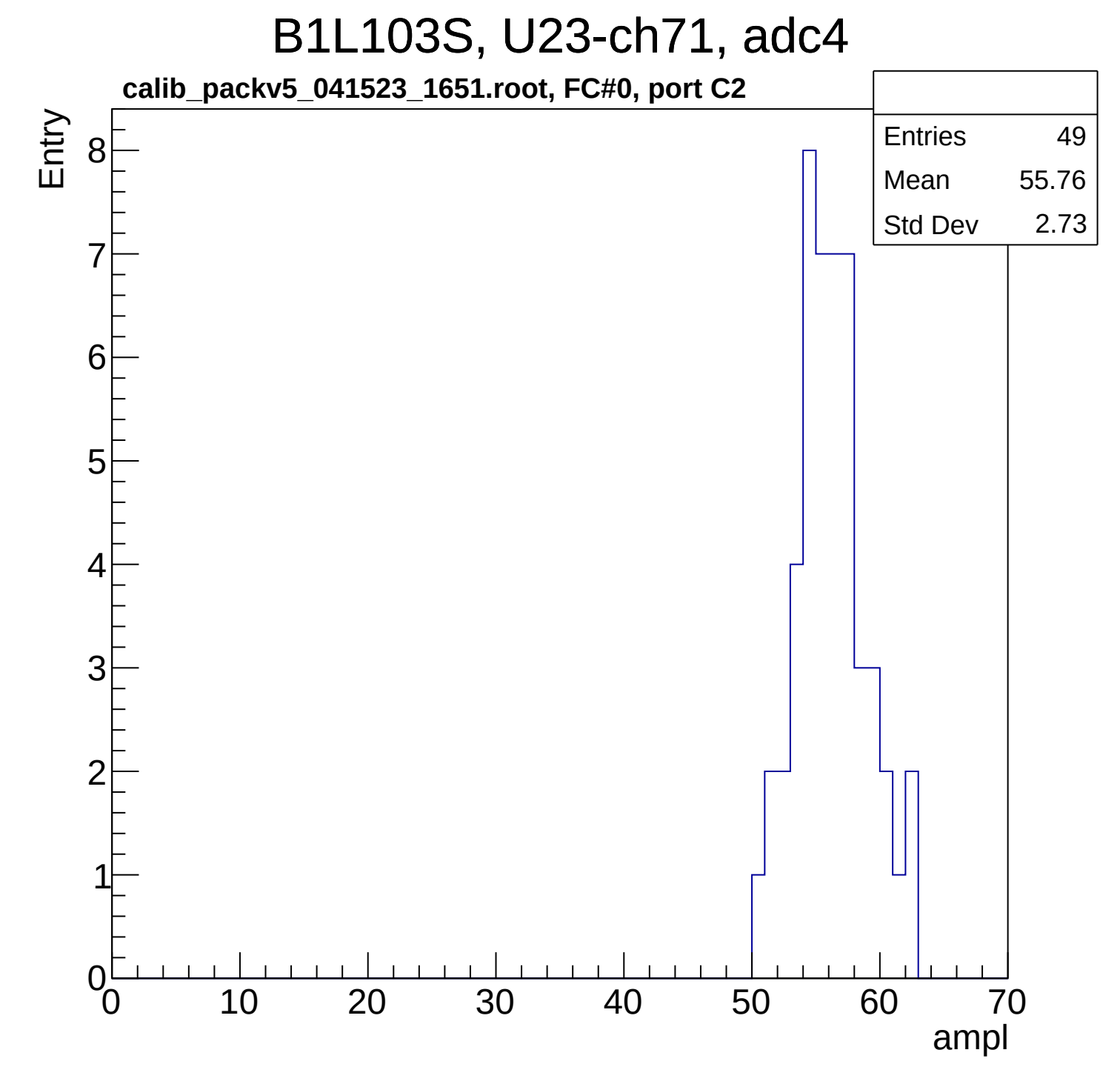

B1L103S, U23-ch70, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1
Std Dev 4.243
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

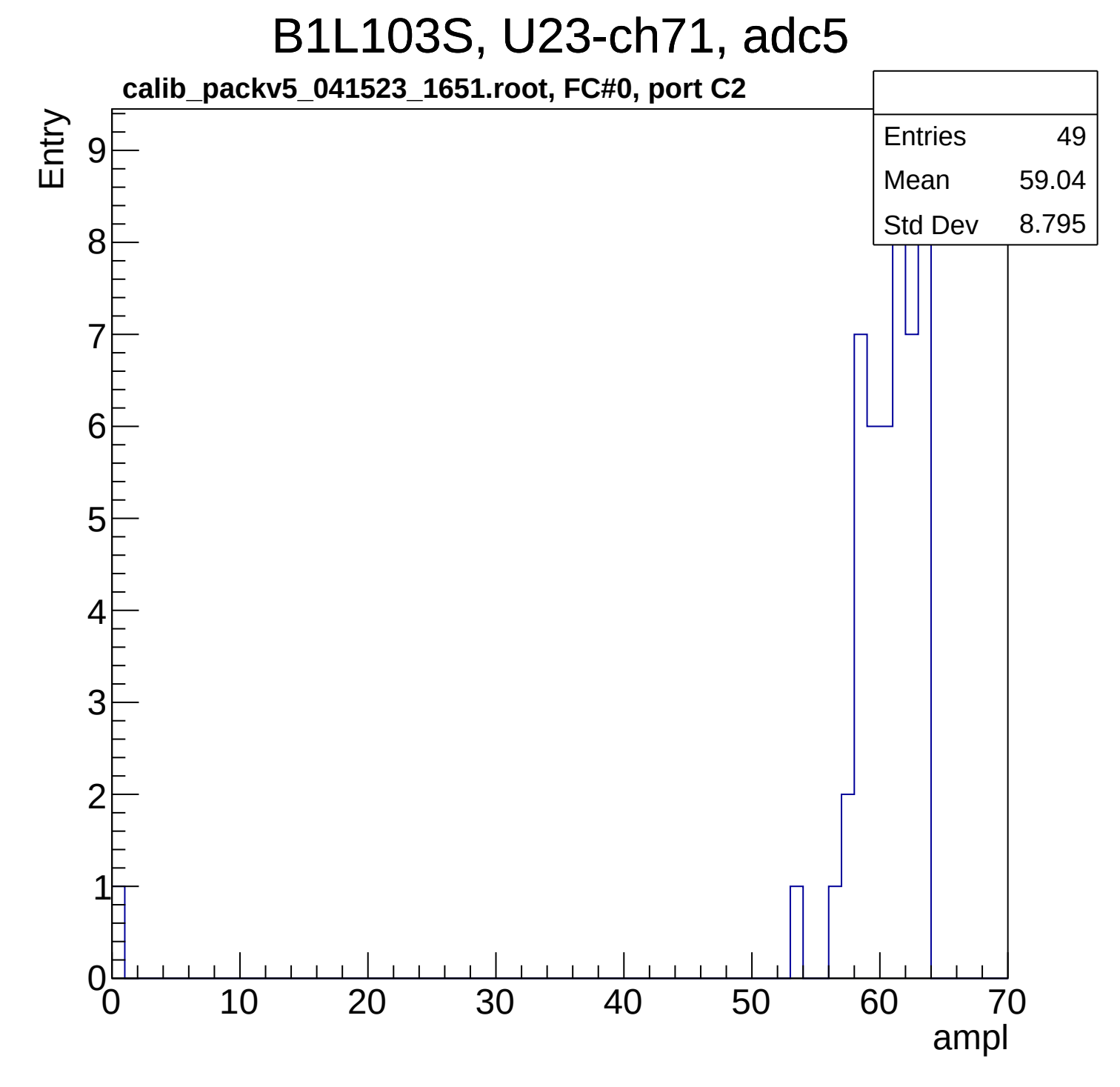

B1L103S, U23-ch71, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 25.01
Std Dev 10.67
Gaus mean : 30.0242
Gaus Width: 3.5027
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

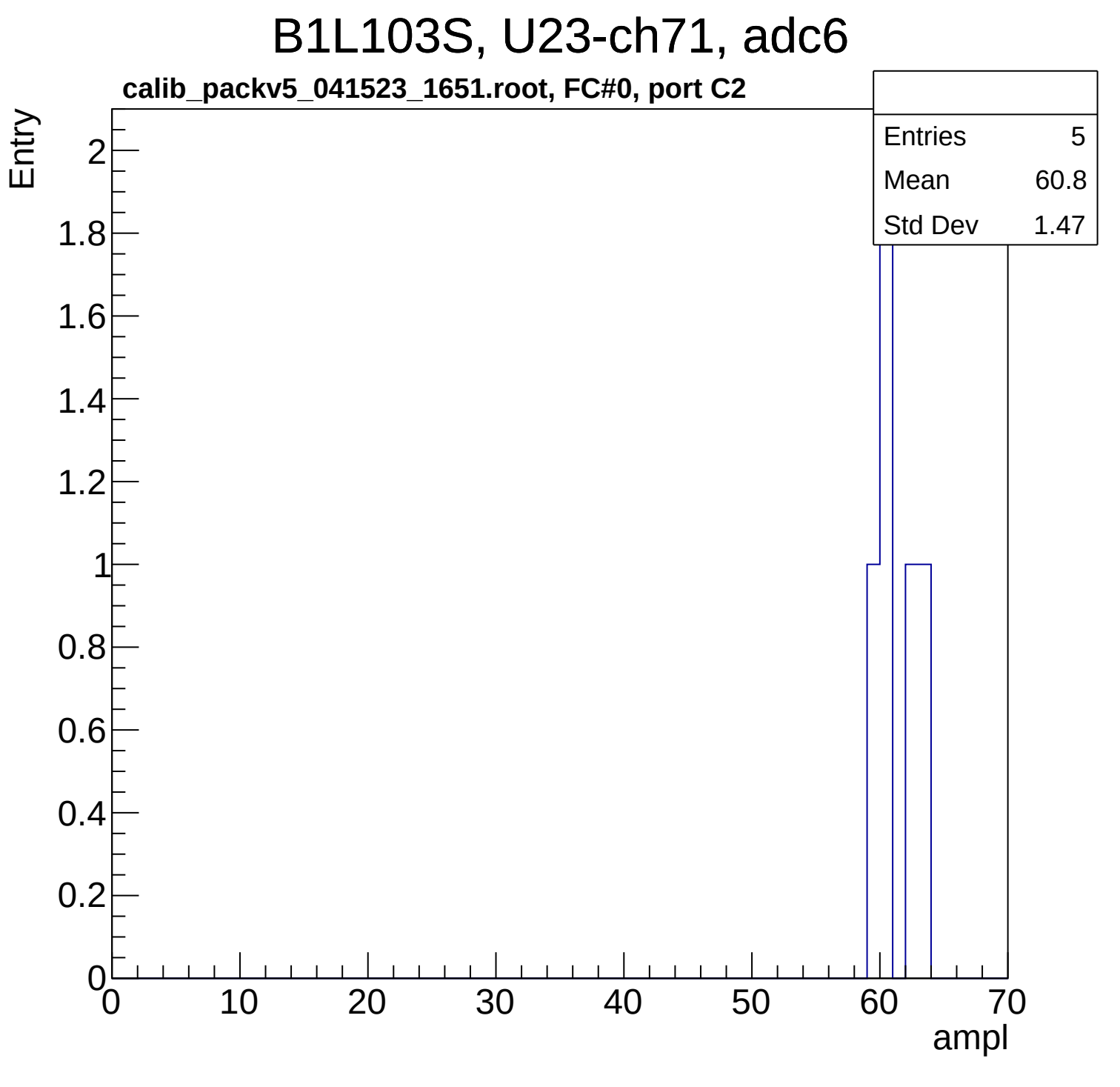

B1L103S, U23-ch71, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 34.92
Std Dev 5.262
Gaus mean : 35.8395
Gaus Width: 3.5423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

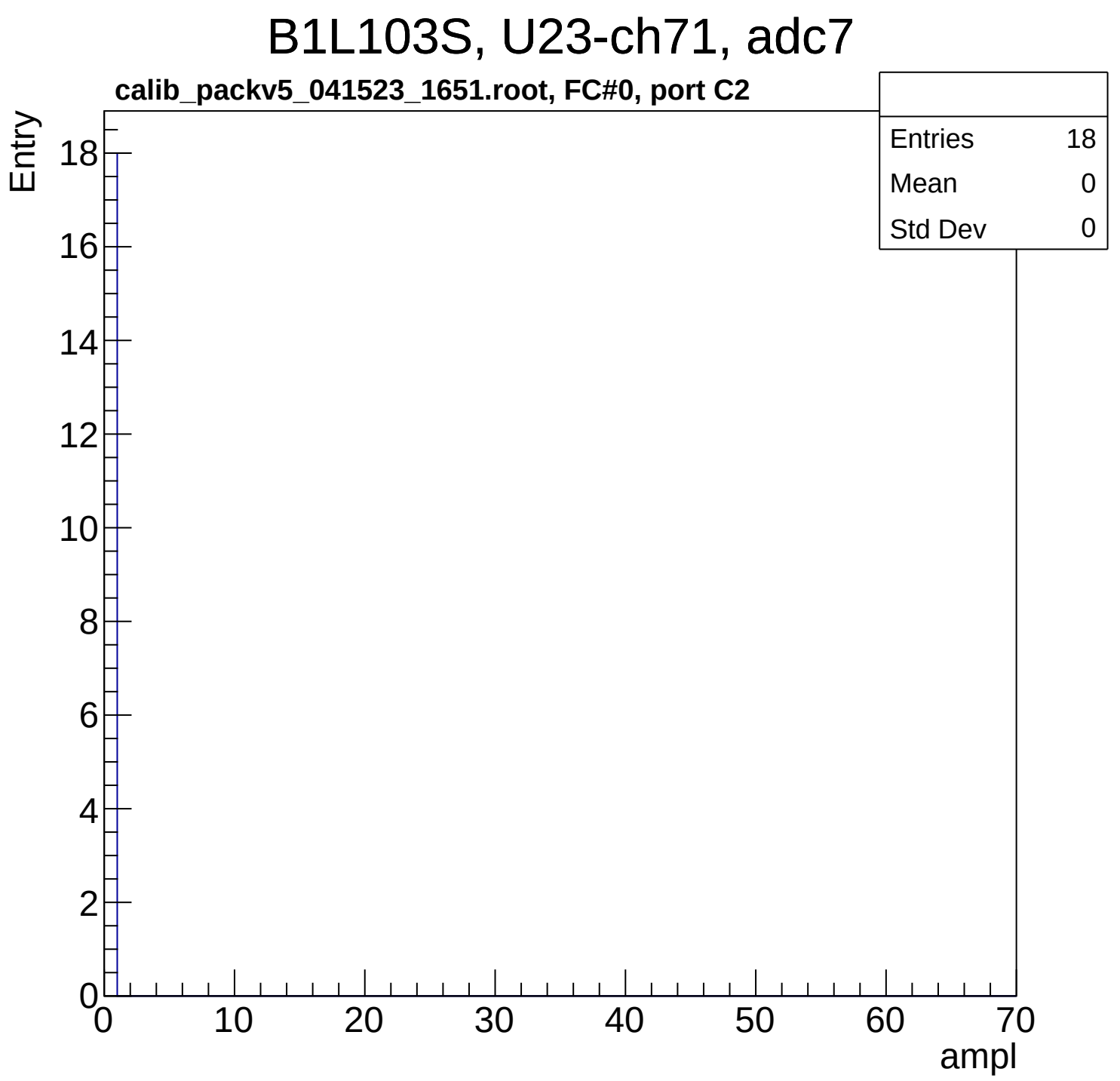

B1L103S, U23-ch71, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 35.88
Std Dev 16.37
Gaus mean : 43.6822
Gaus Width: 2.9019
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

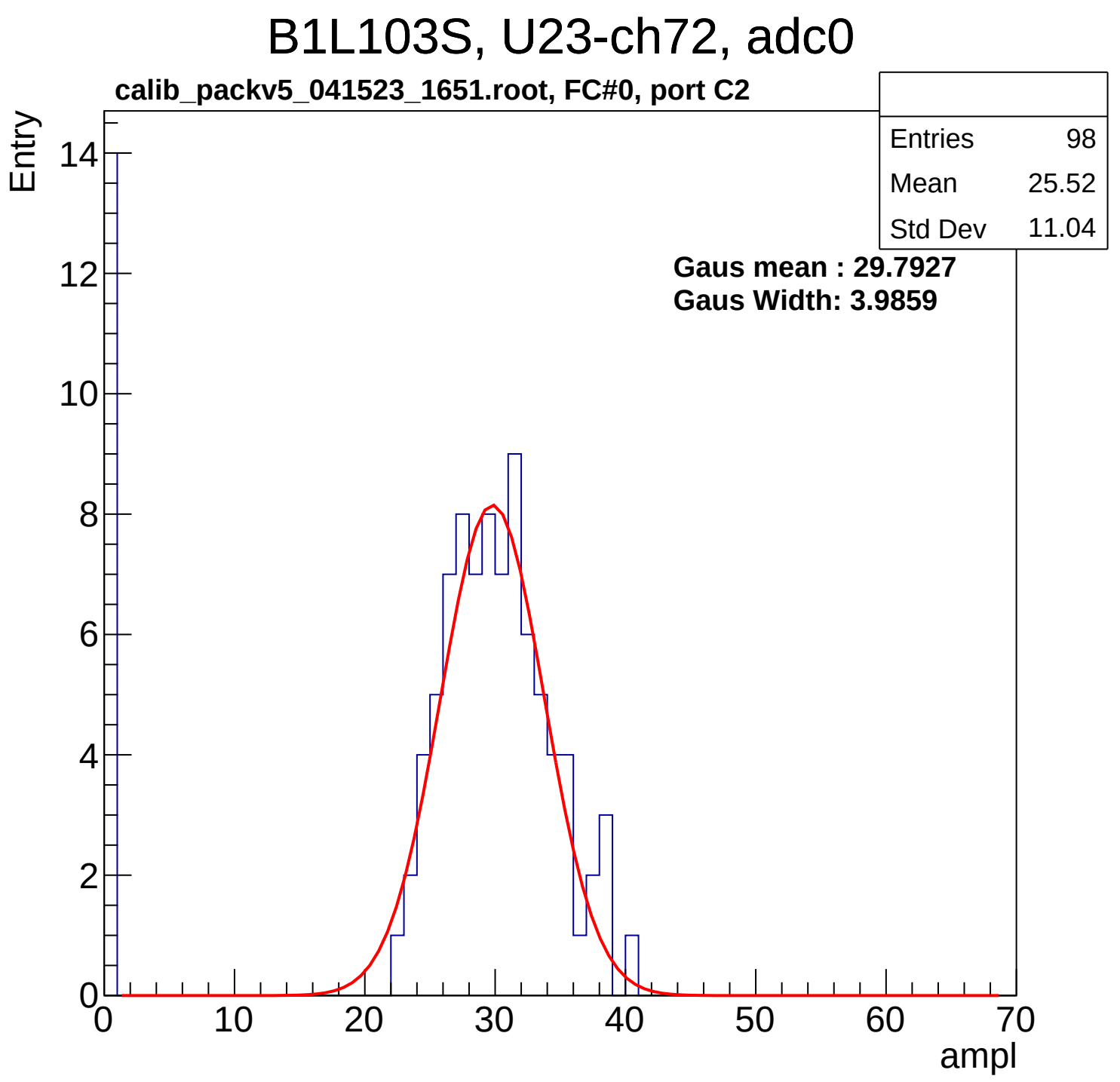

B1L103S, U23-ch71, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 49.11
Std Dev 6.688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

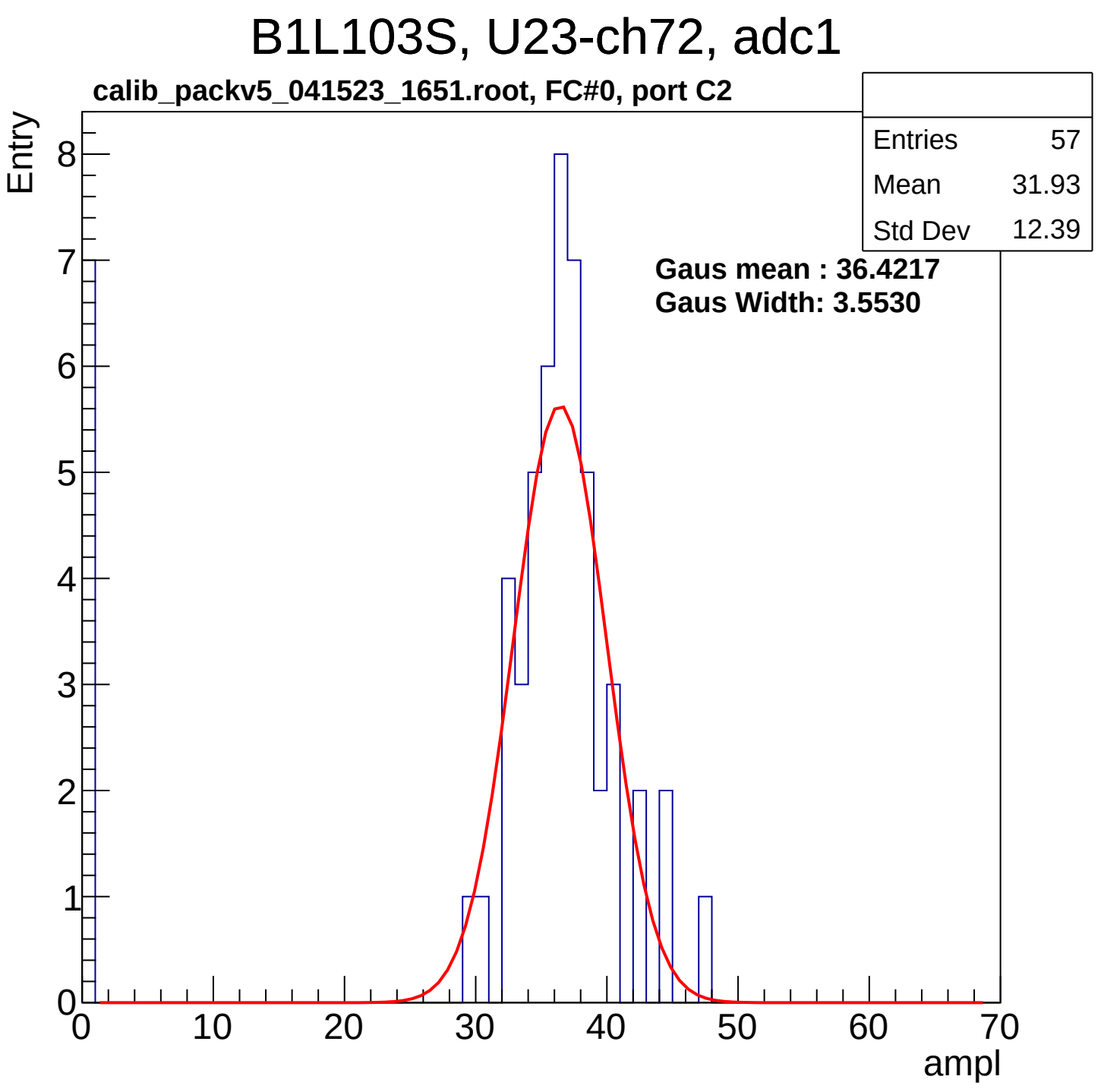

B1L103S, U23-ch71, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 55.76
Std Dev 2.73
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

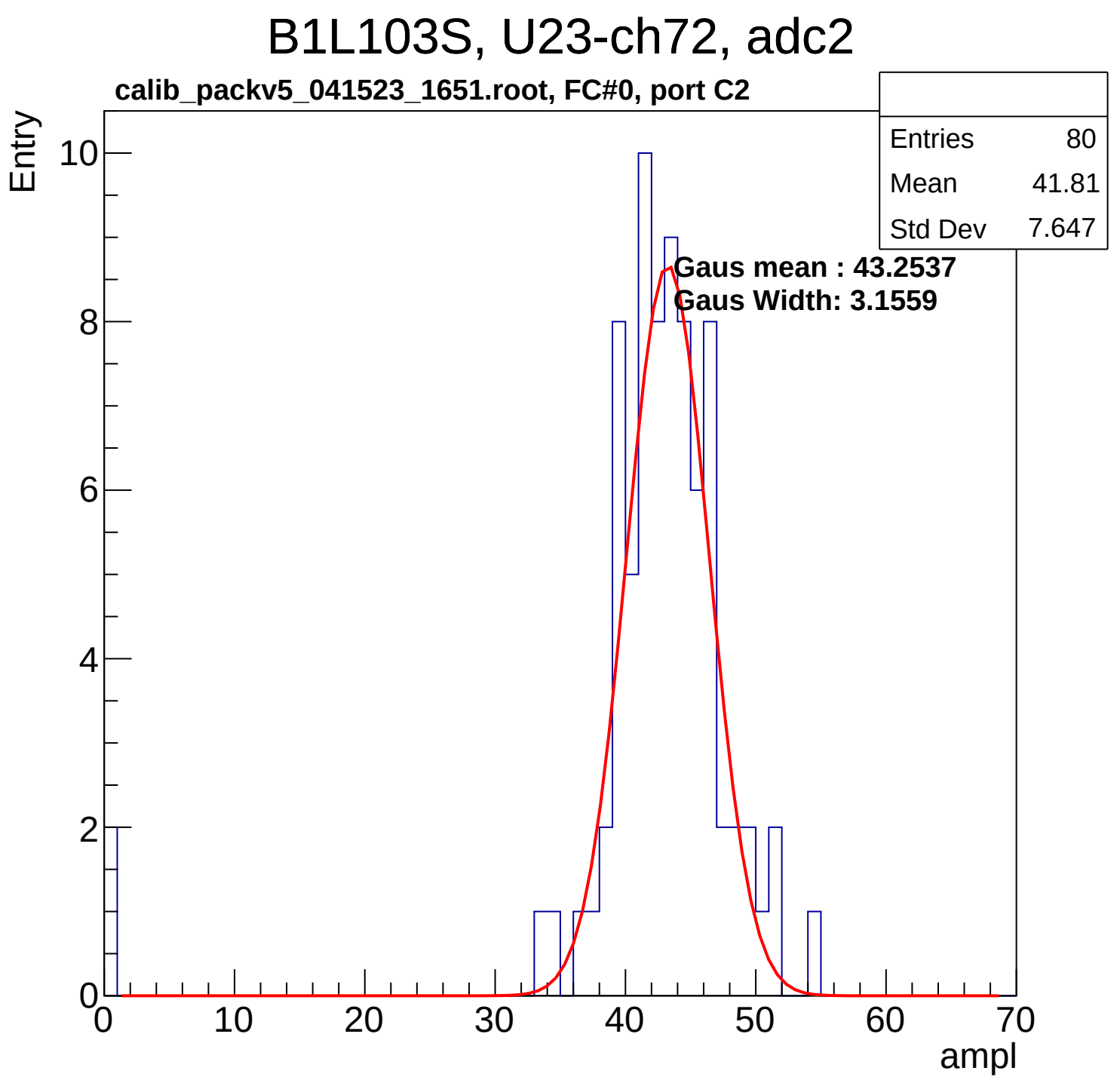

B1L103S, U23-ch71, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 59.04
Std Dev 8.795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

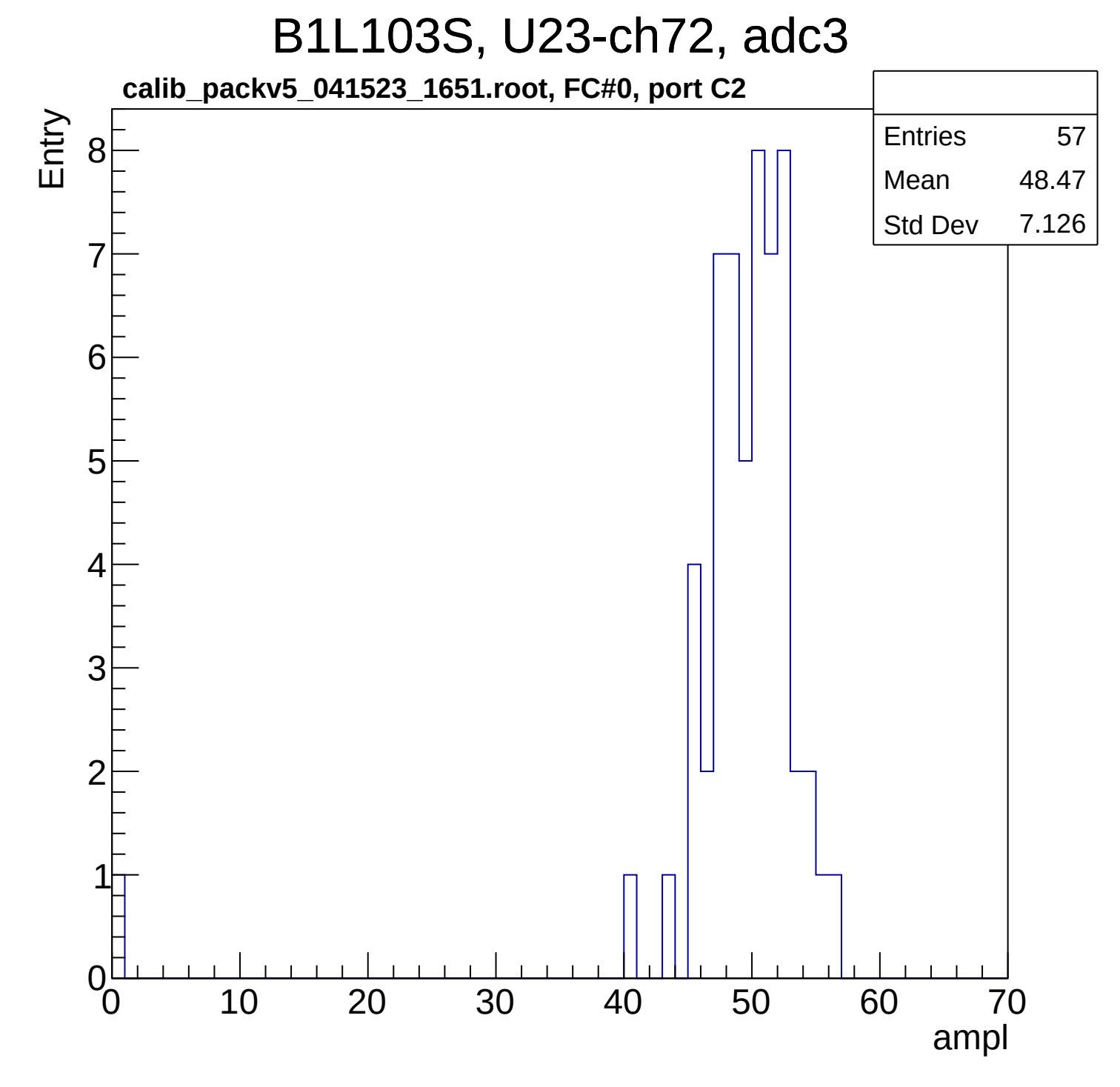

B1L103S, U23-ch71, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 60.8
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

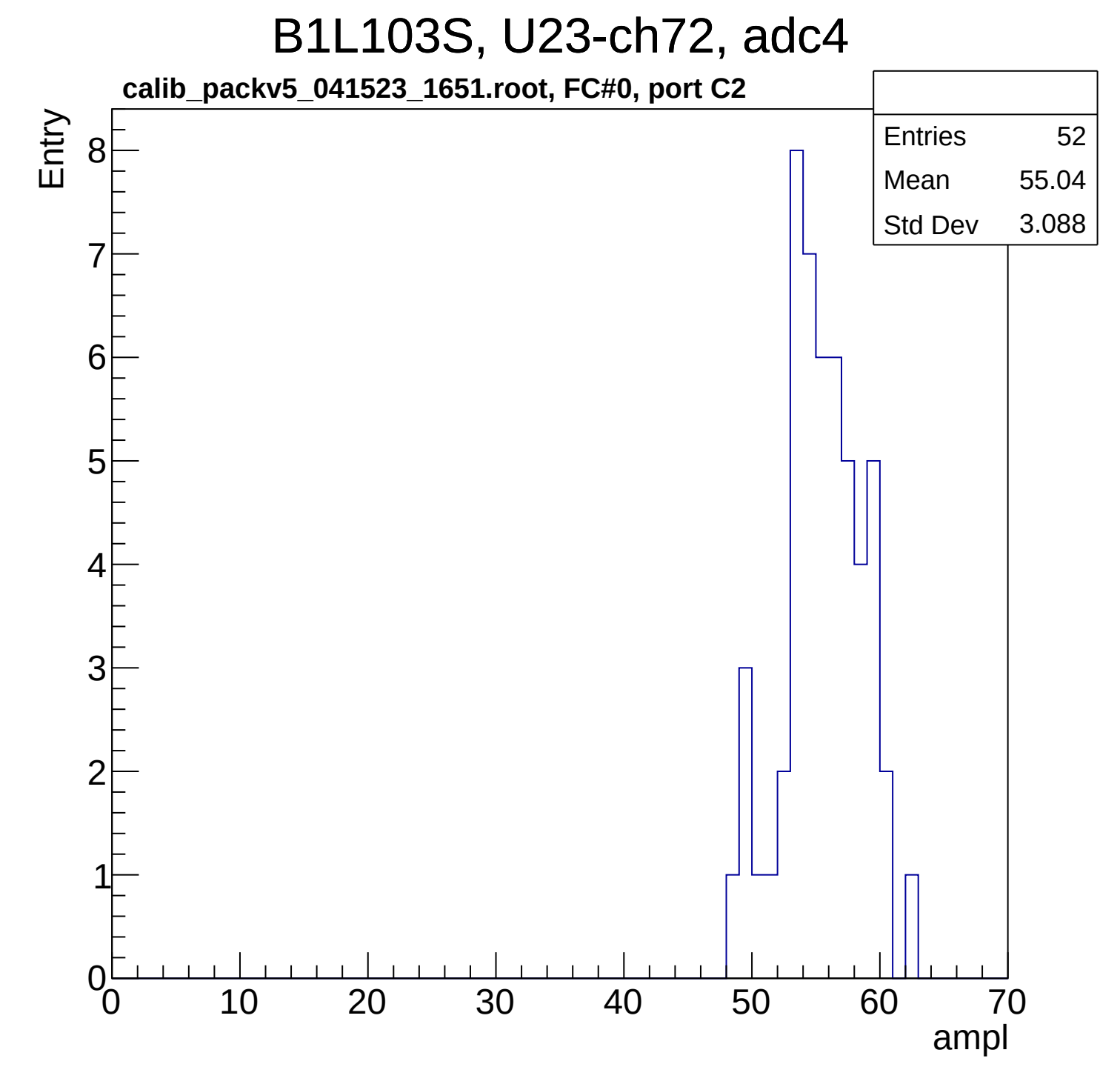

B1L103S, U23-ch71, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

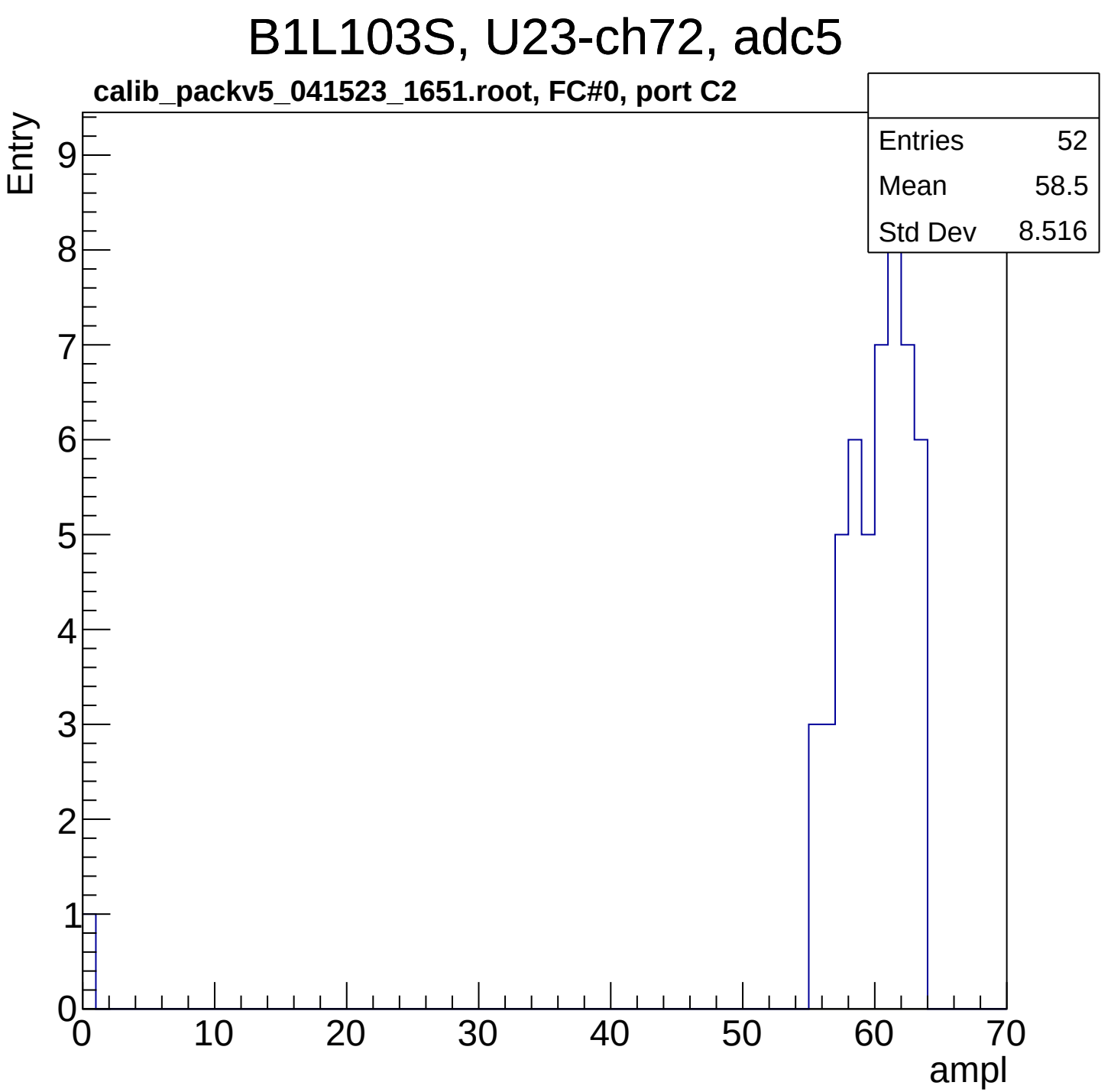

B1L103S, U23-ch72, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 98
Mean 25.52
Std Dev 11.04
Gaus mean : 29.7927
Gaus Width: 3.9859
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

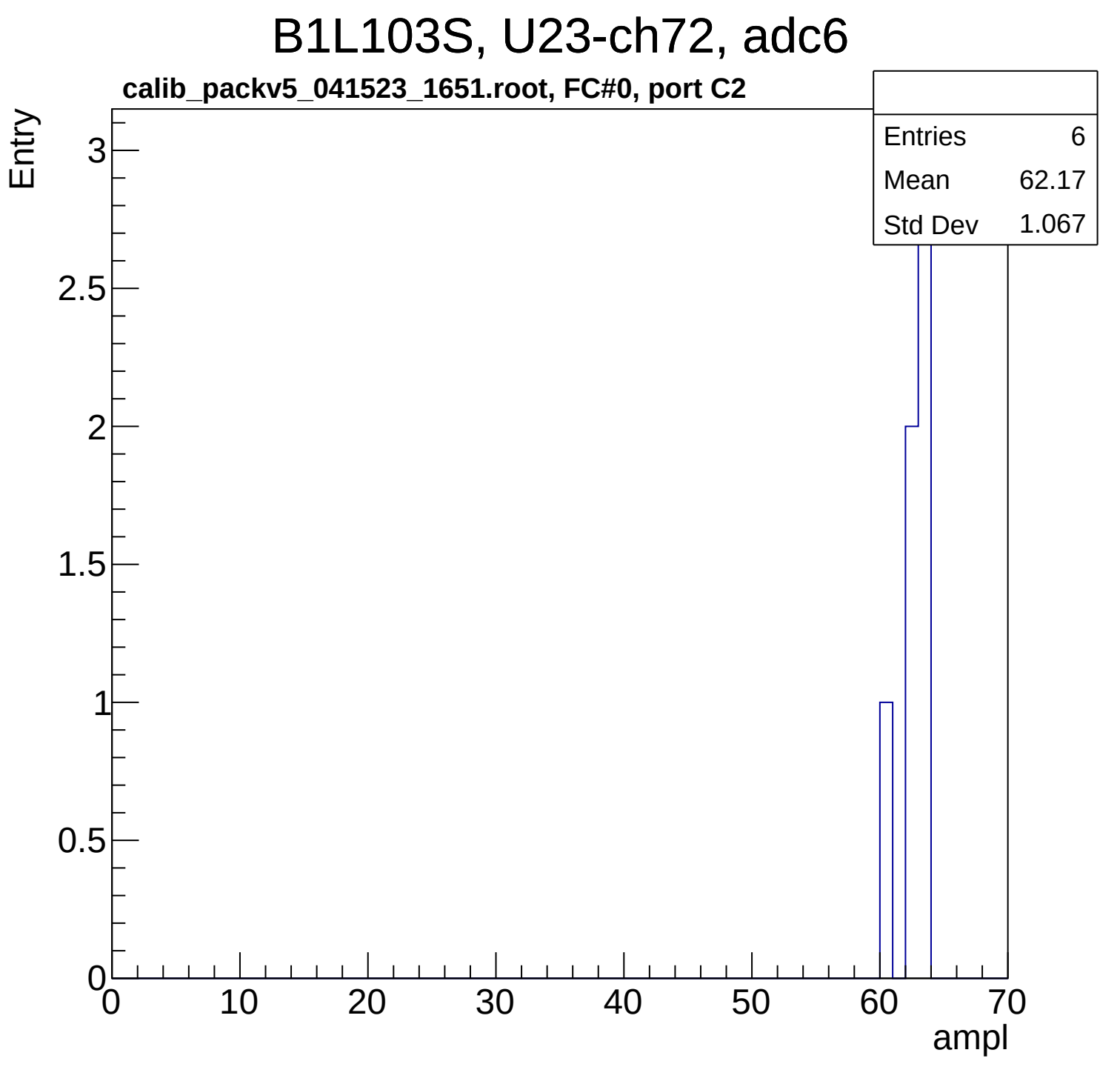

B1L103S, U23-ch72, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 31.93
Std Dev 12.39
Gaus mean : 36.4217
Gaus Width: 3.5530
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

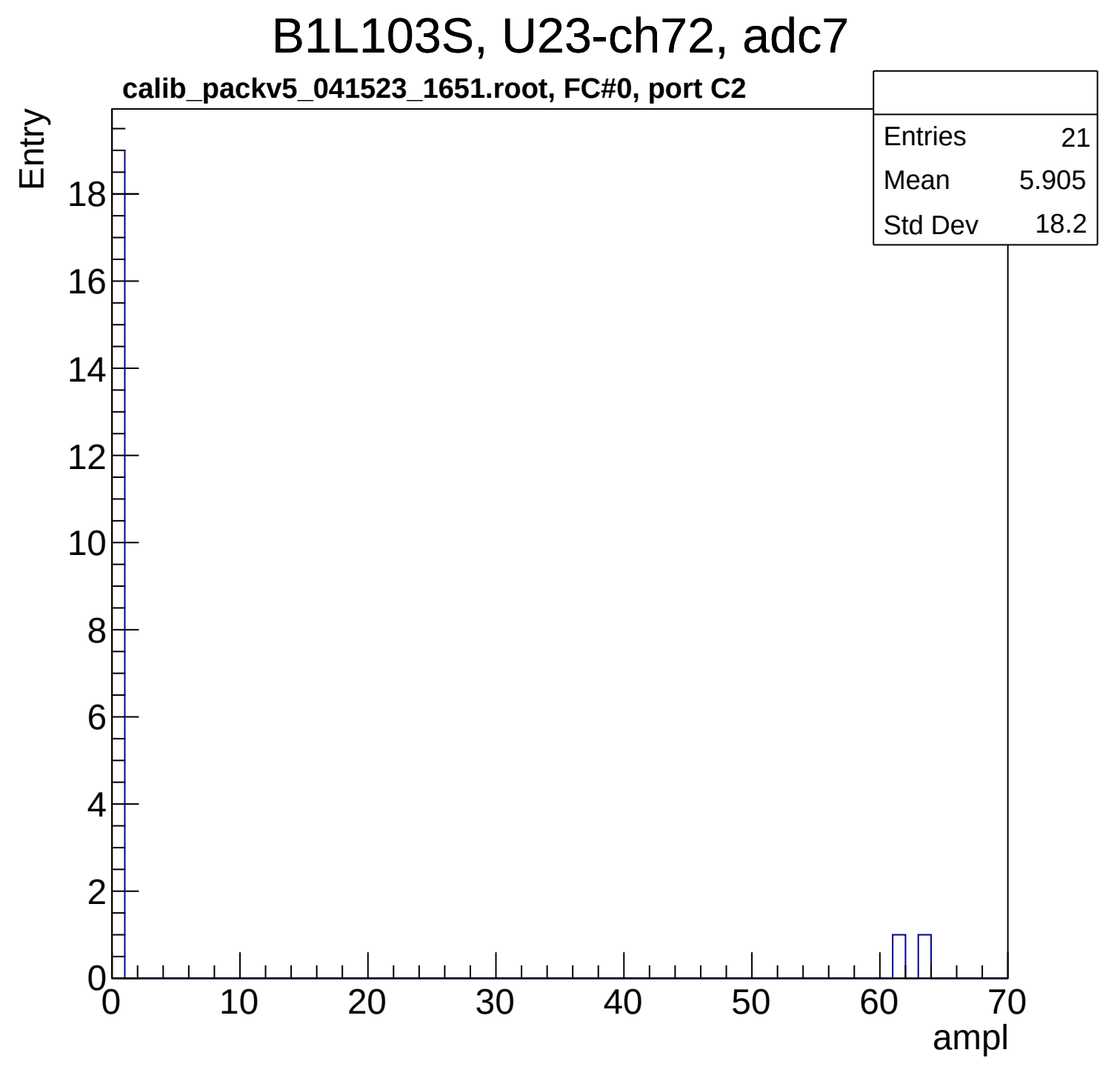

B1L103S, U23-ch72, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 41.81
Std Dev 7.647
Gaus mean : 43.2537
Gaus Width: 3.1559
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

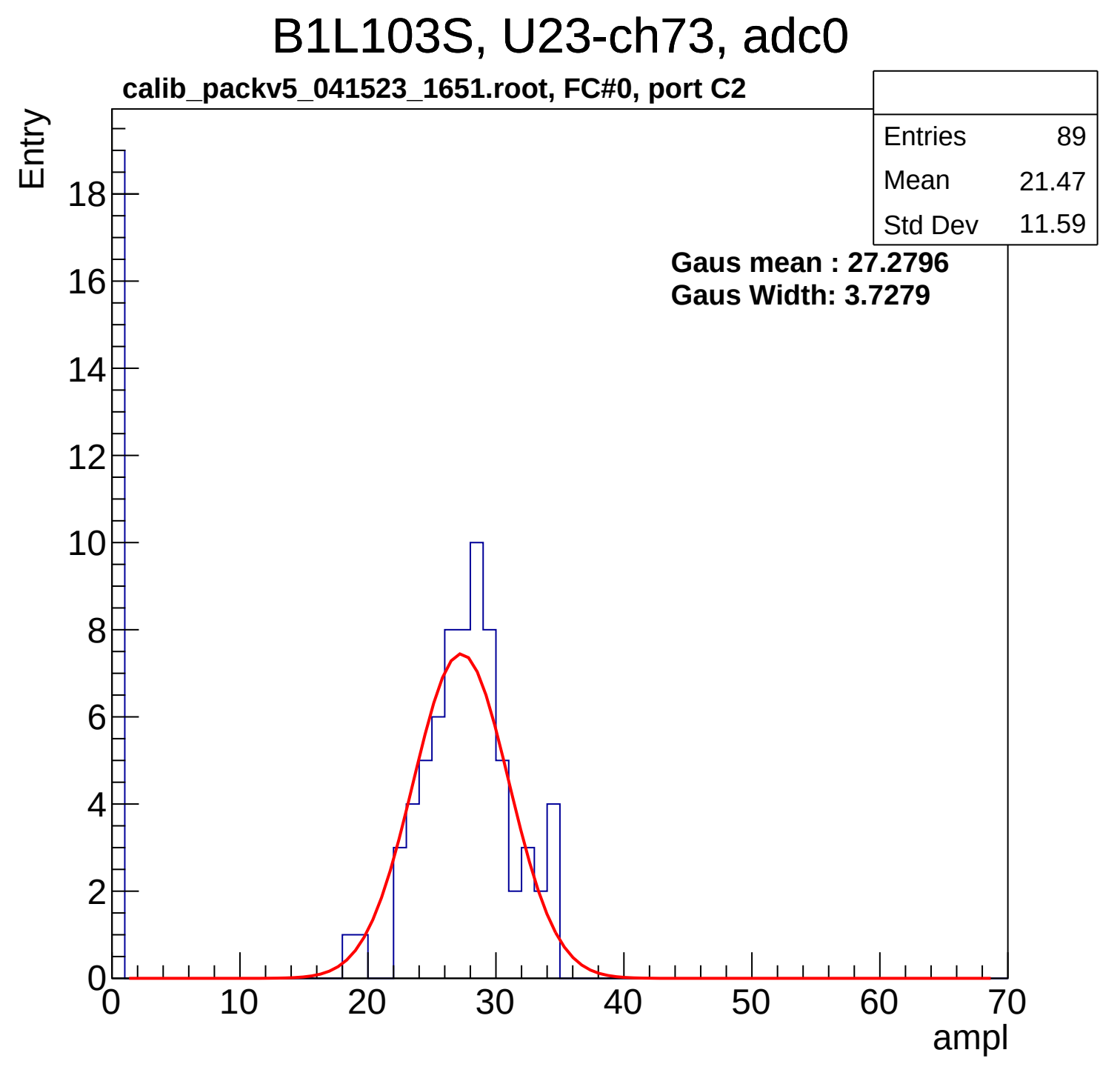

B1L103S, U23-ch72, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 48.47
Std Dev 7.126
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

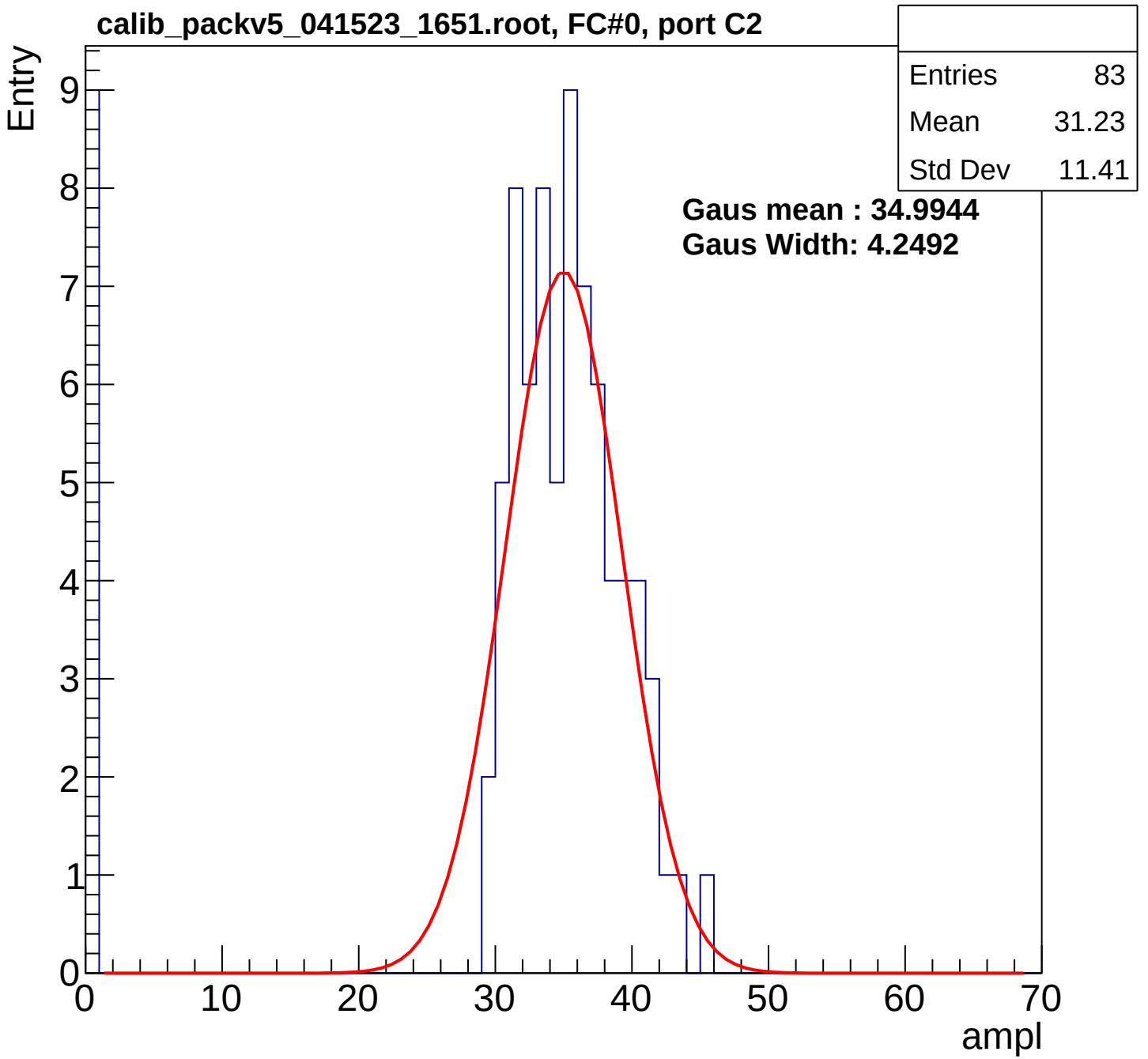

B1L103S, U23-ch72, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 55.04
Std Dev 3.088
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

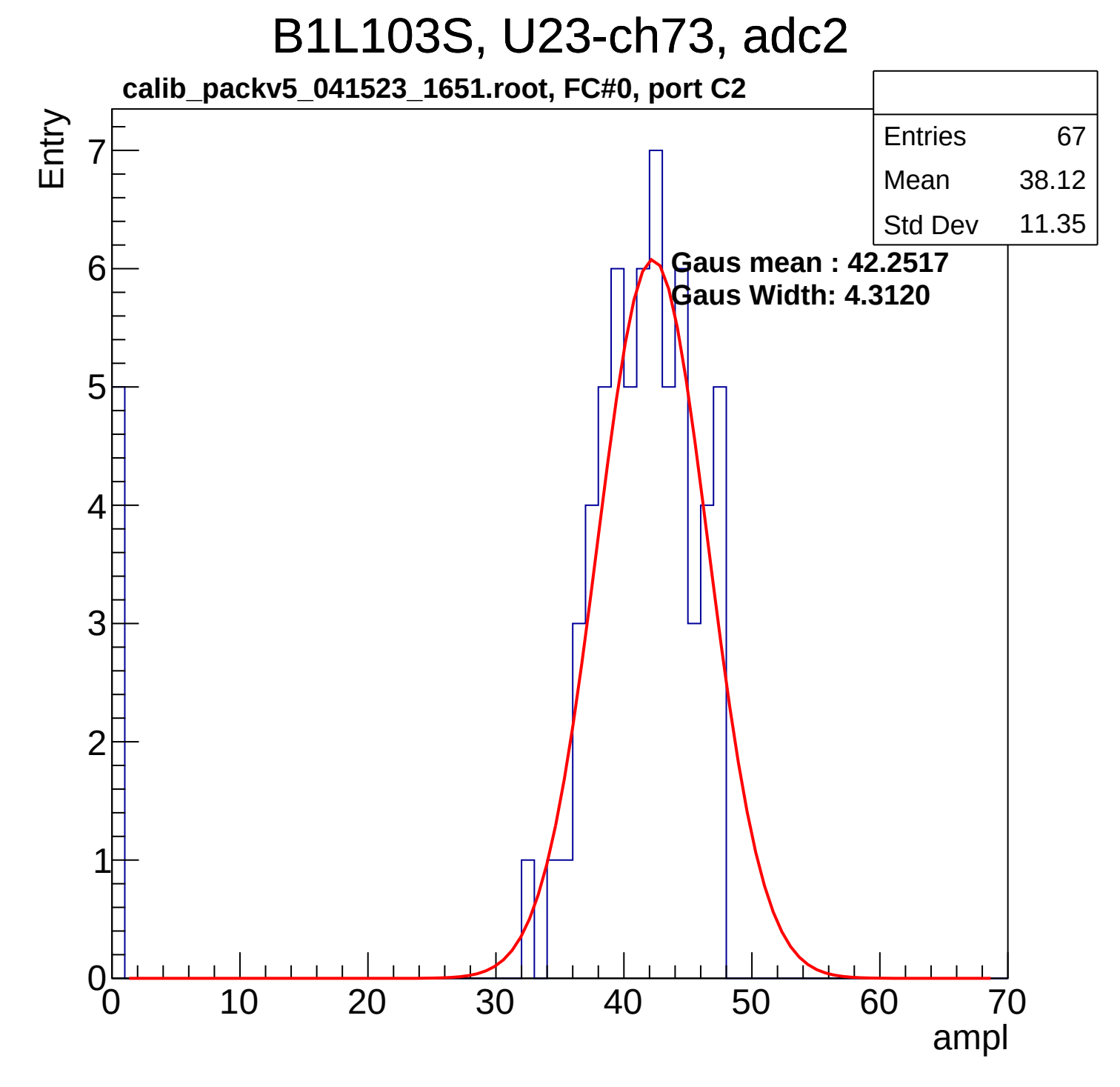

B1L103S, U23-ch72, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 58.5
Std Dev 8.516
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

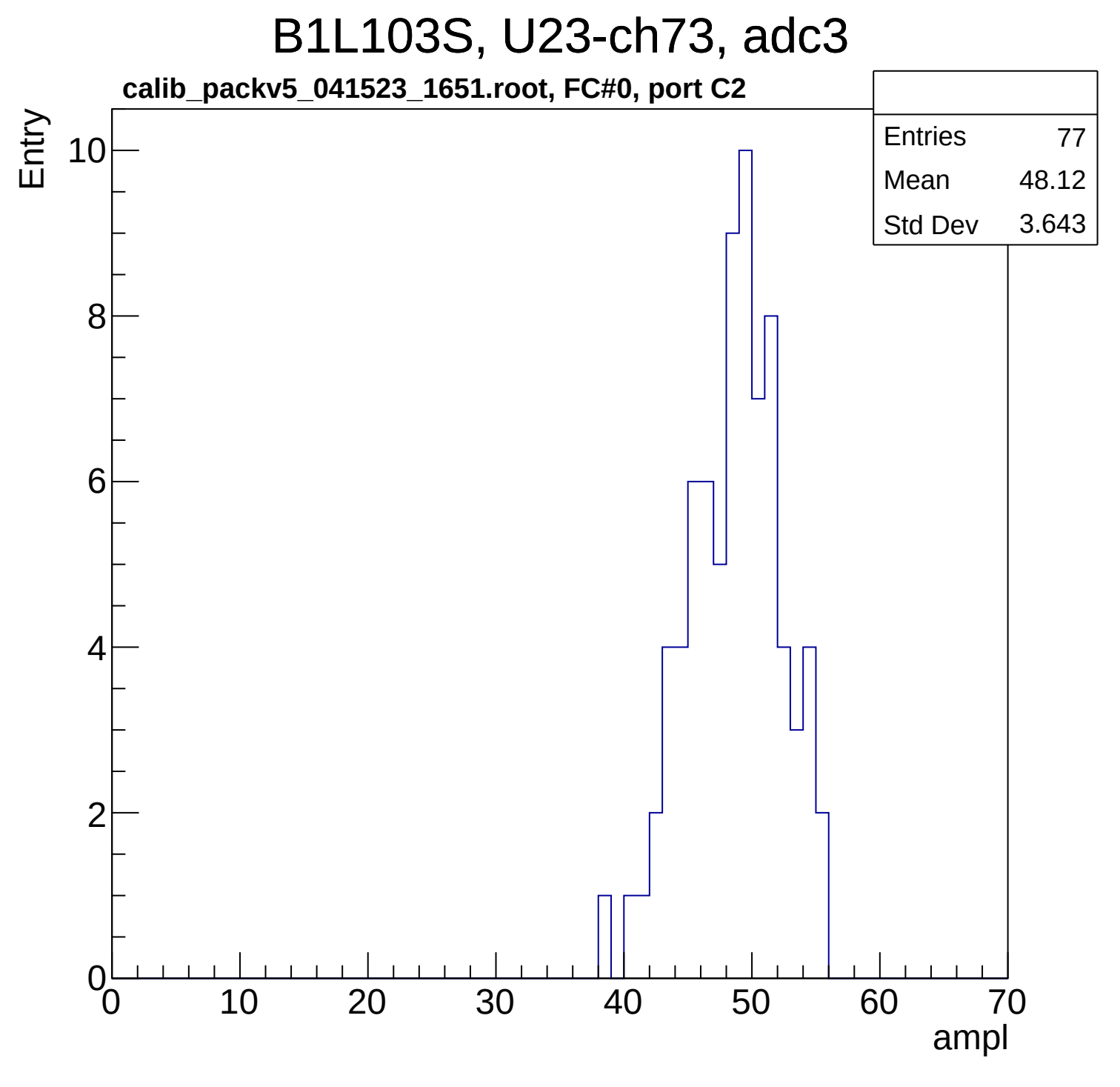

B1L103S, U23-ch72, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

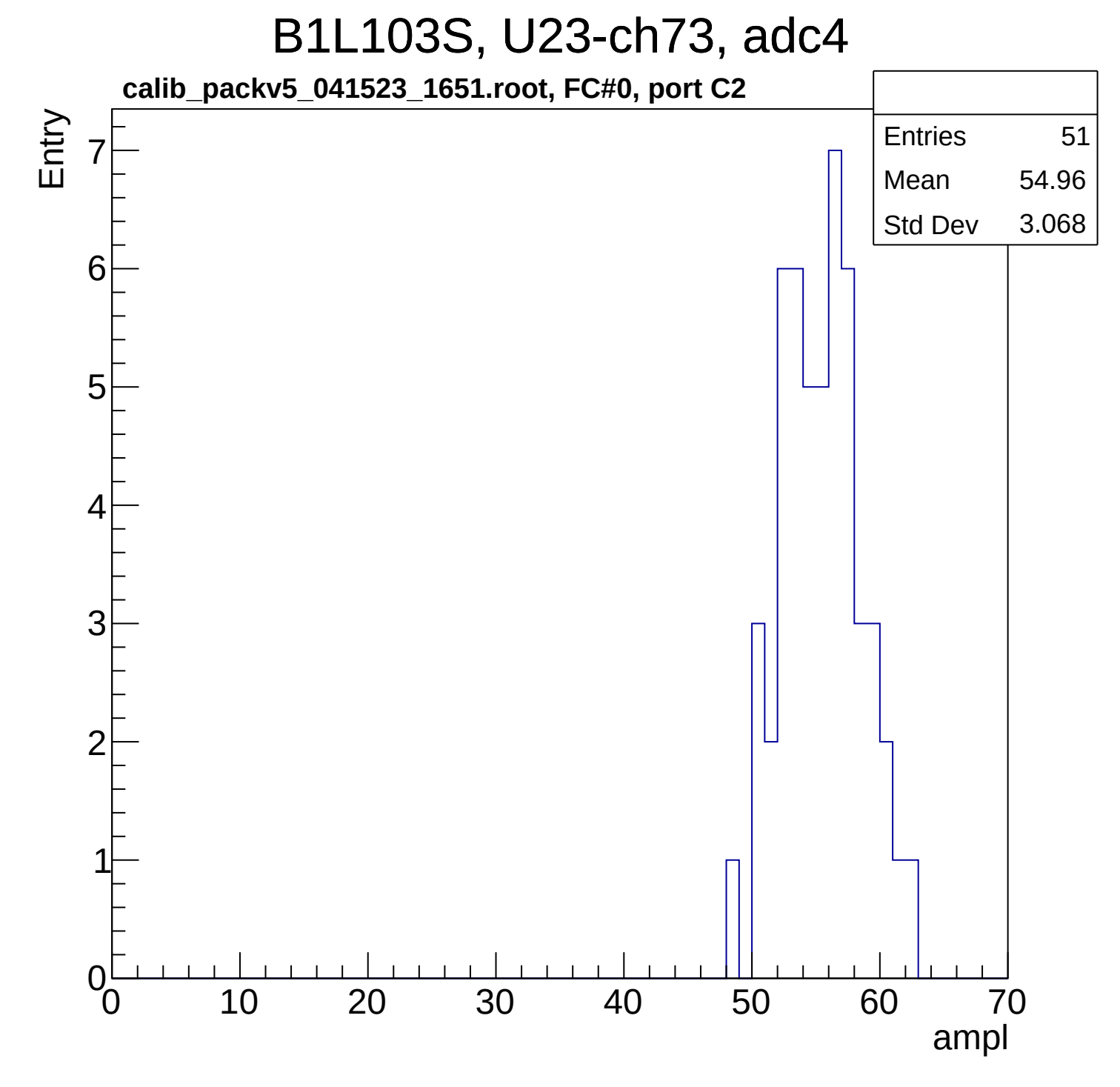

B1L103S, U23-ch72, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.905
Std Dev 18.2
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

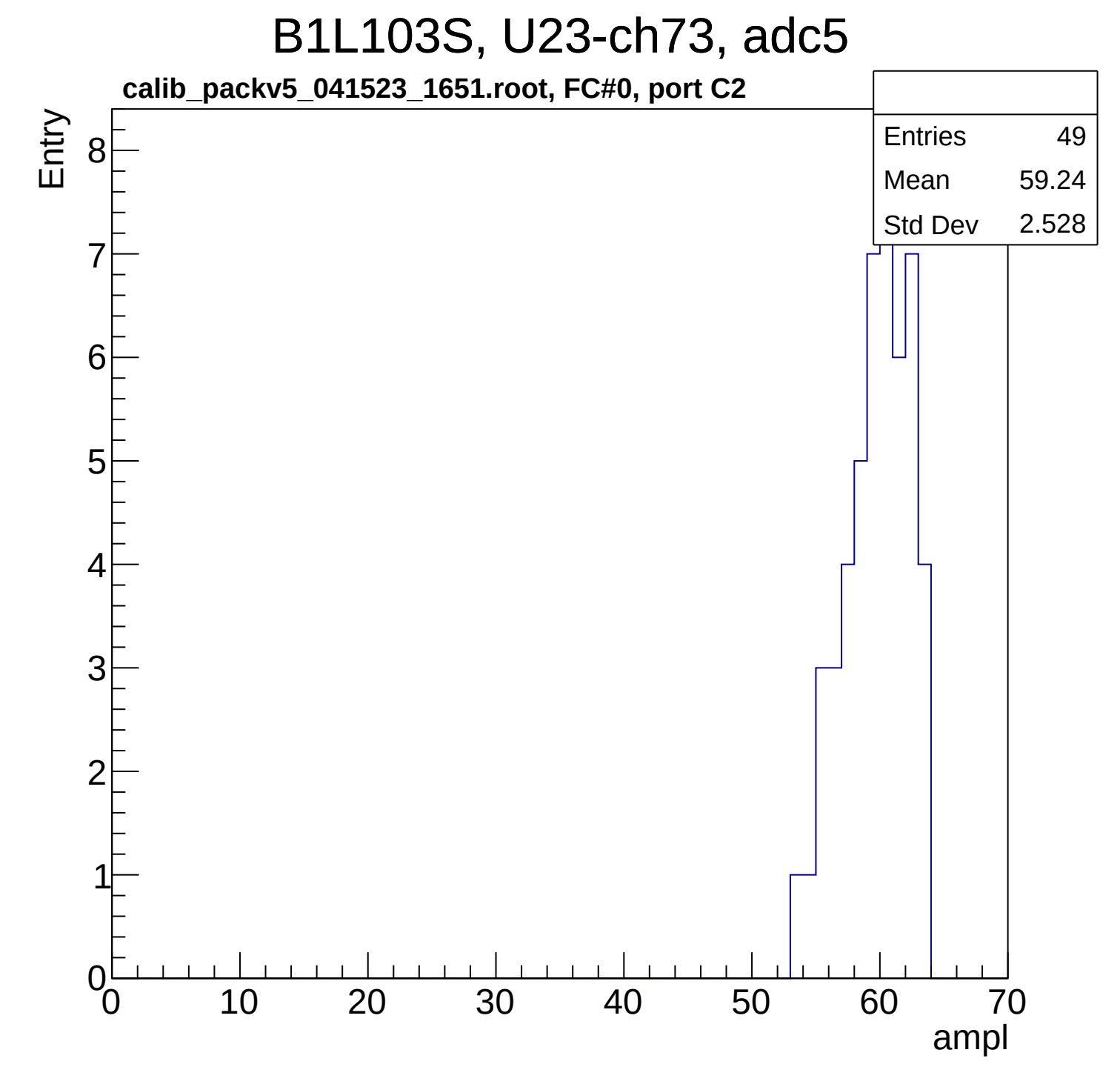

B1L103S, U23-ch73, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 21.47
Std Dev 11.59
Gaus mean : 27.2796
Gaus Width: 3.7279
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

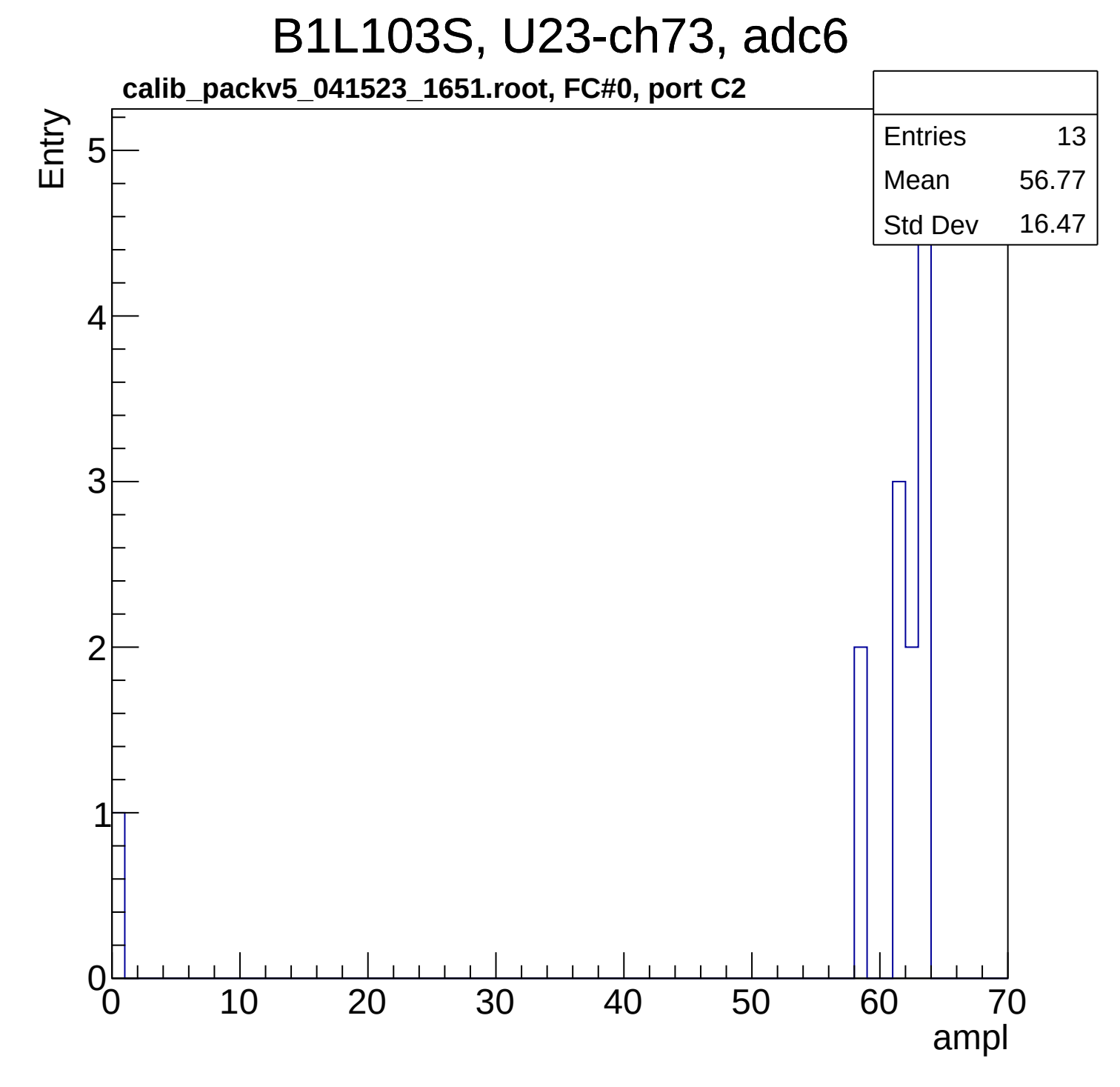

B1L103S, U23-ch73, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 31.23
Std Dev 11.41
Gaus mean : 34.9944
Gaus Width: 4.2492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

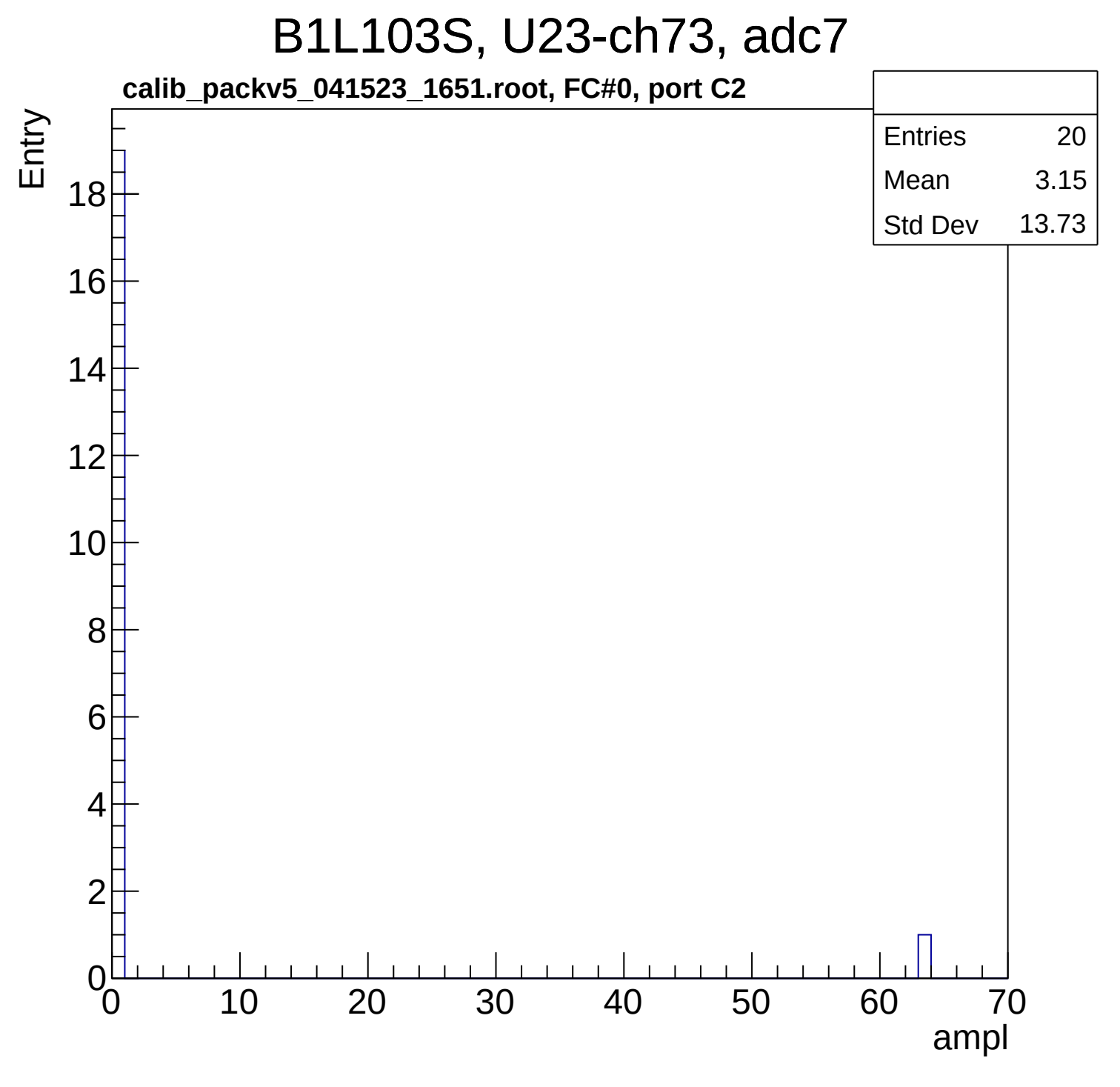

B1L103S, U23-ch73, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 38.12
Std Dev 11.35
Gaus mean : 42.2517
Gaus Width: 4.3120
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

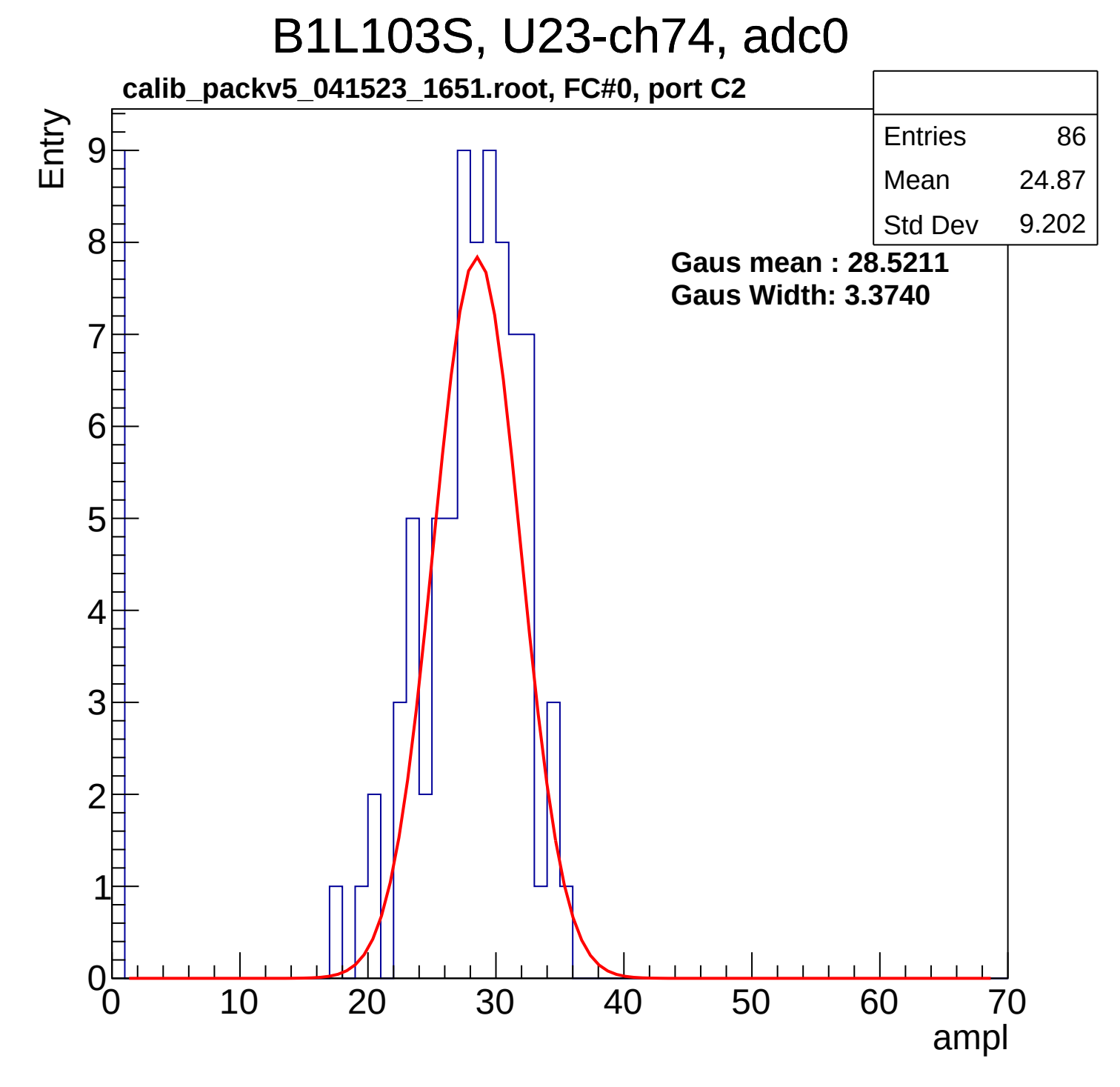

B1L103S, U23-ch73, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 48.12
Std Dev 3.643
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

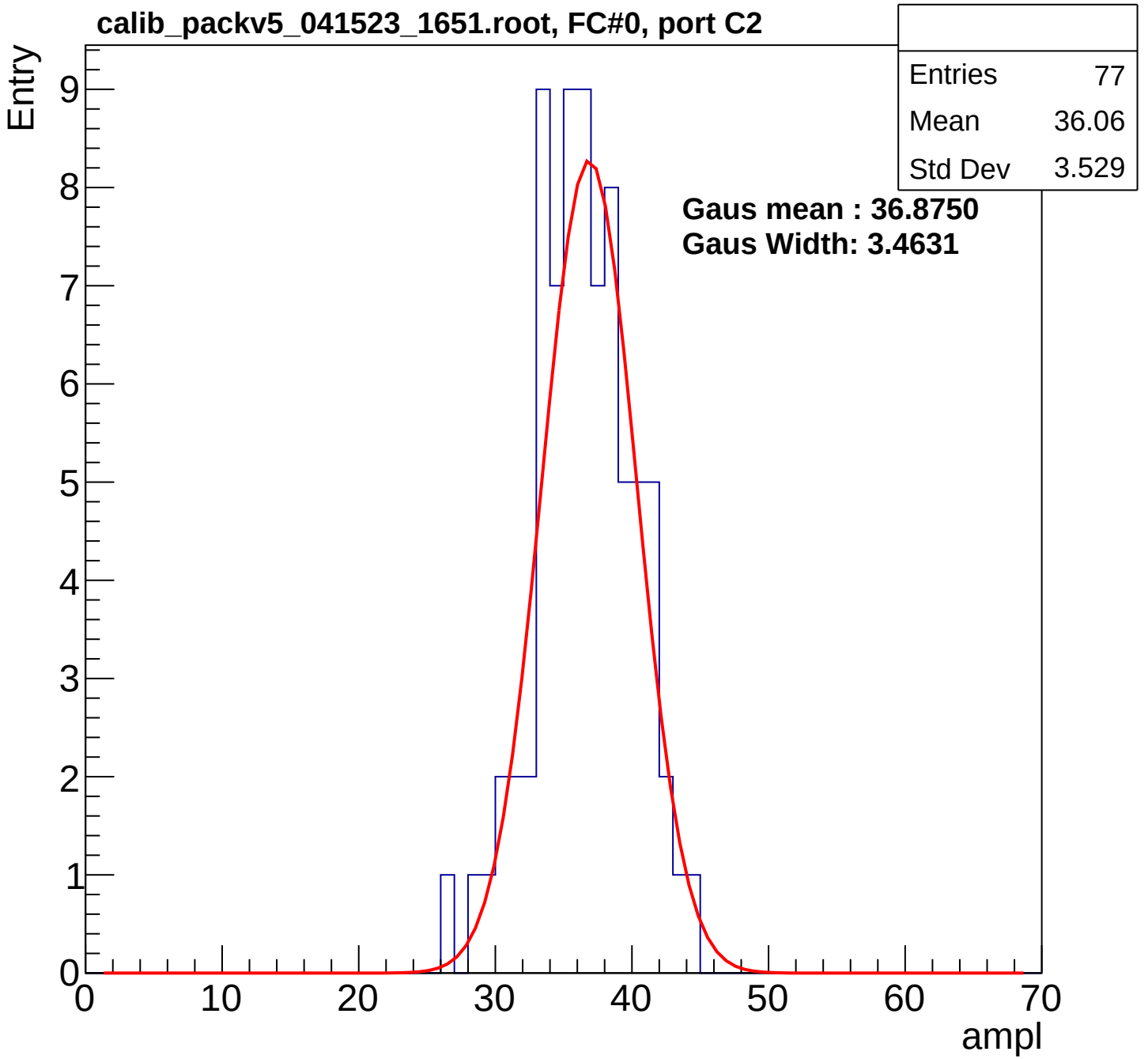

B1L103S, U23-ch73, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 54.96
Std Dev 3.068
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

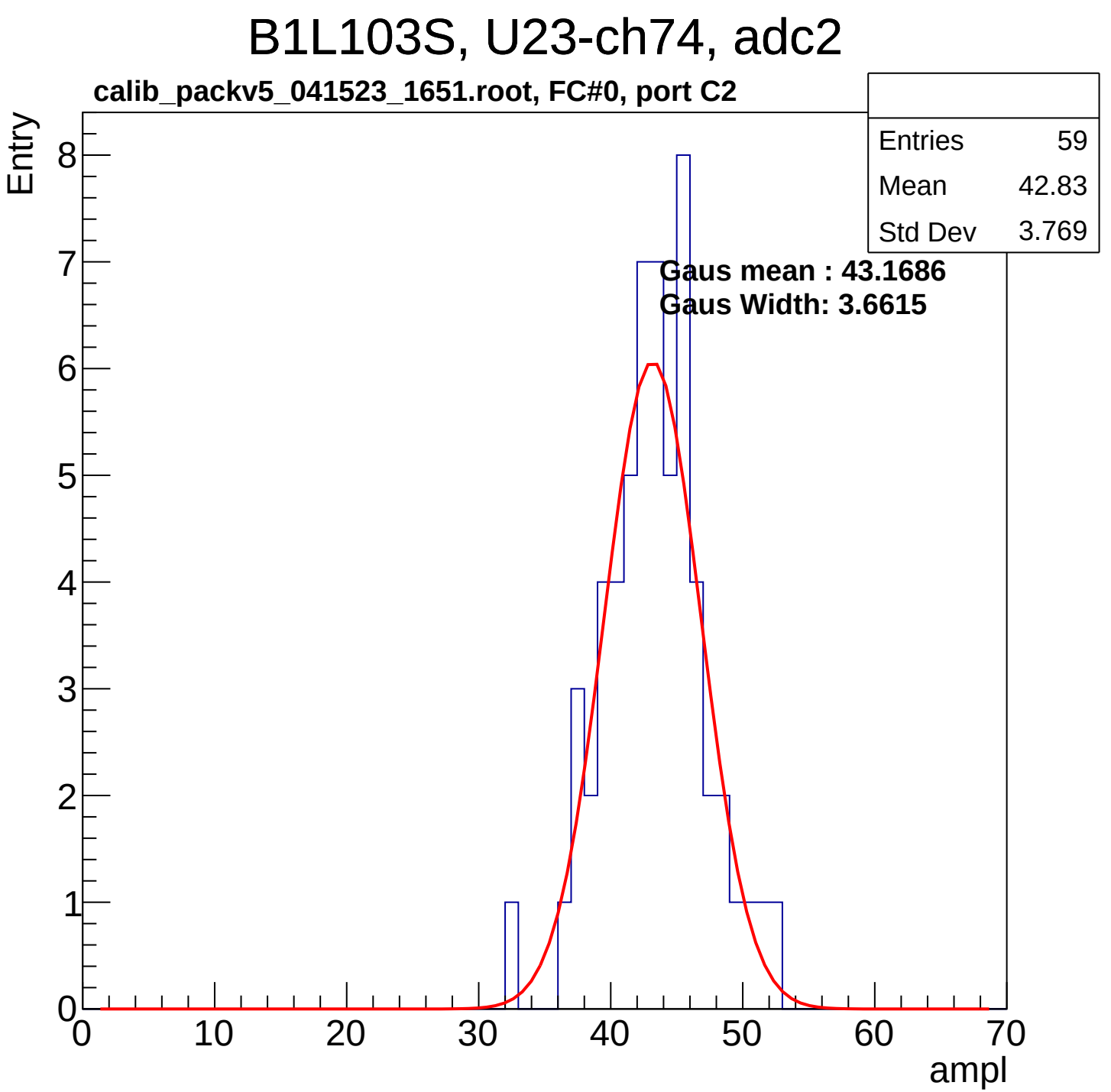

B1L103S, U23-ch73, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 59.24
Std Dev 2.528
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

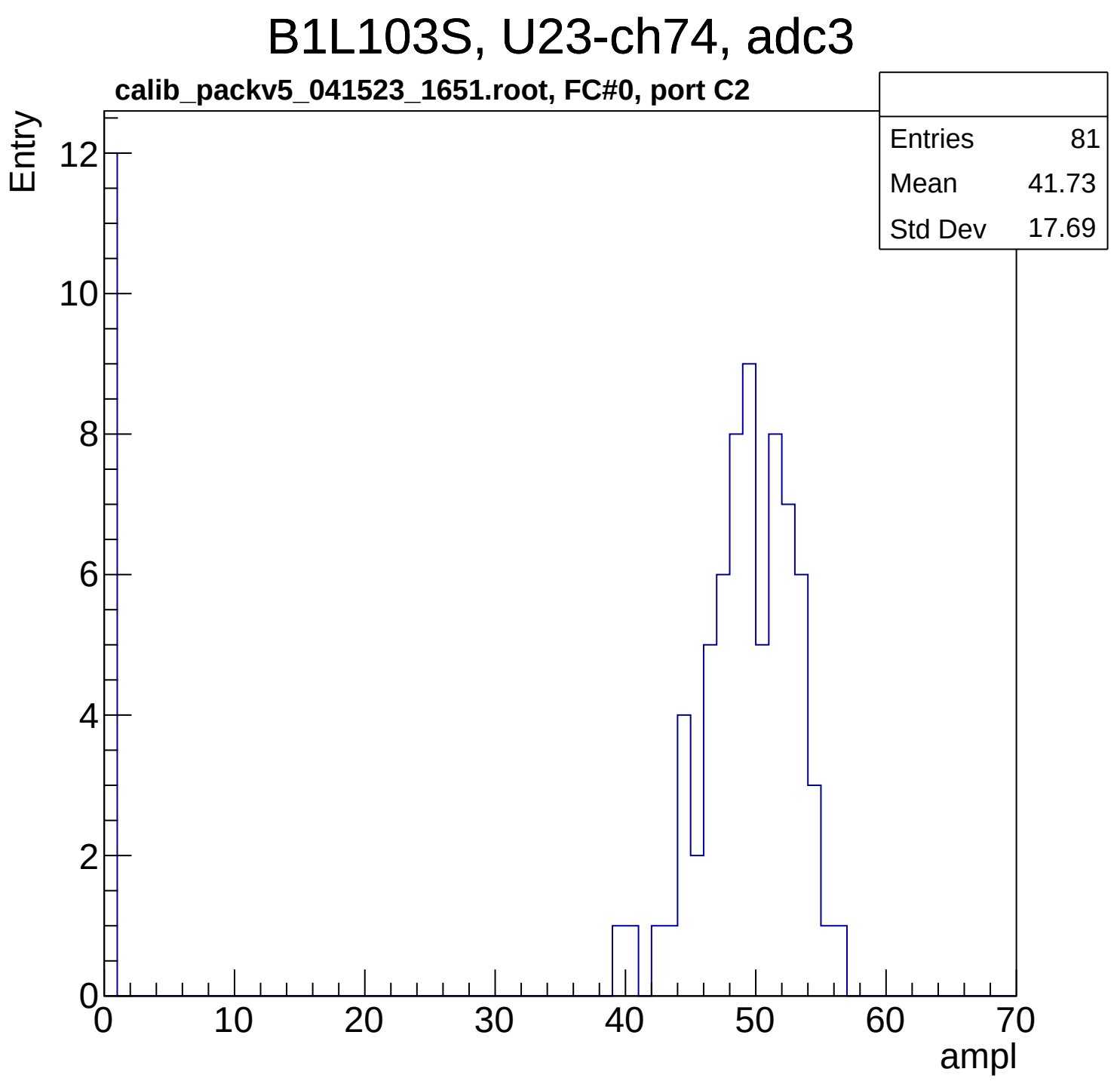

B1L103S, U23-ch73, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 13
Mean 56.77
Std Dev 16.47
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

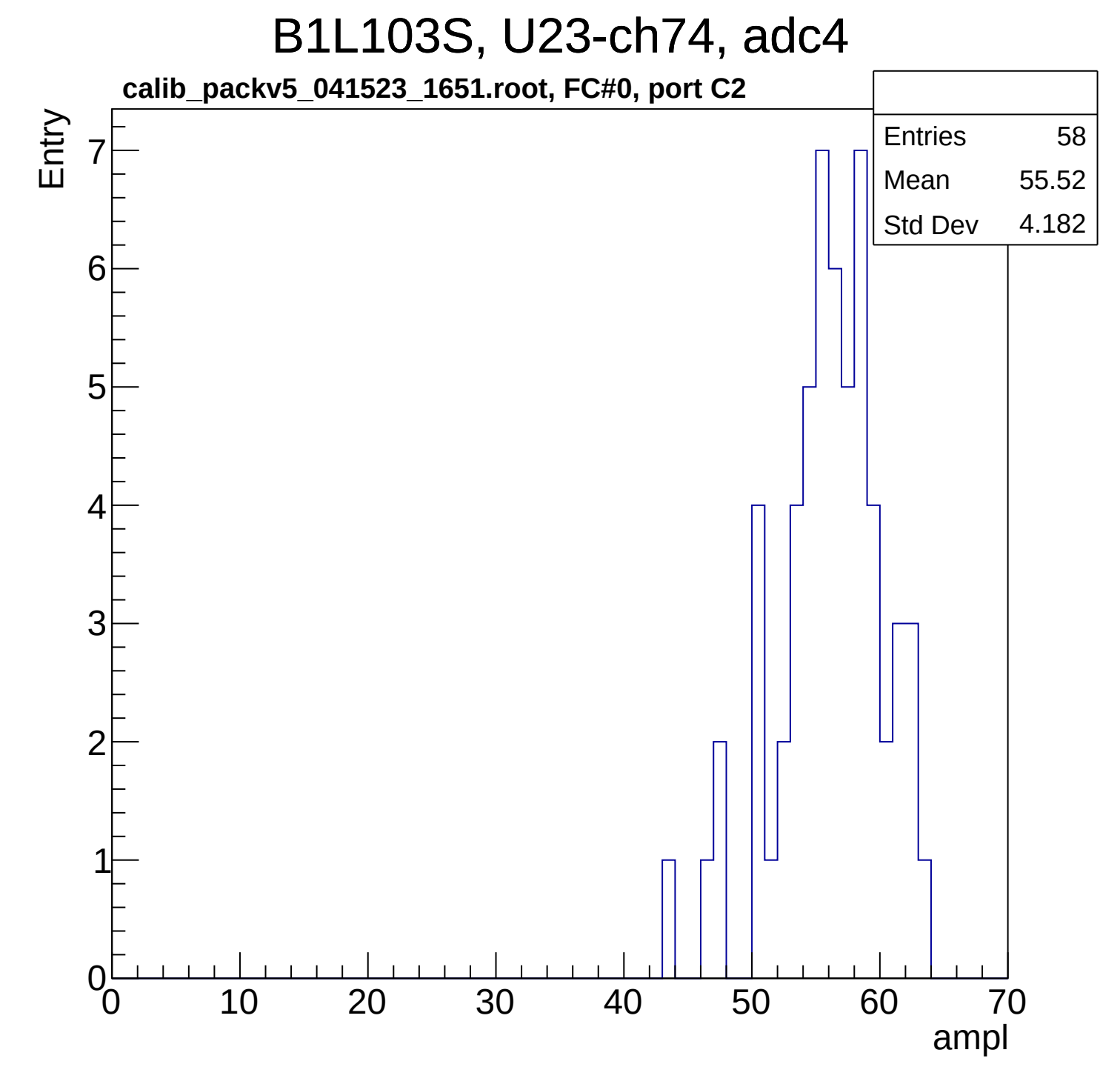

B1L103S, U23-ch73, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.15
Std Dev 13.73
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

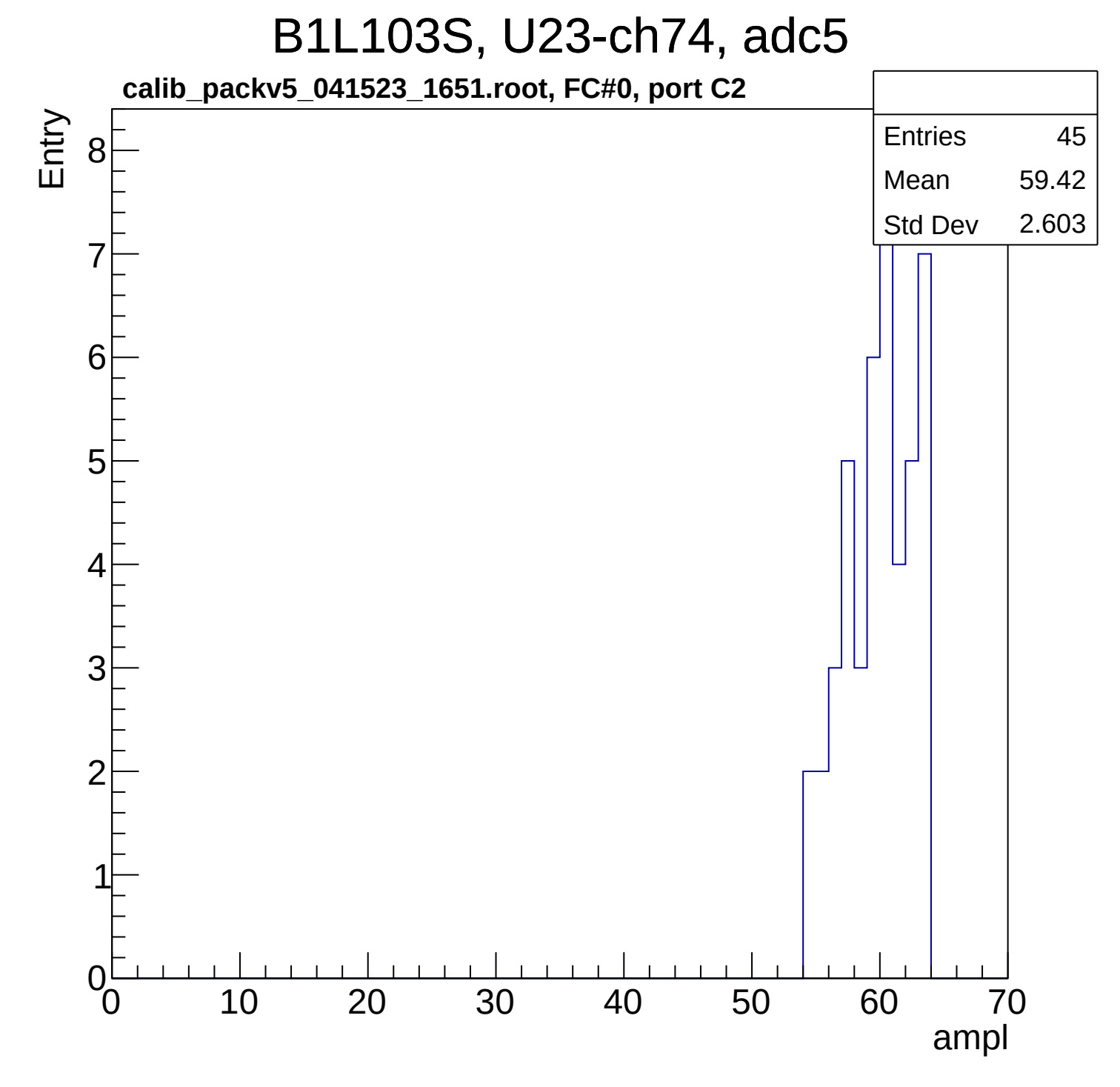

B1L103S, U23-ch74, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 24.87
Std Dev 9.202
Gaus mean : 28.5211
Gaus Width: 3.3740
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

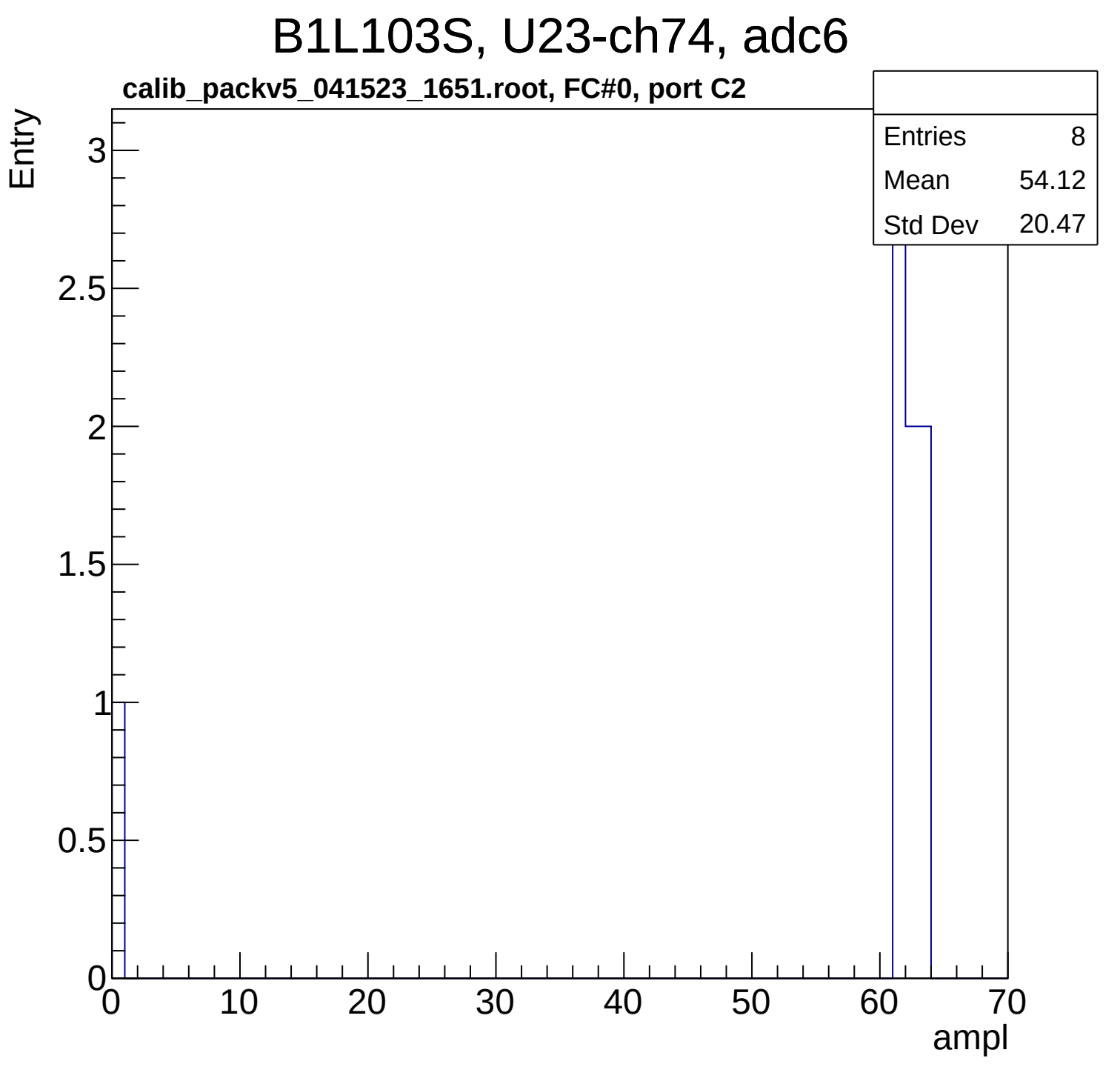

B1L103S, U23-ch74, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 36.06
Std Dev 3.529
Gaus mean : 36.8750
Gaus Width: 3.4631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

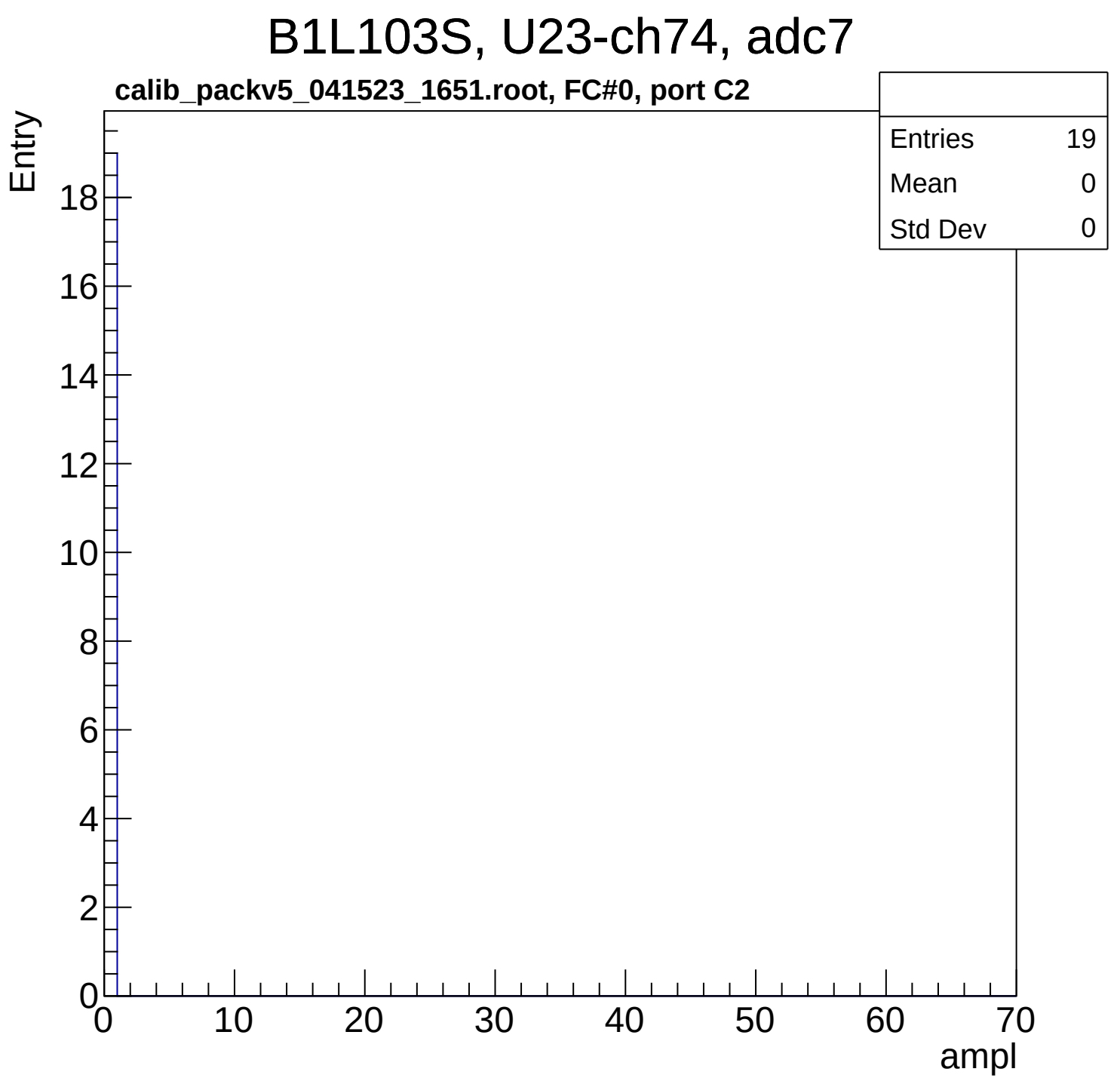

B1L103S, U23-ch74, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 42.83
Std Dev 3.769
Gaus mean : 43.1686
Gaus Width: 3.6615
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

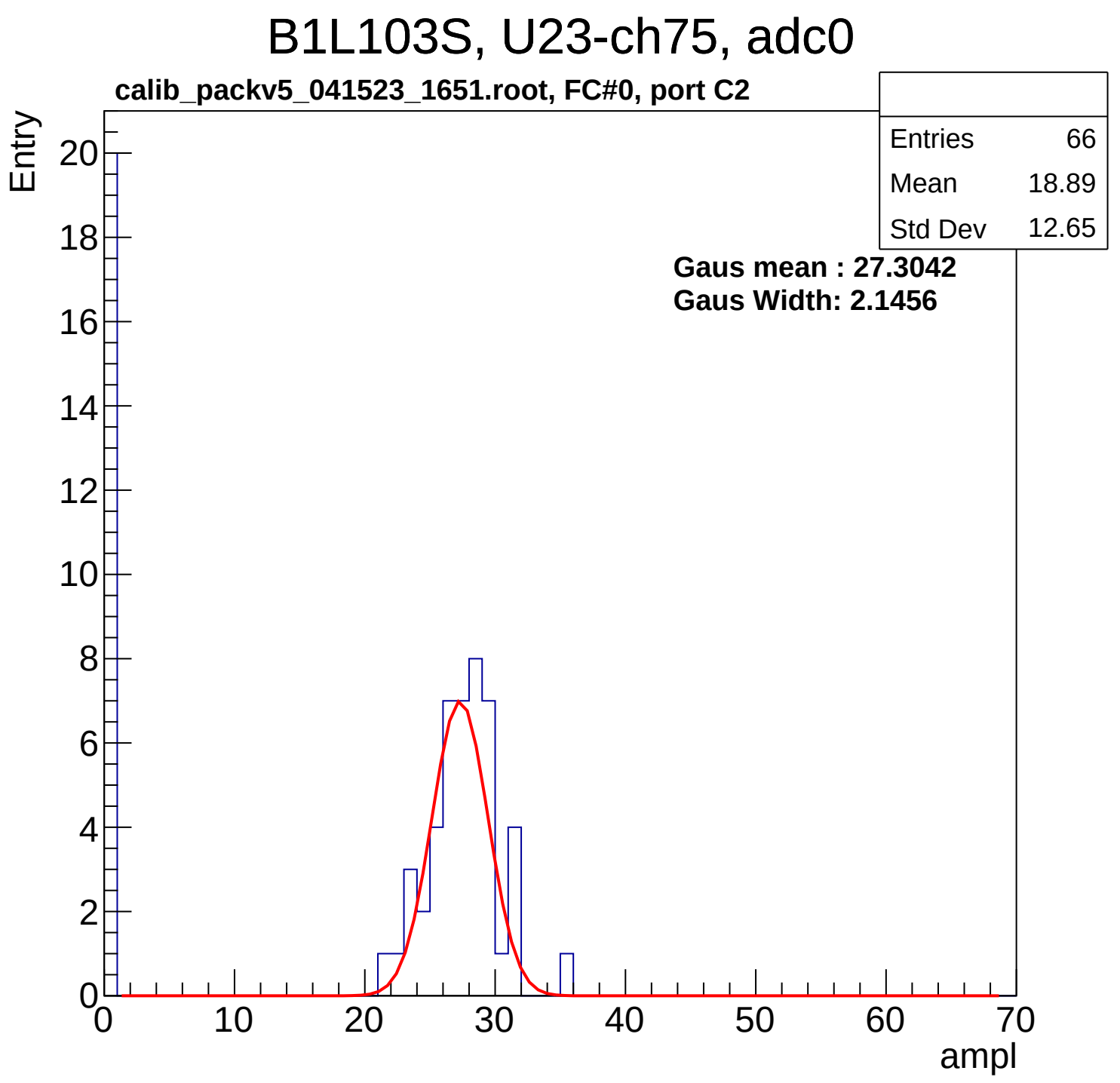

B1L103S, U23-ch74, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 41.73
Std Dev 17.69
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

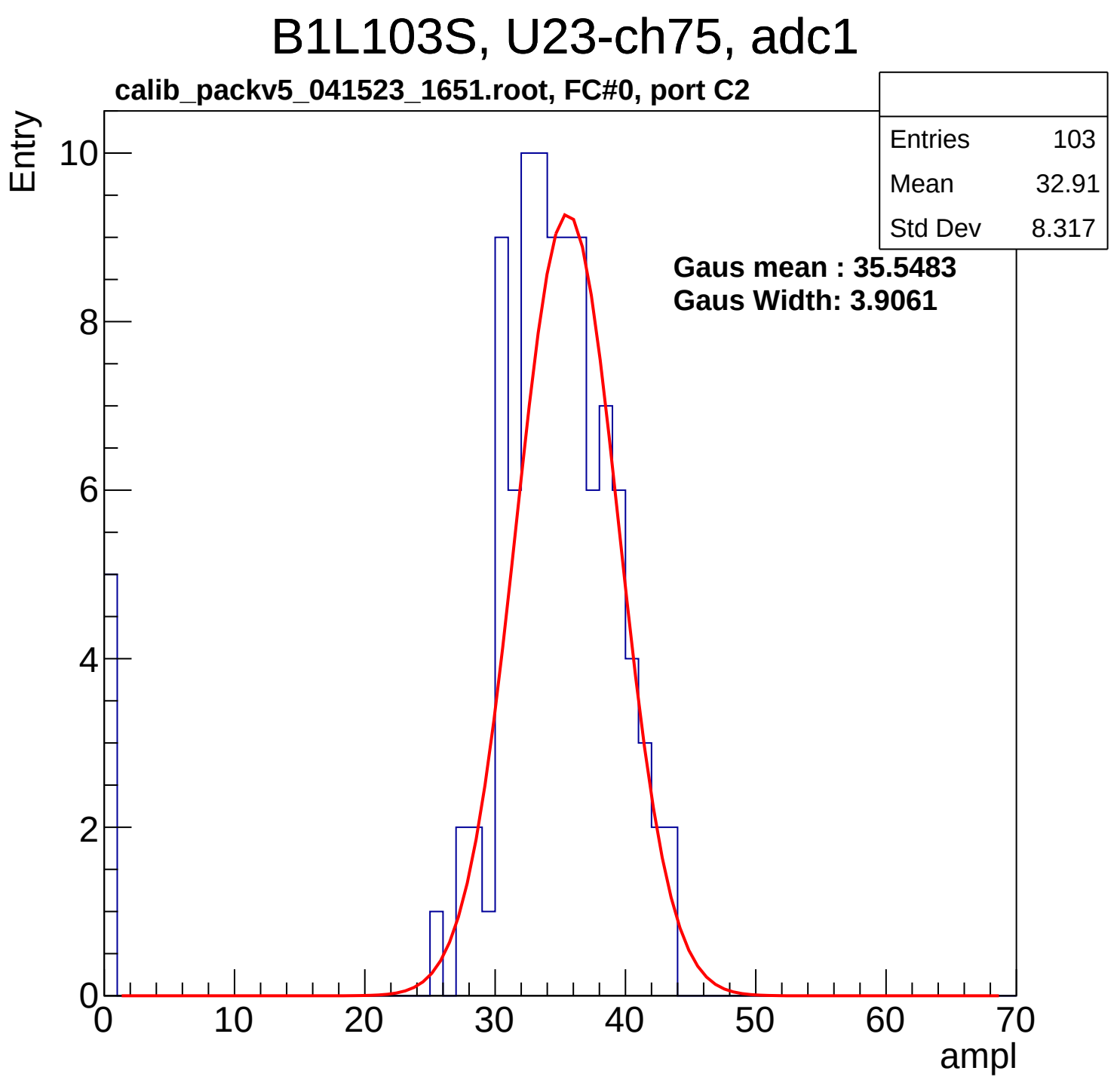

B1L103S, U23-ch74, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 55.52
Std Dev 4.182
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

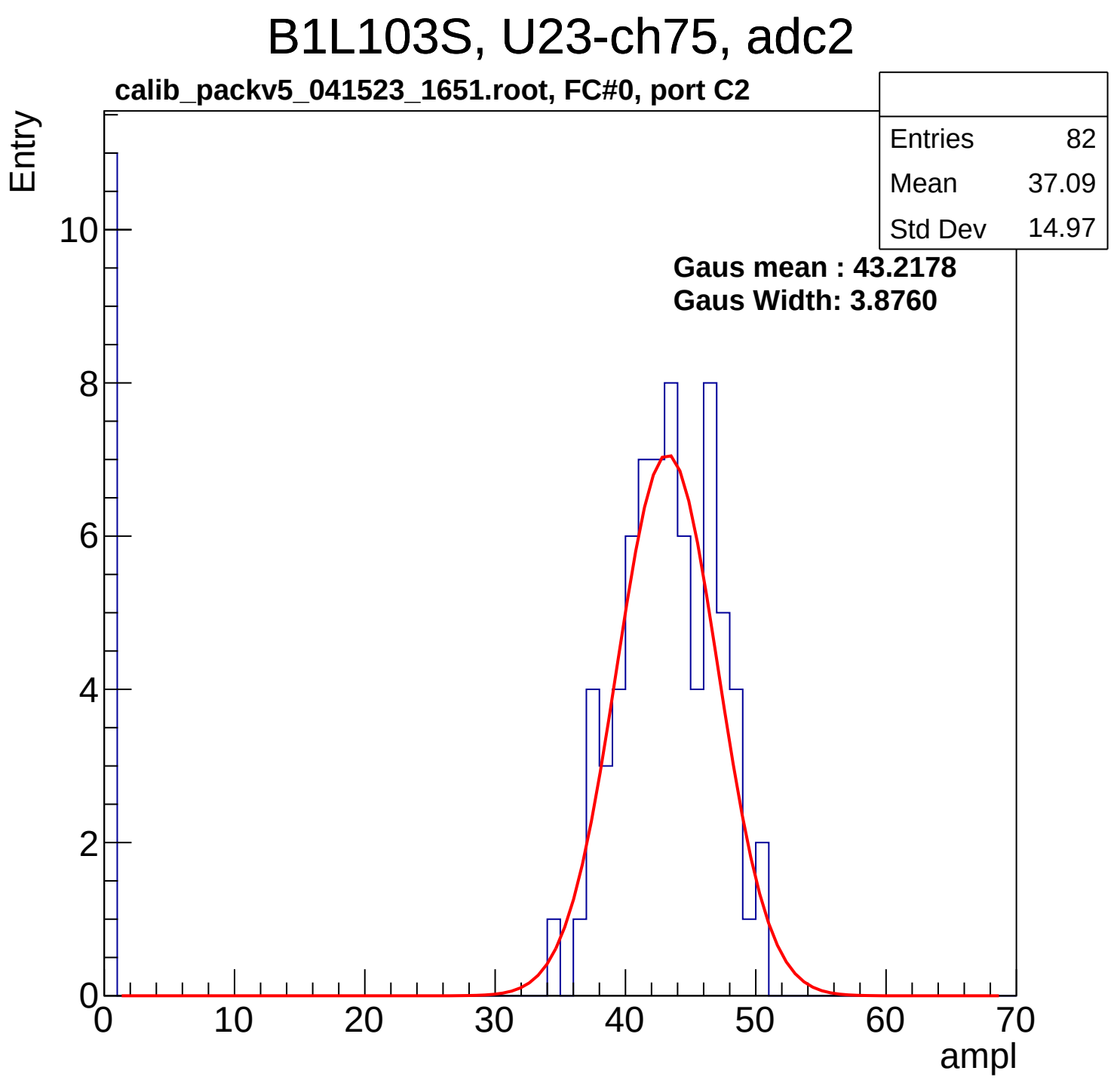

B1L103S, U23-ch74, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 45
Mean 59.42
Std Dev 2.603
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

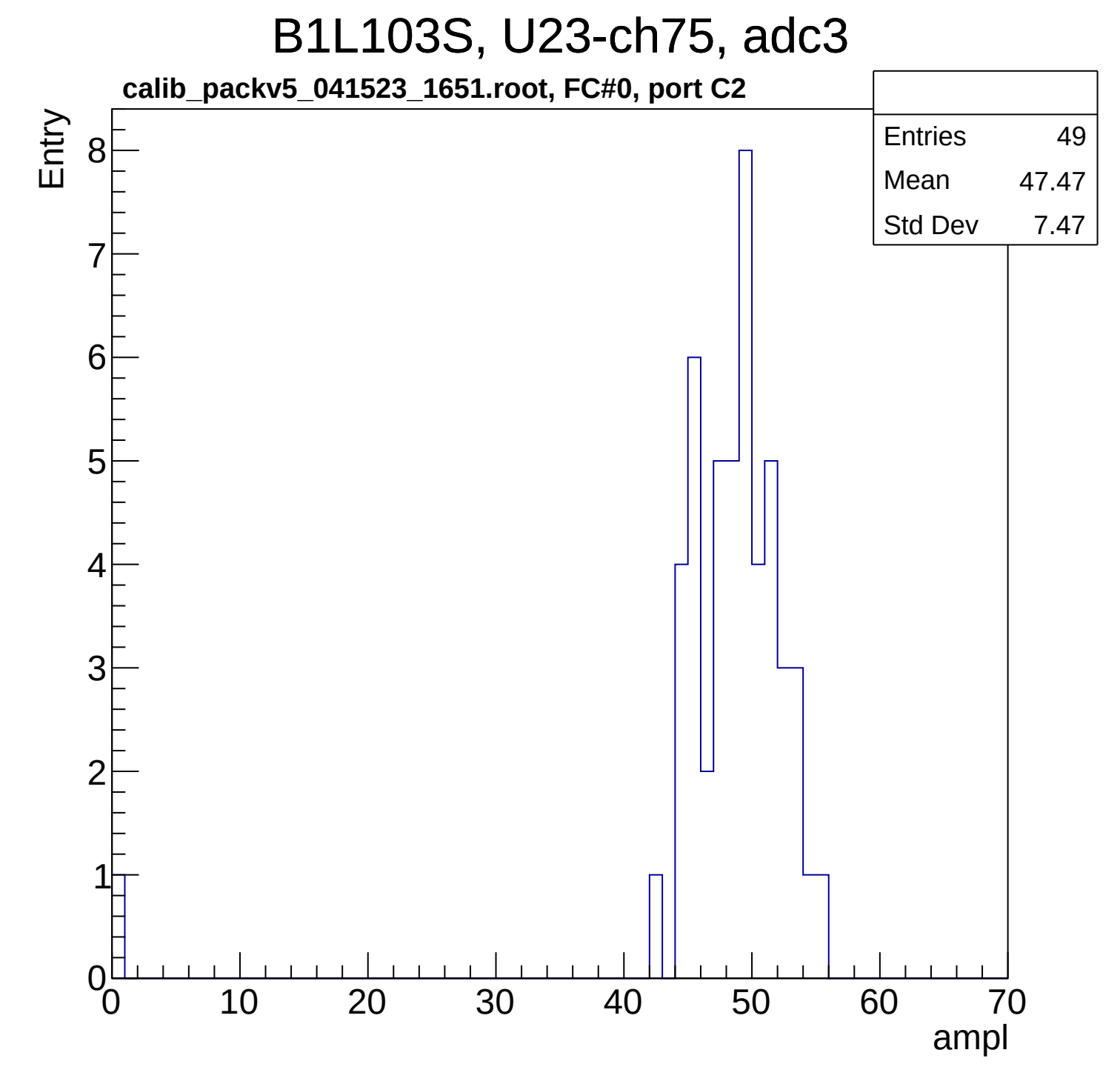

B1L103S, U23-ch74, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 54.12
Std Dev 20.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

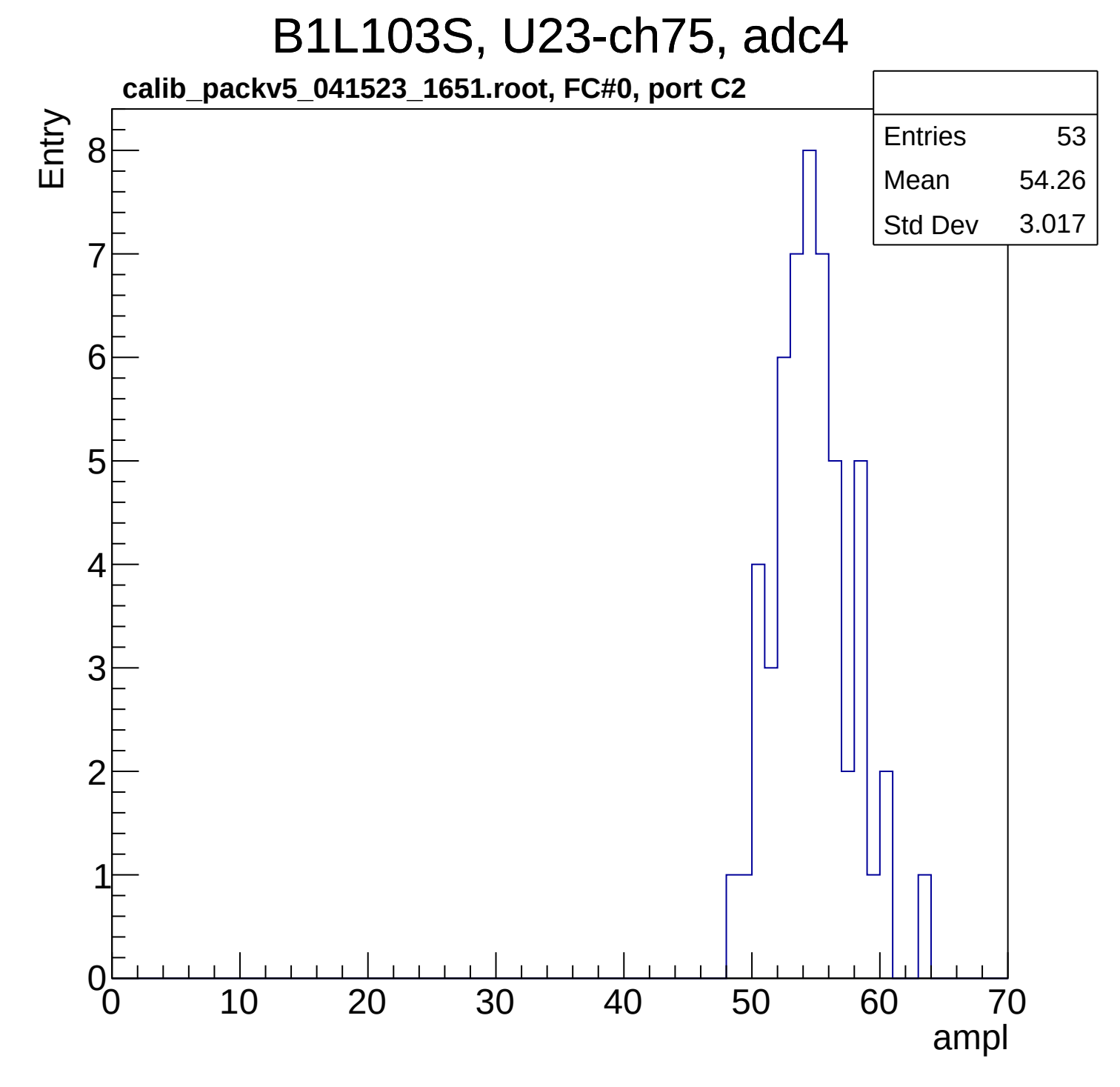

B1L103S, U23-ch74, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

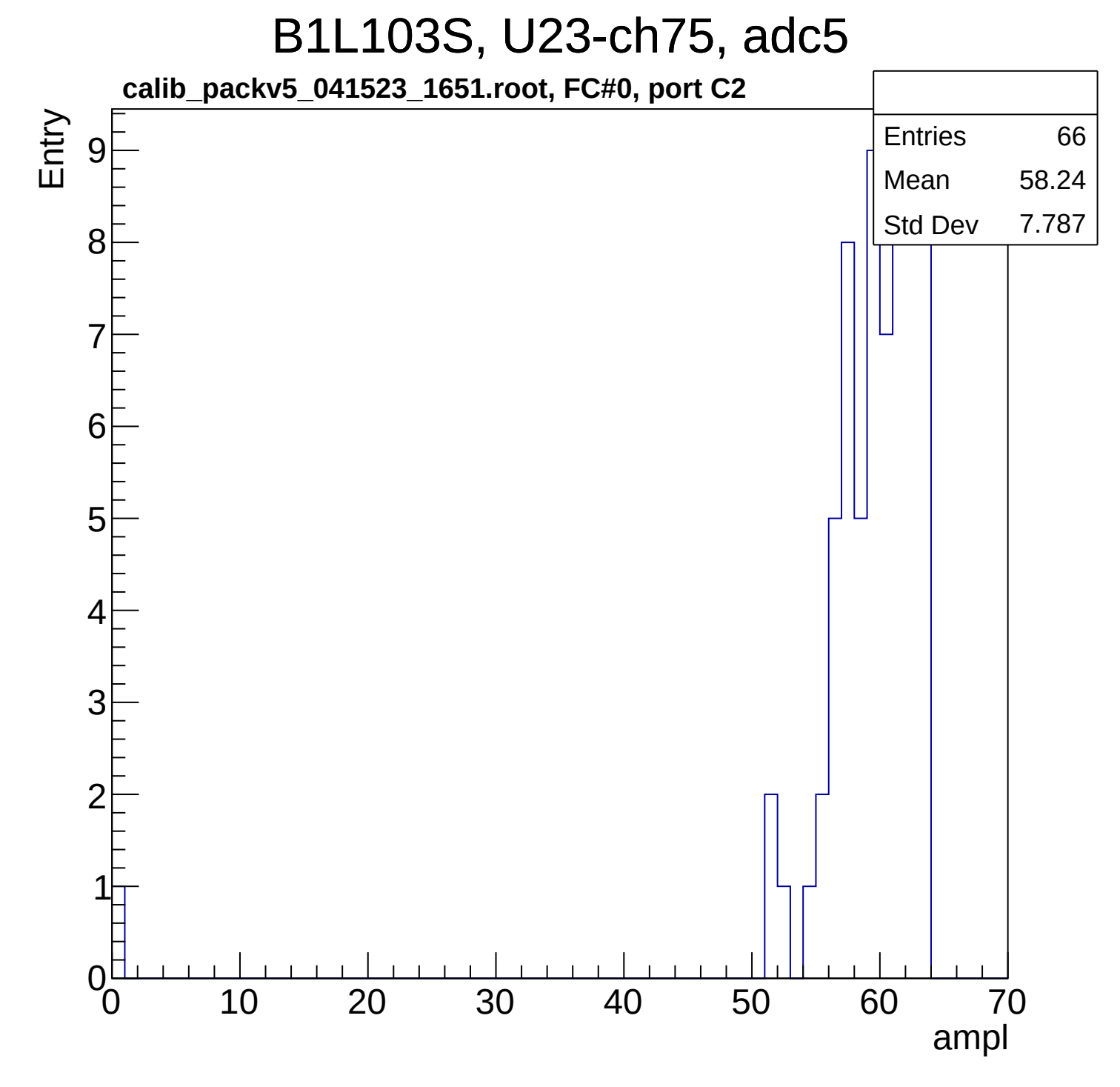

B1L103S, U23-ch75, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 18.89
Std Dev 12.65
Gaus mean : 27.3042
Gaus Width: 2.1456
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

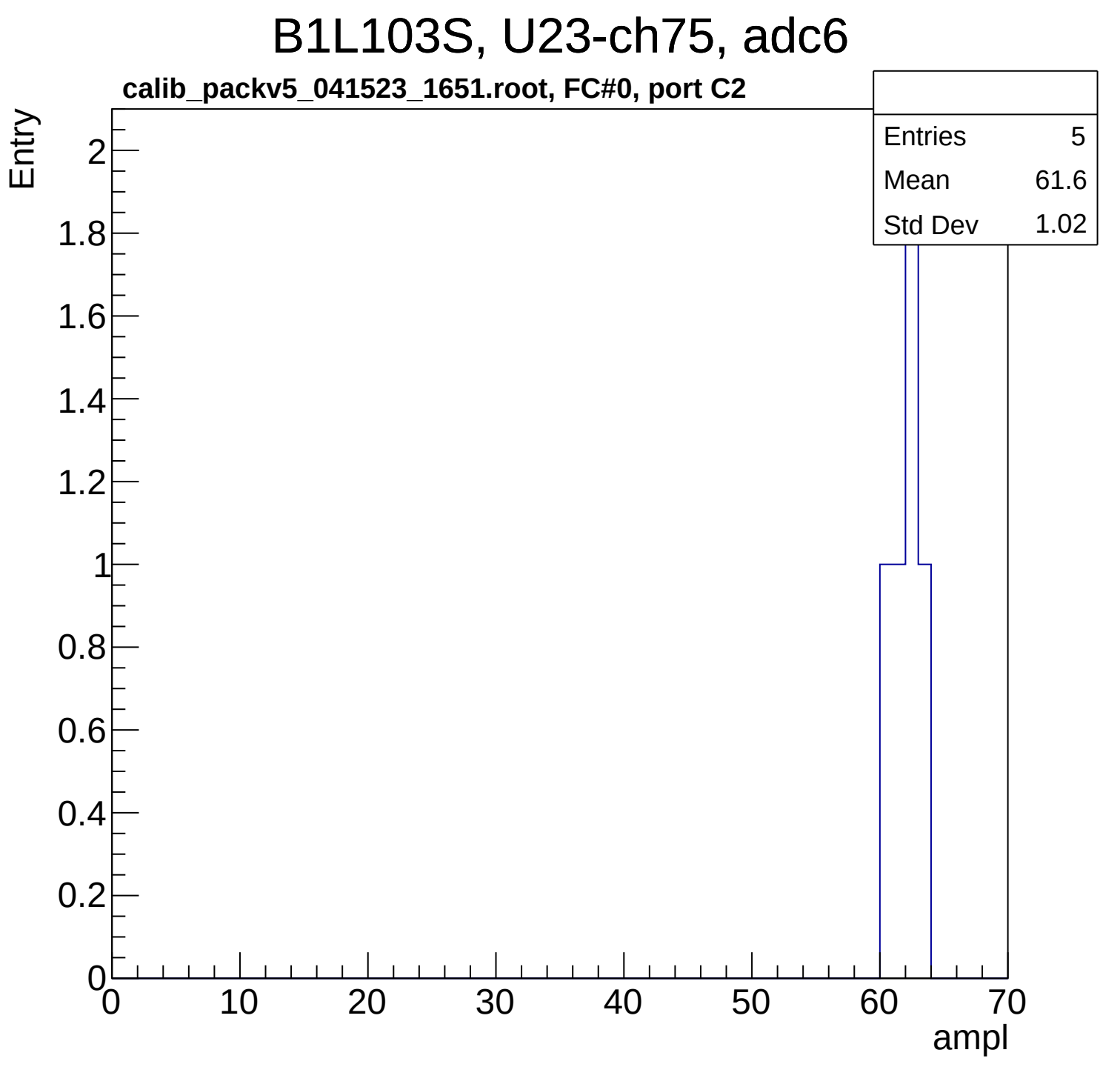

B1L103S, U23-ch75, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 103
Mean 32.91
Std Dev 8.317
Gaus mean : 35.5483
Gaus Width: 3.9061
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

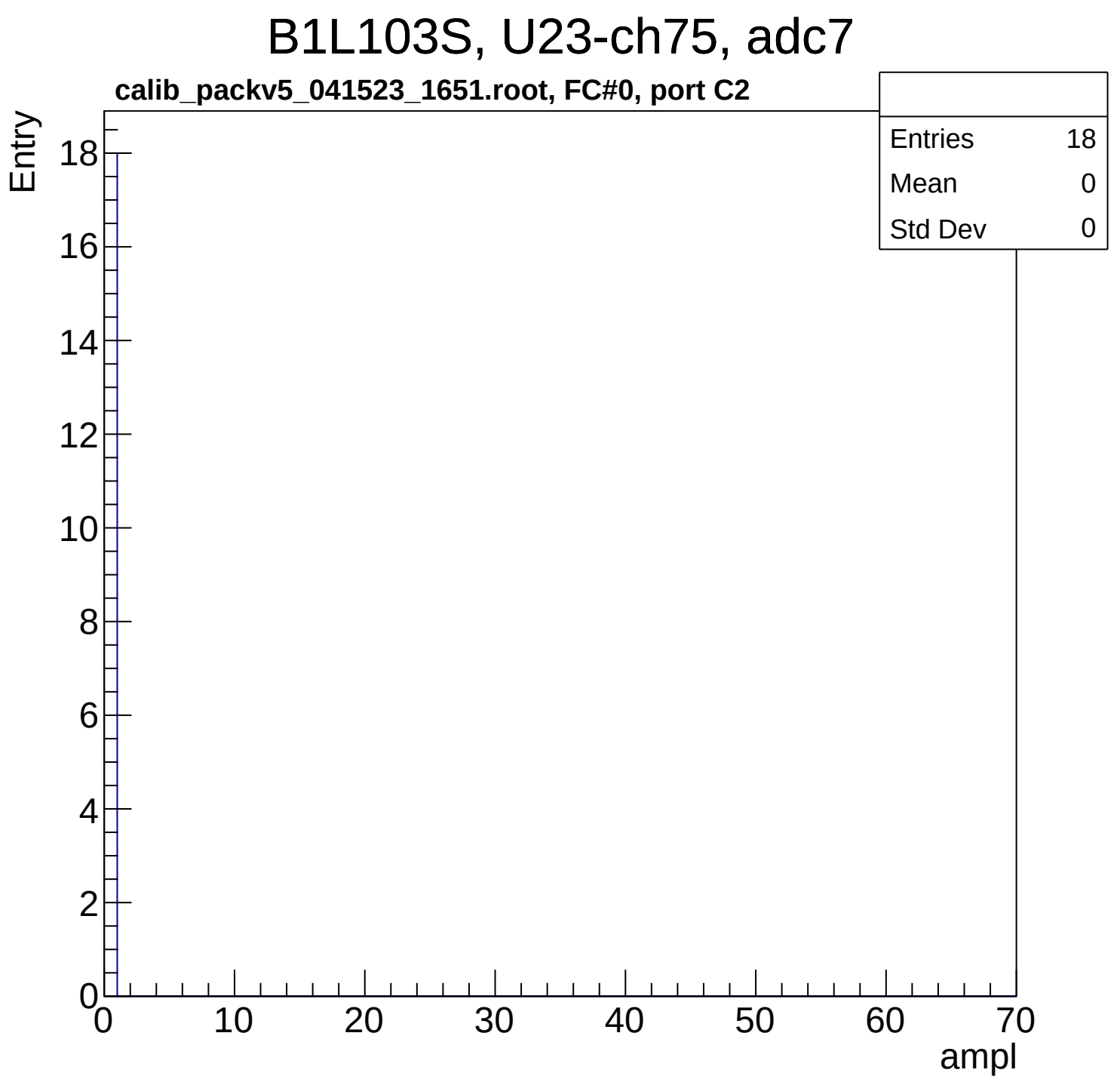

B1L103S, U23-ch75, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 37.09
Std Dev 14.97
Gaus mean : 43.2178
Gaus Width: 3.8760
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

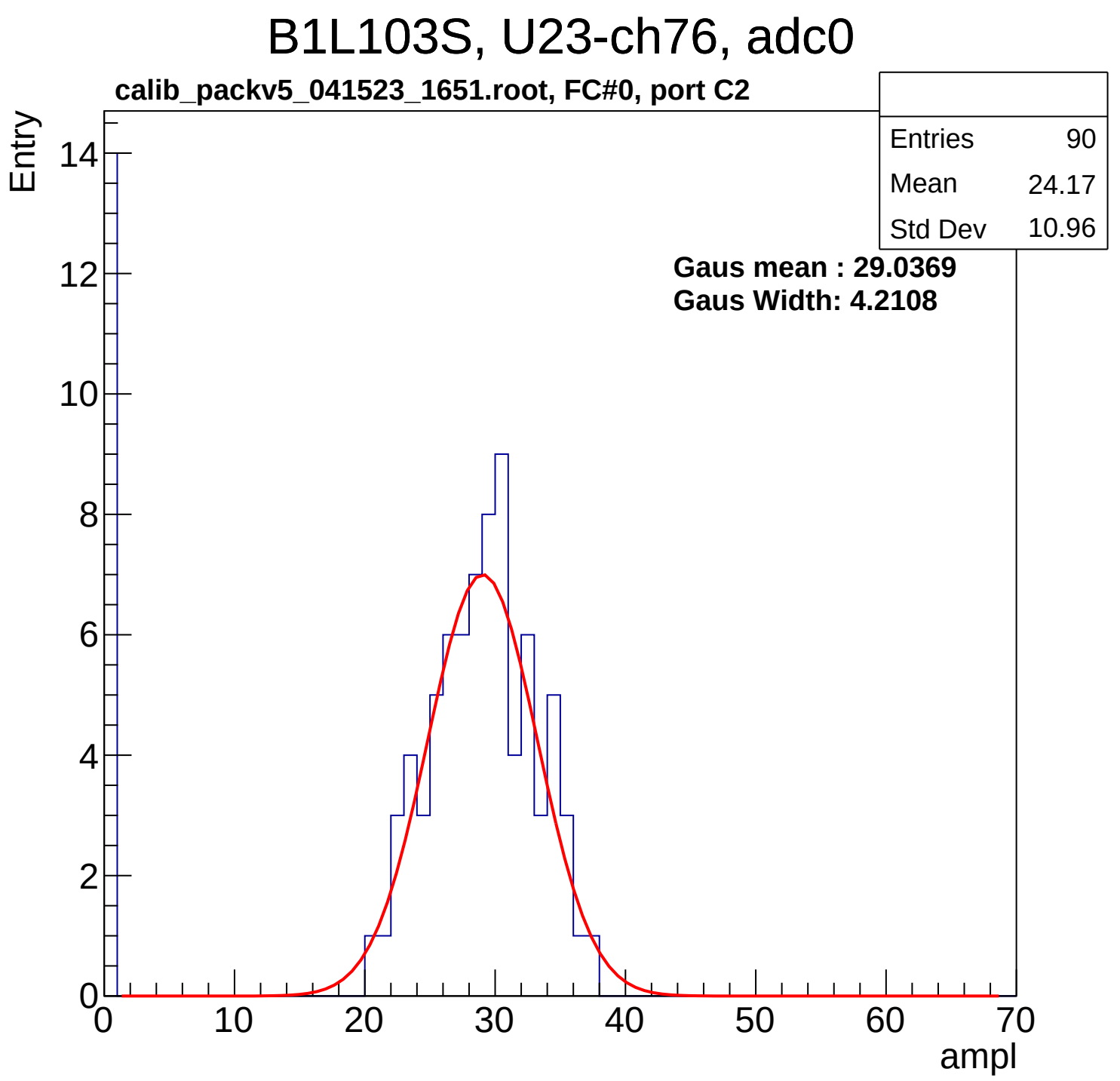

B1L103S, U23-ch75, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 47.47
Std Dev 7.47
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

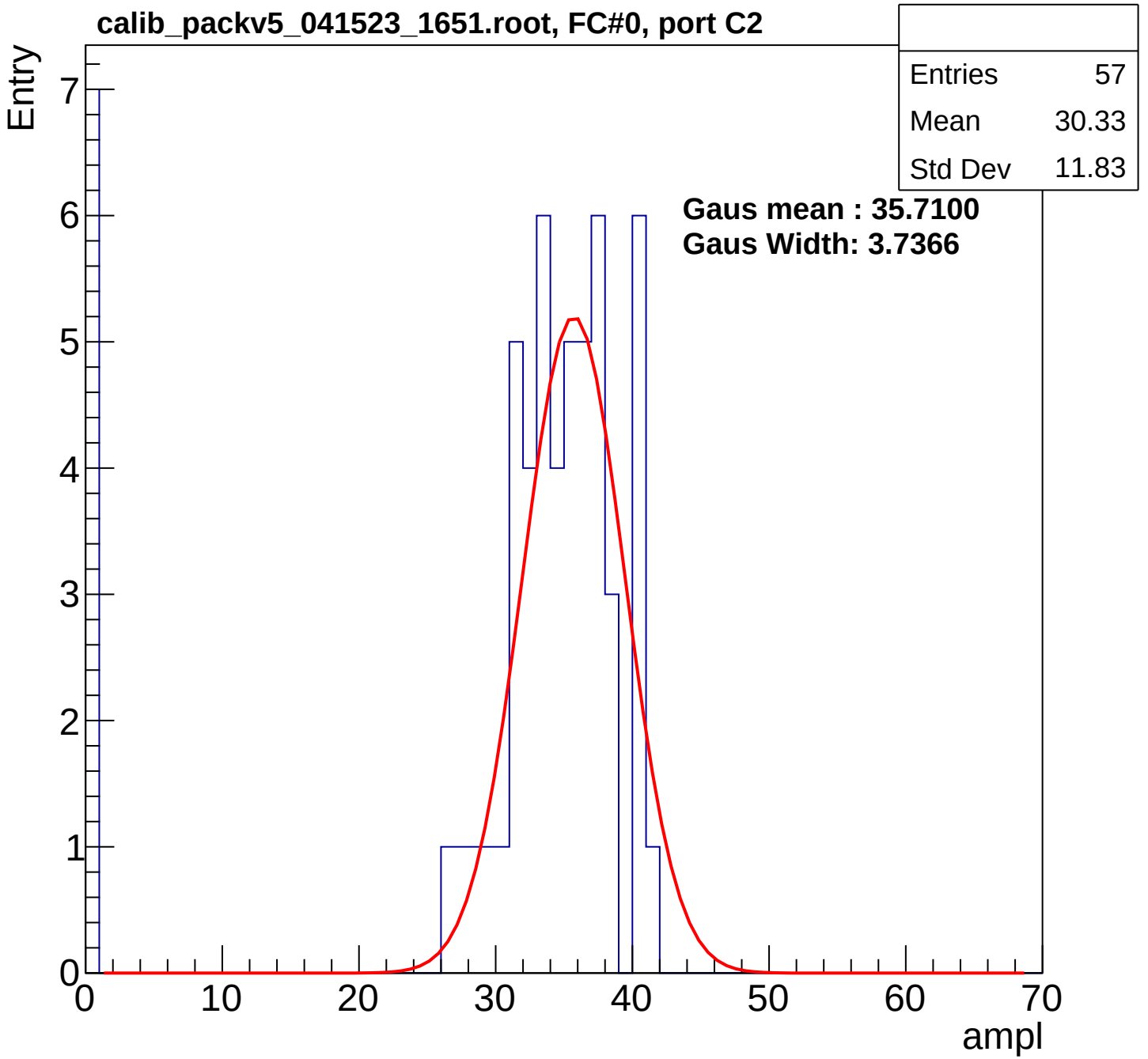

B1L103S, U23-ch75, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 54.26
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

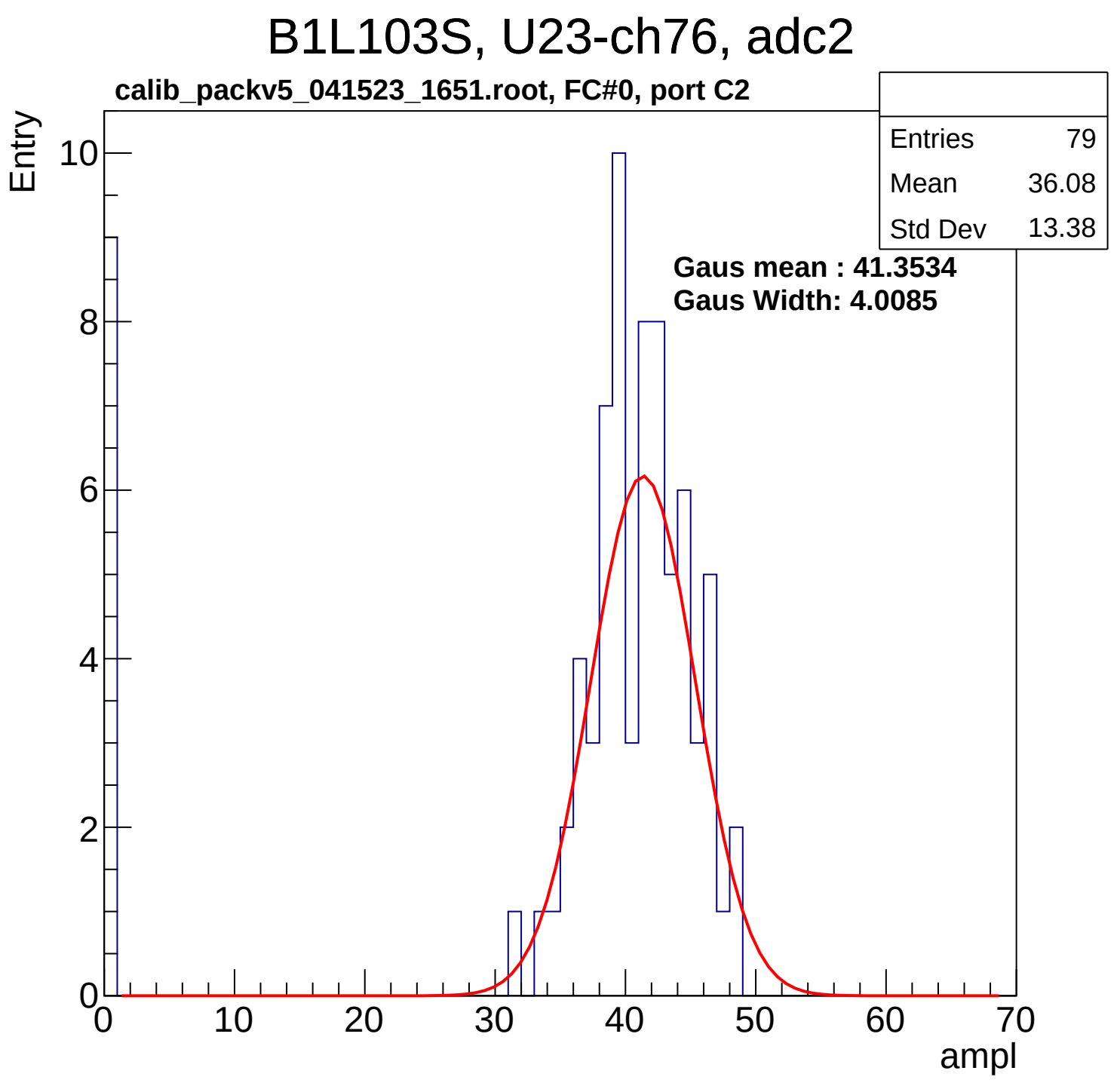

B1L103S, U23-ch75, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 58.24
Std Dev 7.787
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

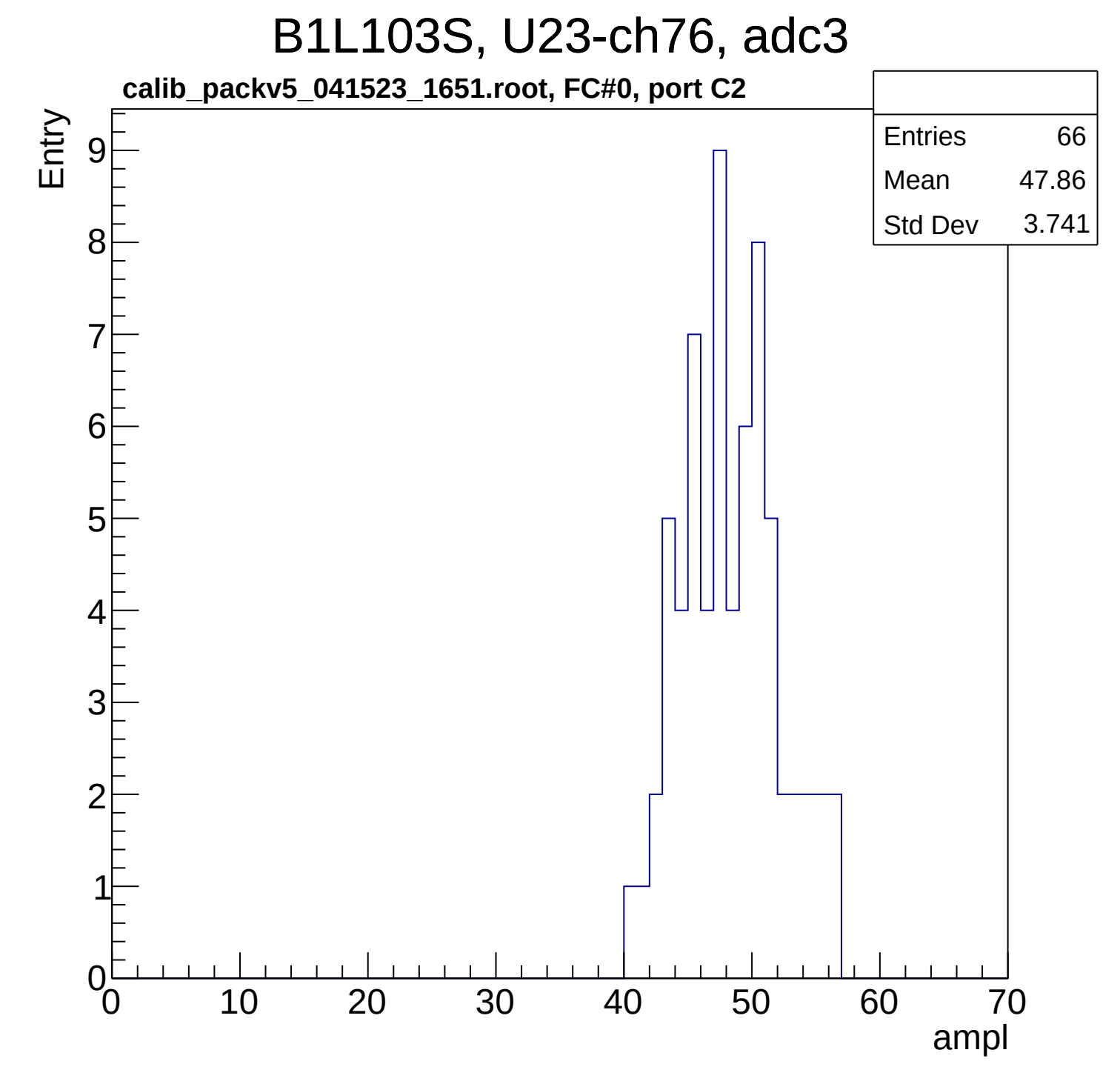

B1L103S, U23-ch75, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

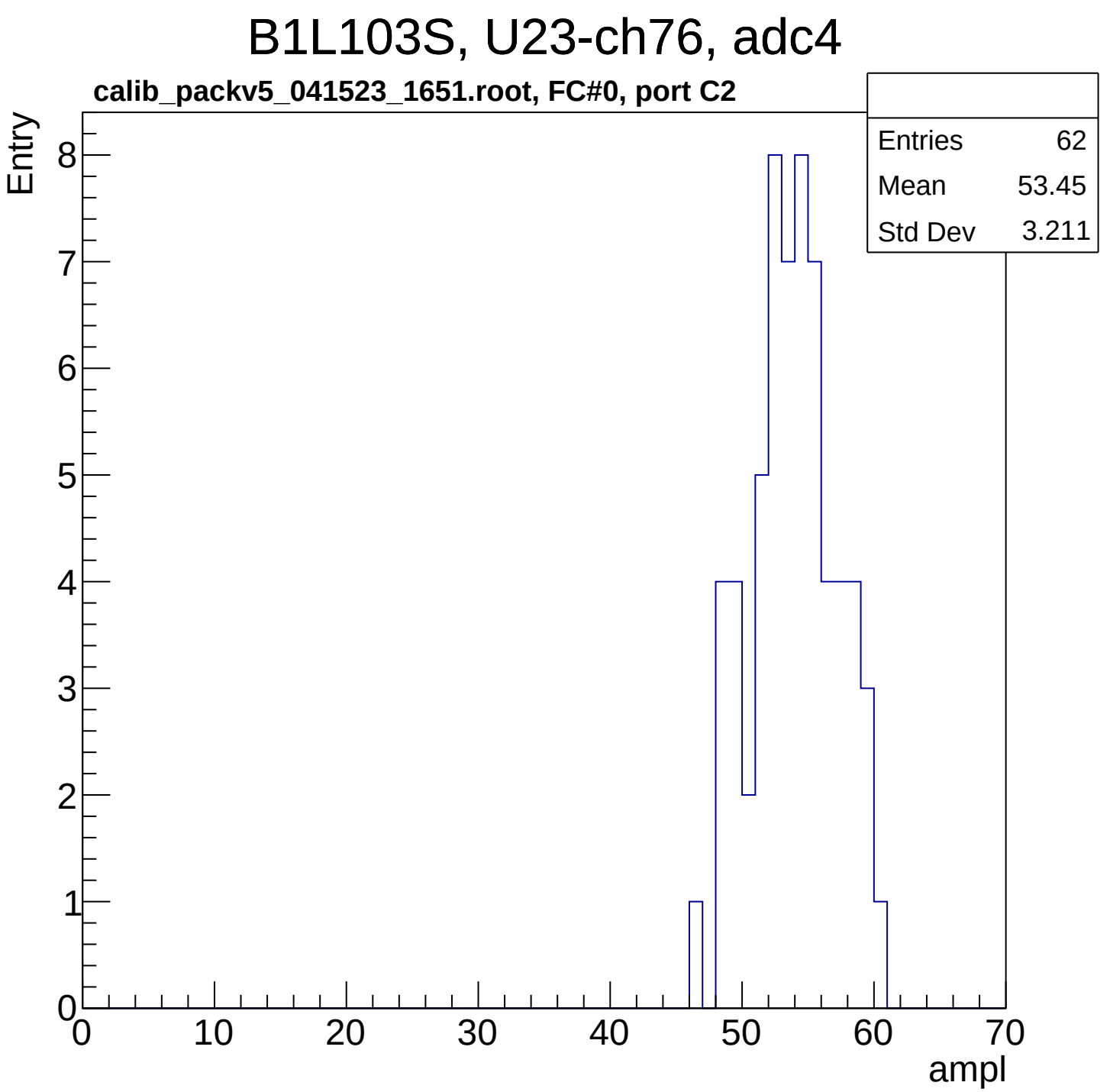

B1L103S, U23-ch75, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

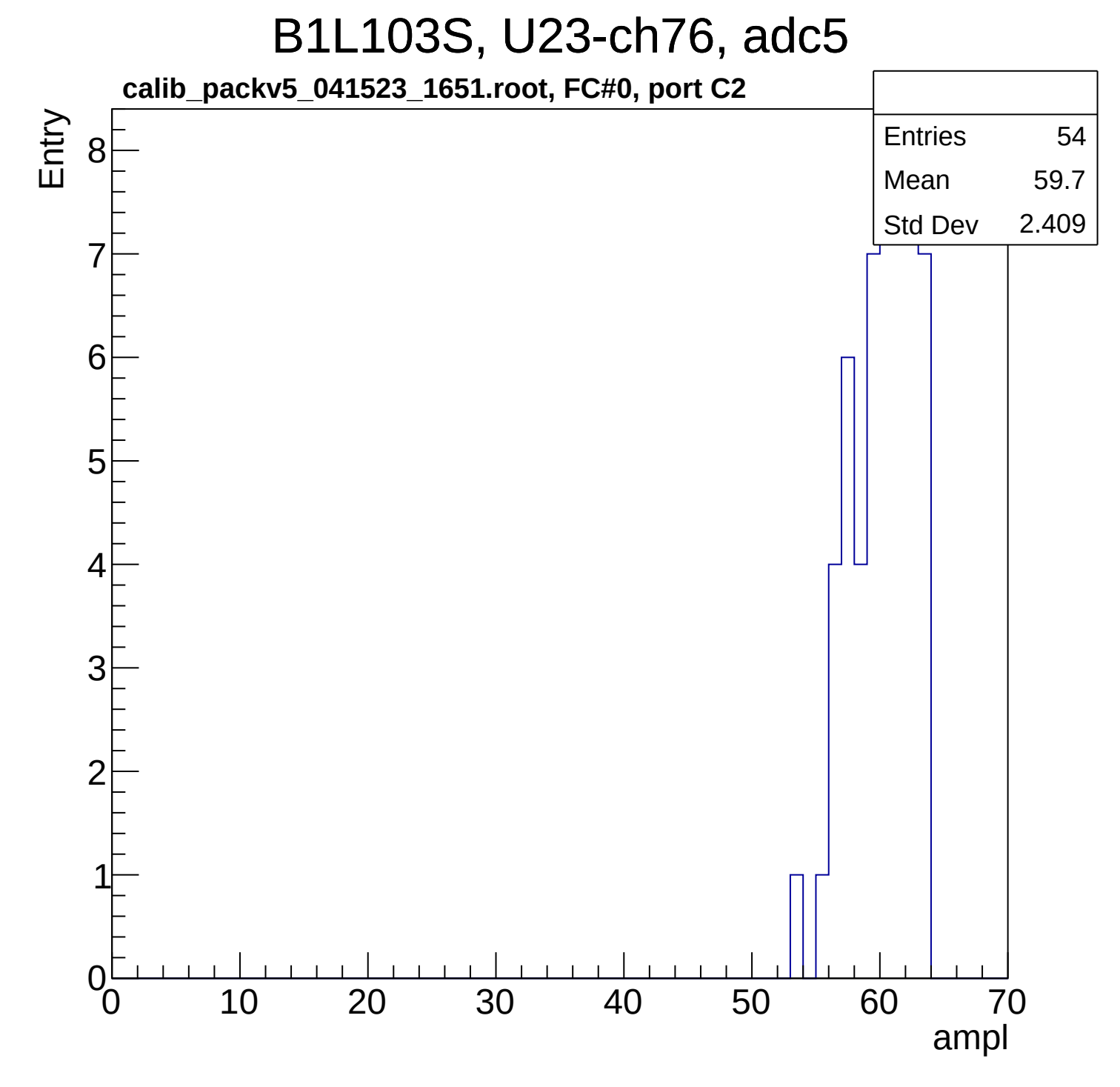

B1L103S, U23-ch76, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 90
Mean 24.17
Std Dev 10.96
Gaus mean : 29.0369
Gaus Width: 4.2108
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

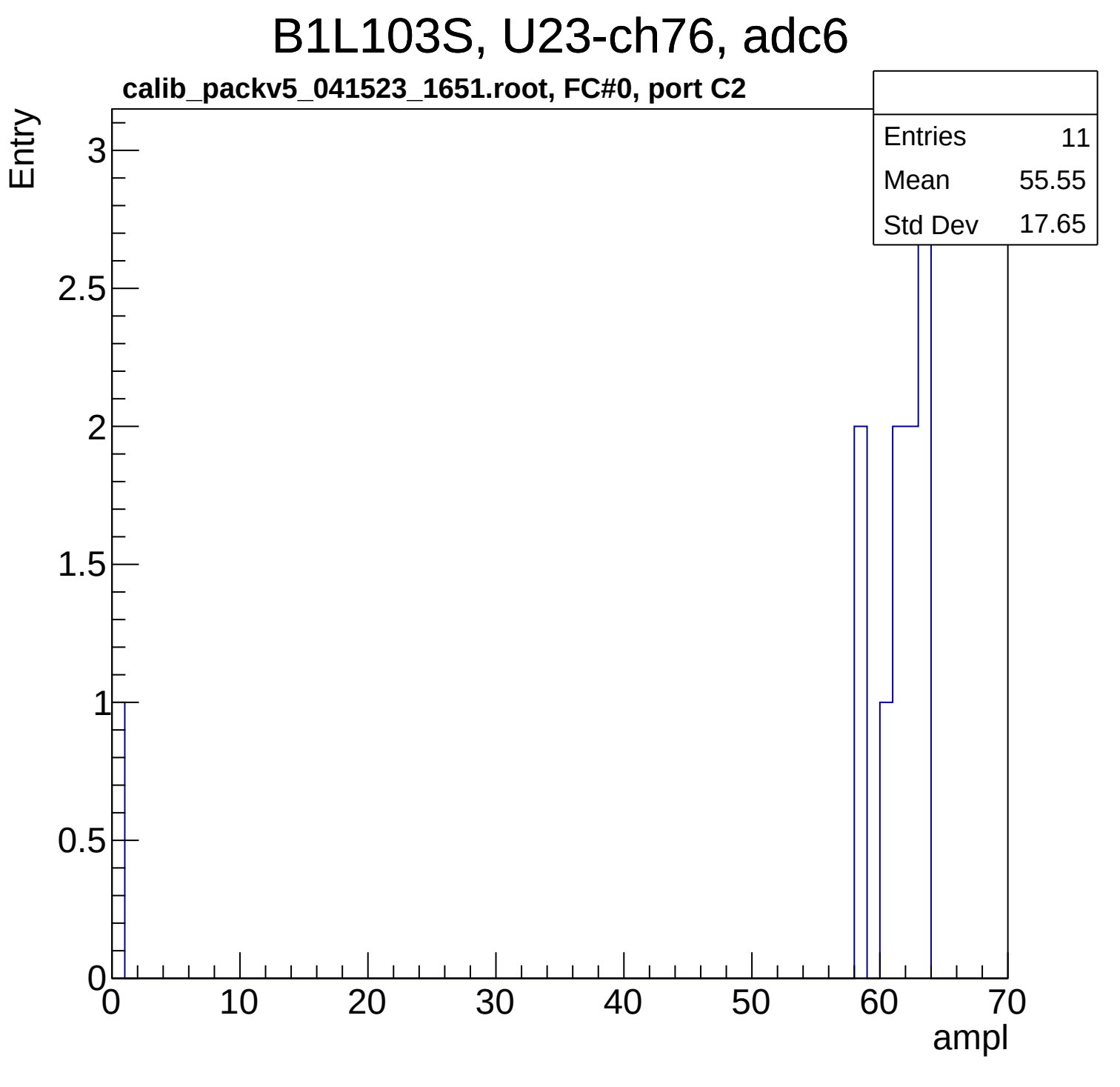

B1L103S, U23-ch76, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 30.33
Std Dev 11.83
Gaus mean : 35.7100
Gaus Width: 3.7366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

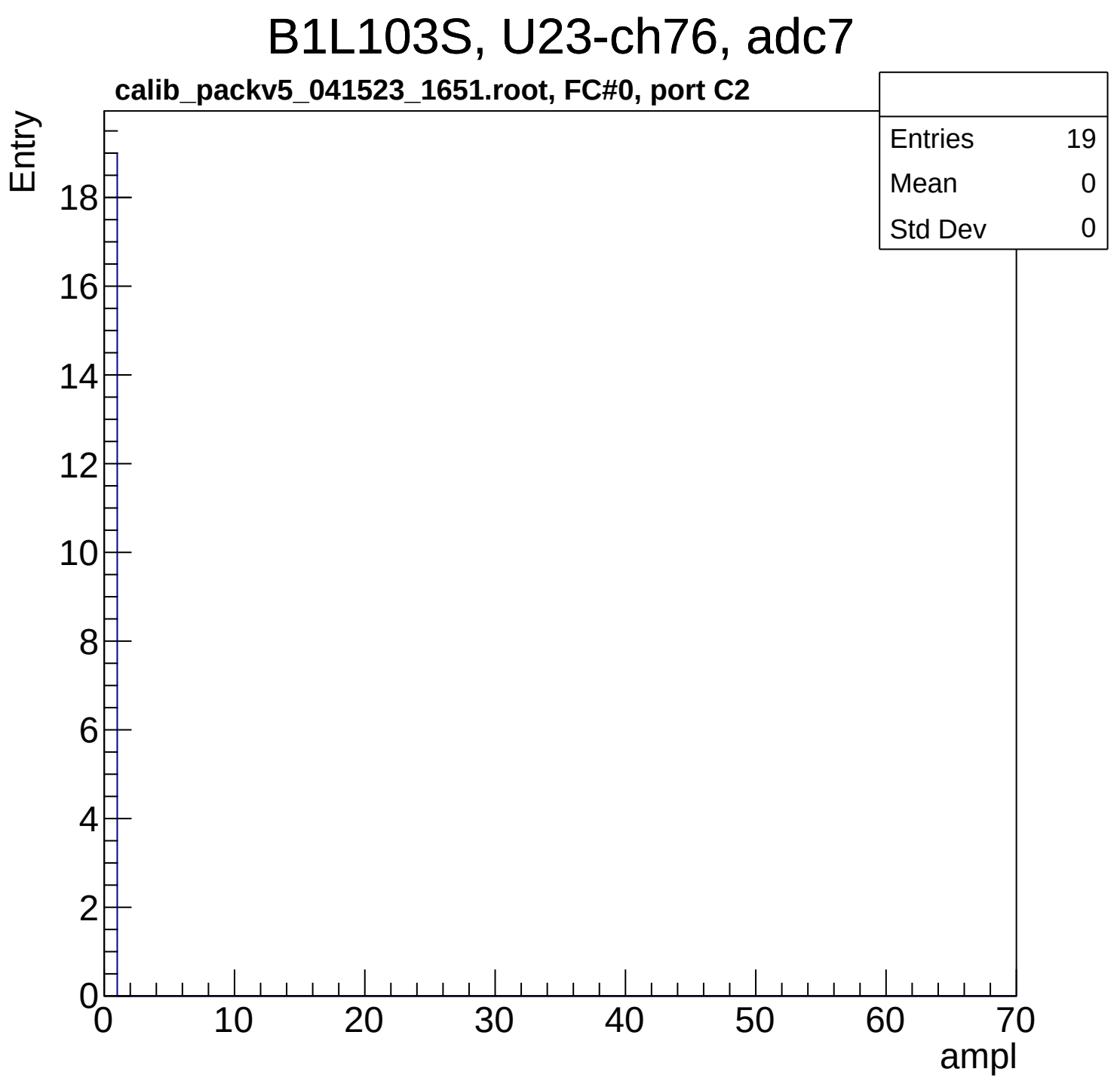

B1L103S, U23-ch76, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 36.08
Std Dev 13.38
Gaus mean : 41.3534
Gaus Width: 4.0085
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

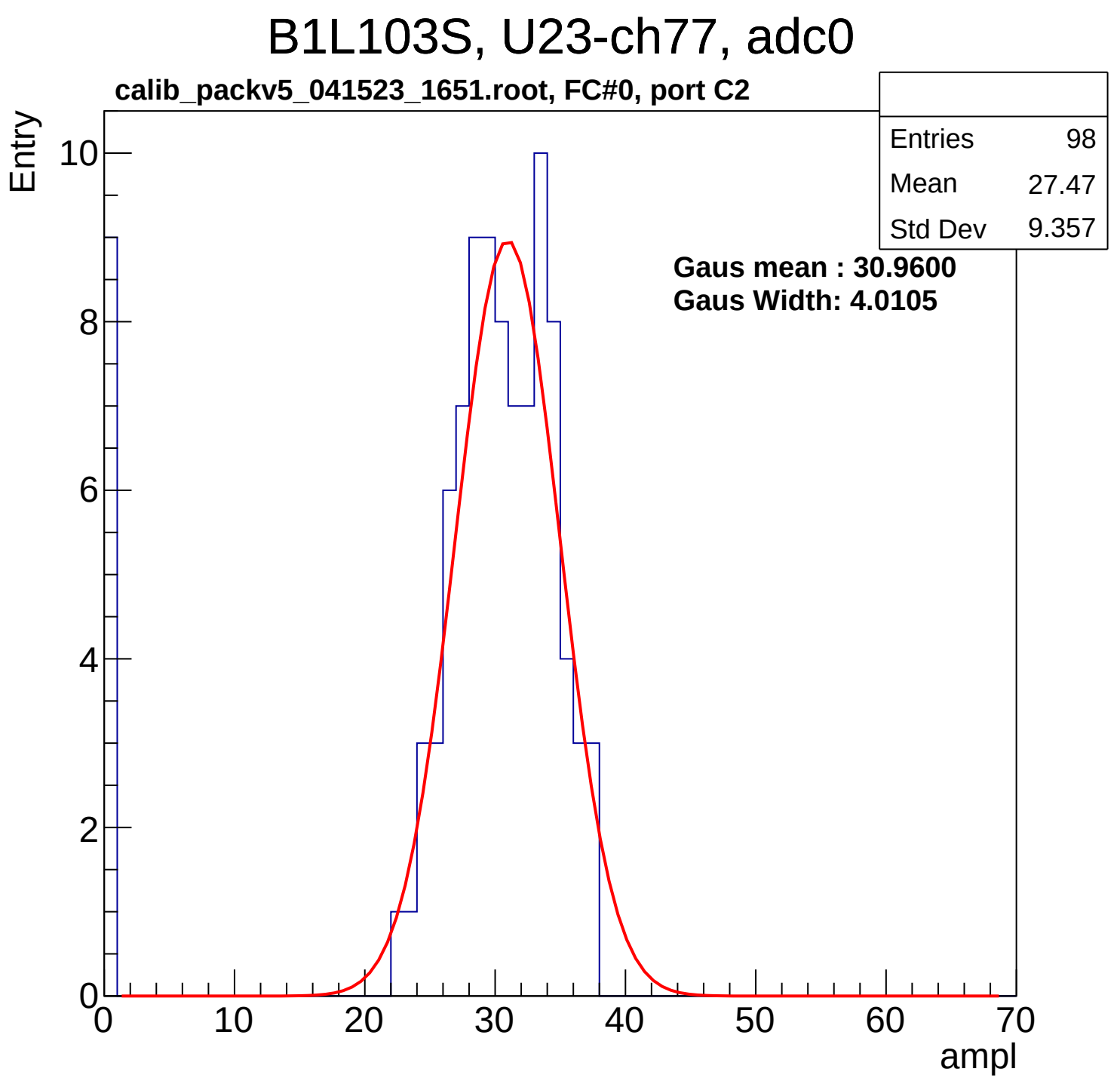

B1L103S, U23-ch76, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 47.86
Std Dev 3.741
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

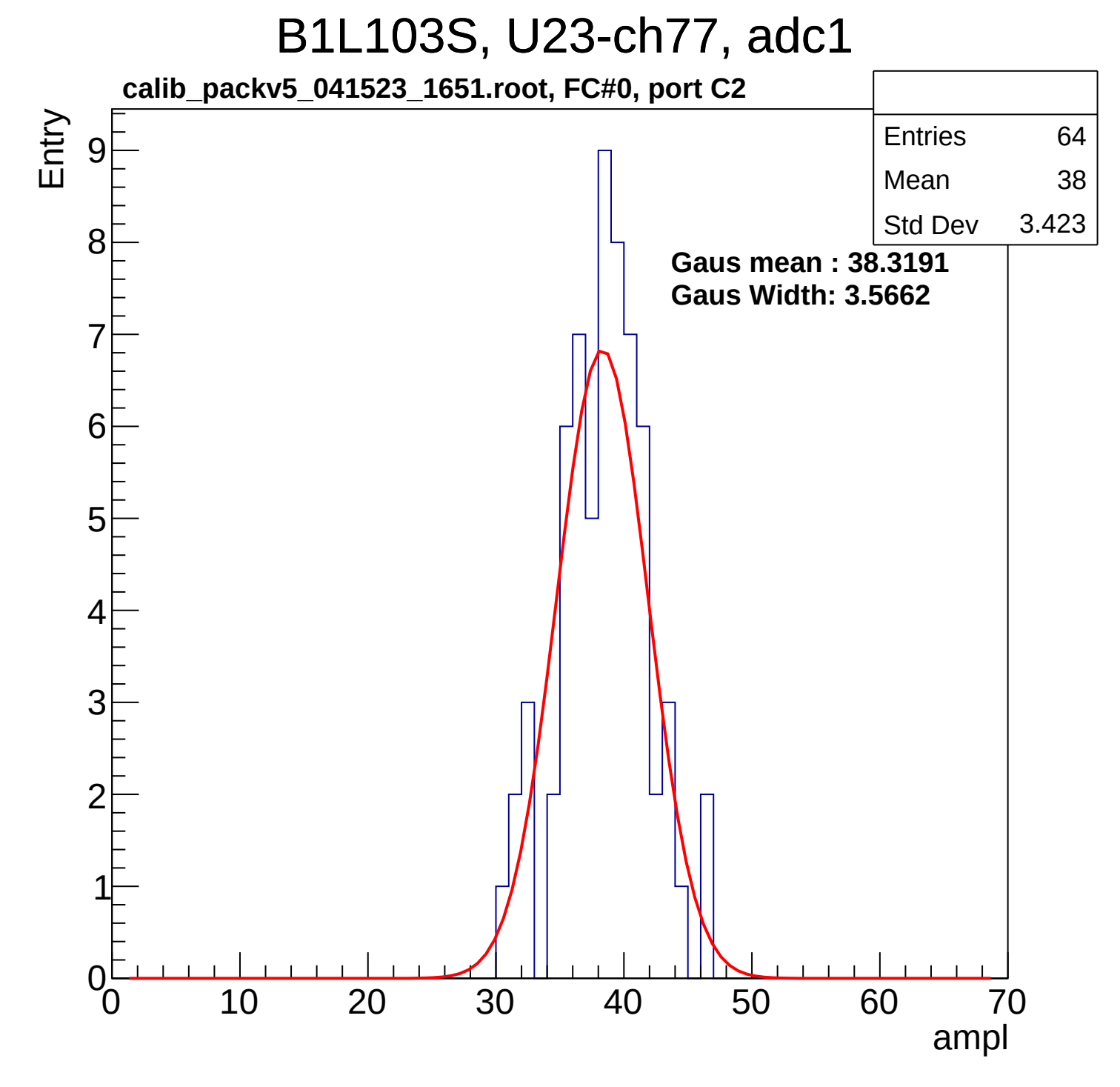

B1L103S, U23-ch76, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 53.45
Std Dev 3.211
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

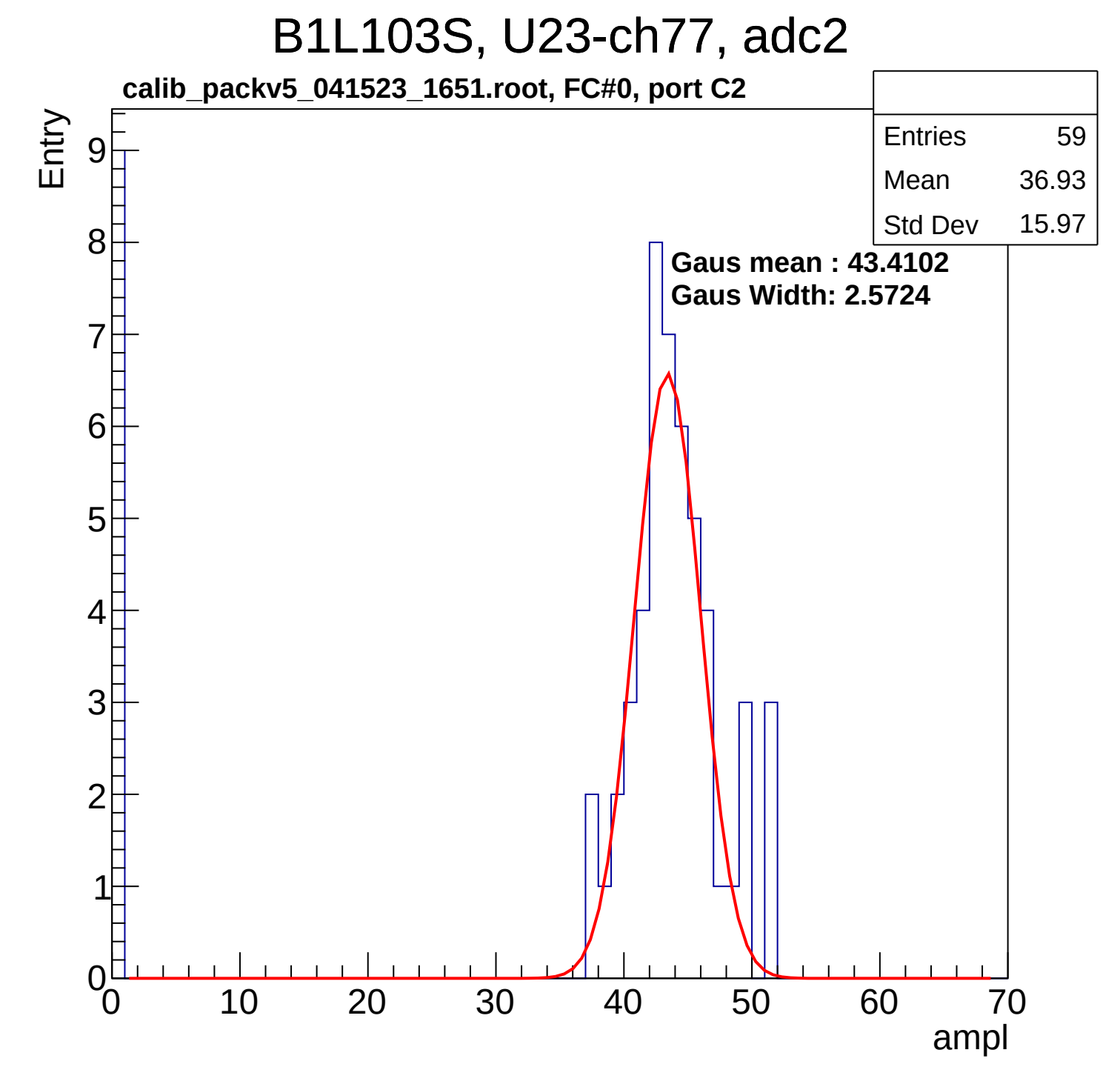

B1L103S, U23-ch76, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 59.7
Std Dev 2.409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

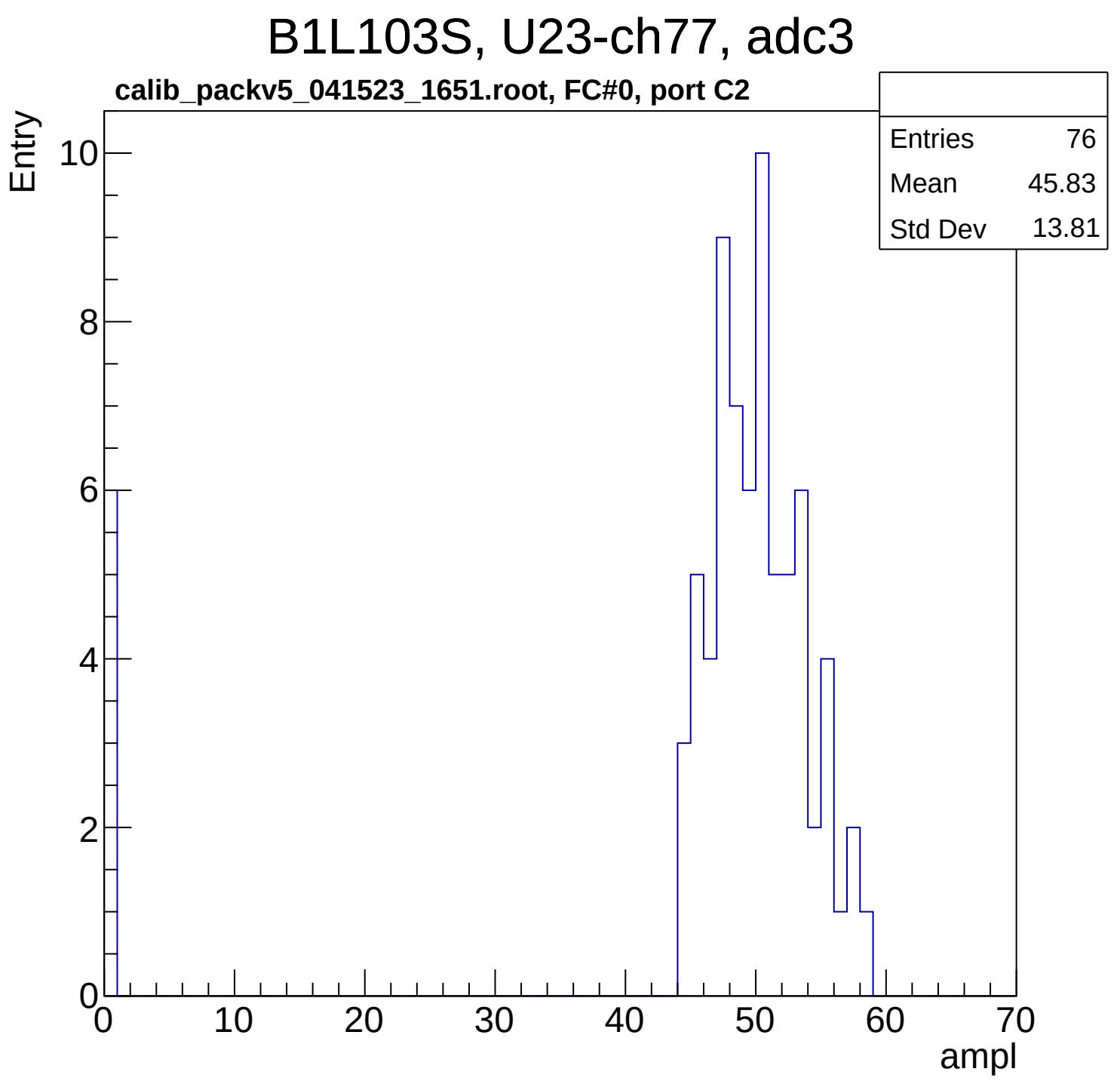

B1L103S, U23-ch76, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 55.55
Std Dev 17.65
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

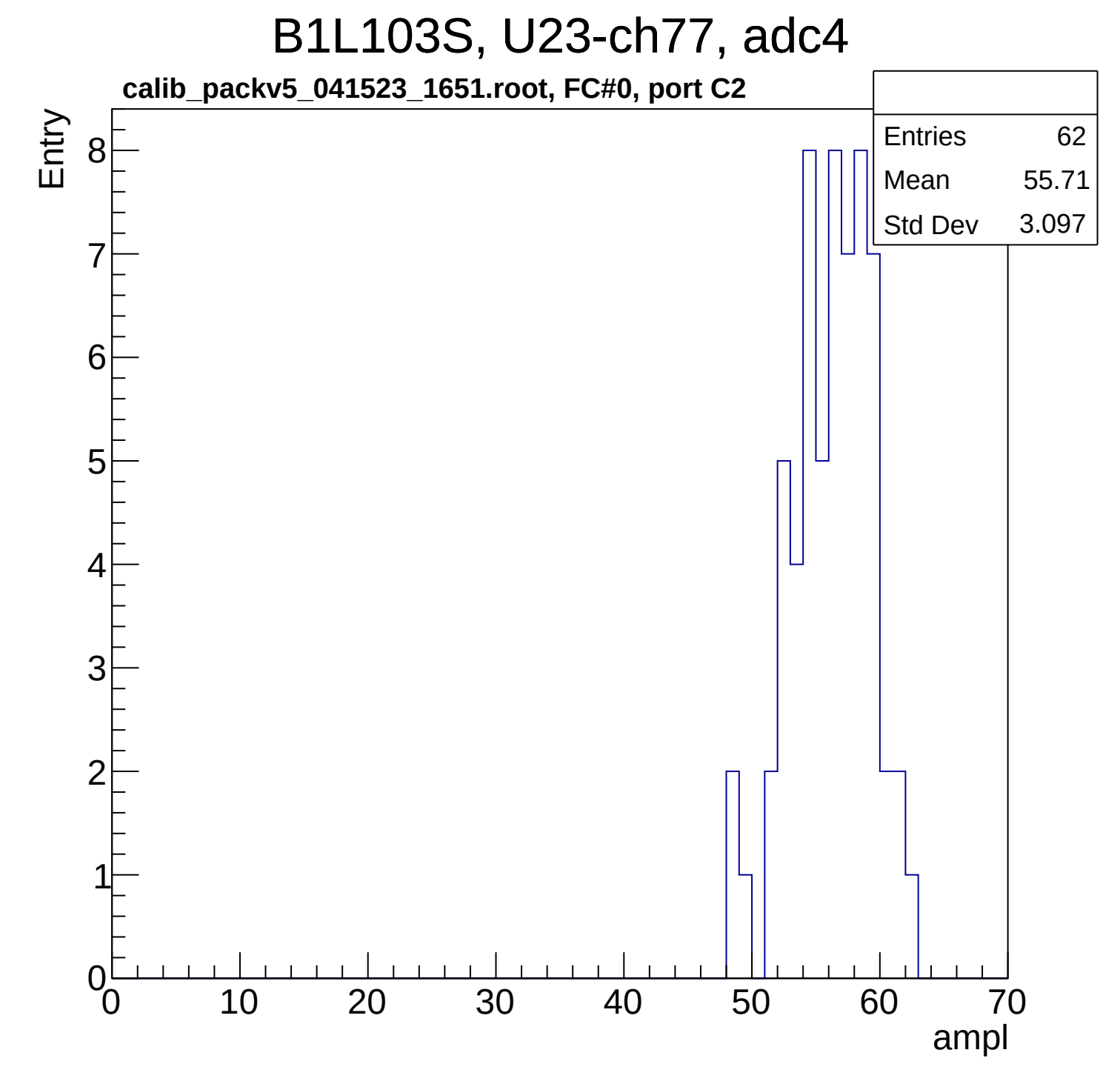

B1L103S, U23-ch76, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

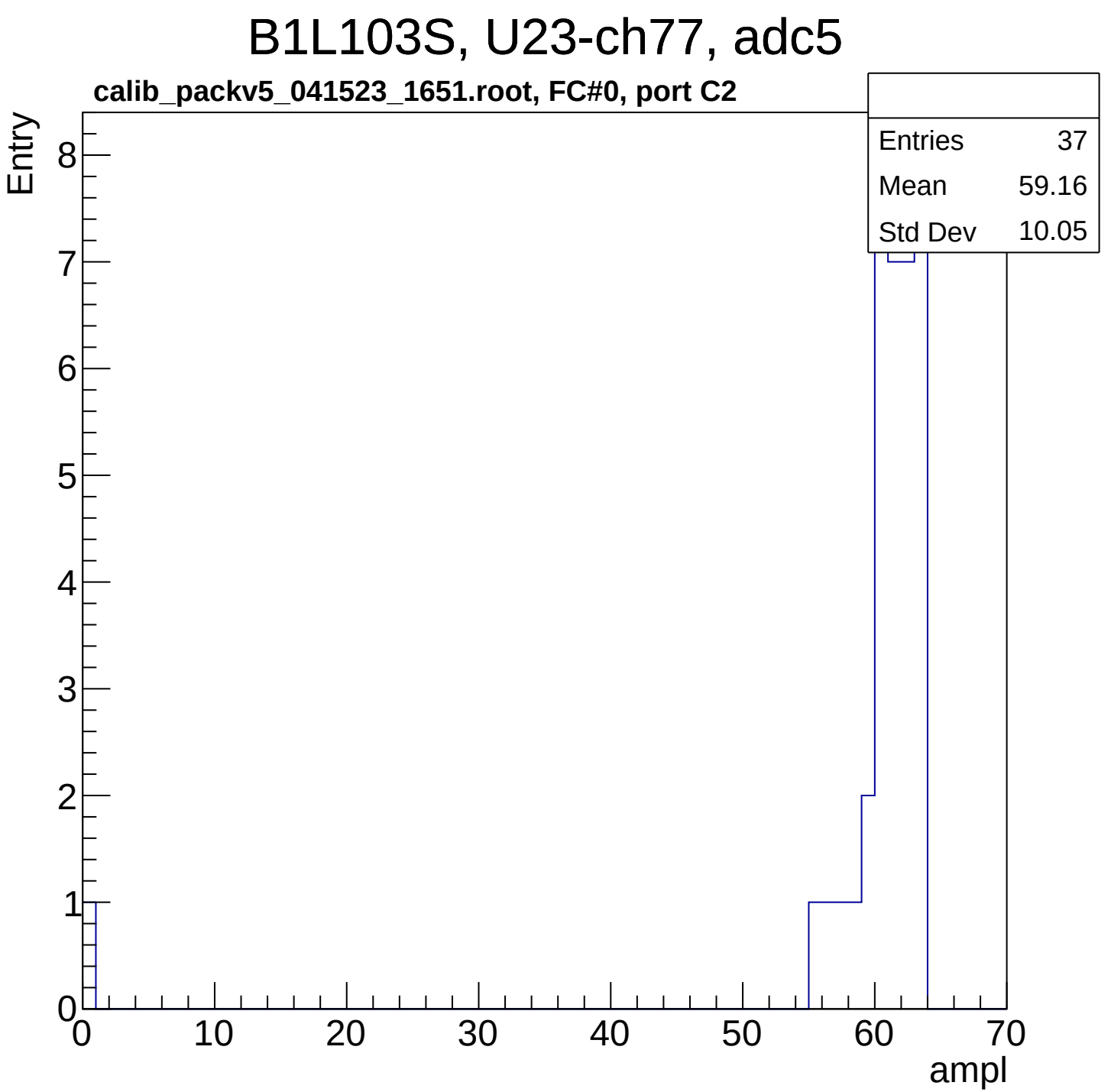

B1L103S, U23-ch77, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 98
Mean 27.47
Std Dev 9.357
Gaus mean : 30.9600
Gaus Width: 4.0105
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

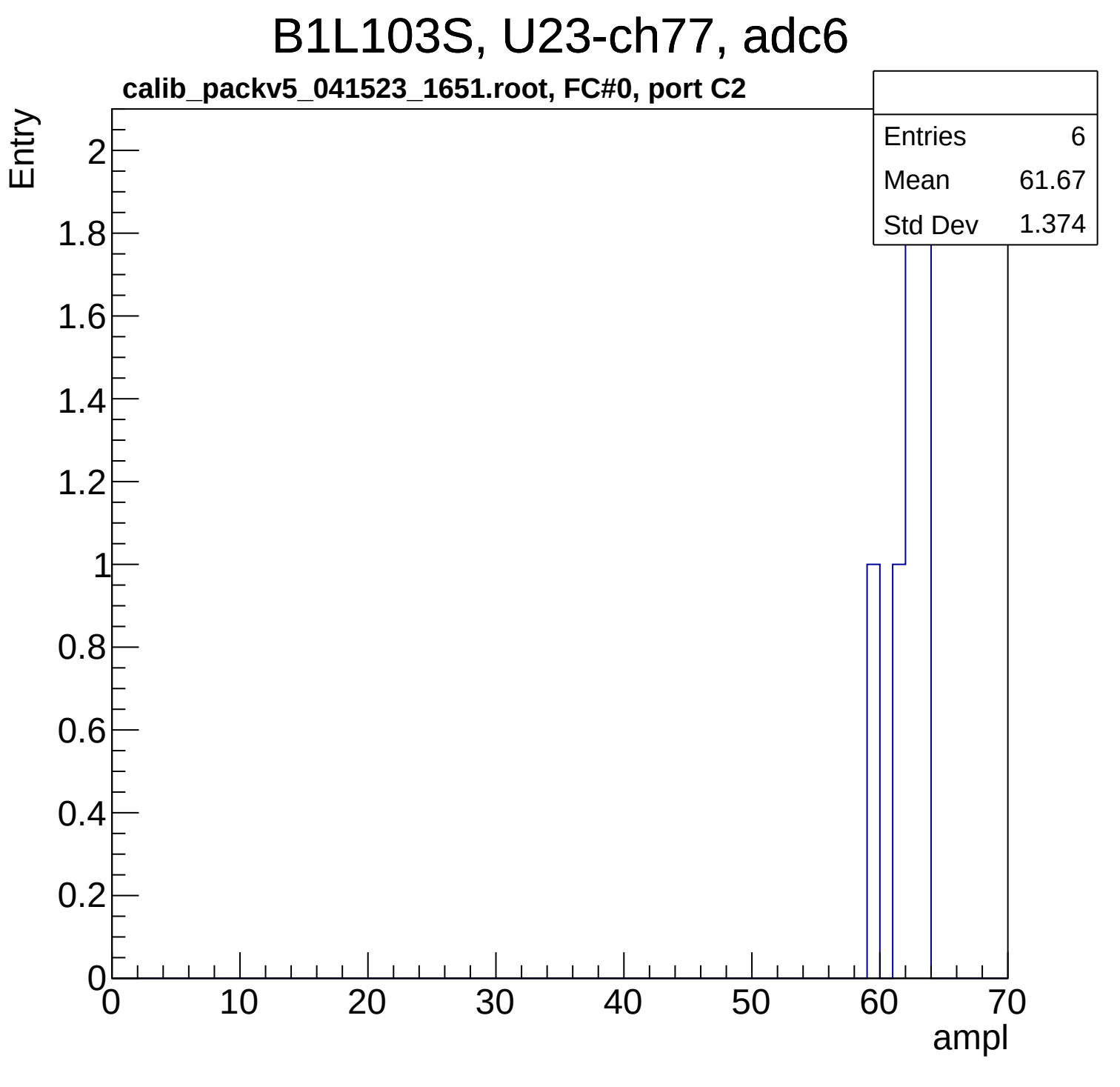

B1L103S, U23-ch77, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 38
Std Dev 3.423
Gaus mean : 38.3191
Gaus Width: 3.5662
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

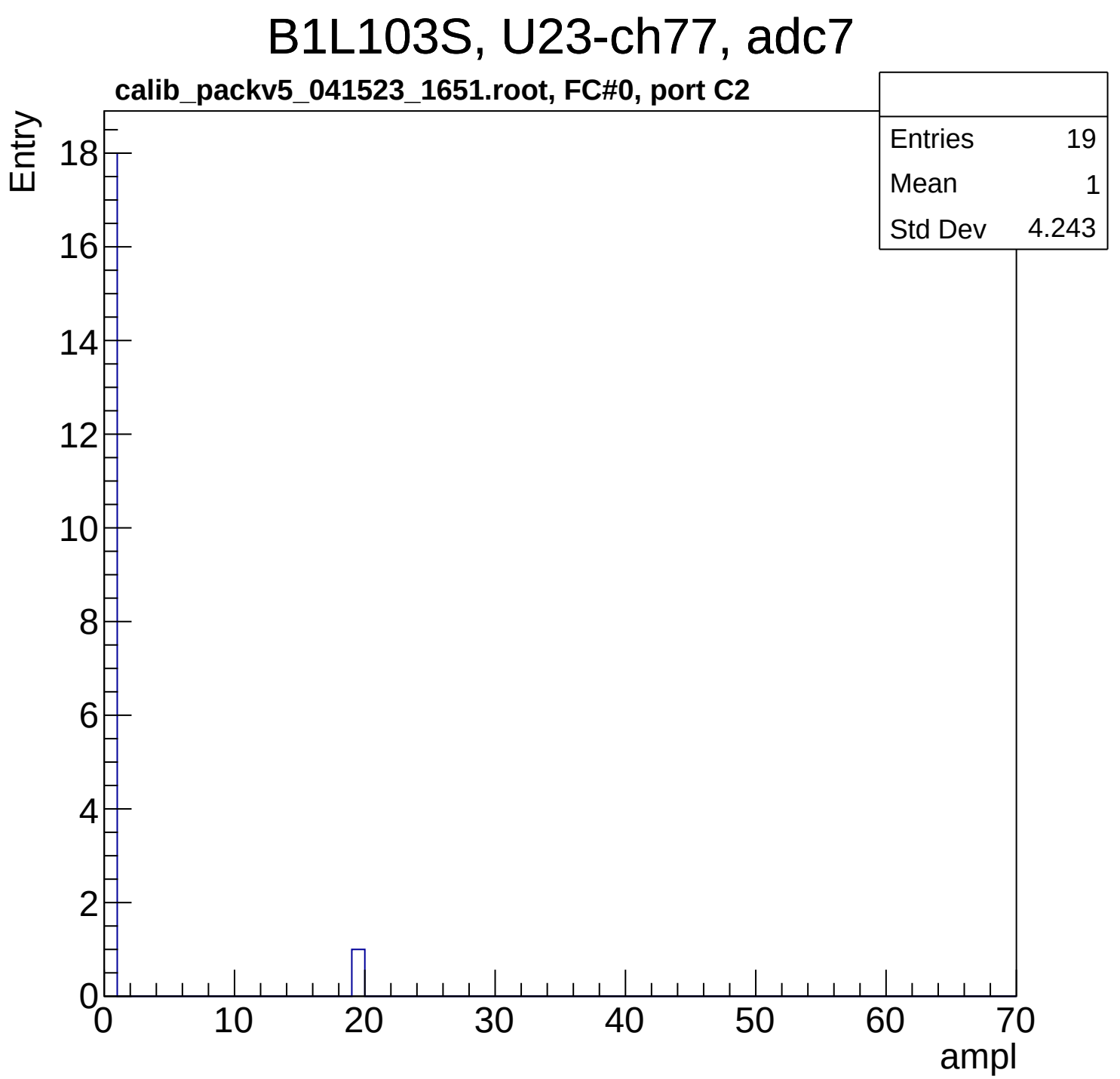

B1L103S, U23-ch77, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 36.93
Std Dev 15.97
Gaus mean : 43.4102
Gaus Width: 2.5724
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

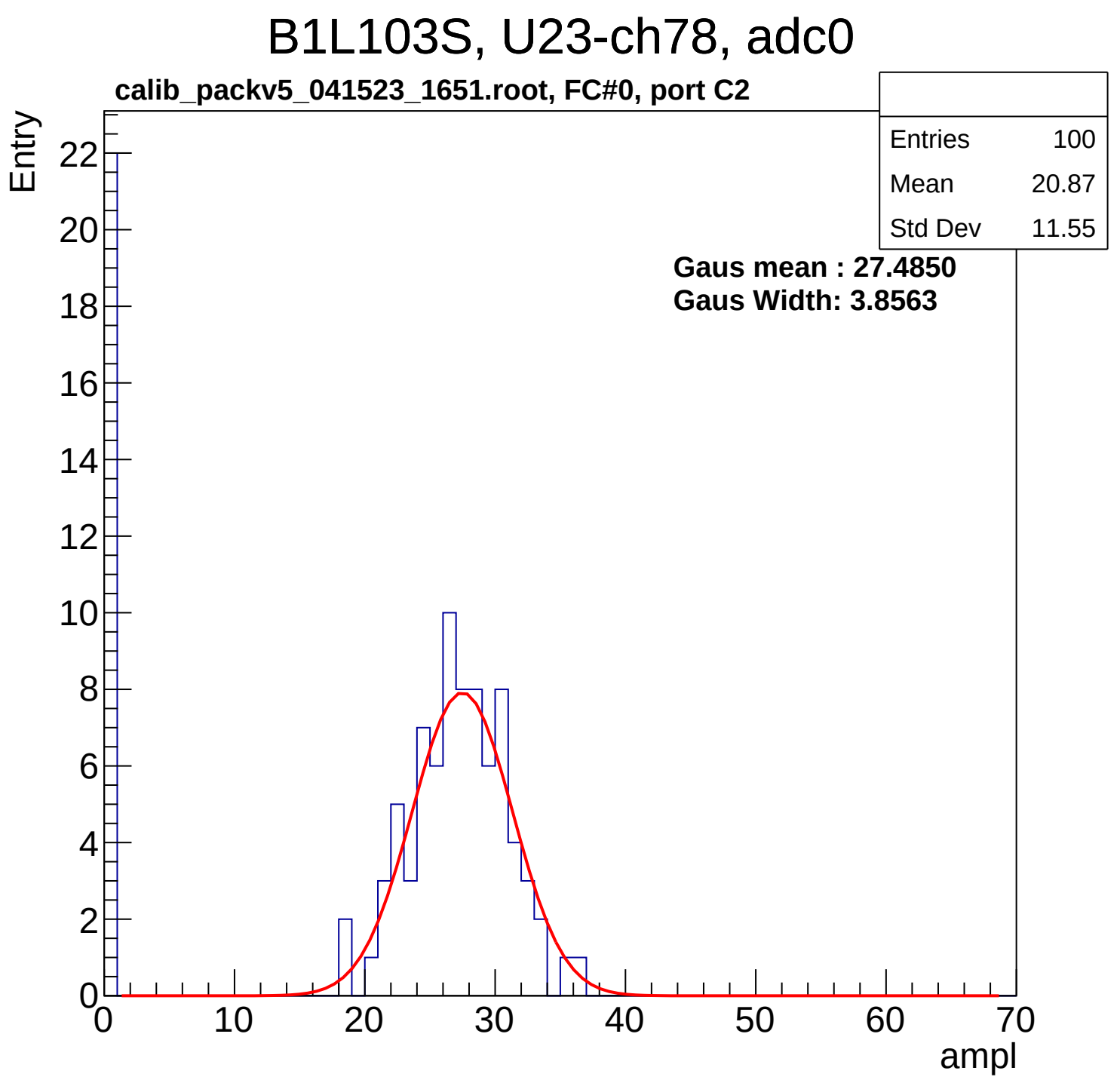

B1L103S, U23-ch77, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 45.83
Std Dev 13.81
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

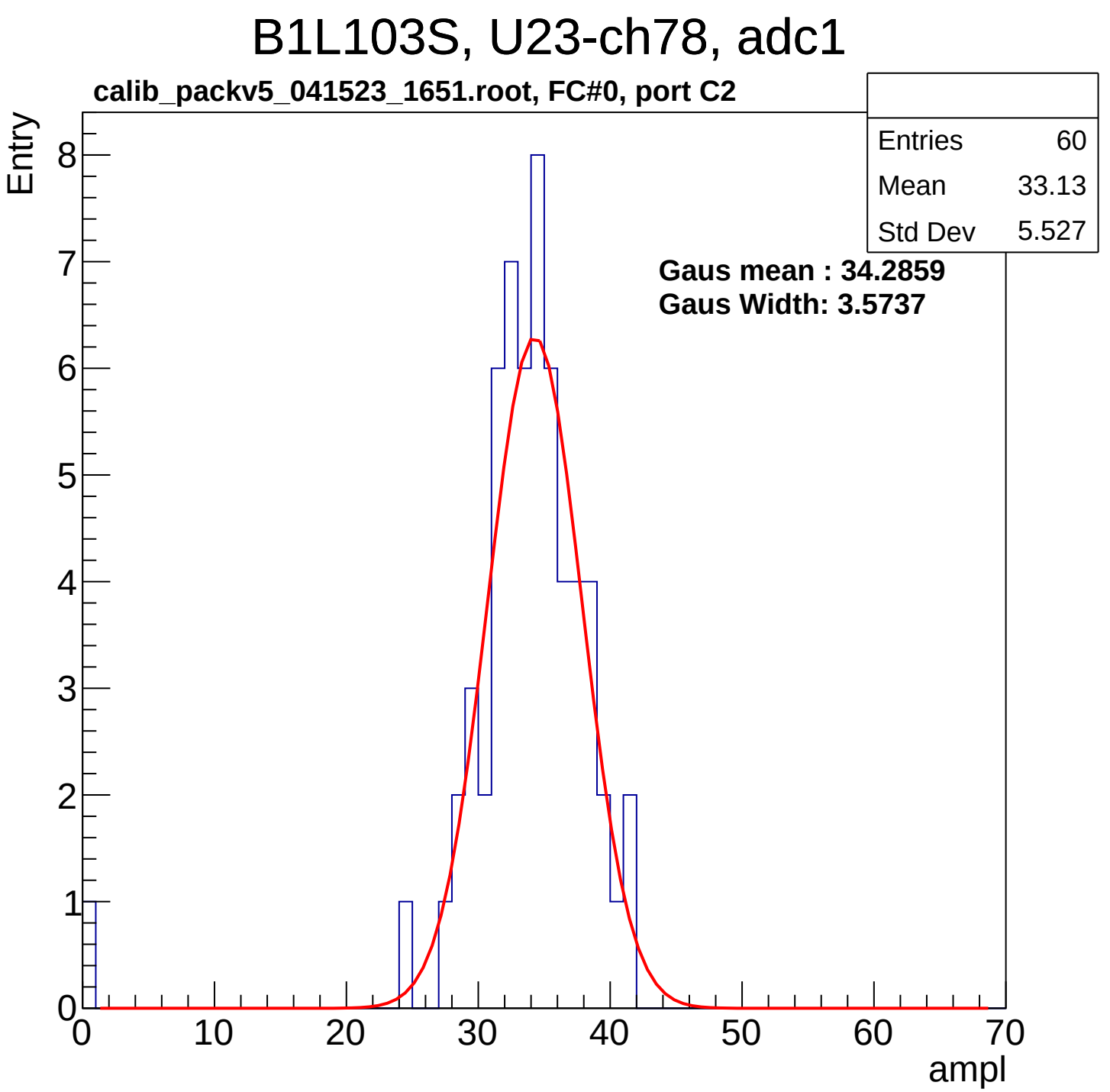

B1L103S, U23-ch77, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 55.71
Std Dev 3.097
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

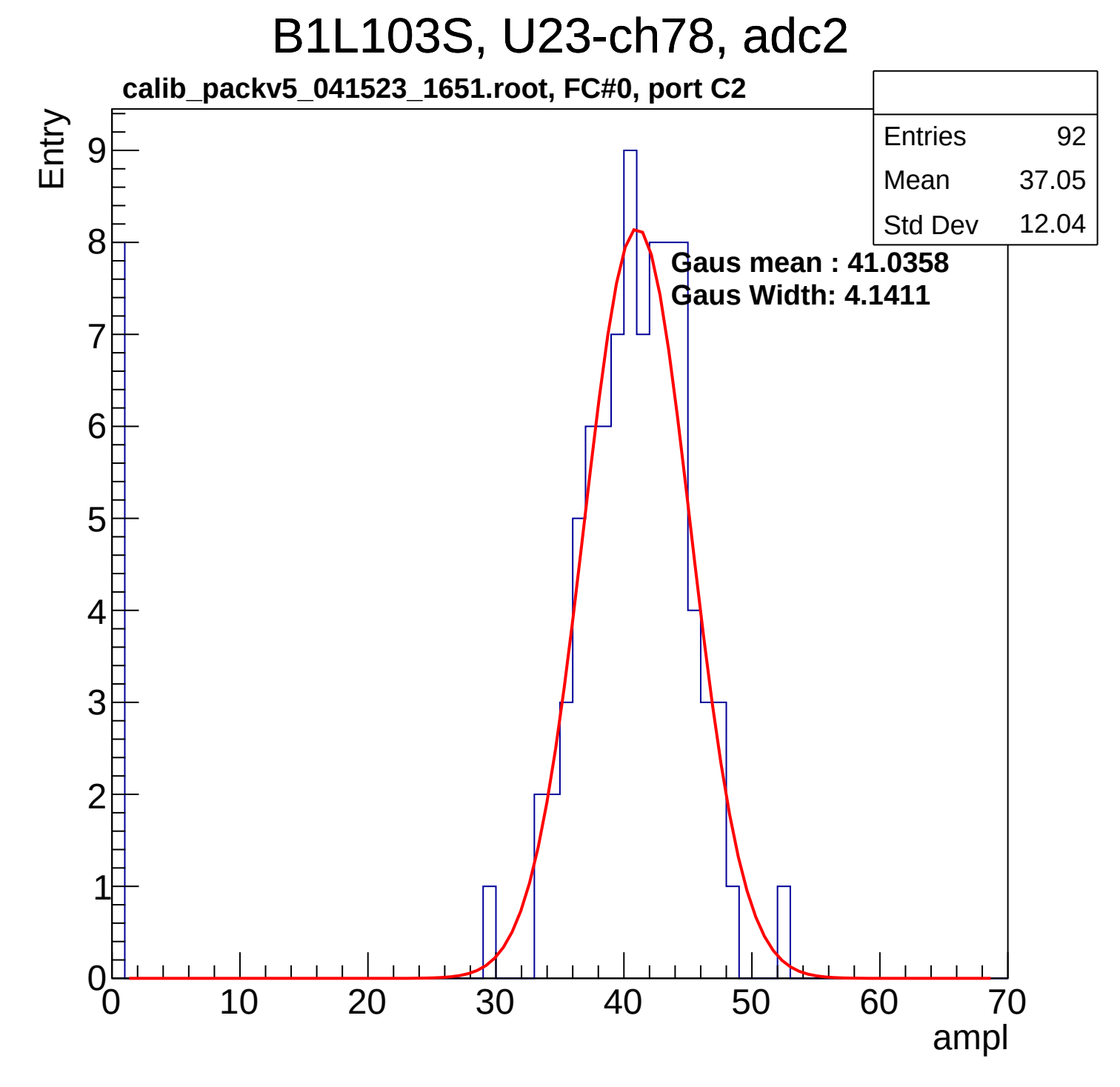

B1L103S, U23-ch77, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 37
Mean 59.16
Std Dev 10.05
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

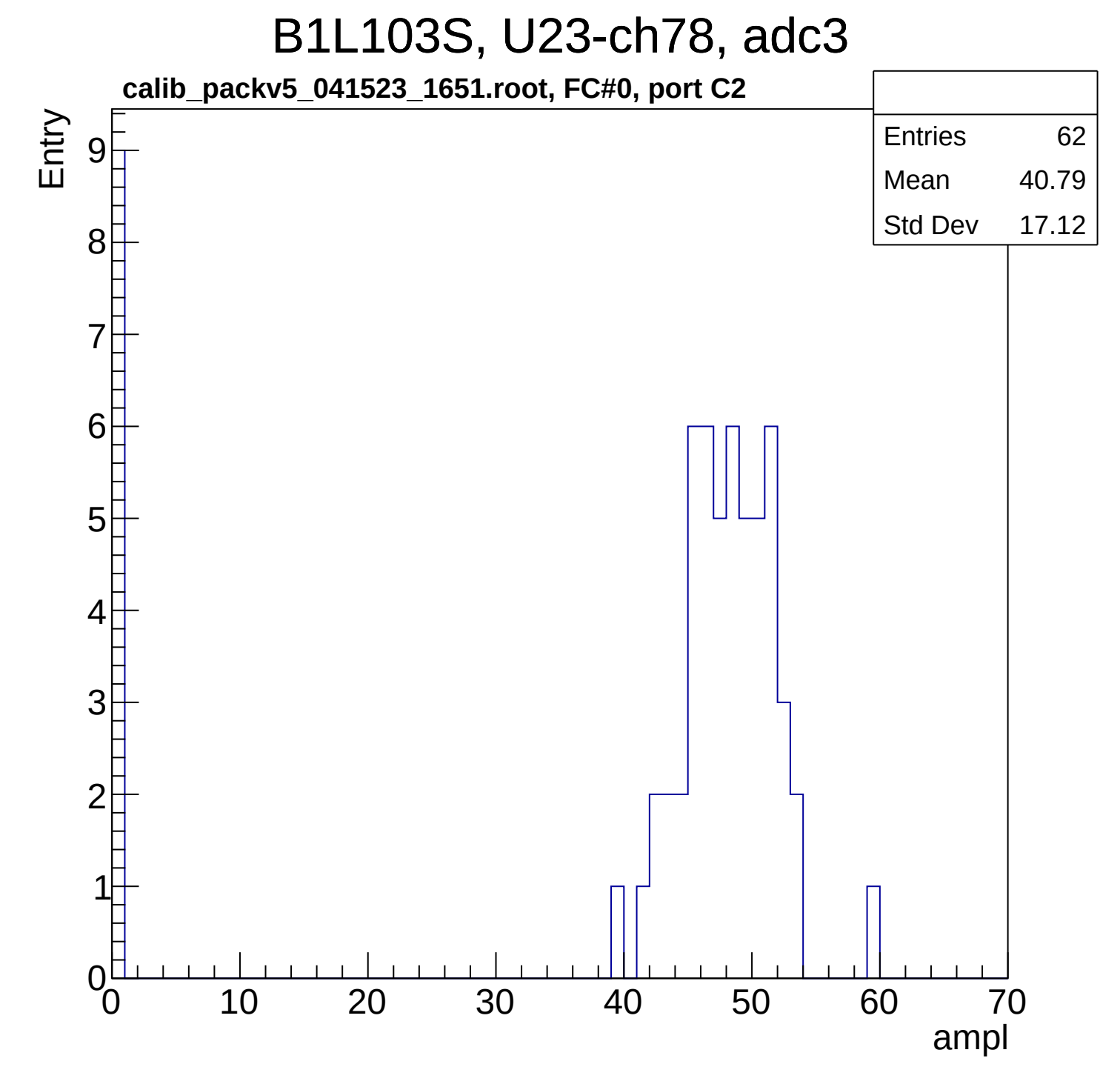

B1L103S, U23-ch77, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 61.67
Std Dev 1.374
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

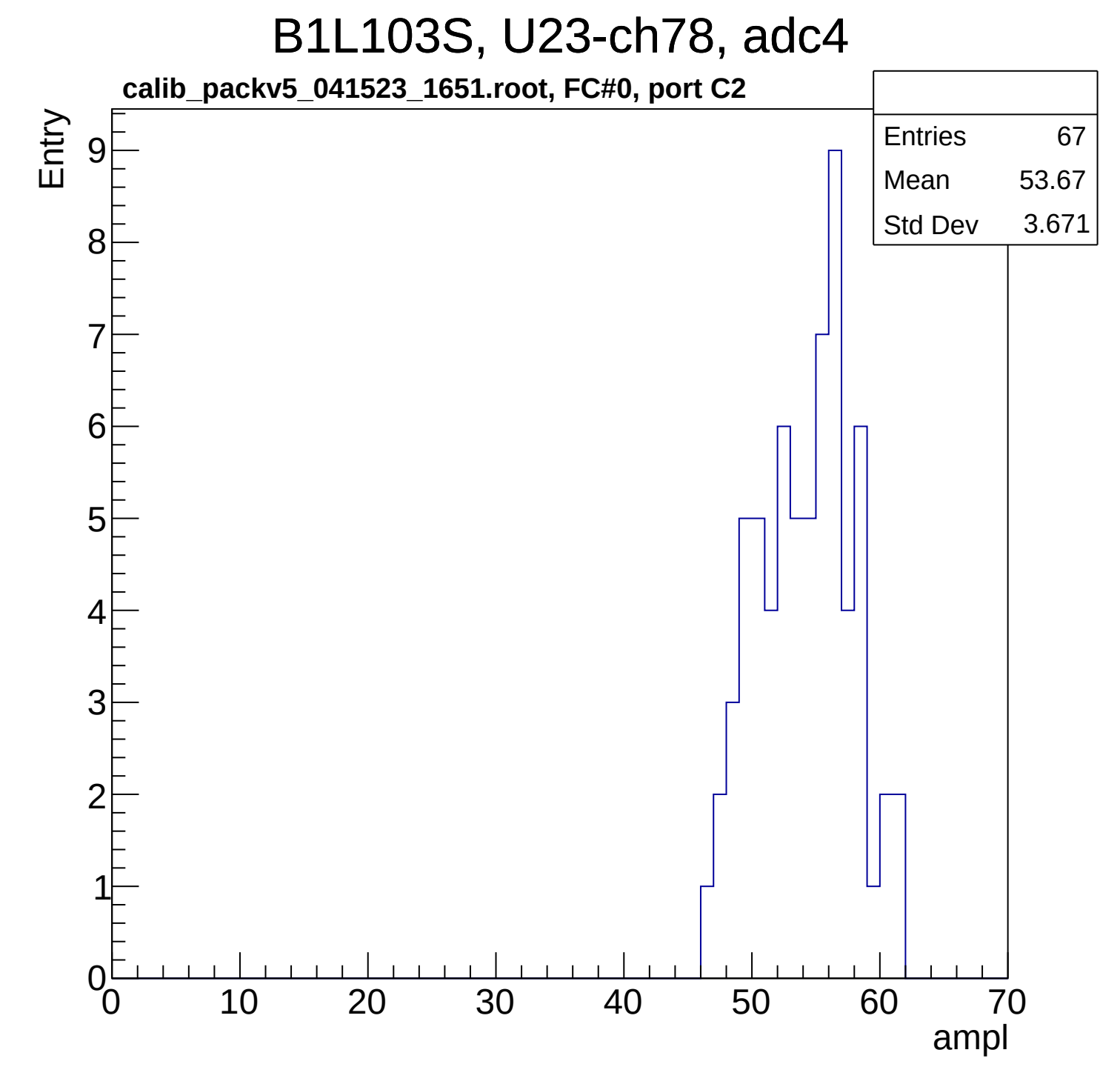

B1L103S, U23-ch77, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1
Std Dev 4.243
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

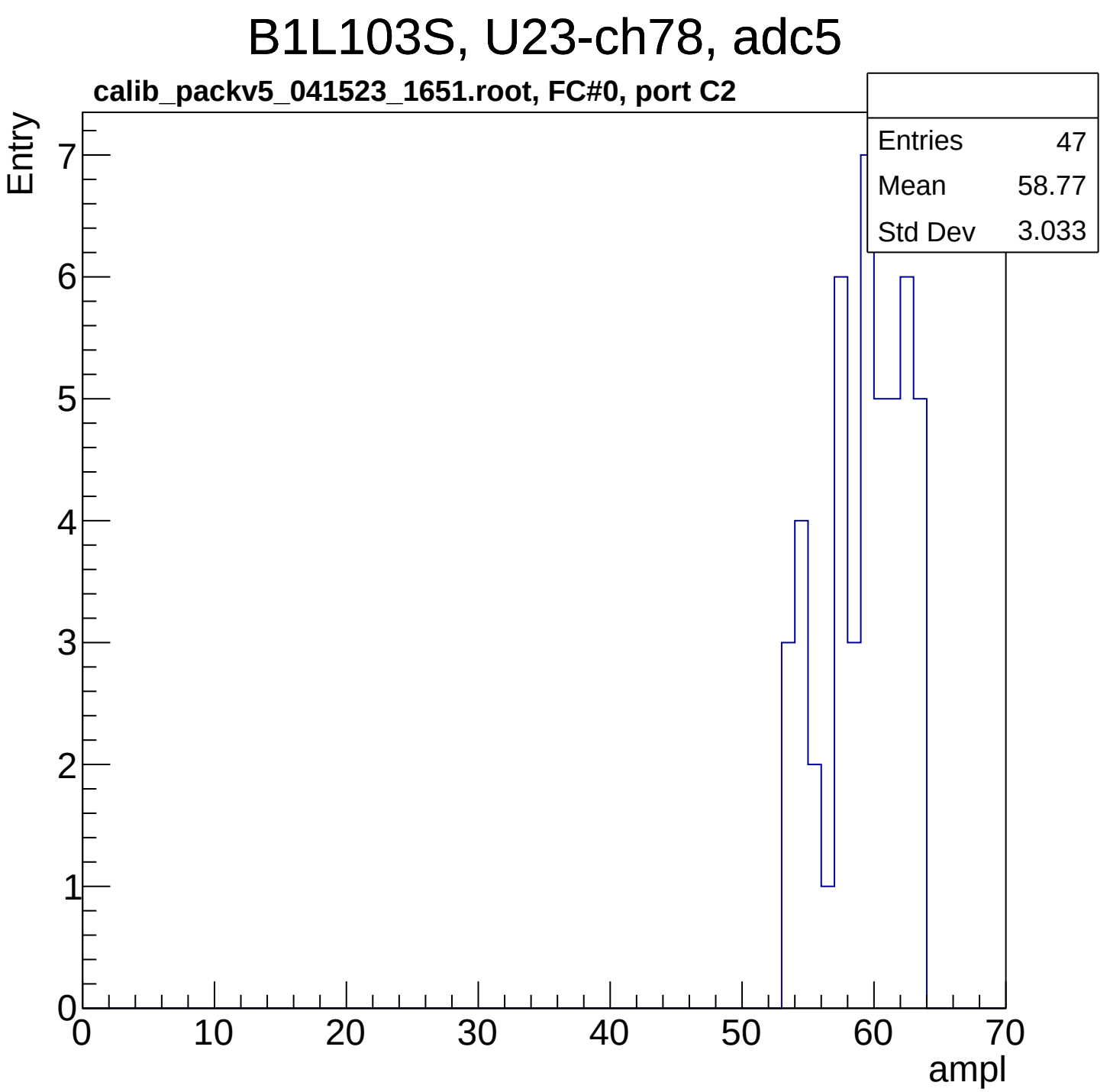

B1L103S, U23-ch78, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 20.87
Std Dev 11.55
Gaus mean : 27.4850
Gaus Width: 3.8563
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

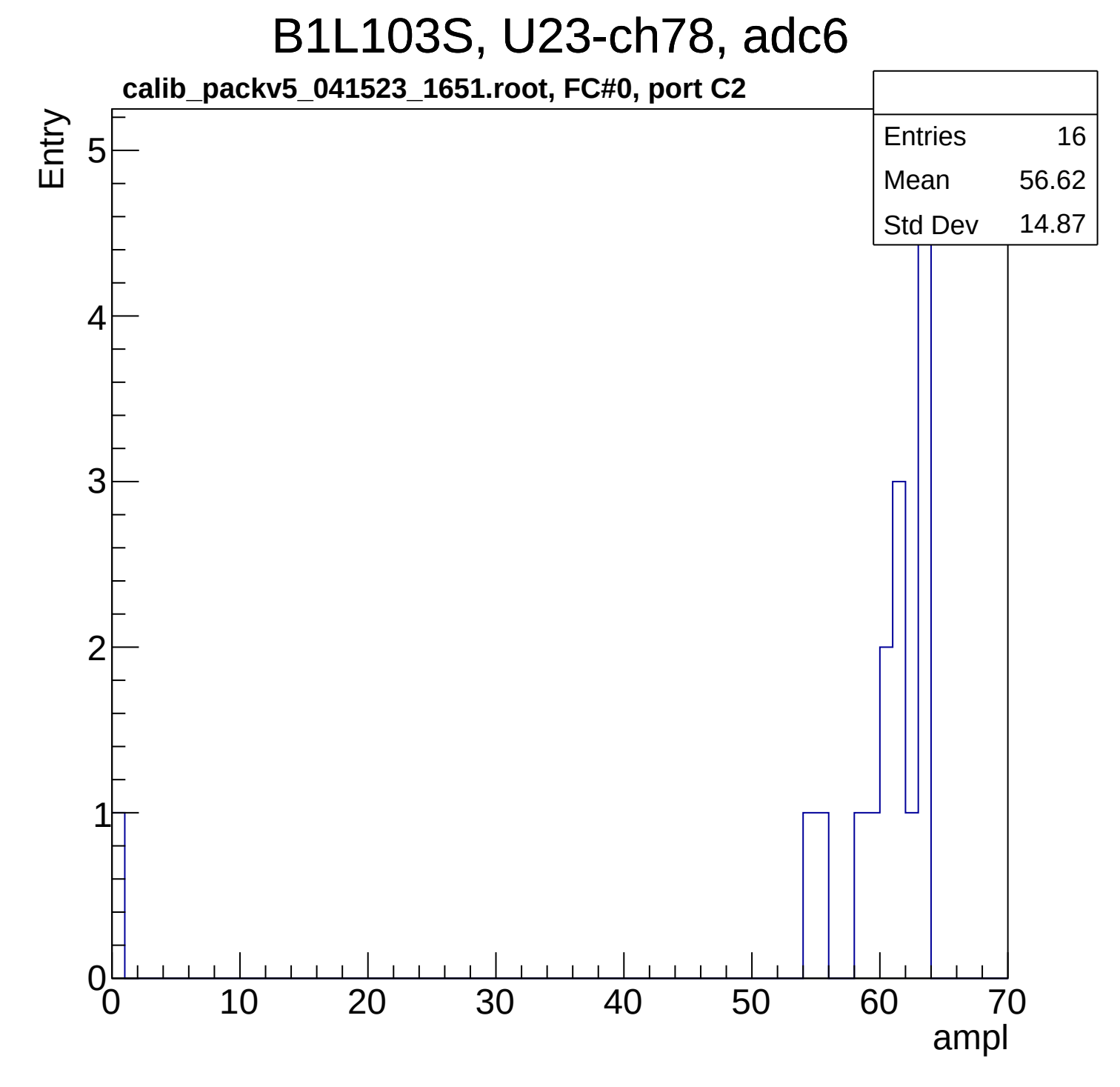

B1L103S, U23-ch78, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 33.13
Std Dev 5.527
Gaus mean : 34.2859
Gaus Width: 3.5737
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

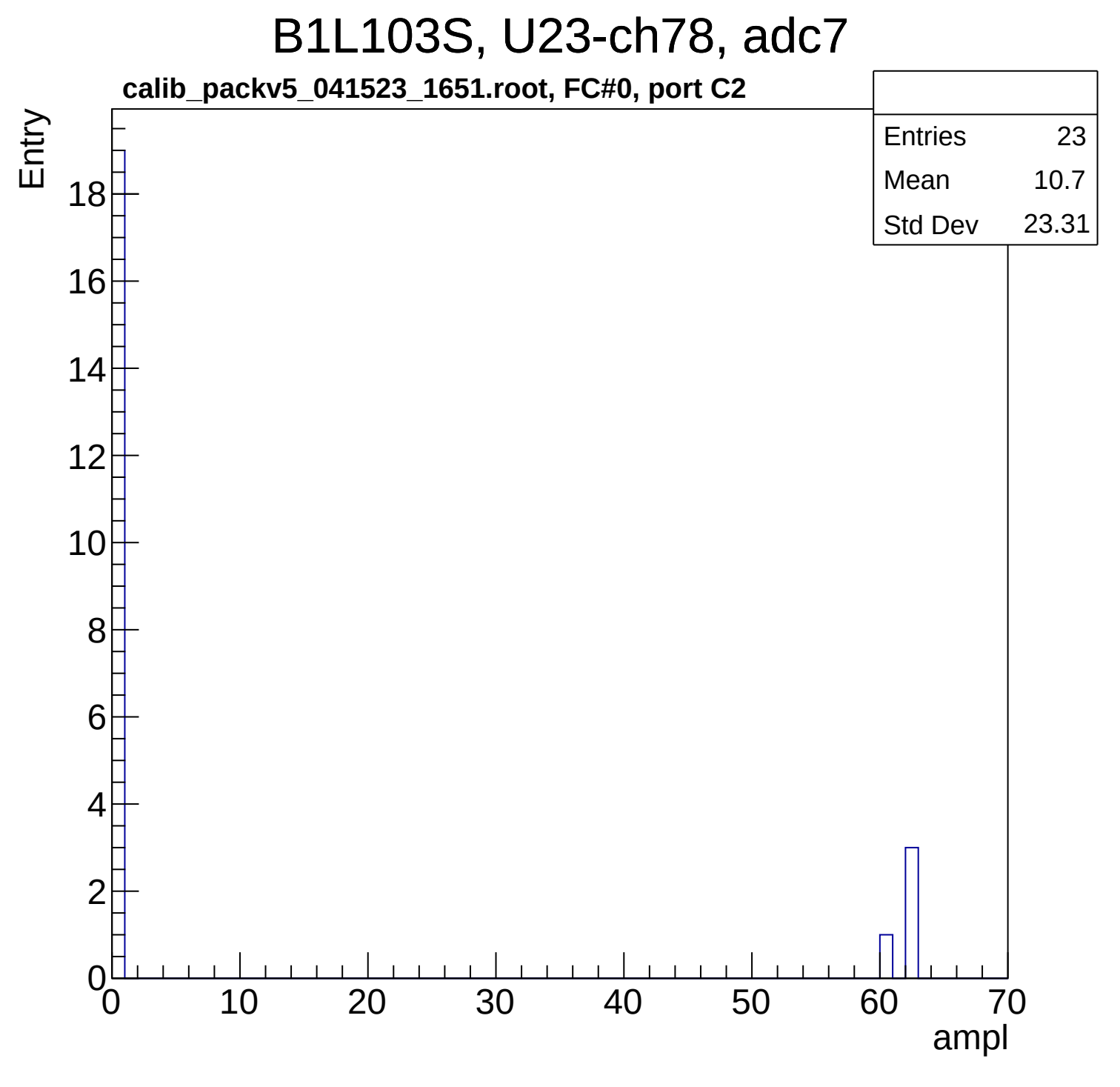

B1L103S, U23-ch78, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 92
Mean 37.05
Std Dev 12.04
Gaus mean : 41.0358
Gaus Width: 4.1411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

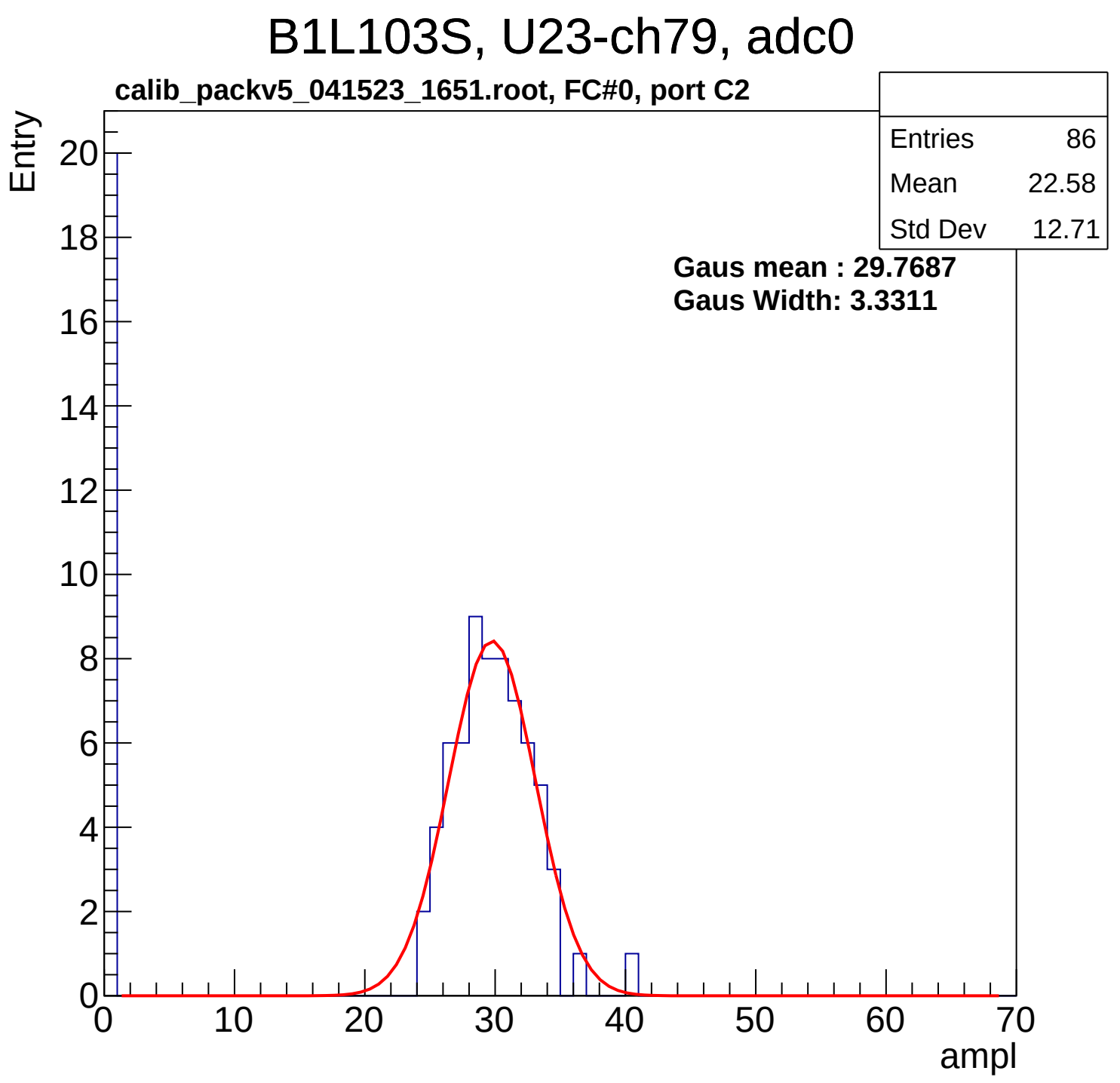

B1L103S, U23-ch78, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 40.79
Std Dev 17.12
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

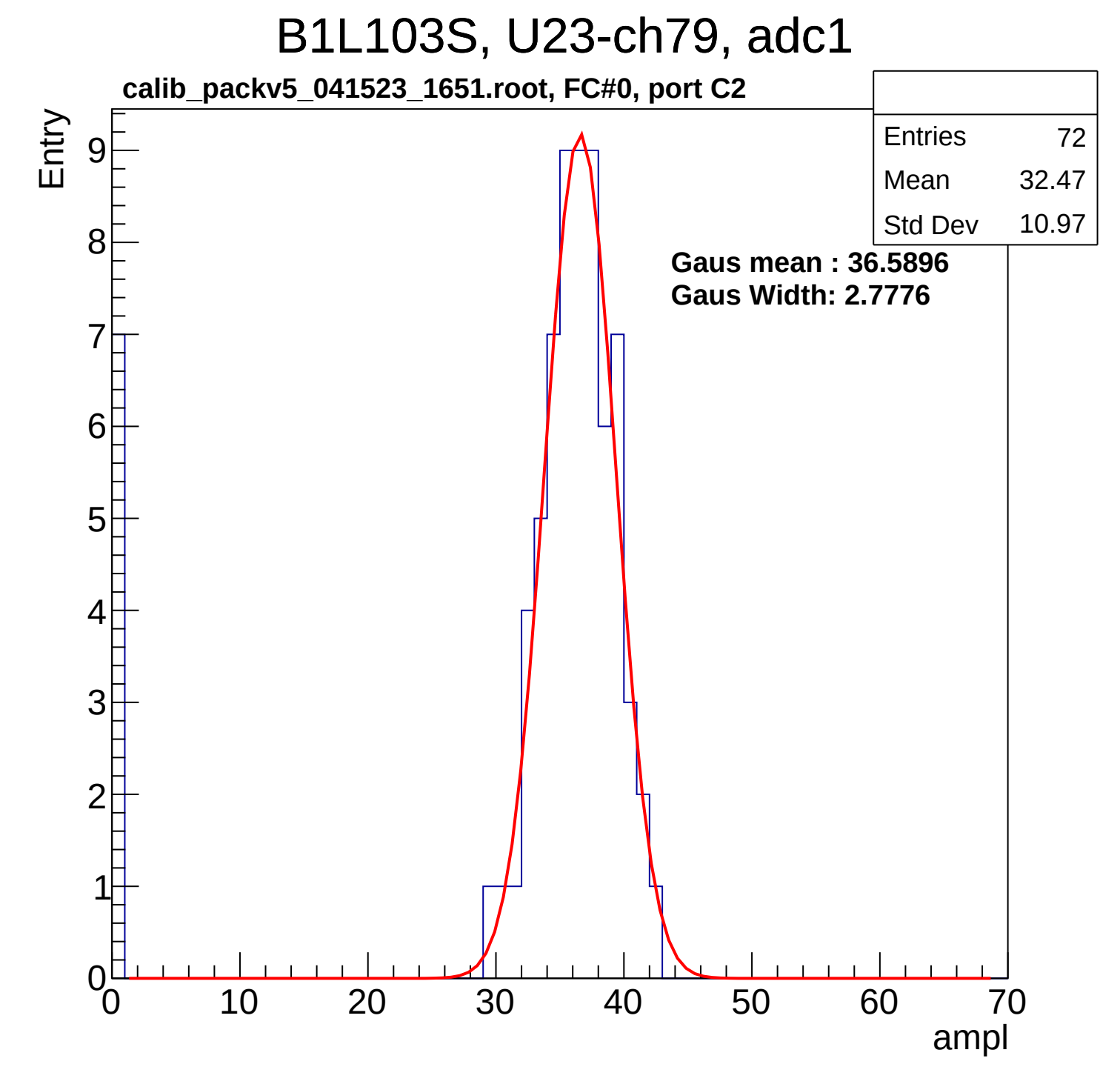

B1L103S, U23-ch78, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 53.67
Std Dev 3.671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

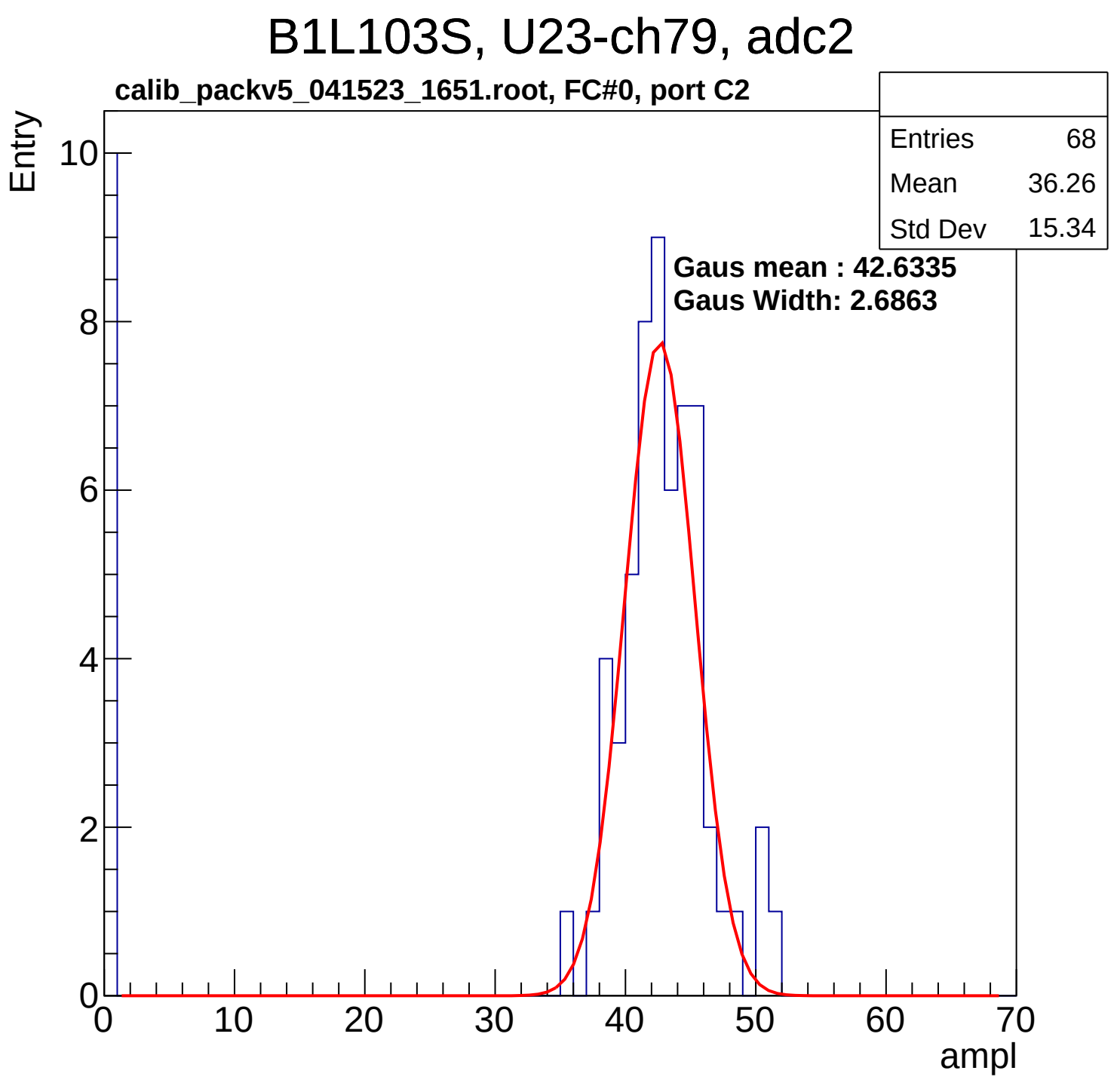

B1L103S, U23-ch78, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 47
Mean 58.77
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

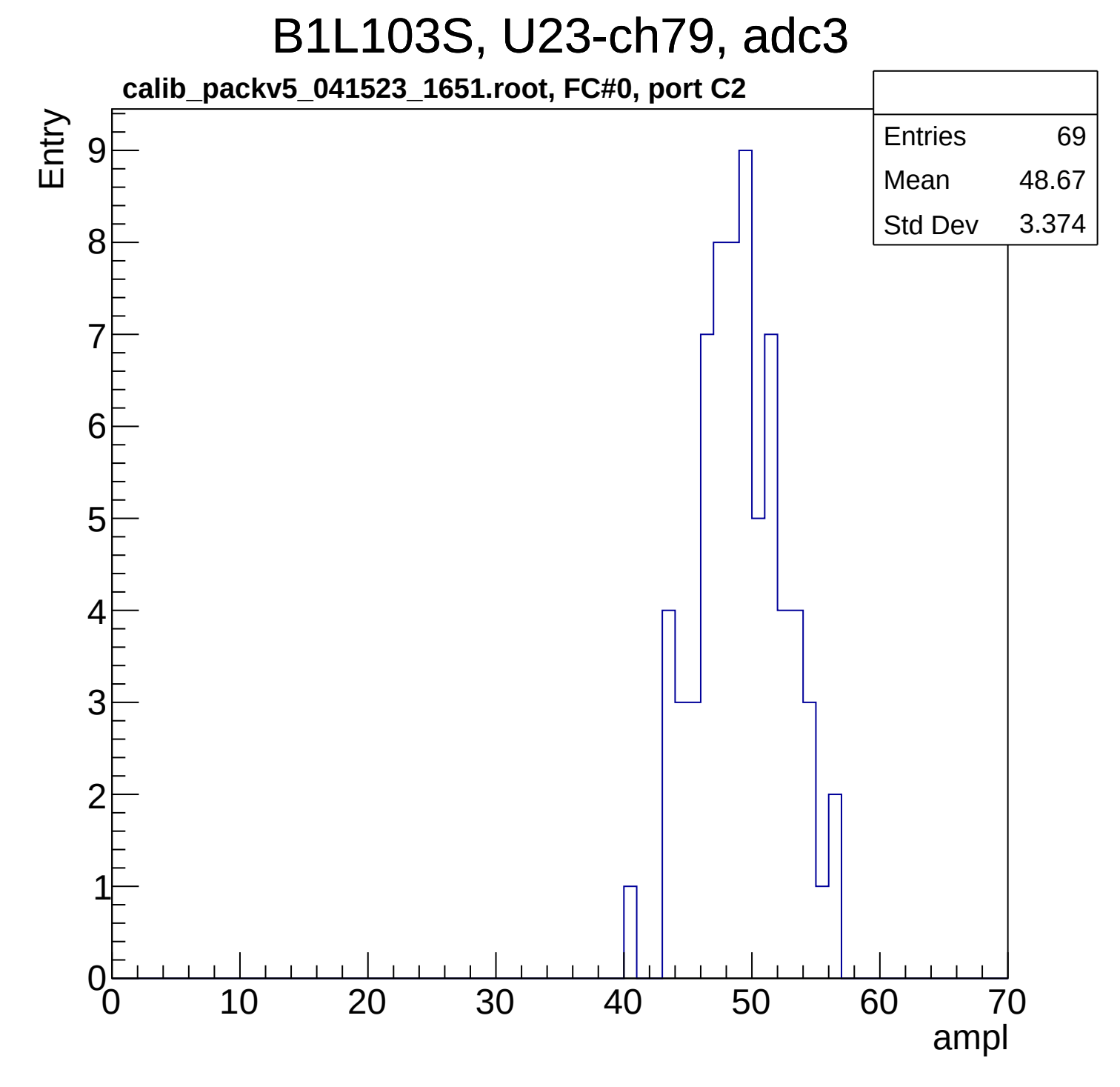

B1L103S, U23-ch78, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 16
Mean 56.62
Std Dev 14.87
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

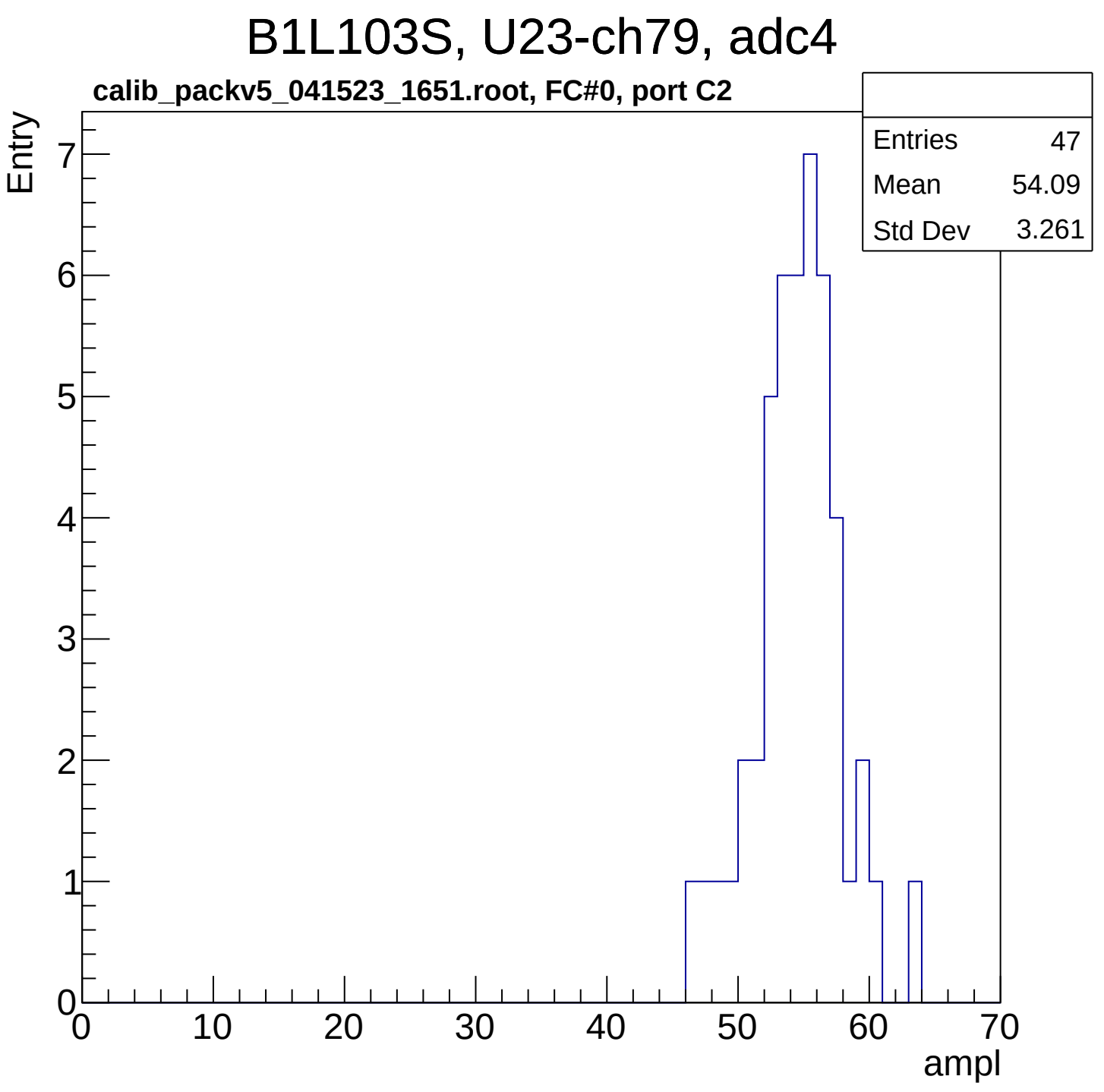

B1L103S, U23-ch78, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 23
Mean 10.7
Std Dev 23.31
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

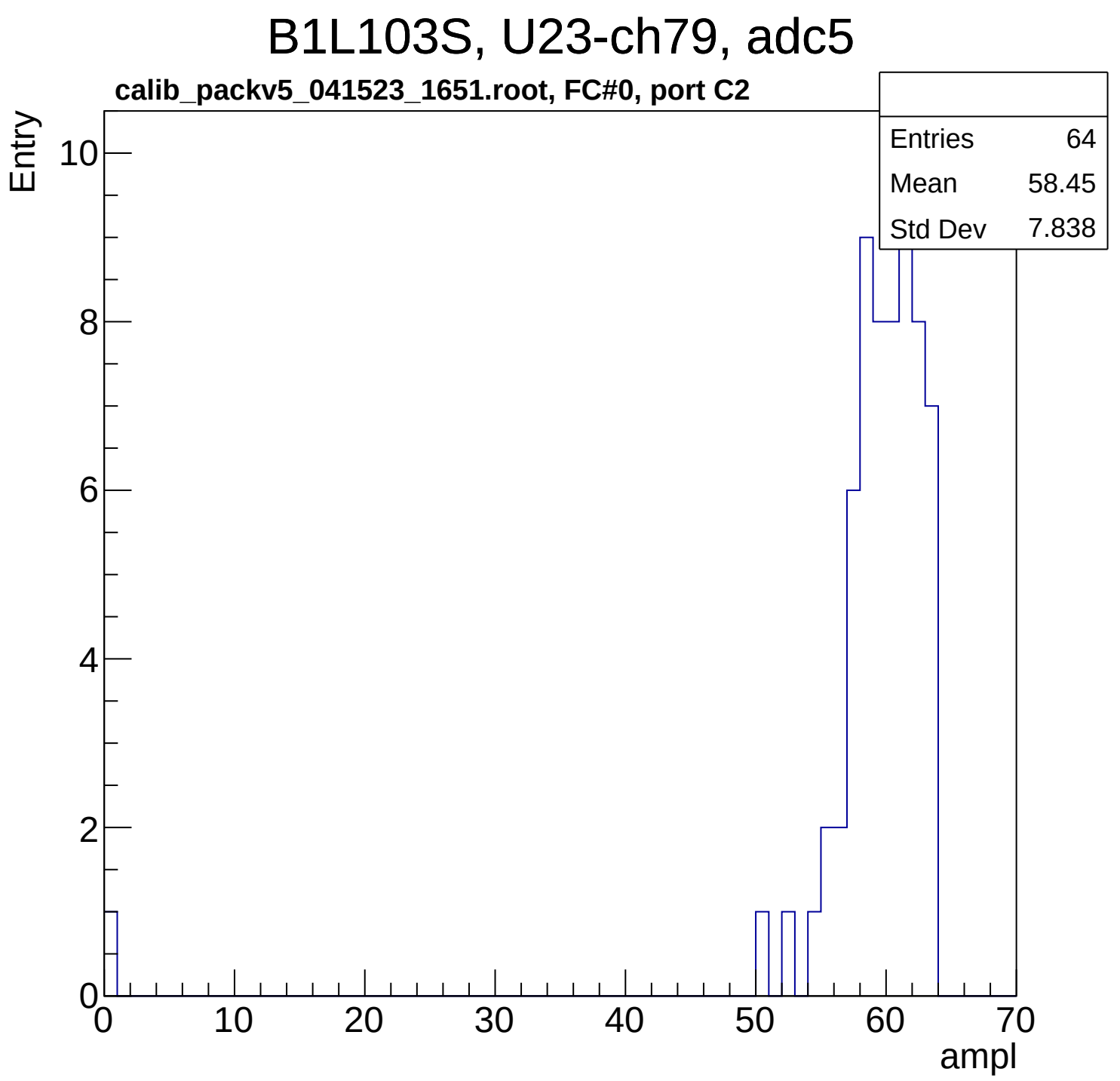

B1L103S, U23-ch79, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 22.58
Std Dev 12.71
Gaus mean : 29.7687
Gaus Width: 3.3311
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

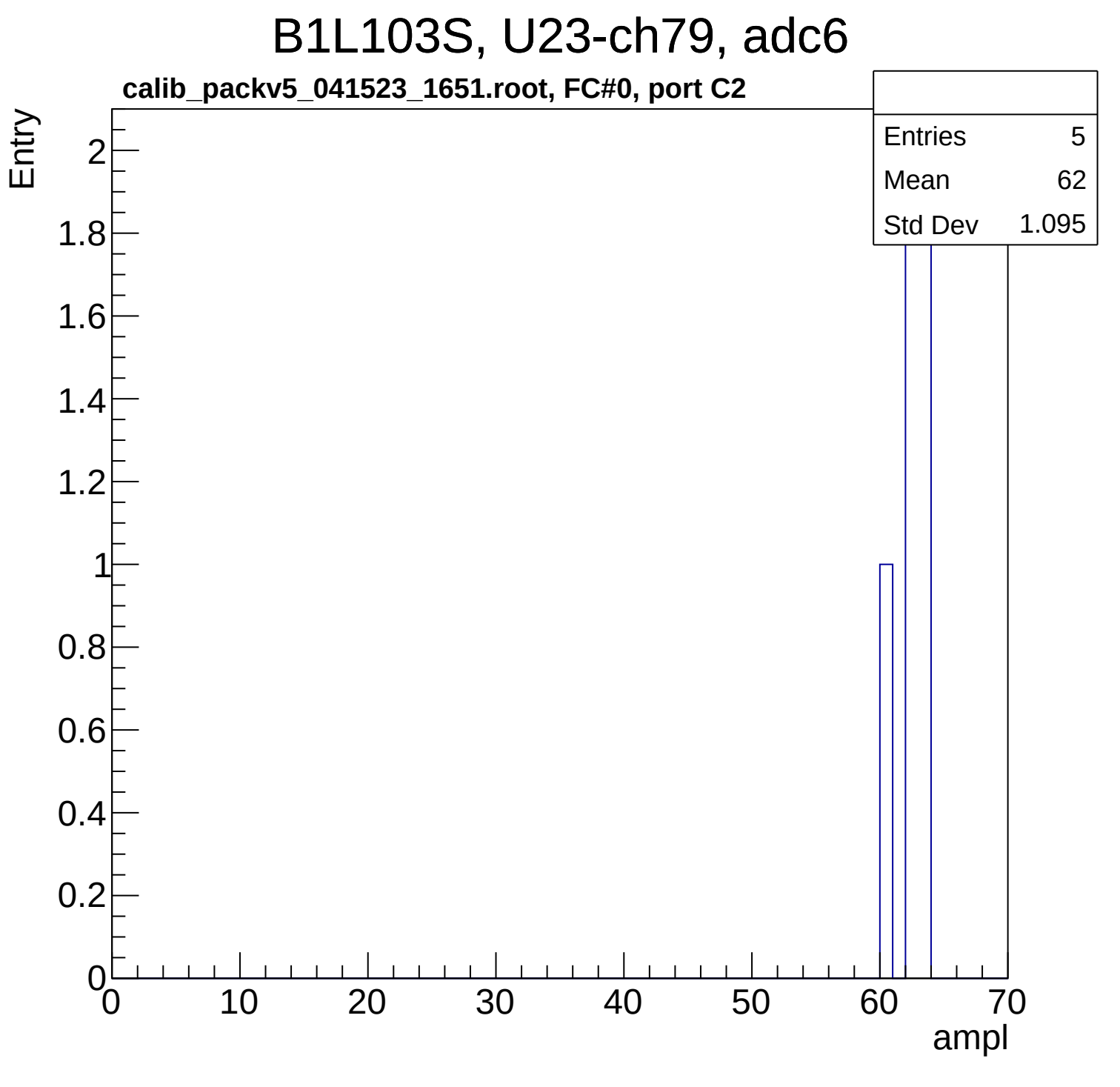

B1L103S, U23-ch79, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 32.47
Std Dev 10.97
Gaus mean : 36.5896
Gaus Width: 2.7776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

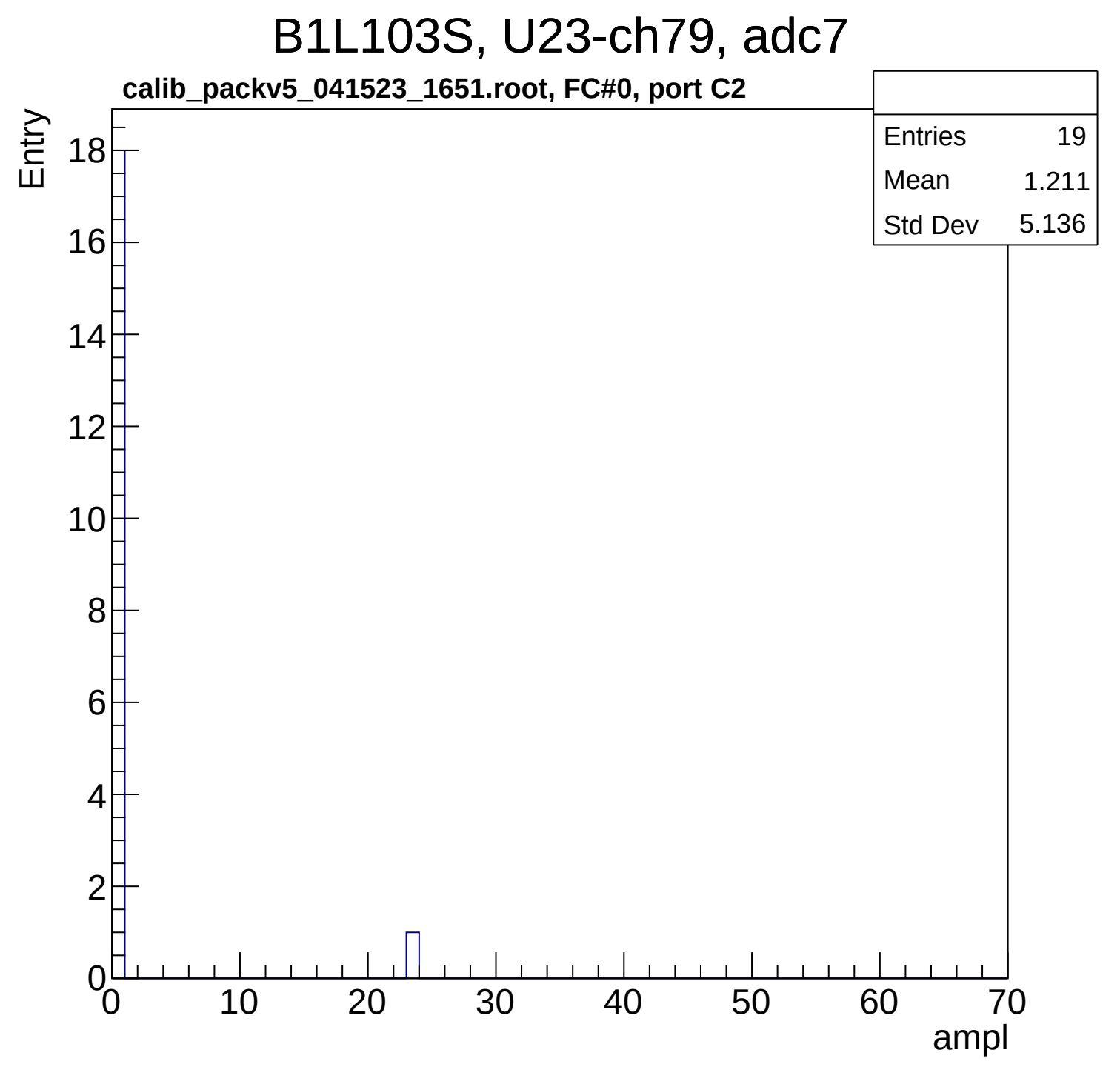

B1L103S, U23-ch79, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 36.26
Std Dev 15.34
Gaus mean : 42.6335
Gaus Width: 2.6863
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

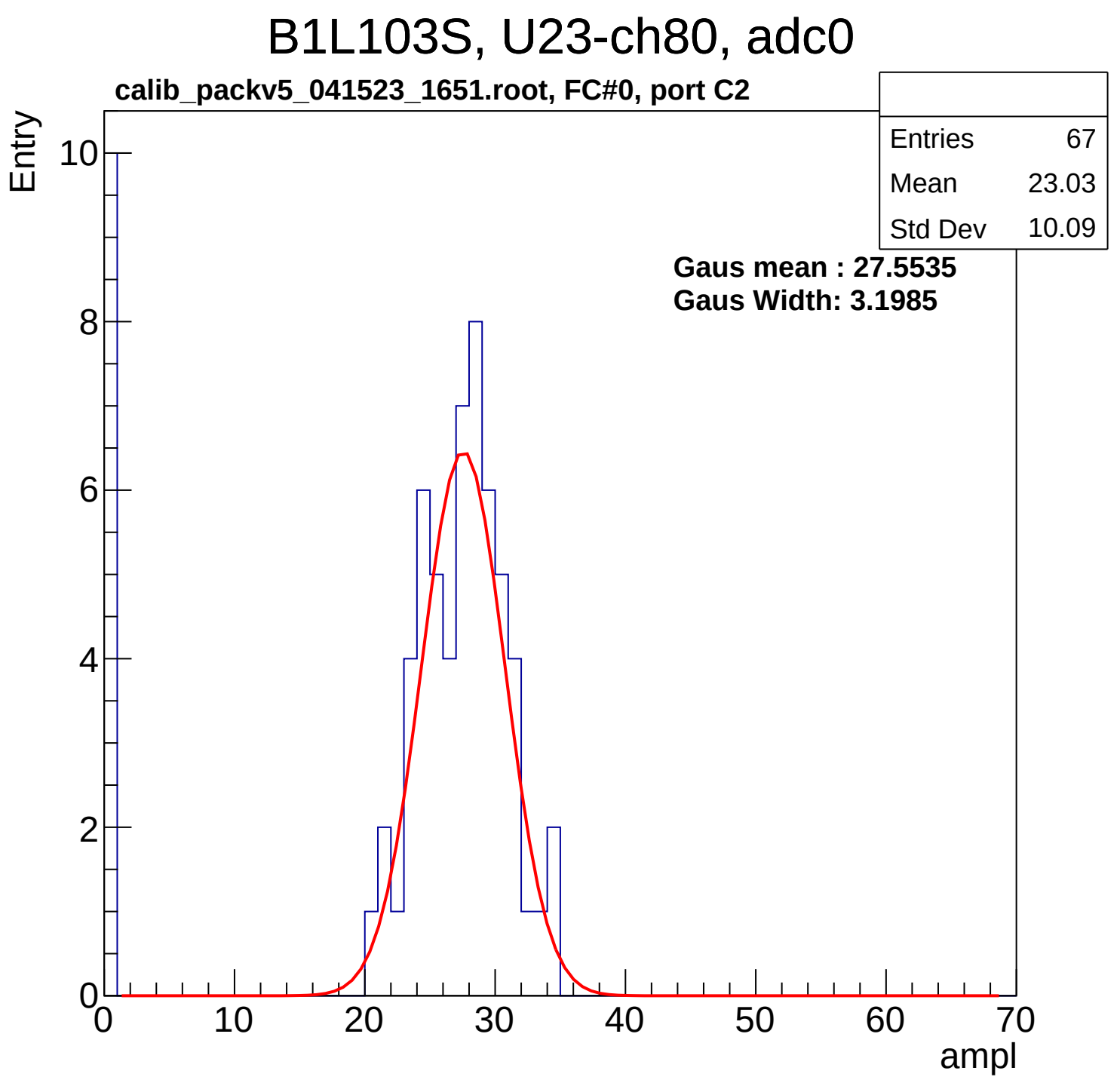

B1L103S, U23-ch79, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 48.67
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

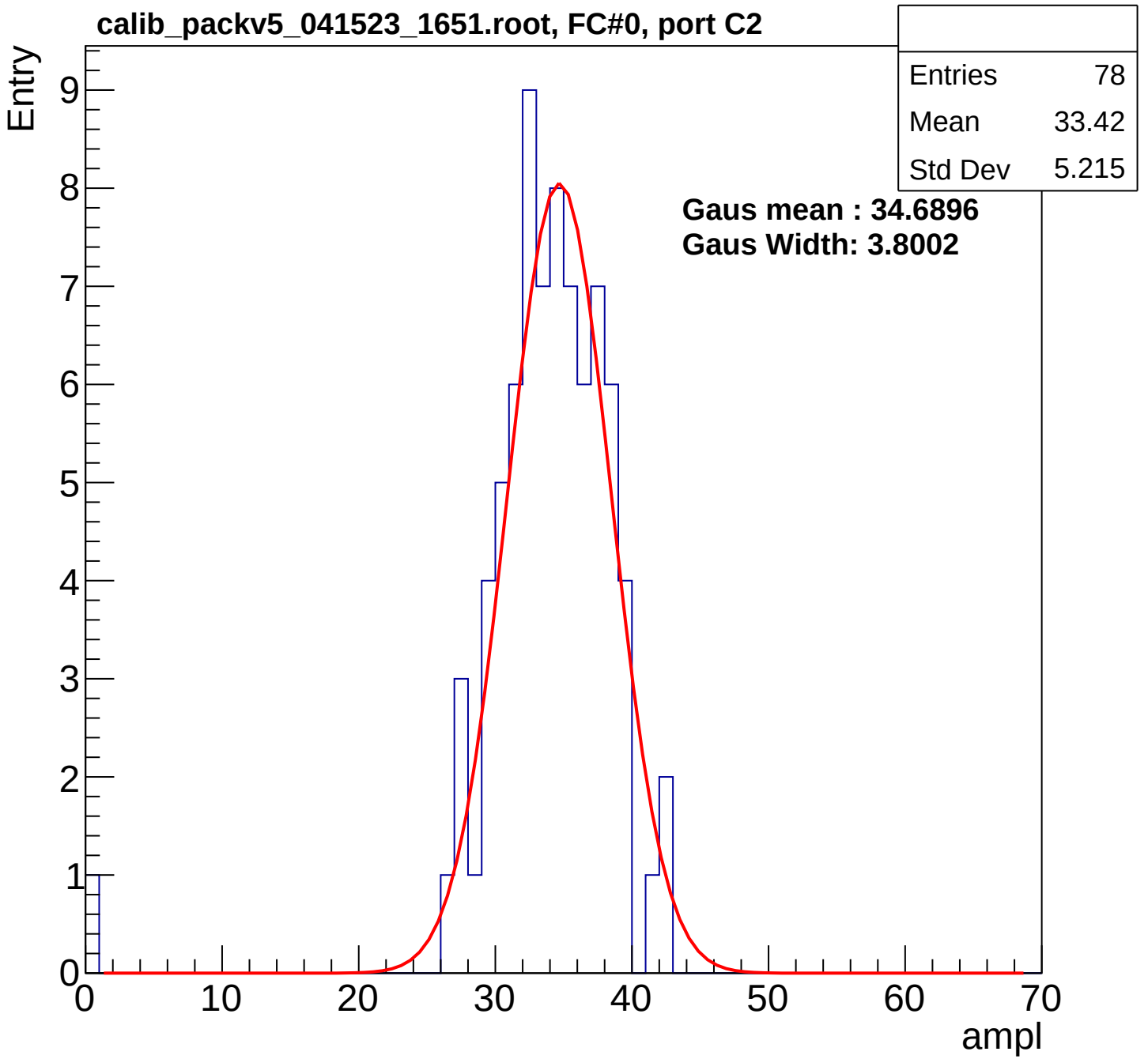

B1L103S, U23-ch79, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 47
Mean 54.09
Std Dev 3.261
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

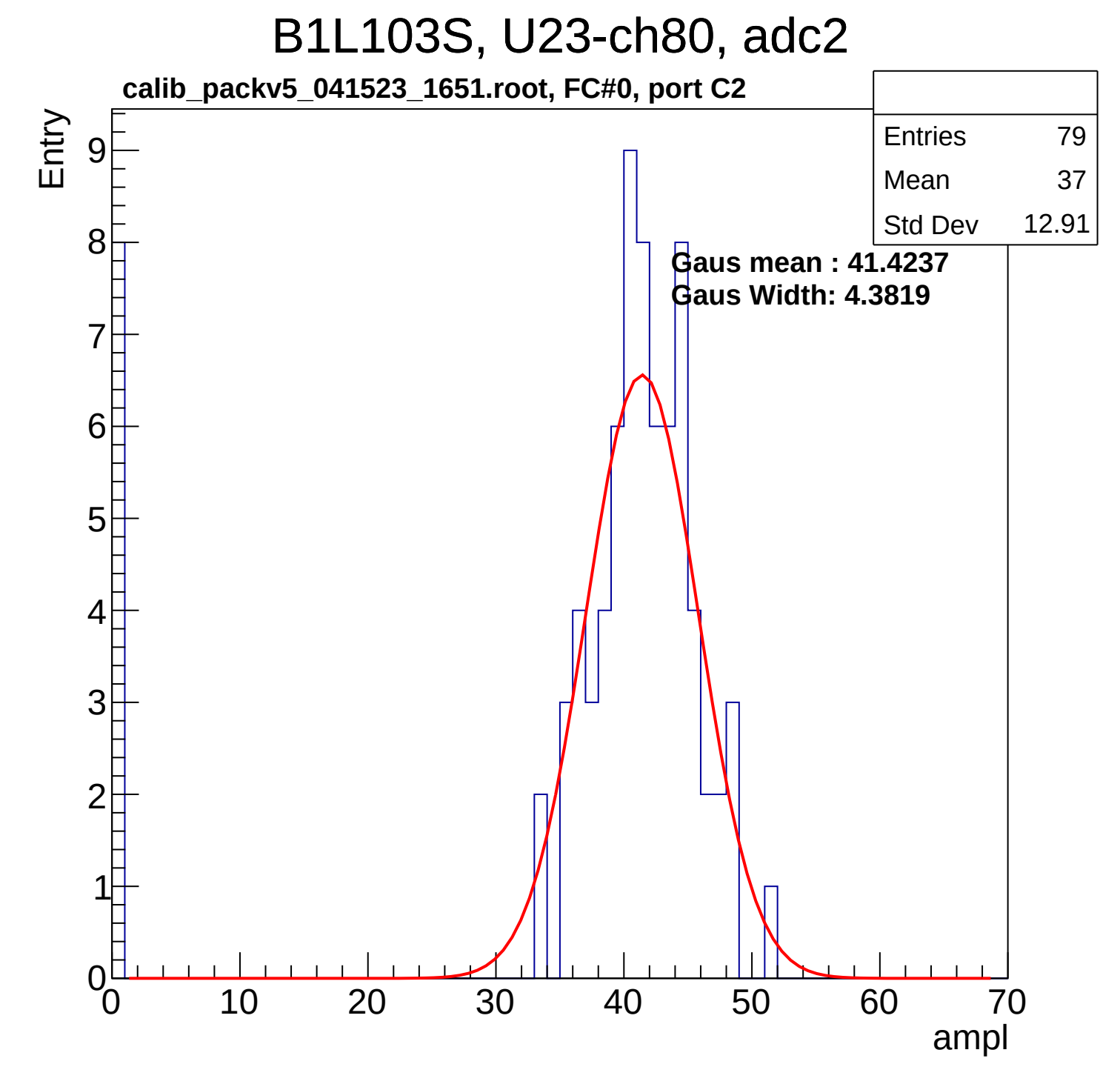

B1L103S, U23-ch79, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 58.45
Std Dev 7.838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

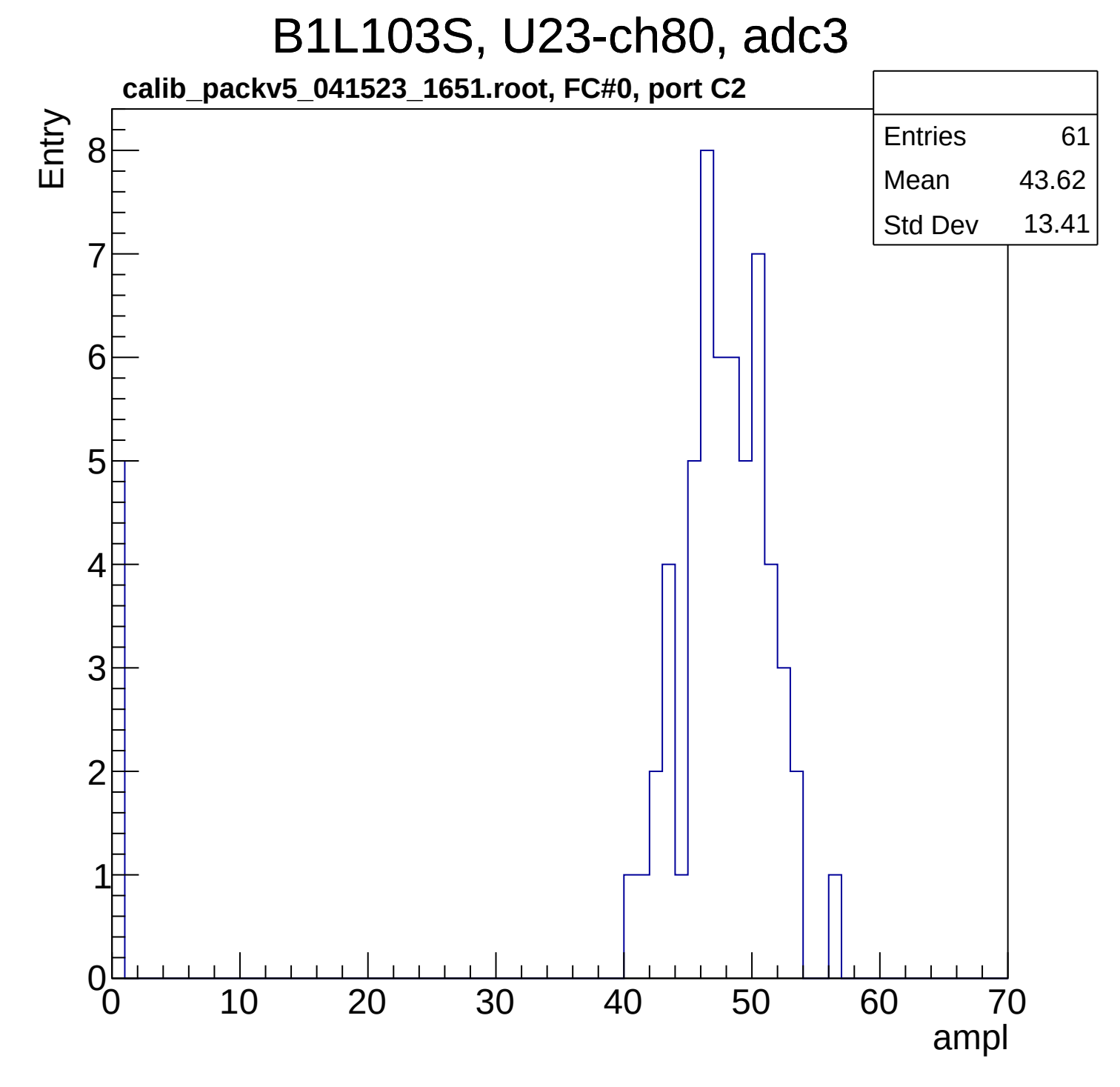

B1L103S, U23-ch79, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

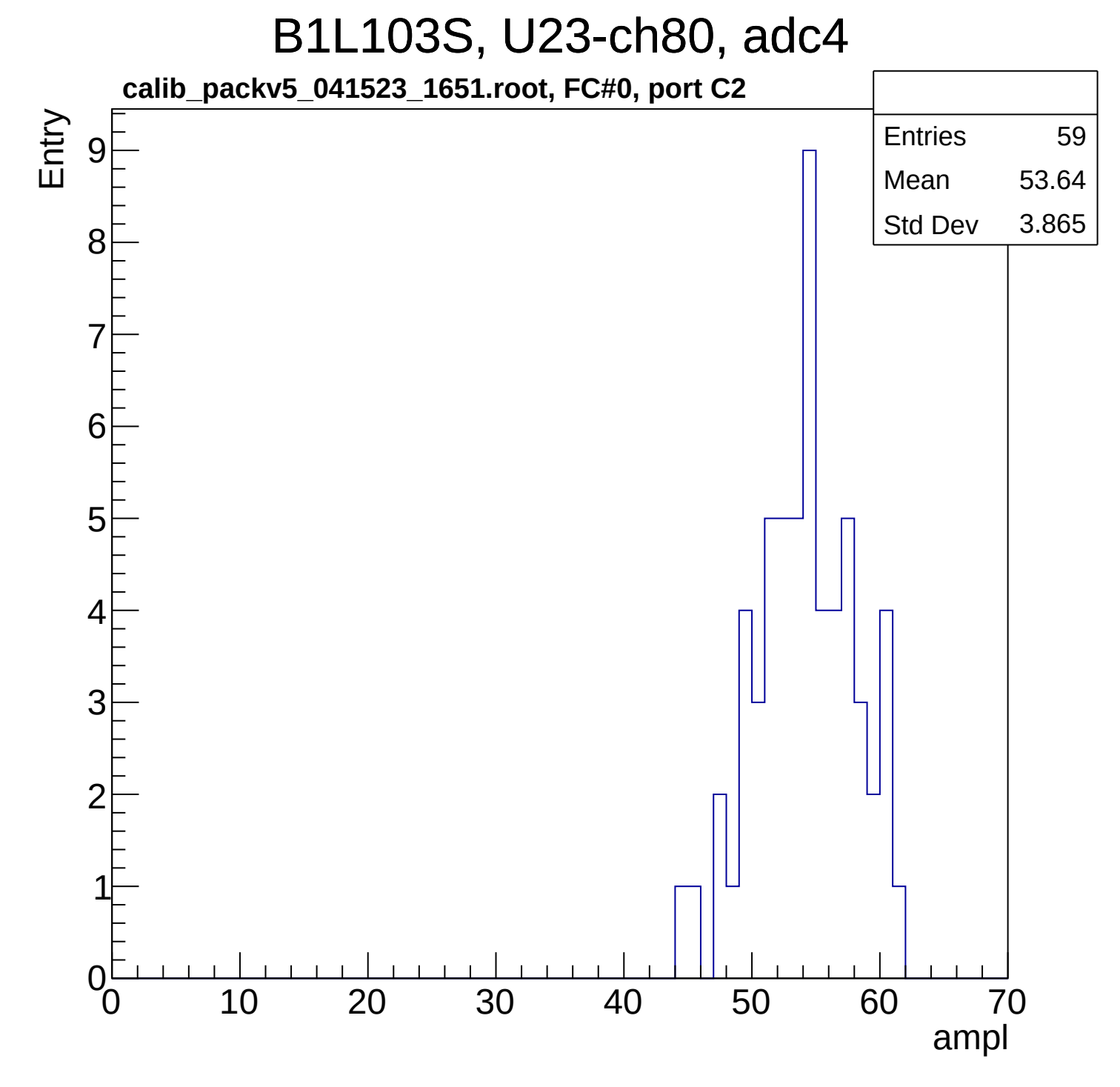

B1L103S, U23-ch79, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 1.211
Std Dev 5.136
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

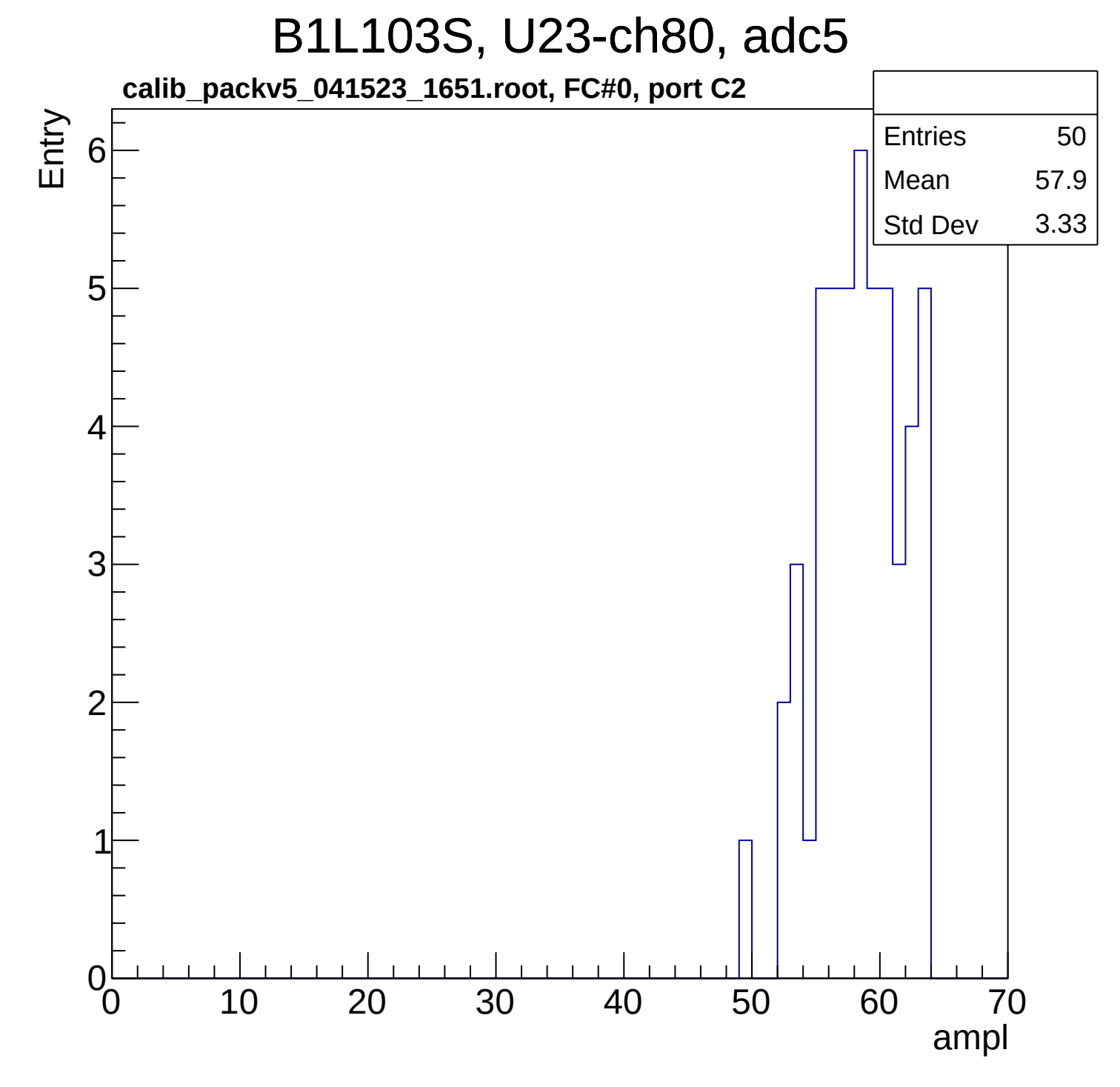

B1L103S, U23-ch80, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 23.03
Std Dev 10.09
Gaus mean : 27.5535
Gaus Width: 3.1985
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

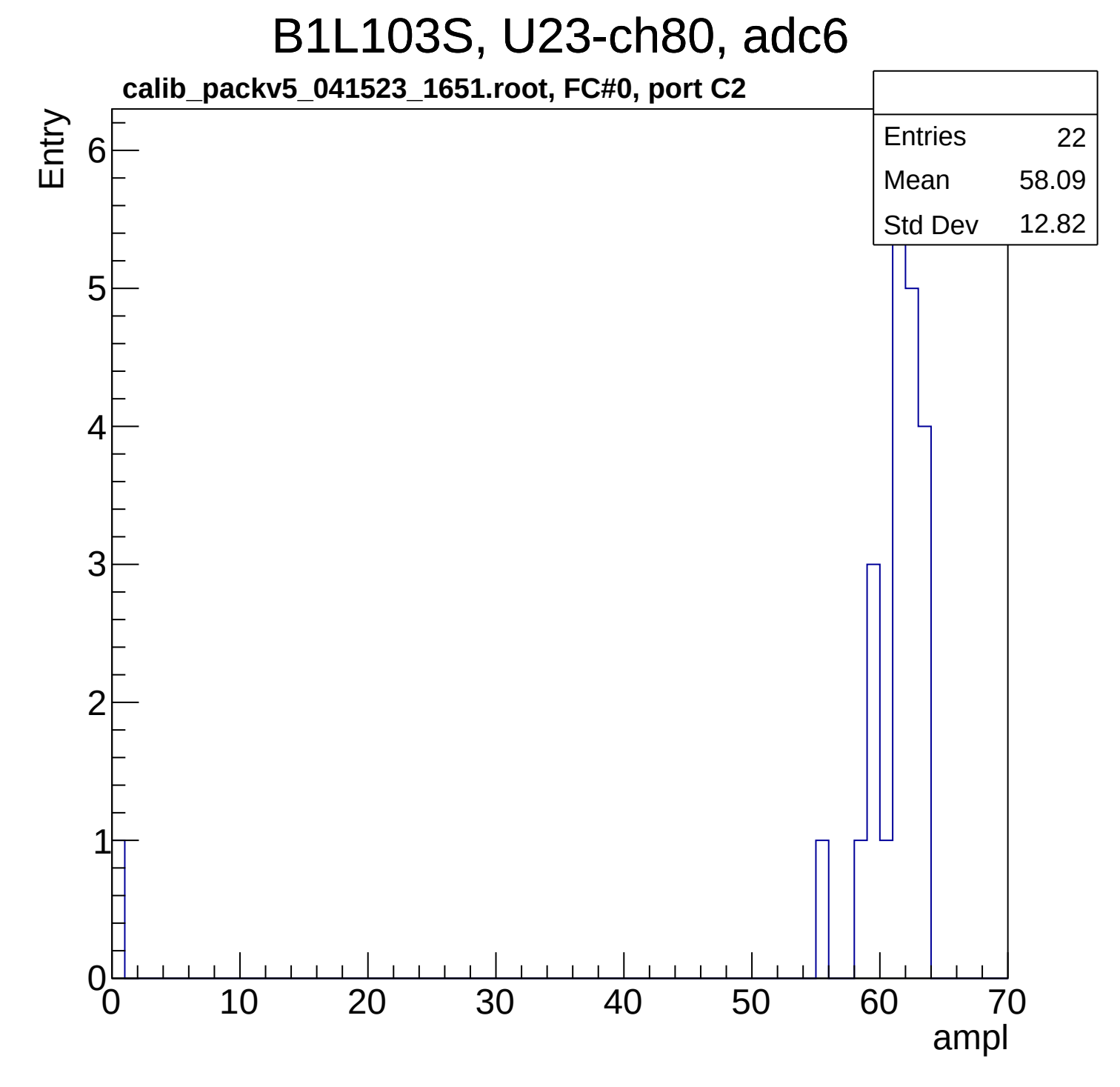

B1L103S, U23-ch80, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 33.42
Std Dev 5.215
Gaus mean : 34.6896
Gaus Width: 3.8002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

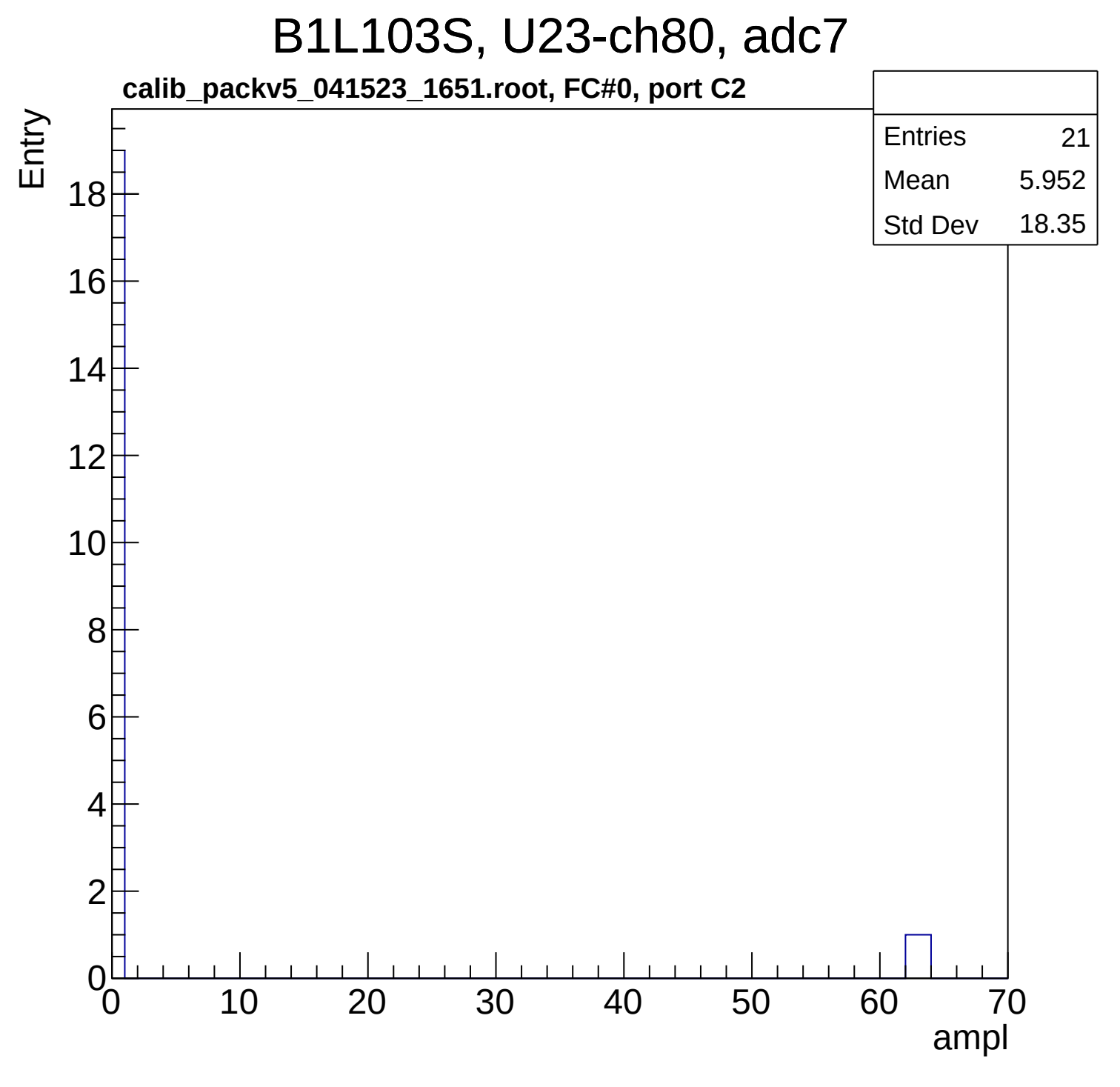

B1L103S, U23-ch80, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 37
Std Dev 12.91
Gaus mean : 41.4237
Gaus Width: 4.3819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

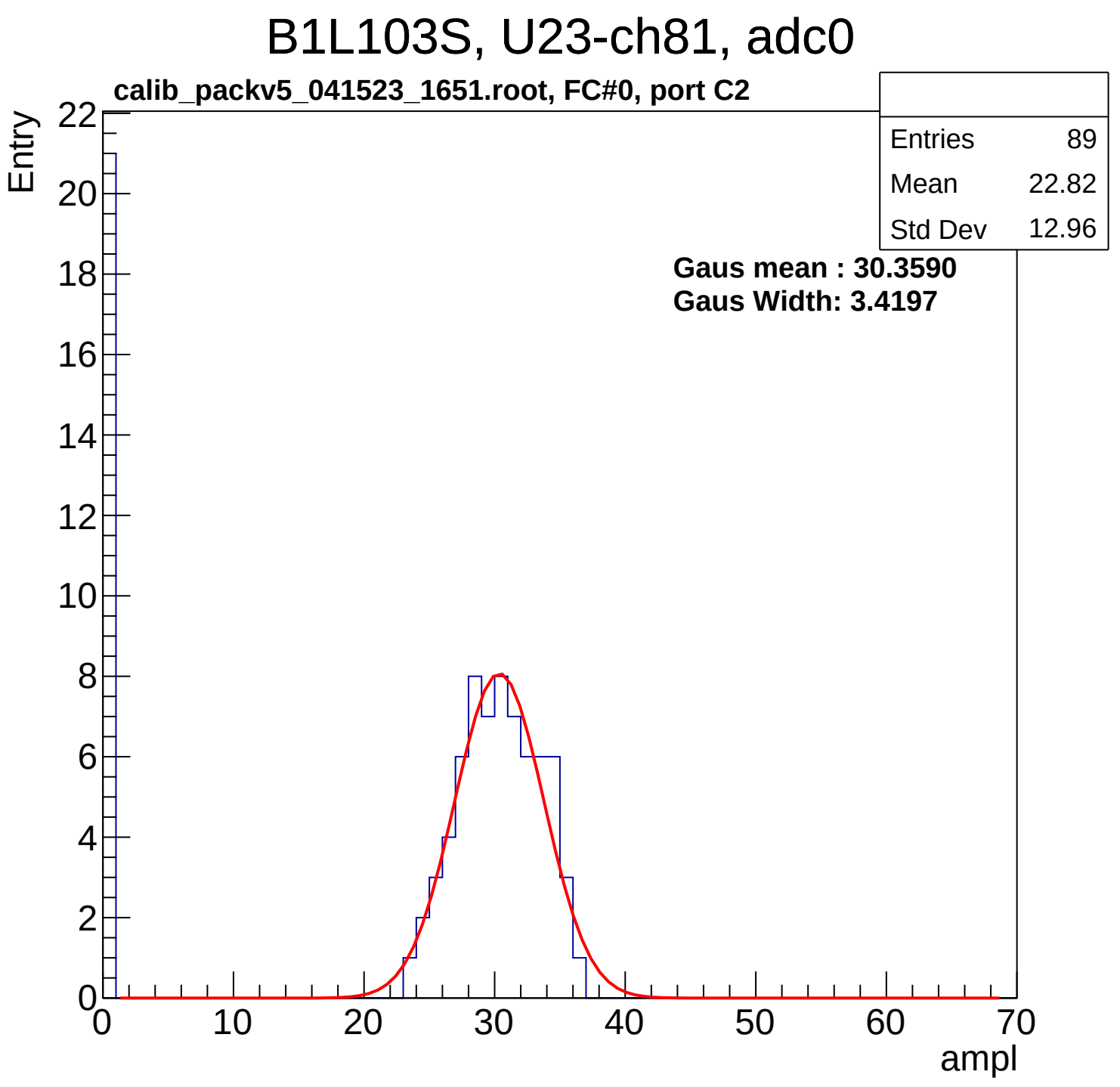

B1L103S, U23-ch80, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 43.62
Std Dev 13.41
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

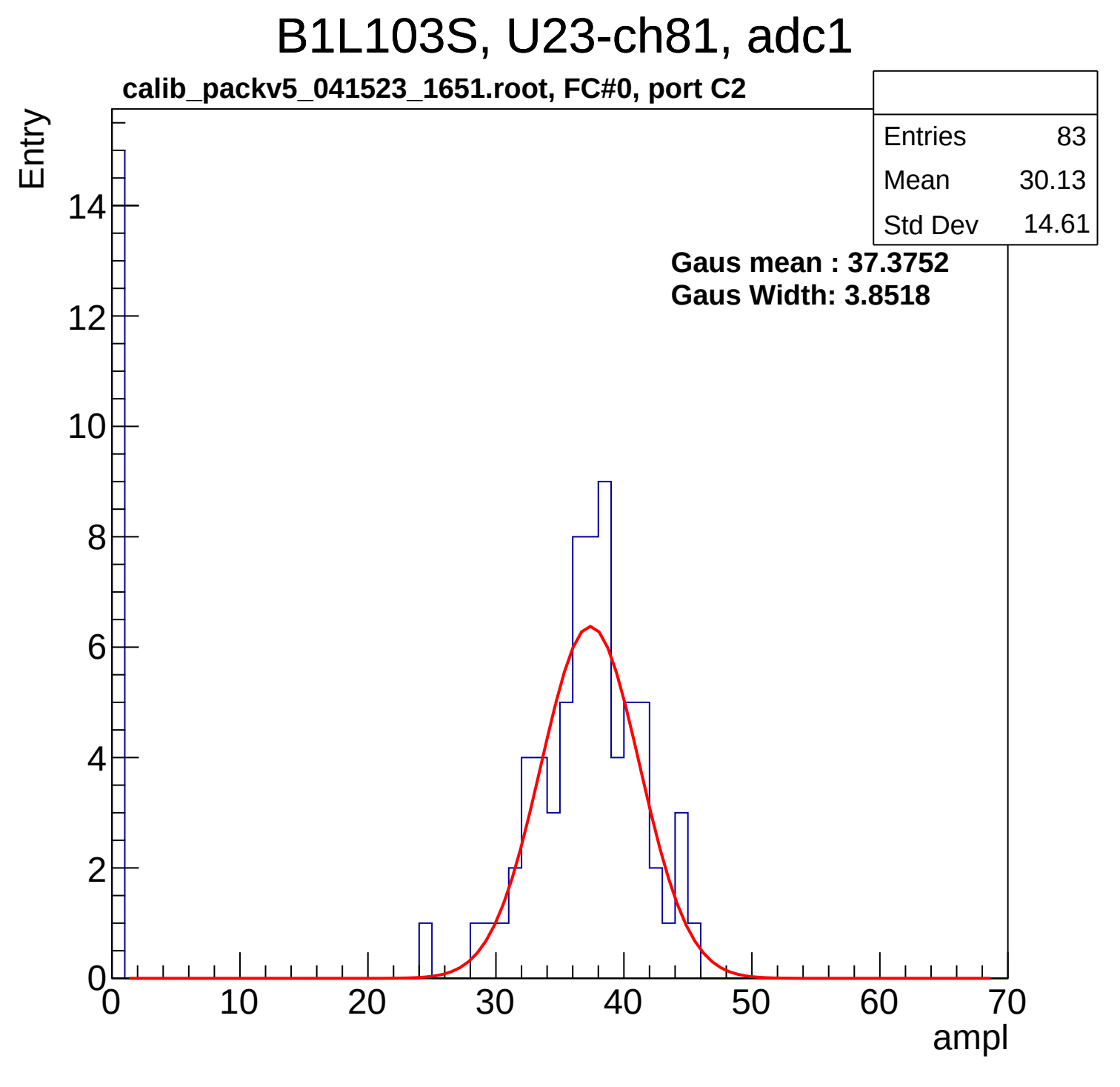

B1L103S, U23-ch80, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 53.64
Std Dev 3.865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

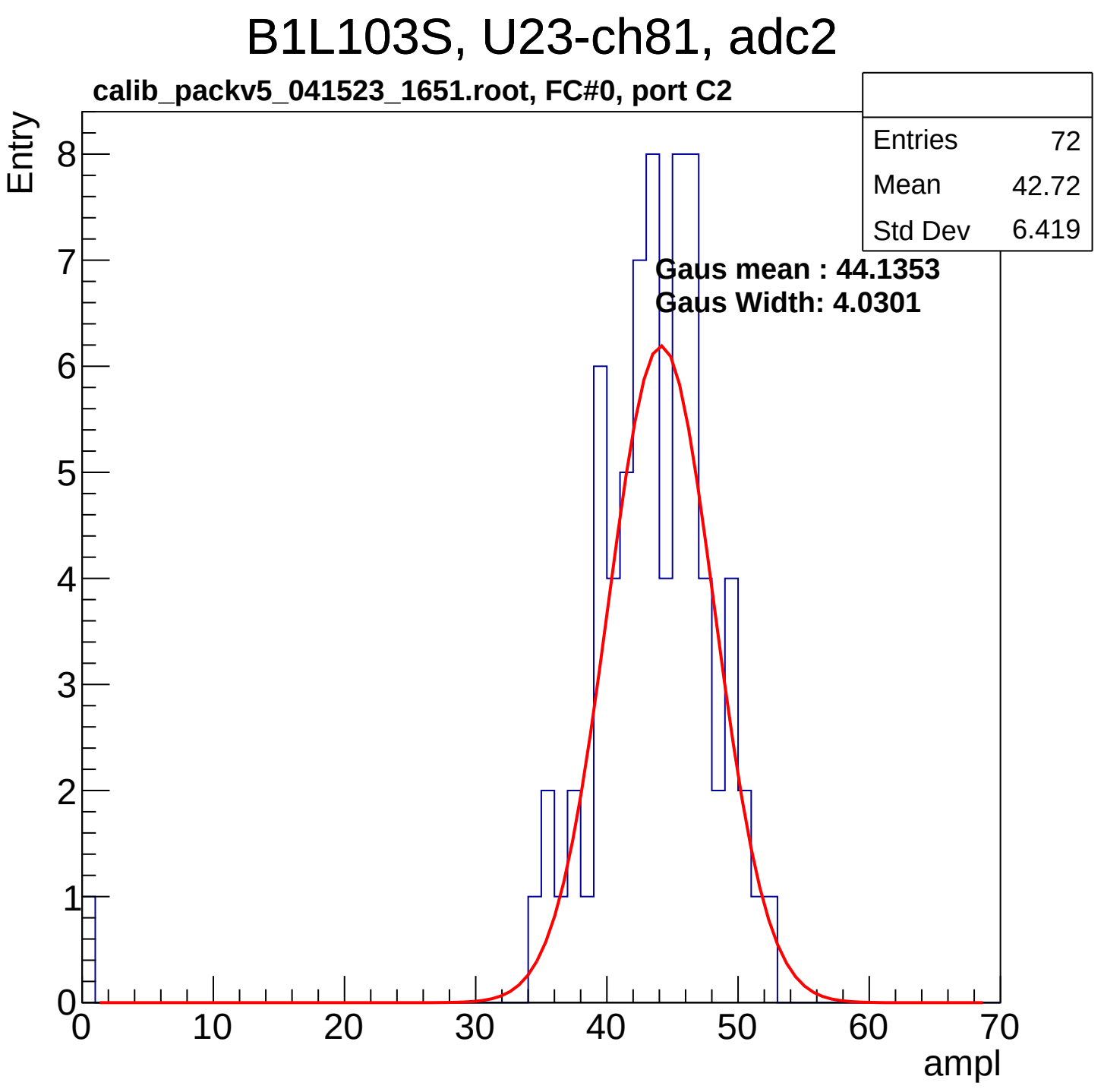

B1L103S, U23-ch80, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 50
Mean 57.9
Std Dev 3.33
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

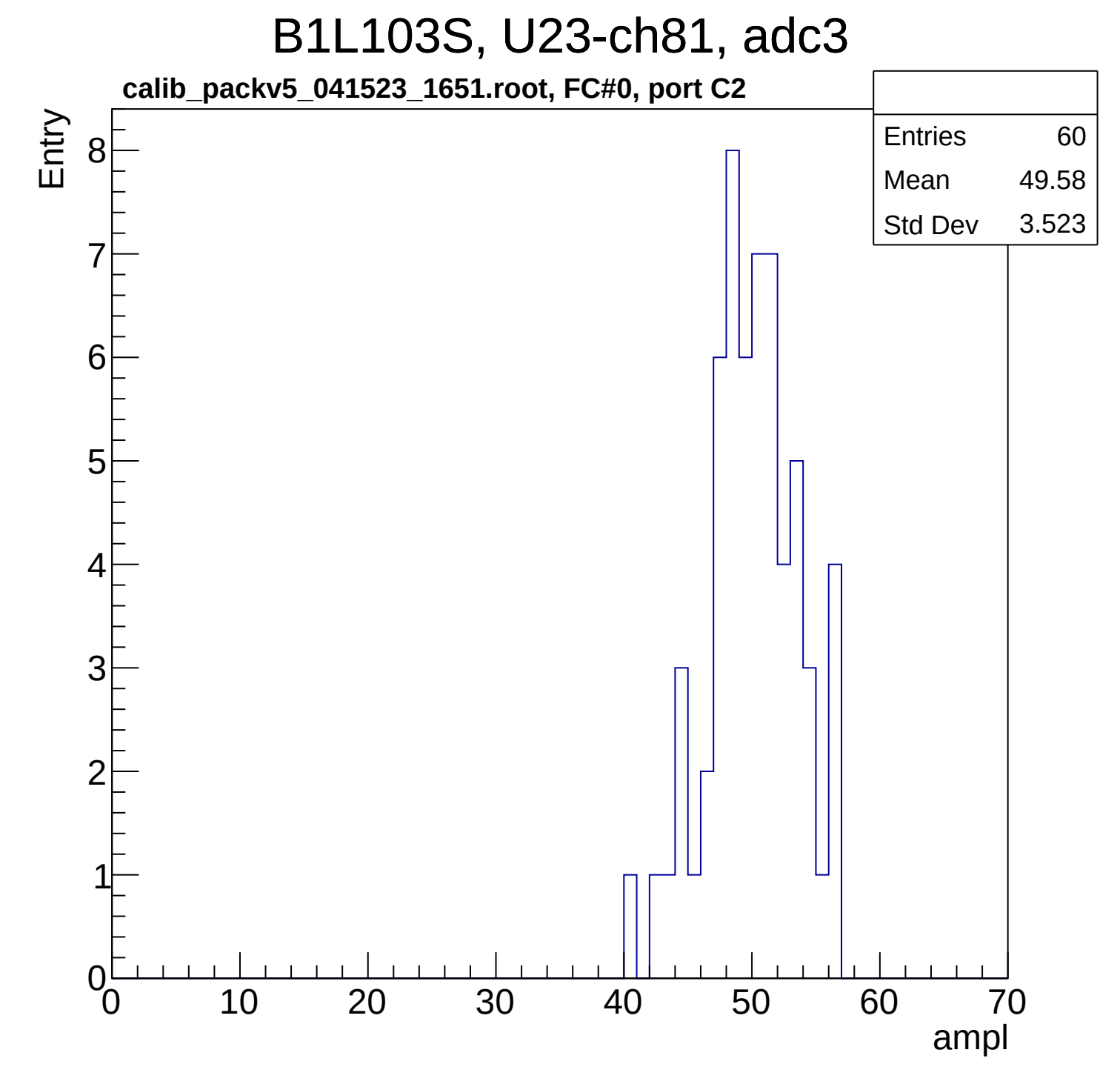

B1L103S, U23-ch80, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 22
Mean 58.09
Std Dev 12.82
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

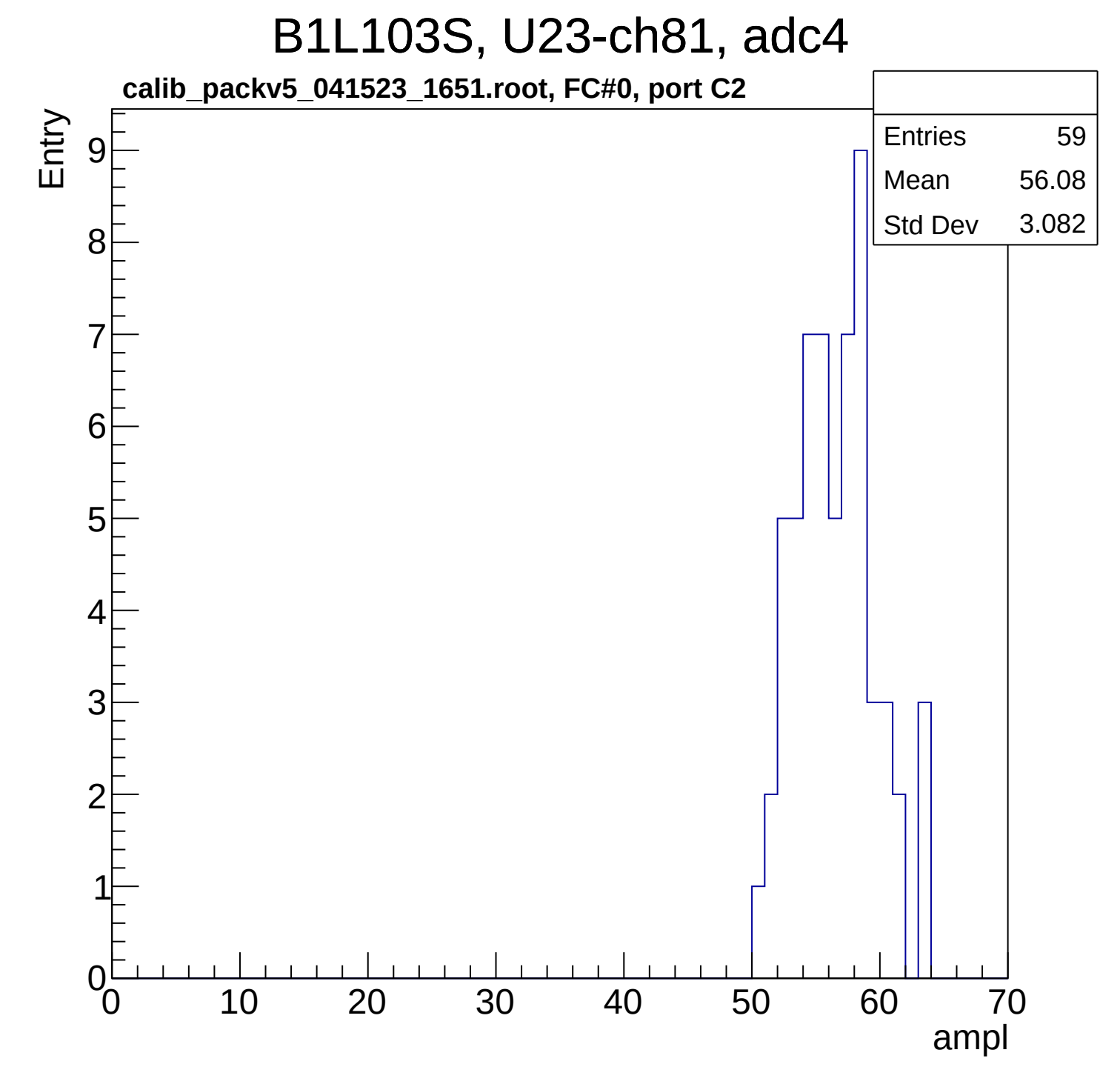

B1L103S, U23-ch80, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.952
Std Dev 18.35
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

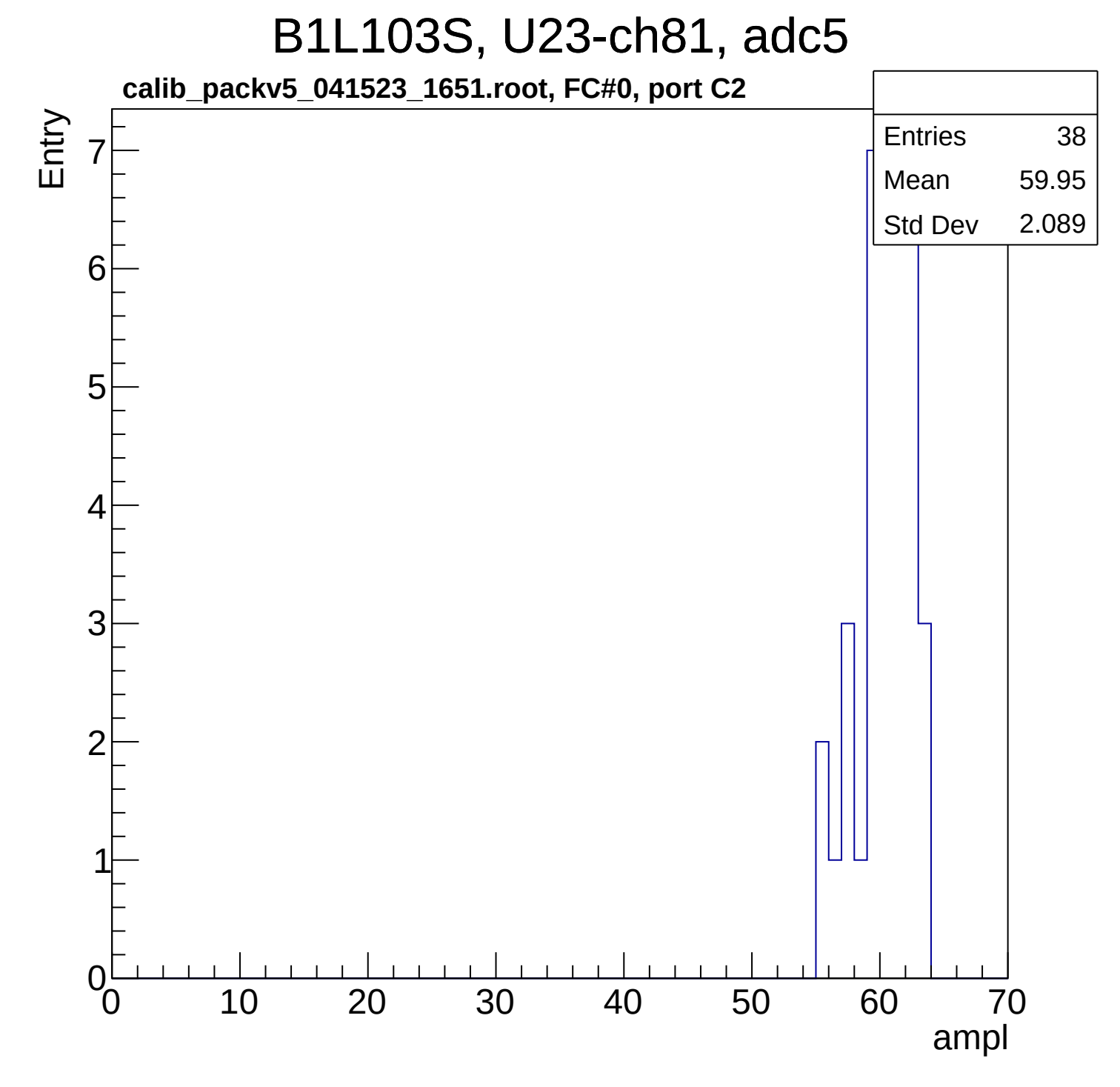

B1L103S, U23-ch81, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 22.82
Std Dev 12.96
Gaus mean : 30.3590
Gaus Width: 3.4197
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

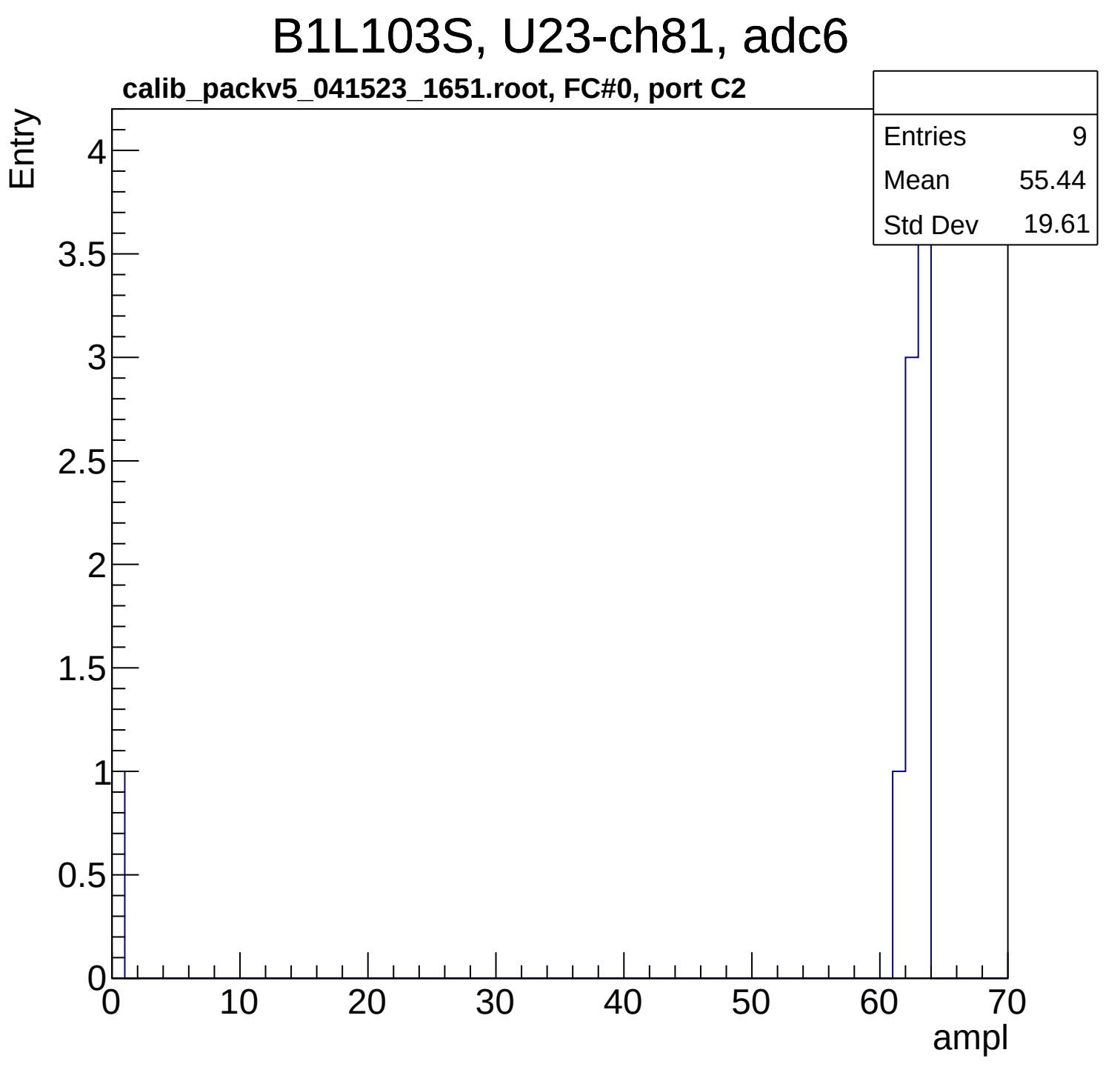

B1L103S, U23-ch81, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 30.13
Std Dev 14.61
Gaus mean : 37.3752
Gaus Width: 3.8518
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

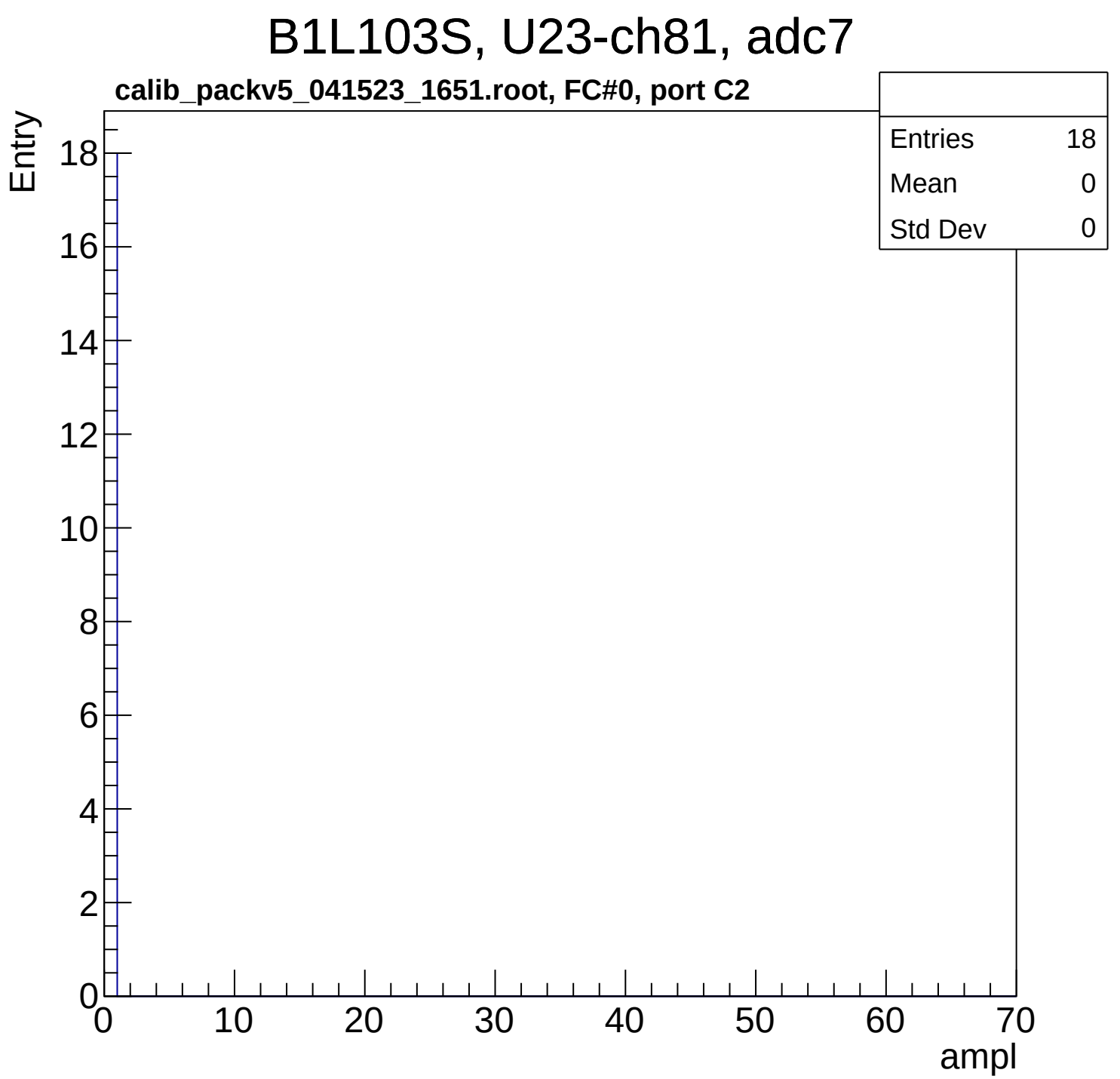

B1L103S, U23-ch81, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 42.72
Std Dev 6.419
Gaus mean : 44.1353
Gaus Width: 4.0301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

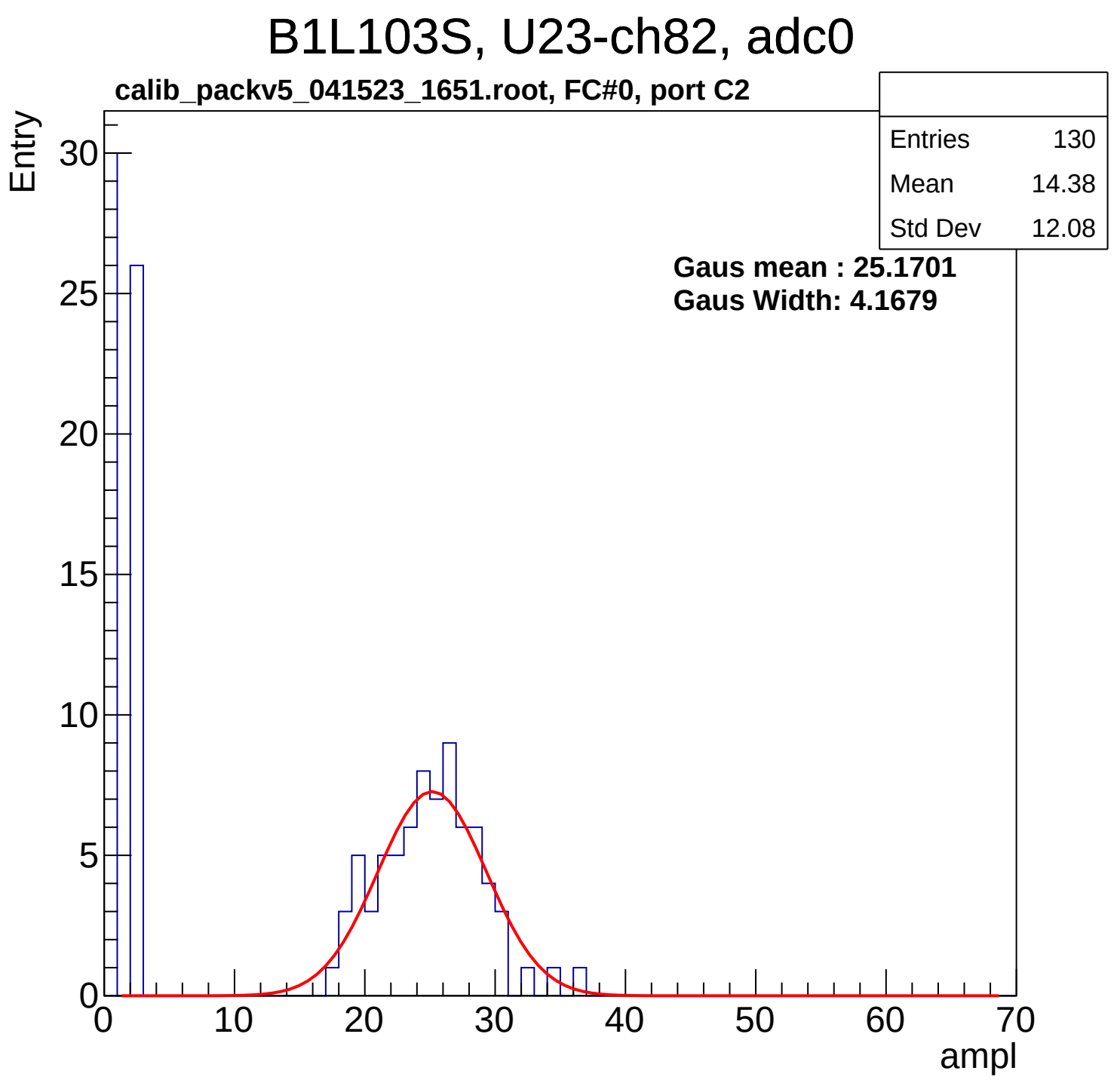

B1L103S, U23-ch81, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 49.58
Std Dev 3.523
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

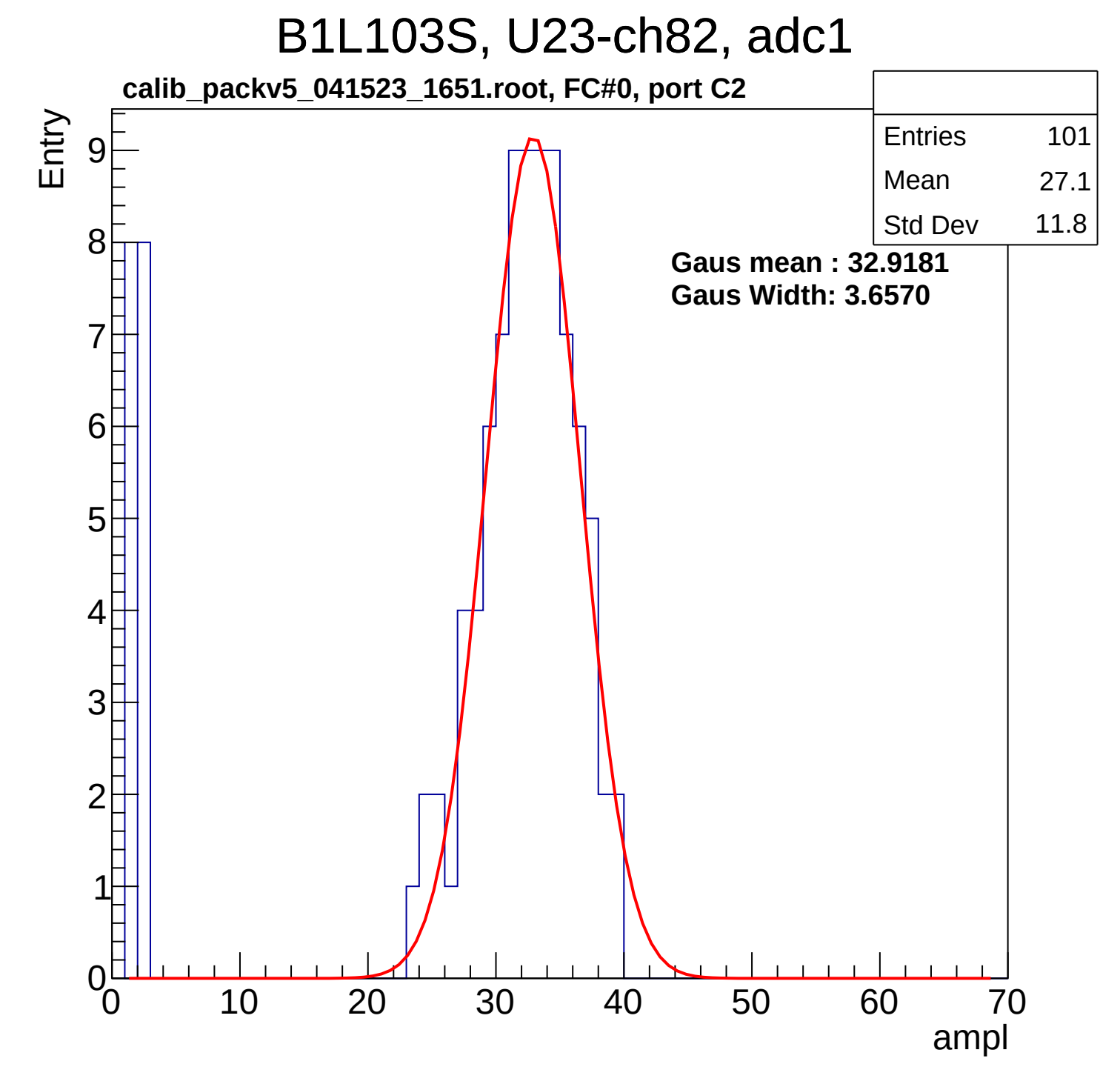

B1L103S, U23-ch81, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 56.08
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

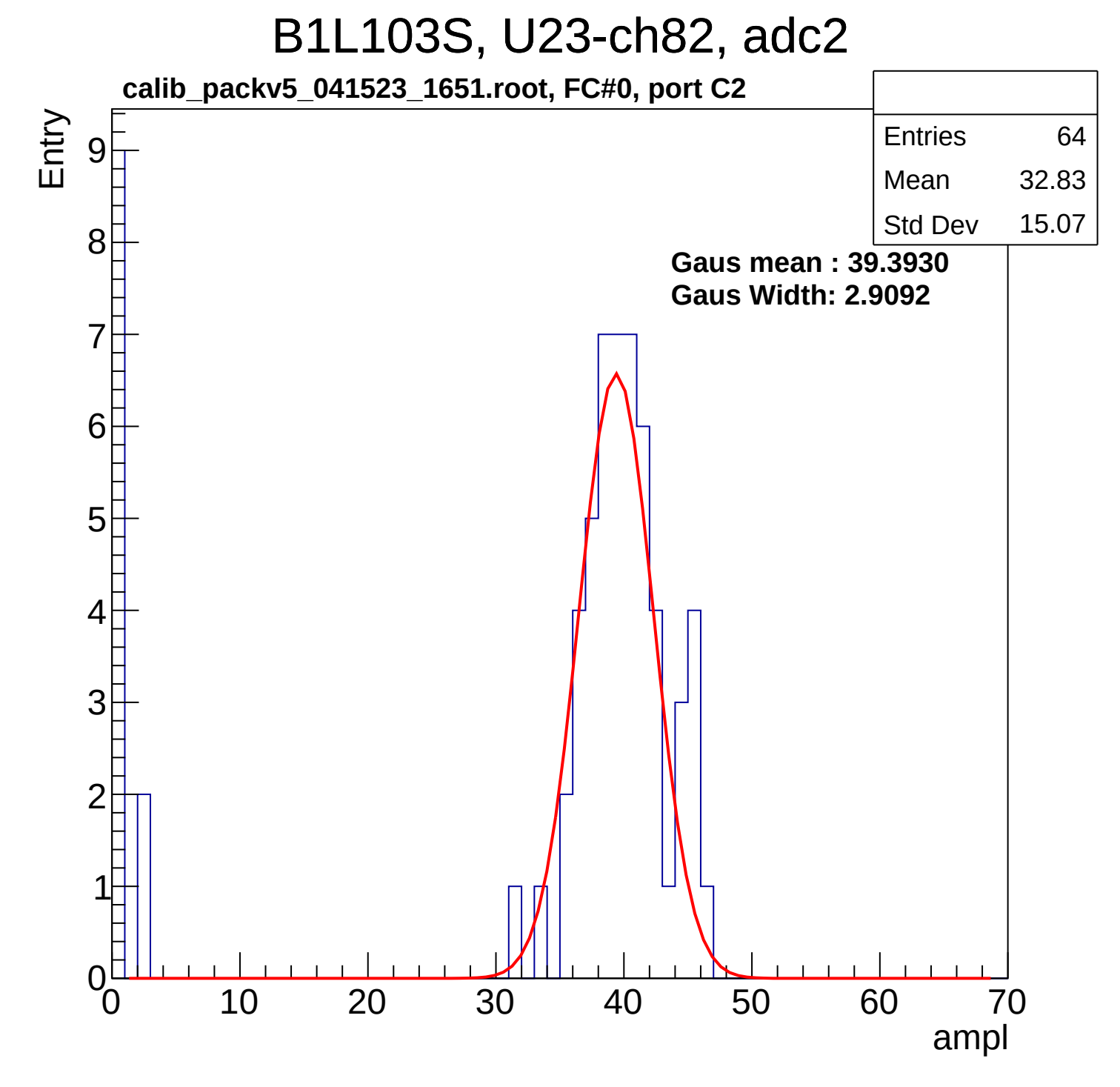

B1L103S, U23-ch81, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 38
Mean 59.95
Std Dev 2.089
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

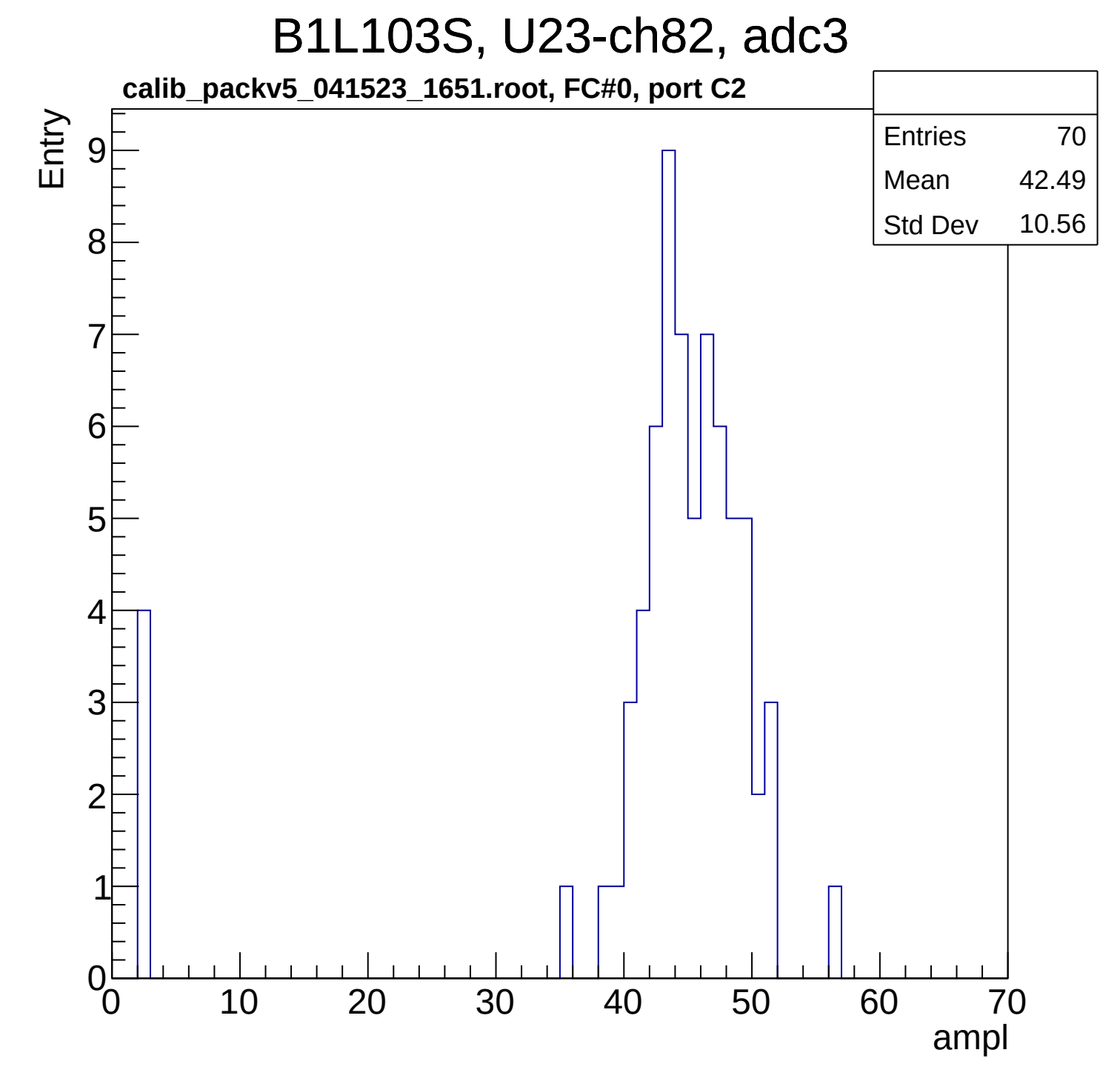

B1L103S, U23-ch81, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 55.44
Std Dev 19.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

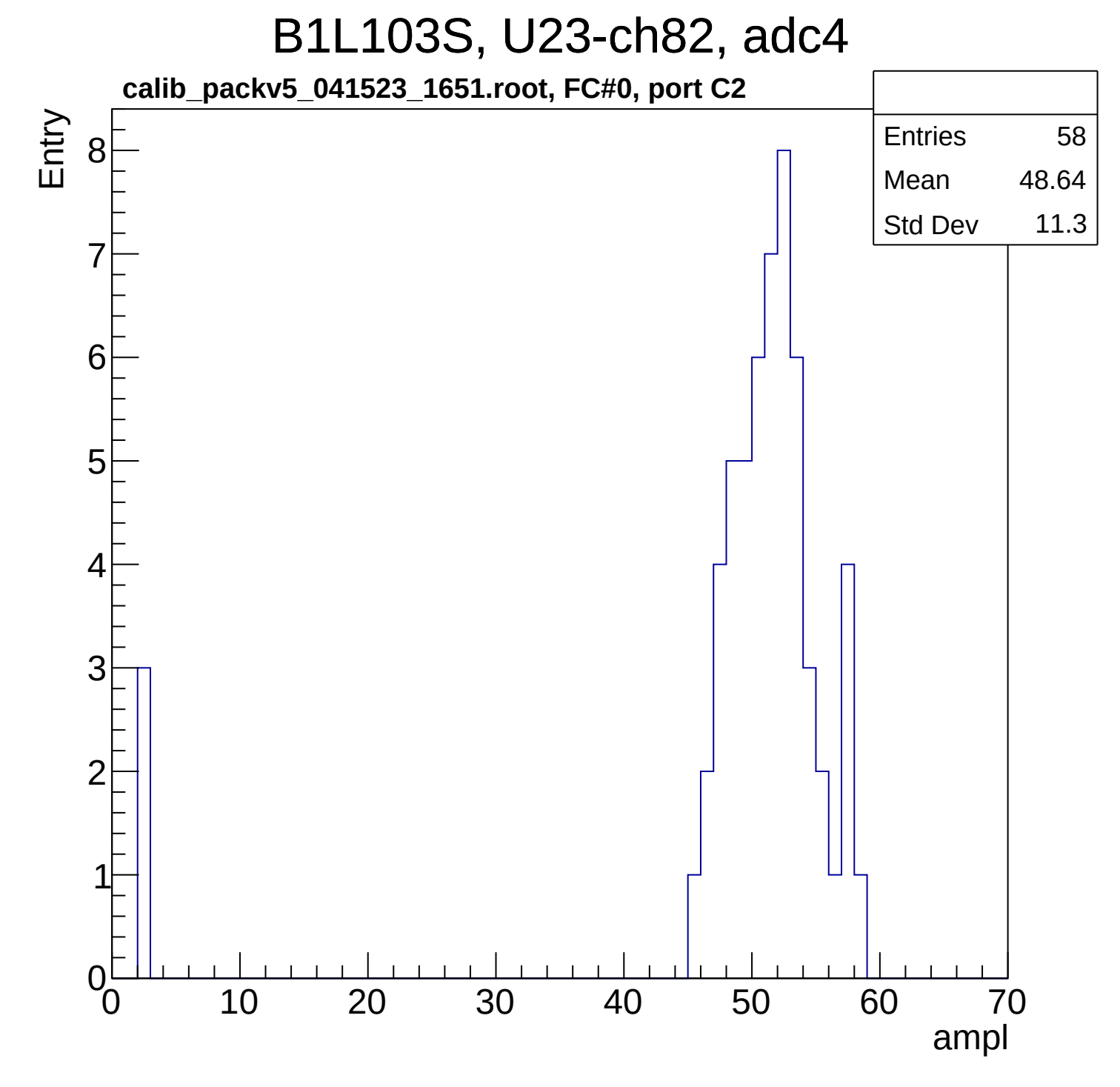

B1L103S, U23-ch81, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

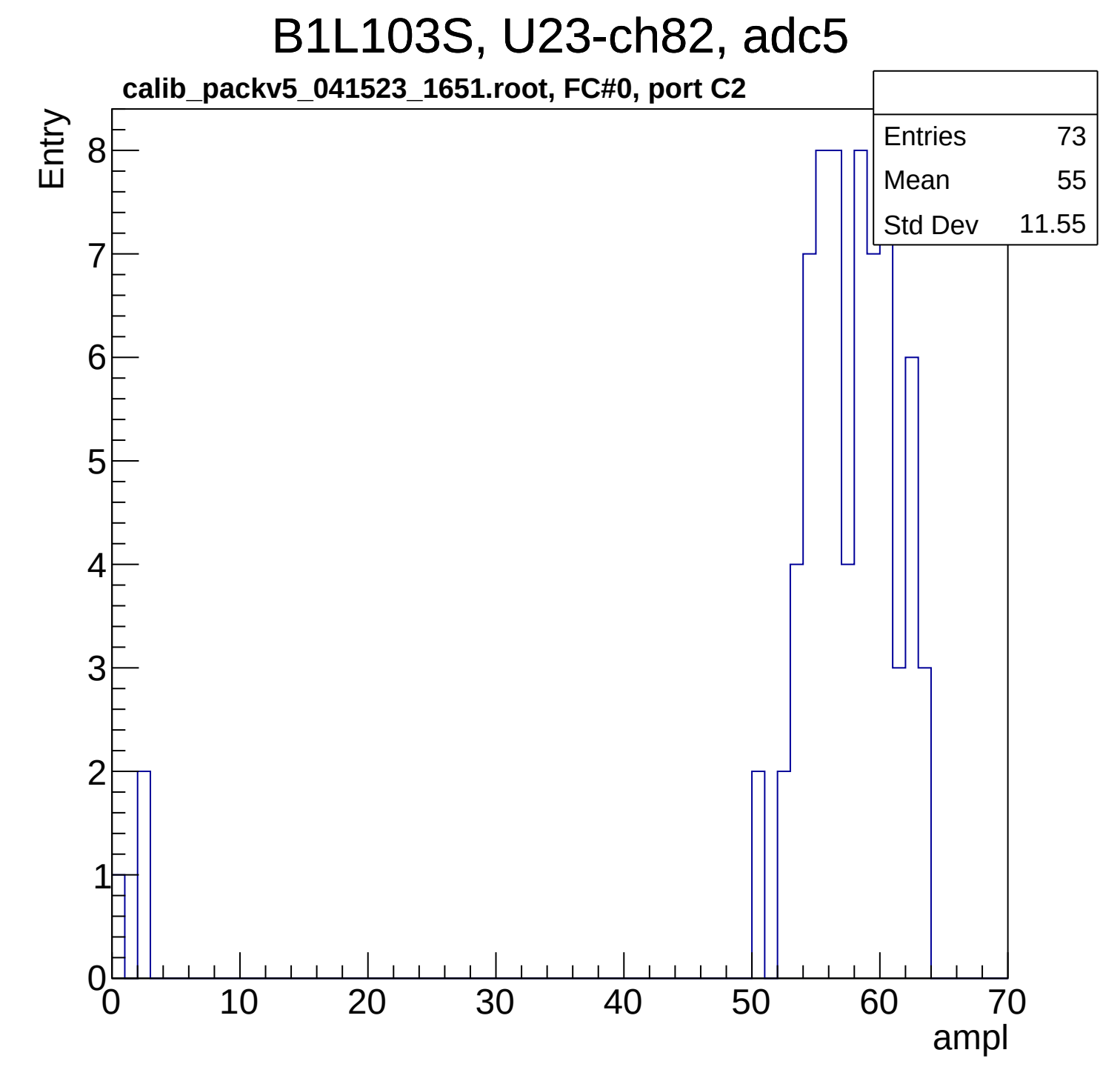

B1L103S, U23-ch82, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 130
Mean 14.38
Std Dev 12.08
Gaus mean : 25.1701
Gaus Width: 4.1679
Entry
ampl
0
5
10
15
20
25
30
0
10
20
30
40
50
60
70

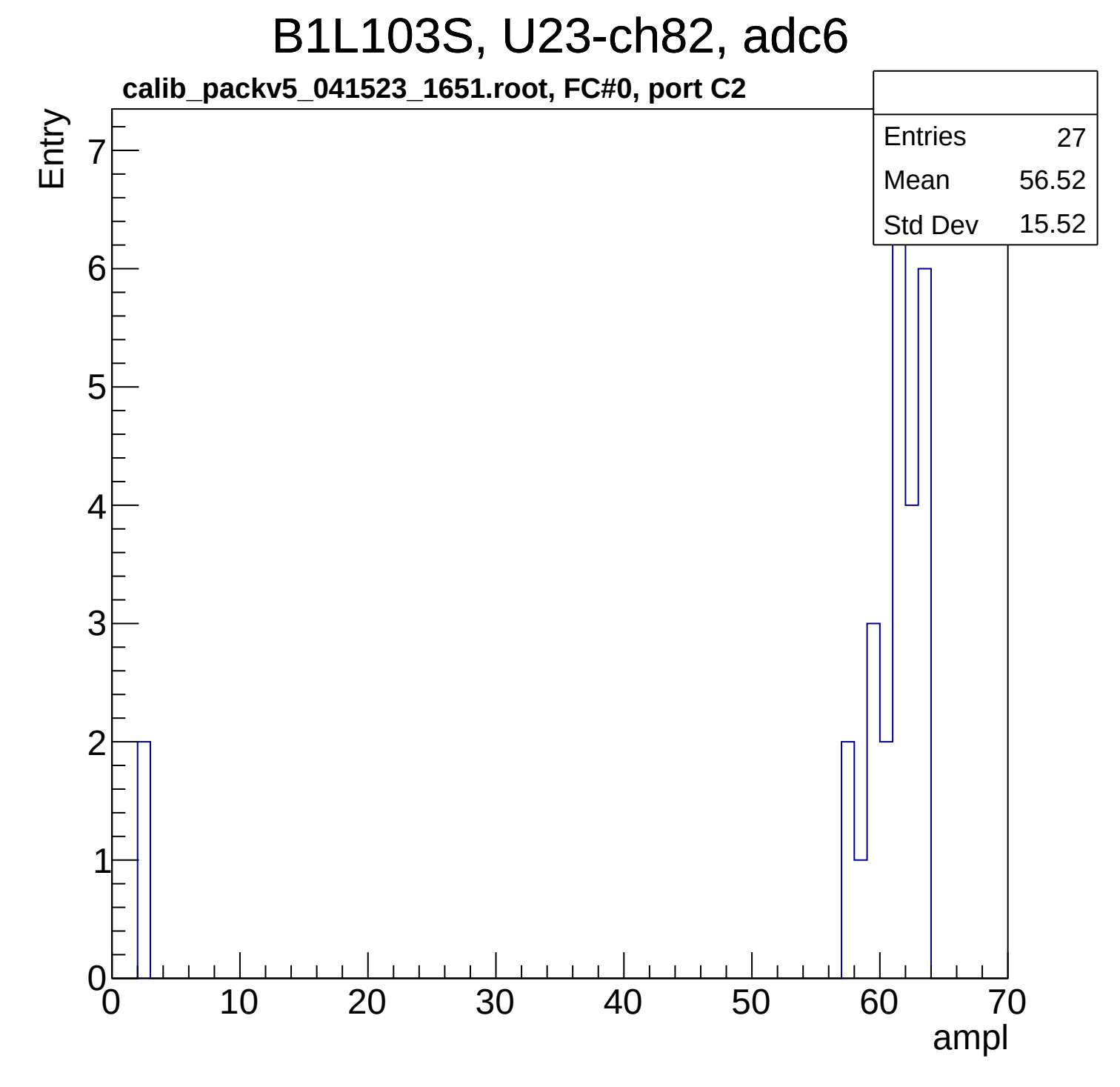

B1L103S, U23-ch82, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 101
Mean 27.1
Std Dev 11.8
Gaus mean : 32.9181
Gaus Width: 3.6570
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

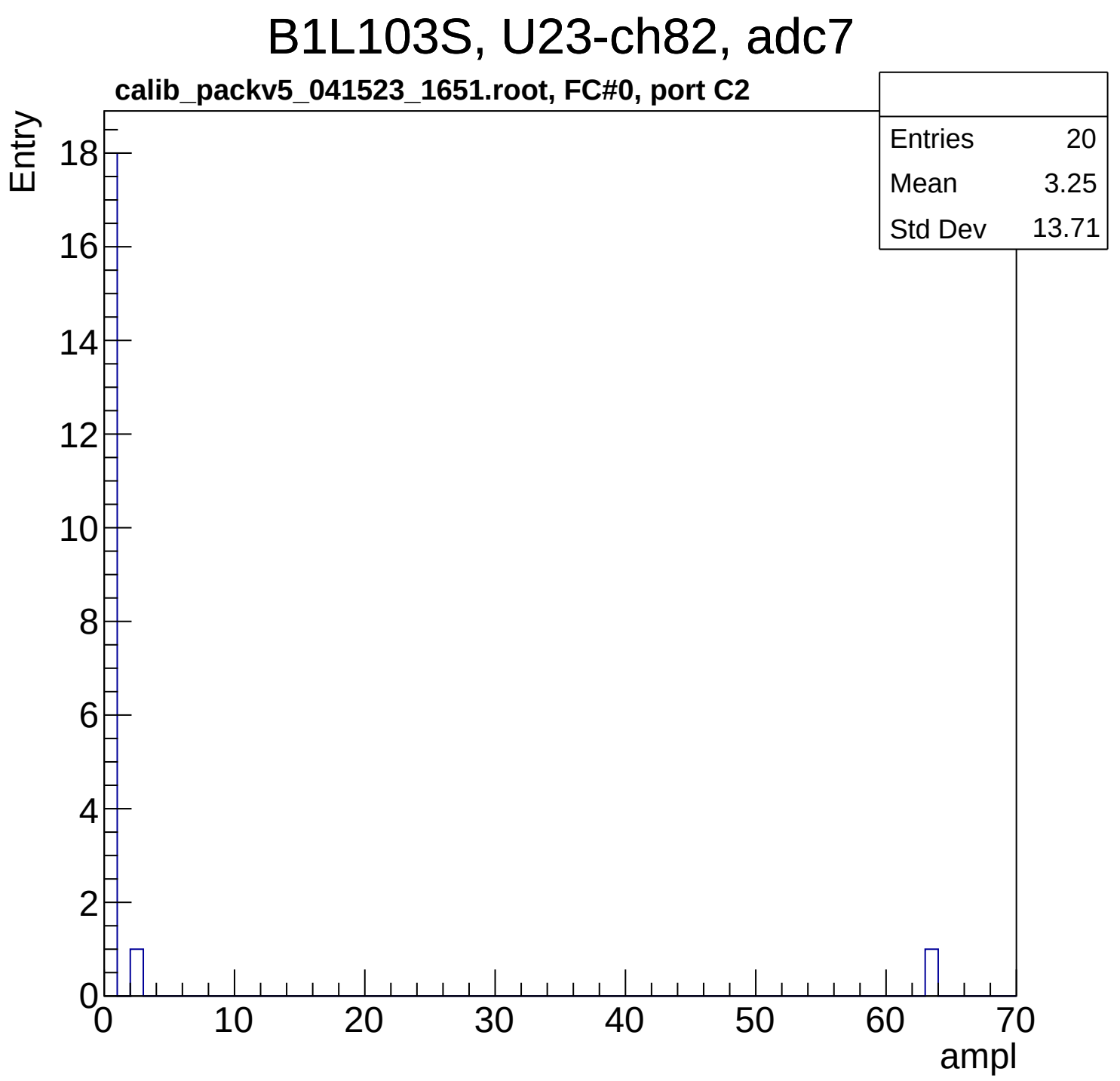

B1L103S, U23-ch82, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 32.83
Std Dev 15.07
Gaus mean : 39.3930
Gaus Width: 2.9092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

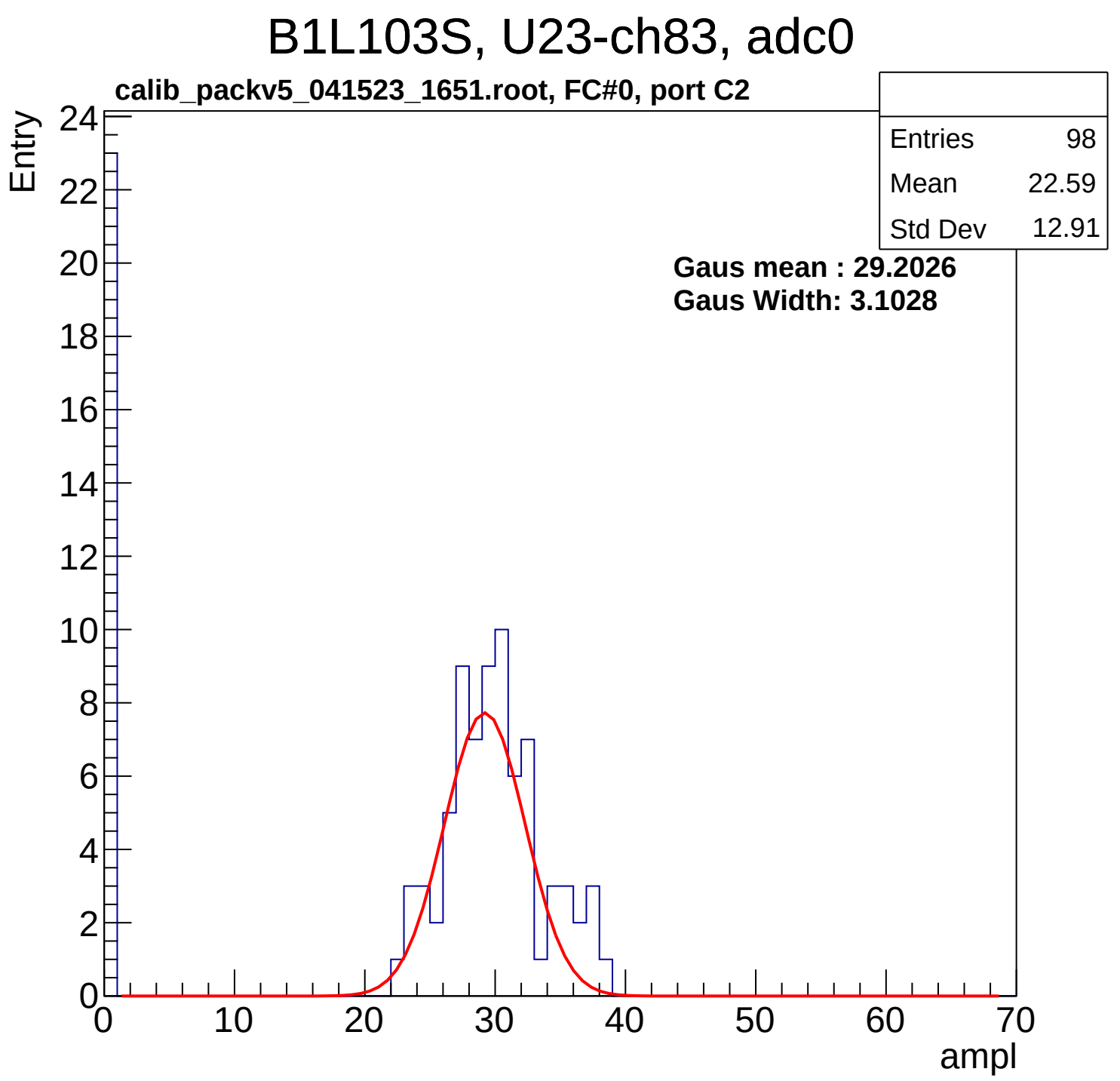

B1L103S, U23-ch82, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 42.49
Std Dev 10.56
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

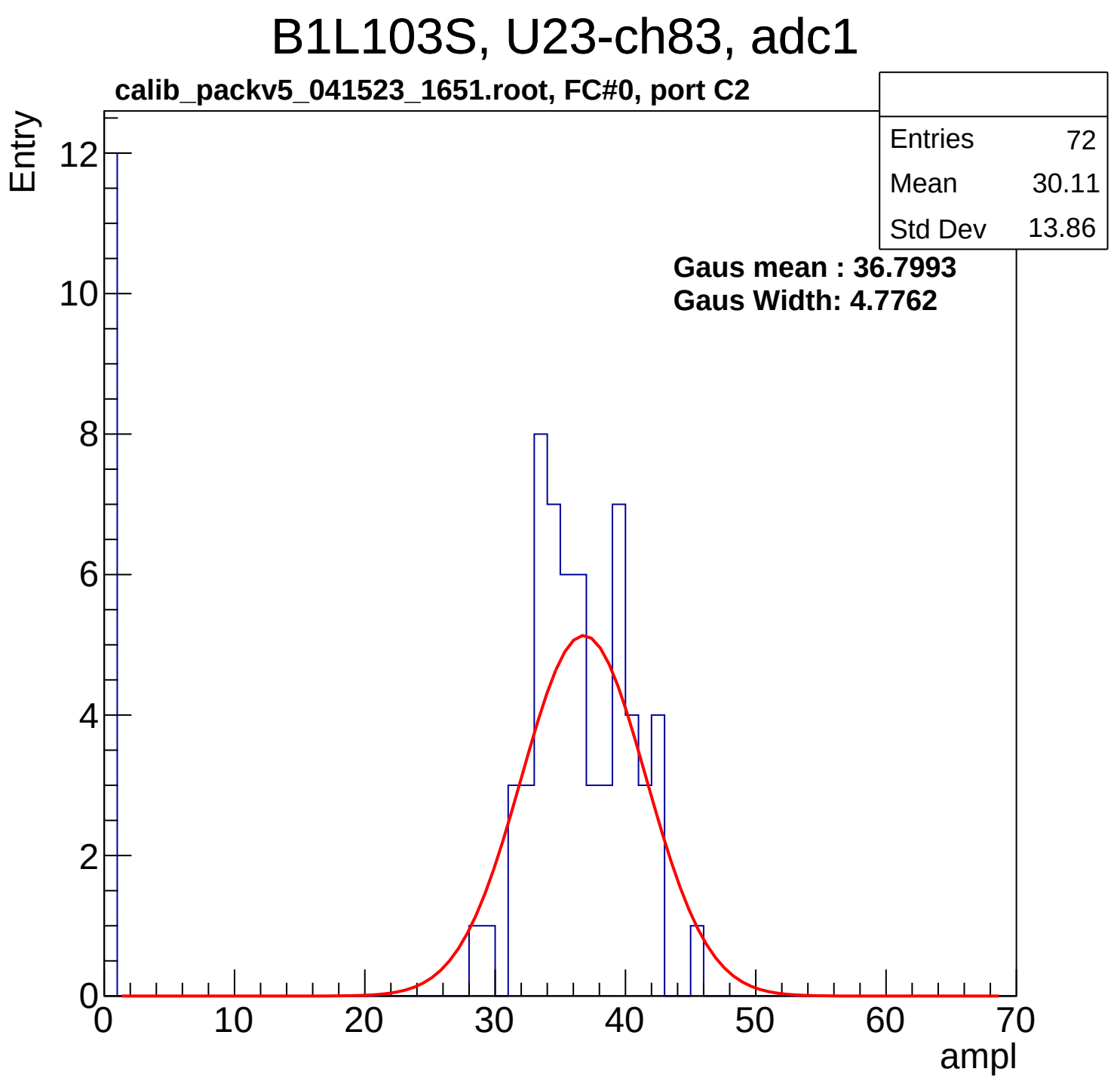

B1L103S, U23-ch82, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 48.64
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

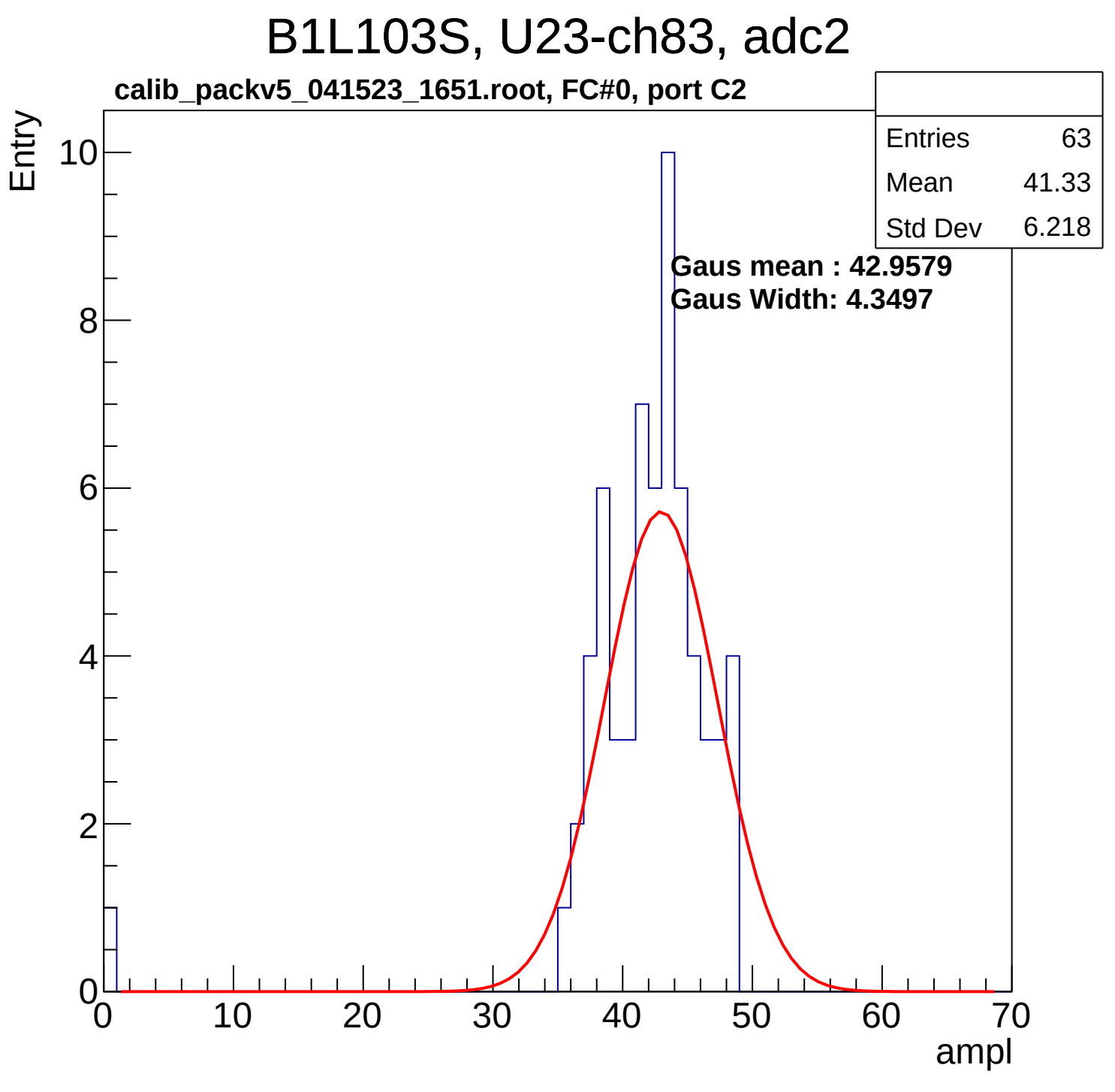

B1L103S, U23-ch82, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 55
Std Dev 11.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

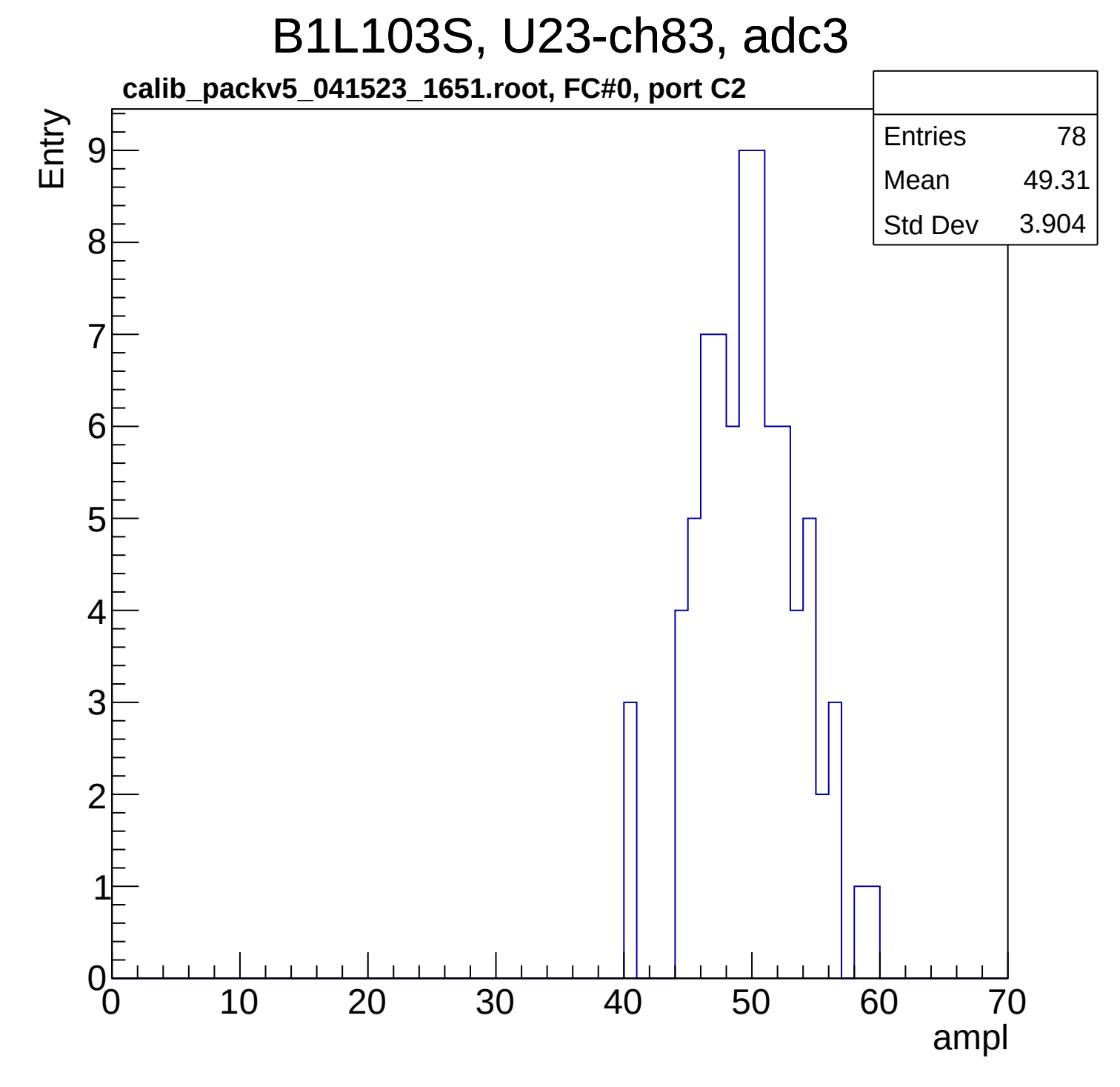

B1L103S, U23-ch82, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 27
Mean 56.52
Std Dev 15.52
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

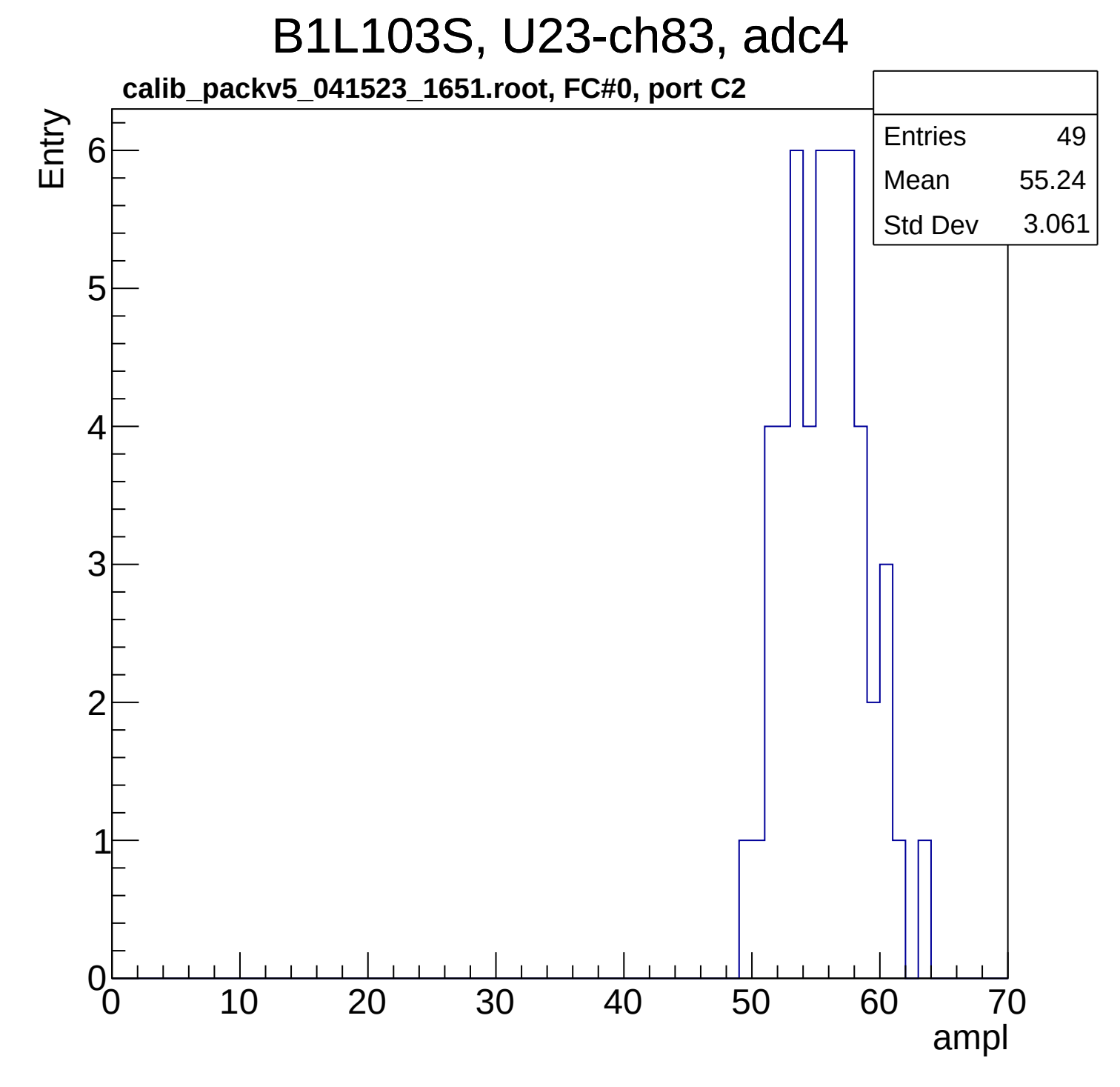

B1L103S, U23-ch82, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.25
Std Dev 13.71
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

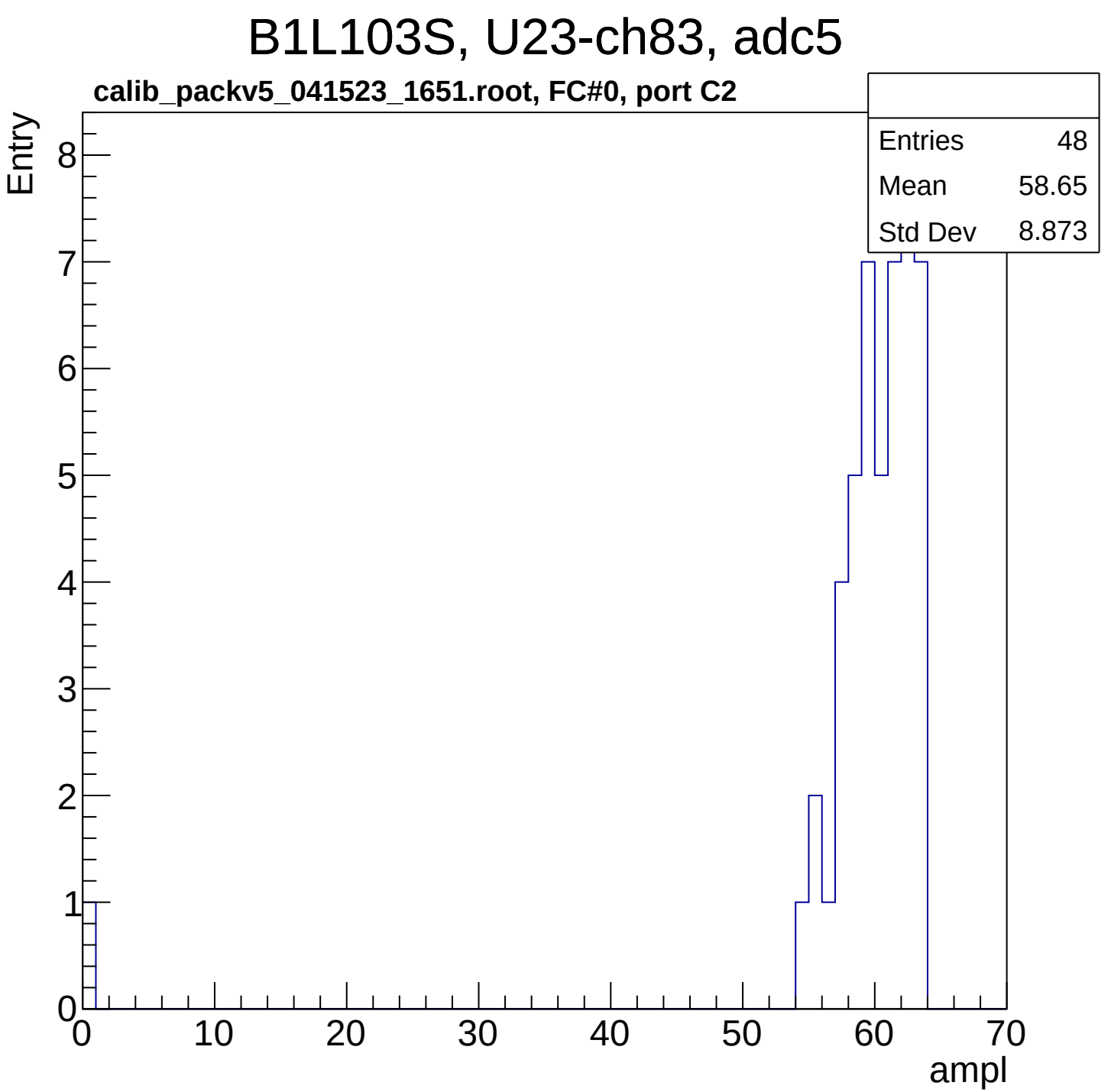

B1L103S, U23-ch83, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 98
Mean 22.59
Std Dev 12.91
Gaus mean : 29.2026
Gaus Width: 3.1028
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
24
0
10
20
30
40
50
60
70

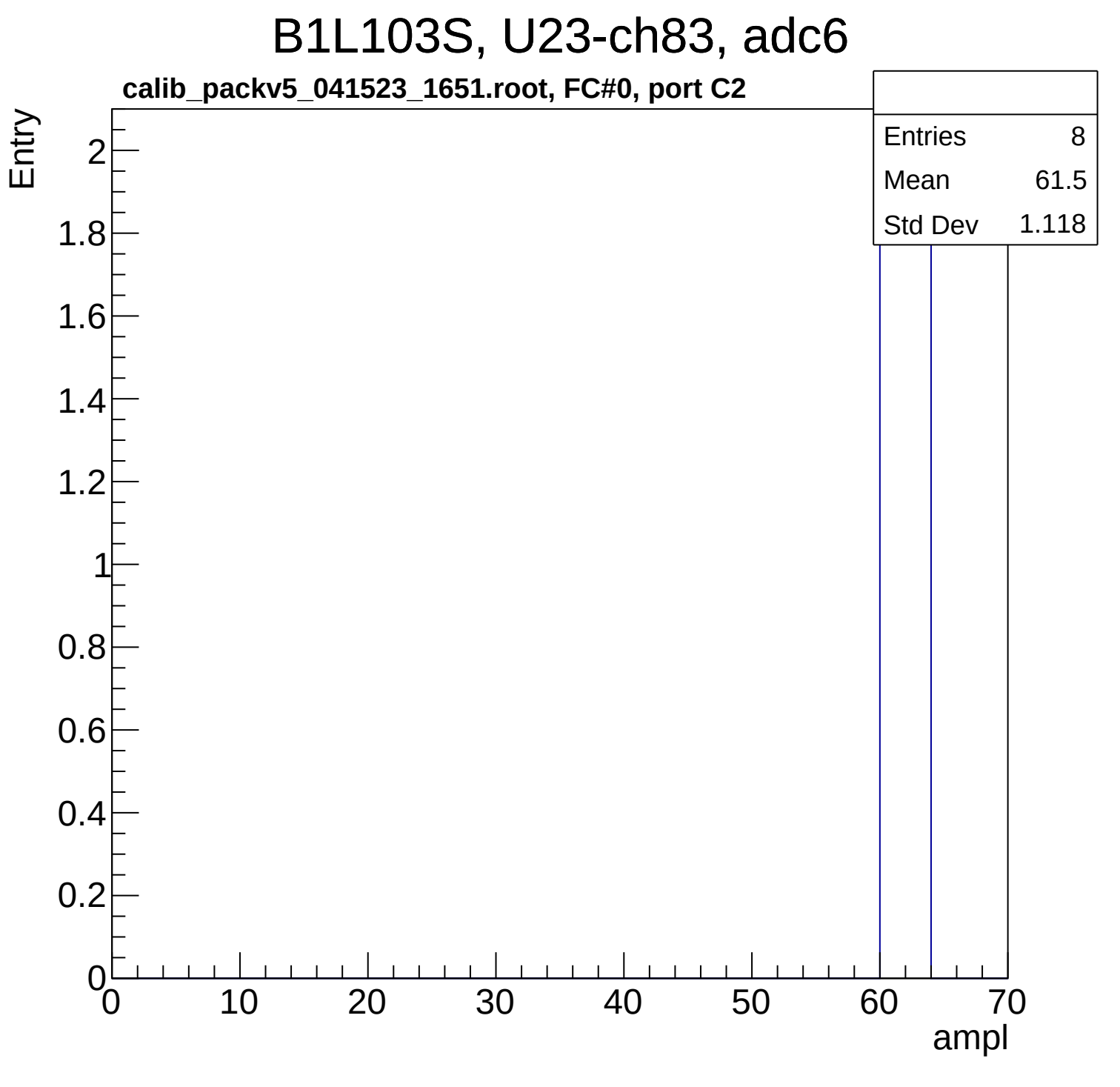

B1L103S, U23-ch83, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 30.11
Std Dev 13.86
Gaus mean : 36.7993
Gaus Width: 4.7762
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

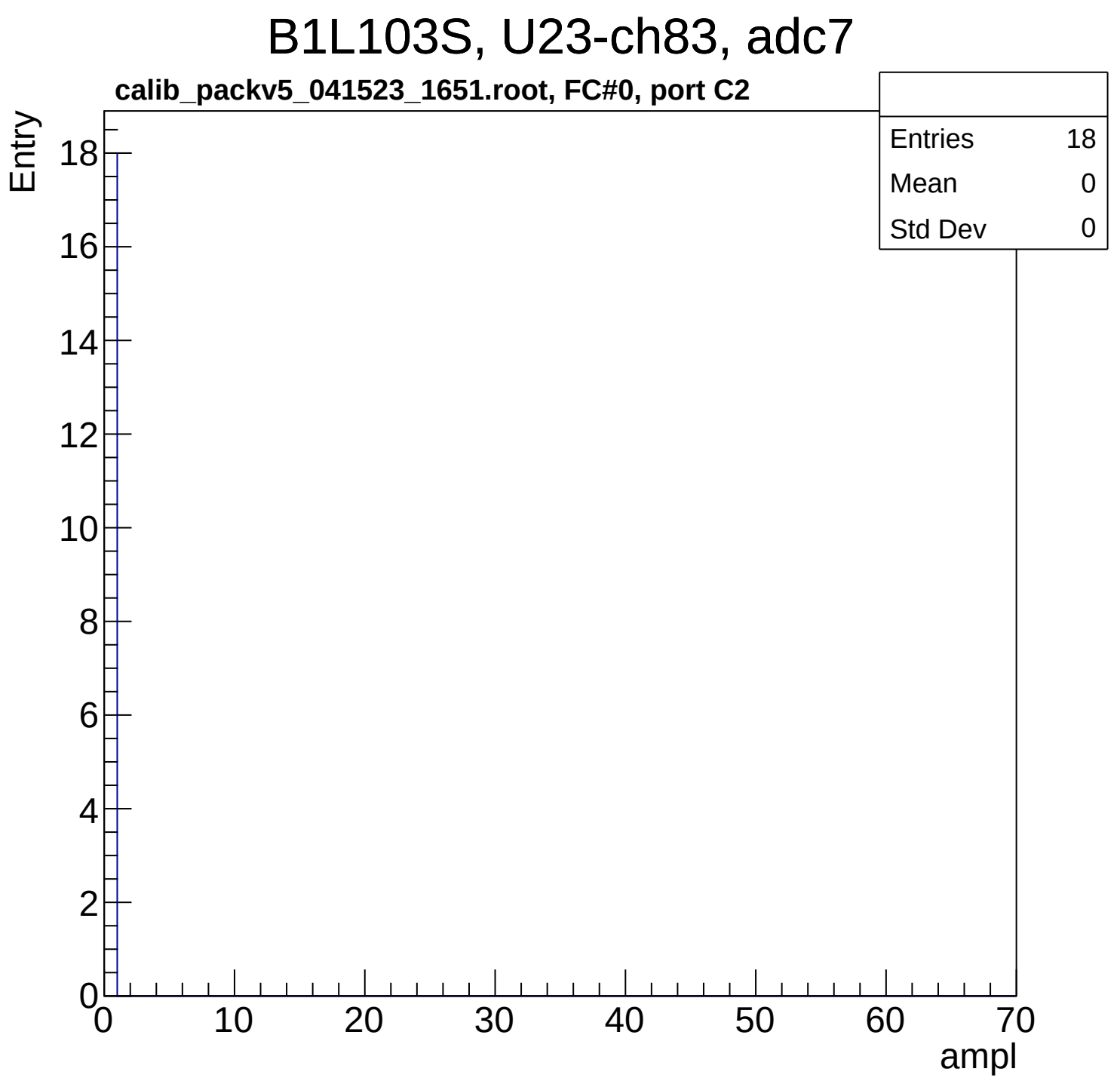

B1L103S, U23-ch83, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 41.33
Std Dev 6.218
Gaus mean : 42.9579
Gaus Width: 4.3497
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

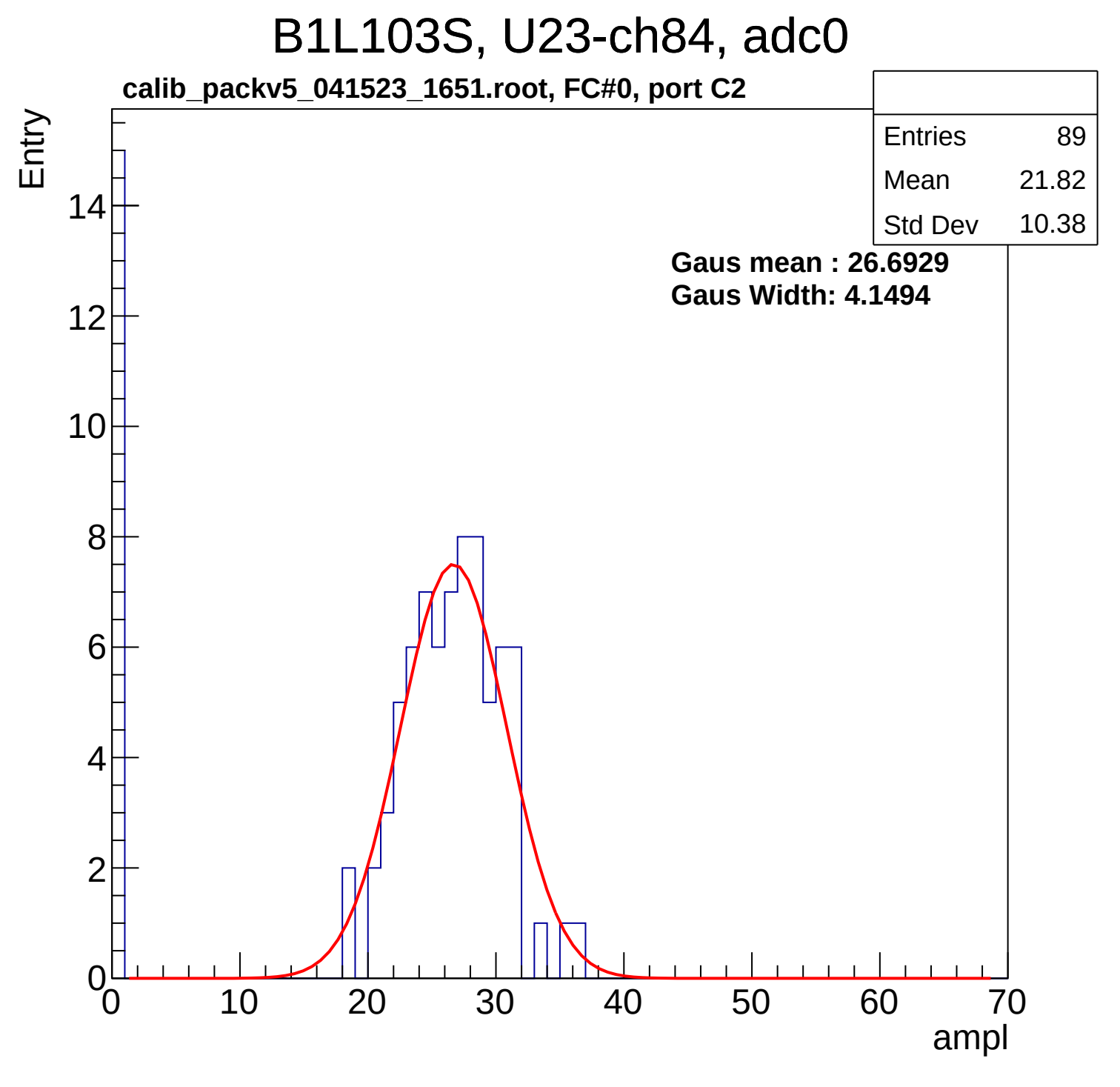

B1L103S, U23-ch83, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 49.31
Std Dev 3.904
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

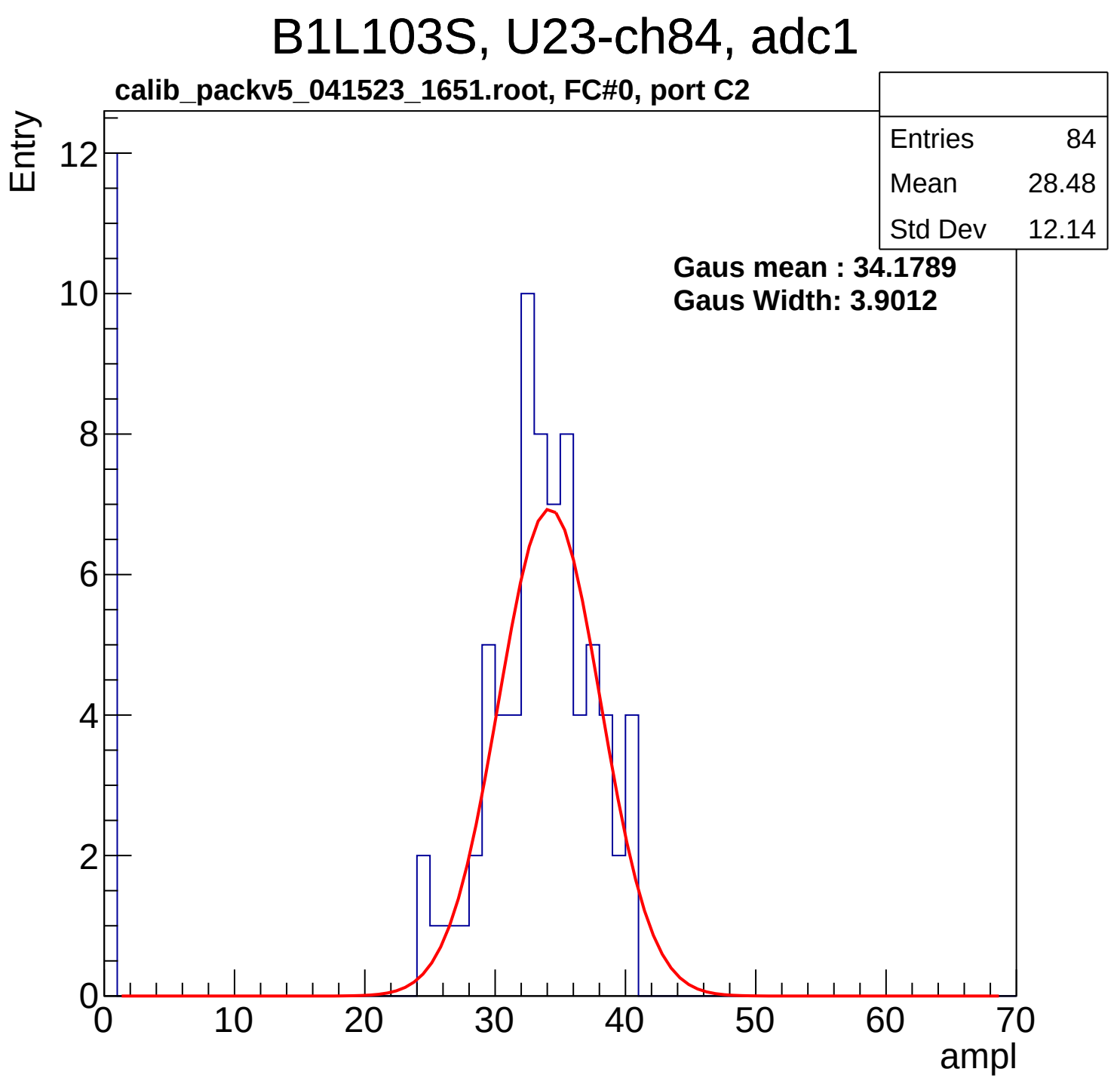

B1L103S, U23-ch83, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 55.24
Std Dev 3.061
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

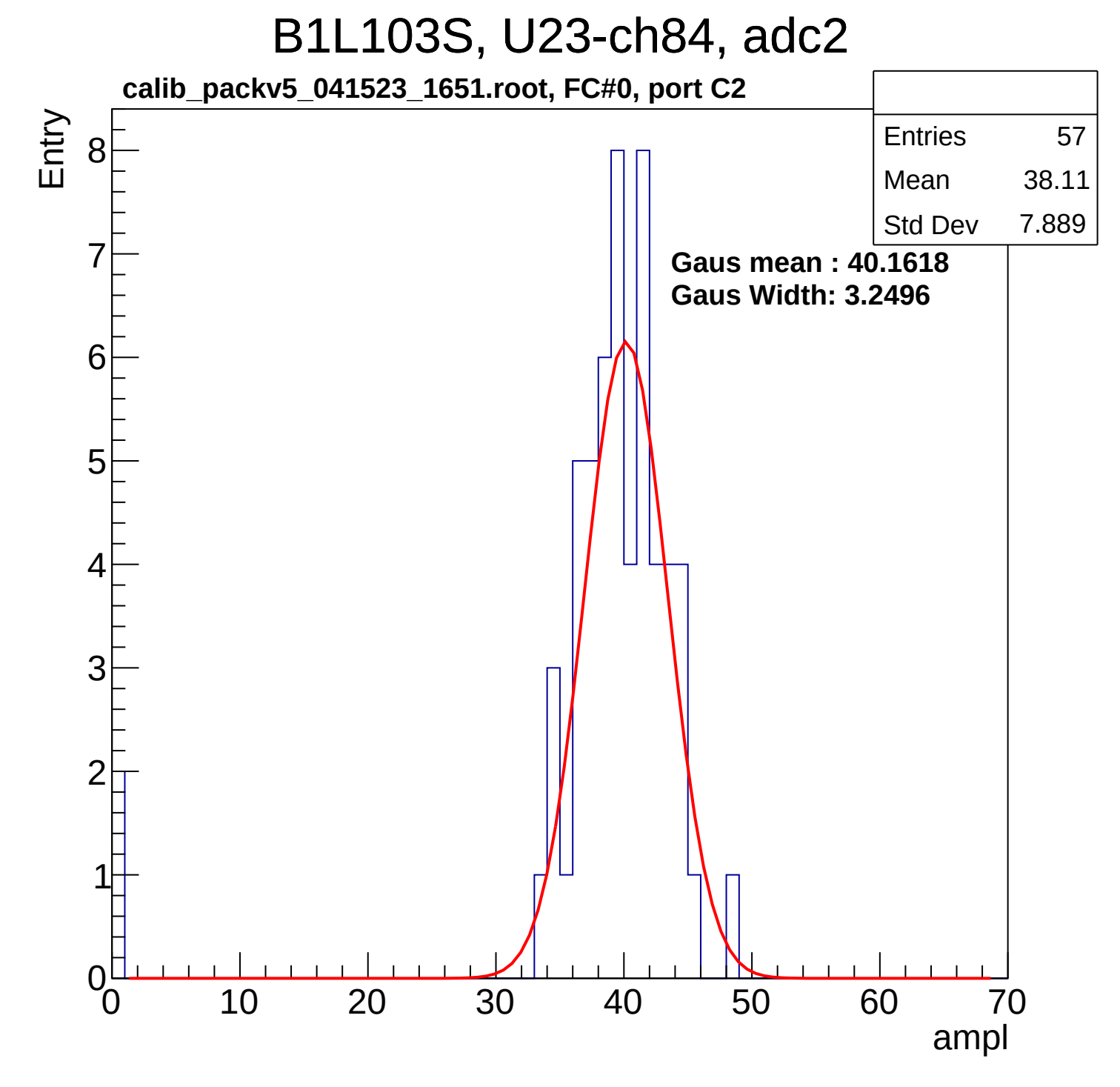

B1L103S, U23-ch83, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 58.65
Std Dev 8.873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

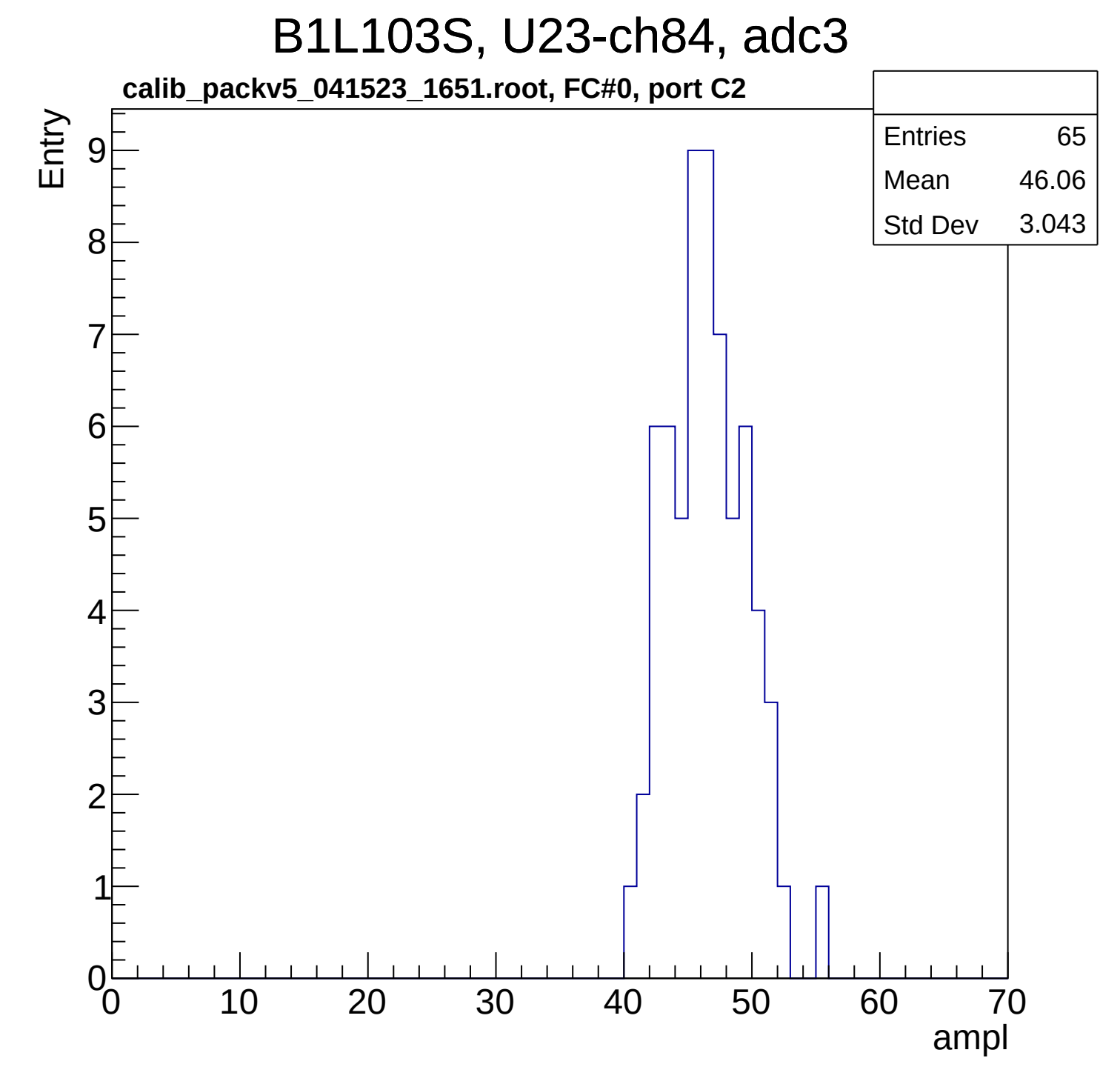

B1L103S, U23-ch83, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 61.5
Std Dev 1.118
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

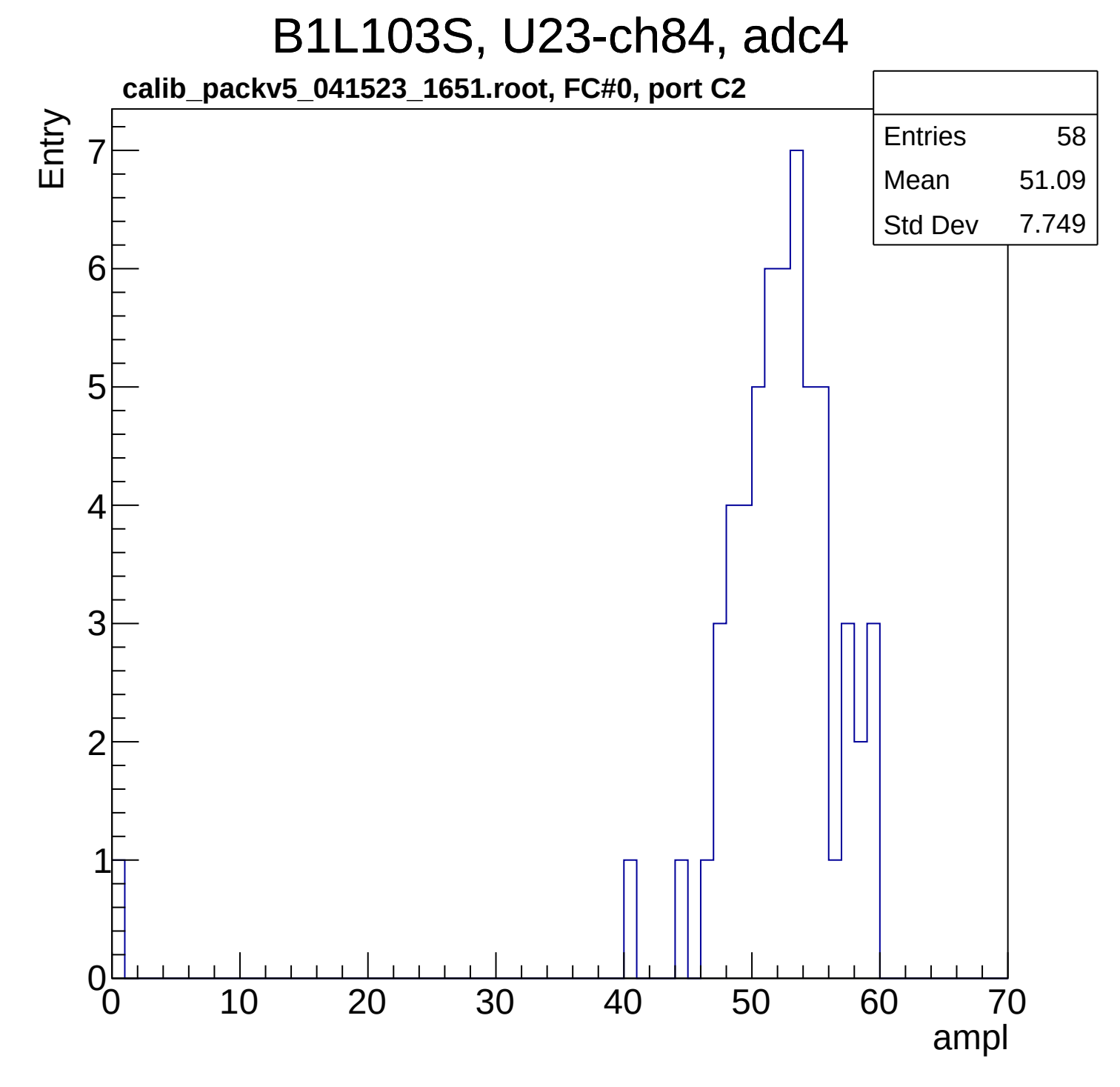

B1L103S, U23-ch83, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

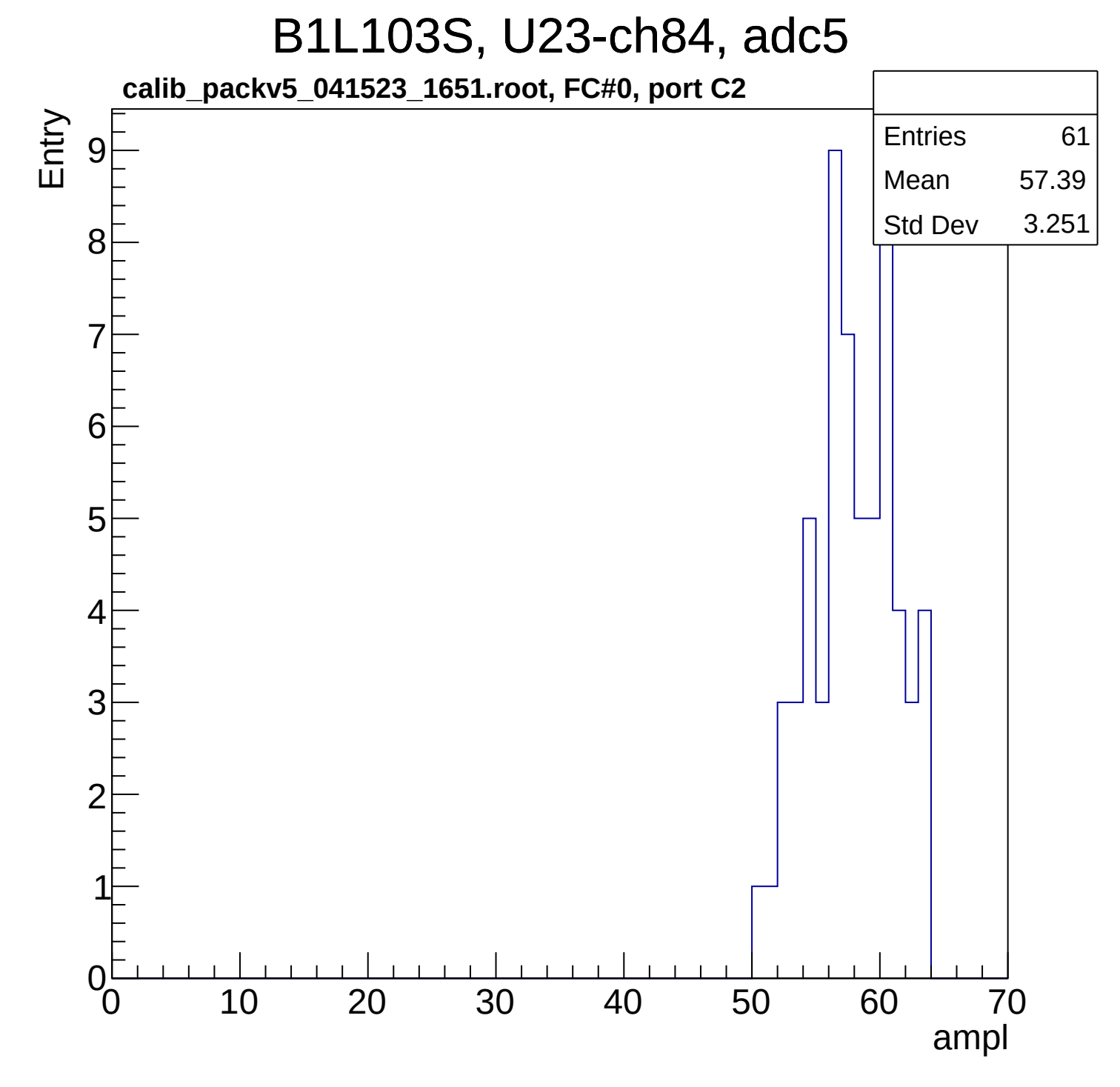

B1L103S, U23-ch84, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 21.82
Std Dev 10.38
Gaus mean : 26.6929
Gaus Width: 4.1494
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

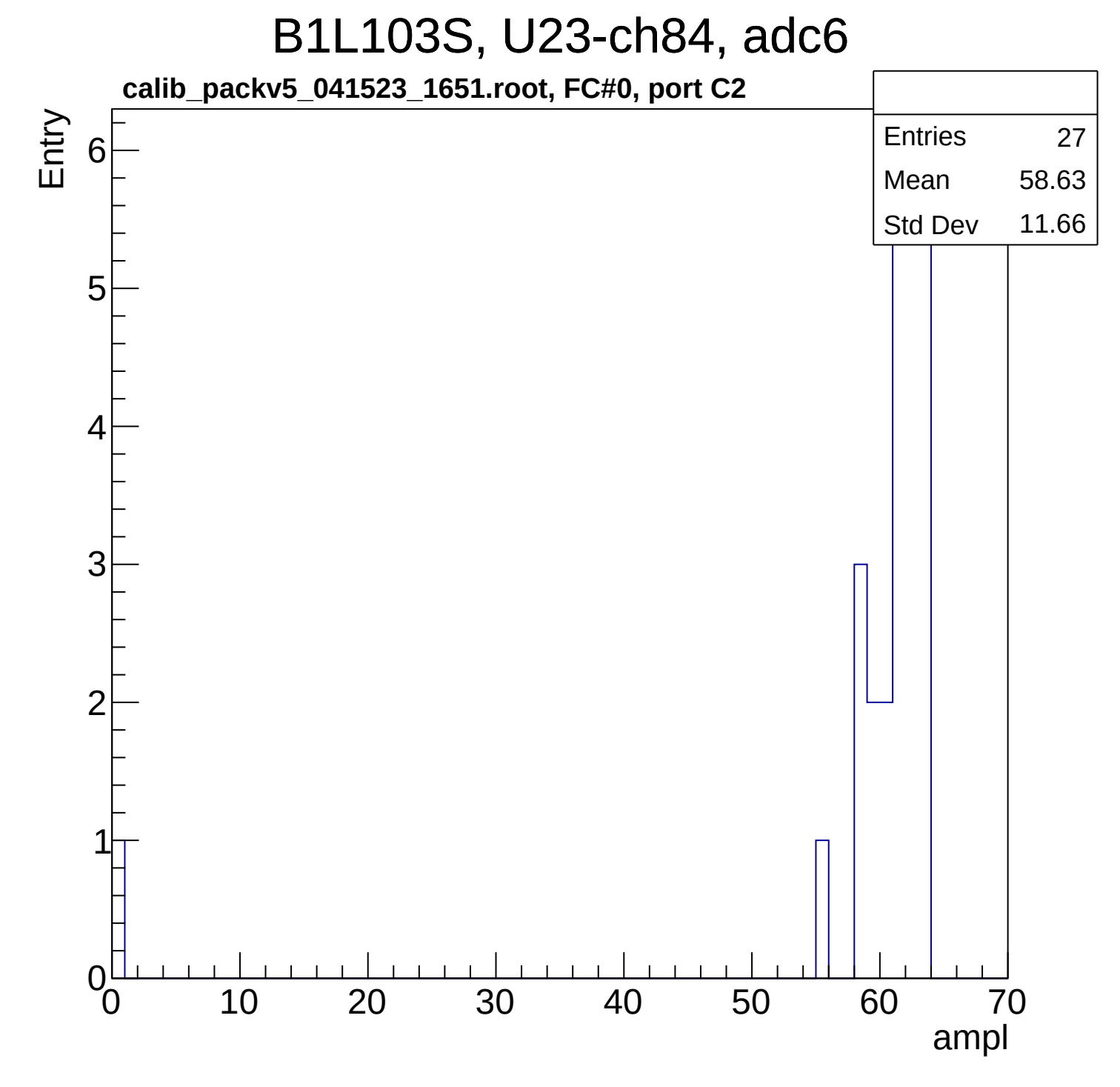

B1L103S, U23-ch84, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 84
Mean 28.48
Std Dev 12.14
Gaus mean : 34.1789
Gaus Width: 3.9012
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

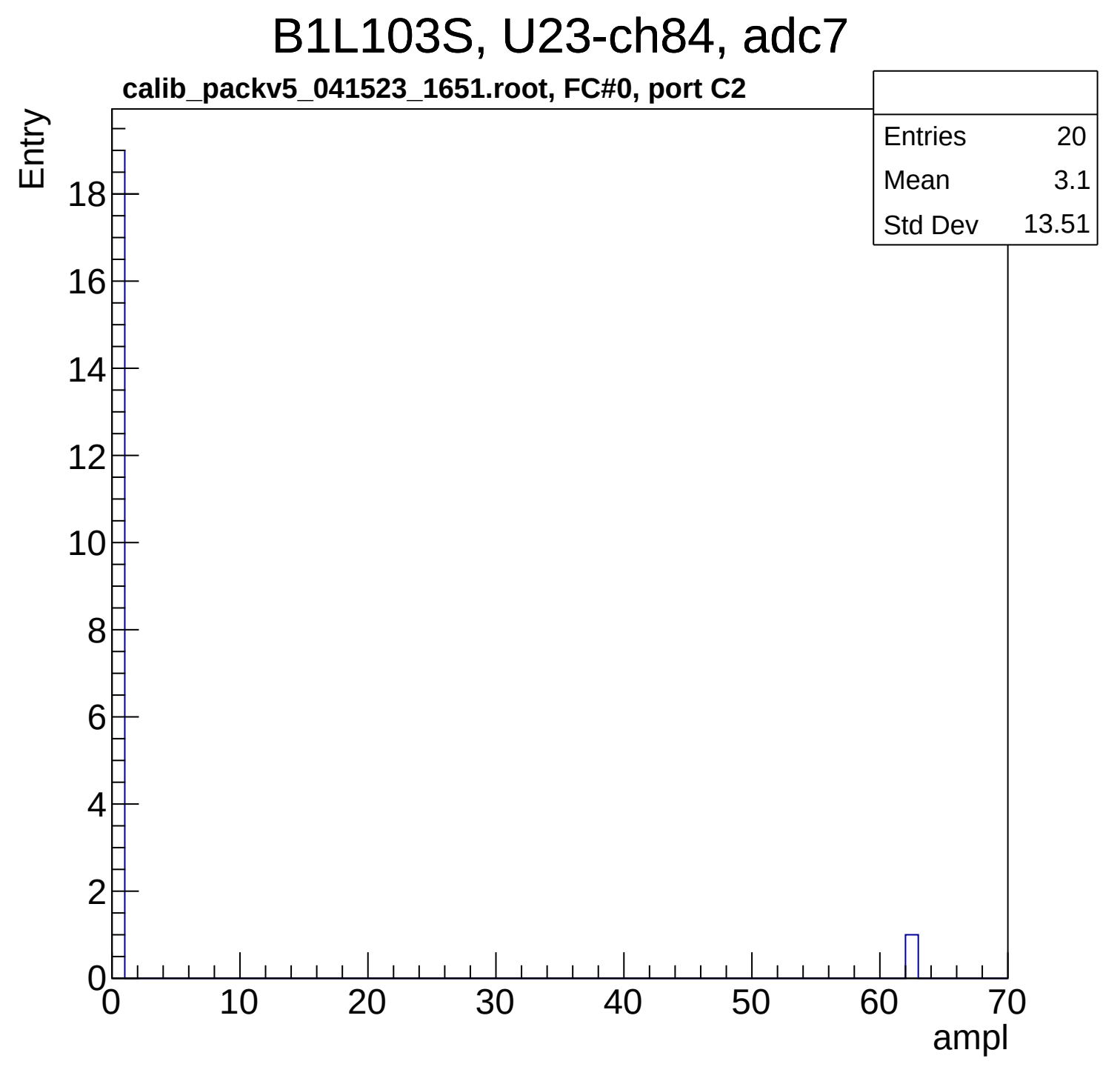

B1L103S, U23-ch84, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 38.11
Std Dev 7.889
Gaus mean : 40.1618
Gaus Width: 3.2496
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

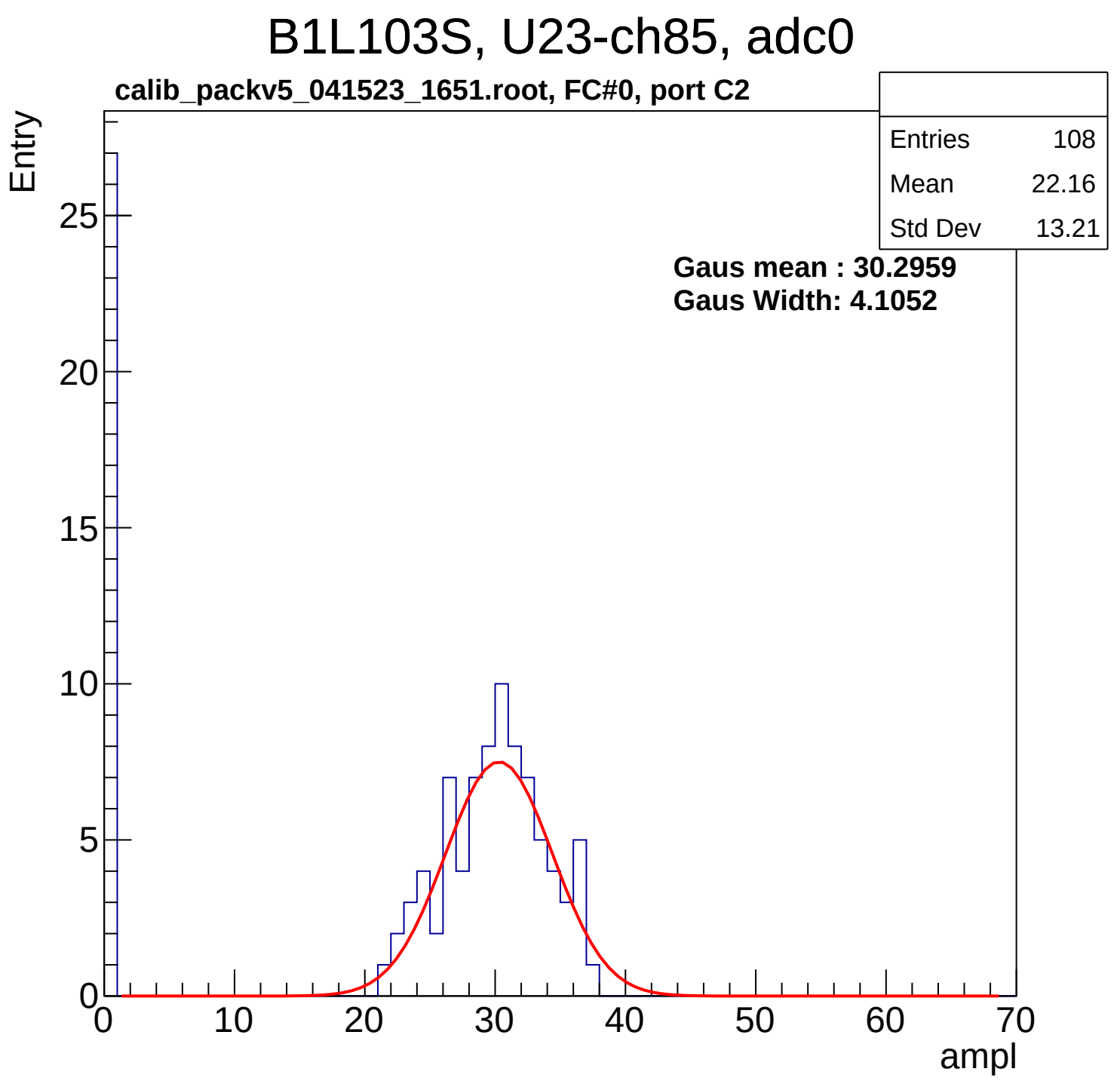

B1L103S, U23-ch84, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 46.06
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

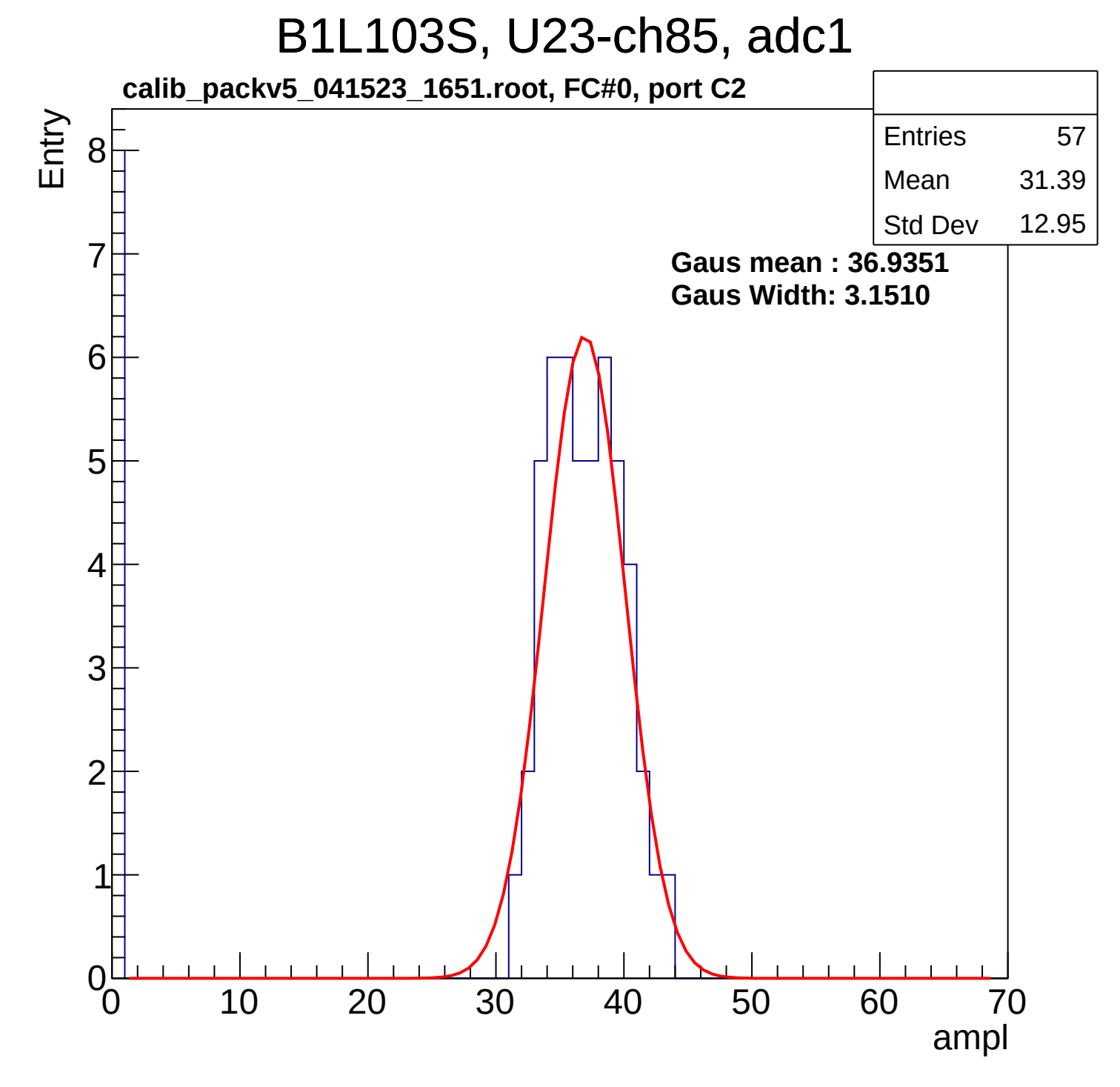

B1L103S, U23-ch84, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 51.09
Std Dev 7.749
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

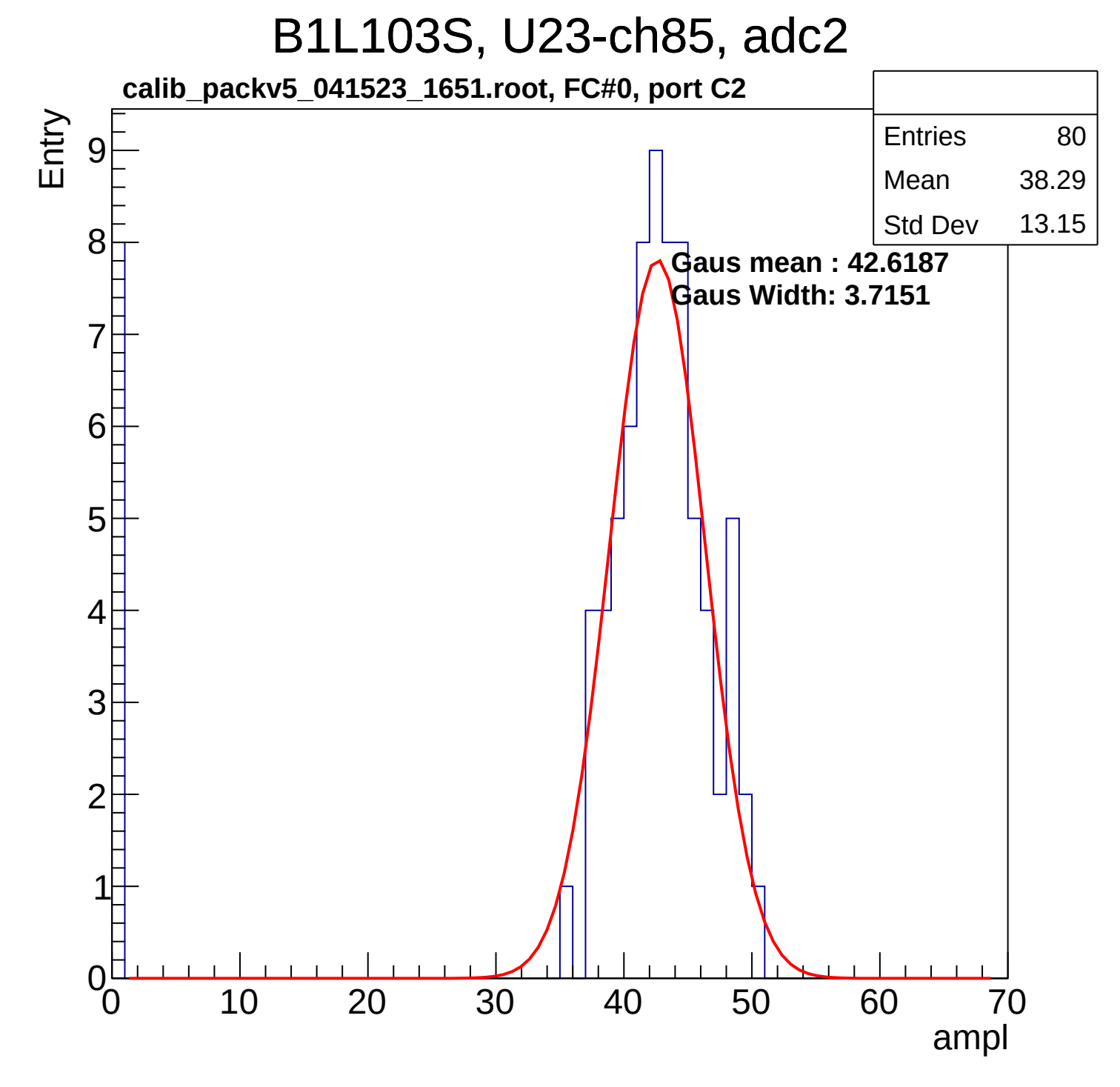

B1L103S, U23-ch84, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 57.39
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

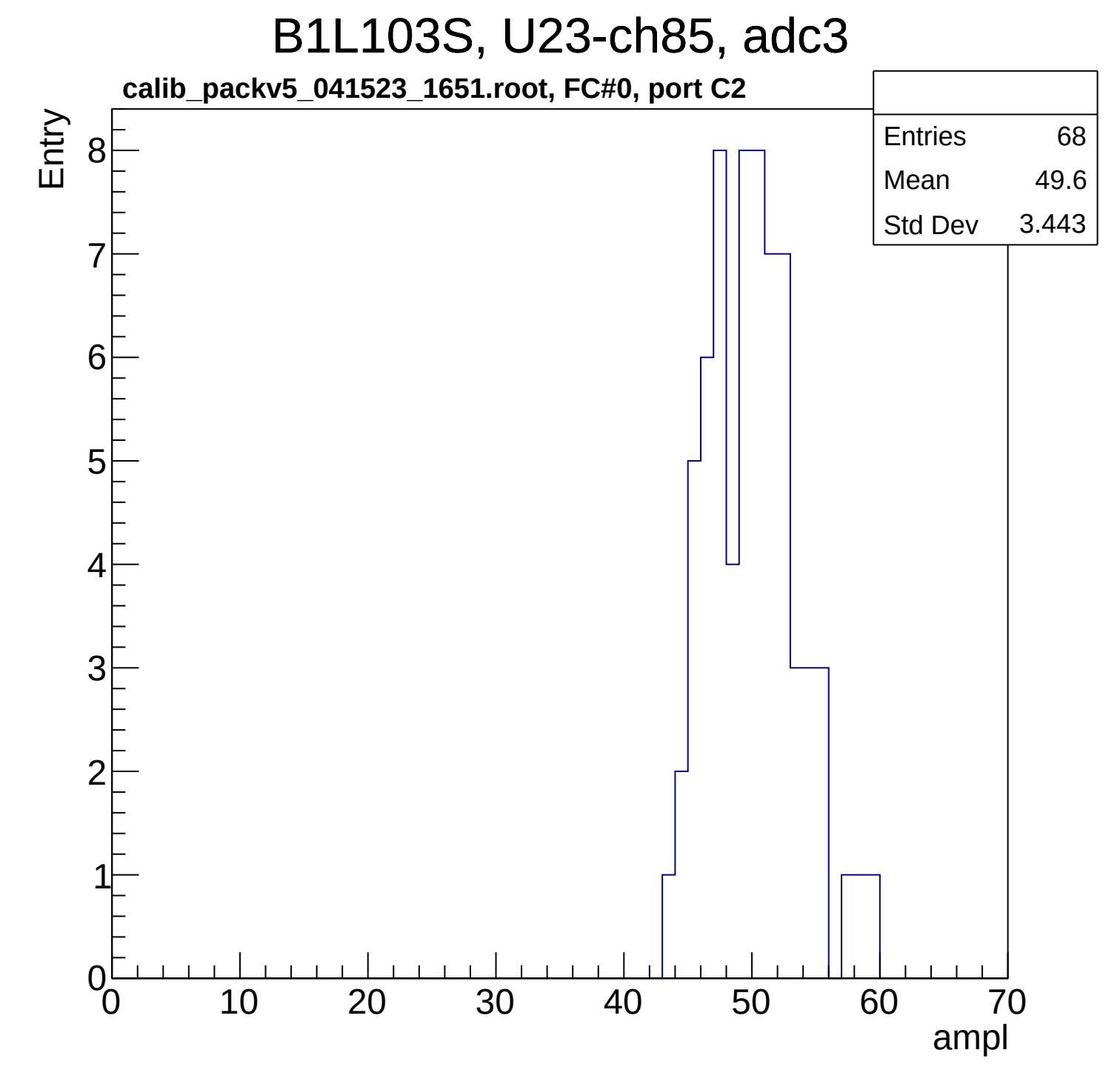

B1L103S, U23-ch84, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 27
Mean 58.63
Std Dev 11.66
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

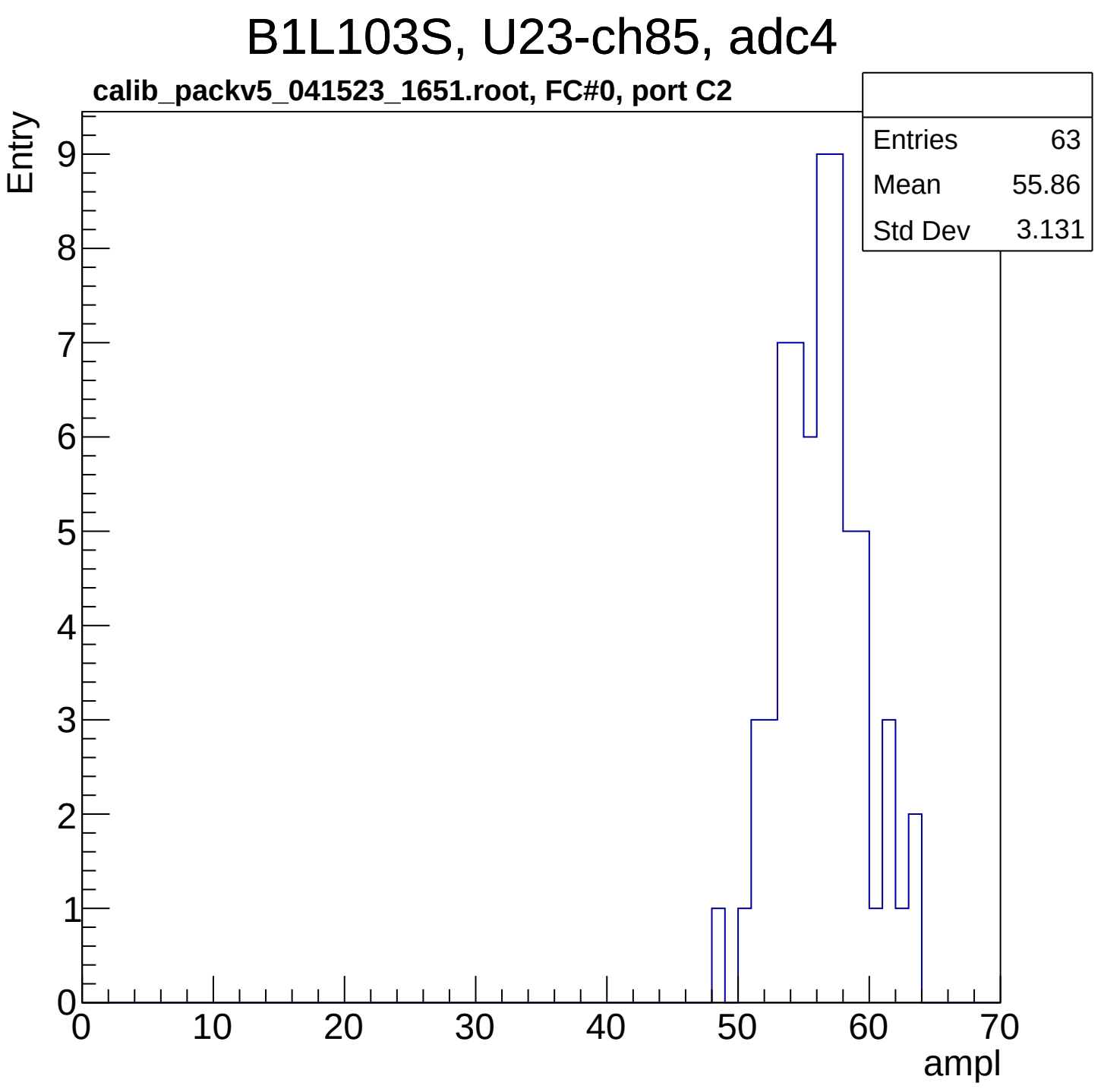

B1L103S, U23-ch84, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.1
Std Dev 13.51
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

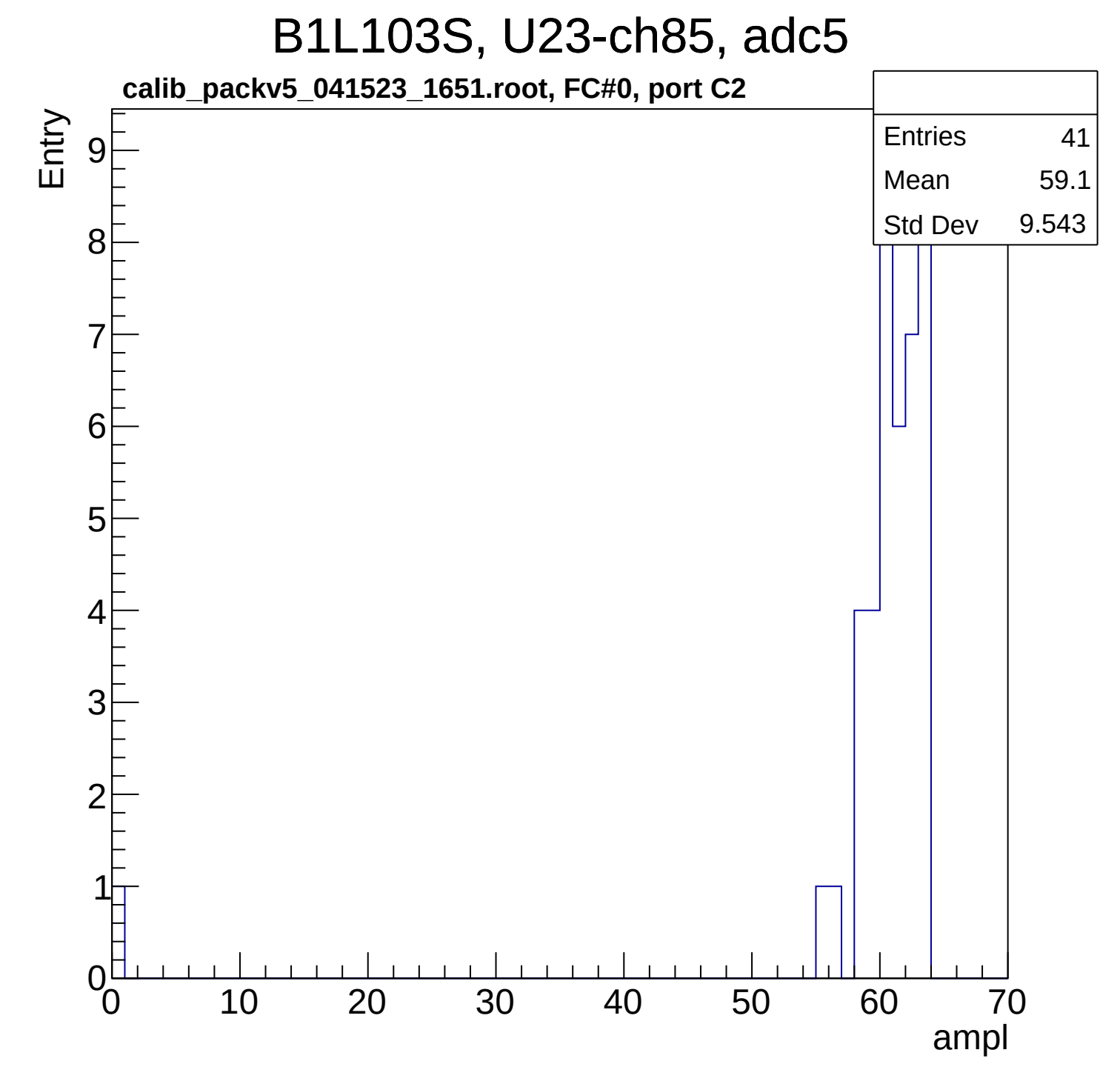

B1L103S, U23-ch85, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 108
Mean 22.16
Std Dev 13.21
Gaus mean : 30.2959
Gaus Width: 4.1052
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

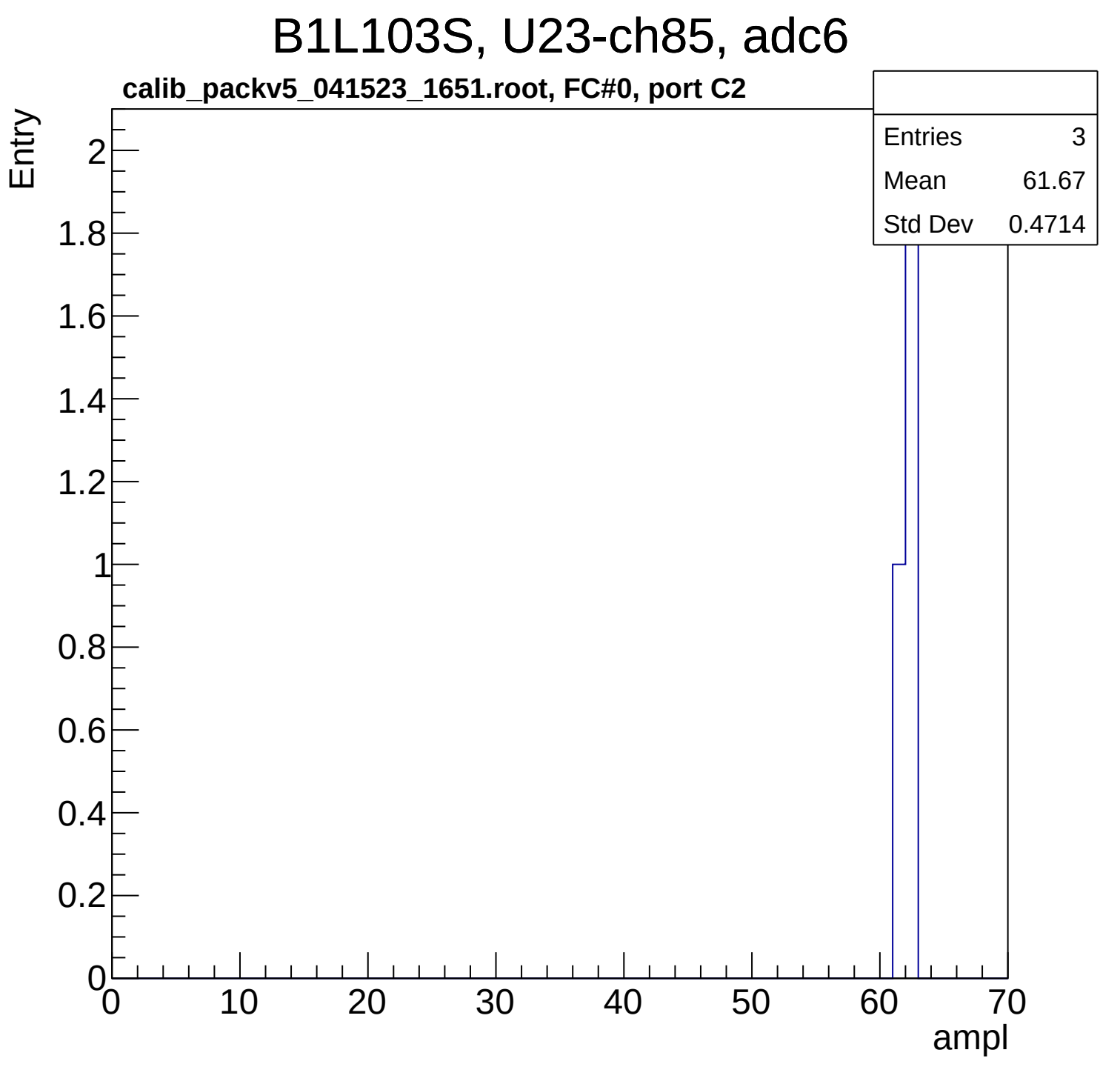

B1L103S, U23-ch85, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 31.39
Std Dev 12.95
Gaus mean : 36.9351
Gaus Width: 3.1510
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

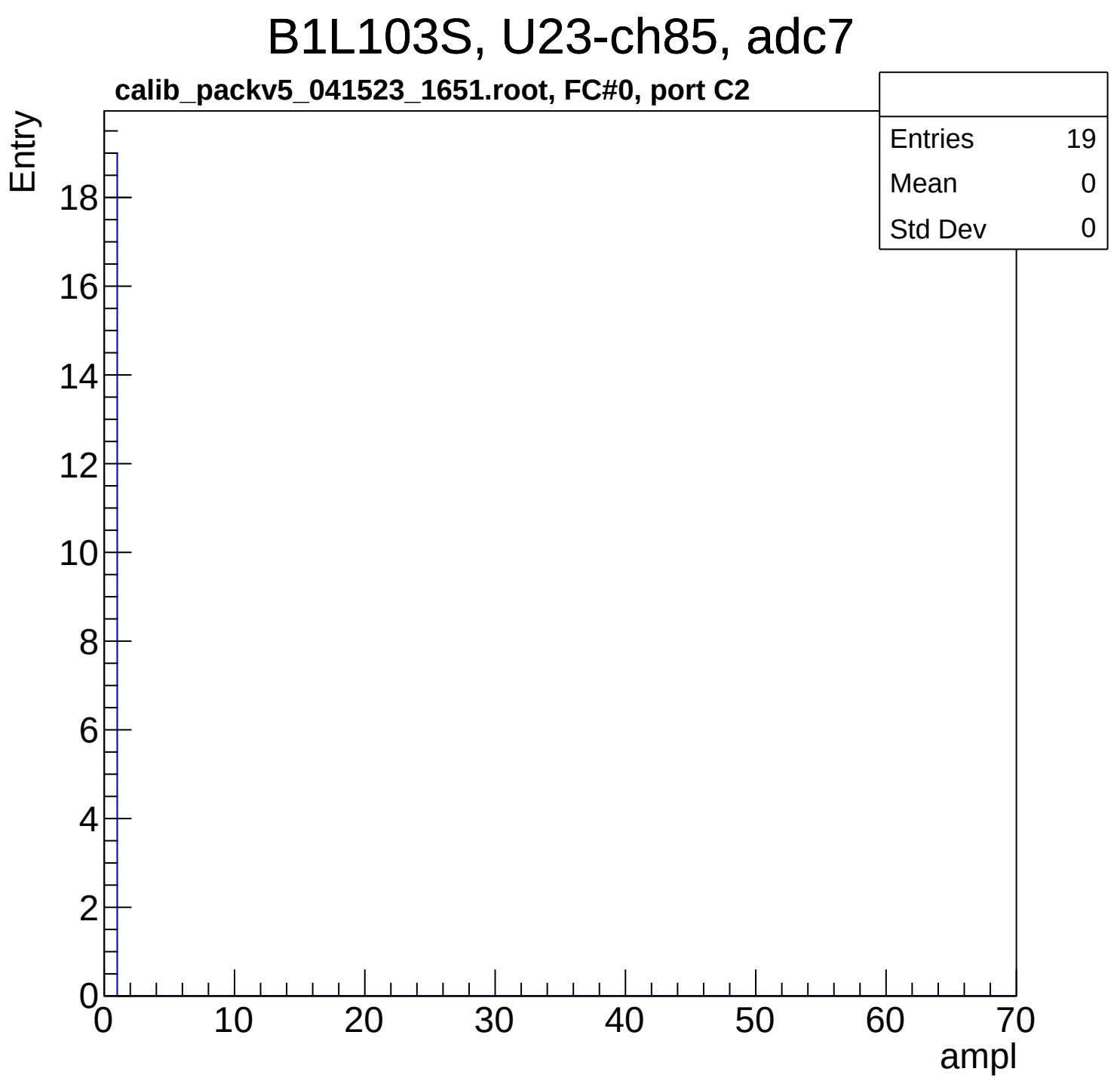

B1L103S, U23-ch85, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 38.29
Std Dev 13.15
Gaus mean : 42.6187
Gaus Width: 3.7151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

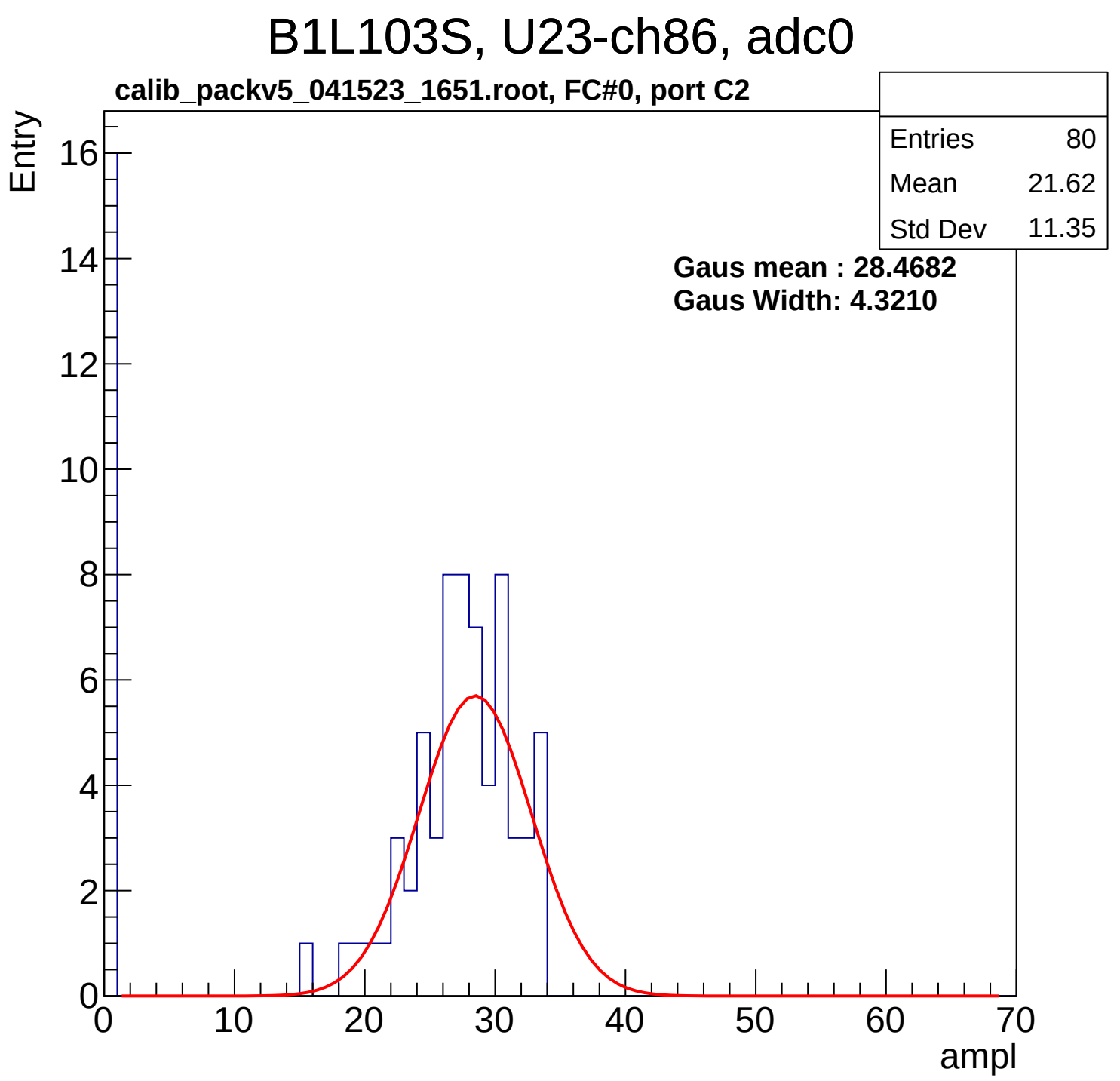

B1L103S, U23-ch85, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 49.6
Std Dev 3.443
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

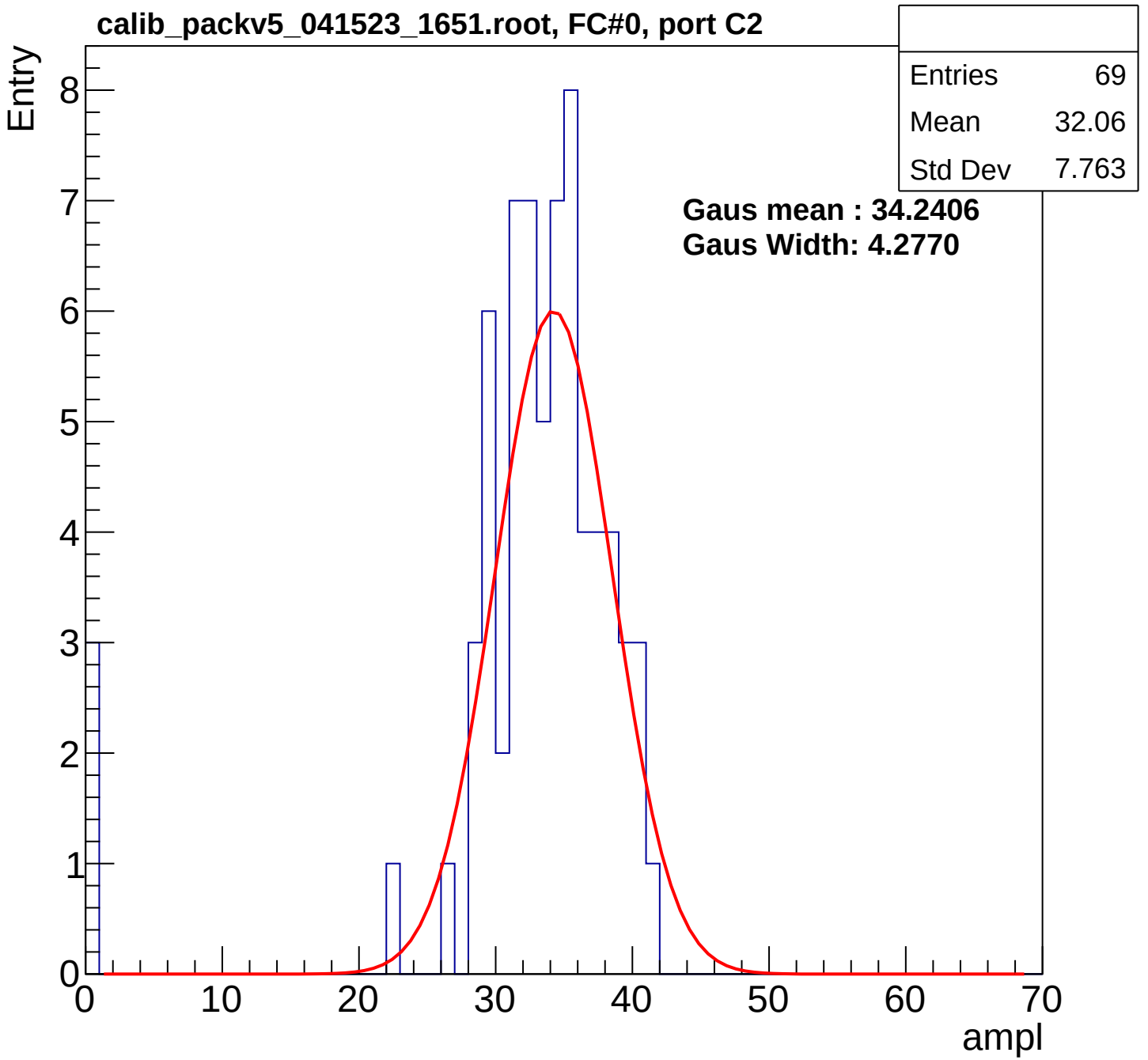

B1L103S, U23-ch85, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 55.86
Std Dev 3.131
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

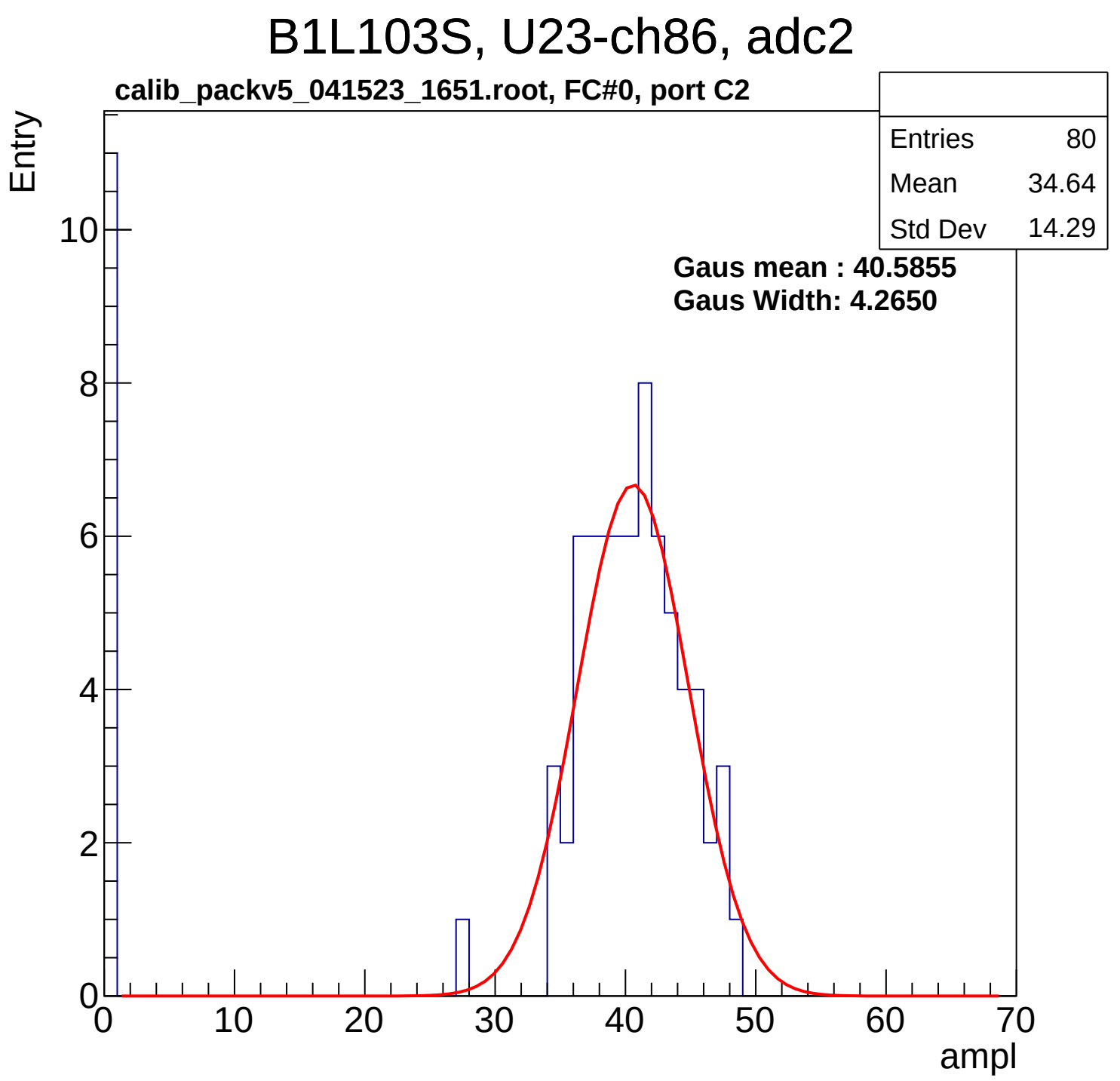

B1L103S, U23-ch85, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 41
Mean 59.1
Std Dev 9.543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

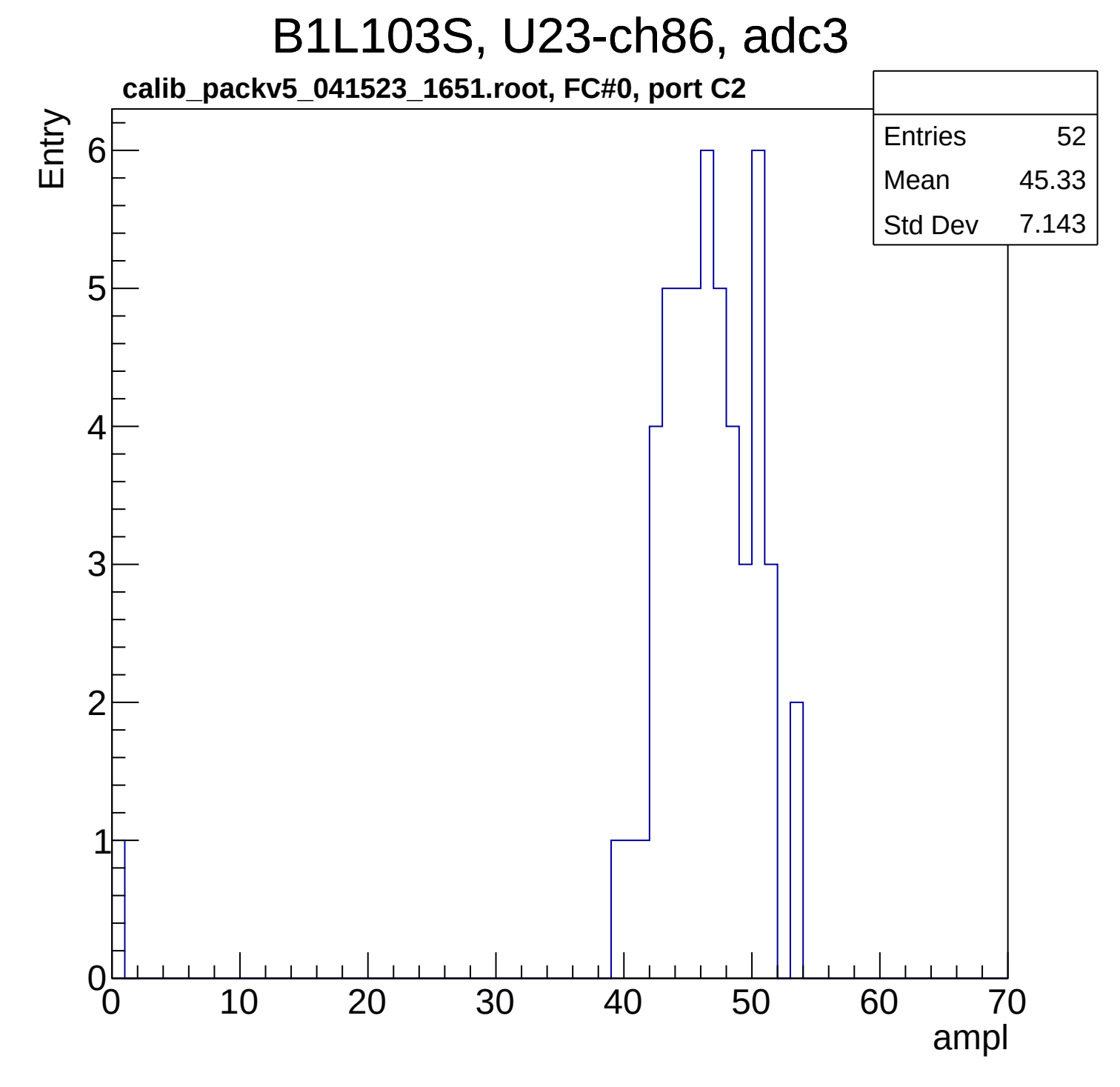

B1L103S, U23-ch85, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

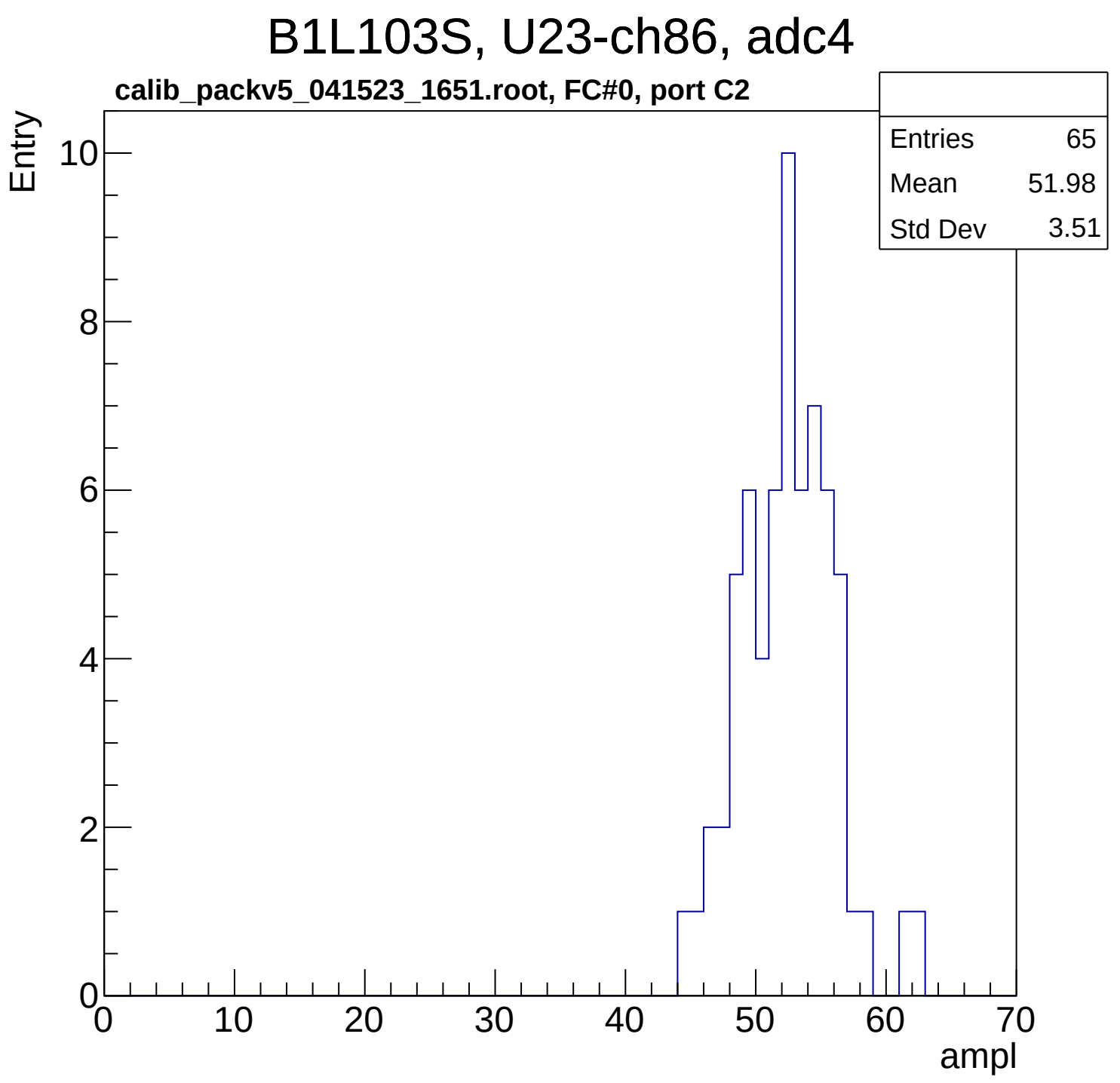

B1L103S, U23-ch85, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

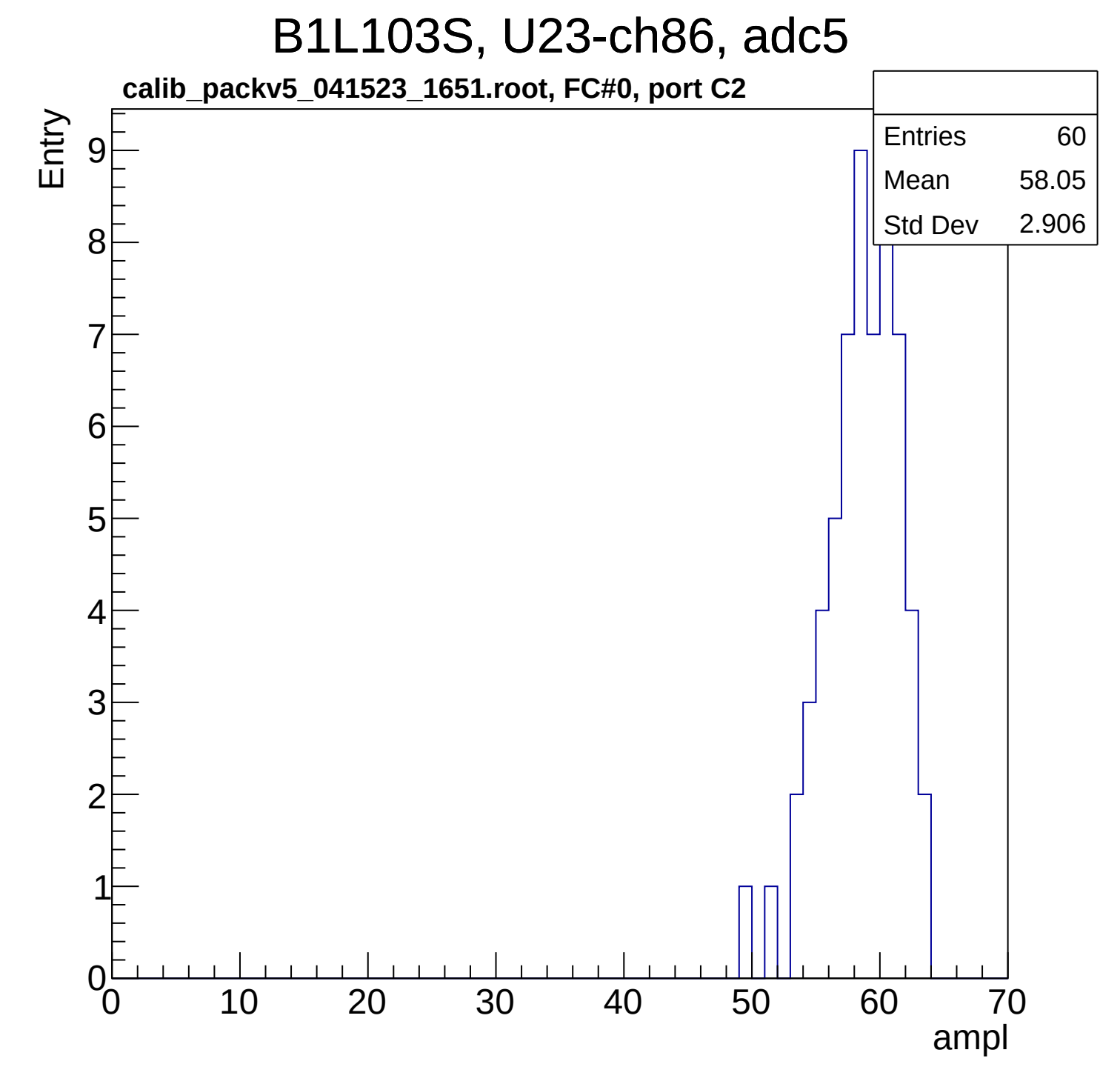

B1L103S, U23-ch86, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 21.62
Std Dev 11.35
Gaus mean : 28.4682
Gaus Width: 4.3210
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

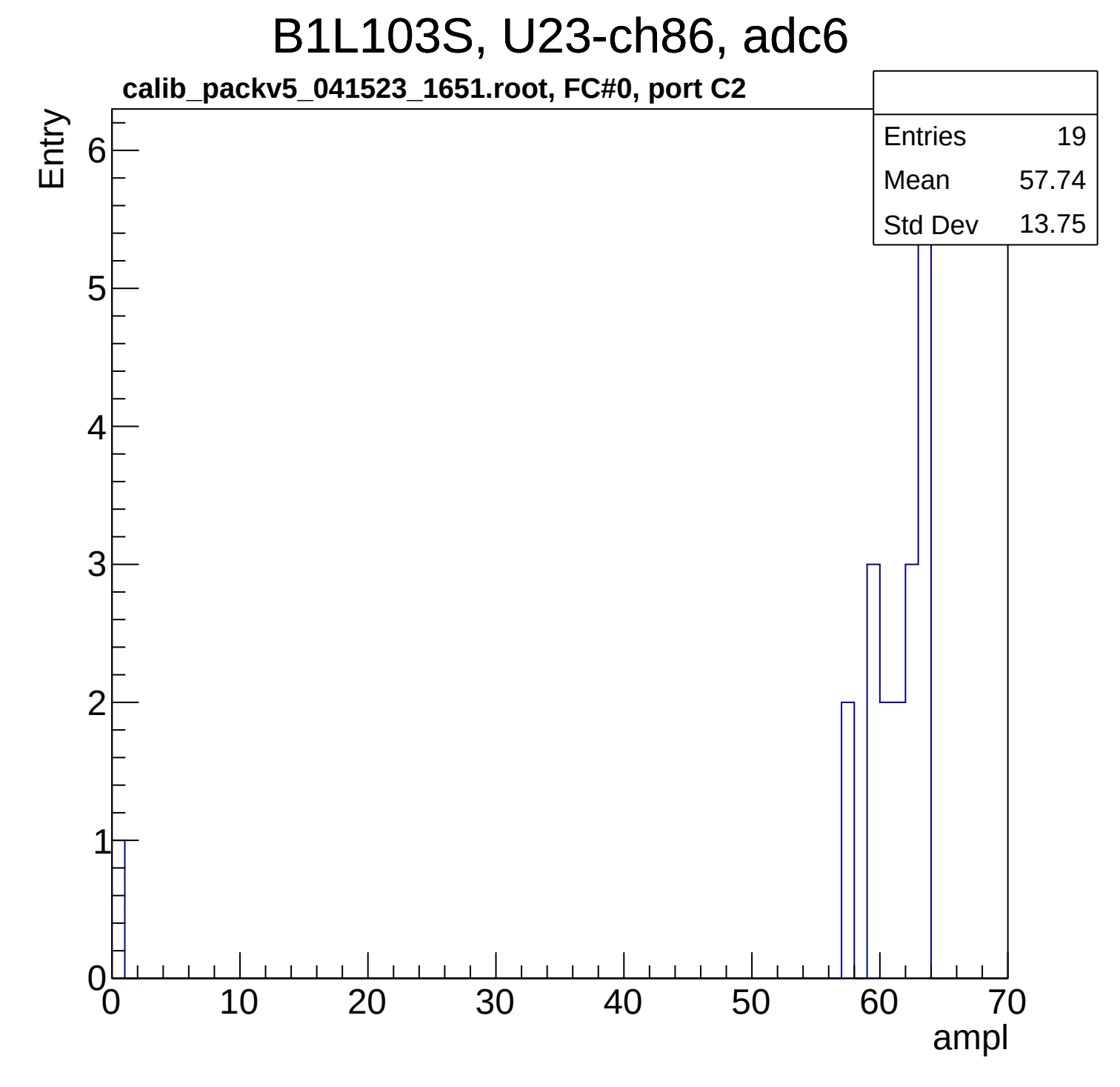

B1L103S, U23-ch86, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 32.06
Std Dev 7.763
Gaus mean : 34.2406
Gaus Width: 4.2770
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

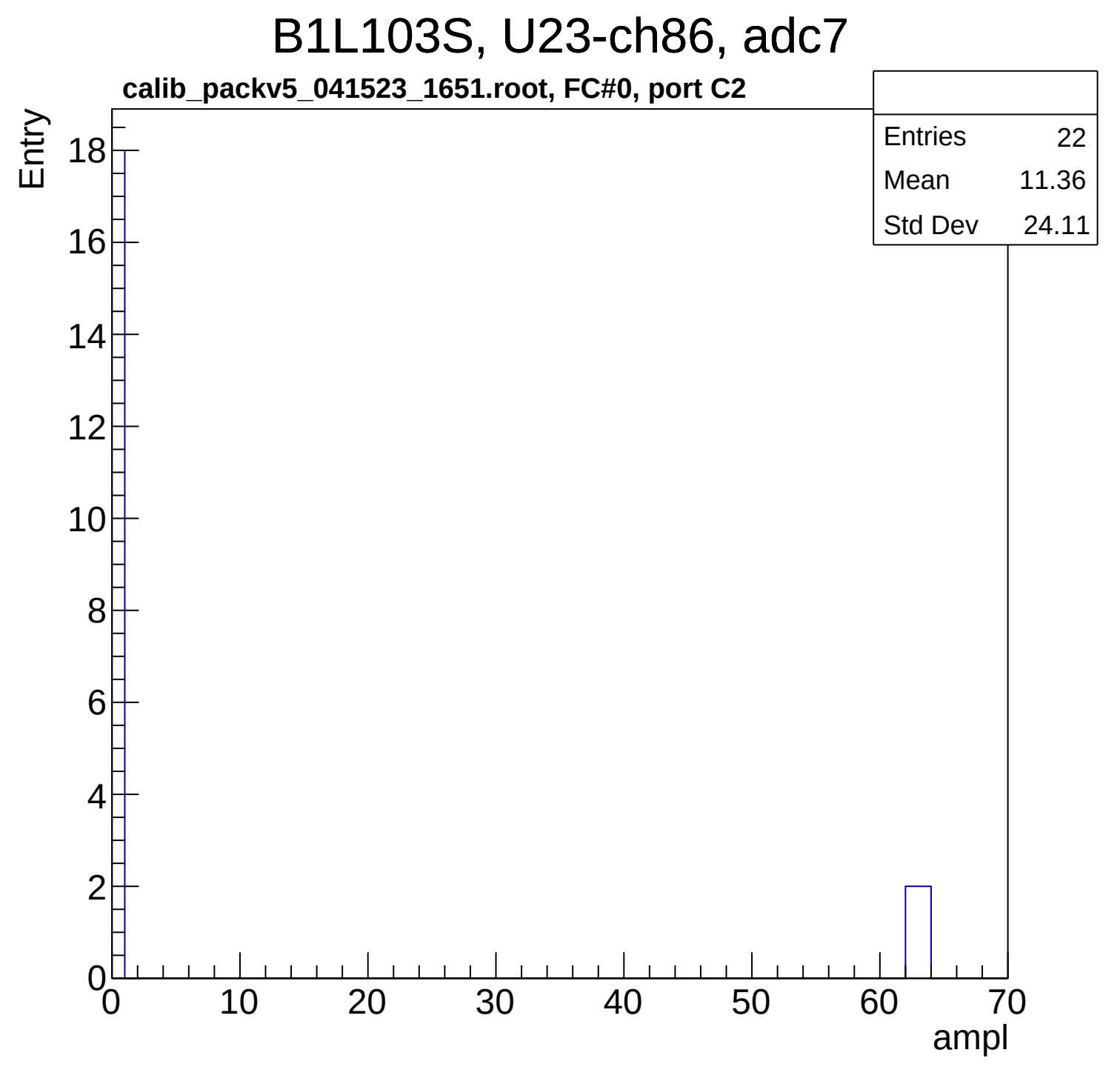

B1L103S, U23-ch86, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 34.64
Std Dev 14.29
Gaus mean : 40.5855
Gaus Width: 4.2650
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

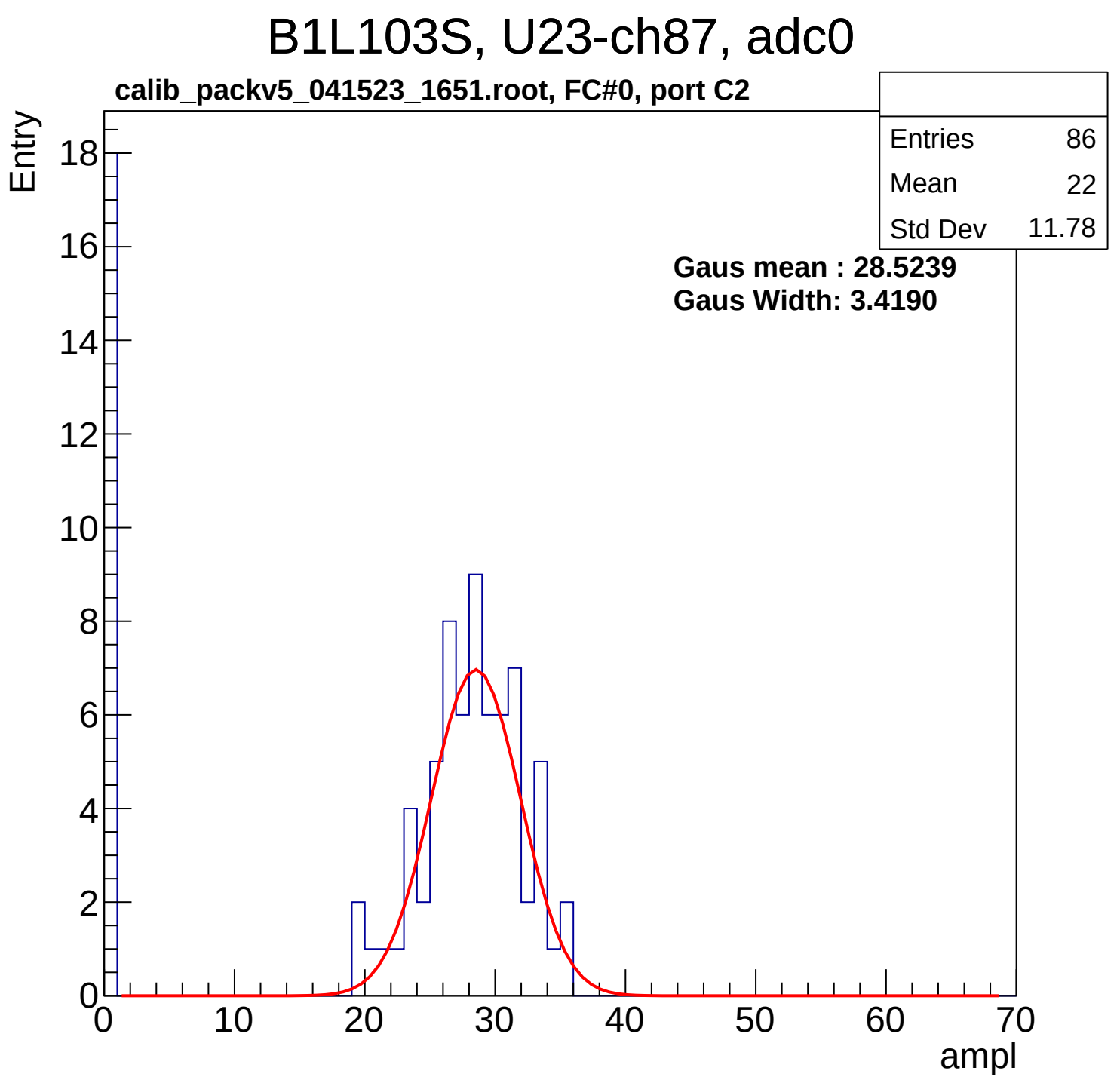

B1L103S, U23-ch86, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 45.33
Std Dev 7.143
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

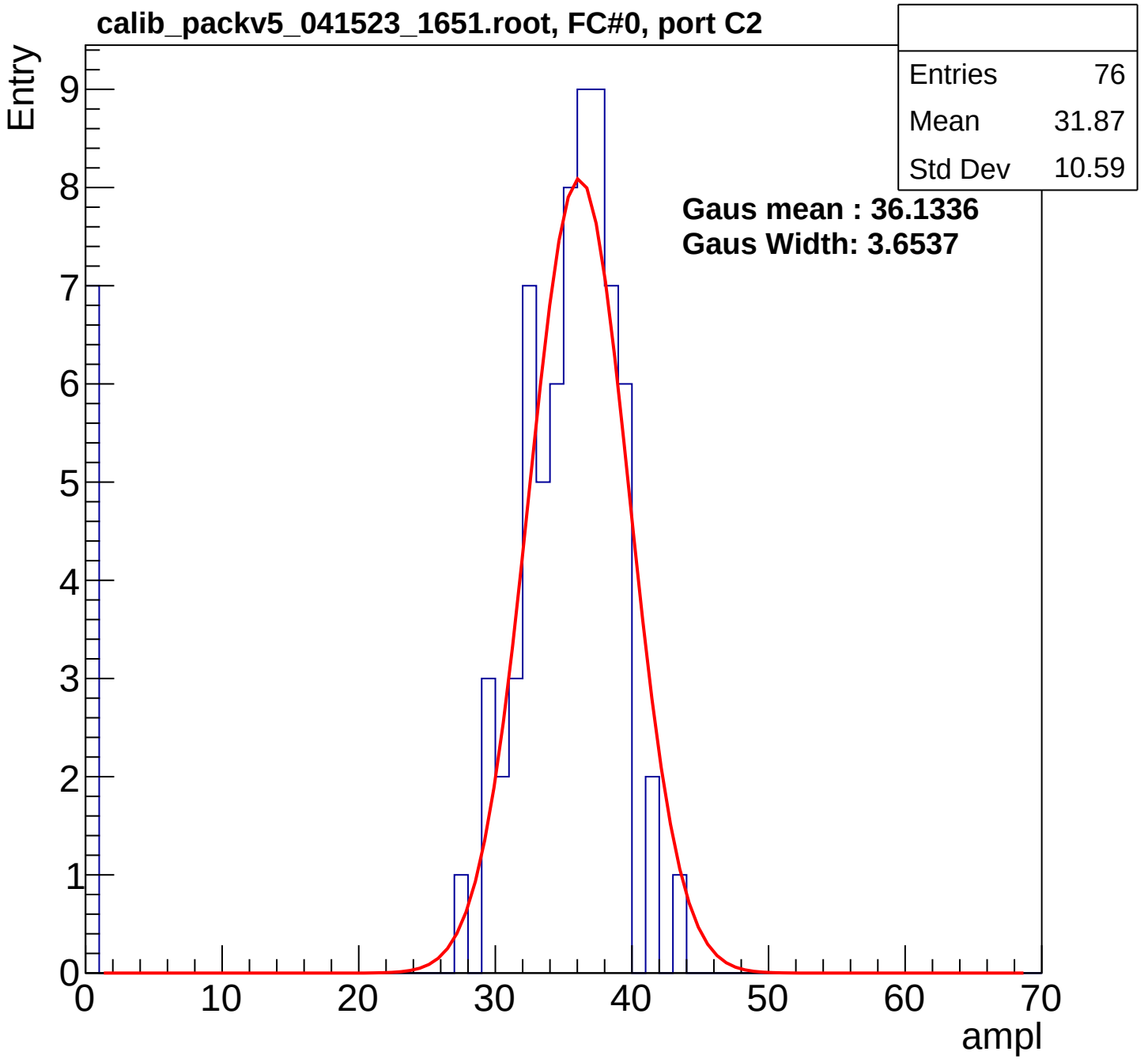

B1L103S, U23-ch86, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 51.98
Std Dev 3.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

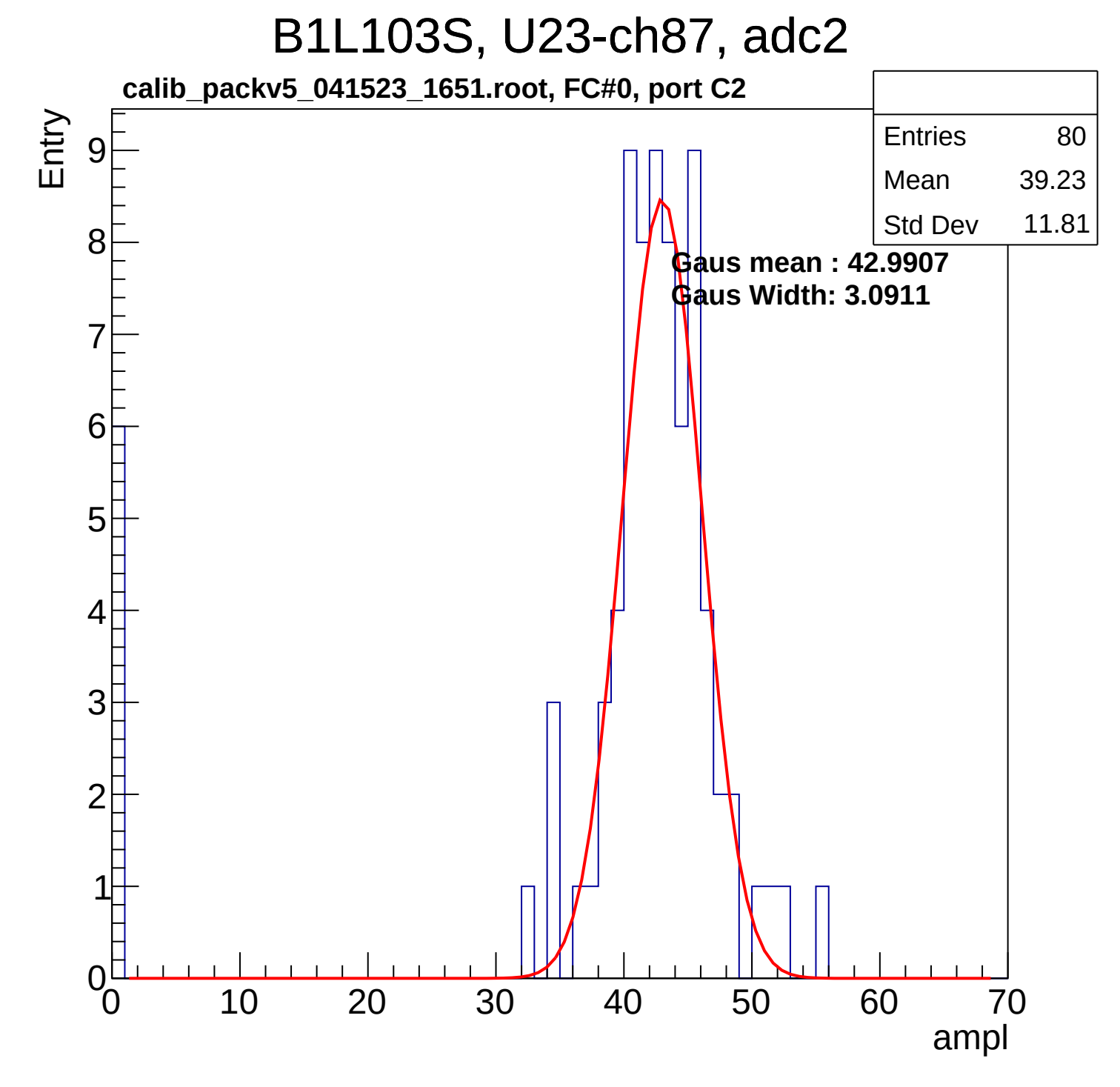

B1L103S, U23-ch86, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 58.05
Std Dev 2.906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

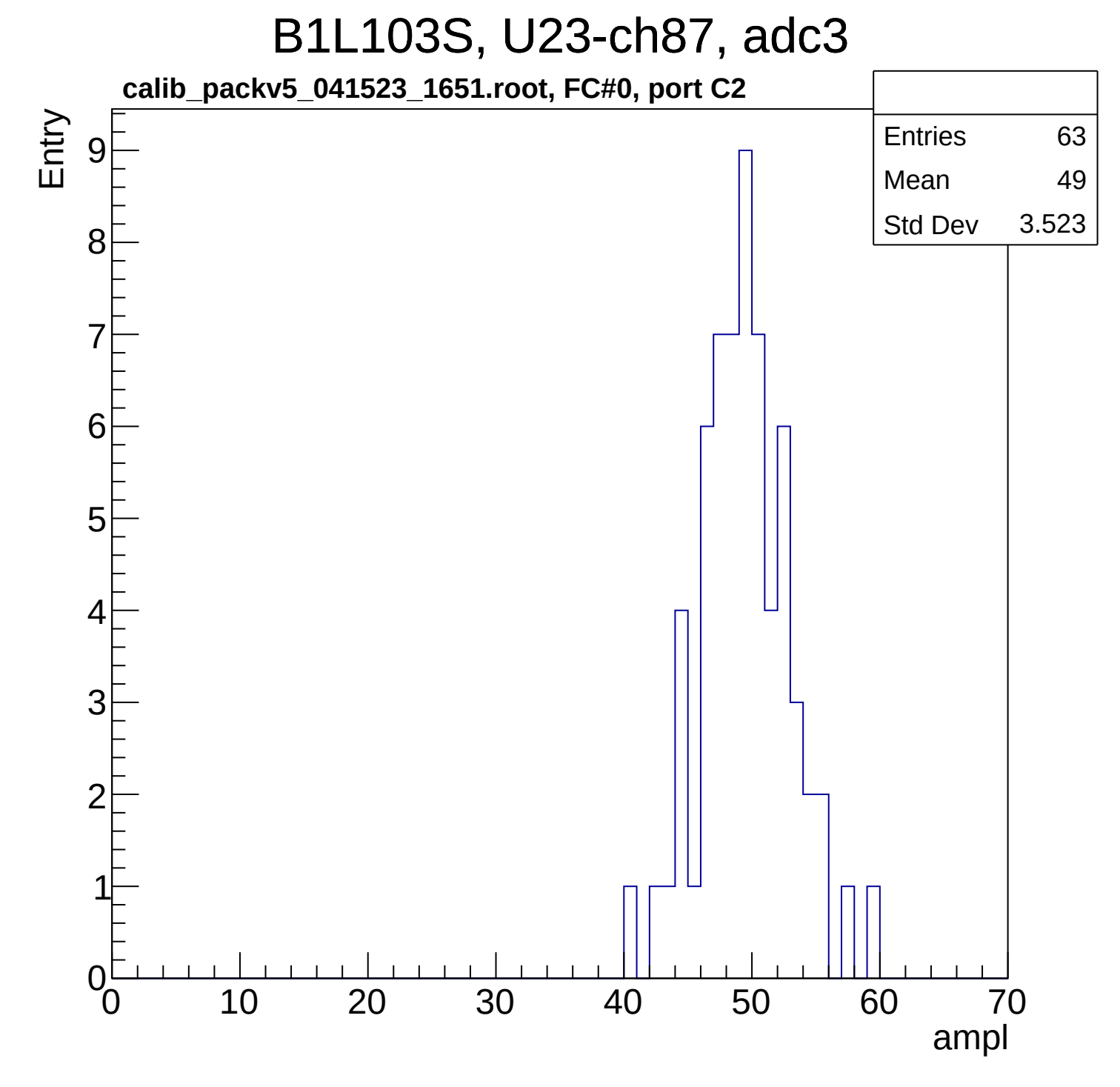

B1L103S, U23-ch86, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 57.74
Std Dev 13.75
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

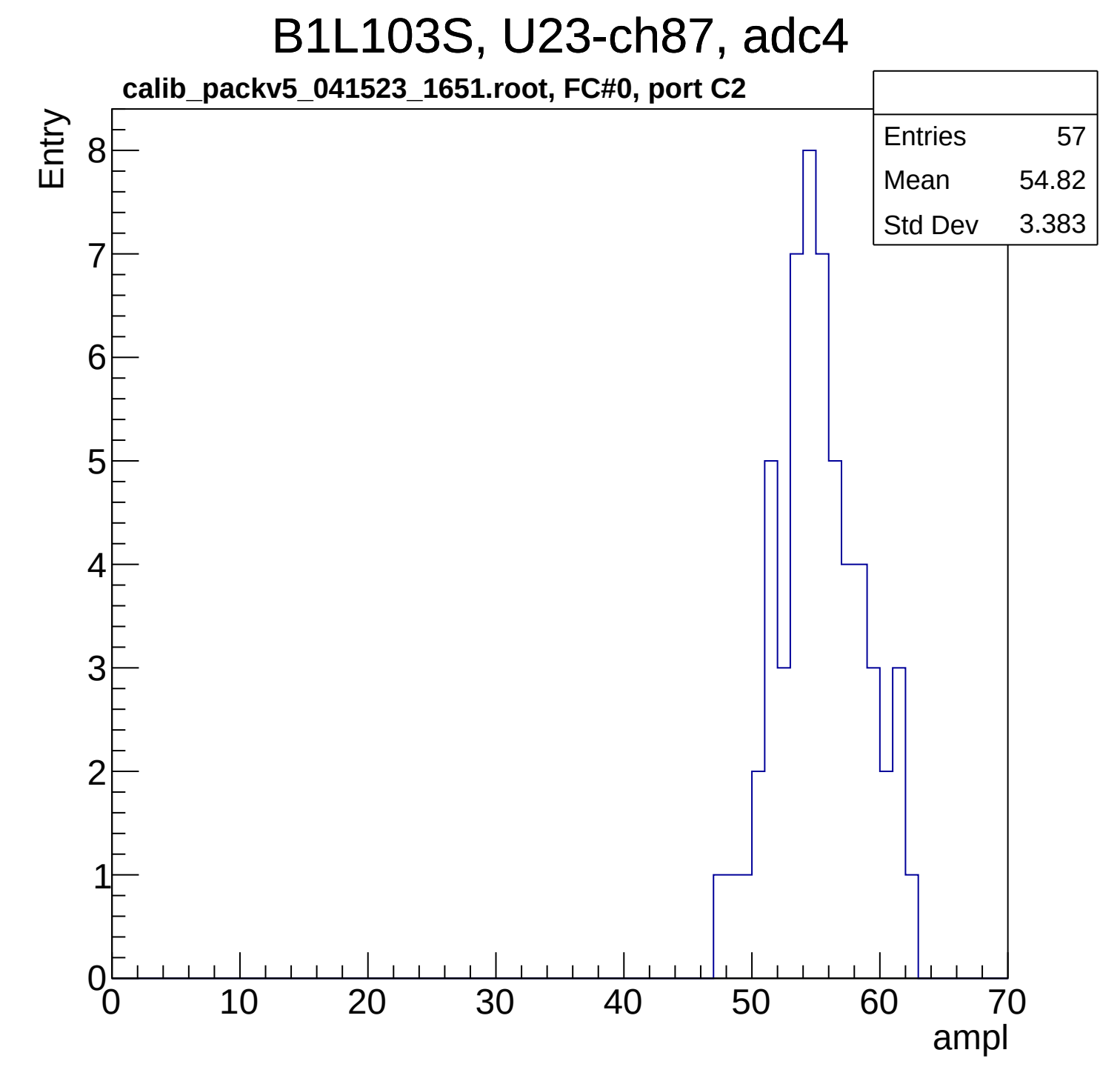

B1L103S, U23-ch86, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 22
Mean 11.36
Std Dev 24.11
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

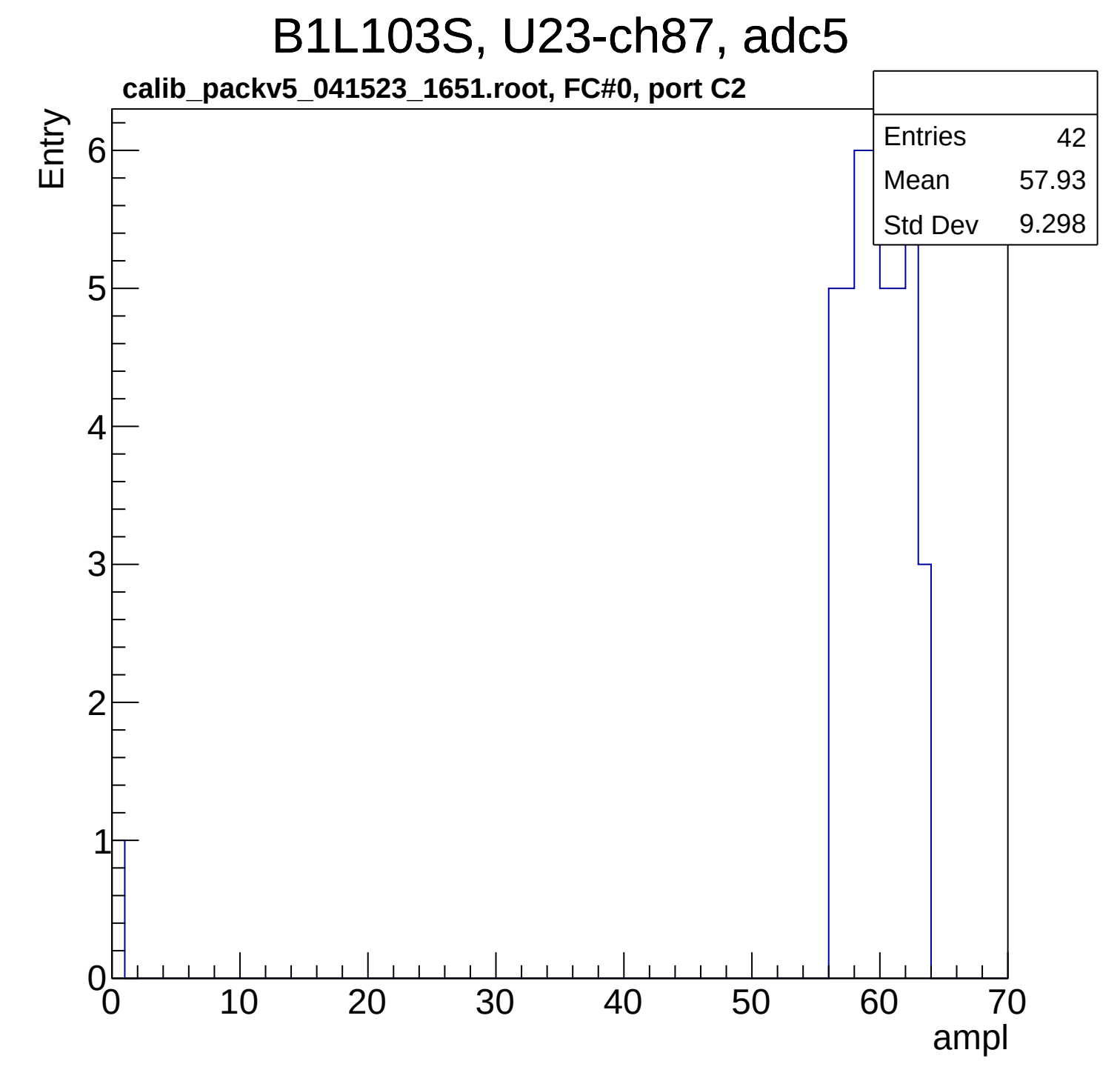

B1L103S, U23-ch87, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 22
Std Dev 11.78
Gaus mean : 28.5239
Gaus Width: 3.4190
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

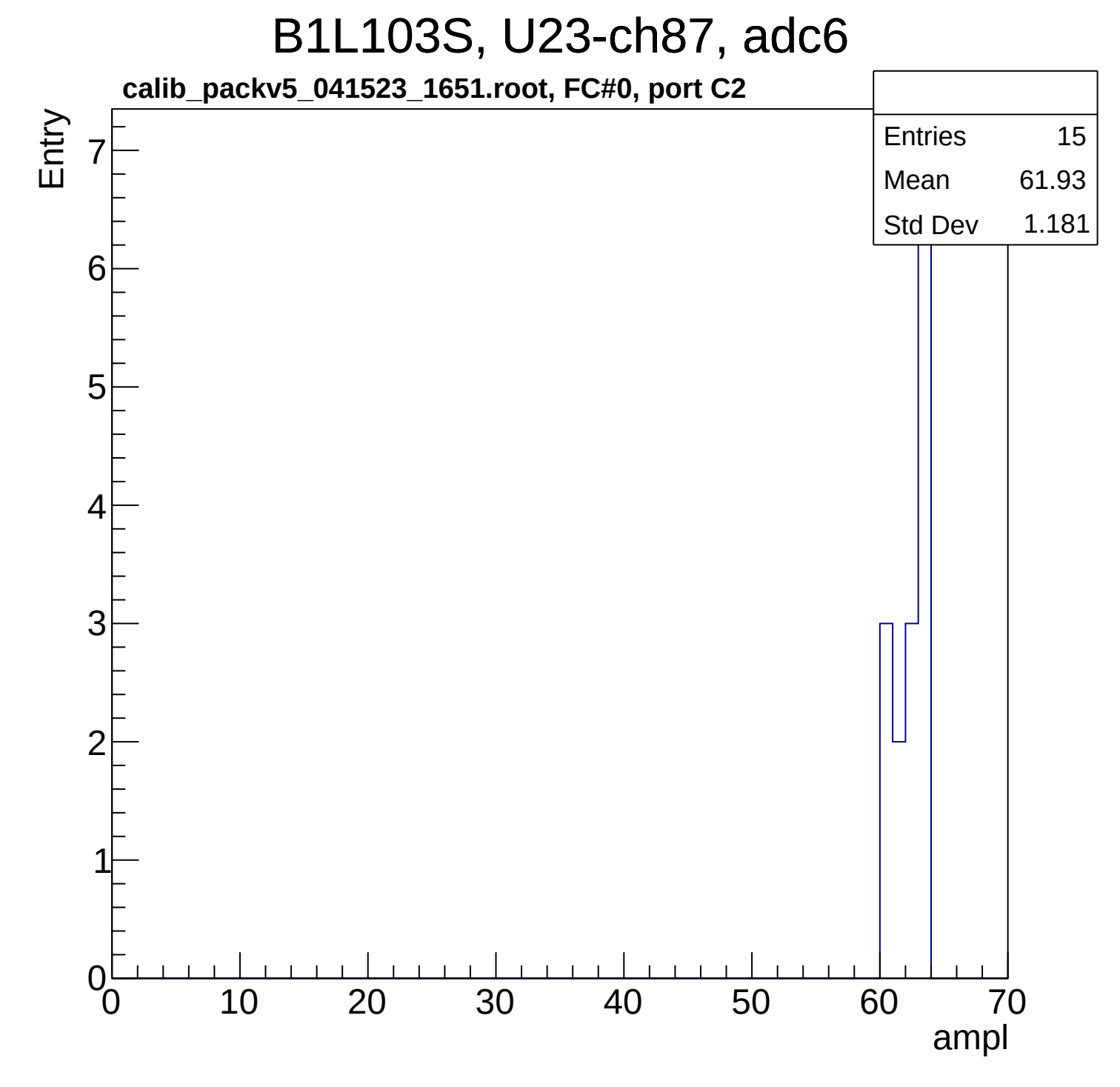

B1L103S, U23-ch87, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 31.87
Std Dev 10.59
Gaus mean : 36.1336
Gaus Width: 3.6537
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

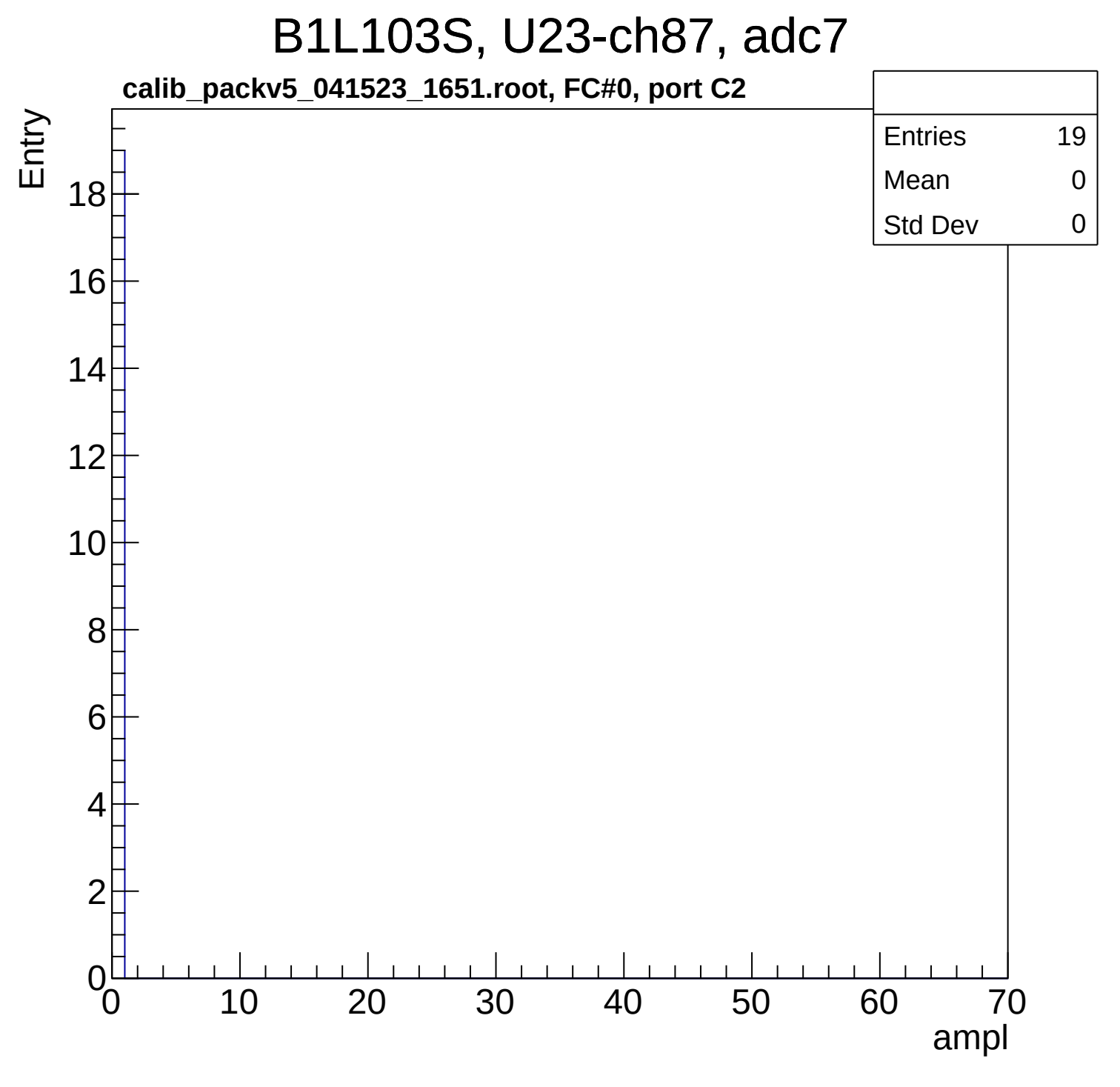

B1L103S, U23-ch87, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 39.23
Std Dev 11.81
Gaus mean : 42.9907
Gaus Width: 3.0911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

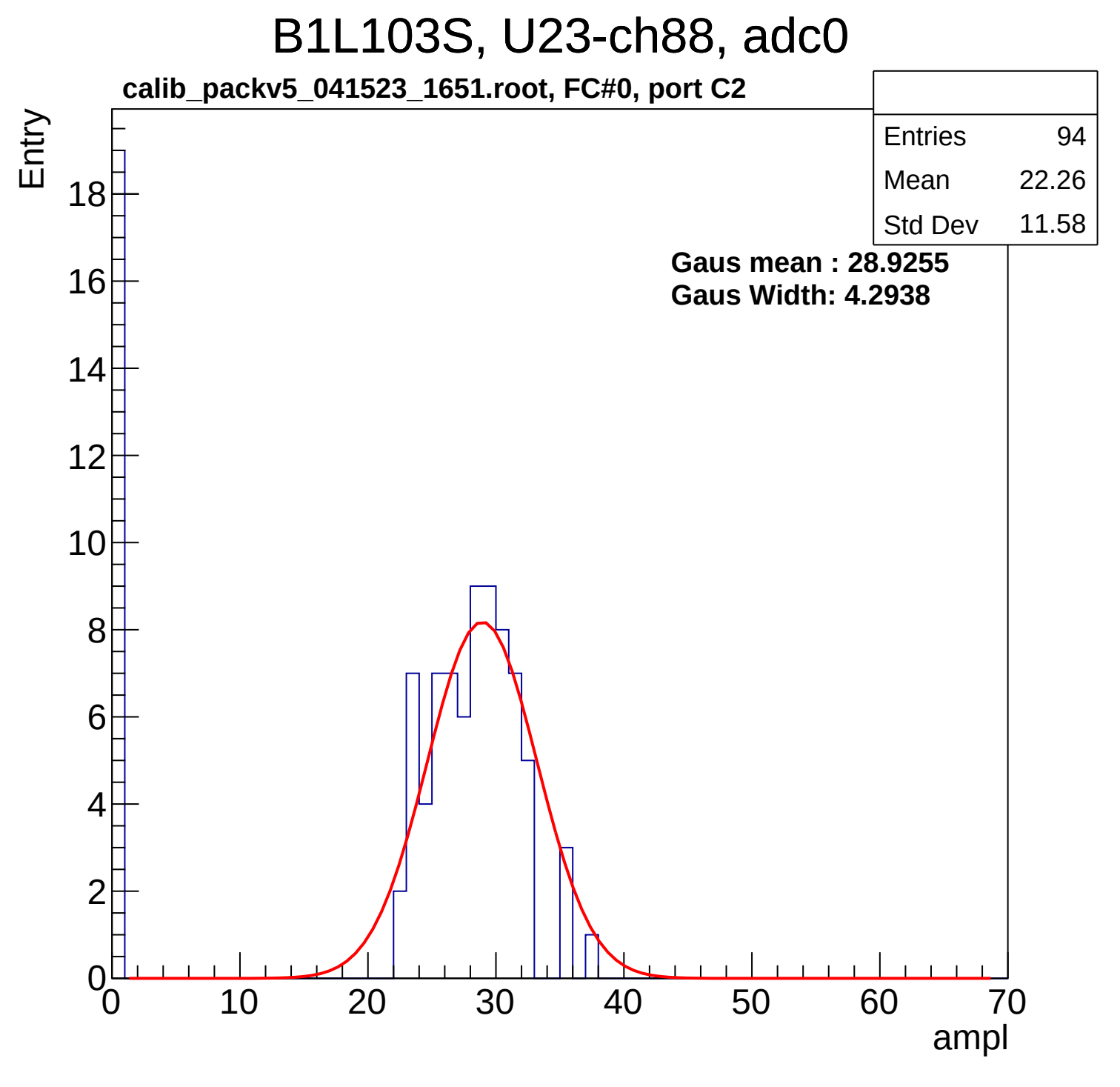

B1L103S, U23-ch87, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 49
Std Dev 3.523
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

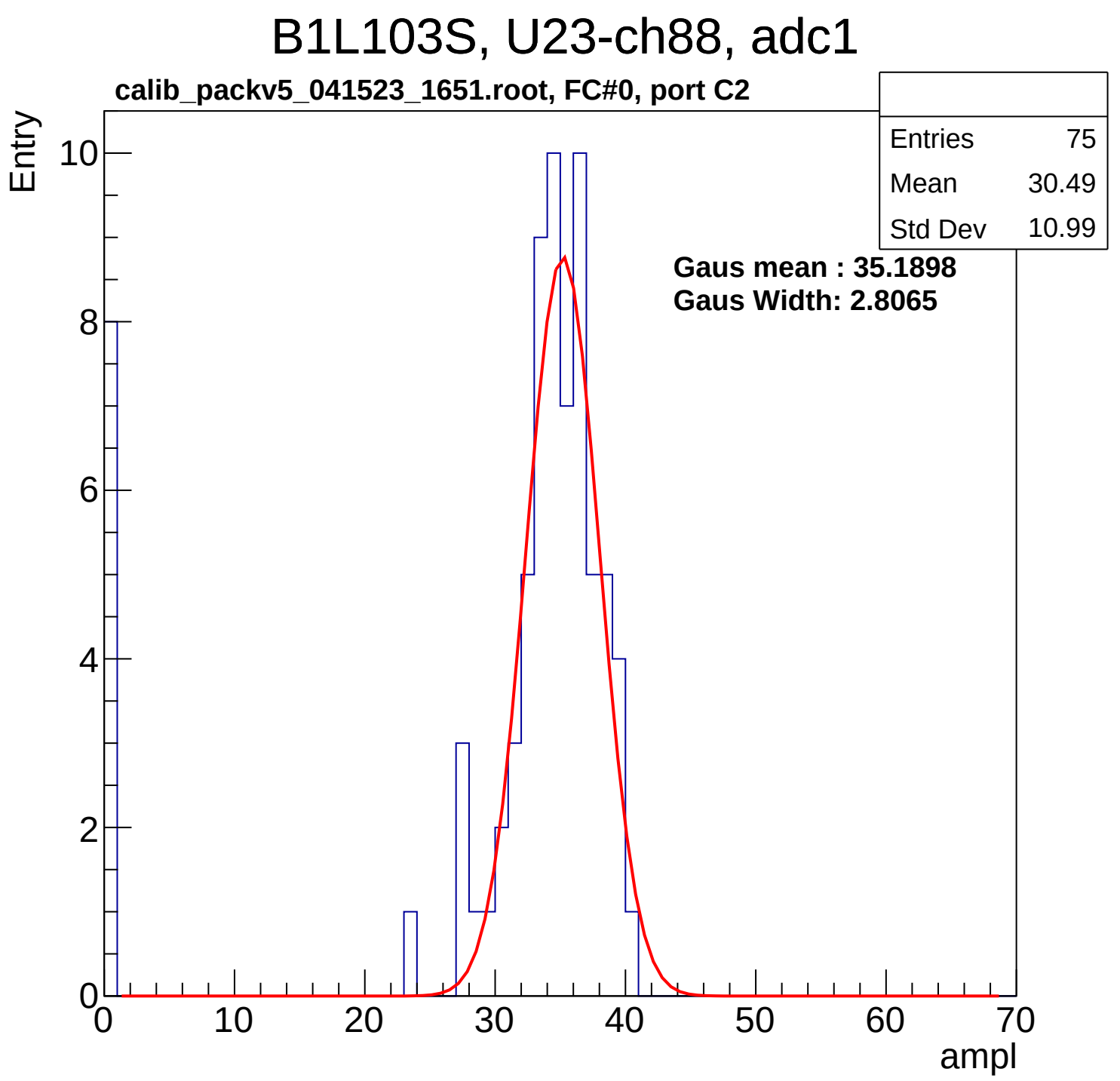

B1L103S, U23-ch87, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 54.82
Std Dev 3.383
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

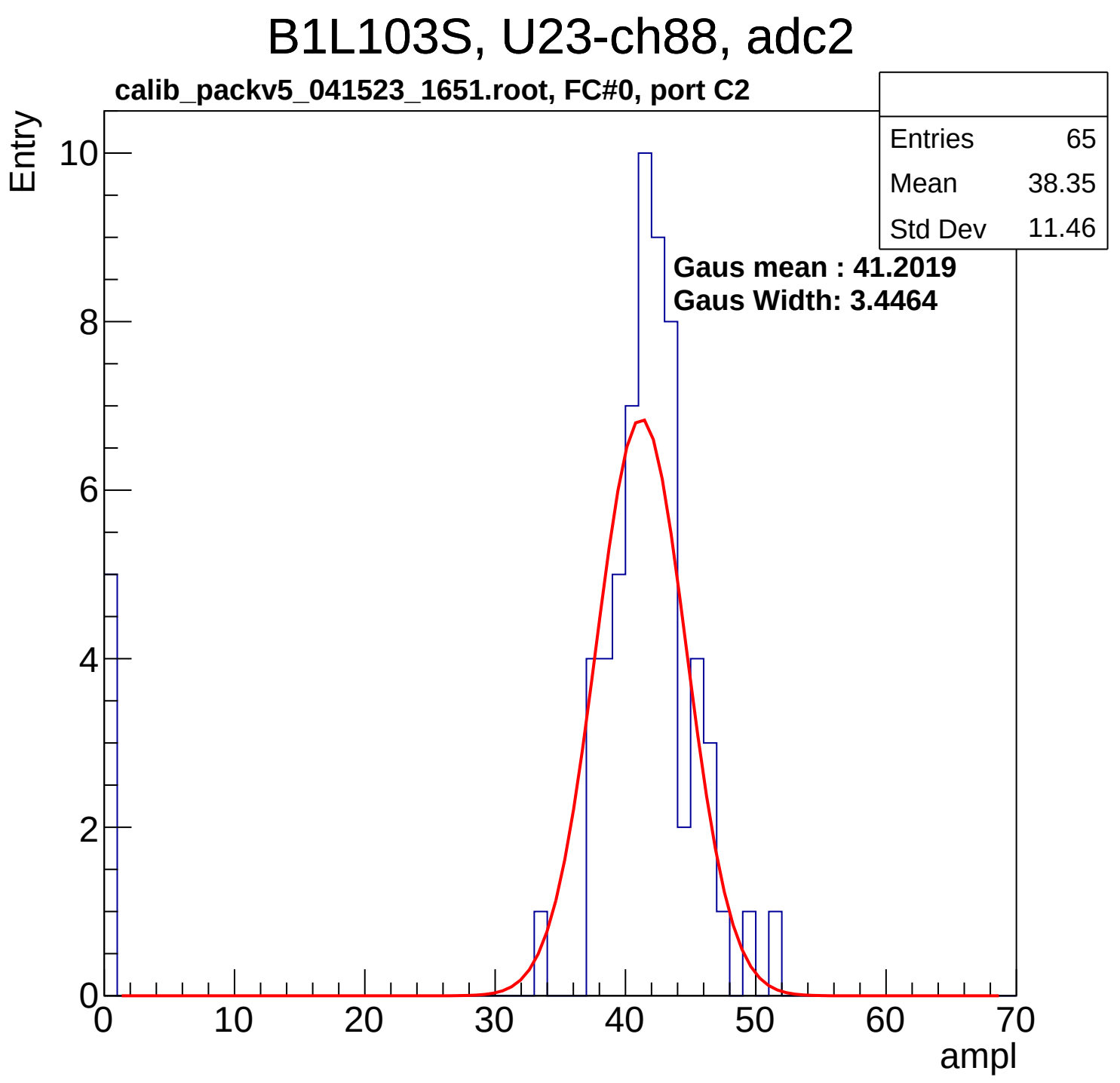

B1L103S, U23-ch87, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 42
Mean 57.93
Std Dev 9.298
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

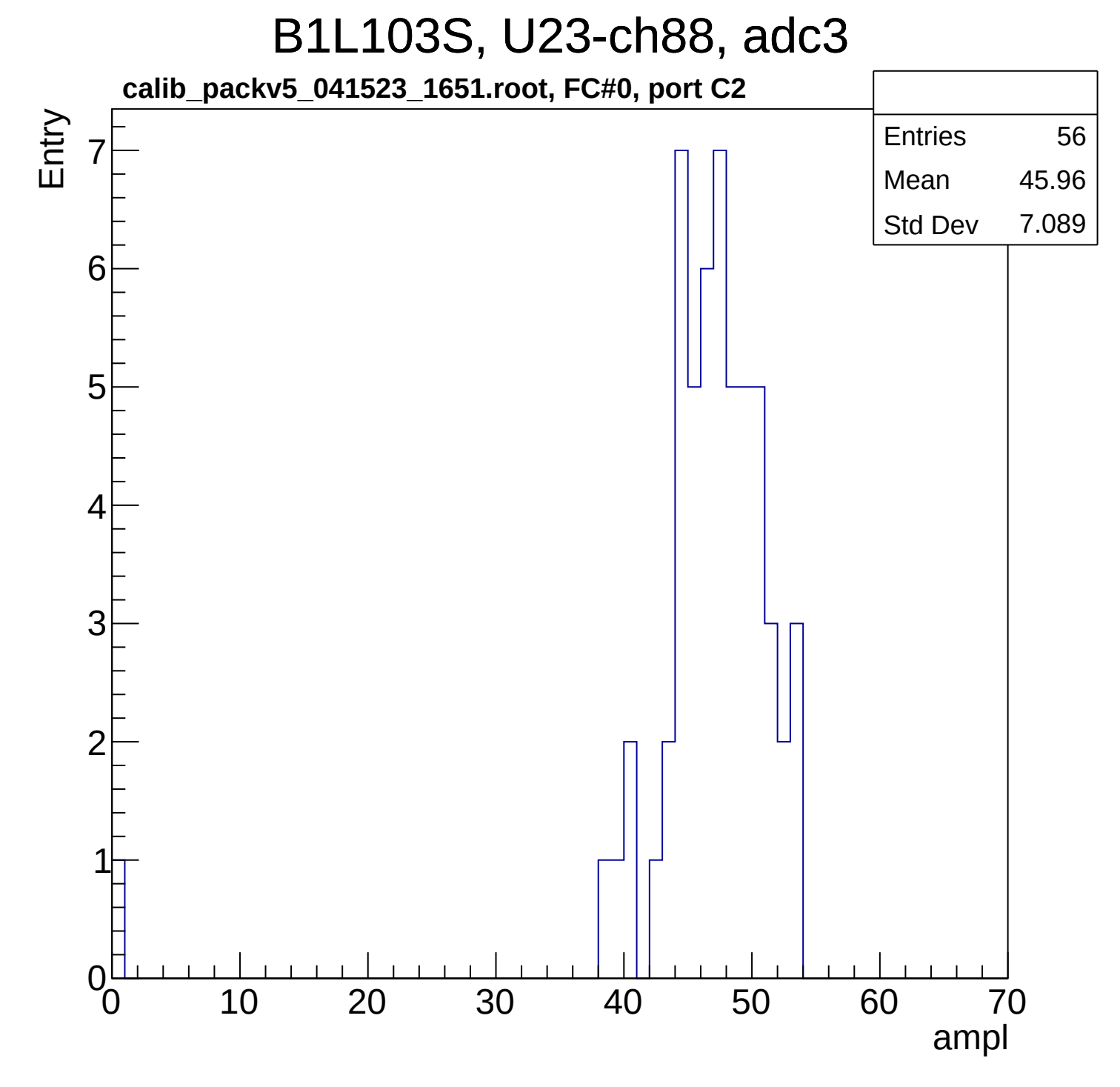

B1L103S, U23-ch87, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 15
Mean 61.93
Std Dev 1.181
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

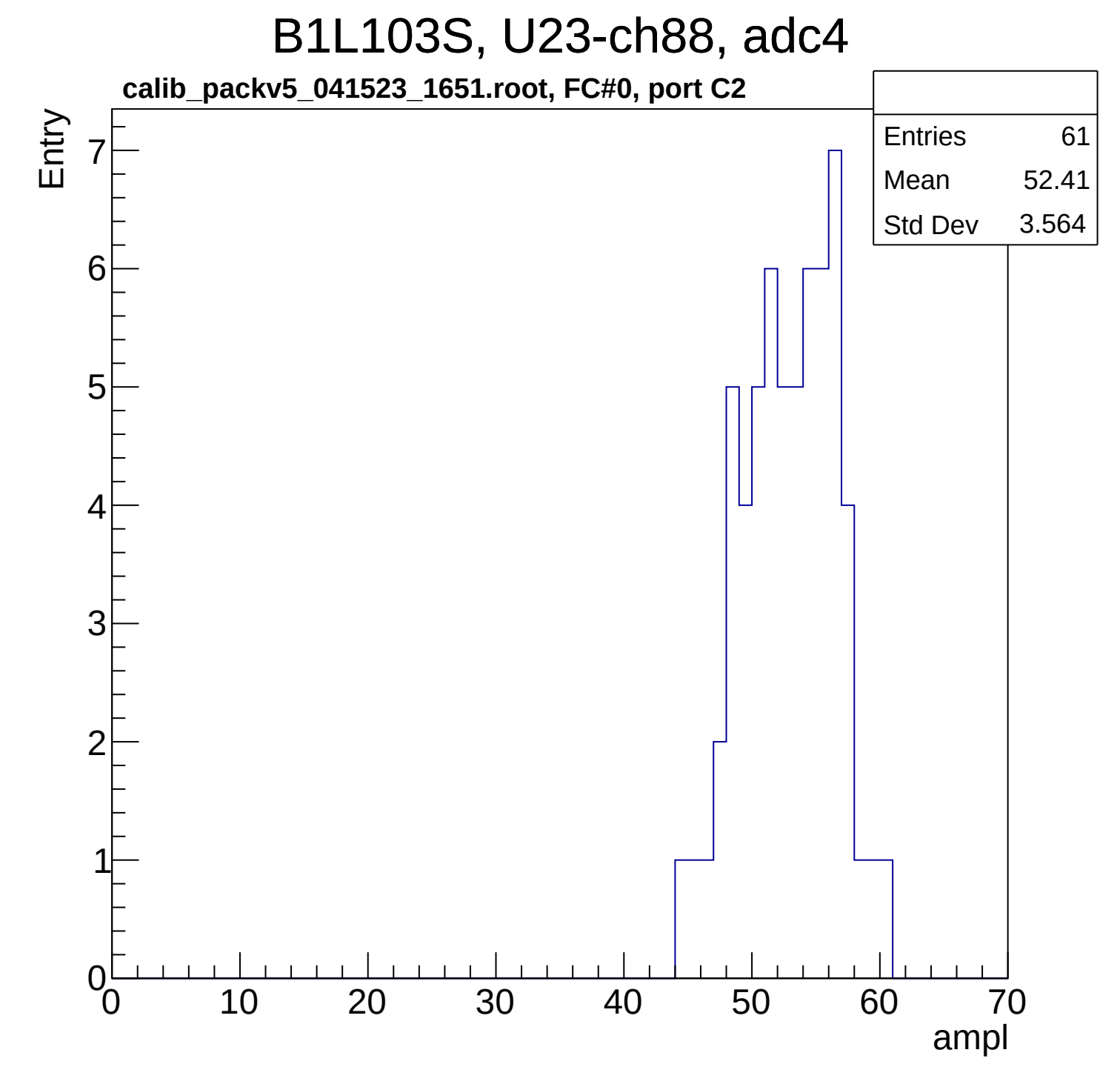

B1L103S, U23-ch87, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

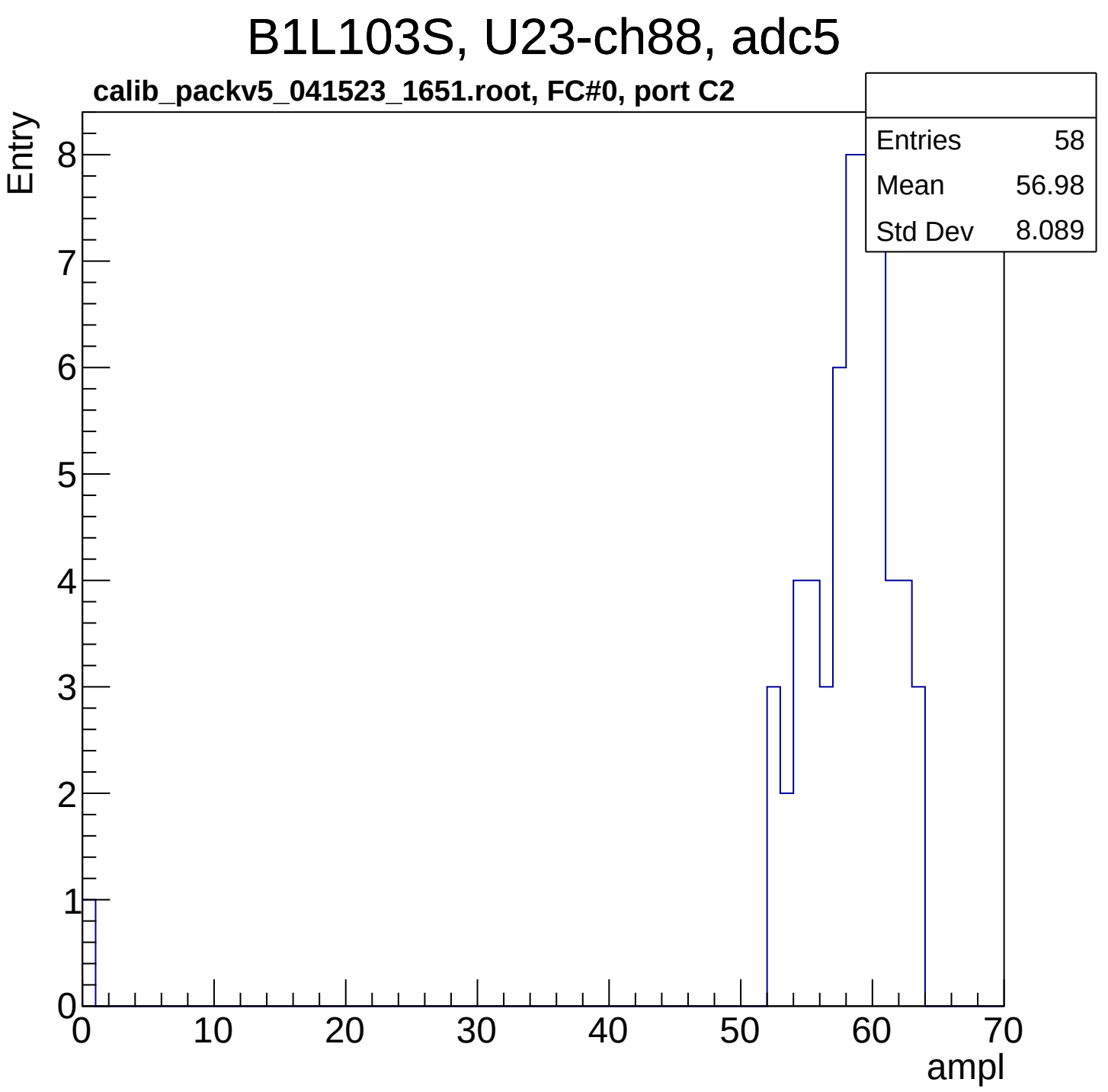

B1L103S, U23-ch88, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 94
Mean 22.26
Std Dev 11.58
Gaus mean : 28.9255
Gaus Width: 4.2938
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

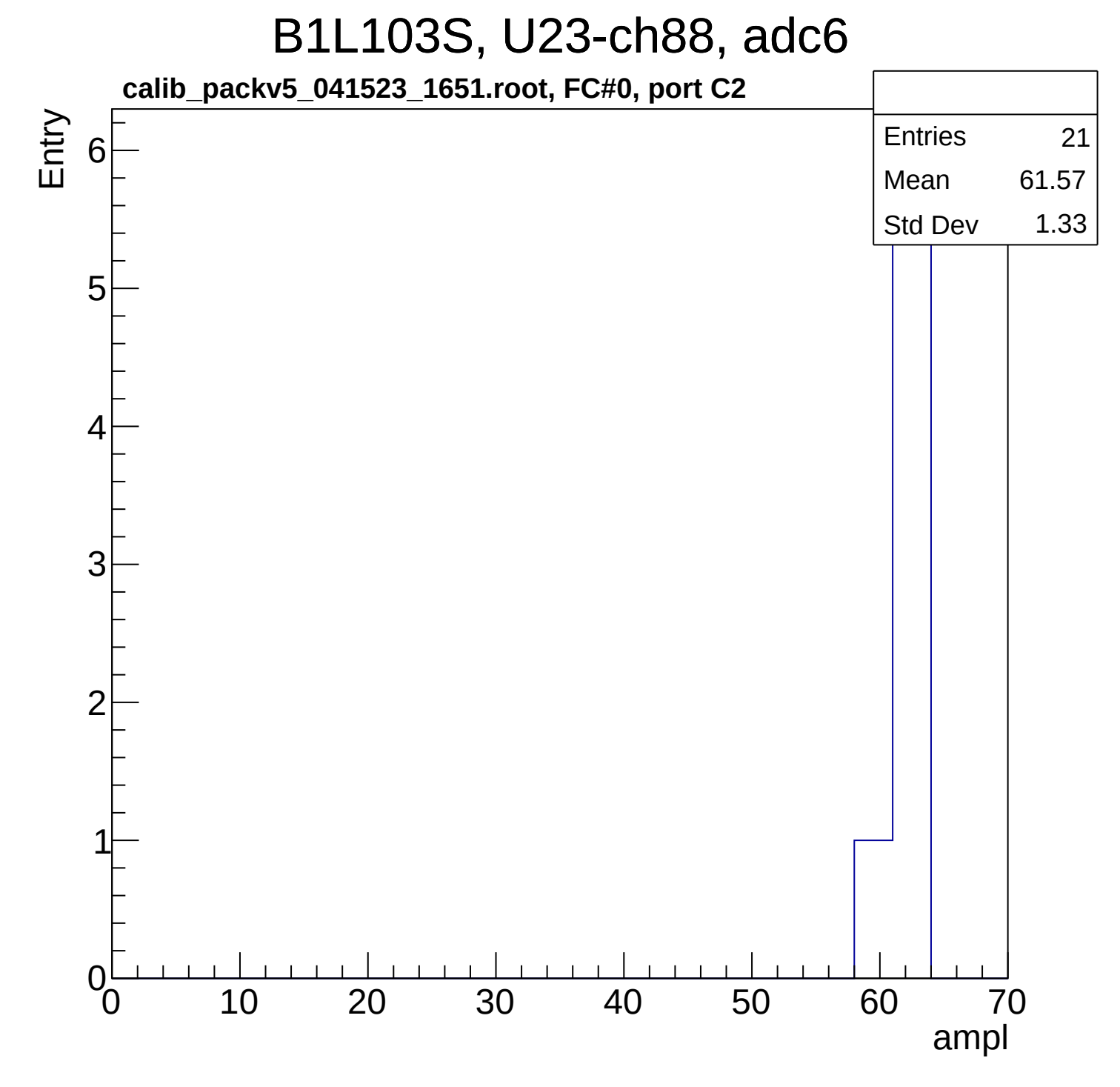

B1L103S, U23-ch88, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 30.49
Std Dev 10.99
Gaus mean : 35.1898
Gaus Width: 2.8065
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

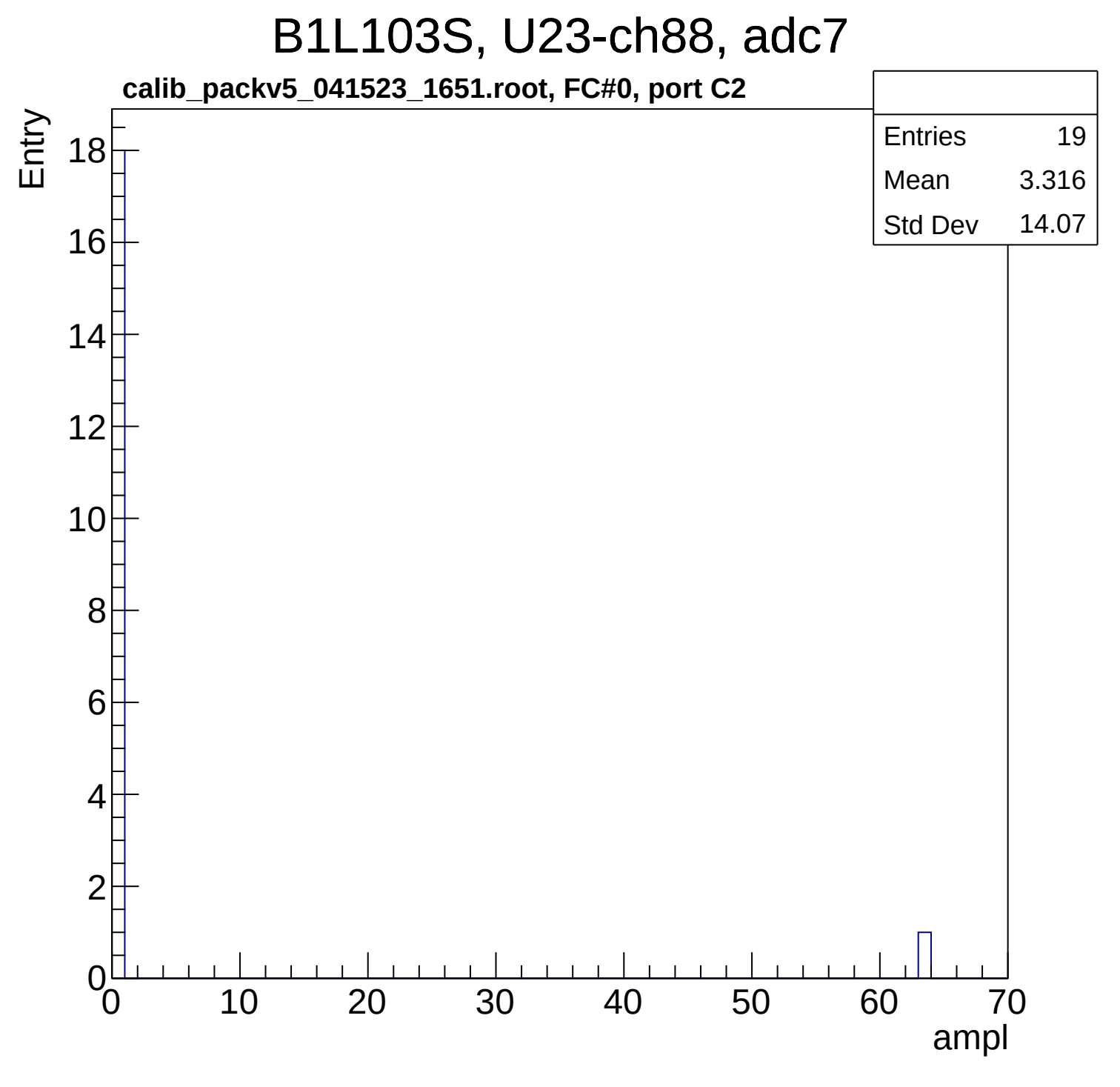

B1L103S, U23-ch88, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 38.35
Std Dev 11.46
Gaus mean : 41.2019
Gaus Width: 3.4464
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

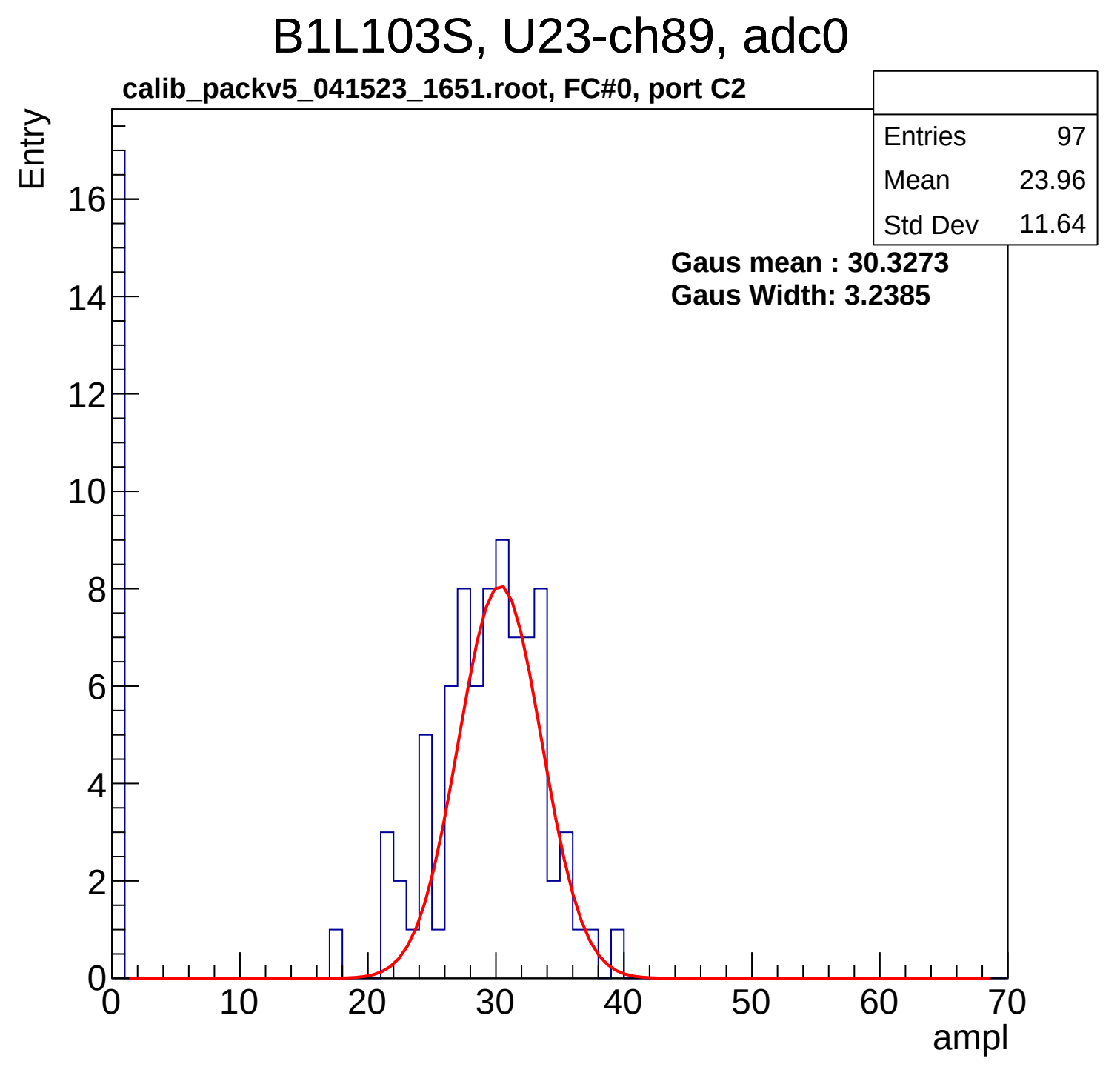

B1L103S, U23-ch88, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 45.96
Std Dev 7.089
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

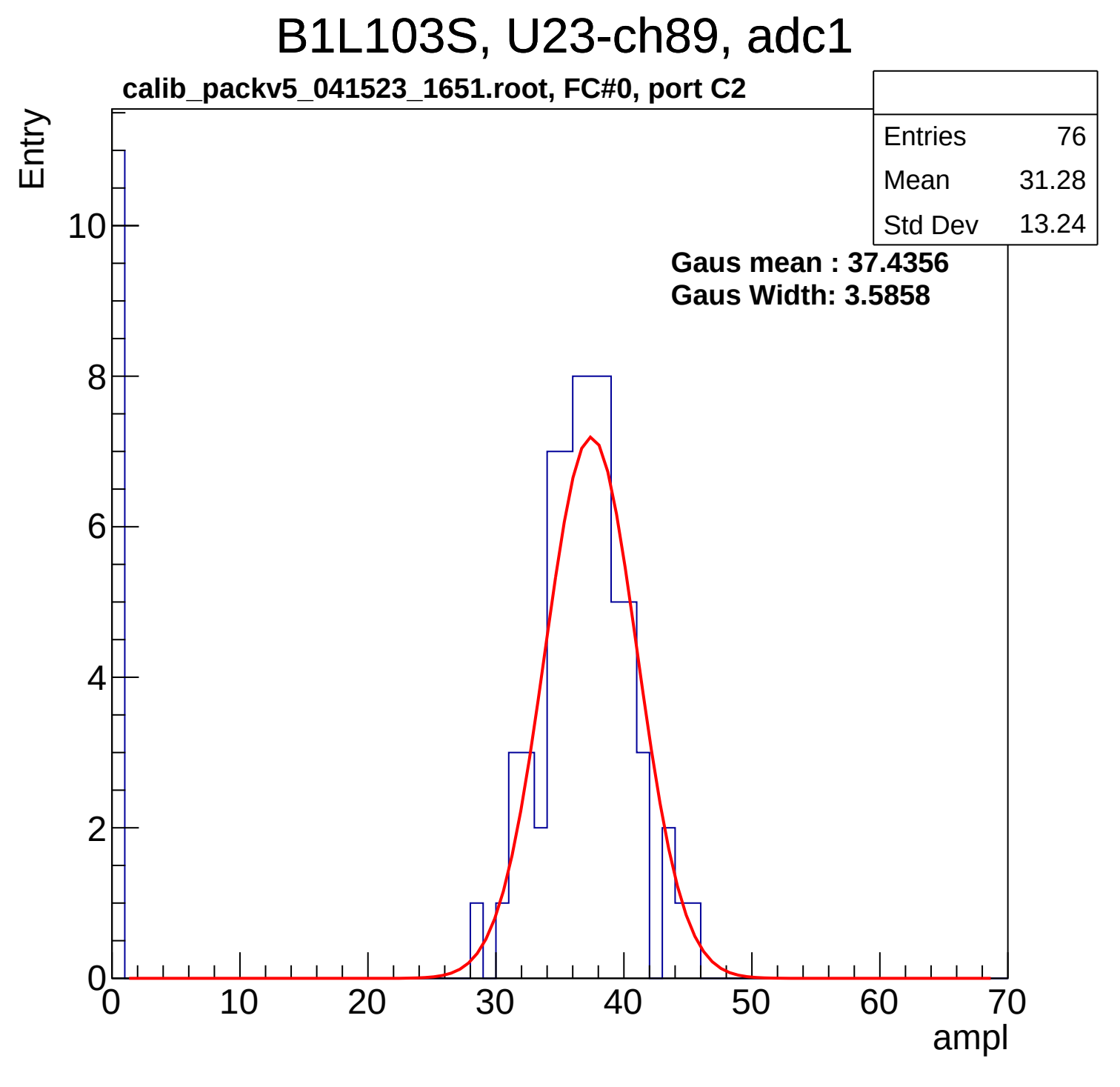

B1L103S, U23-ch88, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 52.41
Std Dev 3.564
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

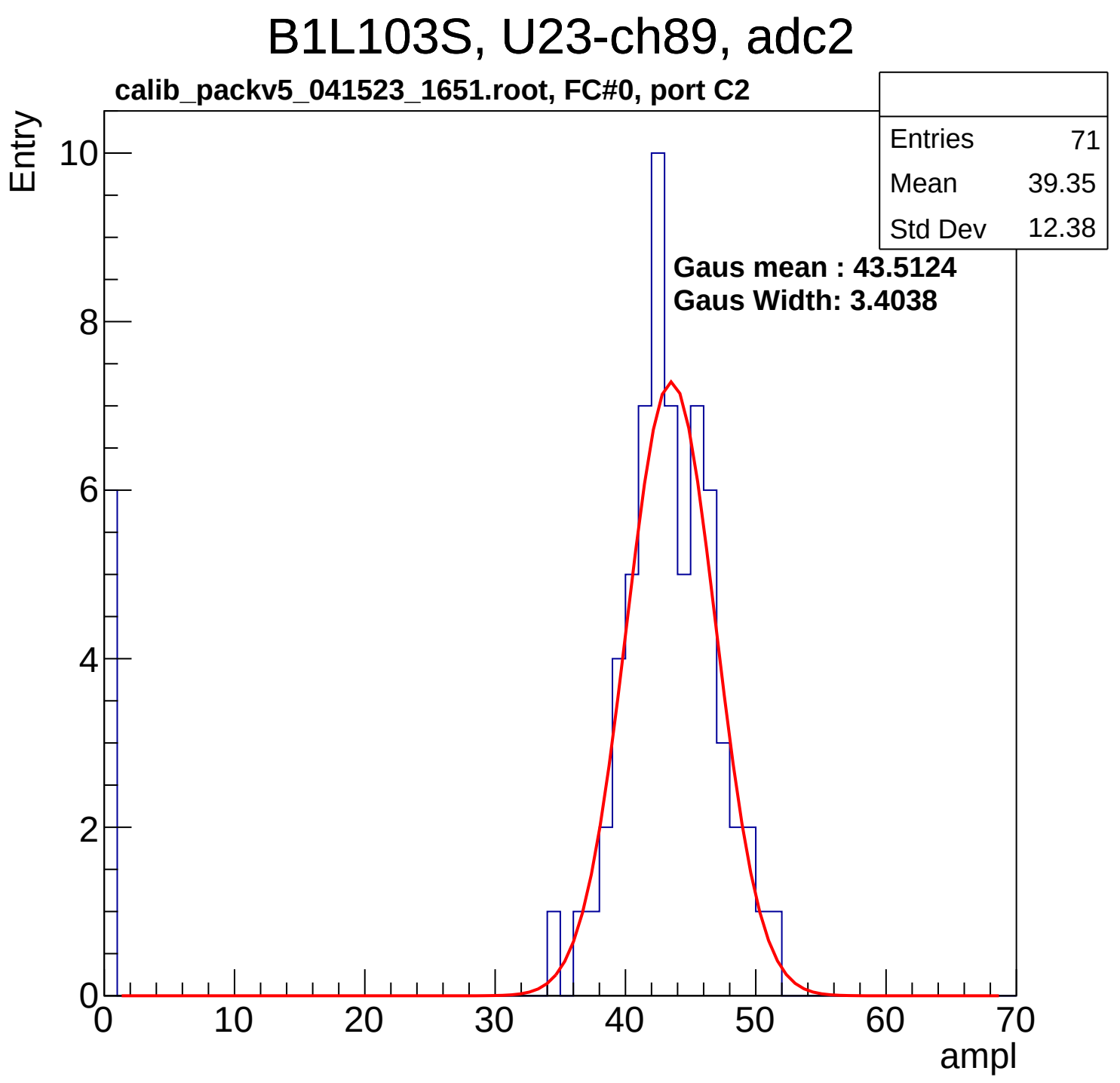

B1L103S, U23-ch88, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 56.98
Std Dev 8.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

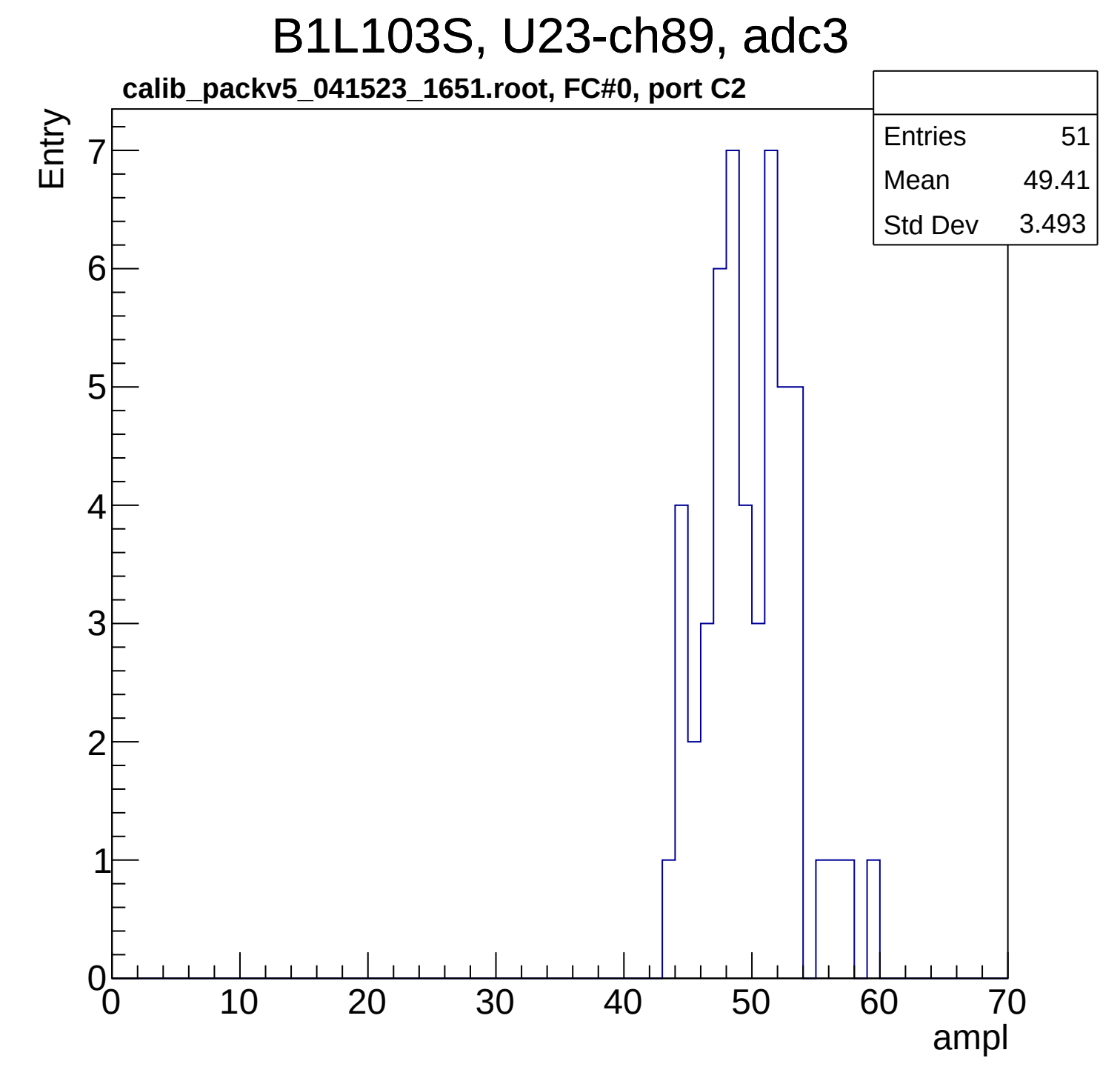

B1L103S, U23-ch88, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 61.57
Std Dev 1.33
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

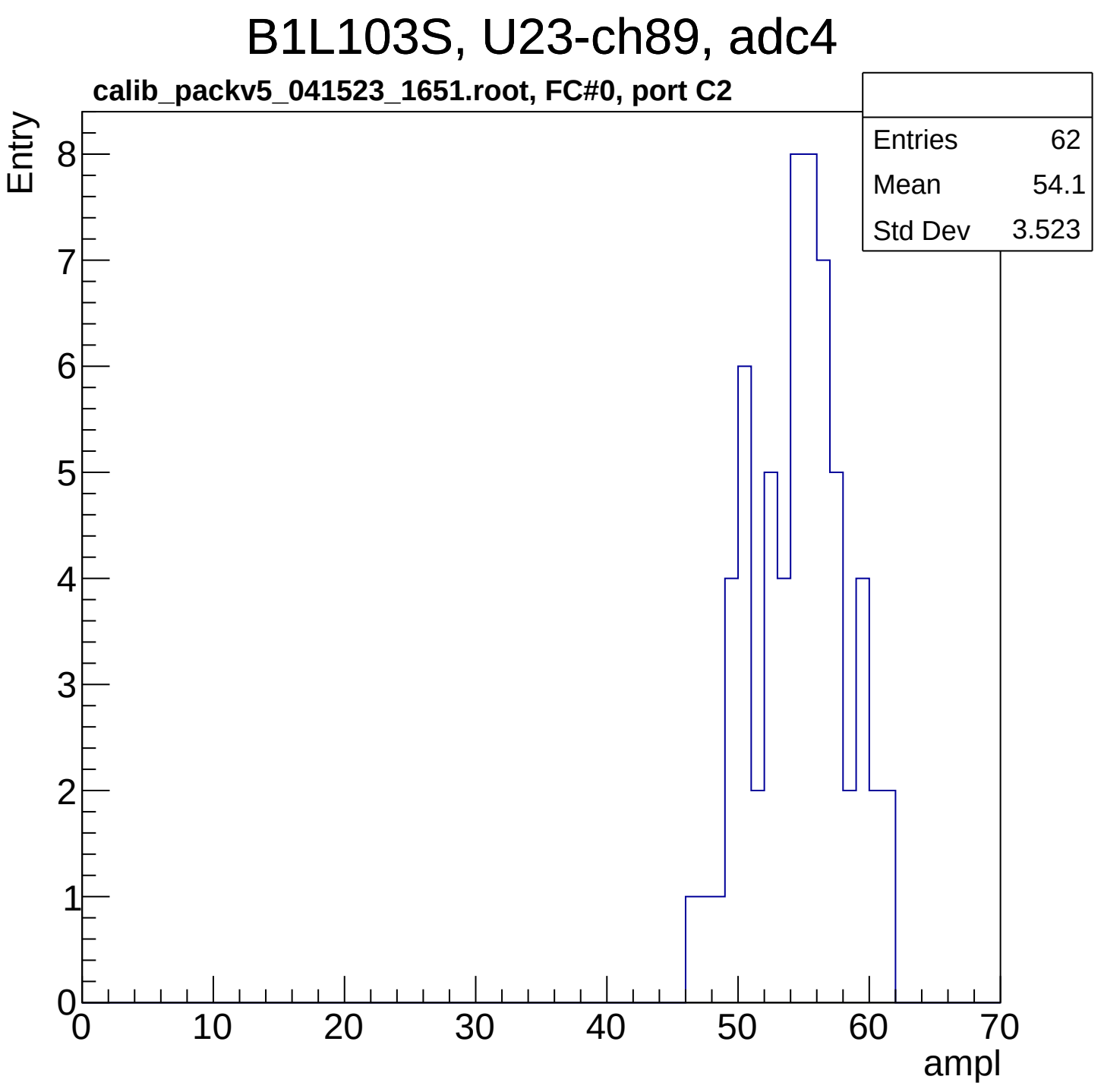

B1L103S, U23-ch88, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 3.316
Std Dev 14.07
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

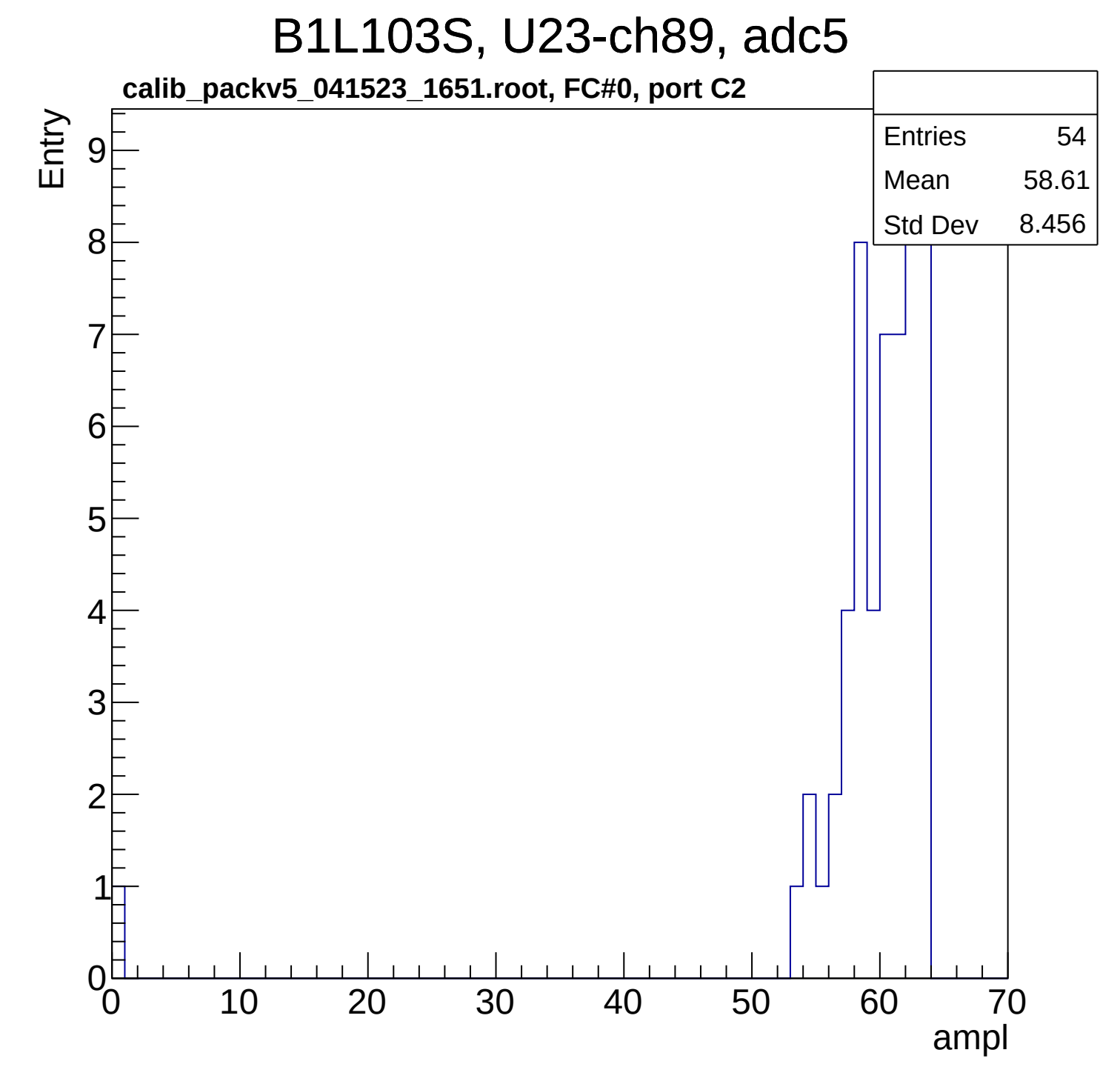

B1L103S, U23-ch89, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 97
Mean 23.96
Std Dev 11.64
Gaus mean : 30.3273
Gaus Width: 3.2385
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

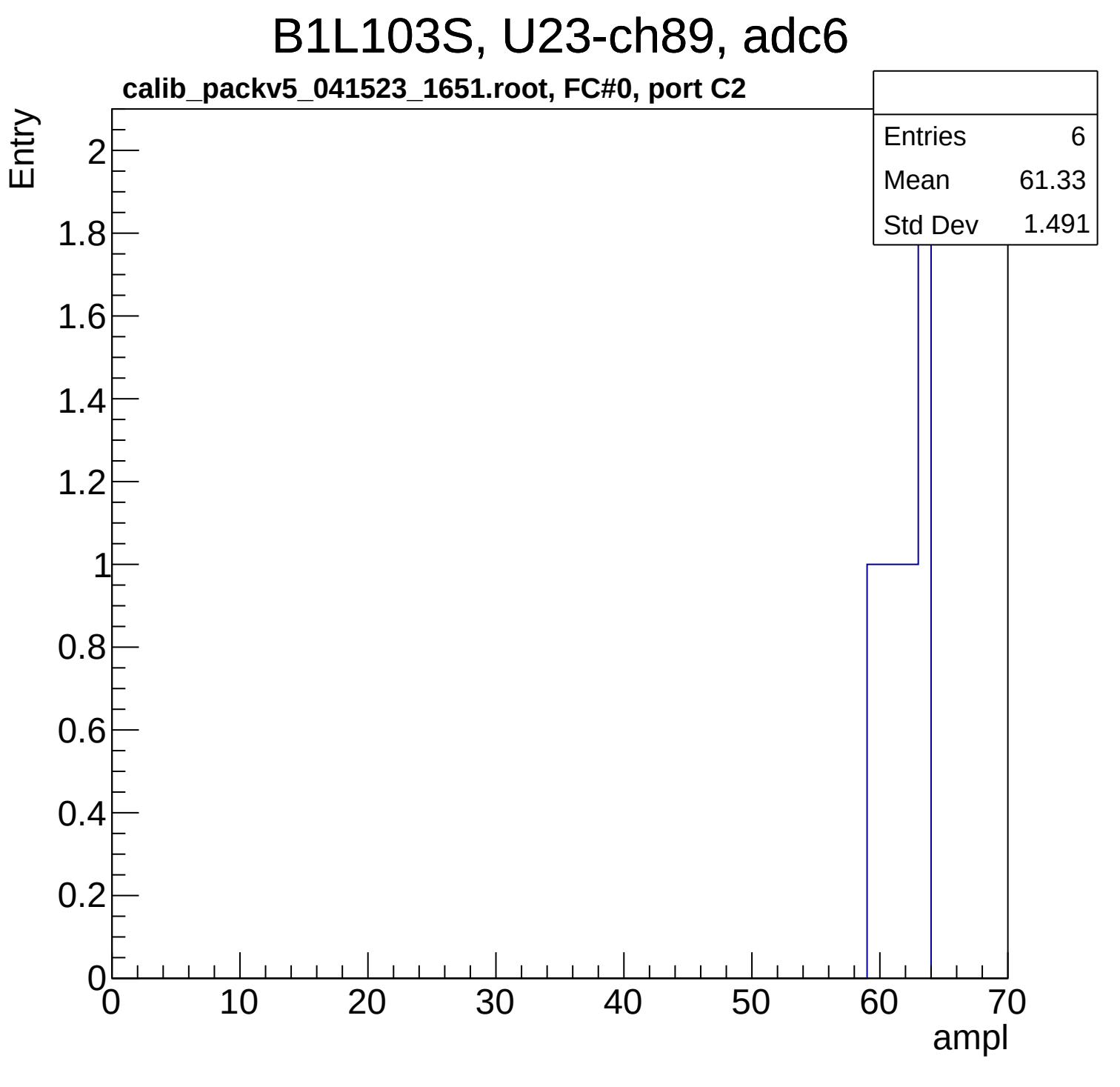

B1L103S, U23-ch89, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 31.28
Std Dev 13.24
Gaus mean : 37.4356
Gaus Width: 3.5858
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

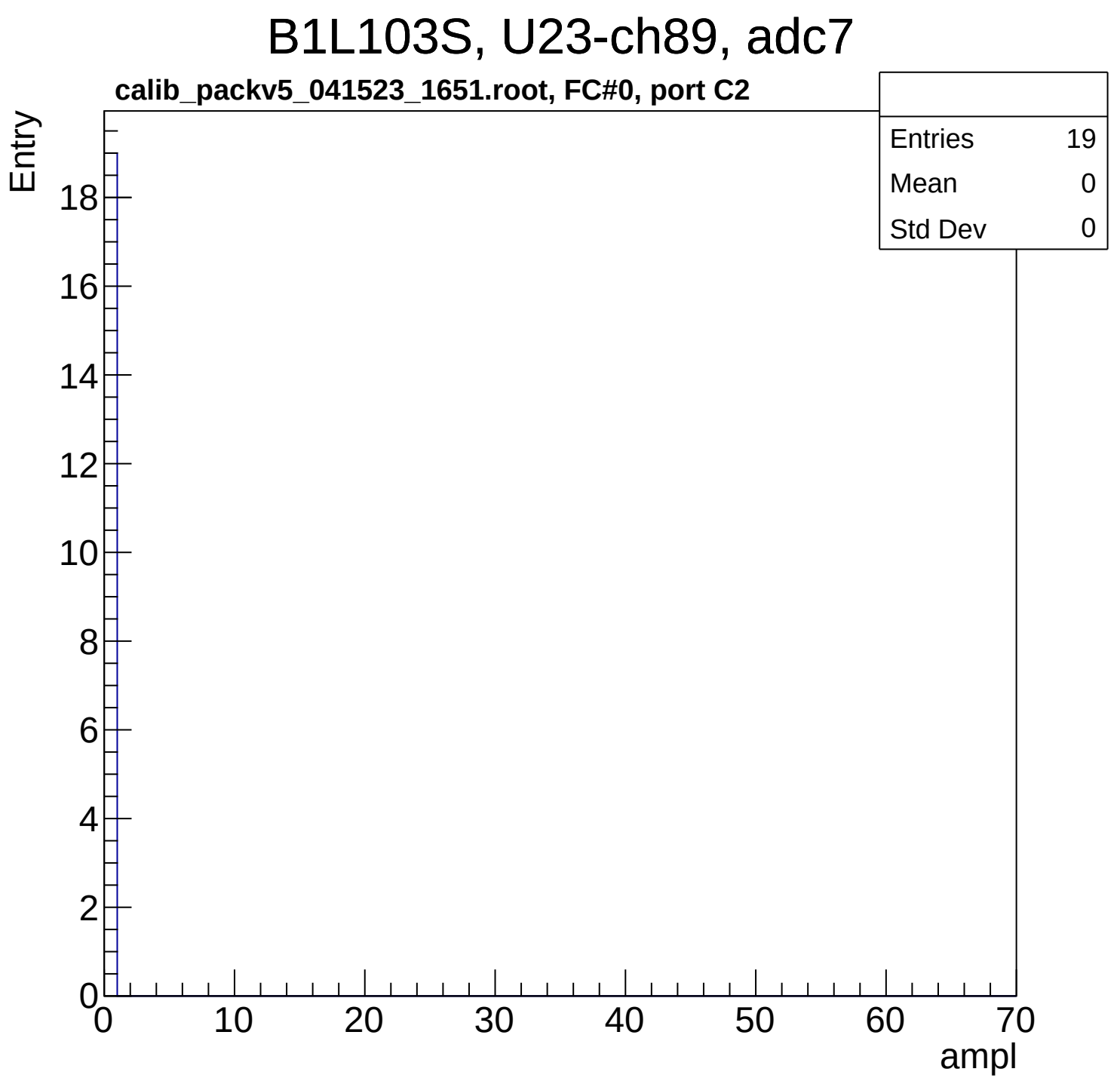

B1L103S, U23-ch89, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 39.35
Std Dev 12.38
Gaus mean : 43.5124
Gaus Width: 3.4038
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

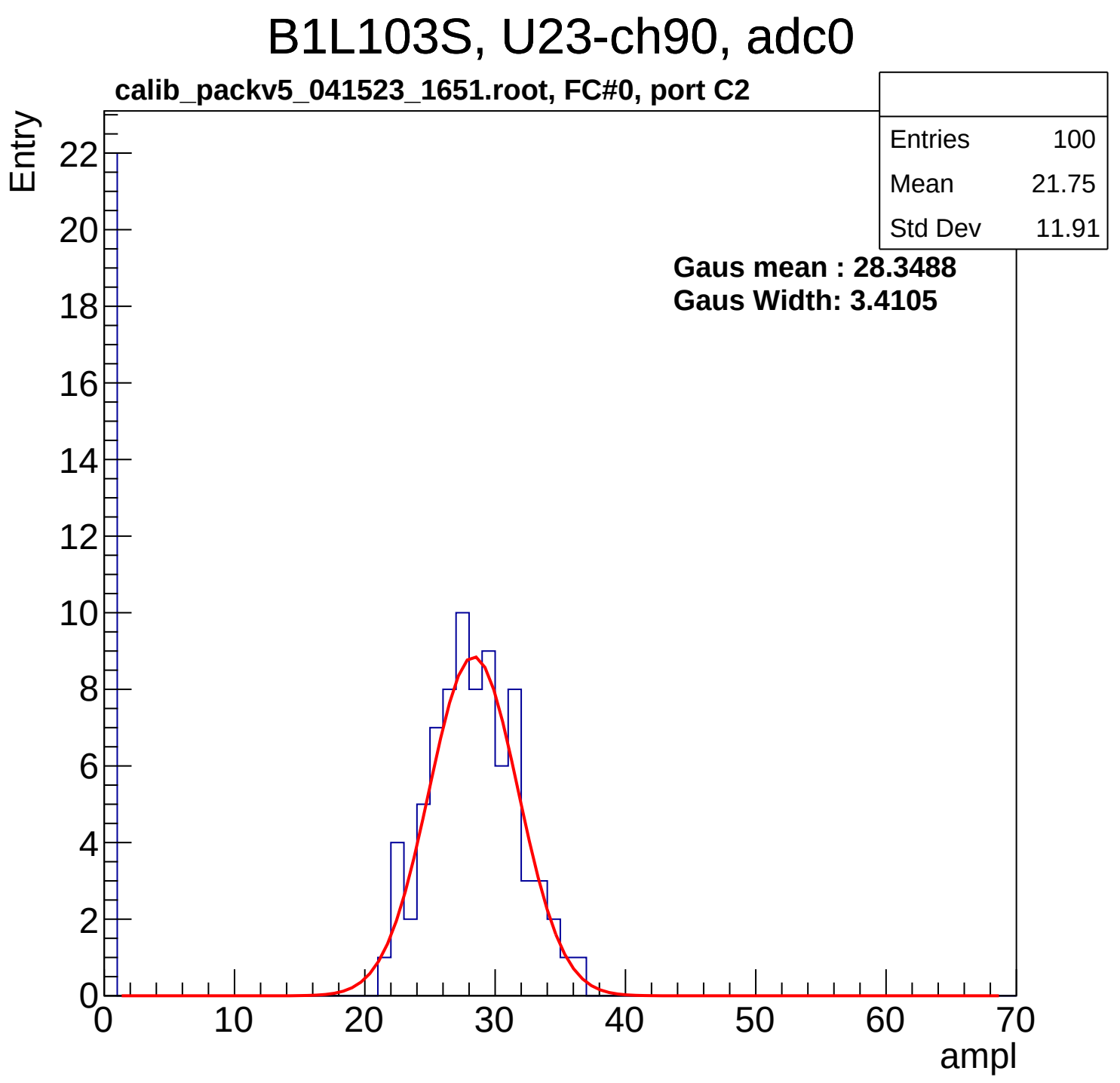

B1L103S, U23-ch89, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 49.41
Std Dev 3.493
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

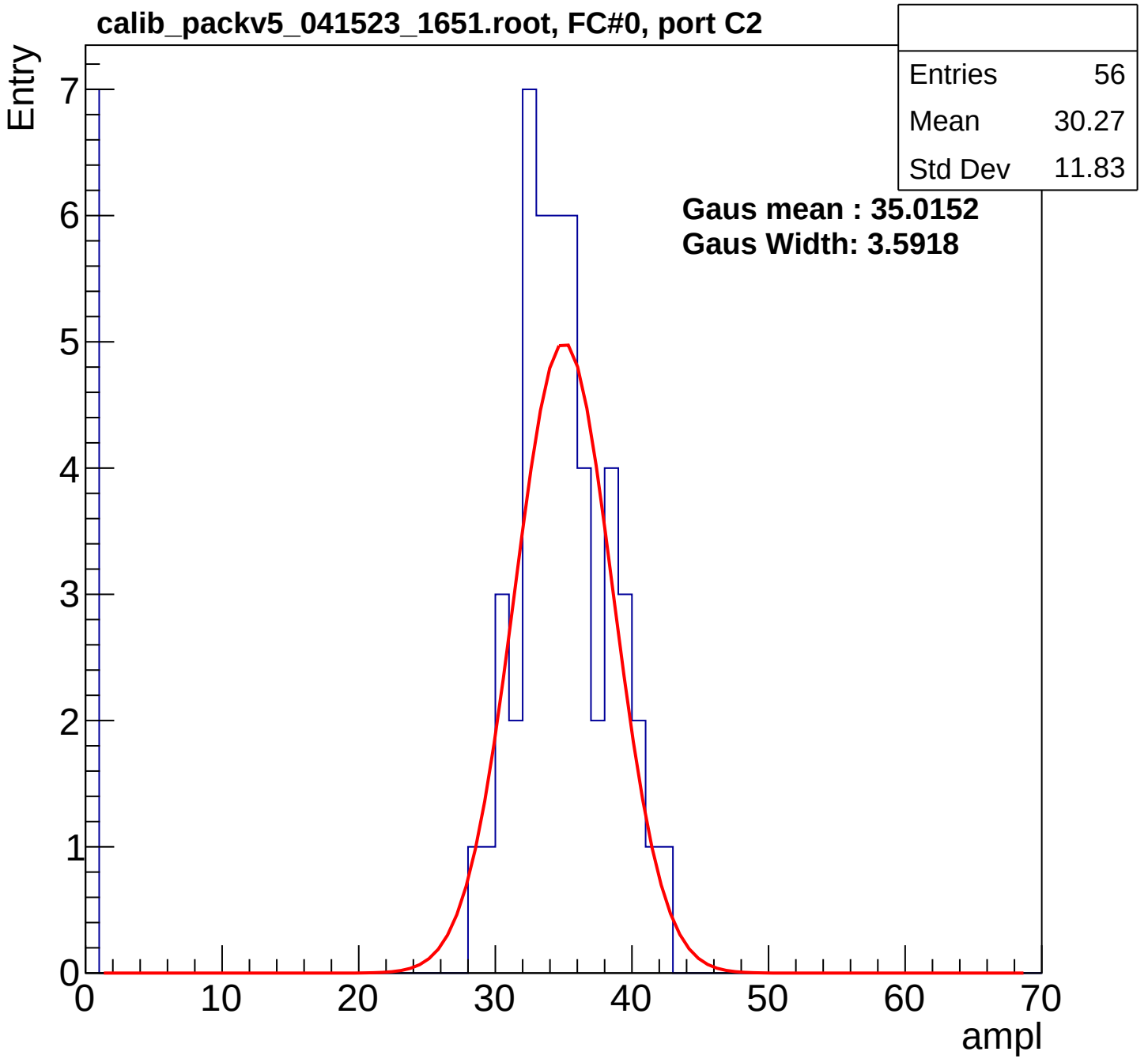

B1L103S, U23-ch89, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 54.1
Std Dev 3.523
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

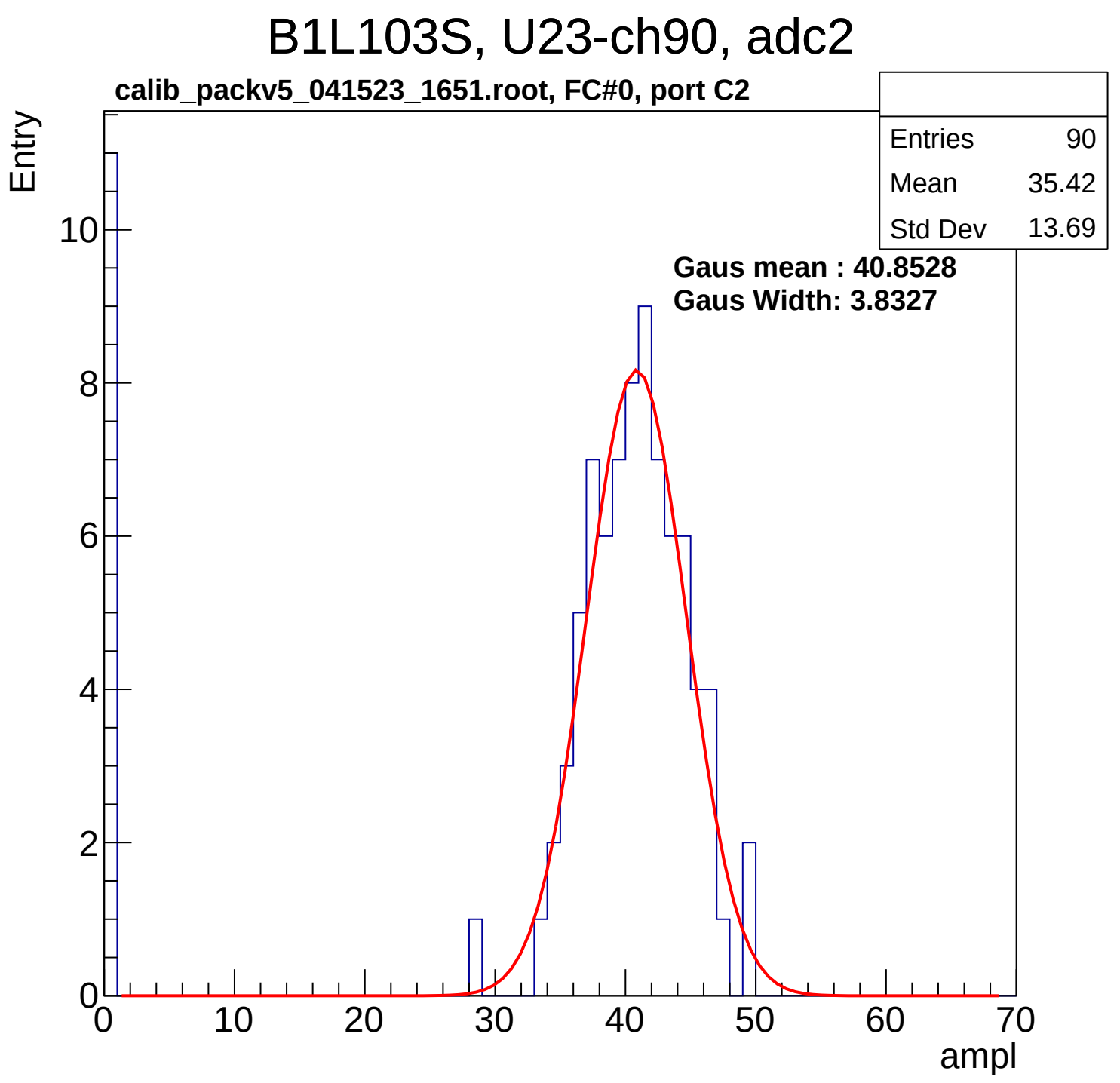

B1L103S, U23-ch89, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 58.61
Std Dev 8.456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

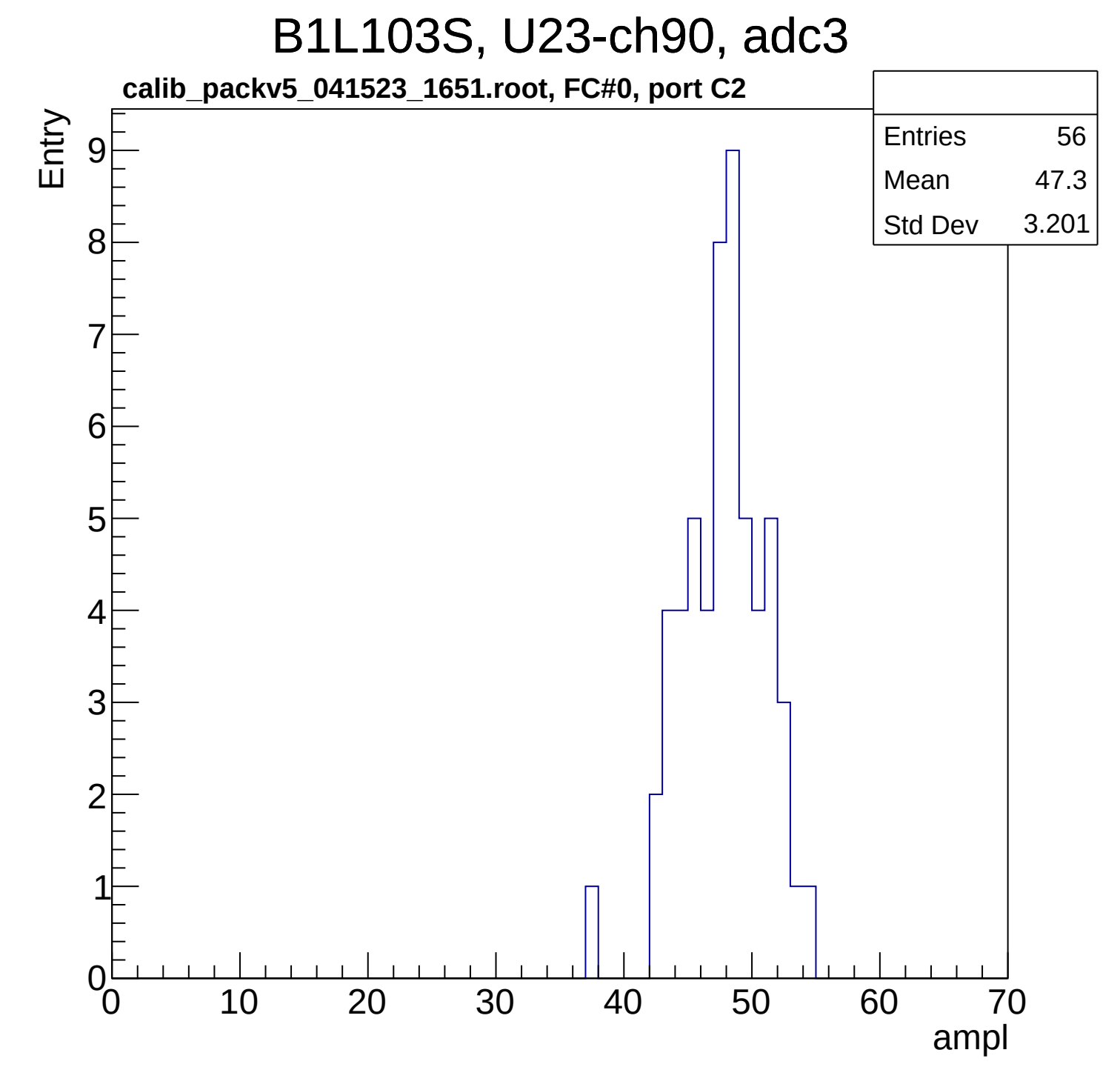

B1L103S, U23-ch89, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 61.33
Std Dev 1.491
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

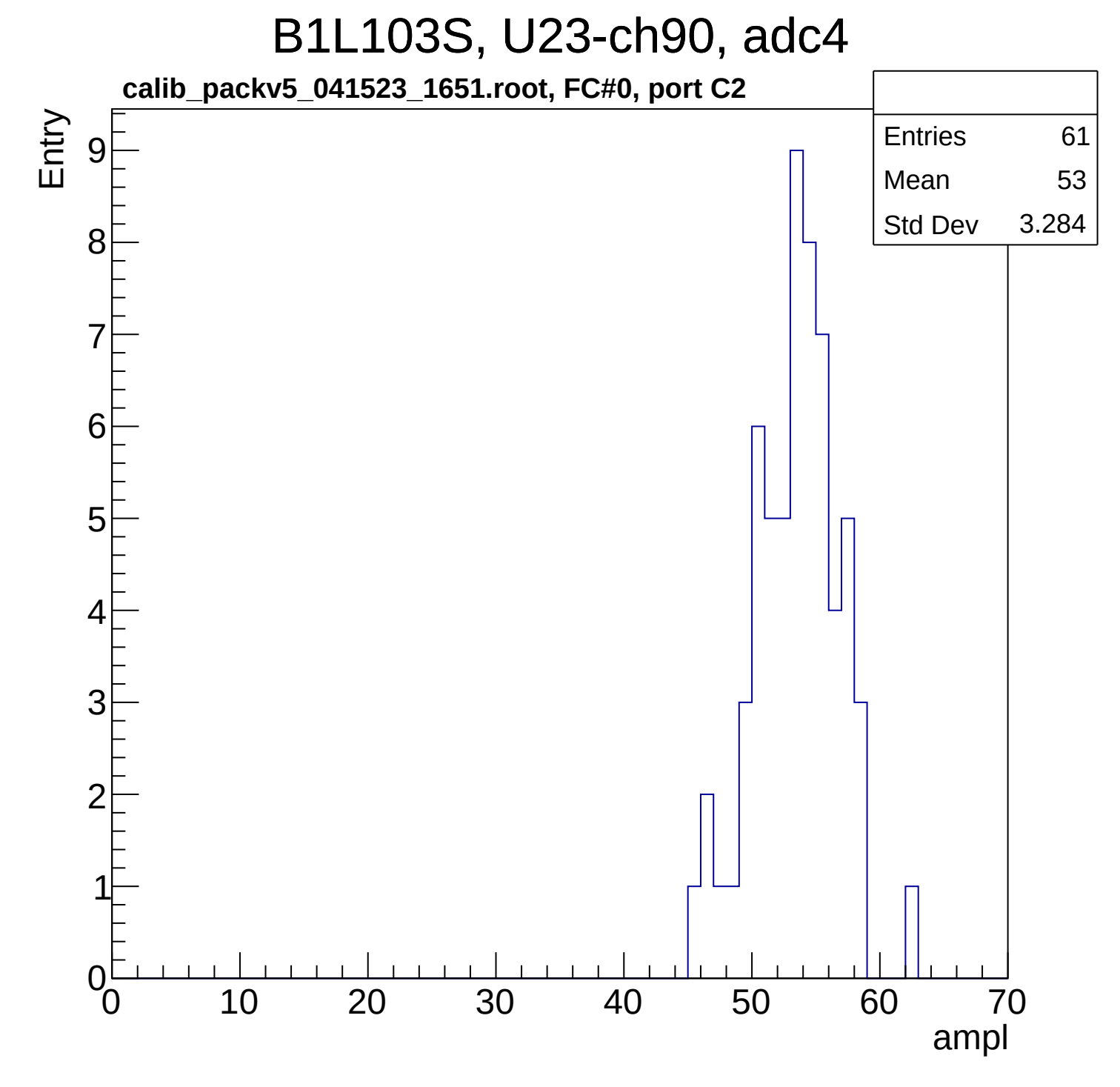

B1L103S, U23-ch89, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

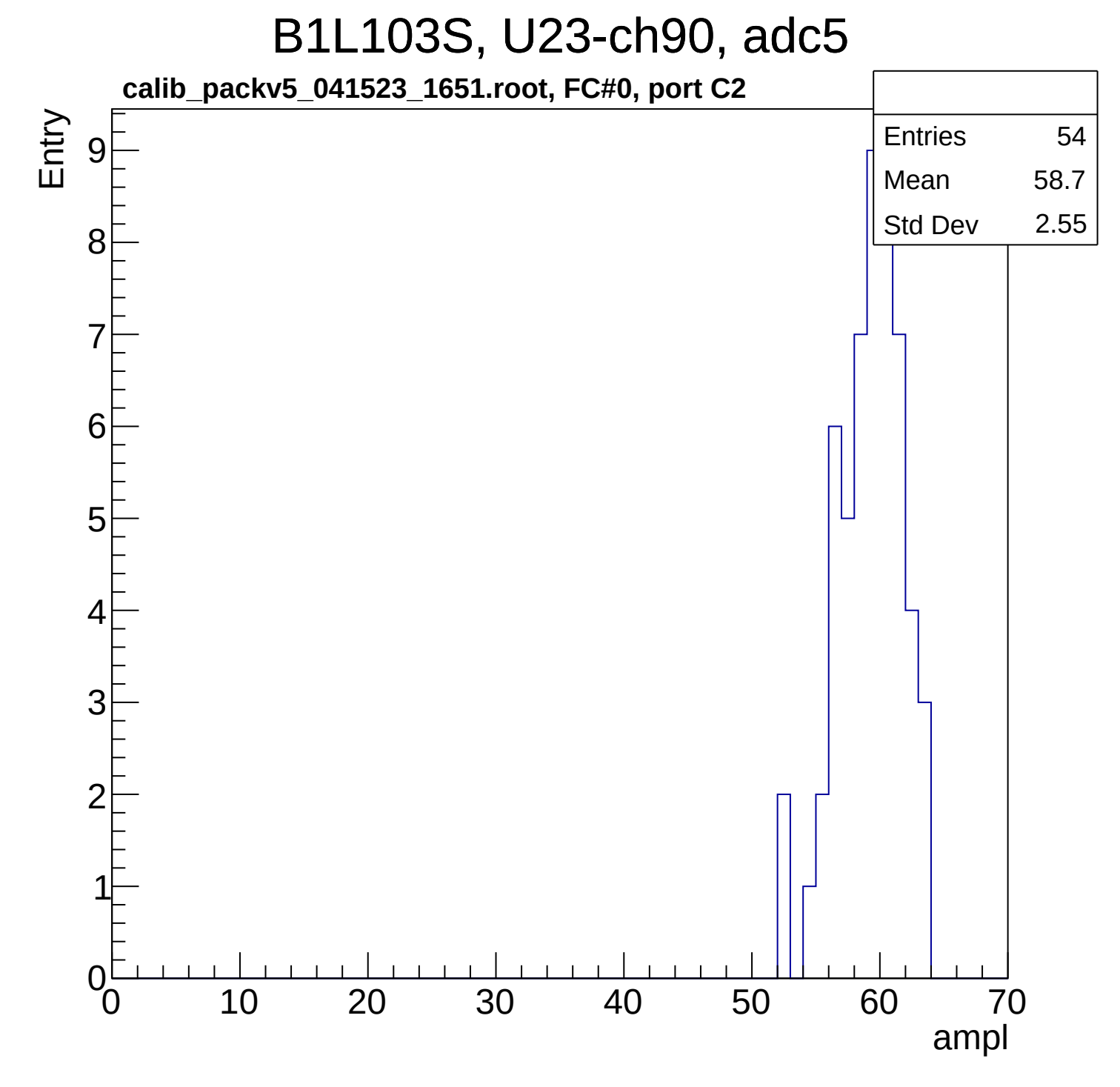

B1L103S, U23-ch90, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 21.75
Std Dev 11.91
Gaus mean : 28.3488
Gaus Width: 3.4105
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
0
10
20
30
40
50
60
70

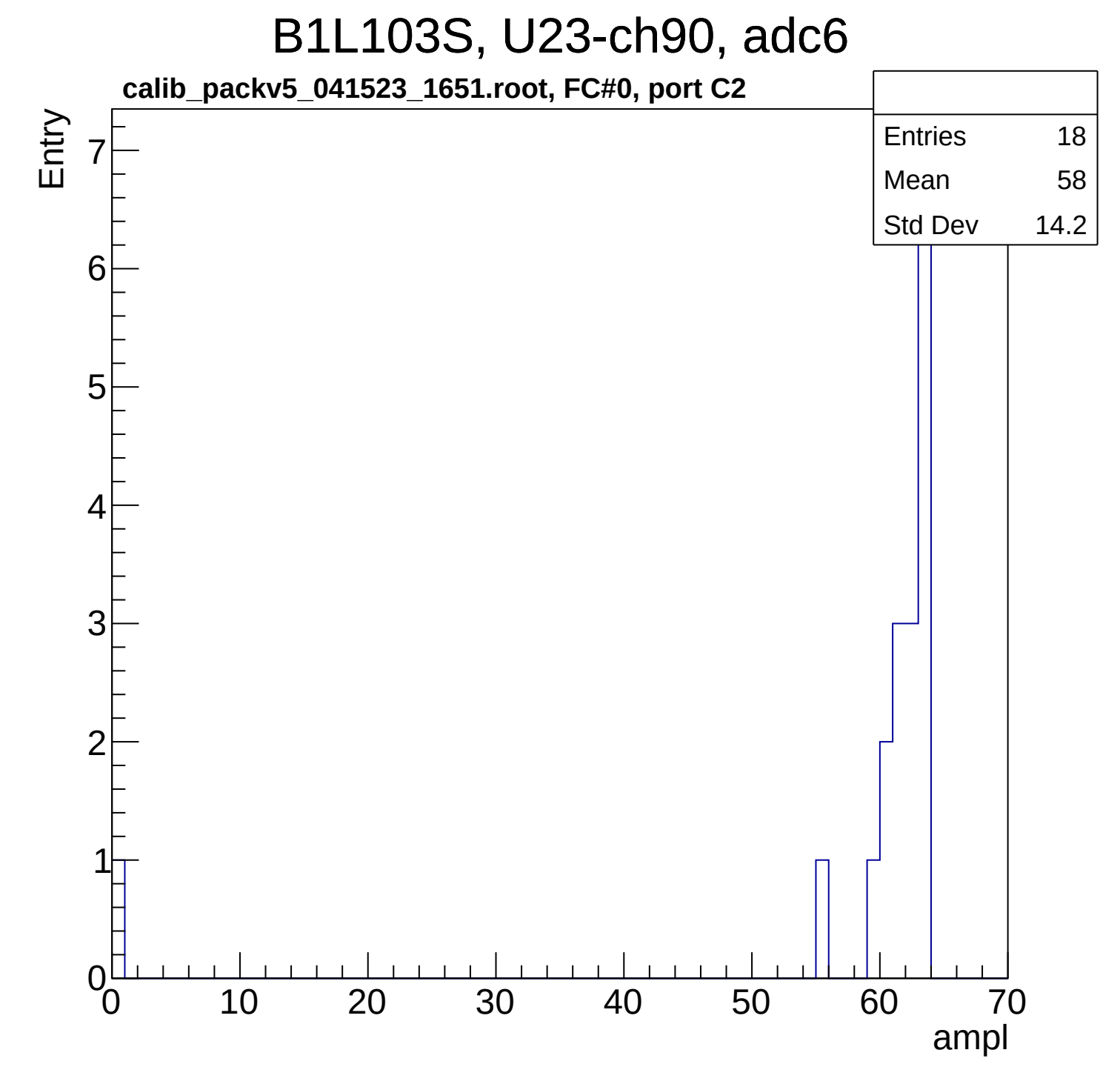

B1L103S, U23-ch90, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 30.27
Std Dev 11.83
Gaus mean : 35.0152
Gaus Width: 3.5918
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

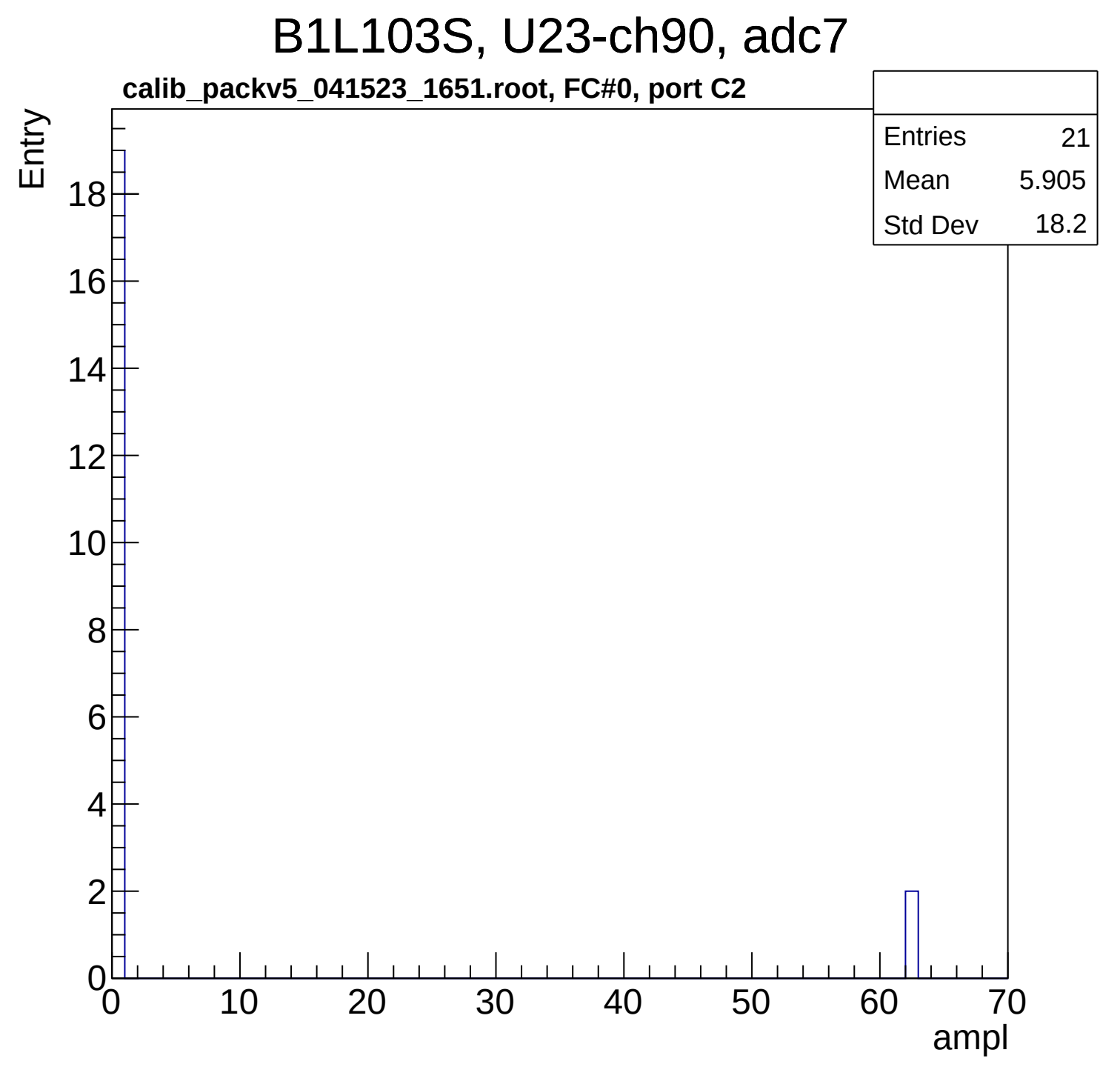

B1L103S, U23-ch90, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 90
Mean 35.42
Std Dev 13.69
Gaus mean : 40.8528
Gaus Width: 3.8327
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

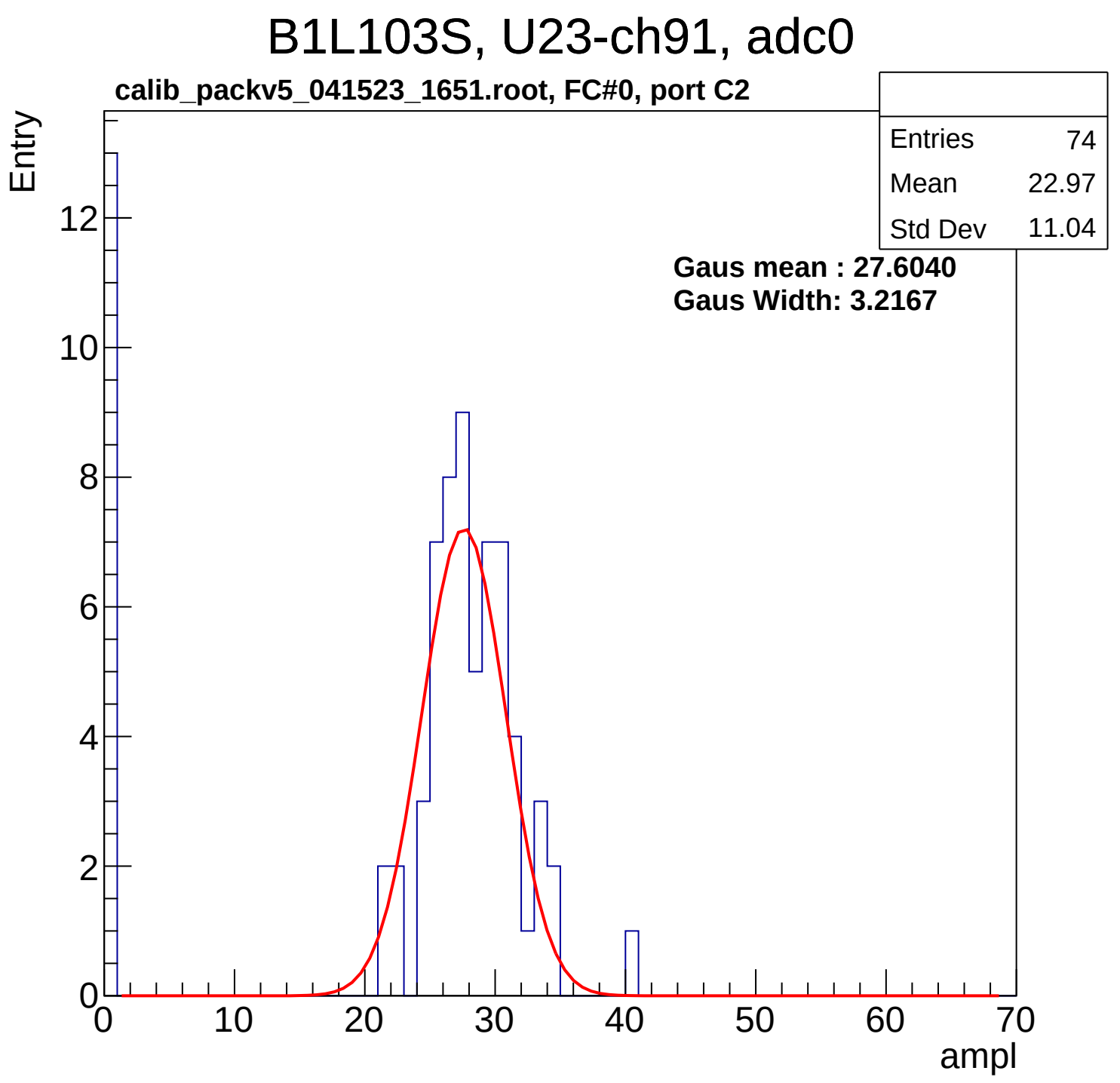

B1L103S, U23-ch90, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 47.3
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

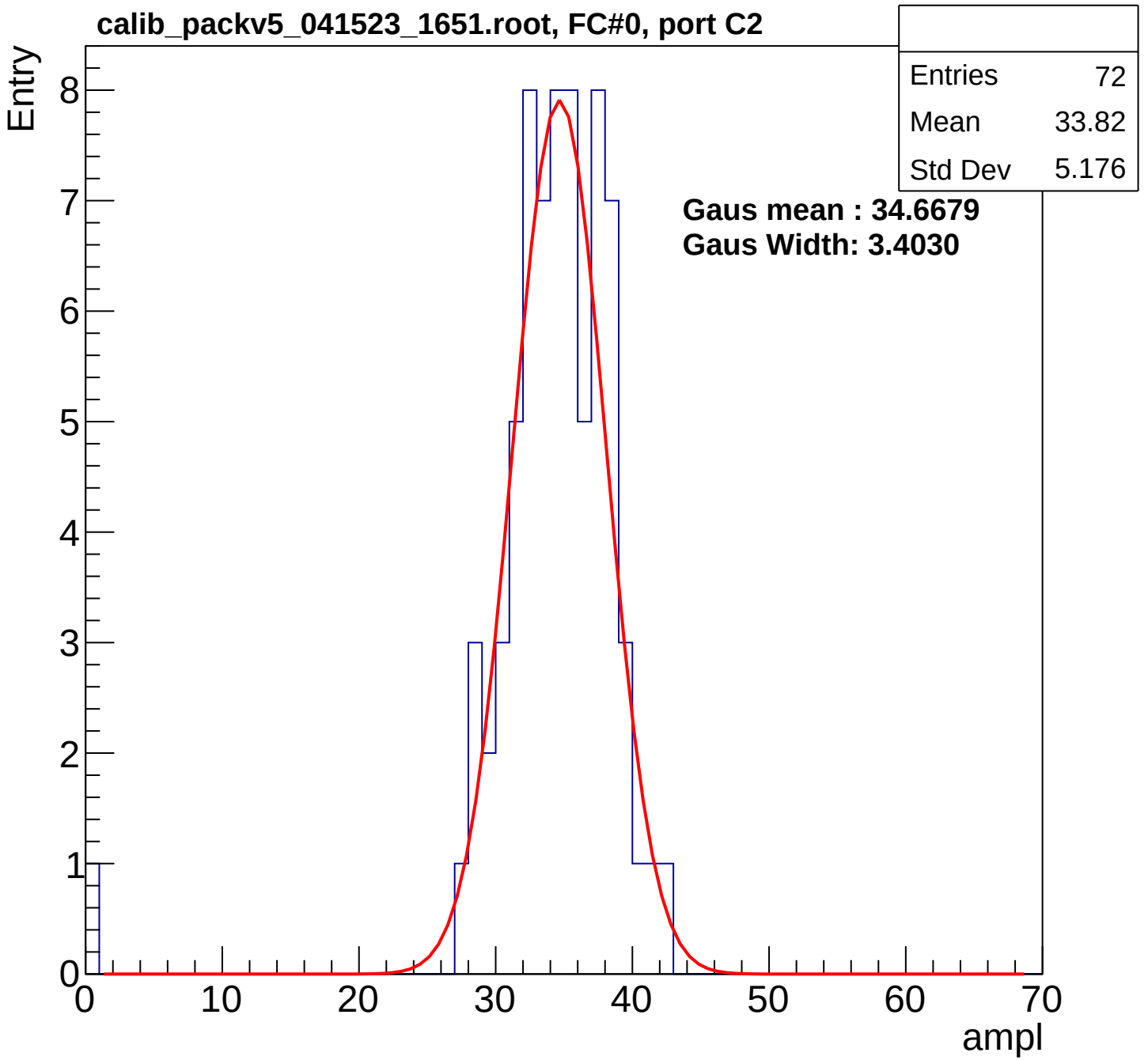

B1L103S, U23-ch90, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 53
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

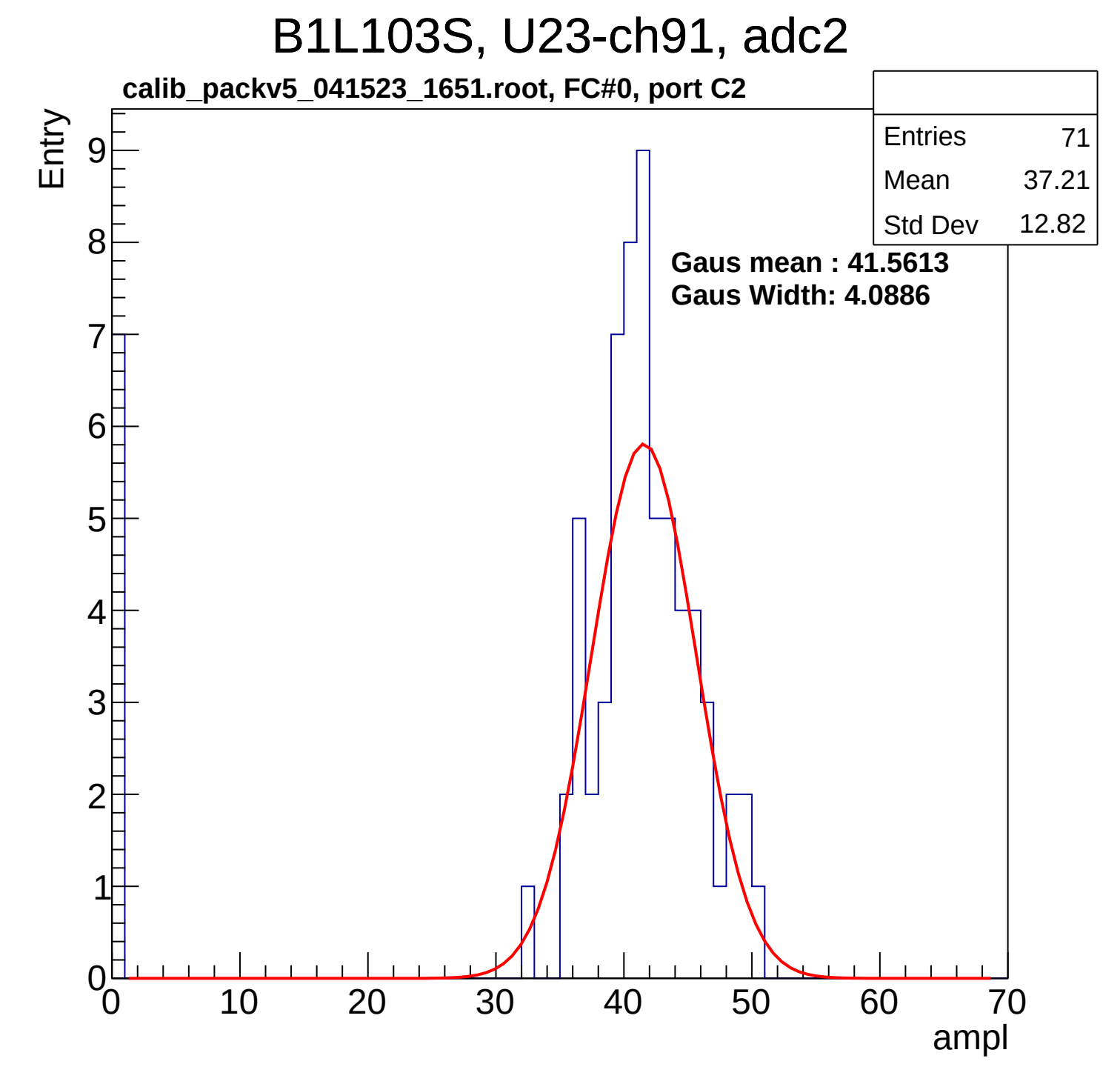

B1L103S, U23-ch90, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 58.7
Std Dev 2.55
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

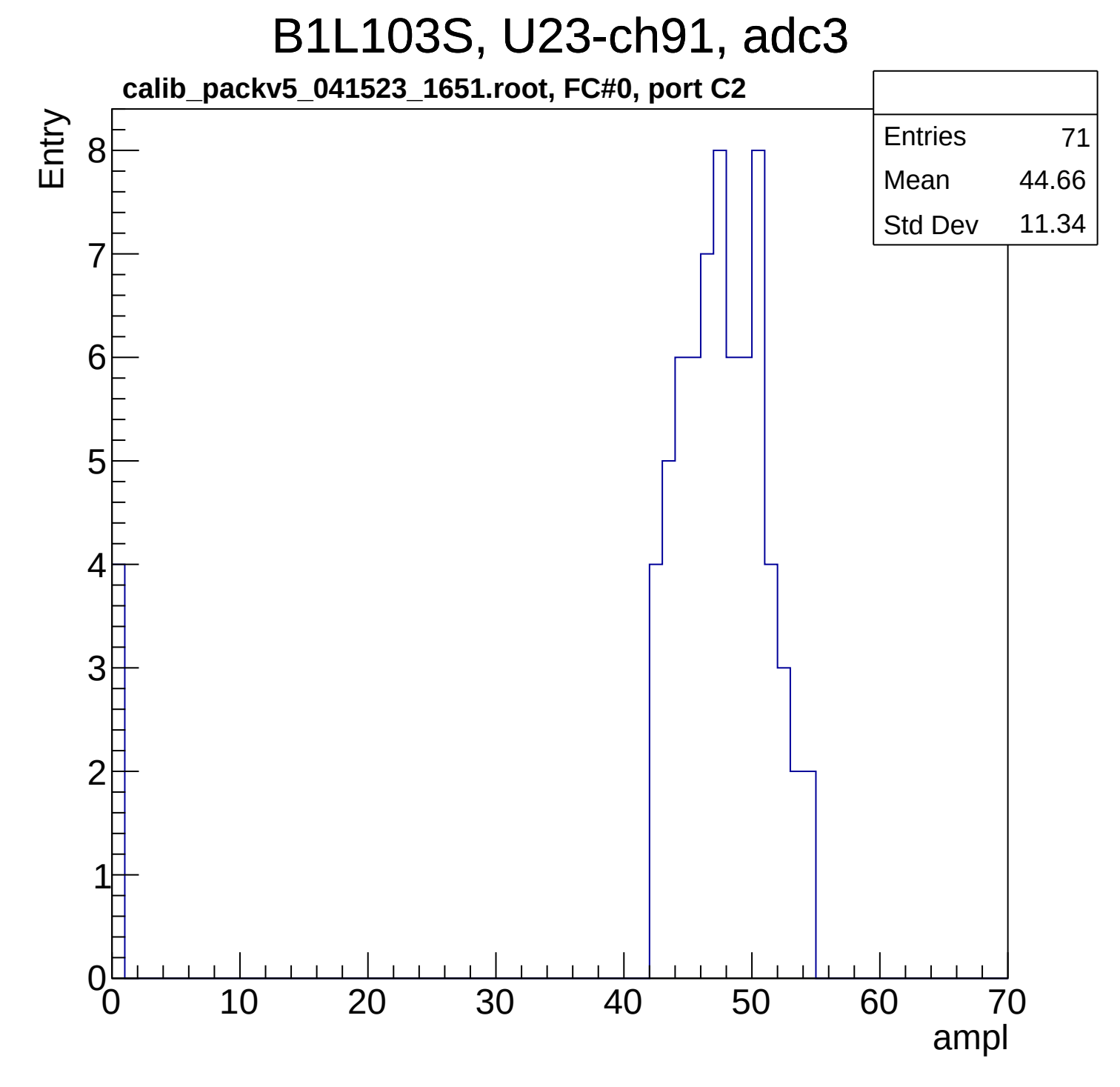

B1L103S, U23-ch90, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 58
Std Dev 14.2
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

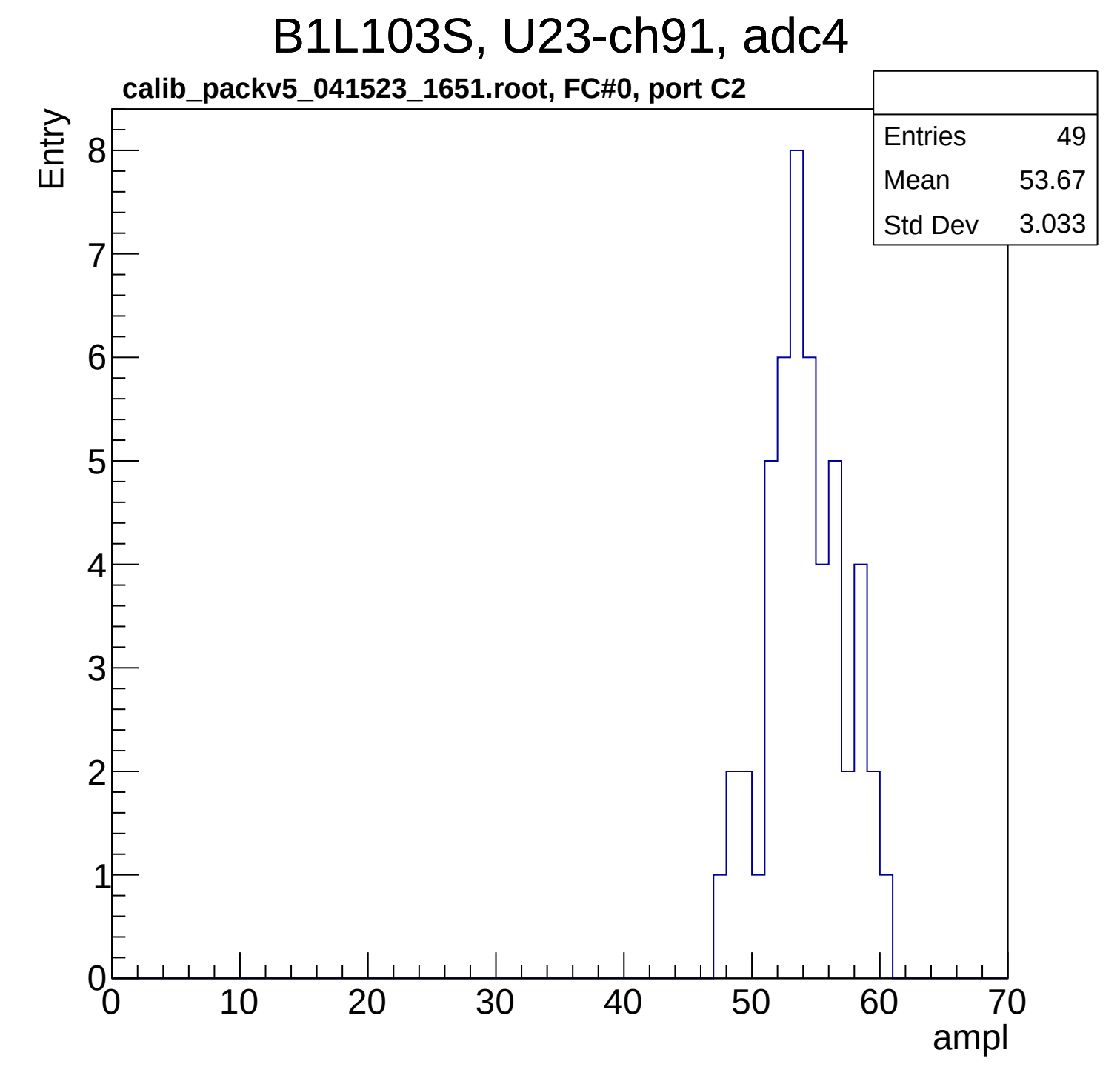

B1L103S, U23-ch90, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.905
Std Dev 18.2
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

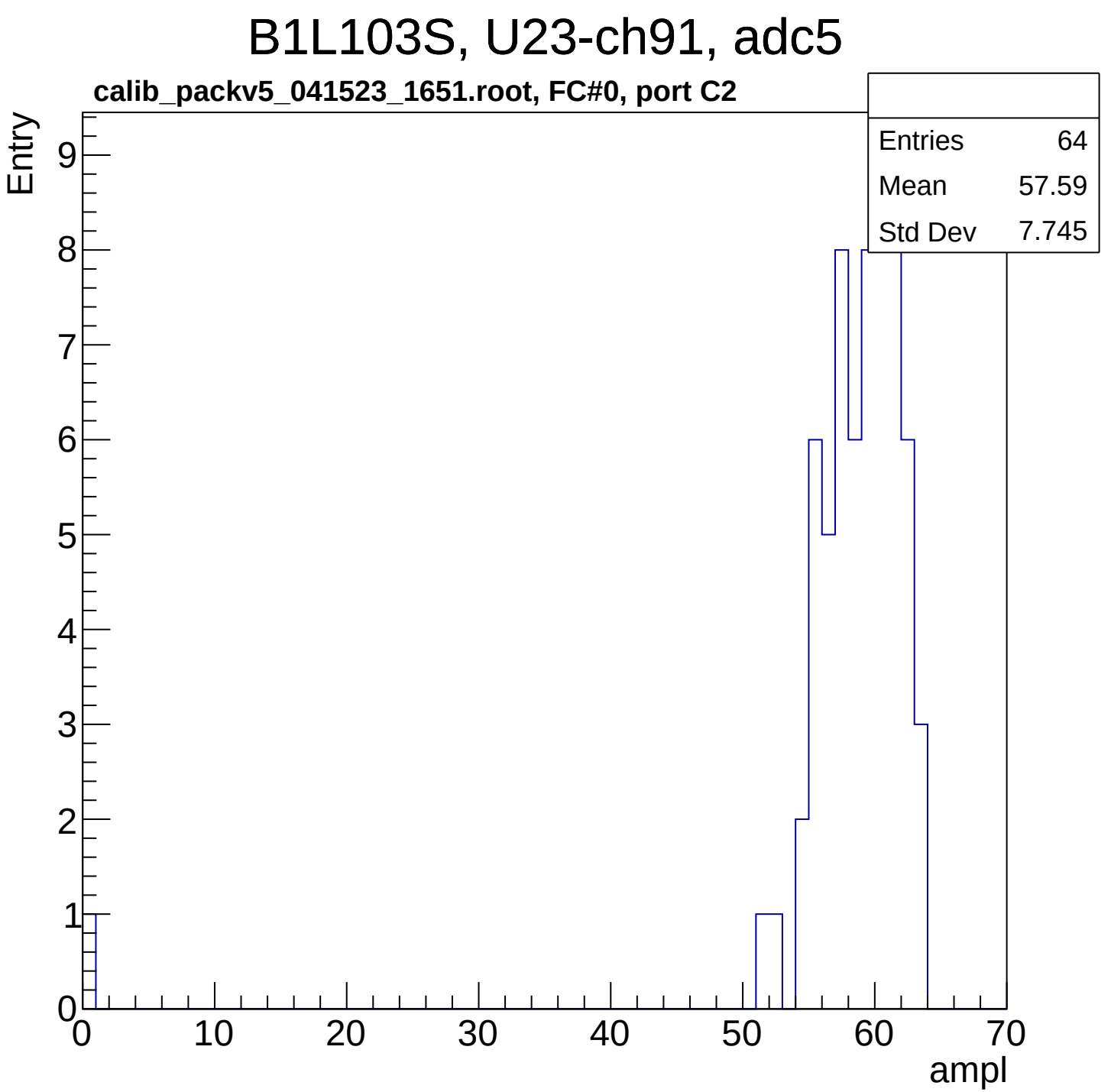

B1L103S, U23-ch91, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 22.97
Std Dev 11.04
Gaus mean : 27.6040
Gaus Width: 3.2167
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

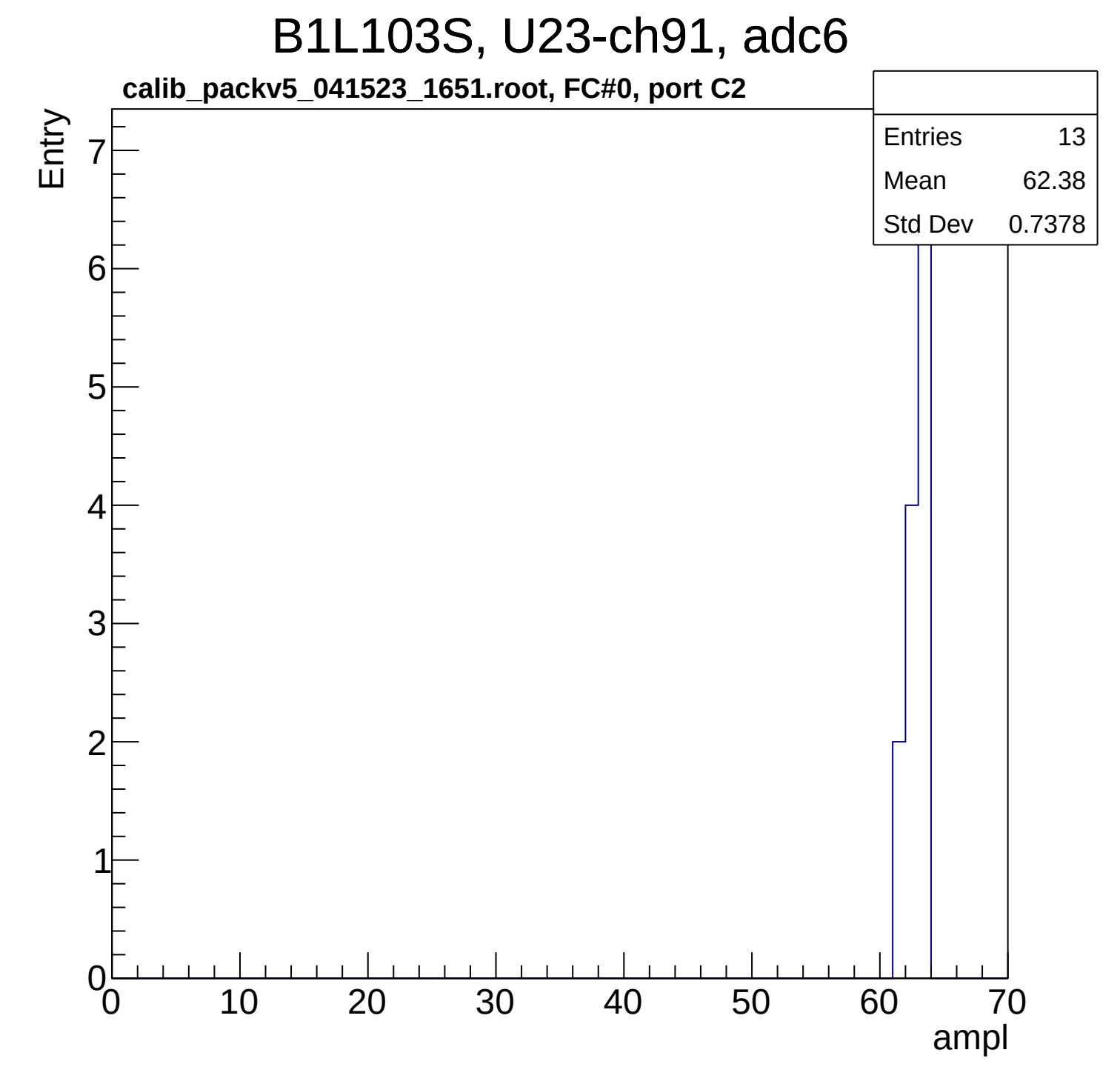

B1L103S, U23-ch91, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 33.82
Std Dev 5.176
Gaus mean : 34.6679
Gaus Width: 3.4030
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

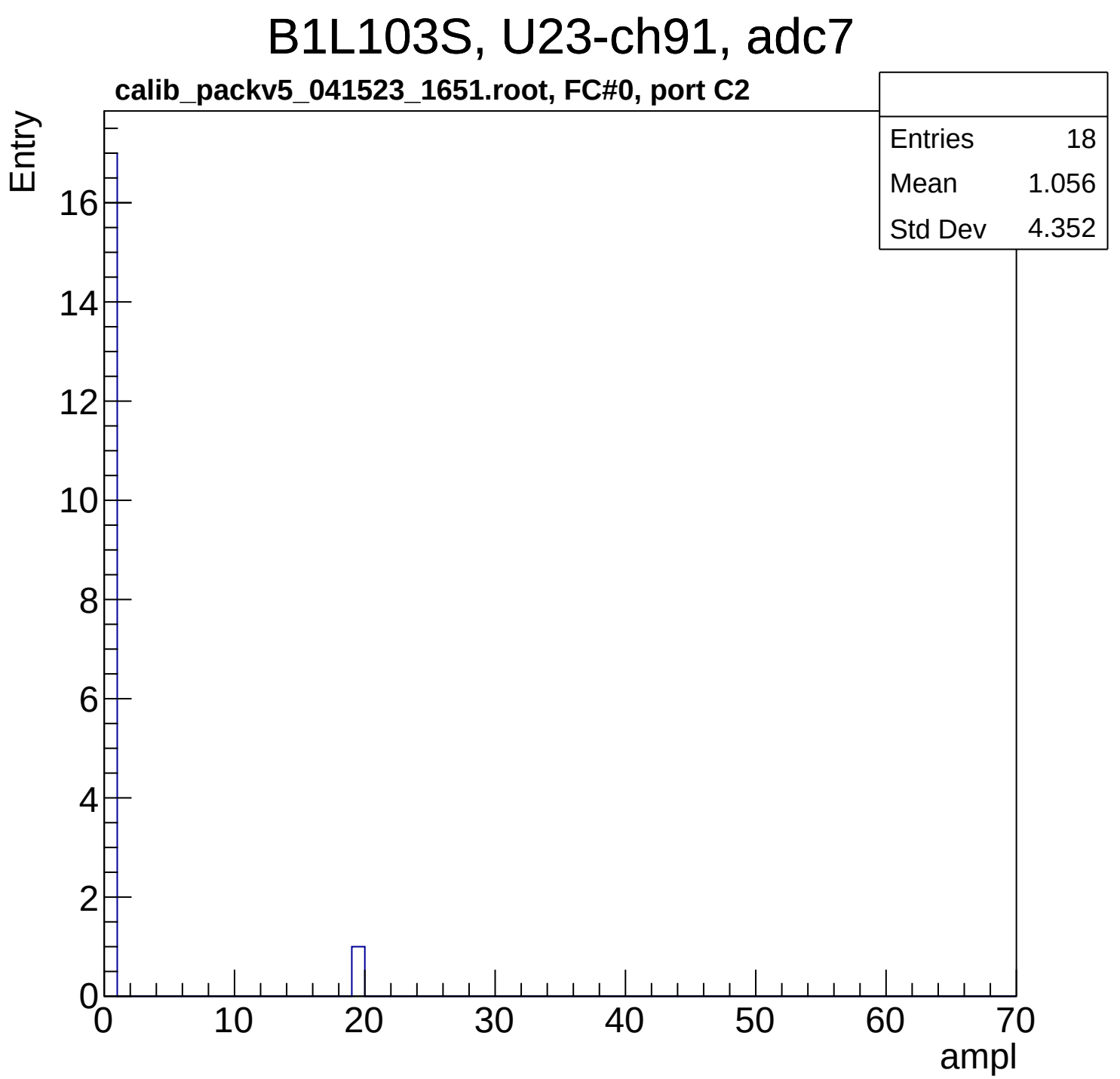

B1L103S, U23-ch91, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 37.21
Std Dev 12.82
Gaus mean : 41.5613
Gaus Width: 4.0886
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

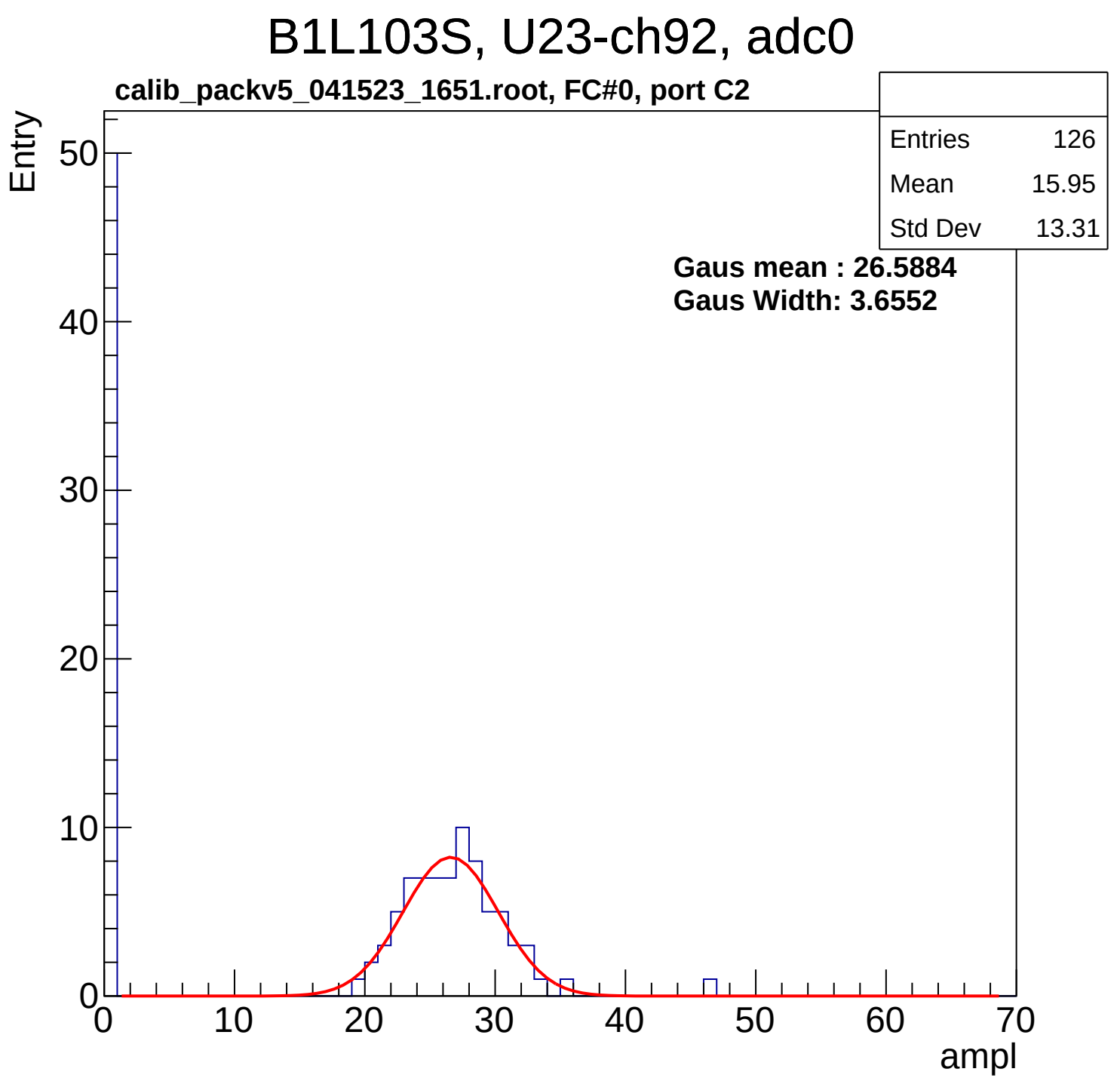

B1L103S, U23-ch91, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 44.66
Std Dev 11.34
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

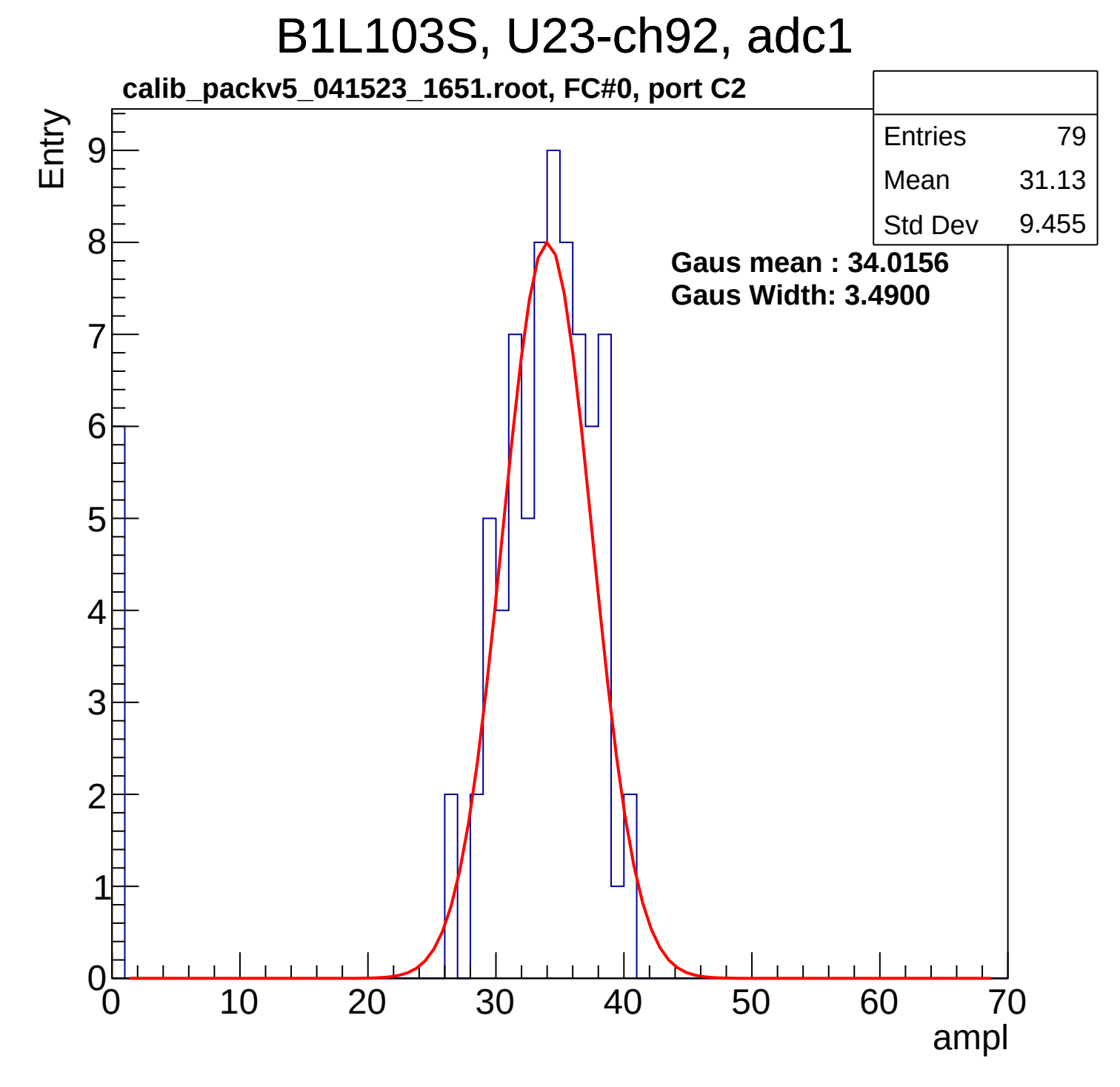

B1L103S, U23-ch91, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 53.67
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

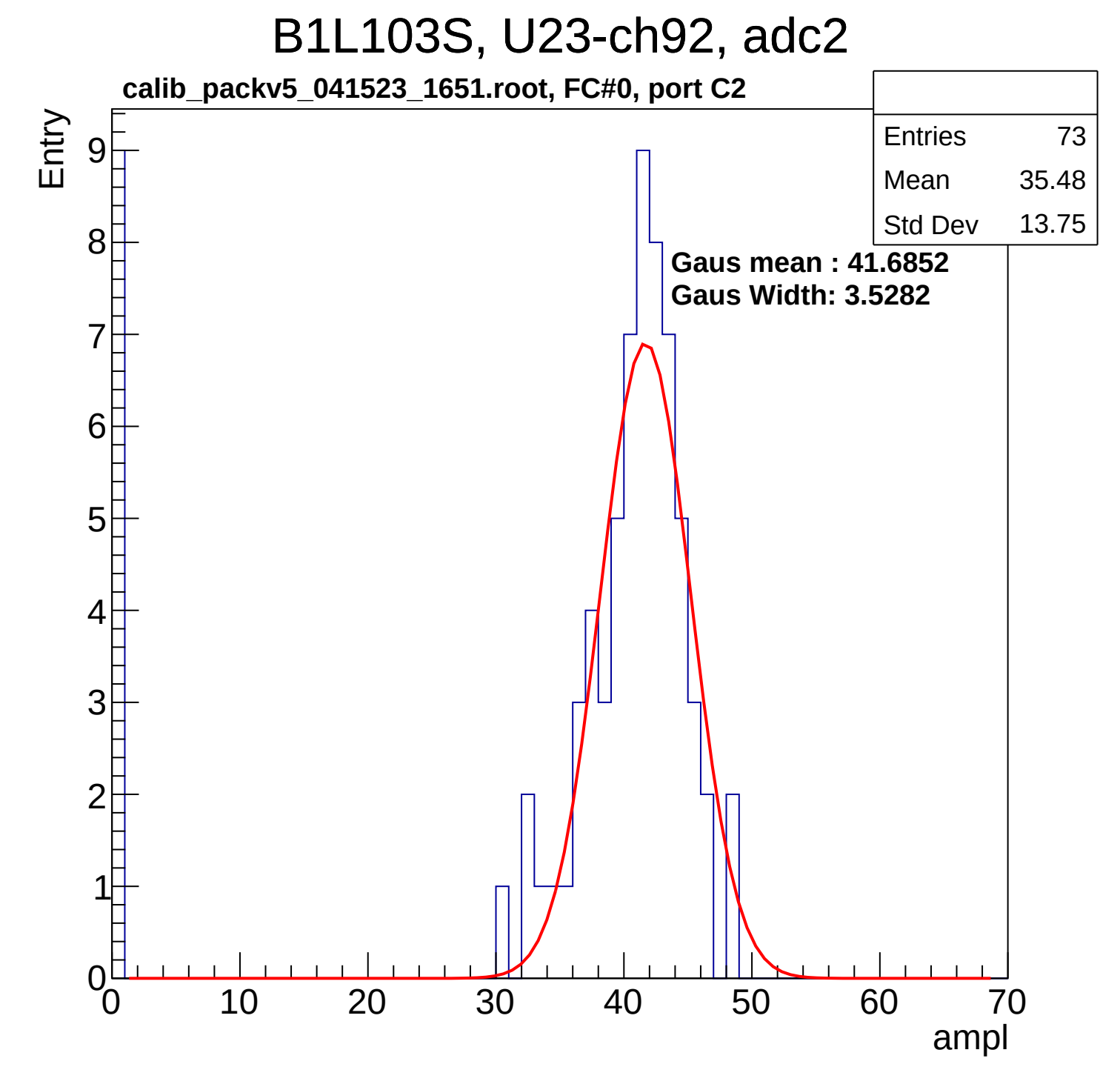

B1L103S, U23-ch91, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 57.59
Std Dev 7.745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

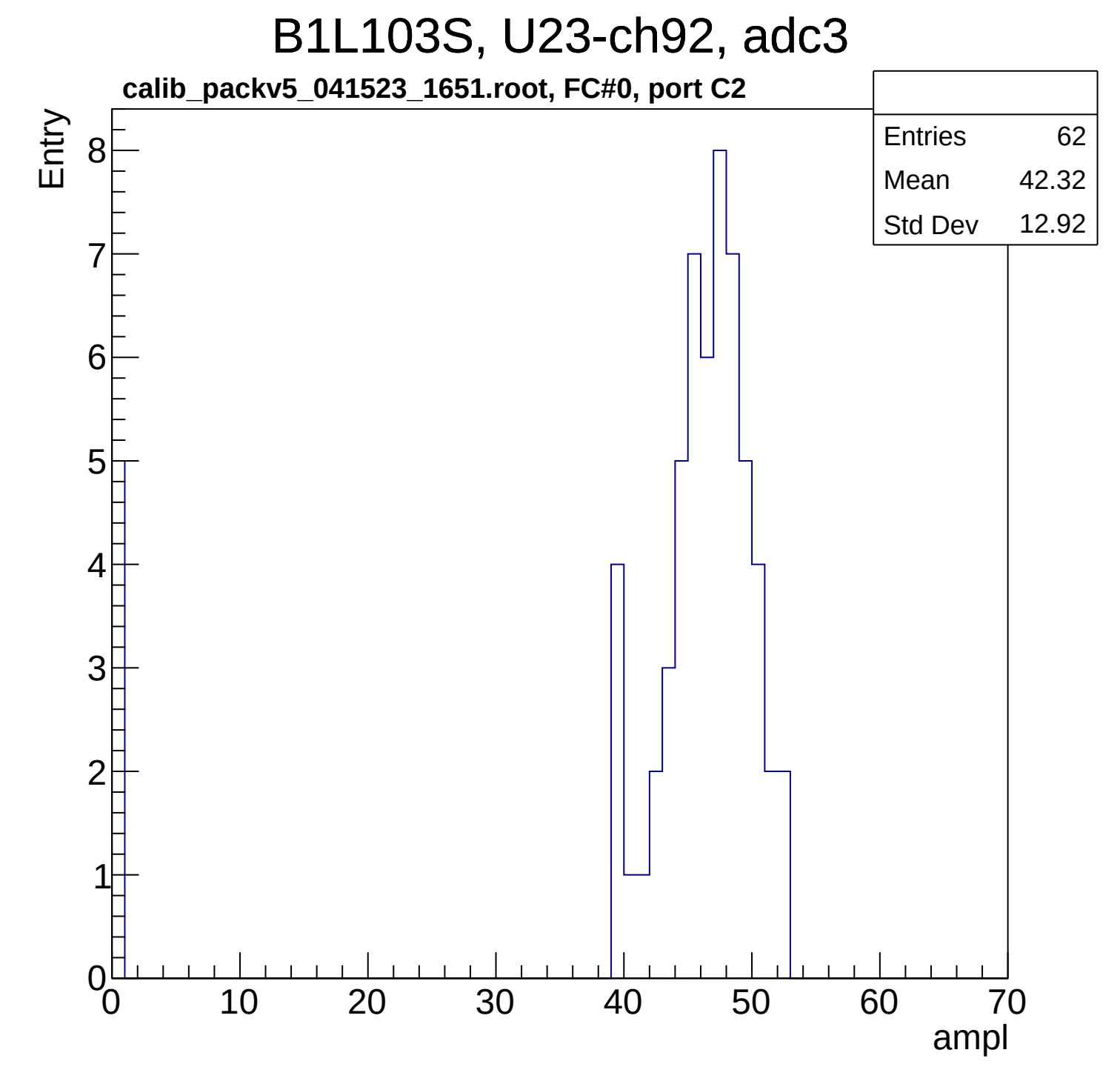

B1L103S, U23-ch91, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 13
Mean 62.38
Std Dev 0.7378
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

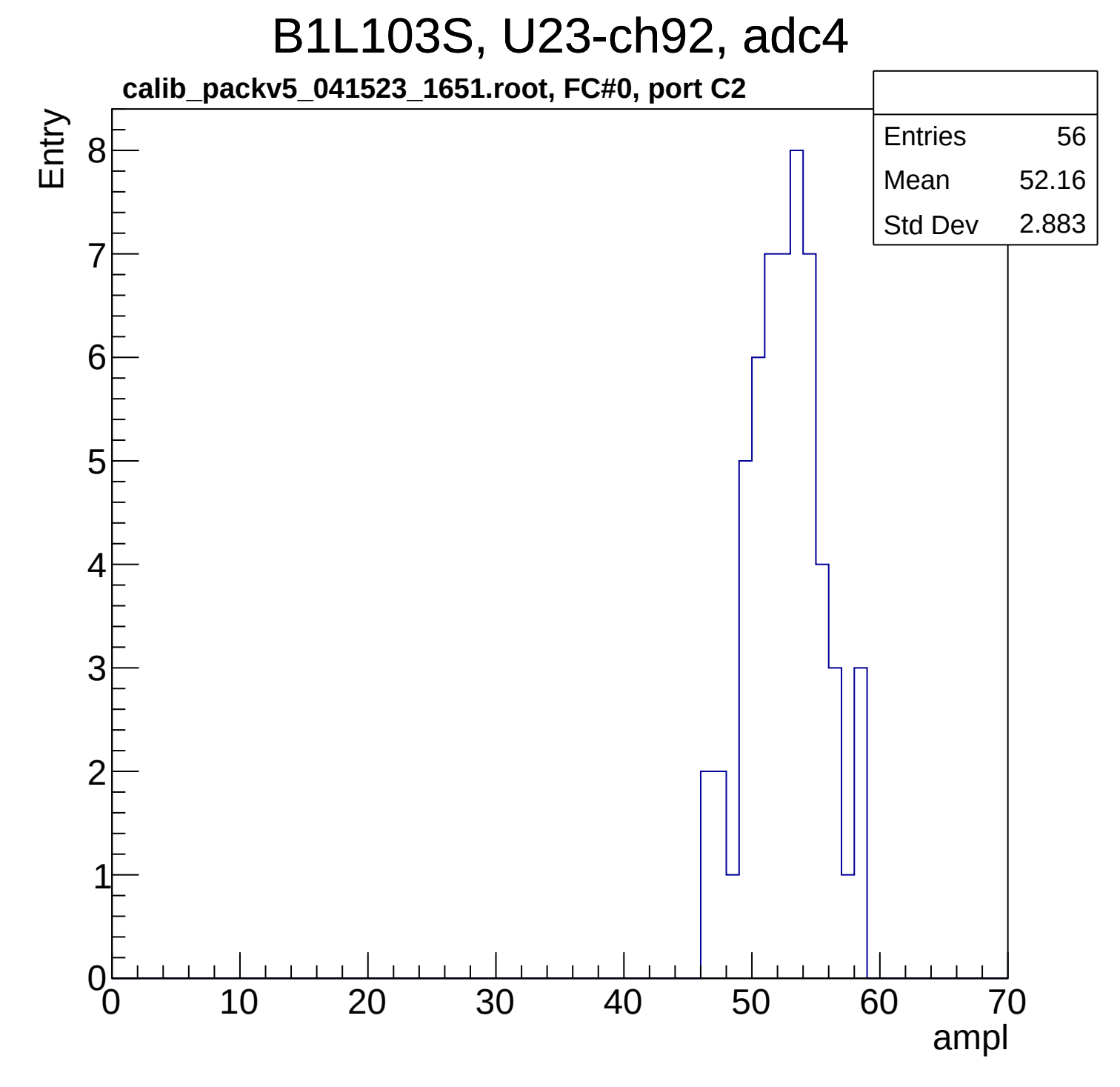

B1L103S, U23-ch91, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 1.056
Std Dev 4.352
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

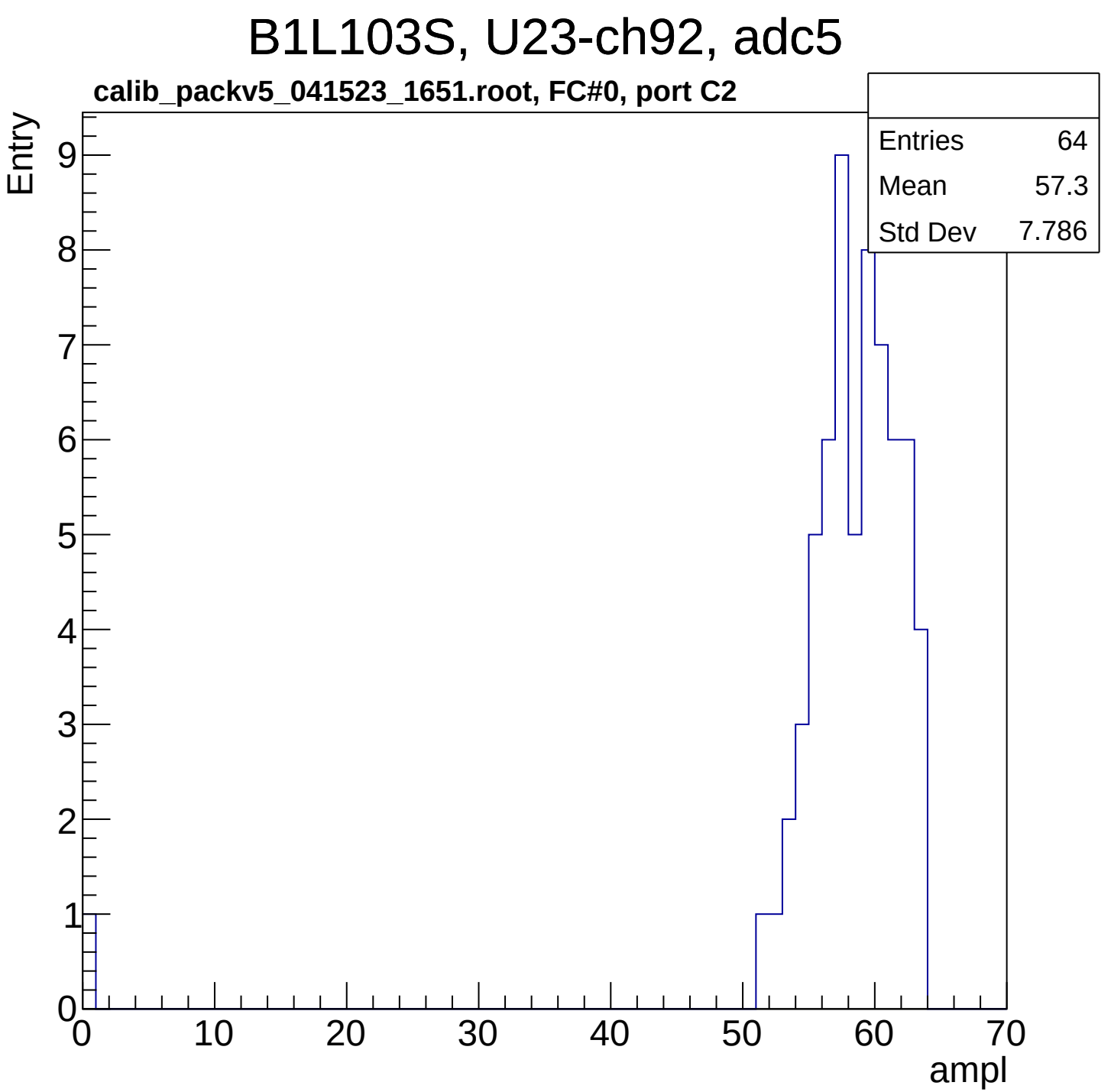

B1L103S, U23-ch92, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 126
Mean 15.95
Std Dev 13.31
Gaus mean : 26.5884
Gaus Width: 3.6552
Entry
ampl
0
10
20
30
40
50
60
70

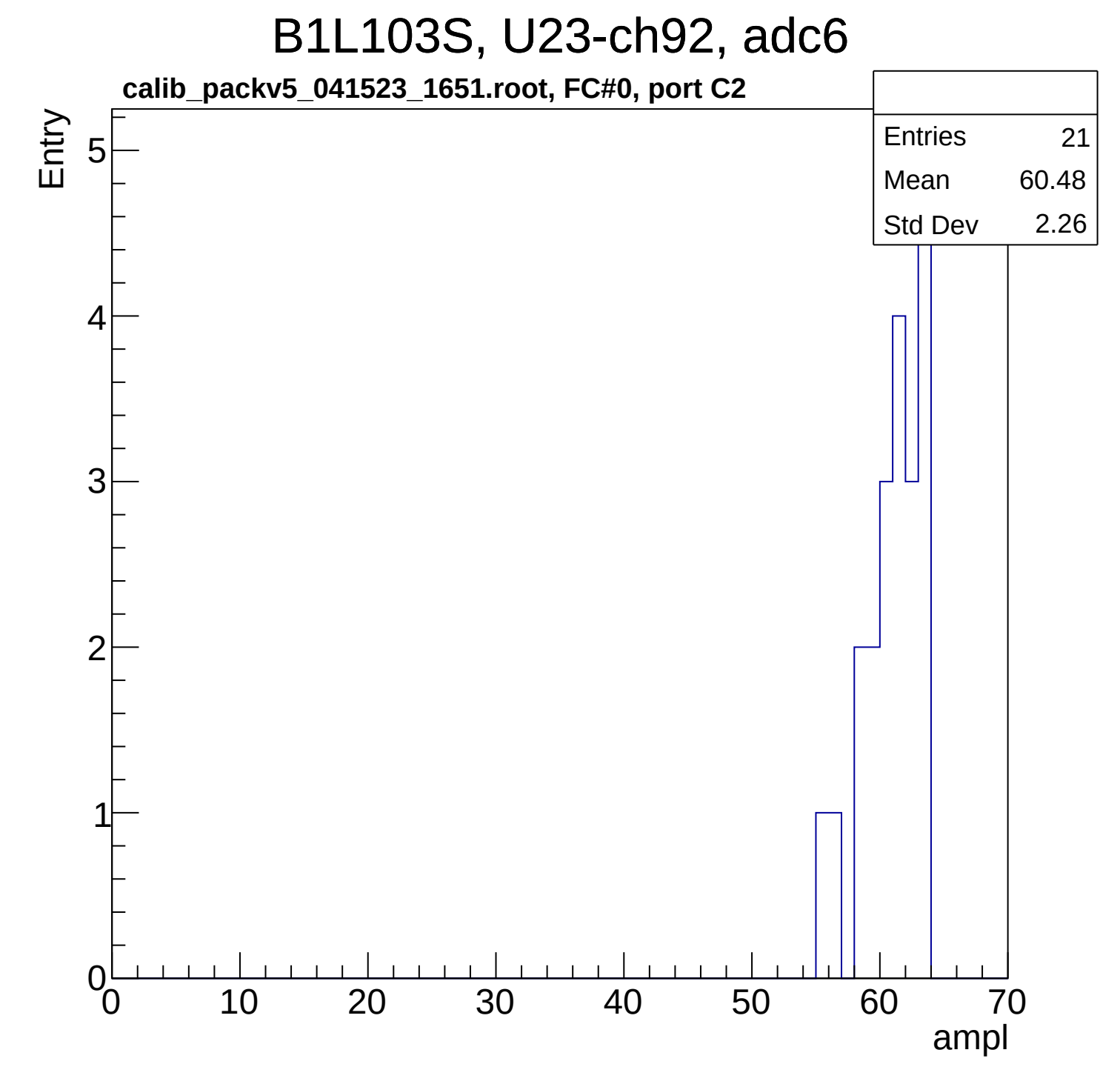

B1L103S, U23-ch92, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 31.13
Std Dev 9.455
Gaus mean : 34.0156
Gaus Width: 3.4900
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

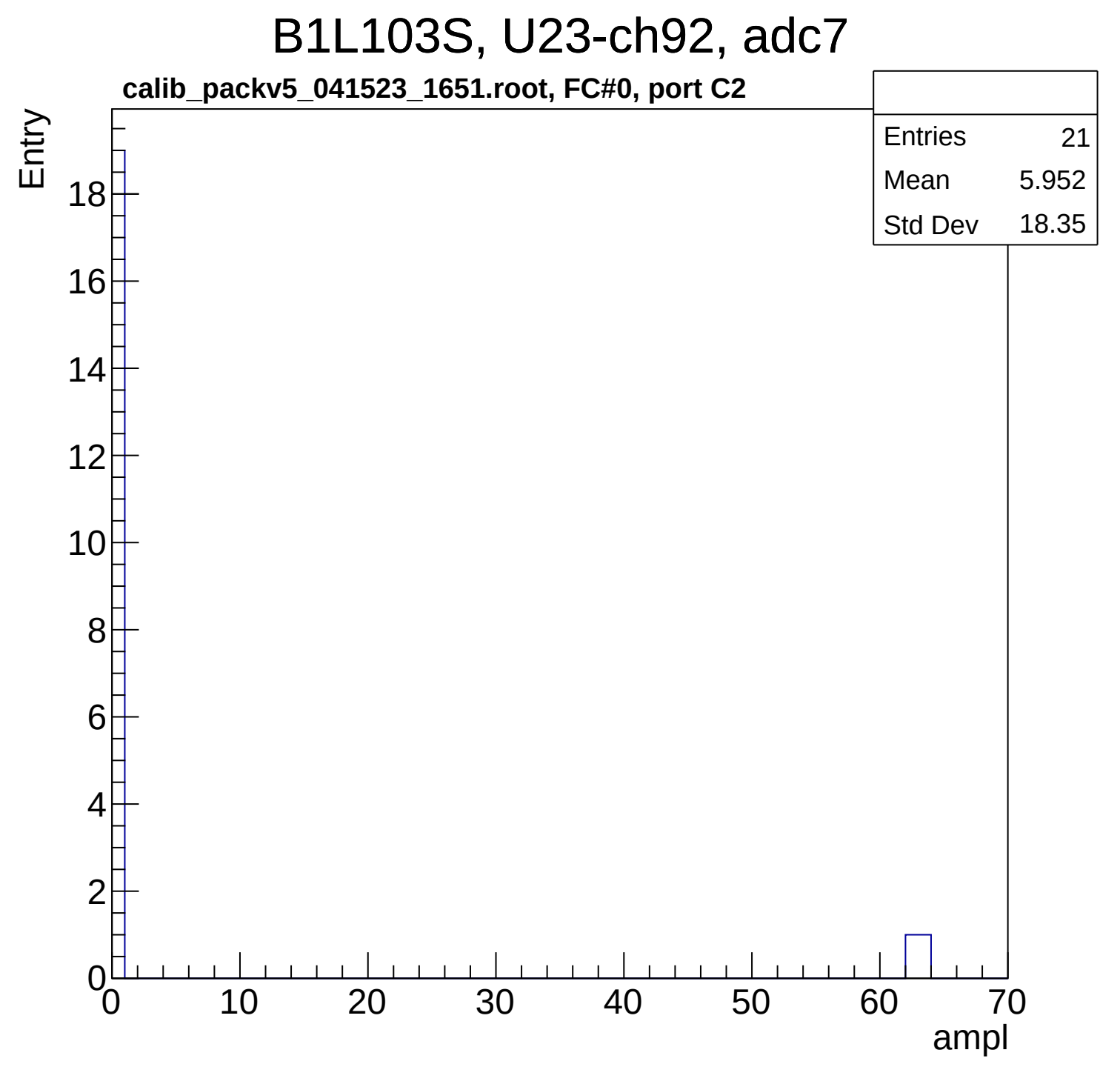

B1L103S, U23-ch92, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 35.48
Std Dev 13.75
Gaus mean : 41.6852
Gaus Width: 3.5282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

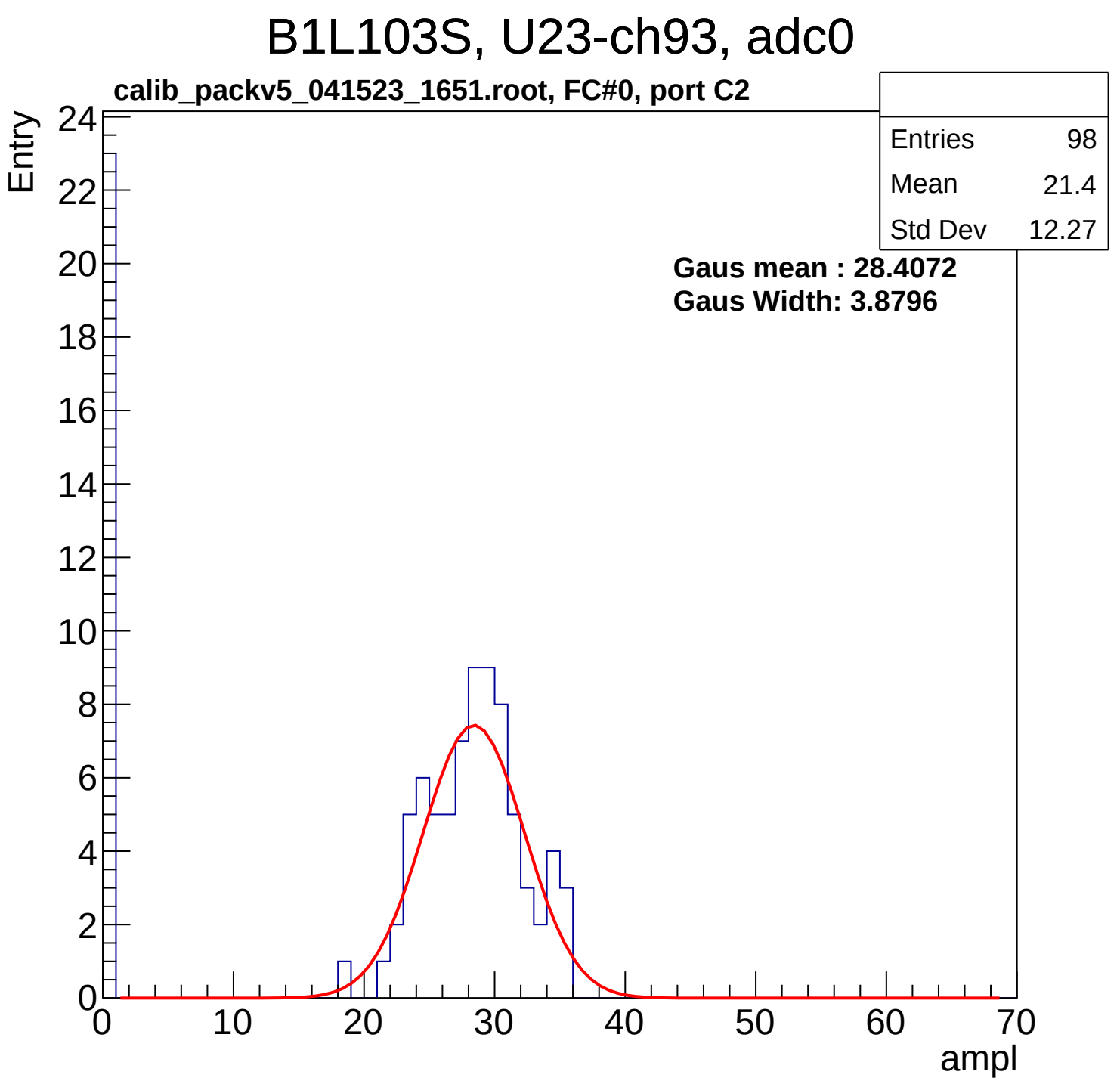

B1L103S, U23-ch92, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 42.32
Std Dev 12.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

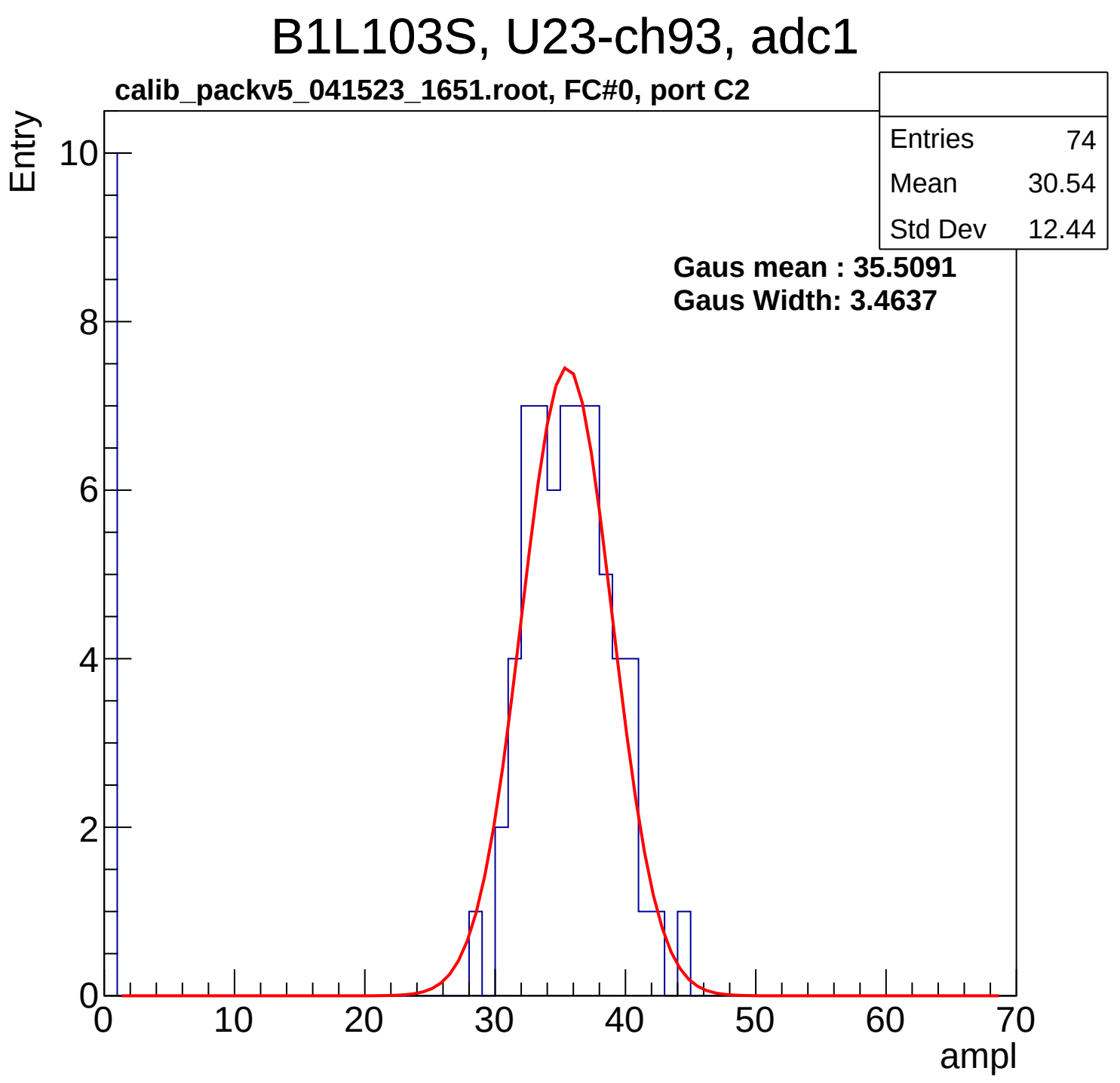

B1L103S, U23-ch92, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 52.16
Std Dev 2.883
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

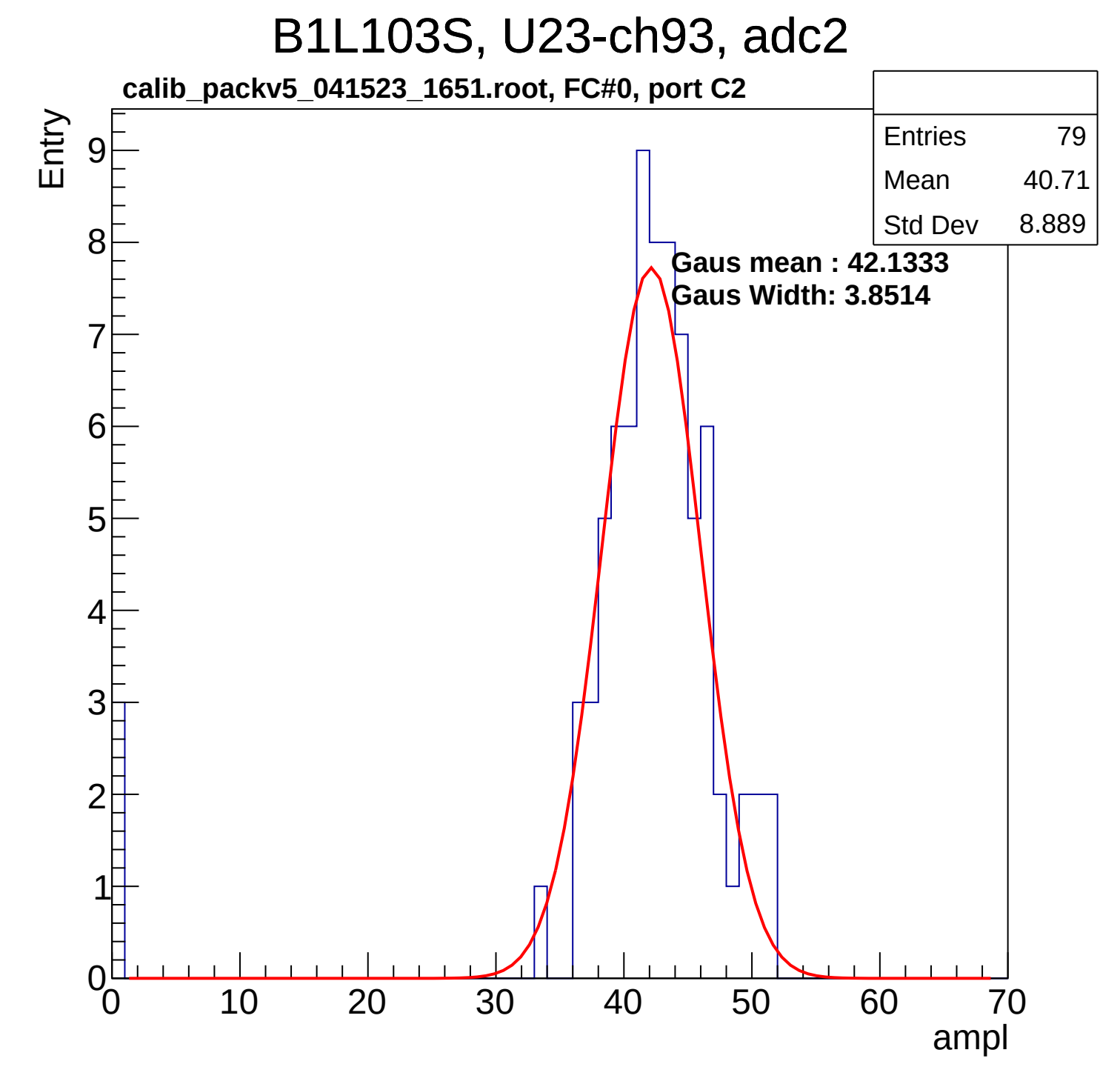

B1L103S, U23-ch92, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 57.3
Std Dev 7.786
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

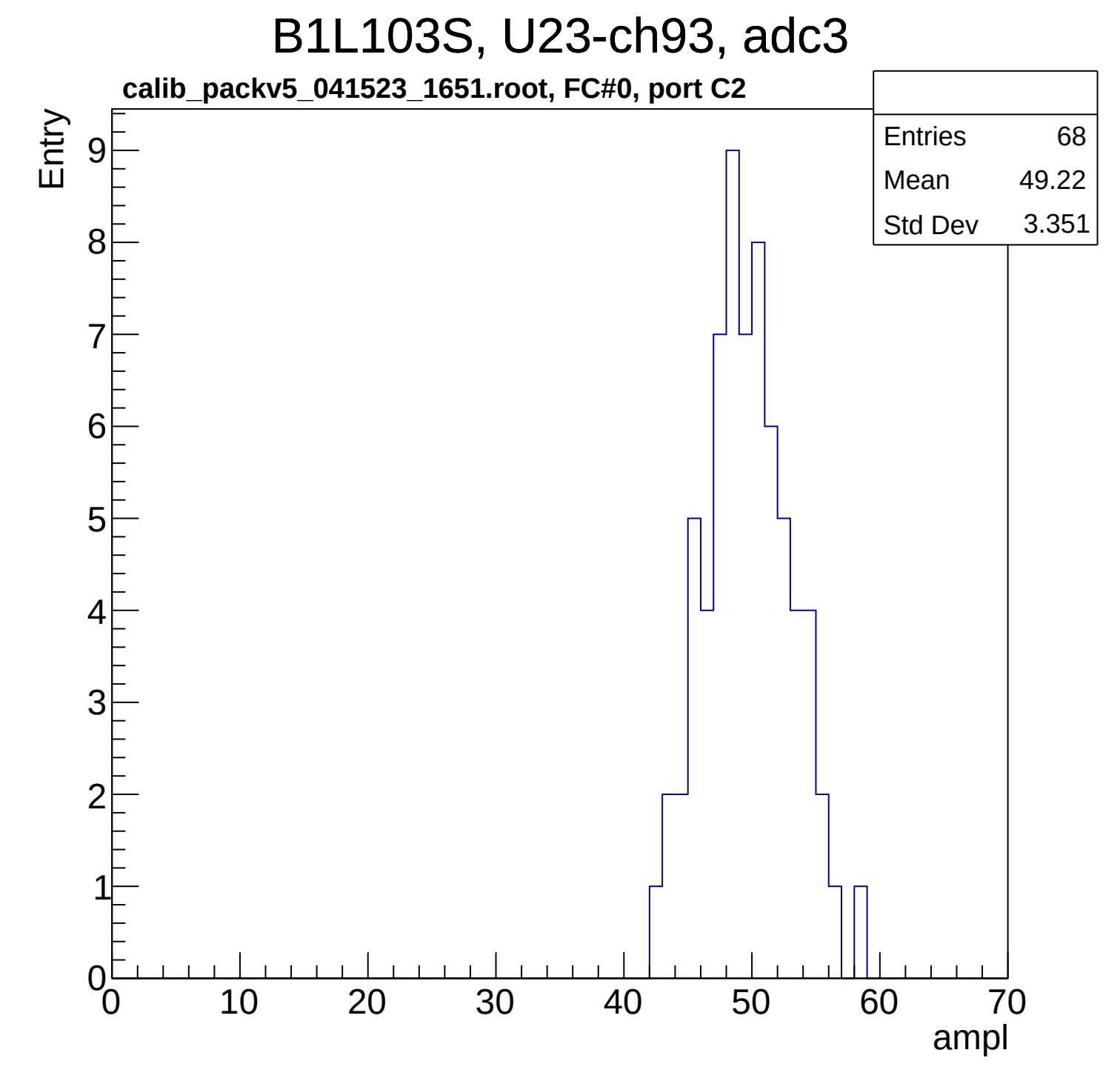

B1L103S, U23-ch92, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 60.48
Std Dev 2.26
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

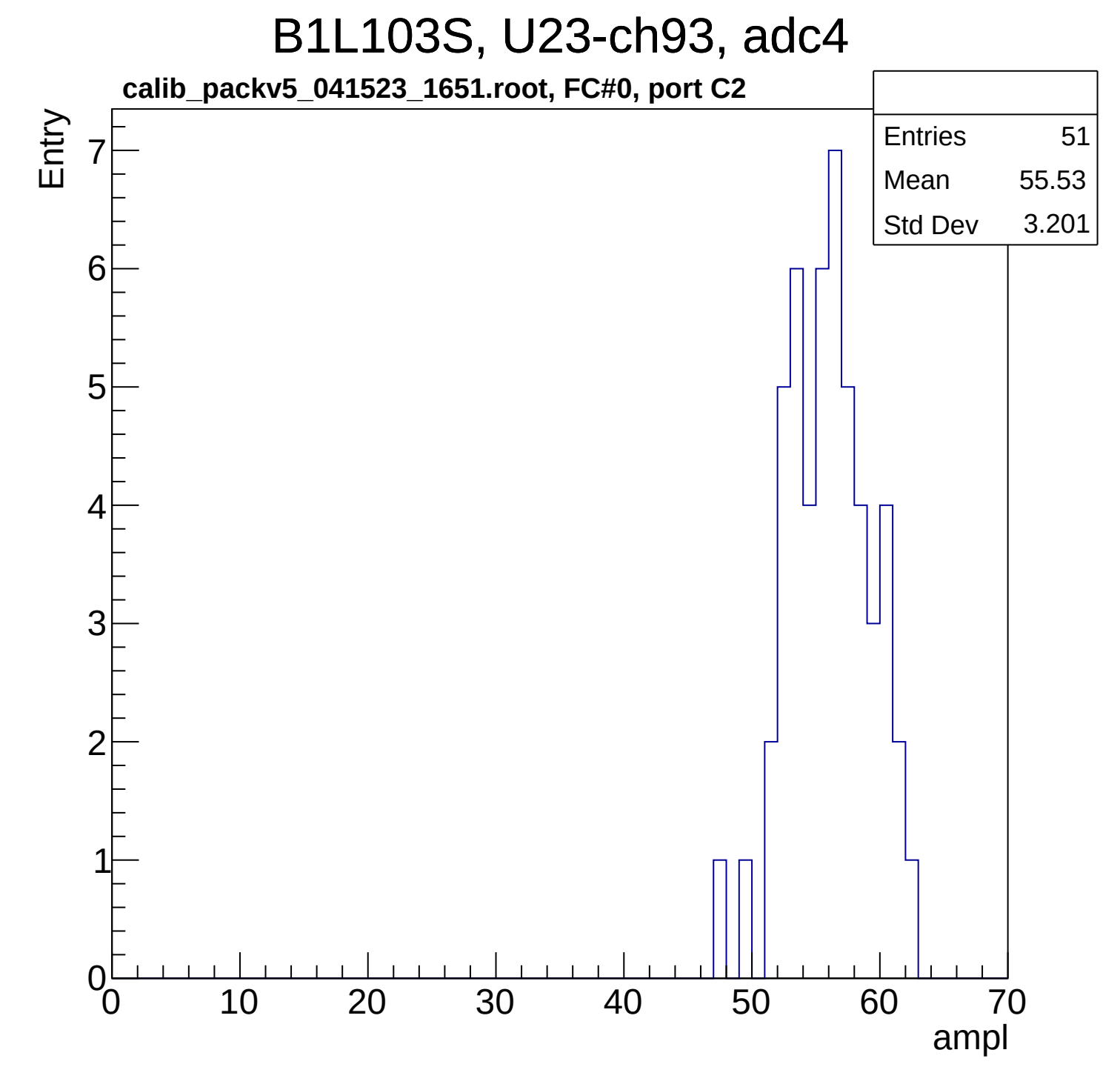

B1L103S, U23-ch92, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.952
Std Dev 18.35
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

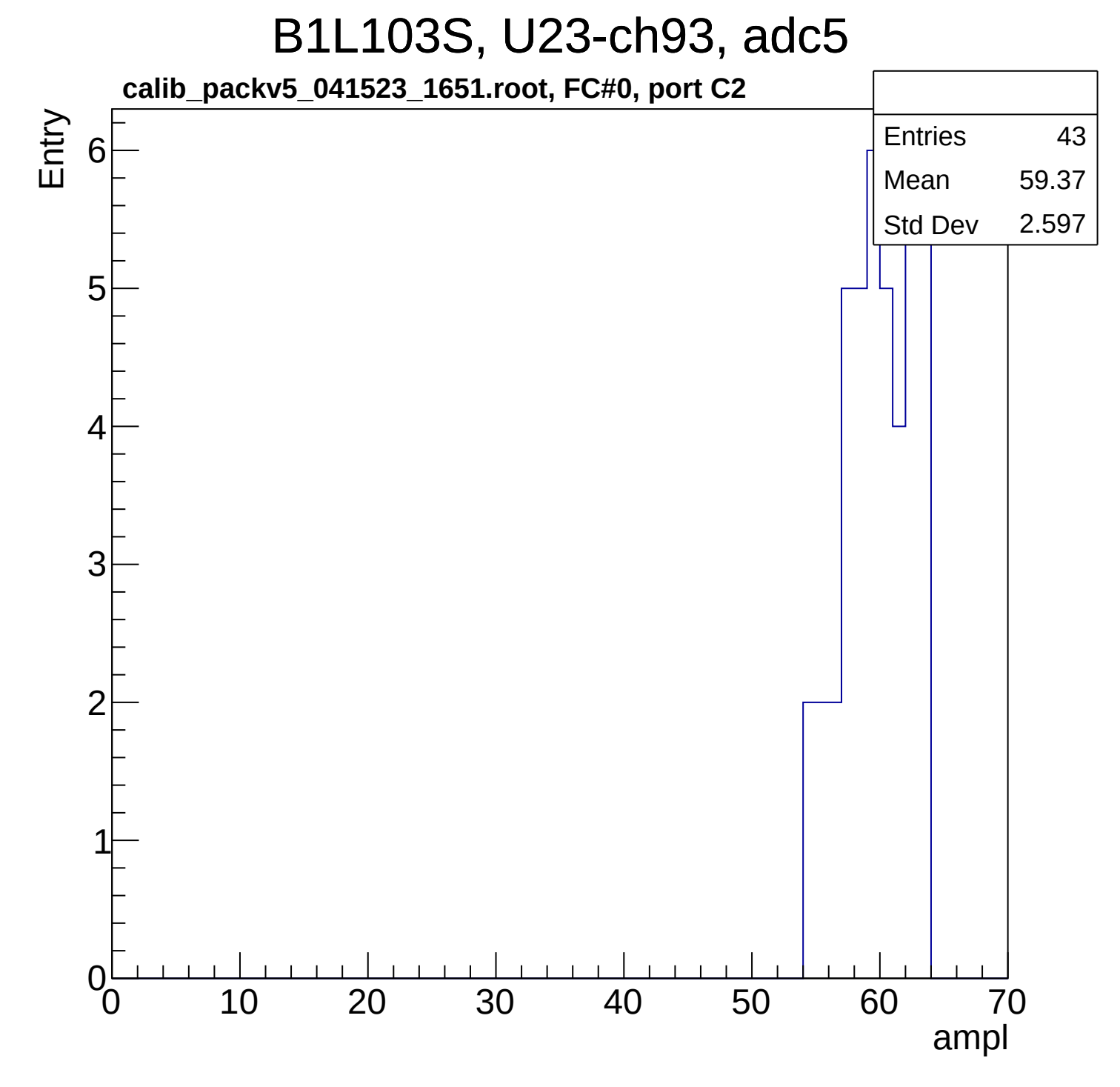

B1L103S, U23-ch93, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 98
Mean 21.4
Std Dev 12.27
Gaus mean : 28.4072
Gaus Width: 3.8796
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
24
0
10
20
30
40
50
60
70

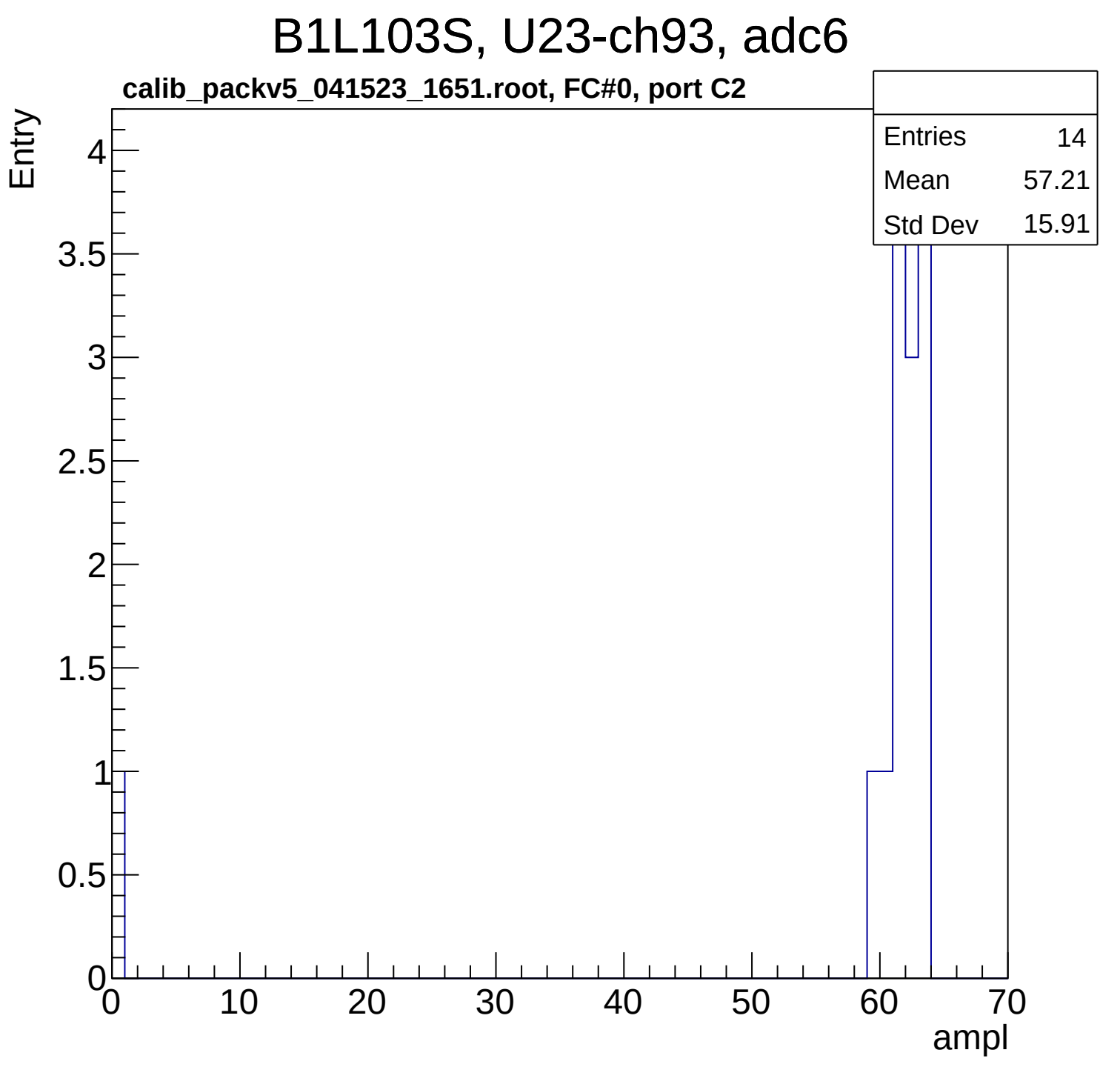

B1L103S, U23-ch93, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 30.54
Std Dev 12.44
Gaus mean : 35.5091
Gaus Width: 3.4637
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

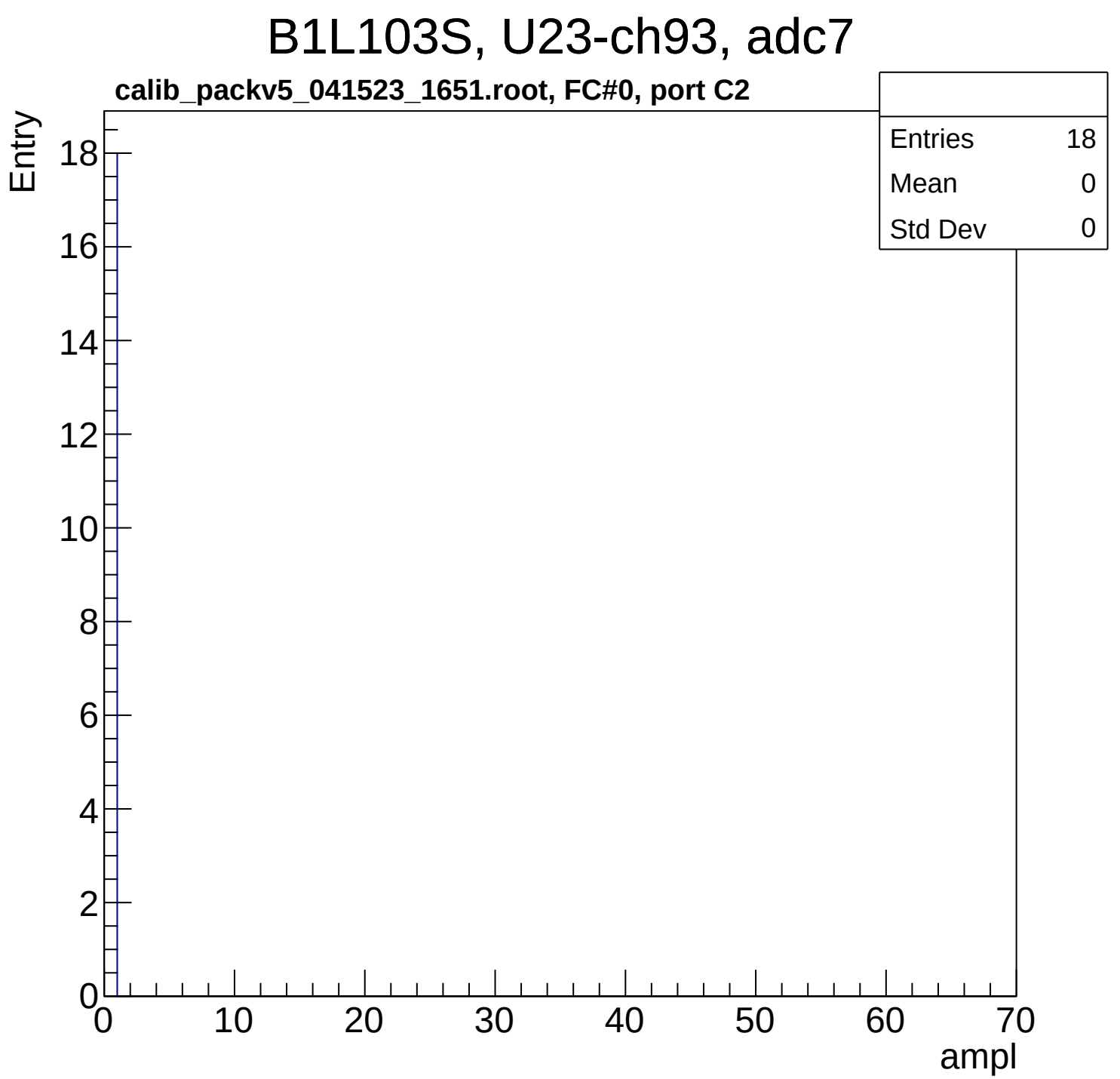

B1L103S, U23-ch93, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 40.71
Std Dev 8.889
Gaus mean : 42.1333
Gaus Width: 3.8514
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

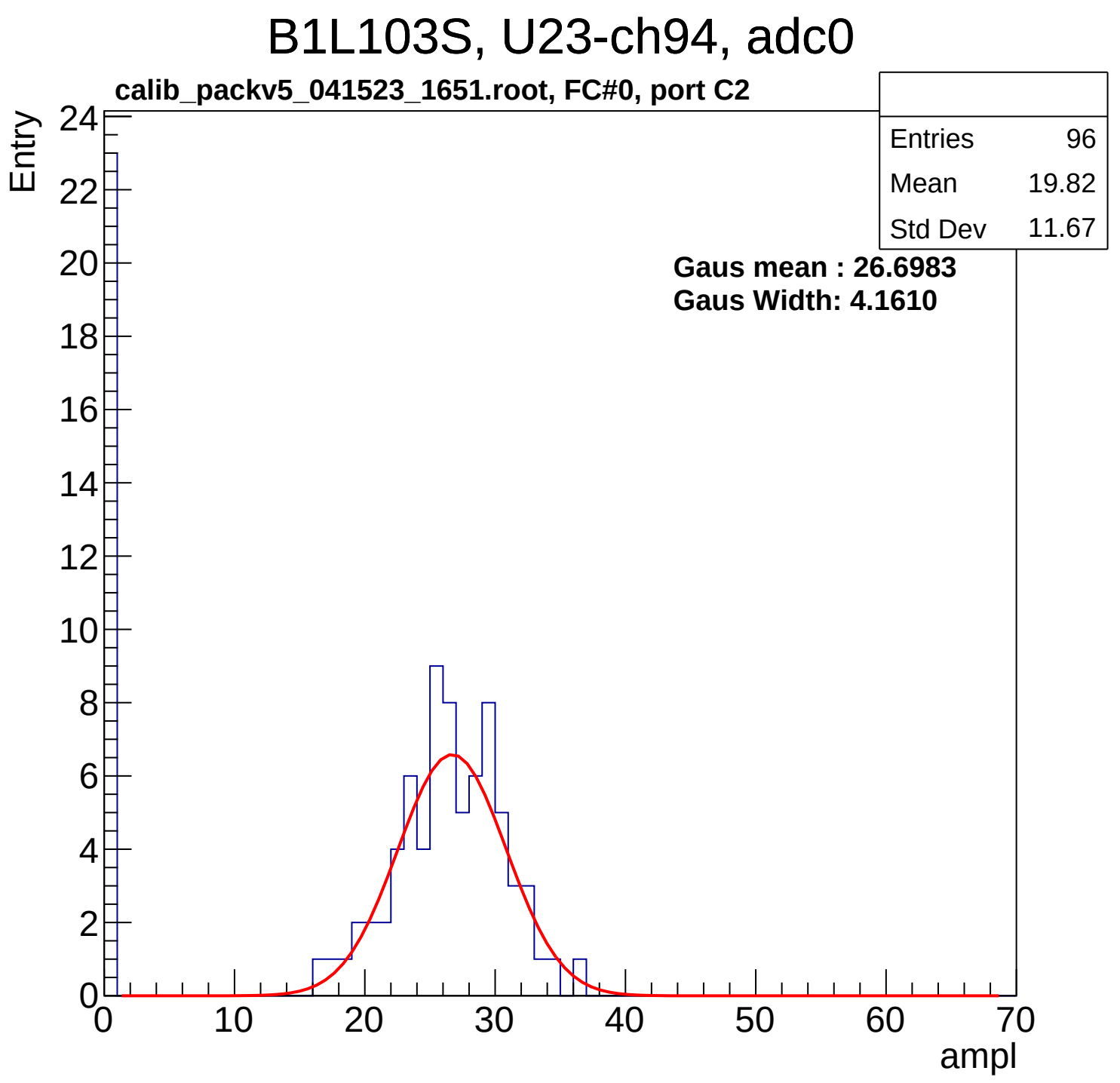

B1L103S, U23-ch93, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 49.22
Std Dev 3.351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

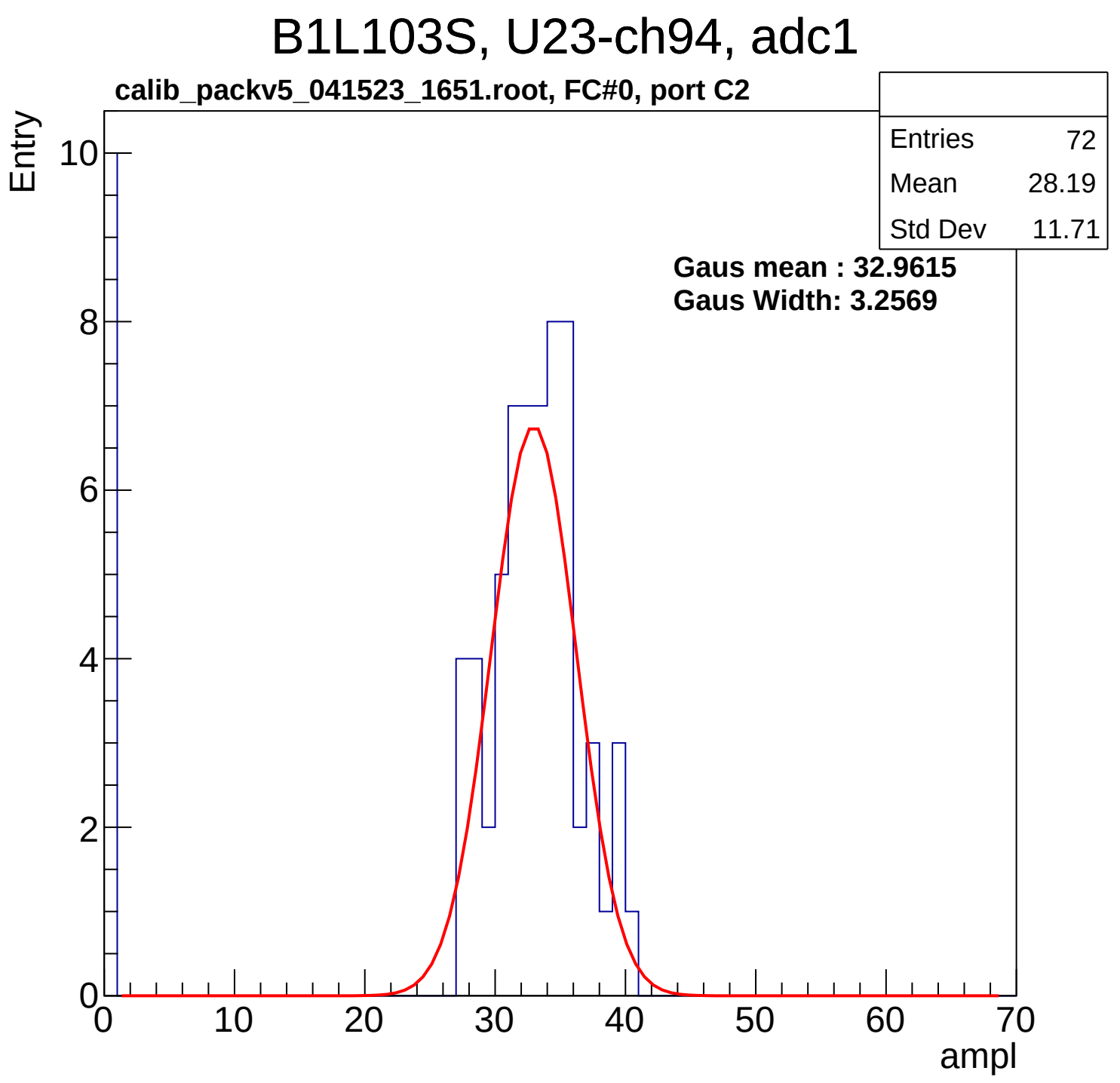

B1L103S, U23-ch93, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 55.53
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

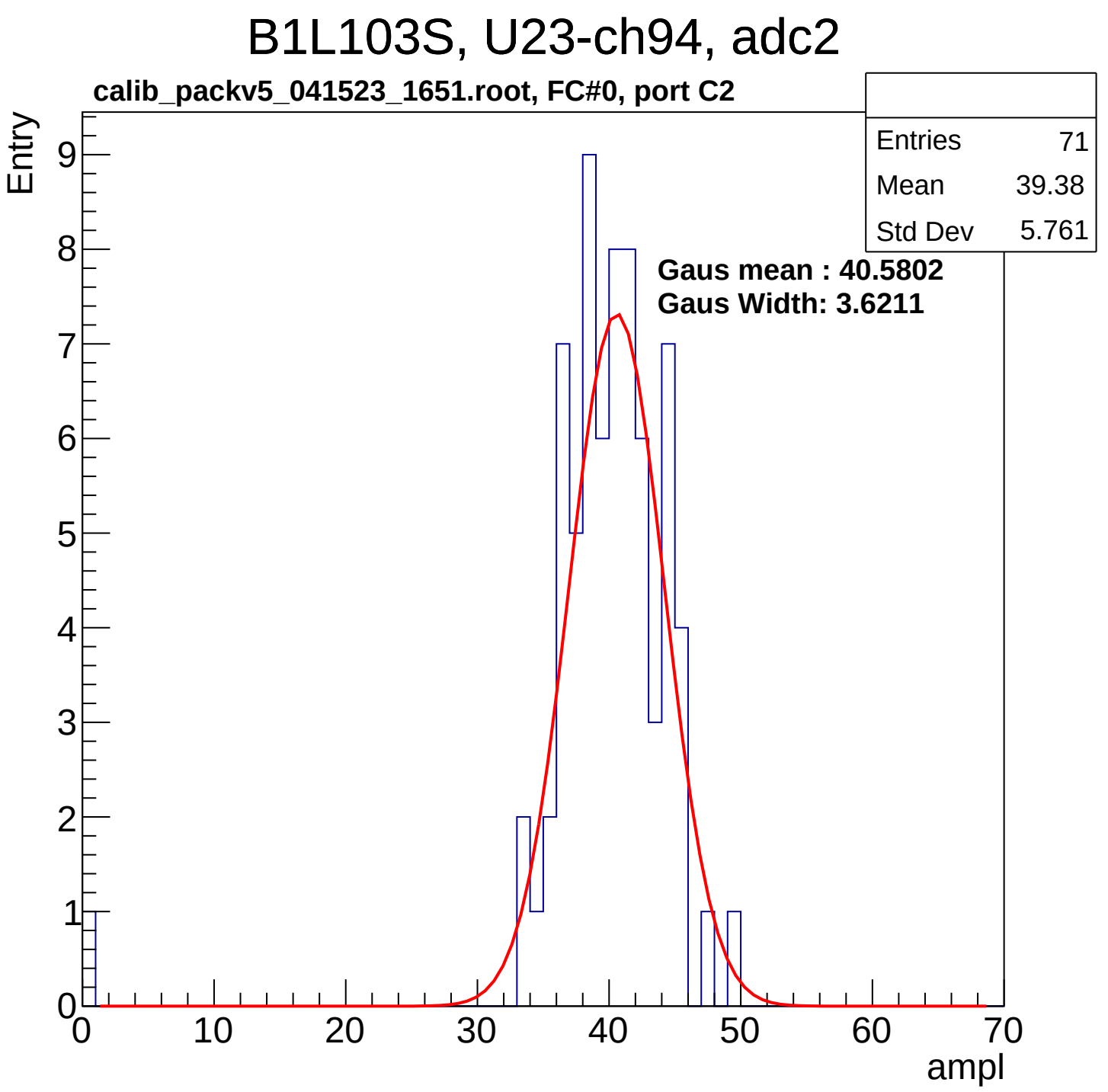

B1L103S, U23-ch93, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 43
Mean 59.37
Std Dev 2.597
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

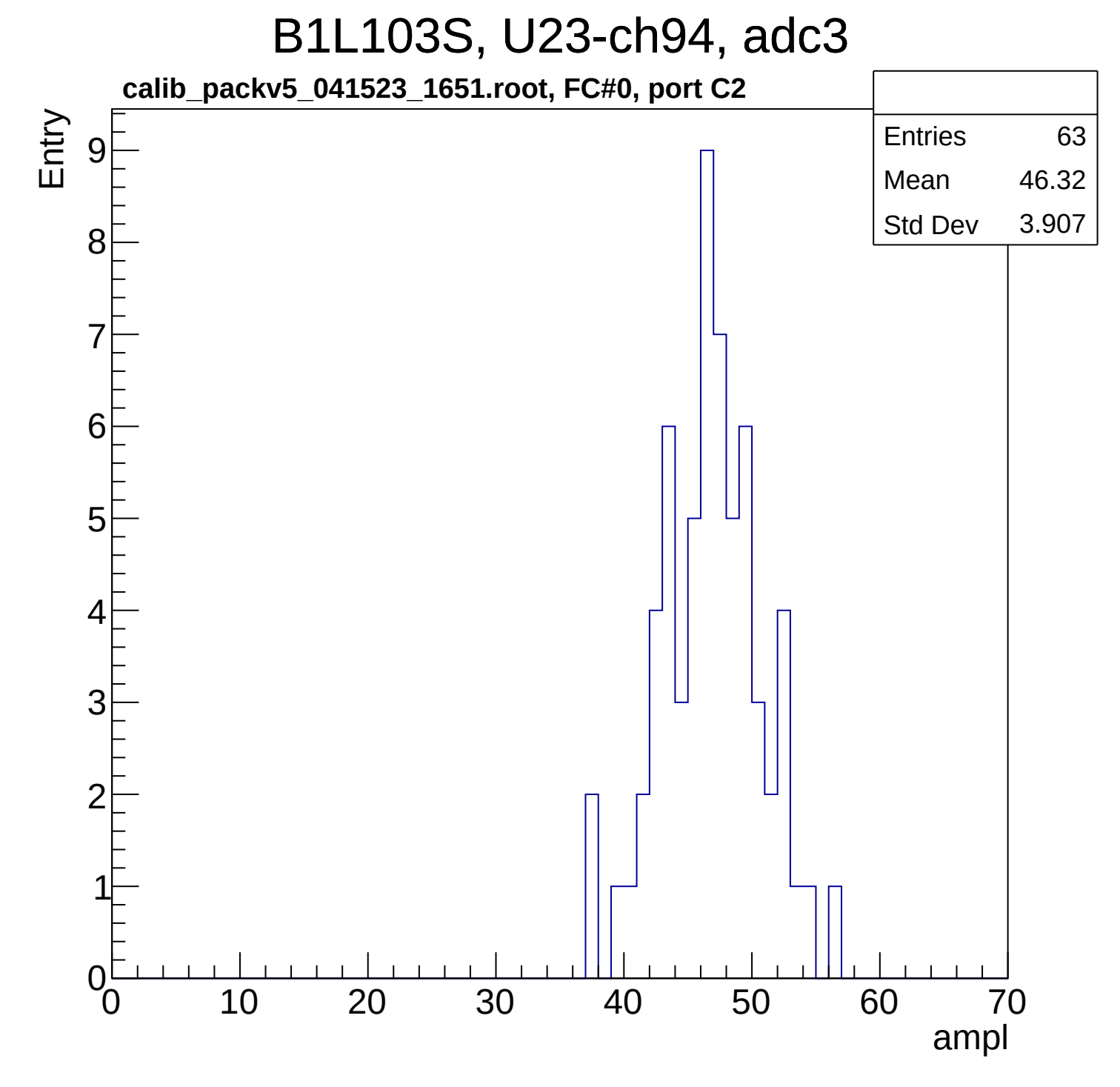

B1L103S, U23-ch93, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 14
Mean 57.21
Std Dev 15.91
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

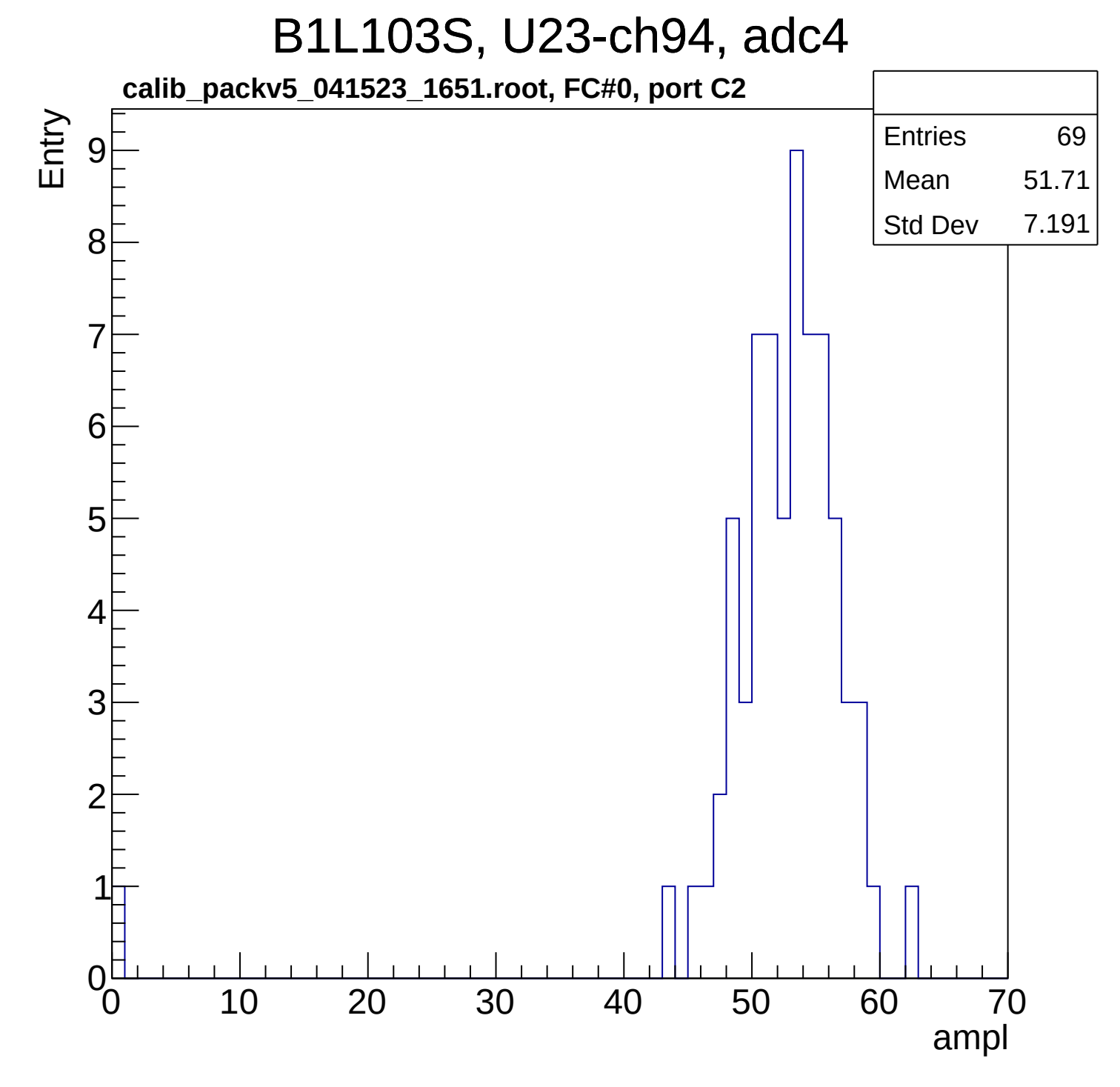

B1L103S, U23-ch93, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

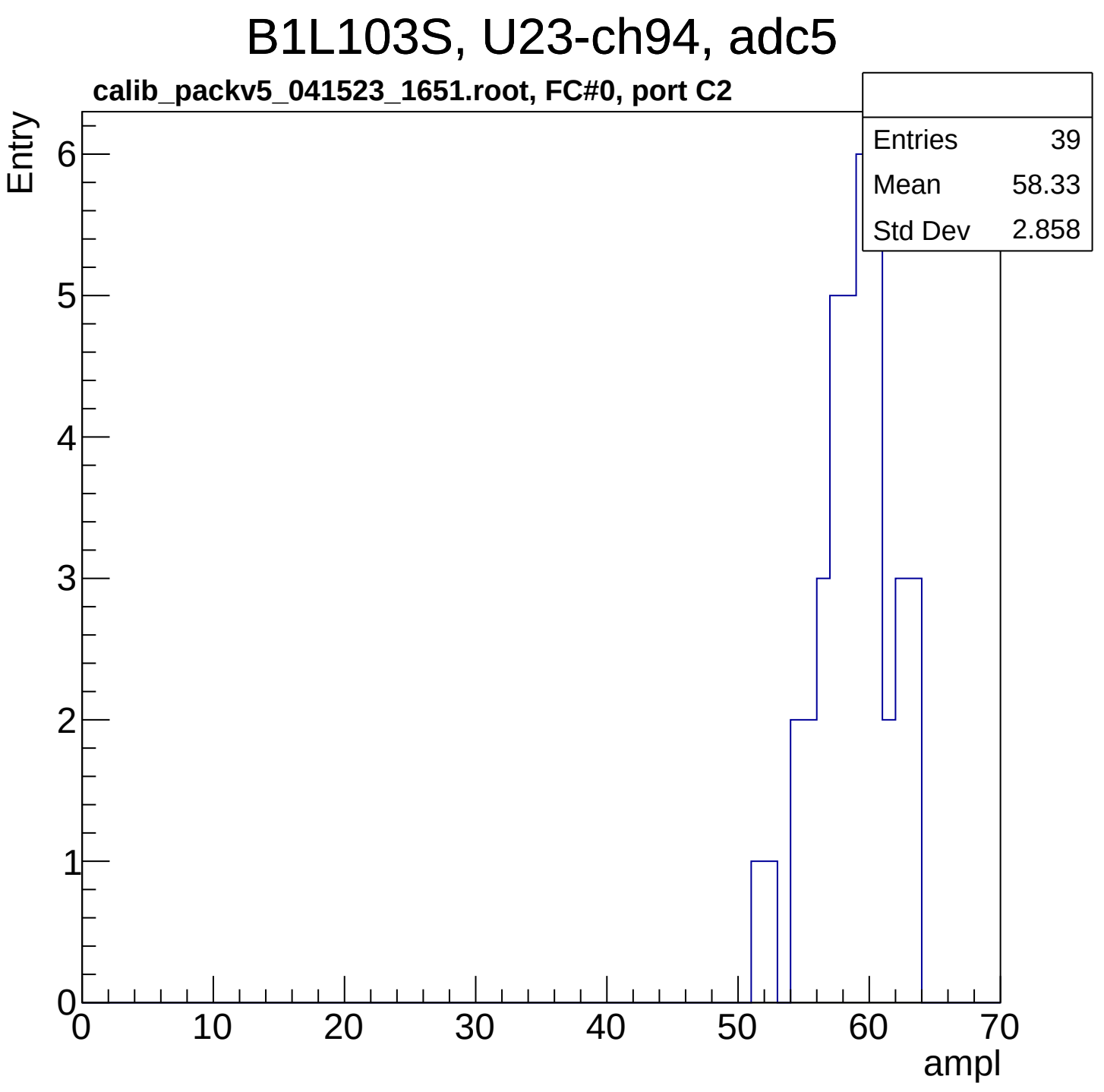

B1L103S, U23-ch94, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 96
Mean 19.82
Std Dev 11.67
Gaus mean : 26.6983
Gaus Width: 4.1610
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
24
0
10
20
30
40
50
60
70

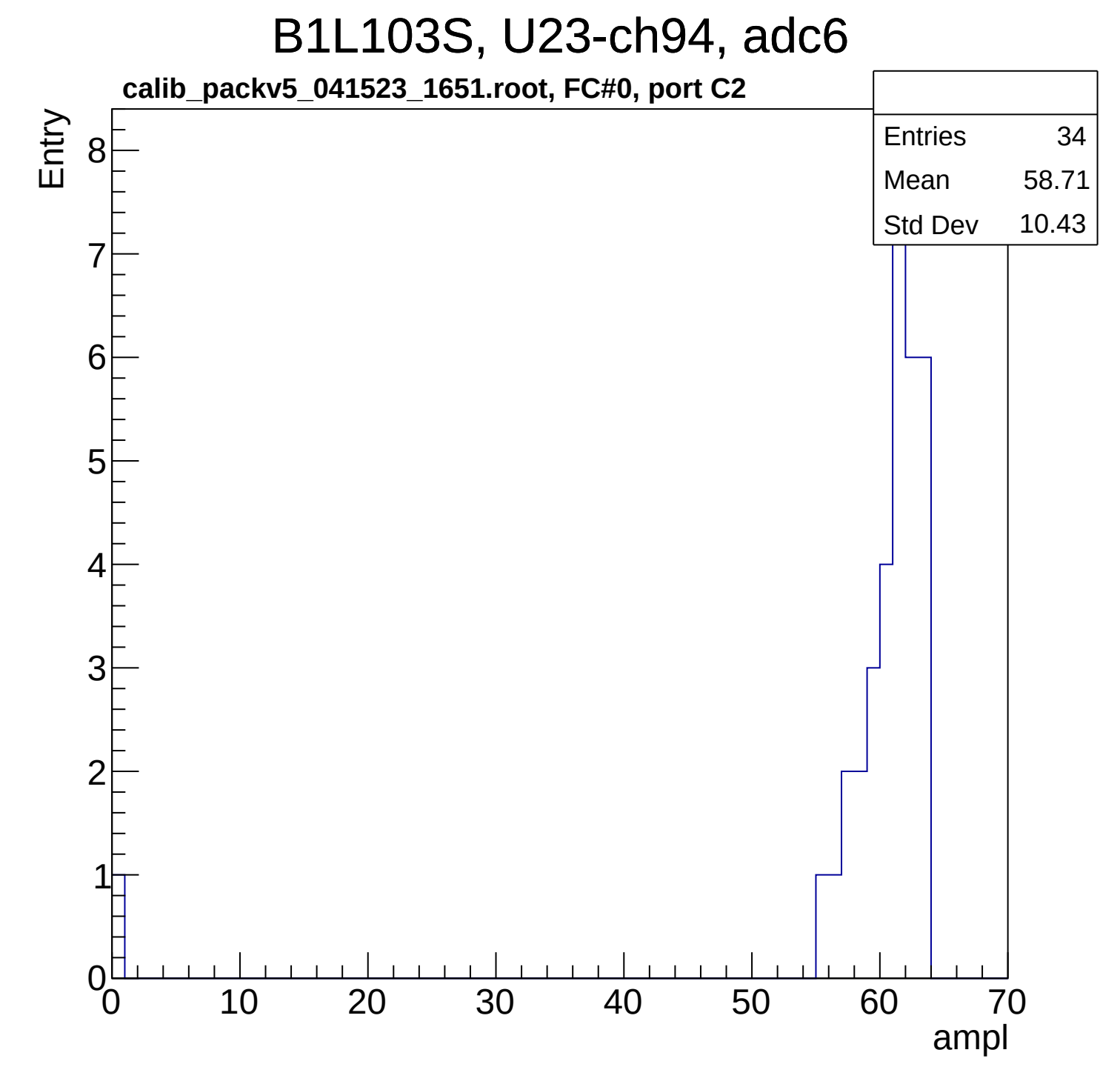

B1L103S, U23-ch94, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 28.19
Std Dev 11.71
Gaus mean : 32.9615
Gaus Width: 3.2569
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

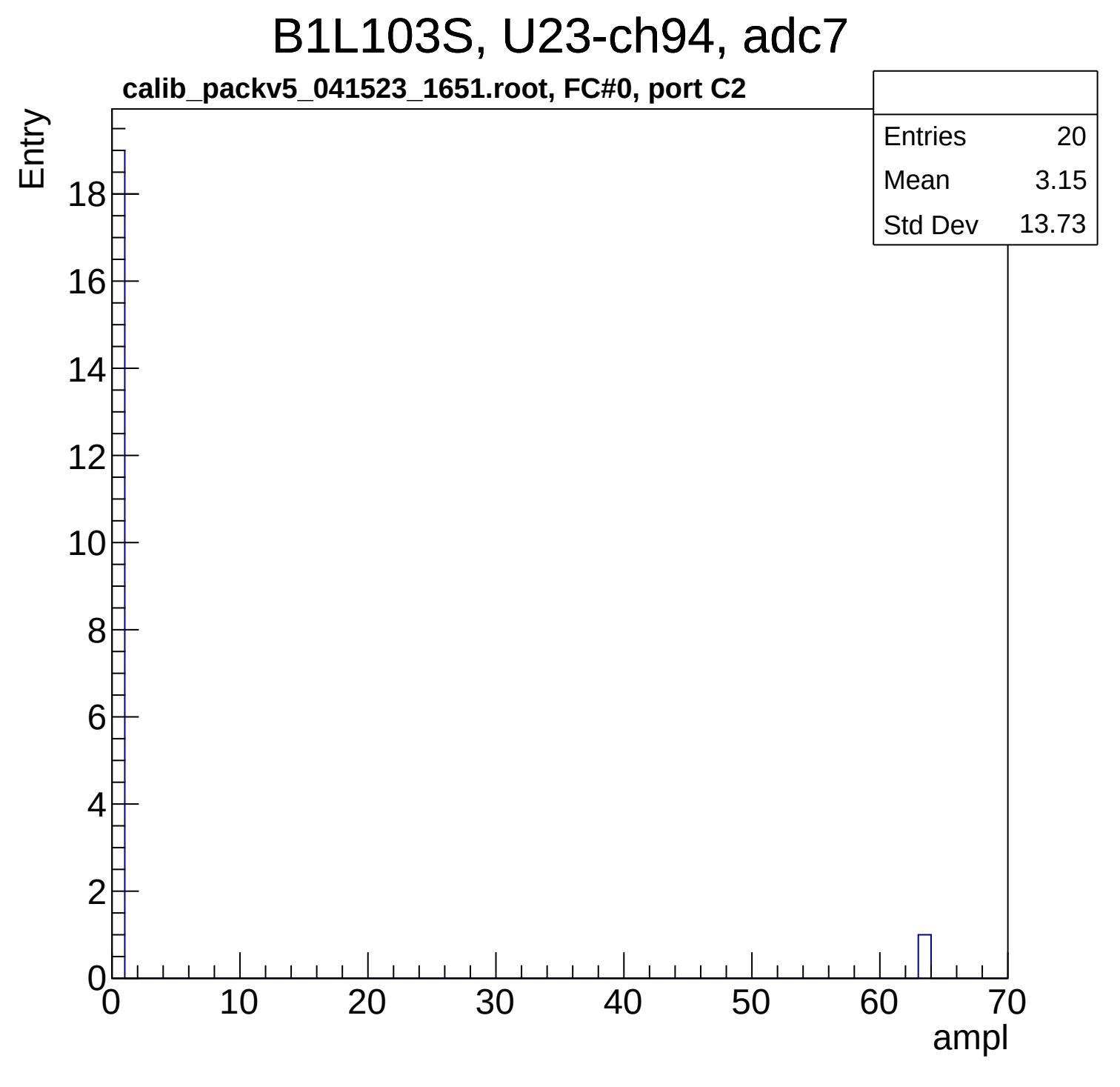

B1L103S, U23-ch94, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 39.38
Std Dev 5.761
Gaus mean : 40.5802
Gaus Width: 3.6211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

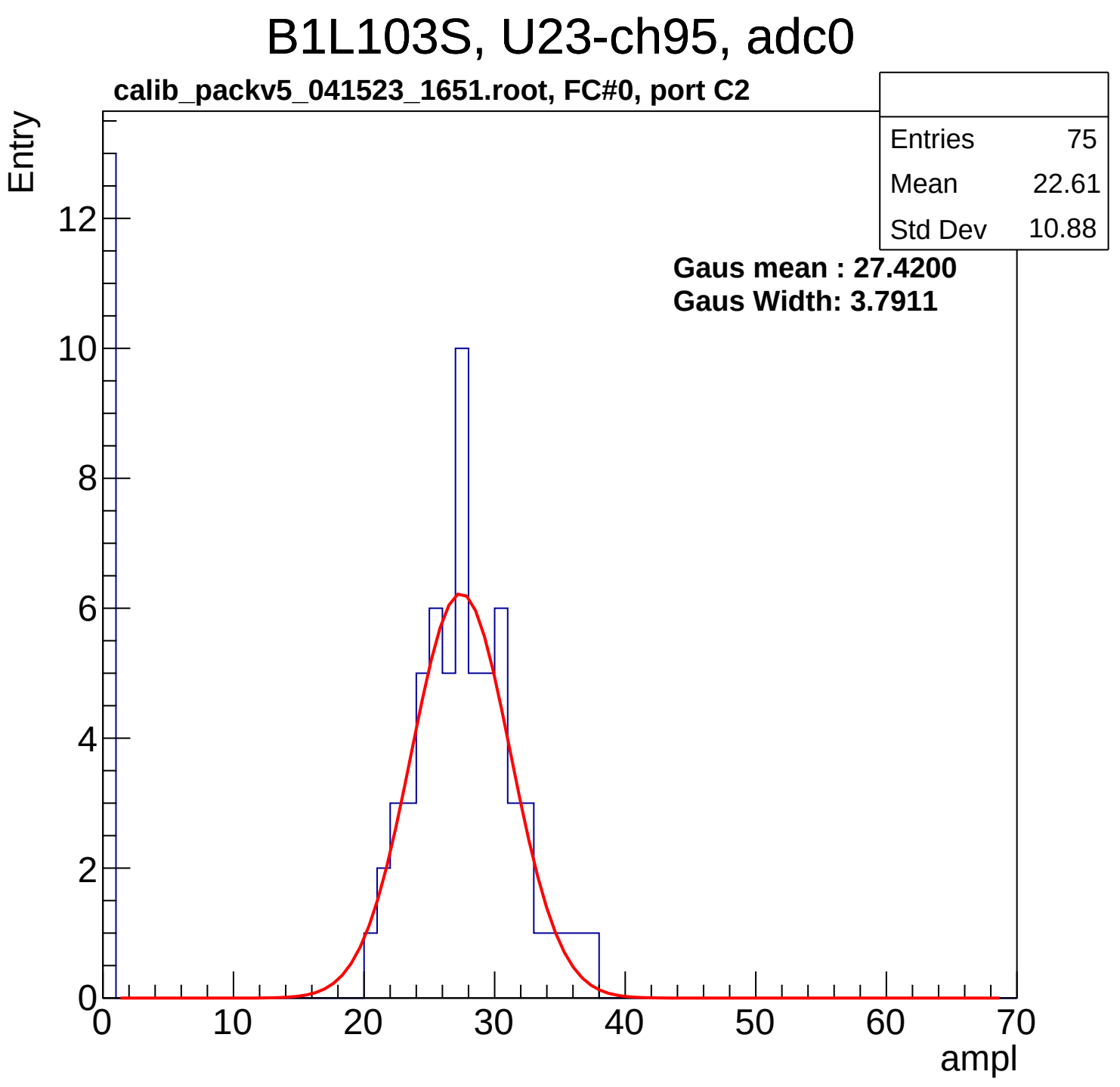

B1L103S, U23-ch94, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 46.32
Std Dev 3.907
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

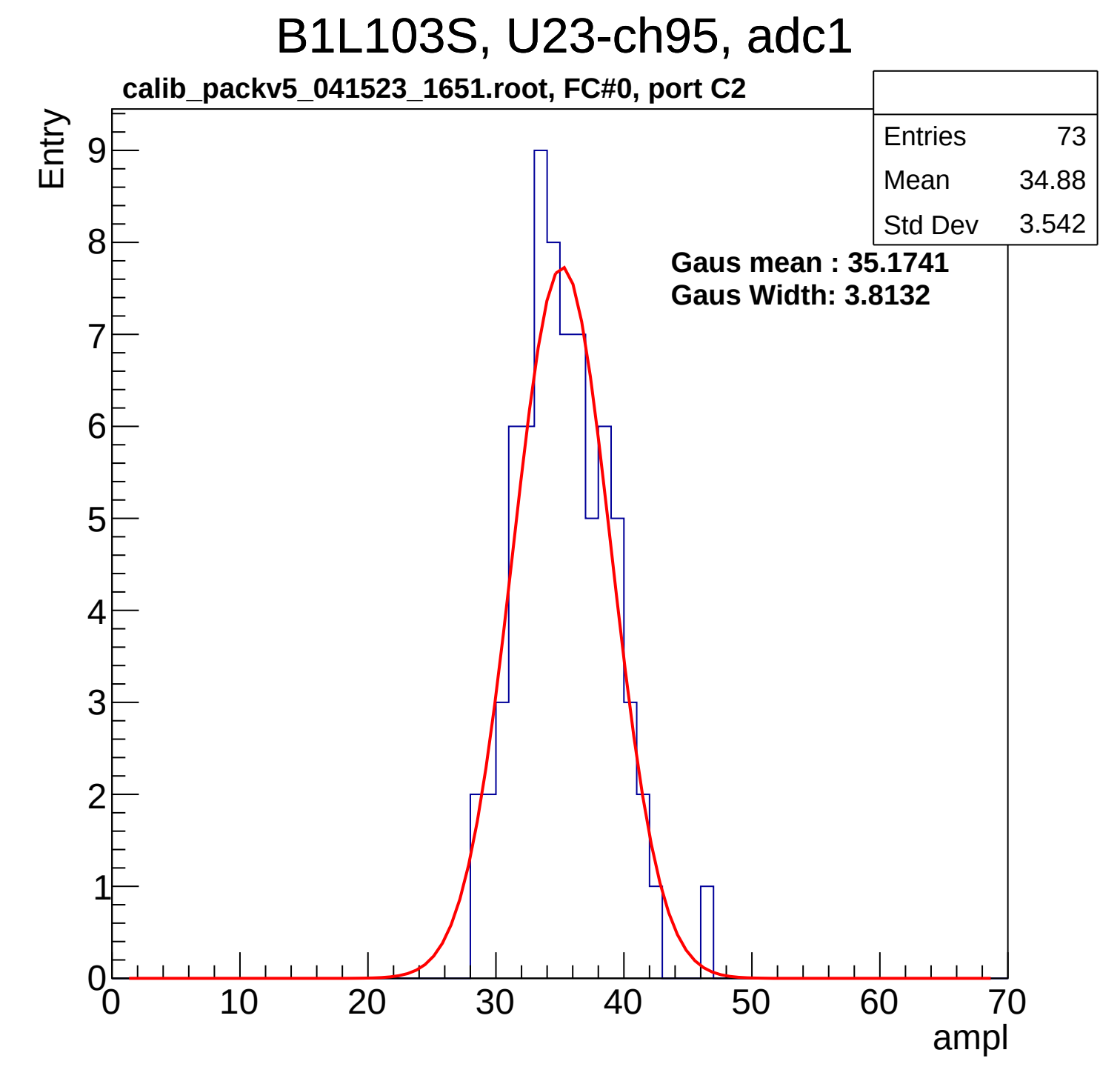

B1L103S, U23-ch94, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 51.71
Std Dev 7.191
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

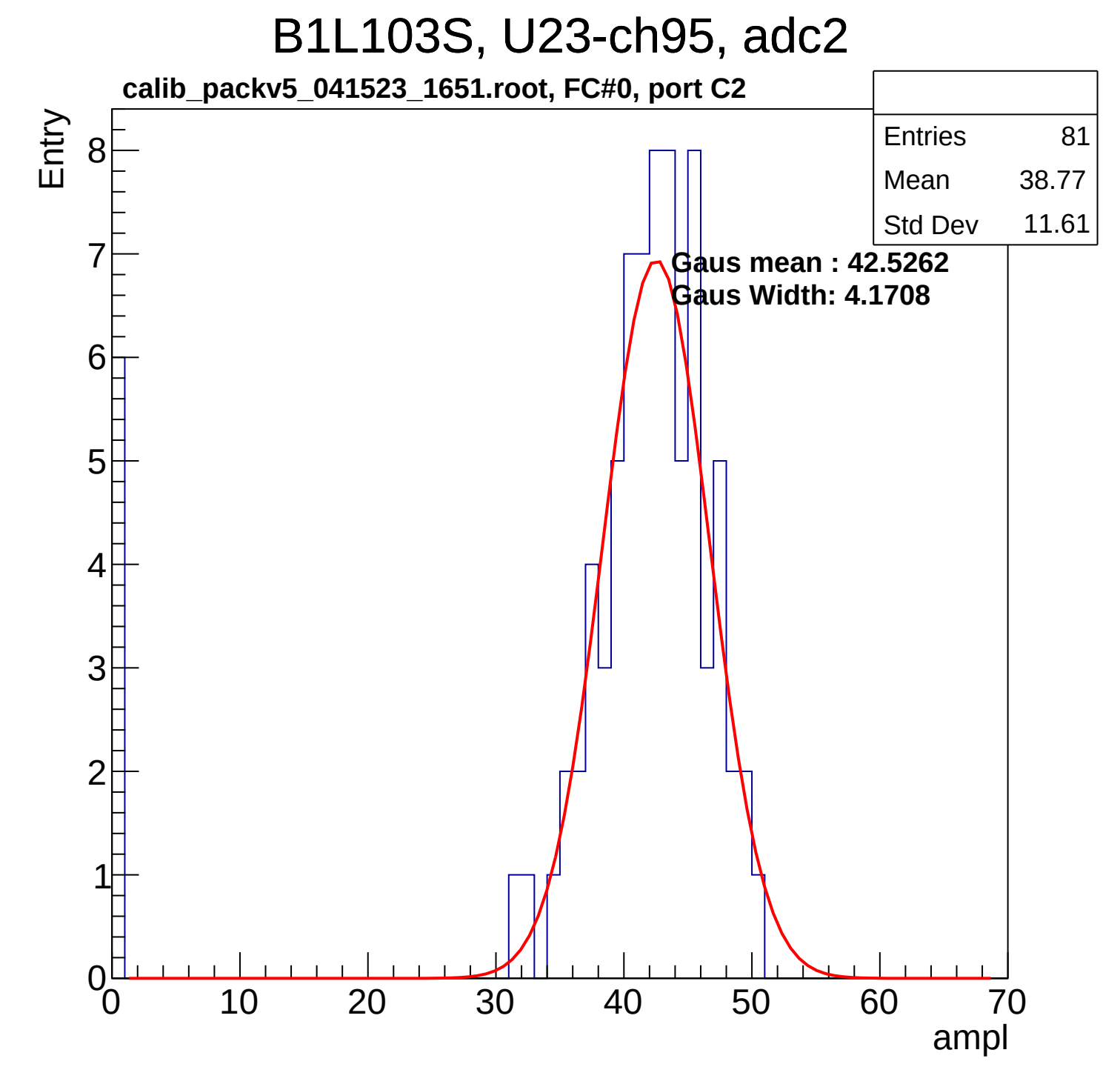

B1L103S, U23-ch94, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 39
Mean 58.33
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

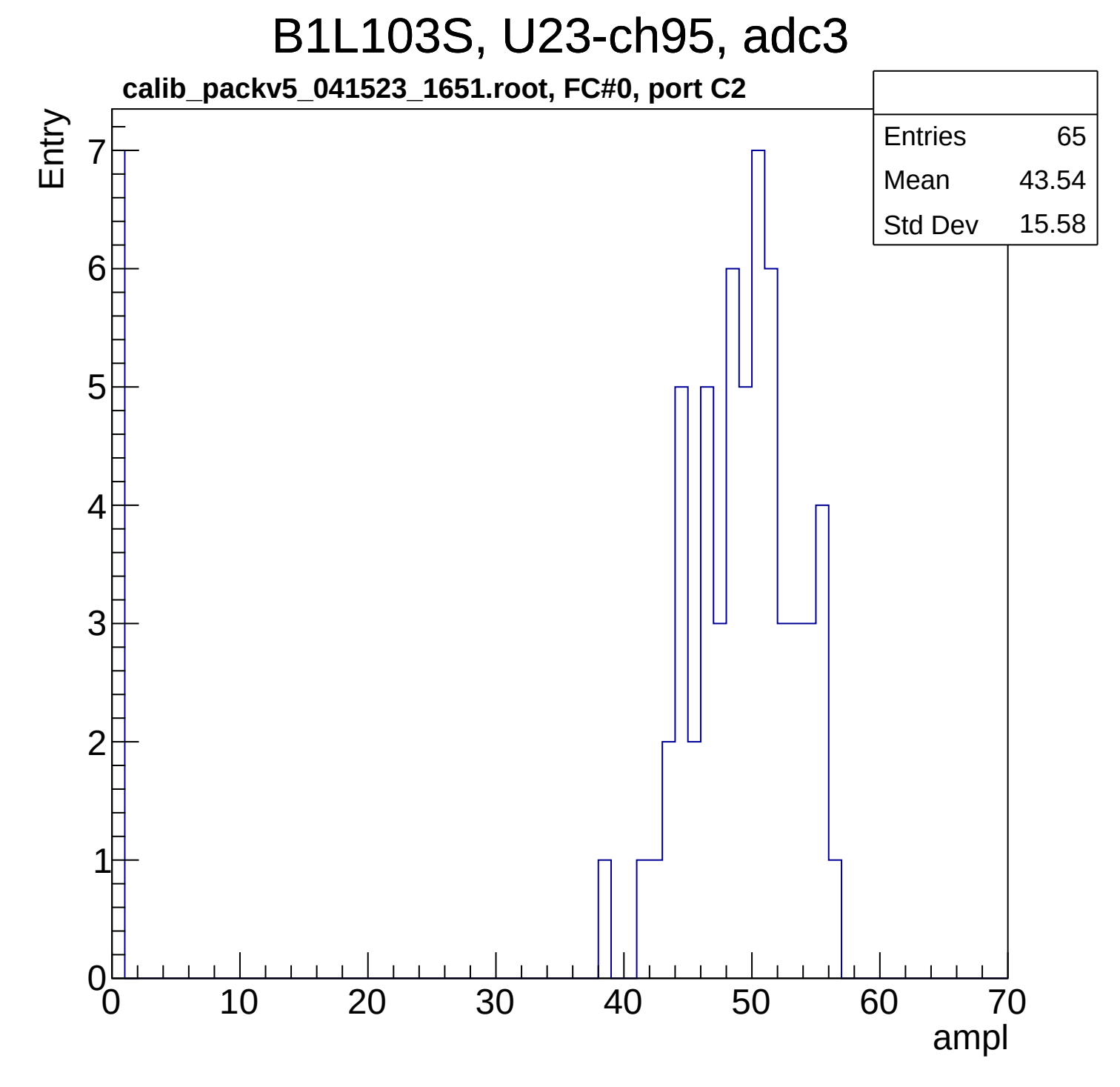

B1L103S, U23-ch94, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 34
Mean 58.71
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

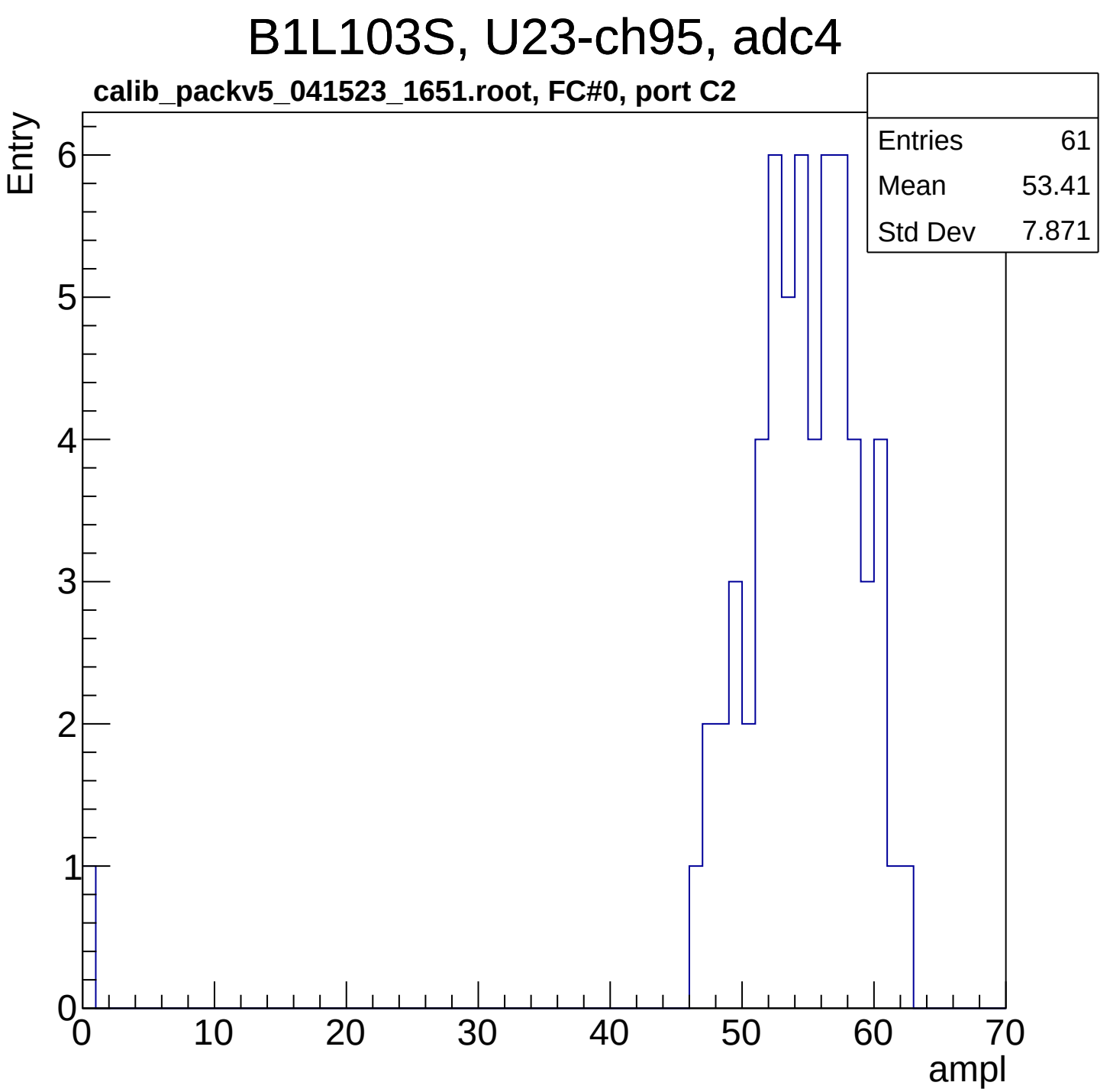

B1L103S, U23-ch94, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.15
Std Dev 13.73
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

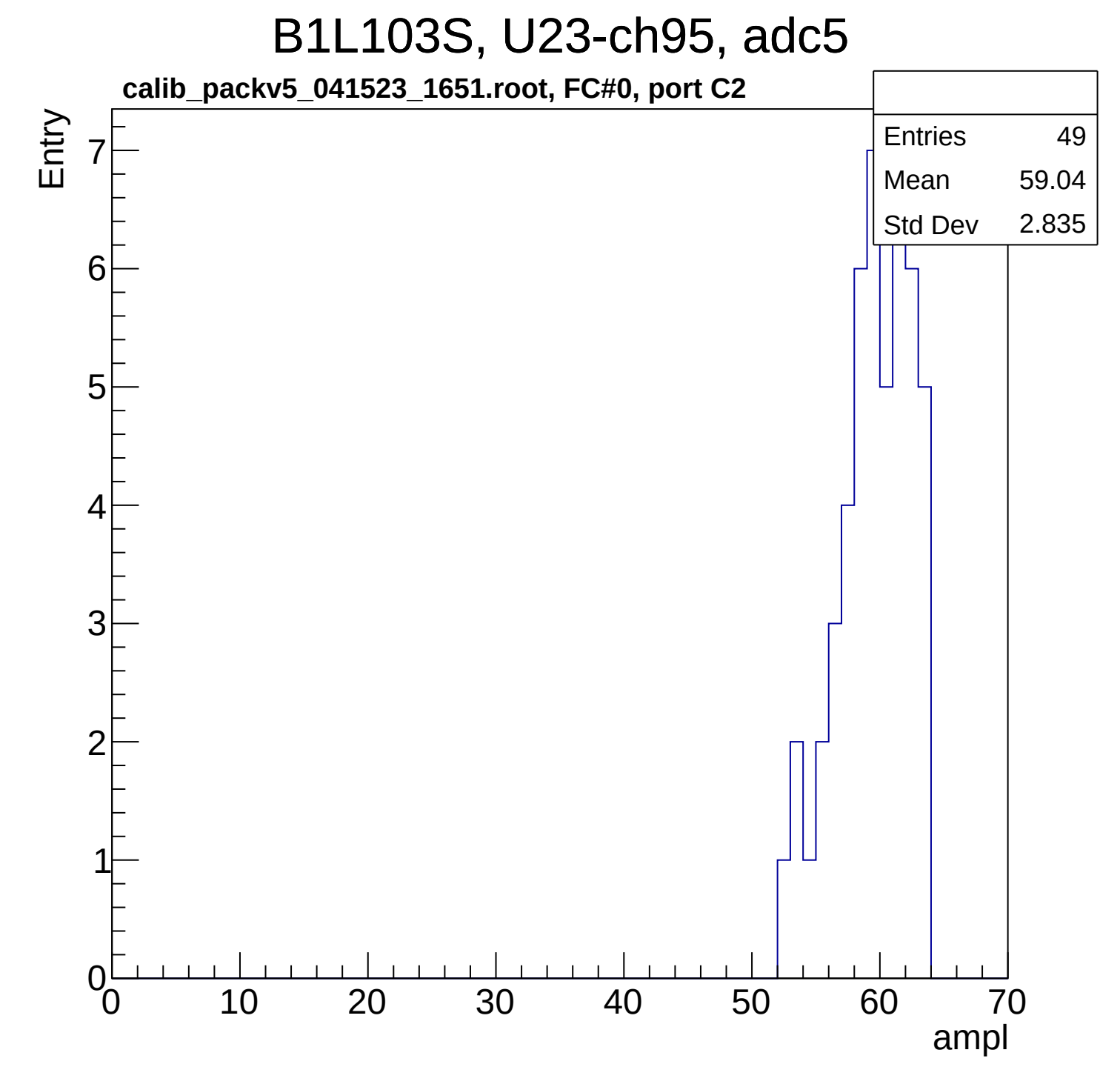

B1L103S, U23-ch95, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 22.61
Std Dev 10.88
Gaus mean : 27.4200
Gaus Width: 3.7911
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

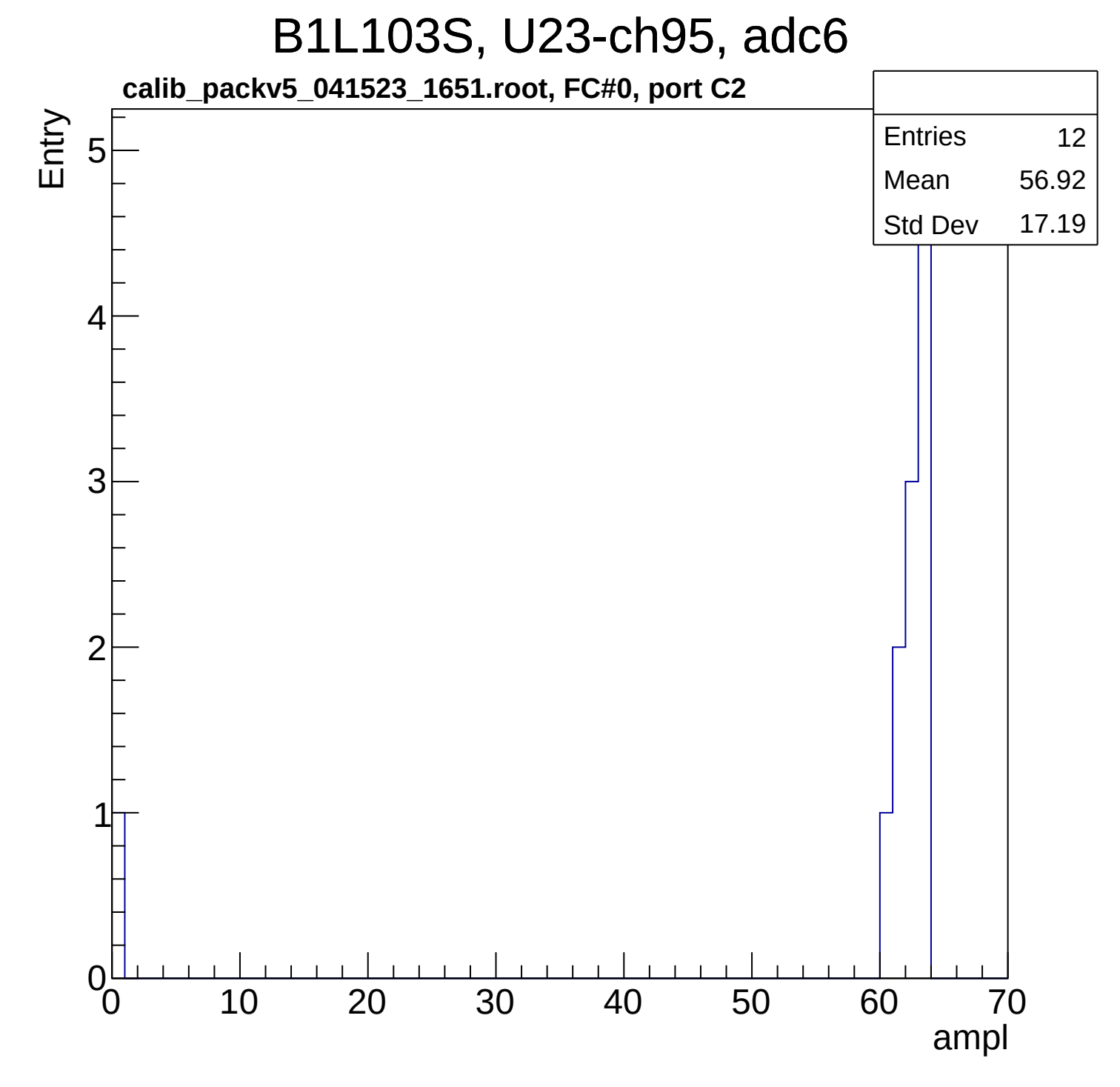

B1L103S, U23-ch95, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 34.88
Std Dev 3.542
Gaus mean : 35.1741
Gaus Width: 3.8132
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

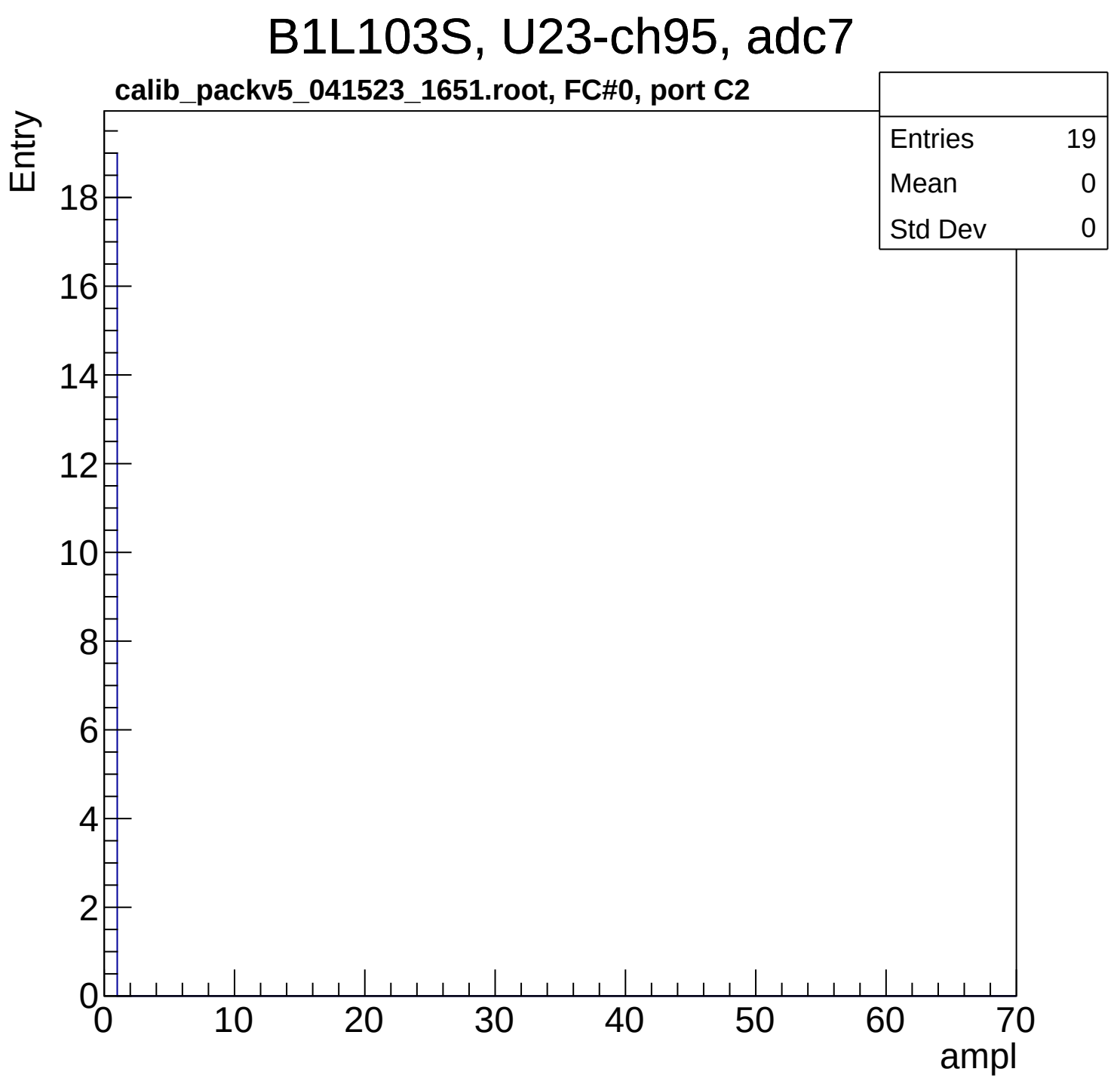

B1L103S, U23-ch95, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 38.77
Std Dev 11.61
Gaus mean : 42.5262
Gaus Width: 4.1708
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

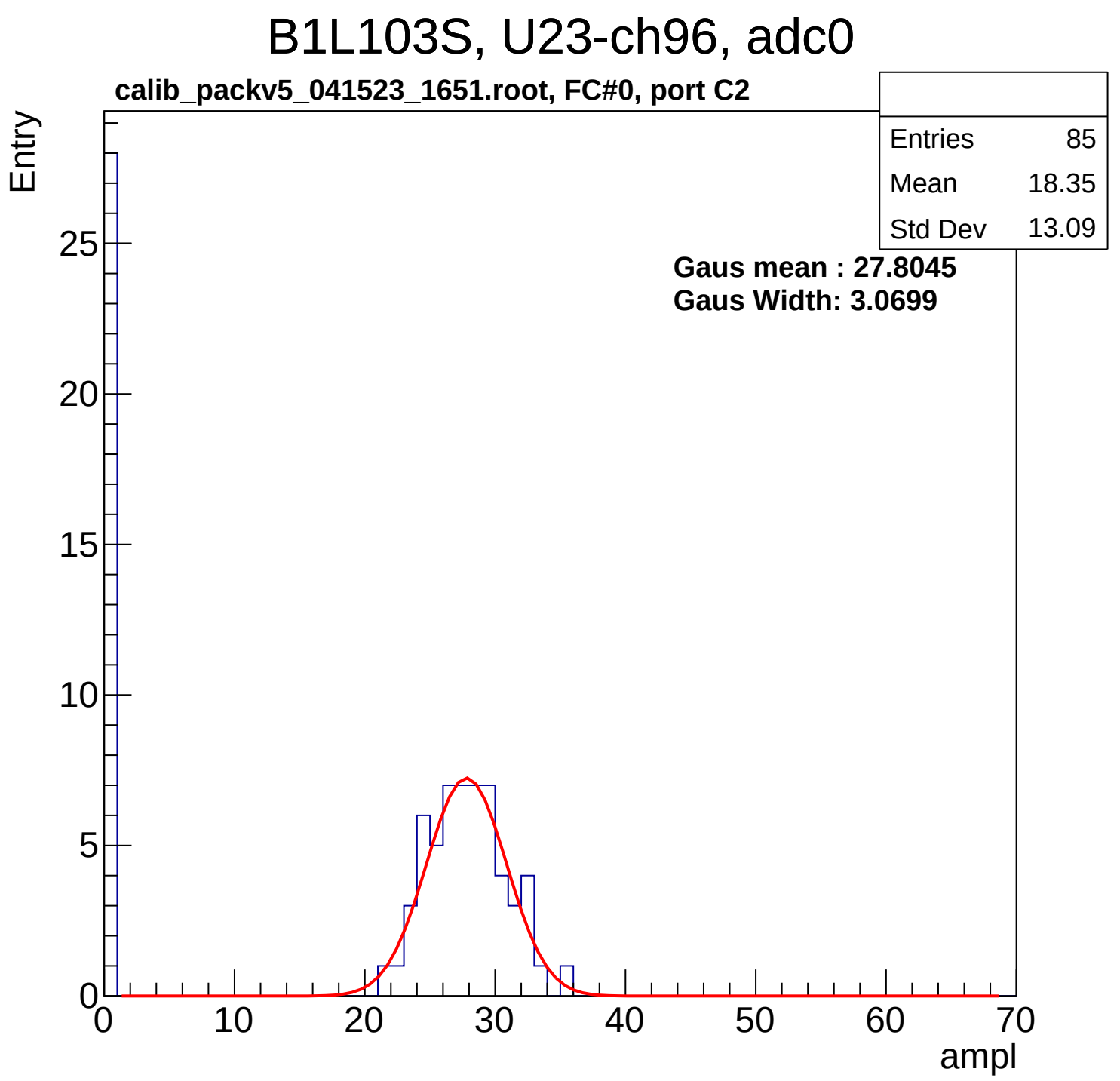

B1L103S, U23-ch95, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 43.54
Std Dev 15.58
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

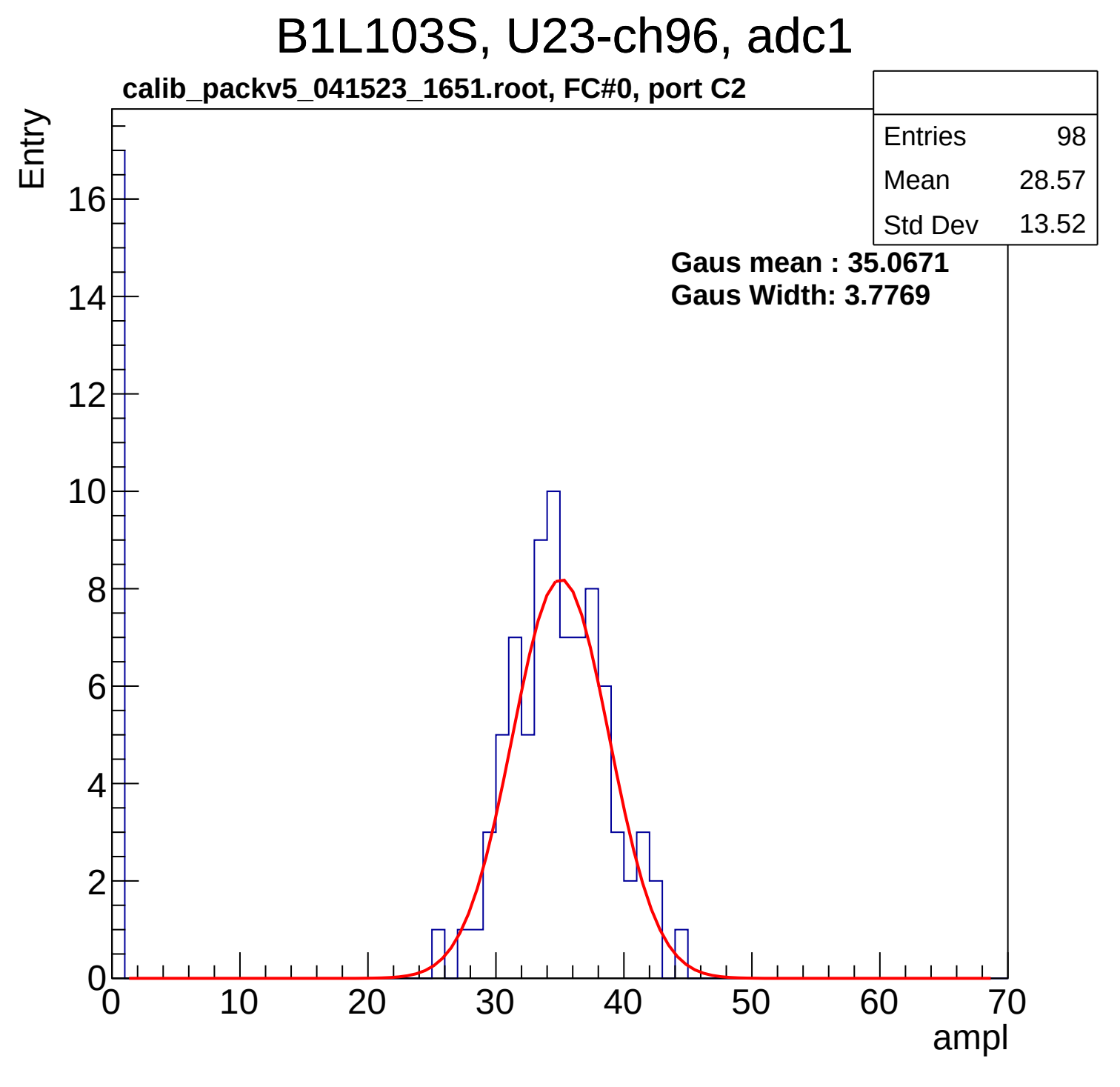

B1L103S, U23-ch95, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 53.41
Std Dev 7.871
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

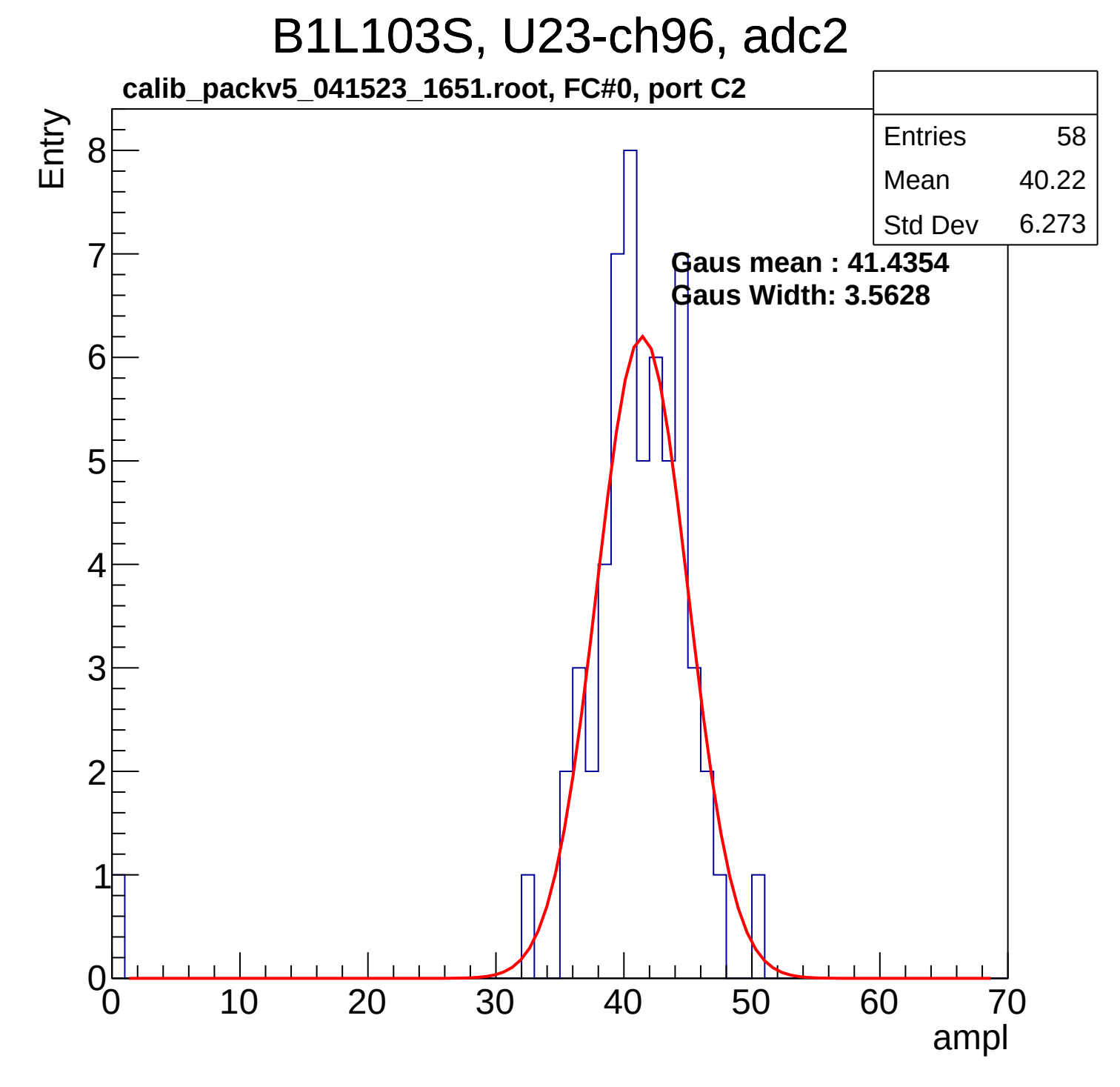

B1L103S, U23-ch95, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 59.04
Std Dev 2.835
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

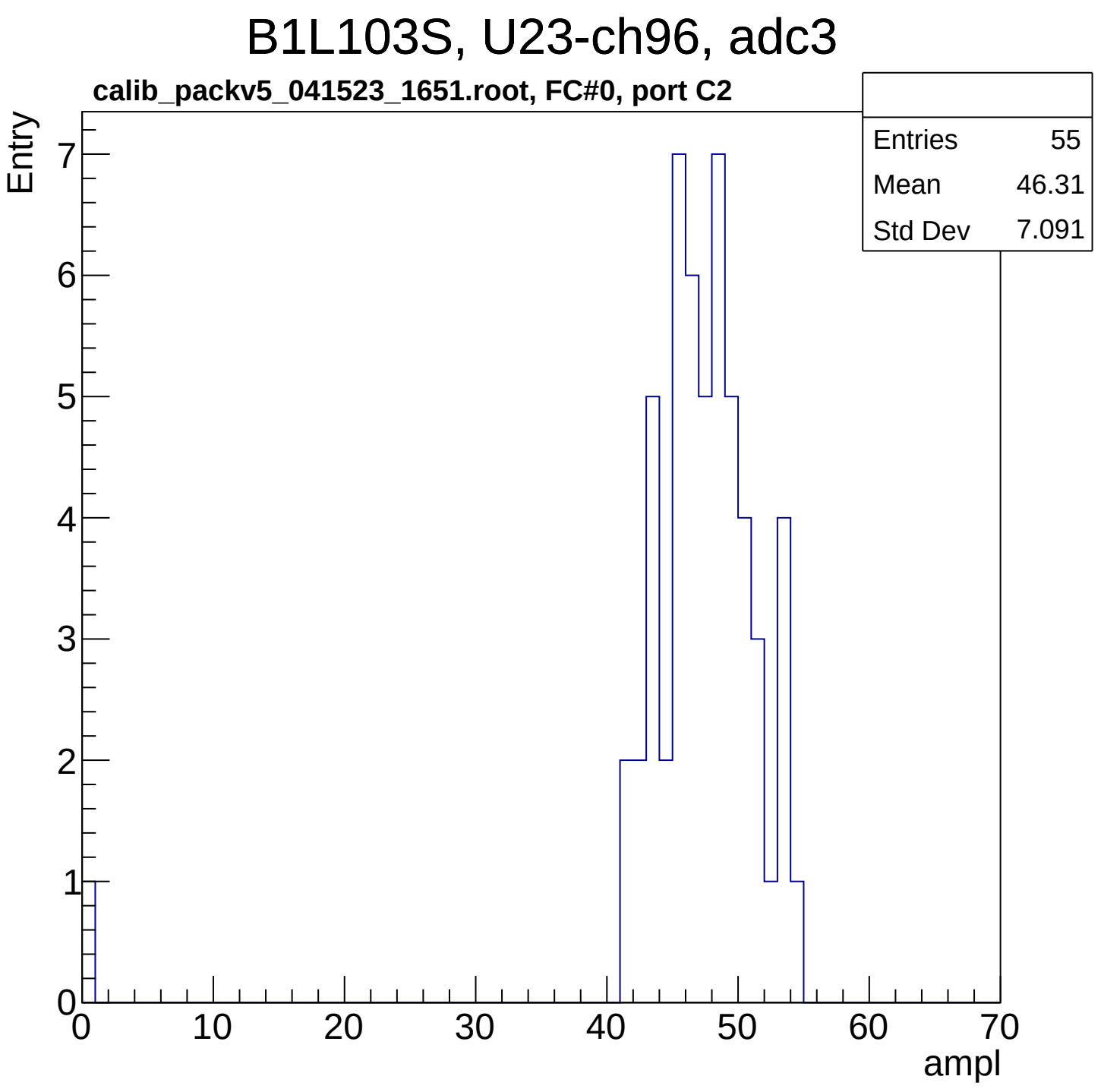

B1L103S, U23-ch95, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 12
Mean 56.92
Std Dev 17.19
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

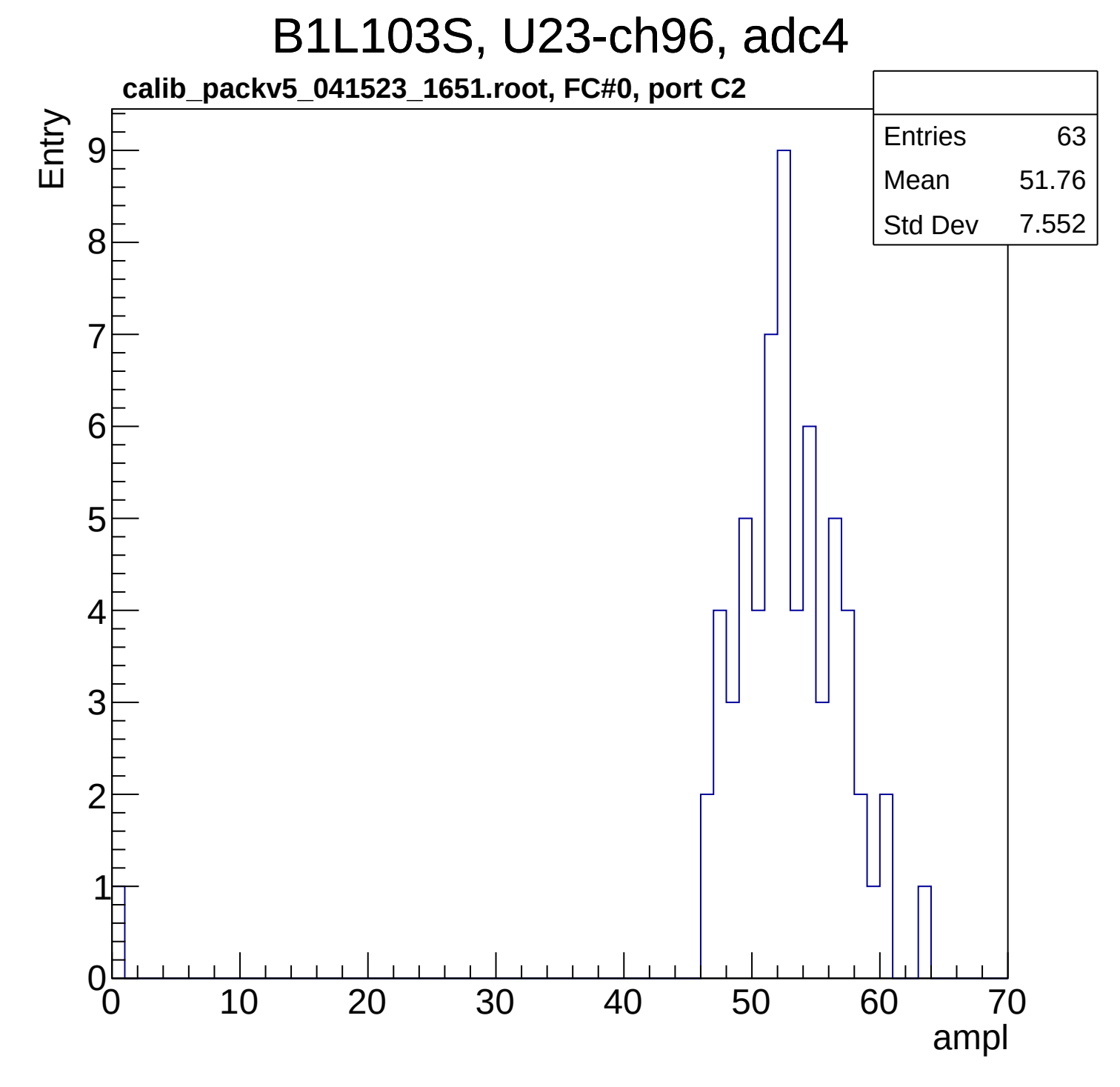

B1L103S, U23-ch95, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

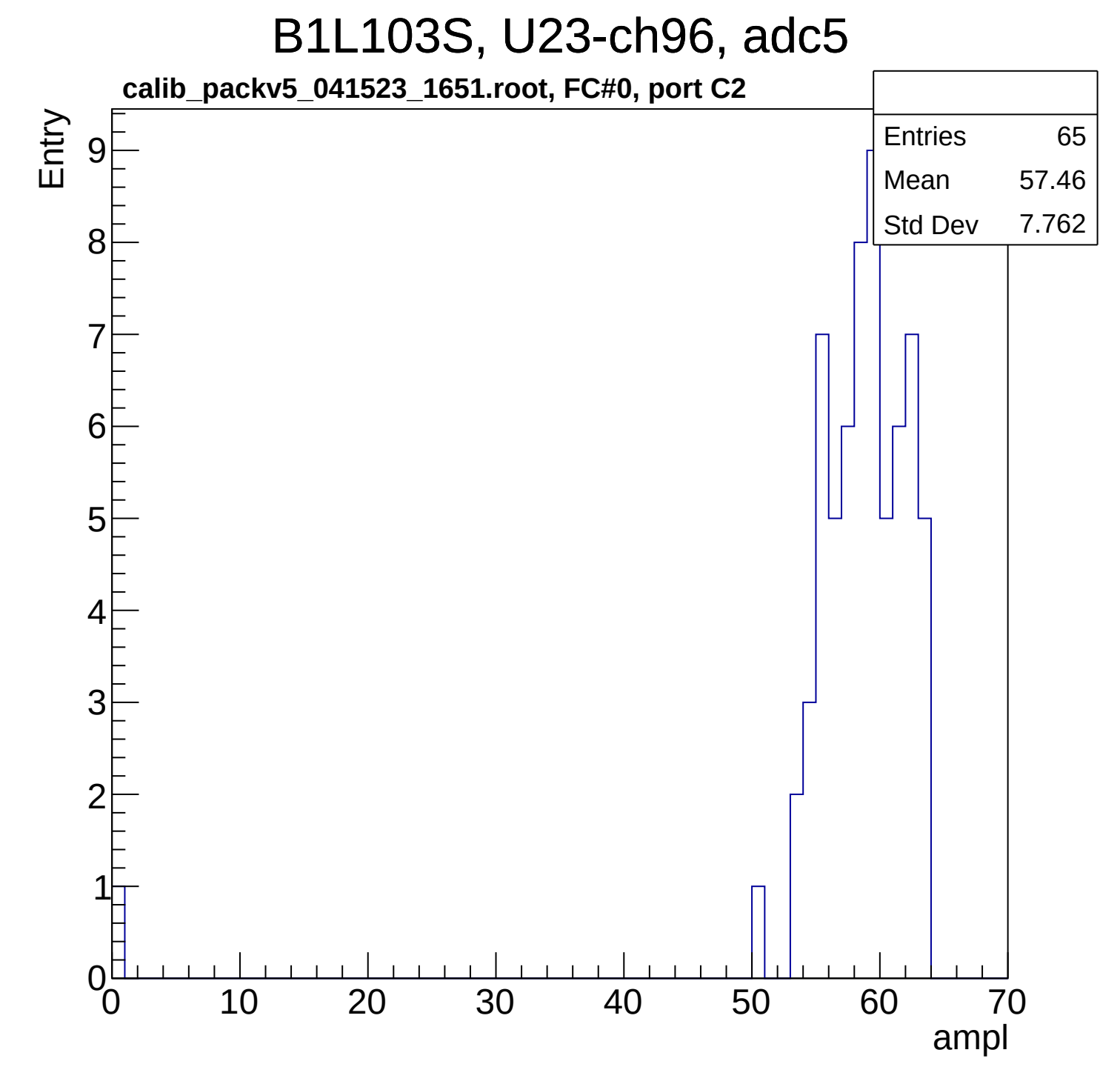

B1L103S, U23-ch96, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 18.35
Std Dev 13.09
Gaus mean : 27.8045
Gaus Width: 3.0699
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

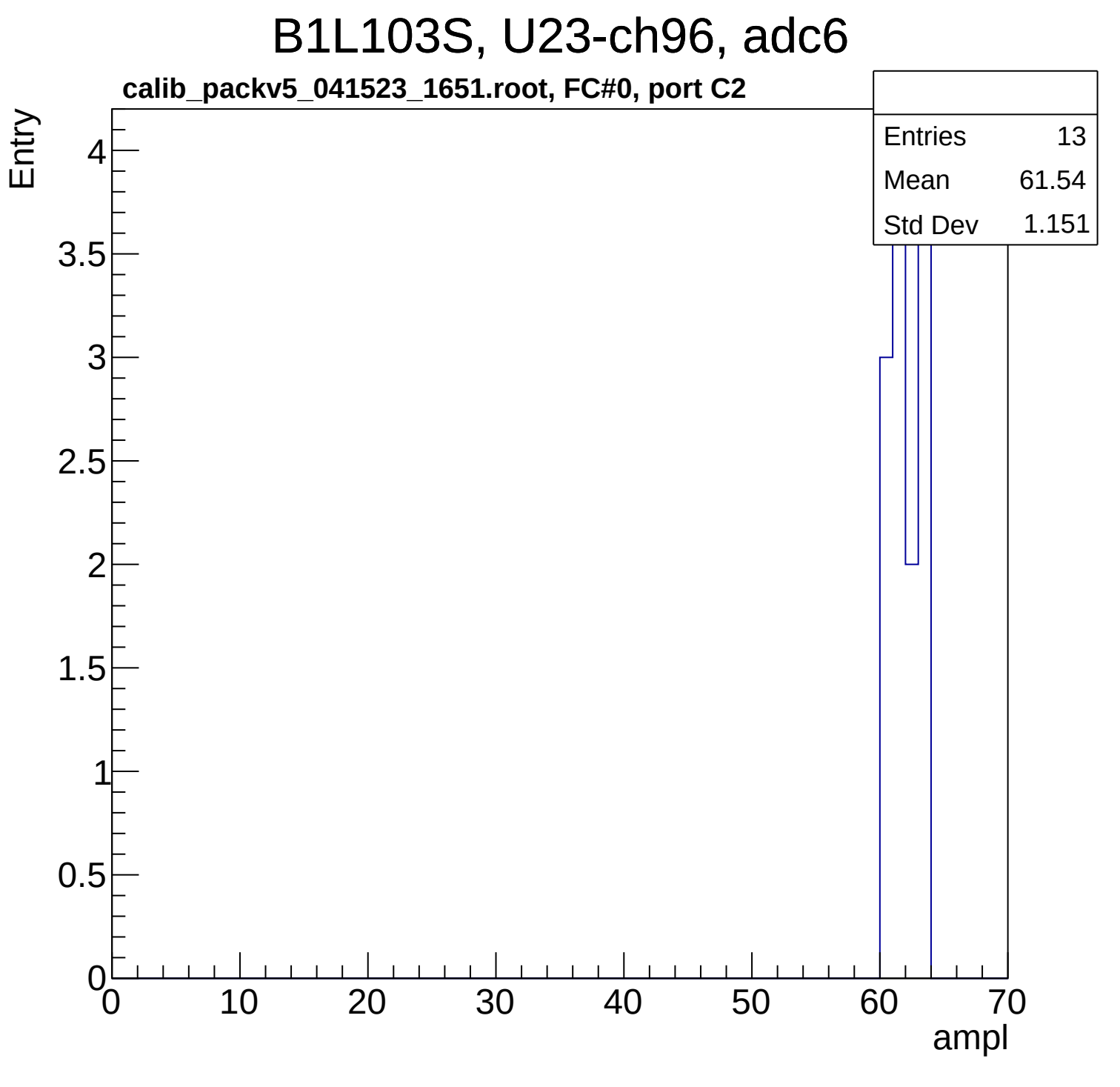

B1L103S, U23-ch96, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 98
Mean 28.57
Std Dev 13.52
Gaus mean : 35.0671
Gaus Width: 3.7769
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

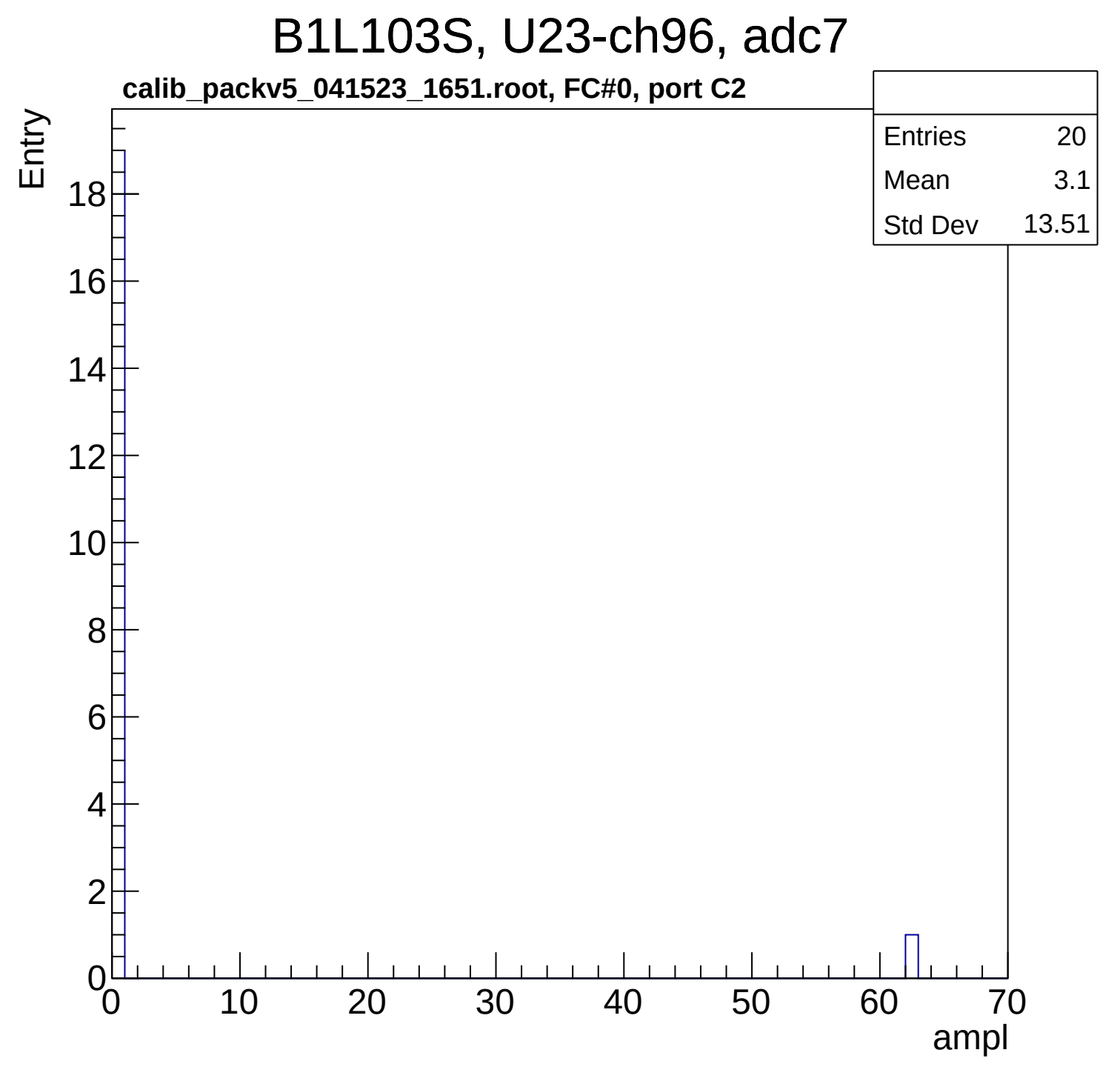

B1L103S, U23-ch96, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 40.22
Std Dev 6.273
Gaus mean : 41.4354
Gaus Width: 3.5628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

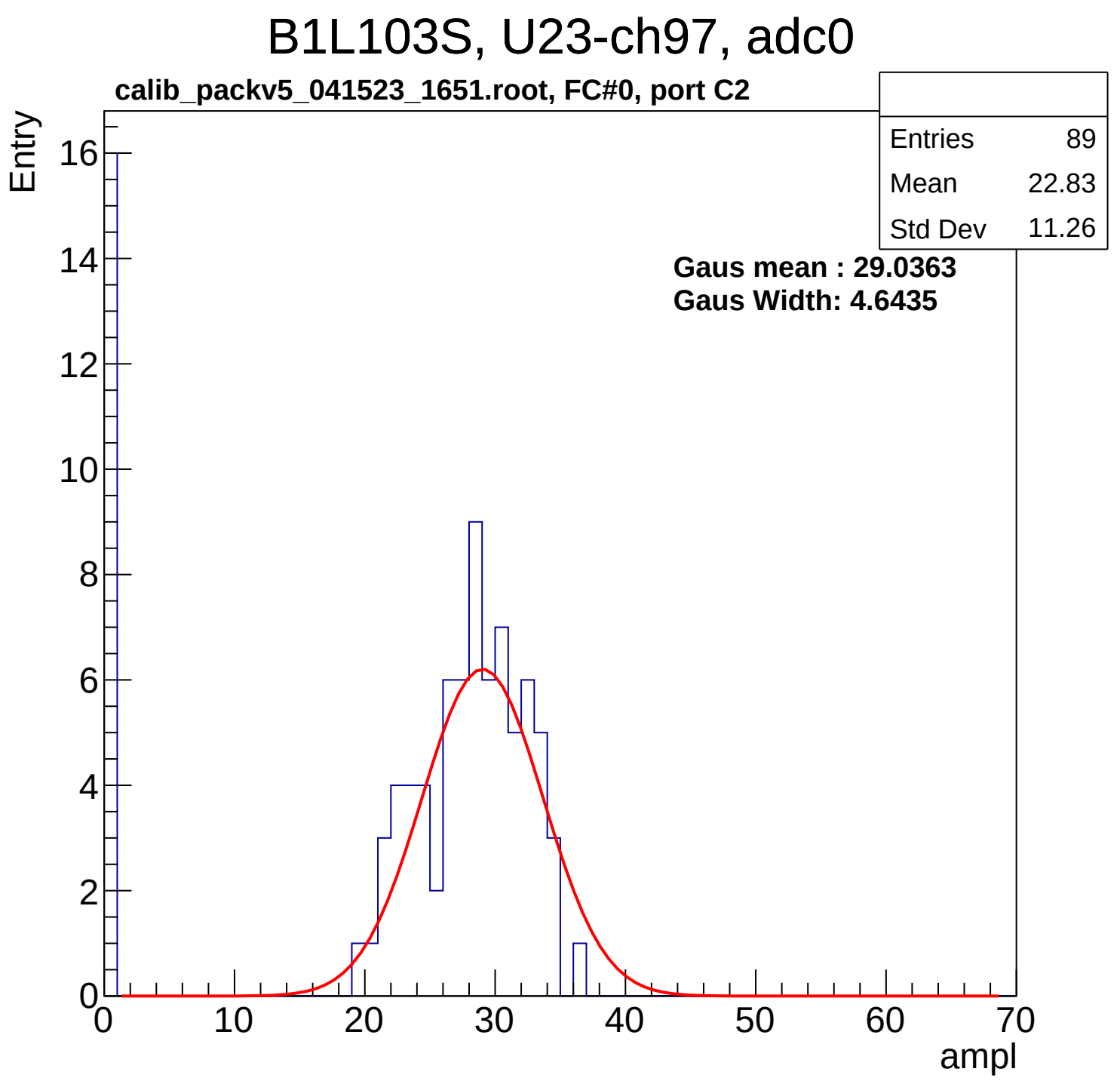

B1L103S, U23-ch96, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 55
Mean 46.31
Std Dev 7.091
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

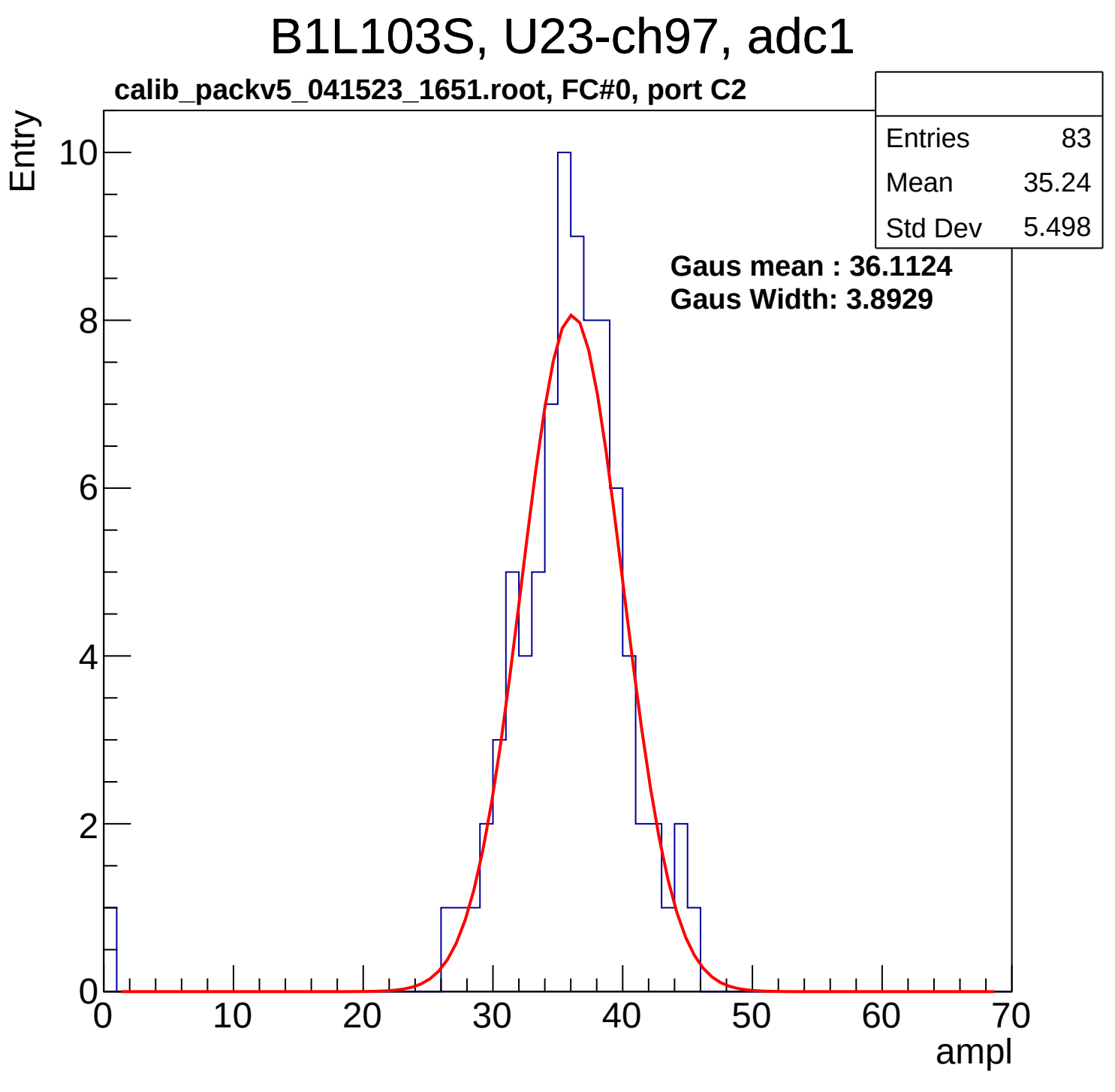

B1L103S, U23-ch96, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 51.76
Std Dev 7.552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

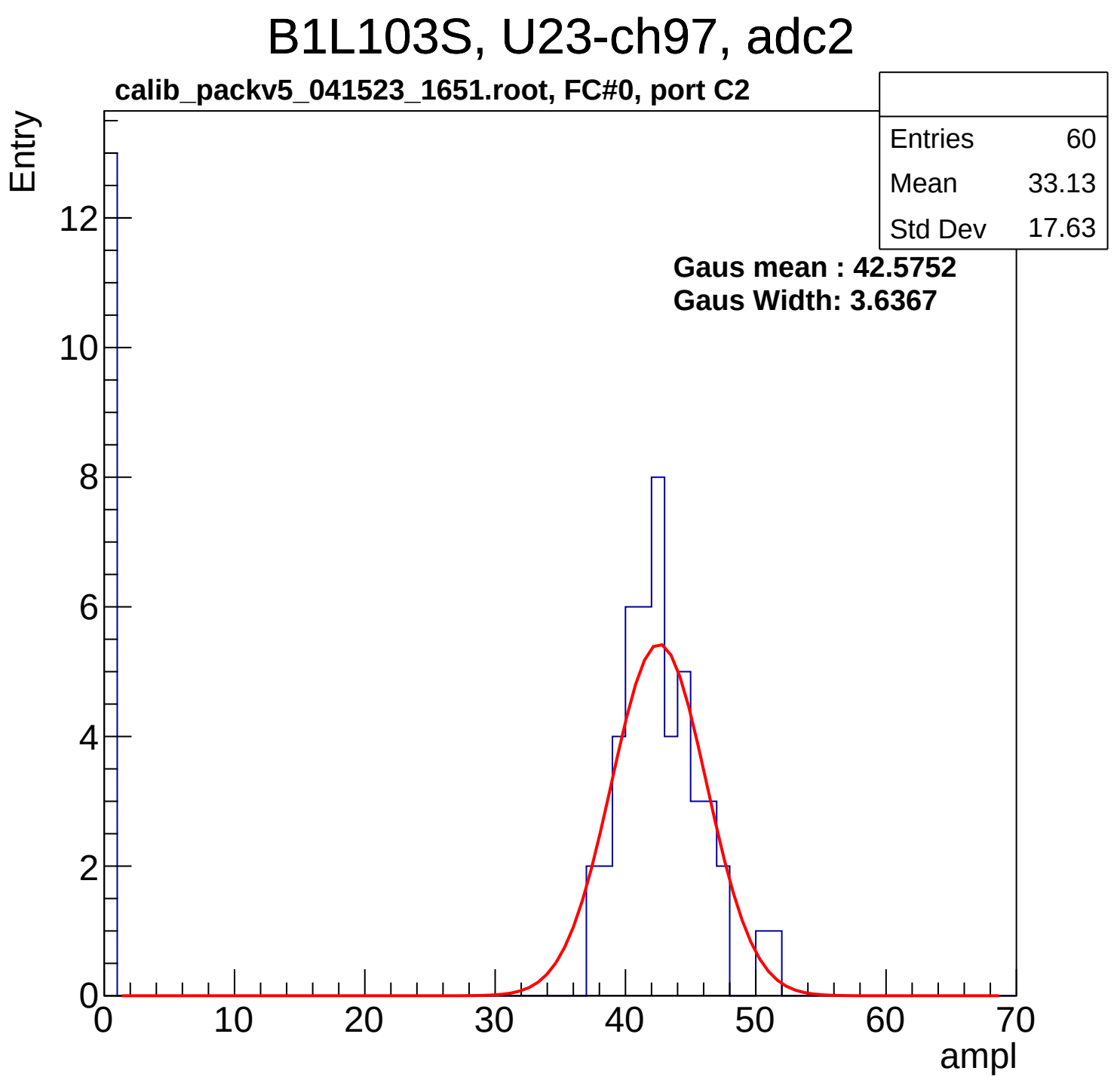

B1L103S, U23-ch96, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 57.46
Std Dev 7.762
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

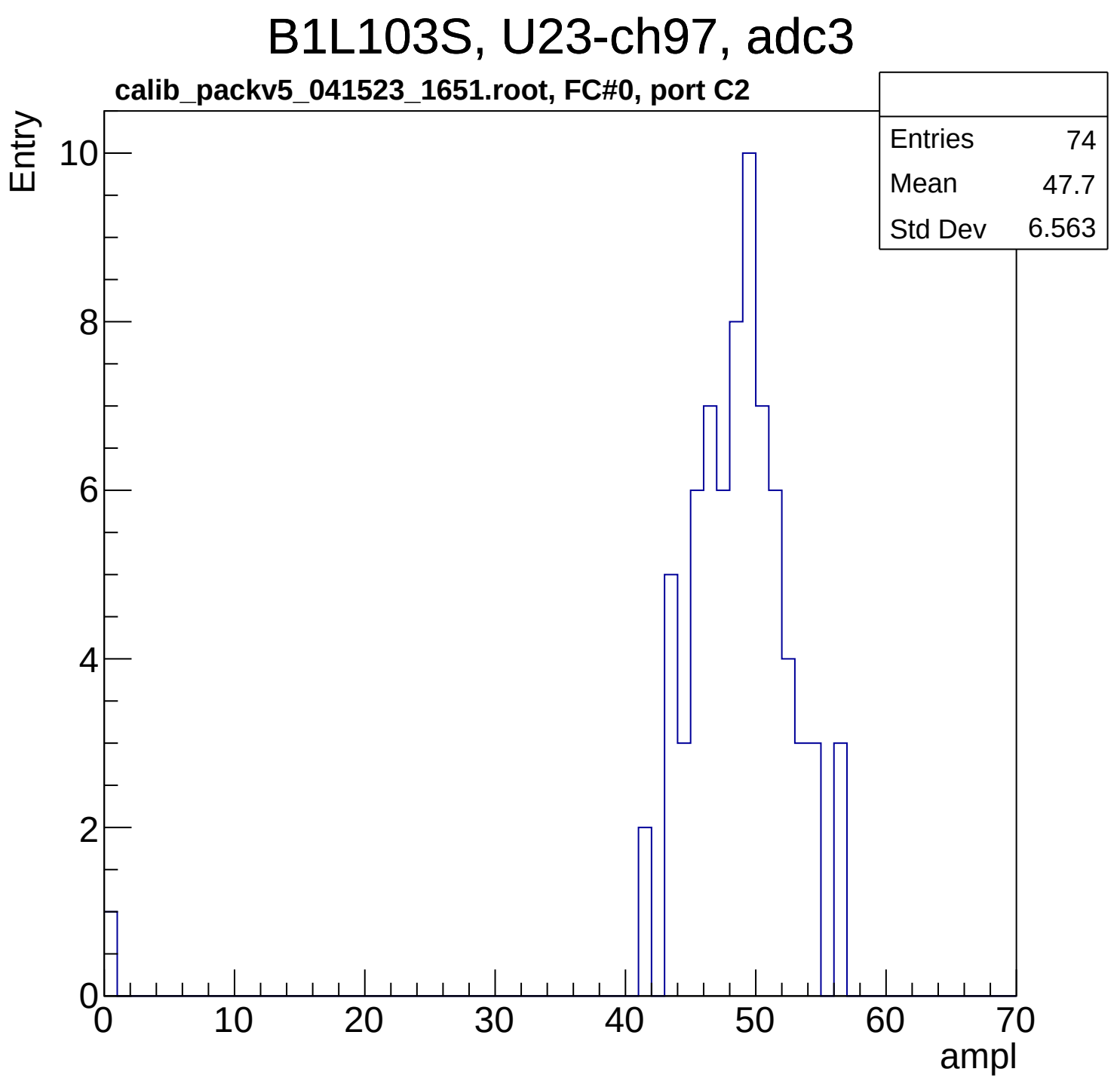

B1L103S, U23-ch96, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 13
Mean 61.54
Std Dev 1.151
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

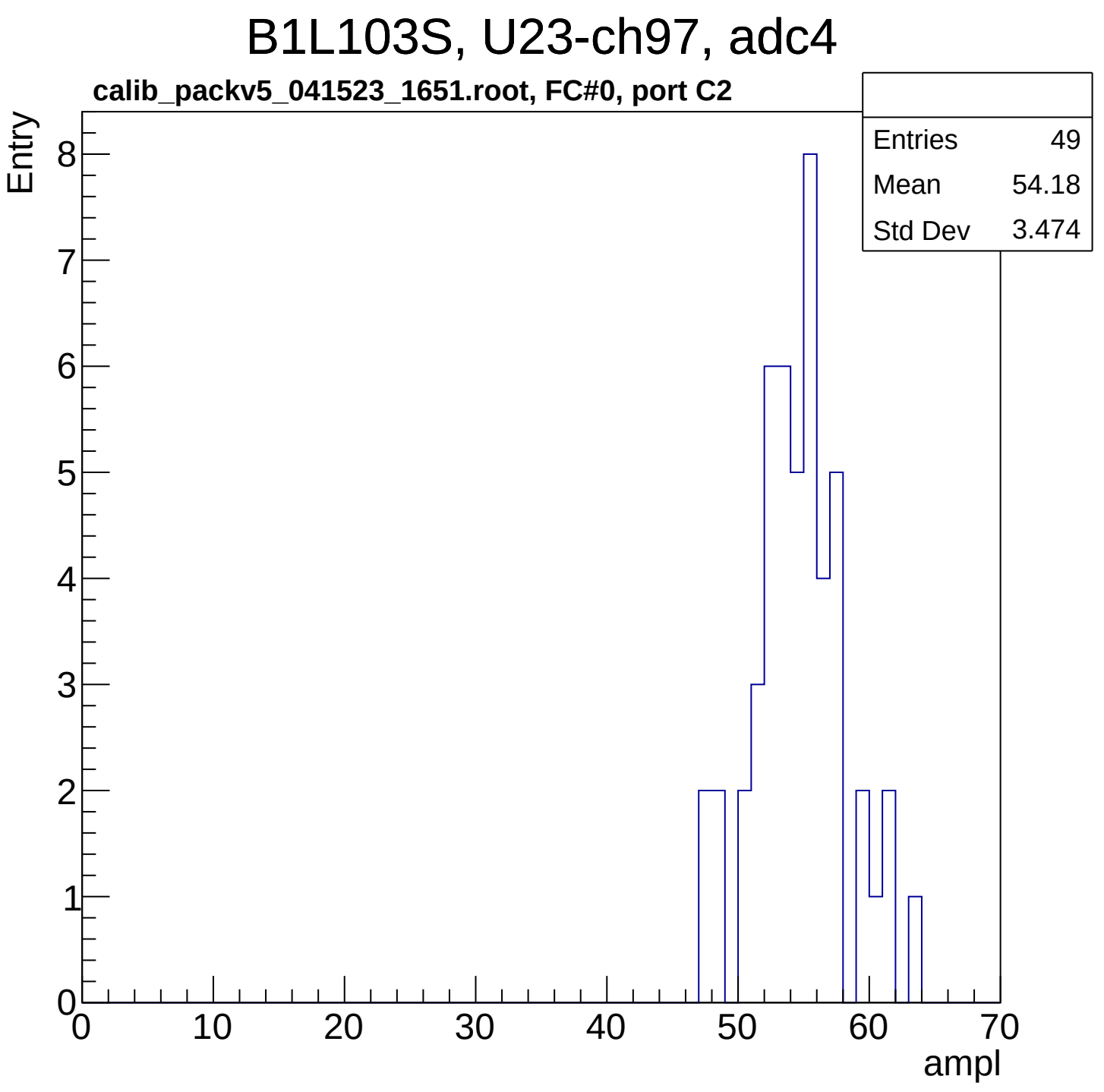

B1L103S, U23-ch96, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.1
Std Dev 13.51
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

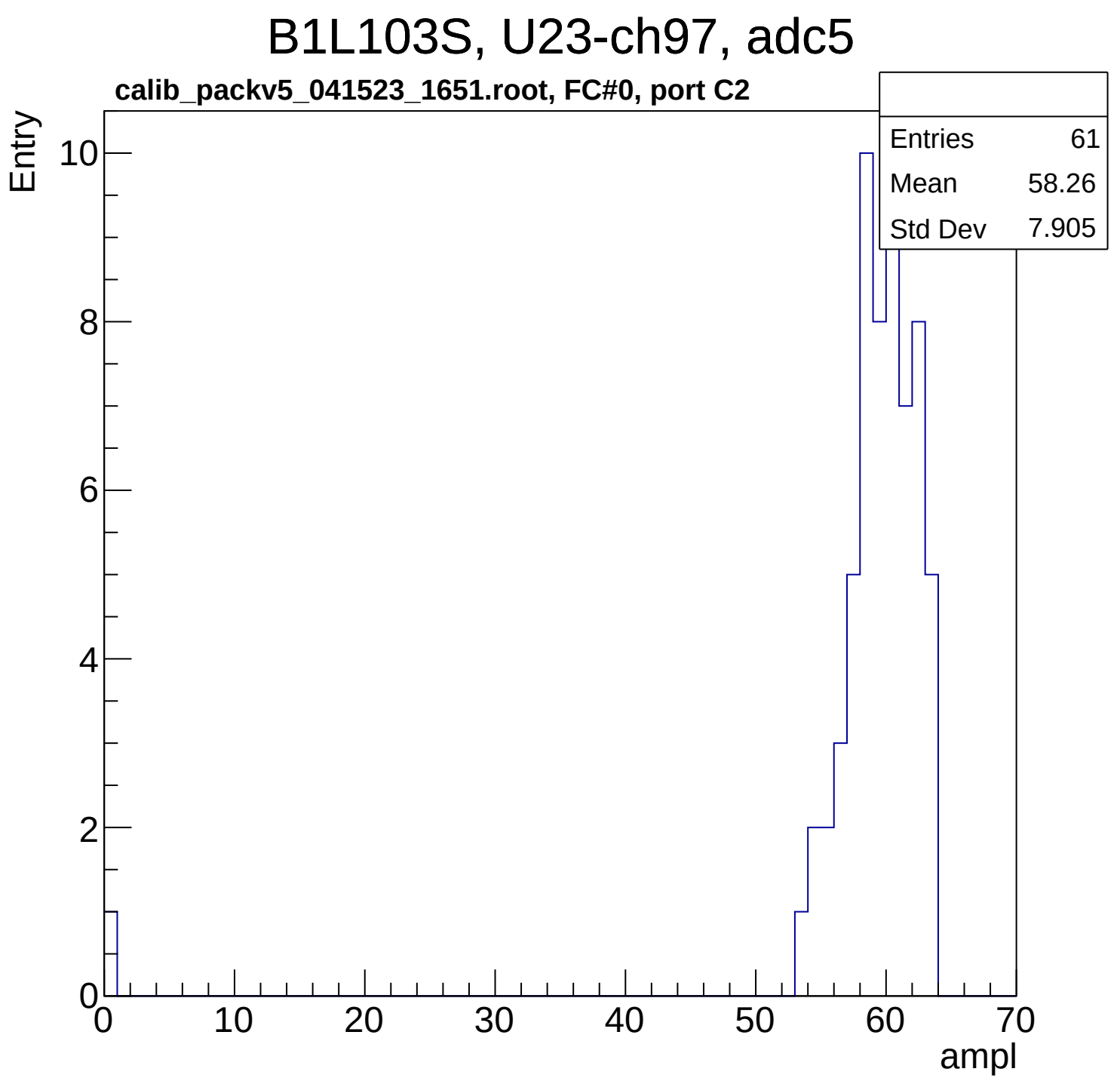

B1L103S, U23-ch97, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 22.83
Std Dev 11.26
Gaus mean : 29.0363
Gaus Width: 4.6435
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

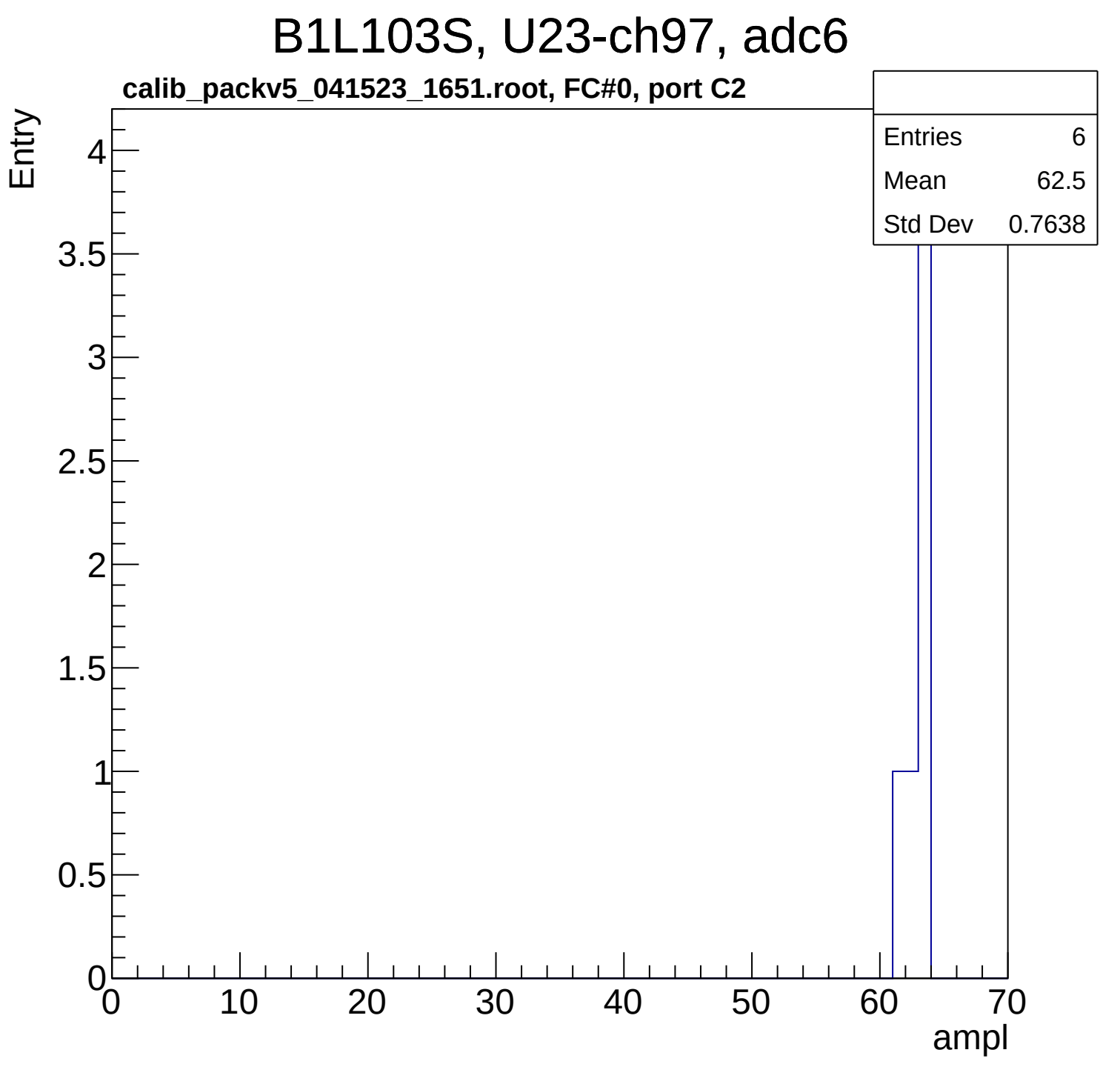

B1L103S, U23-ch97, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 35.24
Std Dev 5.498
Gaus mean : 36.1124
Gaus Width: 3.8929
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

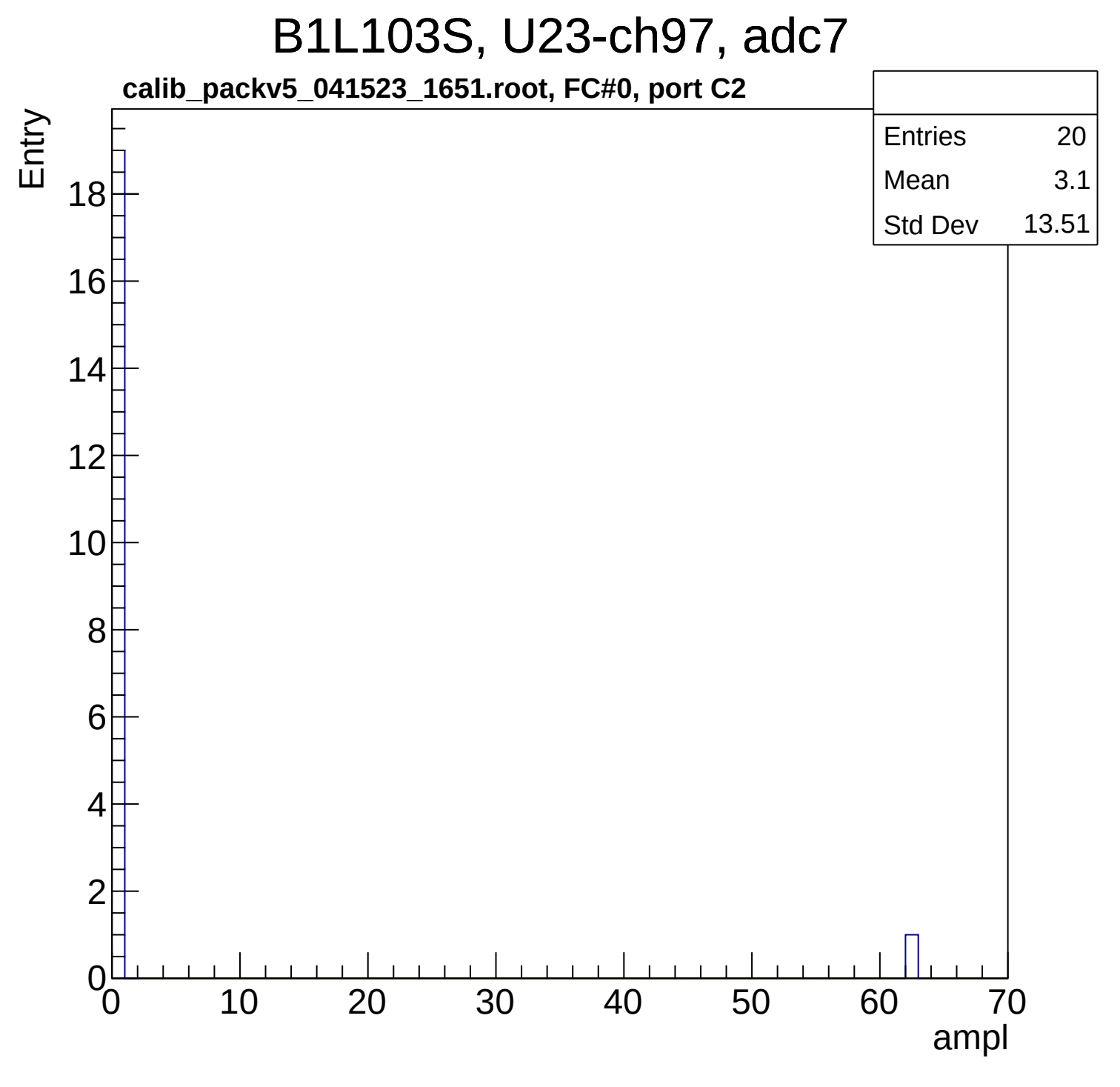

B1L103S, U23-ch97, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 33.13
Std Dev 17.63
Gaus mean : 42.5752
Gaus Width: 3.6367
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

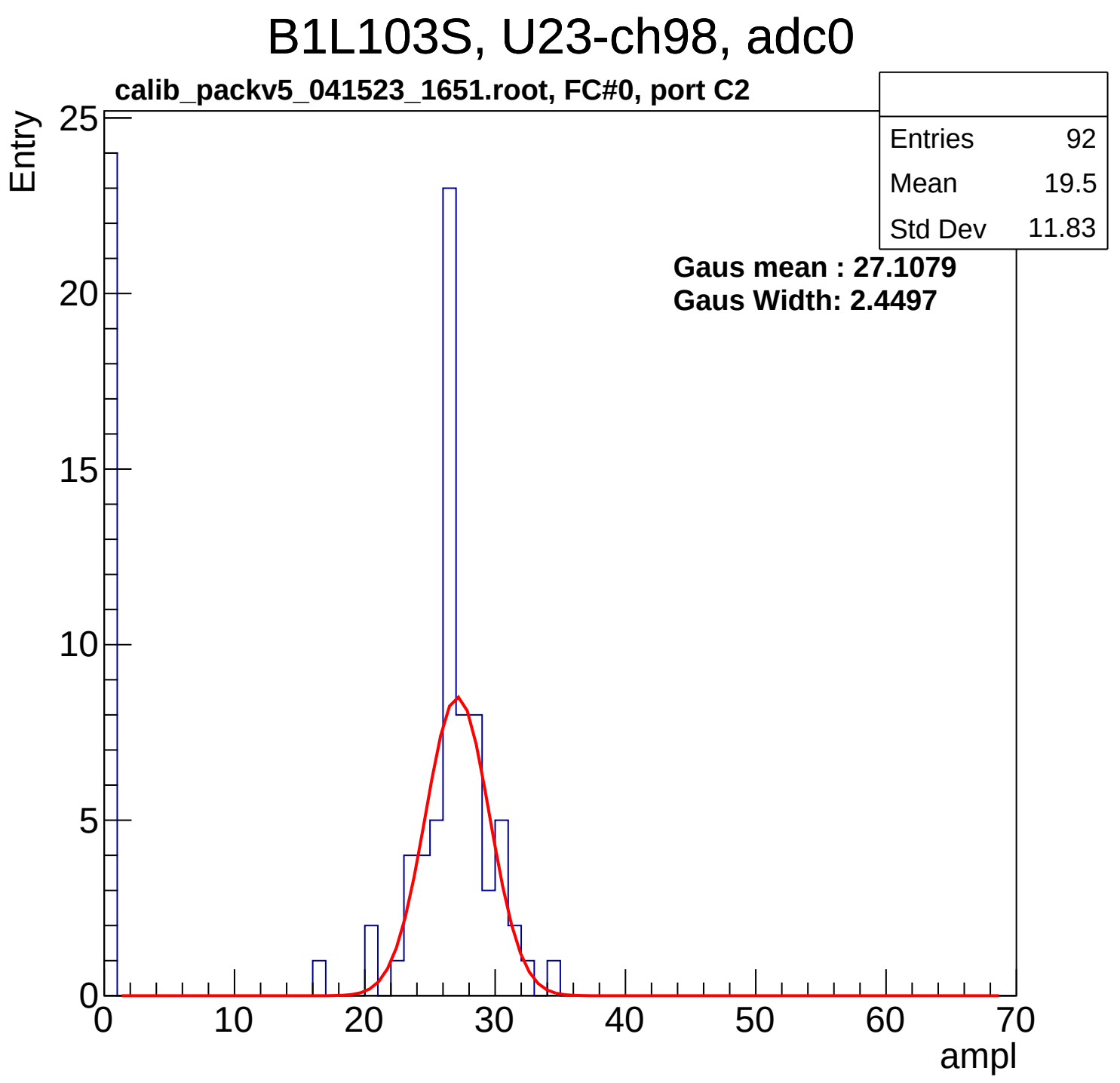

B1L103S, U23-ch97, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 47.7
Std Dev 6.563
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

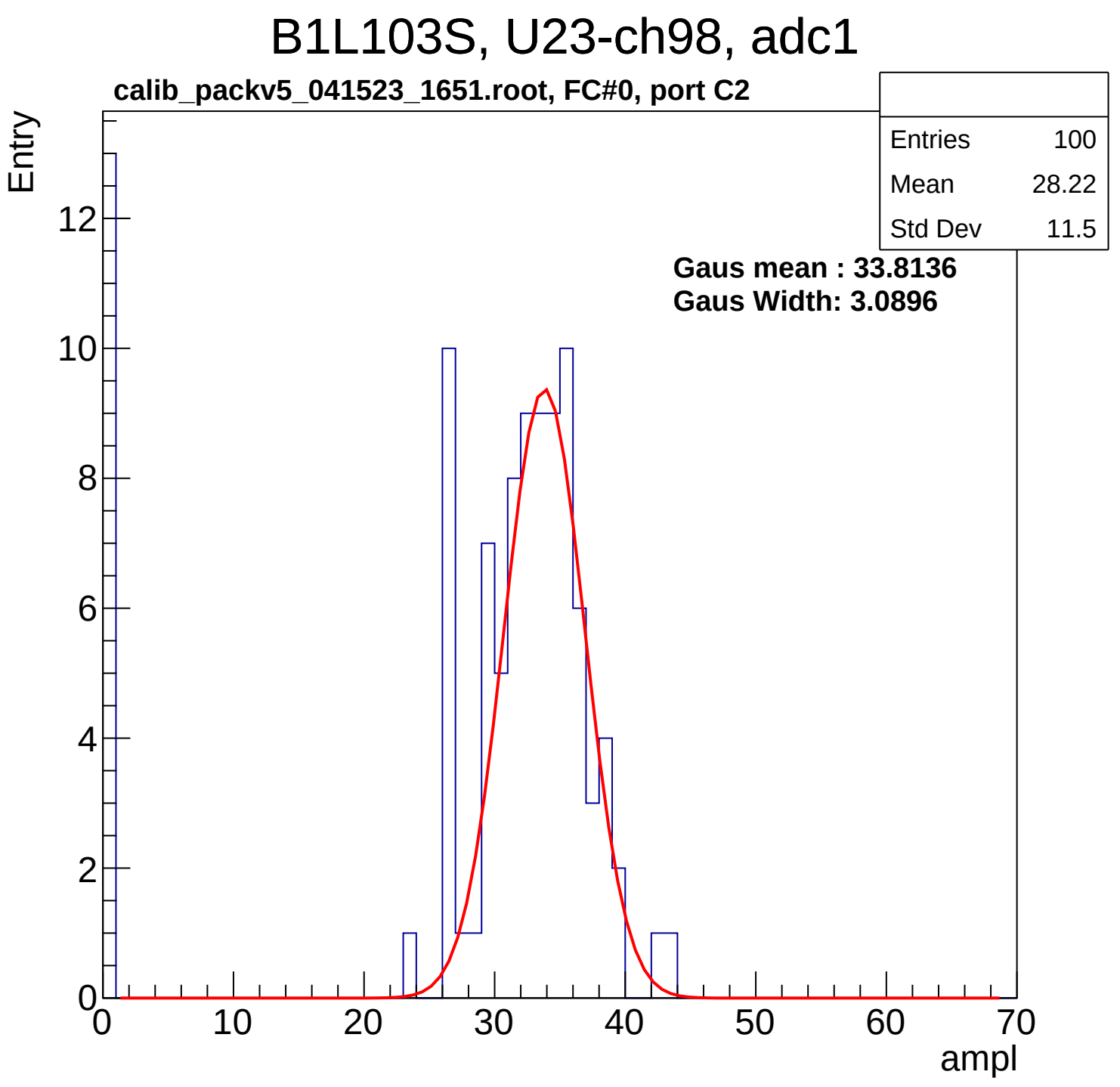

B1L103S, U23-ch97, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 54.18
Std Dev 3.474
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

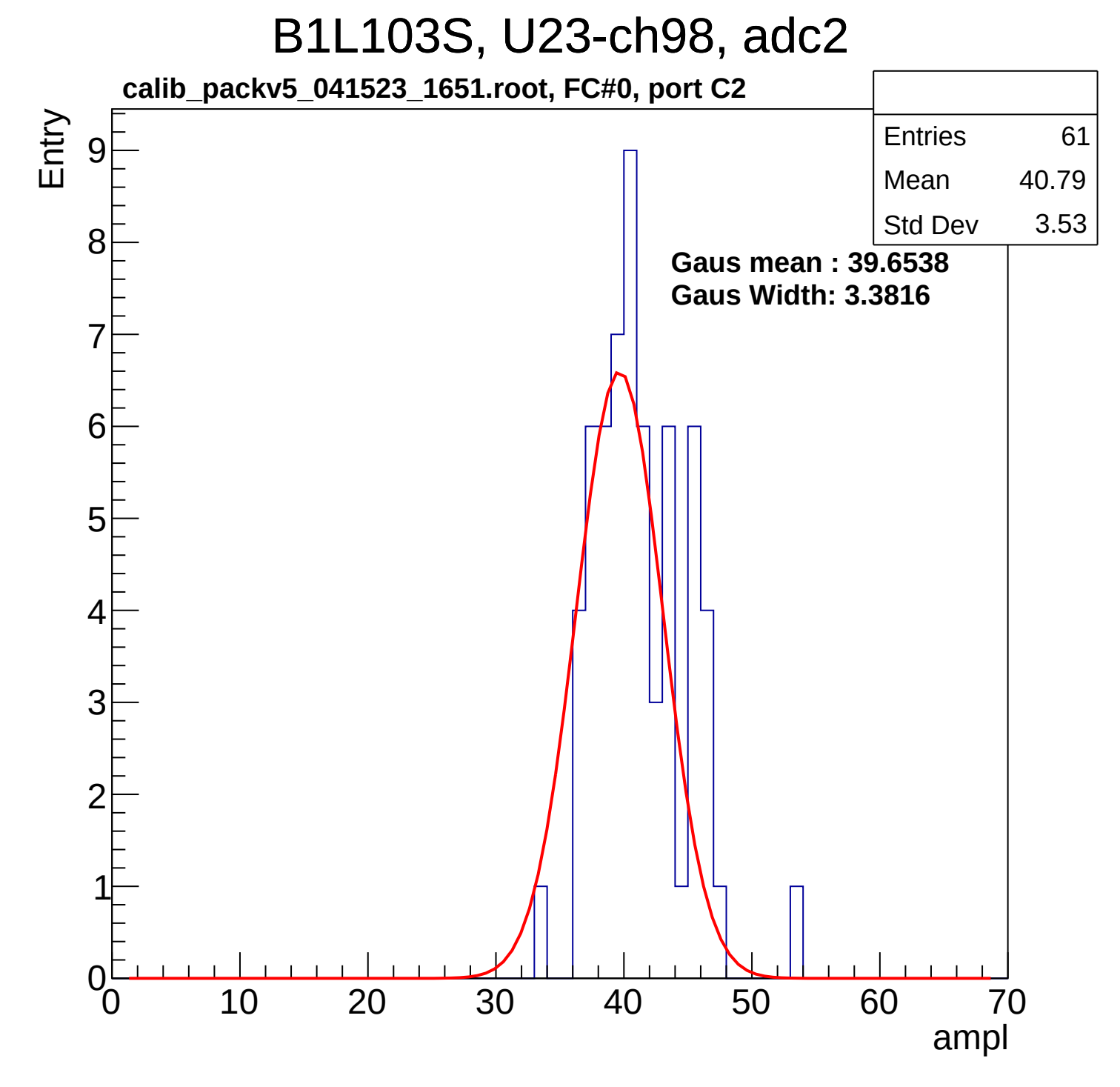

B1L103S, U23-ch97, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 58.26
Std Dev 7.905
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

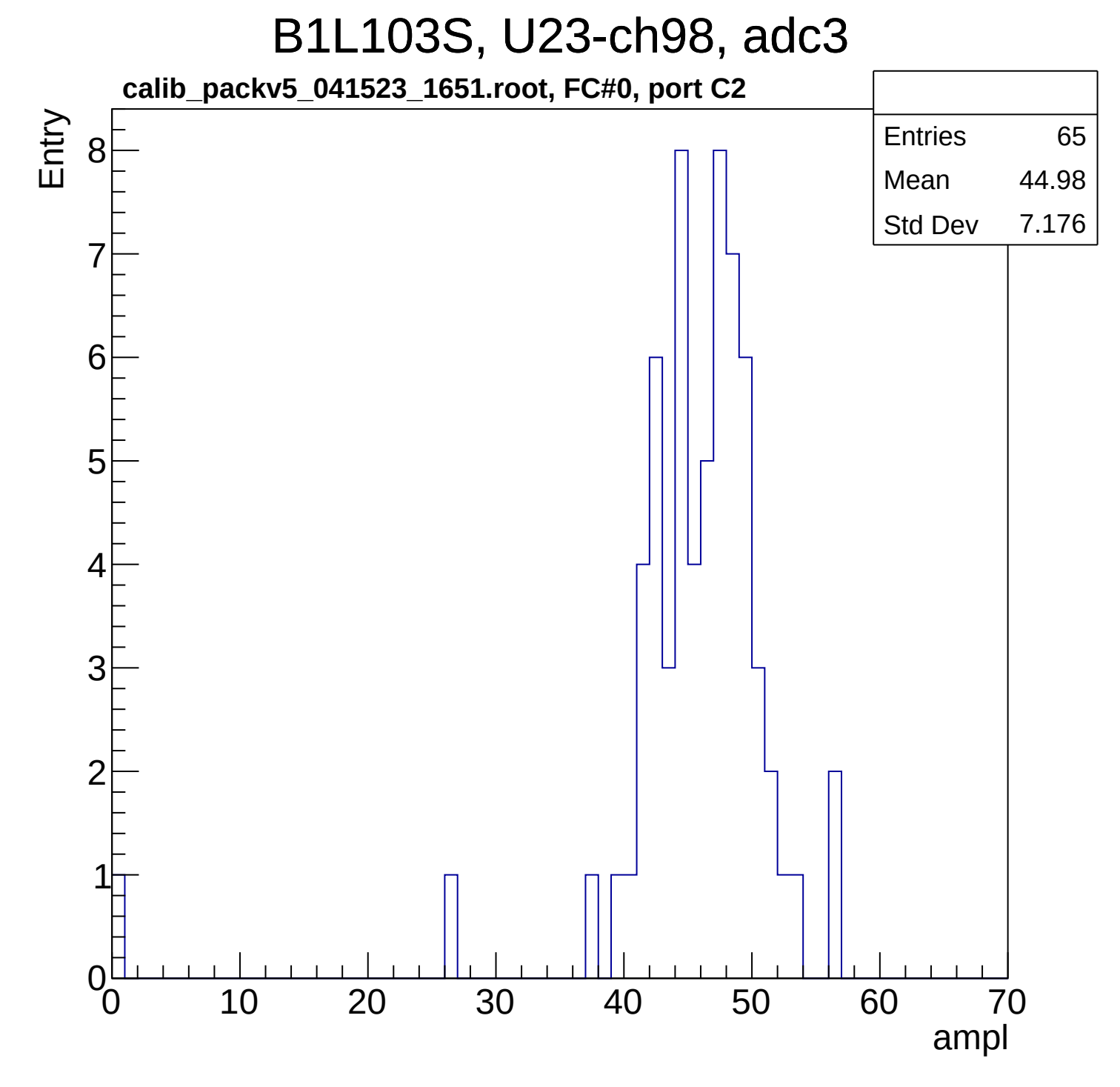

B1L103S, U23-ch97, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

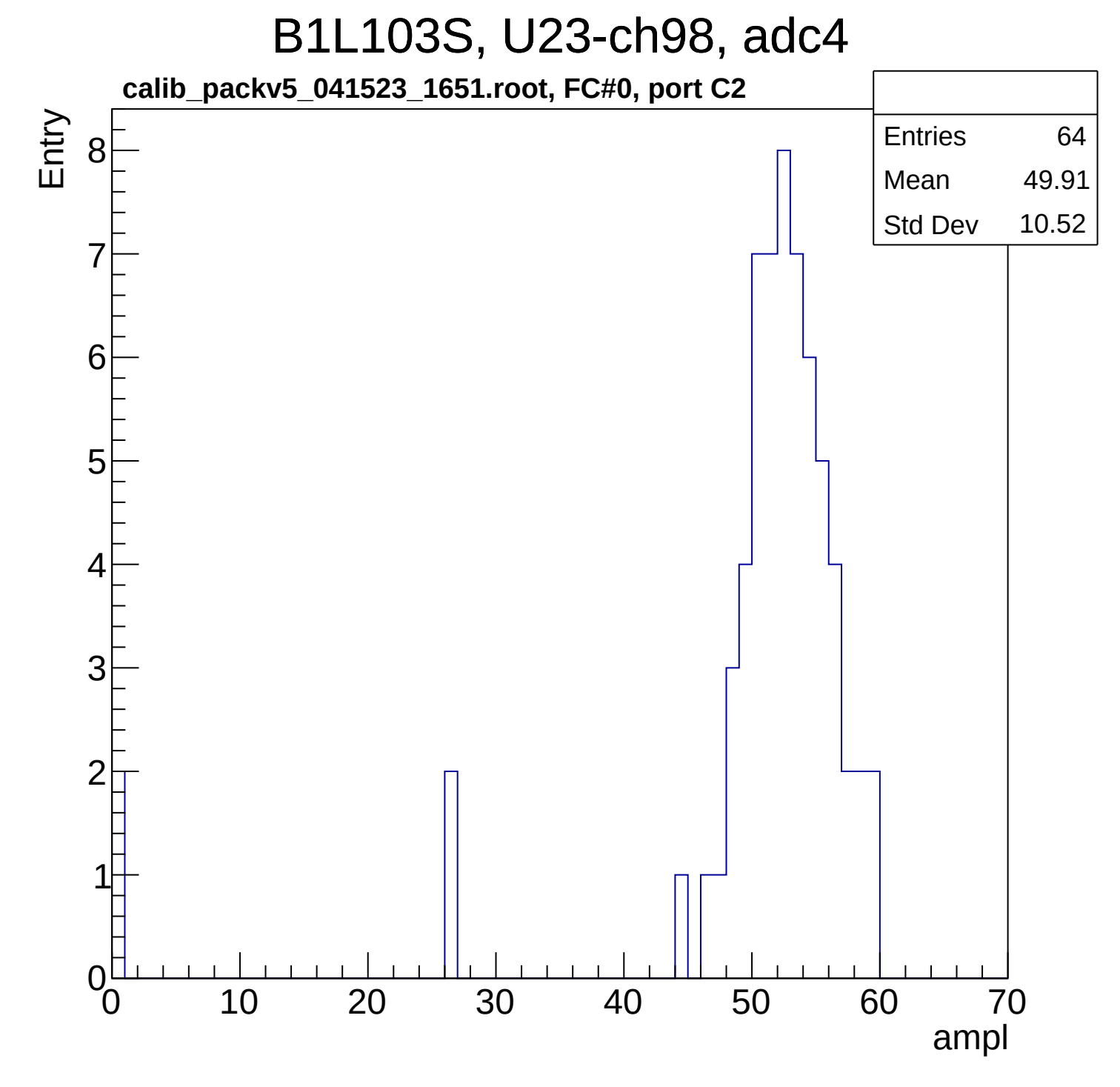

B1L103S, U23-ch97, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.1
Std Dev 13.51
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

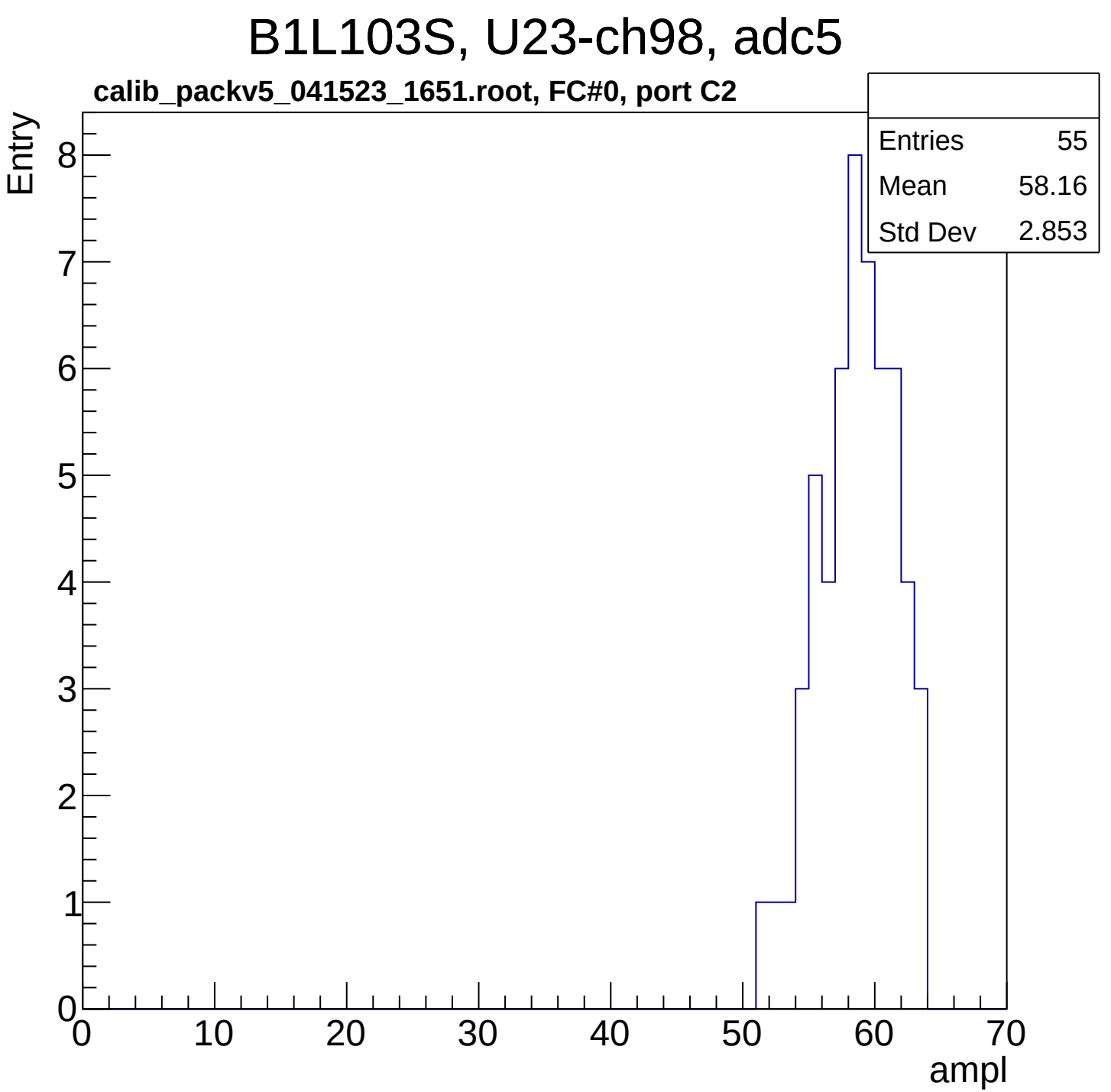

B1L103S, U23-ch98, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 92
Mean 19.5
Std Dev 11.83
Gaus mean : 27.1079
Gaus Width: 2.4497
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

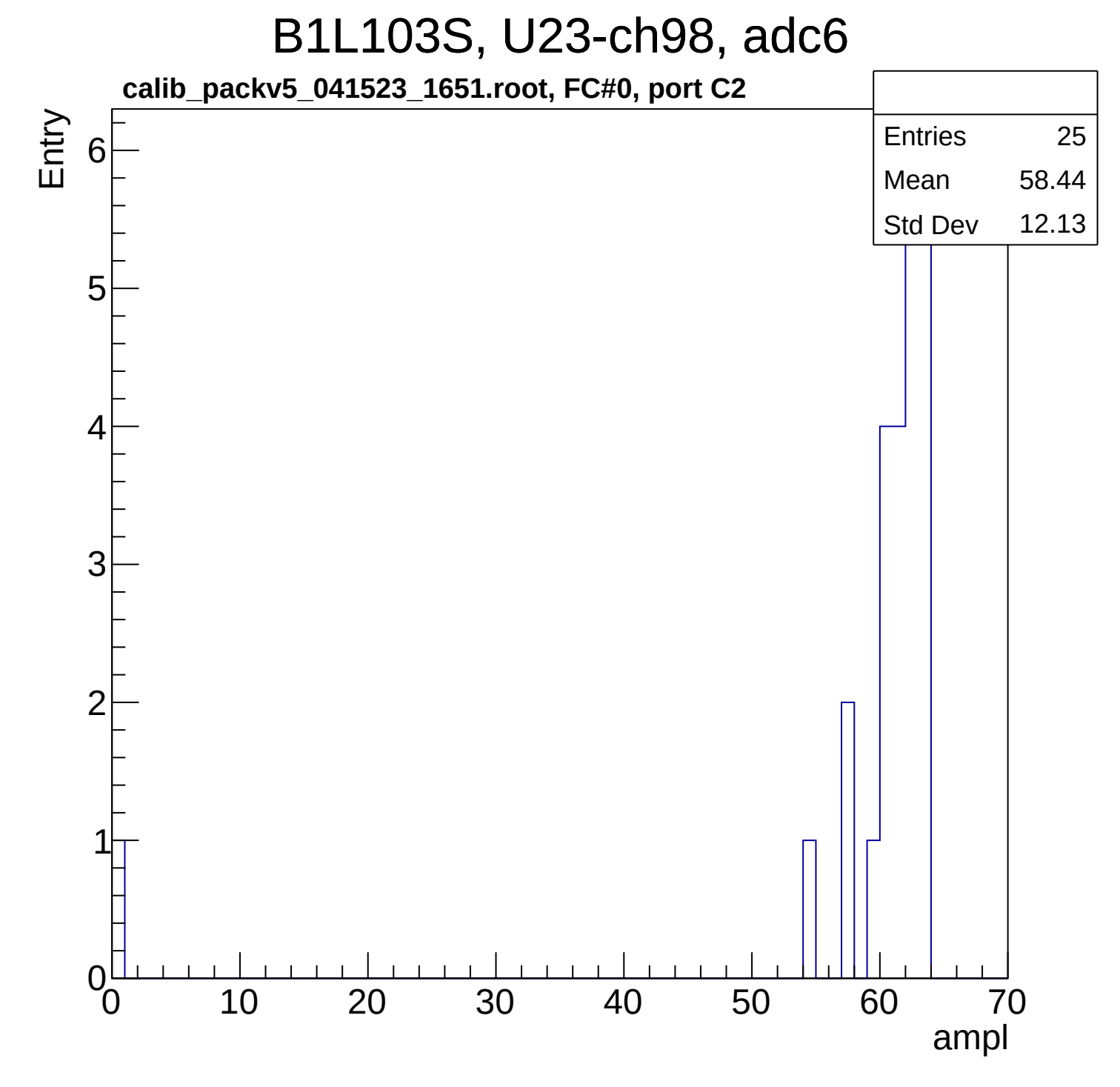

B1L103S, U23-ch98, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 28.22
Std Dev 11.5
Gaus mean : 33.8136
Gaus Width: 3.0896
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

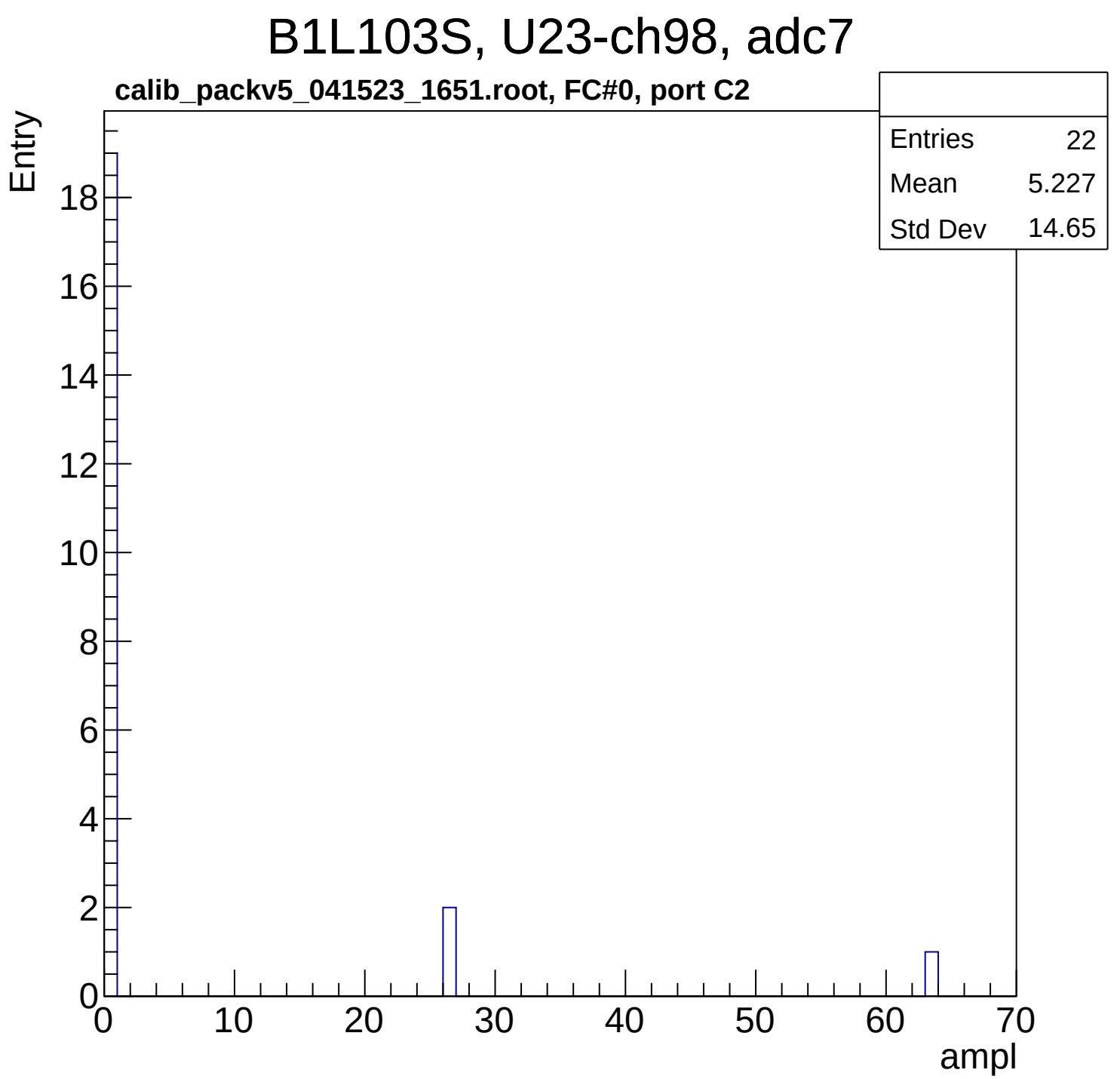

B1L103S, U23-ch98, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 40.79
Std Dev 3.53
Gaus mean : 39.6538
Gaus Width: 3.3816
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

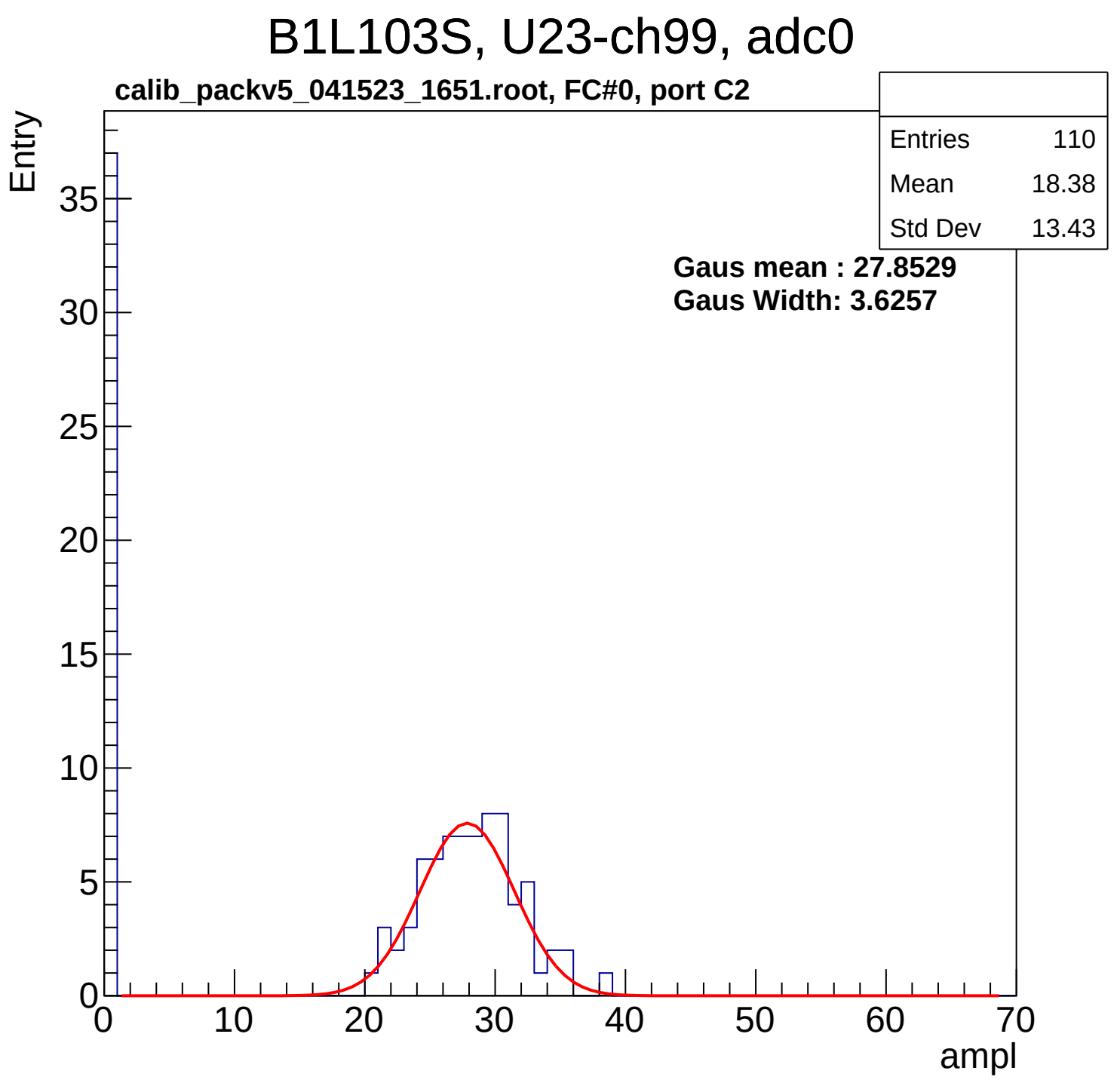

B1L103S, U23-ch98, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 44.98
Std Dev 7.176
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

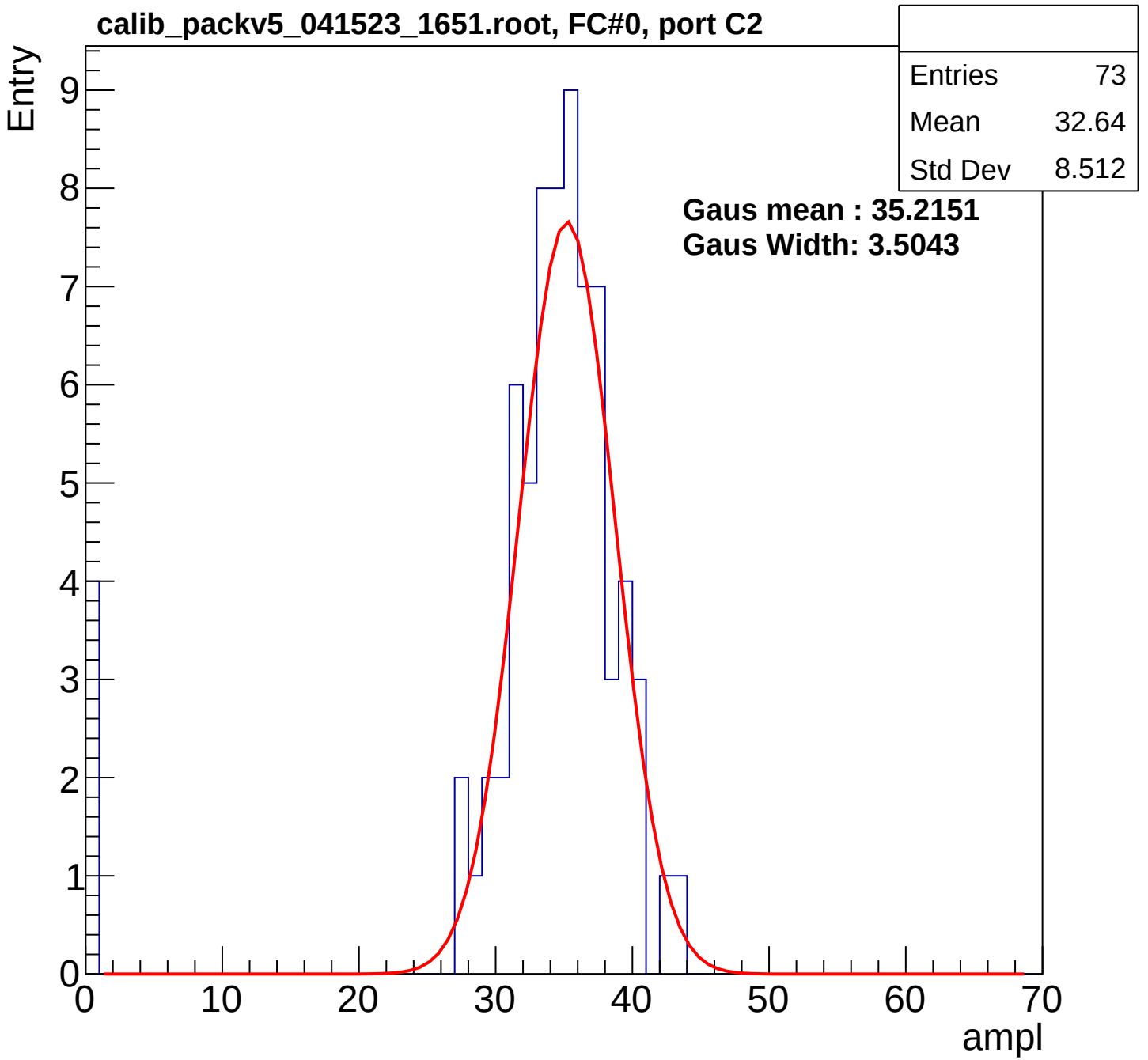

B1L103S, U23-ch98, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 49.91
Std Dev 10.52
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

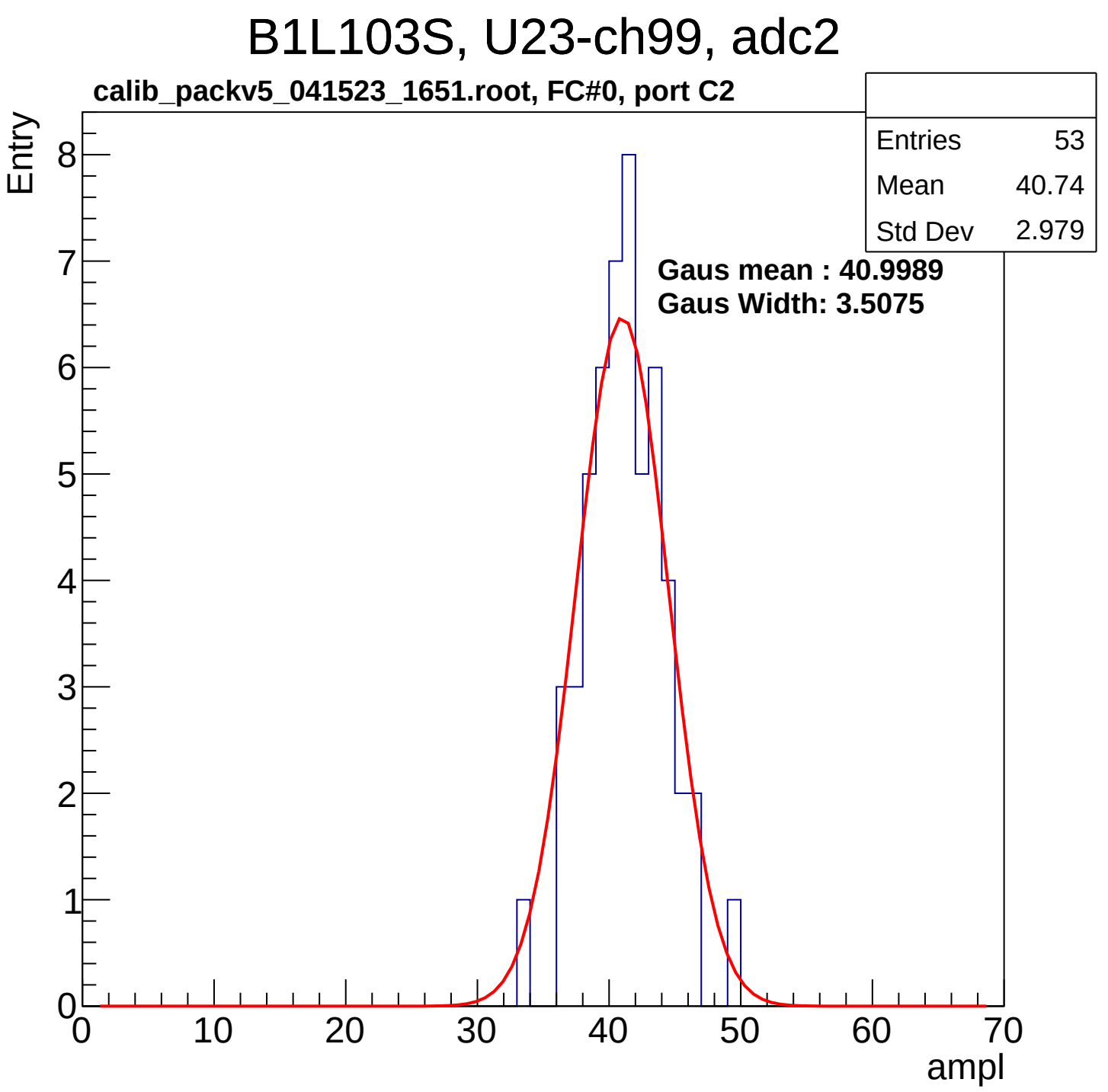

B1L103S, U23-ch98, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 55
Mean 58.16
Std Dev 2.853
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

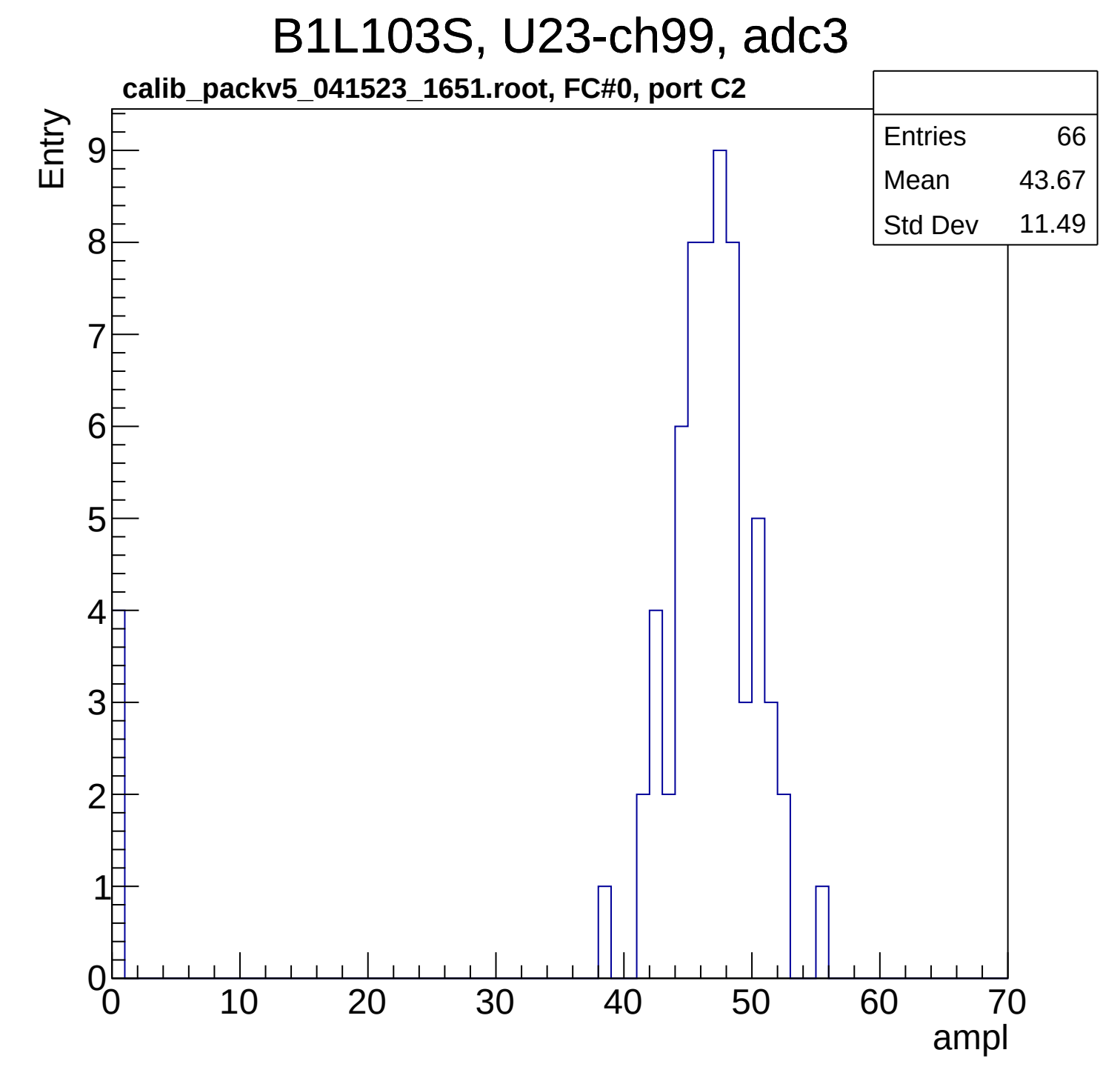

B1L103S, U23-ch98, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 25
Mean 58.44
Std Dev 12.13
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

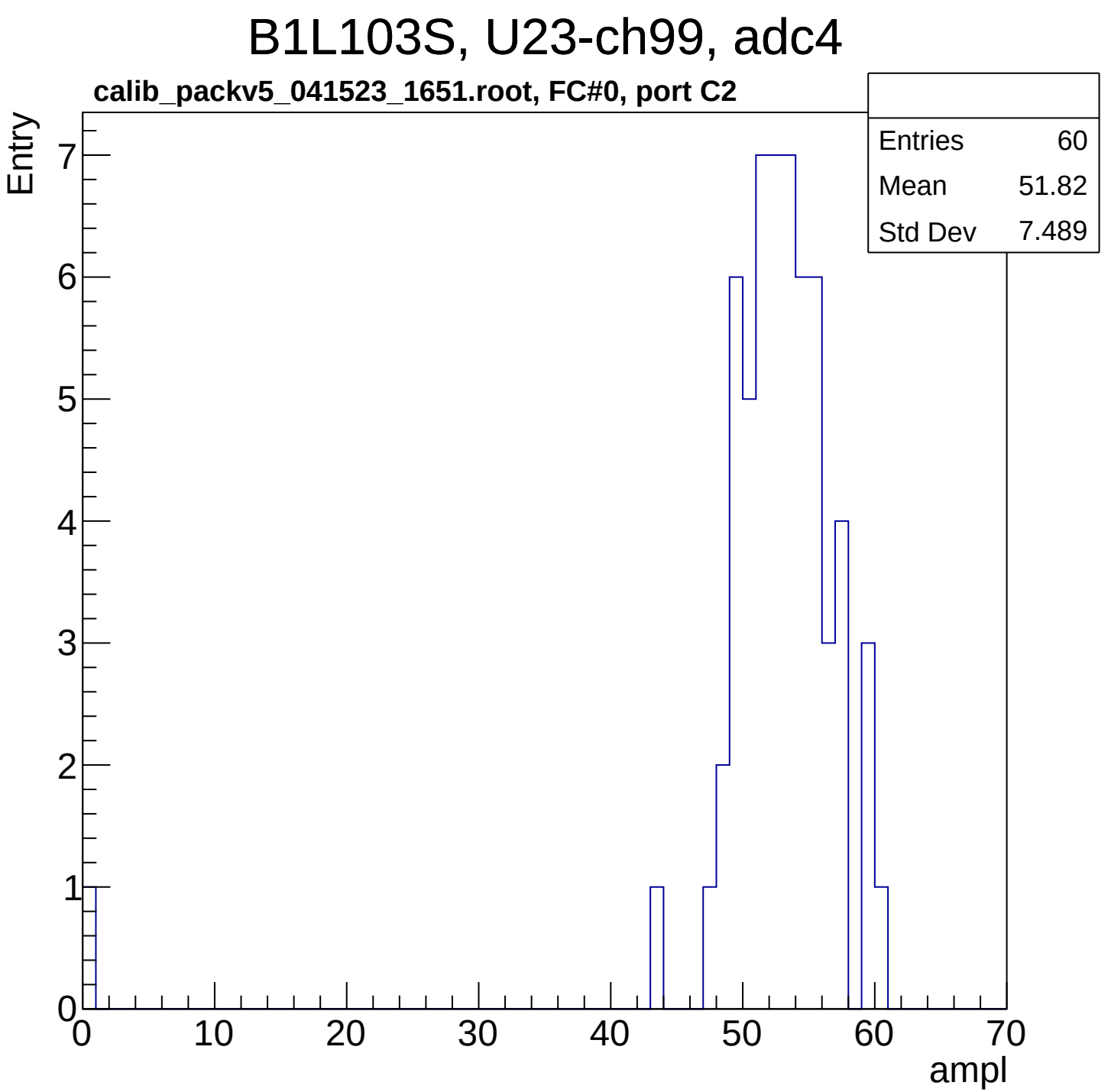

B1L103S, U23-ch98, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 22
Mean 5.227
Std Dev 14.65
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

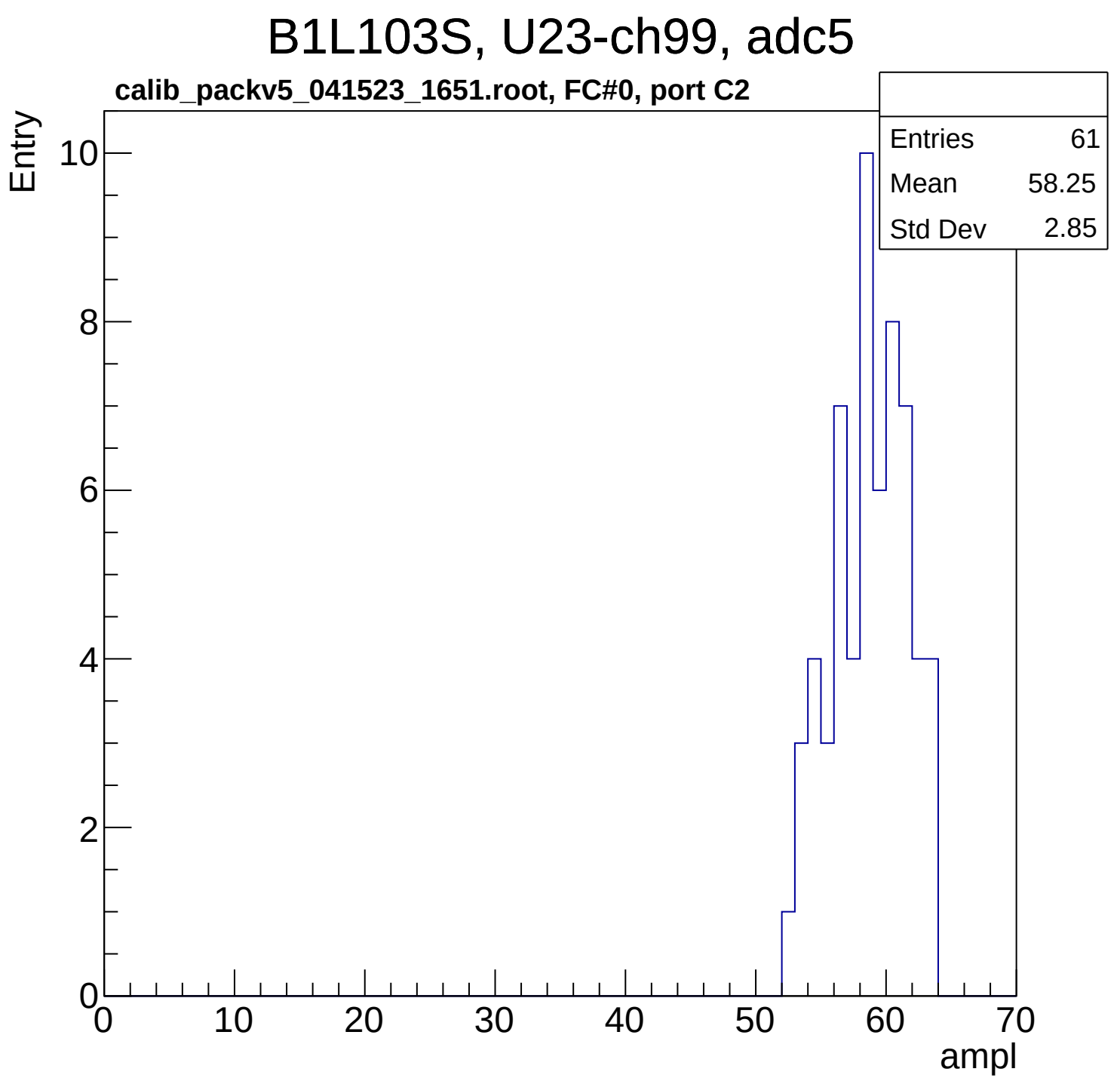

B1L103S, U23-ch99, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 110
Mean 18.38
Std Dev 13.43
Gaus mean : 27.8529
Gaus Width: 3.6257
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

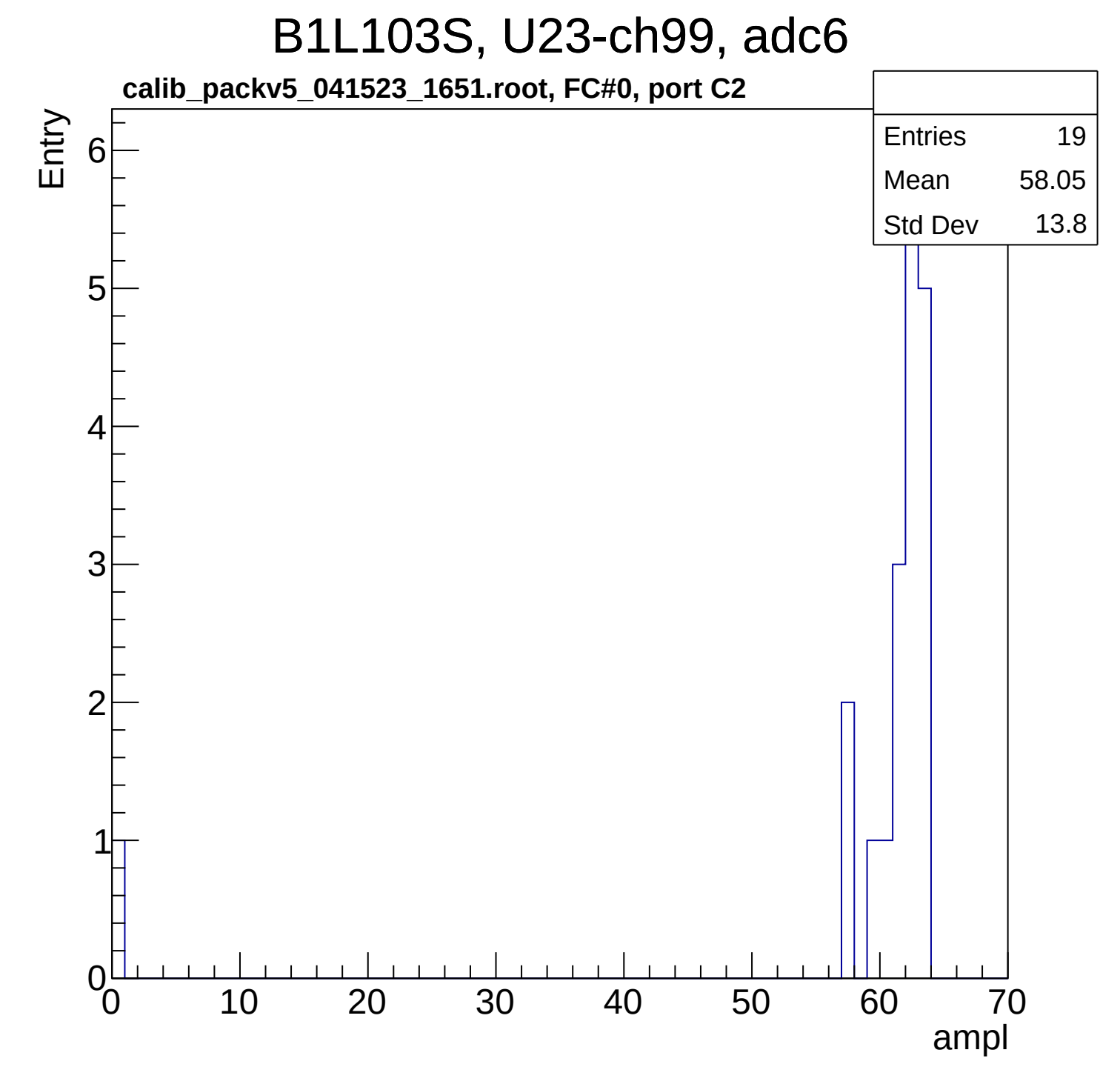

B1L103S, U23-ch99, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 32.64
Std Dev 8.512
Gaus mean : 35.2151
Gaus Width: 3.5043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

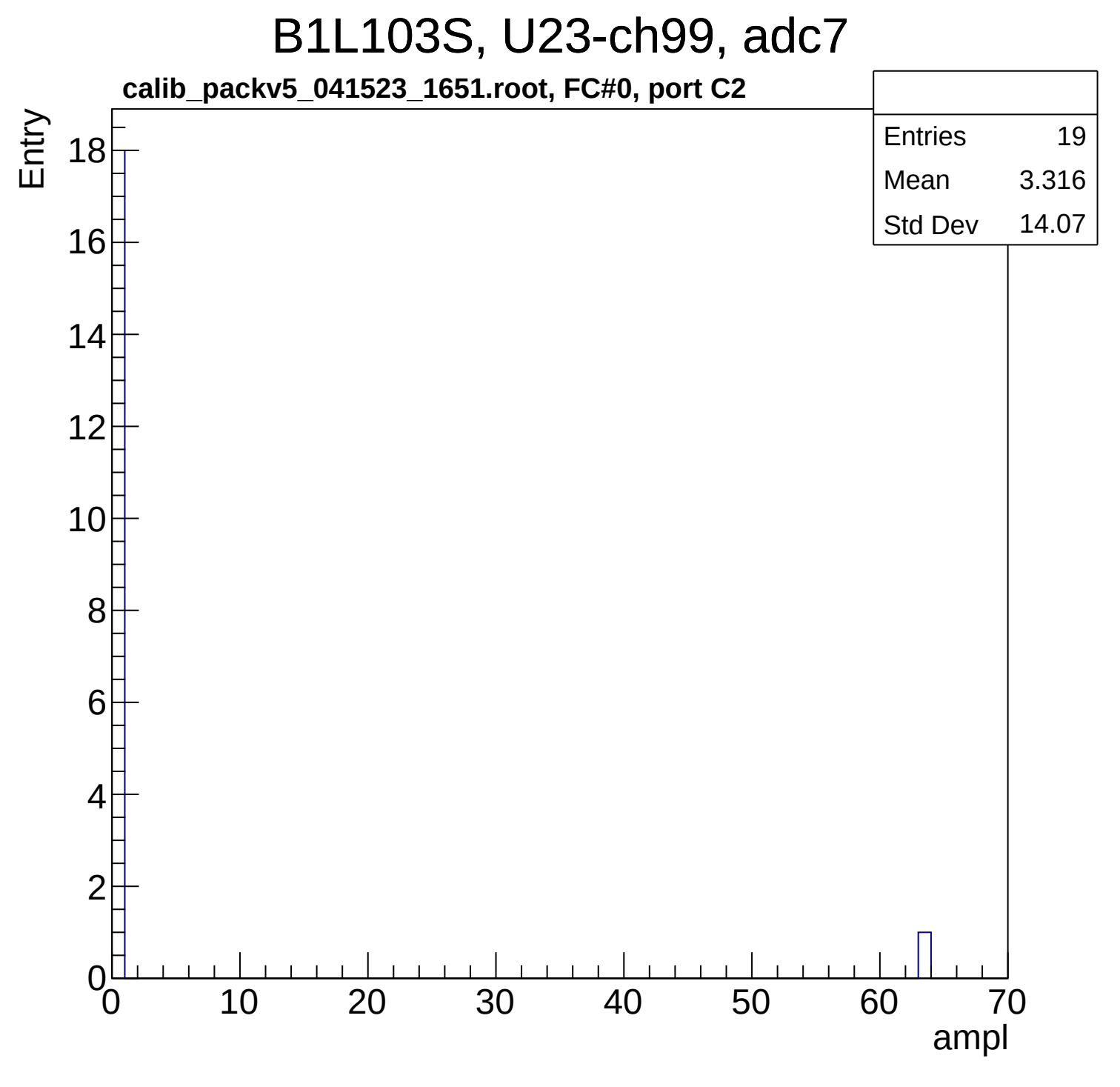

B1L103S, U23-ch99, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 40.74
Std Dev 2.979
Gaus mean : 40.9989
Gaus Width: 3.5075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

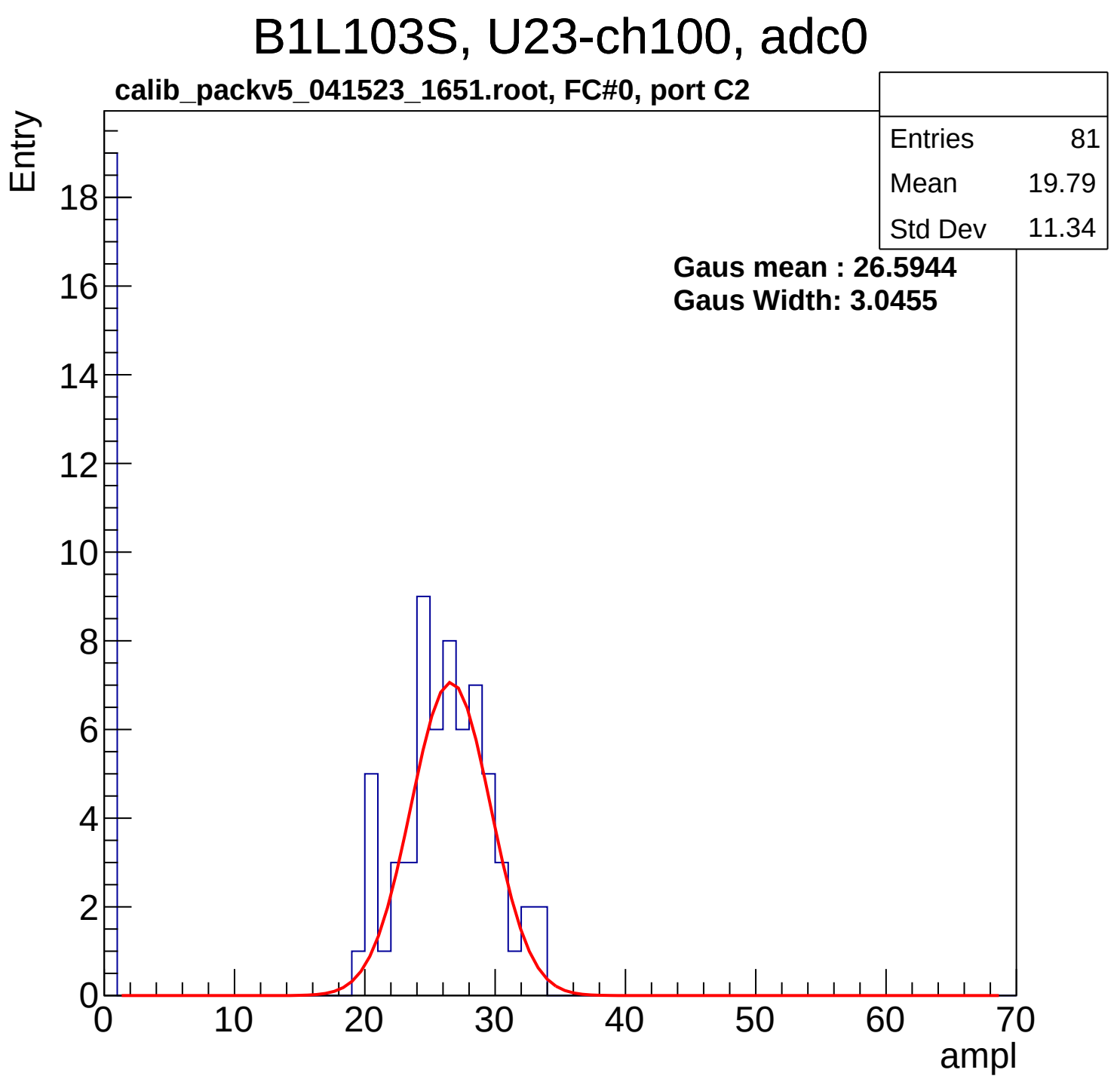

B1L103S, U23-ch99, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 43.67
Std Dev 11.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

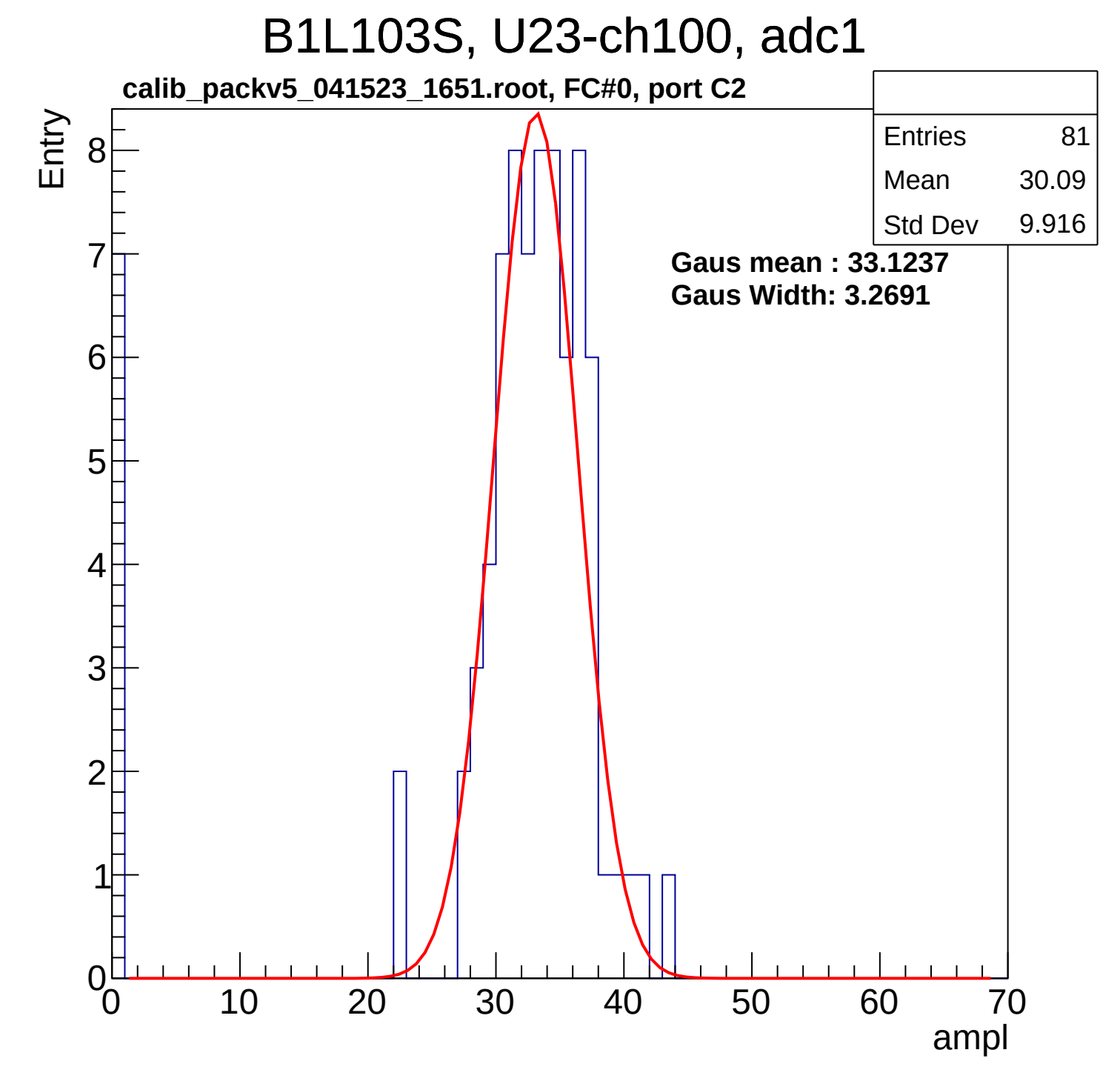

B1L103S, U23-ch99, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 51.82
Std Dev 7.489
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

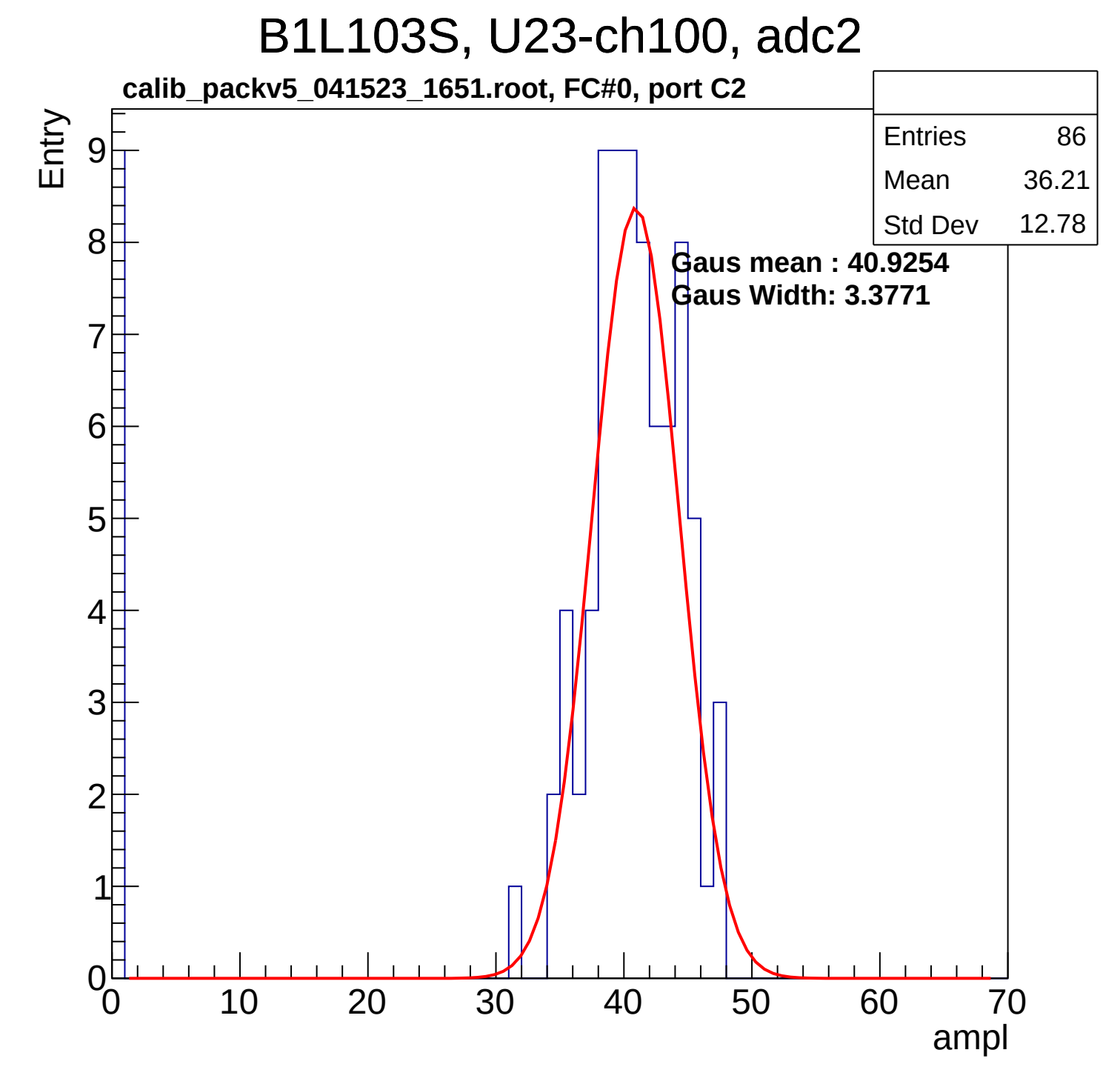

B1L103S, U23-ch99, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 58.25
Std Dev 2.85
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

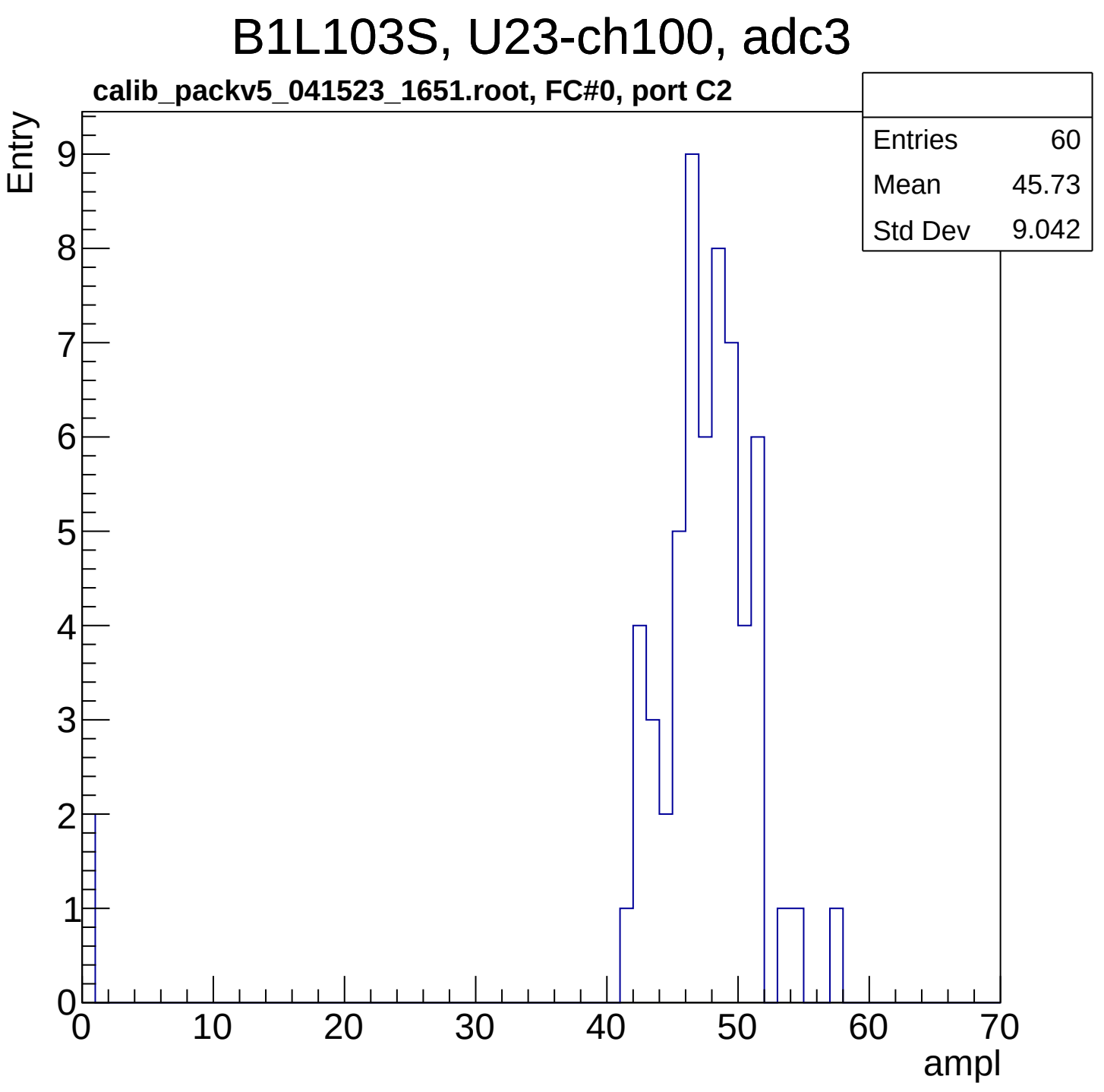

B1L103S, U23-ch99, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 58.05
Std Dev 13.8
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

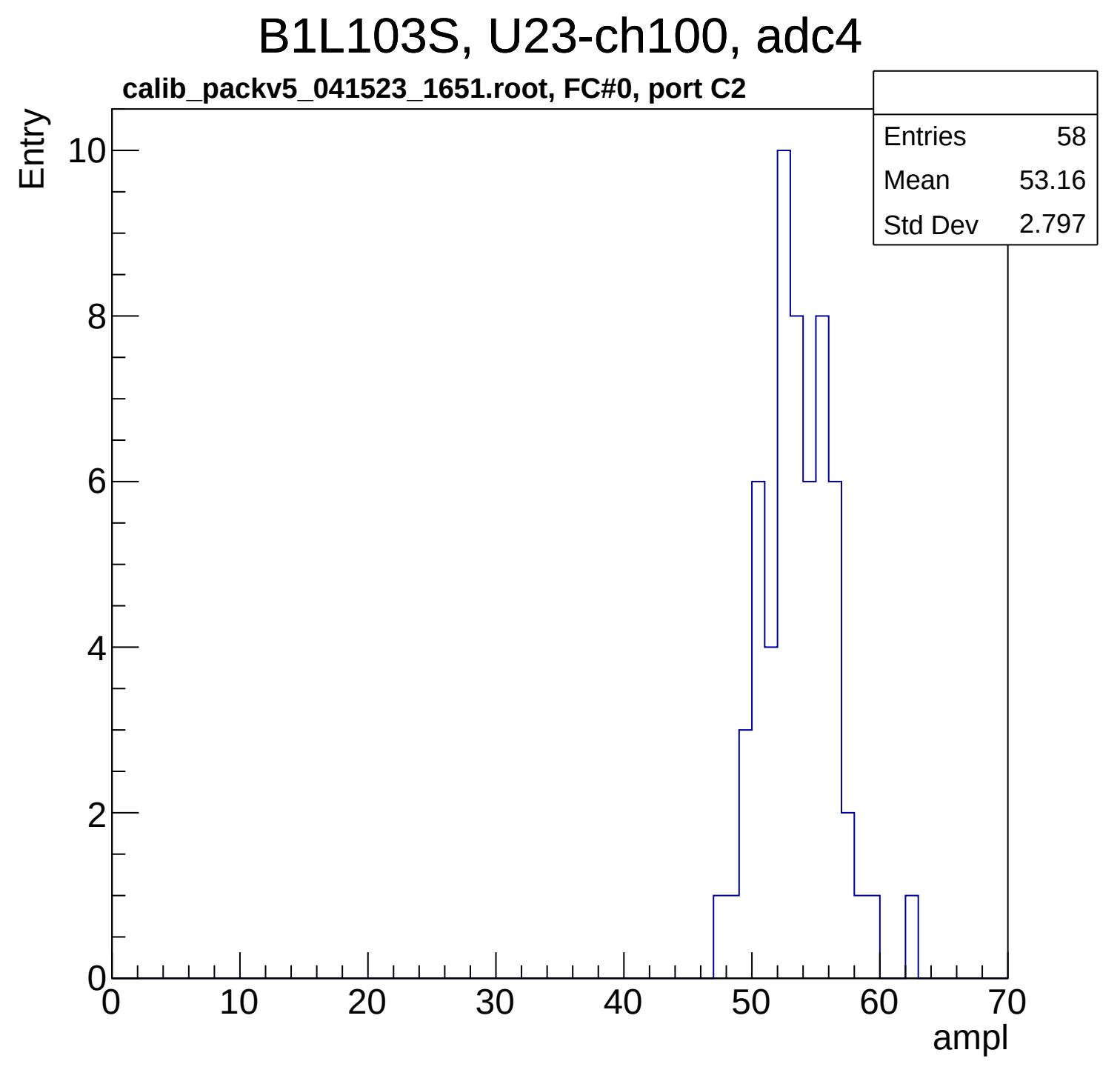

B1L103S, U23-ch99, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 3.316
Std Dev 14.07
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

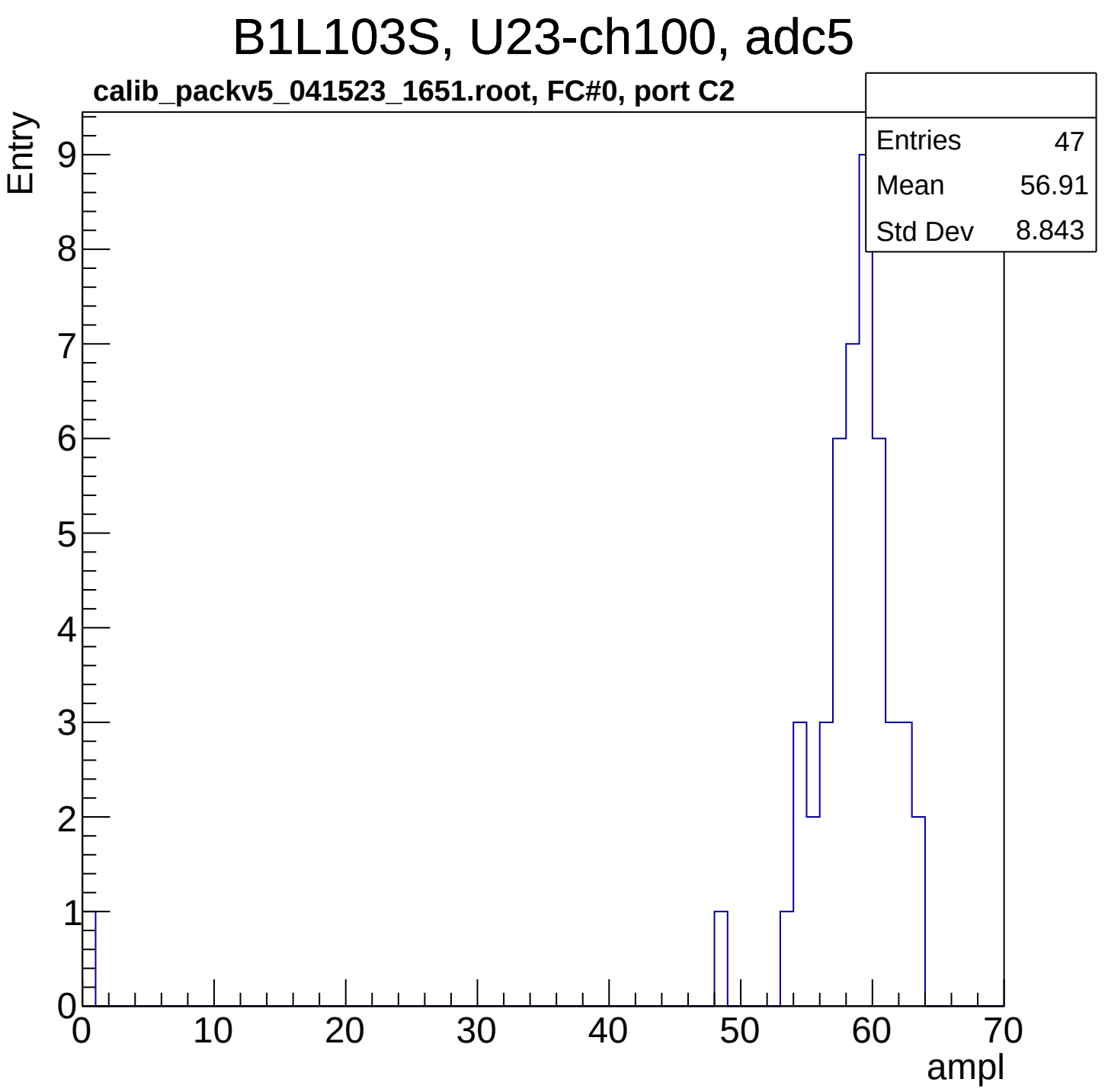

B1L103S, U23-ch100, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 19.79
Std Dev 11.34
Gaus mean : 26.5944
Gaus Width: 3.0455
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

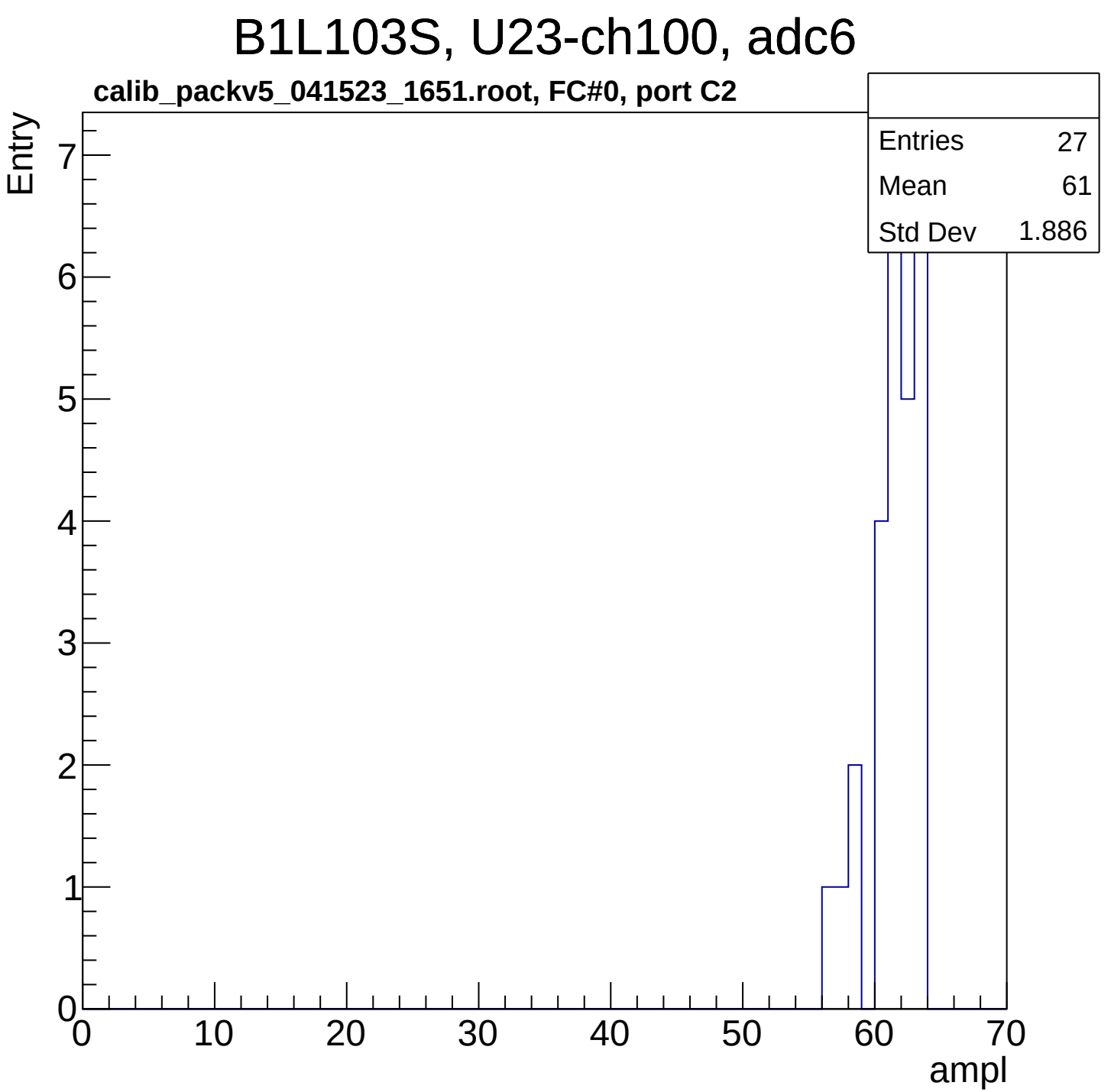

B1L103S, U23-ch100, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 30.09
Std Dev 9.916
Gaus mean : 33.1237
Gaus Width: 3.2691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

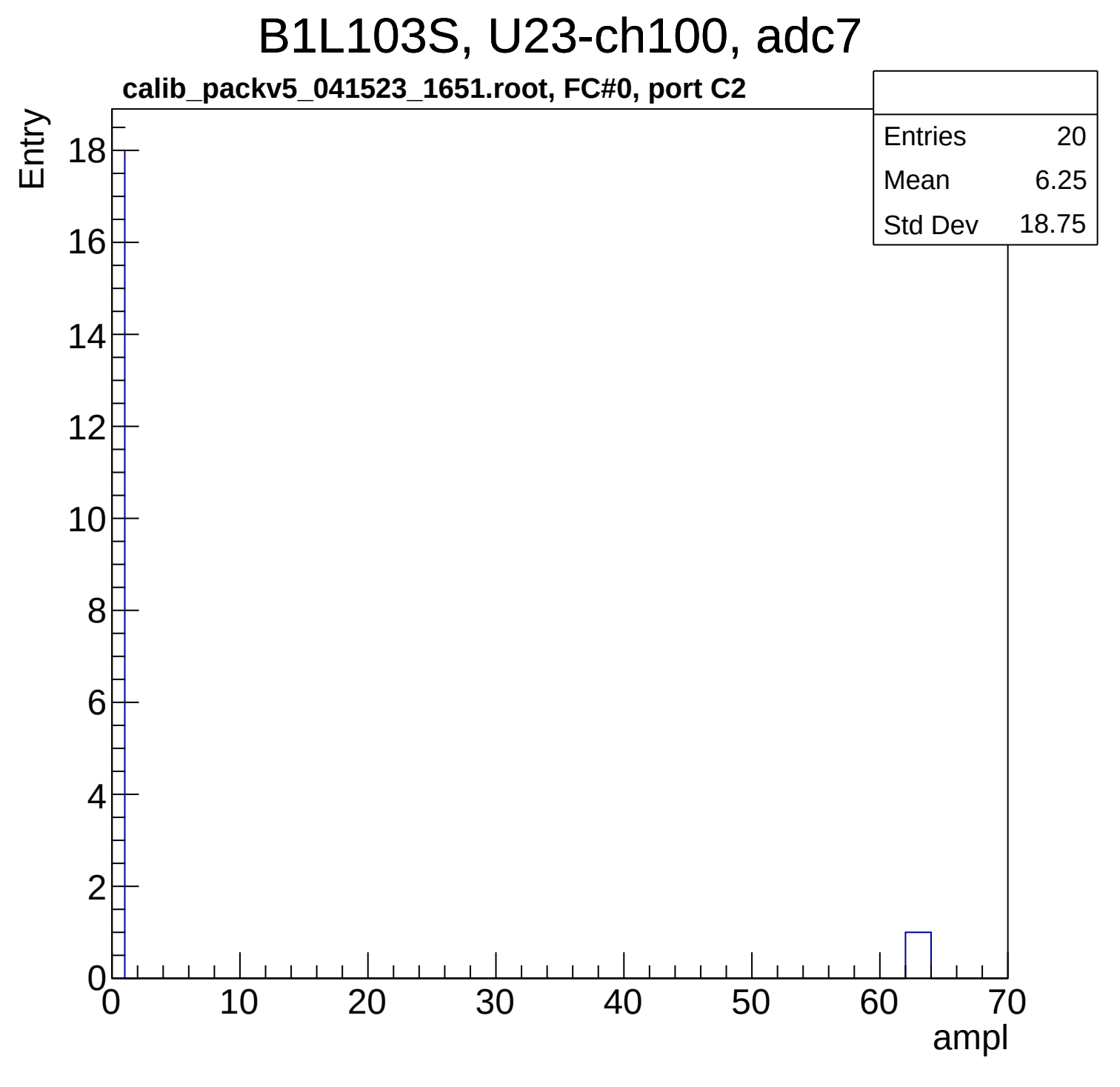

B1L103S, U23-ch100, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 36.21
Std Dev 12.78
Gaus mean : 40.9254
Gaus Width: 3.3771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

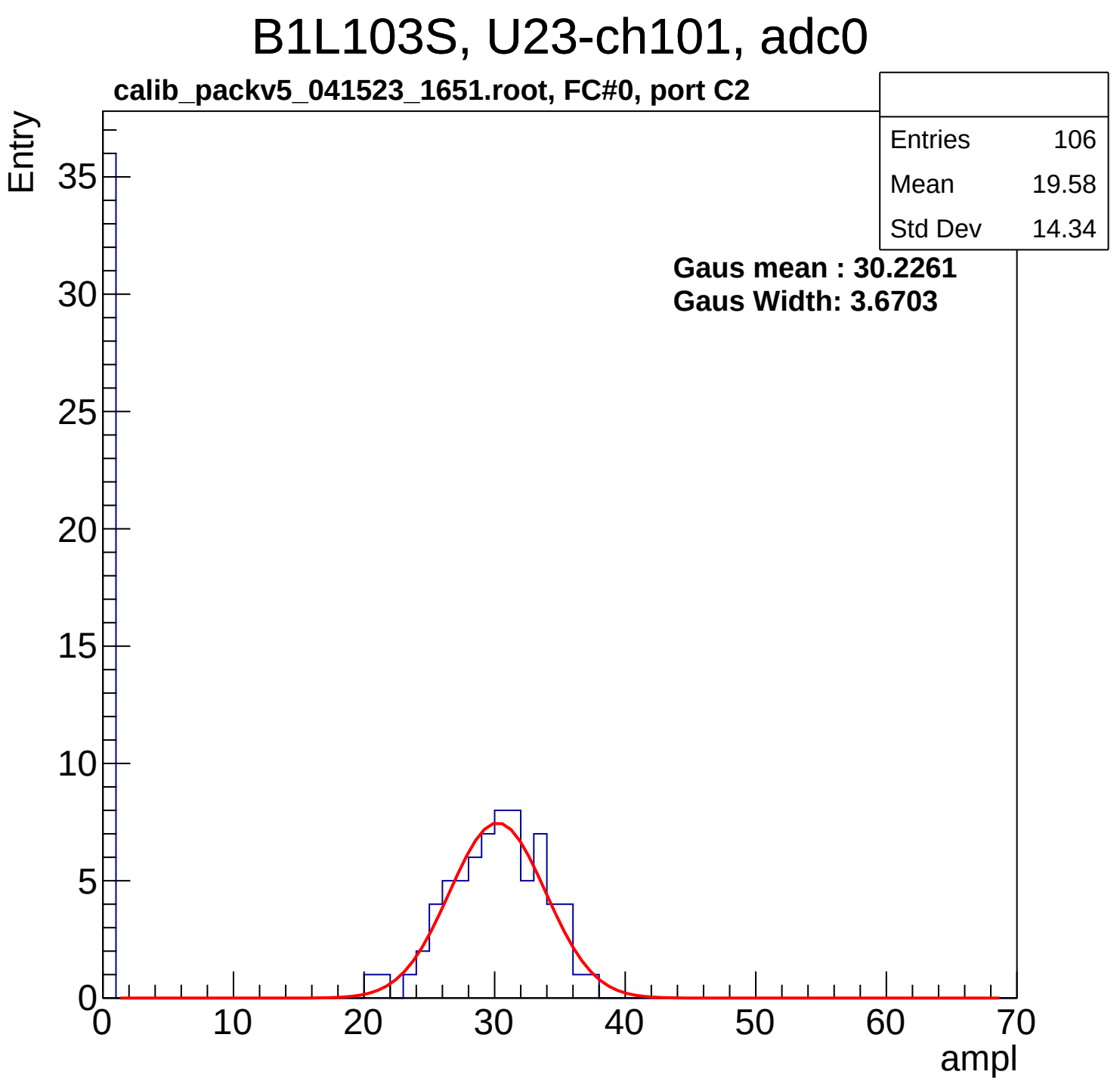

B1L103S, U23-ch100, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 45.73
Std Dev 9.042
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

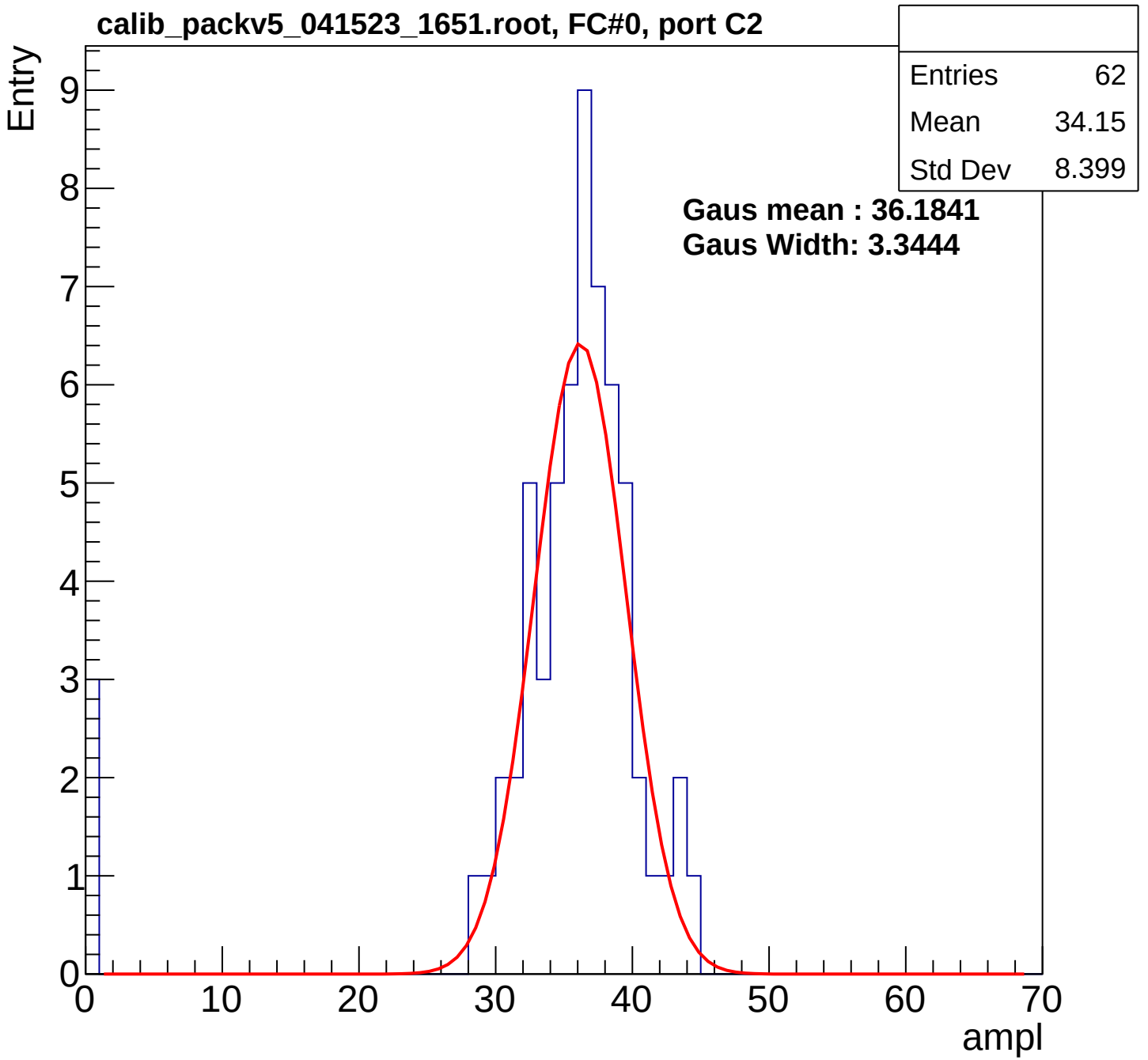

B1L103S, U23-ch100, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 53.16
Std Dev 2.797
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

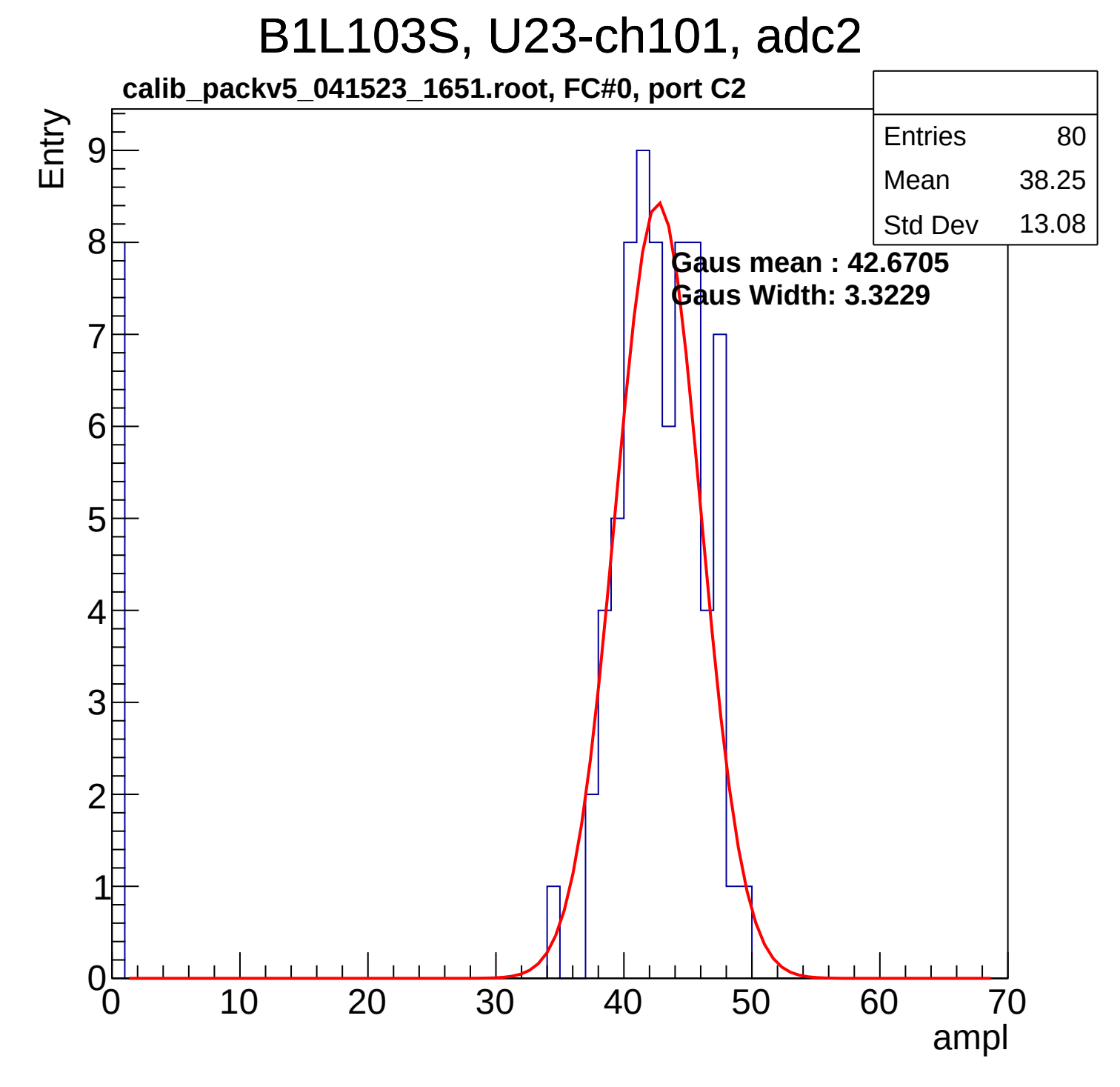

B1L103S, U23-ch100, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 47
Mean 56.91
Std Dev 8.843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

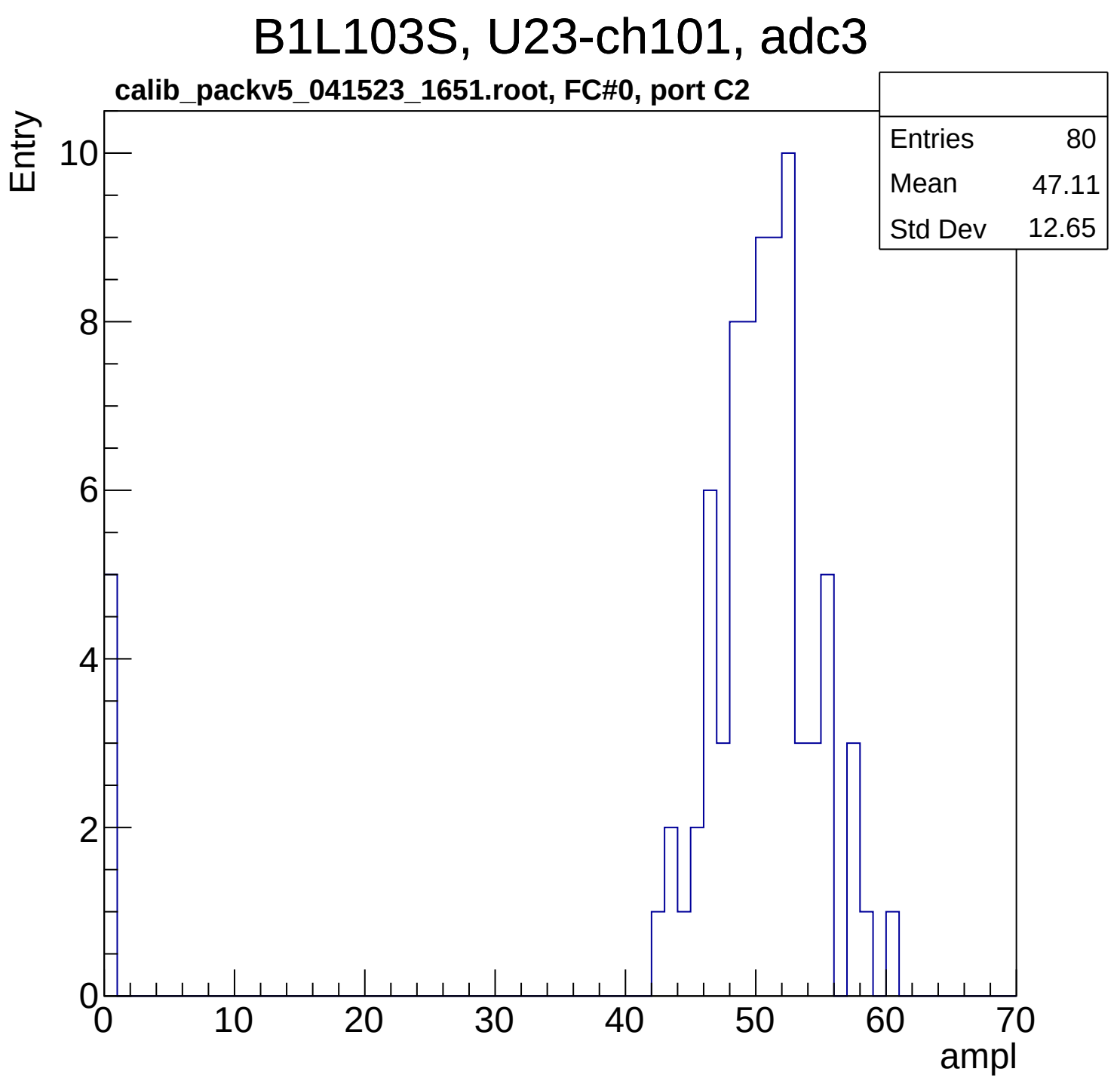

B1L103S, U23-ch100, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 27
Mean 61
Std Dev 1.886
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

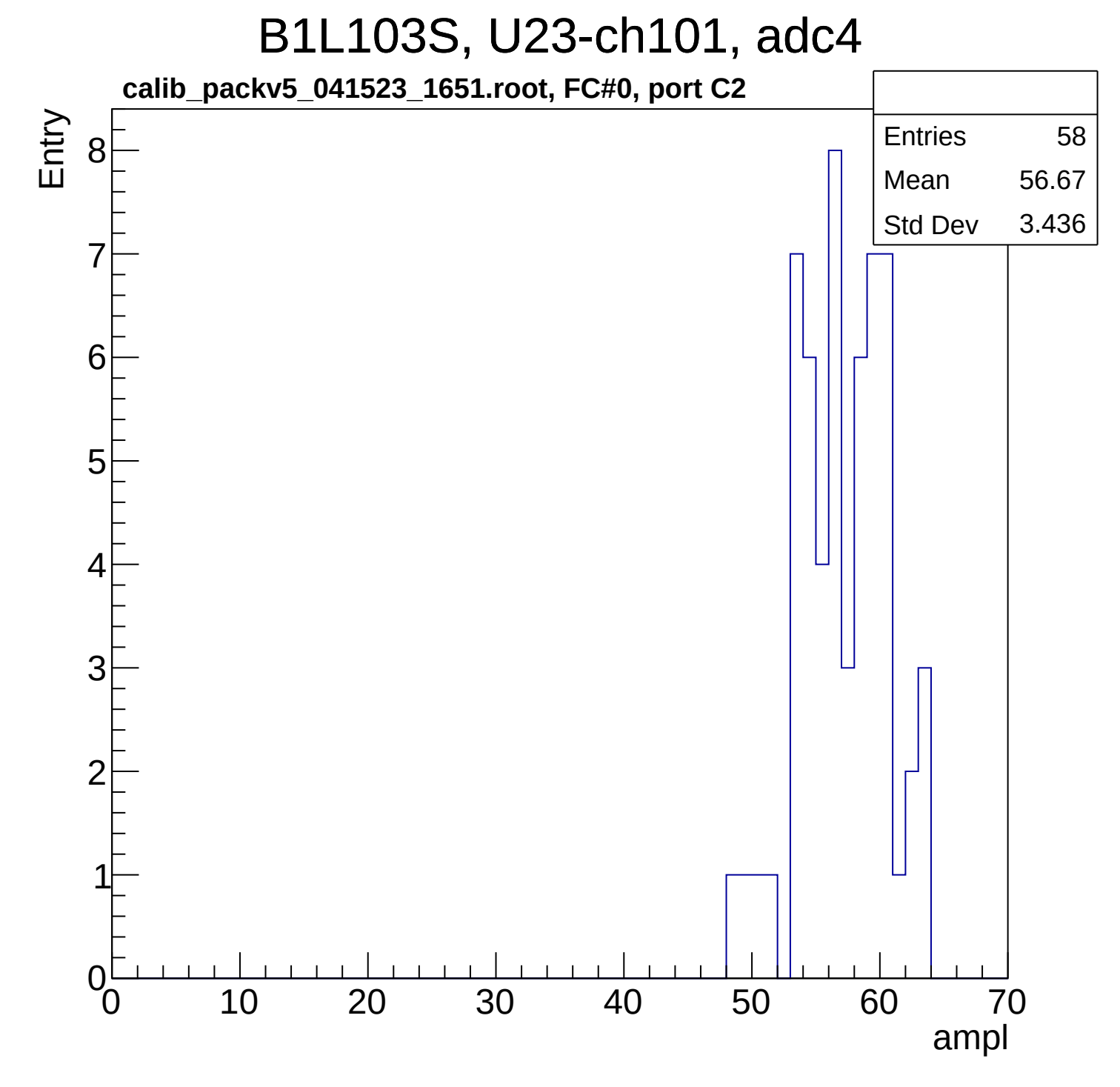

B1L103S, U23-ch100, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 6.25
Std Dev 18.75
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

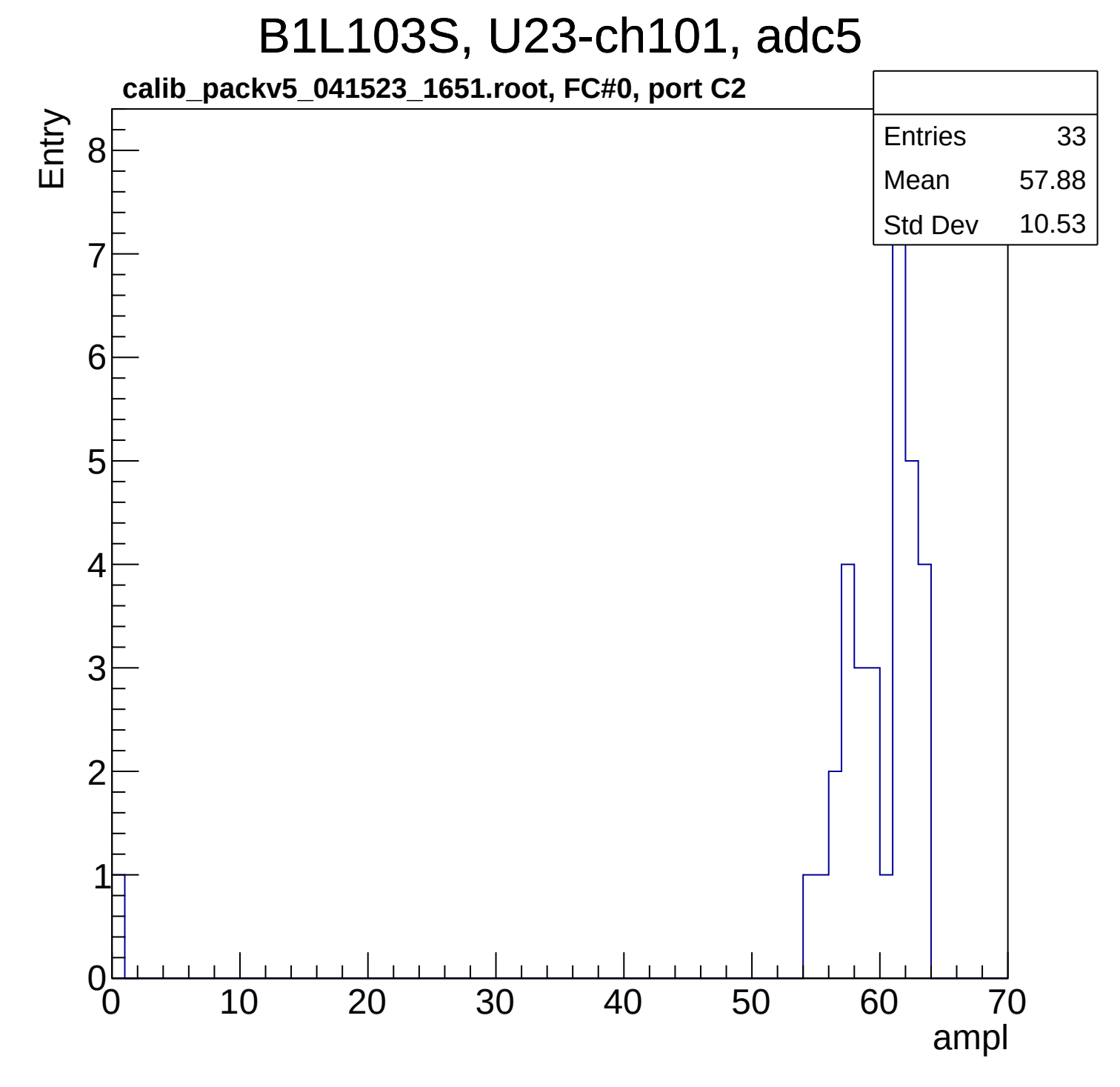

B1L103S, U23-ch101, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 106
Mean 19.58
Std Dev 14.34
Gaus mean : 30.2261
Gaus Width: 3.6703
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

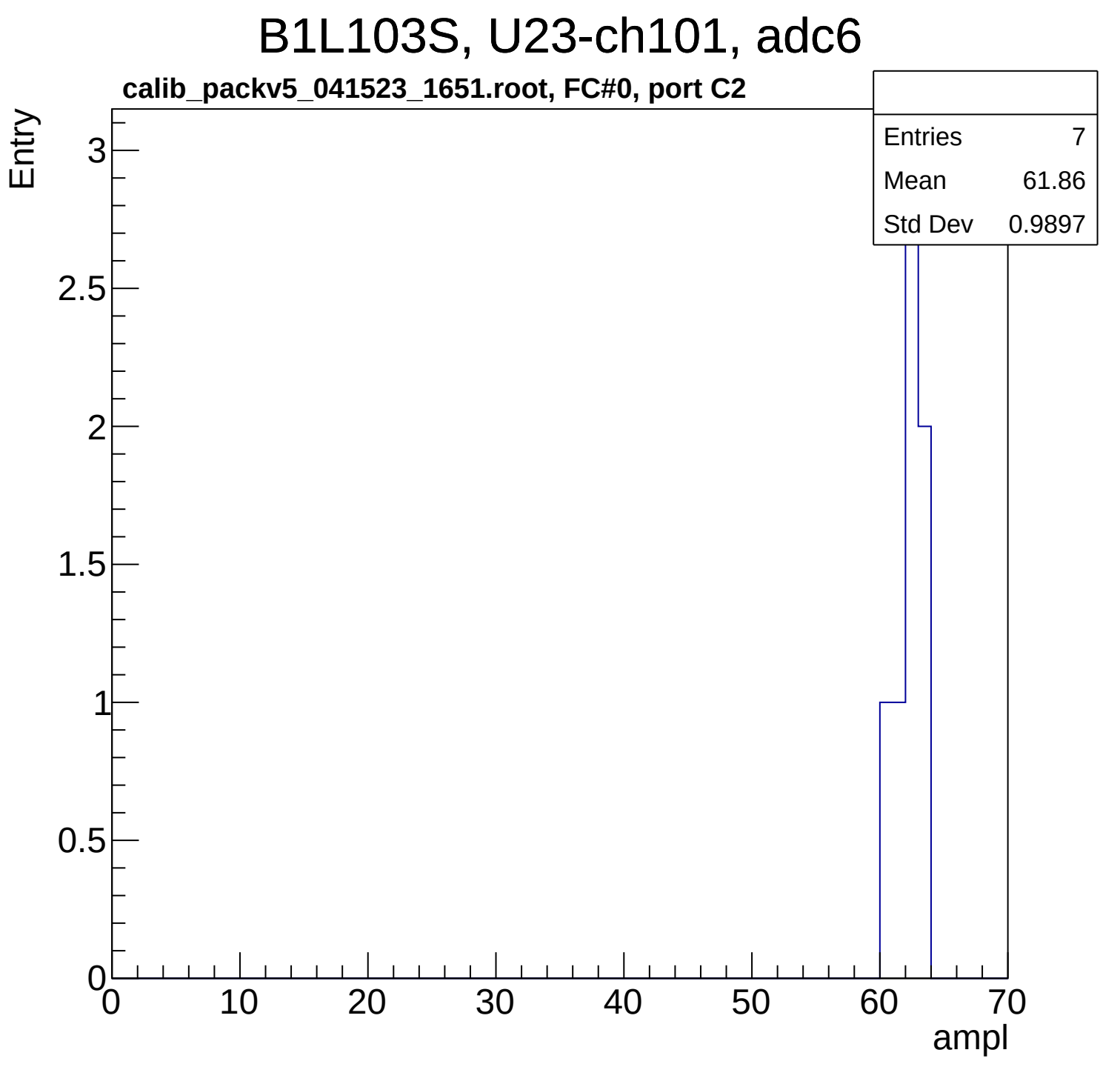

B1L103S, U23-ch101, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 34.15
Std Dev 8.399
Gaus mean : 36.1841
Gaus Width: 3.3444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

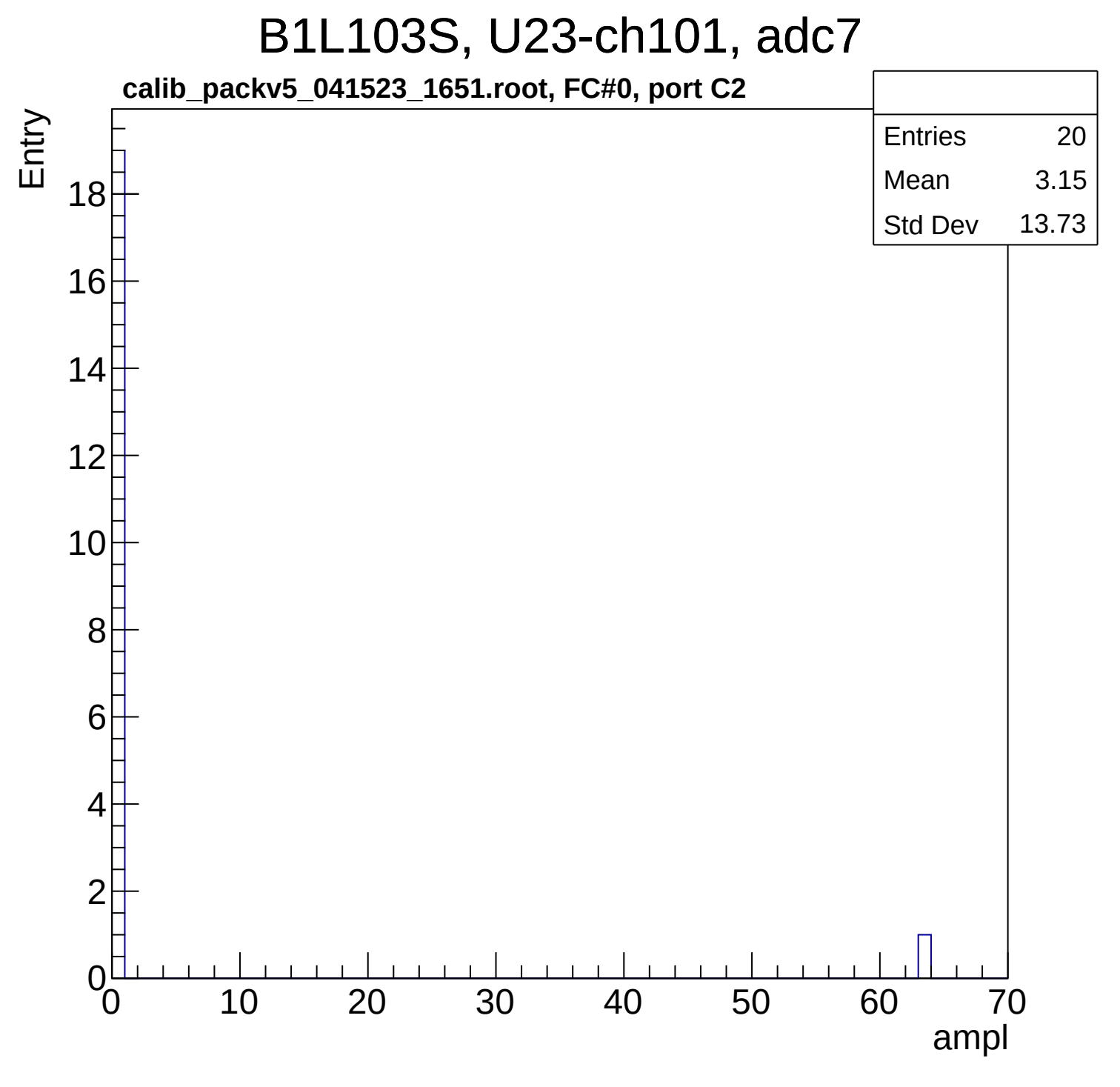

B1L103S, U23-ch101, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 38.25
Std Dev 13.08
Gaus mean : 42.6705
Gaus Width: 3.3229
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

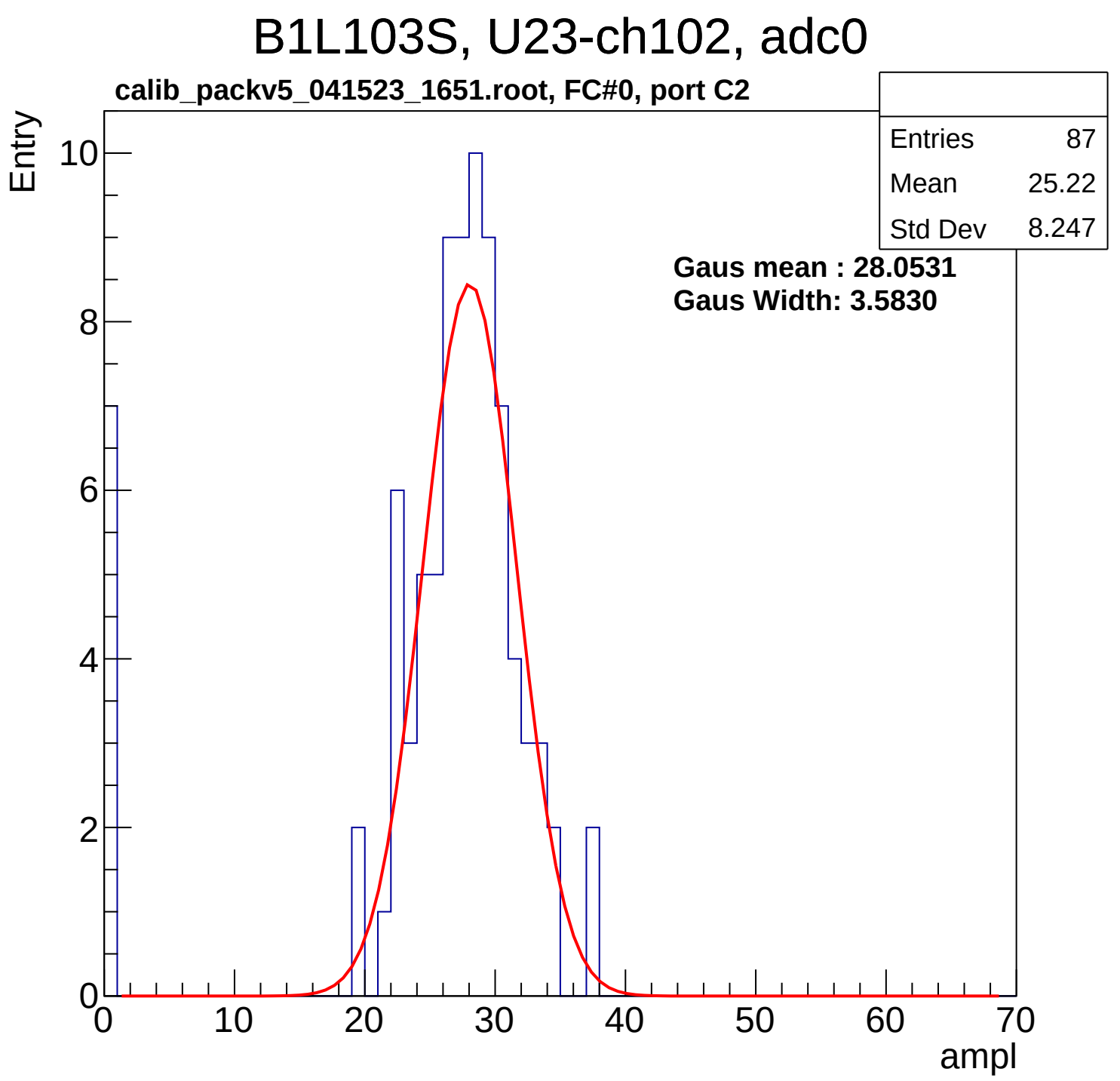

B1L103S, U23-ch101, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 47.11
Std Dev 12.65
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

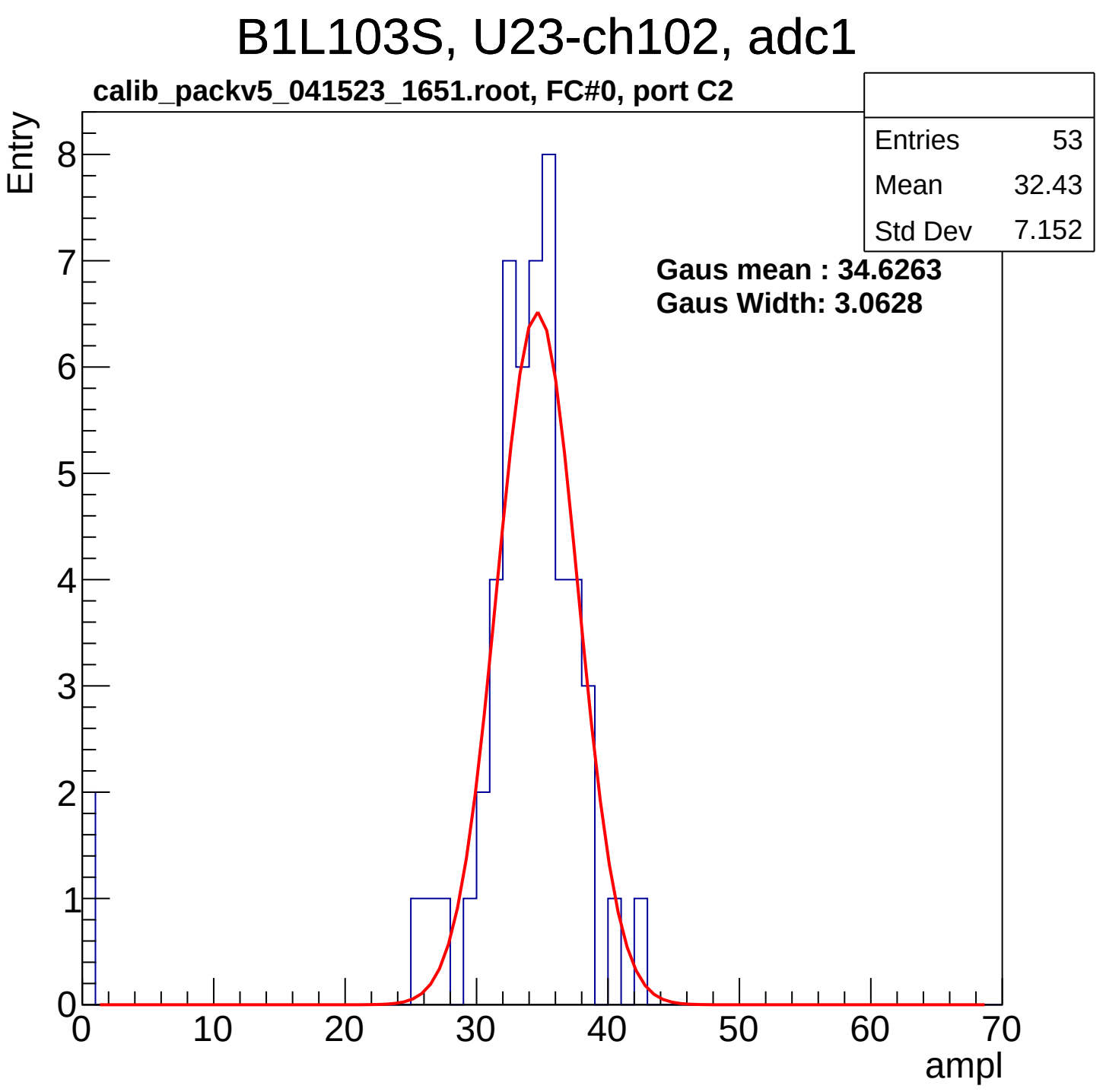

B1L103S, U23-ch101, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 56.67
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

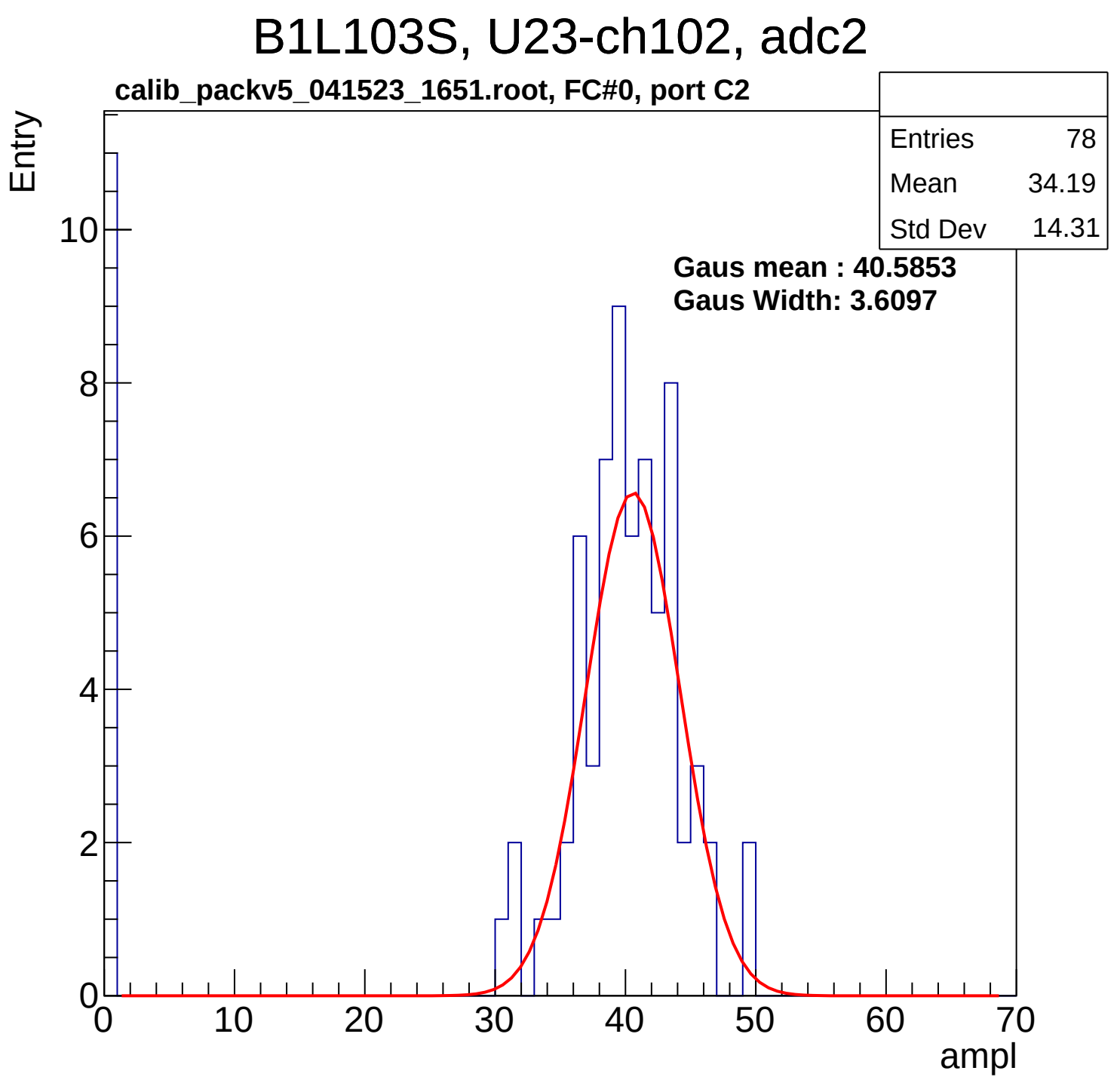

B1L103S, U23-ch101, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 33
Mean 57.88
Std Dev 10.53
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

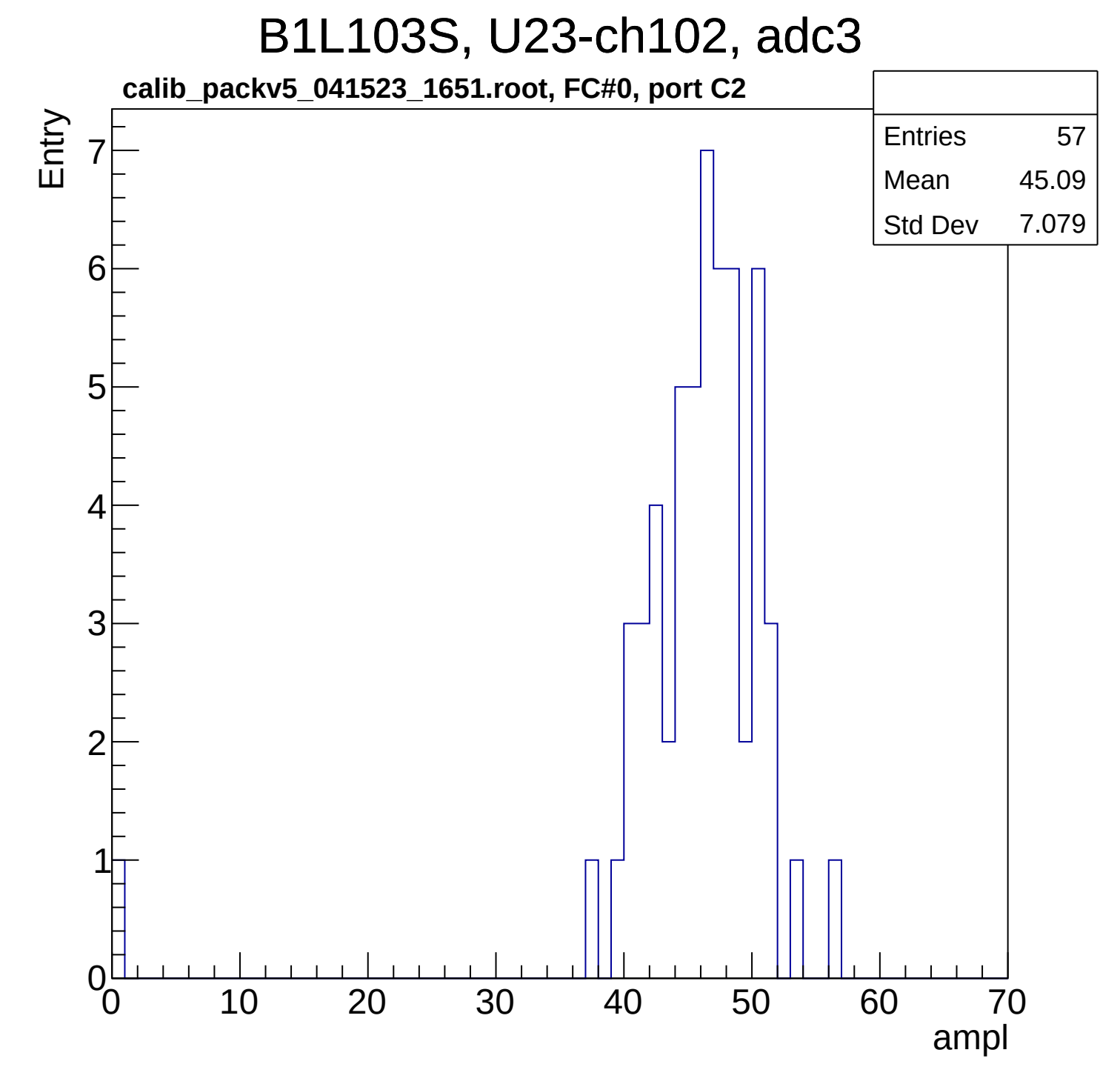

B1L103S, U23-ch101, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

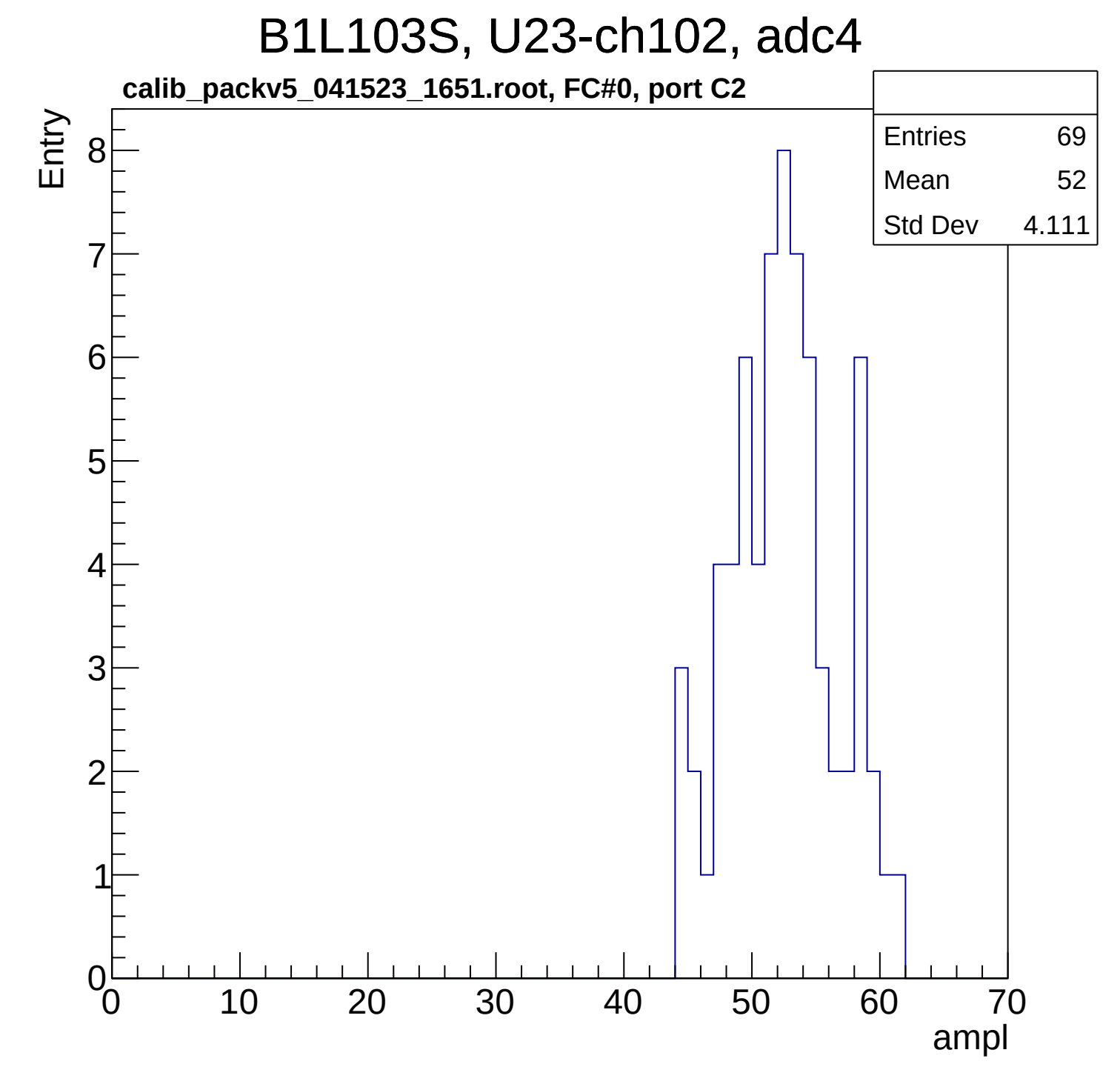

B1L103S, U23-ch101, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.15
Std Dev 13.73
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

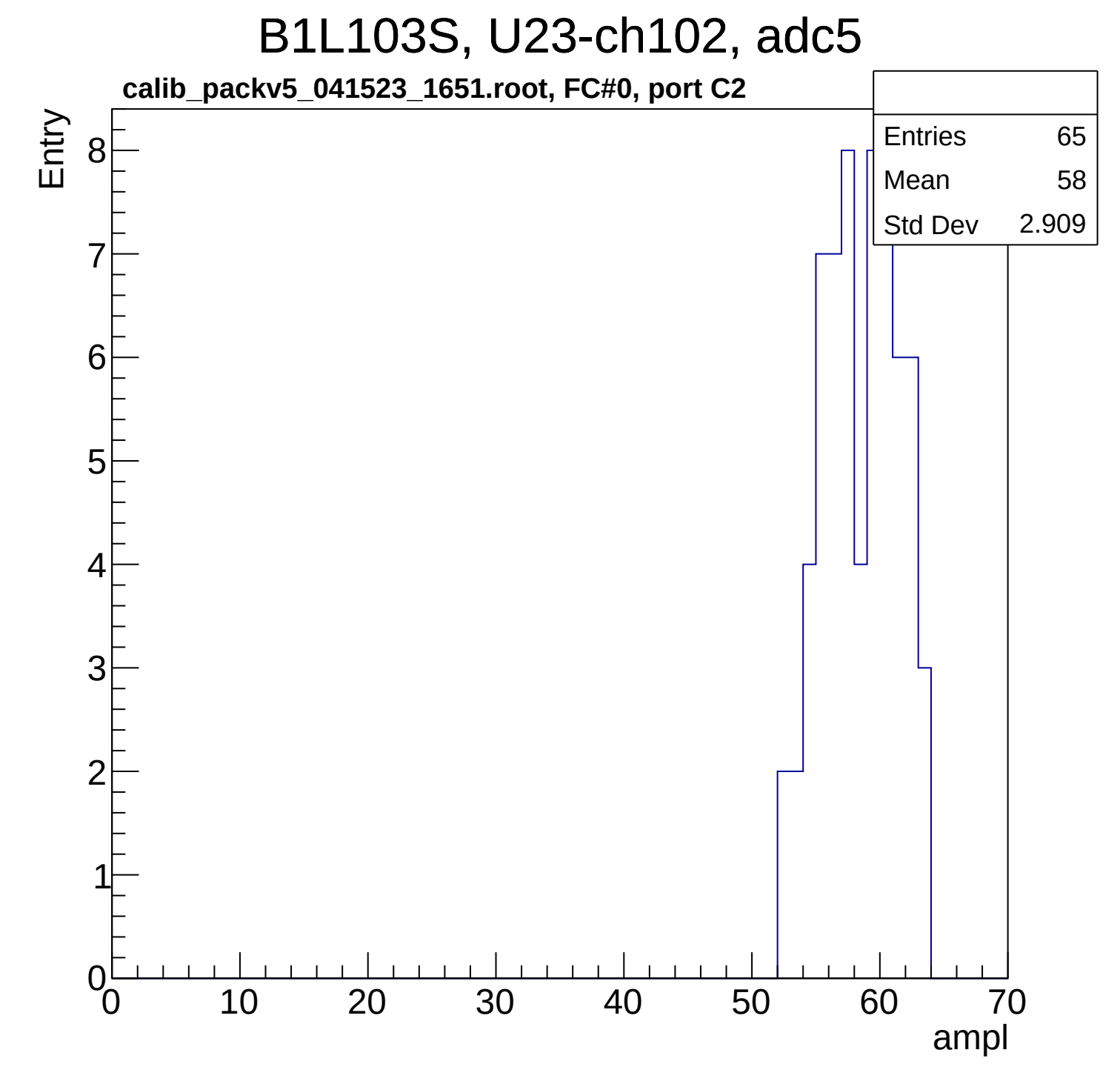

B1L103S, U23-ch102, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 87
Mean 25.22
Std Dev 8.247
Gaus mean : 28.0531
Gaus Width: 3.5830
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

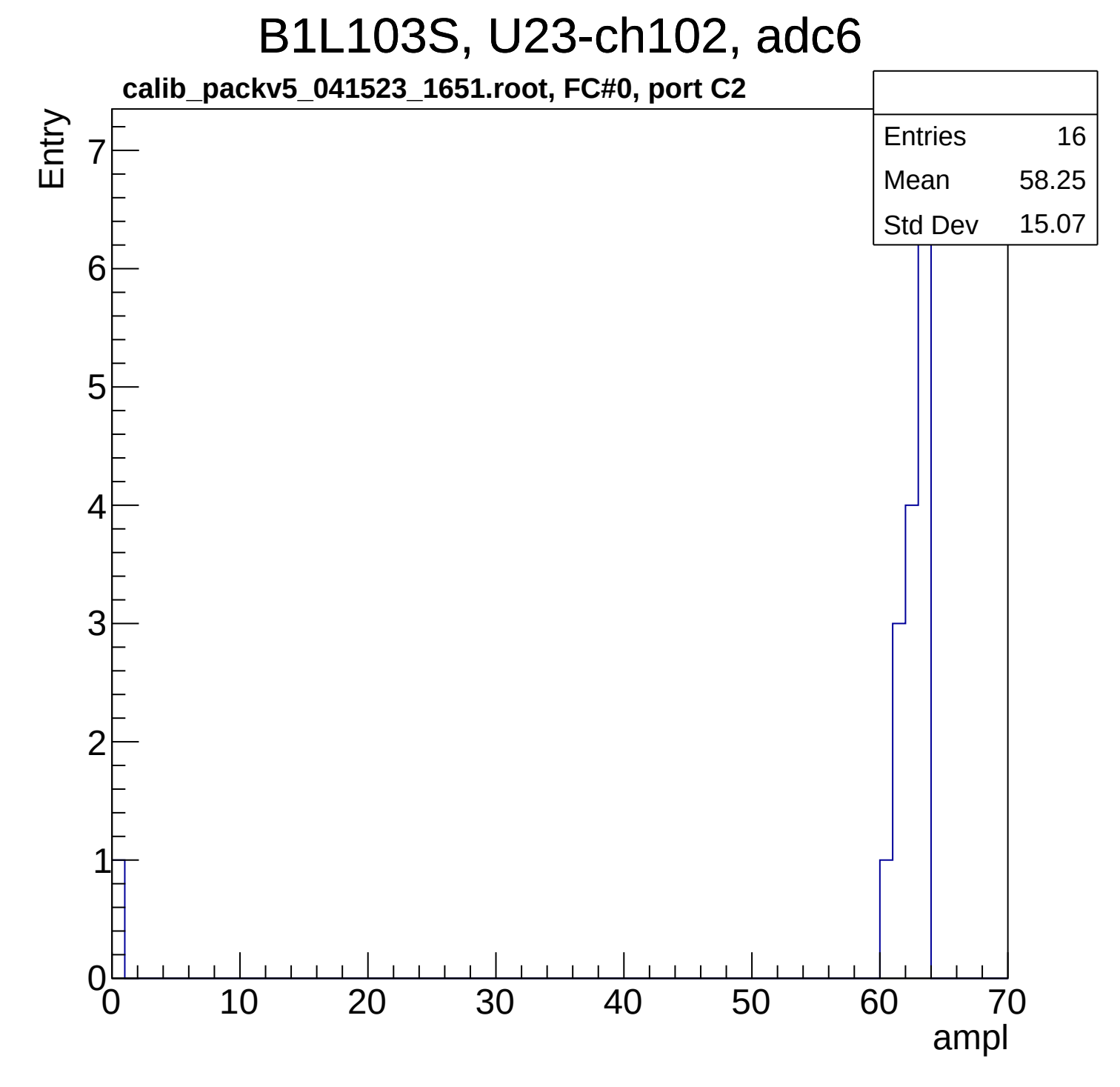

B1L103S, U23-ch102, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 32.43
Std Dev 7.152
Gaus mean : 34.6263
Gaus Width: 3.0628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

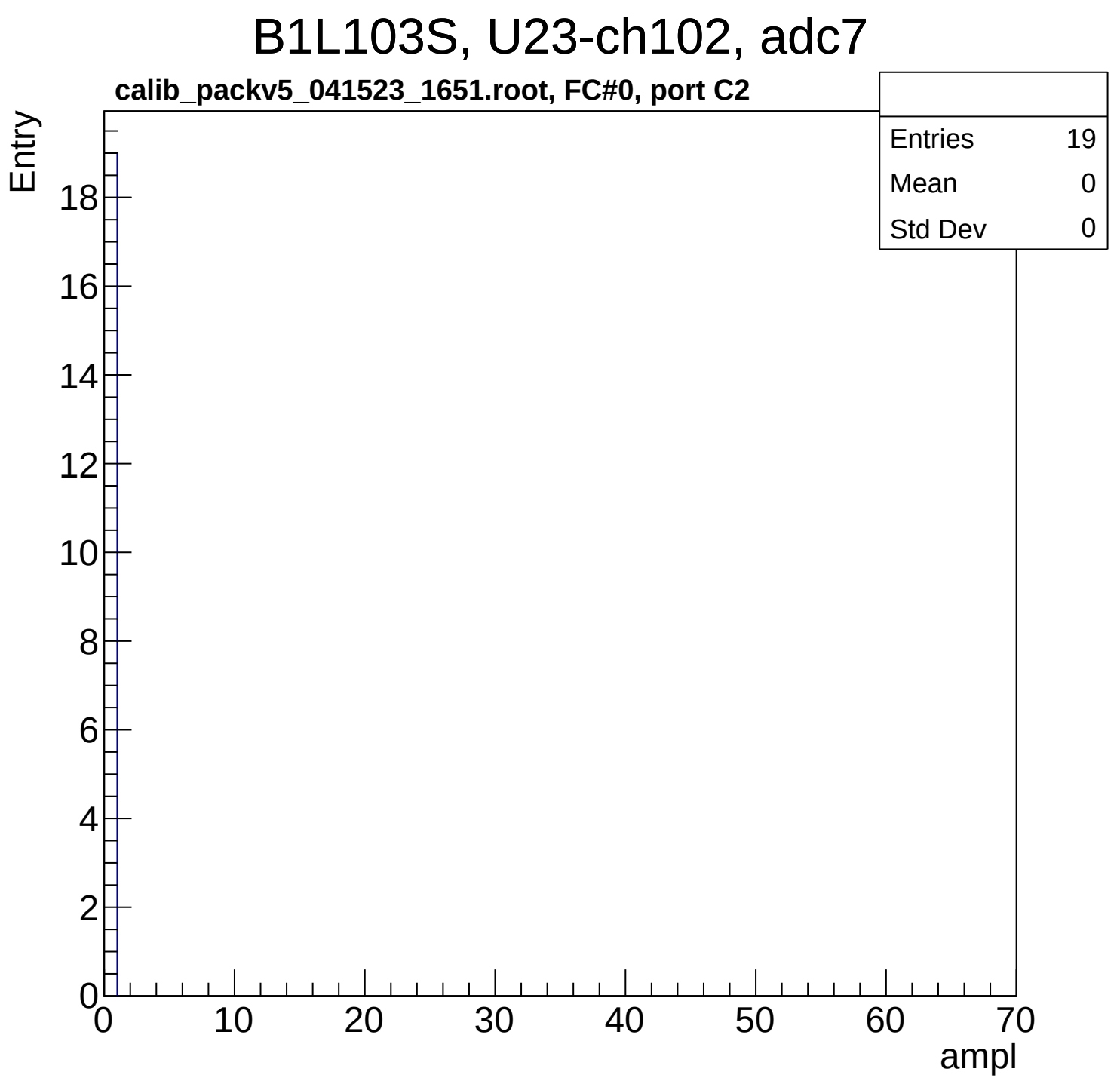

B1L103S, U23-ch102, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 78
Mean 34.19
Std Dev 14.31
Gaus mean : 40.5853
Gaus Width: 3.6097
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

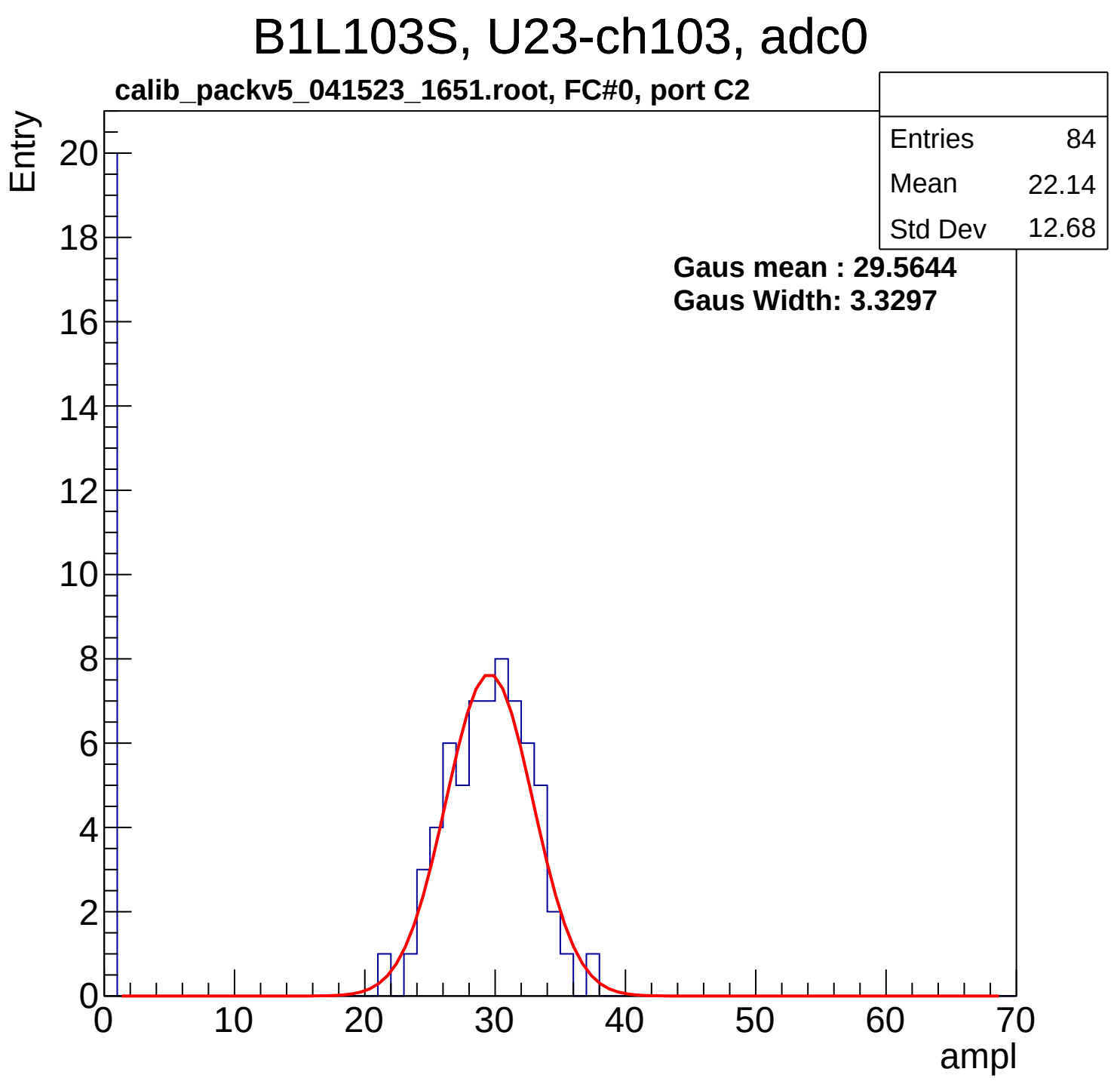

B1L103S, U23-ch102, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 45.09
Std Dev 7.079
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

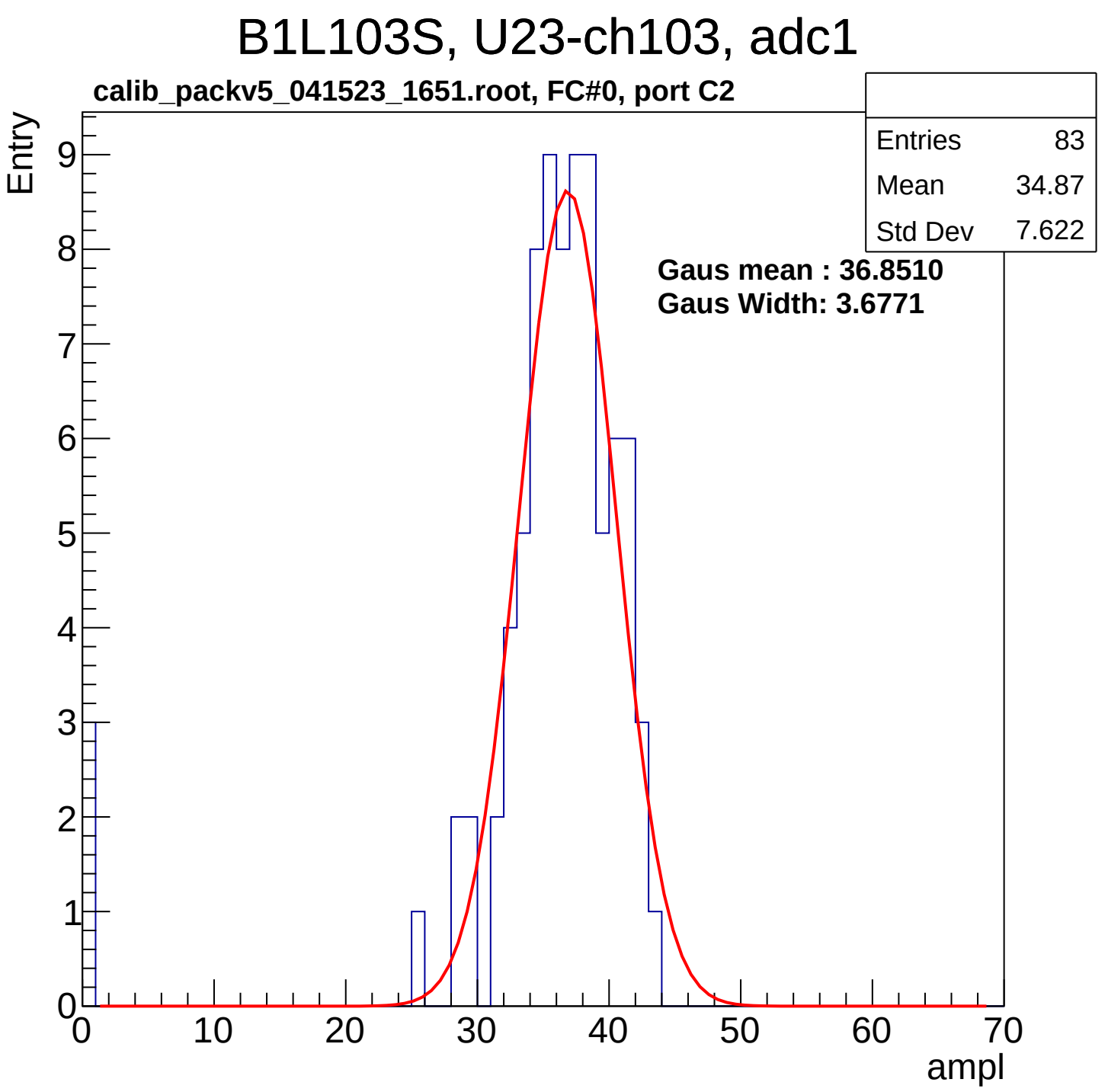

B1L103S, U23-ch102, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 52
Std Dev 4.111
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

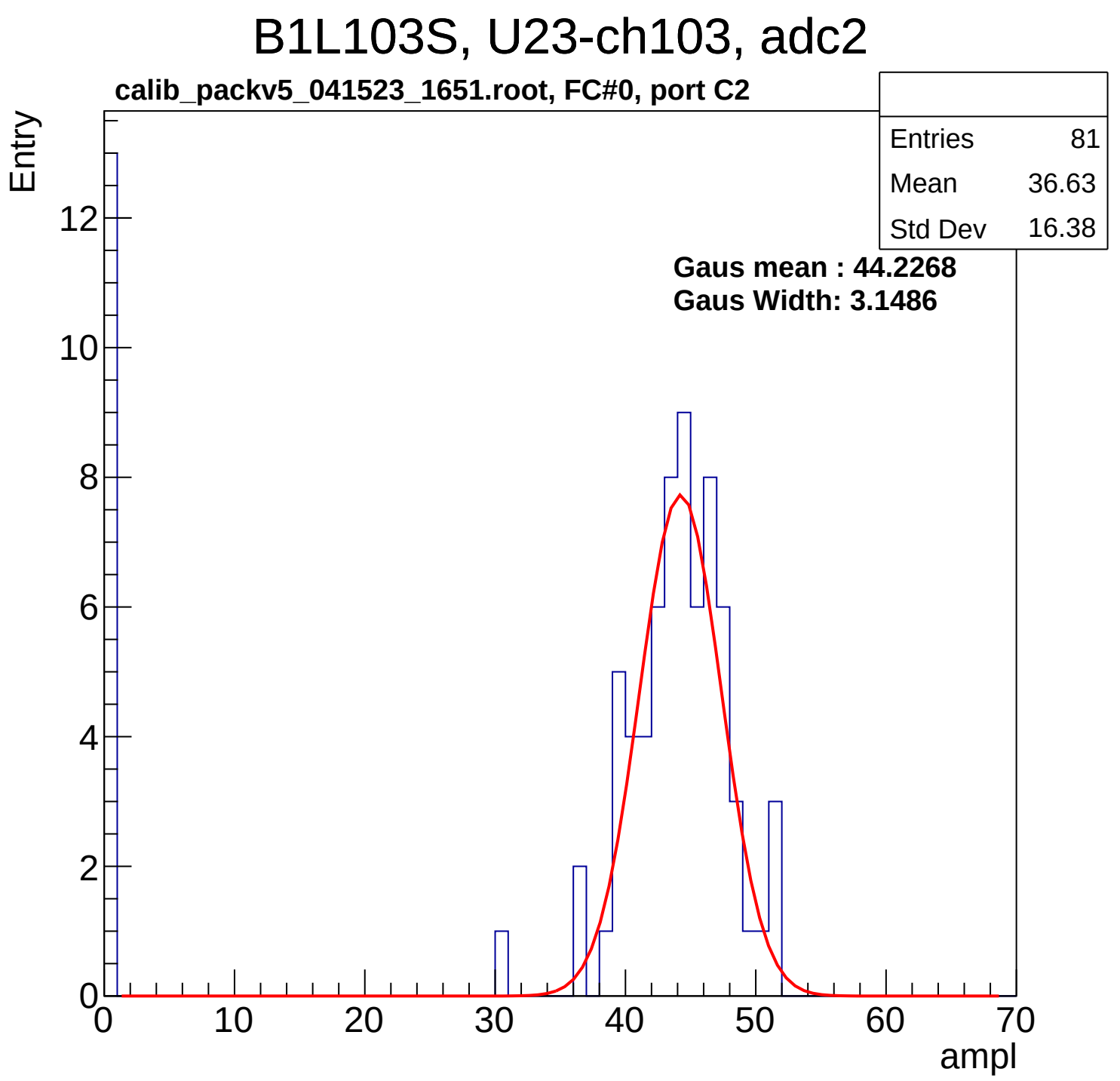

B1L103S, U23-ch102, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 58
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

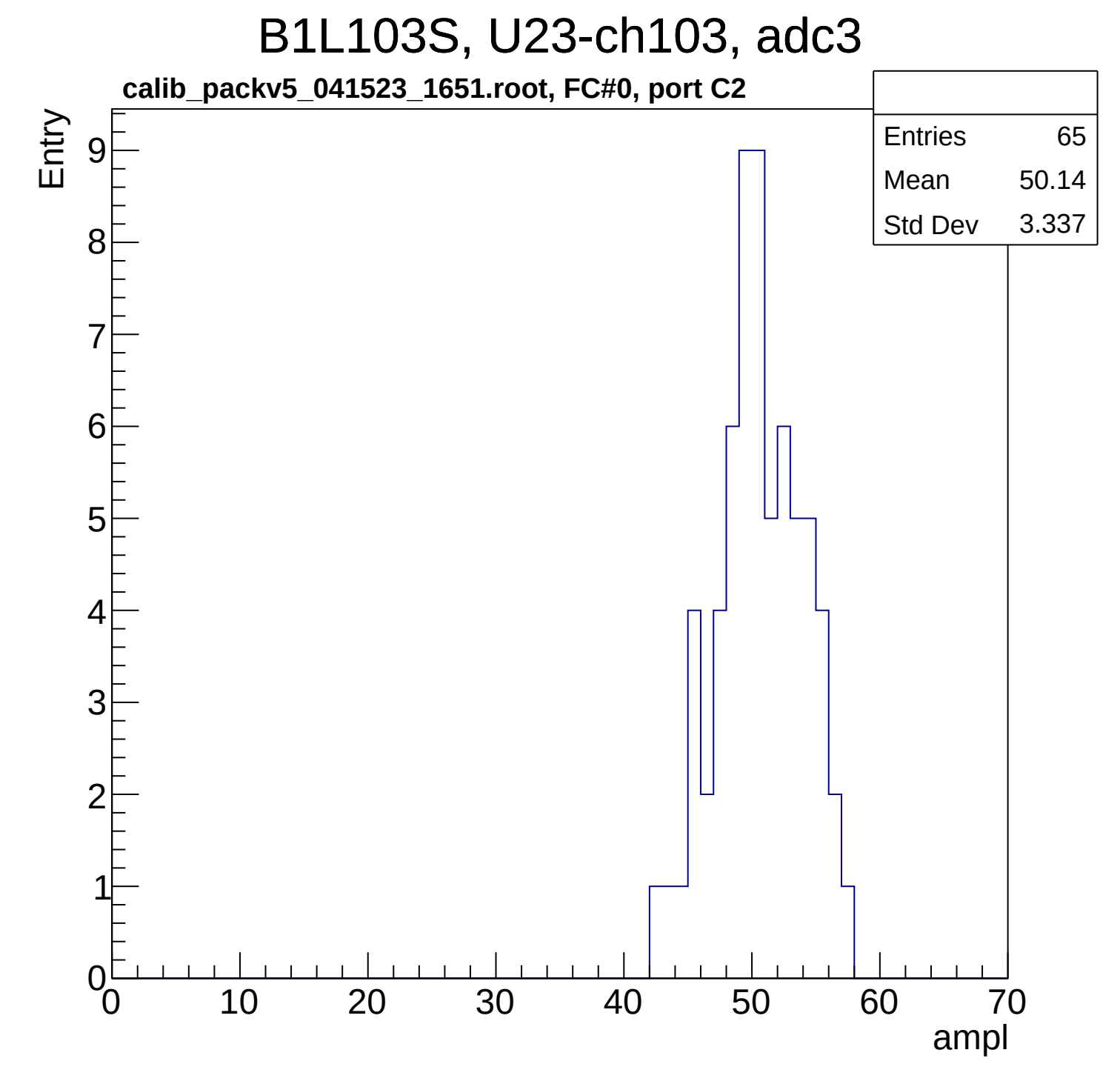

B1L103S, U23-ch102, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 16
Mean 58.25
Std Dev 15.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

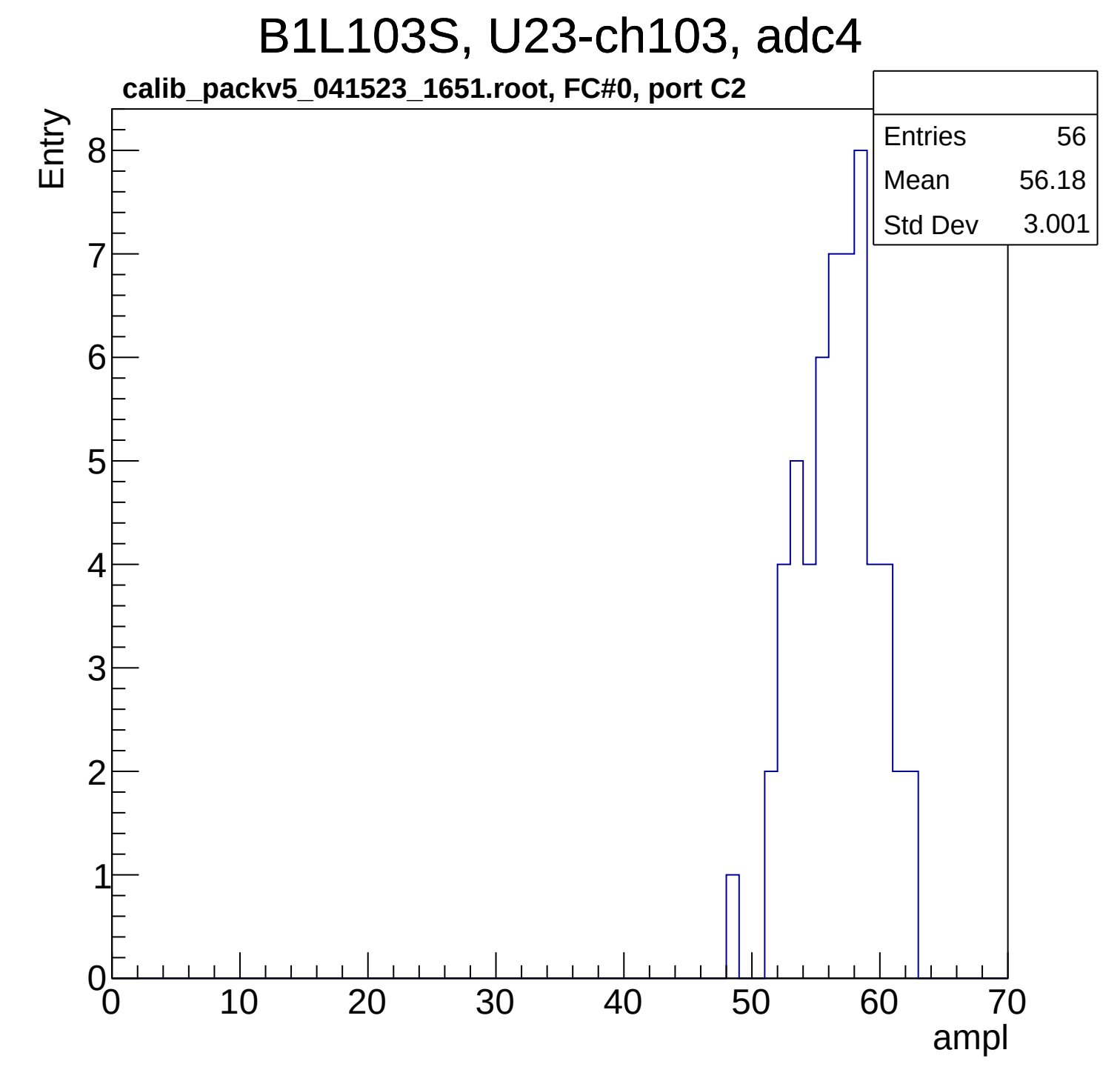

B1L103S, U23-ch102, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

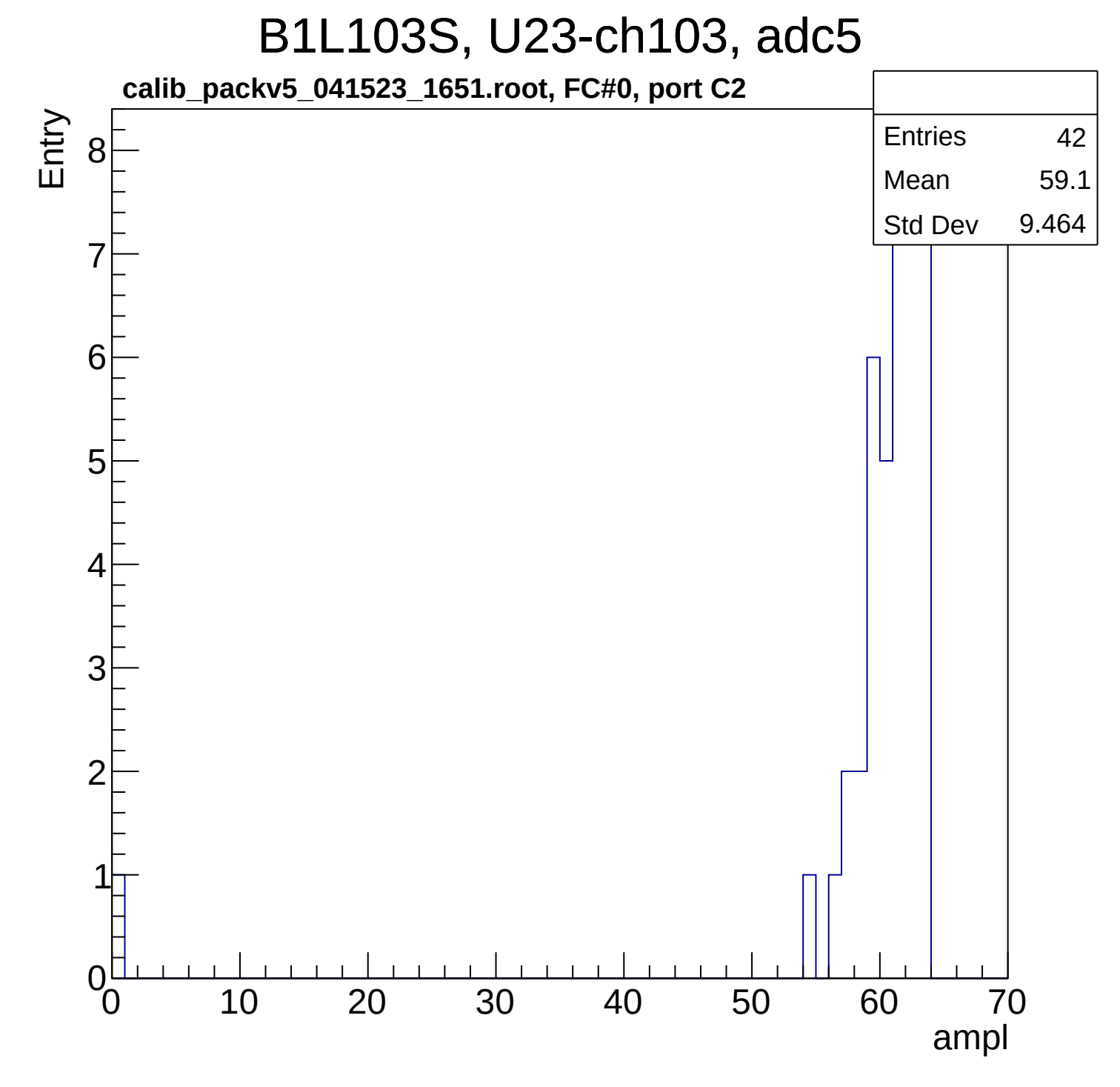

B1L103S, U23-ch103, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 84
Mean 22.14
Std Dev 12.68
Gaus mean : 29.5644
Gaus Width: 3.3297
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

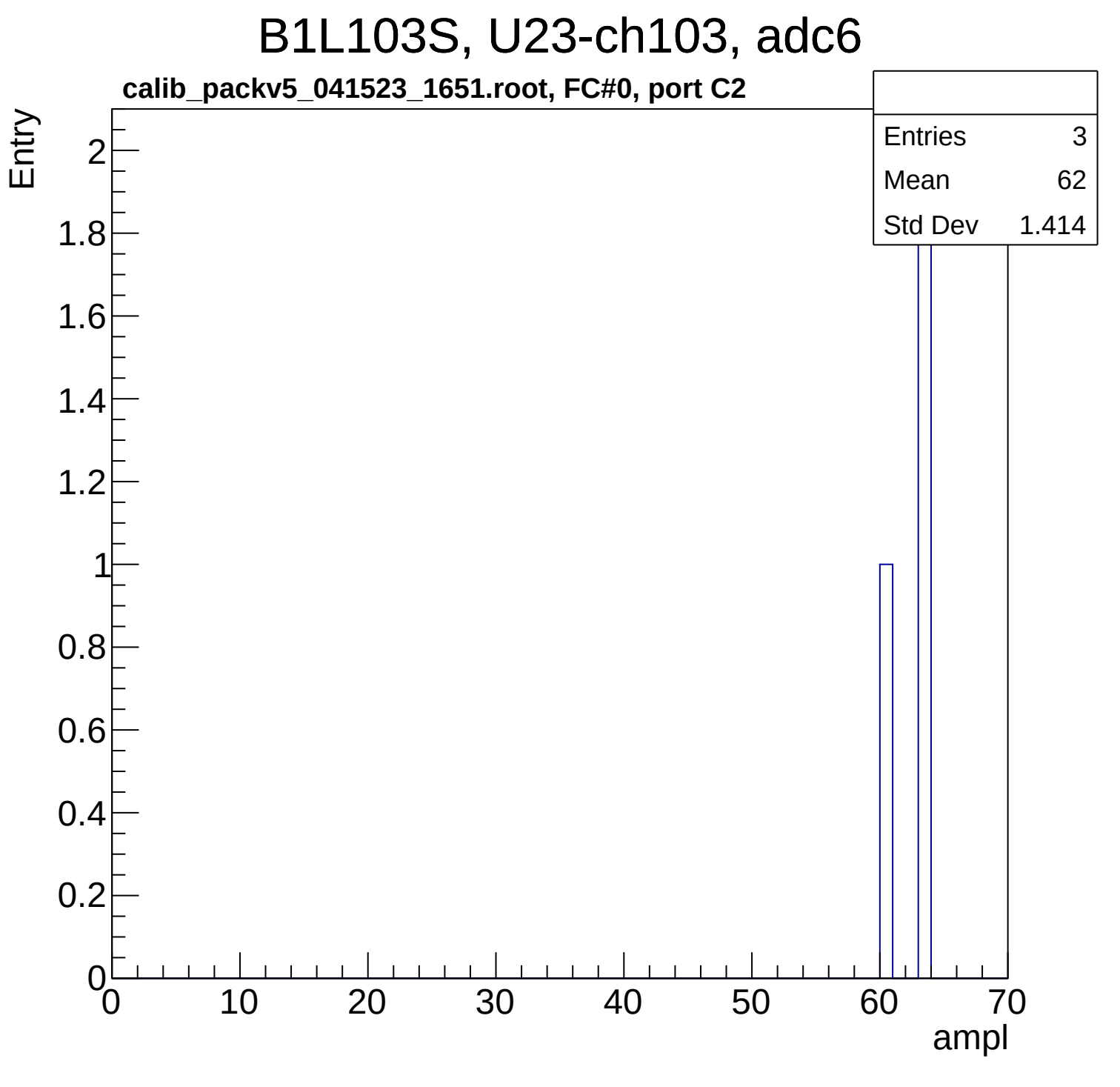

B1L103S, U23-ch103, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 34.87
Std Dev 7.622
Gaus mean : 36.8510
Gaus Width: 3.6771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

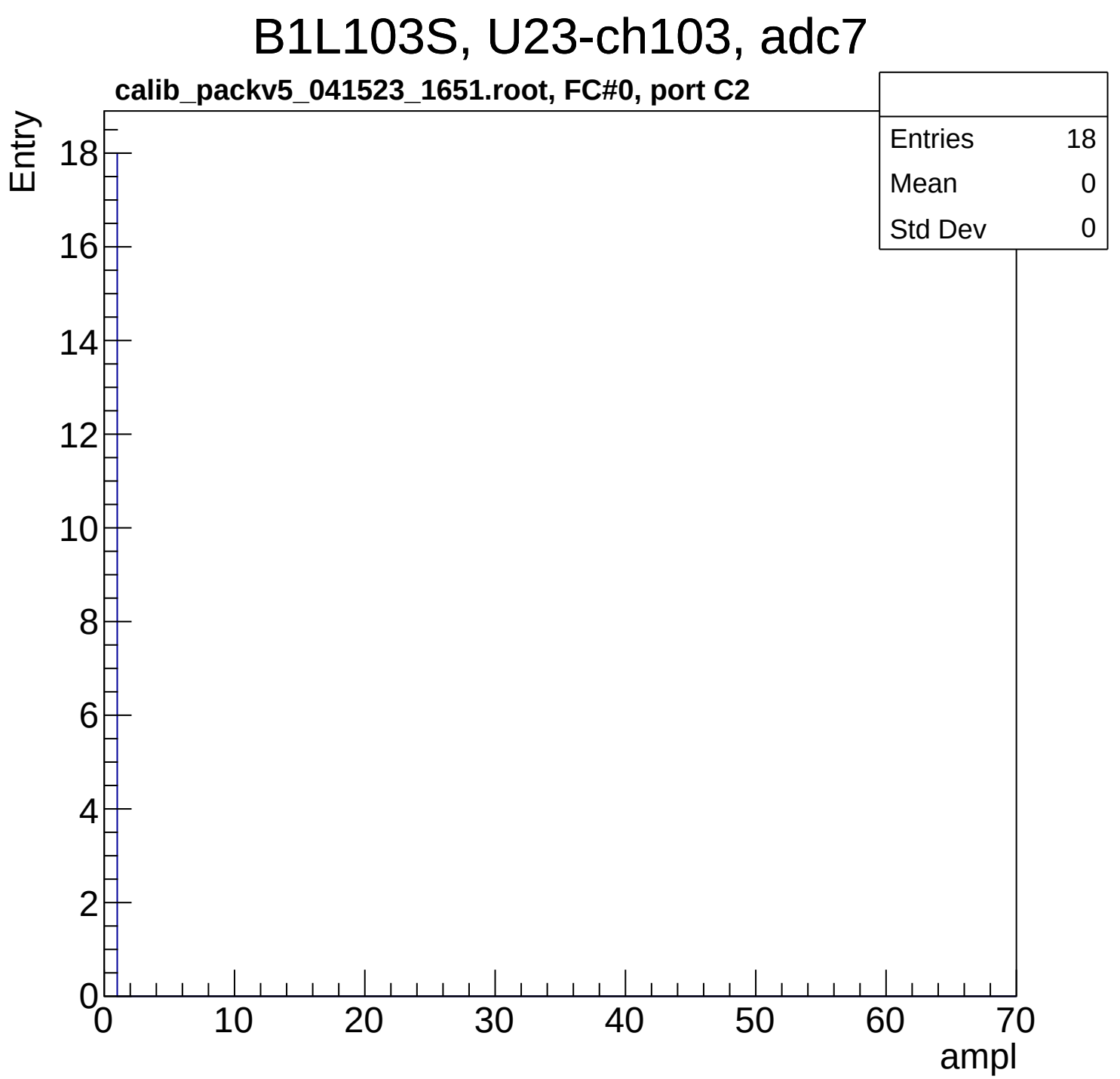

B1L103S, U23-ch103, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 36.63
Std Dev 16.38
Gaus mean : 44.2268
Gaus Width: 3.1486
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

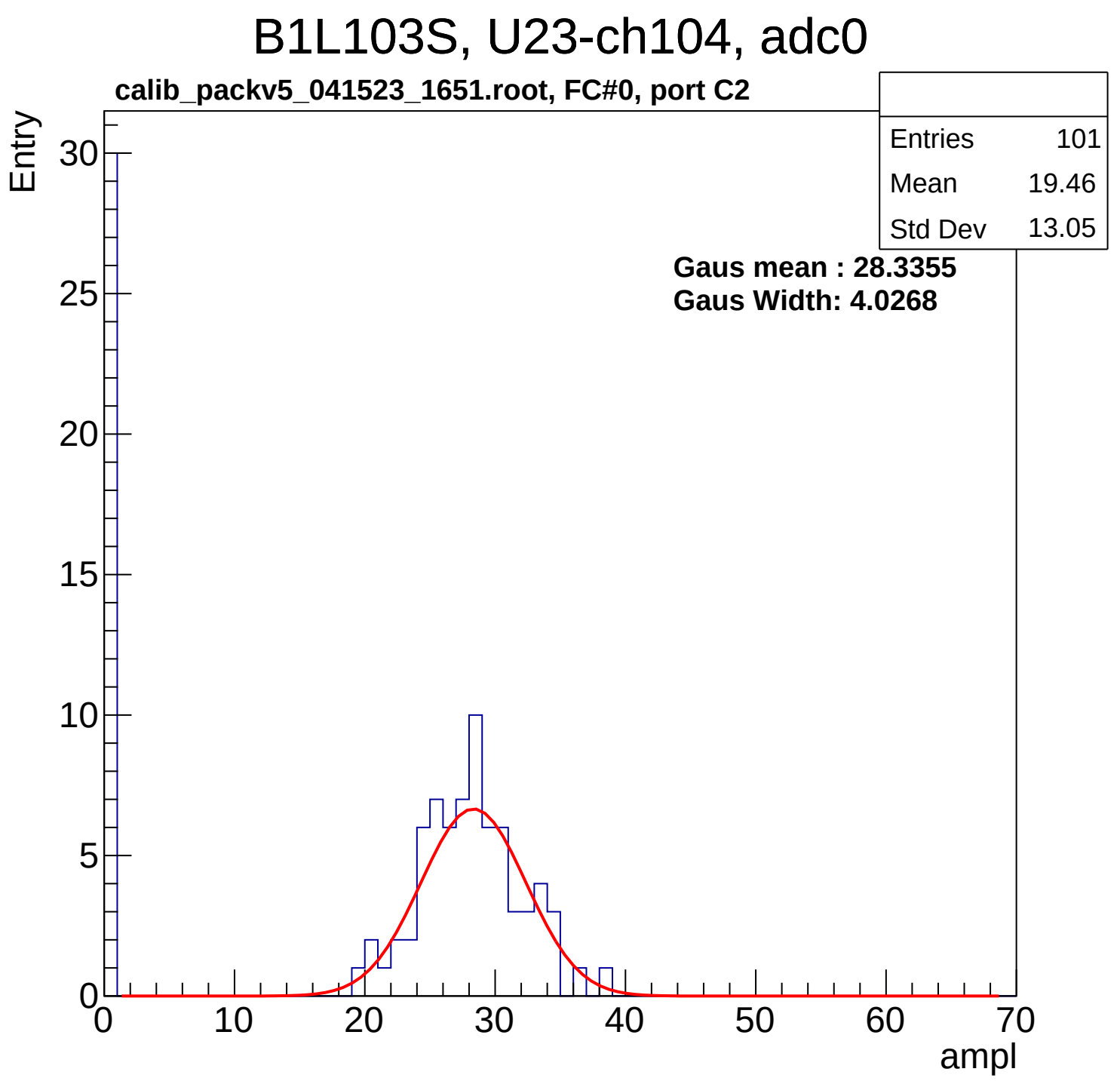

B1L103S, U23-ch103, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 50.14
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

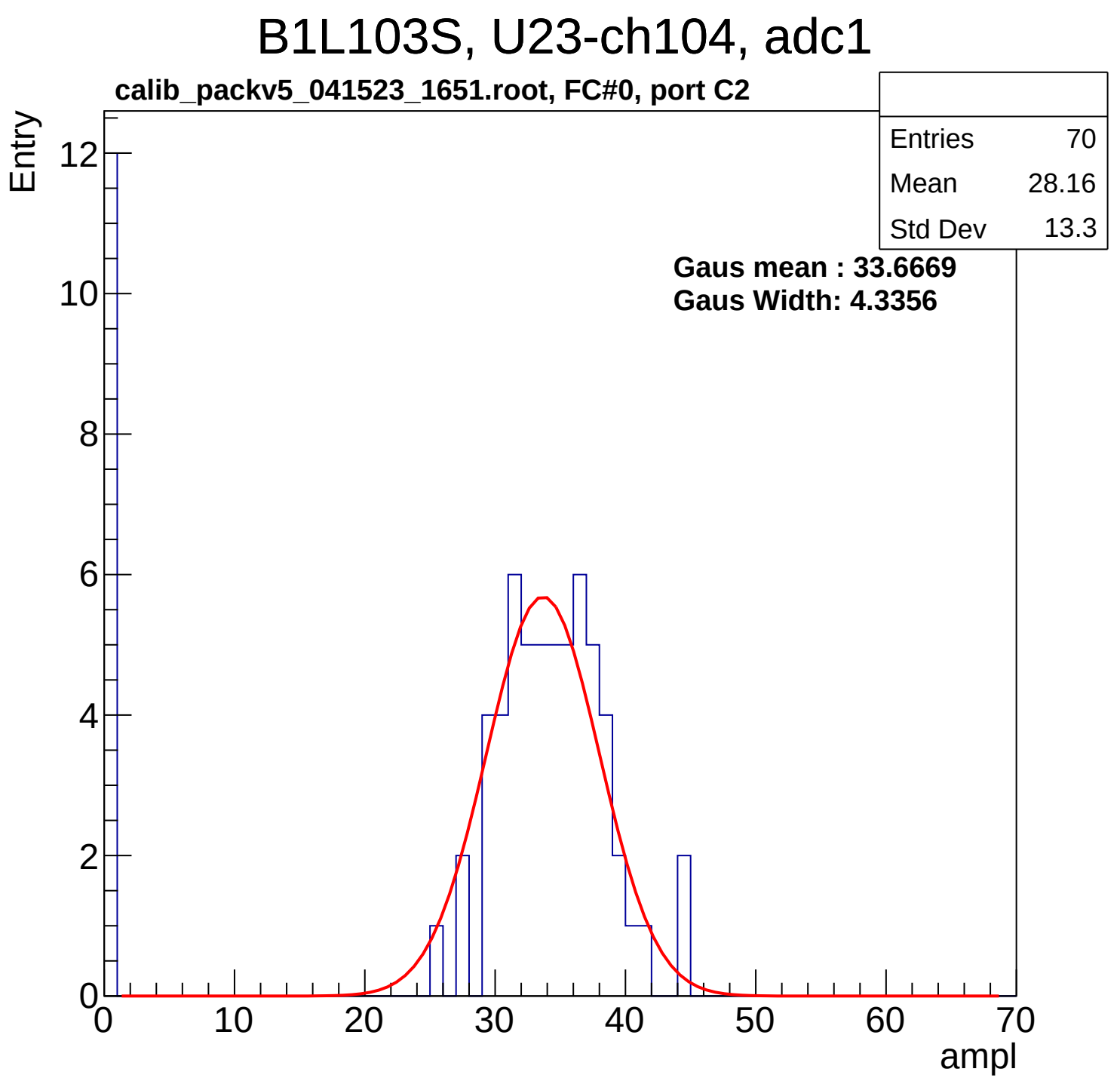

B1L103S, U23-ch103, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 56.18
Std Dev 3.001
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

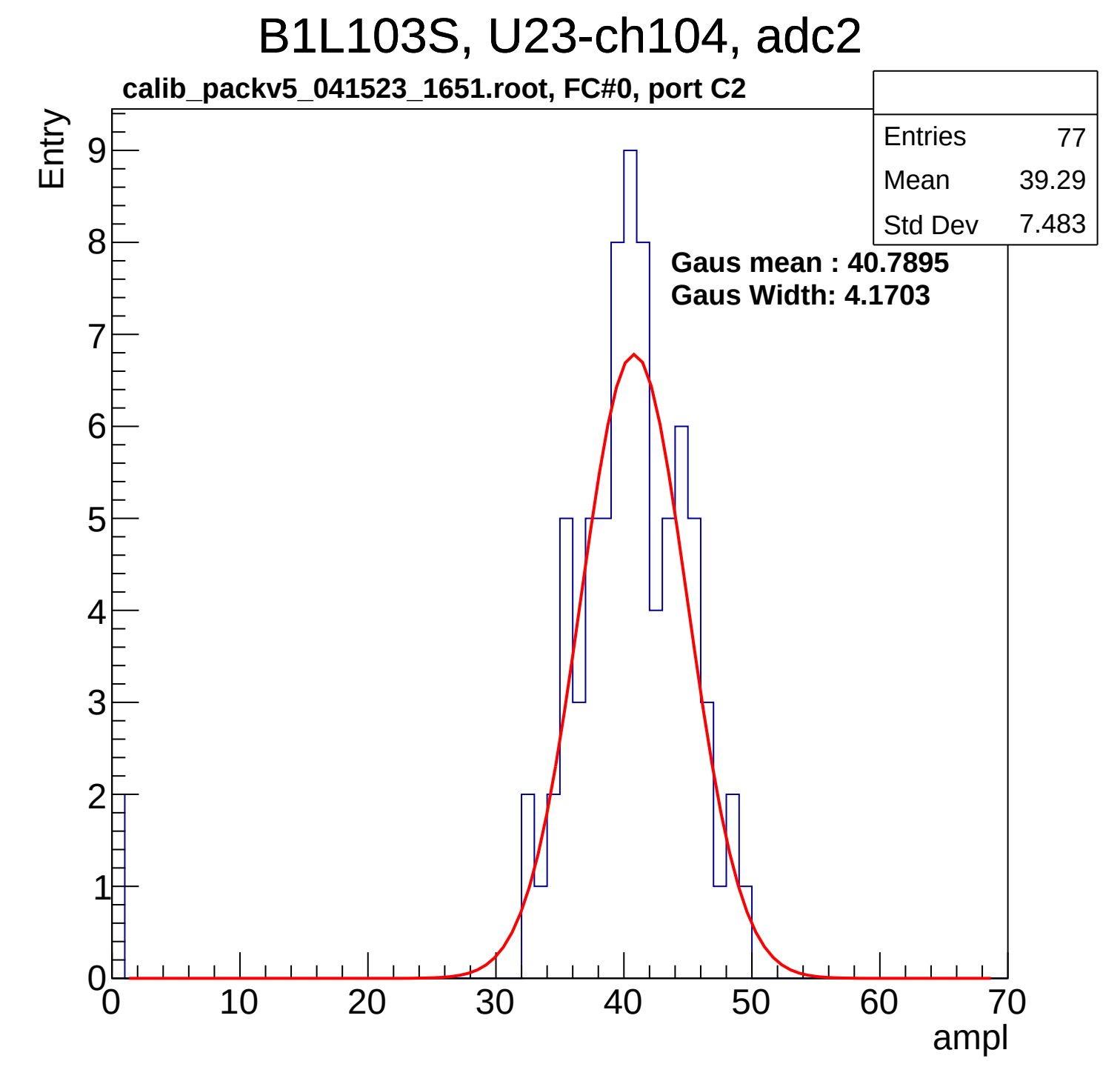

B1L103S, U23-ch103, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 42
Mean 59.1
Std Dev 9.464
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

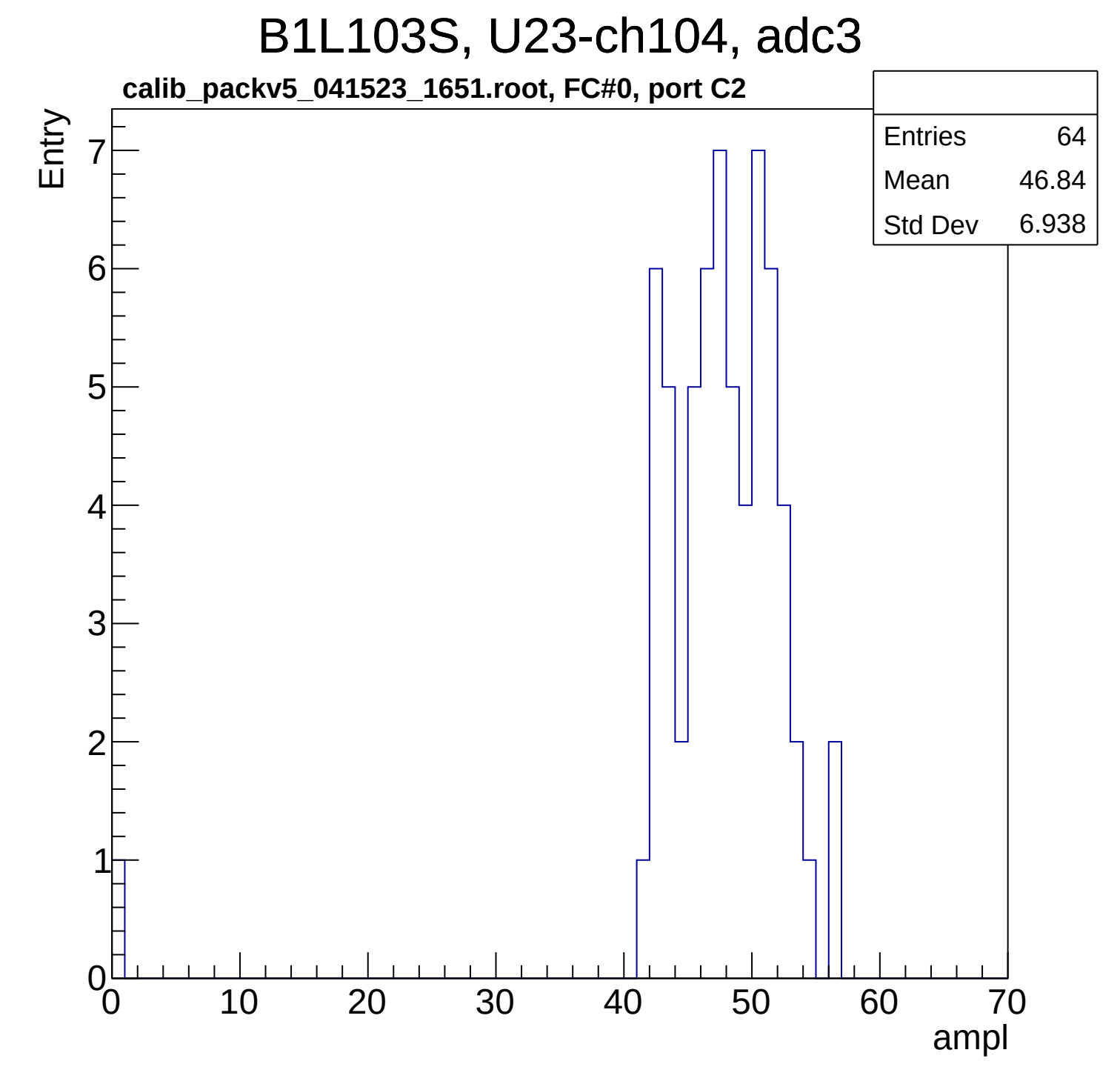

B1L103S, U23-ch103, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

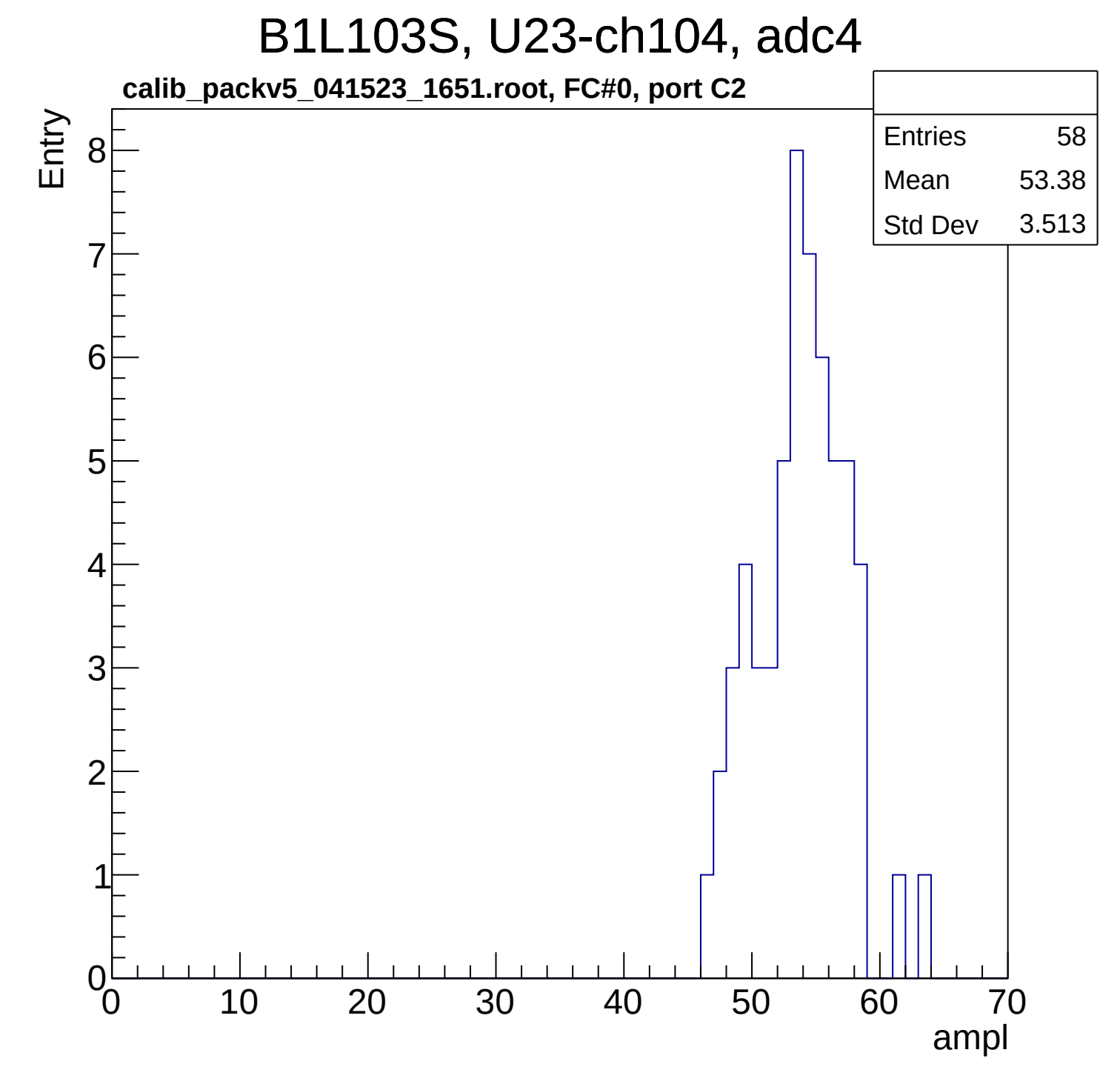

B1L103S, U23-ch103, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

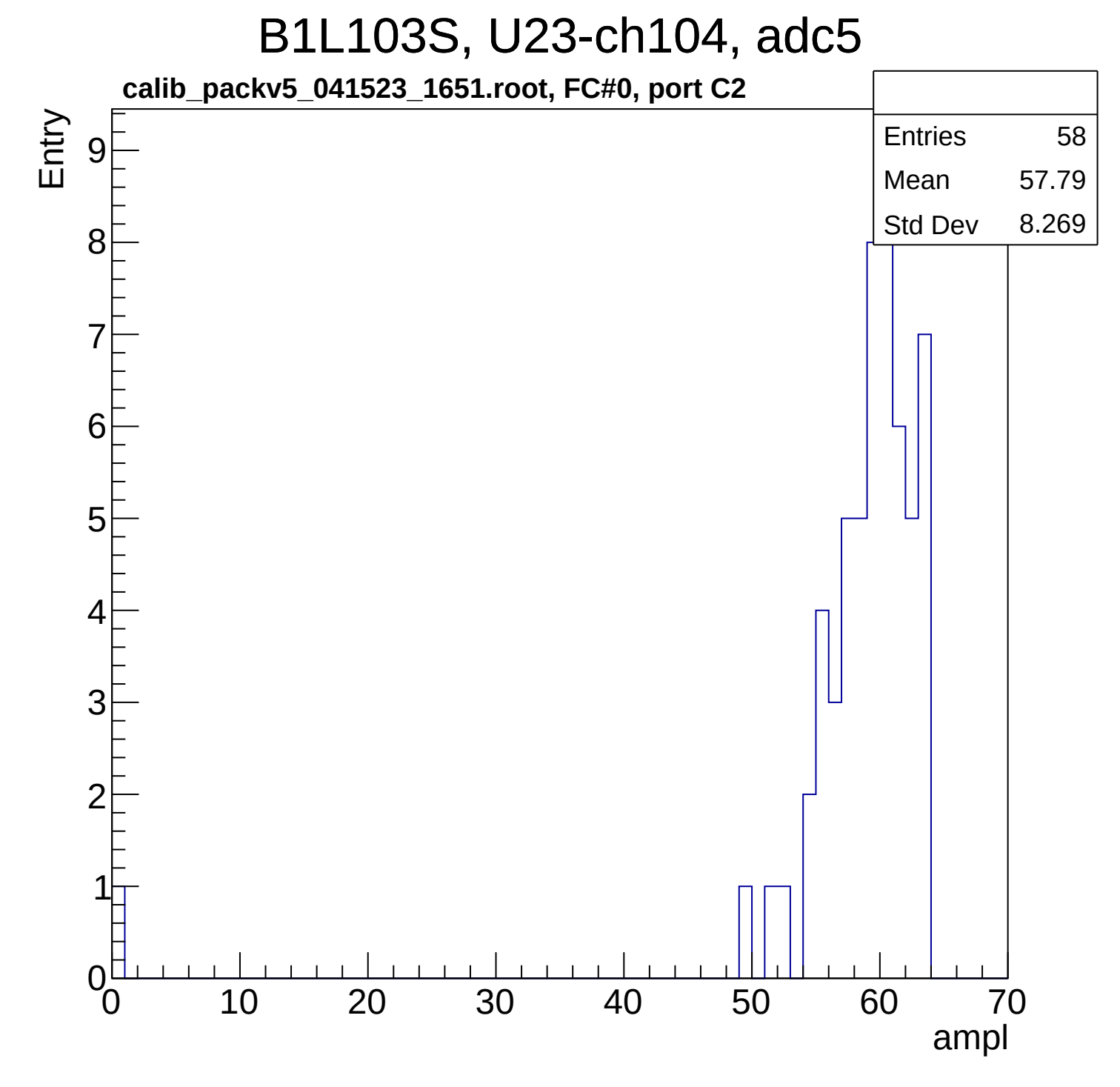

B1L103S, U23-ch104, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 101
Mean 19.46
Std Dev 13.05
Gaus mean : 28.3355
Gaus Width: 4.0268
Entry
ampl
0
5
10
15
20
25
30
0
10
20
30
40
50
60
70

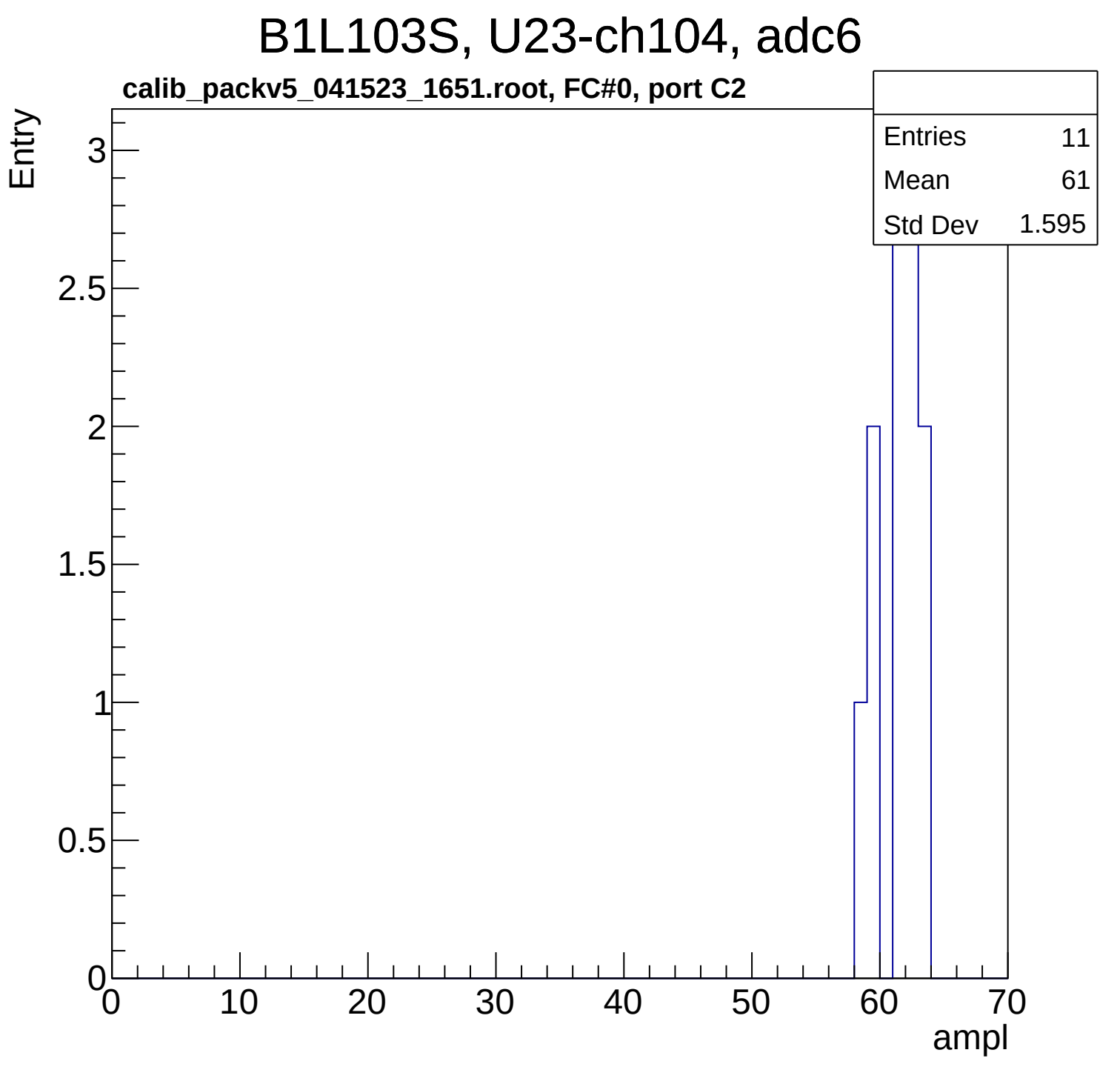

B1L103S, U23-ch104, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 28.16
Std Dev 13.3
Gaus mean : 33.6669
Gaus Width: 4.3356
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

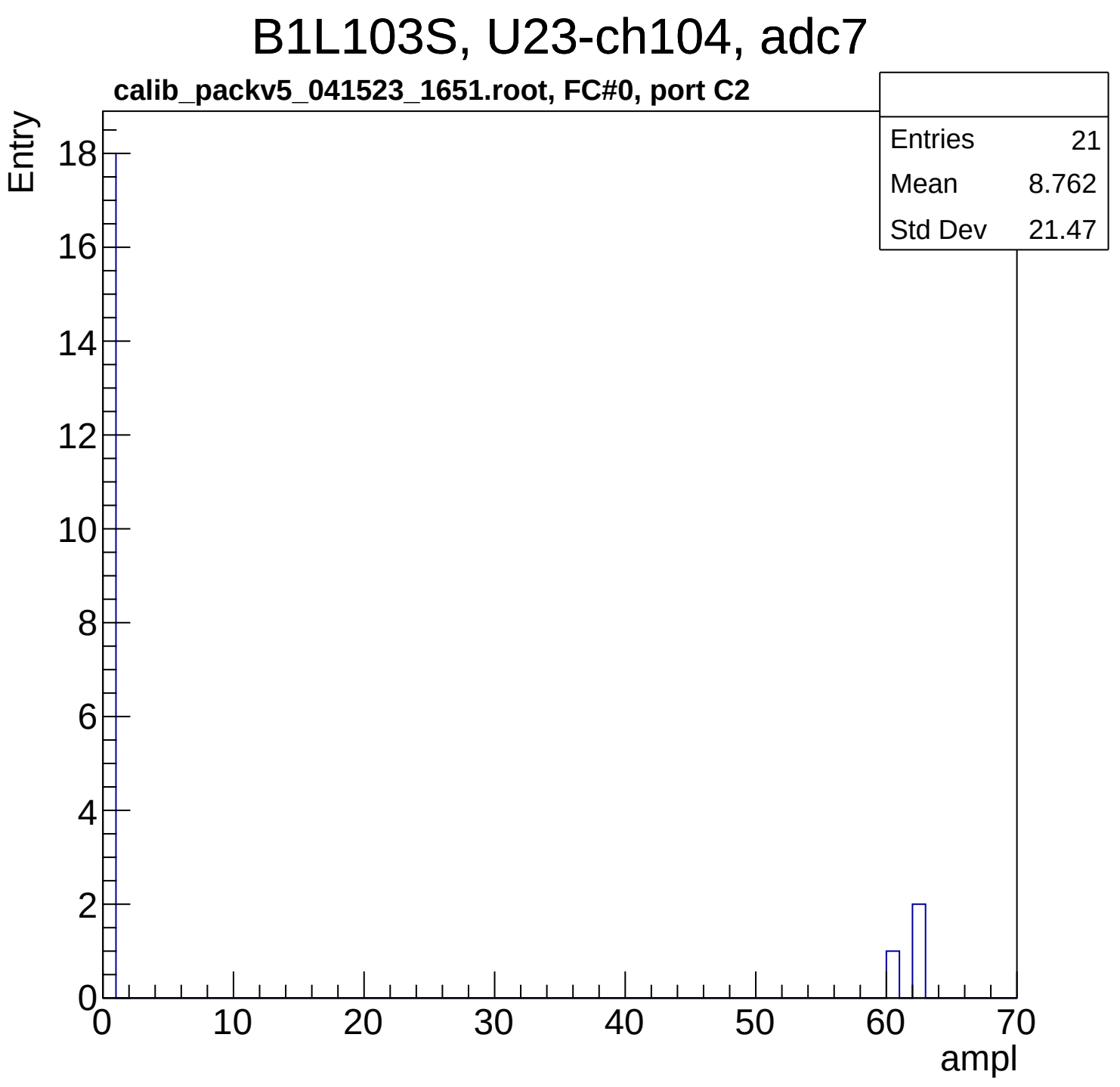

B1L103S, U23-ch104, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 39.29
Std Dev 7.483
Gaus mean : 40.7895
Gaus Width: 4.1703
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

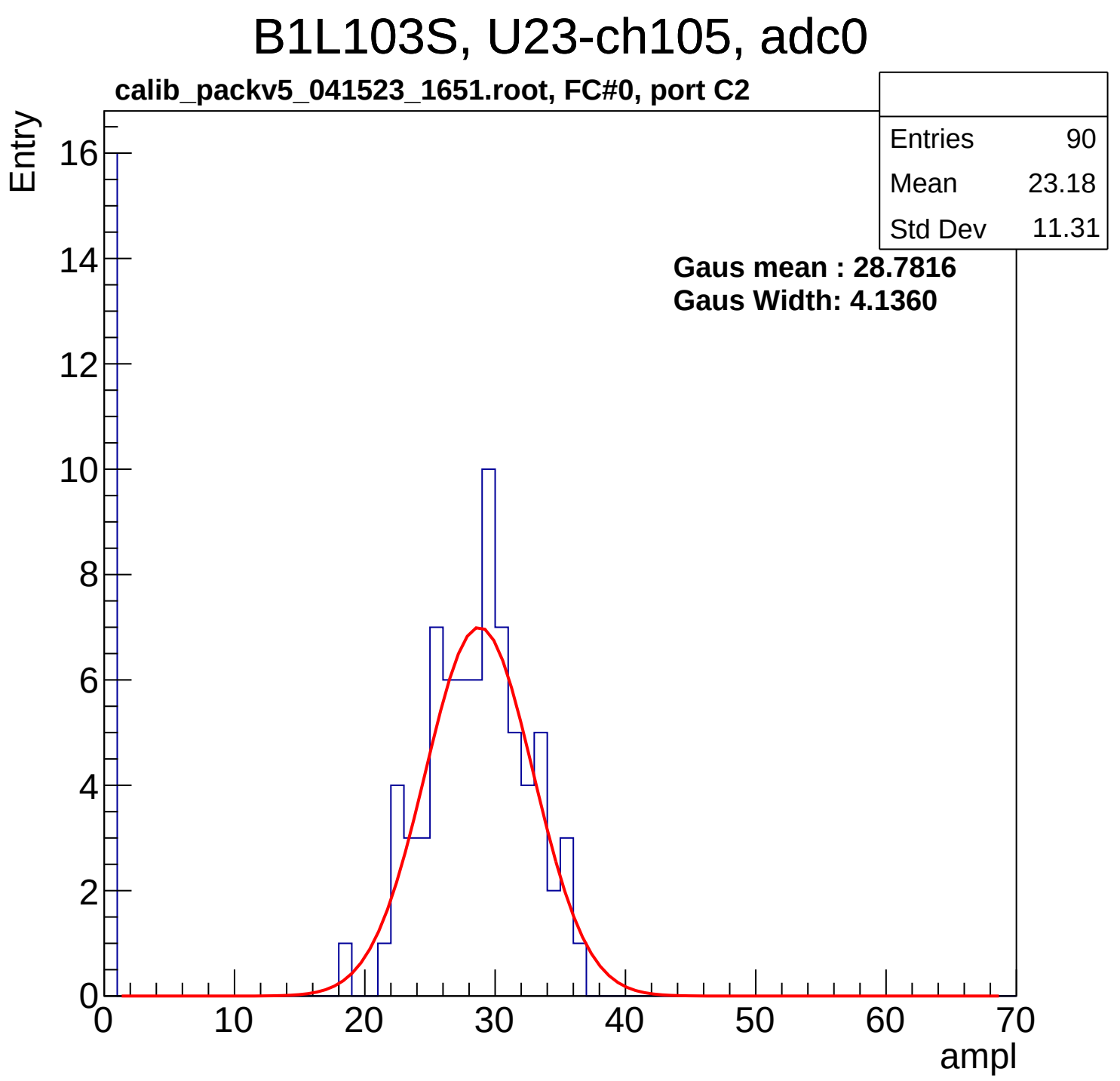

B1L103S, U23-ch104, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 46.84
Std Dev 6.938
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

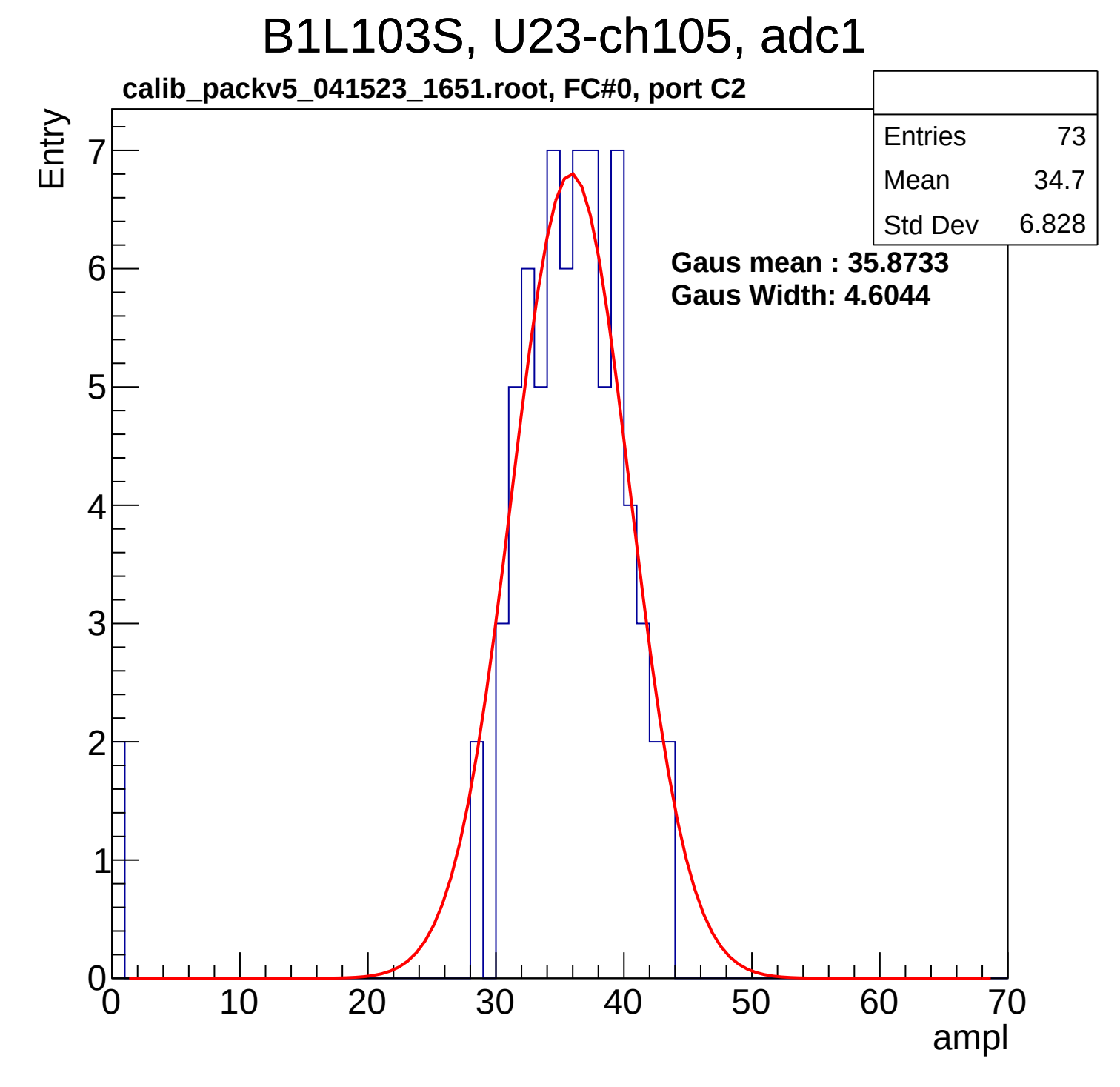

B1L103S, U23-ch104, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 53.38
Std Dev 3.513
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

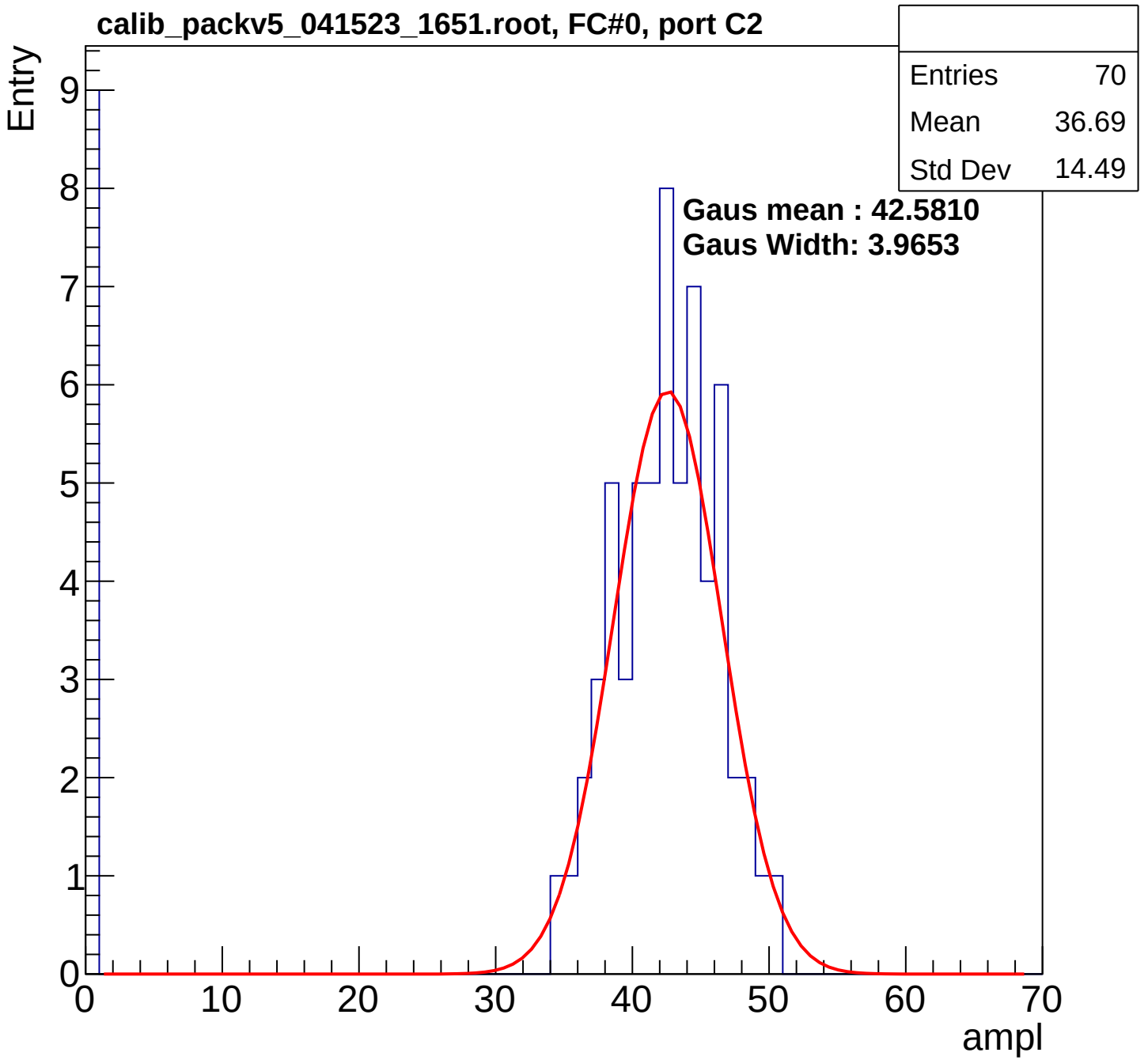

B1L103S, U23-ch104, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 57.79
Std Dev 8.269
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

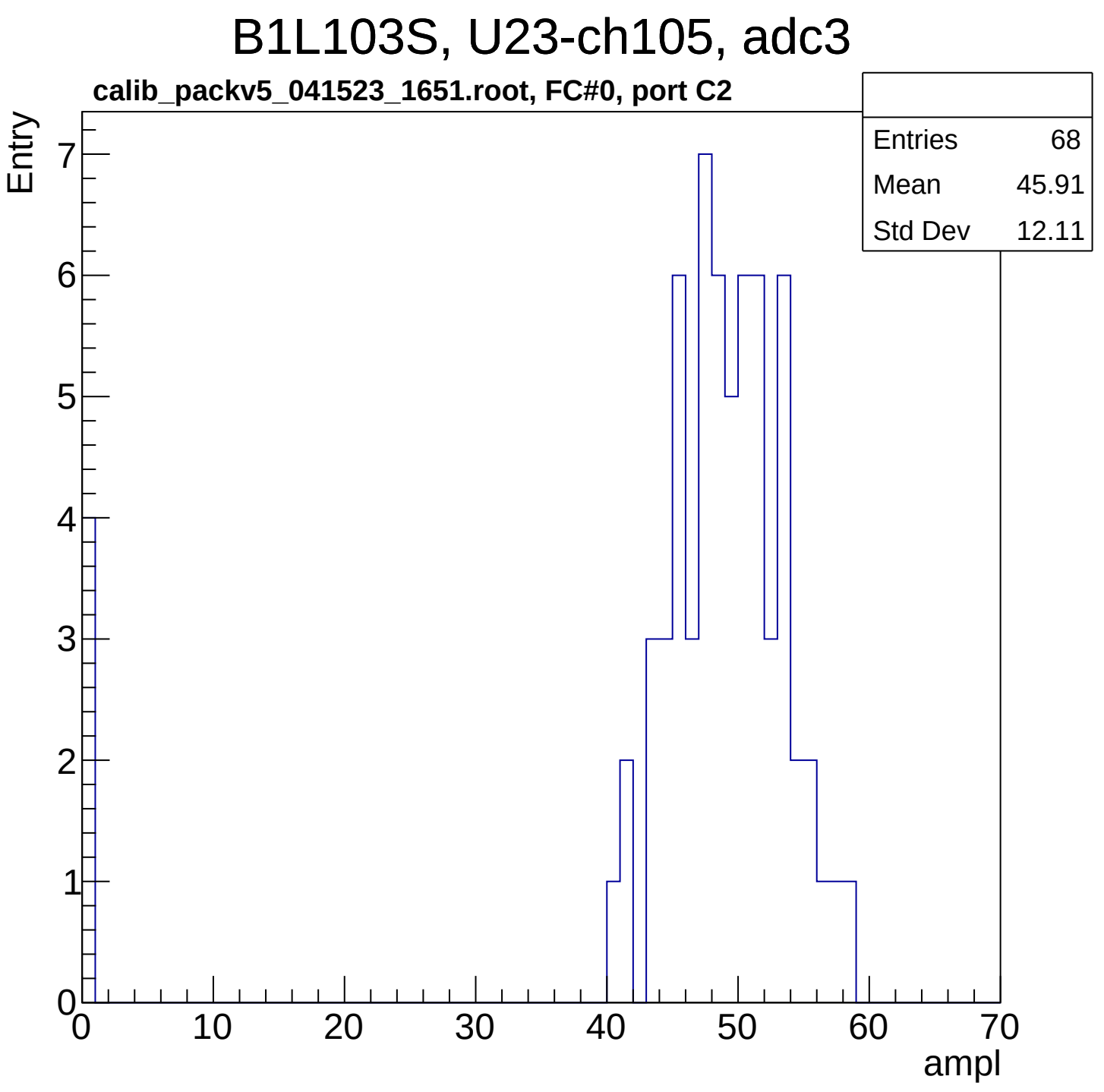

B1L103S, U23-ch104, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 61
Std Dev 1.595
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

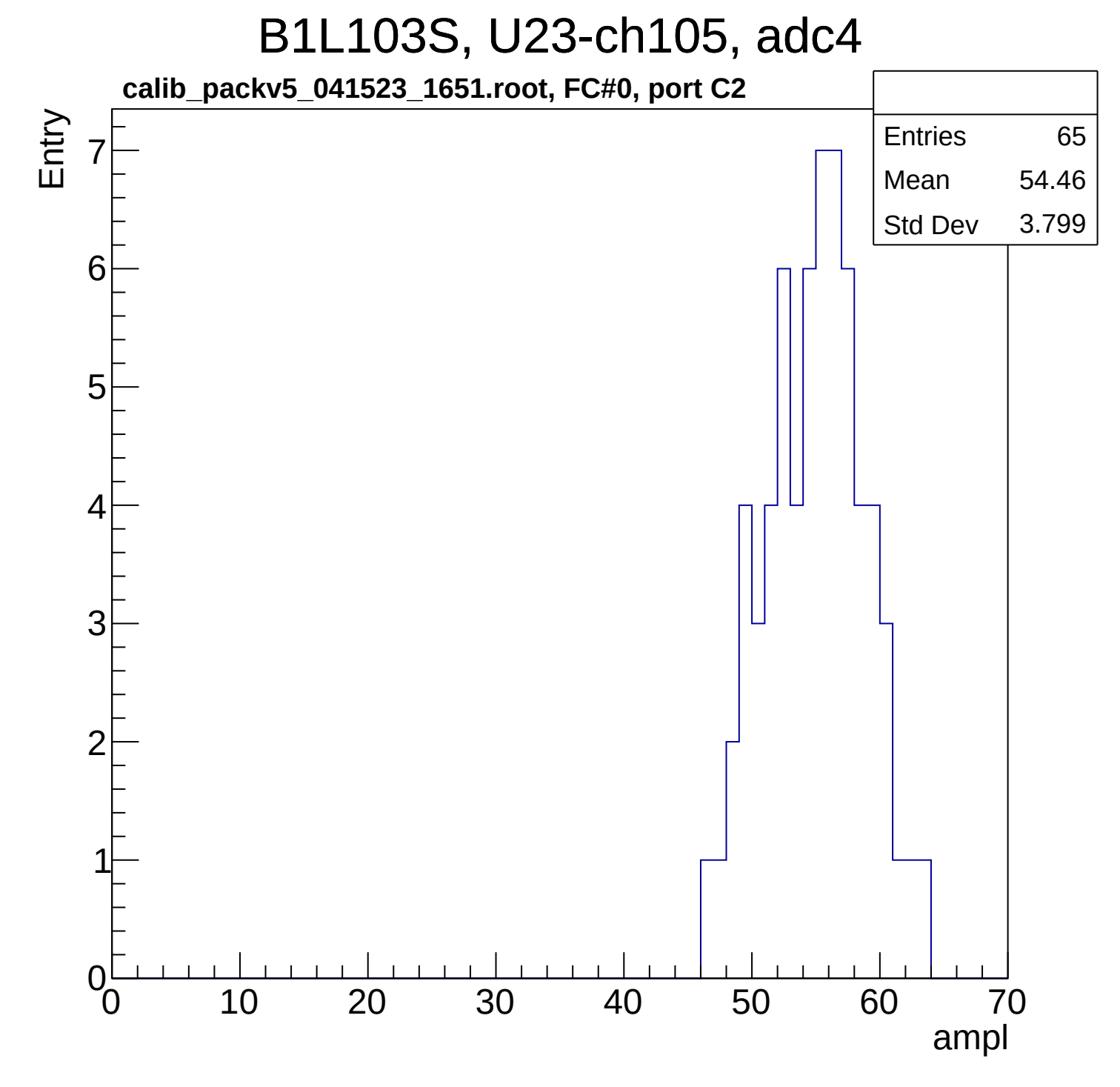

B1L103S, U23-ch104, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 8.762
Std Dev 21.47
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

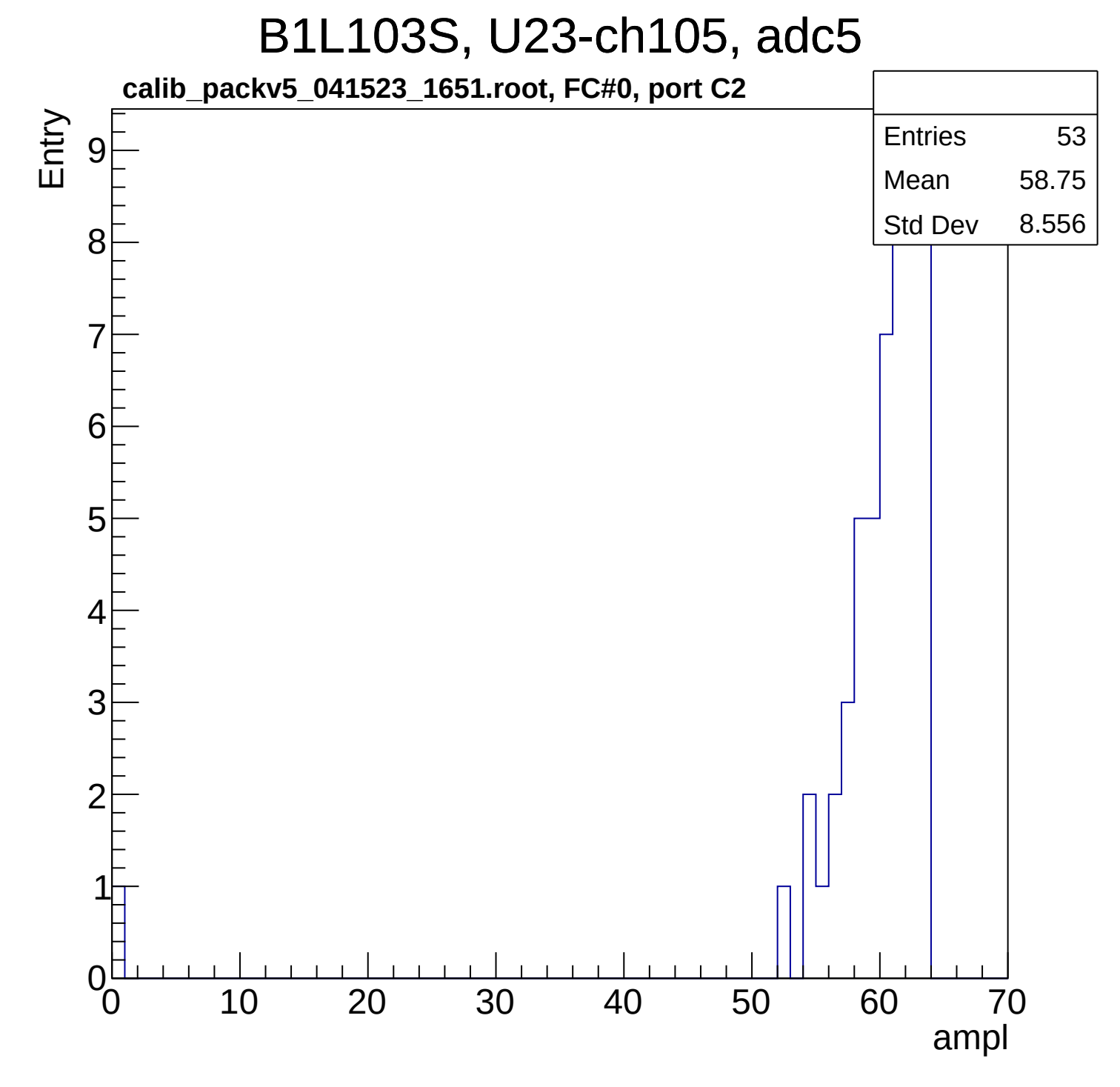

B1L103S, U23-ch105, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 90
Mean 23.18
Std Dev 11.31
Gaus mean : 28.7816
Gaus Width: 4.1360
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

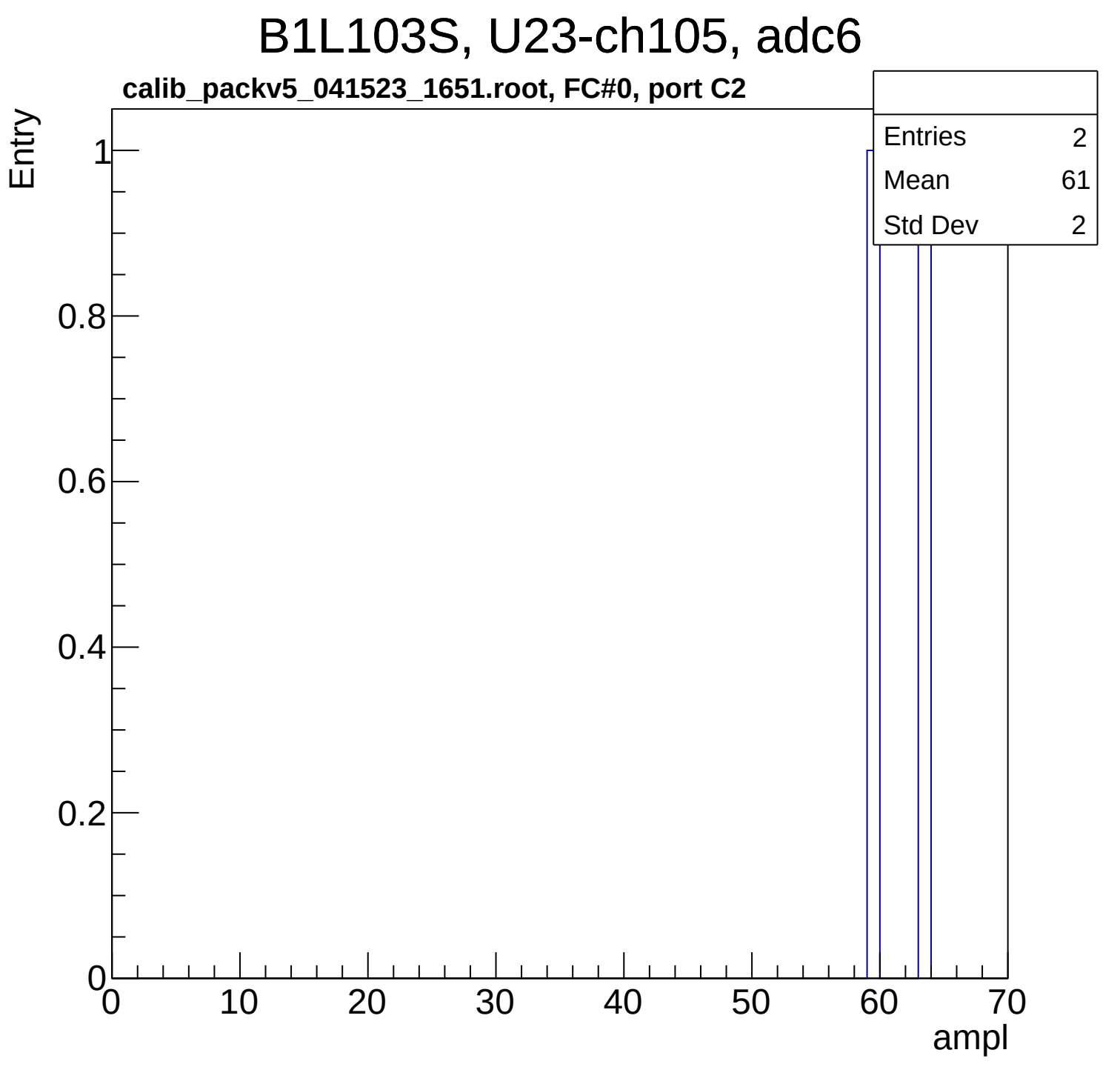

B1L103S, U23-ch105, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 34.7
Std Dev 6.828
Gaus mean : 35.8733
Gaus Width: 4.6044
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

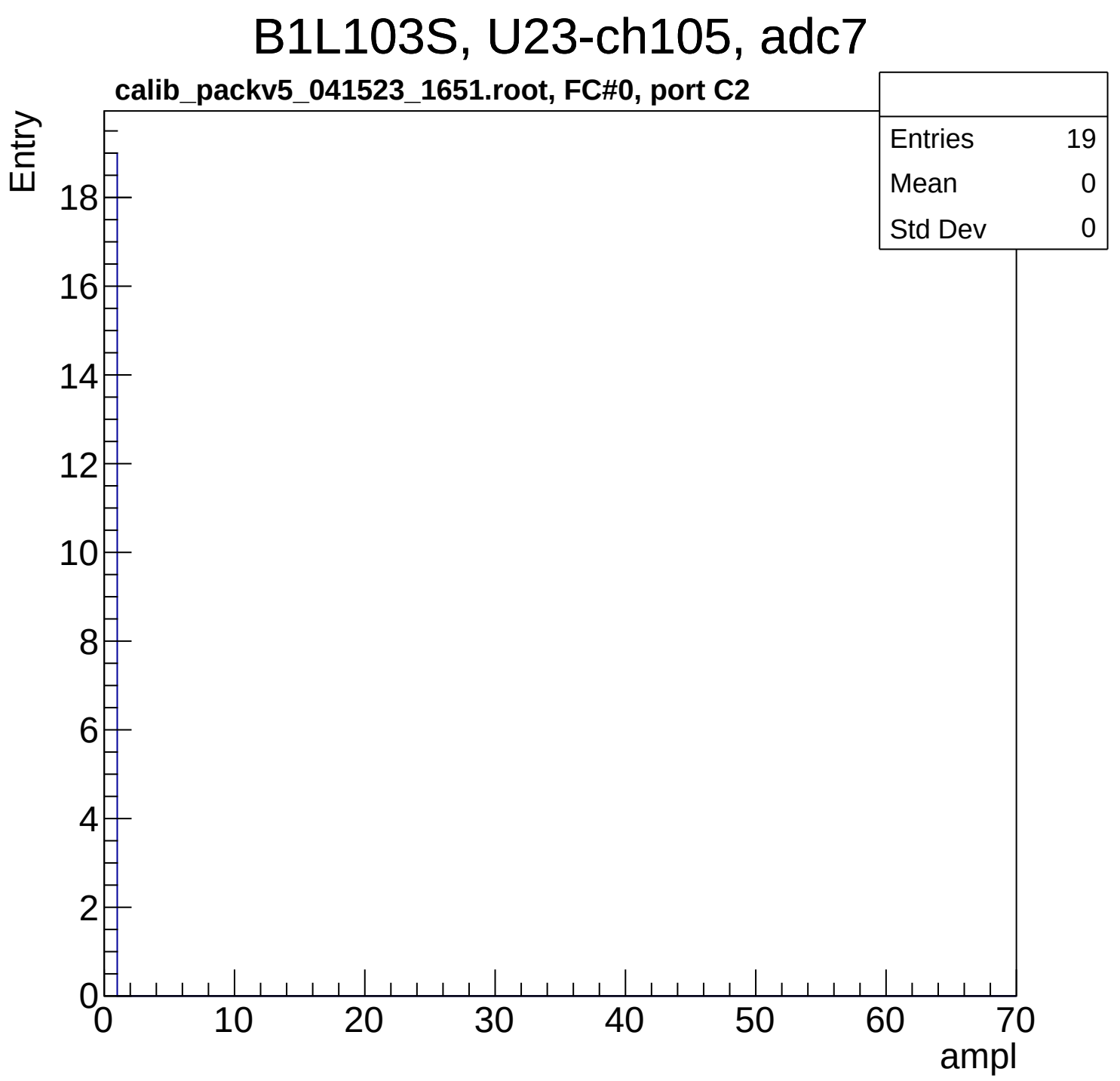

B1L103S, U23-ch105, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 36.69
Std Dev 14.49
Gaus mean : 42.5810
Gaus Width: 3.9653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

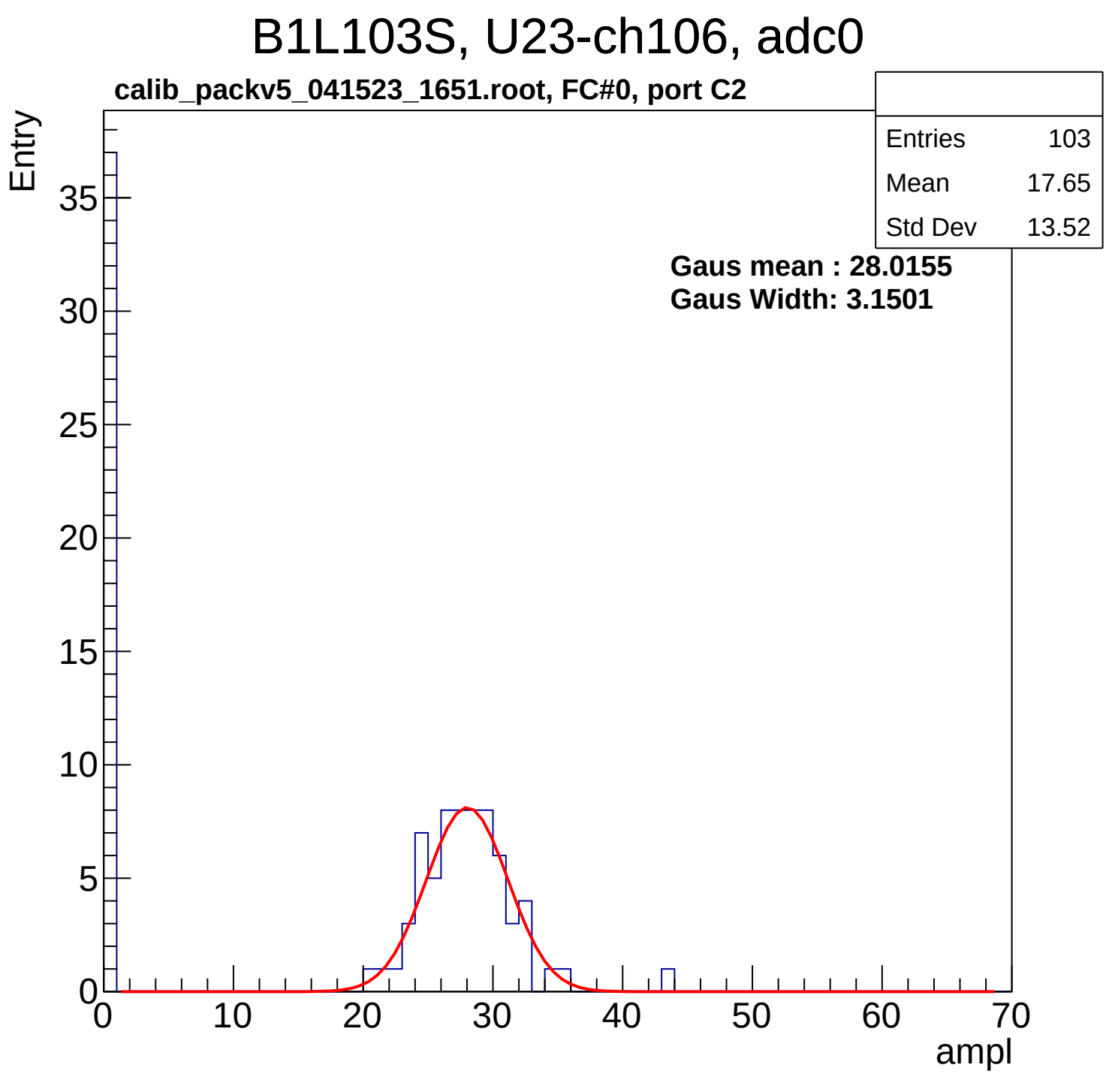

B1L103S, U23-ch105, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 45.91
Std Dev 12.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

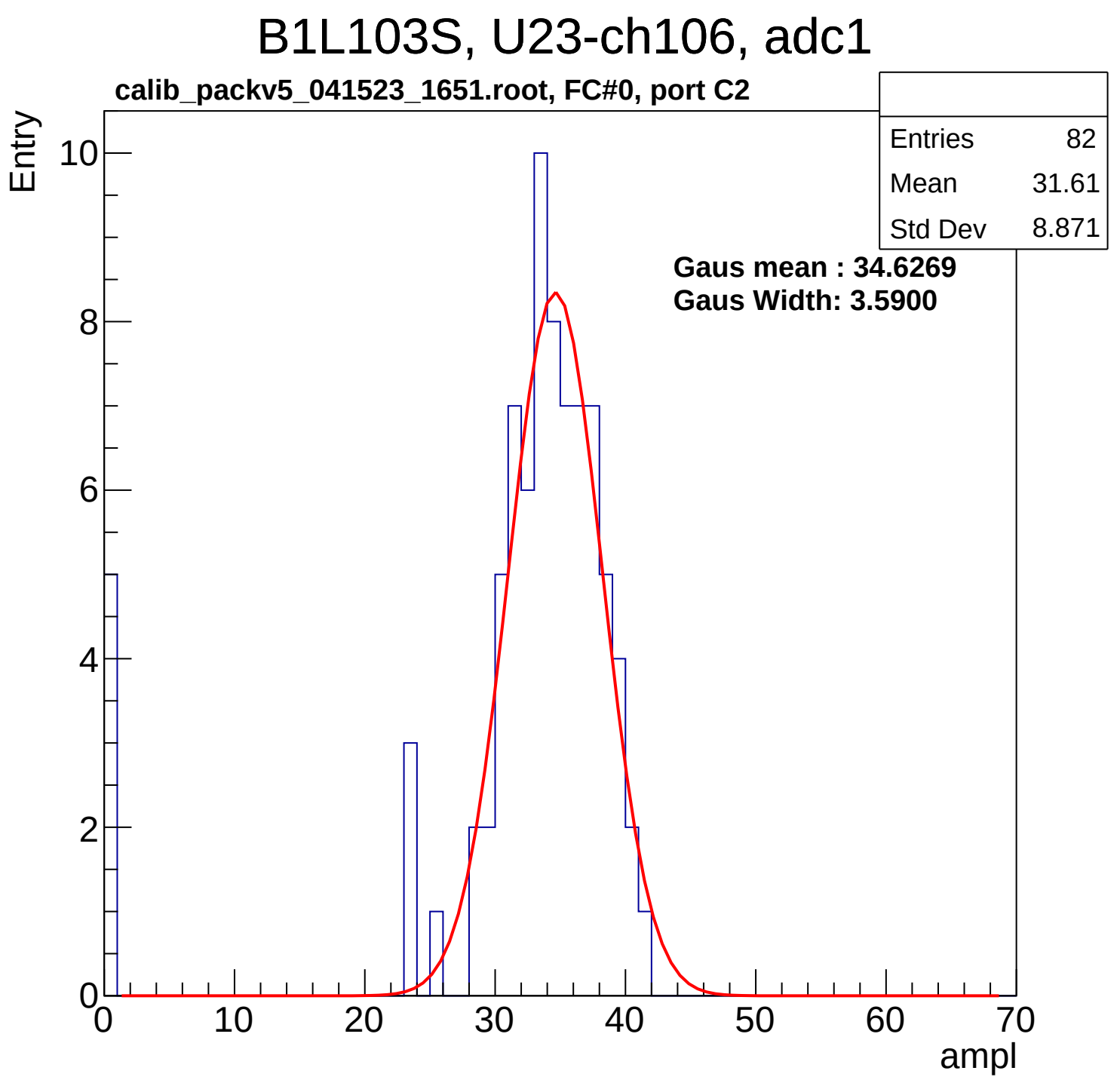

B1L103S, U23-ch105, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 54.46
Std Dev 3.799
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

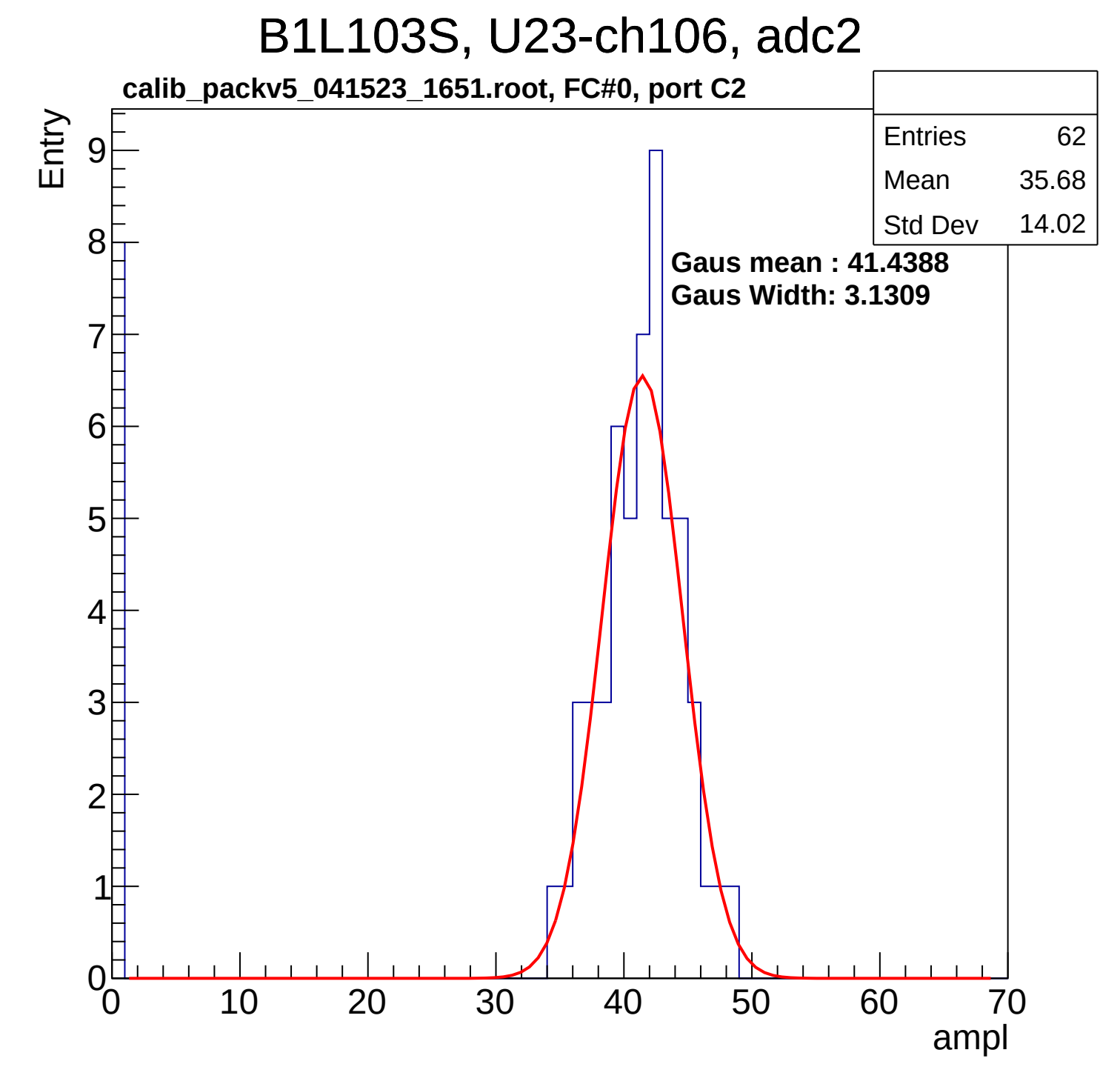

B1L103S, U23-ch105, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 58.75
Std Dev 8.556
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

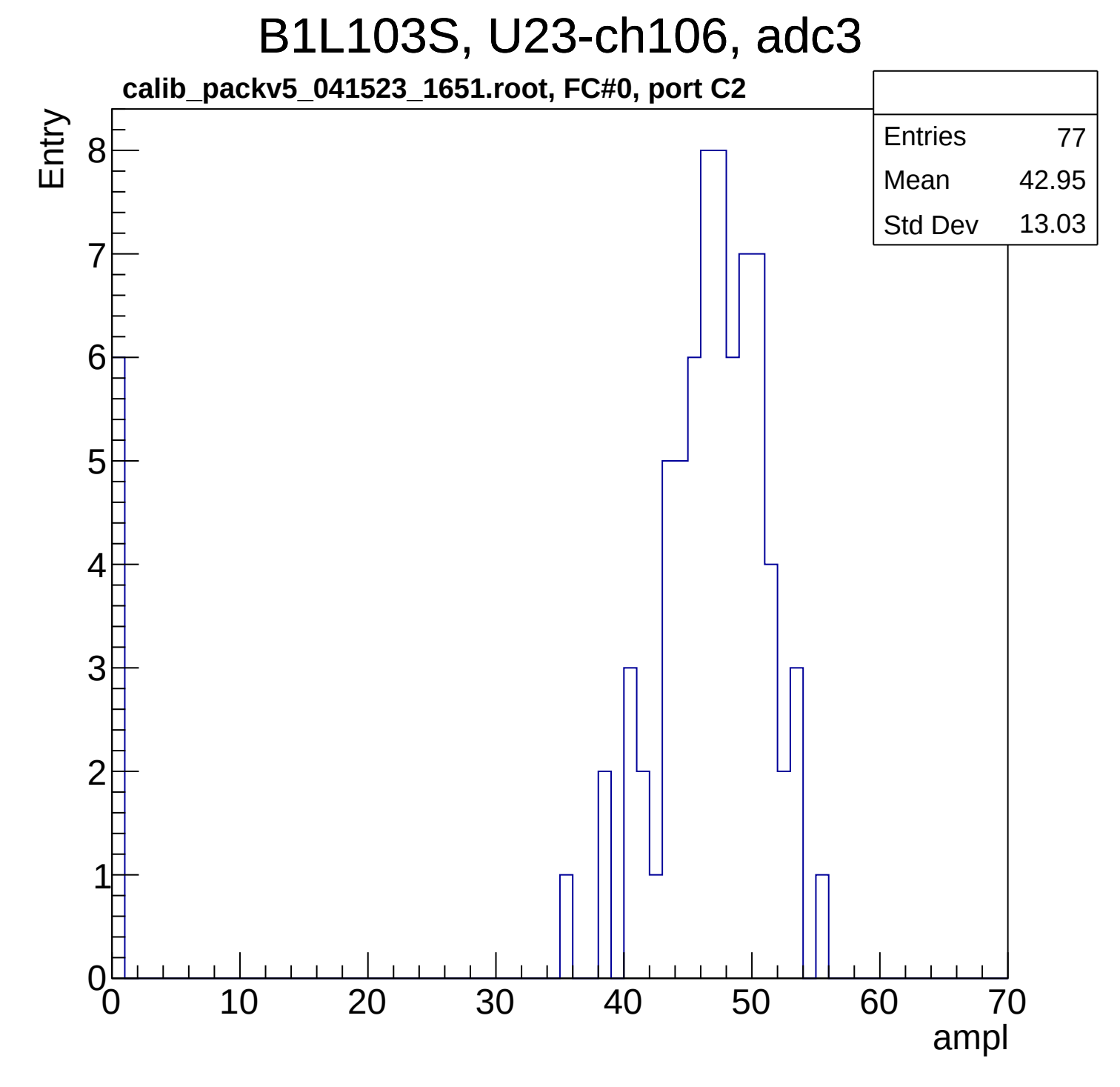

B1L103S, U23-ch105, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

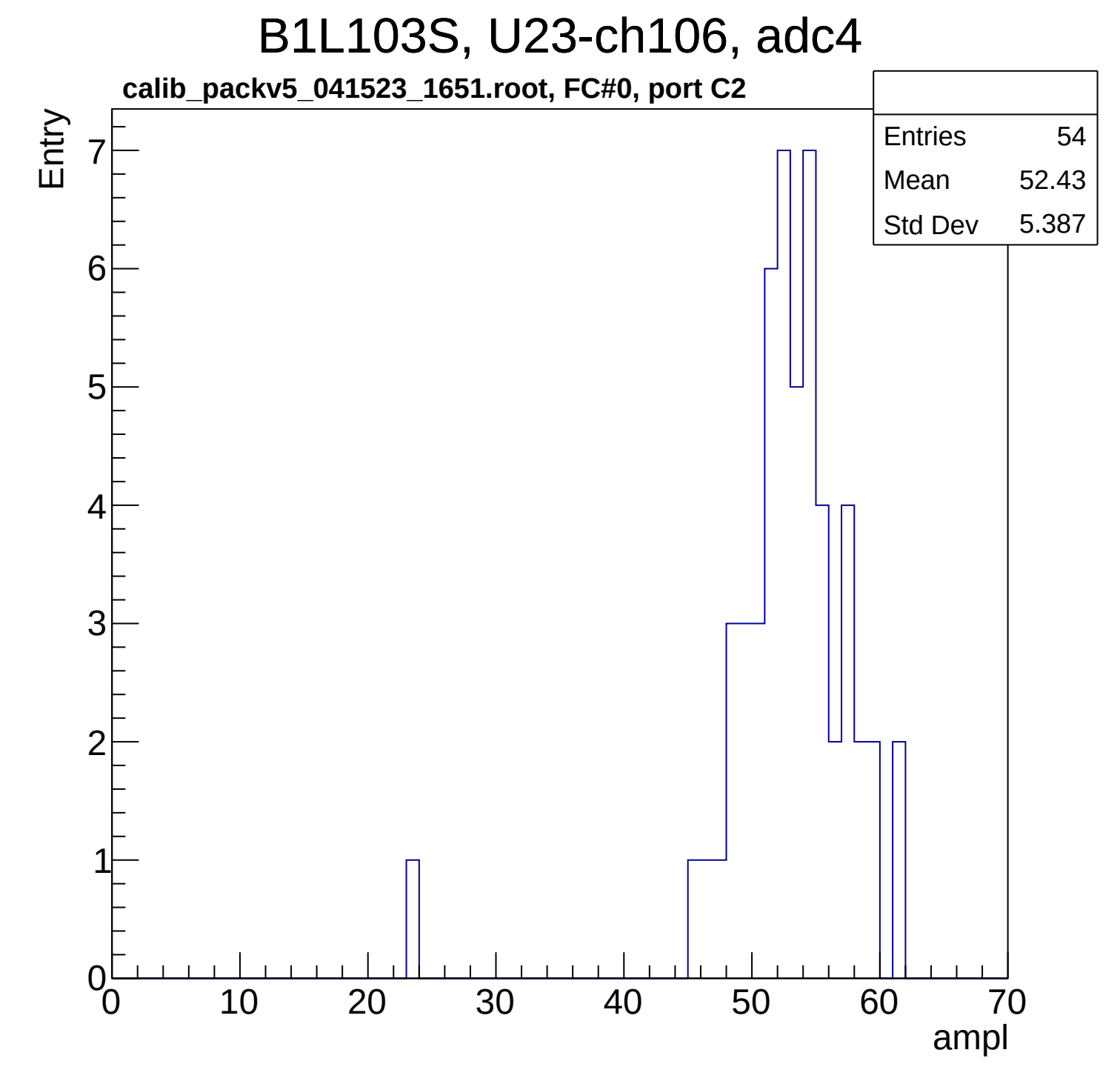

B1L103S, U23-ch105, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

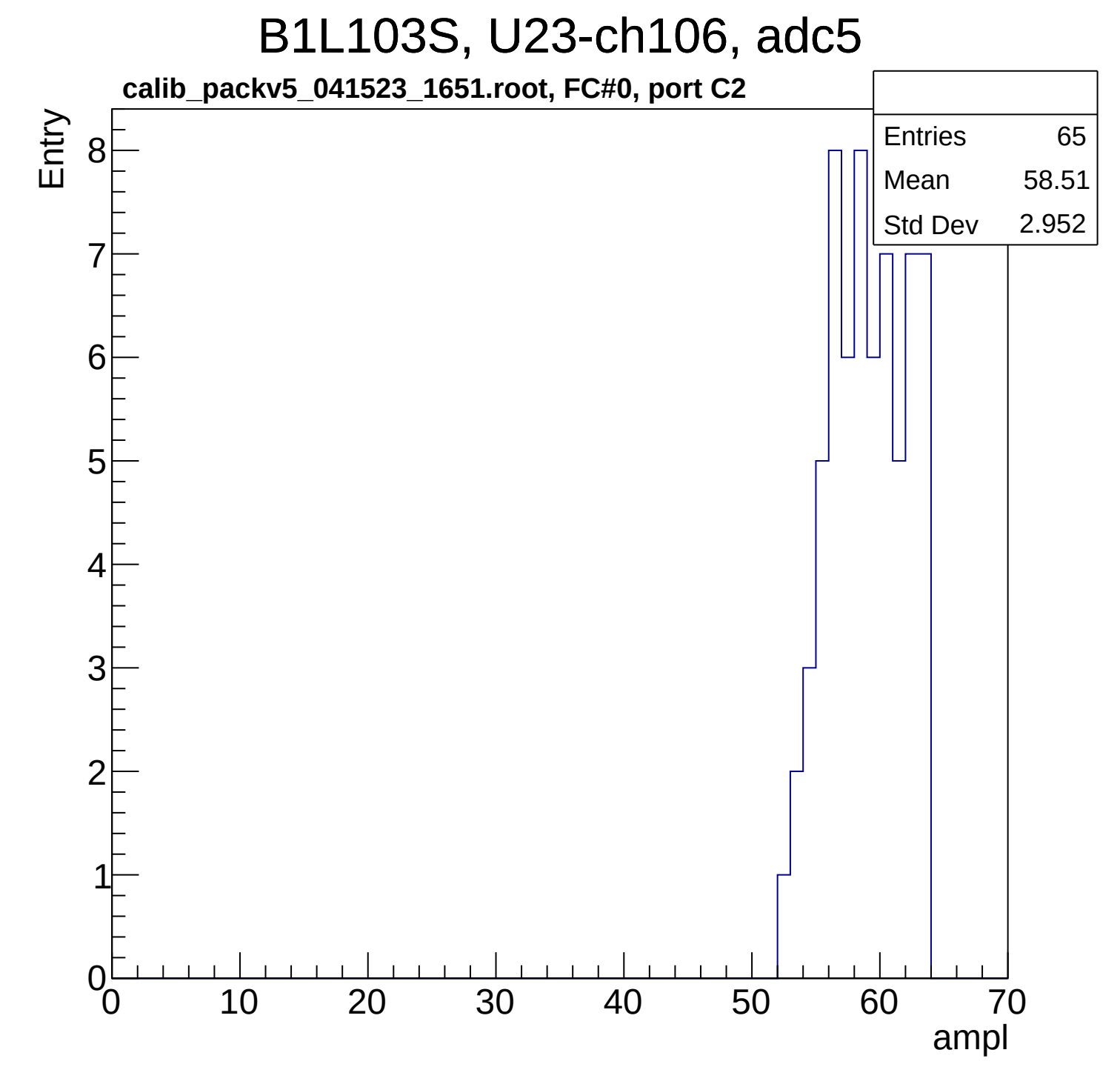

B1L103S, U23-ch106, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 103
Mean 17.65
Std Dev 13.52
Gaus mean : 28.0155
Gaus Width: 3.1501
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

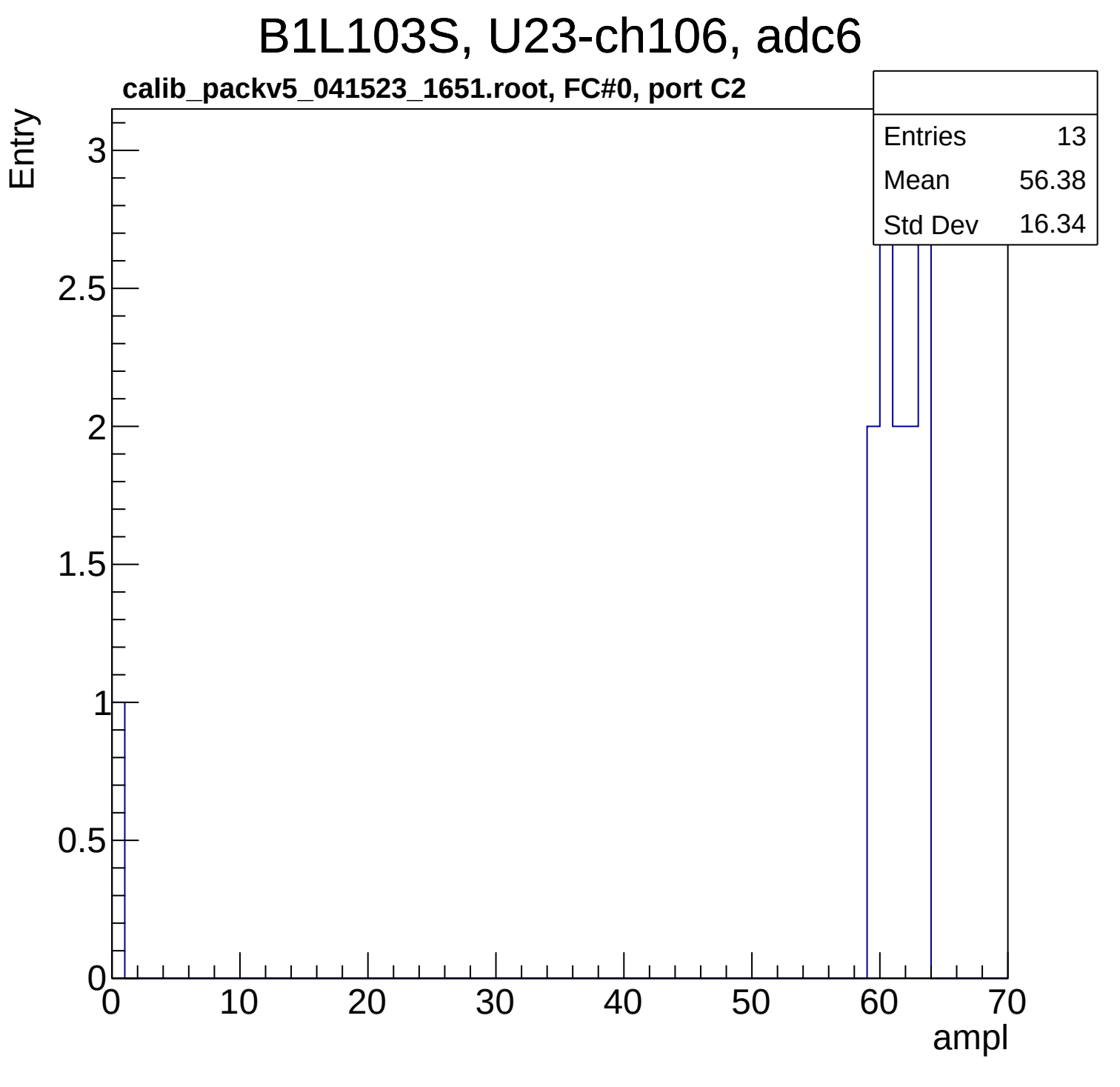

B1L103S, U23-ch106, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 31.61
Std Dev 8.871
Gaus mean : 34.6269
Gaus Width: 3.5900
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

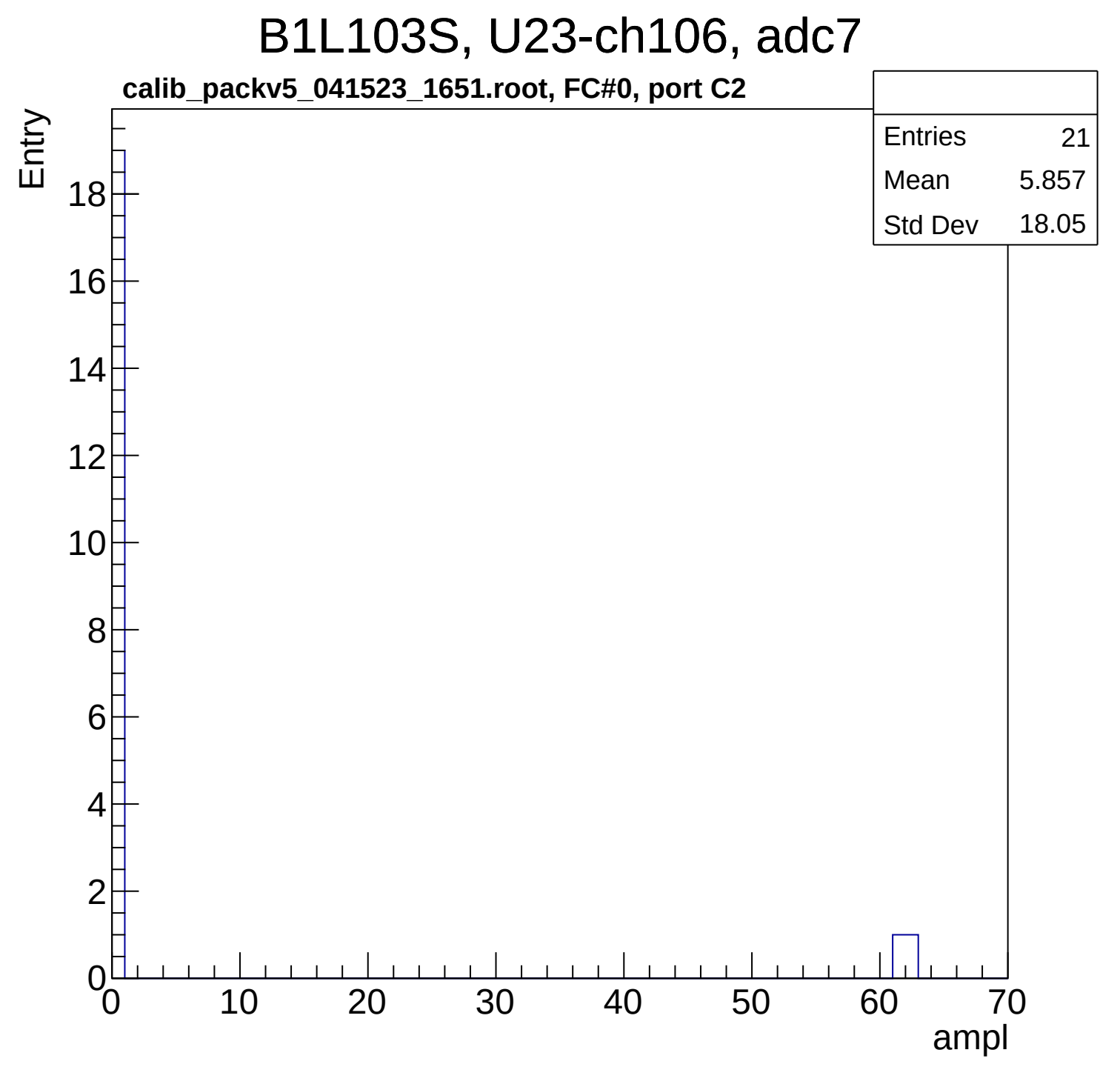

B1L103S, U23-ch106, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 35.68
Std Dev 14.02
Gaus mean : 41.4388
Gaus Width: 3.1309
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

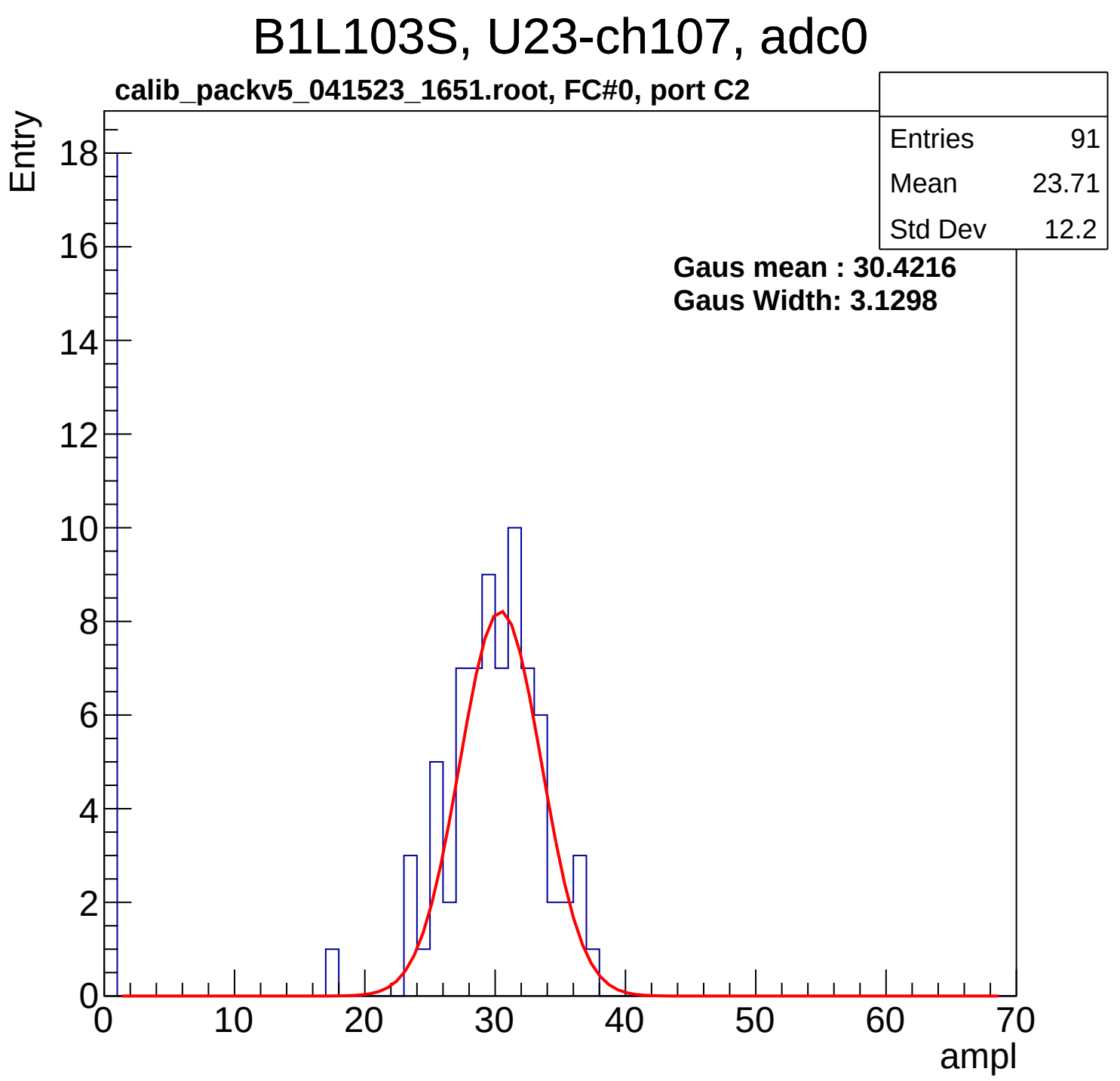

B1L103S, U23-ch106, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 42.95
Std Dev 13.03
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

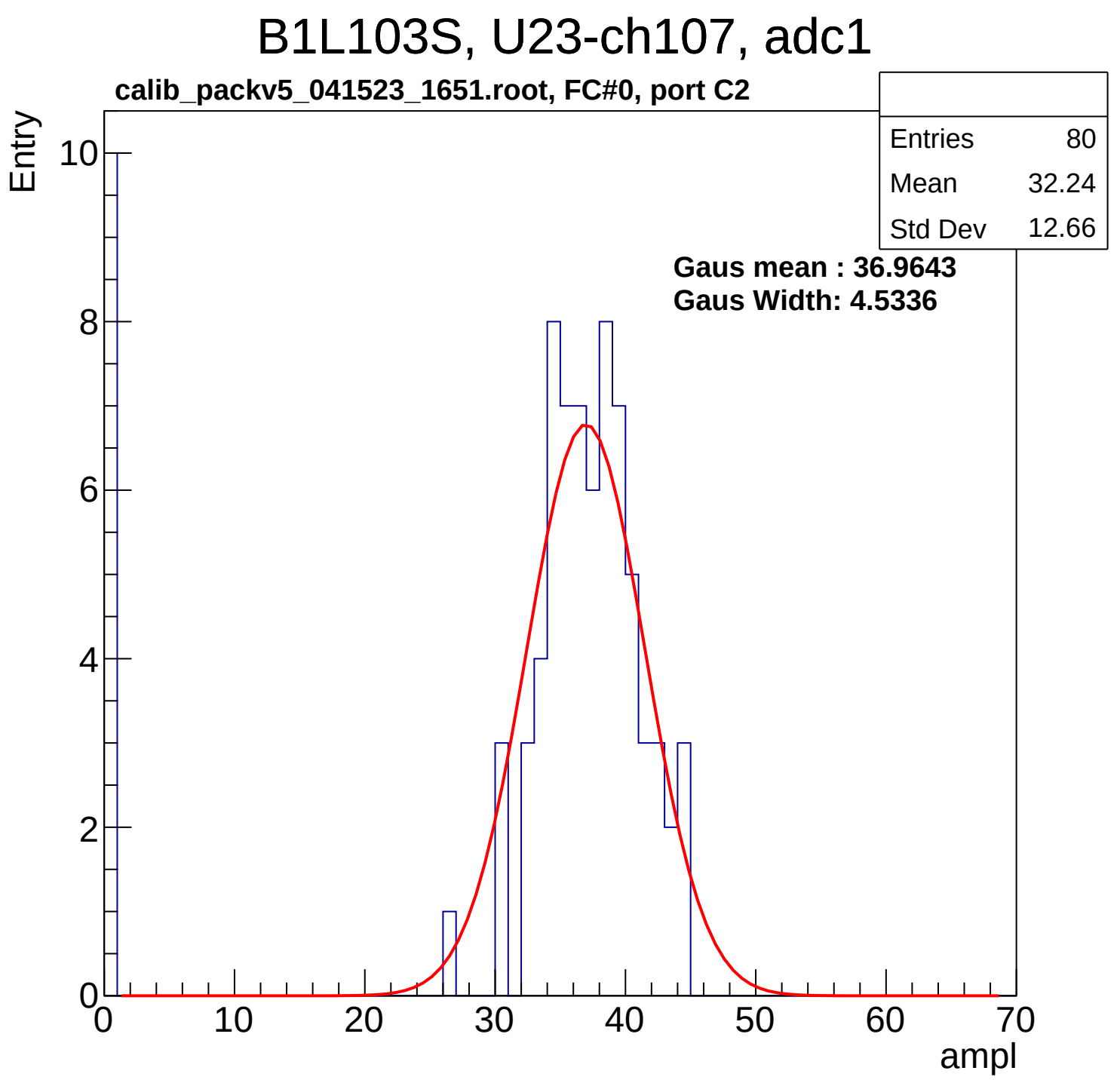

B1L103S, U23-ch106, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 54
Mean 52.43
Std Dev 5.387
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

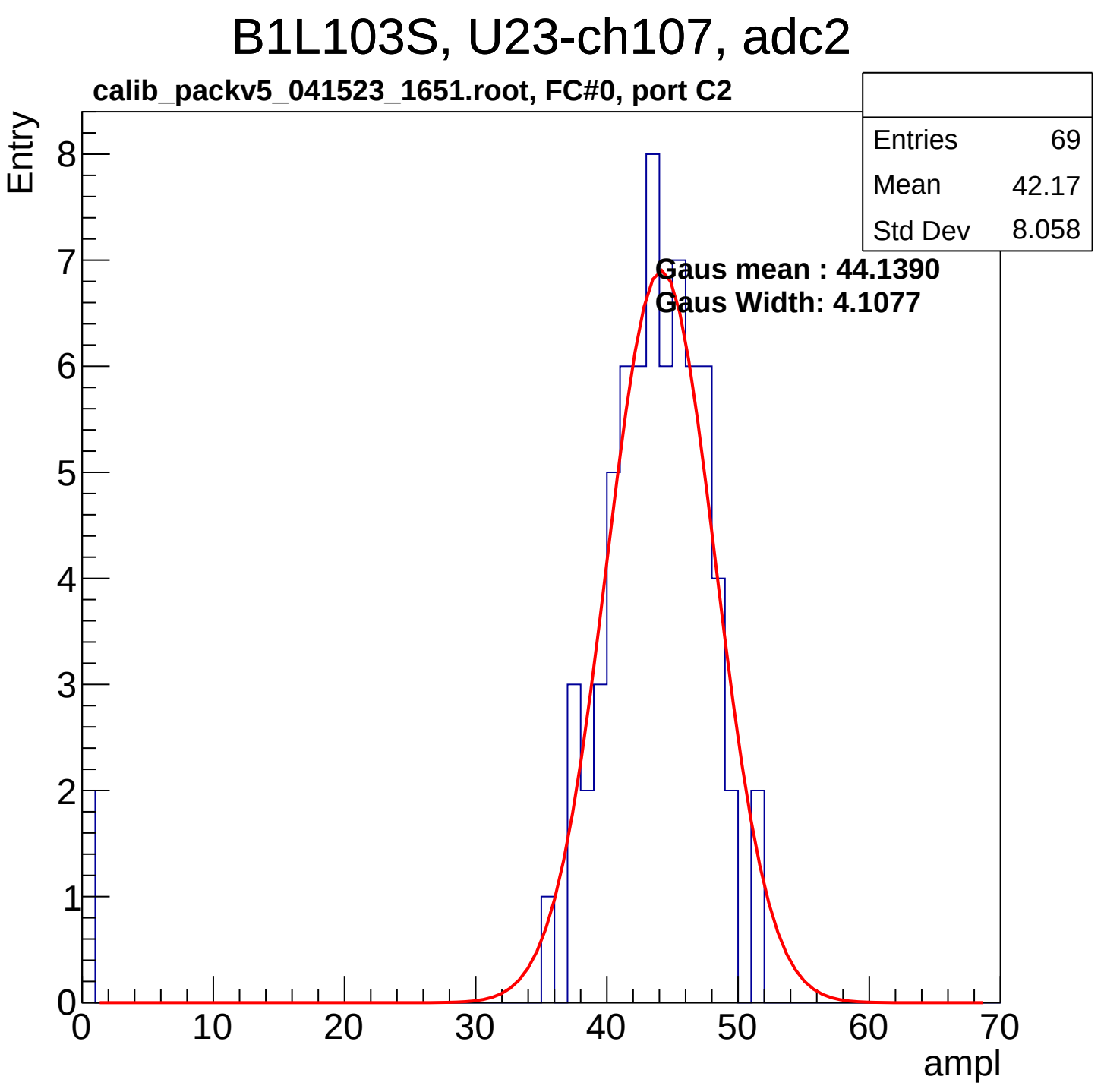

B1L103S, U23-ch106, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 58.51
Std Dev 2.952
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

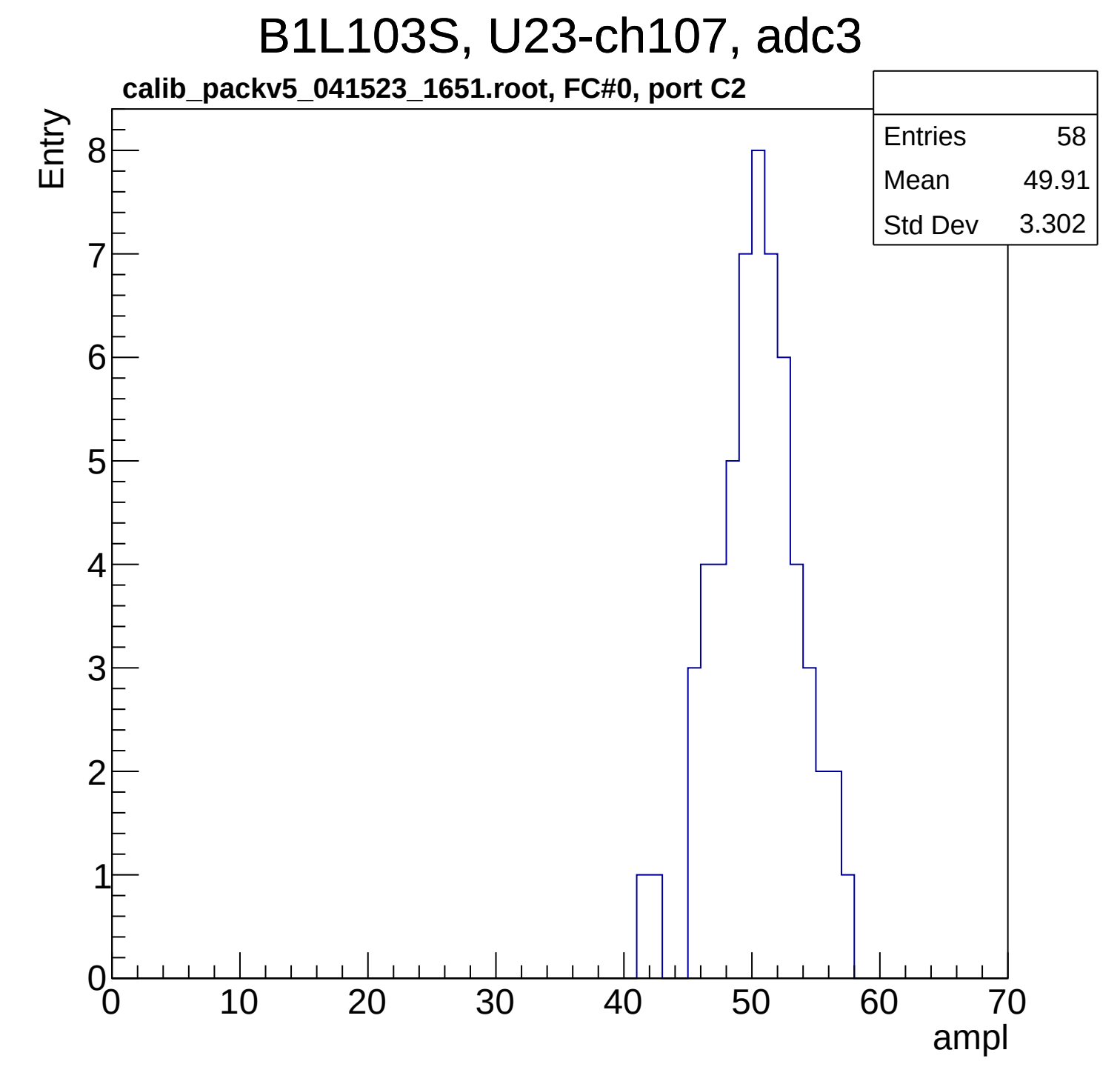

B1L103S, U23-ch106, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 13
Mean 56.38
Std Dev 16.34
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

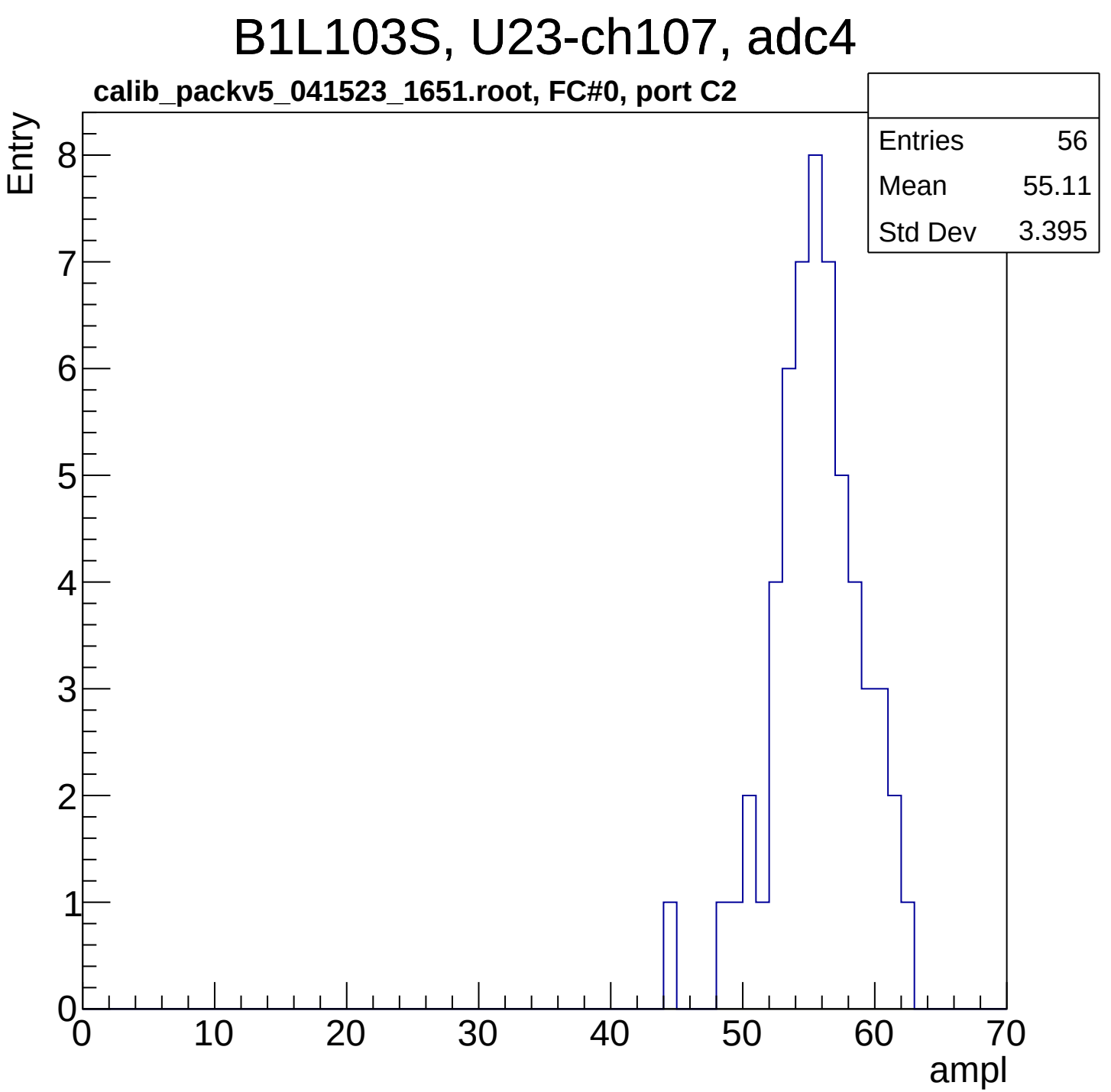

B1L103S, U23-ch106, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 5.857
Std Dev 18.05
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

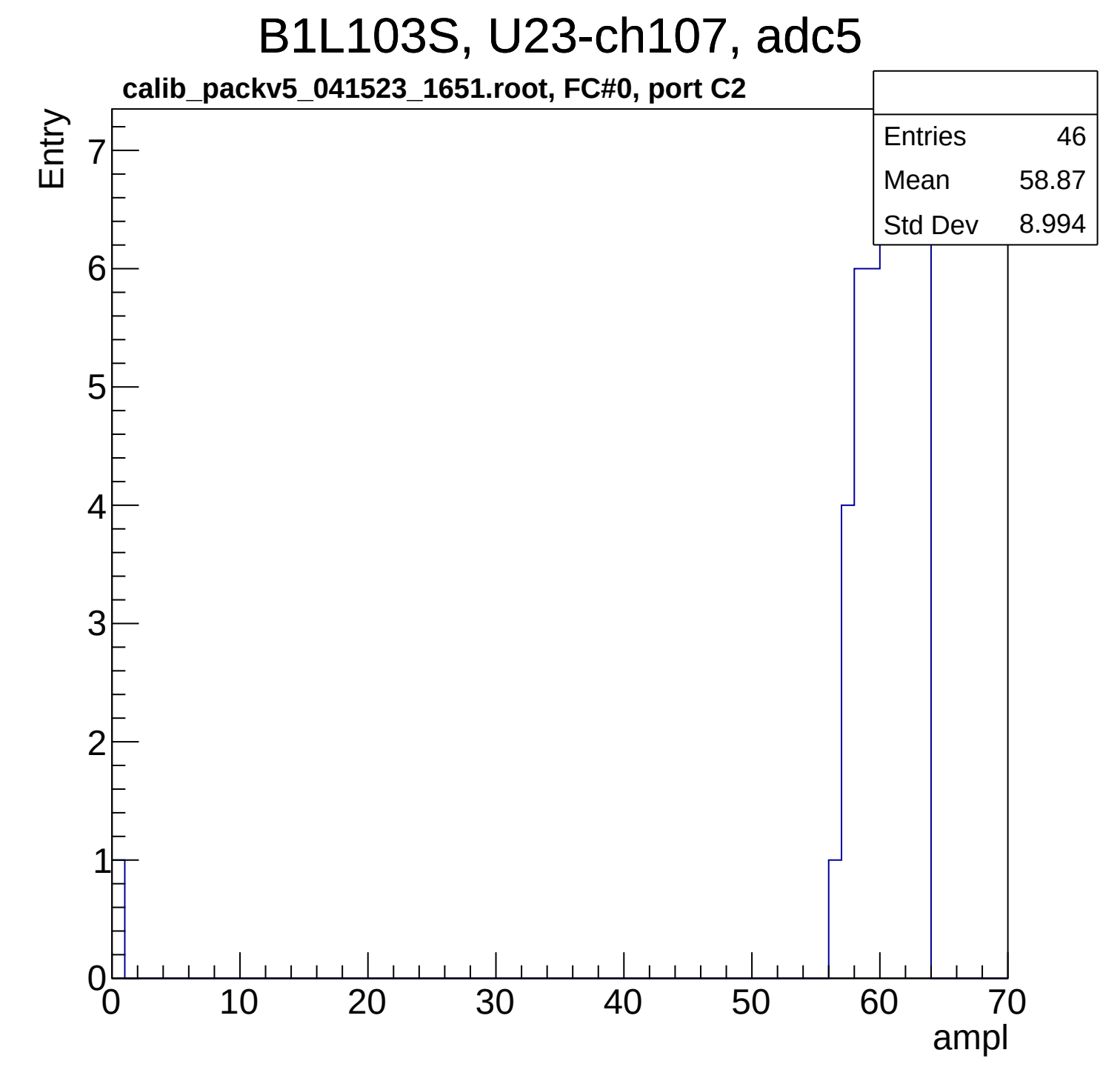

B1L103S, U23-ch107, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 91
Mean 23.71
Std Dev 12.2
Gaus mean : 30.4216
Gaus Width: 3.1298
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

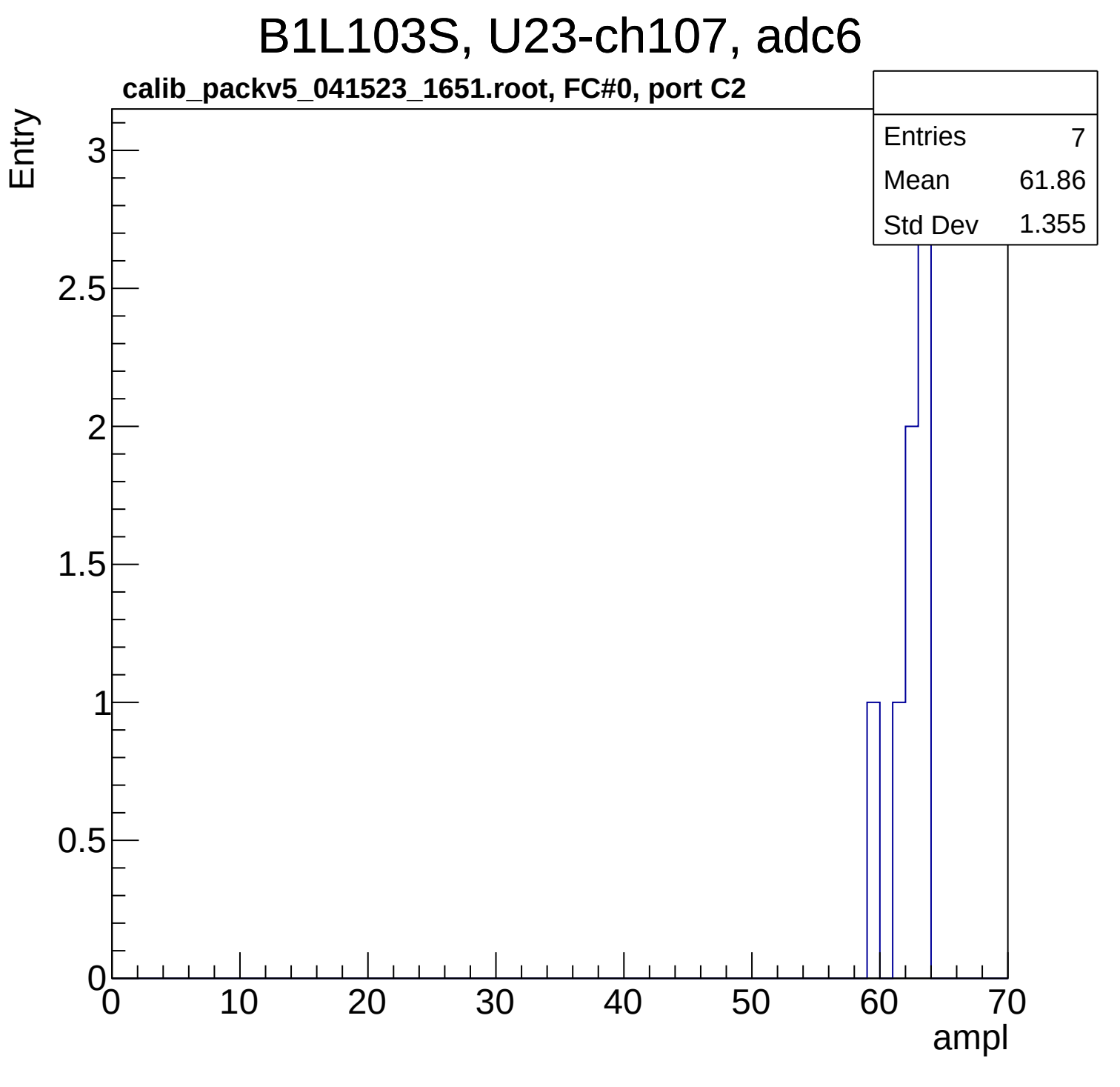

B1L103S, U23-ch107, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 80
Mean 32.24
Std Dev 12.66
Gaus mean : 36.9643
Gaus Width: 4.5336
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

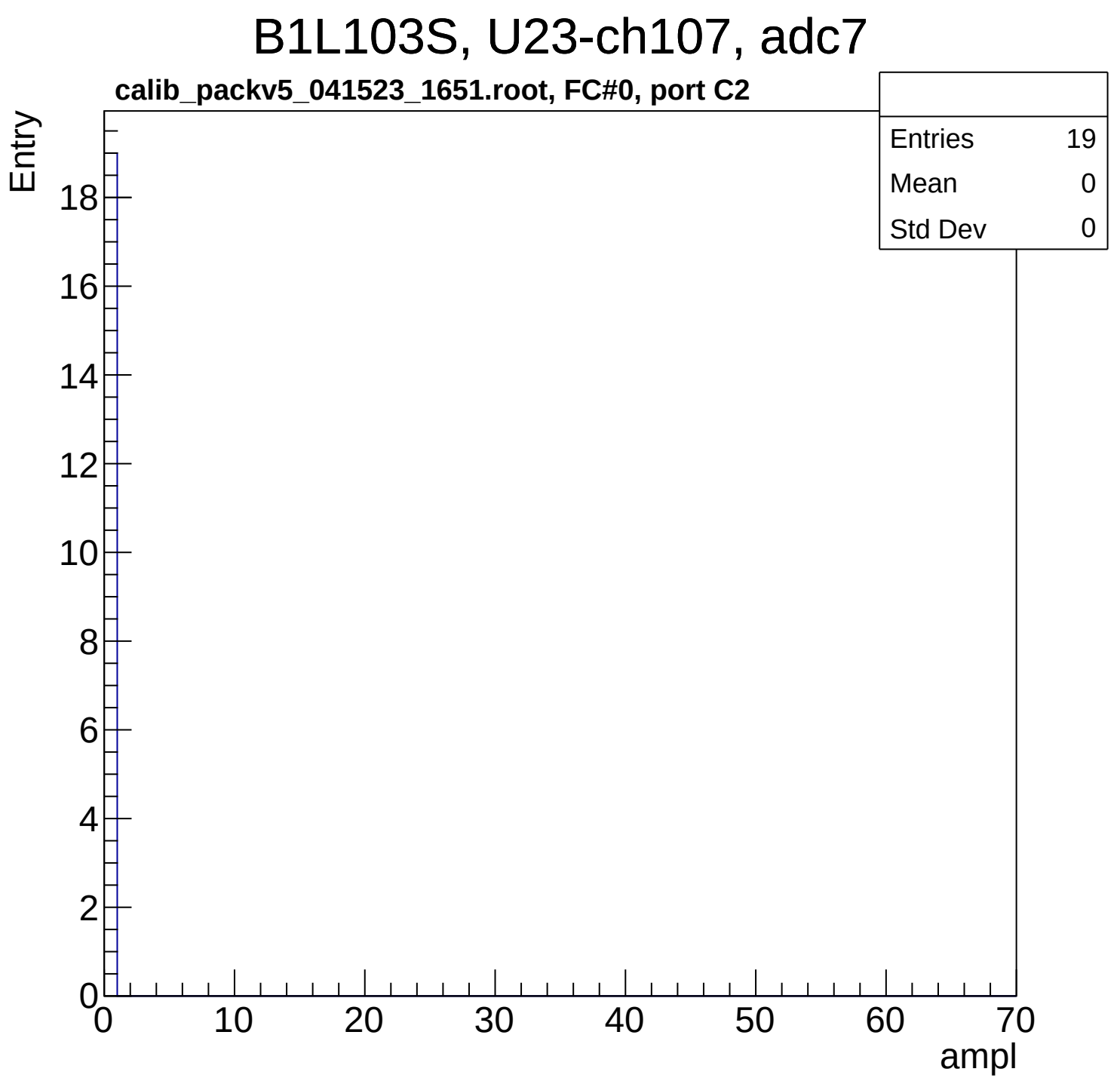

B1L103S, U23-ch107, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 42.17
Std Dev 8.058
Gaus mean : 44.1390
Gaus Width: 4.1077
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

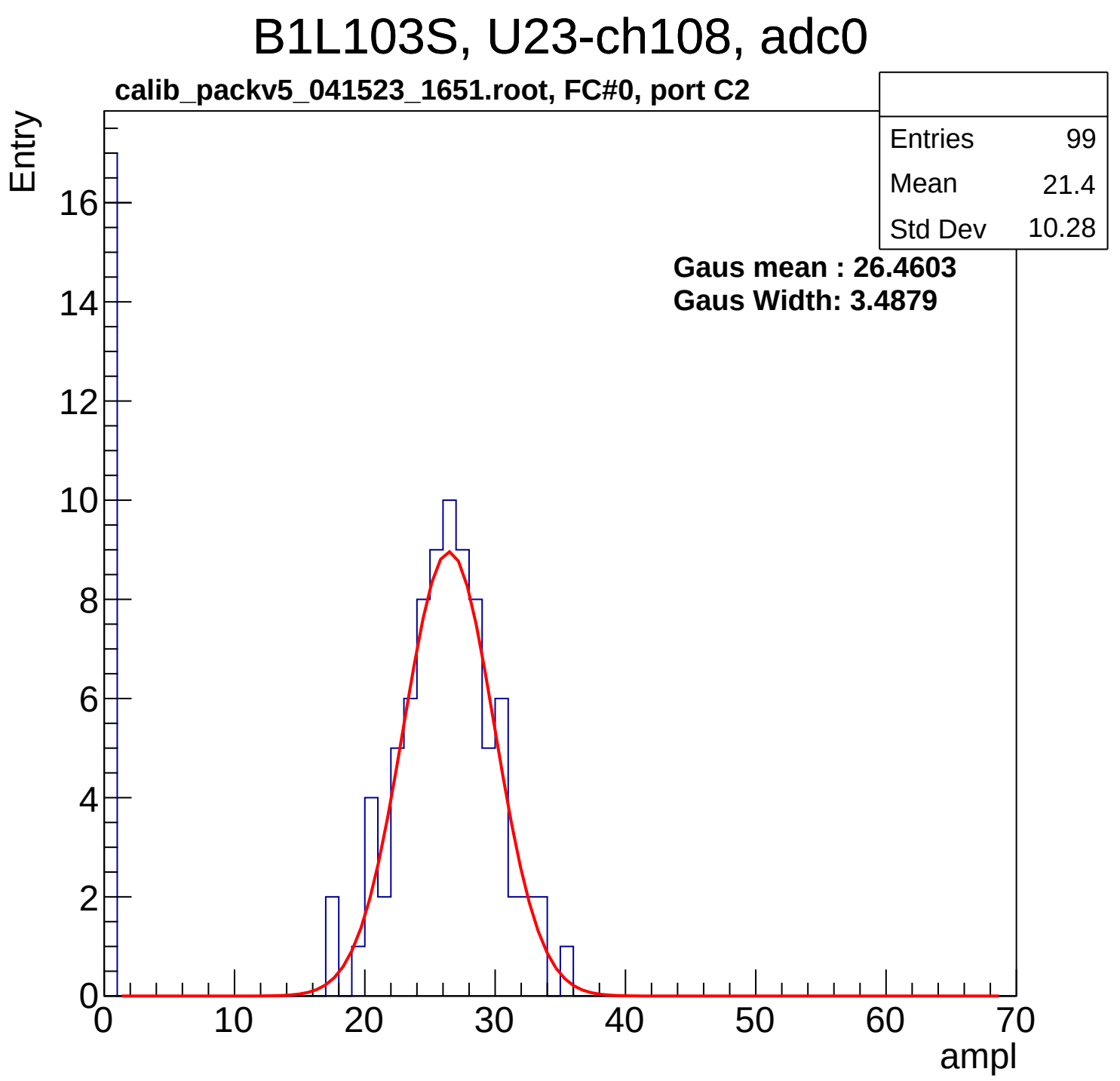

B1L103S, U23-ch107, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 49.91
Std Dev 3.302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

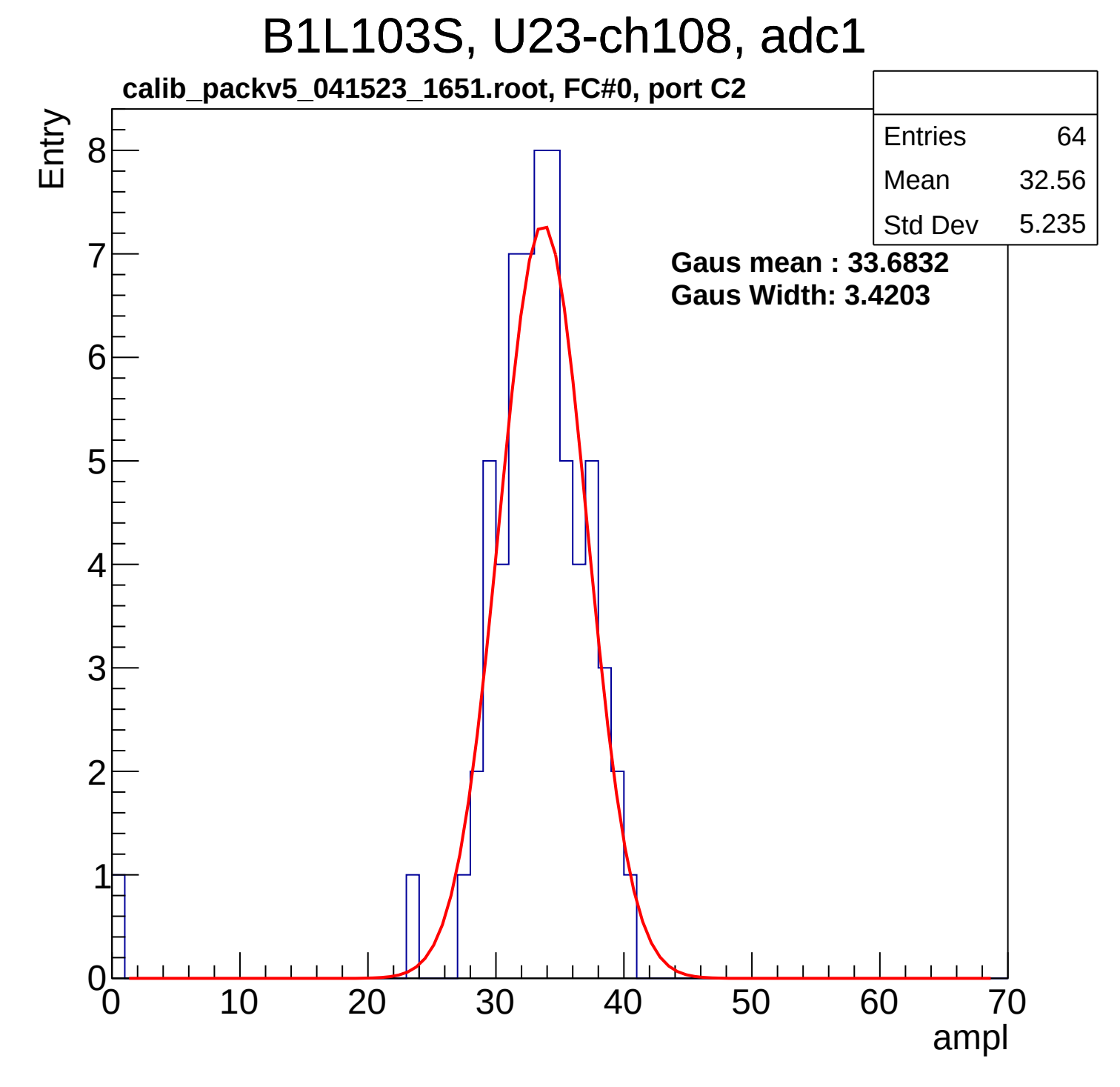

B1L103S, U23-ch107, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 55.11
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

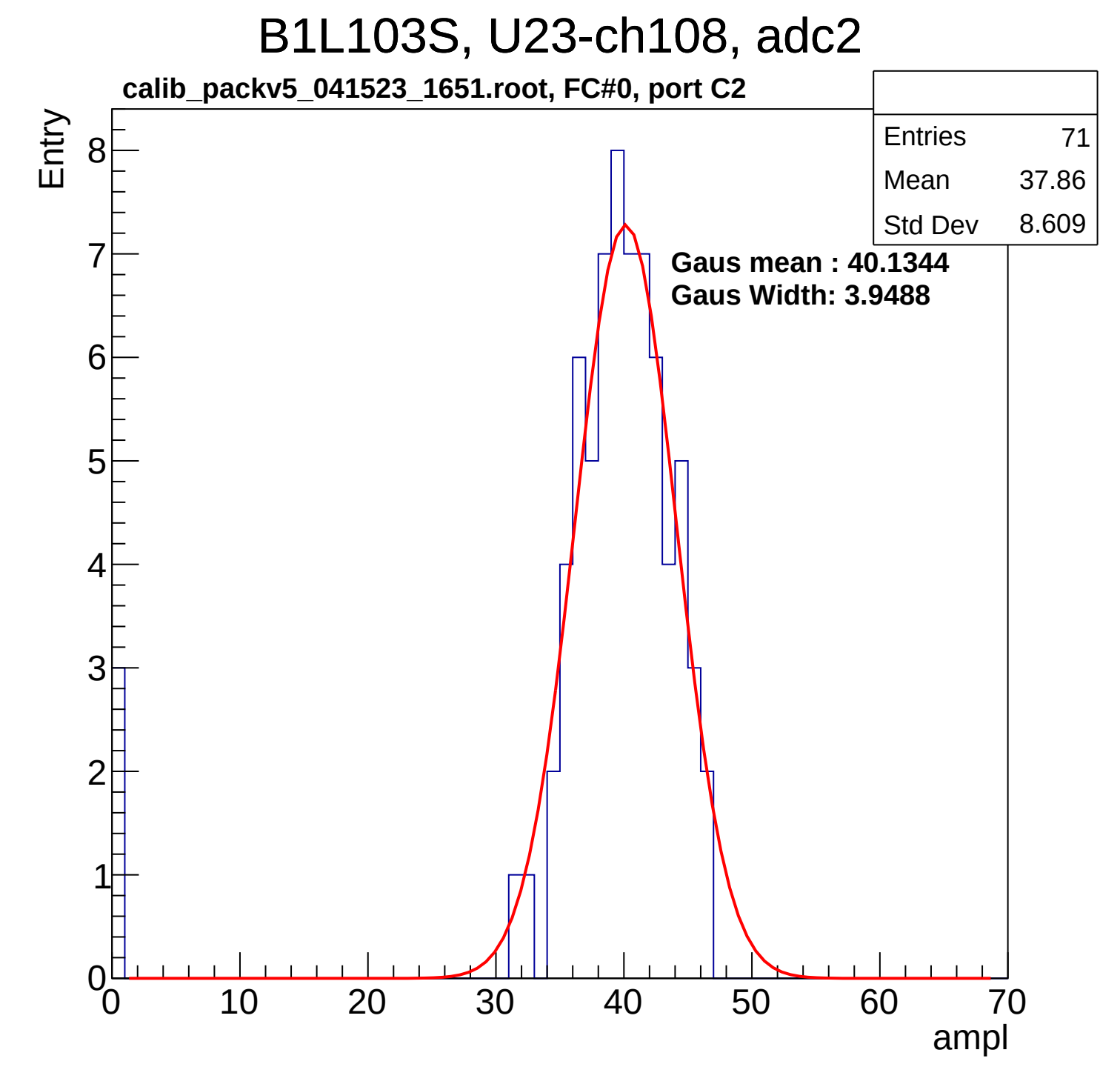

B1L103S, U23-ch107, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 46
Mean 58.87
Std Dev 8.994
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

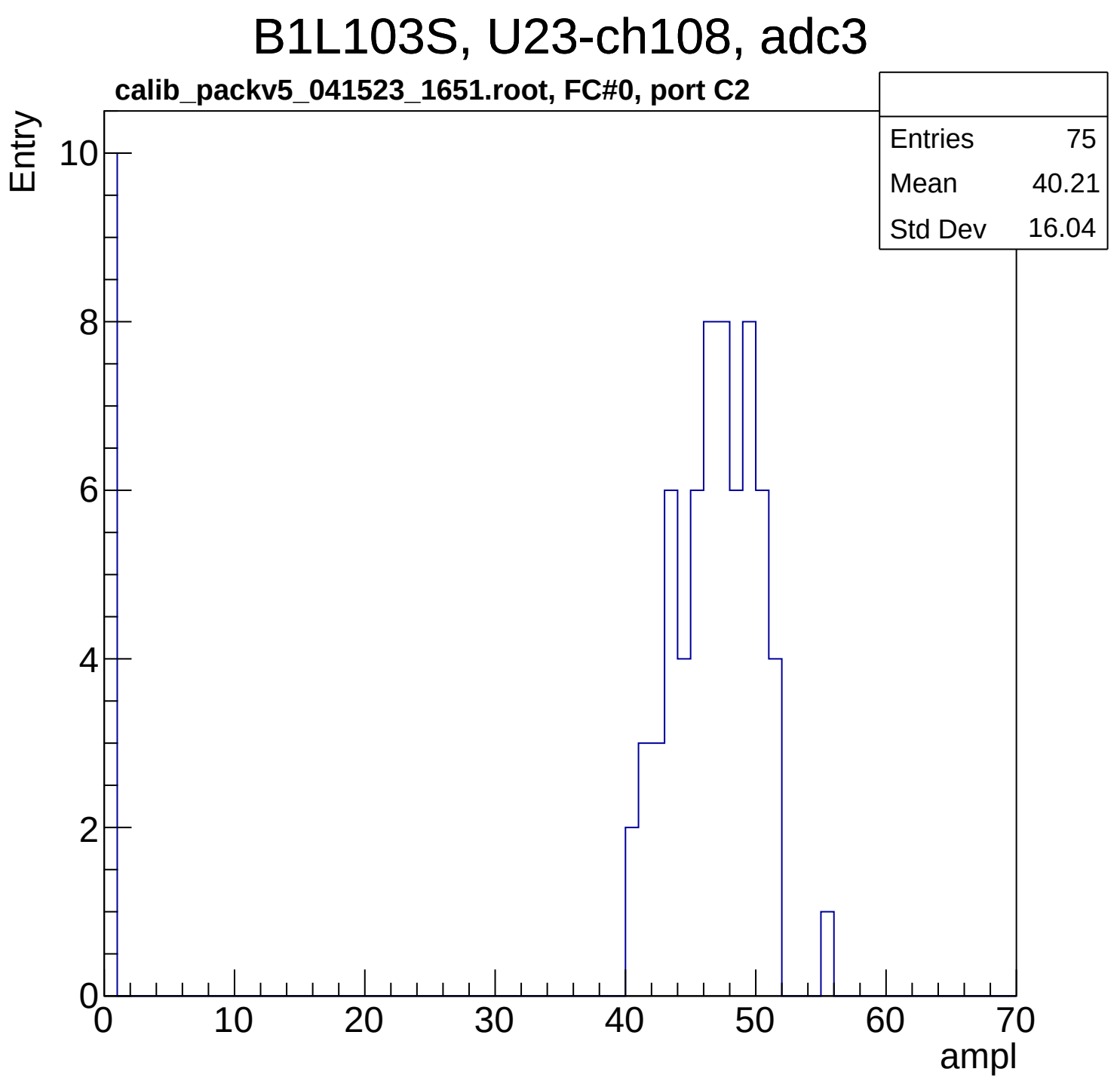

B1L103S, U23-ch107, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 7
Mean 61.86
Std Dev 1.355
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

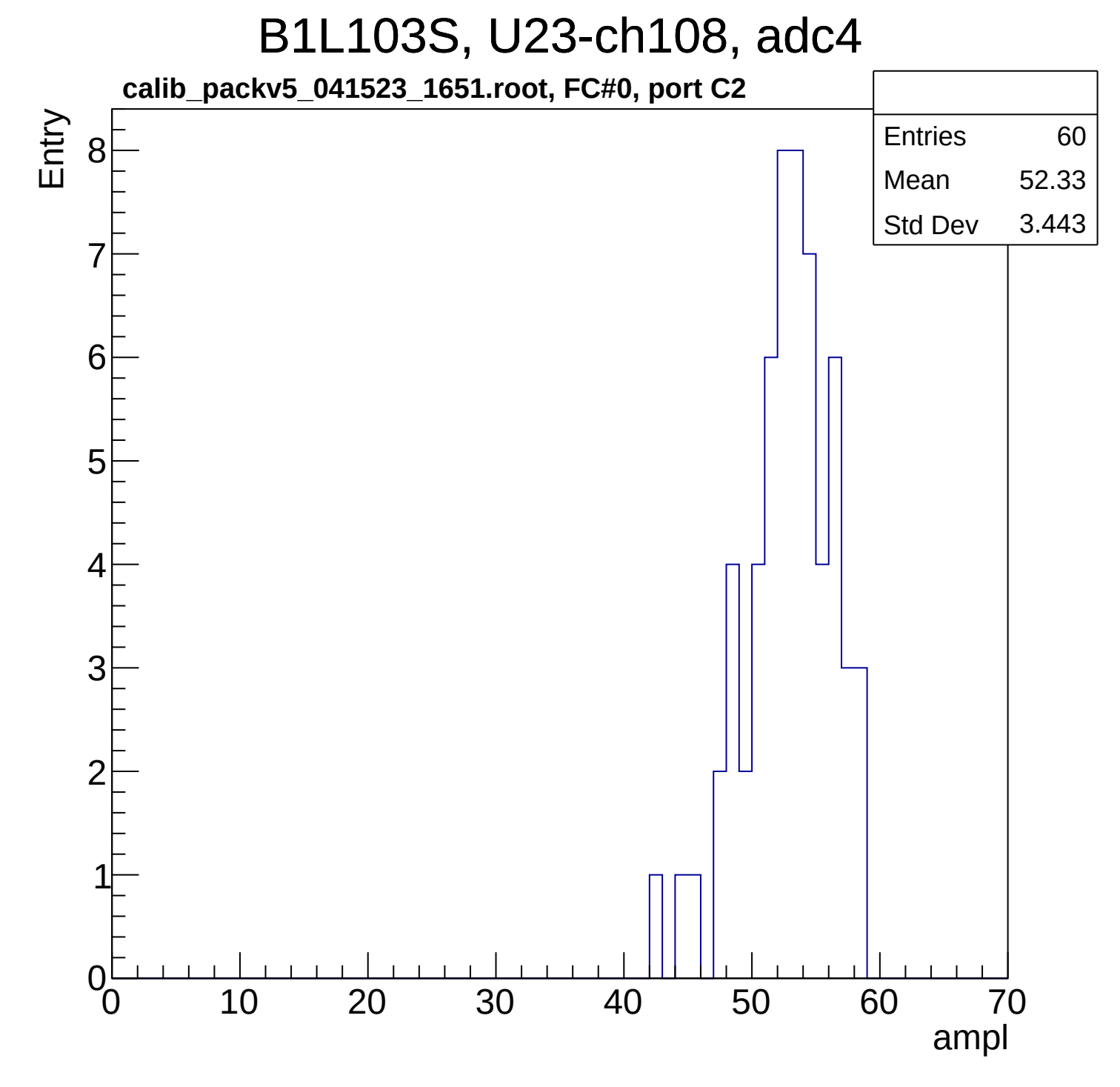

B1L103S, U23-ch107, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

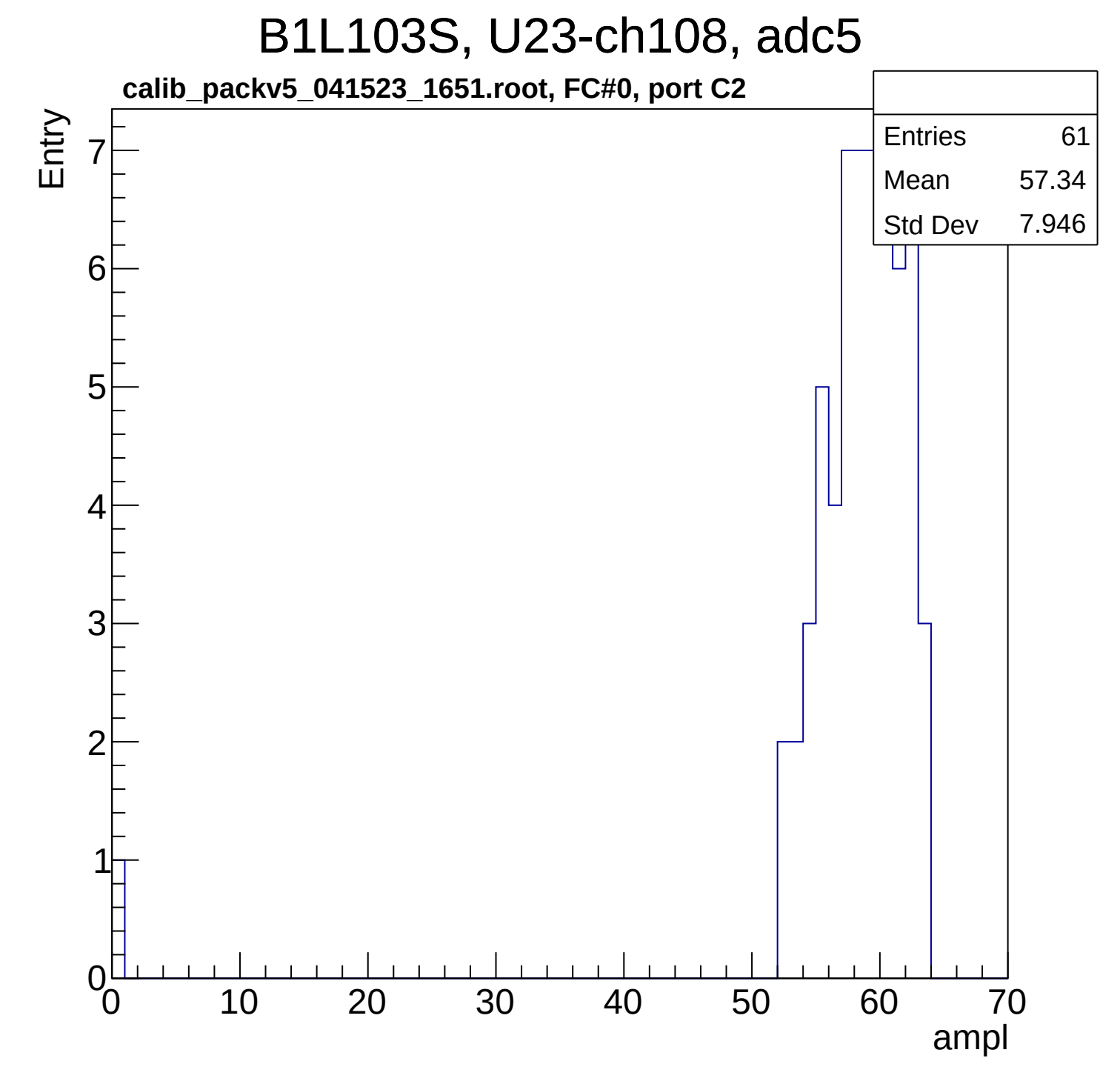

B1L103S, U23-ch108, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 99
Mean 21.4
Std Dev 10.28
Gaus mean : 26.4603
Gaus Width: 3.4879
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

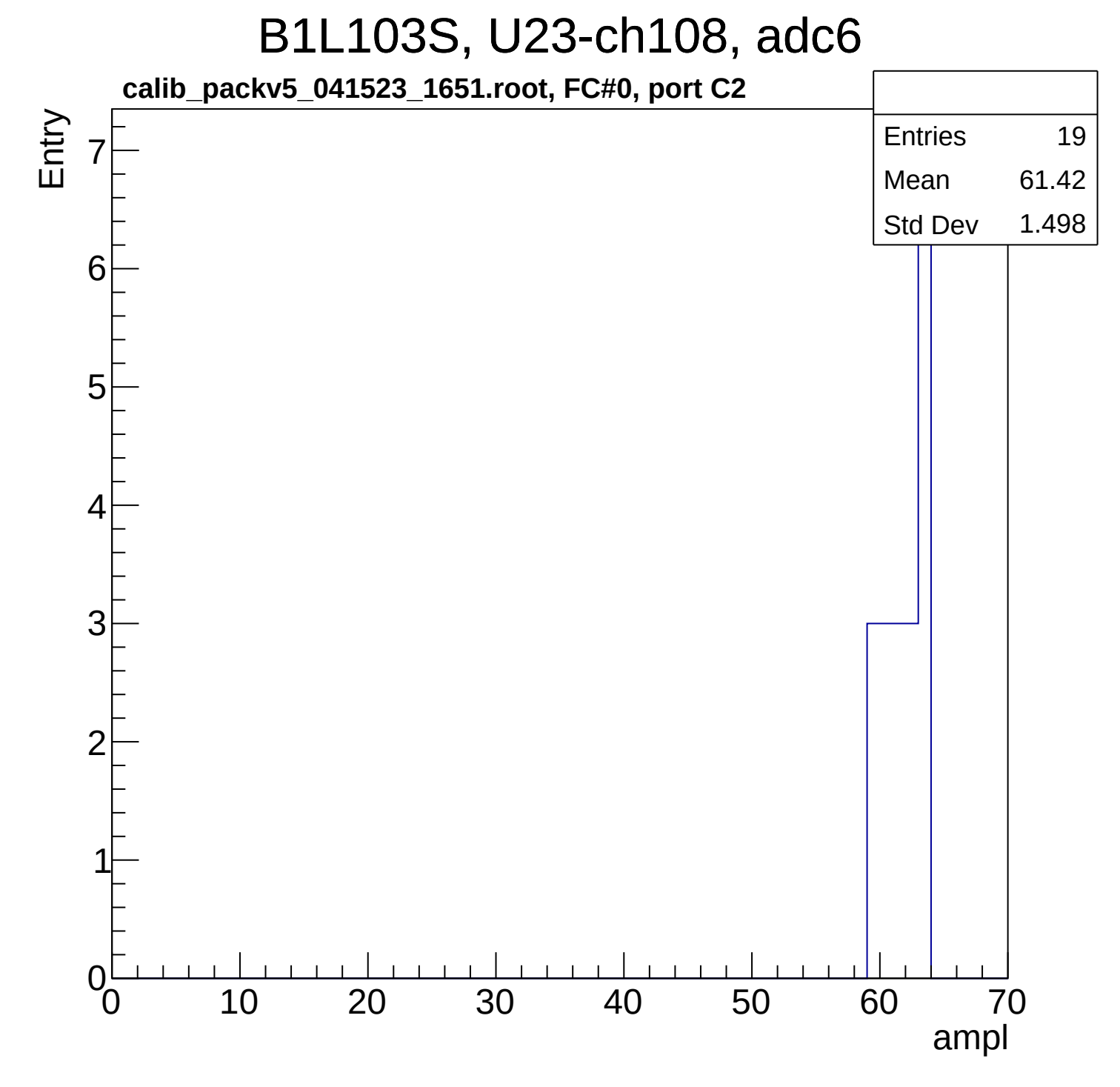

B1L103S, U23-ch108, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 32.56
Std Dev 5.235
Gaus mean : 33.6832
Gaus Width: 3.4203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

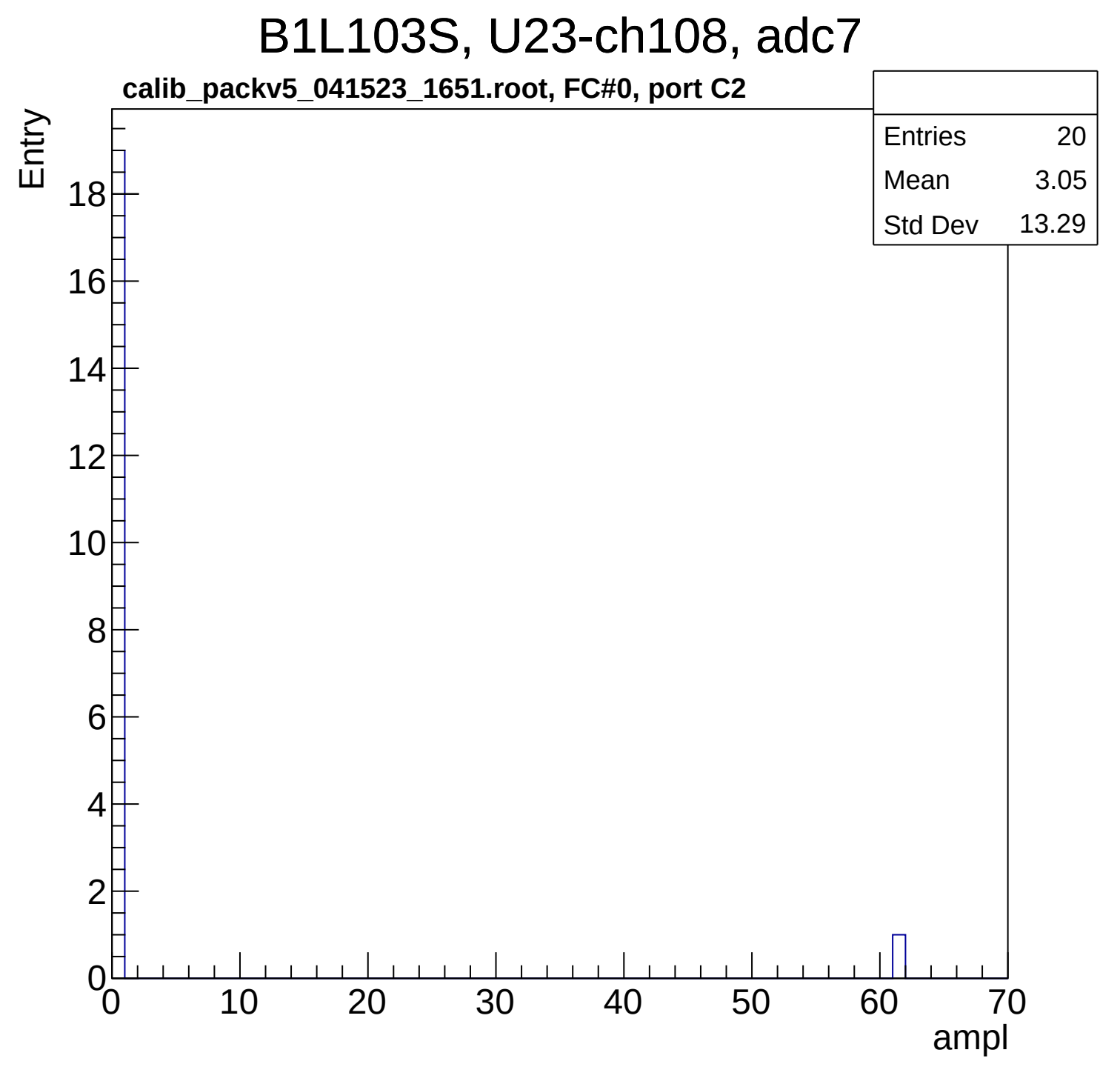

B1L103S, U23-ch108, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 37.86
Std Dev 8.609
Gaus mean : 40.1344
Gaus Width: 3.9488
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

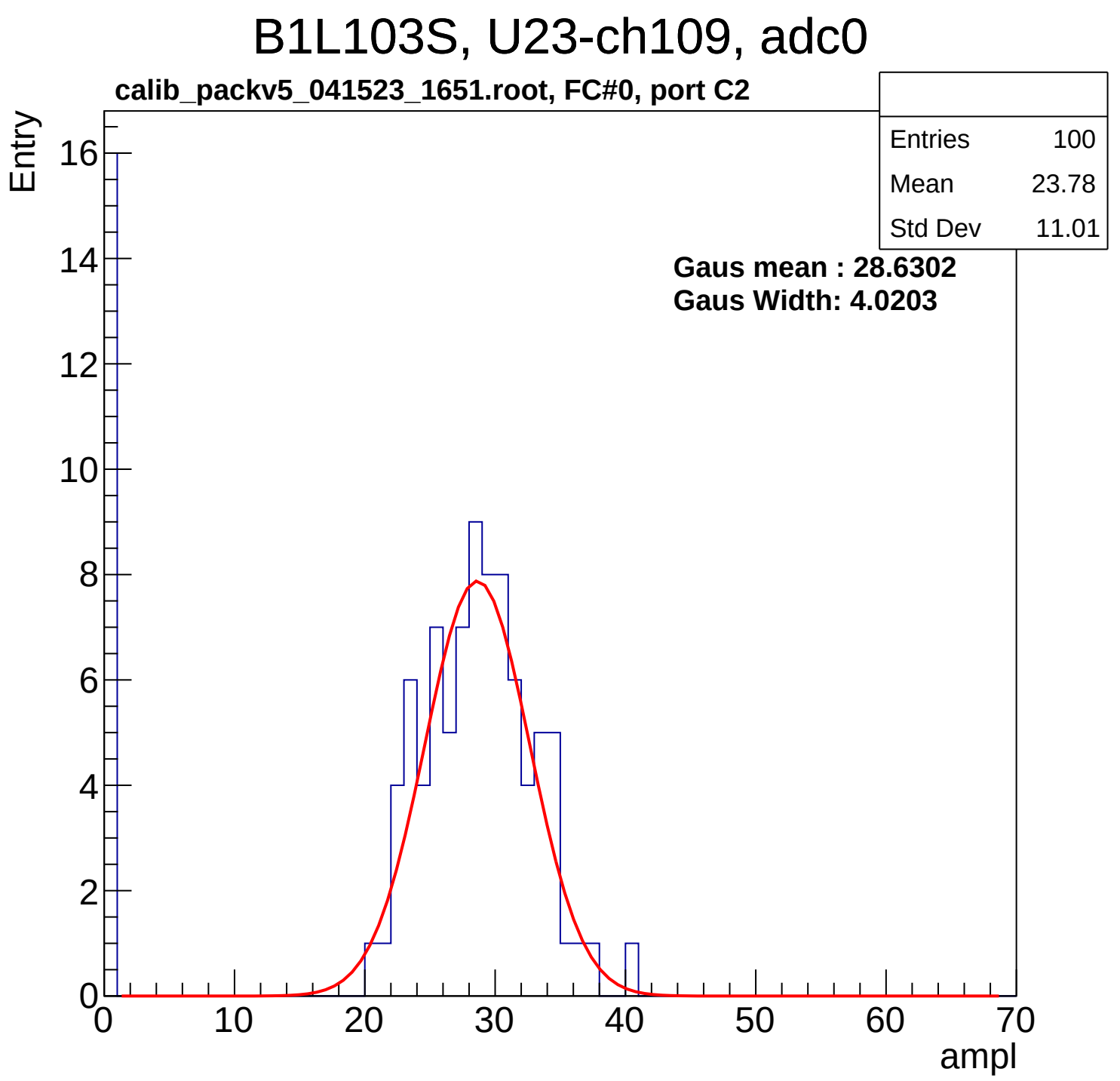

B1L103S, U23-ch108, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 40.21
Std Dev 16.04
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

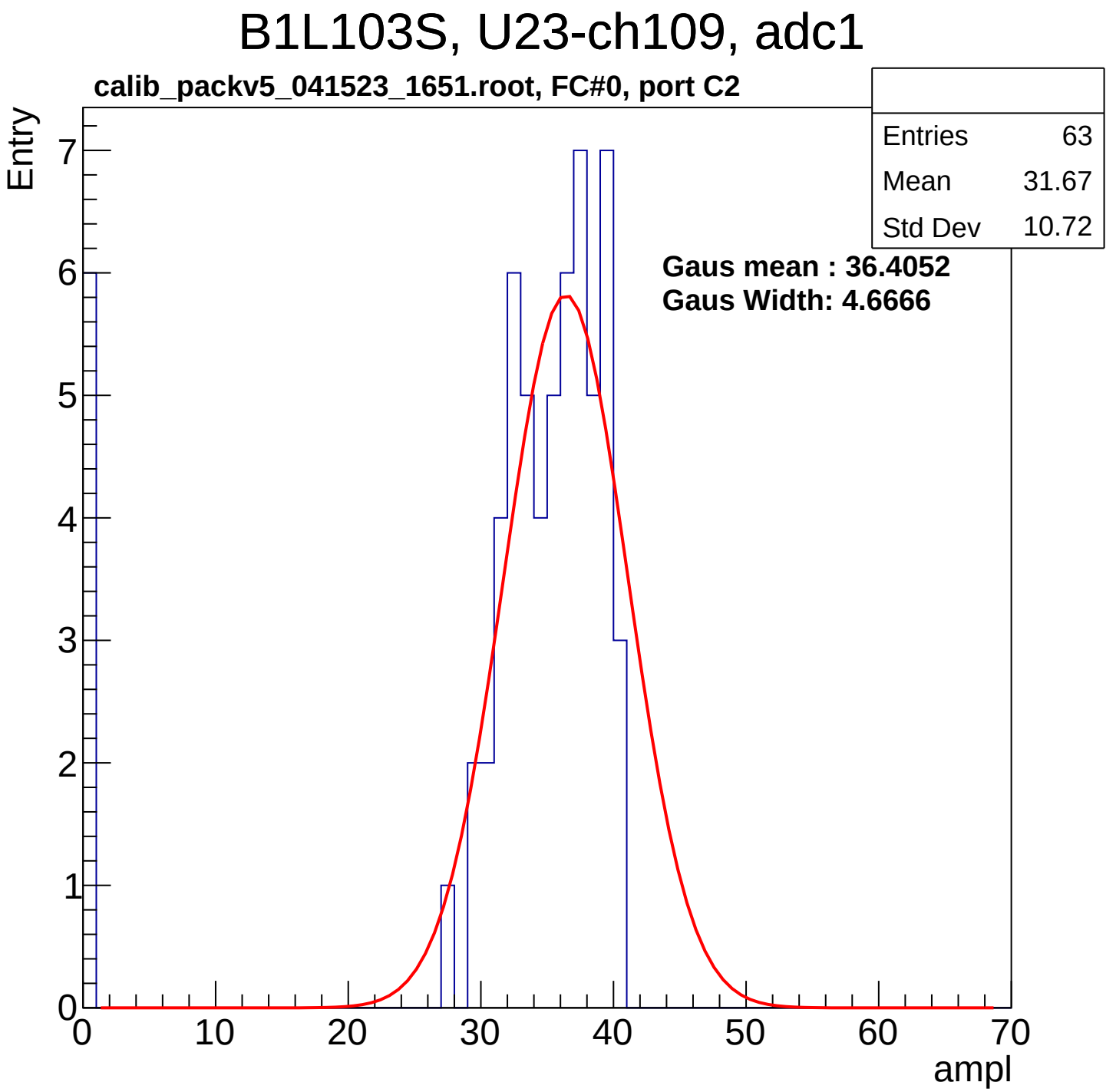

B1L103S, U23-ch108, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 52.33
Std Dev 3.443
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

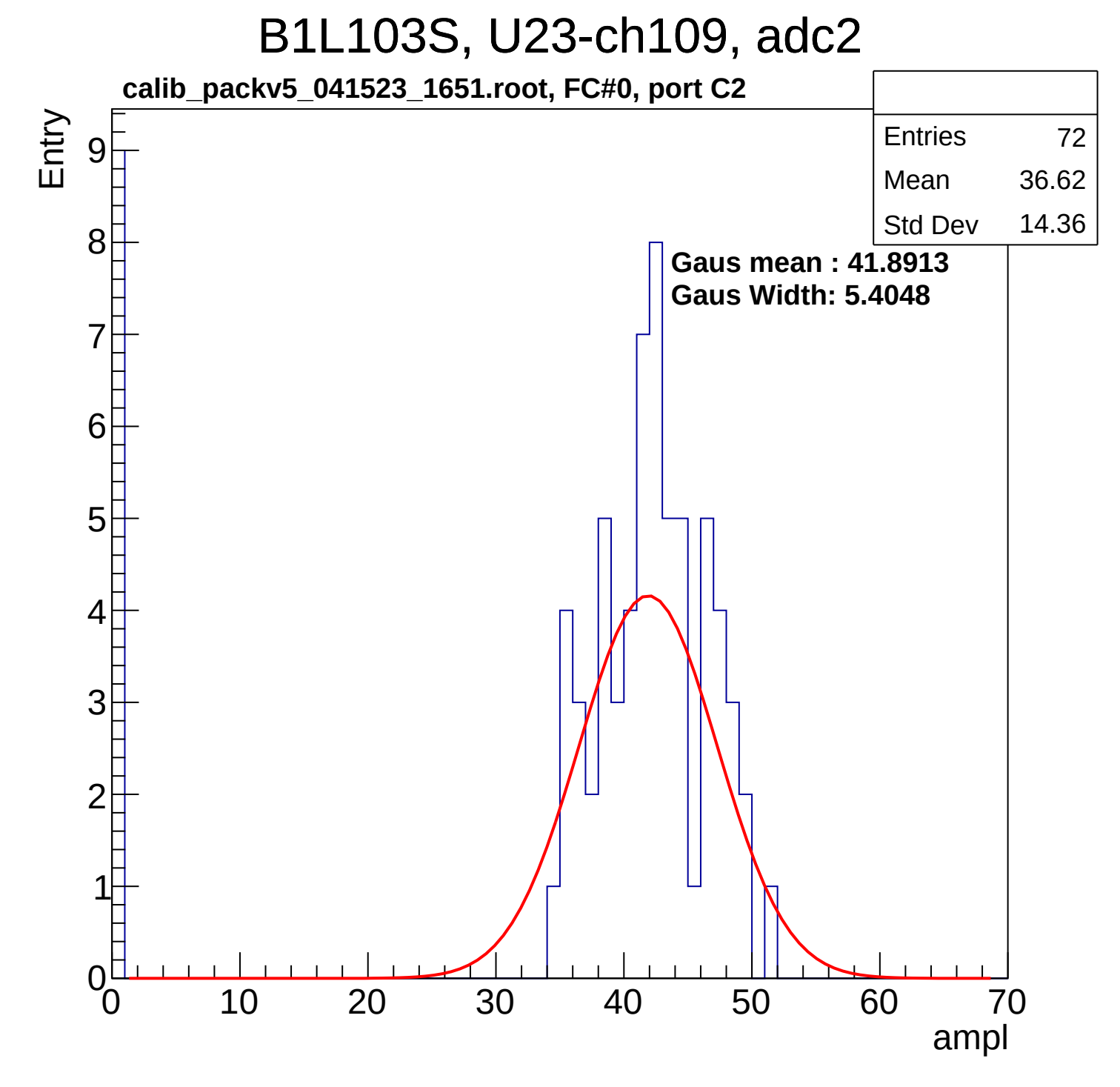

B1L103S, U23-ch108, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 61
Mean 57.34
Std Dev 7.946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

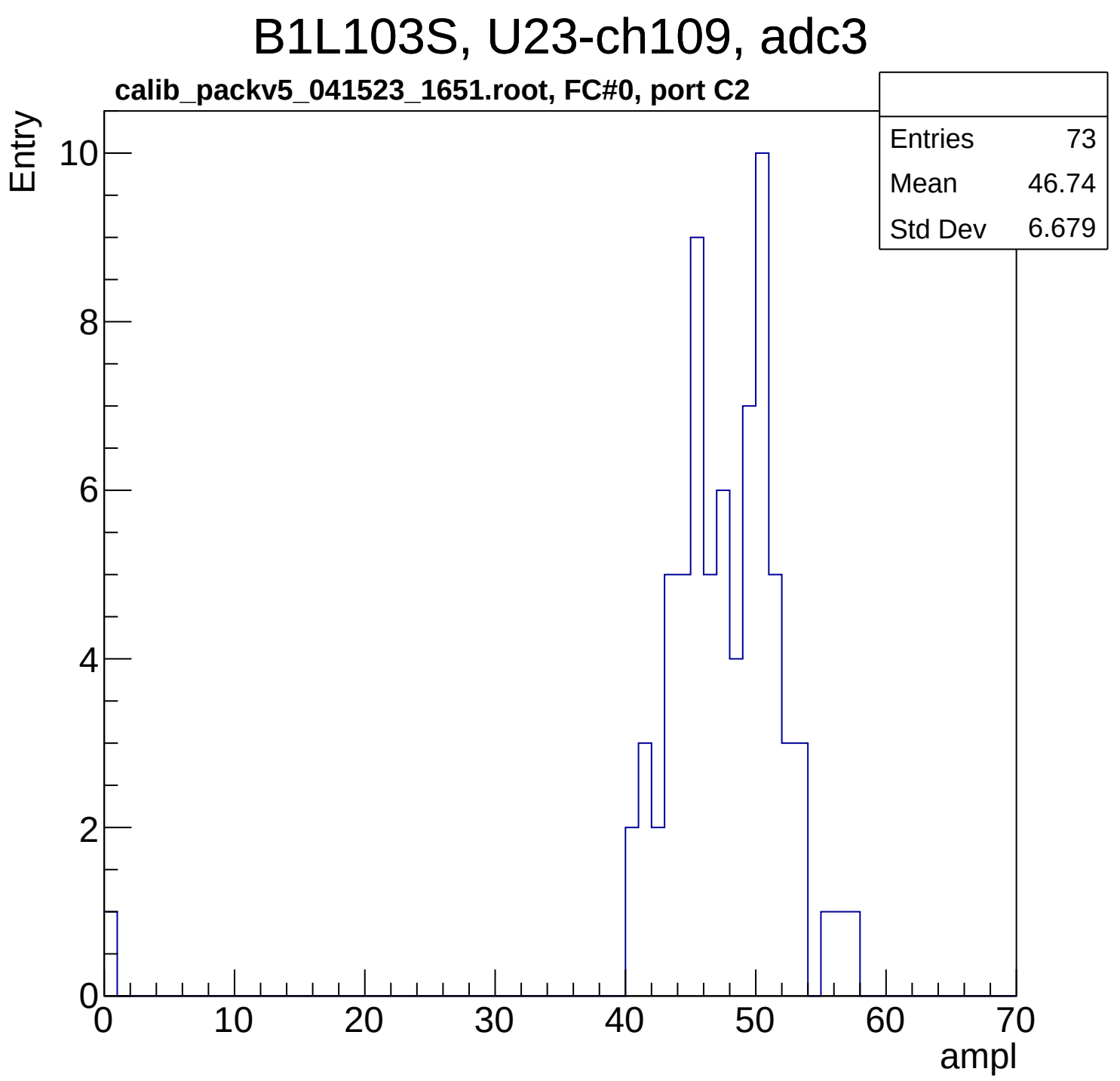

B1L103S, U23-ch108, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 61.42
Std Dev 1.498
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

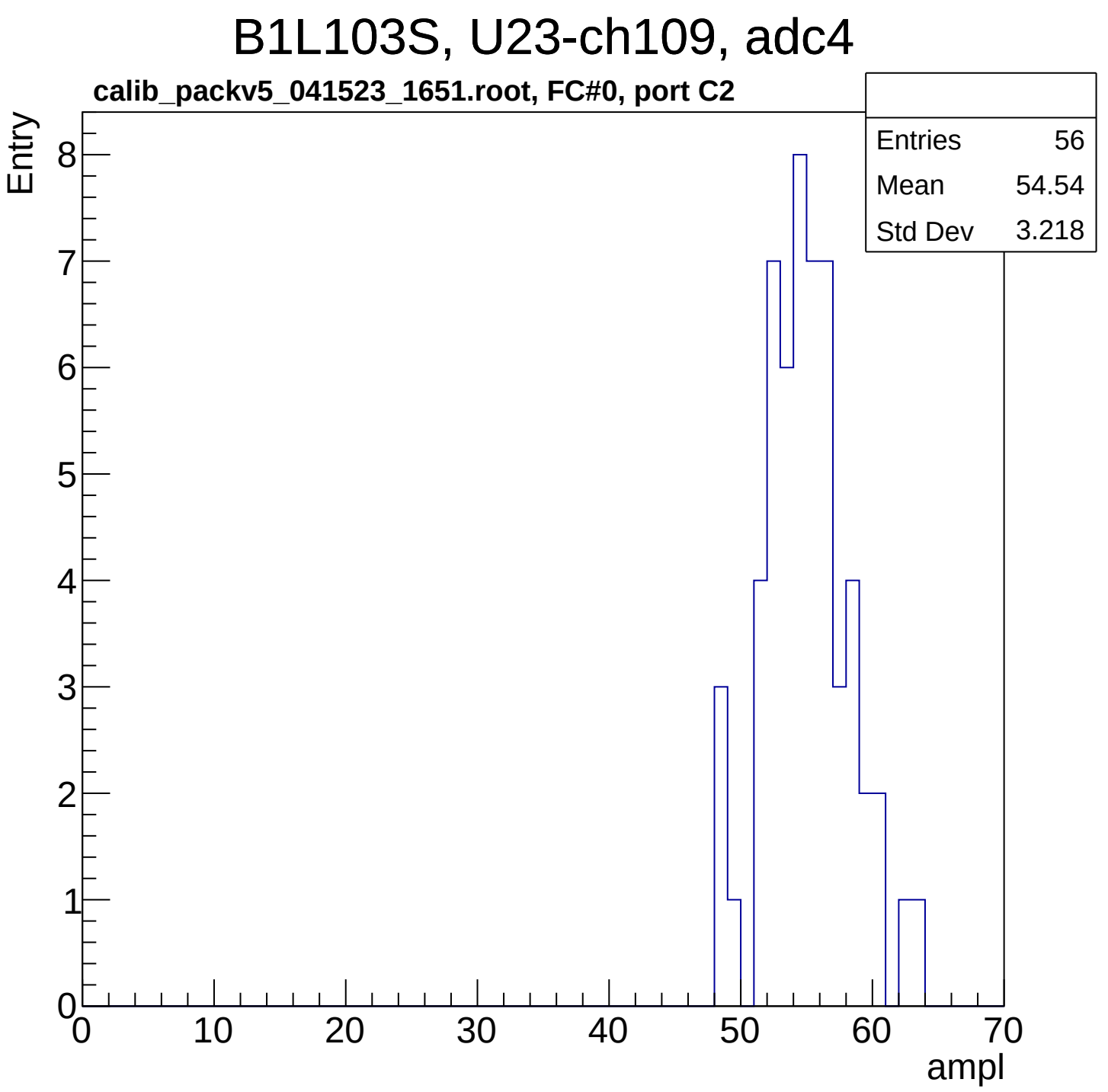

B1L103S, U23-ch108, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.05
Std Dev 13.29
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

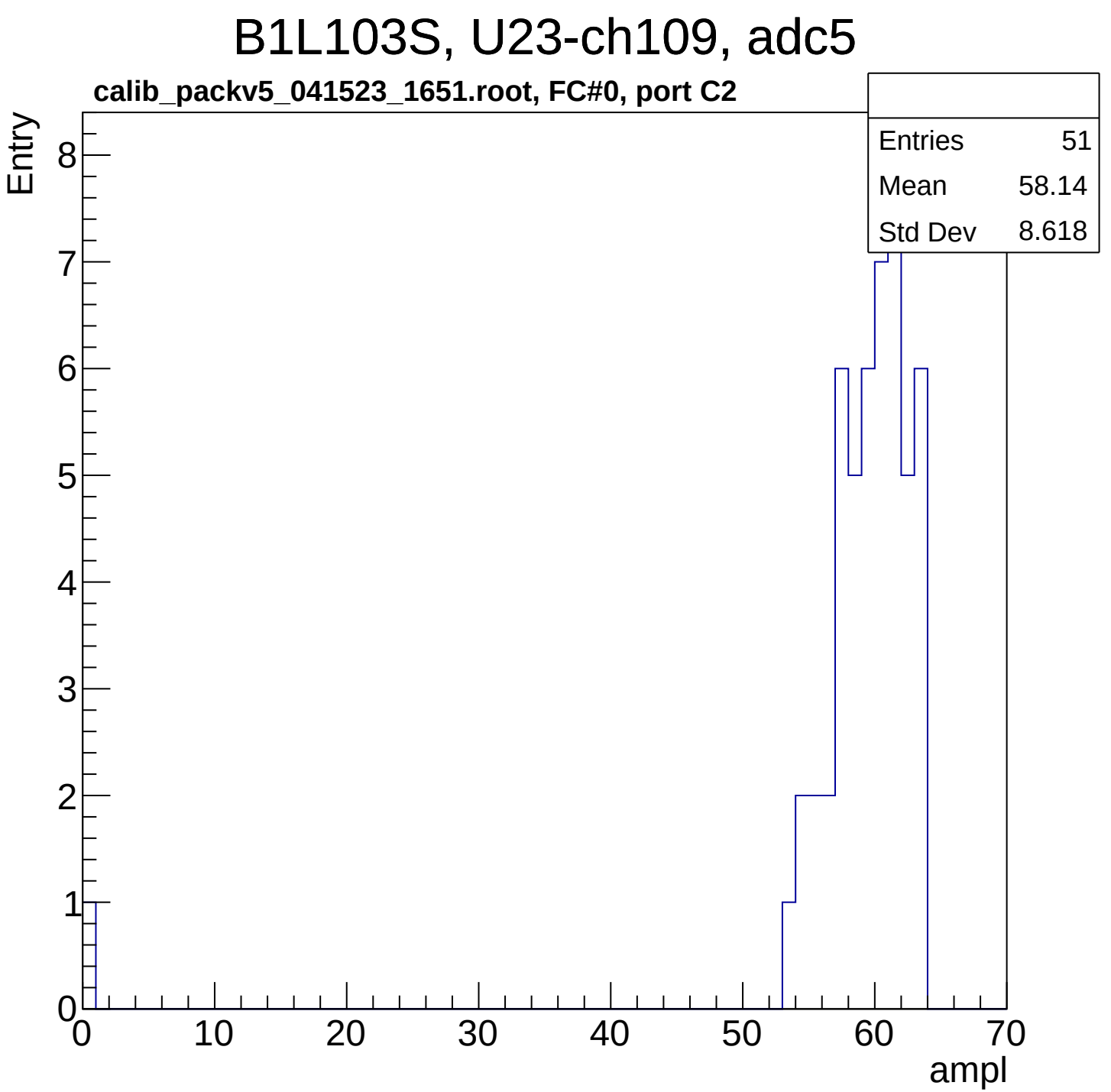

B1L103S, U23-ch109, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 100
Mean 23.78
Std Dev 11.01
Gaus mean : 28.6302
Gaus Width: 4.0203
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

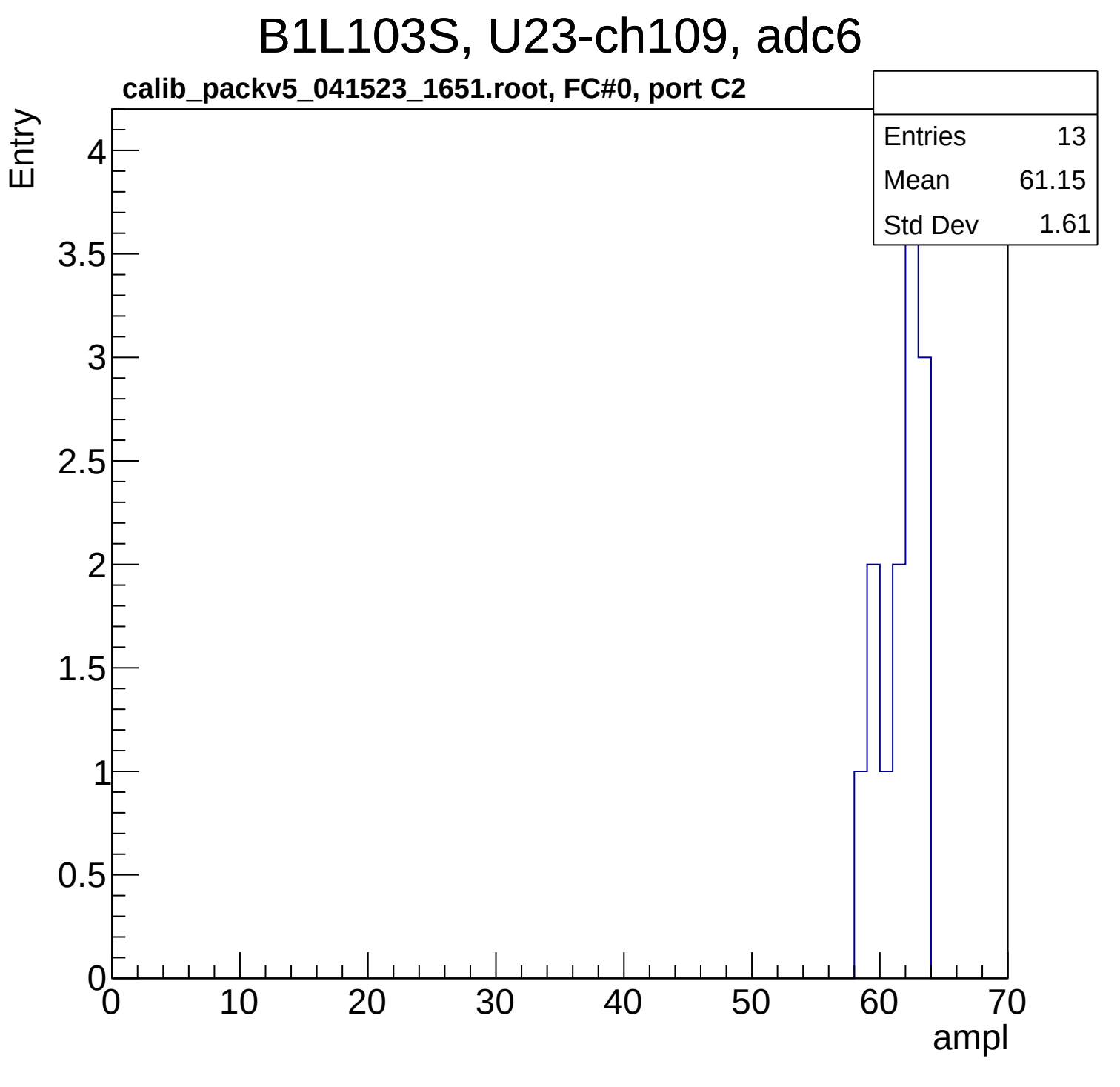

B1L103S, U23-ch109, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 31.67
Std Dev 10.72
Gaus mean : 36.4052
Gaus Width: 4.6666
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

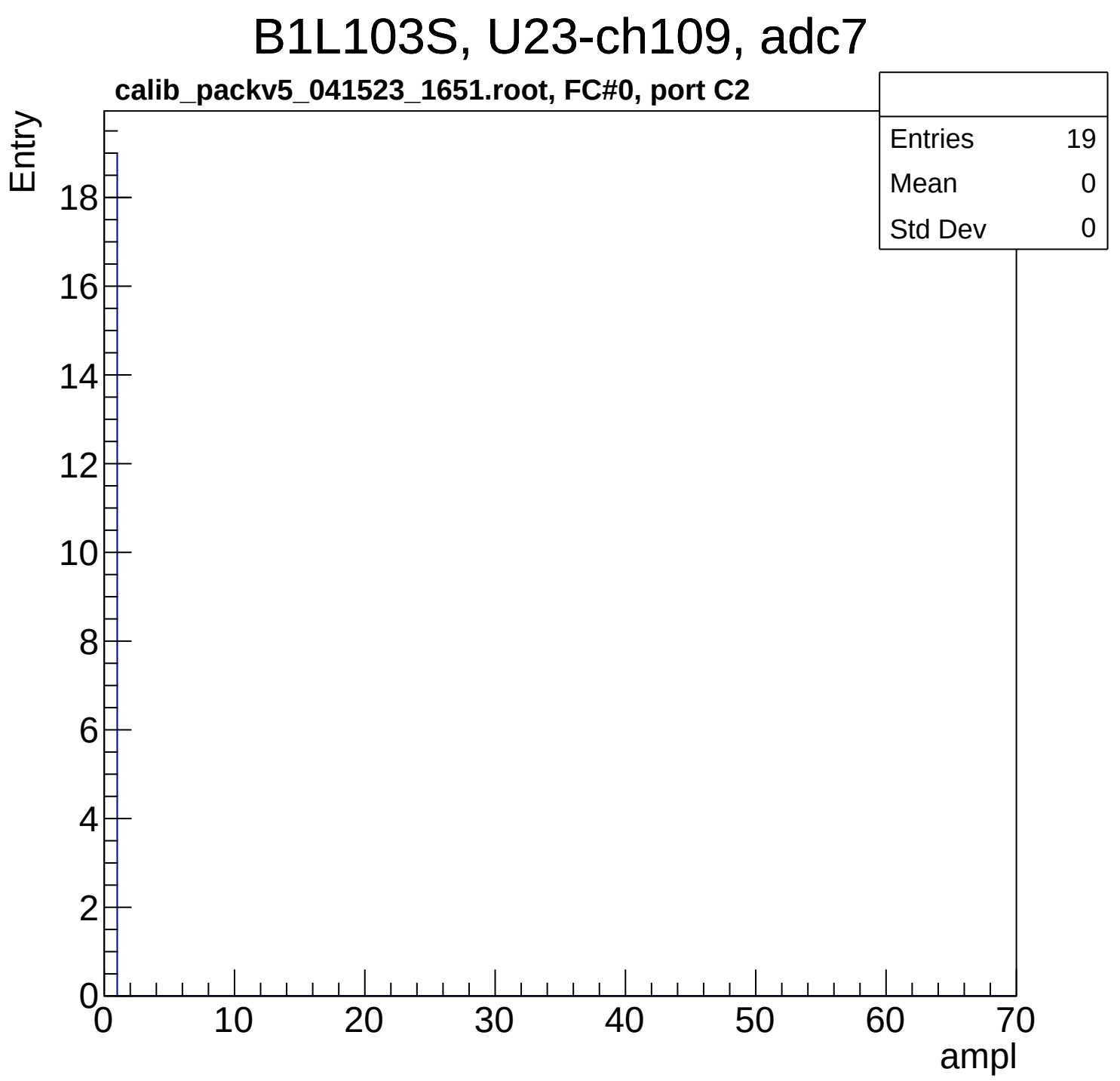

B1L103S, U23-ch109, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 36.62
Std Dev 14.36
Gaus mean : 41.8913
Gaus Width: 5.4048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

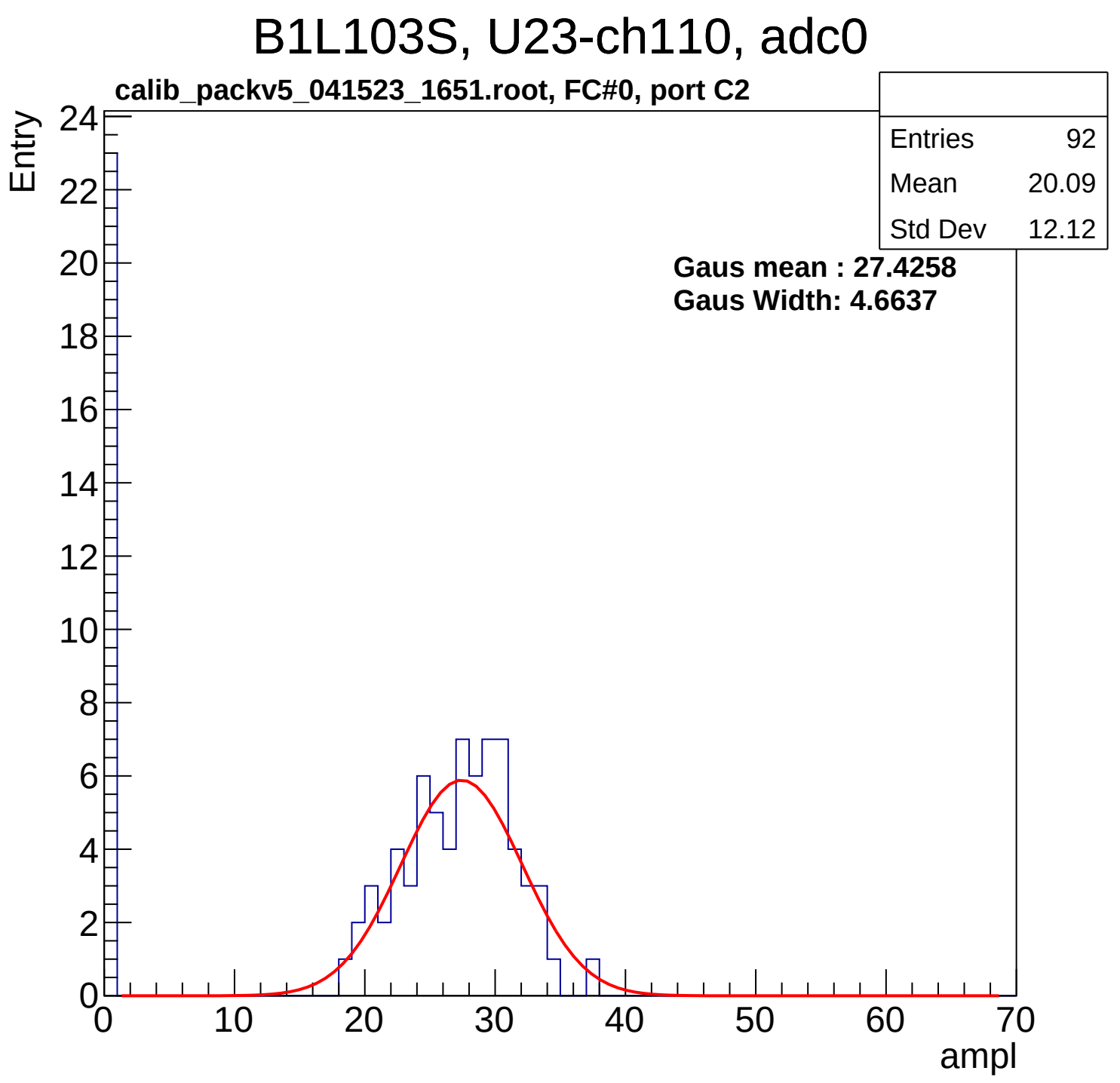

B1L103S, U23-ch109, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 73
Mean 46.74
Std Dev 6.679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

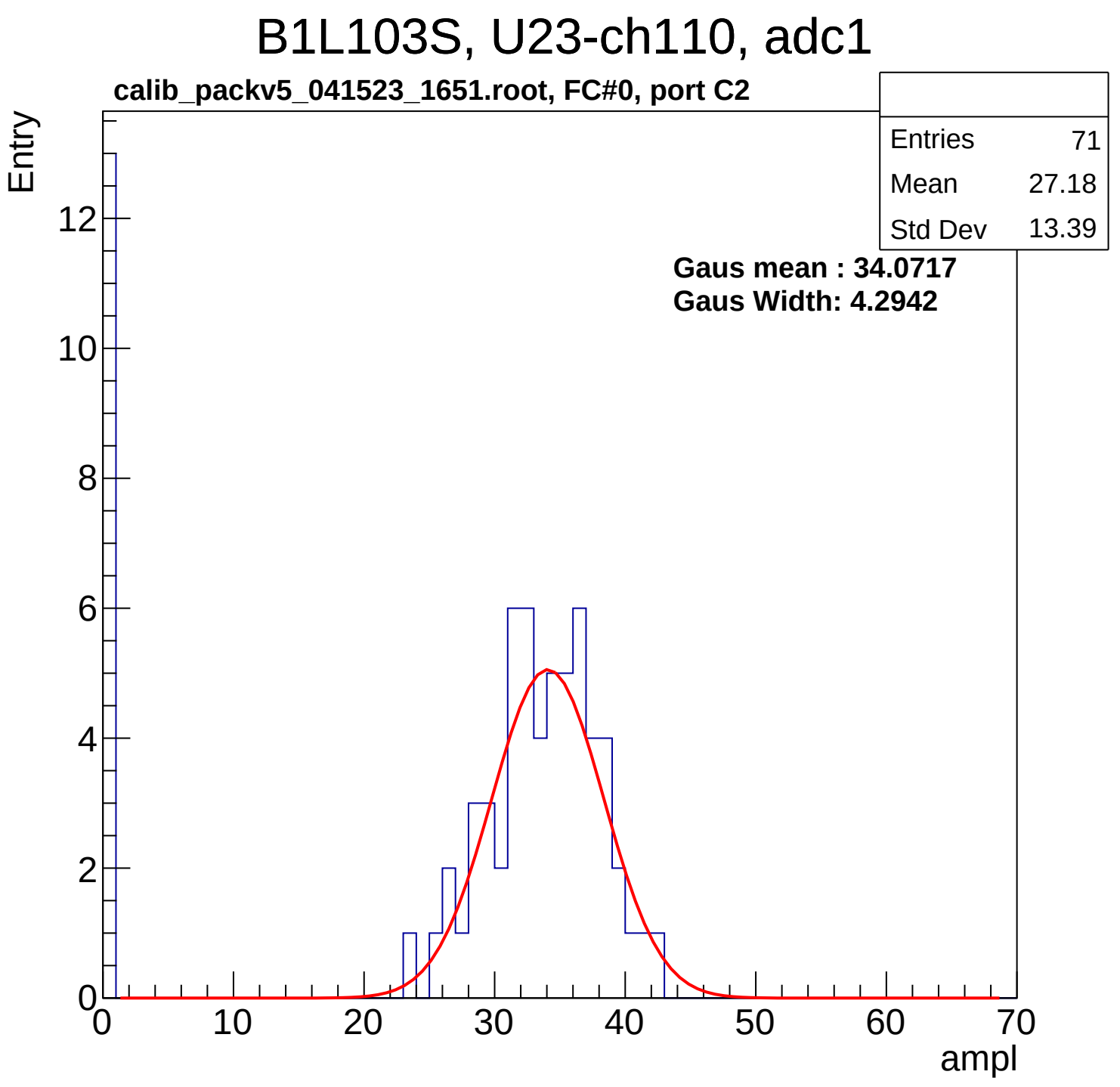

B1L103S, U23-ch109, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 56
Mean 54.54
Std Dev 3.218
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

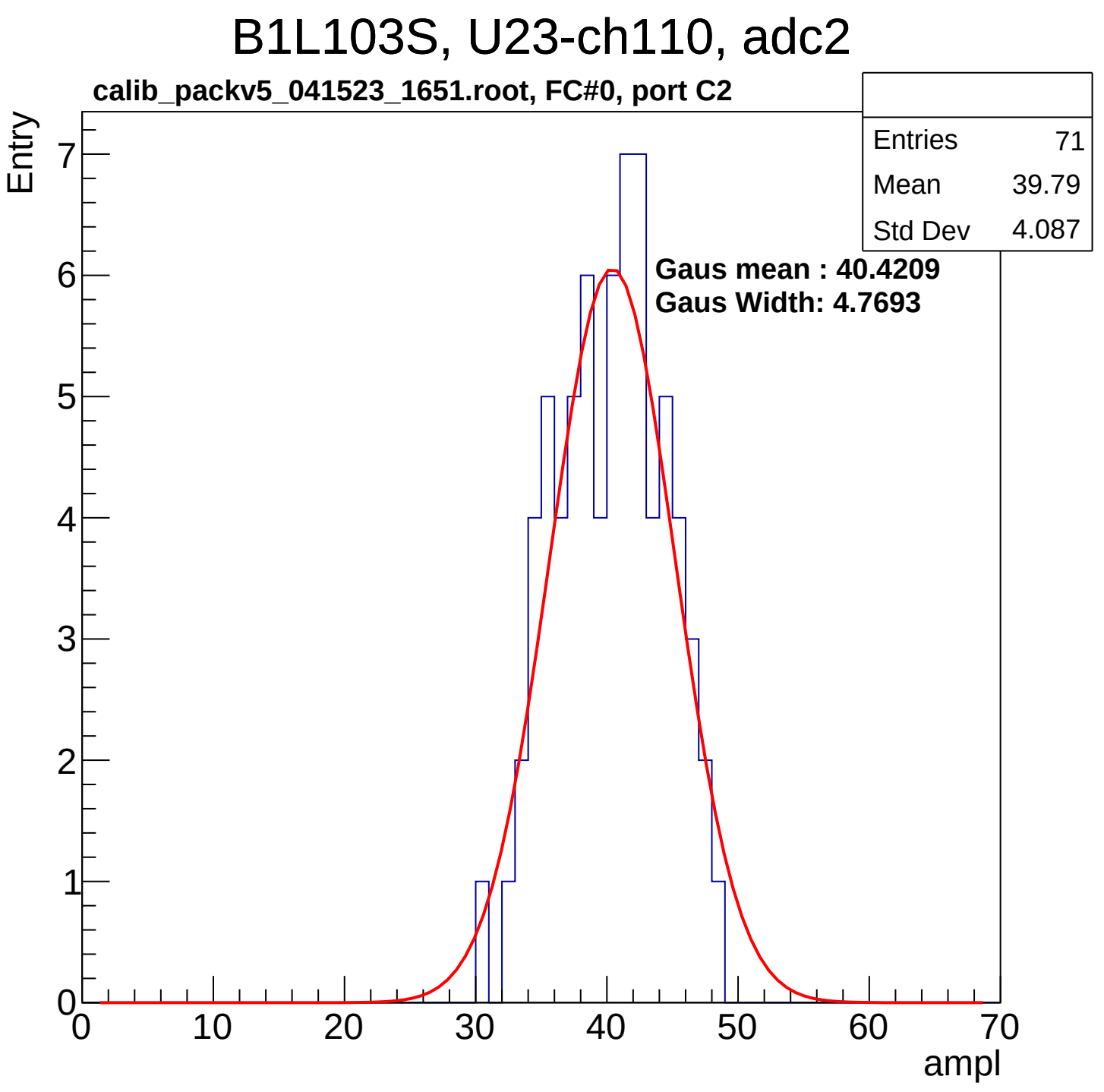

B1L103S, U23-ch109, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 58.14
Std Dev 8.618
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

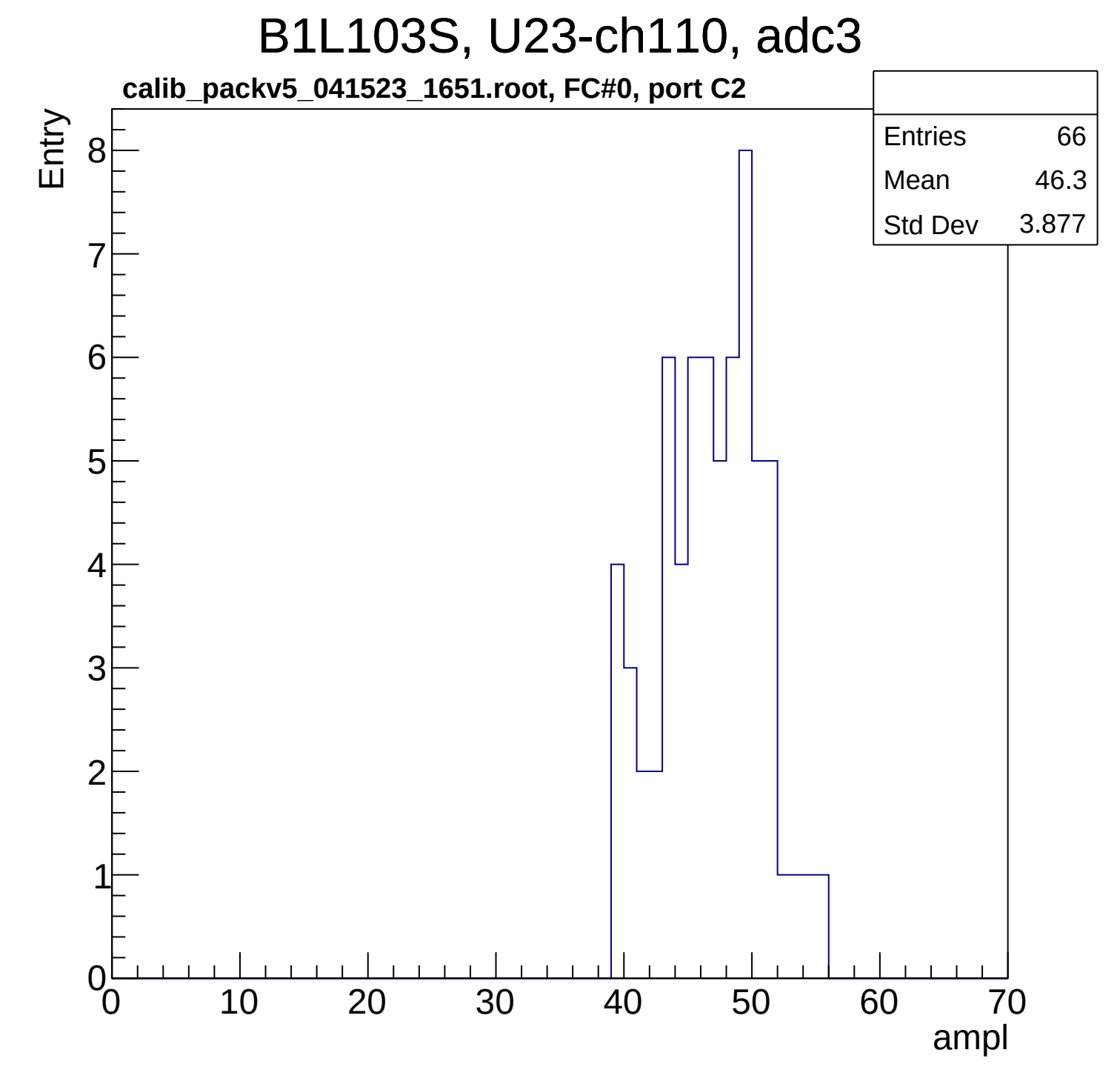

B1L103S, U23-ch109, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 13
Mean 61.15
Std Dev 1.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

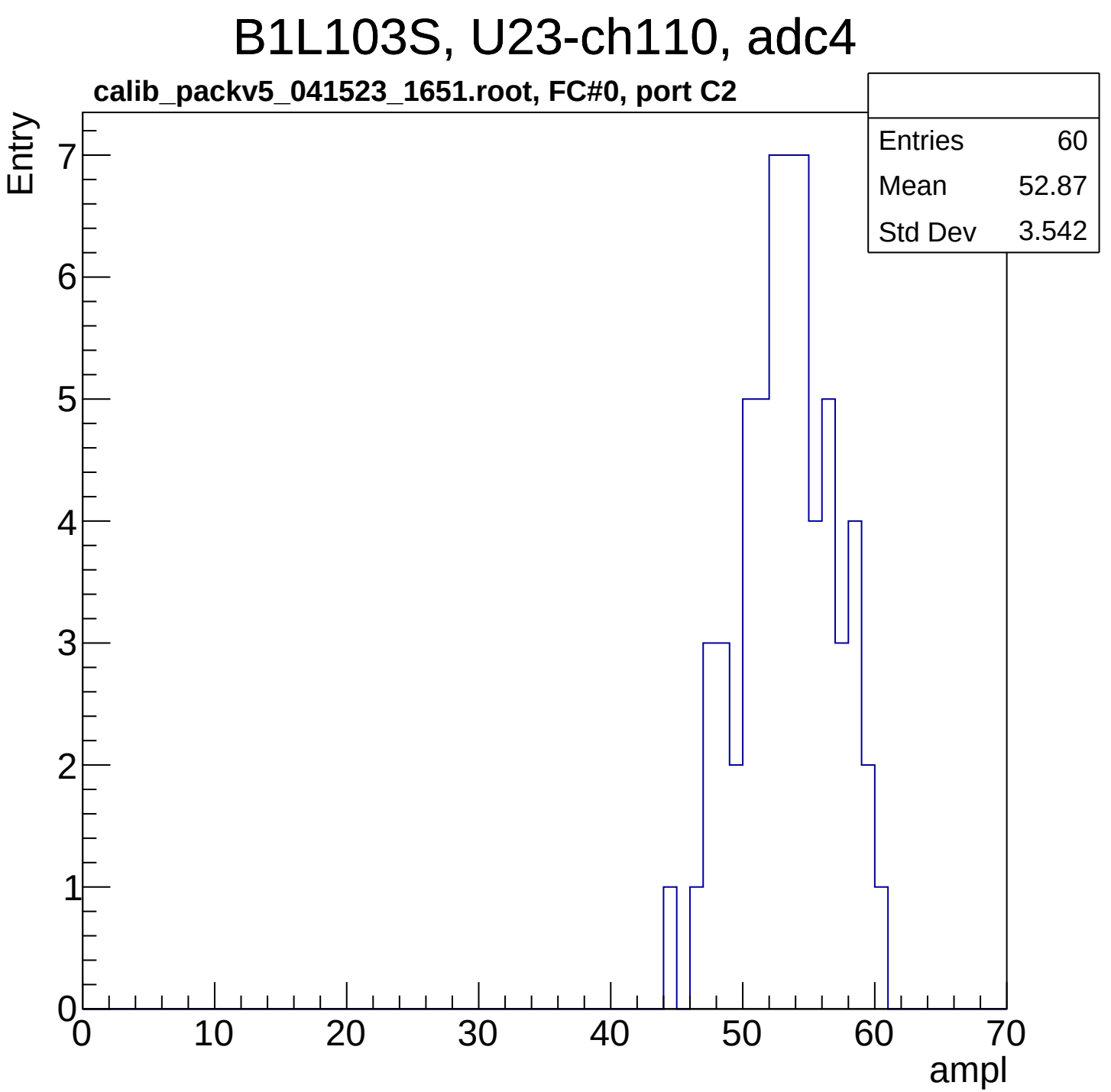

B1L103S, U23-ch109, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

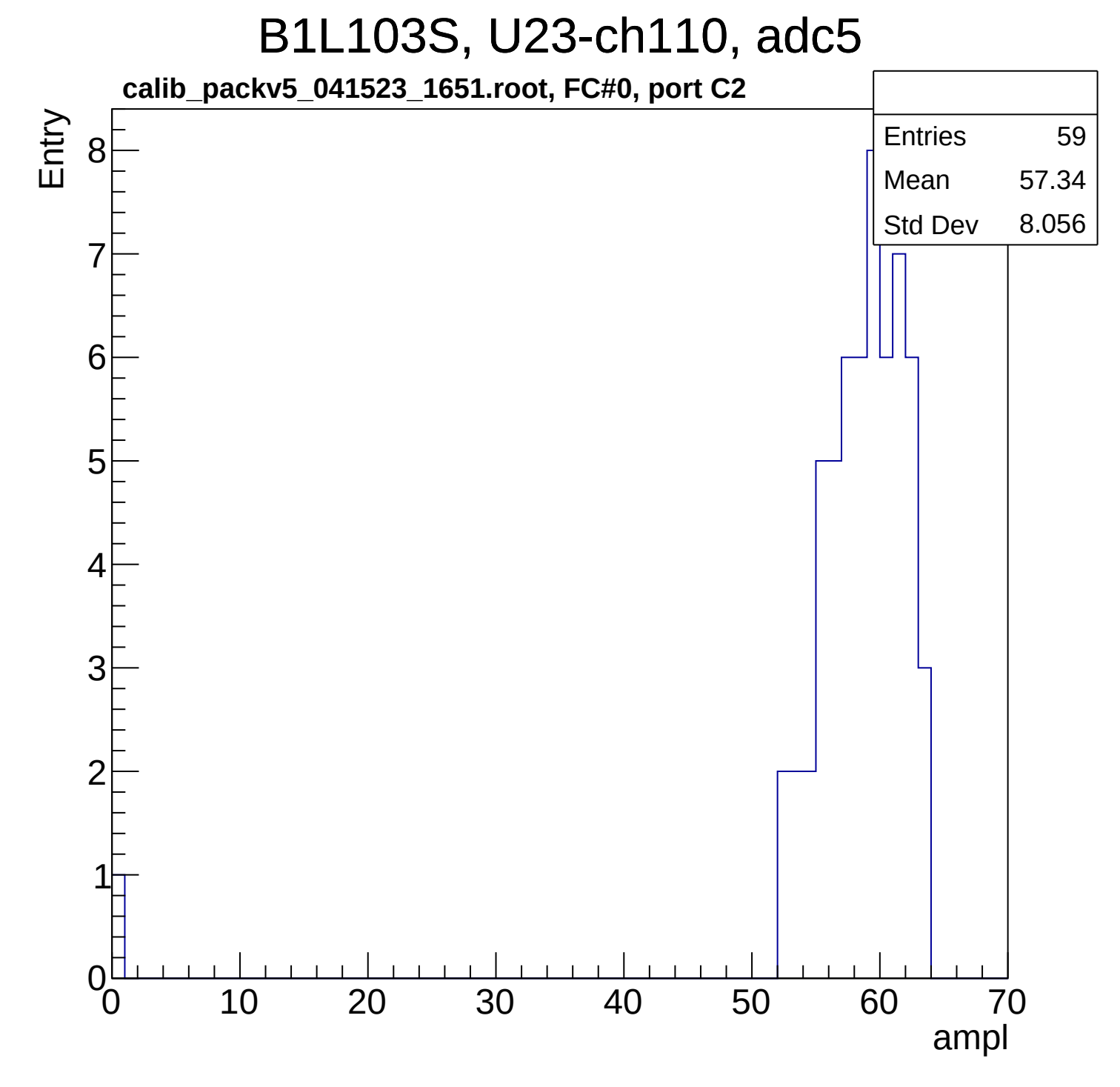

B1L103S, U23-ch110, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 92
Mean 20.09
Std Dev 12.12
Gaus mean : 27.4258
Gaus Width: 4.6637
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
22
24
0
10
20
30
40
50
60
70

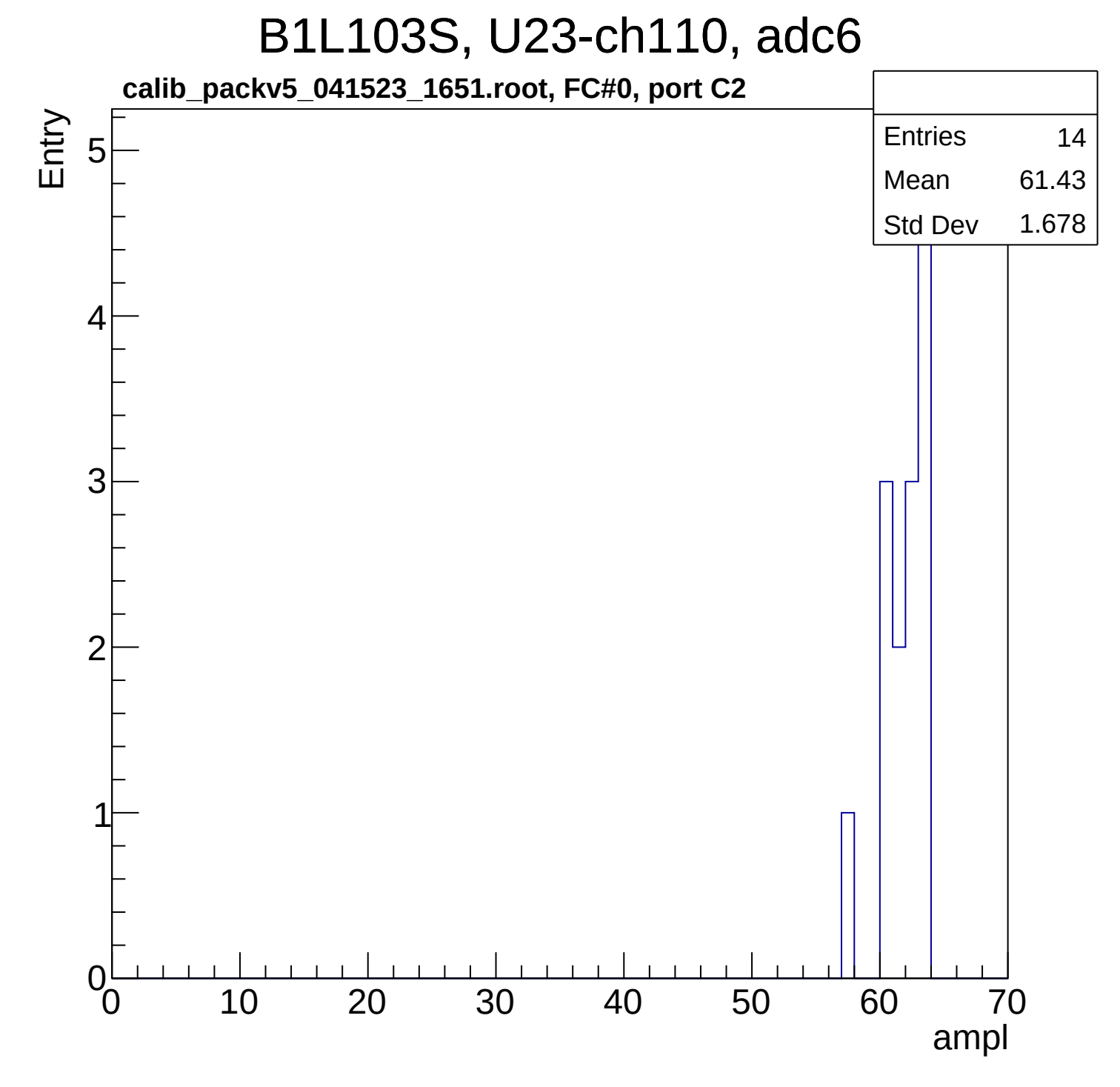

B1L103S, U23-ch110, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 27.18
Std Dev 13.39
Gaus mean : 34.0717
Gaus Width: 4.2942
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

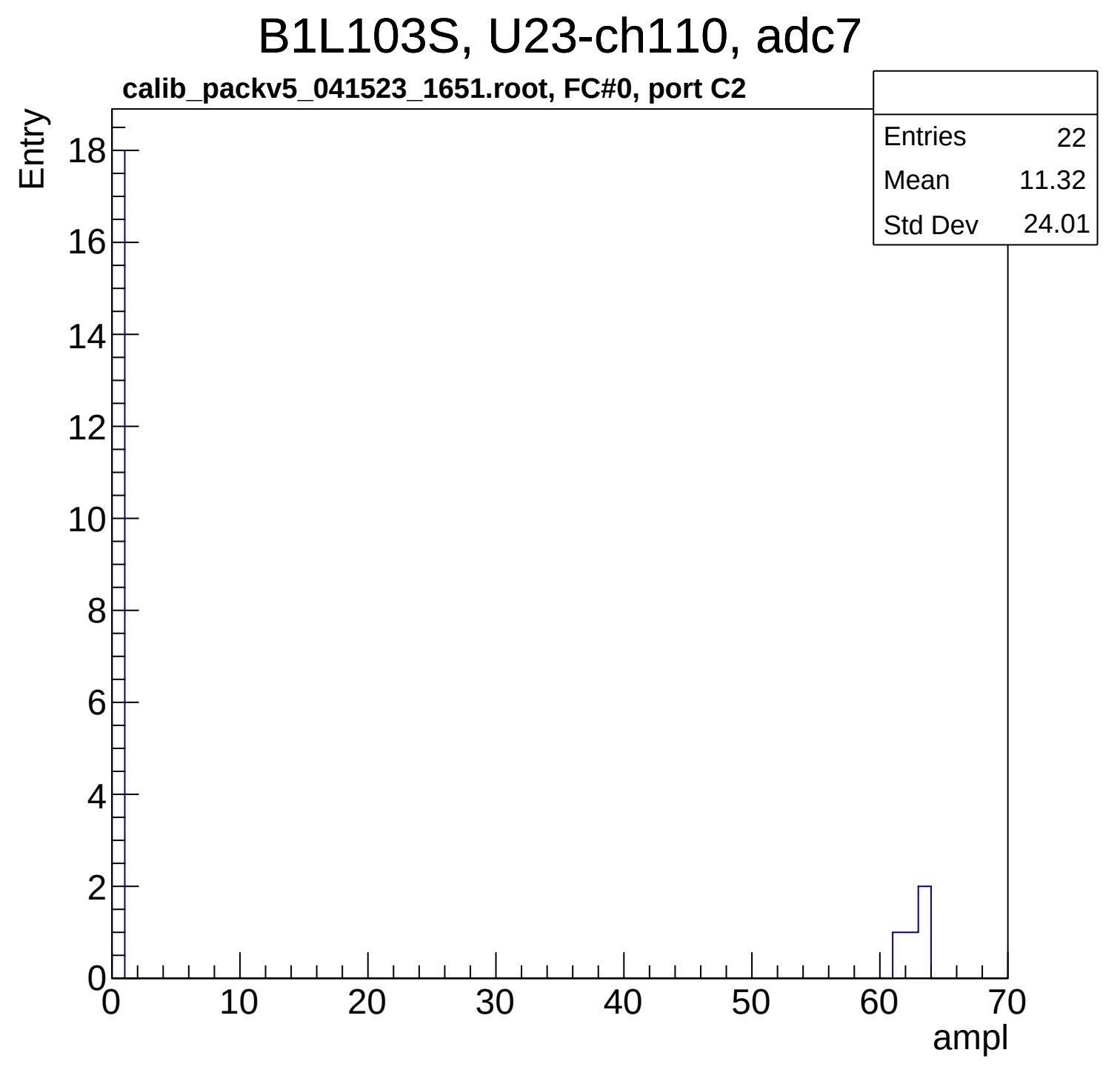

B1L103S, U23-ch110, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 39.79
Std Dev 4.087
Gaus mean : 40.4209
Gaus Width: 4.7693
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

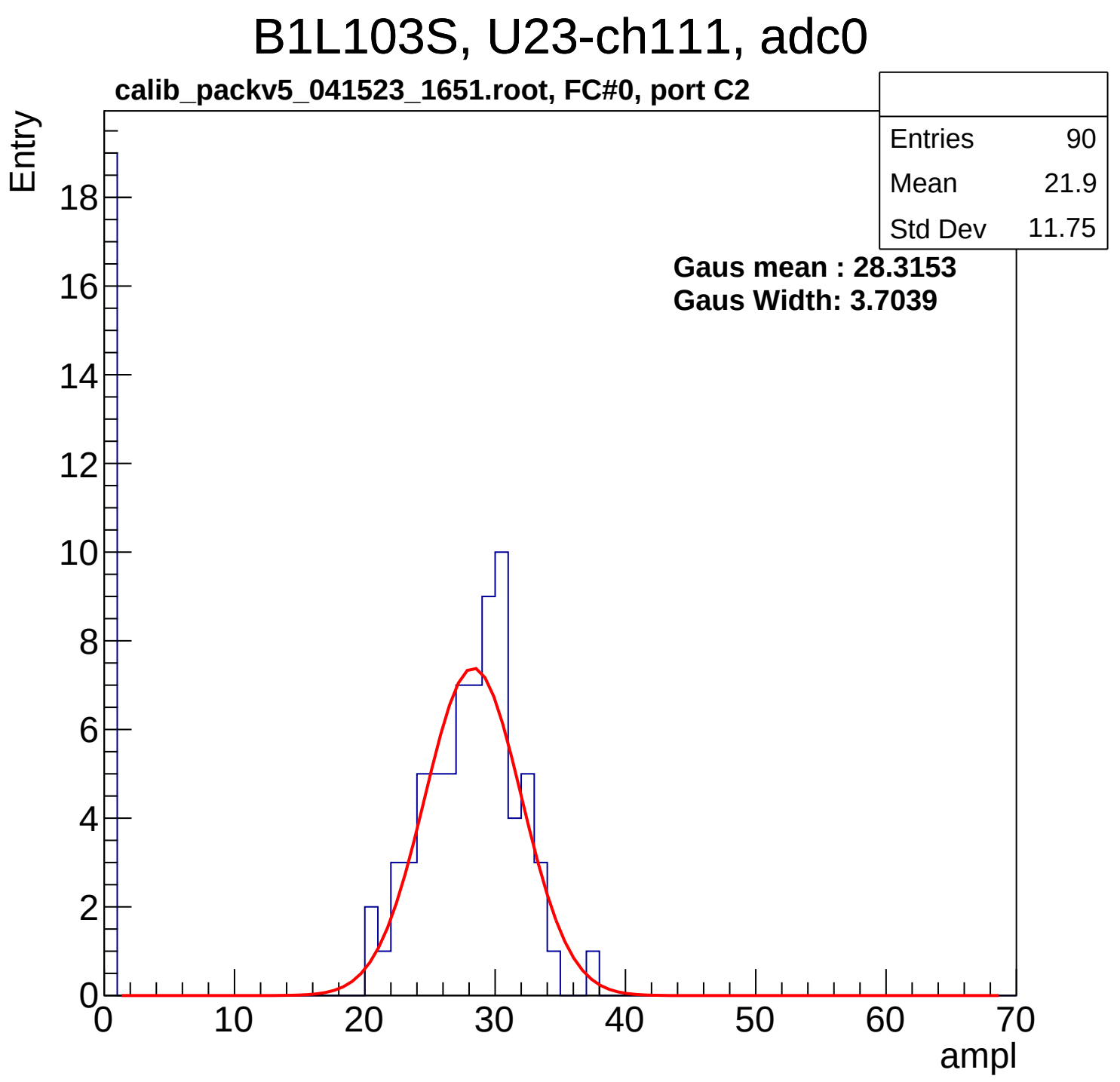

B1L103S, U23-ch110, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 46.3
Std Dev 3.877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

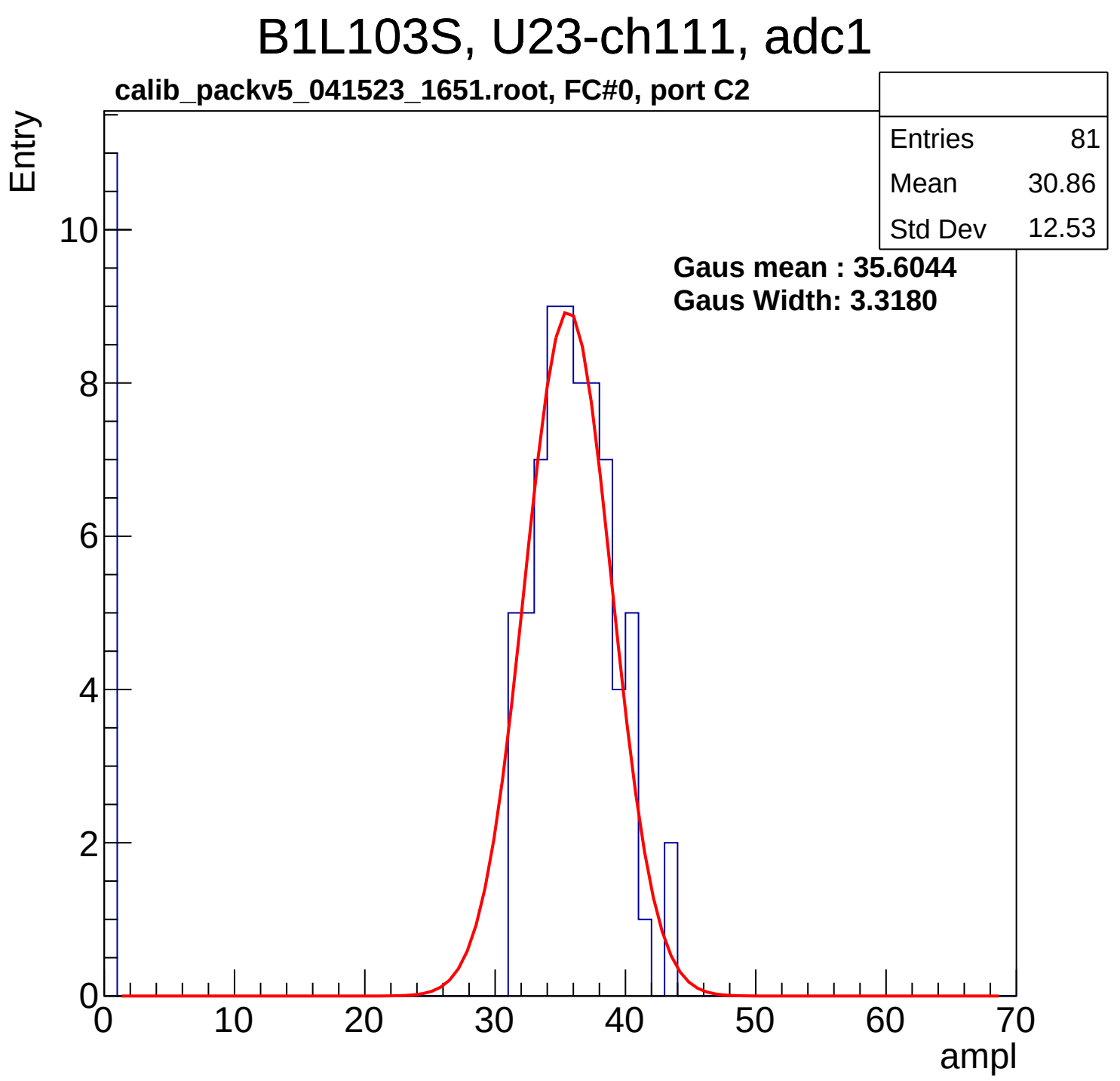

B1L103S, U23-ch110, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 52.87
Std Dev 3.542
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

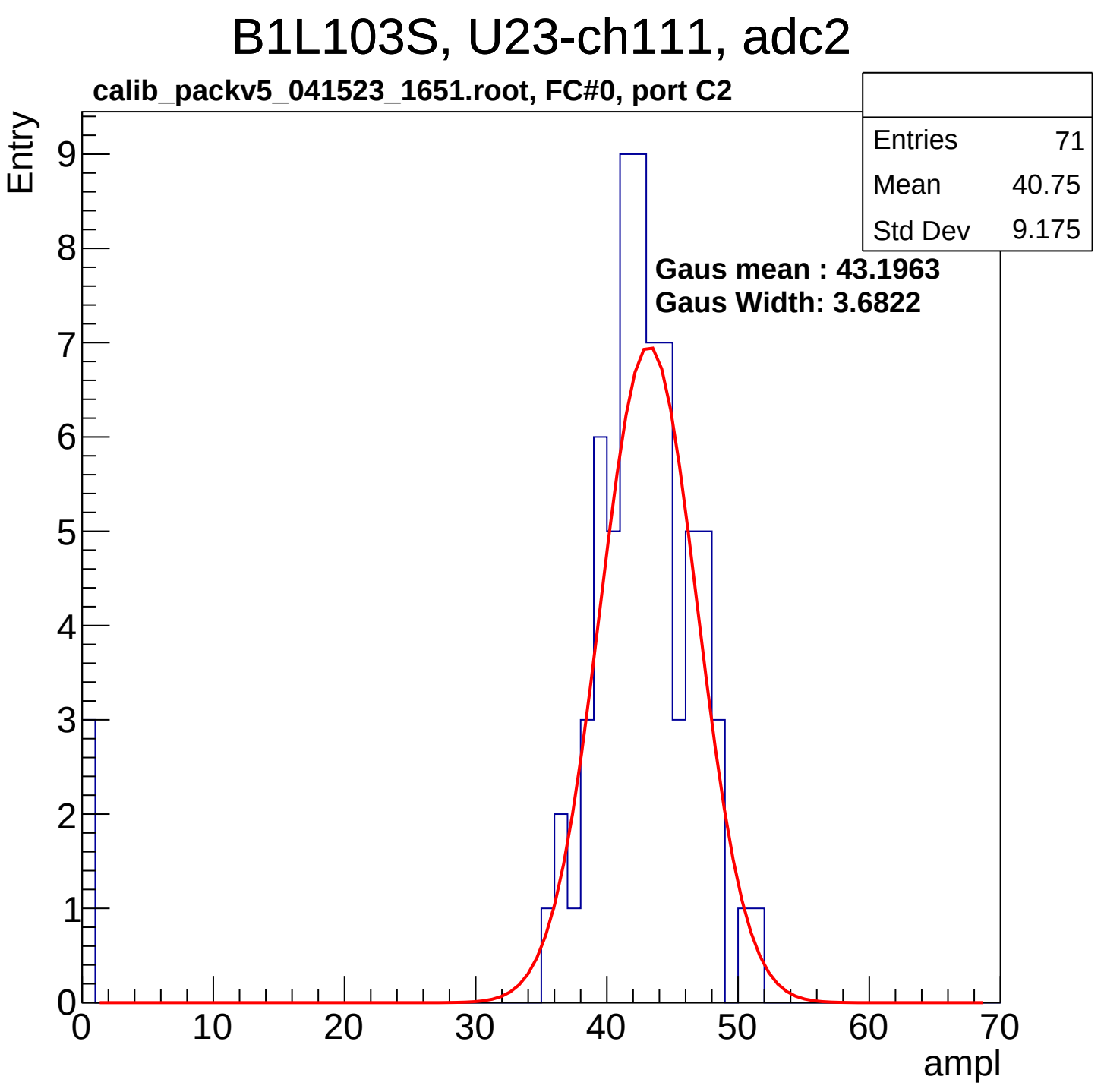

B1L103S, U23-ch110, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 57.34
Std Dev 8.056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

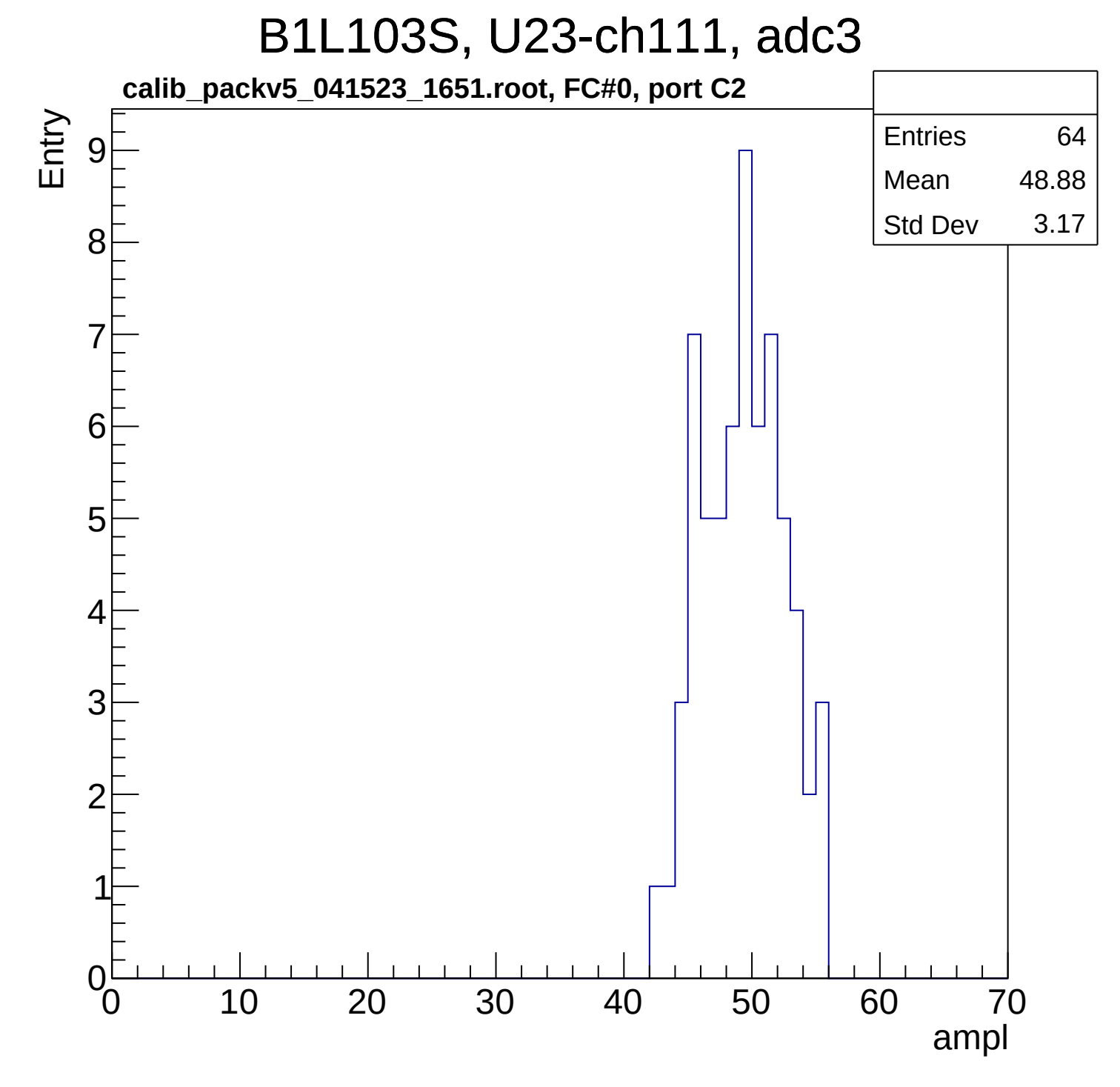

B1L103S, U23-ch110, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 14
Mean 61.43
Std Dev 1.678
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

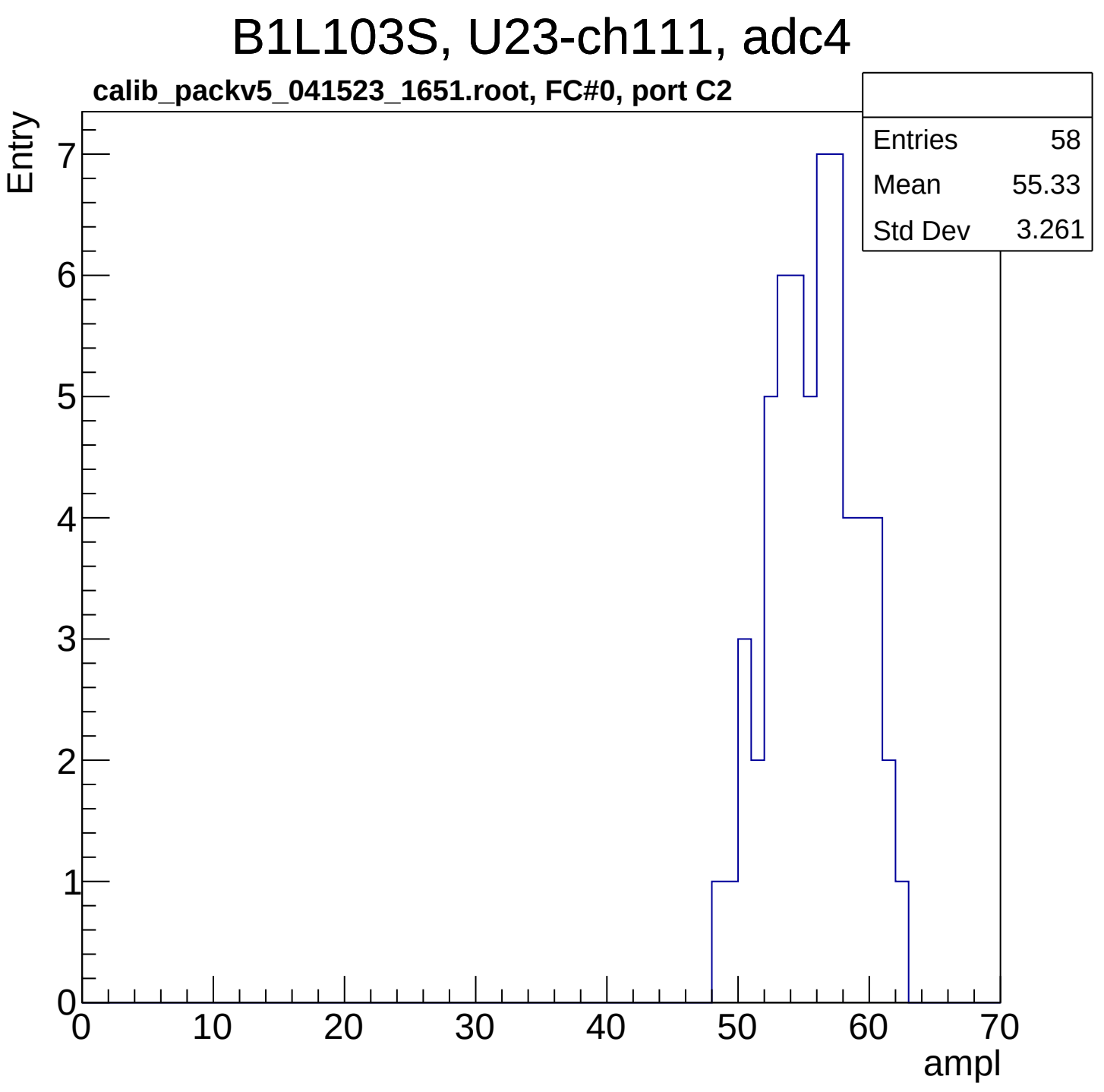

B1L103S, U23-ch110, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 22
Mean 11.32
Std Dev 24.01
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

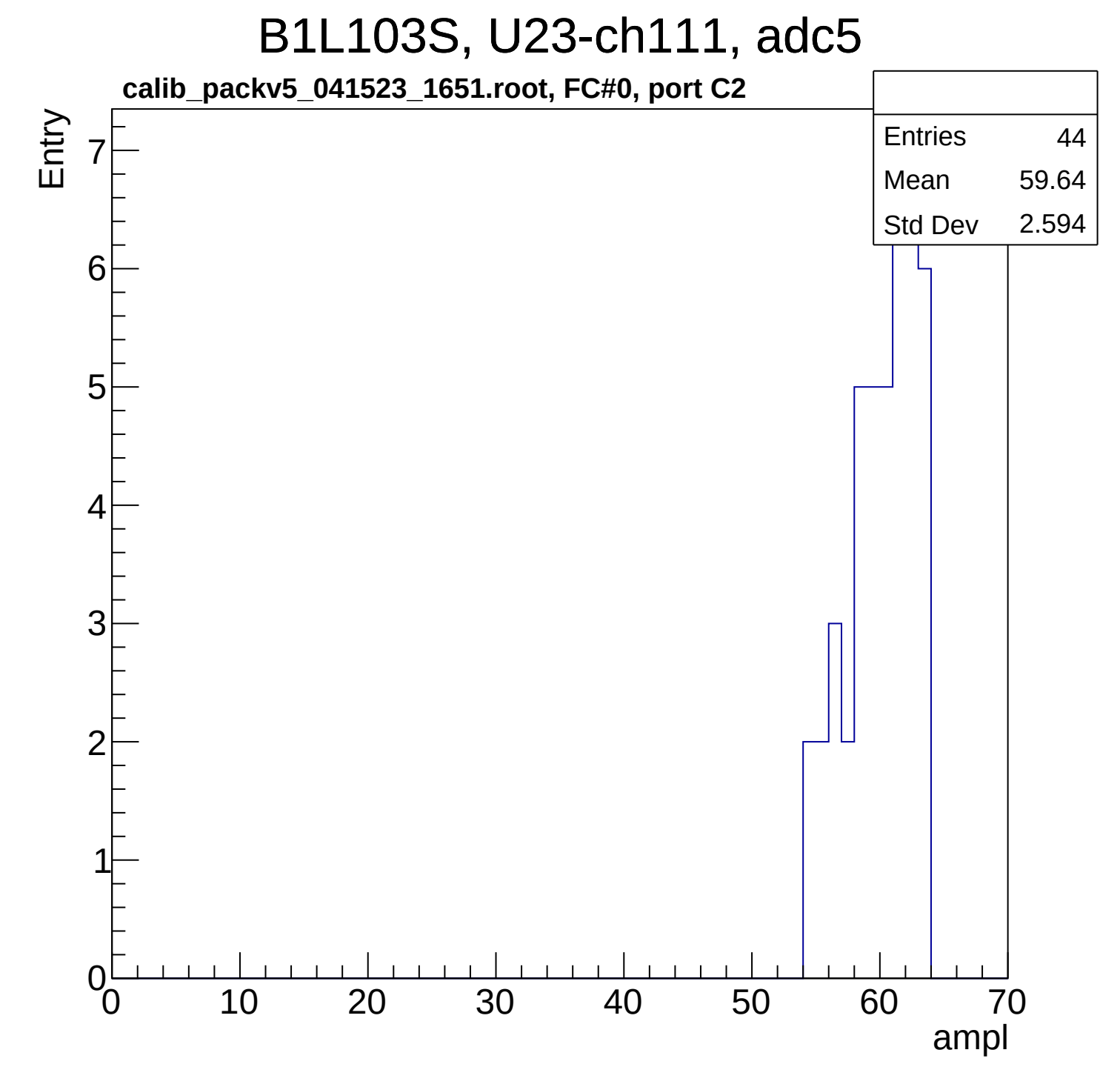

B1L103S, U23-ch111, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 90
Mean 21.9
Std Dev 11.75
Gaus mean : 28.3153
Gaus Width: 3.7039
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

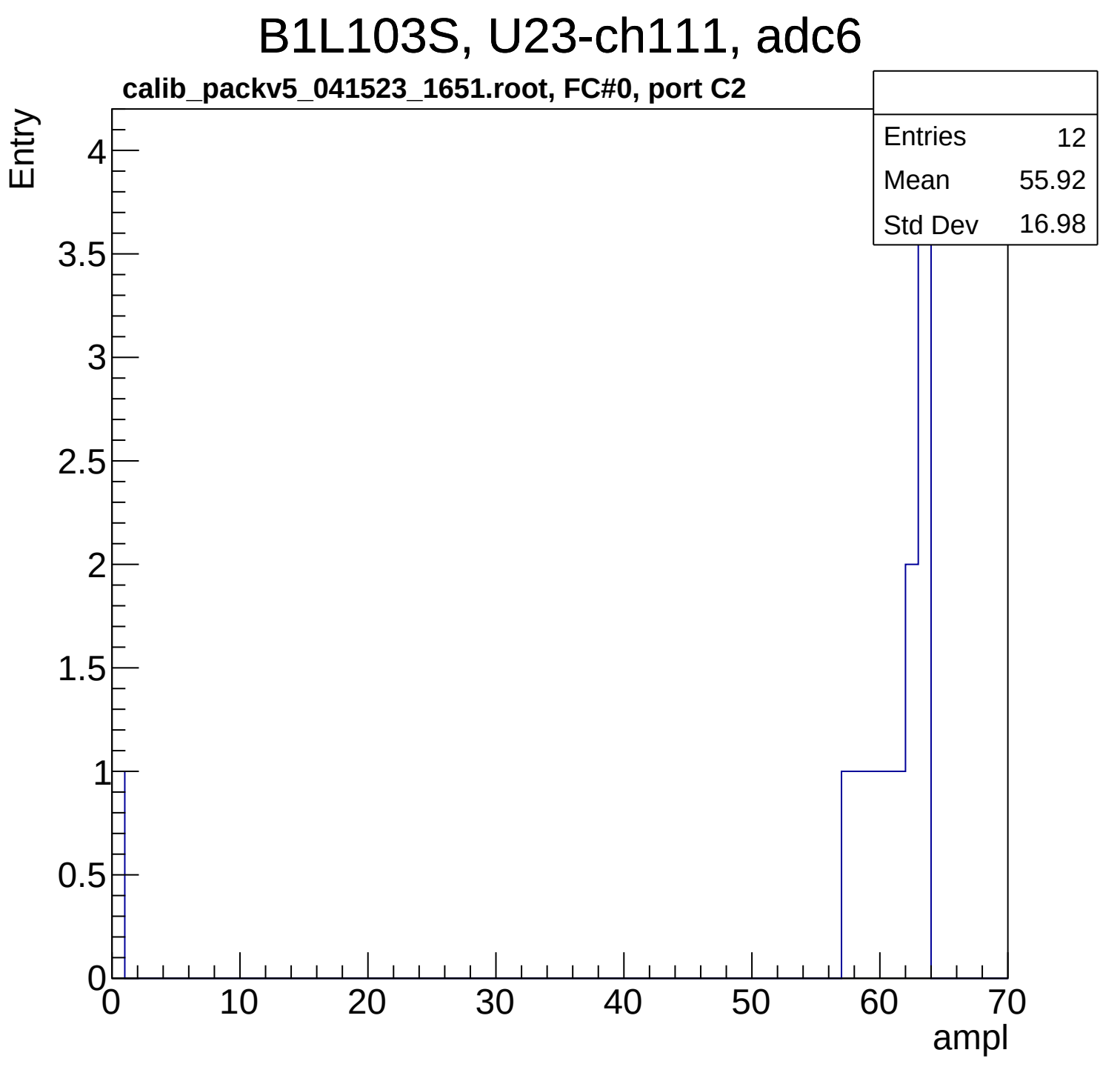

B1L103S, U23-ch111, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 30.86
Std Dev 12.53
Gaus mean : 35.6044
Gaus Width: 3.3180
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

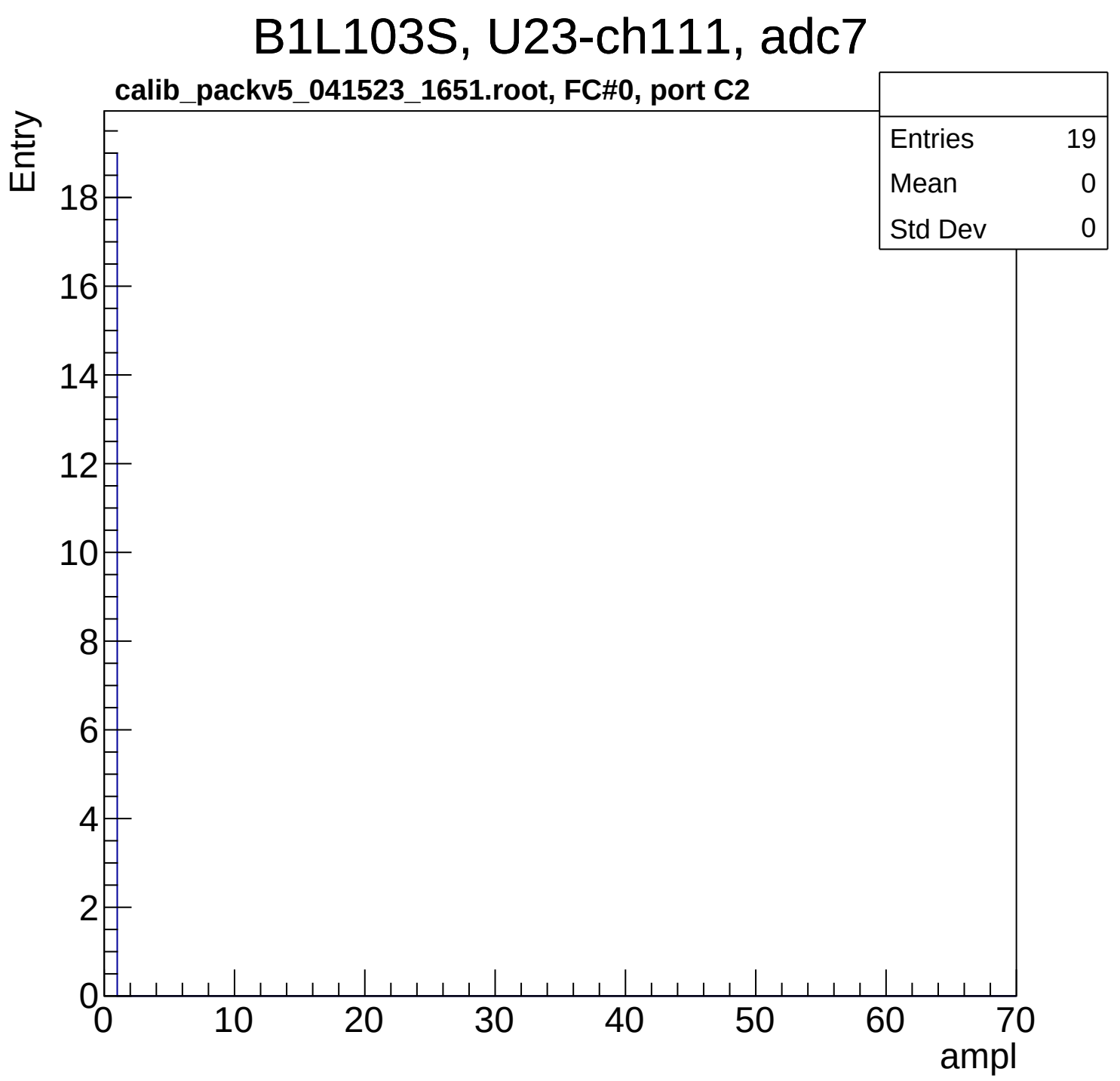

B1L103S, U23-ch111, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 40.75
Std Dev 9.175
Gaus mean : 43.1963
Gaus Width: 3.6822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

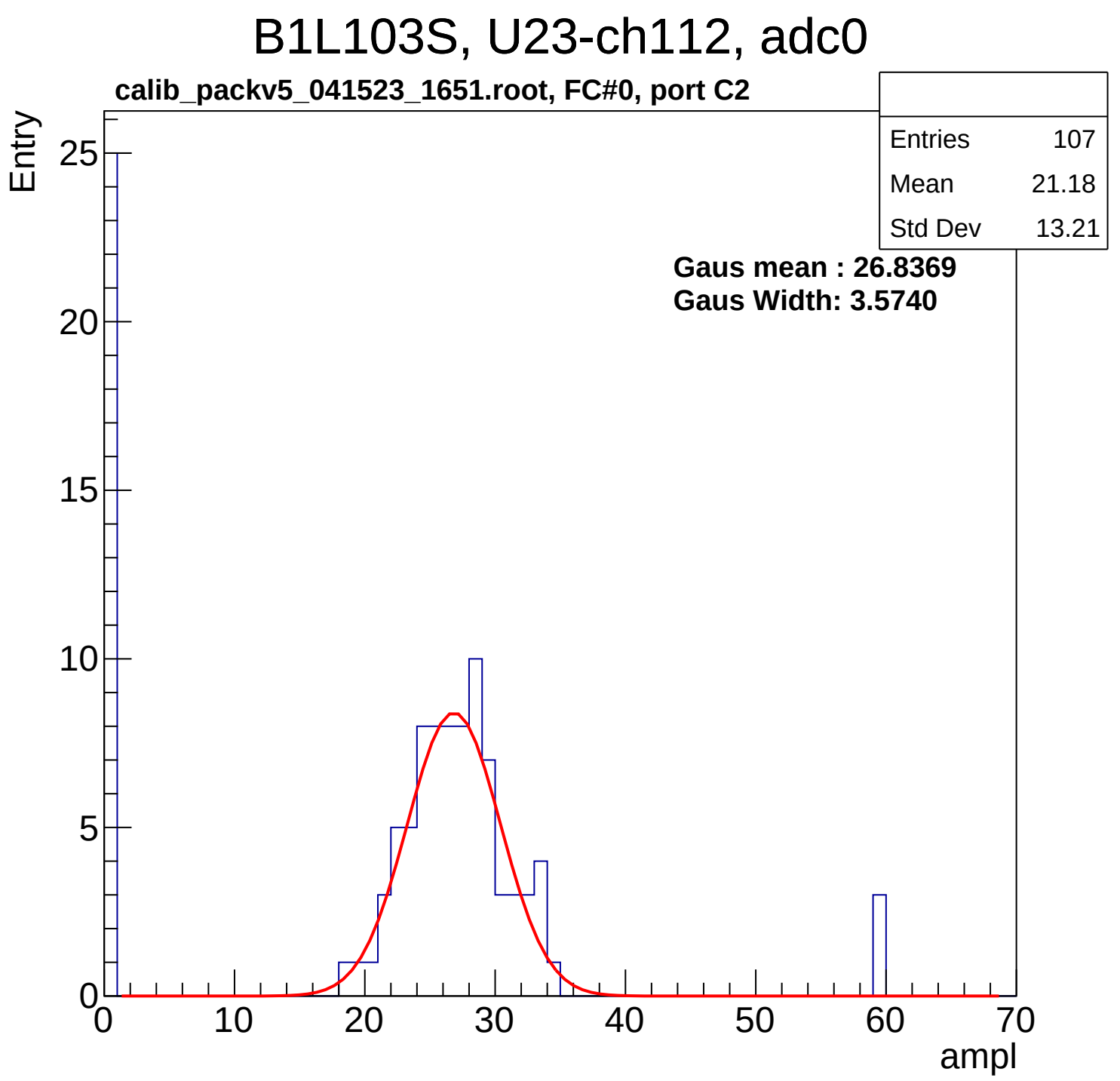

B1L103S, U23-ch111, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 48.88
Std Dev 3.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

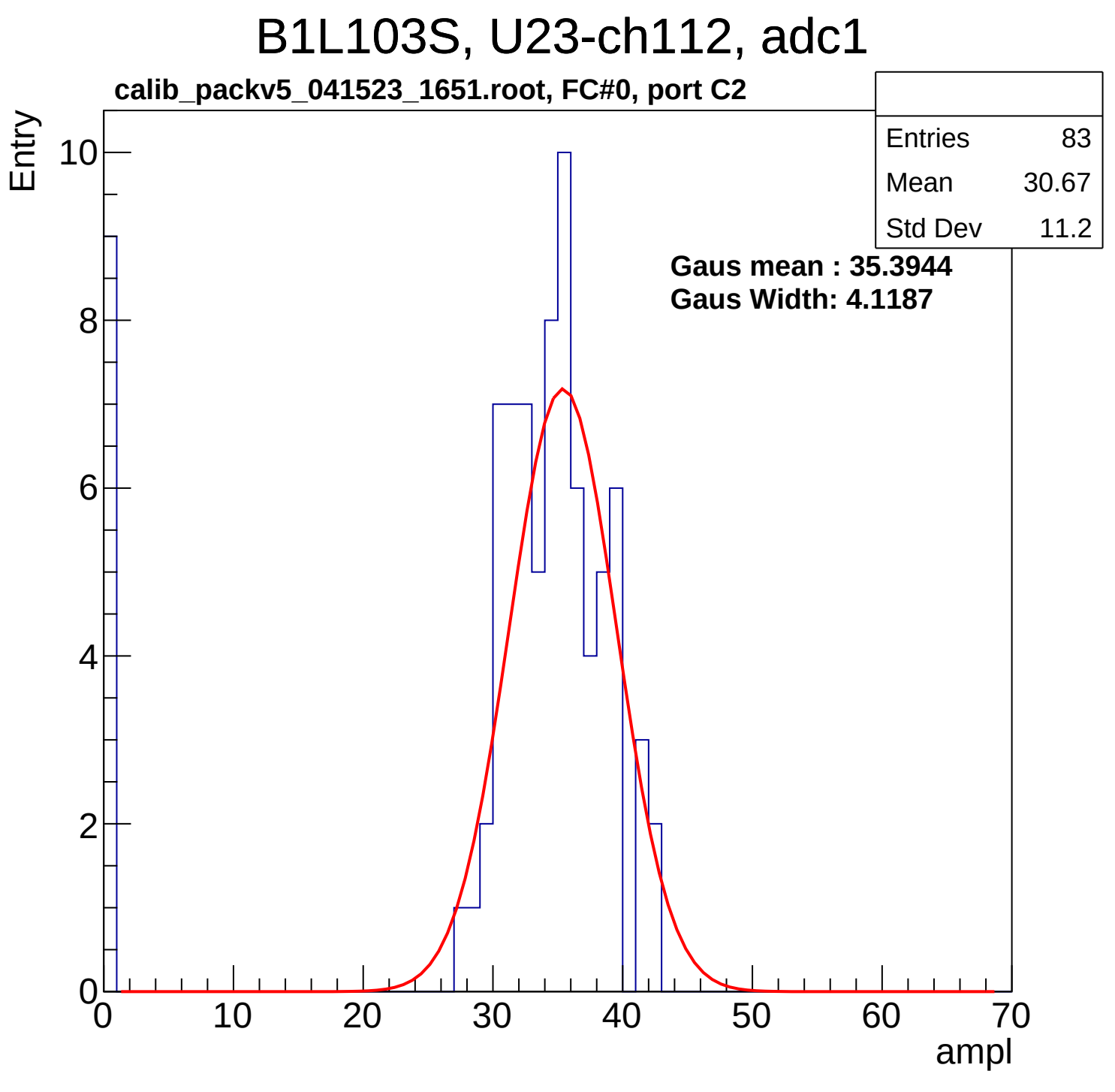

B1L103S, U23-ch111, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 55.33
Std Dev 3.261
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

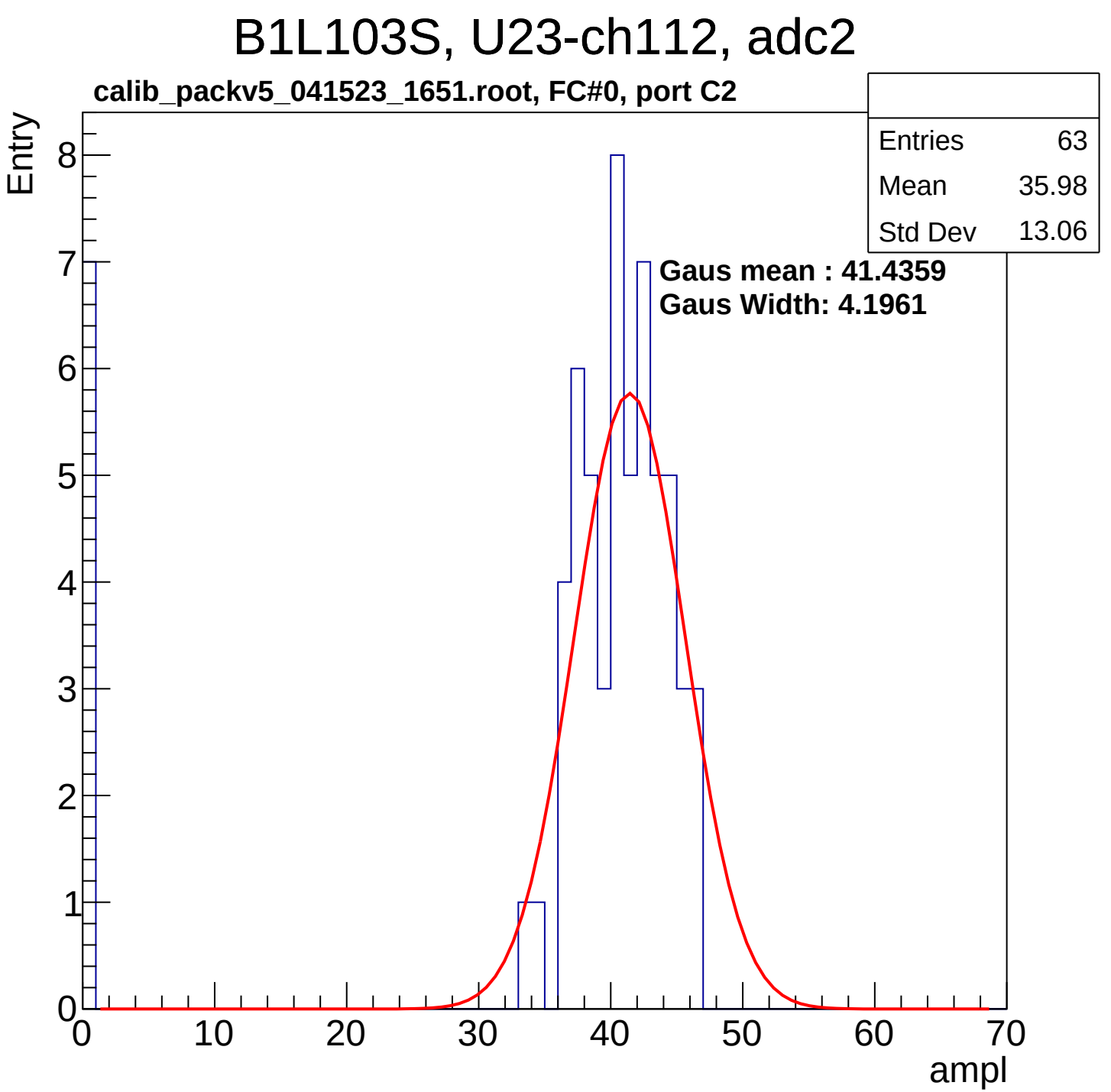

B1L103S, U23-ch111, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 44
Mean 59.64
Std Dev 2.594
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

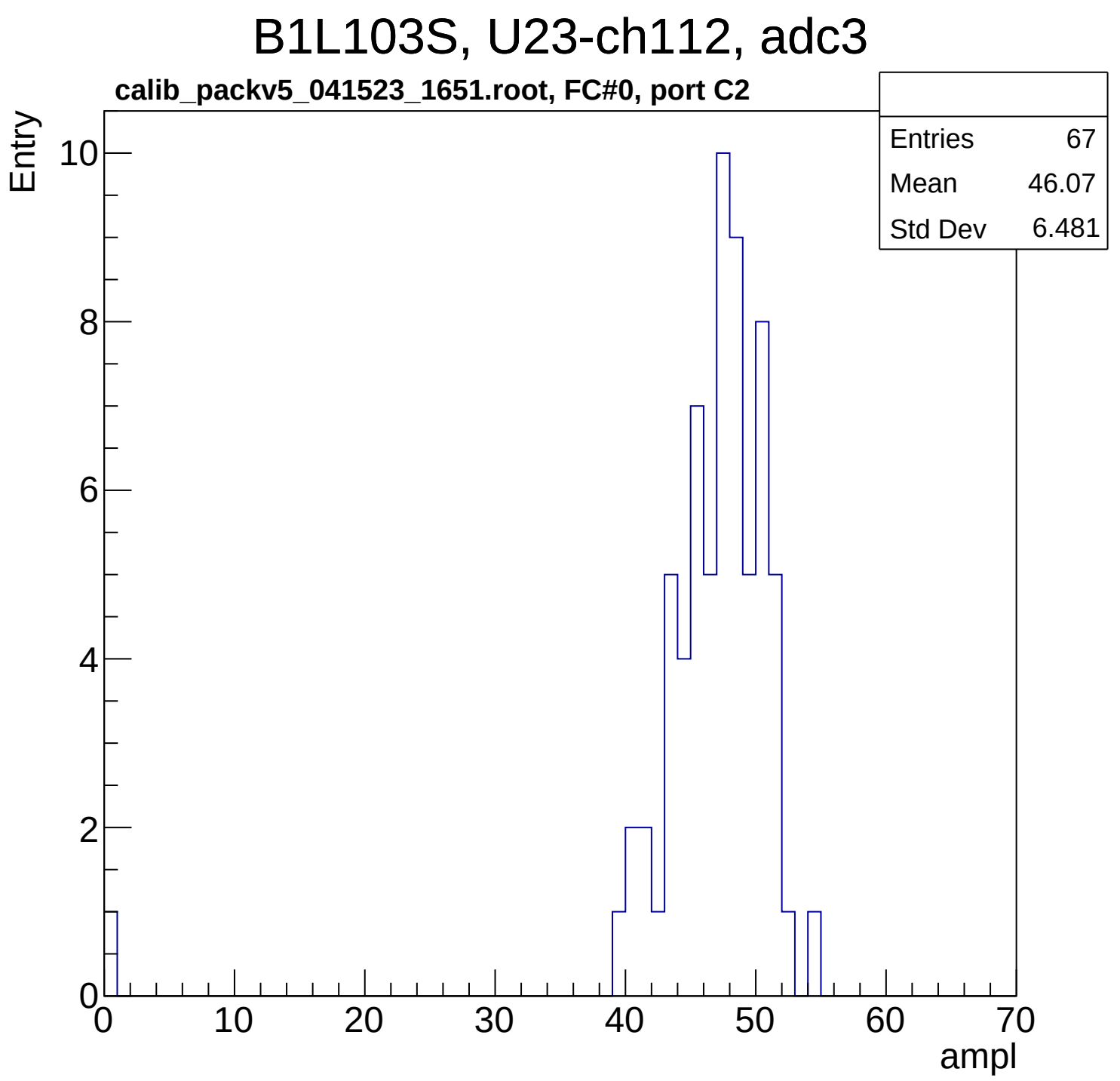

B1L103S, U23-ch111, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 12
Mean 55.92
Std Dev 16.98
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

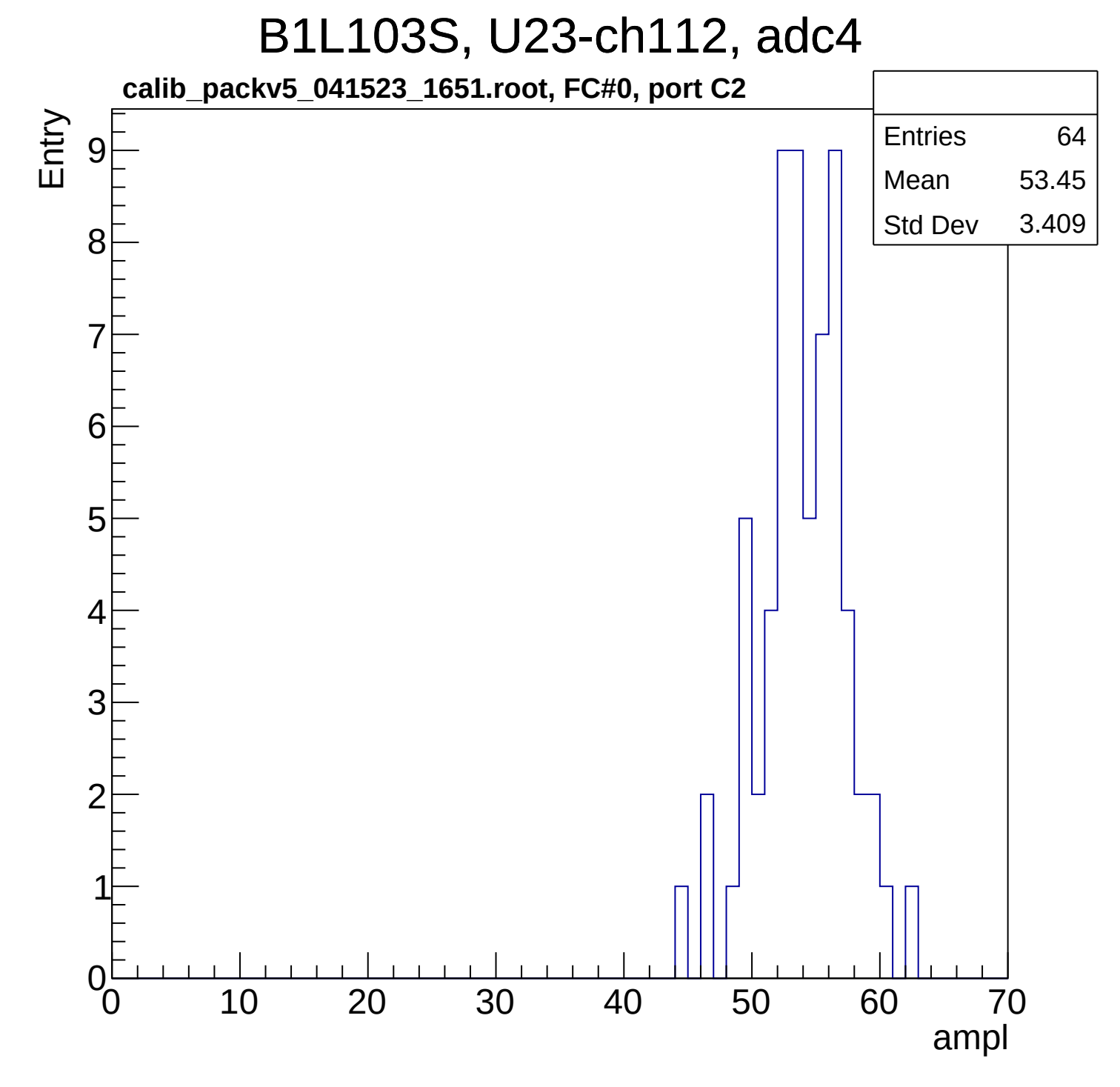

B1L103S, U23-ch111, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

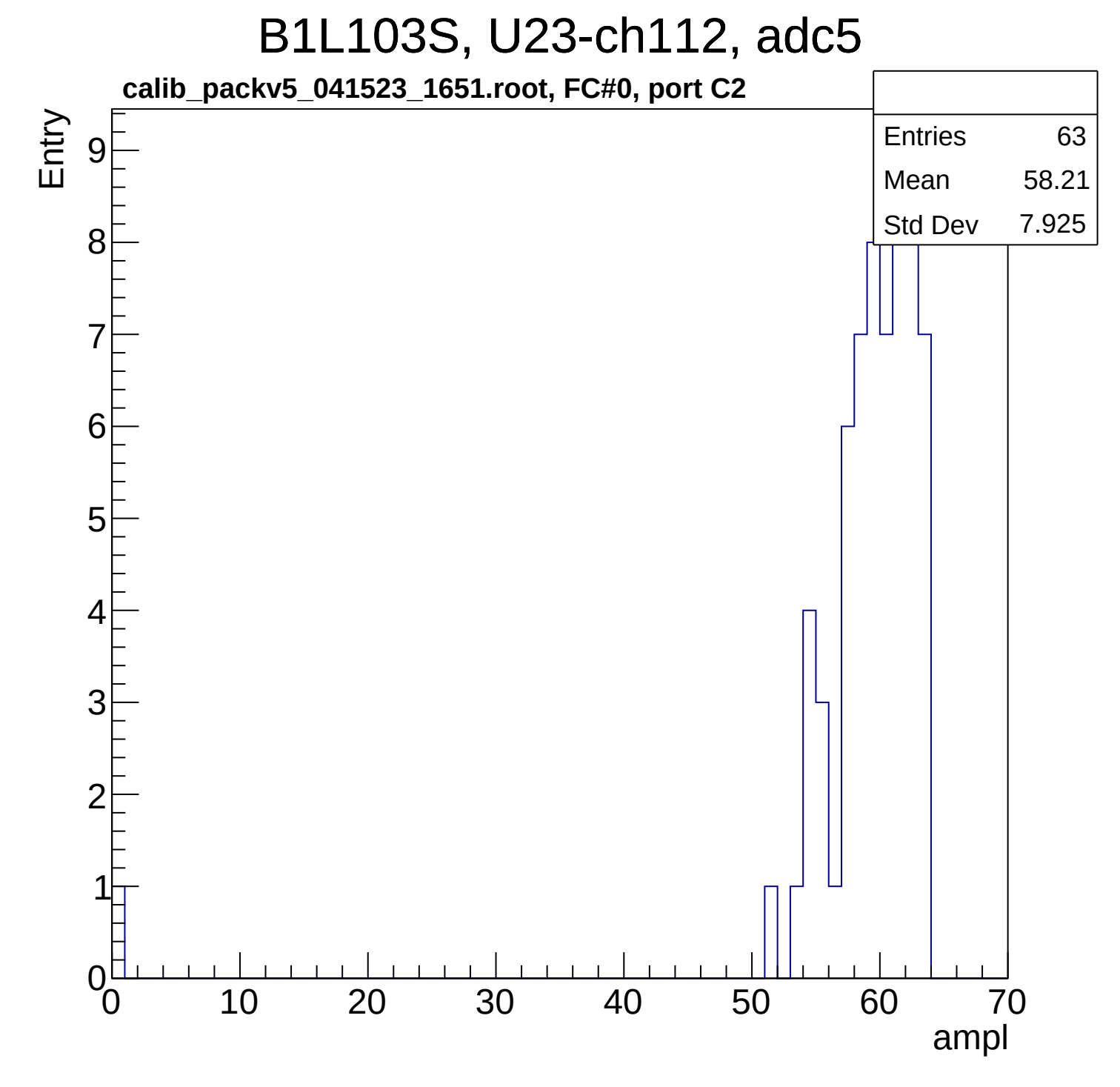

B1L103S, U23-ch112, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 107
Mean 21.18
Std Dev 13.21
Gaus mean : 26.8369
Gaus Width: 3.5740
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

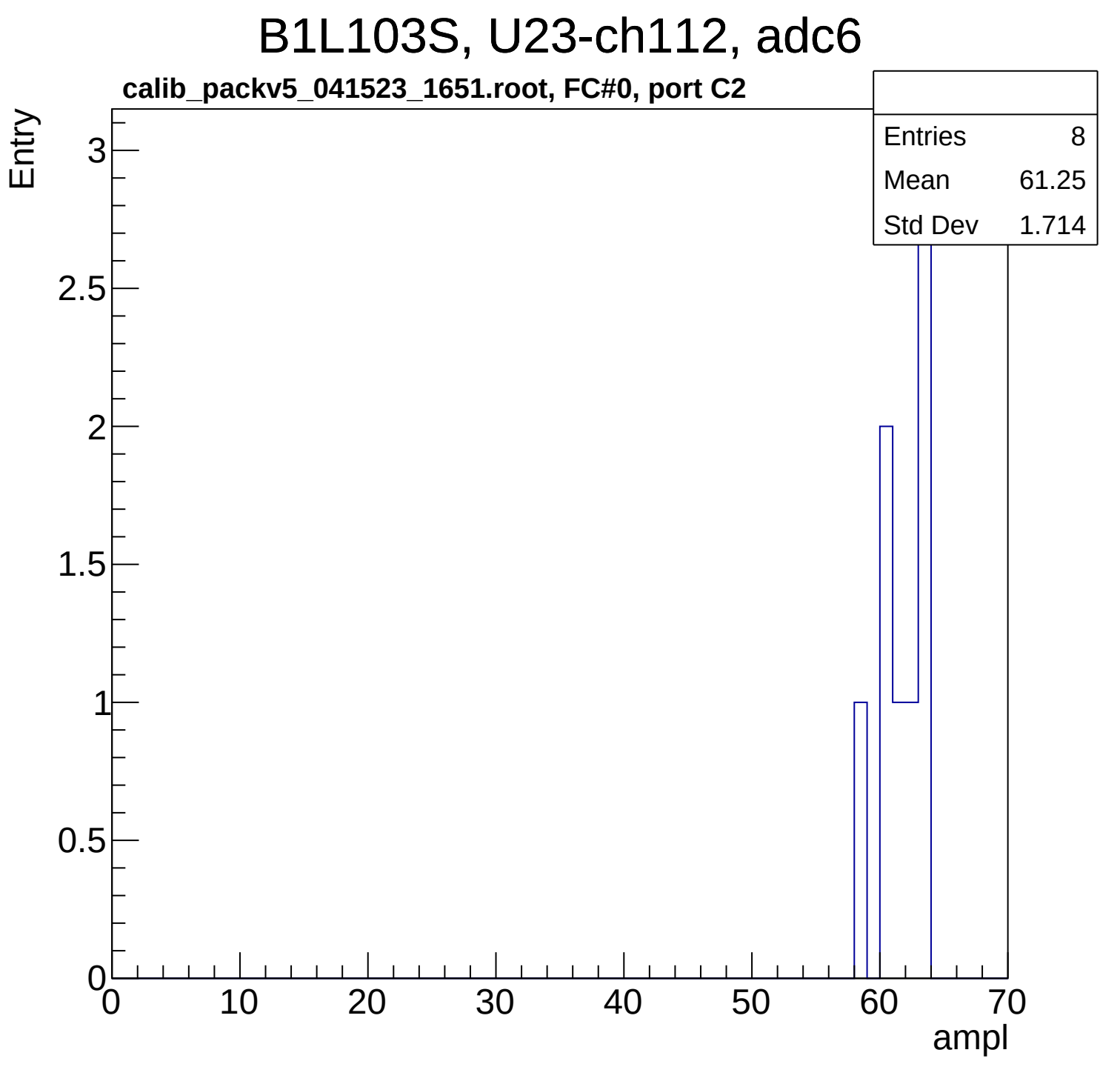

B1L103S, U23-ch112, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 83
Mean 30.67
Std Dev 11.2
Gaus mean : 35.3944
Gaus Width: 4.1187
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

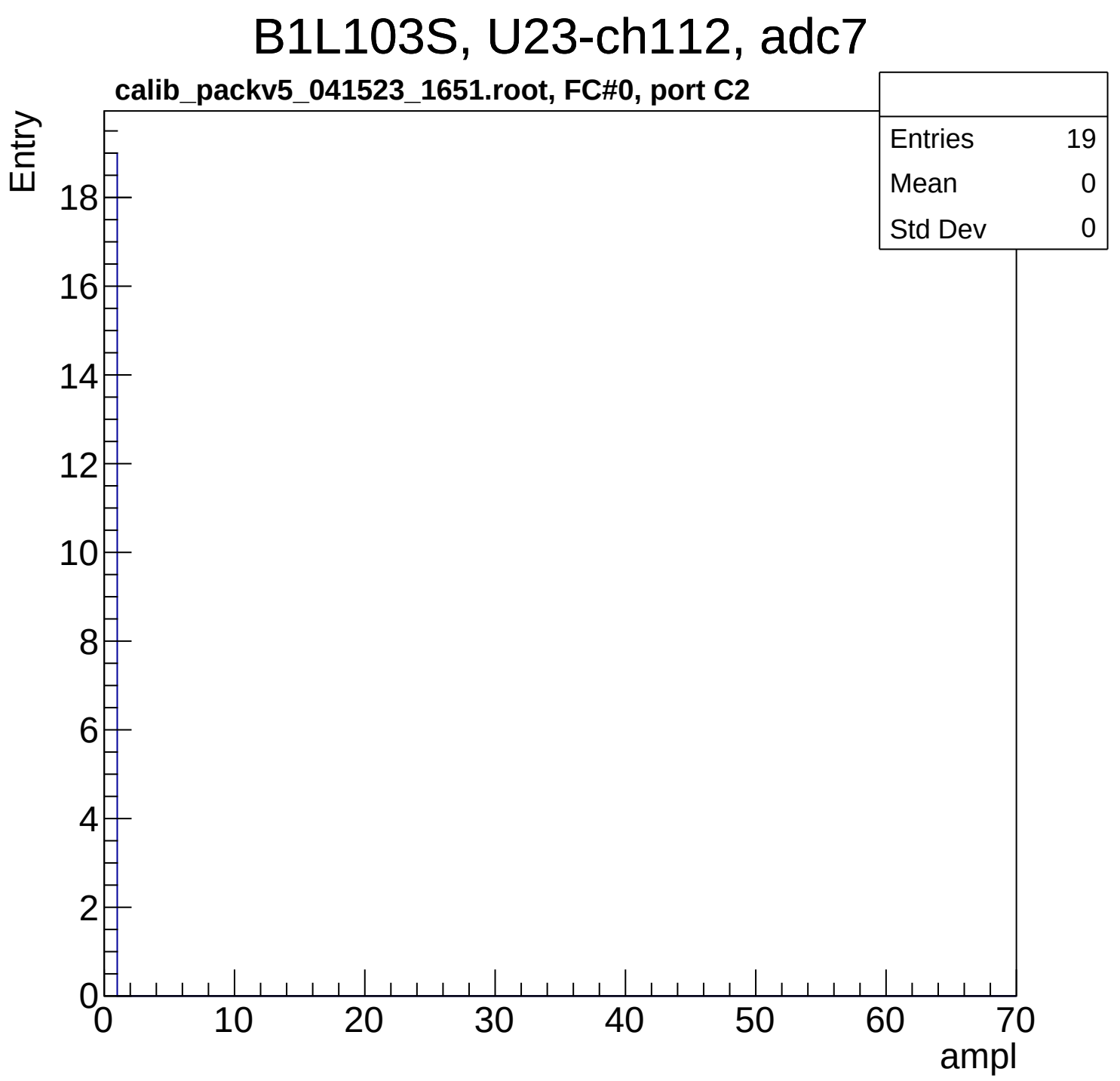

B1L103S, U23-ch112, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 35.98
Std Dev 13.06
Gaus mean : 41.4359
Gaus Width: 4.1961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

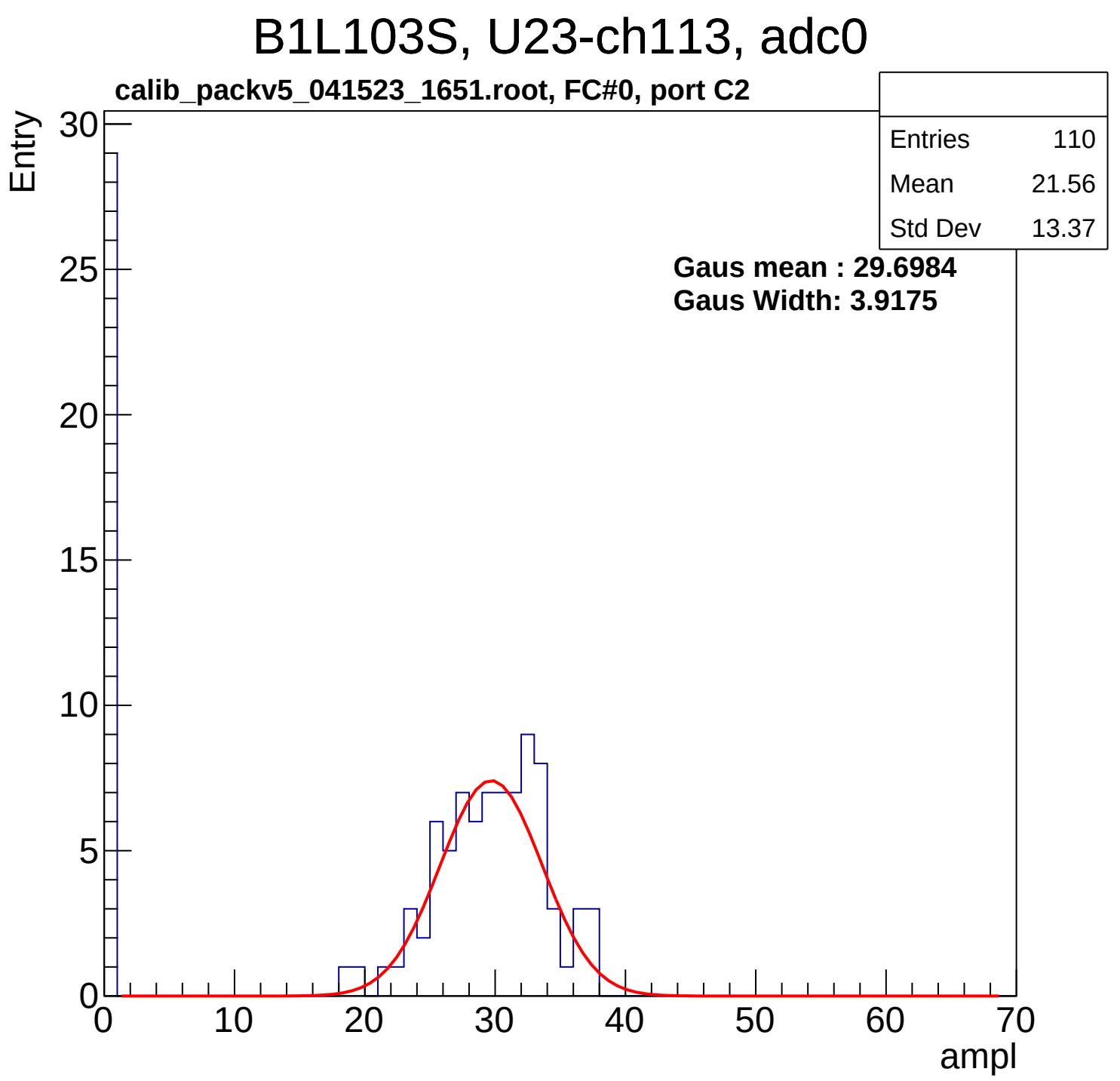

B1L103S, U23-ch112, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 46.07
Std Dev 6.481
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

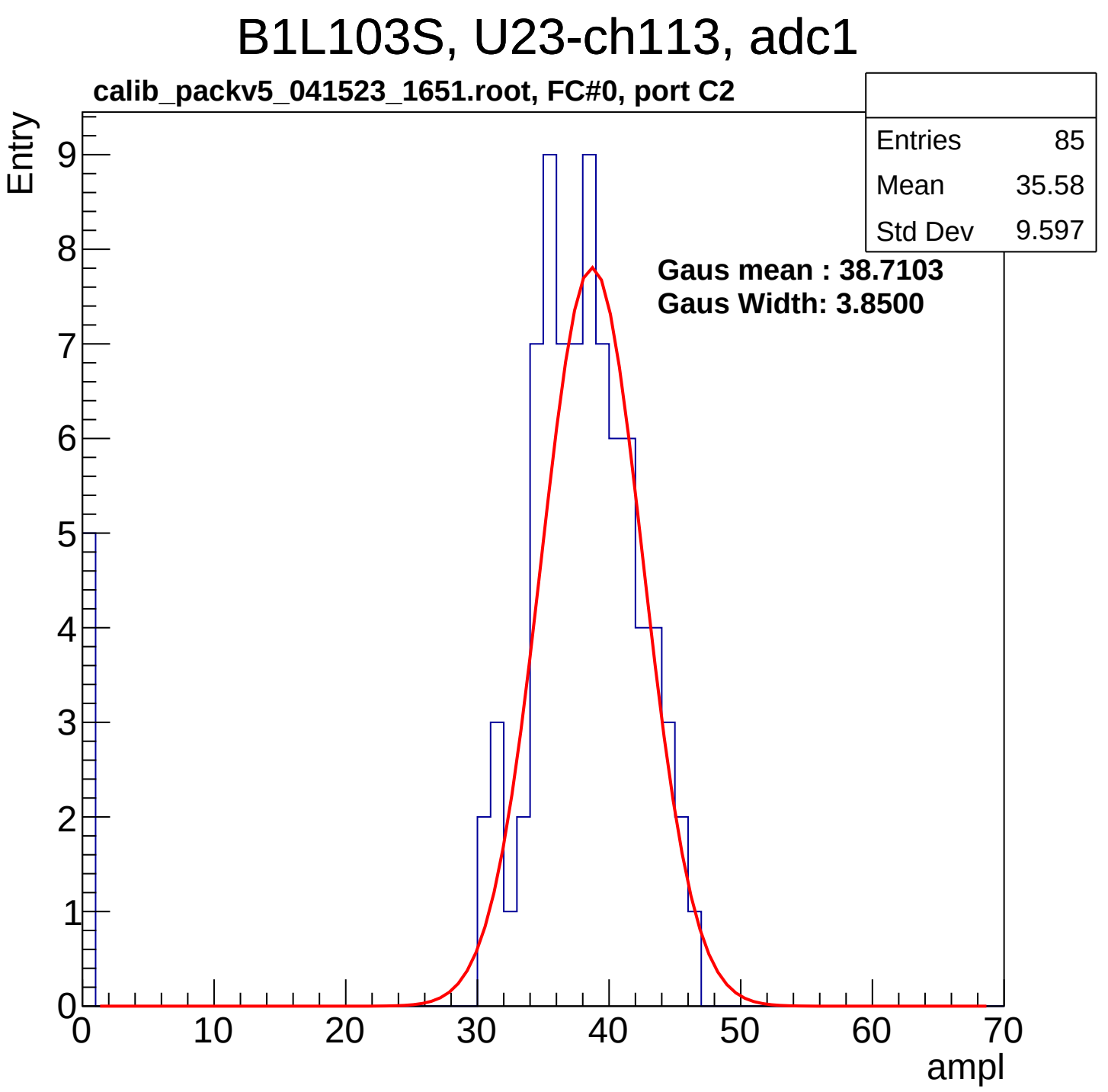

B1L103S, U23-ch112, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 53.45
Std Dev 3.409
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

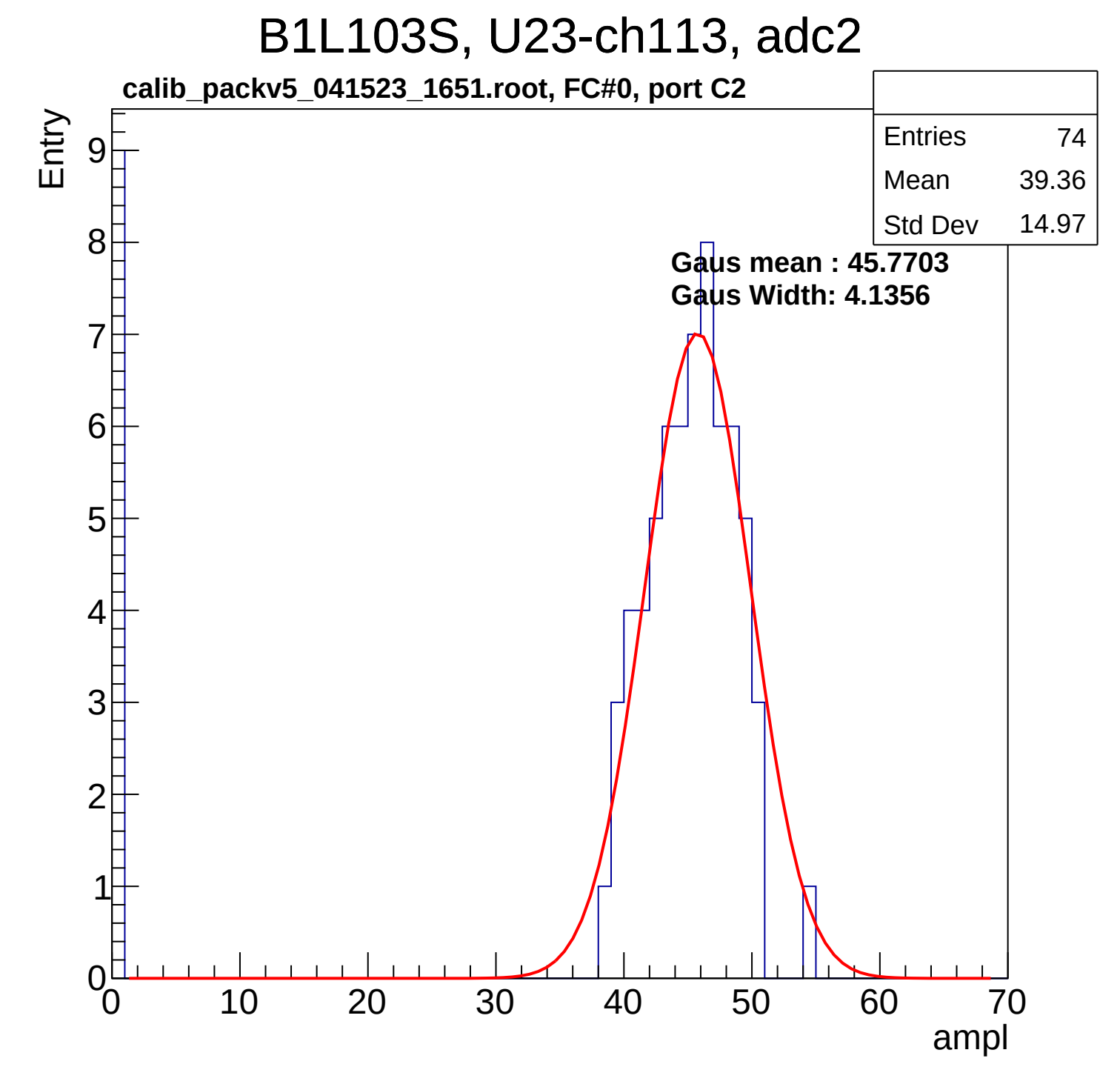

B1L103S, U23-ch112, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 58.21
Std Dev 7.925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

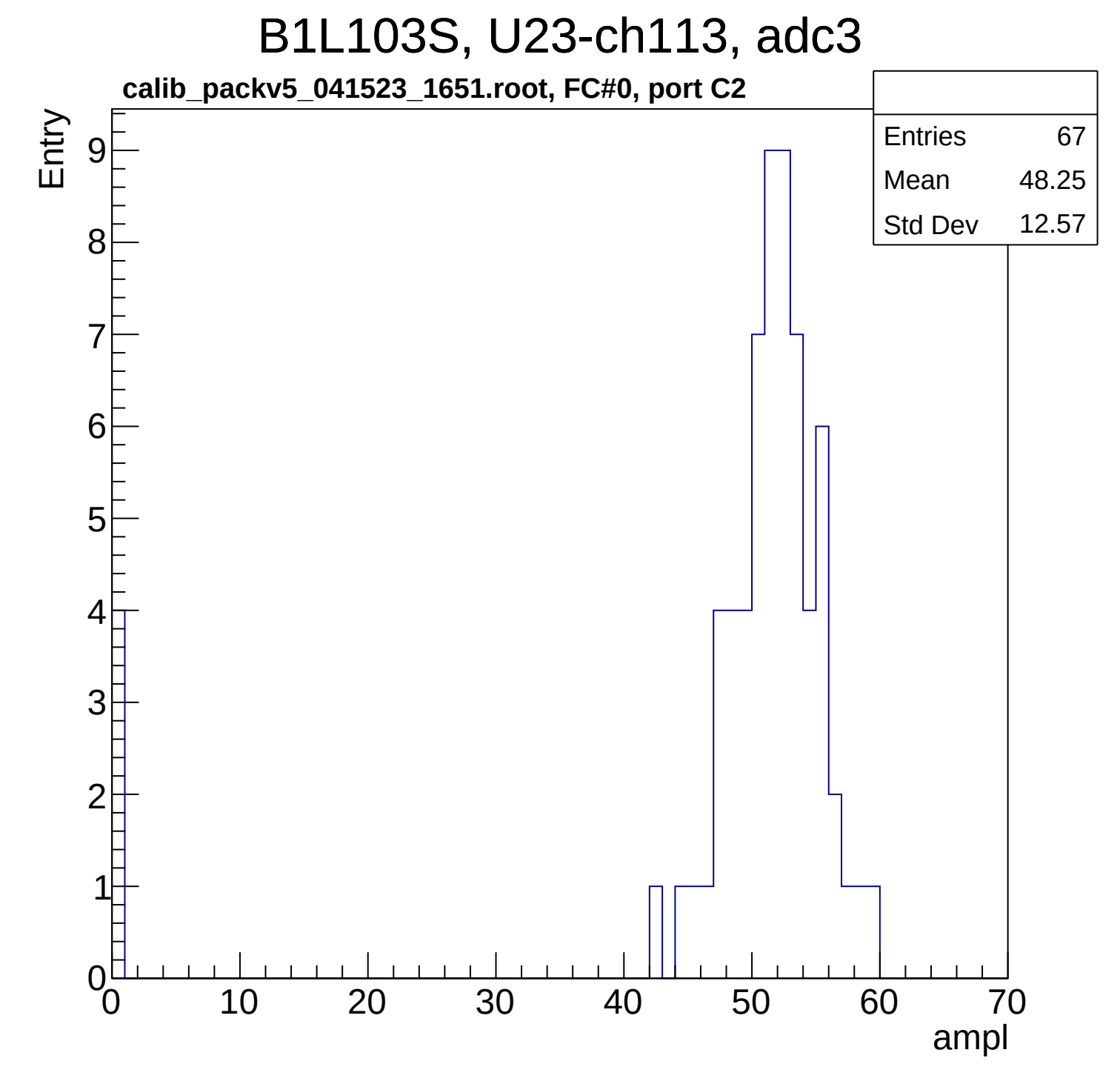

B1L103S, U23-ch112, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 8
Mean 61.25
Std Dev 1.714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

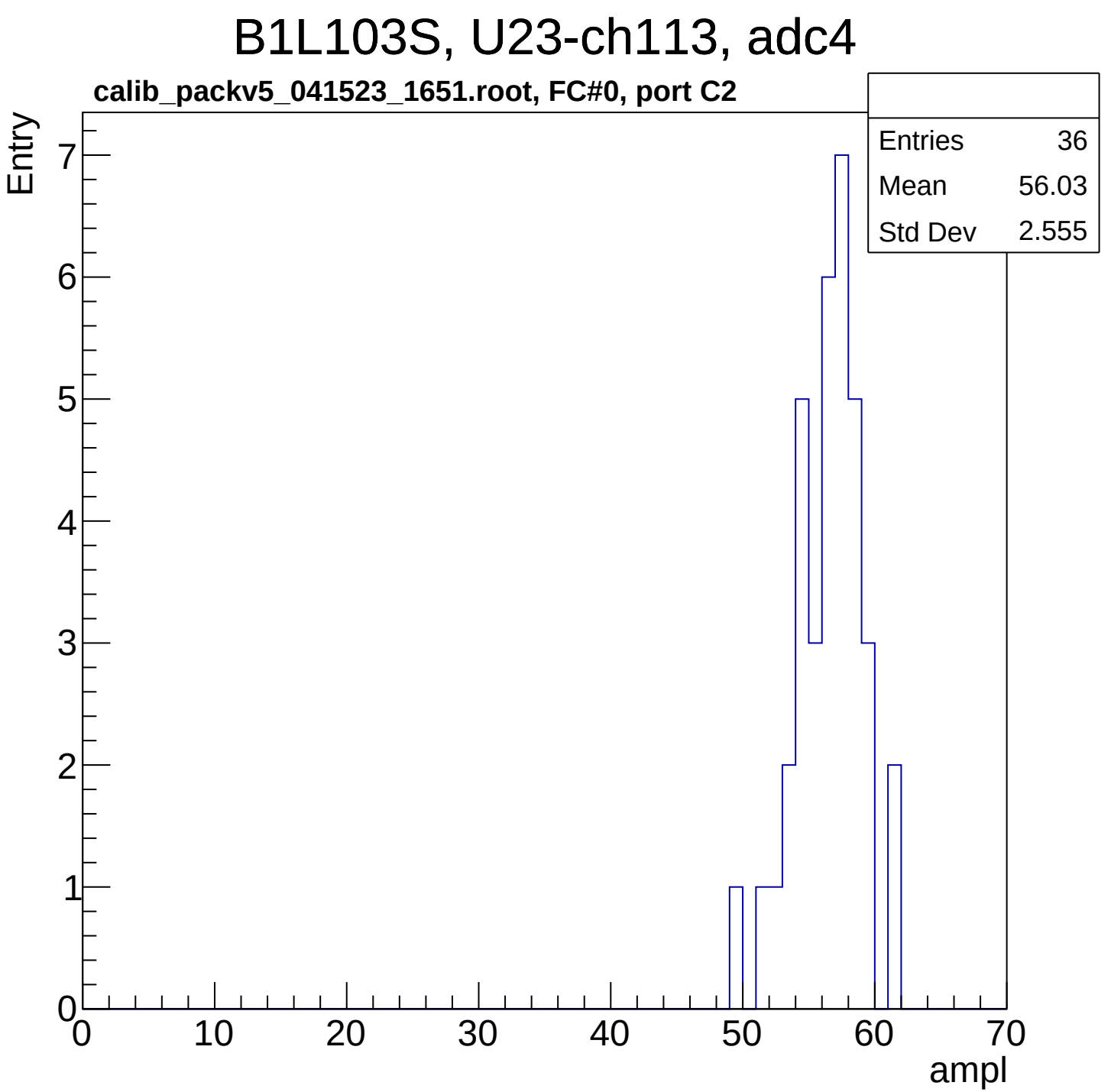

B1L103S, U23-ch112, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

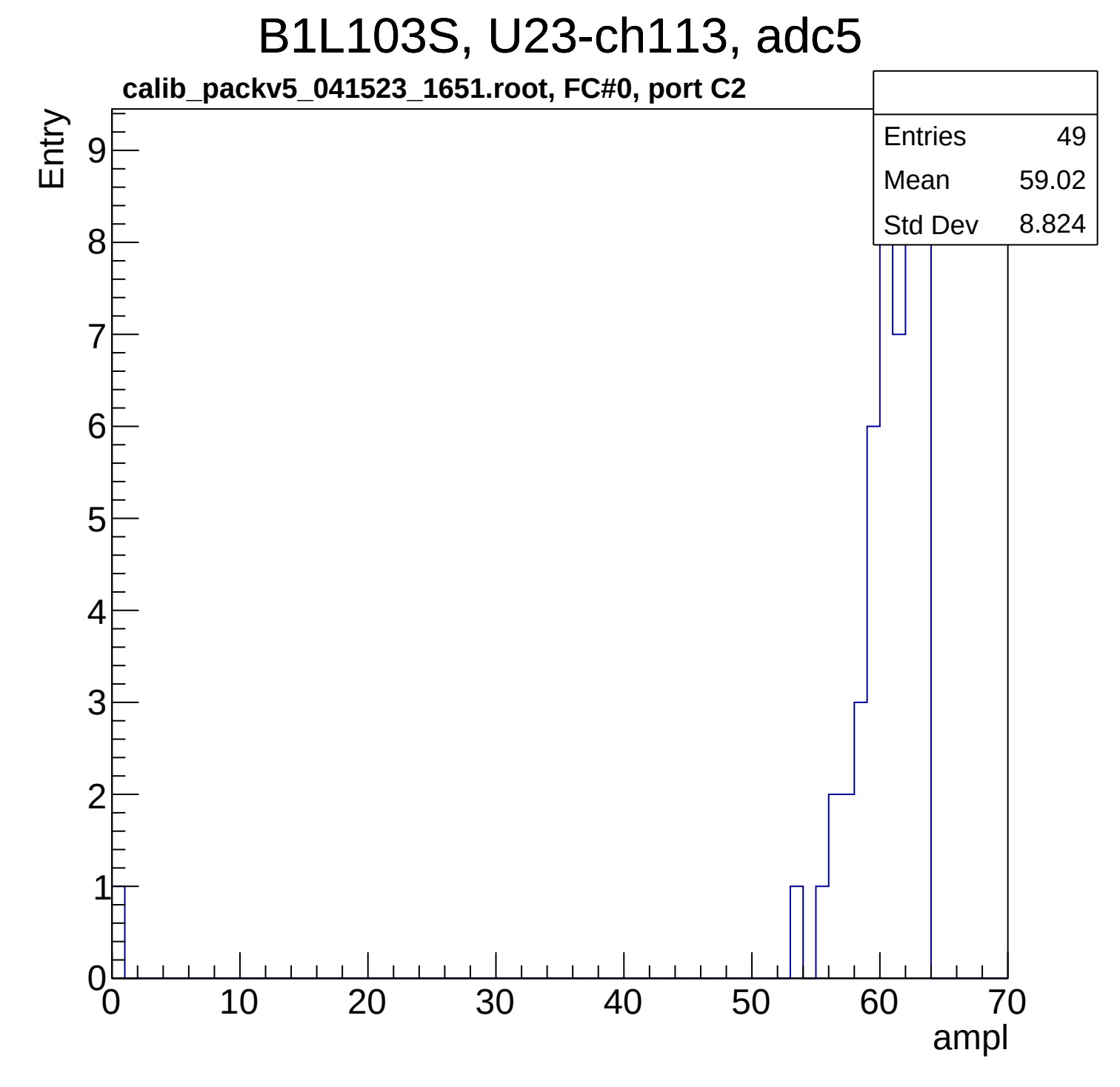

B1L103S, U23-ch113, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 110
Mean 21.56
Std Dev 13.37
Gaus mean : 29.6984
Gaus Width: 3.9175
Entry
ampl
0
5
10
15
20
25
30
0
10
20
30
40
50
60
70

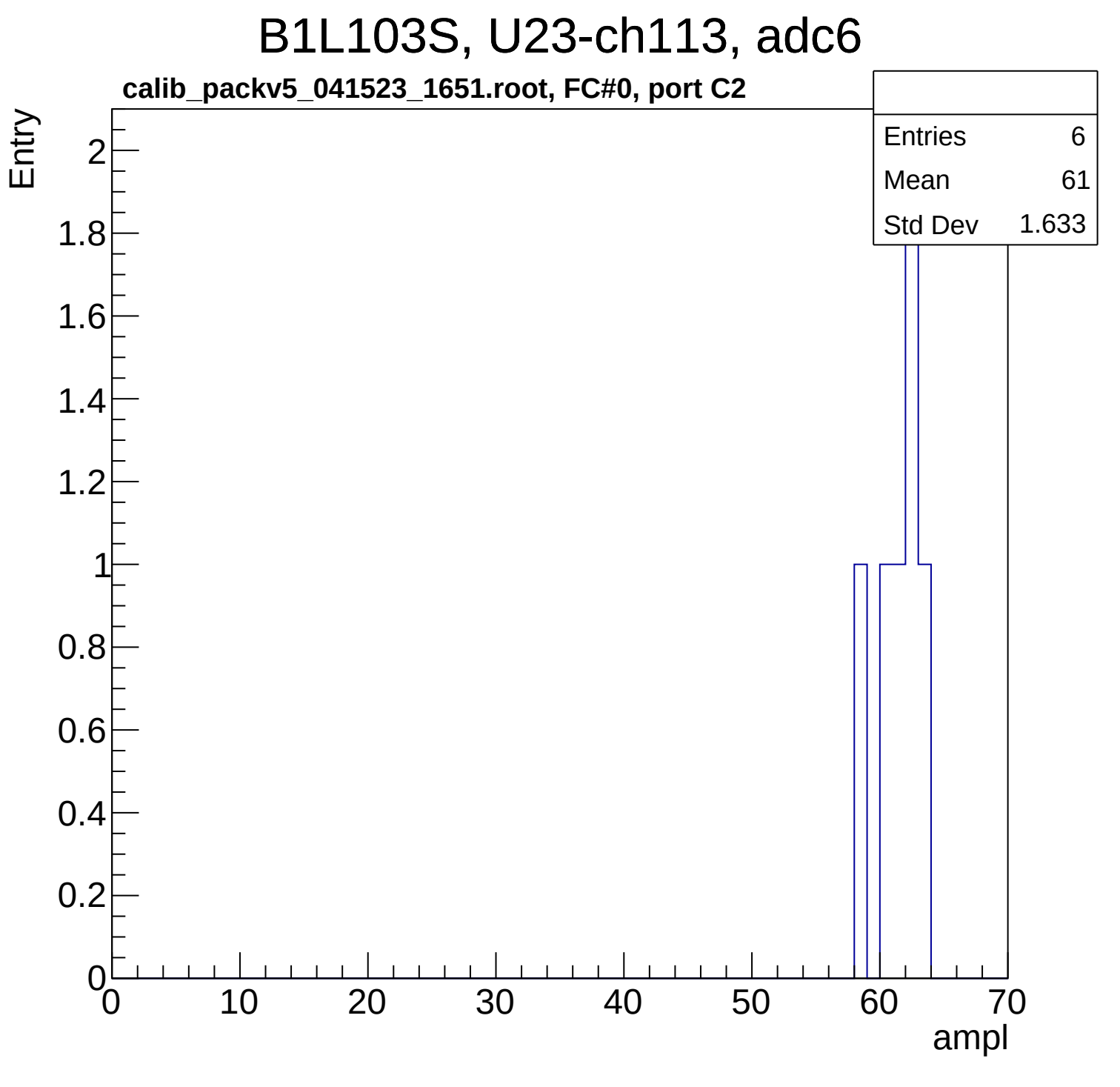

B1L103S, U23-ch113, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 85
Mean 35.58
Std Dev 9.597
Gaus mean : 38.7103
Gaus Width: 3.8500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

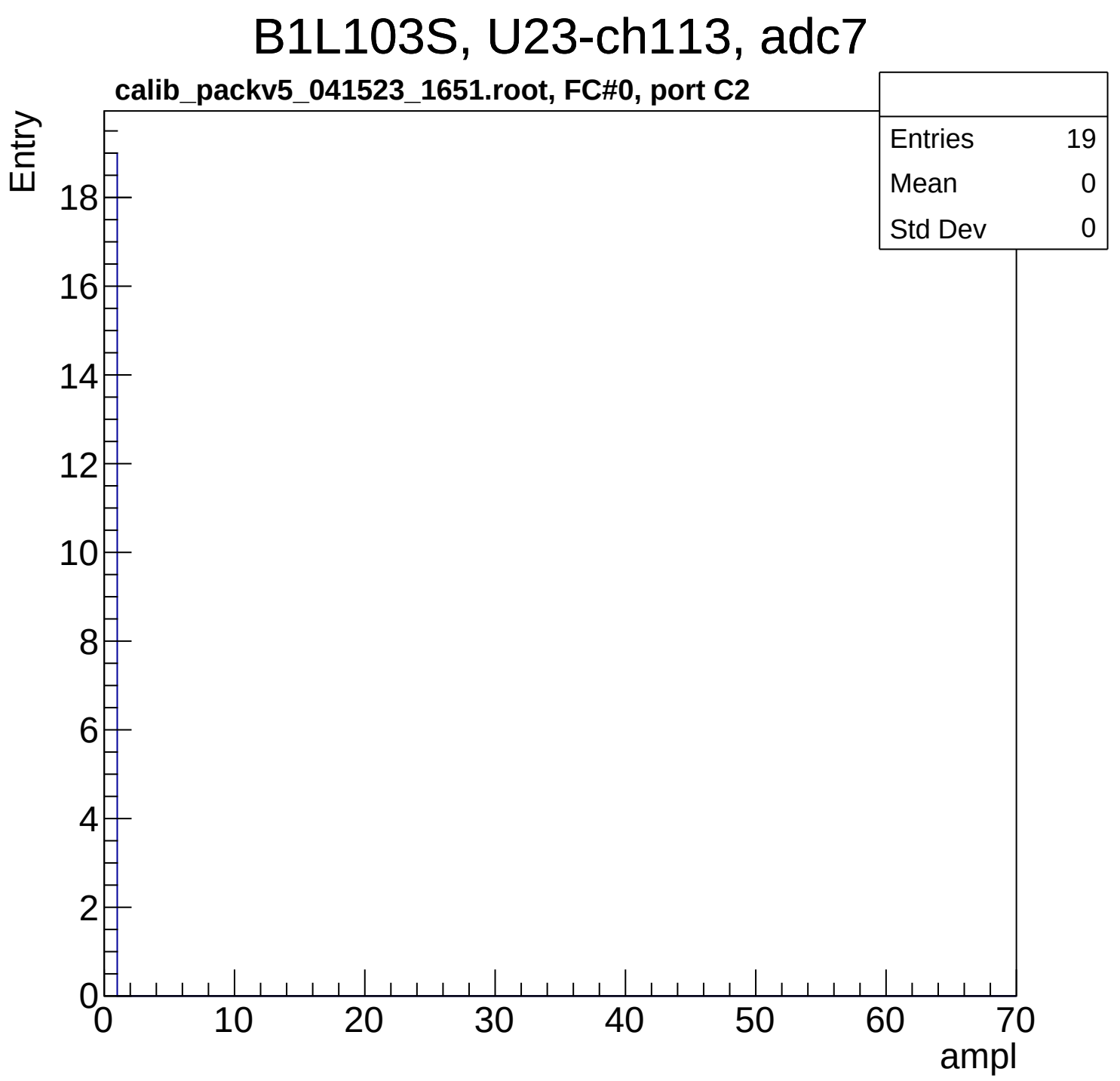

B1L103S, U23-ch113, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 39.36
Std Dev 14.97
Gaus mean : 45.7703
Gaus Width: 4.1356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

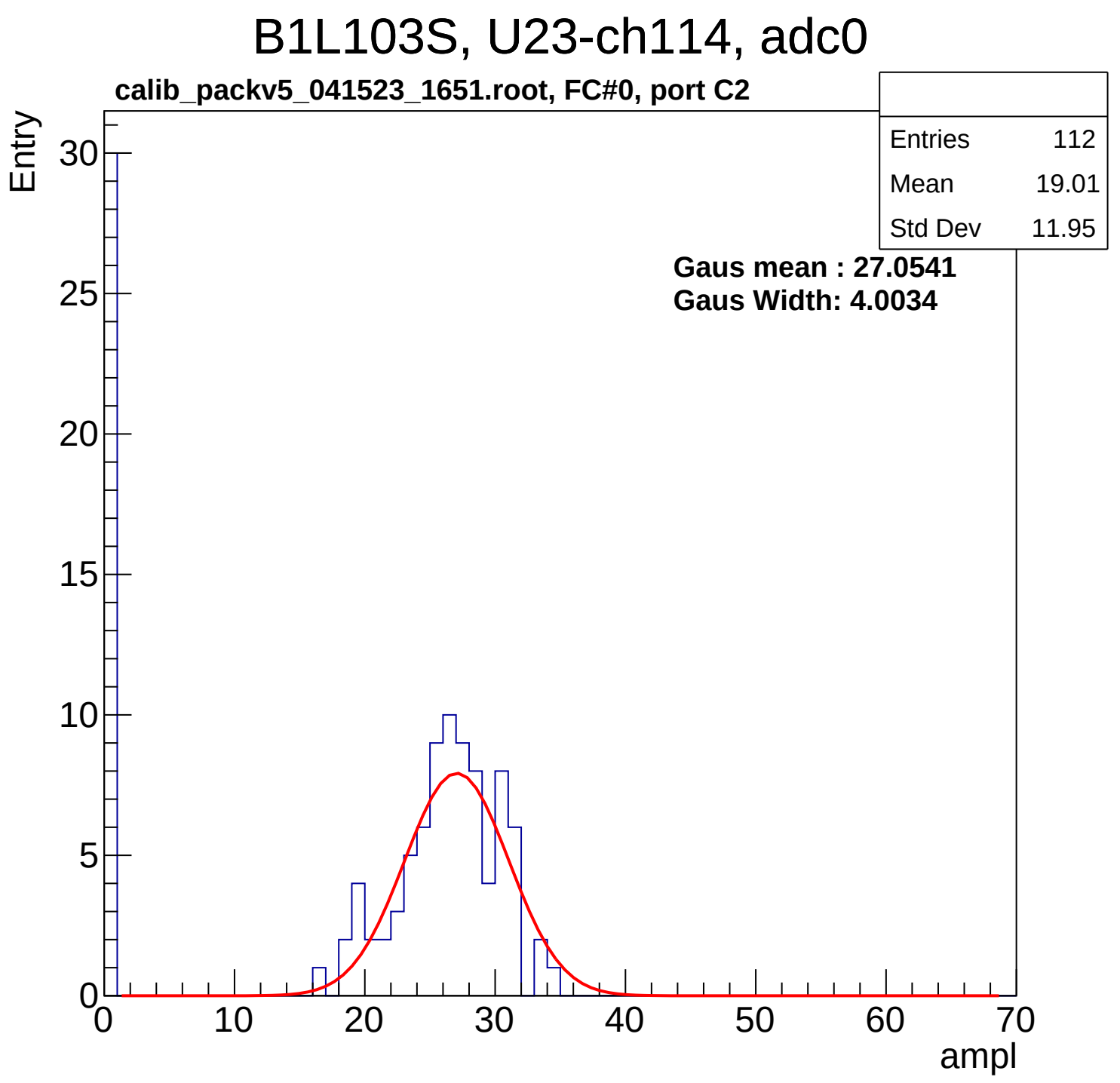

B1L103S, U23-ch113, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 48.25
Std Dev 12.57
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

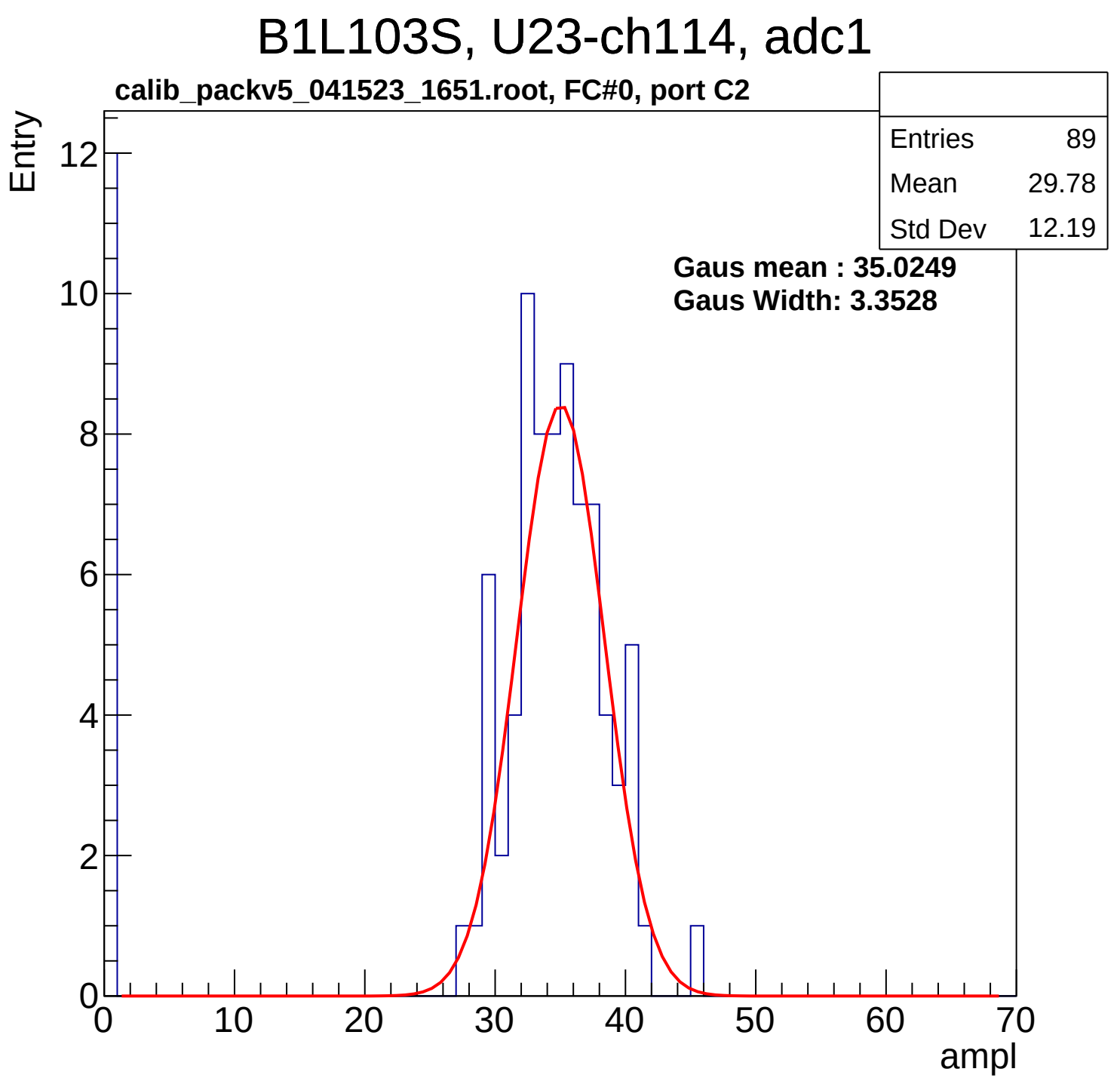

B1L103S, U23-ch113, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 36
Mean 56.03
Std Dev 2.555
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

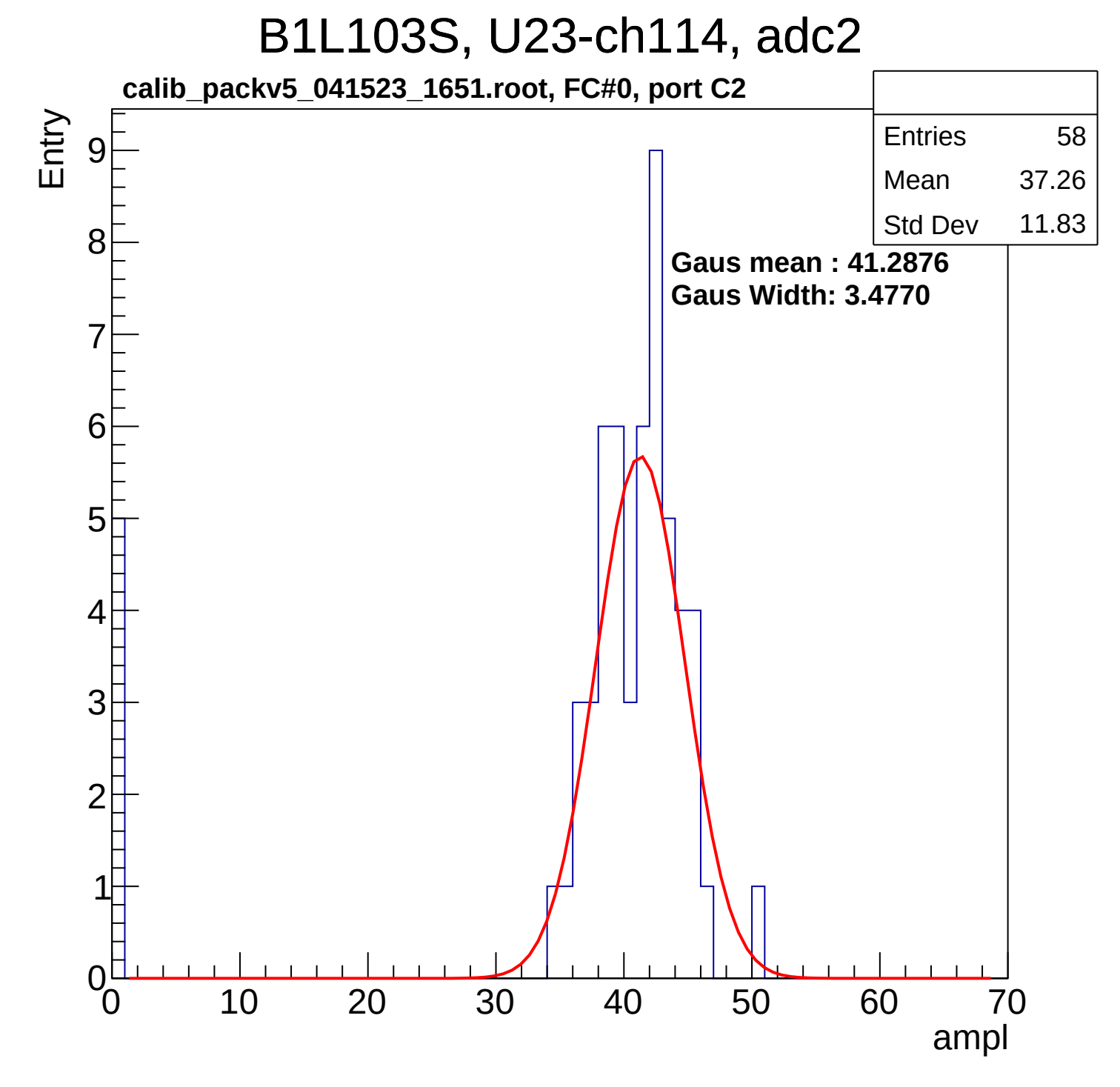

B1L103S, U23-ch113, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 59.02
Std Dev 8.824
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

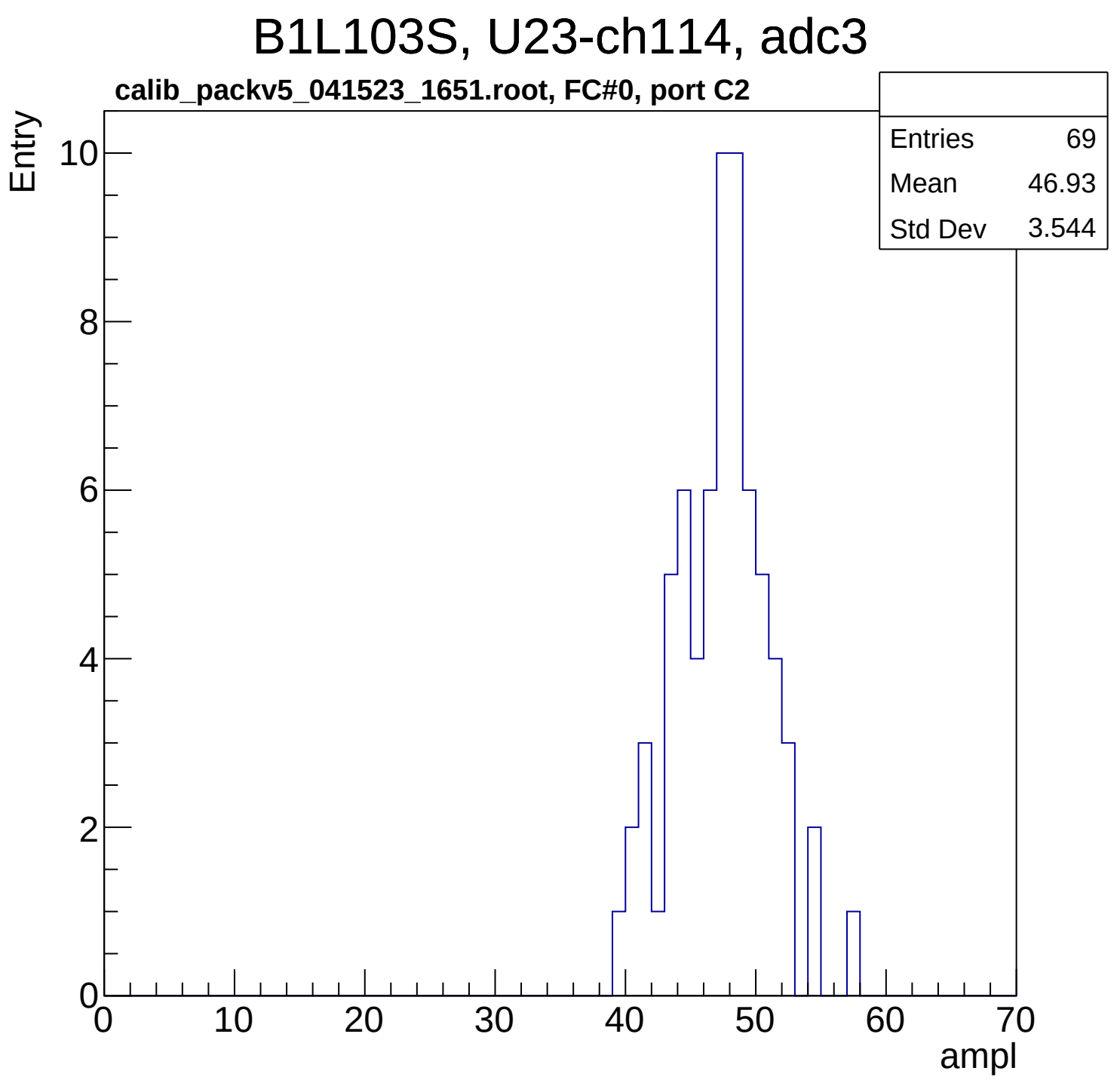

B1L103S, U23-ch113, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 61
Std Dev 1.633
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

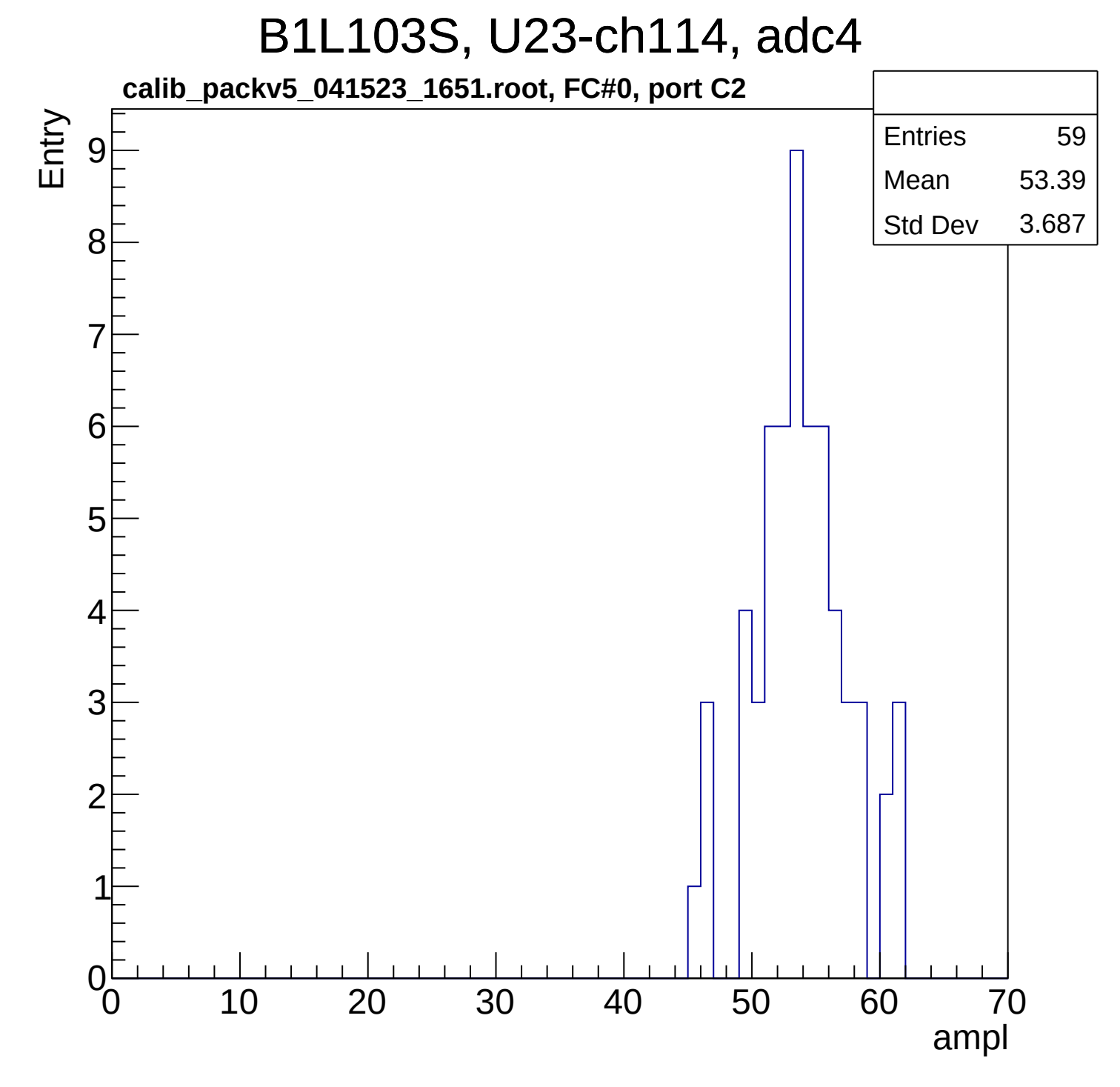

B1L103S, U23-ch113, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

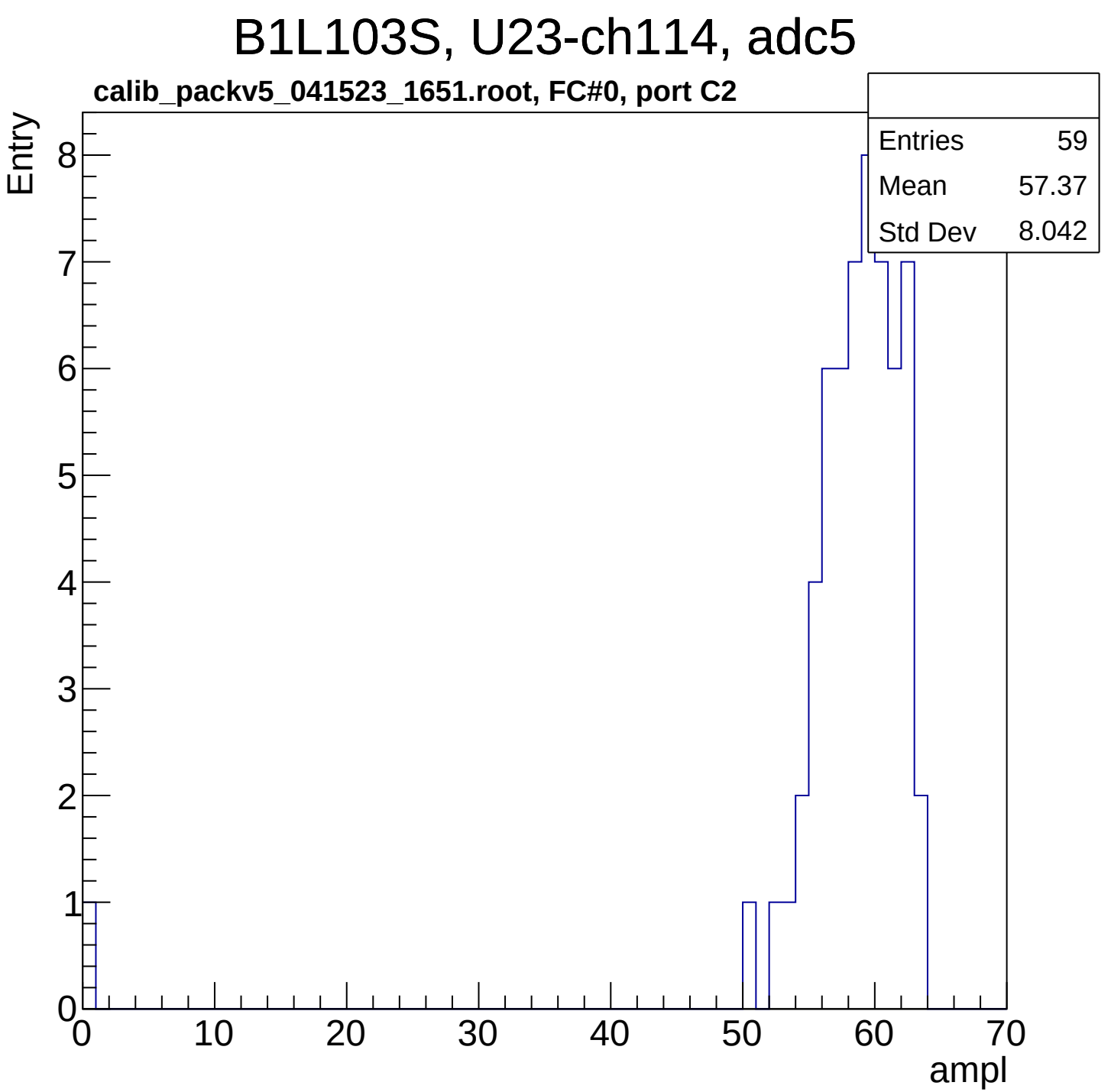

B1L103S, U23-ch114, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 112
Mean 19.01
Std Dev 11.95
Gaus mean : 27.0541
Gaus Width: 4.0034
Entry
ampl
0
5
10
15
20
25
30
0
10
20
30
40
50
60
70

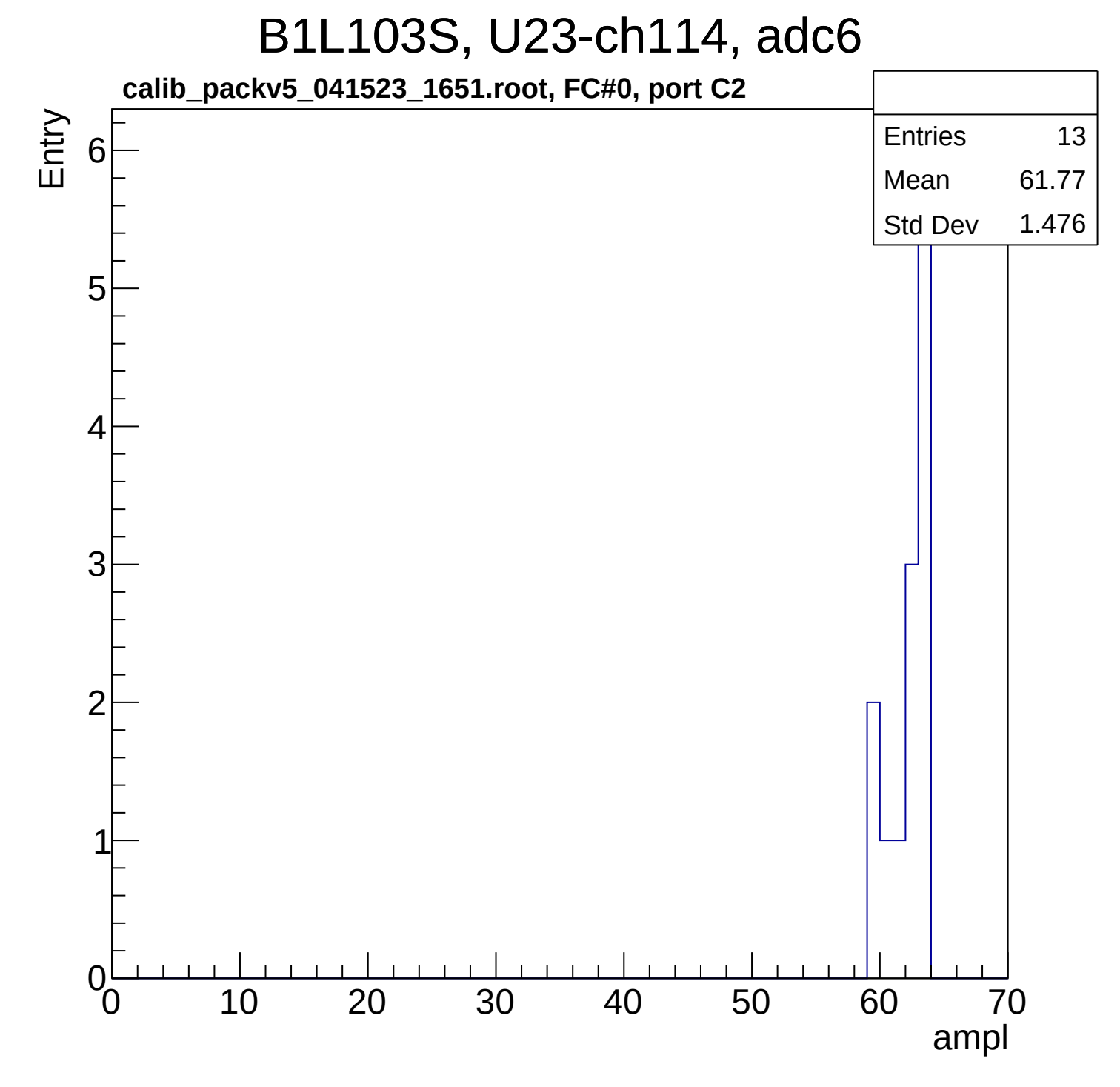

B1L103S, U23-ch114, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 89
Mean 29.78
Std Dev 12.19
Gaus mean : 35.0249
Gaus Width: 3.3528
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

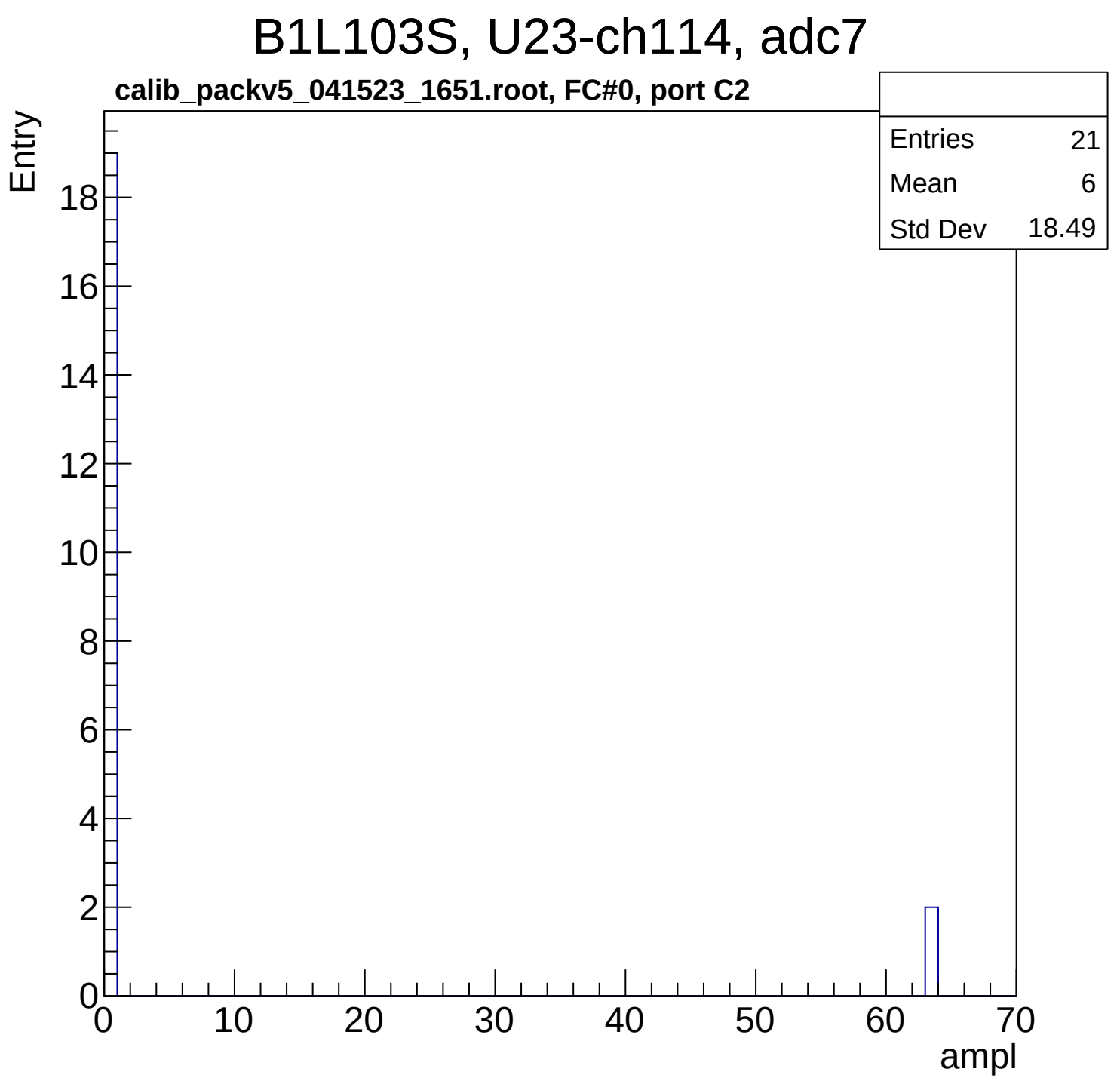

B1L103S, U23-ch114, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 37.26
Std Dev 11.83
Gaus mean : 41.2876
Gaus Width: 3.4770
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

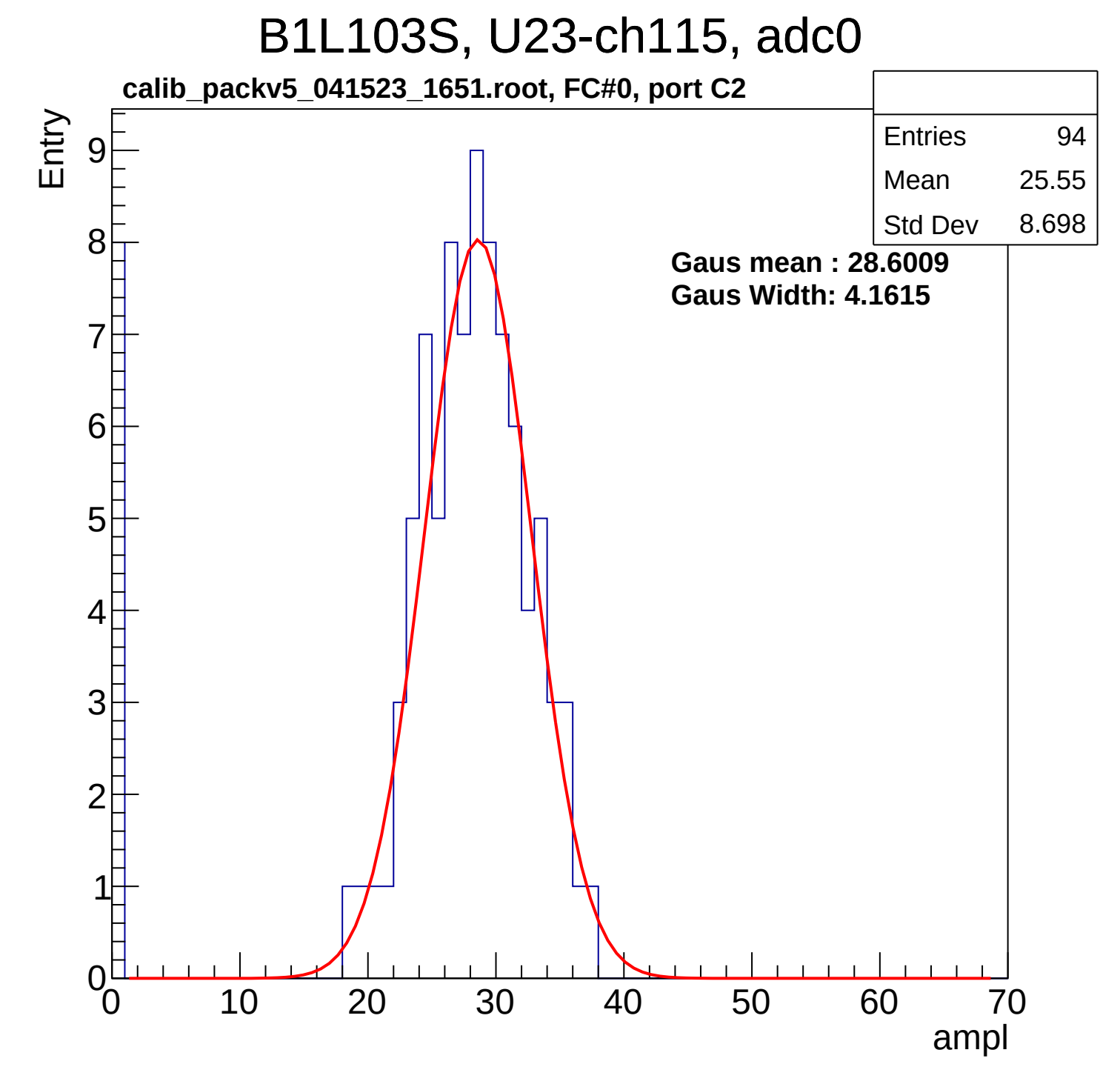

B1L103S, U23-ch114, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 46.93
Std Dev 3.544
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

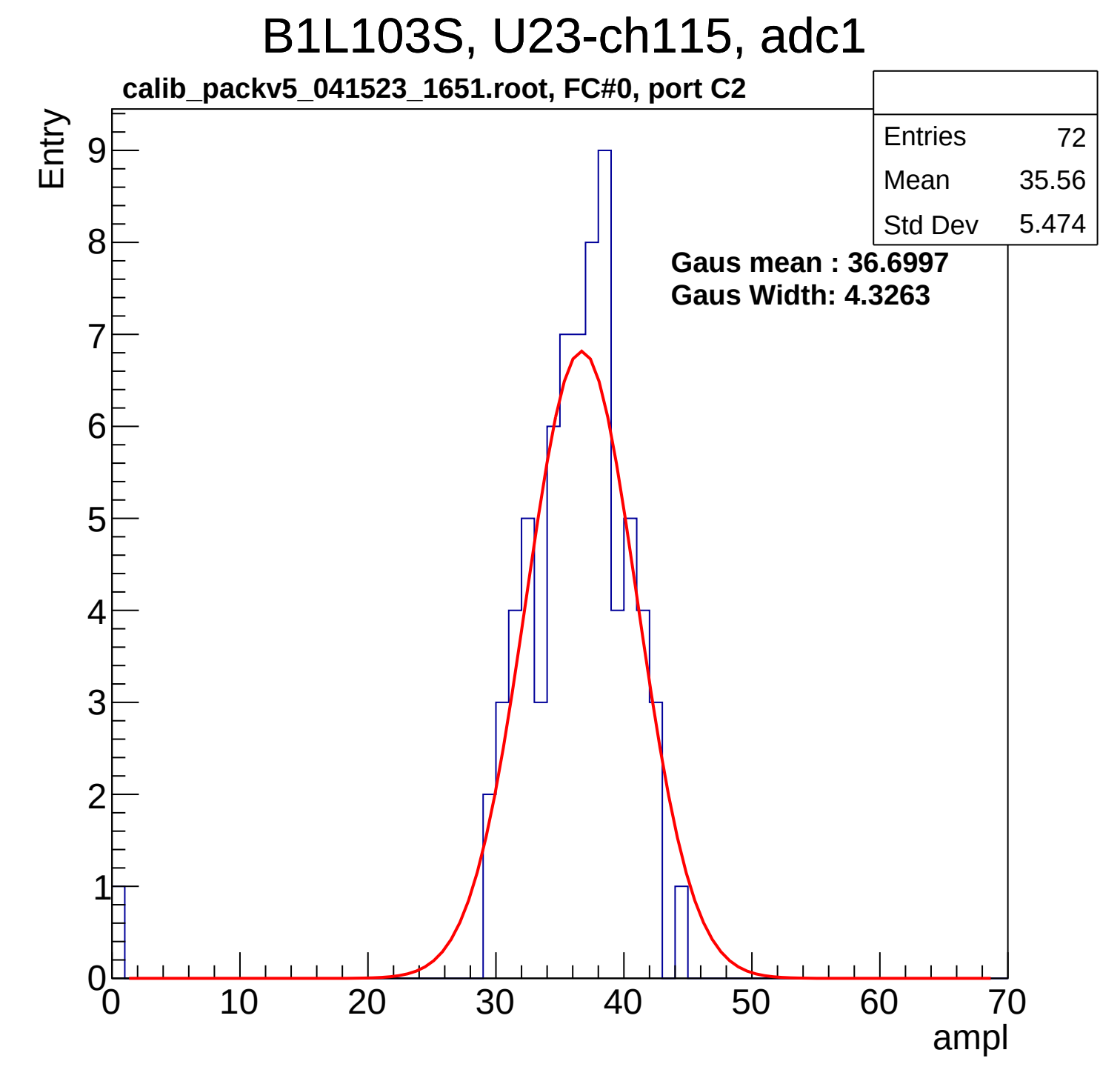

B1L103S, U23-ch114, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 53.39
Std Dev 3.687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

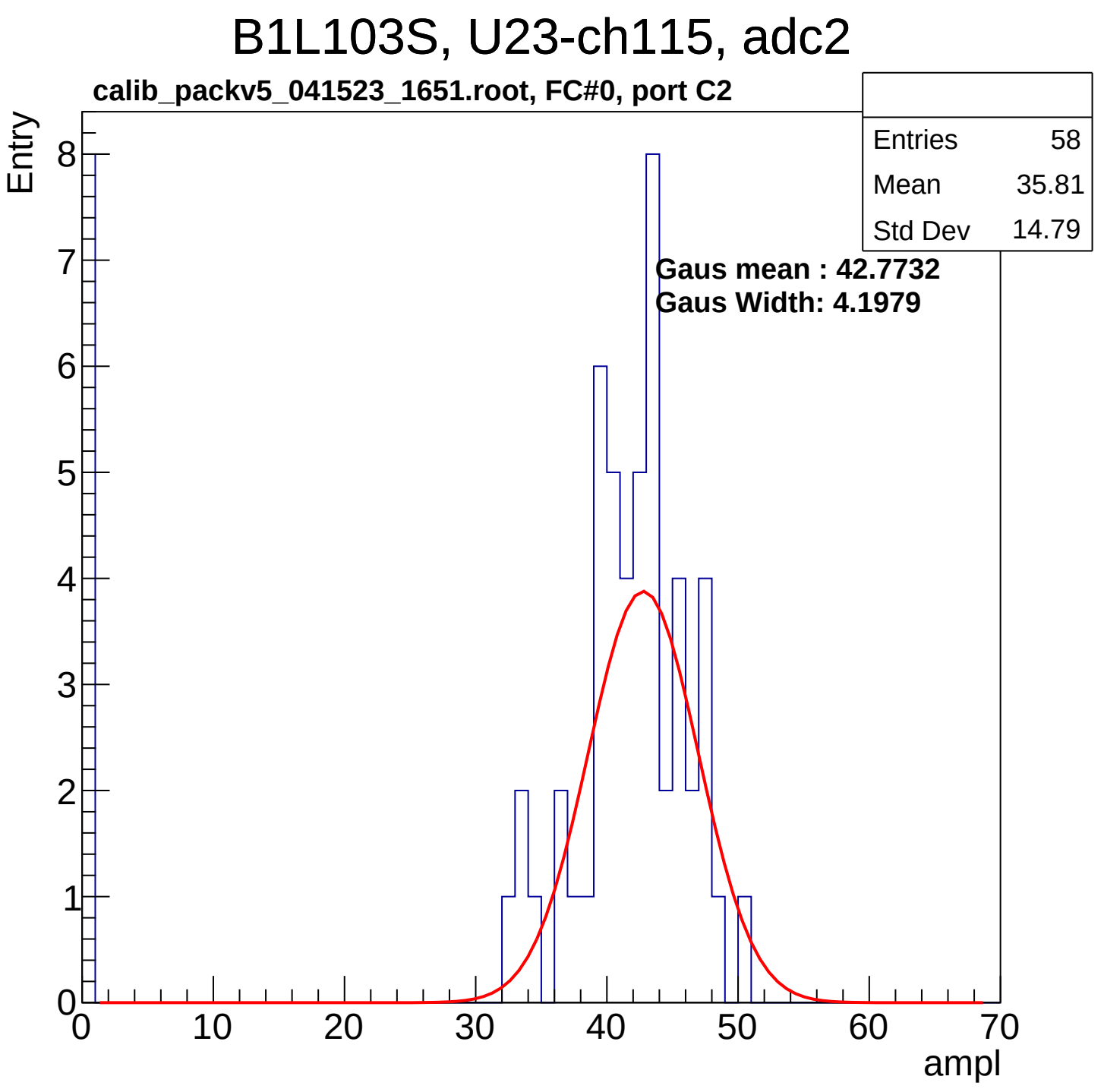

B1L103S, U23-ch114, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 57.37
Std Dev 8.042
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

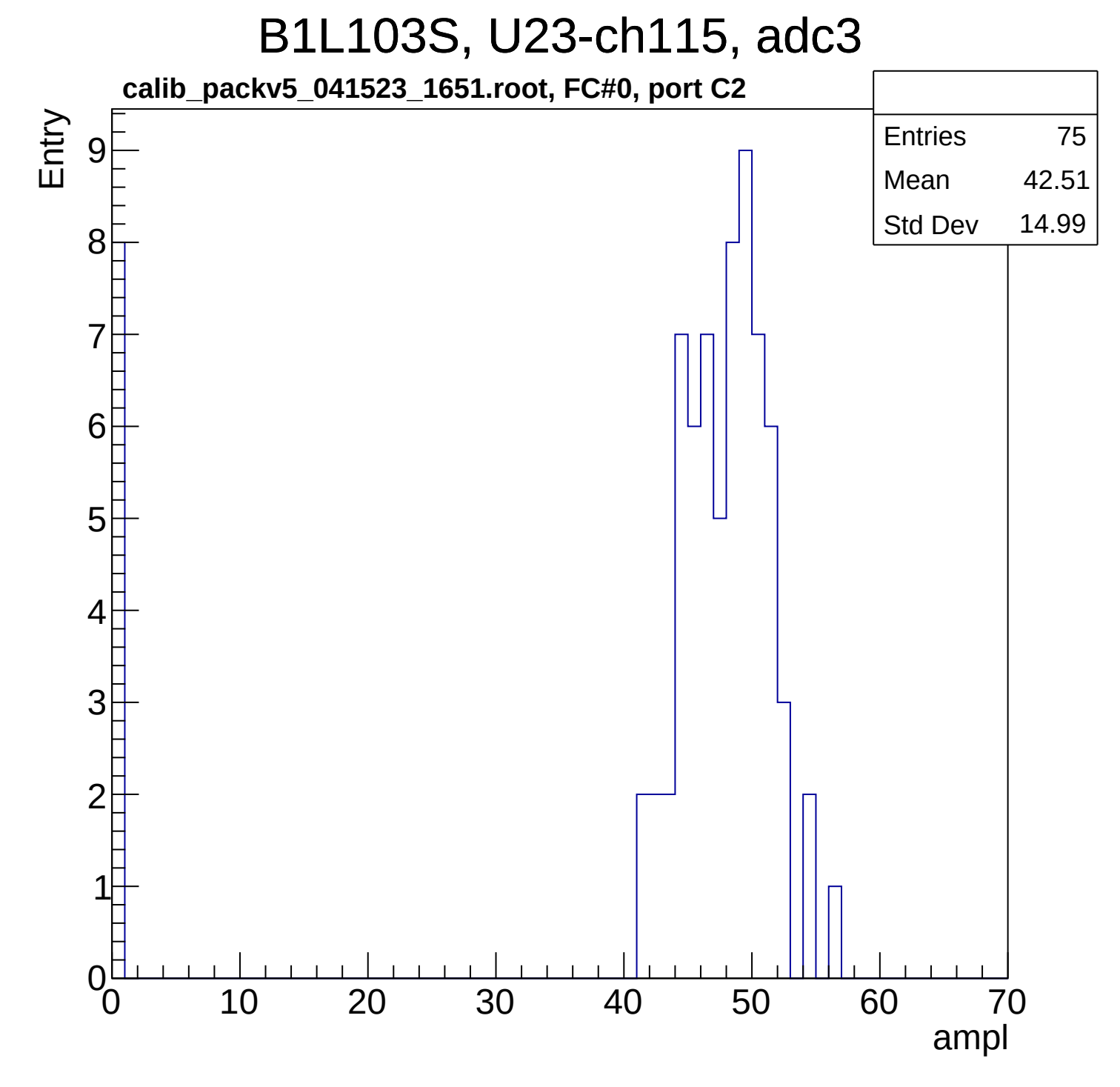

B1L103S, U23-ch114, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 13
Mean 61.77
Std Dev 1.476
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

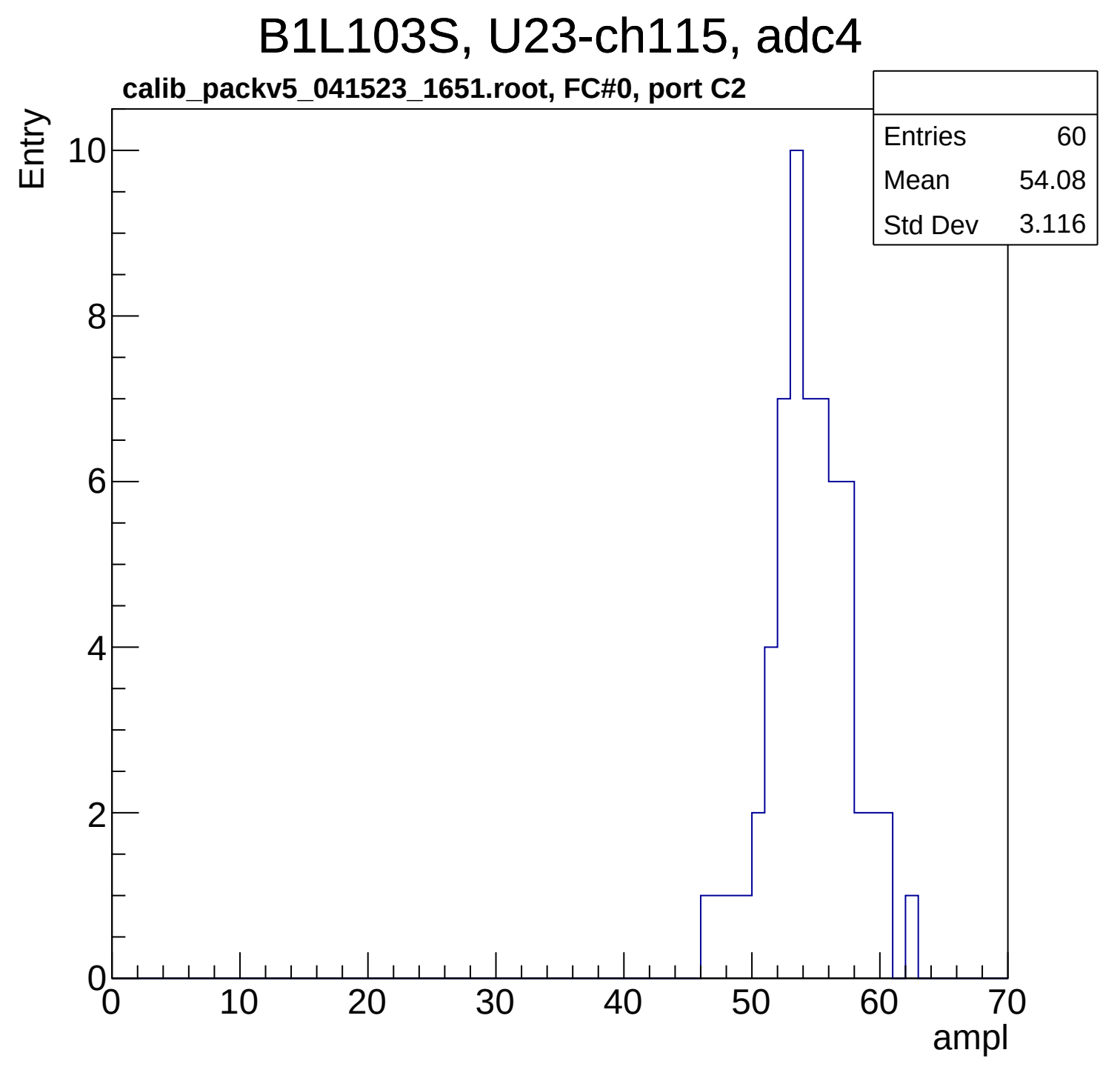

B1L103S, U23-ch114, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 6
Std Dev 18.49
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

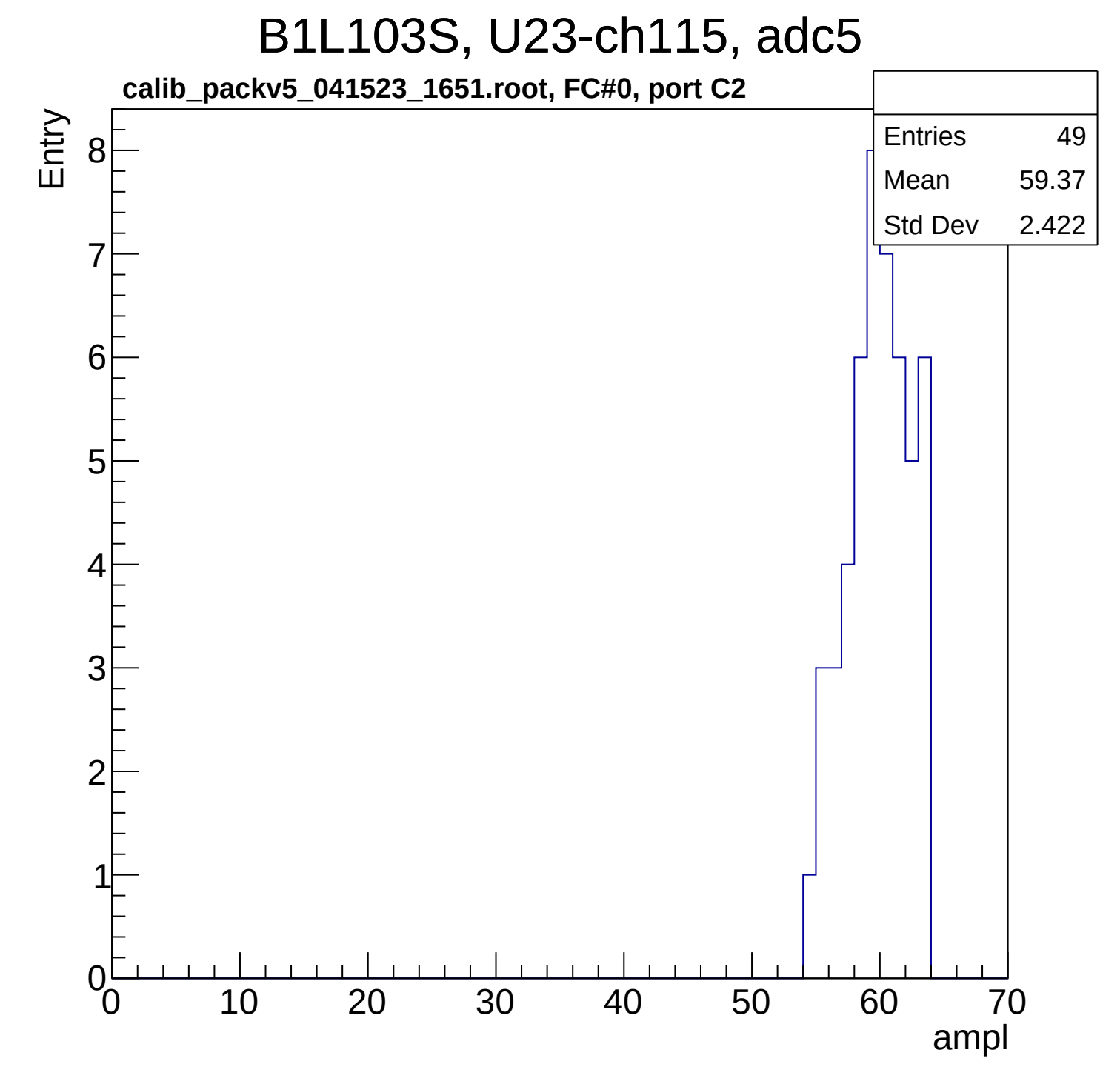

B1L103S, U23-ch115, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 94
Mean 25.55
Std Dev 8.698
Gaus mean : 28.6009
Gaus Width: 4.1615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

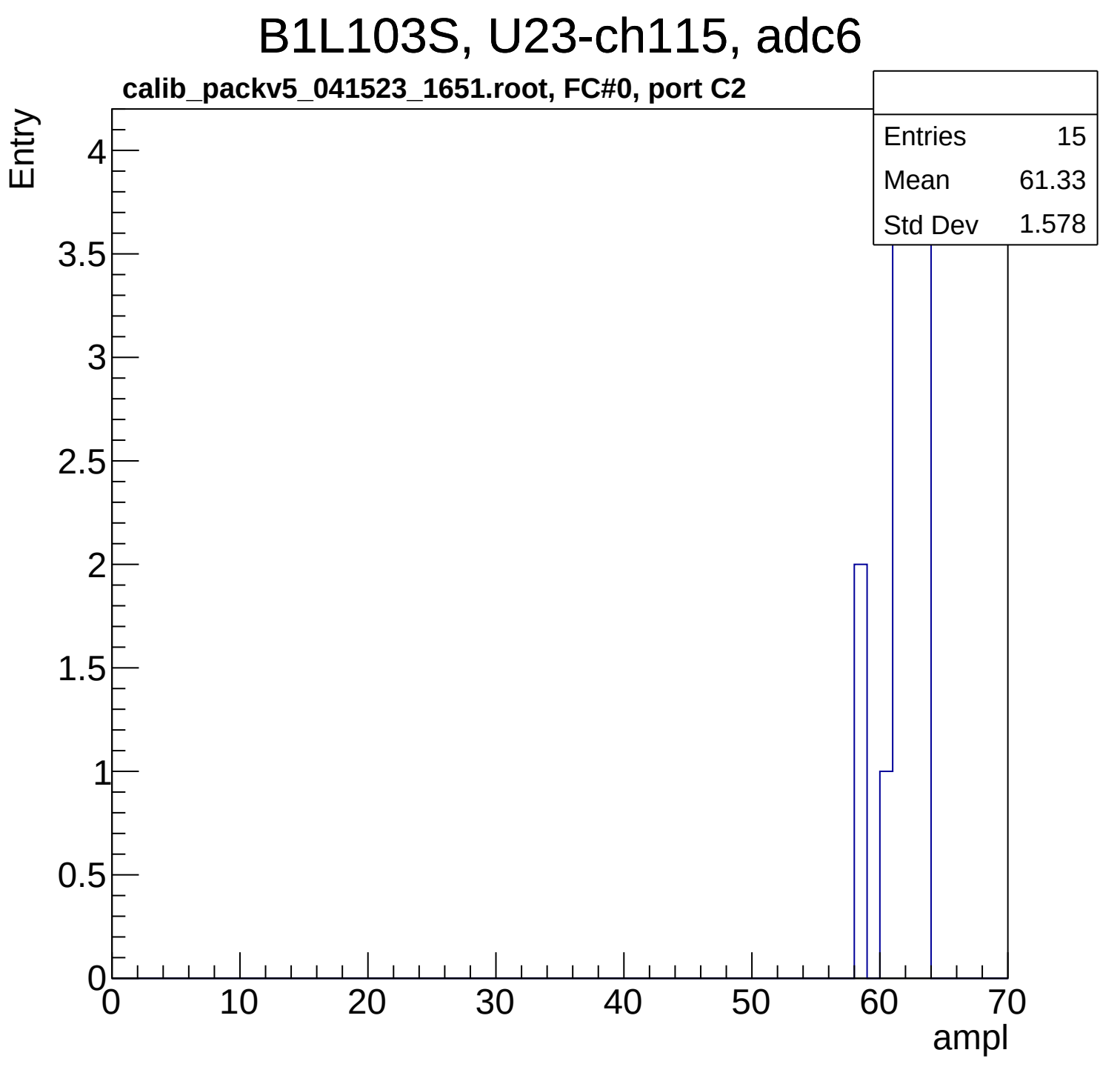

B1L103S, U23-ch115, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 35.56
Std Dev 5.474
Gaus mean : 36.6997
Gaus Width: 4.3263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

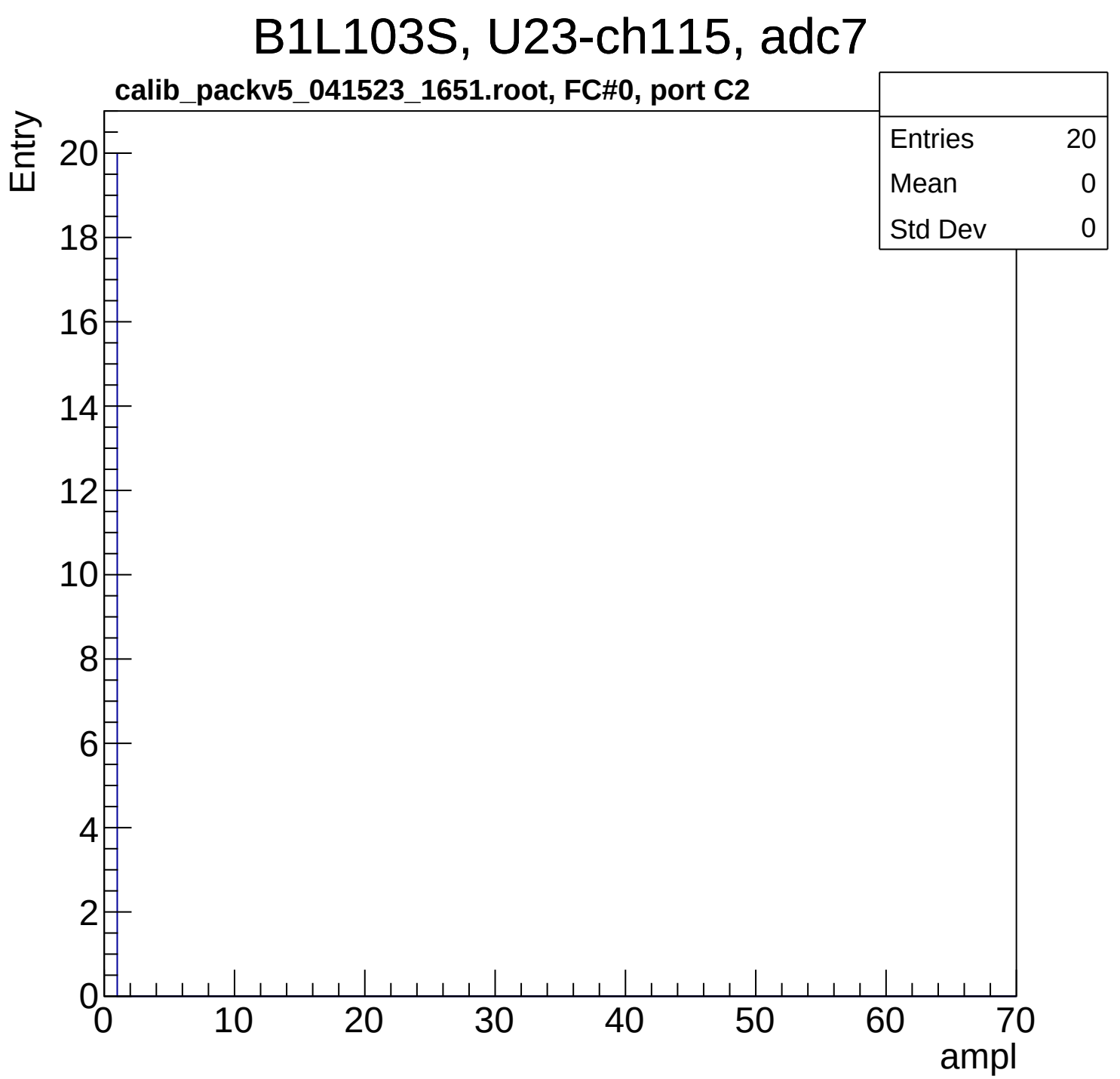

B1L103S, U23-ch115, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 35.81
Std Dev 14.79
Gaus mean : 42.7732
Gaus Width: 4.1979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

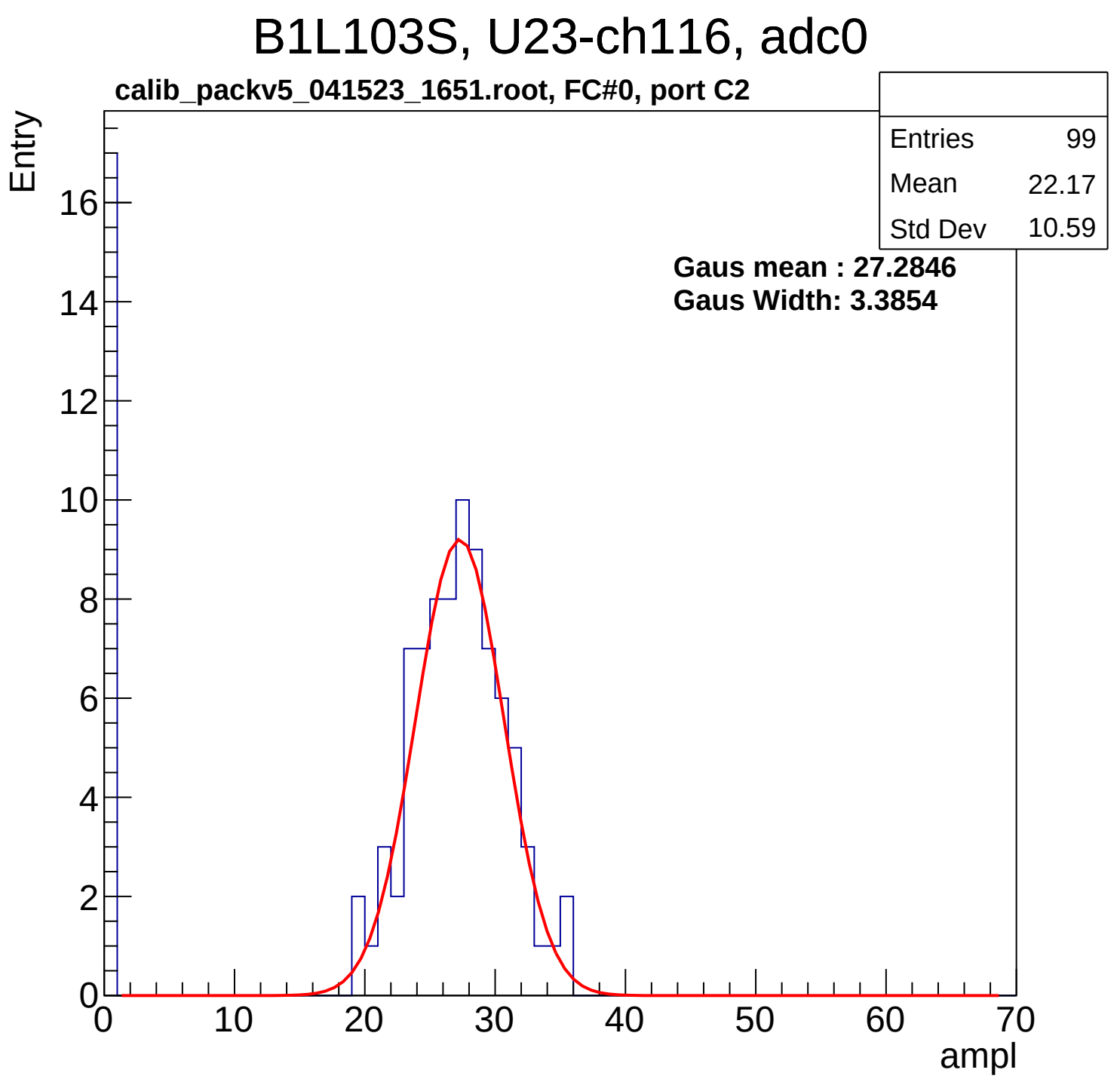

B1L103S, U23-ch115, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 42.51
Std Dev 14.99
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

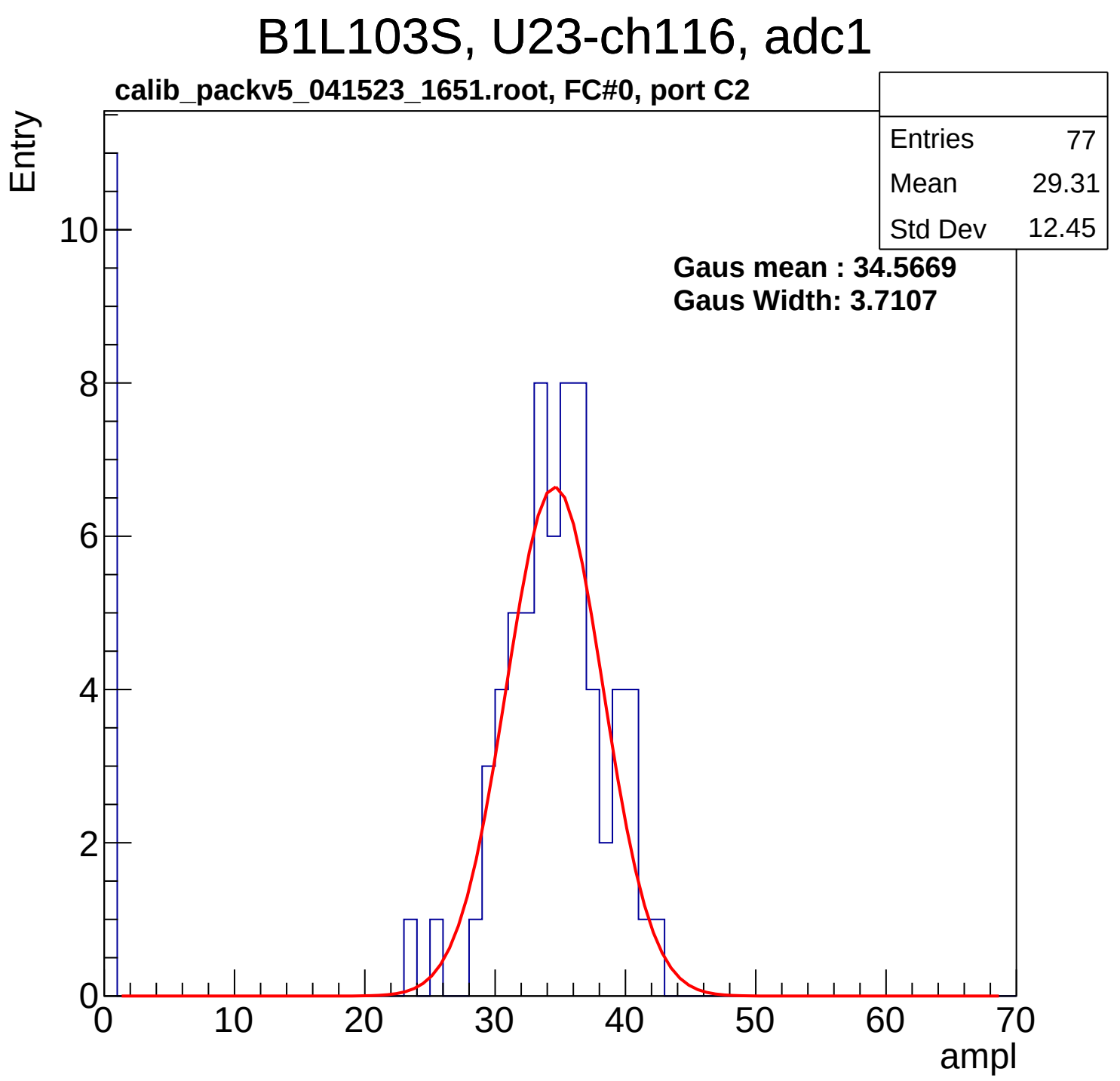

B1L103S, U23-ch115, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 54.08
Std Dev 3.116
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

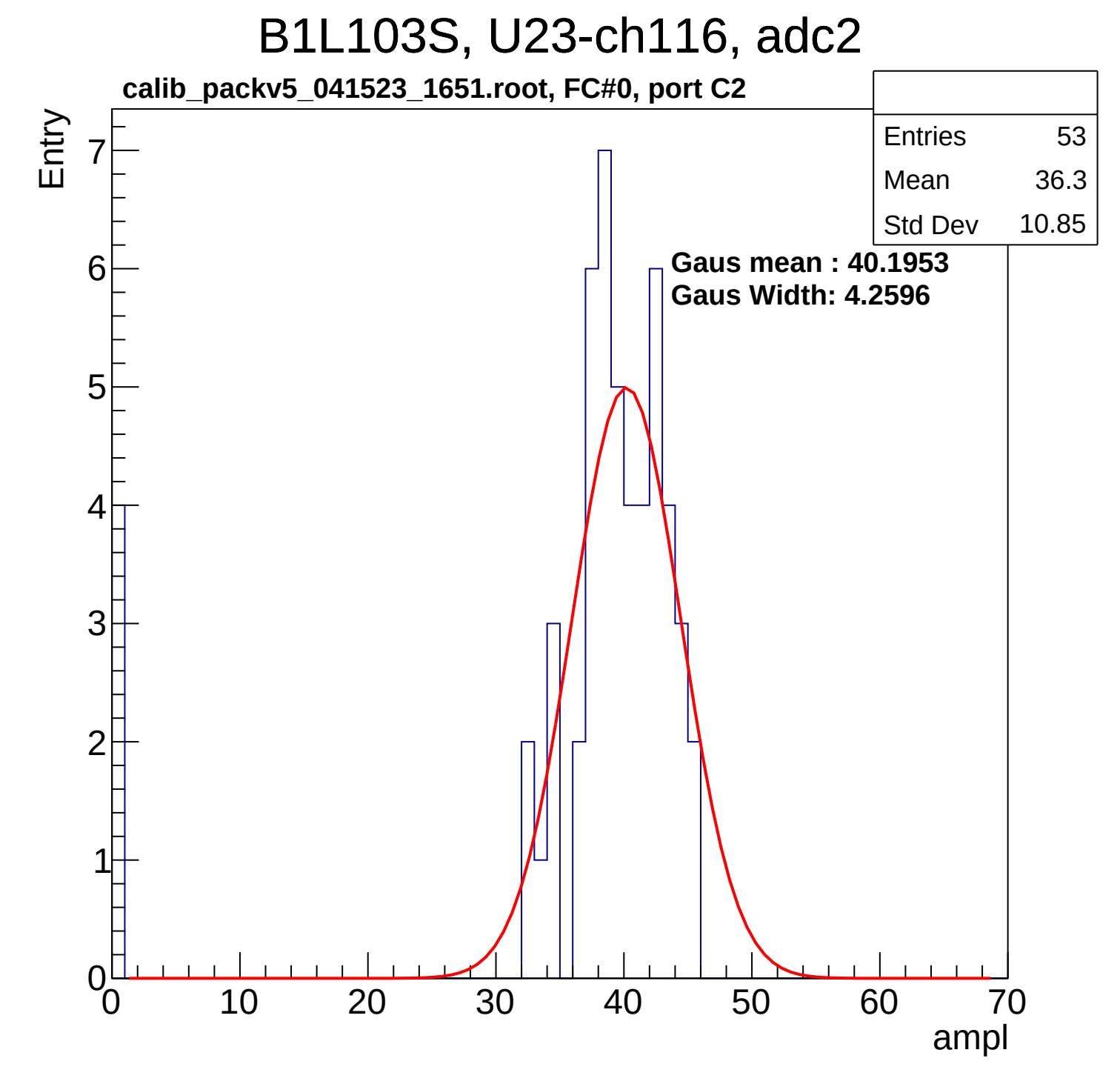

B1L103S, U23-ch115, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 59.37
Std Dev 2.422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

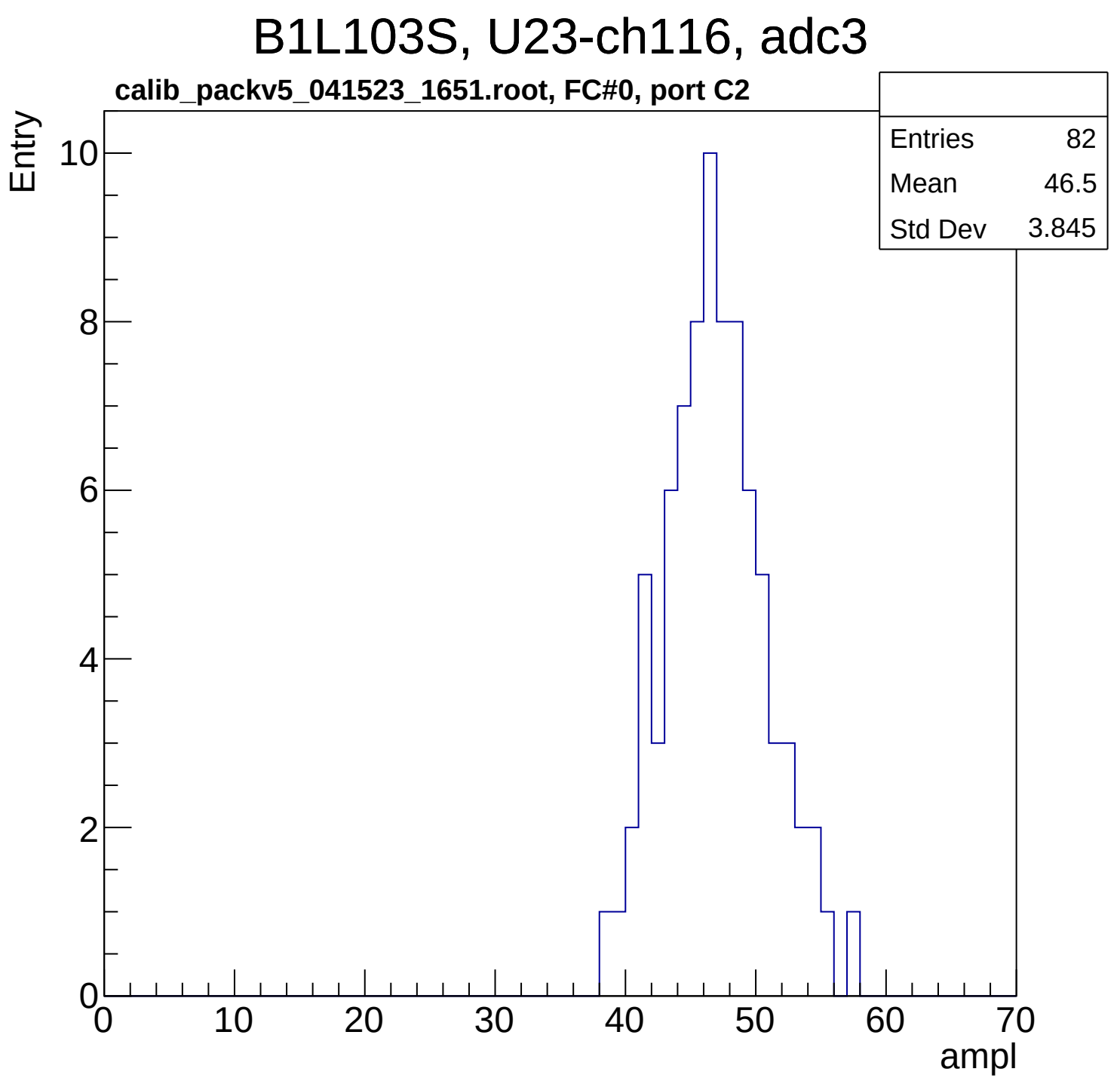

B1L103S, U23-ch115, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 15
Mean 61.33
Std Dev 1.578
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

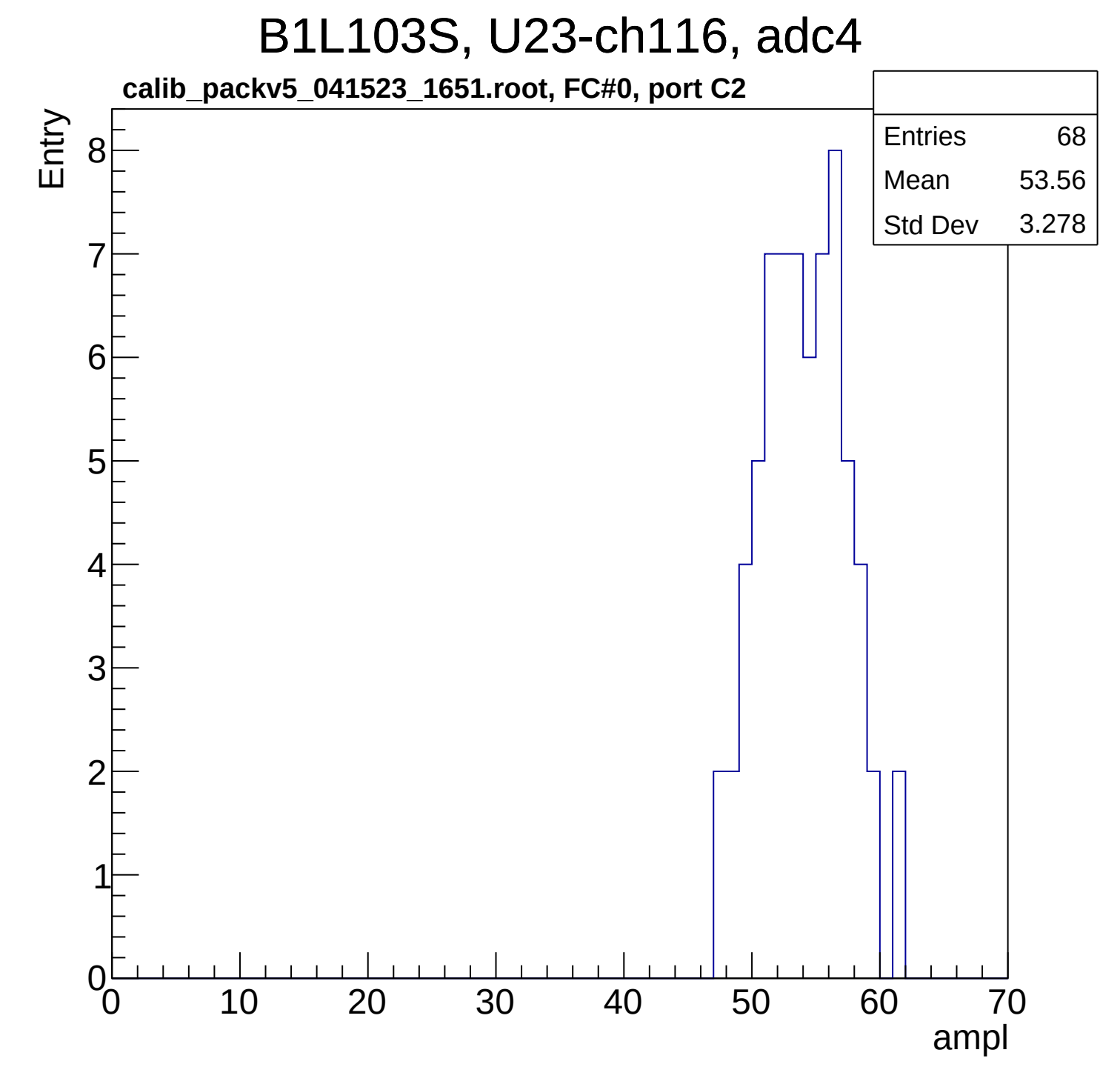

B1L103S, U23-ch115, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70
ampl

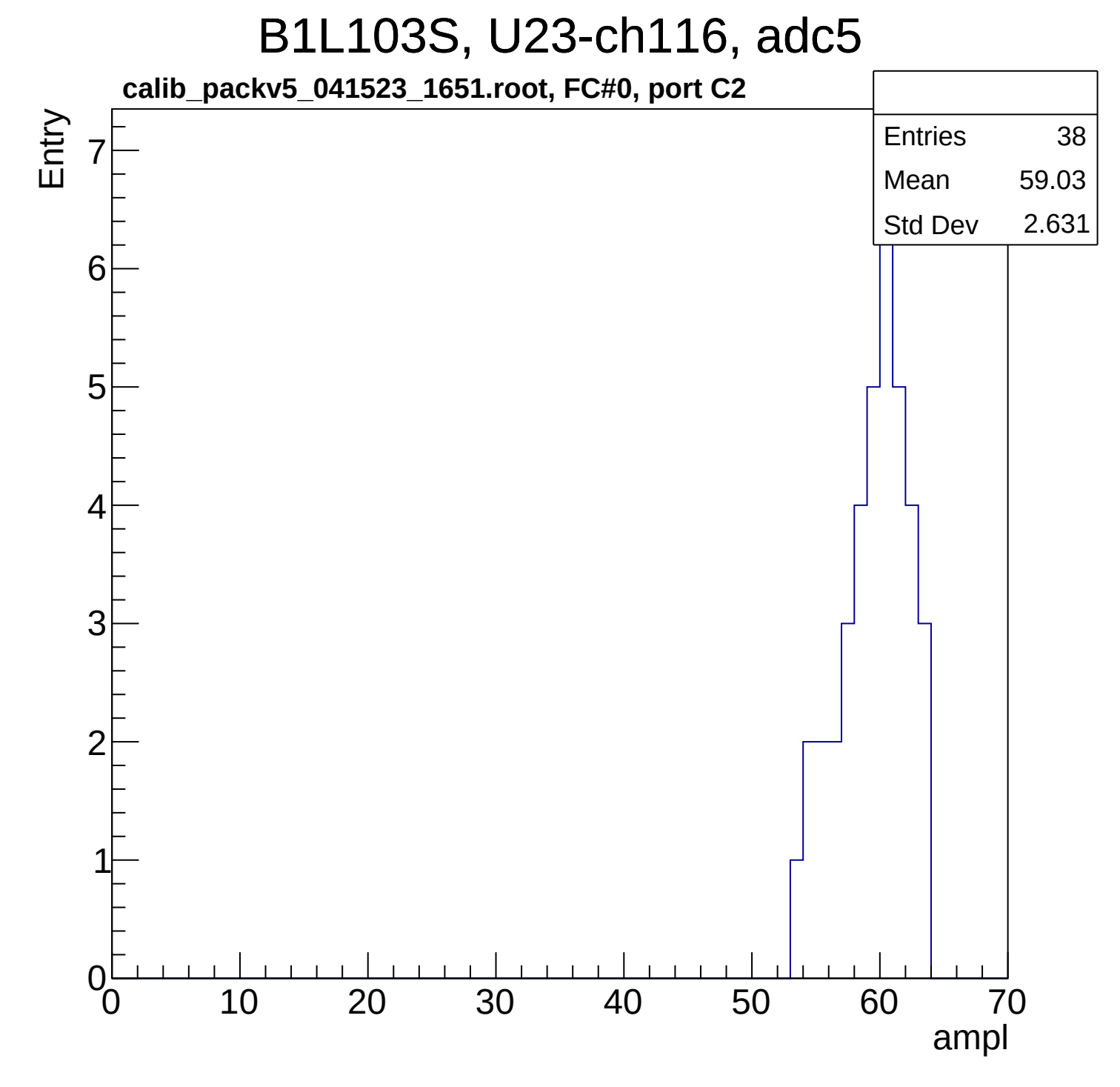

B1L103S, U23-ch116, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 99
Mean 22.17
Std Dev 10.59
Gaus mean : 27.2846
Gaus Width: 3.3854
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

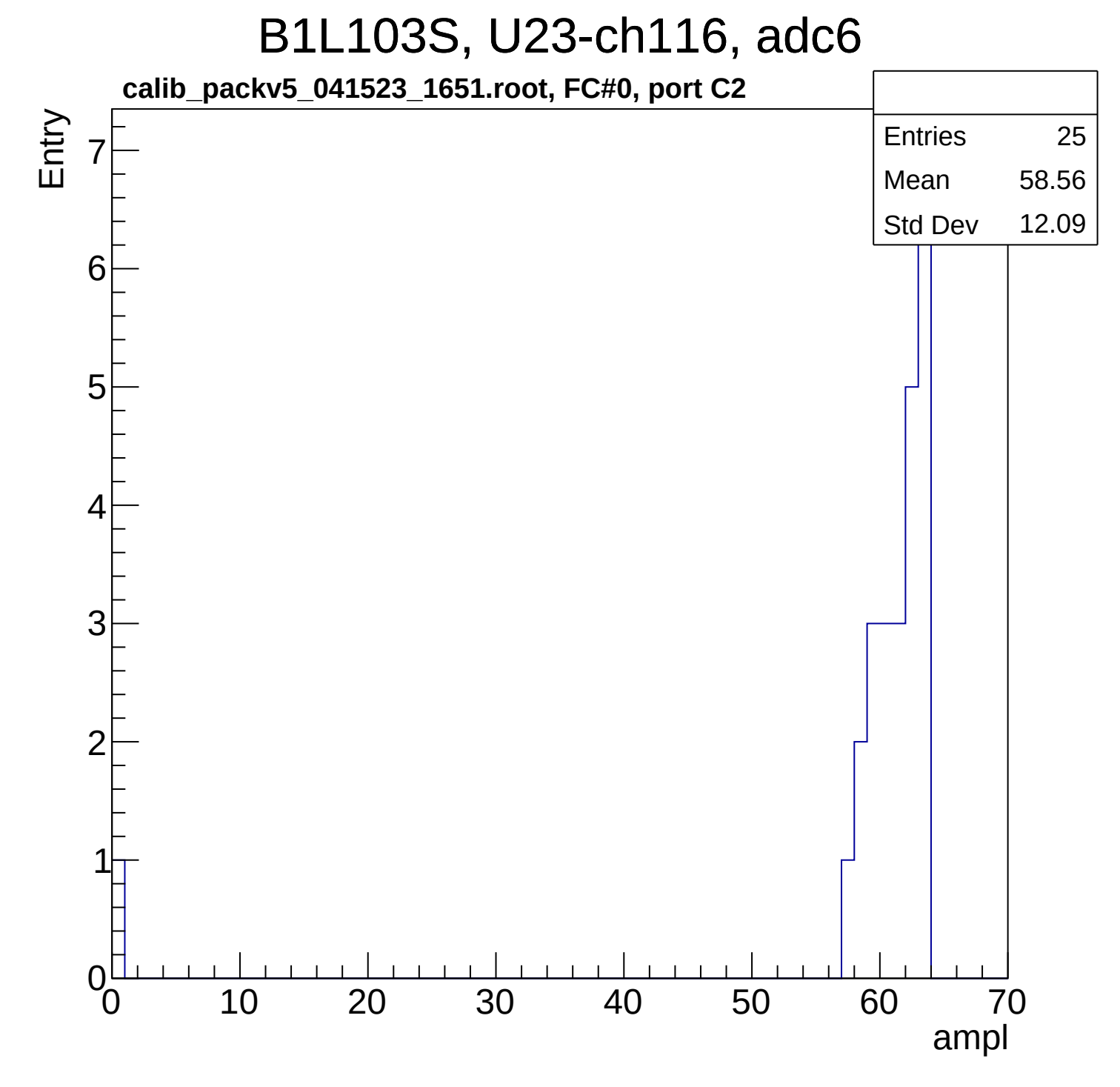

B1L103S, U23-ch116, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 29.31
Std Dev 12.45
Gaus mean : 34.5669
Gaus Width: 3.7107
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

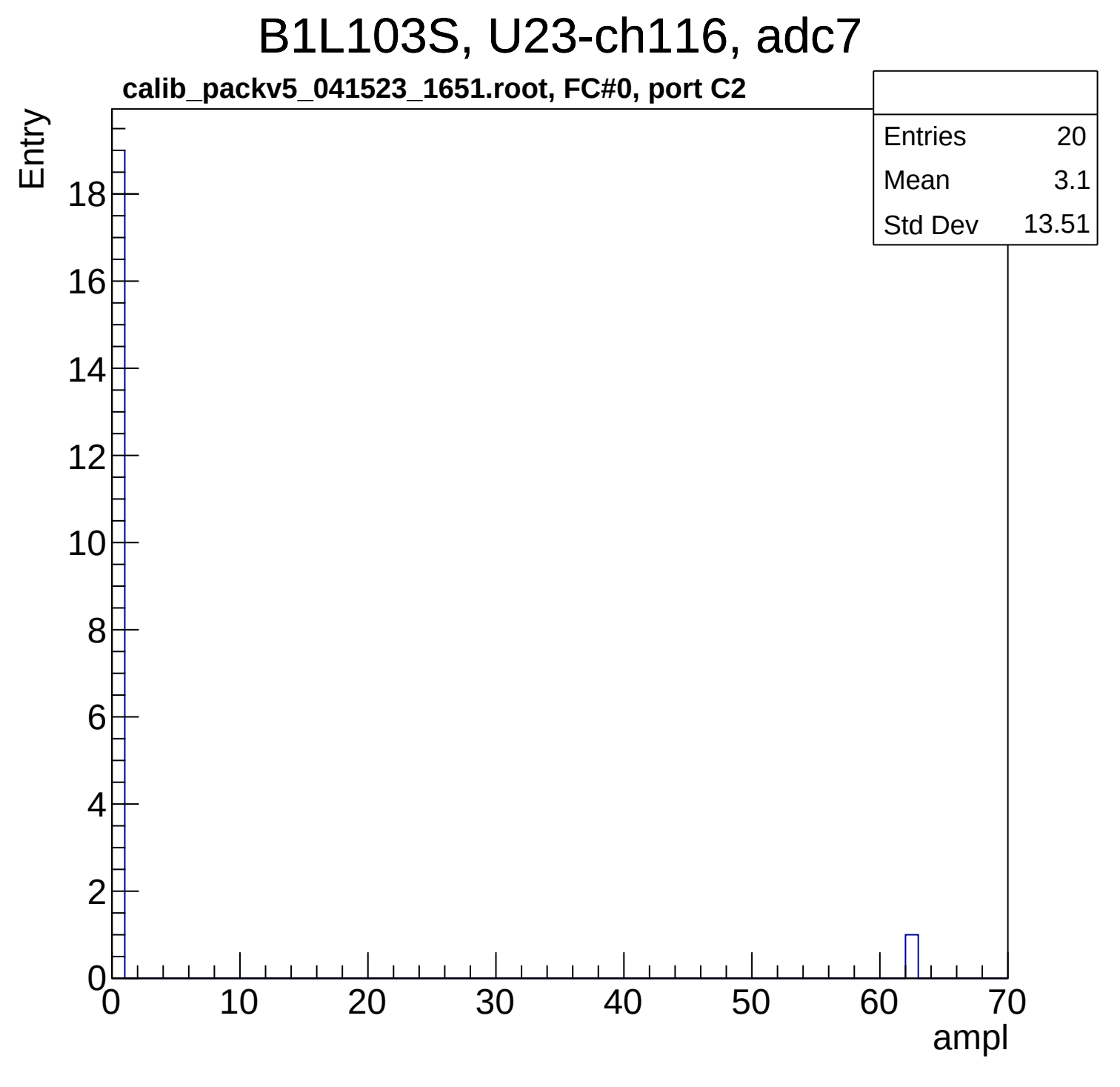

B1L103S, U23-ch116, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 36.3
Std Dev 10.85
Gaus mean : 40.1953
Gaus Width: 4.2596
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

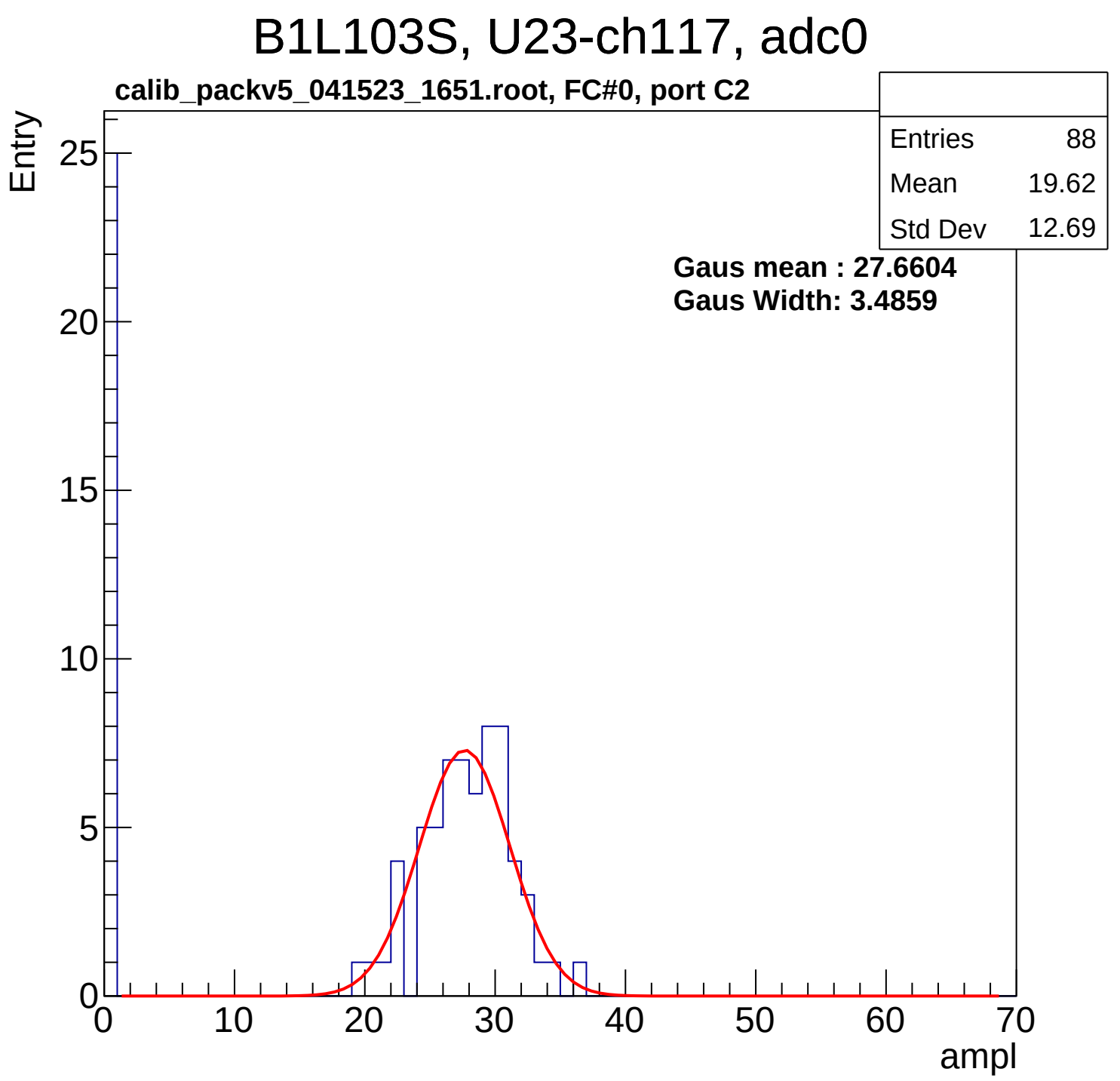

B1L103S, U23-ch116, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 46.5
Std Dev 3.845
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

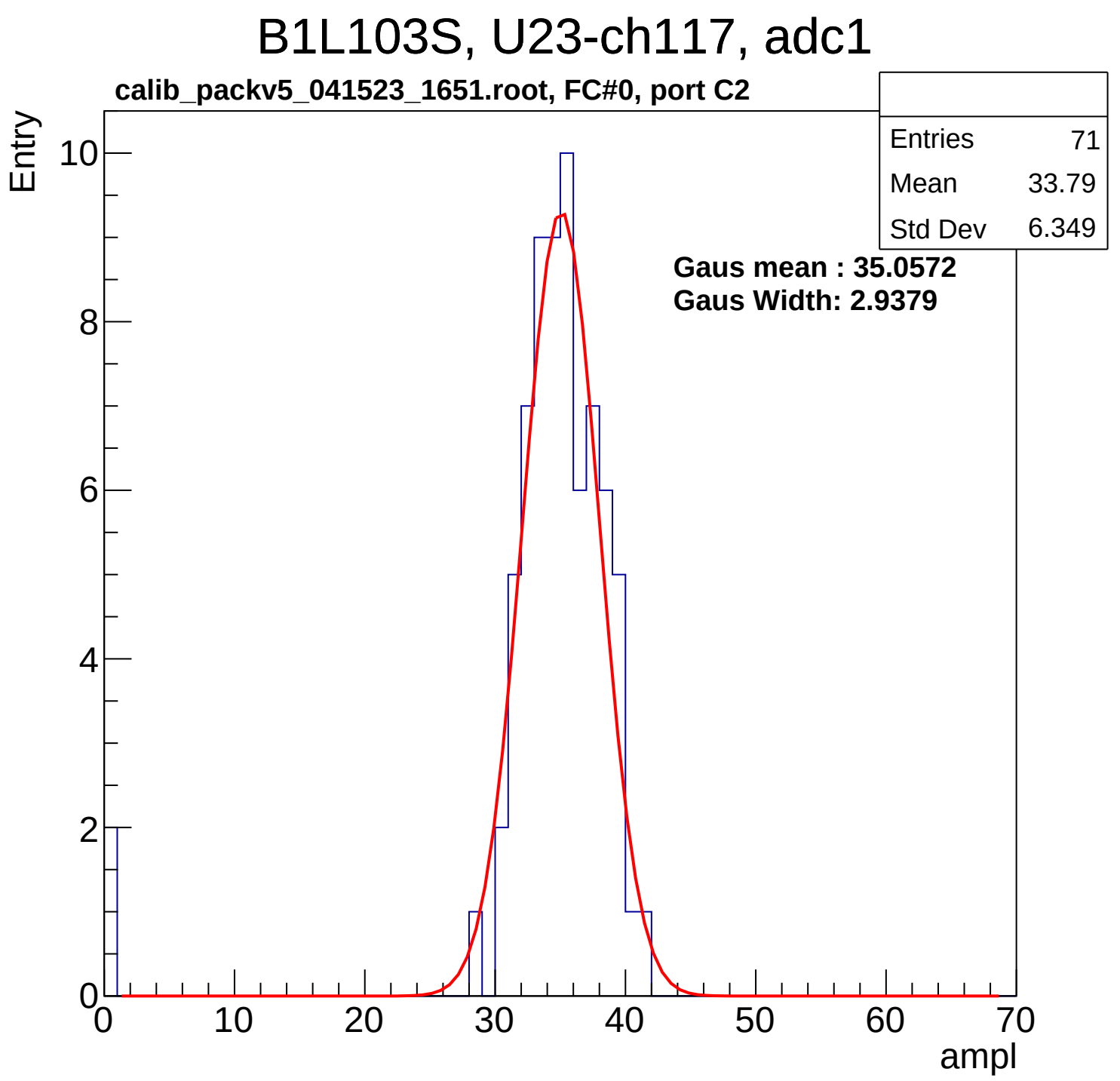

B1L103S, U23-ch116, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 53.56
Std Dev 3.278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

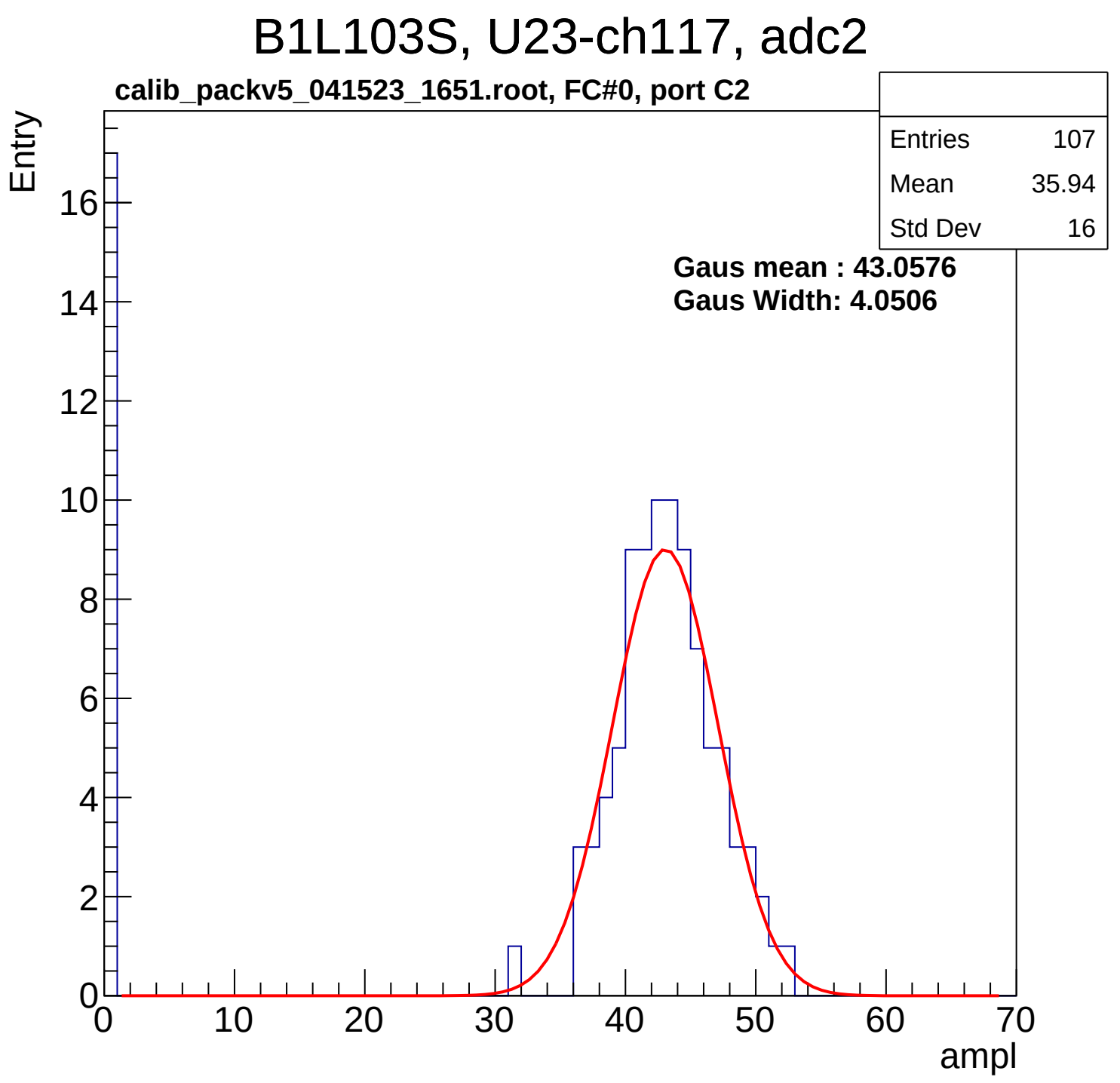

B1L103S, U23-ch116, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 38
Mean 59.03
Std Dev 2.631
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

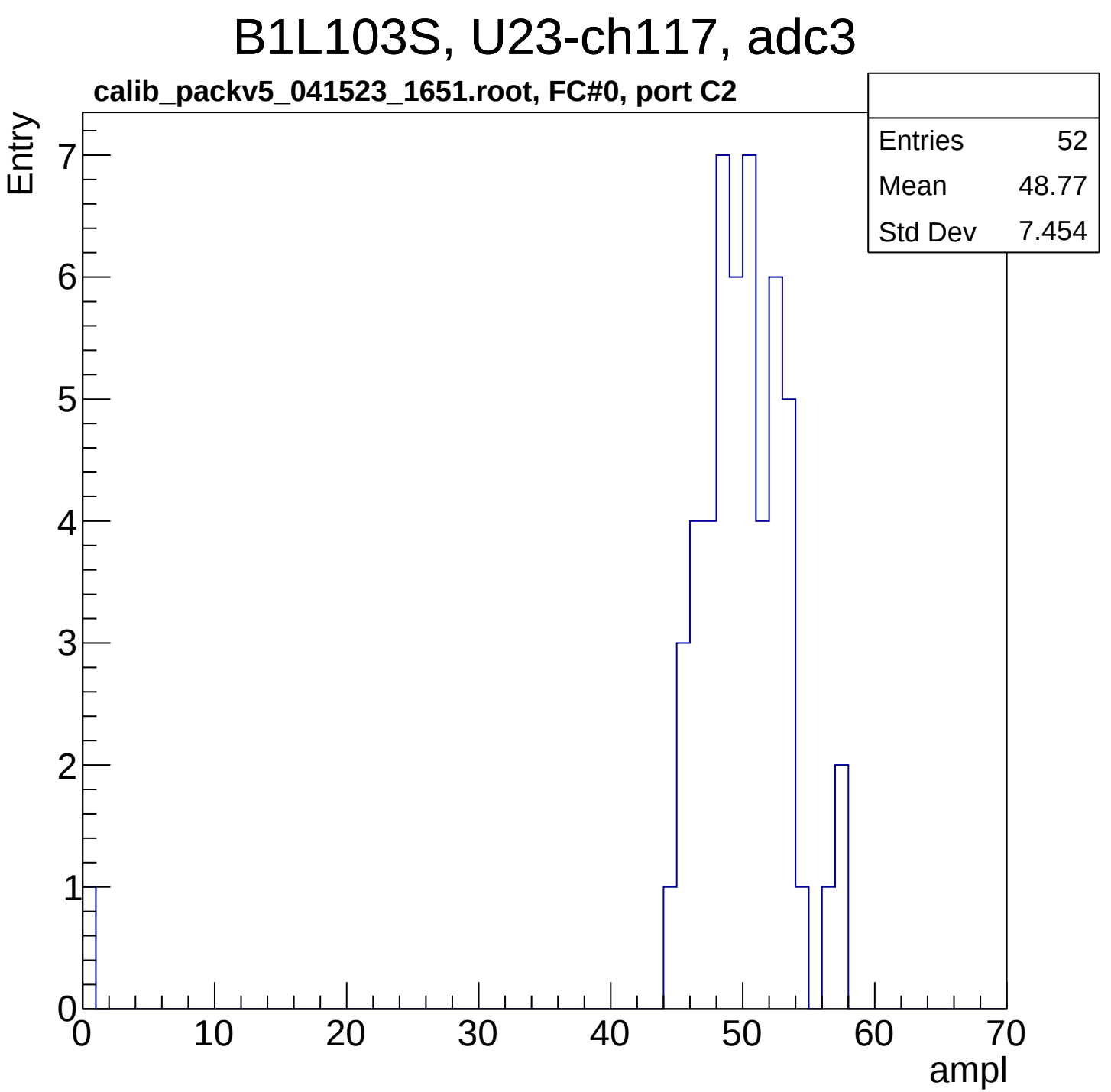

B1L103S, U23-ch116, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 25
Mean 58.56
Std Dev 12.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

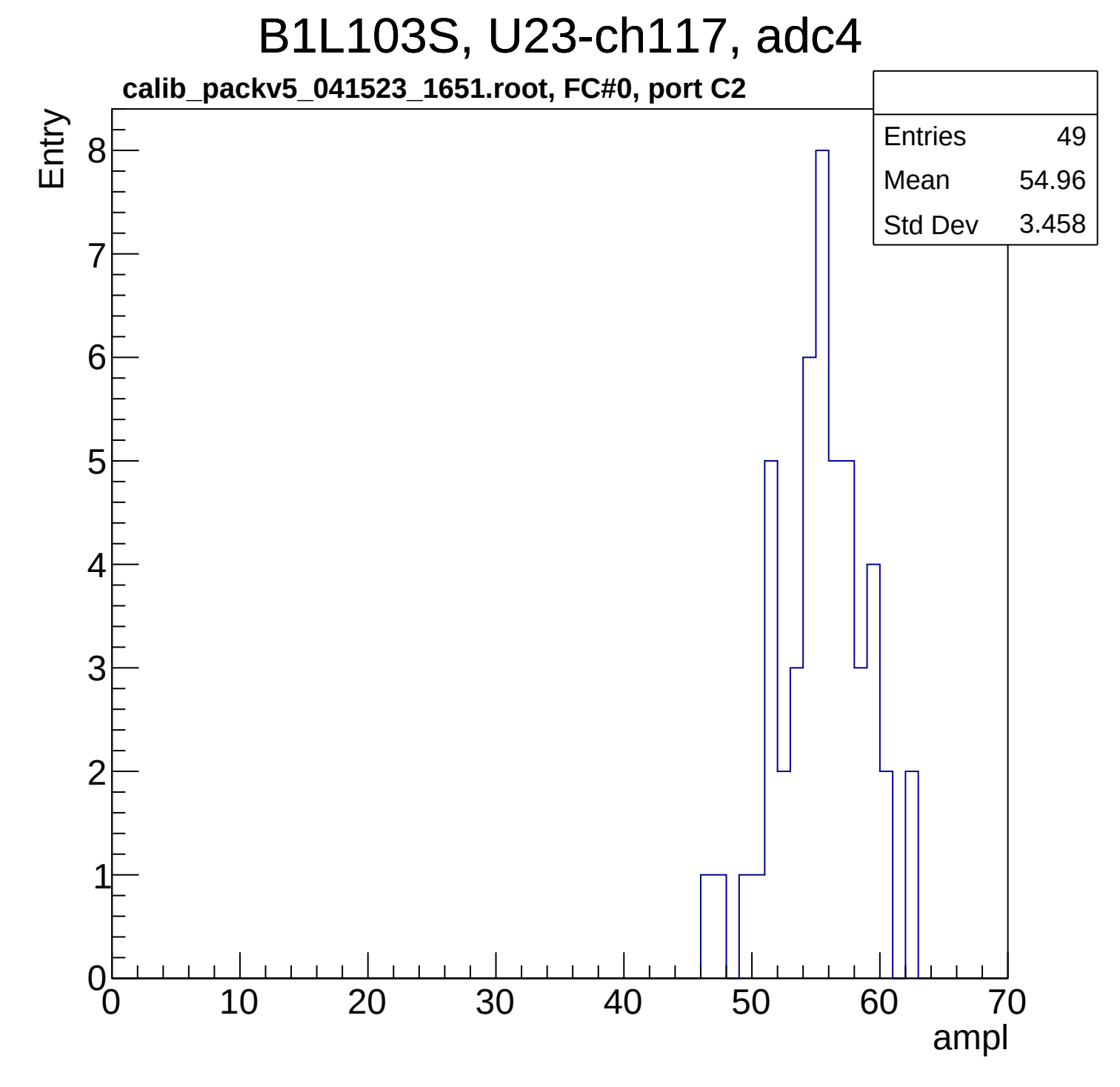

B1L103S, U23-ch116, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.1
Std Dev 13.51
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

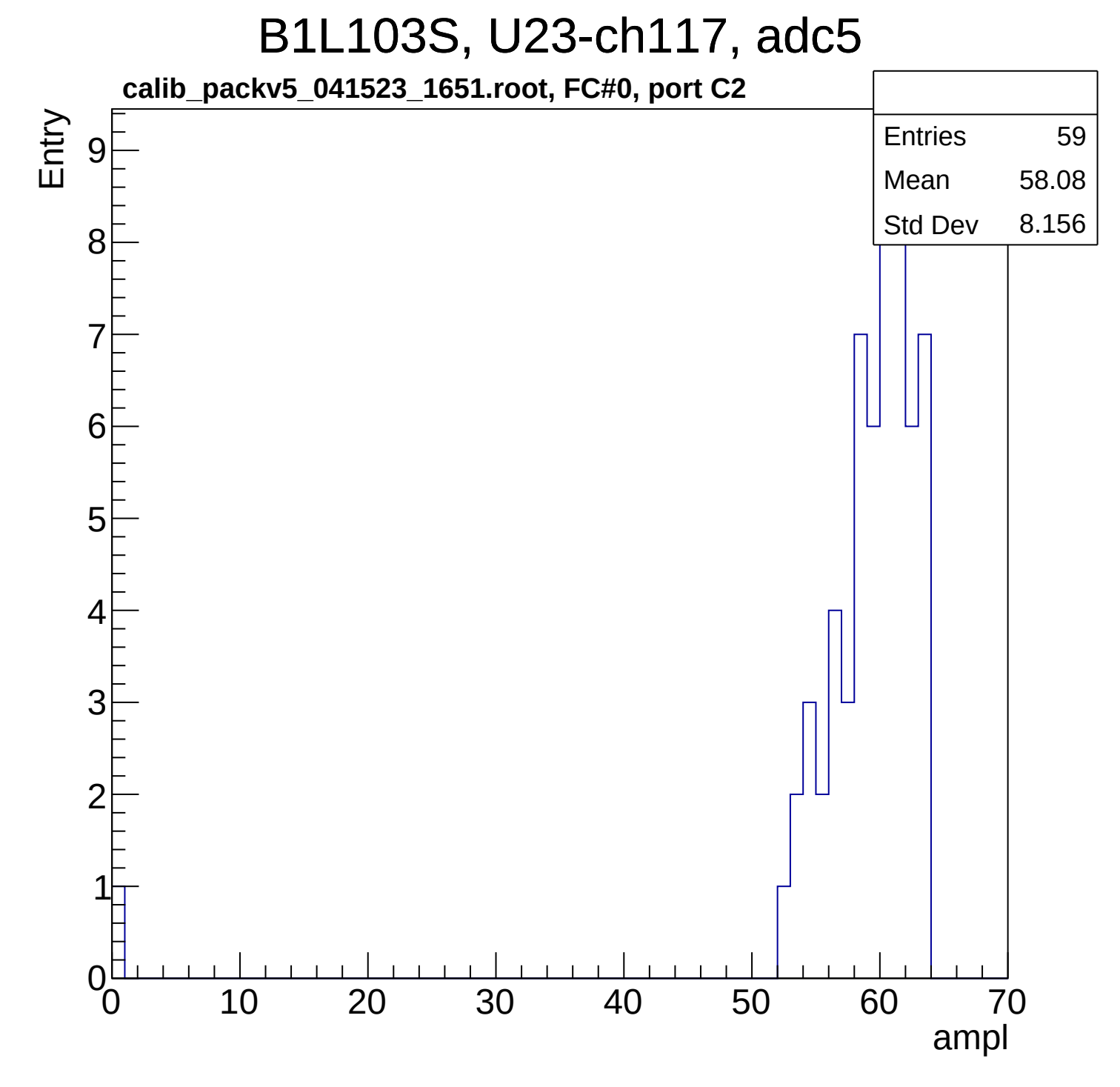

B1L103S, U23-ch117, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 88
Mean 19.62
Std Dev 12.69
Gaus mean : 27.6604
Gaus Width: 3.4859
Entry
ampl
0
5
10
15
20
25
0
10
20
30
40
50
60
70

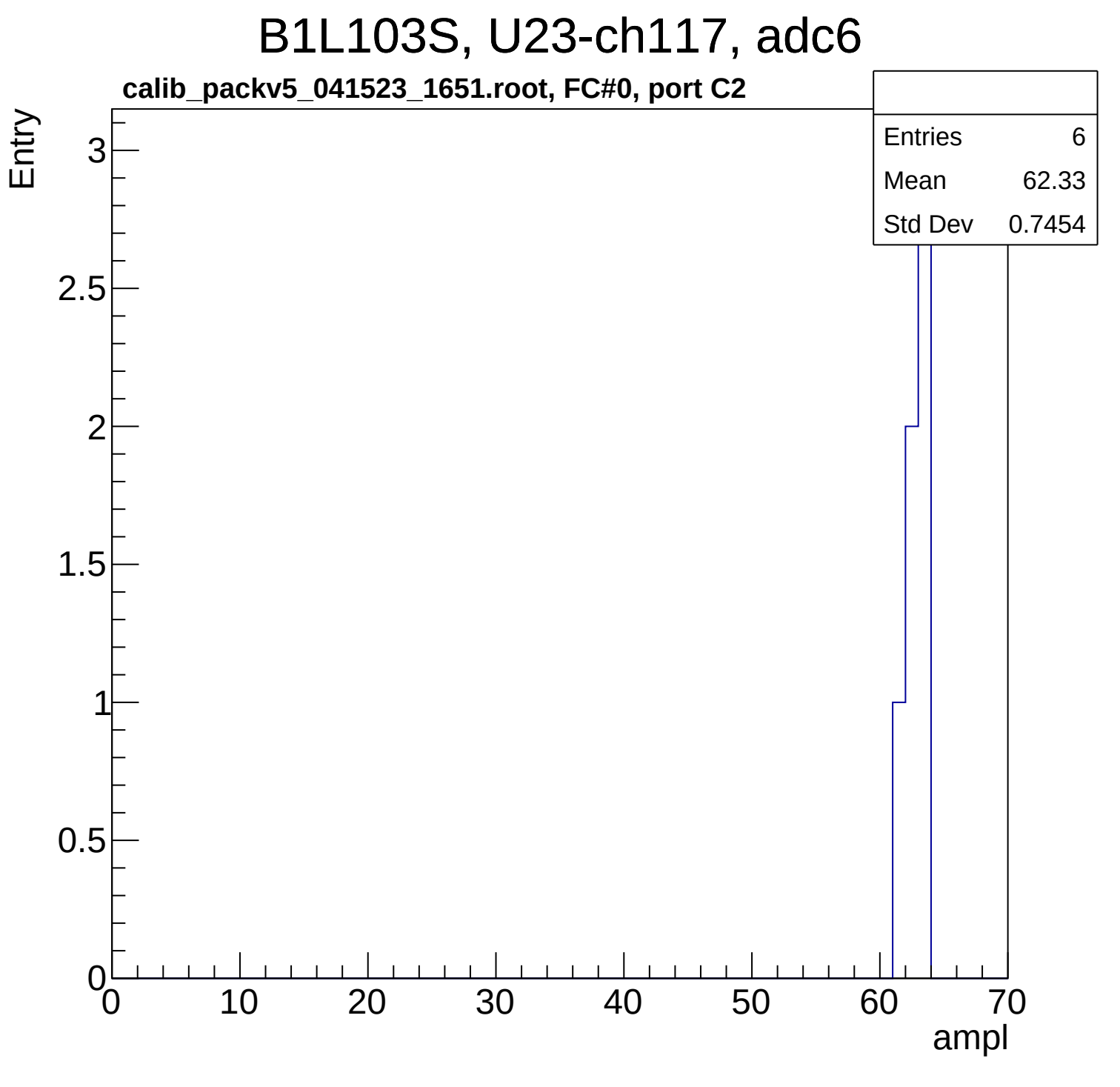

B1L103S, U23-ch117, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 33.79
Std Dev 6.349
Gaus mean : 35.0572
Gaus Width: 2.9379
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

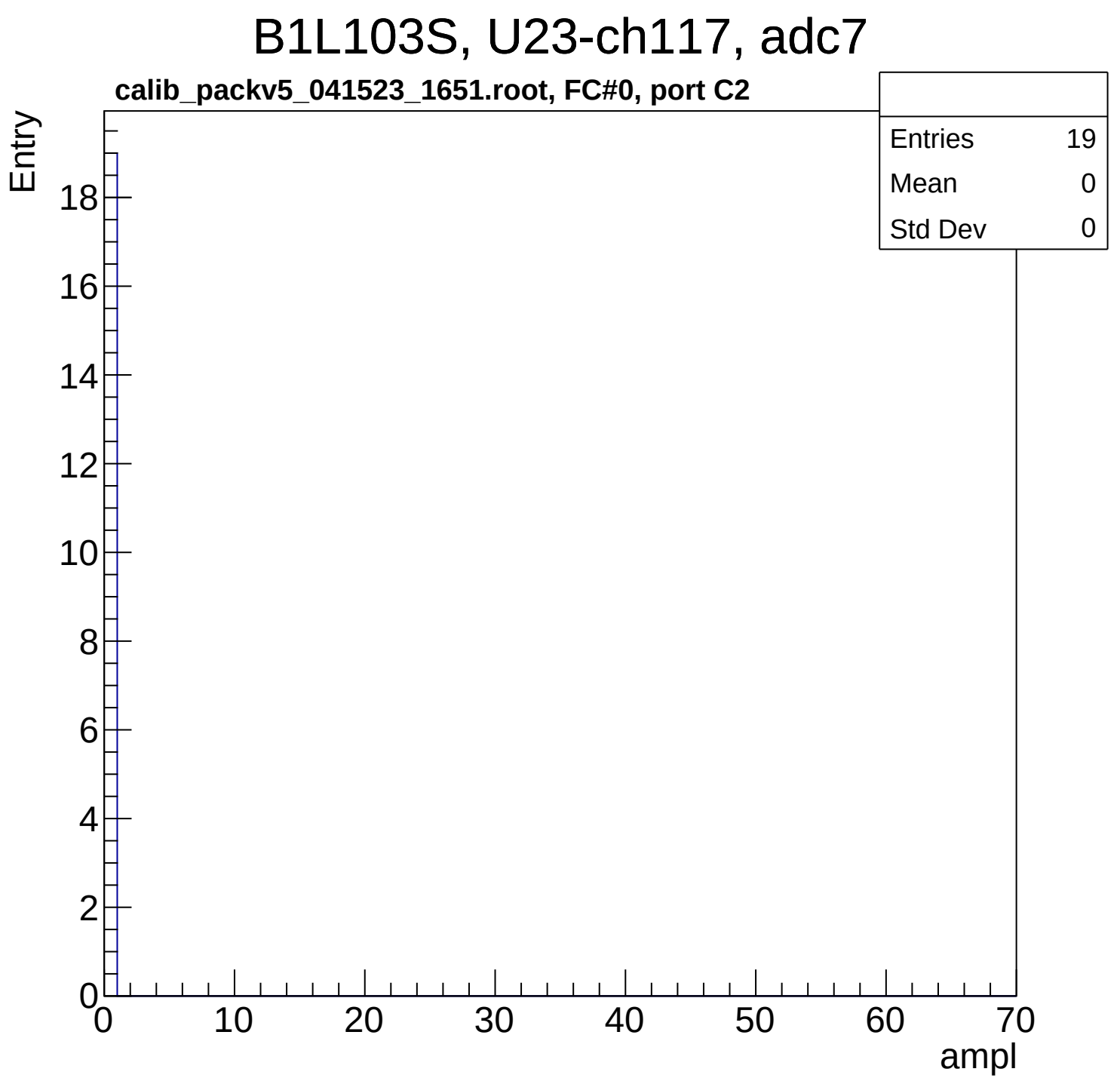

B1L103S, U23-ch117, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 107
Mean 35.94
Std Dev 16
Gaus mean : 43.0576
Gaus Width: 4.0506
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

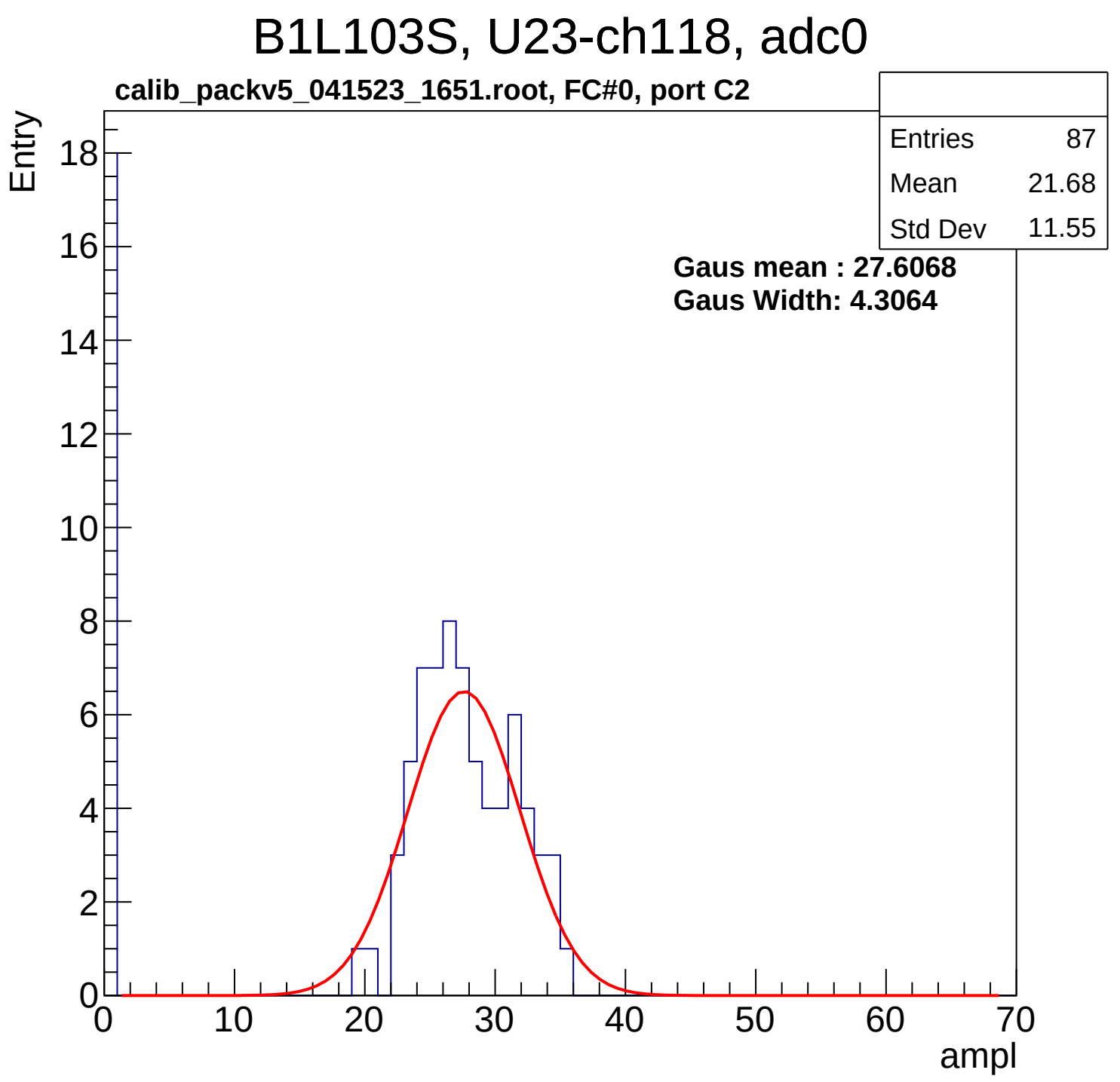

B1L103S, U23-ch117, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 48.77
Std Dev 7.454
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

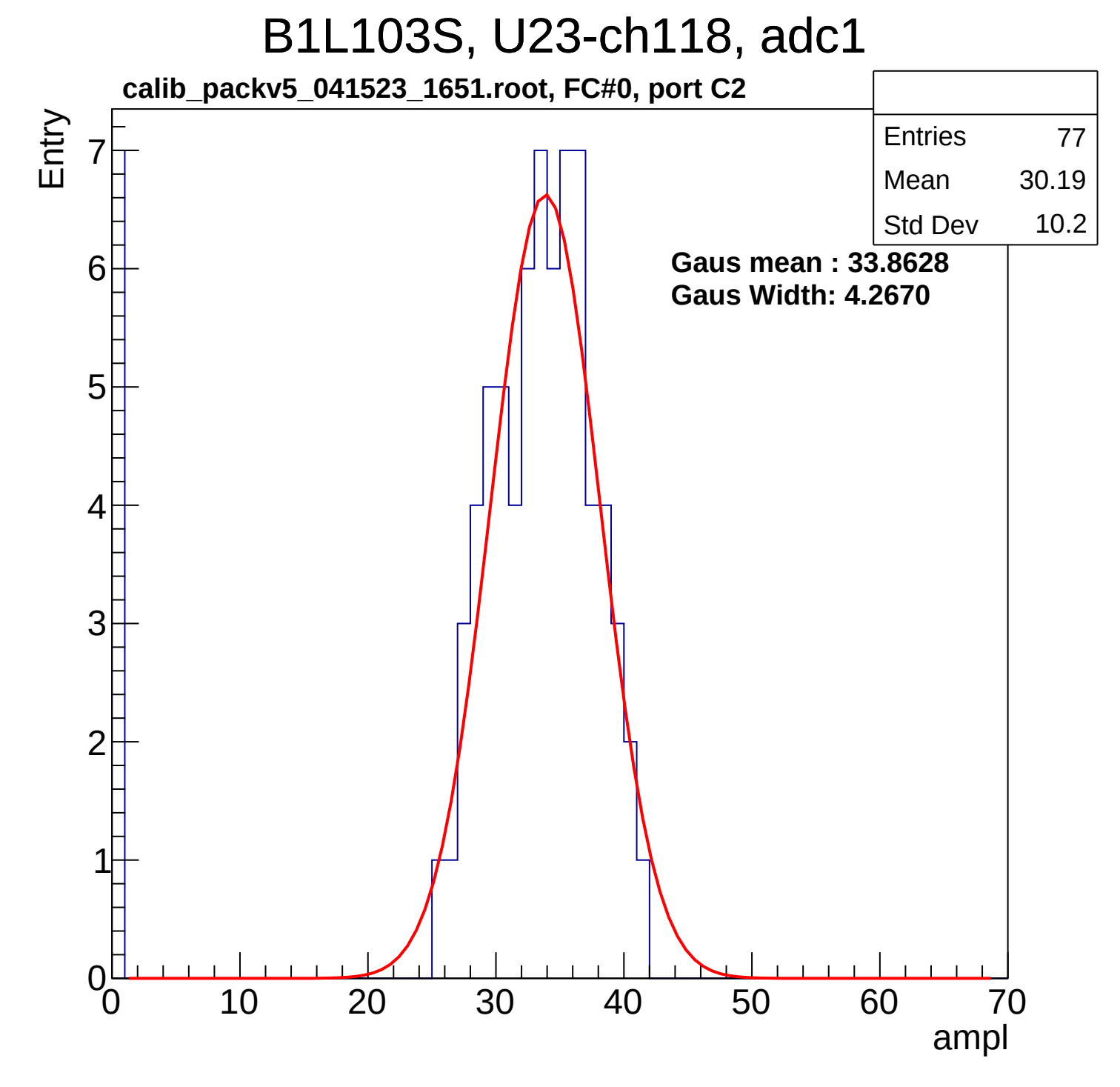

B1L103S, U23-ch117, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 54.96
Std Dev 3.458
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

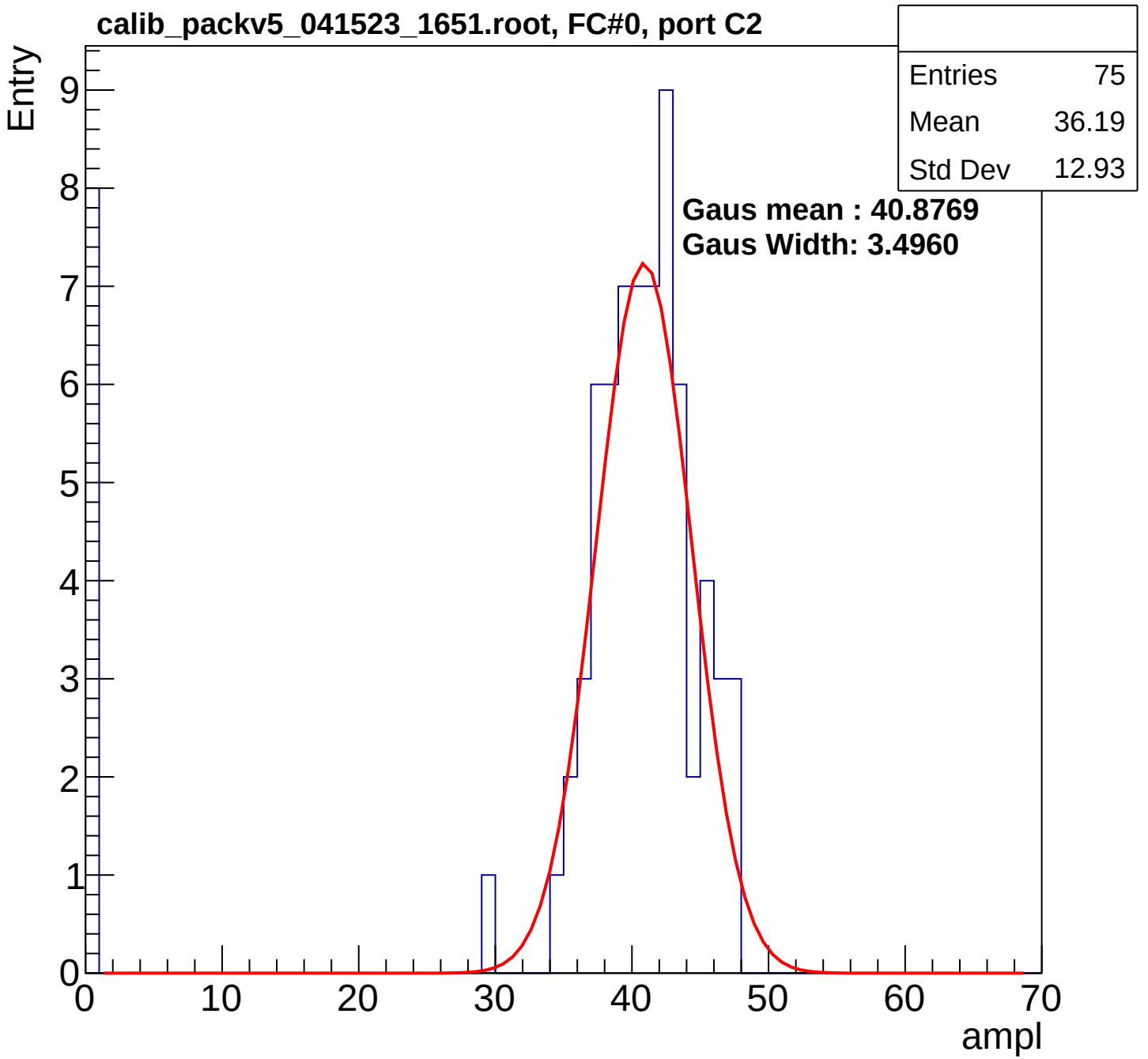

B1L103S, U23-ch117, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 59
Mean 58.08
Std Dev 8.156
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

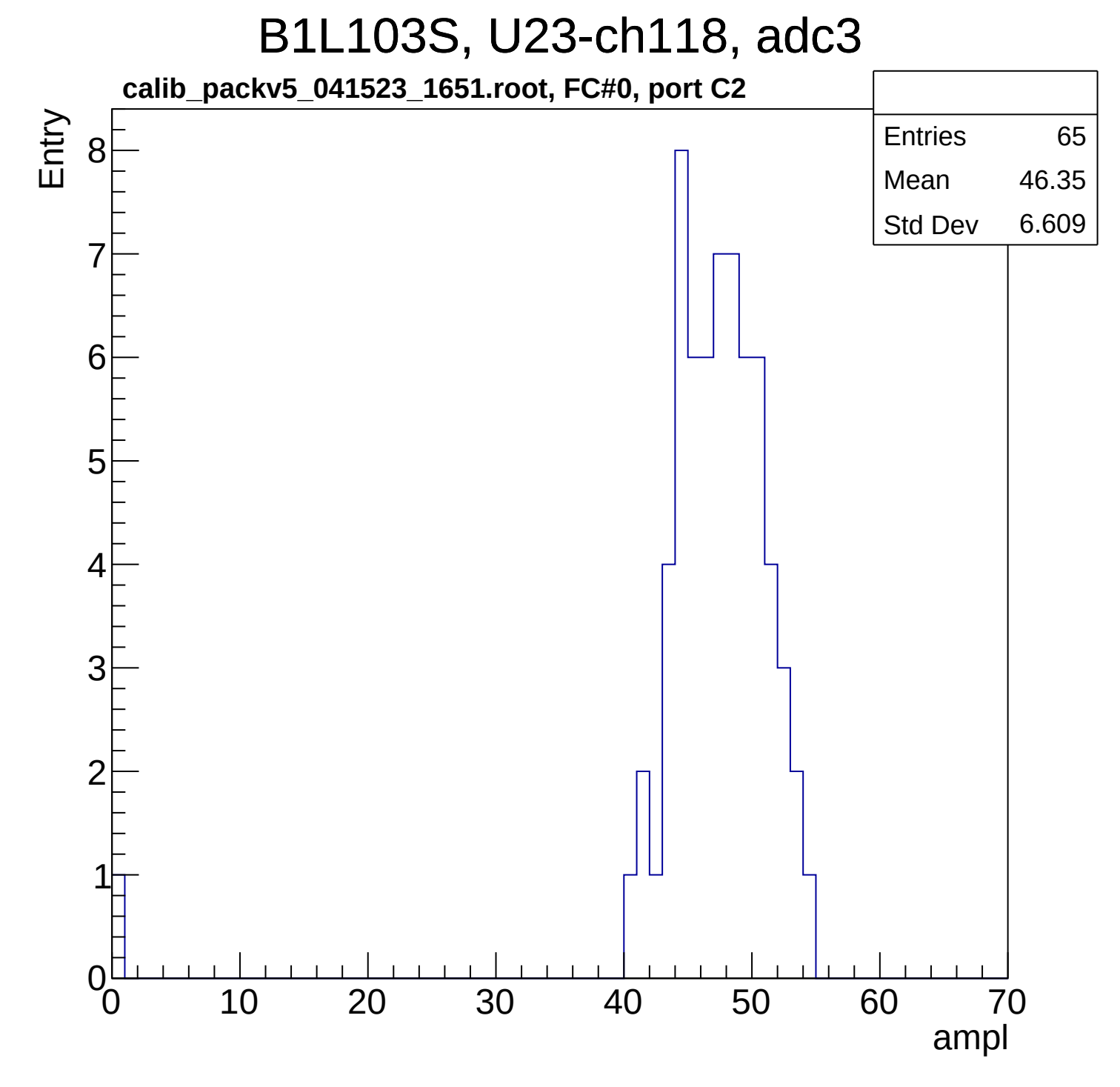

B1L103S, U23-ch117, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

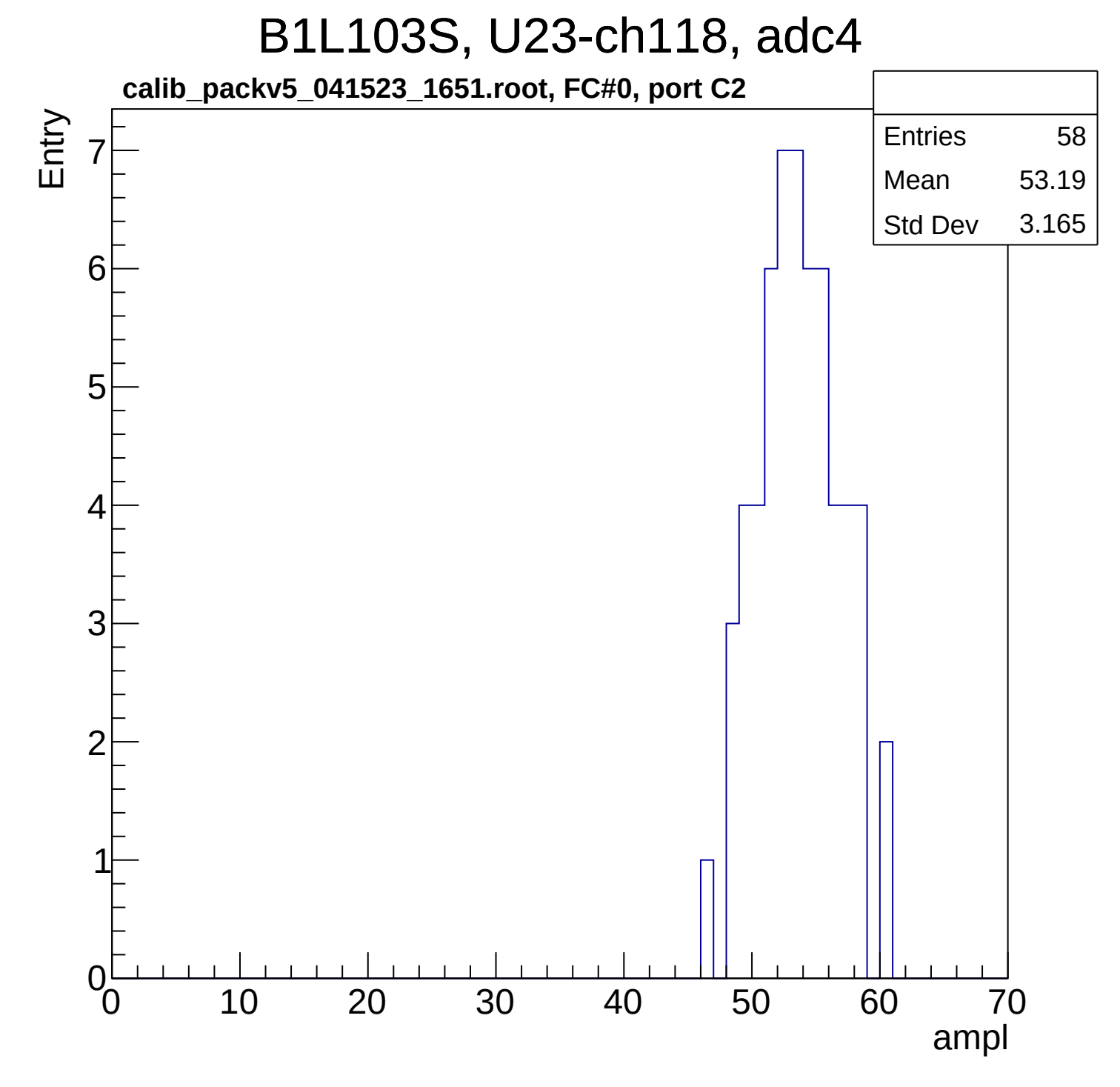

B1L103S, U23-ch117, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

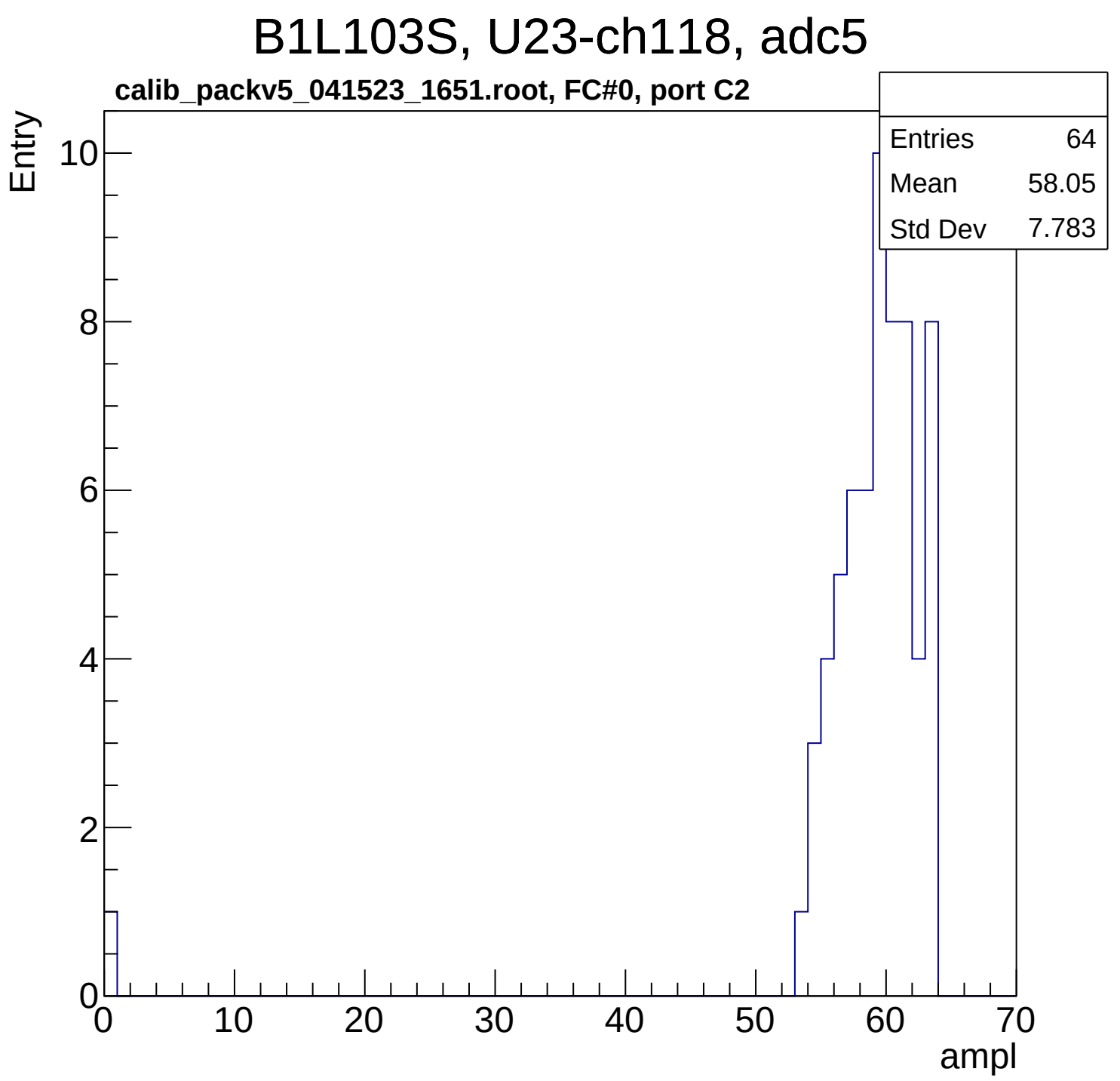

B1L103S, U23-ch118, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 87
Mean 21.68
Std Dev 11.55
Gaus mean : 27.6068
Gaus Width: 4.3064
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

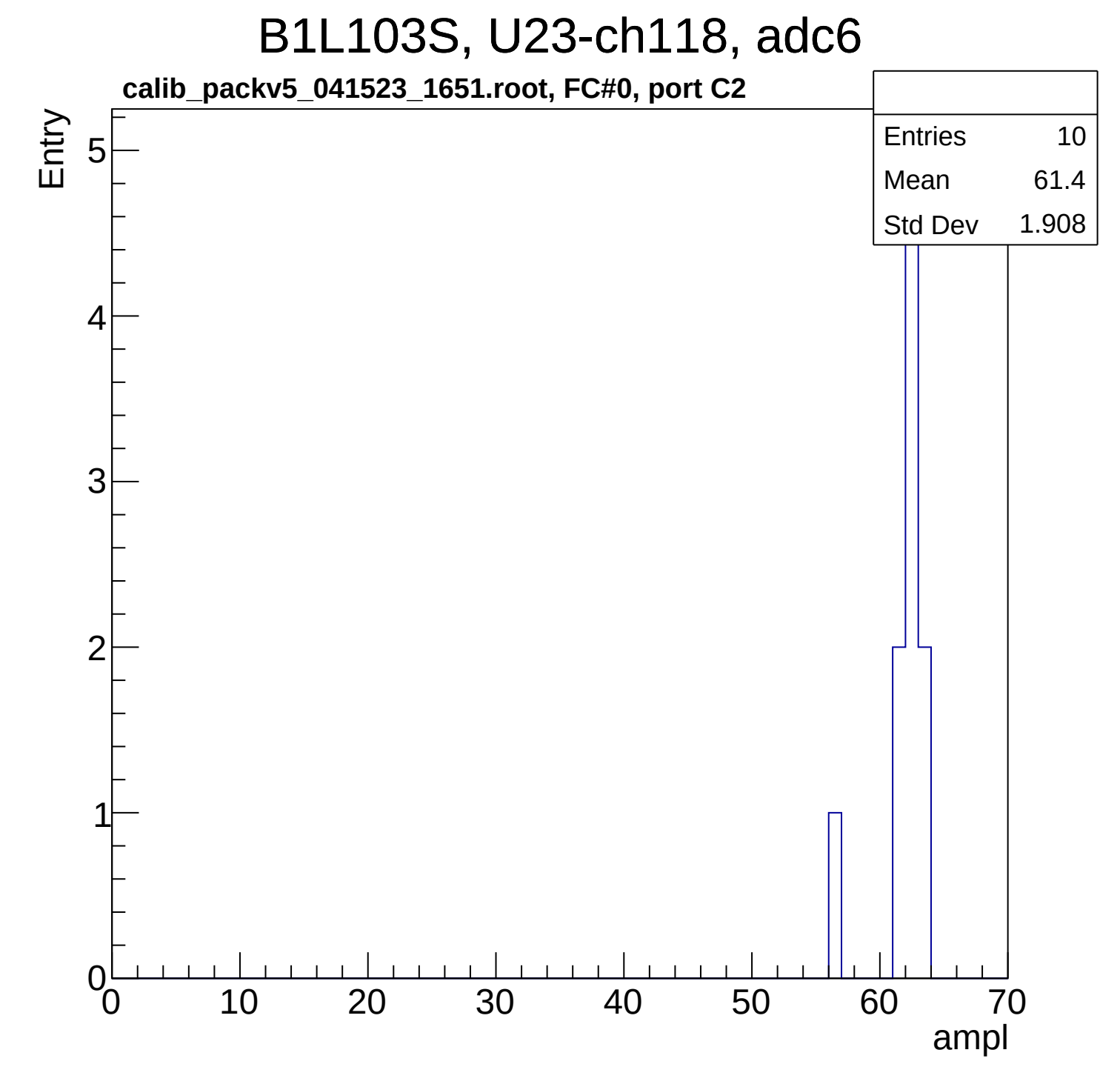

B1L103S, U23-ch118, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 77
Mean 30.19
Std Dev 10.2
Gaus mean : 33.8628
Gaus Width: 4.2670
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

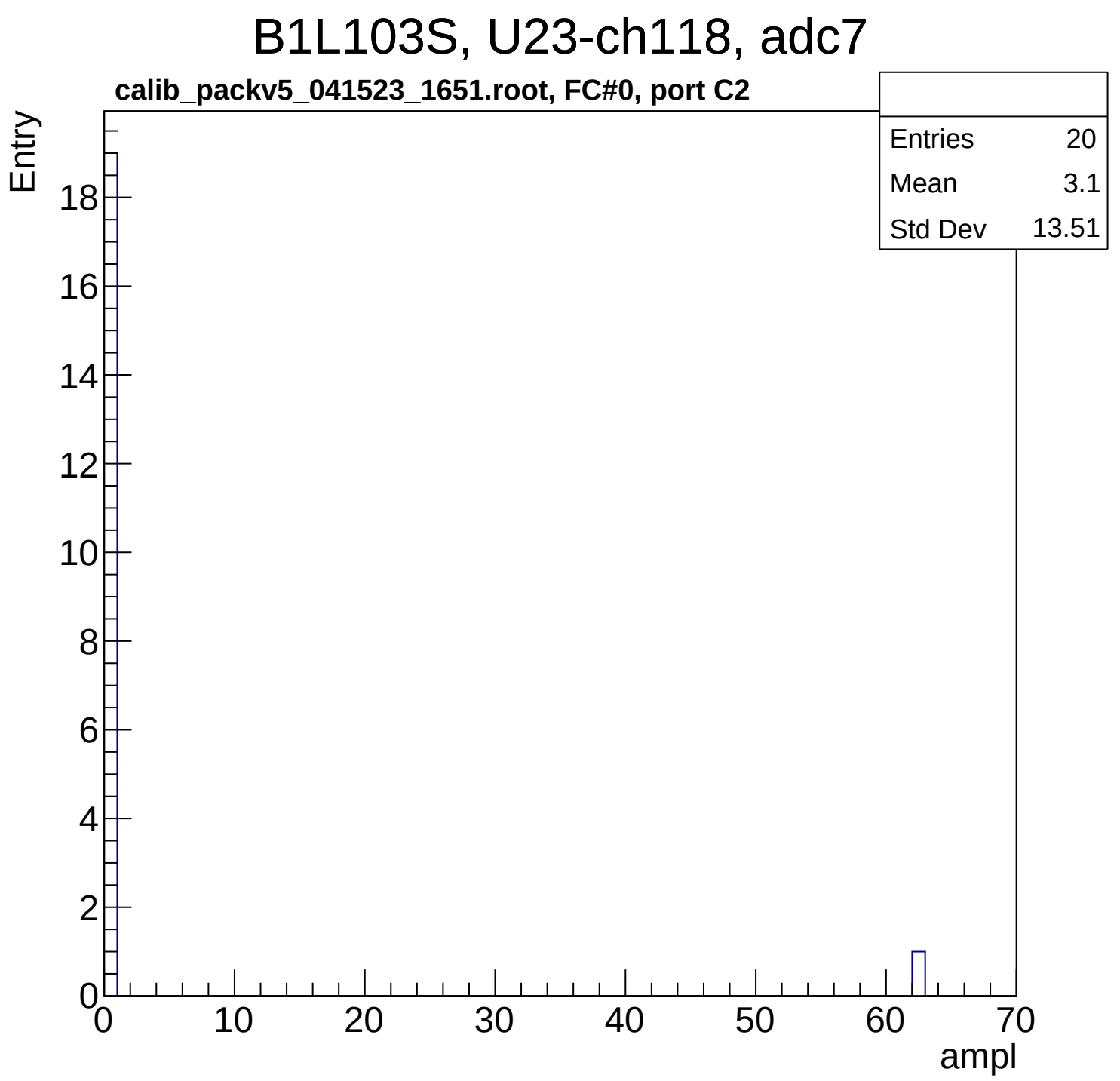

B1L103S, U23-ch118, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 36.19
Std Dev 12.93
Gaus mean : 40.8769
Gaus Width: 3.4960
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

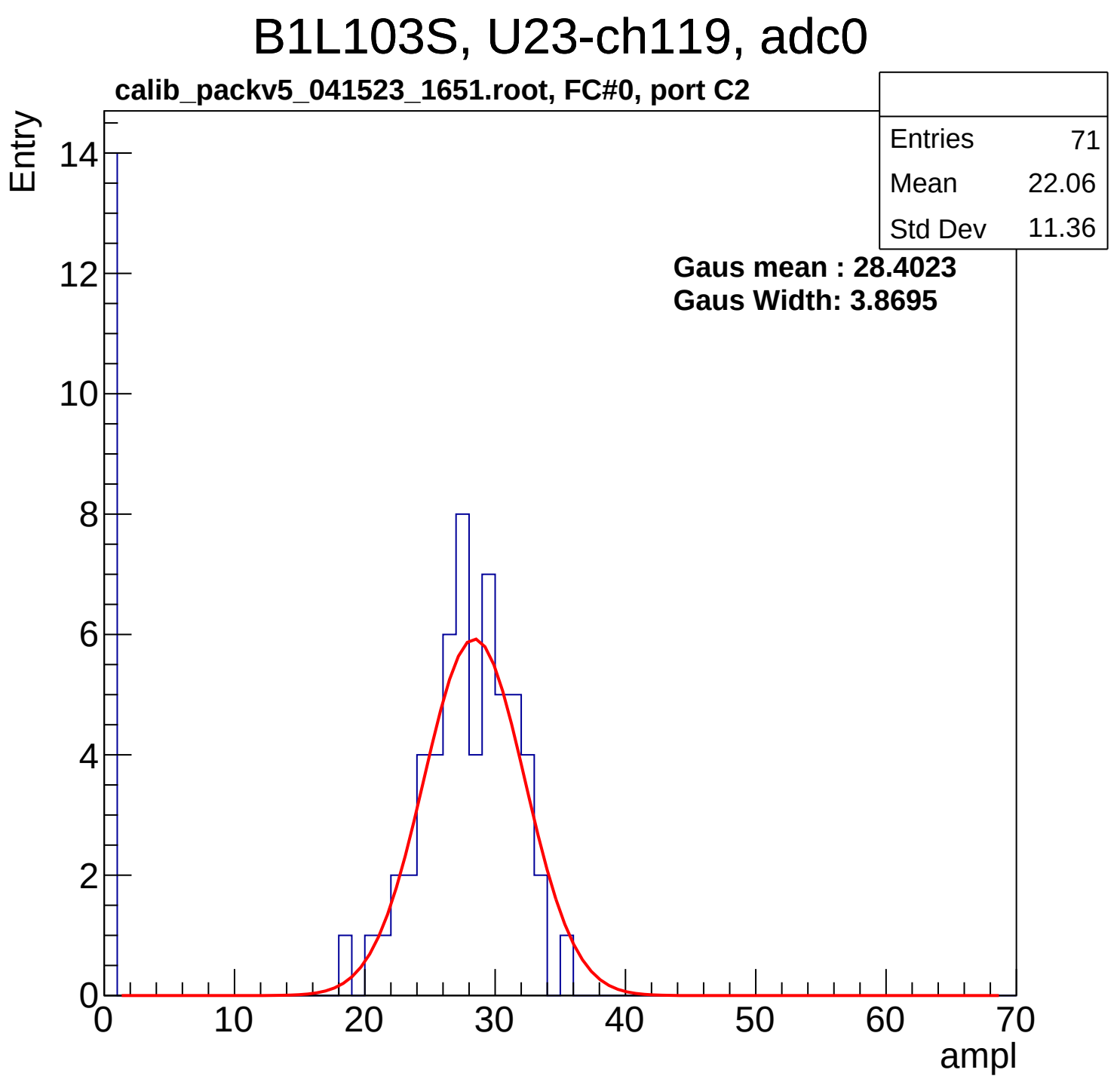

B1L103S, U23-ch118, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 46.35
Std Dev 6.609
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

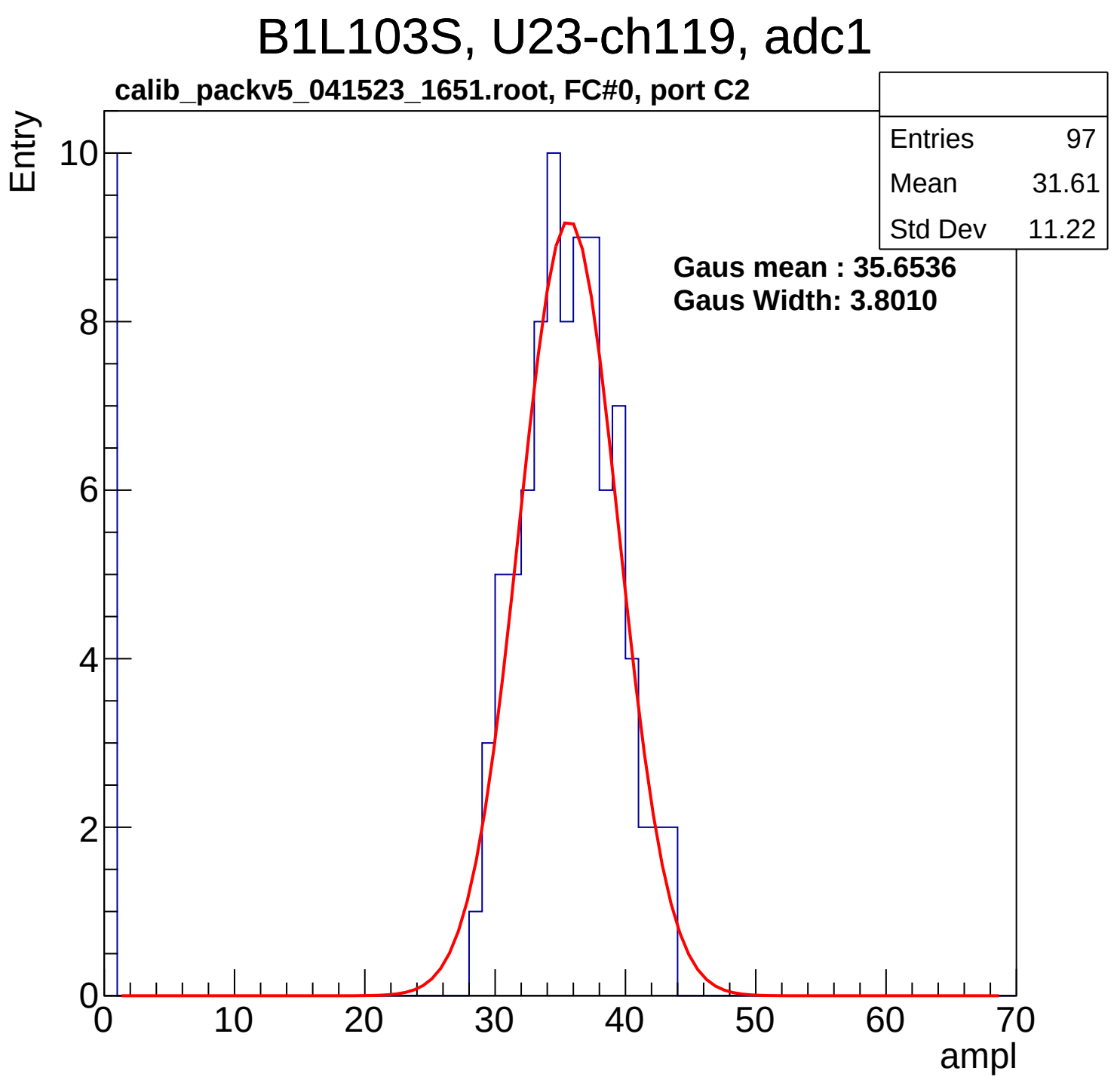

B1L103S, U23-ch118, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 53.19
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

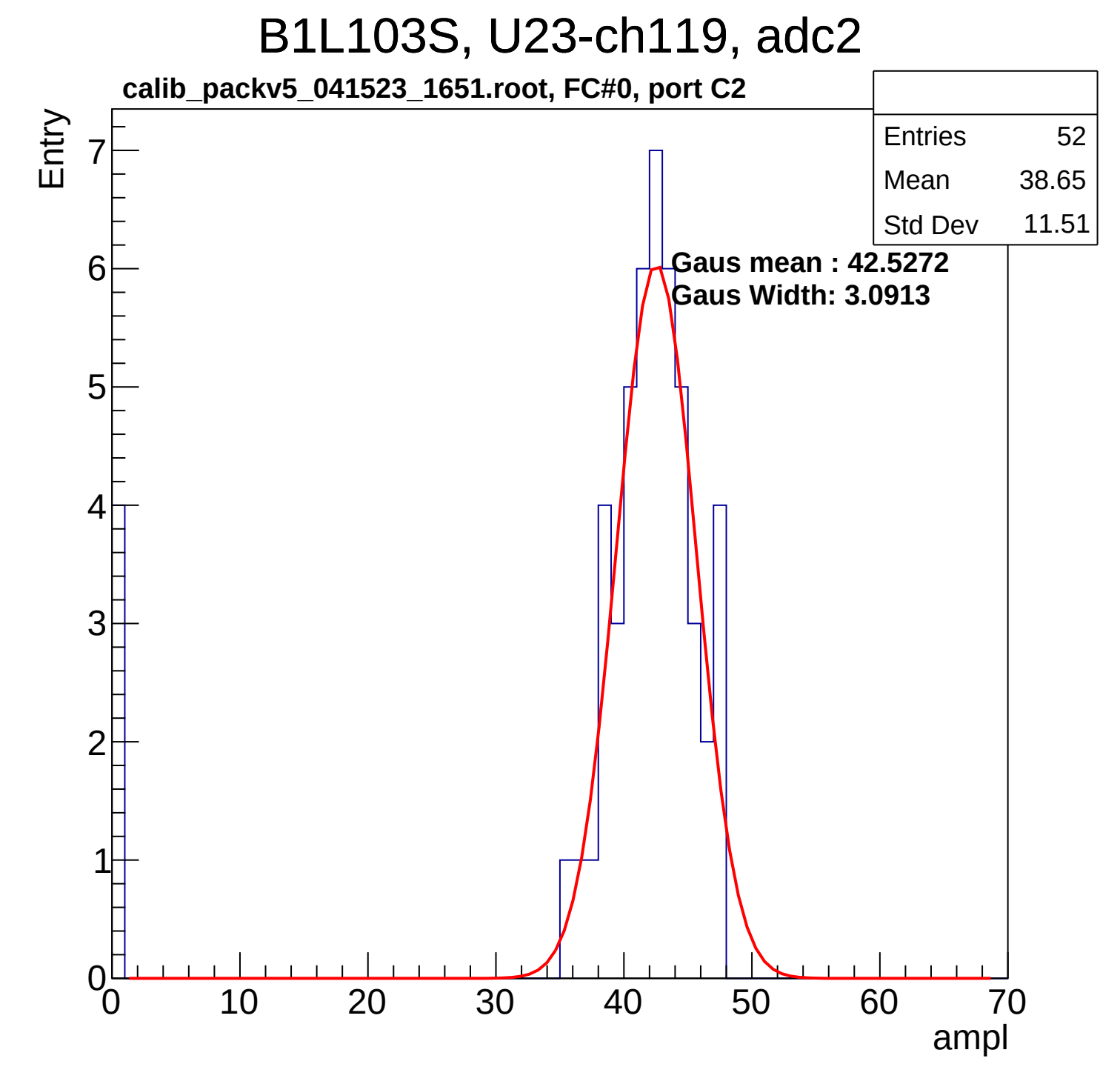

B1L103S, U23-ch118, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 58.05
Std Dev 7.783
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

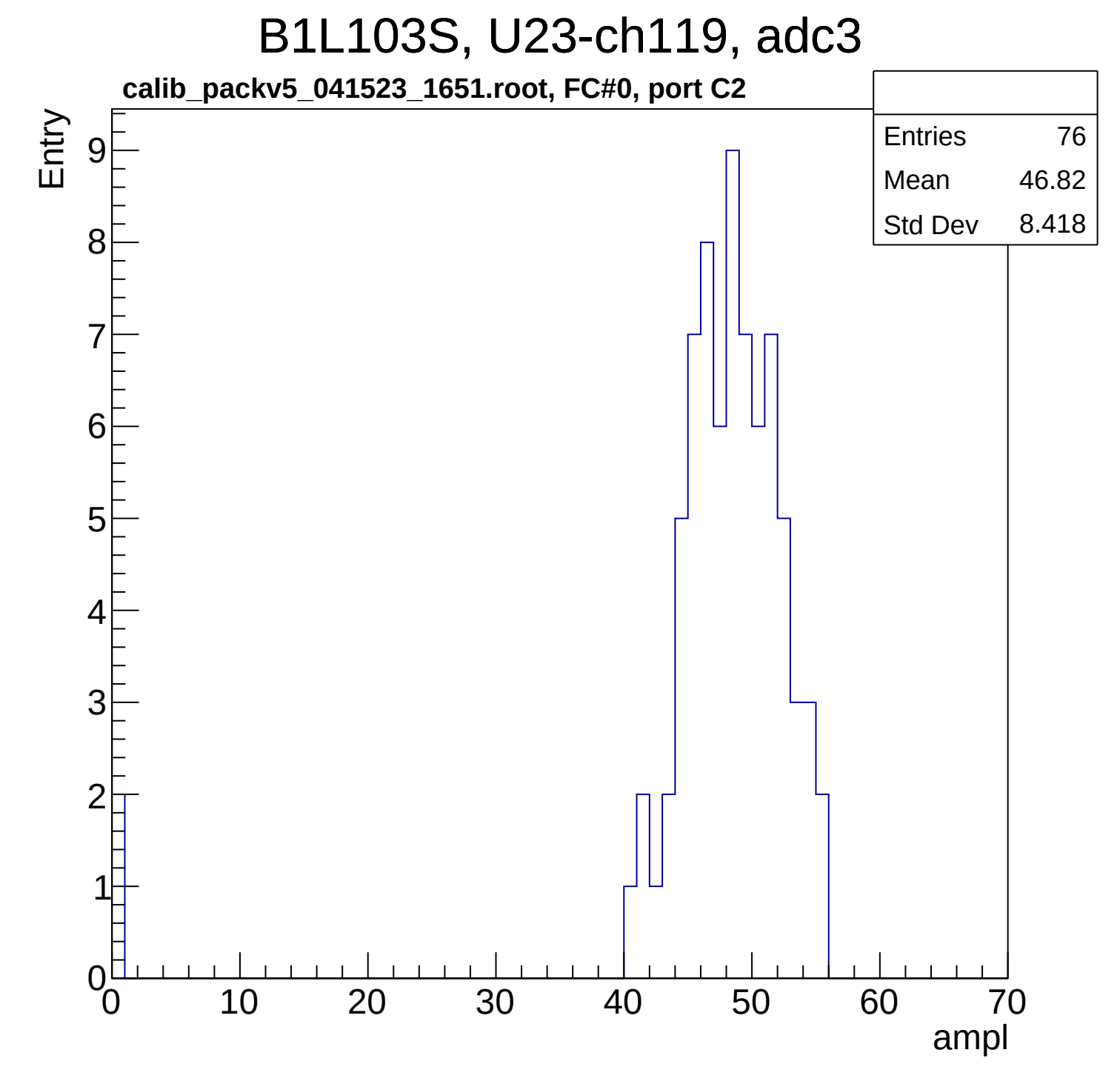

B1L103S, U23-ch118, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 10
Mean 61.4
Std Dev 1.908
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

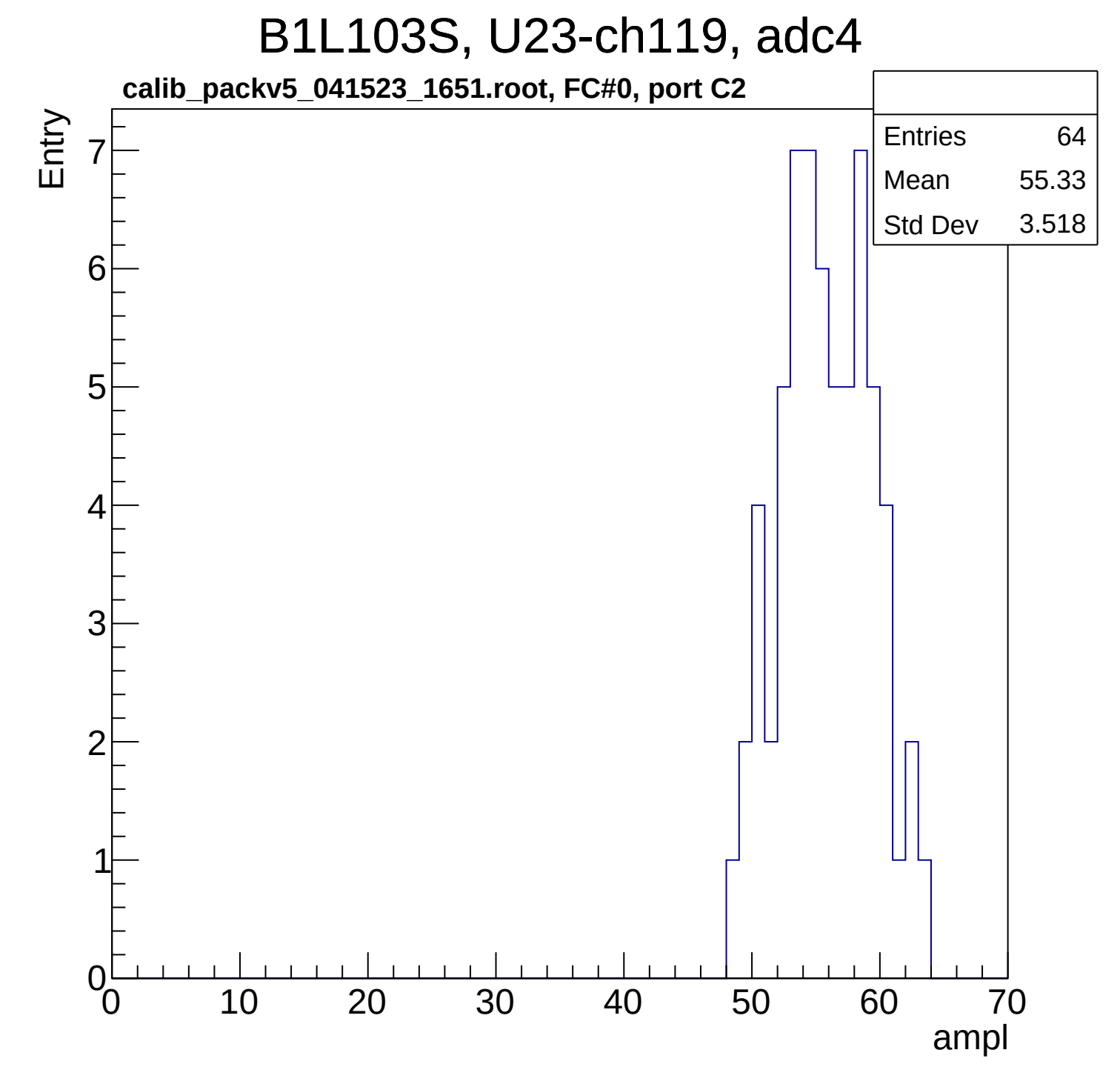

B1L103S, U23-ch118, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 3.1
Std Dev 13.51
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

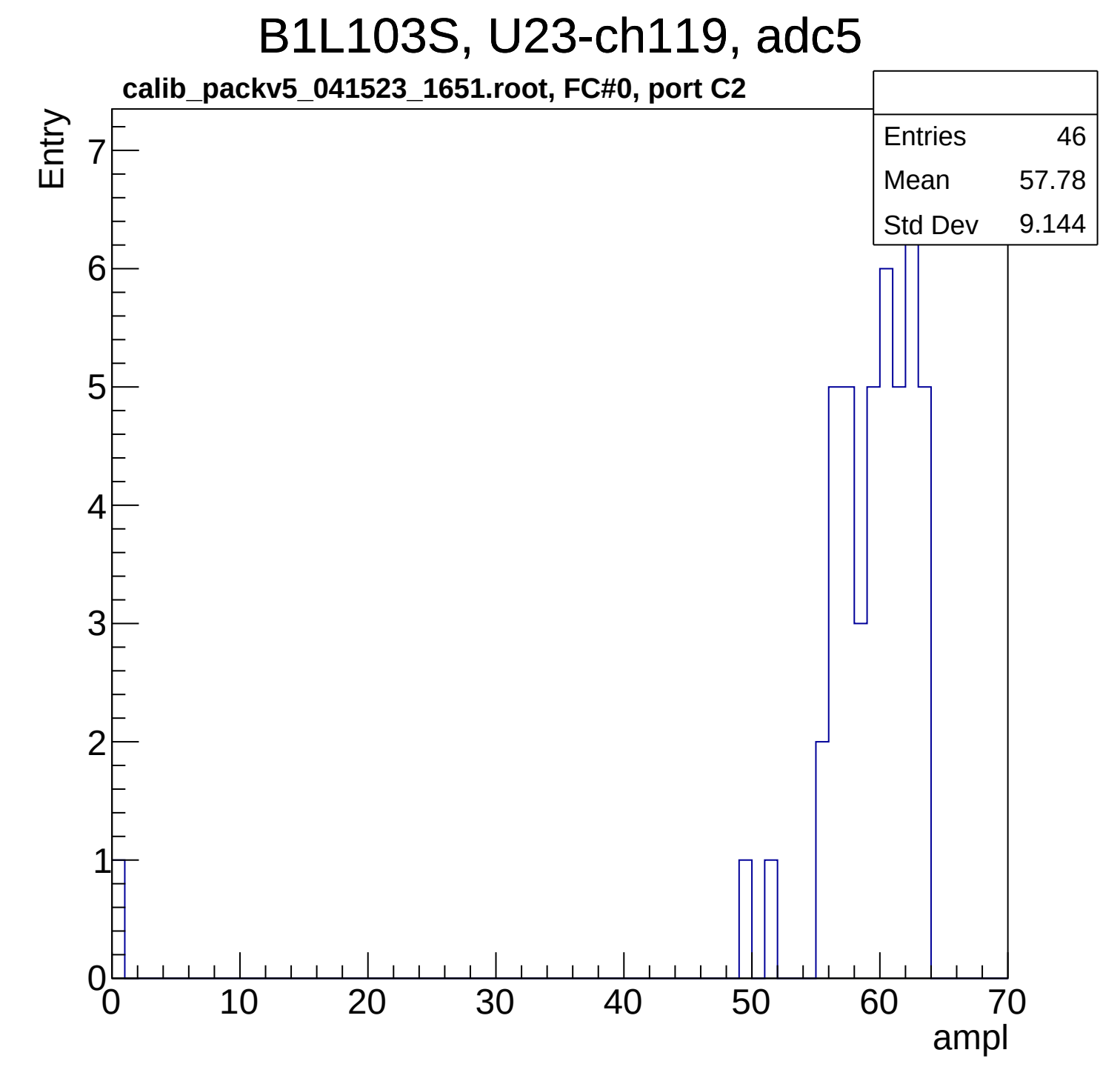

B1L103S, U23-ch119, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 22.06
Std Dev 11.36
Gaus mean : 28.4023
Gaus Width: 3.8695
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

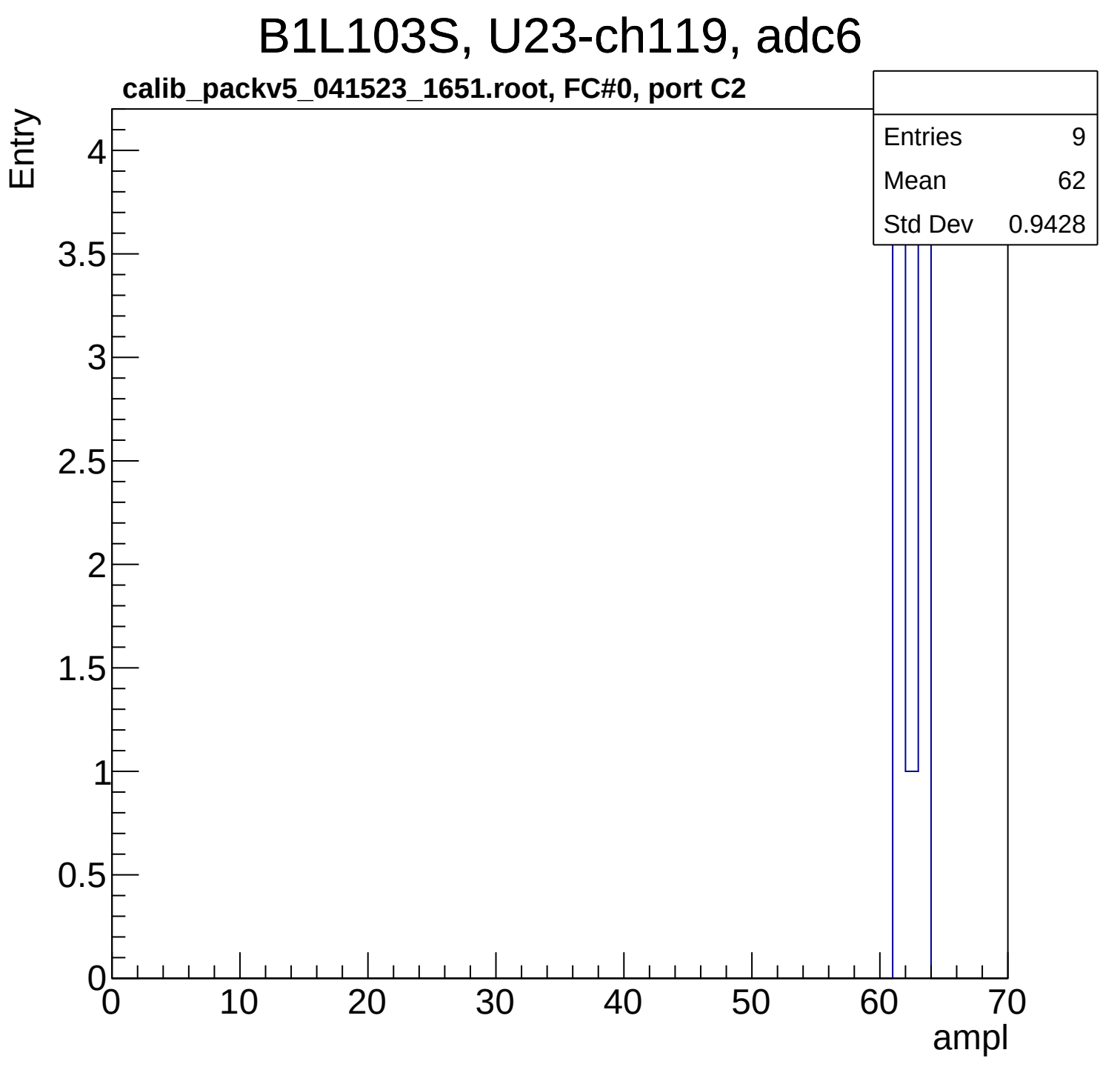

B1L103S, U23-ch119, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 97
Mean 31.61
Std Dev 11.22
Gaus mean : 35.6536
Gaus Width: 3.8010
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

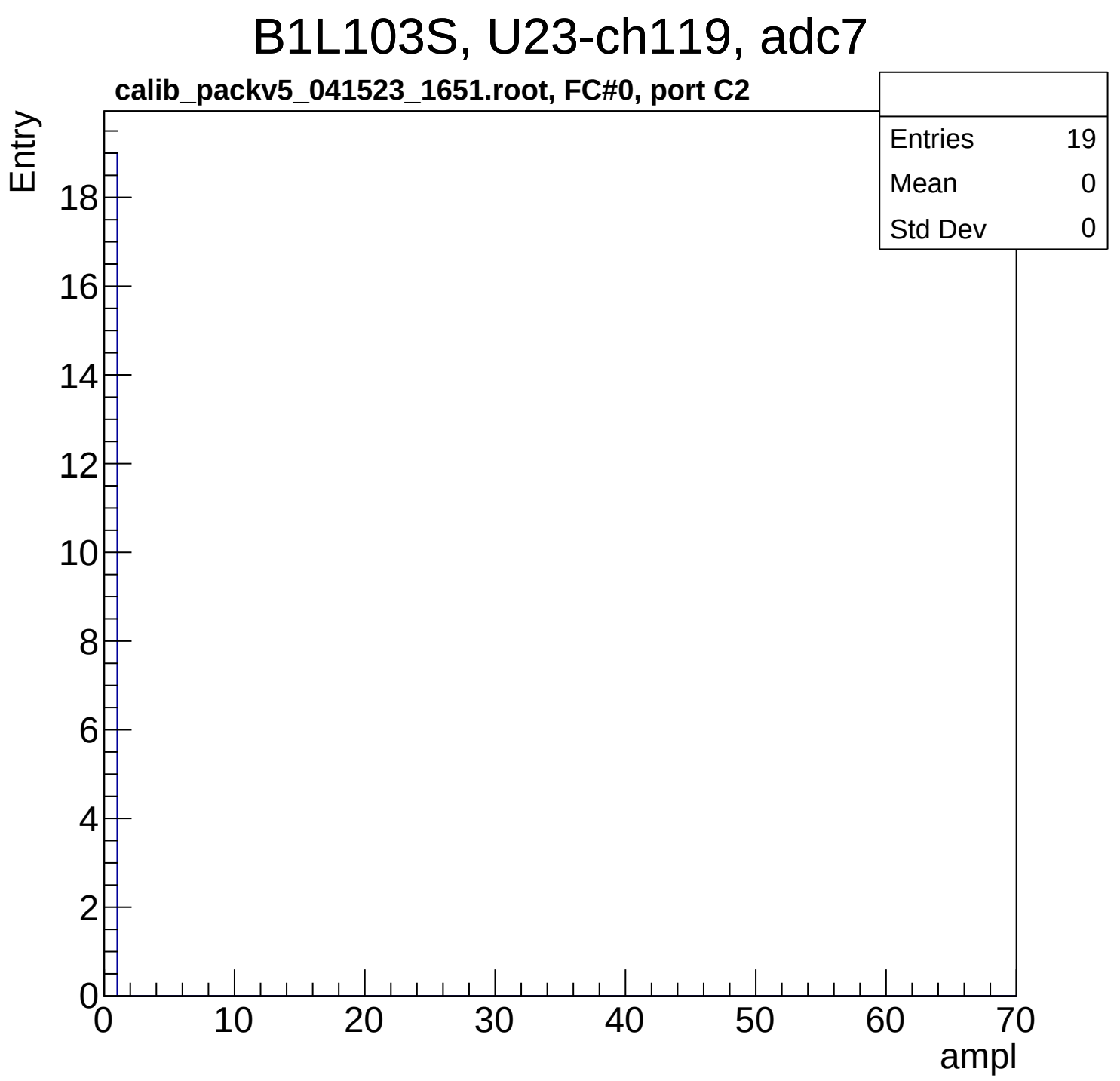

B1L103S, U23-ch119, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 52
Mean 38.65
Std Dev 11.51
Gaus mean : 42.5272
Gaus Width: 3.0913
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

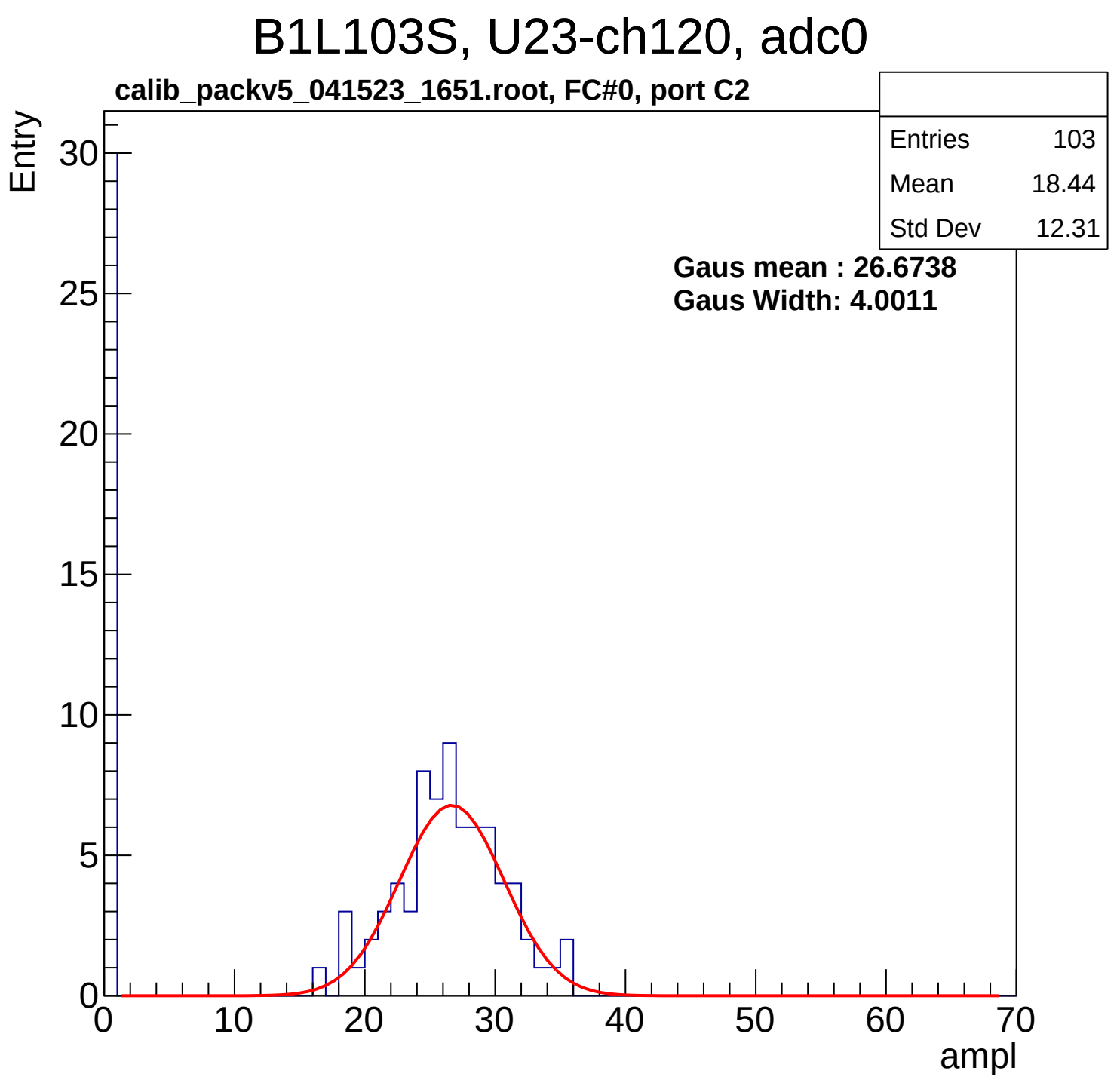

B1L103S, U23-ch119, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 46.82
Std Dev 8.418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

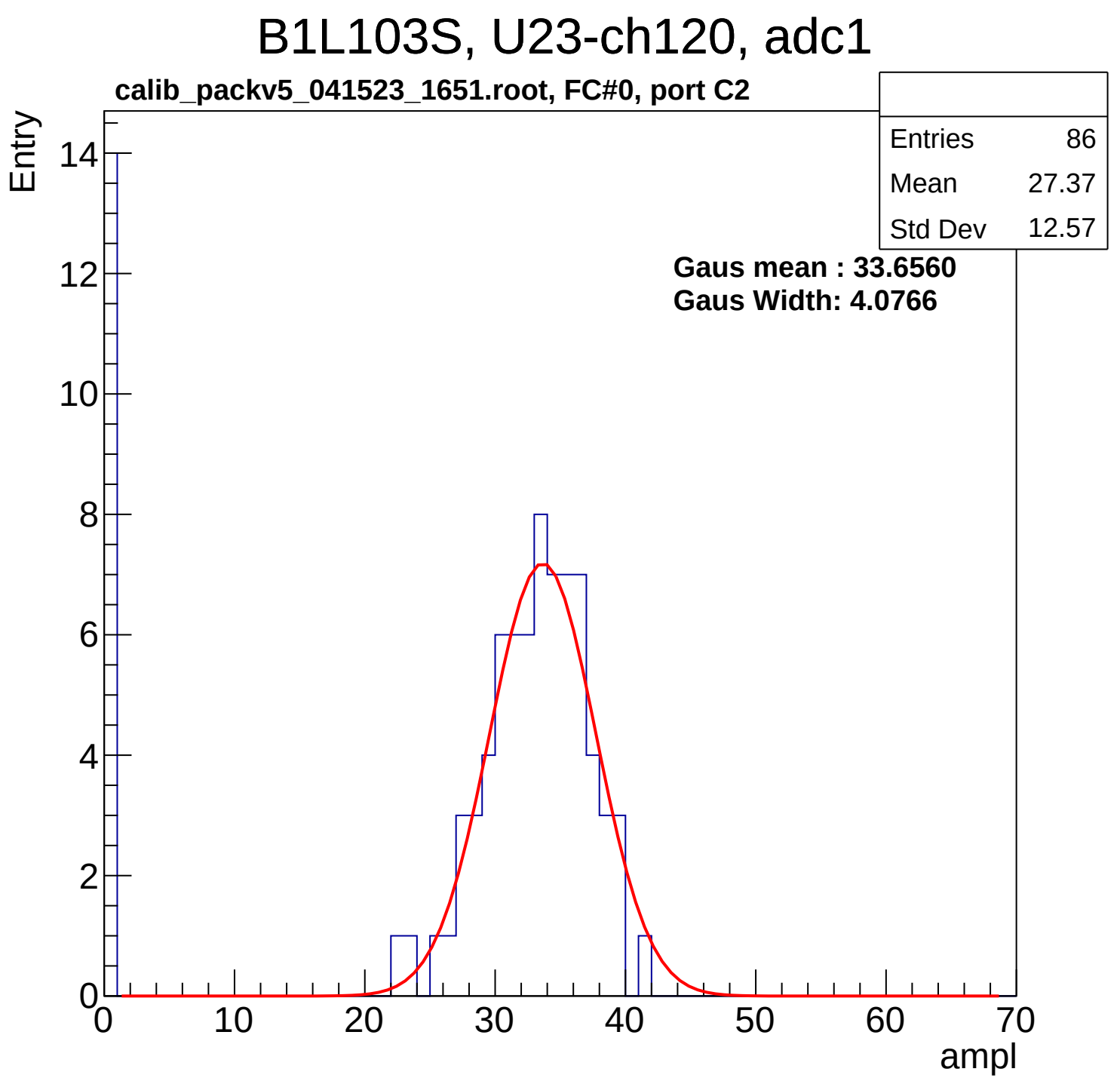

B1L103S, U23-ch119, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 55.33
Std Dev 3.518
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

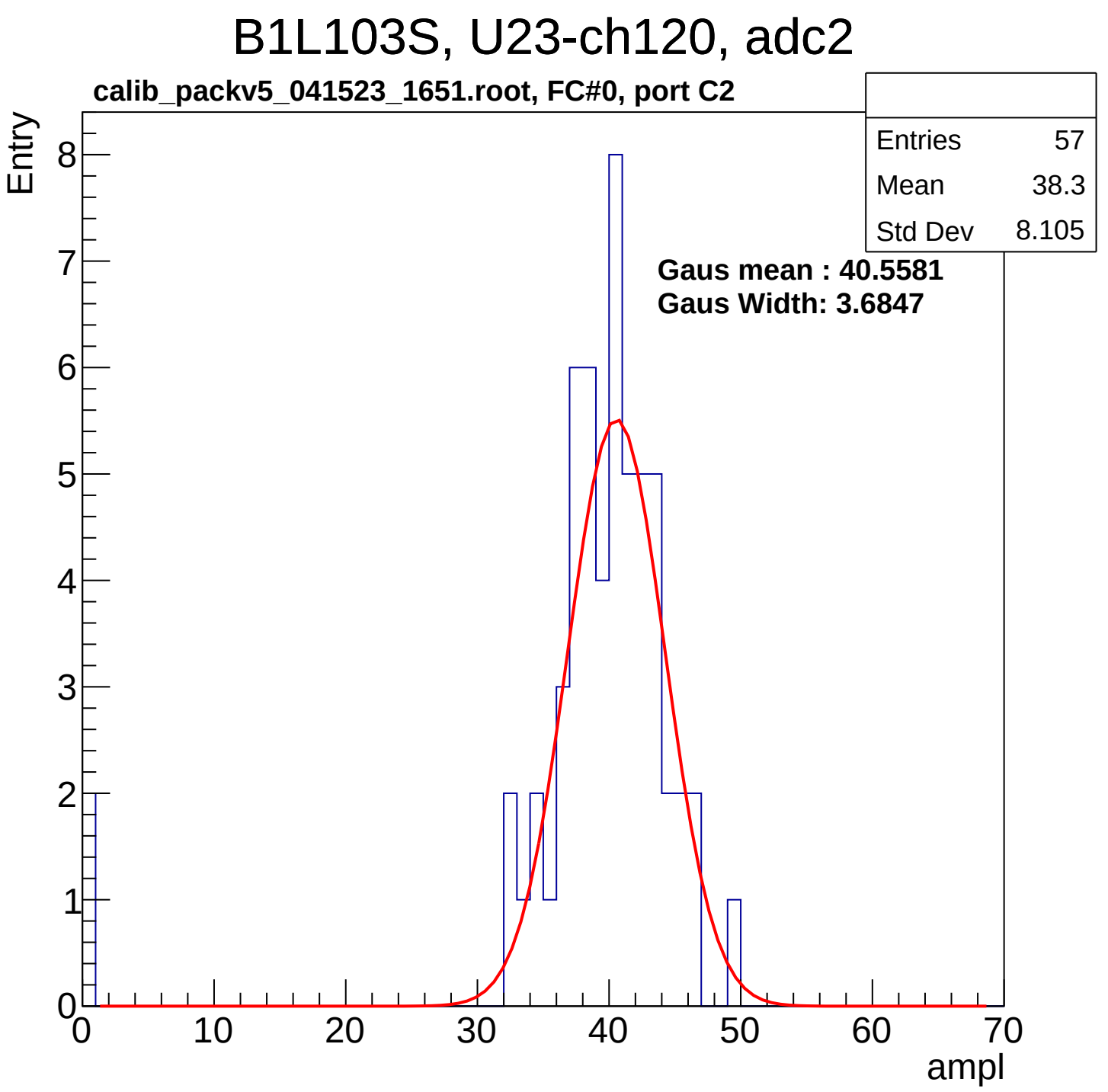

B1L103S, U23-ch119, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 46
Mean 57.78
Std Dev 9.144
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

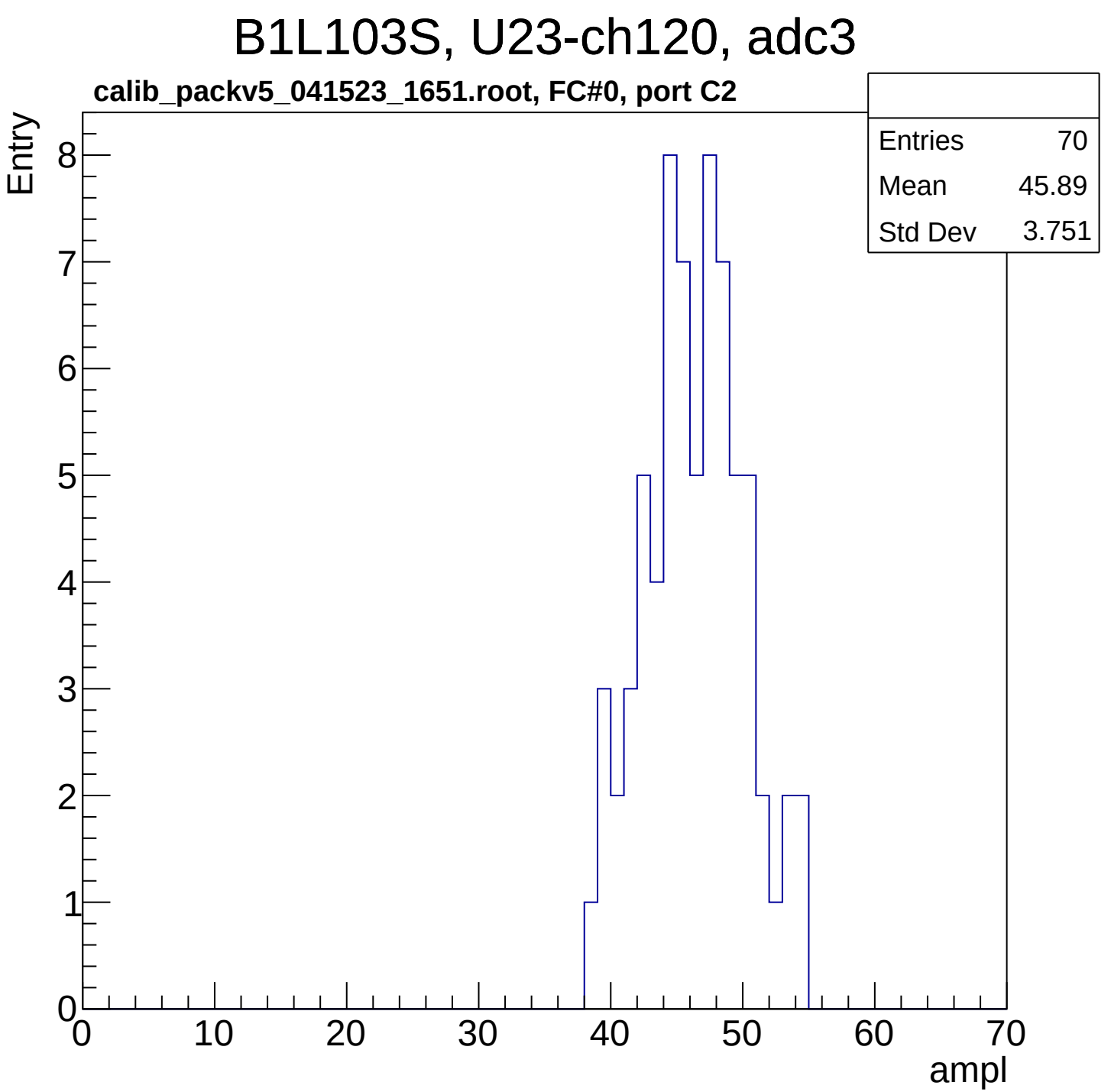

B1L103S, U23-ch119, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 62
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

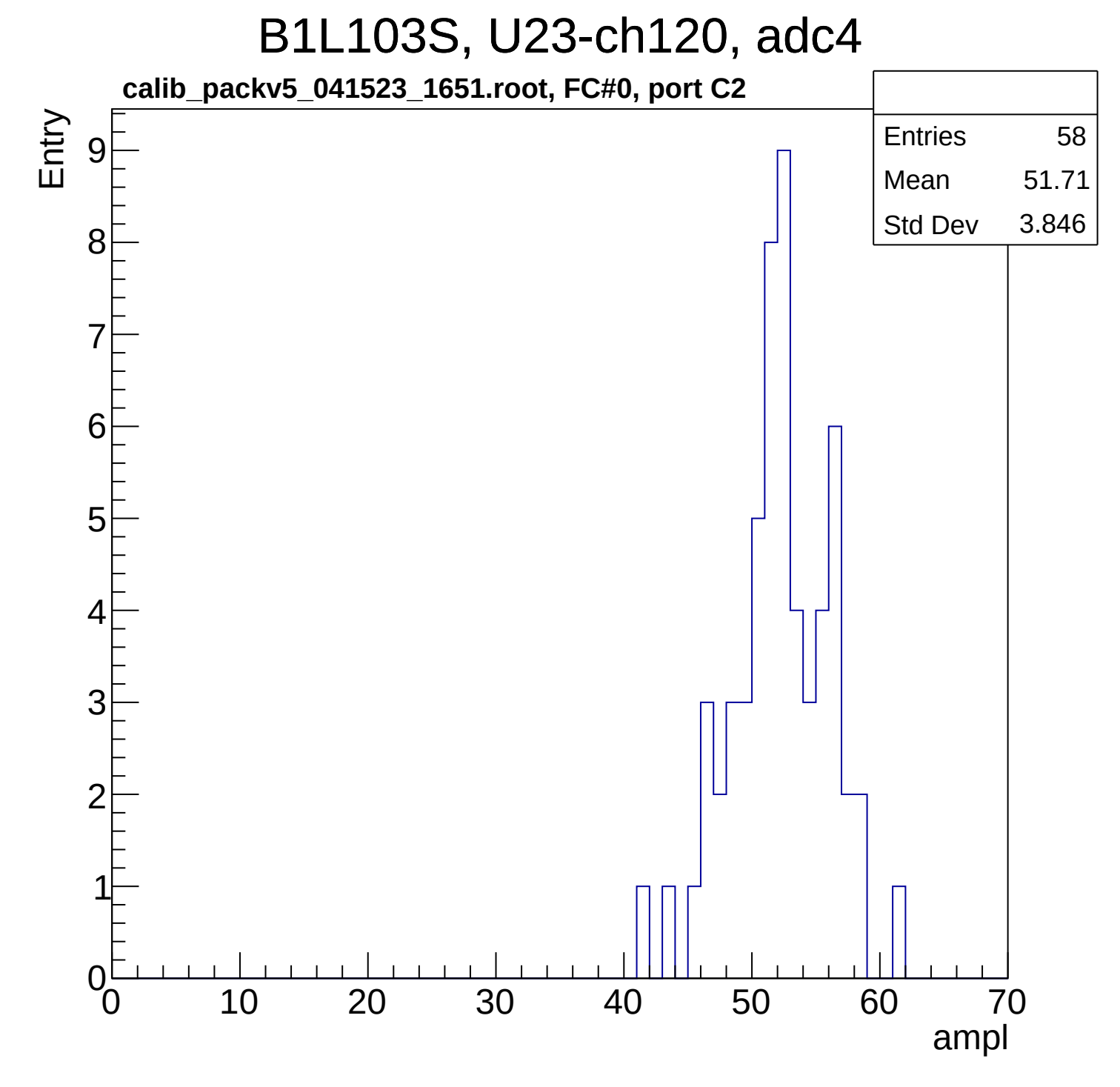

B1L103S, U23-ch119, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

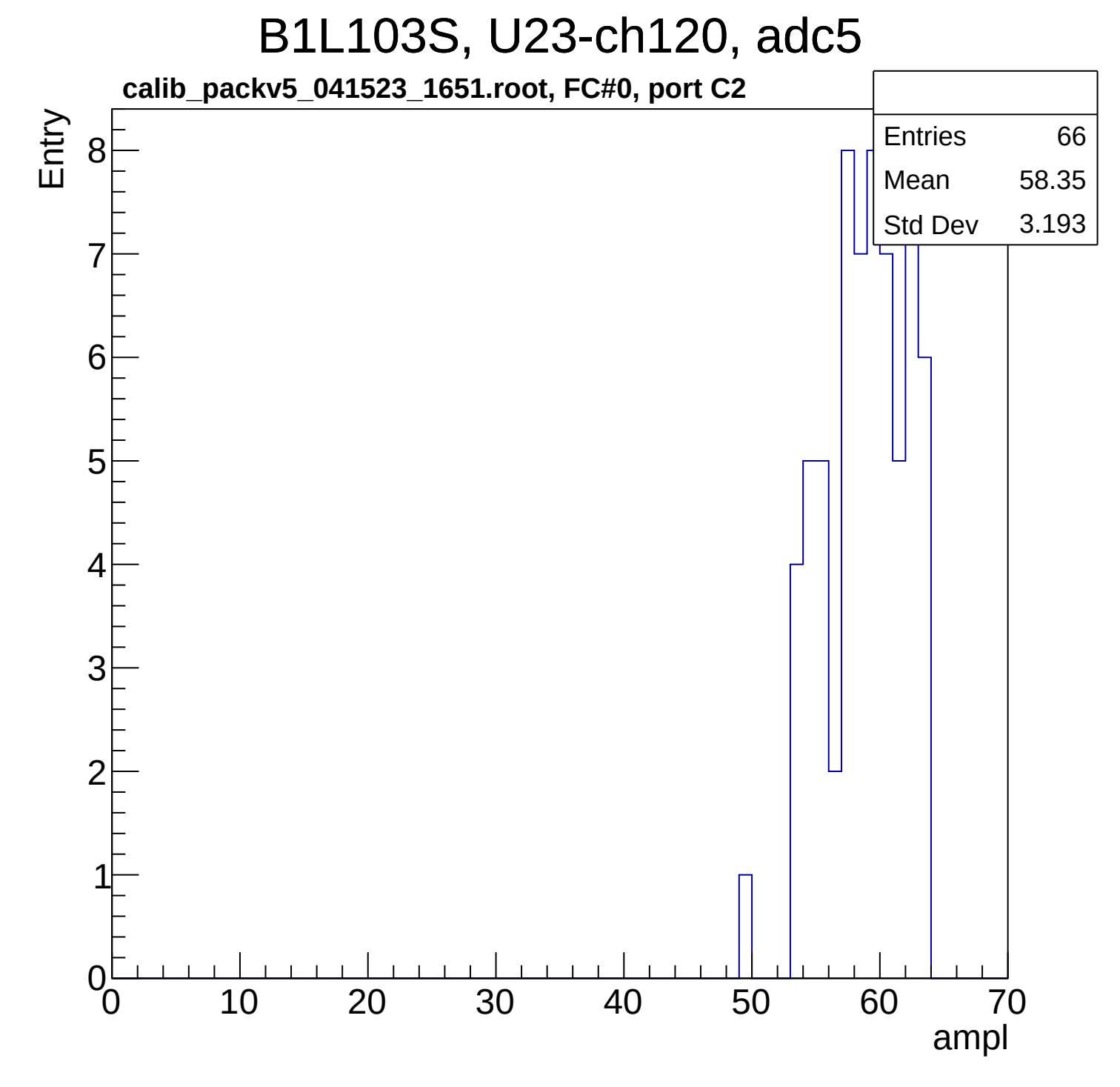

B1L103S, U23-ch120, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 103
Mean 18.44
Std Dev 12.31
Gaus mean : 26.6738
Gaus Width: 4.0011
Entry
ampl
0
5
10
15
20
25
30
0
10
20
30
40
50
60
70

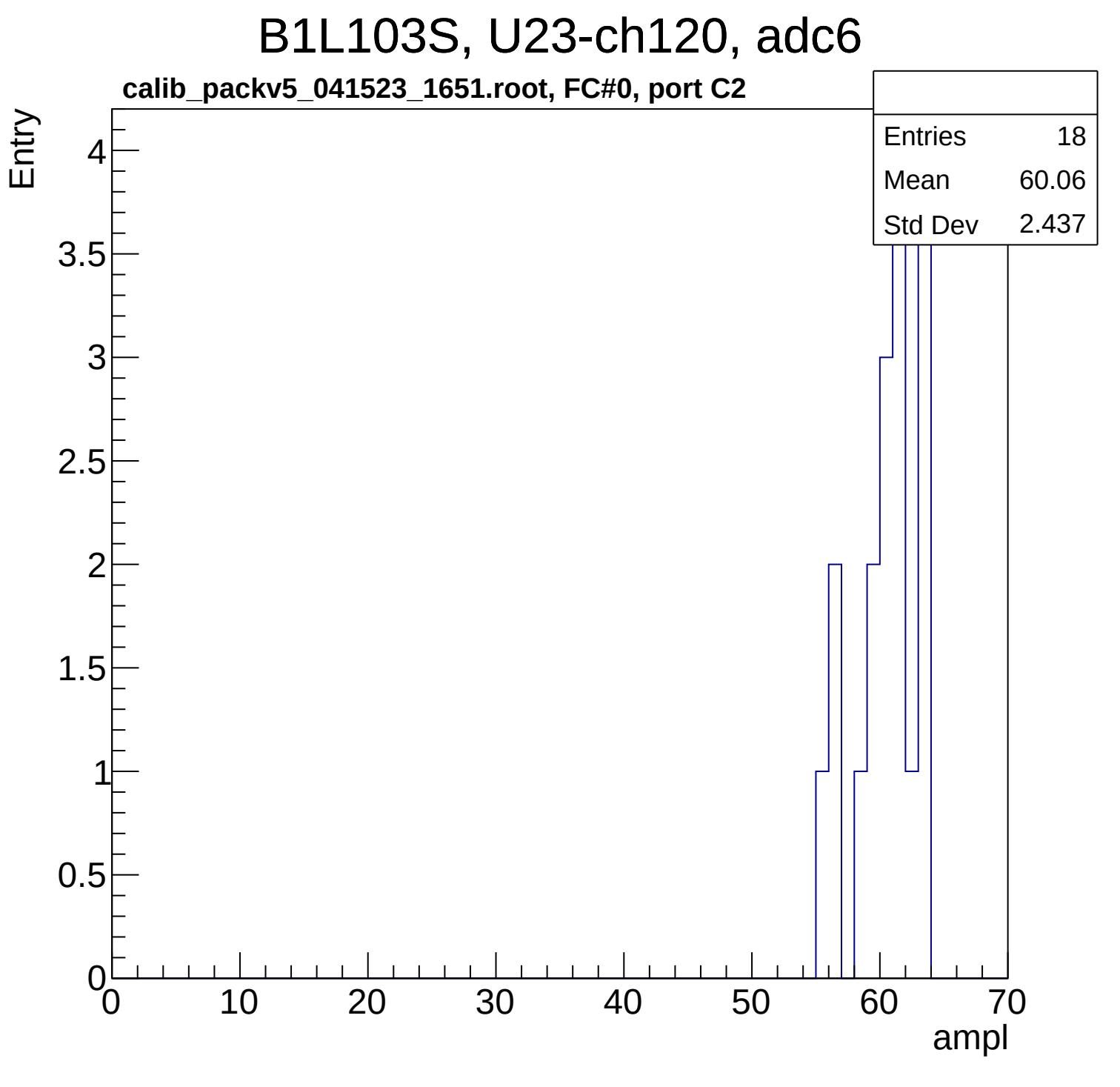

B1L103S, U23-ch120, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 27.37
Std Dev 12.57
Gaus mean : 33.6560
Gaus Width: 4.0766
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

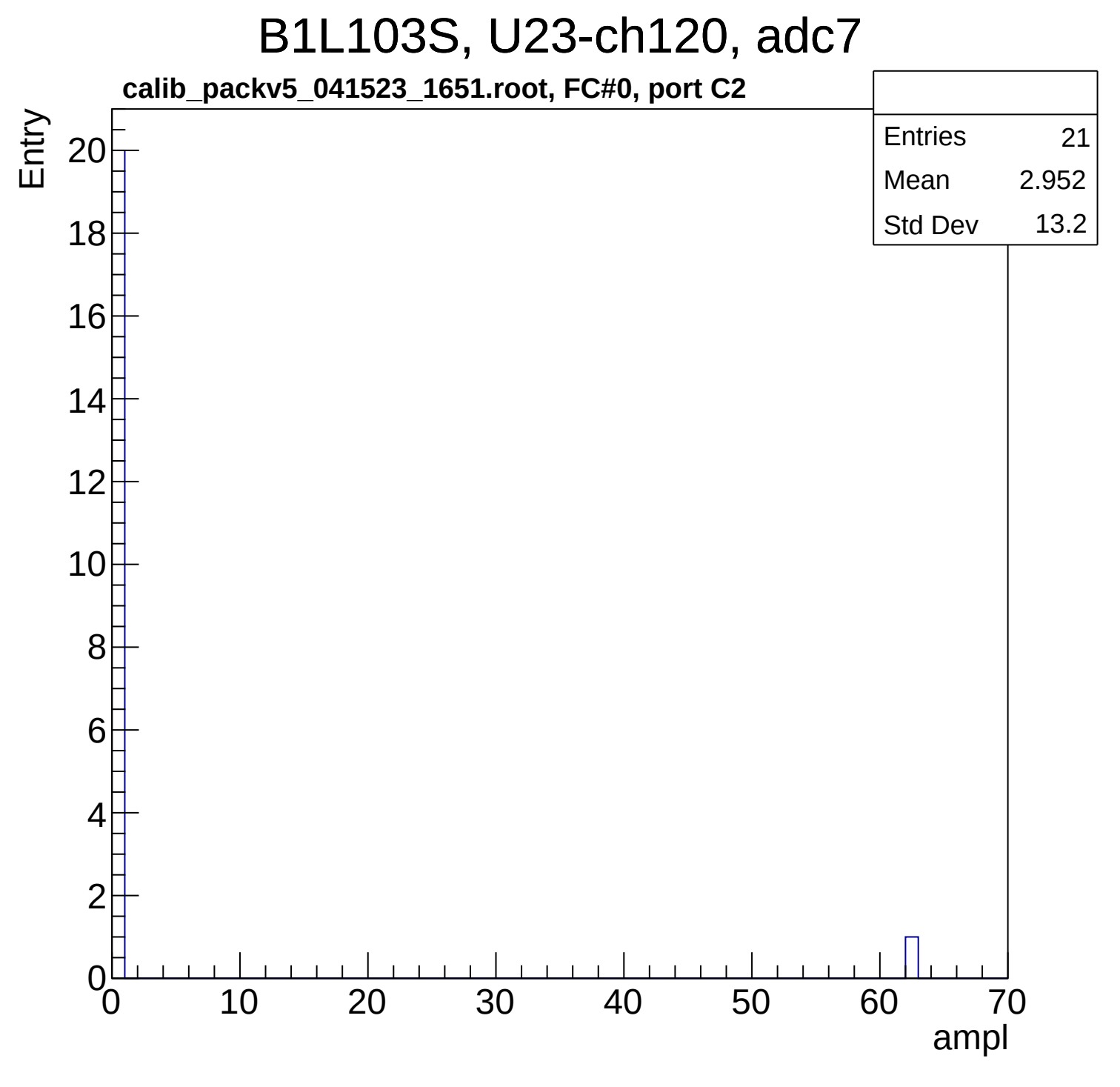

B1L103S, U23-ch120, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 57
Mean 38.3
Std Dev 8.105
Gaus mean : 40.5581
Gaus Width: 3.6847
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

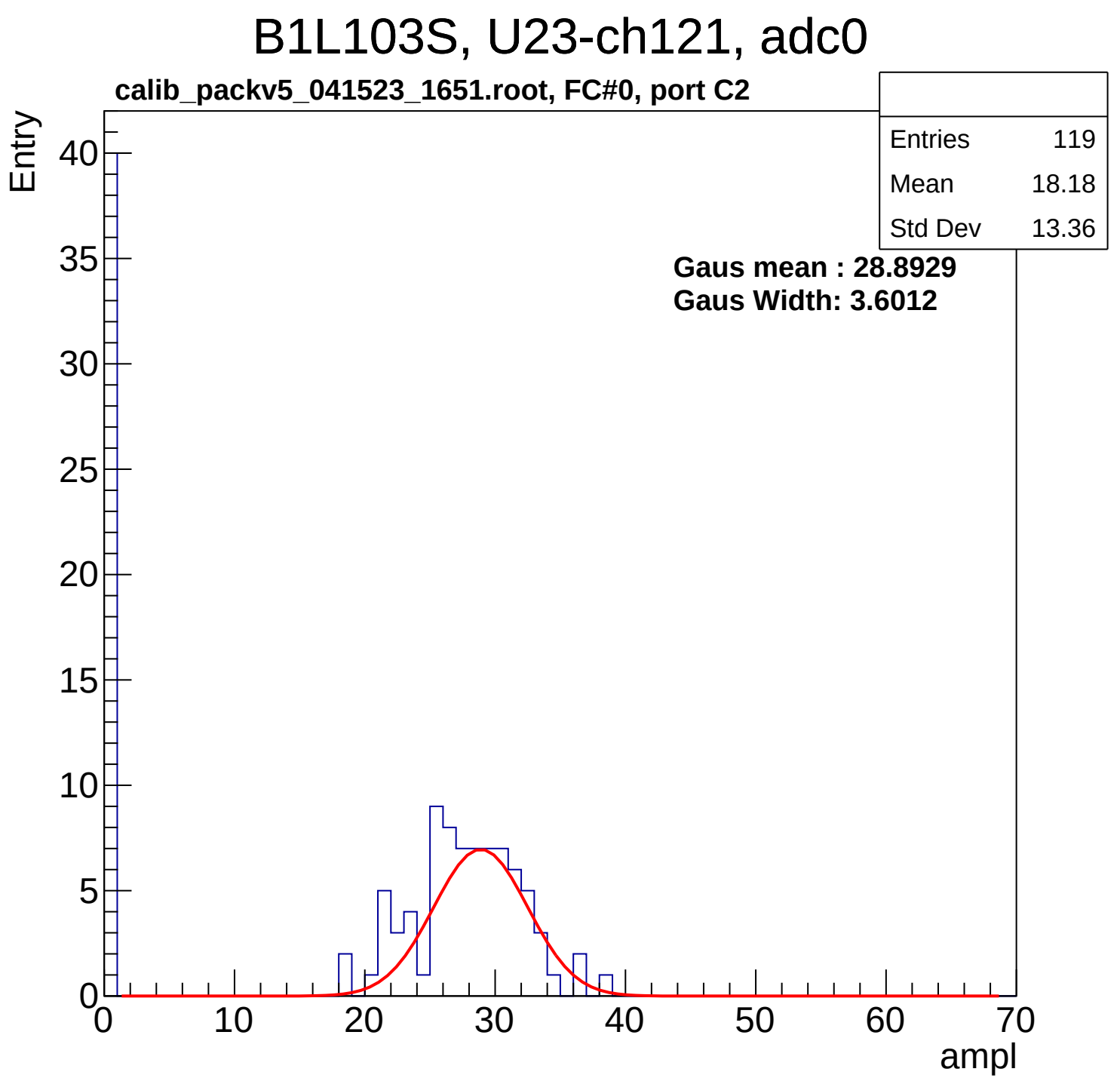

B1L103S, U23-ch120, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 70
Mean 45.89
Std Dev 3.751
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

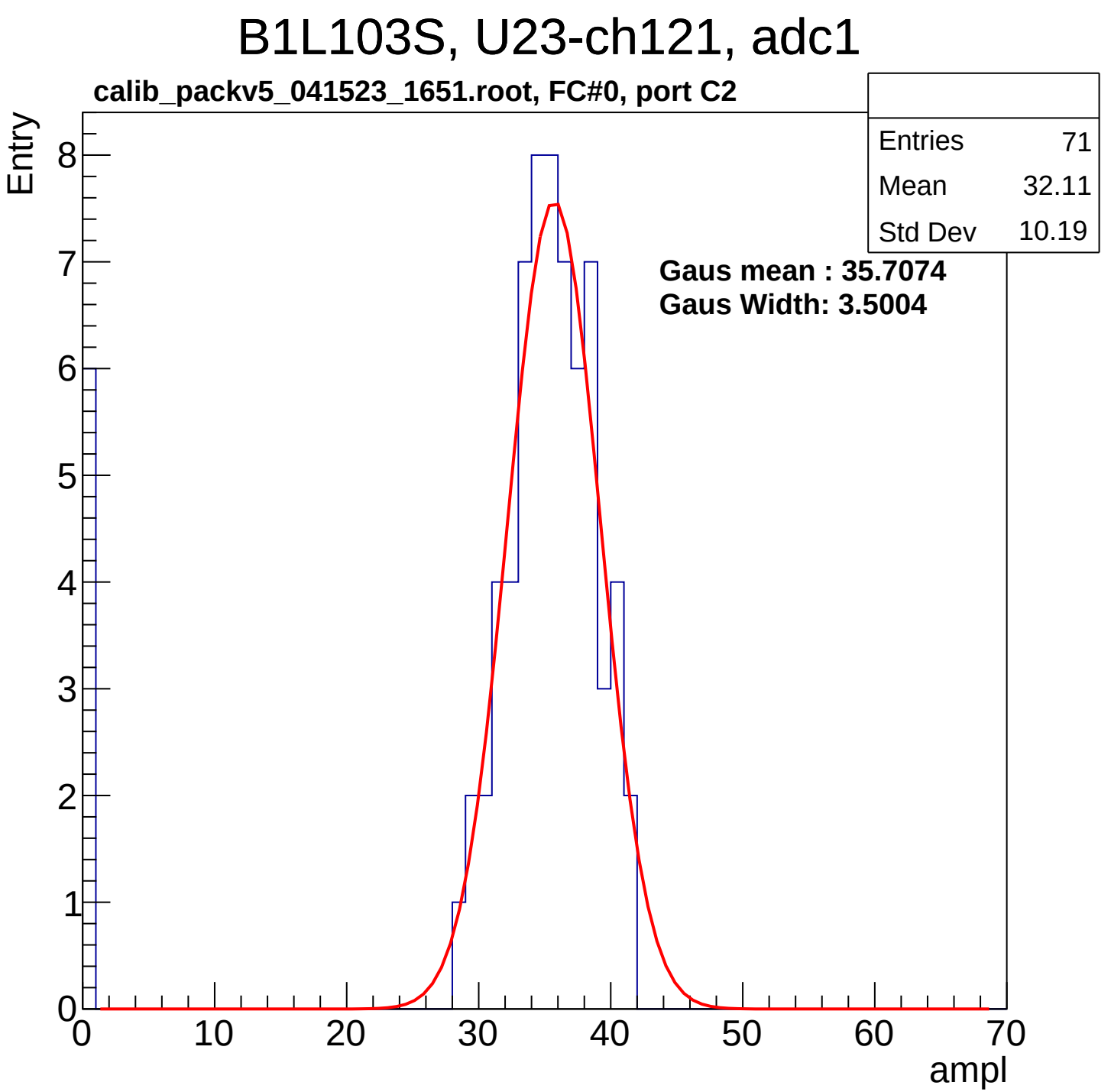

B1L103S, U23-ch120, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 58
Mean 51.71
Std Dev 3.846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

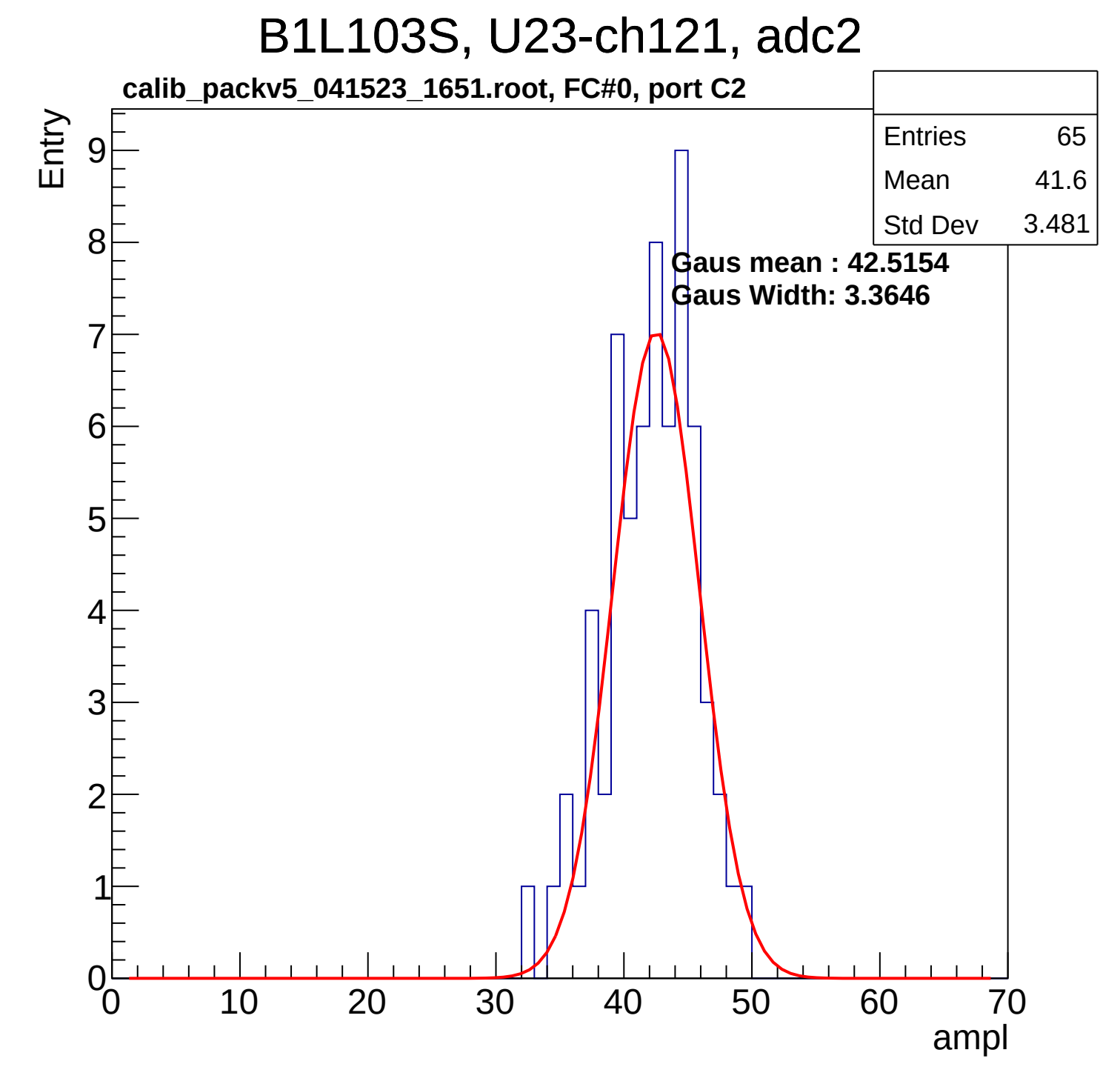

B1L103S, U23-ch120, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 66
Mean 58.35
Std Dev 3.193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

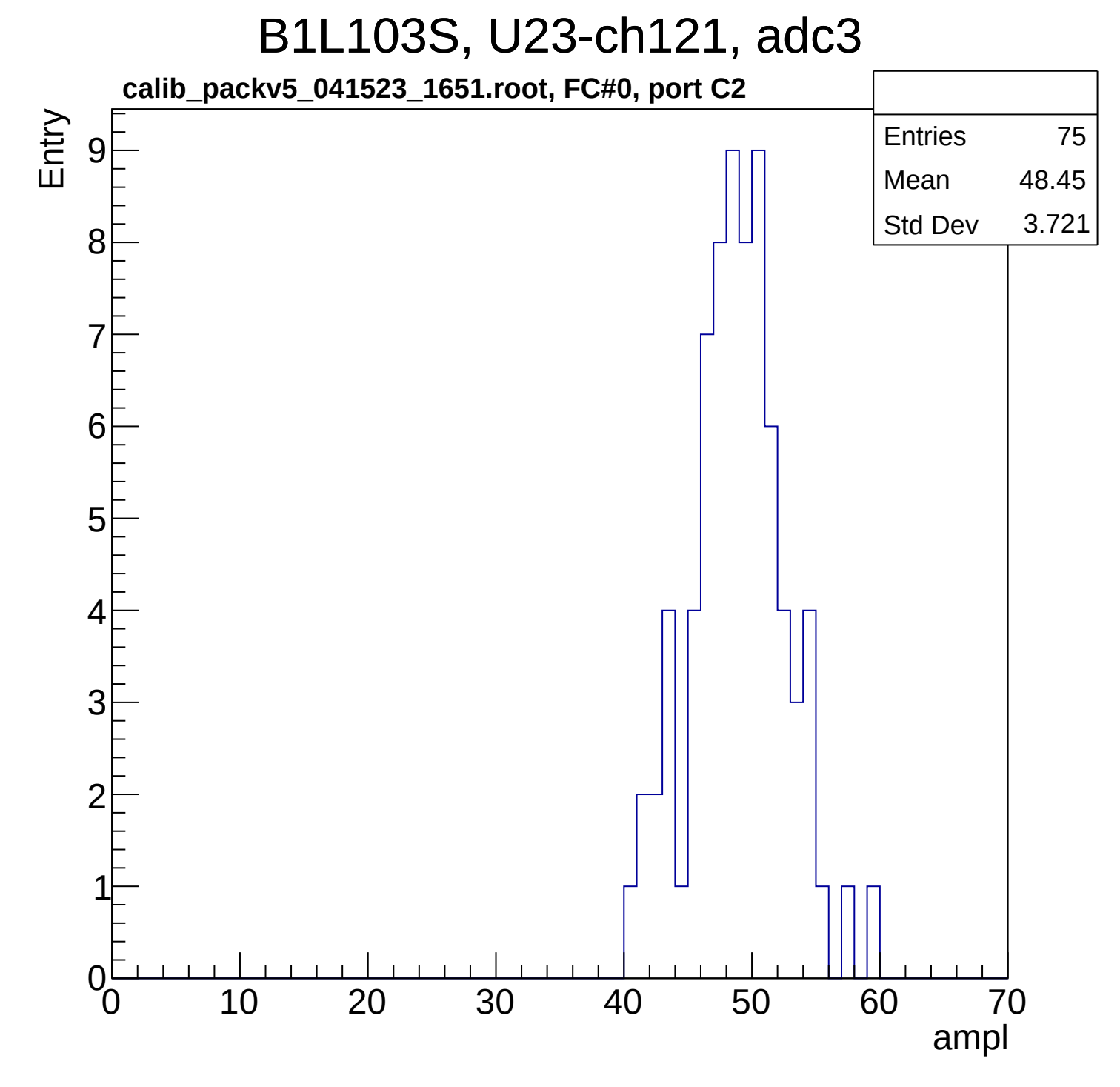

B1L103S, U23-ch120, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 18
Mean 60.06
Std Dev 2.437
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

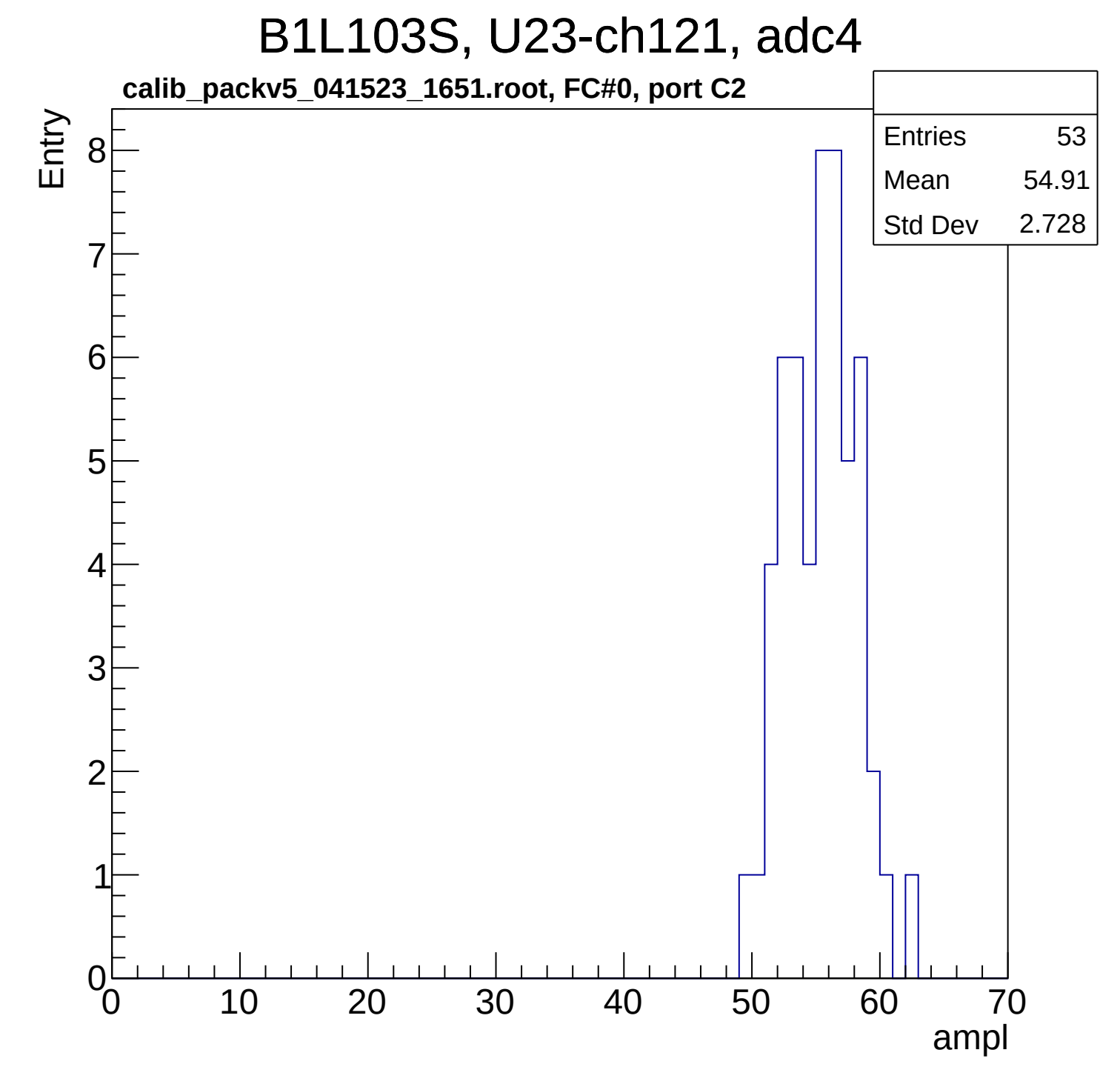

B1L103S, U23-ch120, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 2.952
Std Dev 13.2
Entry
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70
ampl

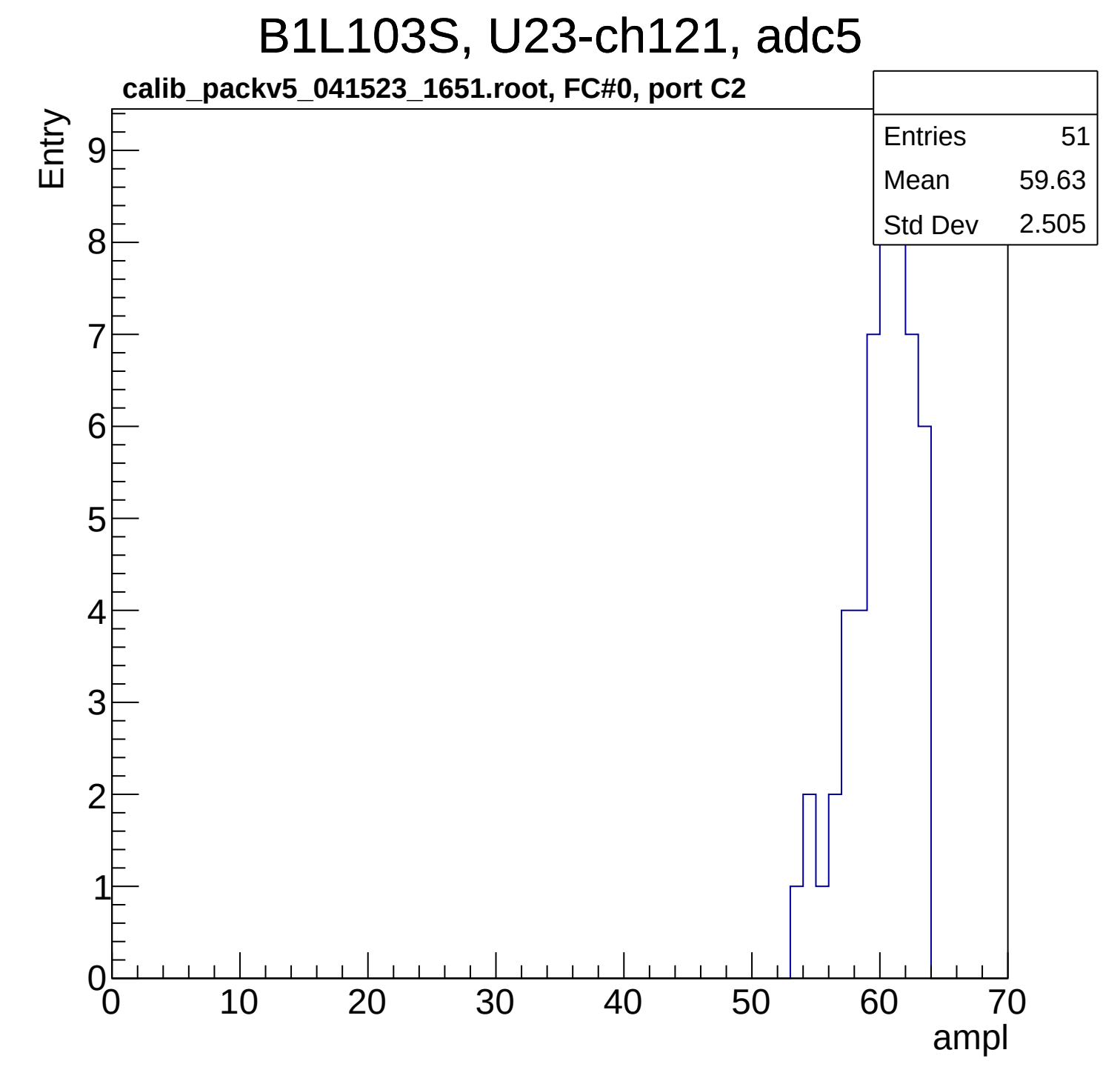

B1L103S, U23-ch121, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 119
Mean 18.18
Std Dev 13.36
Gaus mean : 28.8929
Gaus Width: 3.6012
Entry
ampl
0
5
10
15
20
25
30
35
40
0
10
20
30
40
50
60
70

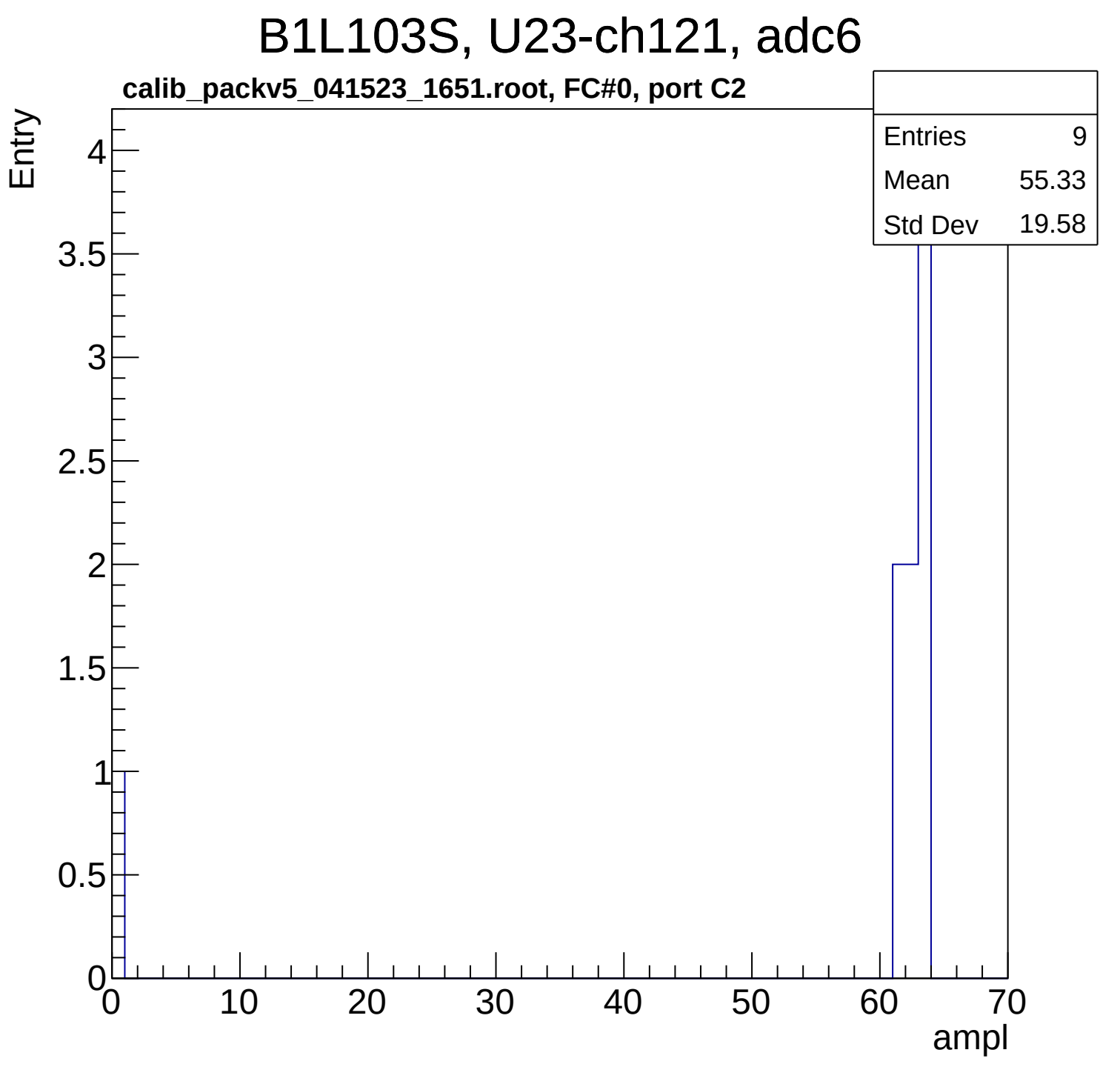

B1L103S, U23-ch121, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 32.11
Std Dev 10.19
Gaus mean : 35.7074
Gaus Width: 3.5004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

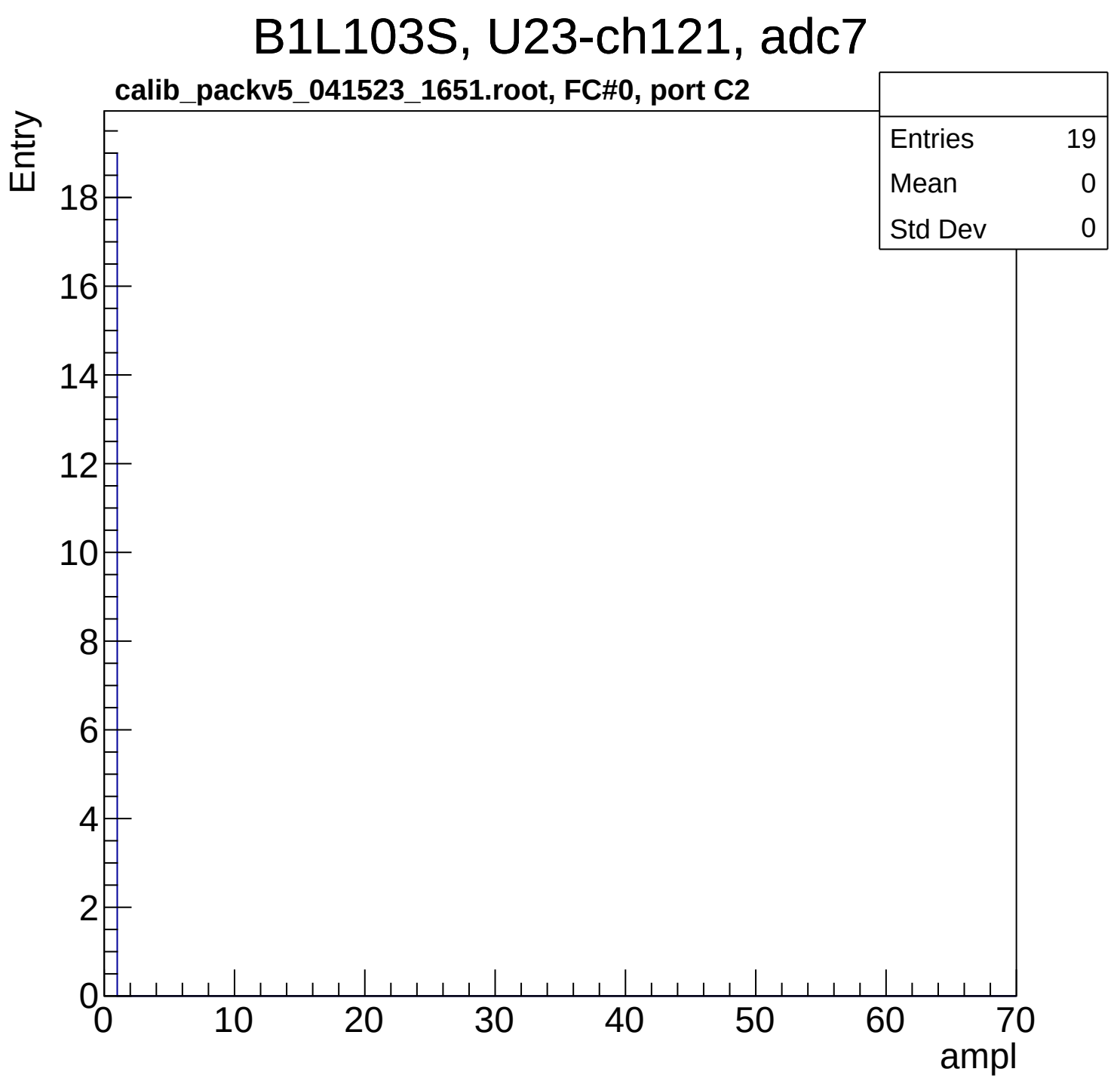

B1L103S, U23-ch121, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 41.6
Std Dev 3.481
Gaus mean : 42.5154
Gaus Width: 3.3646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

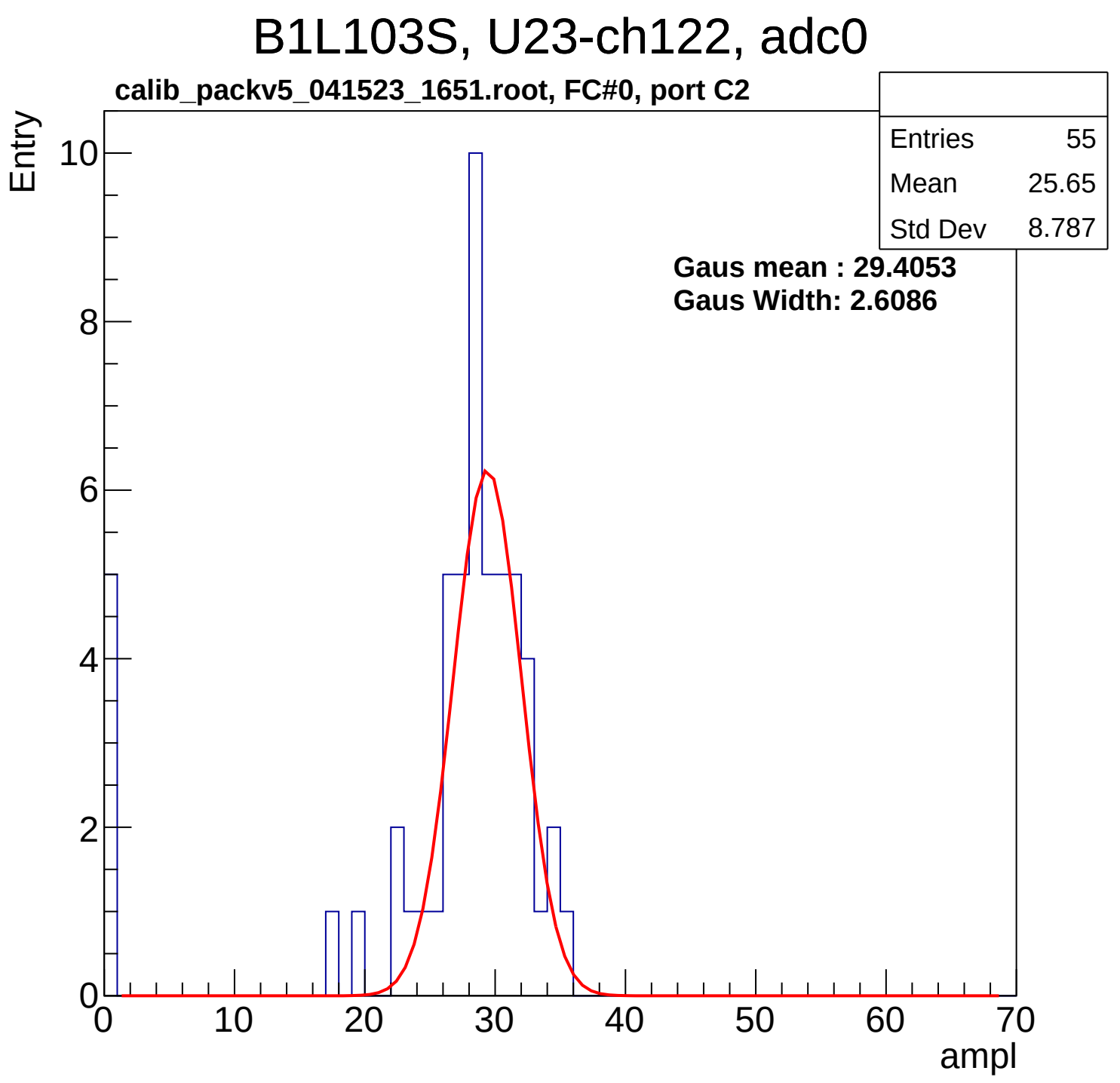

B1L103S, U23-ch121, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 48.45
Std Dev 3.721
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

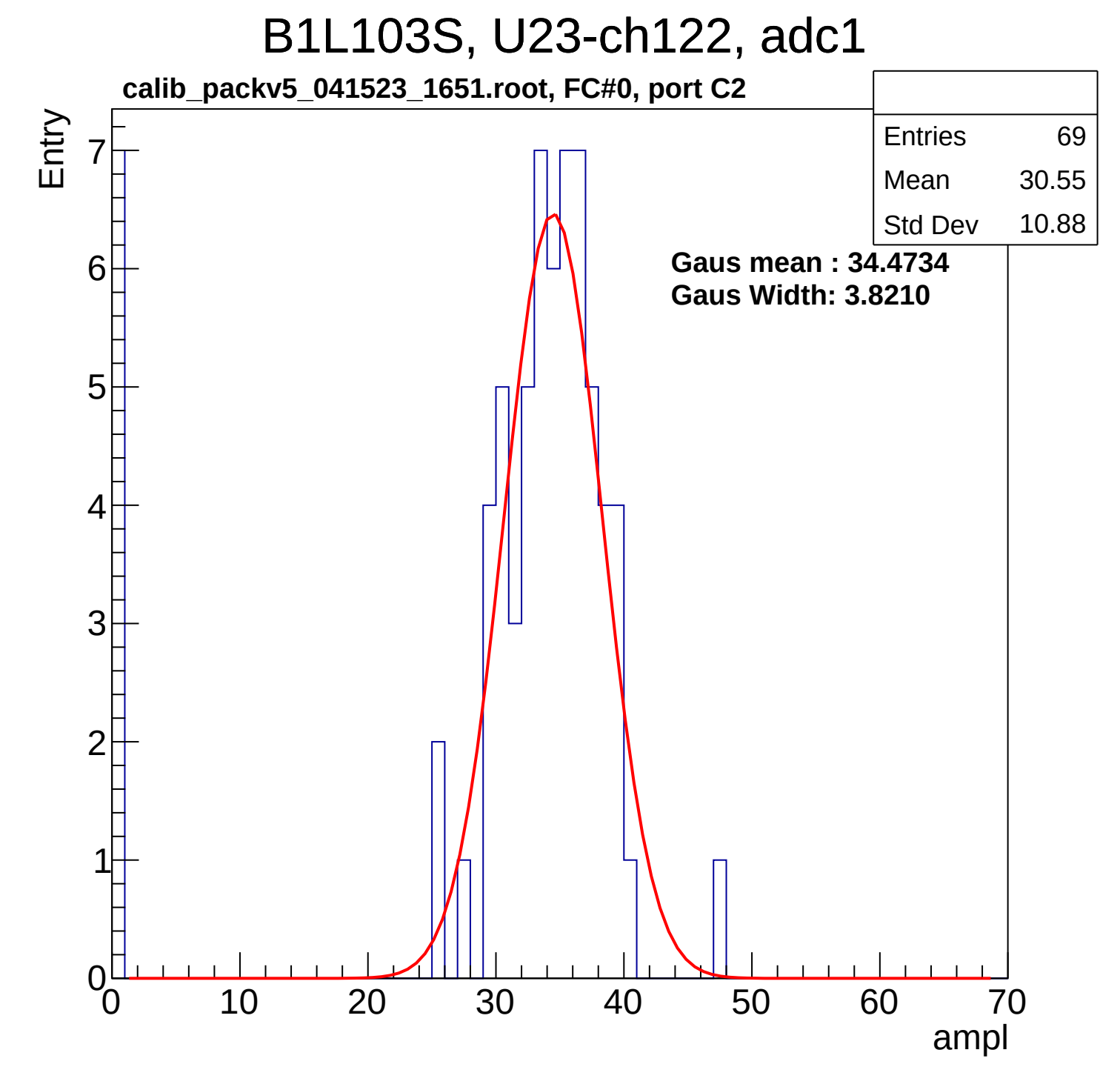

B1L103S, U23-ch121, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 53
Mean 54.91
Std Dev 2.728
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

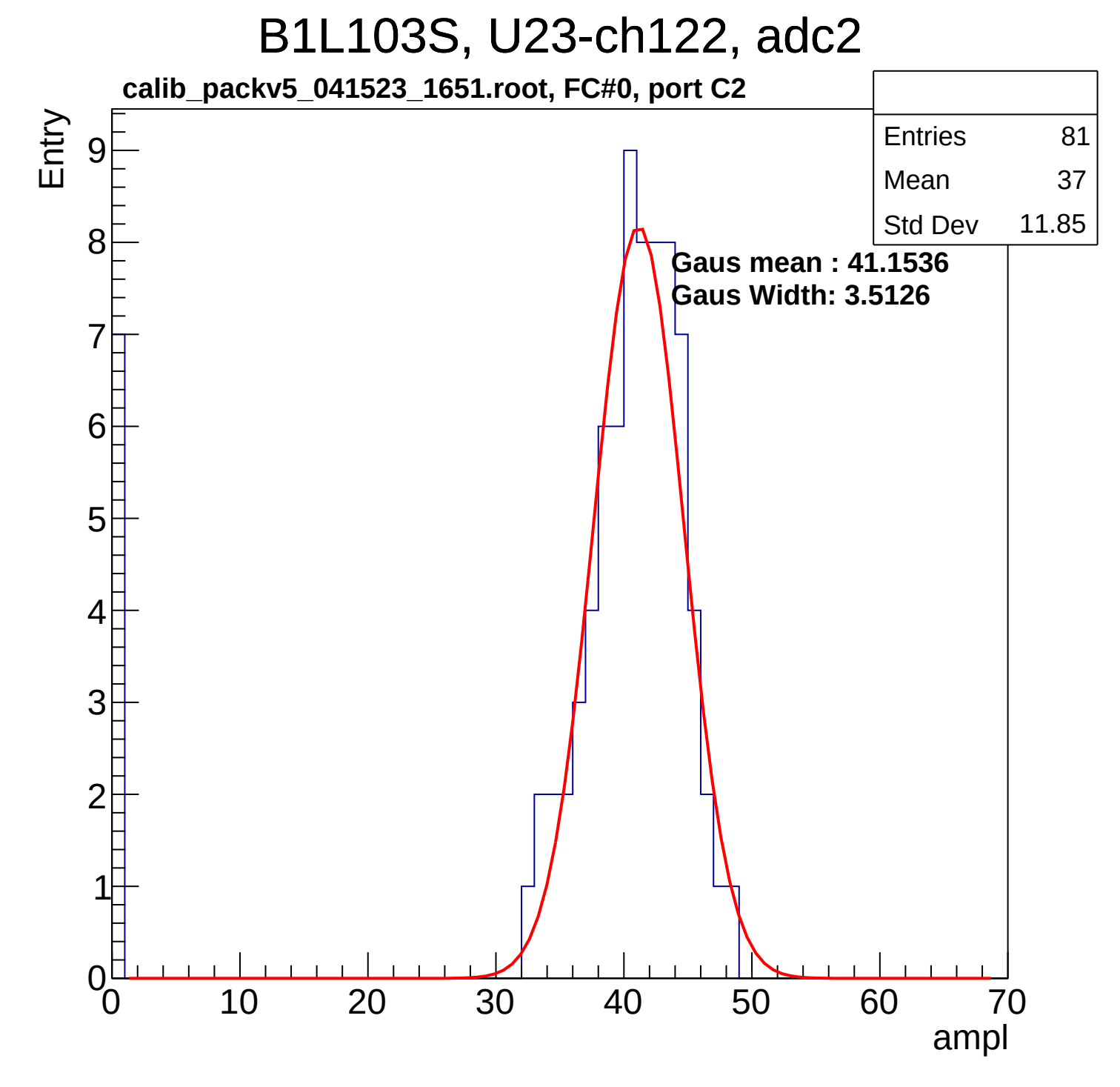

B1L103S, U23-ch121, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 51
Mean 59.63
Std Dev 2.505
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

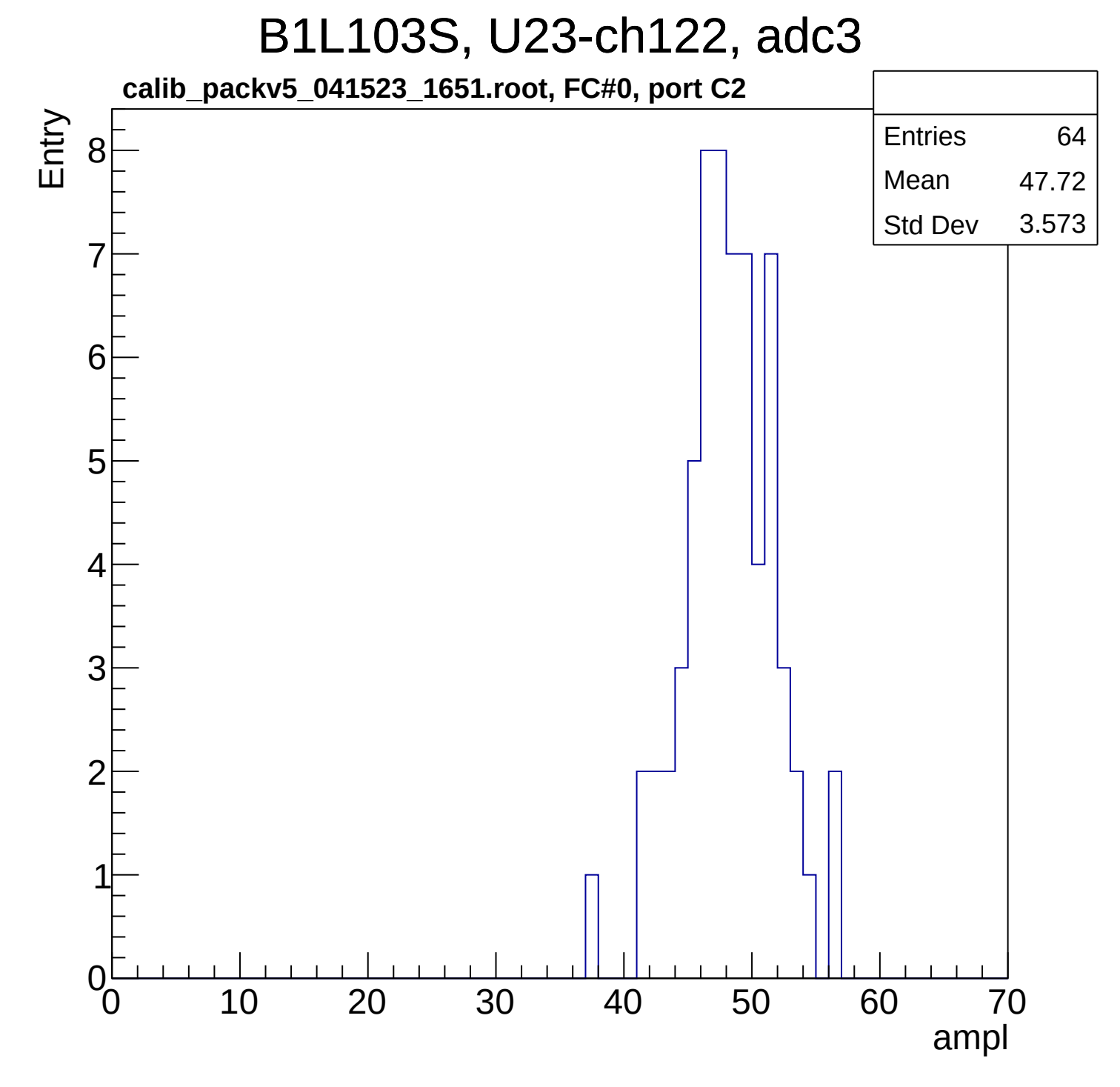

B1L103S, U23-ch121, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

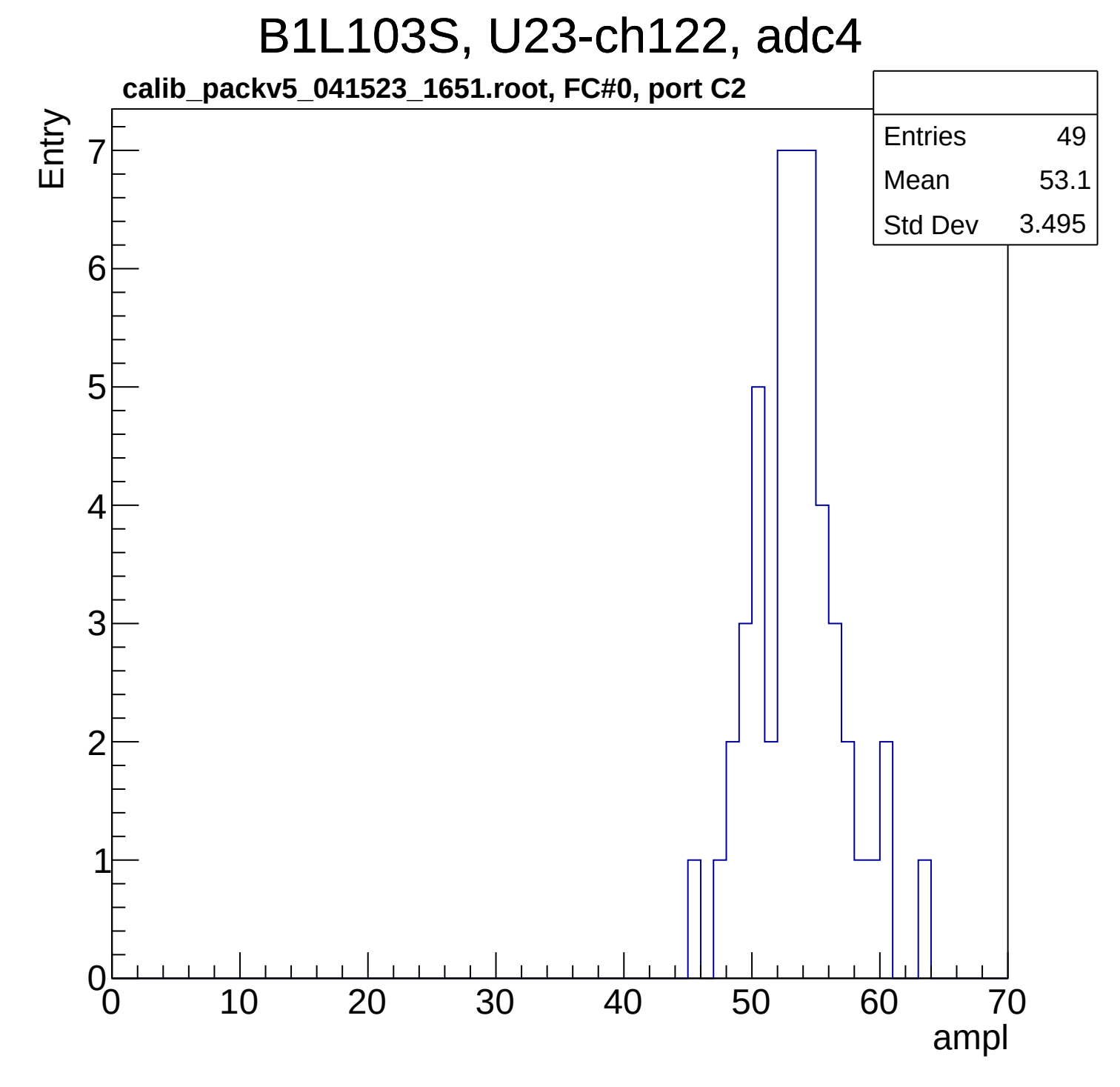

B1L103S, U23-ch121, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

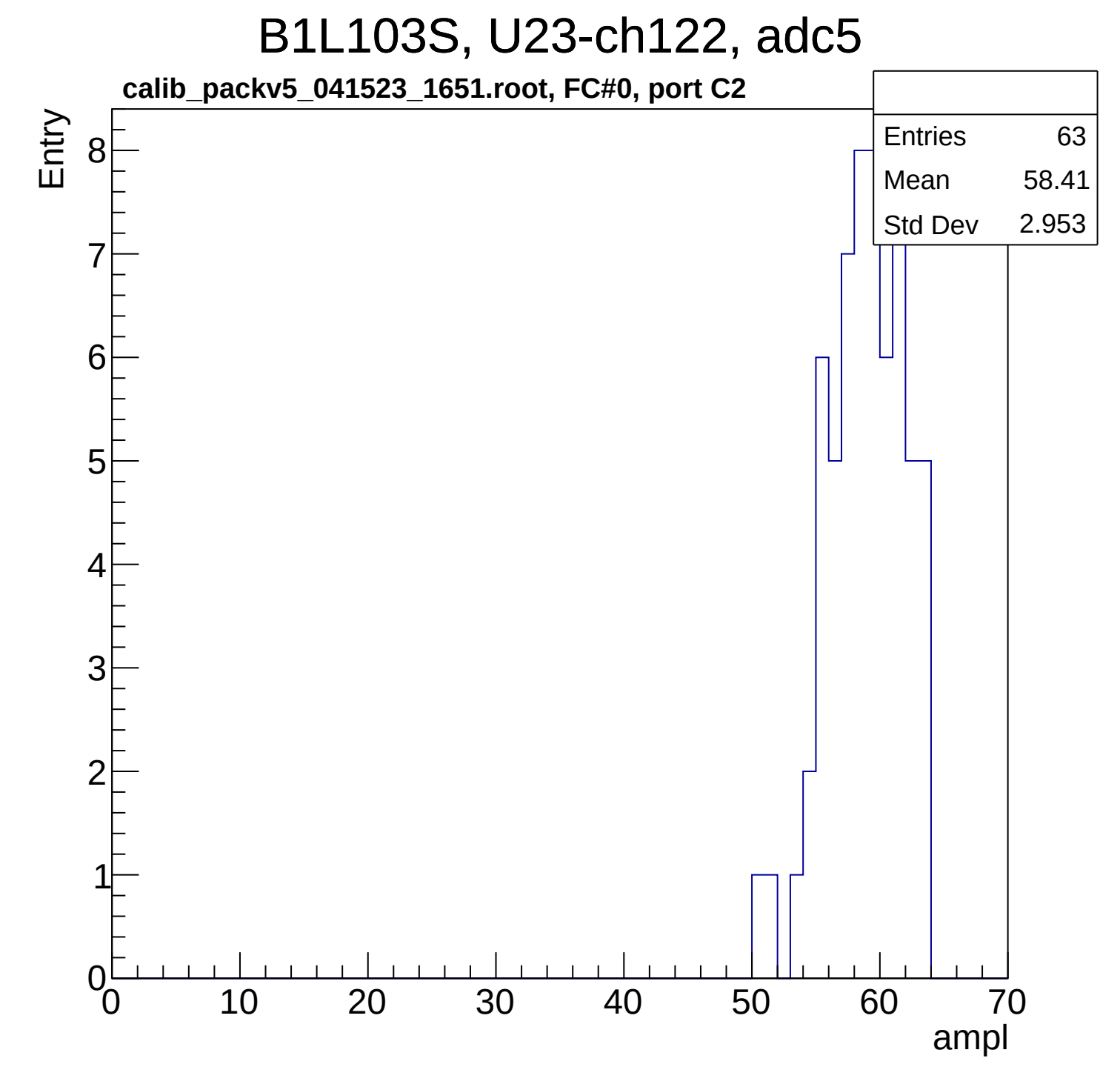

B1L103S, U23-ch122, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 55
Mean 25.65
Std Dev 8.787
Gaus mean : 29.4053
Gaus Width: 2.6086
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

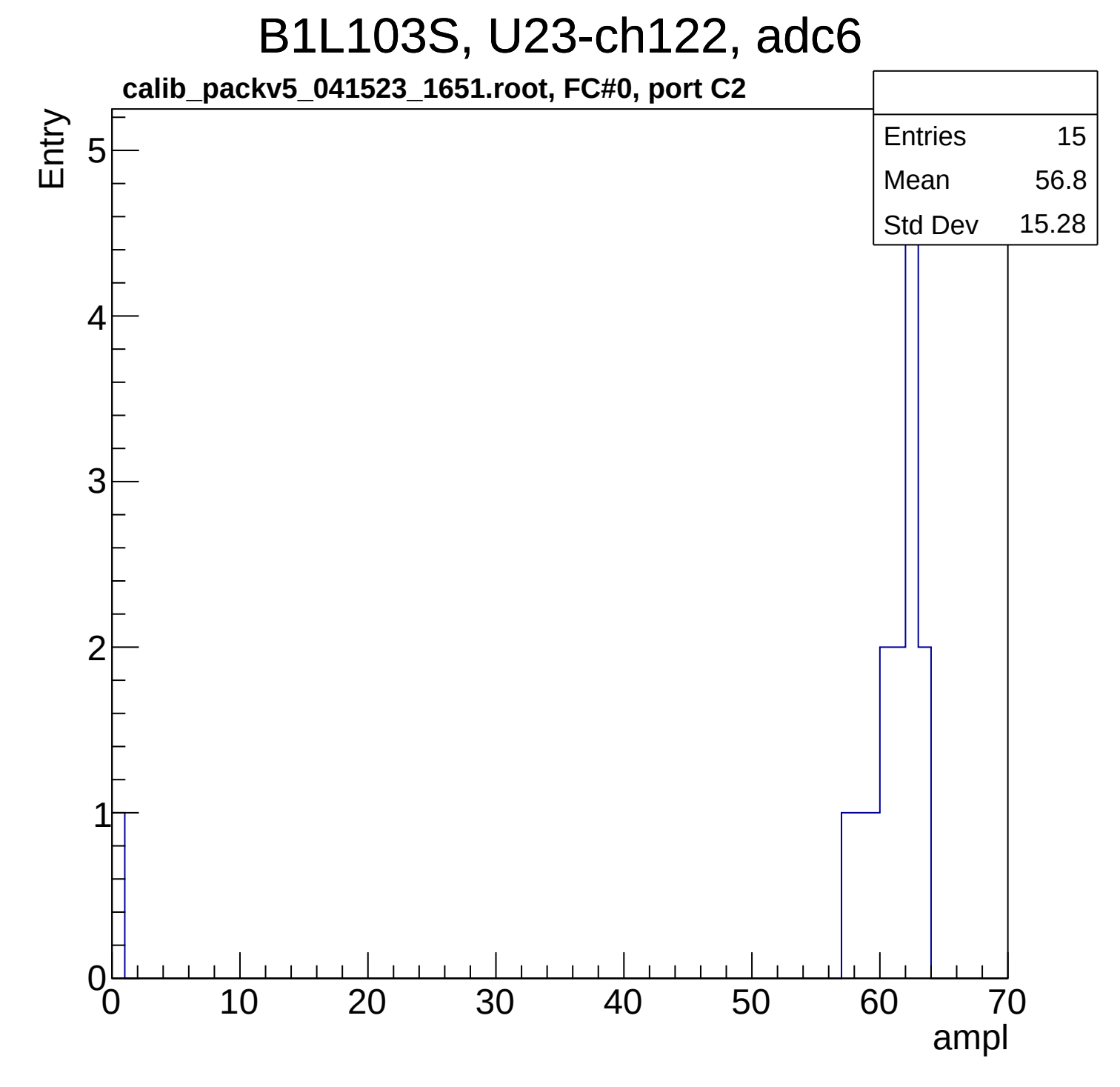

B1L103S, U23-ch122, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 30.55
Std Dev 10.88
Gaus mean : 34.4734
Gaus Width: 3.8210
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

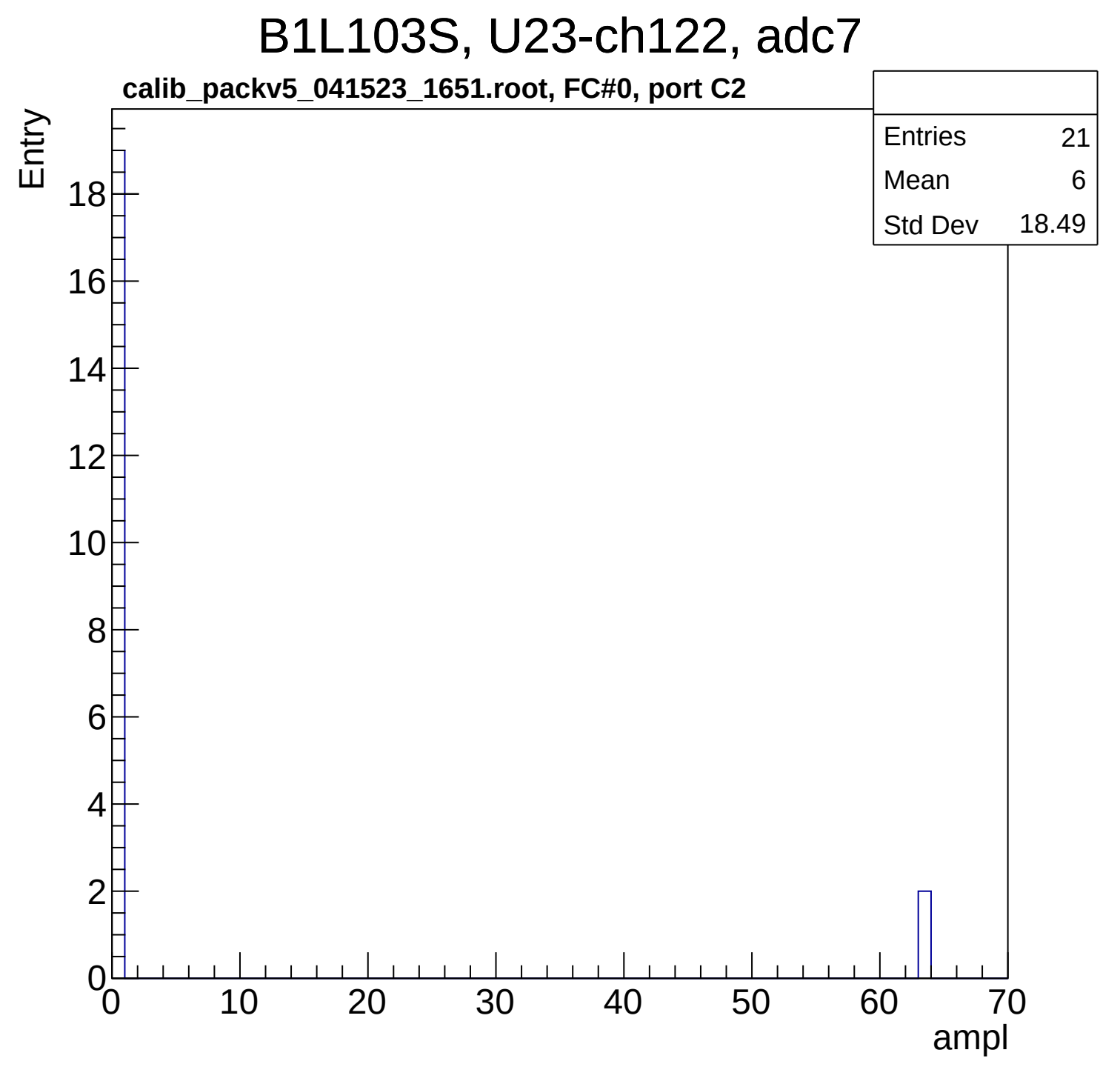

B1L103S, U23-ch122, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 81
Mean 37
Std Dev 11.85
Gaus mean : 41.1536
Gaus Width: 3.5126
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

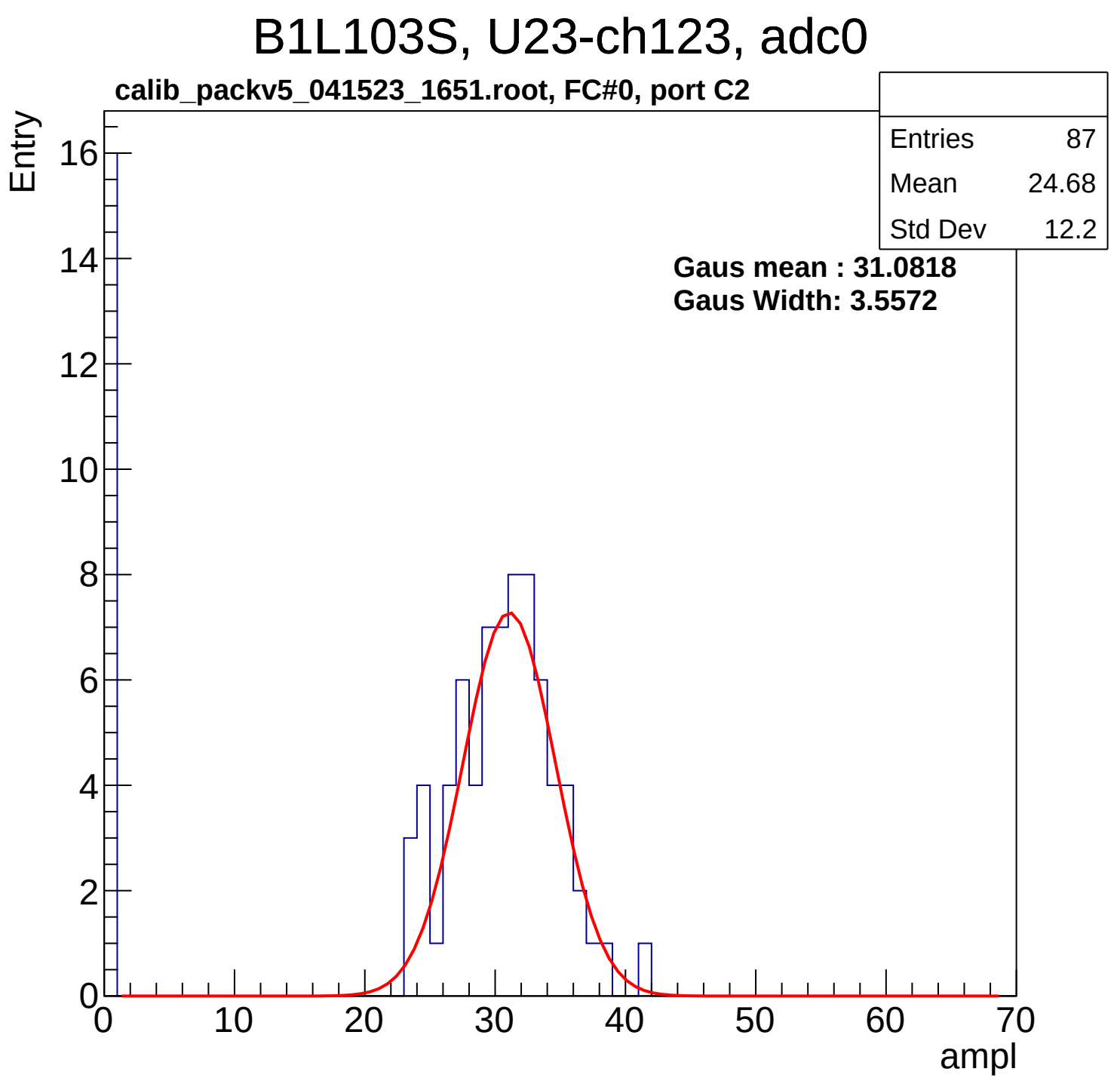

B1L103S, U23-ch122, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 47.72
Std Dev 3.573
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

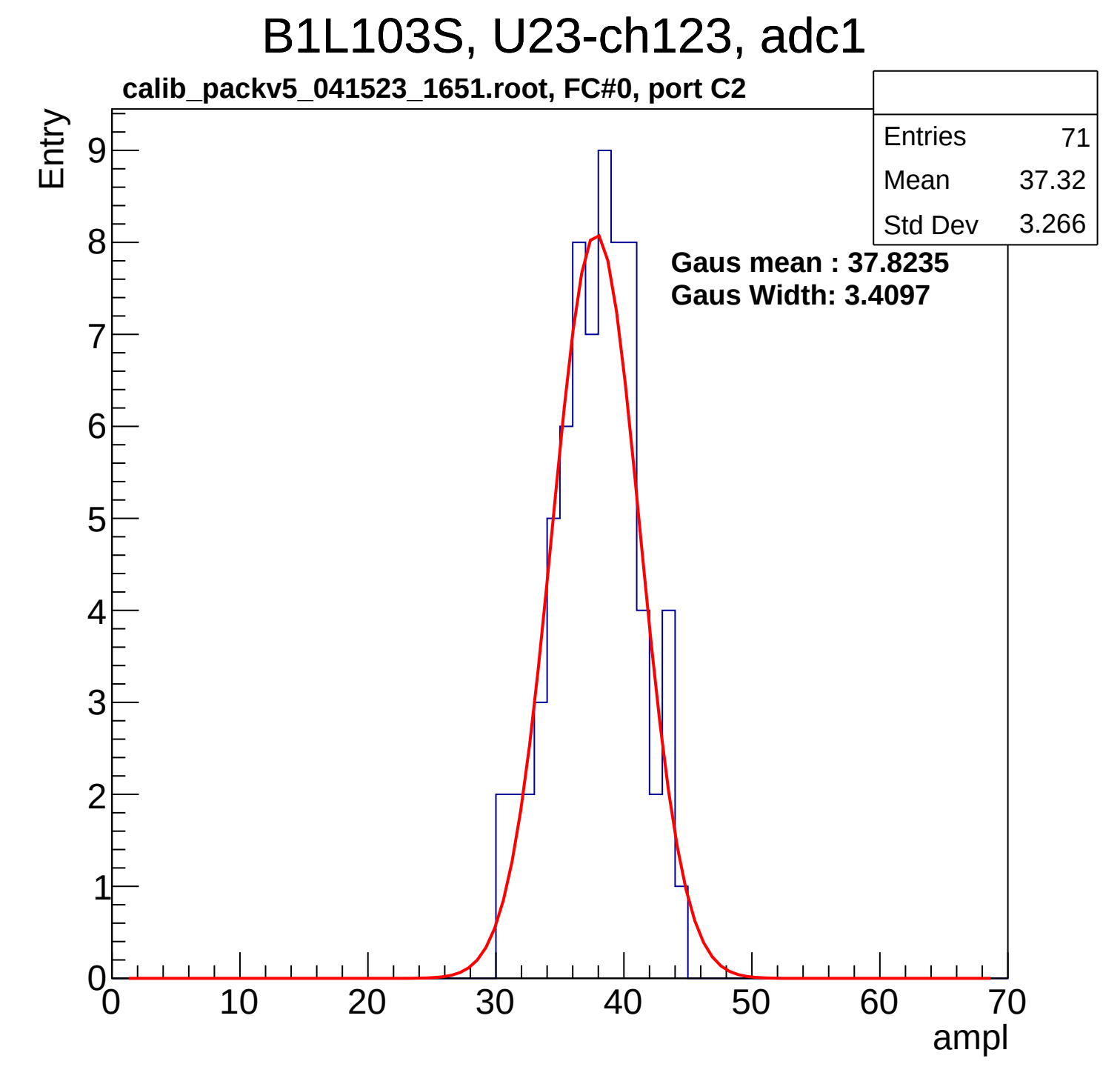

B1L103S, U23-ch122, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 53.1
Std Dev 3.495
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

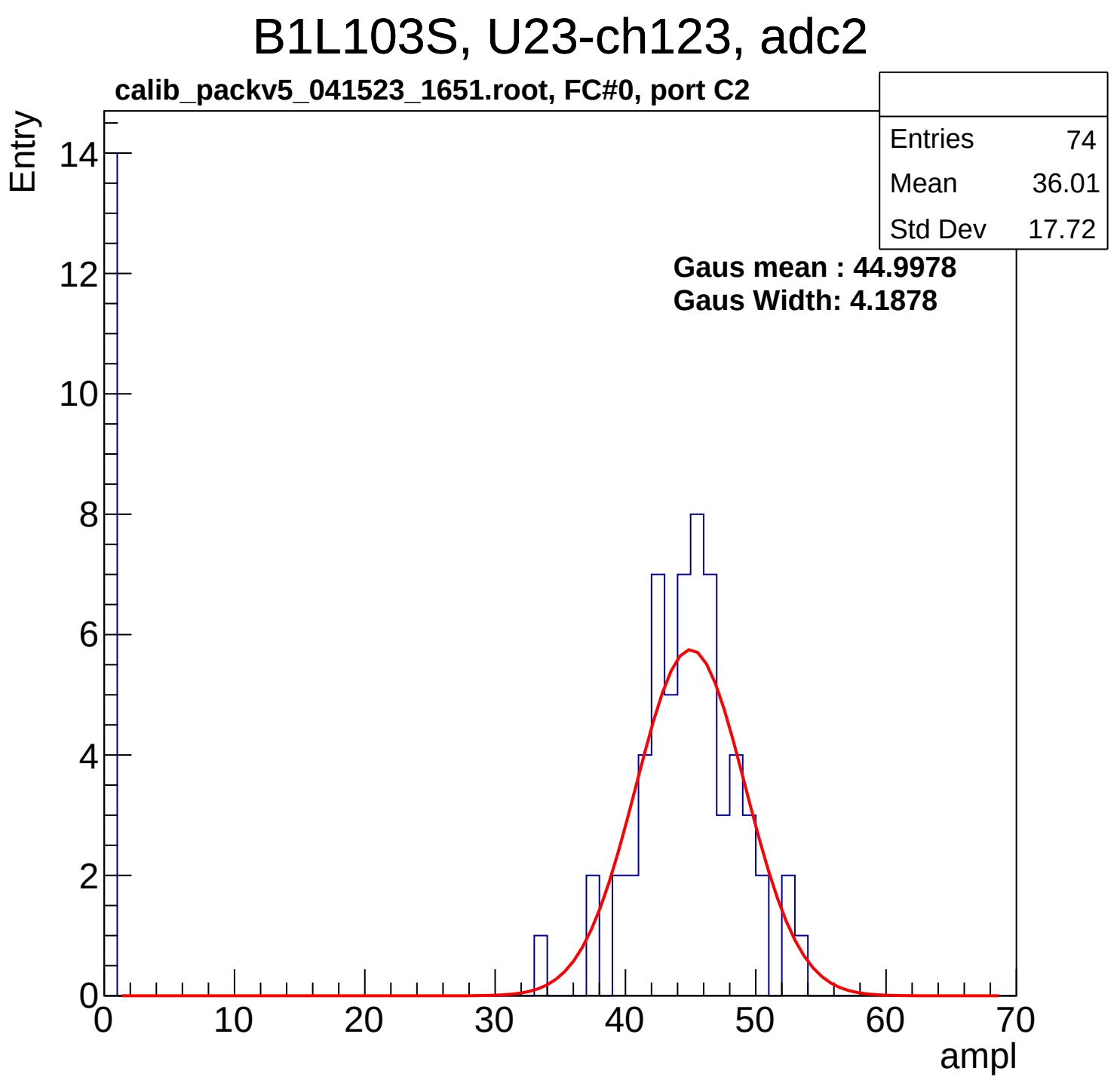

B1L103S, U23-ch122, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 58.41
Std Dev 2.953
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

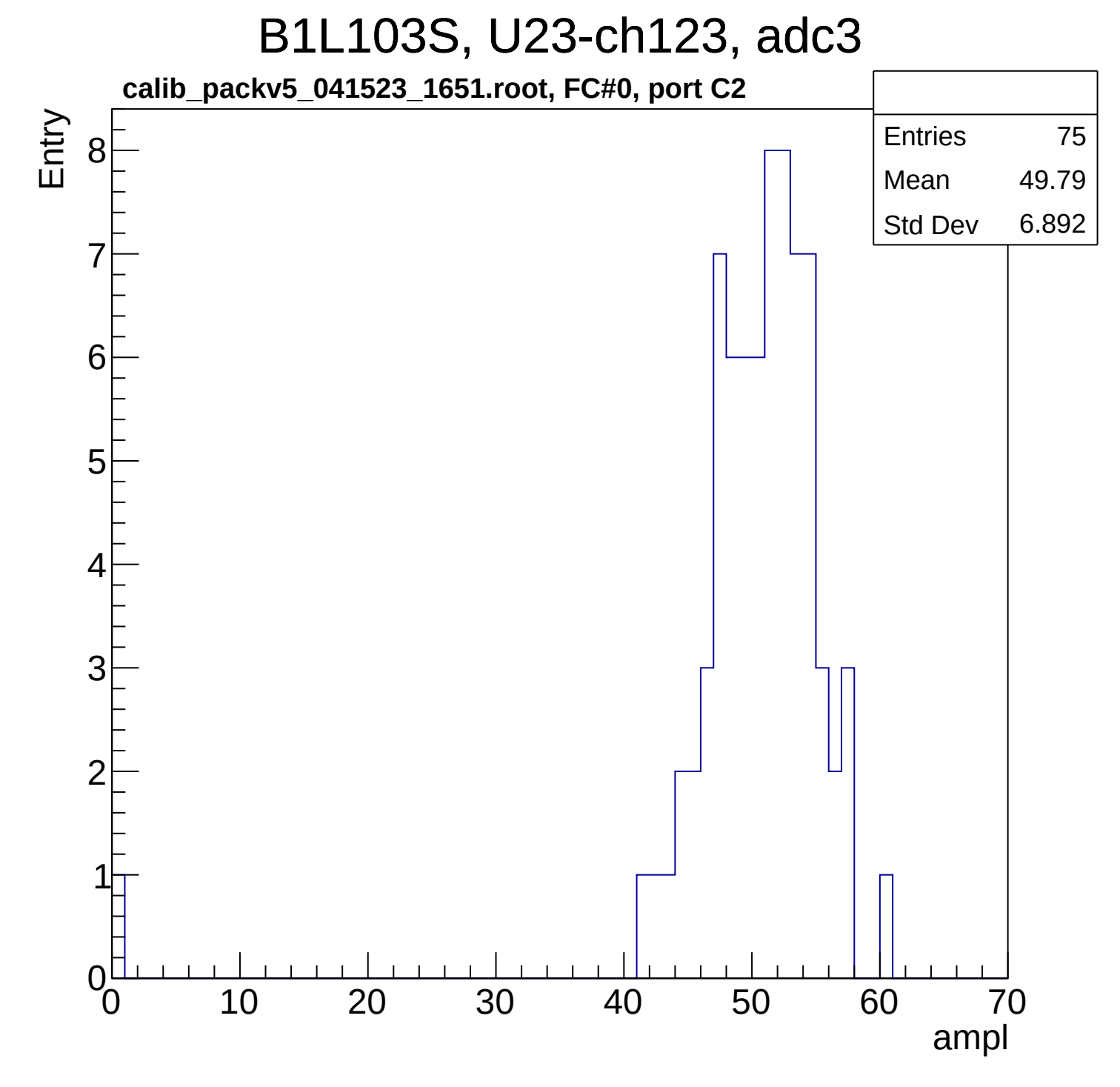

B1L103S, U23-ch122, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 15
Mean 56.8
Std Dev 15.28
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

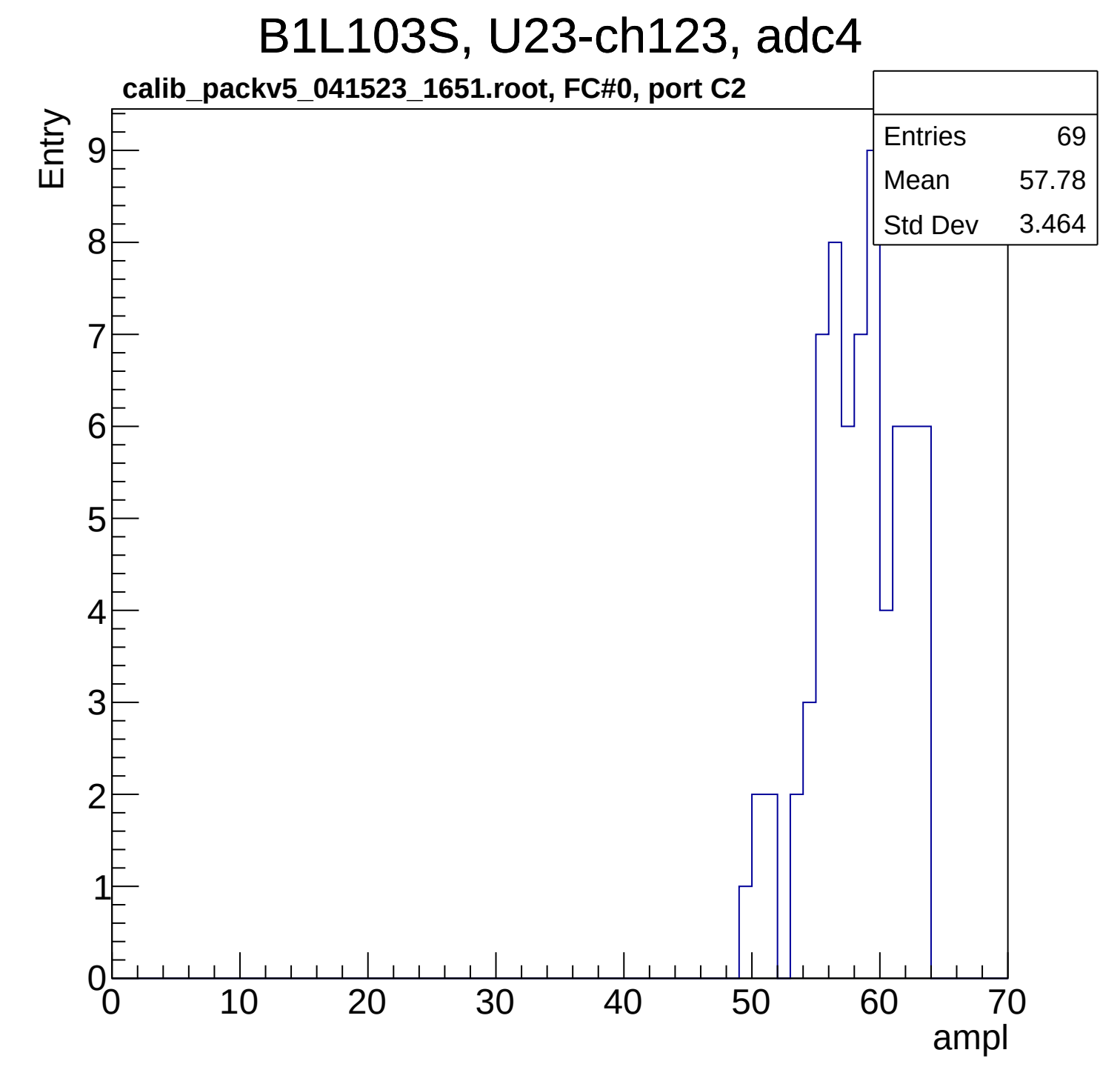

B1L103S, U23-ch122, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 6
Std Dev 18.49
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

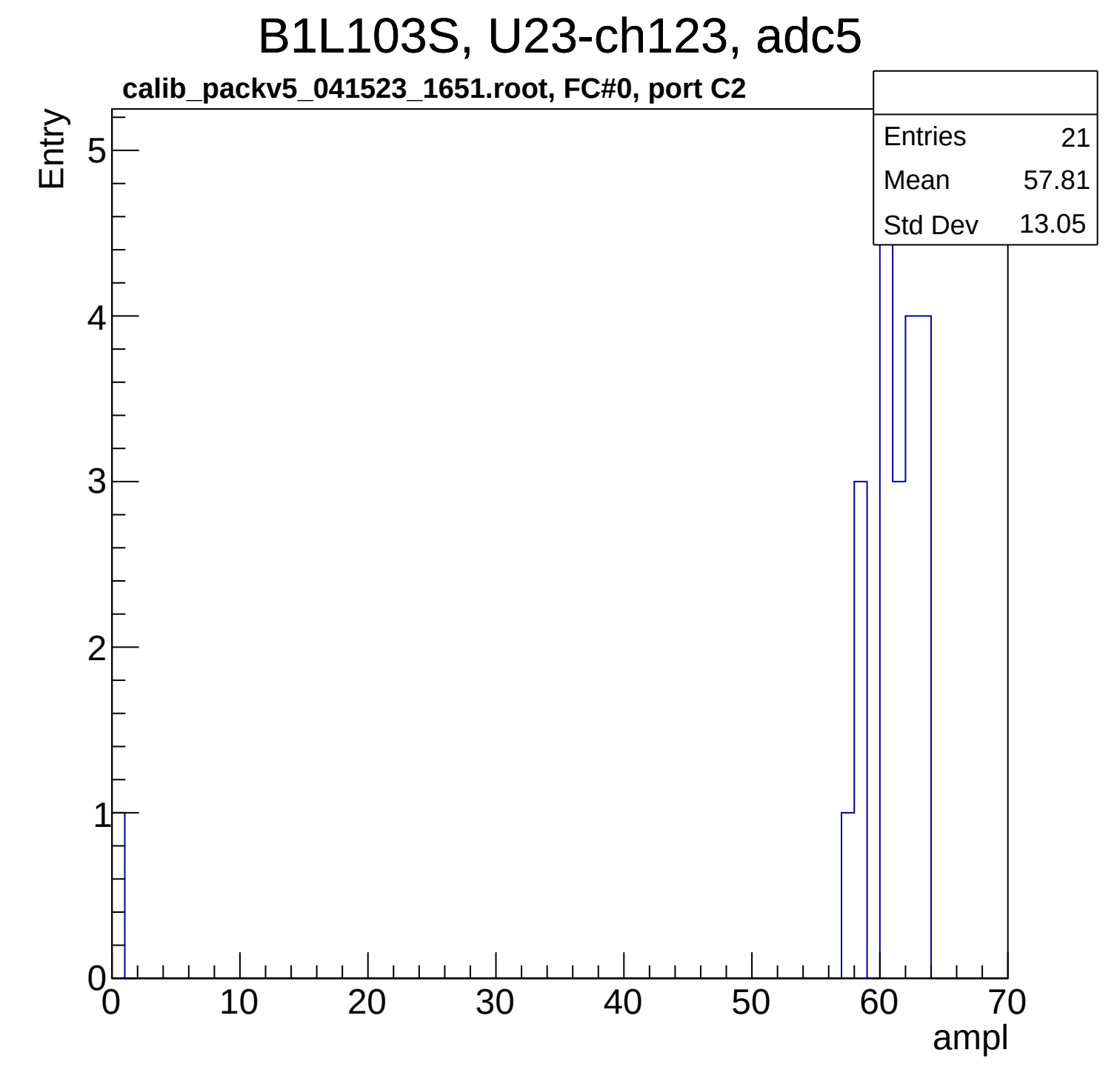

B1L103S, U23-ch123, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 87
Mean 24.68
Std Dev 12.2
Gaus mean : 31.0818
Gaus Width: 3.5572
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

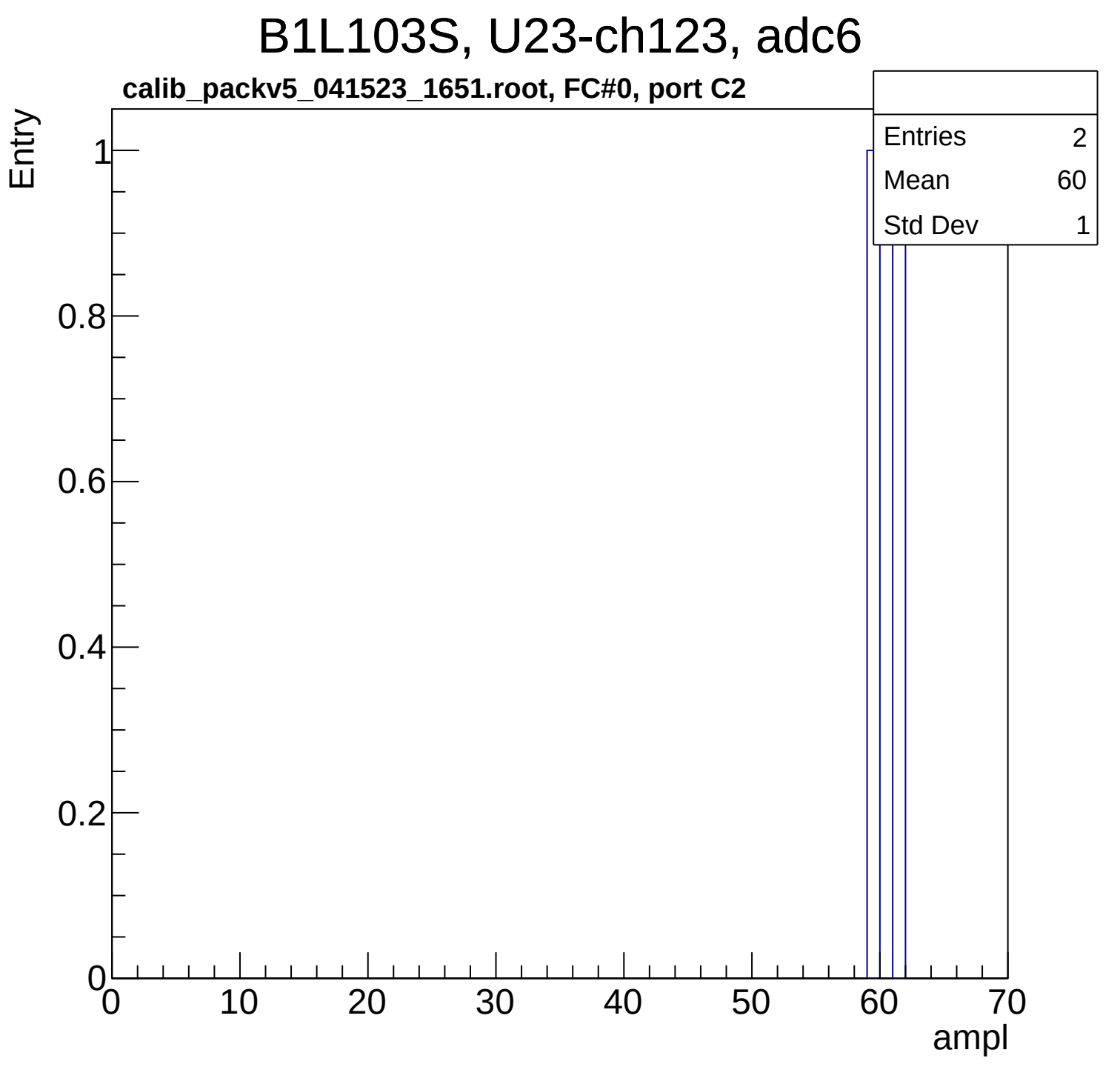

B1L103S, U23-ch123, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 71
Mean 37.32
Std Dev 3.266
Gaus mean : 37.8235
Gaus Width: 3.4097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

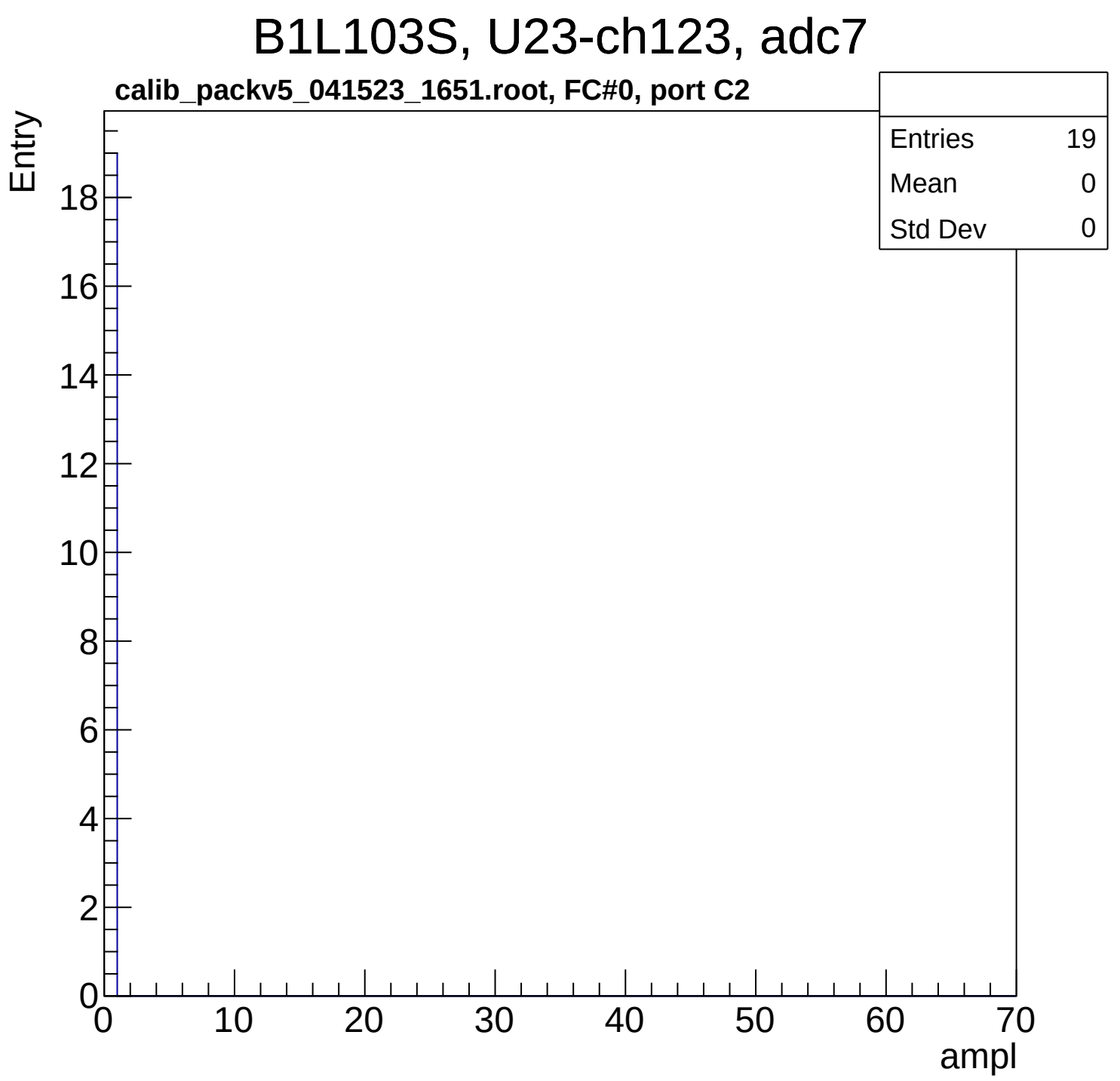

B1L103S, U23-ch123, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 36.01
Std Dev 17.72
Gaus mean : 44.9978
Gaus Width: 4.1878
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

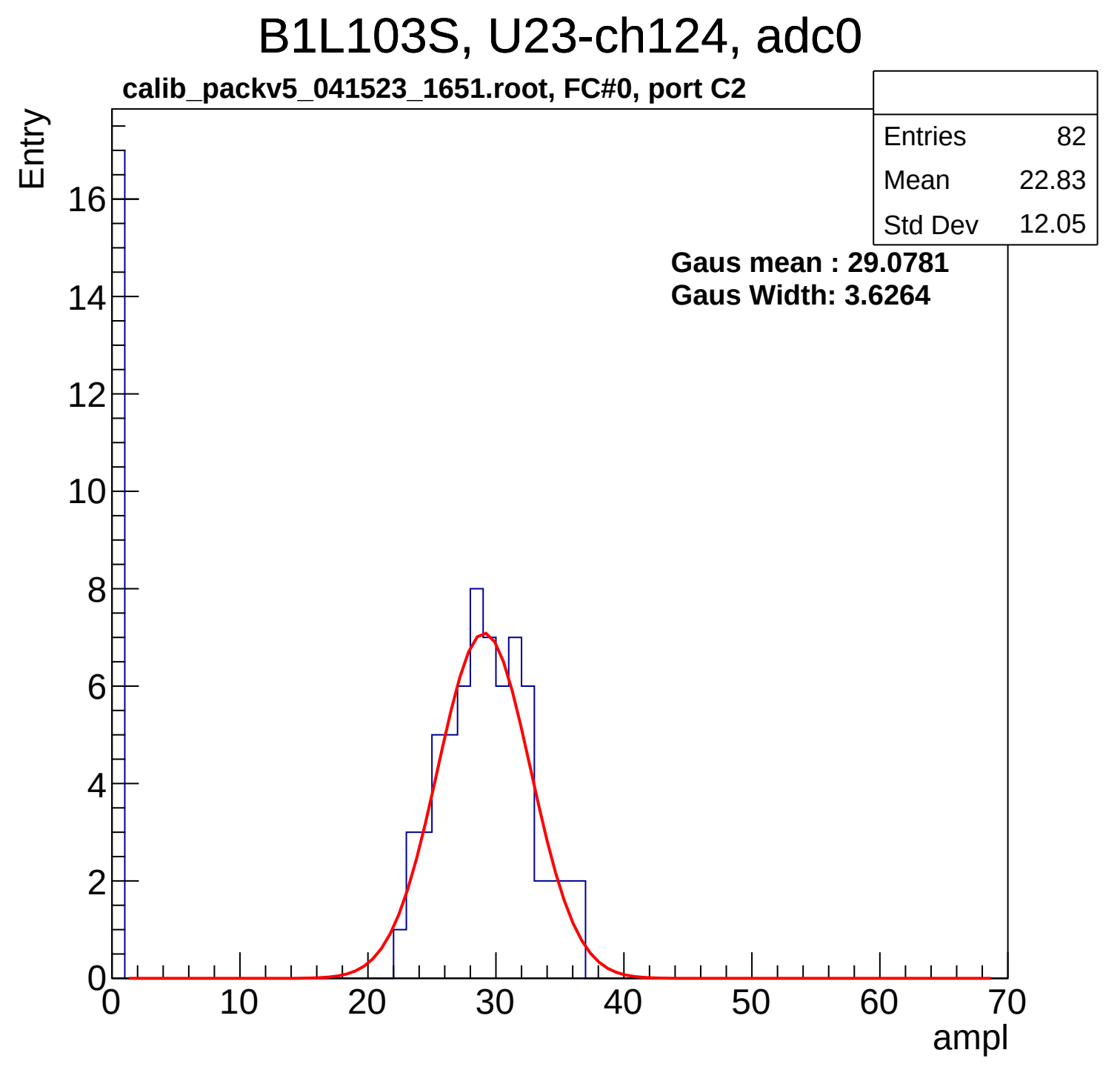

B1L103S, U23-ch123, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 49.79
Std Dev 6.892
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

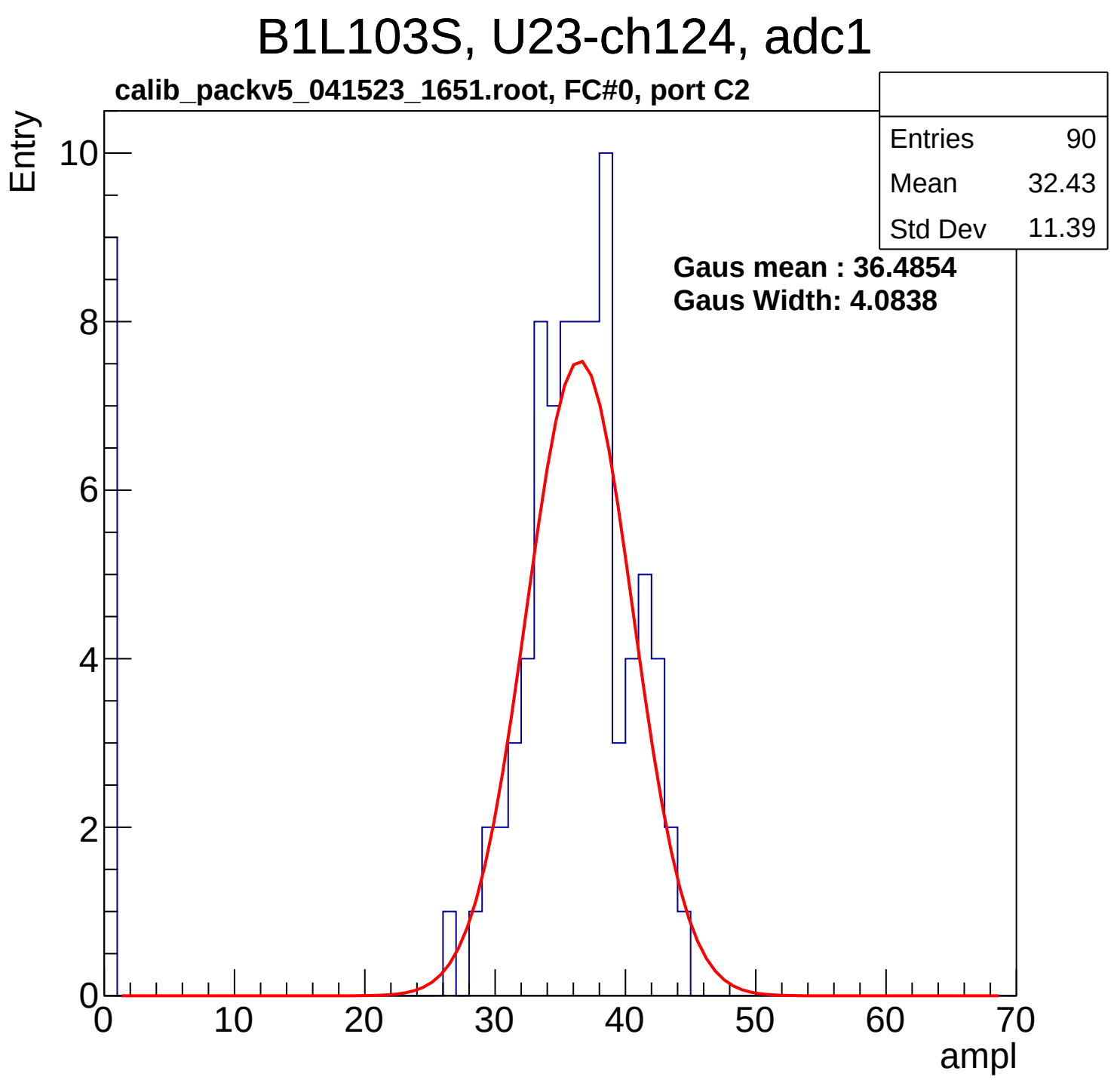

B1L103S, U23-ch123, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 69
Mean 57.78
Std Dev 3.464
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

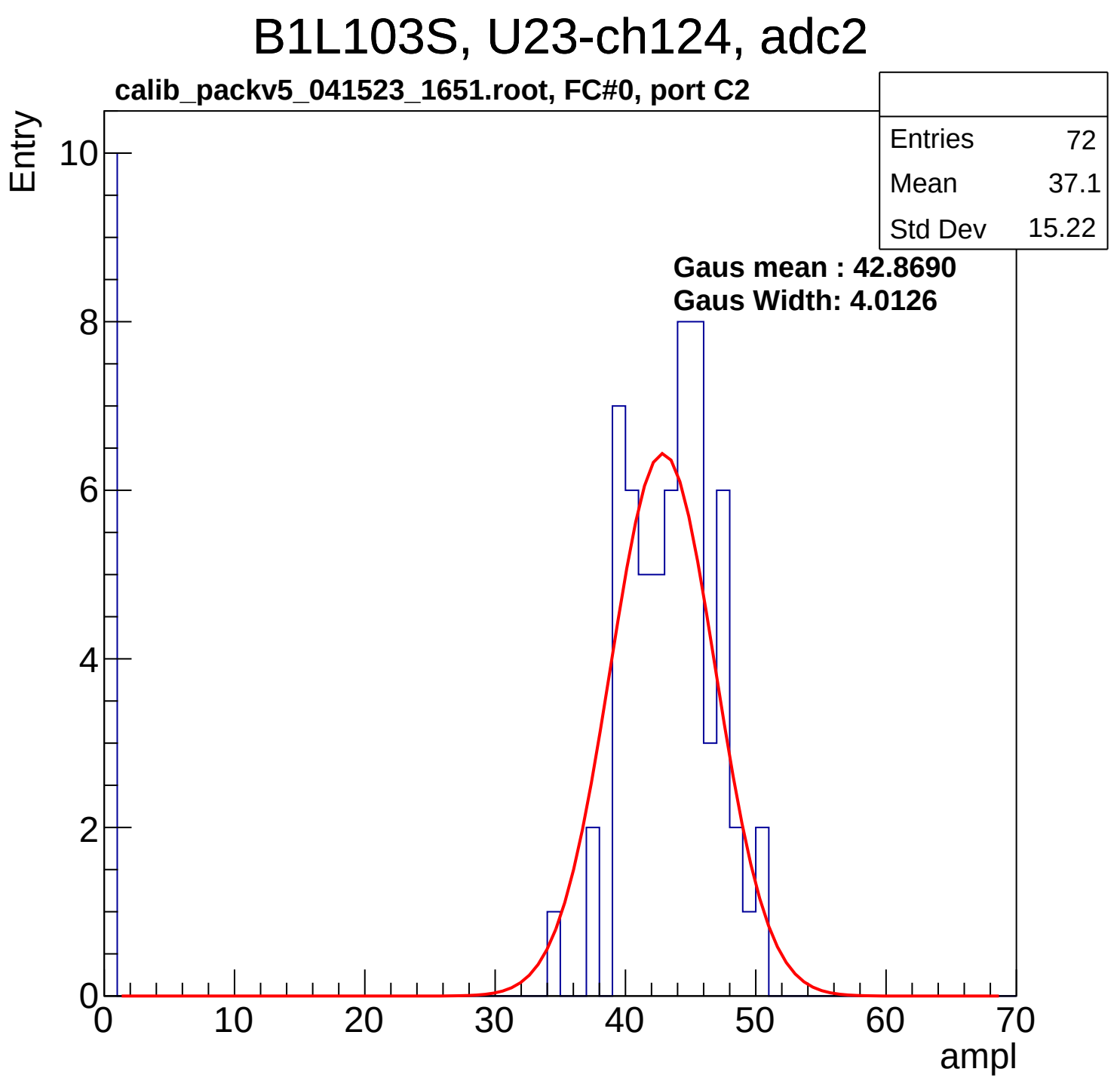

B1L103S, U23-ch123, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 57.81
Std Dev 13.05
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

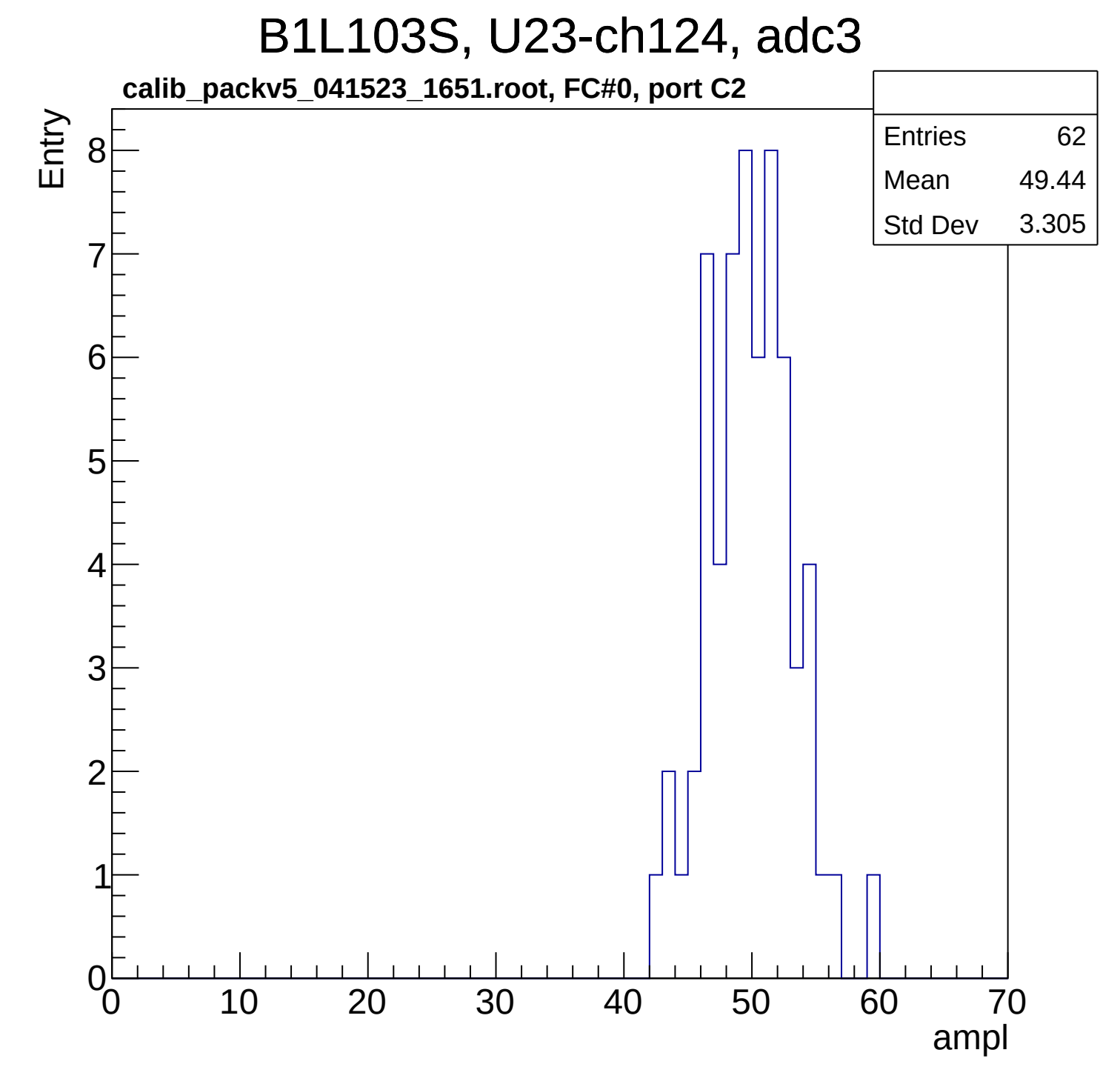

B1L103S, U23-ch123, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 2
Mean 60
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

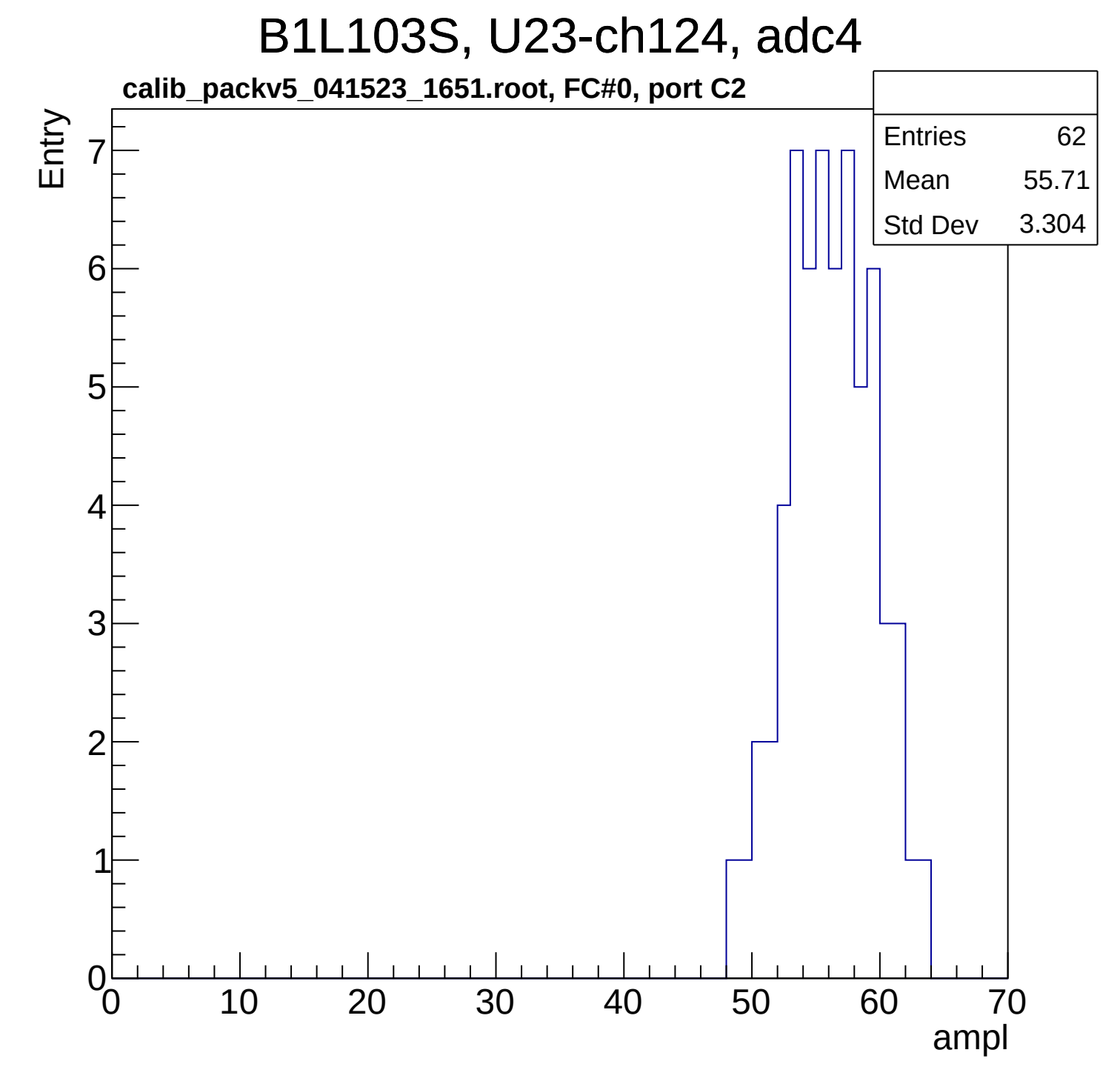

B1L103S, U23-ch123, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

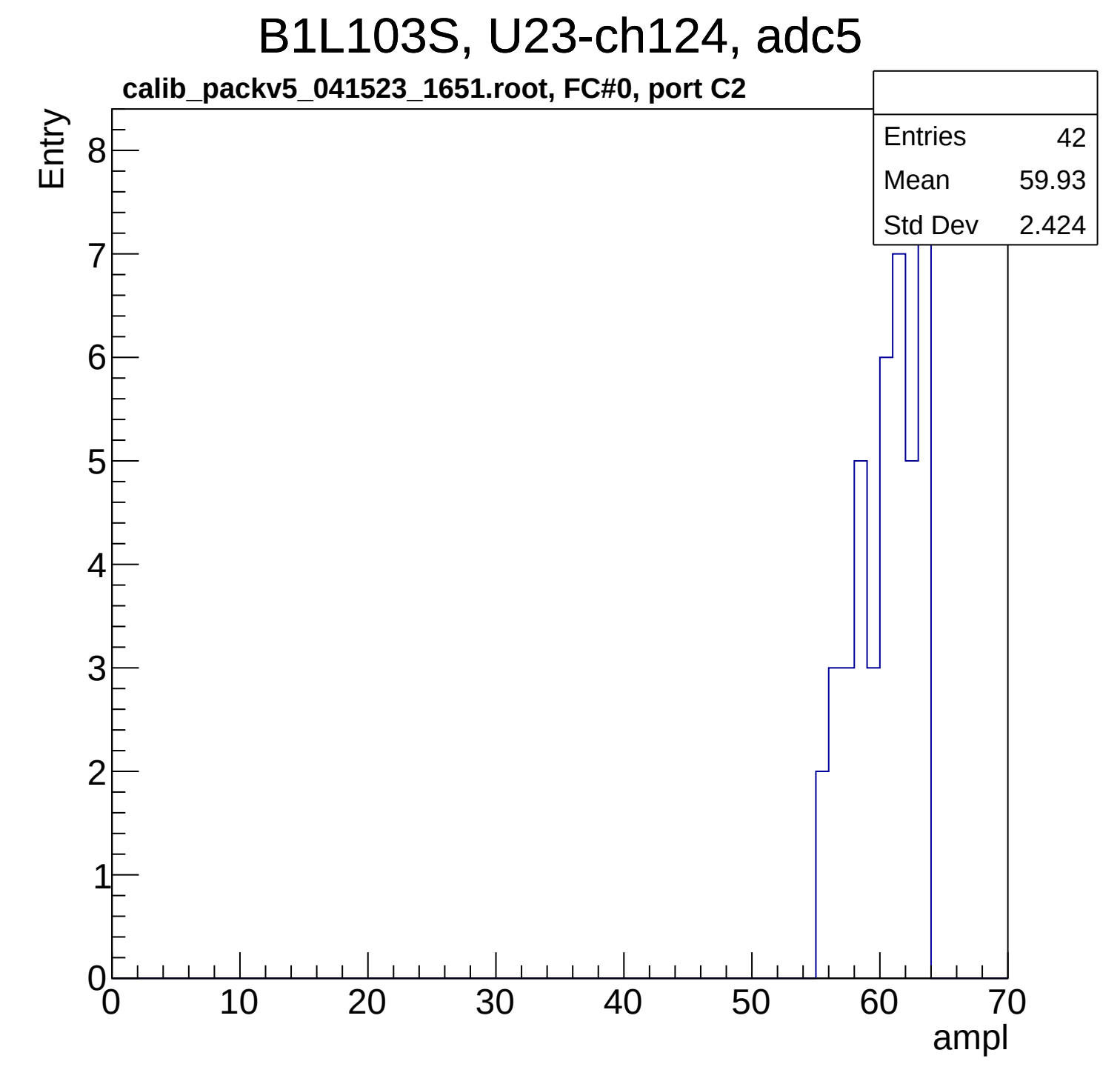

B1L103S, U23-ch124, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 82
Mean 22.83
Std Dev 12.05
Gaus mean : 29.0781
Gaus Width: 3.6264
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

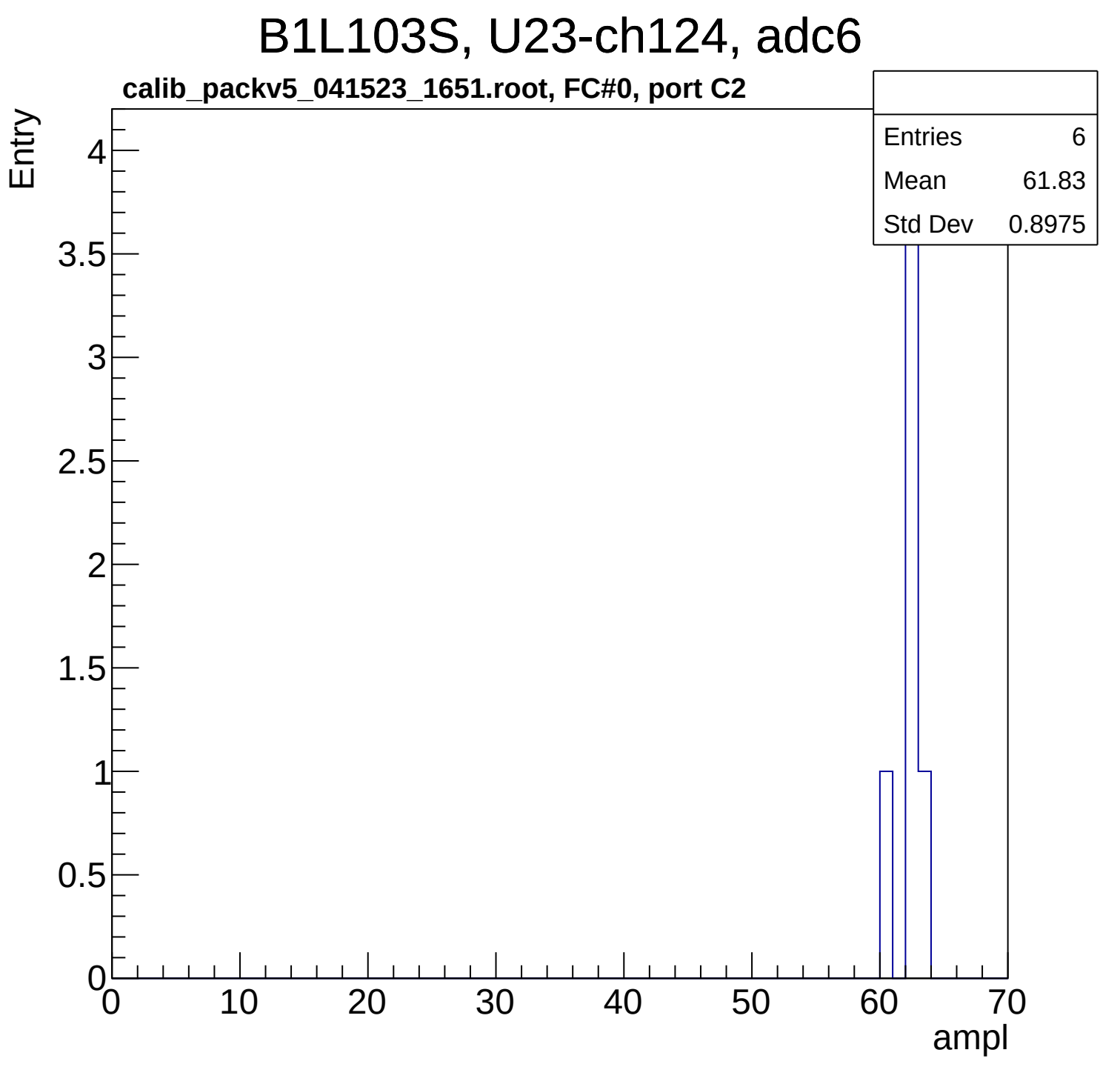

B1L103S, U23-ch124, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 90
Mean 32.43
Std Dev 11.39
Gaus mean : 36.4854
Gaus Width: 4.0838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

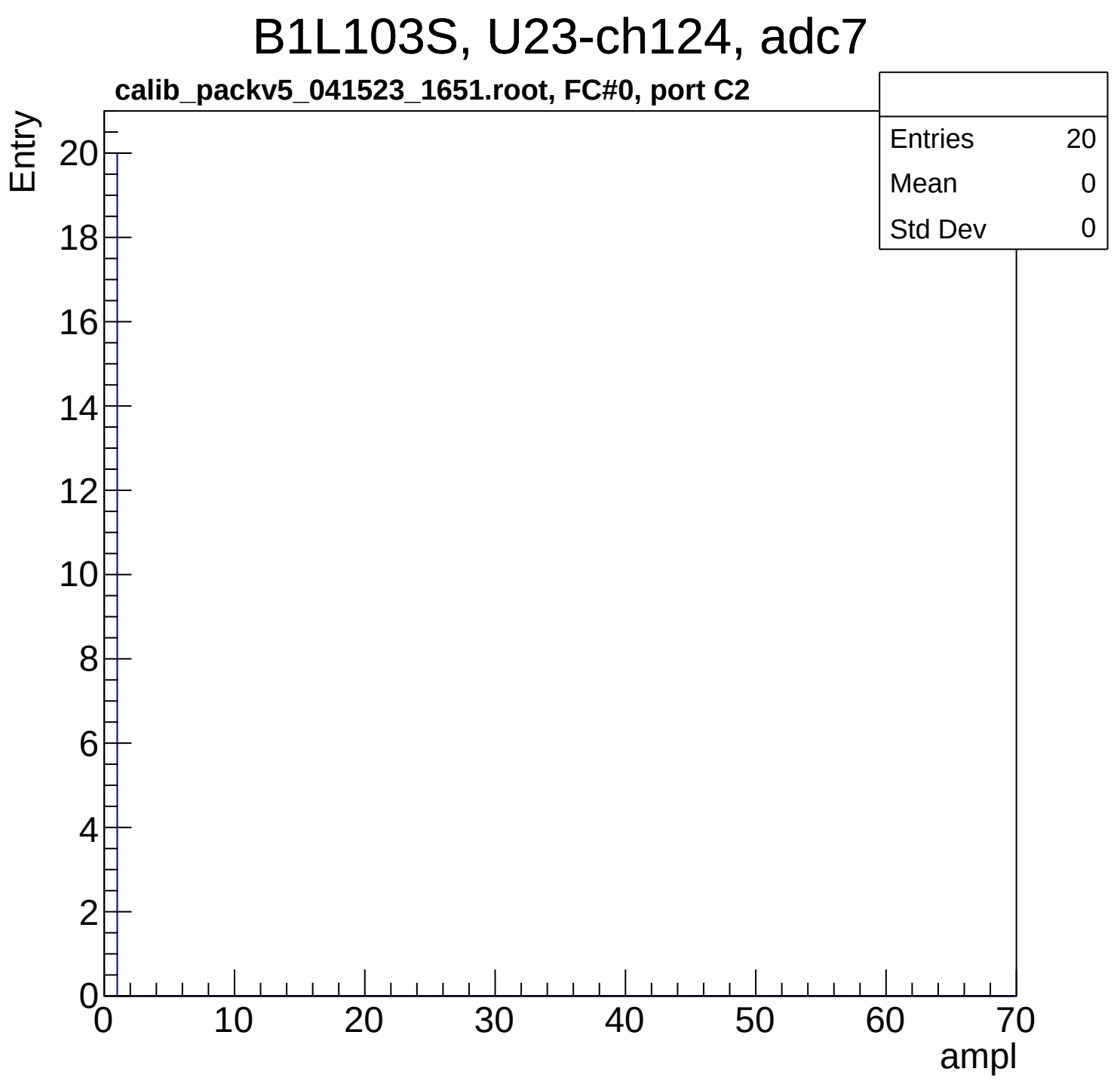

B1L103S, U23-ch124, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 72
Mean 37.1
Std Dev 15.22
Gaus mean : 42.8690
Gaus Width: 4.0126
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

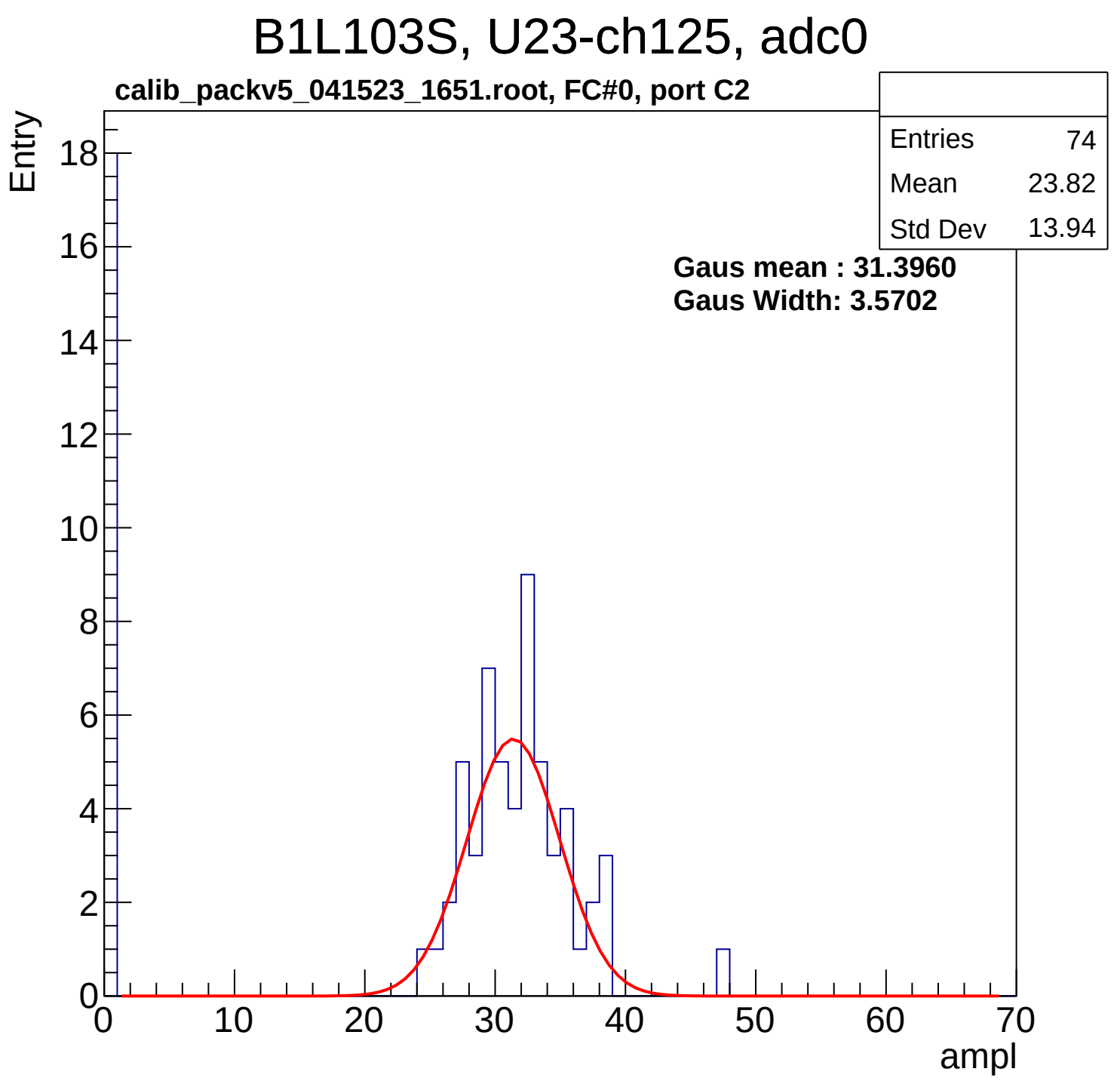

B1L103S, U23-ch124, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 49.44
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

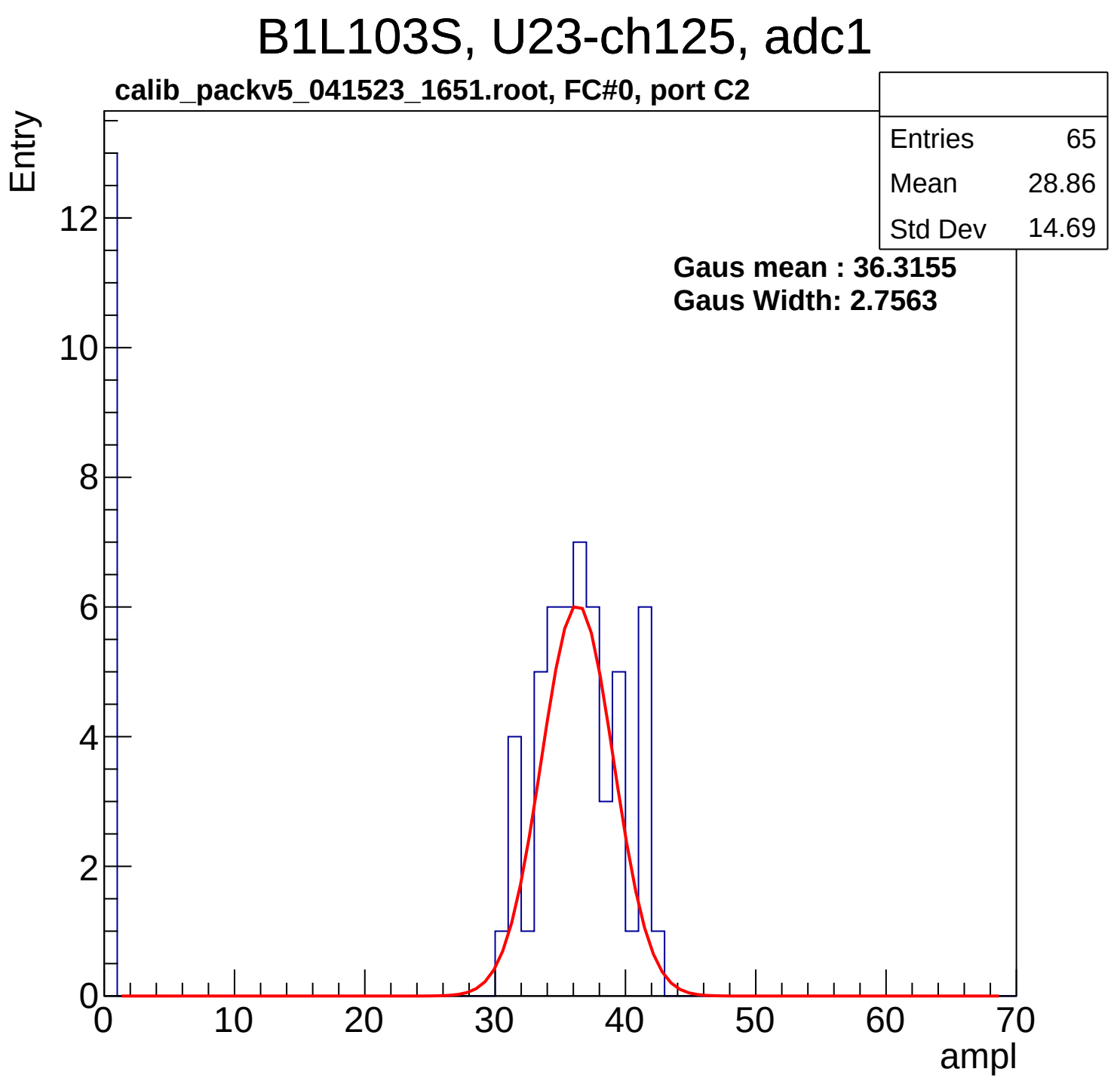

B1L103S, U23-ch124, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 62
Mean 55.71
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

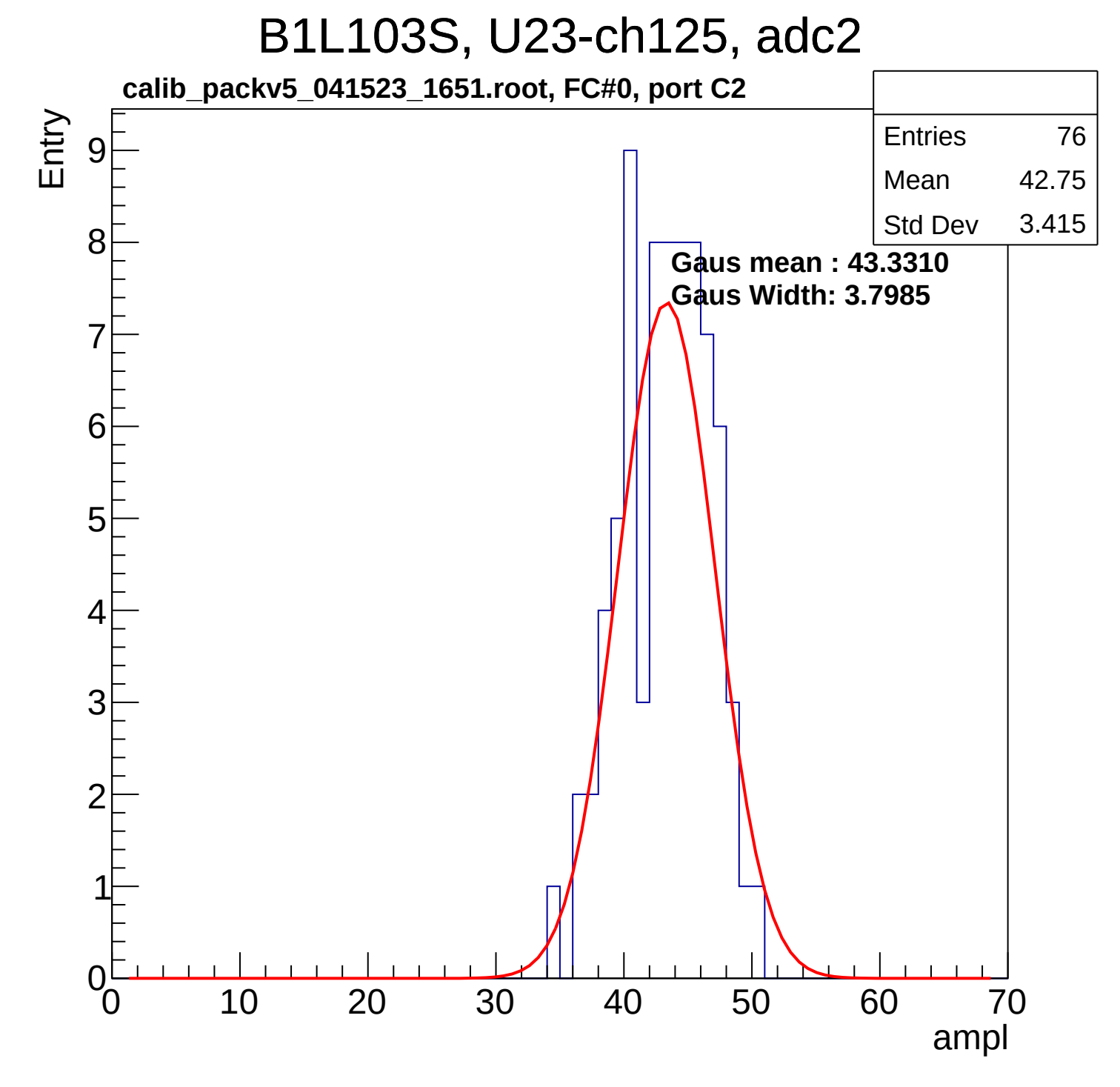

B1L103S, U23-ch124, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 42
Mean 59.93
Std Dev 2.424
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

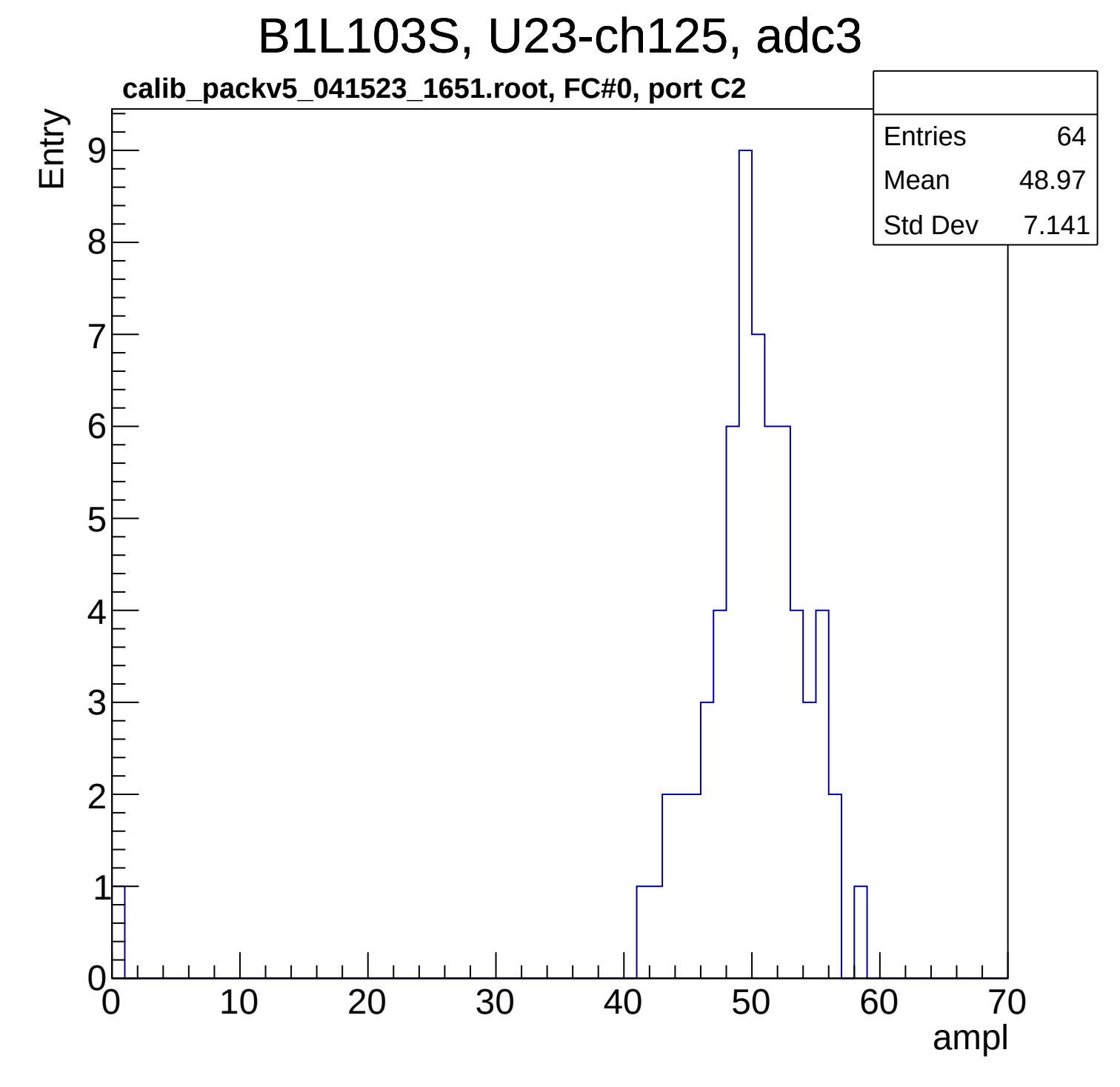

B1L103S, U23-ch124, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 6
Mean 61.83
Std Dev 0.8975
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

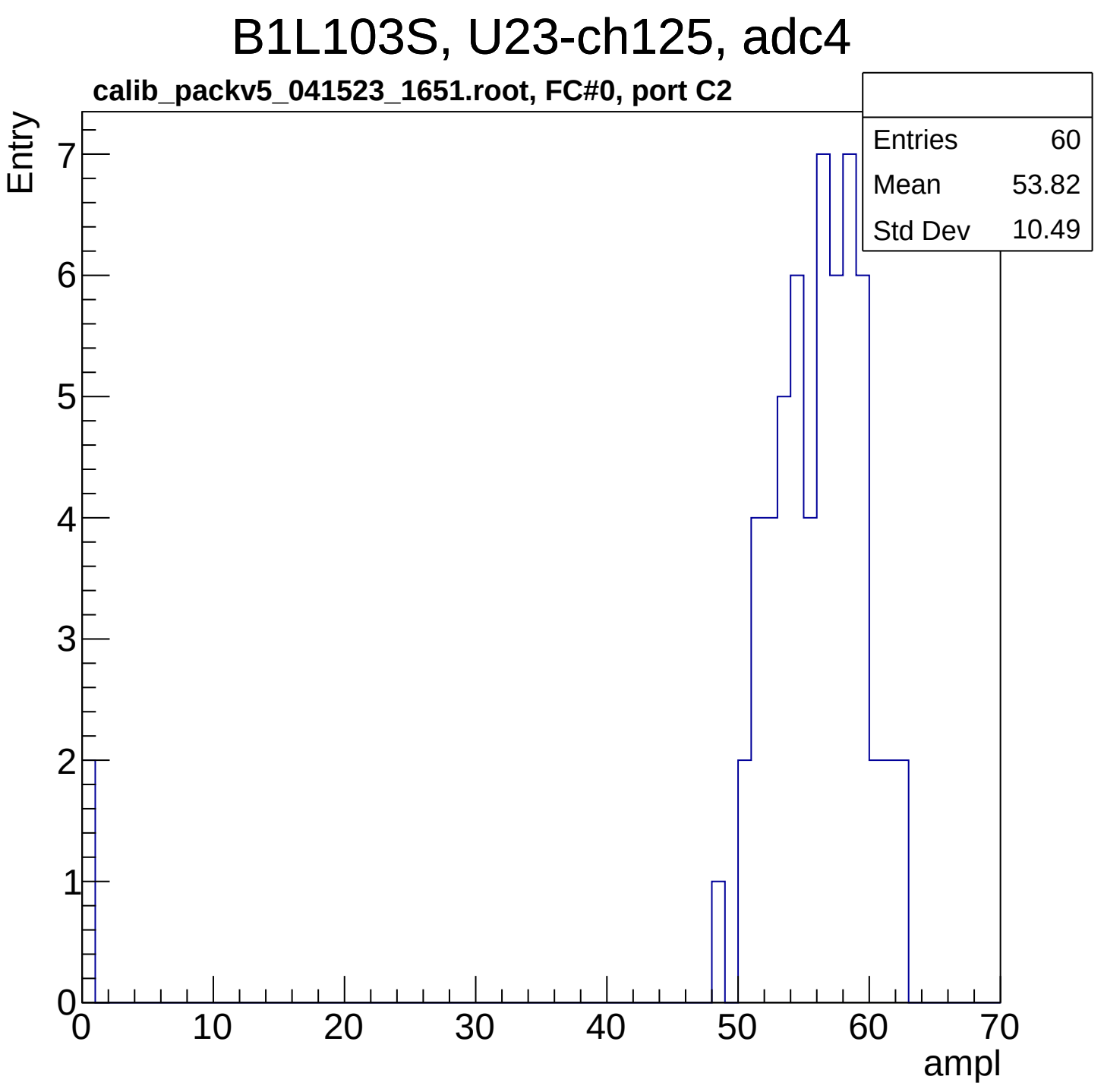

B1L103S, U23-ch124, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 20
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70
ampl

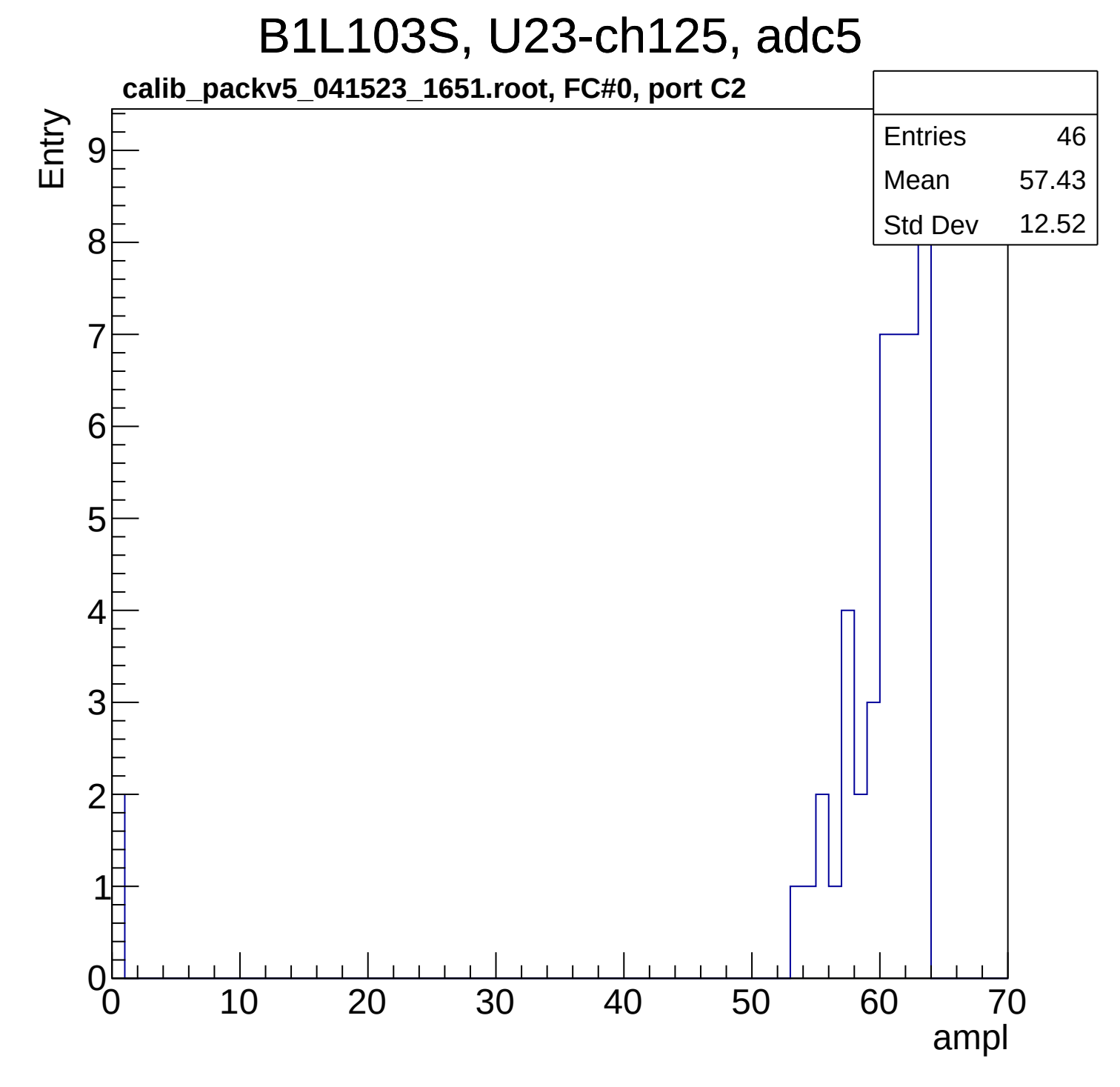

B1L103S, U23-ch125, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 74
Mean 23.82
Std Dev 13.94
Gaus mean : 31.3960
Gaus Width: 3.5702
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

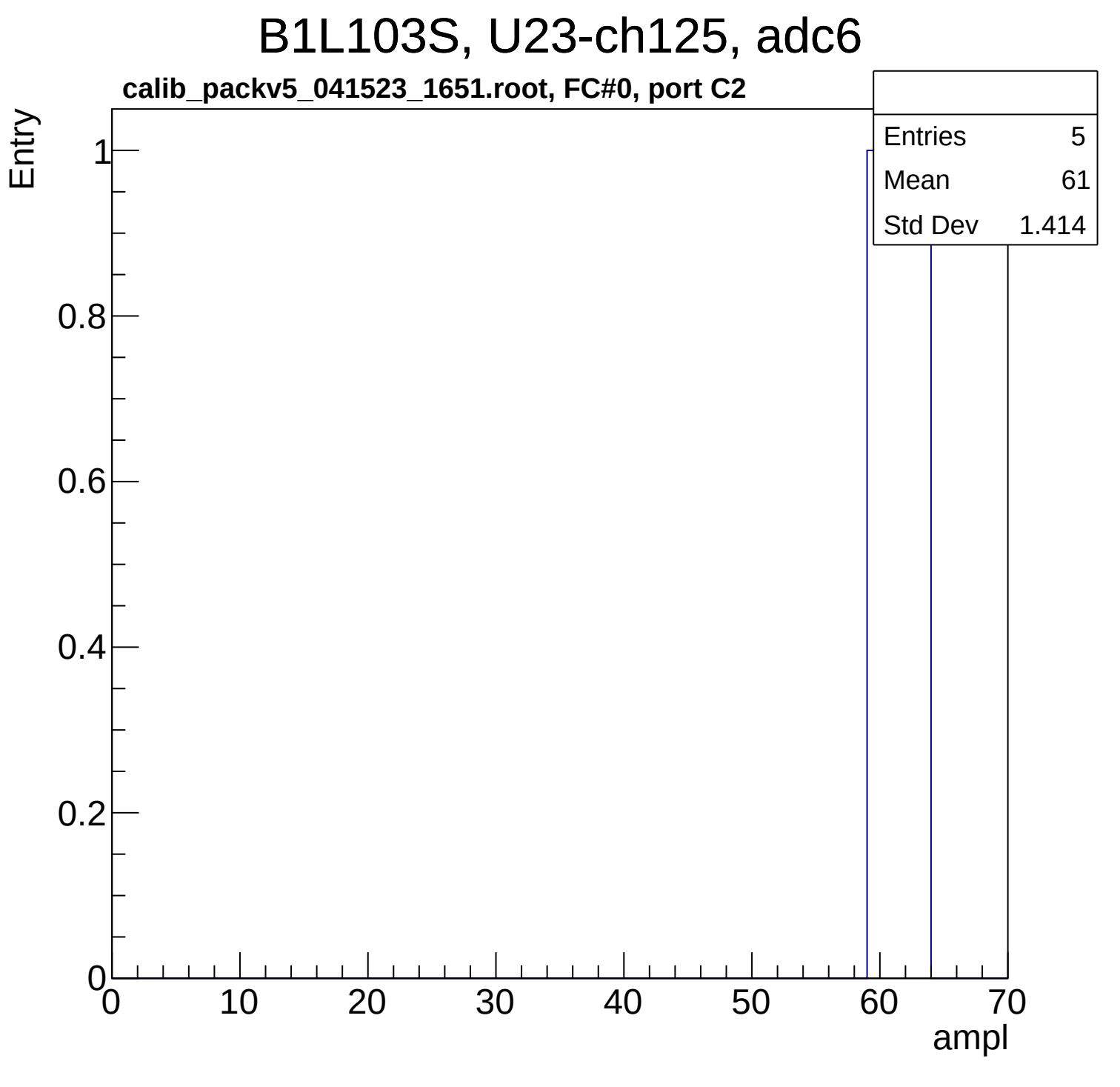

B1L103S, U23-ch125, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 65
Mean 28.86
Std Dev 14.69
Gaus mean : 36.3155
Gaus Width: 2.7563
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

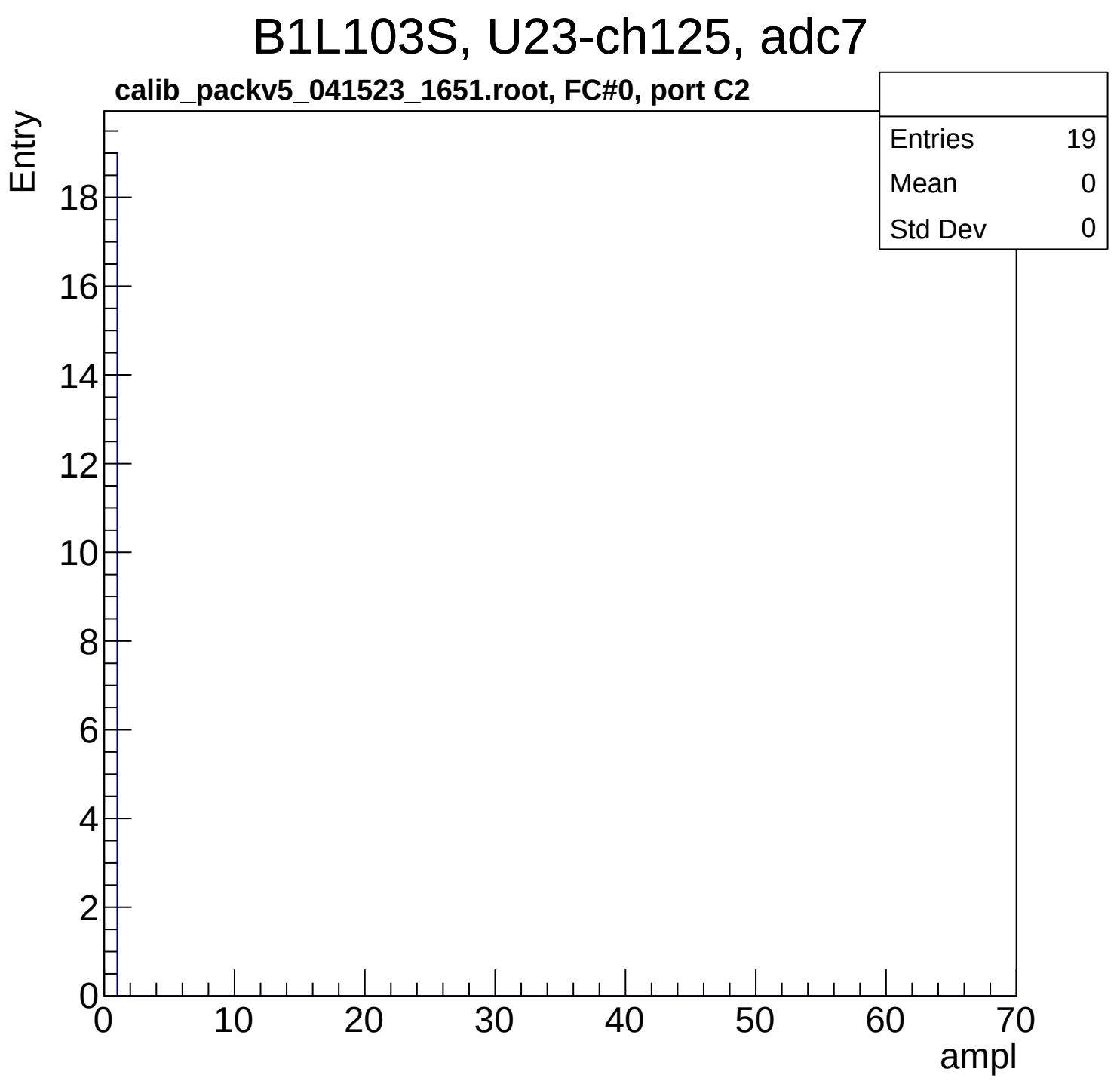

B1L103S, U23-ch125, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 76
Mean 42.75
Std Dev 3.415
Gaus mean : 43.3310
Gaus Width: 3.7985
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

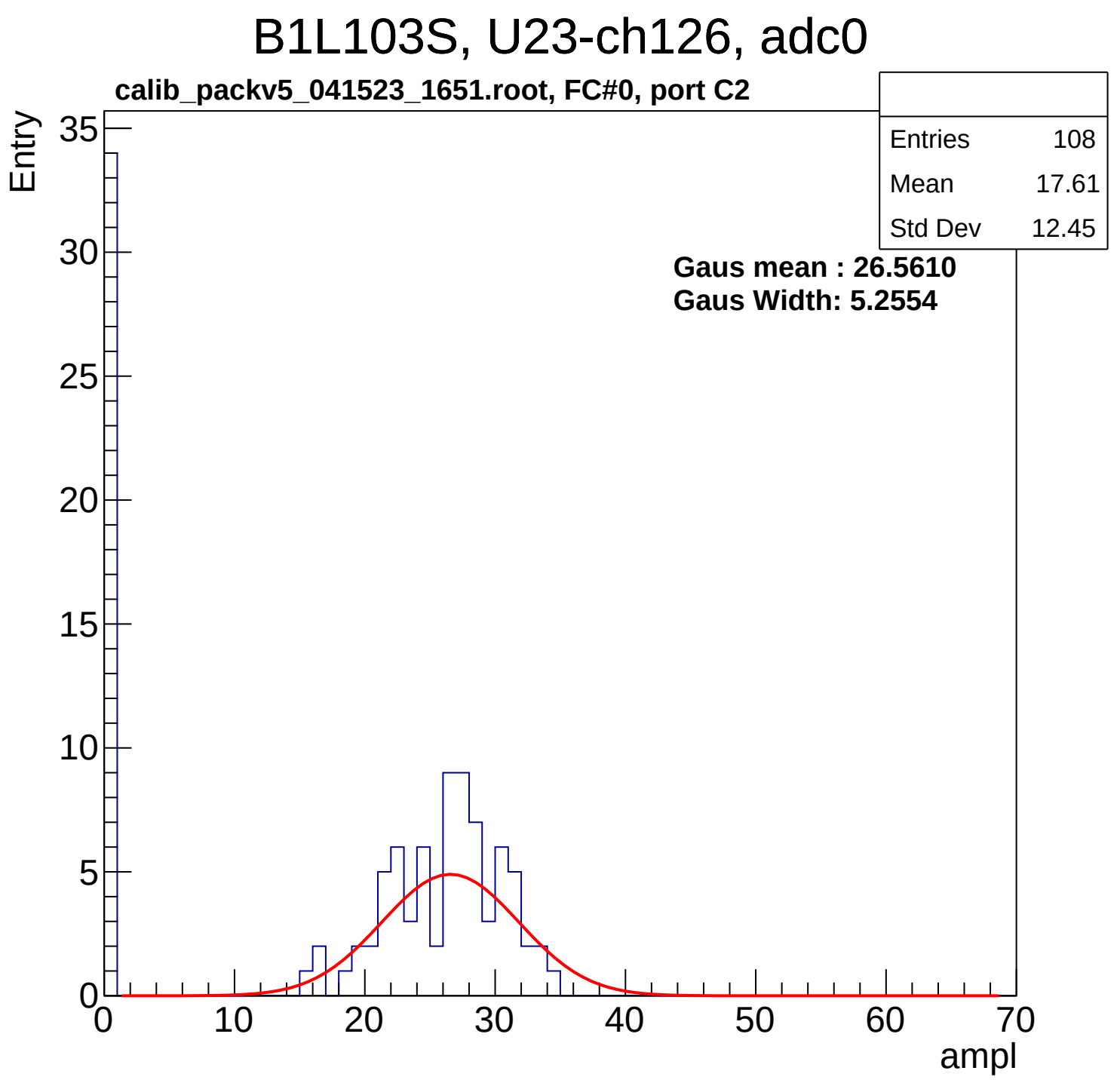

B1L103S, U23-ch125, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 48.97
Std Dev 7.141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

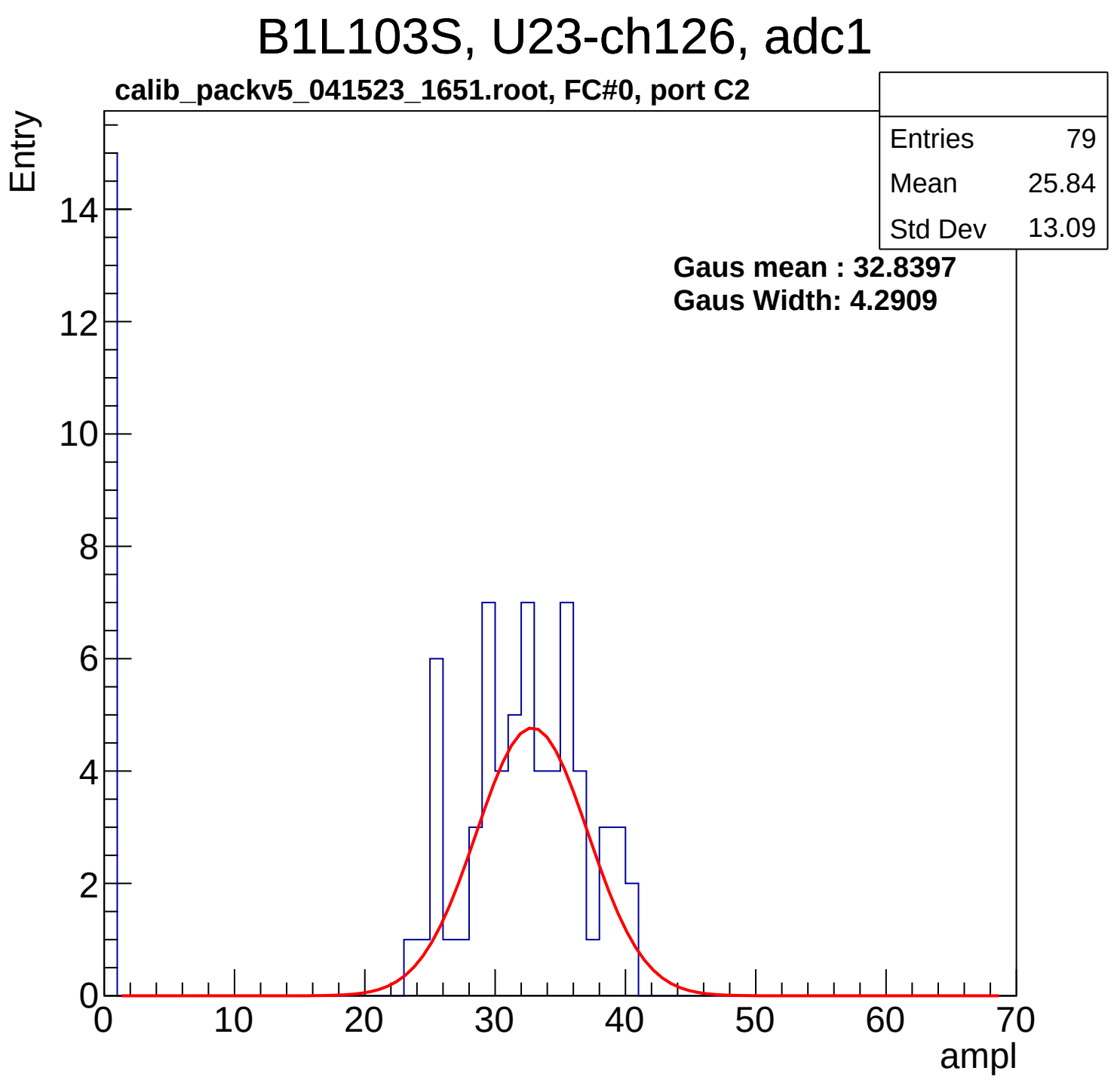

B1L103S, U23-ch125, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 60
Mean 53.82
Std Dev 10.49
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

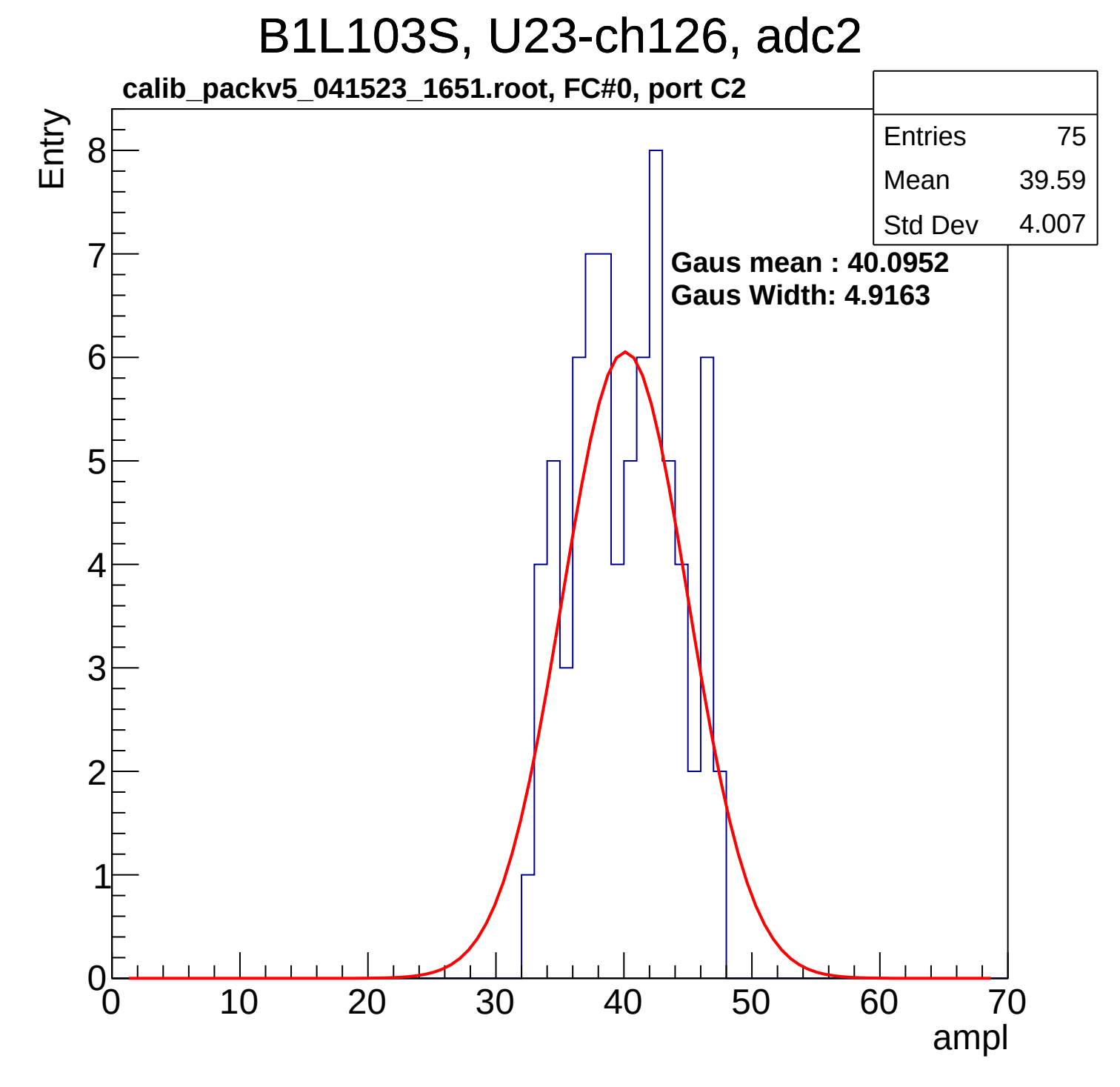

B1L103S, U23-ch125, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 46
Mean 57.43
Std Dev 12.52
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

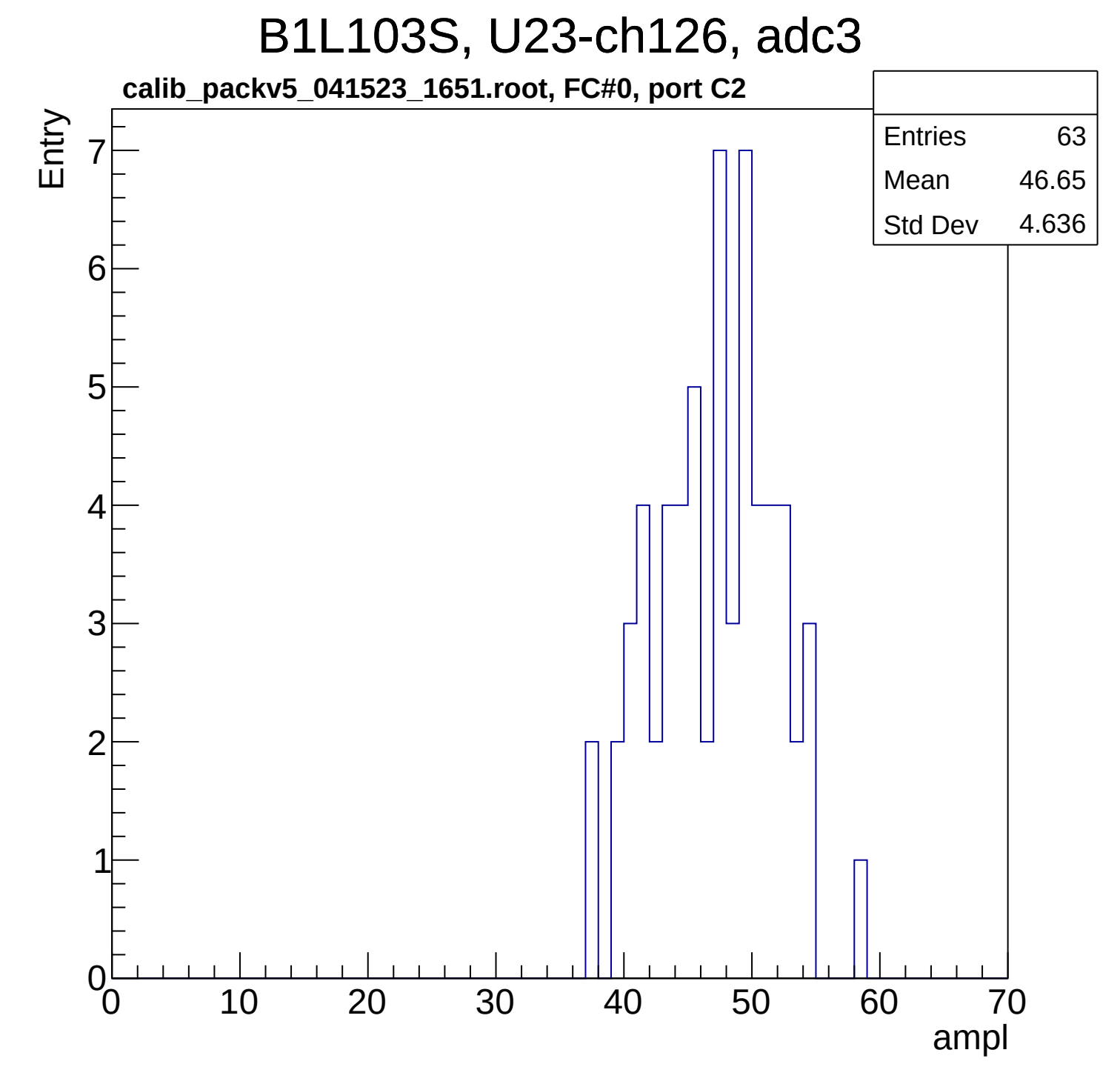

B1L103S, U23-ch125, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 5
Mean 61
Std Dev 1.414
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

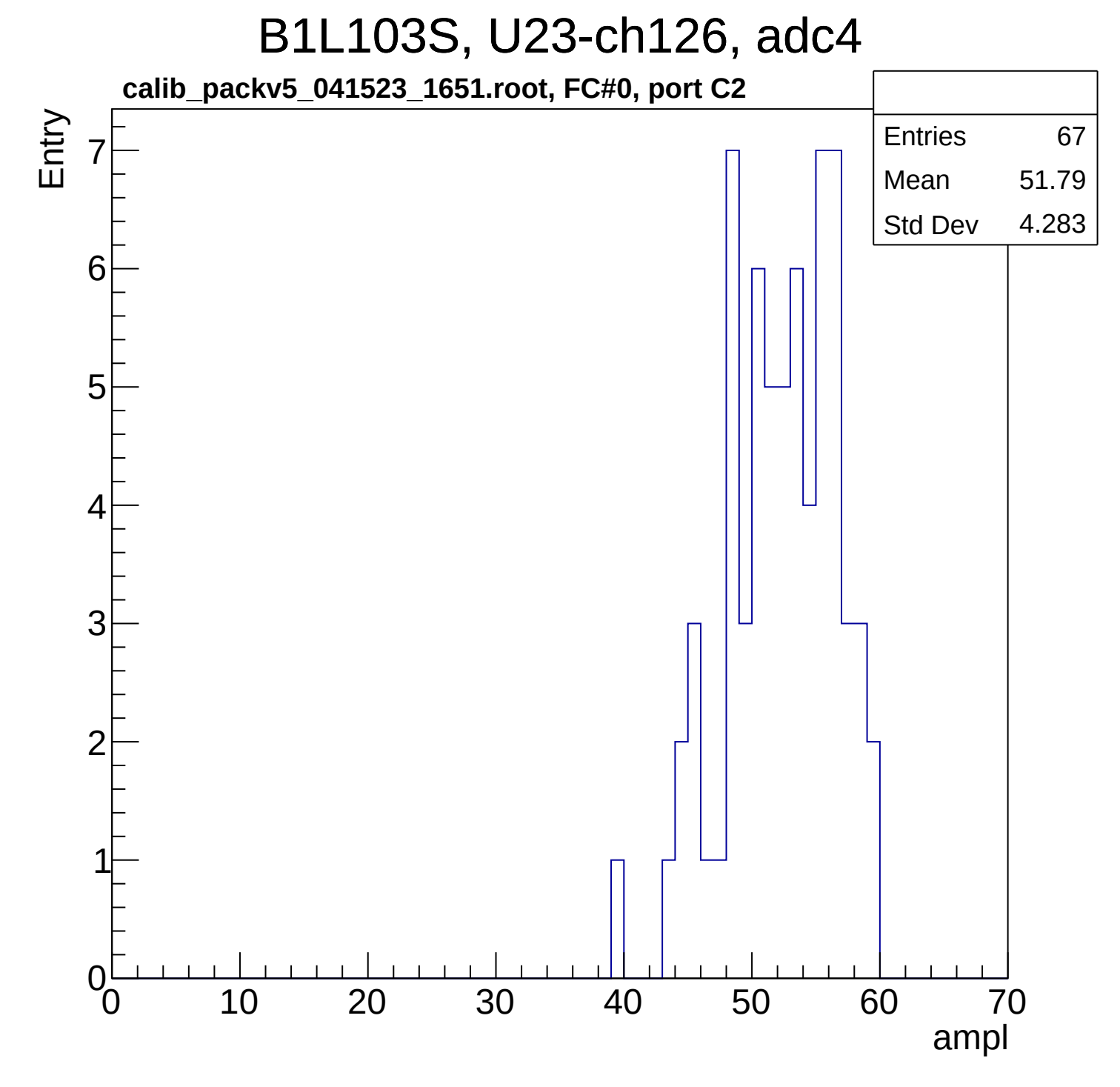

B1L103S, U23-ch125, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 19
Mean 0
Std Dev 0
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

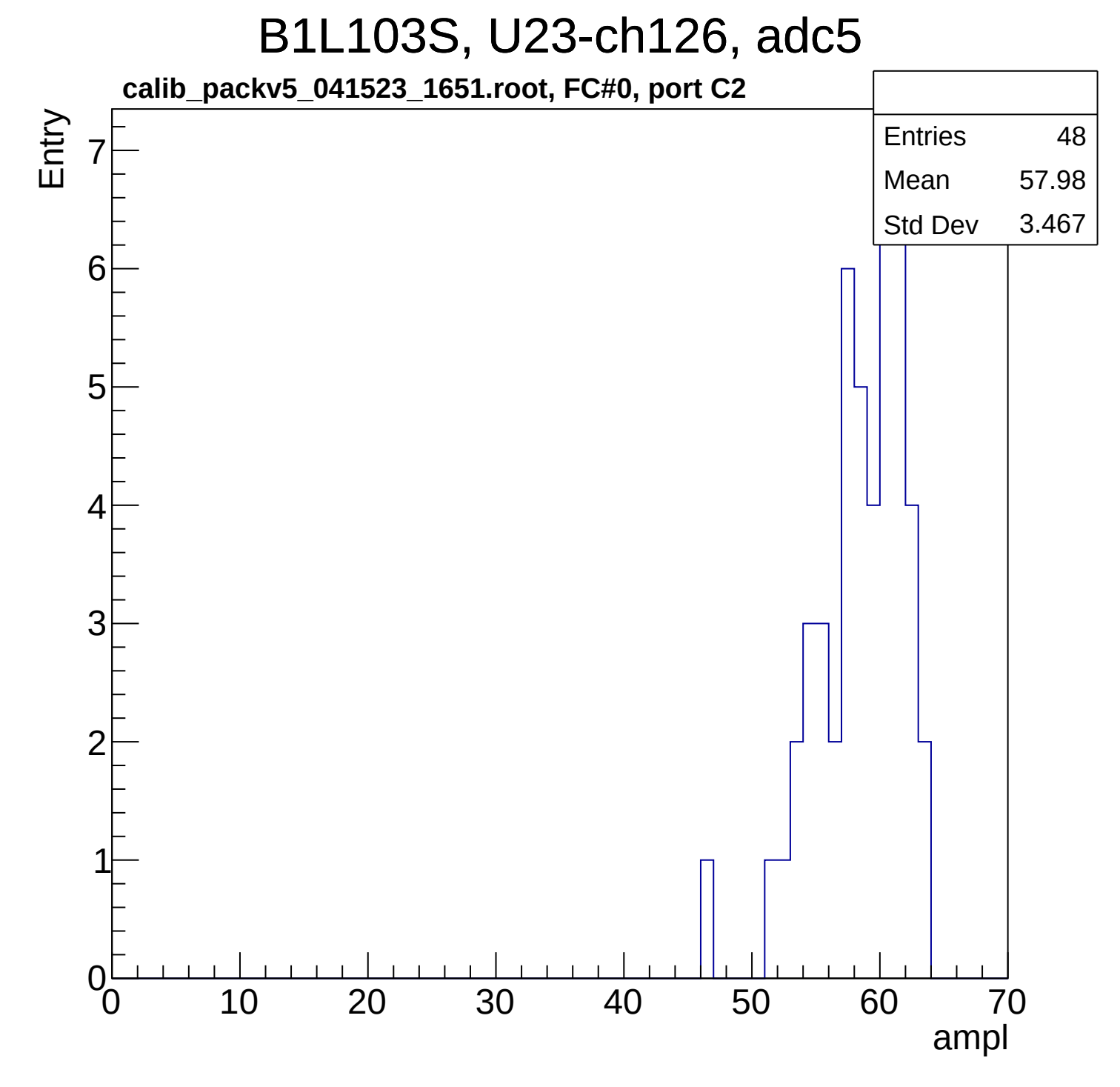

B1L103S, U23-ch126, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 108
Mean 17.61
Std Dev 12.45
Gaus mean : 26.5610
Gaus Width: 5.2554
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

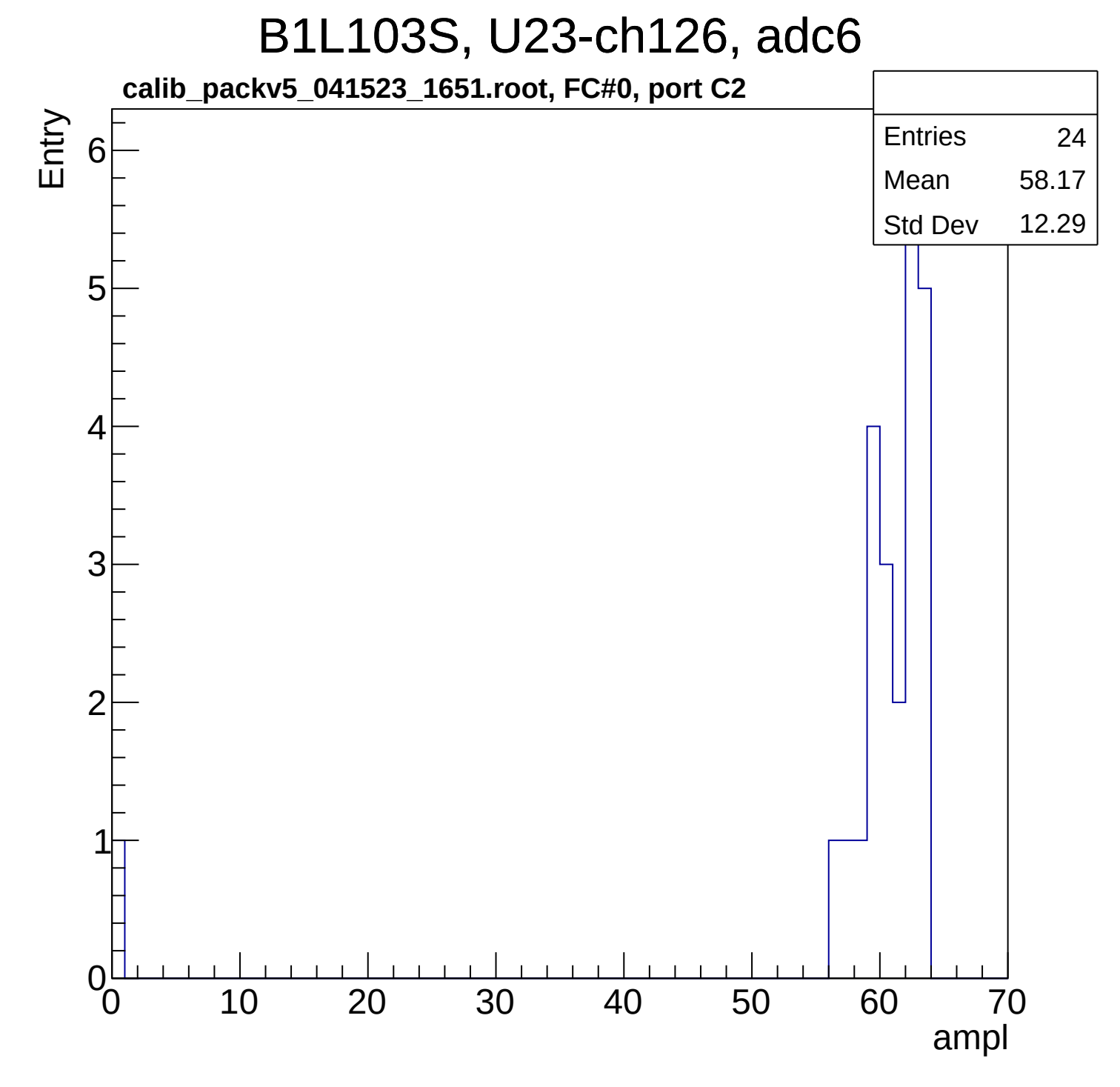

B1L103S, U23-ch126, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 79
Mean 25.84
Std Dev 13.09
Gaus mean : 32.8397
Gaus Width: 4.2909
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

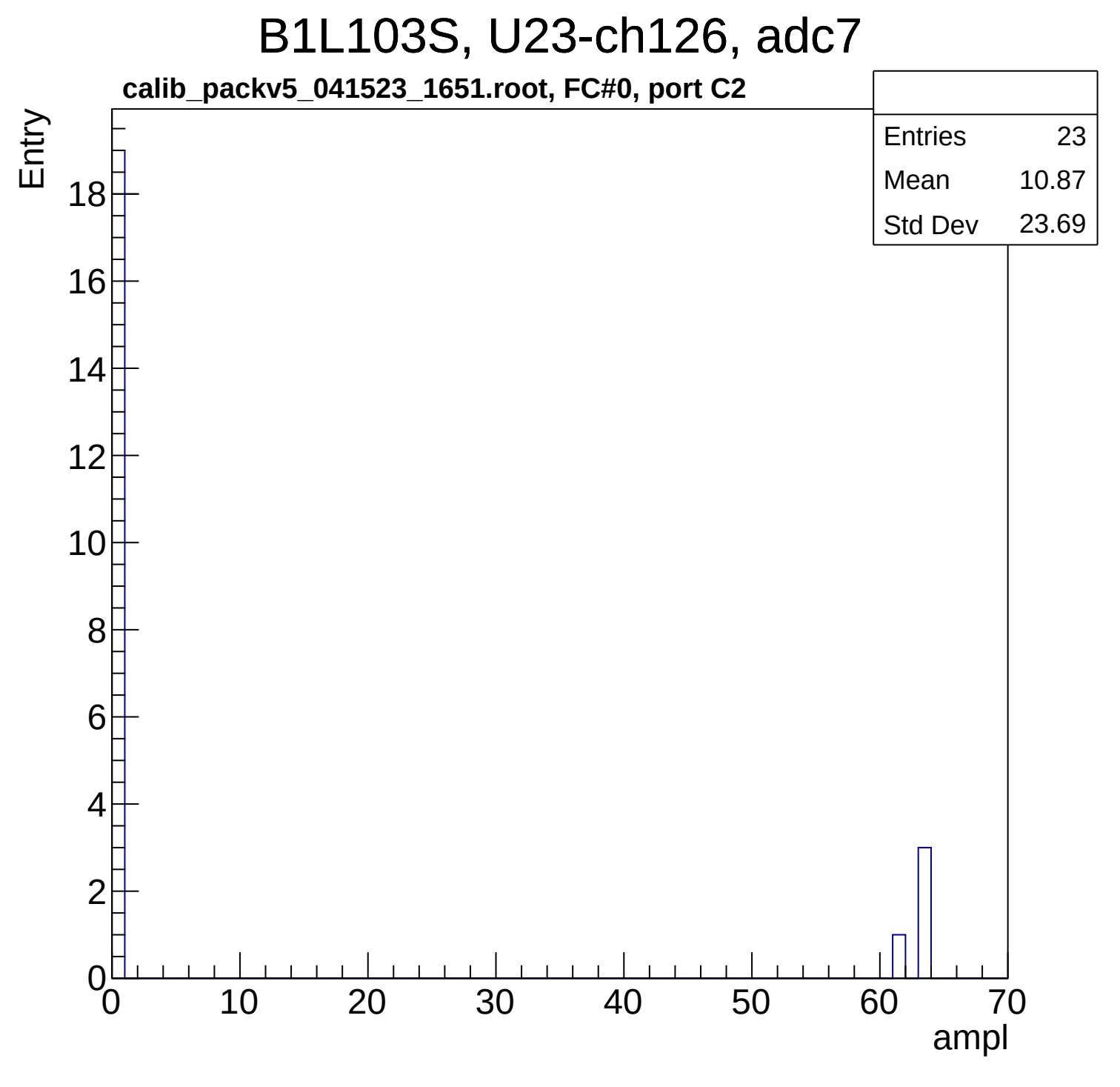

B1L103S, U23-ch126, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 75
Mean 39.59
Std Dev 4.007
Gaus mean : 40.0952
Gaus Width: 4.9163
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

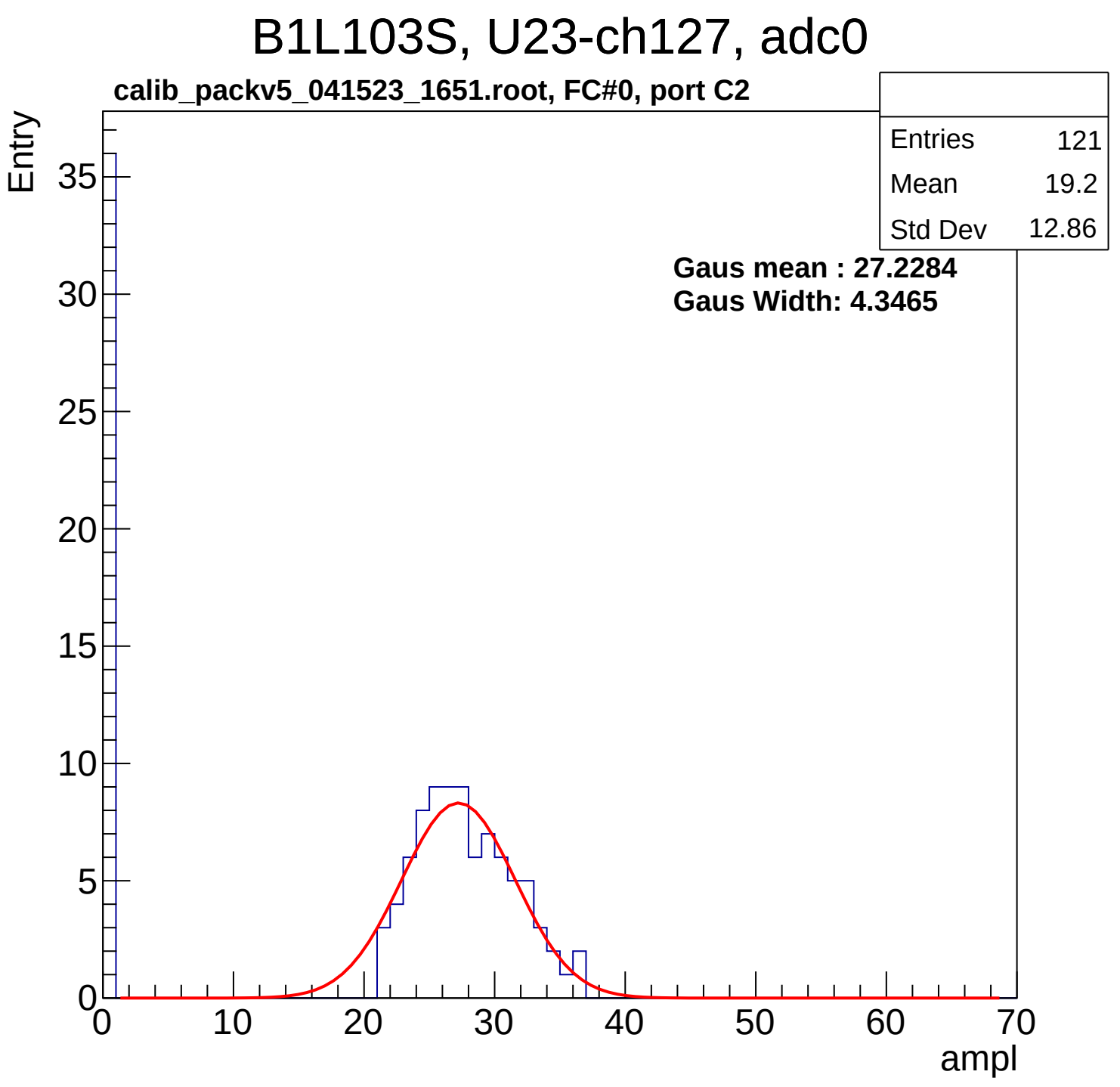

B1L103S, U23-ch126, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 63
Mean 46.65
Std Dev 4.636
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

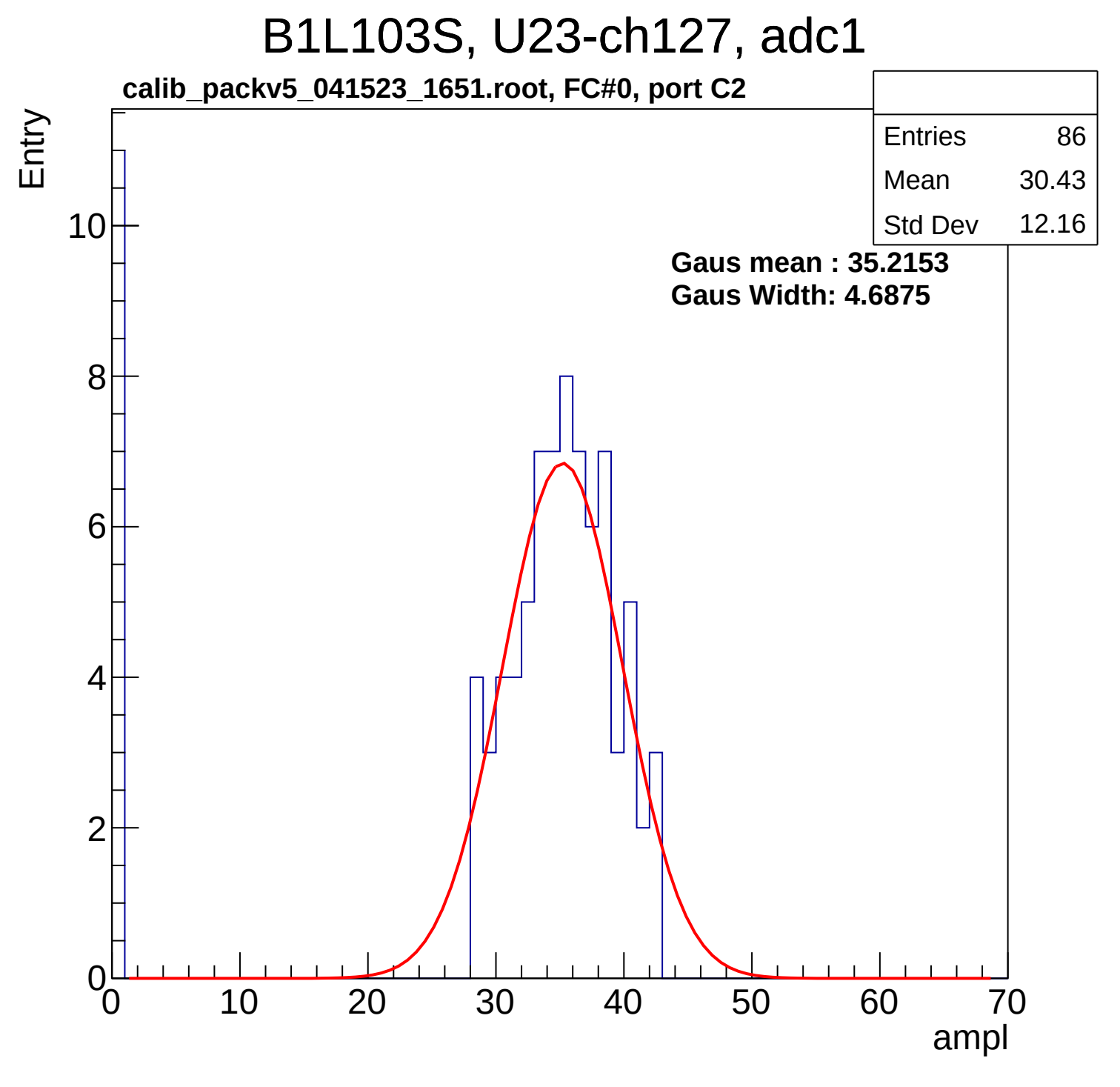

B1L103S, U23-ch126, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 67
Mean 51.79
Std Dev 4.283
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

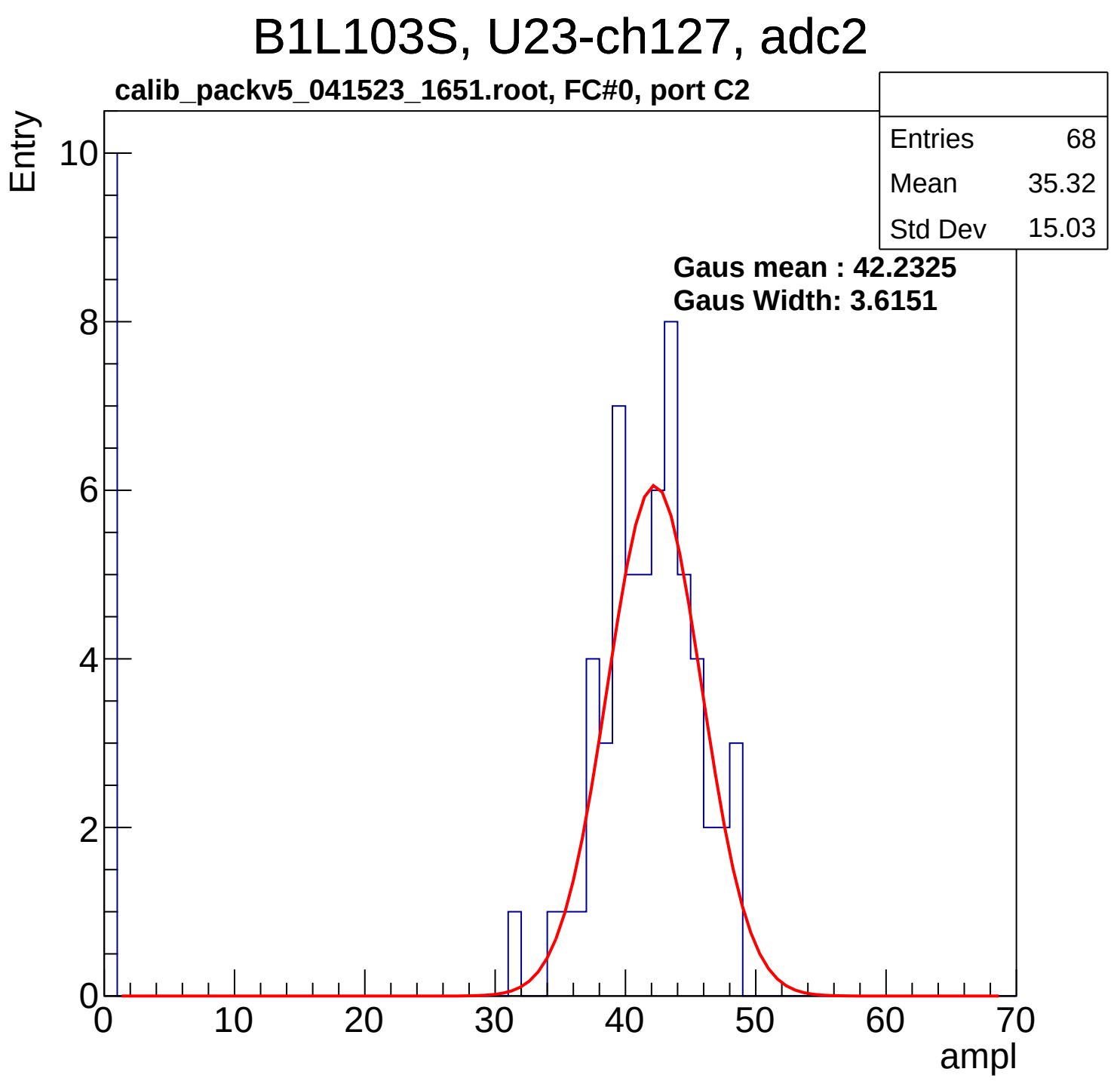

B1L103S, U23-ch126, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 48
Mean 57.98
Std Dev 3.467
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

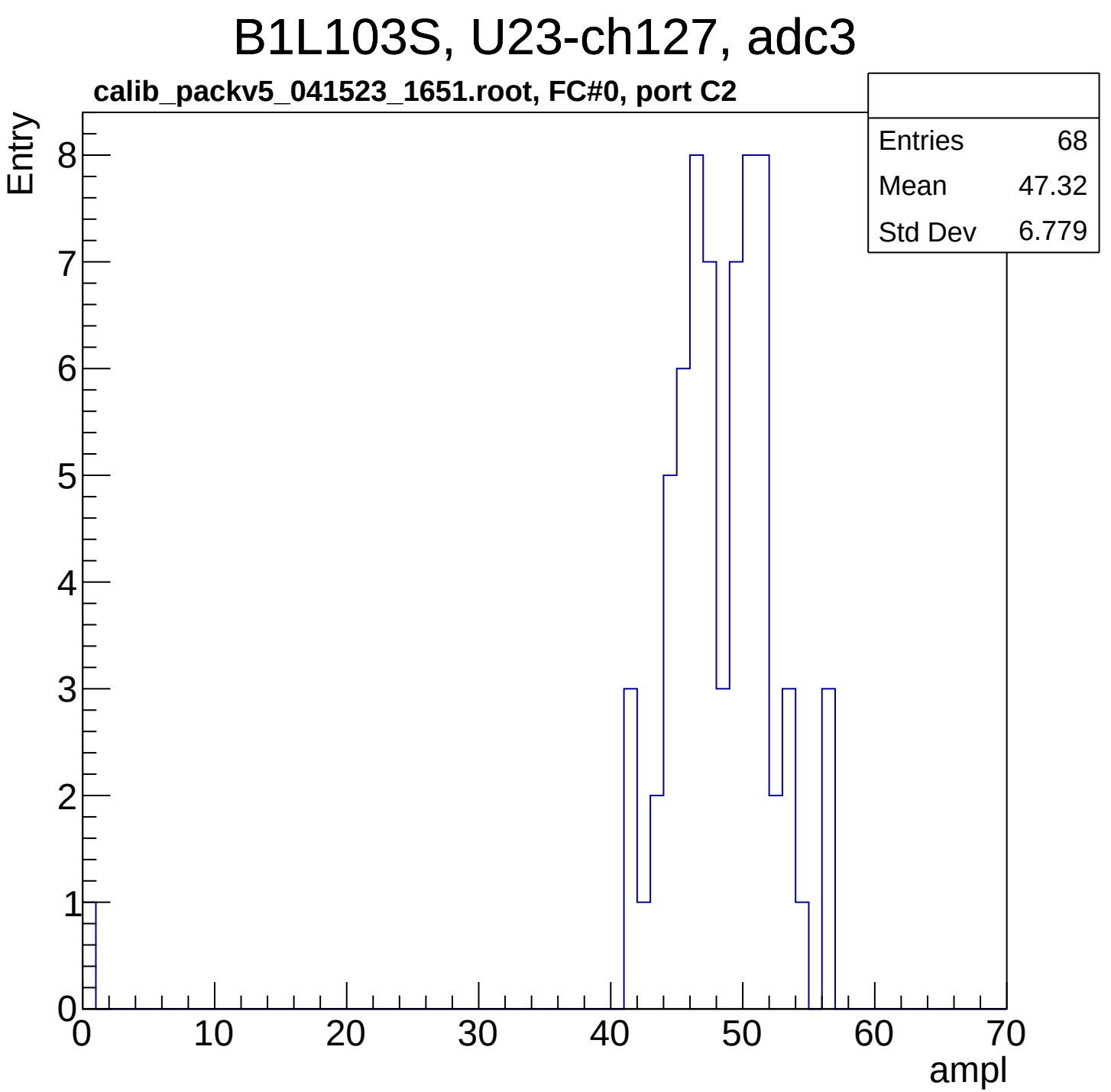

B1L103S, U23-ch126, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 24
Mean 58.17
Std Dev 12.29
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

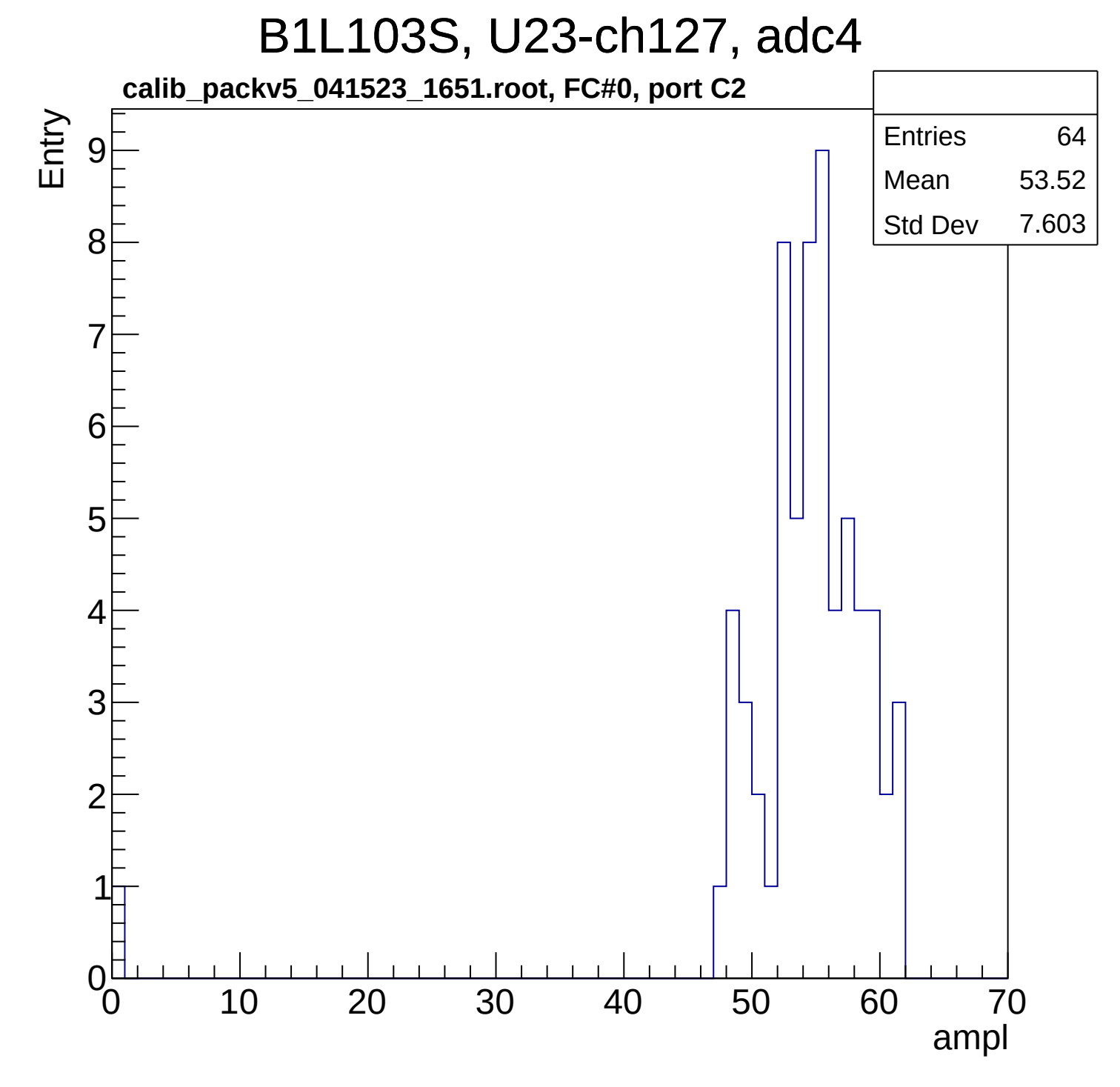

B1L103S, U23-ch126, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 23
Mean 10.87
Std Dev 23.69
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

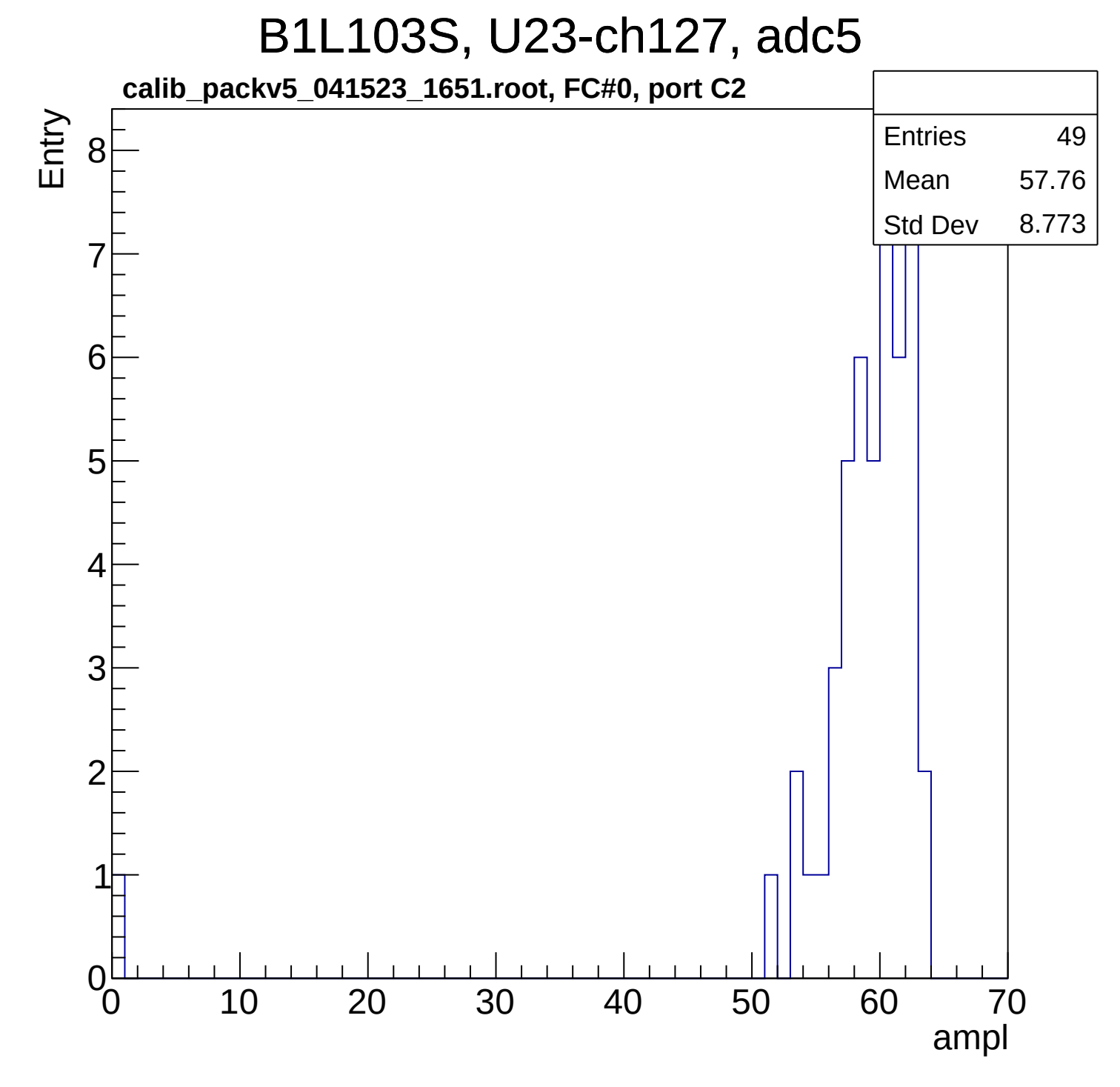

B1L103S, U23-ch127, adc0
calib_packv5_041523_1651.root, FC#0, port C2
Entries 121
Mean 19.2
Std Dev 12.86
Gaus mean : 27.2284
Gaus Width: 4.3465
Entry
ampl
0
5
10
15
20
25
30
35
0
10
20
30
40
50
60
70

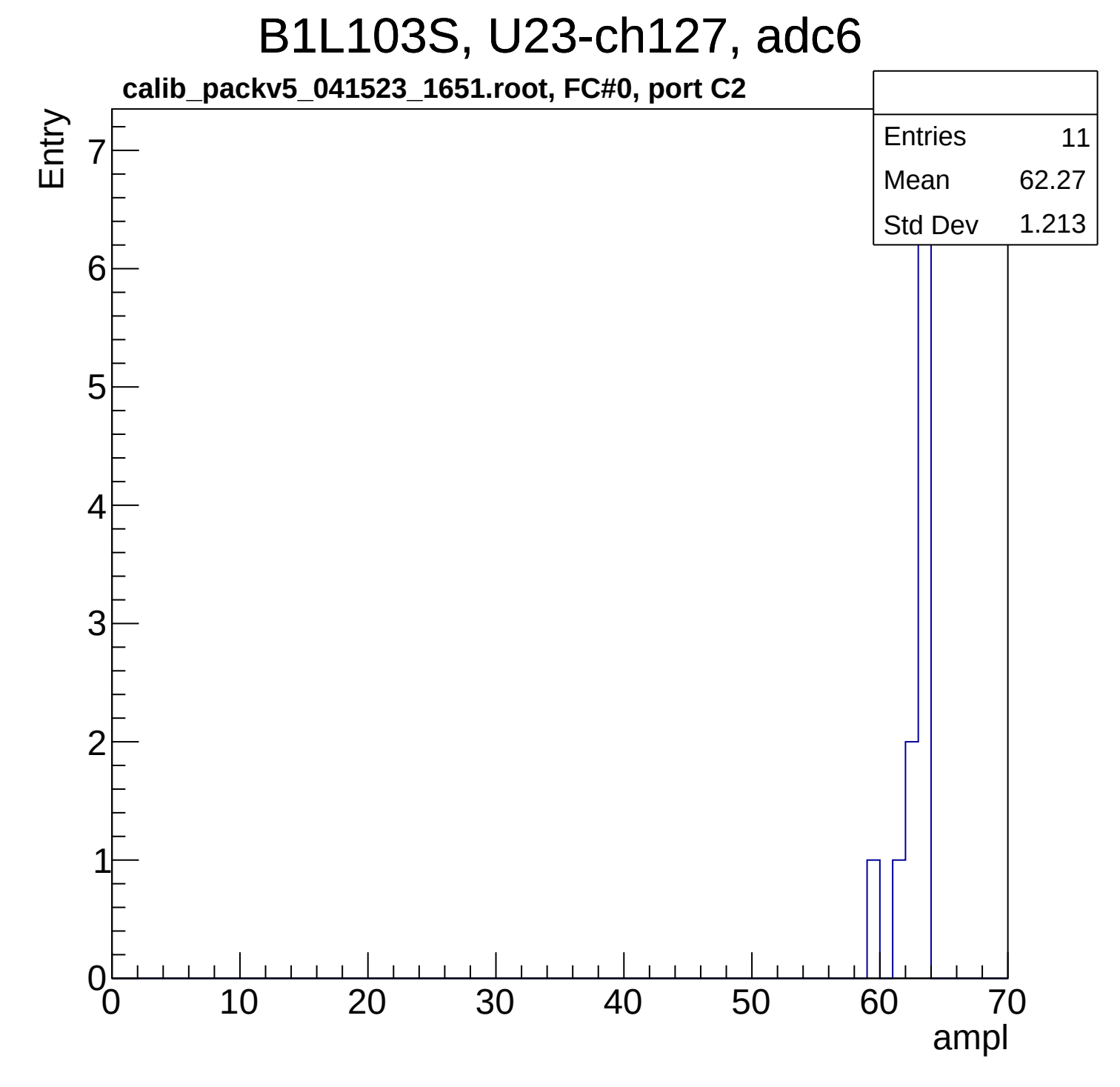

B1L103S, U23-ch127, adc1
calib_packv5_041523_1651.root, FC#0, port C2
Entries 86
Mean 30.43
Std Dev 12.16
Gaus mean : 35.2153
Gaus Width: 4.6875
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

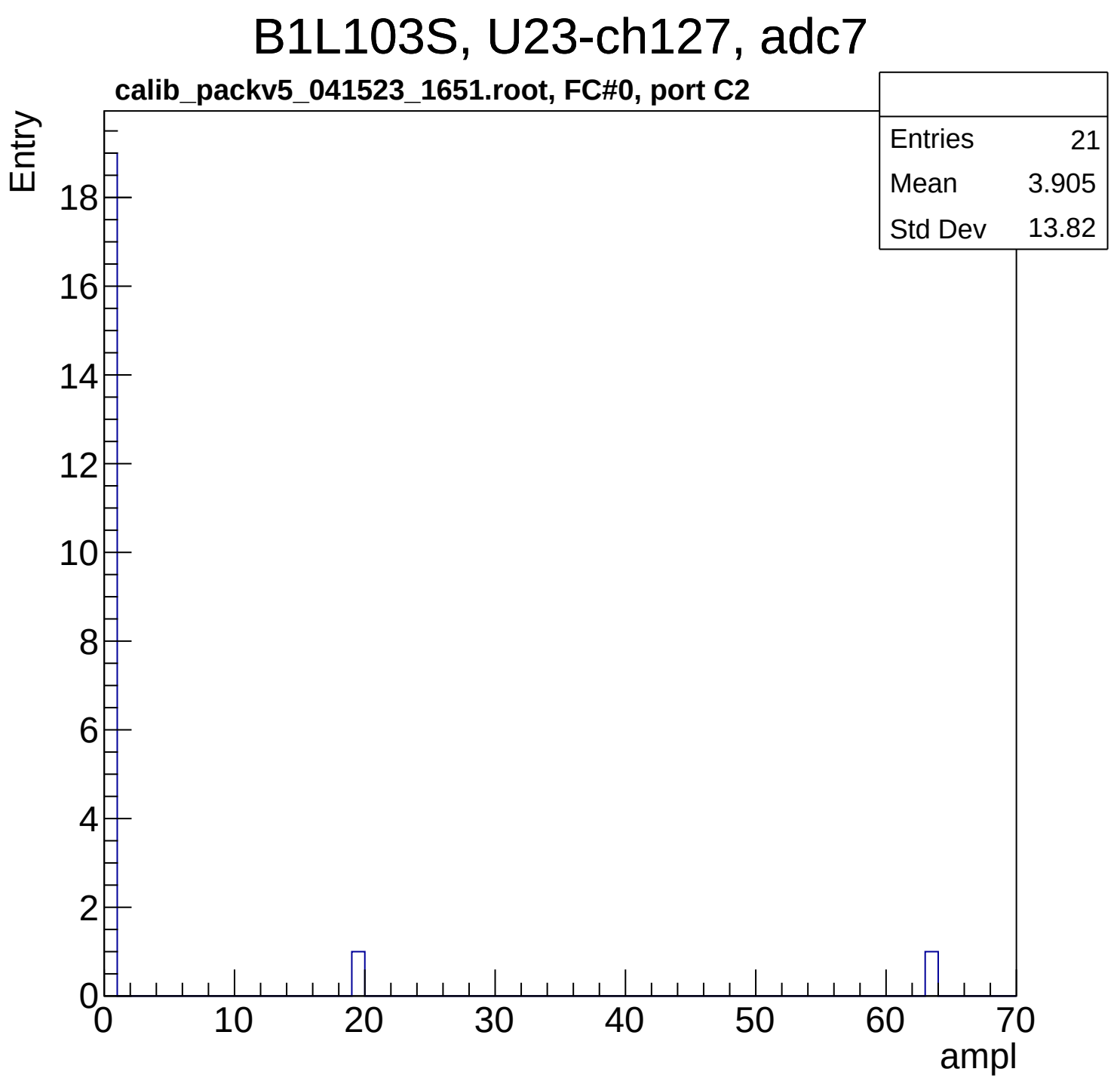

B1L103S, U23-ch127, adc2
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 35.32
Std Dev 15.03
Gaus mean : 42.2325
Gaus Width: 3.6151
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

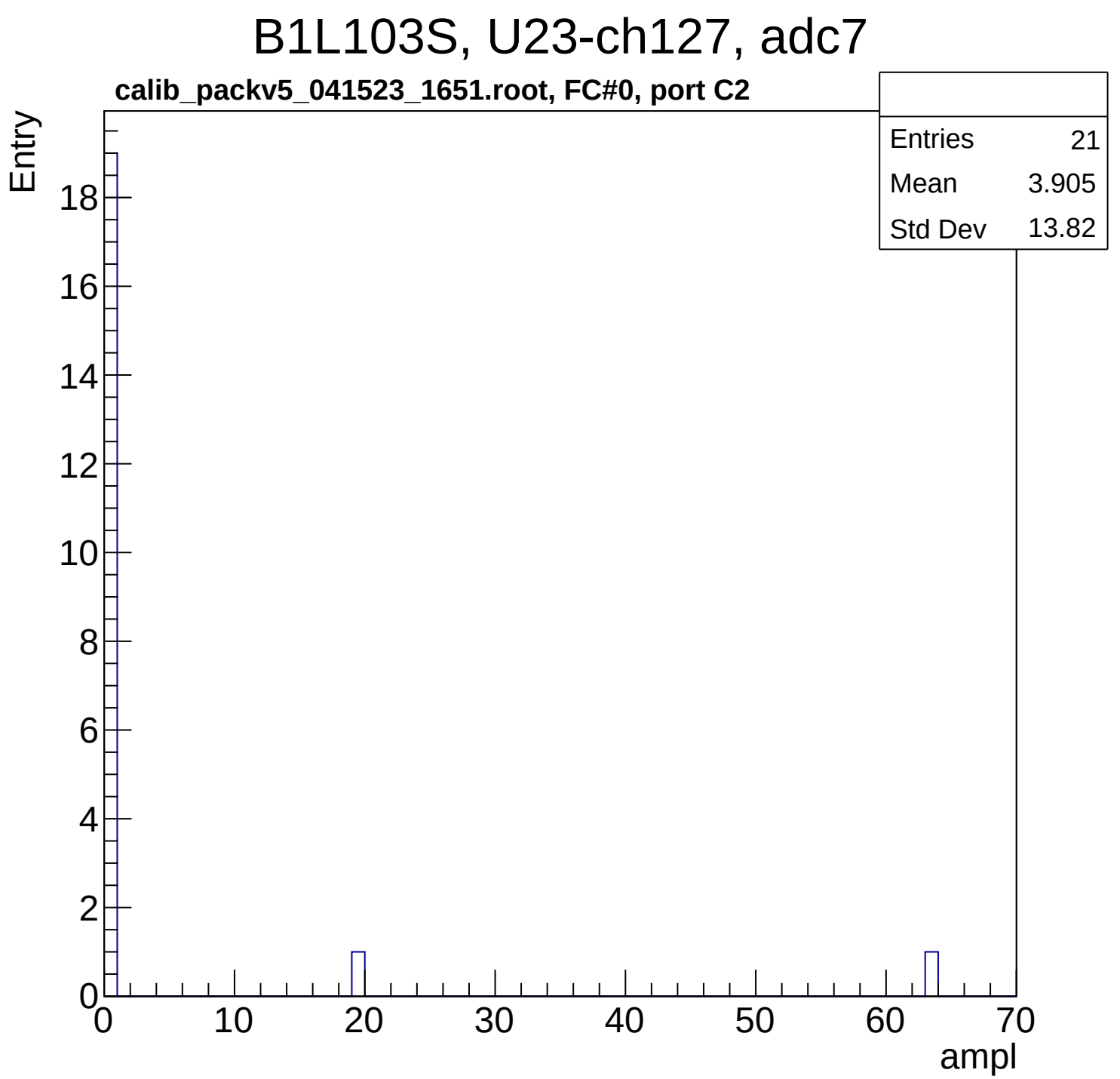

B1L103S, U23-ch127, adc3
calib_packv5_041523_1651.root, FC#0, port C2
Entries 68
Mean 47.32
Std Dev 6.779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U23-ch127, adc4
calib_packv5_041523_1651.root, FC#0, port C2
Entries 64
Mean 53.52
Std Dev 7.603
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U23-ch127, adc5
calib_packv5_041523_1651.root, FC#0, port C2
Entries 49
Mean 57.76
Std Dev 8.773
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U23-ch127, adc6
calib_packv5_041523_1651.root, FC#0, port C2
Entries 11
Mean 62.27
Std Dev 1.213
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U23-ch127, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 3.905
Std Dev 13.82
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L103S, U23-ch127, adc7
calib_packv5_041523_1651.root, FC#0, port C2
Entries 21
Mean 3.905
Std Dev 13.82
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl