# B0L100S, U1-ch0, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 882422 |
|---------|--------|
| Mean | 31.96 |
| Std Dev | 16.8 |

**Gaus mean : 36.0554**
**Gaus Width: 21.7972**

ampl
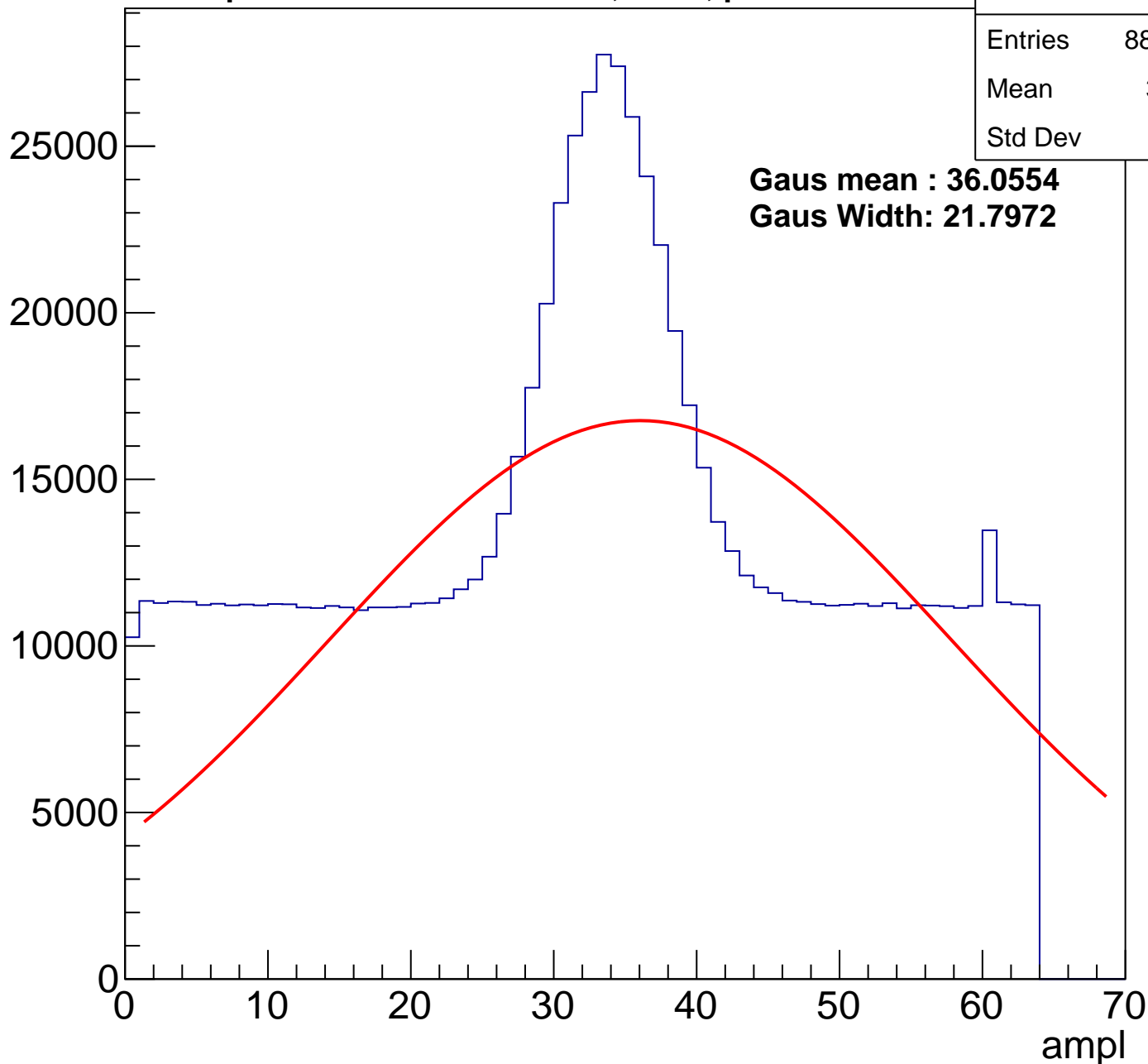
# B0L100S, U1-ch0, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 221853 |
| Mean | 38.33 |
| Std Dev | 10.24 |

**Gaus mean : 40.6374**
**Gaus Width: 4.8818**

ampl
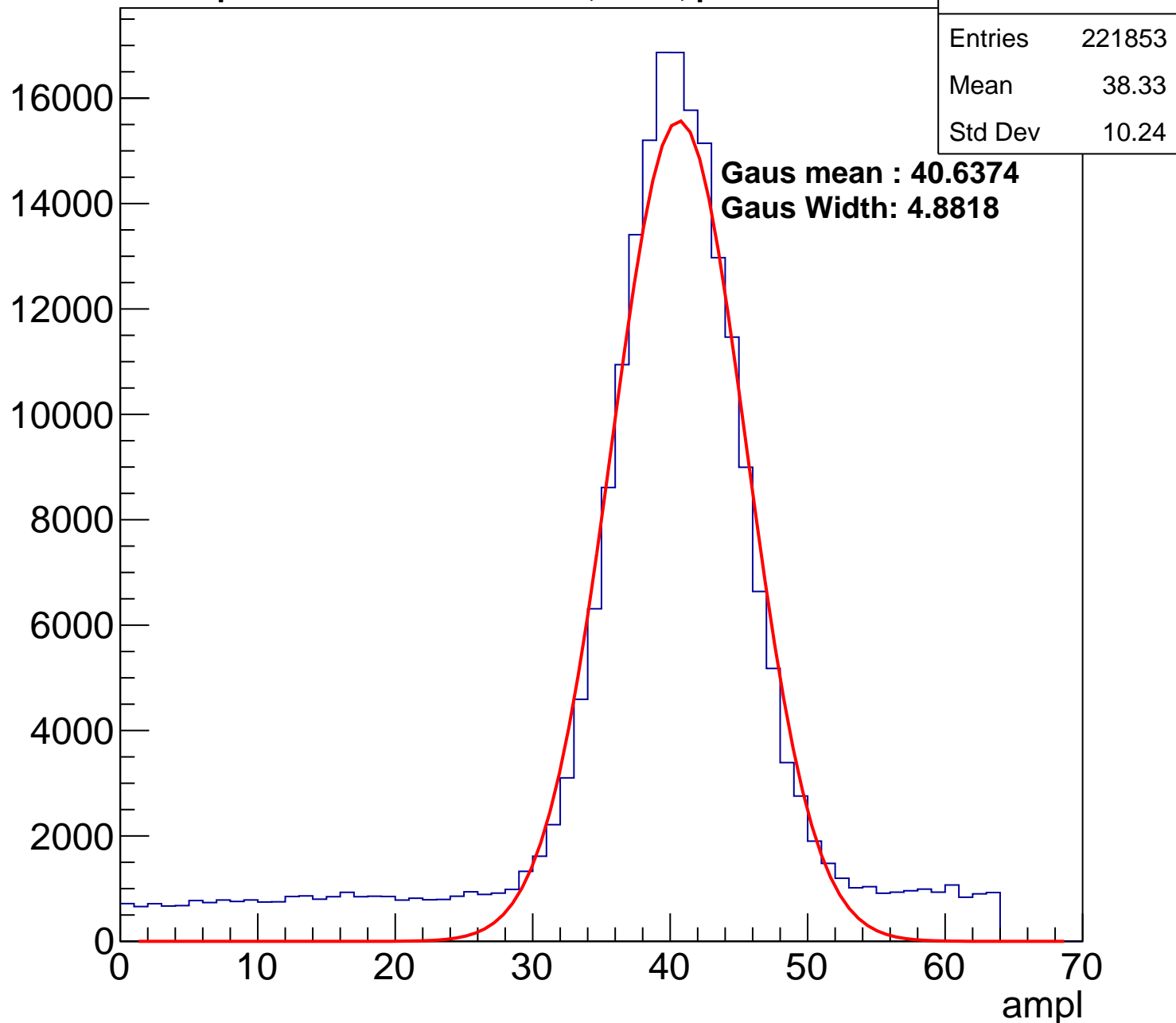
# B0L100S, U1-ch0, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 153002 |
| Mean | 47.04 |
| Std Dev | 5.301 |

**Gaus mean : 47.7443**
**Gaus Width: 4.3253**

ampl

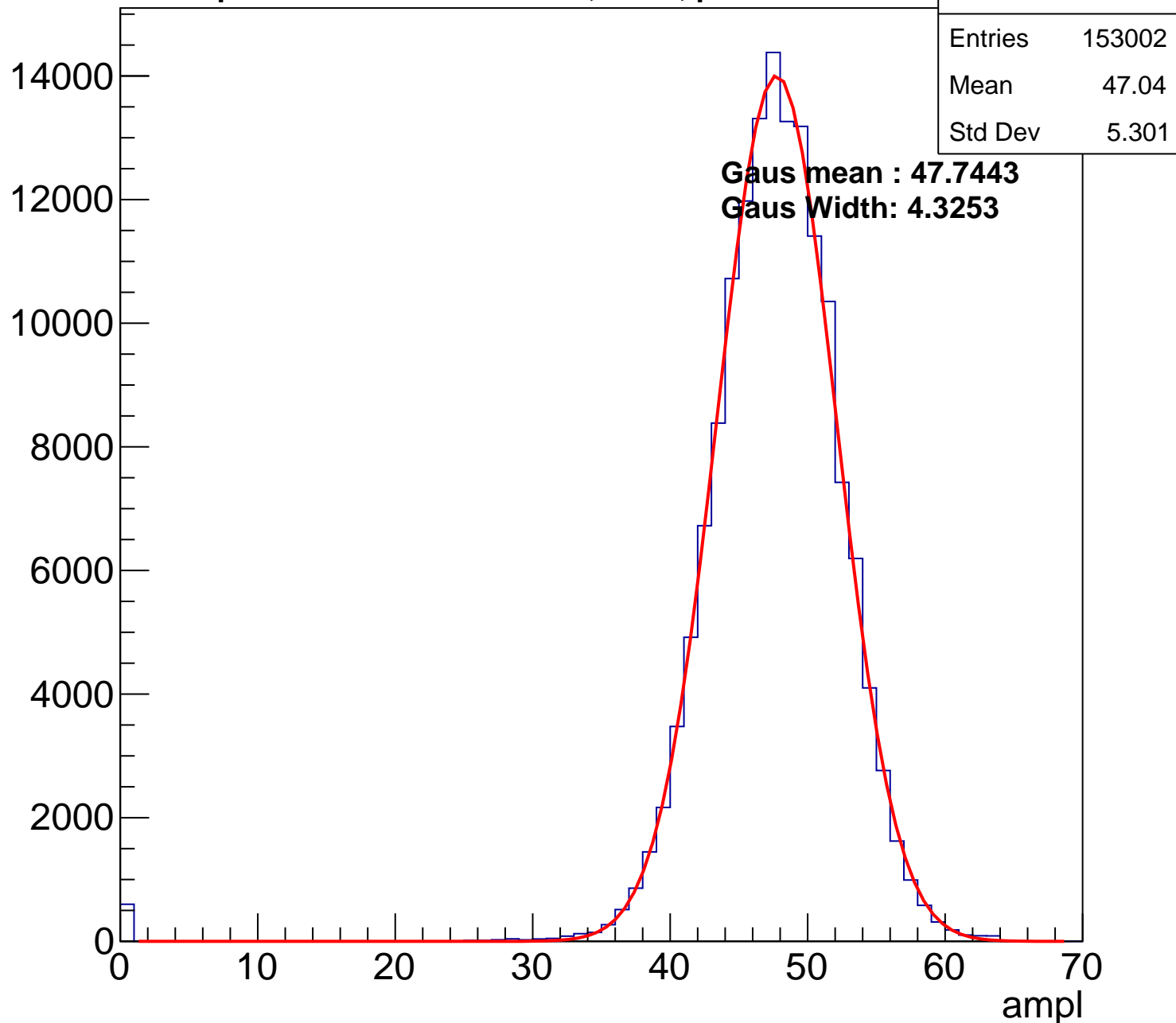B0L100S, U1-ch0, adc3

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 157458 |
| Mean | 53.95 |
| Std Dev | 4.297 |

ampl
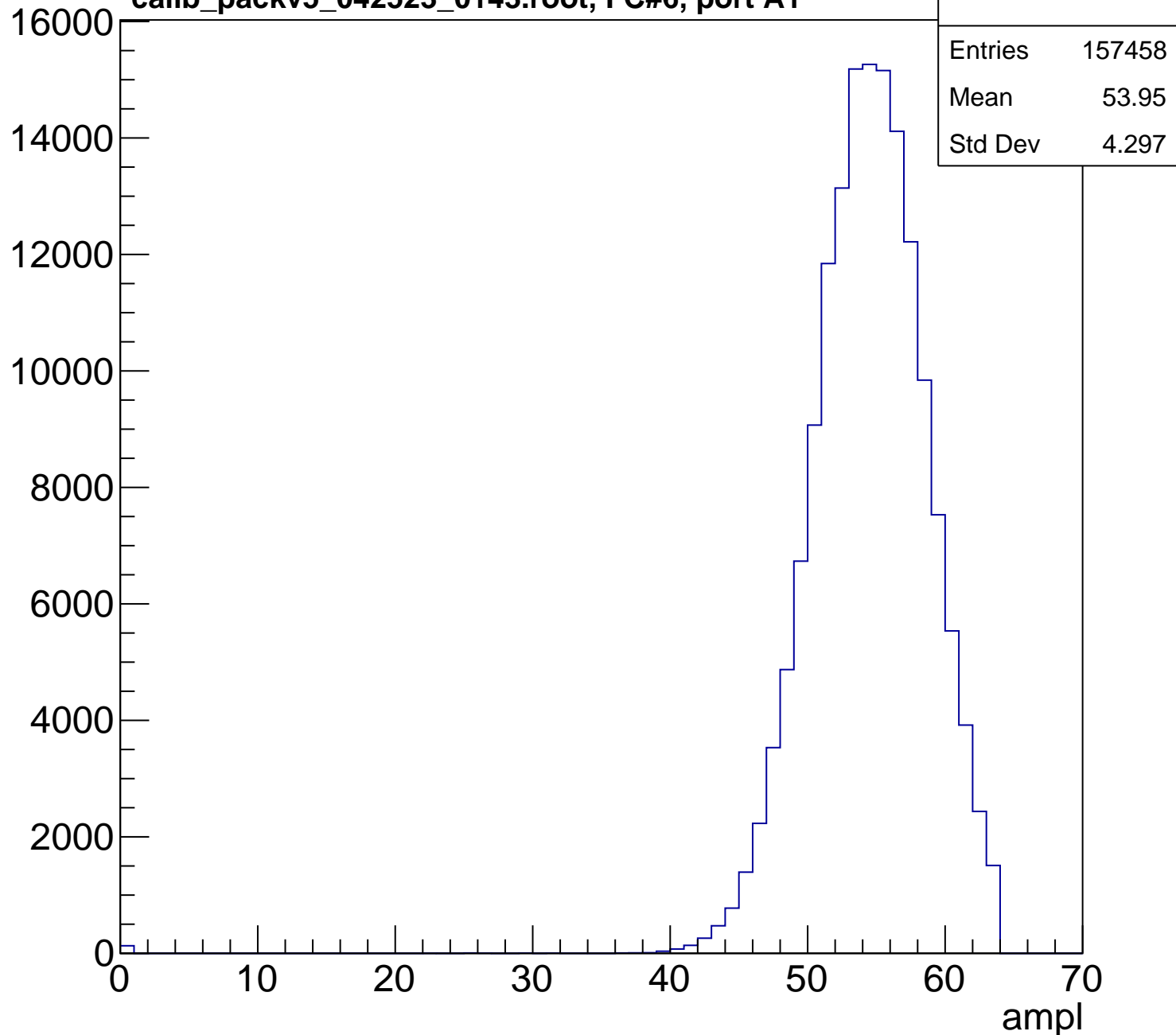
# B0L100S, U1-ch0, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 116339 |
|---------|--------|
| Mean | 58.13 |
| Std Dev | 6.806 |
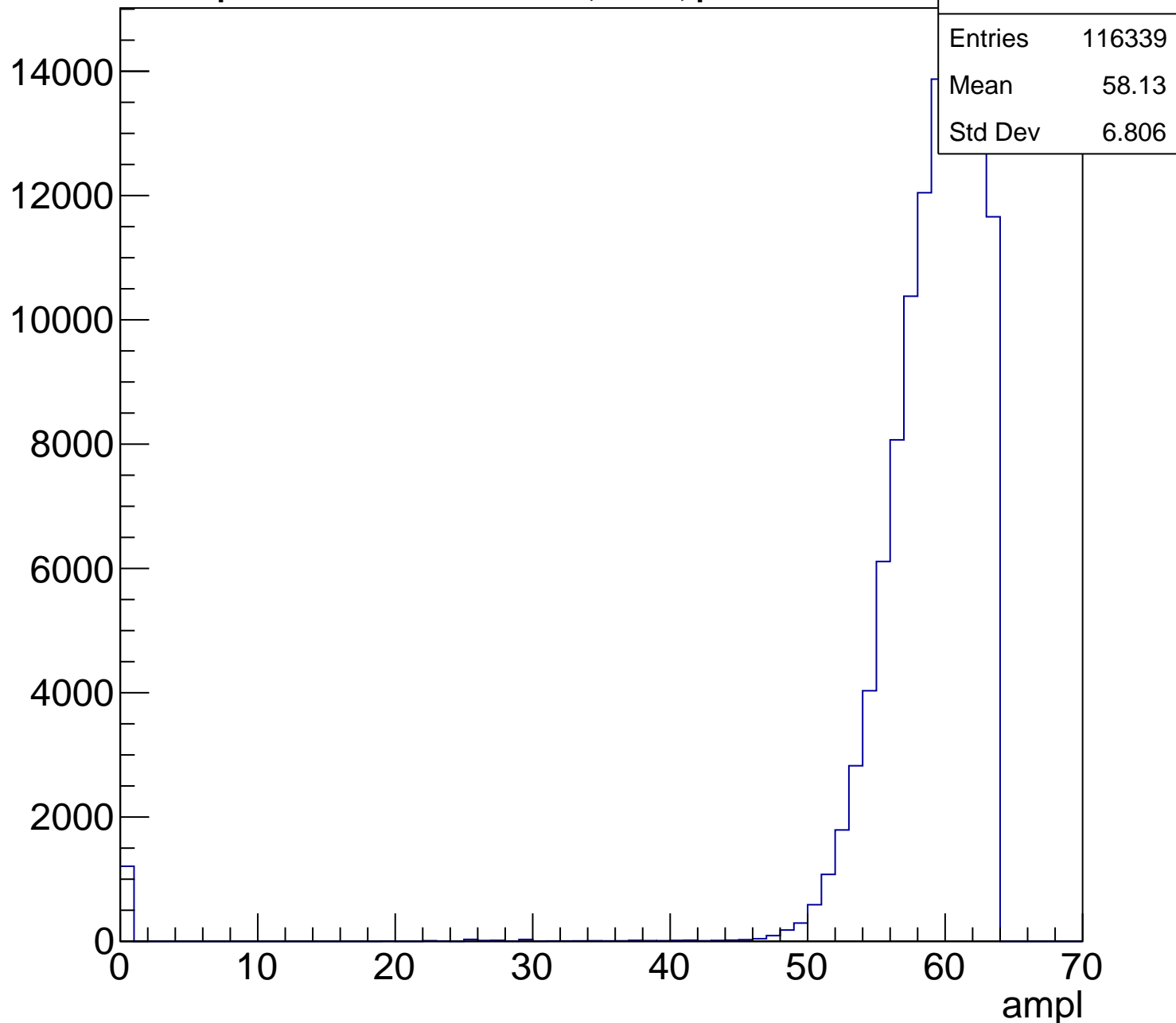
ampl

# B0L100S, U1-ch0, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 37533 |
| Mean | 59.17 |
| Std Dev | 10.51 |
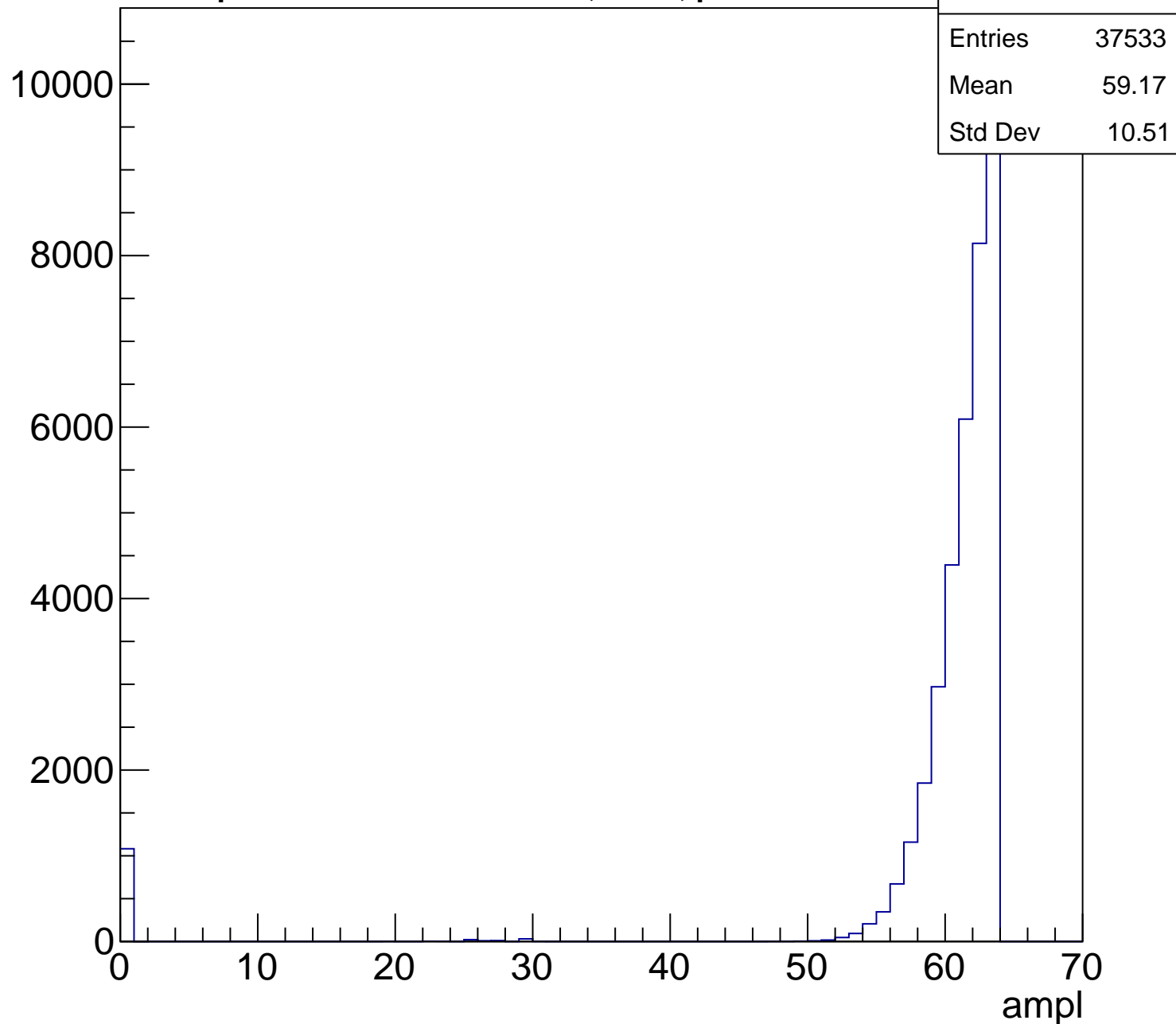
ampl

# B0L100S, U1-ch0, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1792 |
| Mean | 59.31 |
| Std Dev | 12.34 |
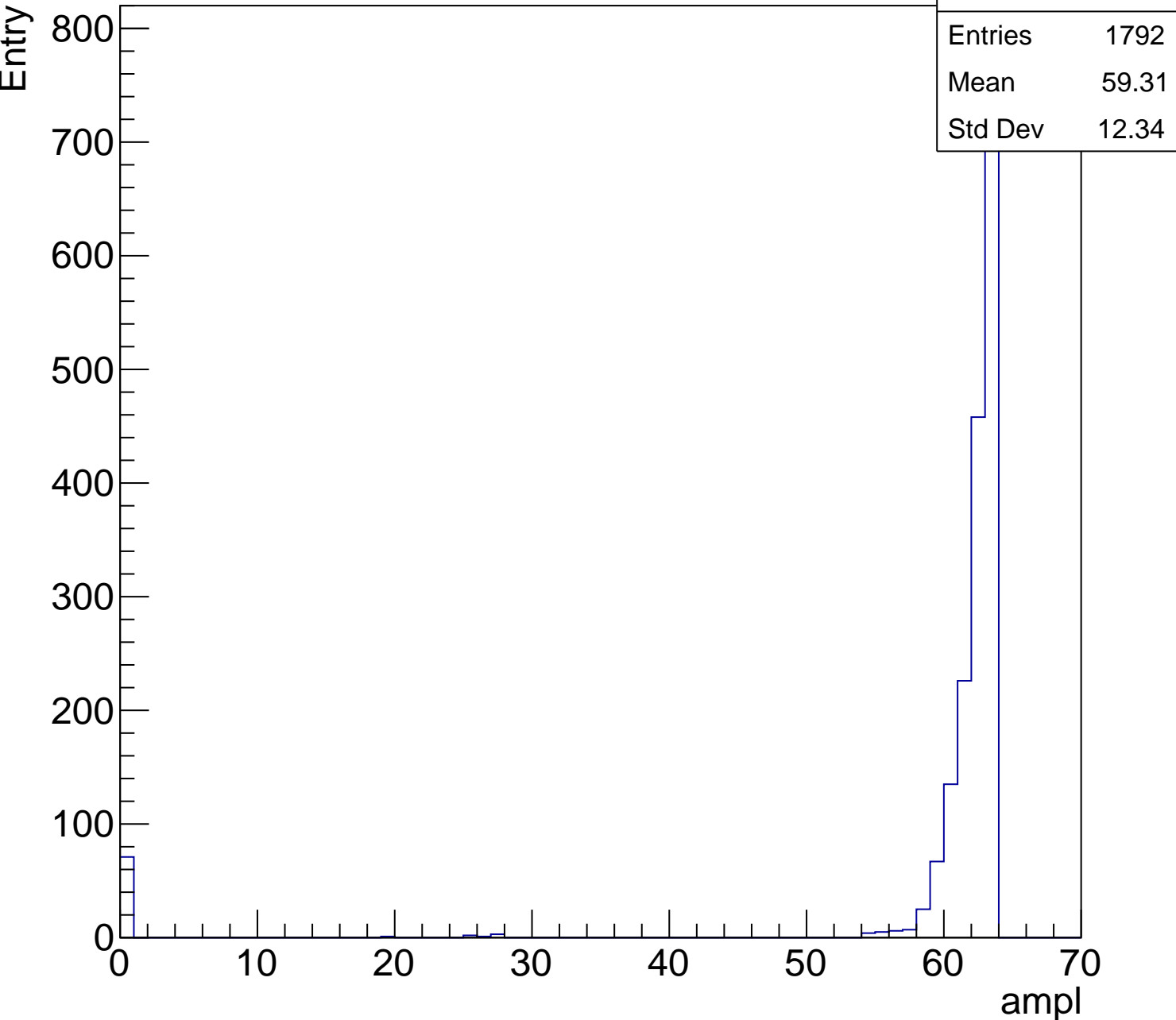
# B0L100S, U1-ch0, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1935 |
|---------|------|
| Mean | 4.447 |
| Std Dev | 12.25 |

ampl
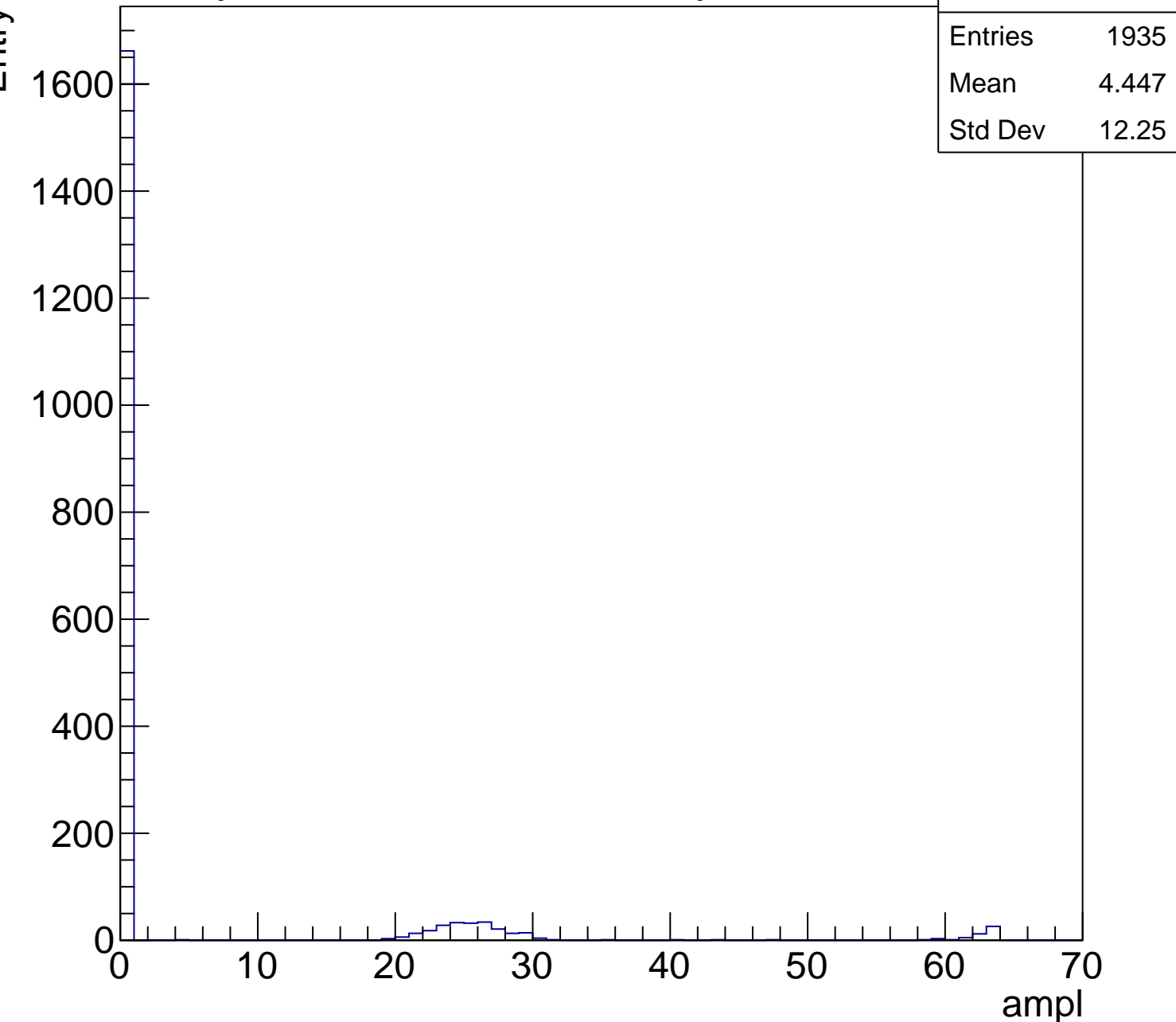
# B0L100S, U1-ch1, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch1, adc1

calib_packv5_042523_0143.root, FC#6, port A1
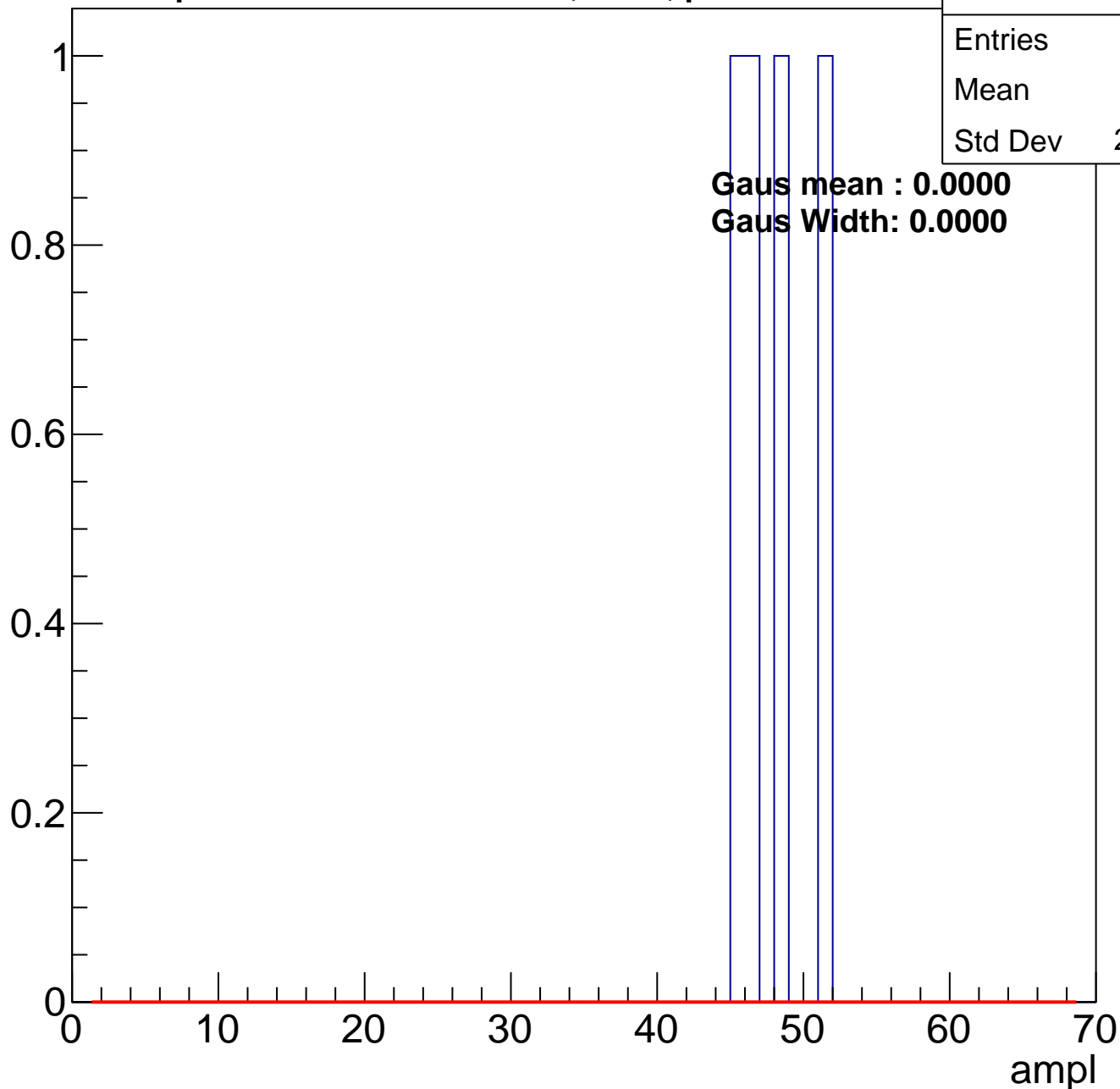
| Entries | 4 |
| Mean | 47.5 |
| Std Dev | 2.291 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

# B0L100S, U1-ch1, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 9 |
| Mean | 47.22 |
| Std Dev | 2.897 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
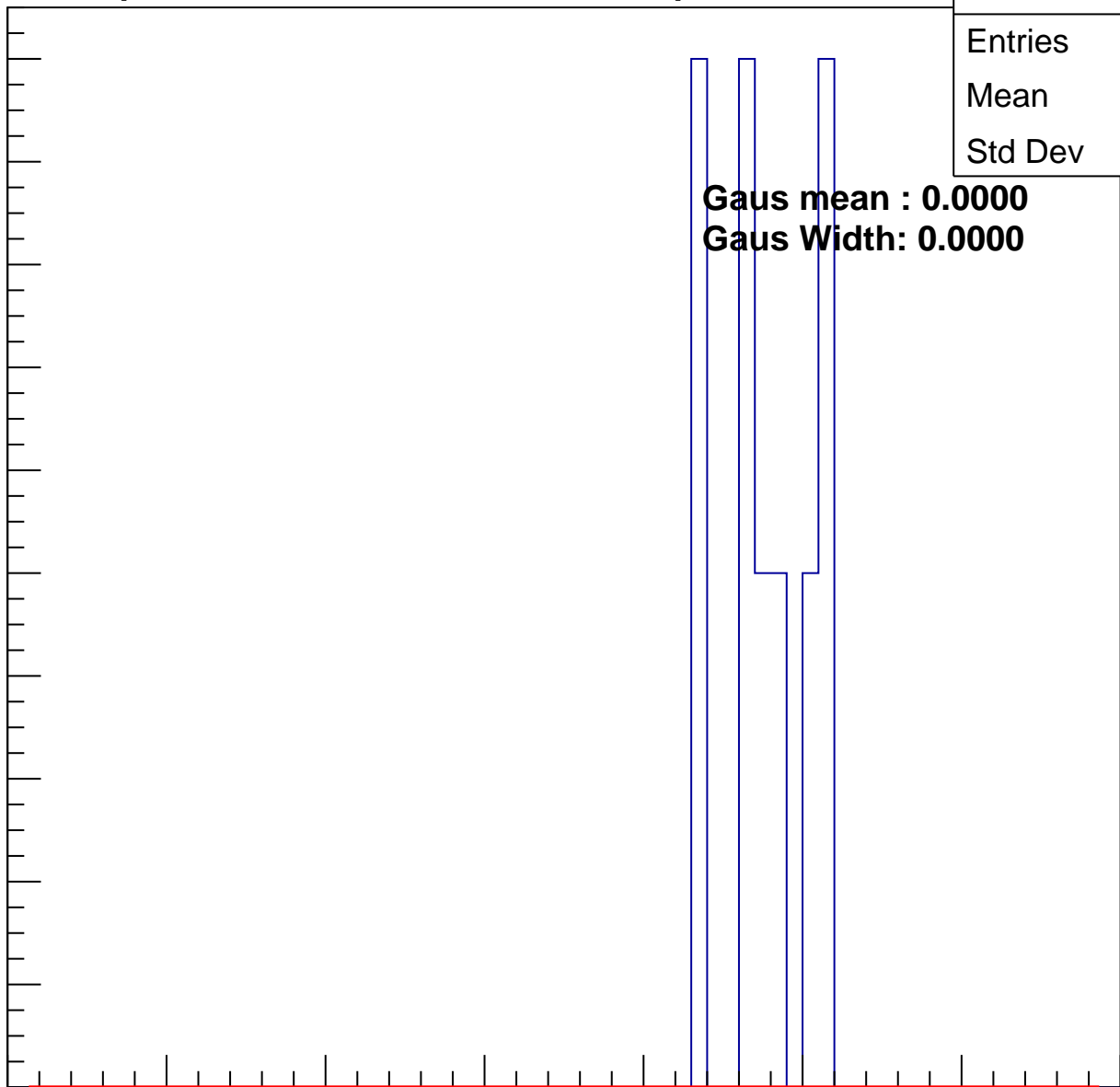
# B0L100S, U1-ch1, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 6 |
|---------|---|
| Mean | 50 |
| Std Dev | 2.646 |

Entry

ampl
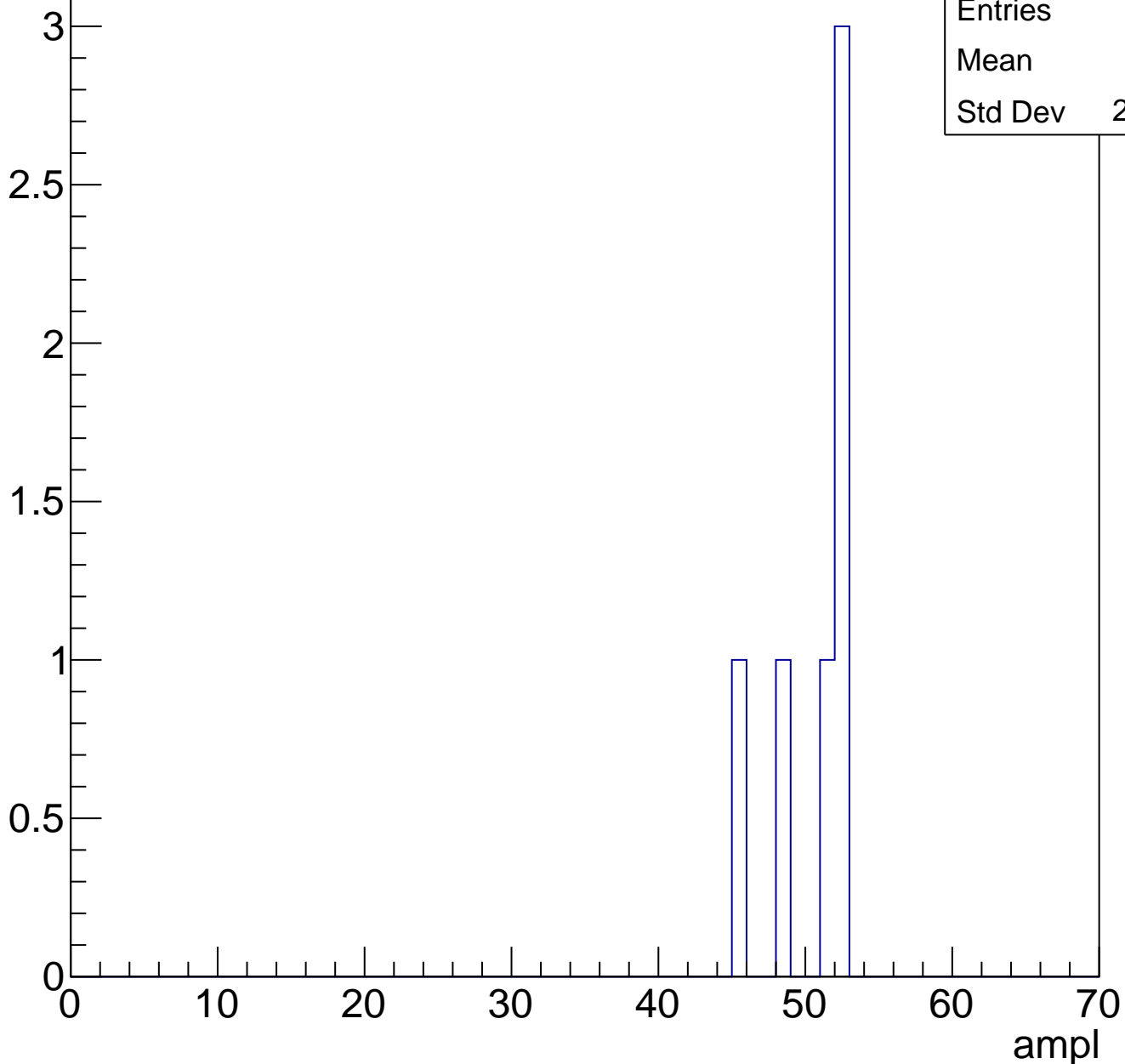
# B0L100S, U1-ch1, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch1, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch1, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch1, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch2, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 10 |
| Mean | 27.1 |
| Std Dev | 13.57 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
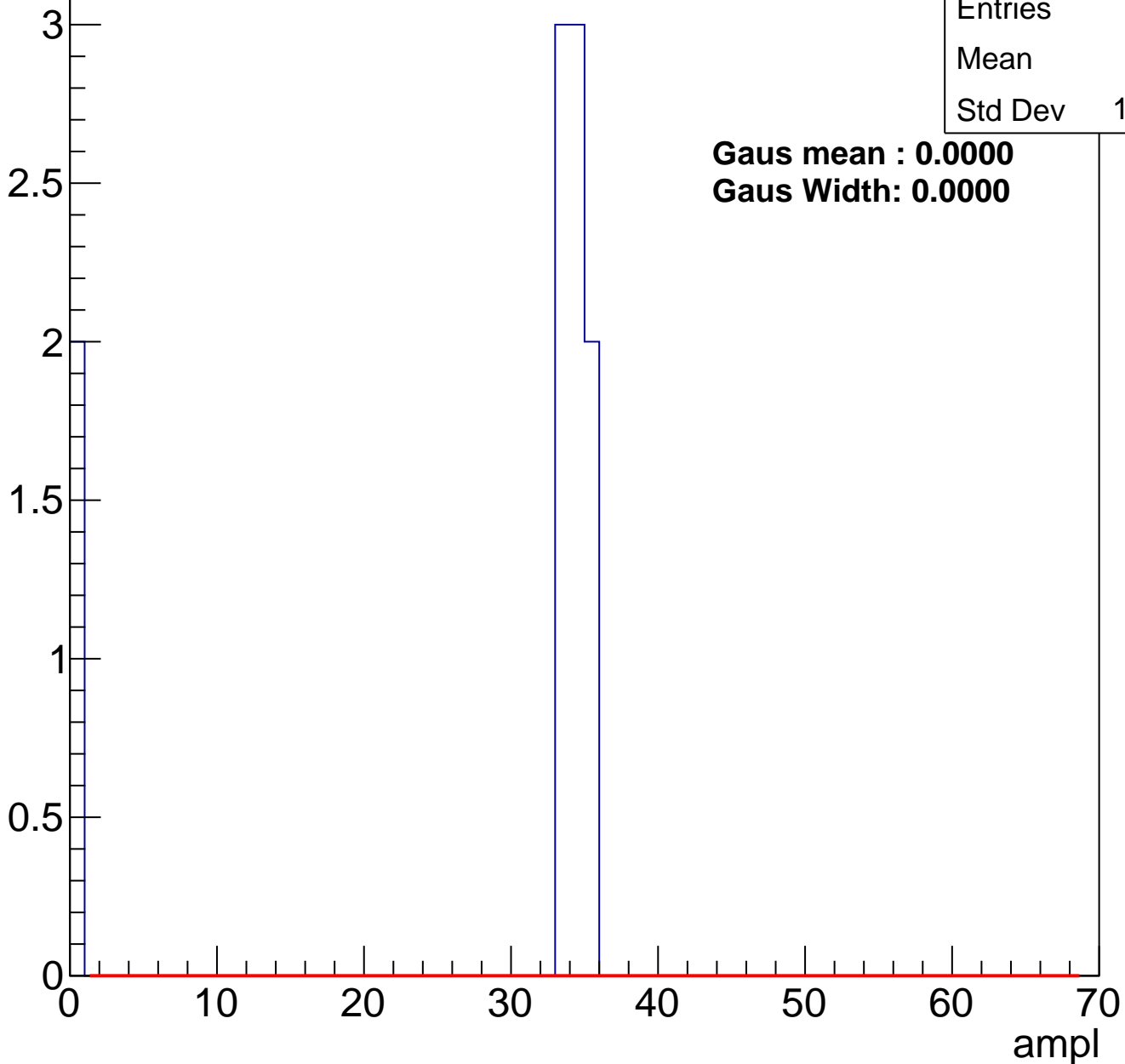
Entry

ampl

# B0L100S, U1-ch2, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 4 |
| Mean | 43 |
| Std Dev | 4.637 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch2, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 4 |
| Mean | 49.5 |
| Std Dev | 2.062 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
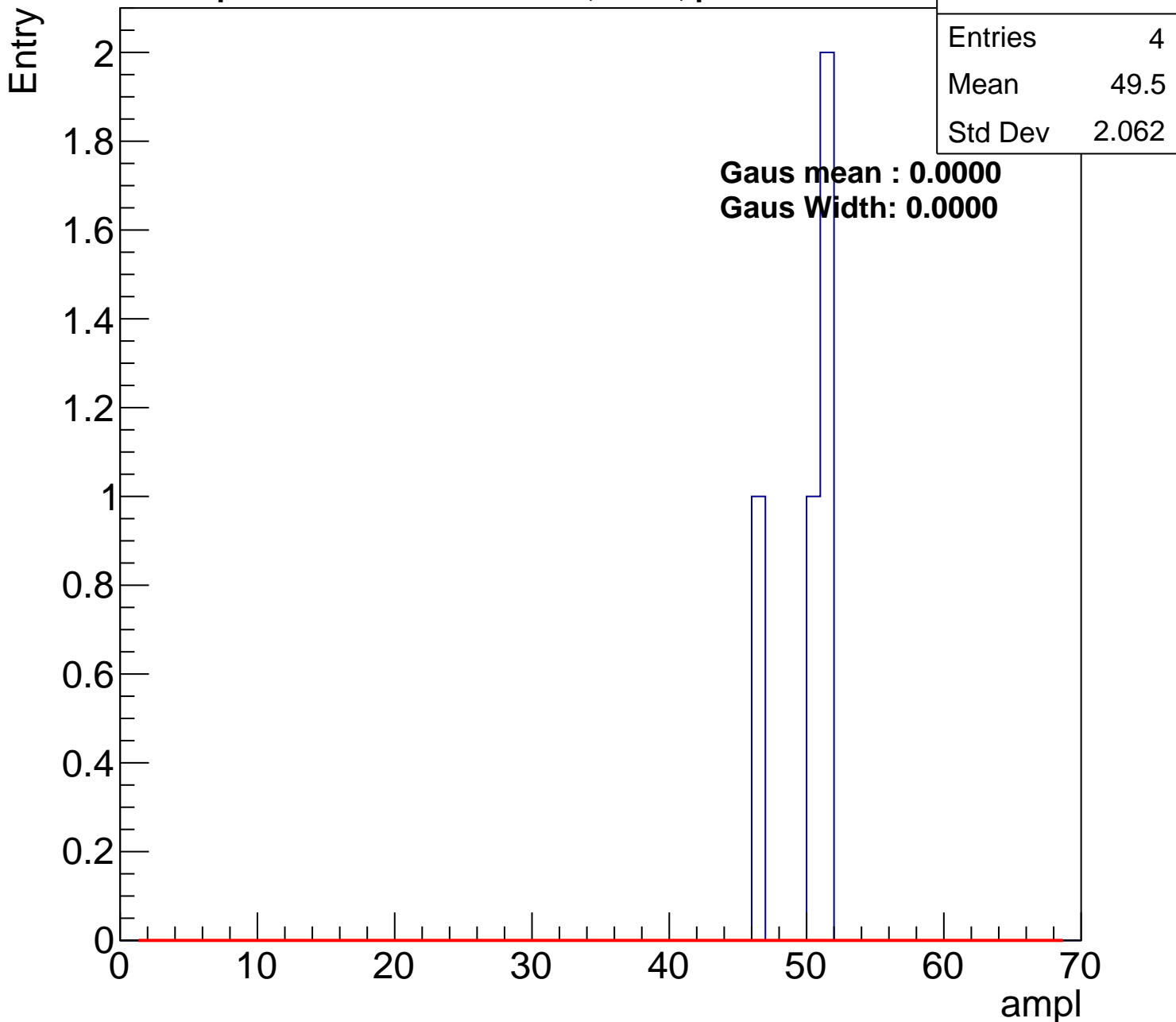
# B0L100S, U1-ch2, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch2, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch2, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch2, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch2, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 1 |
| Mean | 32 |
| Std Dev | 0 |

Entry

ampl
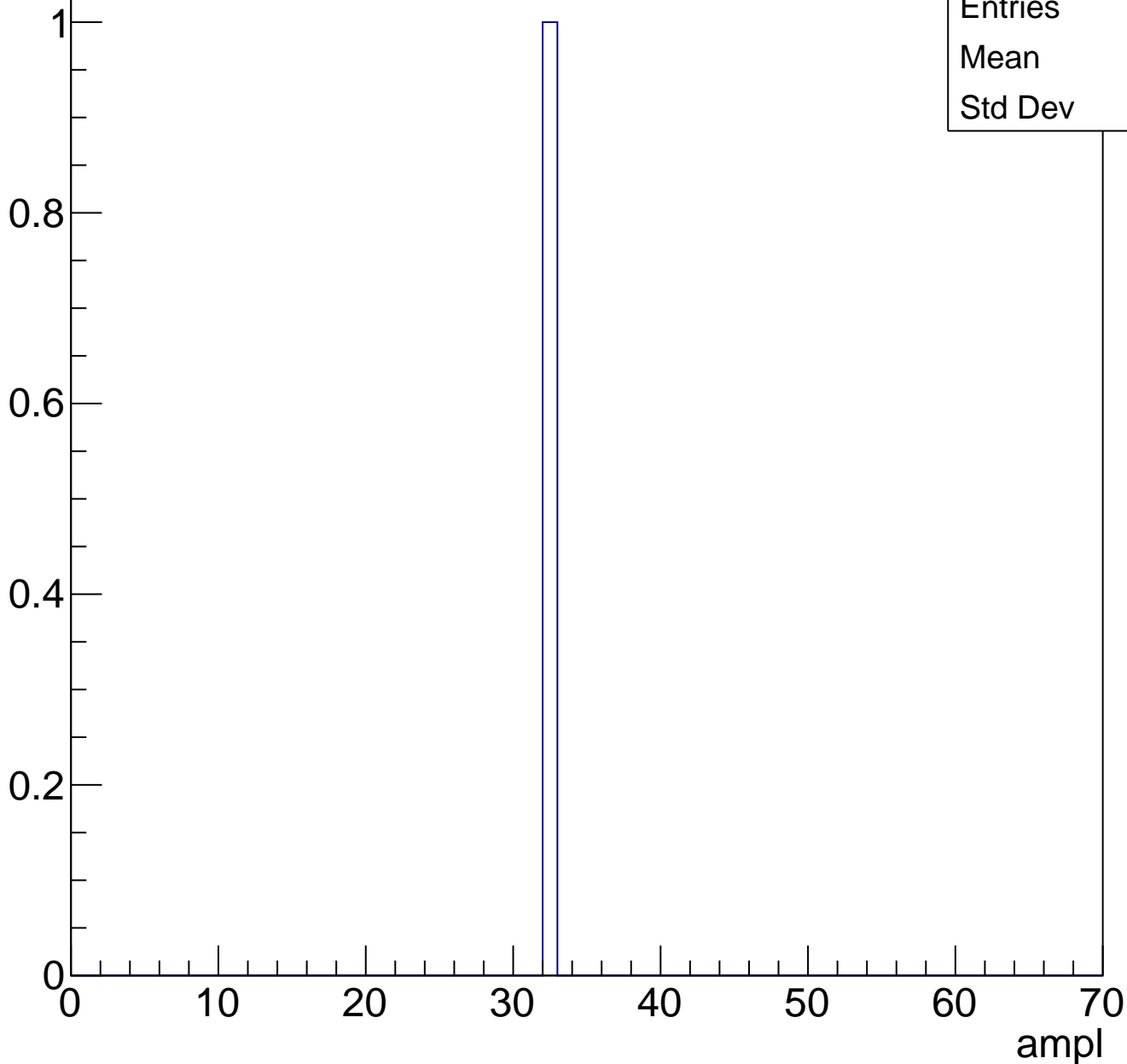
# B0L100S, U1-ch3, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch3, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 45 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch3, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 5 |
| Mean | 46 |
| Std Dev | 1.673 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
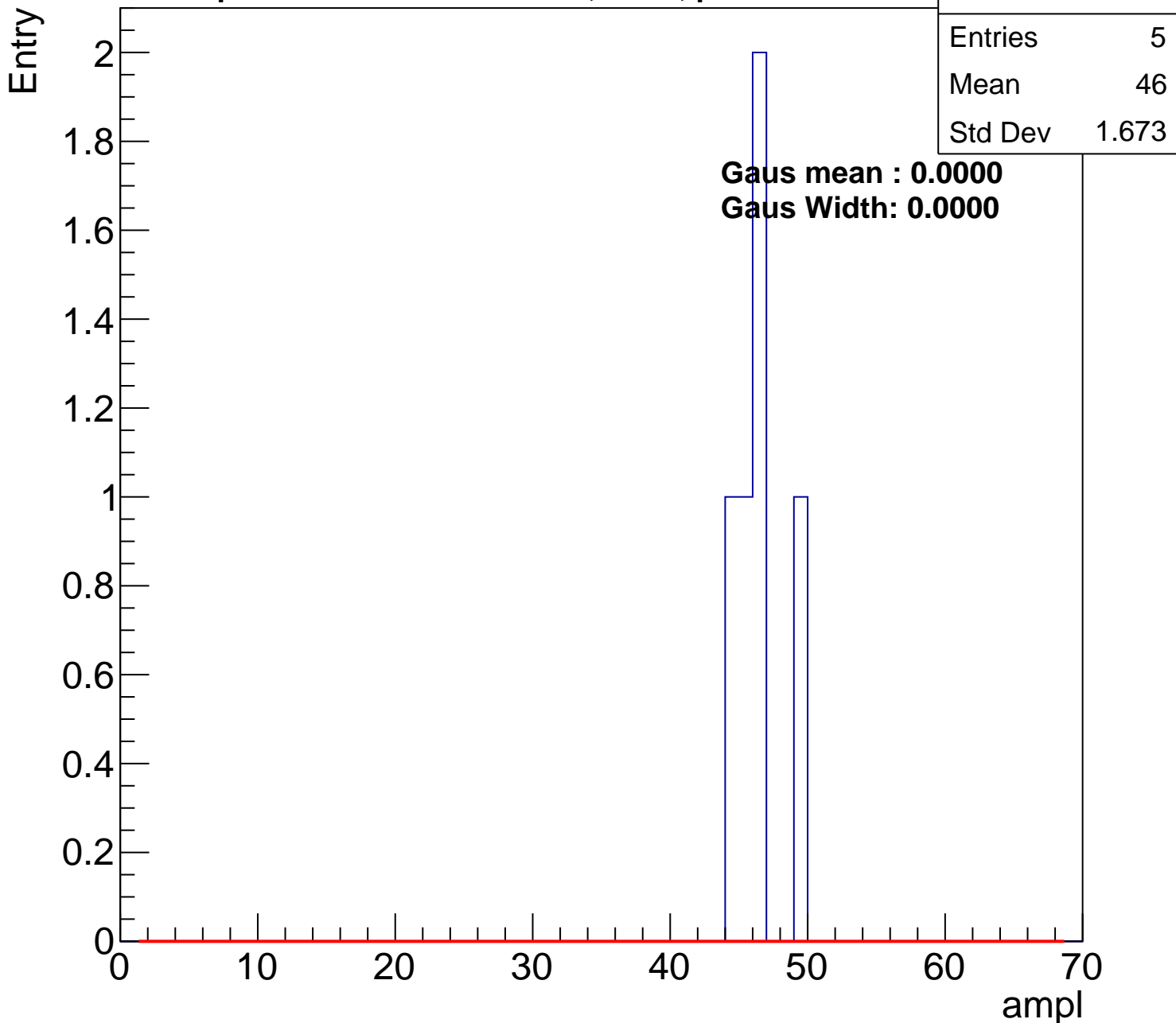
# B0L100S, U1-ch3, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch3, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch3, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch3, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch3, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch4, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**



Gaus mean : 0.0000
Gaus Width: 0.0000
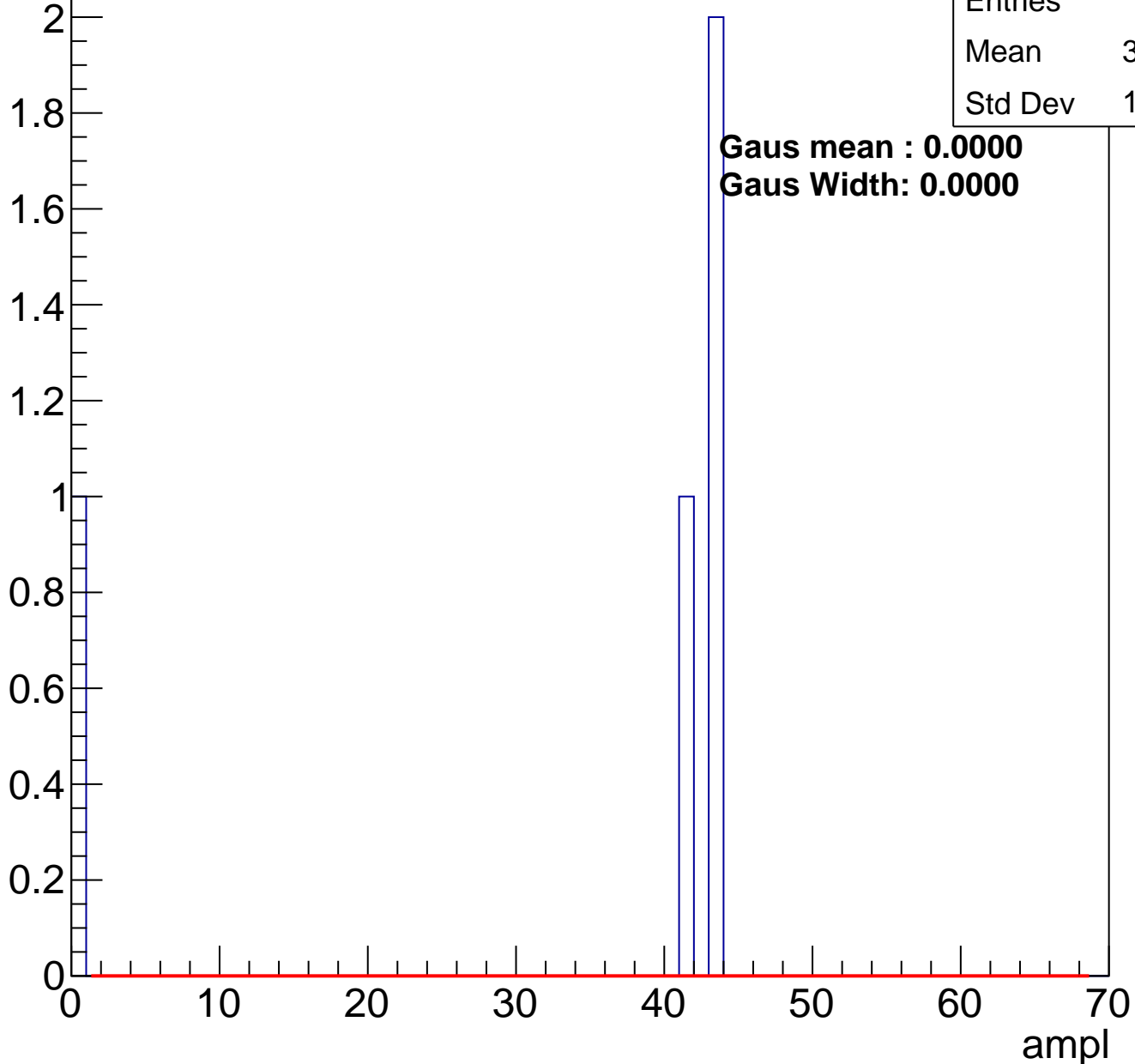
| Entries | 4 |
|---------|------|
| Mean    | 31.75 |
| Std Dev | 18.35 |

# B0L100S, U1-ch4, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**



**Gaus mean : 77.1623**
**Gaus Width: 19.5372**

| | |
|---|---|
| Entries | 12 |
| Mean | 45.25 |
| Std Dev | 1.963 |

Entry

ampl
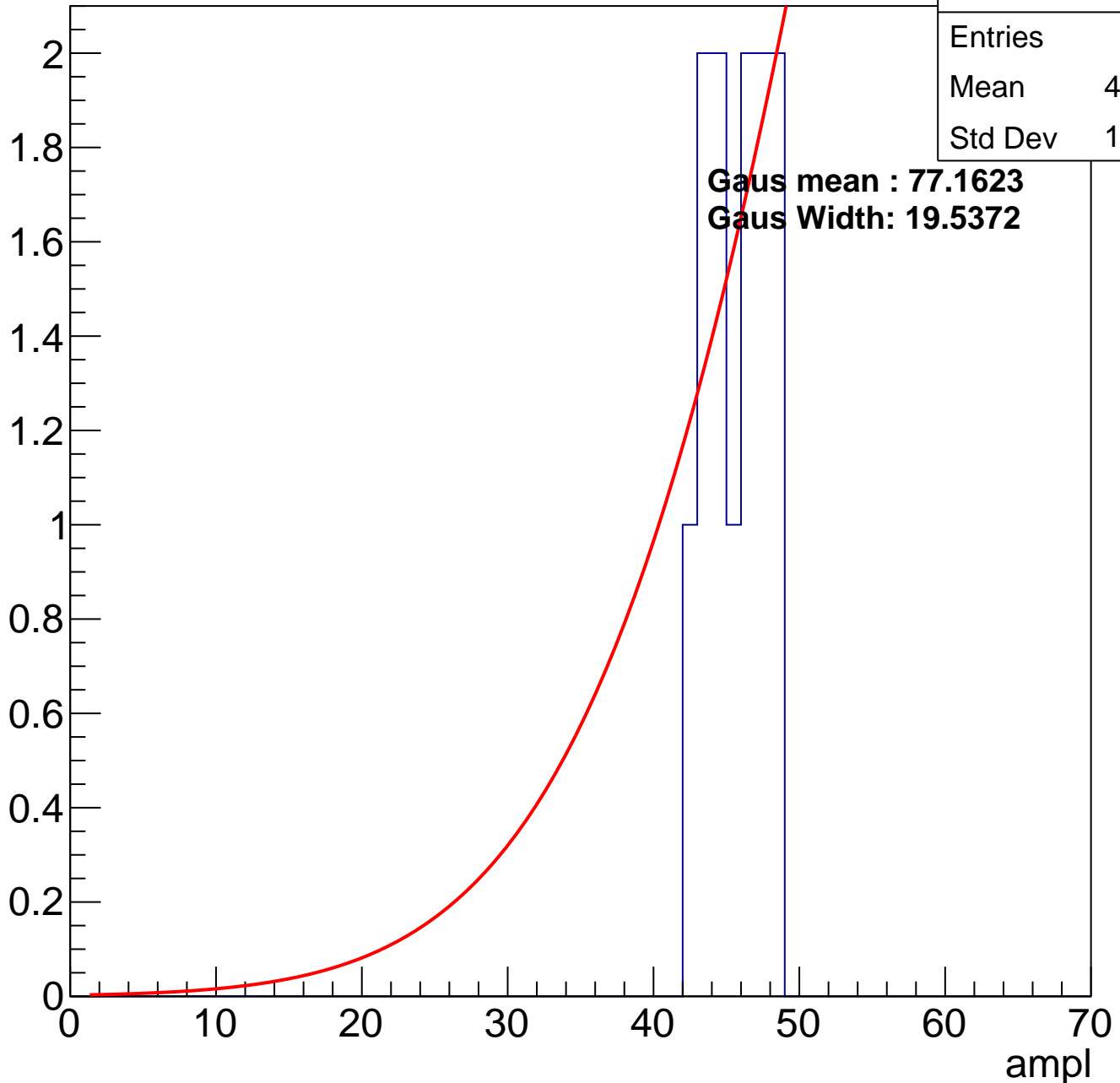
# B0L100S, U1-ch4, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 31 |
| Mean | 47.48 |
| Std Dev | 2.312 |

**Gaus mean : 48.6174**
**Gaus Width: 3.6261**
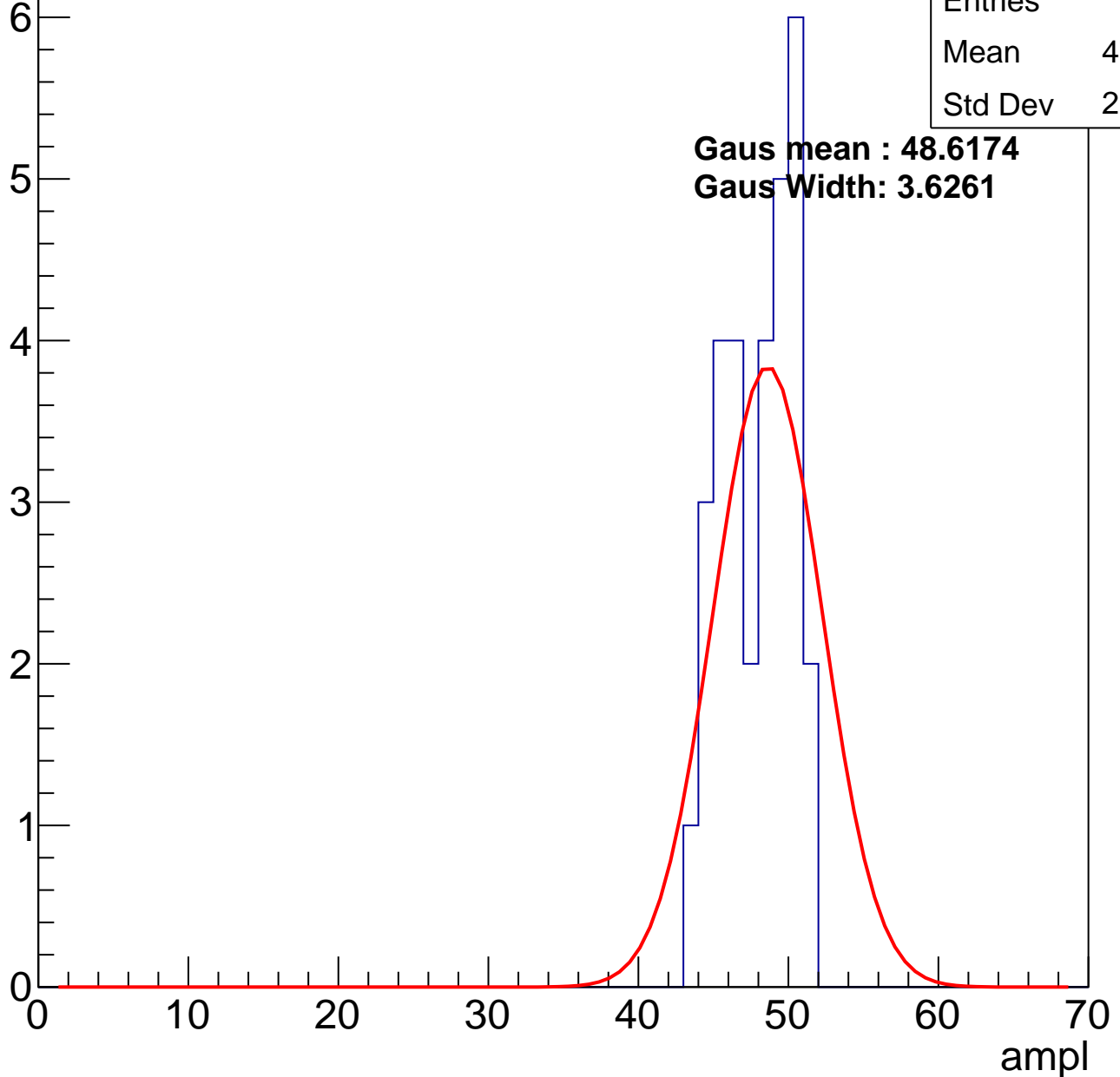
# B0L100S, U1-ch4, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 49 |
| Std Dev | 2.828 |

Entry

ampl
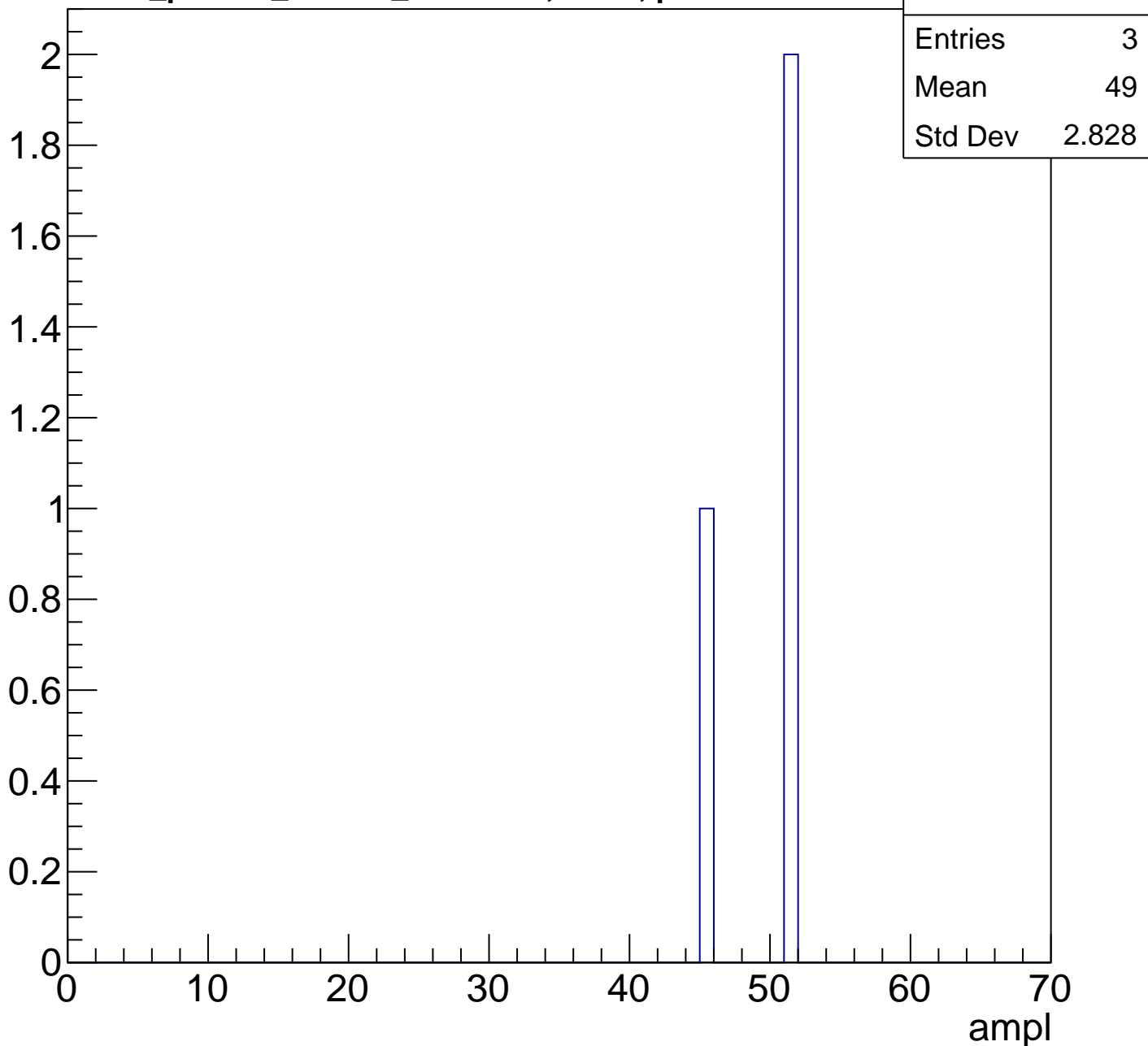
# B0L100S, U1-ch4, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch4, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch4, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch4, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch5, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch5, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch5, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 2 |
| Mean | 52 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
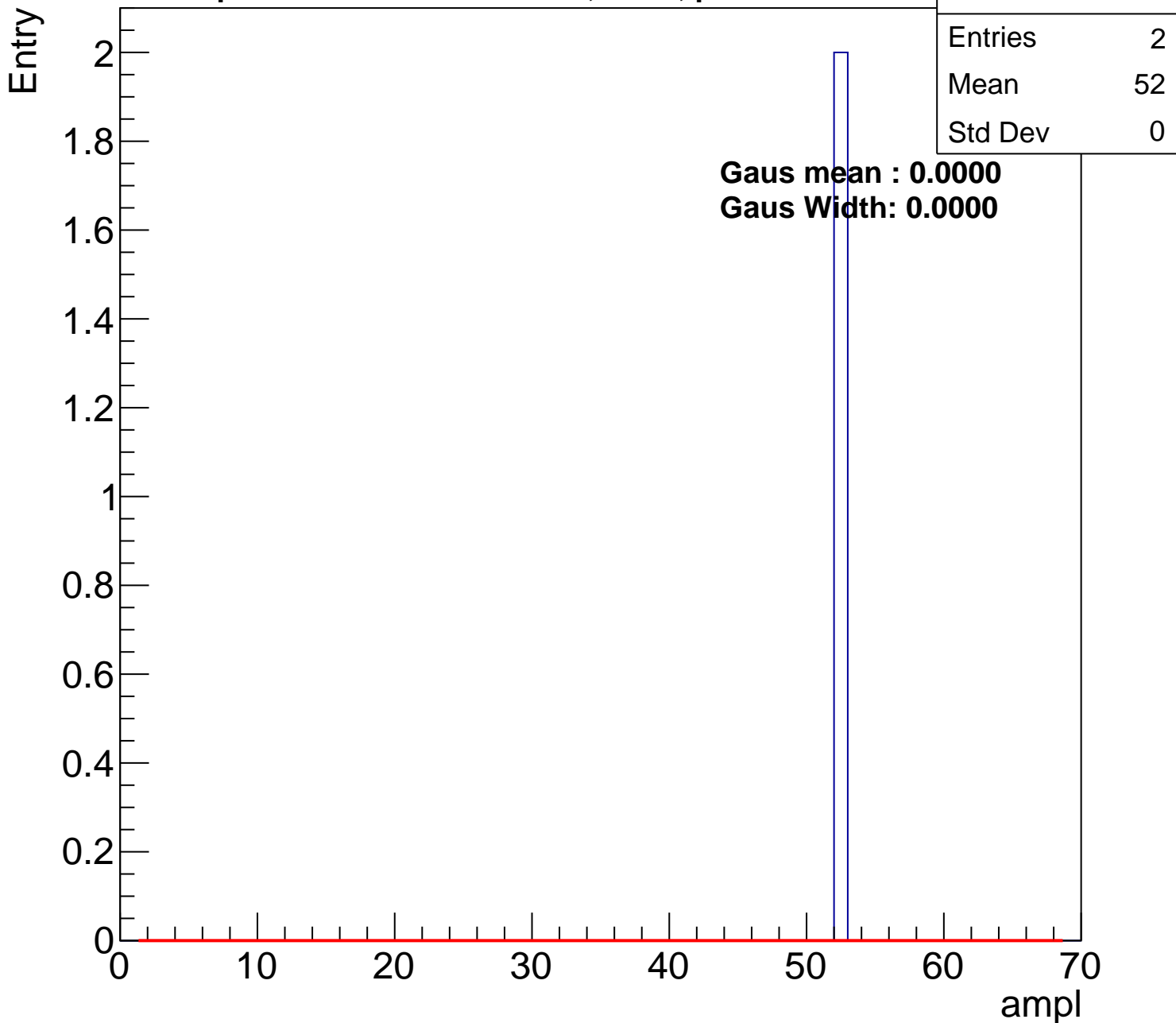
# B0L100S, U1-ch5, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch5, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch5, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch5, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch5, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch6, adc0

calib_packv5_042523_0143.root, FC#6, port A1
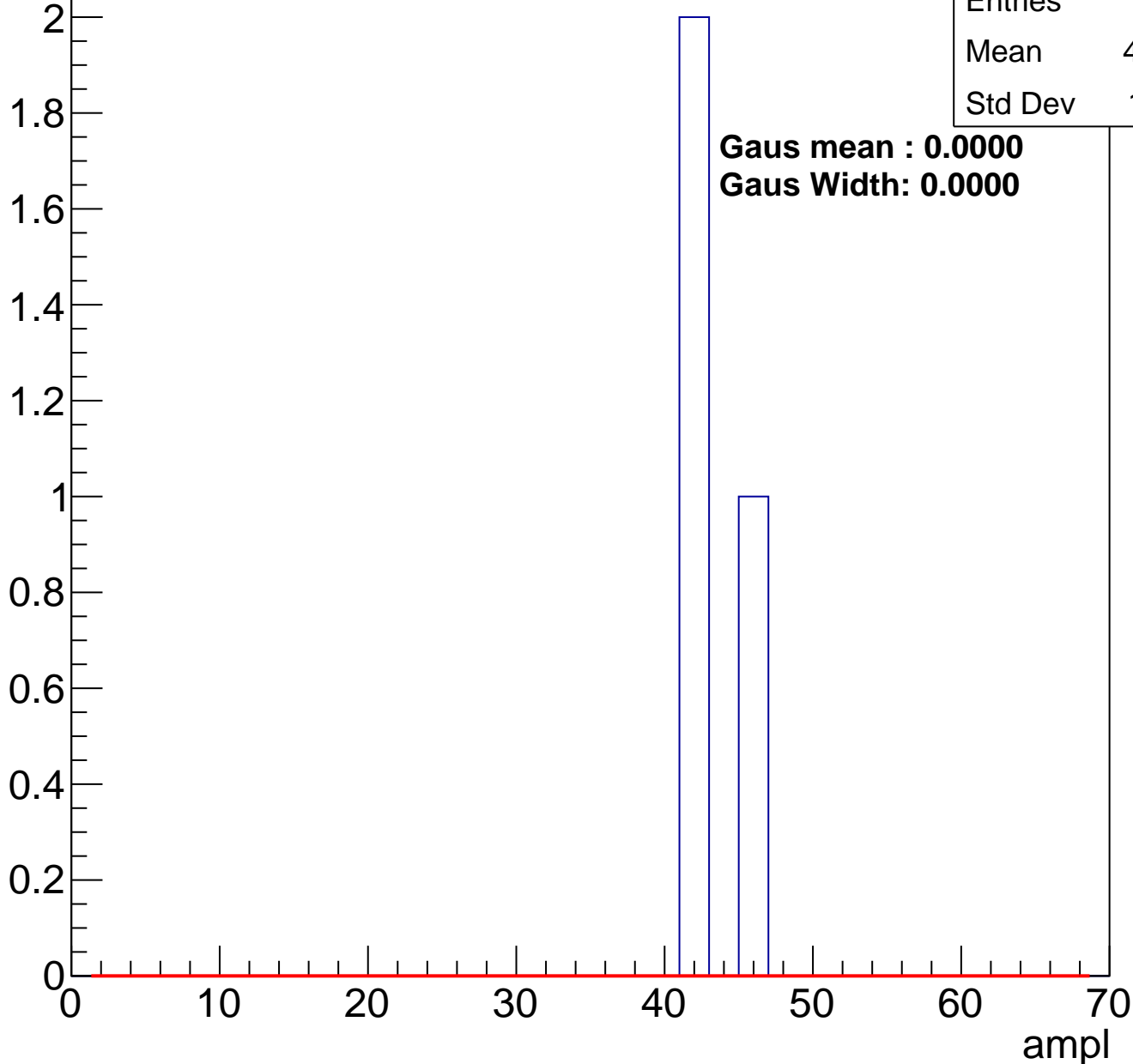
| Entries | 6 |
| Mean | 42.83 |
| Std Dev | 1.951 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch6, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 7 |
|---------|------|
| Mean | 43.57 |
| Std Dev | 2.195 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
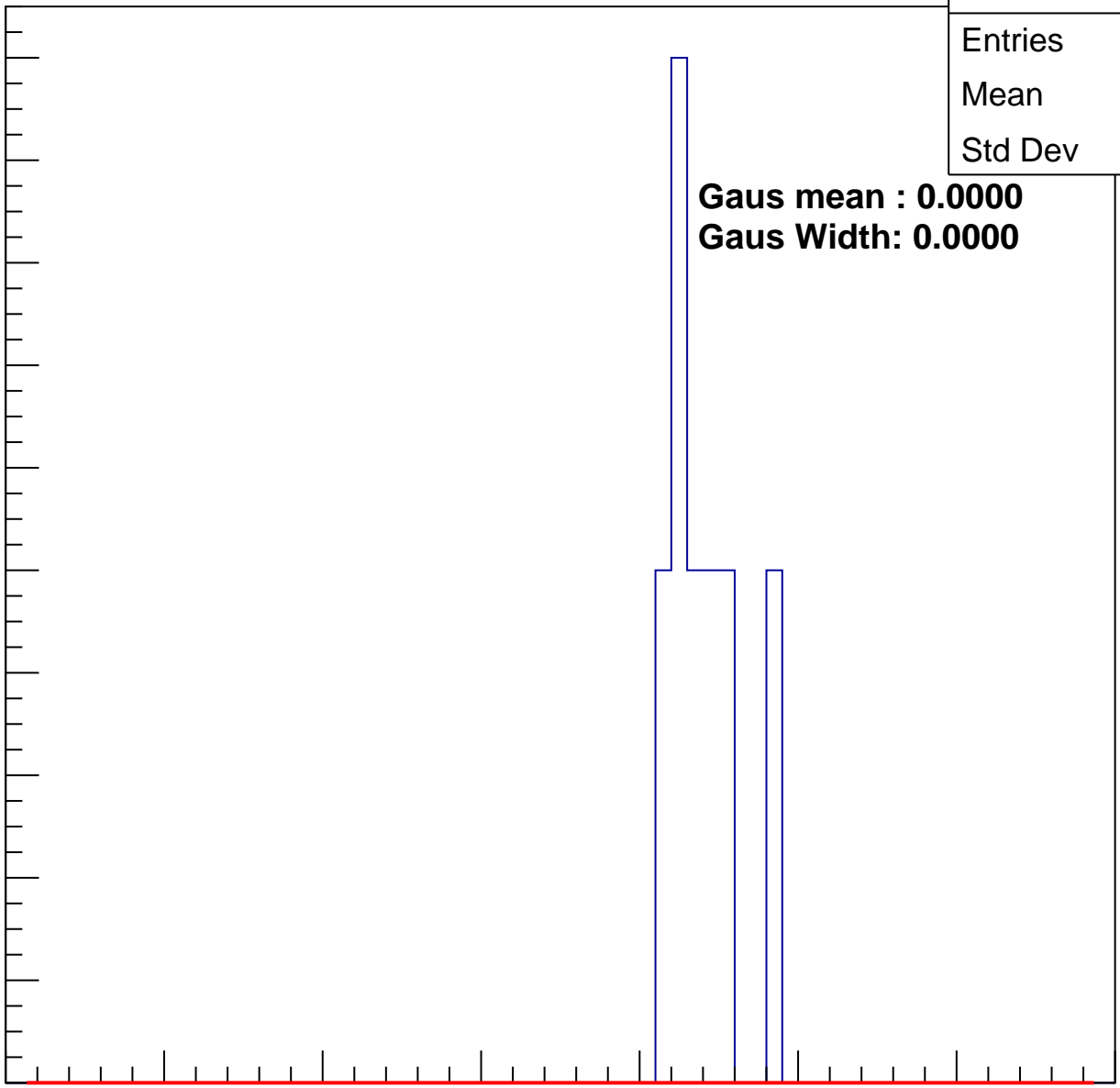
# B0L100S, U1-ch6, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
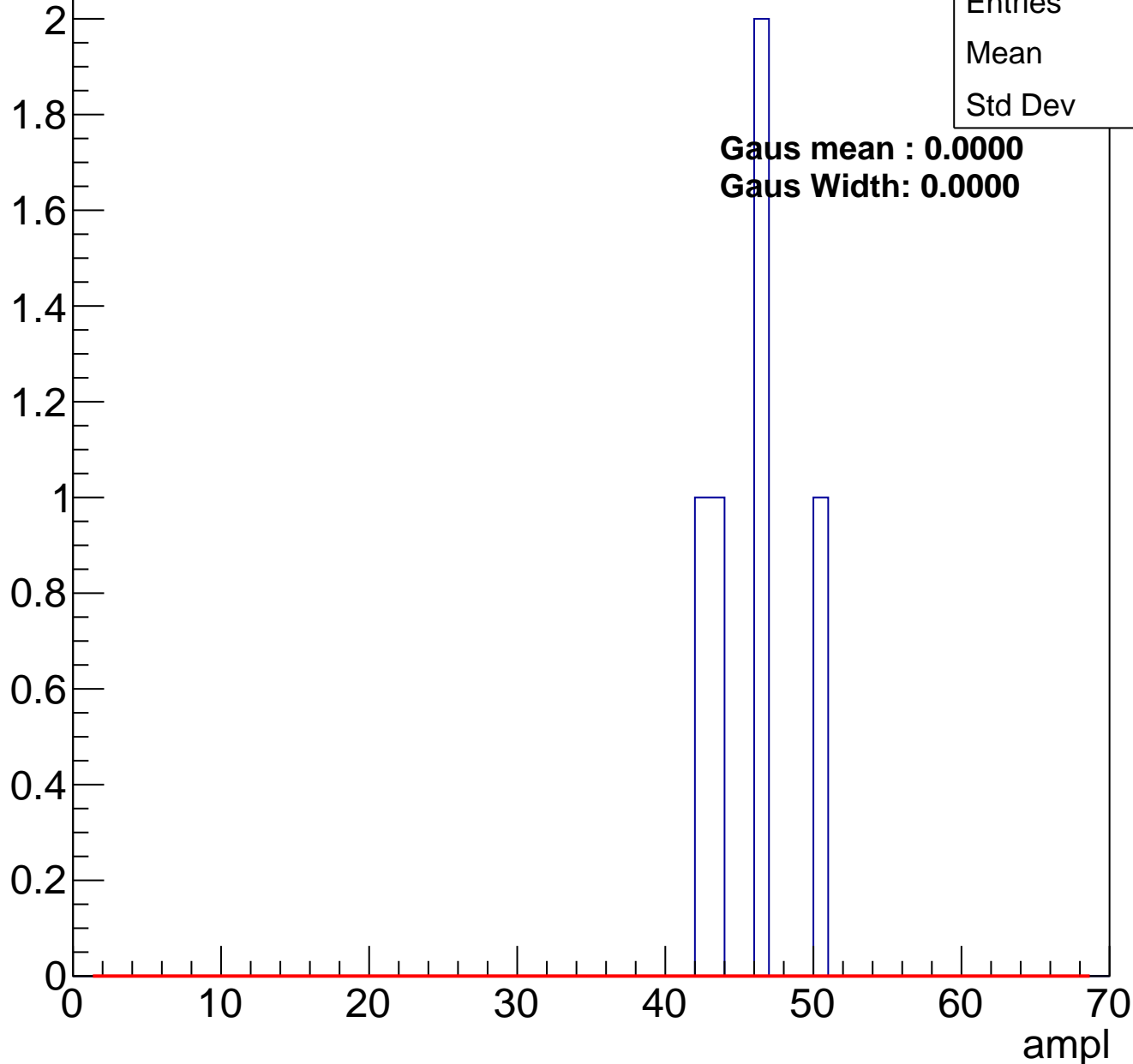
| Entries | 5 |
|---------|---|
| Mean | 45.4 |
| Std Dev | 2.8 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch6, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 43 |
| Std Dev | 0 |

Entry

ampl
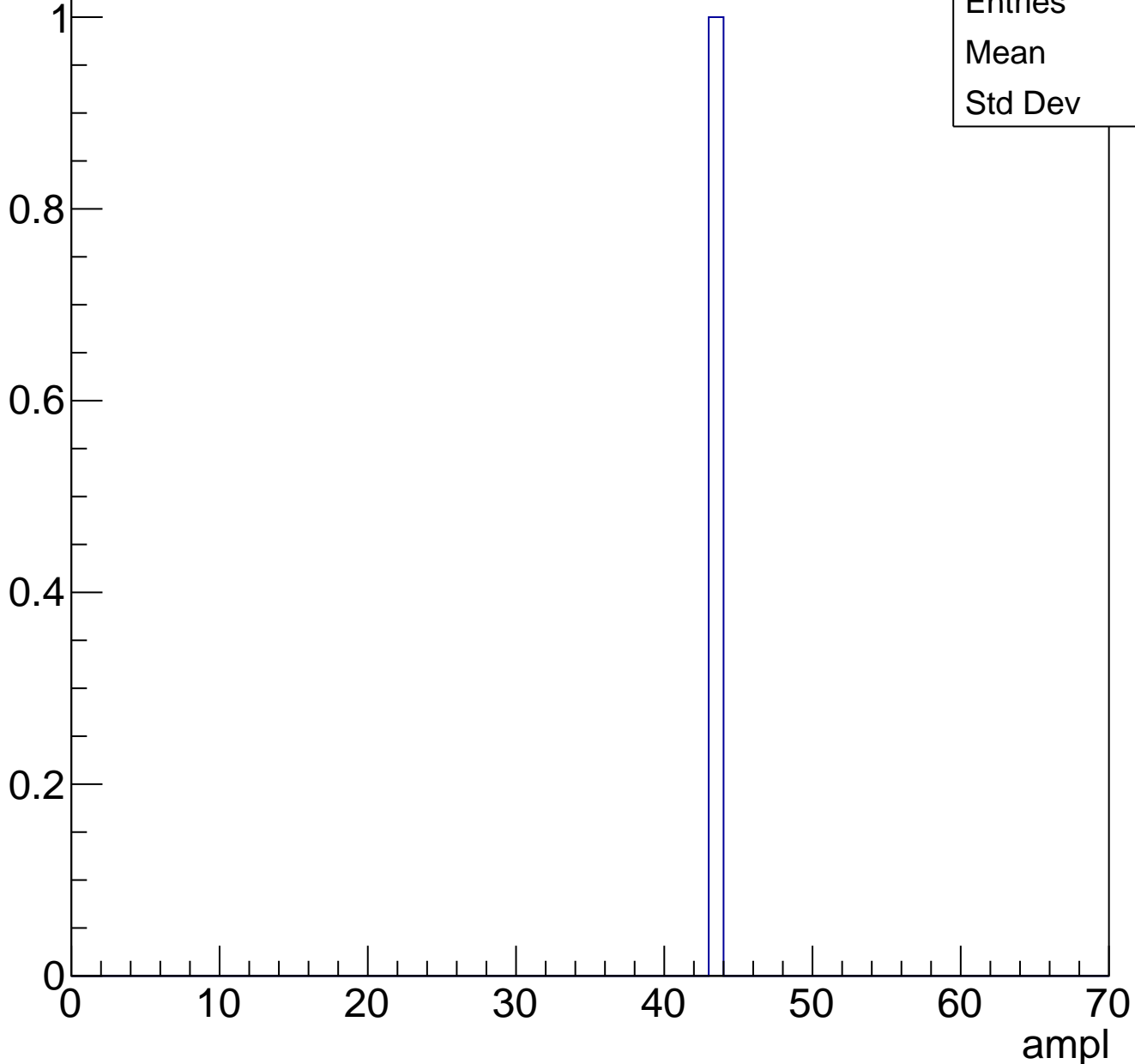
B0L100S, U1-ch6, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch6, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch6, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch6, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch7, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| Entries | 1 |
| Mean | 60 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

1

0.8

0.6

0.4

0.2

0

0   10   20   30   40   50   60   70

ampl

# B0L100S, U1-ch7, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch7, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
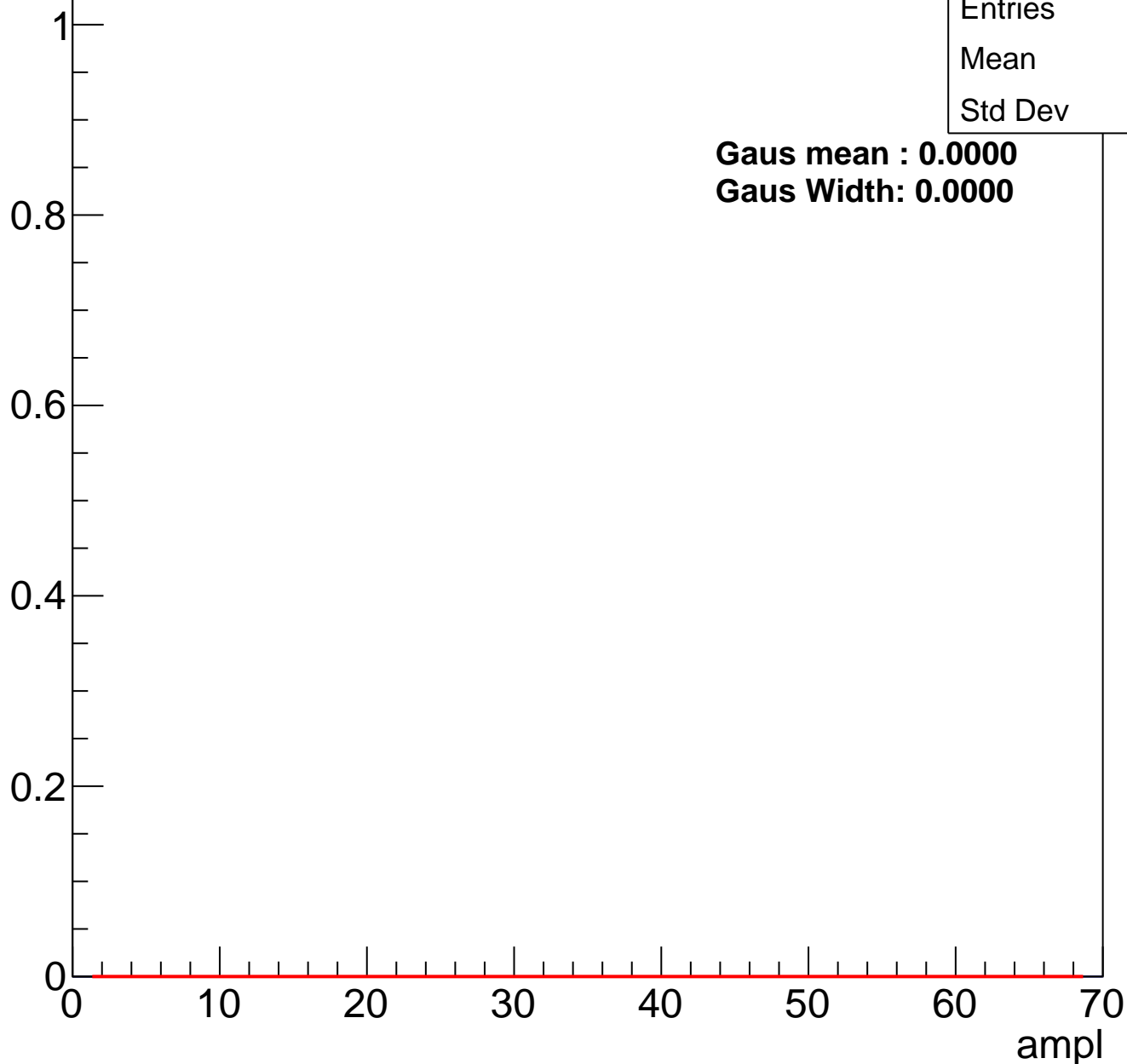
Entry

ampl

# B0L100S, U1-ch7, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 29 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch7, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch7, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch7, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
| --- | --- |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch7, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch8, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch8, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
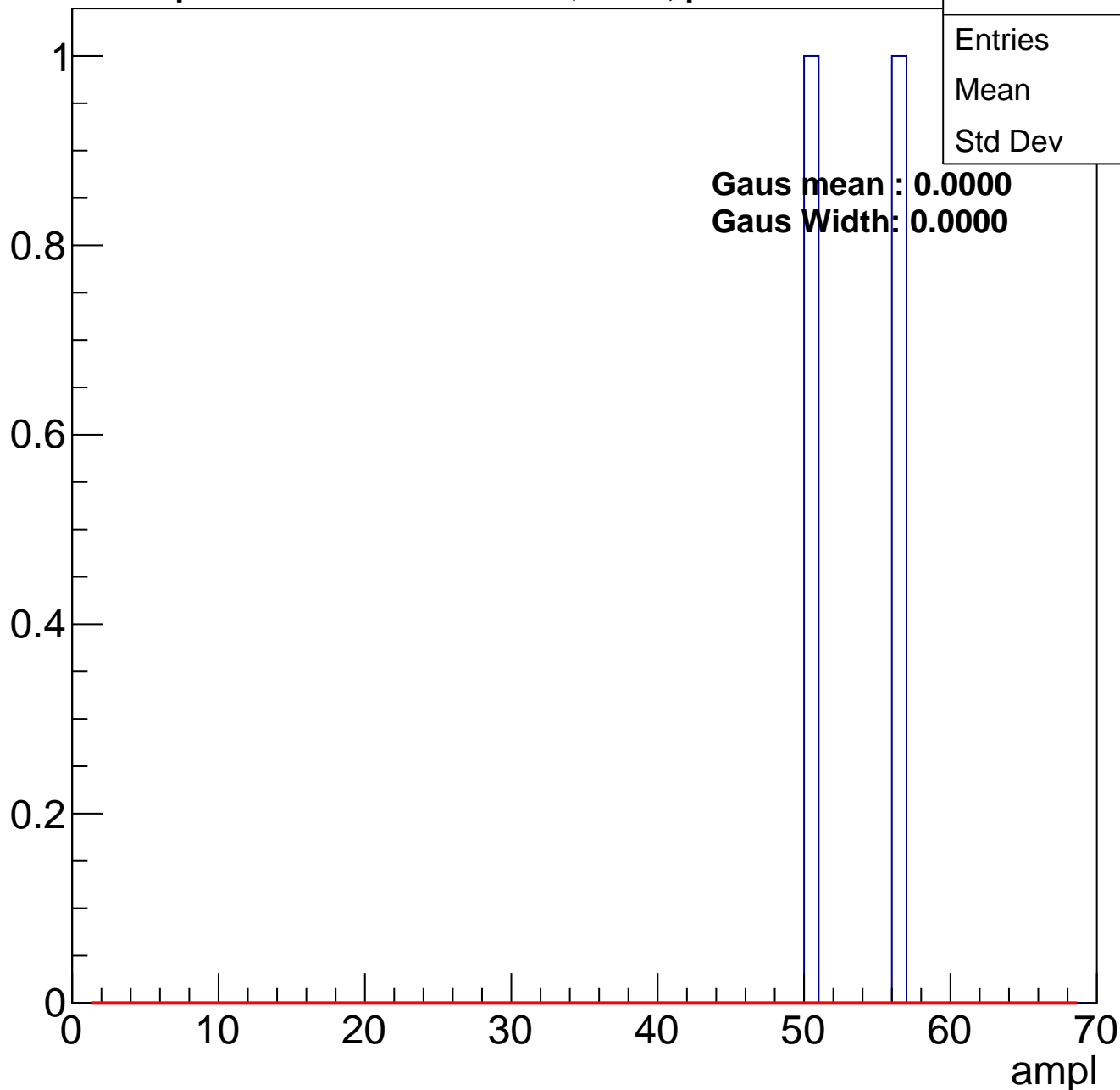
# B0L100S, U1-ch8, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 50 |
| Std Dev | 0 |

Entry

ampl
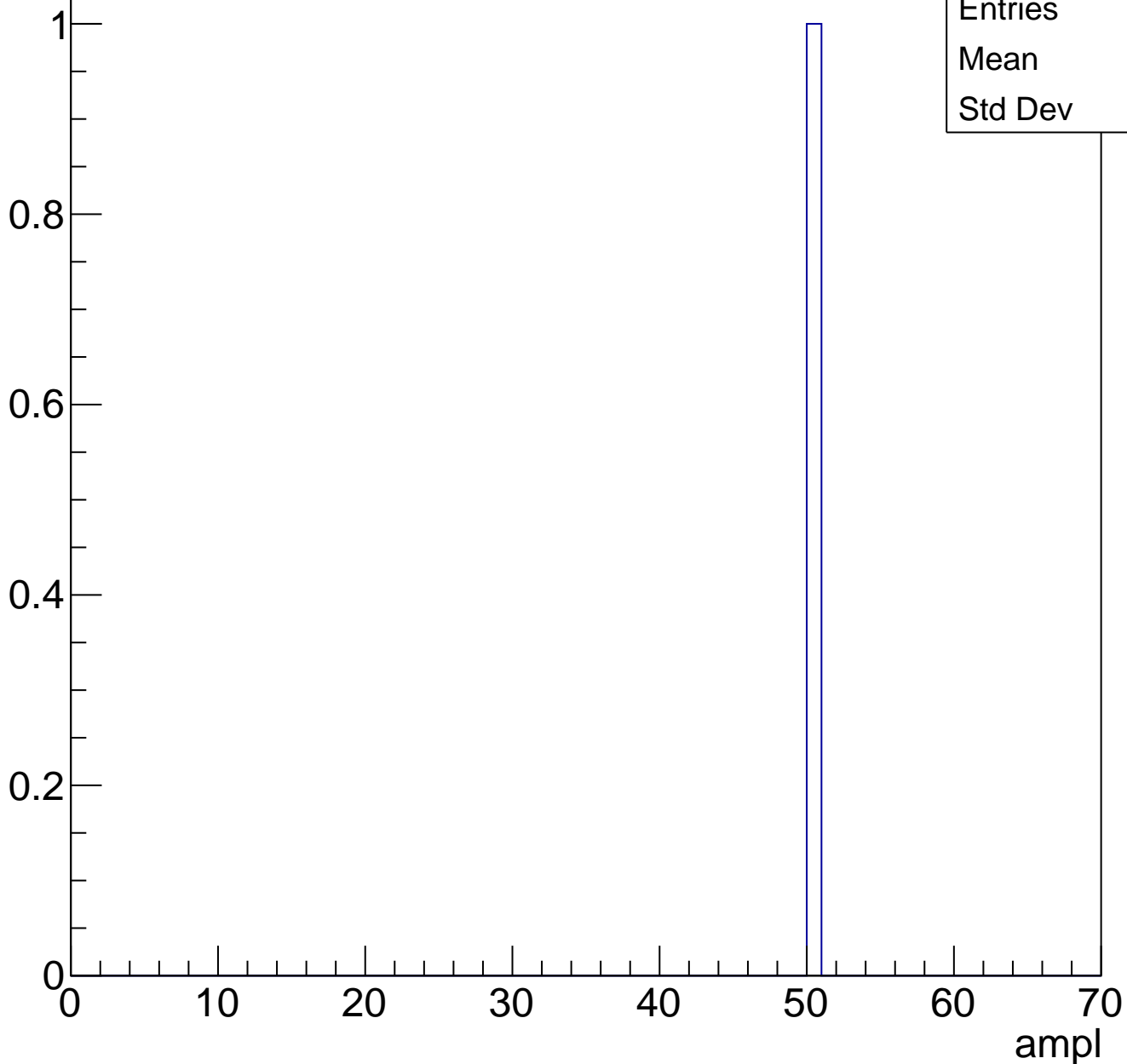
# B0L100S, U1-ch8, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch8, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch8, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch8, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch9, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch9, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch9, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 52 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
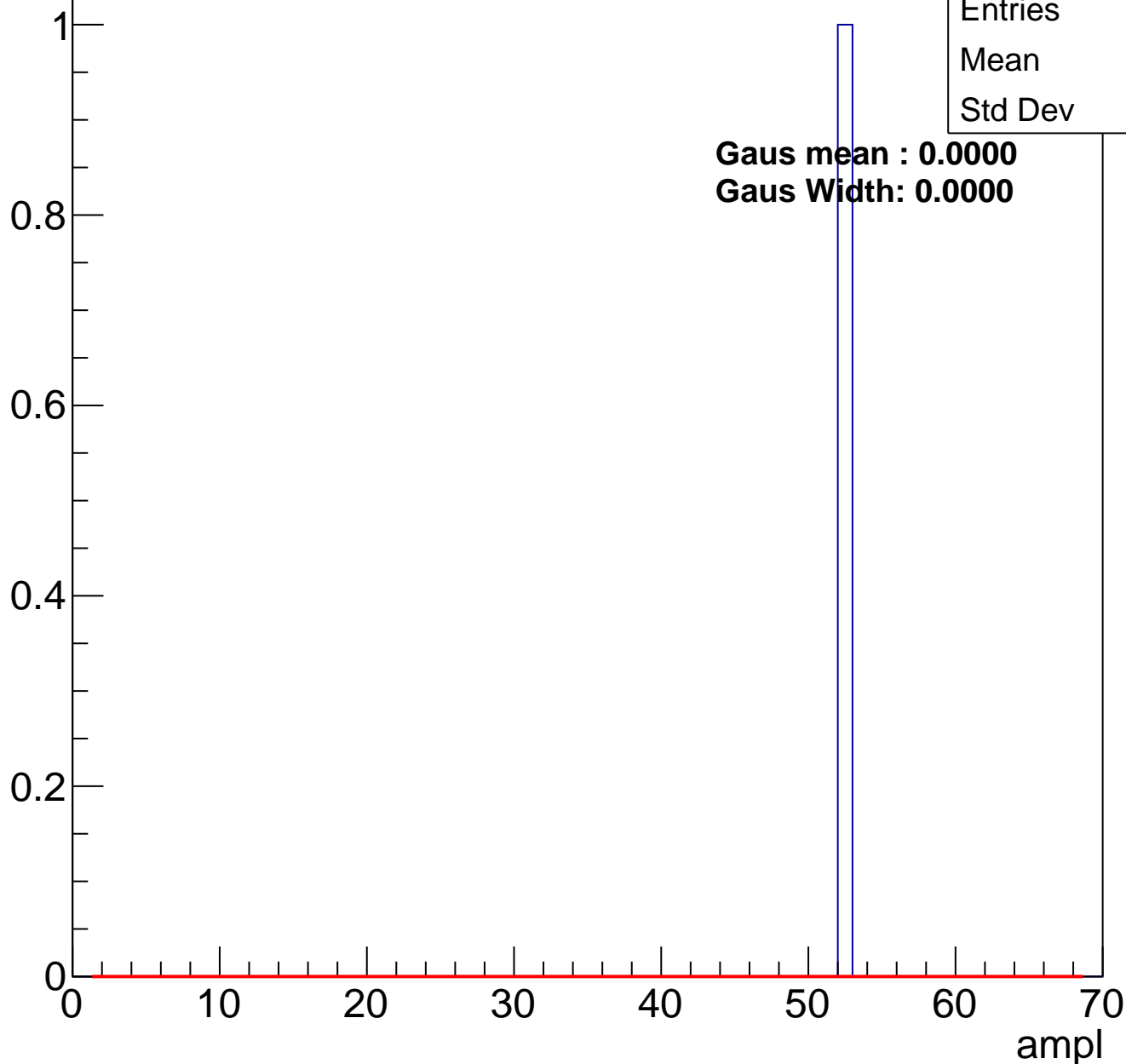
# B0L100S, U1-ch9, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch9, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
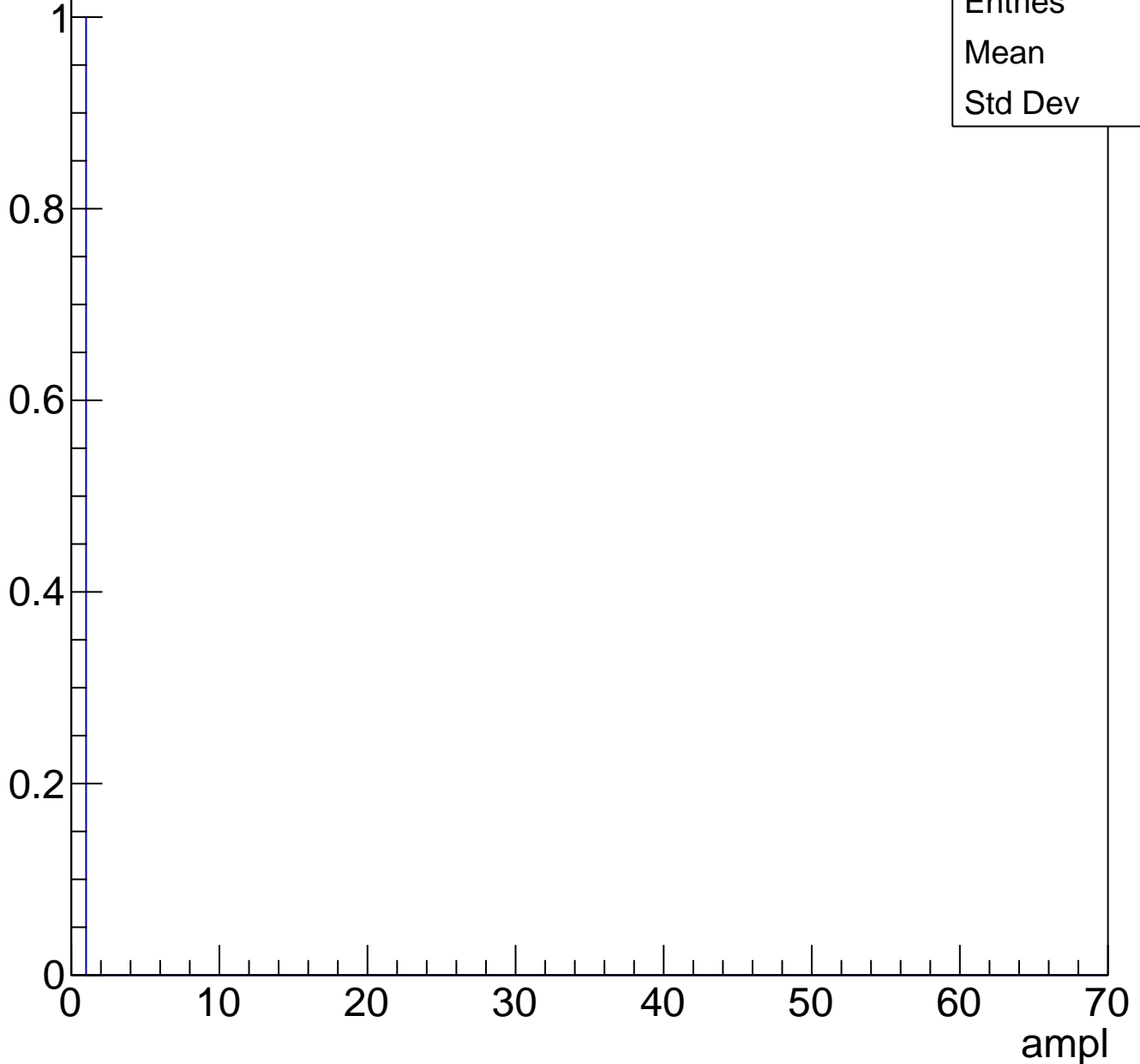
# B0L100S, U1-ch9, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch9, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch9, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch10, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch10, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch10, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch10, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch10, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch10, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch10, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch10, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch11, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch11, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

# B0L100S, U1-ch11, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 49 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
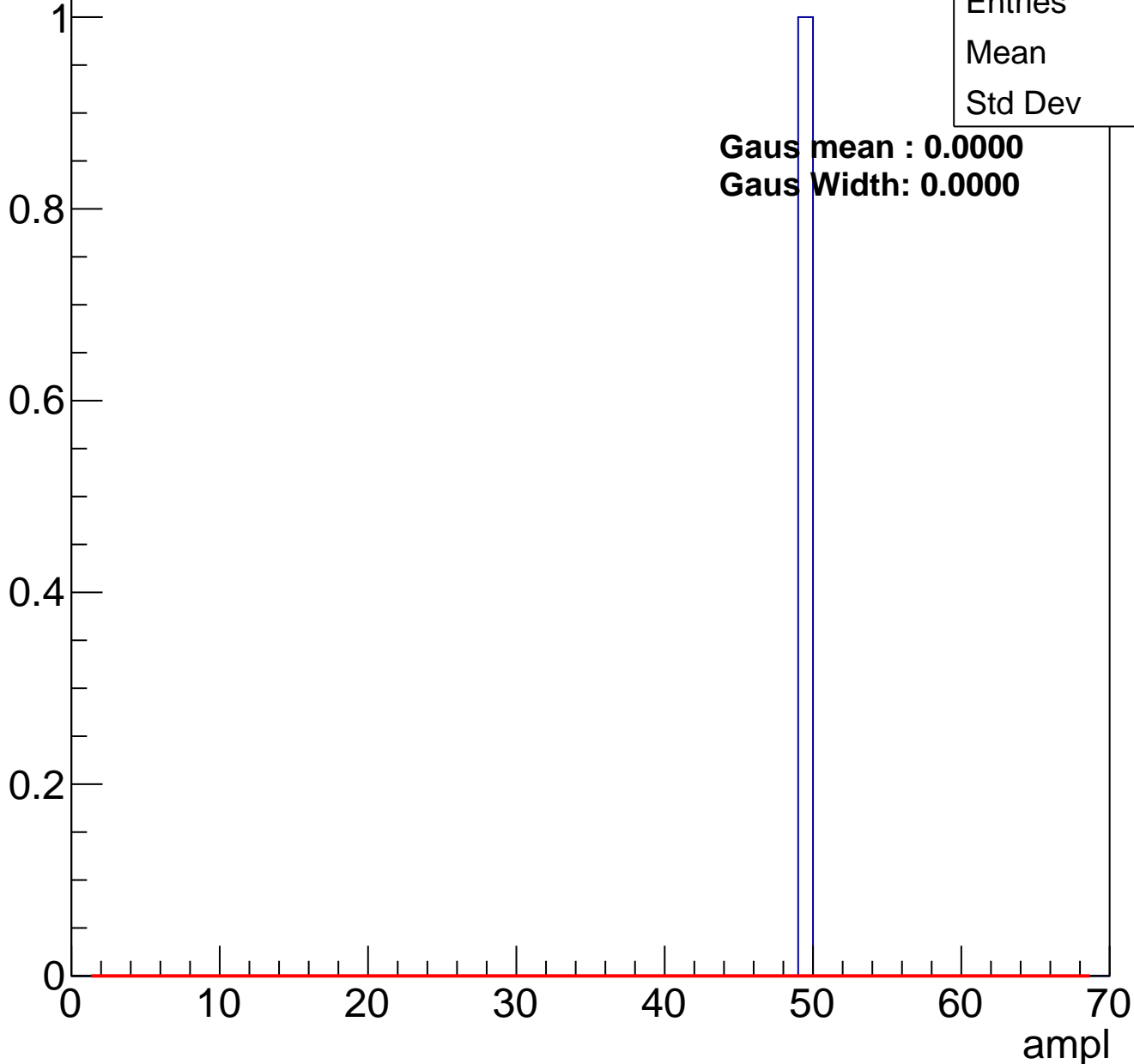
# B0L100S, U1-ch11, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch11, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch11, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch11, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch11, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch12, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 23 |
| Std Dev | 22 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
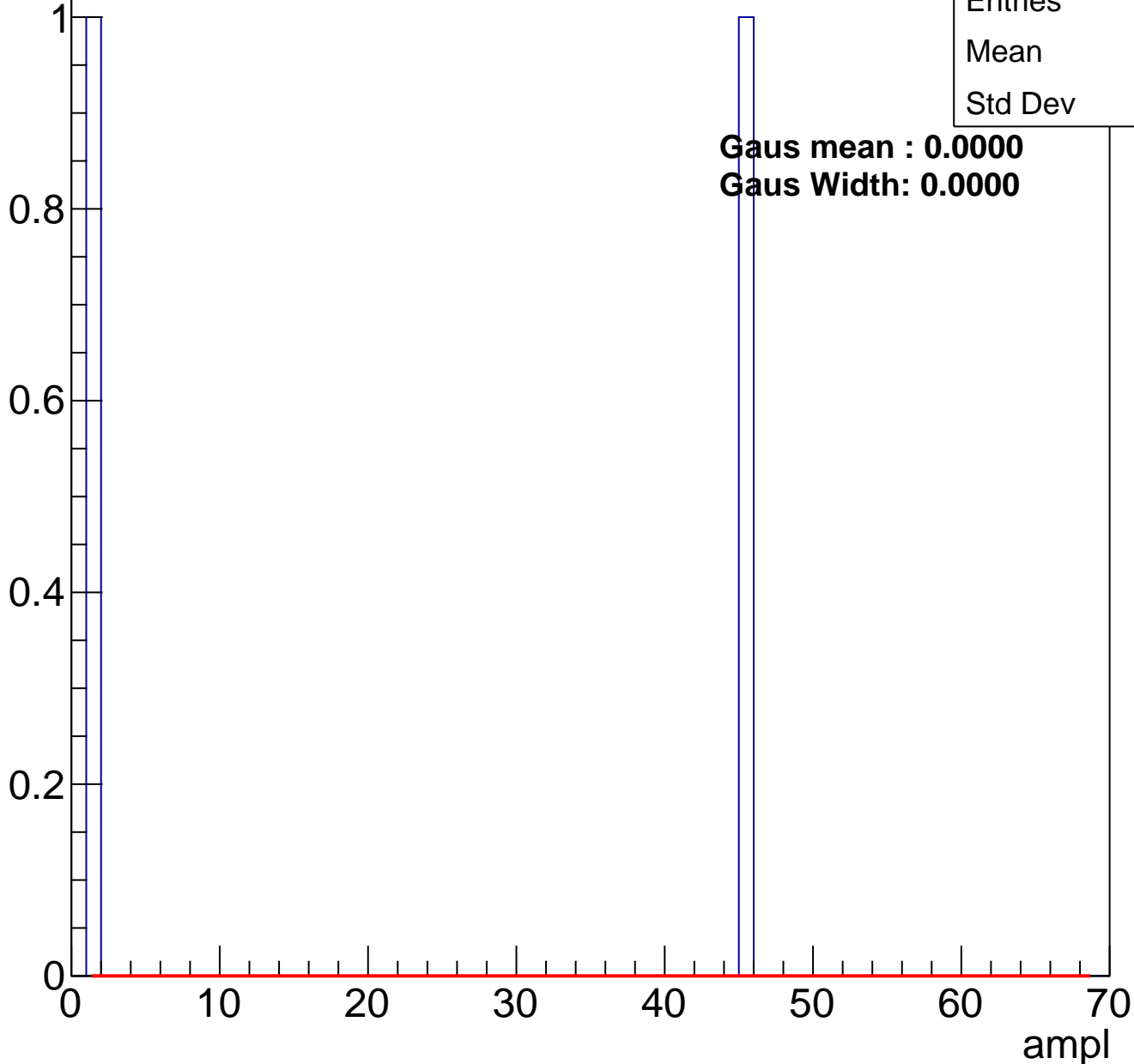
# B0L100S, U1-ch12, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 43 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
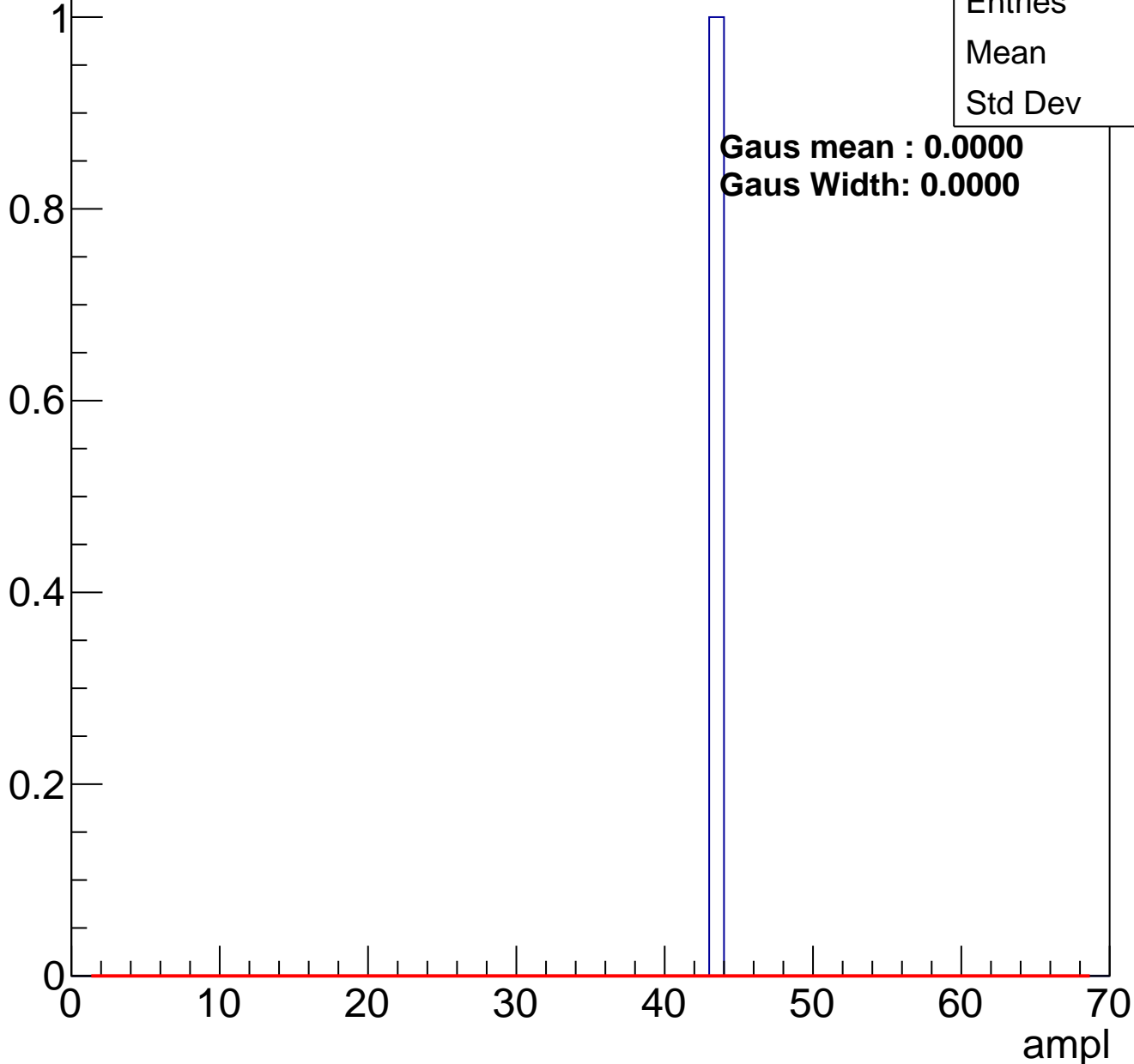
# B0L100S, U1-ch12, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 8 |
|---------|-------|
| Mean | 50.25 |
| Std Dev | 2.817 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
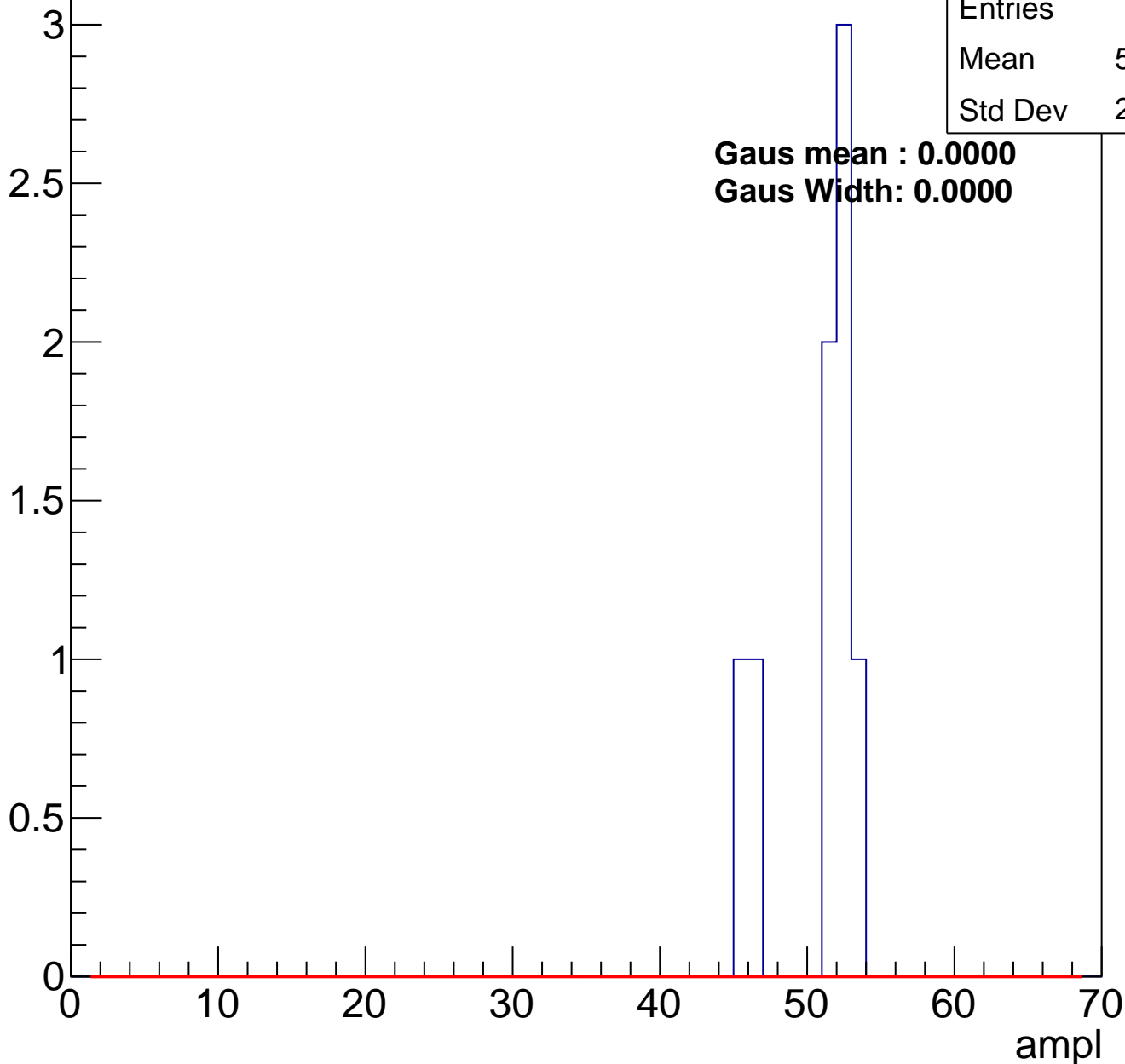
# B0L100S, U1-ch12, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 51.67 |
| Std Dev | 0.4714 |

Entry

ampl
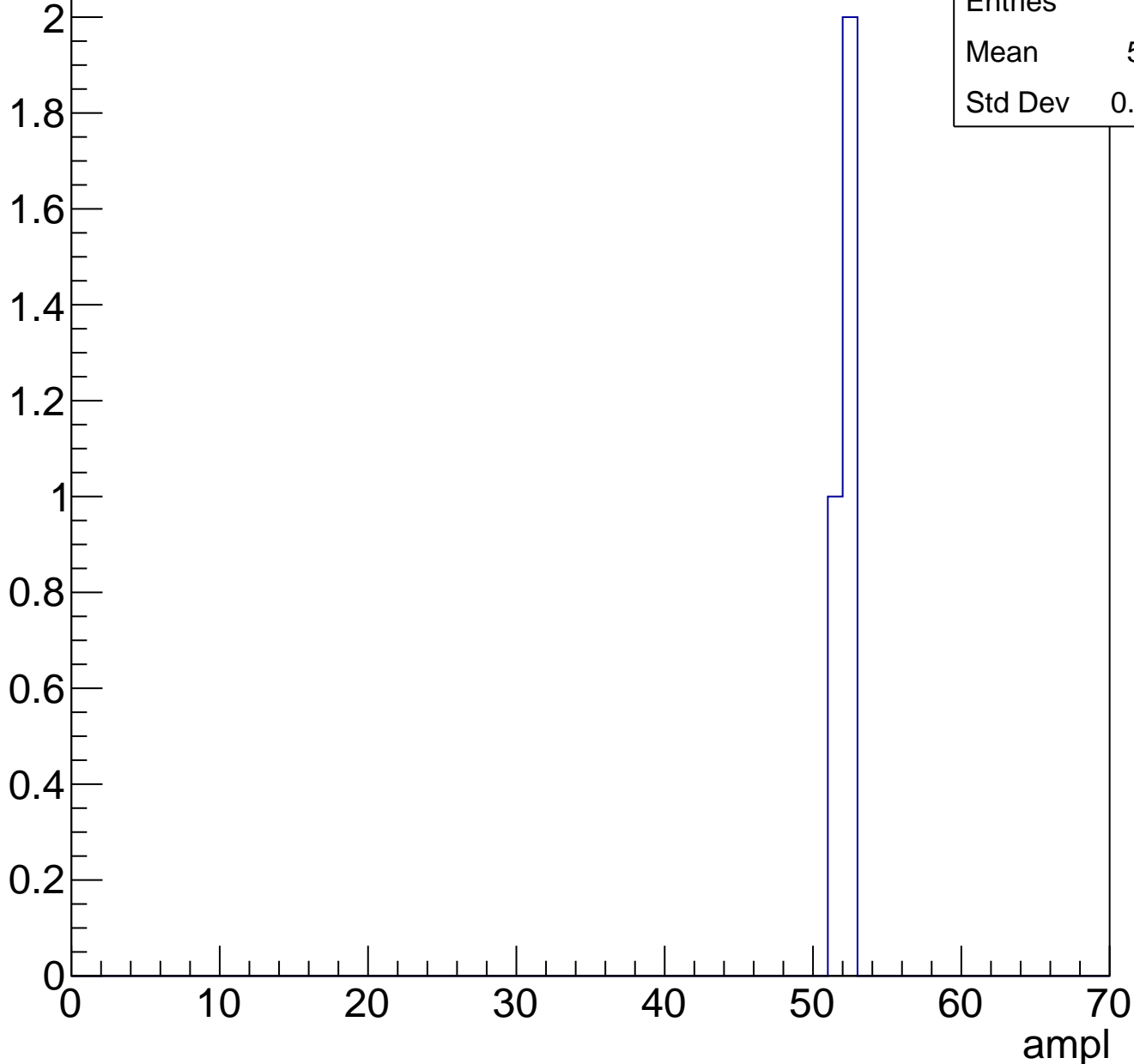
# B0L100S, U1-ch12, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch12, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
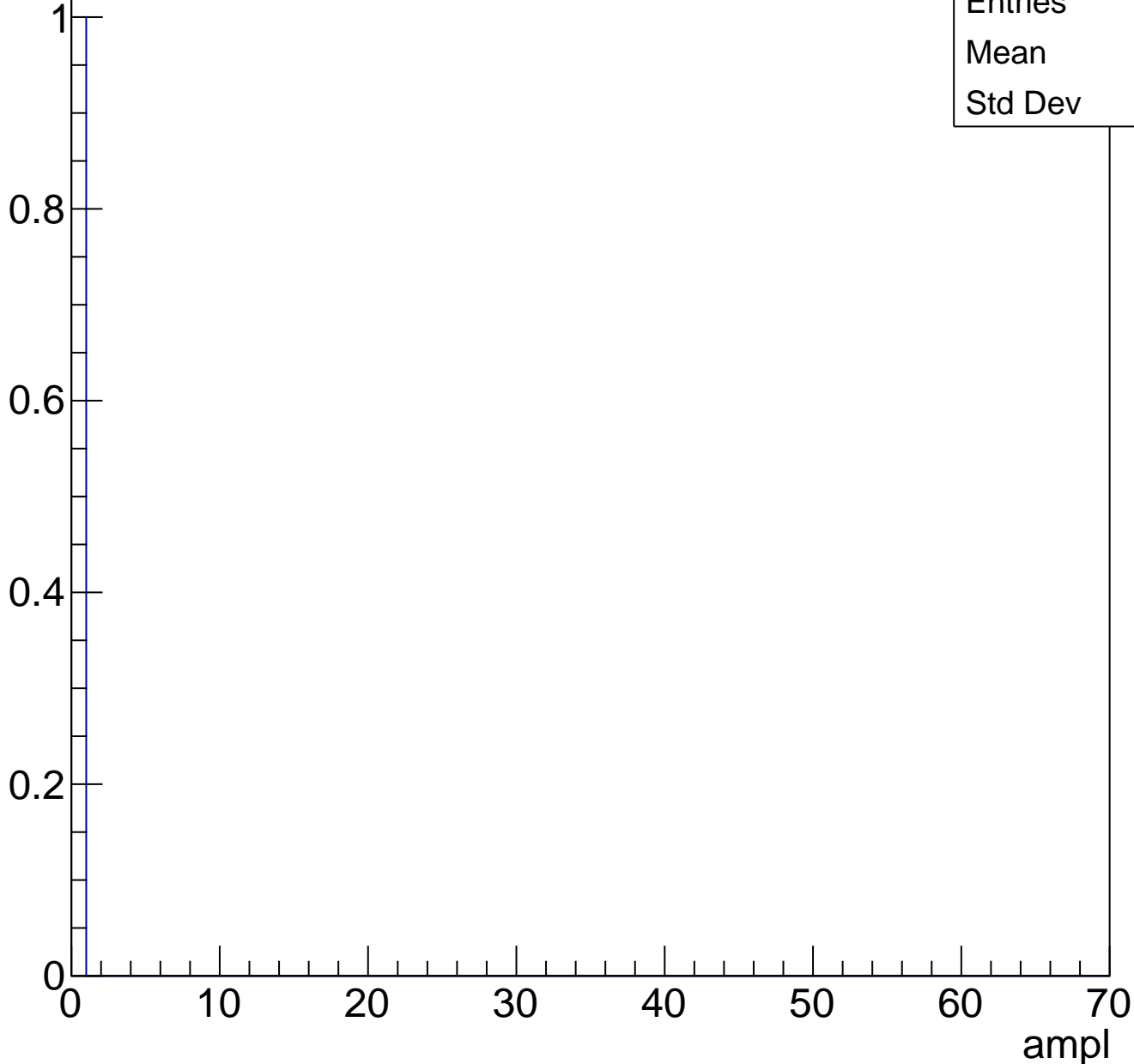
# B0L100S, U1-ch12, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch12, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch13, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch13, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
| Mean | 40 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
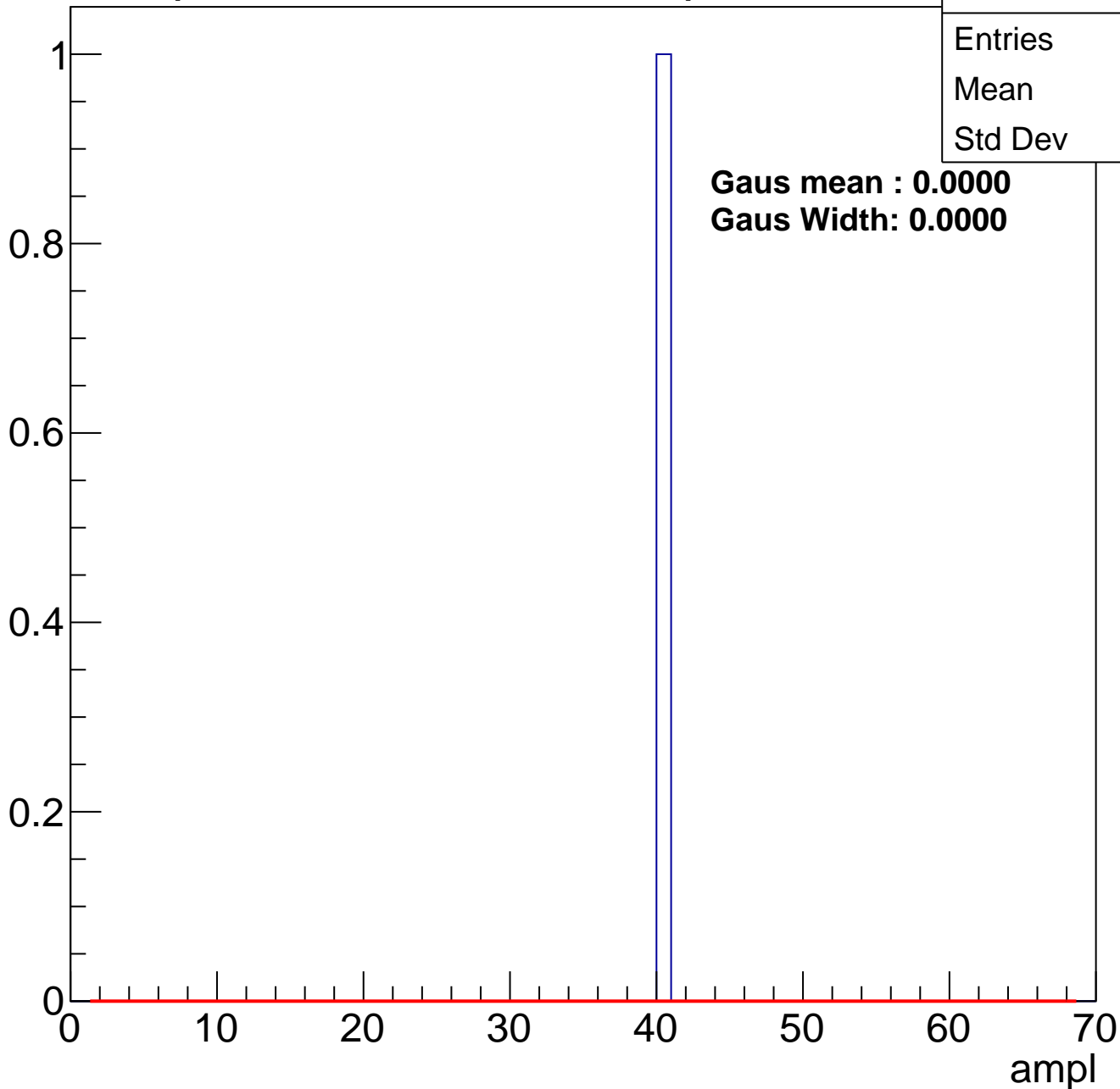
# B0L100S, U1-ch13, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
| --- | --- |
| Mean | 40 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
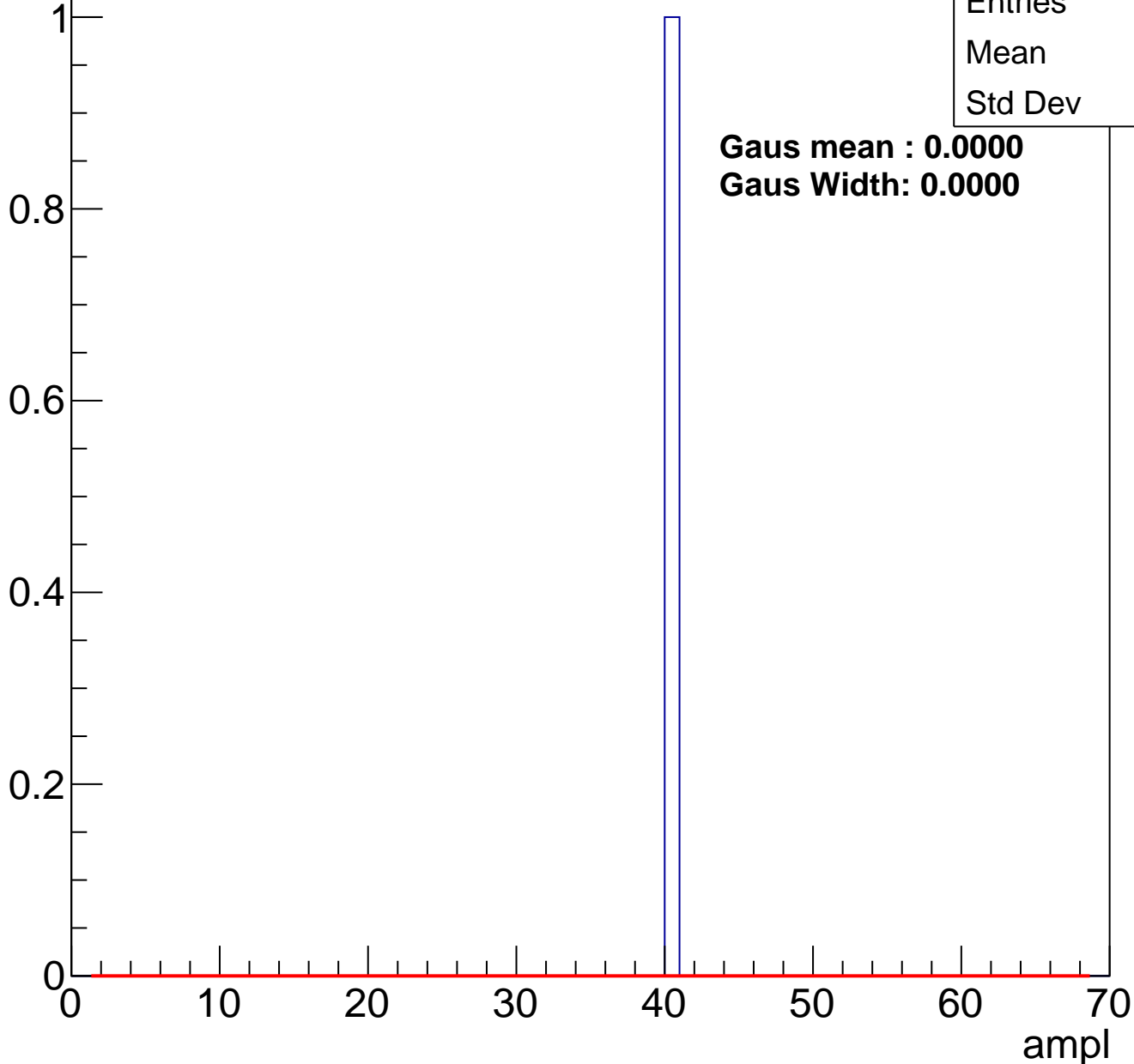
# B0L100S, U1-ch13, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch13, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch13, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch13, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch13, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch14, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch14, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch14, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch14, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch14, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch14, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch14, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch14, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch15, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch15, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 3 |
|---|---|
| Mean | 44.67 |
| Std Dev | 0.9428 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
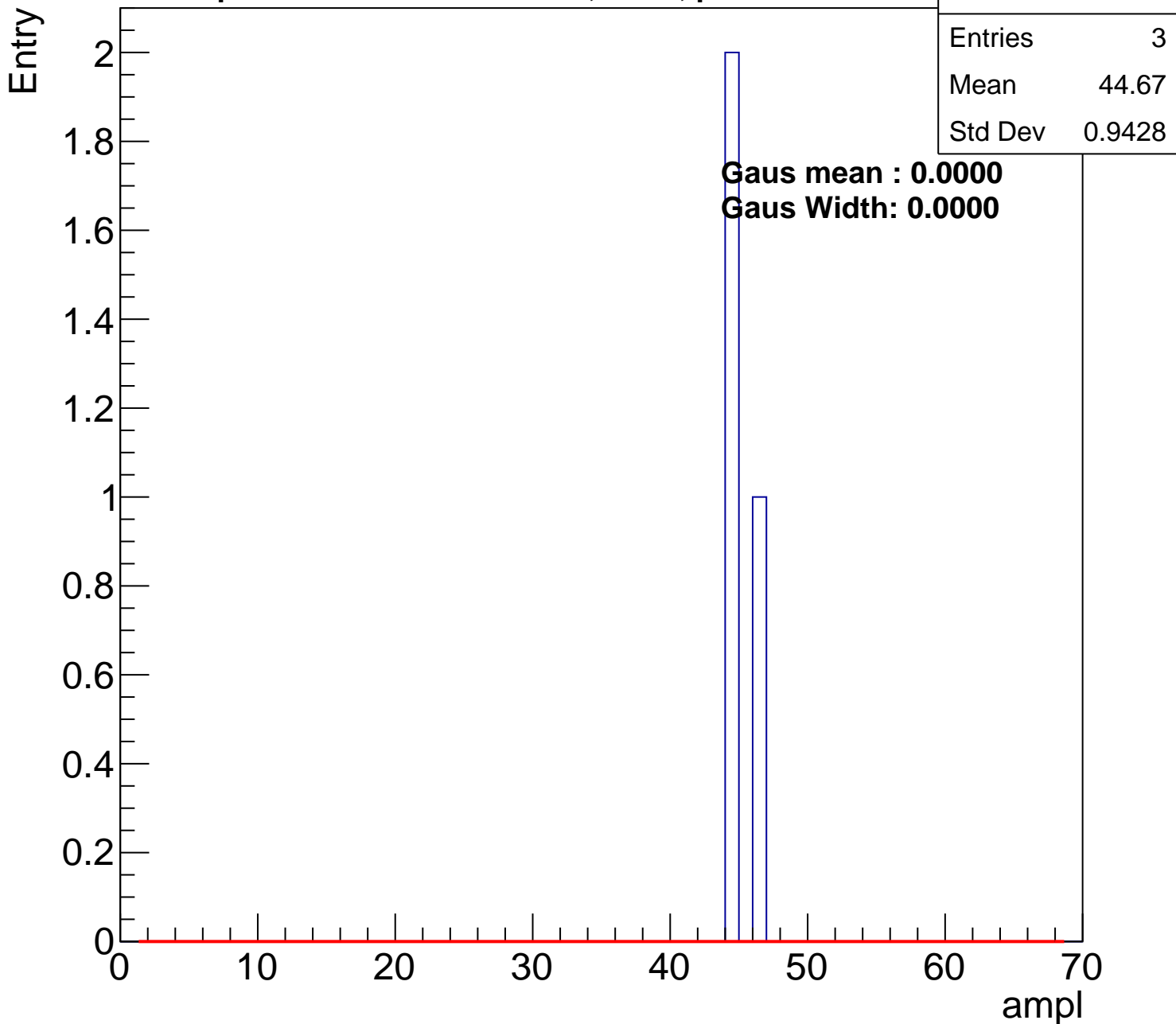
Entry

ampl

# B0L100S, U1-ch15, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | | |
|---|---|---|
| Entries | | 3 |
| Mean | | 47.33 |
| Std Dev | | 1.886 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
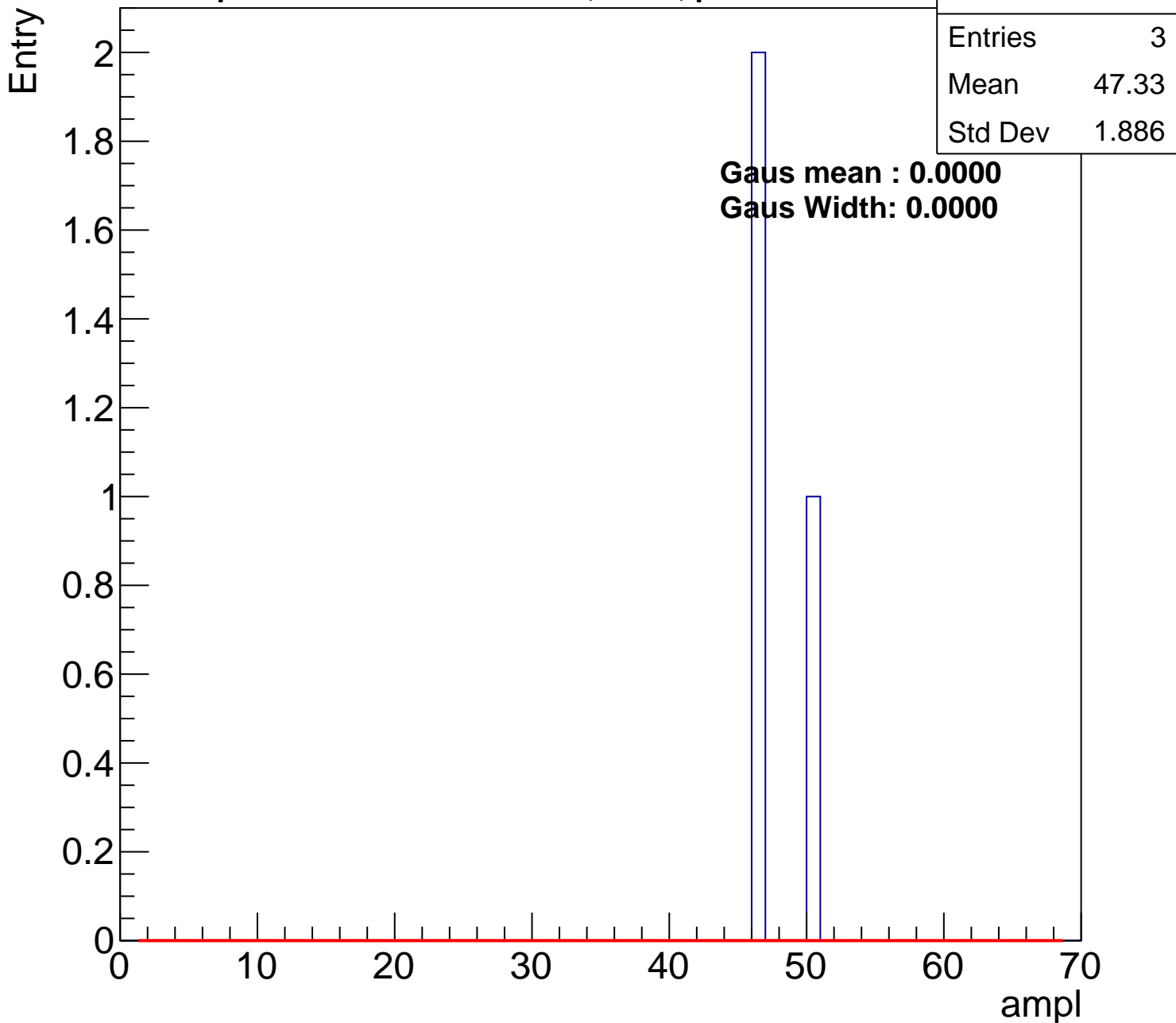
Entry

ampl

# B0L100S, U1-ch15, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 2 |
|---|---|
| Mean | 55 |
| Std Dev | 0 |

Entry

ampl
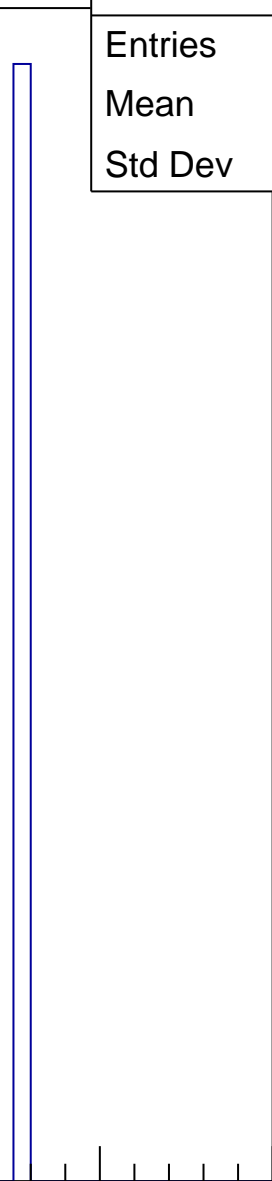
# B0L100S, U1-ch15, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch15, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch15, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch15, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

ampl

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch16, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
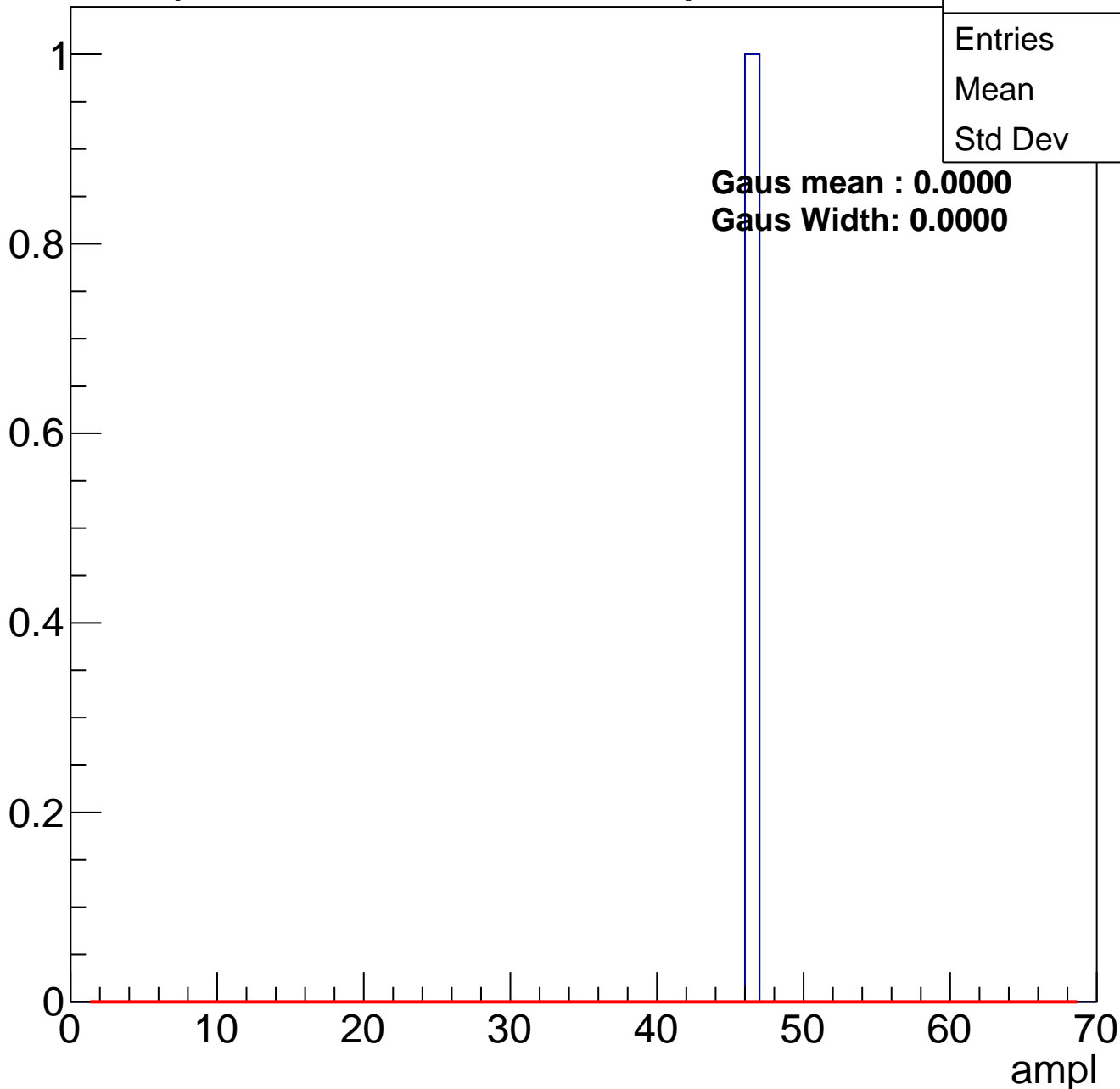
# B0L100S, U1-ch16, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 46 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch16, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch16, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch16, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch16, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch16, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch16, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch17, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

| Entries | 1 |
|---------|---|
| Mean | 44 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch17, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 19 |
| Mean | 45.16 |
| Std Dev | 1.954 |

**Gaus mean : 44.0035**
**Gaus Width: 3.1043**

Entry

ampl
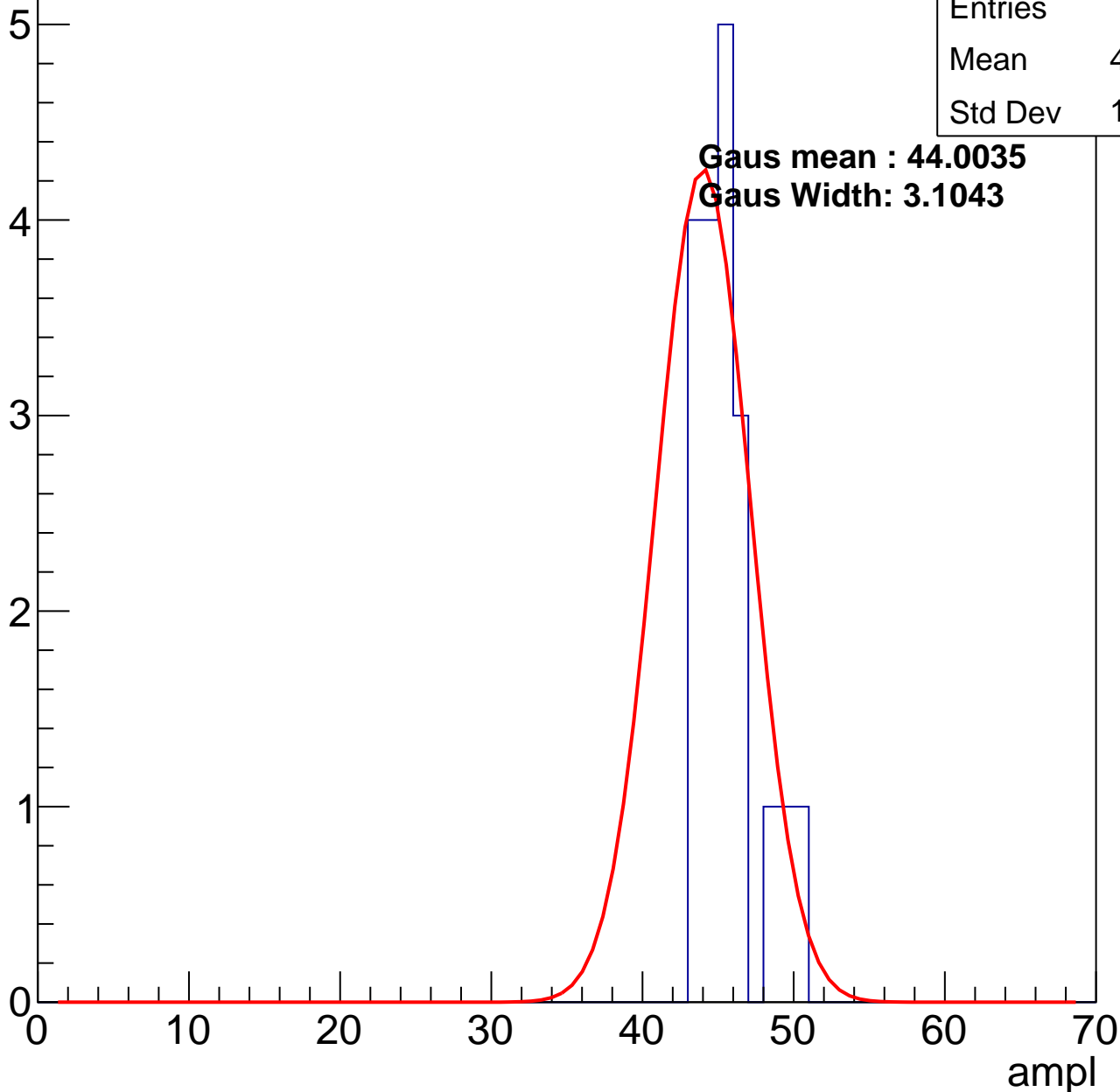
# B0L100S, U1-ch17, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 18 |
| Mean | 49 |
| Std Dev | 1.826 |

**Gaus mean : 49.2365**
**Gaus Width: 2.7605**

ampl
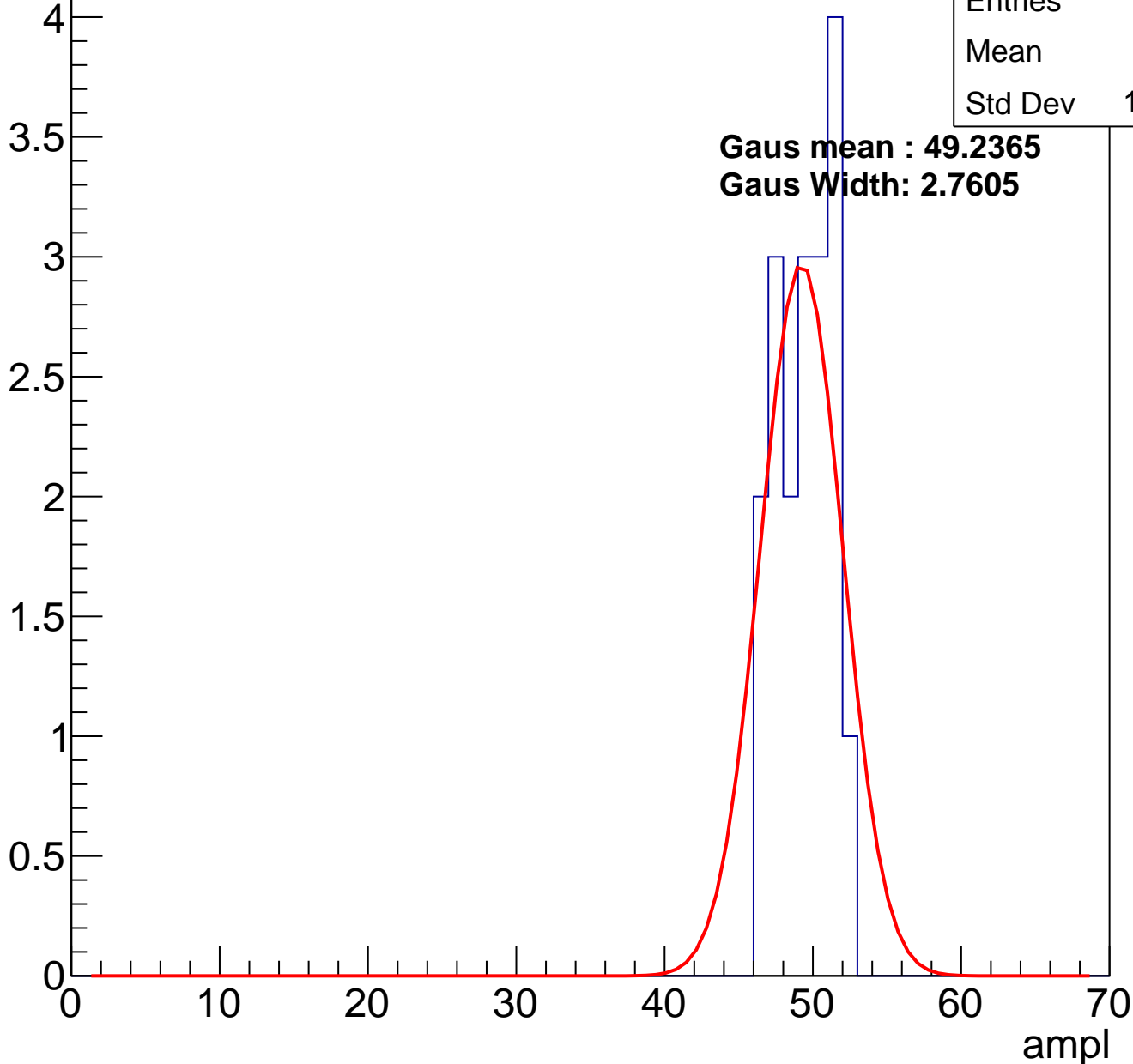
# B0L100S, U1-ch17, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
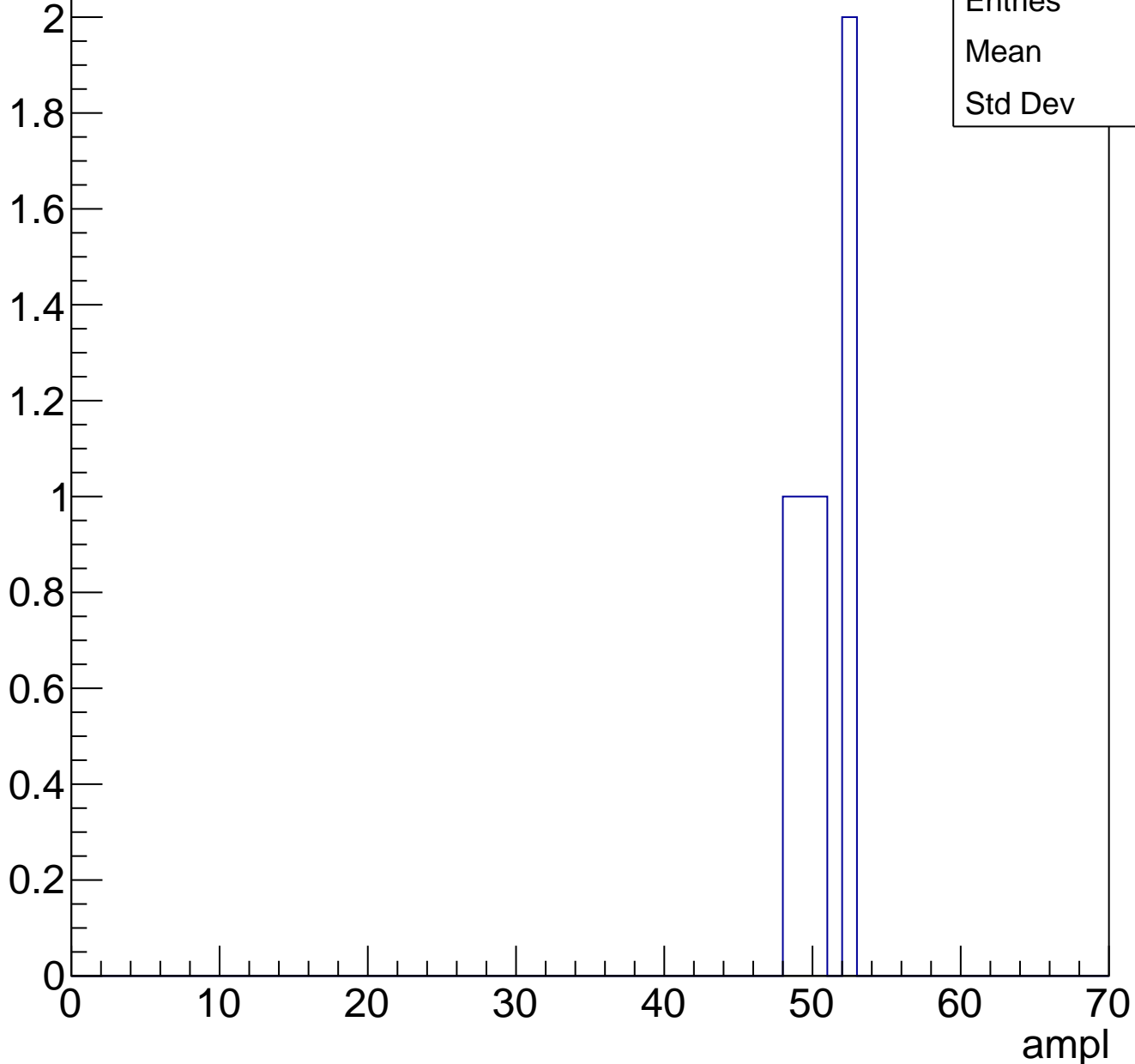


| Entries | 5 |
| Mean | 50.2 |
| Std Dev | 1.6 |

Entry

ampl

# B0L100S, U1-ch17, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch17, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch17, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch17, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch18, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 41.67 |
| Std Dev | 0.9428 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
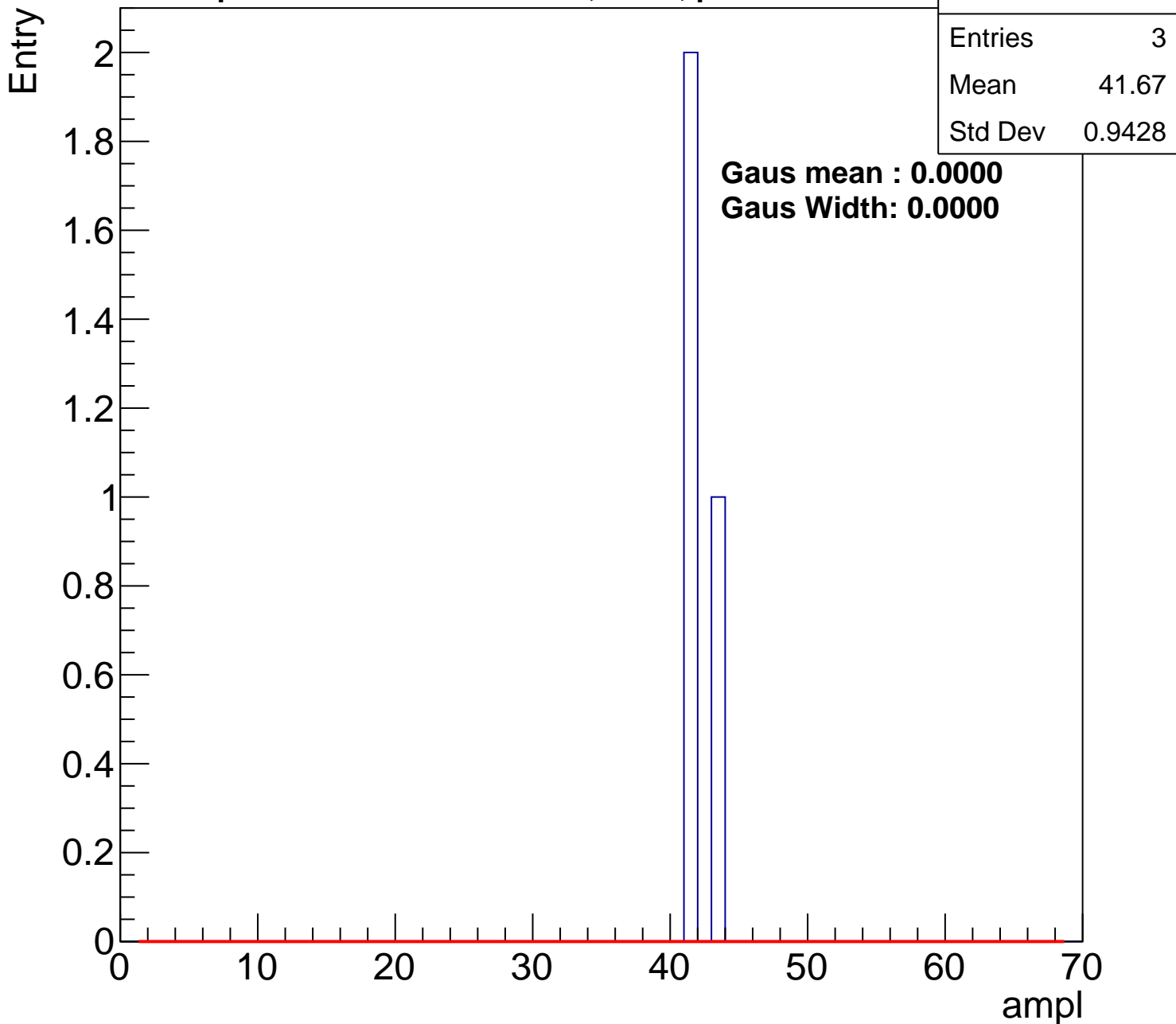
# B0L100S, U1-ch18, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 14 |
|---|---|
| Mean | 43.36 |
| Std Dev | 2.409 |

**Gaus mean : -27.0857**
**Gaus Width: 20.0592**

Entry

ampl
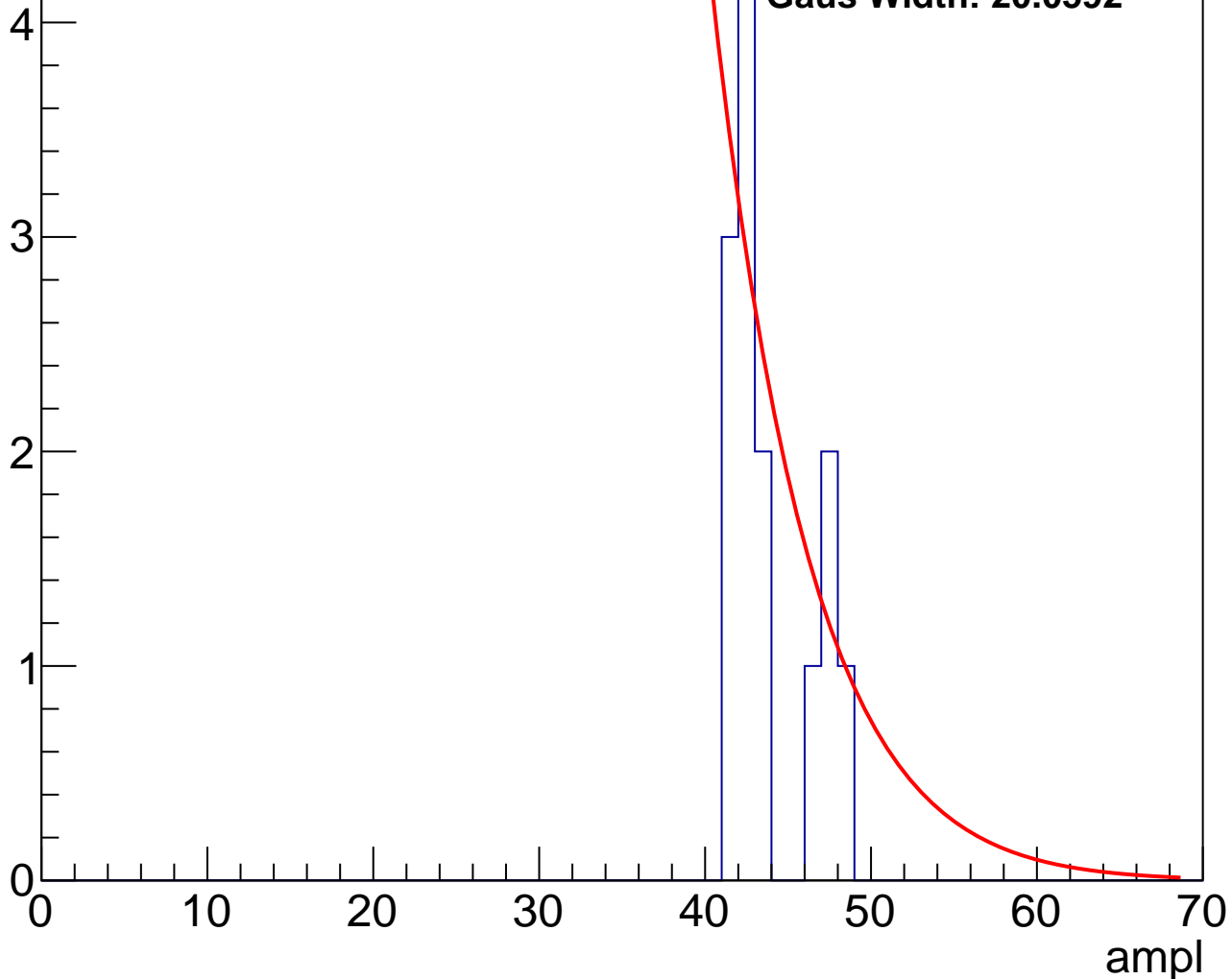
# B0L100S, U1-ch18, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 12 |
| Mean | 47.42 |
| Std Dev | 1.441 |

**Gaus mean : 7.7675**
**Gaus Width: 10.9708**
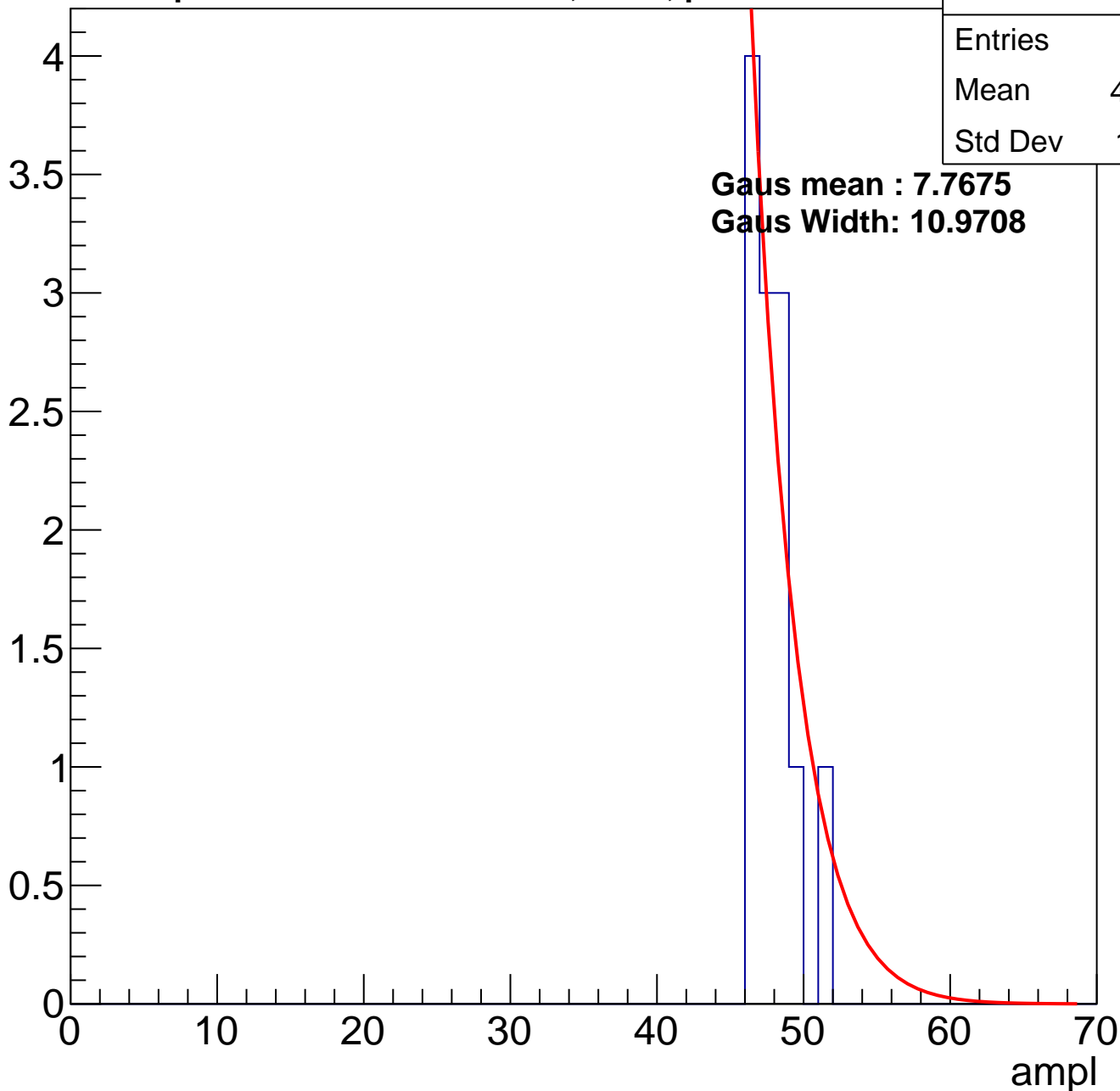
# B0L100S, U1-ch18, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch18, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch18, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch18, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch18, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch19, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch19, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch19, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
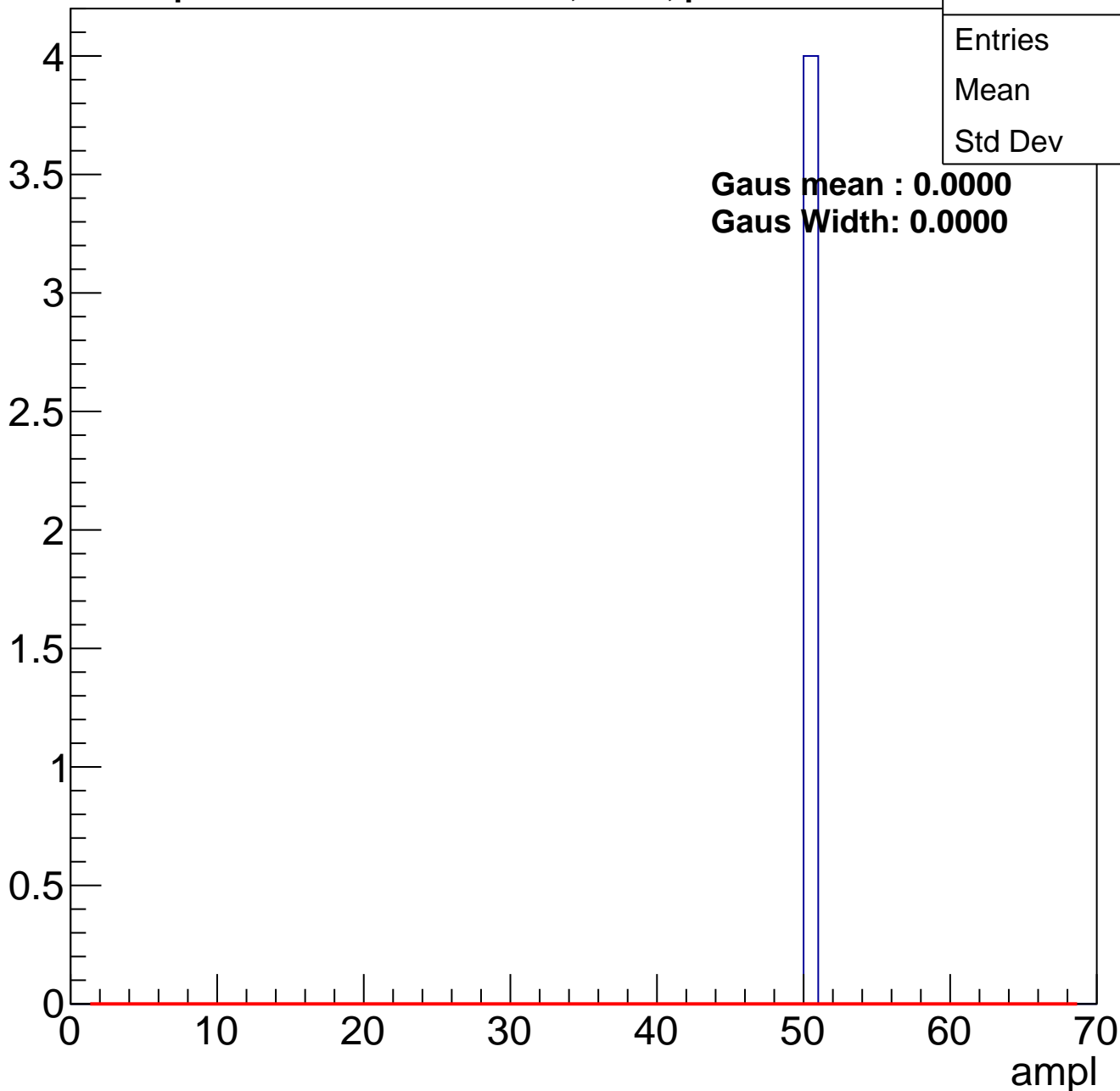
| | |
|---|---|
| Entries | 4 |
| Mean | 50 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch19, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch19, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch19, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch19, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch19, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch20, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch20, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 6 |
| Mean | 48.33 |
| Std Dev | 1.795 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
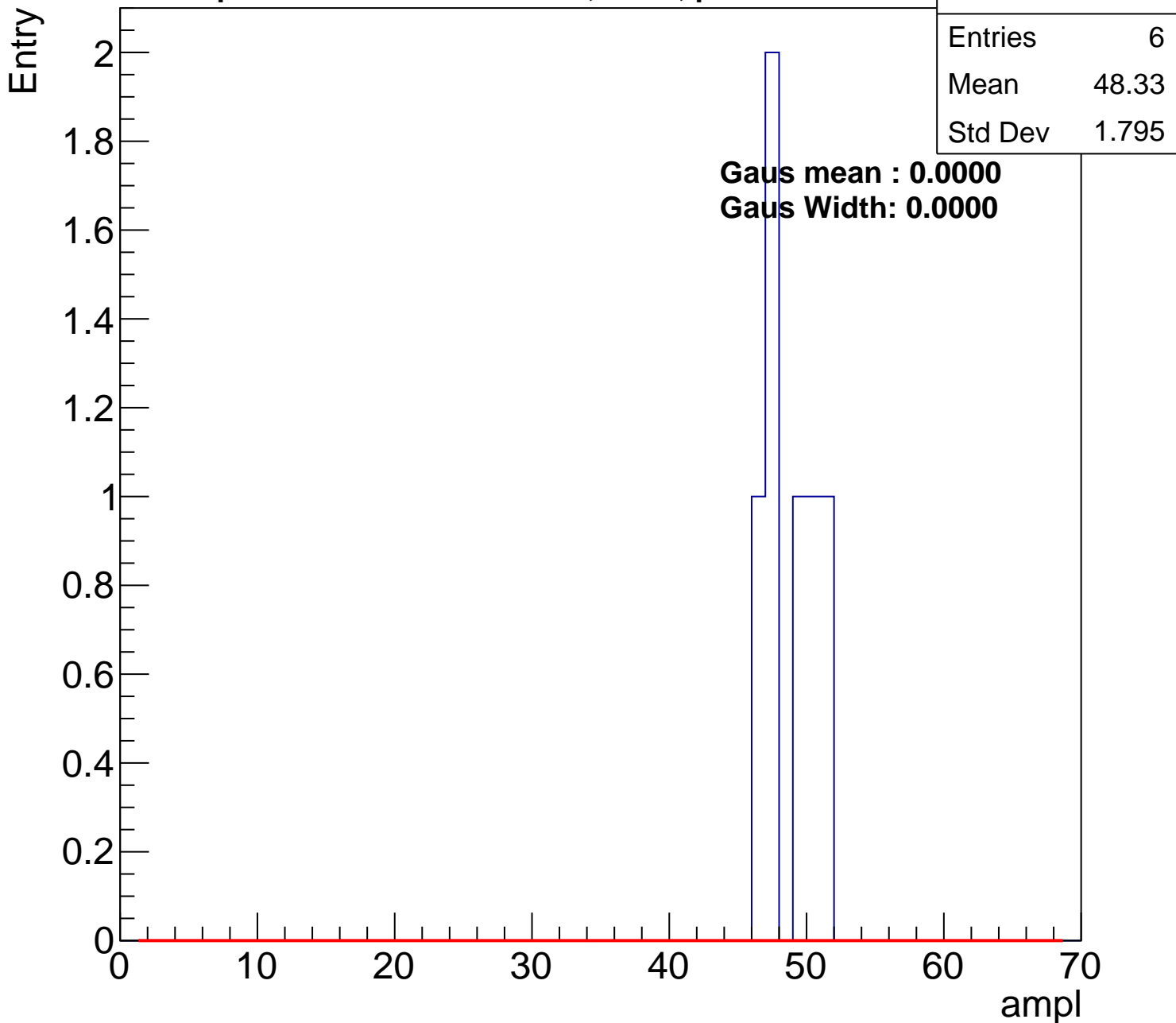
# B0L100S, U1-ch20, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 17 |
| Mean | 50.06 |
| Std Dev | 2.071 |

**Gaus mean : 51.8006**
**Gaus Width: 3.4103**
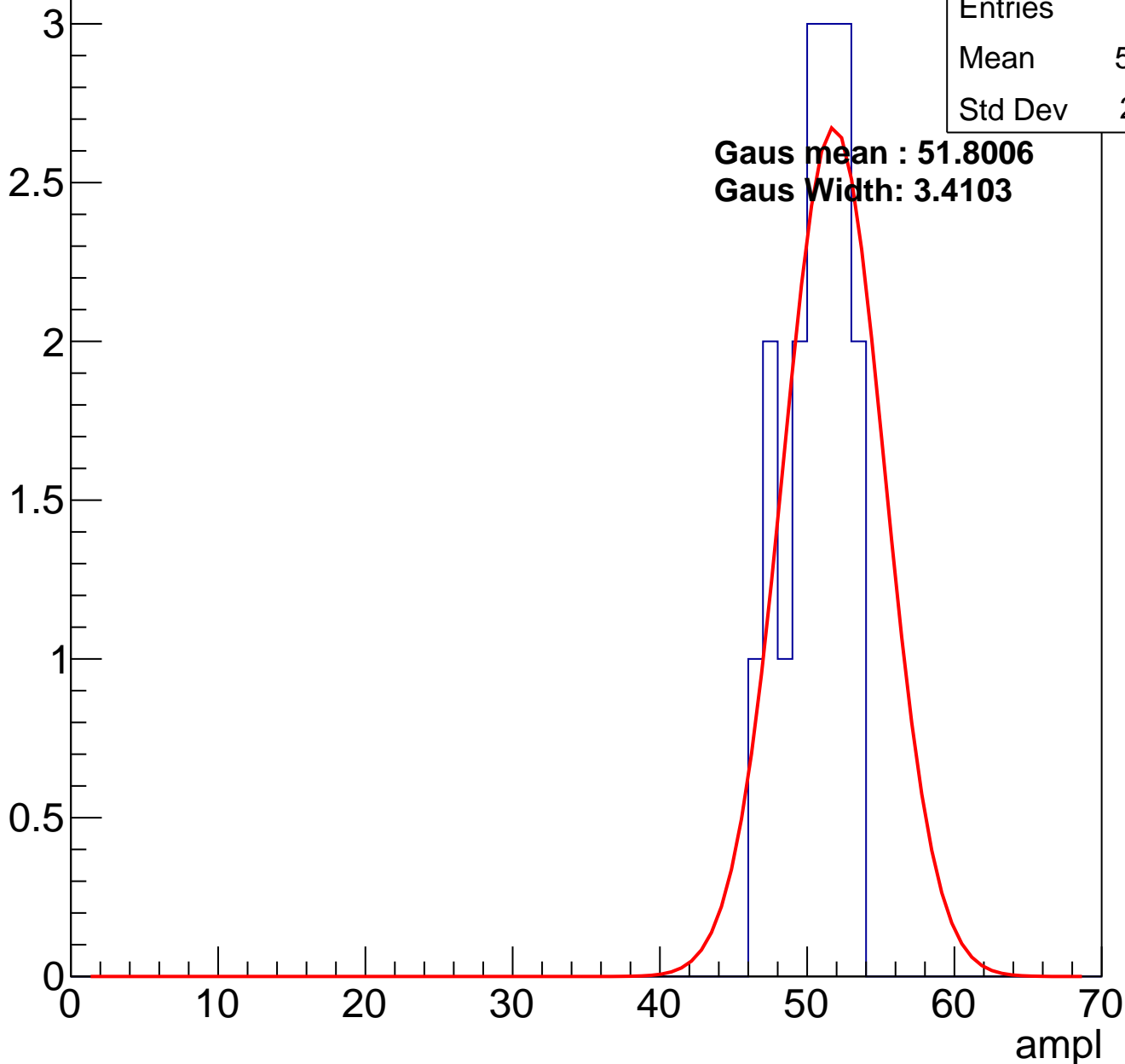
# B0L100S, U1-ch20, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 53 |
| Std Dev | 0 |

Entry

ampl
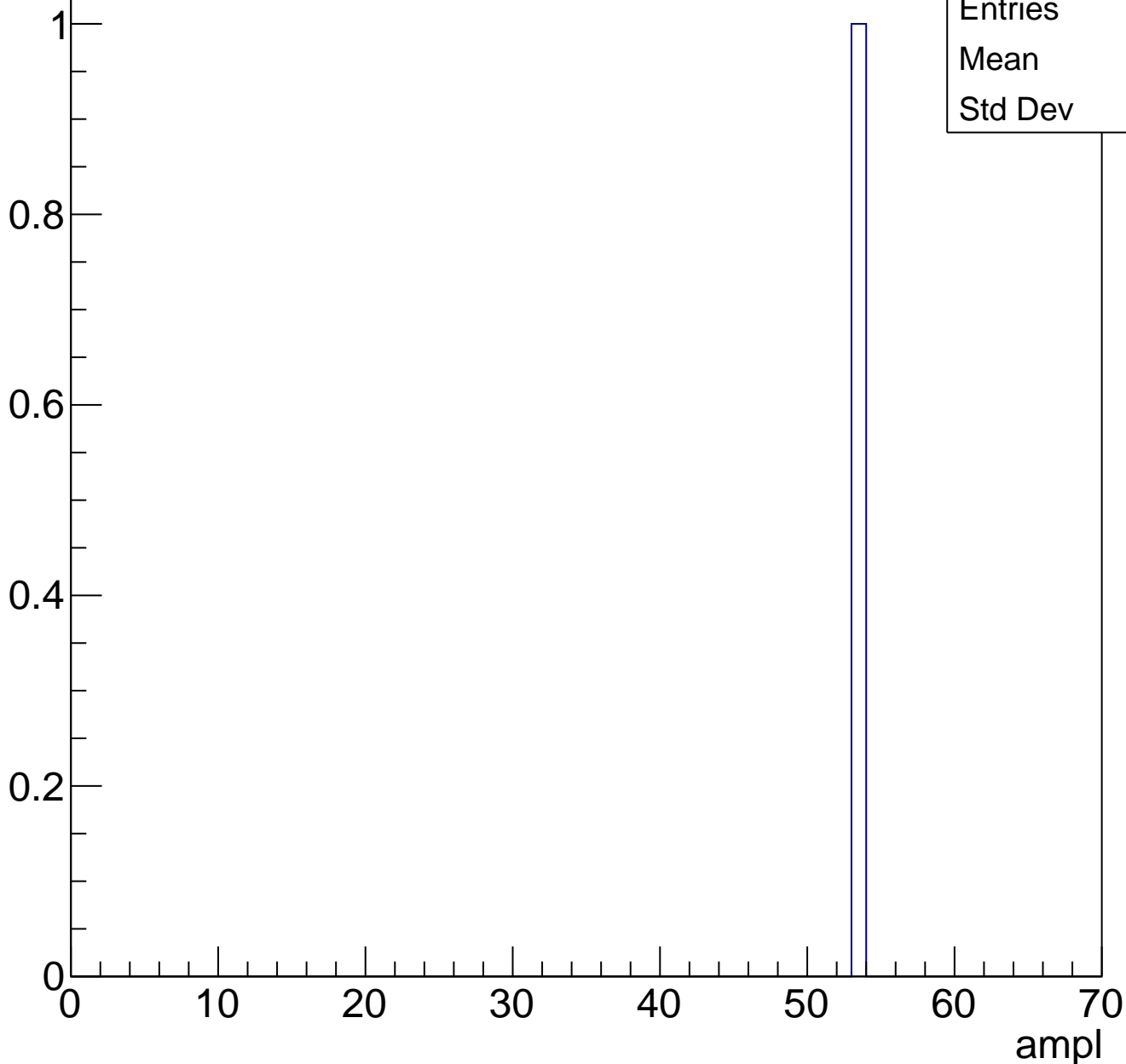
# B0L100S, U1-ch20, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch20, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch20, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

ampl

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch20, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch21, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch21, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 49 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
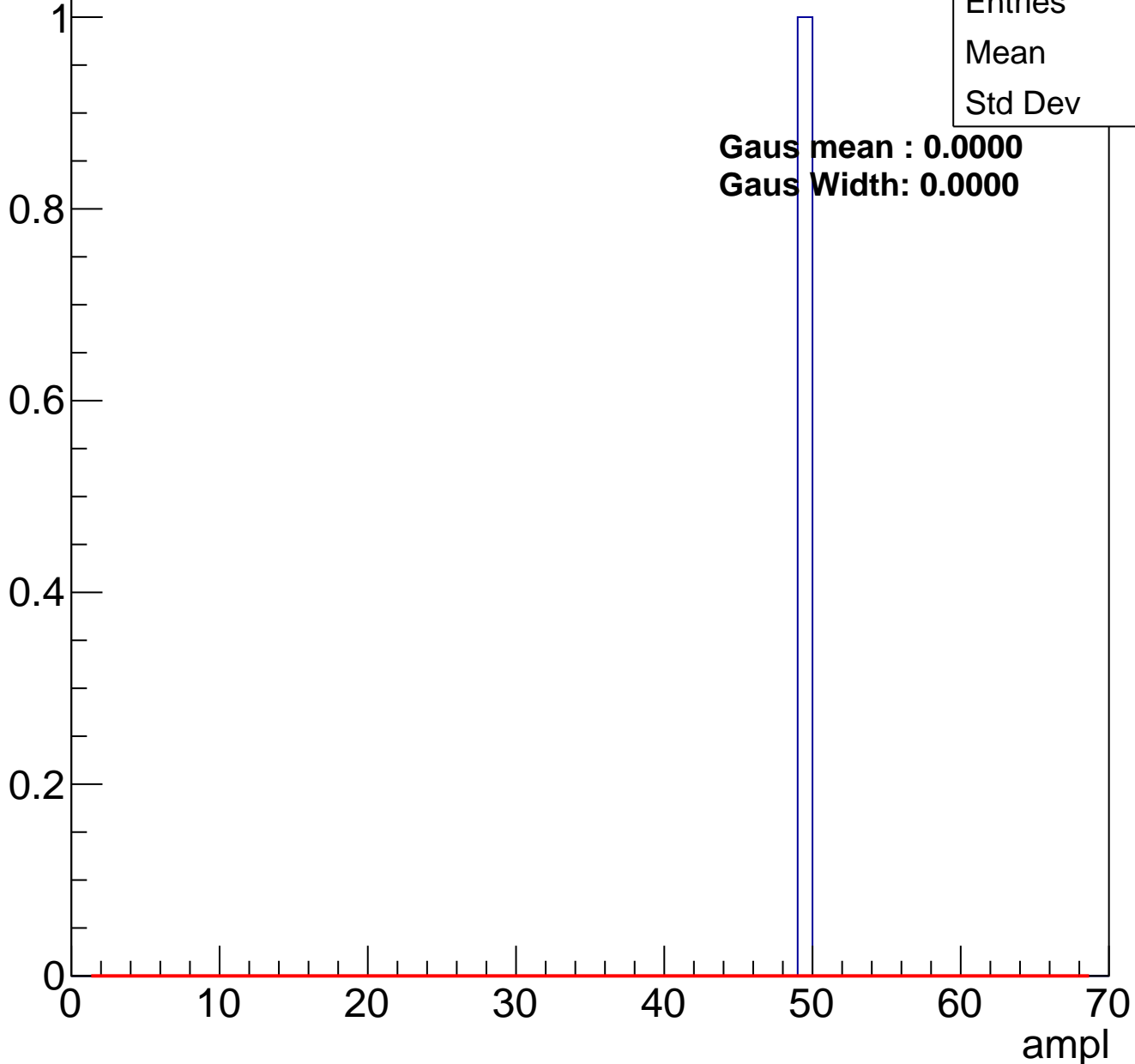
B0L100S, U1-ch21, adc2

calib_packv5_042523_0143.root, FC#6, port A1
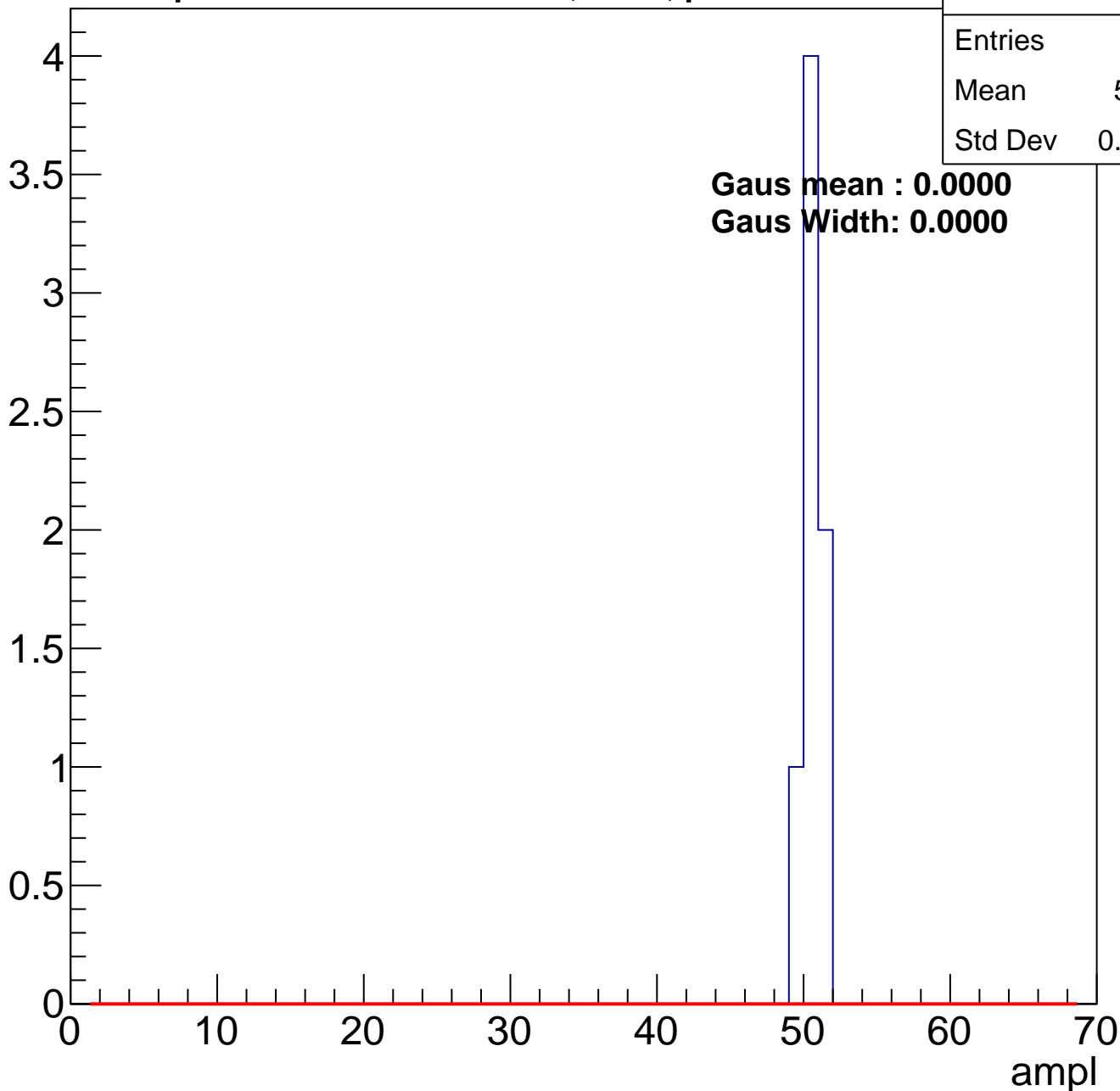
| Entries | 7 |
| Mean | 50.14 |
| Std Dev | 0.6389 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

# B0L100S, U1-ch21, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch21, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch21, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch21, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch21, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch22, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 43 |
| Std Dev | 1 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
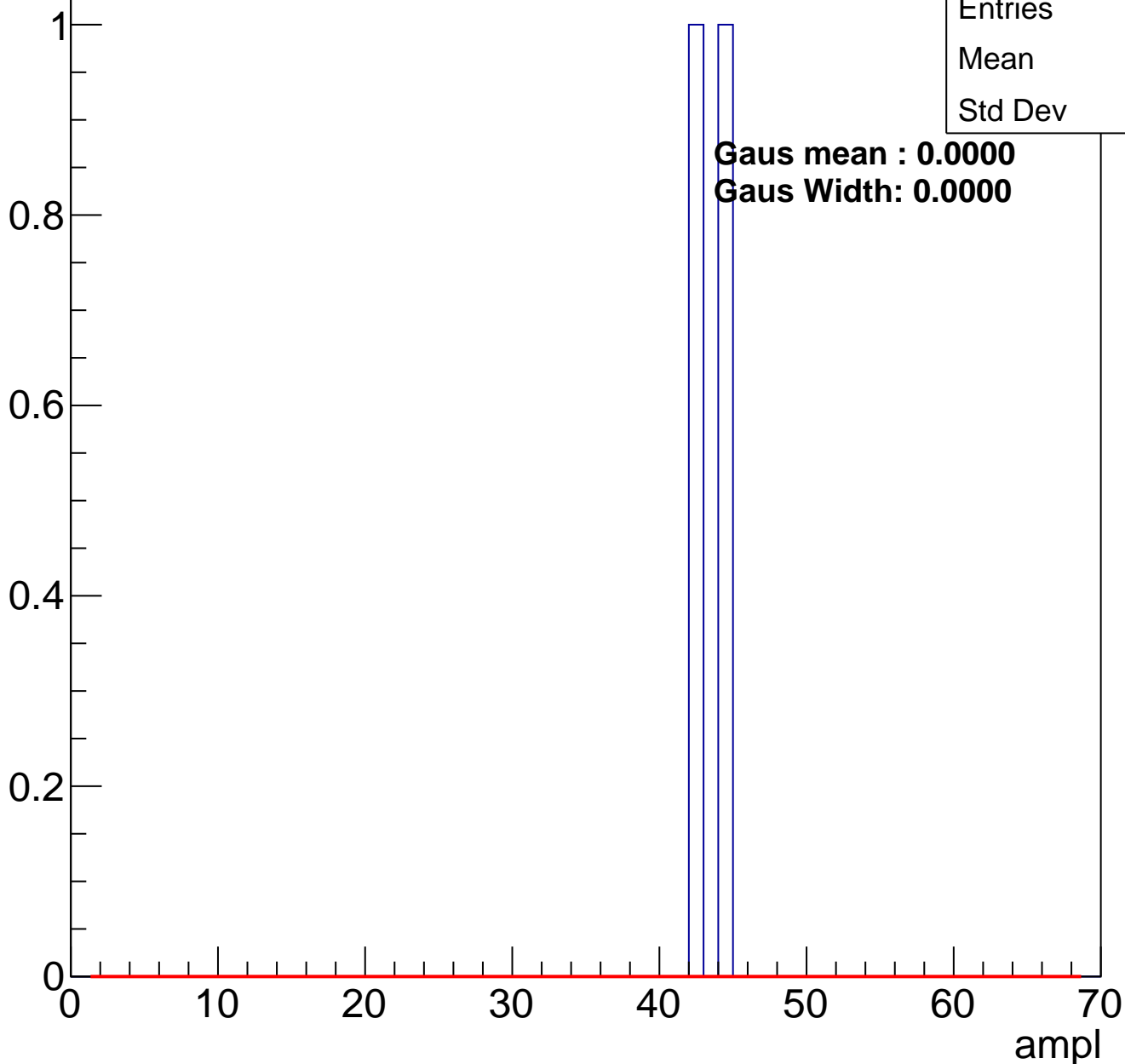
# B0L100S, U1-ch22, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 19 |
| Mean | 44.05 |
| Std Dev | 1.468 |

**Gaus mean : 45.1921**
**Gaus Width: 2.2000**

Entry

ampl
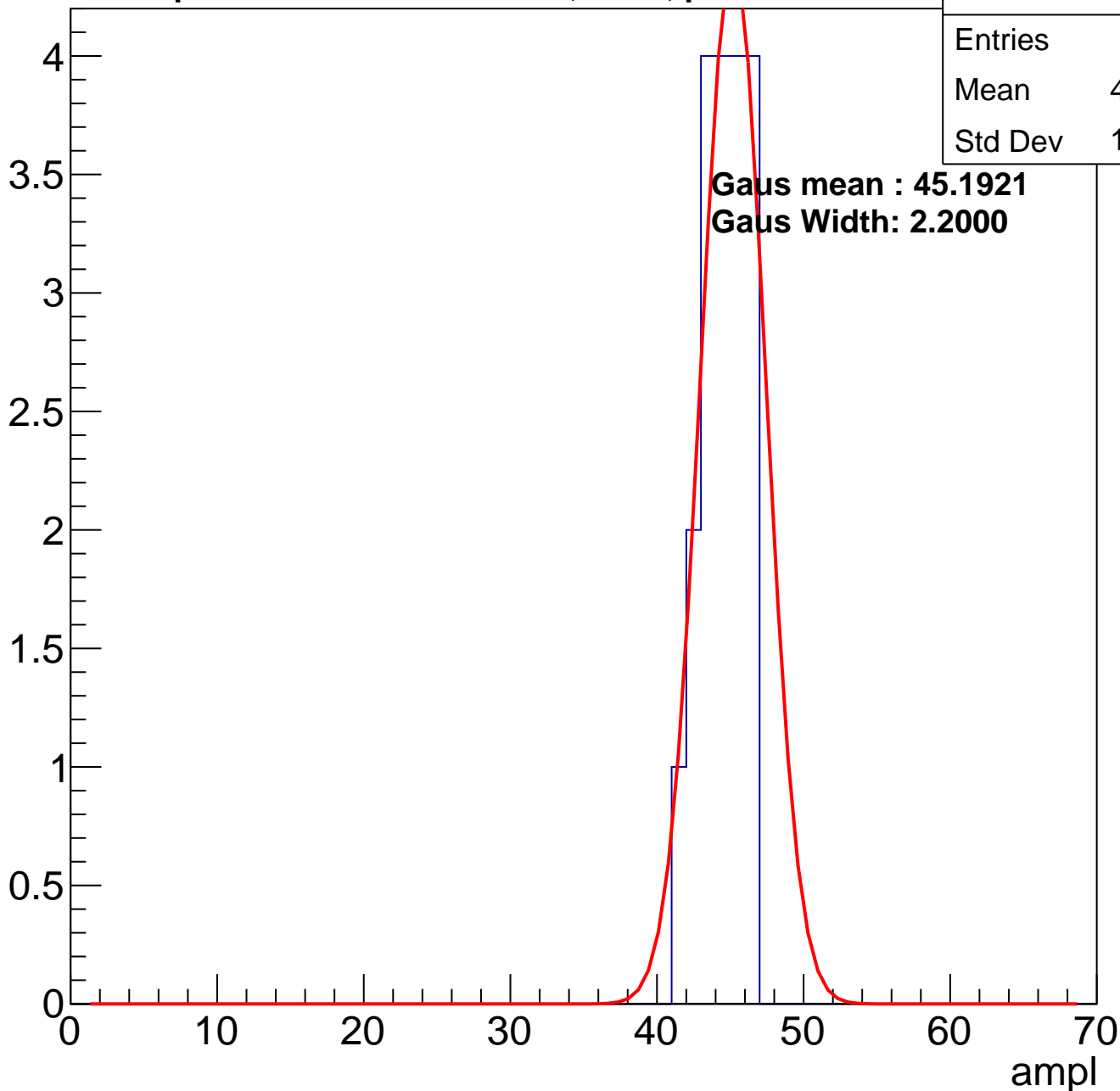
B0L100S, U1-ch22, adc2

calib_packv5_042523_0143.root, FC#6, port A1
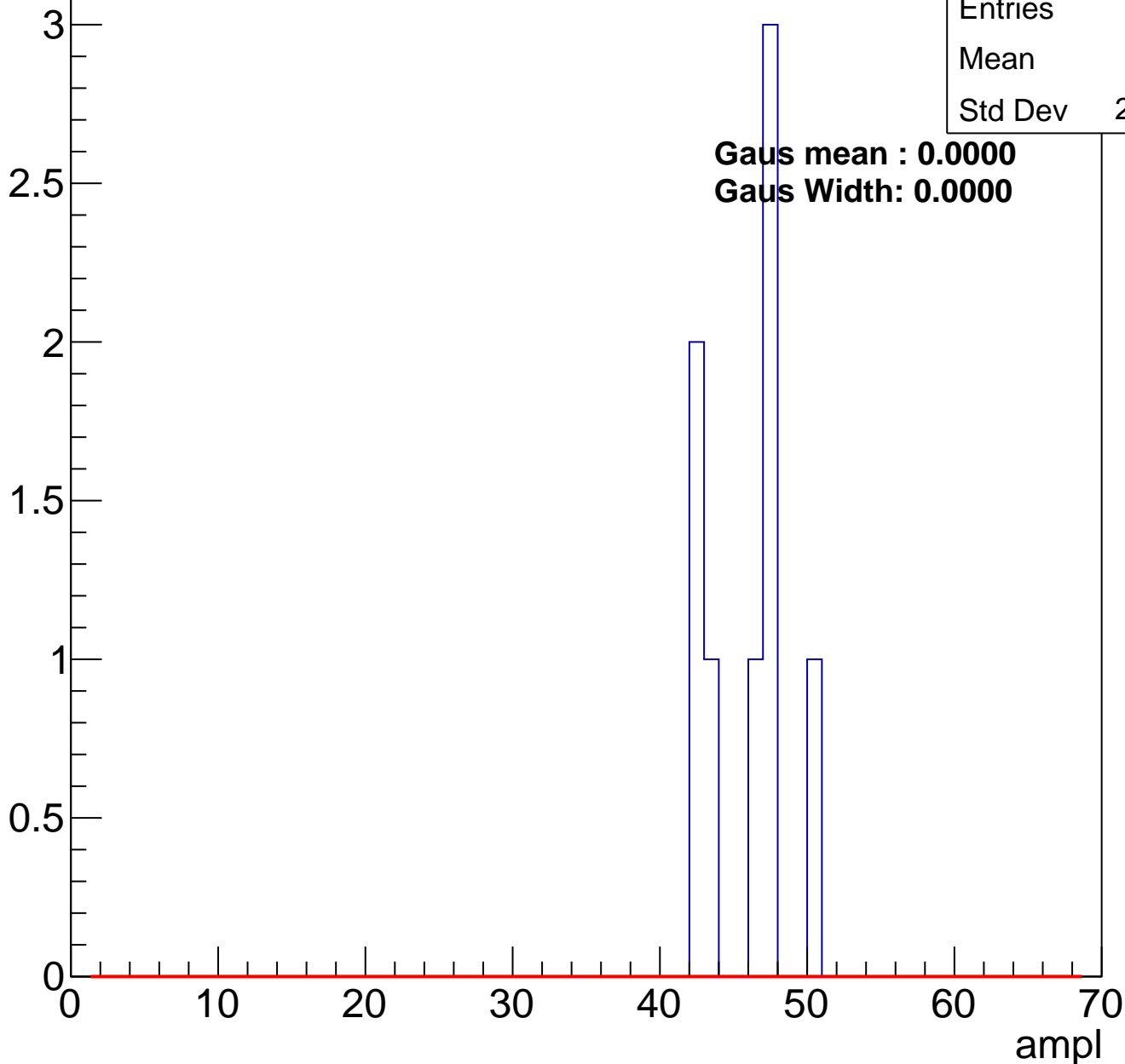
| Entries | 8 |
| Mean | 45.5 |
| Std Dev | 2.693 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch22, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 50 |
| Std Dev | 0 |

Entry

ampl
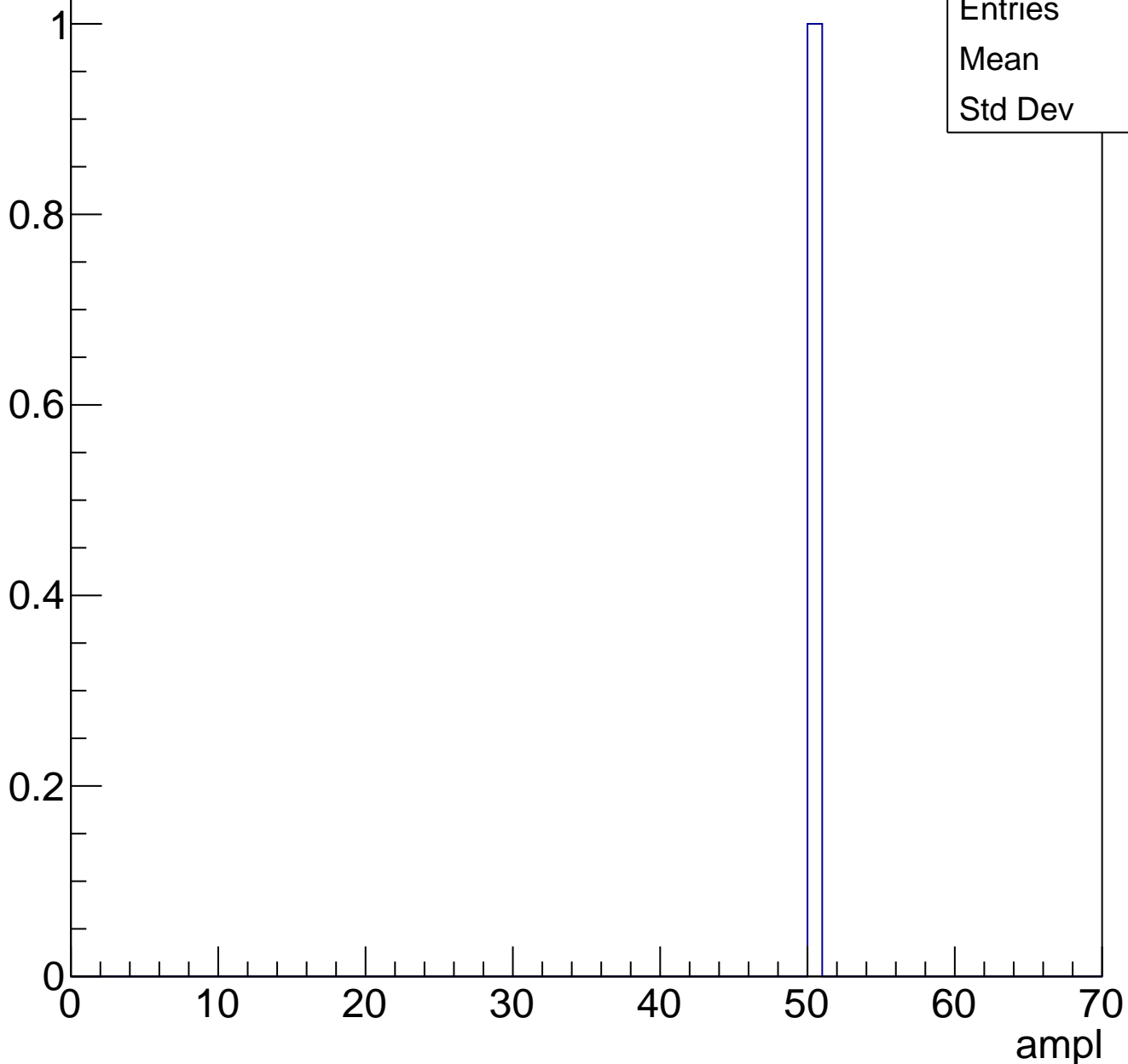
# B0L100S, U1-ch22, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch22, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch22, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch22, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch23, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch23, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 46 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch23, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
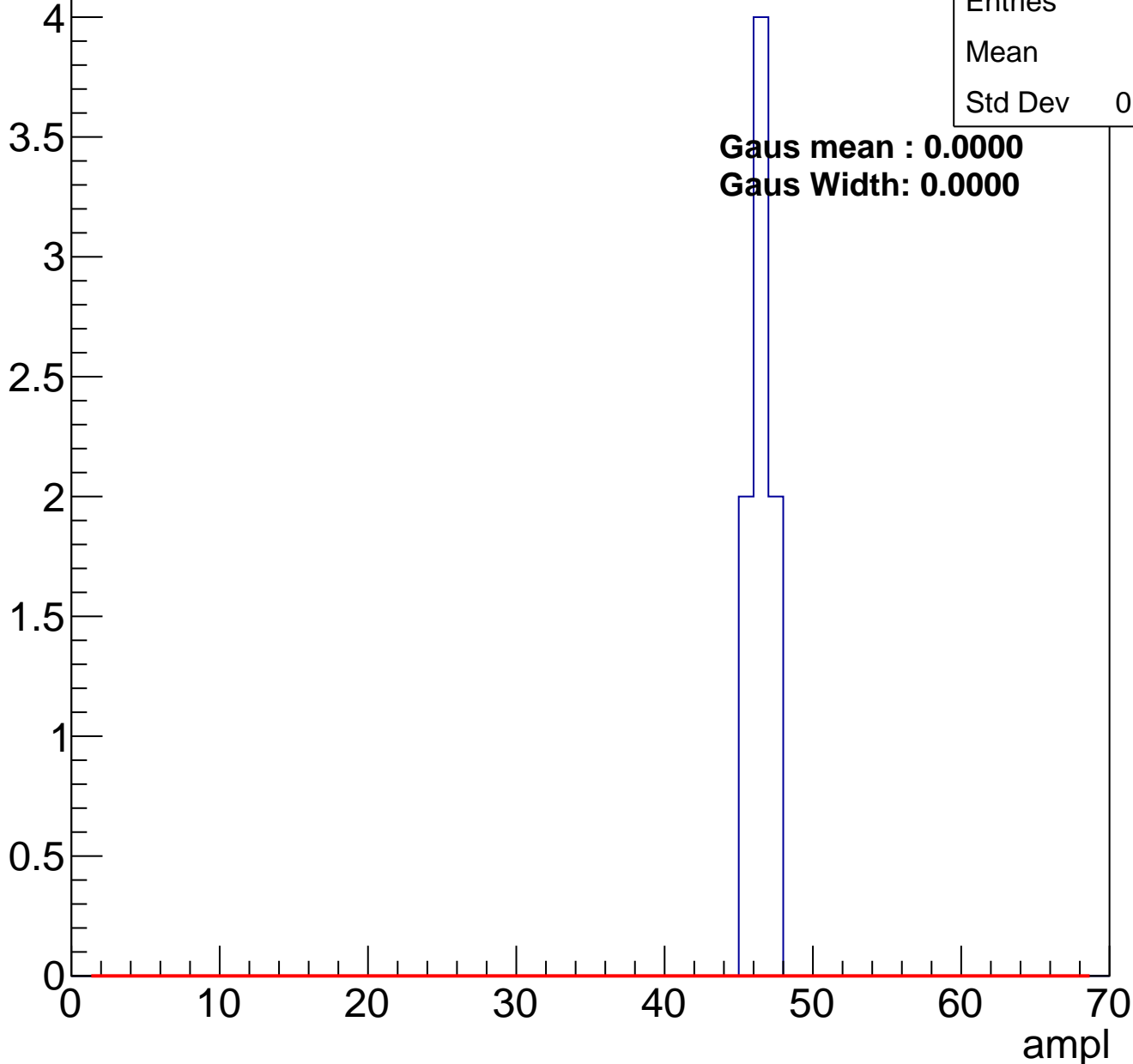
| Entries | 8 |
|---------|------|
| Mean | 46 |
| Std Dev | 0.7071 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch23, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch23, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch23, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch23, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch23, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch24, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**
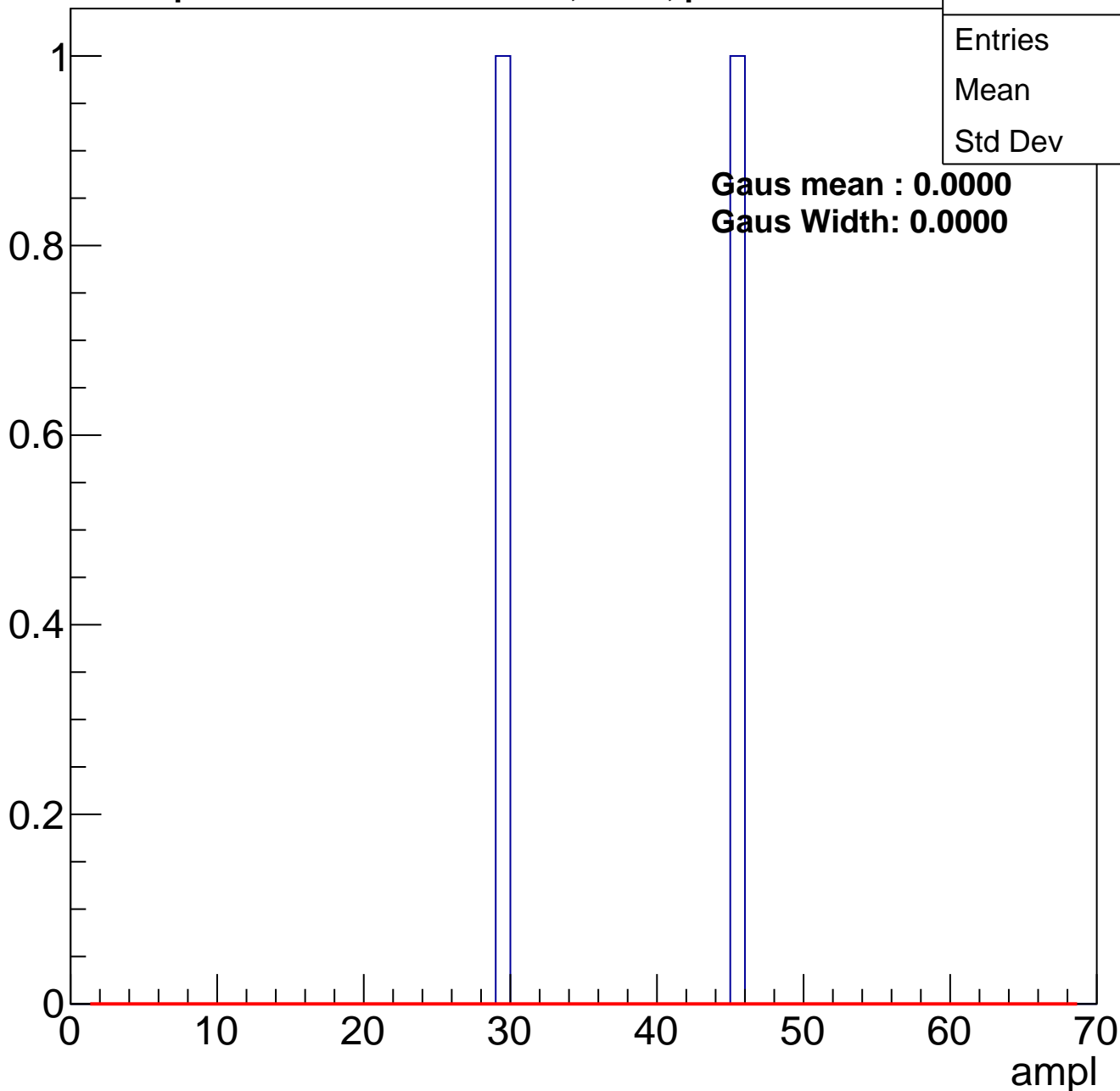
| | |
|---|---|
| Entries | 2 |
| Mean | 37 |
| Std Dev | 8 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch24, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**
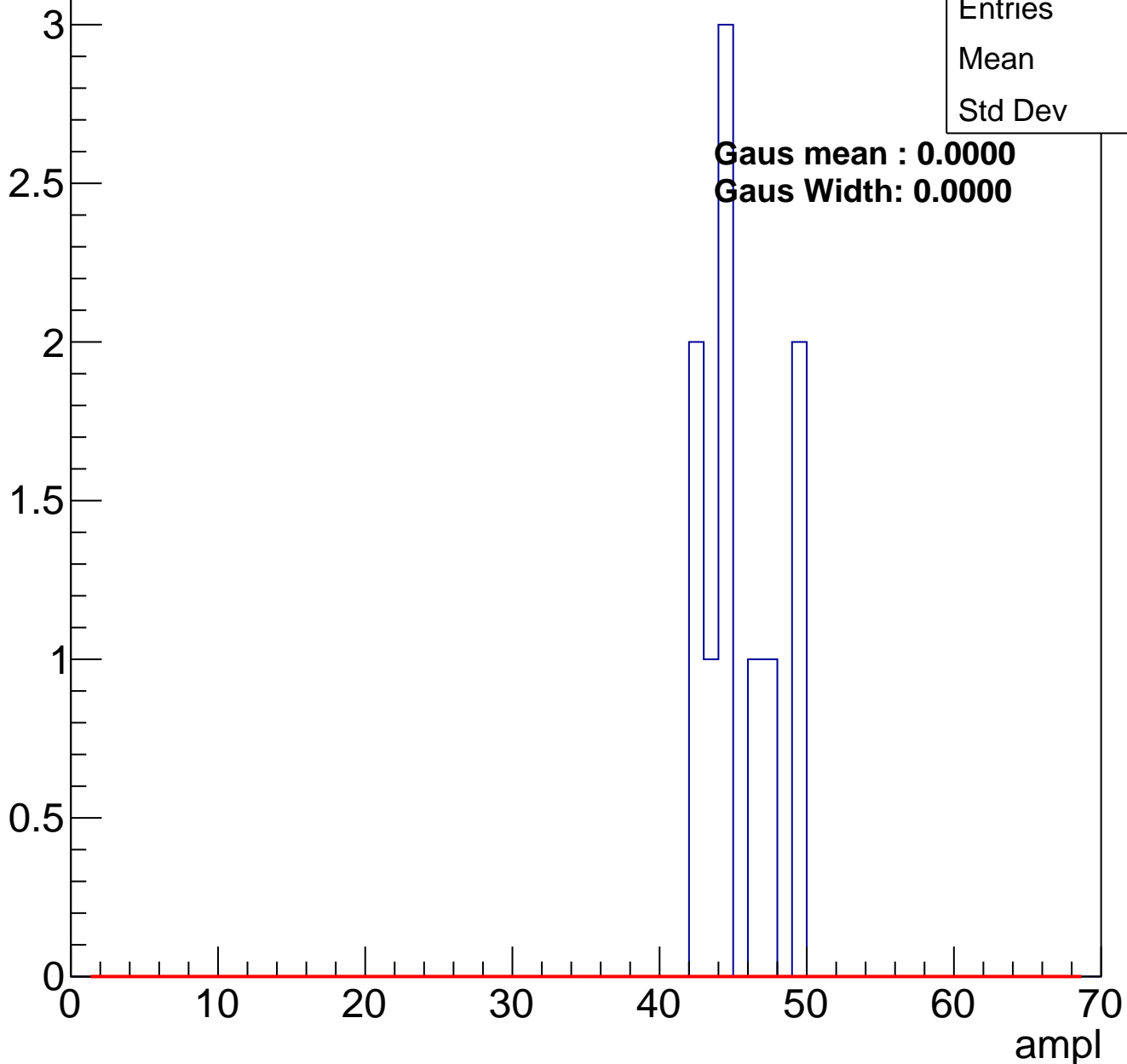
| Entries | 10 |
|---|---|
| Mean | 45 |
| Std Dev | 2.49 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch24, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 20 |
| Mean | 45.15 |
| Std Dev | 2.414 |

**Gaus mean : -1.9614**
**Gaus Width: 24.1401**
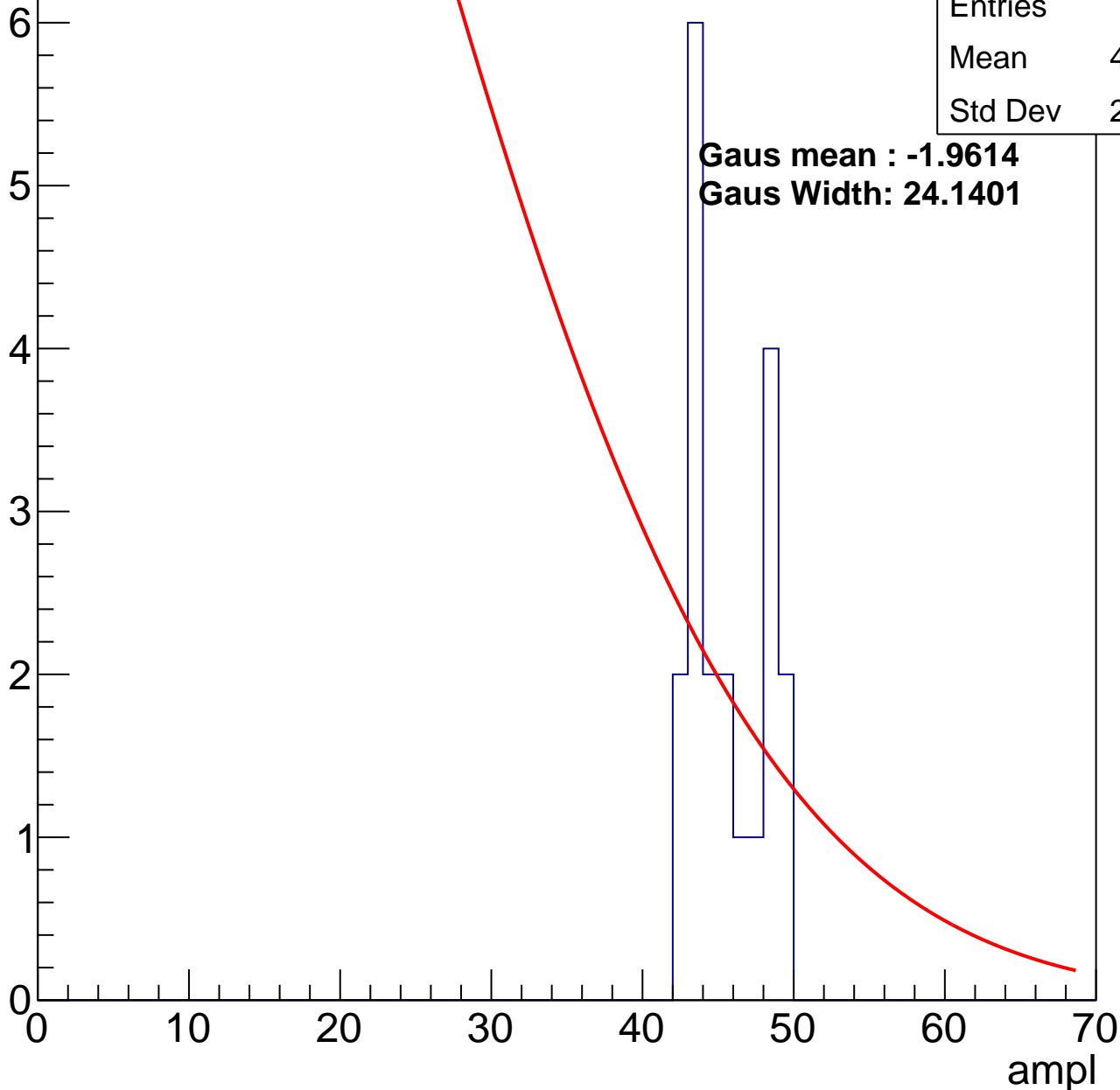
# B0L100S, U1-ch24, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 4 |
| Mean | 48 |
| Std Dev | 1.871 |
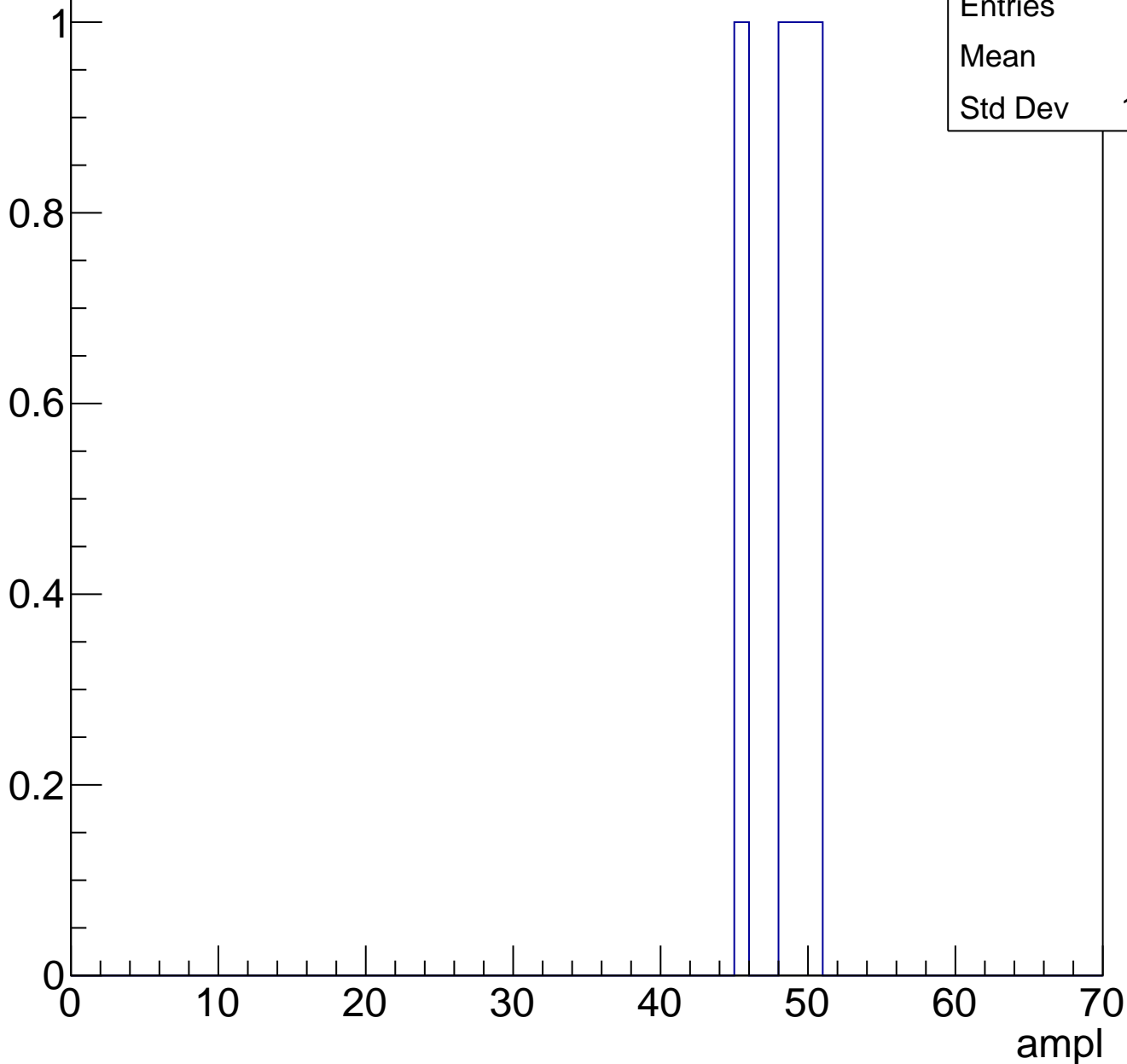
# B0L100S, U1-ch24, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch24, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

B0L100S, U1-ch24, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| | | |
|---|---|---|
| Entries | | 0 |
| Mean | | 0 |
| Std Dev | | 0 |

# B0L100S, U1-ch24, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch25, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch25, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

# B0L100S, U1-ch25, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch25, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch25, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch25, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch25, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch25, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch26, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch26, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

# B0L100S, U1-ch26, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch26, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch26, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch26, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch26, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch26, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch27, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch27, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 46.5 |
| Std Dev | 0.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
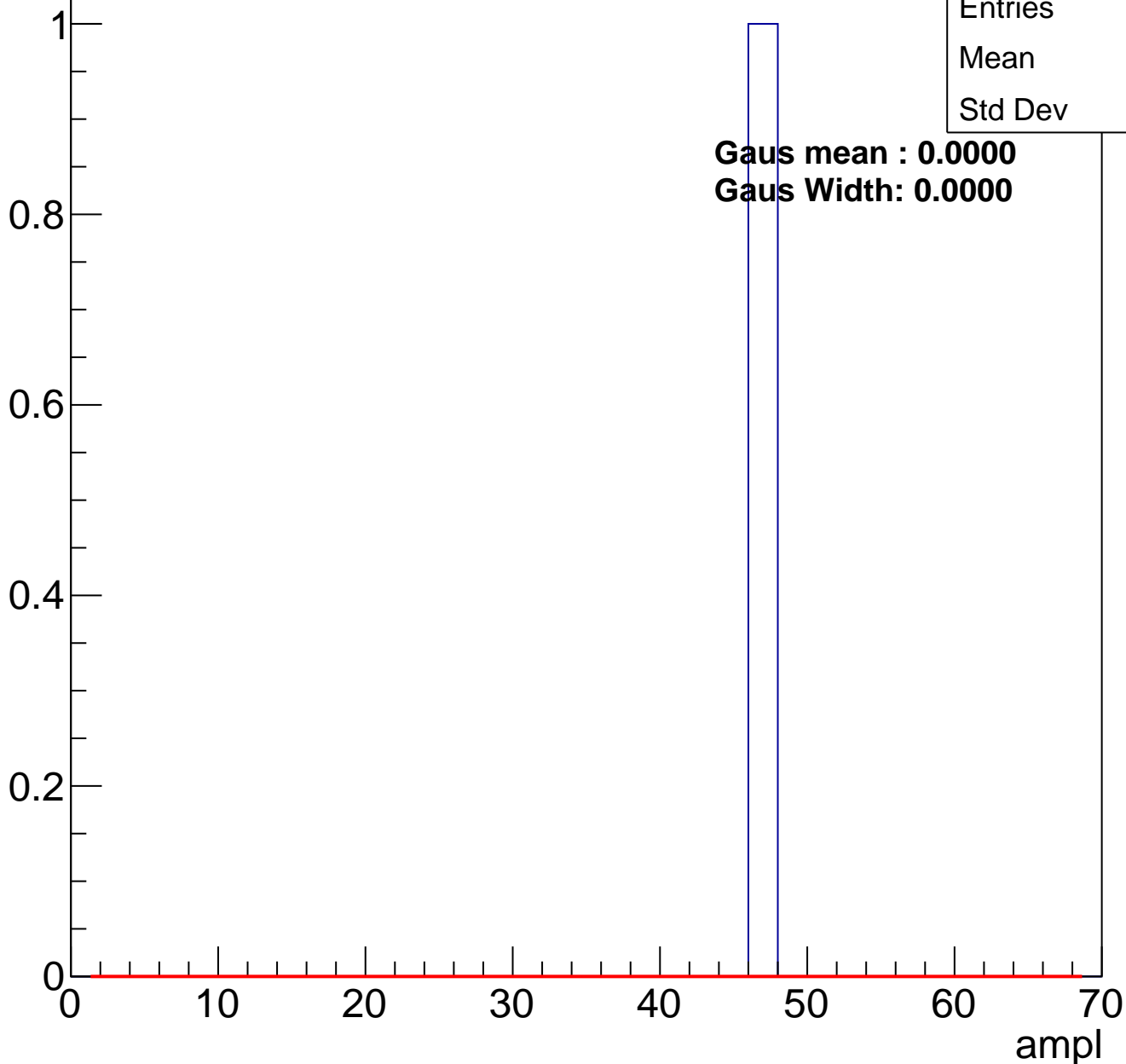
# B0L100S, U1-ch27, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch27, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch27, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch27, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch27, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch27, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch28, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch28, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**
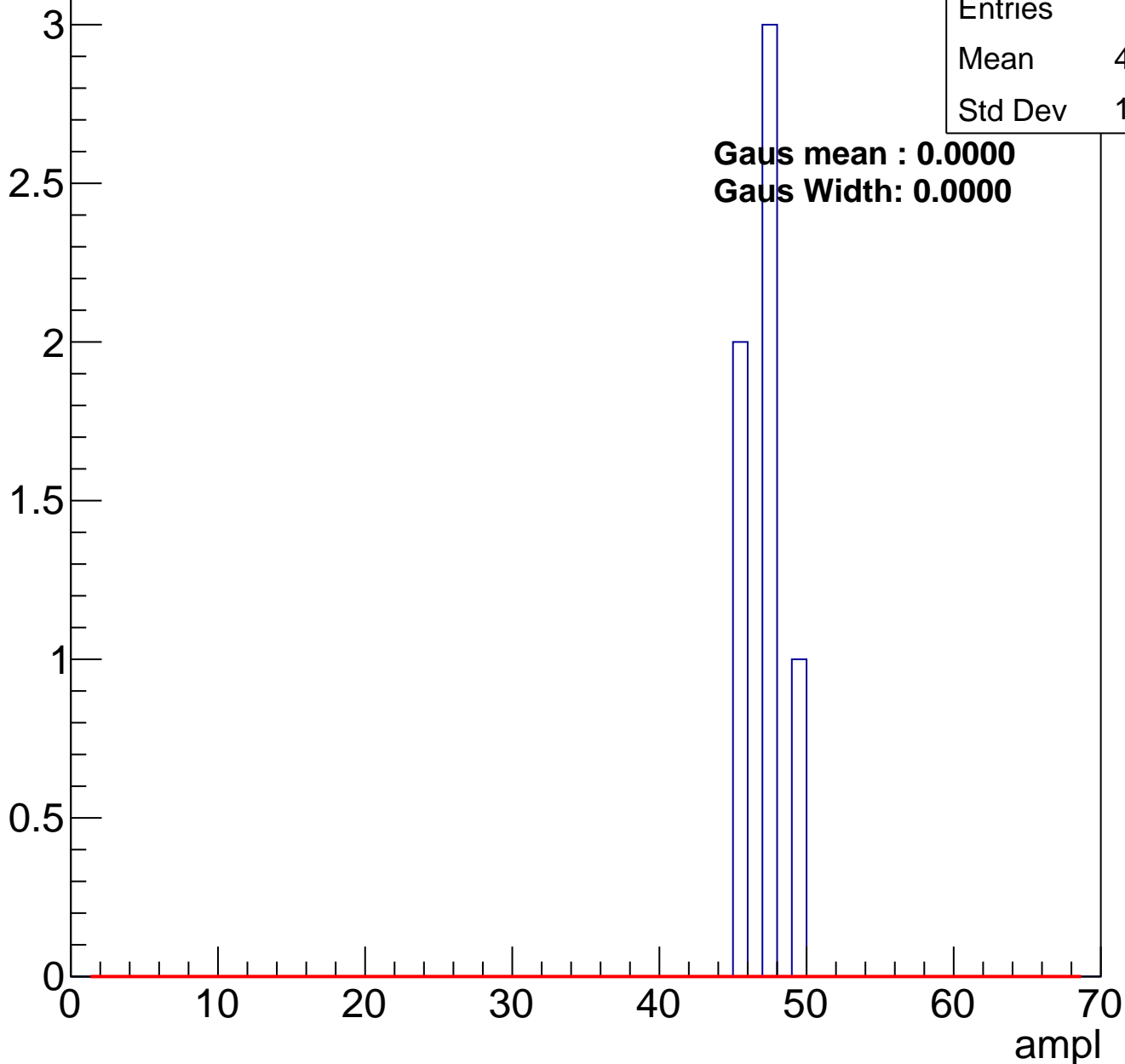
| Entries | 6 |
| Mean | 46.67 |
| Std Dev | 1.374 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch28, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 19 |
| Mean | 48.89 |
| Std Dev | 2.936 |

**Gaus mean : 50.9234**
**Gaus Width: 3.7611**

Entry

ampl

# B0L100S, U1-ch28, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 4 |
| Mean | 52.75 |
| Std Dev | 0.8292 |

Entry

ampl
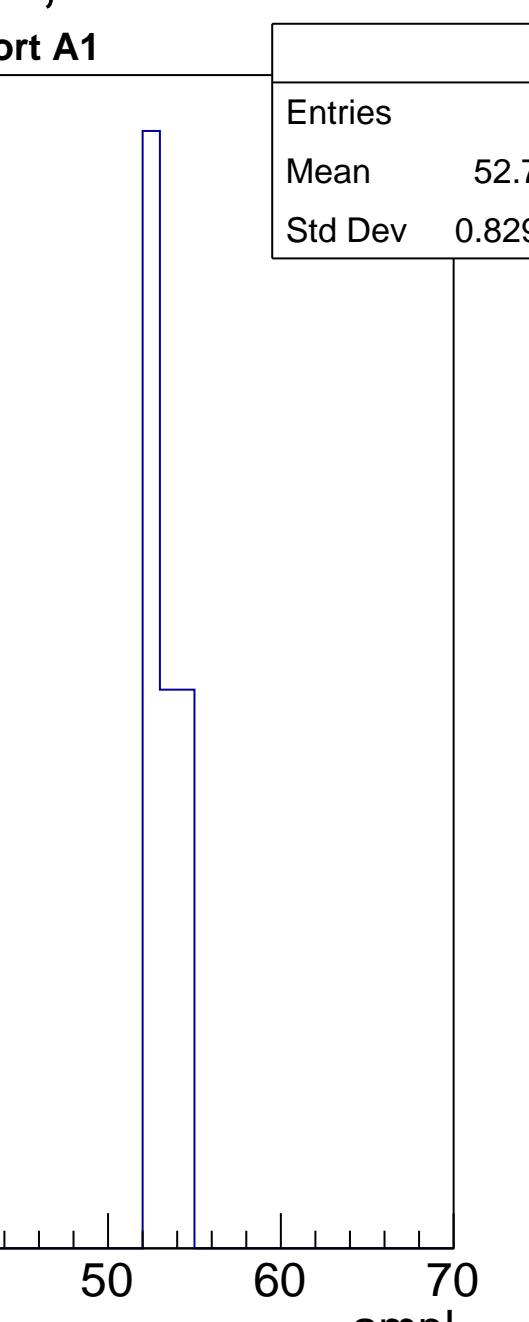
# B0L100S, U1-ch28, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**
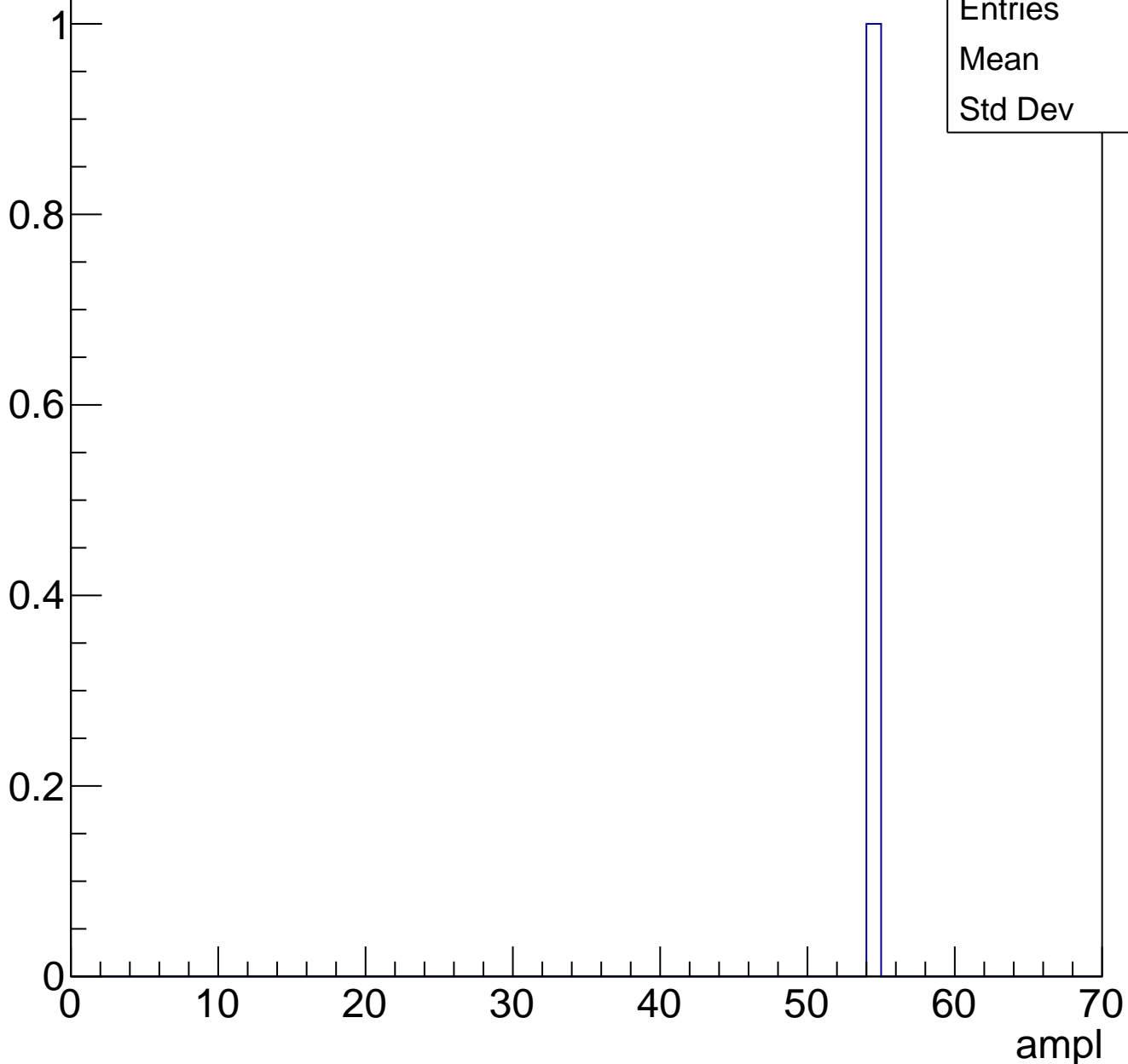
| | |
|---|---|
| Entries | 1 |
| Mean | 54 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch28, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch28, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch28, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch29, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch29, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 48 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
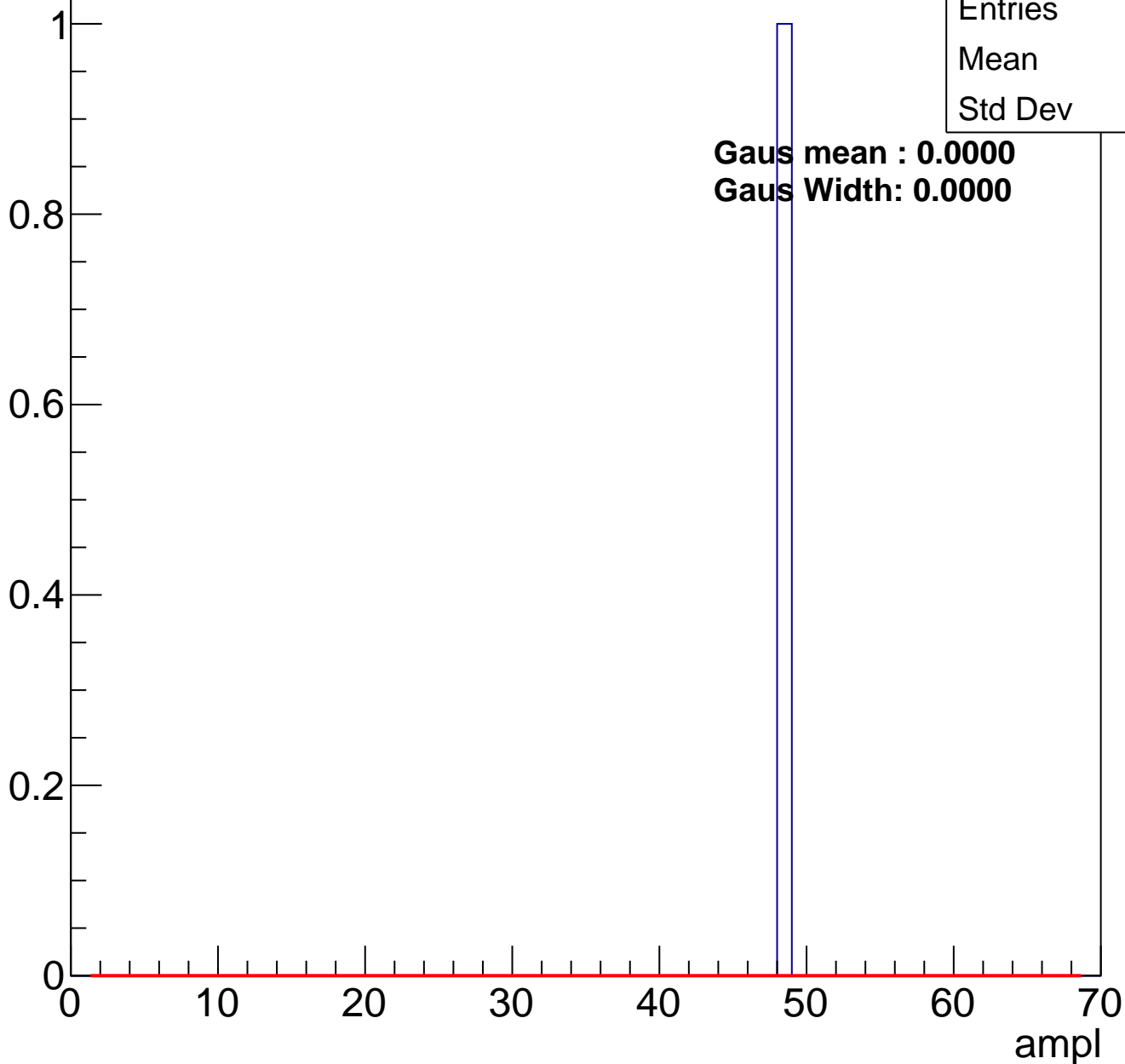
Entry

ampl

# B0L100S, U1-ch29, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 4 |
|---|---|
| Mean | 48.5 |
| Std Dev | 0.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
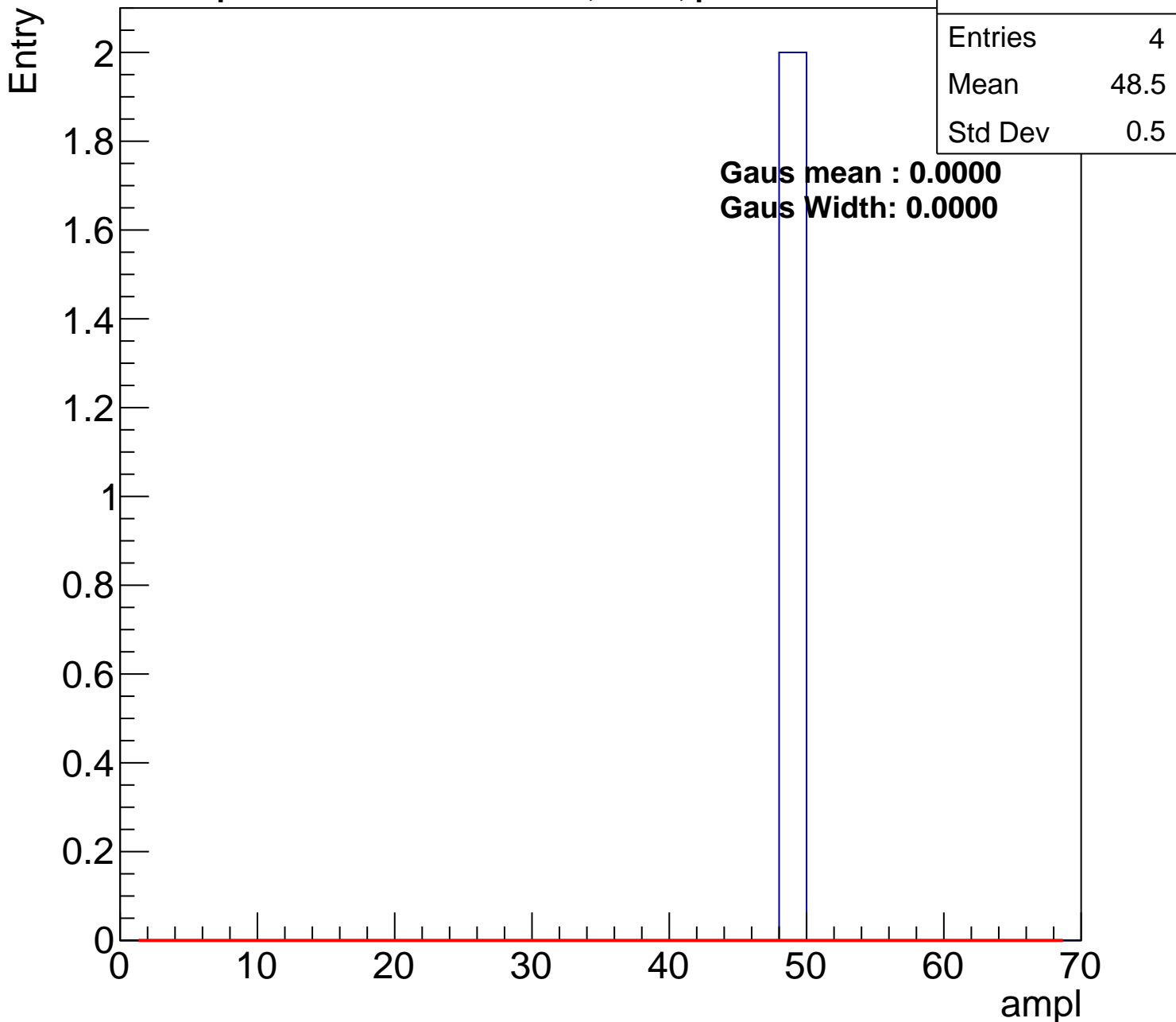
B0L100S, U1-ch29, adc3

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 1 |
| Mean | 48 |
| Std Dev | 0 |
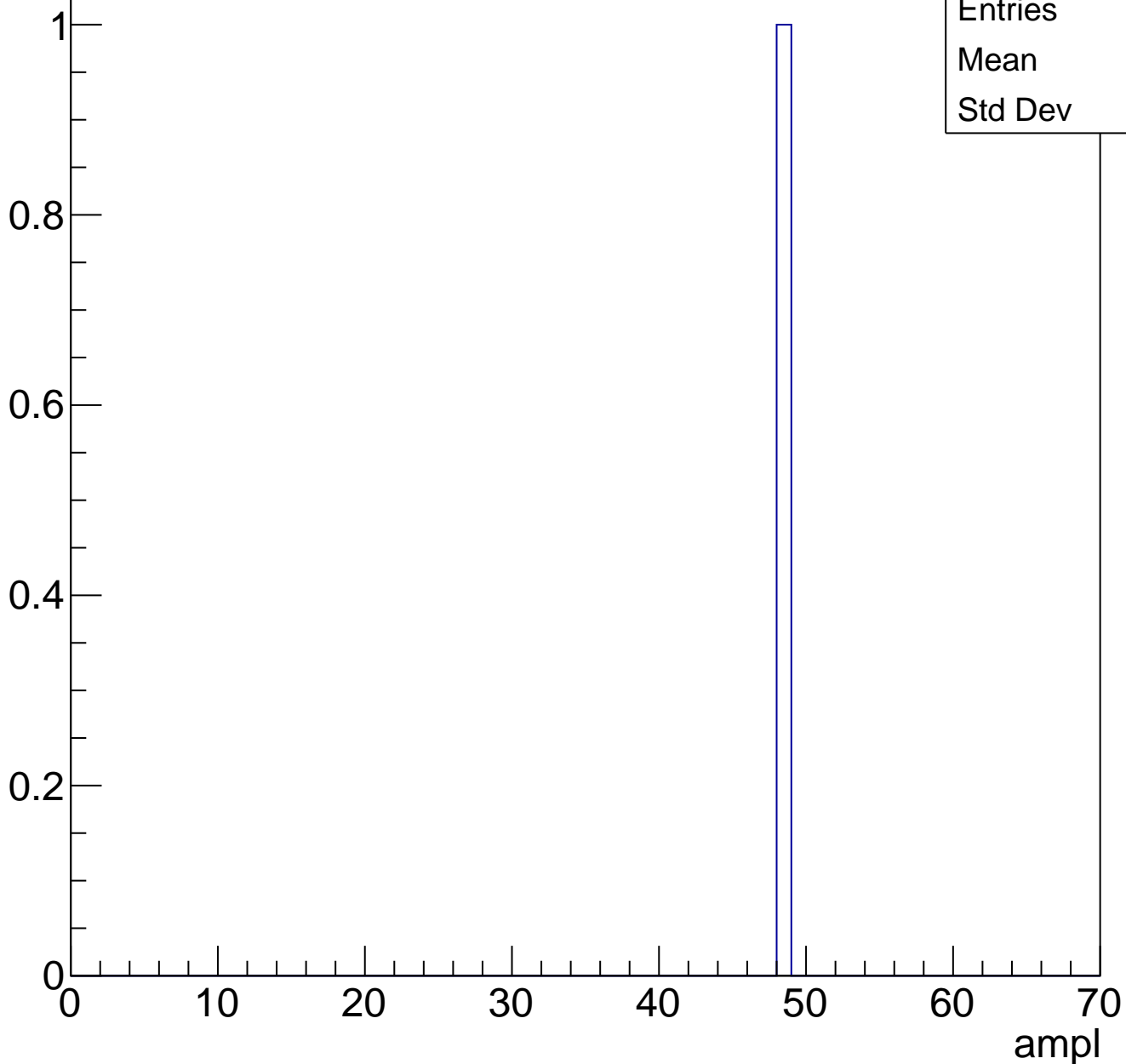
# B0L100S, U1-ch29, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch29, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch29, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch29, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch30, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch30, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 44.5 |
| Std Dev | 5.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
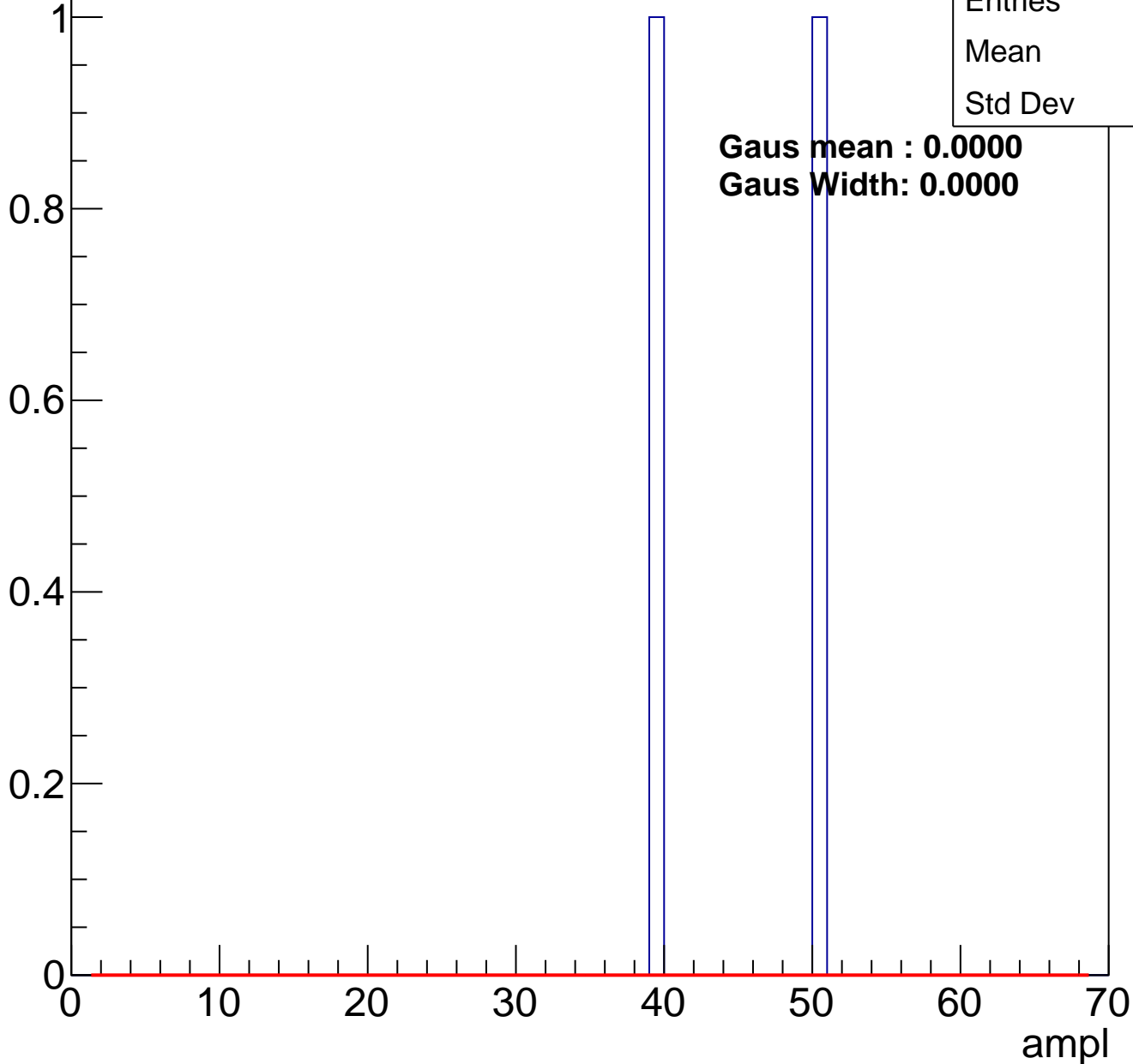
B0L100S, U1-ch30, adc2

calib_packv5_042523_0143.root, FC#6, port A1
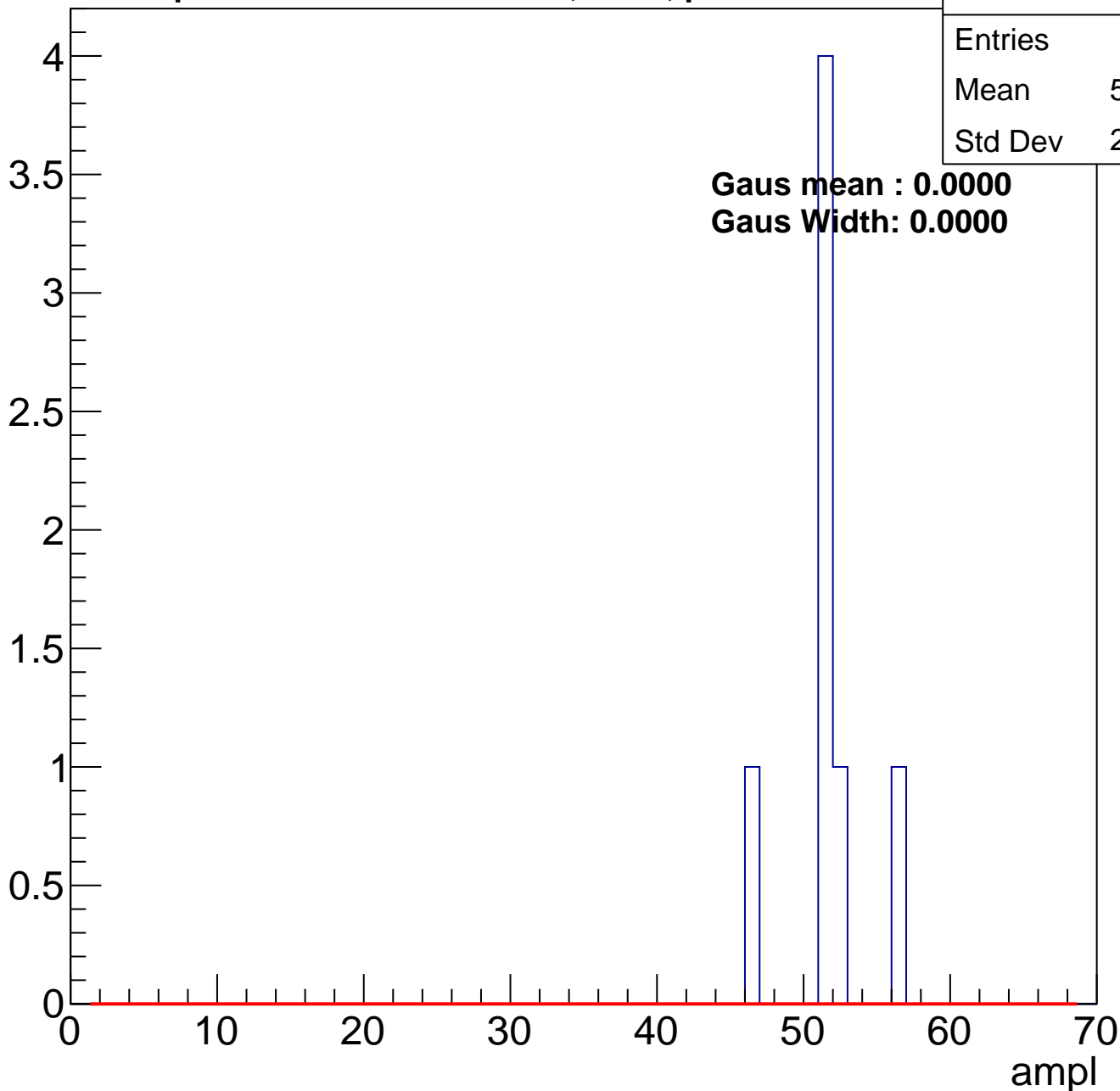
| Entries | 7 |
| Mean | 51.14 |
| Std Dev | 2.695 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch30, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 4 |
| Mean | 54.25 |
| Std Dev | 2.487 |

Entry

ampl
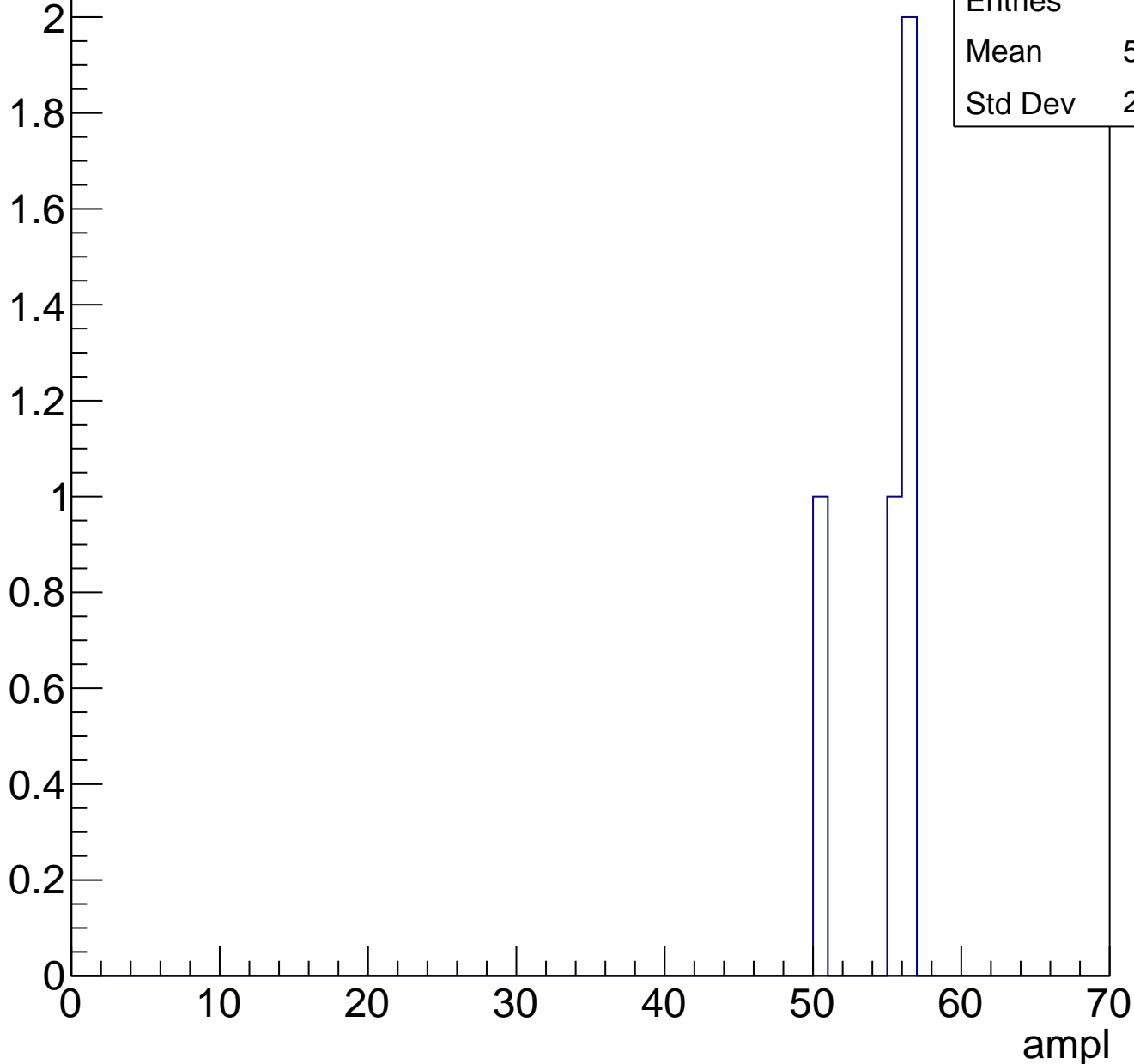
# B0L100S, U1-ch30, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch30, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch30, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch30, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch31, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch31, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch31, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch31, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch31, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch31, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

ampl

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch31, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch31, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |
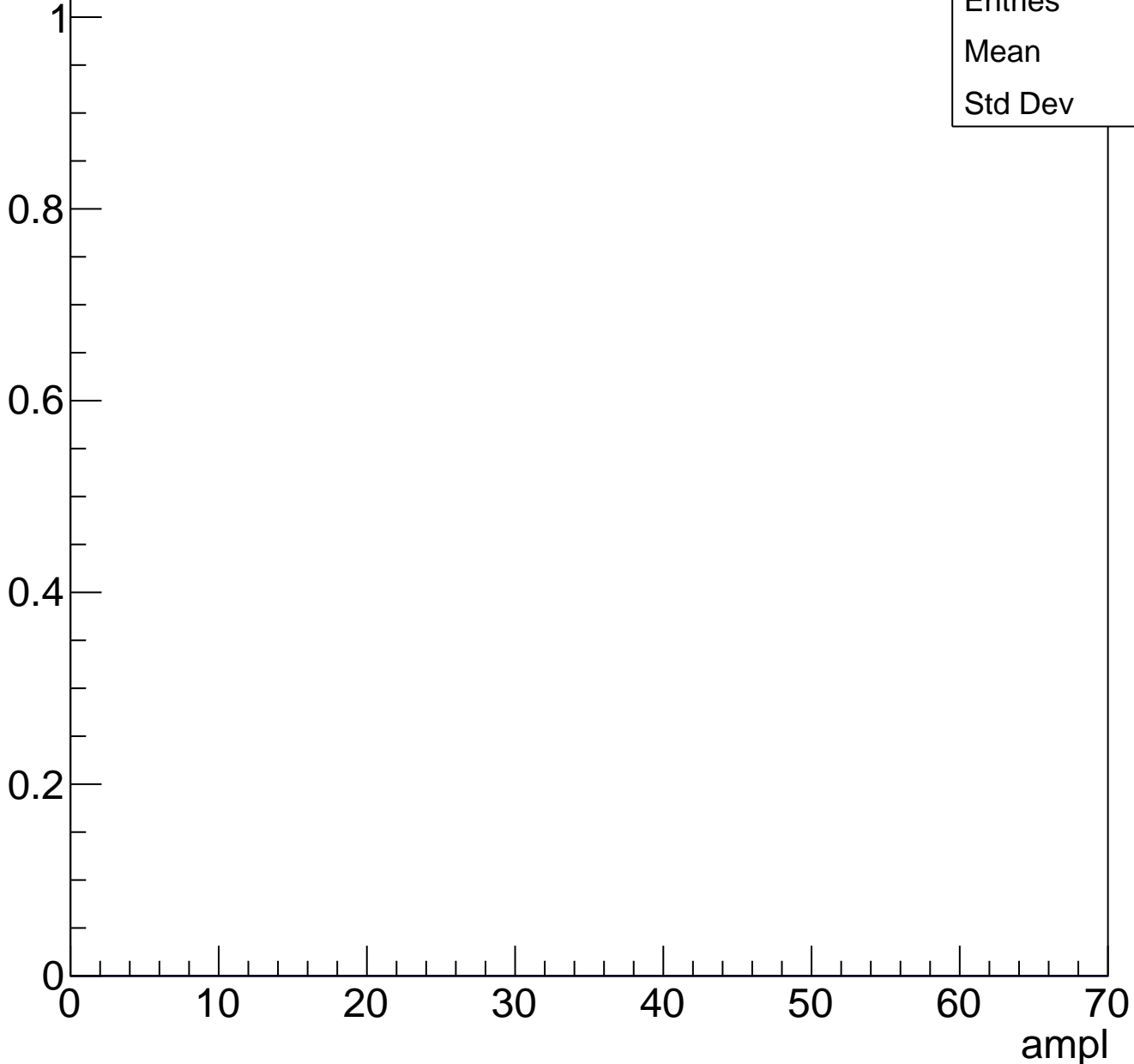
Entry

ampl

# B0L100S, U1-ch32, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch32, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 44 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch32, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 48 |
| Std Dev | 0 |
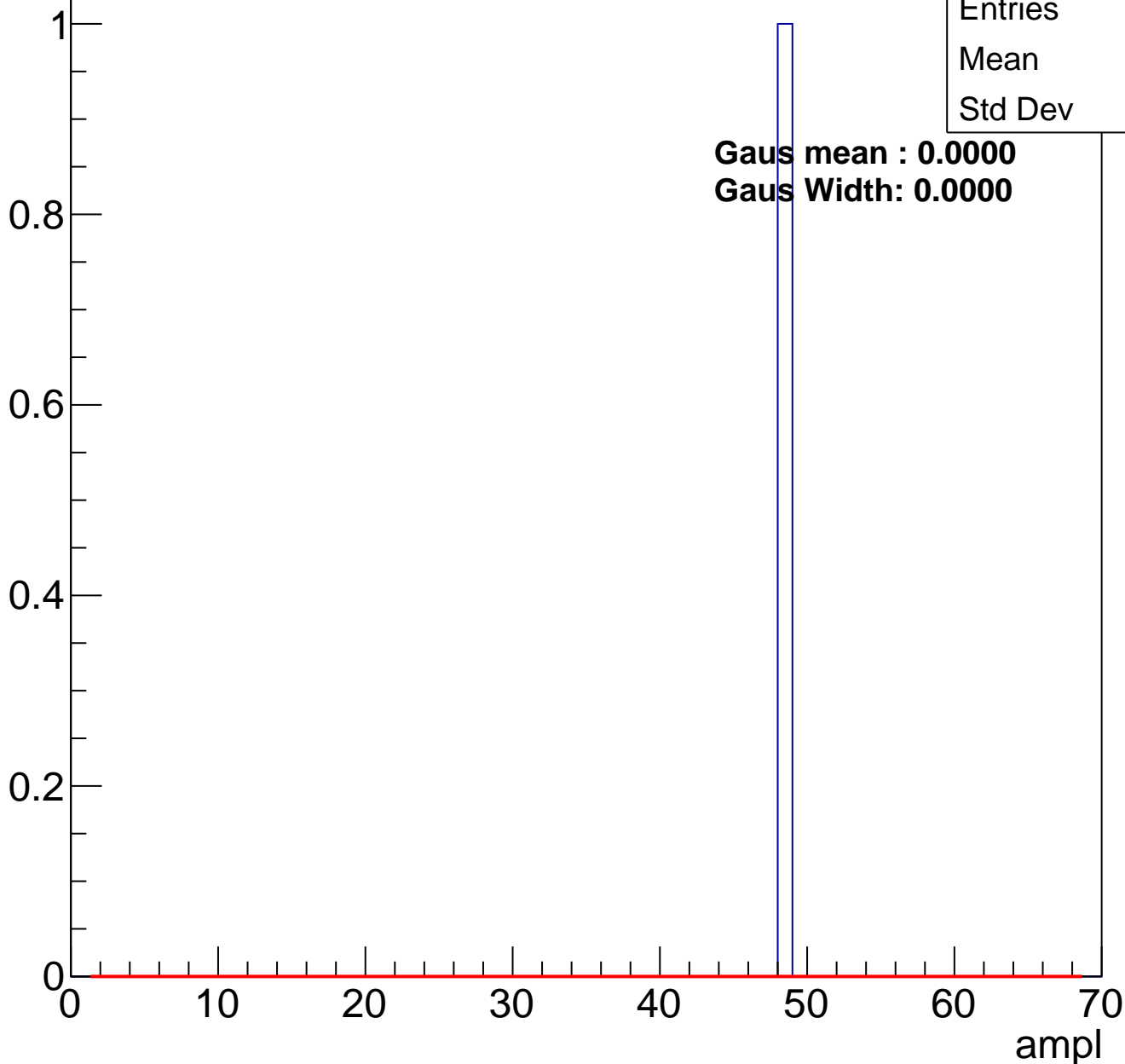
**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

# B0L100S, U1-ch32, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch32, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch32, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch32, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch32, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch33, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch33, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 5 |
| Mean | 43.2 |
| Std Dev | 0.4 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
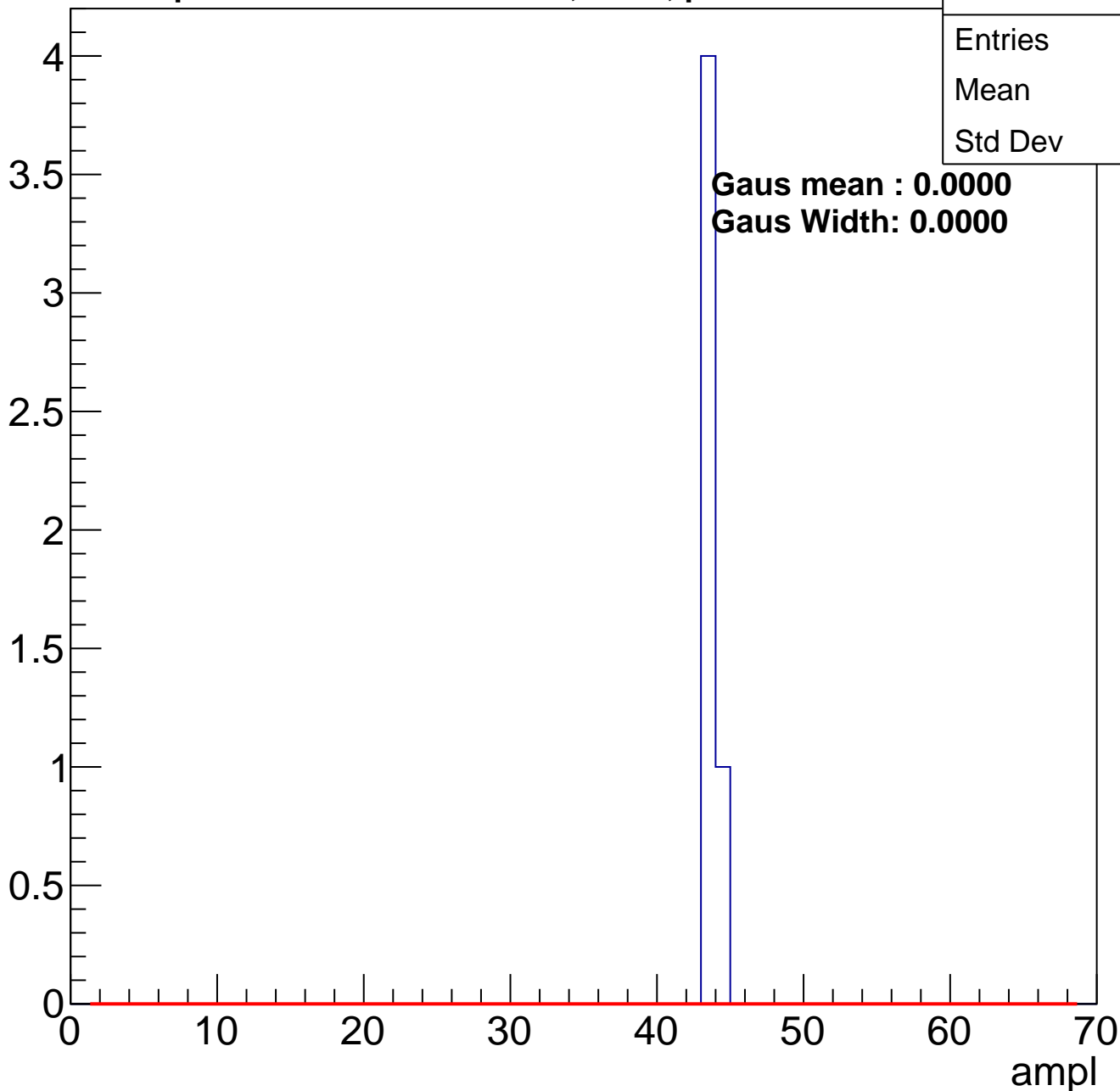
Entry

ampl

# B0L100S, U1-ch33, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 46 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch33, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch33, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch33, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch33, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch33, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch34, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 43 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
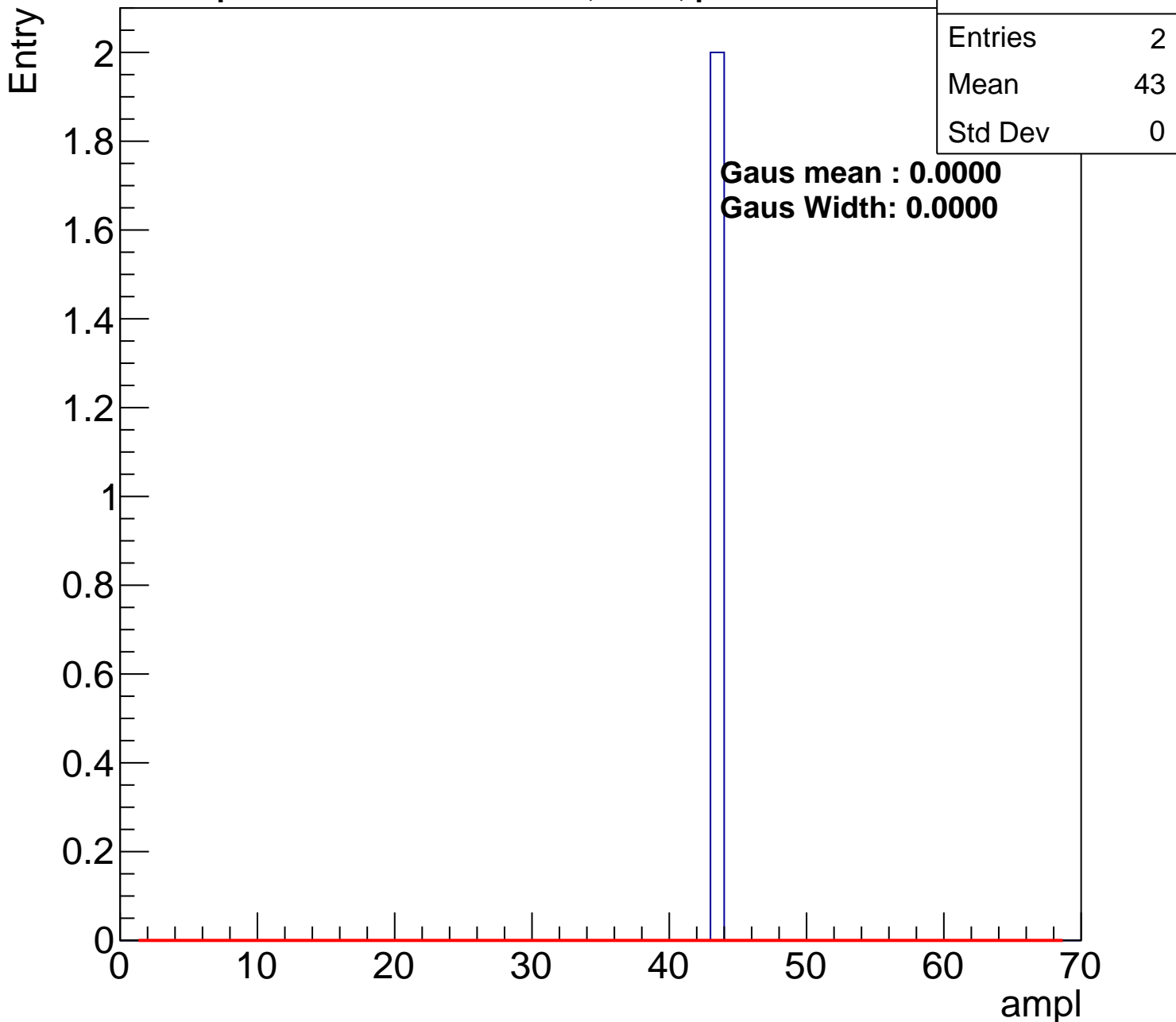
# B0L100S, U1-ch34, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 21 |
|---|---|
| Mean | 43.24 |
| Std Dev | 2.021 |

**Gaus mean : 60.8707**
**Gaus Width: 20.2141**

Entry

ampl
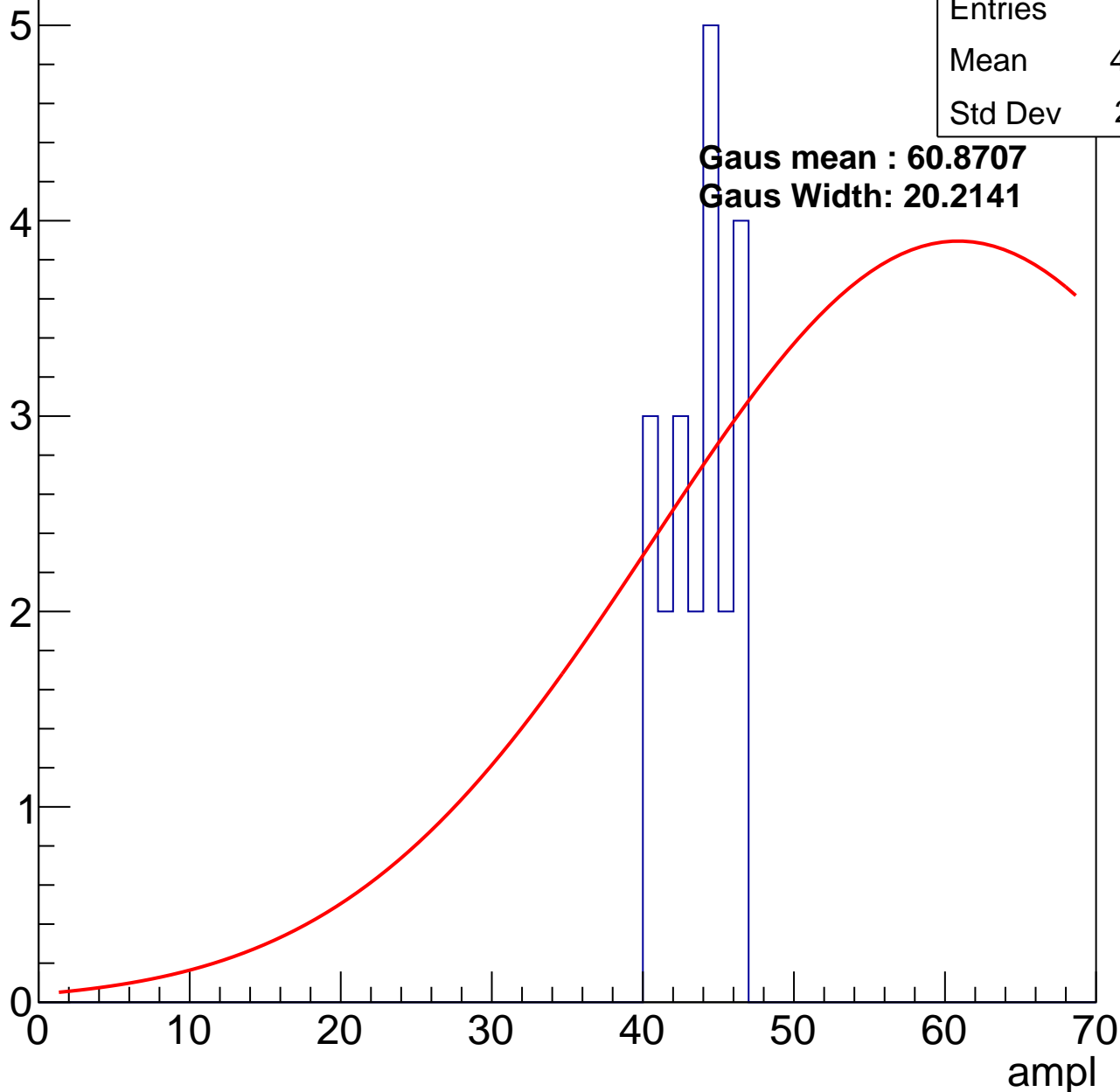
B0L100S, U1-ch34, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 6 |
| Mean | 45.5 |
| Std Dev | 3.304 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
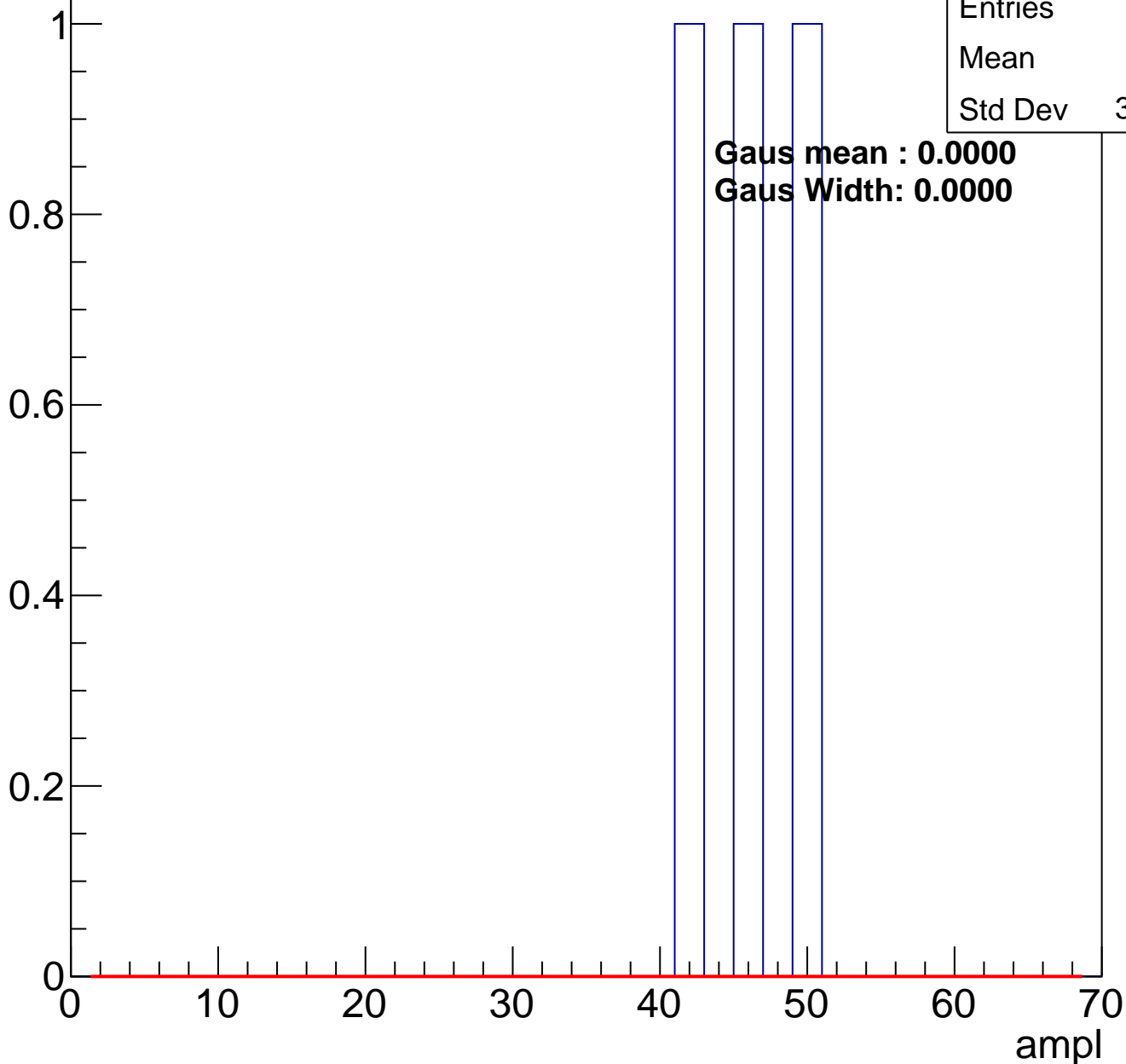
# B0L100S, U1-ch34, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 49 |
| Std Dev | 0 |

ampl
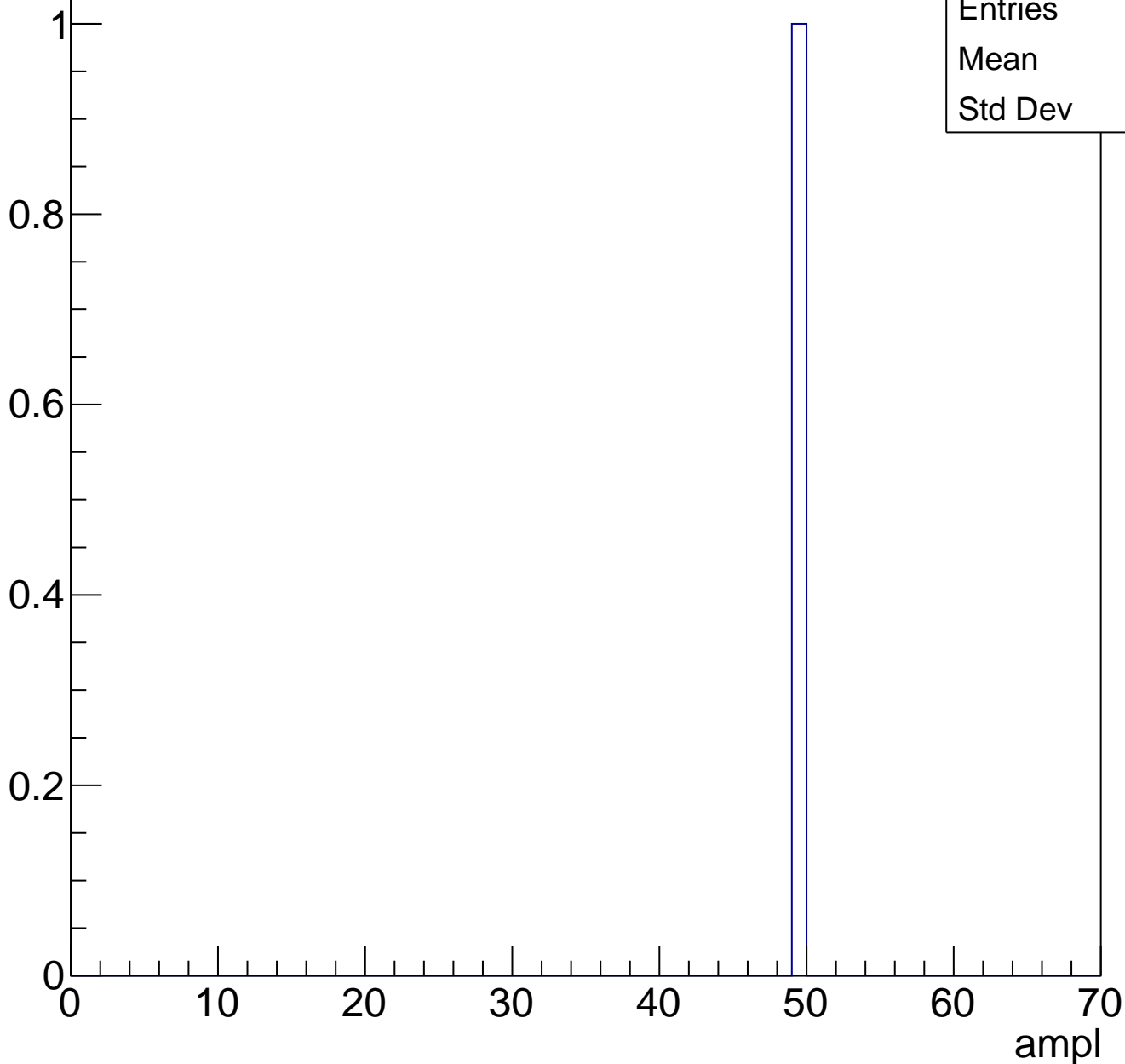
Entry

# B0L100S, U1-ch34, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch34, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch34, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L100S, U1-ch34, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch35, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch35, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 4 |
|---------|-----|
| Mean | 47.5 |
| Std Dev | 0.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch35, adc2

calib_packv5_042523_0143.root, FC#6, port A1
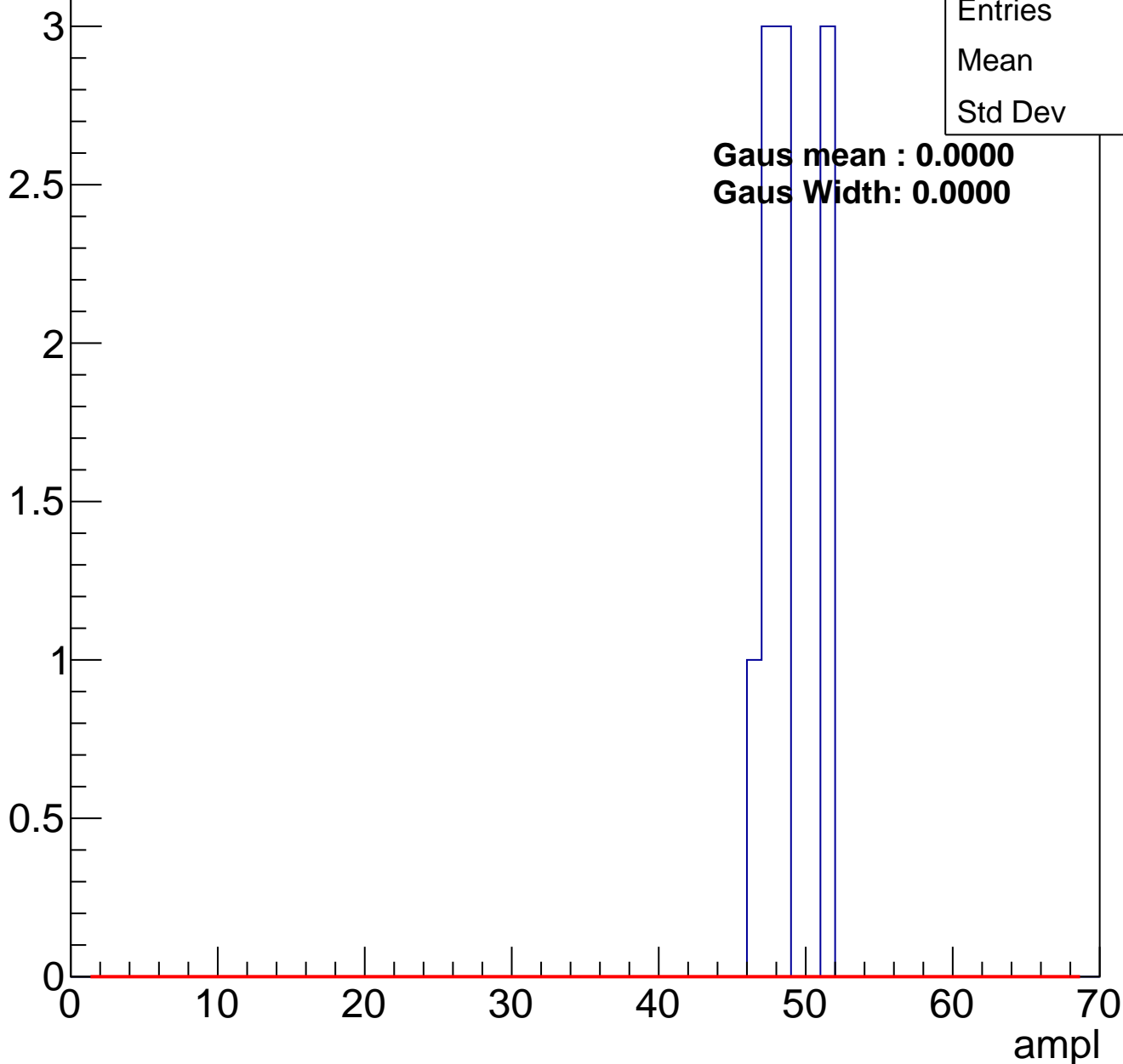
| Entries | 10 |
| Mean | 48.4 |
| Std Dev | 1.8 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch35, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
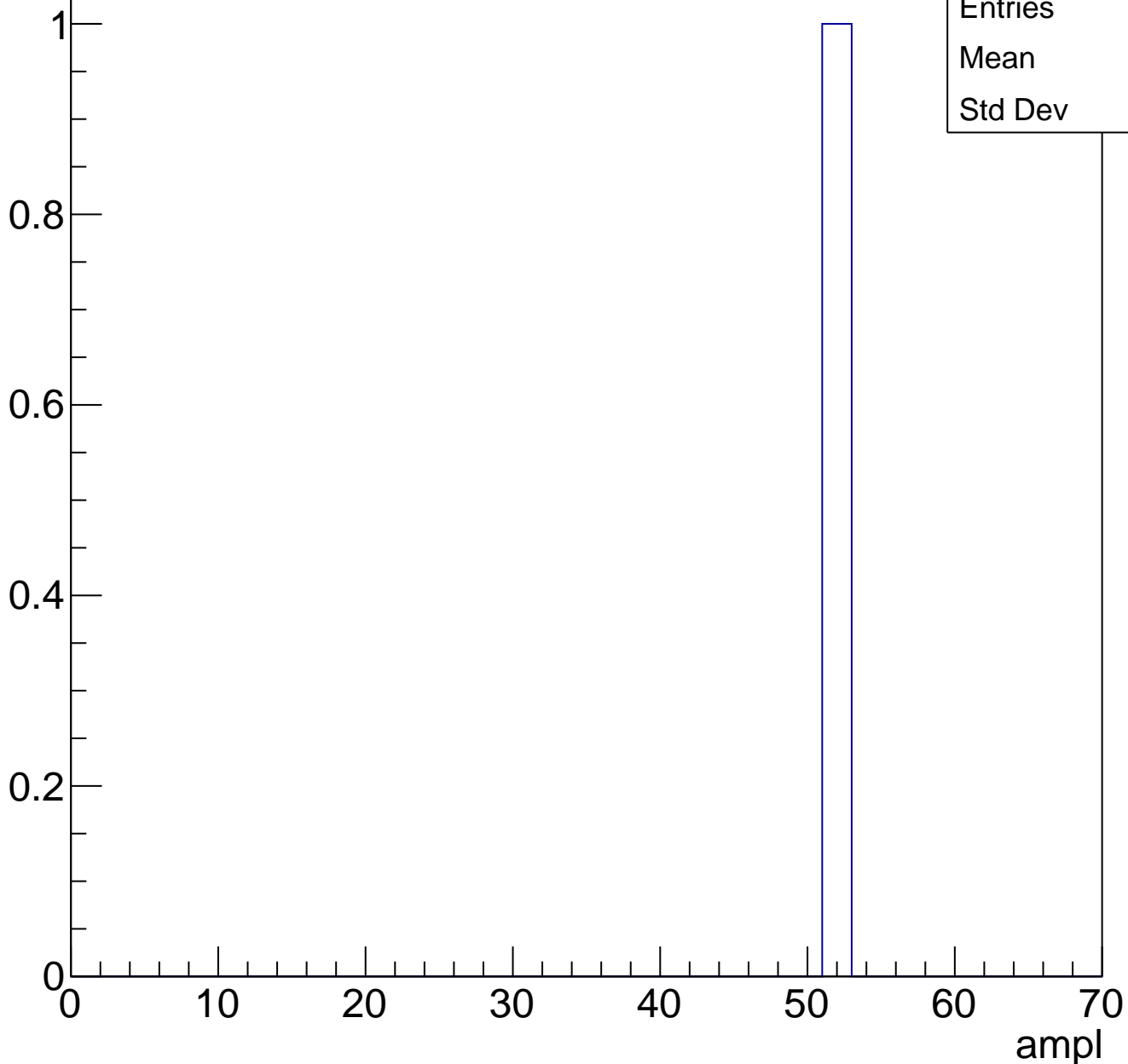


| Entries | 2 |
|---------|-----|
| Mean | 51.5 |
| Std Dev | 0.5 |

# B0L100S, U1-ch35, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch35, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch35, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch35, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
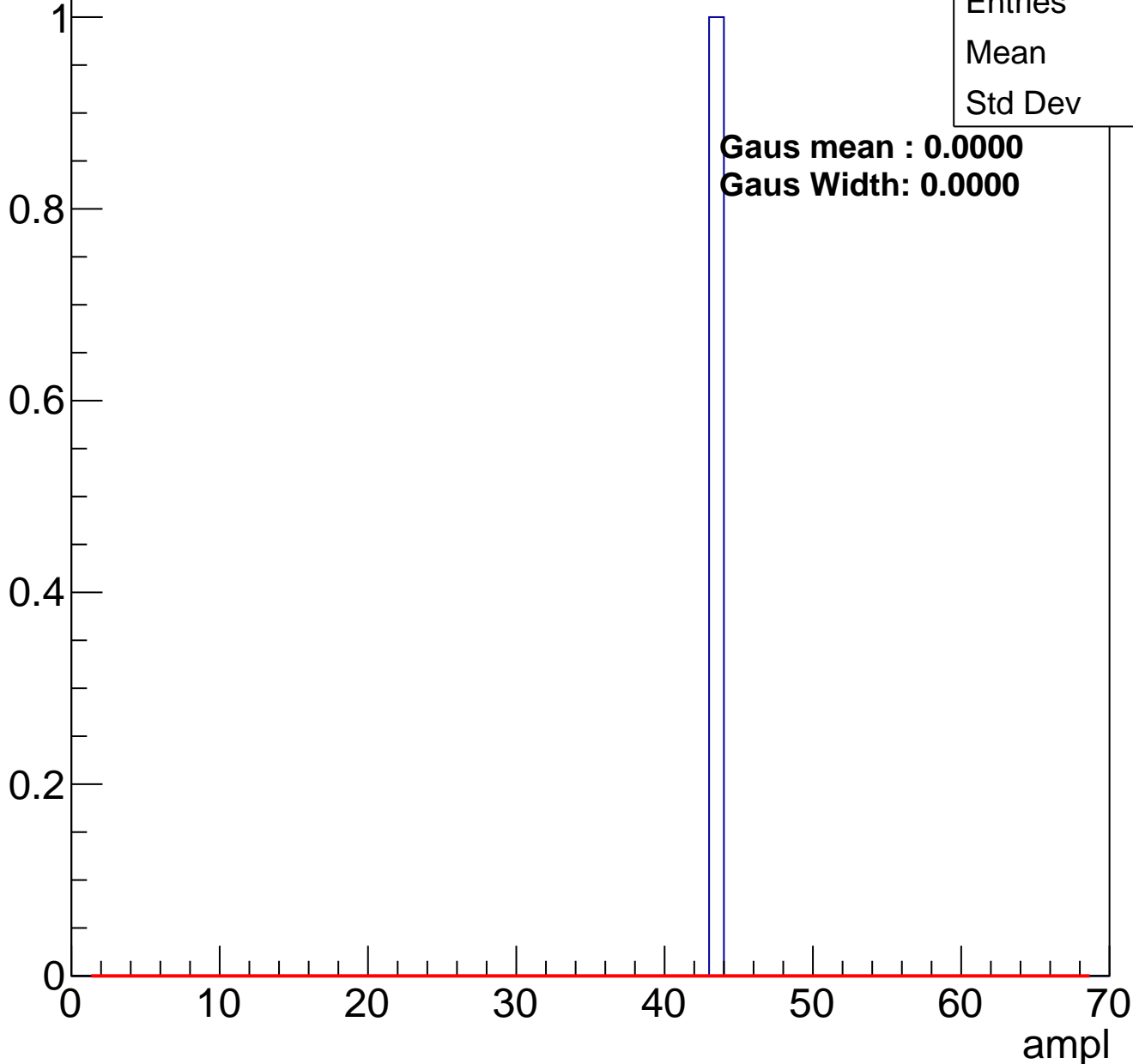
# B0L100S, U1-ch36, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 43 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch36, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 10 |
|---------|-----|
| Mean | 43.2 |
| Std Dev | 0.9798 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
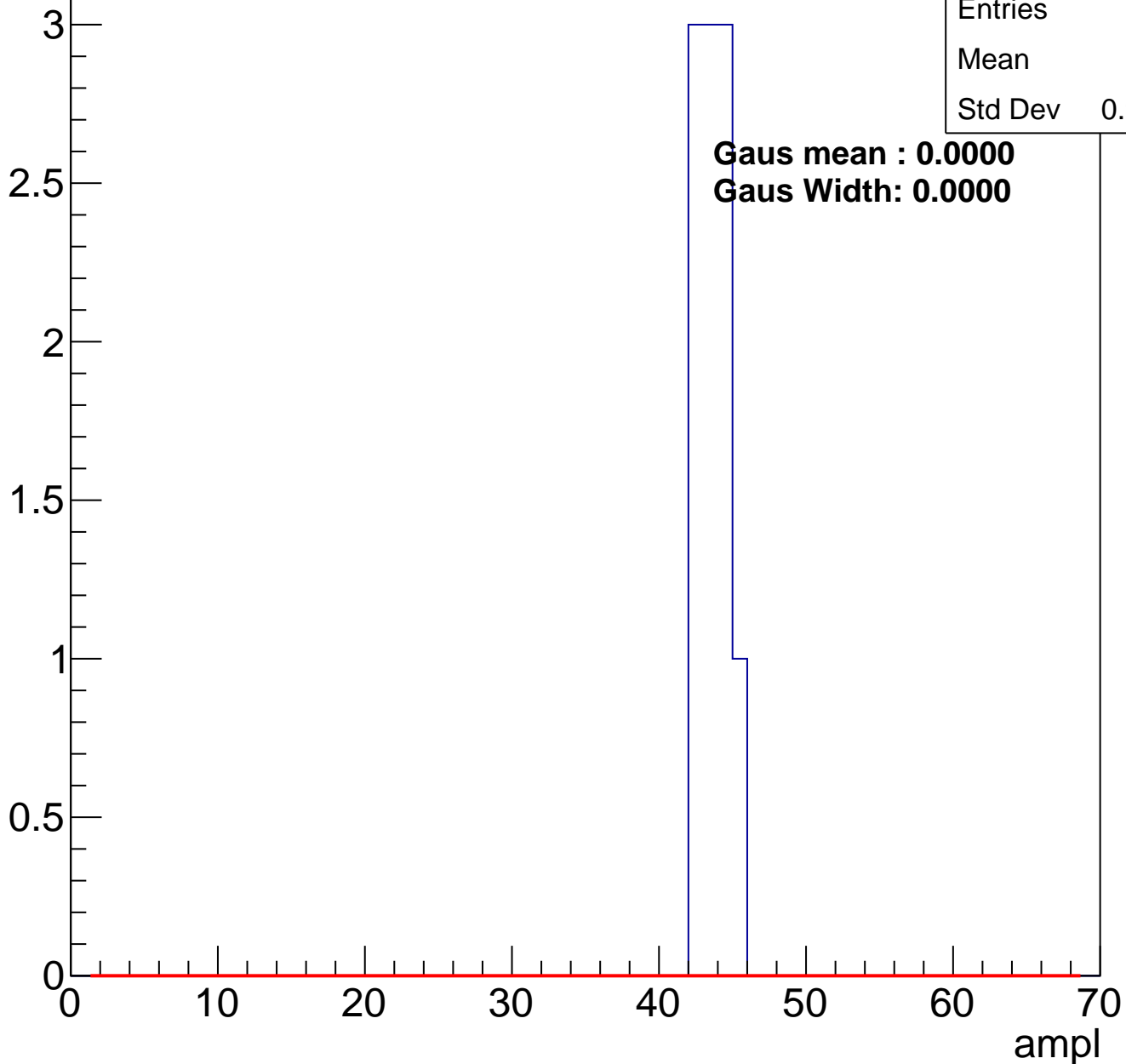
Entry

ampl

# B0L100S, U1-ch36, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 11 |
| Mean | 44.91 |
| Std Dev | 2.151 |

**Gaus mean : 45.3821**
**Gaus Width: 3.4840**

Entry

ampl
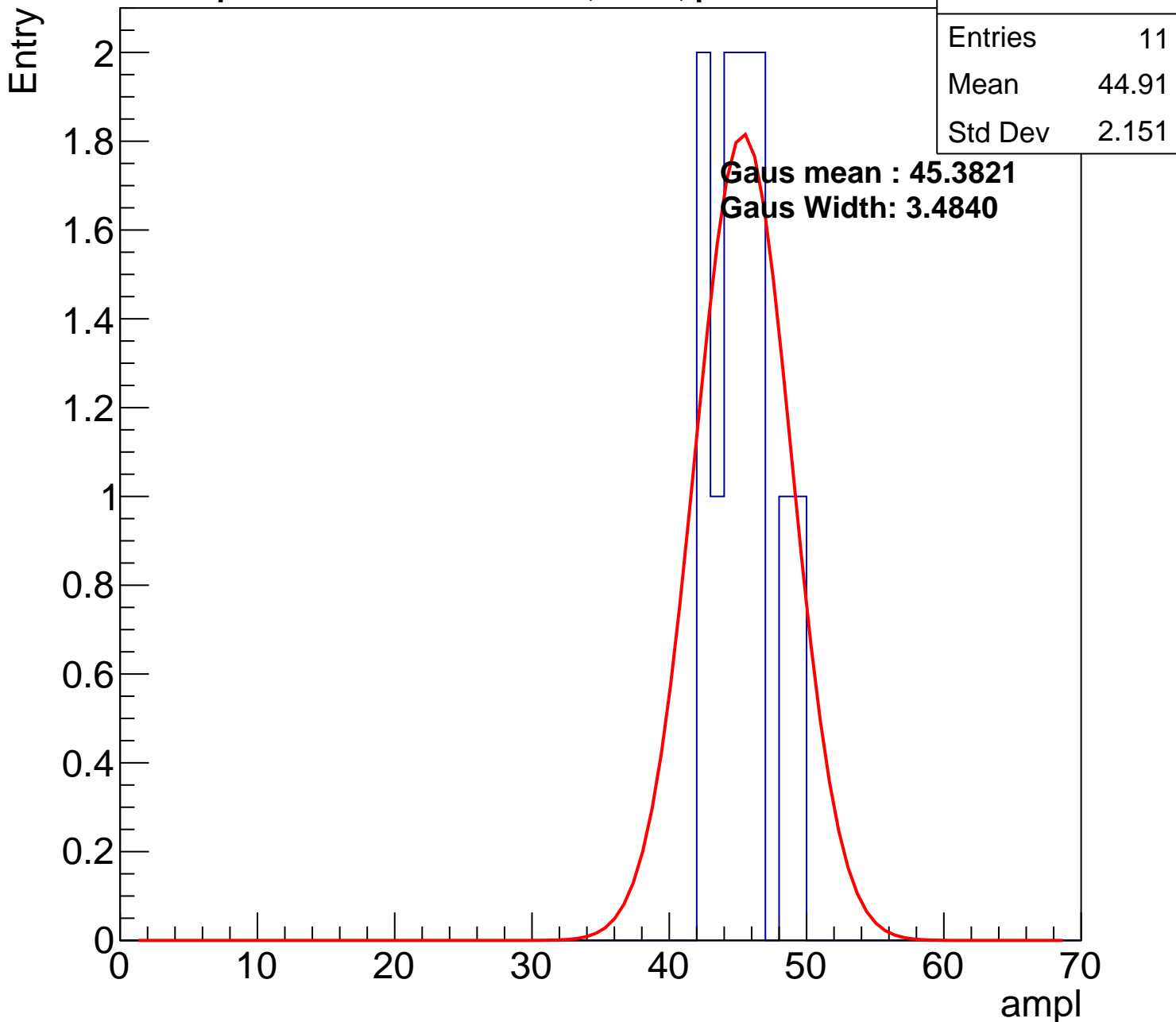
# B0L100S, U1-ch36, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 46 |
| Std Dev | 3 |

Entry

ampl
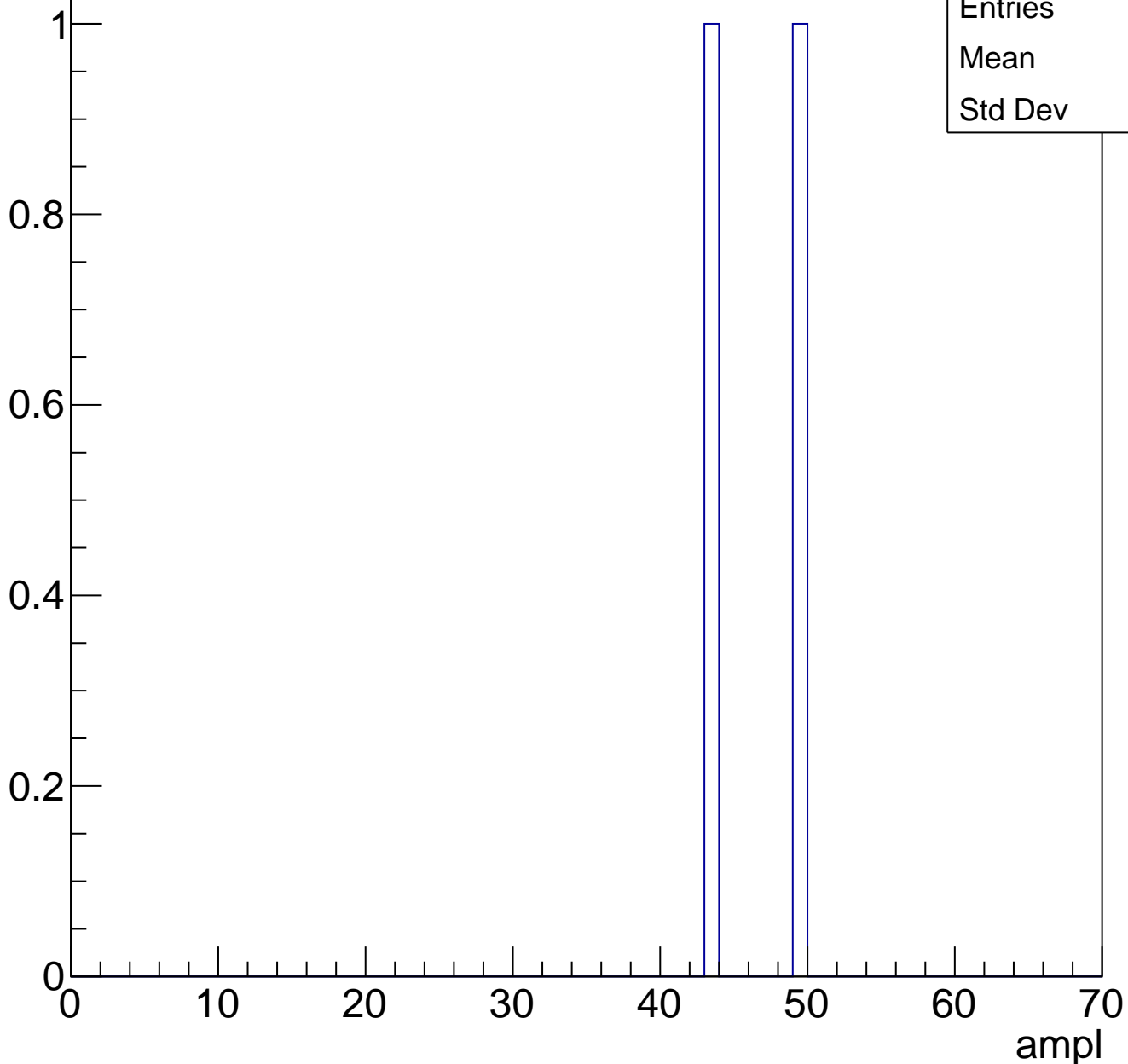
# B0L100S, U1-ch36, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch36, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch36, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch36, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
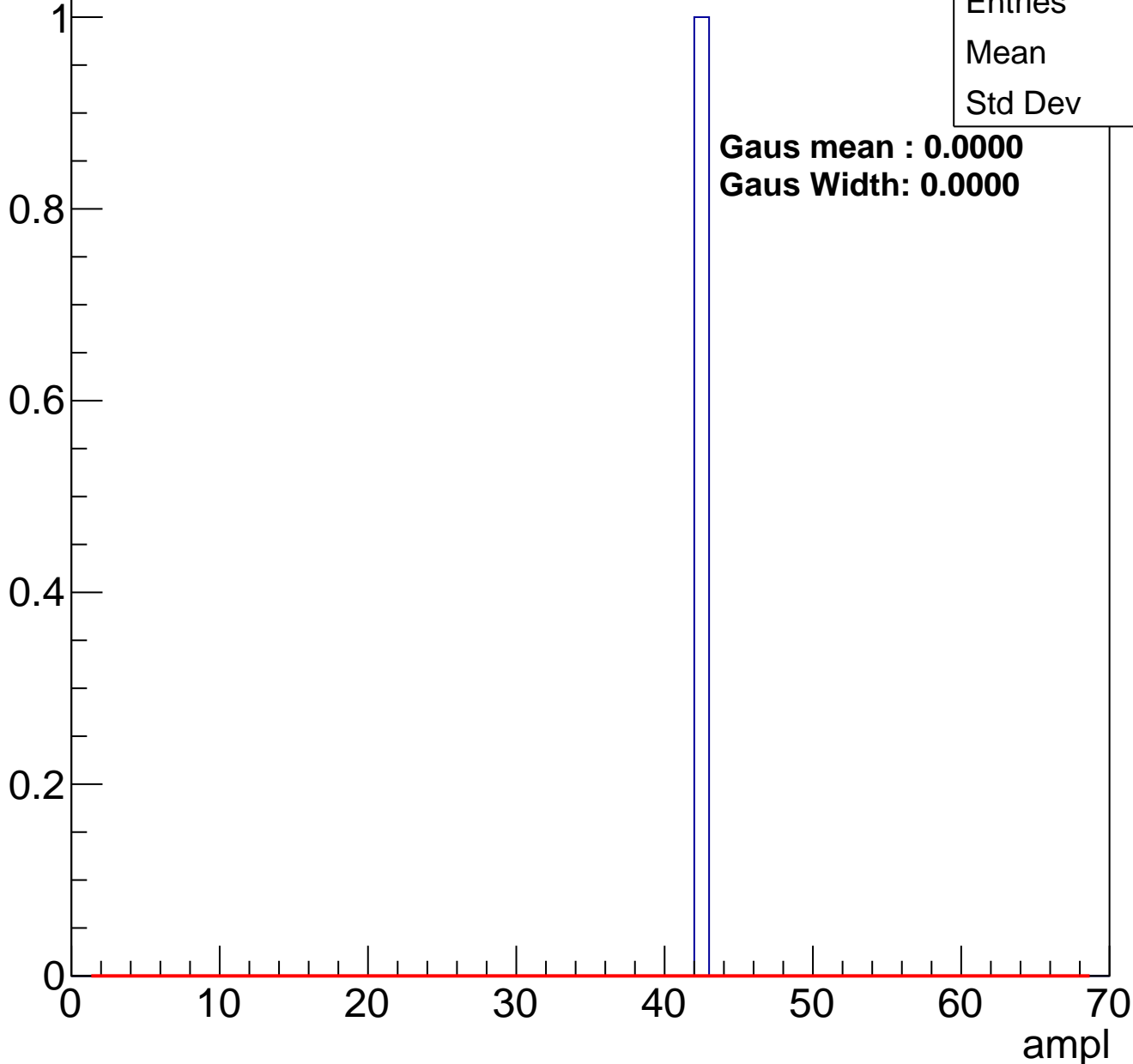
# B0L100S, U1-ch37, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 42 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch37, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| Entries | 1 |
|---------|---|
| Mean | 42 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

# B0L100S, U1-ch37, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 55 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
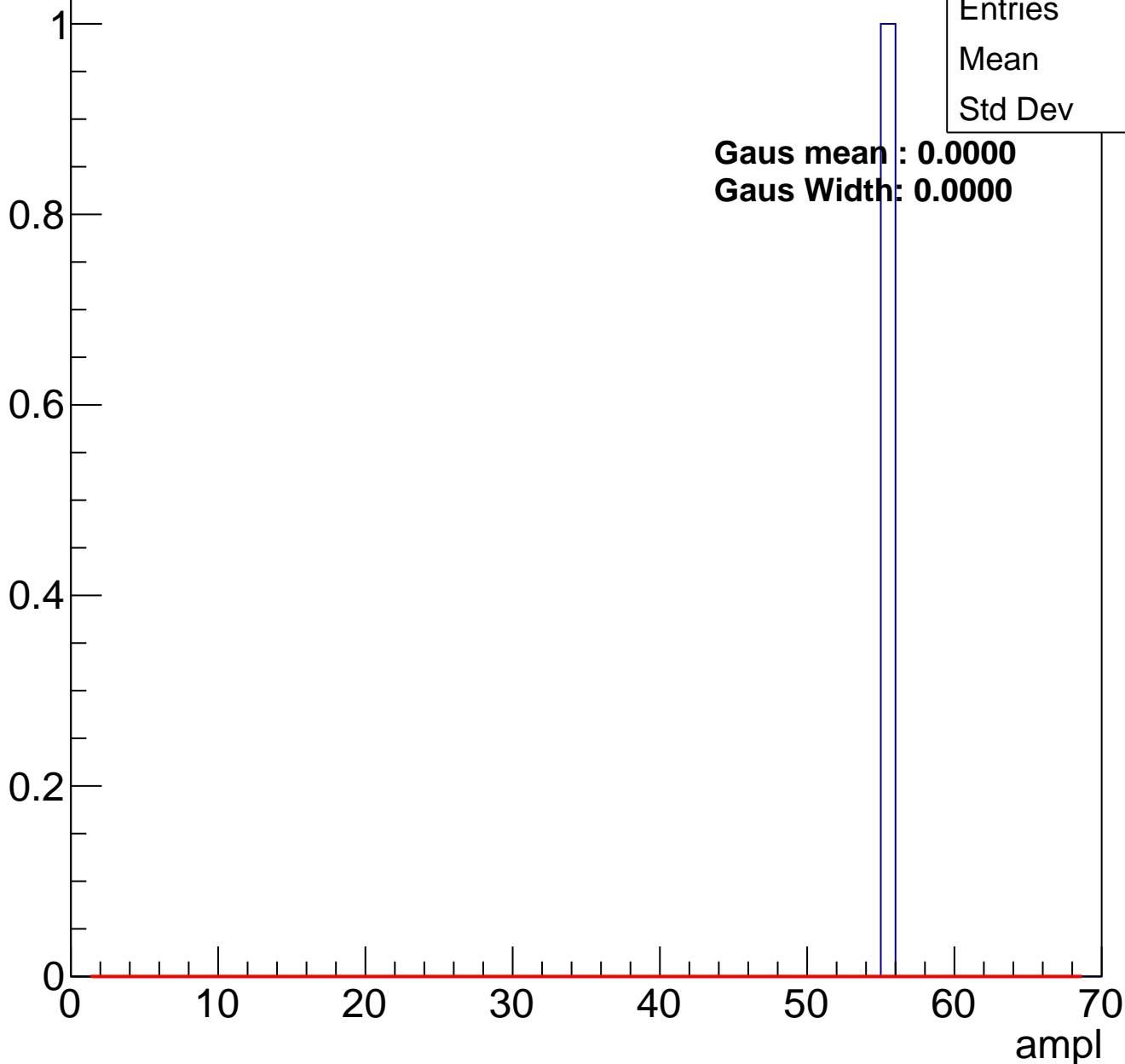
Entry

ampl

# B0L100S, U1-ch37, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 56 |
| Std Dev | 0 |
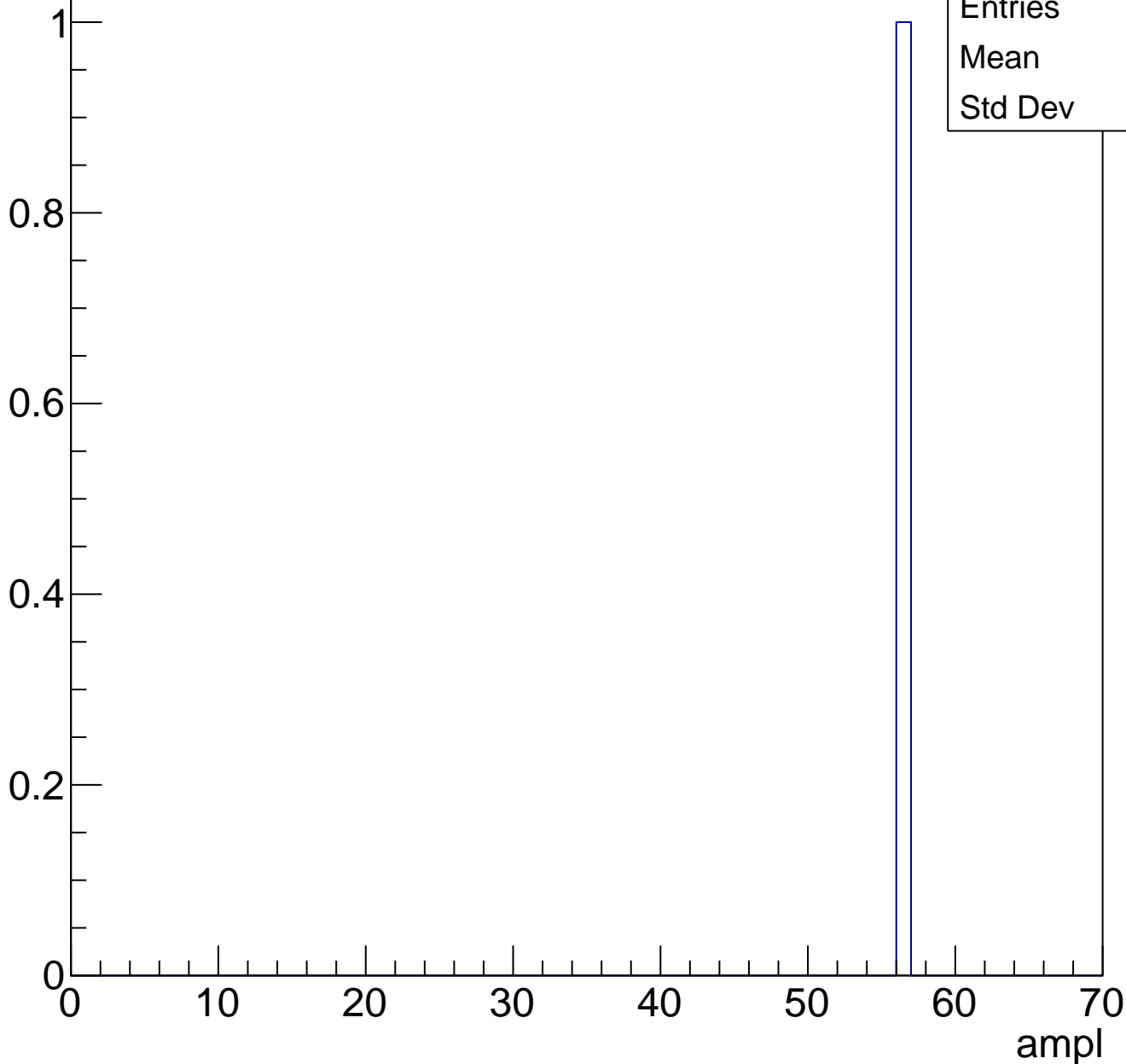
ampl

# B0L100S, U1-ch37, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 4 |
| Mean | 55.75 |
| Std Dev | 0.433 |

Entry

ampl
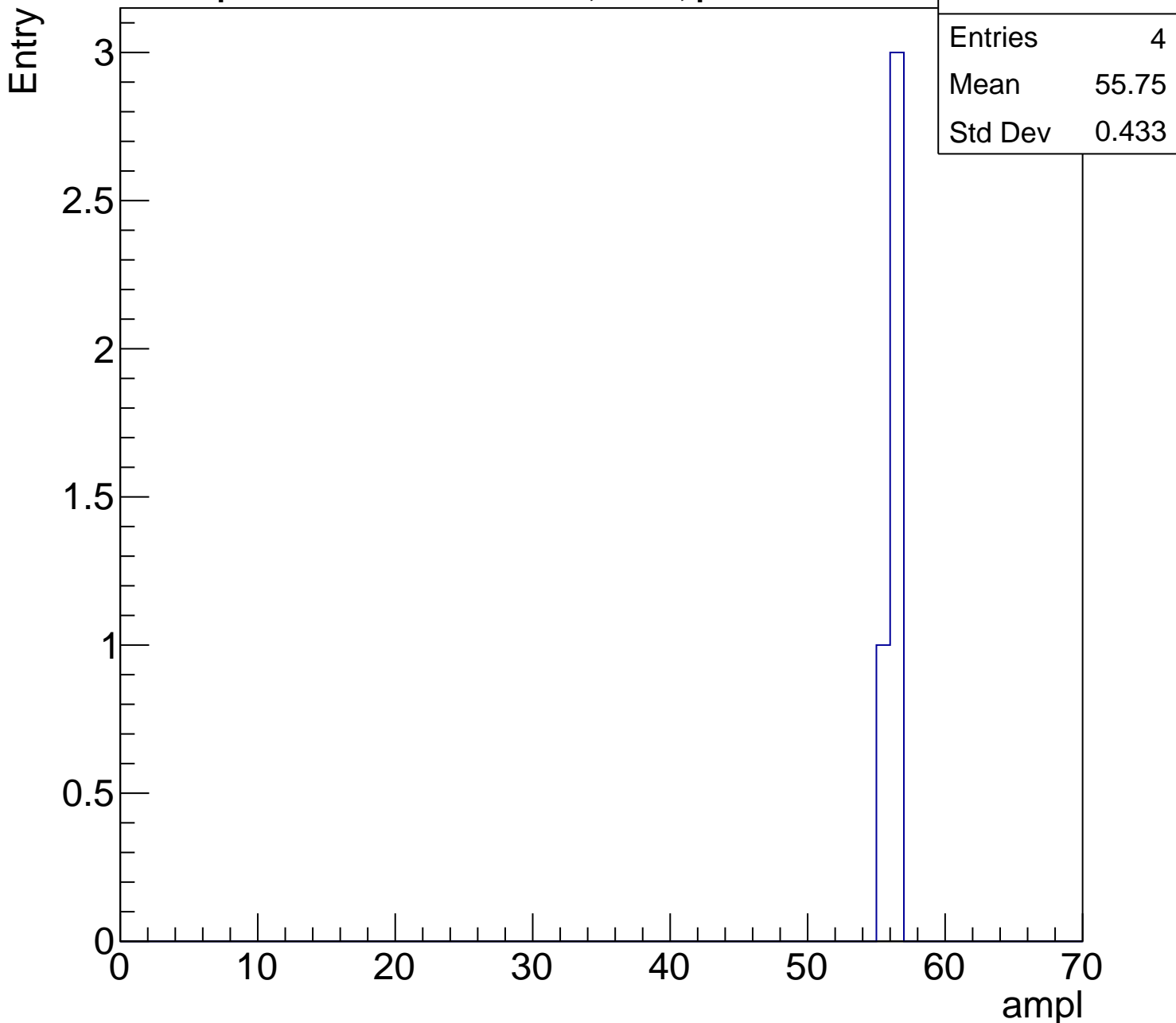
B0L100S, U1-ch37, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch37, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch37, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch38, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch38, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 6 |
|---------|------|
| Mean | 45.67 |
| Std Dev | 2.134 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
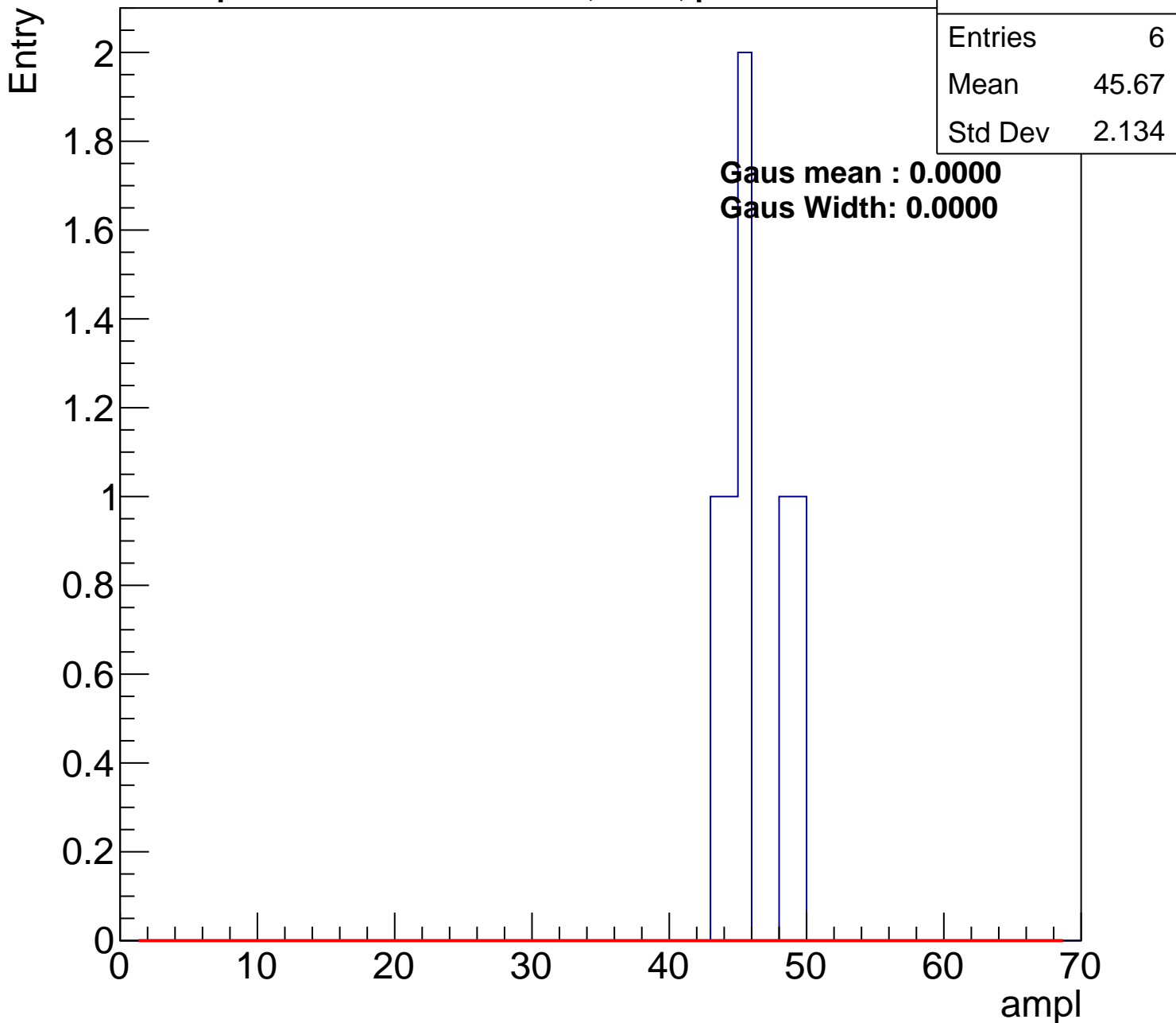
B0L100S, U1-ch38, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 14 |
| Mean | 48.29 |
| Std Dev | 2.63 |

Gaus mean : 49.4335
Gaus Width: 3.3893
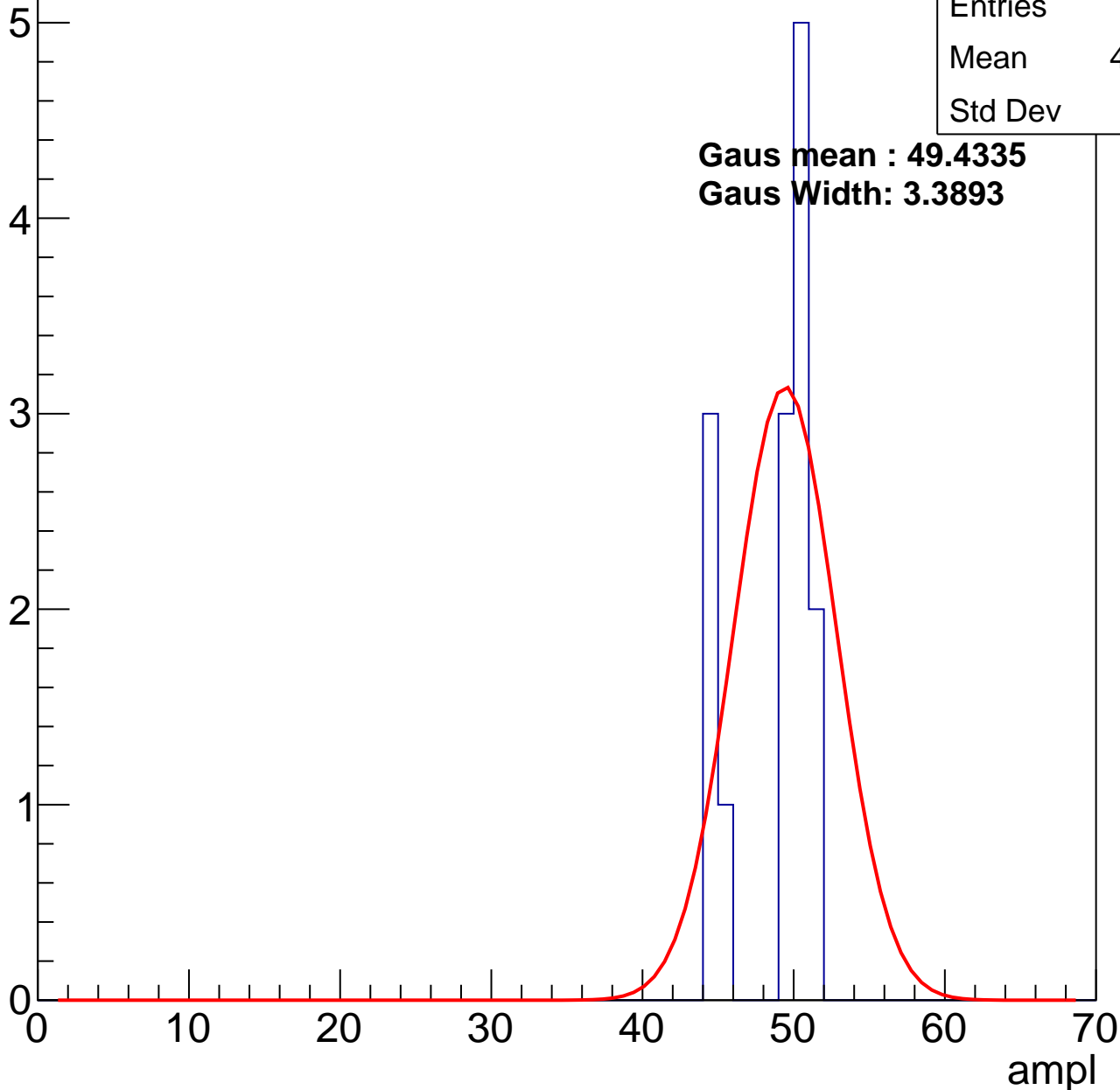
# B0L100S, U1-ch38, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 3 |
| Mean | 50.33 |
| Std Dev | 0.9428 |
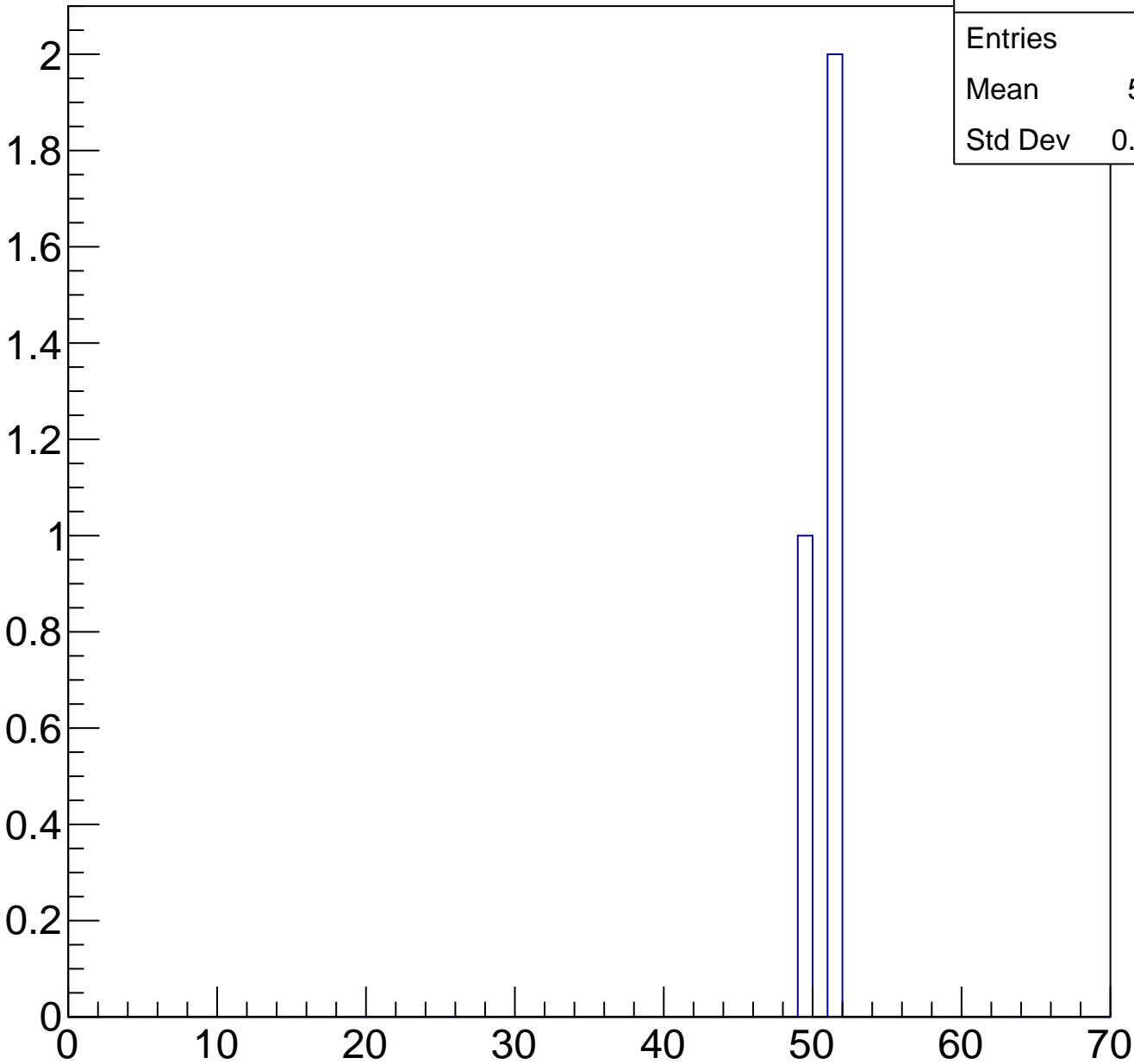
# B0L100S, U1-ch38, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch38, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch38, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch38, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch39, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch39, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch39, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 49 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
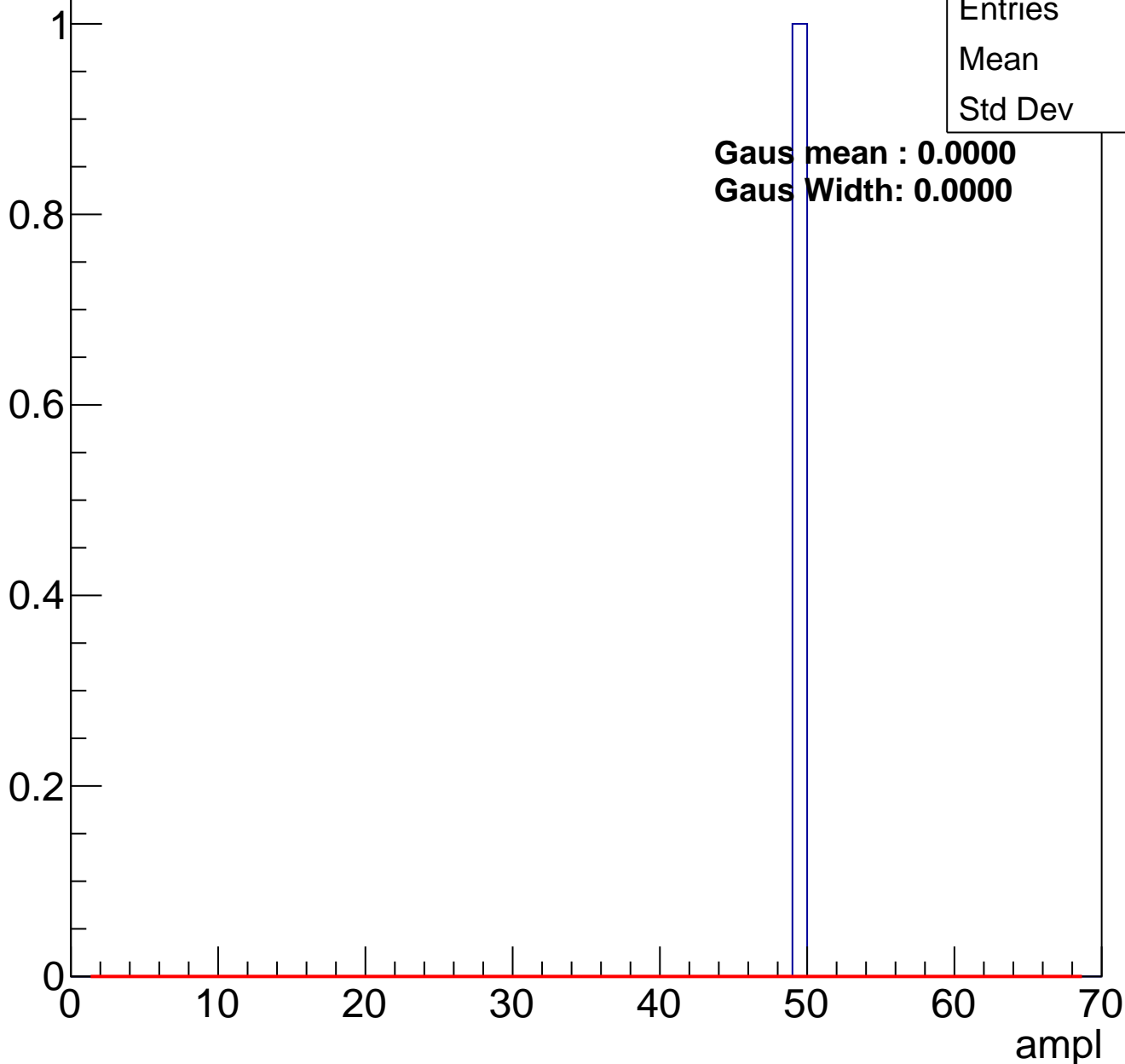
# B0L100S, U1-ch39, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch39, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch39, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch39, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

ampl

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch39, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch40, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch40, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 4 |
|---------|-----|
| Mean | 47.75 |
| Std Dev | 2.385 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch40, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 22 |
|---------|-----|
| Mean | 50.36 |
| Std Dev | 3.053 |

**Gaus mean : 47.4430**
**Gaus Width: 8.1575**

Entry

ampl
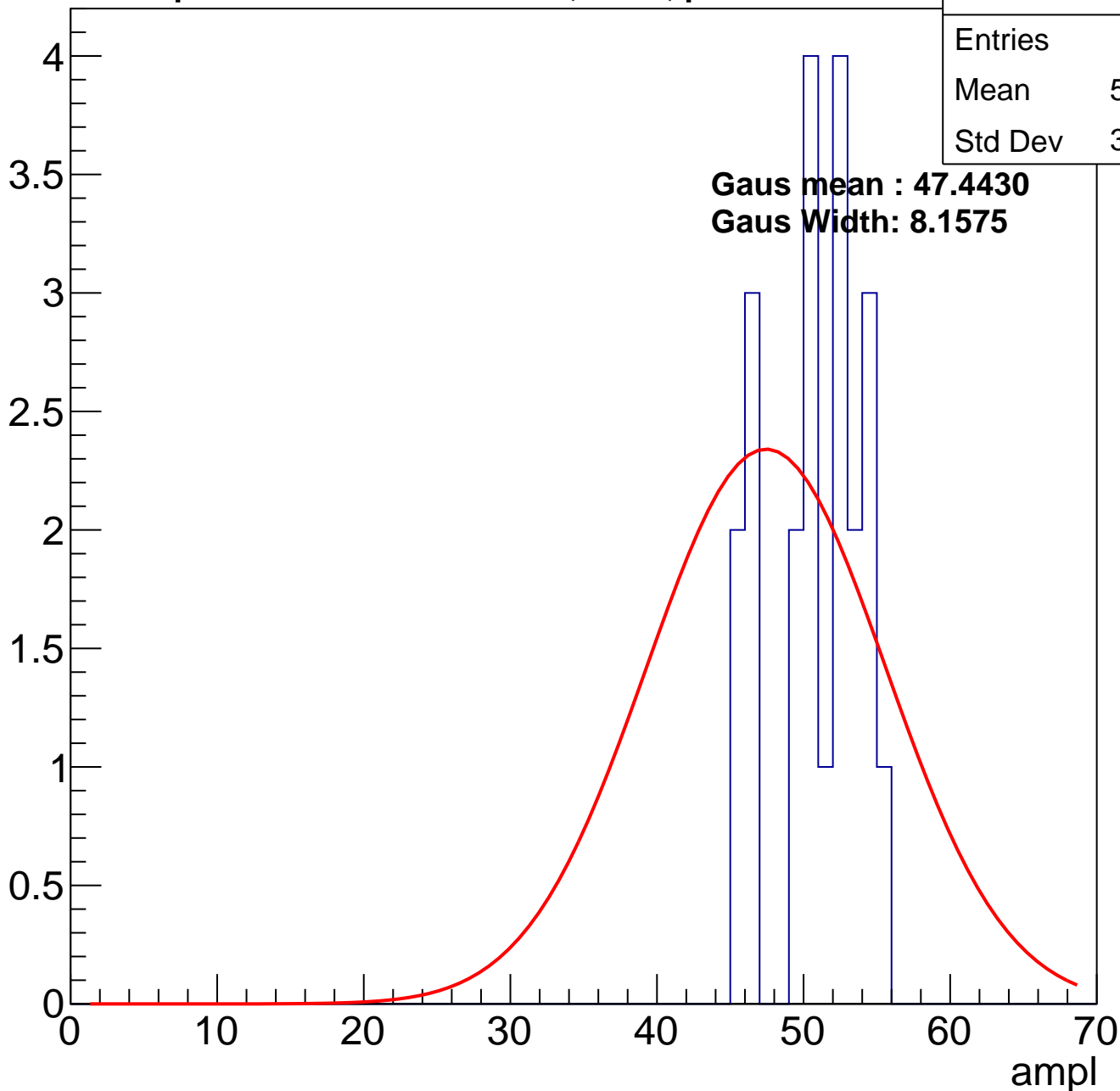
# B0L100S, U1-ch40, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 12 |
| Mean | 52.92 |
| Std Dev | 1.706 |

Entry

ampl
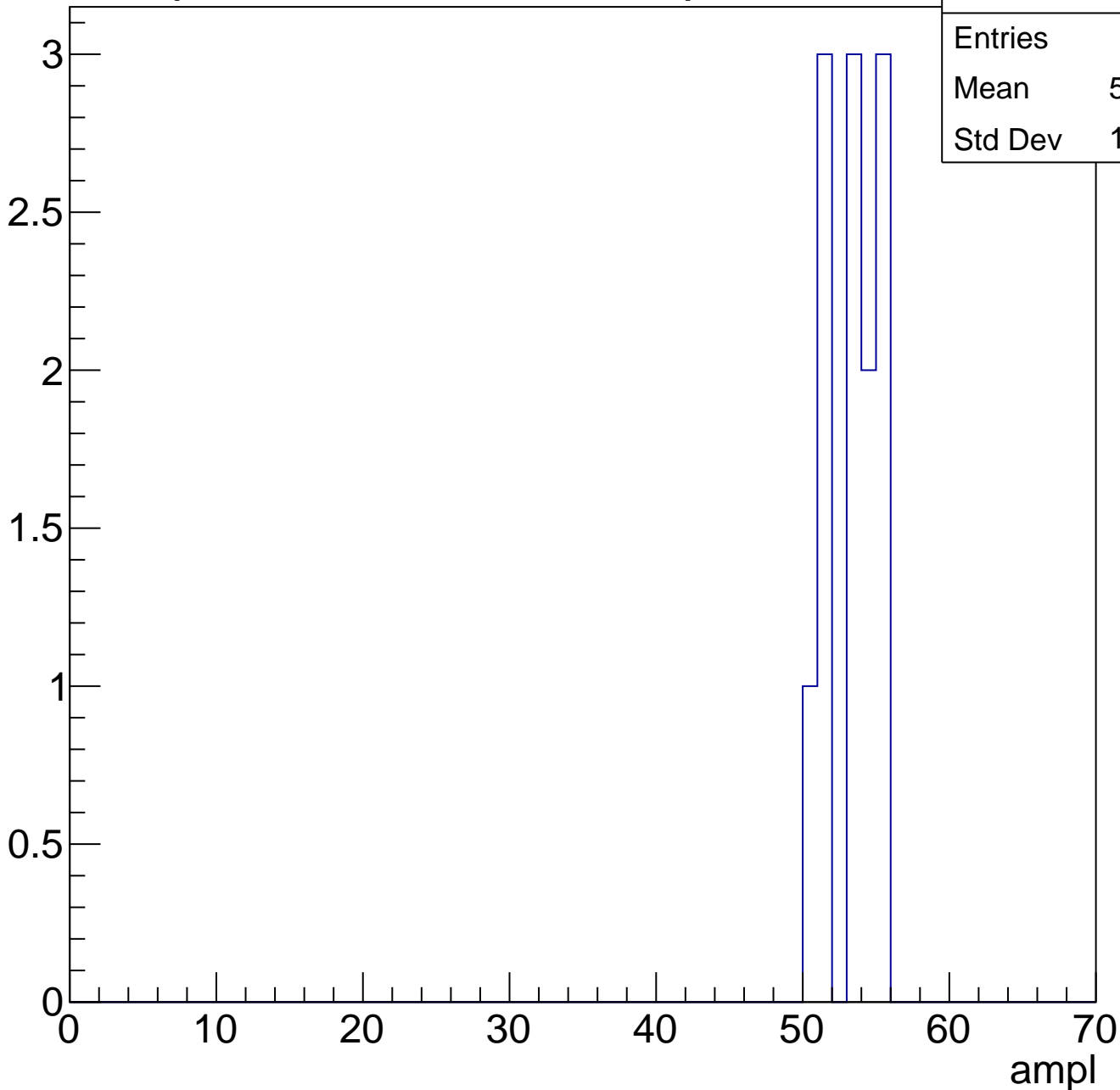
# B0L100S, U1-ch40, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch40, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch40, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch40, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch41, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch41, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch41, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch41, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch41, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch41, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch41, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch41, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch42, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch42, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch42, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch42, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch42, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L100S, U1-ch42, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch42, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch42, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch43, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
| --- | --- |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch43, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch43, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch43, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch43, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch43, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch43, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch43, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch44, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch44, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

B0L100S, U1-ch44, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
|---|---|
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch44, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch44, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch44, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch44, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch44, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch45, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

# B0L100S, U1-ch45, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch45, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch45, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch45, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch45, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch45, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch45, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch46, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch46, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 5 |
|---|---|
| Mean | 46 |
| Std Dev | 0.6325 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
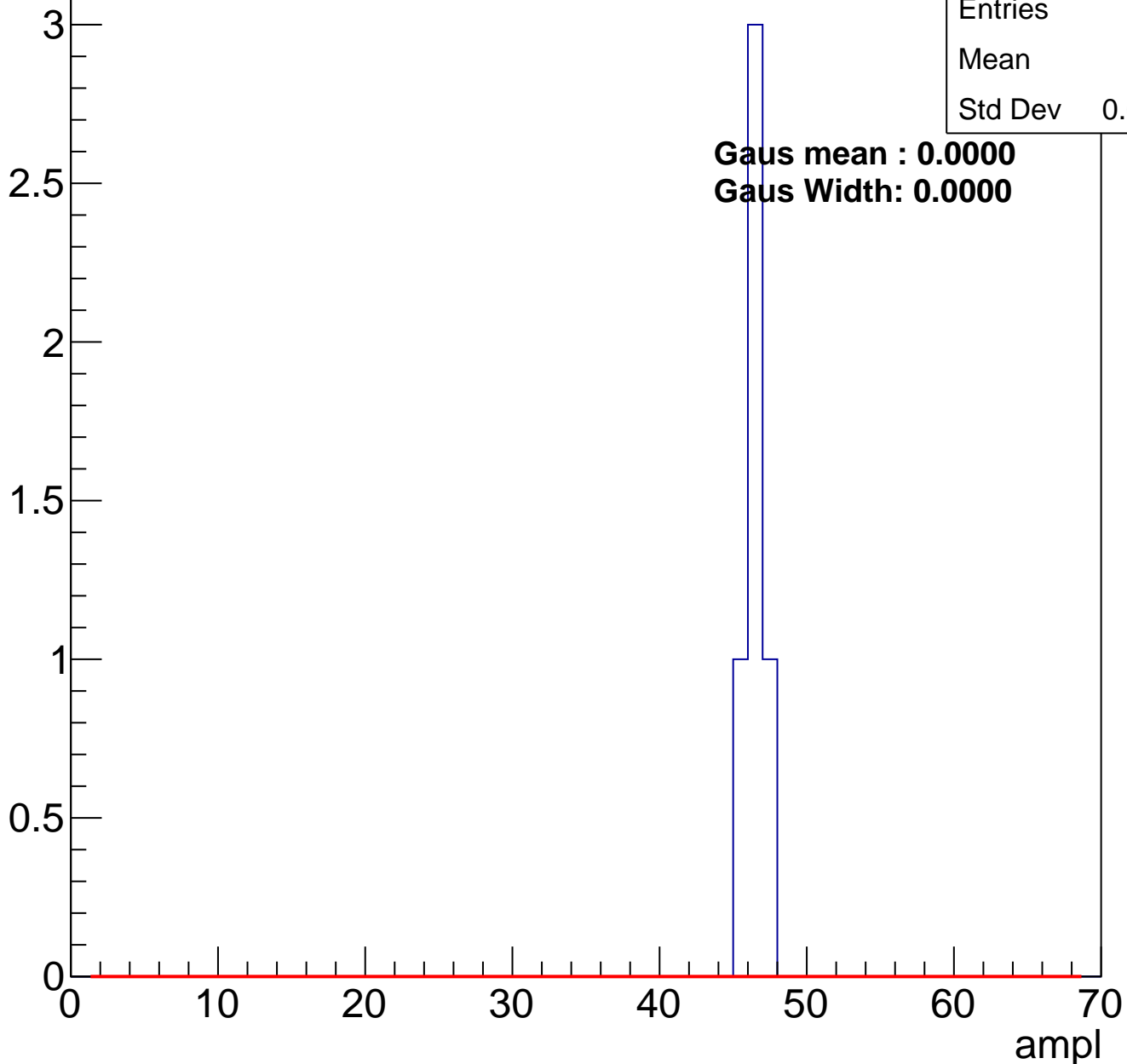
# B0L100S, U1-ch46, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
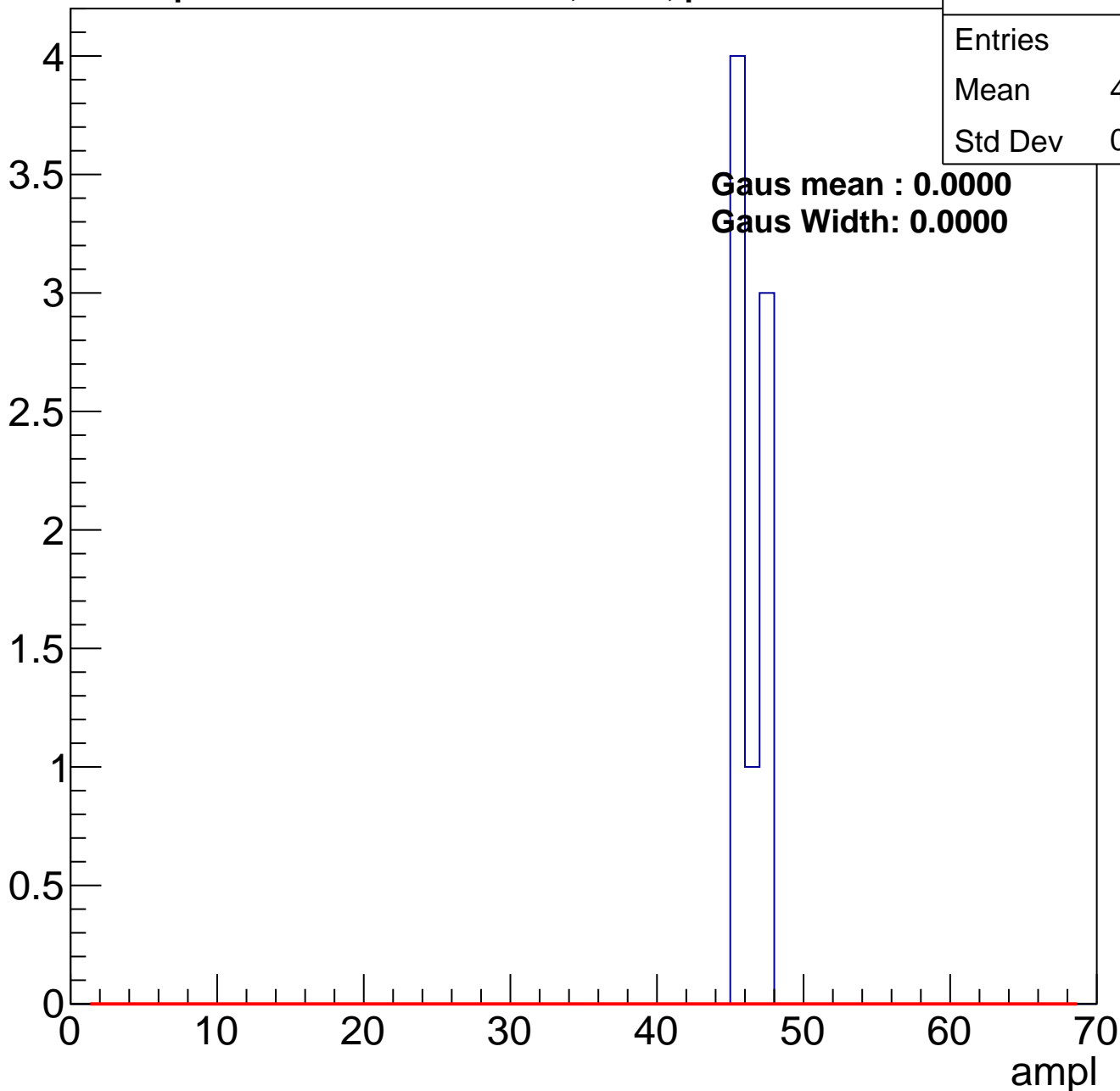
| Entries | 8 |
| Mean | 45.88 |
| Std Dev | 0.927 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch46, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 46 |
| Std Dev | 0 |
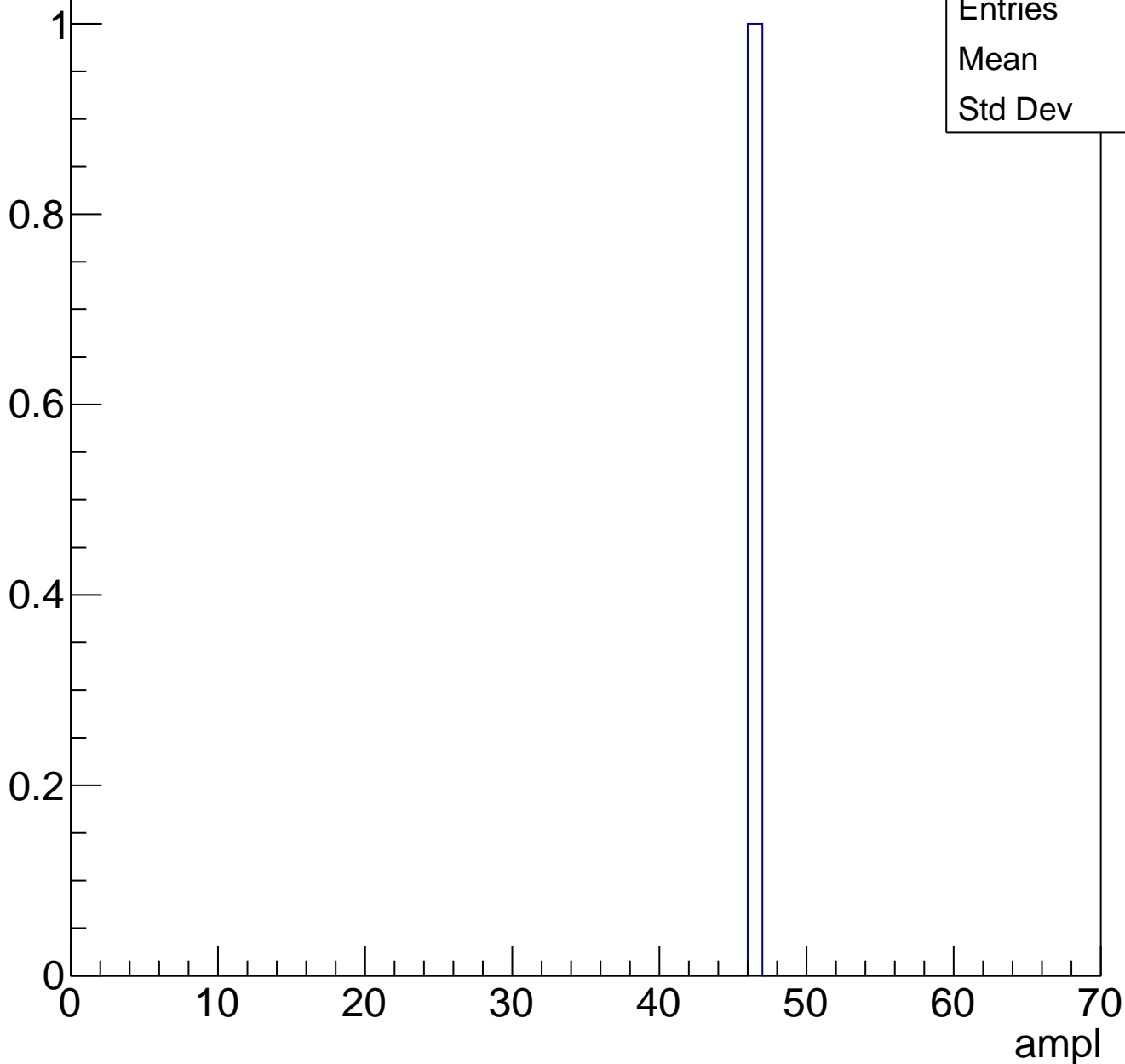
# B0L100S, U1-ch46, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 45 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch46, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch46, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch46, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch47, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**
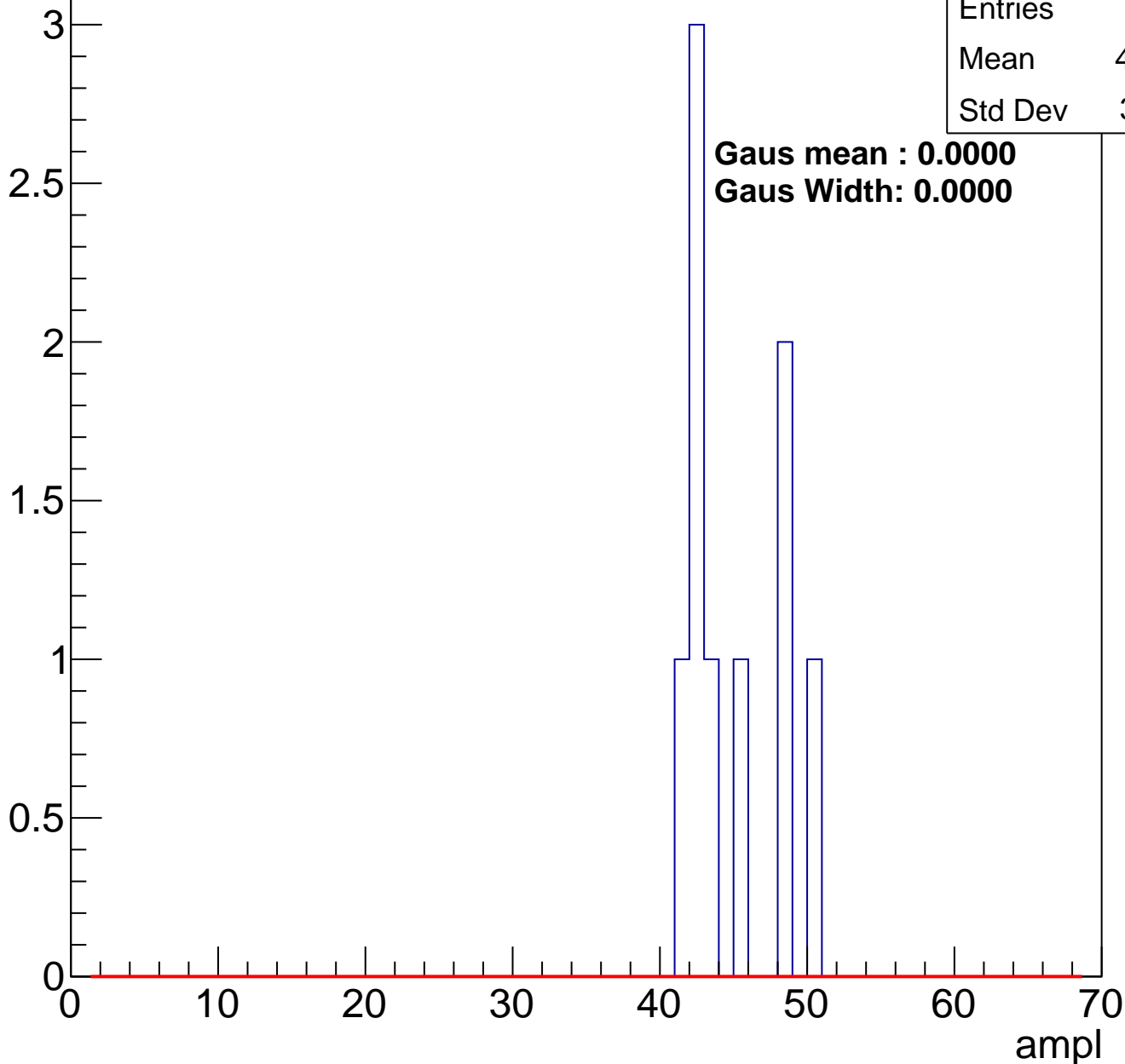


| Entries | 9 |
|---------|------|
| Mean | 44.56 |
| Std Dev | 3.131 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch47, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 17 |
| Mean | 43.59 |
| Std Dev | 1.784 |

**Gaus mean : 43.5920**
**Gaus Width: 2.8026**
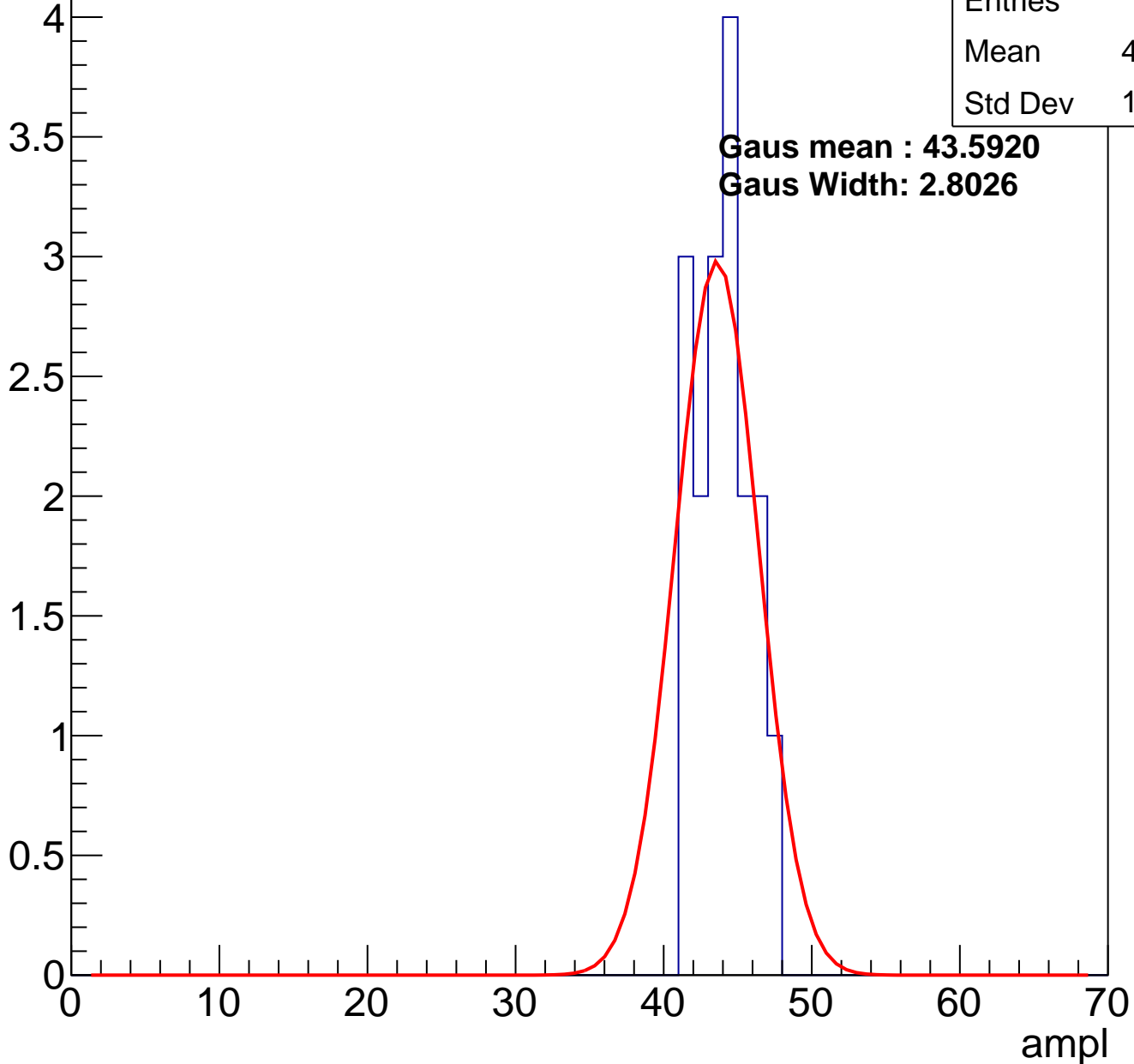
# B0L100S, U1-ch47, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 25 |
| Mean | 48.12 |
| Std Dev | 3.362 |

**Gaus mean : 49.5572**
**Gaus Width: 4.6647**

Entry

ampl
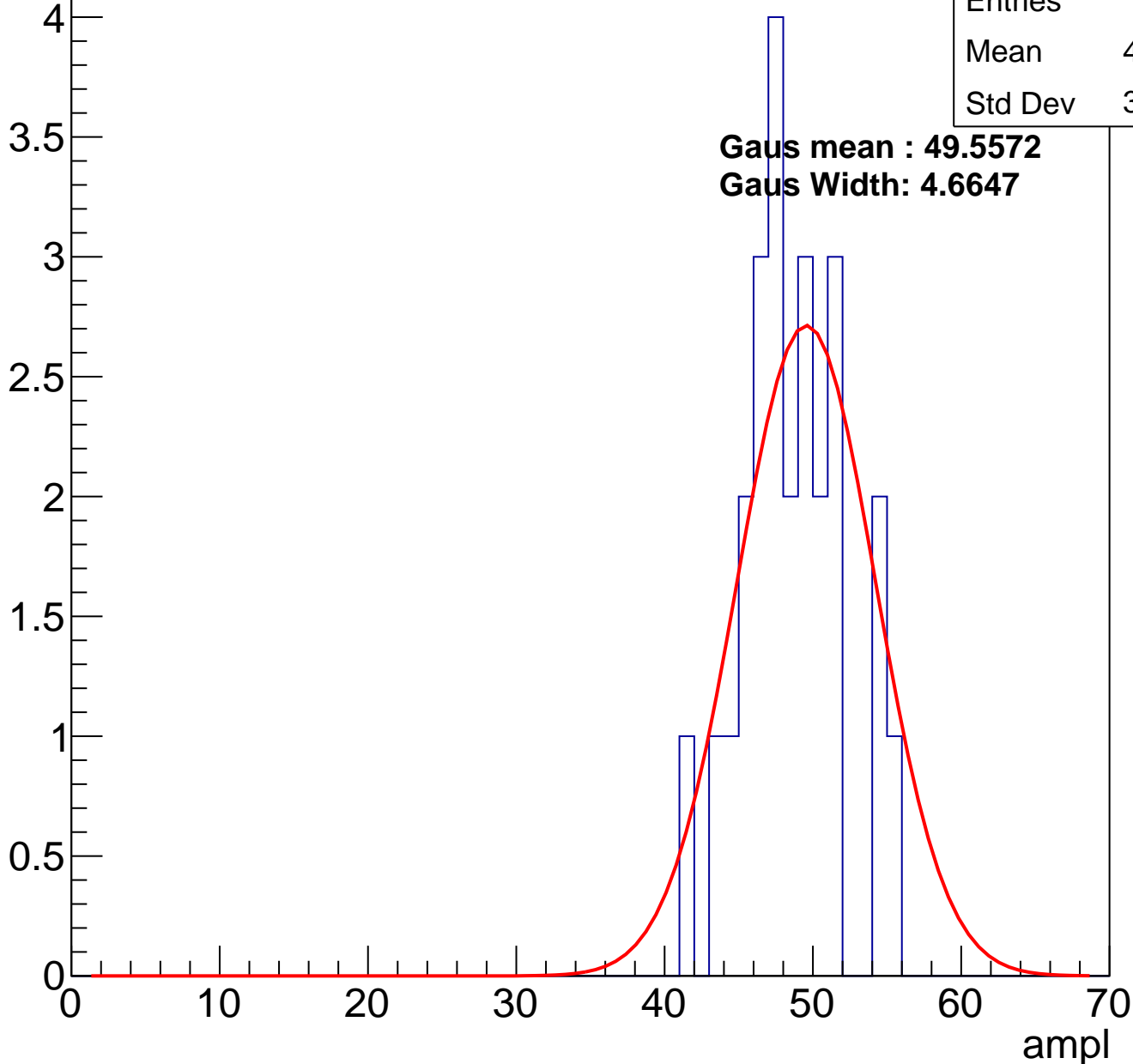
# B0L100S, U1-ch47, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
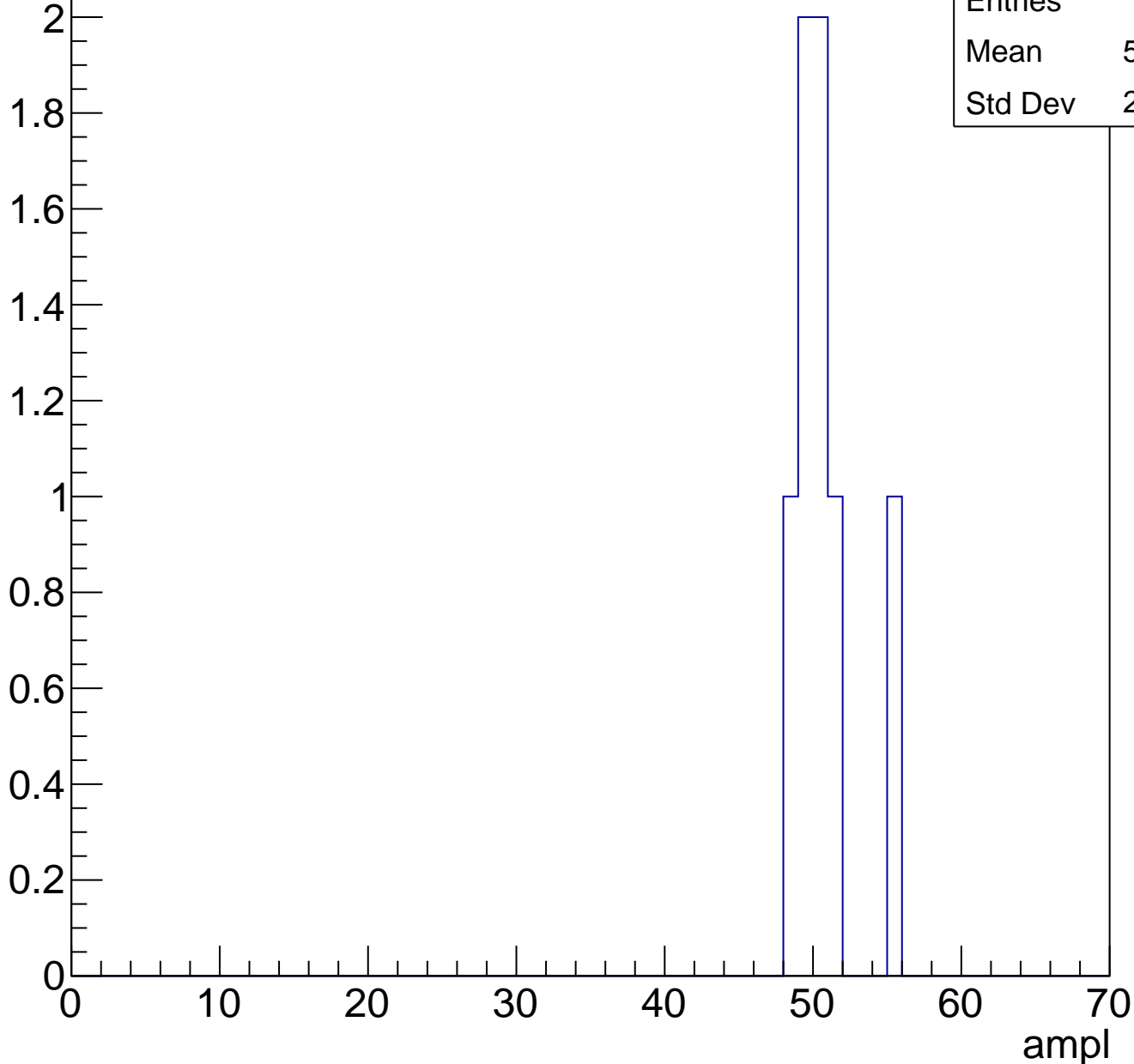


| | |
|---|---|
| Entries | 7 |
| Mean | 50.29 |
| Std Dev | 2.119 |

B0L100S, U1-ch47, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch47, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch47, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch47, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch48, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 29 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
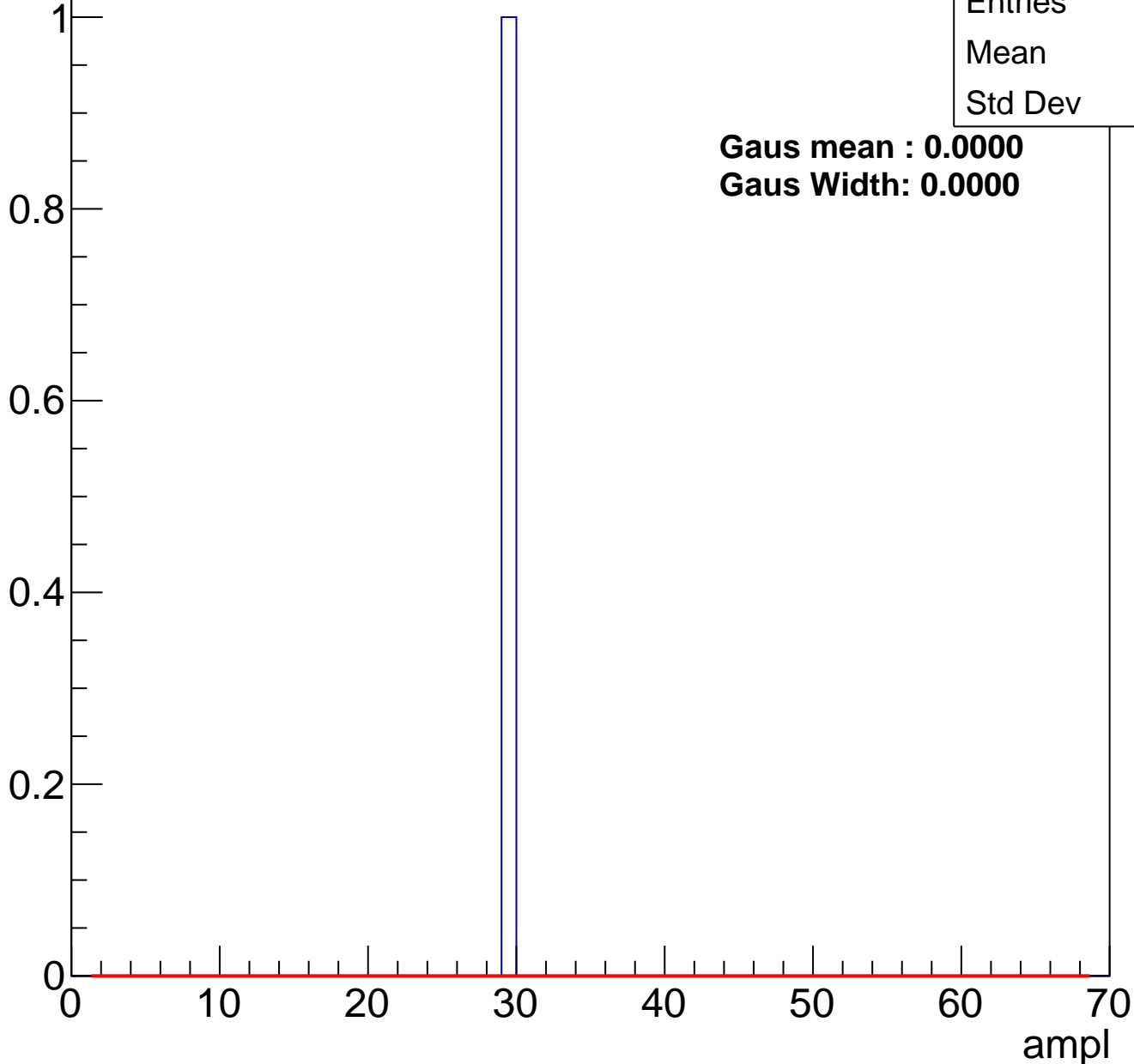
# B0L100S, U1-ch48, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**
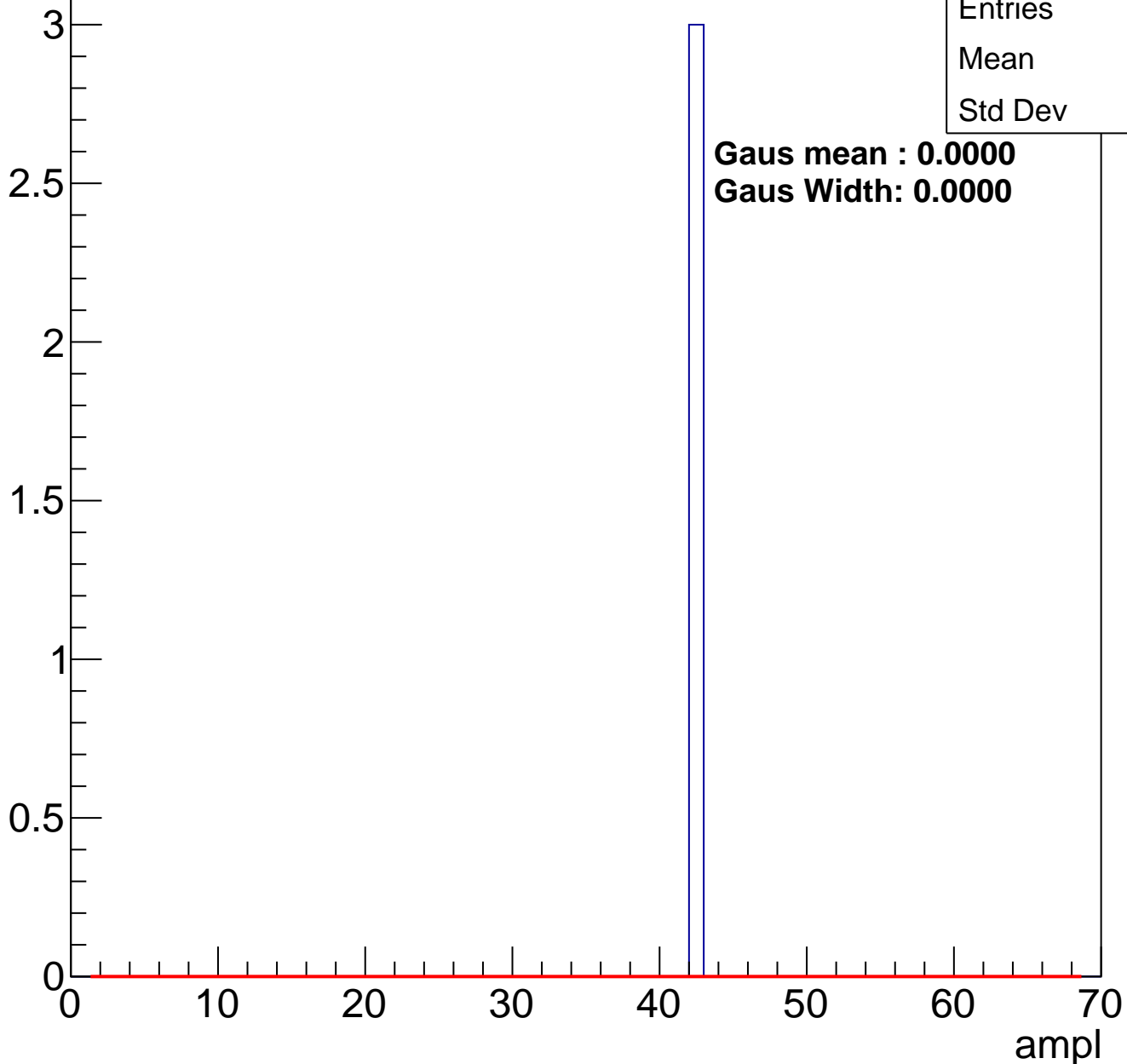
| | |
|---|---|
| Entries | 3 |
| Mean | 42 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch48, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 43 |
| Std Dev | 1 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
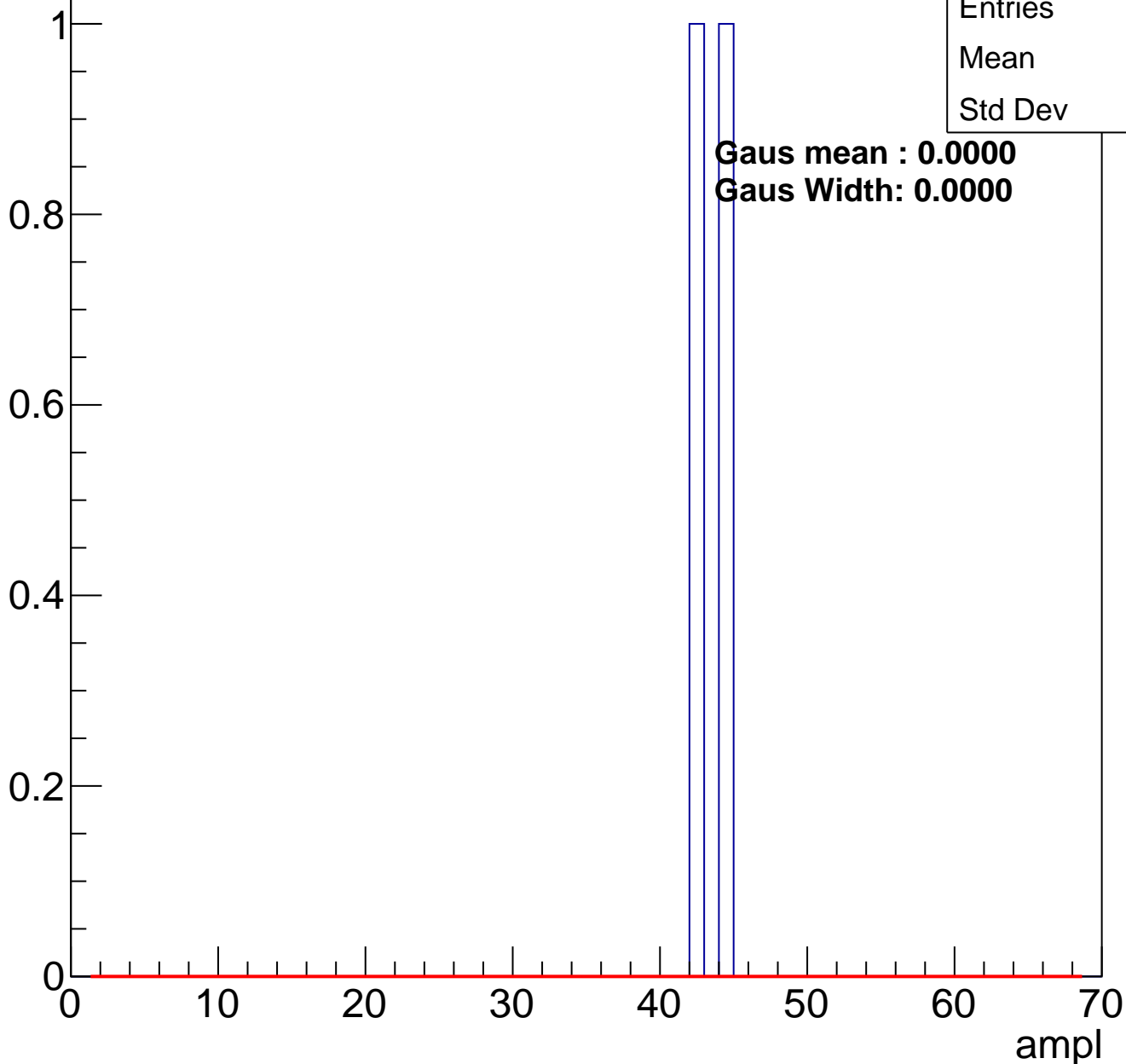
# B0L100S, U1-ch48, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch48, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch48, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch48, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch48, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch49, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch49, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch49, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch49, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
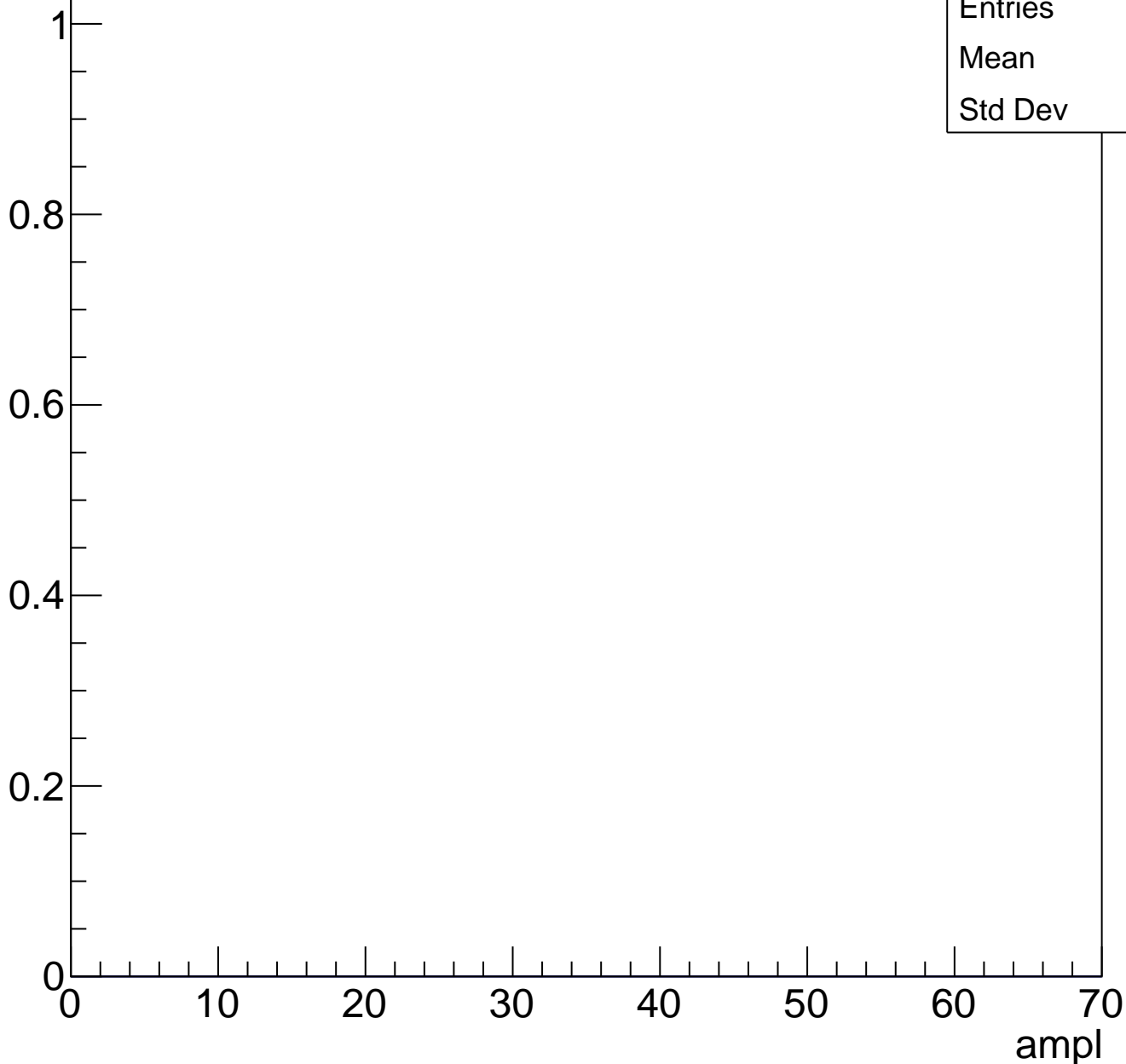
# B0L100S, U1-ch49, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch49, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch49, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch49, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch50, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch50, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch50, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 45 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch50, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch50, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch50, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

B0L100S, U1-ch50, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch50, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch51, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**
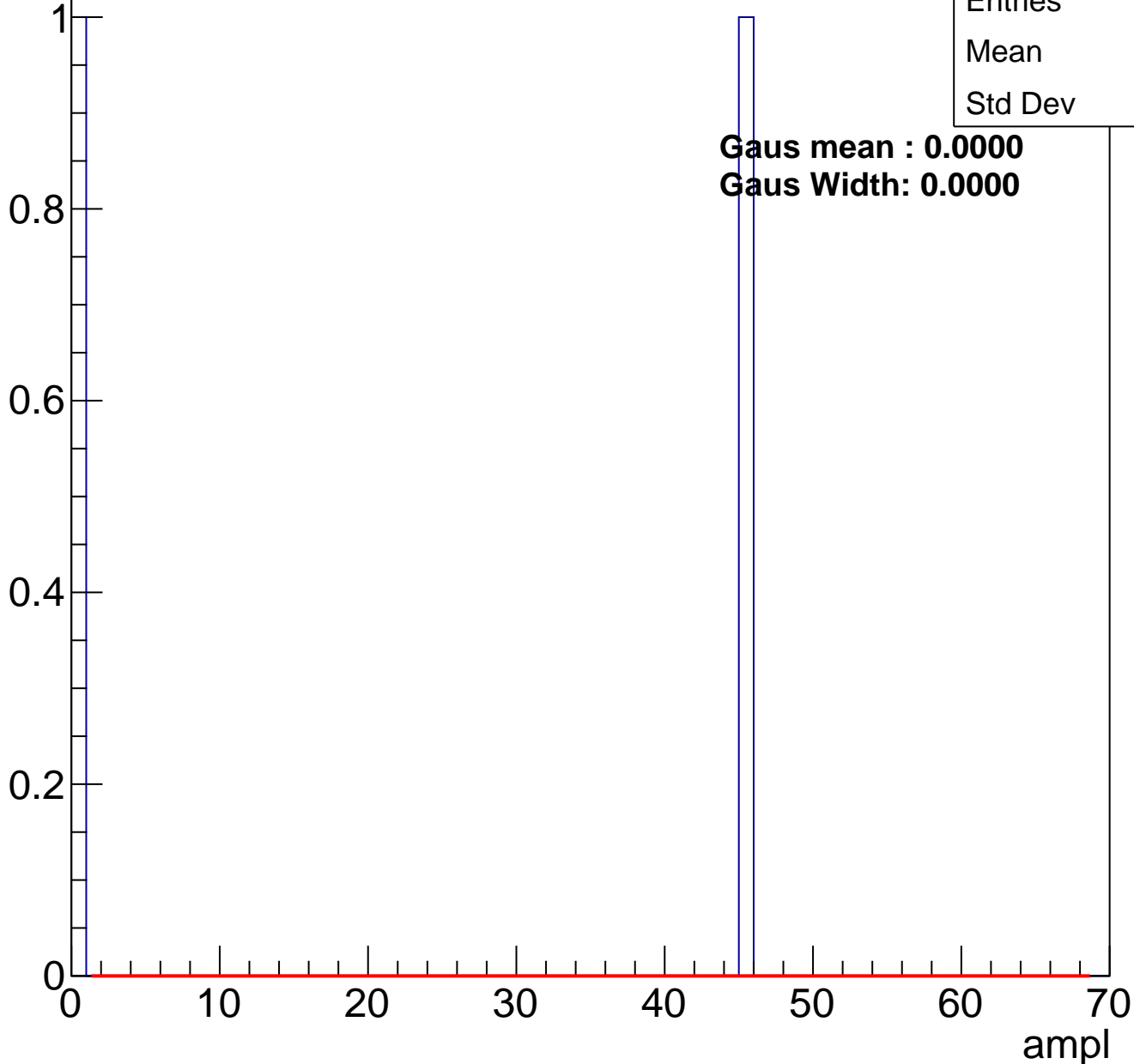
| Entries | 2 |
|---------|-----|
| Mean | 22.5 |
| Std Dev | 22.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch51, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 14 |
| Mean | 47.5 |
| Std Dev | 1.239 |

**Gaus mean : 48.8812**
**Gaus Width: 2.1418**
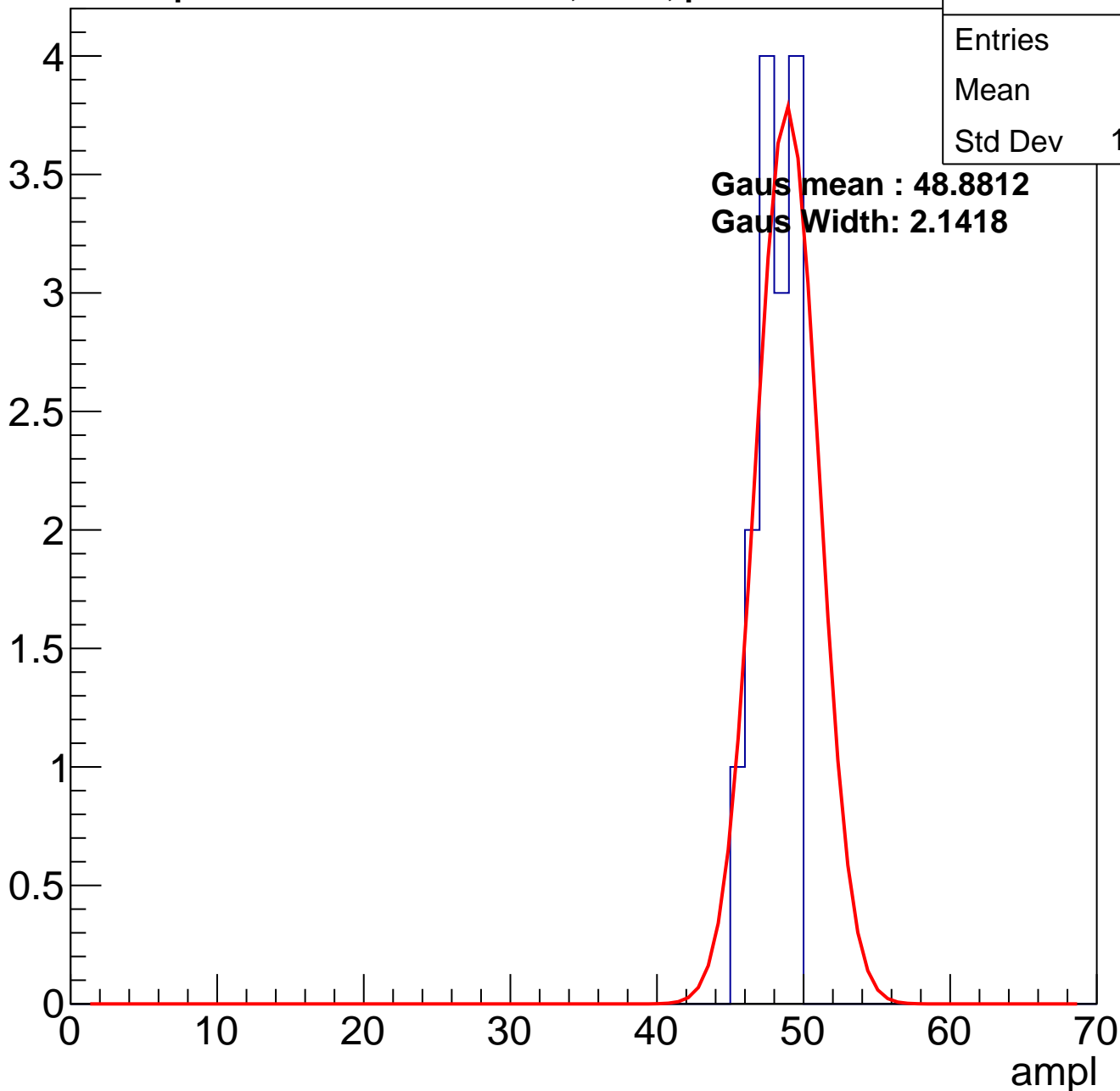
# B0L100S, U1-ch51, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 18 |
| Mean | 48.06 |
| Std Dev | 2.656 |

**Gaus mean : 48.8539**
**Gaus Width: 6.3140**

Entry

ampl
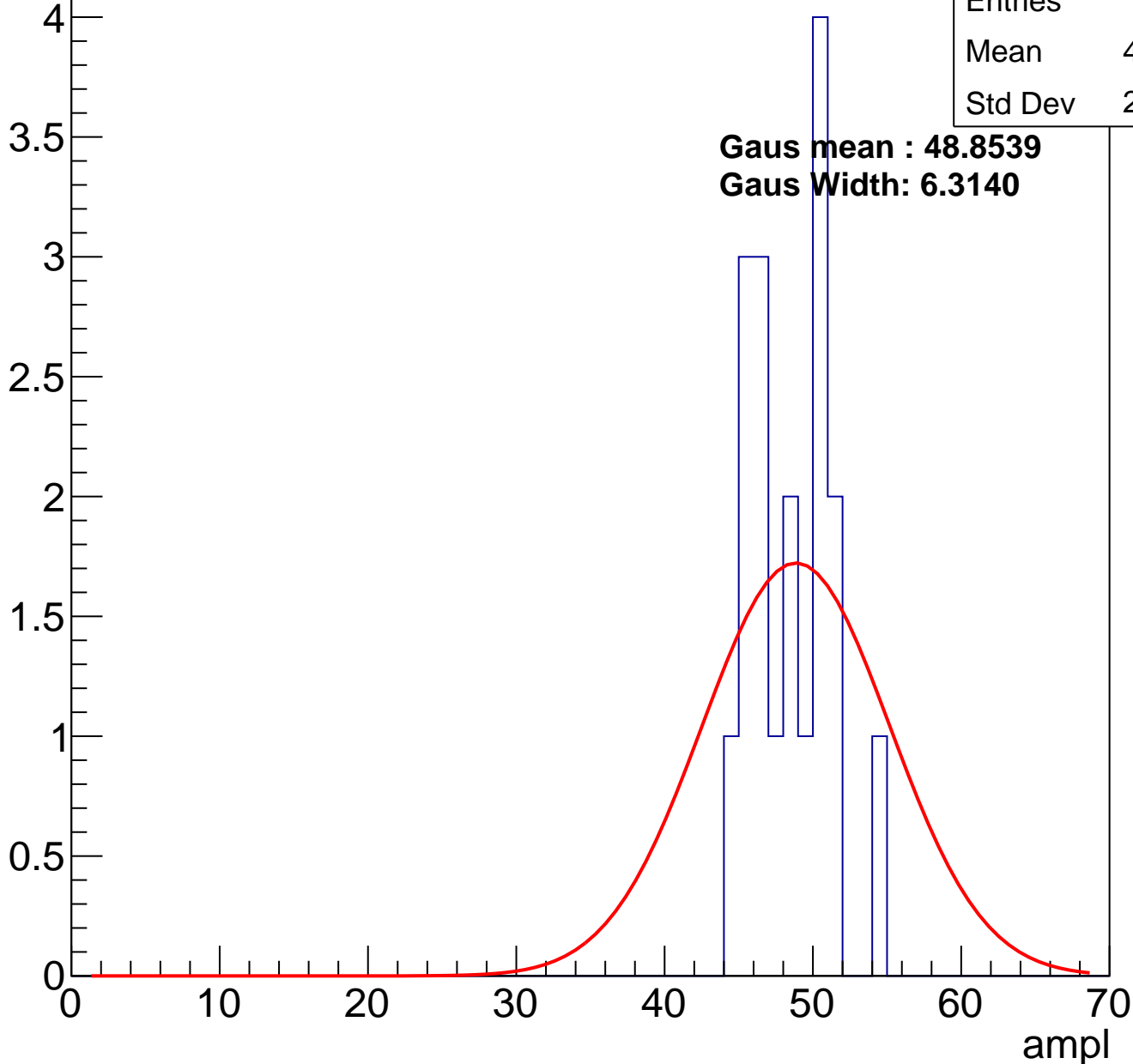
# B0L100S, U1-ch51, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 3 |
| Mean | 52.67 |
| Std Dev | 1.886 |

Entry

ampl
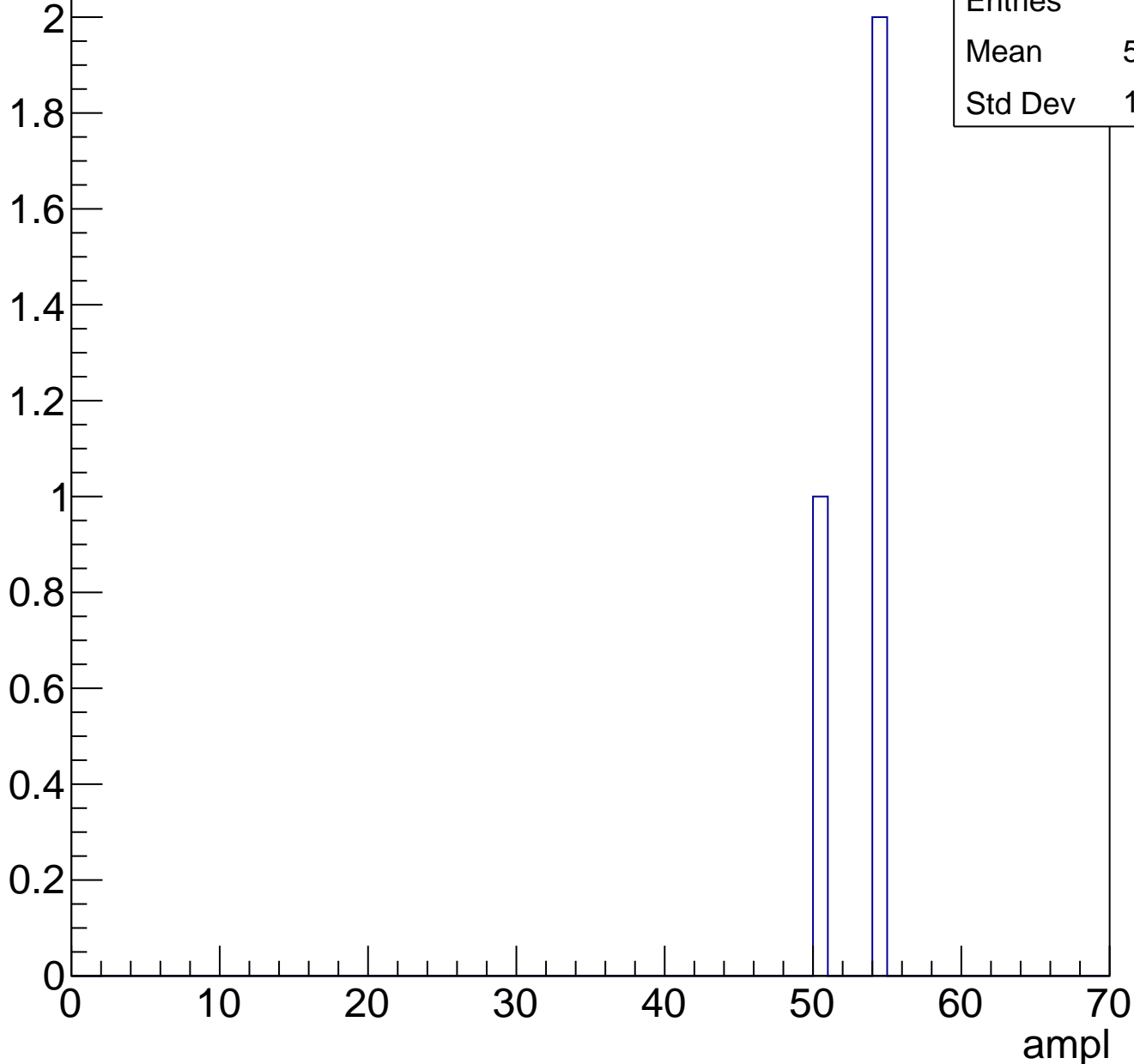
# B0L100S, U1-ch51, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch51, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch51, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch51, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch52, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch52, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch52, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch52, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch52, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch52, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch52, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch52, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch53, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch53, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch53, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch53, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch53, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch53, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch53, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch53, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch54, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 44 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
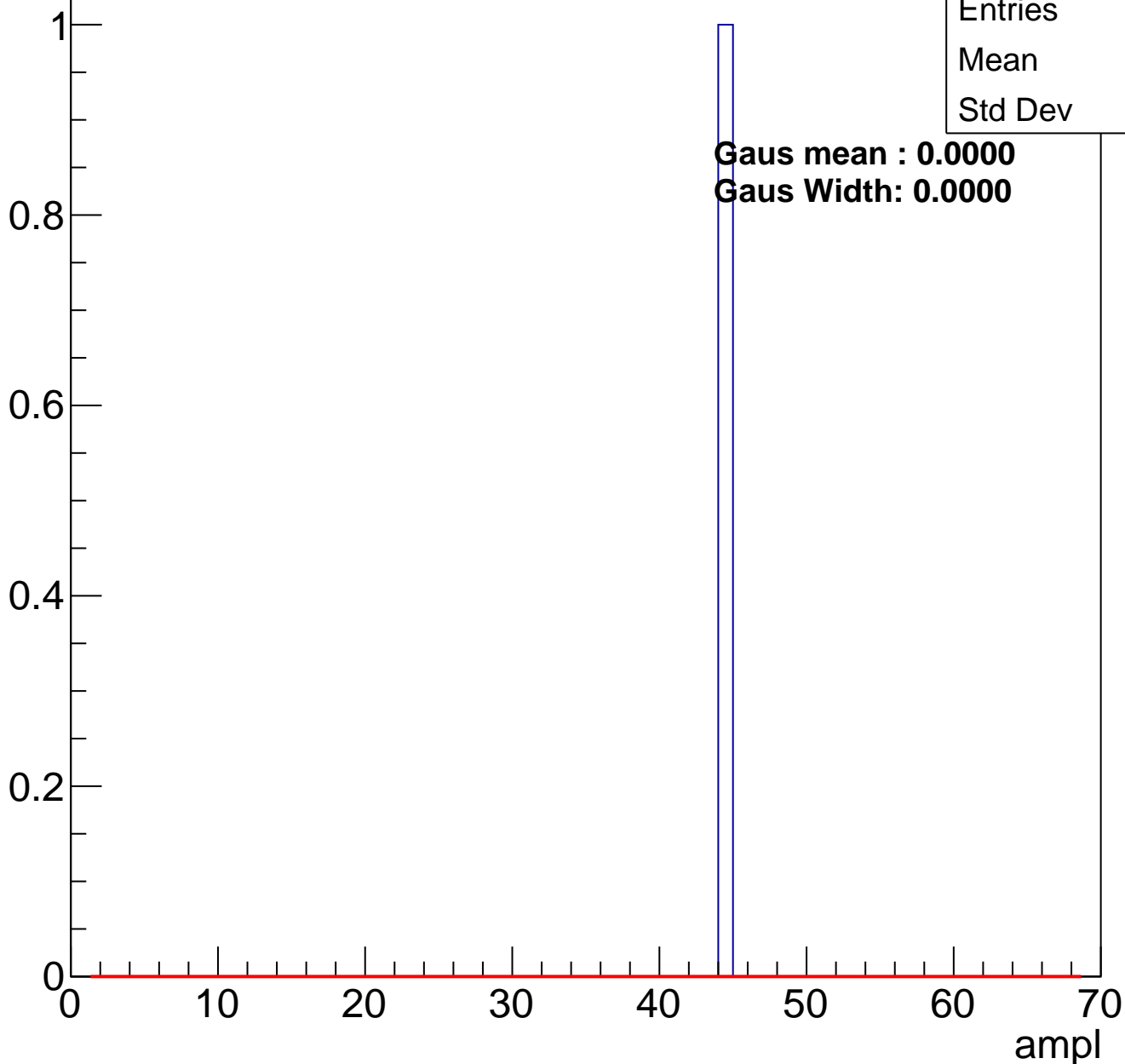
# B0L100S, U1-ch54, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 15 |
| Mean | 42.67 |
| Std Dev | 1.955 |

**Gaus mean : 42.7977**
**Gaus Width: 1.6638**
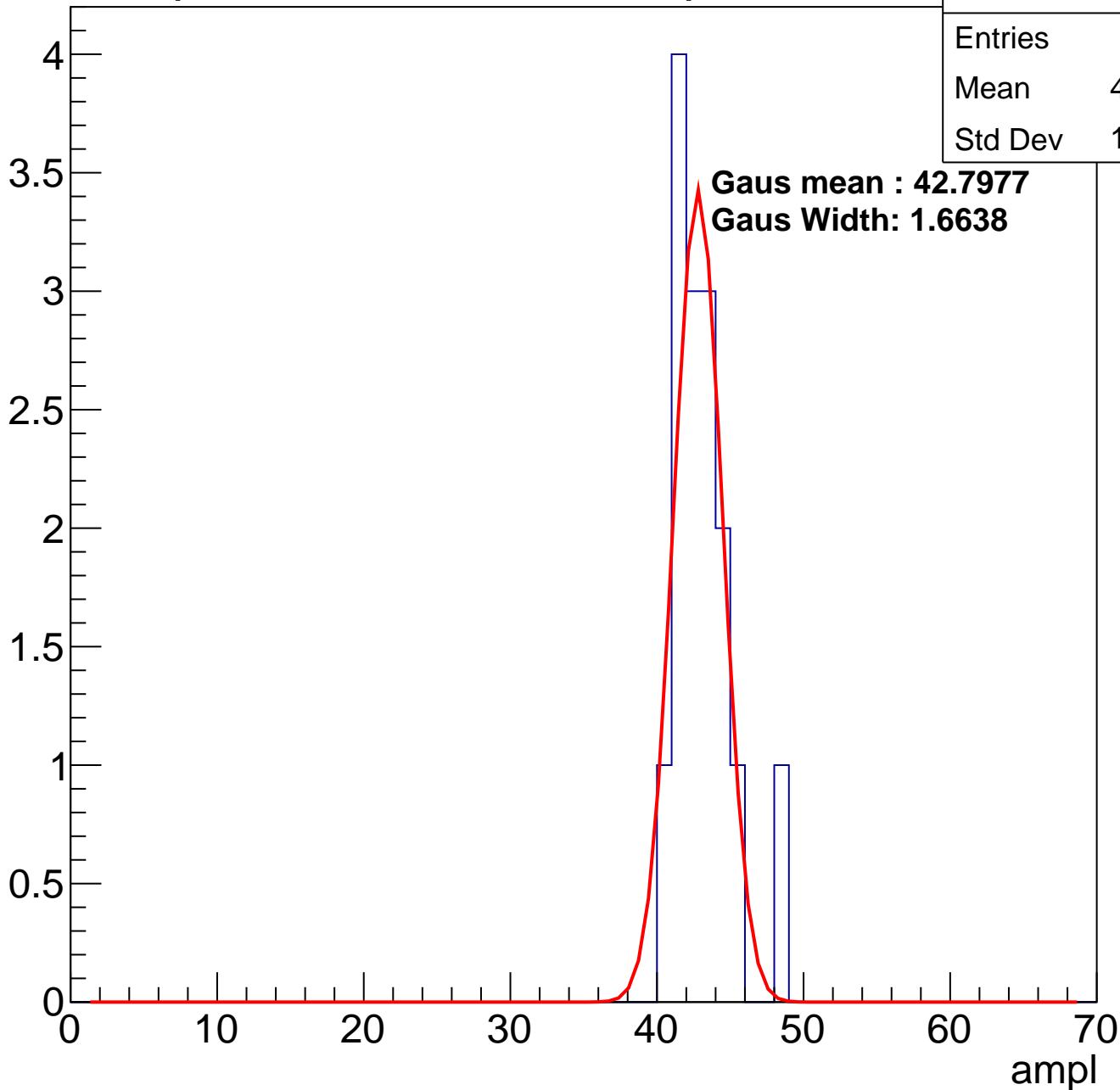
# B0L100S, U1-ch54, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 19 |
|---|---|
| Mean | 47.16 |
| Std Dev | 4.056 |

**Gaus mean : 47.0844**
**Gaus Width: 6.1156**
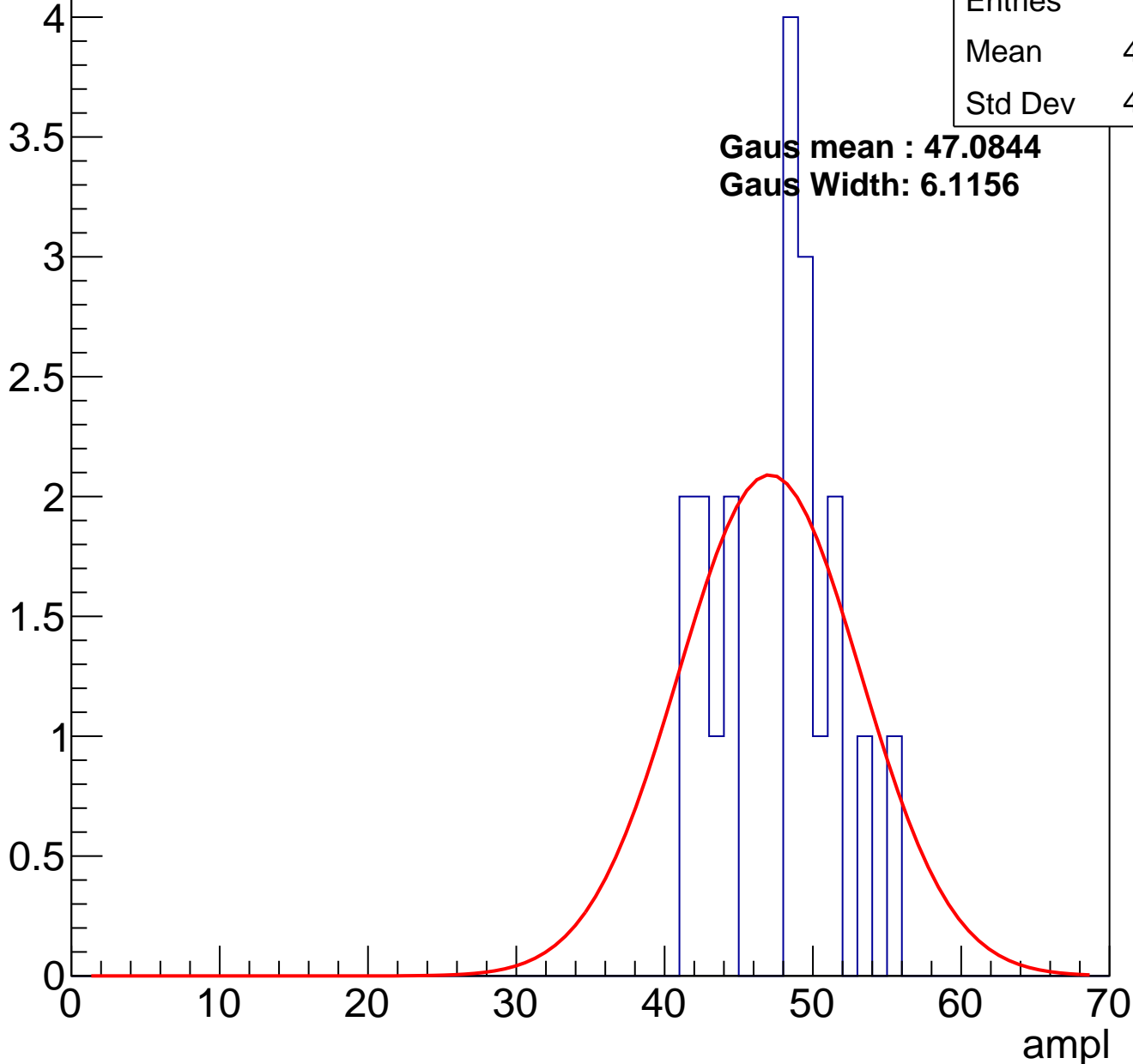
# B0L100S, U1-ch54, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 5 |
| Mean | 52 |
| Std Dev | 1.897 |

ampl
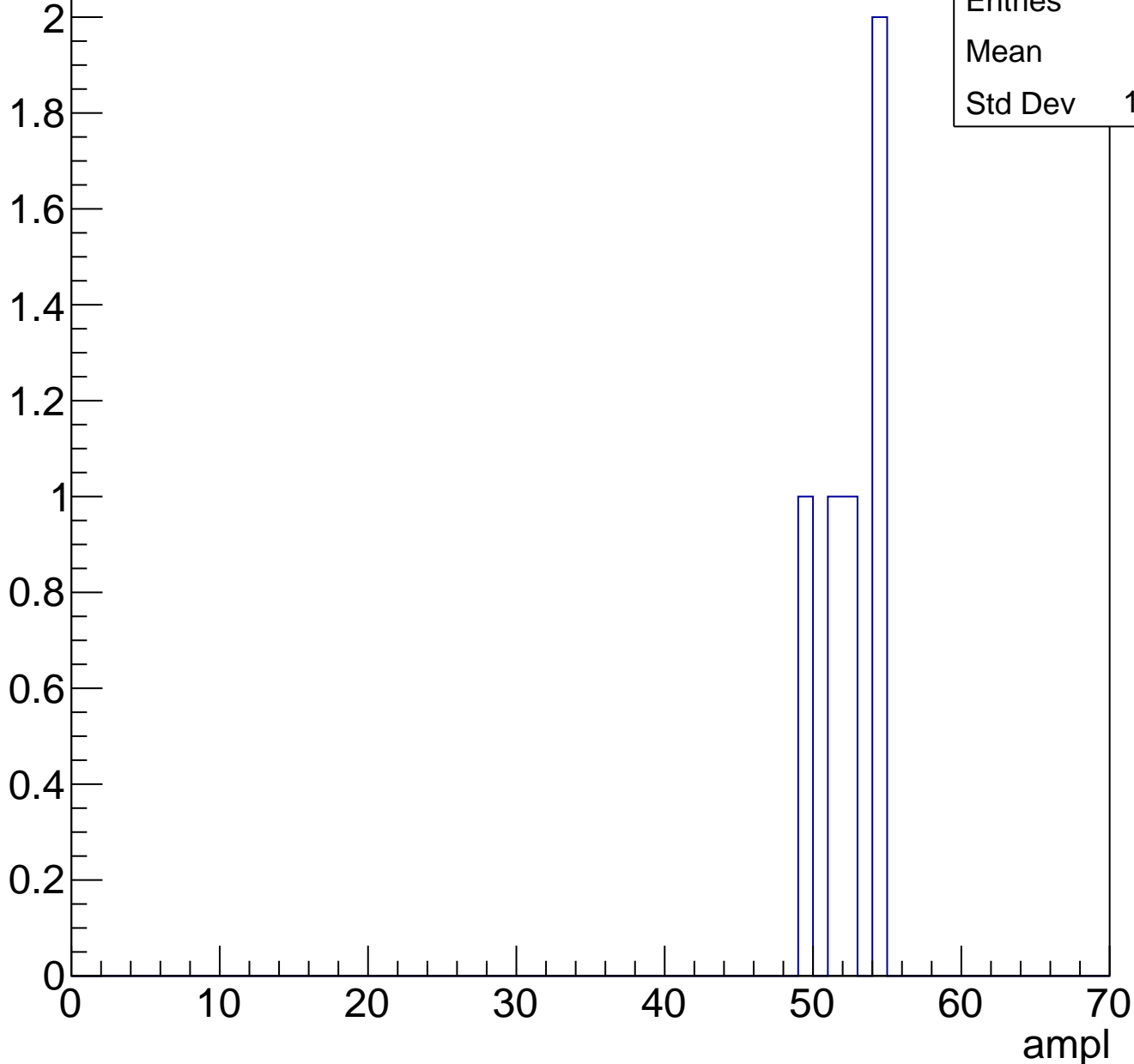
Entry

# B0L100S, U1-ch54, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 3 |
| Mean | 54 |
| Std Dev | 0.8165 |

Entry

ampl
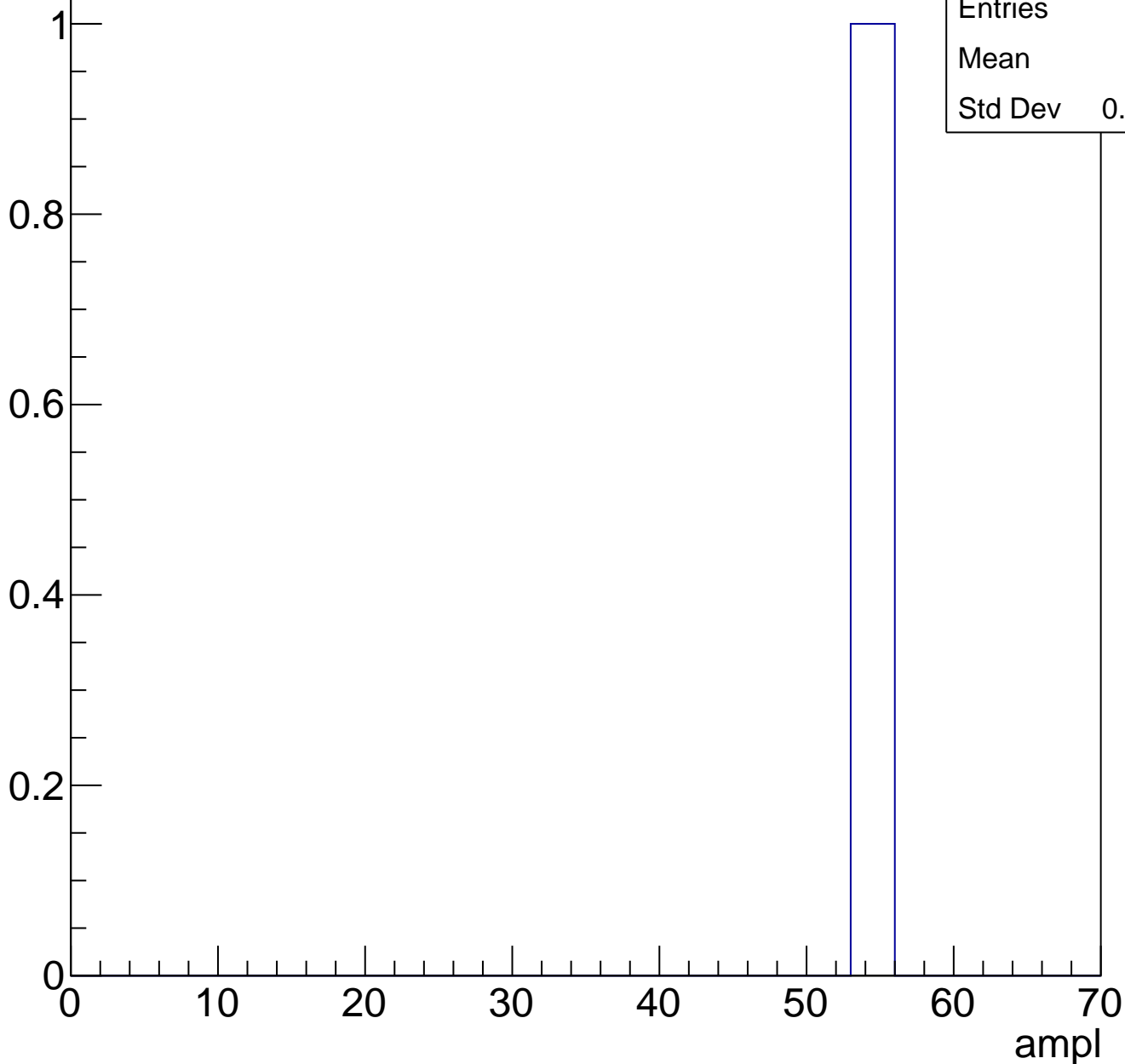
# B0L100S, U1-ch54, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch54, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch54, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch55, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch55, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 43 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch55, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 43 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
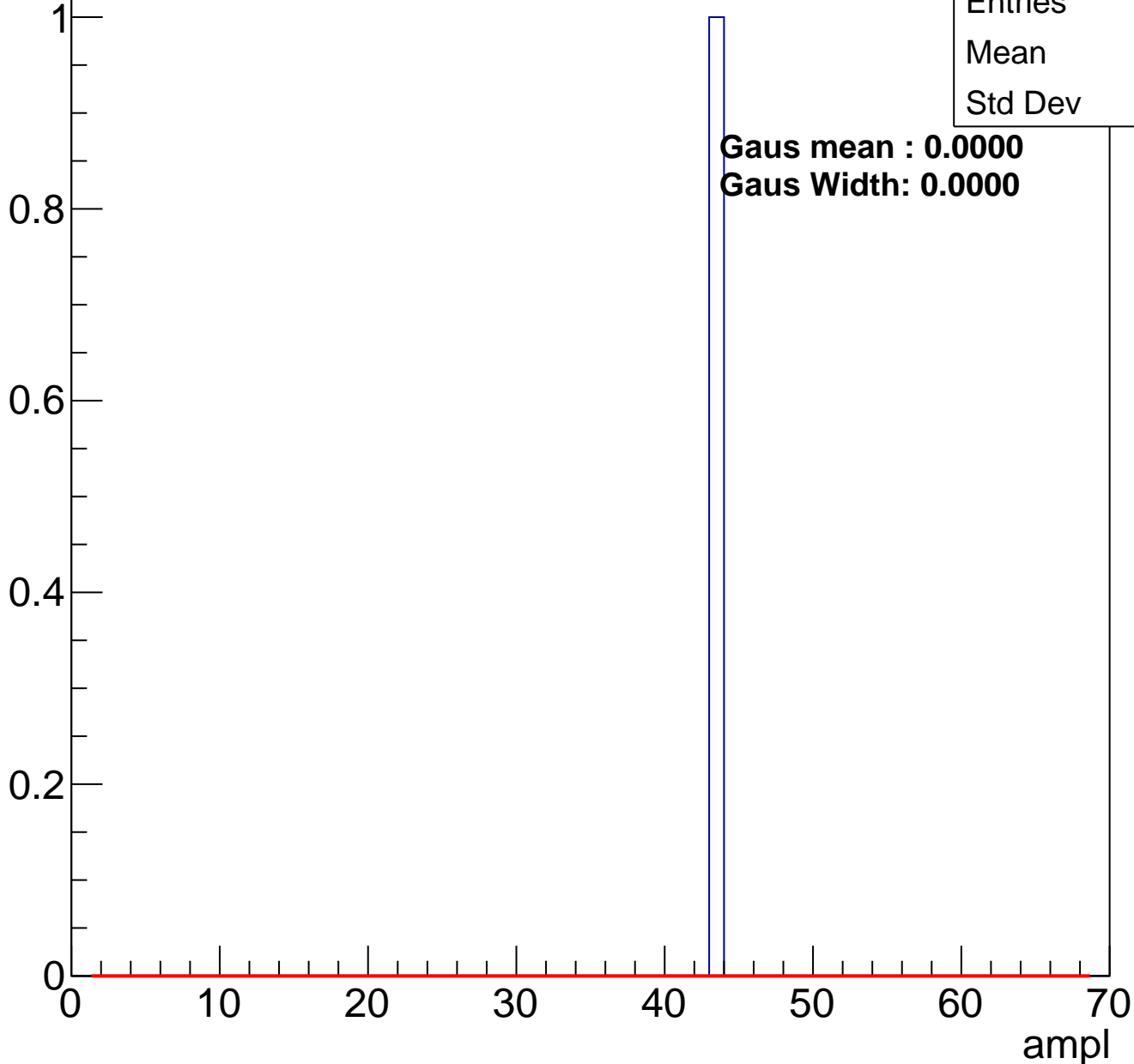
# B0L100S, U1-ch55, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch55, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch55, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch55, adc6
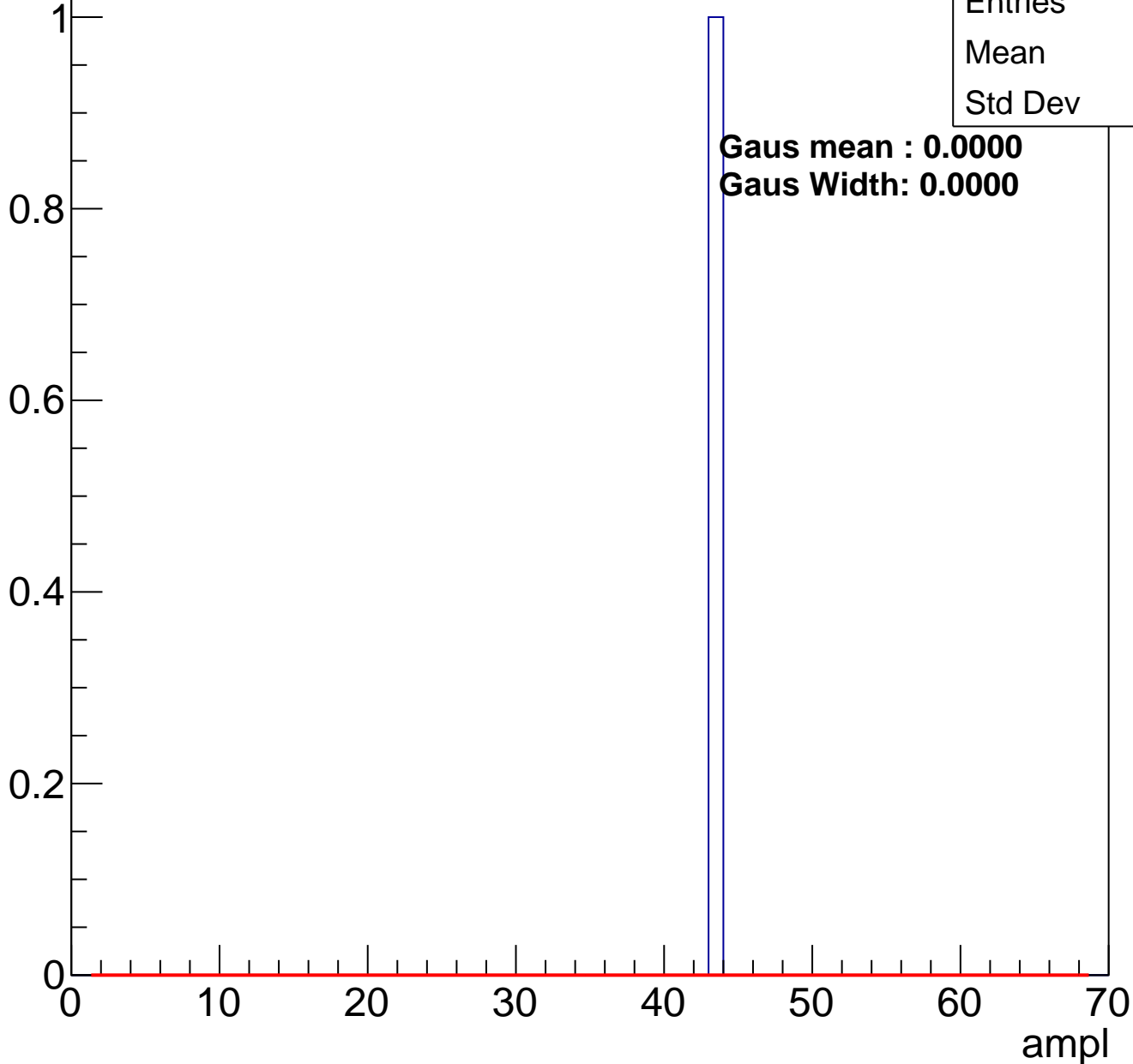
**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

ampl

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch55, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch56, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch56, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 3 |
|---|---|
| Mean | 45.33 |
| Std Dev | 2.055 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch56, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 4 |
| Mean | 48 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
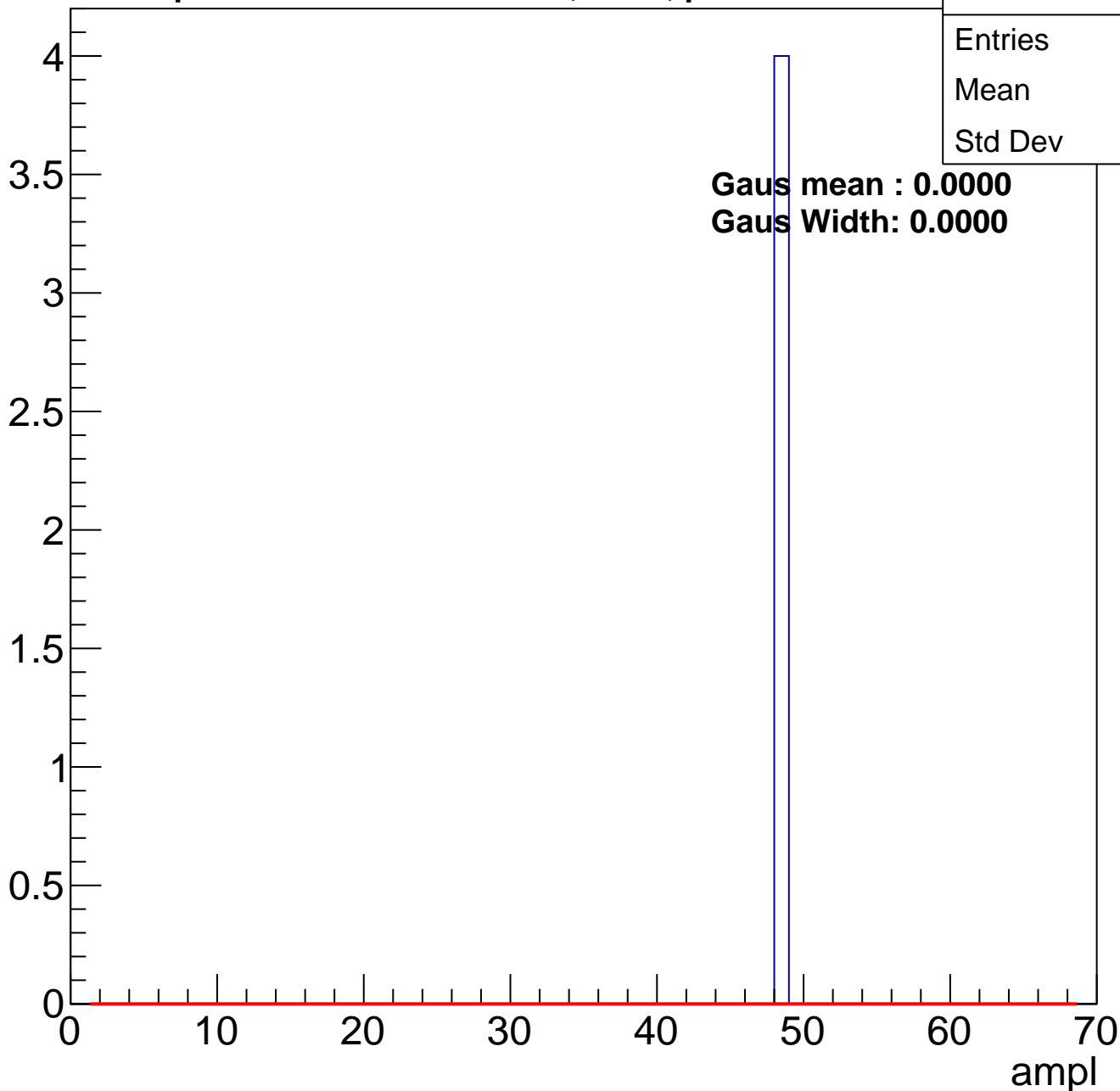
# B0L100S, U1-ch56, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch56, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch56, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch56, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch56, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

ampl

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch57, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch57, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
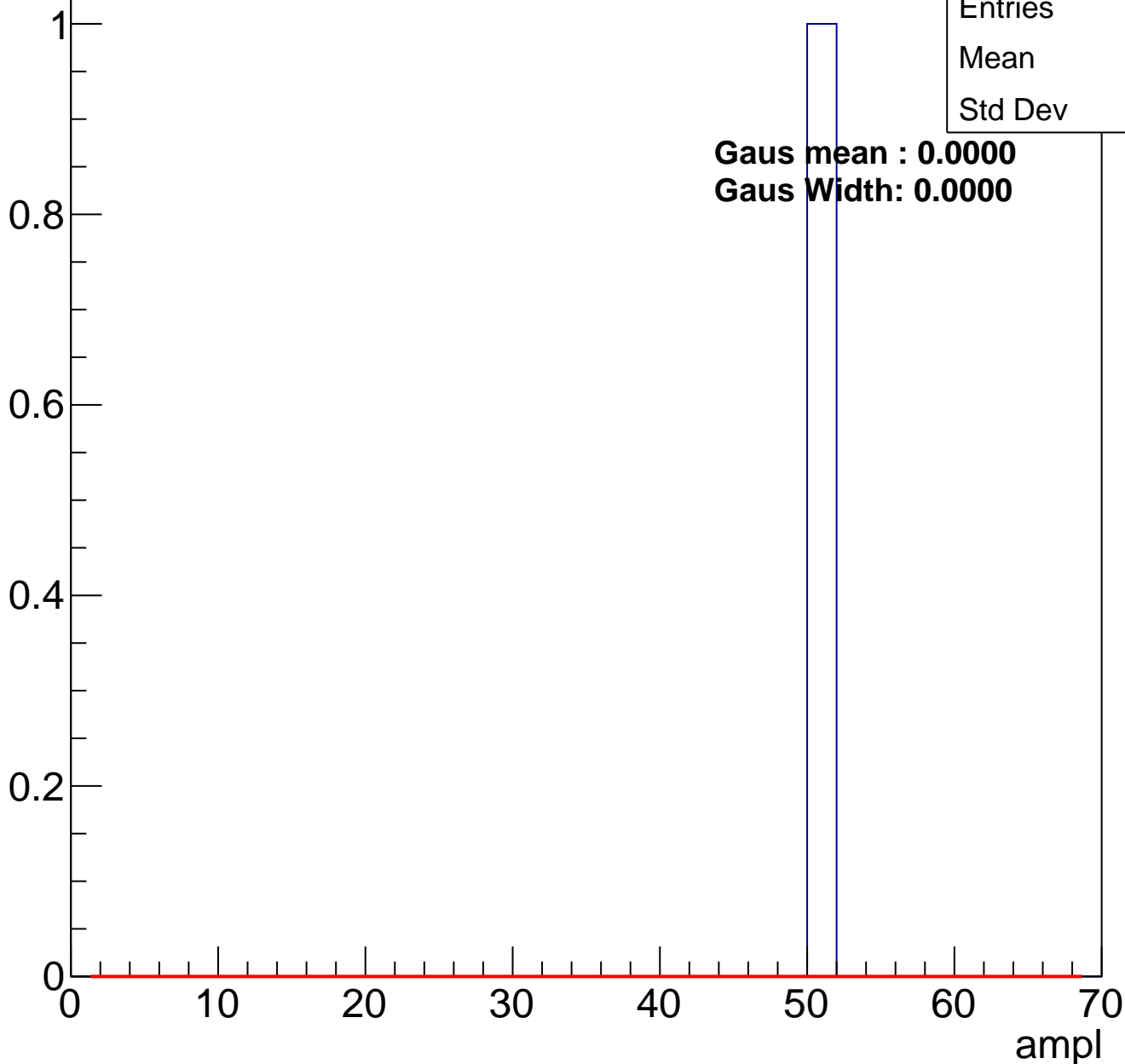
# B0L100S, U1-ch57, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 50.5 |
| Std Dev | 0.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch57, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch57, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch57, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch57, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch57, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L100S, U1-ch58, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch58, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 2 |
| --- | --- |
| Mean | 40 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
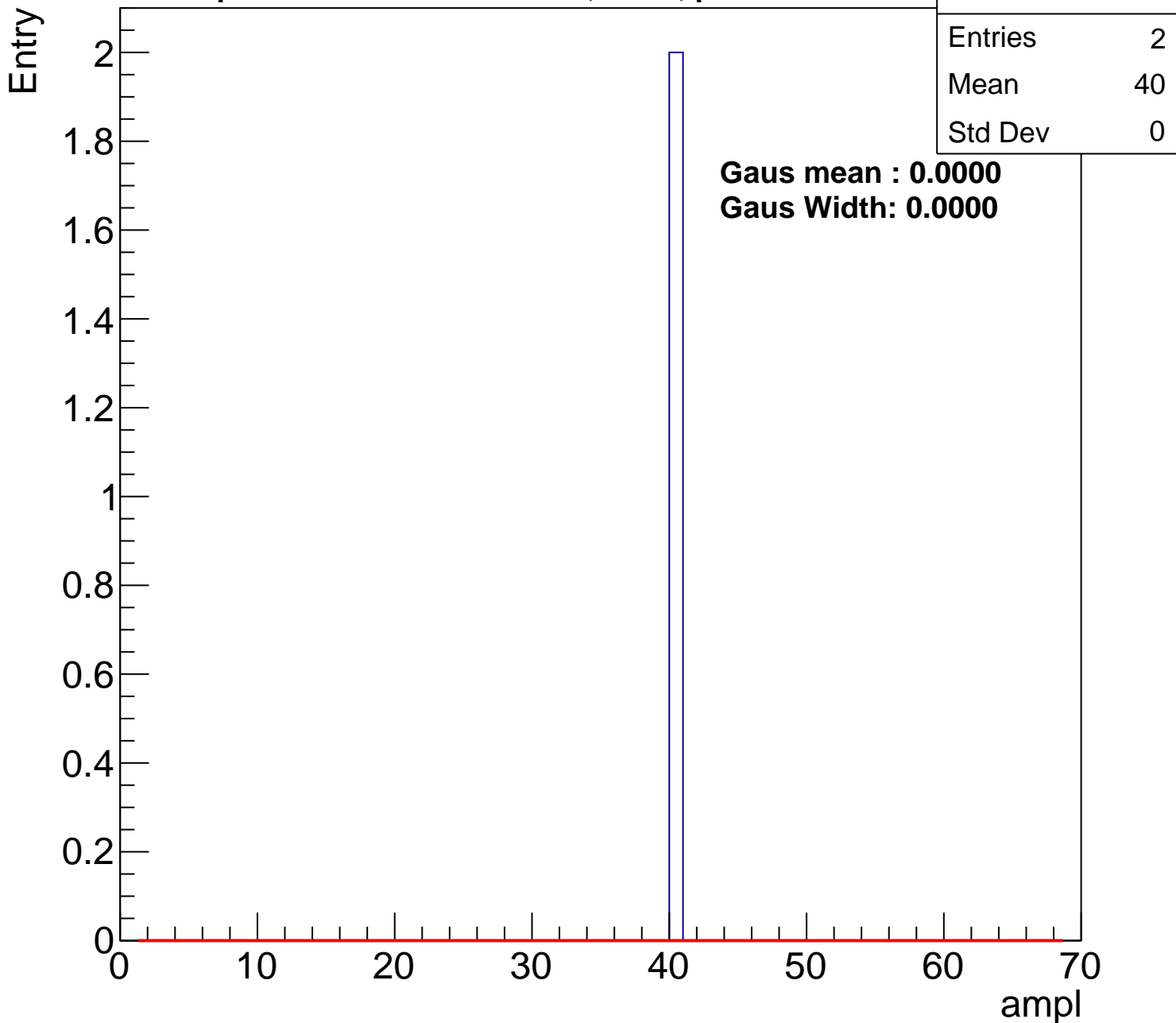
# B0L100S, U1-ch58, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch58, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 51 |
| Std Dev | 0 |

ampl
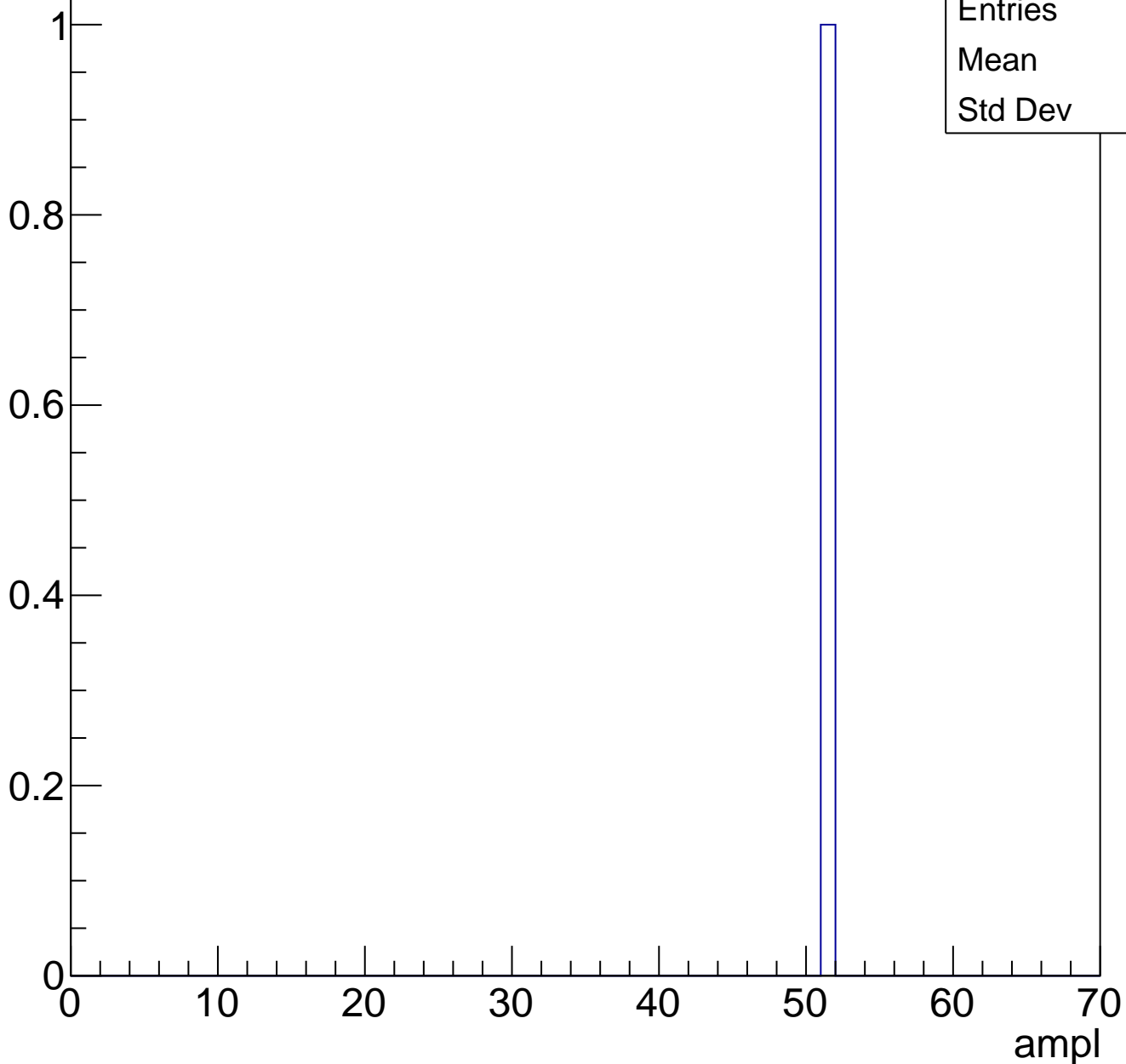
# B0L100S, U1-ch58, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch58, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch58, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch58, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch59, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch59, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 5 |
|---|---|
| Mean | 47.4 |
| Std Dev | 1.855 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
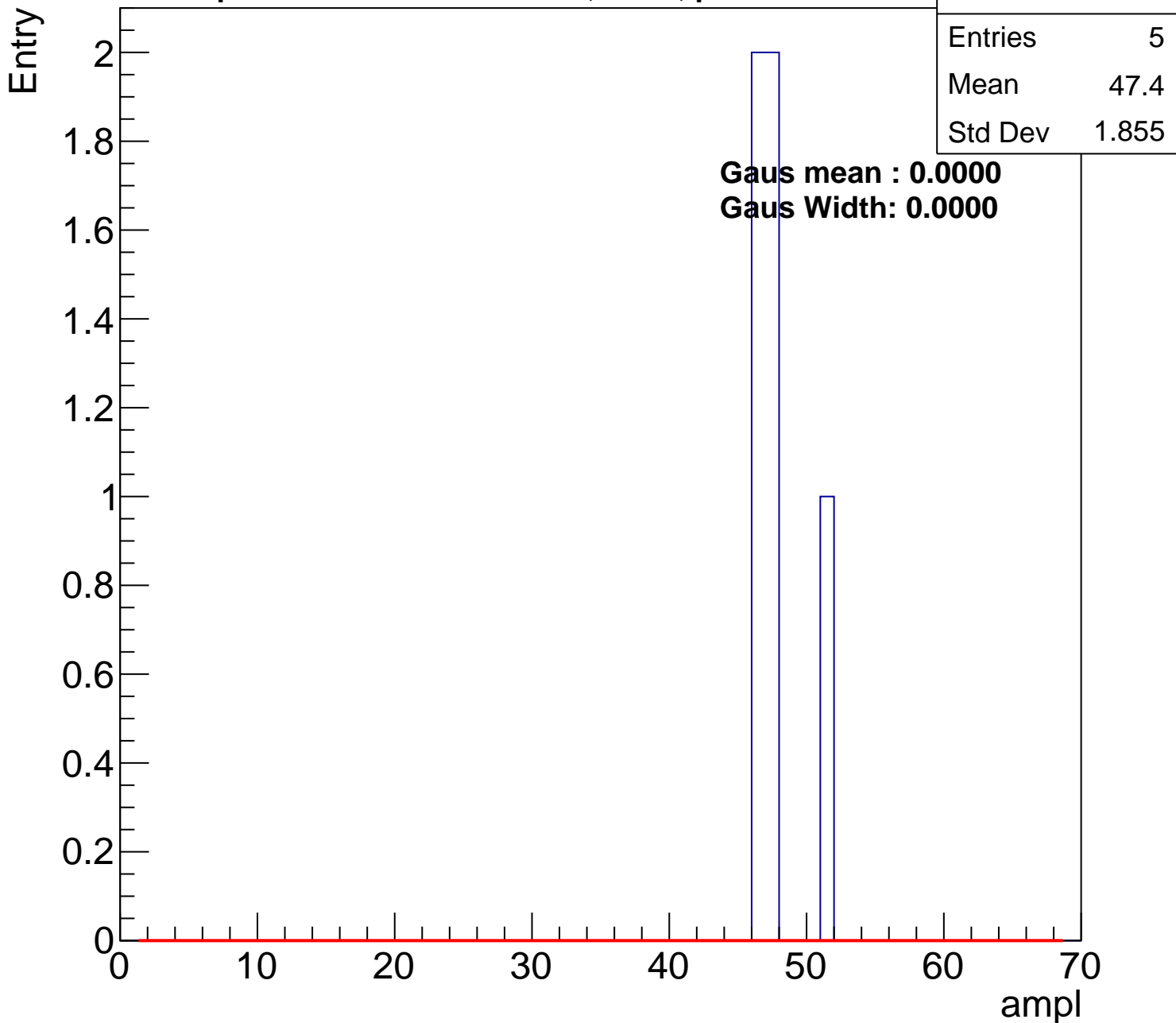
Entry

ampl

# B0L100S, U1-ch59, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 15 |
| Mean | 47.6 |
| Std Dev | 2.417 |

**Gaus mean : -17.7990**
**Gaus Width: 23.7595**
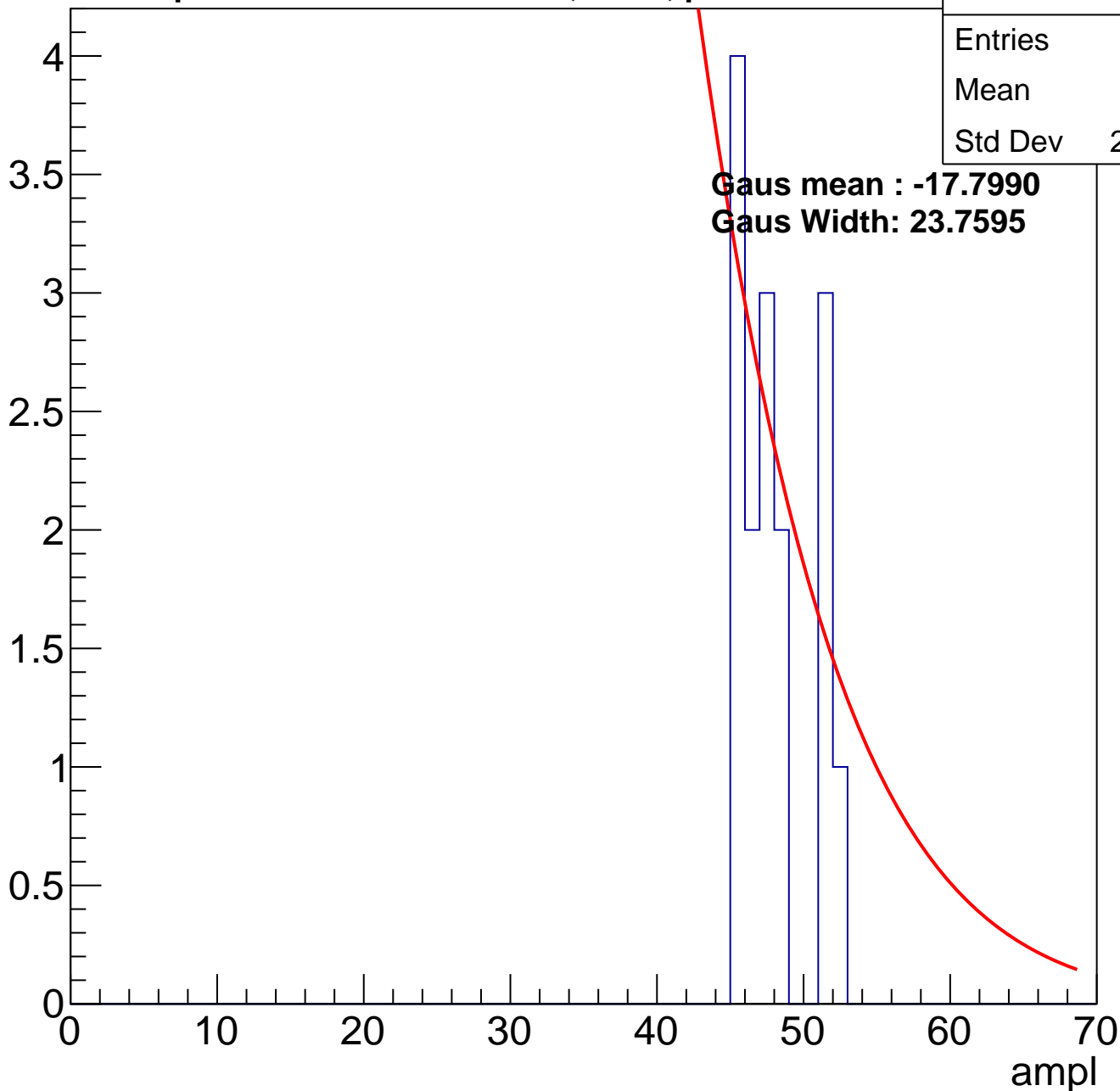
# B0L100S, U1-ch59, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 5 |
| Mean | 49.8 |
| Std Dev | 2.4 |
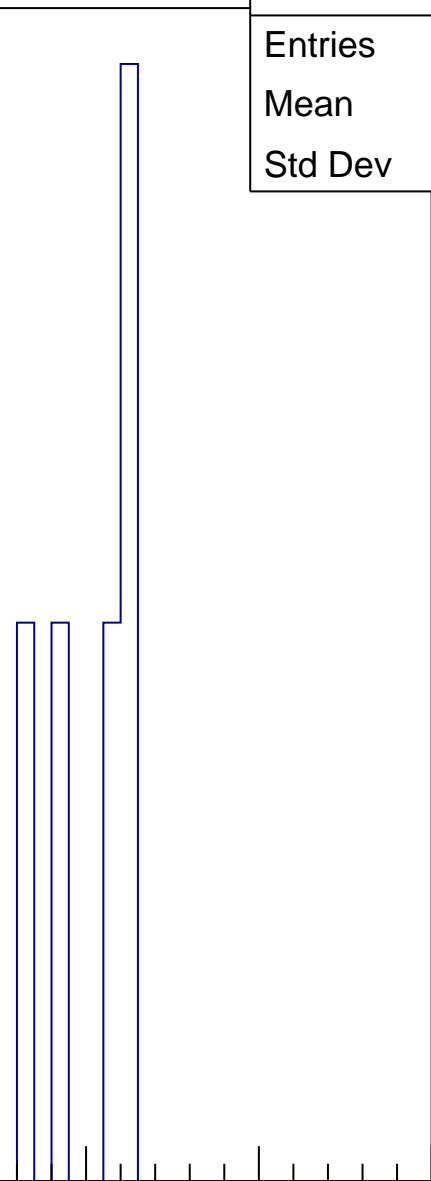
B0L100S, U1-ch59, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch59, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch59, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch59, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch60, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch60, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch60, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch60, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch60, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch60, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch60, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch60, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch61, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch61, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch61, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch61, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch61, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch61, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch61, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch61, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch62, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 49 |
| Std Dev | 0 |
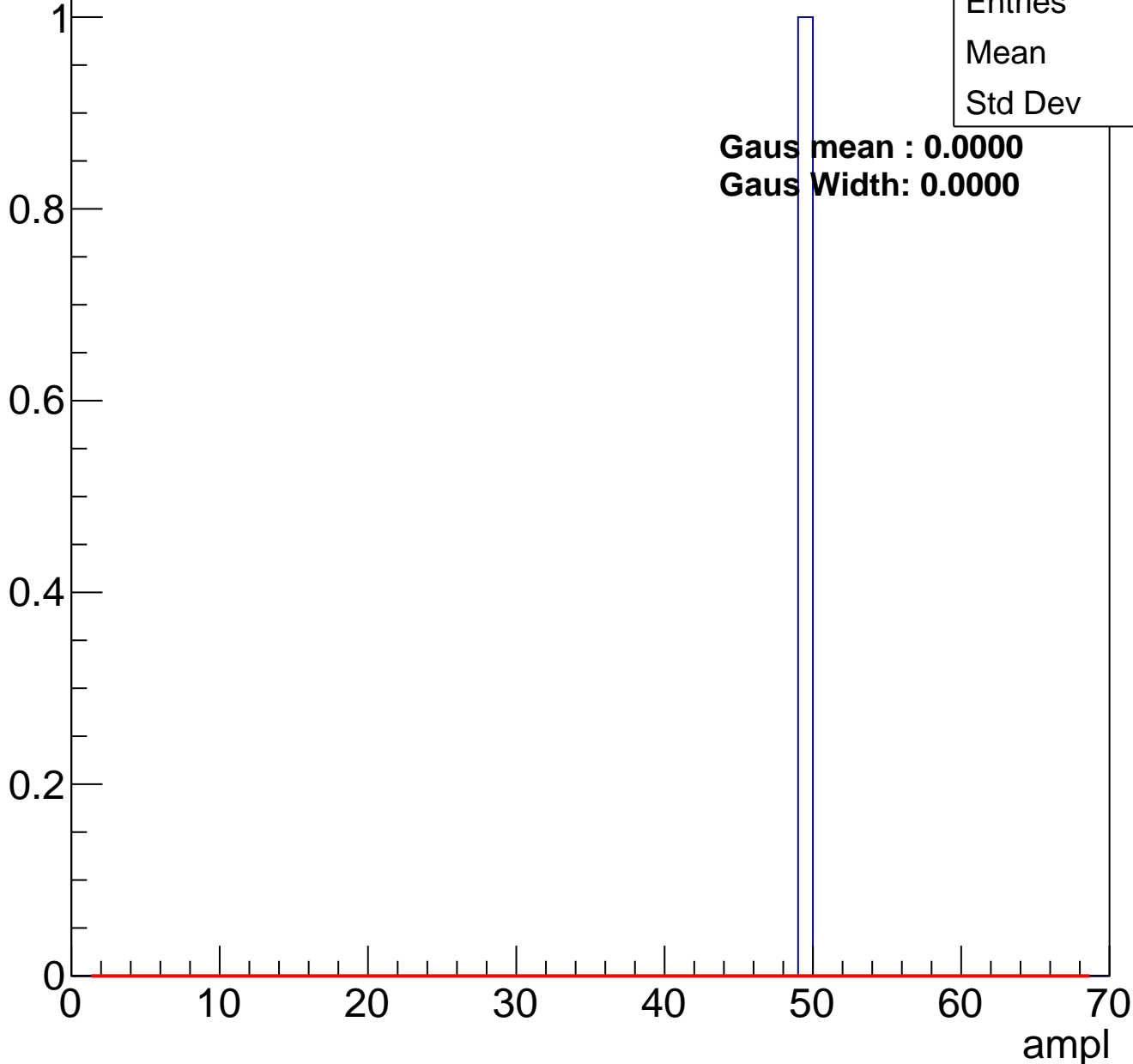
**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch62, adc1

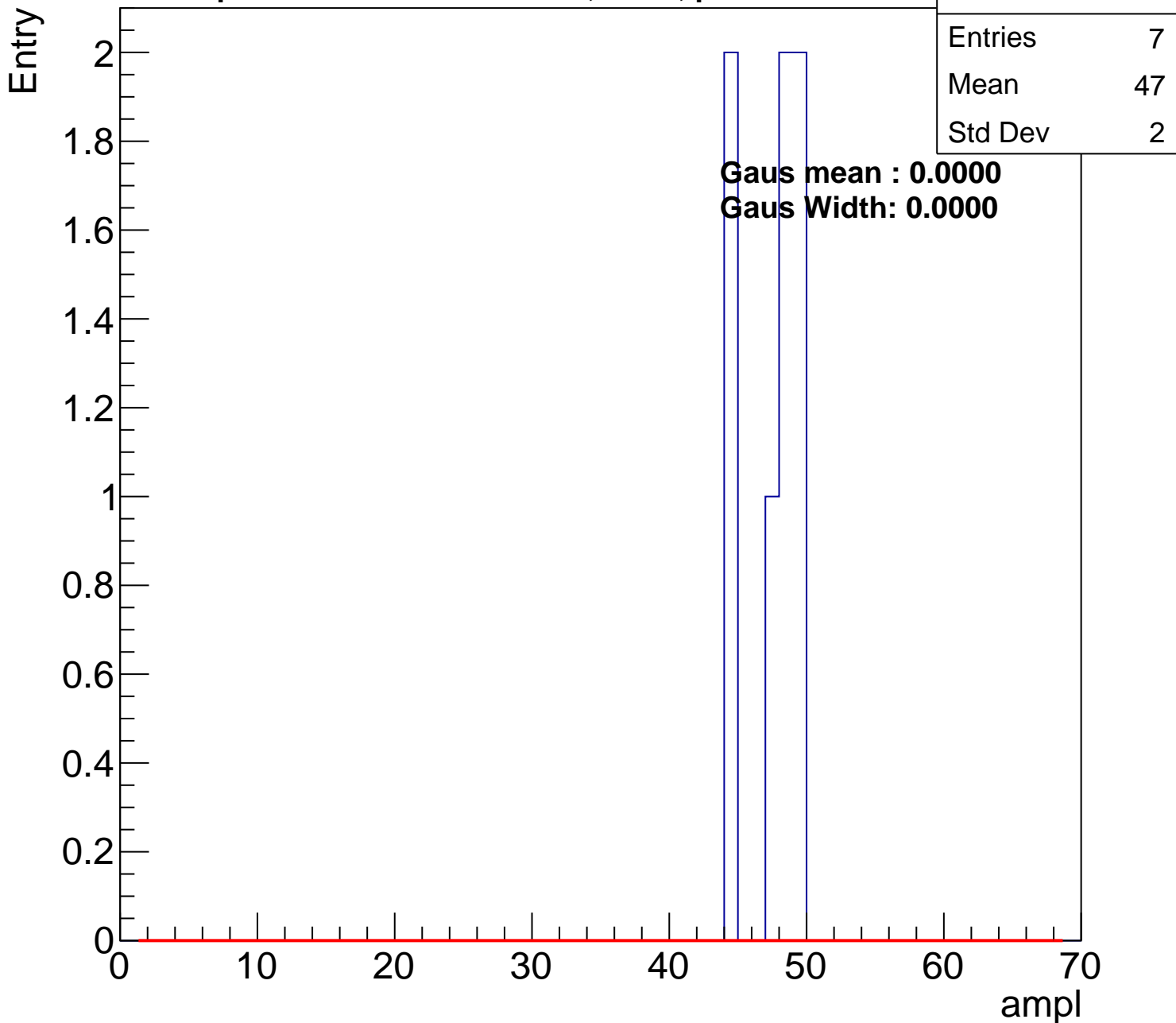calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 7 |
| Mean | 47 |
| Std Dev | 2 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U1-ch62, adc2

calib_packv5_042523_0143.root, FC#6, port A1

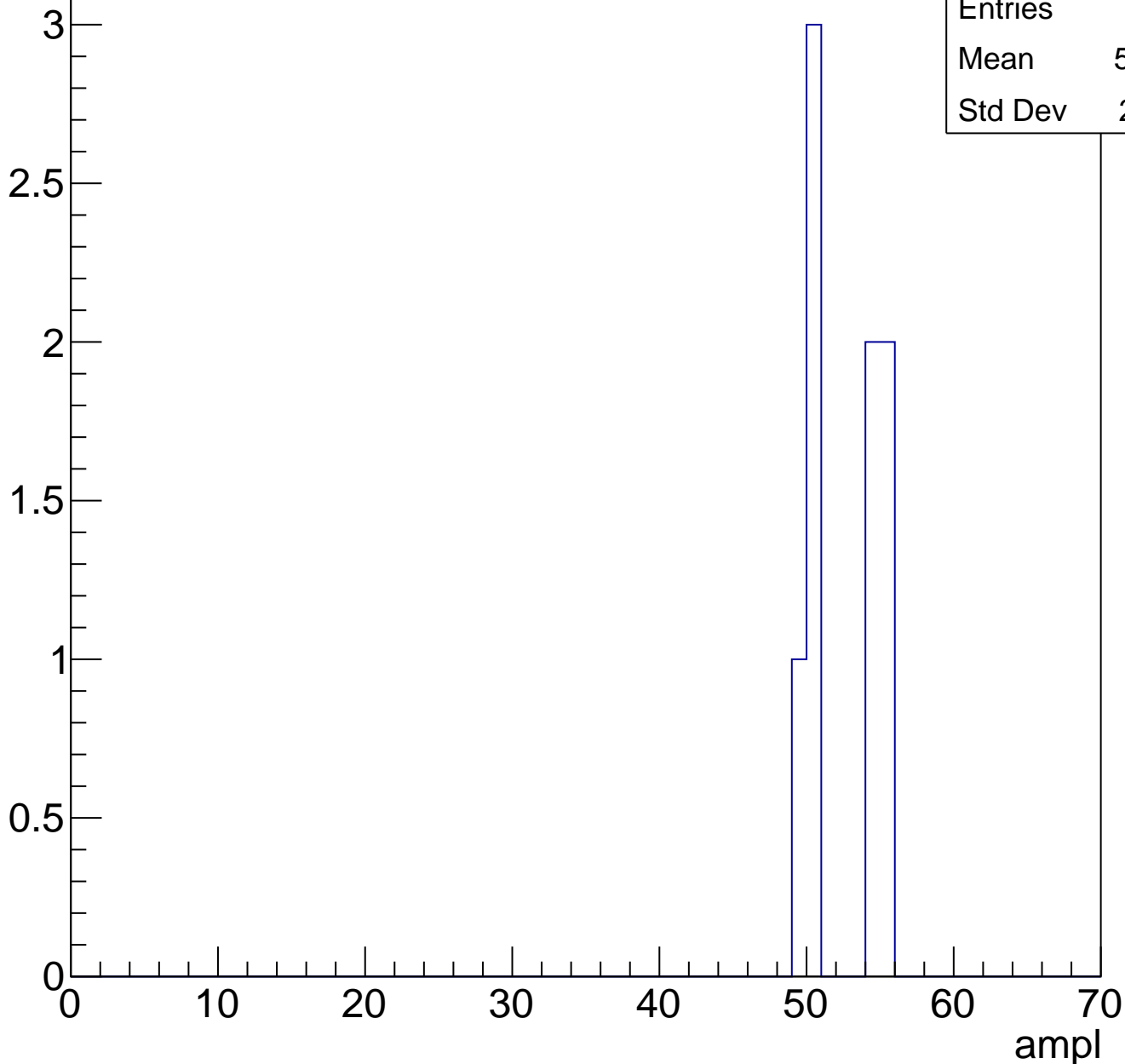| Entries | 11 |
| Mean | 49.82 |
| Std Dev | 2.657 |

Gaus mean : 39.3859
Gaus Width: 9.7282
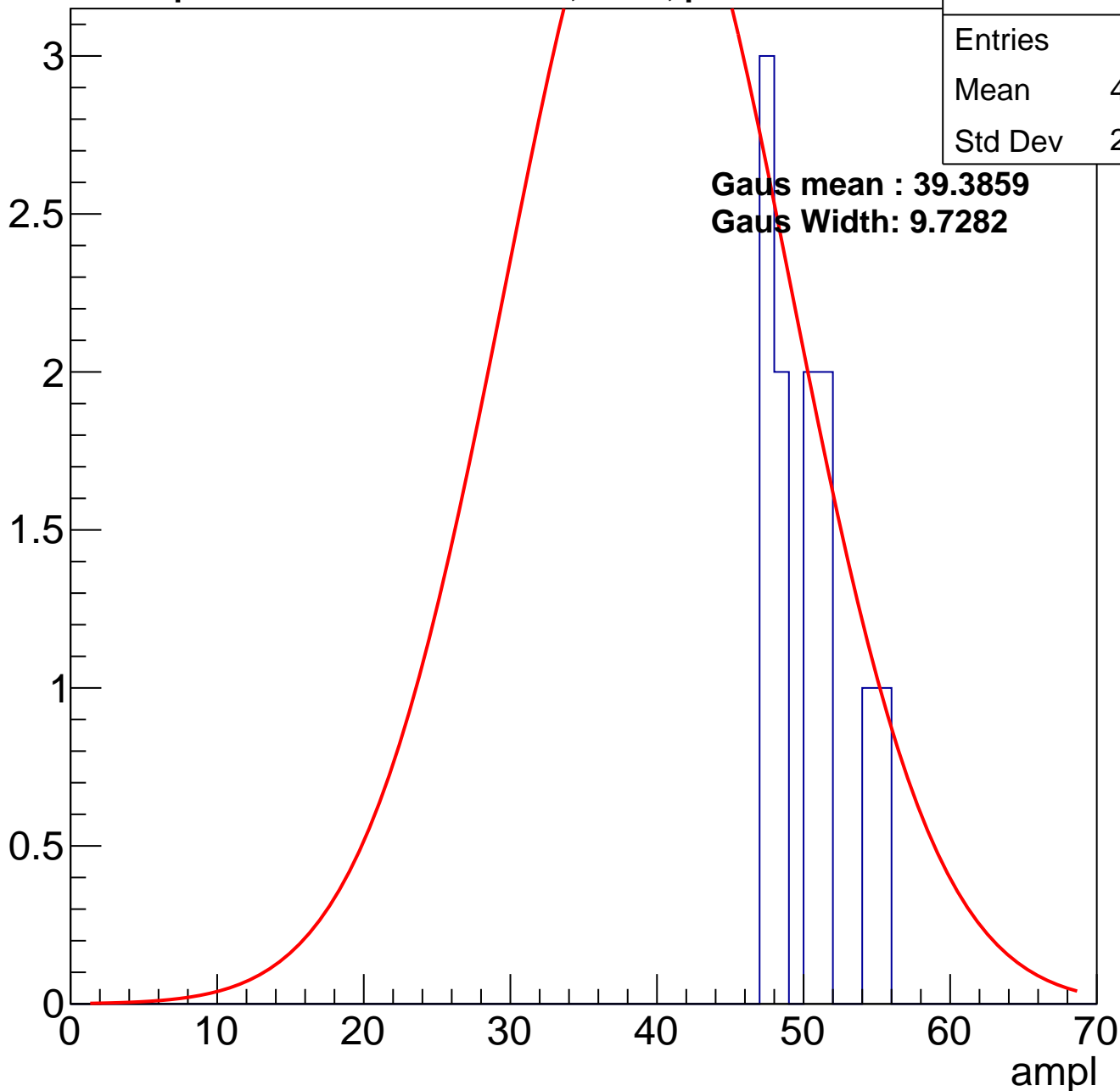
# B0L100S, U1-ch62, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 8 |
| Mean | 52.12 |
| Std Dev | 2.421 |

Entry

ampl

# B0L100S, U1-ch62, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**
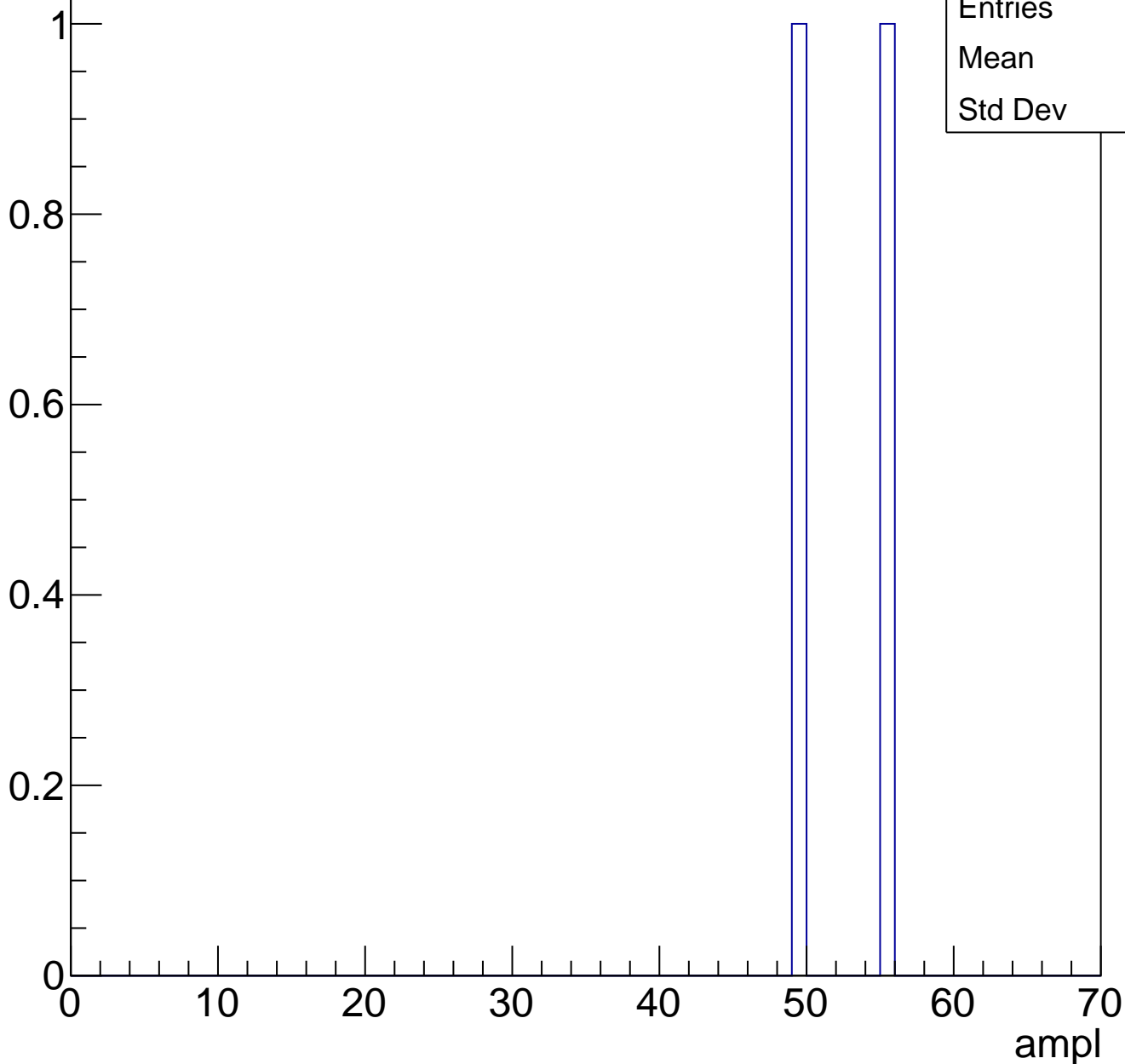


| Entries | 2 |
|---------|---|
| Mean | 52 |
| Std Dev | 3 |

# B0L100S, U1-ch62, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch62, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L100S, U1-ch63, adc0

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 2 |
| Mean | 43 |
| Std Dev | 1 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
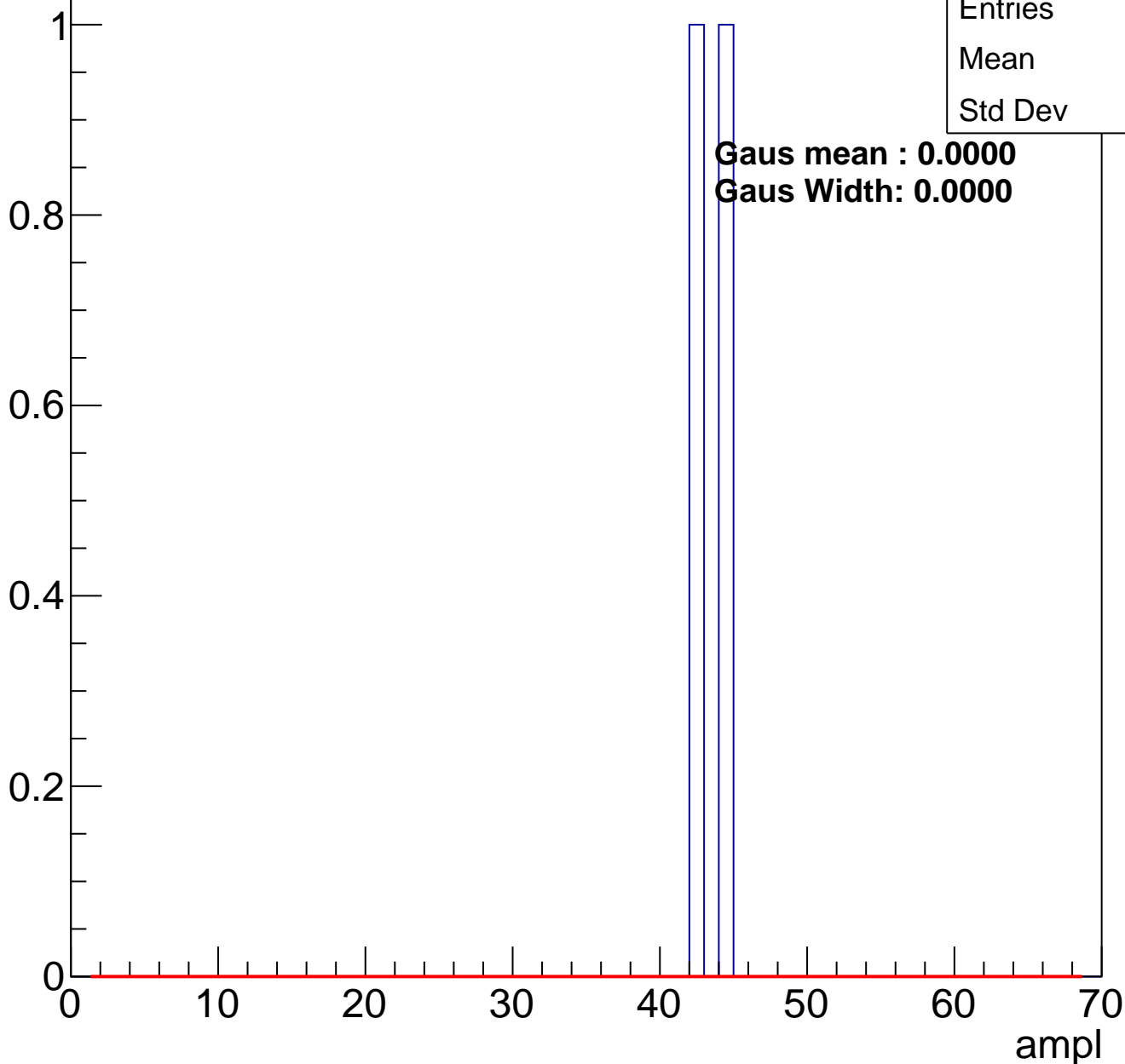
# B0L100S, U1-ch63, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 21 |
| Mean | 44 |
| Std Dev | 1.927 |

**Gaus mean : 43.9725**
**Gaus Width: 1.5848**
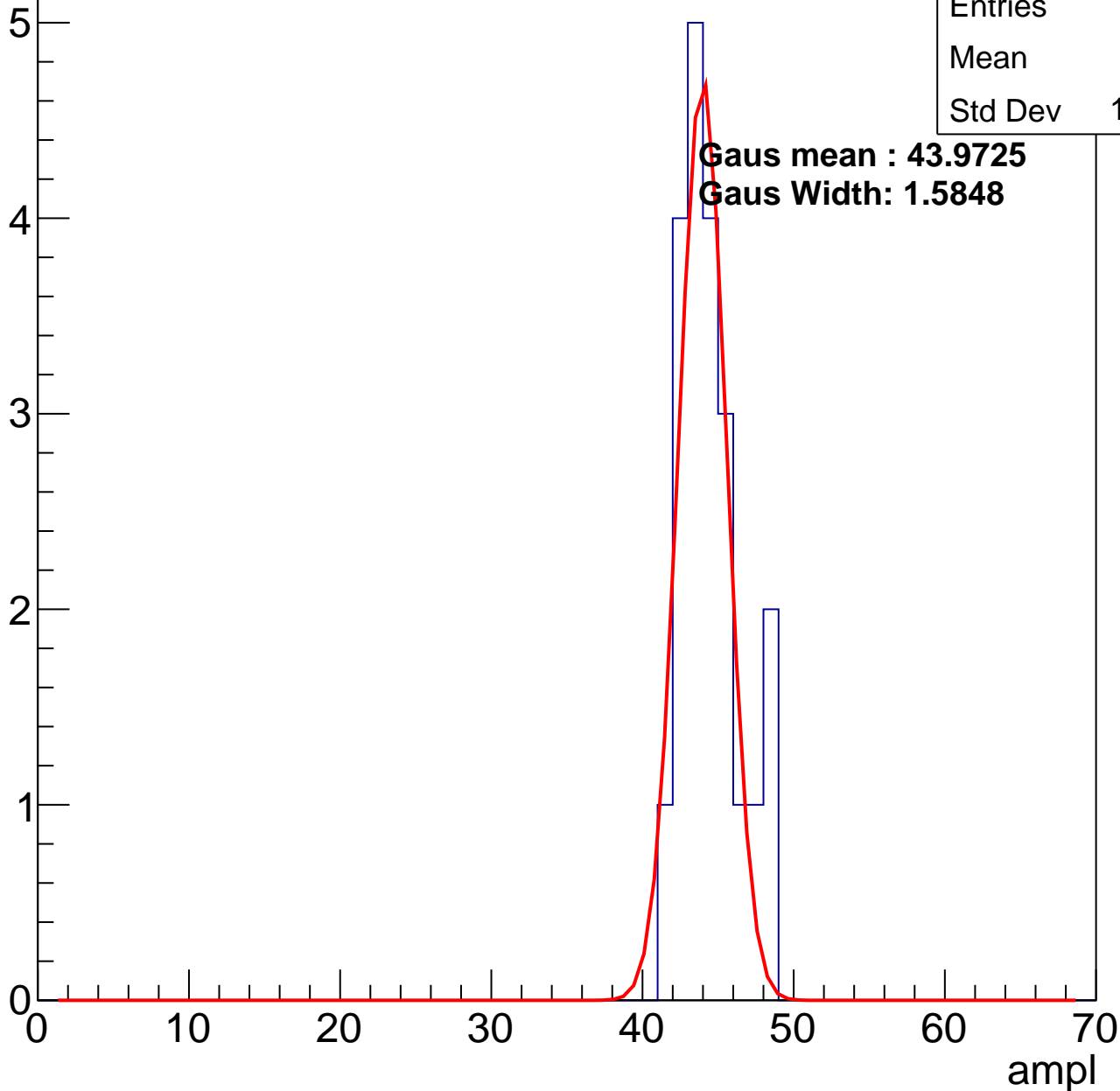
Entry

ampl
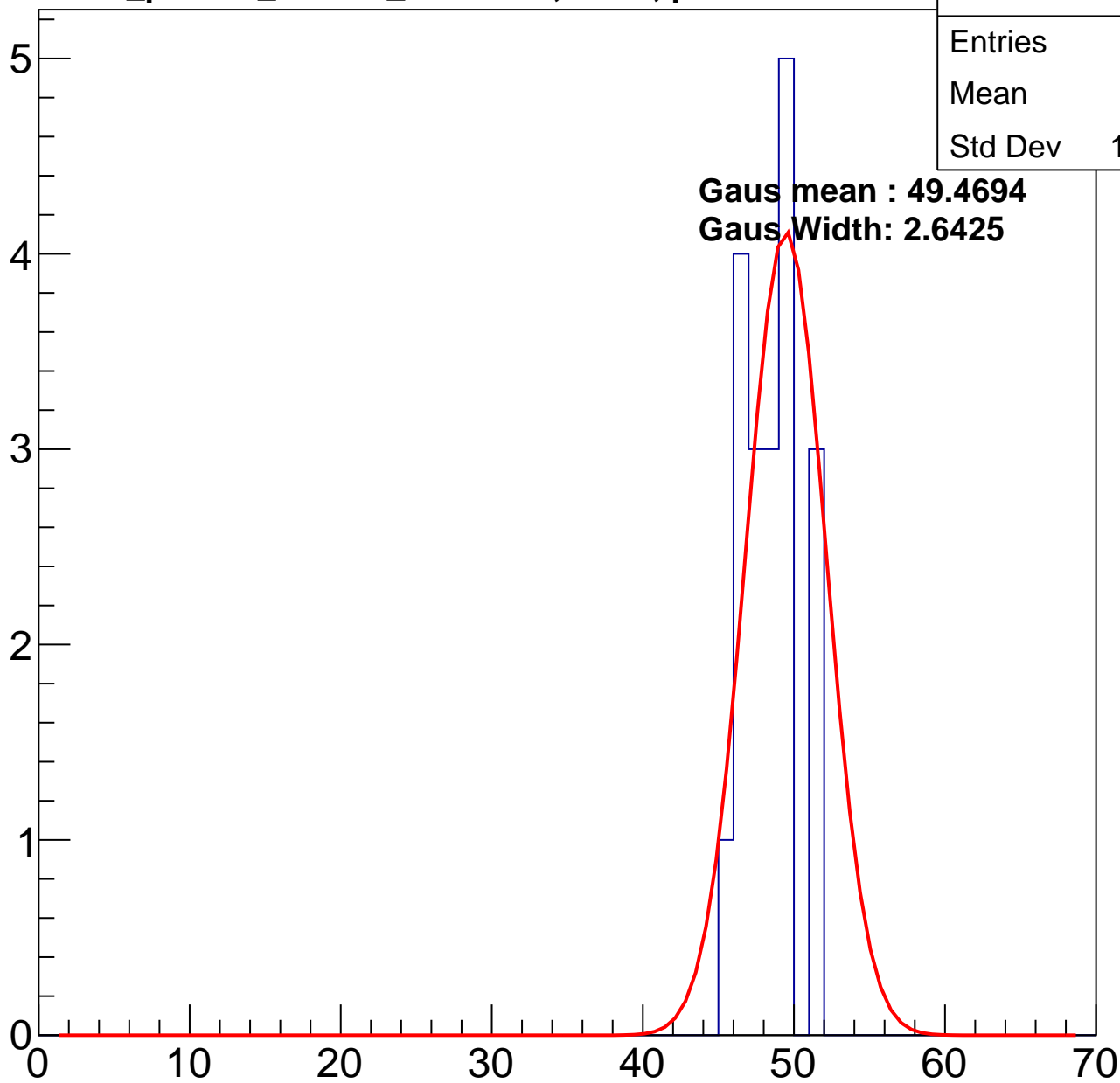
# B0L100S, U1-ch63, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 5 |
| Mean | 49.4 |
| Std Dev | 1.2 |
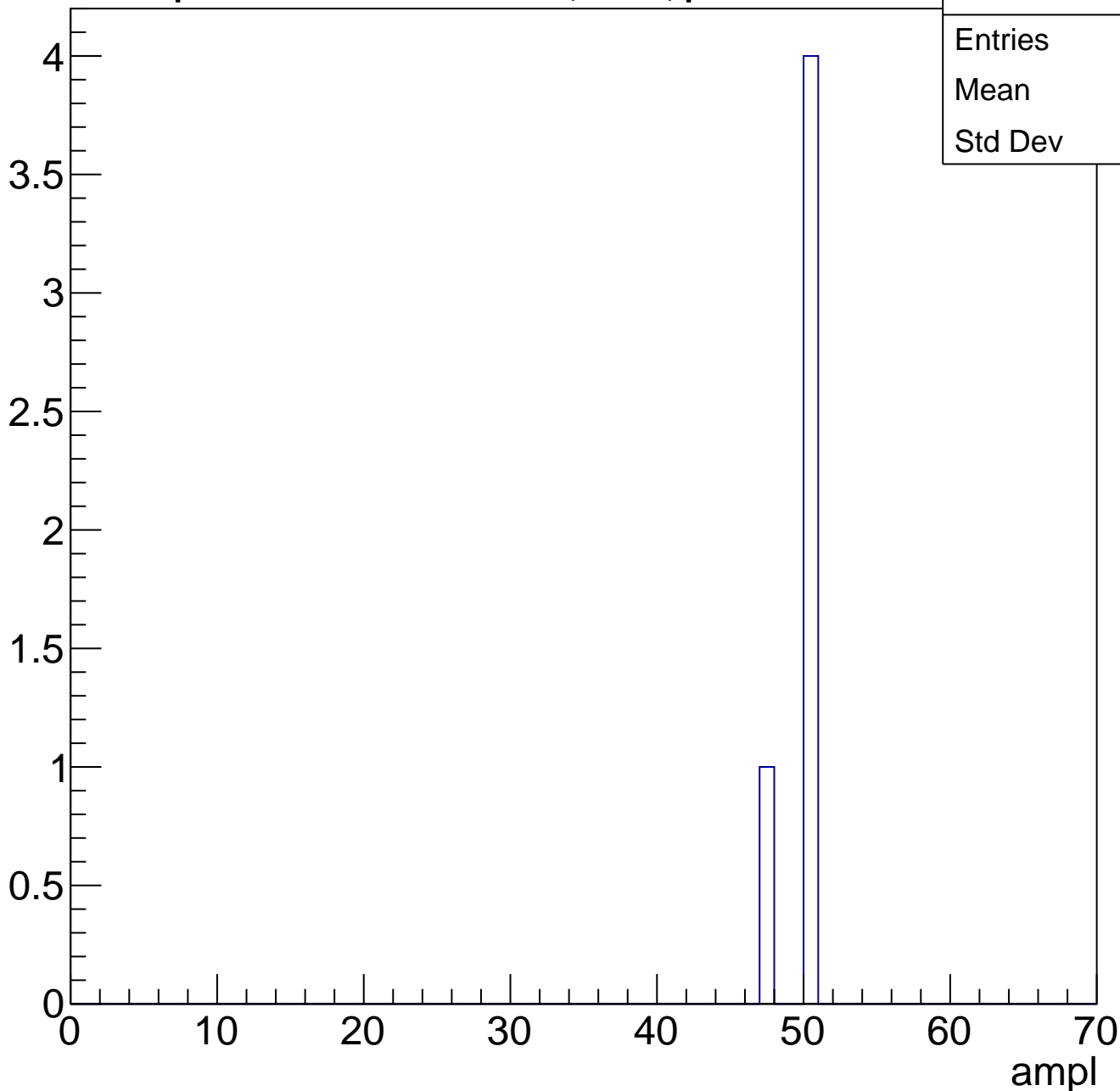
# B0L100S, U1-ch63, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**
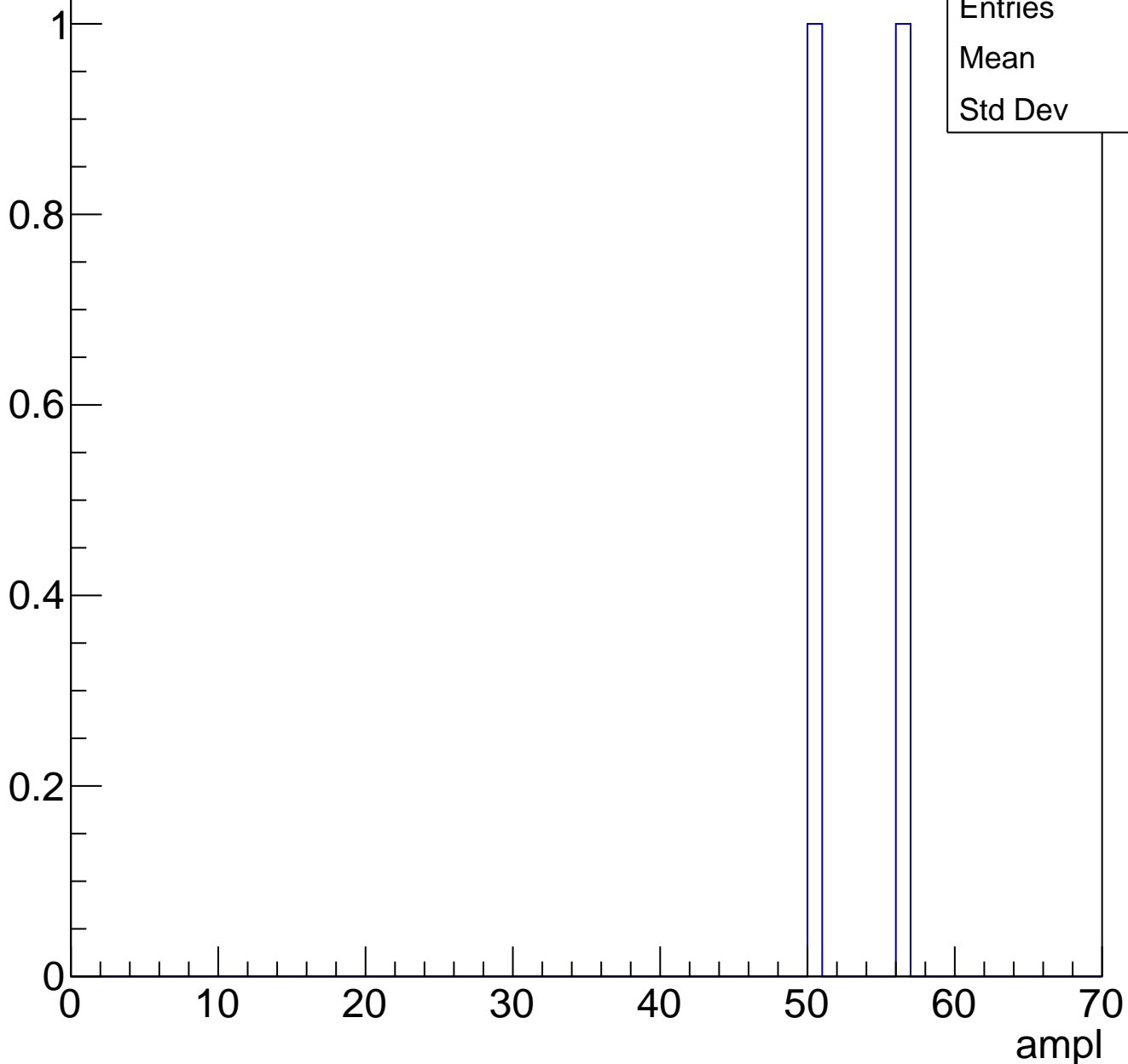


| Entries | 2 |
|---------|---|
| Mean    | 53 |
| Std Dev | 3 |

Entry

ampl

B0L100S, U1-ch63, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch63, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch63, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch64, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**



**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
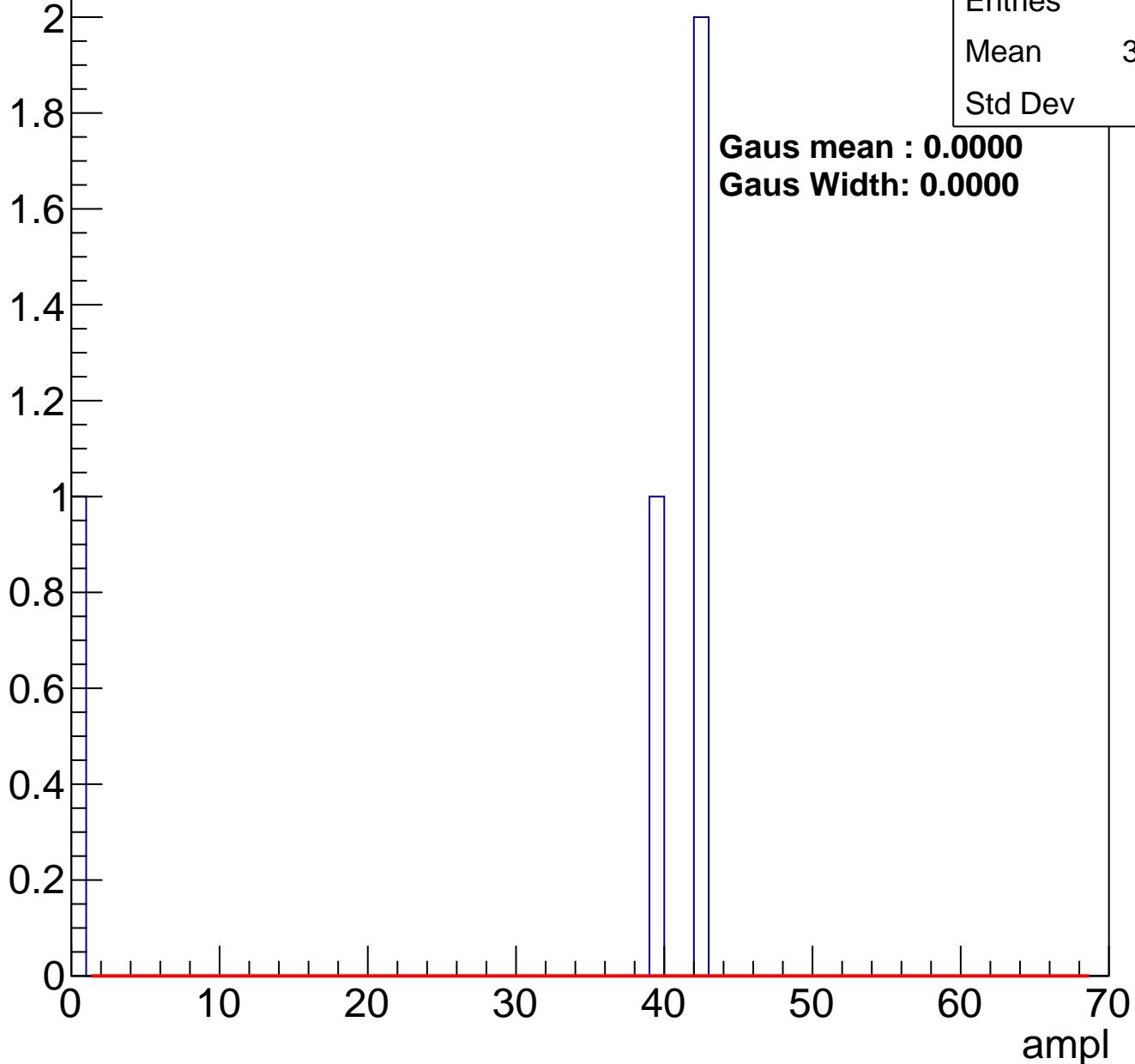
| Entries | 4 |
| Mean | 30.75 |
| Std Dev | 17.8 |

# B0L100S, U1-ch64, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 13 |
| Mean | 43.31 |
| Std Dev | 2.671 |

**Gaus mean : 43.4716**
**Gaus Width: 2.5978**
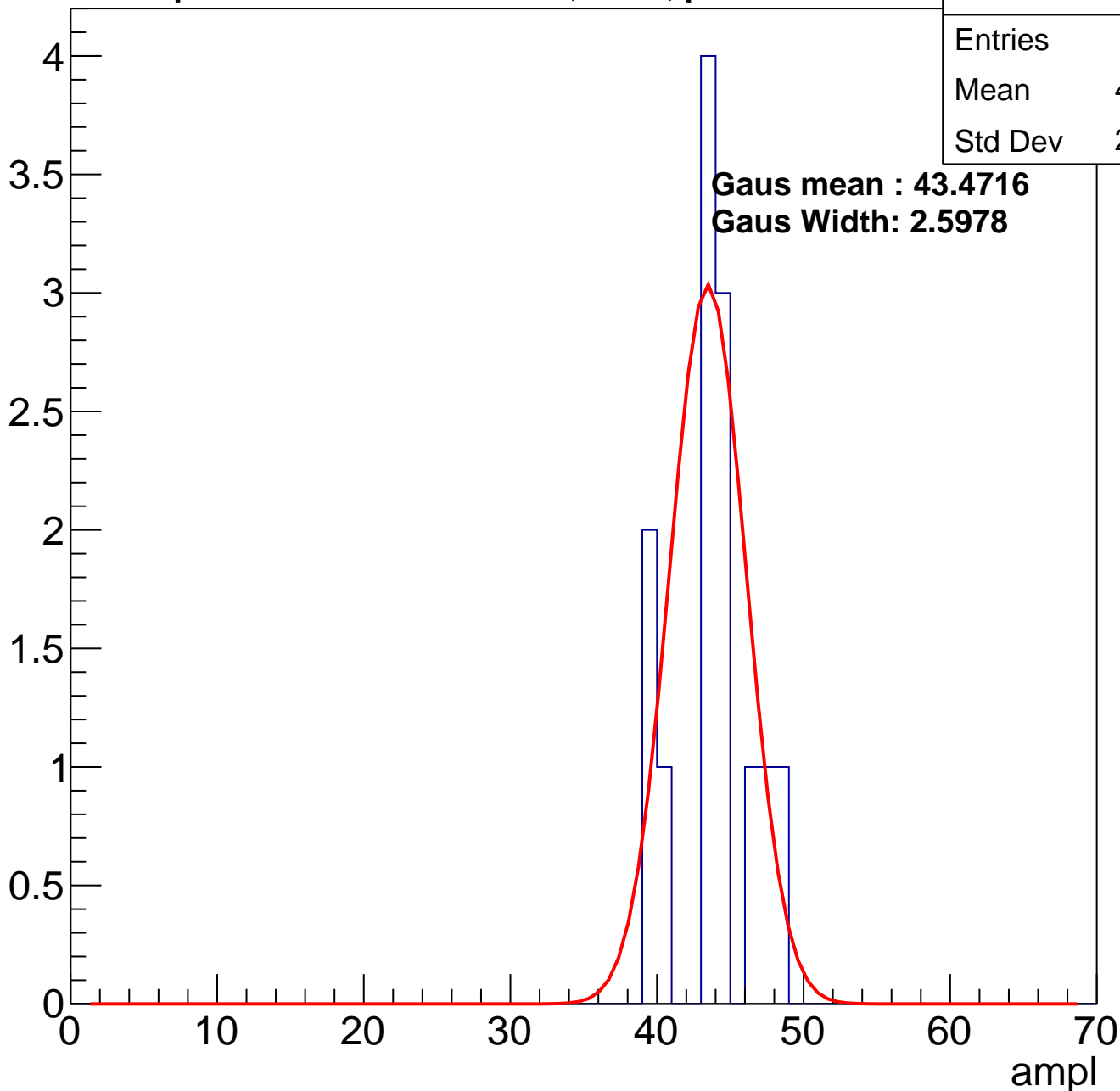
# B0L100S, U1-ch64, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 20 |
| Mean | 47.85 |
| Std Dev | 3.087 |

**Gaus mean : 49.2217**
**Gaus Width: 1.5108**
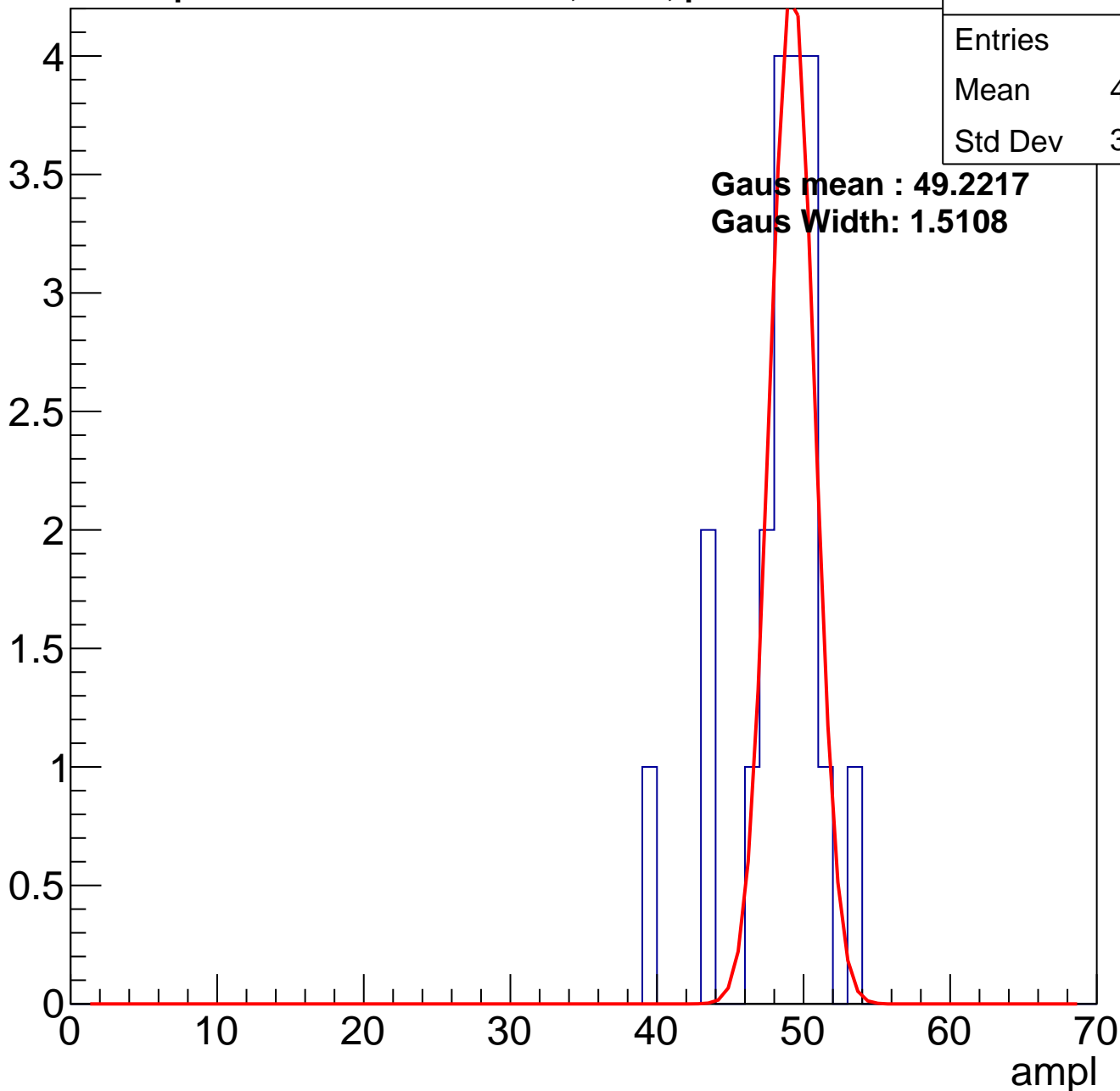
# B0L100S, U1-ch64, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 46.33 |
| Std Dev | 2.055 |

ampl
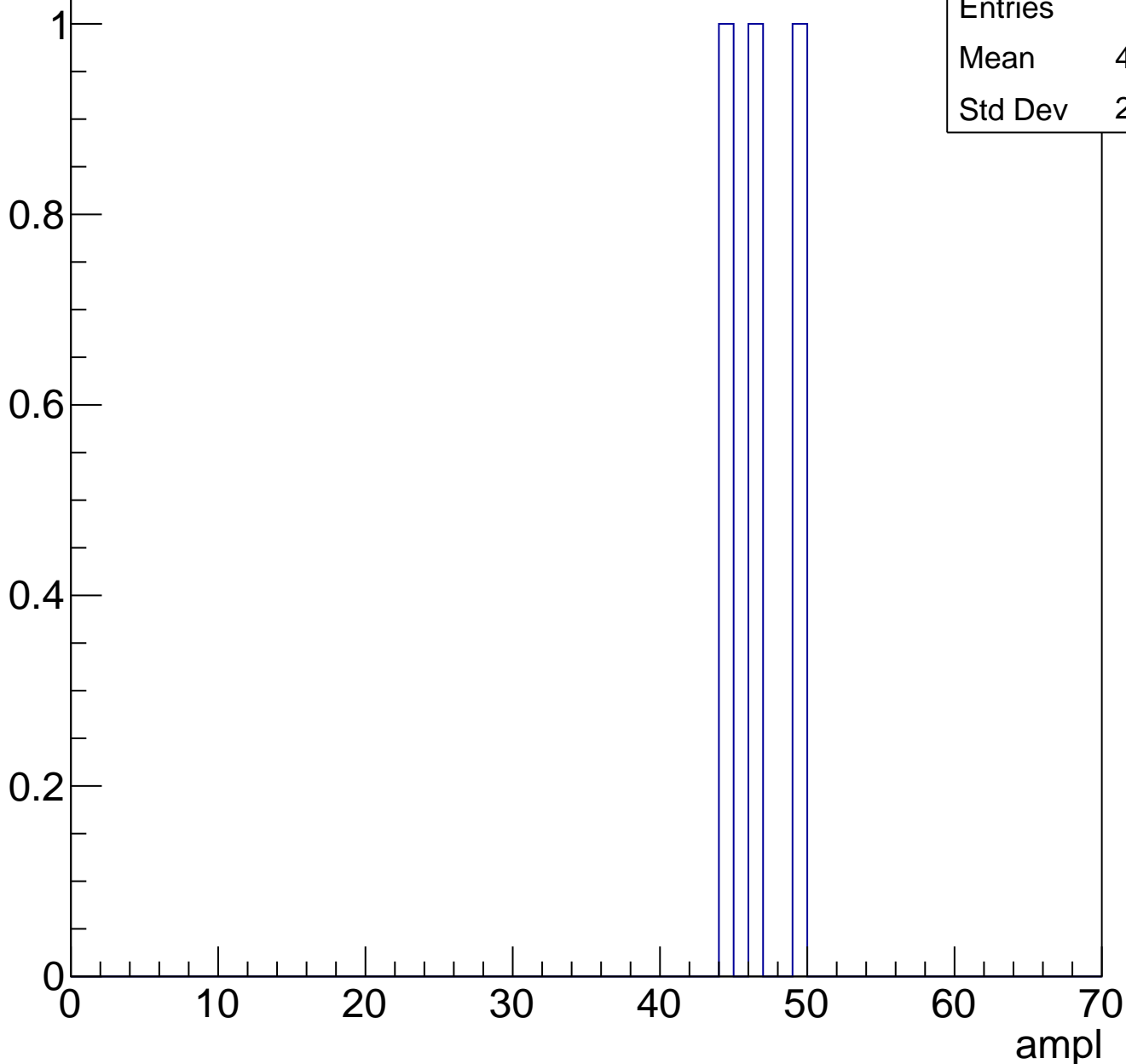
# B0L100S, U1-ch64, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 53 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch64, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl
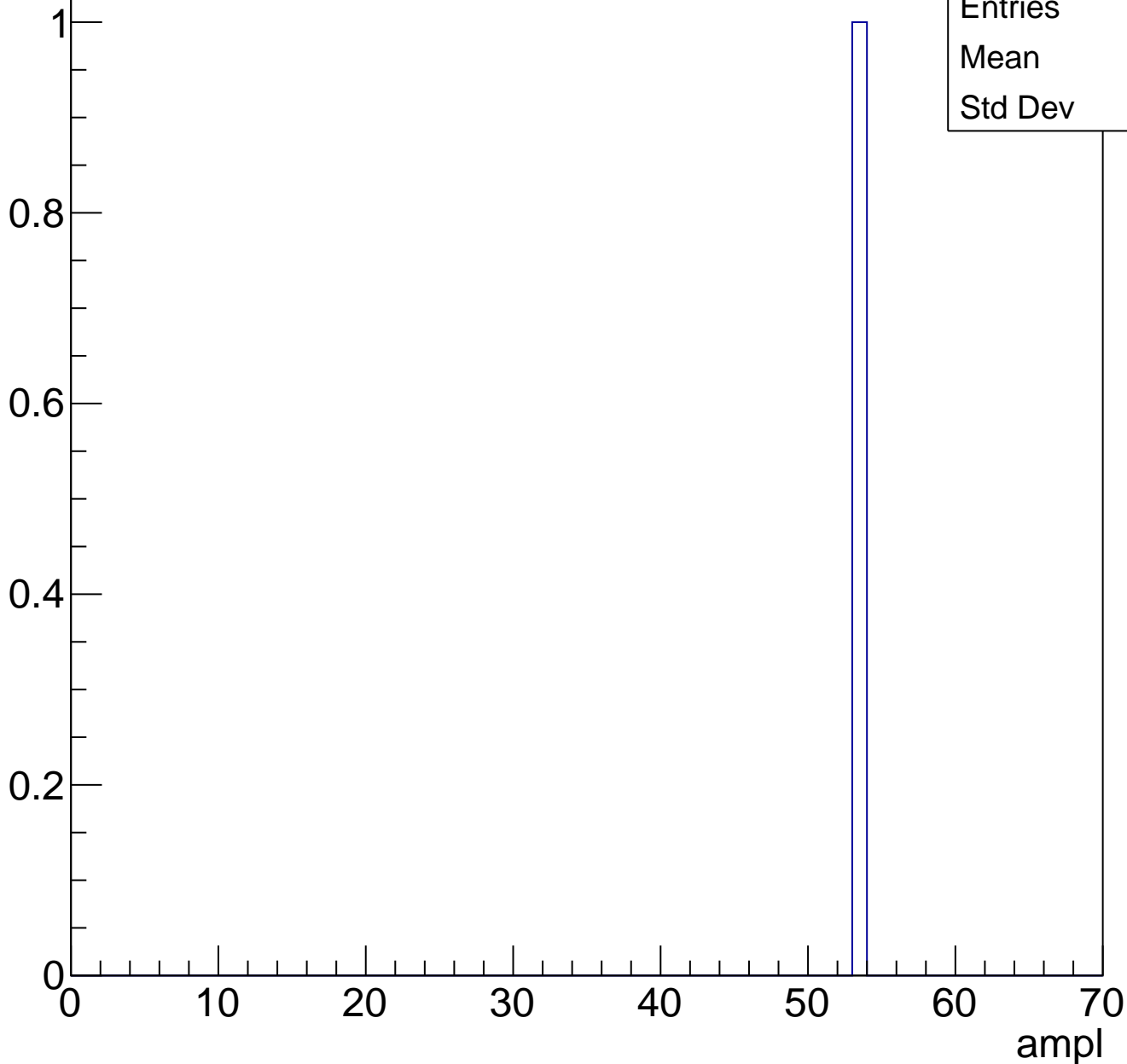
Entry

# B0L100S, U1-ch64, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch64, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch65, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch65, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch65, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch65, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch65, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch65, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
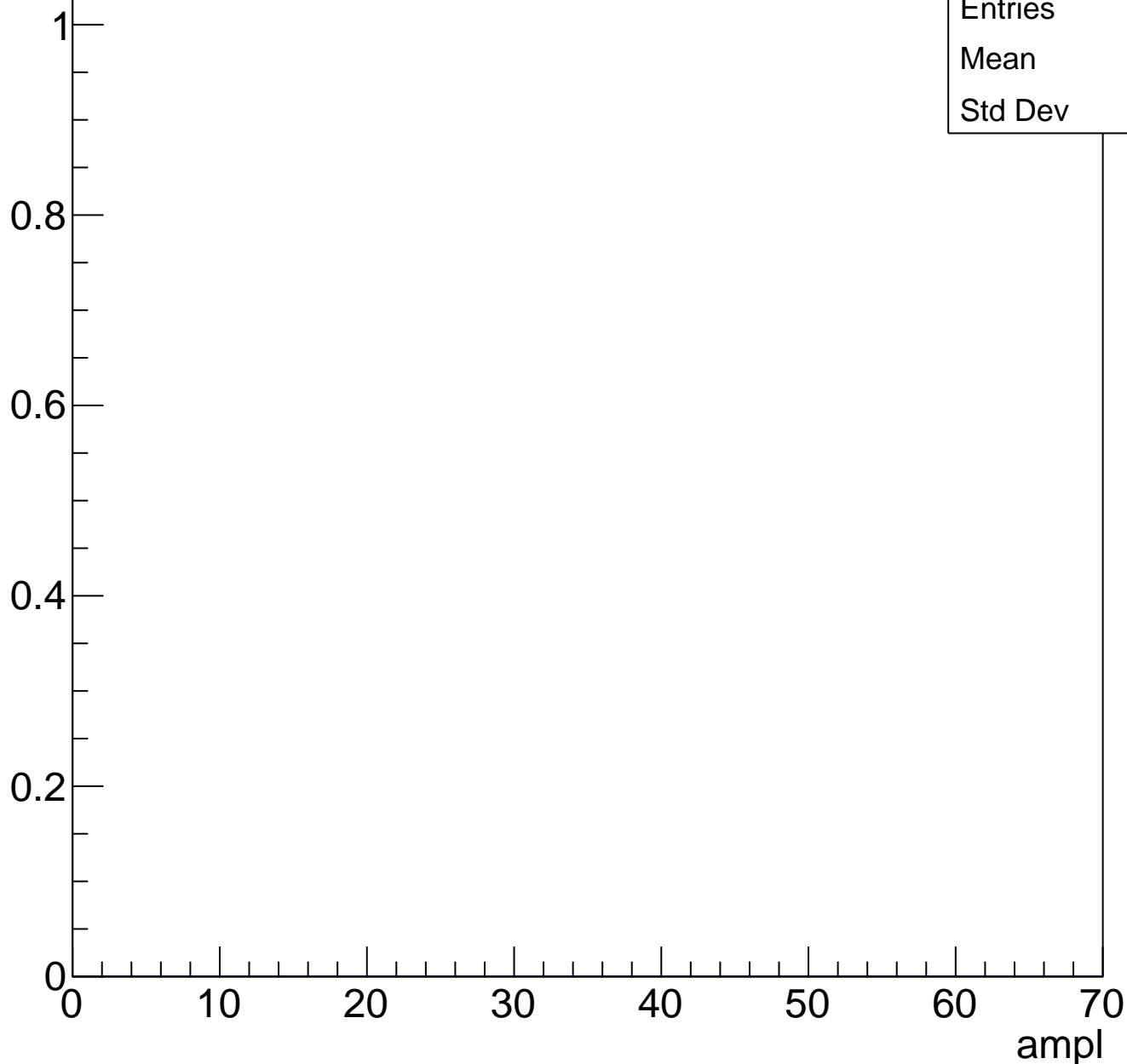
# B0L100S, U1-ch65, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch65, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch66, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch66, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 4 |
| Mean | 48.5 |
| Std Dev | 1.118 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
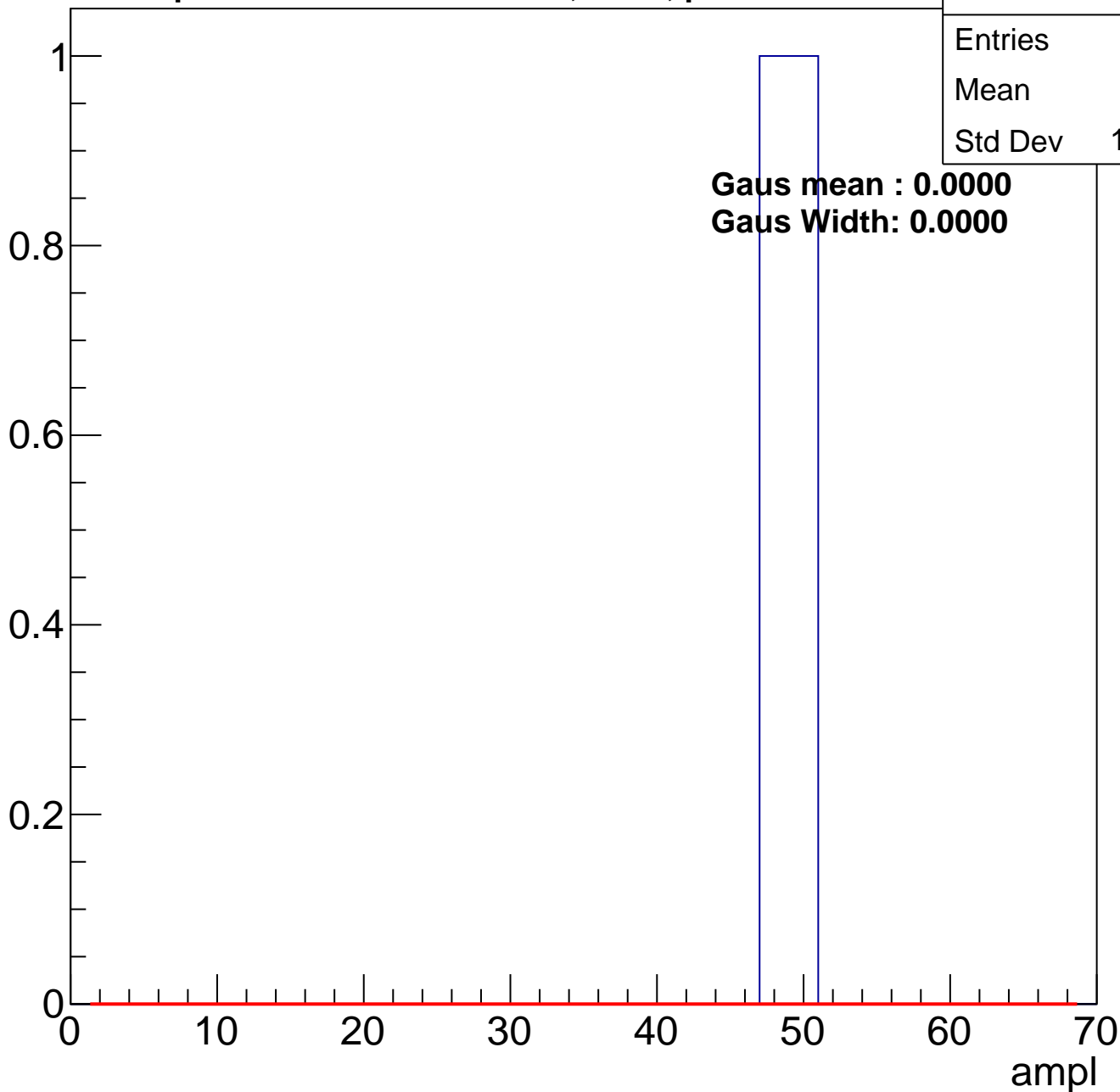
# B0L100S, U1-ch66, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 22 |
| Mean | 49.41 |
| Std Dev | 2.103 |

Gaus mean : 50.2128
Gaus Width: 2.8244
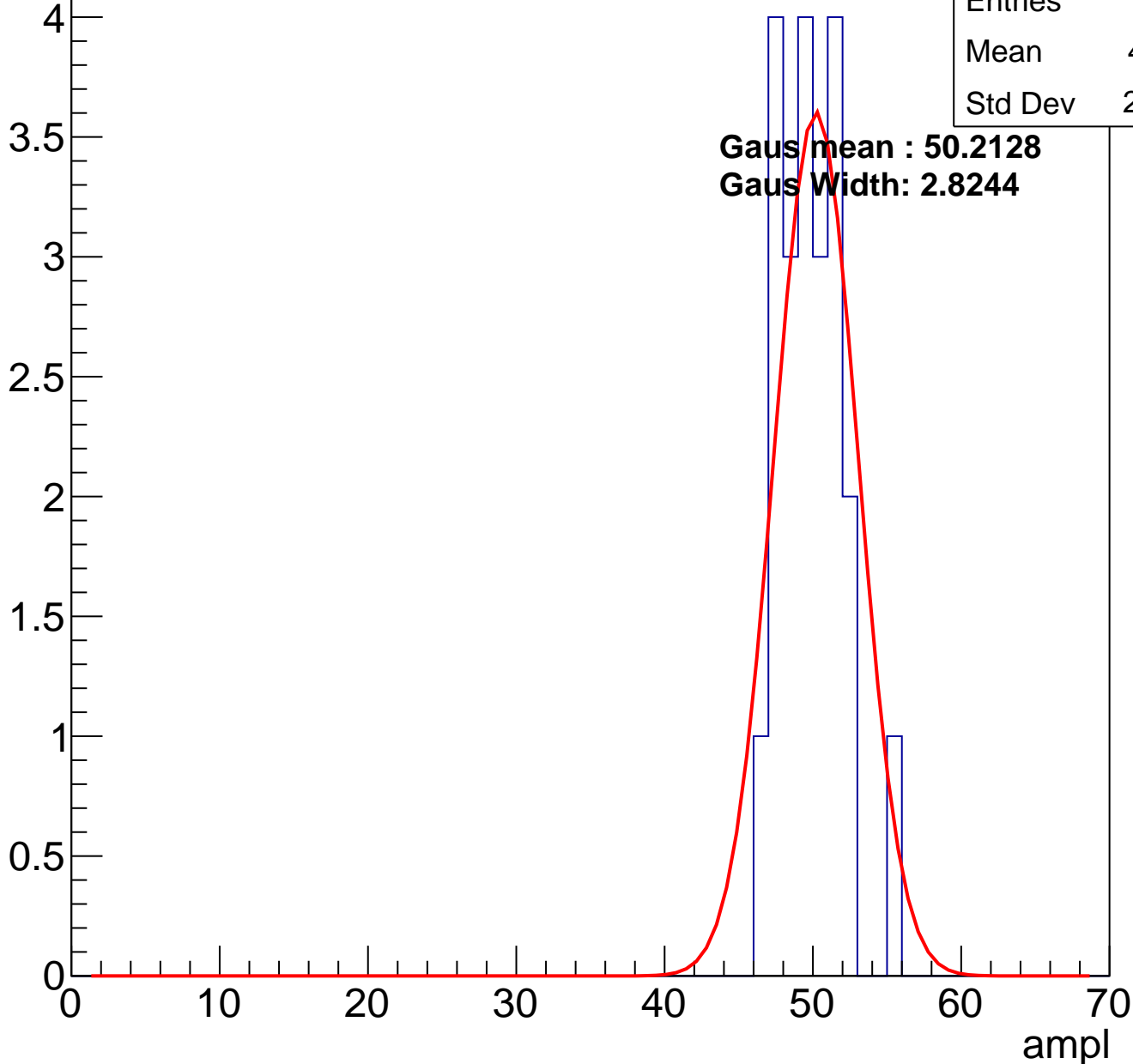
# B0L100S, U1-ch66, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
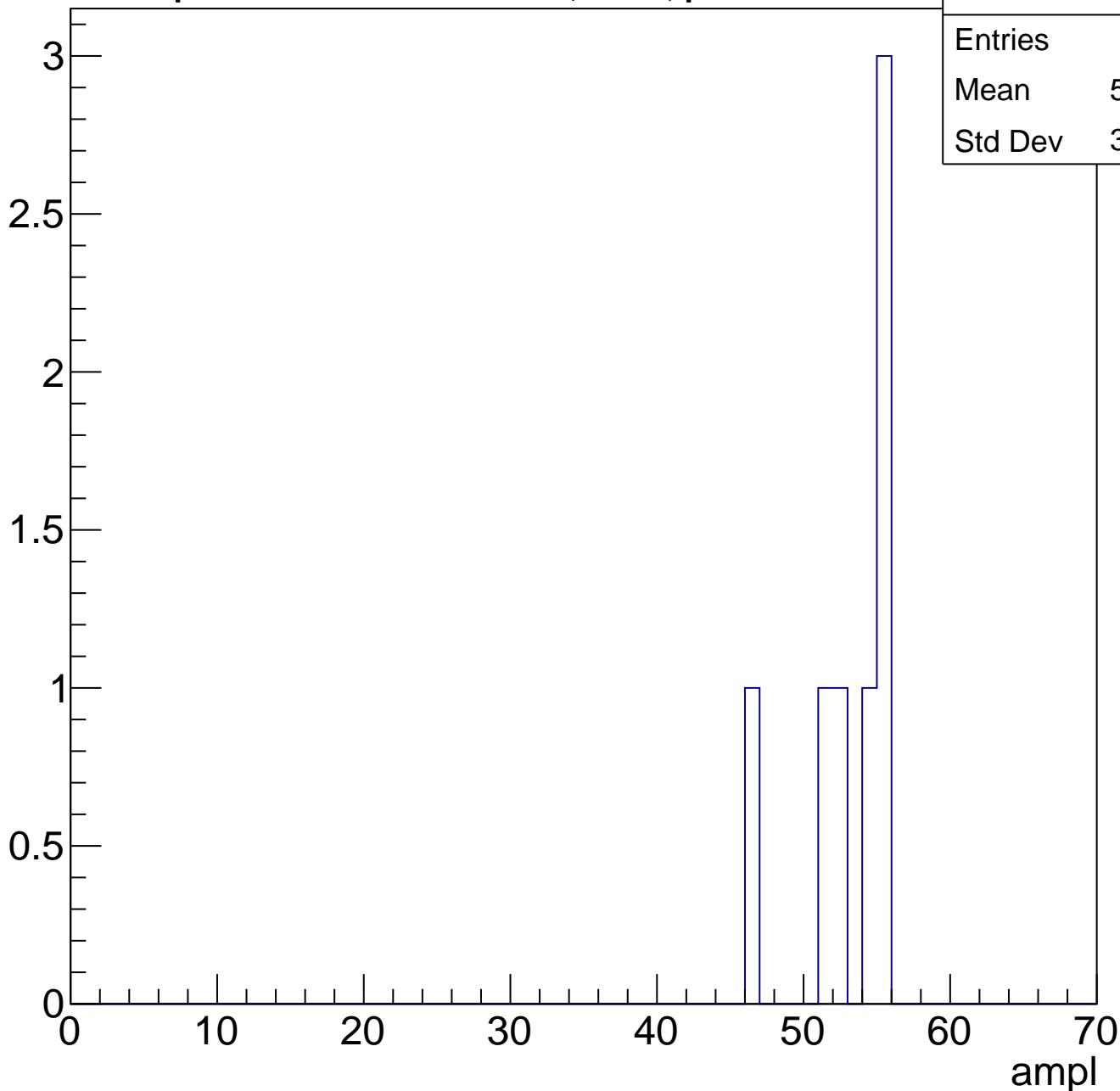


| | |
|---|---|
| Entries | 7 |
| Mean | 52.57 |
| Std Dev | 3.064 |

Entry

ampl

# B0L100S, U1-ch66, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 55 |
| Std Dev | 0 |
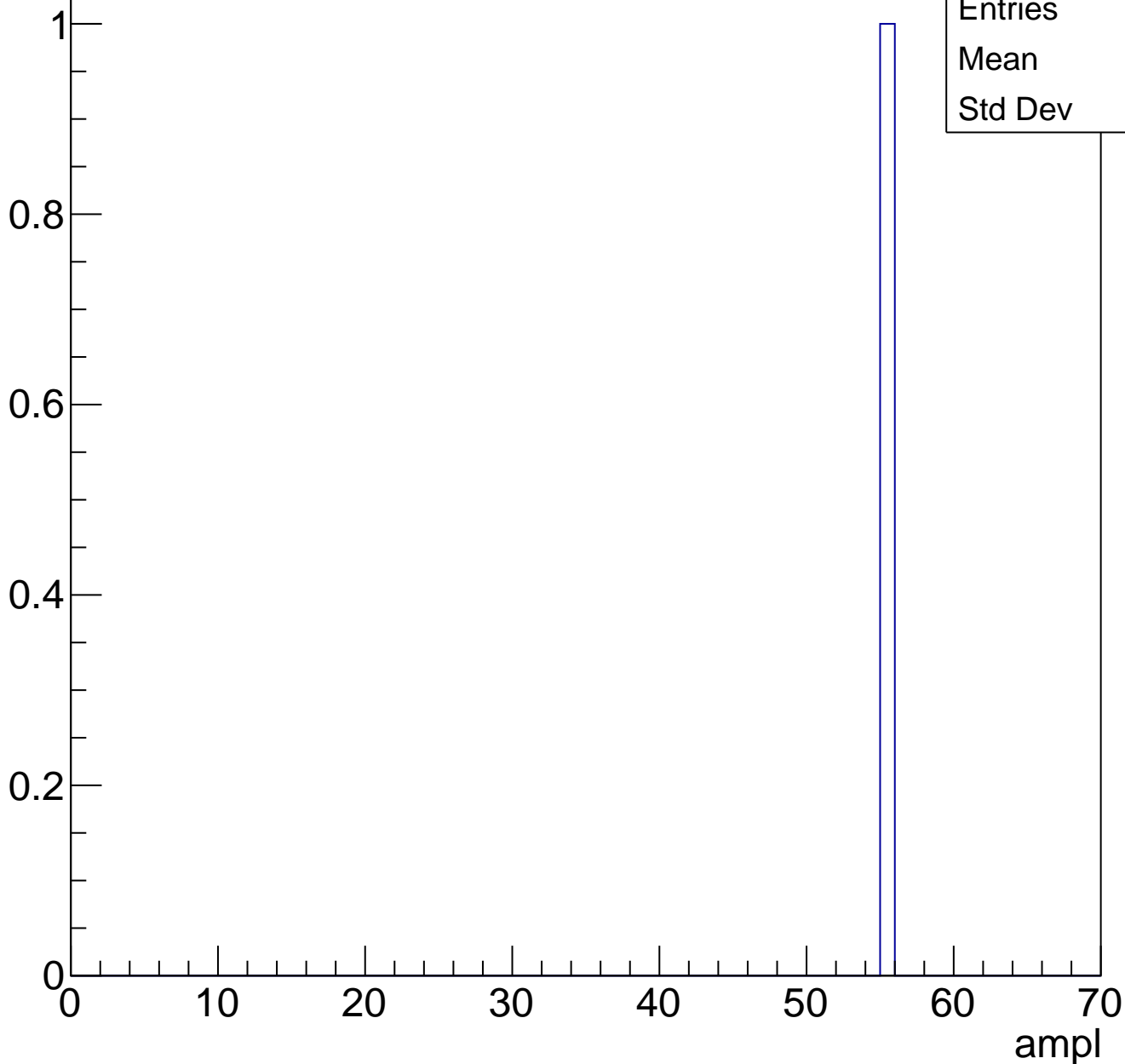
# B0L100S, U1-ch66, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch66, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch66, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch67, adc0

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U1-ch67, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
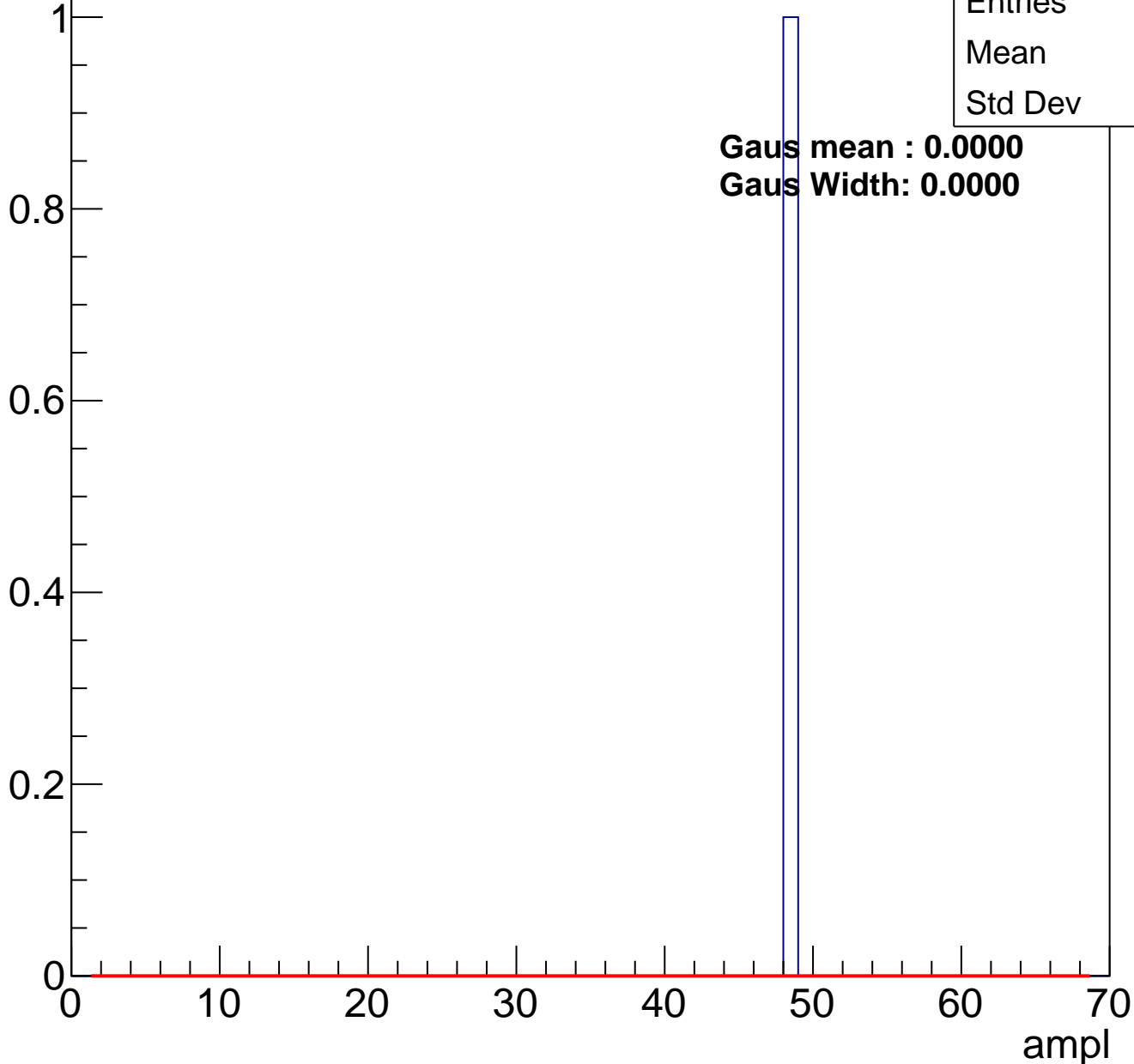
| Entries | 1 |
| Mean | 48 |
| Std Dev | 0 |

# B0L100S, U1-ch67, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch67, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch67, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch67, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch67, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch68, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch68, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 42 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
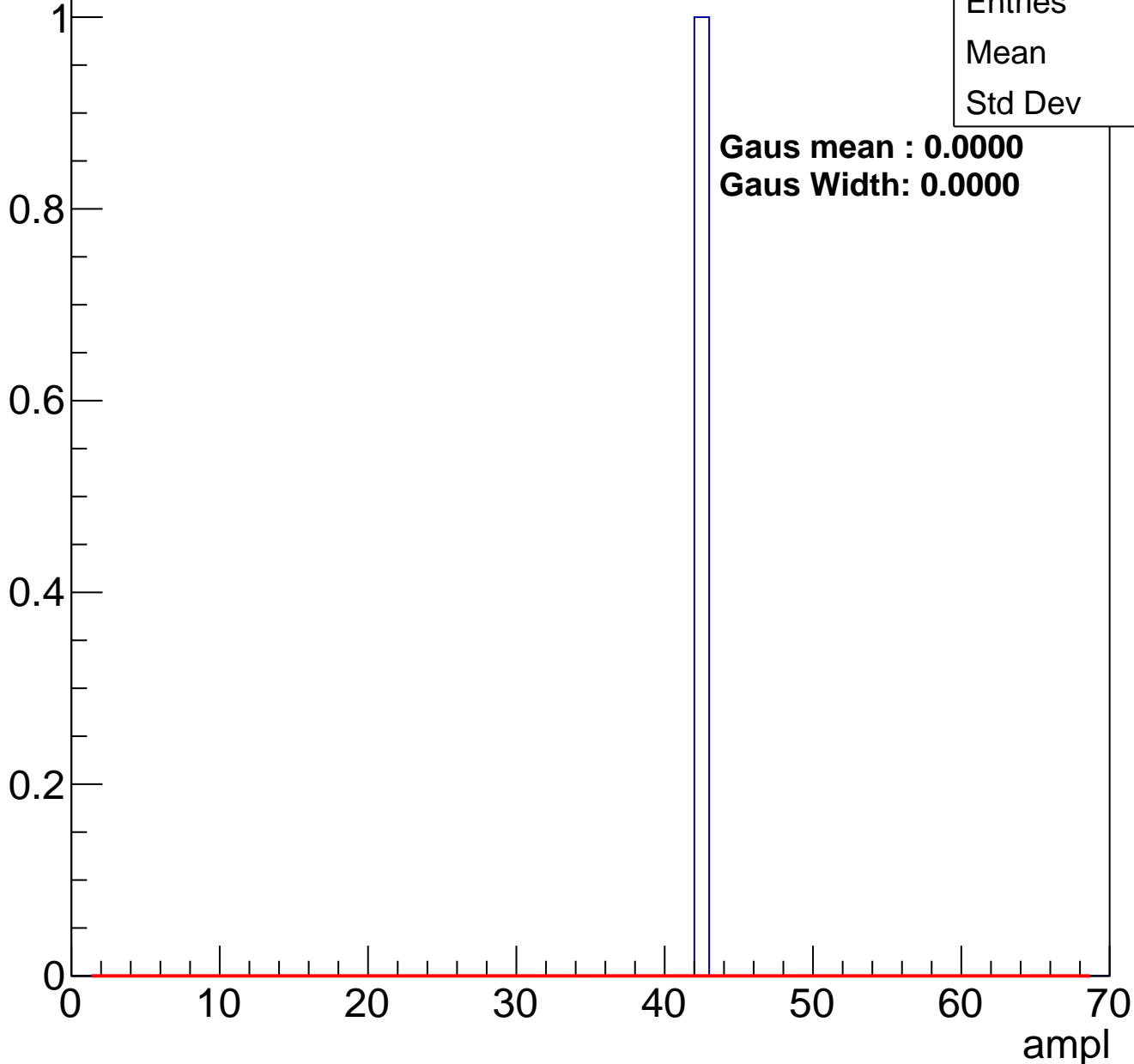
# B0L100S, U1-ch68, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 42 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
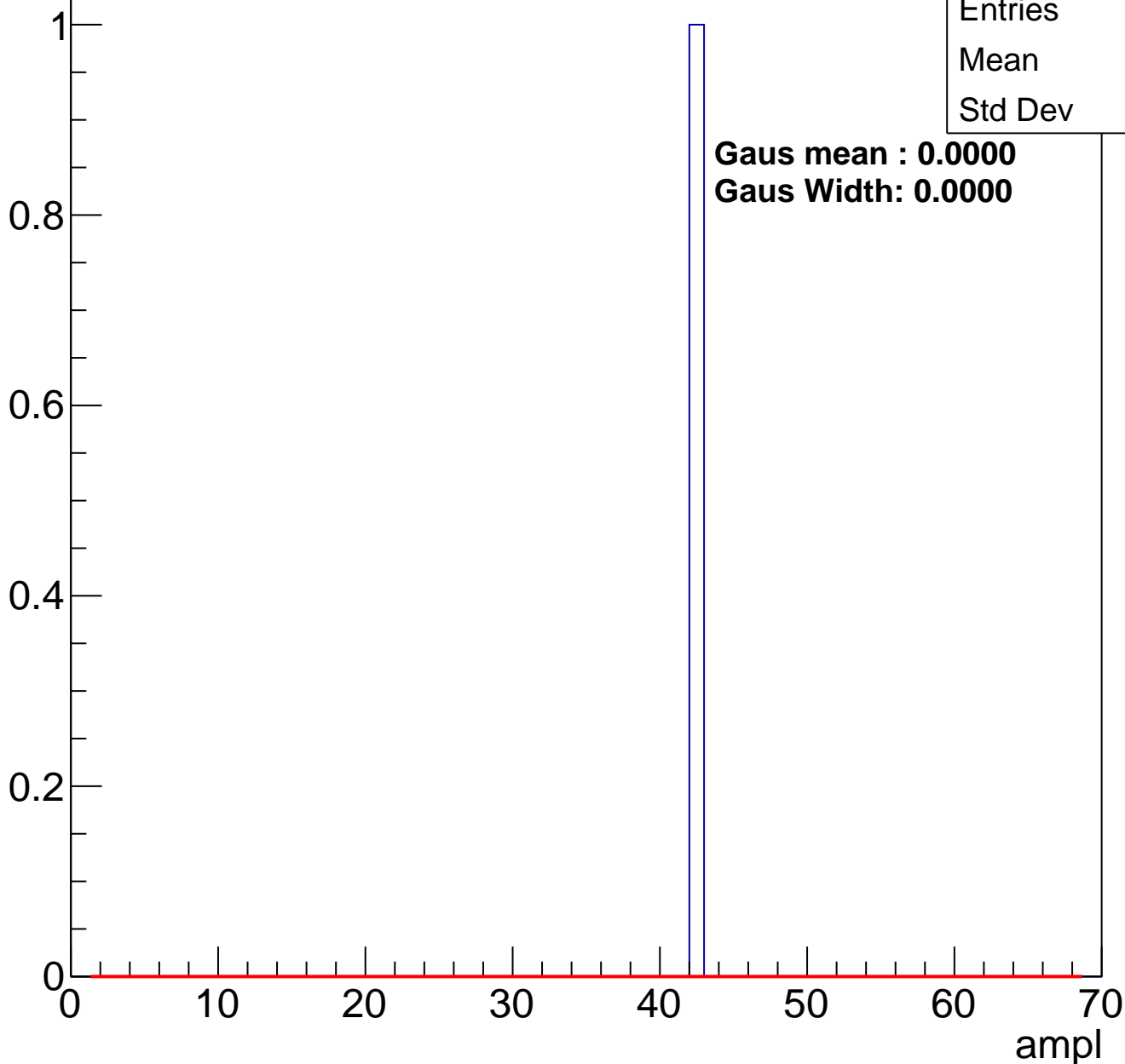
# B0L100S, U1-ch68, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch68, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch68, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch68, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch68, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch69, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 43 |
| Std Dev | 2.16 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
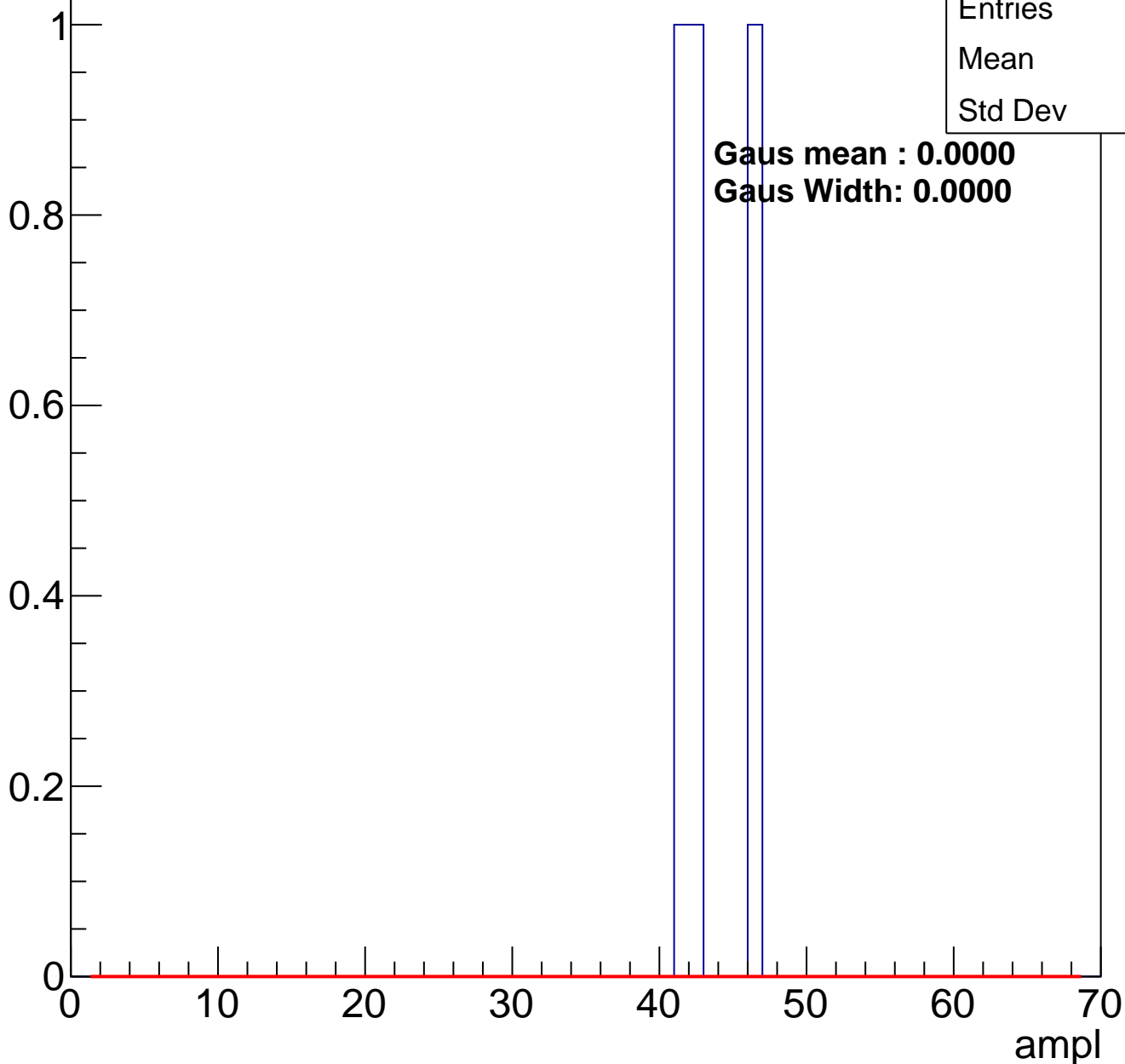
# B0L100S, U1-ch69, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 18 |
| Mean | 43.56 |
| Std Dev | 2.14 |

**Gaus mean : 43.2544**
**Gaus Width: 2.2445**

Entry

ampl
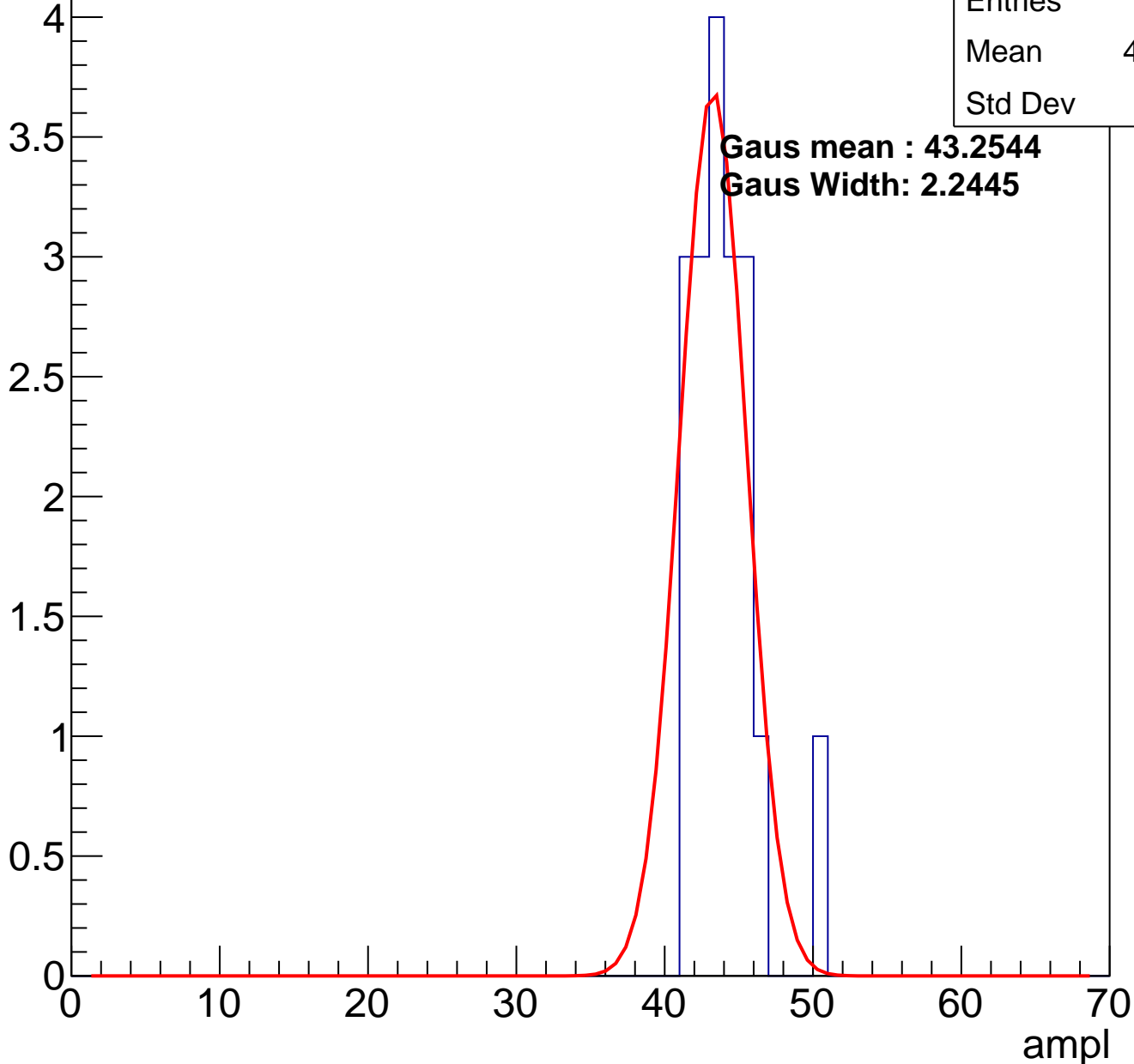
# B0L100S, U1-ch69, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 15 |
| Mean | 47.27 |
| Std Dev | 2.977 |

**Gaus mean : 49.0209**
**Gaus Width: 4.6192**

Entry

ampl
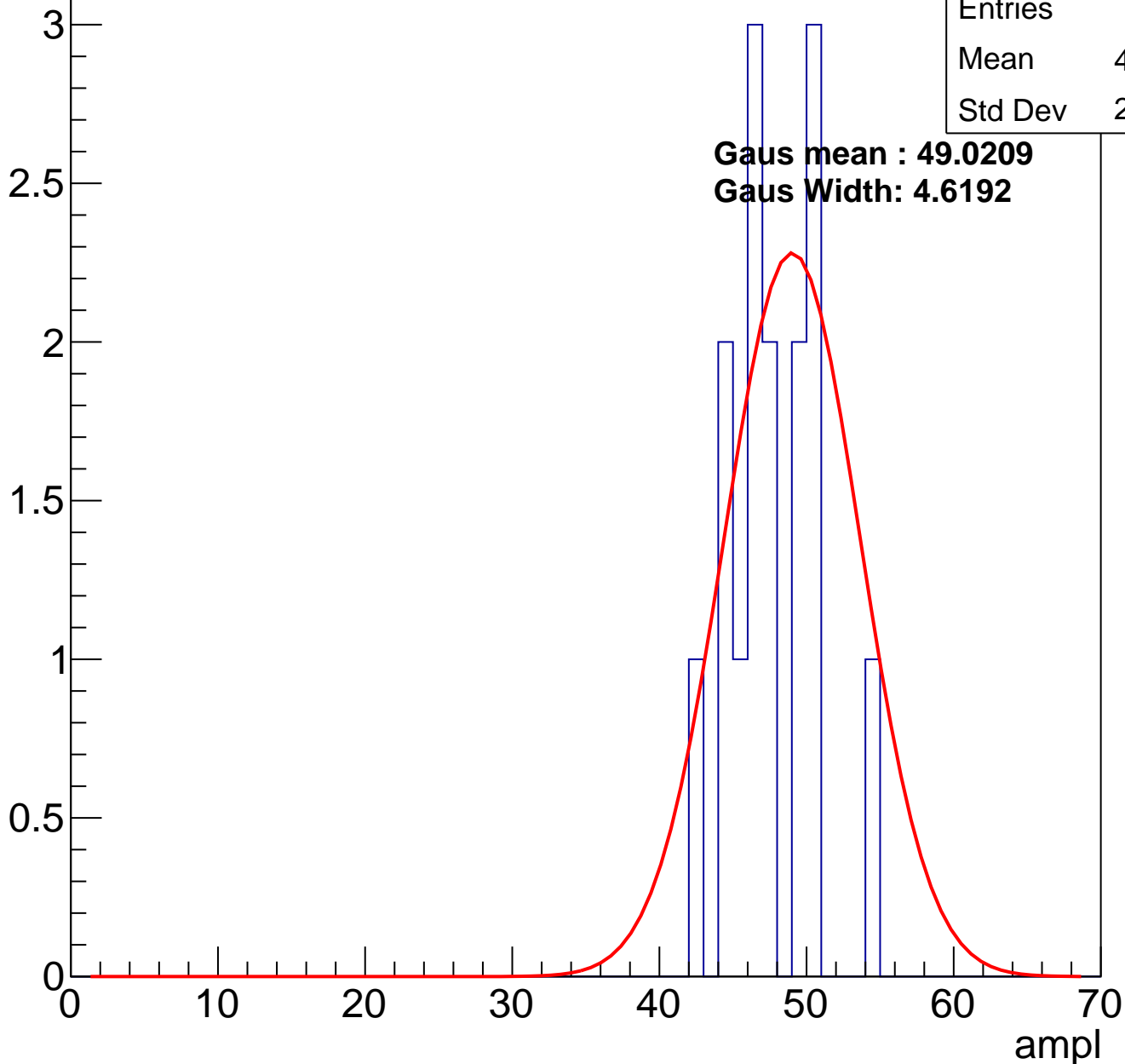
# B0L100S, U1-ch69, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch69, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch69, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch69, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch69, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch70, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch70, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch70, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch70, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch70, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl
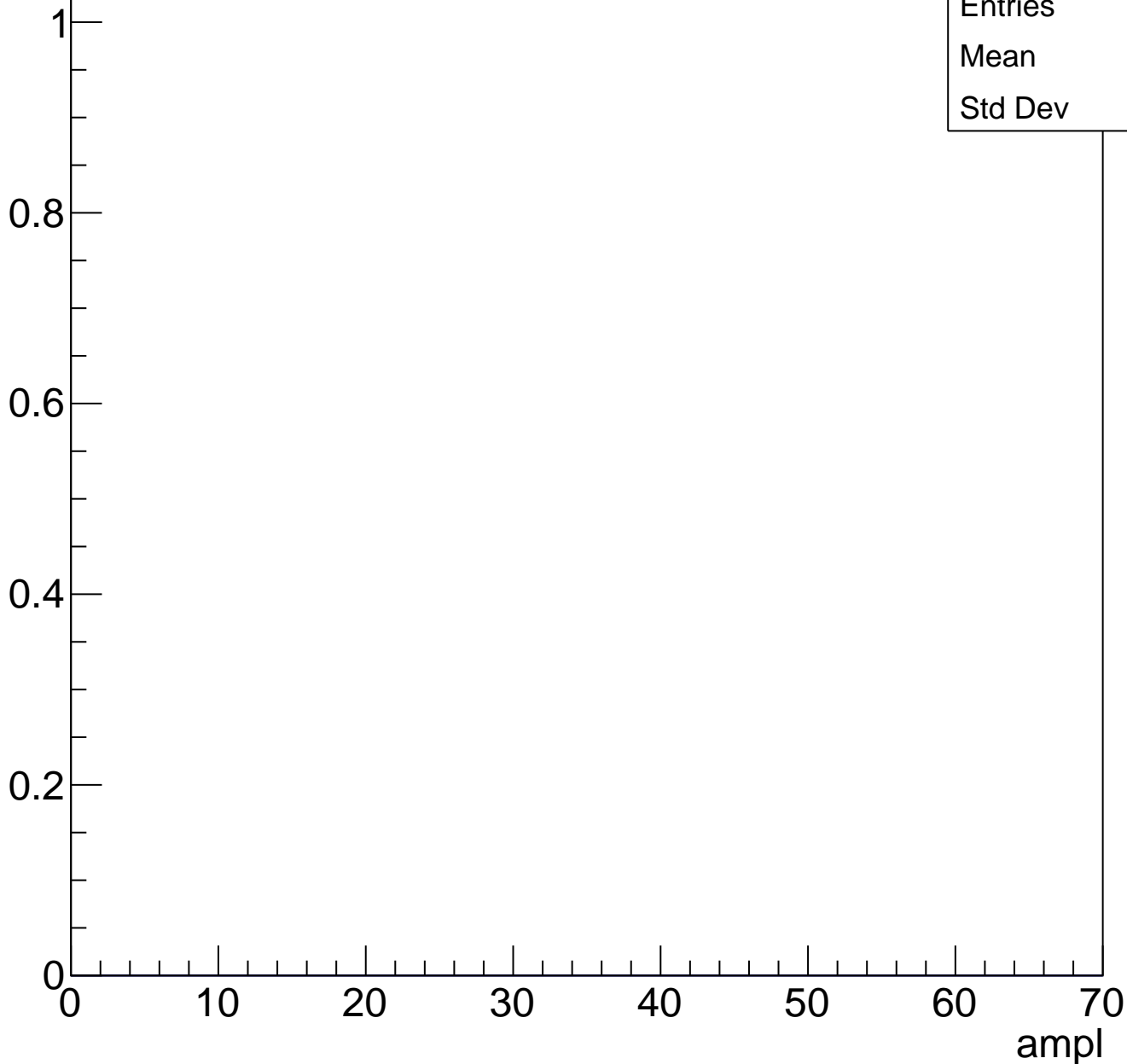
# B0L100S, U1-ch70, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch70, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L100S, U1-ch70, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch71, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch71, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
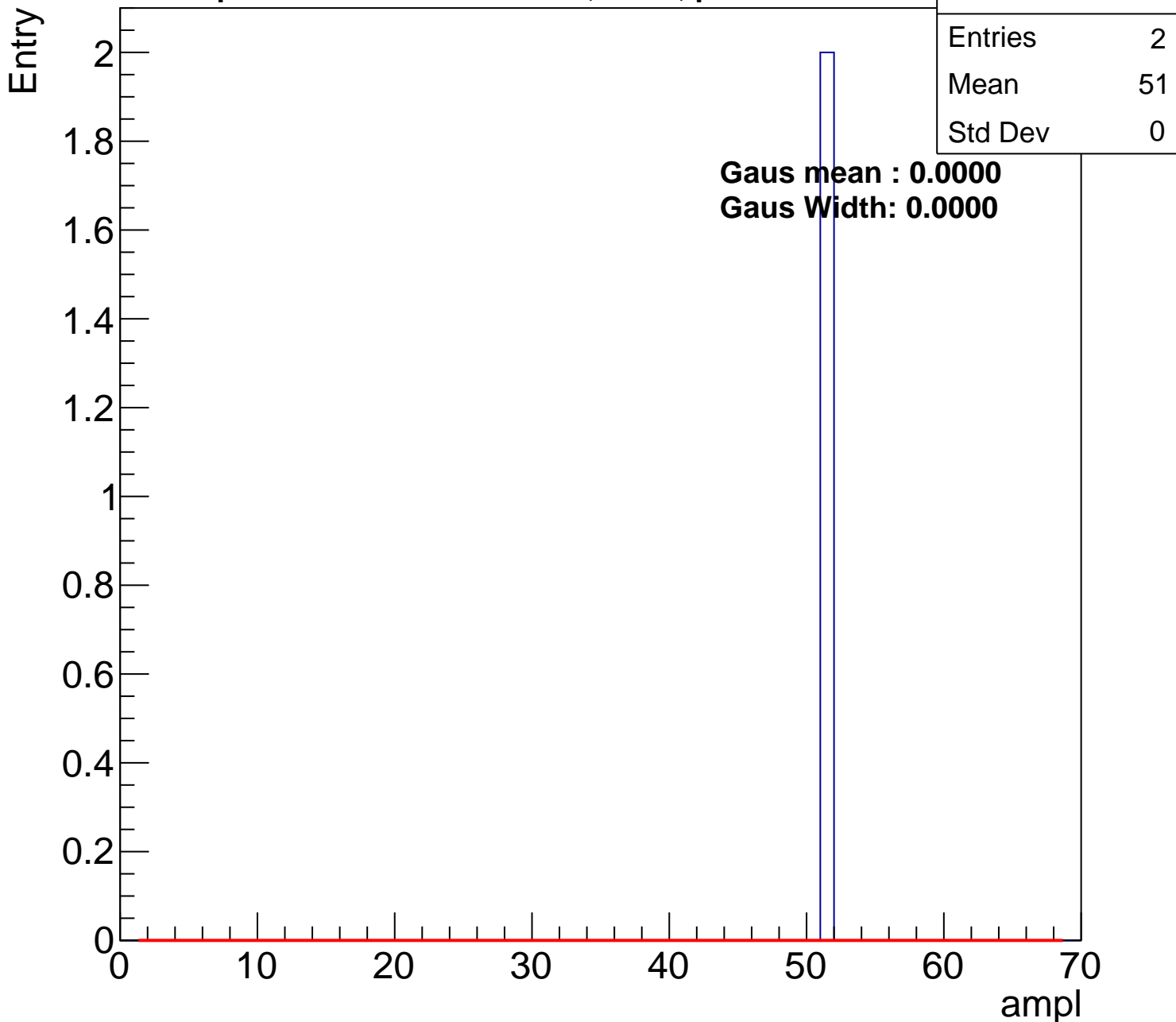
# B0L100S, U1-ch71, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch71, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch71, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch71, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch71, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch72, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 42 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch72, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 12 |
|---|---|
| Mean | 44 |
| Std Dev | 2.449 |

**Gaus mean : 44.1568**
**Gaus Width: 4.7502**

Entry

ampl
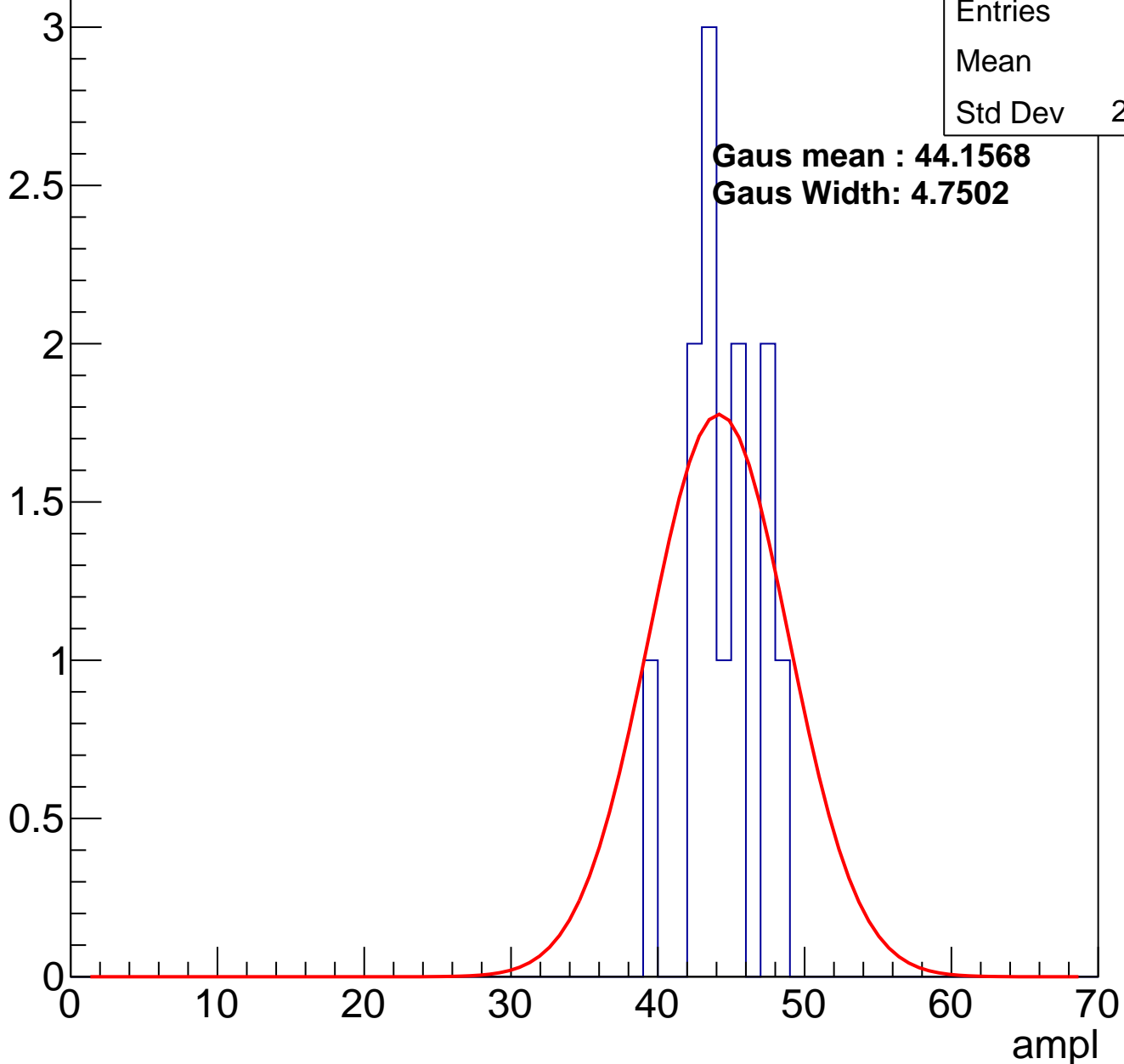
# B0L100S, U1-ch72, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 32 |
|---|---|
| Mean | 46.88 |
| Std Dev | 2.997 |

**Gaus mean : 46.6406**
**Gaus Width: 4.4729**
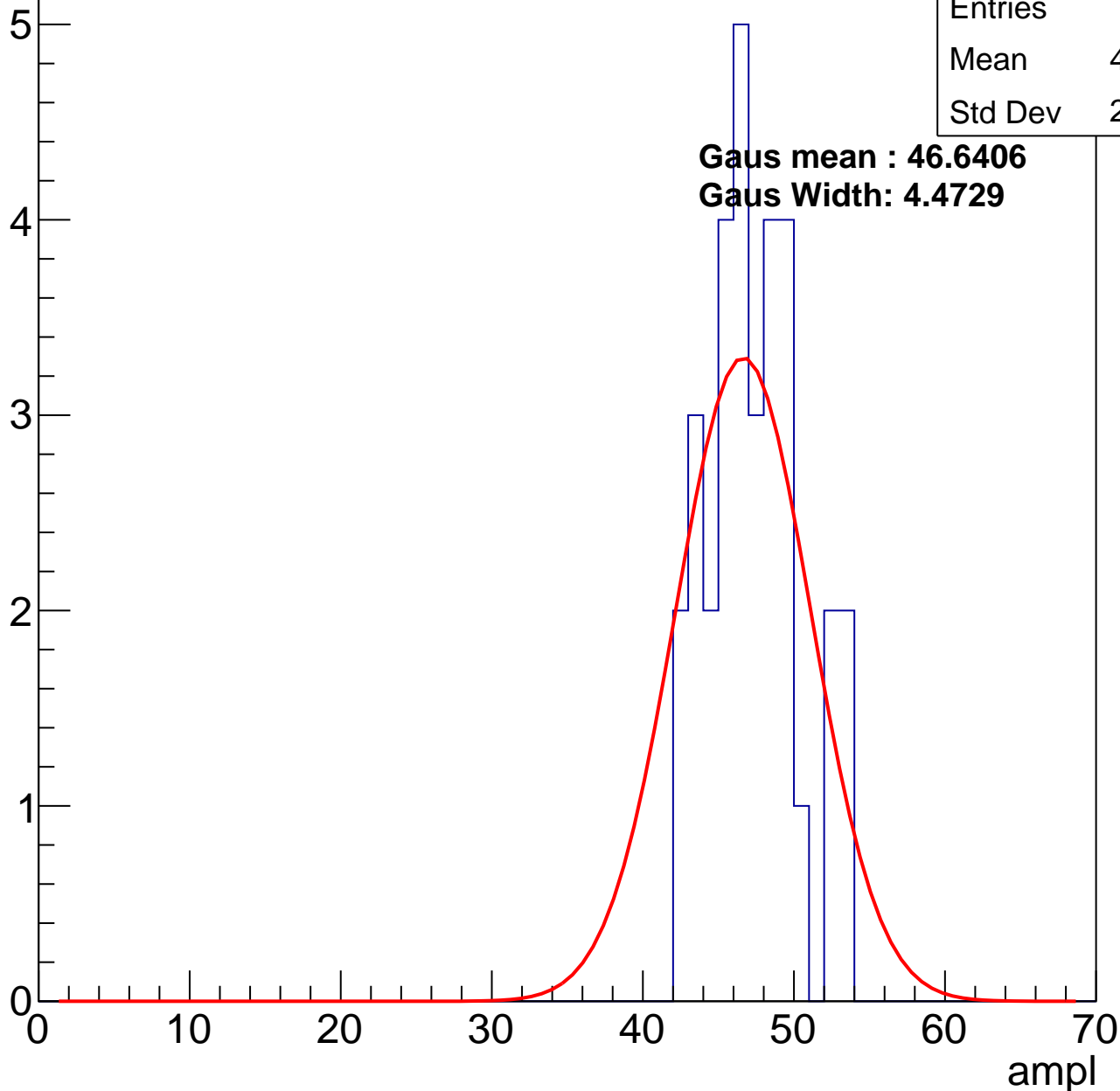
# B0L100S, U1-ch72, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
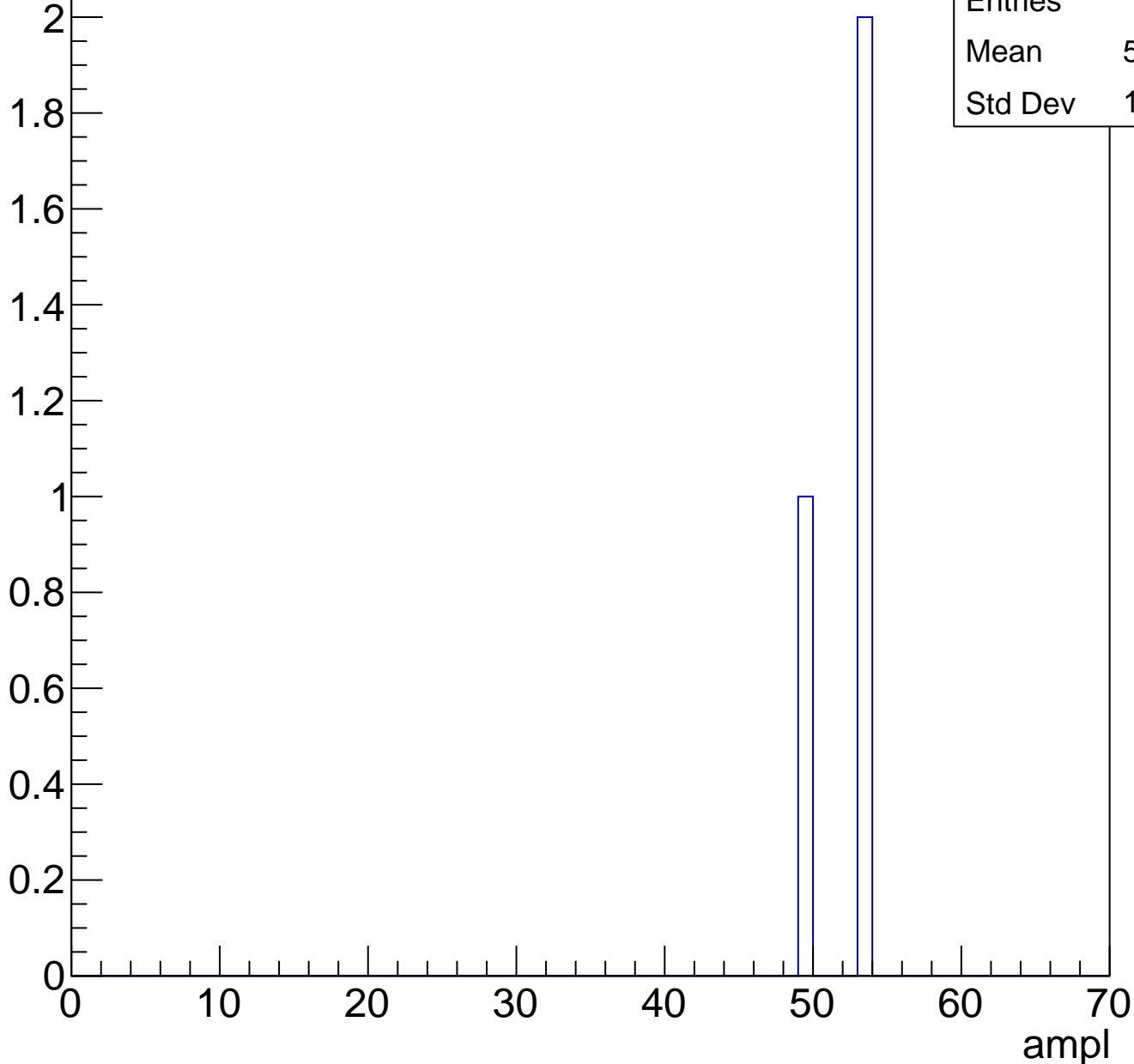


| Entries | 3 |
| Mean | 51.67 |
| Std Dev | 1.886 |

Entry

ampl

# B0L100S, U1-ch72, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch72, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch72, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch72, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch73, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch73, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
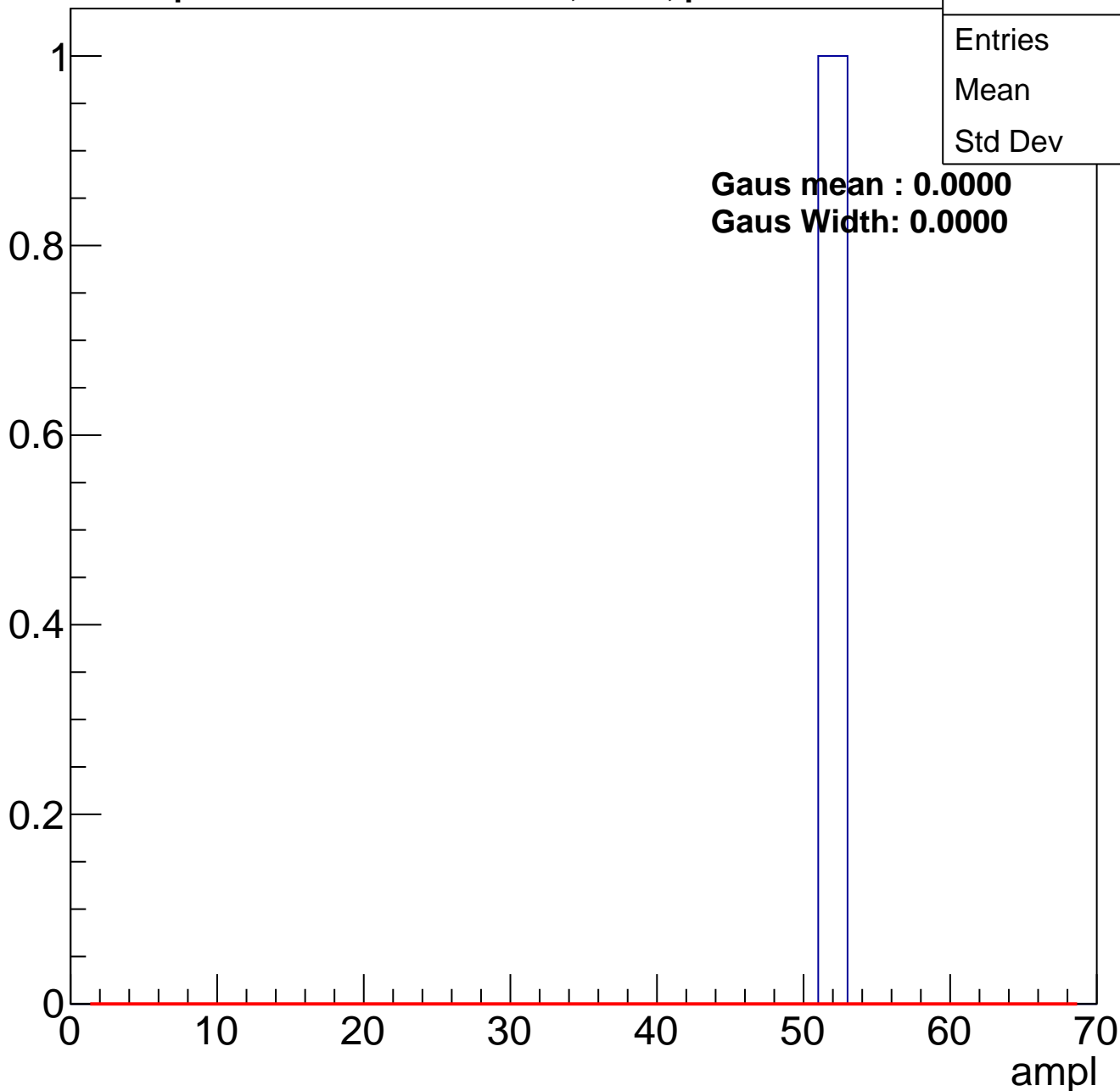
# B0L100S, U1-ch73, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 3 |
| Mean | 51.67 |
| Std Dev | 0.4714 |

ampl
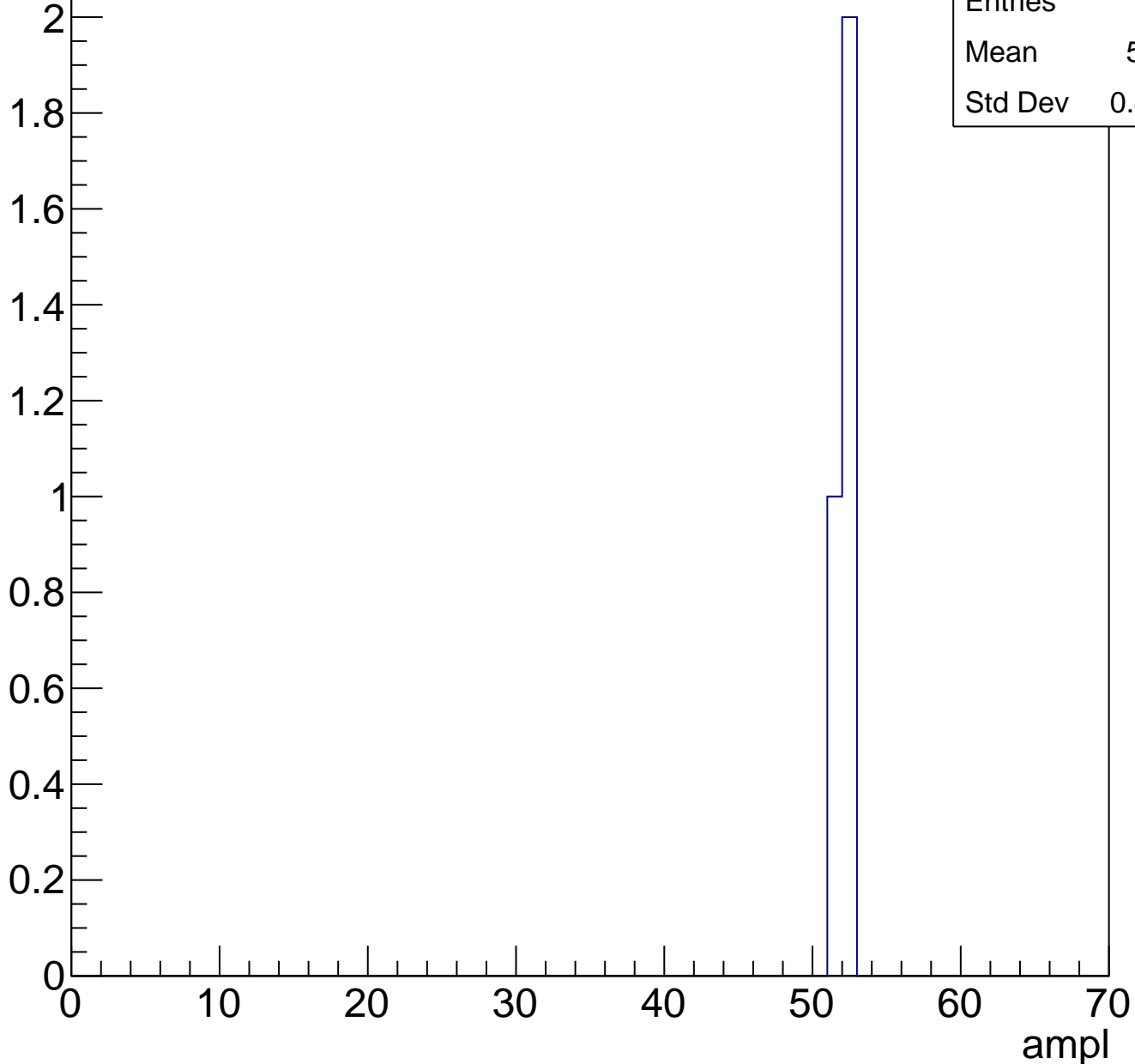
Entry

# B0L100S, U1-ch73, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch73, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch73, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch73, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L100S, U1-ch74, adc0

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch74, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**
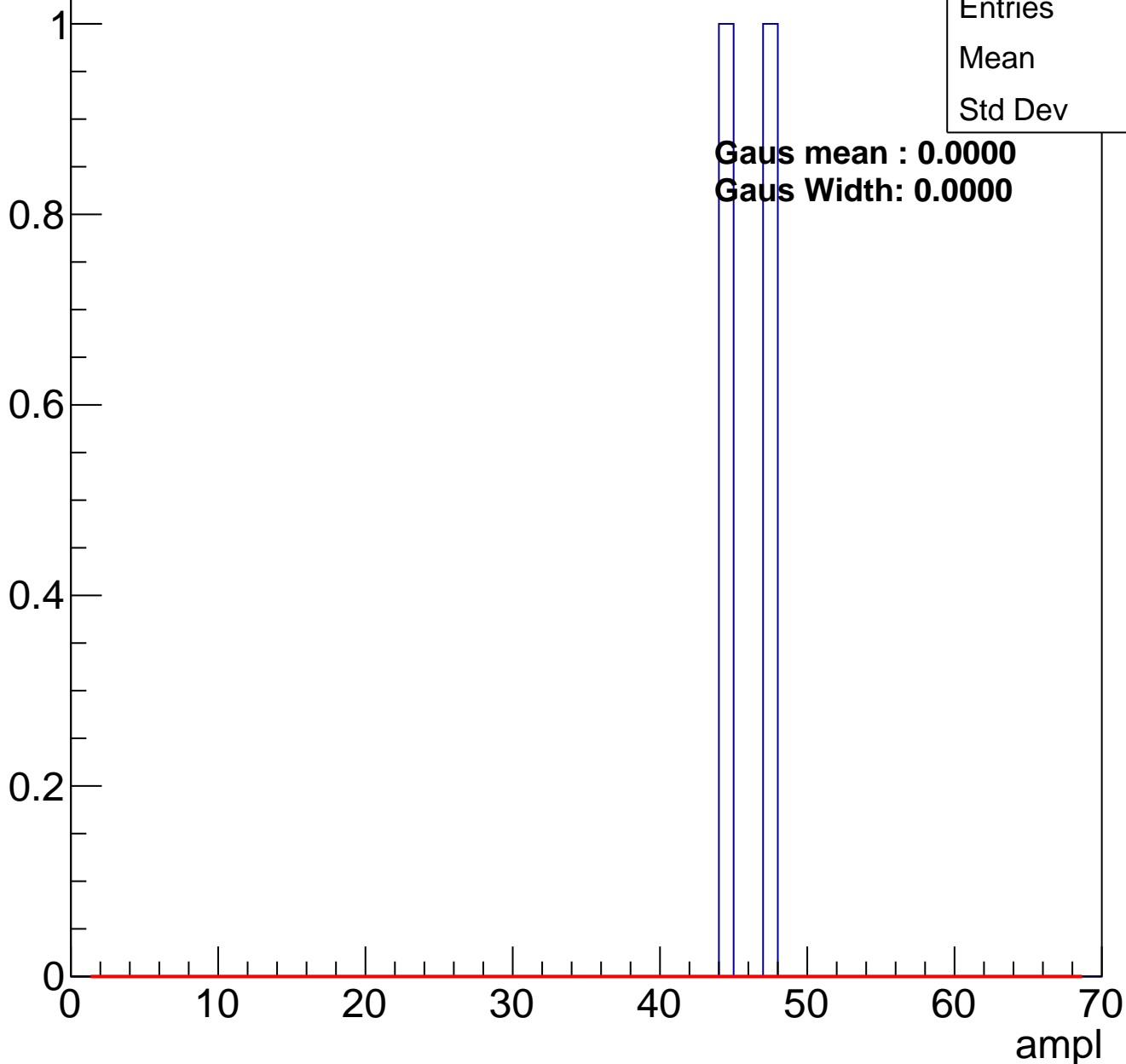
| Entries | 2 |
| Mean | 45.5 |
| Std Dev | 1.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

# B0L100S, U1-ch74, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 3 |
| Mean | 46 |
| Std Dev | 1.414 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
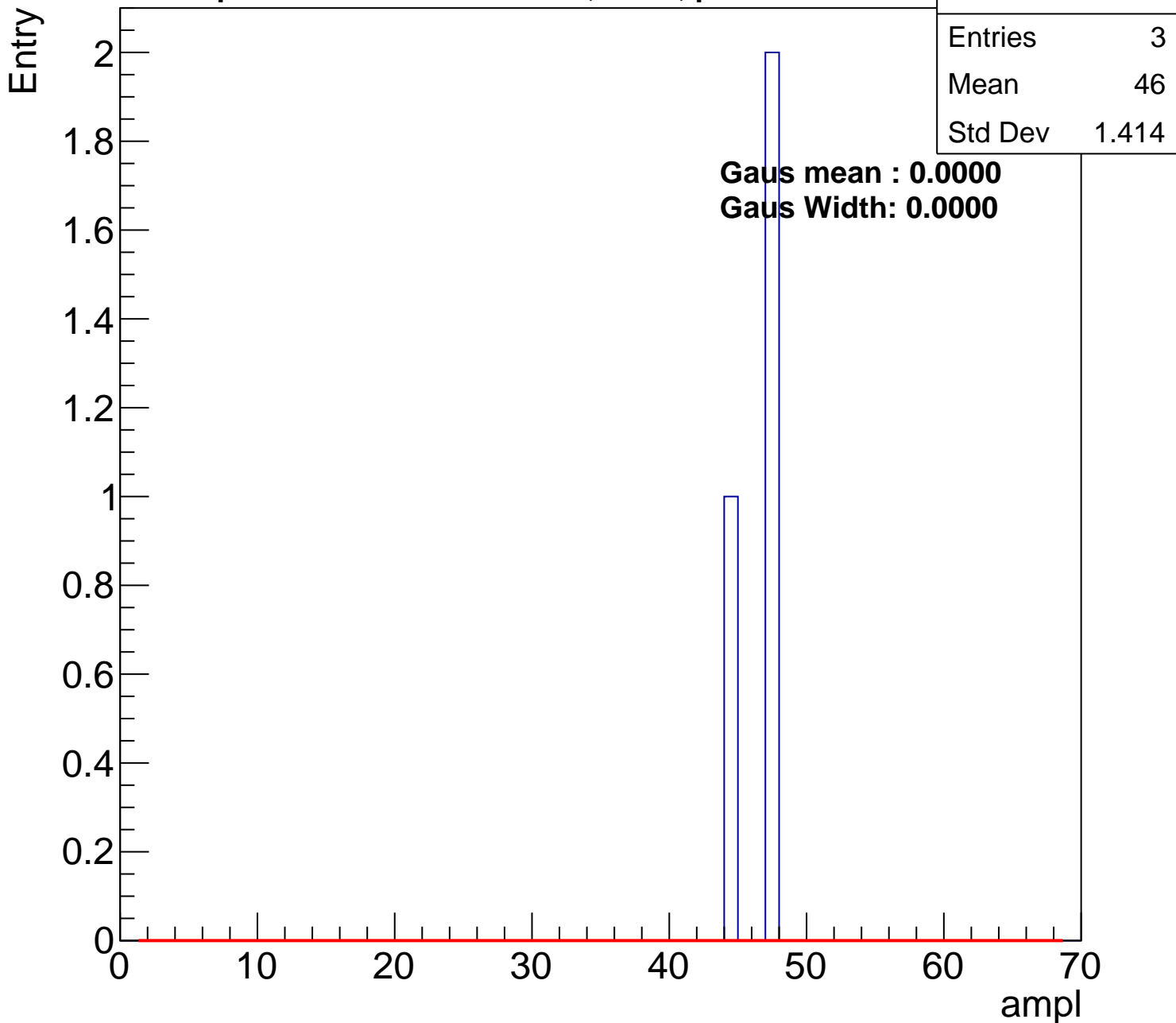
# B0L100S, U1-ch74, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 47 |
| Std Dev | 0 |

ampl
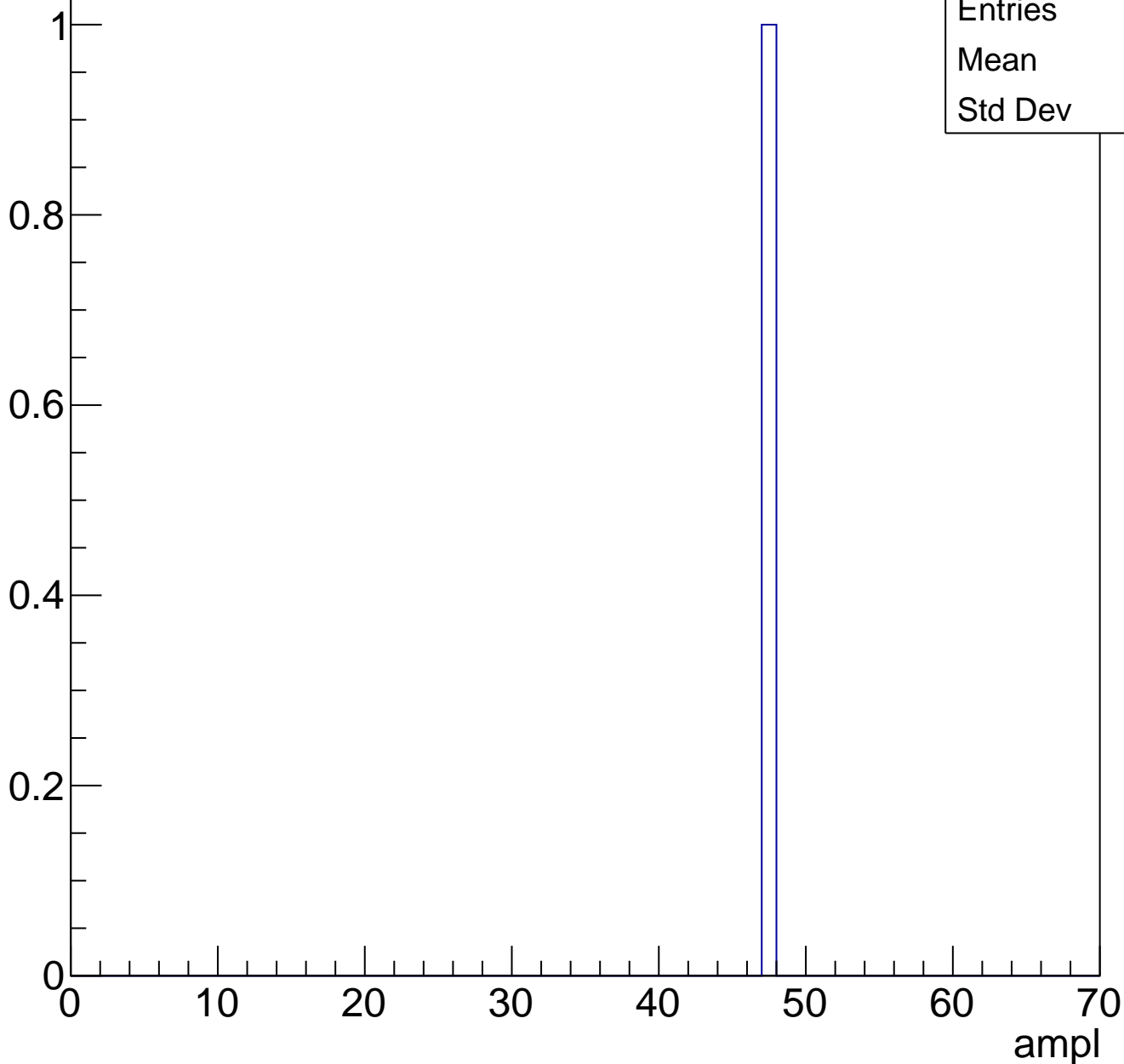
Entry

# B0L100S, U1-ch74, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch74, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L100S, U1-ch74, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L100S, U1-ch74, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch75, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch75, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch75, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U1-ch75, adc3

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch75, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch75, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch75, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch75, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |
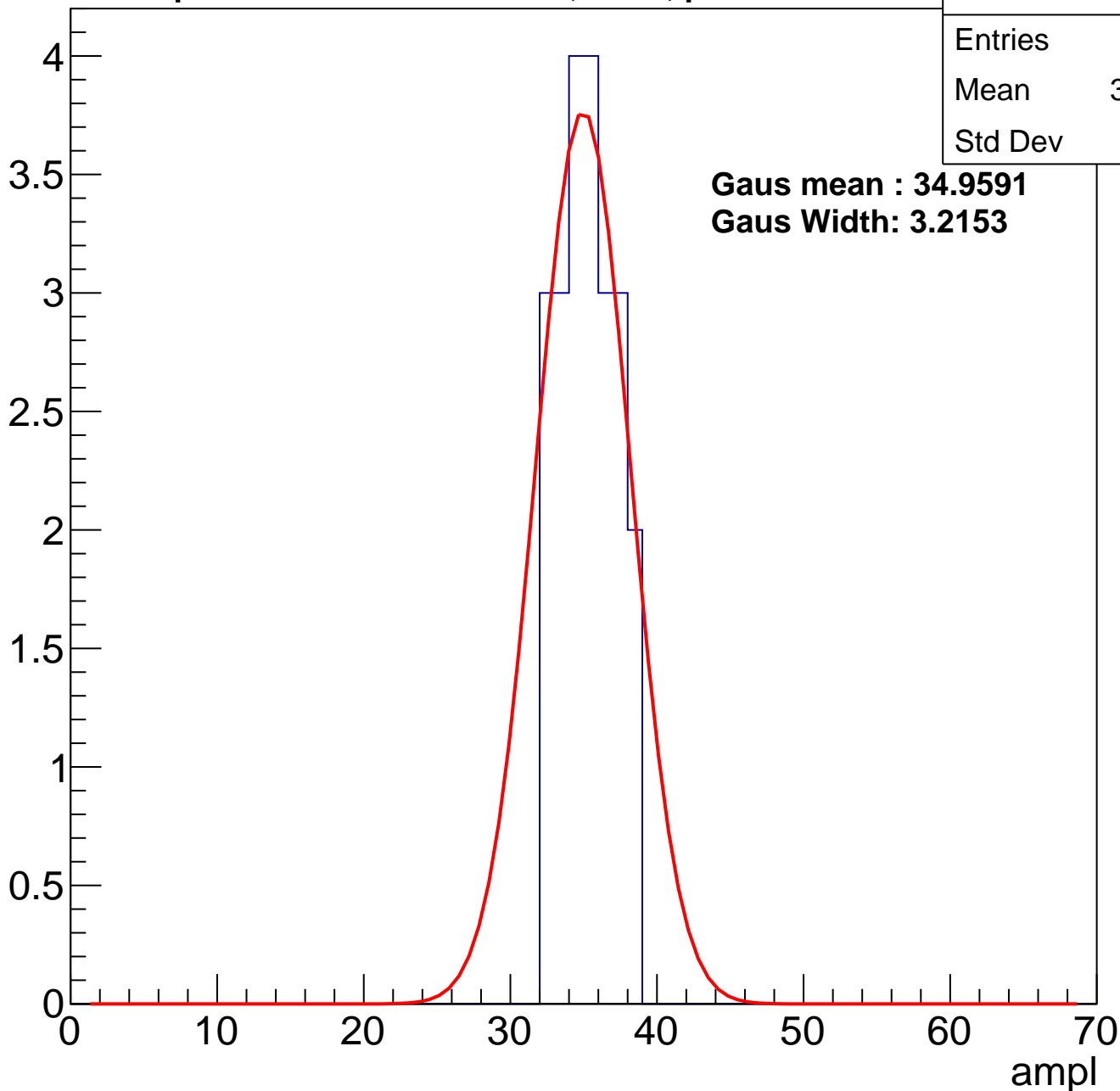
ampl

# B0L100S, U1-ch76, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 9 |
| Mean | 37.56 |
| Std Dev | 2.872 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
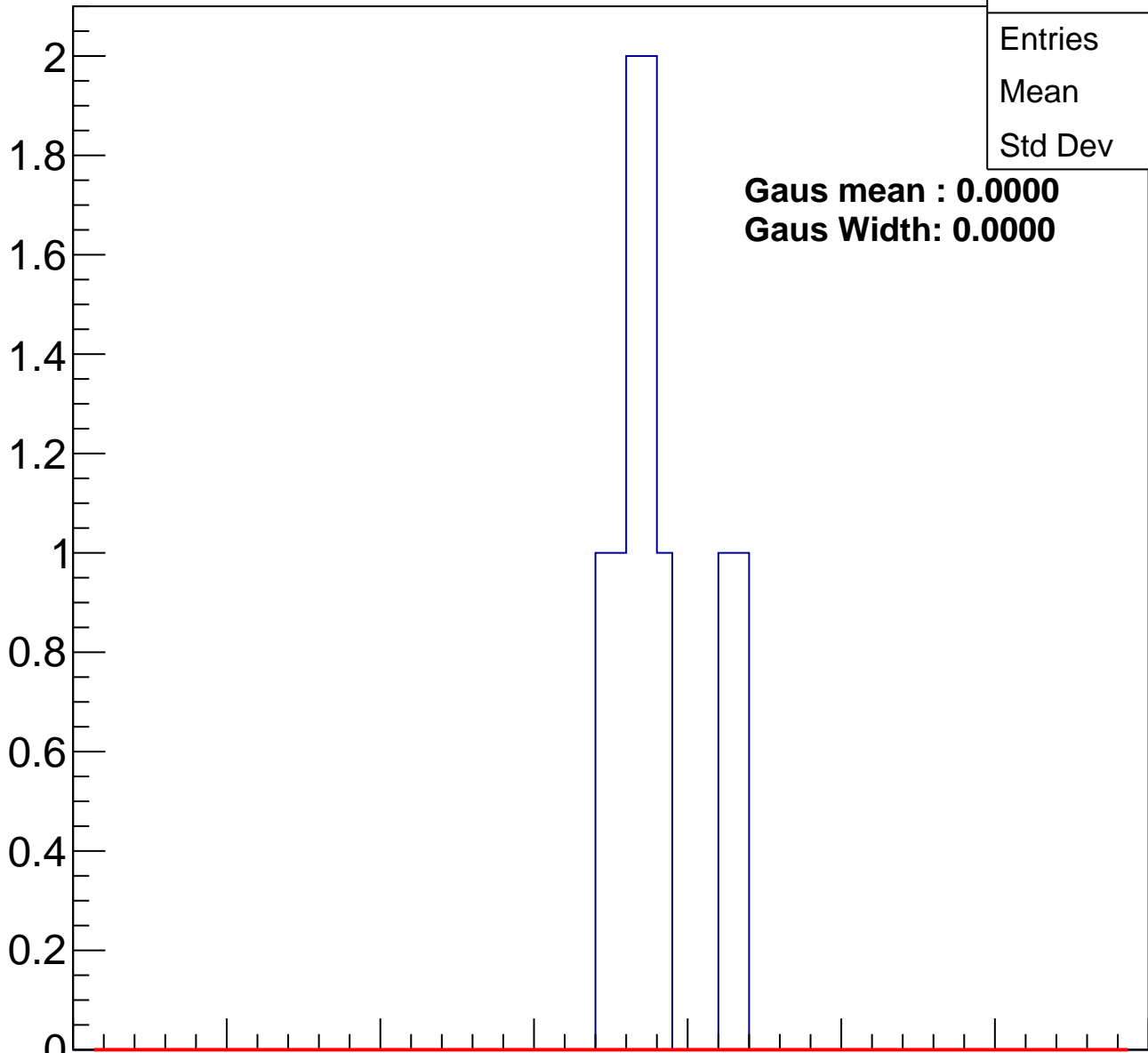
Entry

ampl

# B0L100S, U1-ch76, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 2 |
|---------|---|
| Mean | 43 |
| Std Dev | 1 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
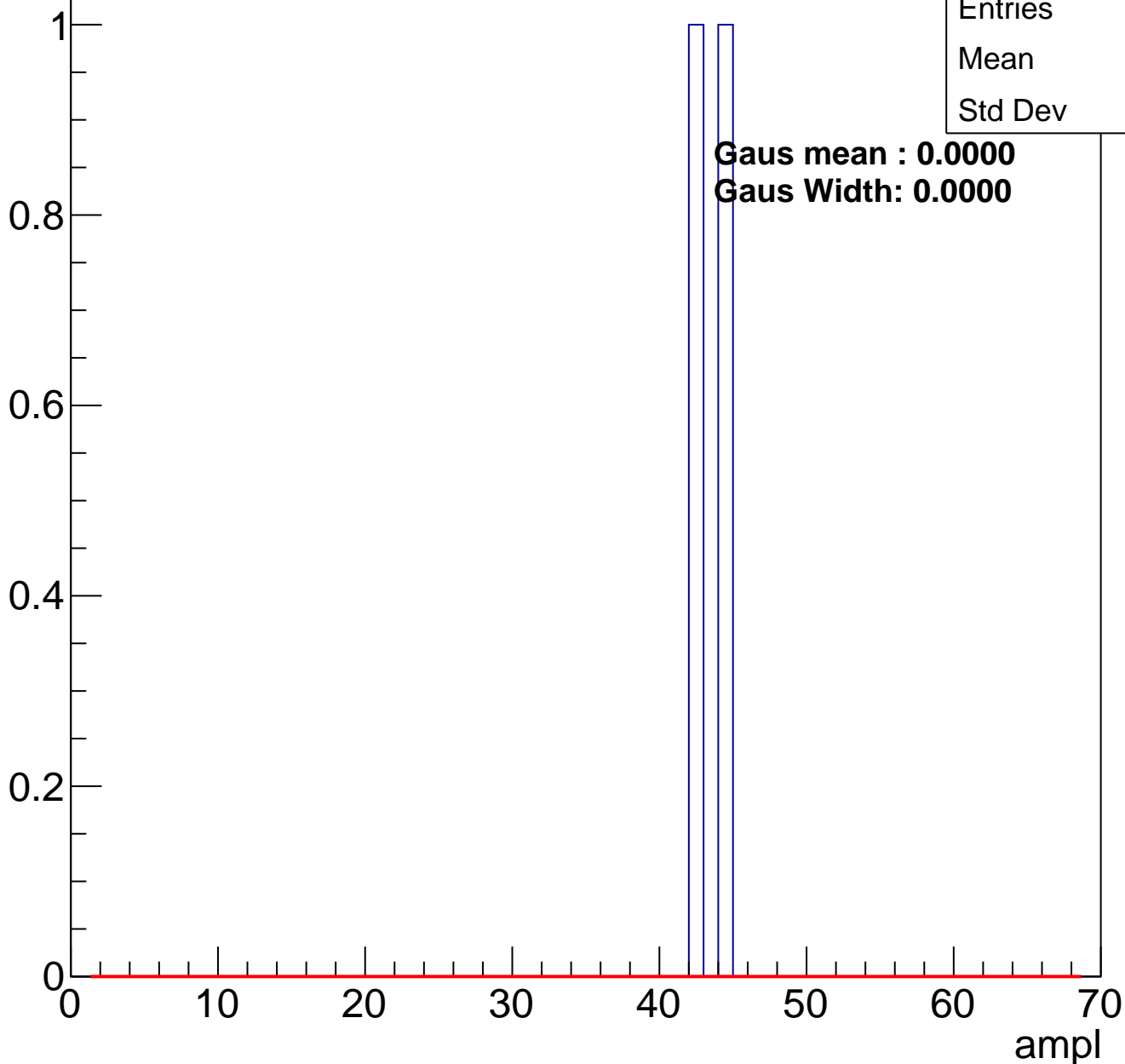
# B0L100S, U1-ch76, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 4 |
| Mean | 52 |
| Std Dev | 1 |

B0L100S, U1-ch76, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch76, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch76, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch76, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch77, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch77, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch77, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 52 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
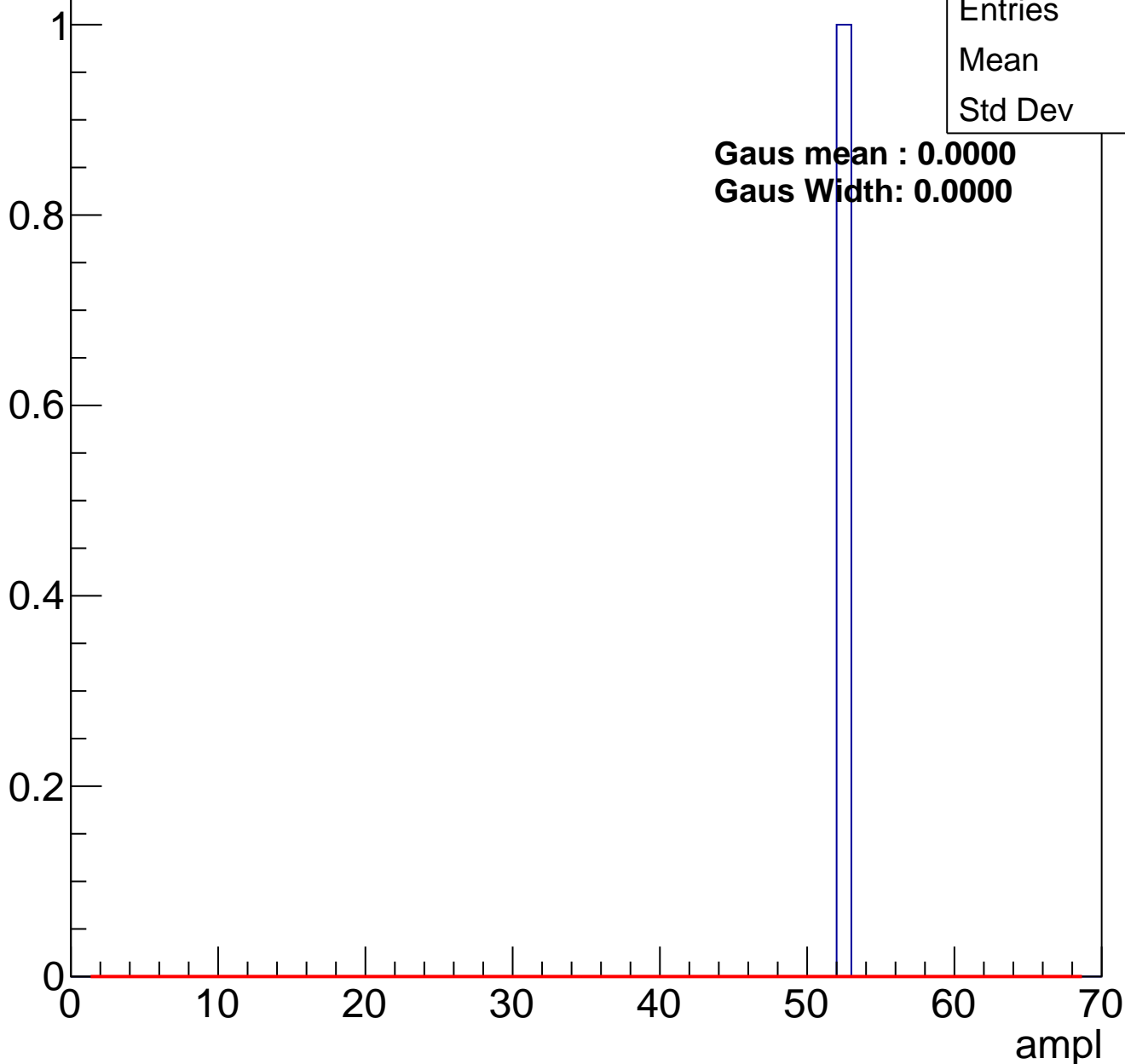
# B0L100S, U1-ch77, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 1 |
|---------|---|
| Mean | 52 |
| Std Dev | 0 |

ampl
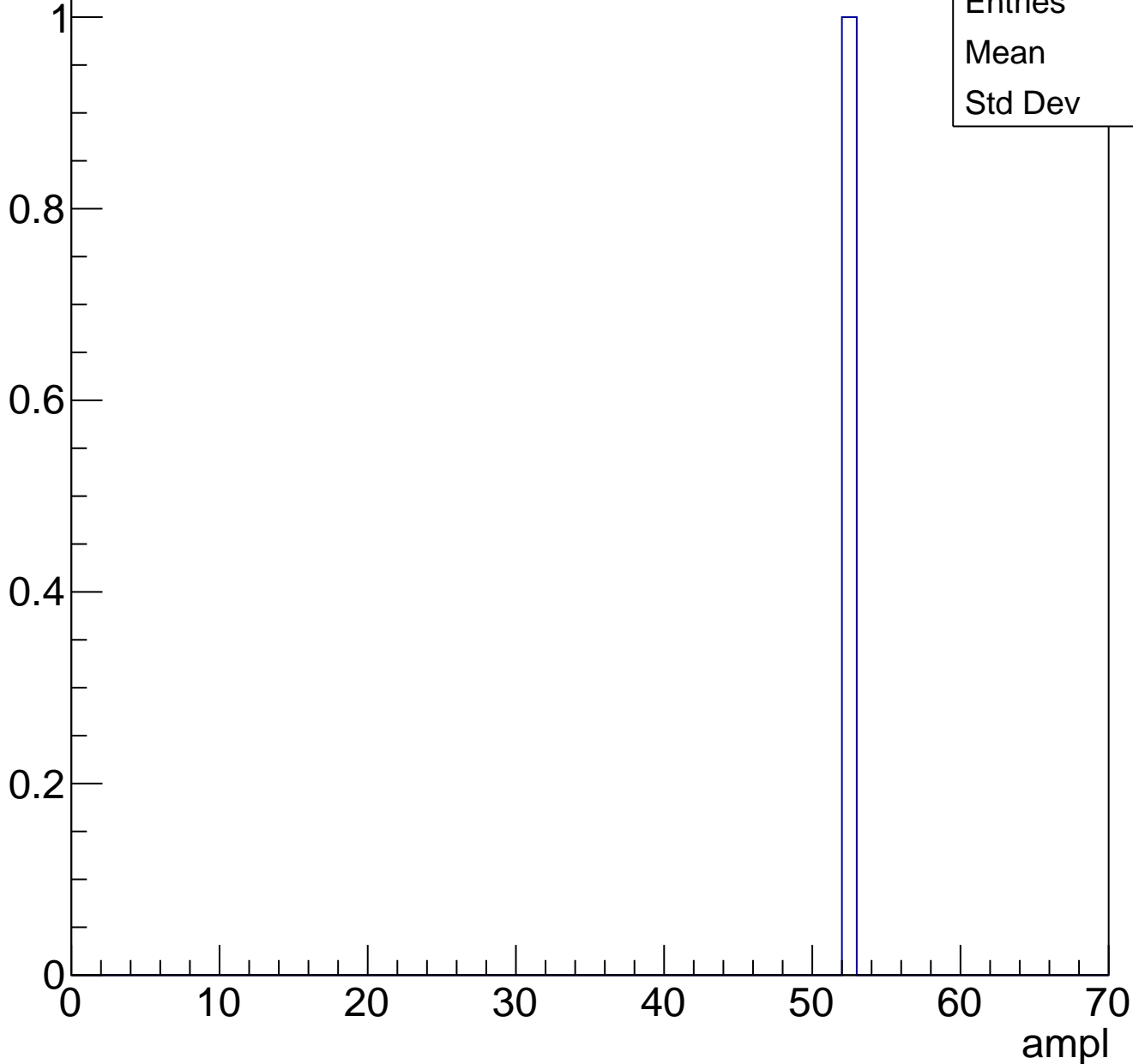
Entry

# B0L100S, U1-ch77, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch77, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch77, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch77, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch78, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| Entries | 3 |
|---------|-----|
| Mean | 44.67 |
| Std Dev | 4.11 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl
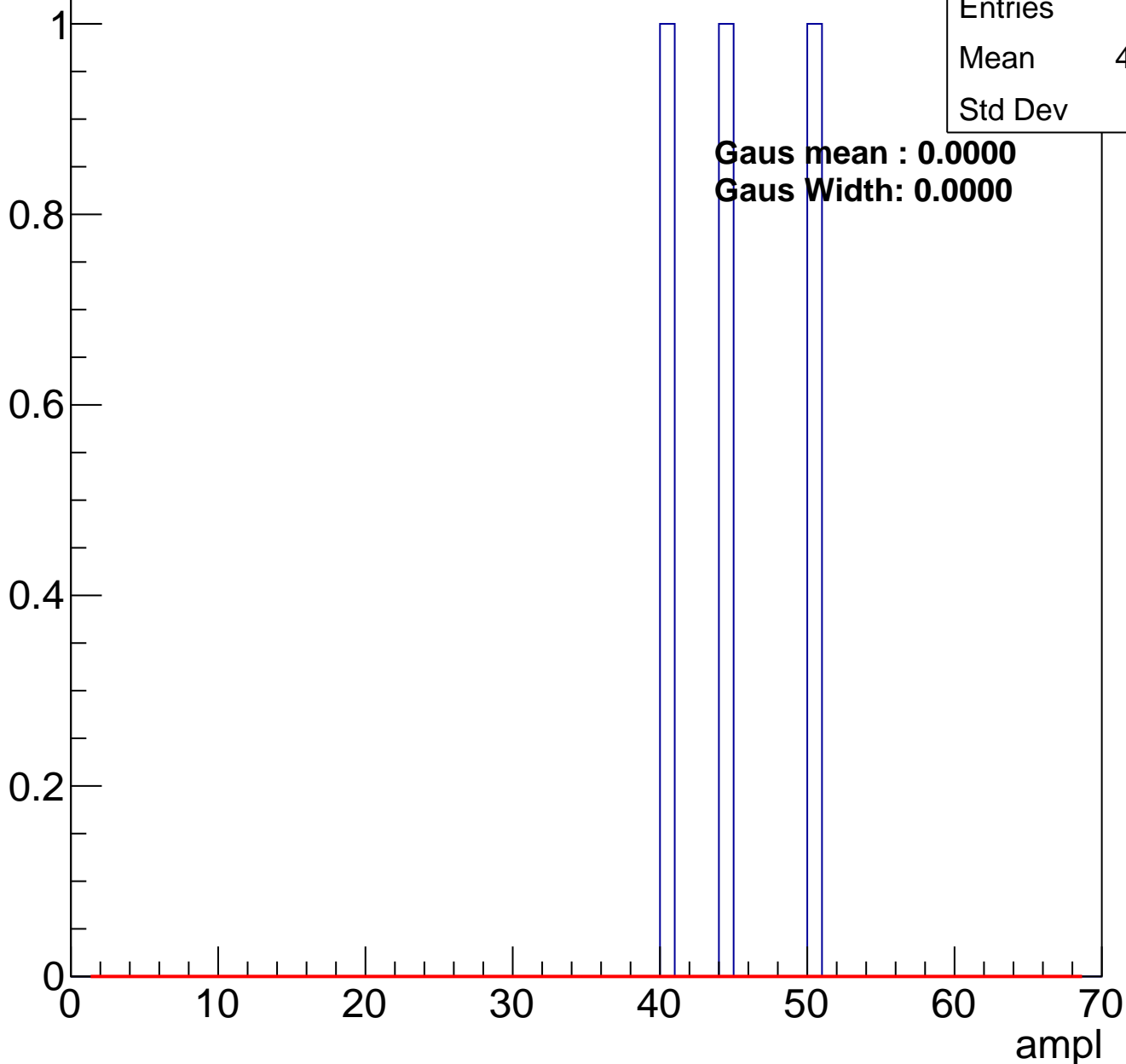
# B0L100S, U1-ch78, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 12 |
|---|---|
| Mean | 44.83 |
| Std Dev | 3.287 |

**Gaus mean : 50.9624**
**Gaus Width: 32.8717**

Entry

ampl
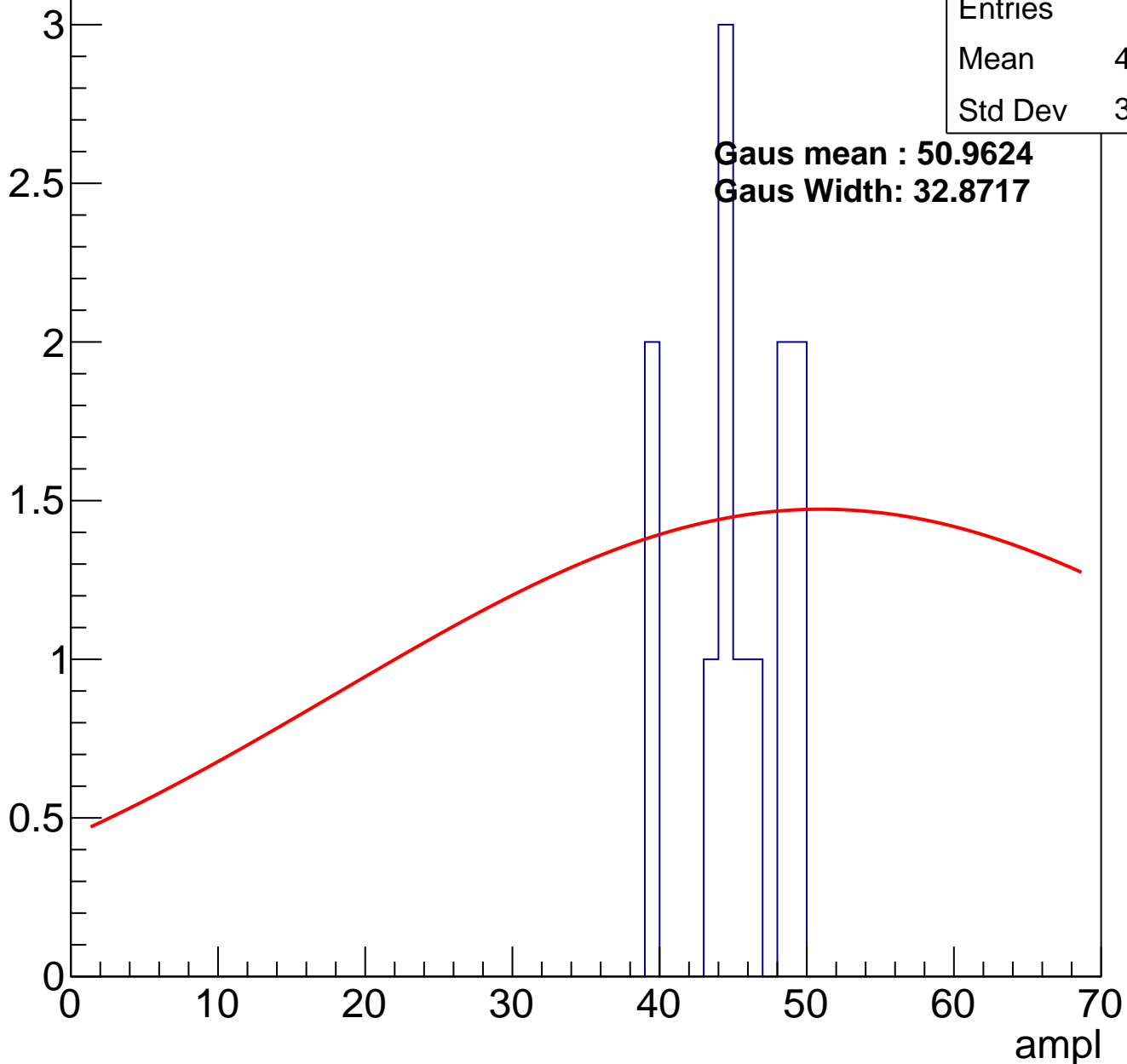
# B0L100S, U1-ch78, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 9 |
| Mean | 46.22 |
| Std Dev | 3.489 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
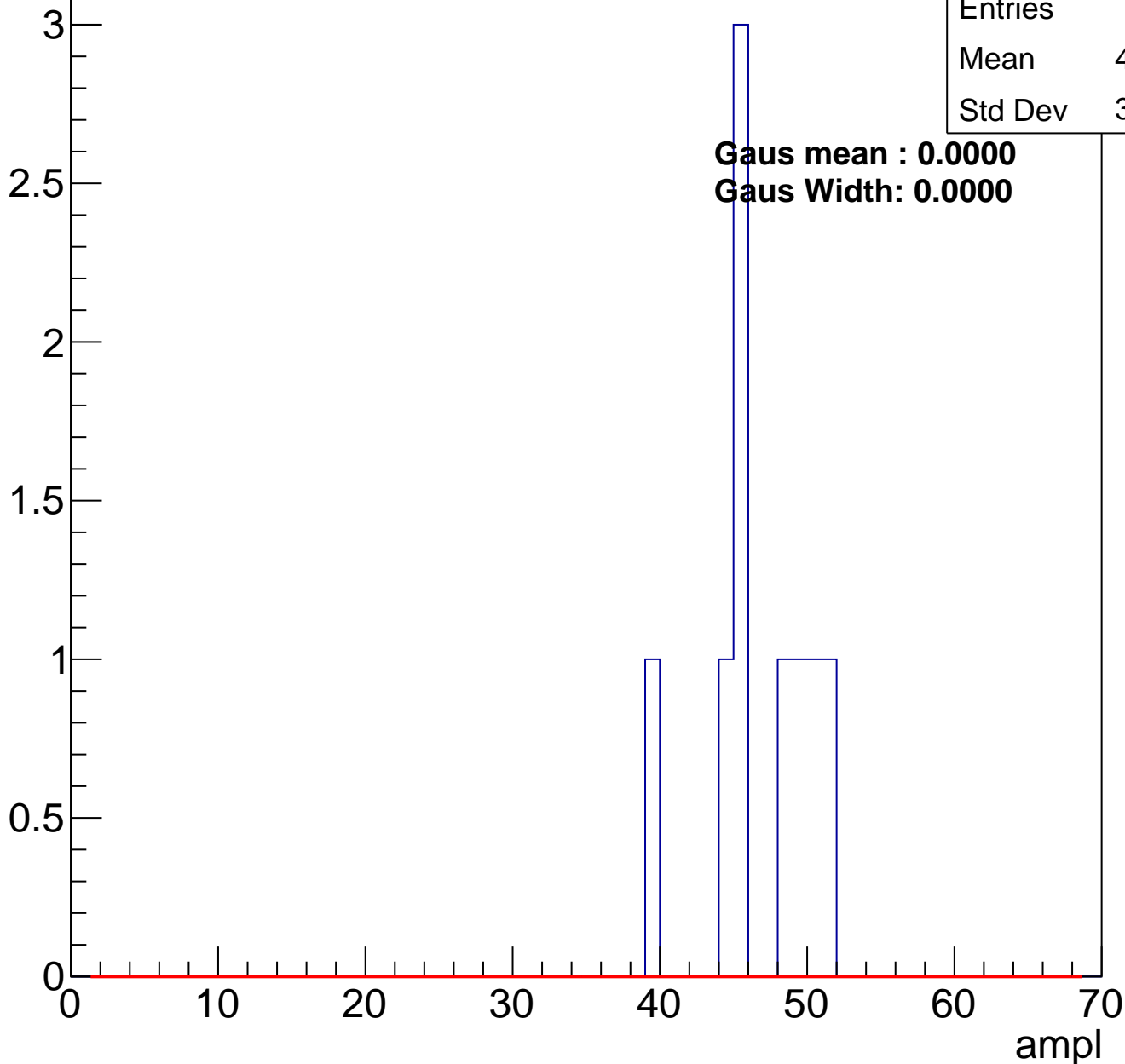
# B0L100S, U1-ch78, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 10 |
| Mean | 49 |
| Std Dev | 1.897 |



Entry

ampl
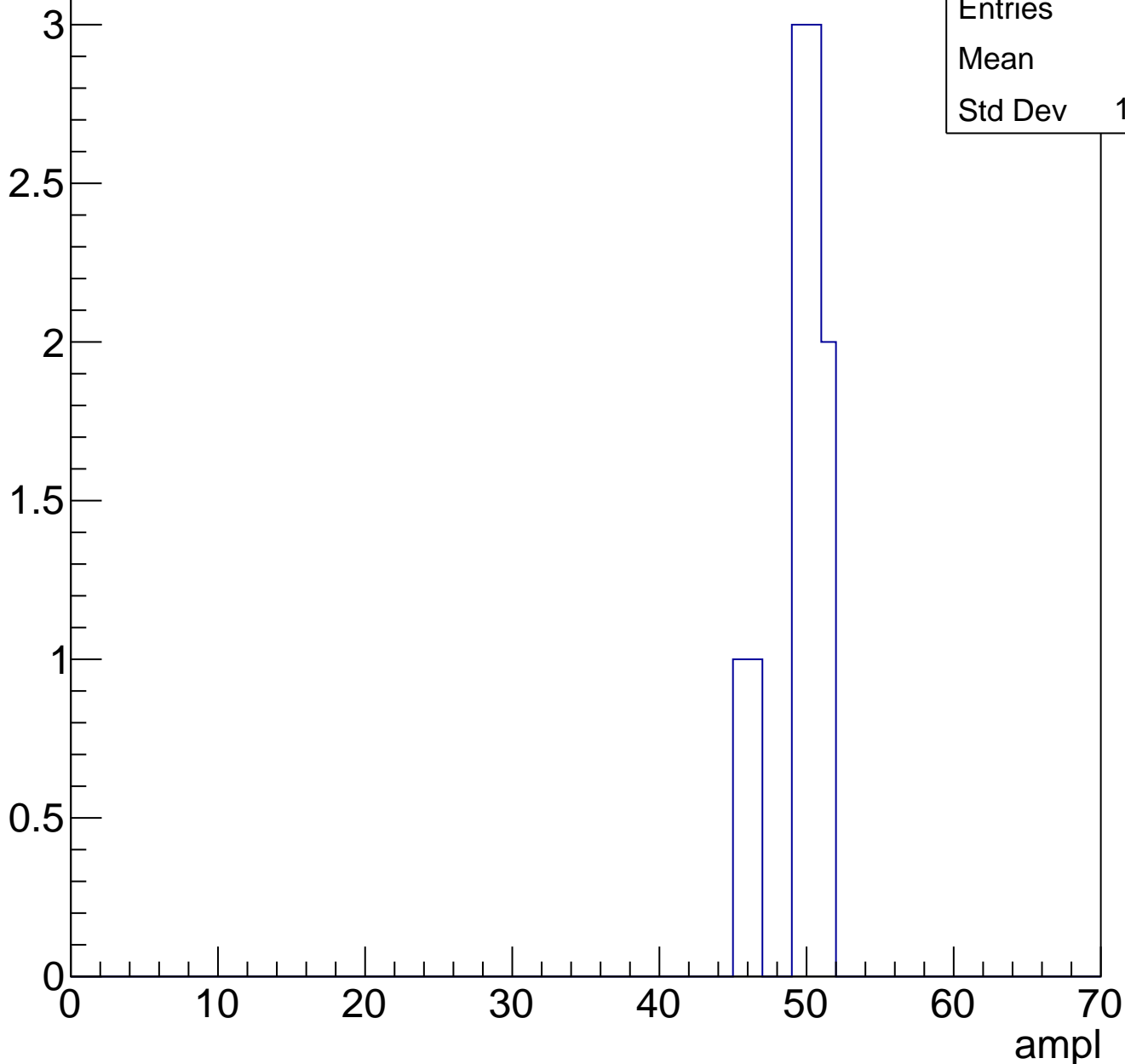
# B0L100S, U1-ch78, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch78, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch78, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch78, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch79, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch79, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 2 |
| Mean | 46.5 |
| Std Dev | 1.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
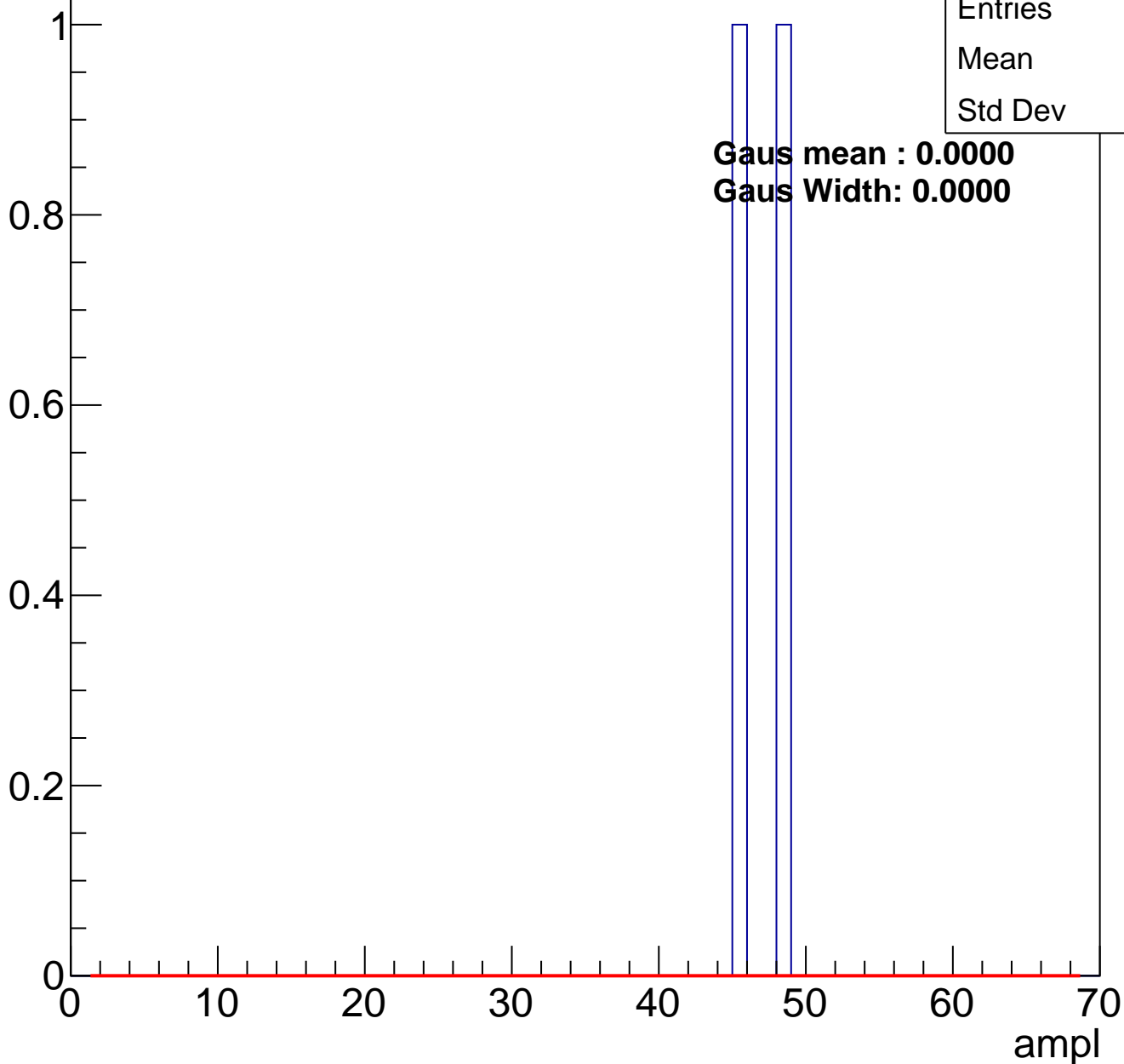
Entry

ampl

# B0L100S, U1-ch79, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 16 |
|---------|-----|
| Mean | 46.81 |
| Std Dev | 1.014 |

**Gaus mean : 47.8804**
**Gaus Width: 1.7310**

Entry

ampl

# B0L100S, U1-ch79, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
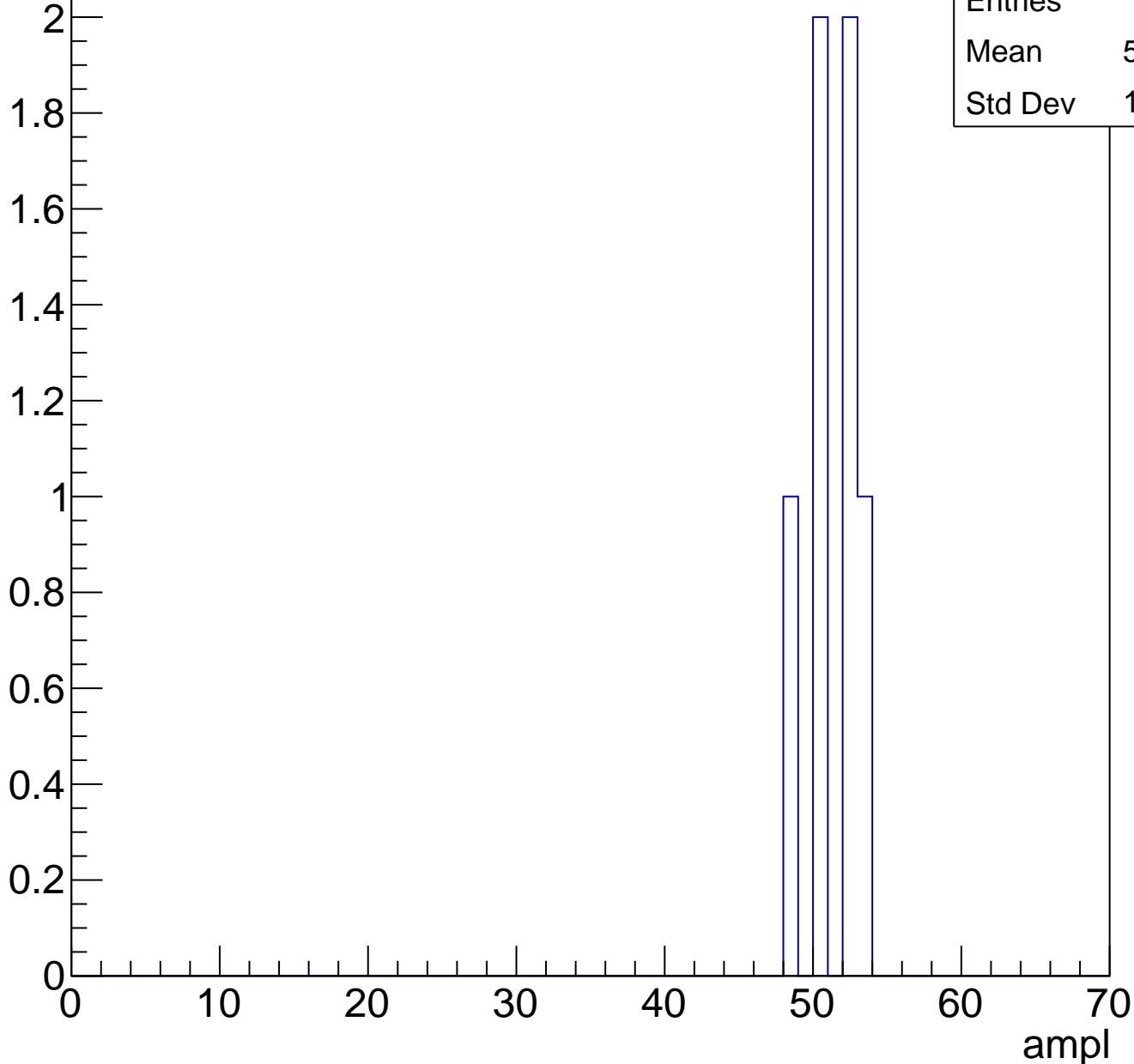
| | |
|---|---|
| Entries | 6 |
| Mean | 50.83 |
| Std Dev | 1.675 |

Entry

ampl

# B0L100S, U1-ch79, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch79, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch79, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch79, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch80, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch80, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 52 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
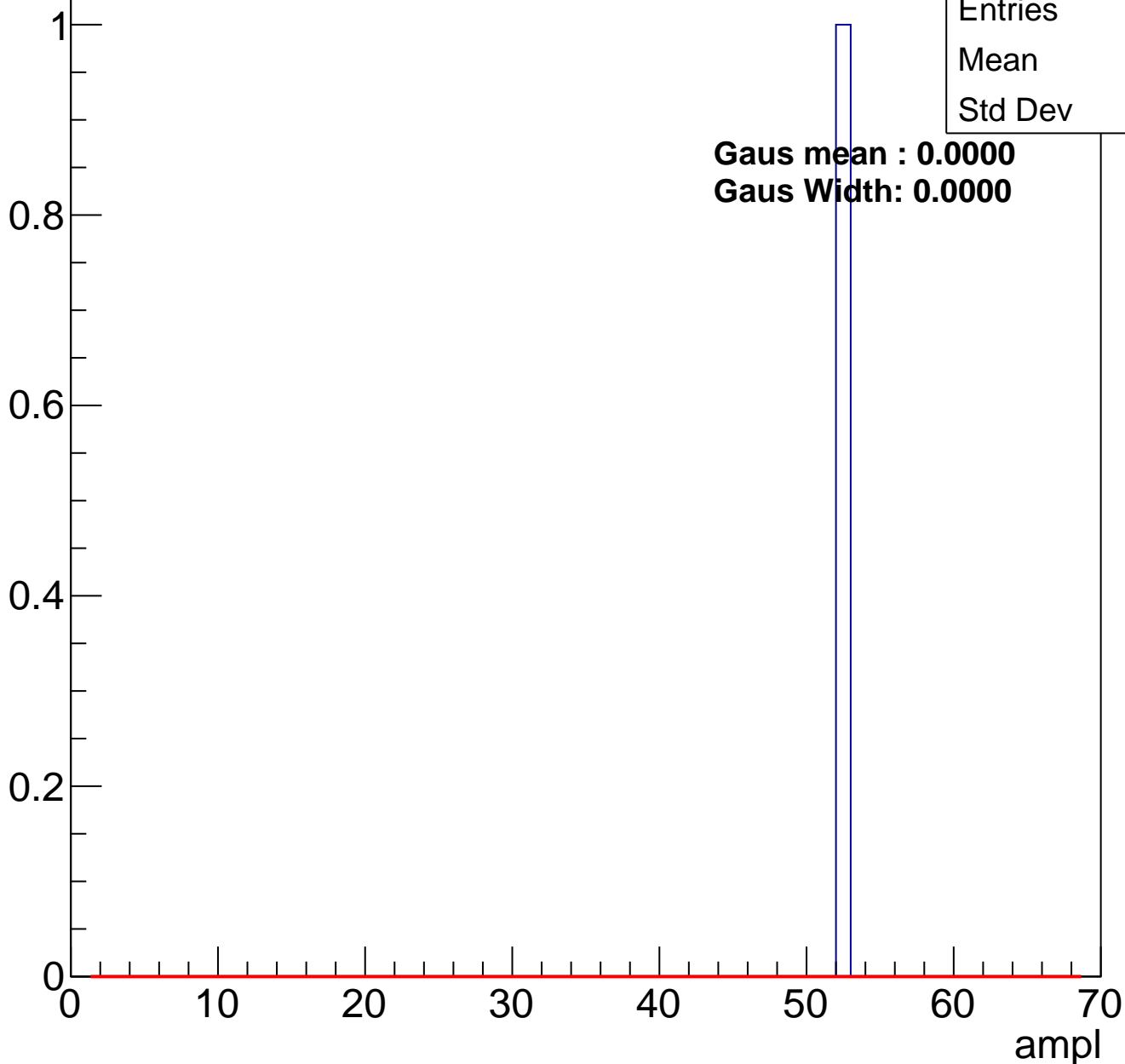
Entry

ampl

B0L100S, U1-ch80, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 2 |
| Mean | 52 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000
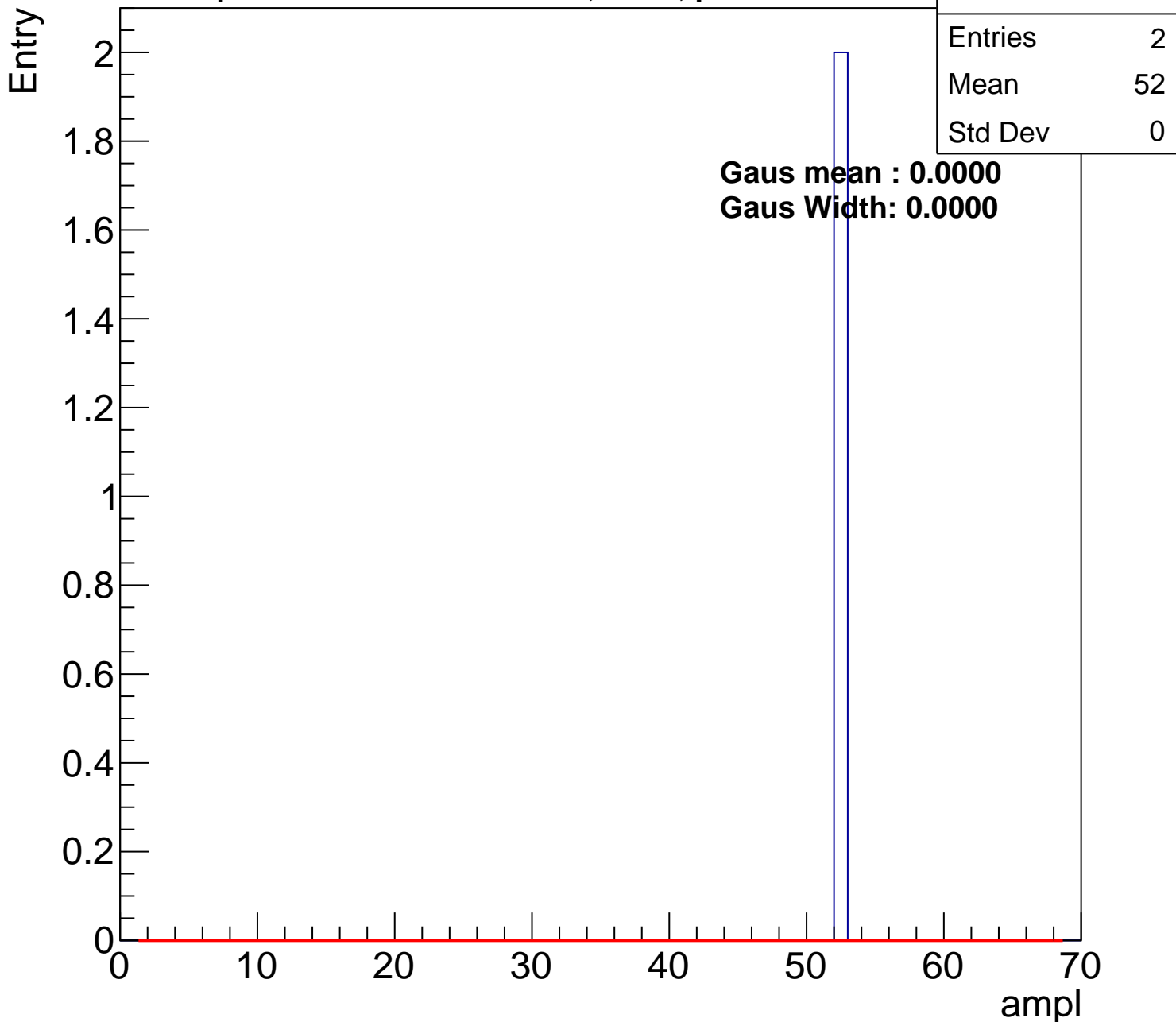
Entry

ampl

# B0L100S, U1-ch80, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 4 |
| Mean | 52.5 |
| Std Dev | 0.5 |

Entry

ampl

# B0L100S, U1-ch80, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch80, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch80, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch80, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch81, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch81, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

| Entries | 1 |
| Mean | 47 |
| Std Dev | 0 |
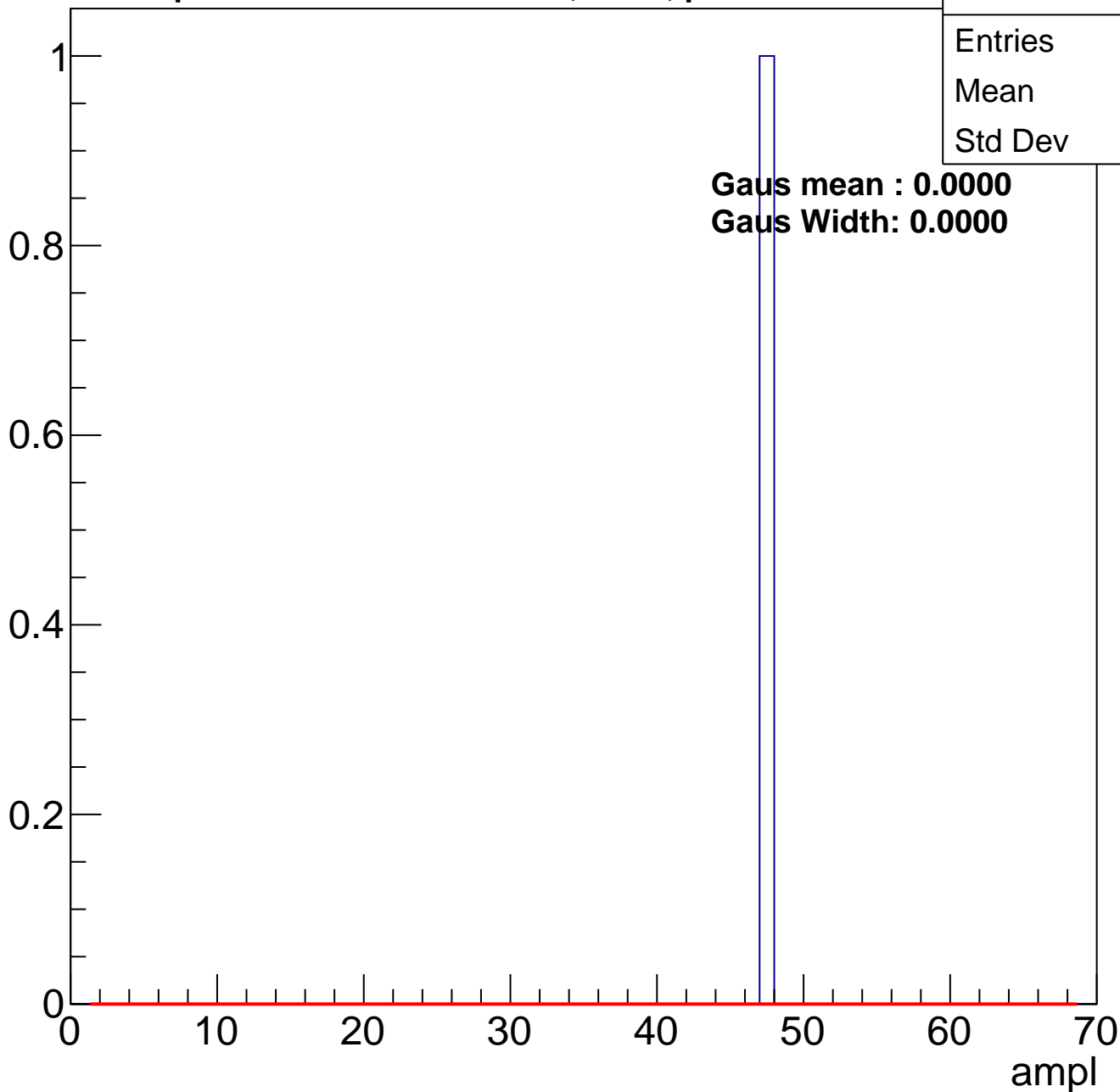
# B0L100S, U1-ch81, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 47.67 |
| Std Dev | 0.4714 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch81, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 2 |
|---|---|
| Mean | 49.5 |
| Std Dev | 1.5 |

Entry

ampl
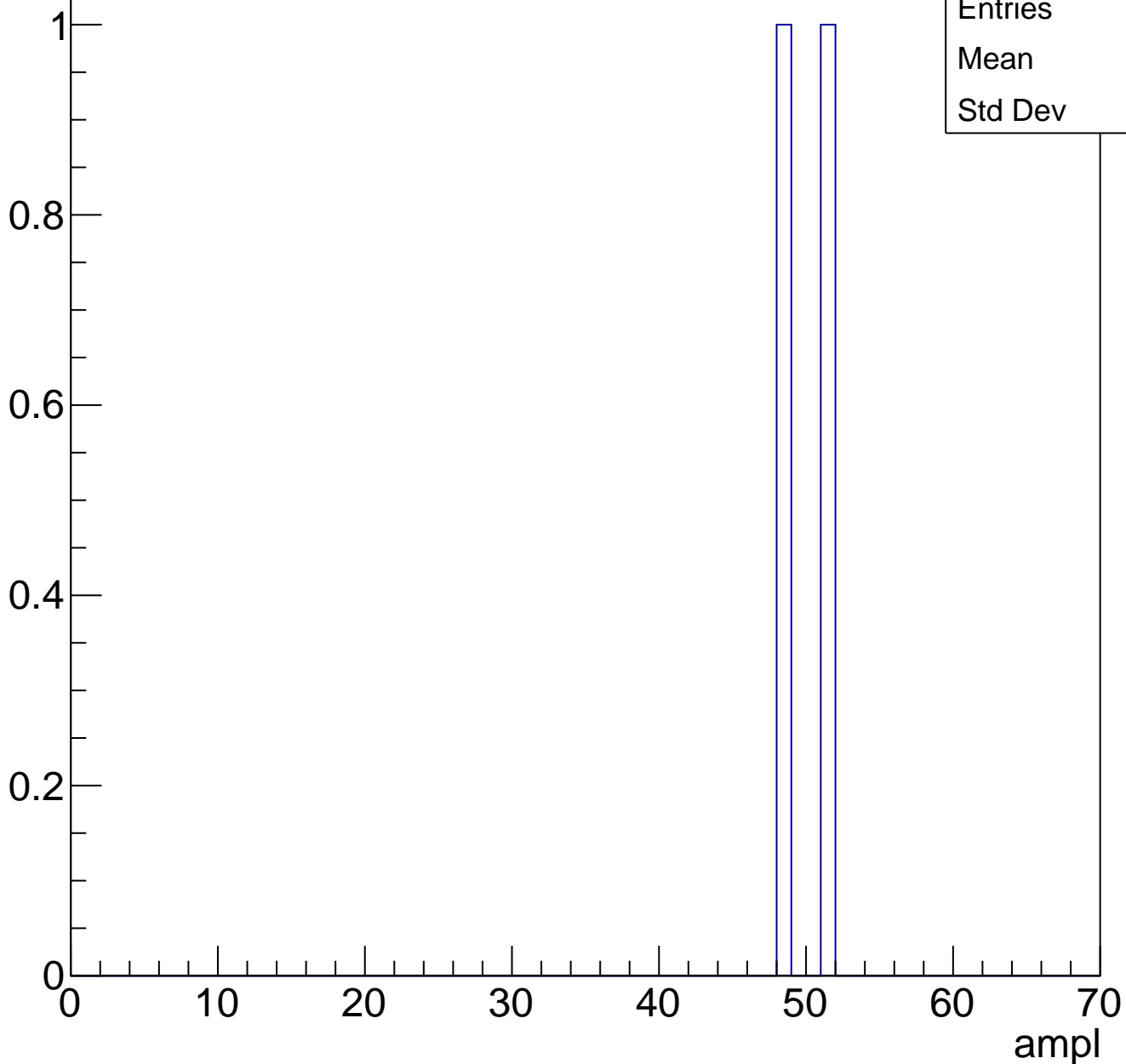
# B0L100S, U1-ch81, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch81, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch81, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch81, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch82, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch82, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**
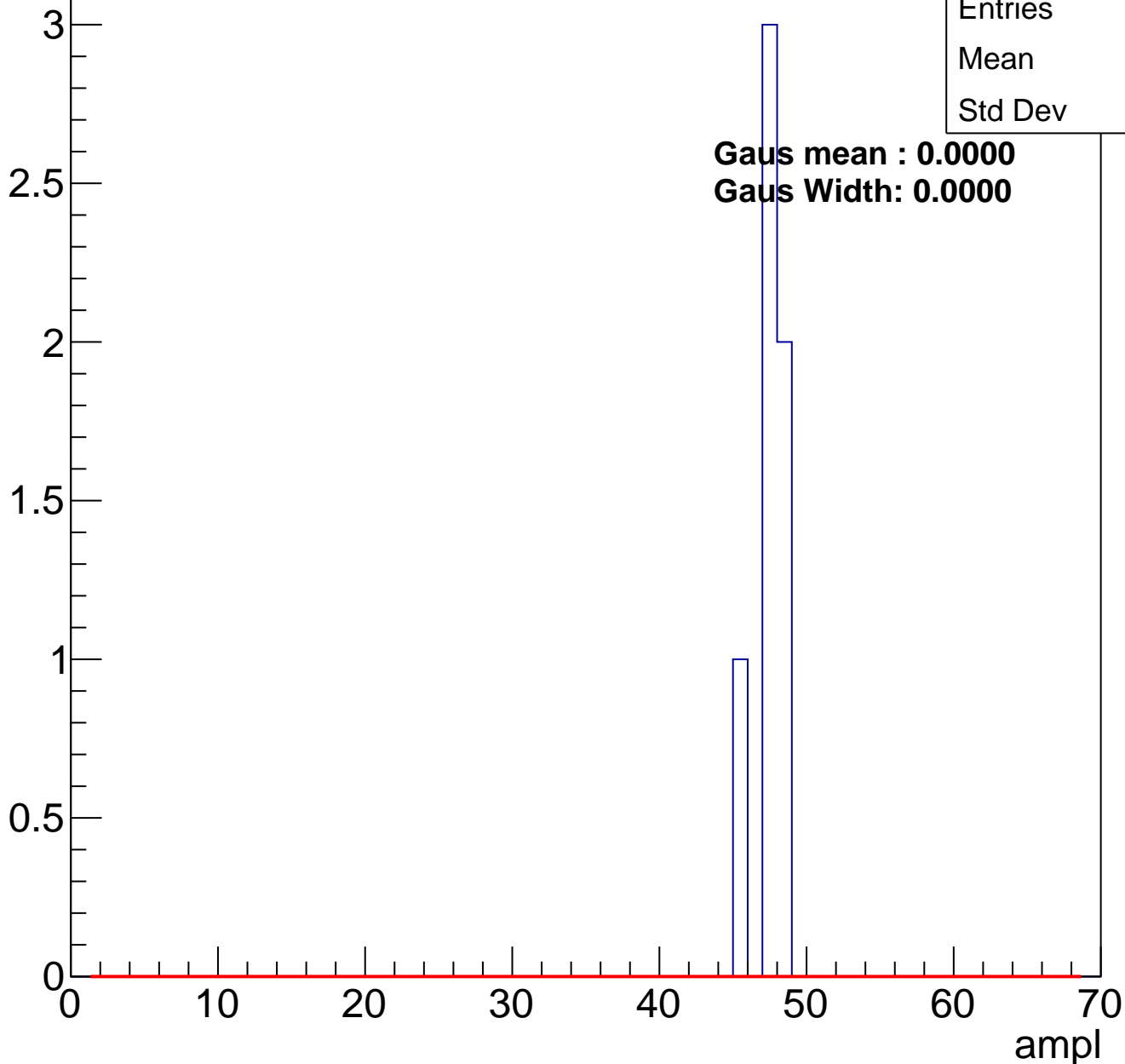
| Entries | 6 |
|---------|---|
| Mean | 47 |
| Std Dev | 1 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
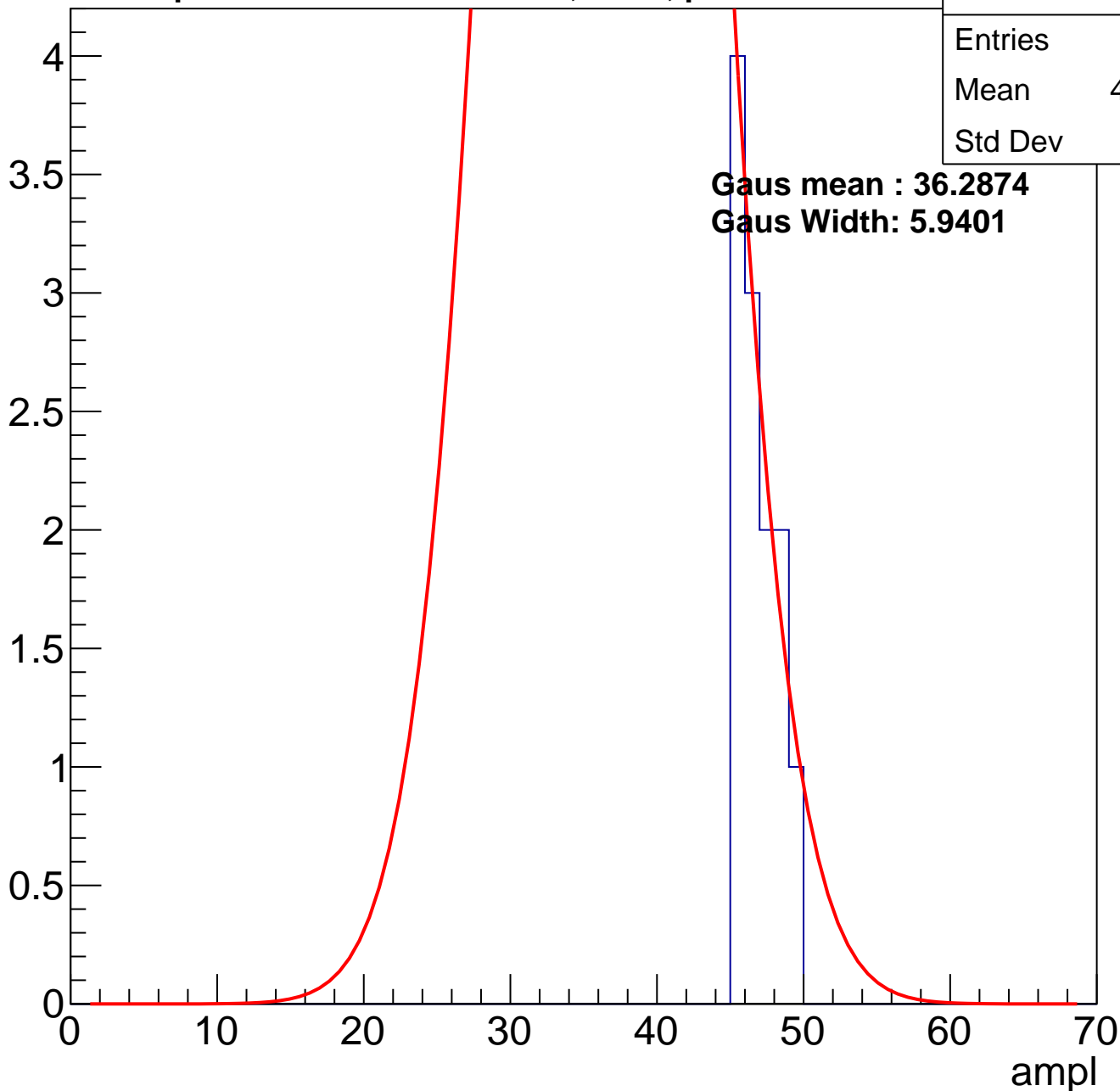
B0L100S, U1-ch82, adc3

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 3 |
| Mean | 46.67 |
| Std Dev | 0.9428 |

Entry

ampl
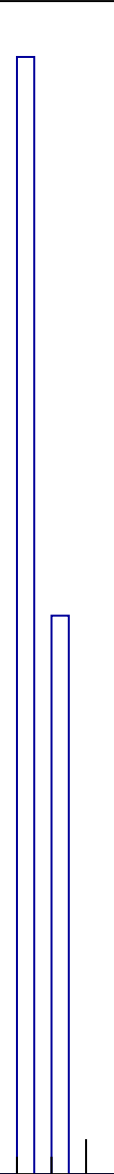
B0L100S, U1-ch82, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch82, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch82, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch82, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch83, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch83, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch83, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch83, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch83, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch83, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch83, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch84, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
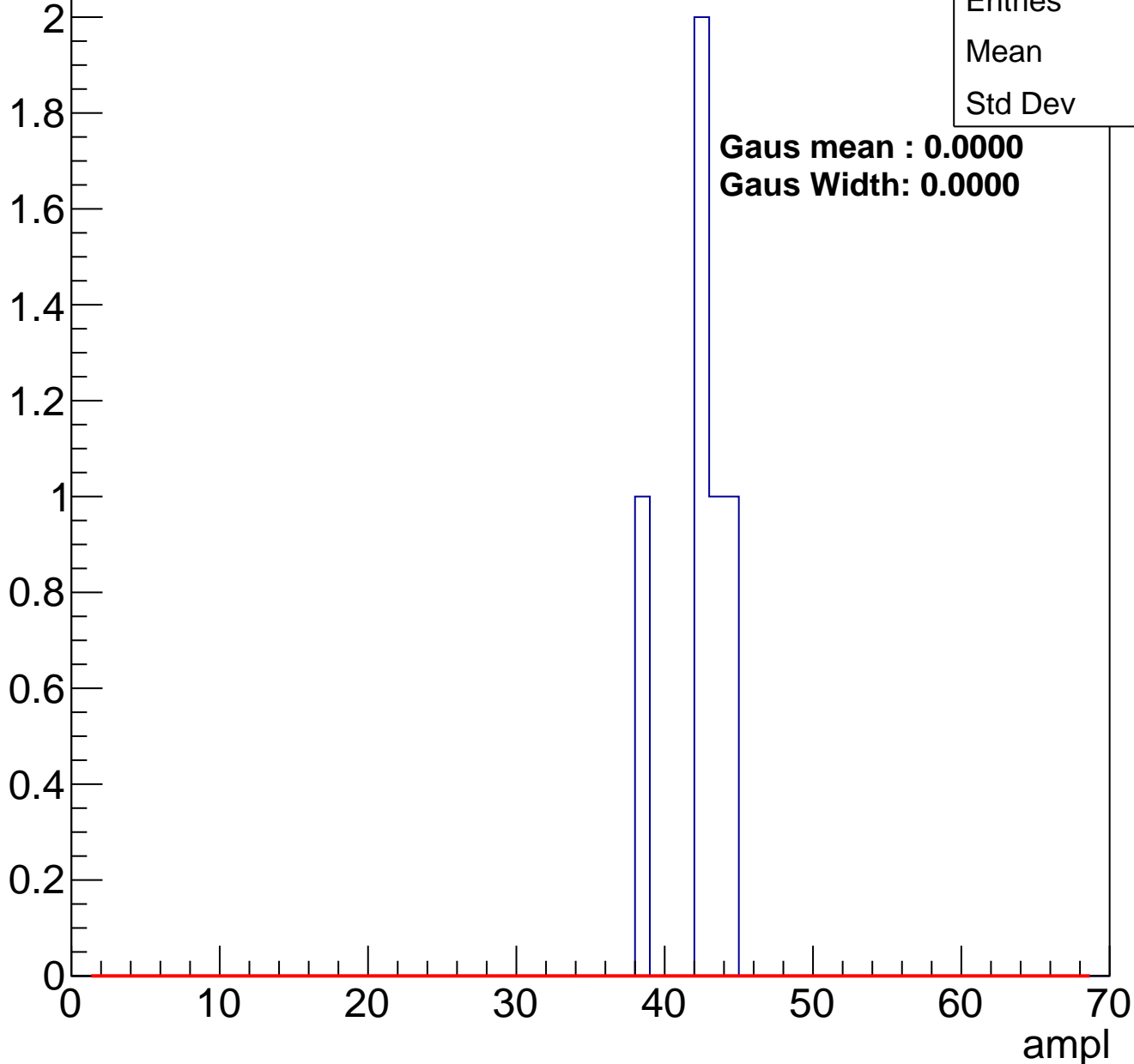
| Entries | 5 |
| Mean | 41.8 |
| Std Dev | 2.04 |

# B0L100S, U1-ch84, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 11 |
| --- | --- |
| Mean | 42.27 |
| Std Dev | 3.792 |

**Gaus mean : 44.0773**
**Gaus Width: 7.6400**

Entry

ampl
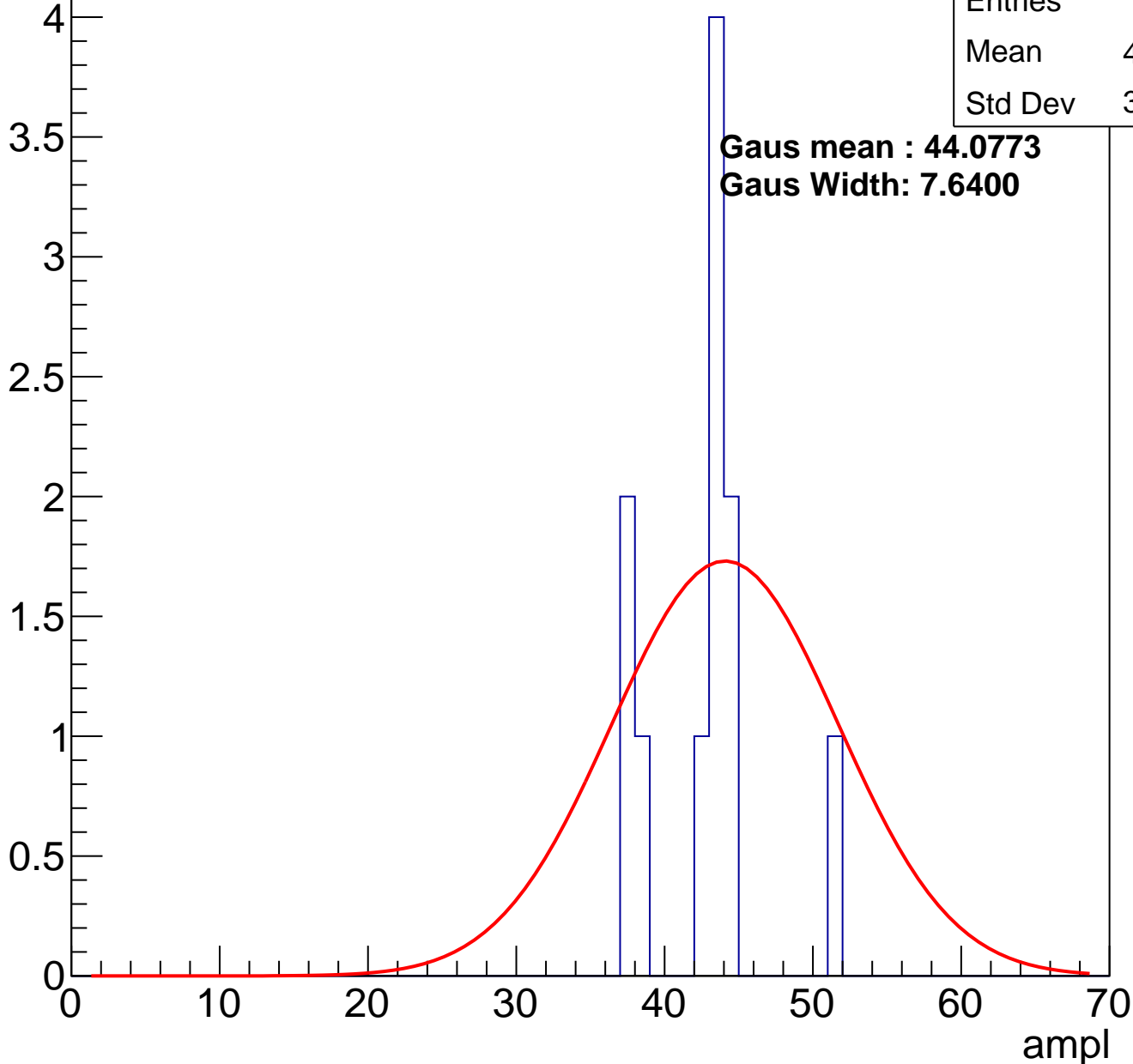
# B0L100S, U1-ch84, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
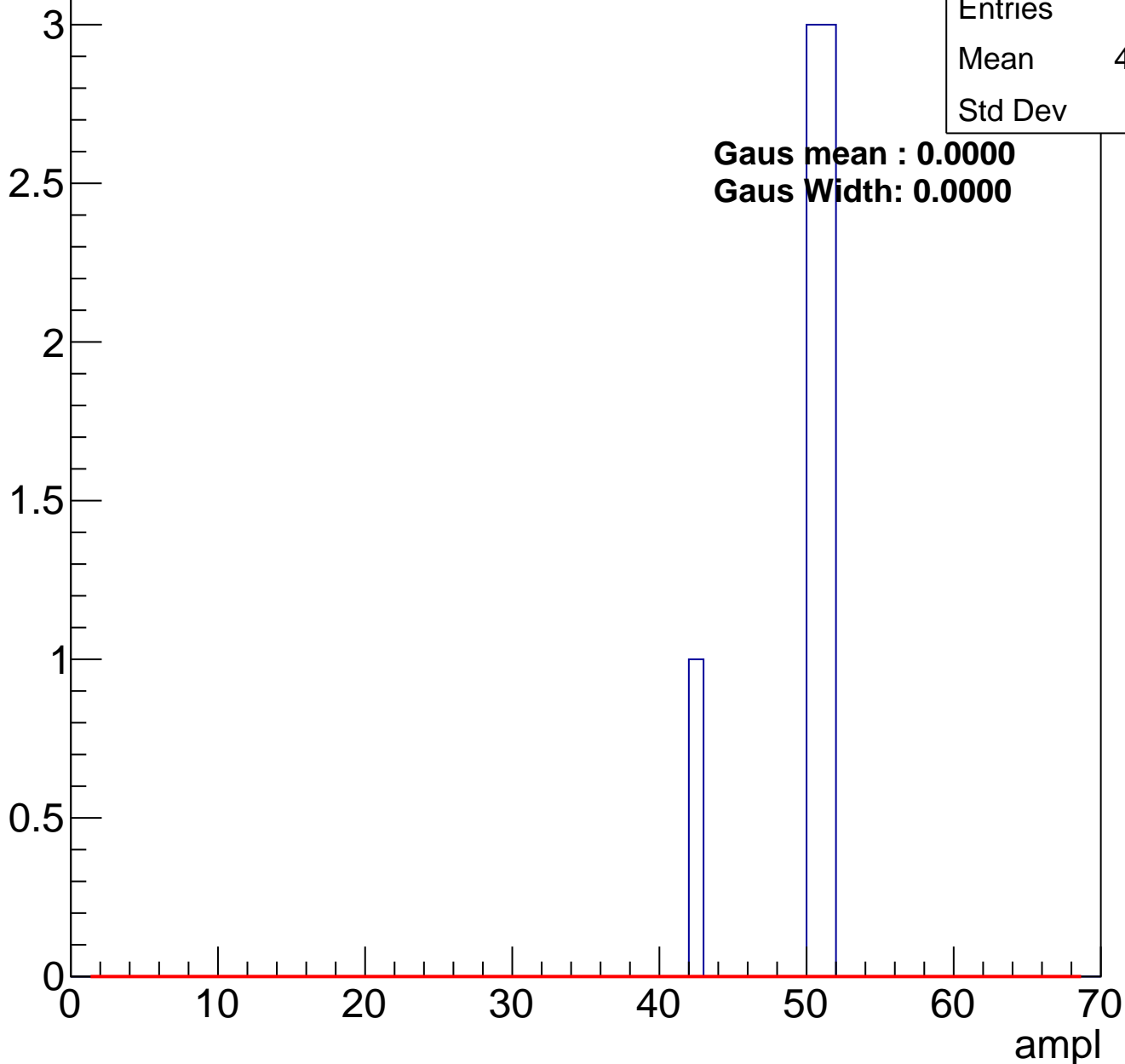
| | |
|---|---|
| Entries | 7 |
| Mean | 49.29 |
| Std Dev | 3.01 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch84, adc3

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 3 |
| Mean | 51.67 |
| Std Dev | 0.4714 |

ampl
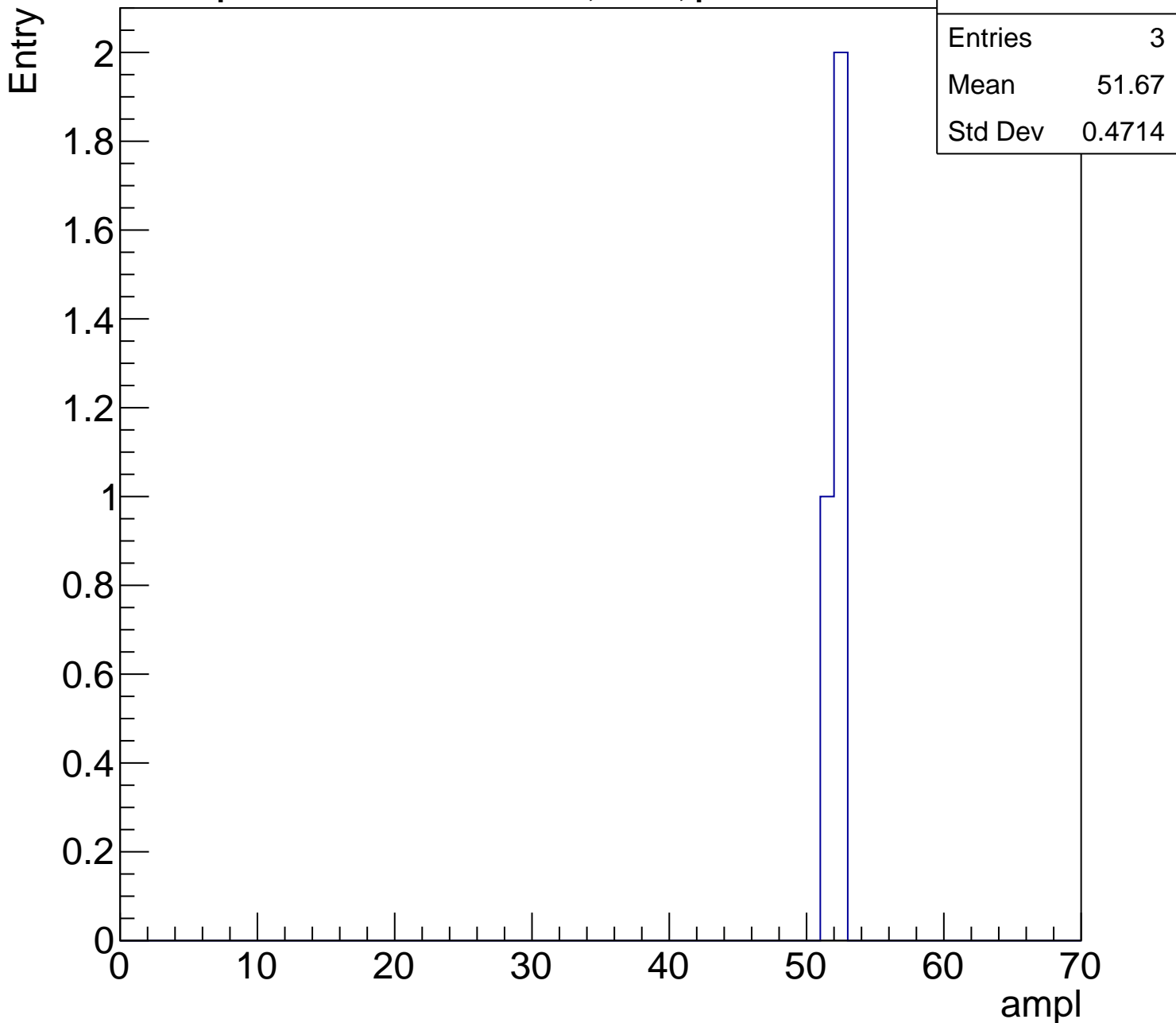
# B0L100S, U1-ch84, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch84, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch84, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch84, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch85, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch85, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 7 |
| Mean | 48.14 |
| Std Dev | 2.587 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
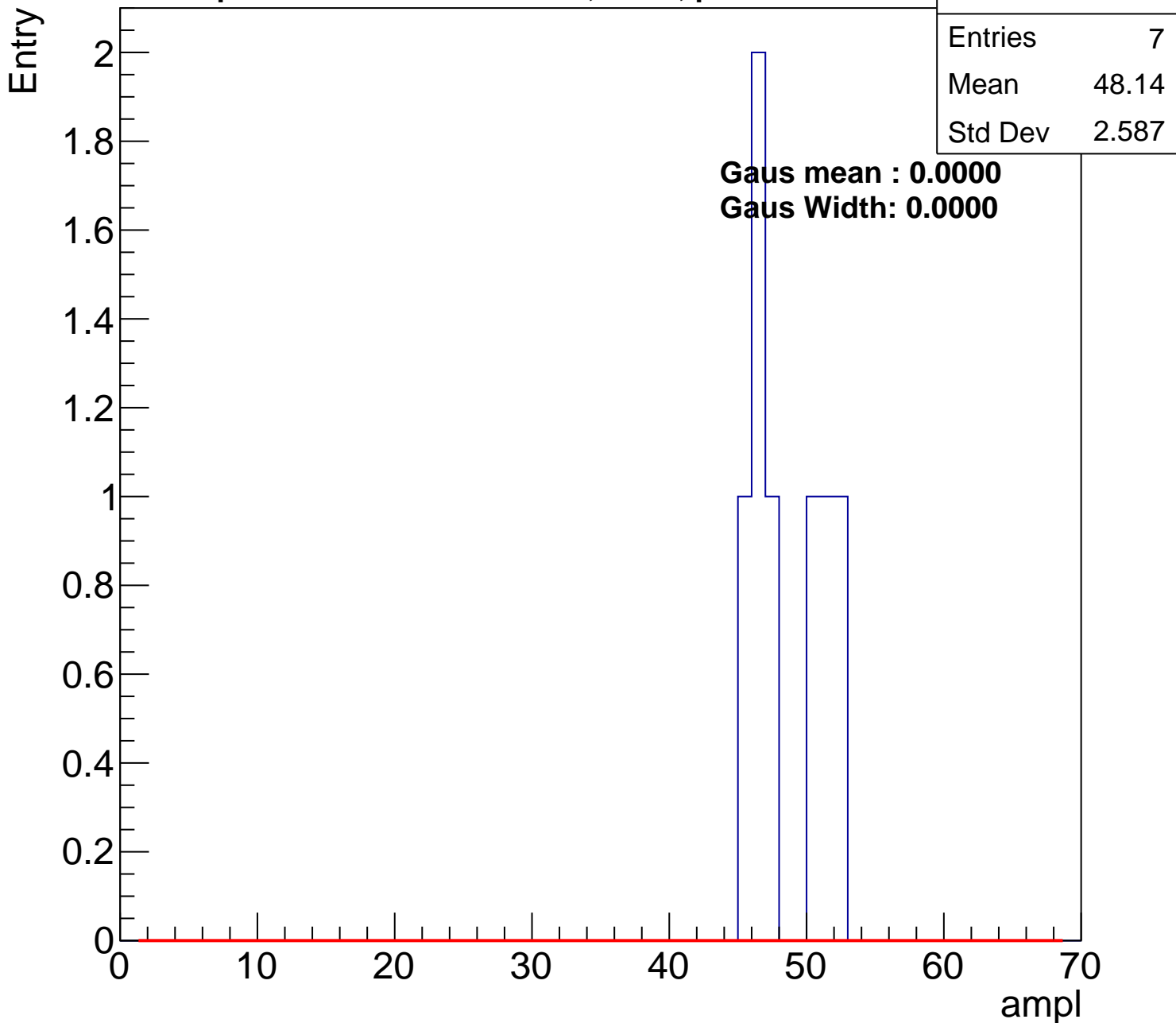
Entry

ampl

# B0L100S, U1-ch85, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 22 |
|---------|-----|
| Mean | 48.82 |
| Std Dev | 1.898 |

**Gaus mean : 48.7060**
**Gaus Width: 2.0906**
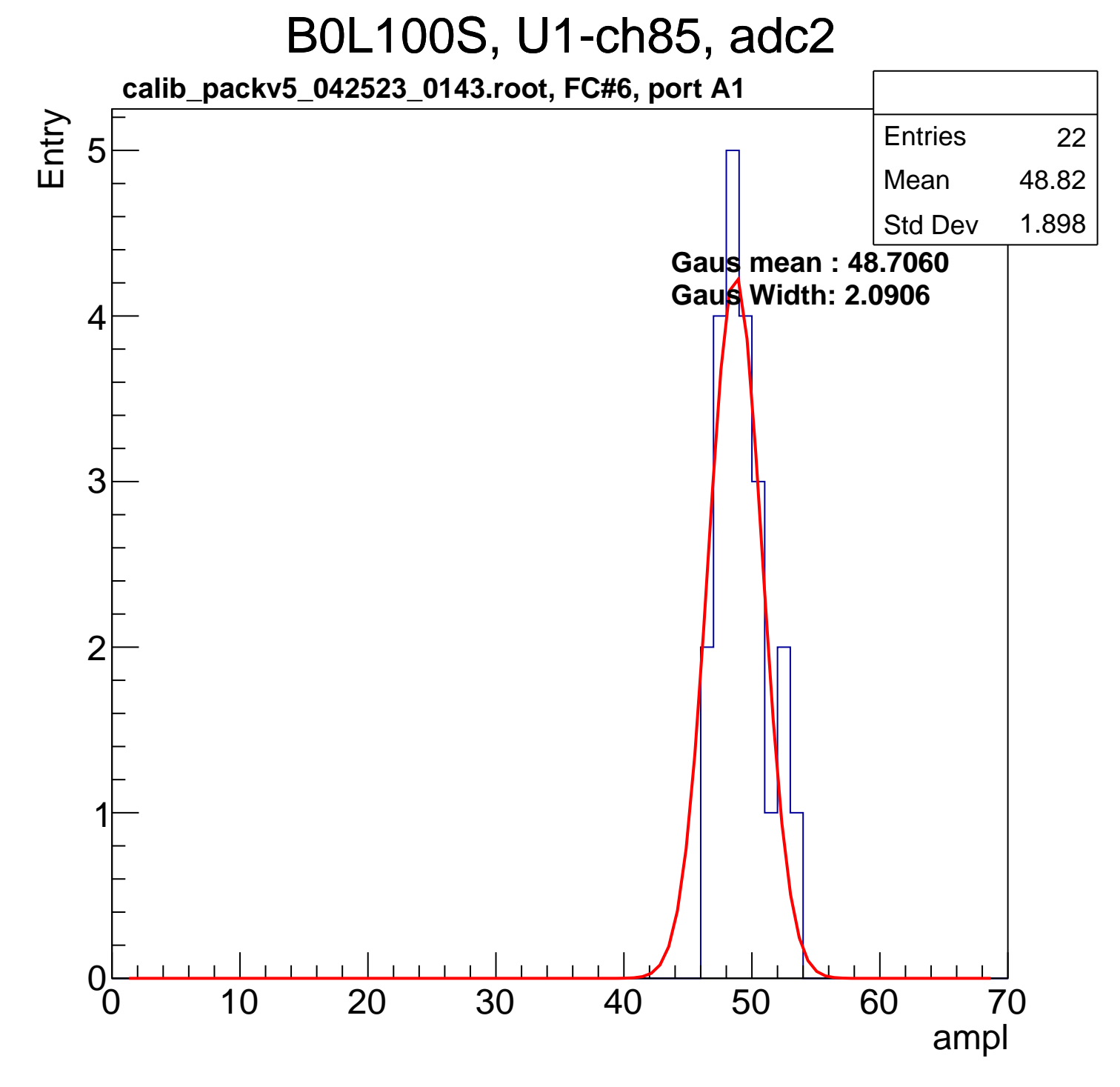
# B0L100S, U1-ch85, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 5 |
| Mean | 50.6 |
| Std Dev | 1.02 |
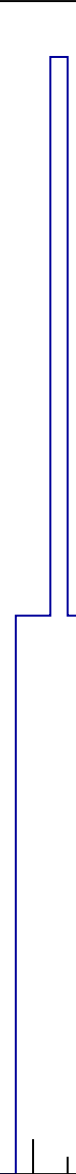
# B0L100S, U1-ch85, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 2 |
| Mean | 53 |
| Std Dev | 0 |

ampl
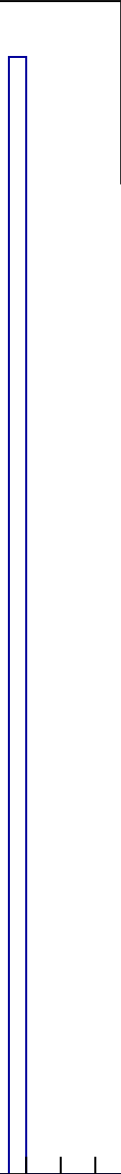
Entry

# B0L100S, U1-ch85, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch85, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch85, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch86, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch86, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 52 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
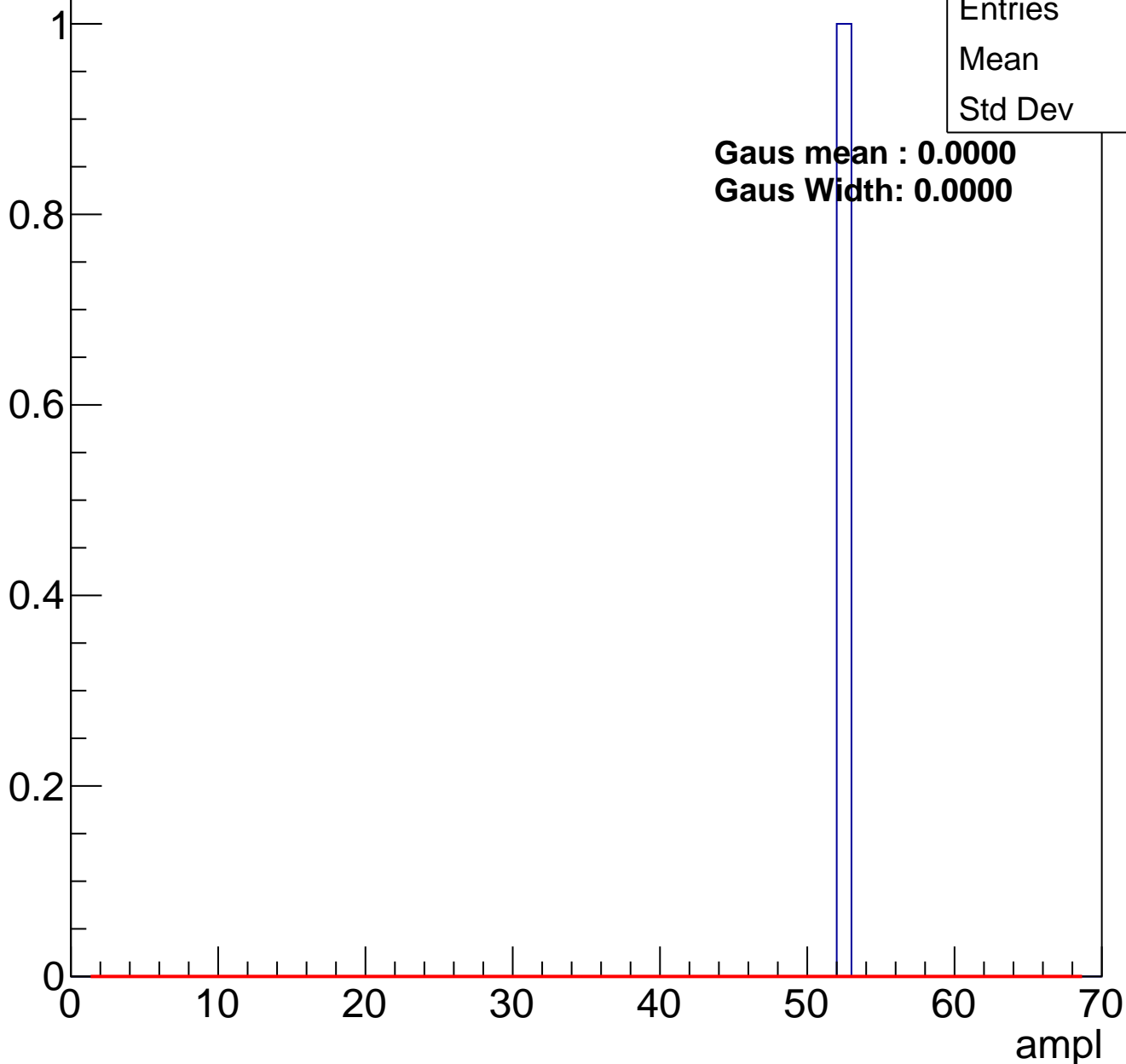
# B0L100S, U1-ch86, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 4 |
| Mean | 50.5 |
| Std Dev | 2.291 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
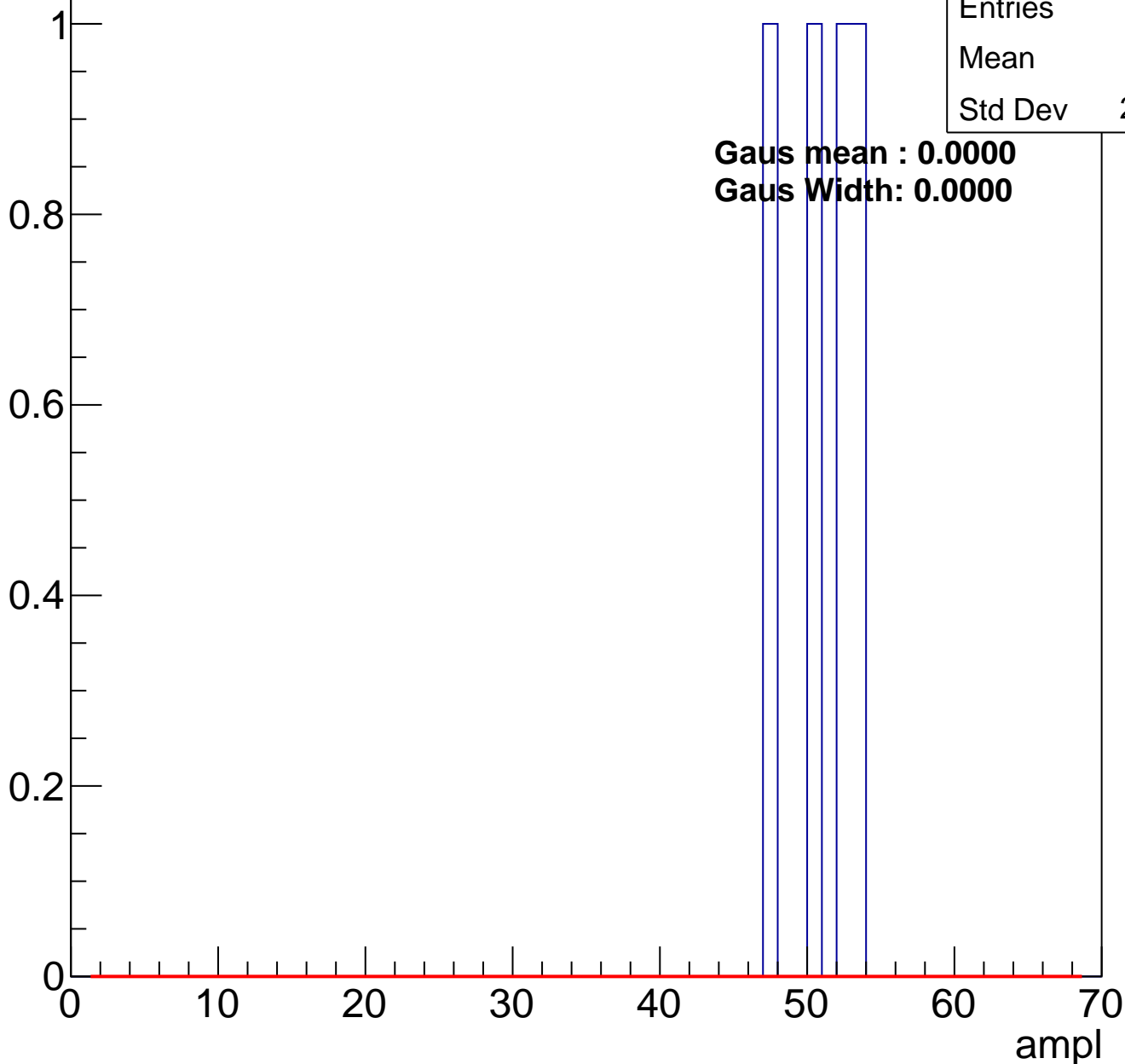
# B0L100S, U1-ch86, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 5 |
| Mean | 52.4 |
| Std Dev | 1.2 |

Entry

ampl
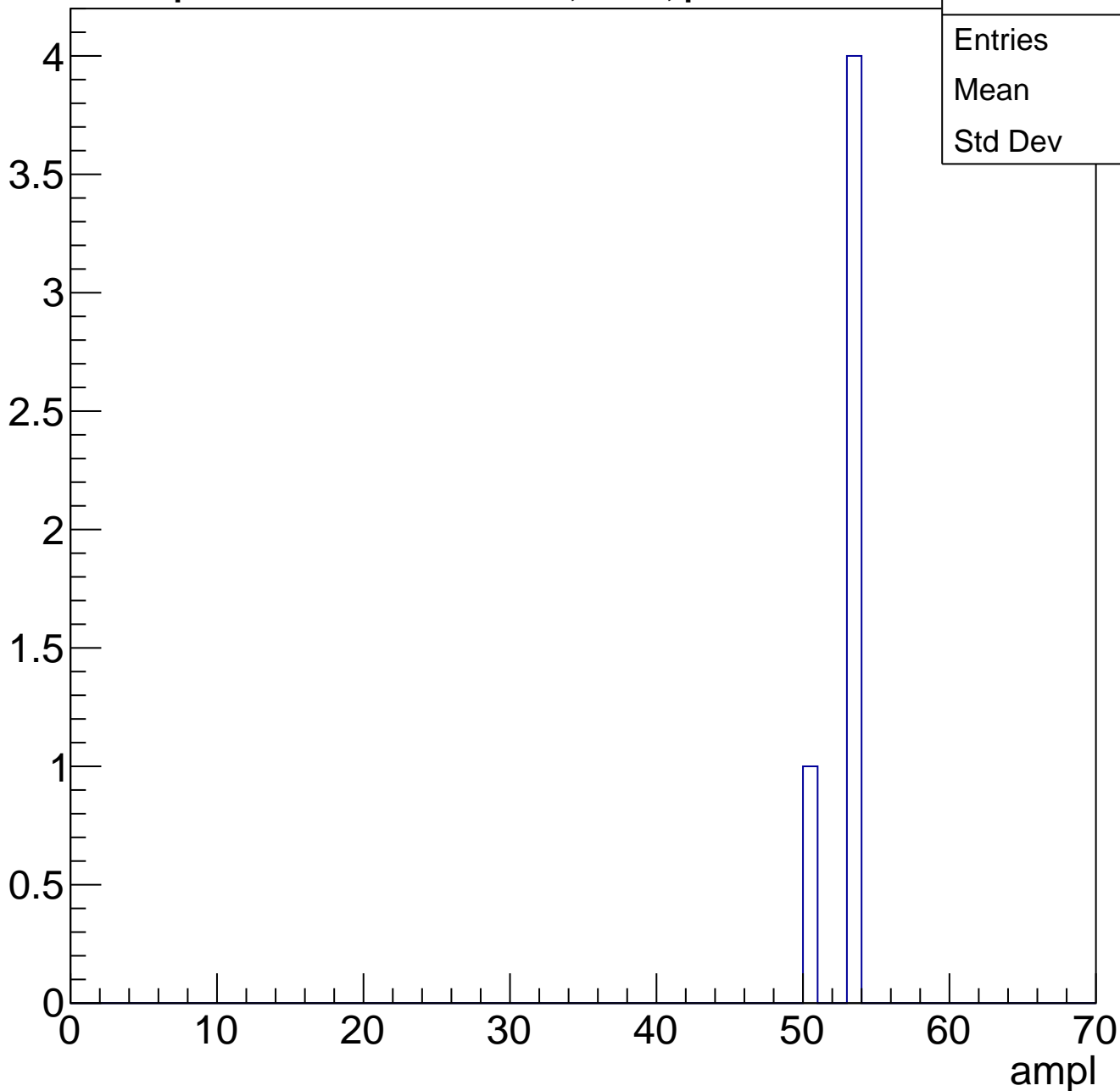
B0L100S, U1-ch86, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch86, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
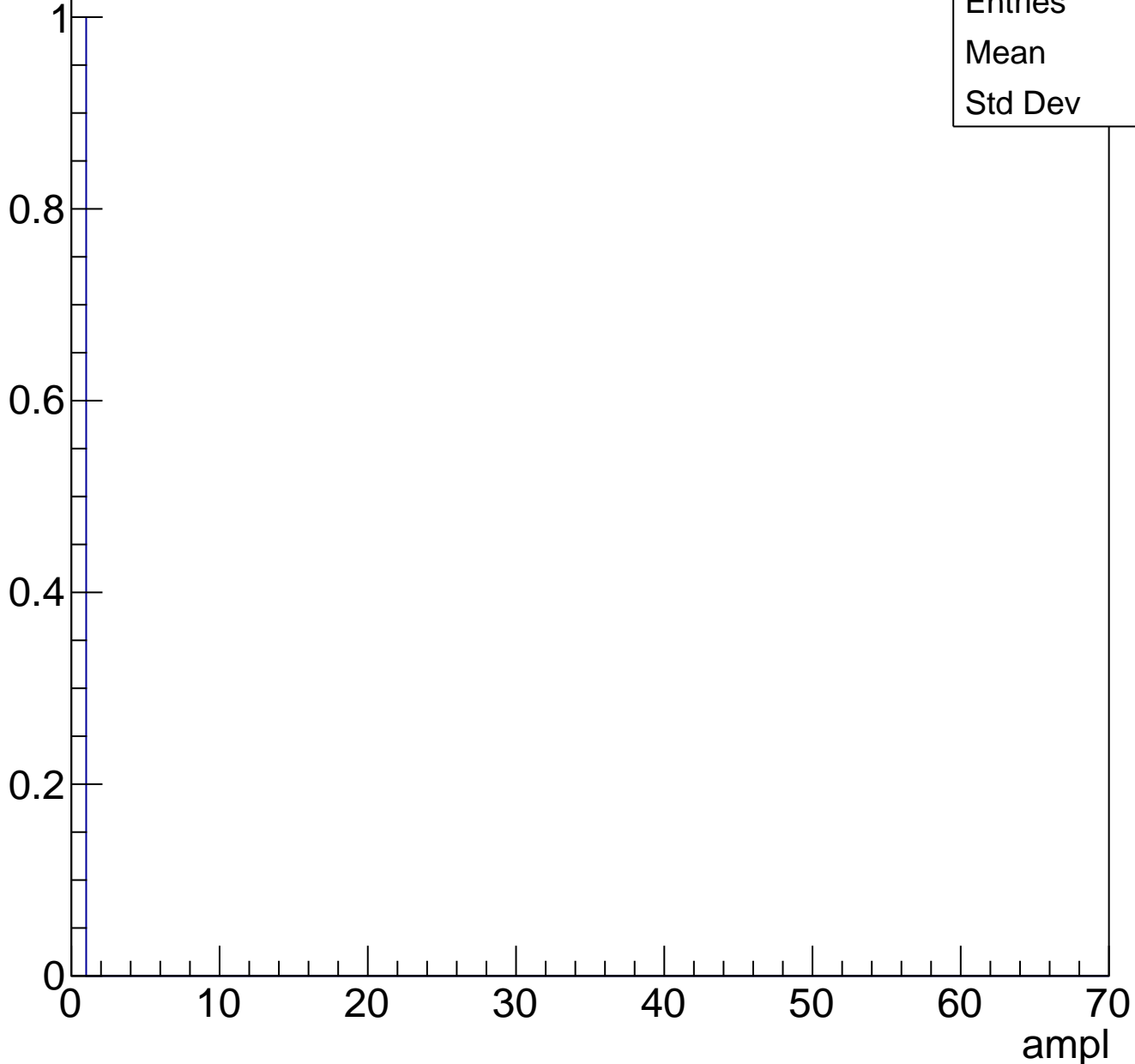
# B0L100S, U1-ch86, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch86, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch87, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

# B0L100S, U1-ch87, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch87, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch87, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch87, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch87, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
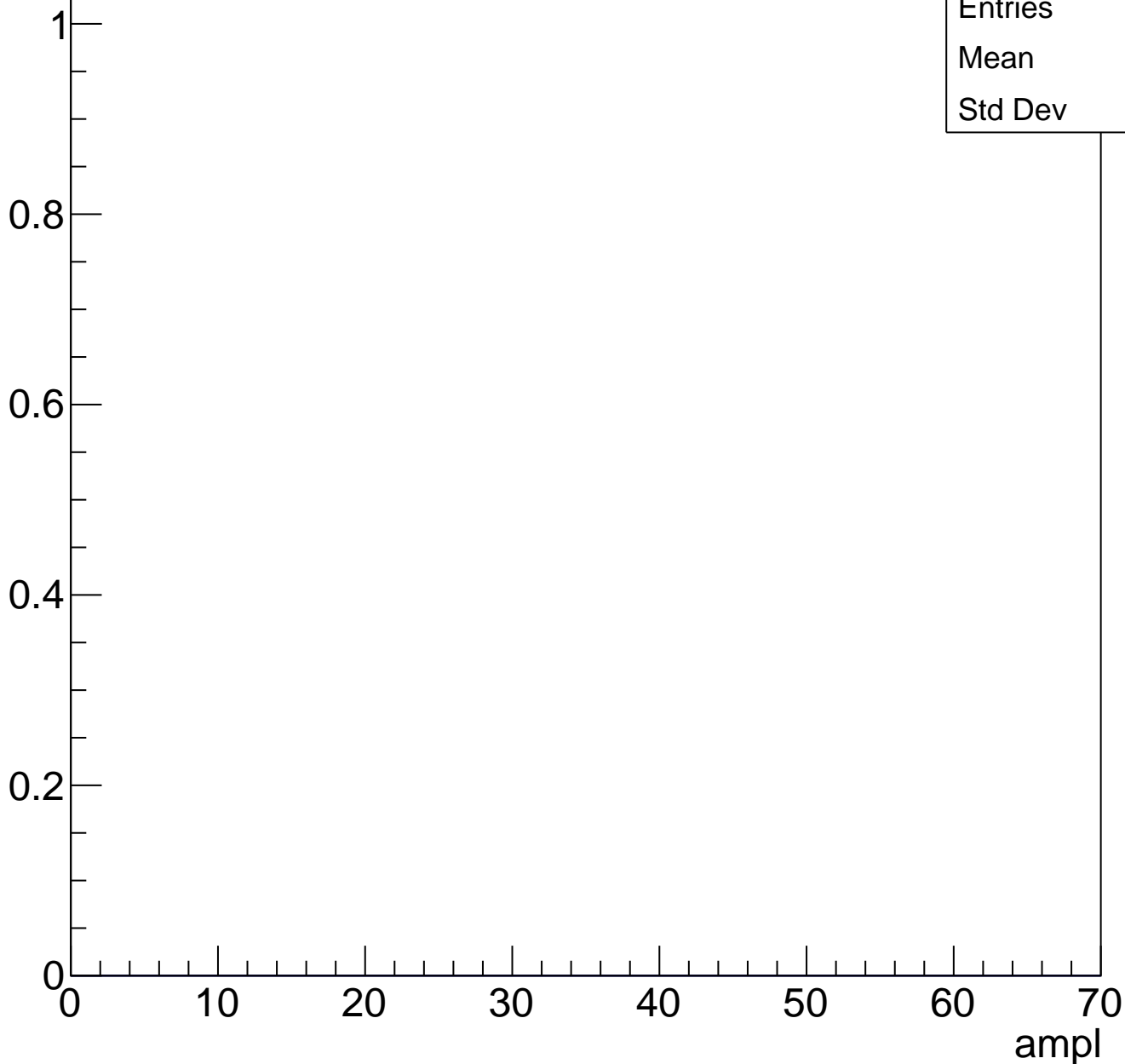
# B0L100S, U1-ch87, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch88, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 2 |
|---|---|
| Mean | 32 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
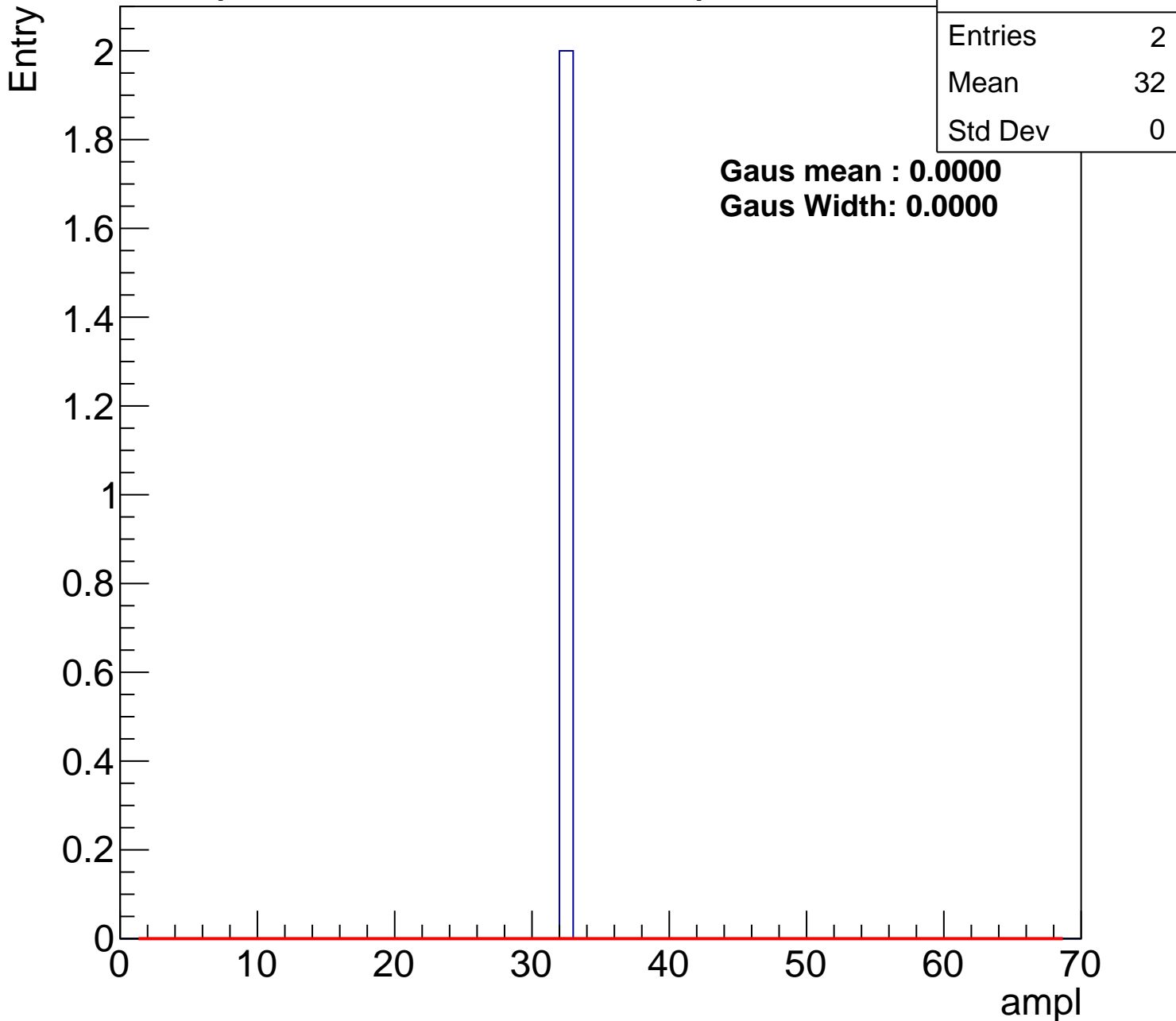
# B0L100S, U1-ch88, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**
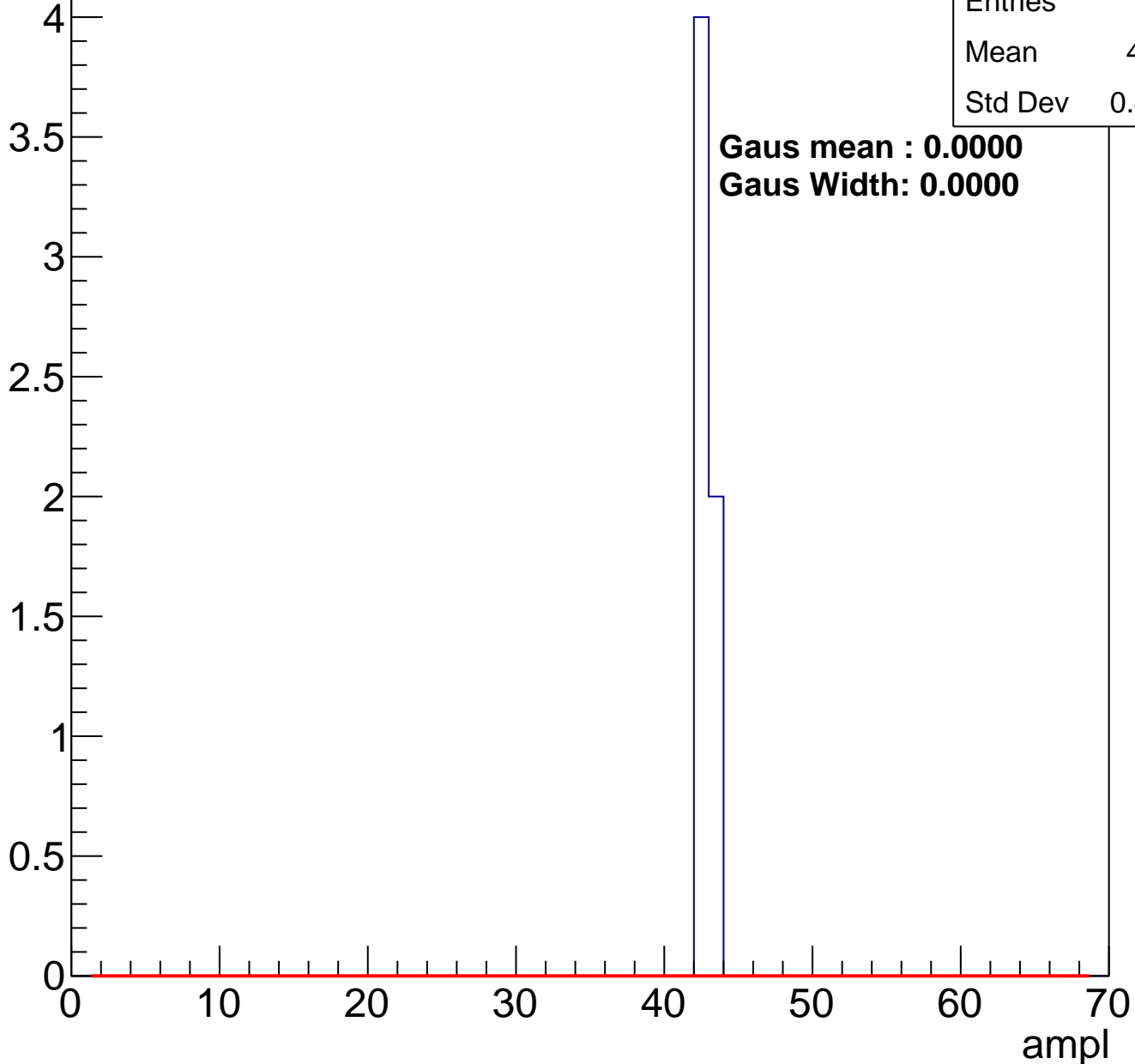
| | |
|---|---|
| Entries | 6 |
| Mean | 42.33 |
| Std Dev | 0.4714 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch88, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 8 |
|---------|-----|
| Mean | 47.88 |
| Std Dev | 4.106 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
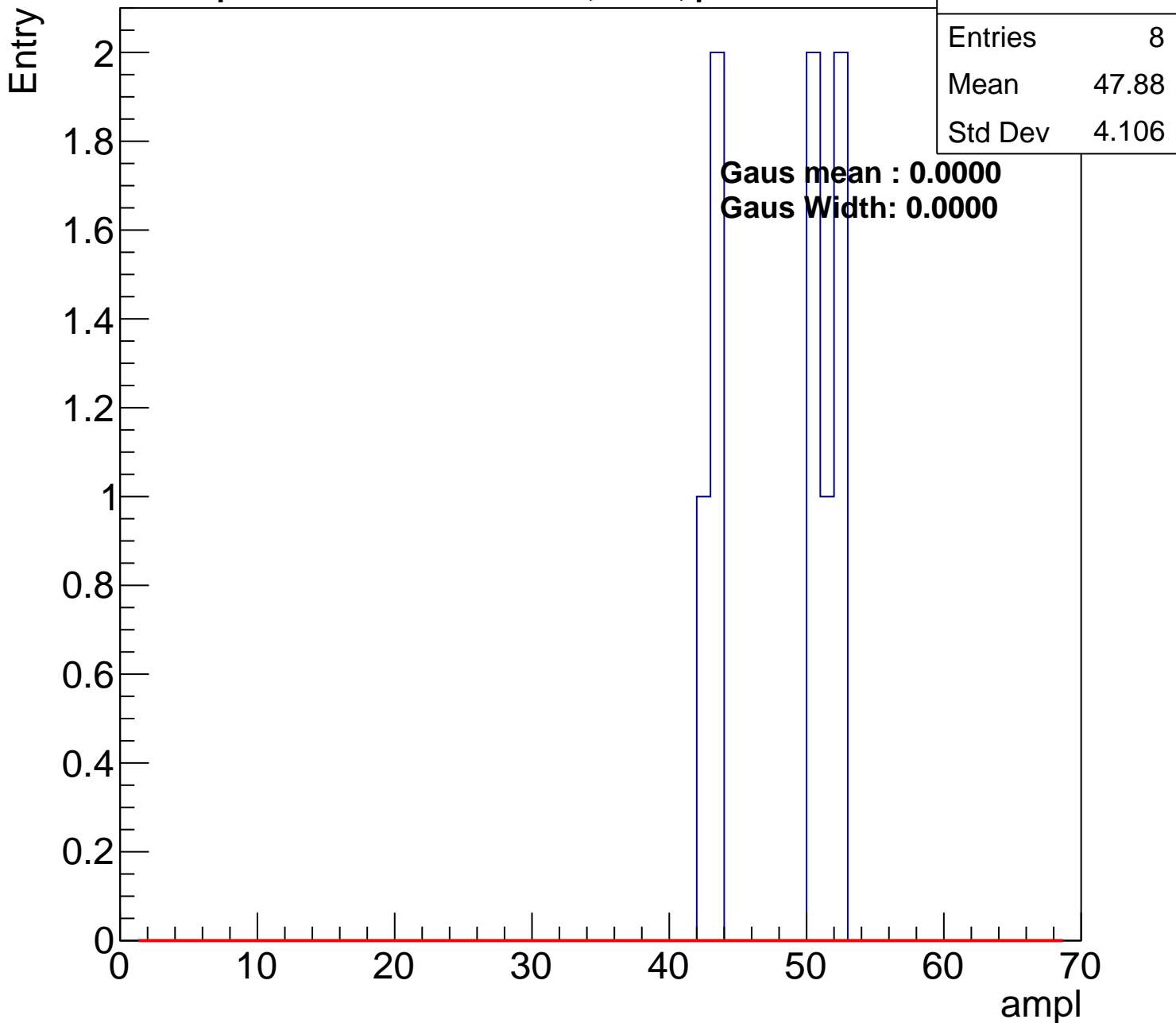
# B0L100S, U1-ch88, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 7 |
| --- | --- |
| Mean | 51 |
| Std Dev | 0.7559 |
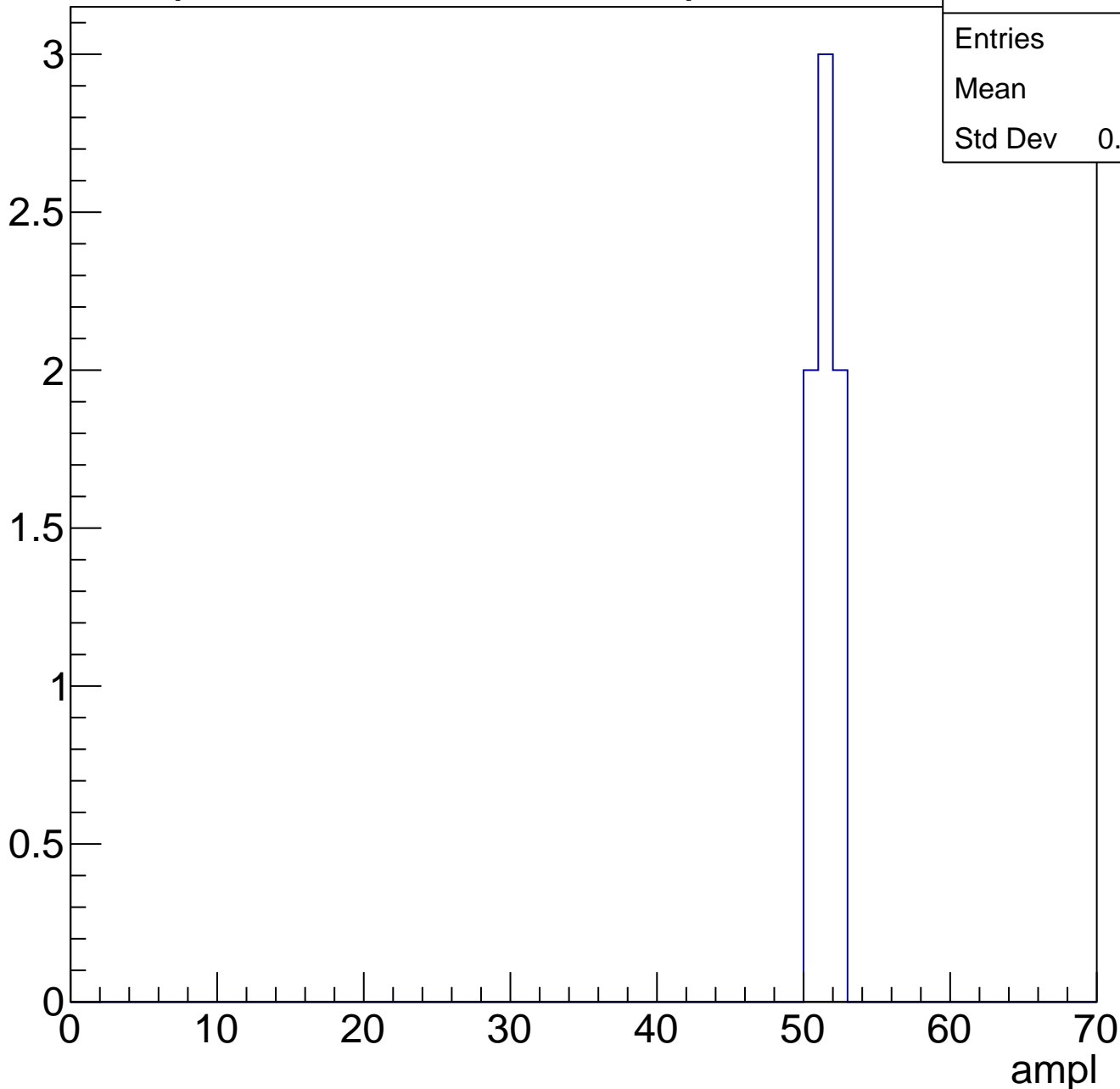
# B0L100S, U1-ch88, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 3 |
| Mean | 37.33 |
| Std Dev | 9.672 |
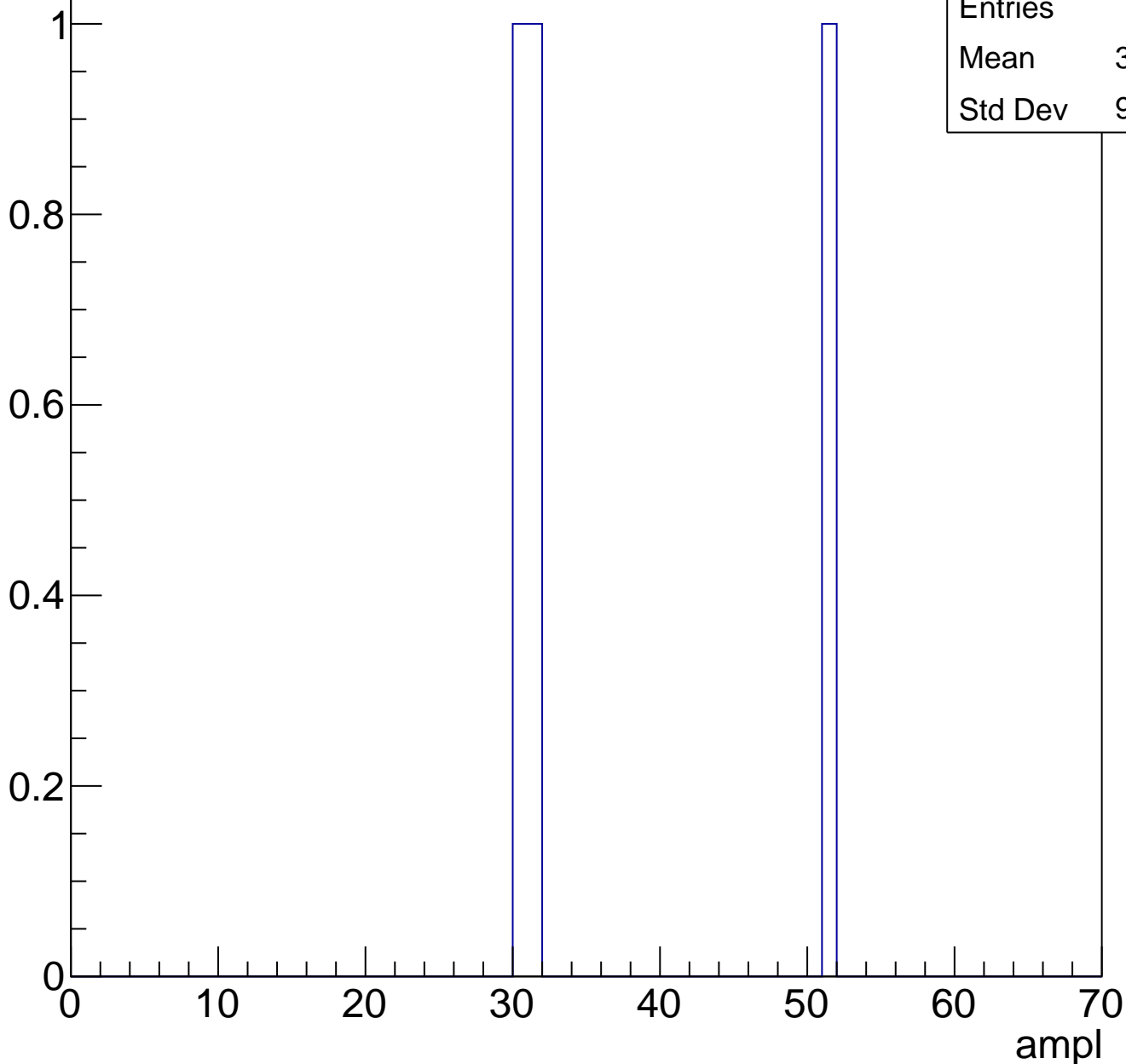
# B0L100S, U1-ch88, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch88, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch88, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch89, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch89, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

# B0L100S, U1-ch89, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch89, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch89, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L100S, U1-ch89, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch89, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch90, adc0

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U1-ch90, adc1

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 13 |
| Mean | 46.23 |
| Std Dev | 2.189 |

**Gaus mean : 47.1654**
**Gaus Width: 2.3192**

Entry

ampl
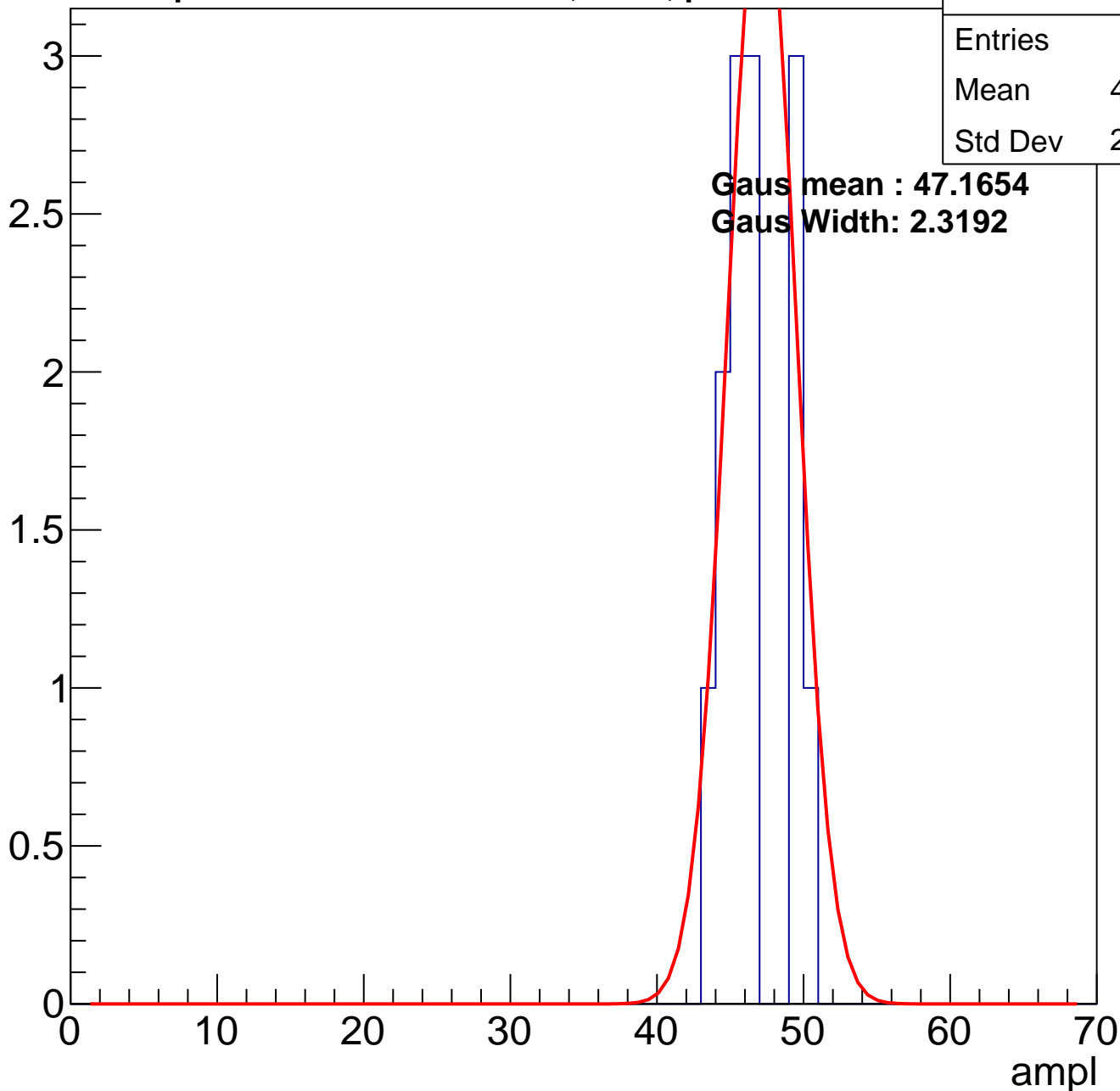
# B0L100S, U1-ch90, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 20 |
| Mean | 48 |
| Std Dev | 3.209 |

**Gaus mean : 101.2282**
**Gaus Width: 32.0935**

Entry

ampl
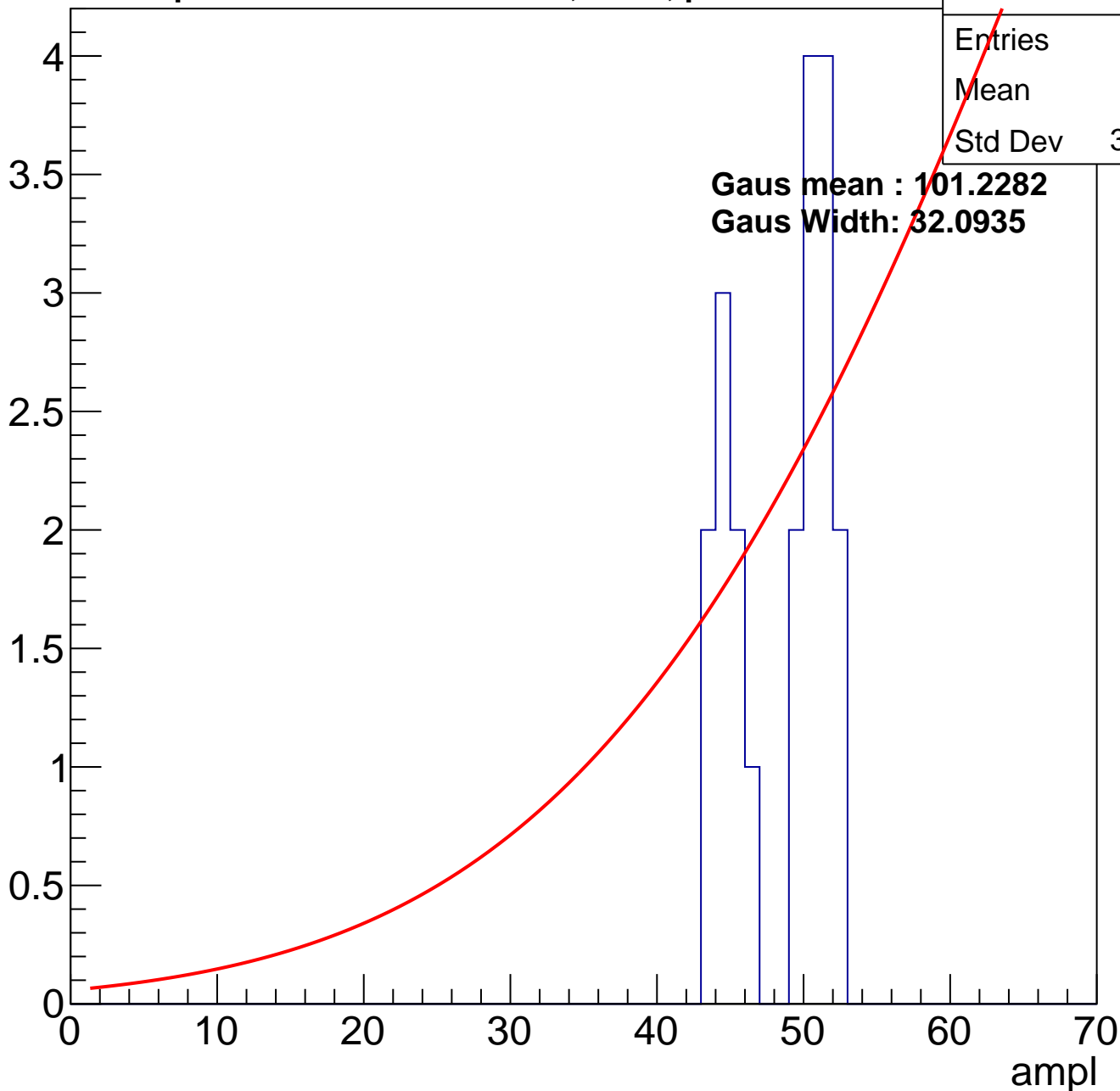
# B0L100S, U1-ch90, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 51 |
| Std Dev | 0 |

ampl
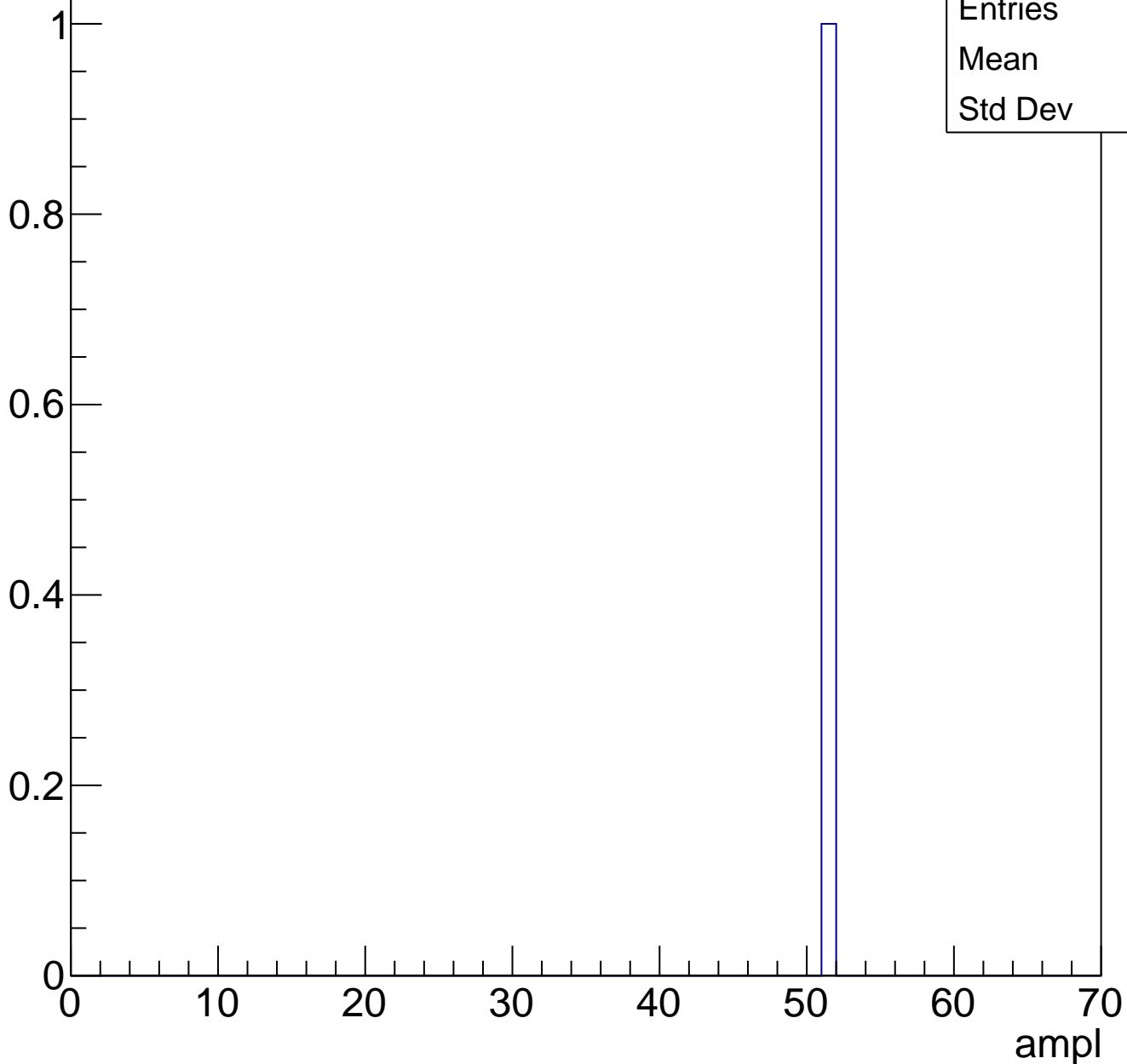
# B0L100S, U1-ch90, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch90, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch90, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch90, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch91, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch91, adc1

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 1 |
| Mean | 52 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000
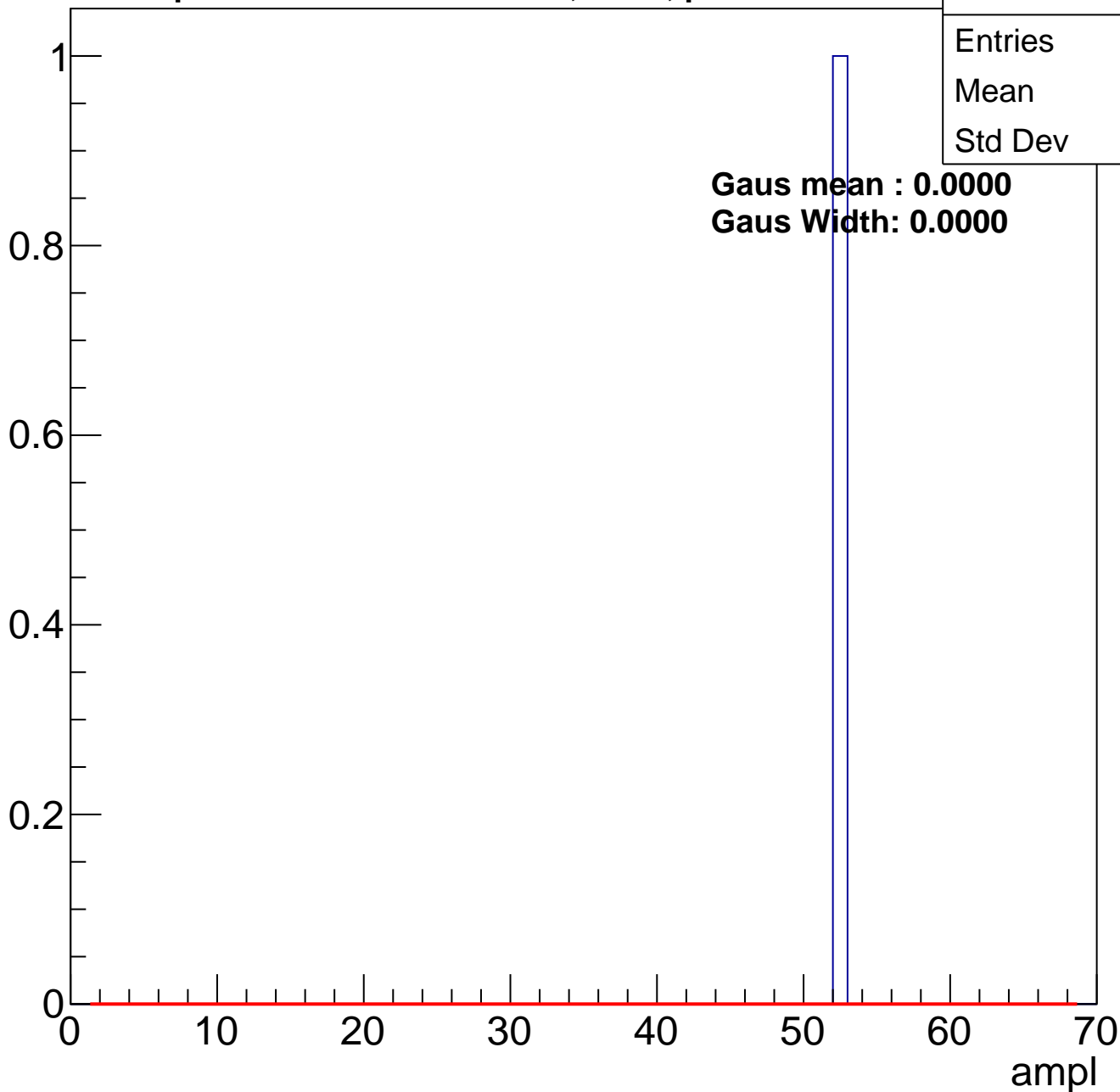
# B0L100S, U1-ch91, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 9 |
| Mean | 52.22 |
| Std Dev | 2.439 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**
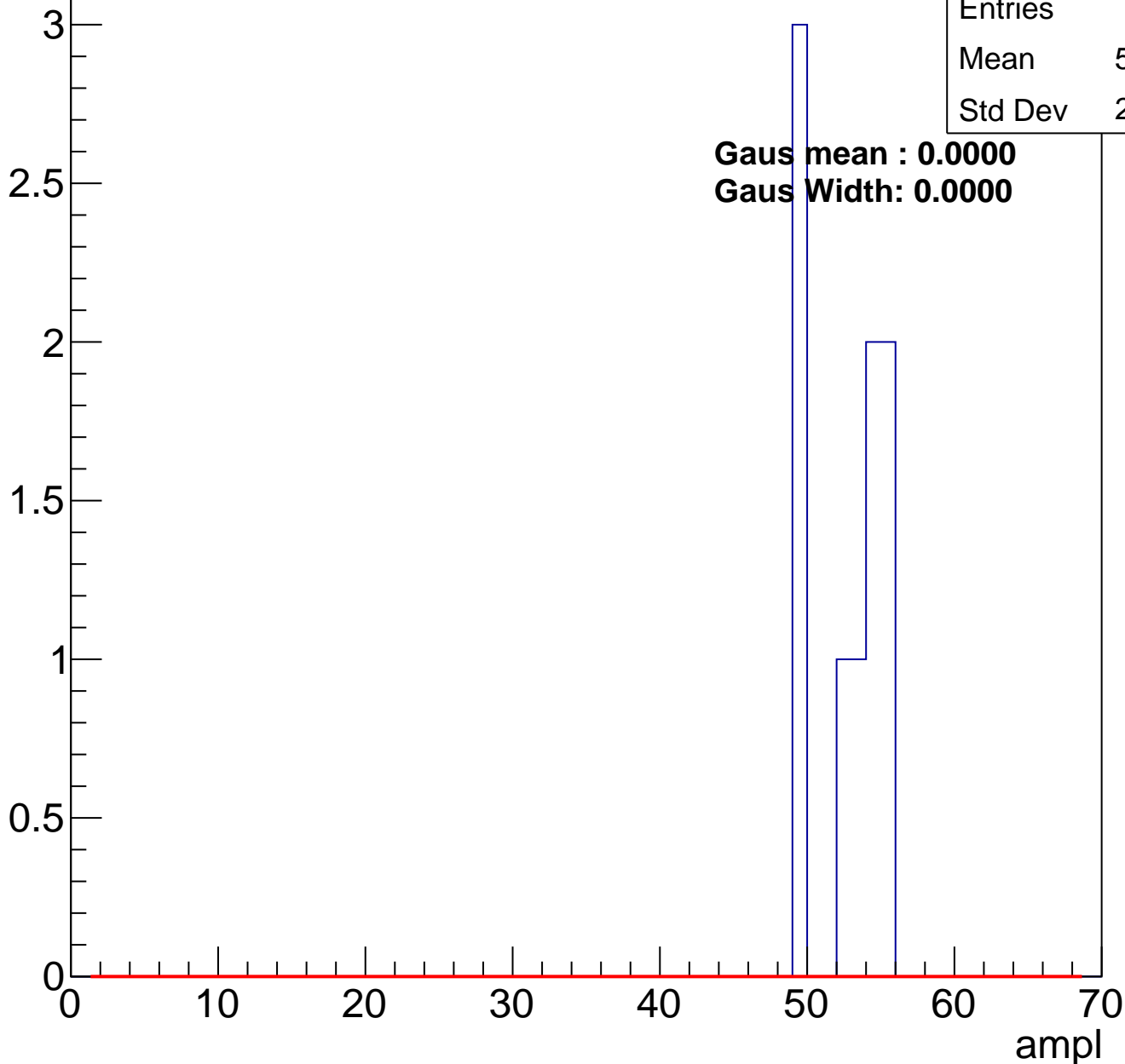
# B0L100S, U1-ch91, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 9 |
| Mean | 52.67 |
| Std Dev | 1.944 |

Entry

ampl

# B0L100S, U1-ch91, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch91, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch91, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch91, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch92, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch92, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch92, adc2

calib_packv5_042523_0143.root, FC#6, port A1
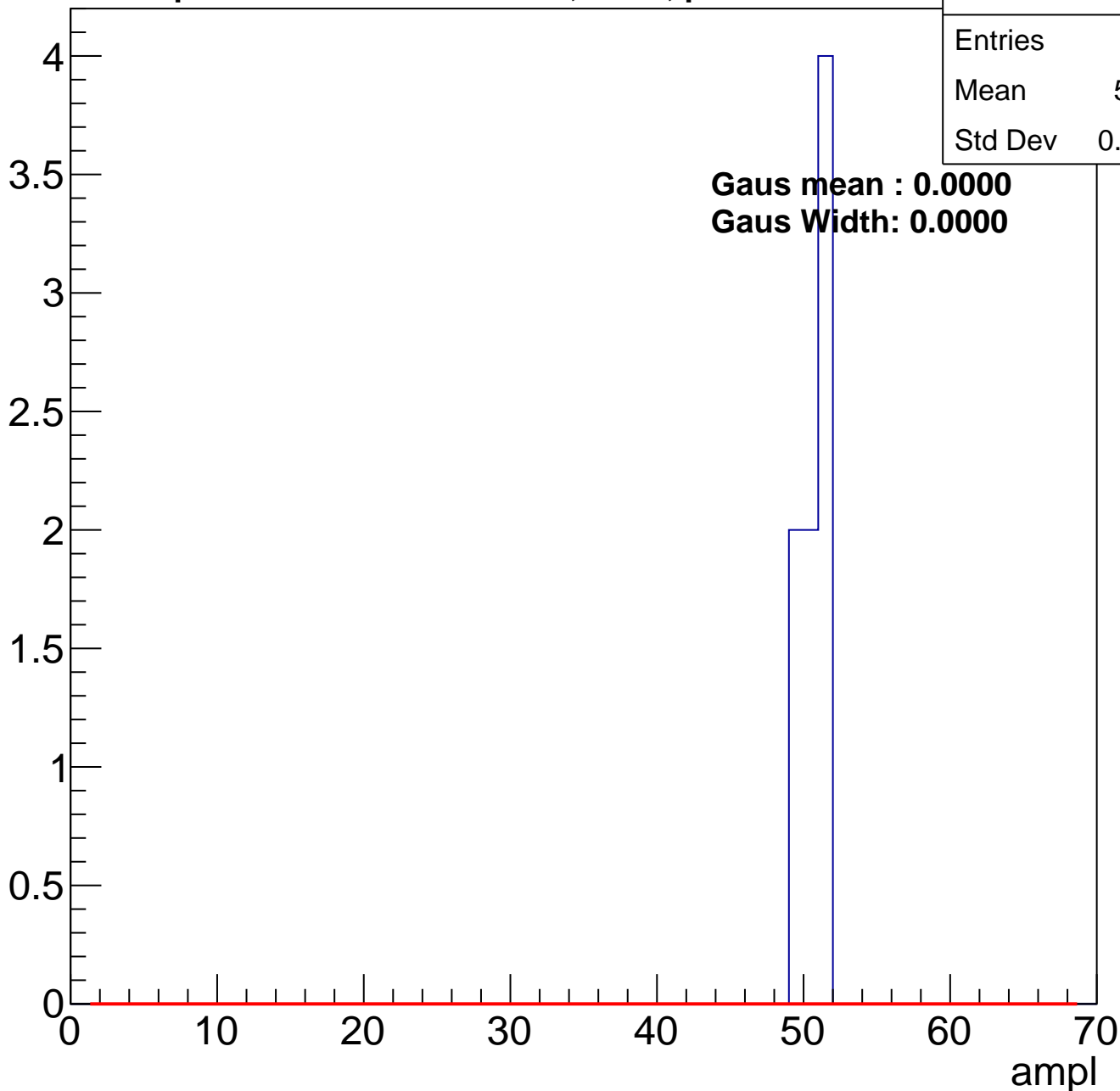
| Entries | 8 |
| Mean | 50.25 |
| Std Dev | 0.8292 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

# B0L100S, U1-ch92, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 5 |
| Mean | 51 |
| Std Dev | 1.549 |
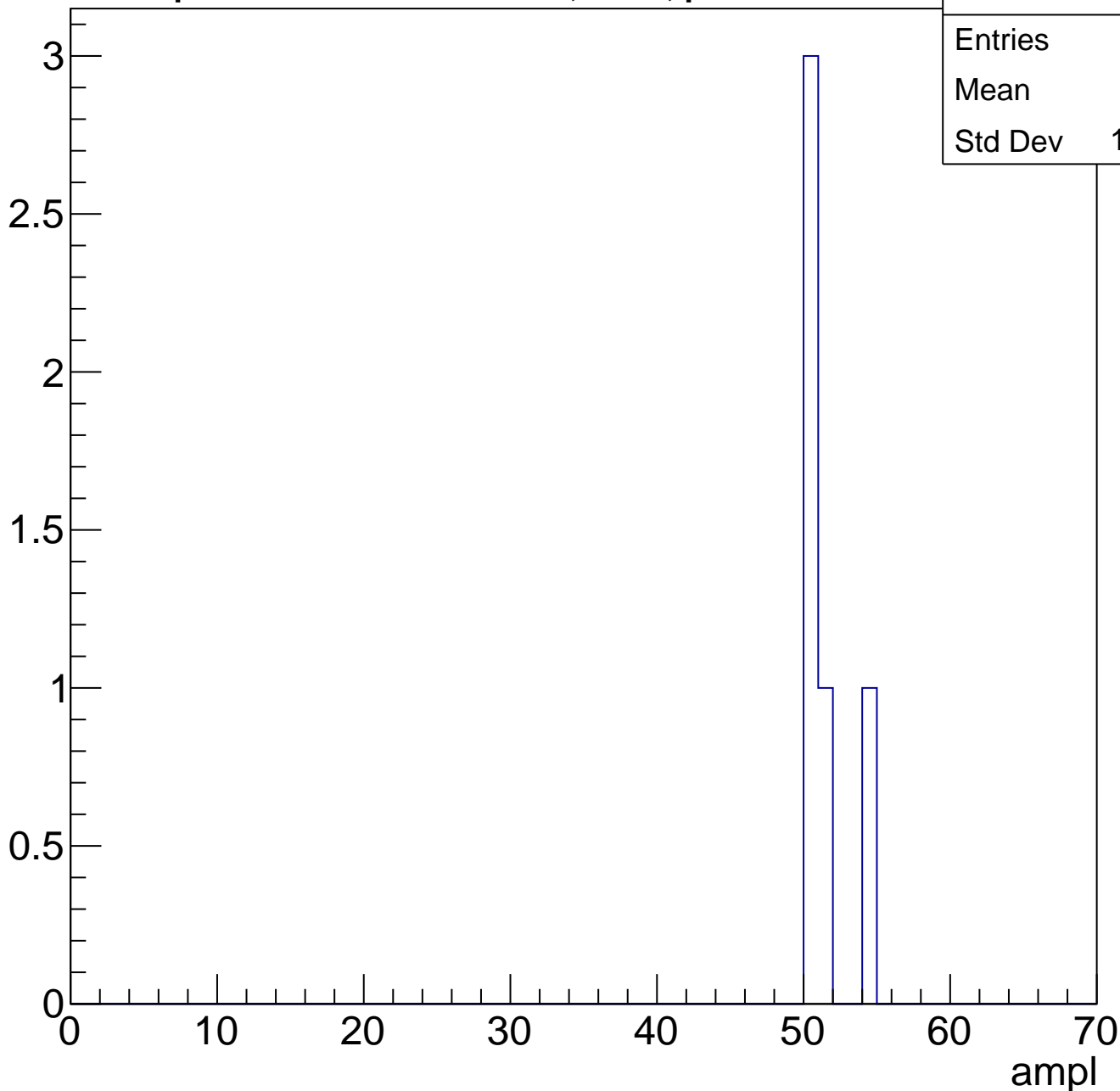
# B0L100S, U1-ch92, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch92, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch92, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch92, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch93, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch93, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch93, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
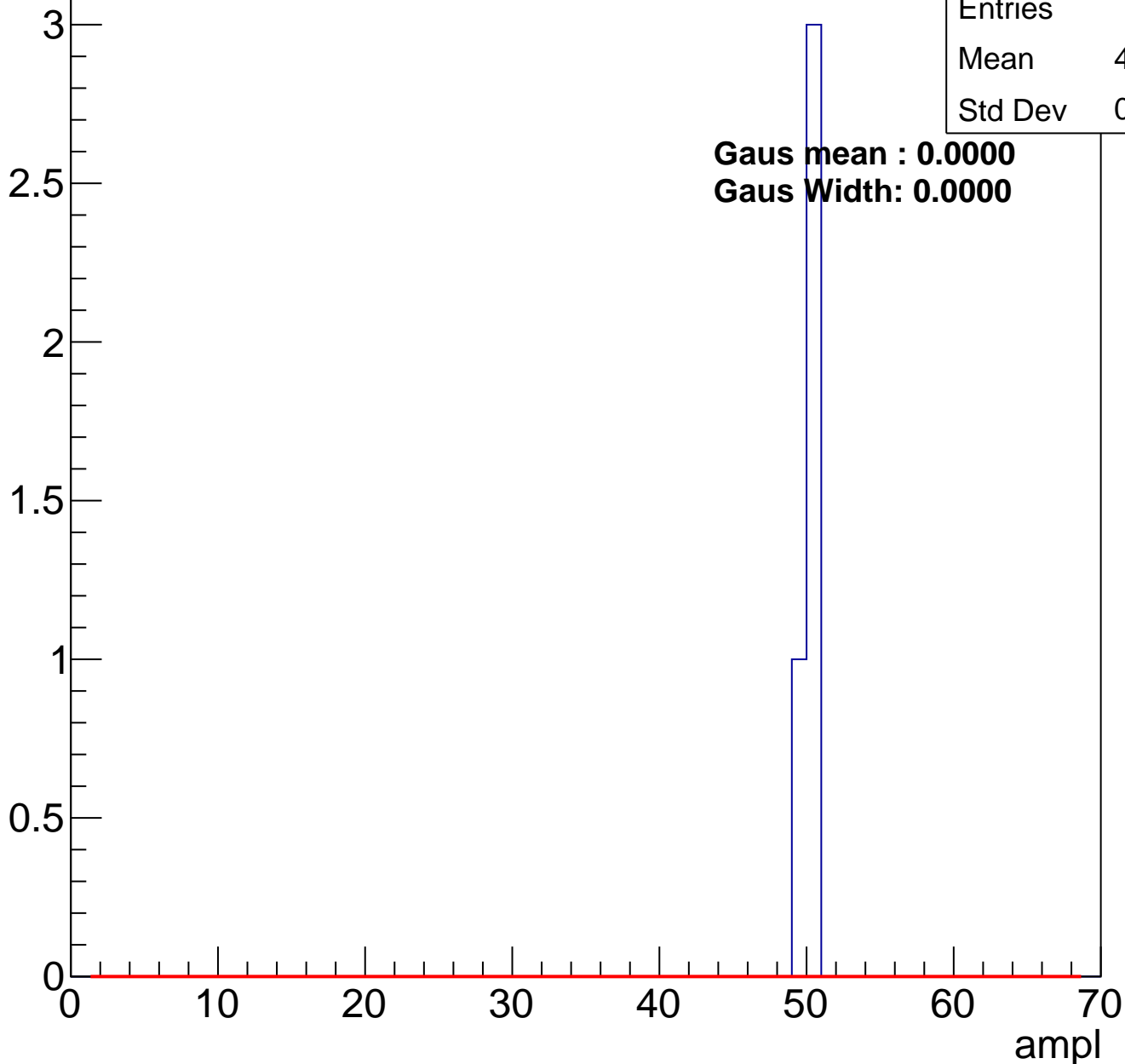
| Entries | 4 |
|---------|------|
| Mean | 49.75 |
| Std Dev | 0.433 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch93, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
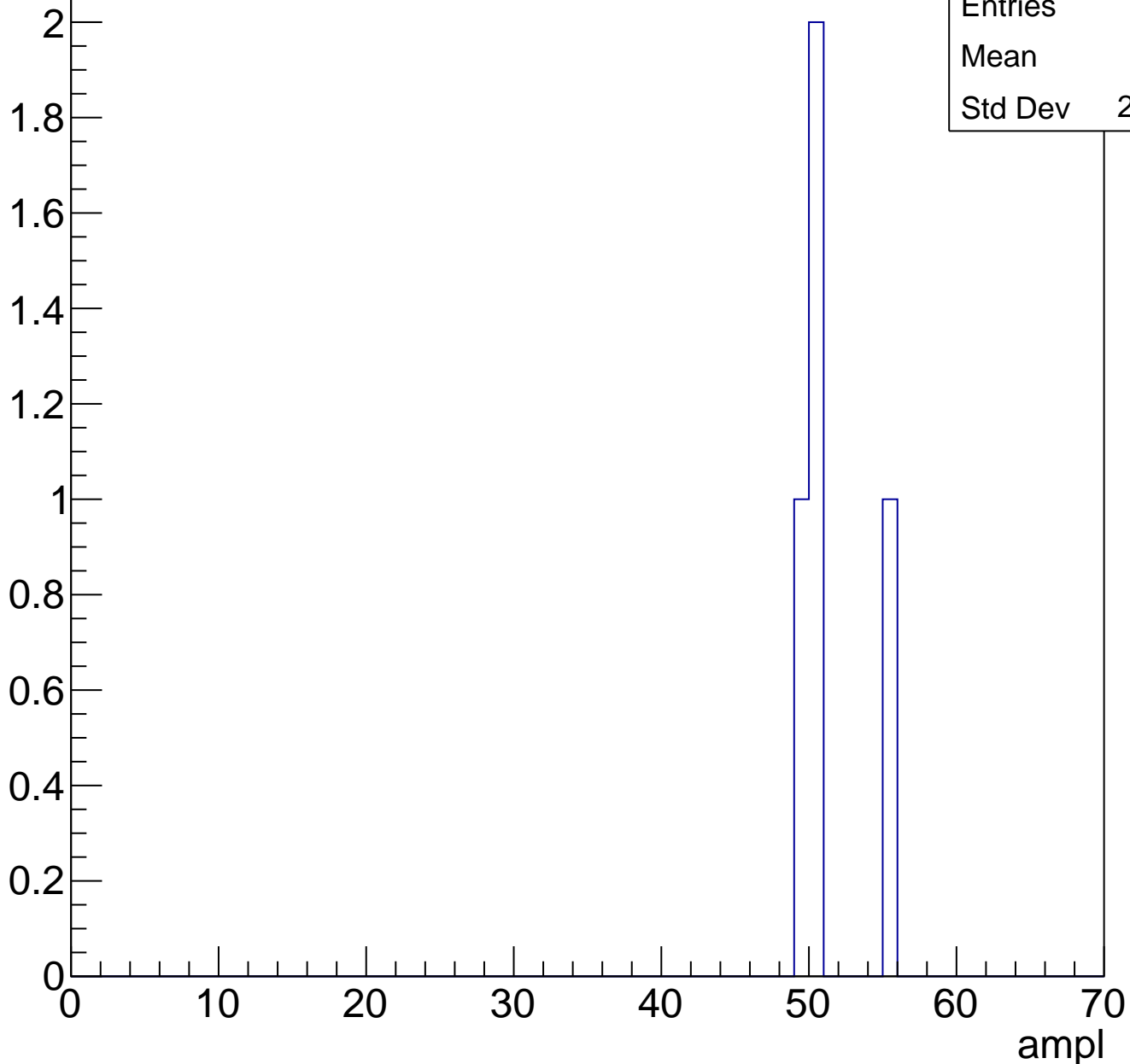


| Entries | 4 |
| Mean | 51 |
| Std Dev | 2.345 |

B0L100S, U1-ch93, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch93, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch93, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch93, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch94, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 43 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch94, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 44 |
| Std Dev | 2.16 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch94, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 6 |
| --- | --- |
| Mean | 46.67 |
| Std Dev | 1.7 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
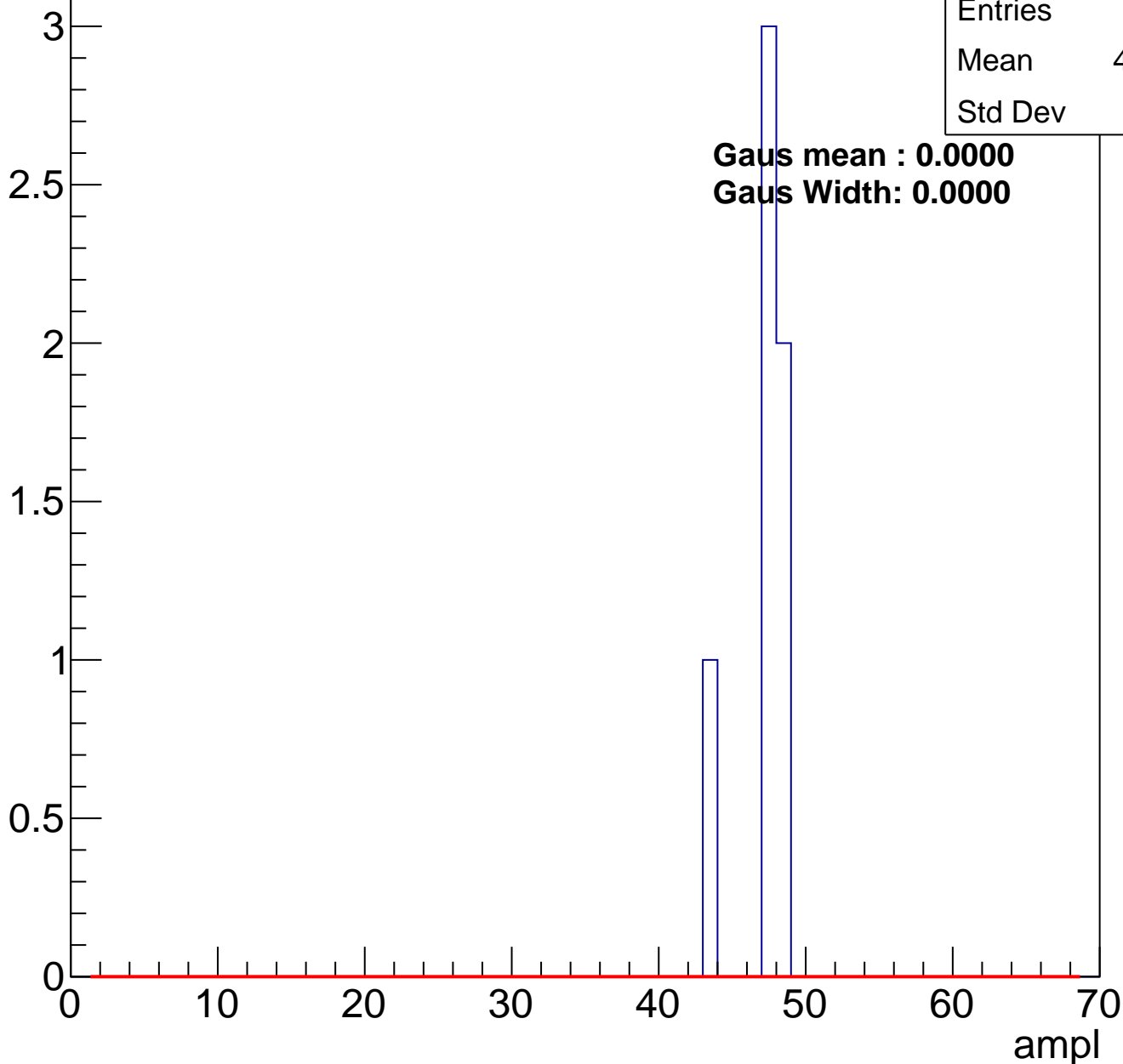
# B0L100S, U1-ch94, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch94, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch94, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch94, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch94, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl
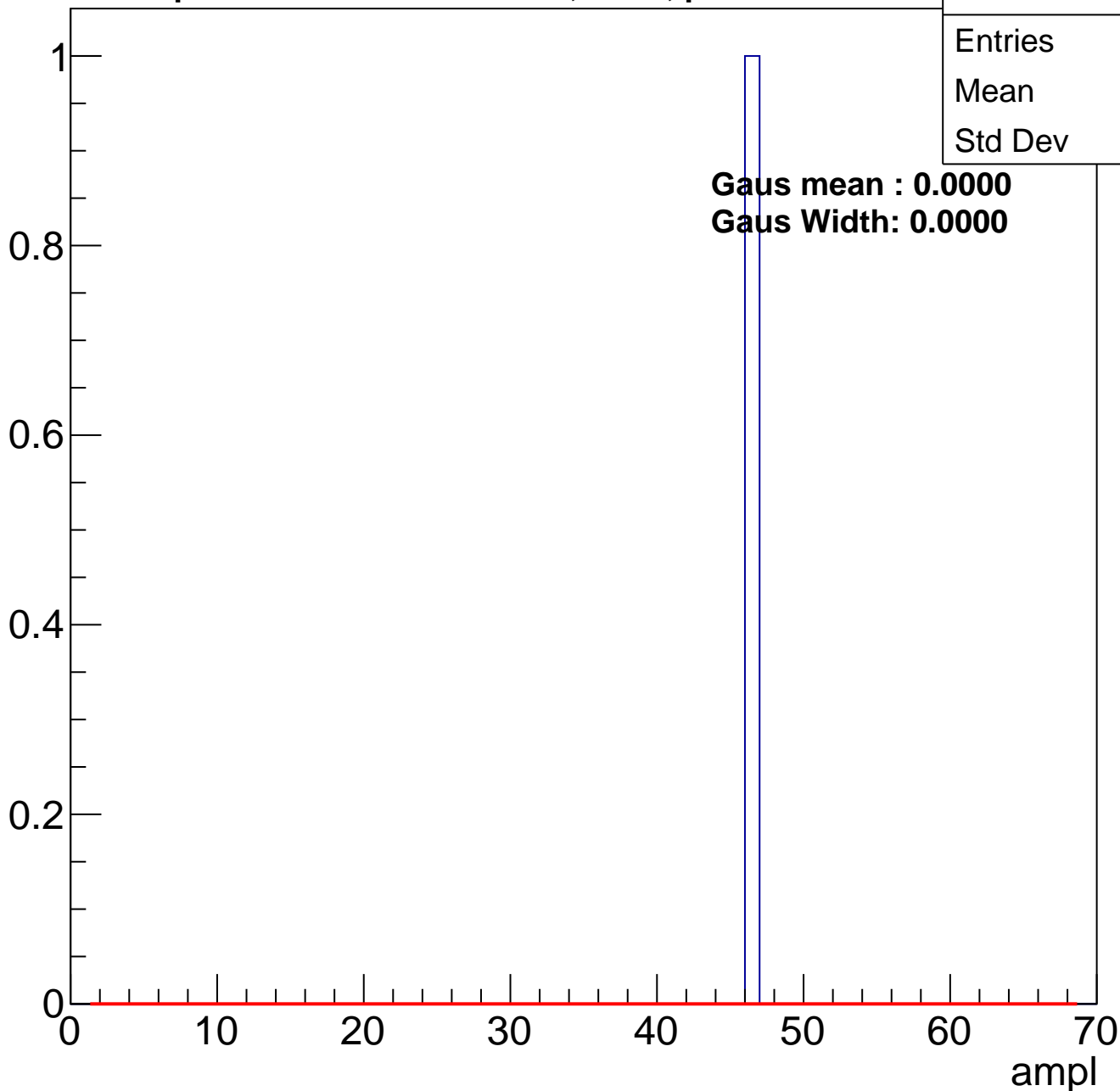
# B0L100S, U1-ch95, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 46 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch95, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 15 |
| Mean | 46.73 |
| Std Dev | 1.769 |

**Gaus mean : -10.4842**
**Gaus Width: 13.5576**
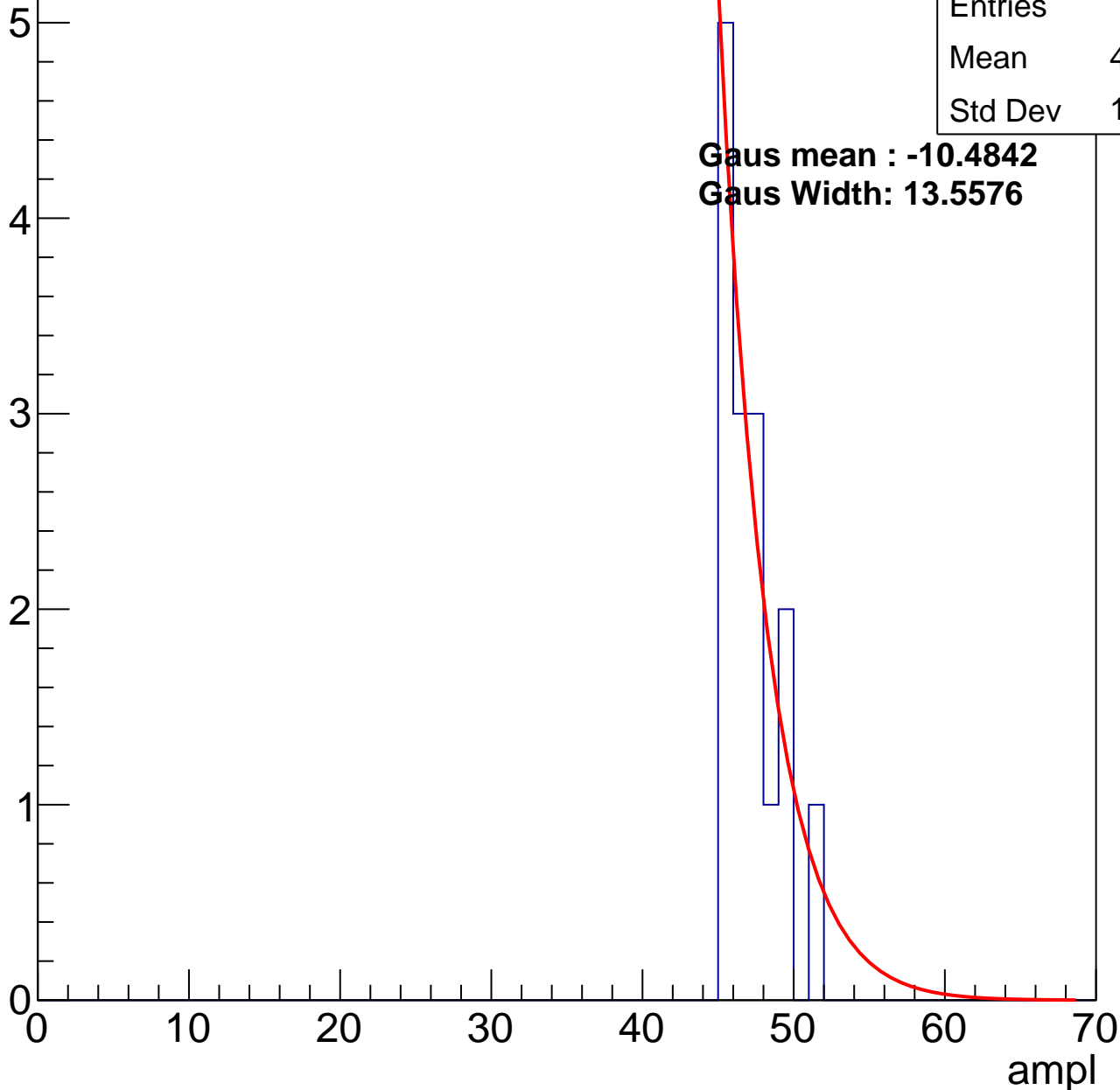
# B0L100S, U1-ch95, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 18 |
| Mean | 49 |
| Std Dev | 2.333 |

**Gaus mean : 50.6353**
**Gaus Width: 3.3498**
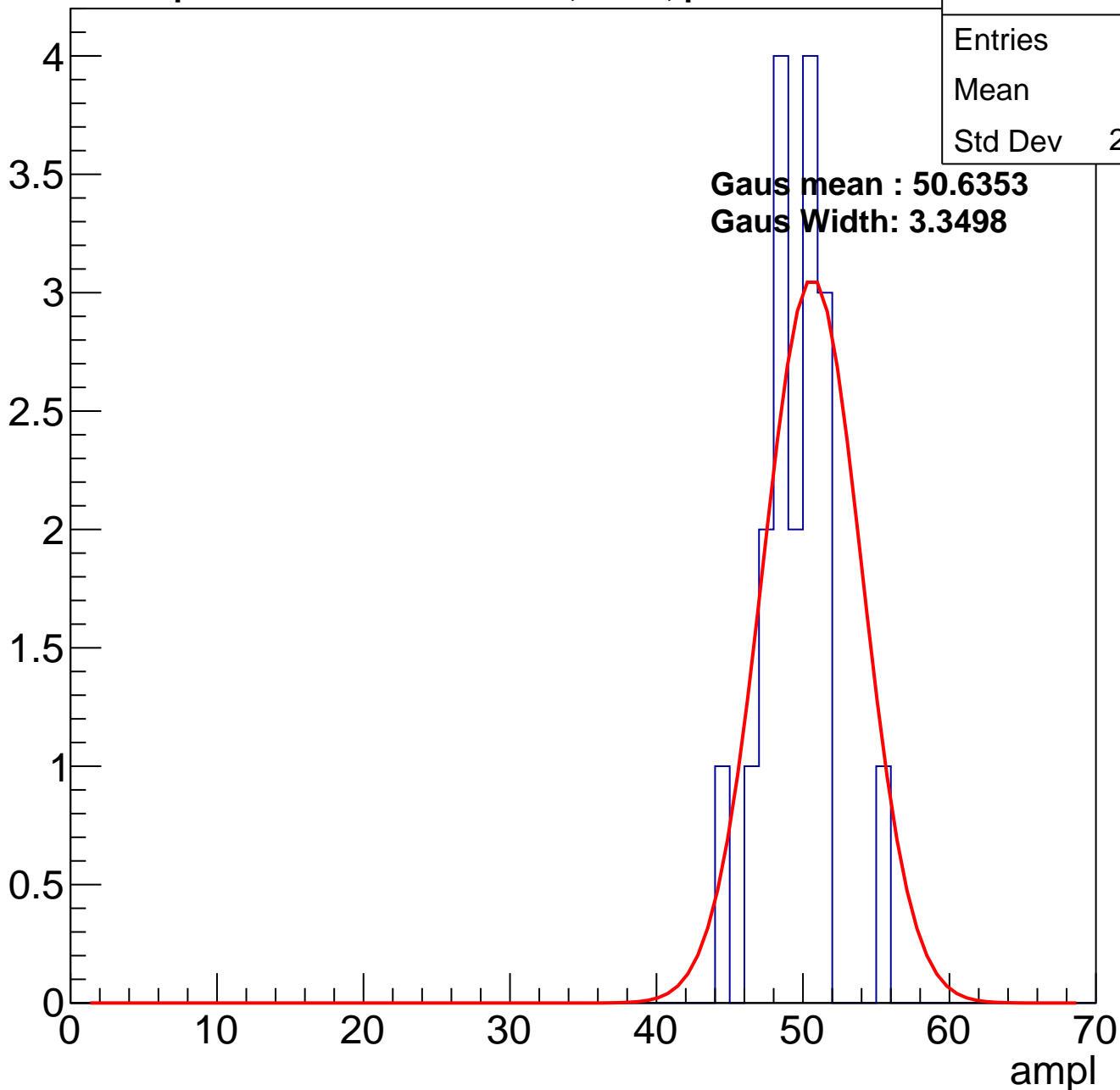
B0L100S, U1-ch95, adc3

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 2 |
| Mean | 49.5 |
| Std Dev | 0.5 |
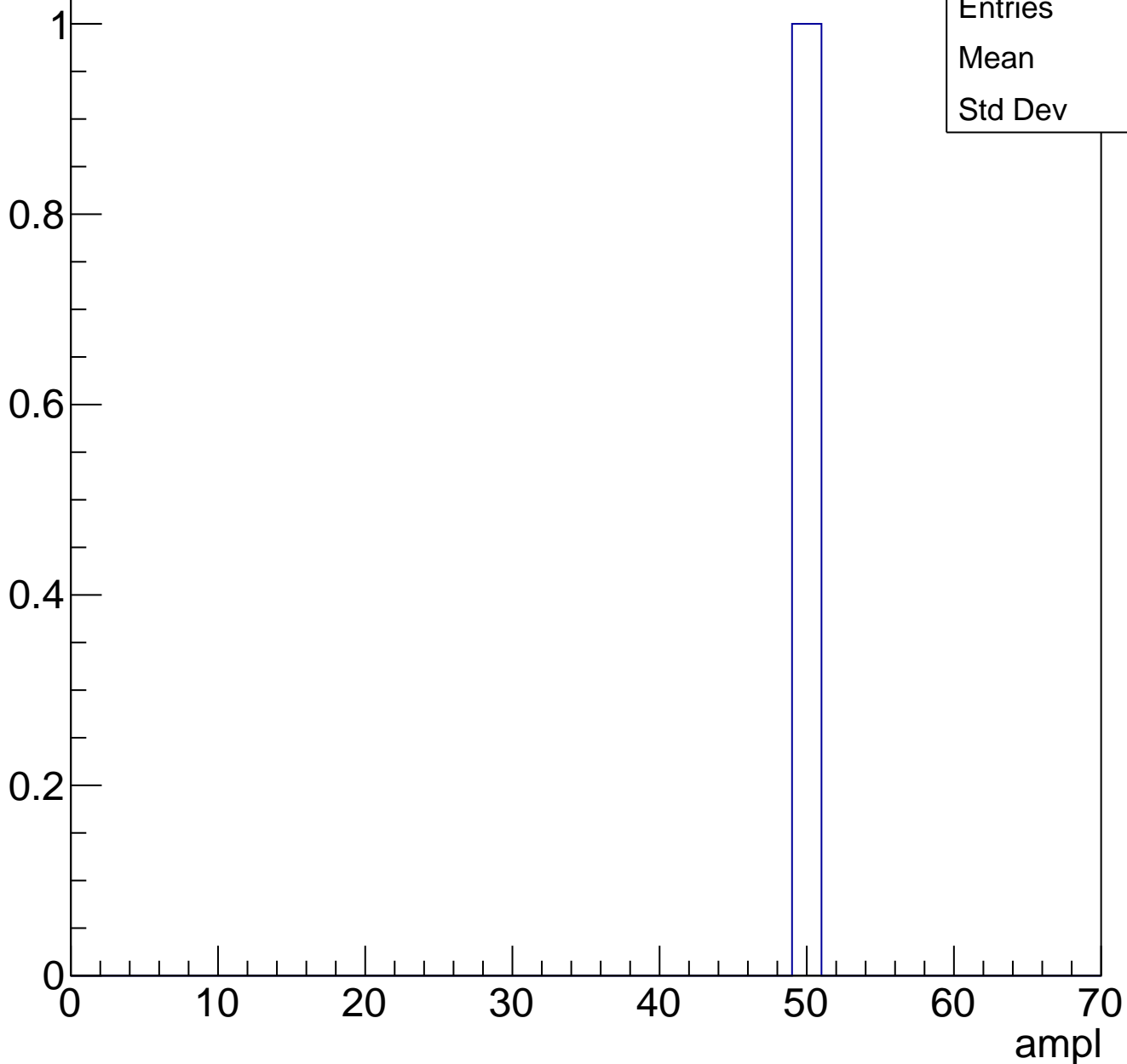
# B0L100S, U1-ch95, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch95, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch95, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch95, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch96, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch96, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 2 |
| Mean | 43 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
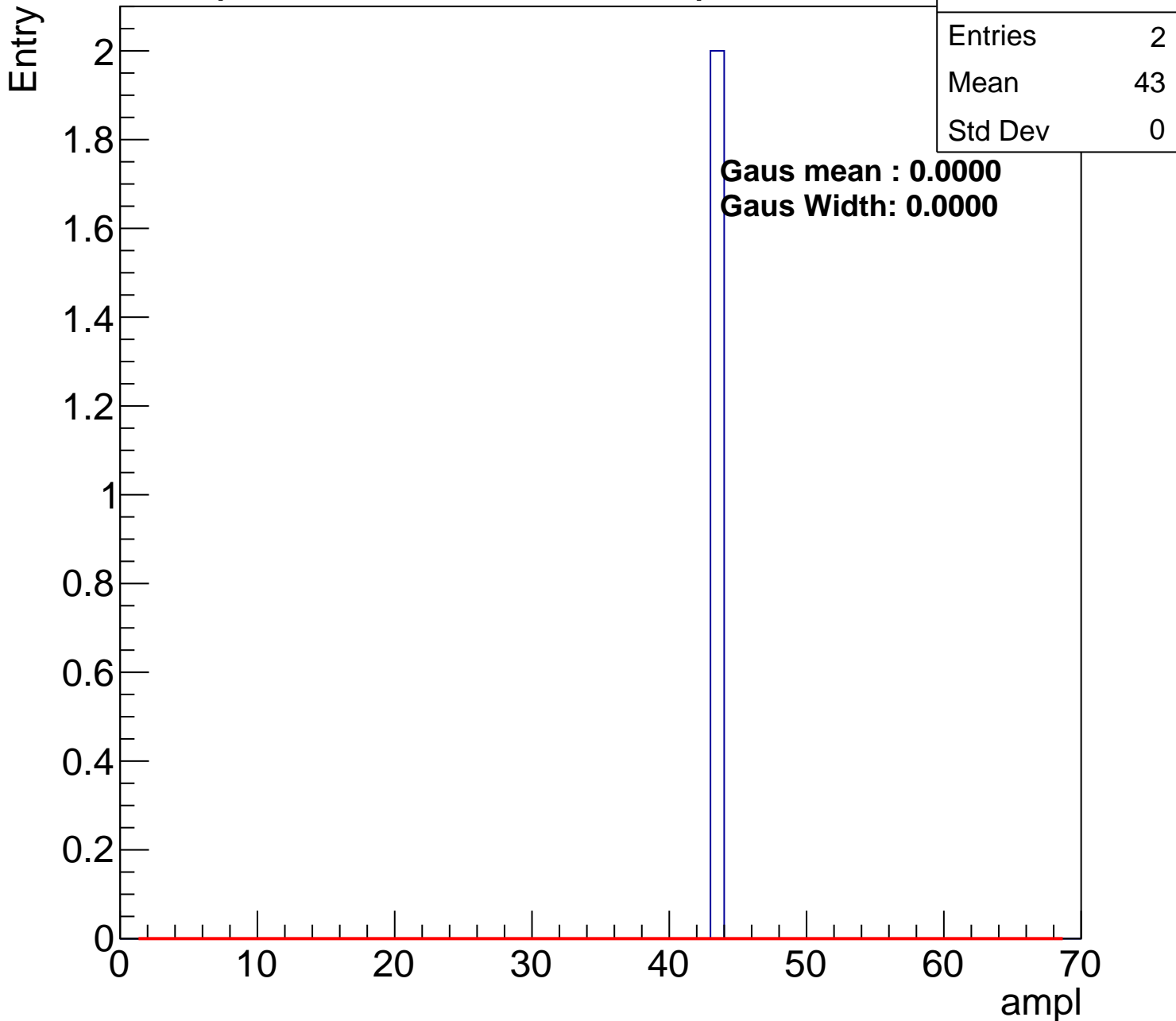
# B0L100S, U1-ch96, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
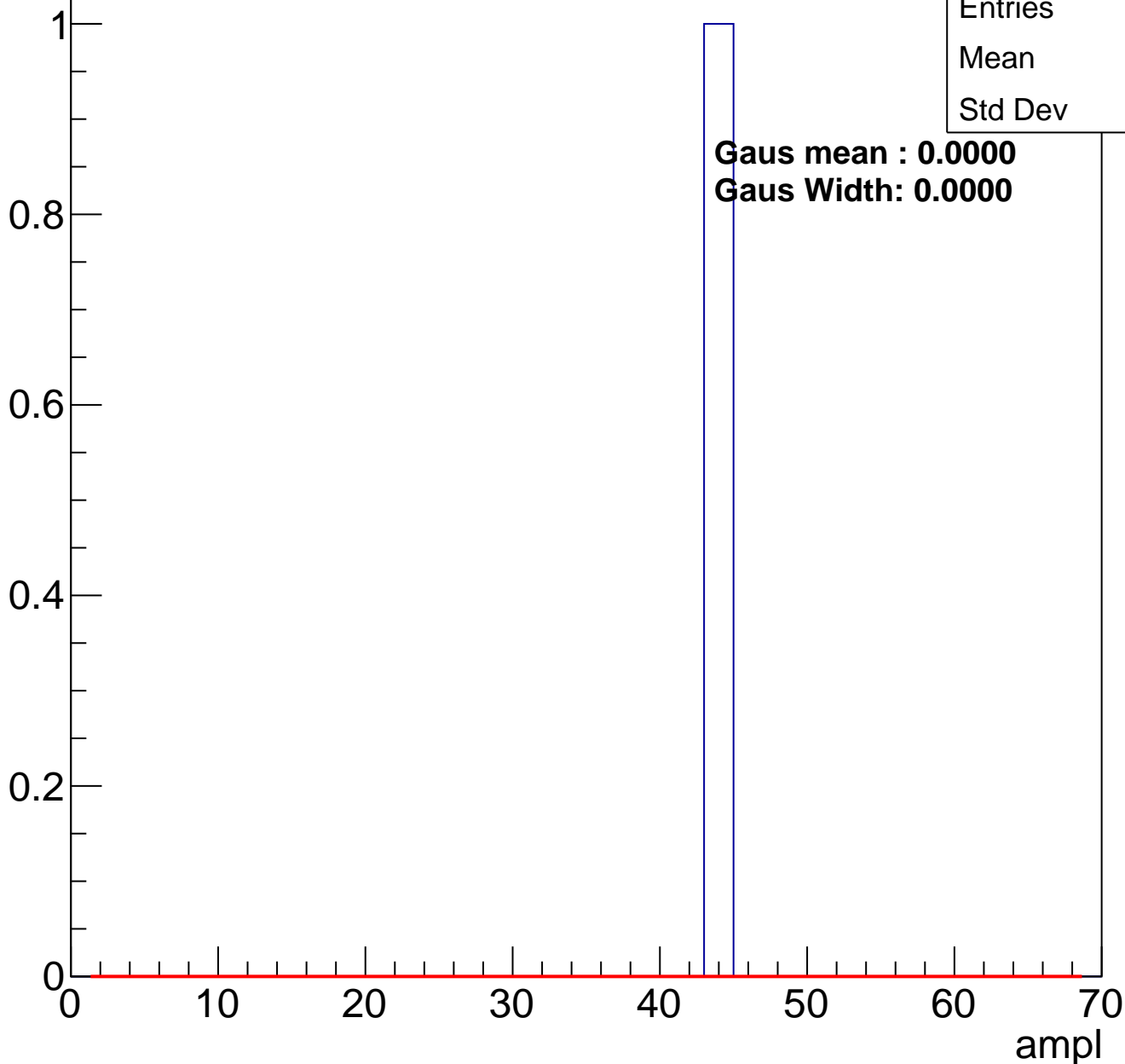
| | |
|---|---|
| Entries | 2 |
| Mean | 43.5 |
| Std Dev | 0.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch96, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 53 |
| Std Dev | 0 |

Entry

ampl
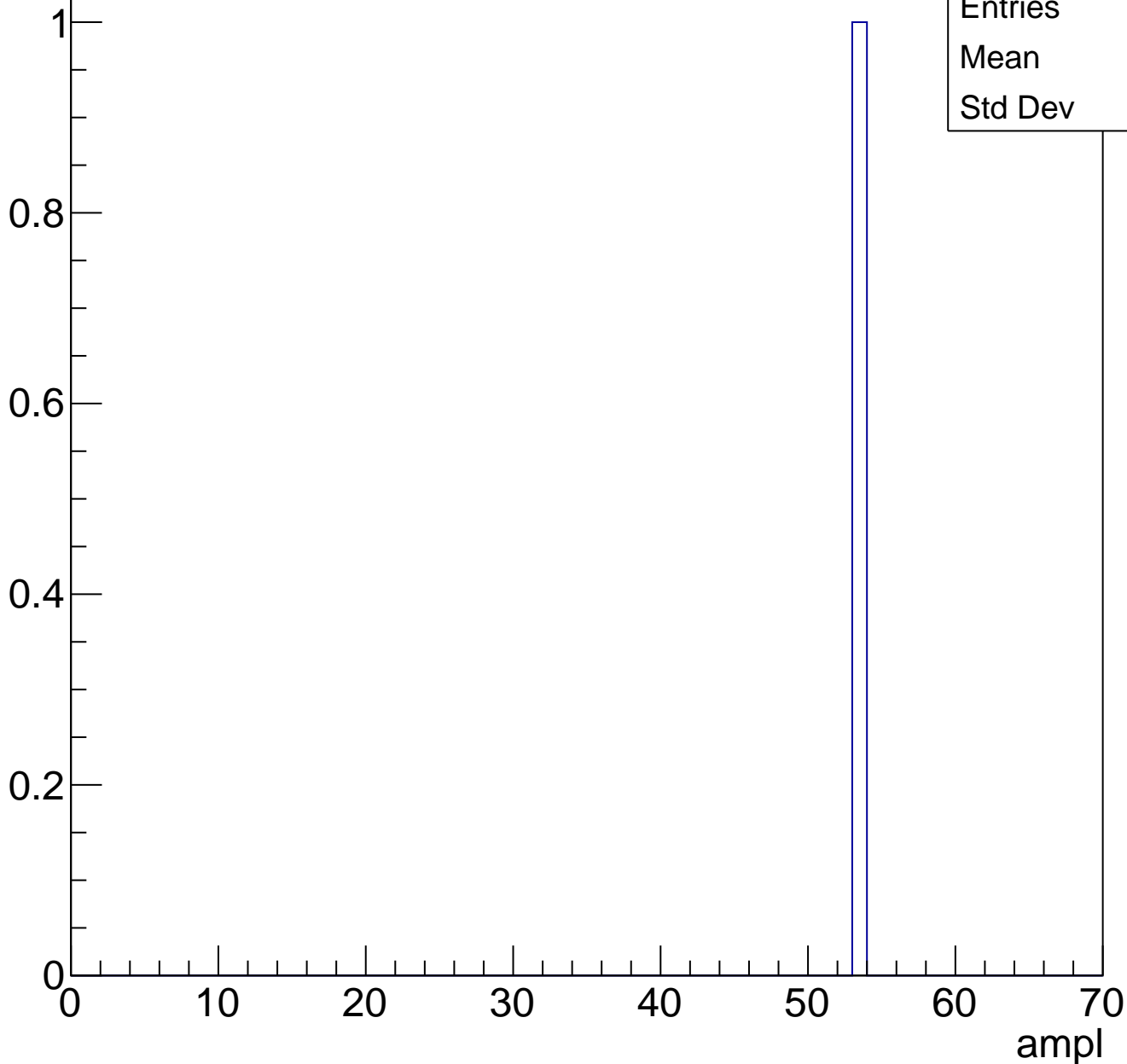
# B0L100S, U1-ch96, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch96, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch96, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch96, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch97, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch97, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 2 |
|---|---|
| Mean | 46 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
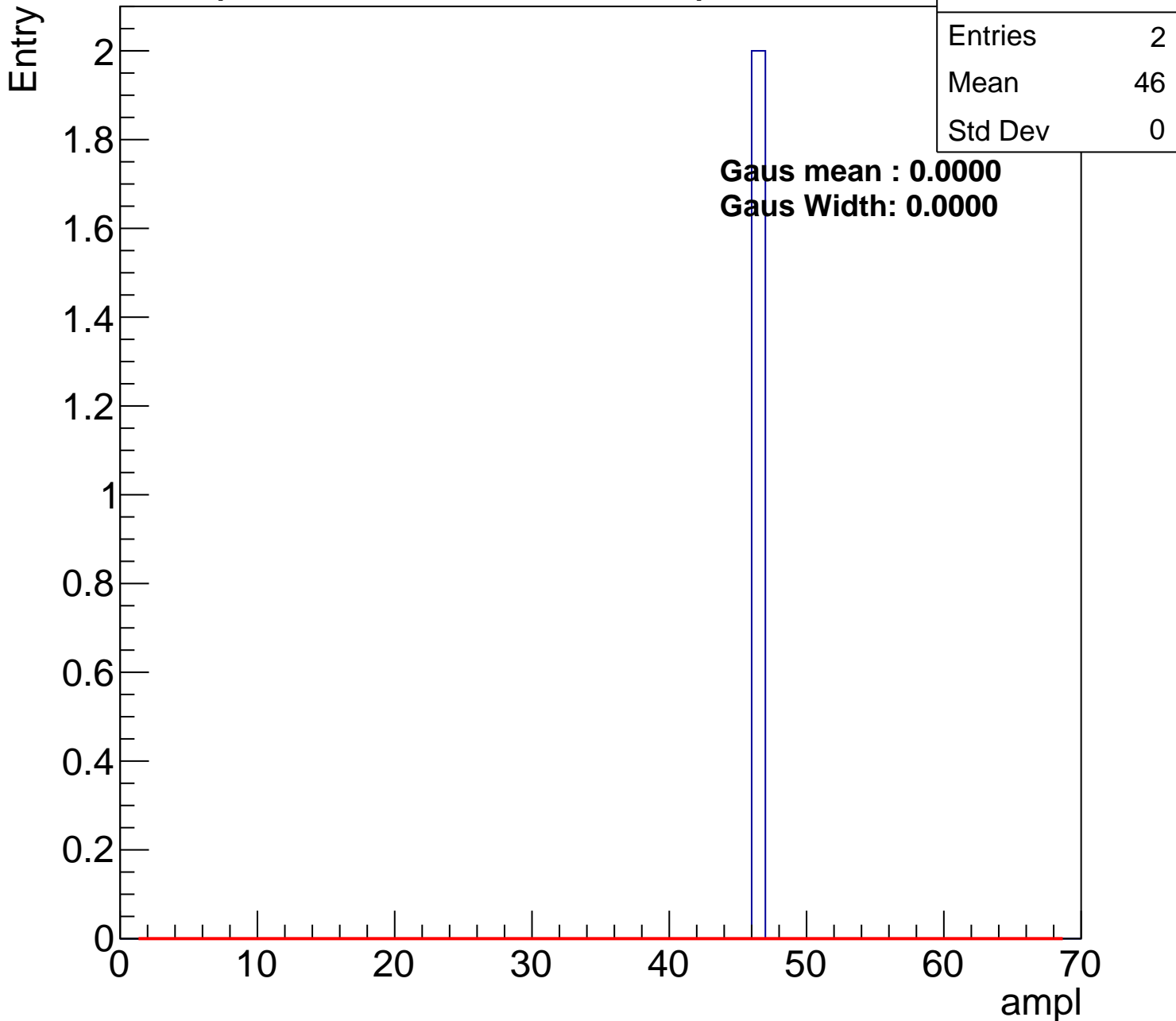
# B0L100S, U1-ch97, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 46 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
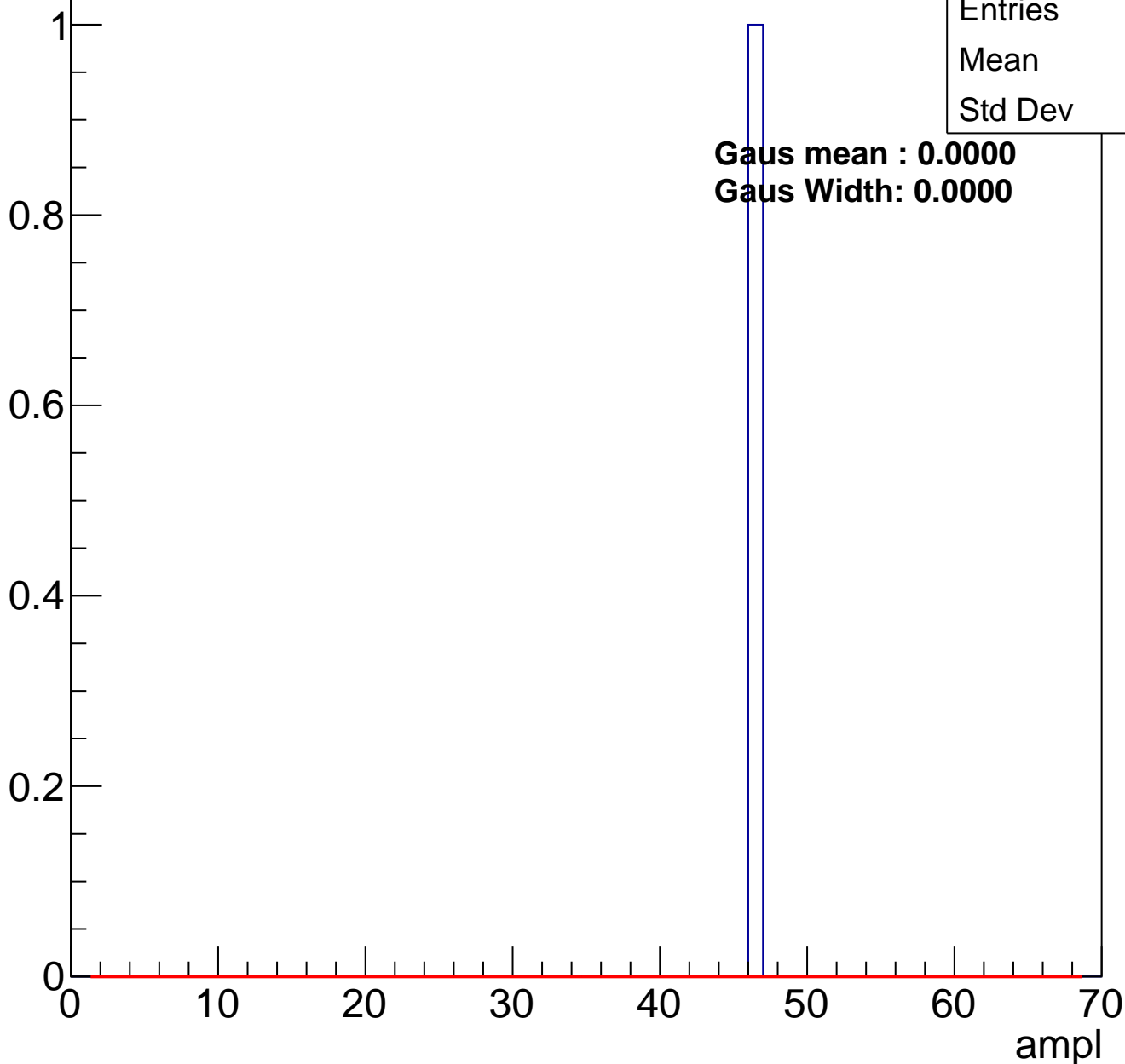
# B0L100S, U1-ch97, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 1 |
| Mean | 53 |
| Std Dev | 0 |

Entry

ampl
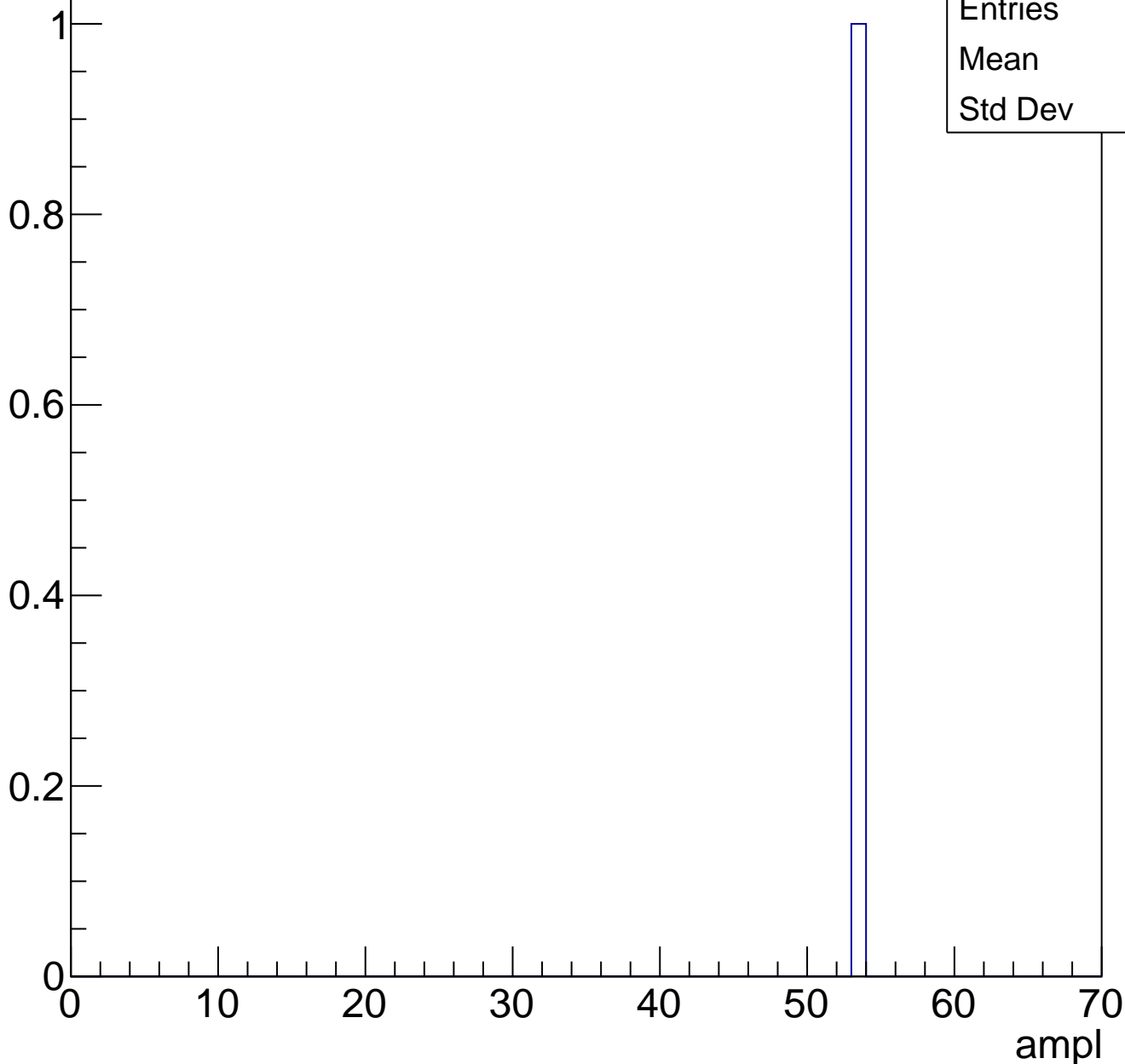
B0L100S, U1-ch97, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L100S, U1-ch97, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch97, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch97, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch98, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch98, adc1

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

# B0L100S, U1-ch98, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

# B0L100S, U1-ch98, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L100S, U1-ch98, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch98, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch99, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
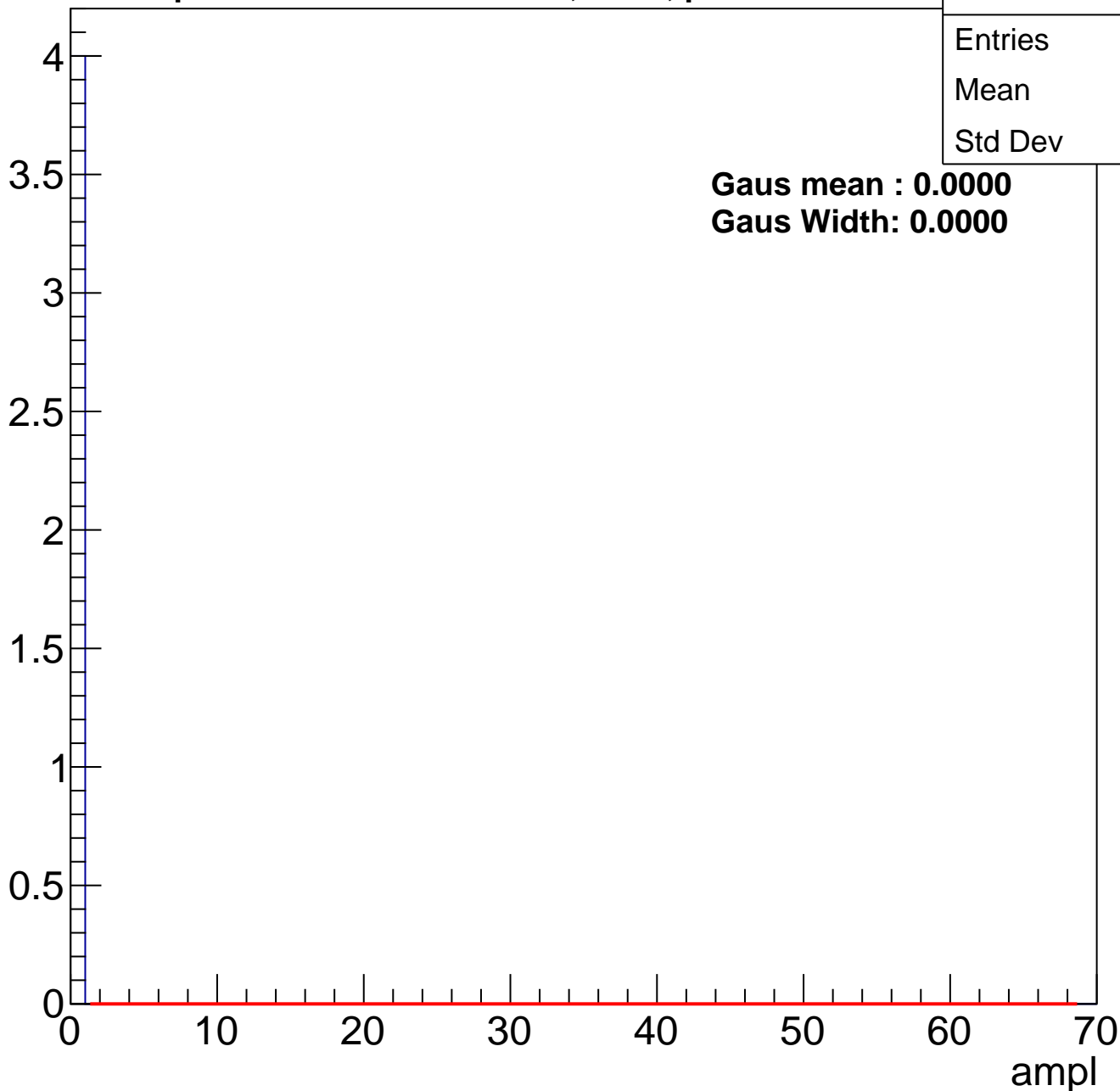
# B0L100S, U1-ch99, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch99, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 4 |
| Mean | 52.5 |
| Std Dev | 1.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
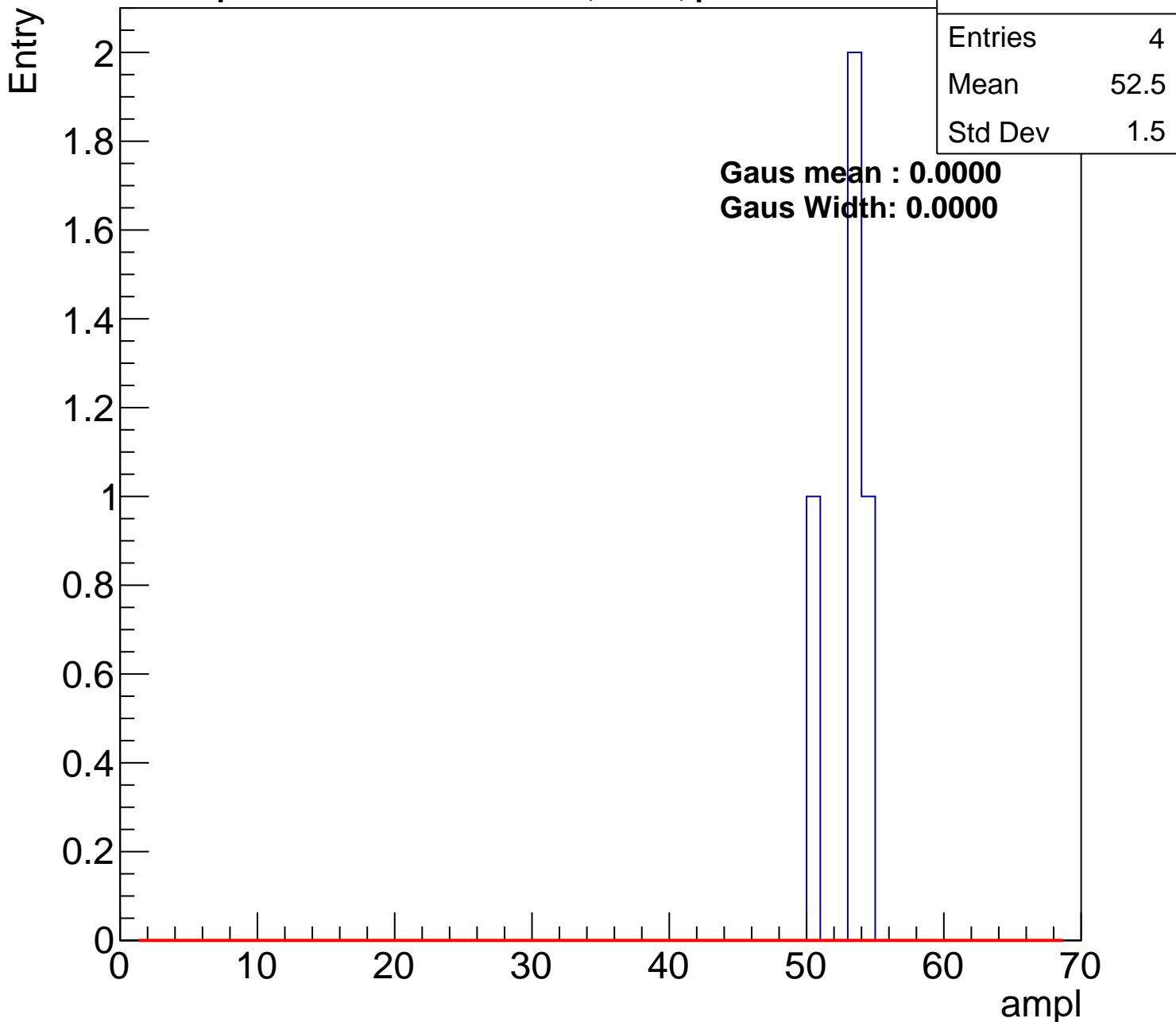
# B0L100S, U1-ch99, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 1 |
| Mean | 53 |
| Std Dev | 0 |

ampl
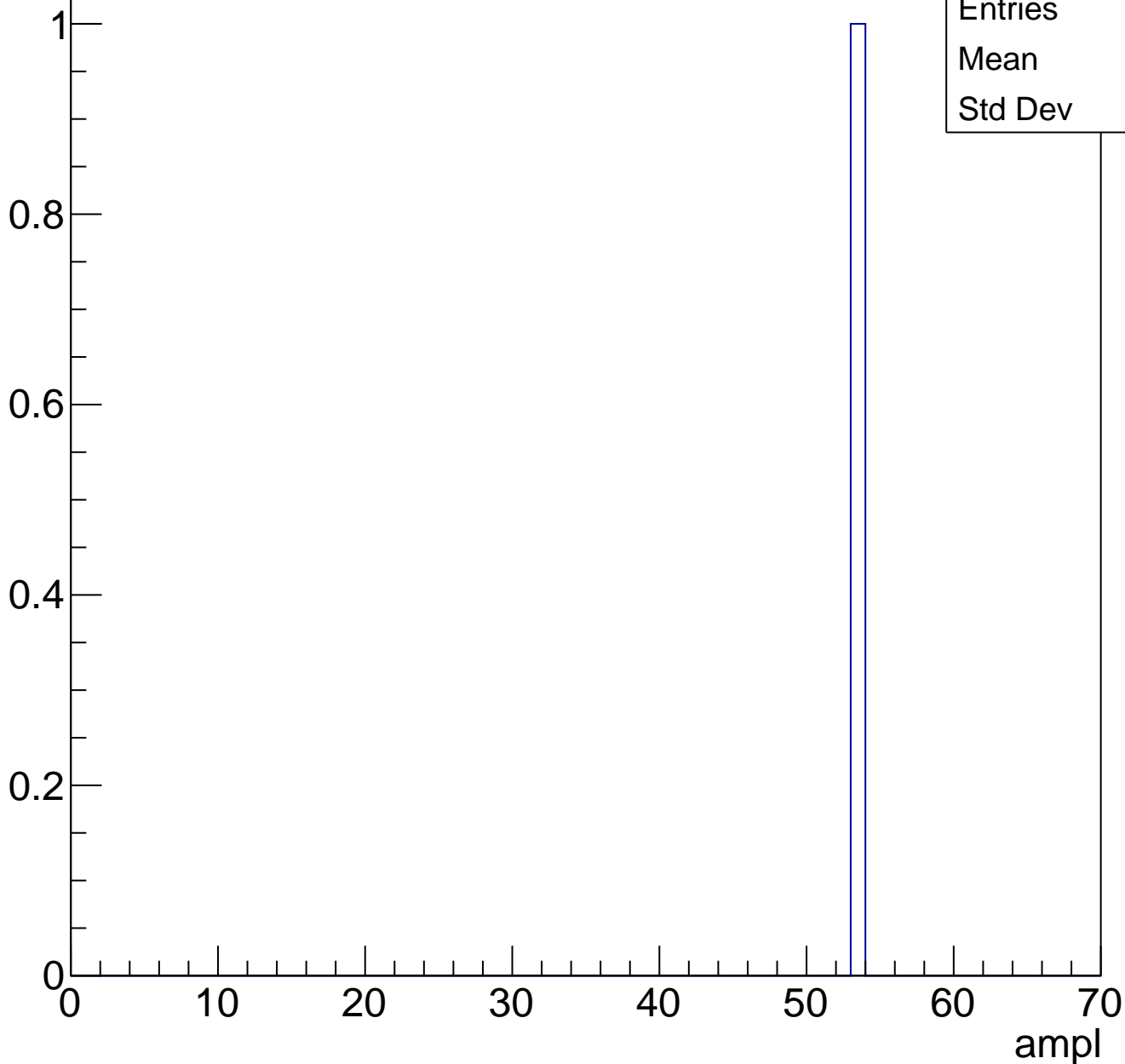
B0L100S, U1-ch99, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 1 |
| Mean | 53 |
| Std Dev | 0 |

# B0L100S, U1-ch99, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch99, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch99, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch100, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch100, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch100, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch100, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch100, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch100, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch100, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch101, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch101, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch101, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 2 |
| Mean | 50.5 |
| Std Dev | 0.5 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

# B0L100S, U1-ch101, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 3 |
| Mean | 50.67 |
| Std Dev | 0.4714 |

Entry

ampl
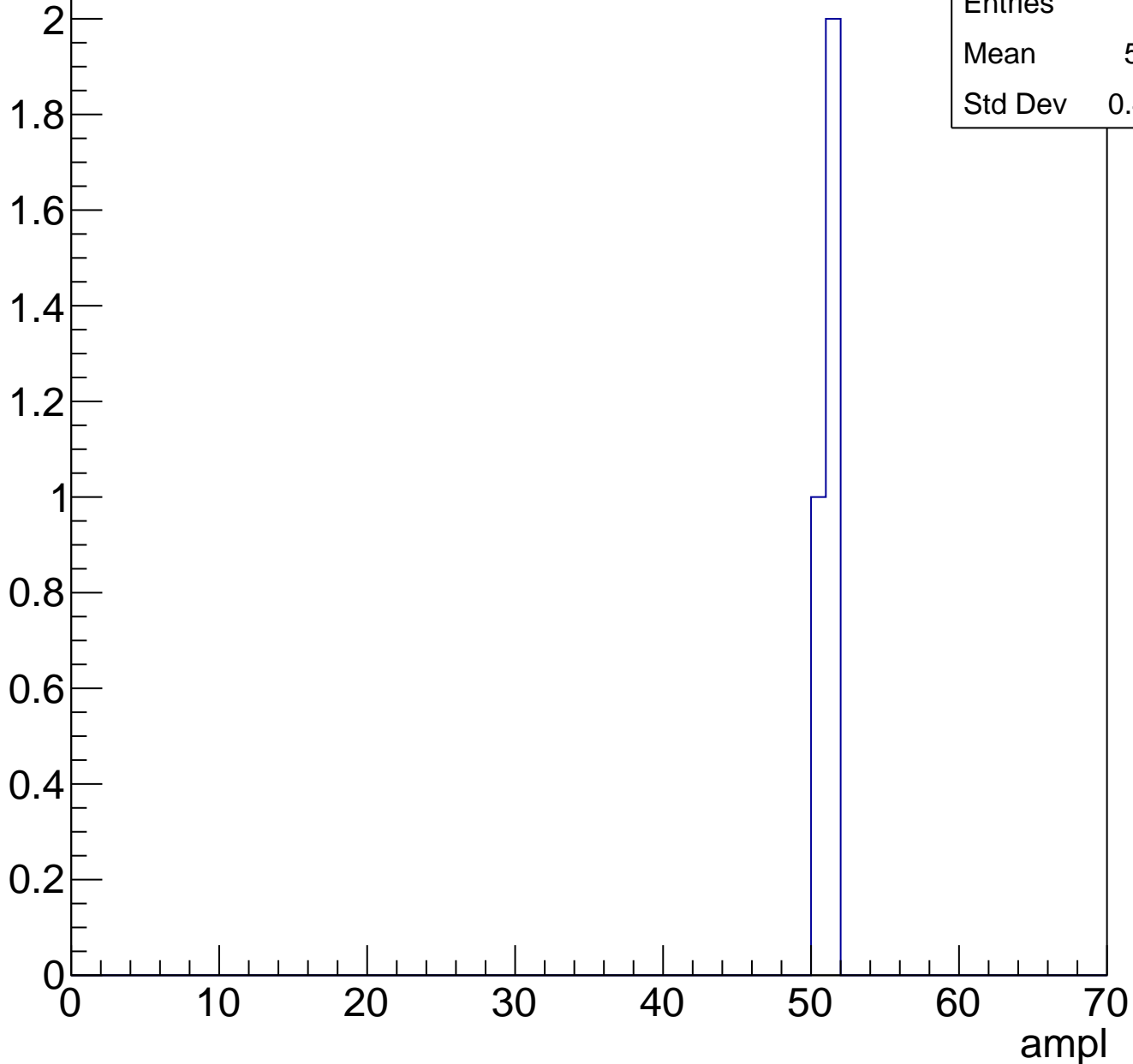
# B0L100S, U1-ch101, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch101, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch101, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch101, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch102, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch102, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch102, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch102, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch102, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch102, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch102, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch102, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch103, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch103, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 48 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
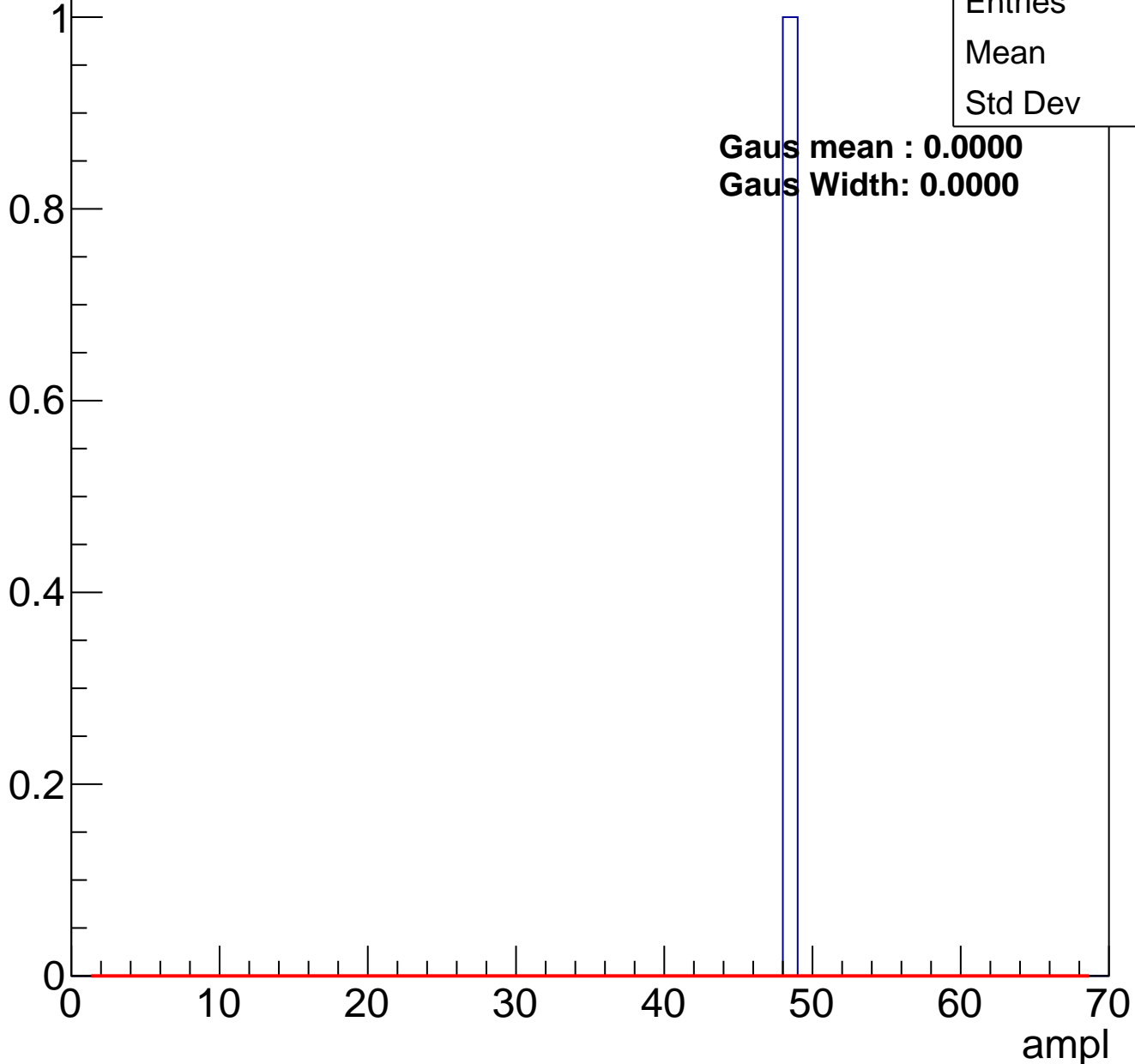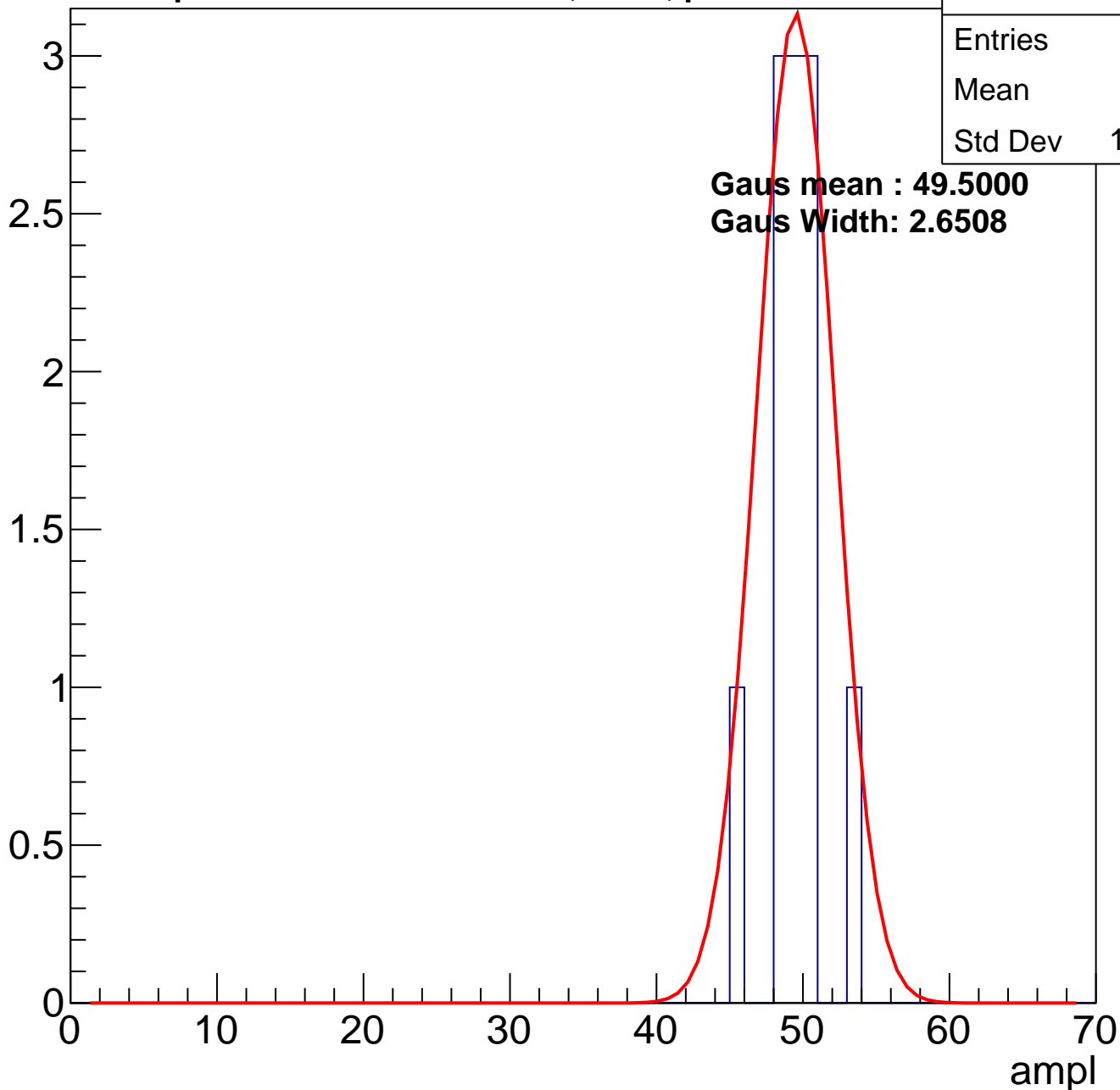
# B0L100S, U1-ch103, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**
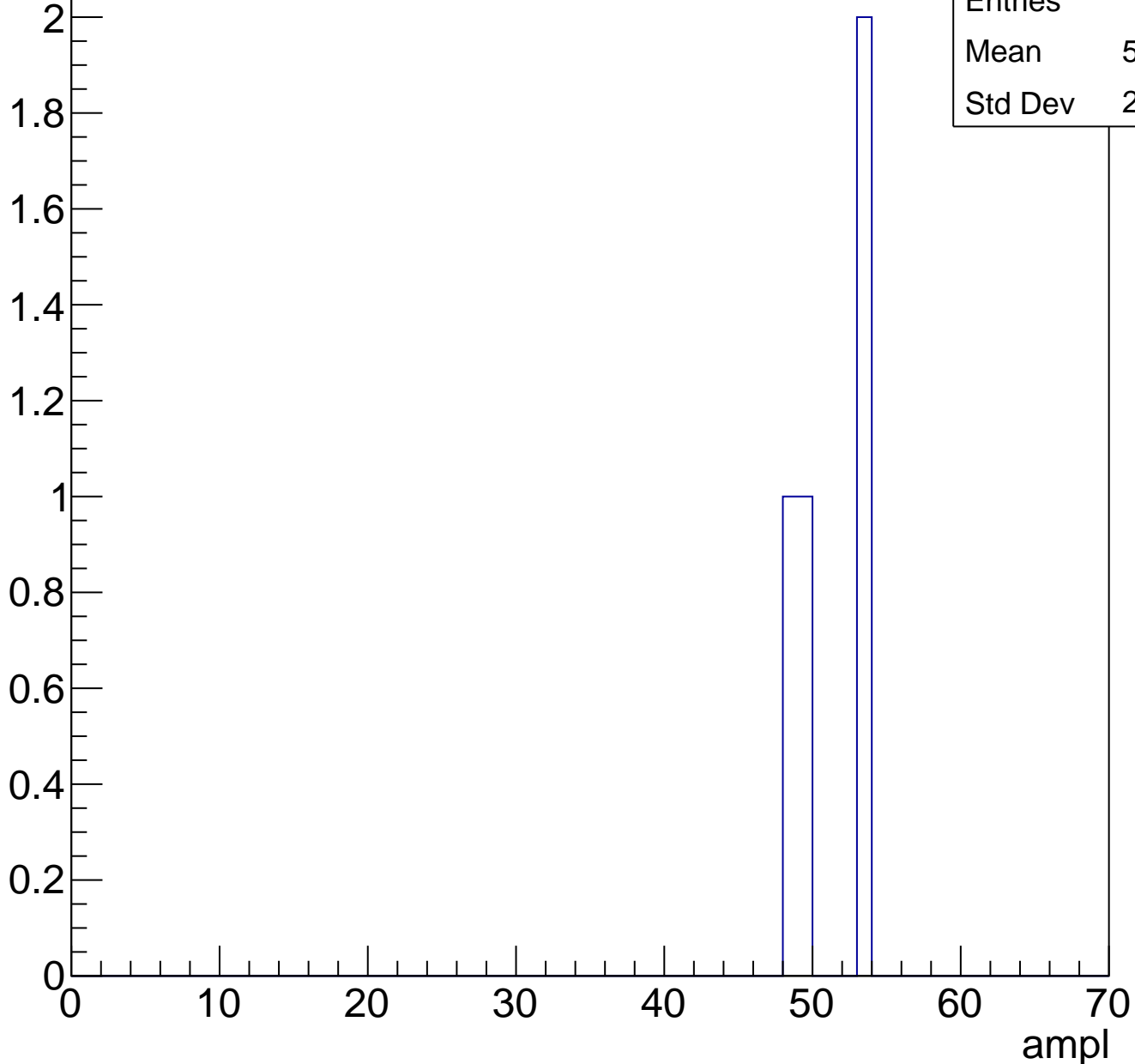
| | |
|---|---|
| Entries | 4 |
| Mean | 50.75 |
| Std Dev | 2.278 |



Entry

ampl

# B0L100S, U1-ch103, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch103, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch103, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch103, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch104, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch104, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch104, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch104, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 49 |
| Std Dev | 0 |

ampl
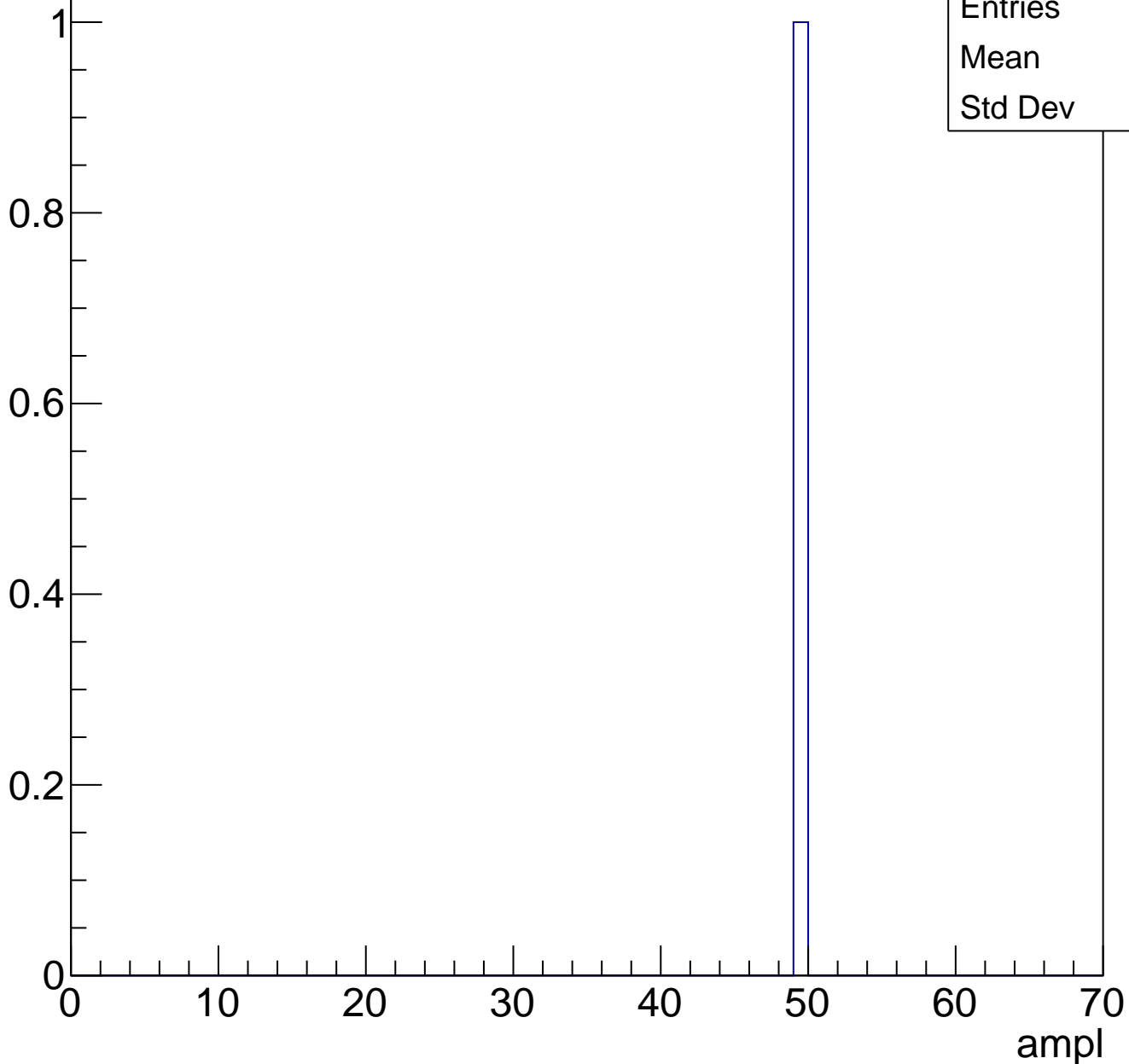
Entry

# B0L100S, U1-ch104, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch104, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch104, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch104, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch105, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch105, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

1

0.8

0.6

0.4

0.2

0

0  10  20  30  40  50  60  70

ampl

# B0L100S, U1-ch105, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch105, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch105, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch105, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch105, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch105, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

ampl

Entry

# B0L100S, U1-ch106, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch106, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch106, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 45.9541
Gaus Width: 5.8808
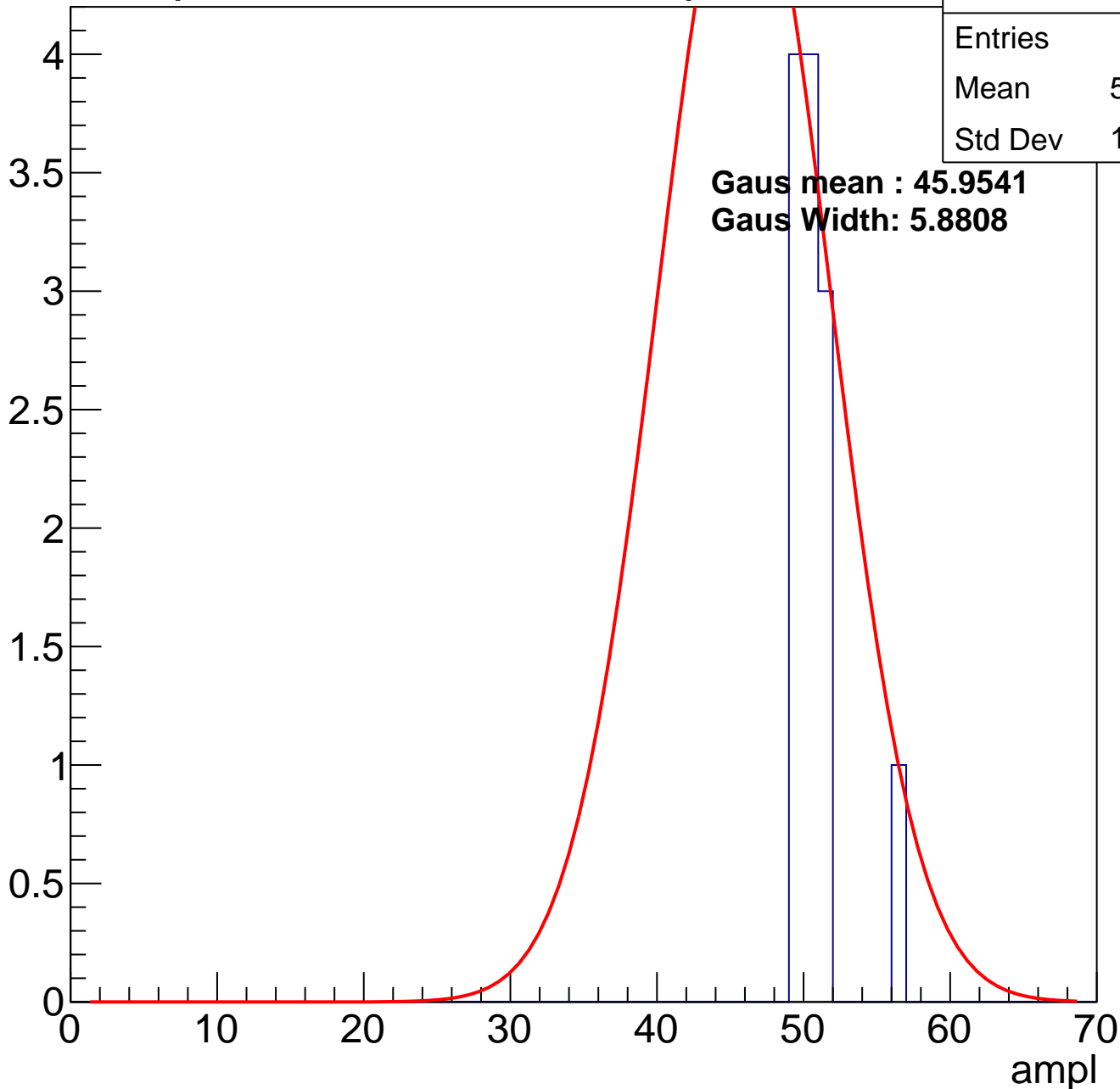
| Entries | 12 |
| Mean | 50.42 |
| Std Dev | 1.847 |

Entry

ampl

# B0L100S, U1-ch106, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 5 |
|---------|---|
| Mean | 51.4 |
| Std Dev | 2.417 |

Entry

ampl
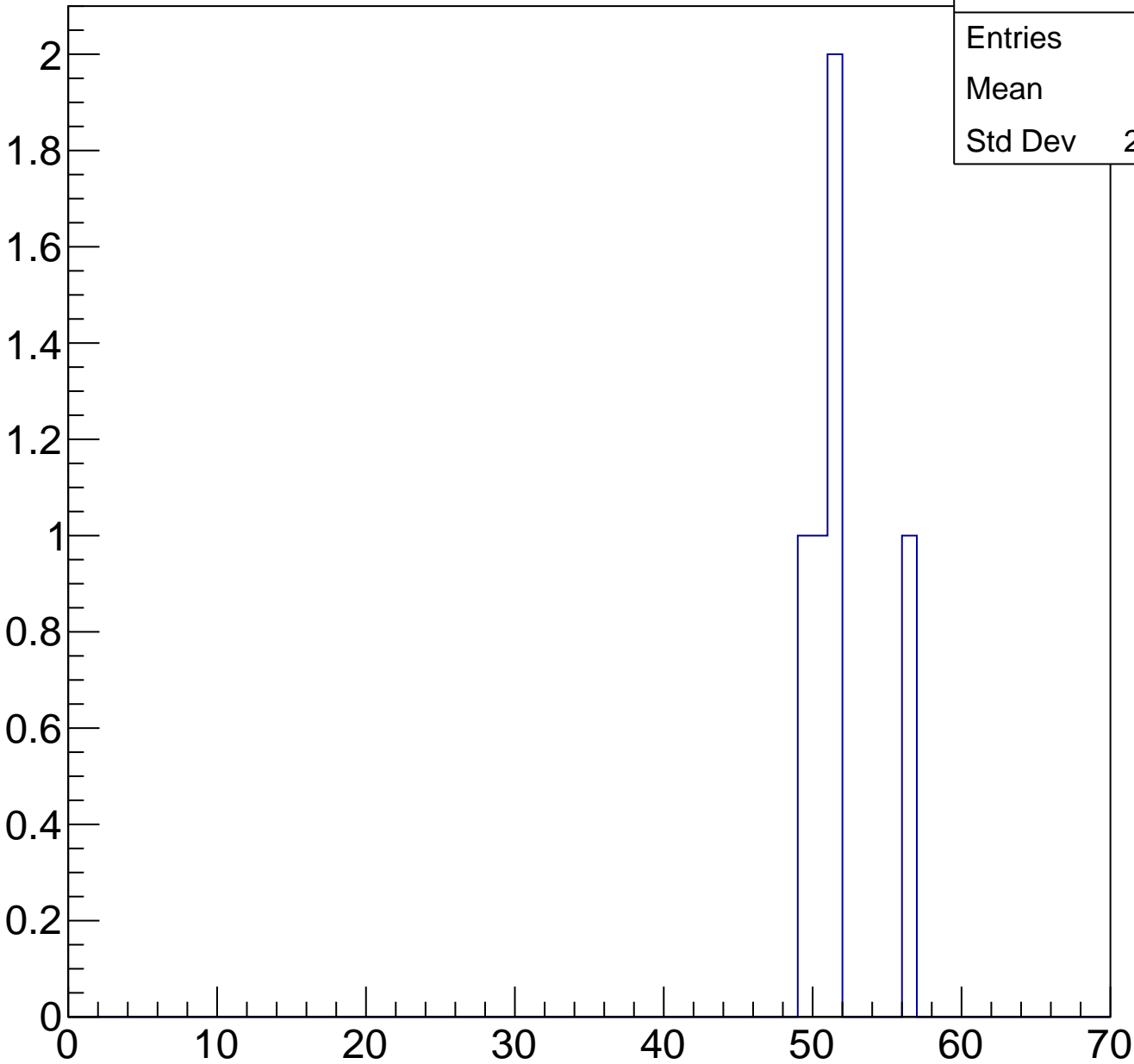
# B0L100S, U1-ch106, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch106, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch106, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch106, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch107, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch107, adc1

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

# B0L100S, U1-ch107, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 50 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
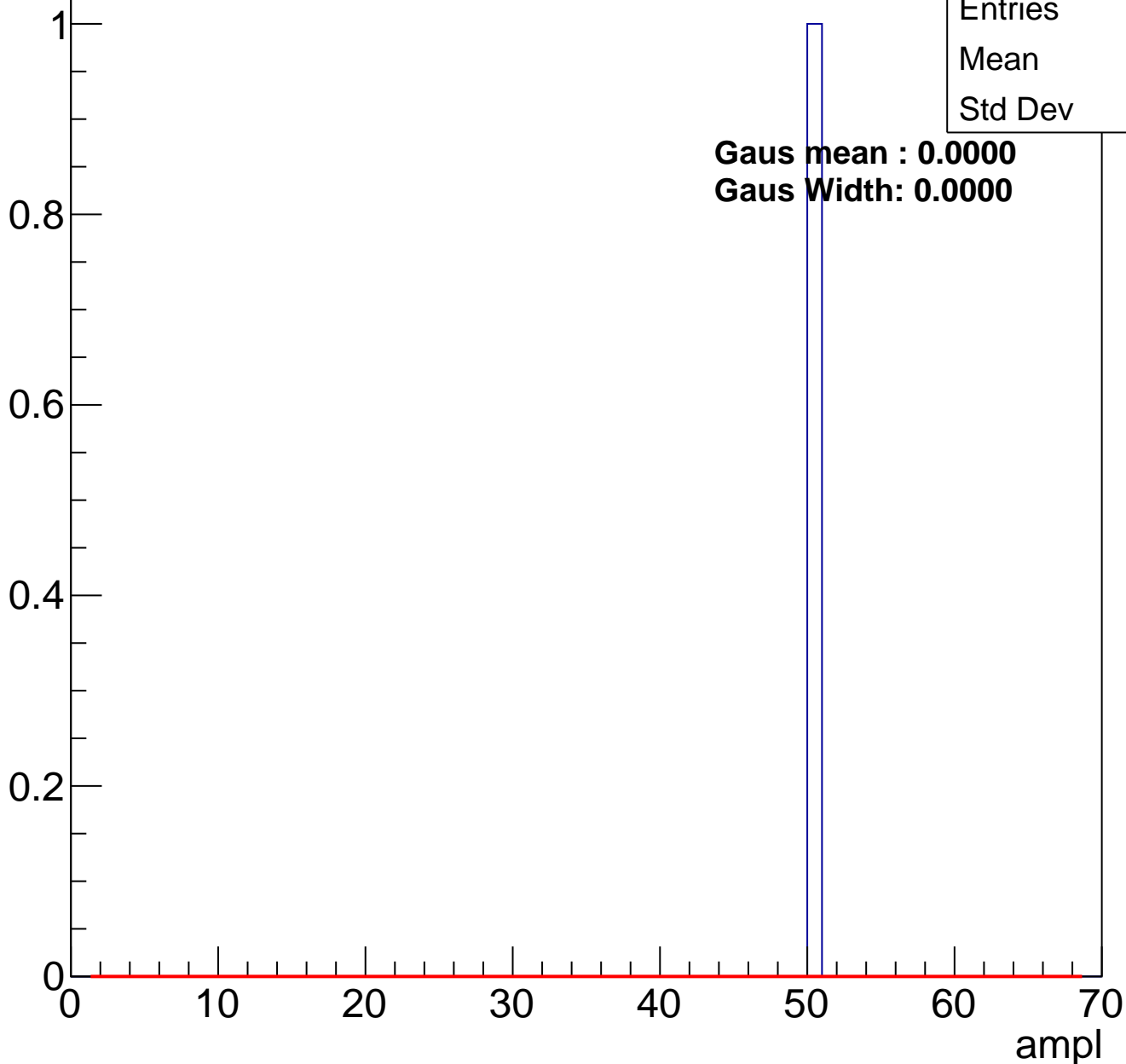
# B0L100S, U1-ch107, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 6 |
|---------|------|
| Mean | 52.5 |
| Std Dev | 0.5 |

ampl
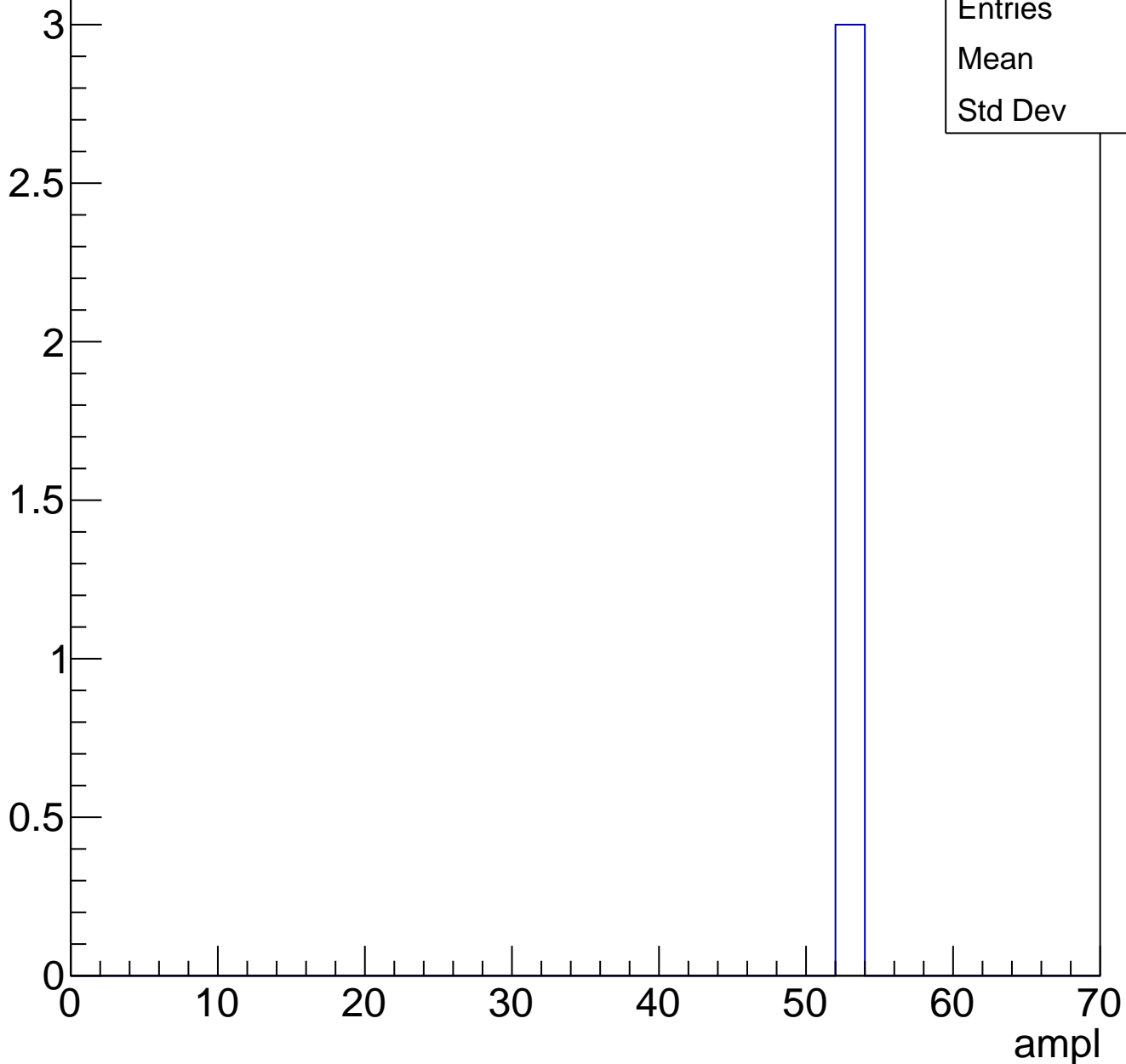
Entry

# B0L100S, U1-ch107, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch107, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch107, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch107, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch108, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch108, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch108, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch108, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch108, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch108, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch108, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch108, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch109, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch109, adc1

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch109, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch109, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch109, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch109, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch109, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch109, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch110, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch110, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch110, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 52 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
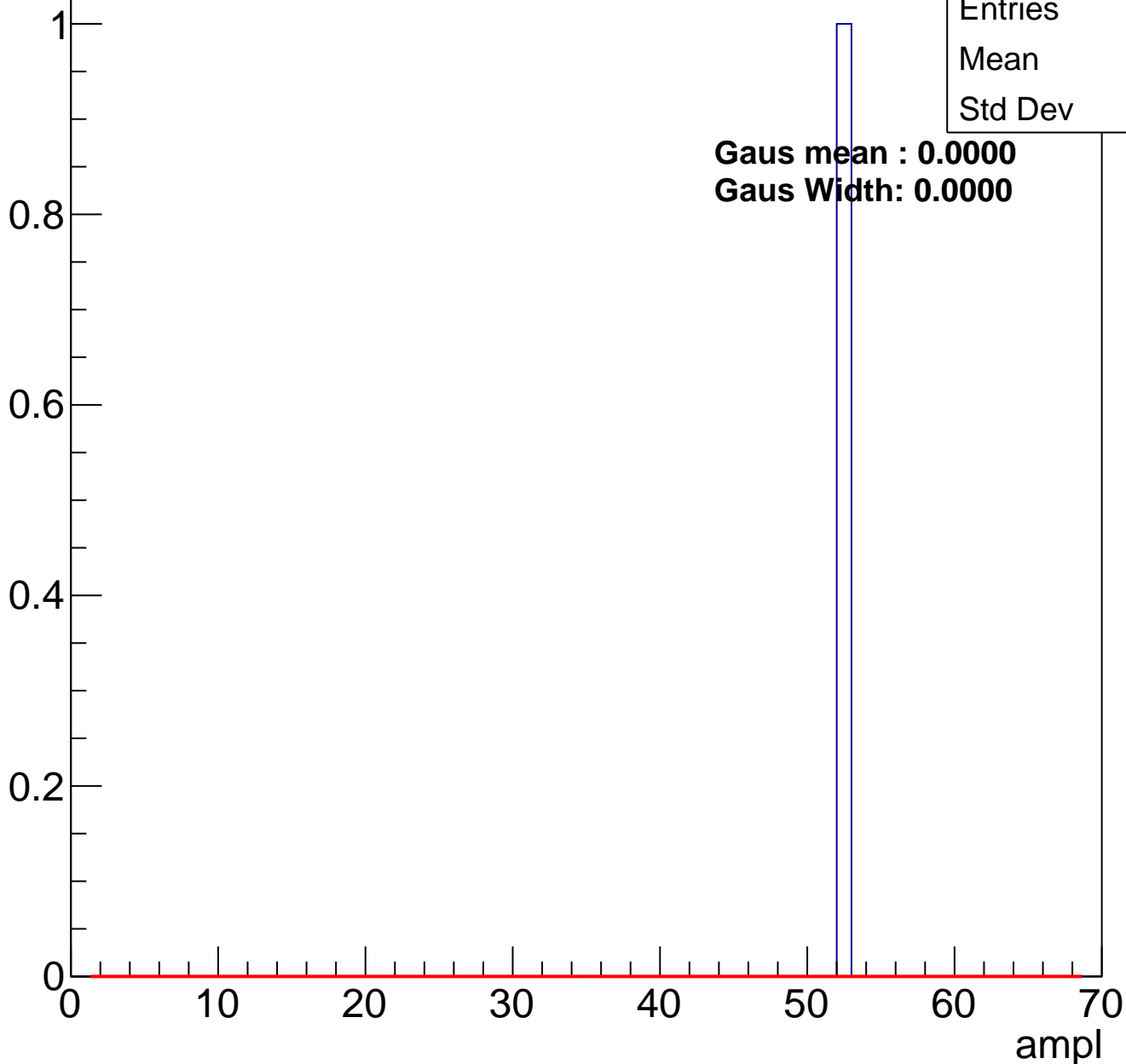
# B0L100S, U1-ch110, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch110, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch110, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch110, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch110, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch111, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

# B0L100S, U1-ch111, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch111, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch111, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch111, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch111, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch111, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch111, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch112, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch112, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch112, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch112, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch112, adc4

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch112, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch112, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch112, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch113, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch113, adc1

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch113, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch113, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch113, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch113, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch113, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch113, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L100S, U1-ch114, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

# B0L100S, U1-ch114, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

# B0L100S, U1-ch114, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

# B0L100S, U1-ch114, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch114, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch114, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch114, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch114, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch115, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch115, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch115, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch115, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch115, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch115, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch115, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch116, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch116, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch116, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
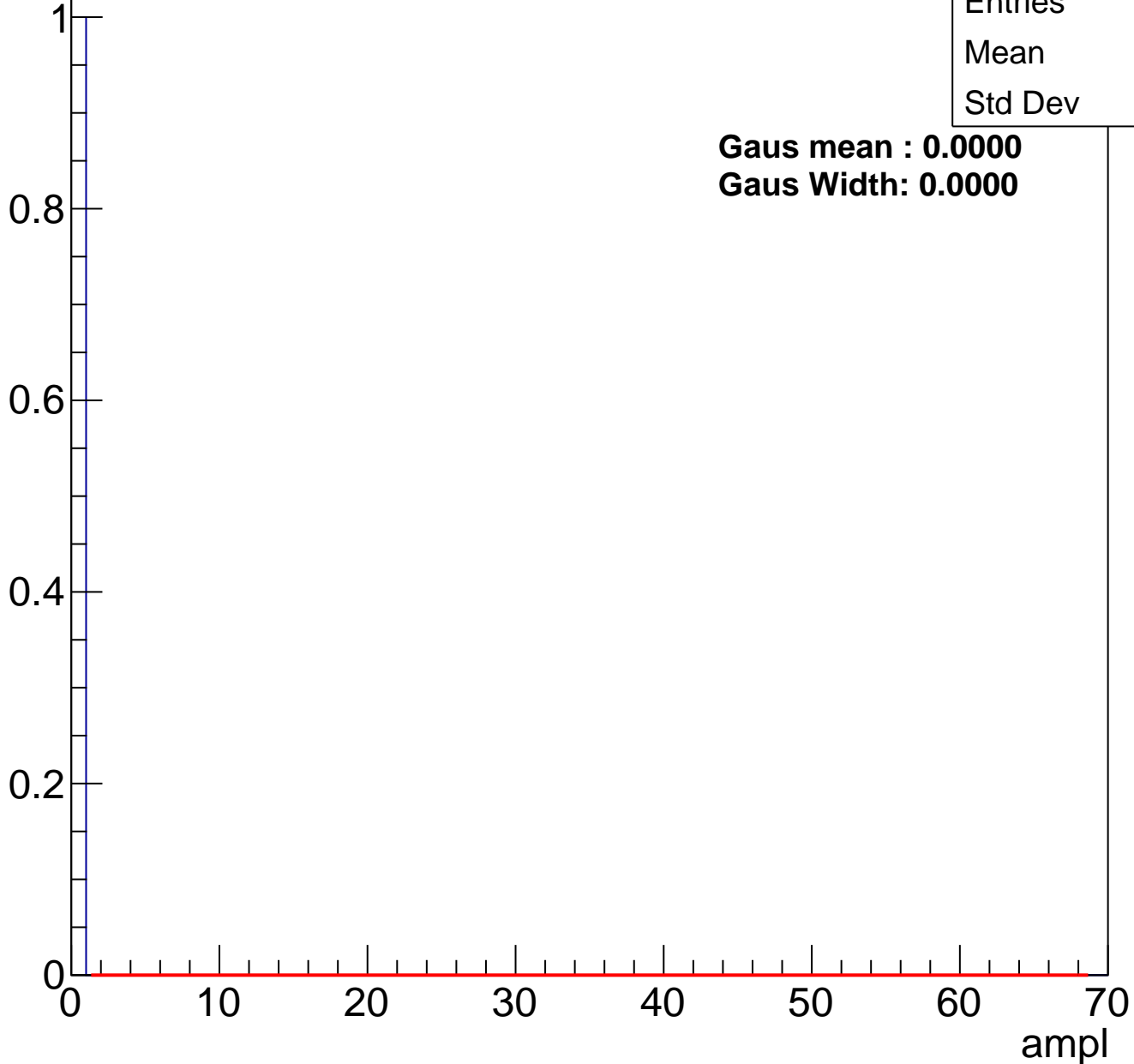
B0L100S, U1-ch116, adc3

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch116, adc4
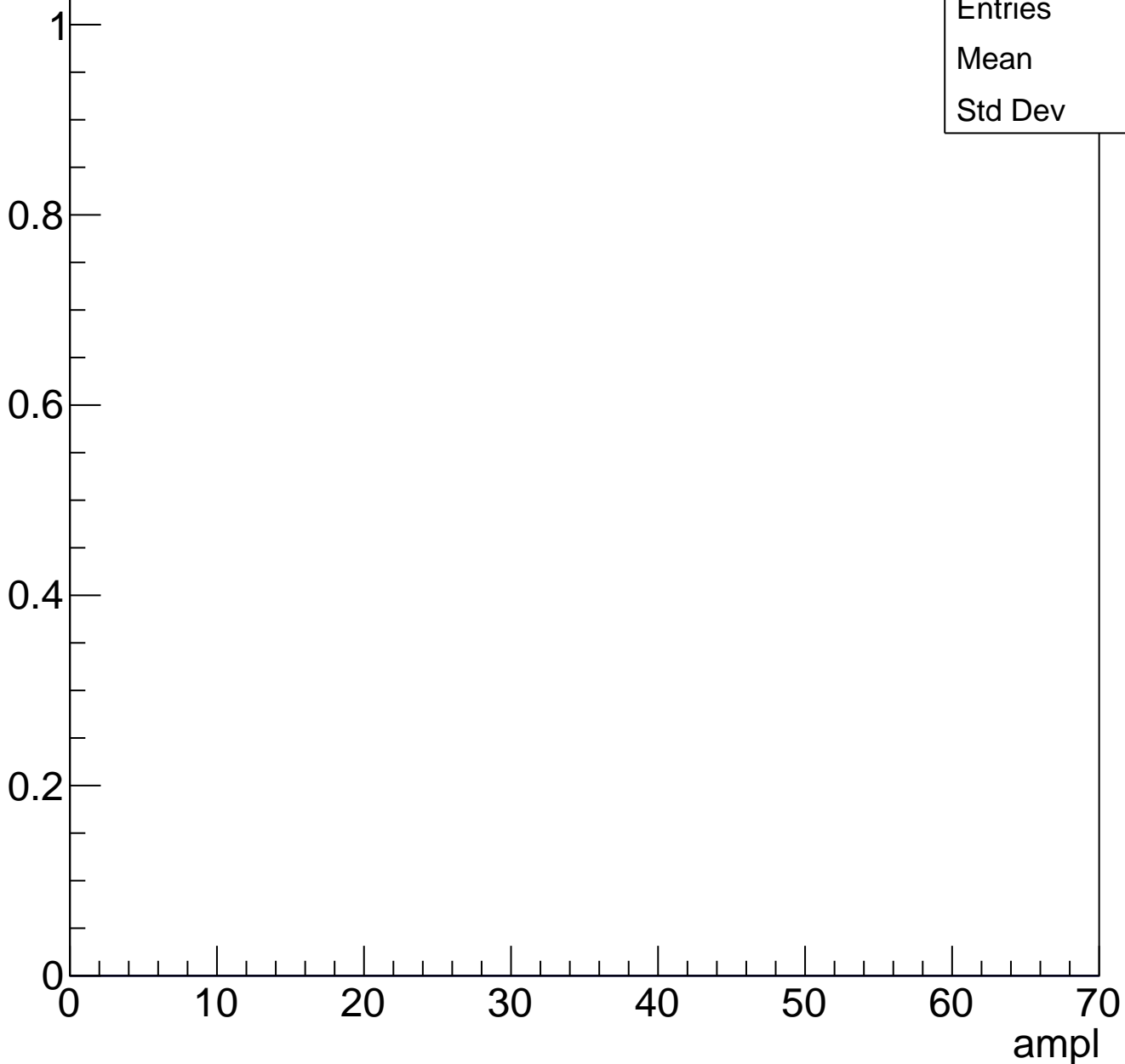
**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch116, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch116, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch116, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch117, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch117, adc1

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch117, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch117, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch117, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch117, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch117, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch117, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch118, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| | |
|---|---|
| Entries | 1 |
| Mean | 46 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl

B0L100S, U1-ch118, adc1

calib_packv5_042523_0143.root, FC#6, port A1
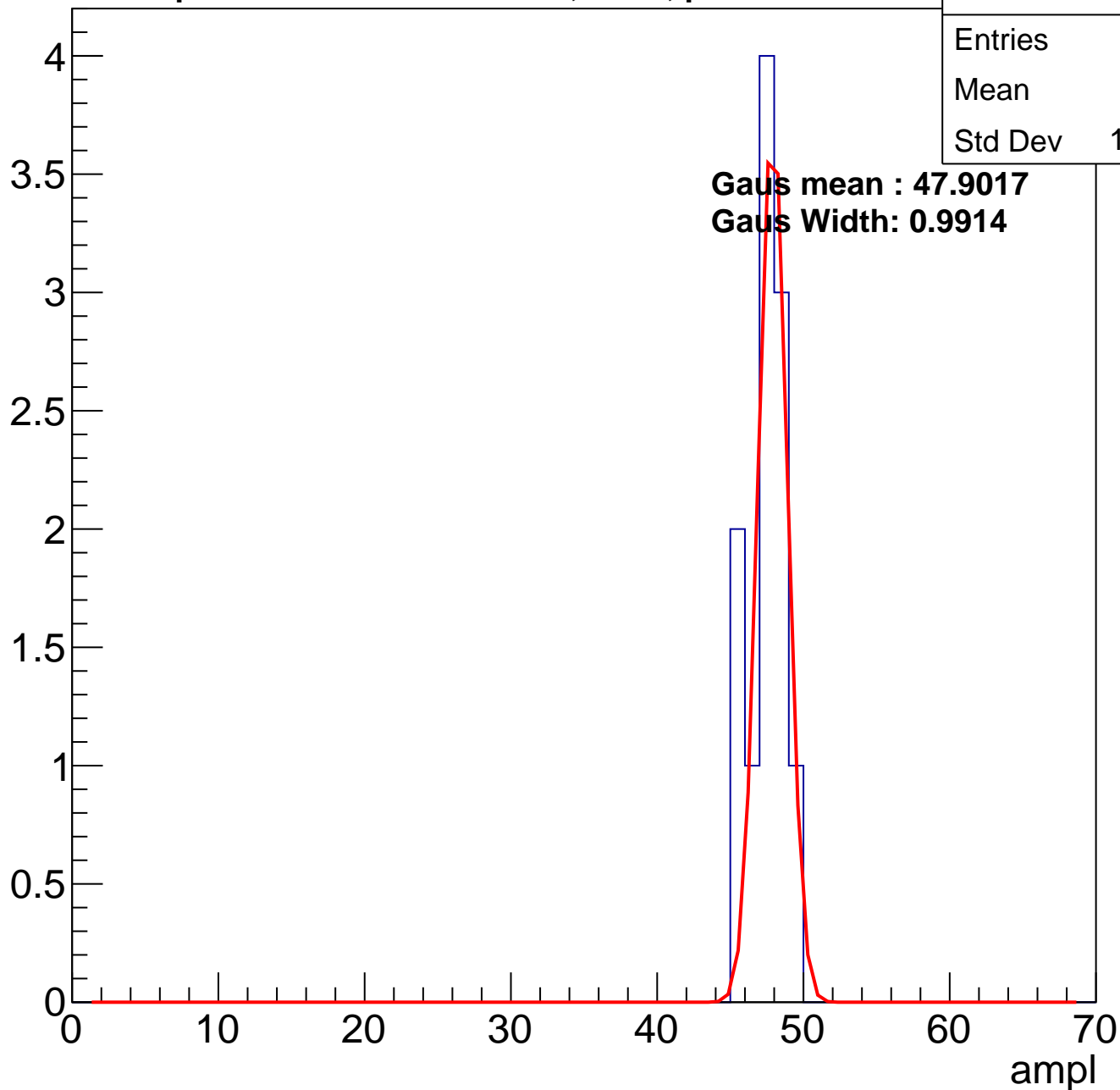
Gaus mean : 47.9017
Gaus Width: 0.9914
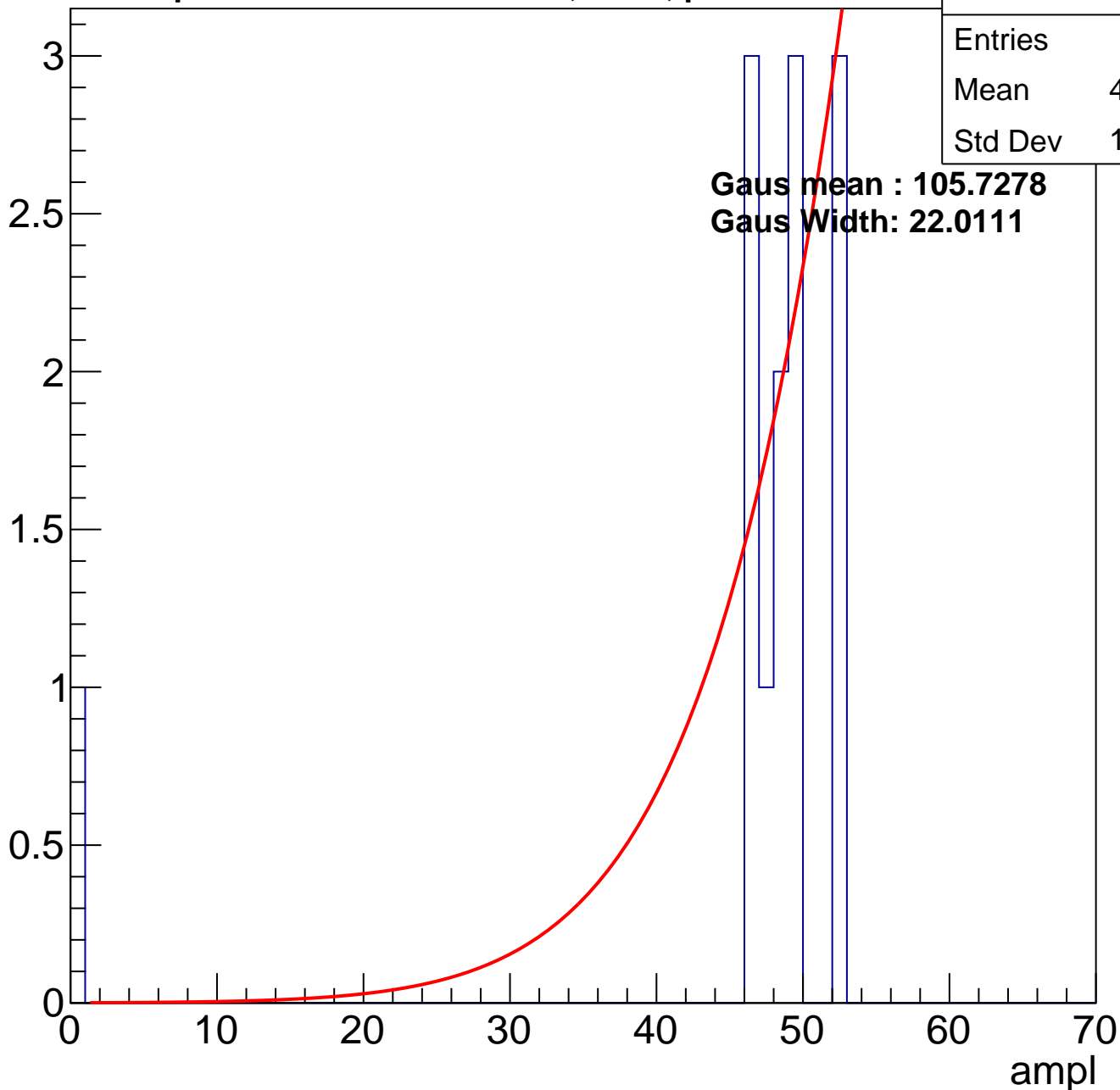
| Entries | 11 |
| Mean | 47 |
| Std Dev | 1.206 |

# B0L100S, U1-ch118, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 5 |
| Mean | 52.6 |
| Std Dev | 0.4899 |

ampl
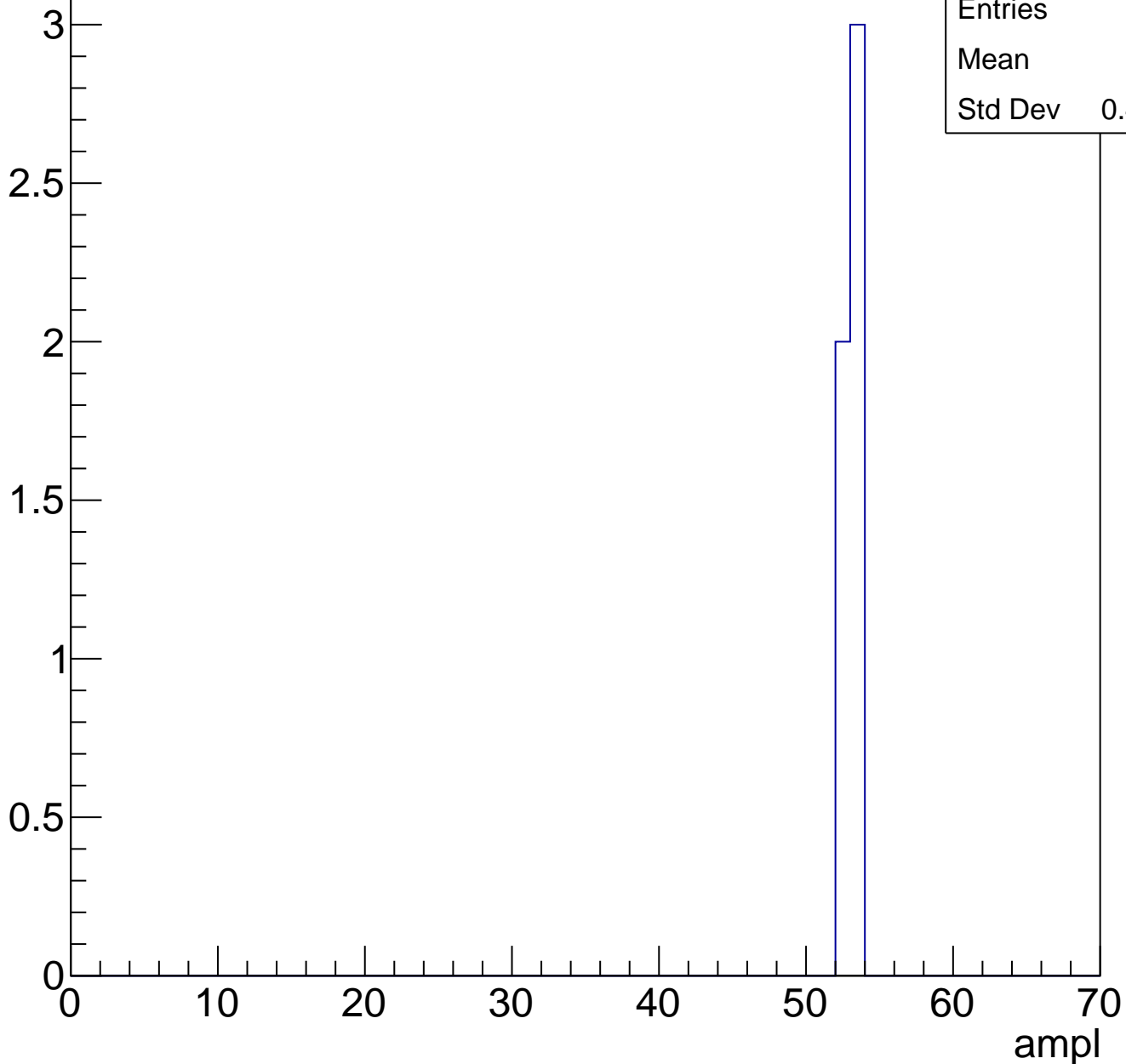
Entry

# B0L100S, U1-ch118, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch118, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**



| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch118, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch118, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch119, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 50 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
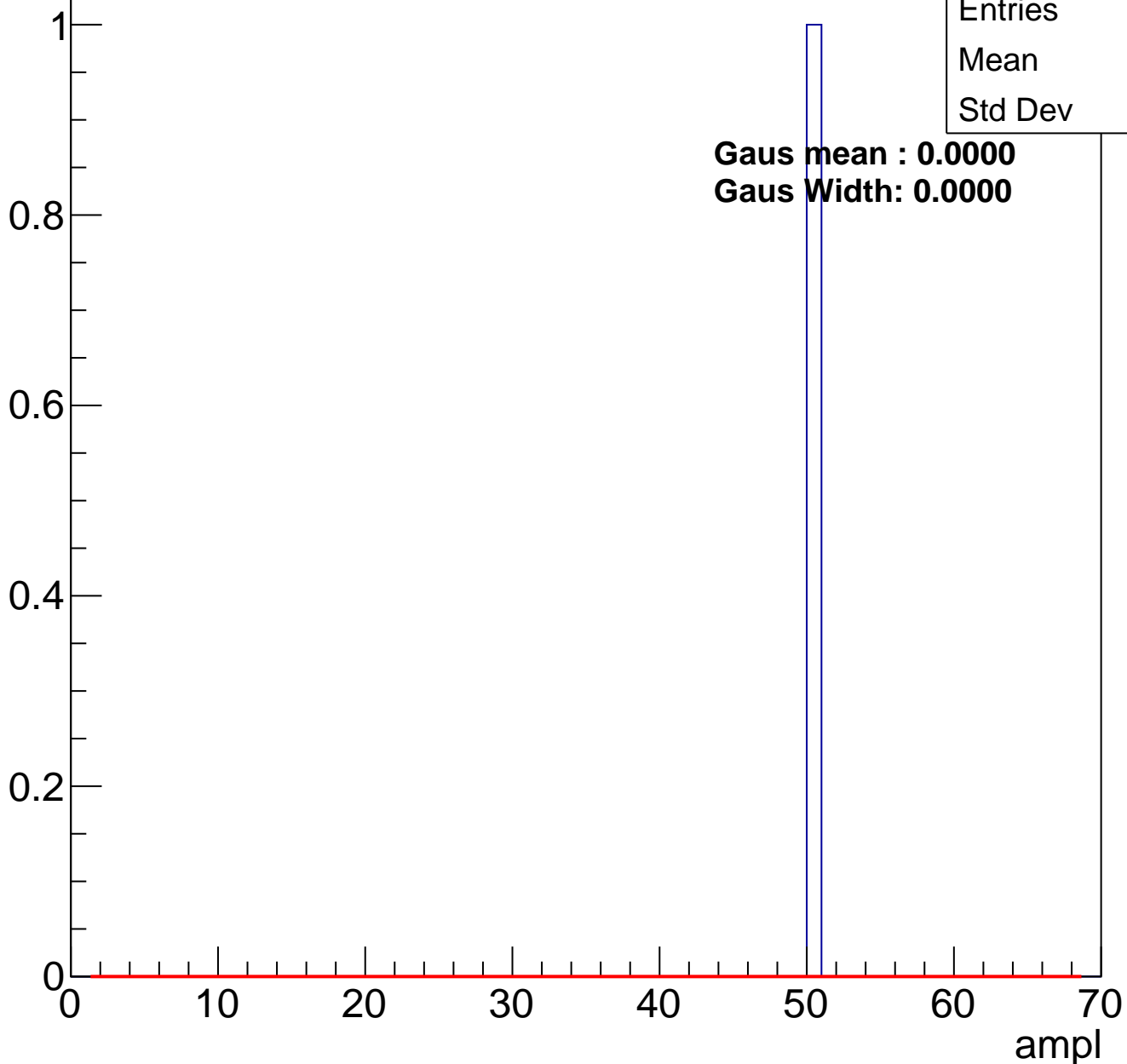
# B0L100S, U1-ch119, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**
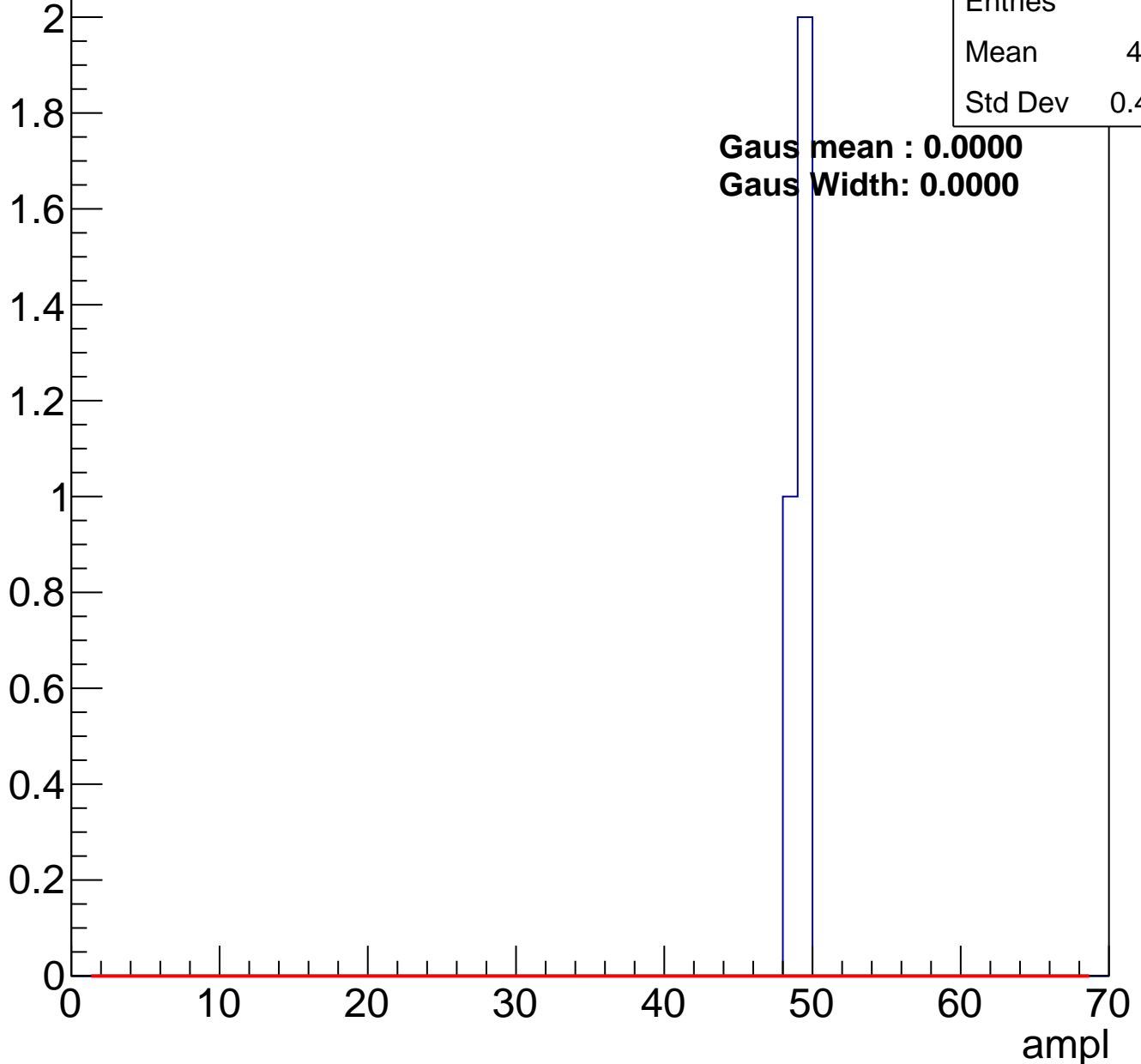
| | |
|---|---|
| Entries | 3 |
| Mean | 48.67 |
| Std Dev | 0.4714 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch119, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**
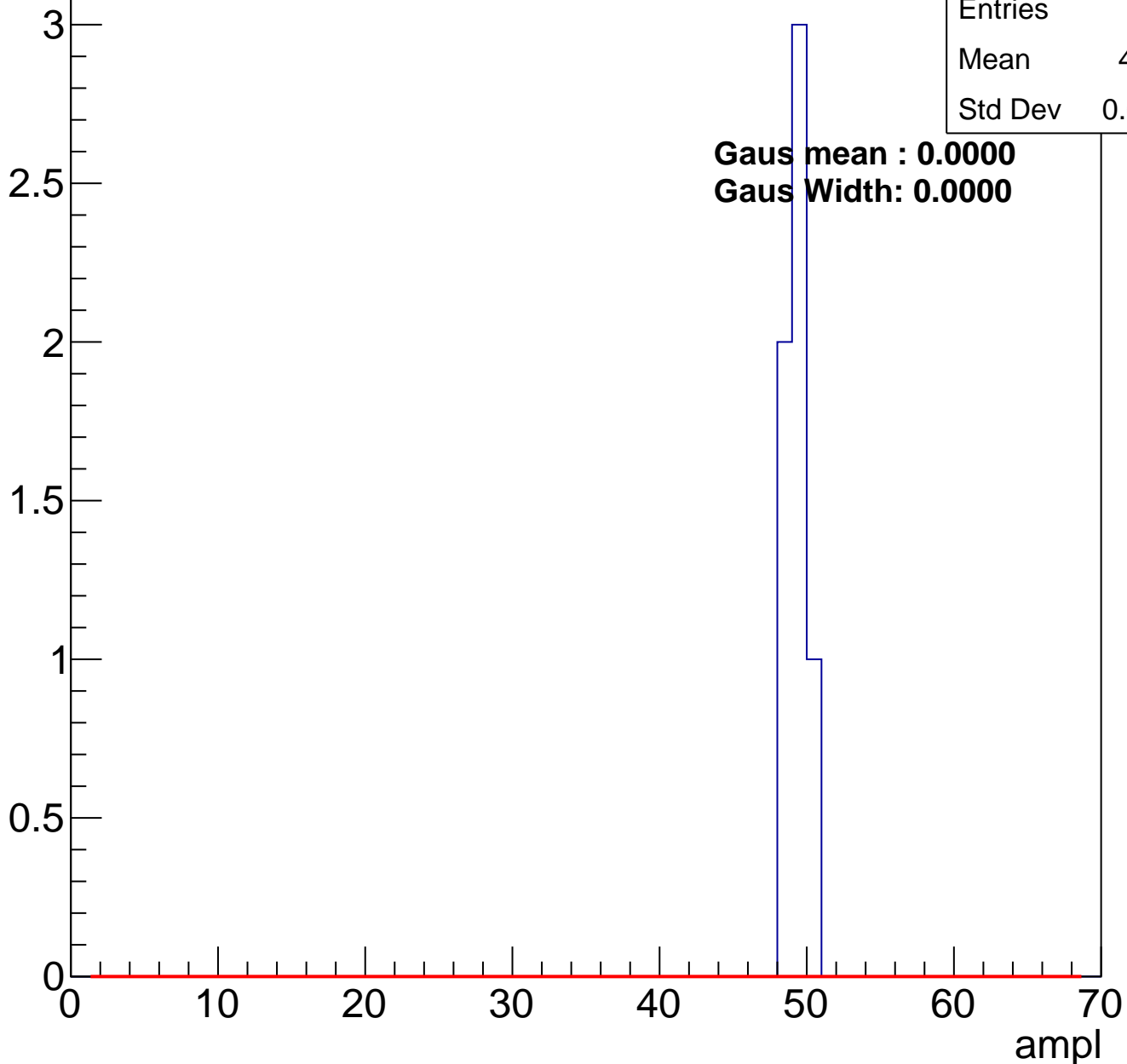
| | |
|---|---|
| Entries | 6 |
| Mean | 48.83 |
| Std Dev | 0.6872 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch119, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch119, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch119, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

ampl

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch119, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch119, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch120, adc0

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

# B0L100S, U1-ch120, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 49 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch120, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

| Entries | 2 |
|---------|---|
| Mean | 46.5 |
| Std Dev | 0.5 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

ampl
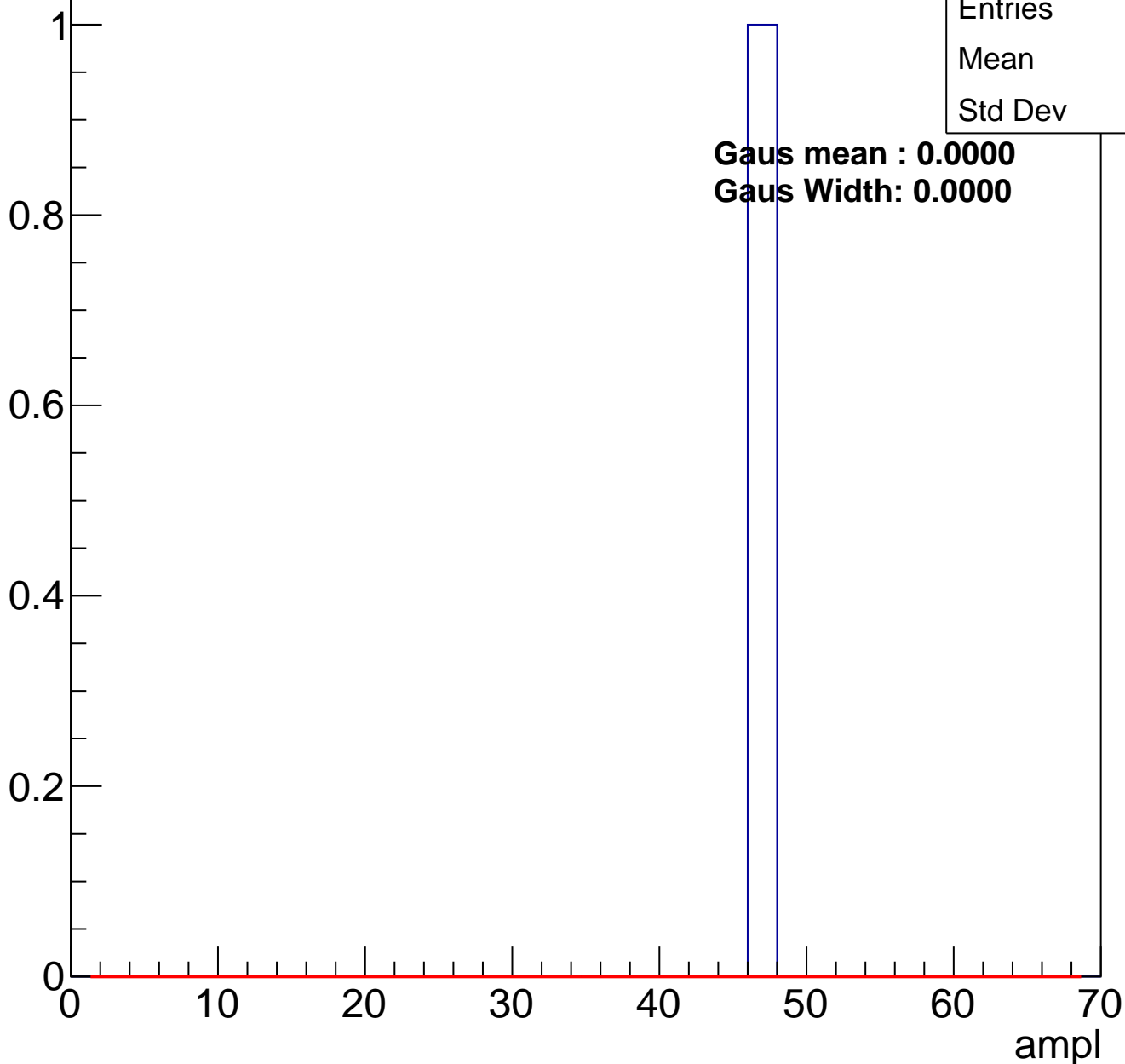
# B0L100S, U1-ch120, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch120, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch120, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch120, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch120, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch121, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch121, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch121, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch121, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch121, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch121, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch121, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch121, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch122, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

Entry

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch122, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

# B0L100S, U1-ch122, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch122, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch122, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch122, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch122, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch122, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch123, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch123, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch123, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch123, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch123, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch123, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch123, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch123, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch124, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch124, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch124, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch124, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch124, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch124, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**
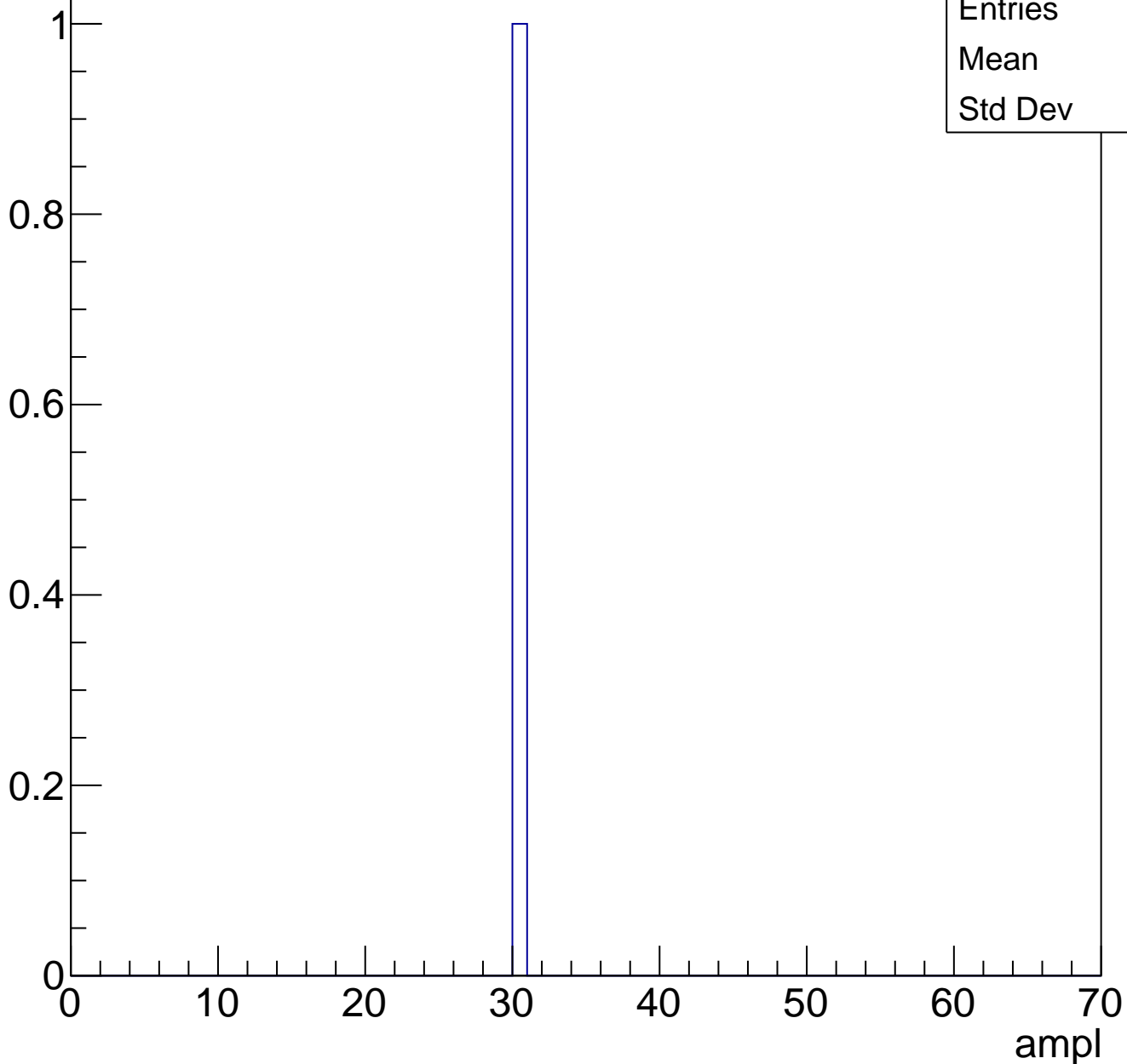


| Entries | 1 |
| --- | --- |
| Mean | 30 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch124, adc6

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch124, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch125, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
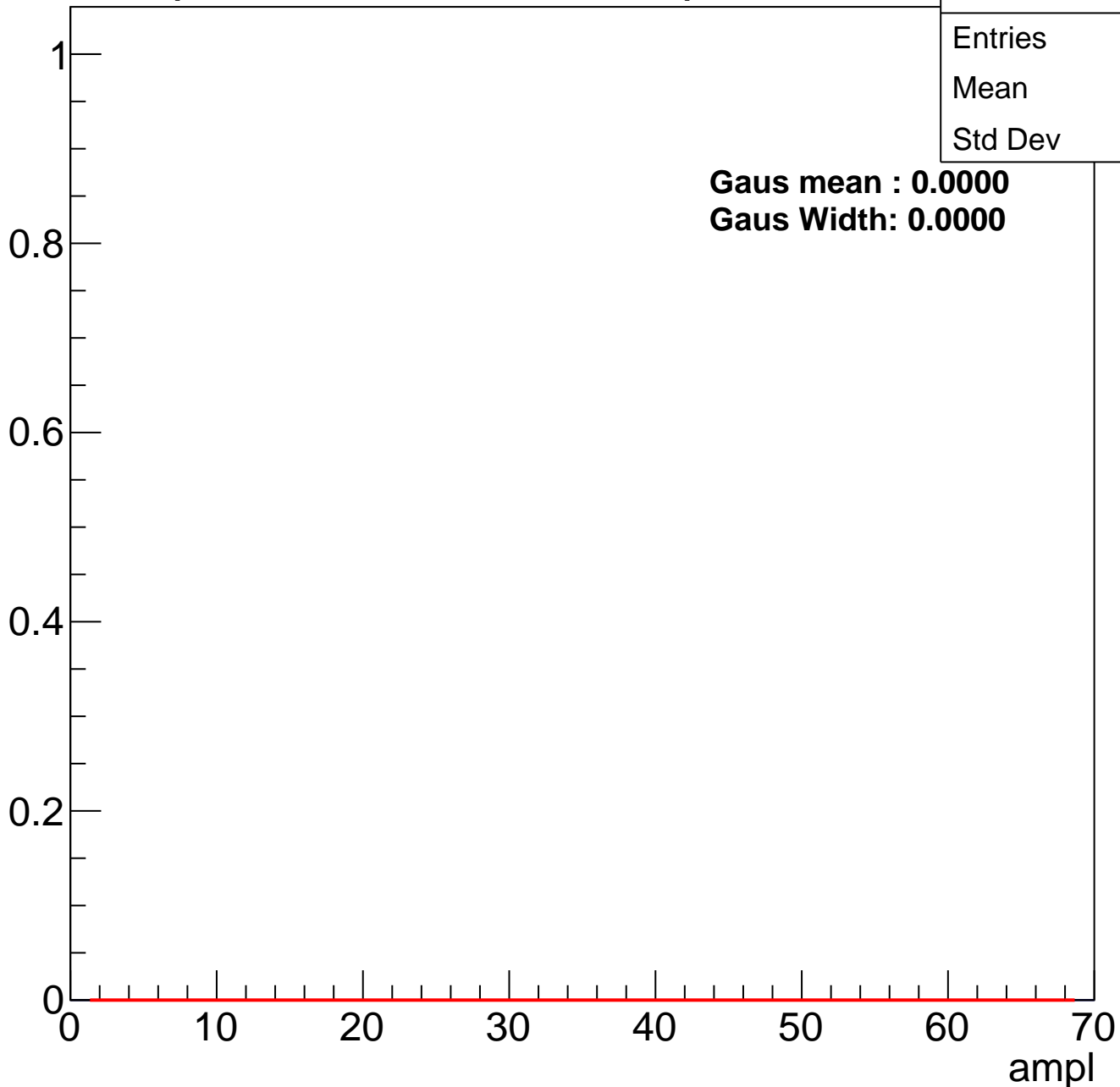
# B0L100S, U1-ch125, adc2

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch125, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch125, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch125, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch125, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

# B0L100S, U1-ch126, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch126, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

B0L100S, U1-ch126, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
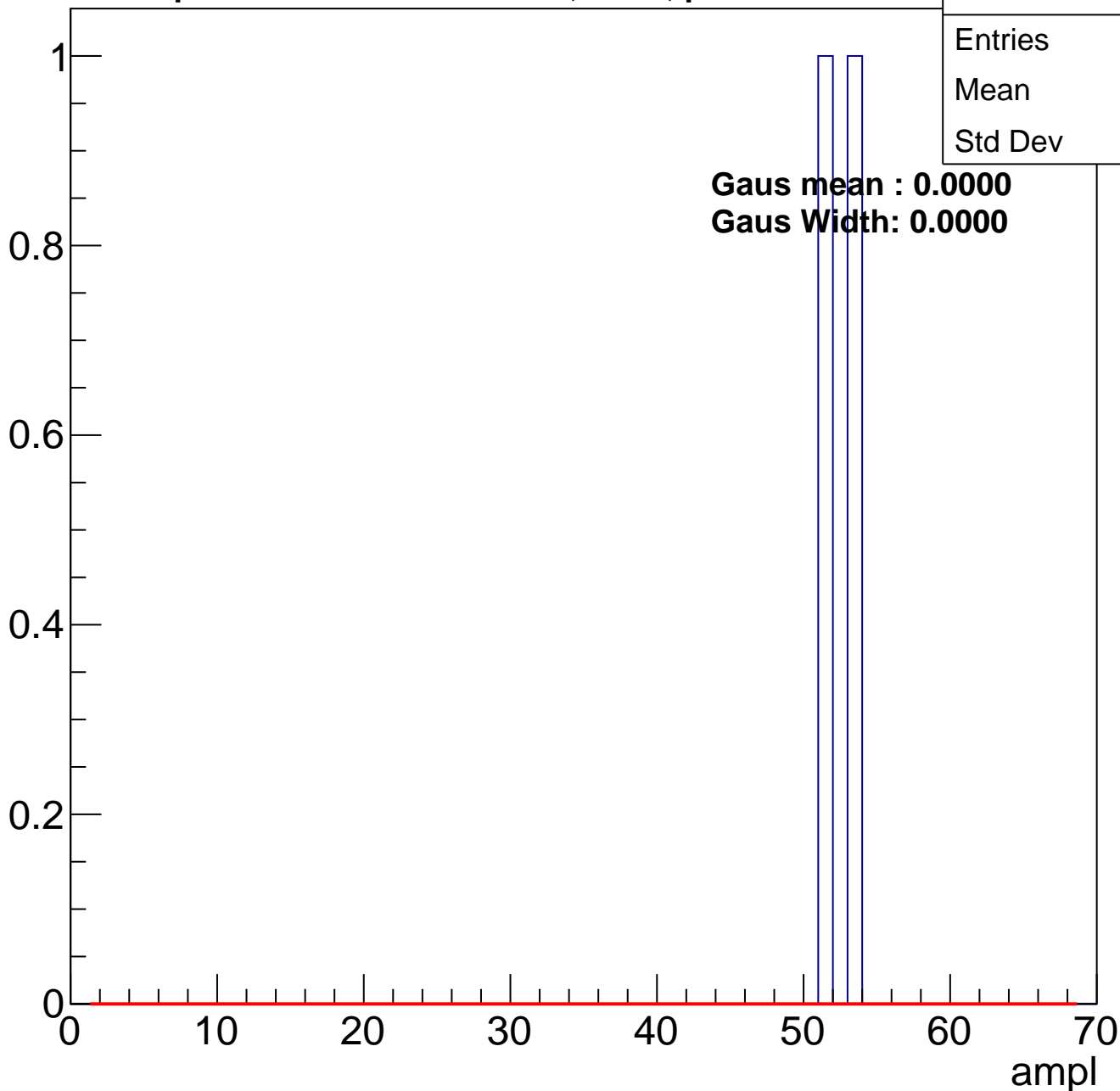
| Entries | 2 |
|---------|---|
| Mean | 52 |
| Std Dev | 1 |

# B0L100S, U1-ch126, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 1 |
| Mean | 53 |
| Std Dev | 0 |

ampl
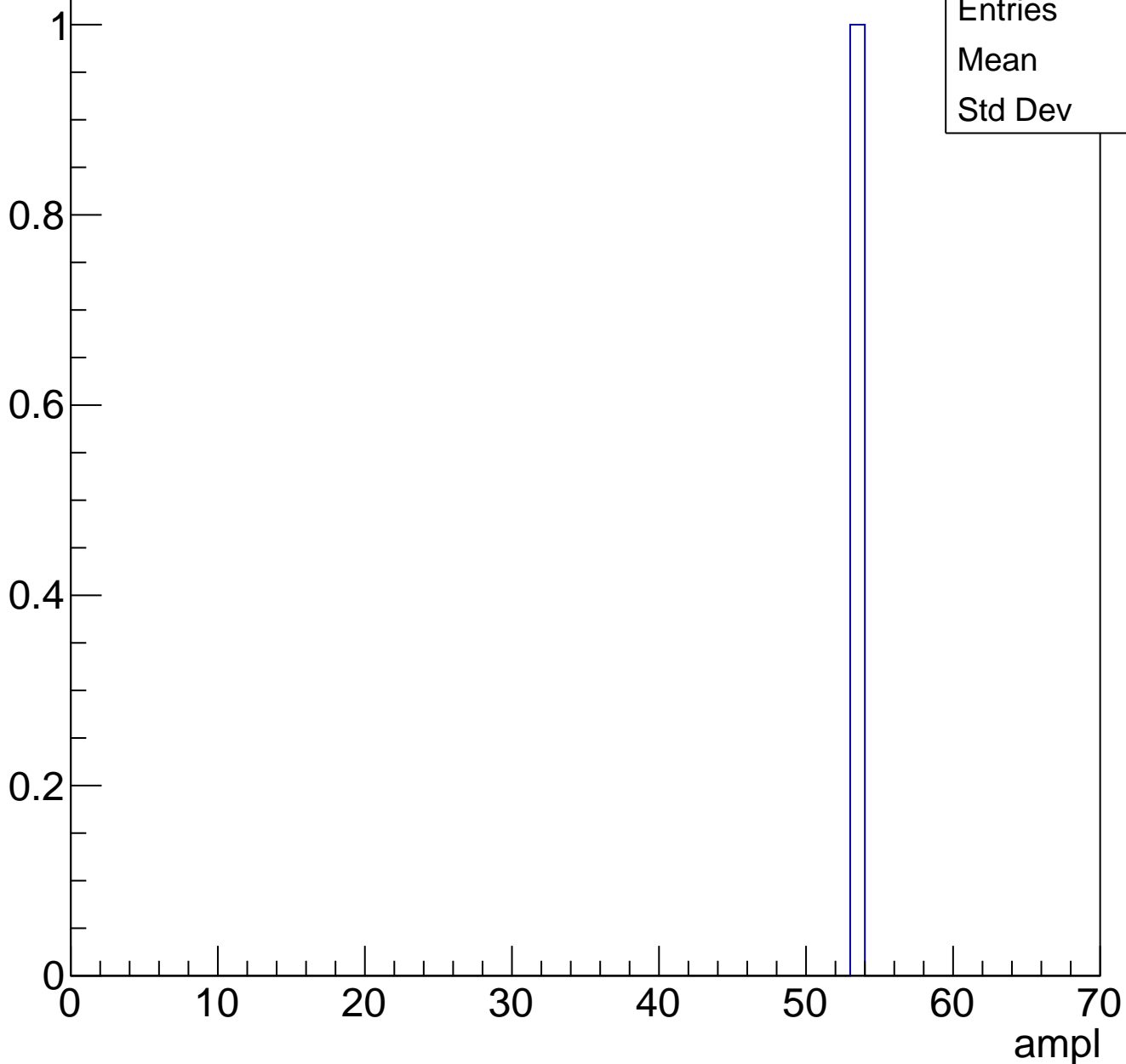
# B0L100S, U1-ch126, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**



| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

B0L100S, U1-ch126, adc5

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch126, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl
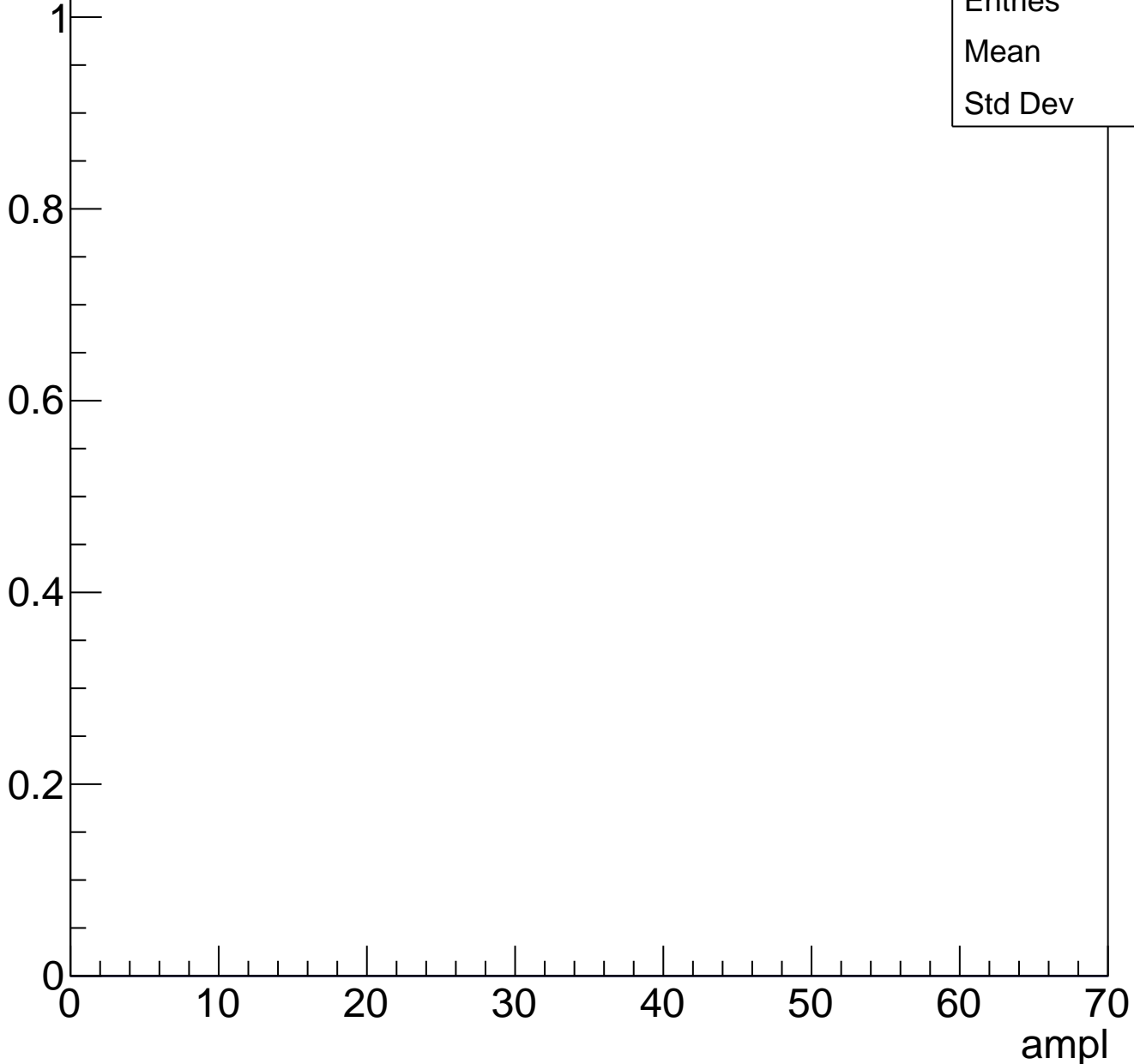
# B0L100S, U1-ch126, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch127, adc0

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl

# B0L100S, U1-ch127, adc1

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 1 |
|---------|---|
| Mean | 60 |
| Std Dev | 0 |

**Gaus mean : 0.0000**
**Gaus Width: 0.0000**

Entry

ampl
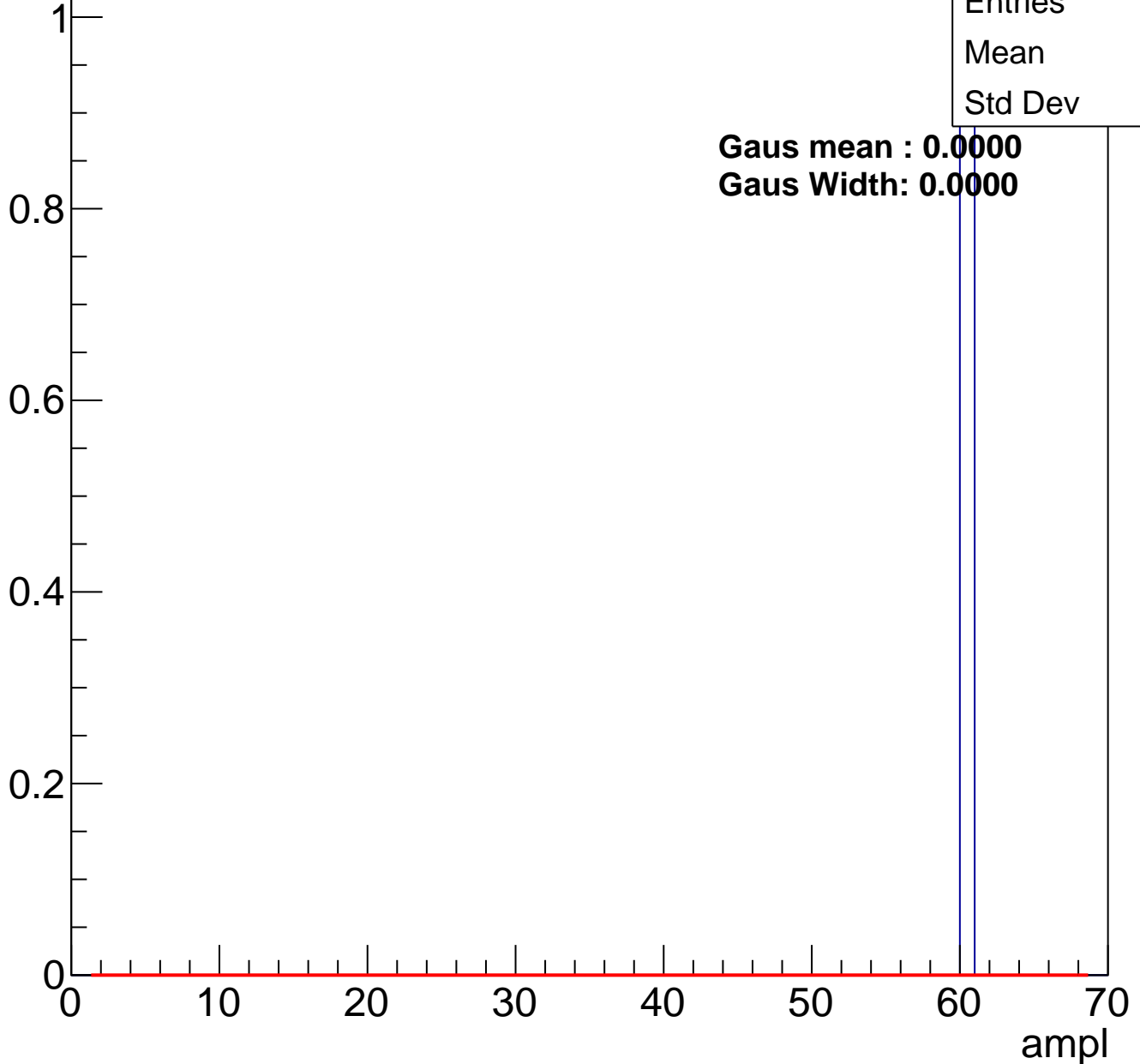
B0L100S, U1-ch127, adc2

calib_packv5_042523_0143.root, FC#6, port A1

| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

# B0L100S, U1-ch127, adc3

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch127, adc4

**calib_packv5_042523_0143.root, FC#6, port A1**

| Entries | 0 |
|---------|---|
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch127, adc5

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl

# B0L100S, U1-ch127, adc6

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L100S, U1-ch127, adc7

calib_packv5_042523_0143.root, FC#6, port A1

| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

# B0L100S, U1-ch127, adc7

**calib_packv5_042523_0143.root, FC#6, port A1**

| | |
|---|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

Entry

ampl