

B0L102S, U10-ch0

calib_packv5_042523_0143.root, FC#12, port B1

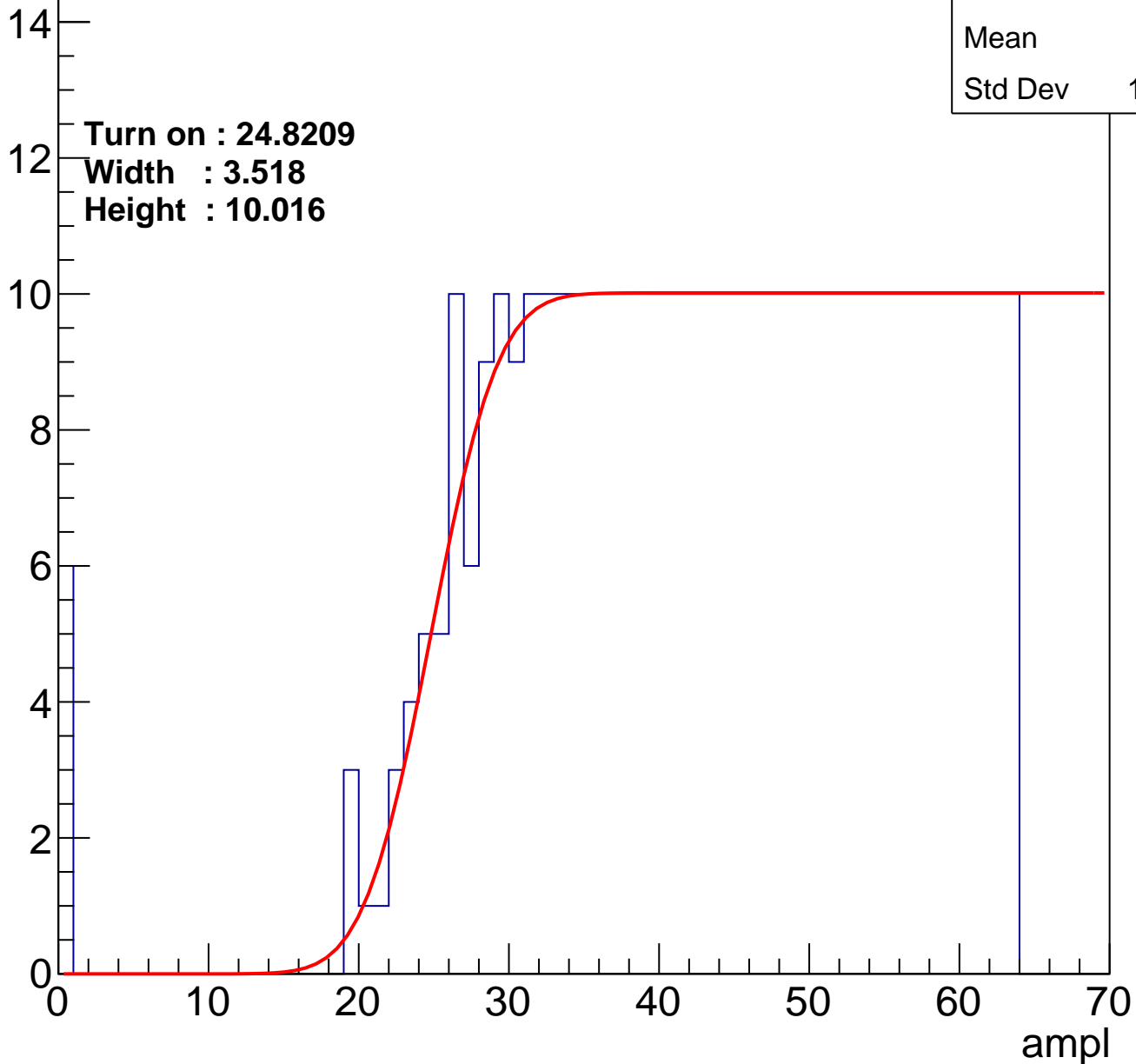
Entries	402
Mean	42.9
Std Dev	12.75

Turn on : 24.8209

Width : 3.518

Height : 10.016

Entry



B0L102S, U10-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.8
Std Dev	11.39

Turn on : 28.1932

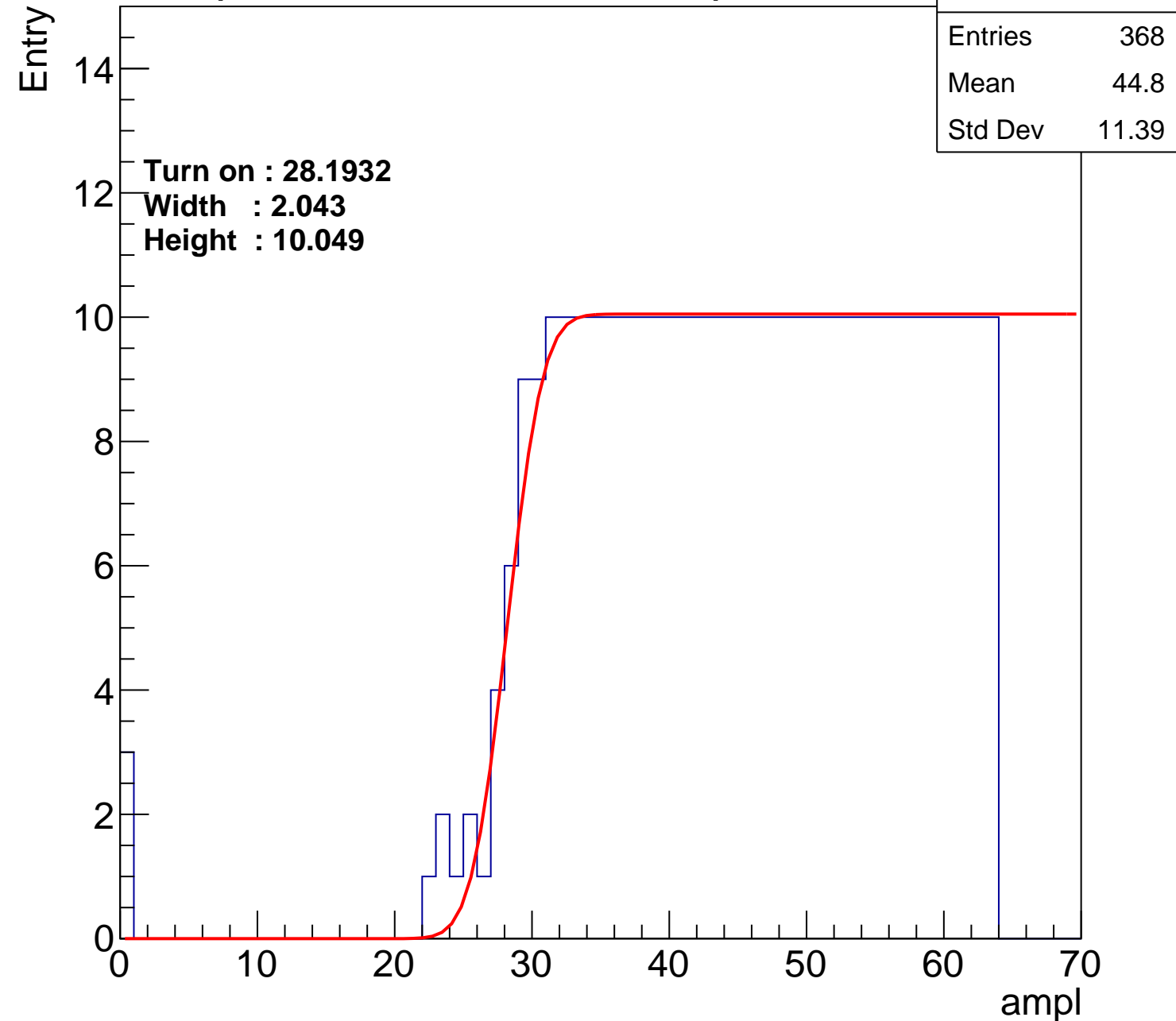
Width : 2.043

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.87
Std Dev	11.78

Turn on : 26.0123

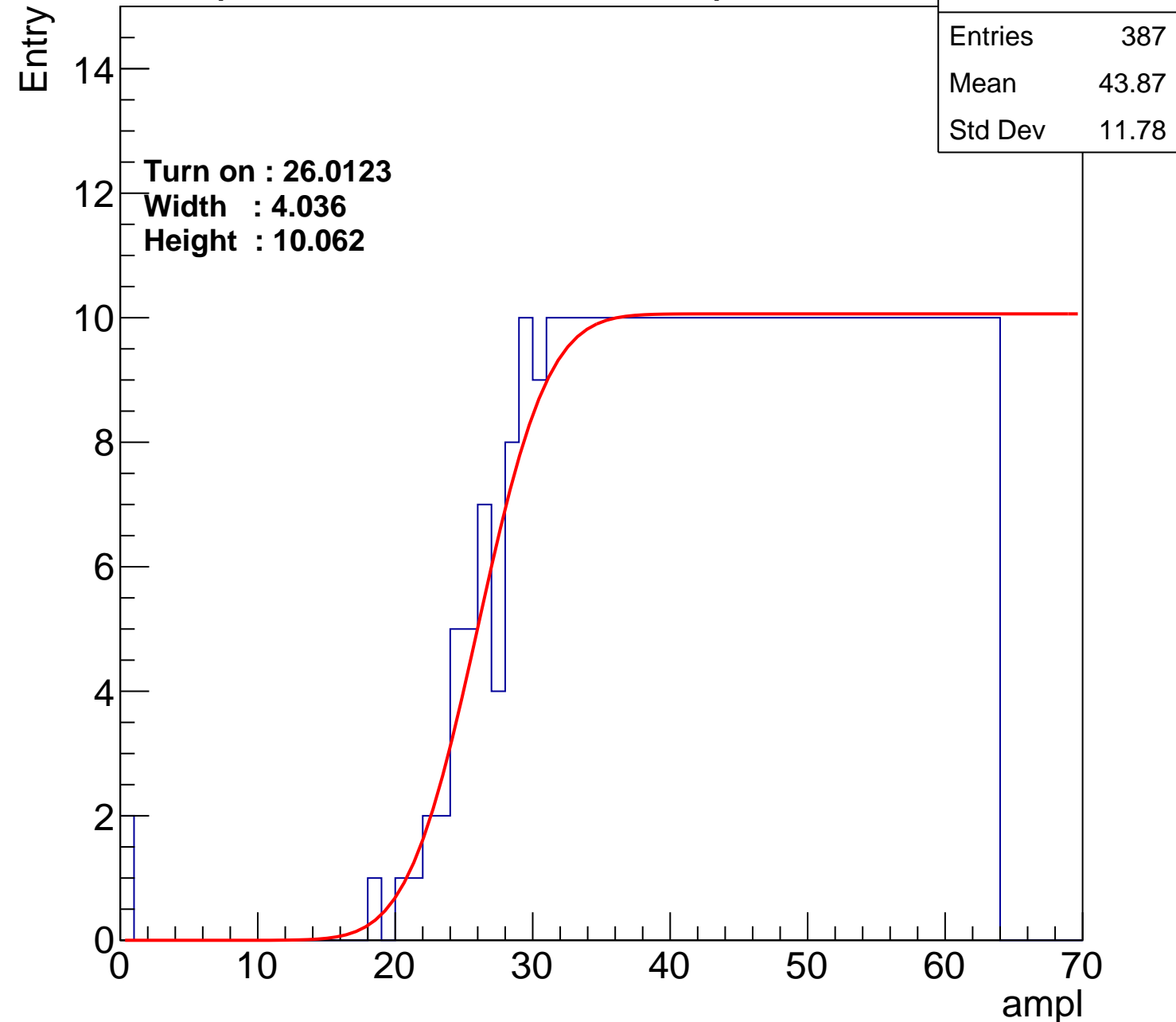
Width : 4.036

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
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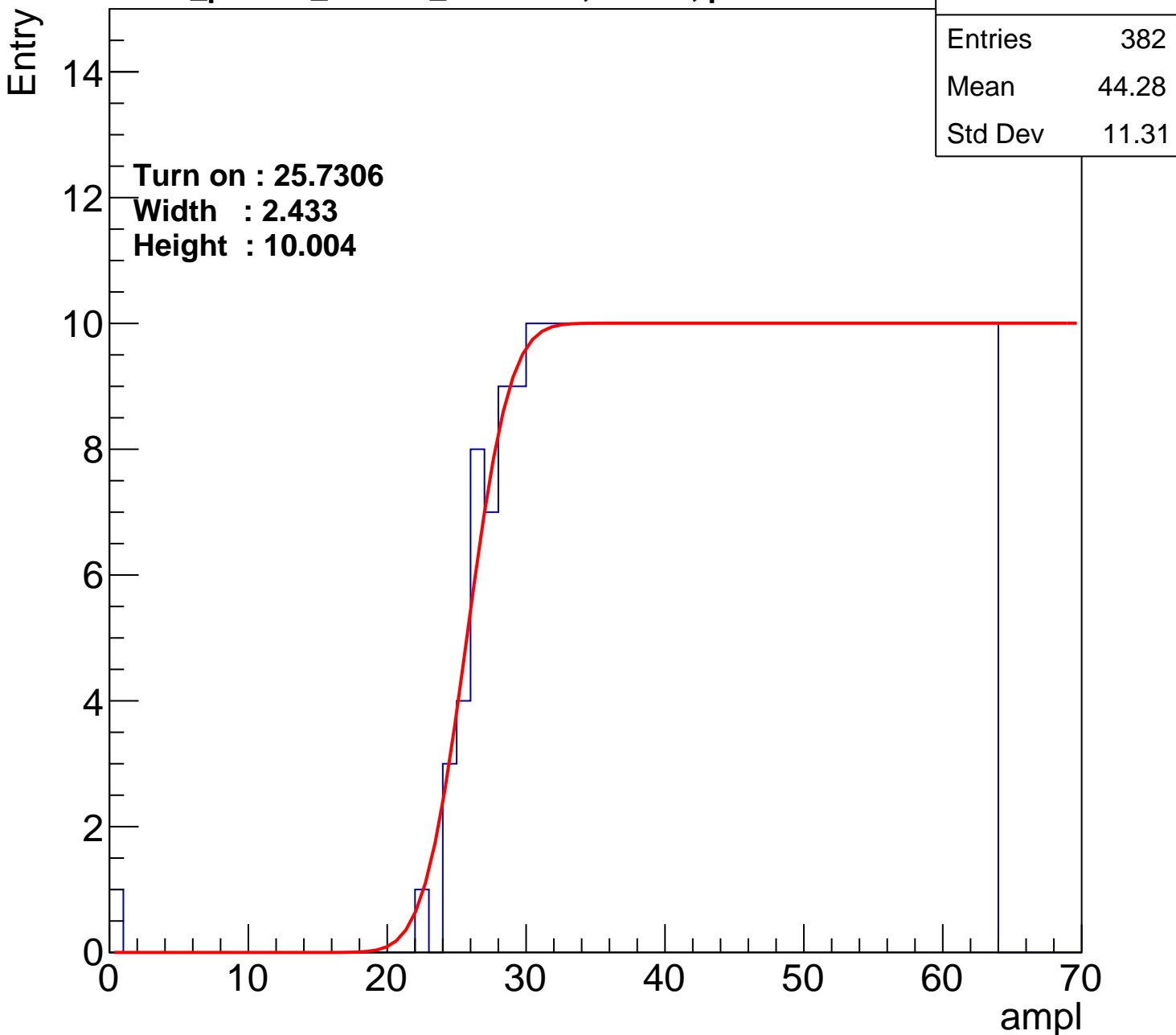
Mean	44.28
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Std Dev	11.31
---------	-------

Turn on : 25.7306

Width : 2.433

Height : 10.004



B0L102S, U10-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.37
Std Dev	11.94

Turn on : 27.8513

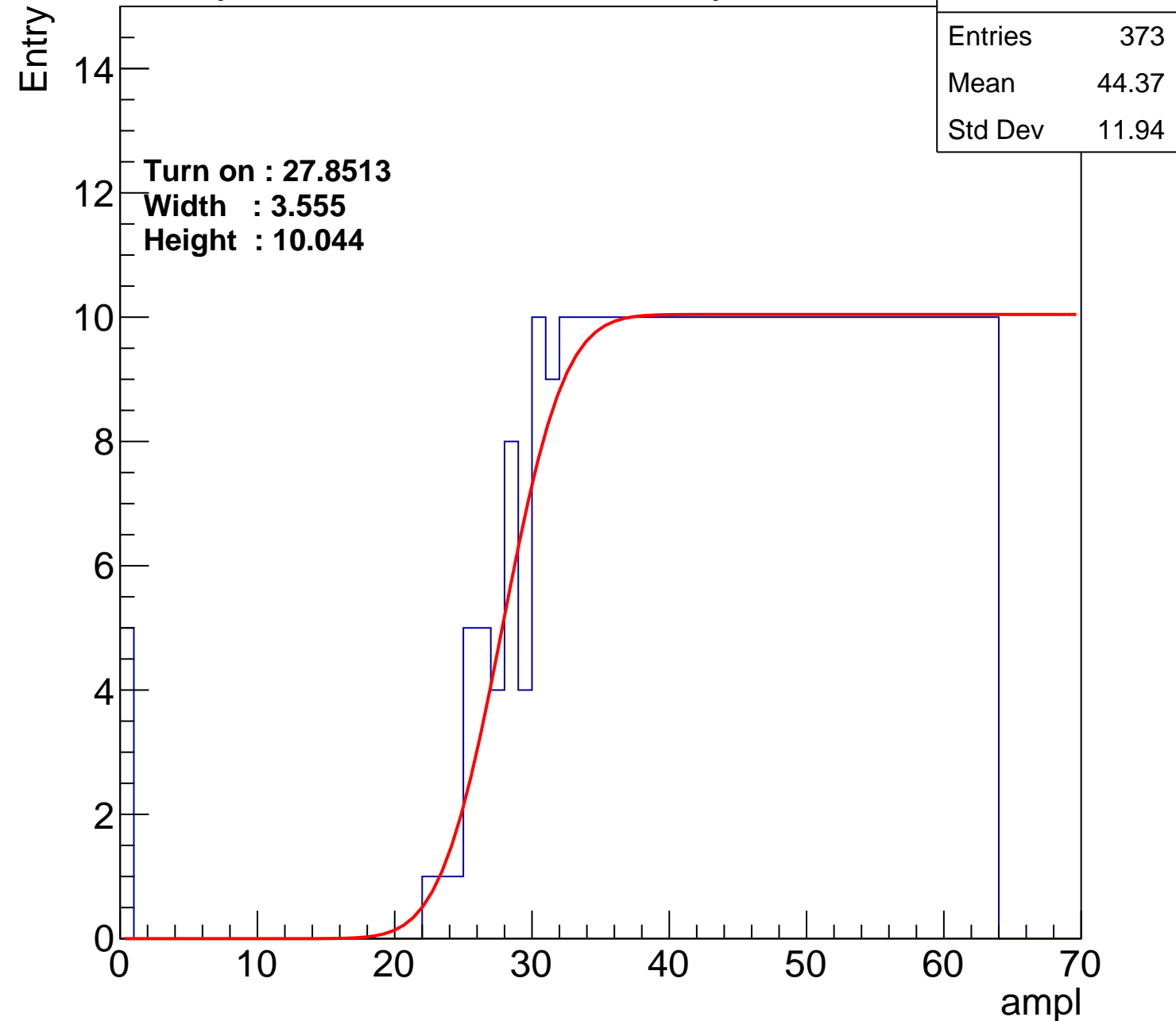
Width : 3.555

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.9872

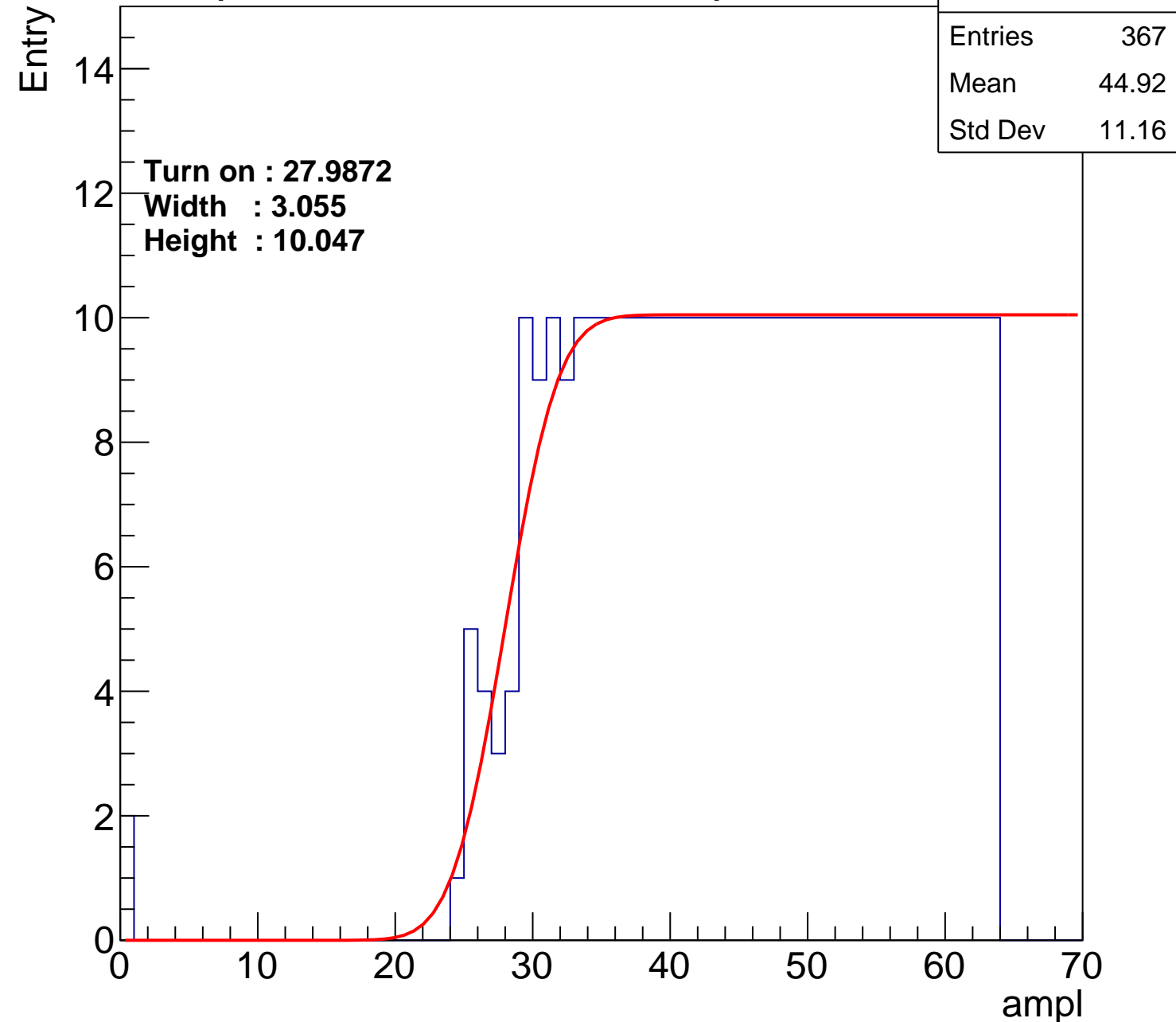
Width : 3.055

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.04
Std Dev	12.09

Turn on : 28.0112

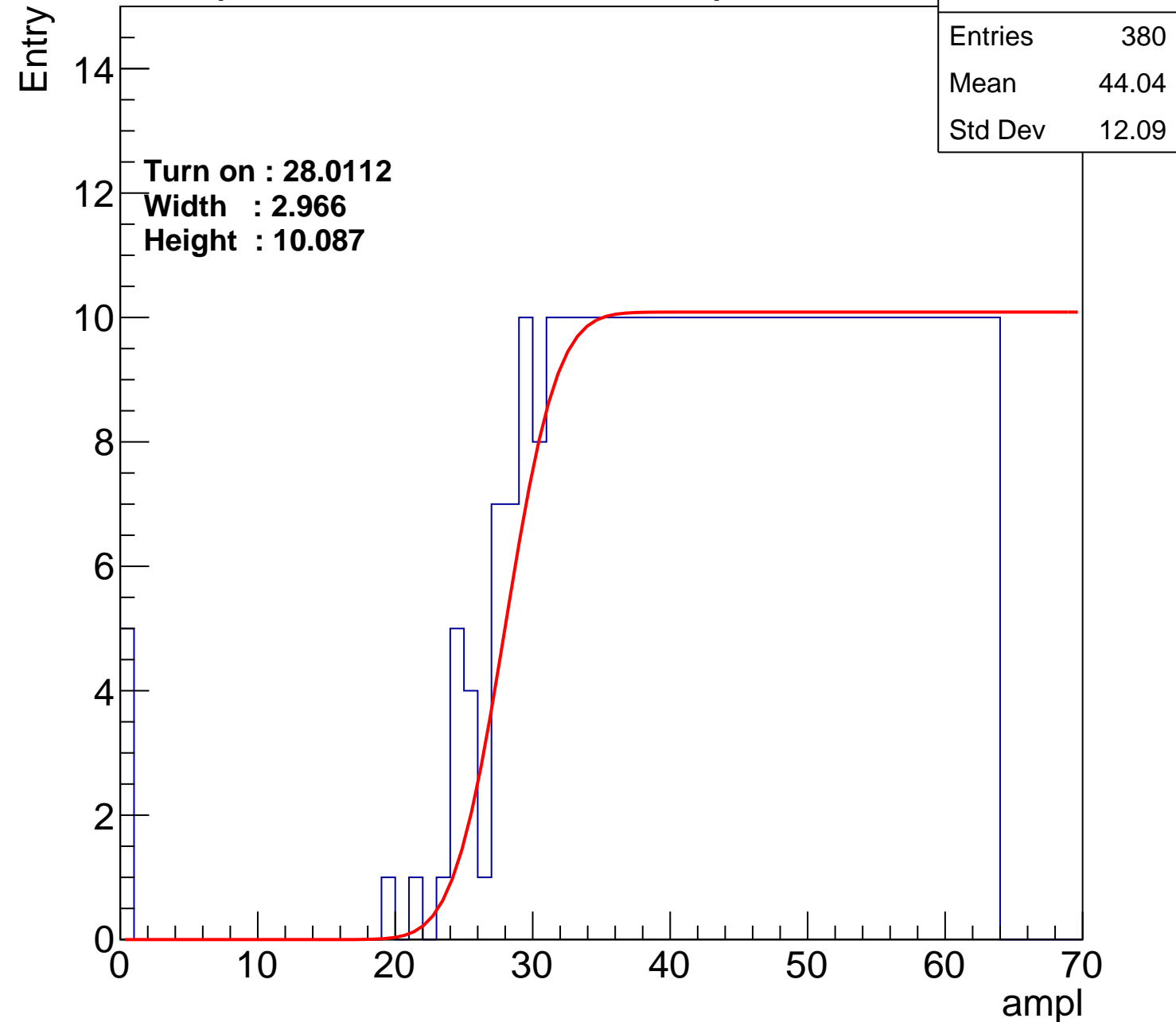
Width : 2.966

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.74
Std Dev	11.14

Turn on : 26.7474

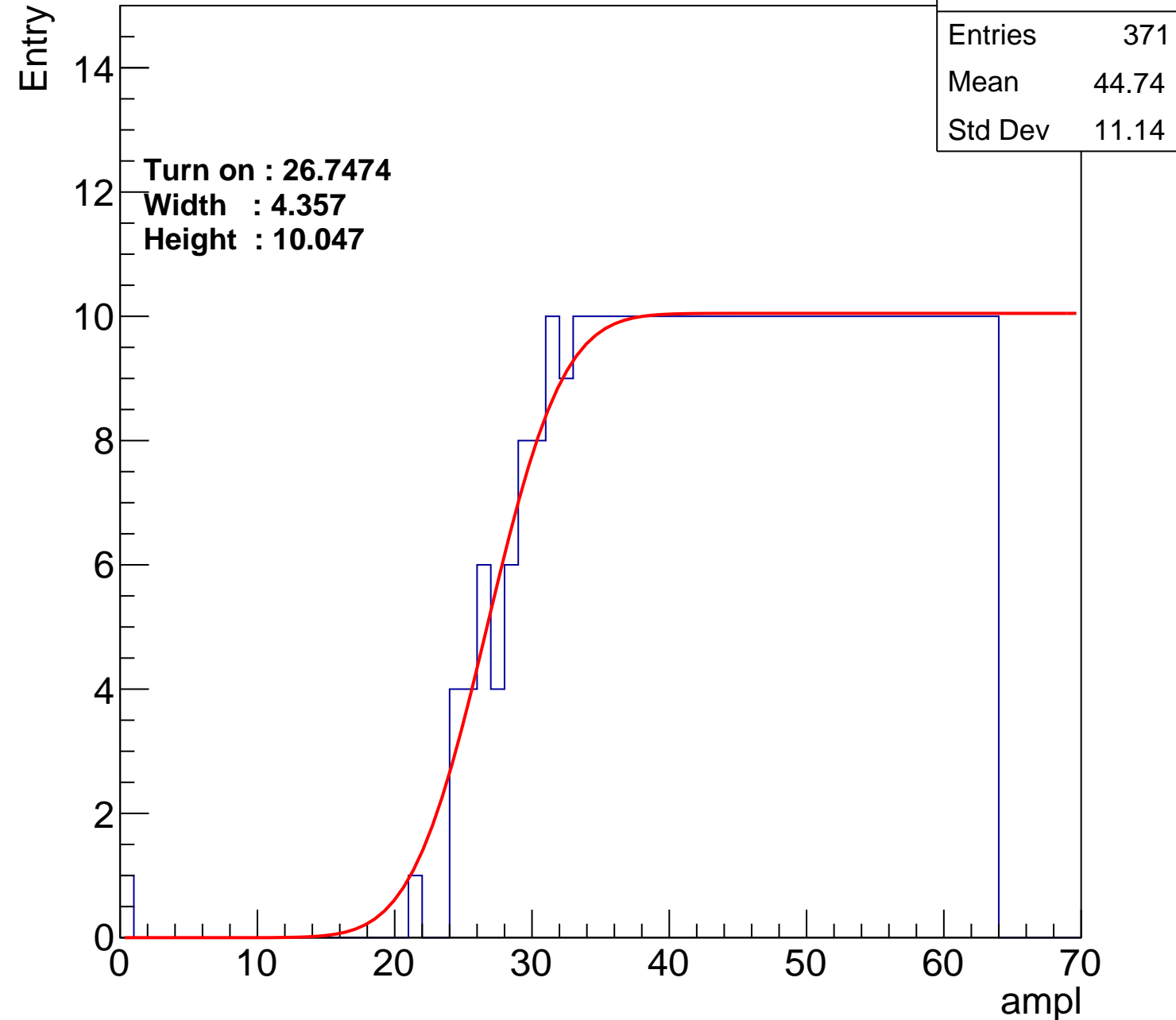
Width : 4.357

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.74
Std Dev	11.15

Turn on : 27.1320

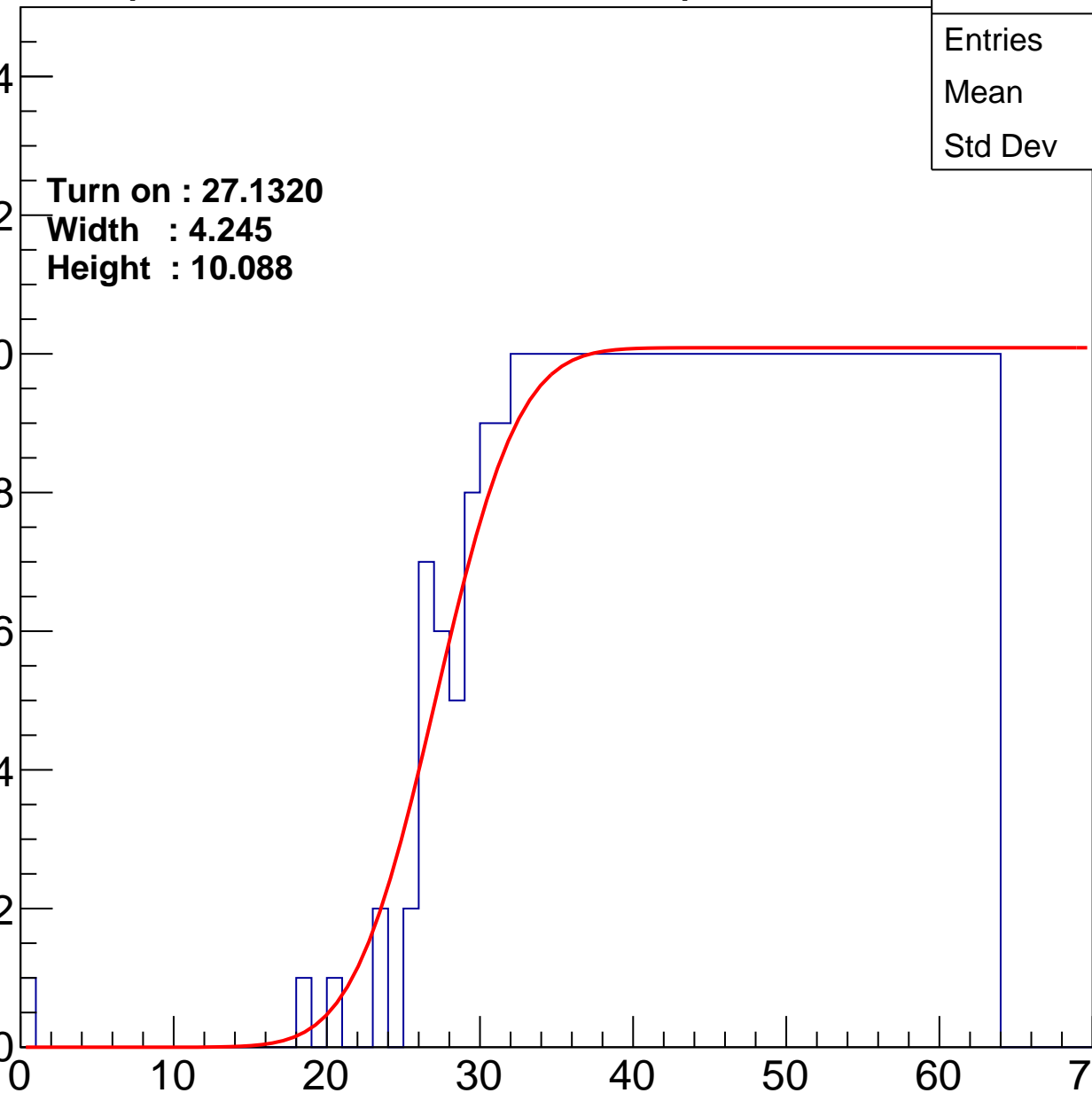
Width : 4.245

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.79
Std Dev	11.26

Turn on : 27.6253

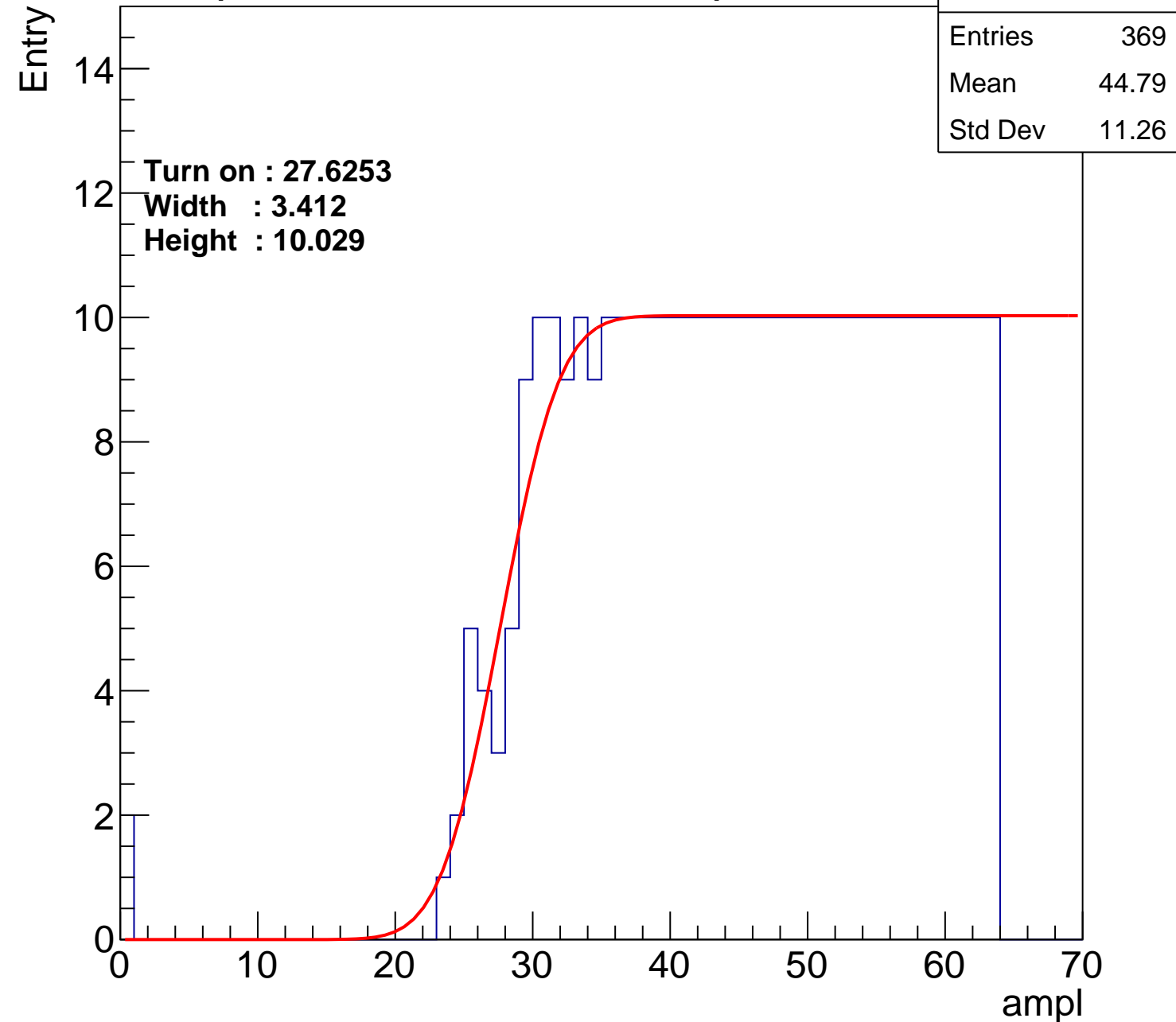
Width : 3.412

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.12
Std Dev	11.46

Turn on : 25.7322

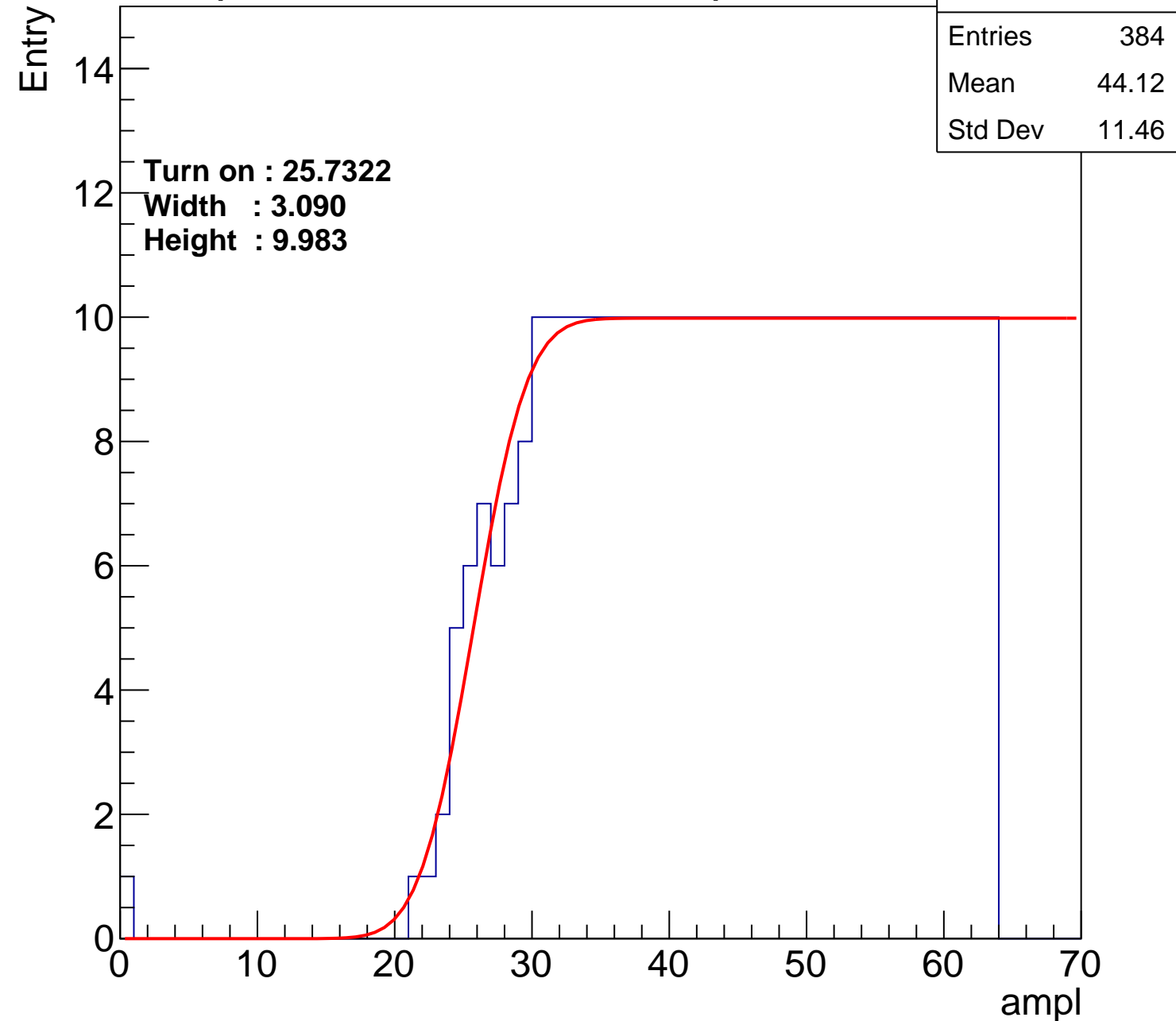
Width : 3.090

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.78
Std Dev	11.41

Turn on : 27.7404

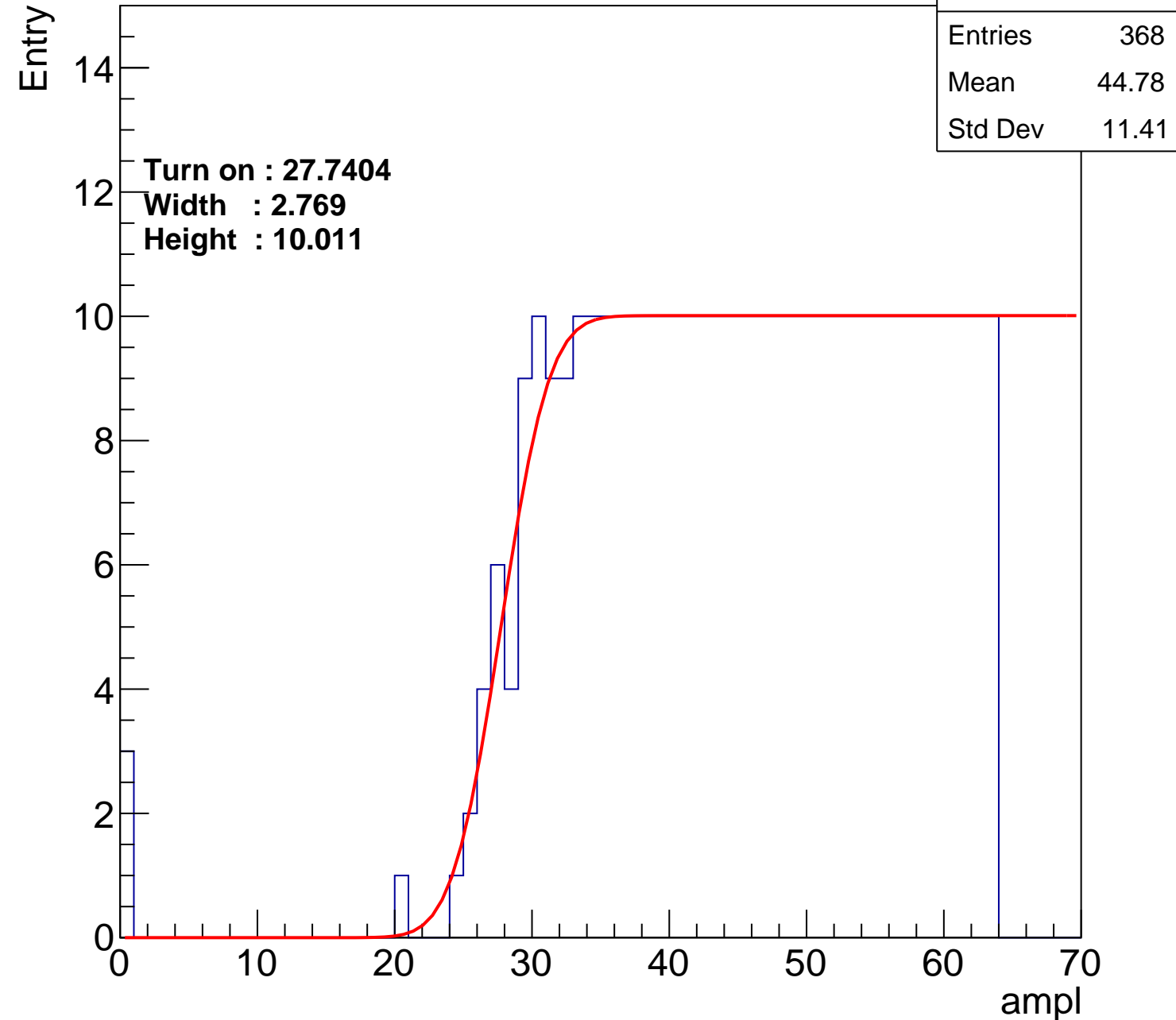
Width : 2.769

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.29
Std Dev	11.48

Turn on : 25.8938

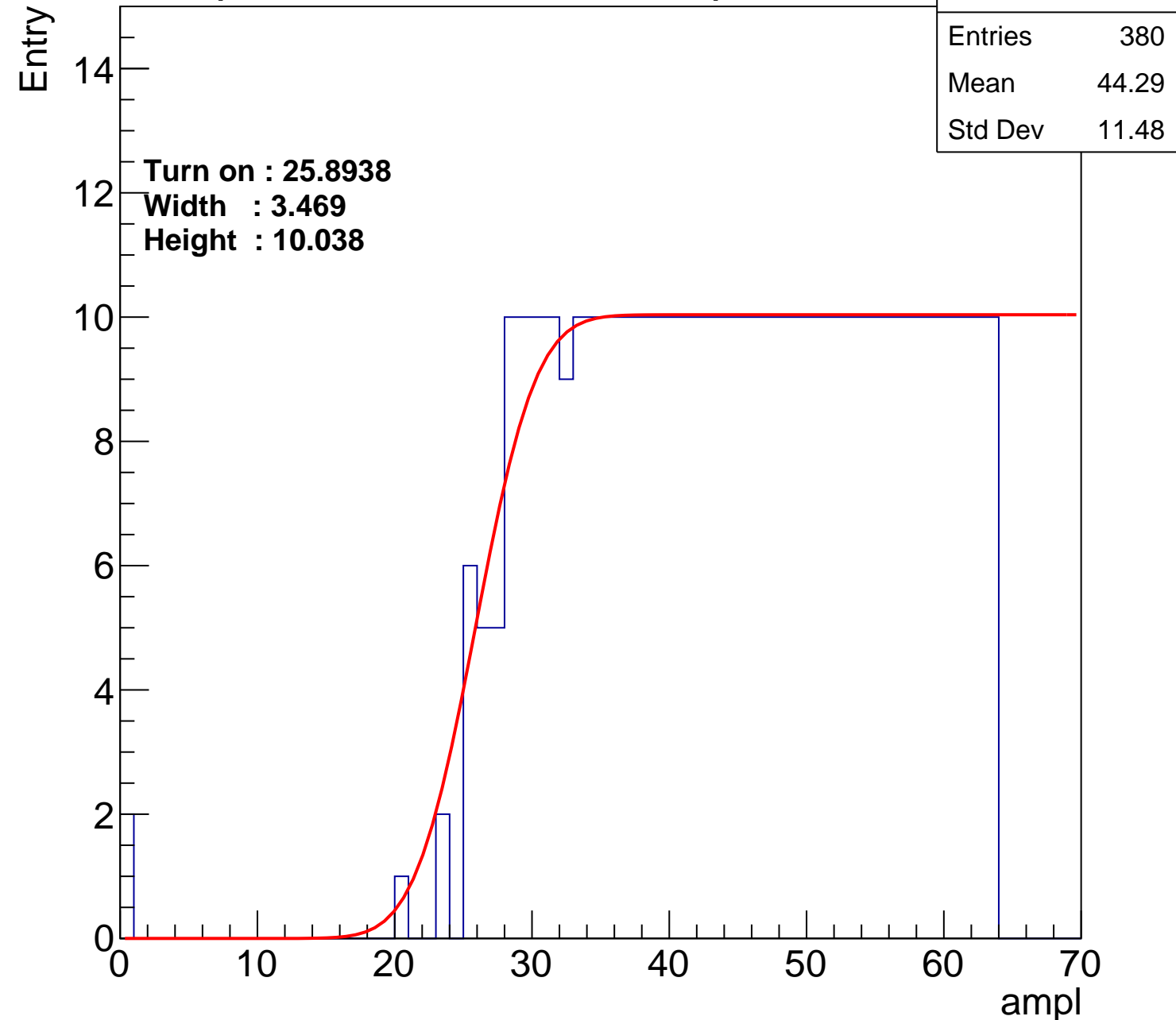
Width : 3.469

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.3
Std Dev	11.43

Turn on : 26.7944

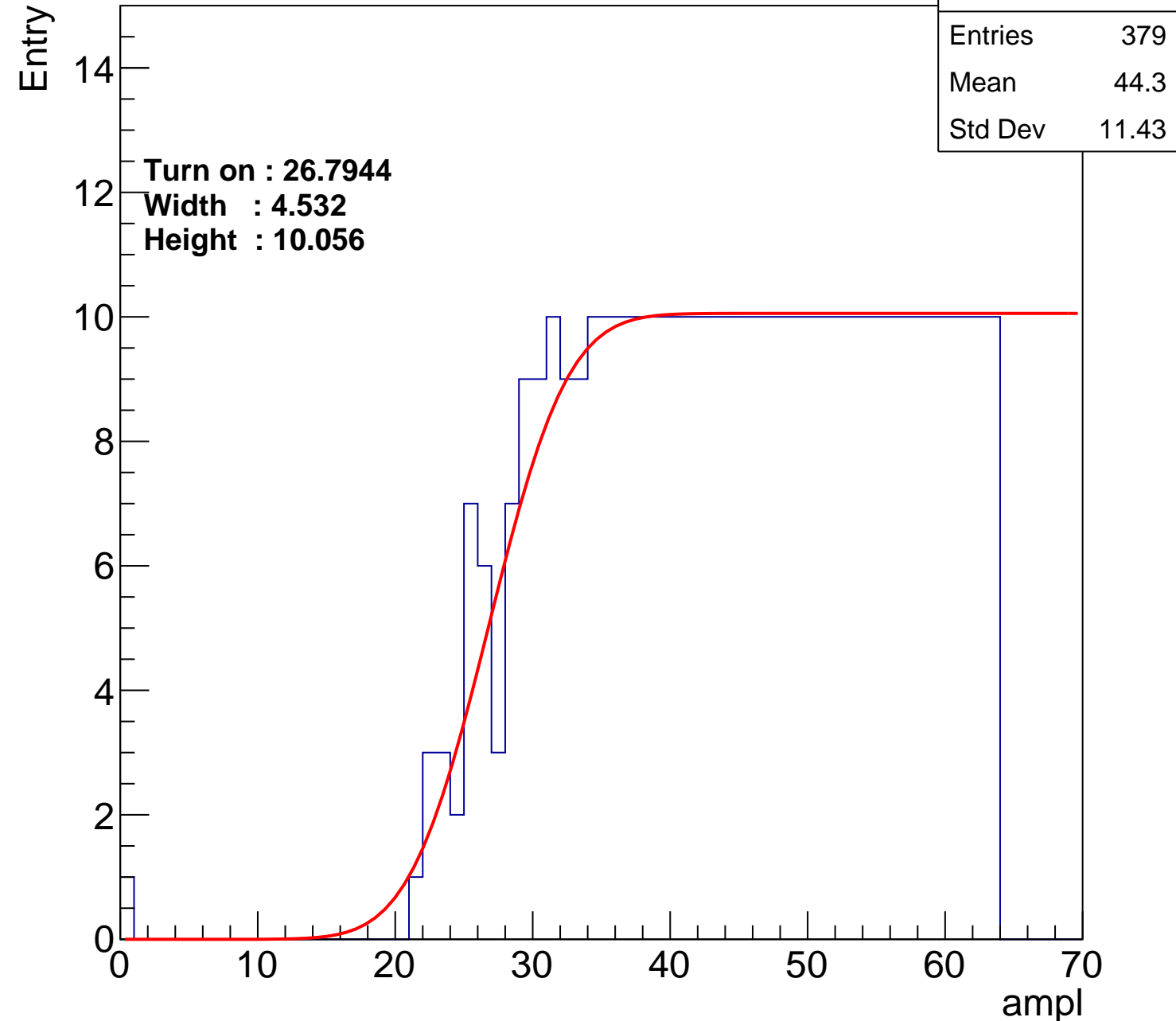
Width : 4.532

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.59
Std Dev	11.83

Turn on : 27.8726

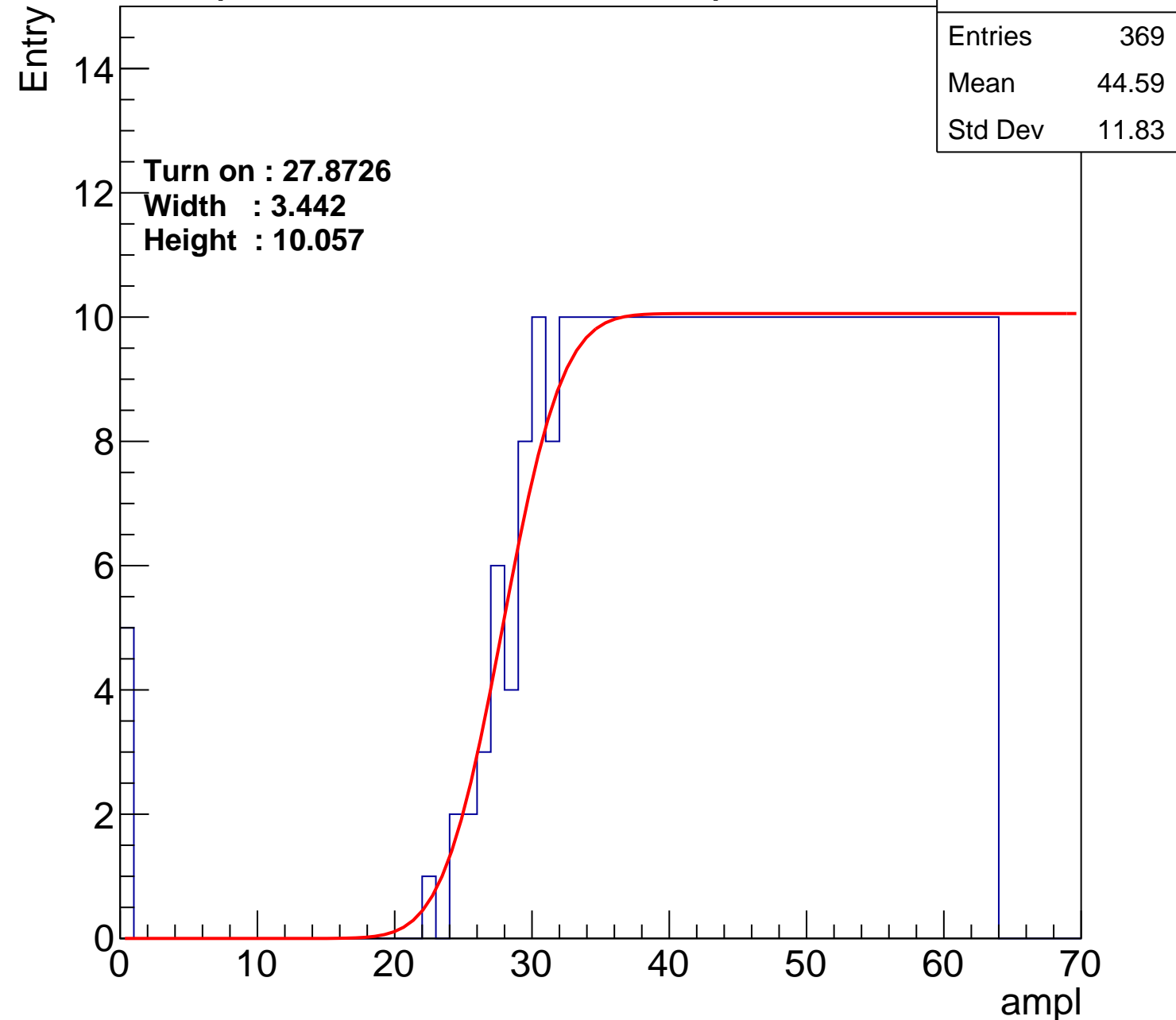
Width : 3.442

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch15

calib_packv5_042523_0143.root, FC#12, port B1

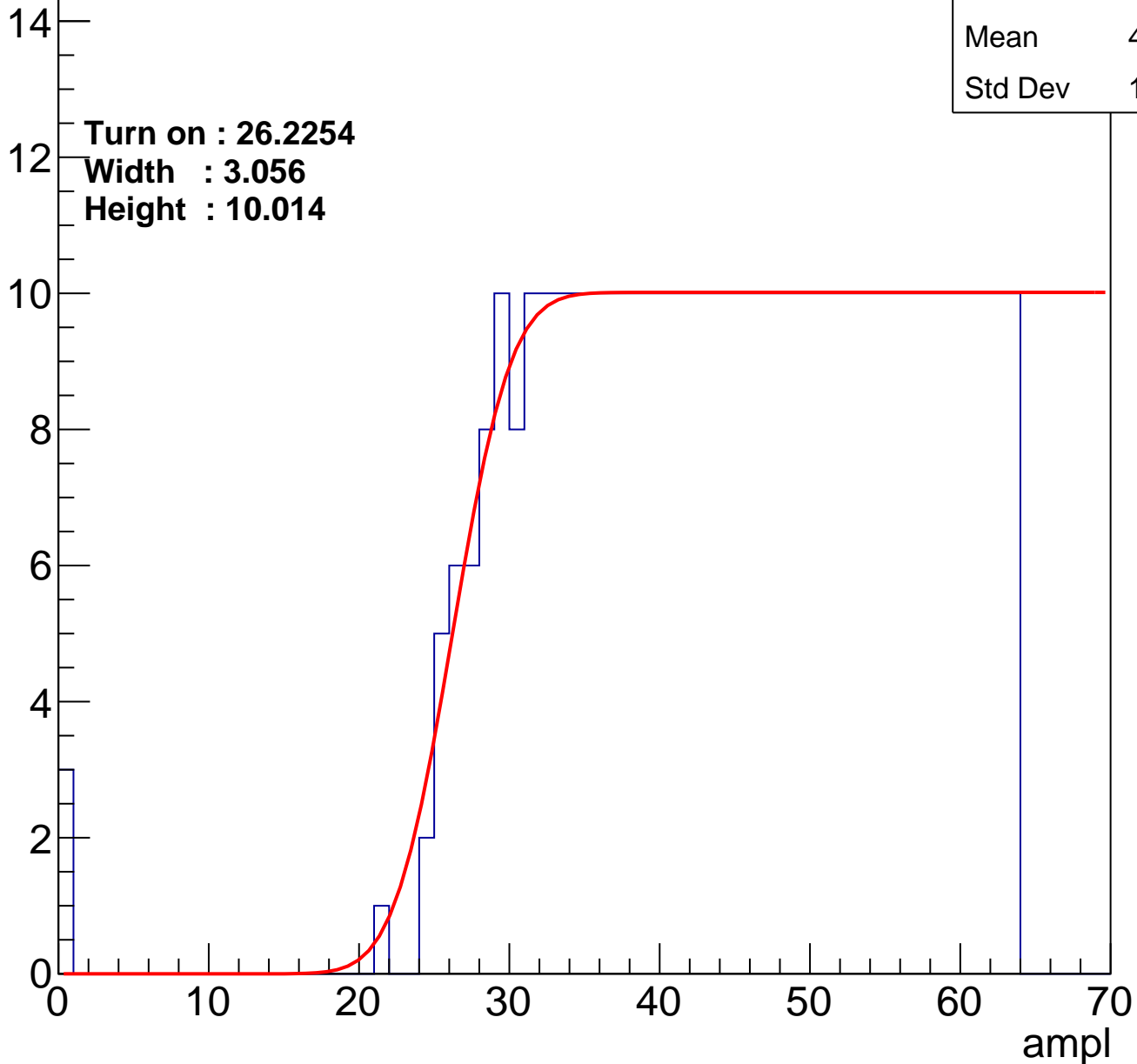
Entries	379
Mean	44.26
Std Dev	11.64

Turn on : 26.2254

Width : 3.056

Height : 10.014

Entry



B0L102S, U10-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.02
Std Dev	11.89

Turn on : 26.2943

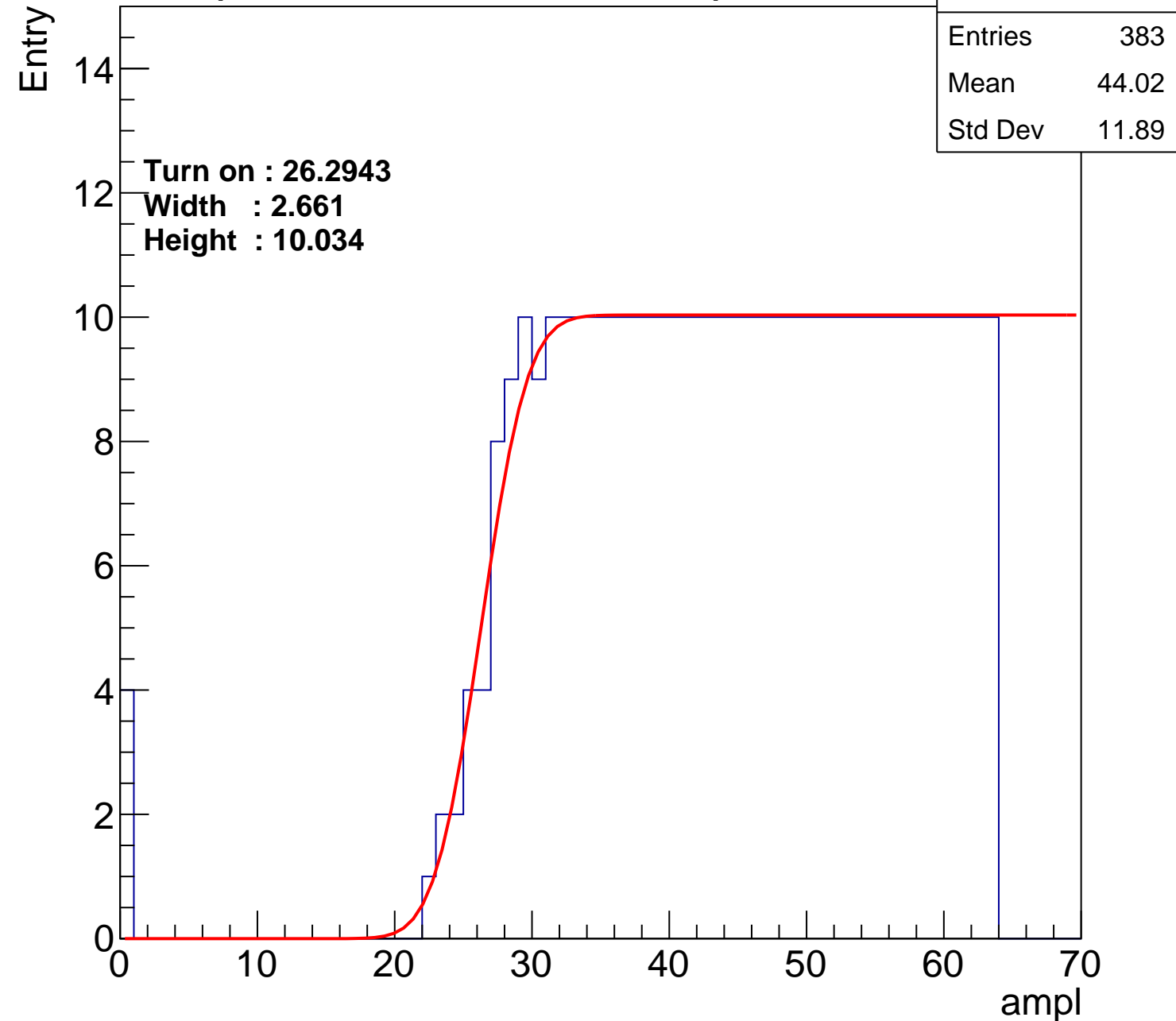
Width : 2.661

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch17

calib_packv5_042523_0143.root, FC#12, port B1

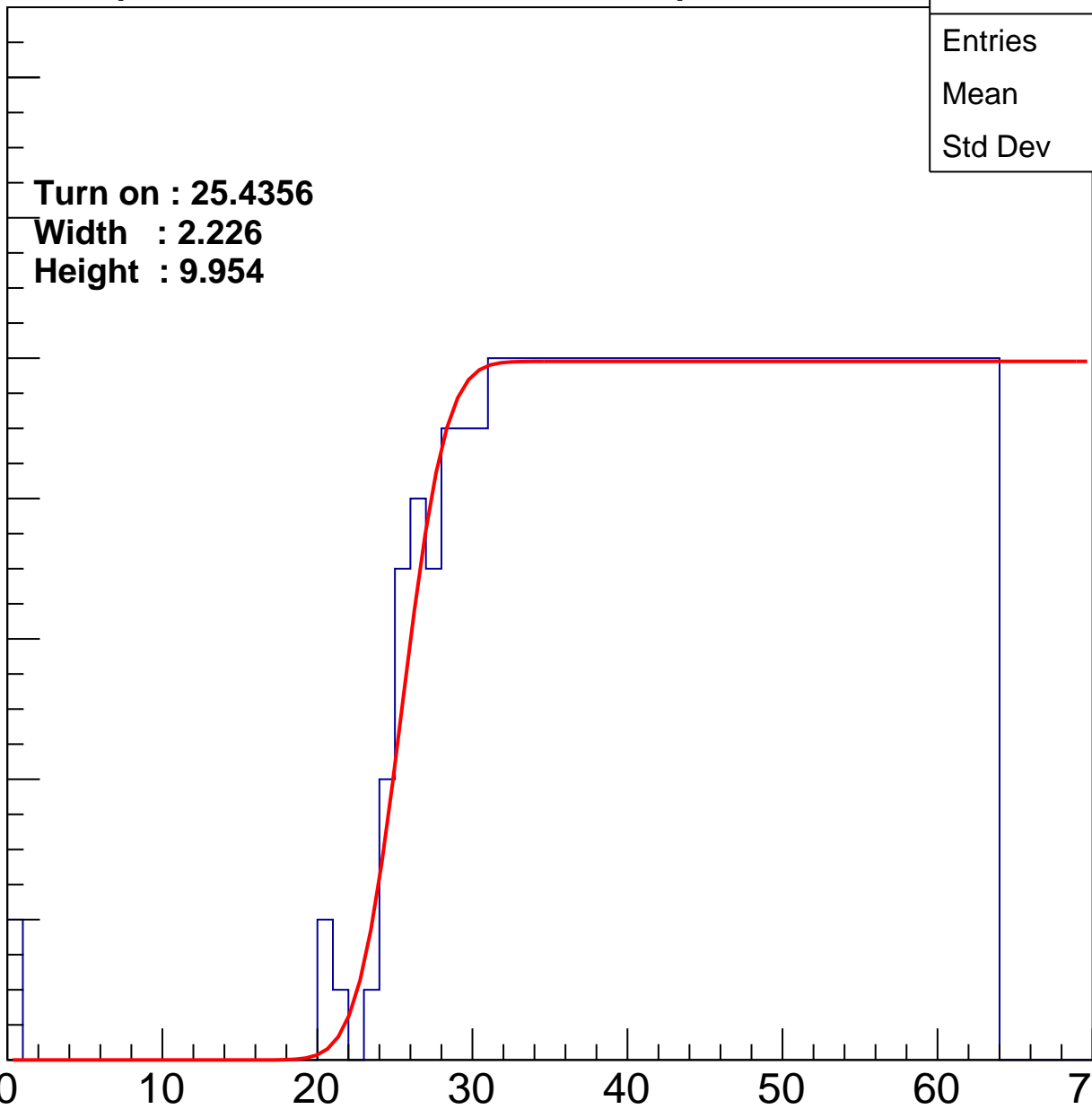
Entry

14
12
10
8
6
4
2
0

Turn on : 25.4356
Width : 2.226
Height : 9.954

Entries	389
Mean	43.82
Std Dev	11.75

ampl



B0L102S, U10-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 26.5078

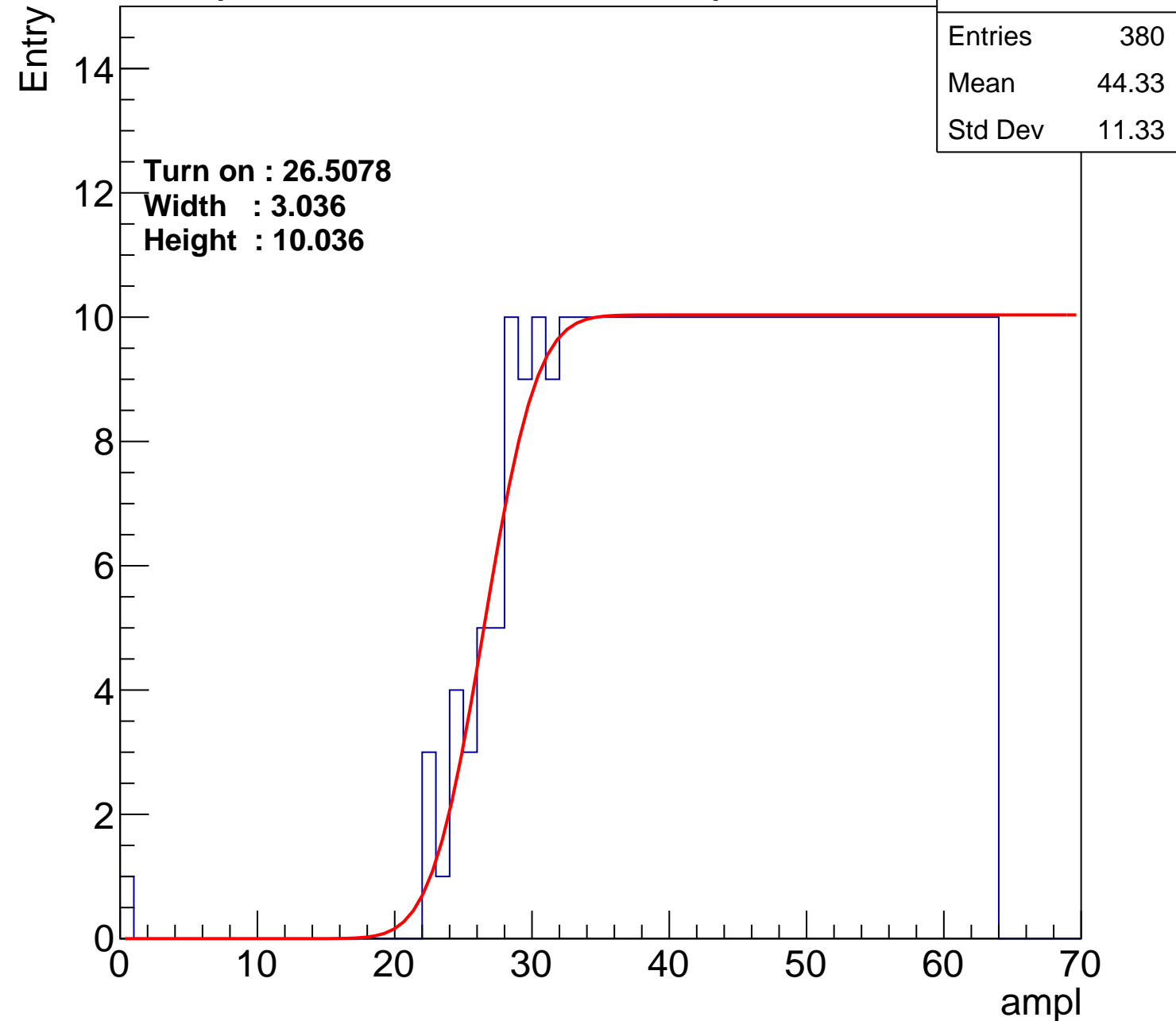
Width : 3.036

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.7
Std Dev	11.1

Turn on : 26.6215

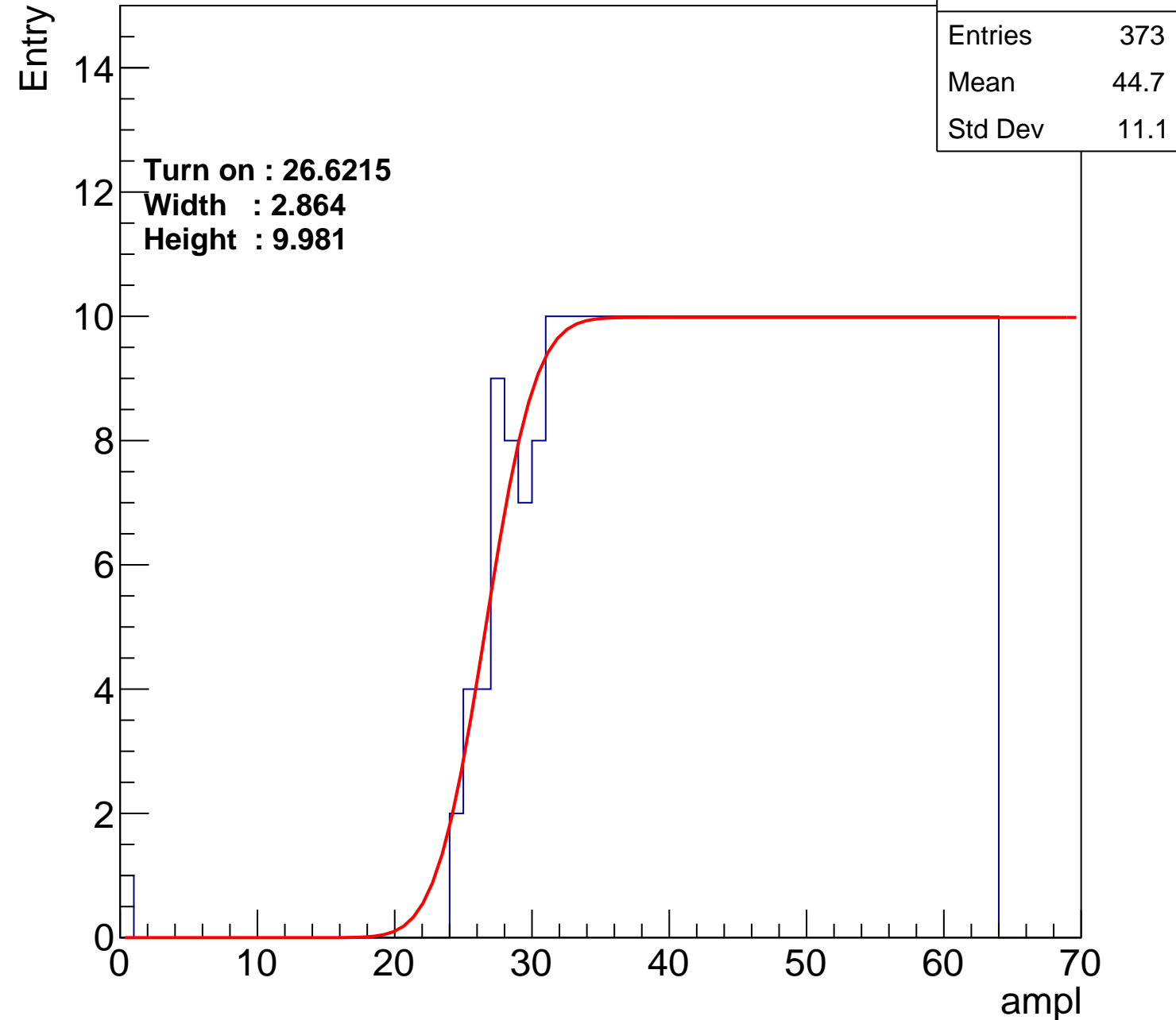
Width : 2.864

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch20

calib_packv5_042523_0143.root, FC#12, port B1

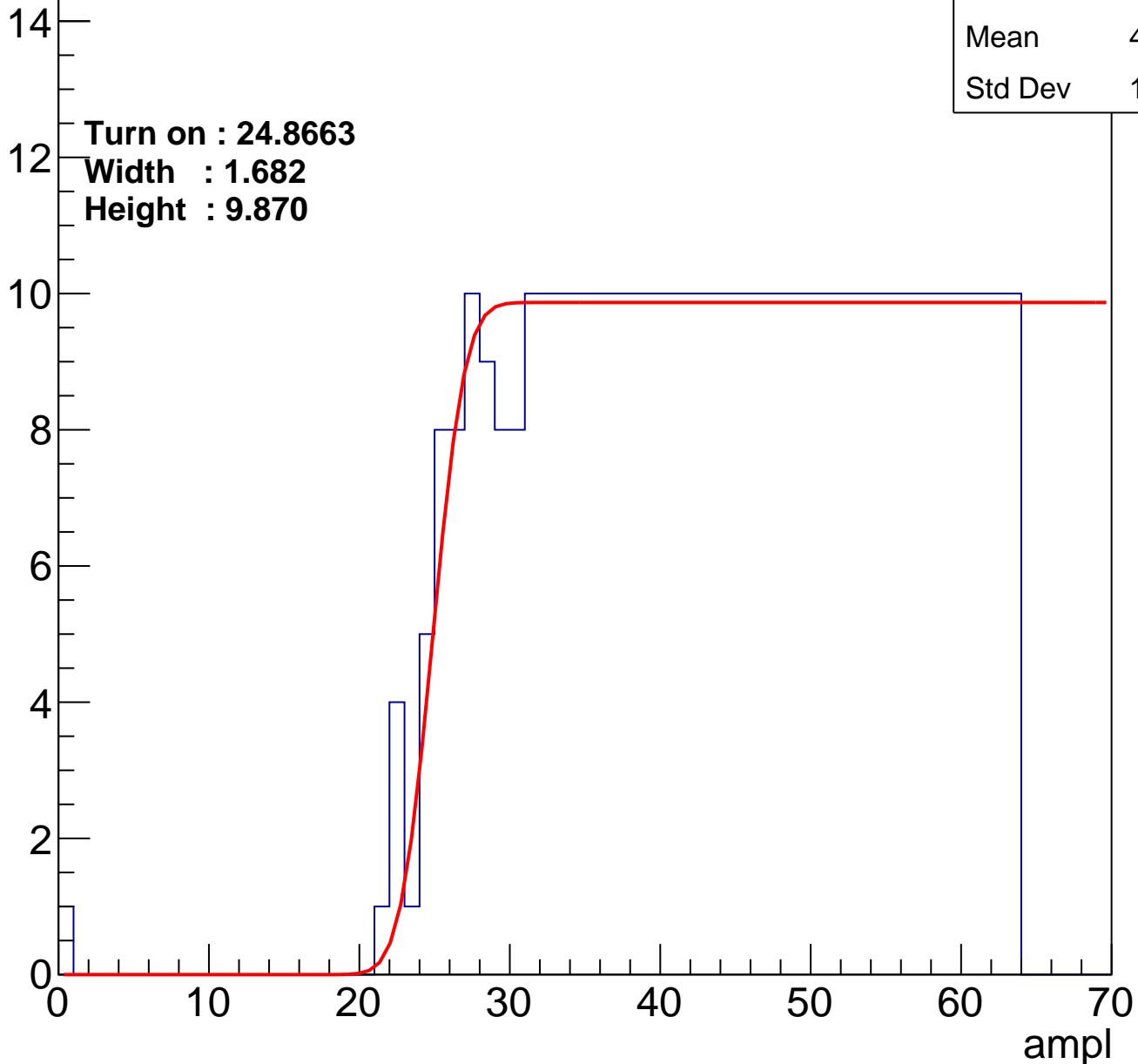
Entries	393
Mean	43.67
Std Dev	11.69

Turn on : 24.8663

Width : 1.682

Height : 9.870

Entry



B0L102S, U10-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.39
Std Dev	11.78

Turn on : 27.1739

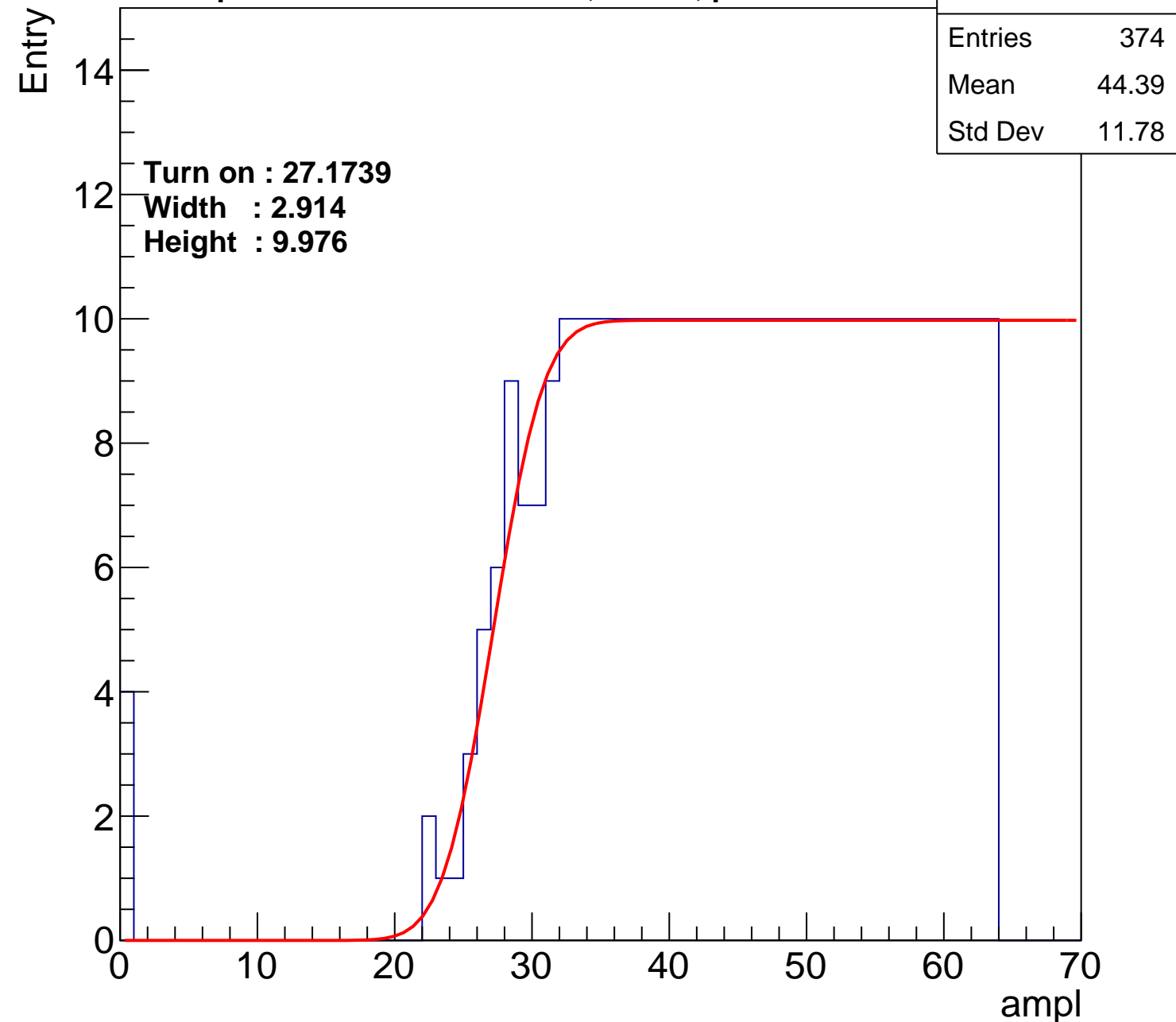
Width : 2.914

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch22

calib_packv5_042523_0143.root, FC#12, port B1

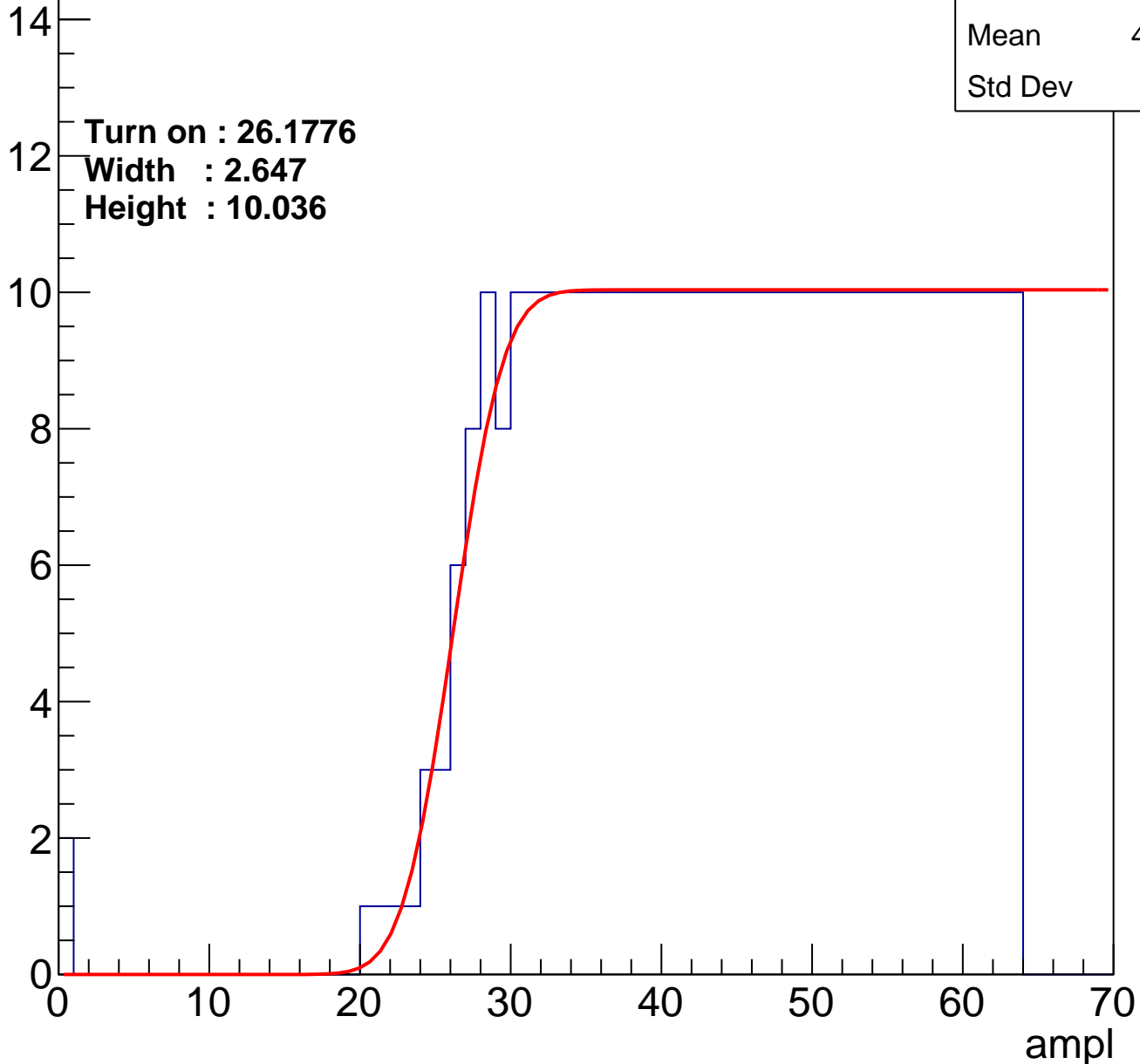
Entries	384
Mean	44.08
Std Dev	11.6

Turn on : 26.1776

Width : 2.647

Height : 10.036

Entry



B0L102S, U10-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.78
Std Dev	11.88

Turn on : 25.5626

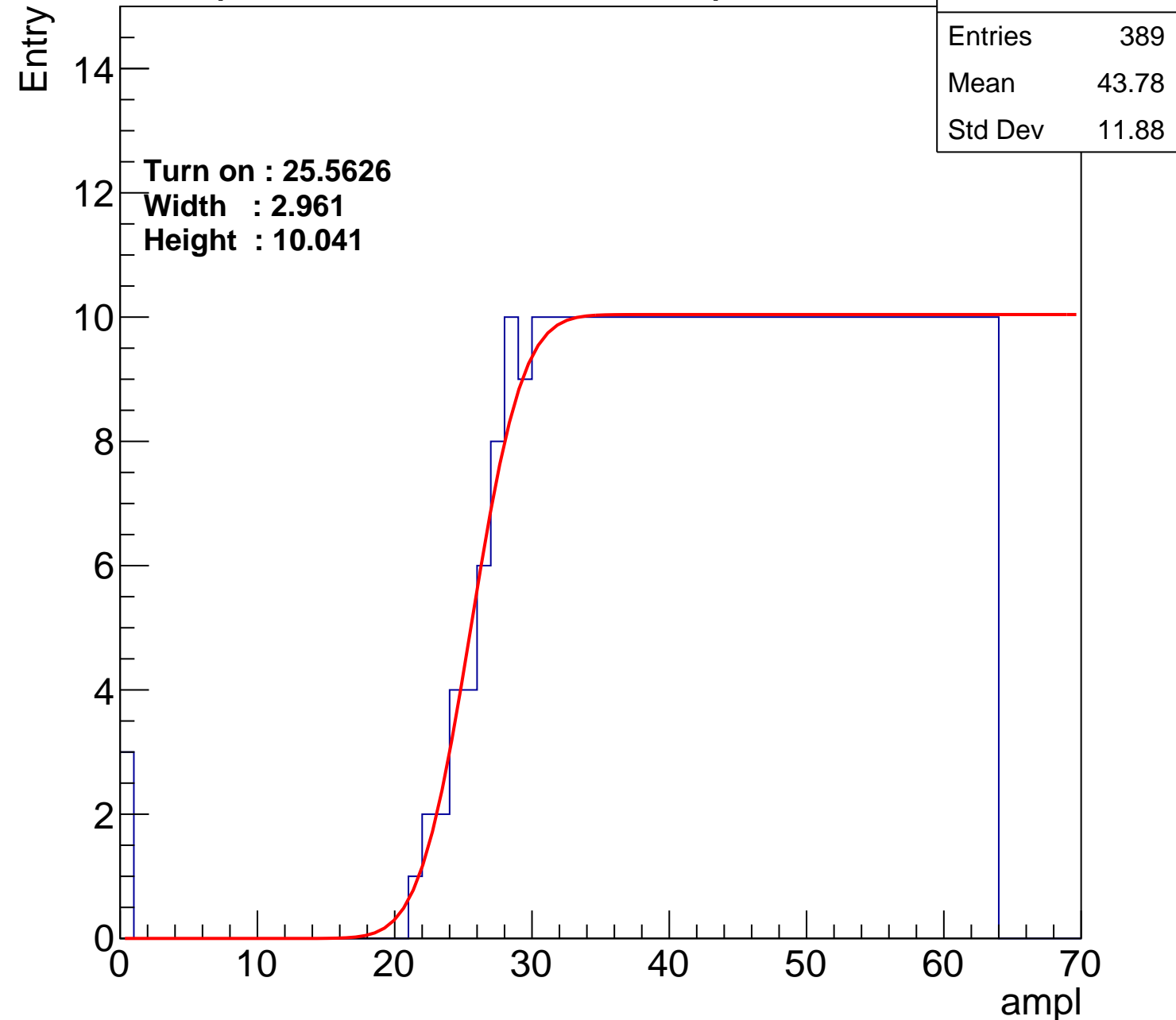
Width : 2.961

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.5
Std Dev	12.01

Turn on : 25.0357

Width : 2.900

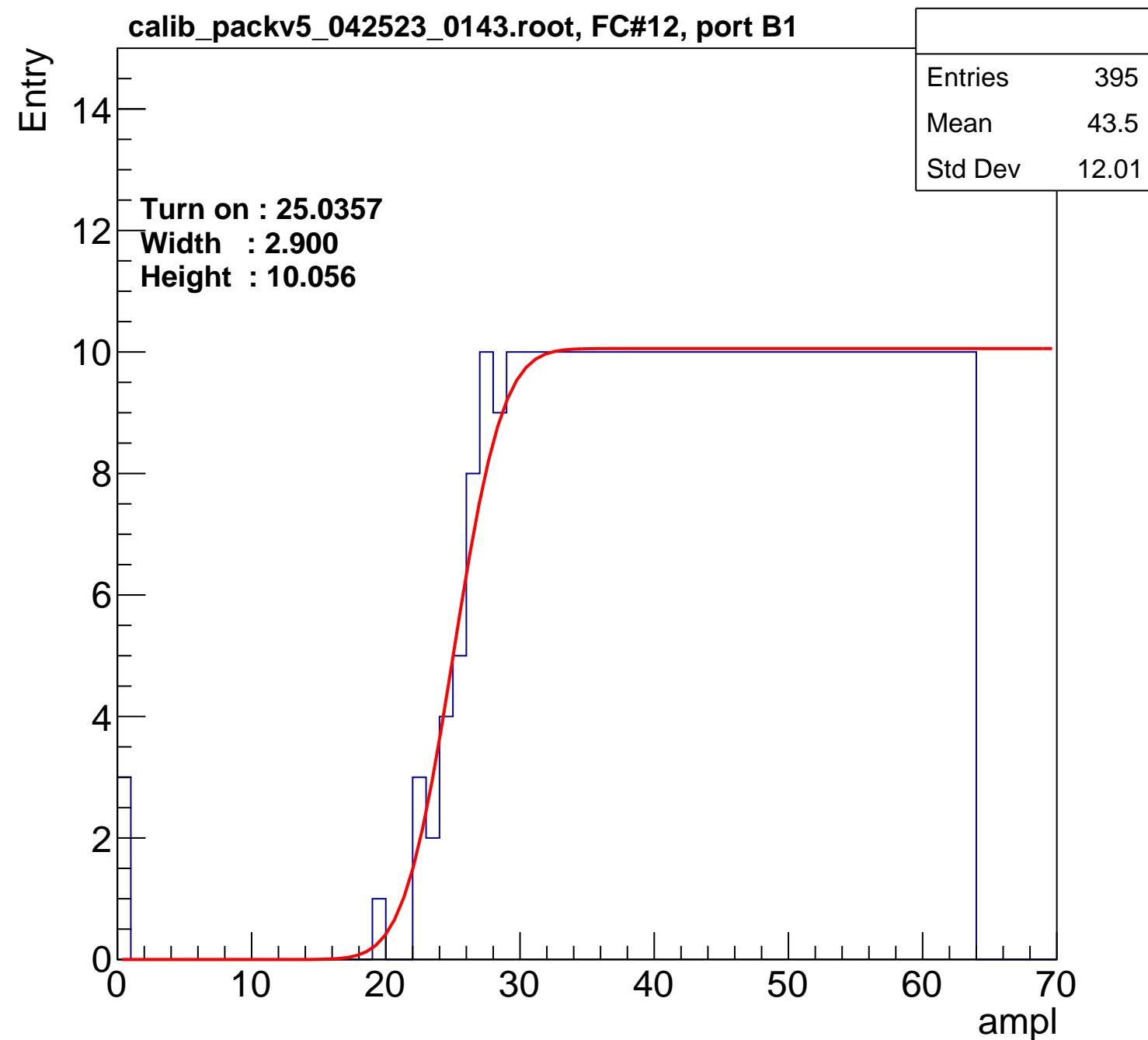
Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U10-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.77
Std Dev	11.95

Turn on : 25.9133

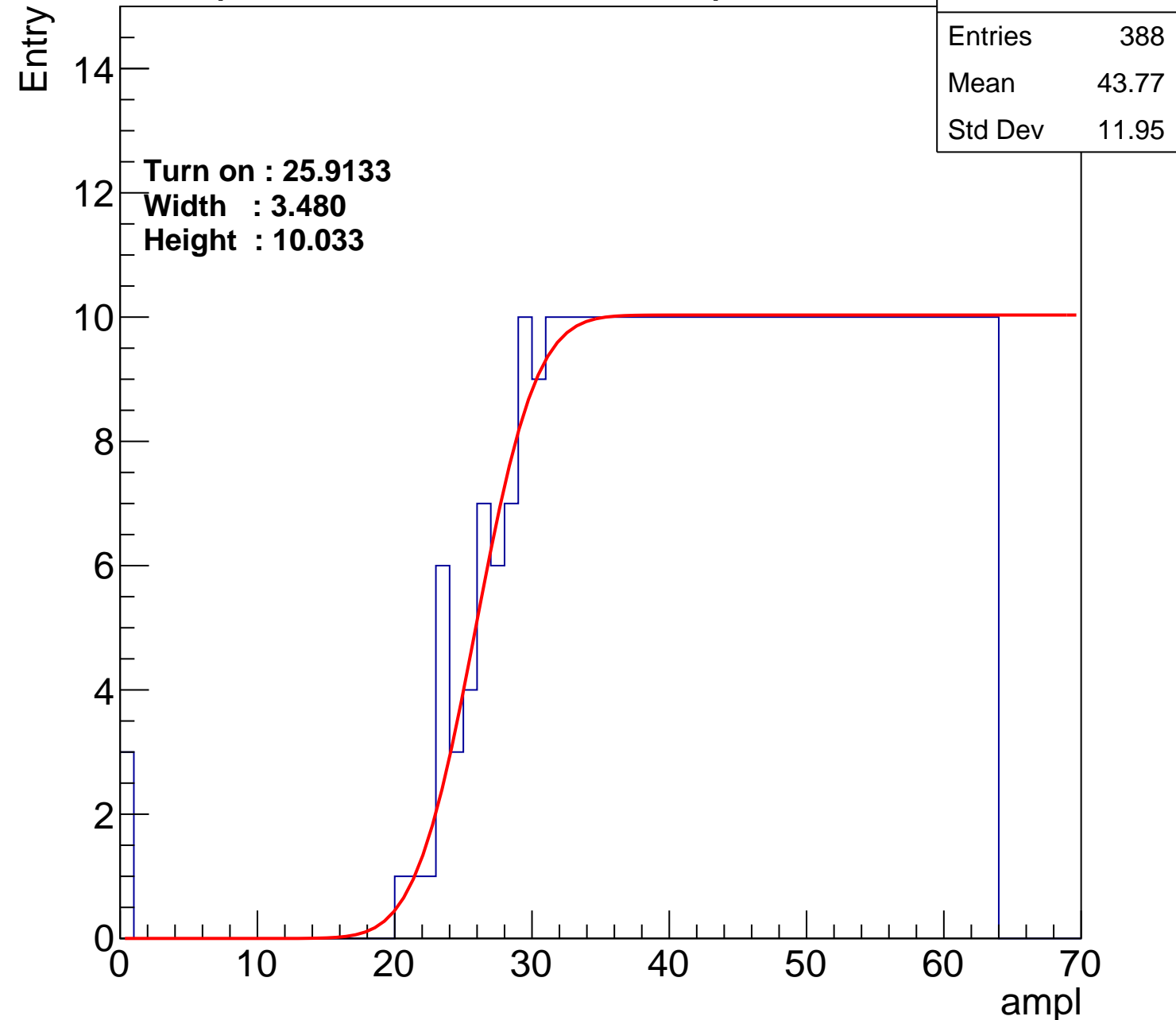
Width : 3.480

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.71
Std Dev	11.98

Turn on : 25.5261

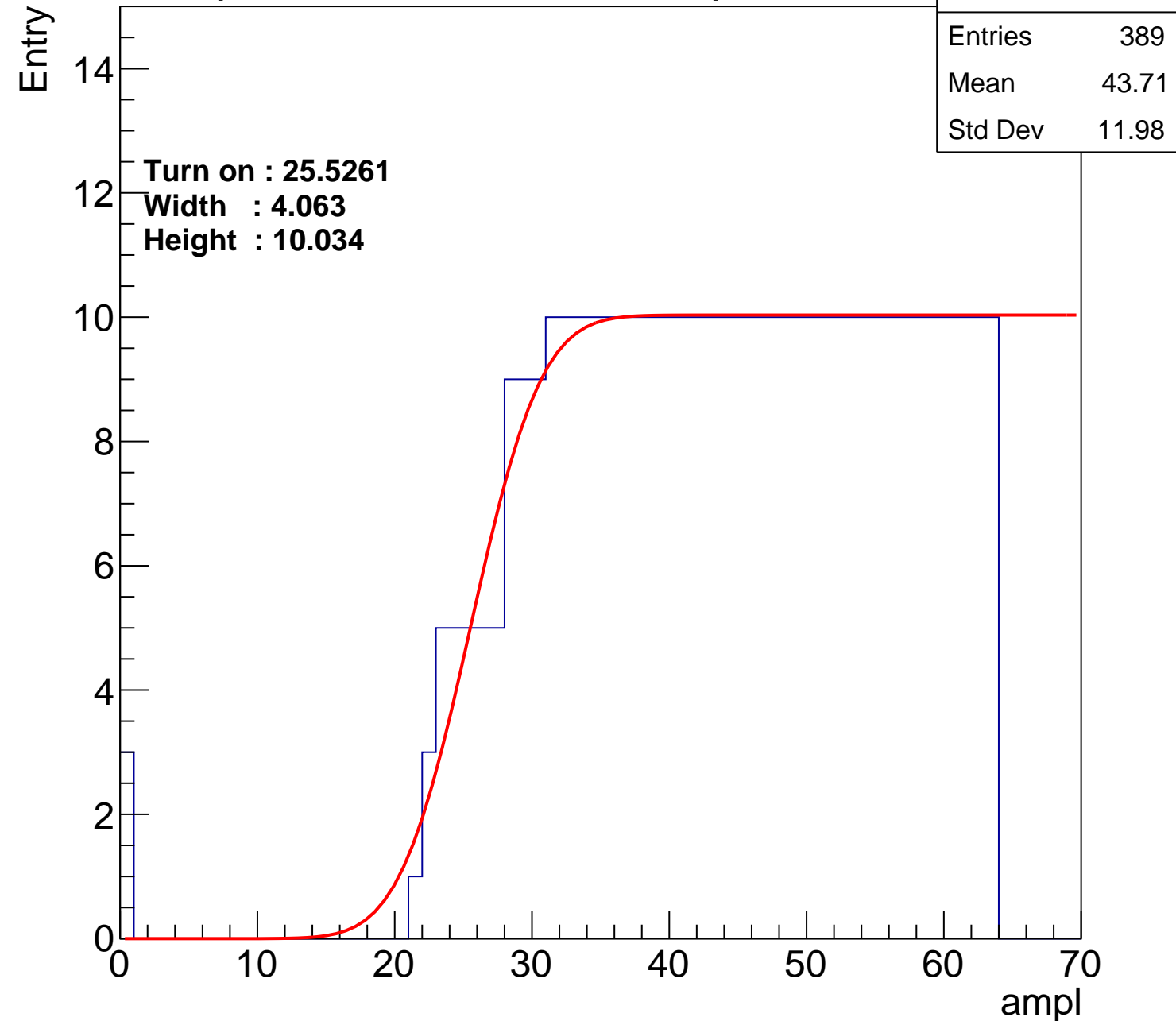
Width : 4.063

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.42
Std Dev	11.43

Turn on : 26.5719

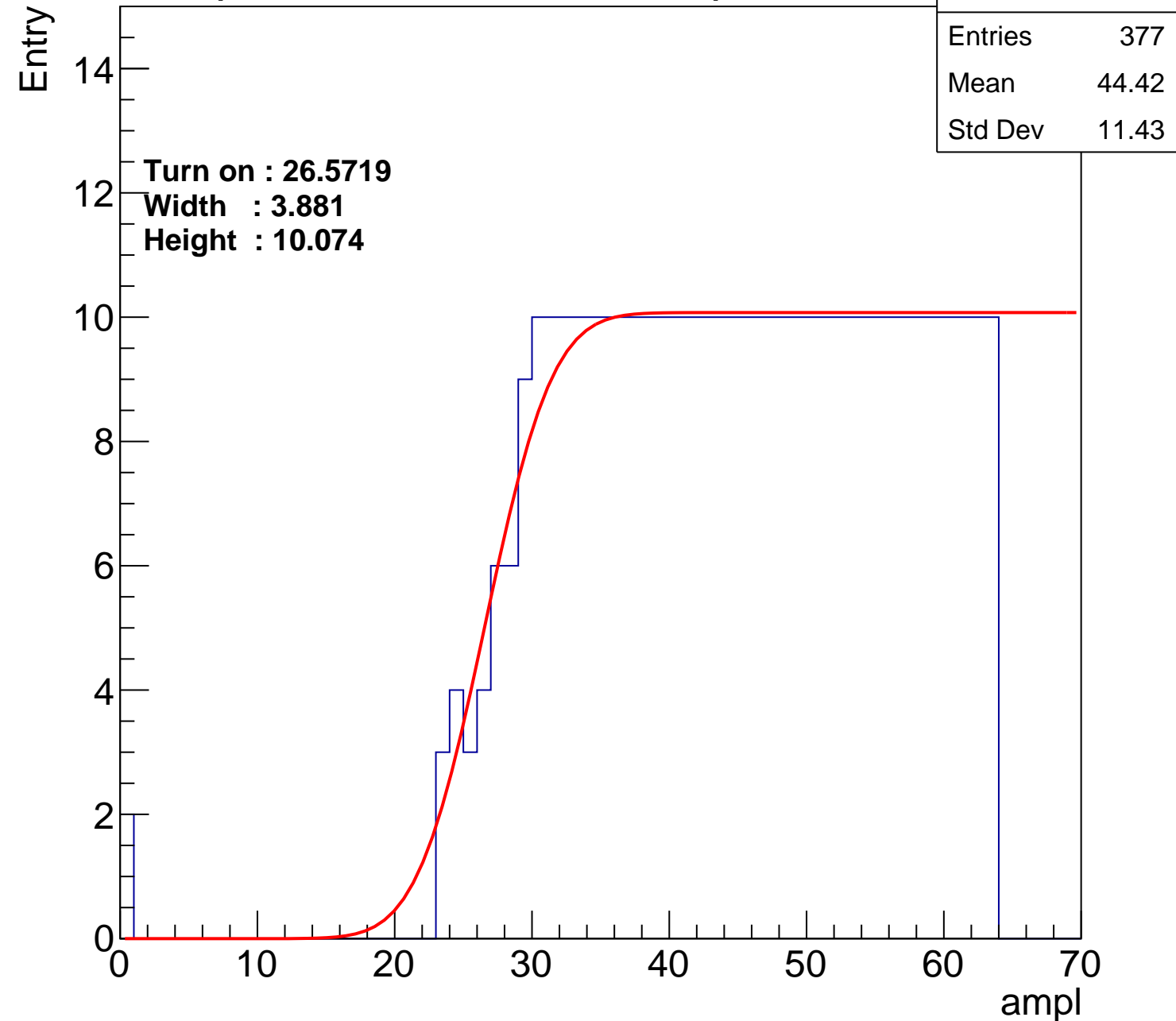
Width : 3.881

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.12
Std Dev	12.23

Turn on : 24.3129

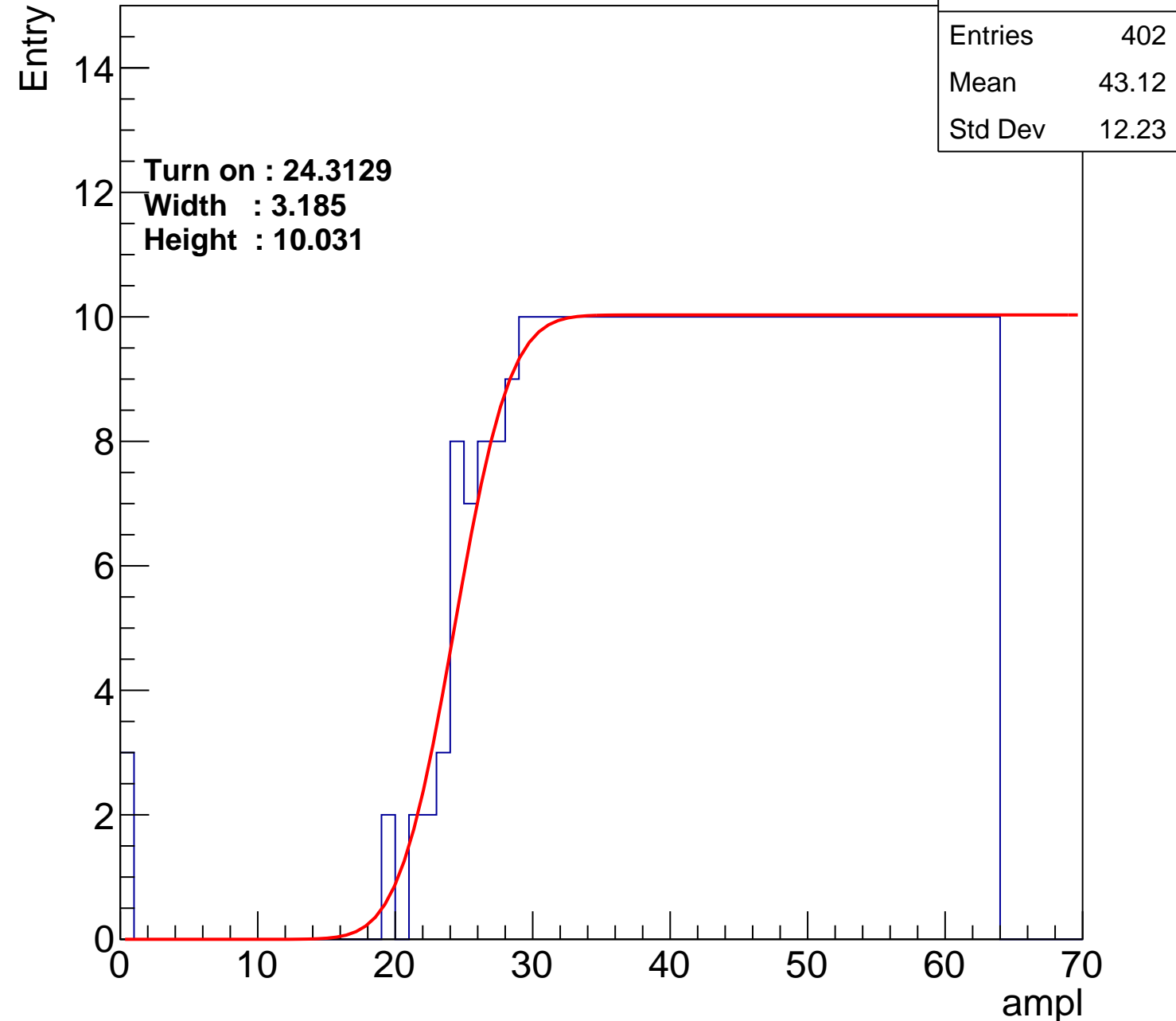
Width : 3.185

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	356
Mean	45.51
Std Dev	10.69

Turn on : 28.3645

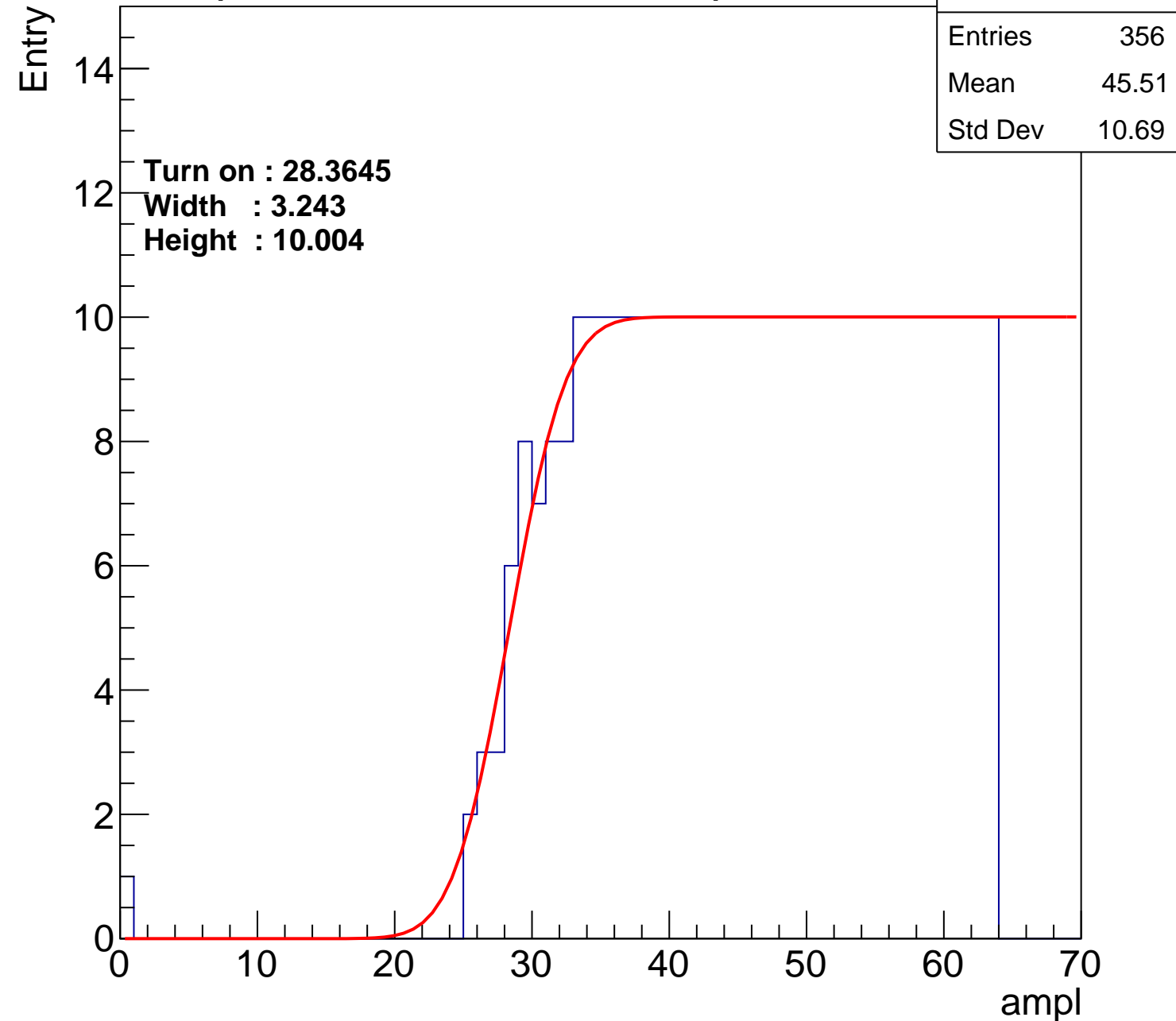
Width : 3.243

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.62
Std Dev	11.18

Turn on : 26.7211

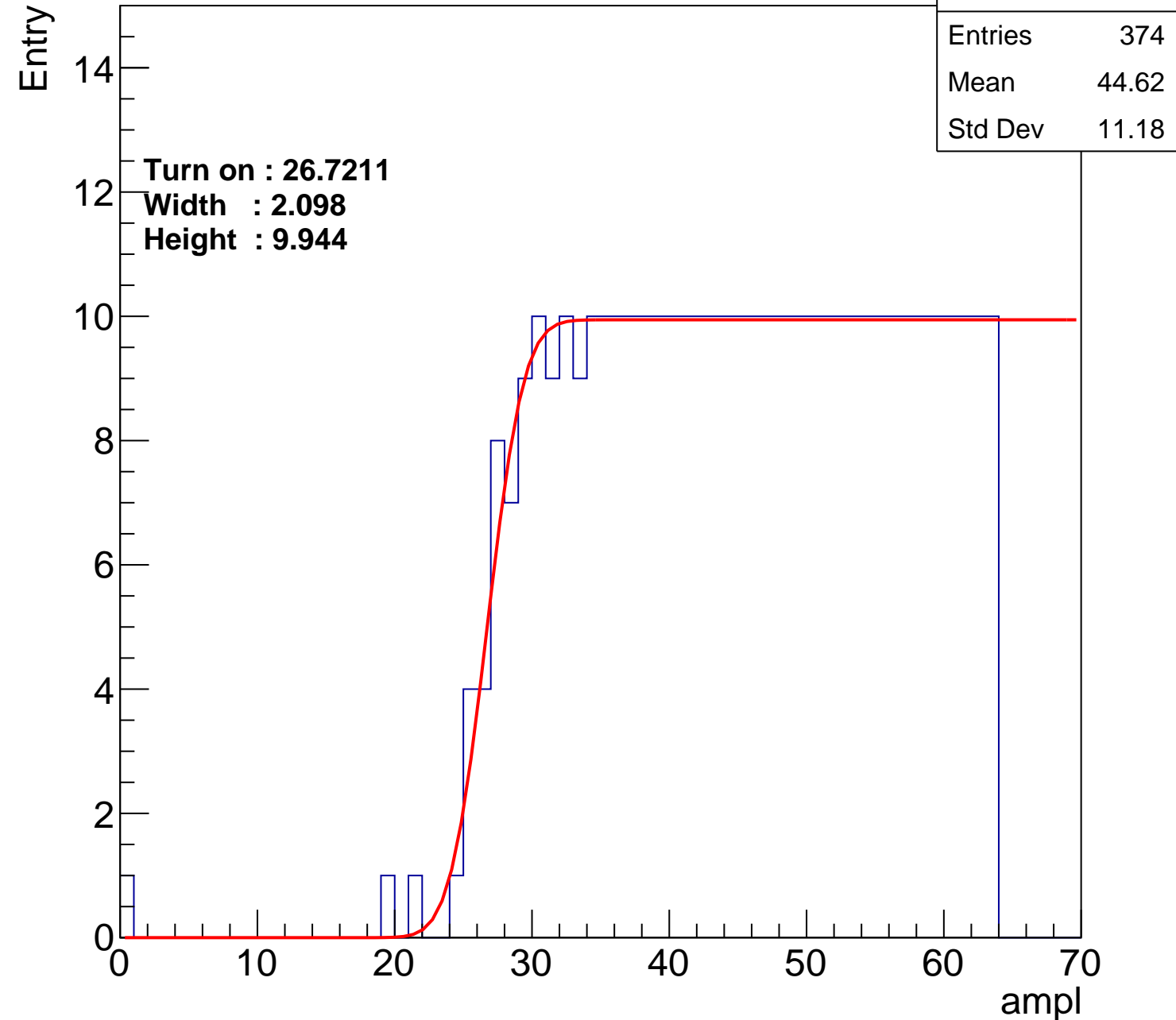
Width : 2.098

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch31

calib_packv5_042523_0143.root, FC#12, port B1

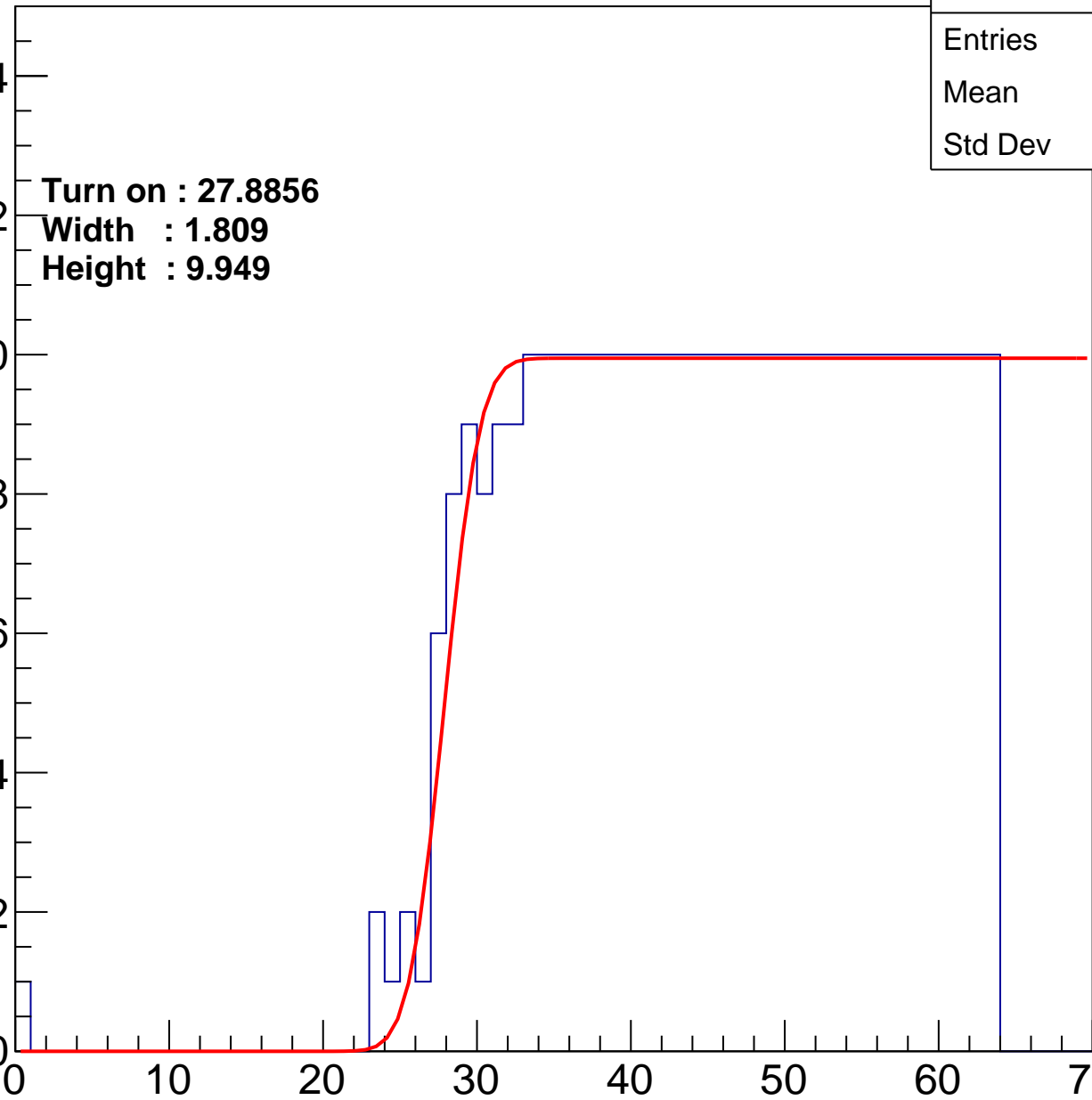
Entry

14
12
10
8
6
4
2
0

Turn on : 27.8856
Width : 1.809
Height : 9.949

Entries	366
Mean	45.03
Std Dev	10.94

ampl



B0L102S, U10-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.02
Std Dev	11.76

Turn on : 25.6754

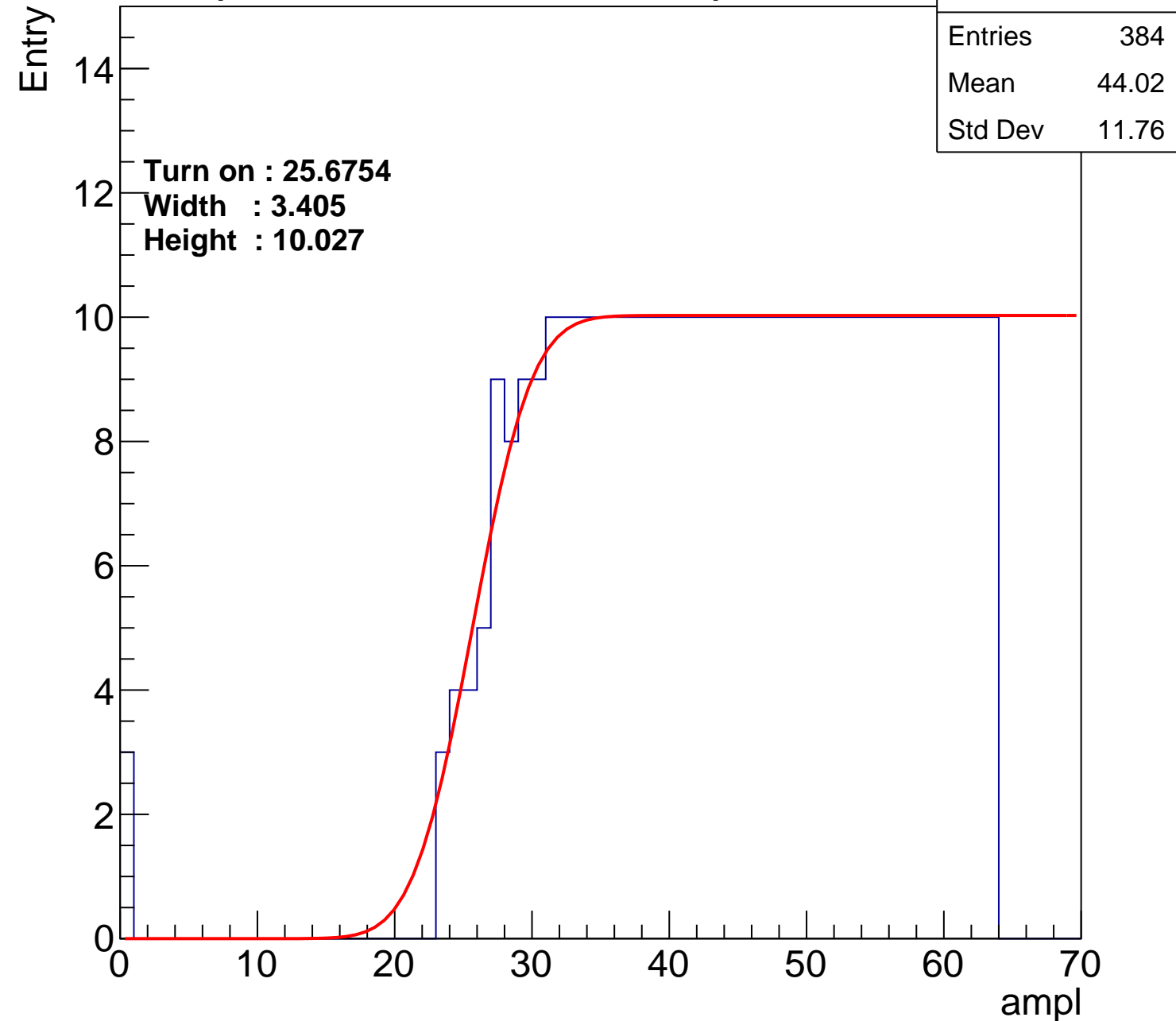
Width : 3.405

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	355
Mean	45.35
Std Dev	11.21

Turn on : 29.5615

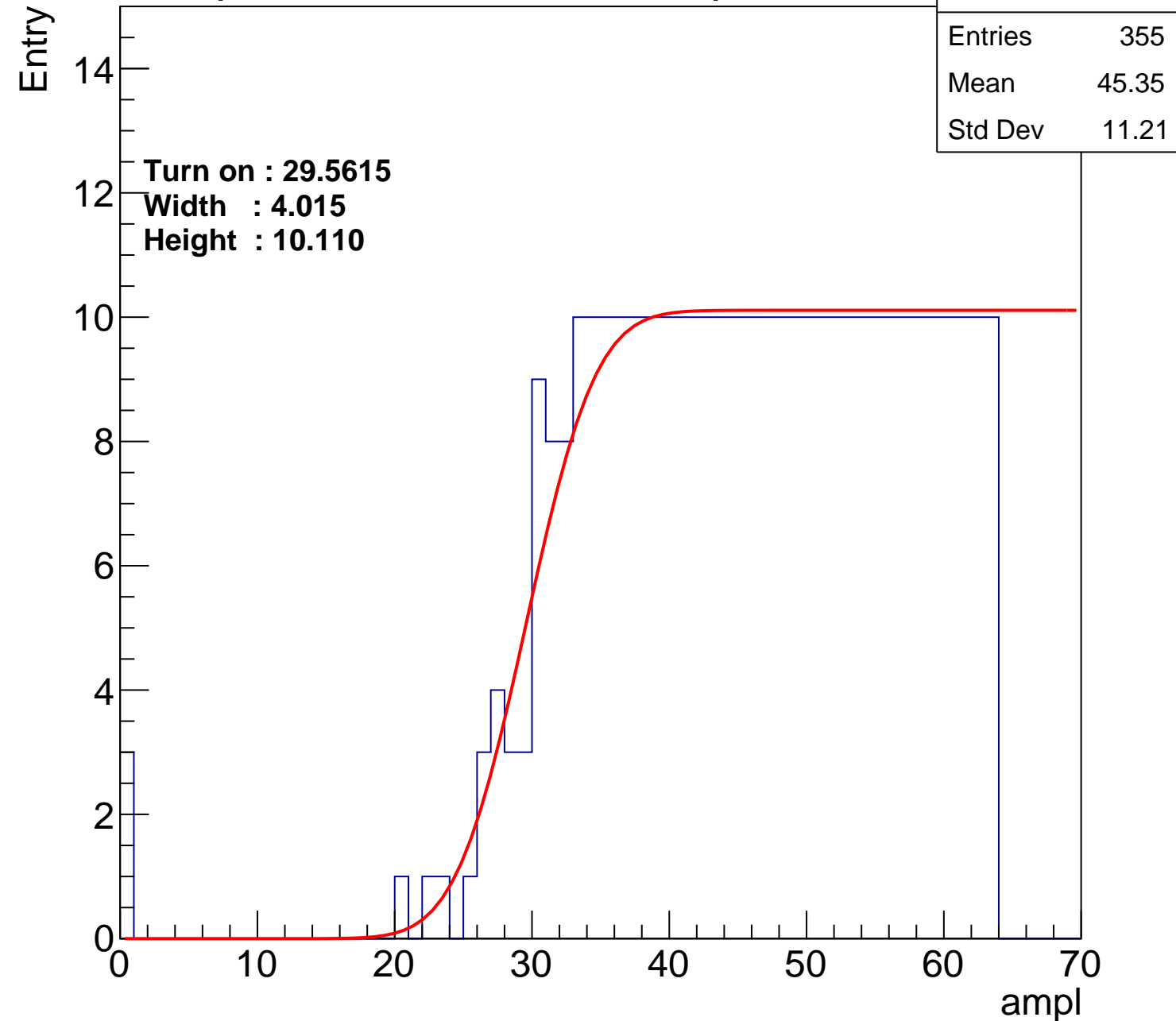
Width : 4.015

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 25.9987

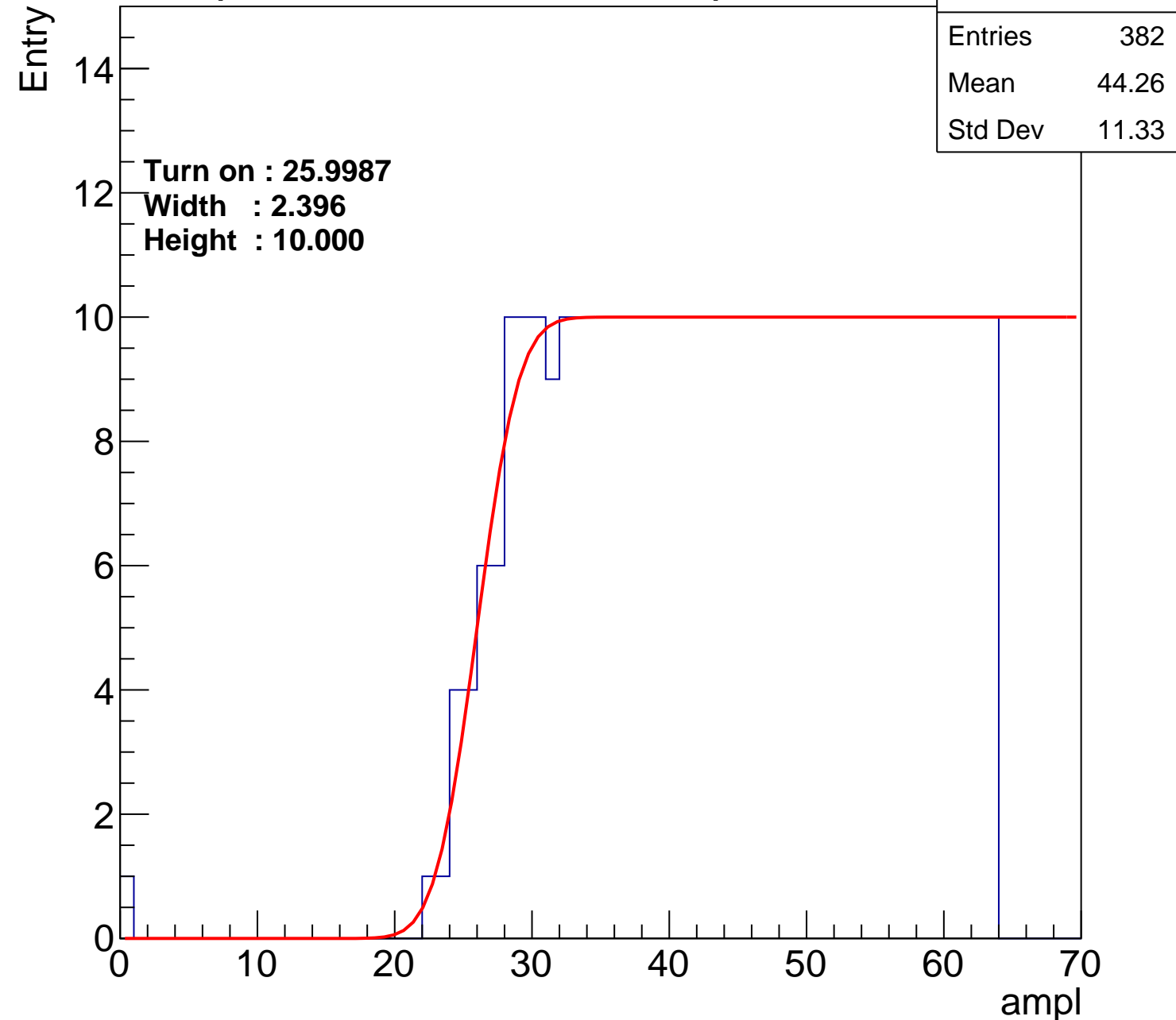
Width : 2.396

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	362
Mean	45.06
Std Dev	11.29

Turn on : 28.5914

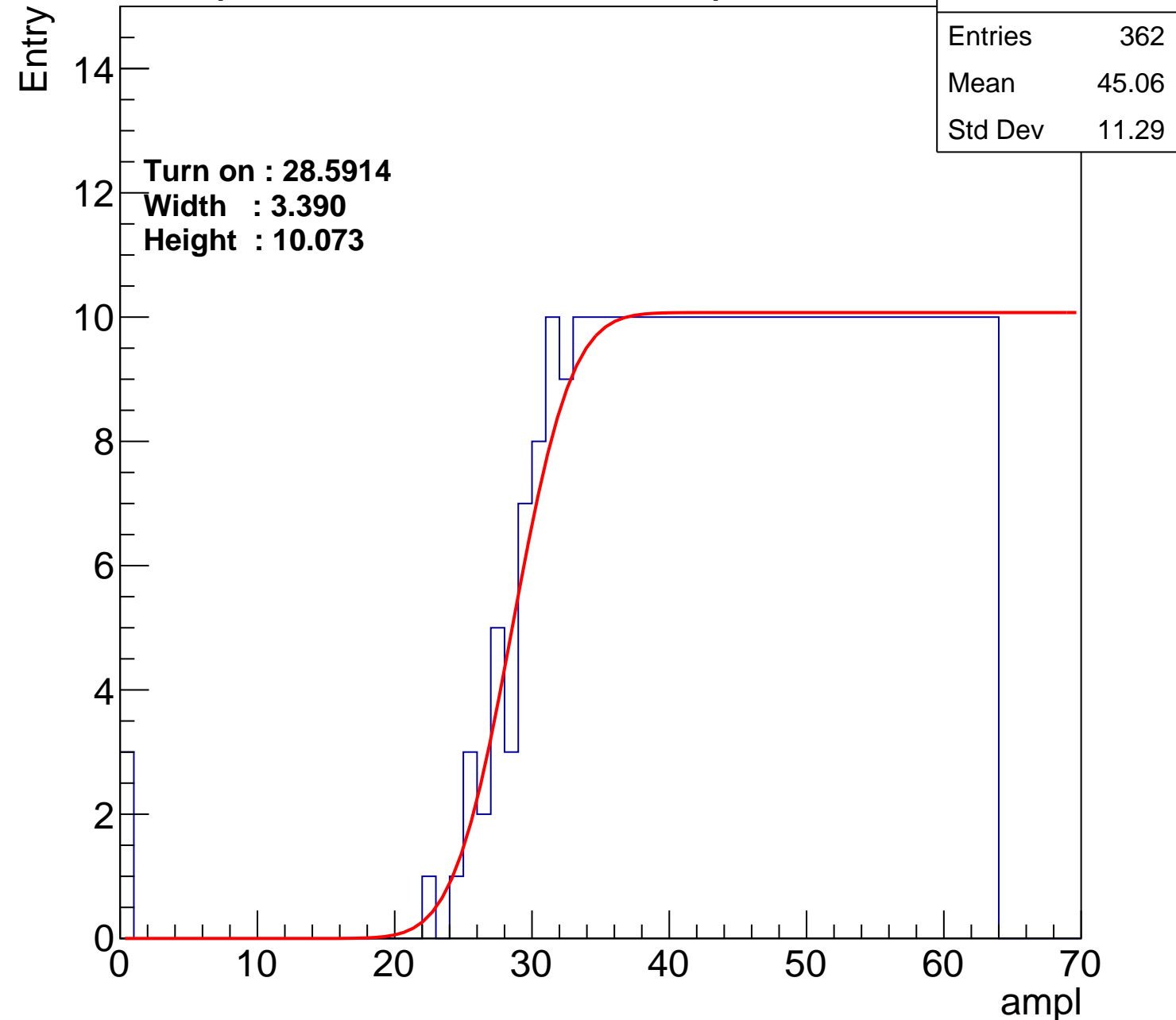
Width : 3.390

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.1408

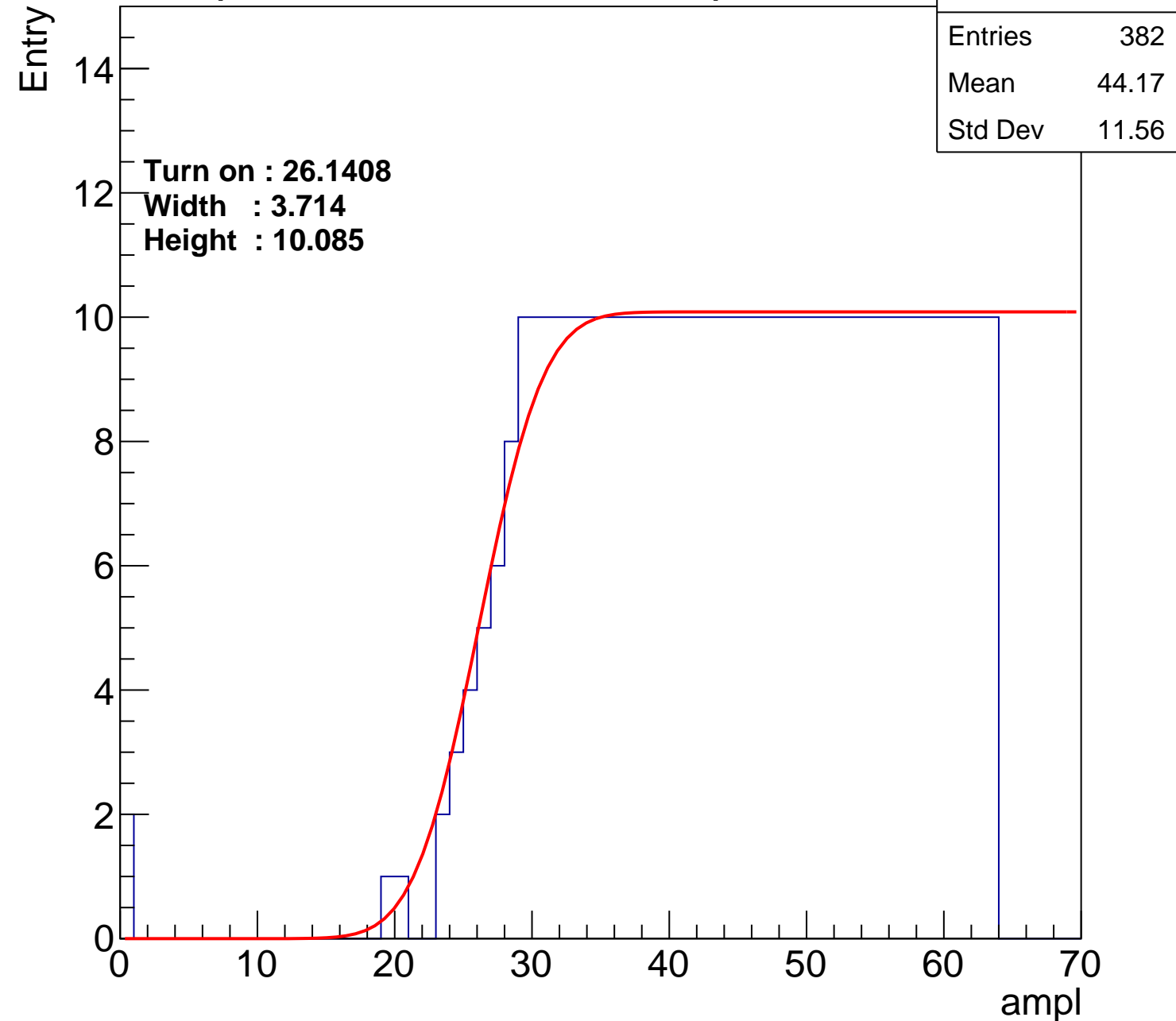
Width : 3.714

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.8676

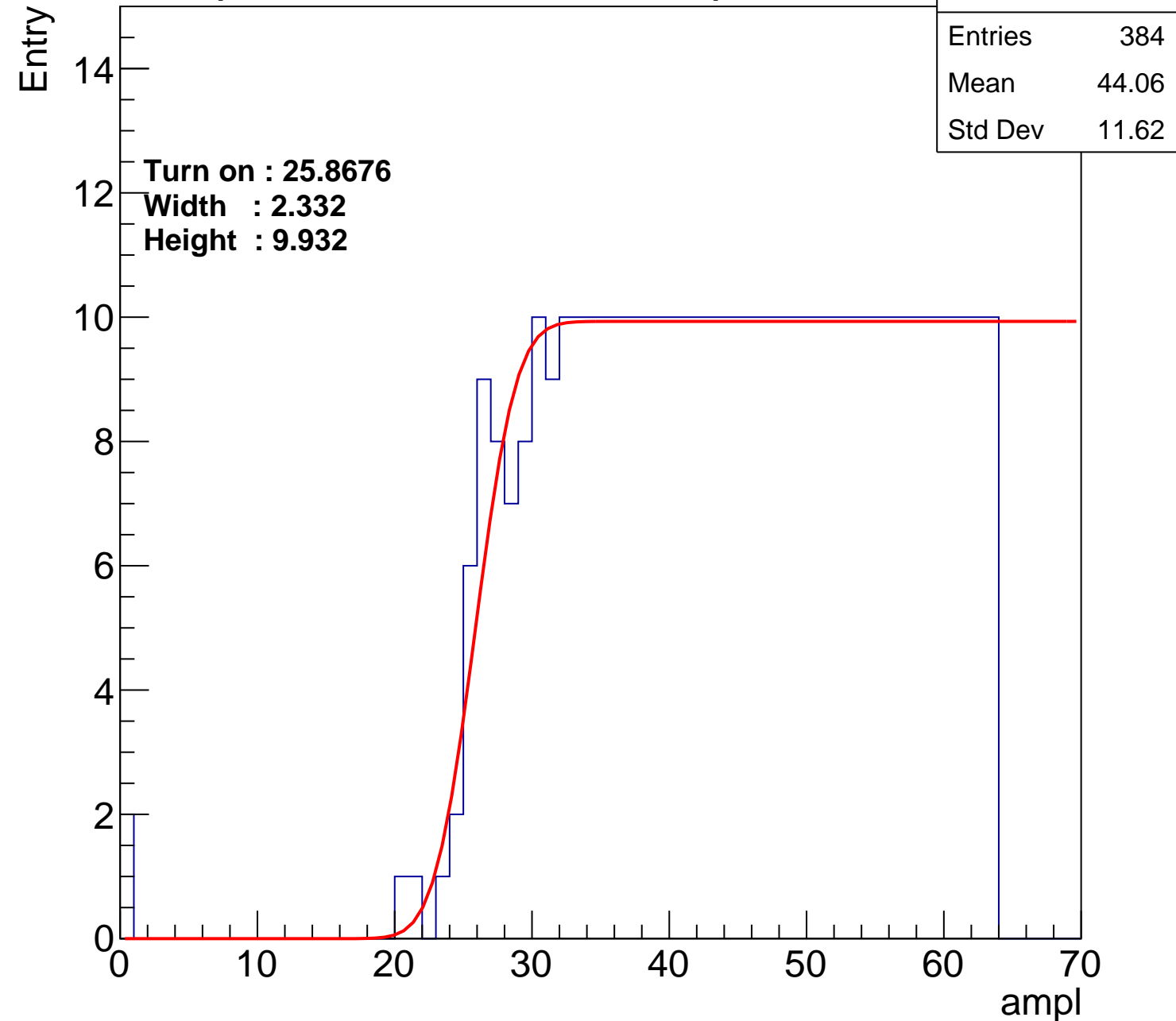
Width : 2.332

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.33
Std Dev	11.88

Turn on : 24.2229

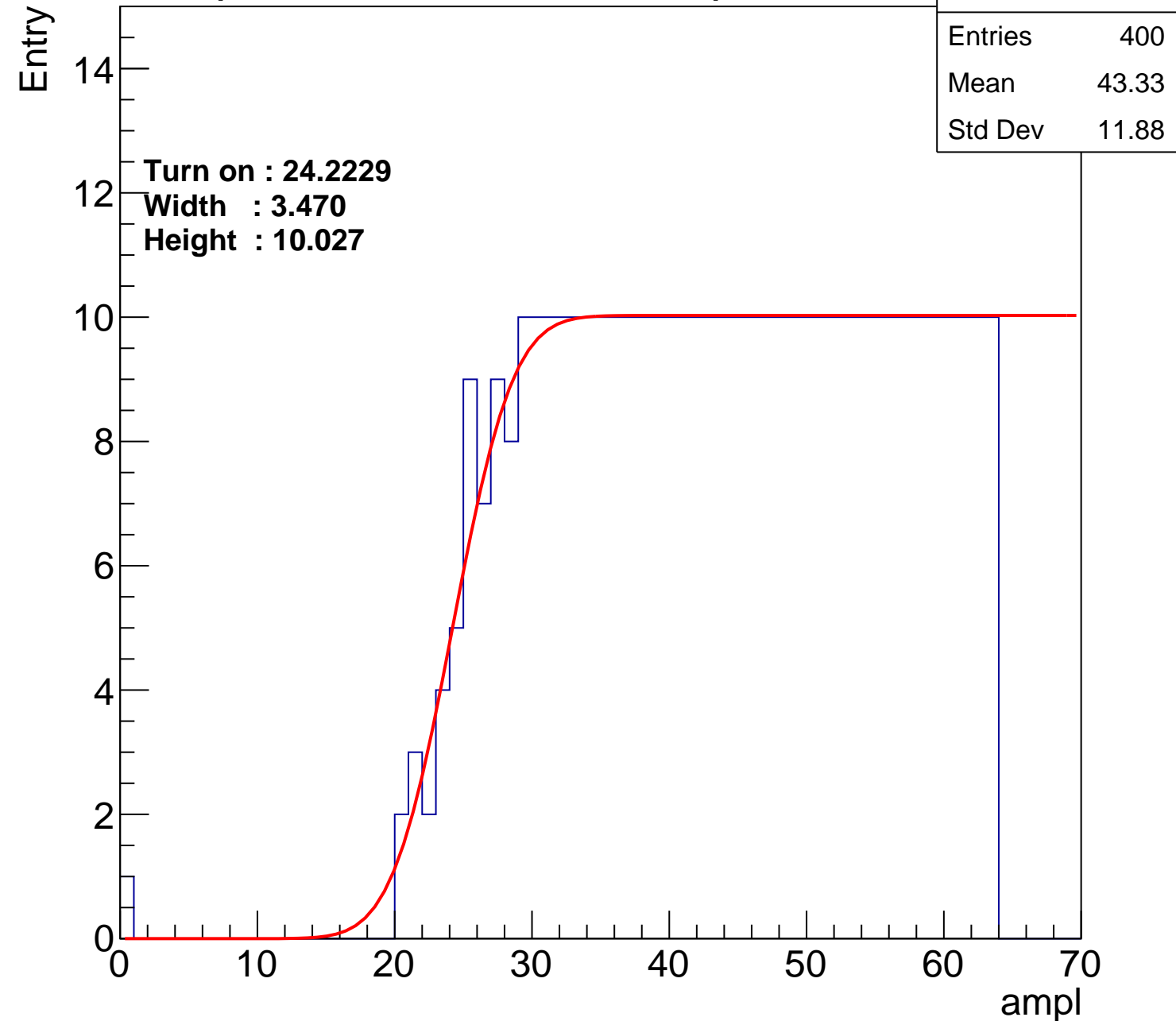
Width : 3.470

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch39

calib_packv5_042523_0143.root, FC#12, port B1

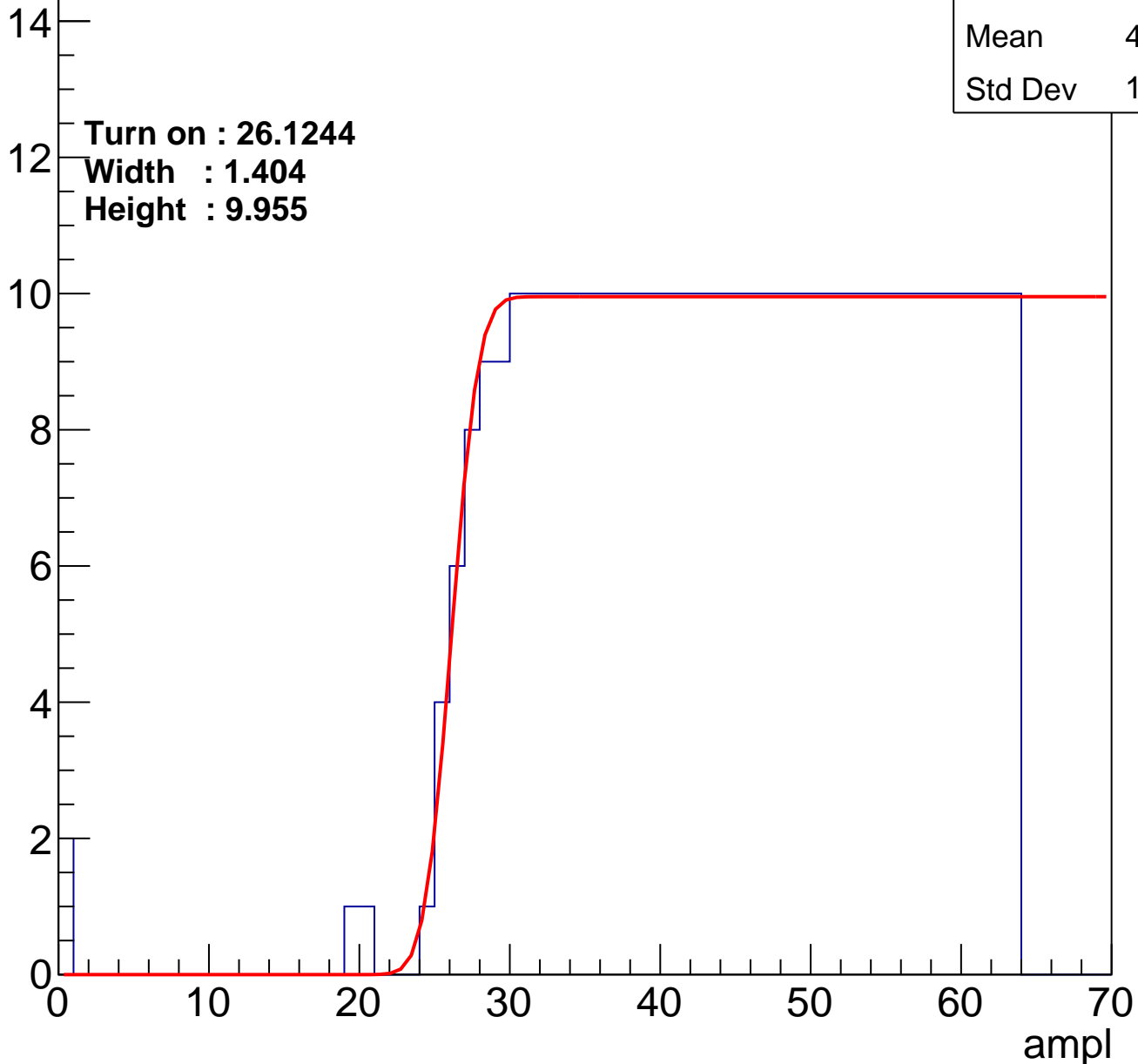
Entries	381
Mean	44.25
Std Dev	11.49

Turn on : 26.1244

Width : 1.404

Height : 9.955

Entry



B0L102S, U10-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.5
Std Dev	12.06

Turn on : 25.5276

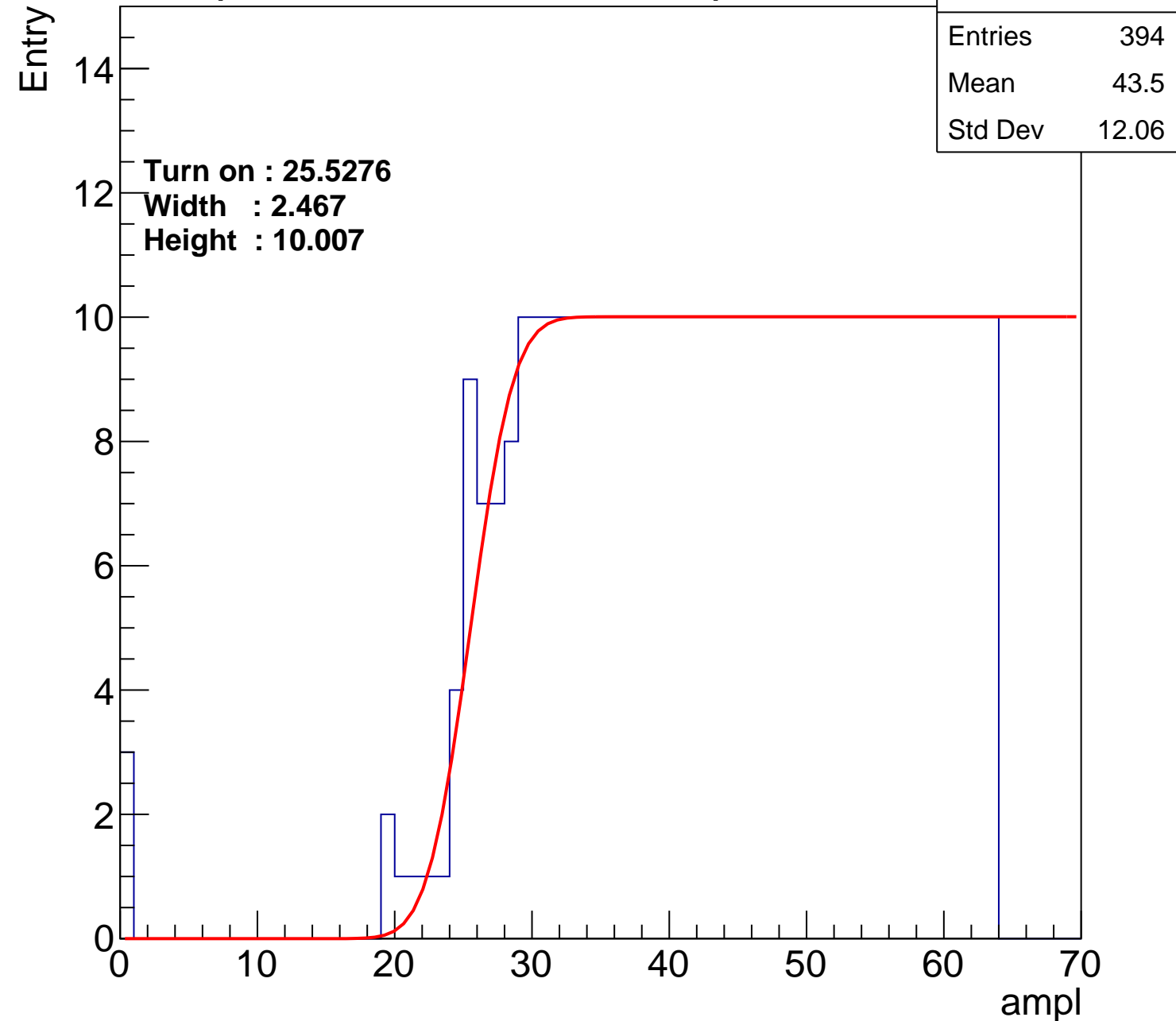
Width : 2.467

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.2
Std Dev	11.37

Turn on : 25.7765

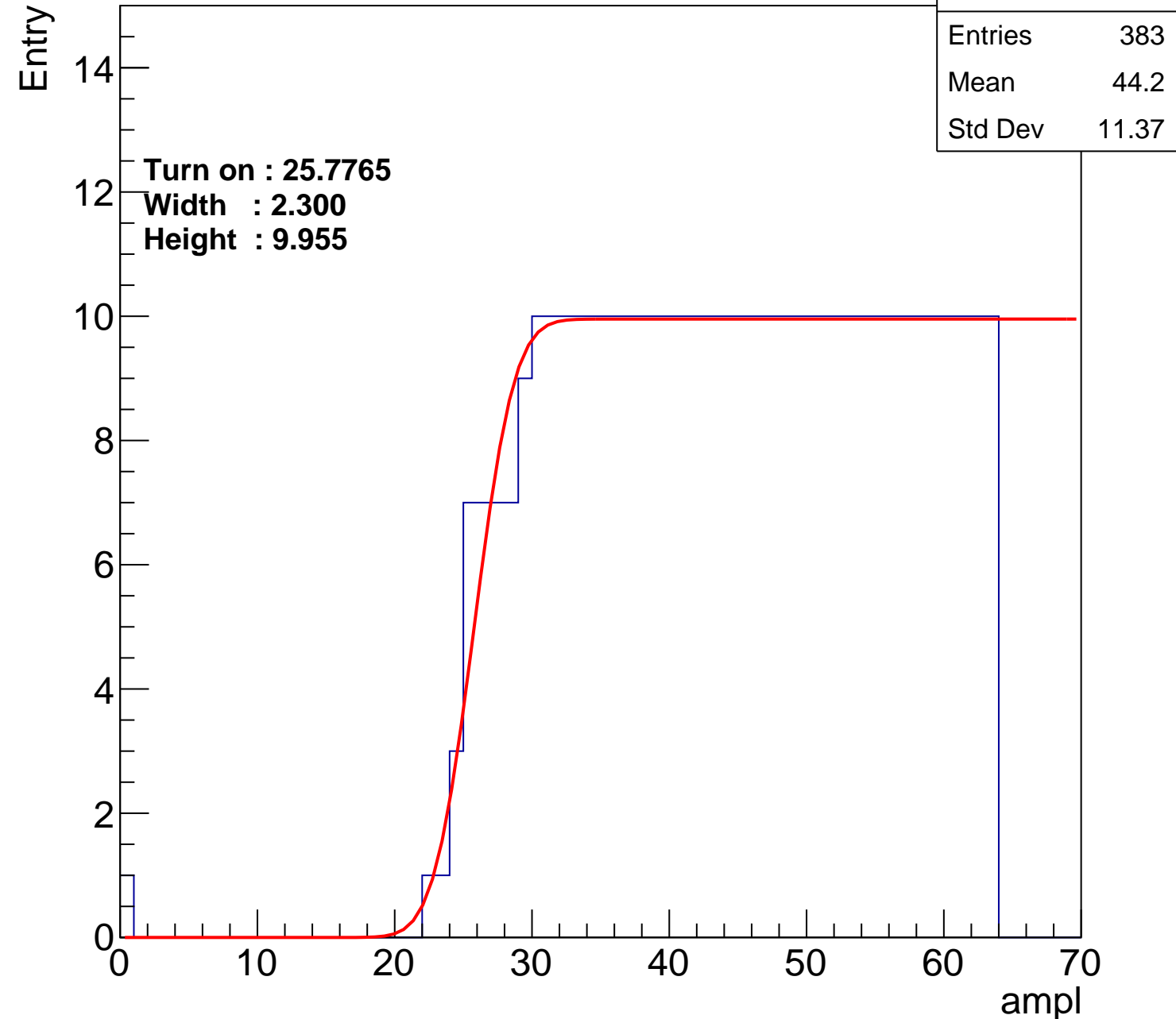
Width : 2.300

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	43.06
Std Dev	12.07

Turn on : 24.1035

Width : 3.883

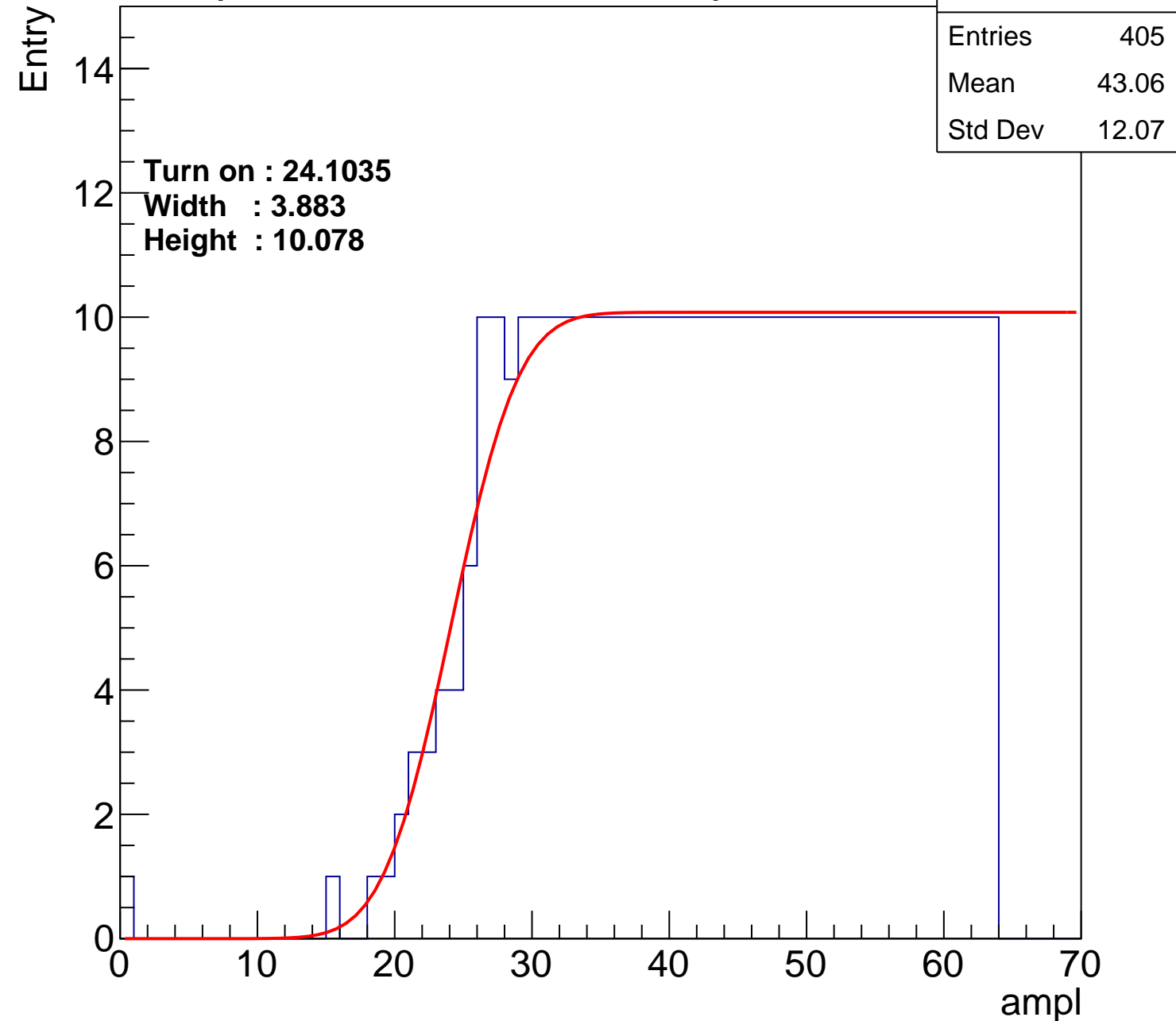
Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U10-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.3
Std Dev	11.45

Turn on : 26.1858

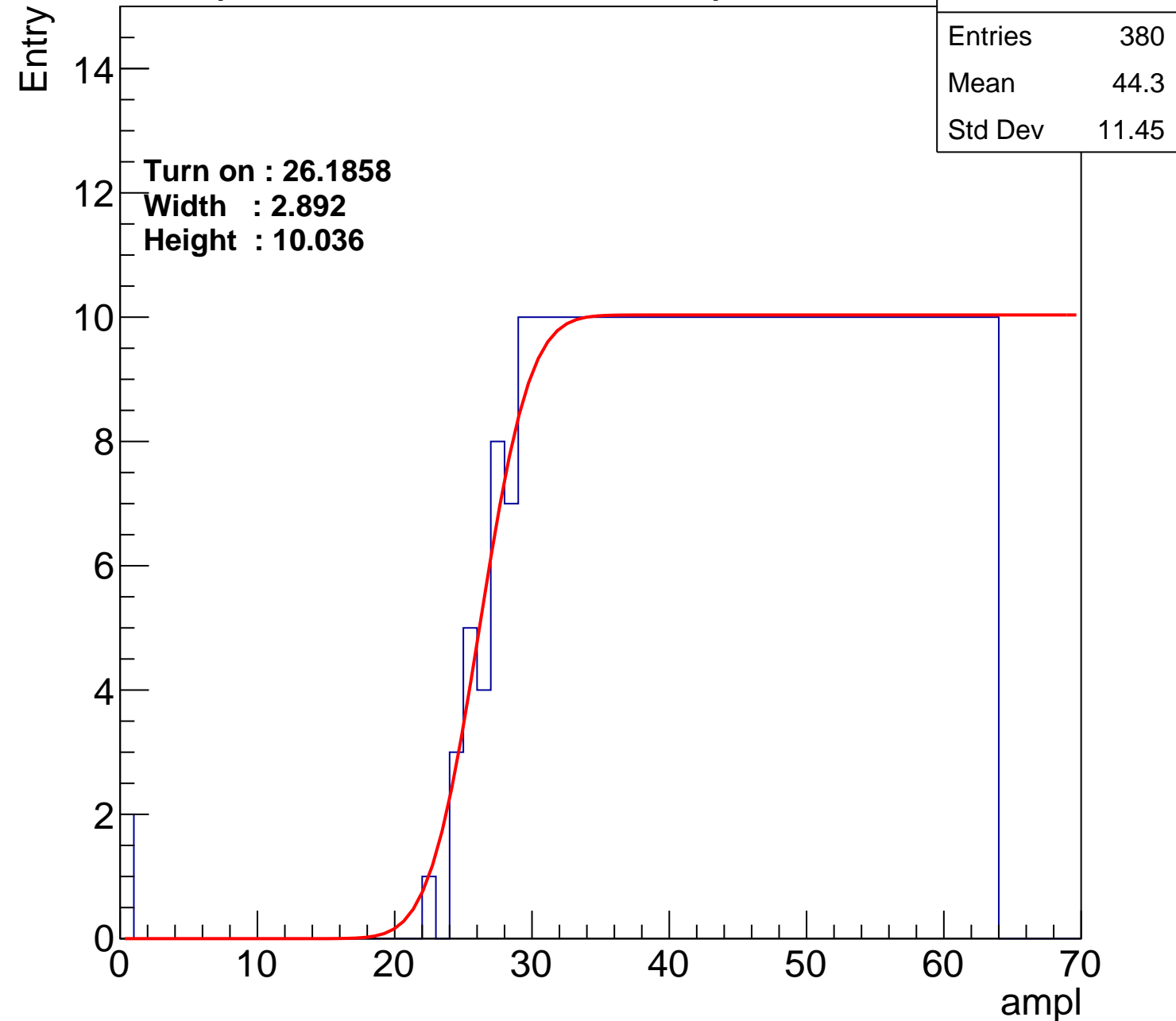
Width : 2.892

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.2
Std Dev	11.56

Turn on : 26.6523

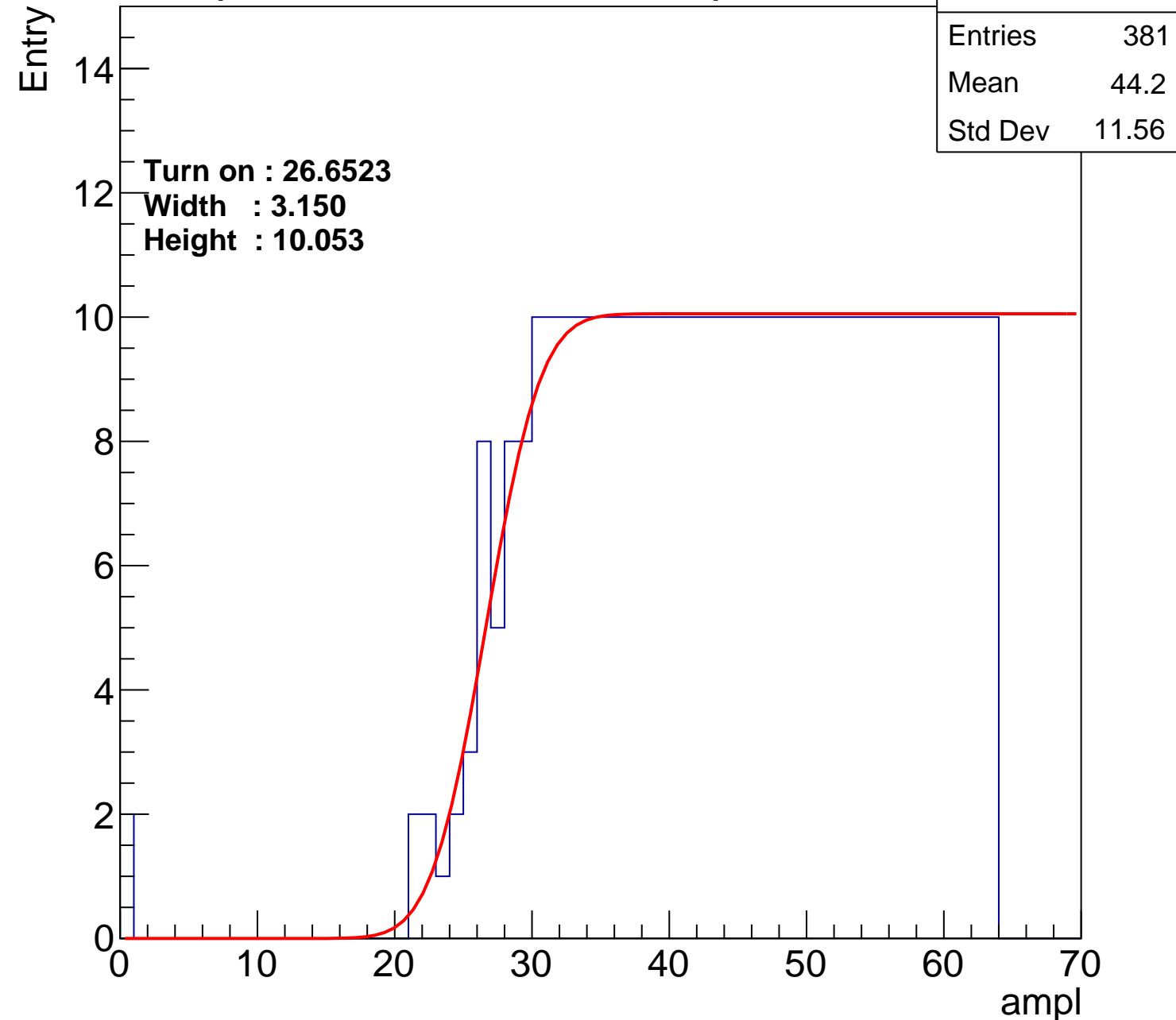
Width : 3.150

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 24.9089

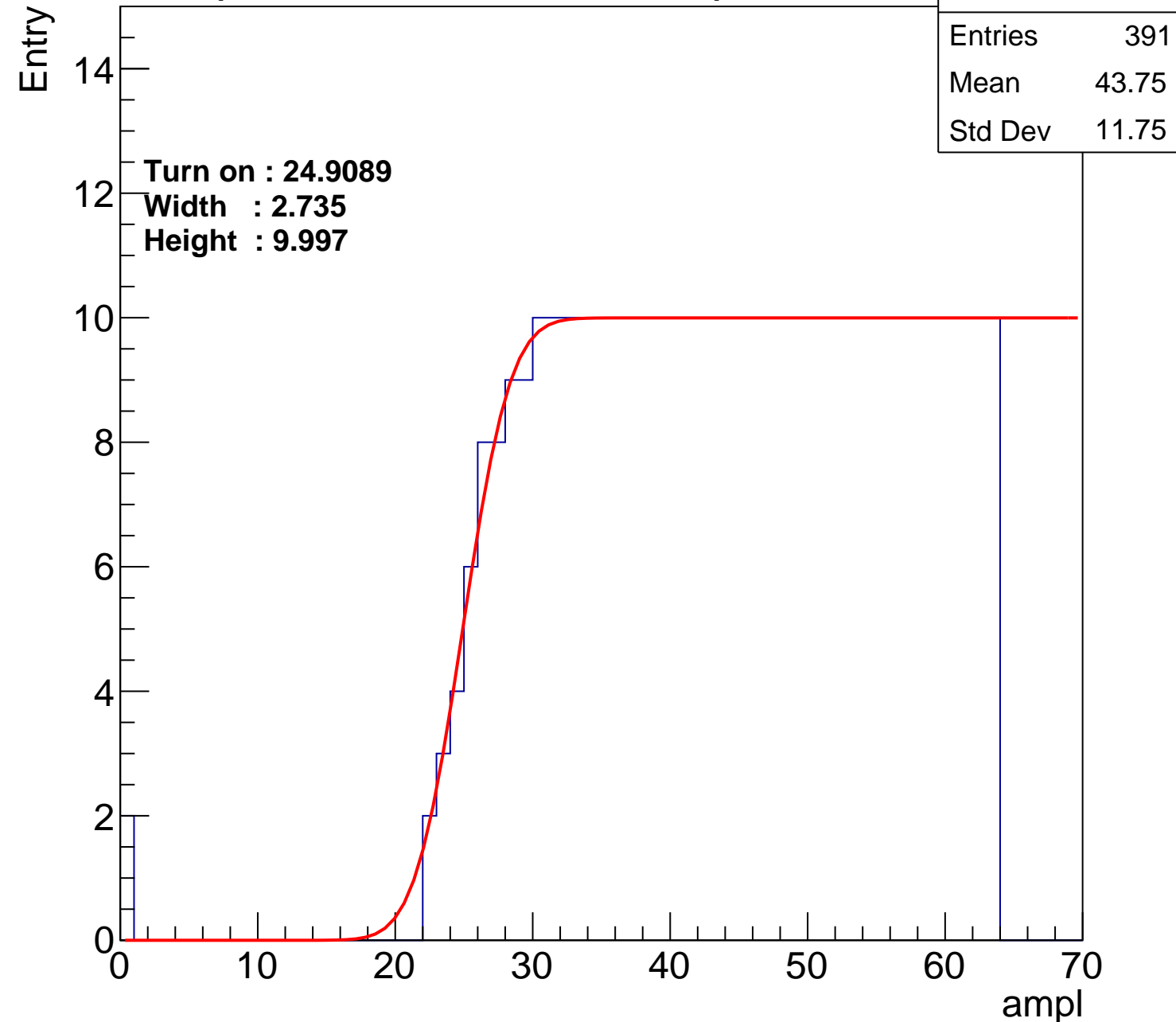
Width : 2.735

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	45.03
Std Dev	10.95

Turn on : 27.8921

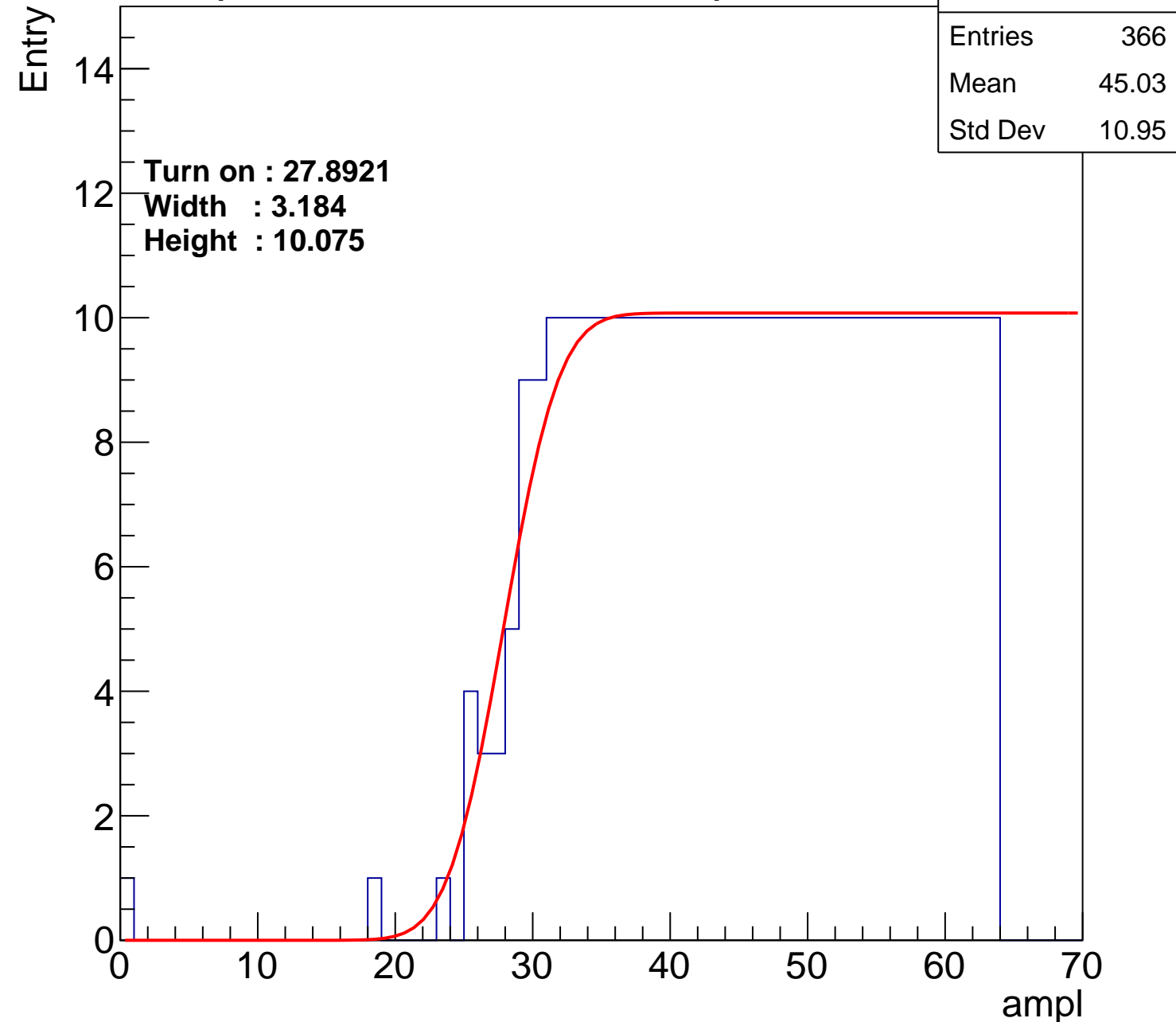
Width : 3.184

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	356
Mean	45.5
Std Dev	10.72

Turn on : 28.7806

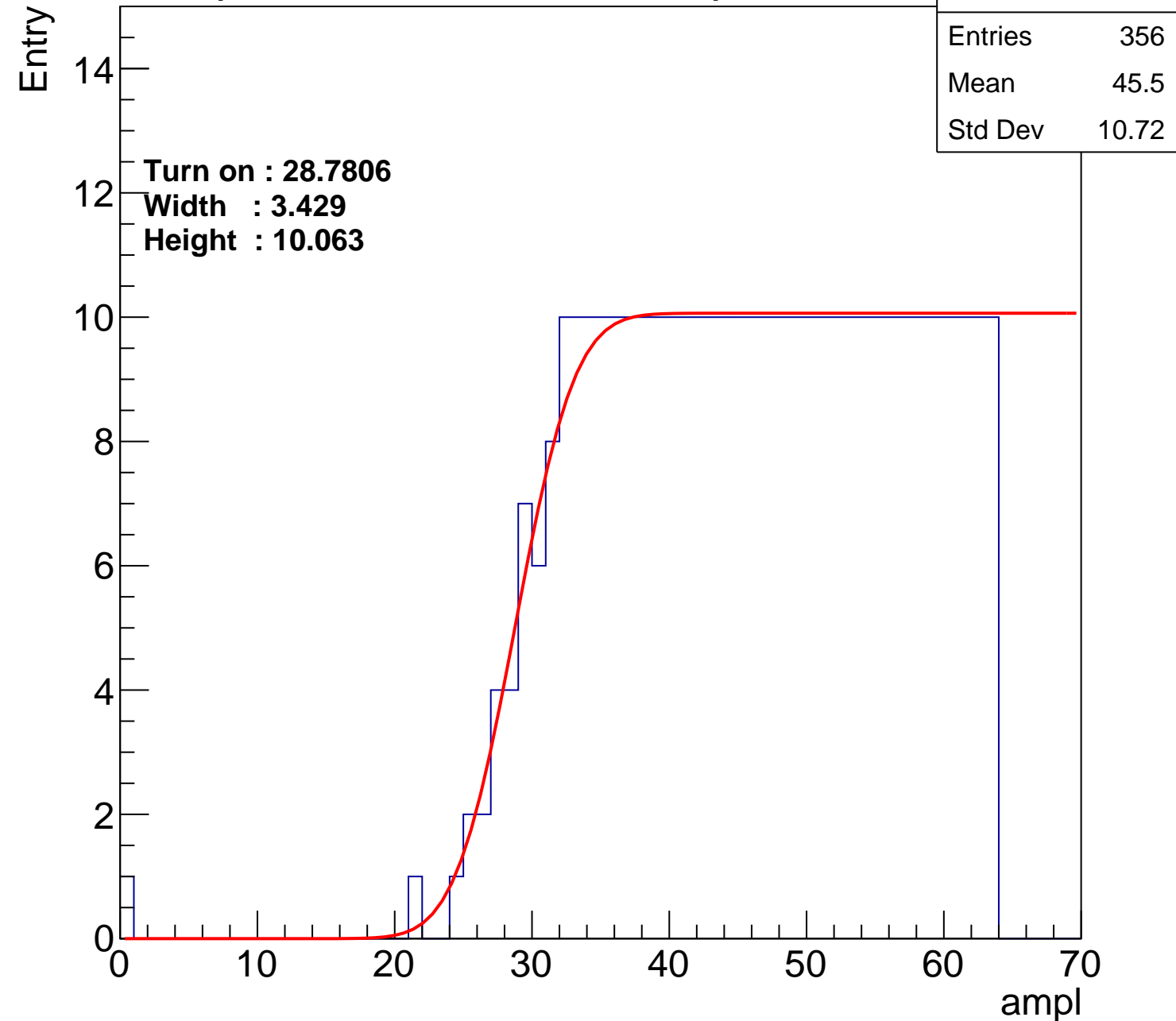
Width : 3.429

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44
Std Dev	11.72

Turn on : 25.8272

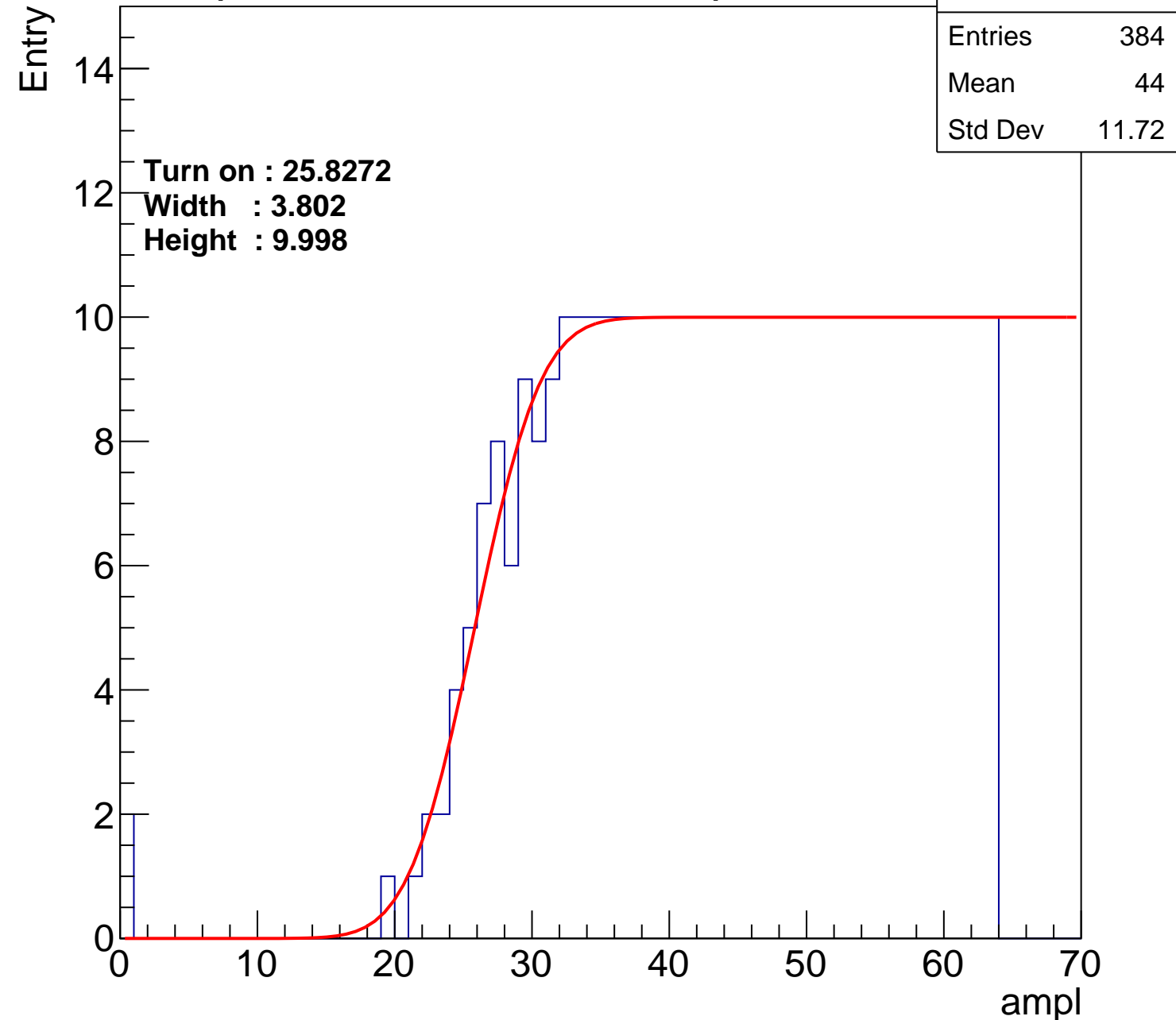
Width : 3.802

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.2
Std Dev	11.82

Turn on : 26.7495

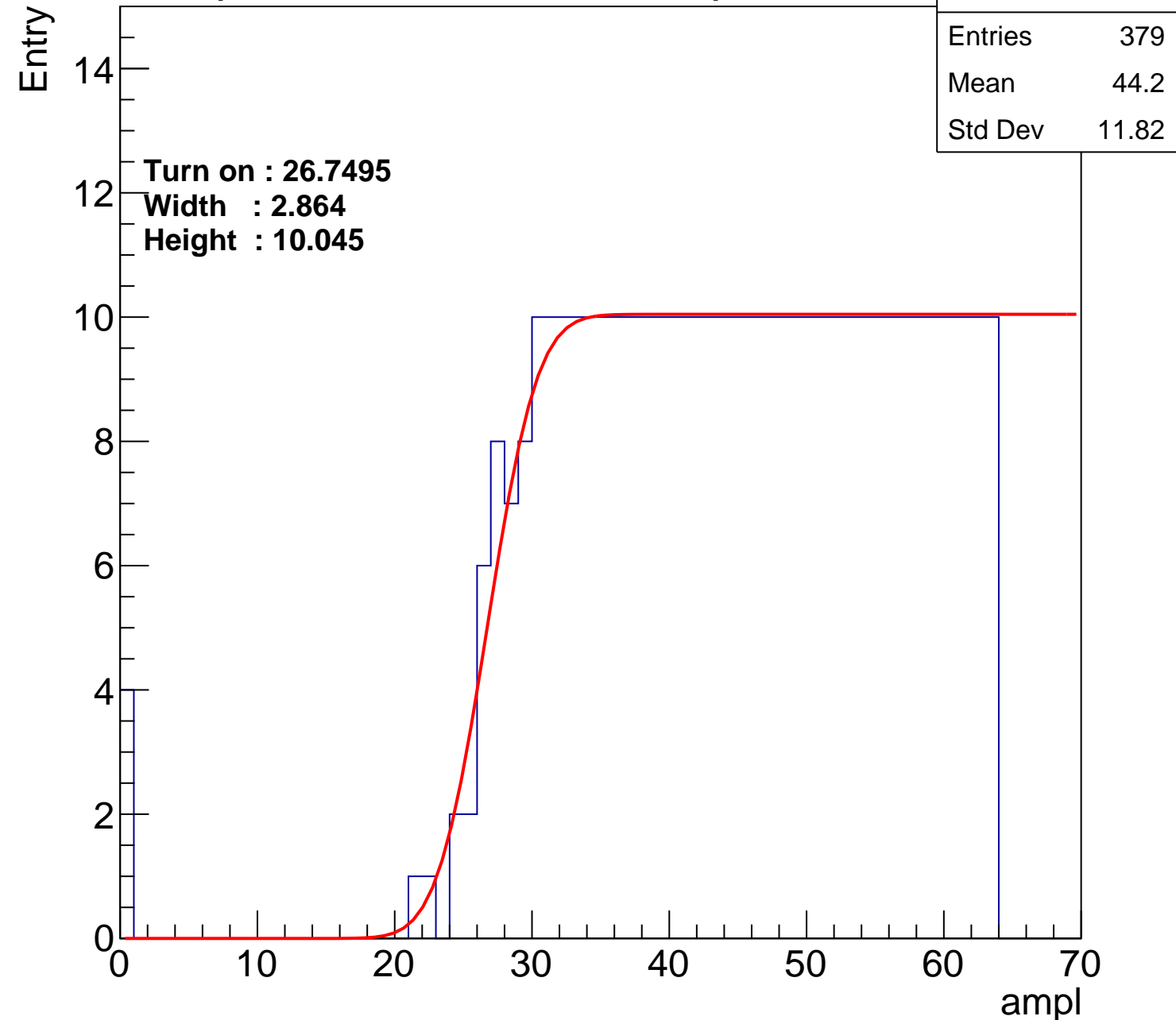
Width : 2.864

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.25
Std Dev	11.84

Turn on : 26.3448

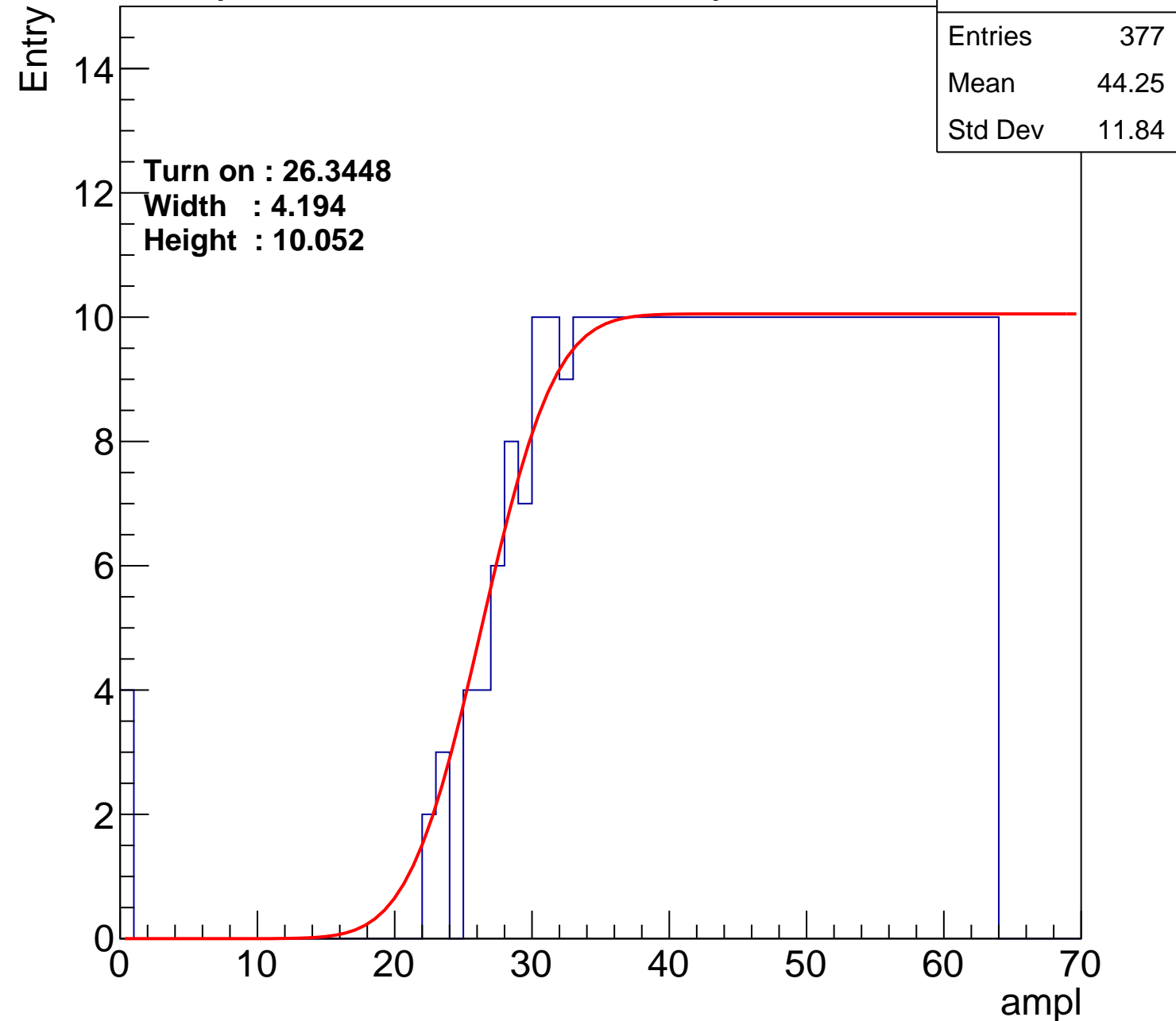
Width : 4.194

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
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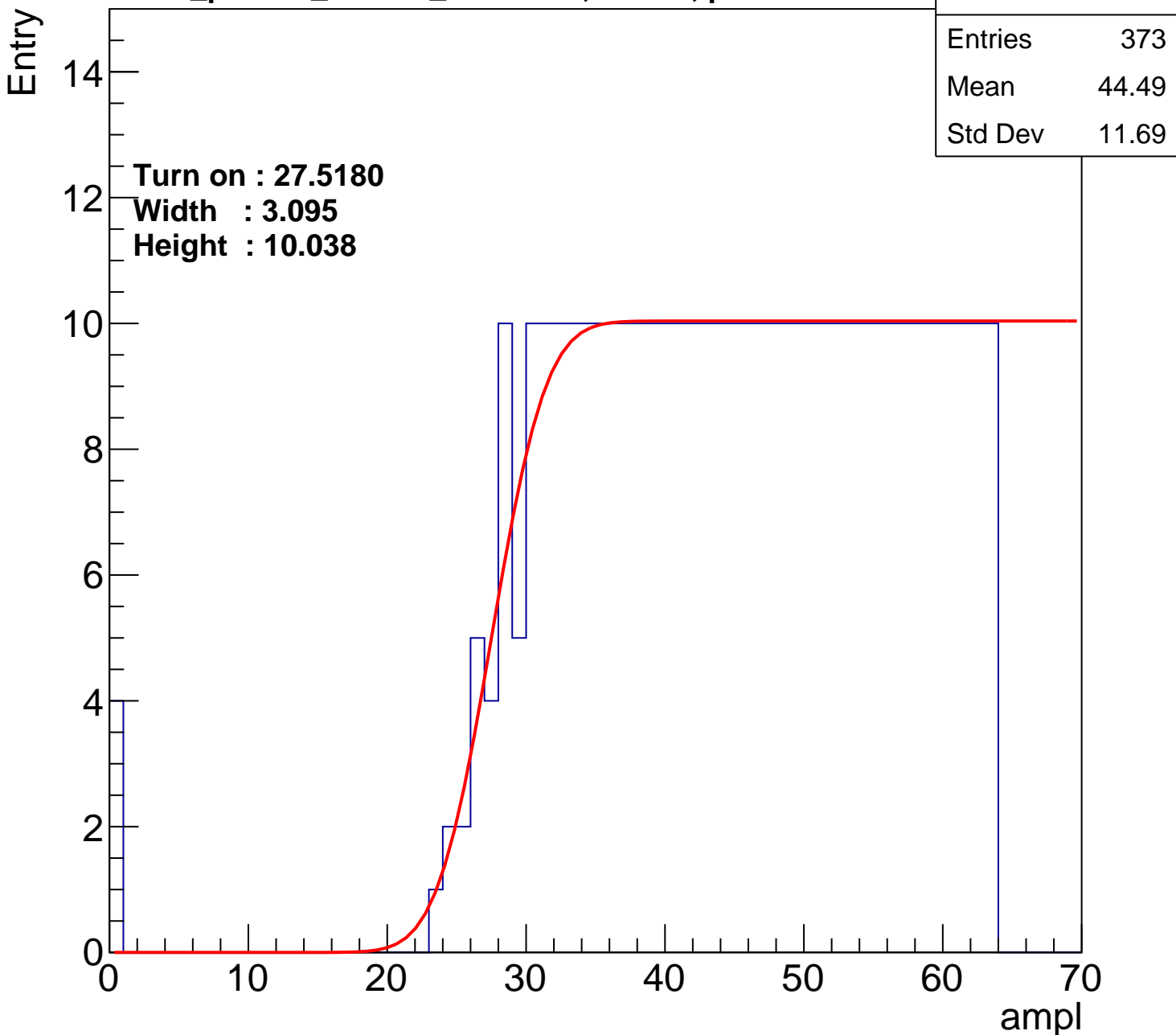
Mean	44.49
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Std Dev	11.69
---------	-------

Turn on : 27.5180

Width : 3.095

Height : 10.038



B0L102S, U10-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.1
Std Dev	11.48

Turn on : 25.5879

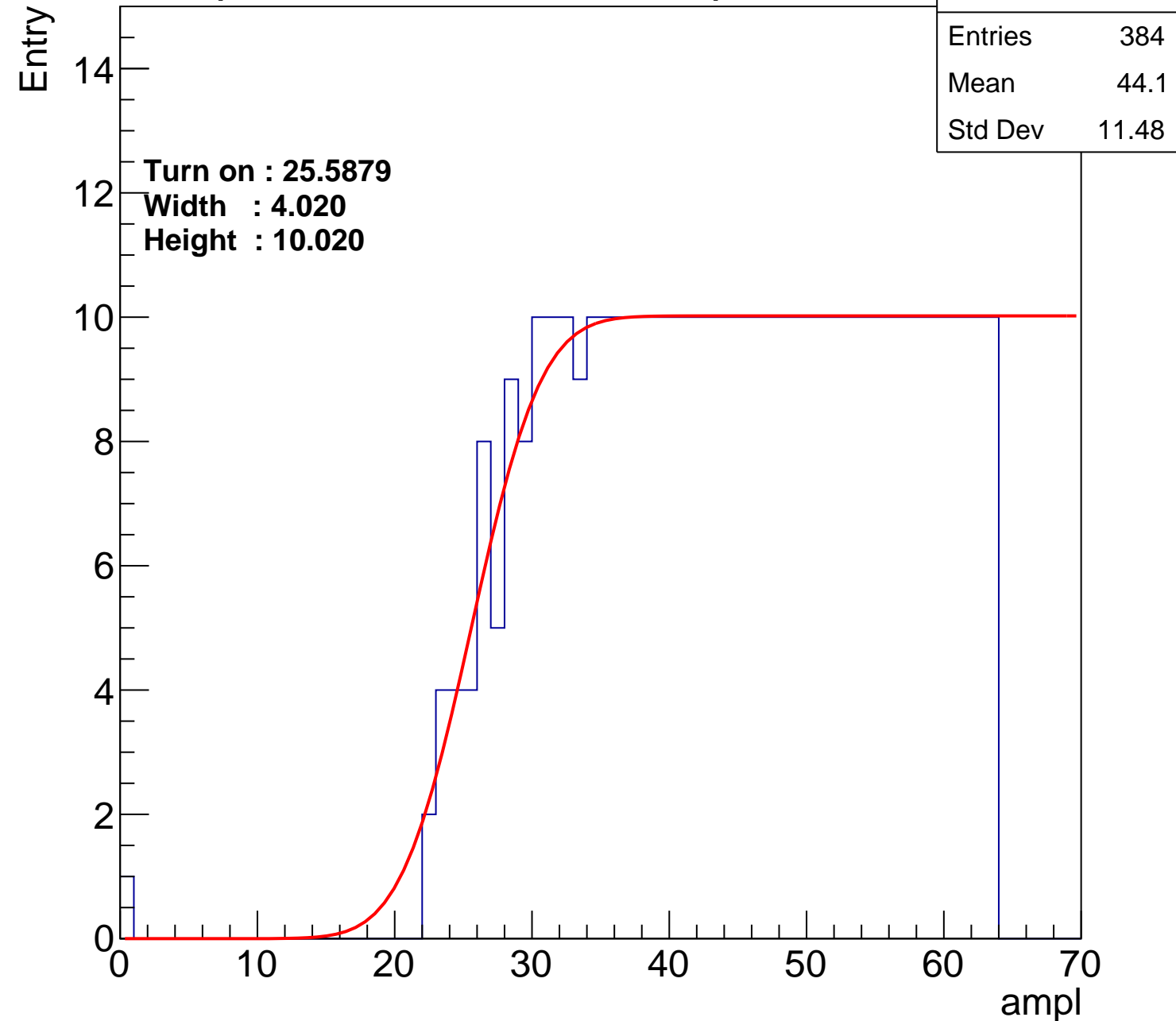
Width : 4.020

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.07
Std Dev	11.97

Turn on : 26.8693

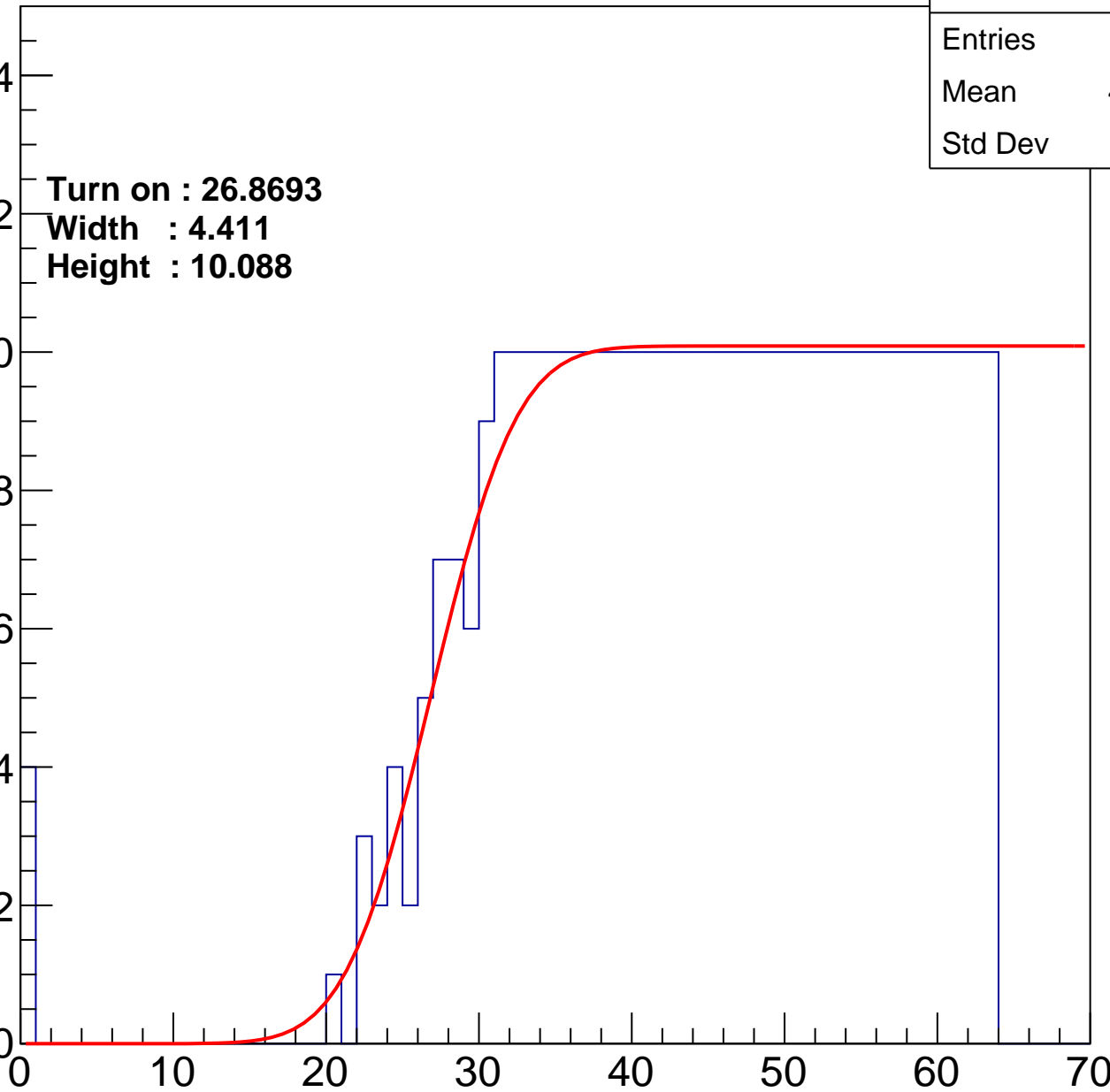
Width : 4.411

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.46
Std Dev	11.84

Turn on : 24.5448

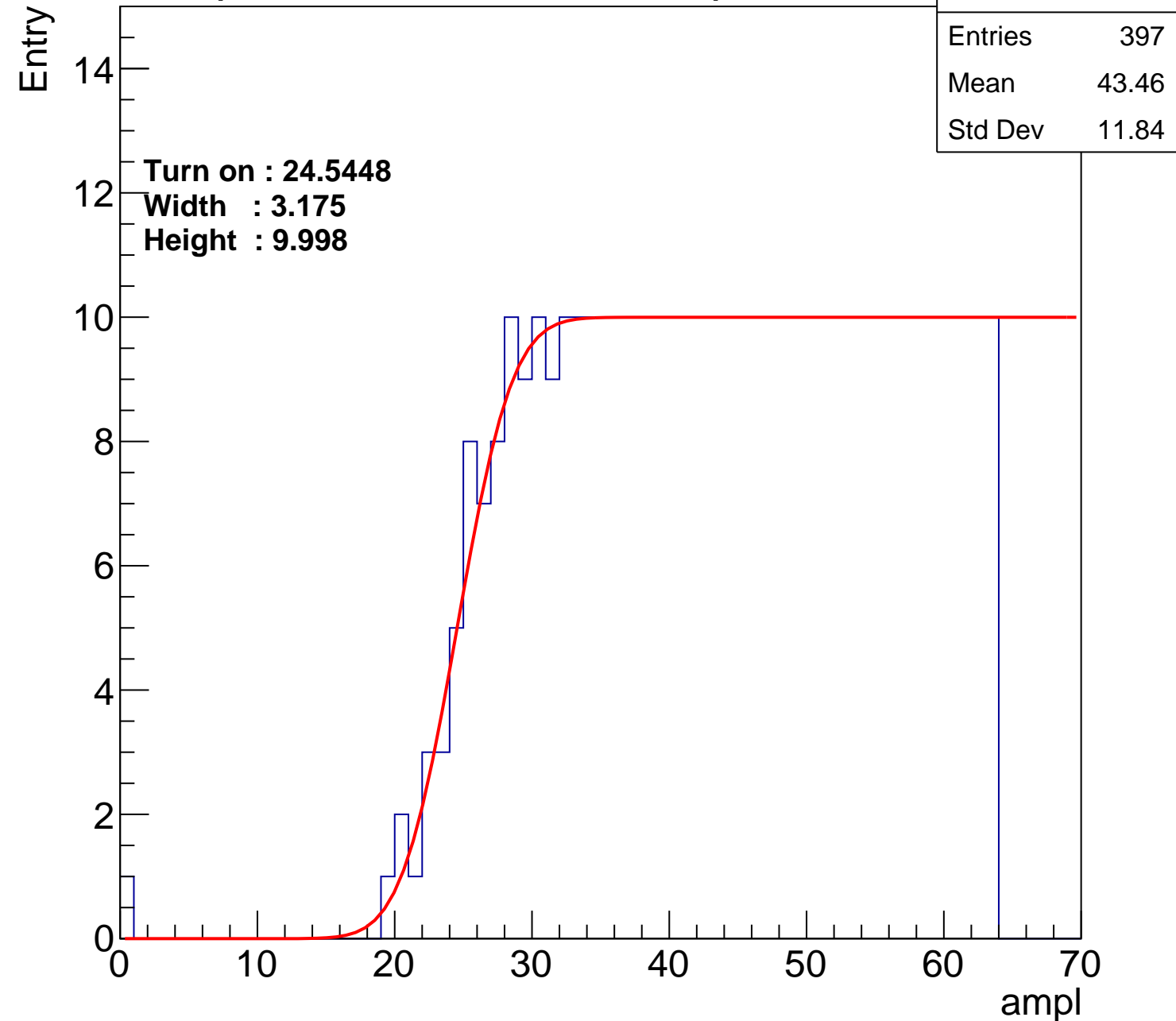
Width : 3.175

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch55

calib_packv5_042523_0143.root, FC#12, port B1

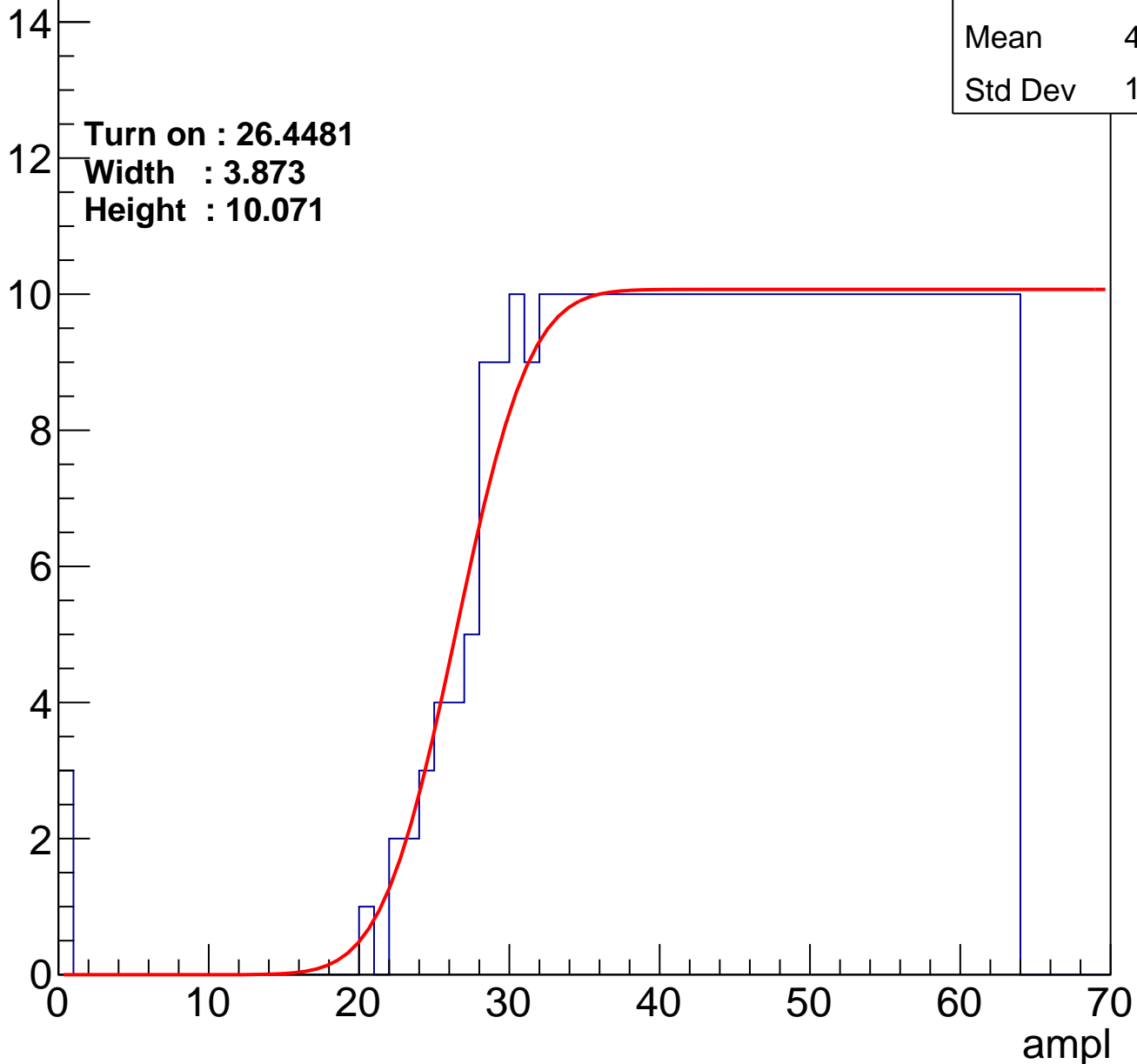
Entries	381
Mean	44.13
Std Dev	11.75

Turn on : 26.4481

Width : 3.873

Height : 10.071

Entry



B0L102S, U10-ch56

calib_packv5_042523_0143.root, FC#12, port B1

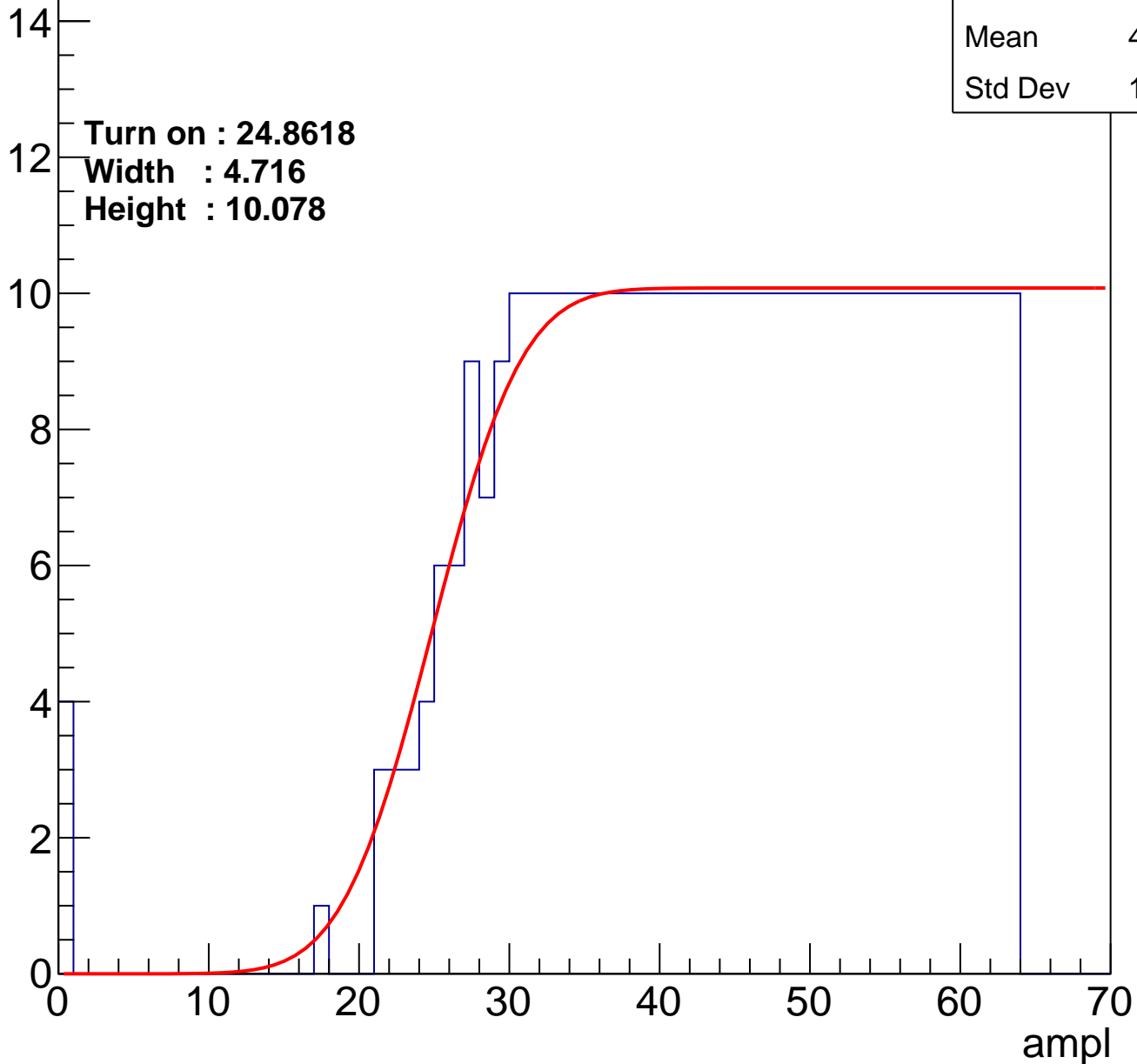
Entries	395
Mean	43.36
Std Dev	12.29

Turn on : 24.8618

Width : 4.716

Height : 10.078

Entry



B0L102S, U10-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 27.9660

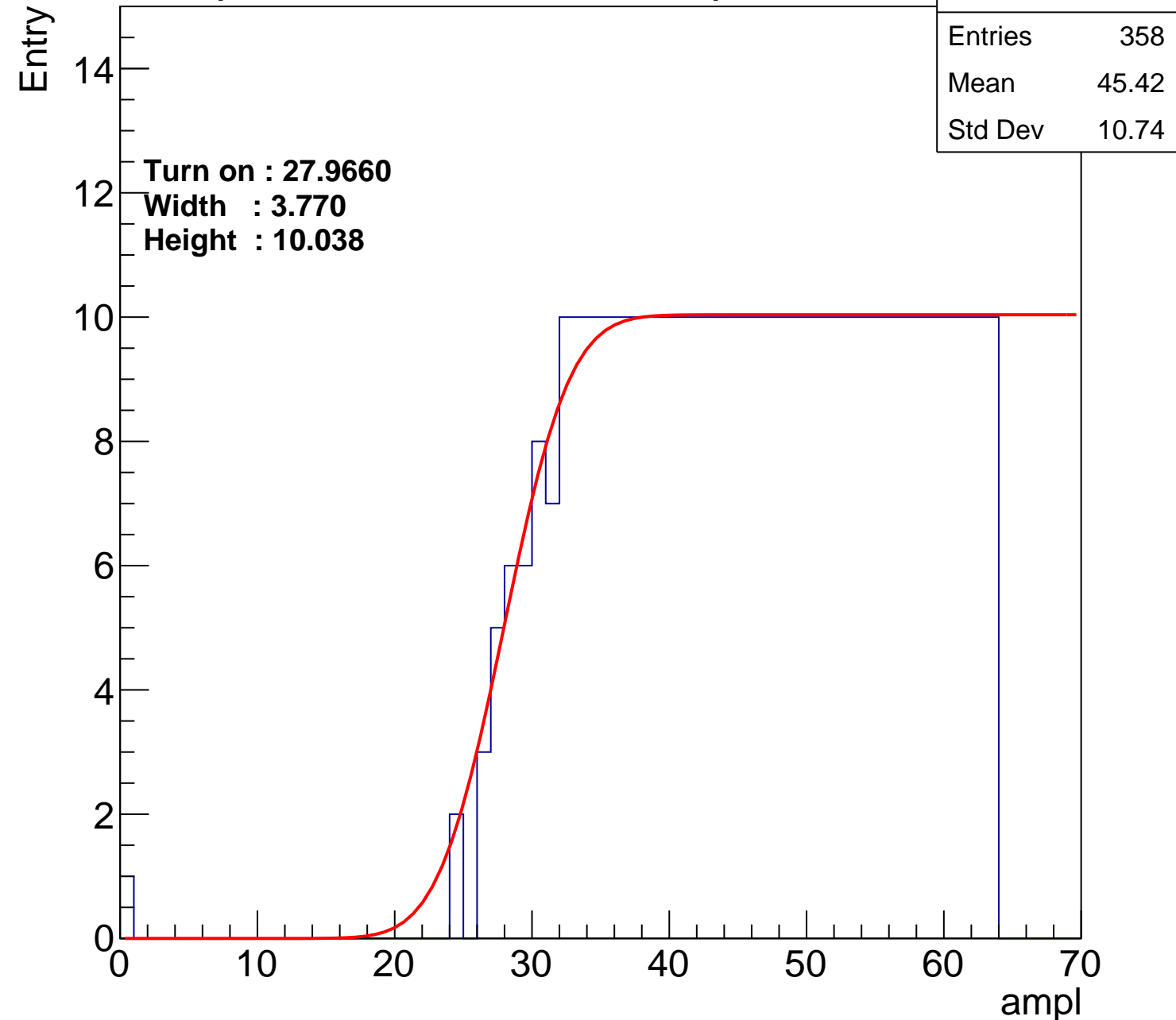
Width : 3.770

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	45.07
Std Dev	11

Turn on : 28.9959

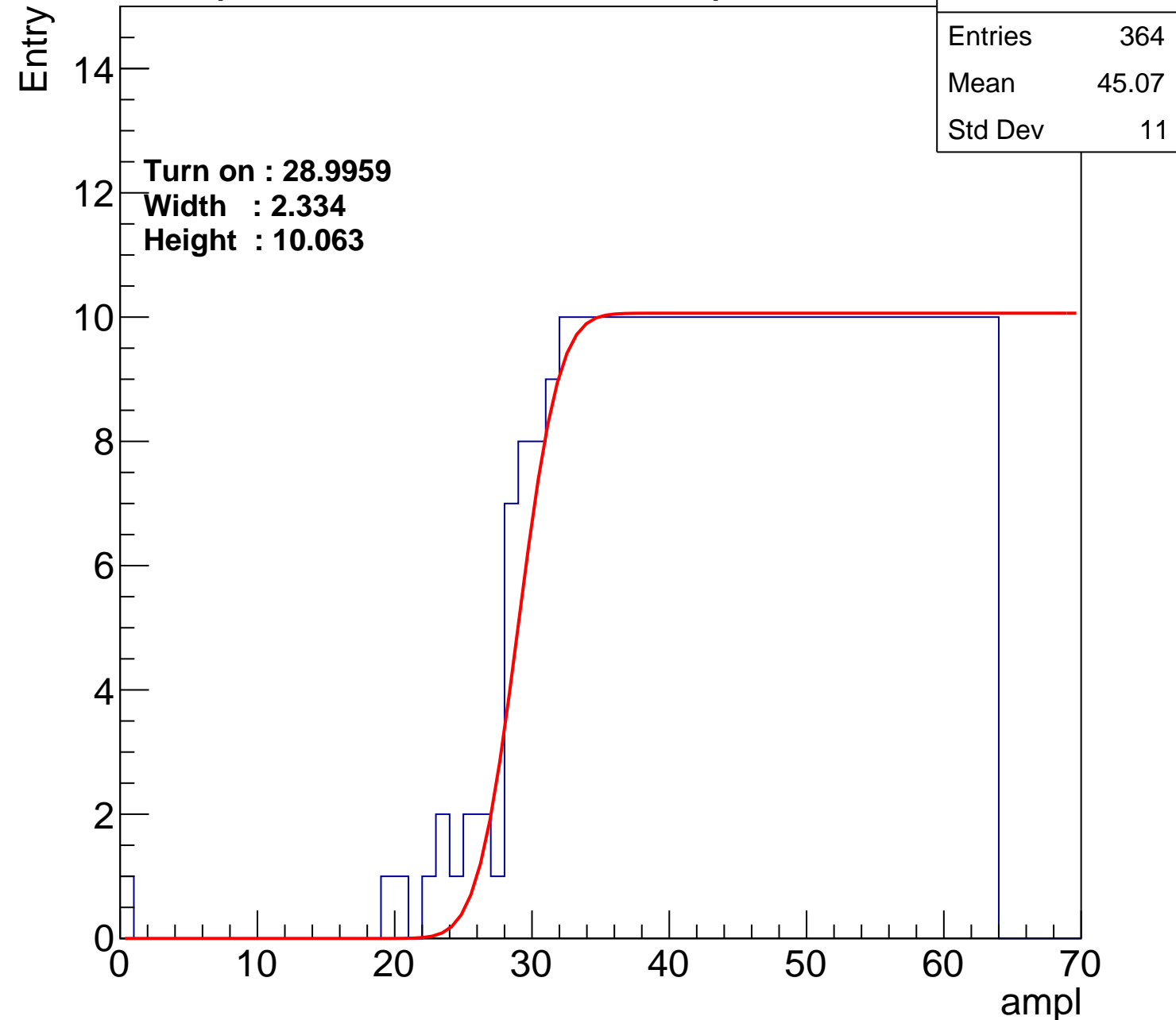
Width : 2.334

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.78
Std Dev	11.09

Turn on : 27.3340

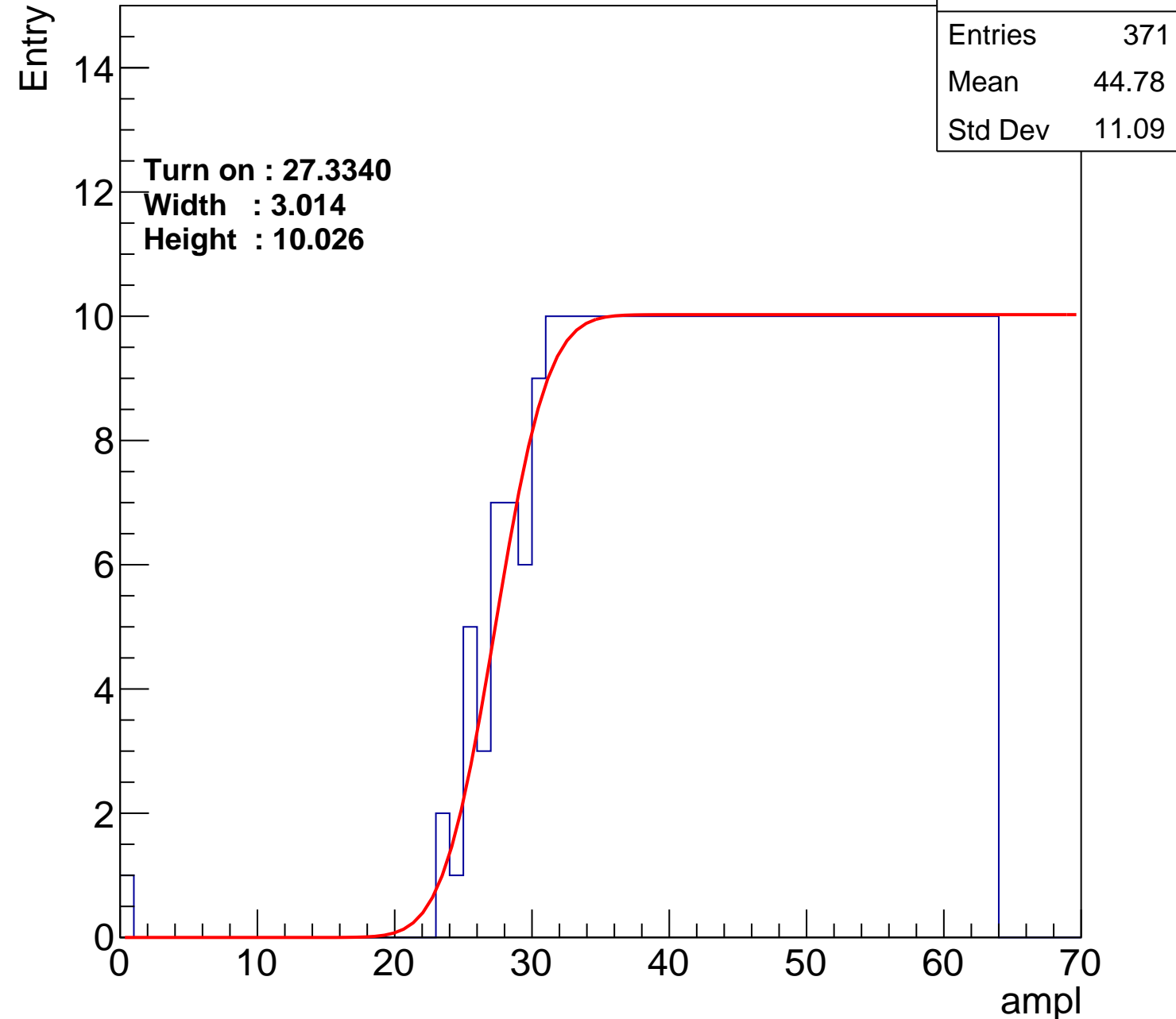
Width : 3.014

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.59
Std Dev	12.06

Turn on : 25.0104

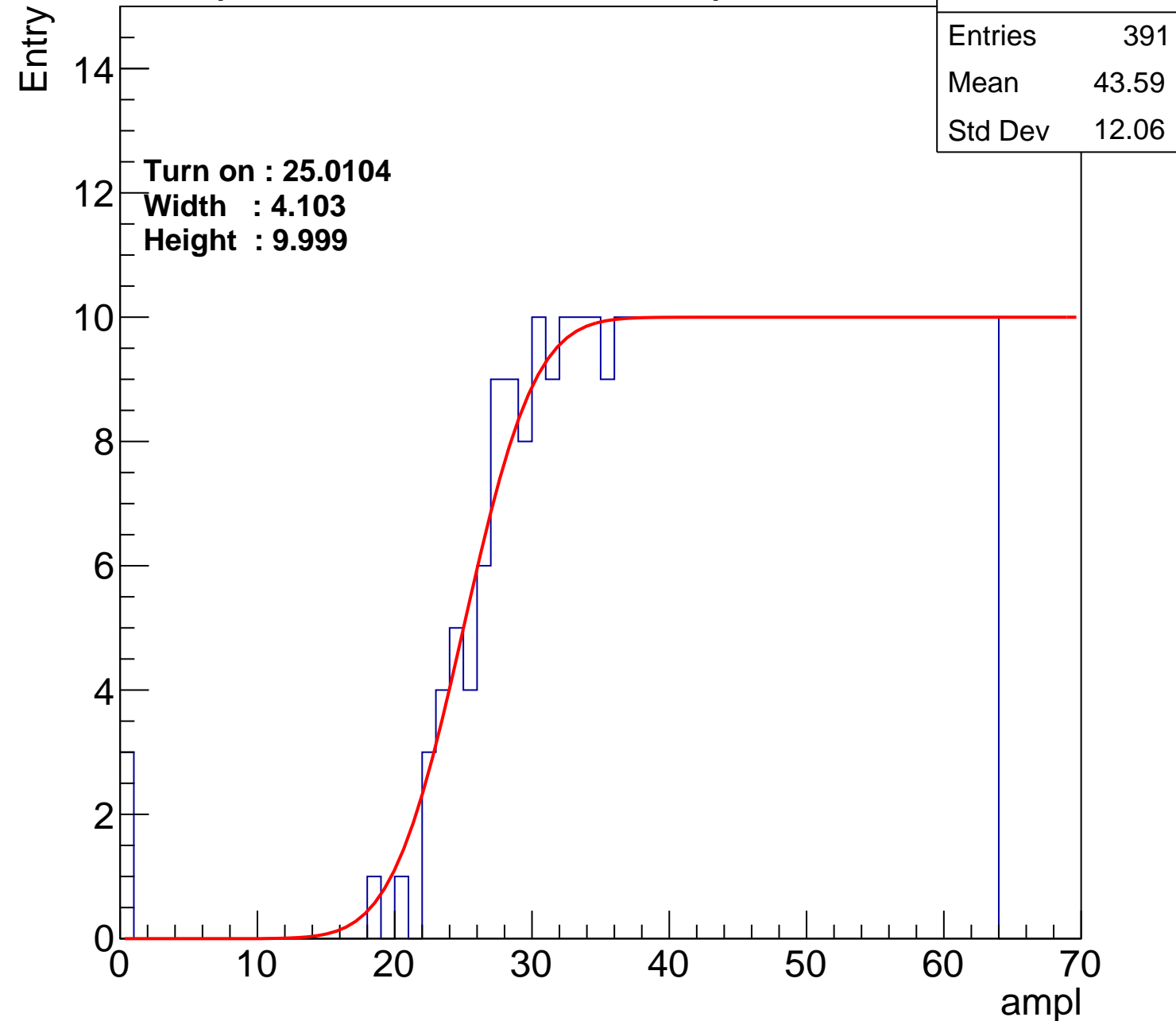
Width : 4.103

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.74
Std Dev	11.75

Turn on : 28.2487

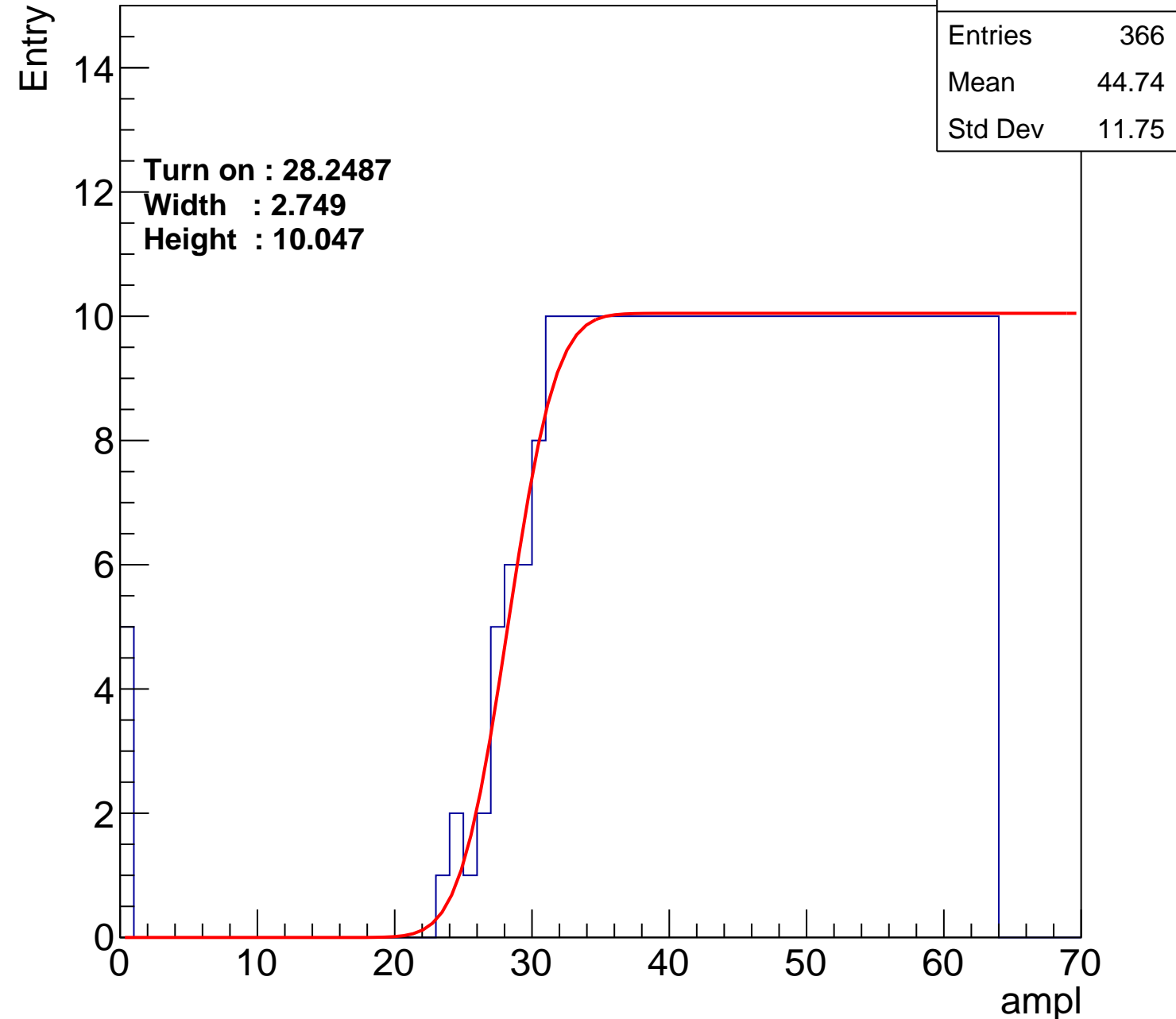
Width : 2.749

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch62

calib_packv5_042523_0143.root, FC#12, port B1

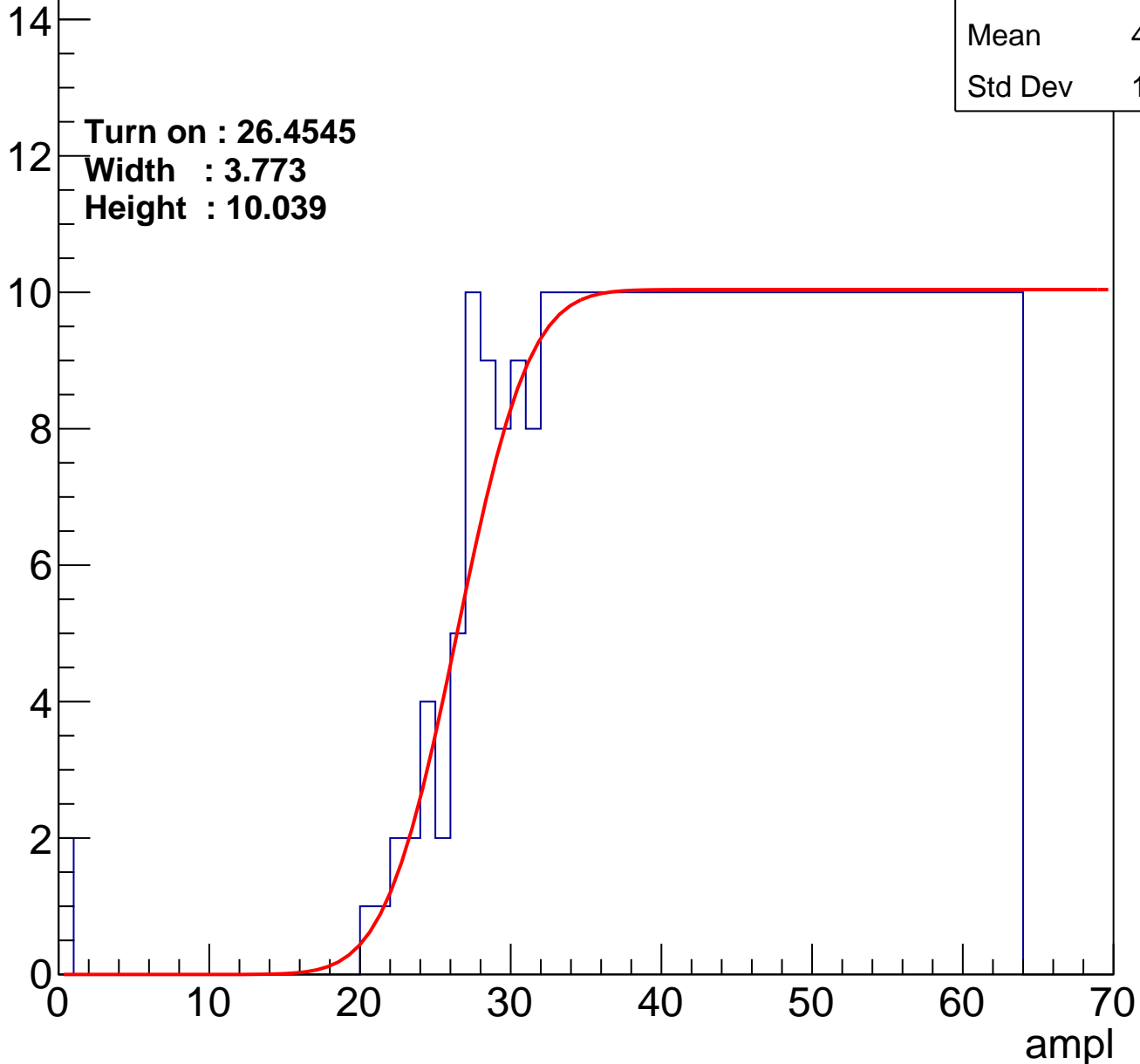
Entries	383
Mean	44.07
Std Dev	11.66

Turn on : 26.4545

Width : 3.773

Height : 10.039

Entry



B0L102S, U10-ch63

calib_packv5_042523_0143.root, FC#12, port B1

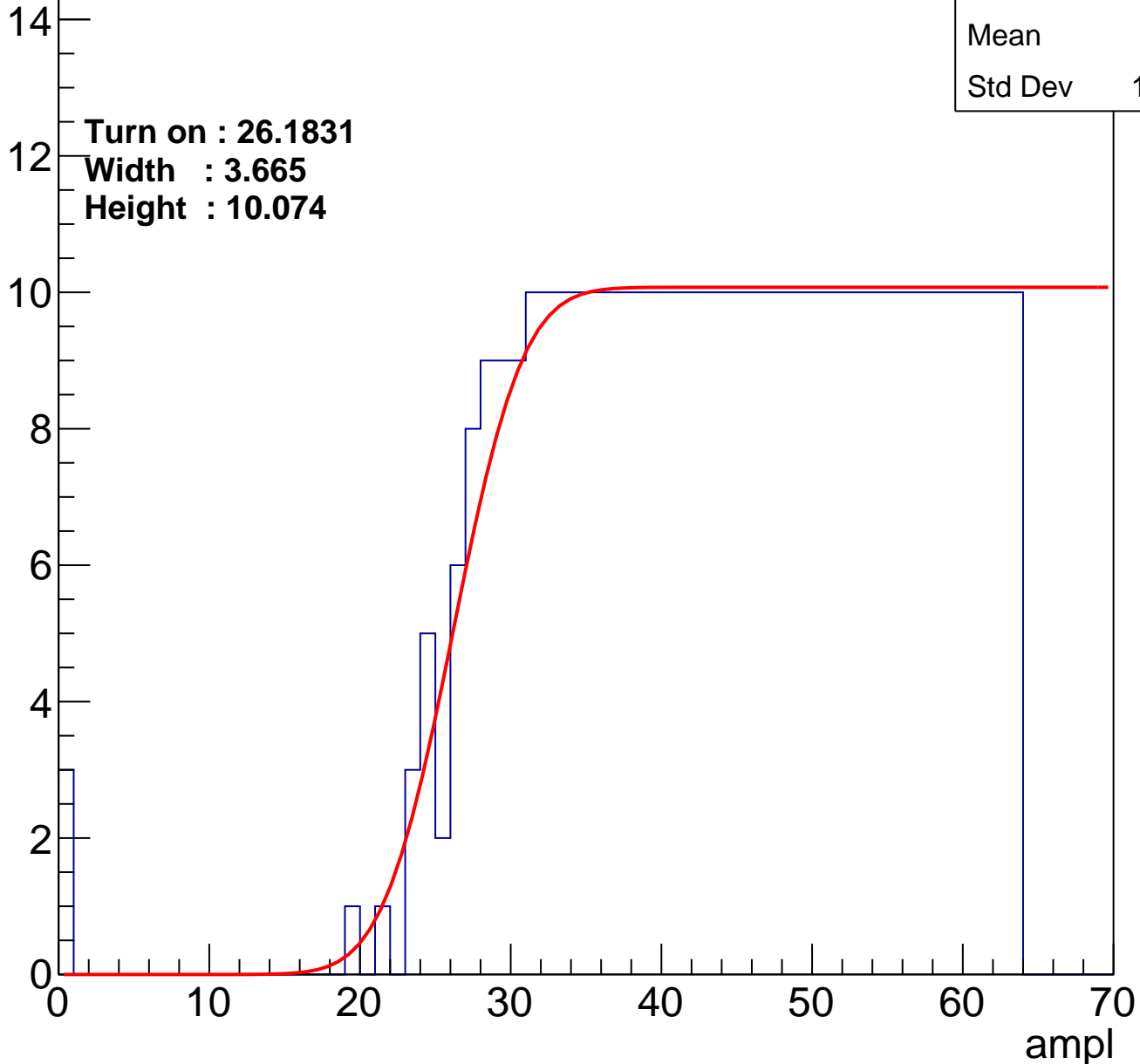
Entries	386
Mean	43.9
Std Dev	11.85

Turn on : 26.1831

Width : 3.665

Height : 10.074

Entry



B0L102S, U10-ch64

calib_packv5_042523_0143.root, FC#12, port B1

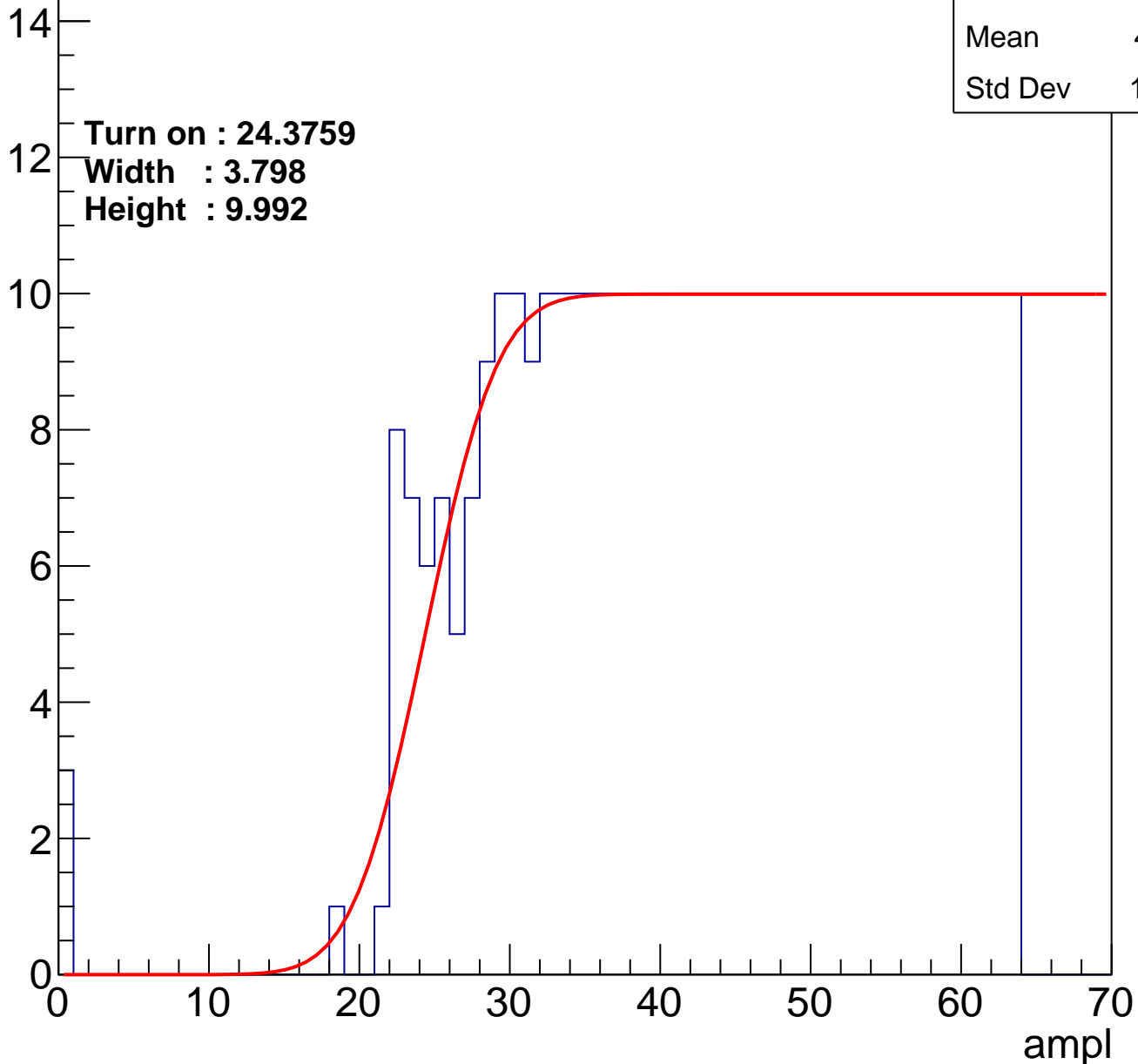
Entries	403
Mean	43.01
Std Dev	12.34

Turn on : 24.3759

Width : 3.798

Height : 9.992

Entry



B0L102S, U10-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.87
Std Dev	11.19

Turn on : 28.3129

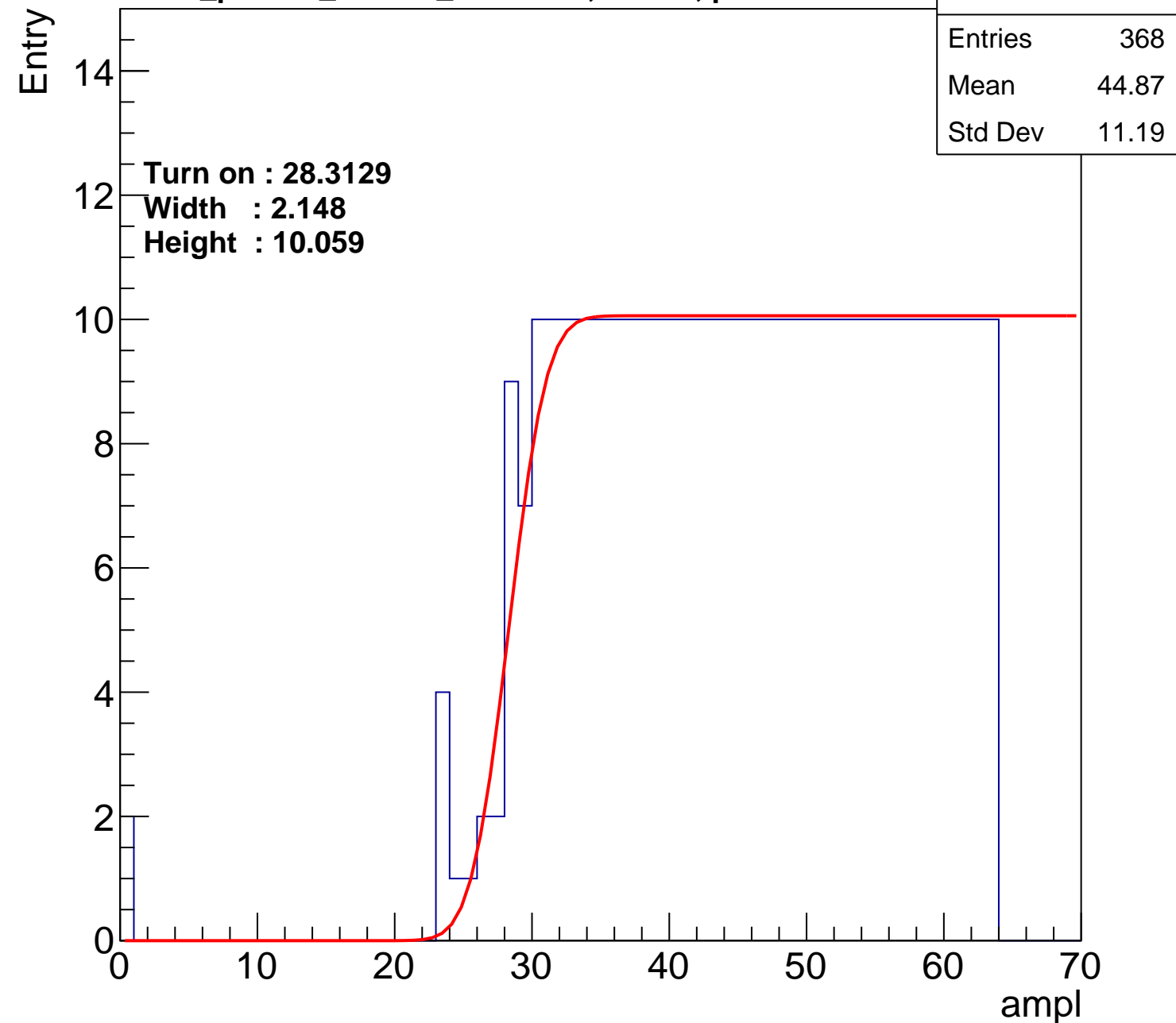
Width : 2.148

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.77
Std Dev	11.29

Turn on : 27.7638

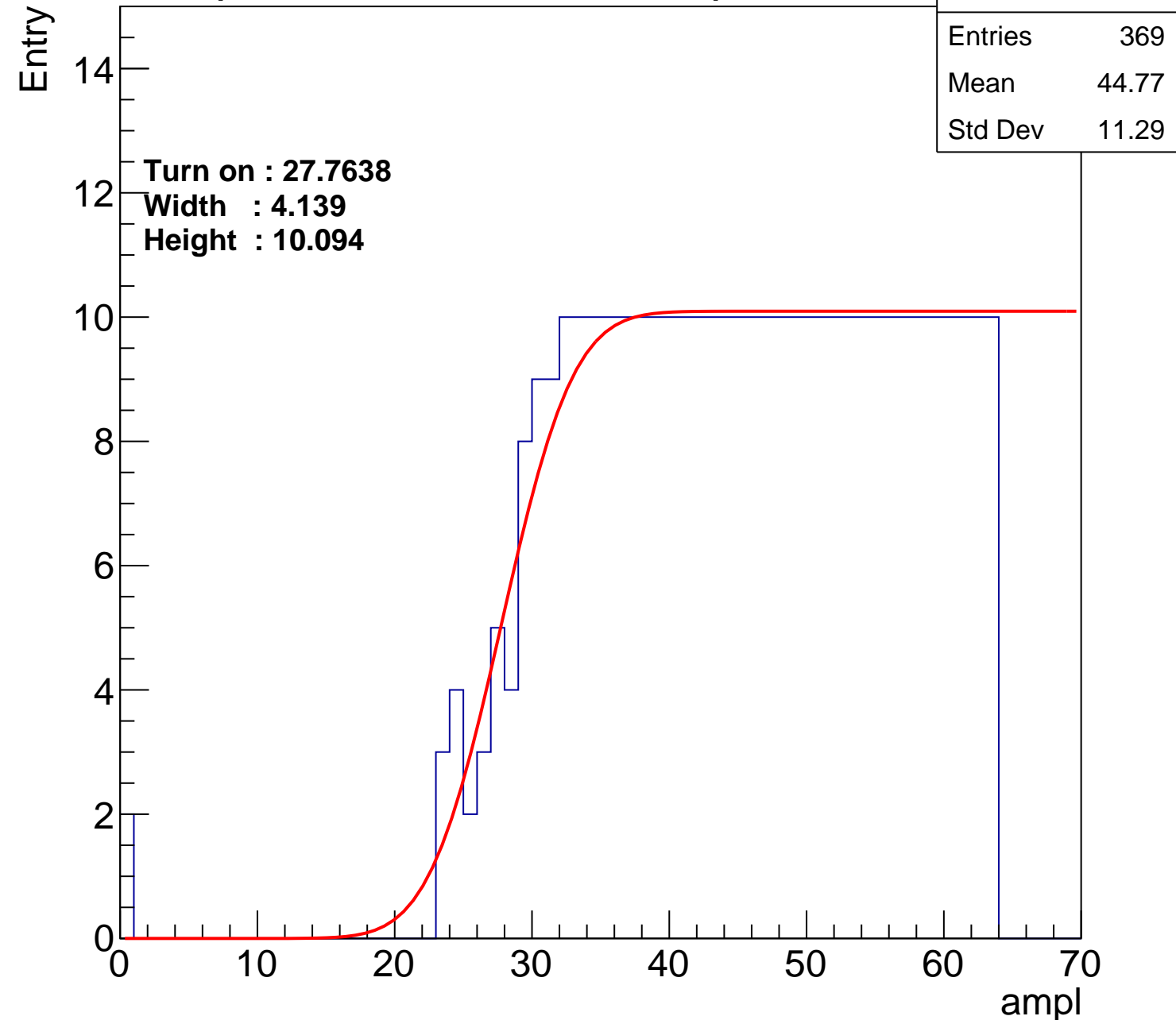
Width : 4.139

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.8279

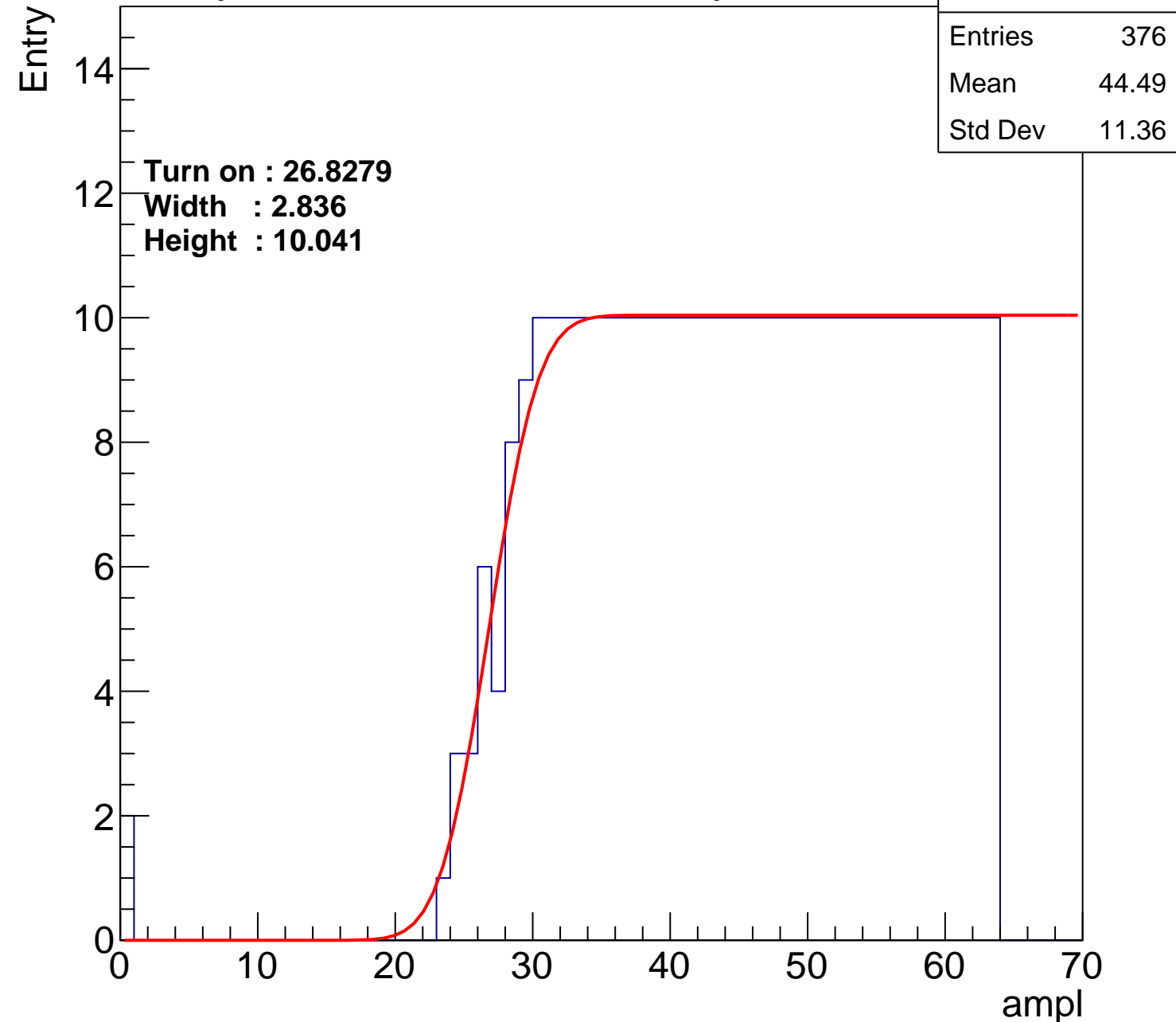
Width : 2.836

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 25.5040

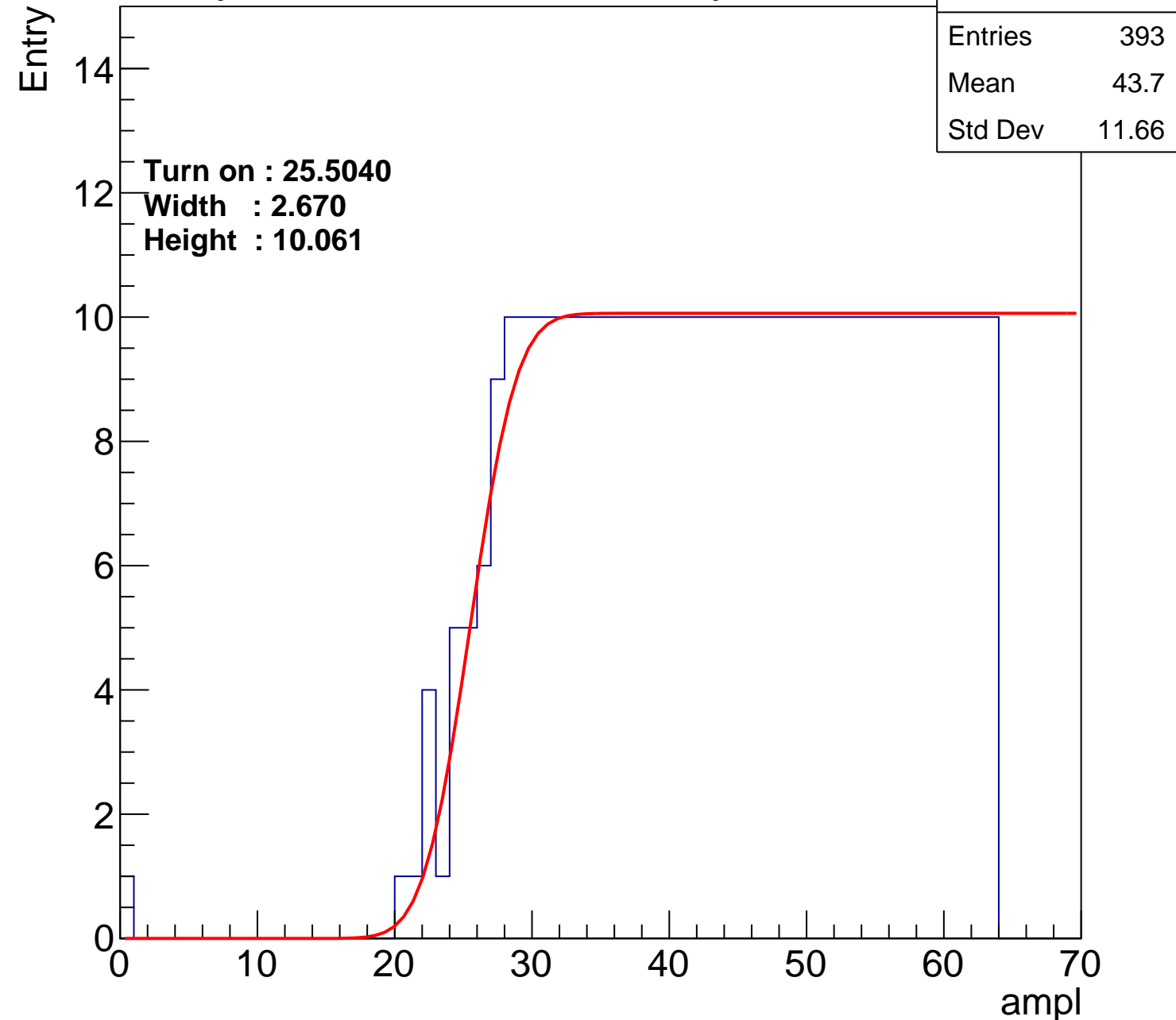
Width : 2.670

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.4285

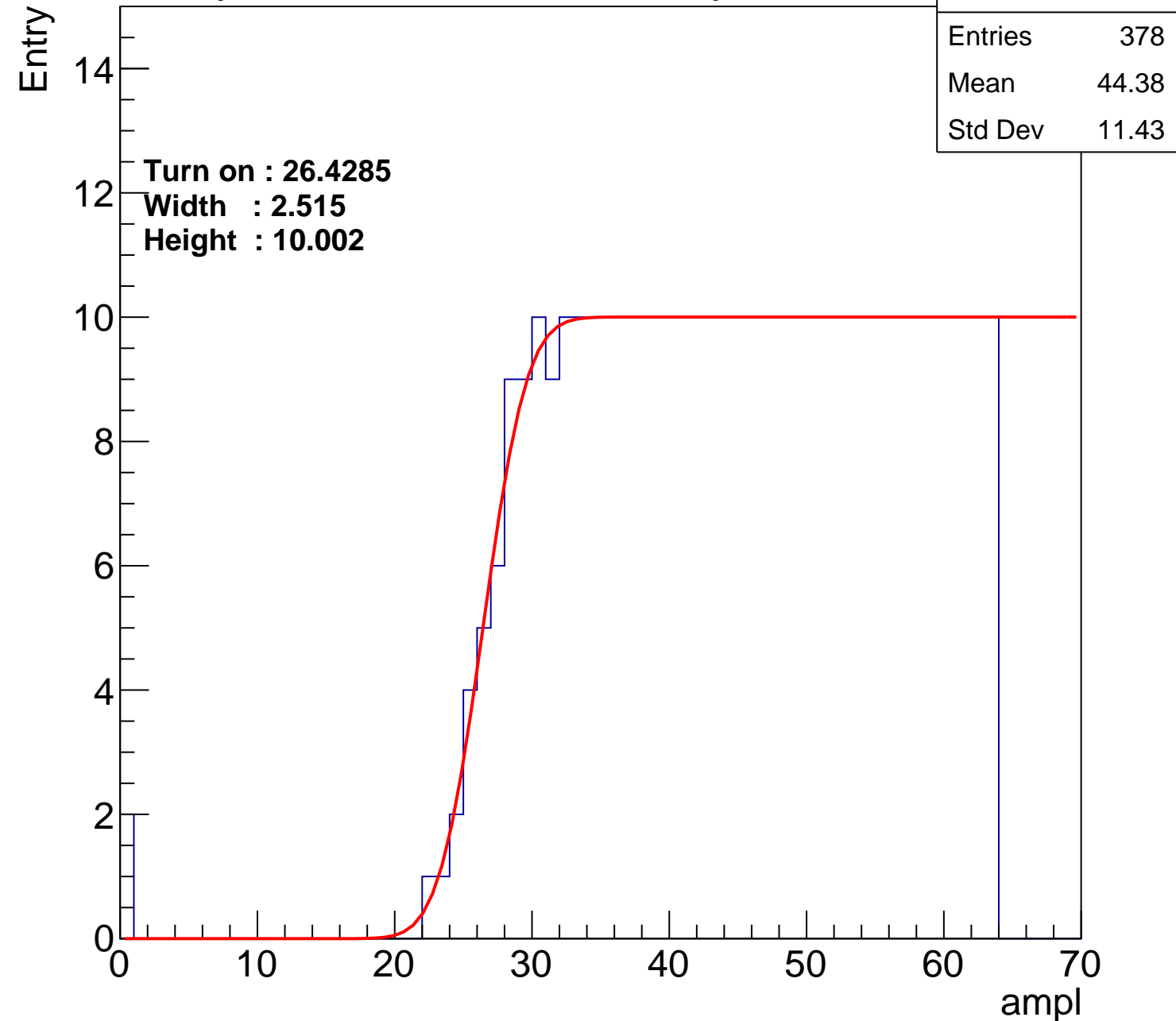
Width : 2.515

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.42
Std Dev	11.31

Turn on : 26.6244

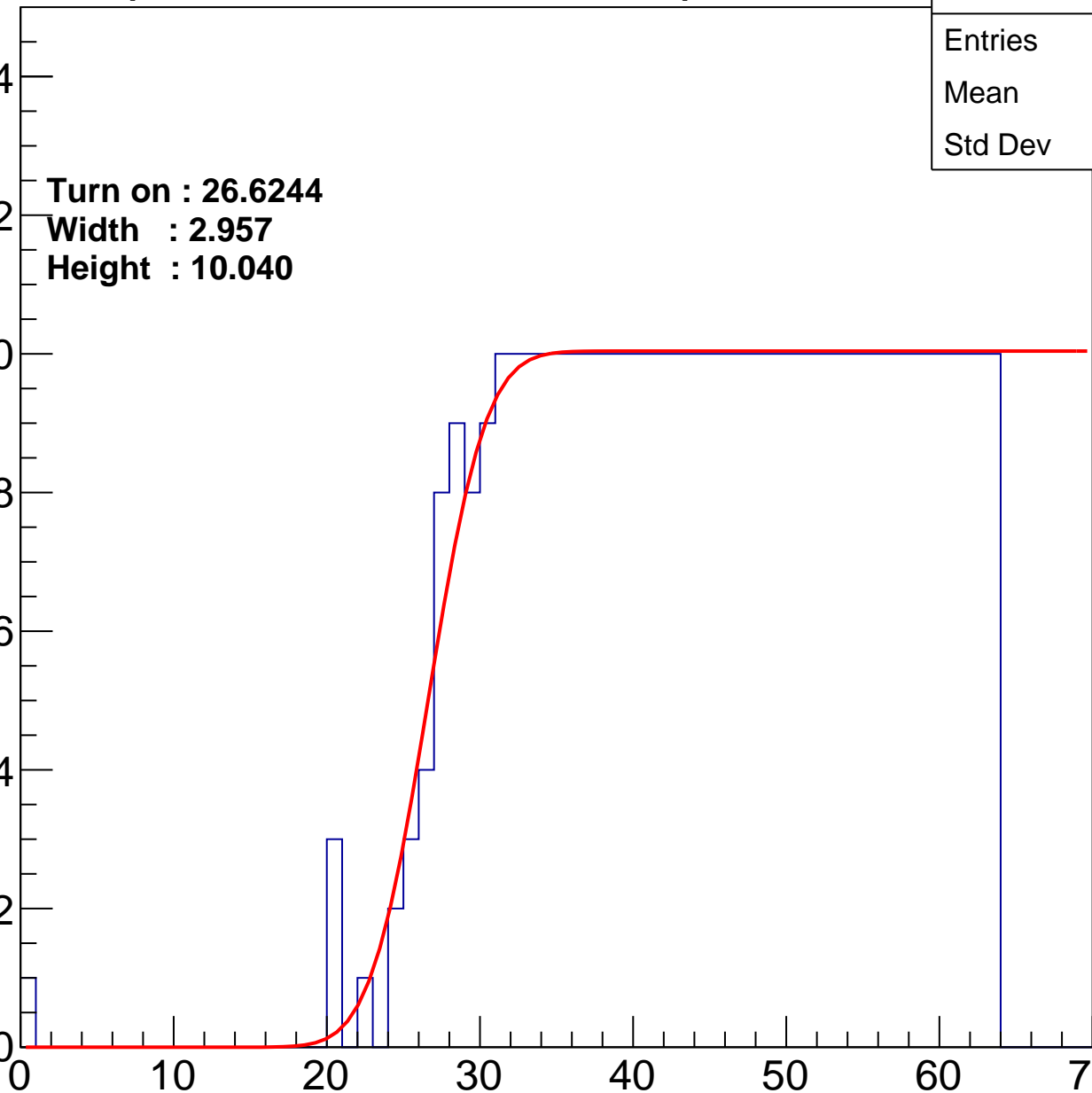
Width : 2.957

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch71

calib_packv5_042523_0143.root, FC#12, port B1

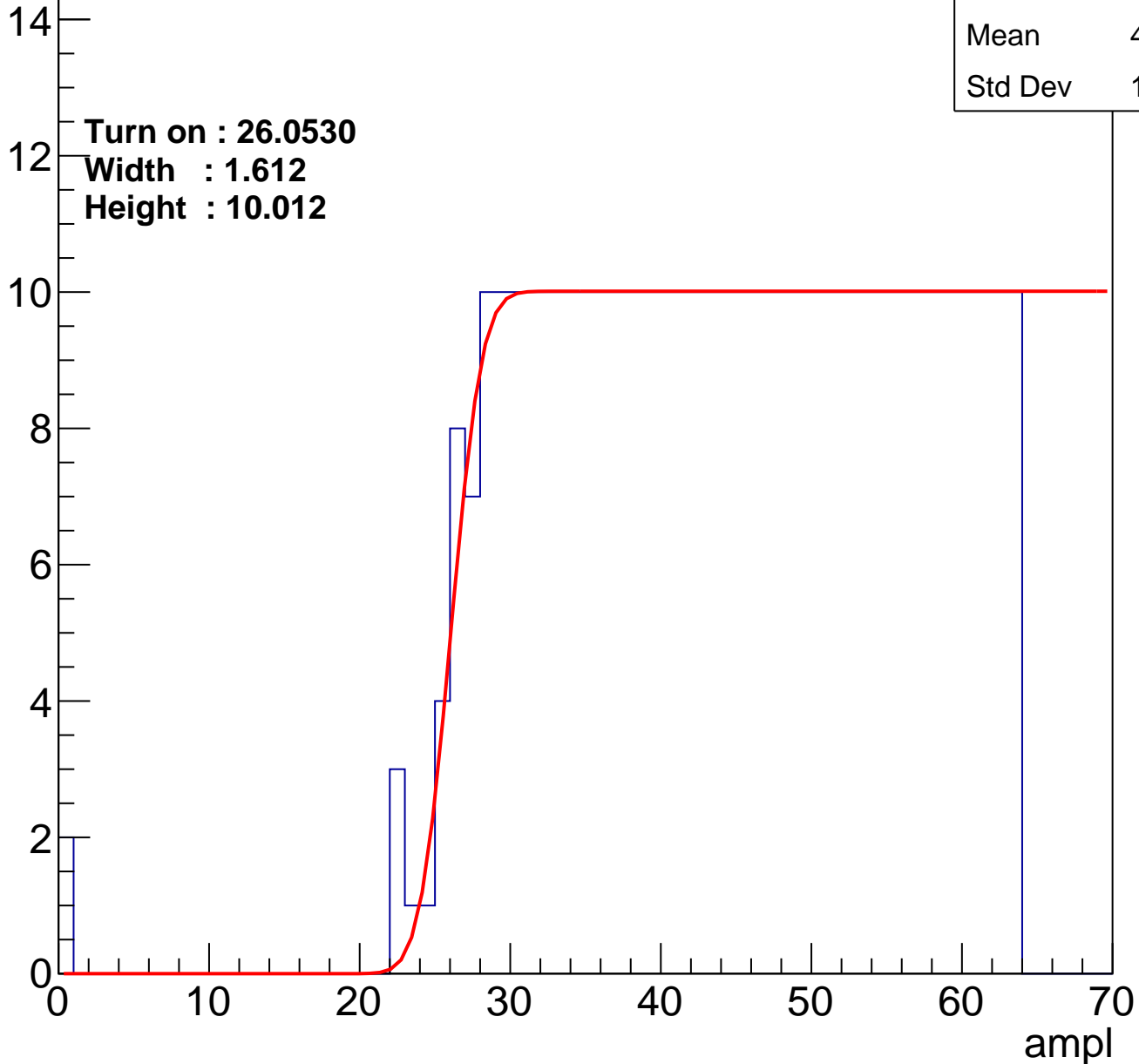
Entries	386
Mean	44.02
Std Dev	11.59

Turn on : 26.0530

Width : 1.612

Height : 10.012

Entry



B0L102S, U10-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.7
Std Dev	12.48

Turn on : 23.3564

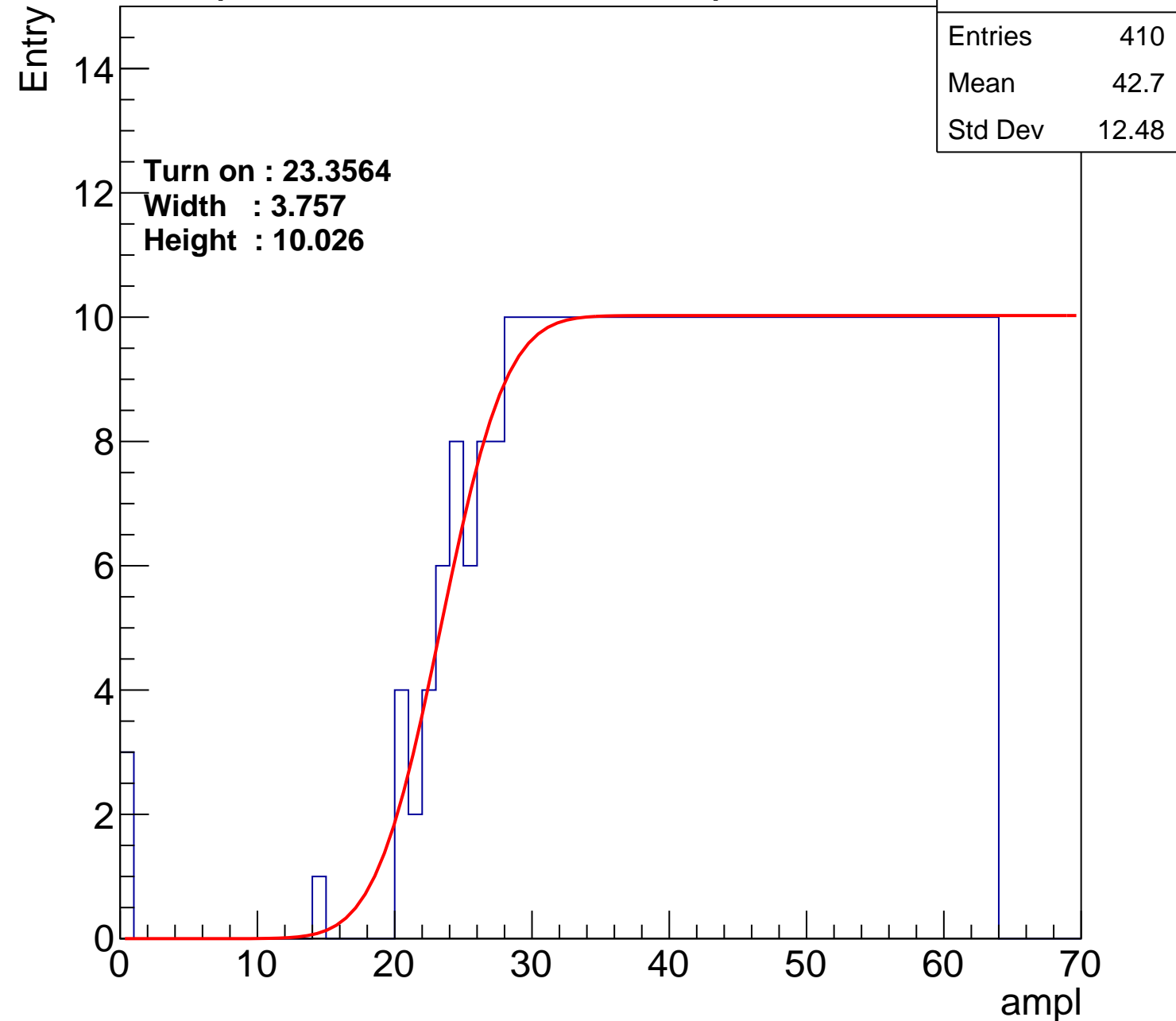
Width : 3.757

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.26
Std Dev	11.66

Turn on : 26.5616

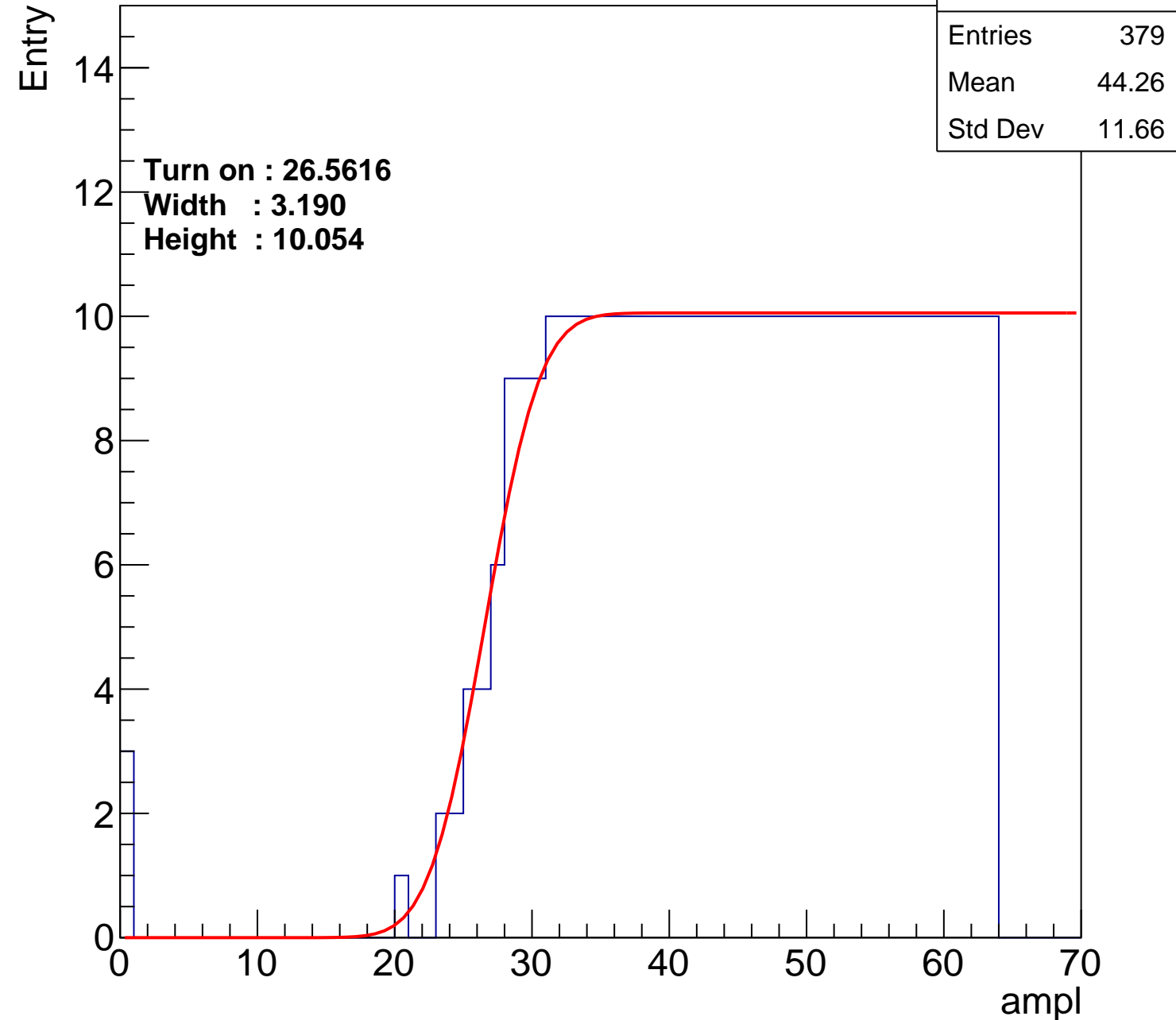
Width : 3.190

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.46
Std Dev	11.92

Turn on : 27.9861

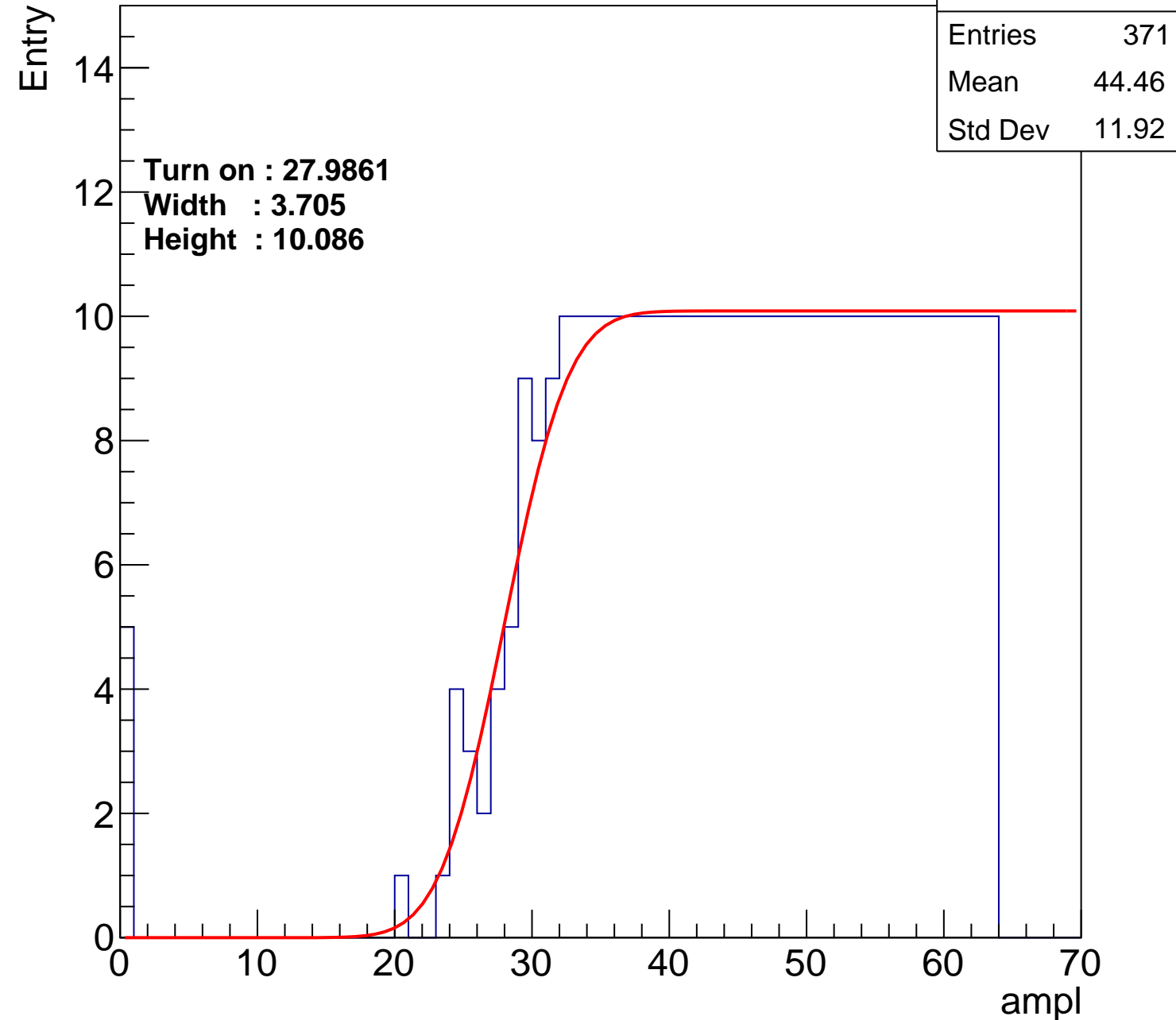
Width : 3.705

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	45.01
Std Dev	10.98

Turn on : 27.8868

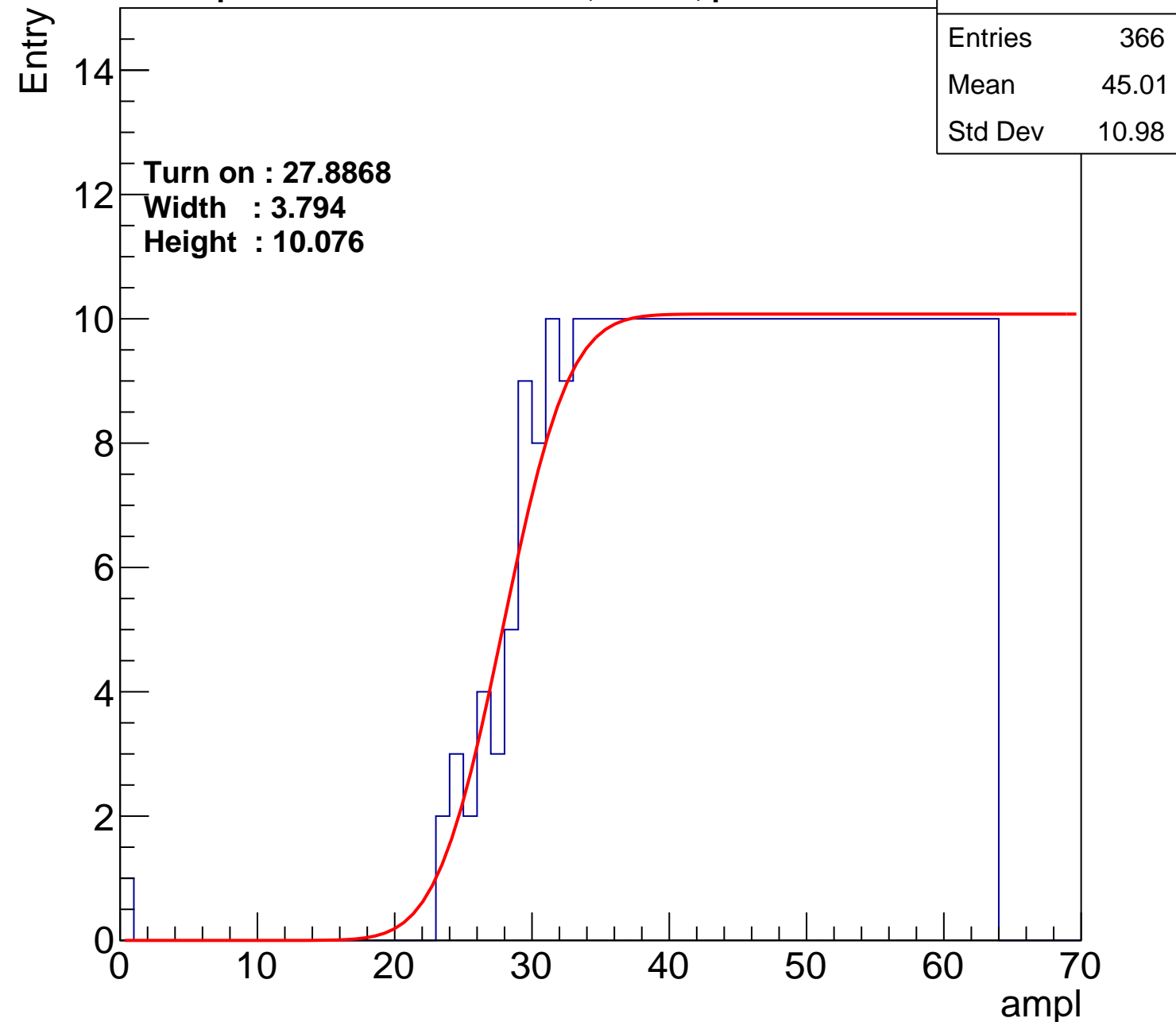
Width : 3.794

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch76

calib_packv5_042523_0143.root, FC#12, port B1

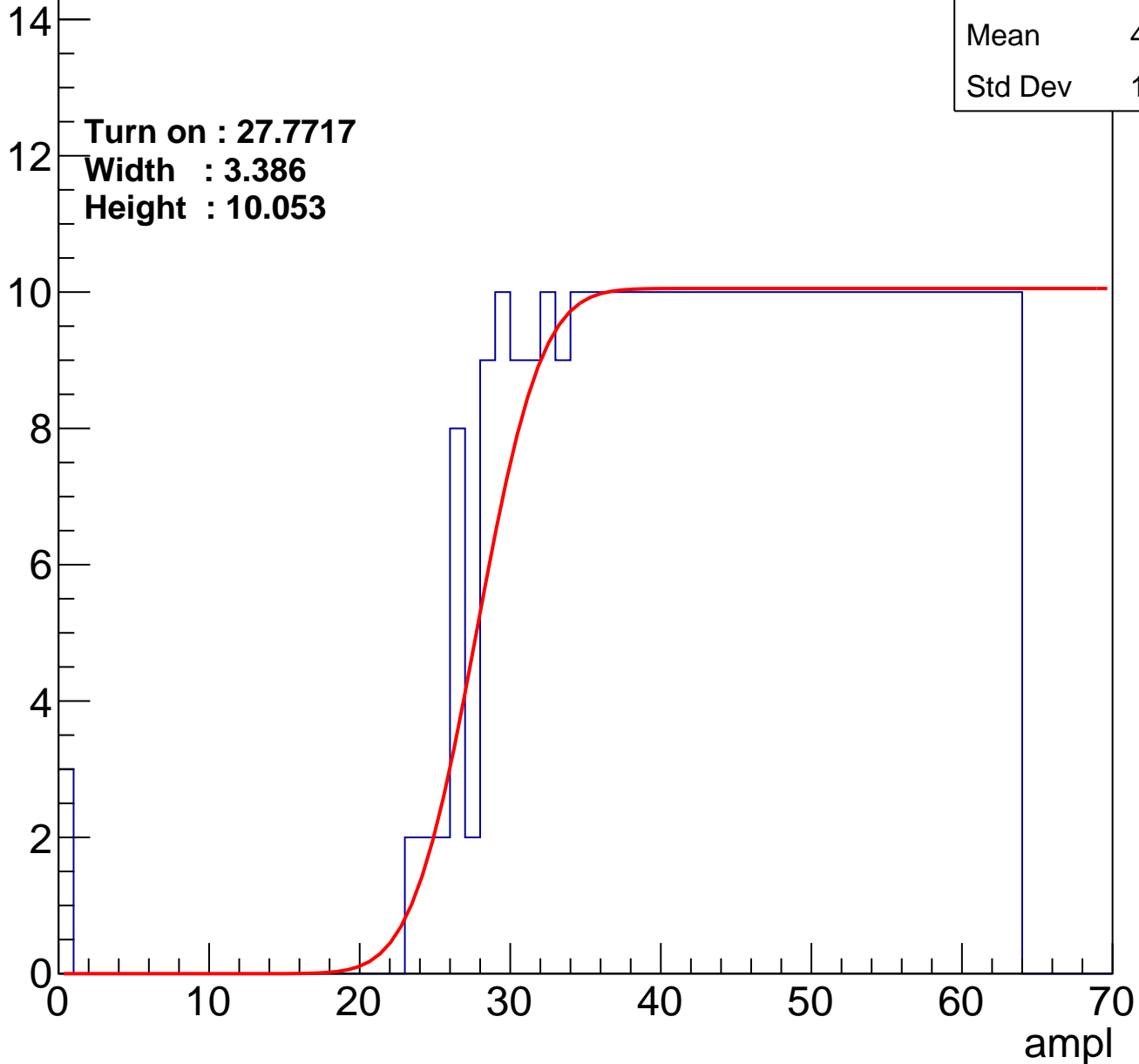
Entries	375
Mean	44.44
Std Dev	11.57

Turn on : 27.7717

Width : 3.386

Height : 10.053

Entry



B0L102S, U10-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.16
Std Dev	12.23

Turn on : 24.3785

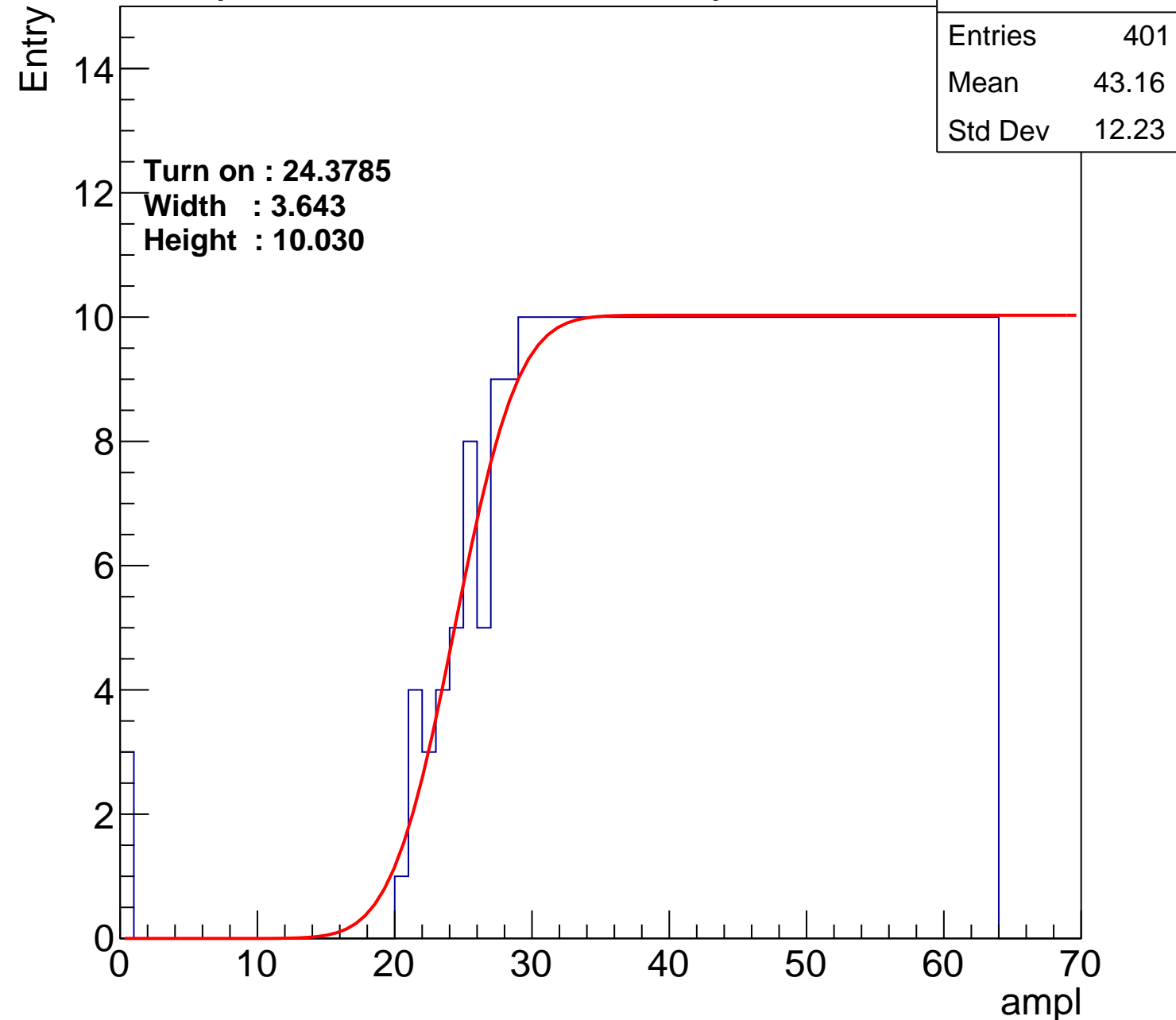
Width : 3.643

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 26.7428

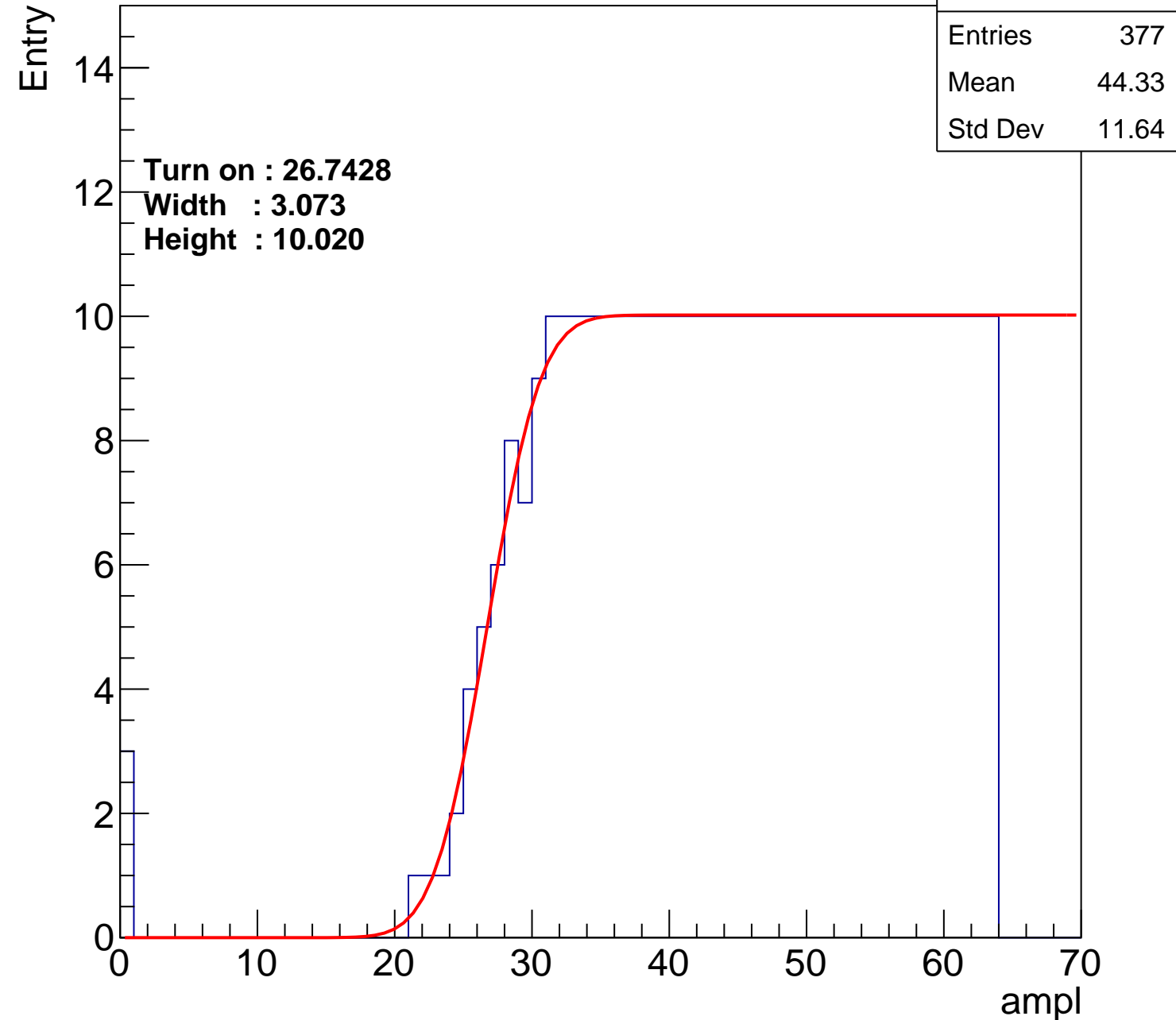
Width : 3.073

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch79

calib_packv5_042523_0143.root, FC#12, port B1

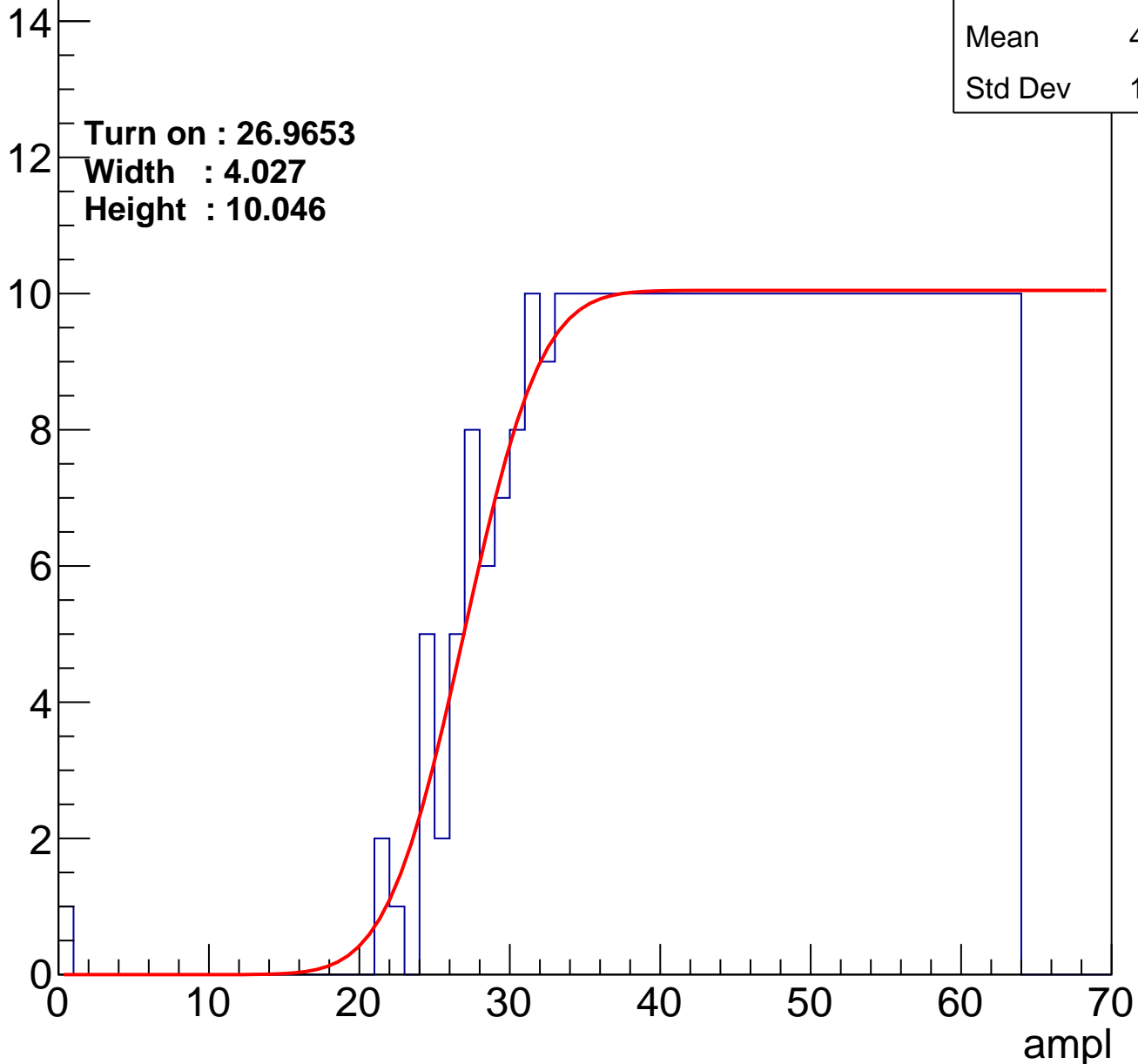
Entries	374
Mean	44.57
Std Dev	11.26

Turn on : 26.9653

Width : 4.027

Height : 10.046

Entry



B0L102S, U10-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.45
Std Dev	11.97

Turn on : 24.8591

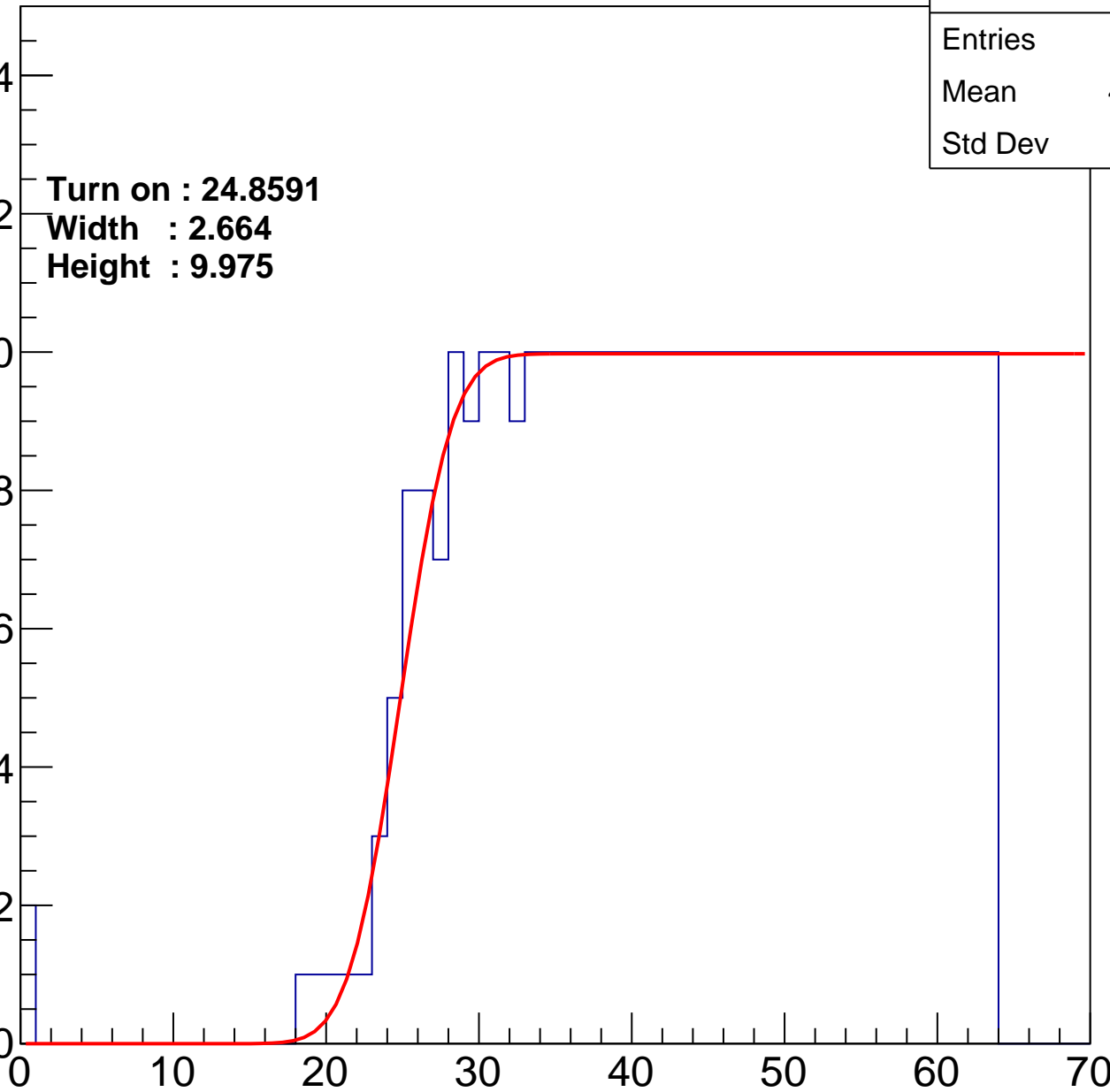
Width : 2.664

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.25
Std Dev	11.45

Turn on : 26.0138

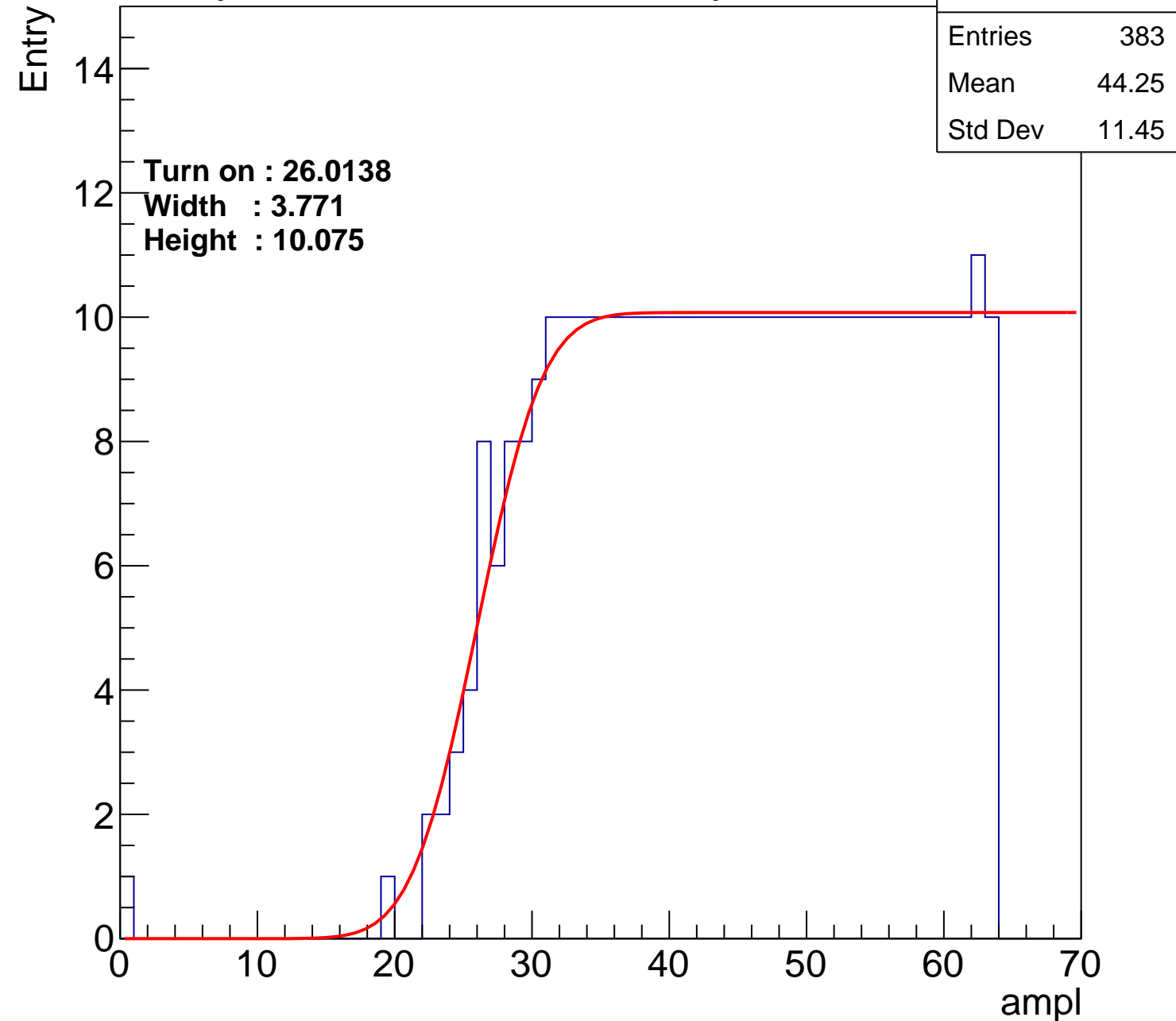
Width : 3.771

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.32
Std Dev	12.17

Turn on : 24.5588

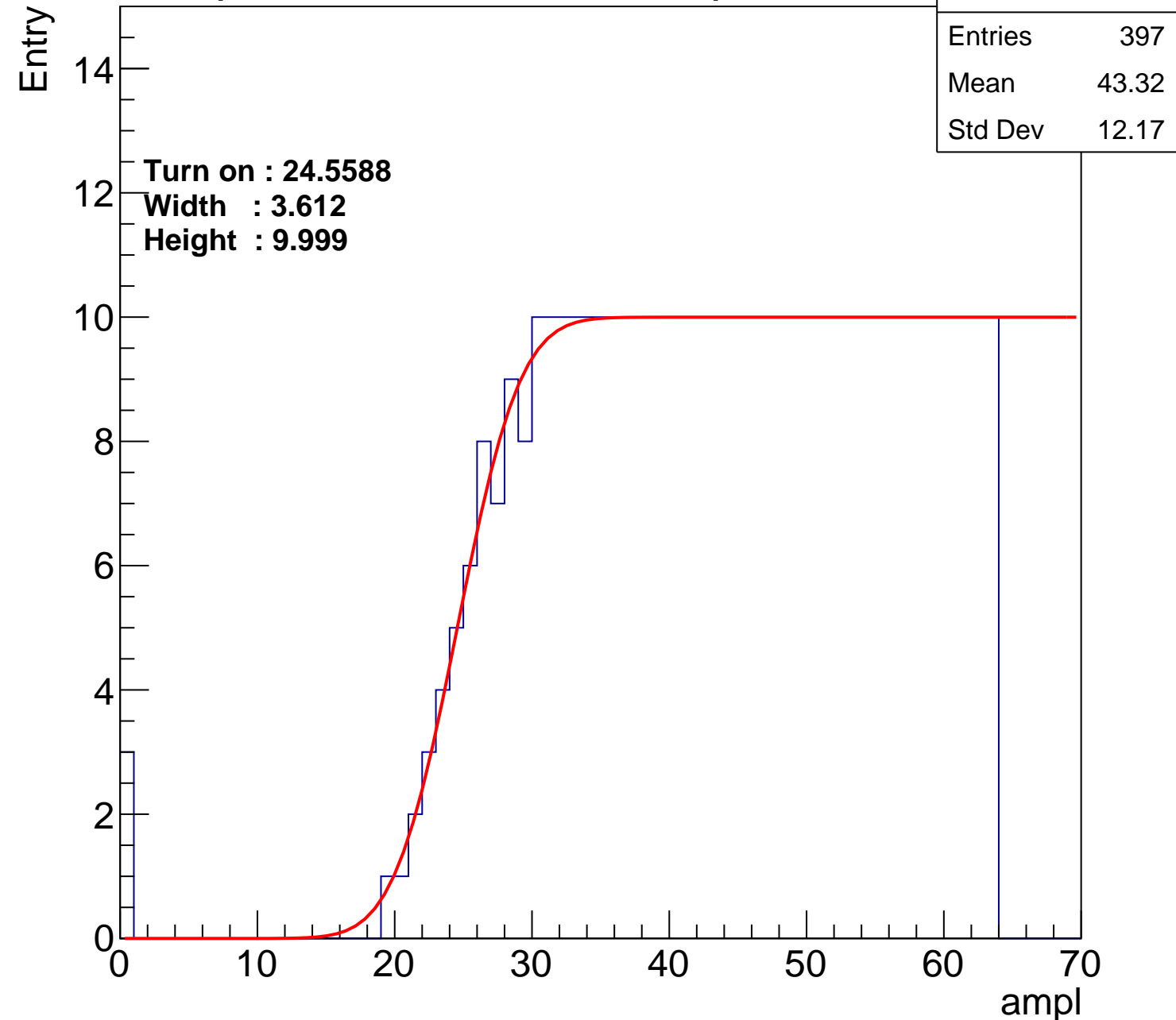
Width : 3.612

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch83

calib_packv5_042523_0143.root, FC#12, port B1

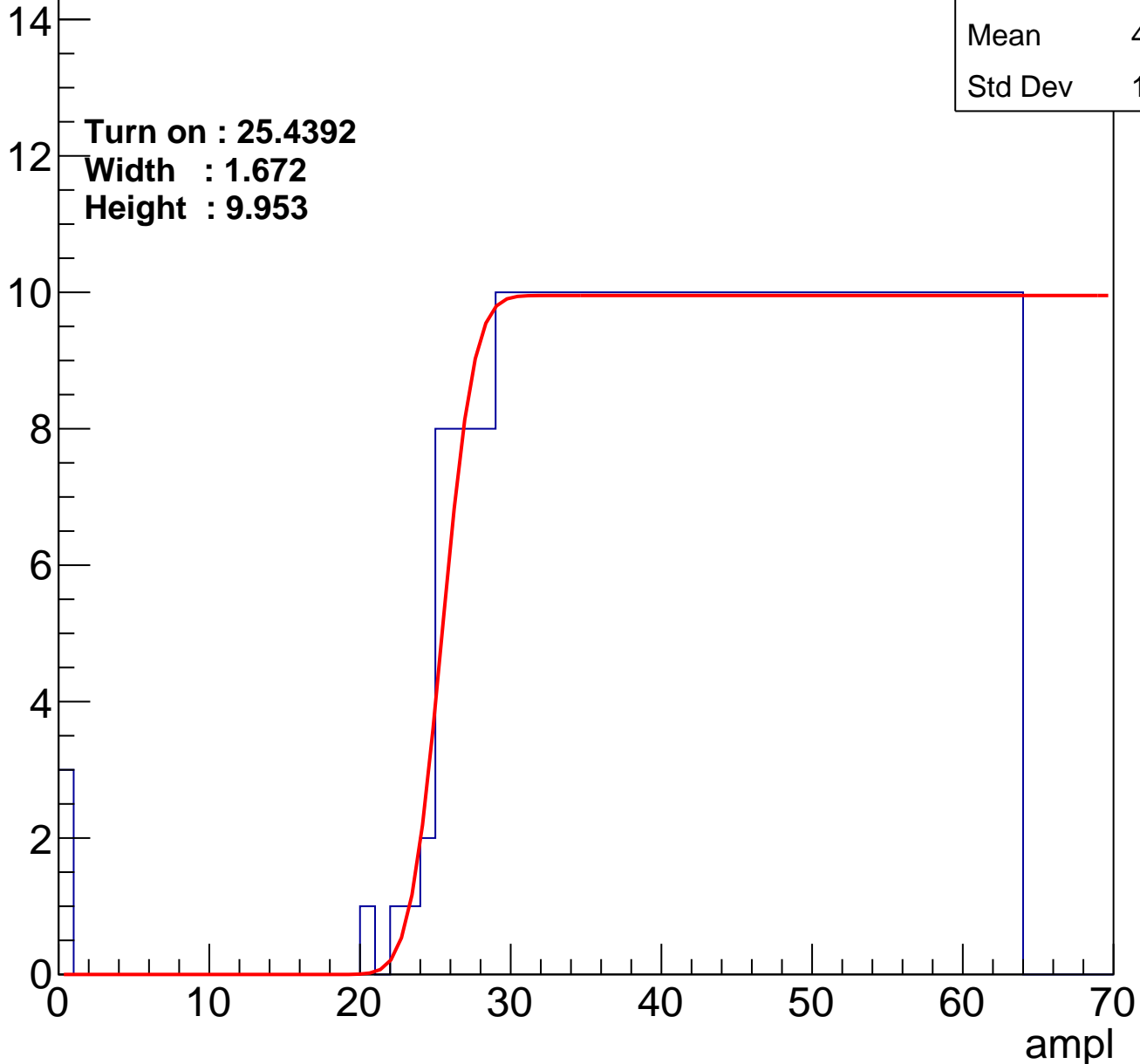
Entries	390
Mean	43.75
Std Dev	11.88

Turn on : 25.4392

Width : 1.672

Height : 9.953

Entry



B0L102S, U10-ch84

calib_packv5_042523_0143.root, FC#12, port B1

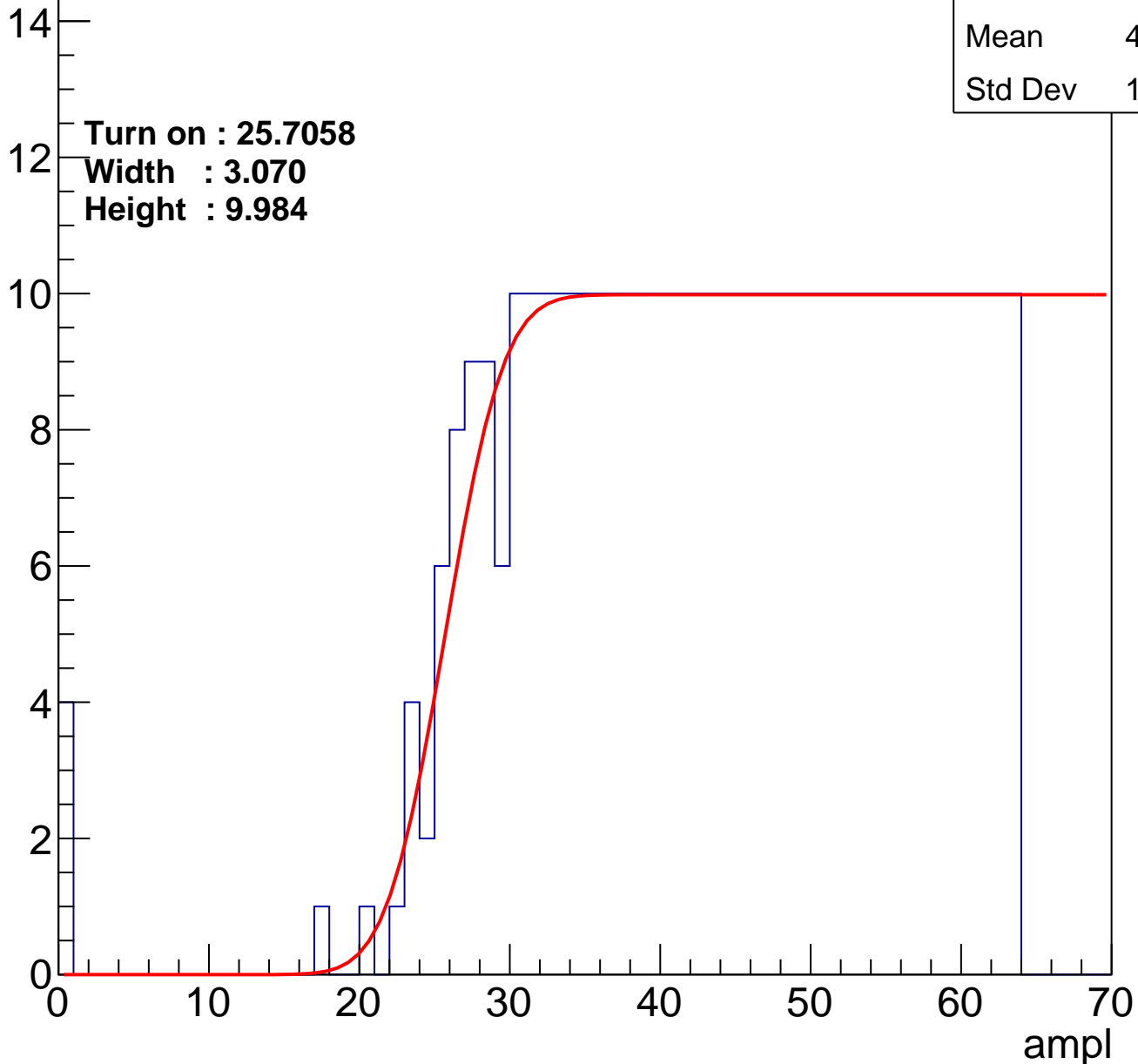
Entries	391
Mean	43.57
Std Dev	12.17

Turn on : 25.7058

Width : 3.070

Height : 9.984

Entry



B0L102S, U10-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.91
Std Dev	11.17

Turn on : 27.7638

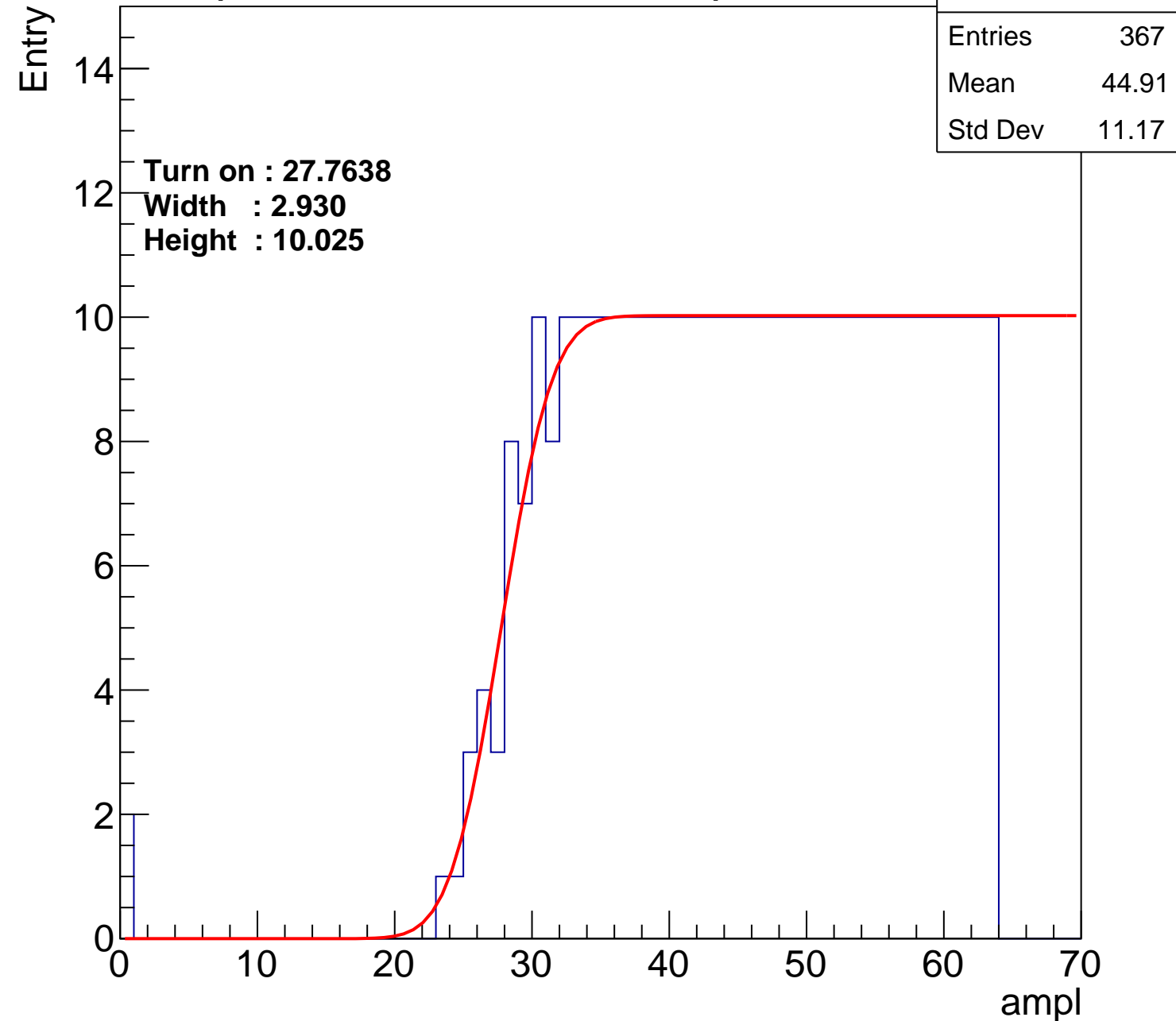
Width : 2.930

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch86

calib_packv5_042523_0143.root, FC#12, port B1

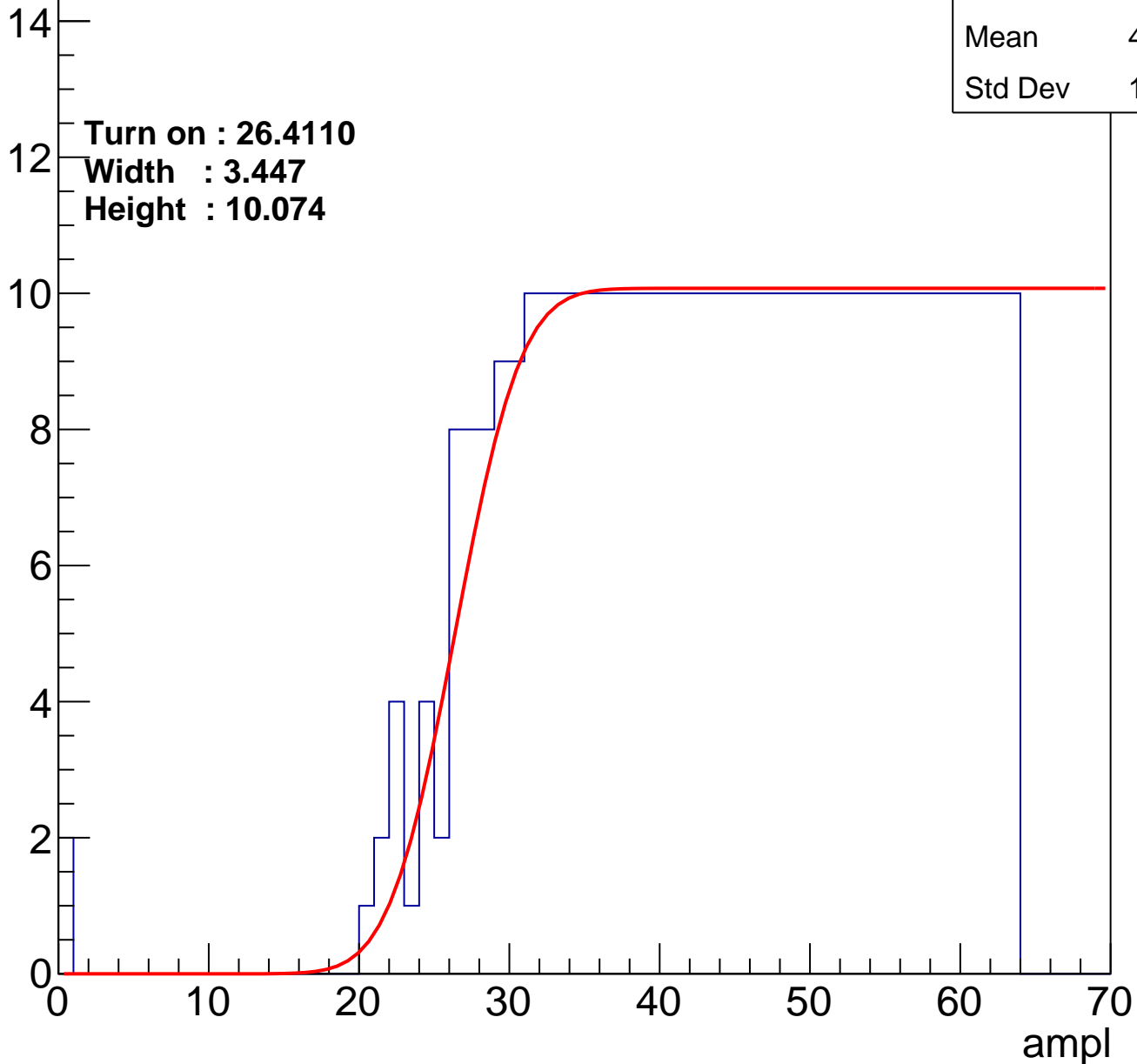
Entries	388
Mean	43.84
Std Dev	11.78

Turn on : 26.4110

Width : 3.447

Height : 10.074

Entry



B0L102S, U10-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.27
Std Dev	11.68

Turn on : 27.1726

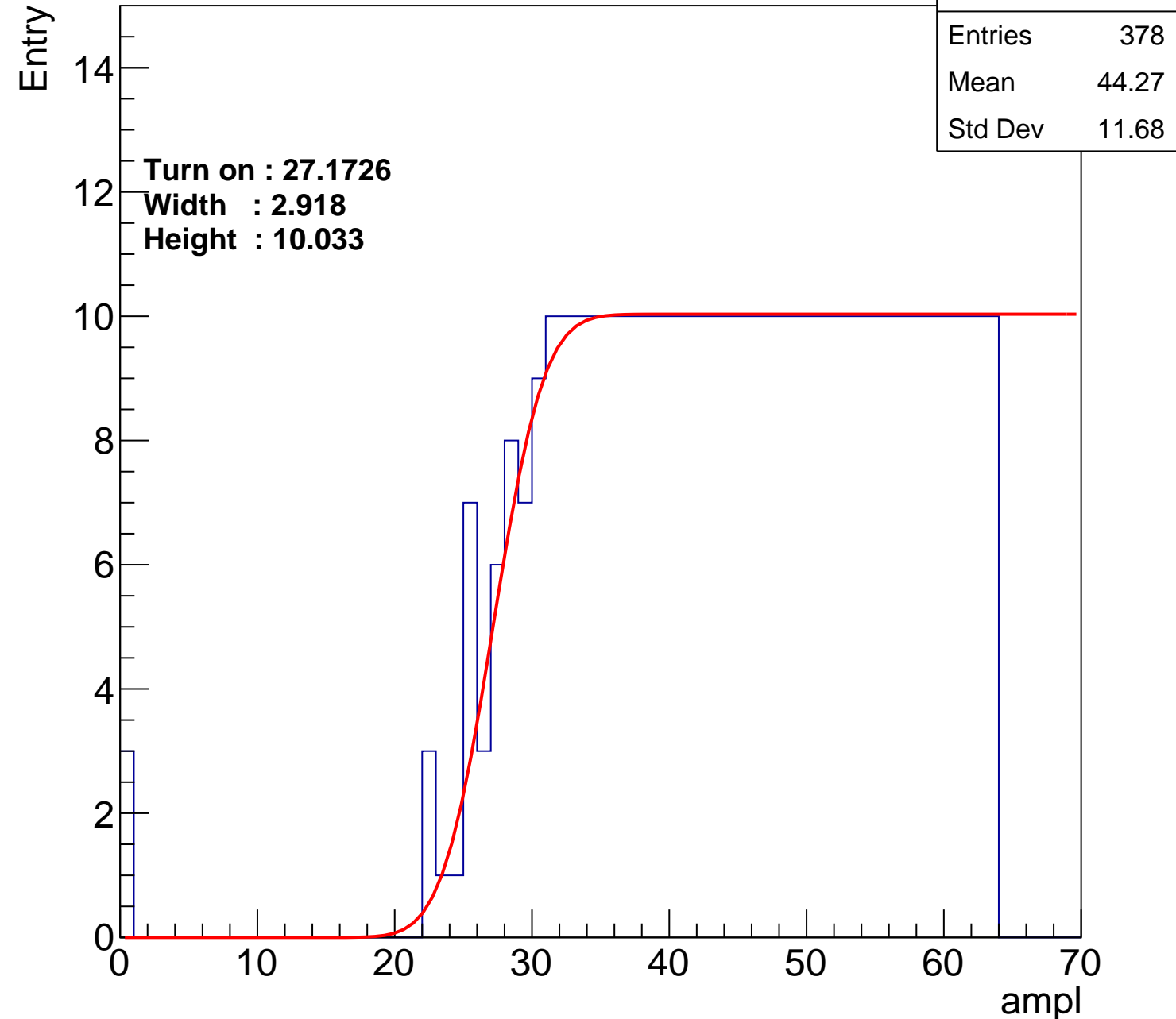
Width : 2.918

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.87
Std Dev	12.17

Turn on : 25.9626

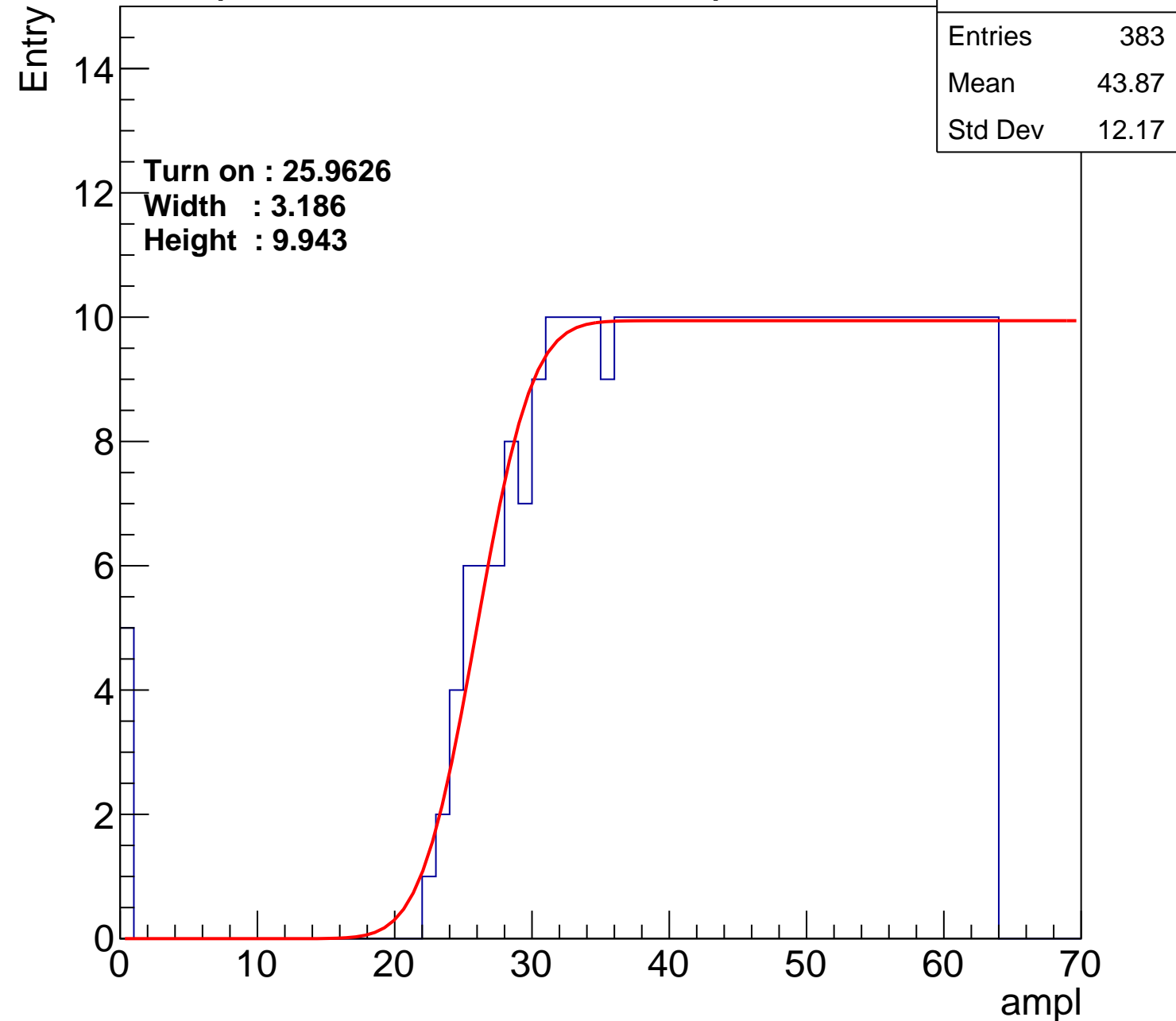
Width : 3.186

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch89

calib_packv5_042523_0143.root, FC#12, port B1

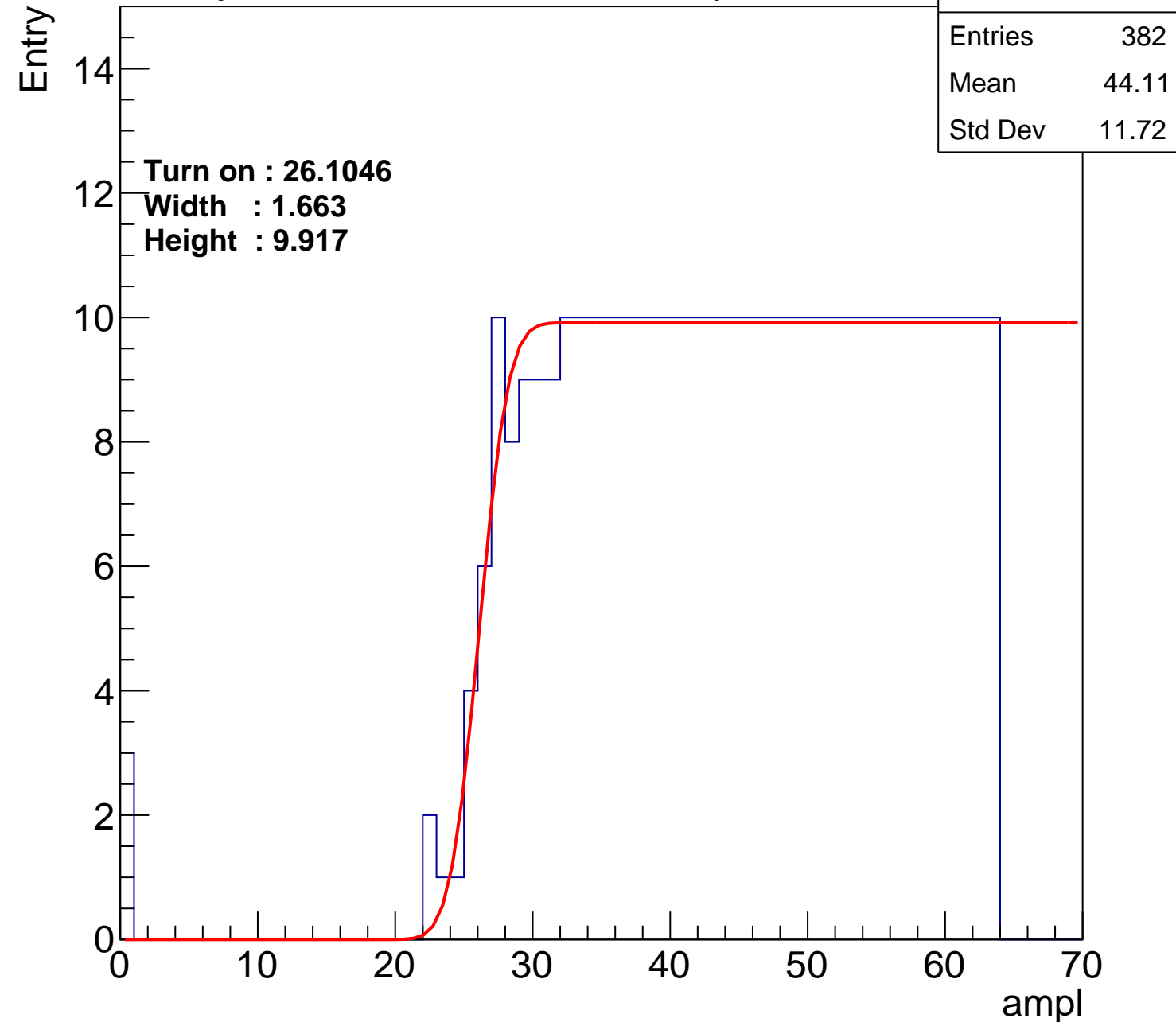
Entry

14
12
10
8
6
4
2
0

Turn on : 26.1046
Width : 1.663
Height : 9.917

Entries	382
Mean	44.11
Std Dev	11.72

ampl



B0L102S, U10-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.76
Std Dev	11.73

Turn on : 25.4483

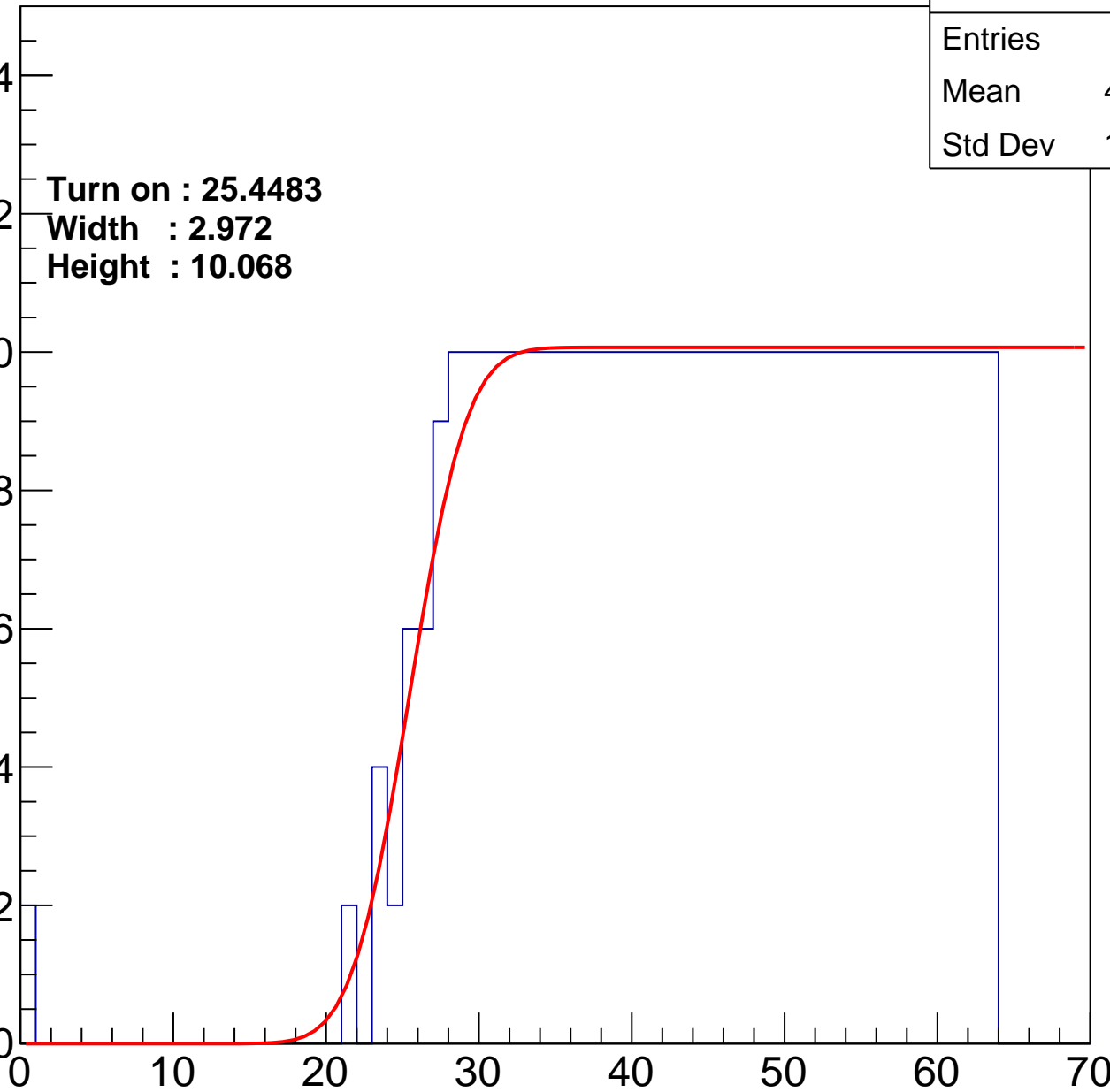
Width : 2.972

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 27.8057

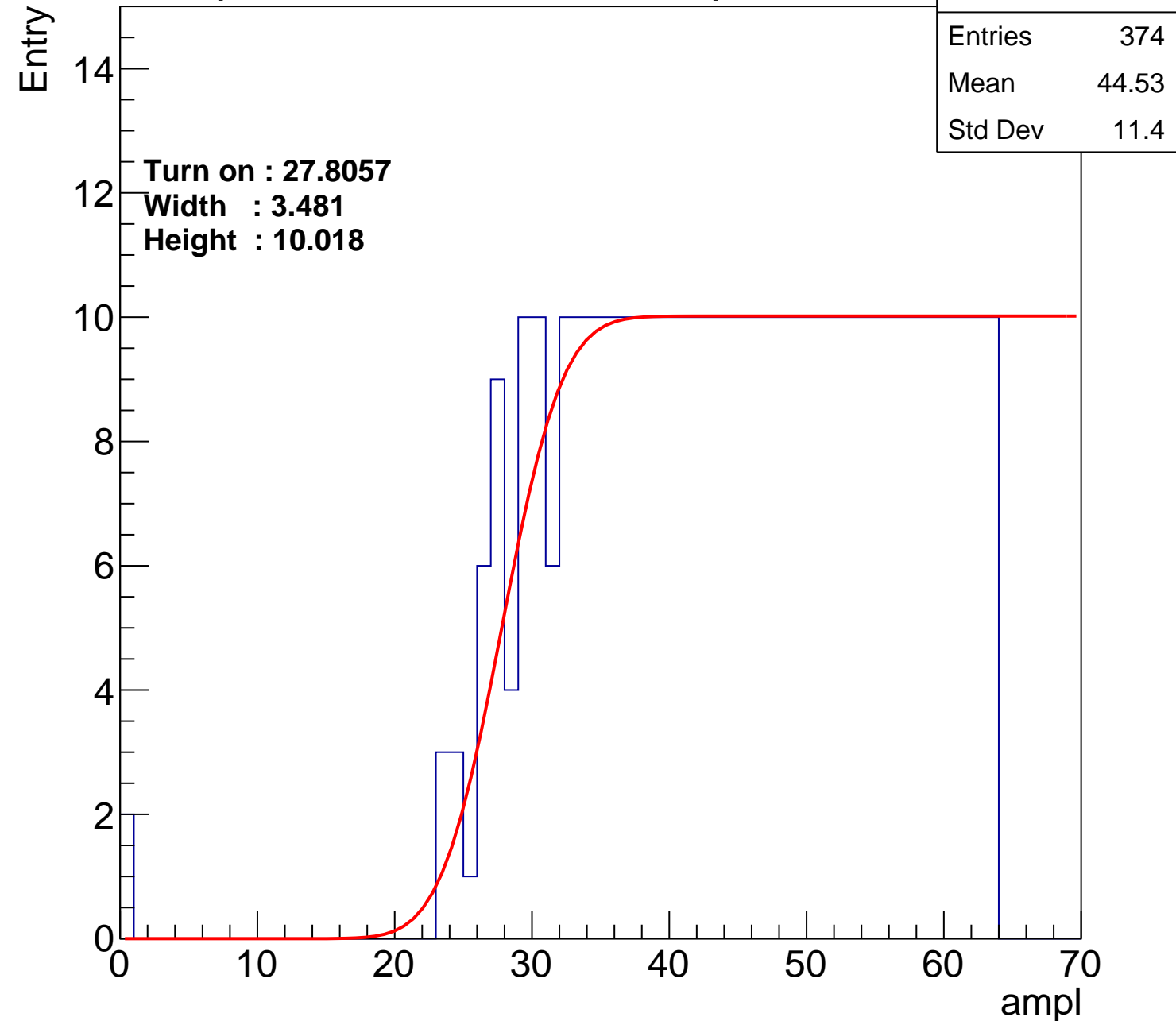
Width : 3.481

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch92

calib_packv5_042523_0143.root, FC#12, port B1

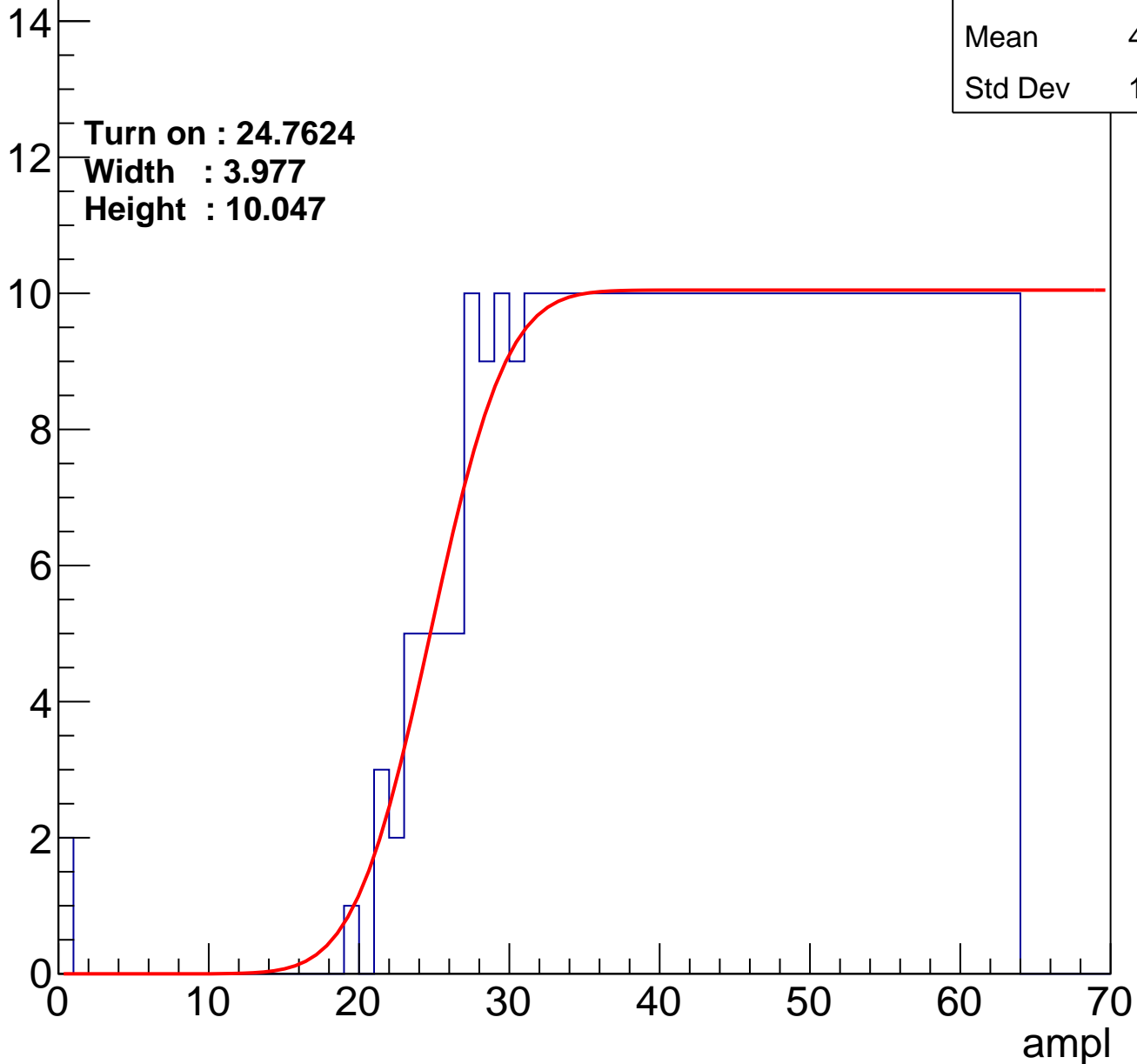
Entries	396
Mean	43.45
Std Dev	11.96

Turn on : 24.7624

Width : 3.977

Height : 10.047

Entry



B0L102S, U10-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.13
Std Dev	11.58

Turn on : 25.9849

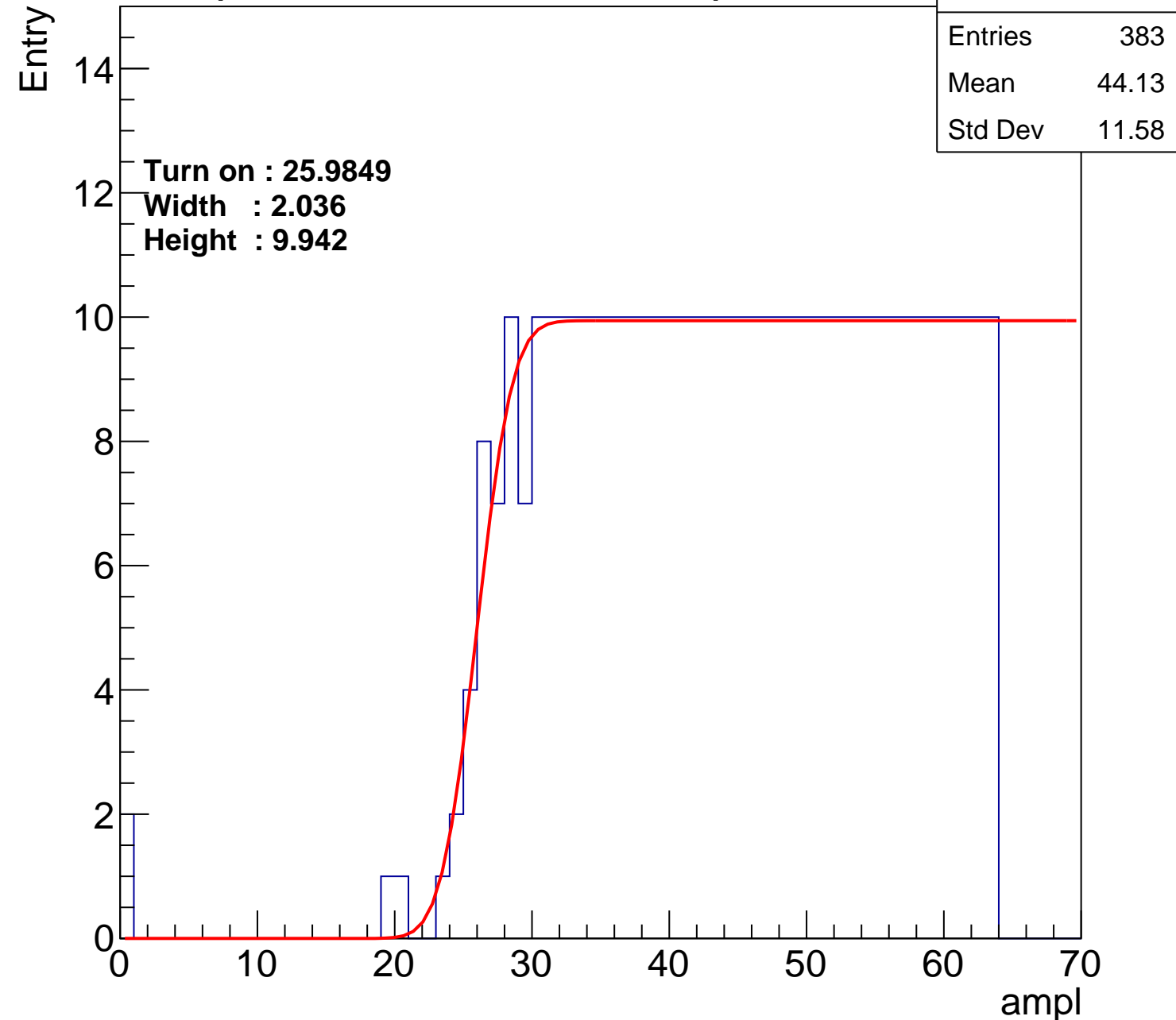
Width : 2.036

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	416
Mean	42.45
Std Dev	12.57

Turn on : 22.5972

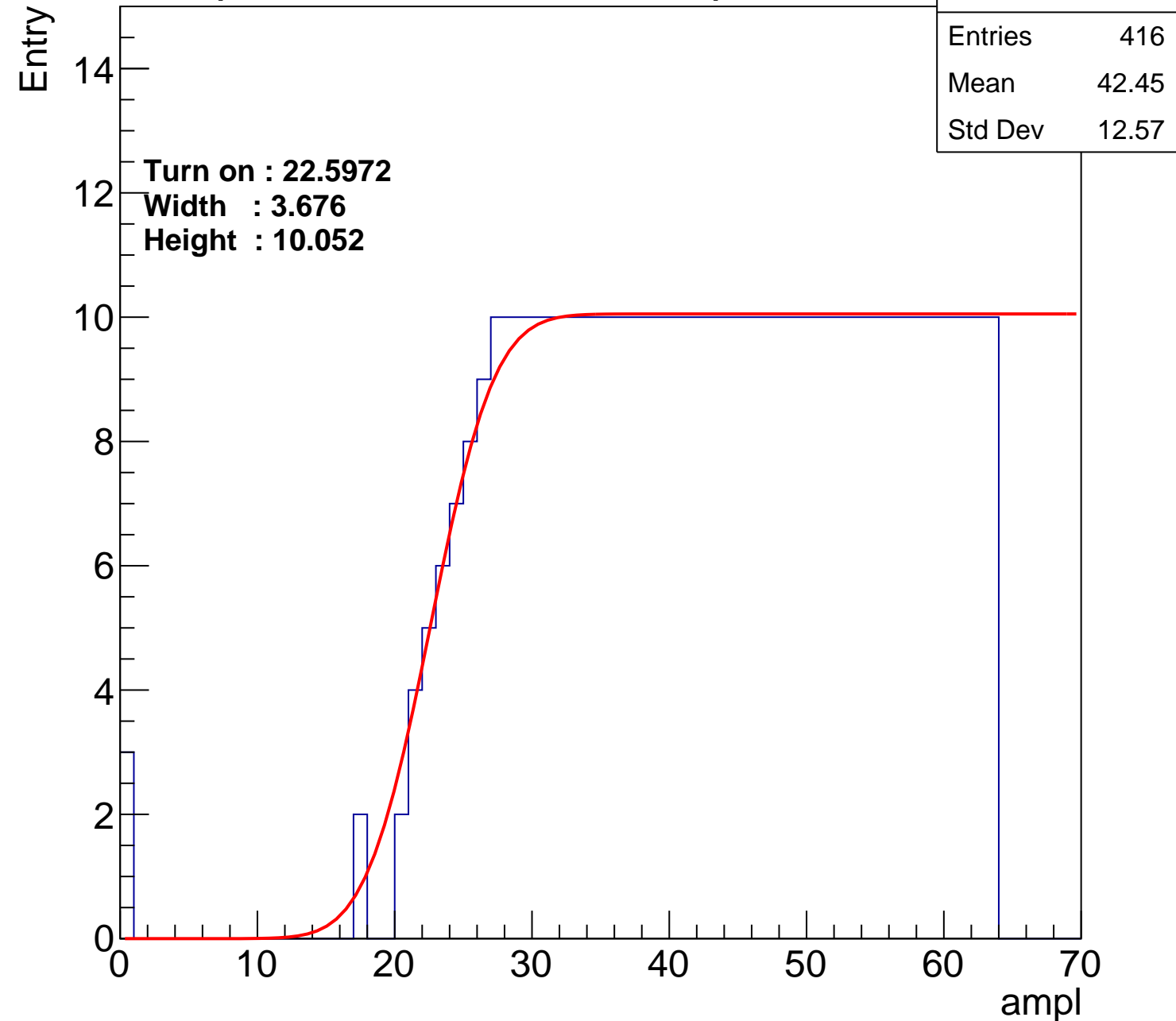
Width : 3.676

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch95

calib_packv5_042523_0143.root, FC#12, port B1

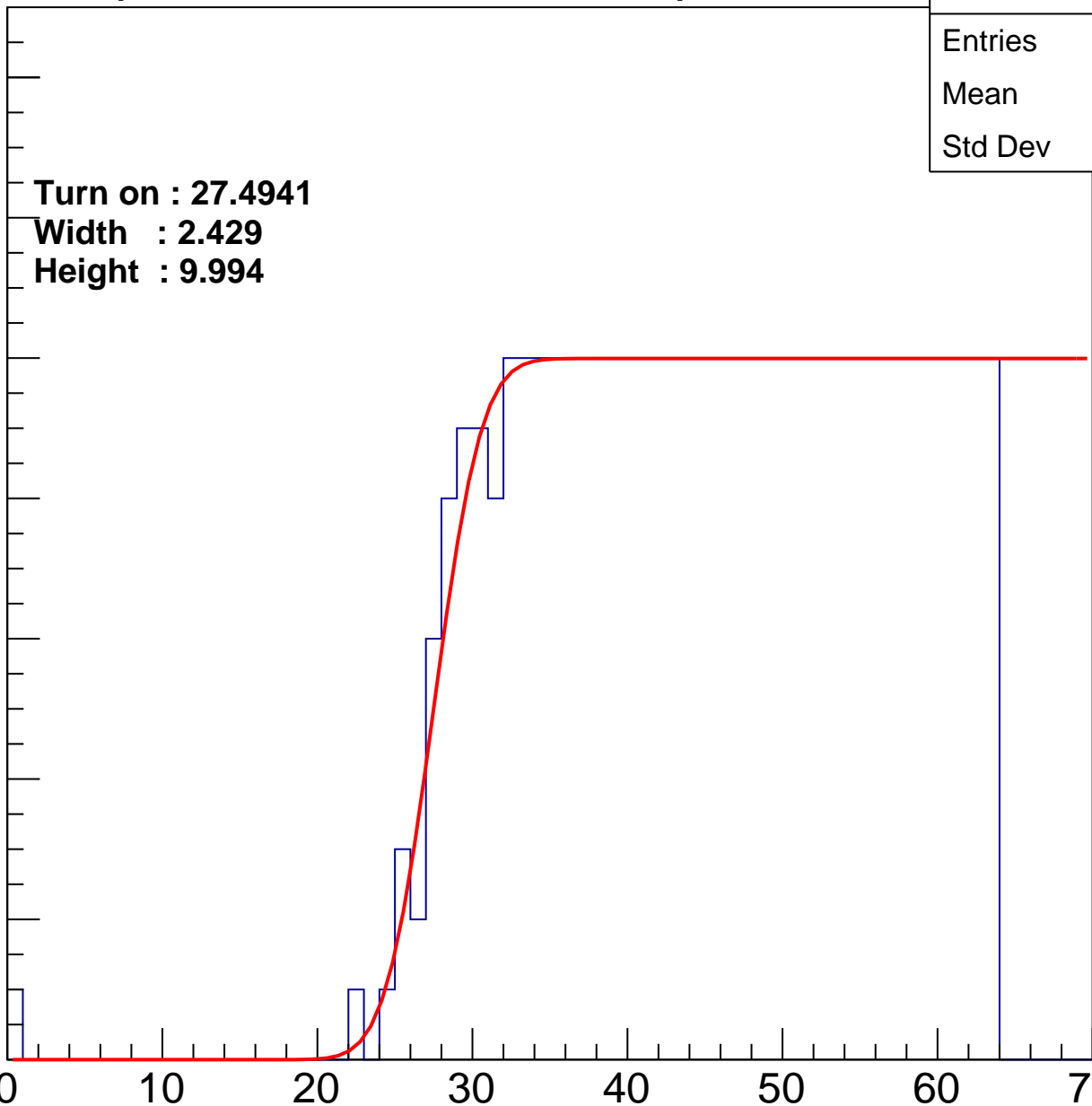
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4941
Width : 2.429
Height : 9.994

Entries	368
Mean	44.94
Std Dev	10.98

ampl



B0L102S, U10-ch96

calib_packv5_042523_0143.root, FC#12, port B1

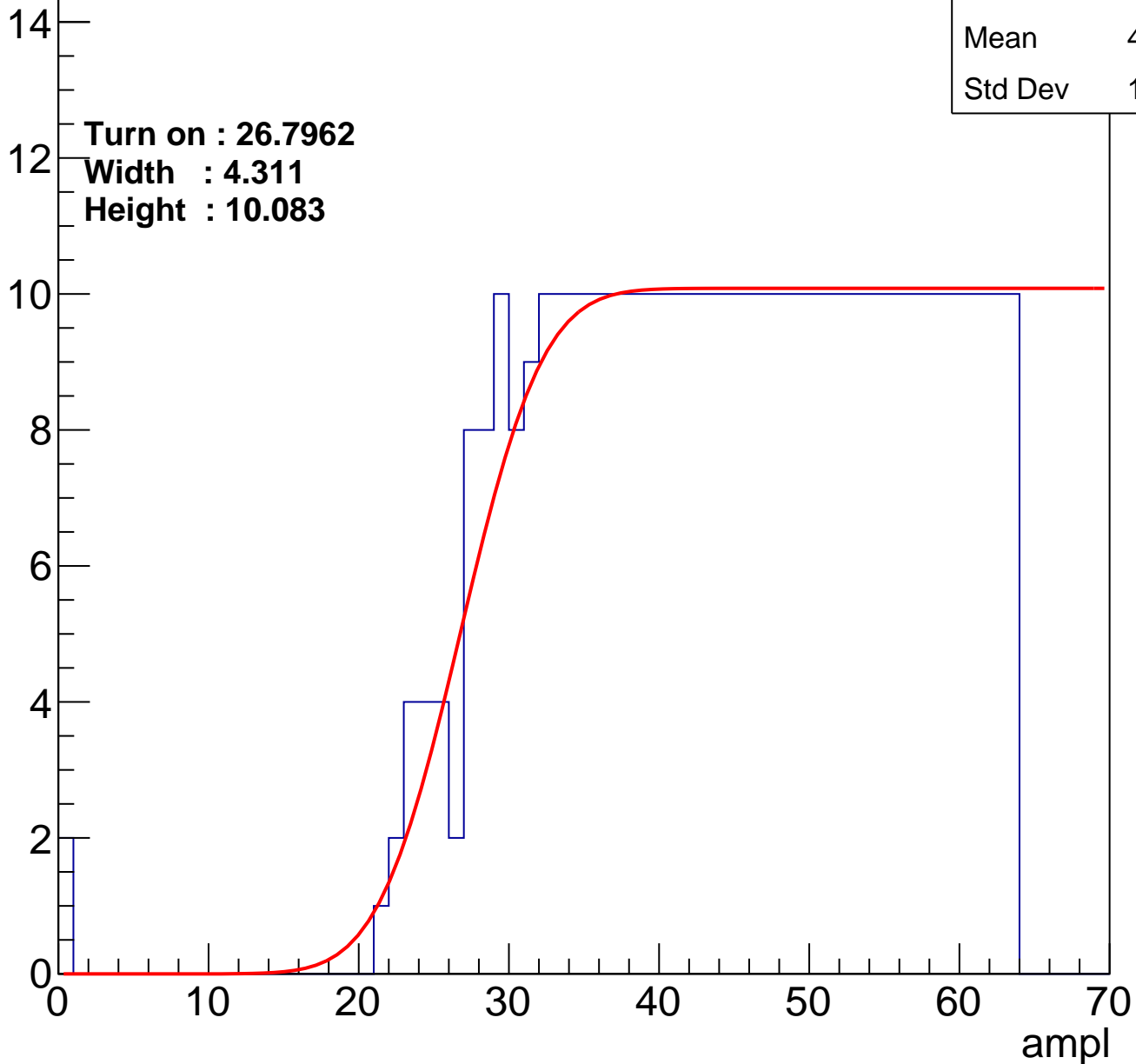
Entries	382
Mean	44.12
Std Dev	11.64

Turn on : 26.7962

Width : 4.311

Height : 10.083

Entry



B0L102S, U10-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.19
Std Dev	11.05

Turn on : 28.8023

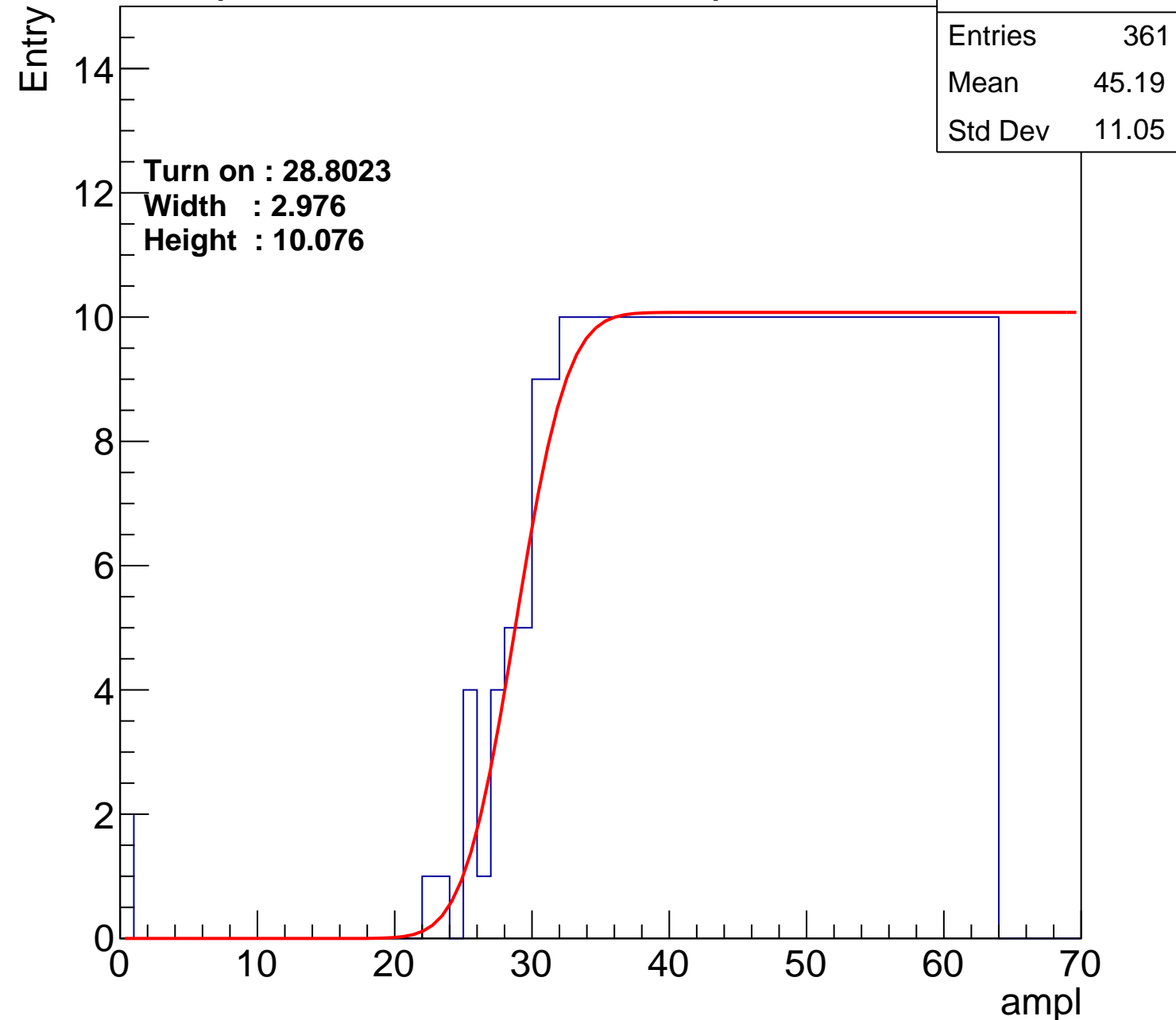
Width : 2.976

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.97
Std Dev	11.83

Turn on : 26.7719

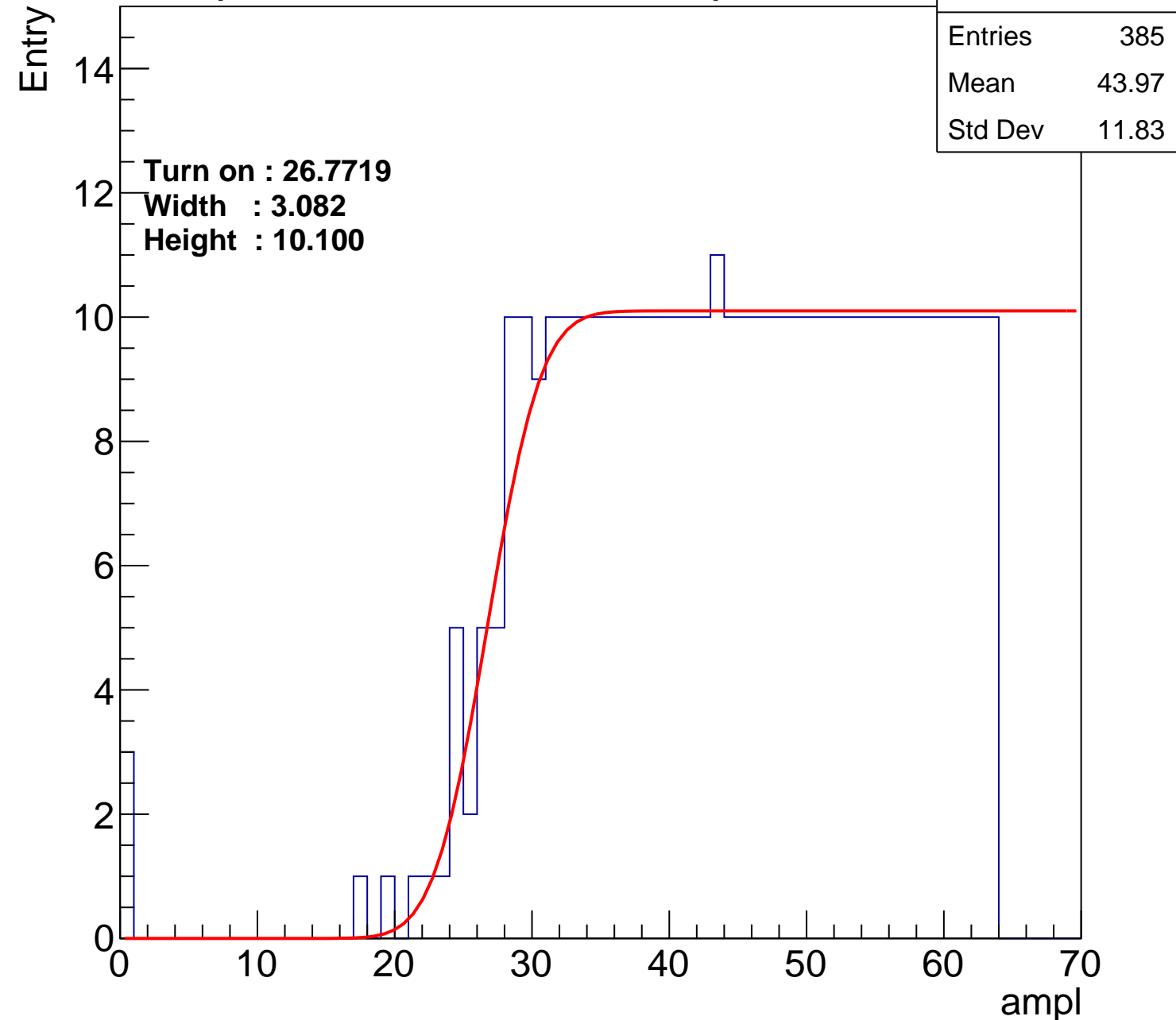
Width : 3.082

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.6569

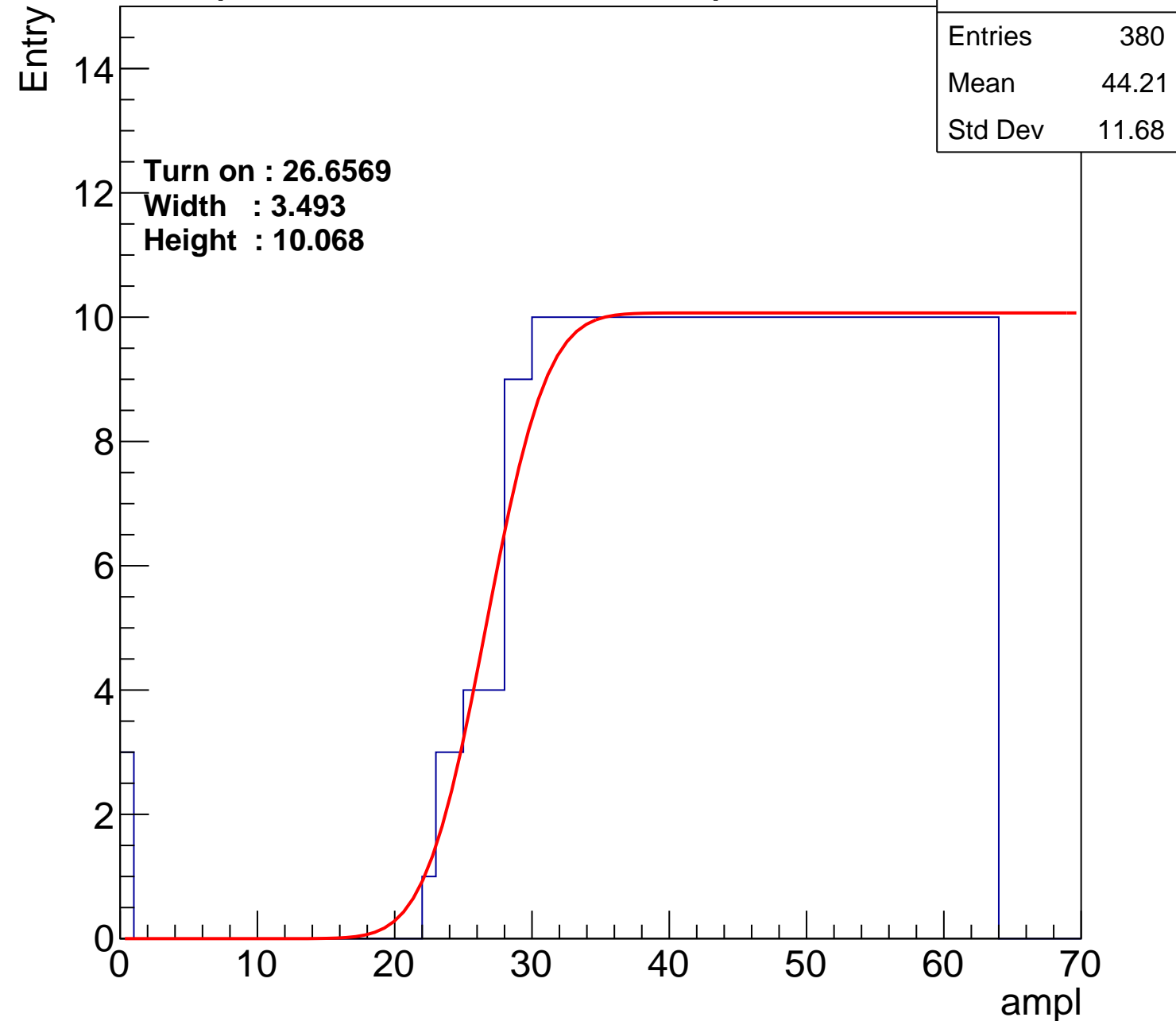
Width : 3.493

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.29
Std Dev	12.23

Turn on : 24.4163

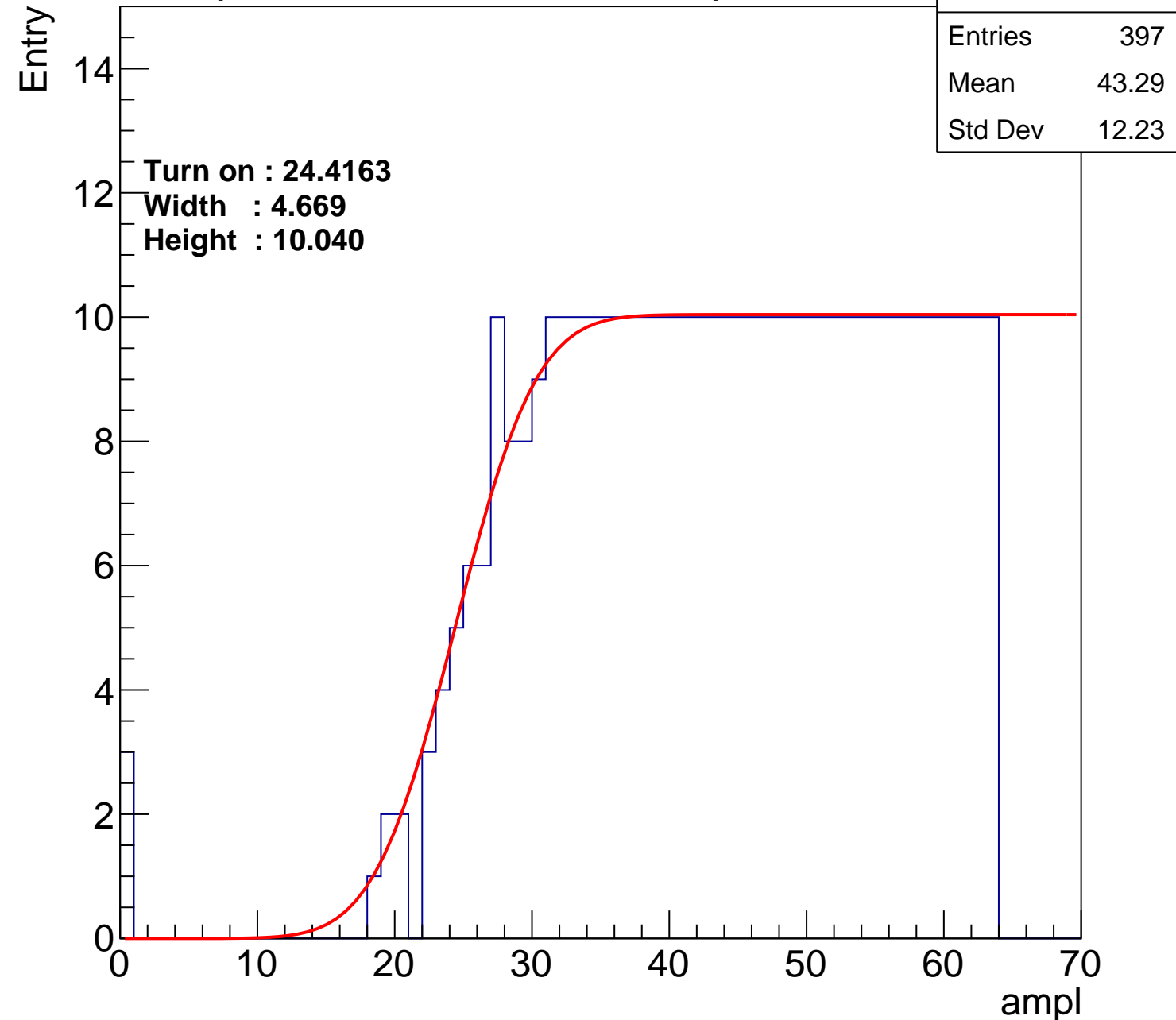
Width : 4.669

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.57
Std Dev	12.06

Turn on : 25.2242

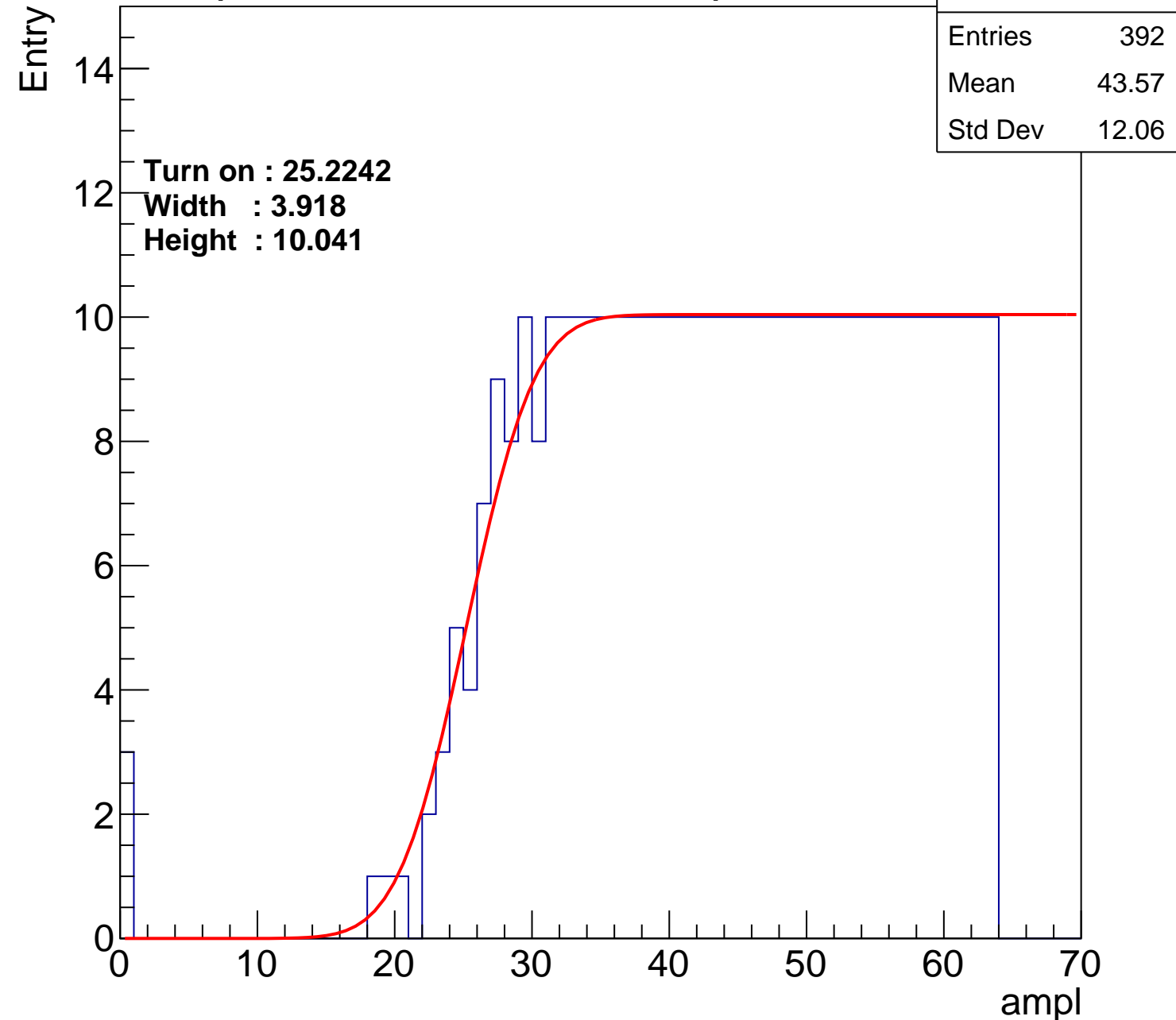
Width : 3.918

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.52
Std Dev	11.34

Turn on : 27.5699

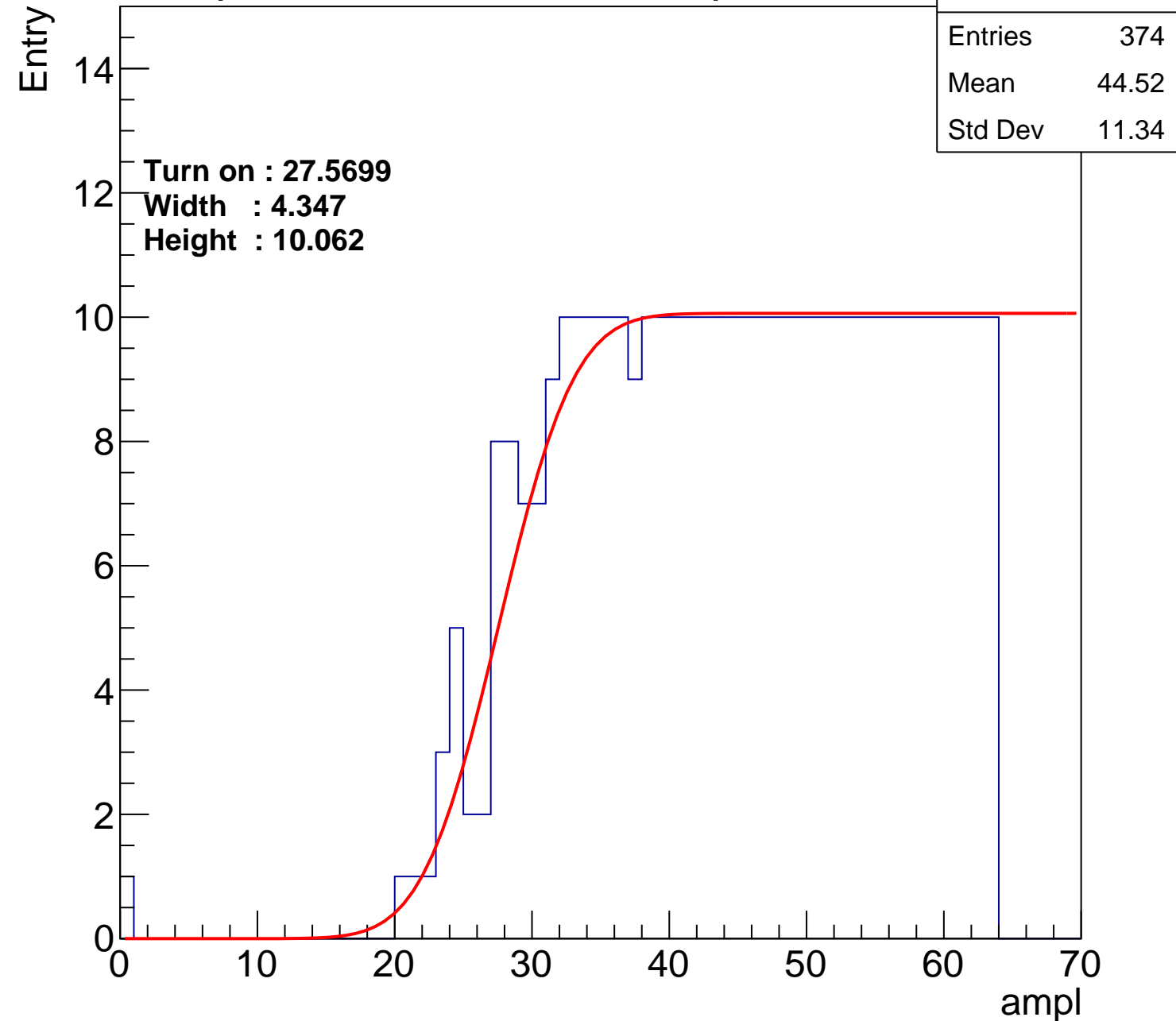
Width : 4.347

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.57
Std Dev	11.4

Turn on : 28.1350

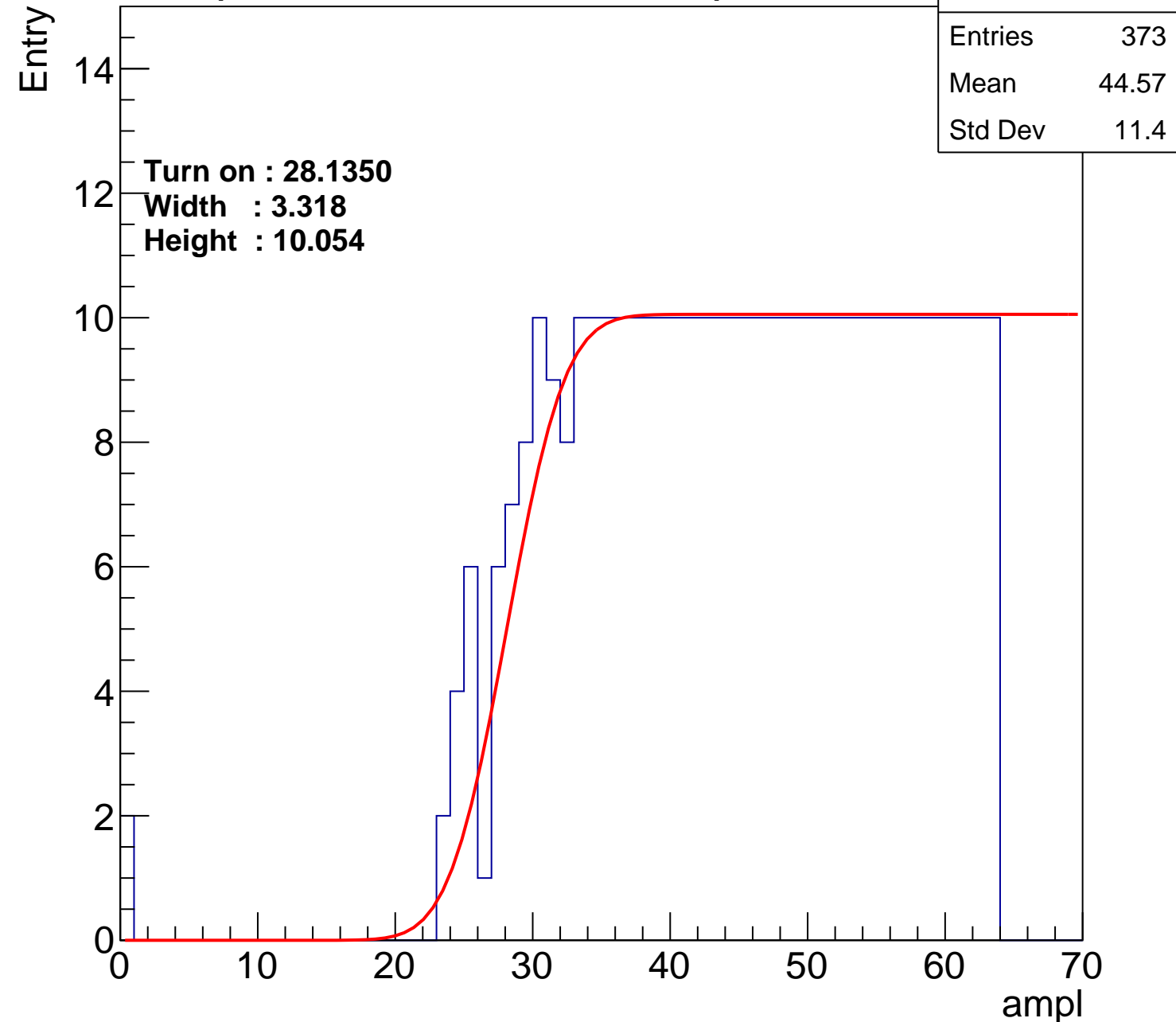
Width : 3.318

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.68
Std Dev	12.36

Turn on : 23.0951

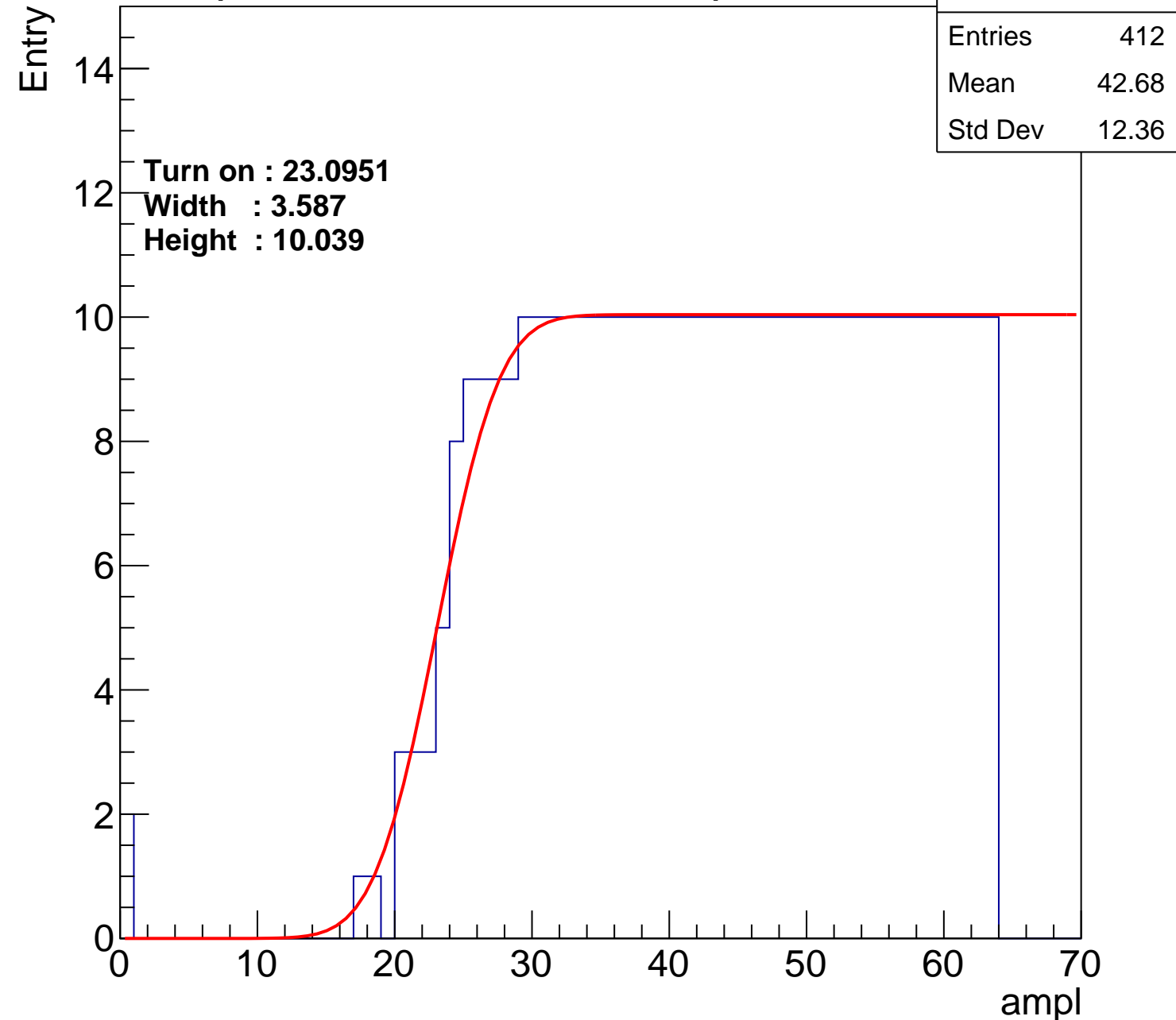
Width : 3.587

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.78
Std Dev	11.69

Turn on : 24.5827

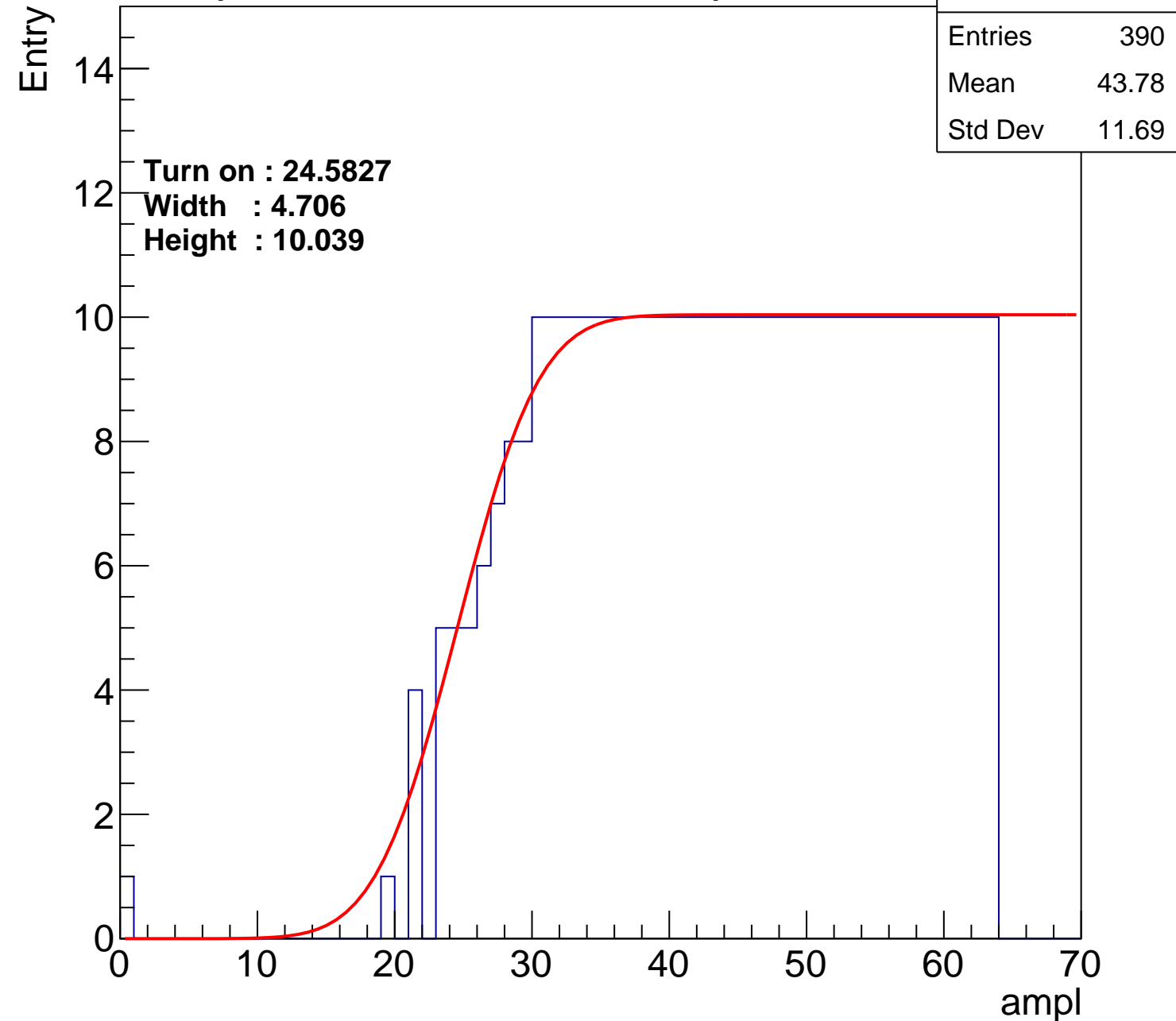
Width : 4.706

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.33
Std Dev	11.88

Turn on : 23.9615

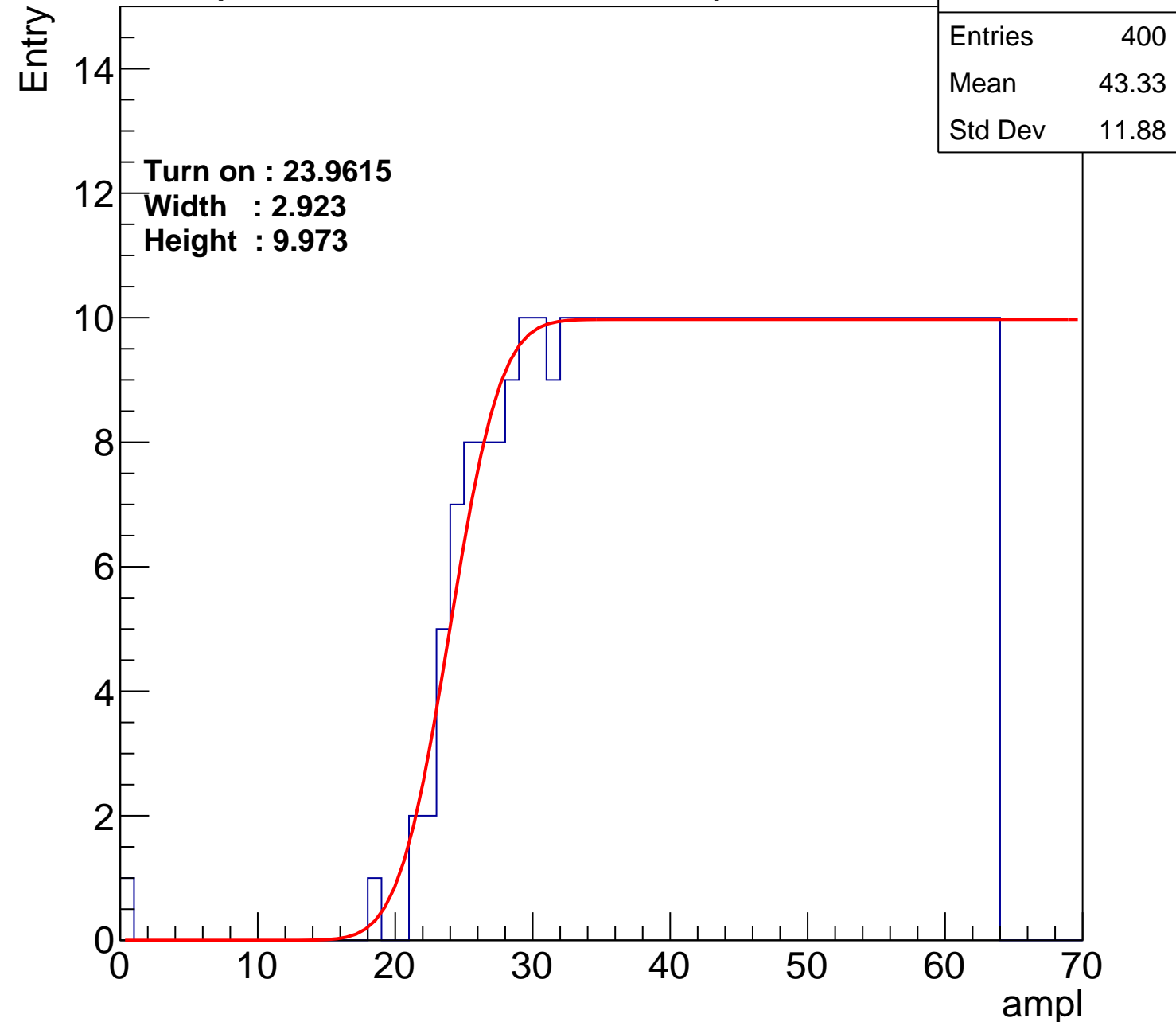
Width : 2.923

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.41
Std Dev	12.21

Turn on : 25.1267

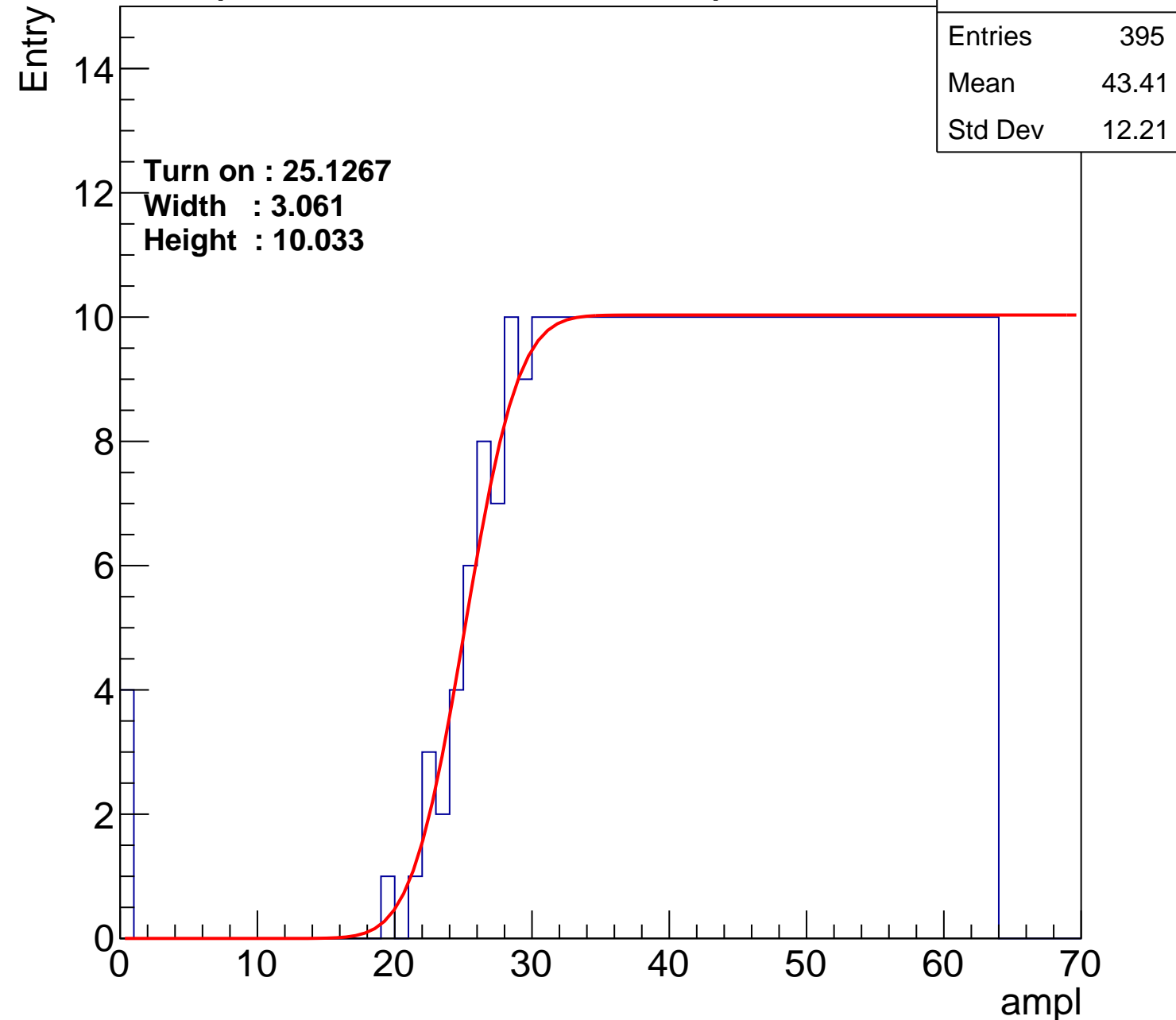
Width : 3.061

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch108

calib_packv5_042523_0143.root, FC#12, port B1

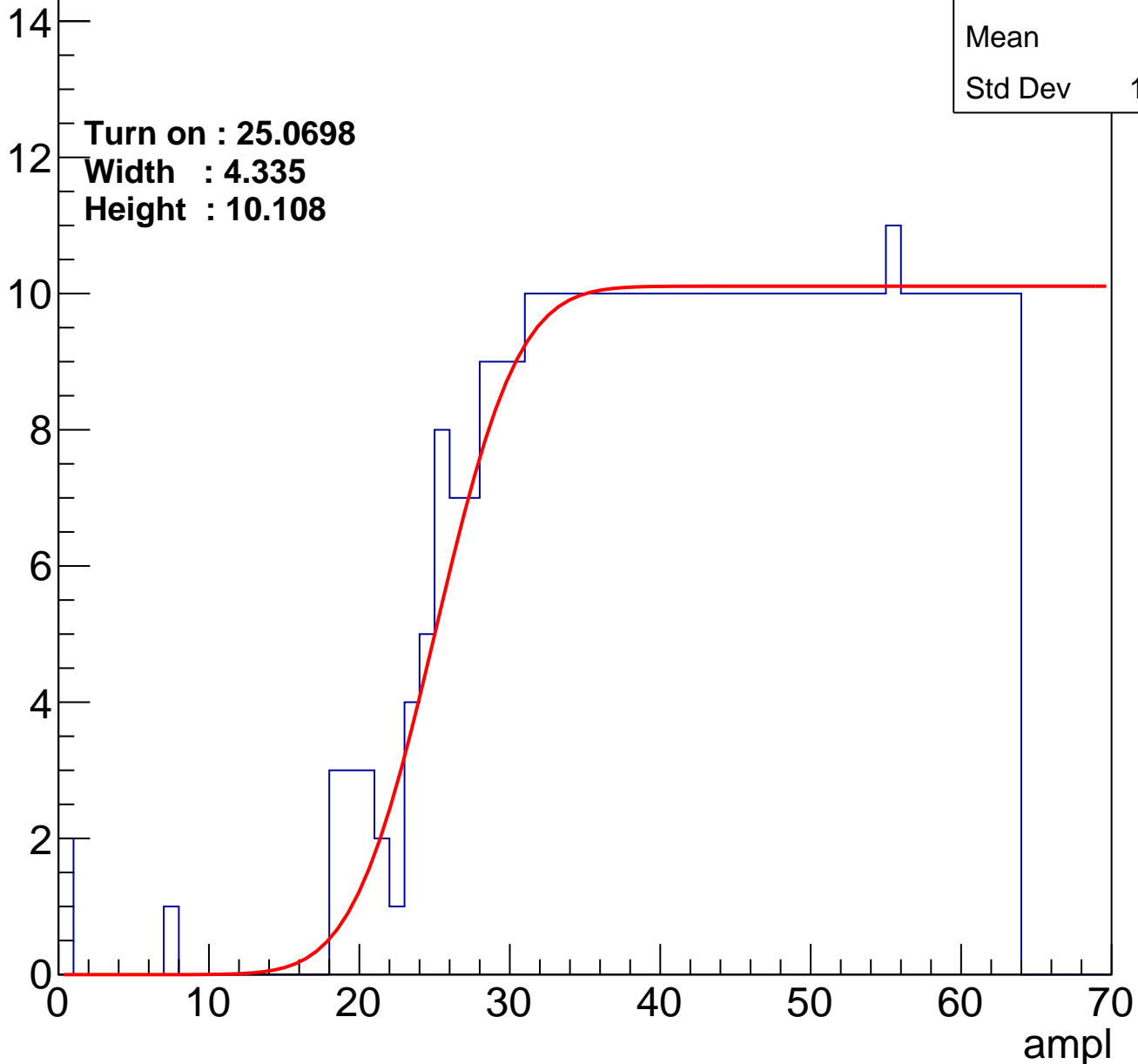
Entries	404
Mean	43
Std Dev	12.39

Turn on : 25.0698

Width : 4.335

Height : 10.108

Entry



B0L102S, U10-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.68
Std Dev	11.87

Turn on : 25.8556

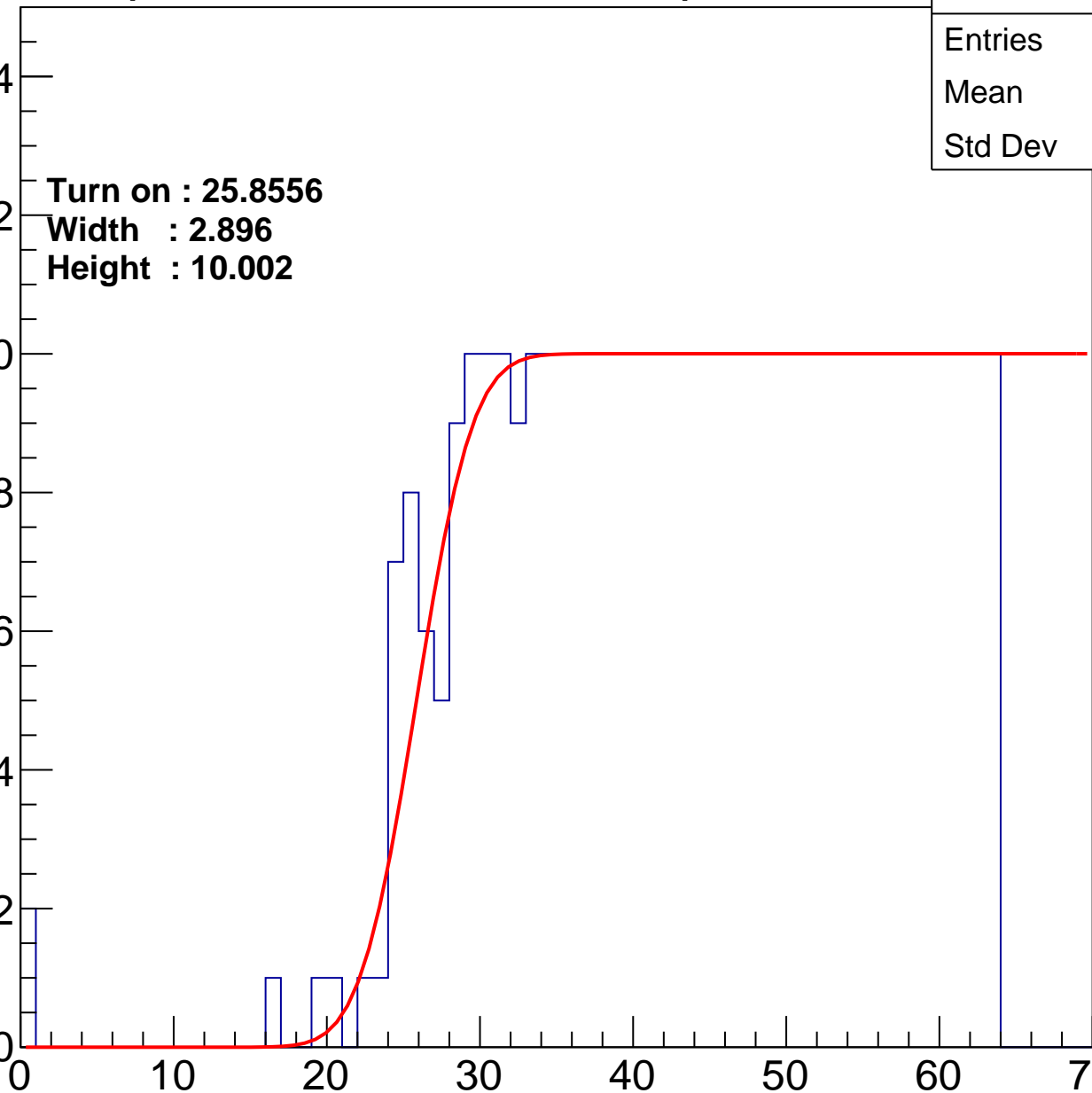
Width : 2.896

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.4
Std Dev	11.52

Turn on : 27.1396

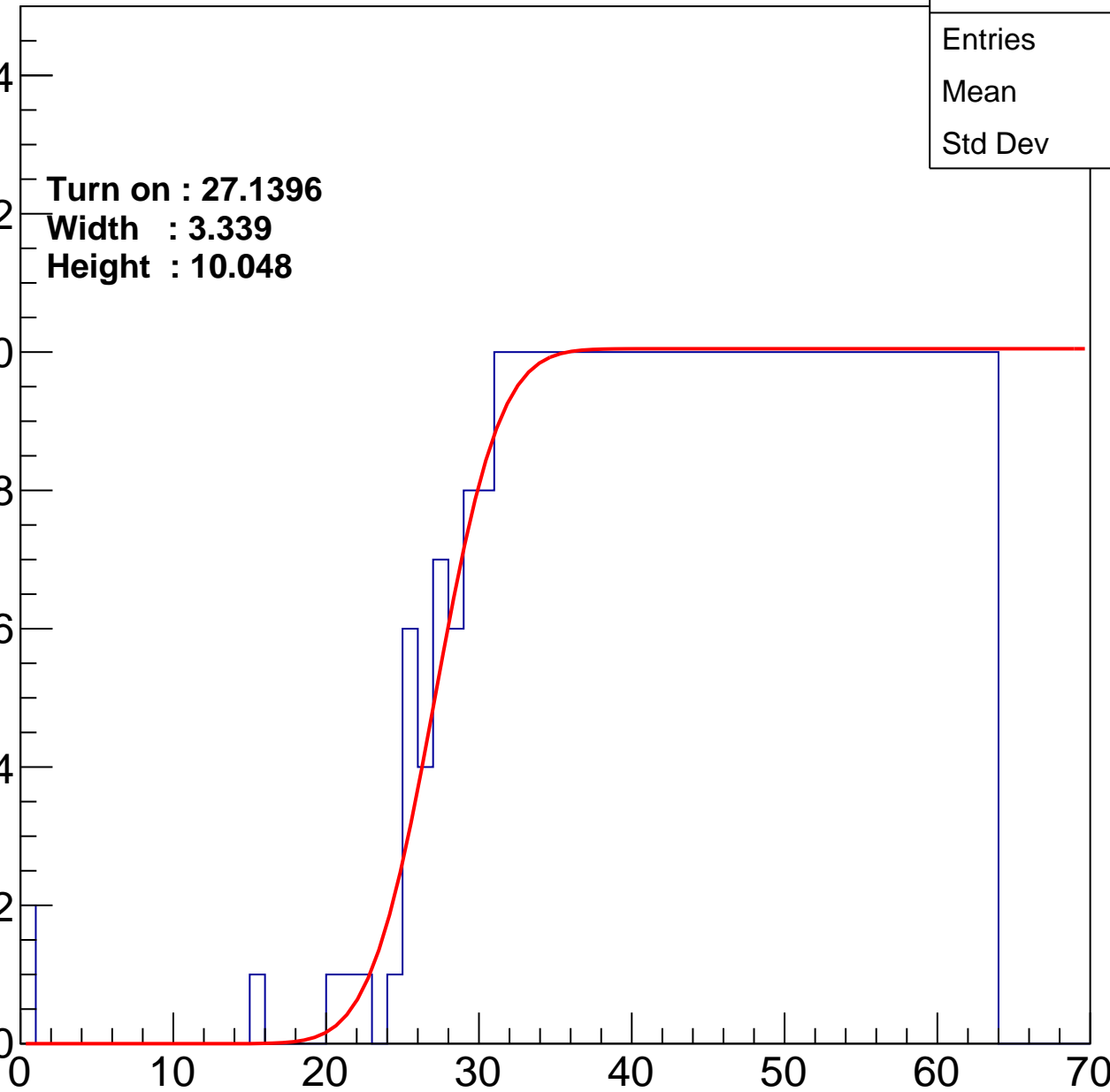
Width : 3.339

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.54
Std Dev	12.03

Turn on : 25.0631

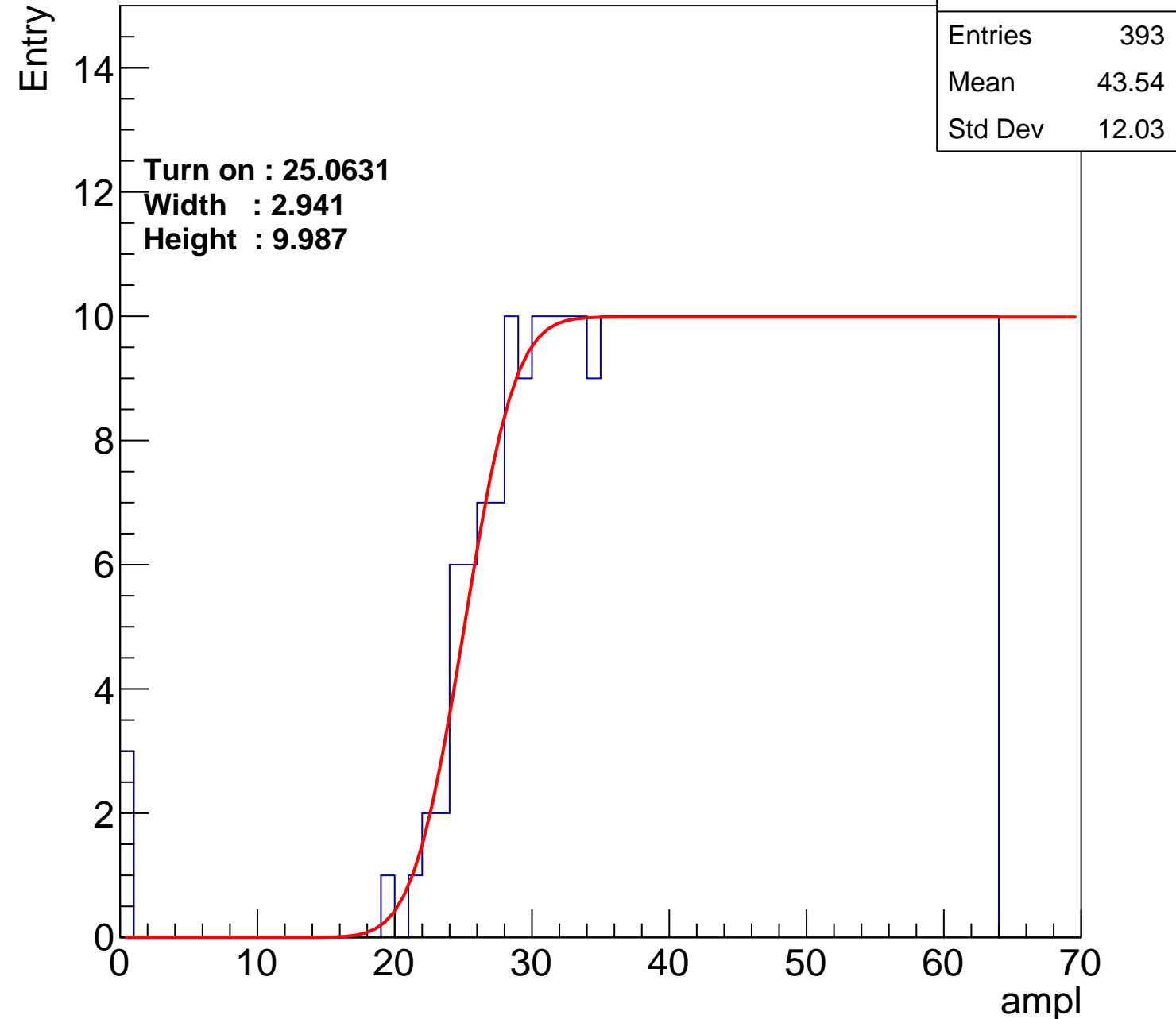
Width : 2.941

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.14
Std Dev	11.68

Turn on : 25.7523

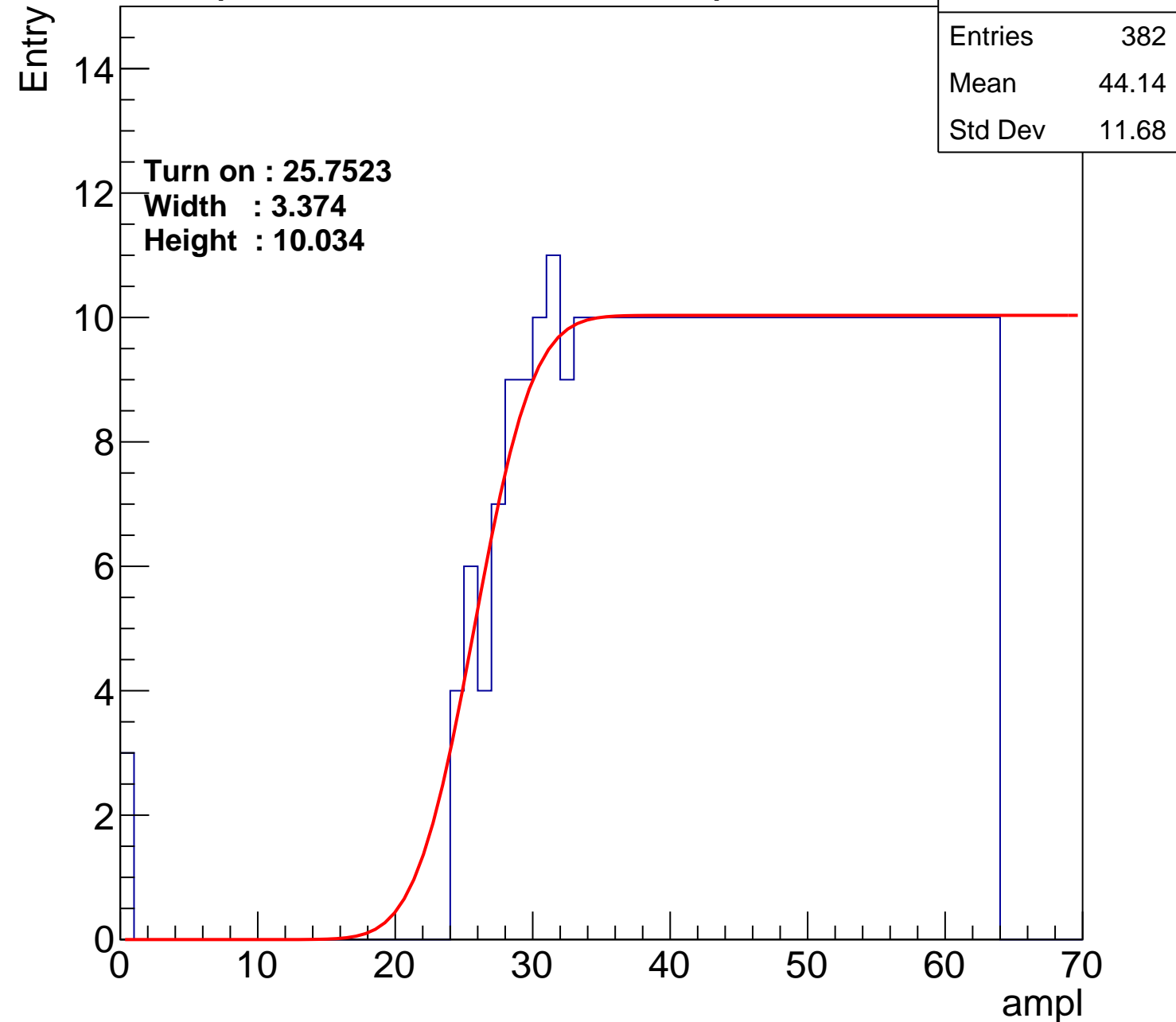
Width : 3.374

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.8
Std Dev	11.77

Turn on : 25.5674

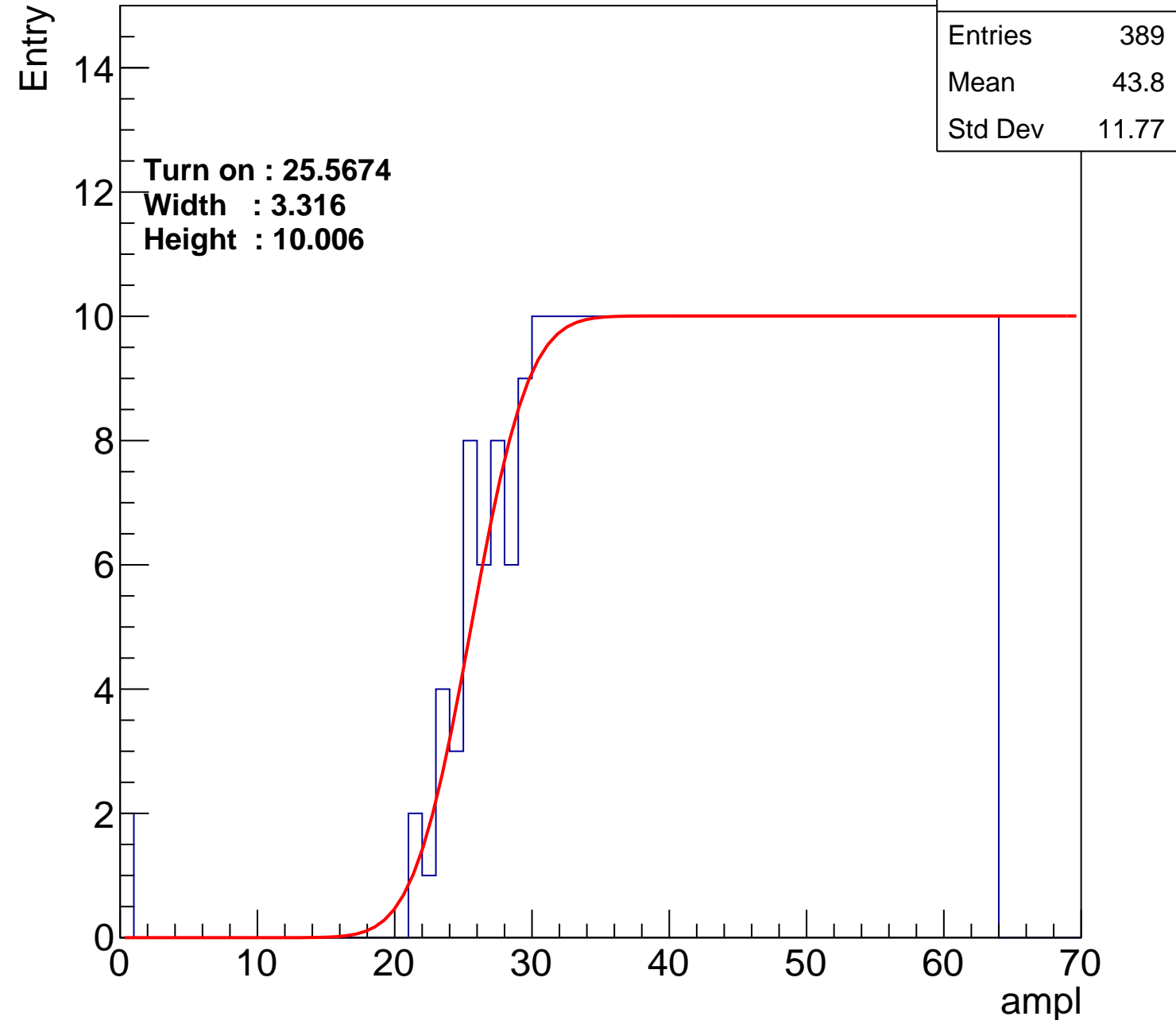
Width : 3.316

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.03
Std Dev	11.55

Turn on : 26.0081

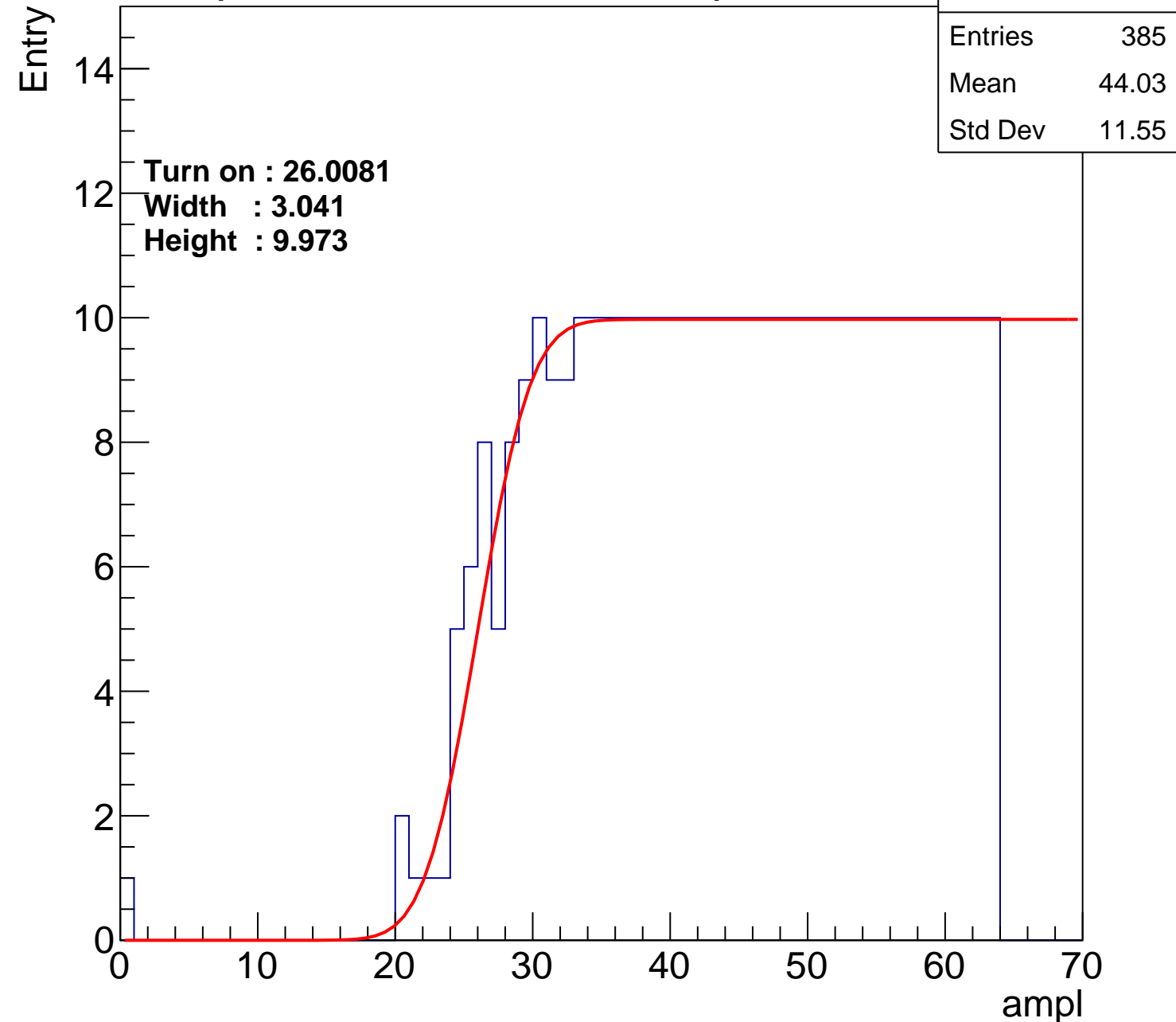
Width : 3.041

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.09
Std Dev	11.09

Turn on : 27.8252

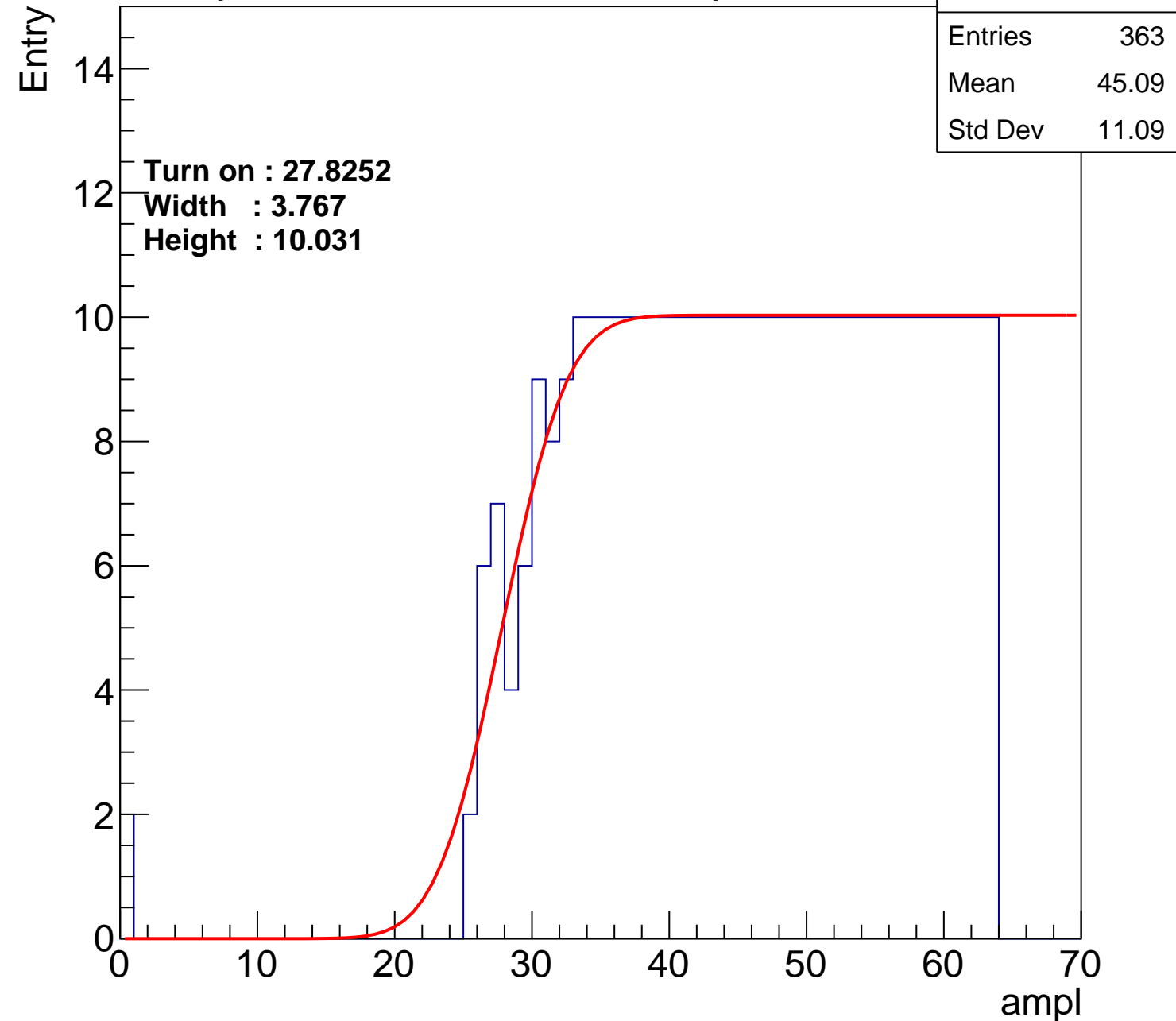
Width : 3.767

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.01
Std Dev	11.85

Turn on : 26.5190

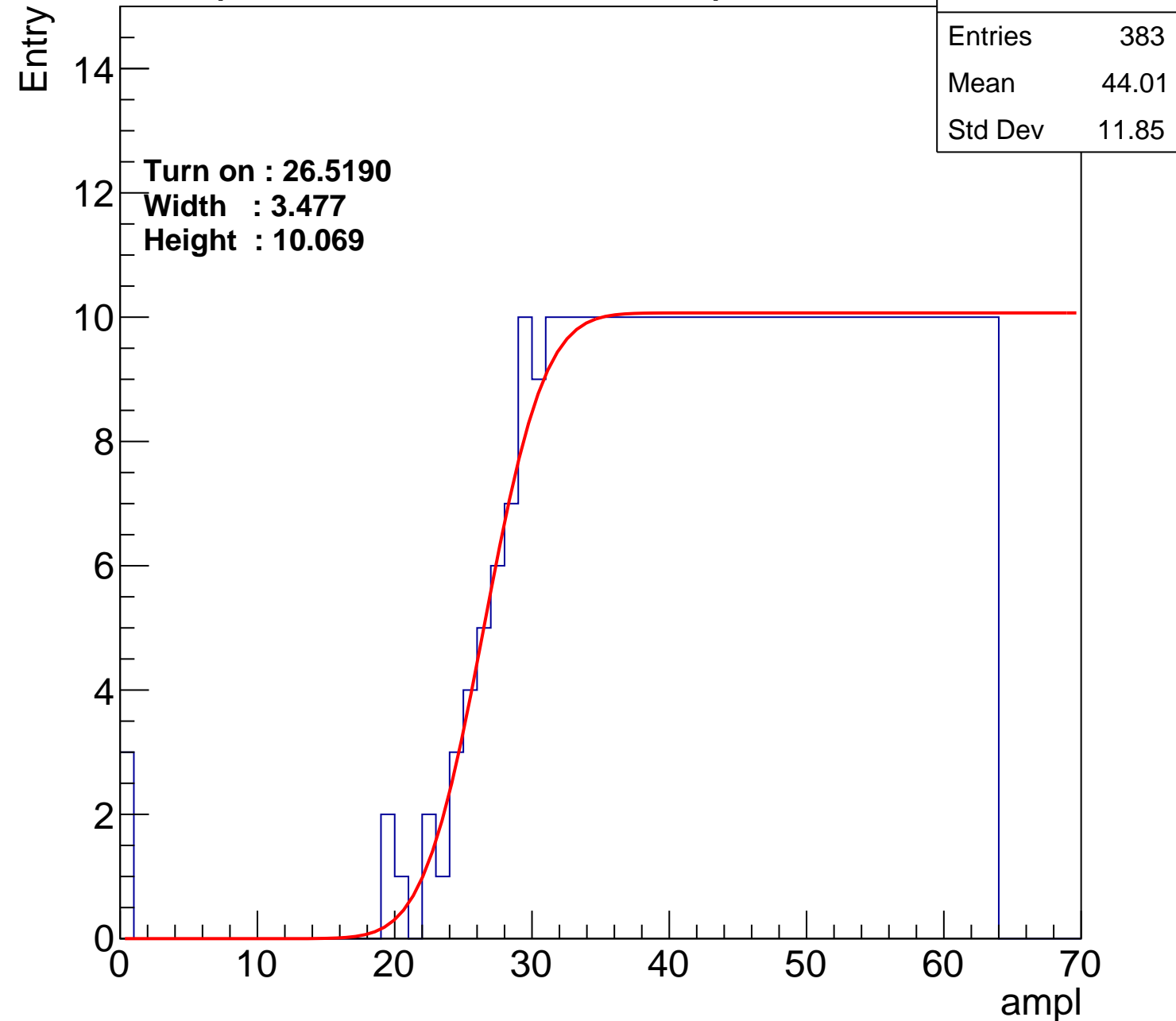
Width : 3.477

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.3
Std Dev	11.69

Turn on : 27.5000

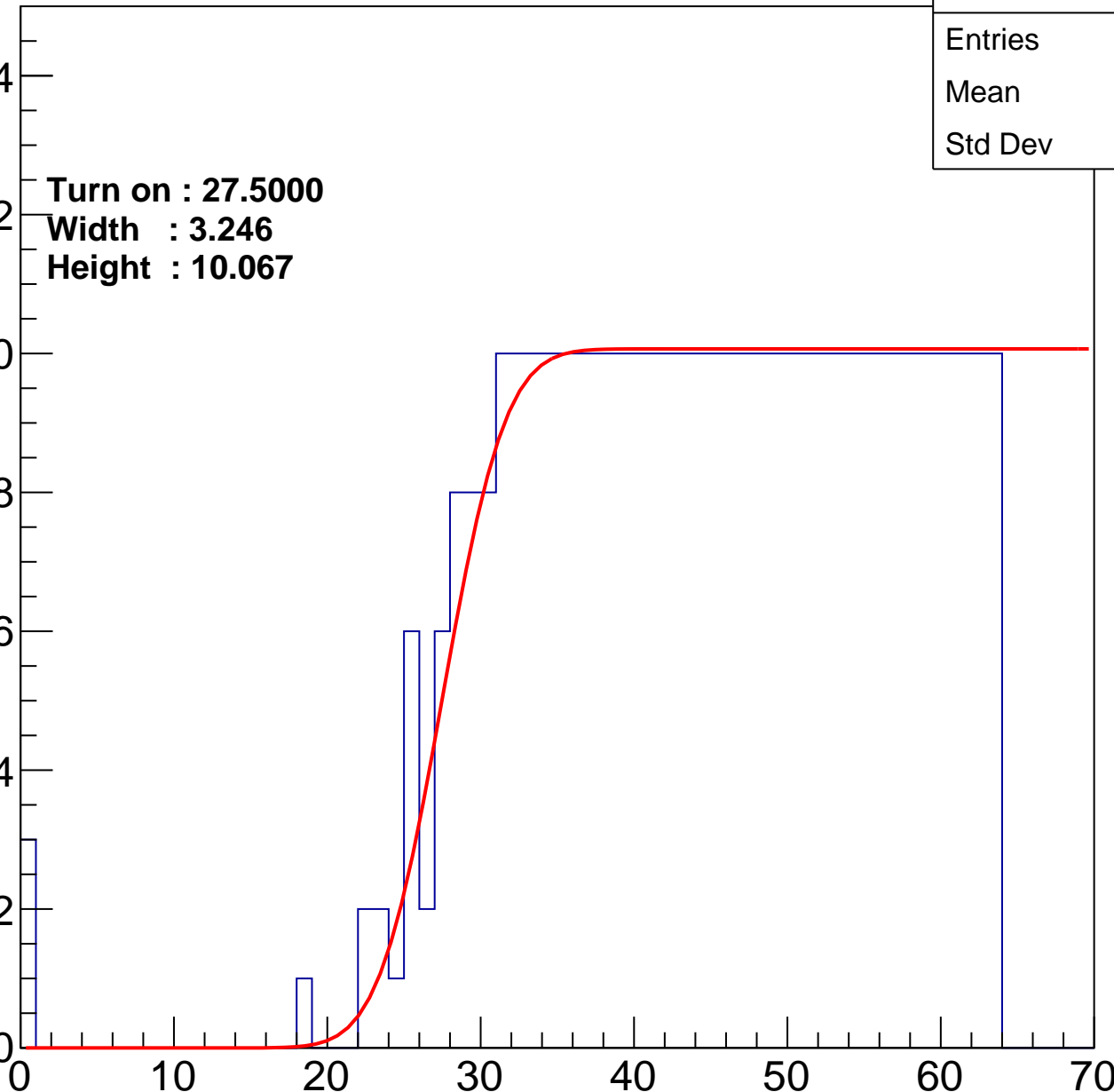
Width : 3.246

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.76
Std Dev	11.79

Turn on : 25.3455

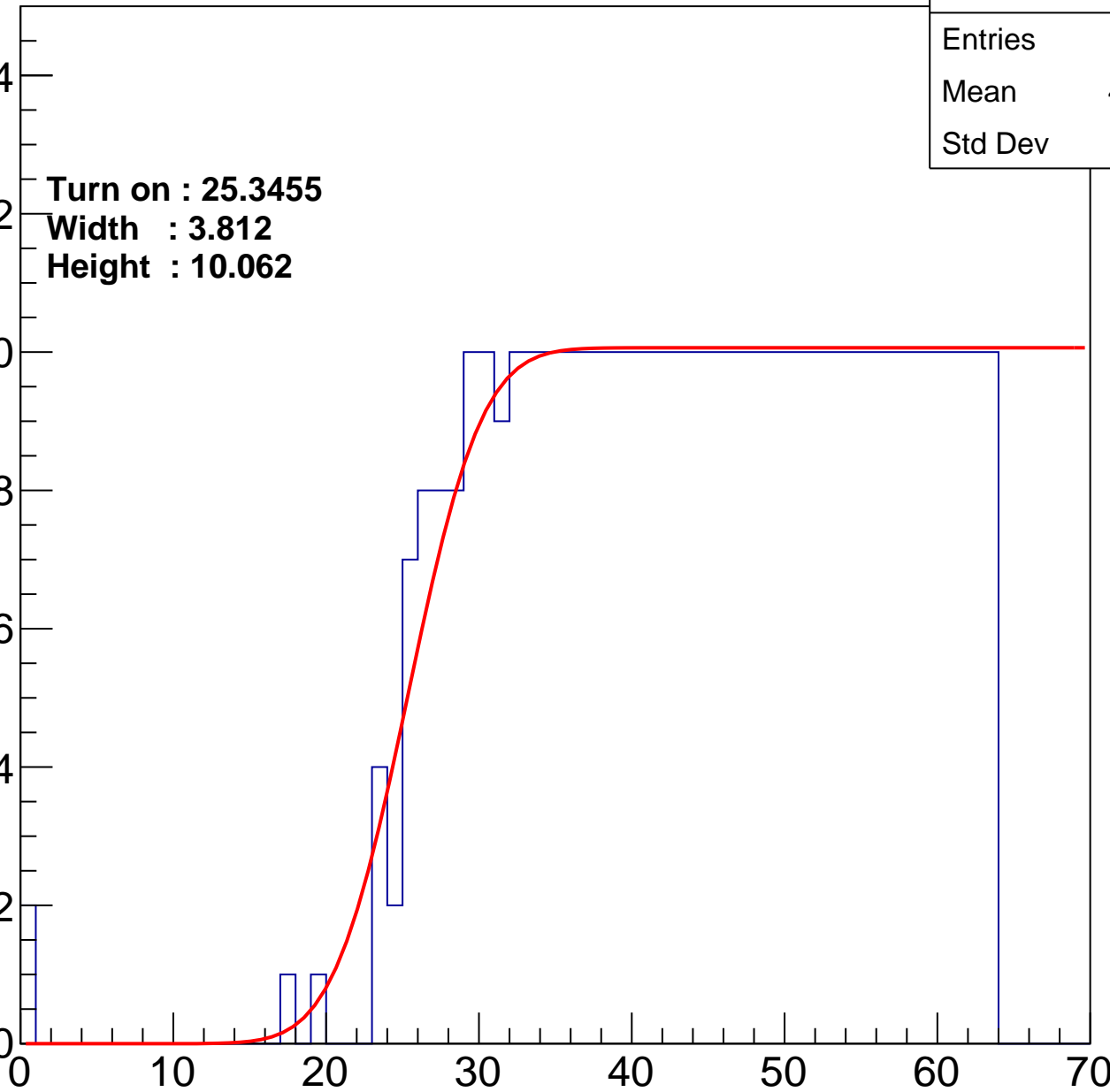
Width : 3.812

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.61
Std Dev	11.82

Turn on : 24.7318

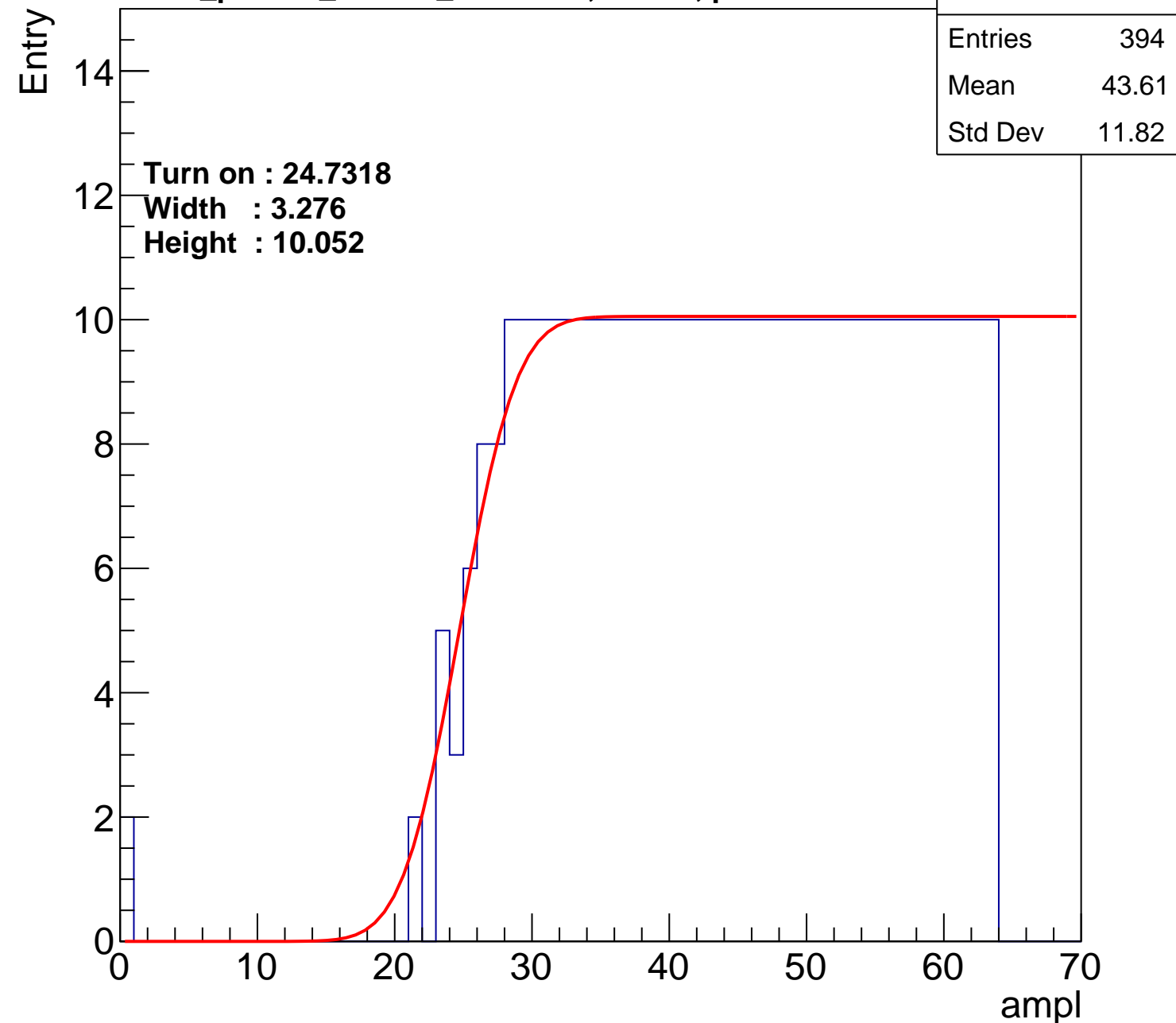
Width : 3.276

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.95
Std Dev	12.47

Turn on : 24.1800

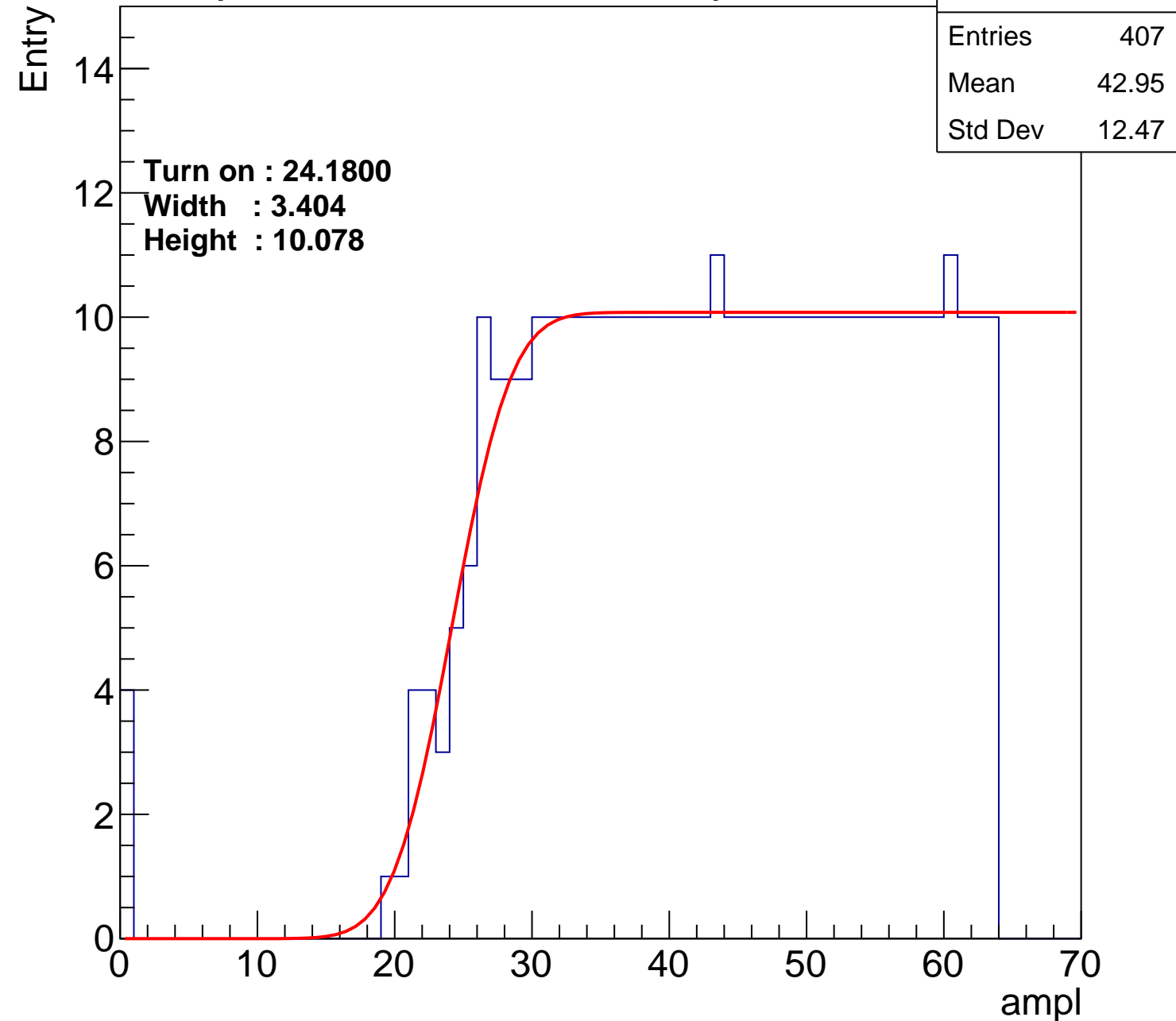
Width : 3.404

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.99
Std Dev	11.88

Turn on : 27.1736

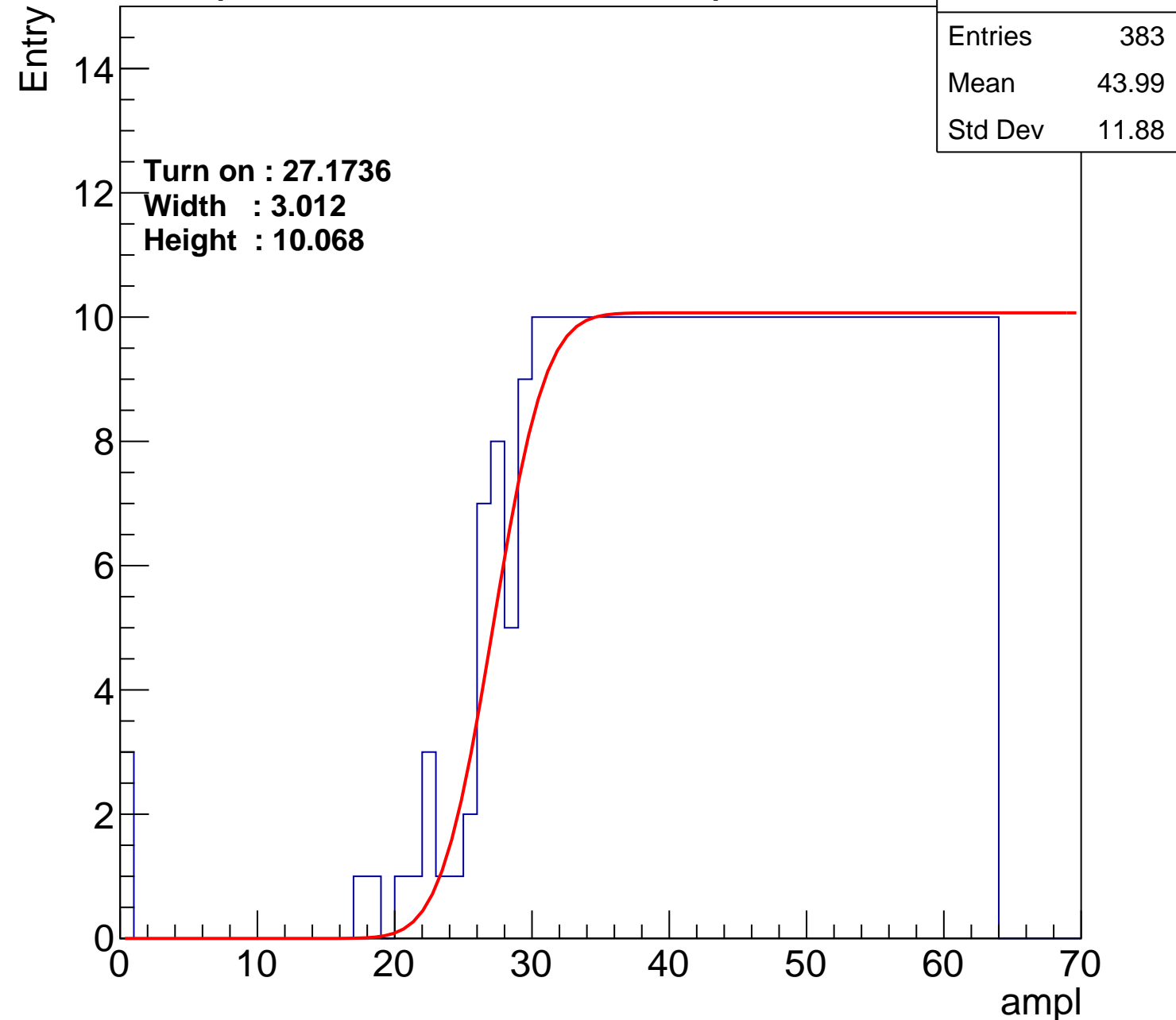
Width : 3.012

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.91
Std Dev	12.06

Turn on : 26.6210

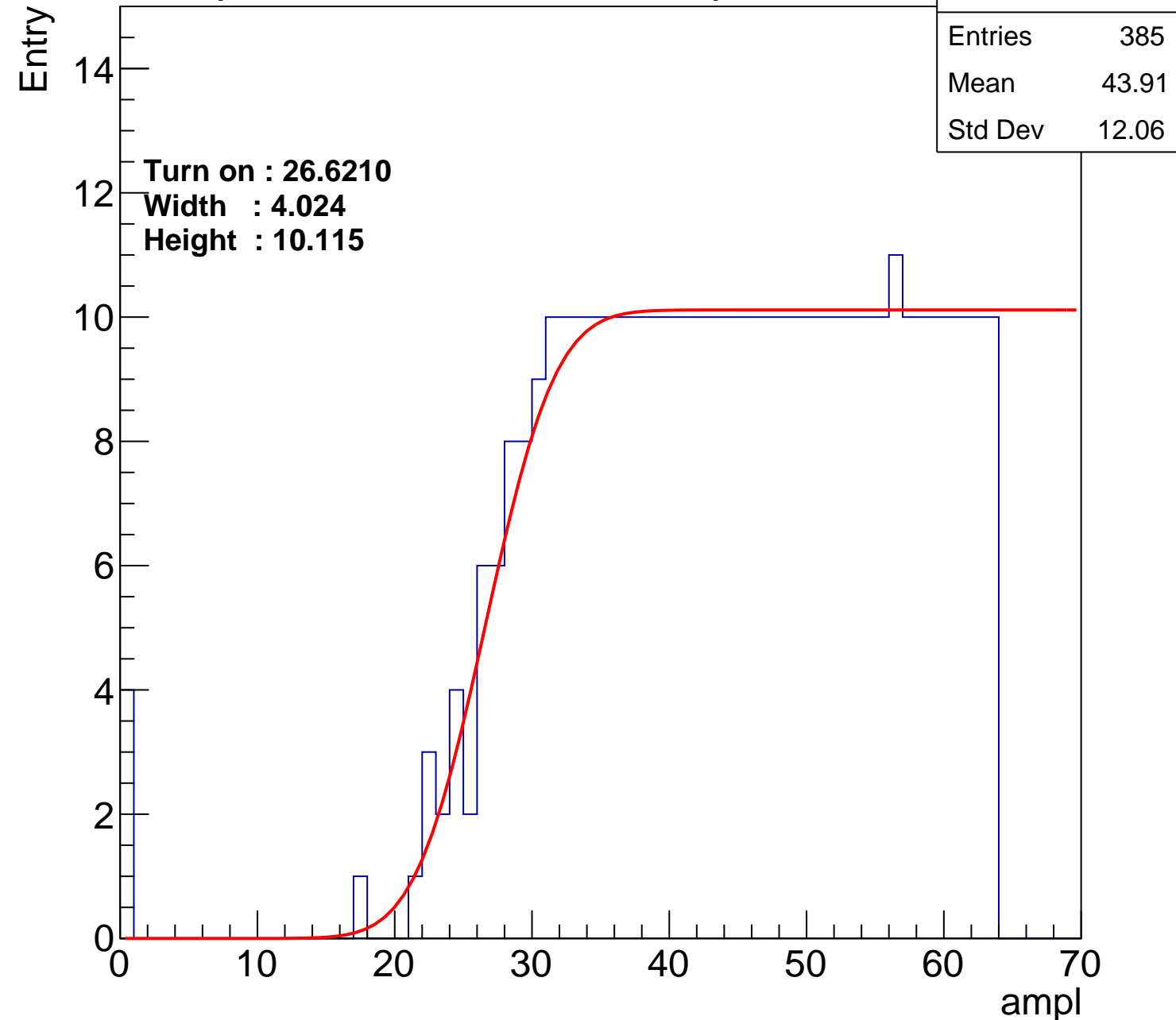
Width : 4.024

Height : 10.115

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.14
Std Dev	12.47

Turn on : 24.5406

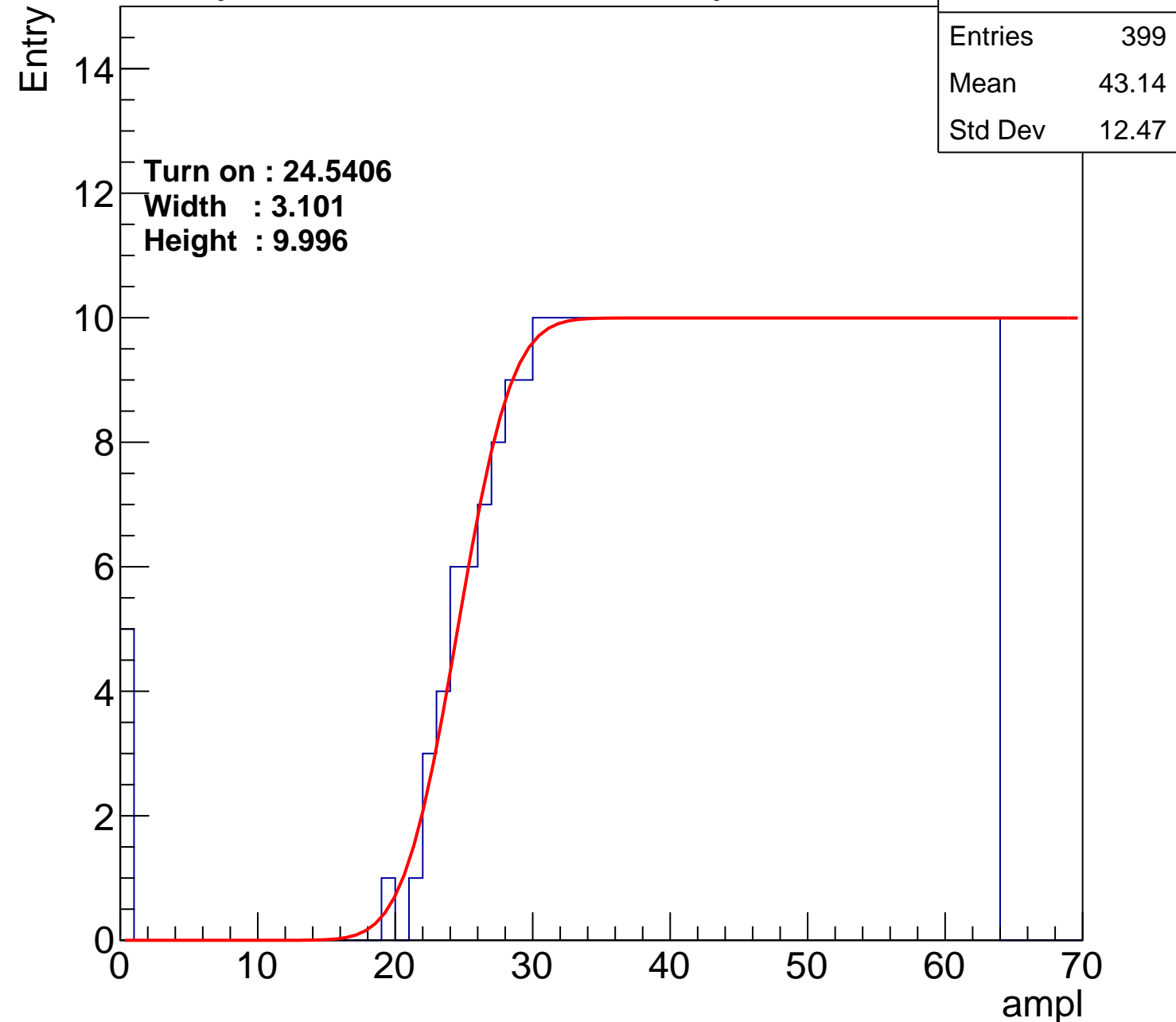
Width : 3.101

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.7
Std Dev	11.94

Turn on : 25.5064

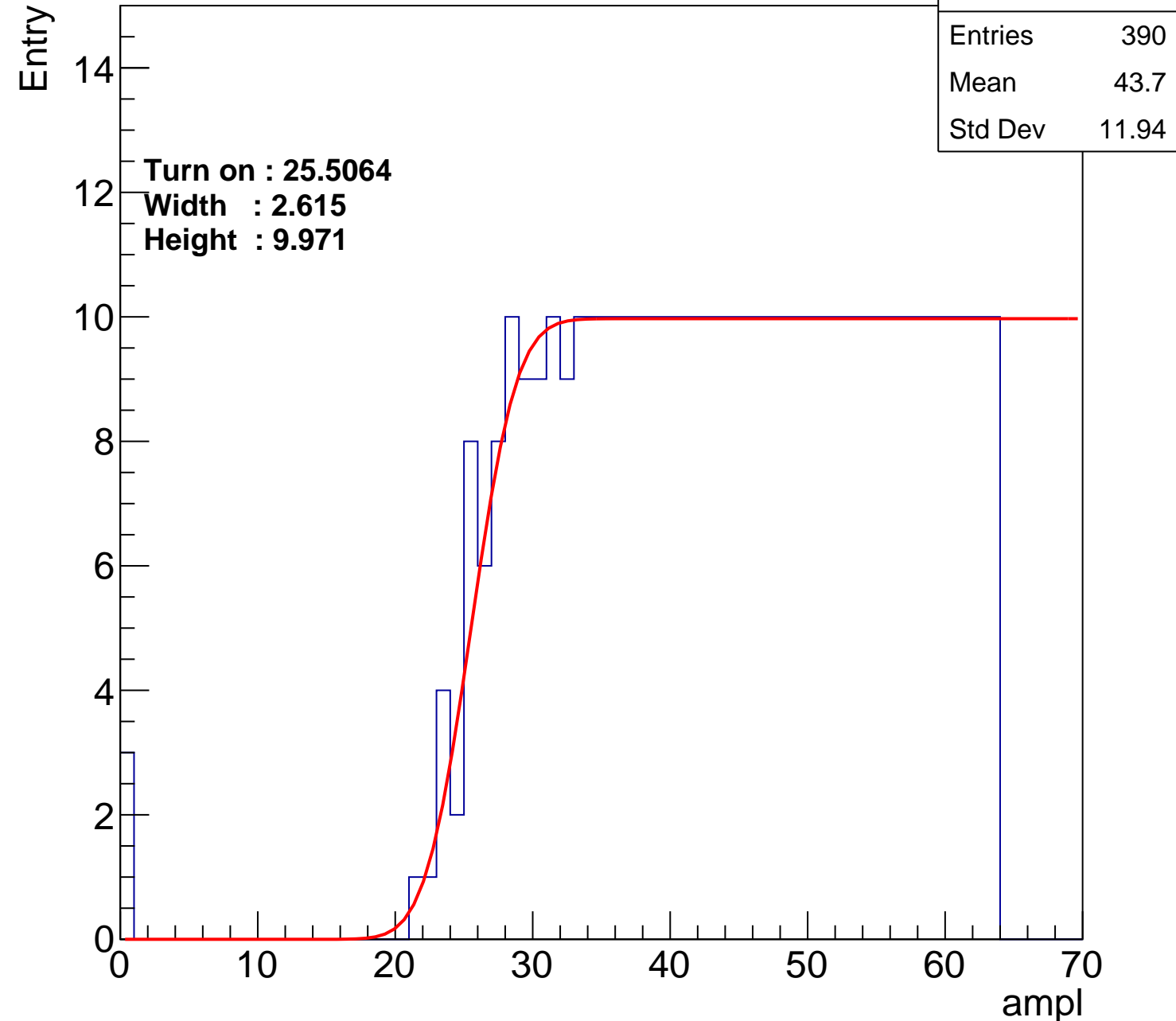
Width : 2.615

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.19
Std Dev	11.72

Turn on : 26.8350

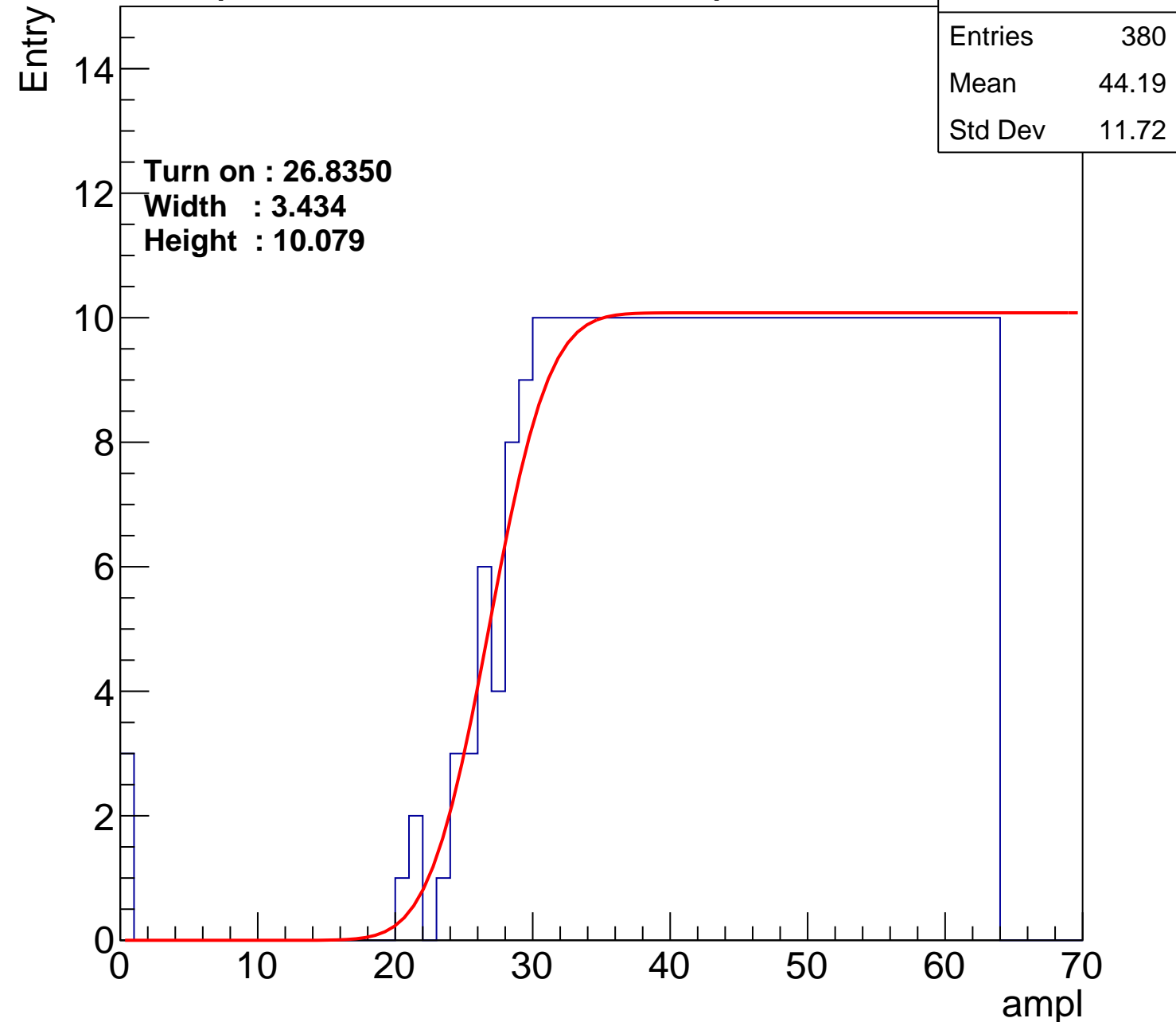
Width : 3.434

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.87
Std Dev	12.27

Turn on : 23.4070

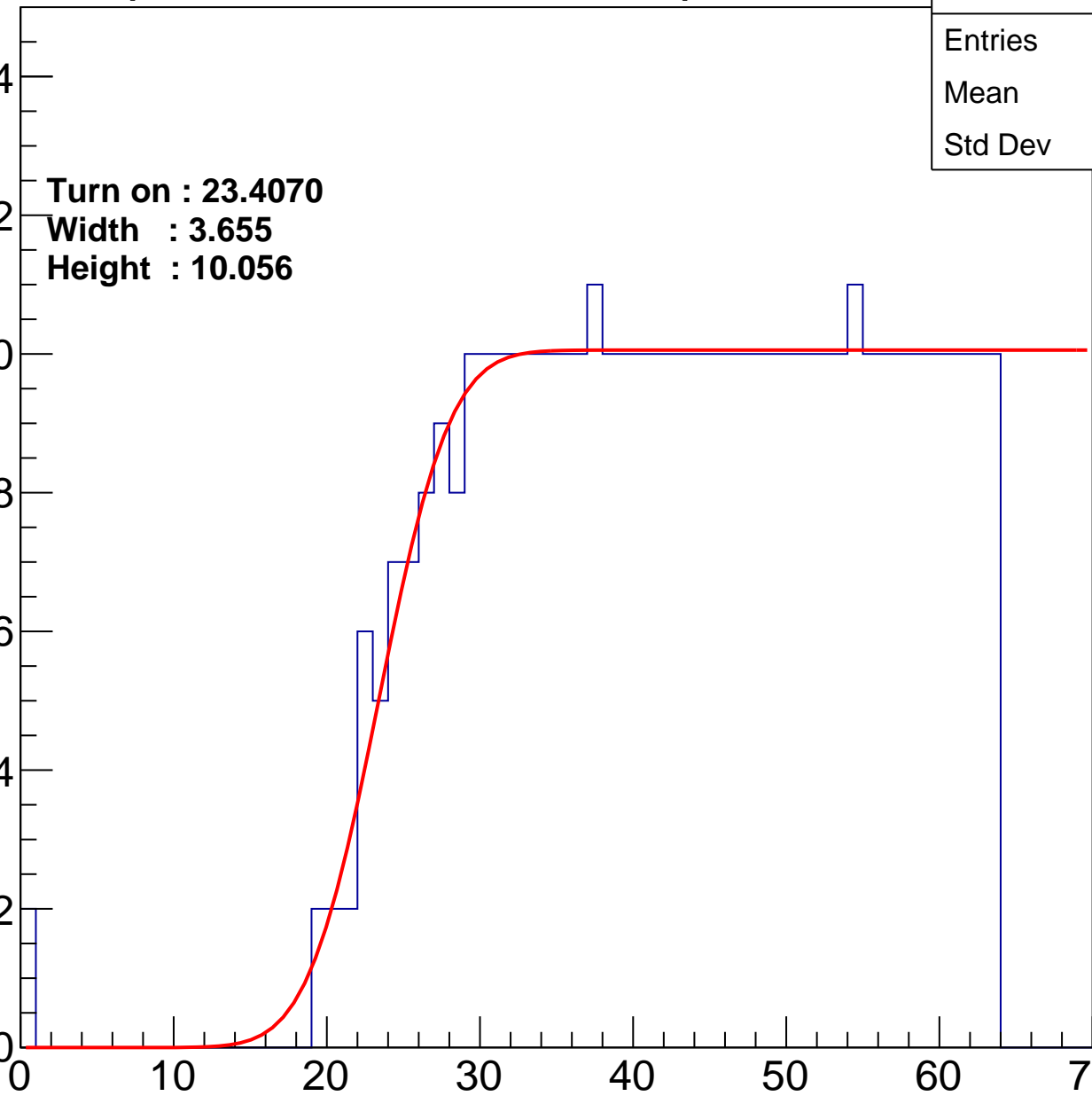
Width : 3.655

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	415
Mean	42.47
Std Dev	12.59

Turn on : 22.6829

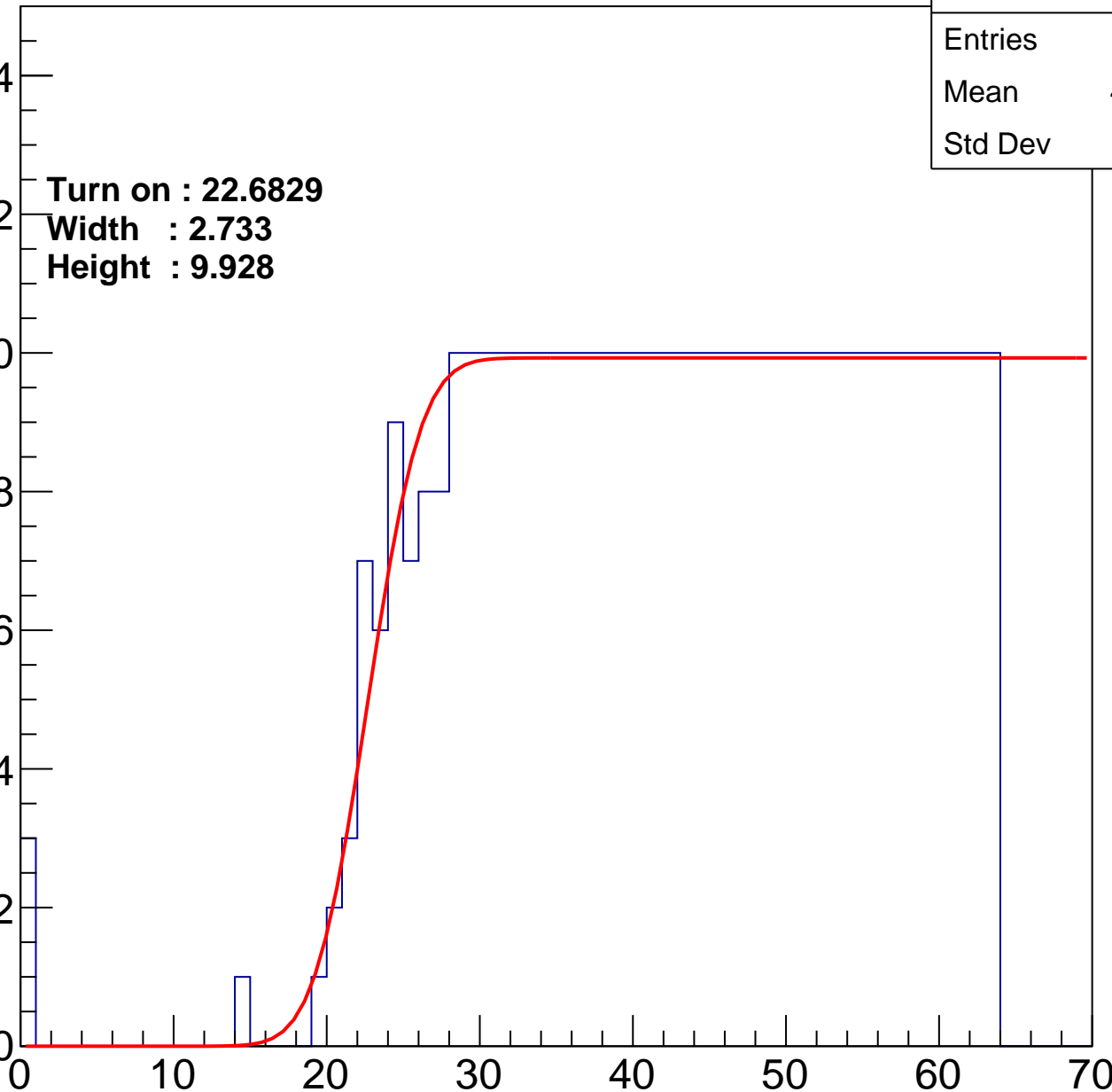
Width : 2.733

Height : 9.928

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U10-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	415
Mean	42.47
Std Dev	12.59

Turn on : 22.6829

Width : 2.733

Height : 9.928

Entry

14
12
10
8
6
4
2
0

ampl

