

B1L100S, U25-ch0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	401
Mean	43.11
Std Dev	12.36

Turn on : 24.4968

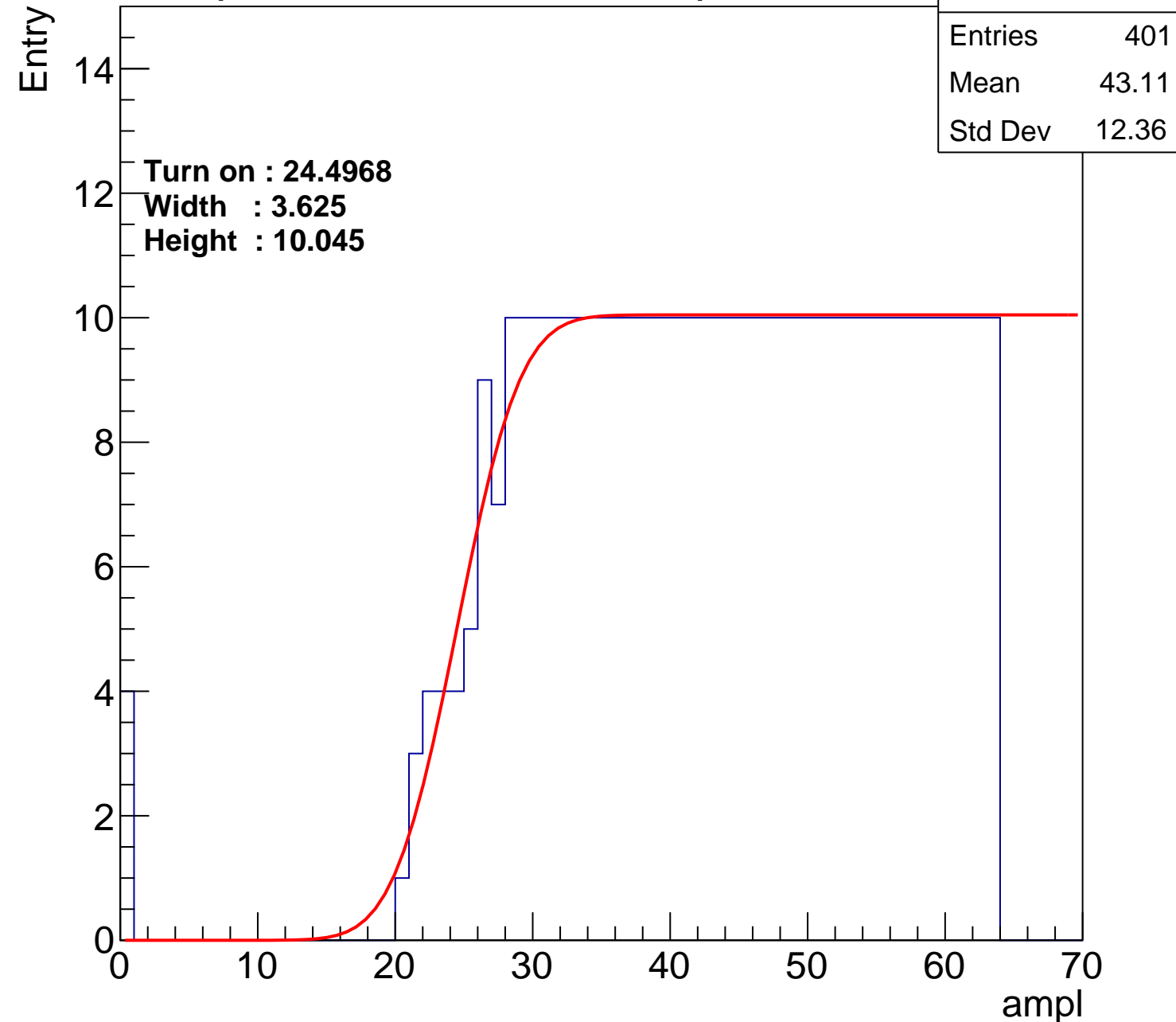
Width : 3.625

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.09
Std Dev	11.63

Turn on : 28.7946

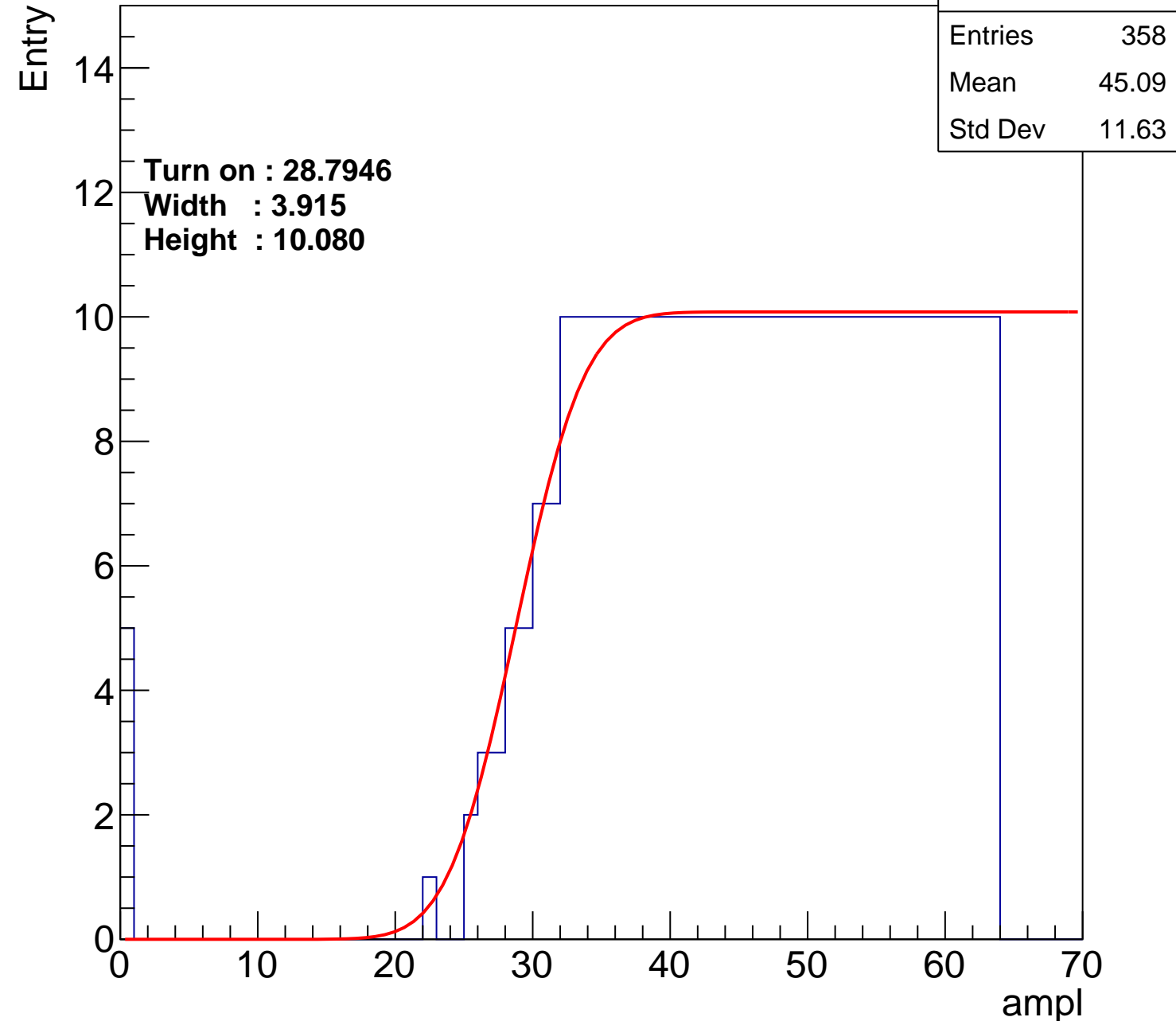
Width : 3.915

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	45
Std Dev	10.93

Turn on : 27.1175

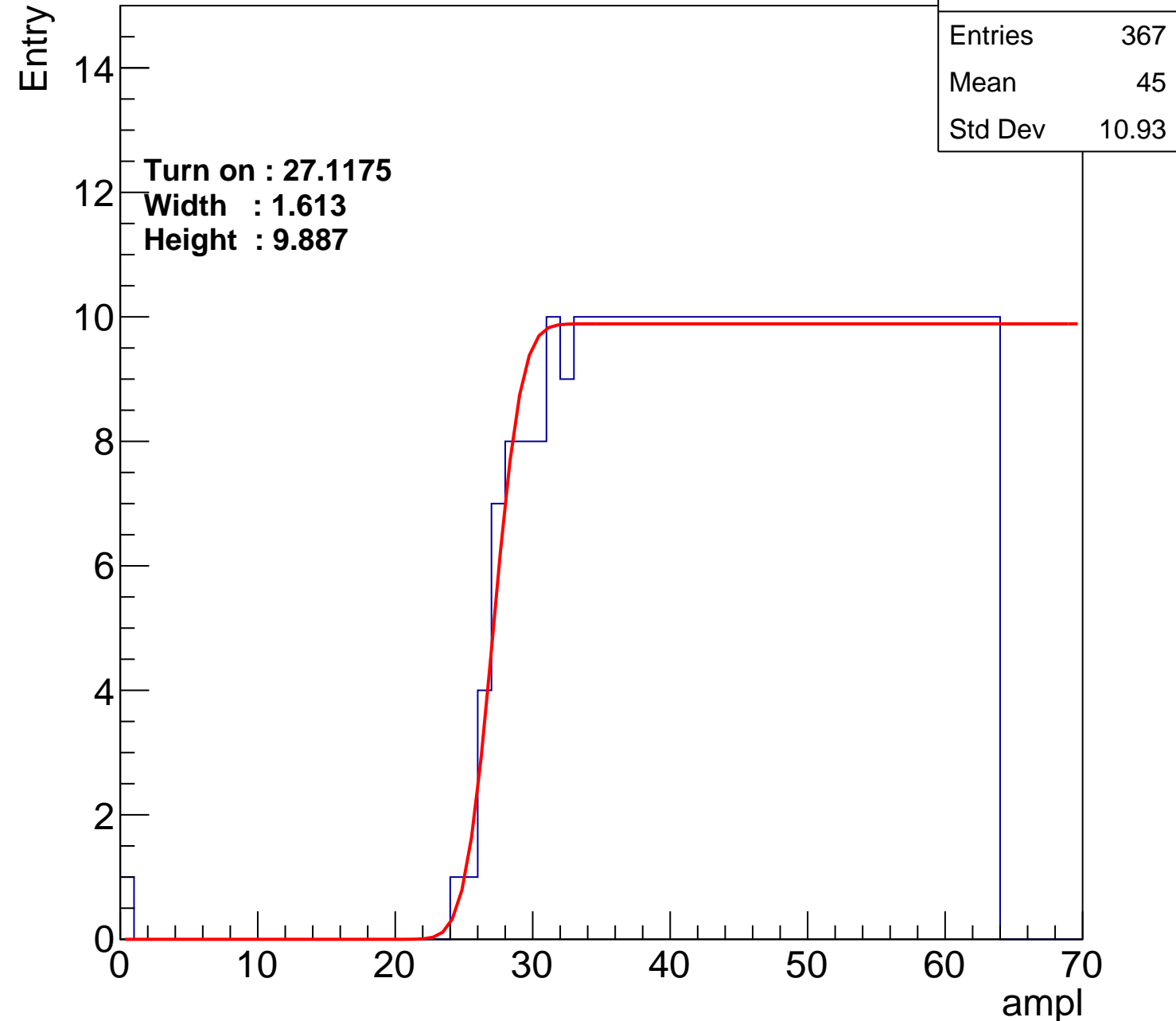
Width : 1.613

Height : 9.887

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 25.1511

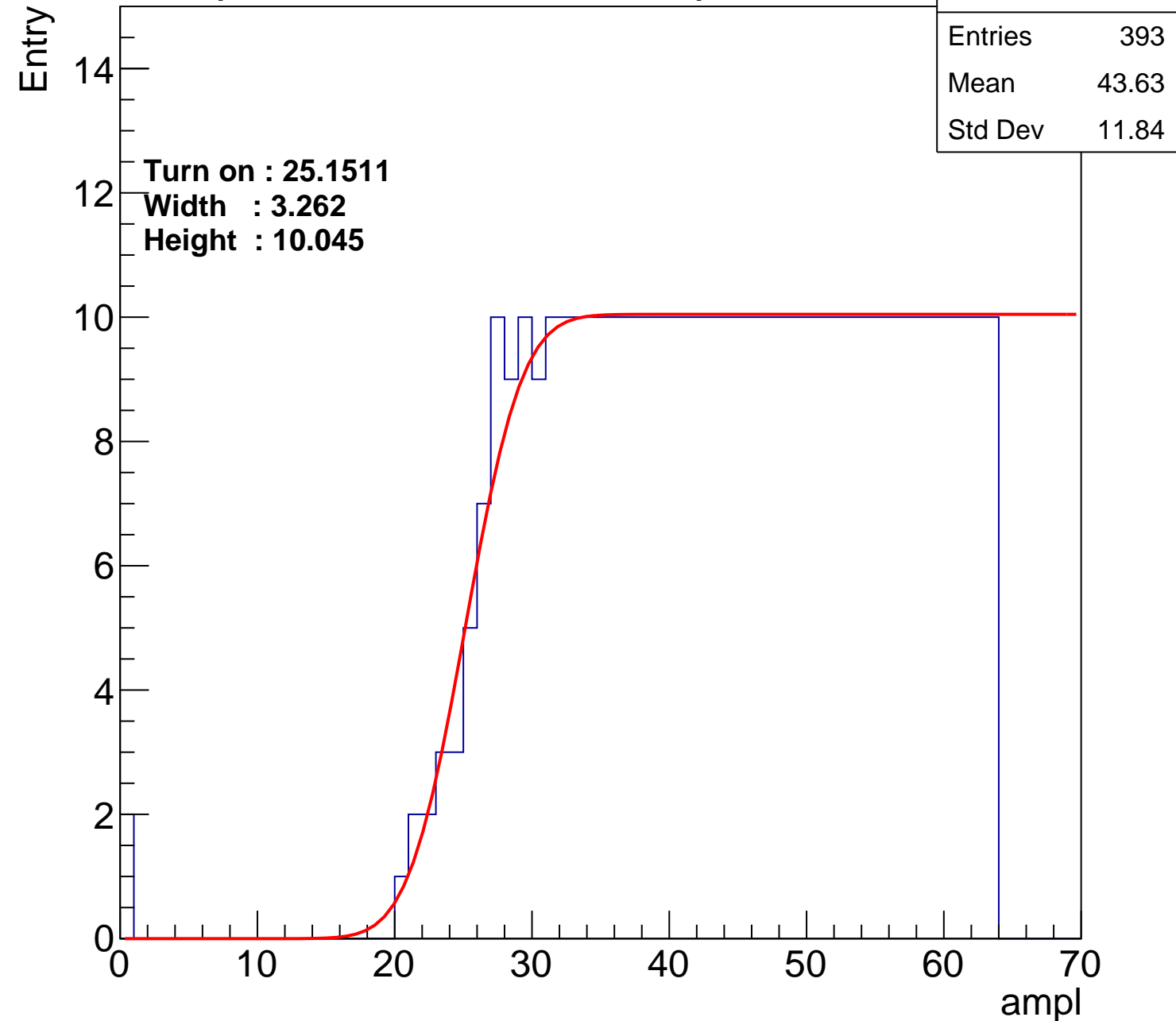
Width : 3.262

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	45.18
Std Dev	11.26

Turn on : 28.7536

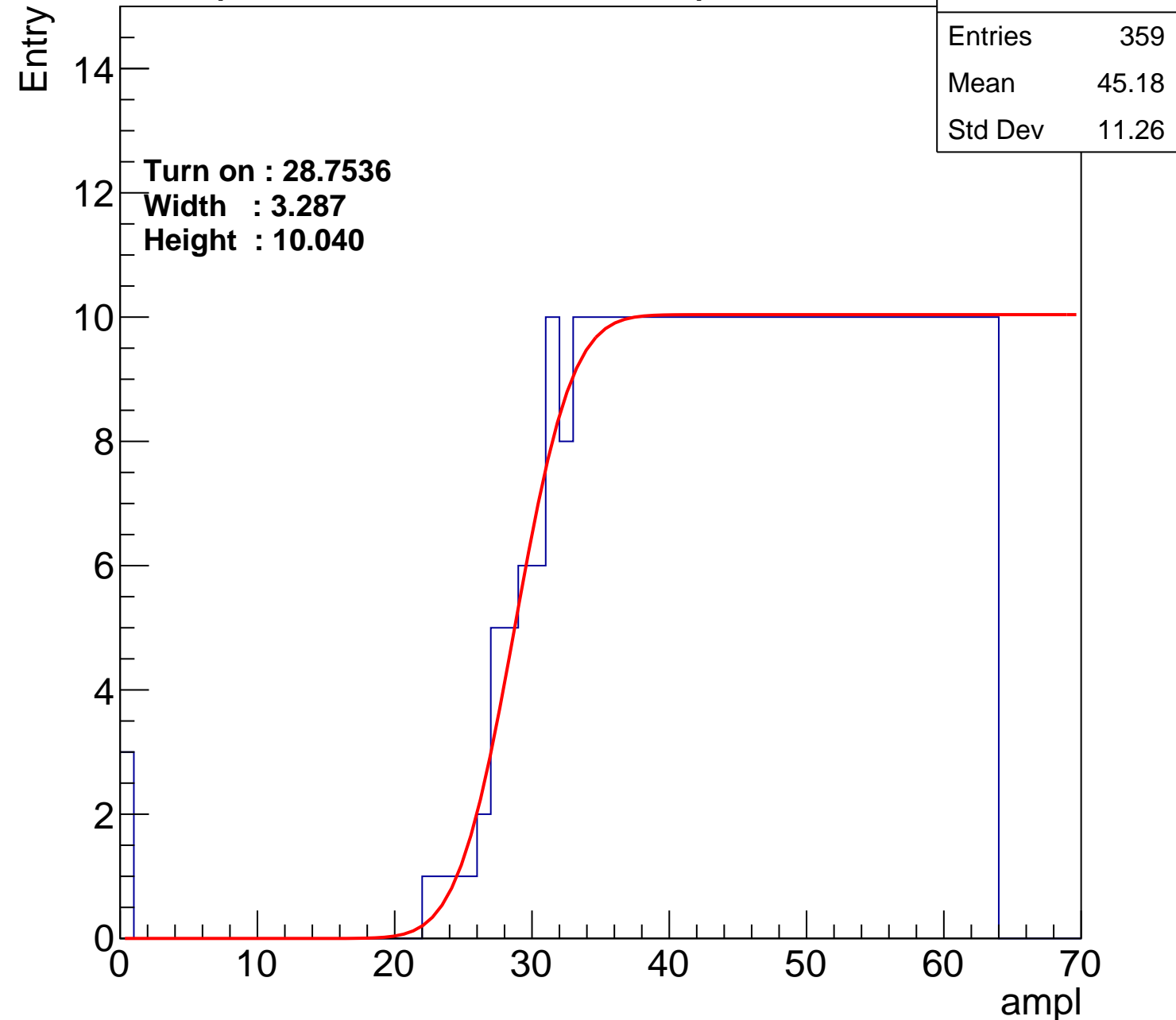
Width : 3.287

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.3202

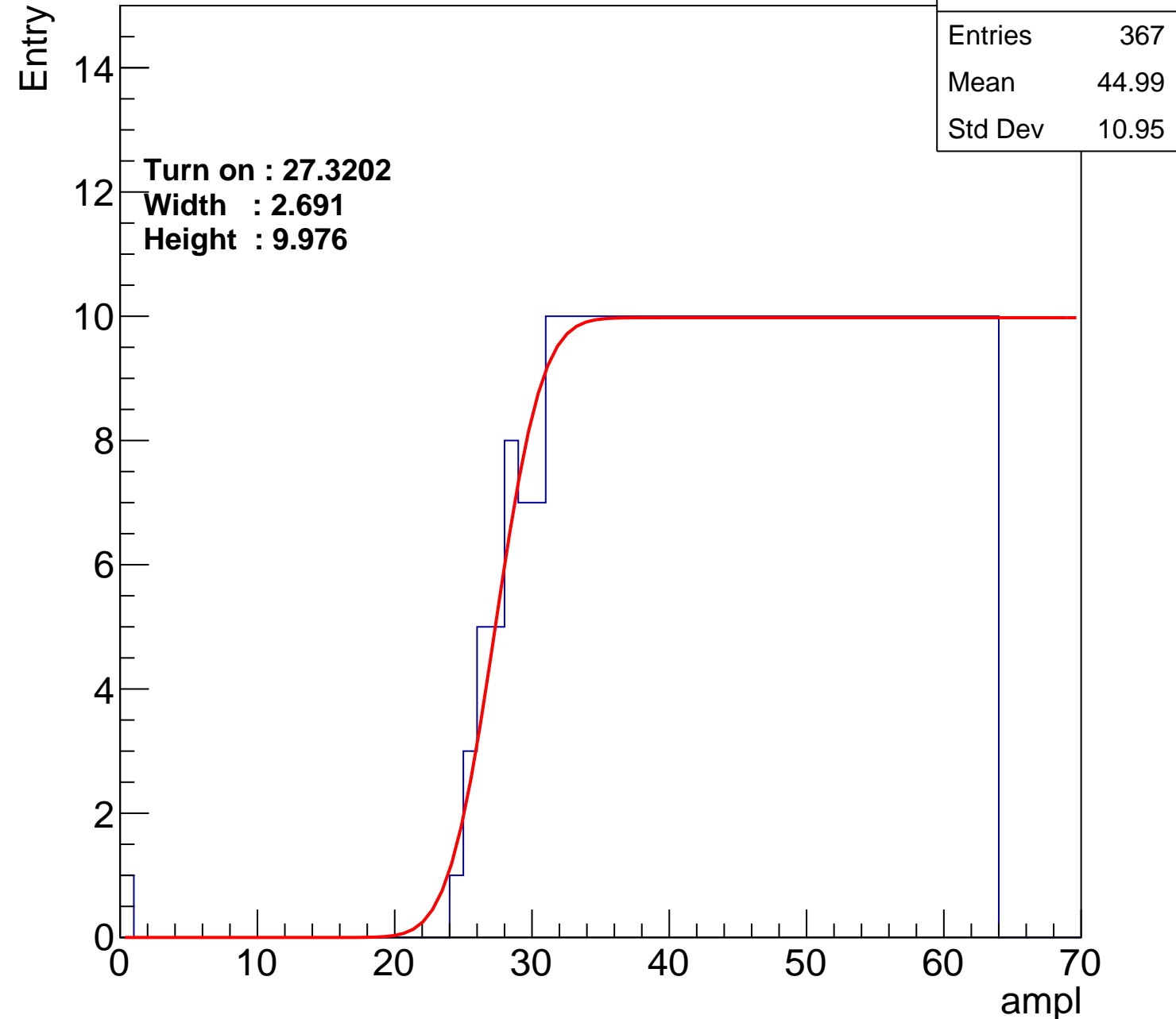
Width : 2.691

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch6

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.23
Std Dev	10.83

Turn on : 28.1201

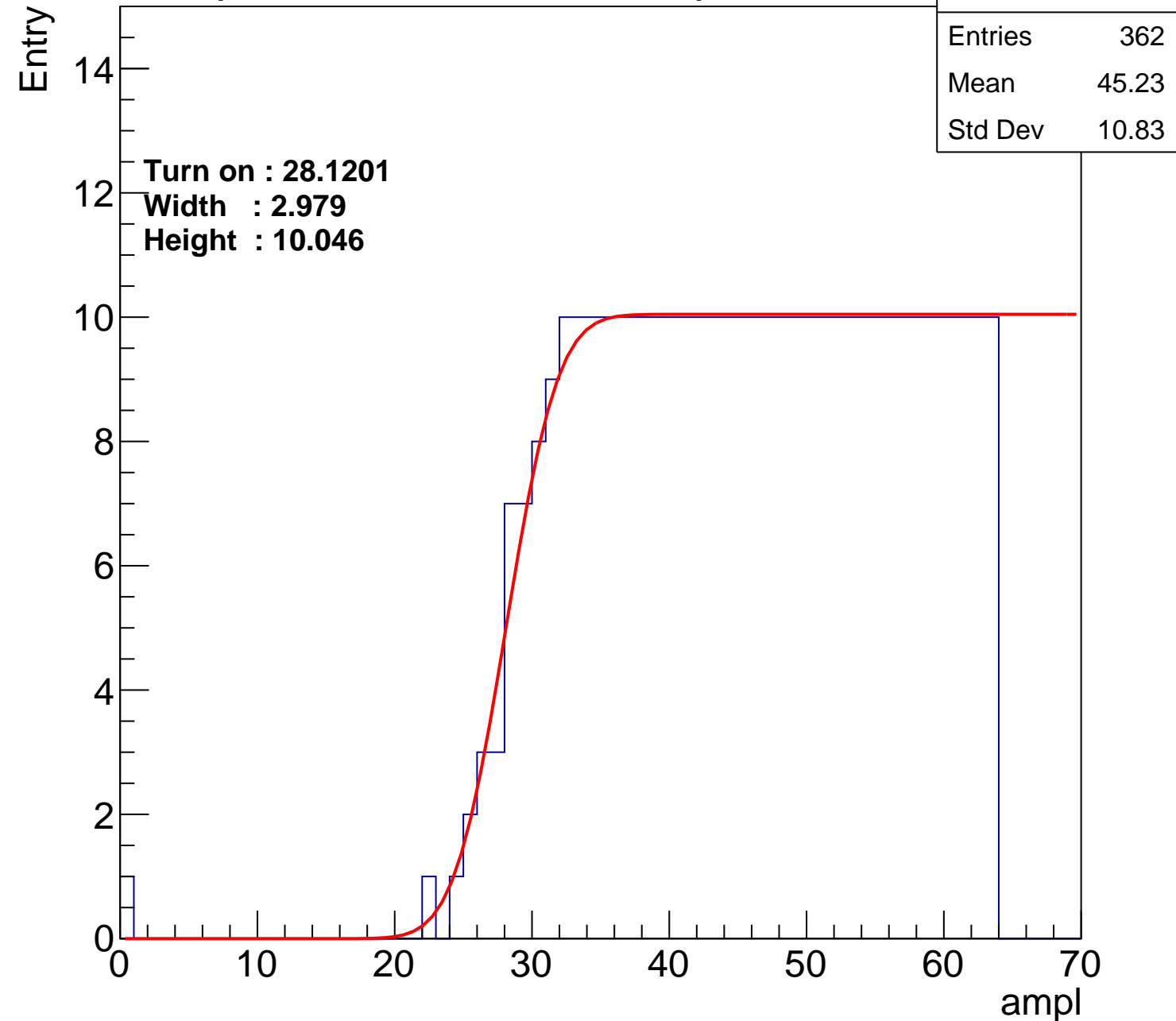
Width : 2.979

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch7

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 27.4505

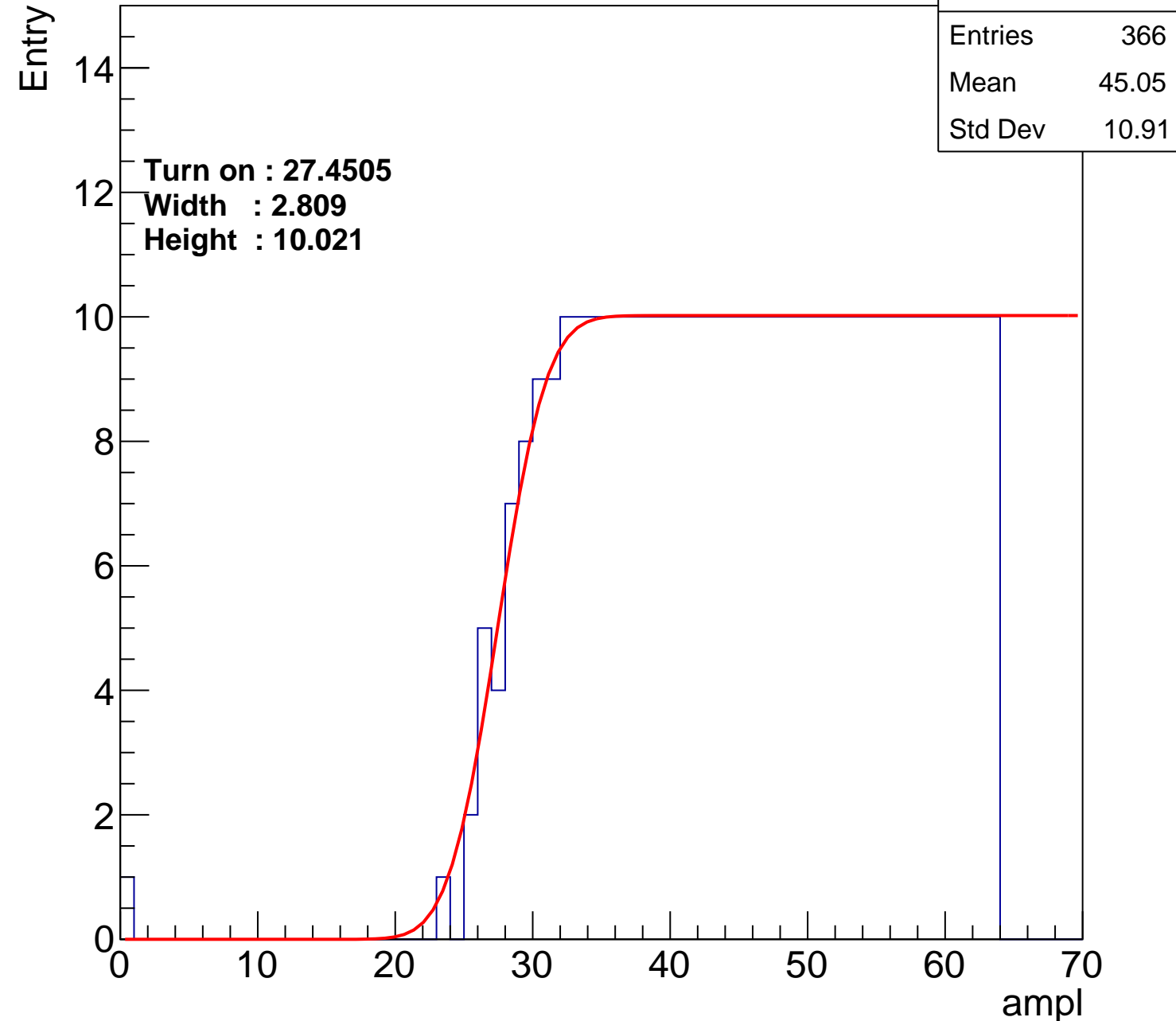
Width : 2.809

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch8

calib_packv5_042523_0143.root, FC#4, port A2

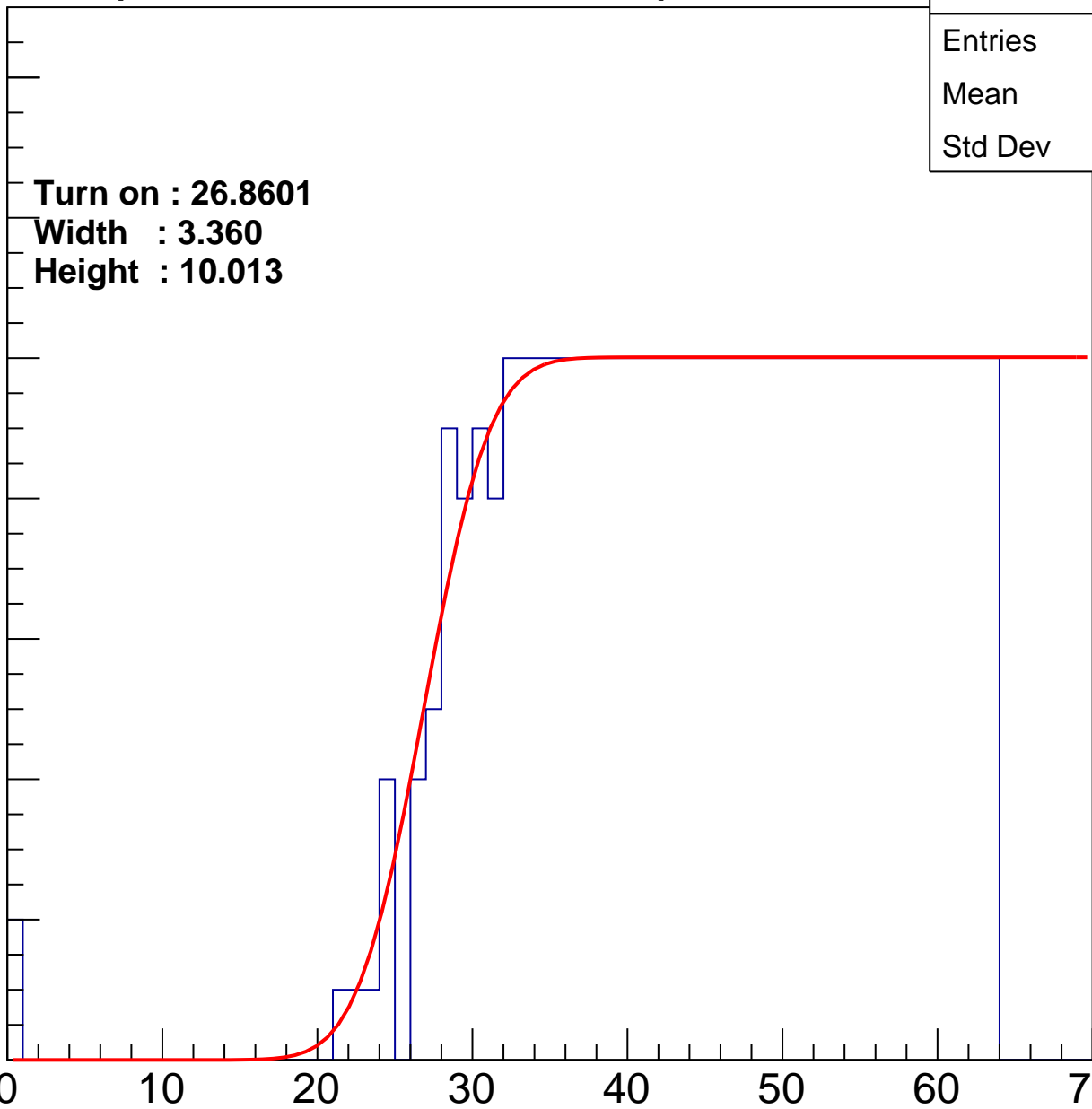
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8601
Width : 3.360
Height : 10.013

Entries	372
Mean	44.63
Std Dev	11.35

ampl



B1L100S, U25-ch9

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.21
Std Dev	11.36

Turn on : 28.8395

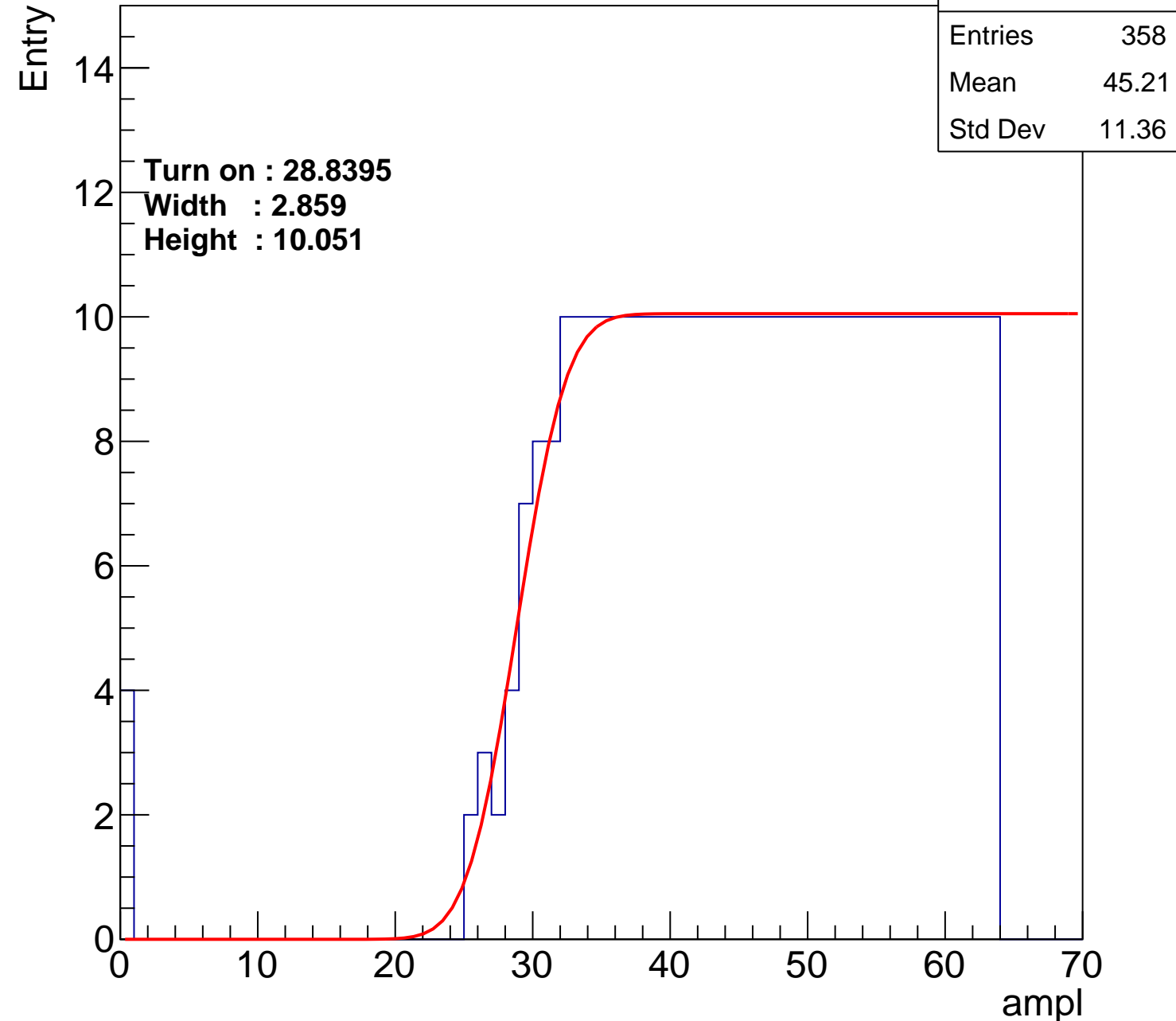
Width : 2.859

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch10

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.21
Std Dev	11.81

Turn on : 26.8680

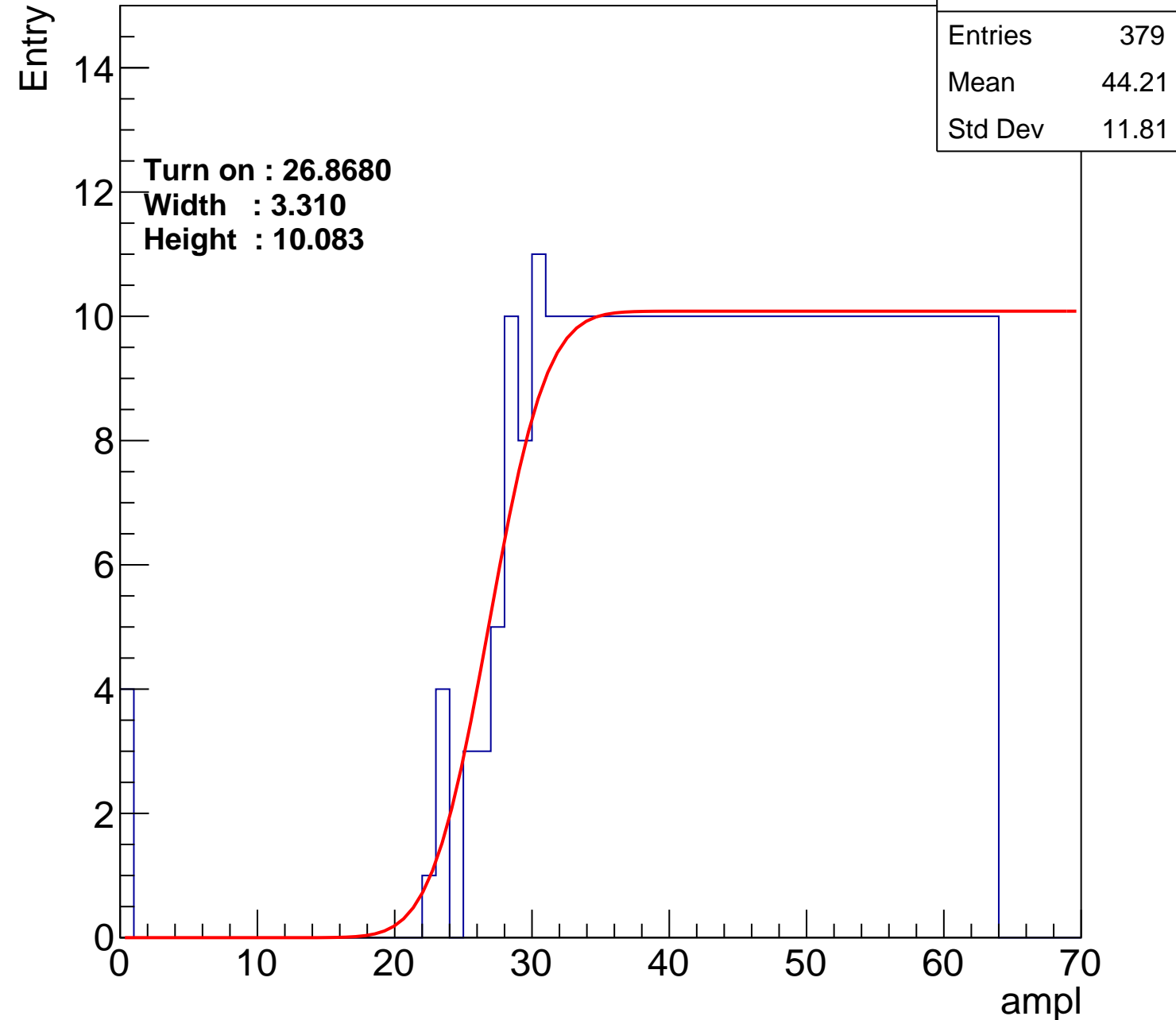
Width : 3.310

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch11

calib_packv5_042523_0143.root, FC#4, port A2

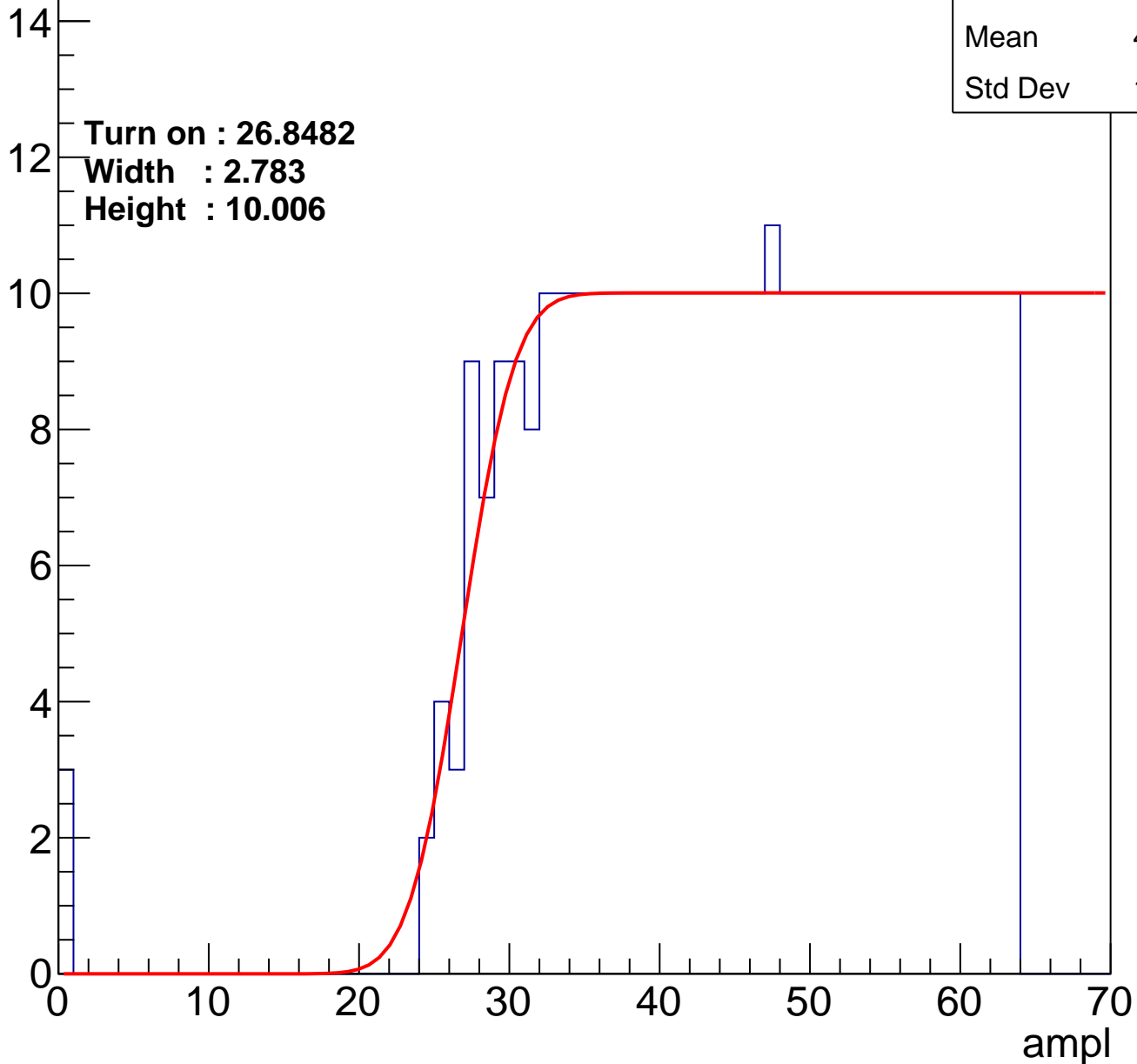
Entries	375
Mean	44.51
Std Dev	11.51

Turn on : 26.8482

Width : 2.783

Height : 10.006

Entry



B1L100S, U25-ch12

calib_packv5_042523_0143.root, FC#4, port A2

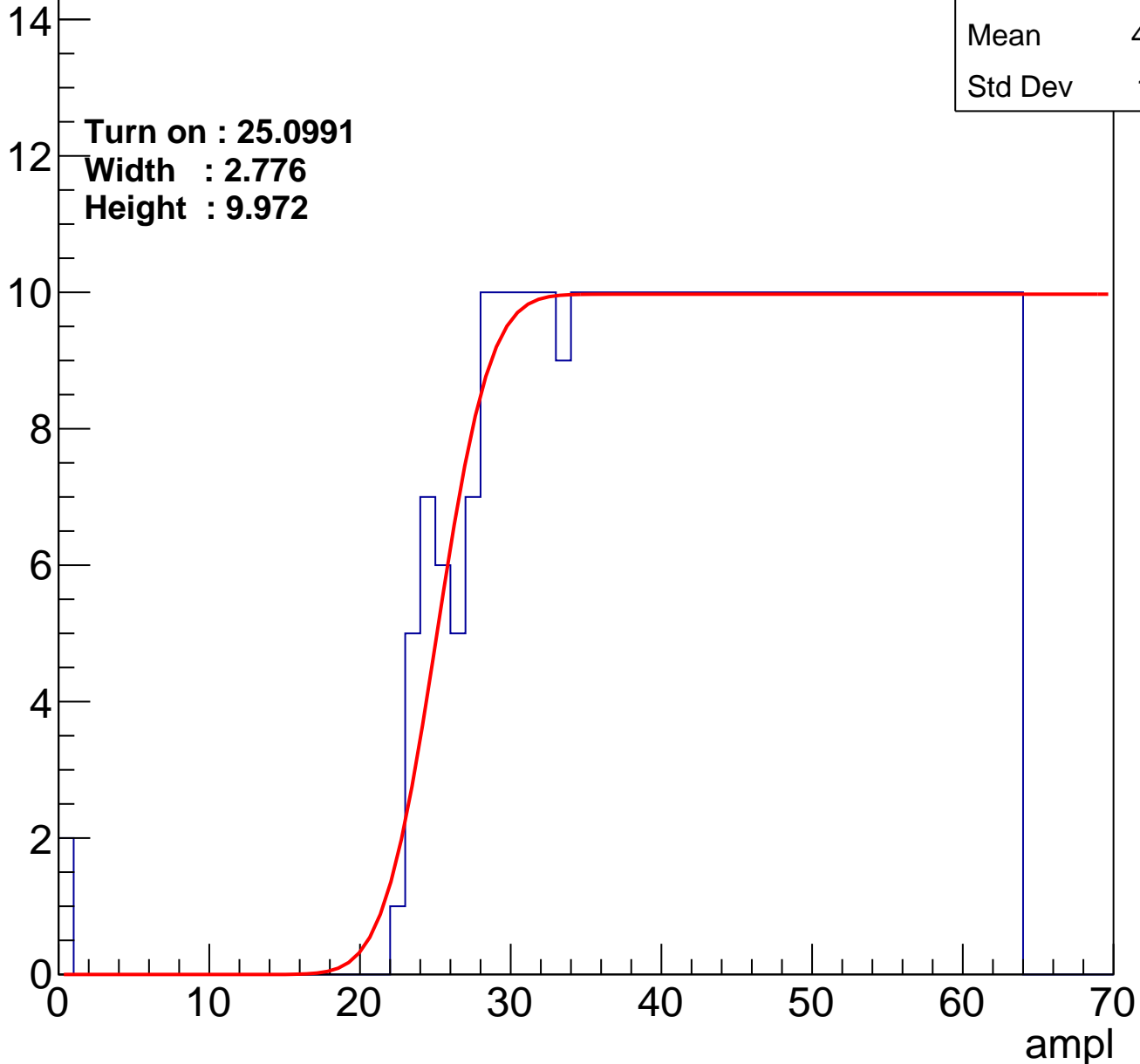
Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 25.0991

Width : 2.776

Height : 9.972

Entry



B1L100S, U25-ch13

calib_packv5_042523_0143.root, FC#4, port A2

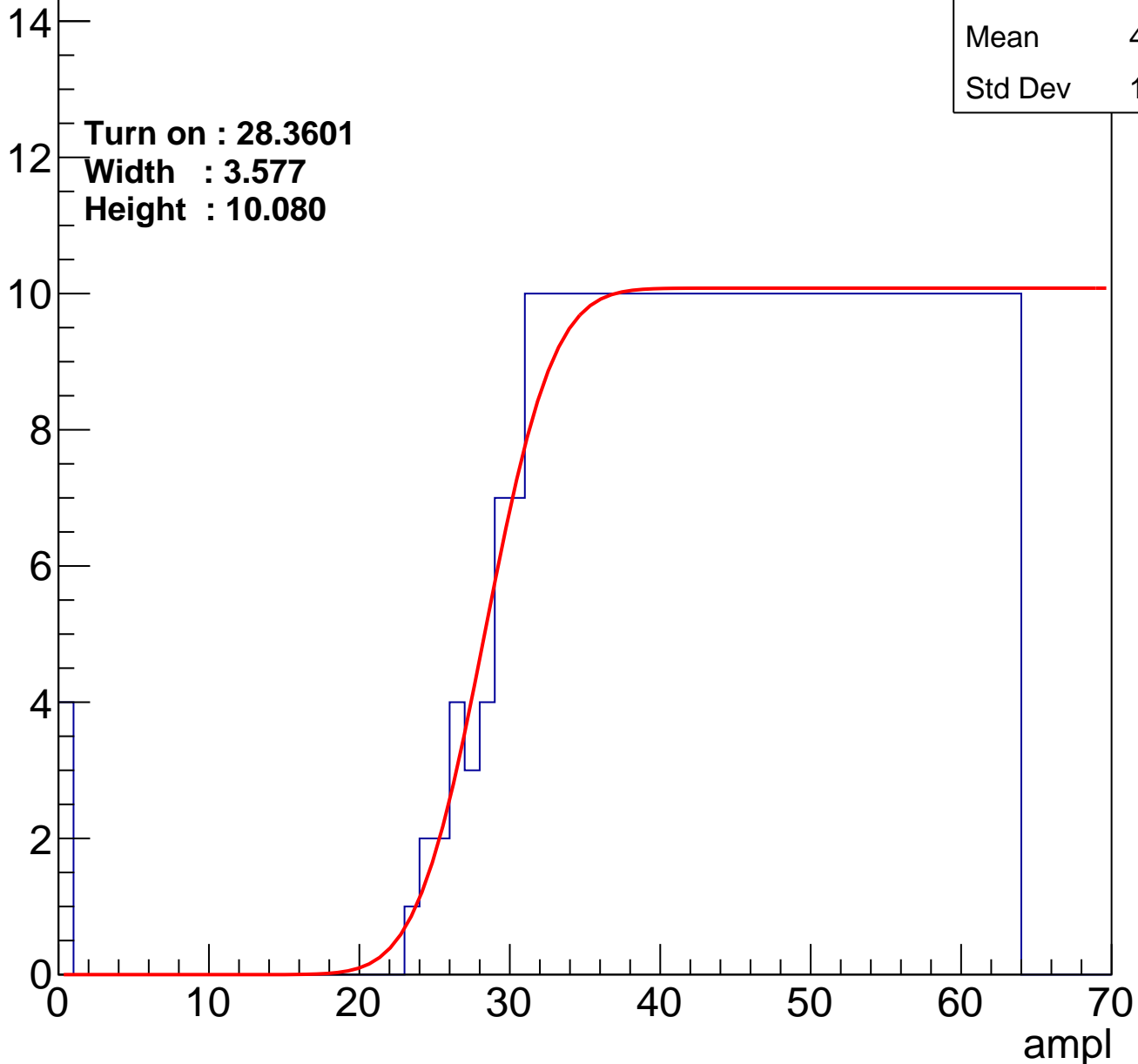
Entries	364
Mean	44.89
Std Dev	11.54

Turn on : 28.3601

Width : 3.577

Height : 10.080

Entry



B1L100S, U25-ch14

calib_packv5_042523_0143.root, FC#4, port A2

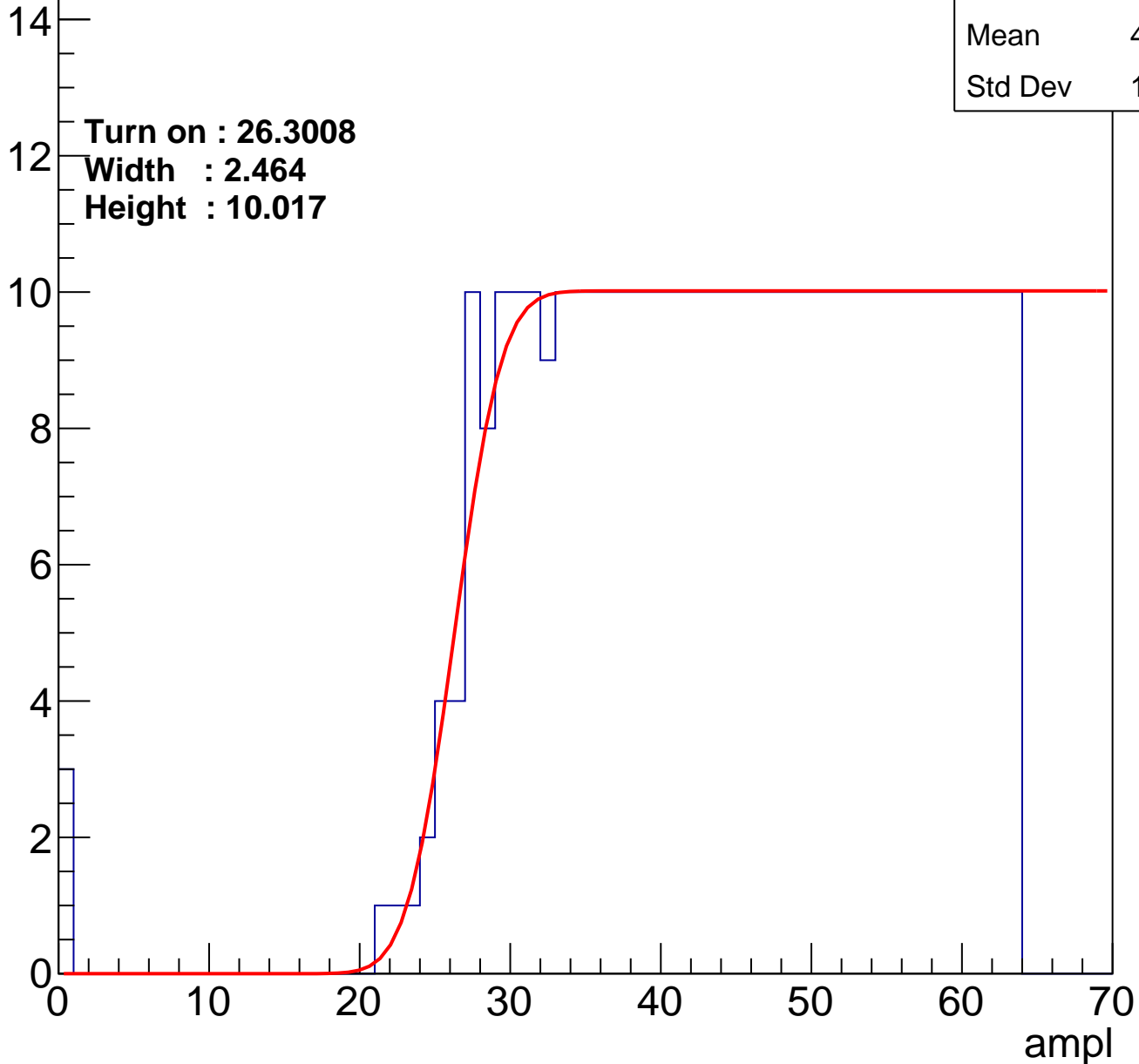
Entries	383
Mean	44.07
Std Dev	11.73

Turn on : 26.3008

Width : 2.464

Height : 10.017

Entry



B1L100S, U25-ch15

calib_packv5_042523_0143.root, FC#4, port A2

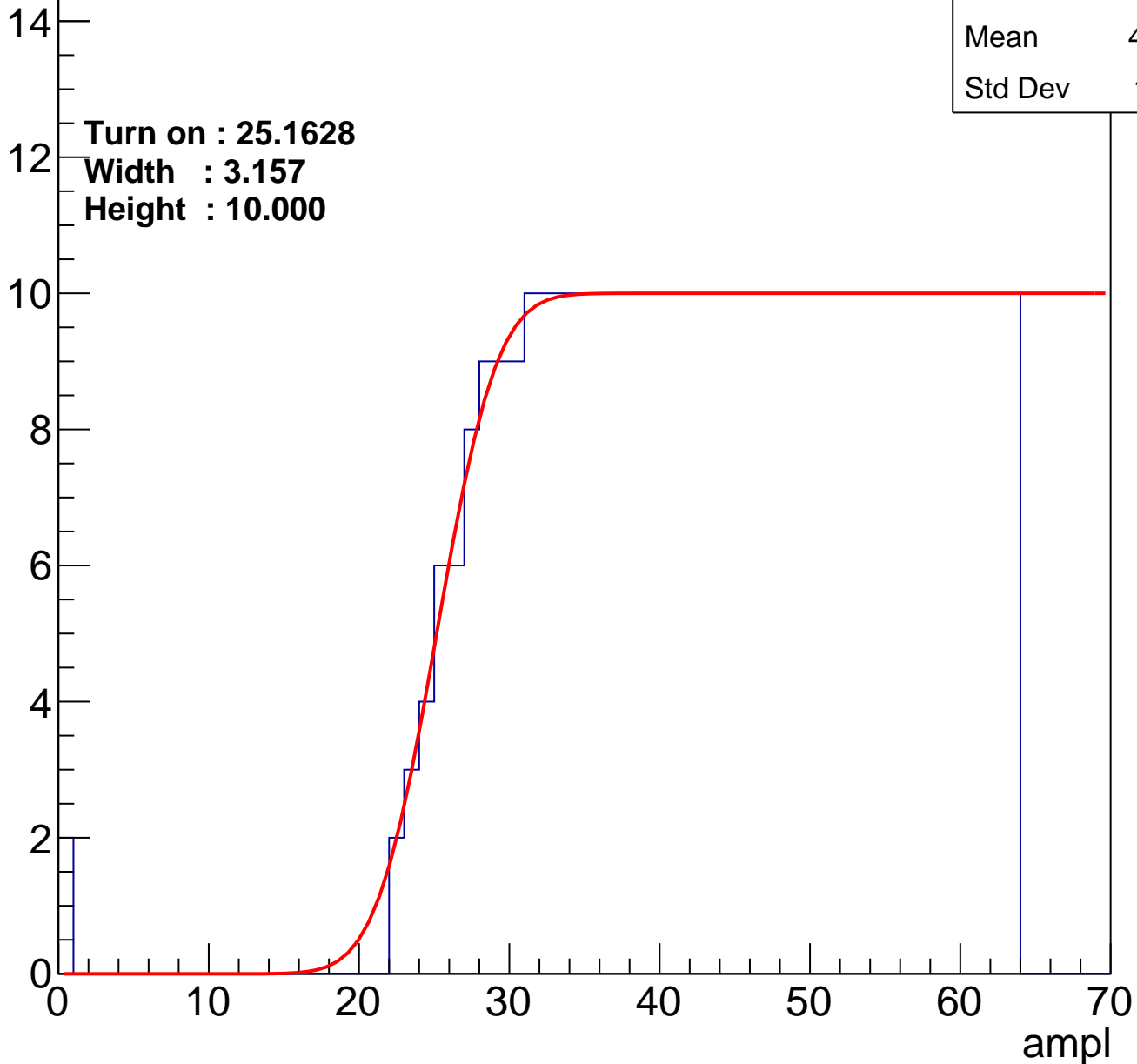
Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.1628

Width : 3.157

Height : 10.000

Entry



B1L100S, U25-ch16

calib_packv5_042523_0143.root, FC#4, port A2

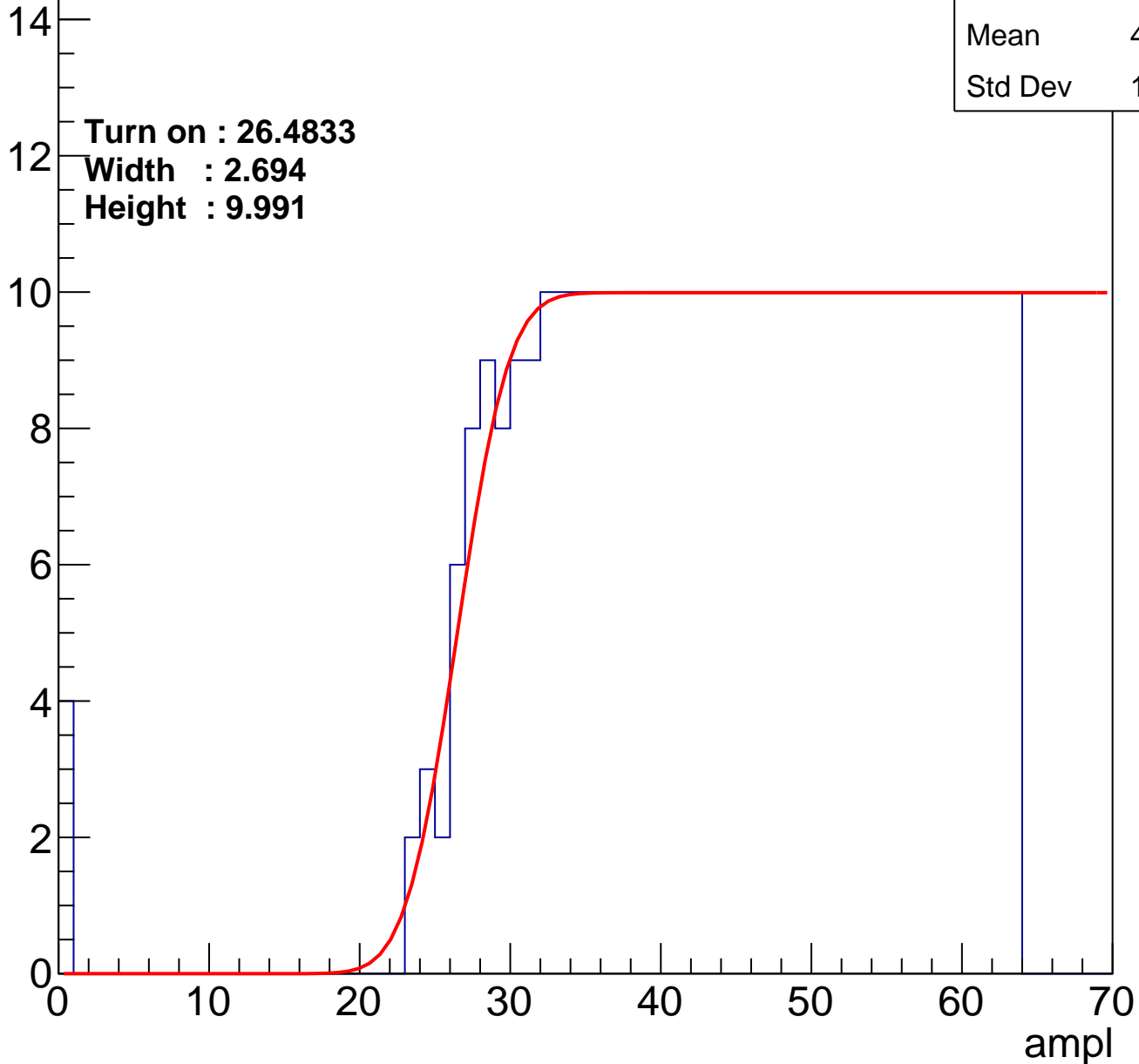
Entries	380
Mean	44.14
Std Dev	11.85

Turn on : 26.4833

Width : 2.694

Height : 9.991

Entry



B1L100S, U25-ch17

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.04
Std Dev	11.07

Turn on : 27.9214

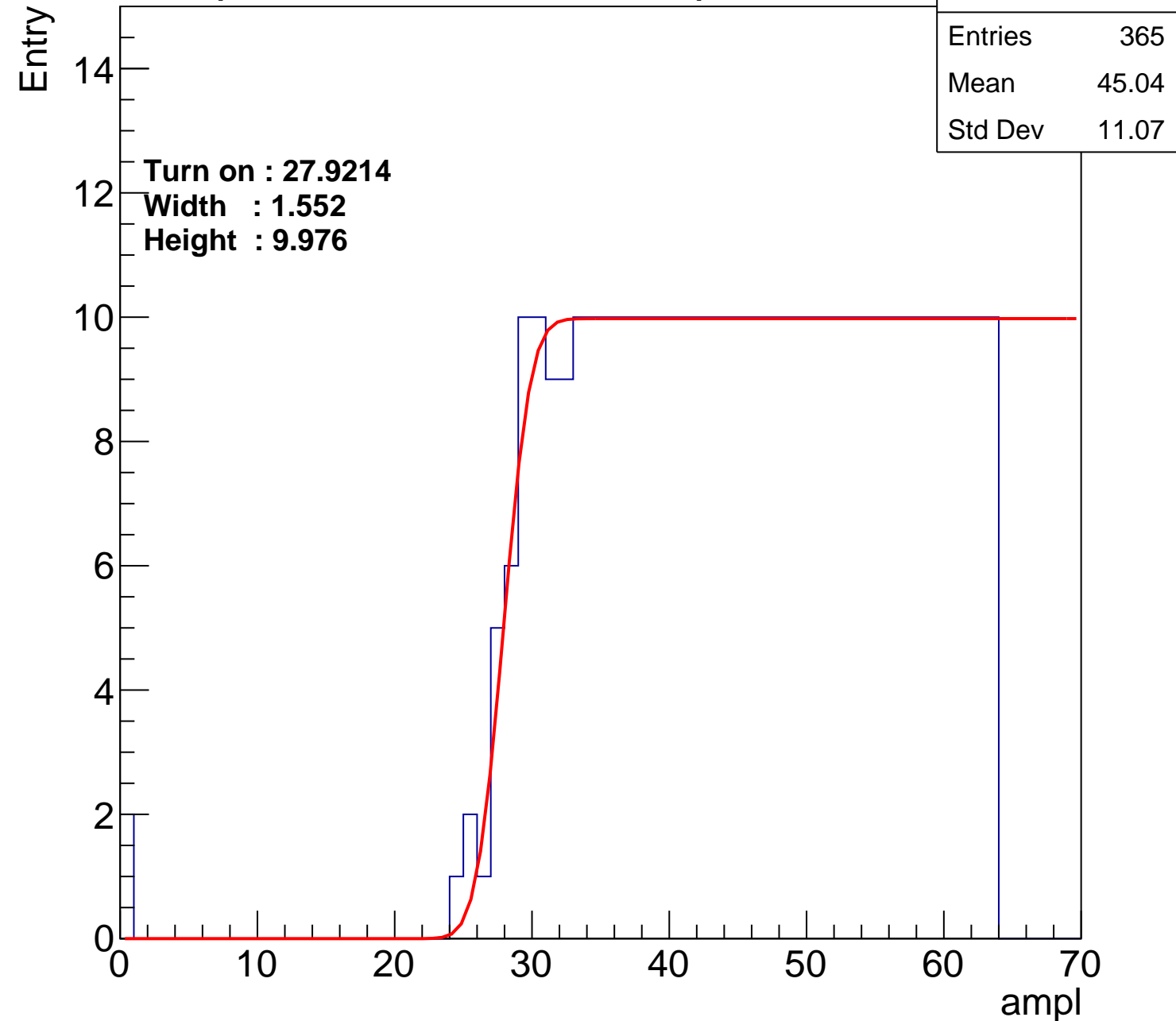
Width : 1.552

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch18

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.72
Std Dev	11.38

Turn on : 28.2825

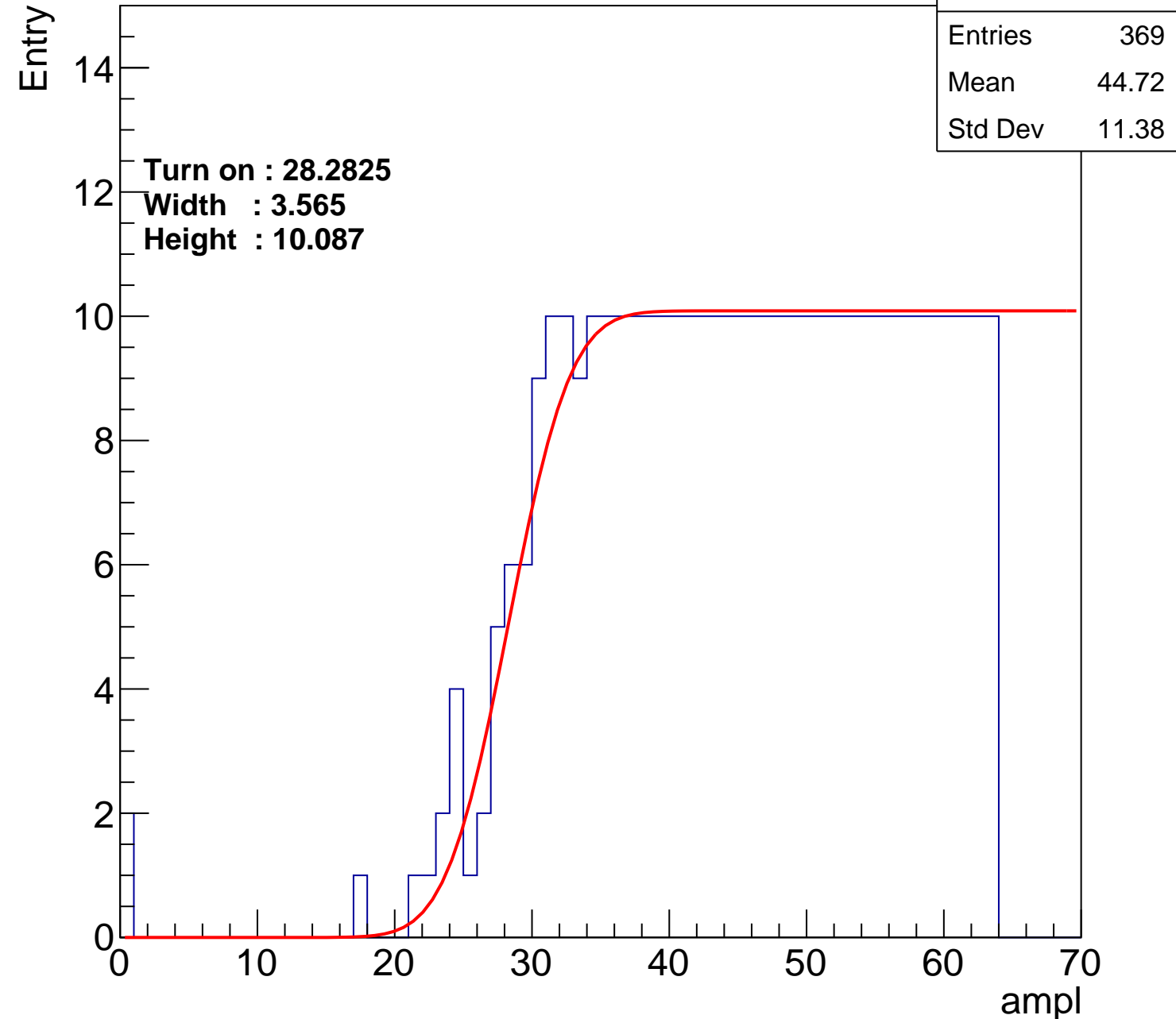
Width : 3.565

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch19

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.33
Std Dev	11.66

Turn on : 26.7915

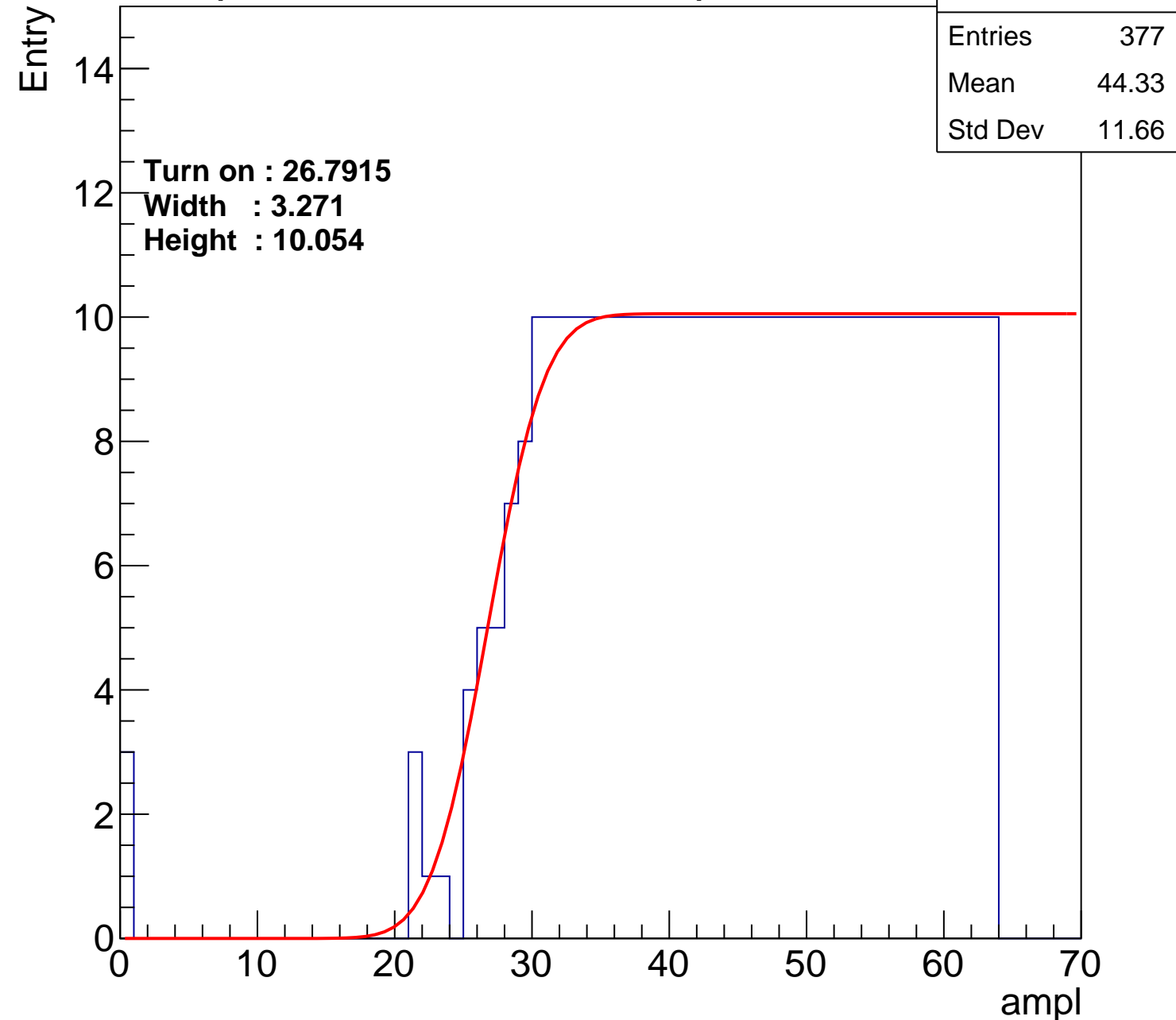
Width : 3.271

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch20

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.15
Std Dev	11.45

Turn on : 28.9980

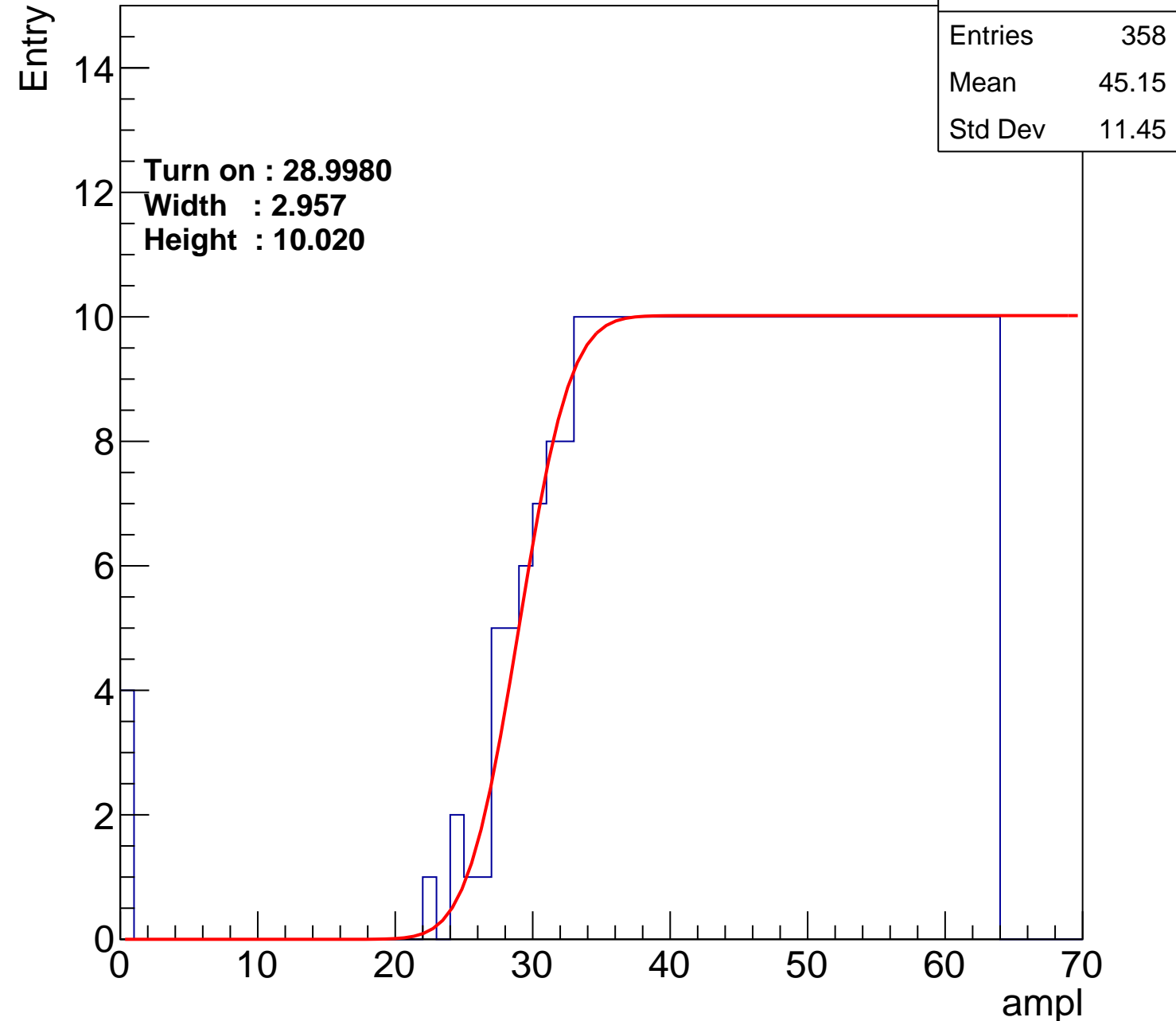
Width : 2.957

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch21

calib_packv5_042523_0143.root, FC#4, port A2

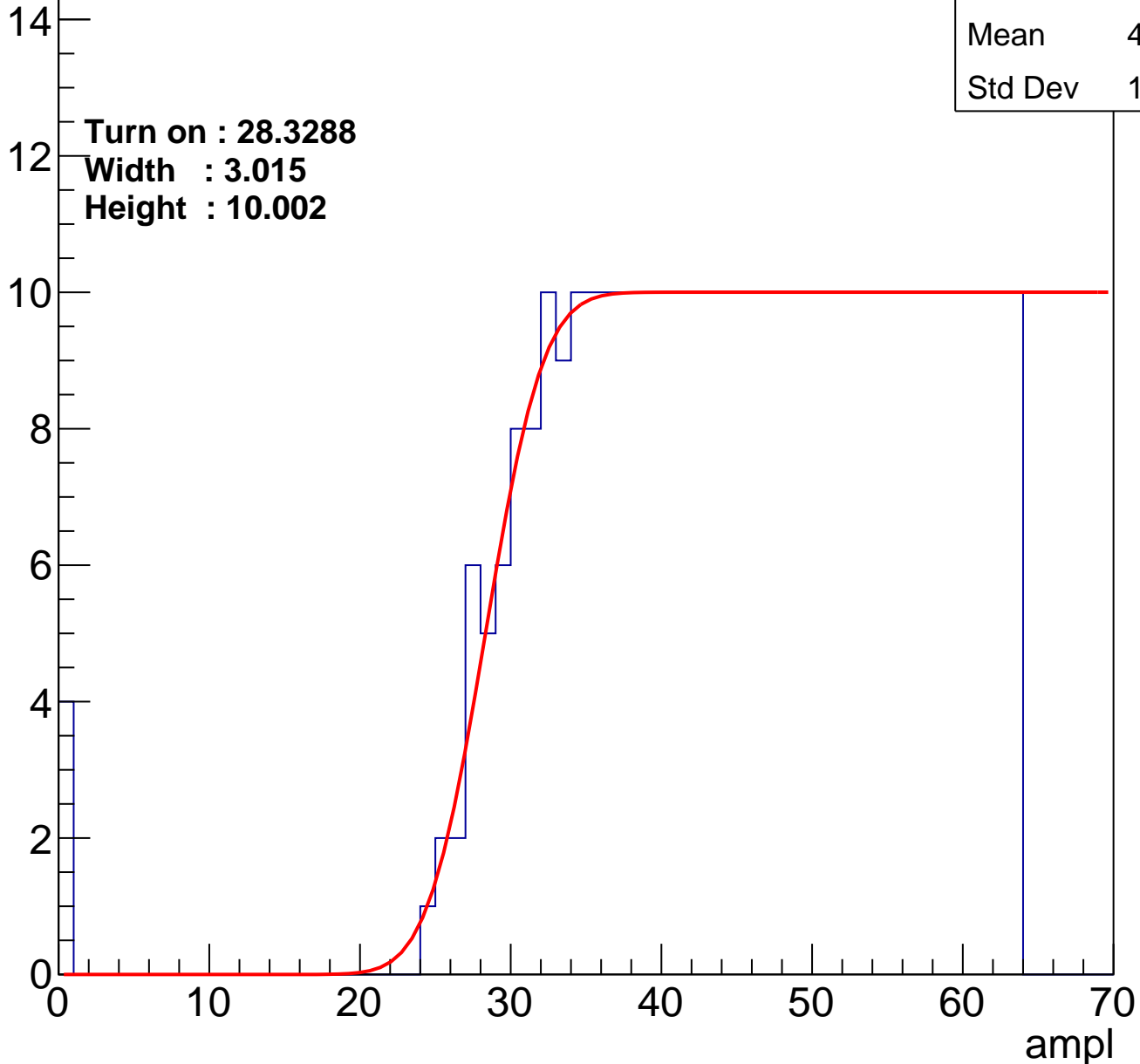
Entries	361
Mean	45.03
Std Dev	11.47

Turn on : 28.3288

Width : 3.015

Height : 10.002

Entry



B1L100S, U25-ch22

calib_packv5_042523_0143.root, FC#4, port A2

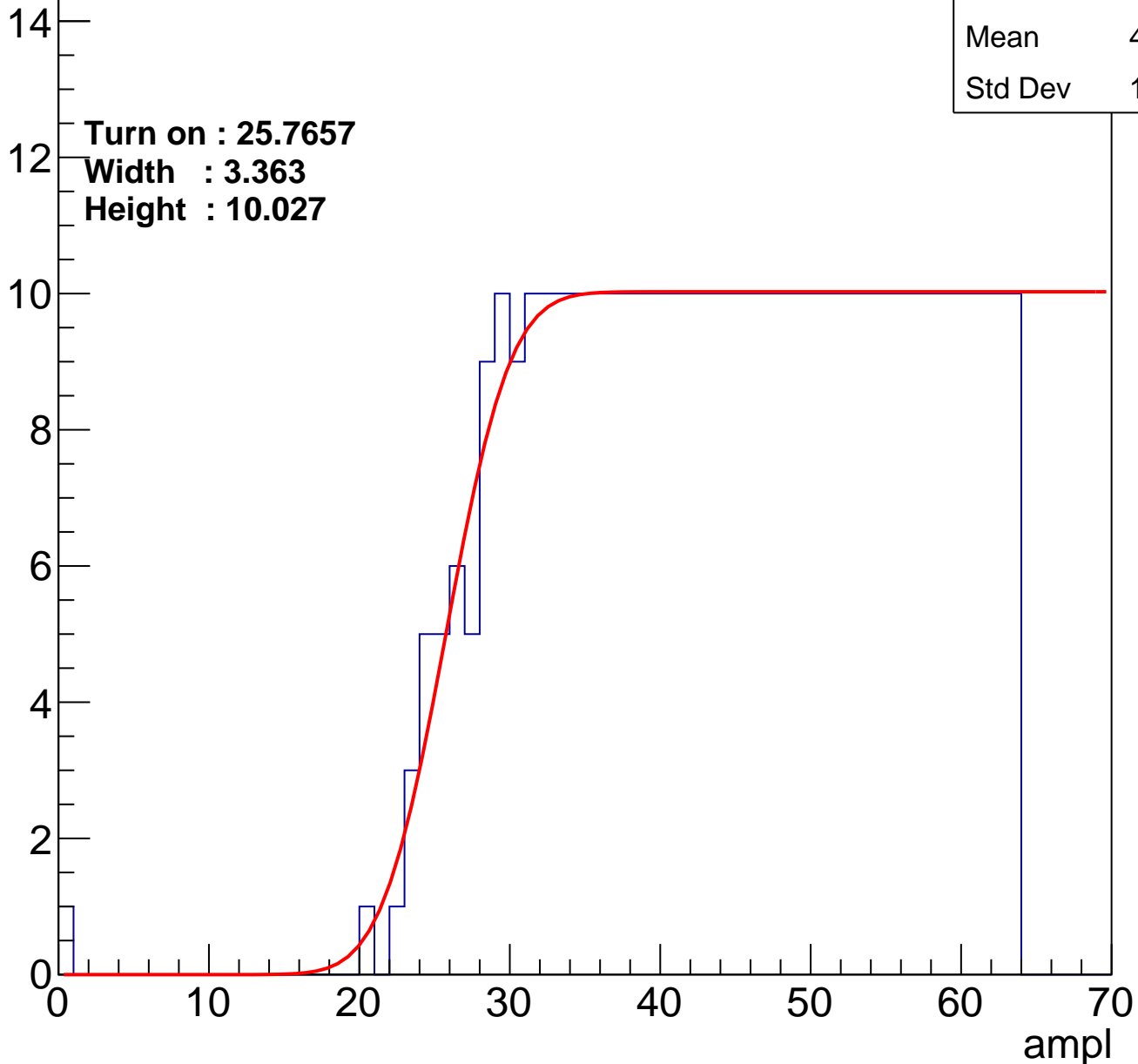
Entries	385
Mean	44.08
Std Dev	11.48

Turn on : 25.7657

Width : 3.363

Height : 10.027

Entry



B1L100S, U25-ch23

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.96
Std Dev	10.94

Turn on : 27.1598

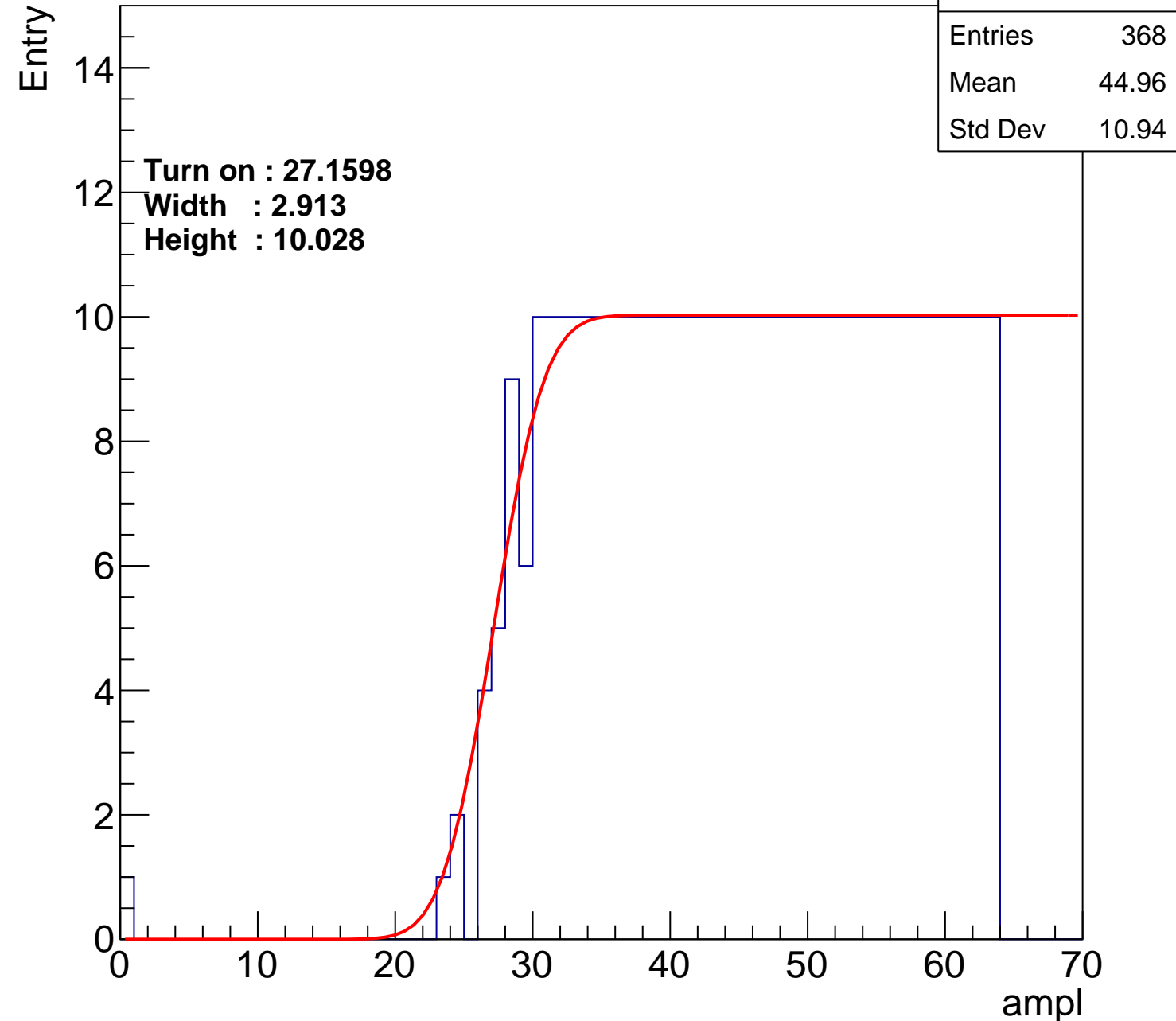
Width : 2.913

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch24

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.67
Std Dev	11.61

Turn on : 27.6259

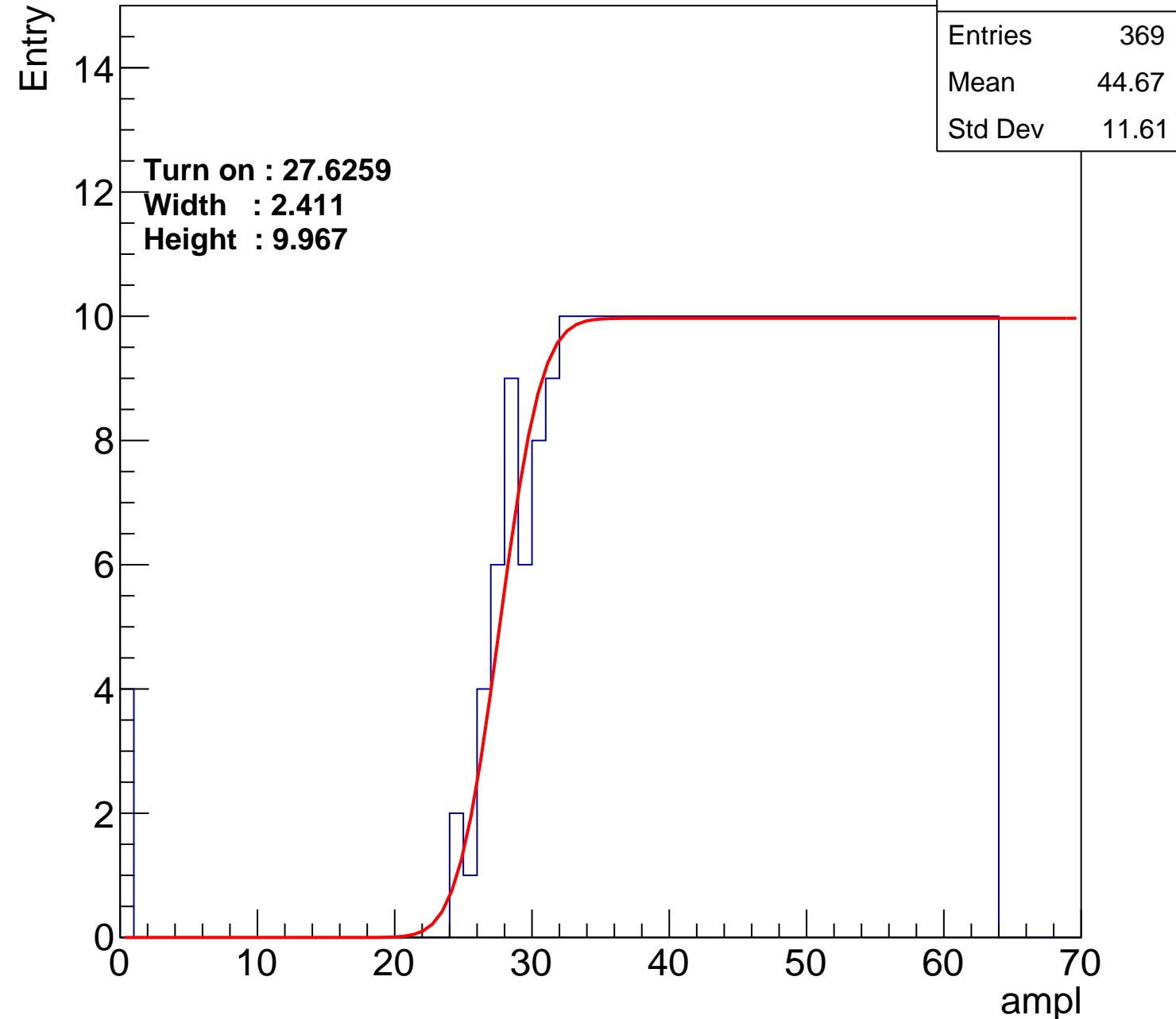
Width : 2.411

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch25

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.56
Std Dev	11.23

Turn on : 26.7380

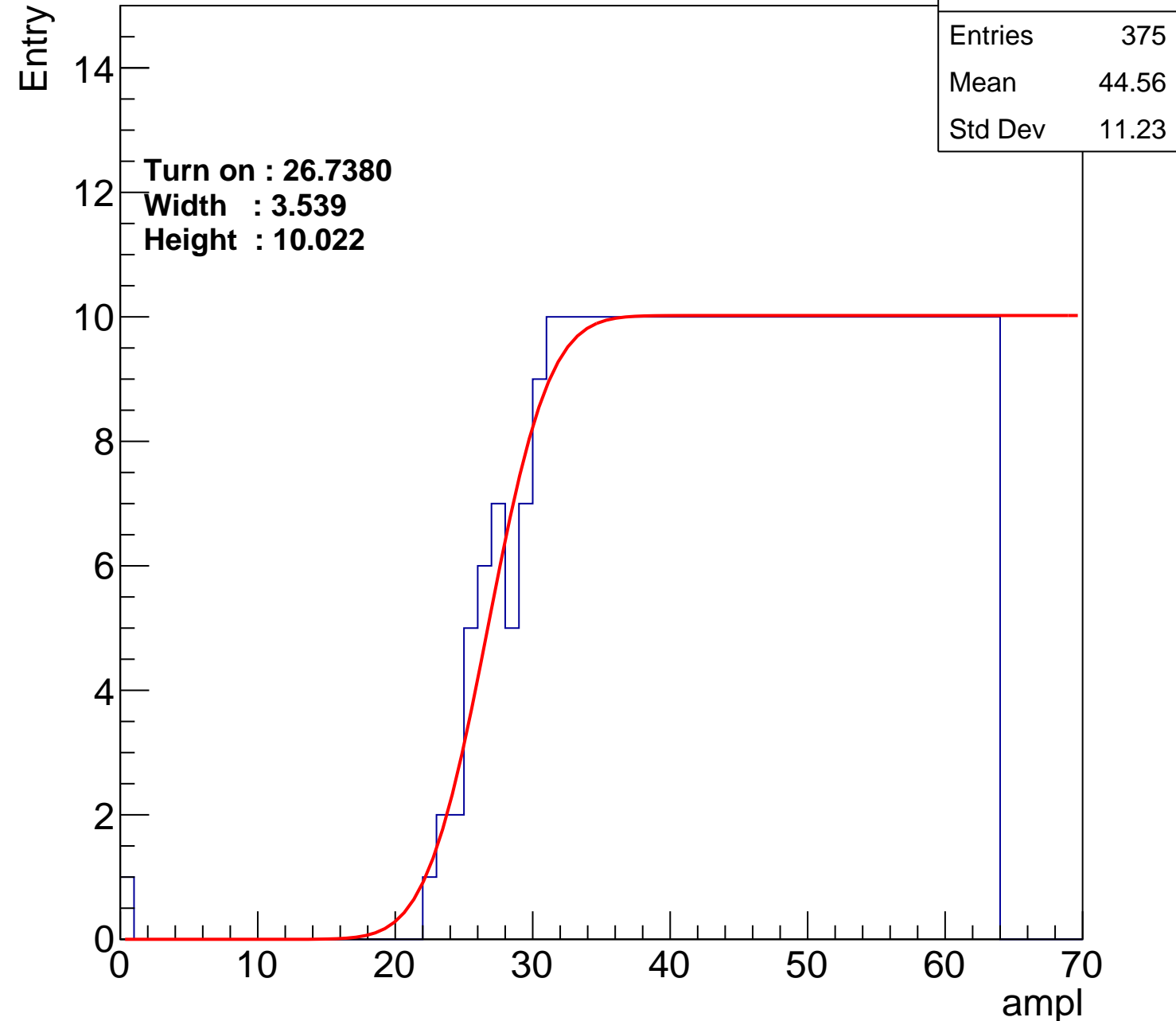
Width : 3.539

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch26

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.2897

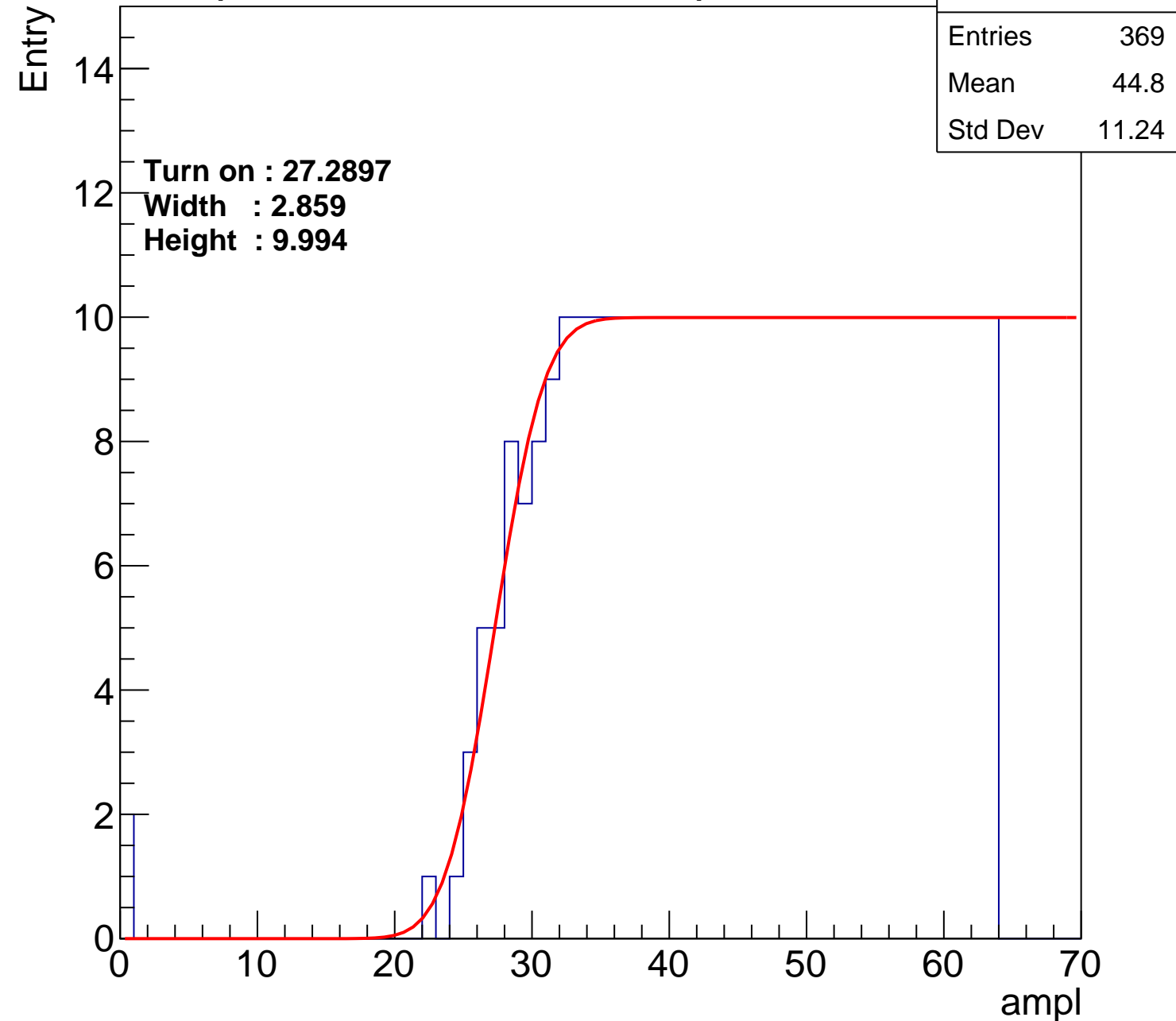
Width : 2.859

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch27

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.99
Std Dev	11.74

Turn on : 26.1312

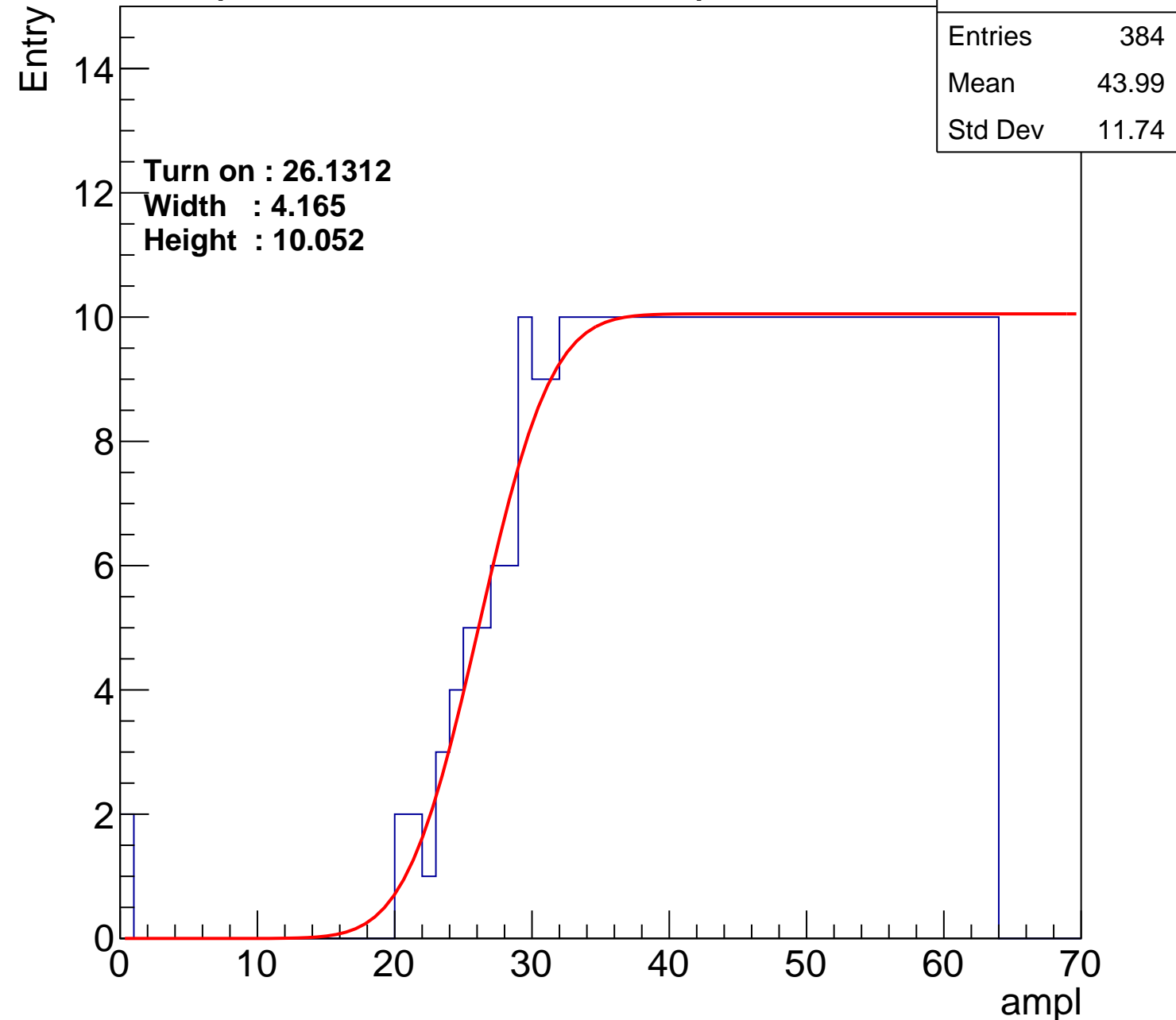
Width : 4.165

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch28

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.6993

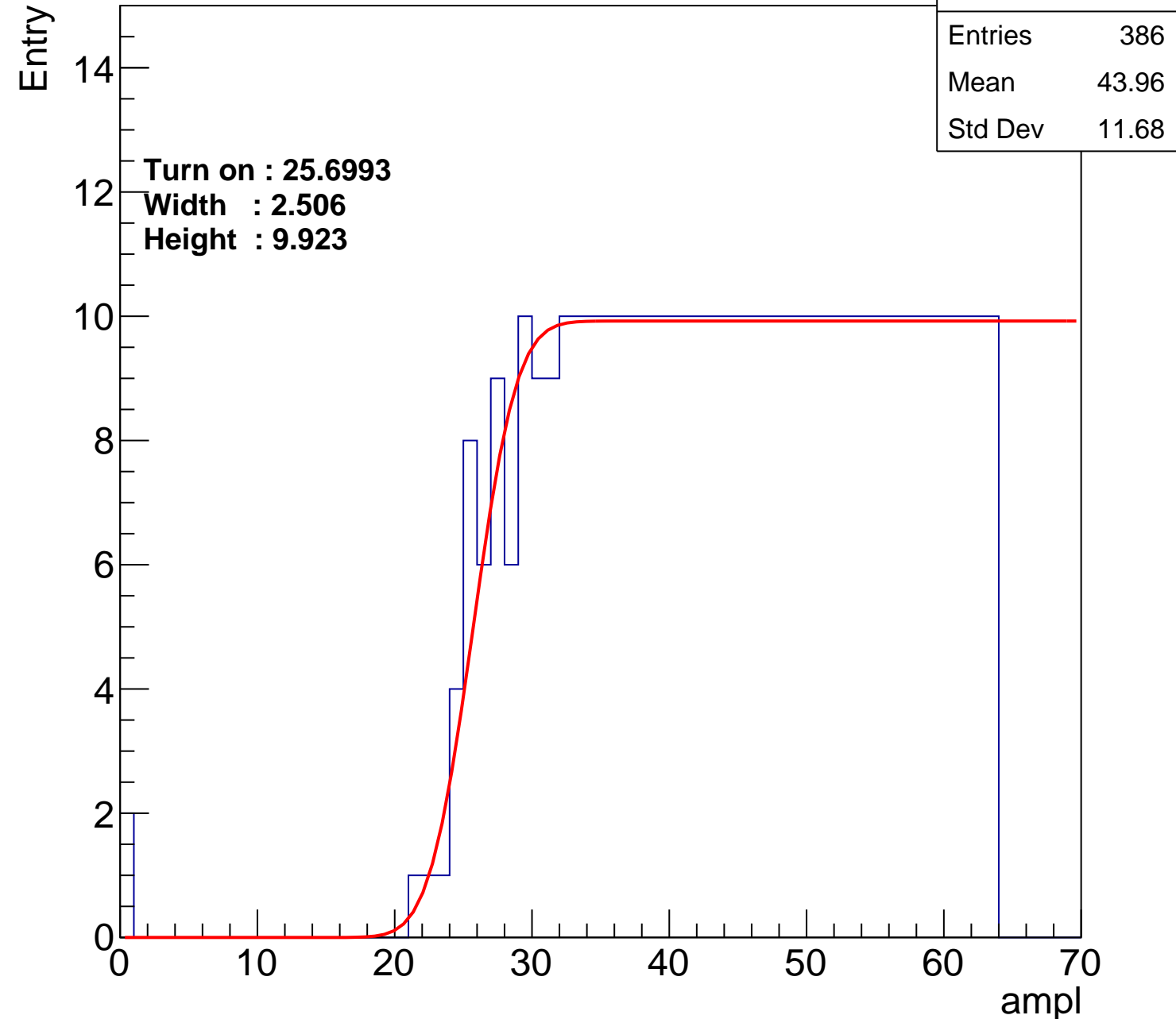
Width : 2.506

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch29

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.34
Std Dev	11.75

Turn on : 26.8170

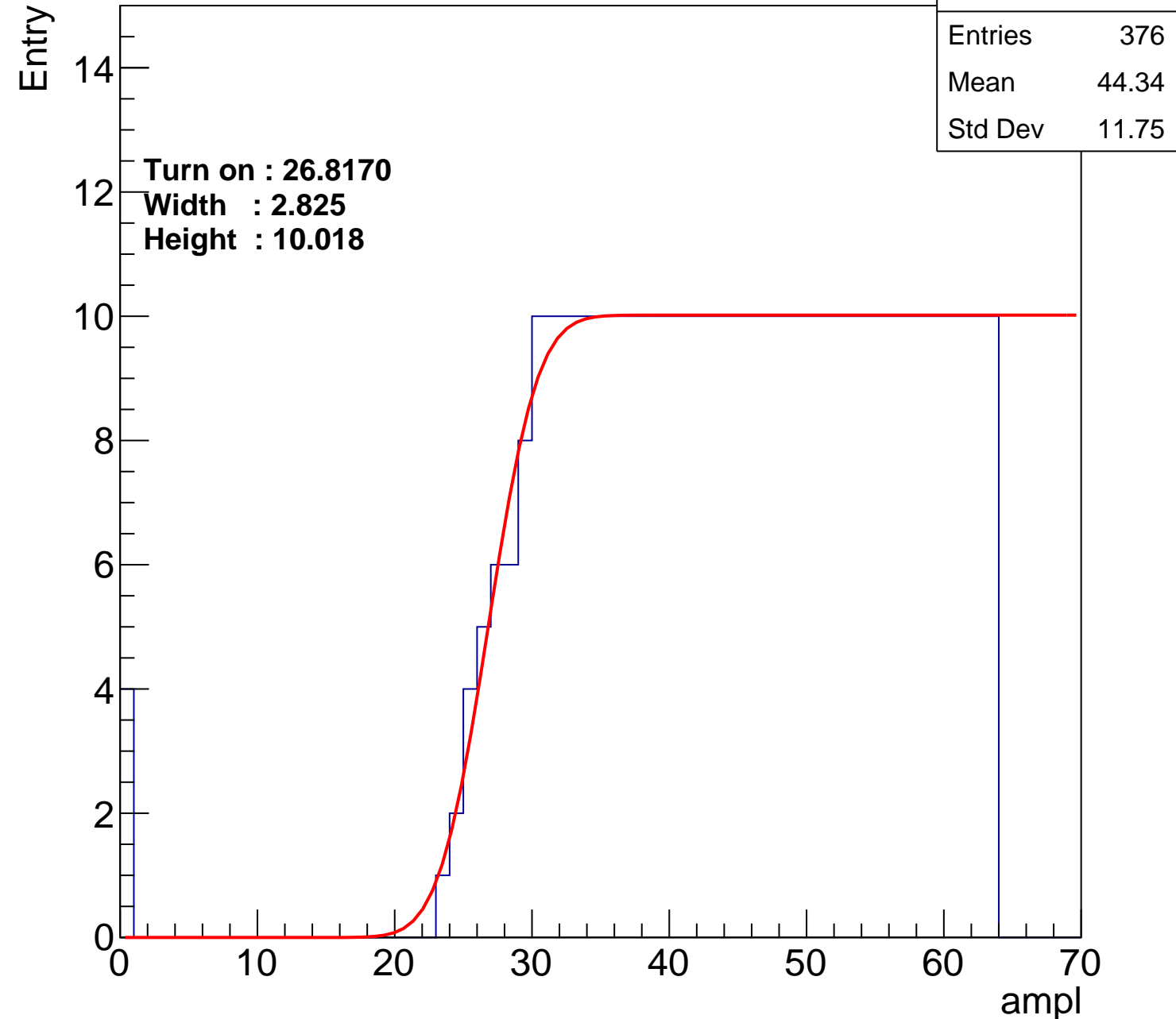
Width : 2.825

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch30

calib_packv5_042523_0143.root, FC#4, port A2

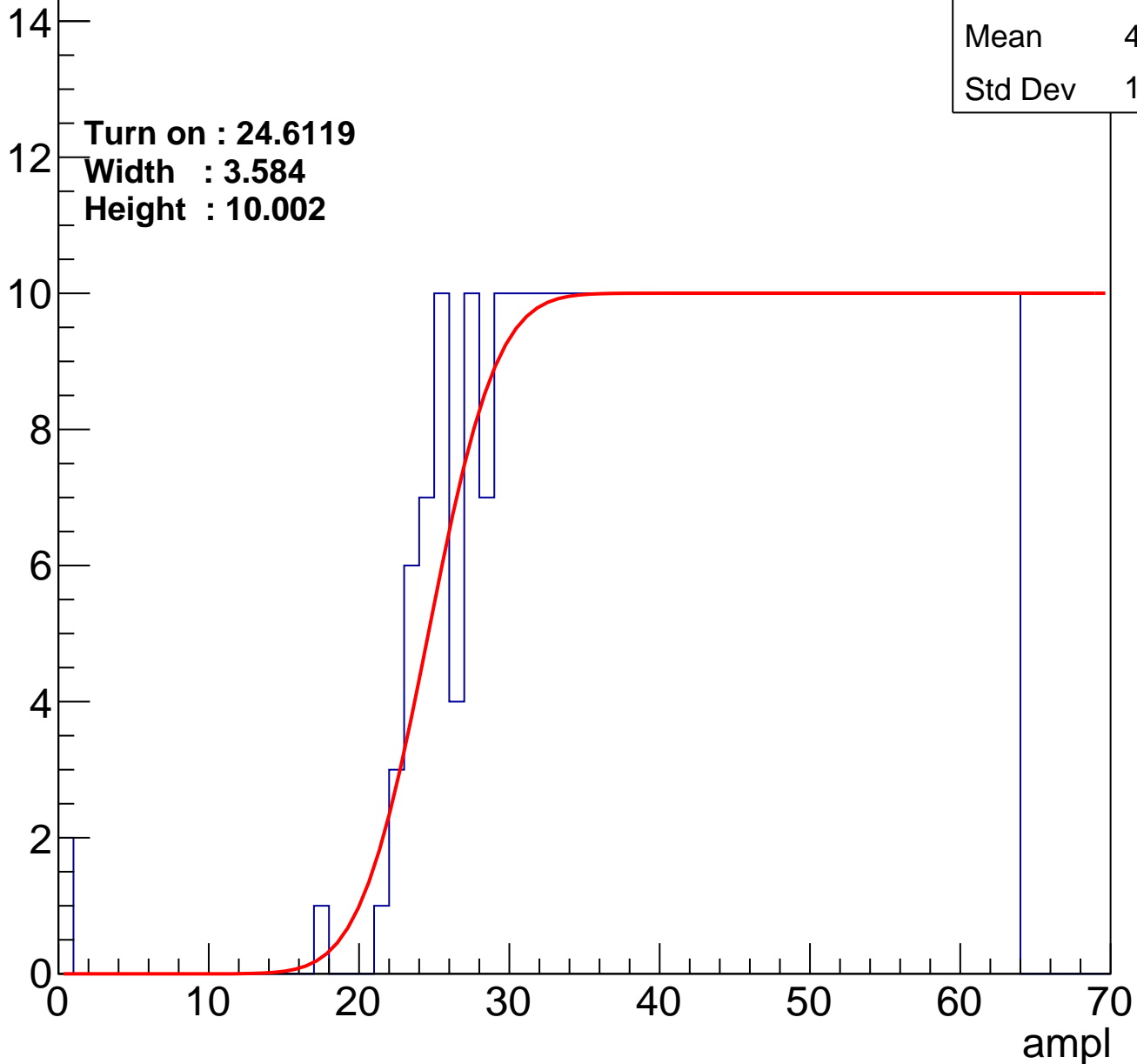
Entries	401
Mean	43.22
Std Dev	12.08

Turn on : 24.6119

Width : 3.584

Height : 10.002

Entry



B1L100S, U25-ch31

calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	45.21
Std Dev	10.92

Turn on : 28.3458

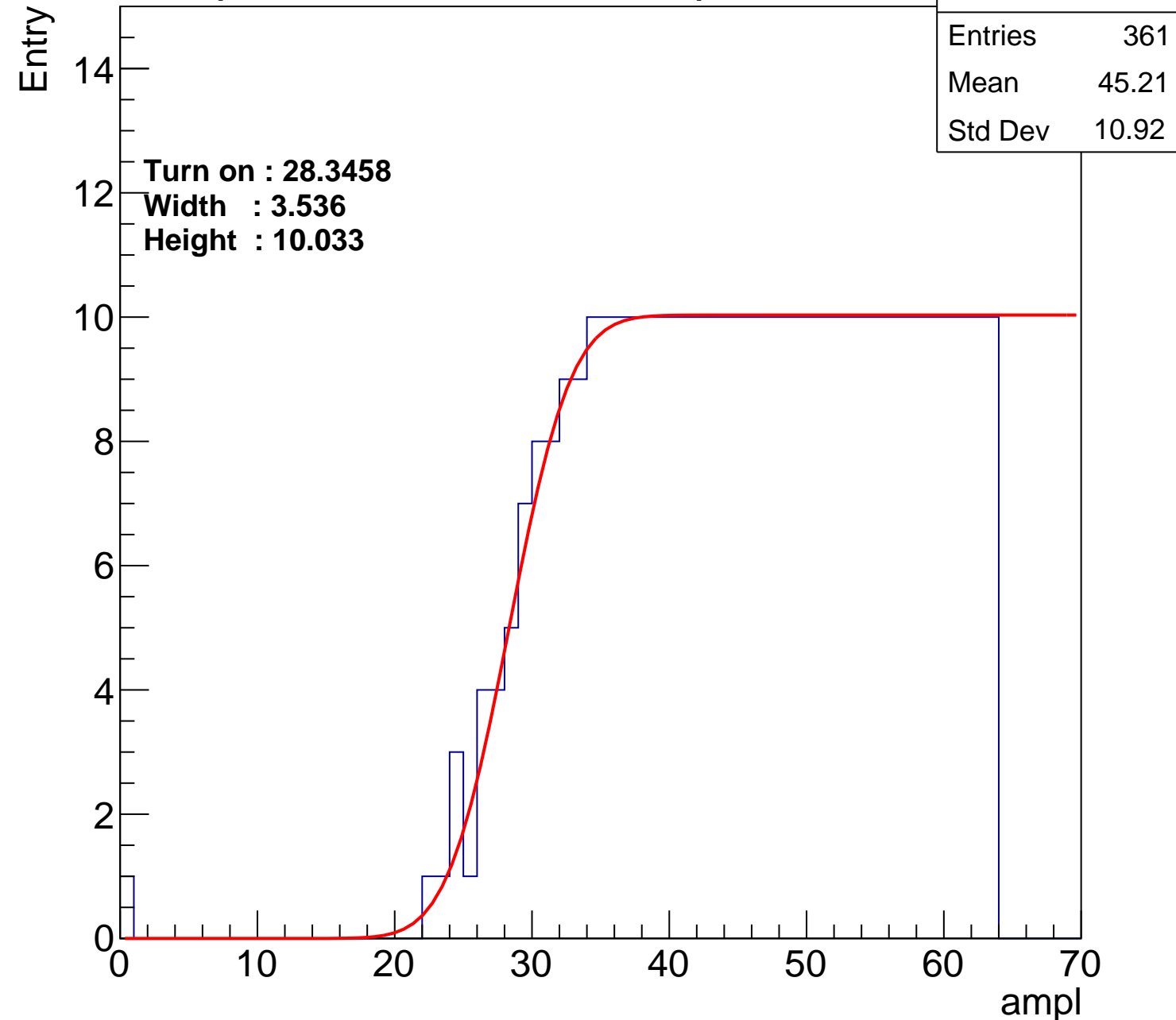
Width : 3.536

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch32

calib_packv5_042523_0143.root, FC#4, port A2

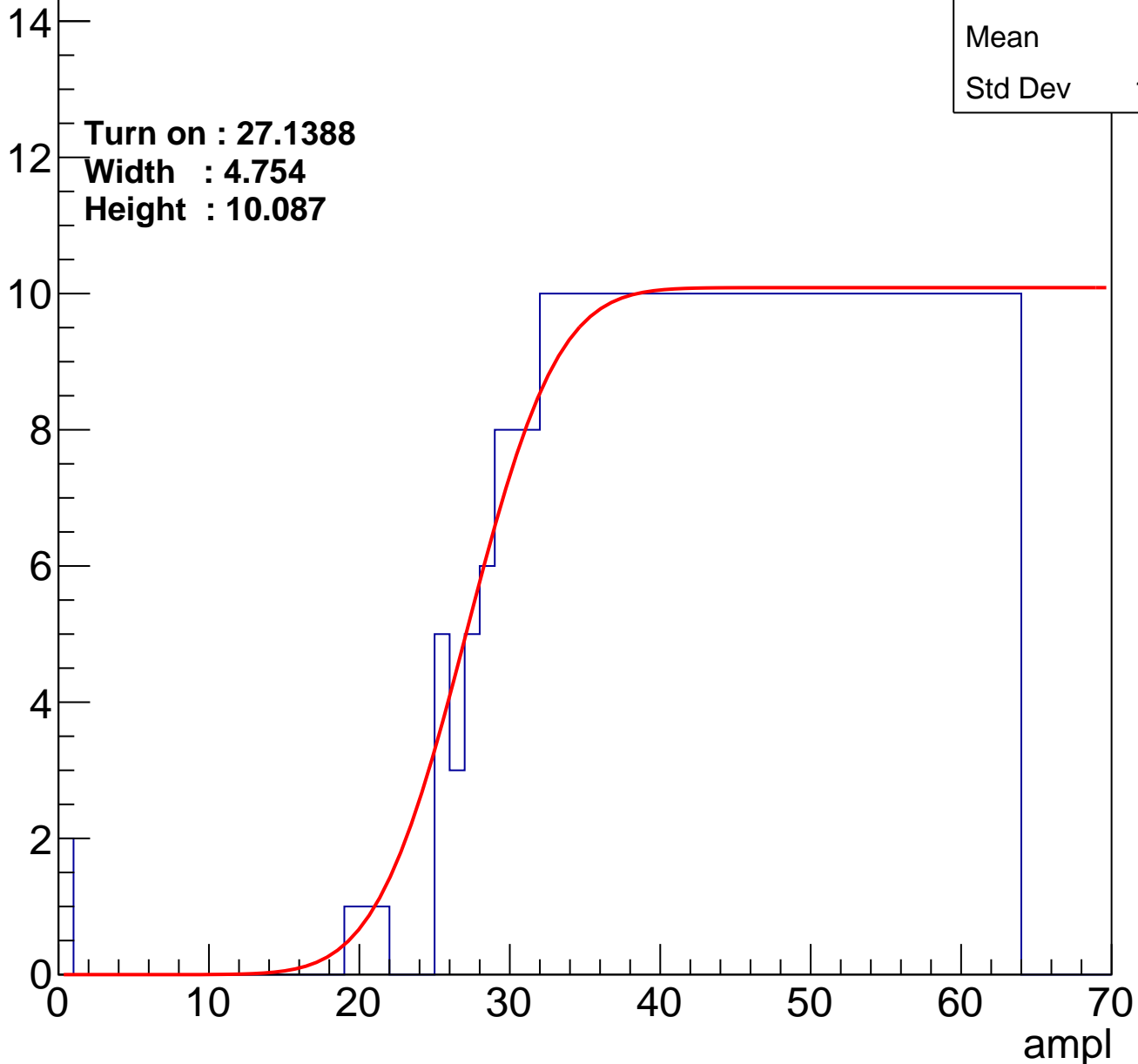
Entries	368
Mean	44.8
Std Dev	11.31

Turn on : 27.1388

Width : 4.754

Height : 10.087

Entry



B1L100S, U25-ch33

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 26.9536

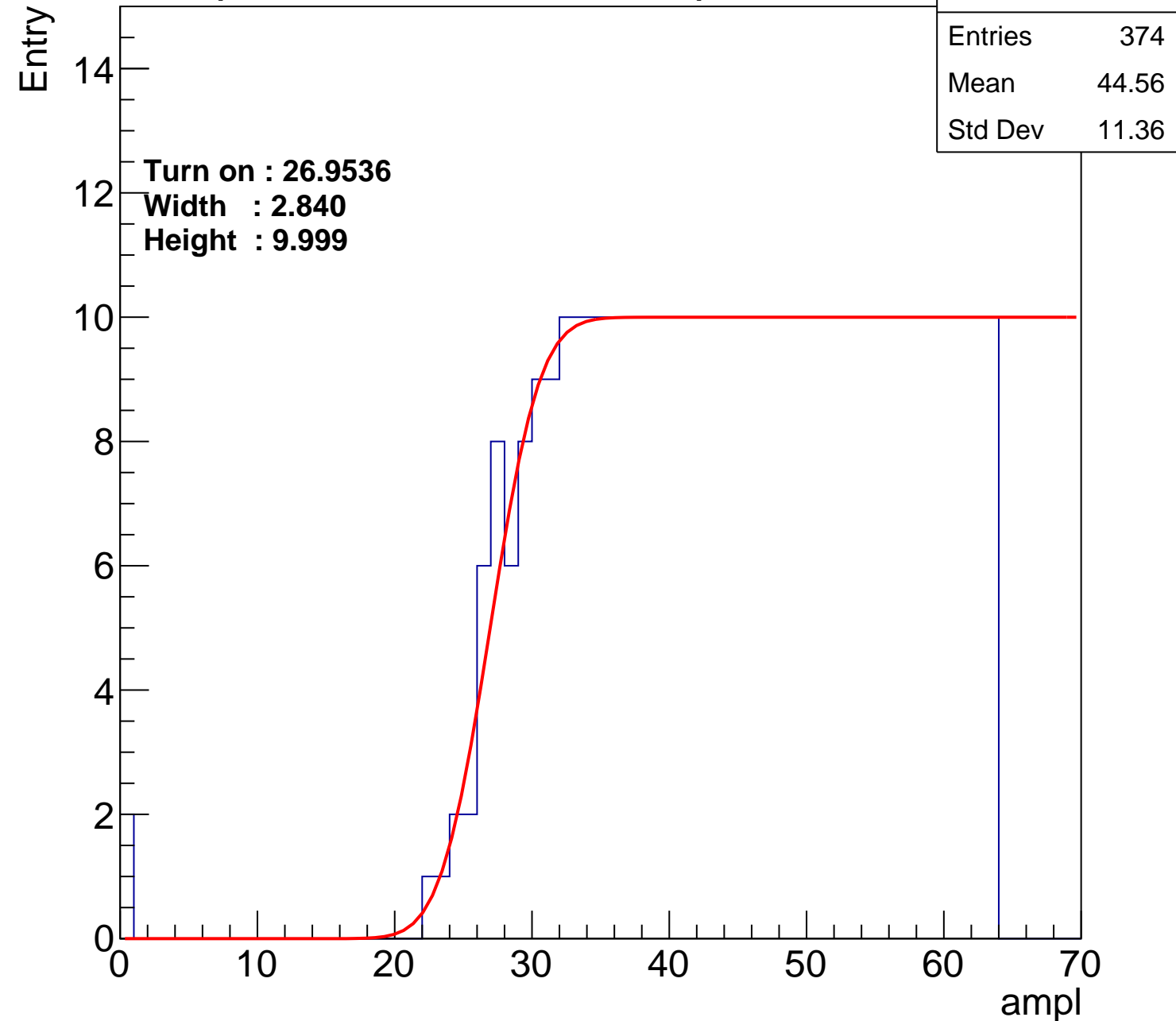
Width : 2.840

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch34

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.98
Std Dev	11.9

Turn on : 25.9779

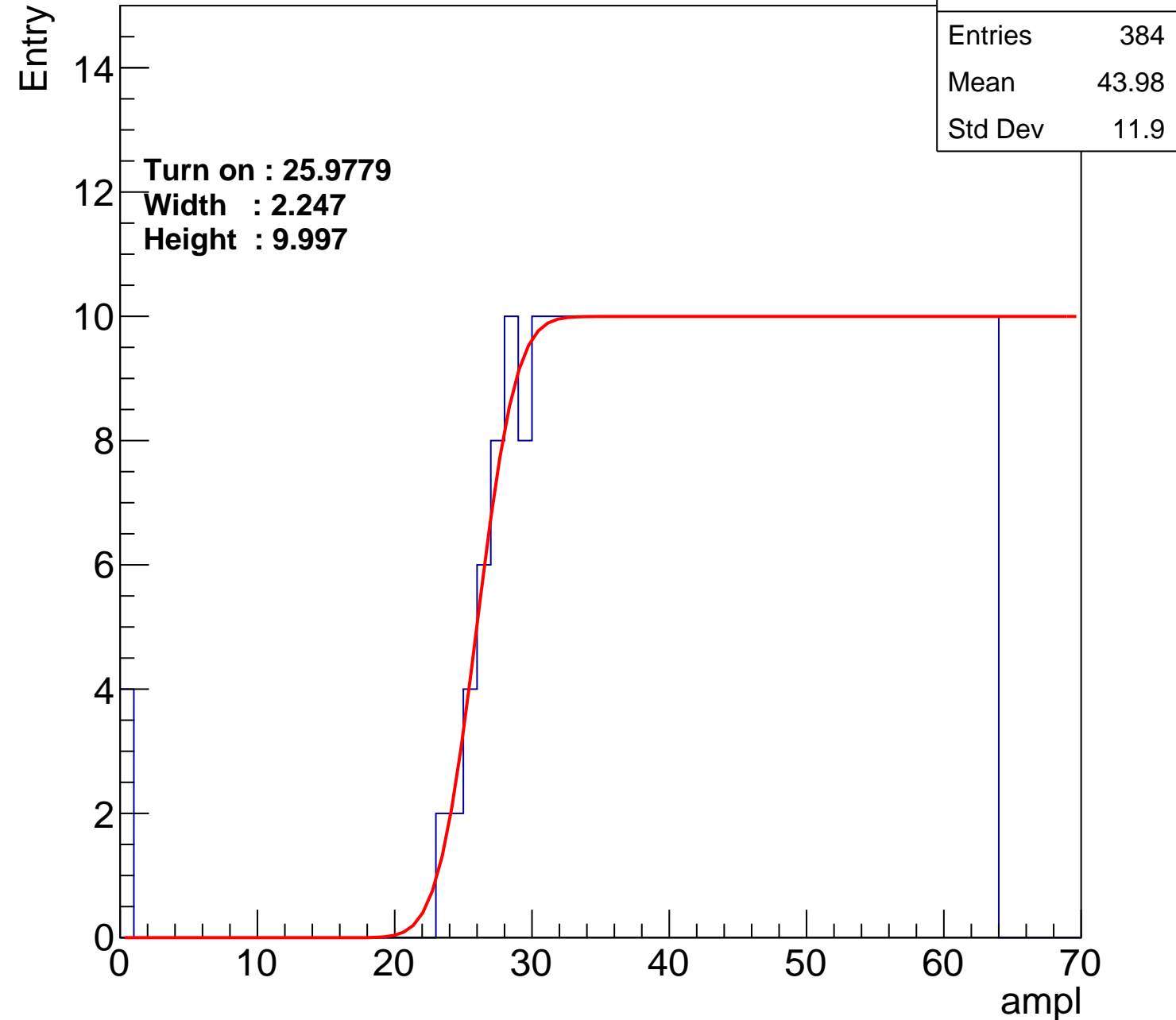
Width : 2.247

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch35

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.07
Std Dev	11.96

Turn on : 26.9427

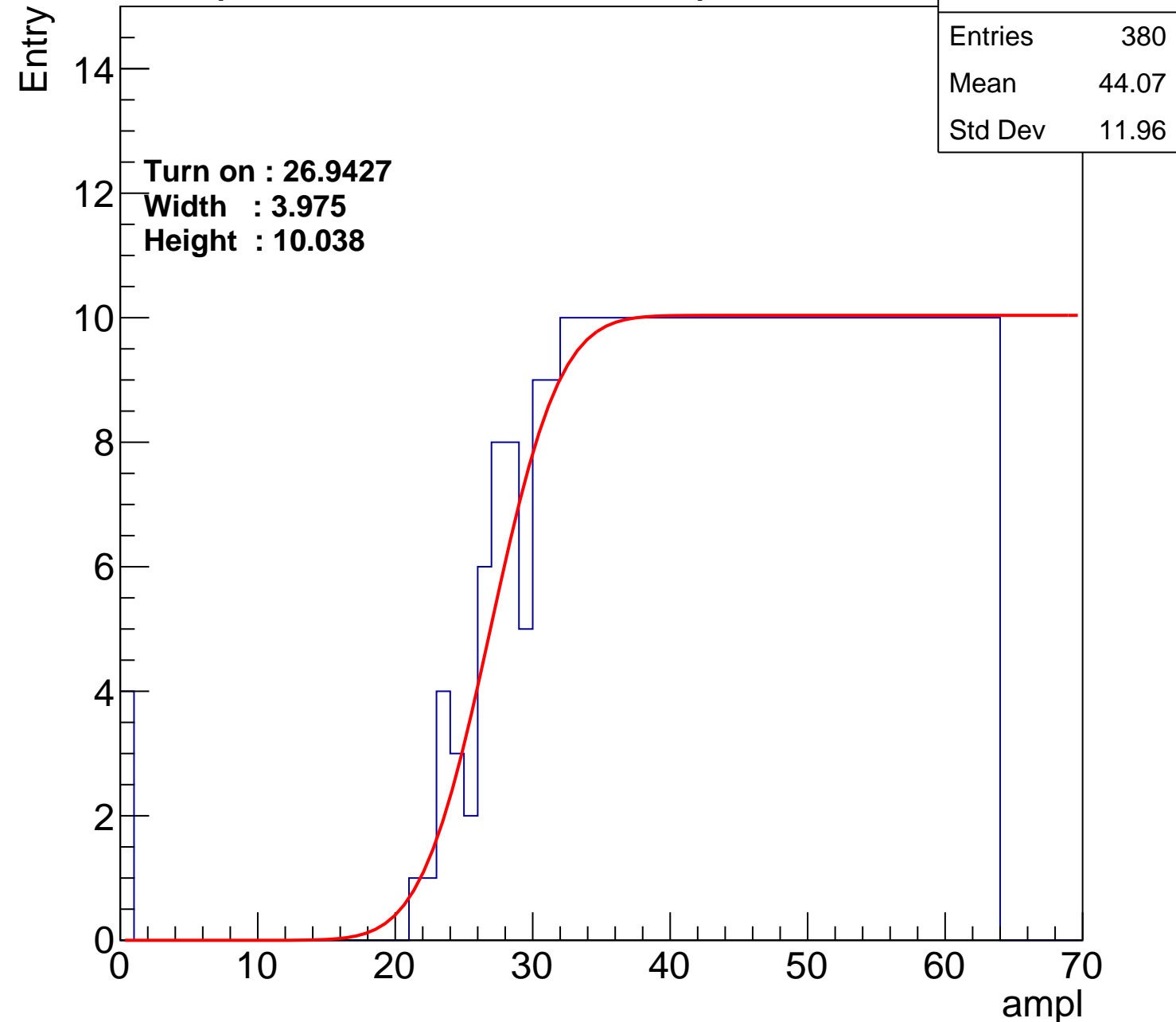
Width : 3.975

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch36

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.43
Std Dev	11.9

Turn on : 27.5788

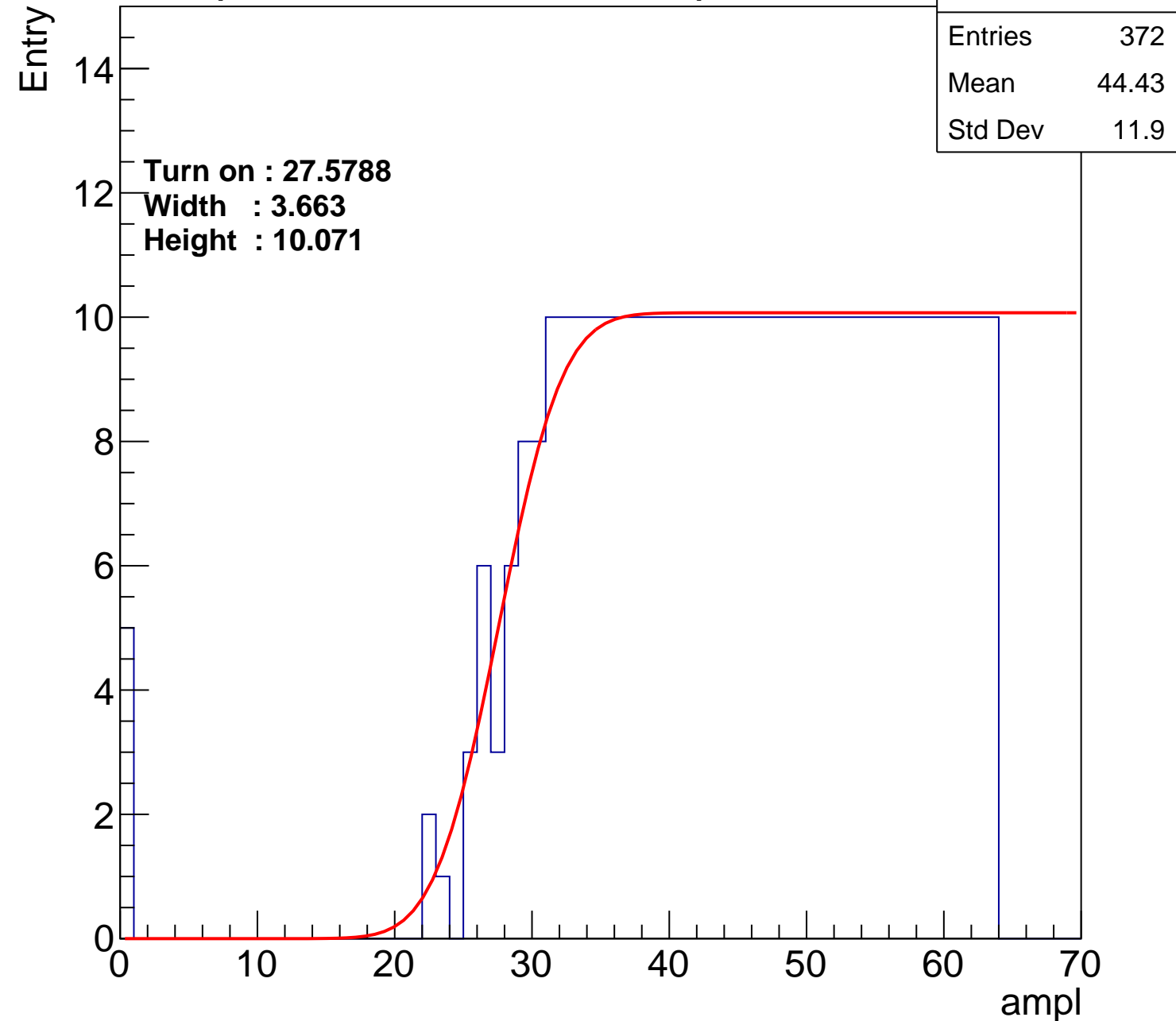
Width : 3.663

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch37

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.18
Std Dev	11.85

Turn on : 26.4788

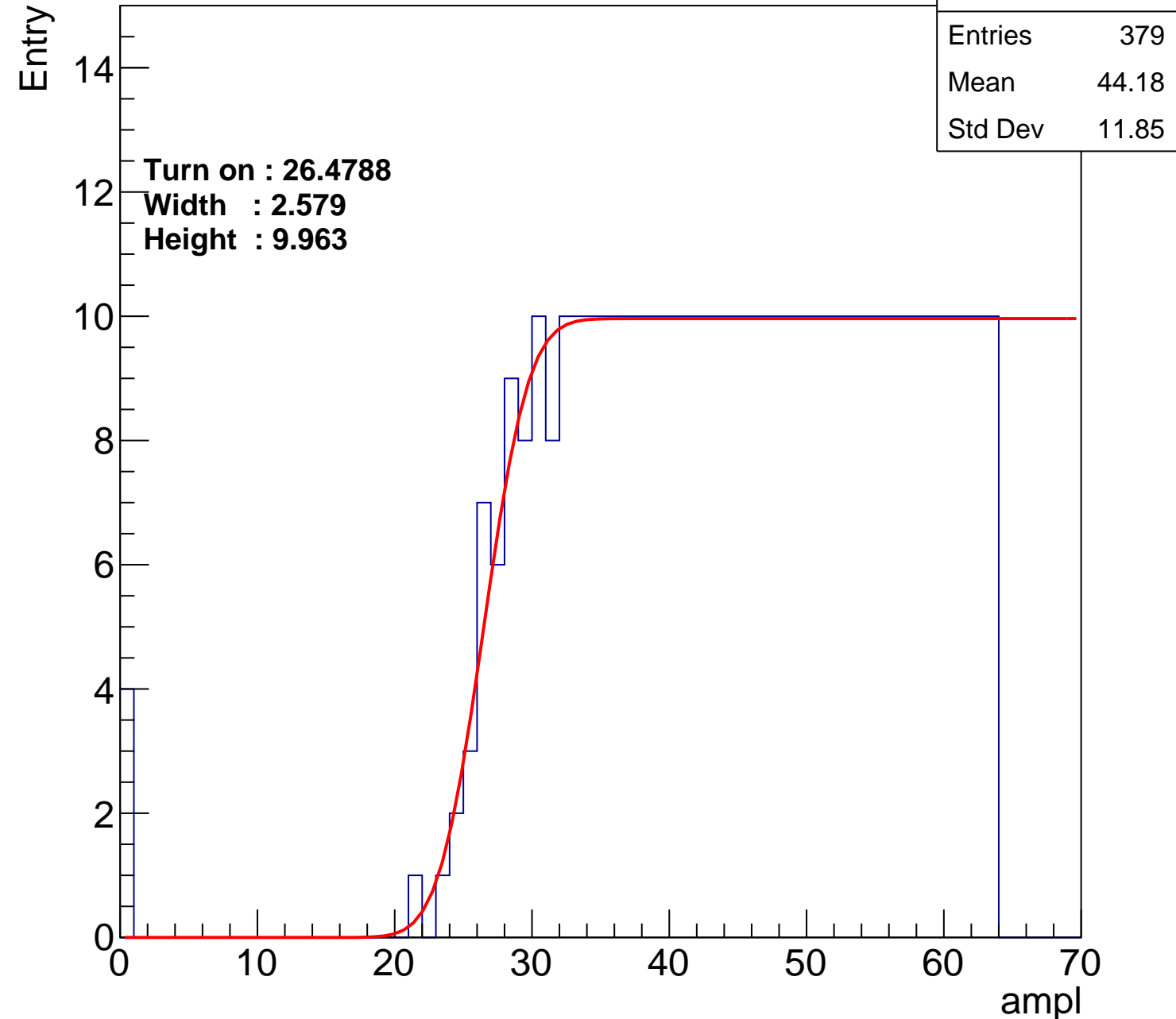
Width : 2.579

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch38

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.8
Std Dev	12.07

Turn on : 26.3102

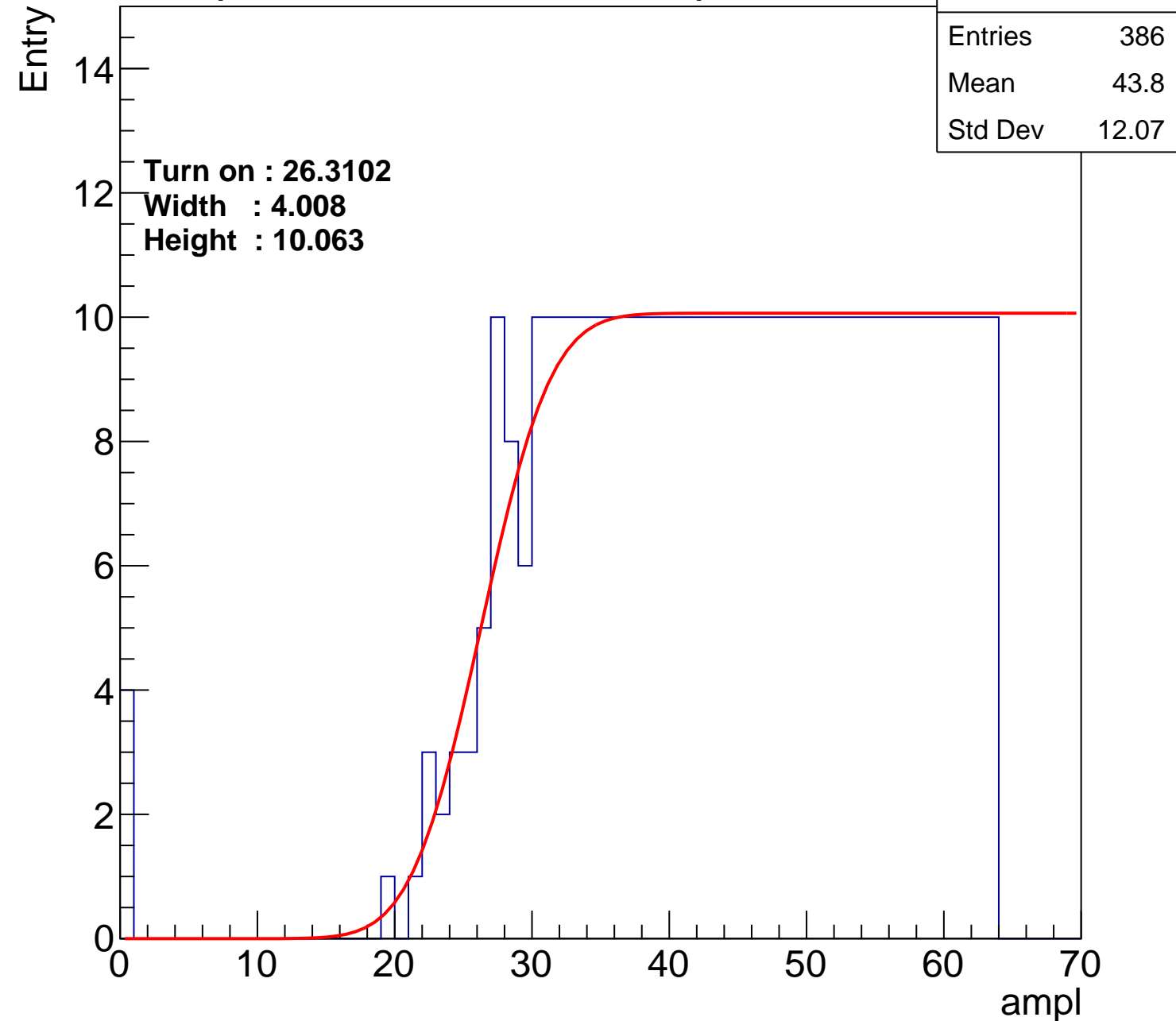
Width : 4.008

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch39

calib_packv5_042523_0143.root, FC#4, port A2

Entries	356
Mean	45.42
Std Dev	10.96

Turn on : 28.9626

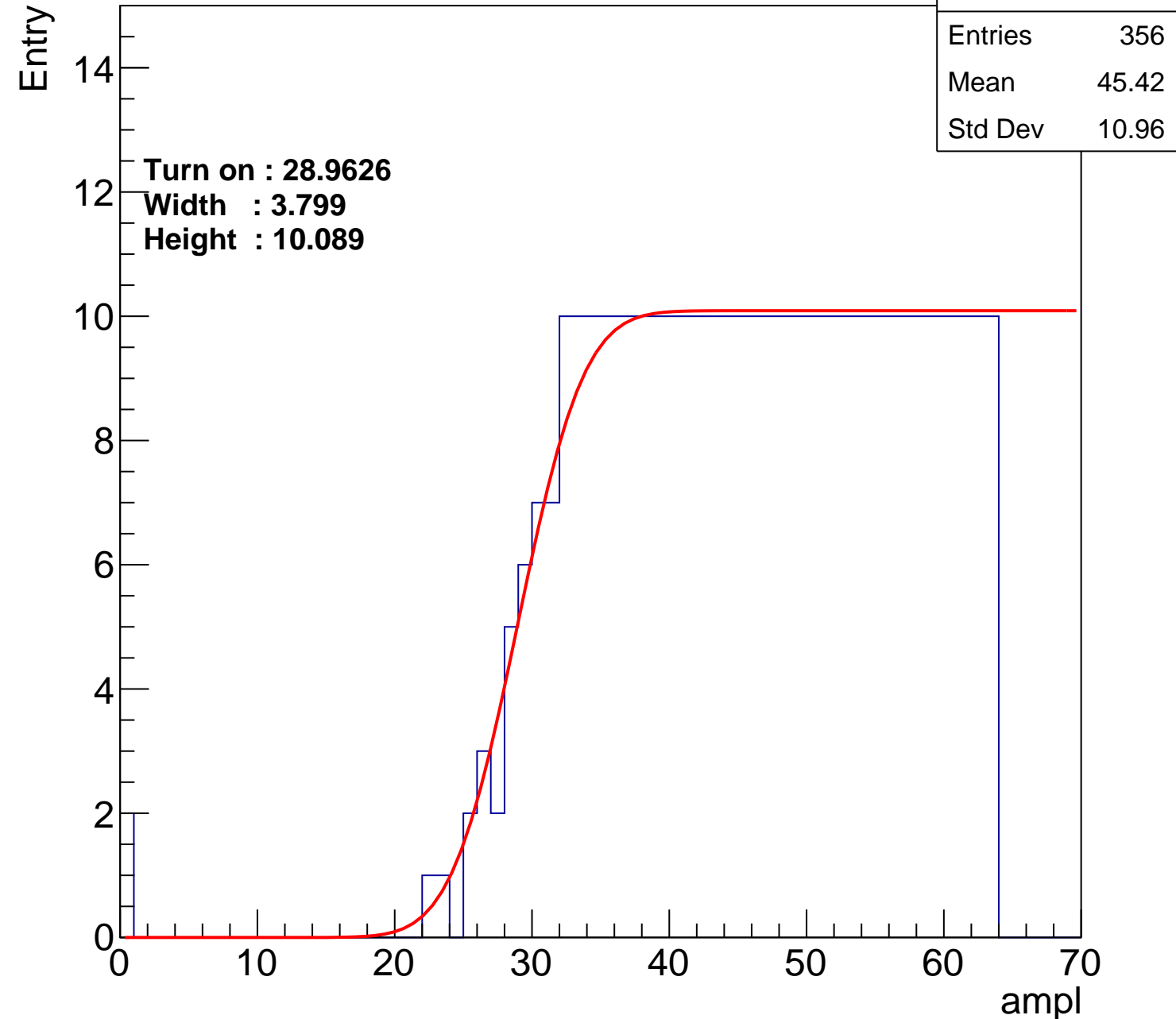
Width : 3.799

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch40

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.77
Std Dev	11.9

Turn on : 25.7267

Width : 3.542

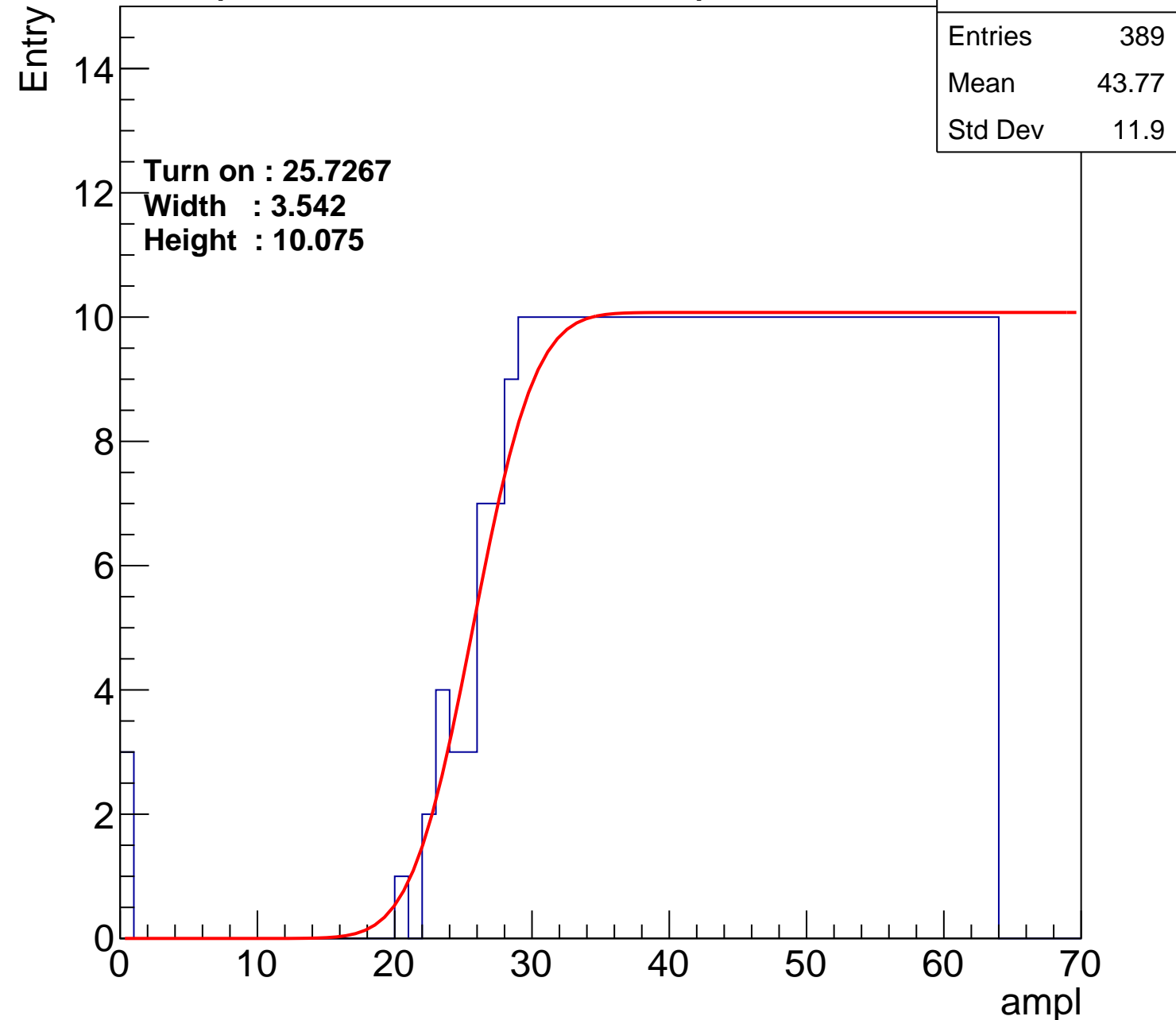
Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch41

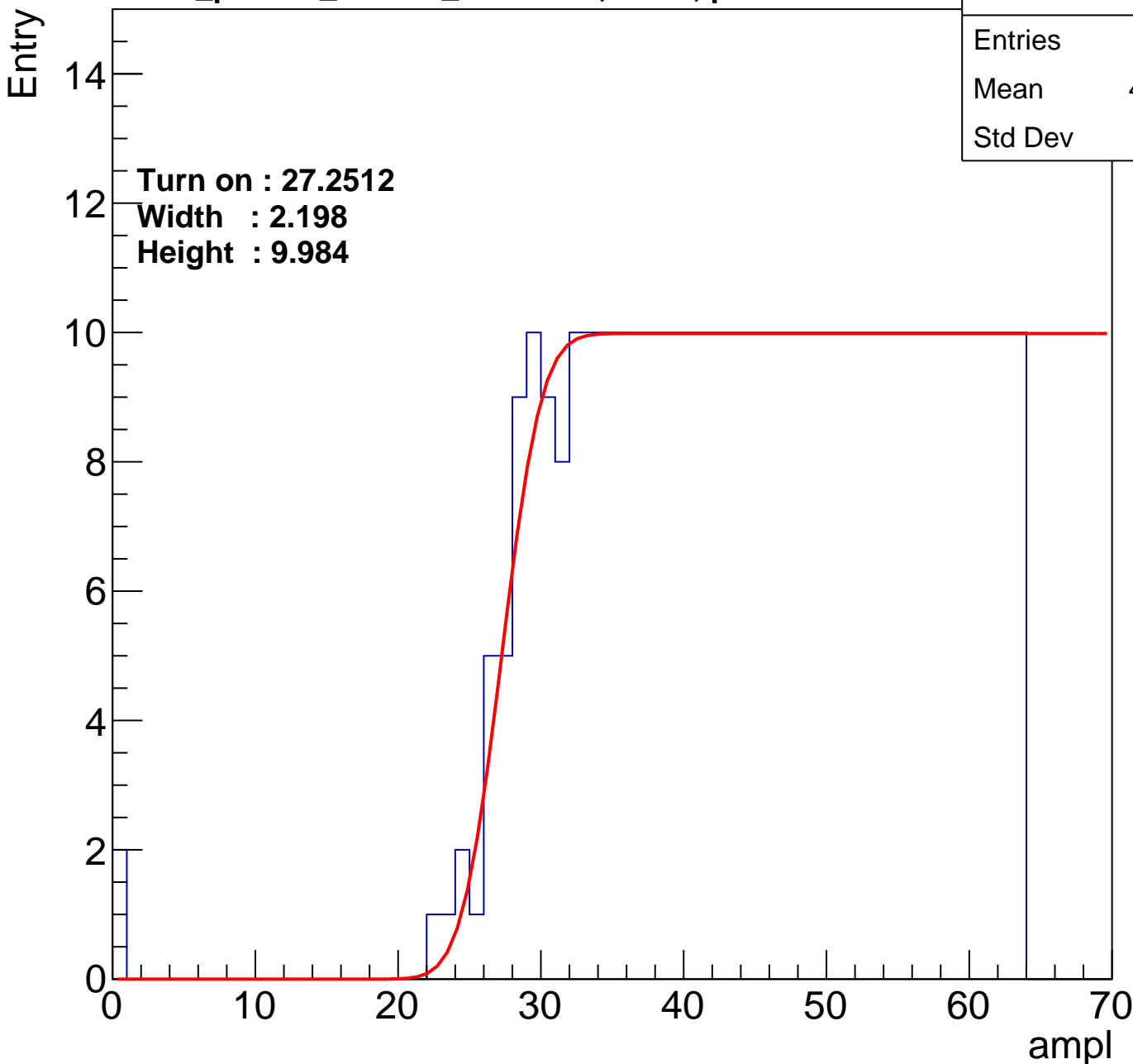
calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 27.2512

Width : 2.198

Height : 9.984



B1L100S, U25-ch42

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 27.6002

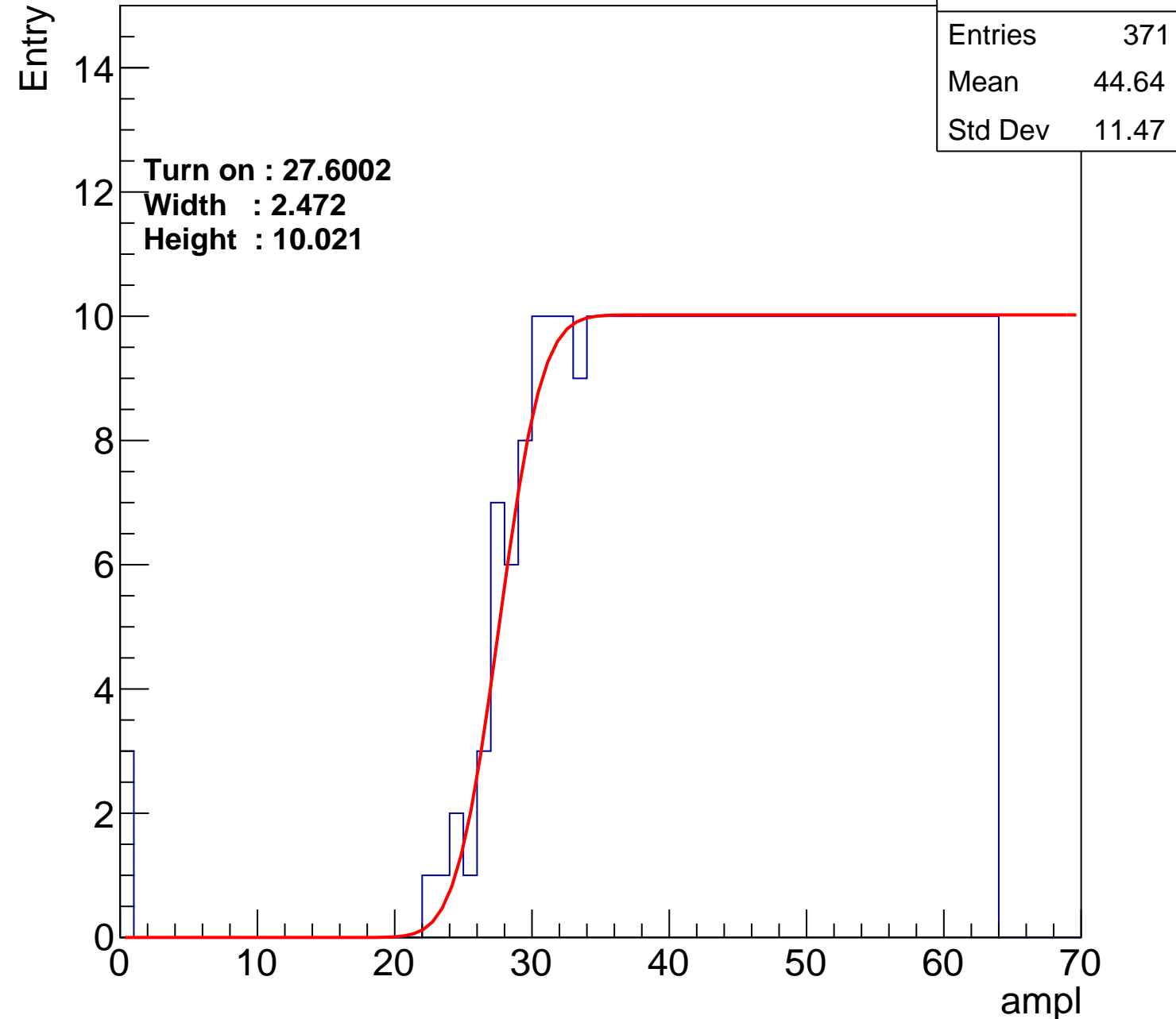
Width : 2.472

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch43

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.42
Std Dev	11.56

Turn on : 27.0493

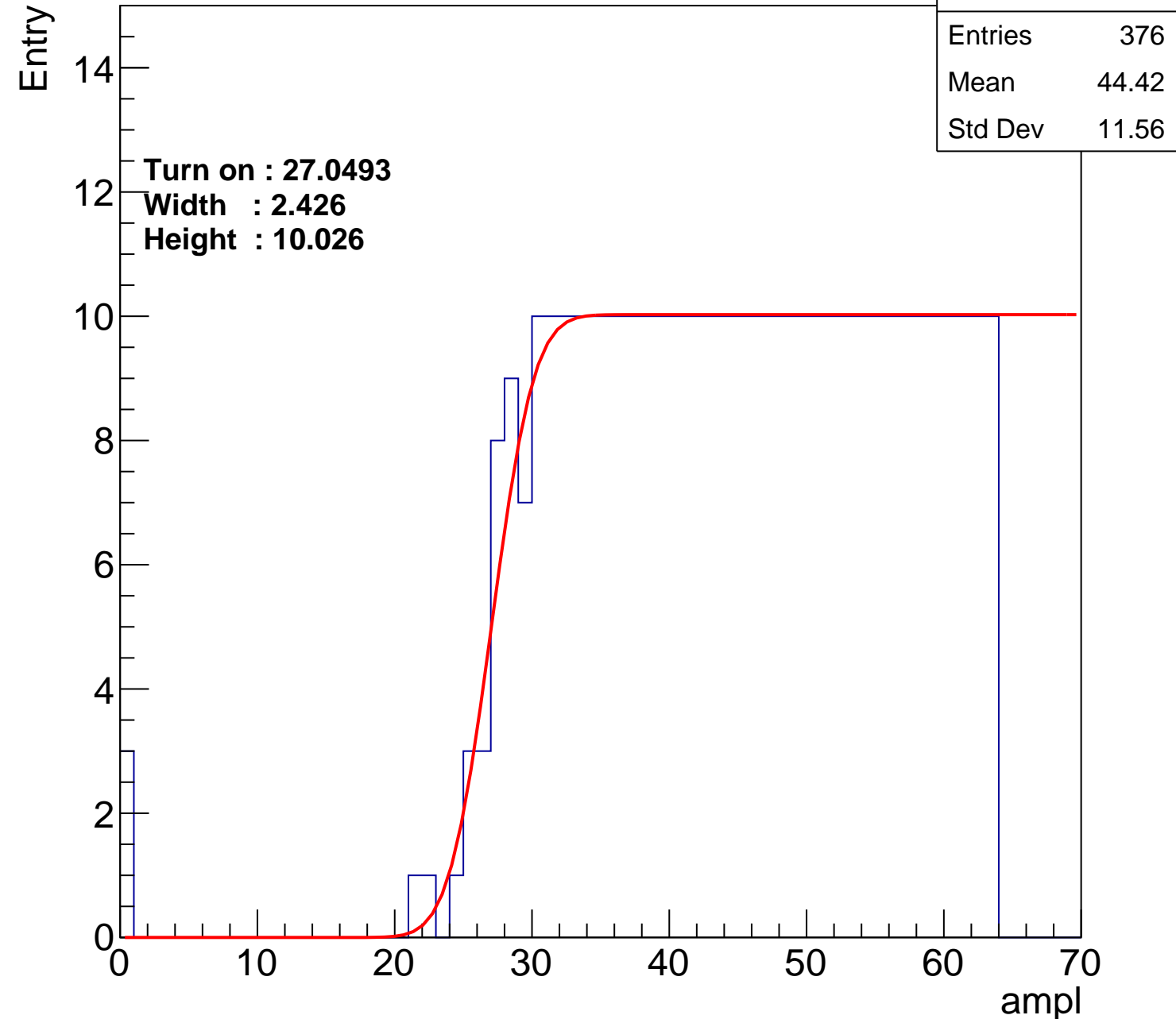
Width : 2.426

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch44

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.71
Std Dev	11.5

Turn on : 27.4114

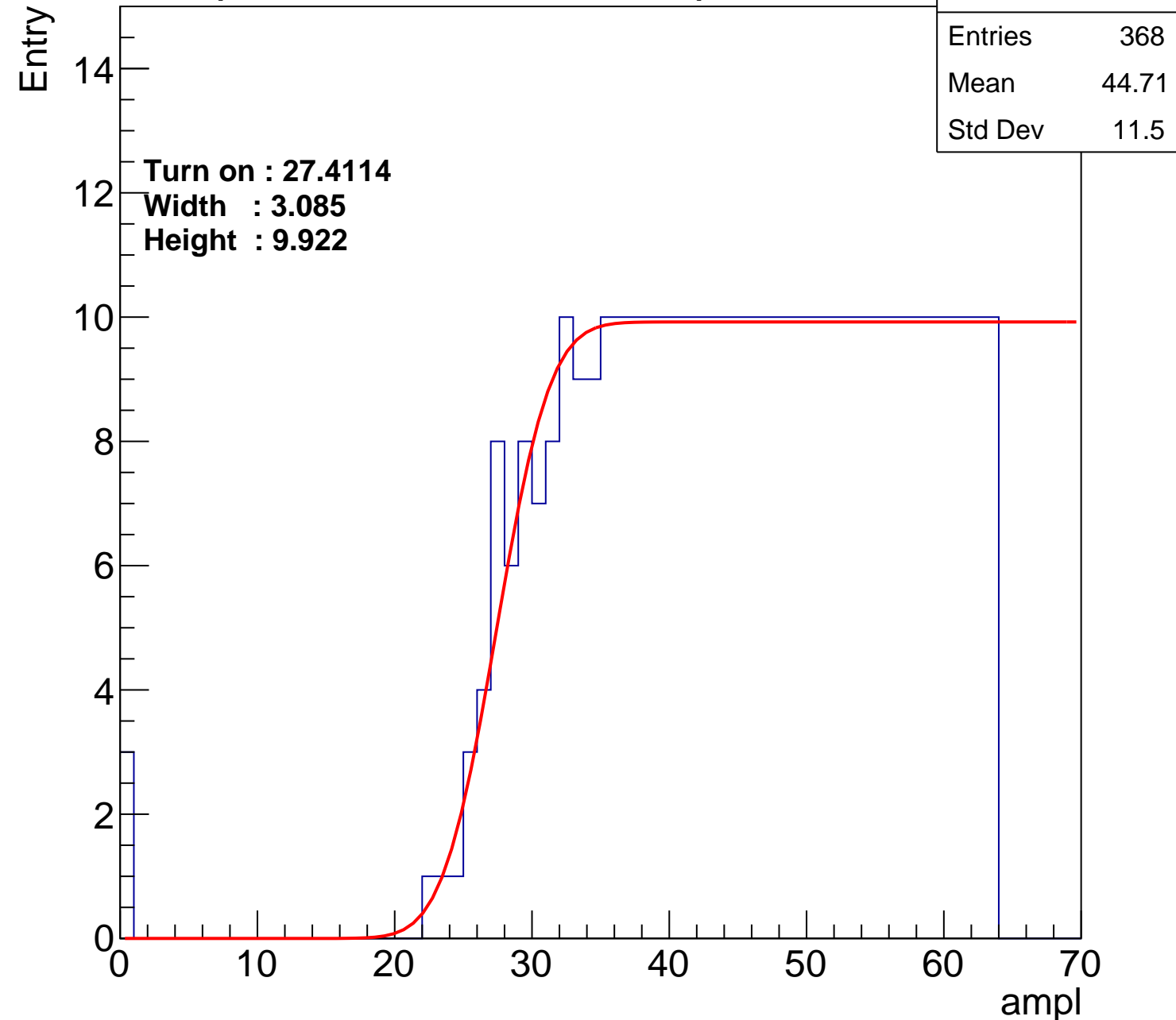
Width : 3.085

Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch45

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.6021

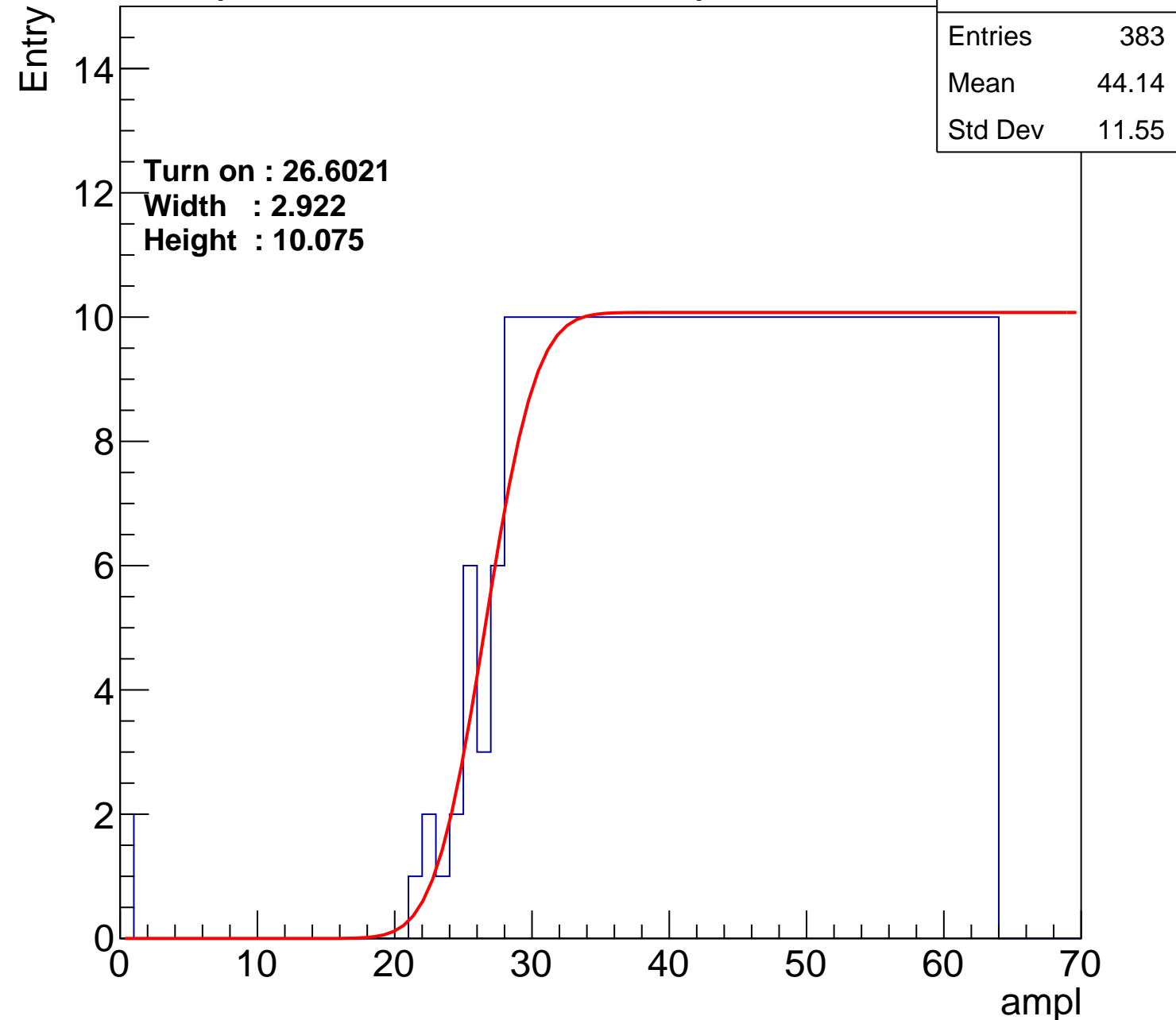
Width : 2.922

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl

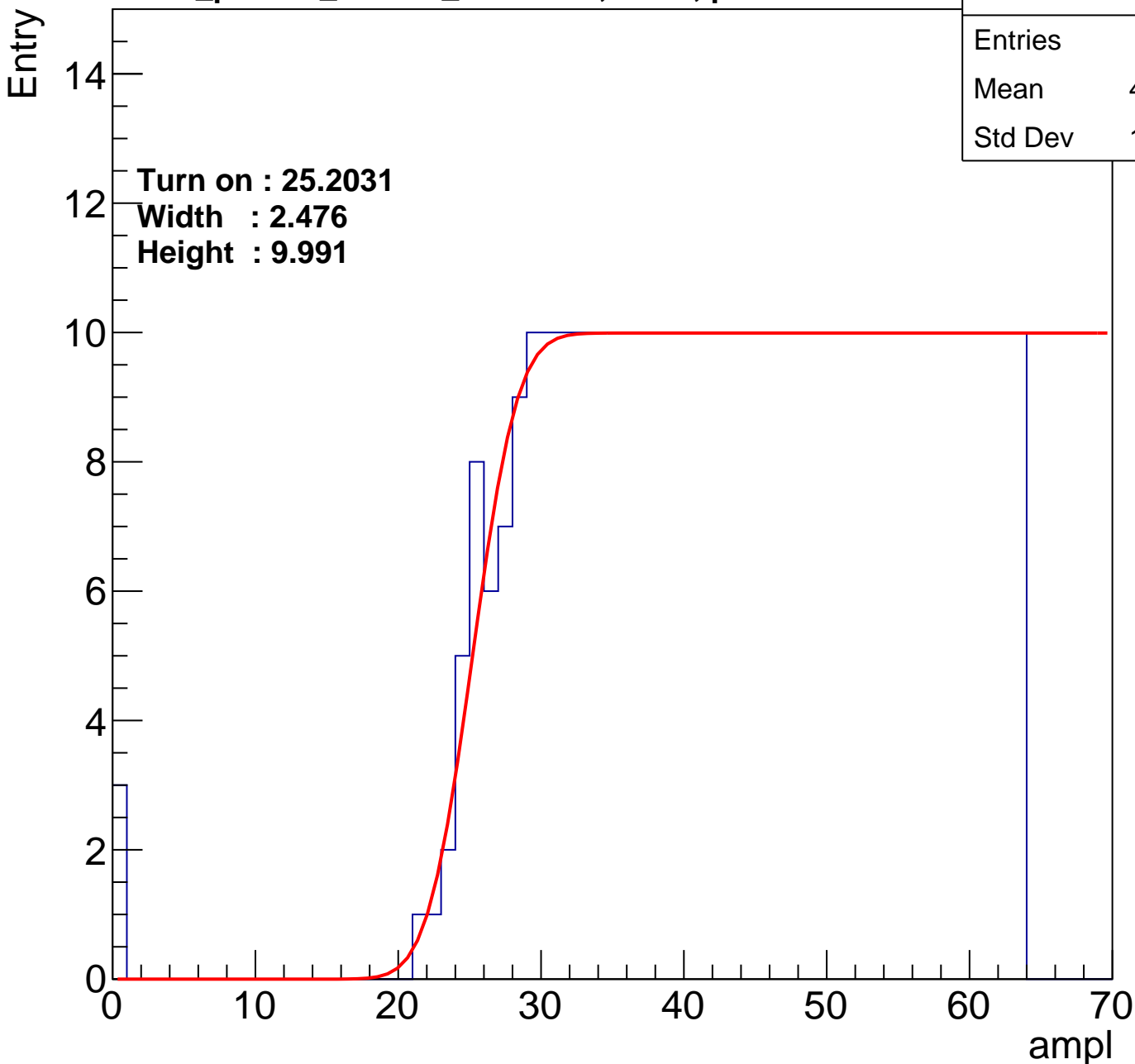


calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.64
Std Dev	11.94

Height : 9.991



B1L100S, U25-ch47

calib_packv5_042523_0143.root, FC#4, port A2

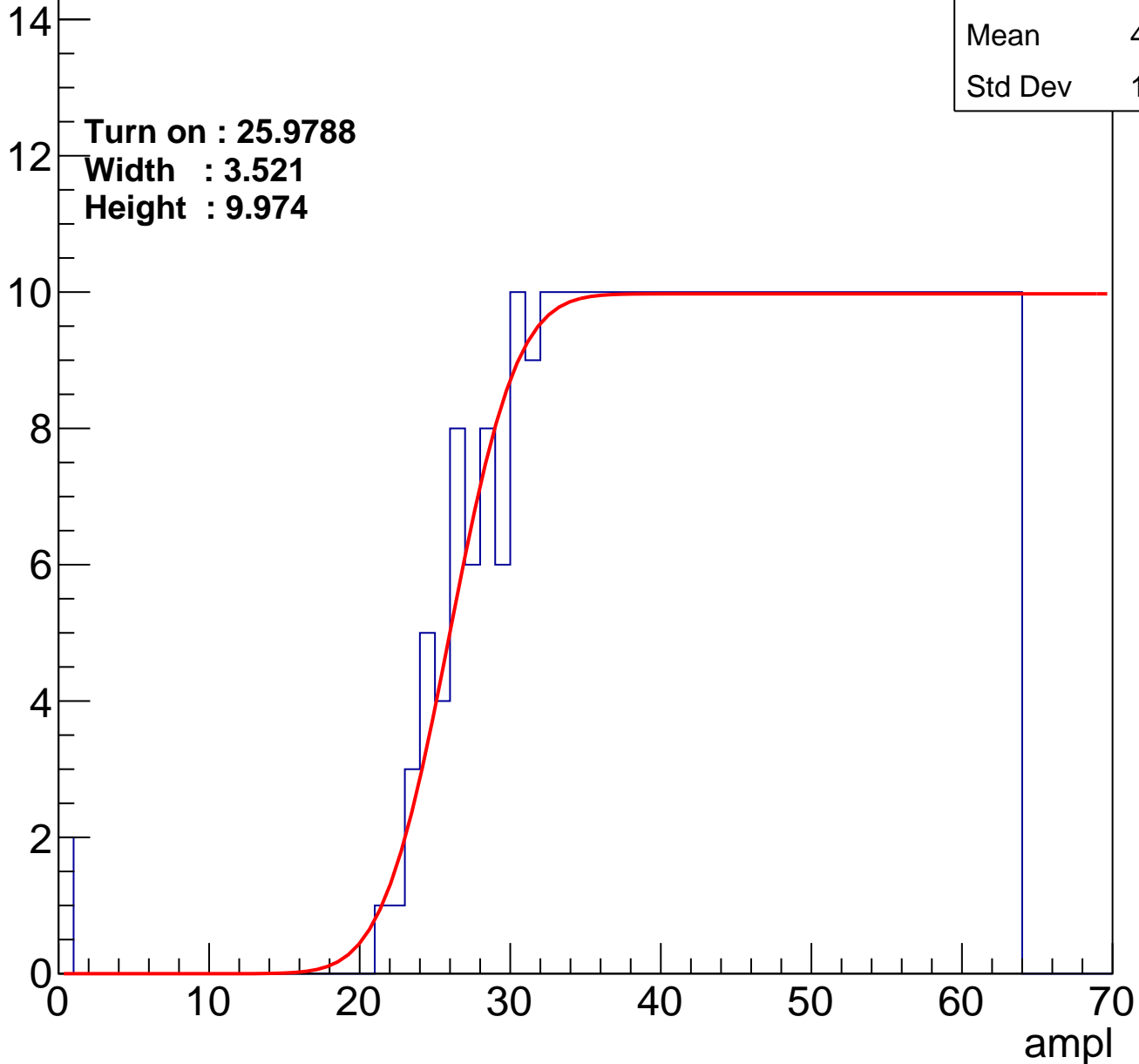
Entries	383
Mean	44.07
Std Dev	11.66

Turn on : 25.9788

Width : 3.521

Height : 9.974

Entry



B1L100S, U25-ch48

calib_packv5_042523_0143.root, FC#4, port A2

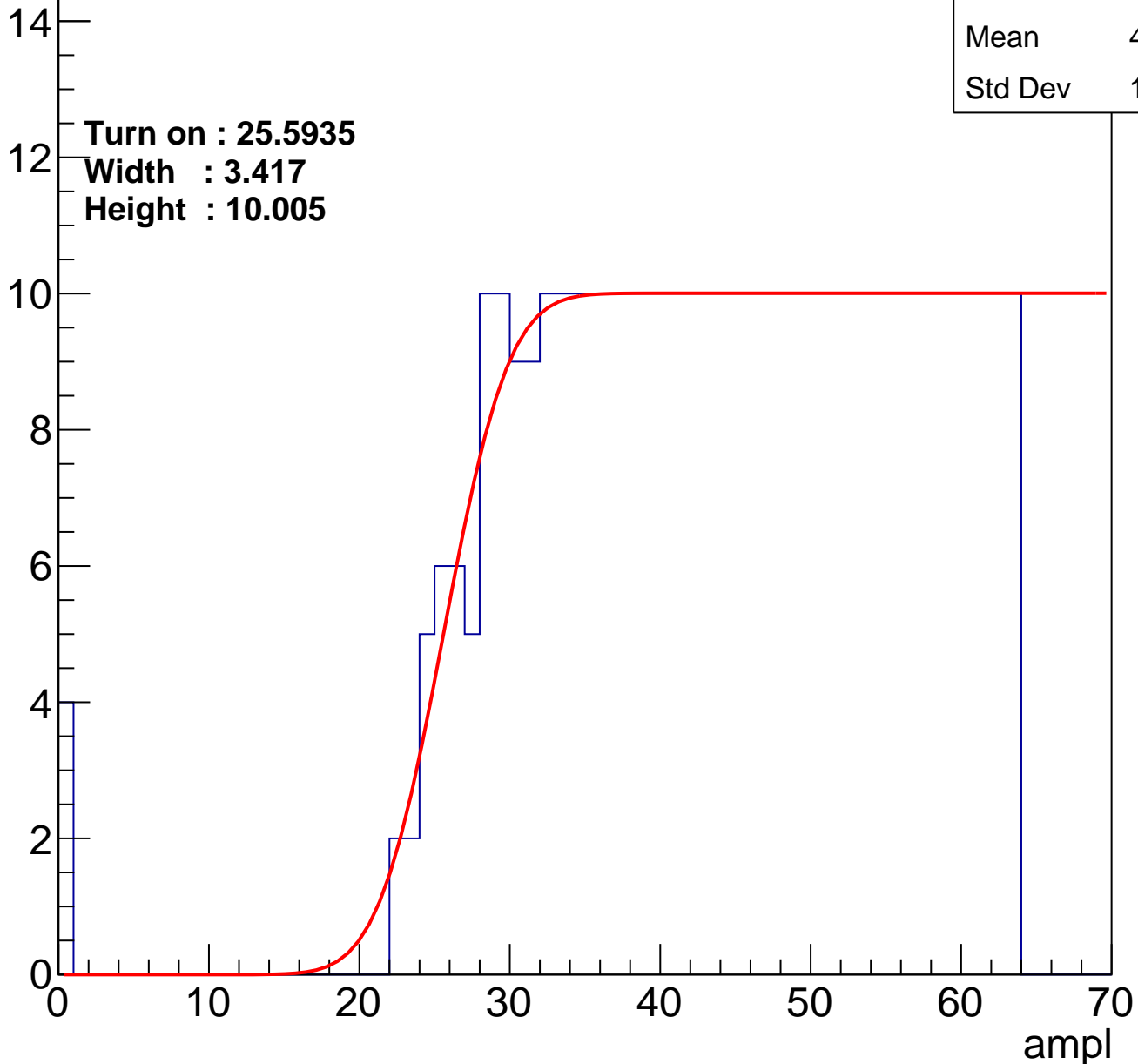
Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 25.5935

Width : 3.417

Height : 10.005

Entry



B1L100S, U25-ch49

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.02
Std Dev	11.63

Turn on : 28.5966

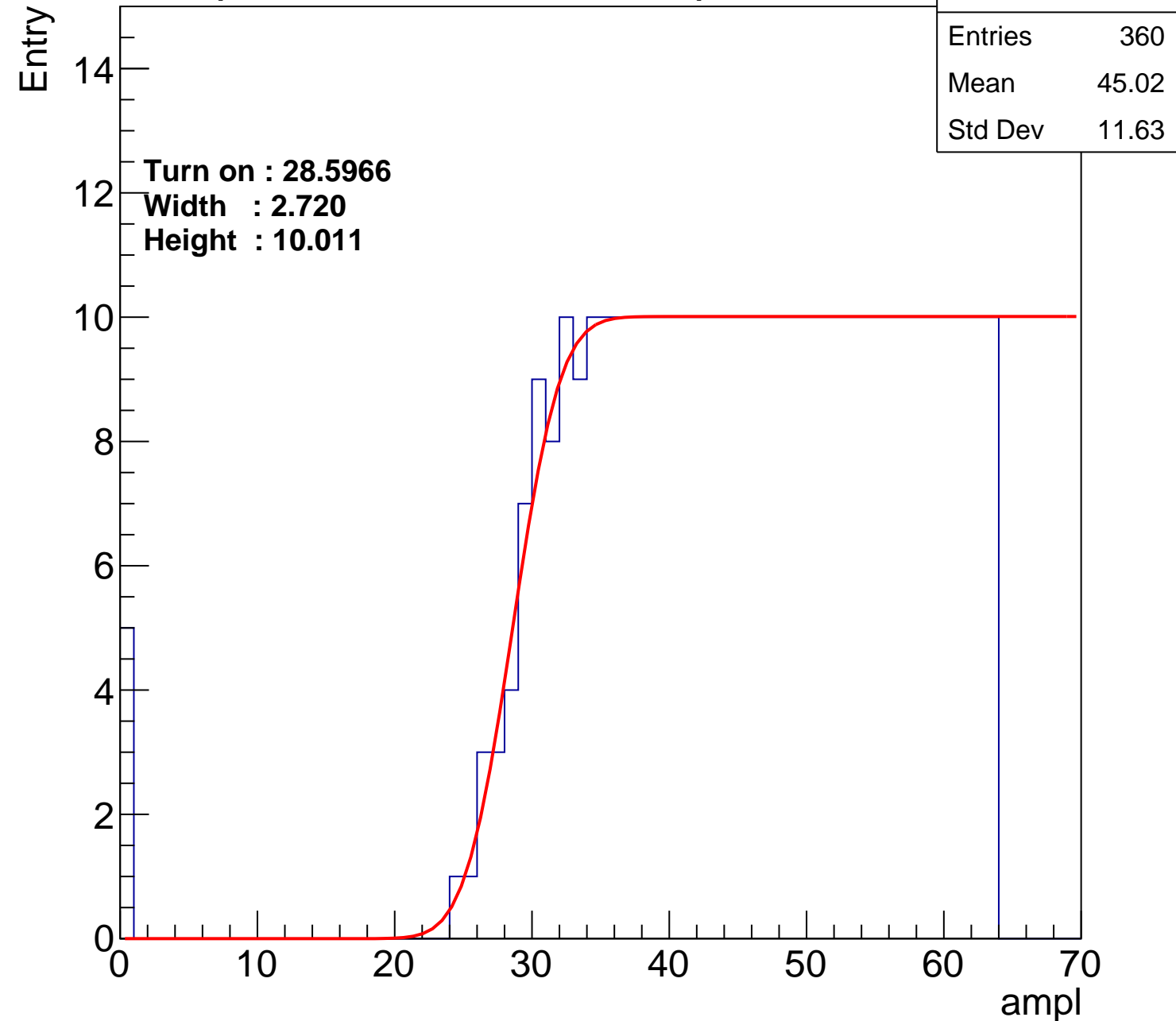
Width : 2.720

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch50

calib_packv5_042523_0143.root, FC#4, port A2

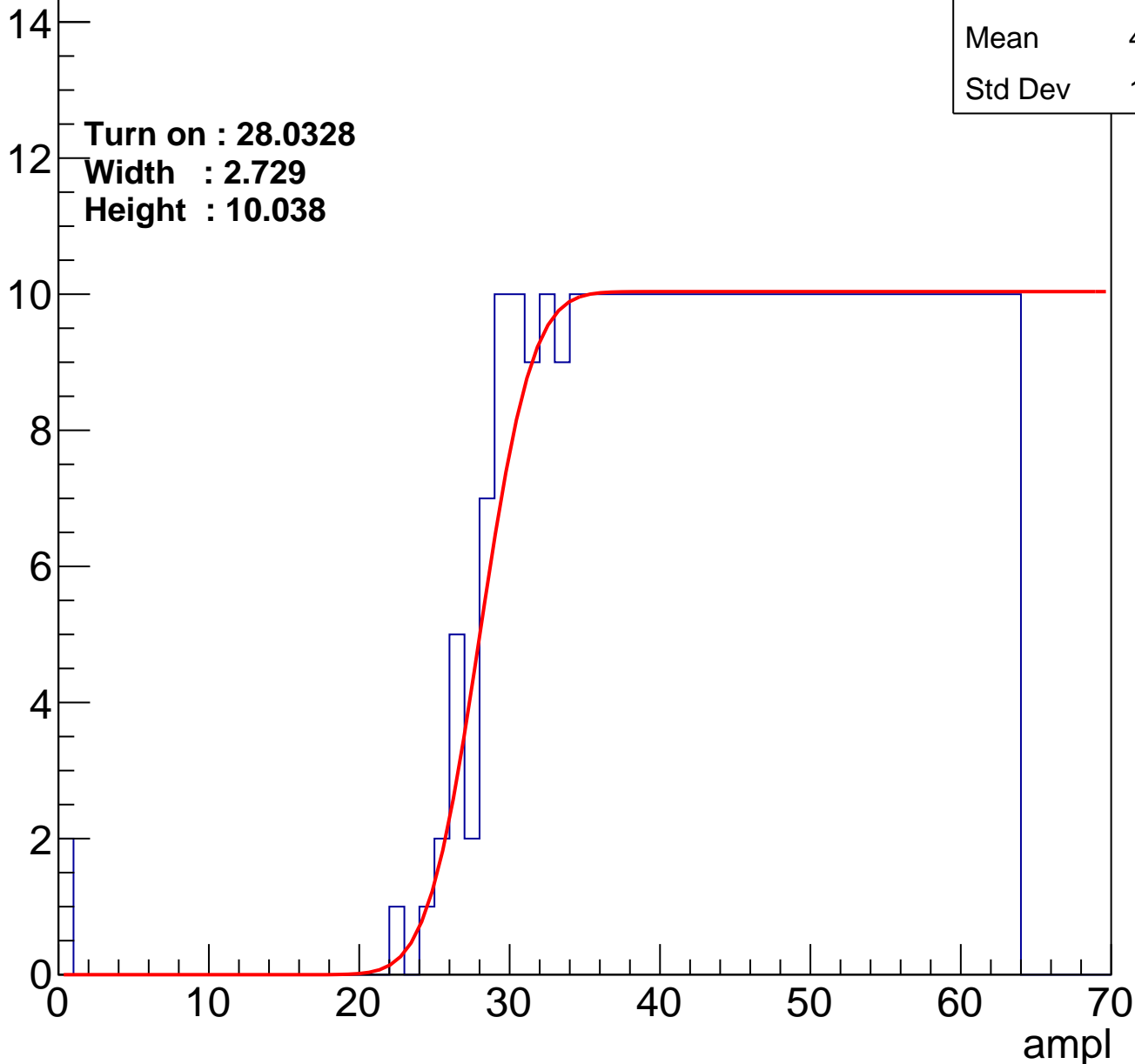
Entry

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 28.0328

Width : 2.729

Height : 10.038



B1L100S, U25-ch51

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.05
Std Dev	11.69

Turn on : 26.4451

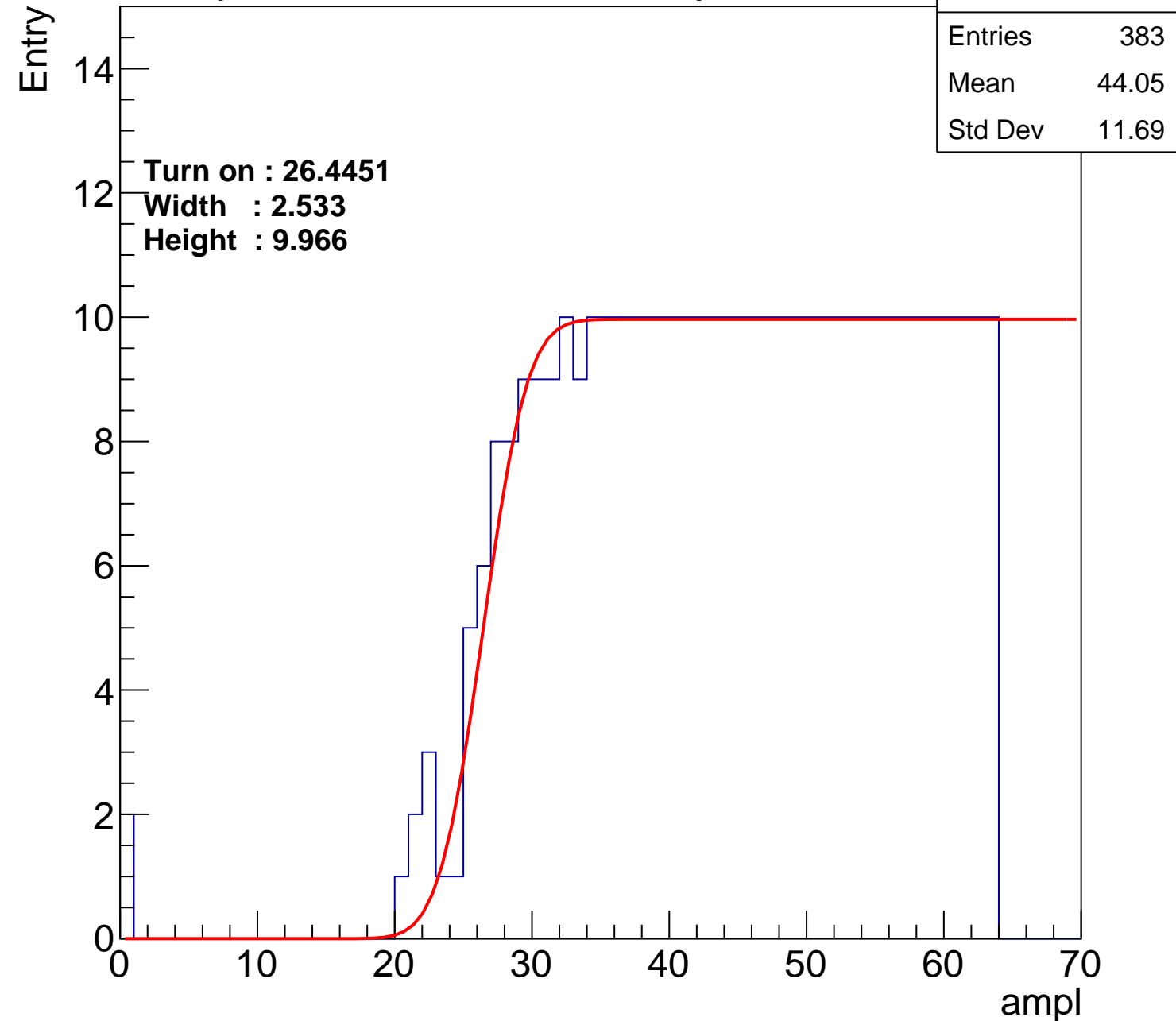
Width : 2.533

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch52

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.41
Std Dev	11.76

Turn on : 27.4295

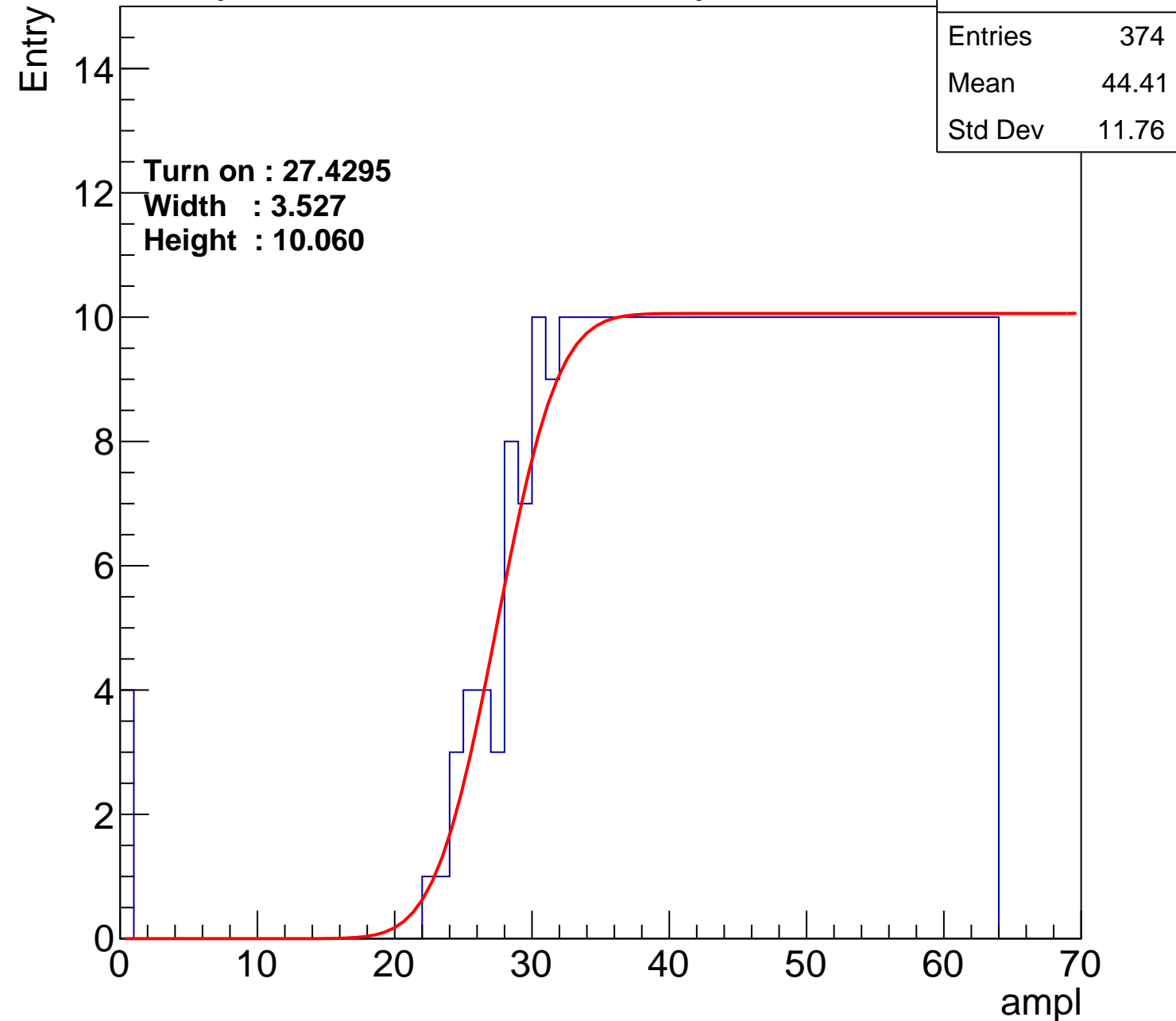
Width : 3.527

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch53

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.51
Std Dev	11.38

Turn on : 26.9356

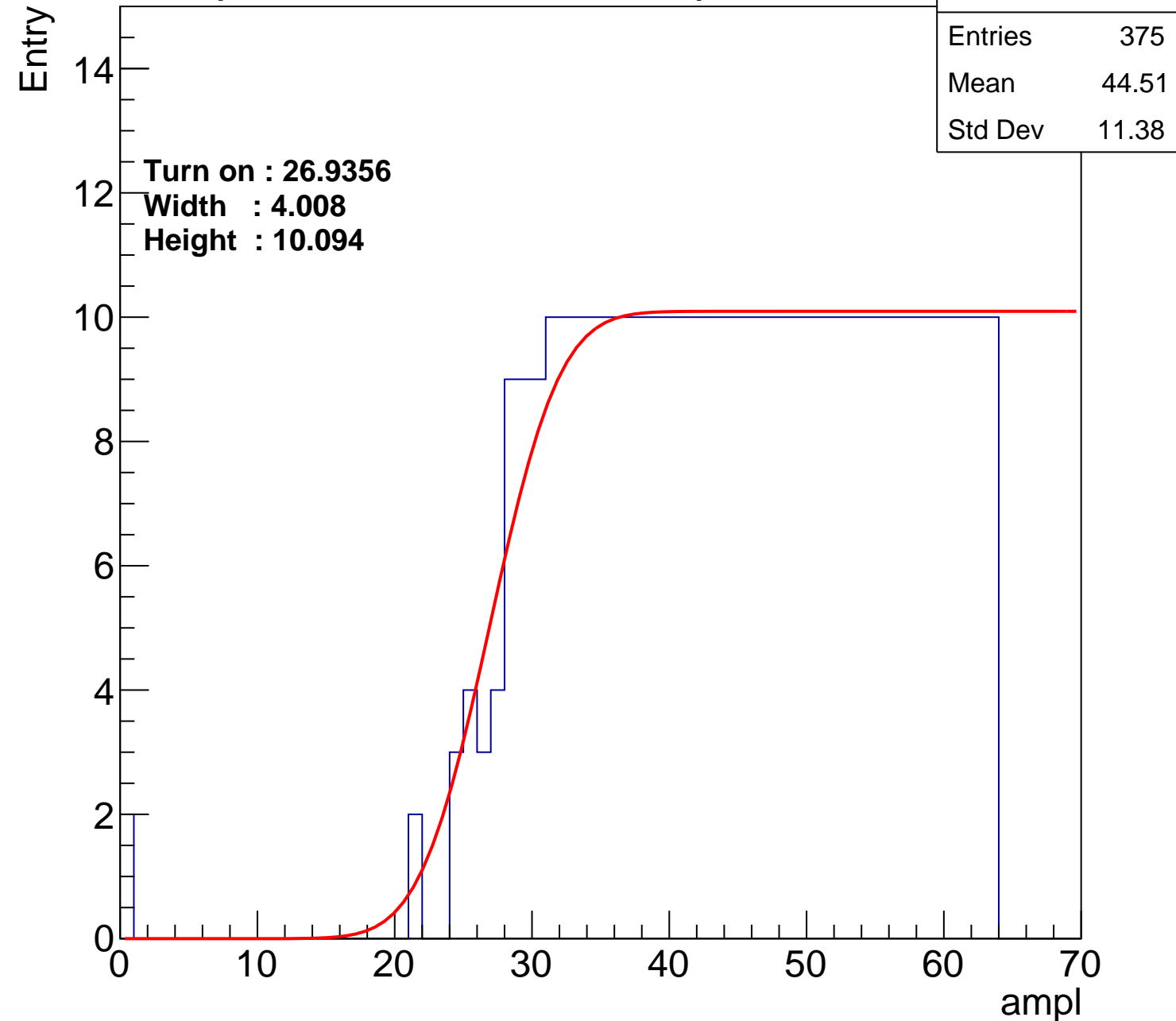
Width : 4.008

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch54

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	43.98
Std Dev	11.95

Turn on : 26.7456

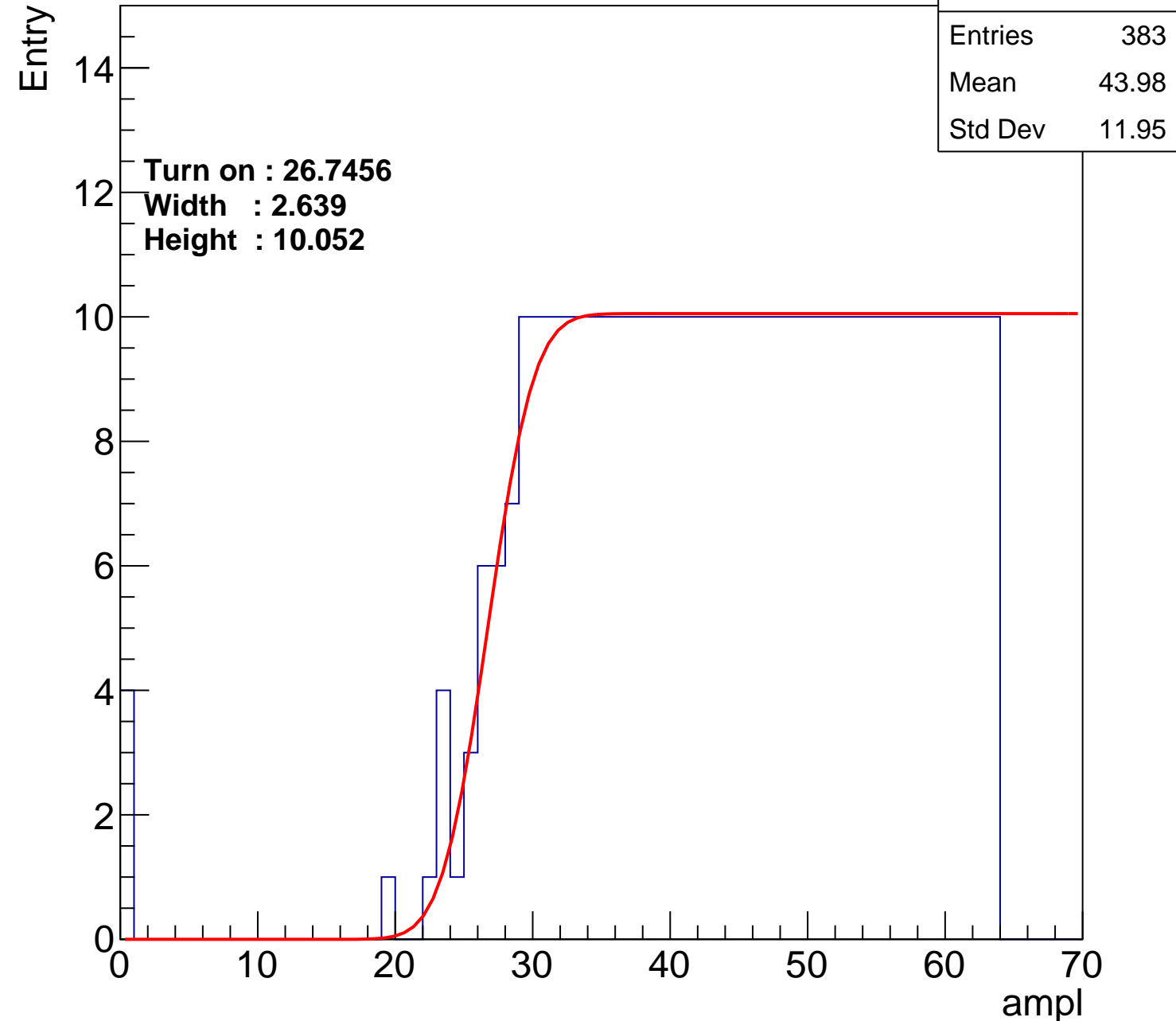
Width : 2.639

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 27.0119
Width : 2.667
Height : 9.974



B1L100S, U25-ch56

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.8
Std Dev	12.19

Turn on : 25.9968

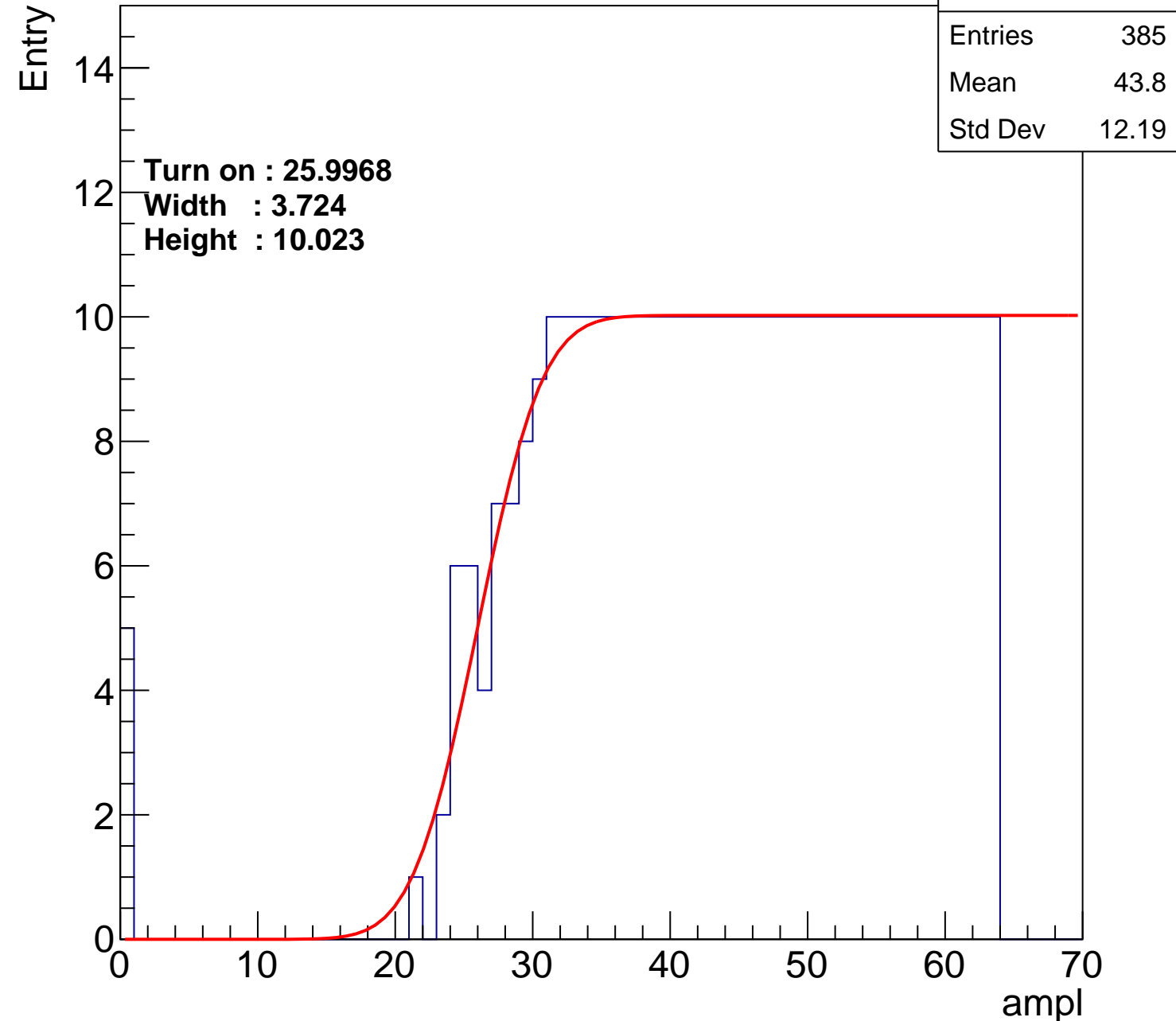
Width : 3.724

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch57

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.59
Std Dev	11.69

Turn on : 27.6528

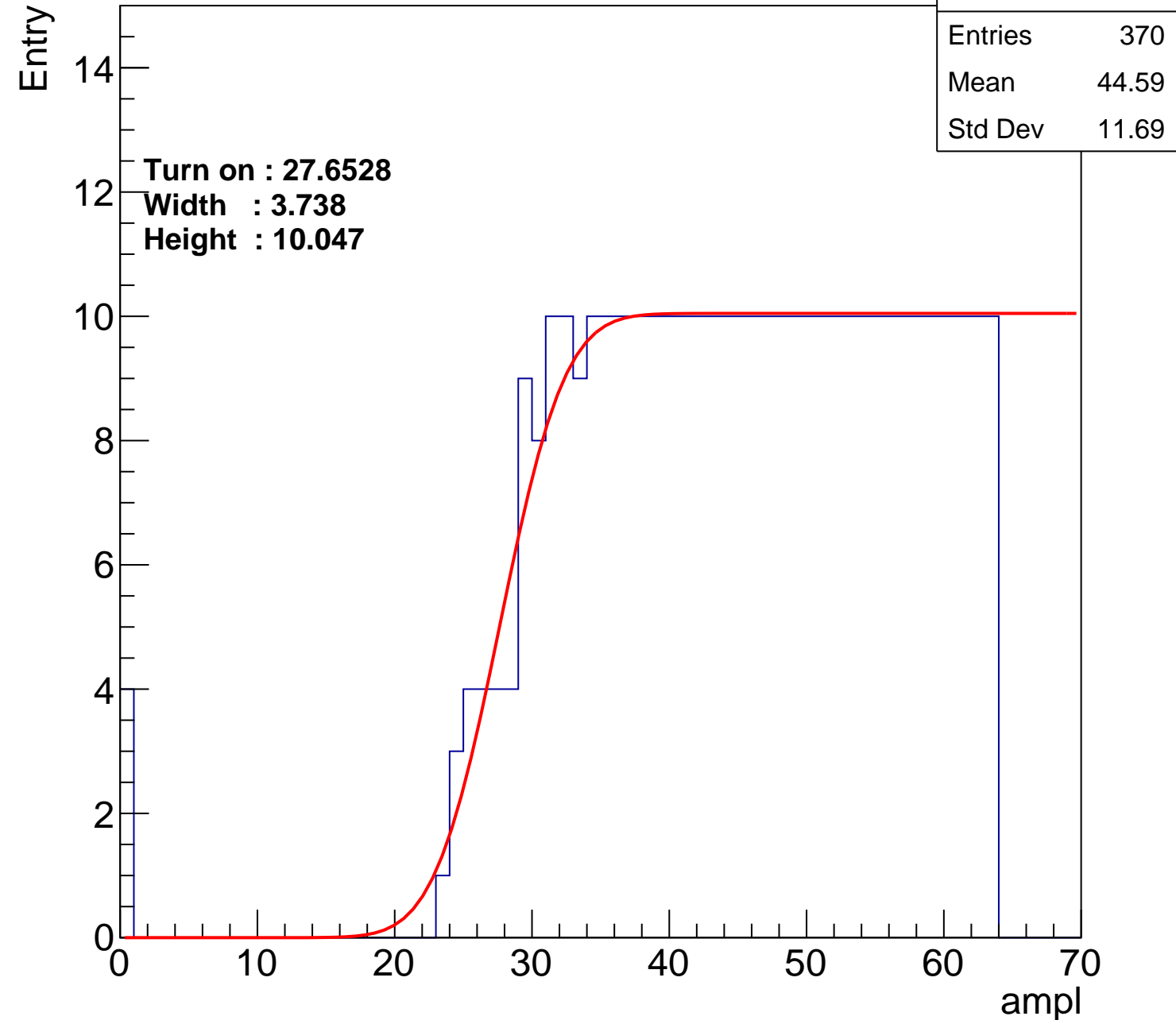
Width : 3.738

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch58

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.76
Std Dev	11.12

Turn on : 27.5778

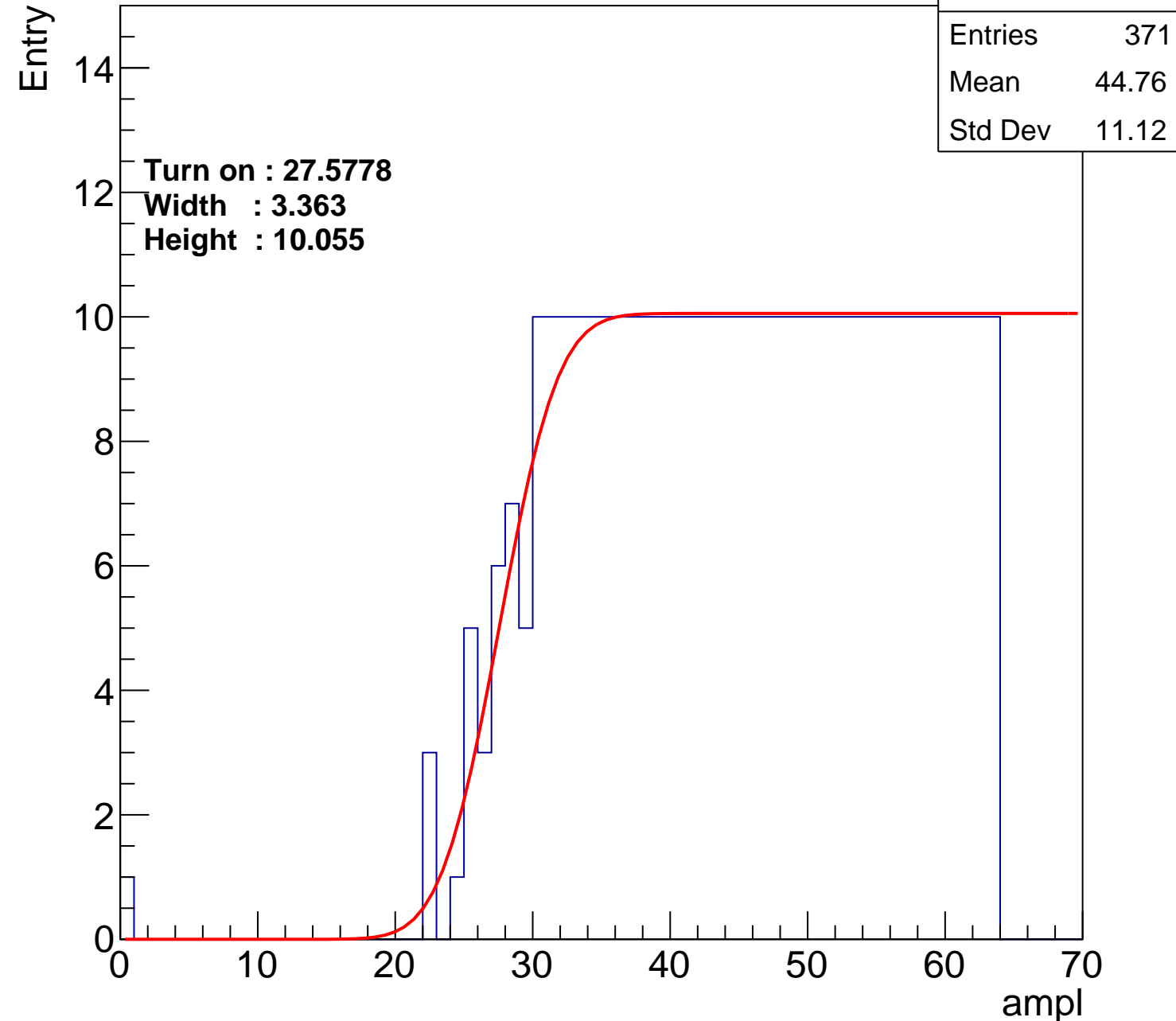
Width : 3.363

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch59

calib_packv5_042523_0143.root, FC#4, port A2

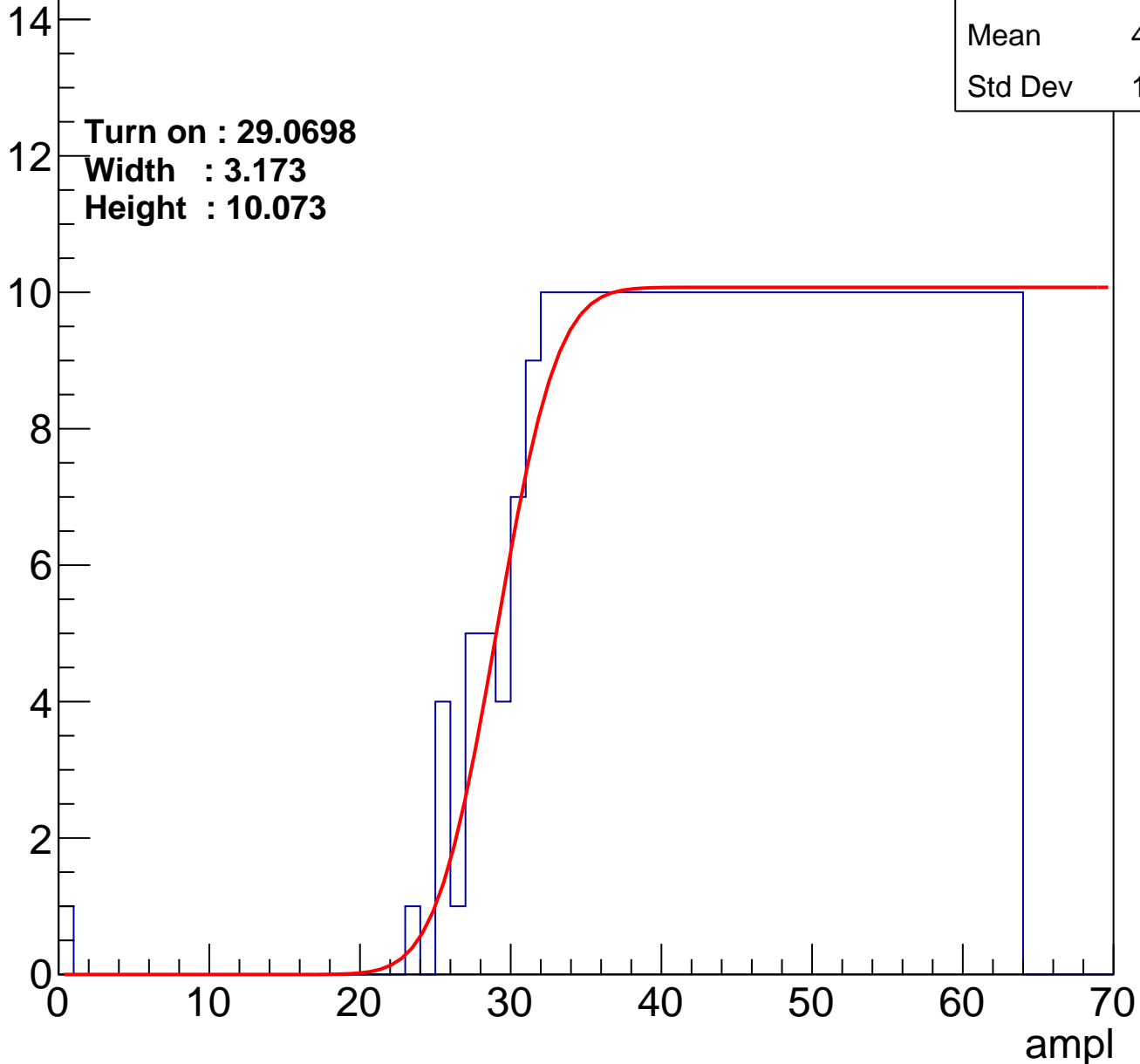
Entries	357
Mean	45.46
Std Dev	10.73

Turn on : 29.0698

Width : 3.173

Height : 10.073

Entry



B1L100S, U25-ch60

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 25.4051

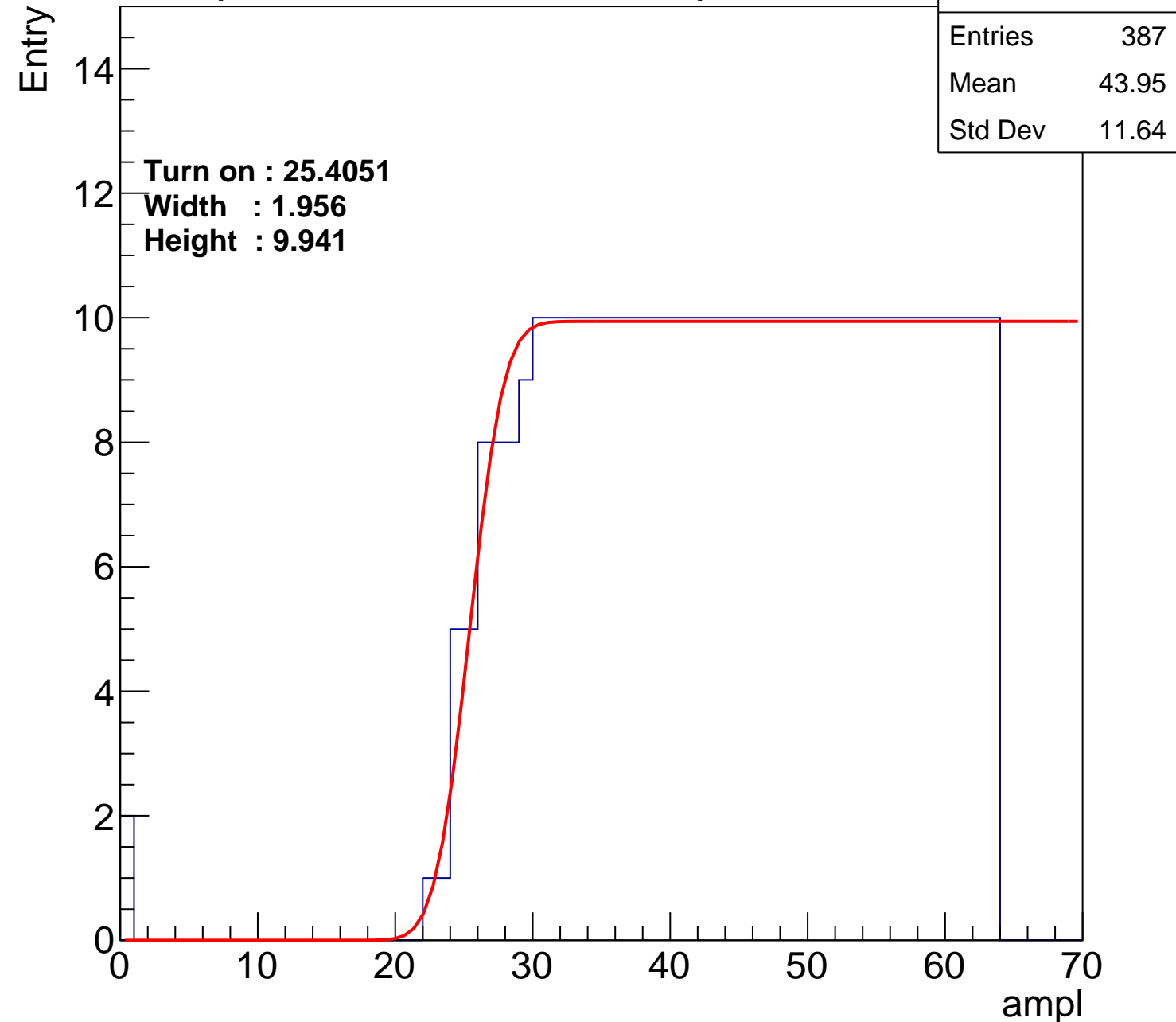
Width : 1.956

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch61

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.53
Std Dev	11.55

Turn on : 27.2357

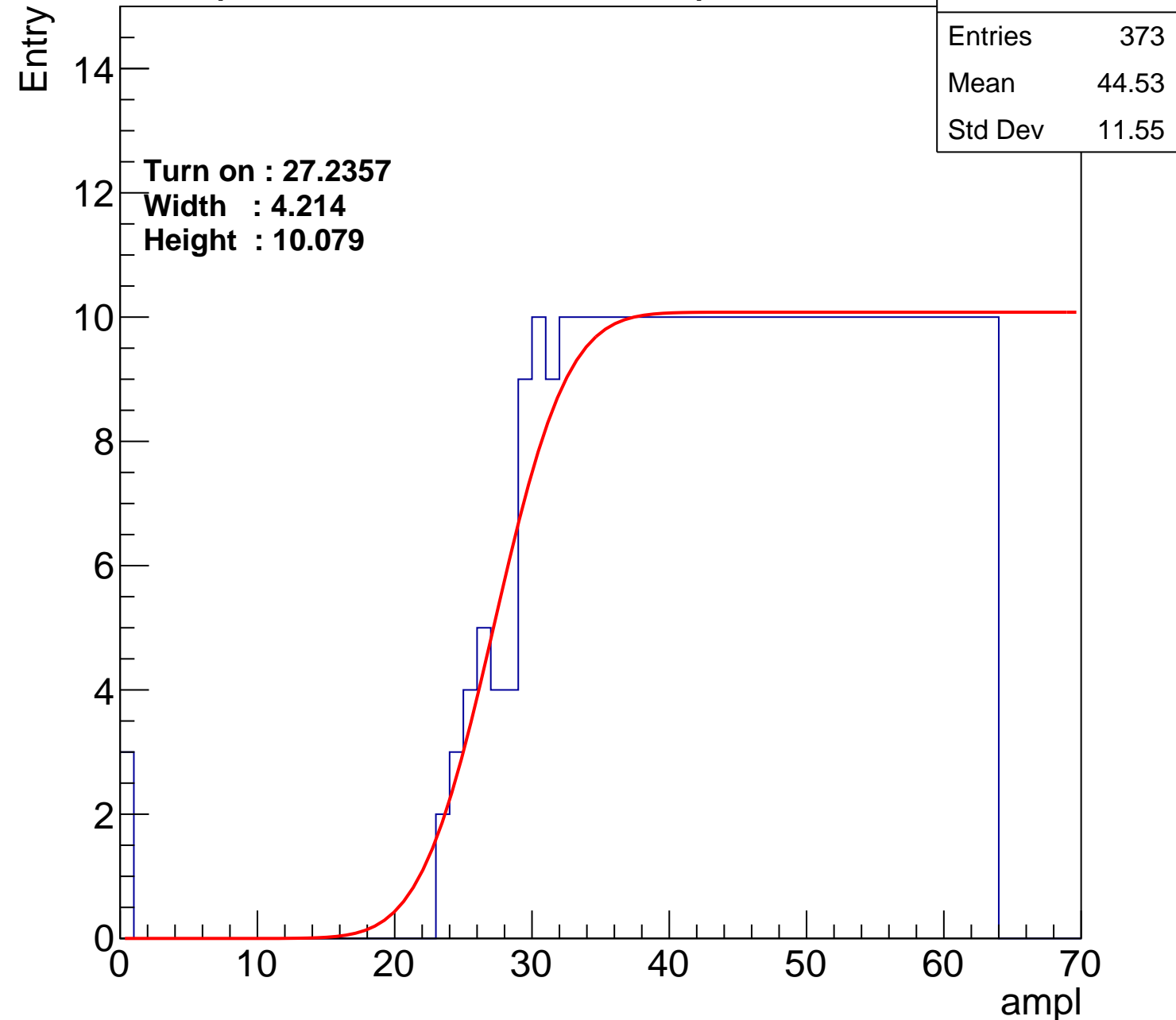
Width : 4.214

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch62

calib_packv5_042523_0143.root, FC#4, port A2

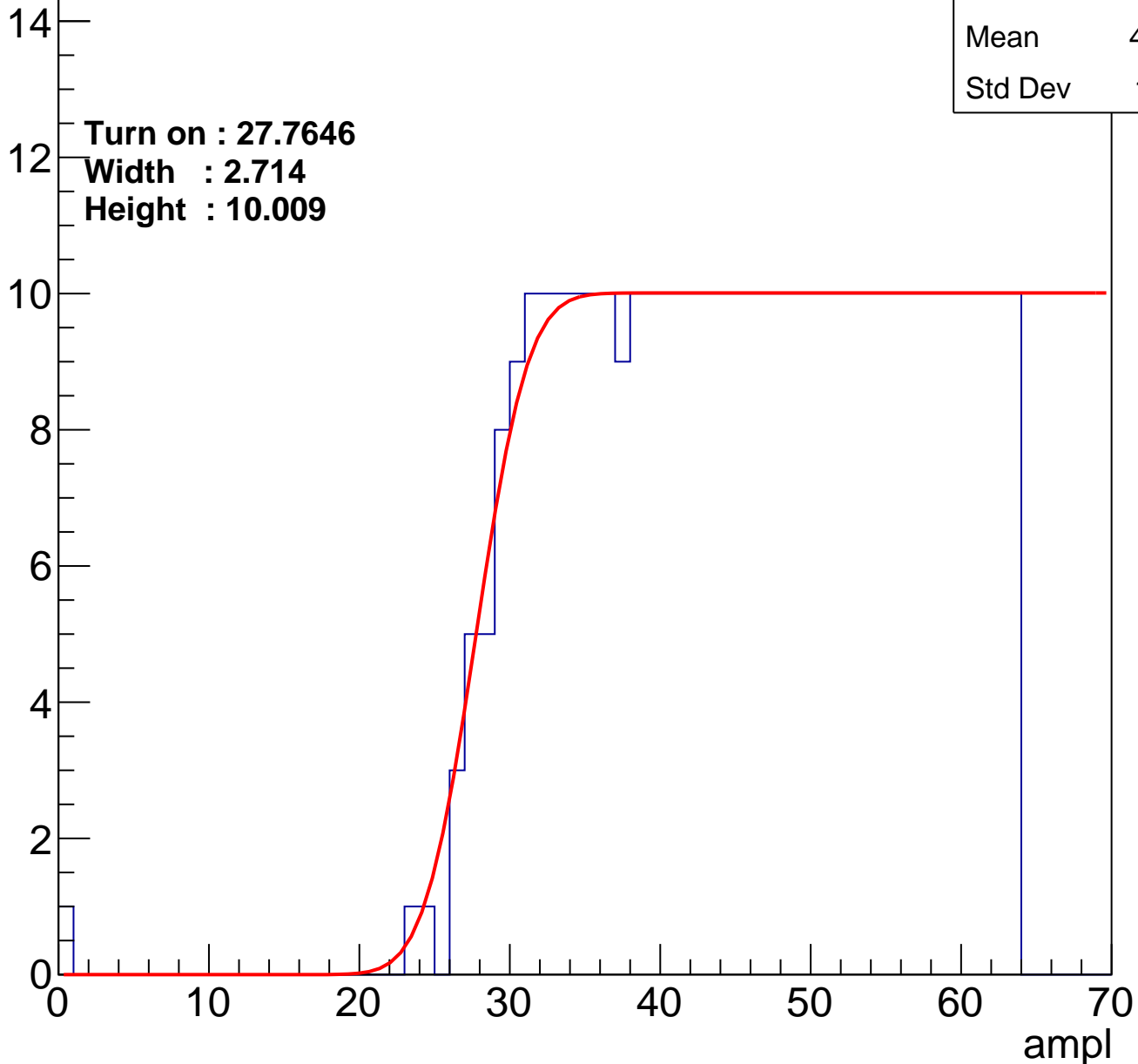
Entries	362
Mean	45.23
Std Dev	10.81

Turn on : 27.7646

Width : 2.714

Height : 10.009

Entry



B1L100S, U25-ch63

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.86
Std Dev	11.39

Turn on : 27.9746

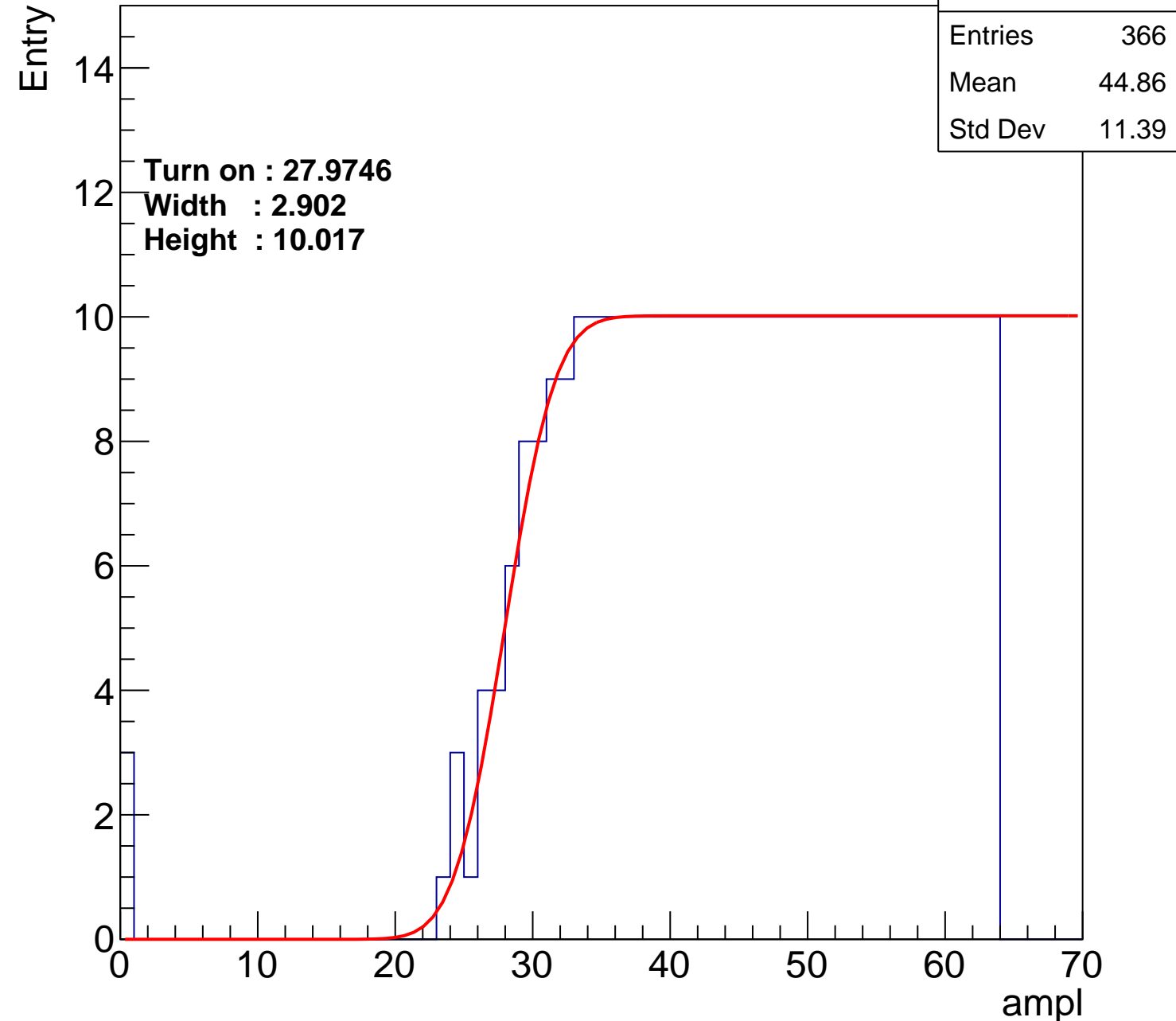
Width : 2.902

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch64

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.71
Std Dev	11.51

Turn on : 27.5747

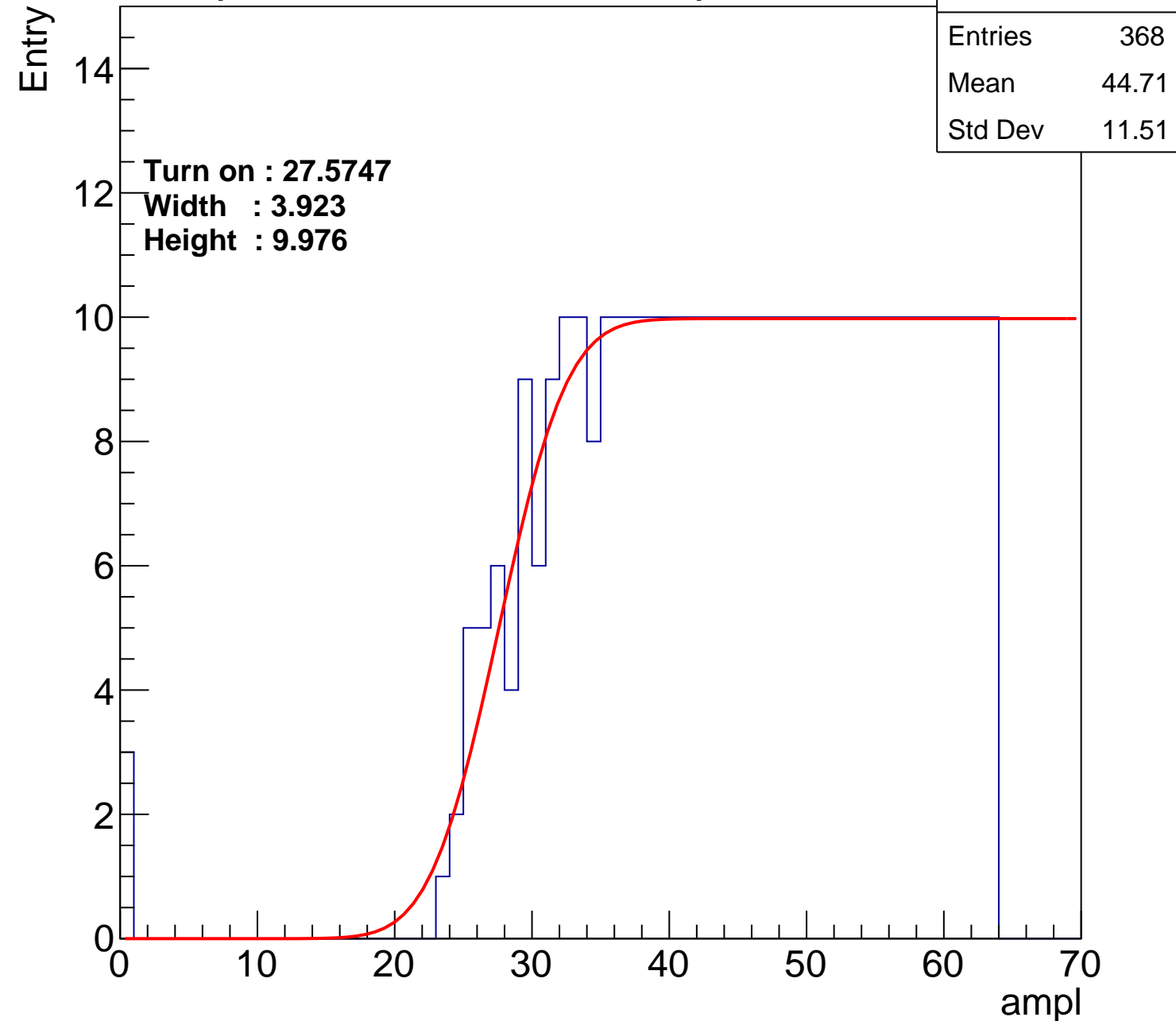
Width : 3.923

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch65

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.24
Std Dev	11.02

Turn on : 28.2800

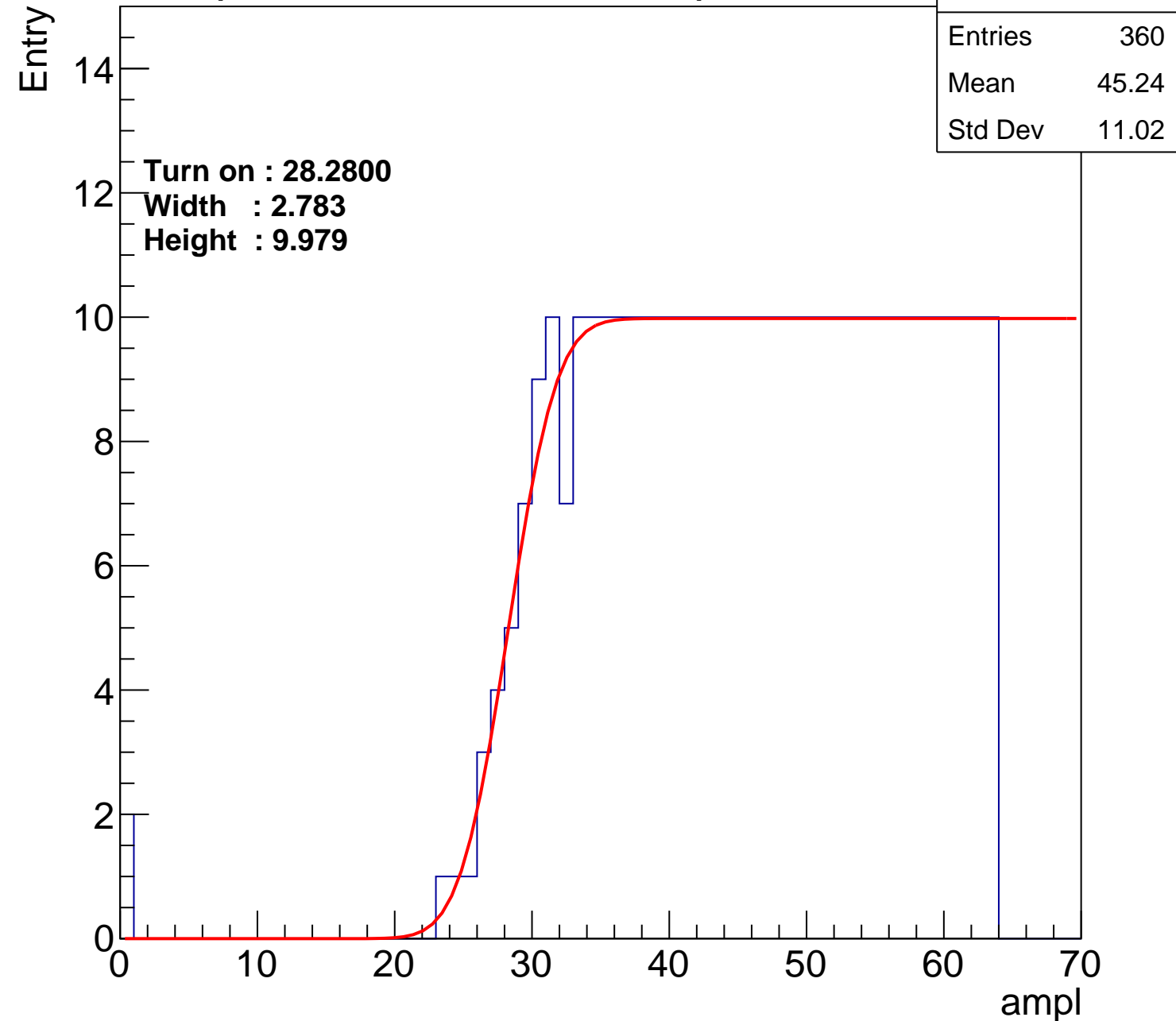
Width : 2.783

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch66

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.13
Std Dev	10.88

Turn on : 27.9567

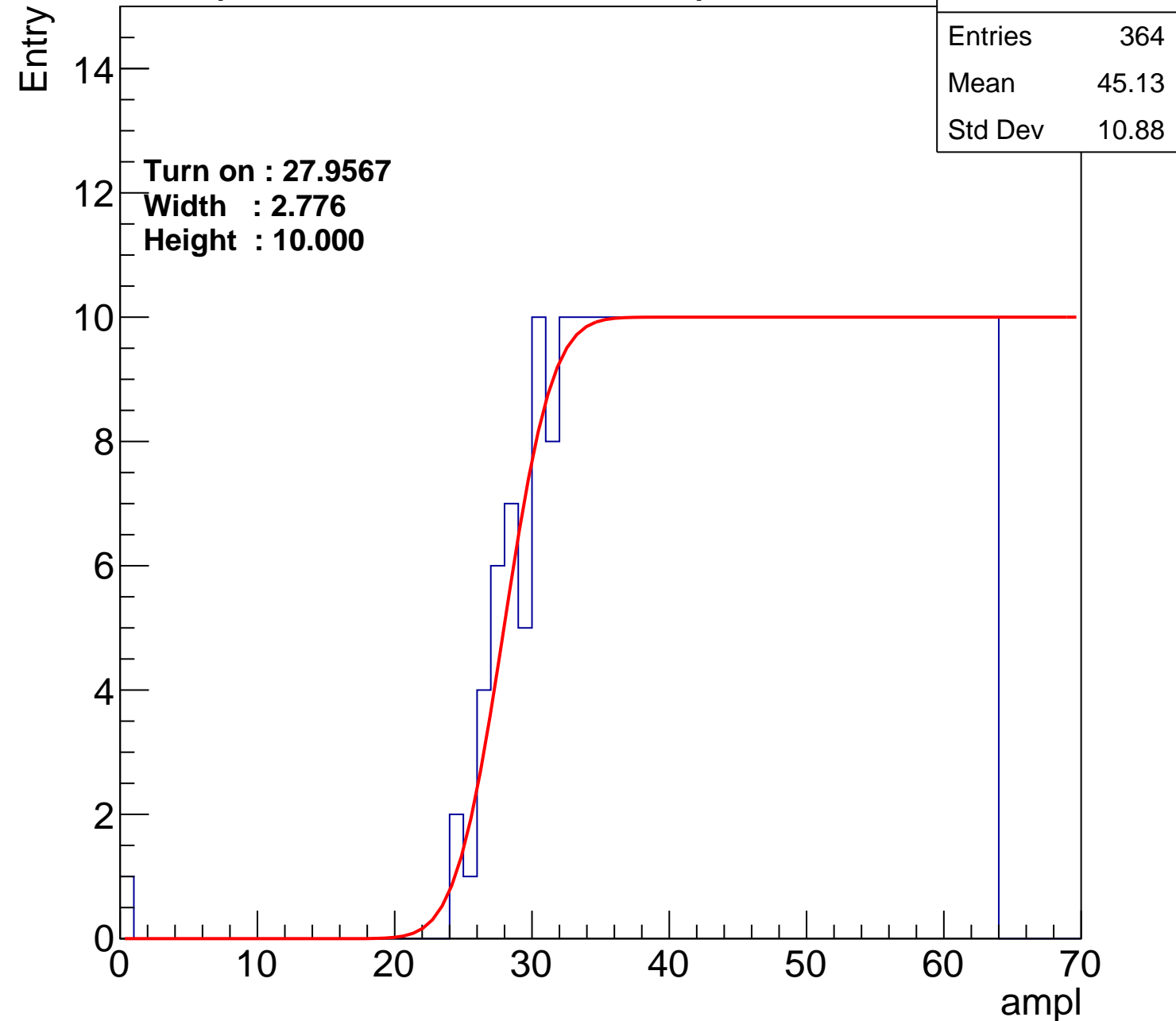
Width : 2.776

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch67

calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	45.3
Std Dev	10.98

Turn on : 28.1240

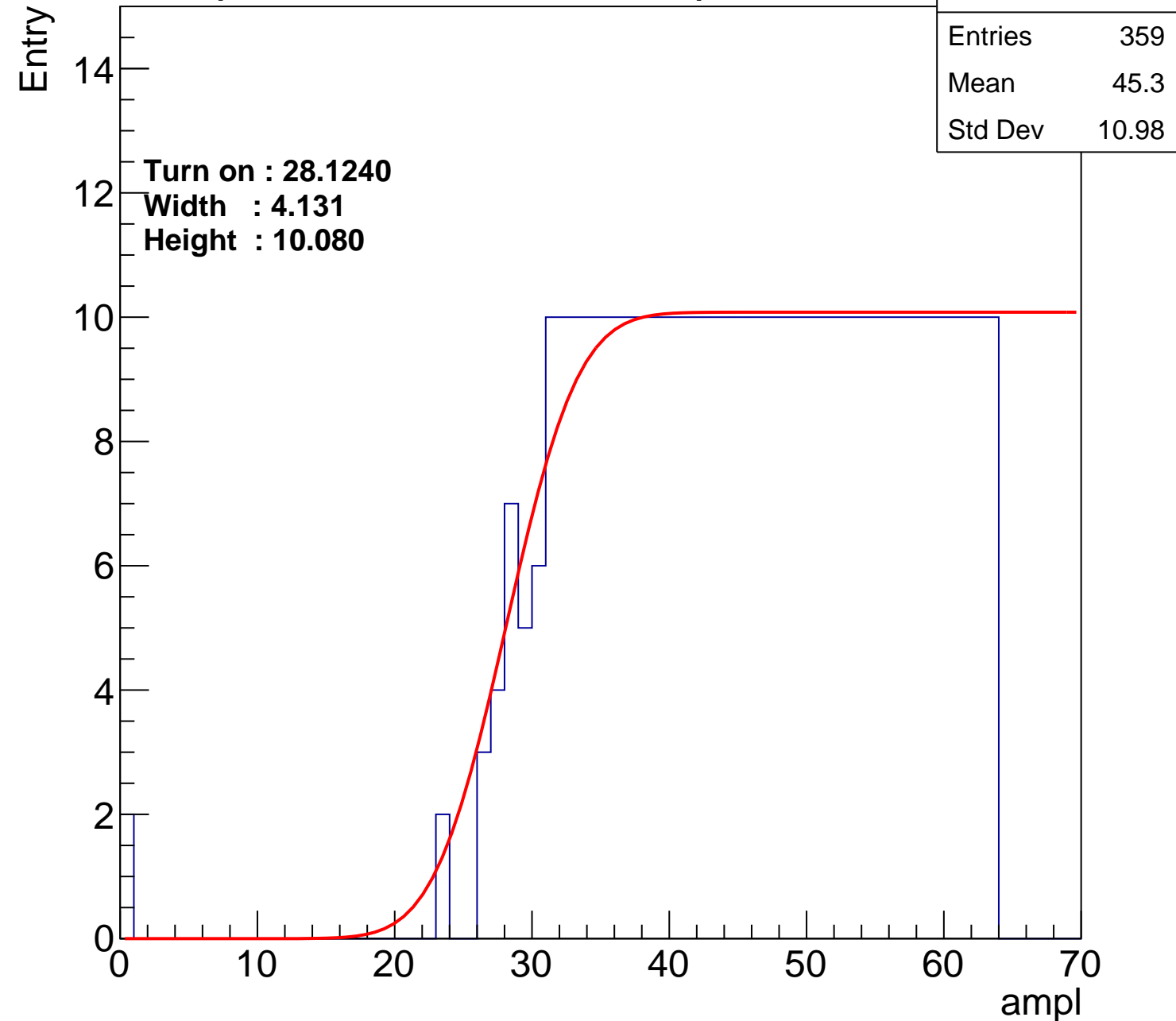
Width : 4.131

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch68

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.89
Std Dev	11.24

Turn on : 27.7814

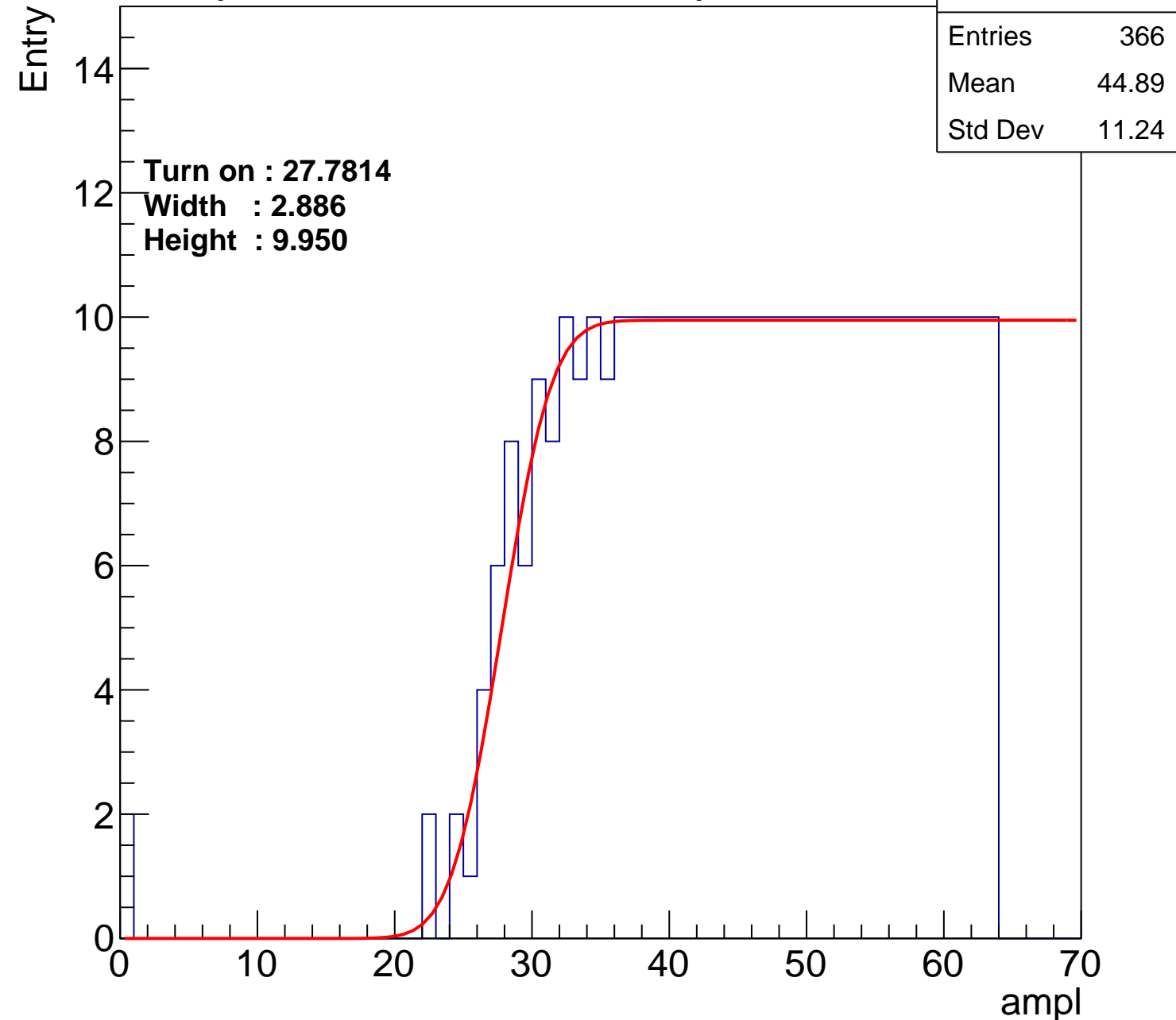
Width : 2.886

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch69

calib_packv5_042523_0143.root, FC#4, port A2

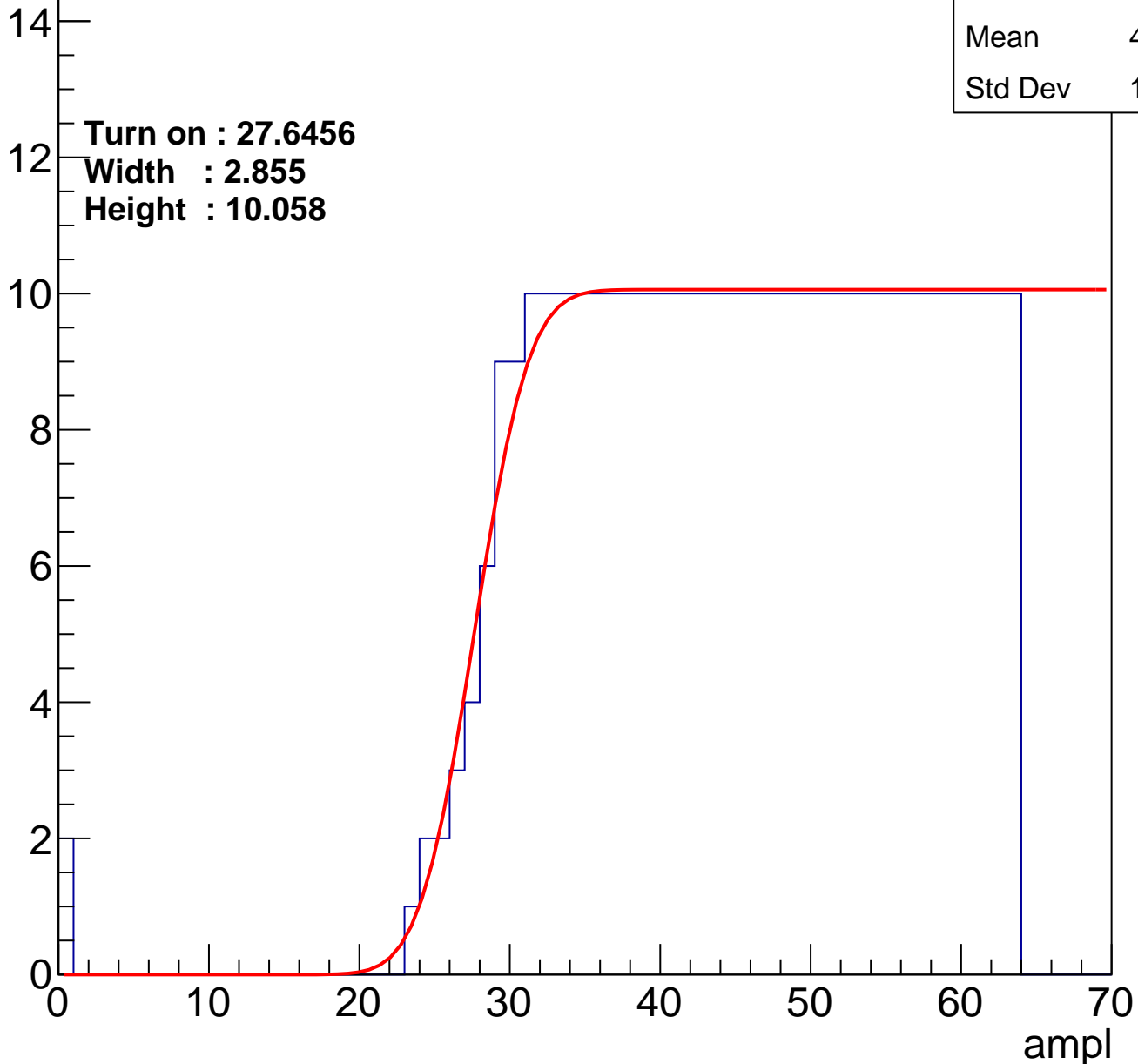
Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 27.6456

Width : 2.855

Height : 10.058

Entry



B1L100S, U25-ch70

calib_packv5_042523_0143.root, FC#4, port A2

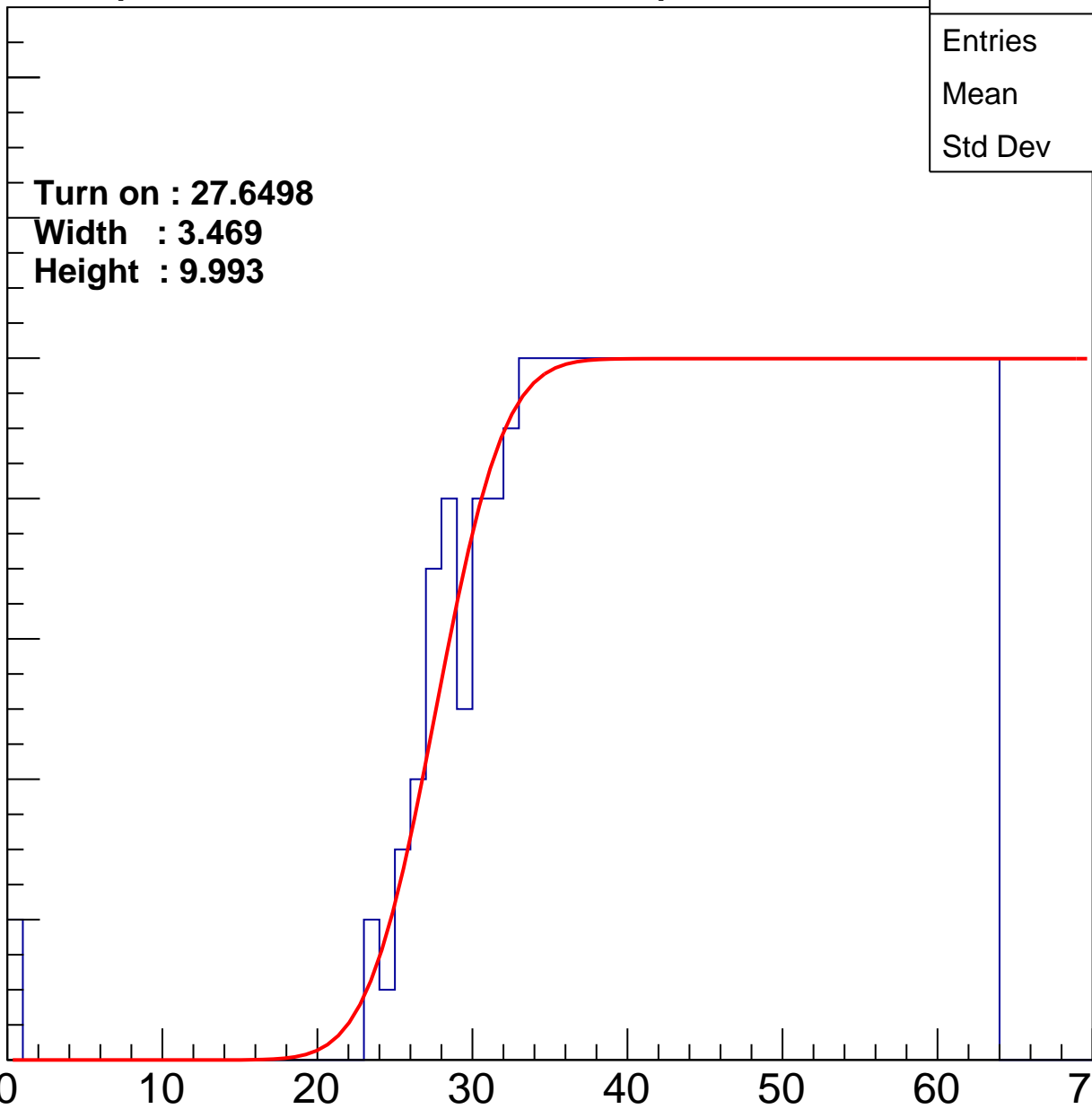
Entry

14
12
10
8
6
4
2
0

Turn on : 27.6498
Width : 3.469
Height : 9.993

Entries	367
Mean	44.86
Std Dev	11.25

ampl



B1L100S, U25-ch71

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.92
Std Dev	11.98

Turn on : 26.0028

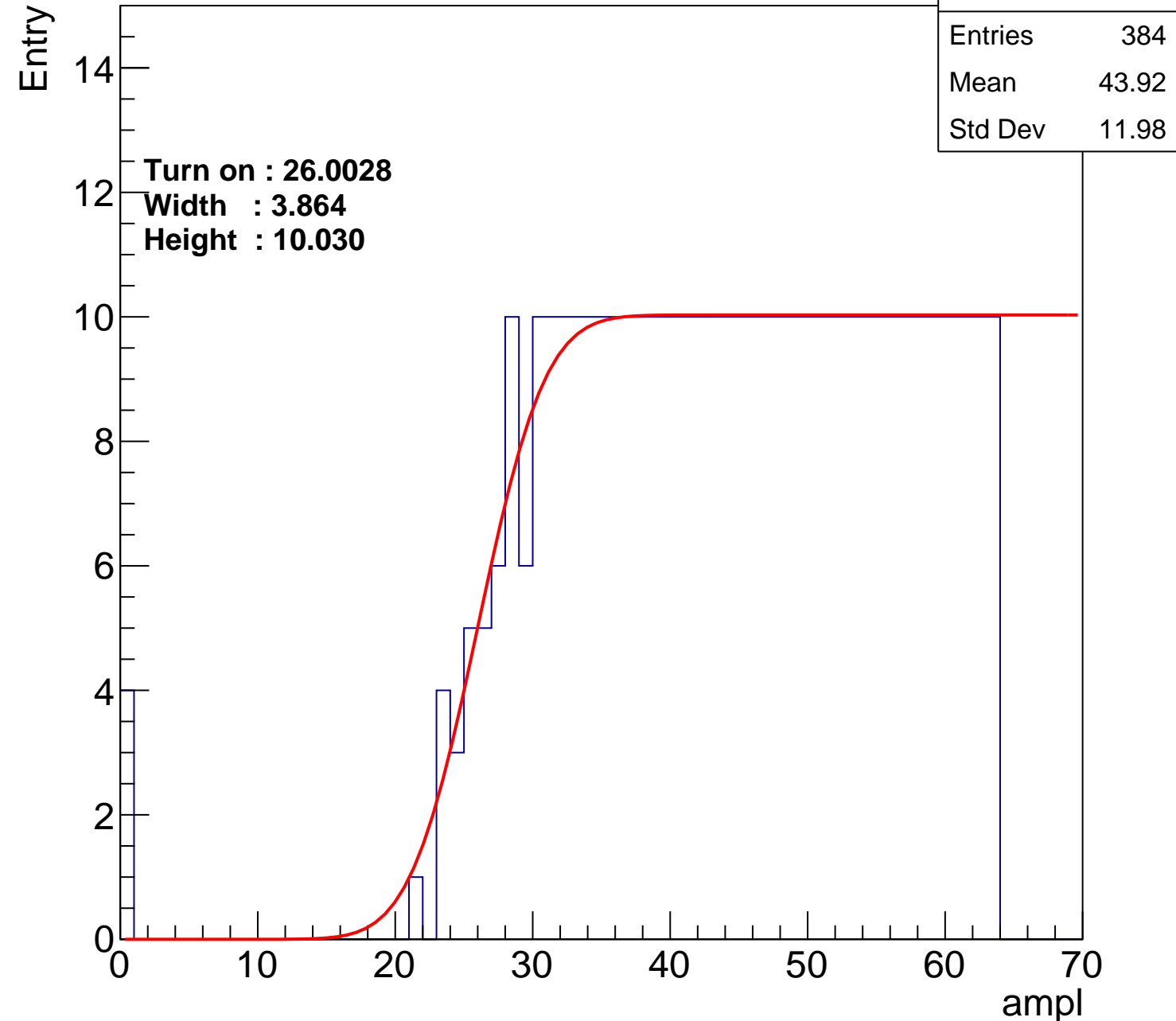
Width : 3.864

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch72

calib_packv5_042523_0143.root, FC#4, port A2

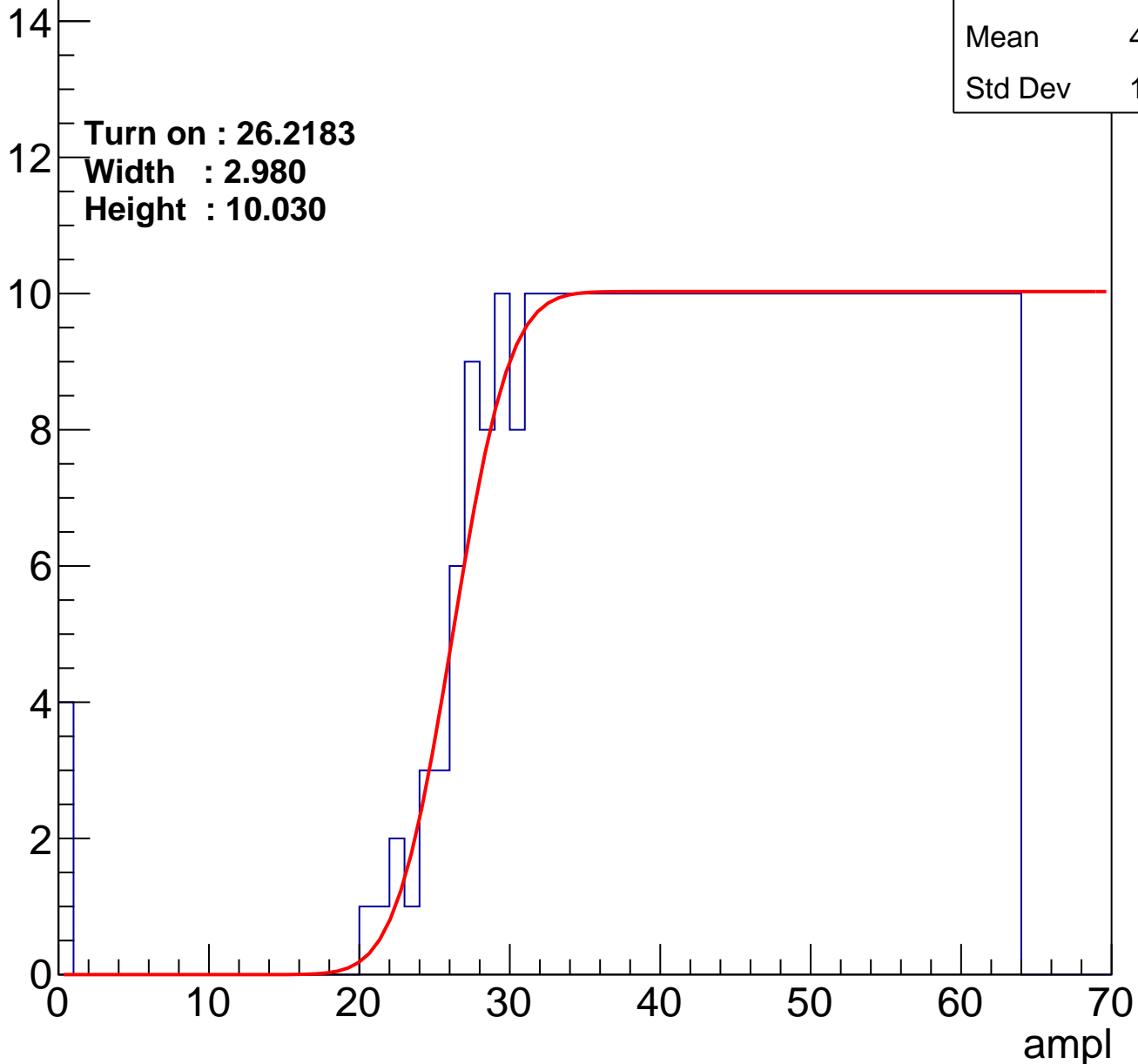
Entries	386
Mean	43.83
Std Dev	12.03

Turn on : 26.2183

Width : 2.980

Height : 10.030

Entry



B1L100S, U25-ch73

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.6
Std Dev	10.6

Turn on : 29.1525

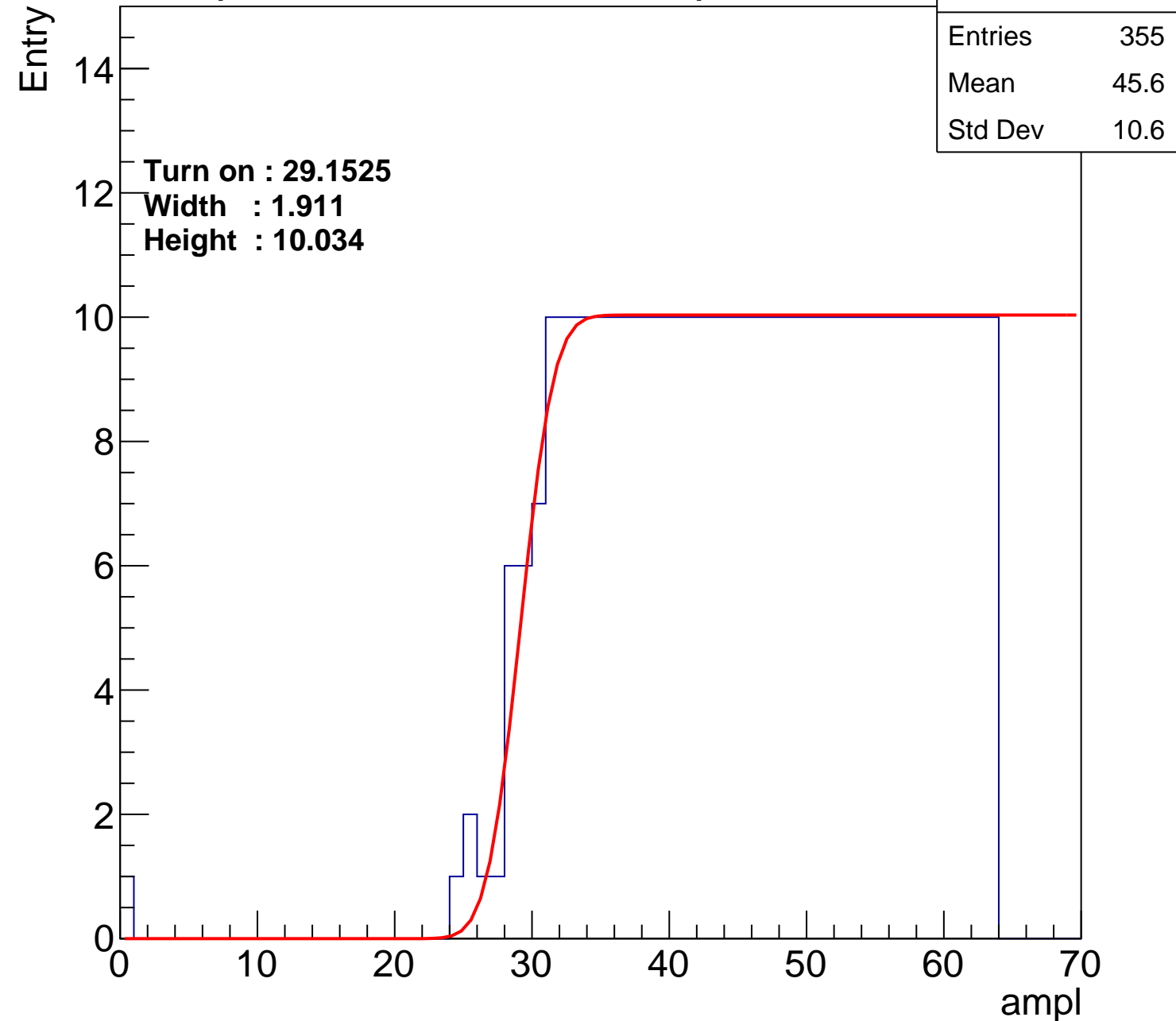
Width : 1.911

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch74

calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	45.16
Std Dev	11.28

Turn on : 28.2050

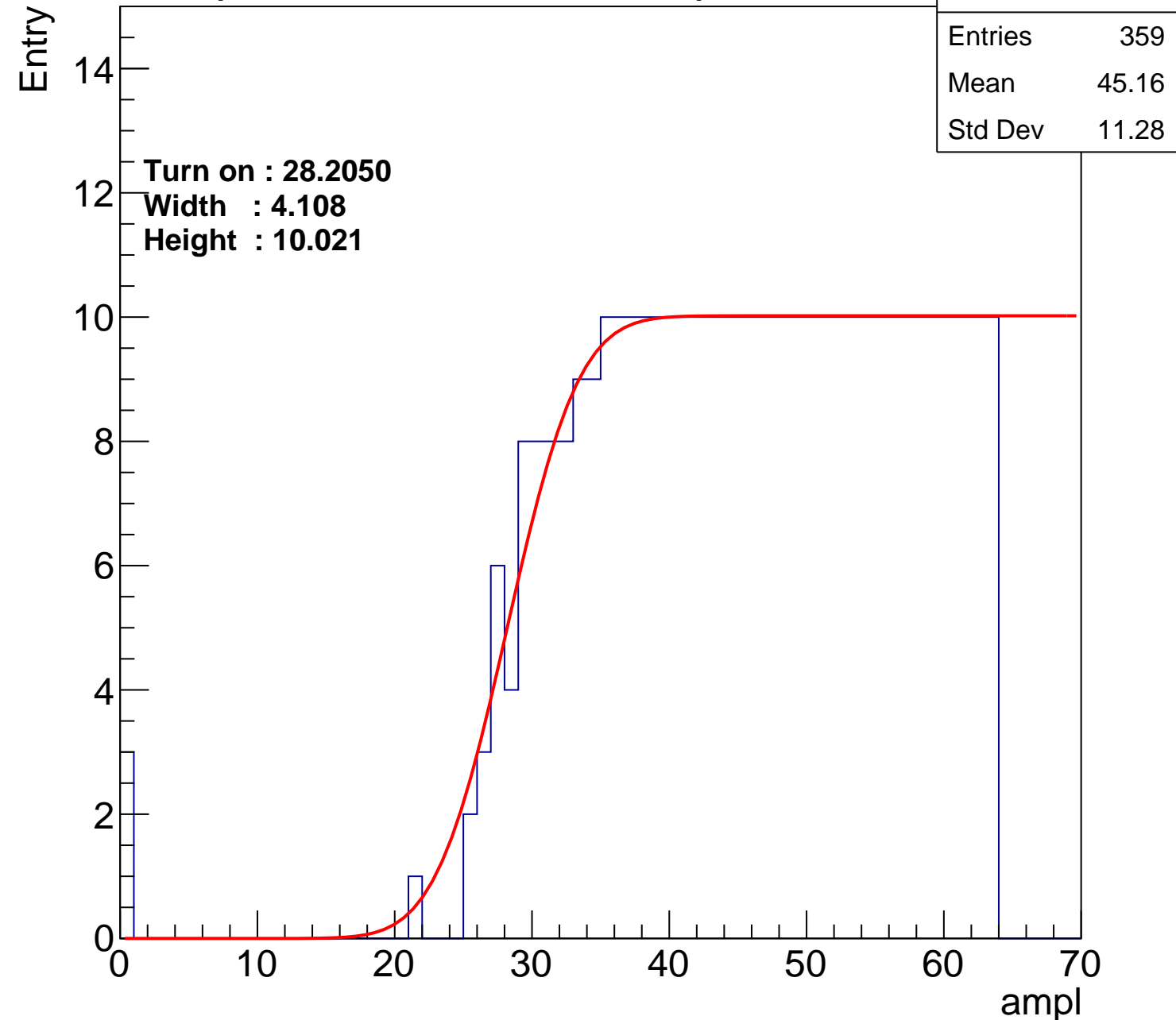
Width : 4.108

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch75

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 26.4226

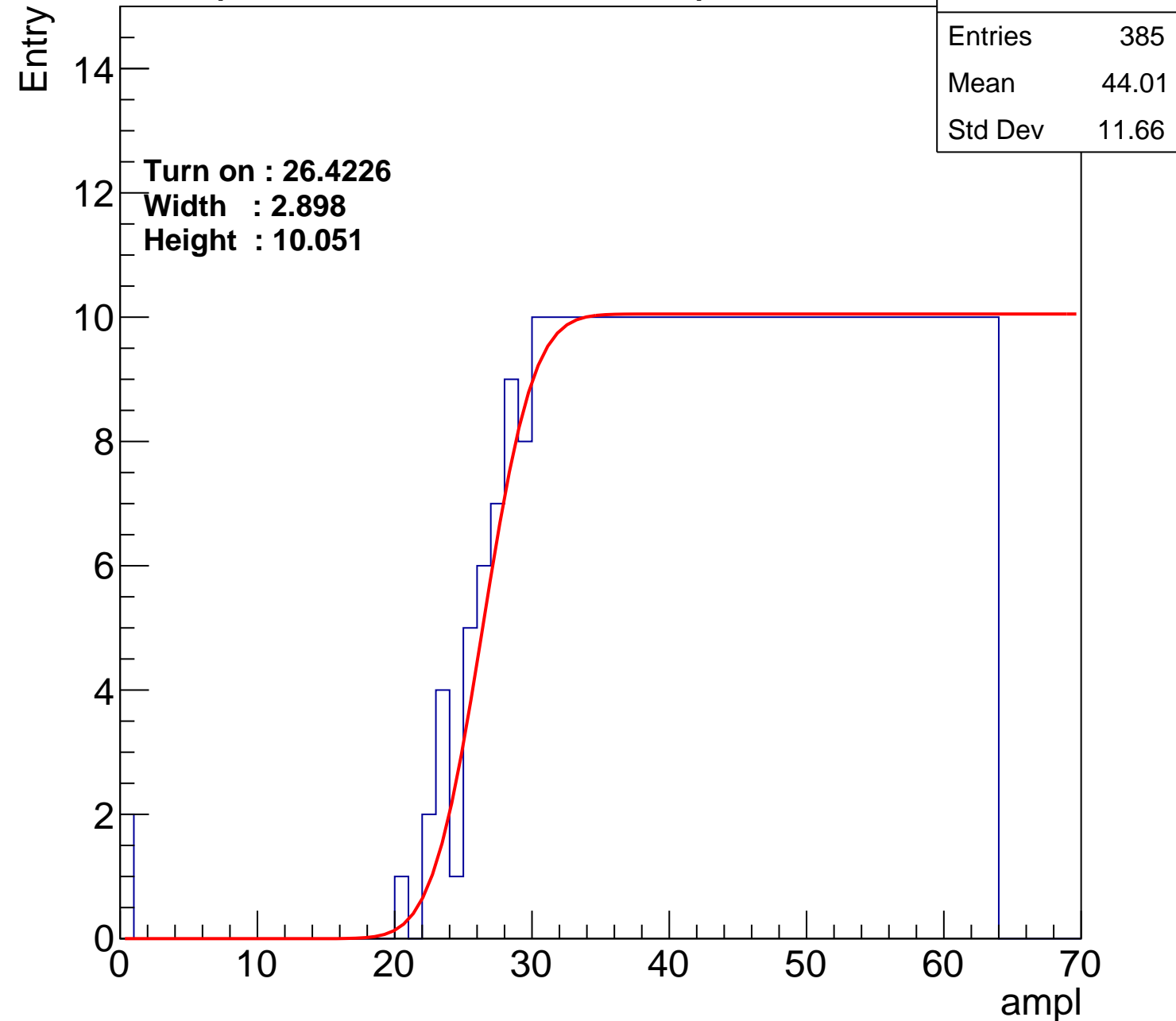
Width : 2.898

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch76

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.57
Std Dev	11.53

Turn on : 27.0709

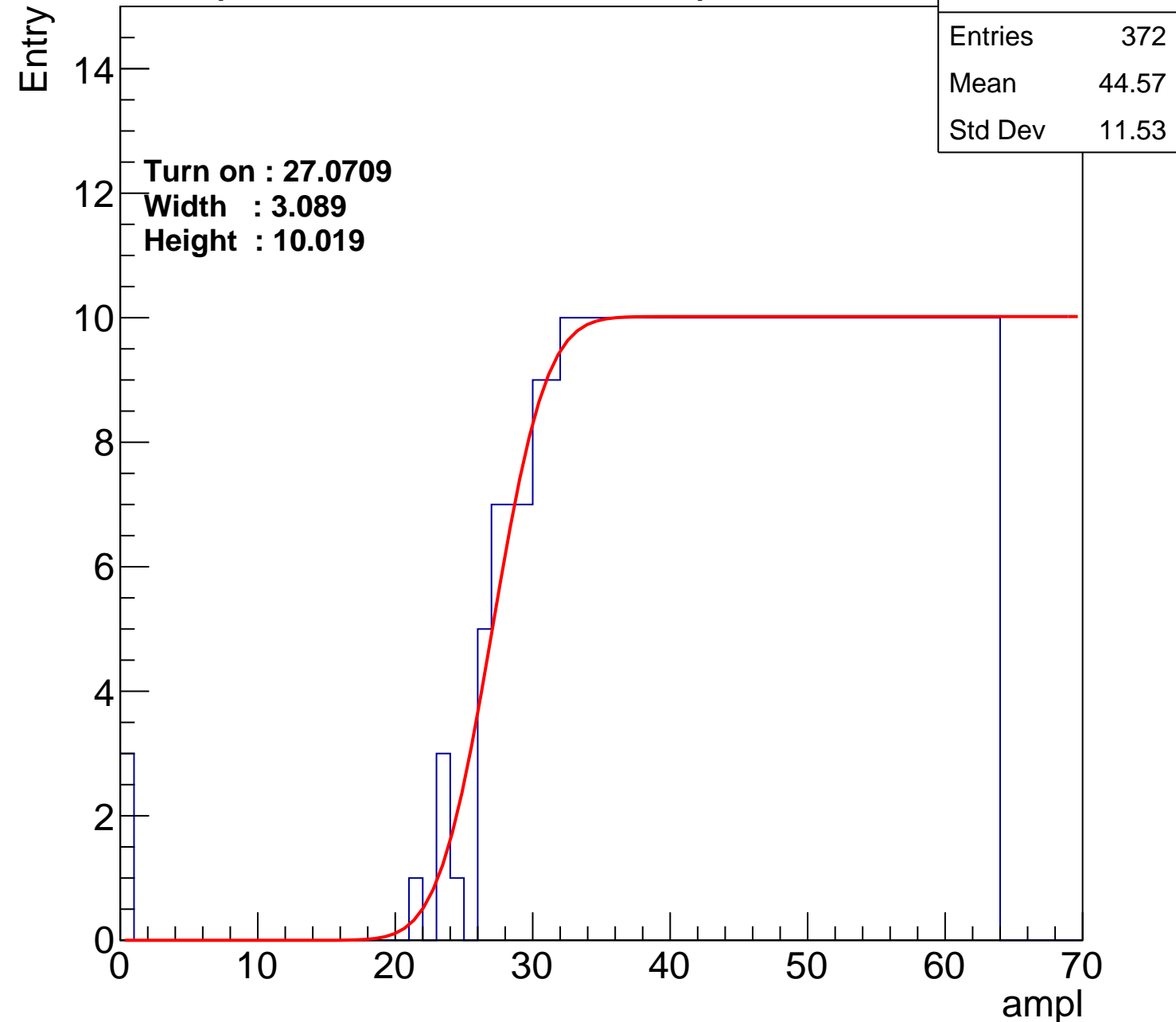
Width : 3.089

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch77

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.5772

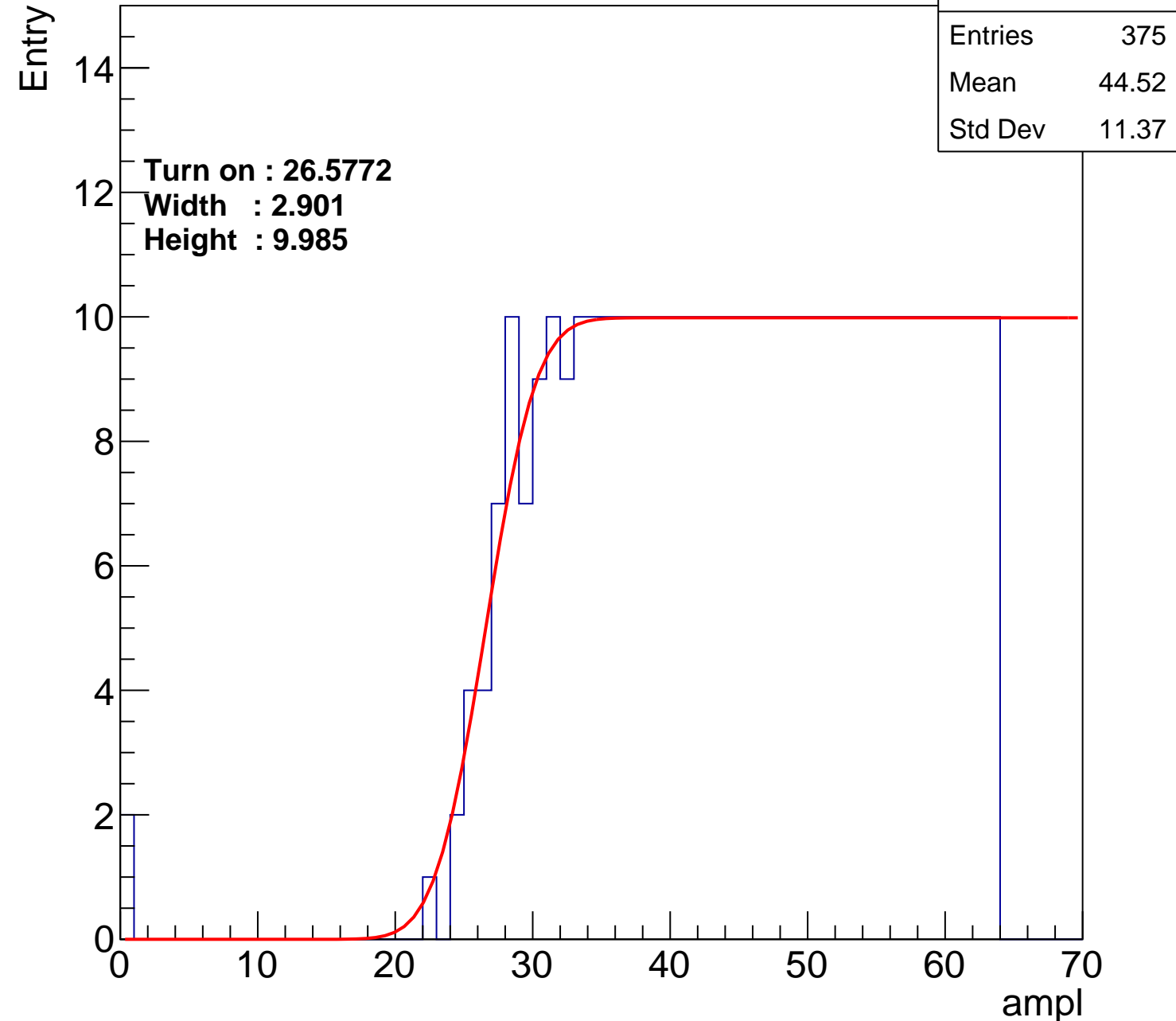
Width : 2.901

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch78

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.32
Std Dev	11.53

Turn on : 26.7609

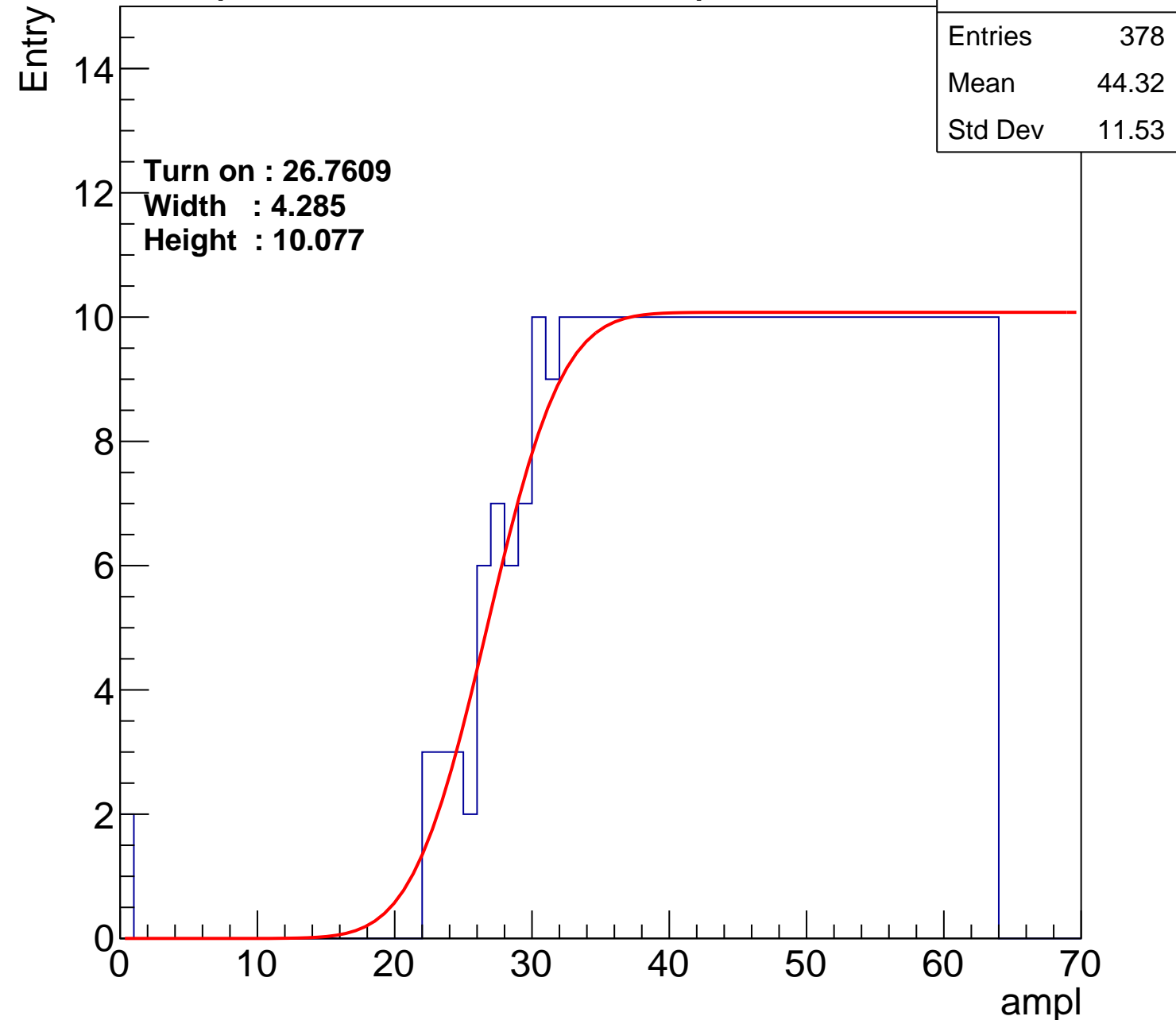
Width : 4.285

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch79

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.63
Std Dev	11.31

Turn on : 26.6345

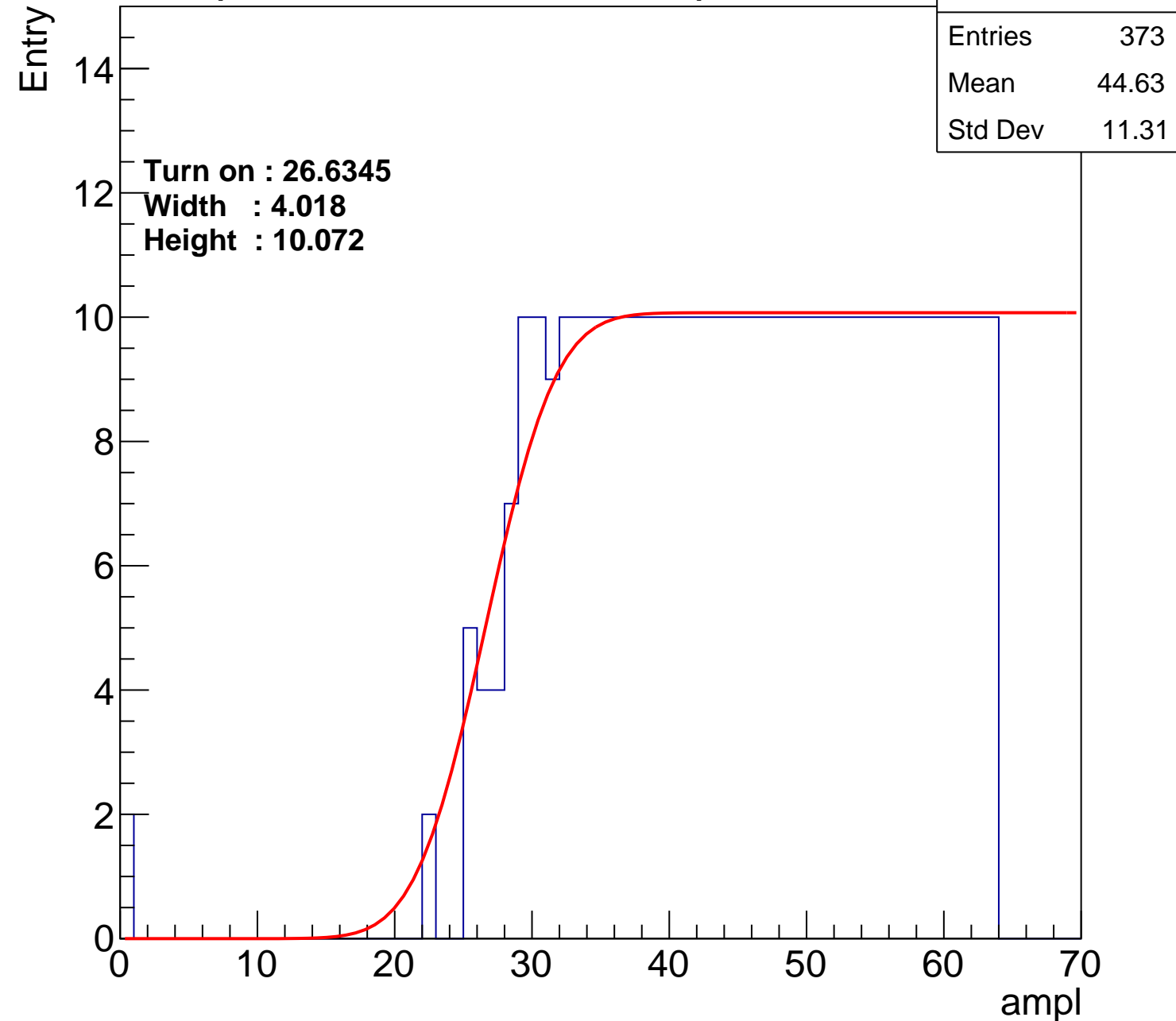
Width : 4.018

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch80

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.21
Std Dev	11.85

Turn on : 27.2342

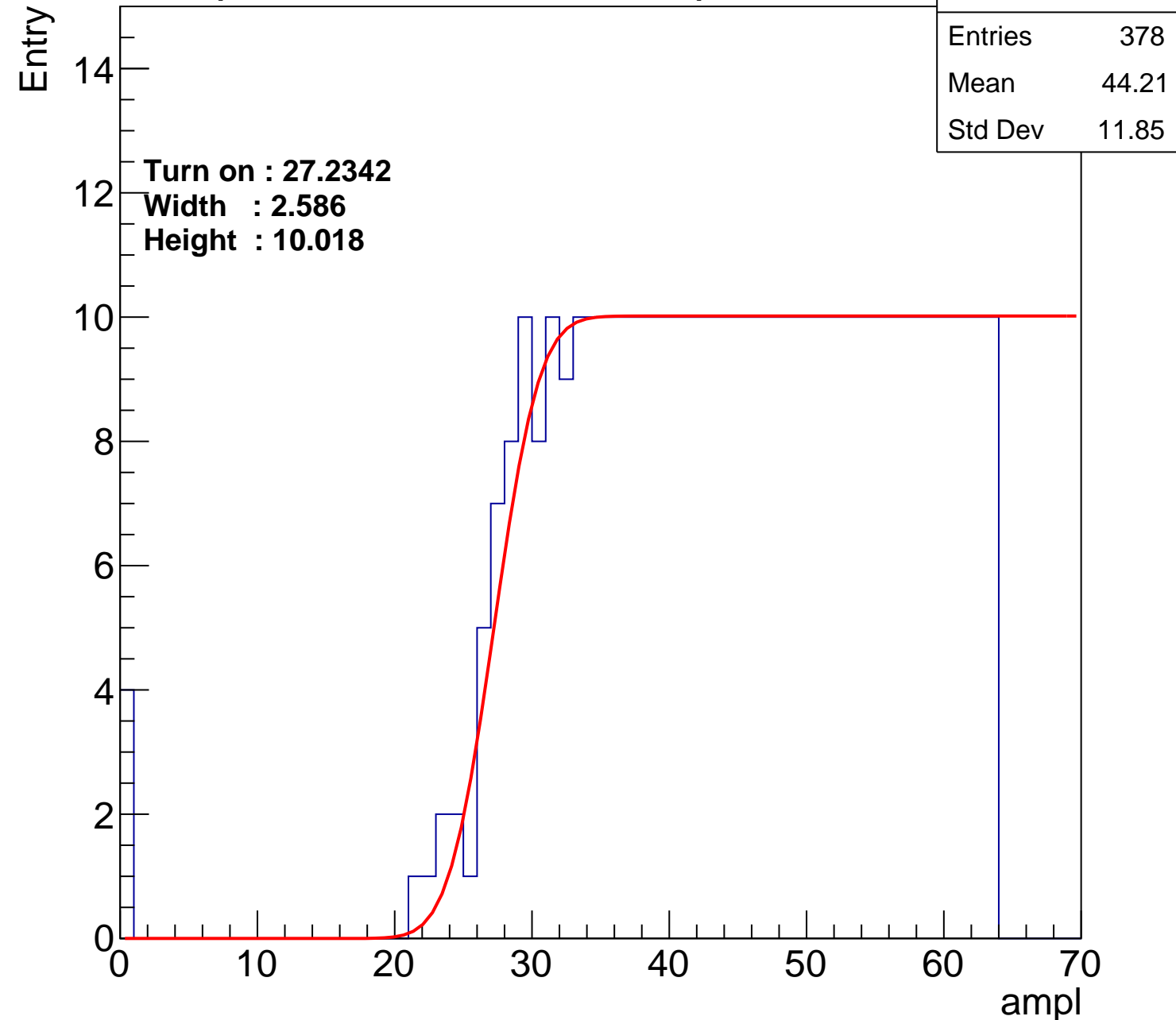
Width : 2.586

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch81

calib_packv5_042523_0143.root, FC#4, port A2

Entries	398
Mean	43.35
Std Dev	12.08

Turn on : 24.5383

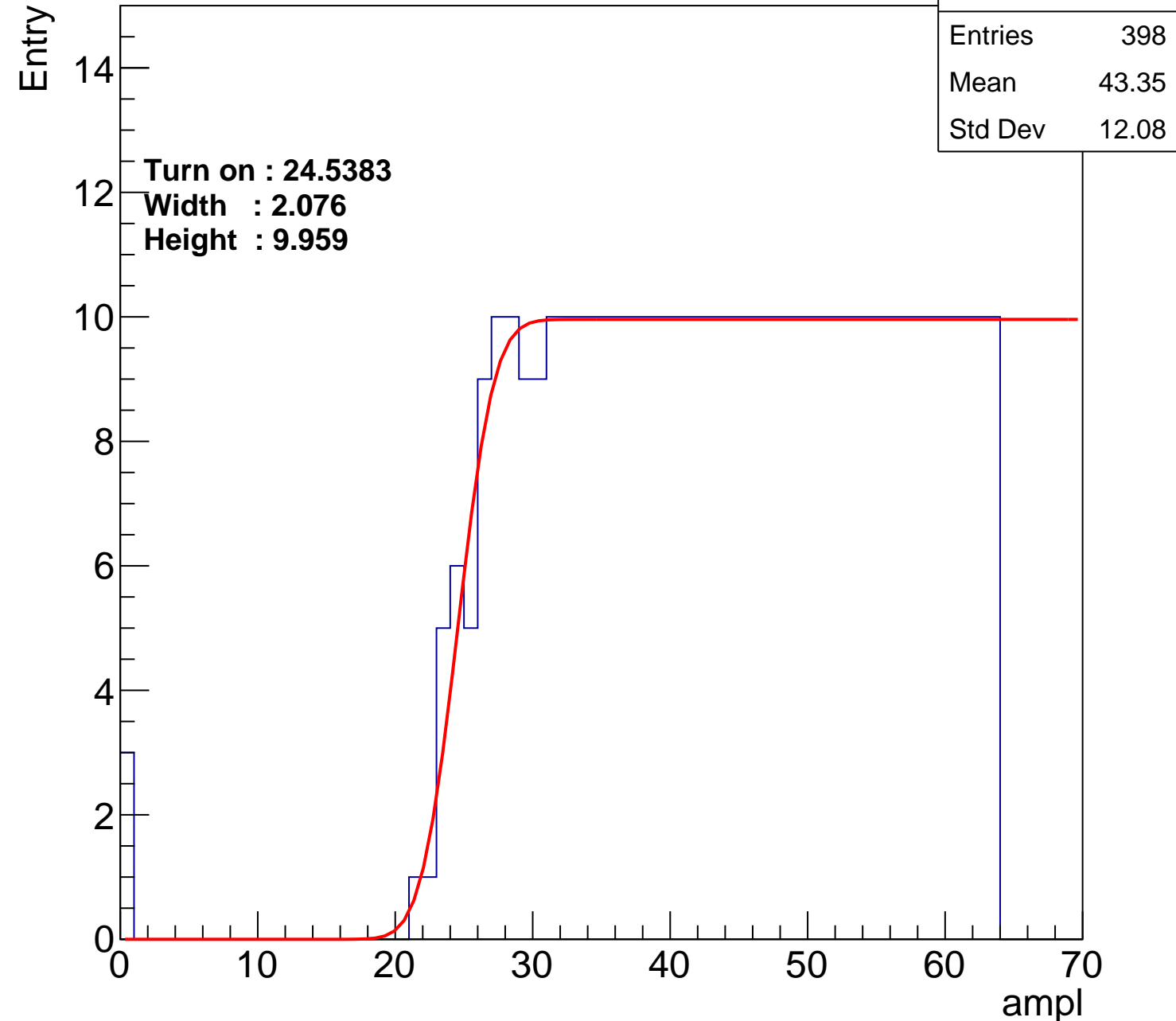
Width : 2.076

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch82

calib_packv5_042523_0143.root, FC#4, port A2

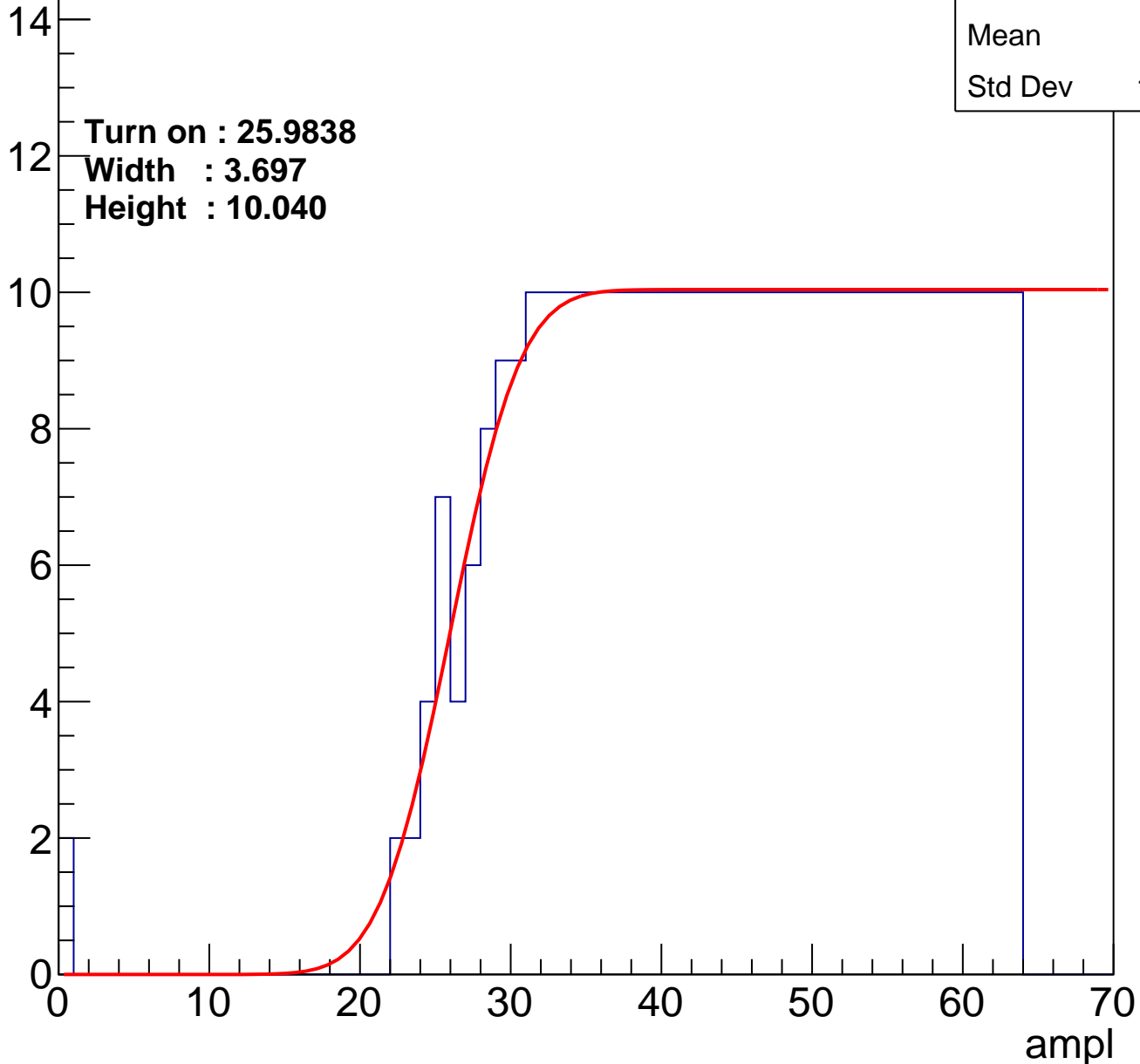
Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 25.9838

Width : 3.697

Height : 10.040

Entry



B1L100S, U25-ch83

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.62
Std Dev	11.96

Turn on : 25.0468

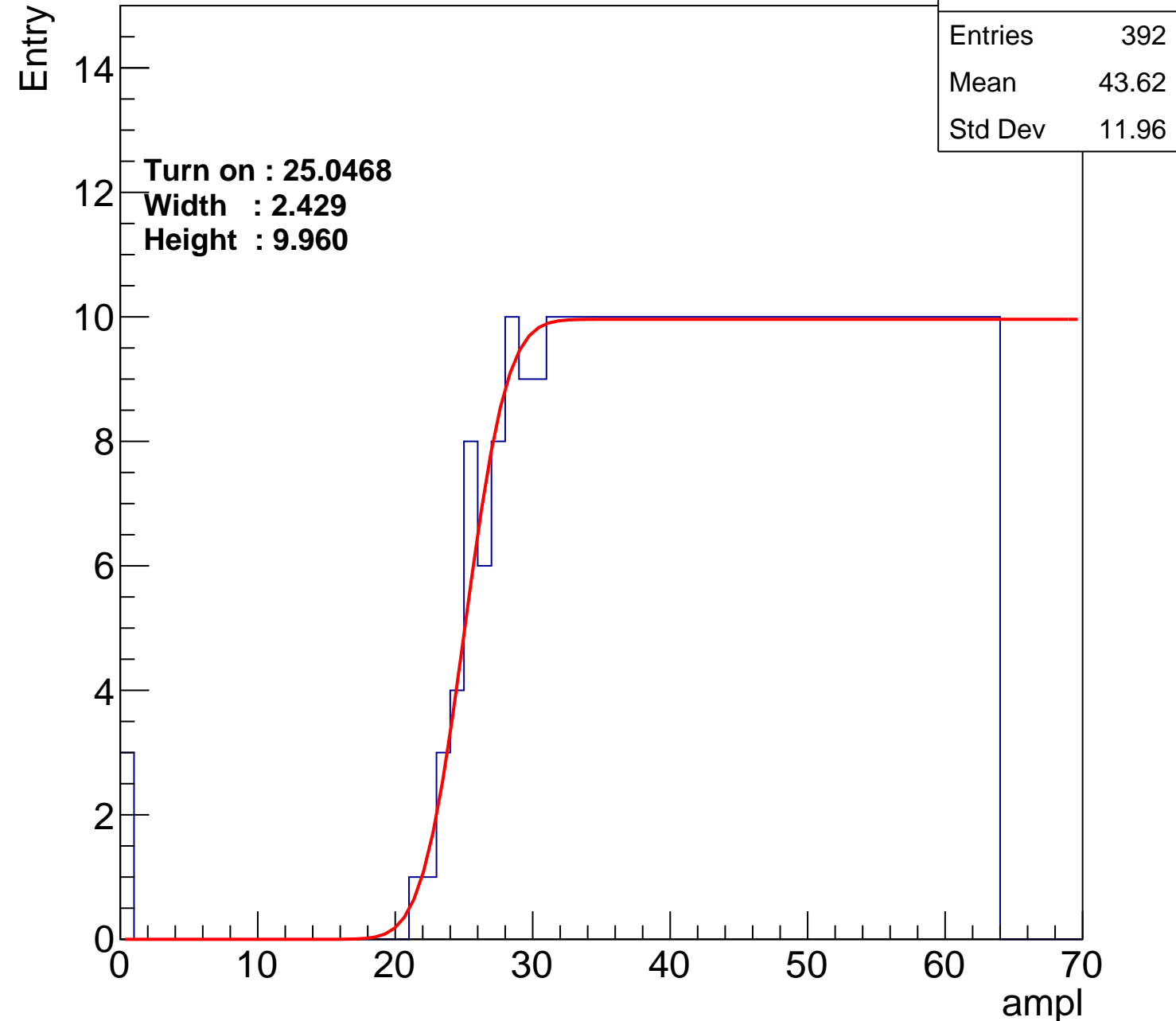
Width : 2.429

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch84

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.66
Std Dev	11.36

Turn on : 27.1699

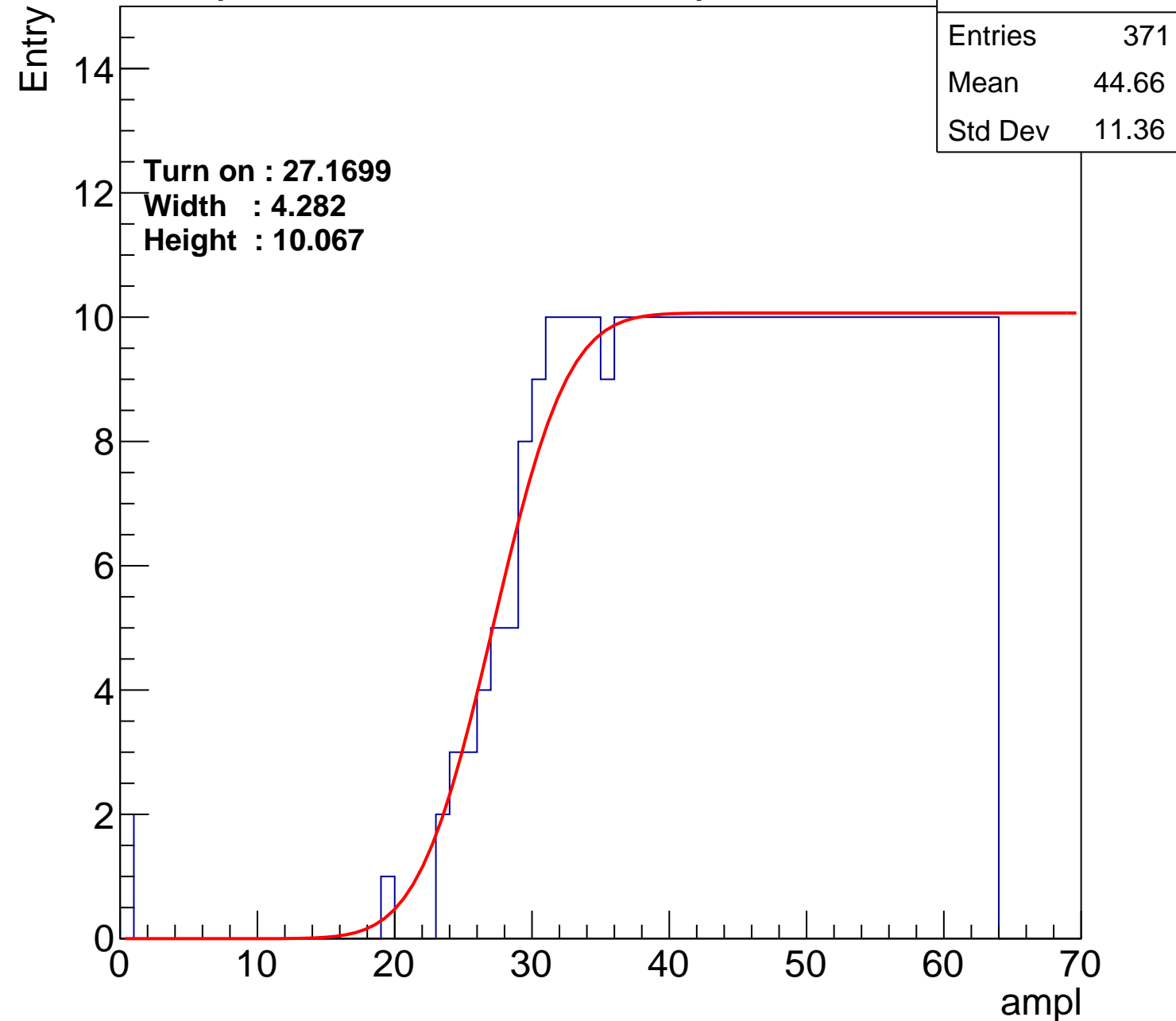
Width : 4.282

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch85

calib_packv5_042523_0143.root, FC#4, port A2

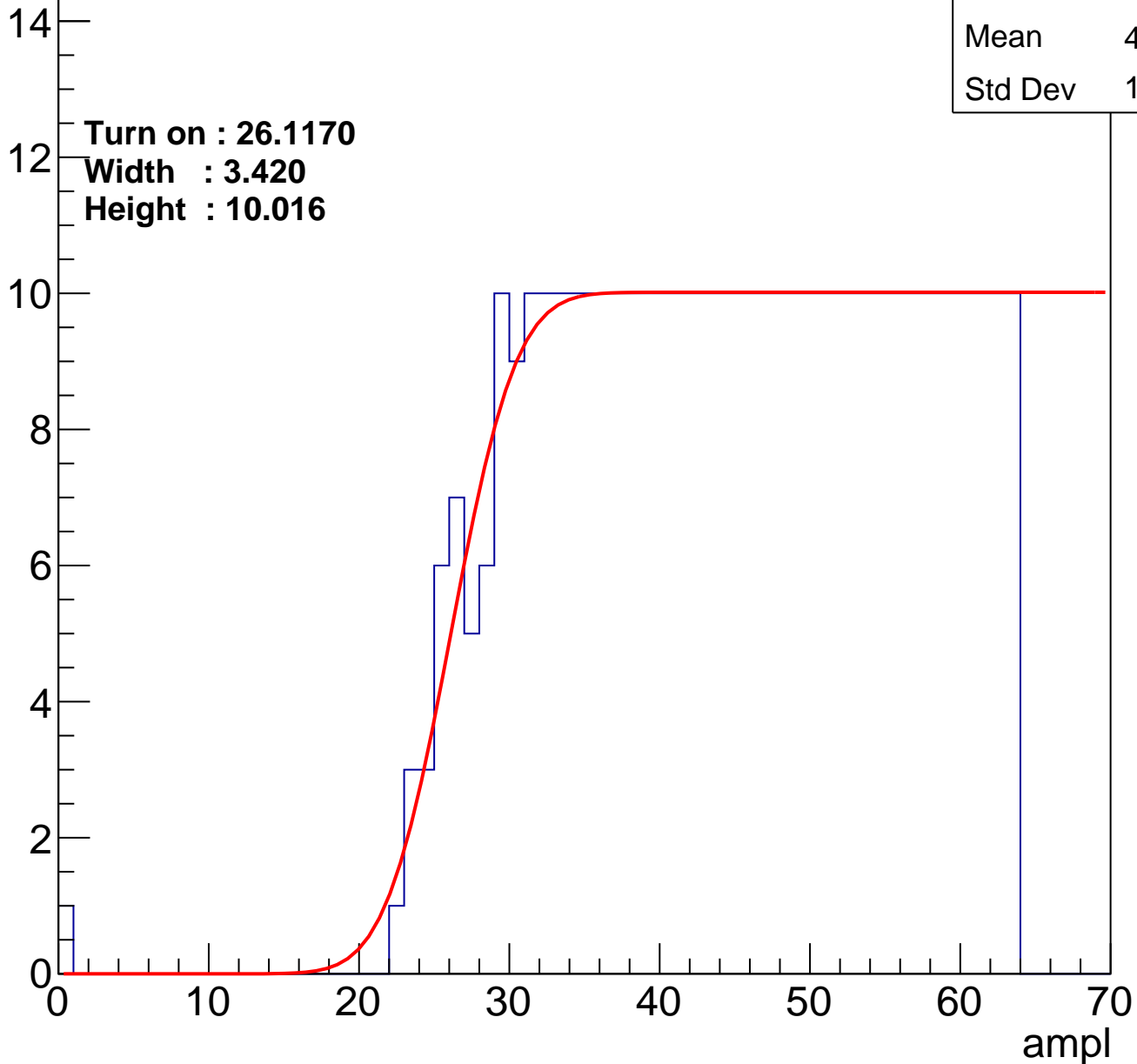
Entries	381
Mean	44.27
Std Dev	11.37

Turn on : 26.1170

Width : 3.420

Height : 10.016

Entry



B1L100S, U25-ch86

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.62
Std Dev	11.68

Turn on : 24.3732

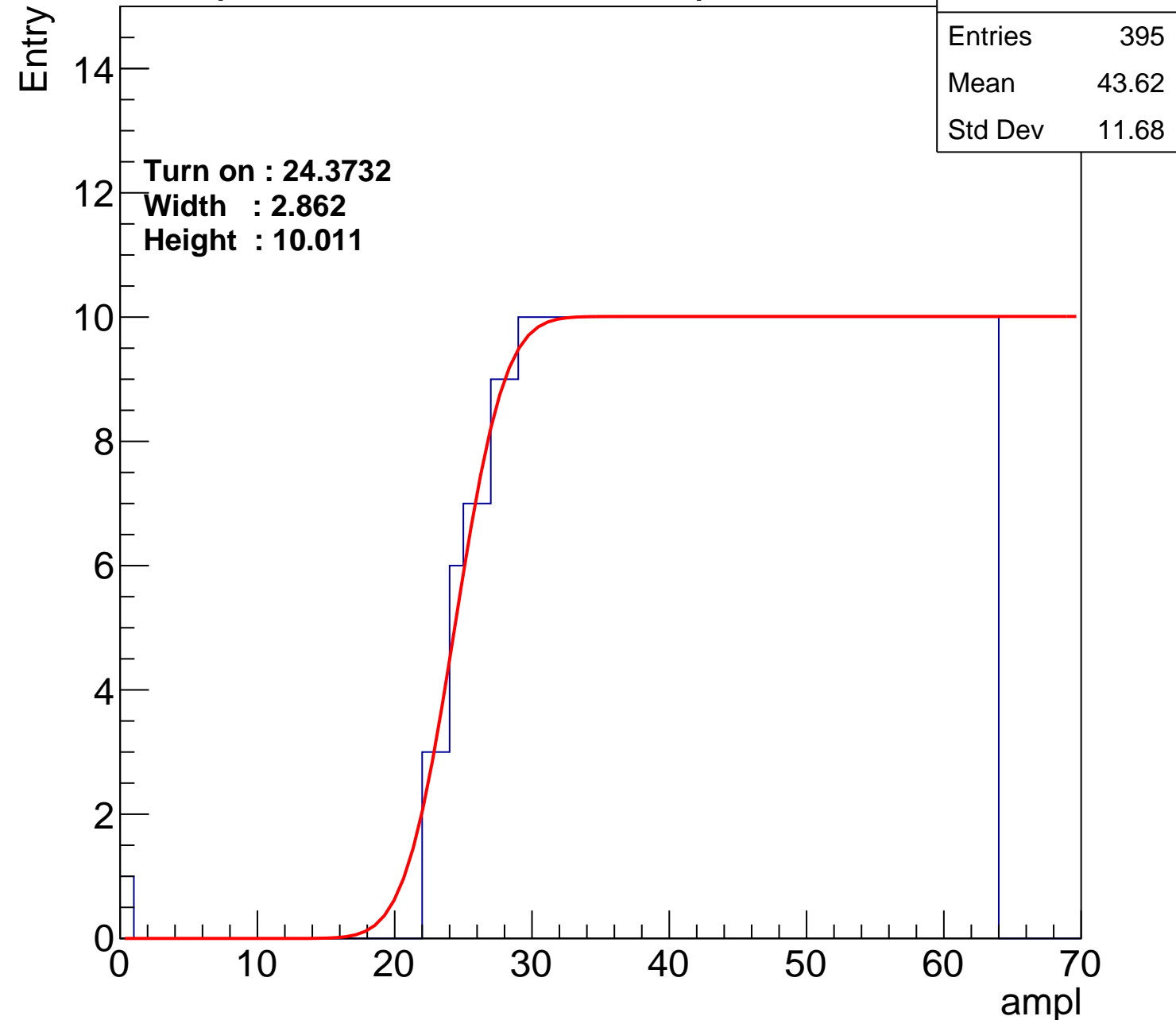
Width : 2.862

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch87

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.75
Std Dev	11.4

Turn on : 28.0070

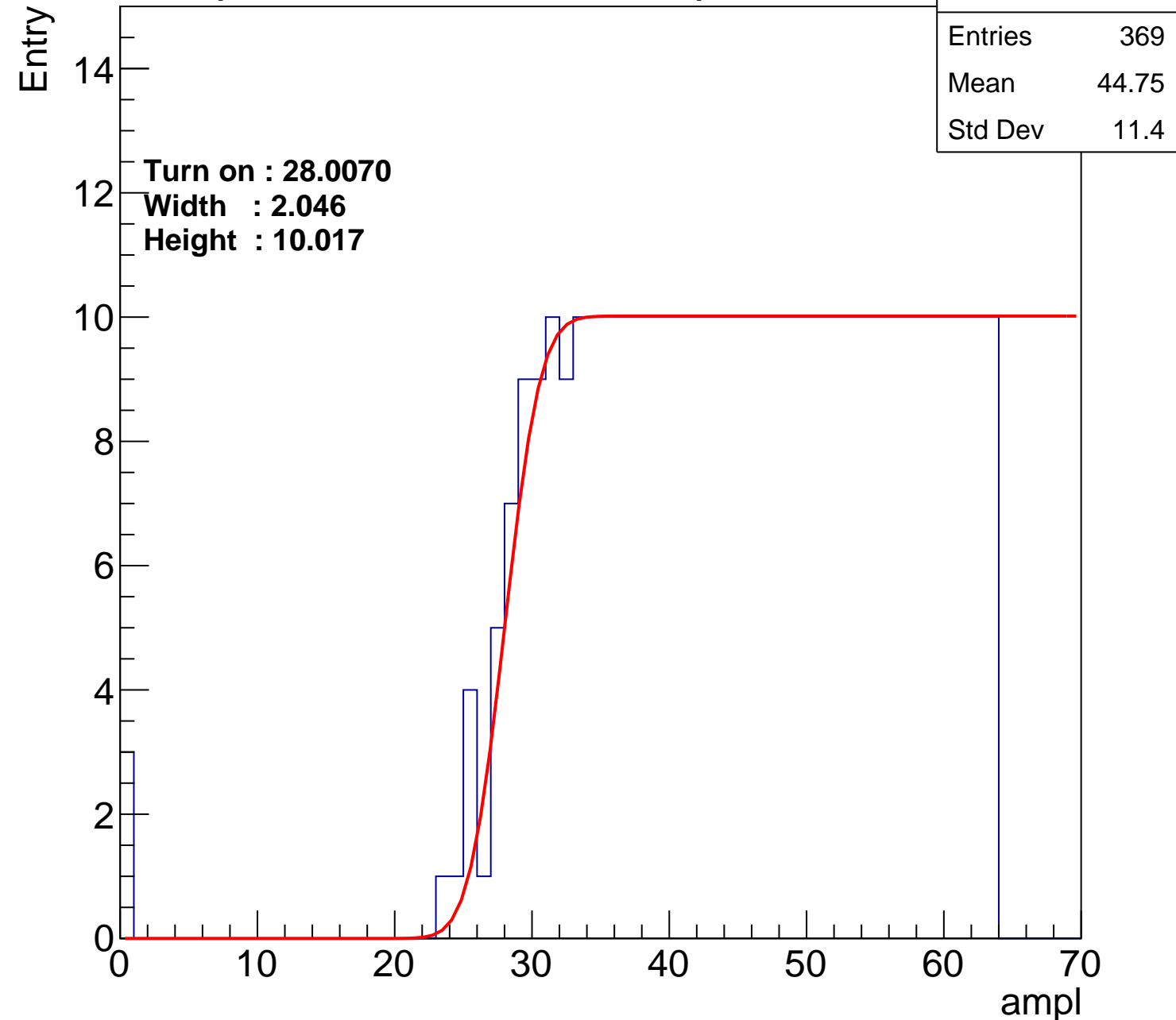
Width : 2.046

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch88

calib_packv5_042523_0143.root, FC#4, port A2

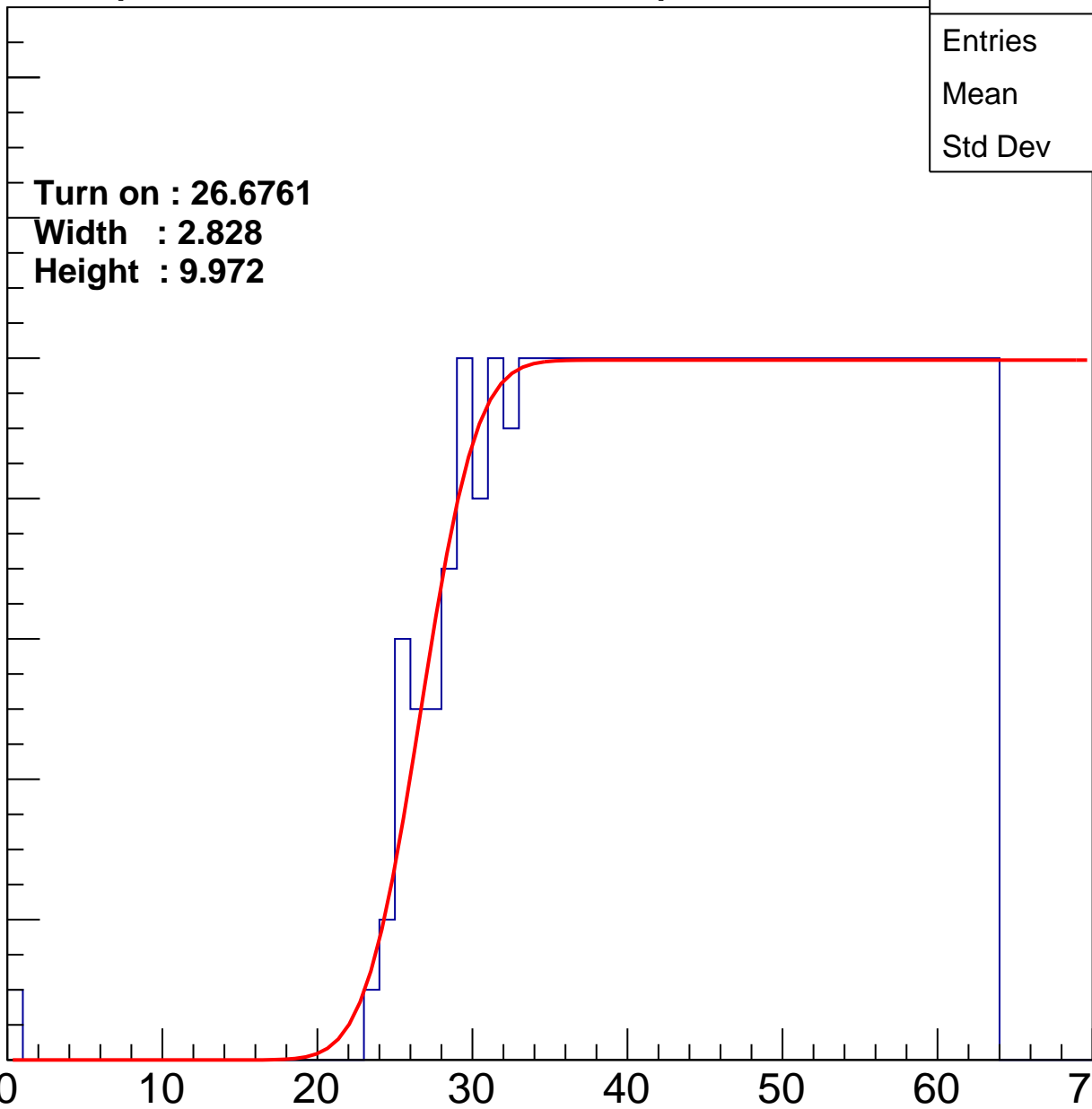
Entry

14
12
10
8
6
4
2
0

Turn on : 26.6761
Width : 2.828
Height : 9.972

Entries	374
Mean	44.63
Std Dev	11.16

ampl



B1L100S, U25-ch89

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
---------	-----

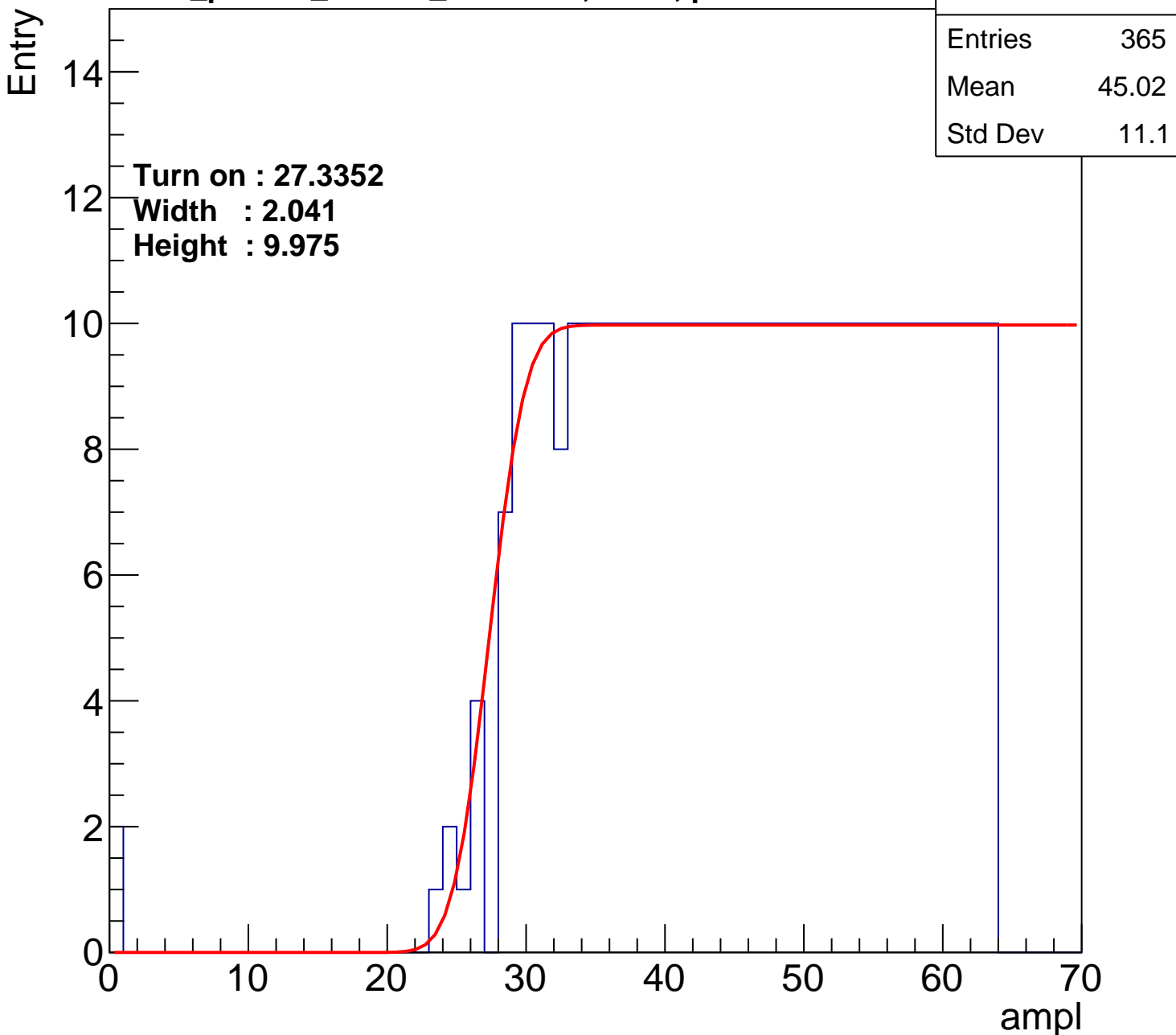
Mean	45.02
------	-------

Std Dev	11.1
---------	------

Turn on : 27.3352

Width : 2.041

Height : 9.975



B1L100S, U25-ch90

calib_packv5_042523_0143.root, FC#4, port A2

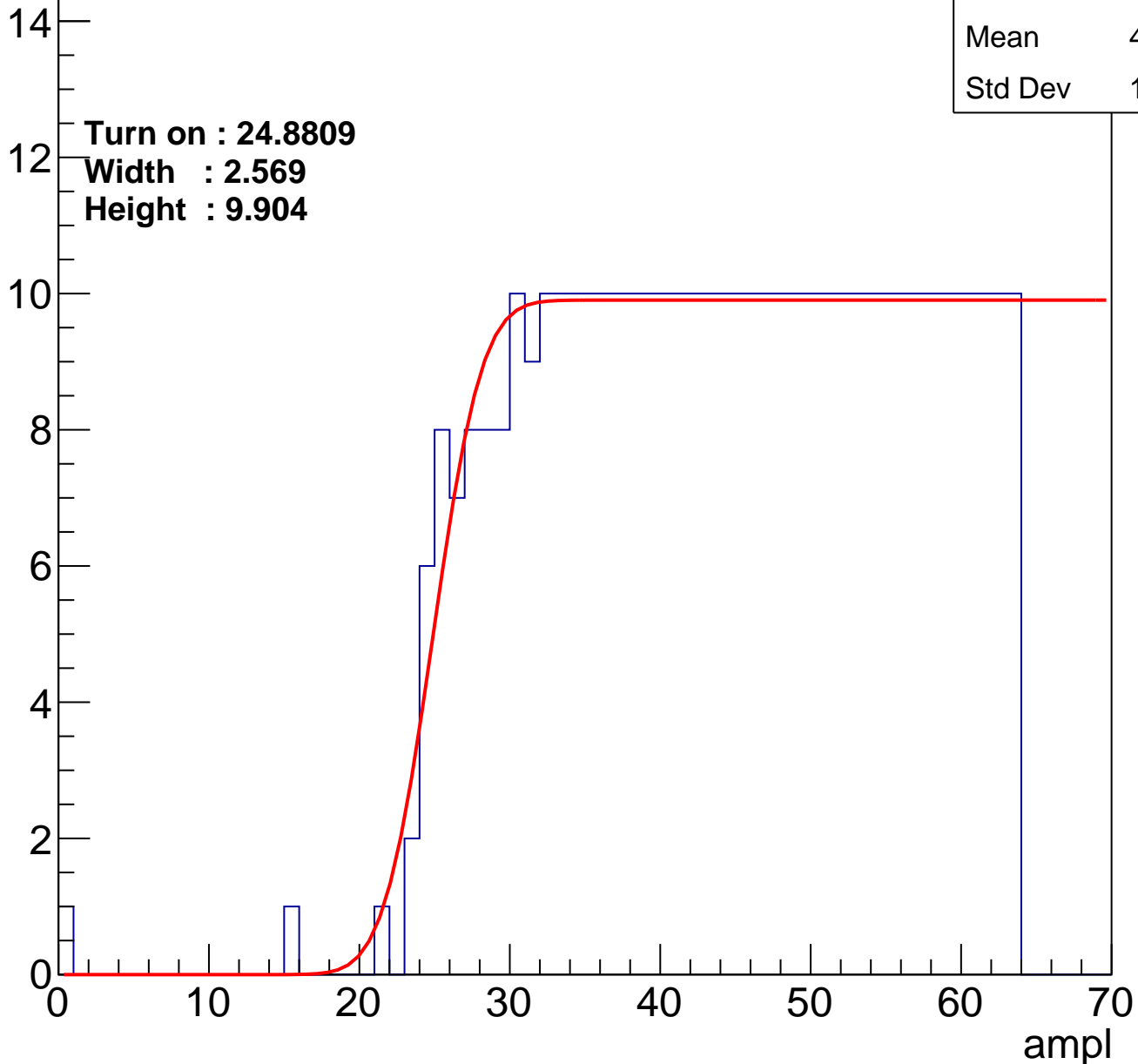
Entries	389
Mean	43.85
Std Dev	11.63

Turn on : 24.8809

Width : 2.569

Height : 9.904

Entry



B1L100S, U25-ch91

calib_packv5_042523_0143.root, FC#4, port A2

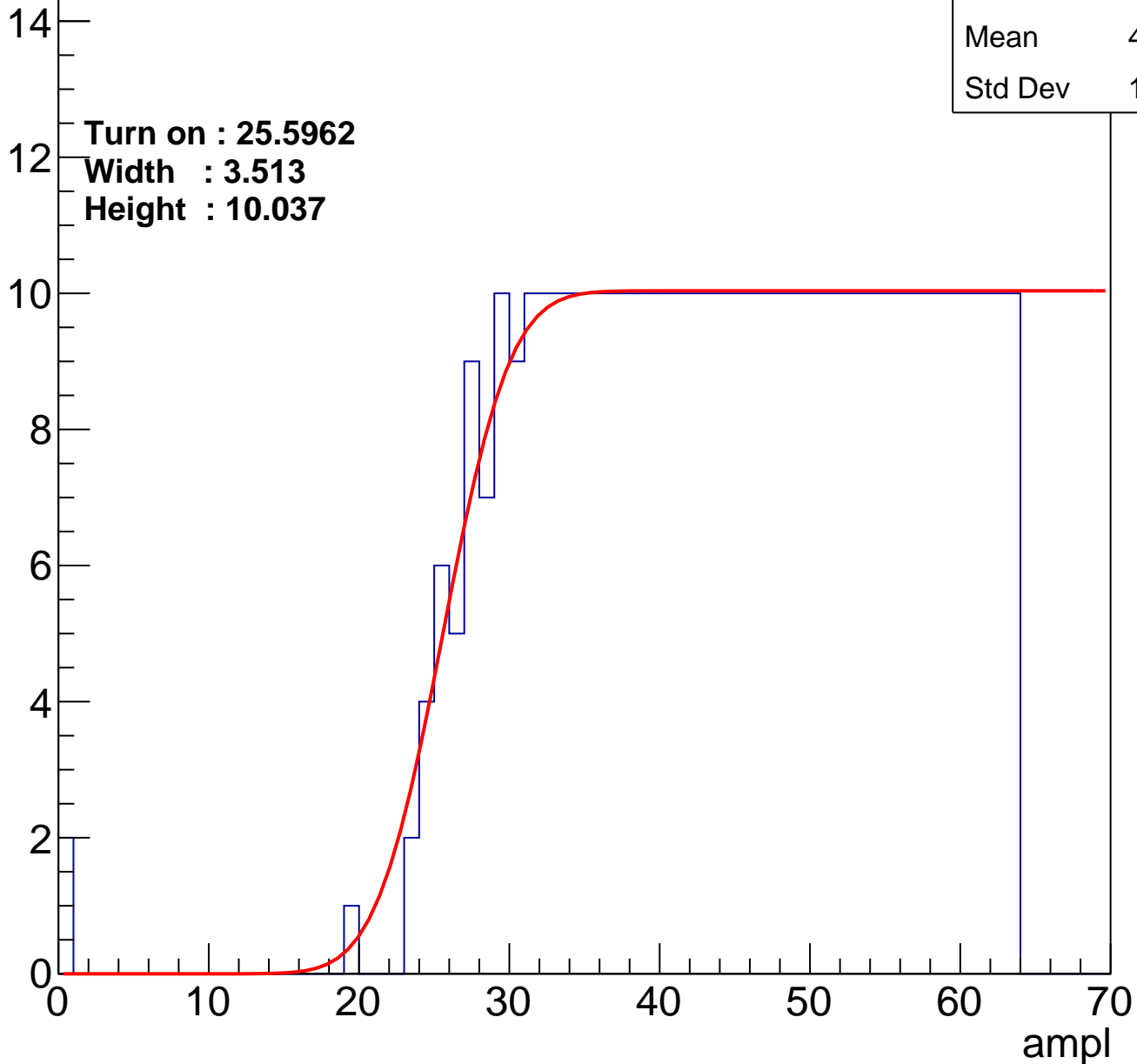
Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 25.5962

Width : 3.513

Height : 10.037

Entry



B1L100S, U25-ch92

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.43
Std Dev	12.46

Turn on : 25.8287

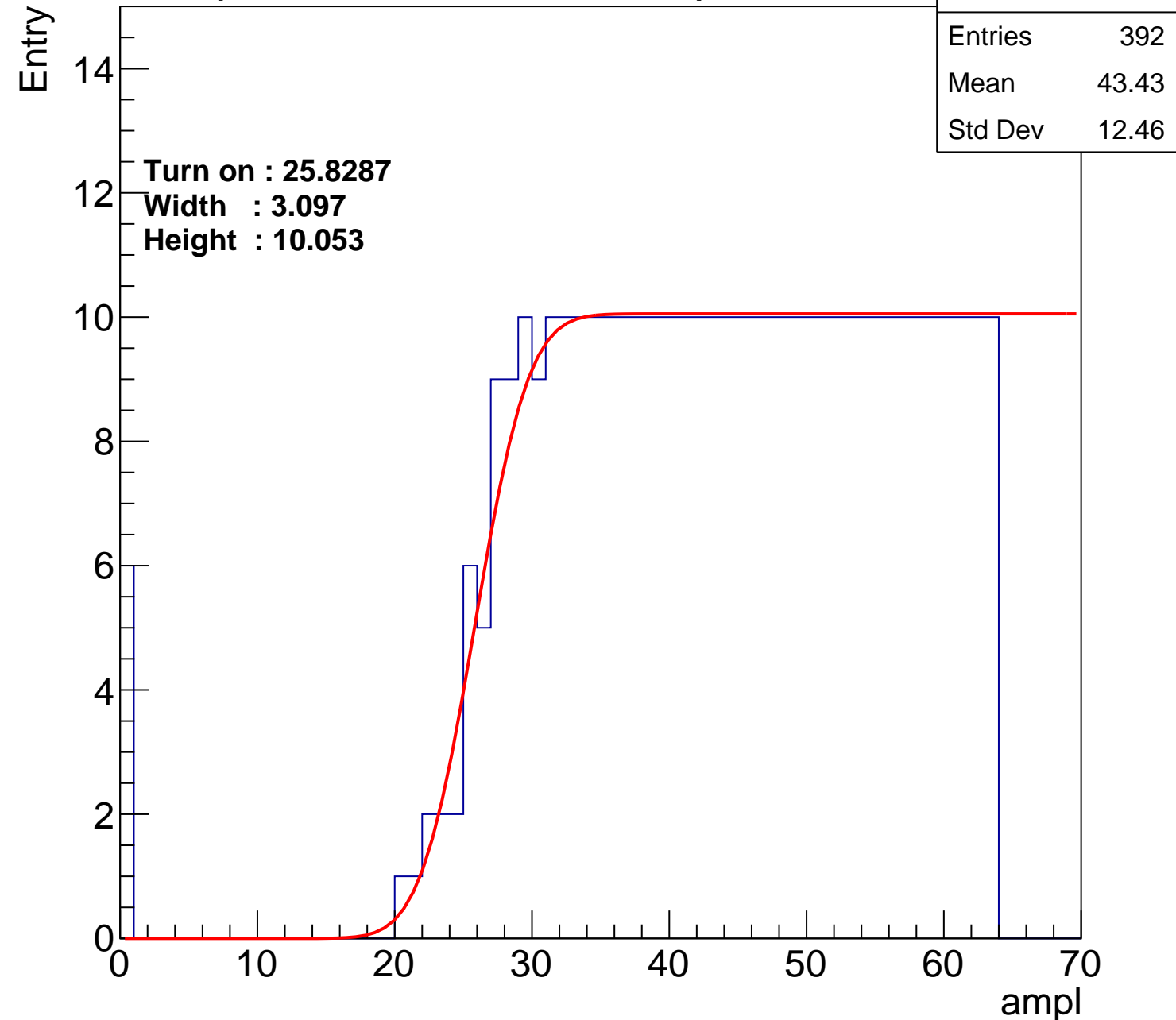
Width : 3.097

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch93

calib_packv5_042523_0143.root, FC#4, port A2

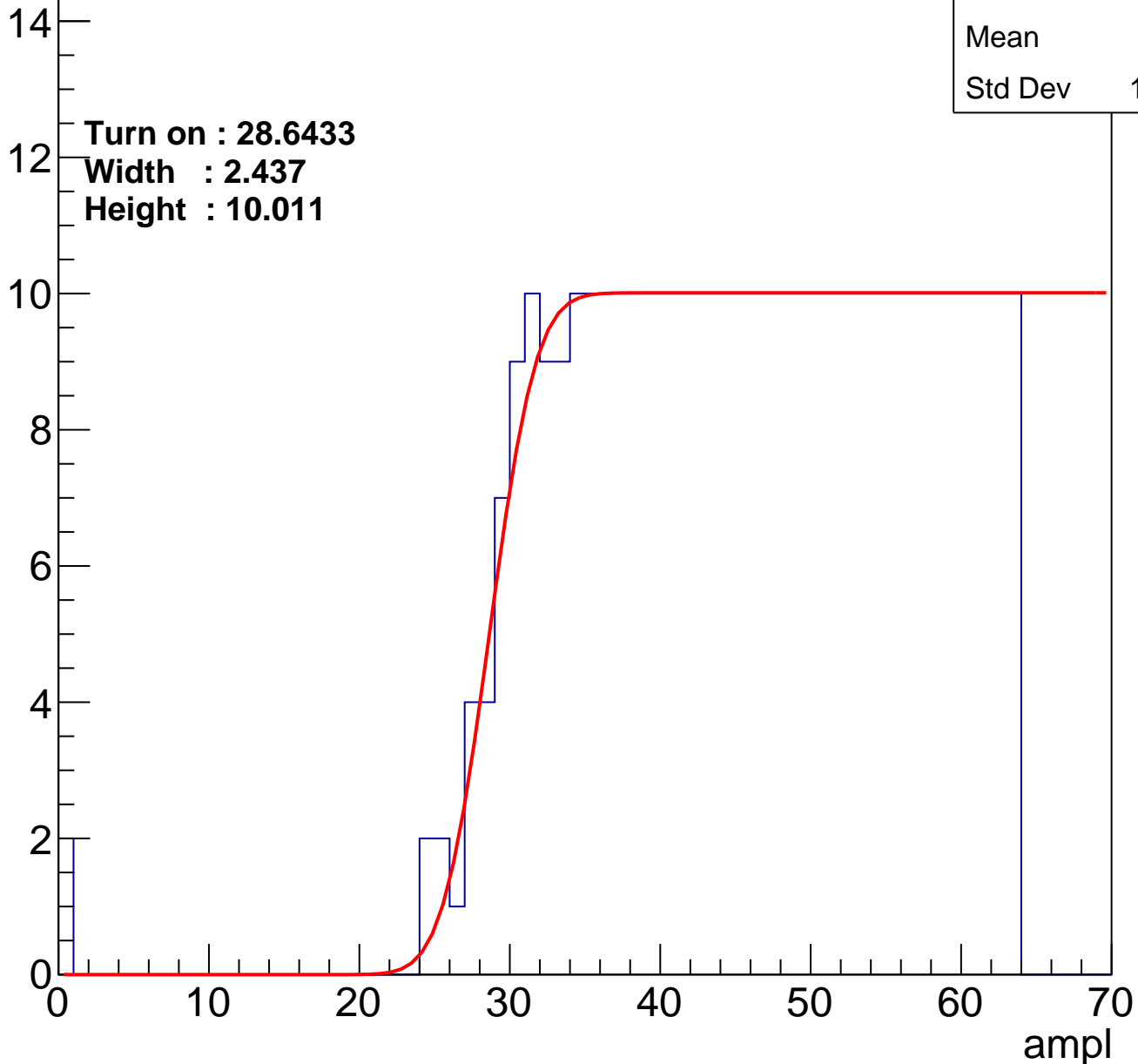
Entries	359
Mean	45.3
Std Dev	10.98

Turn on : 28.6433

Width : 2.437

Height : 10.011

Entry



B1L100S, U25-ch94

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.5913

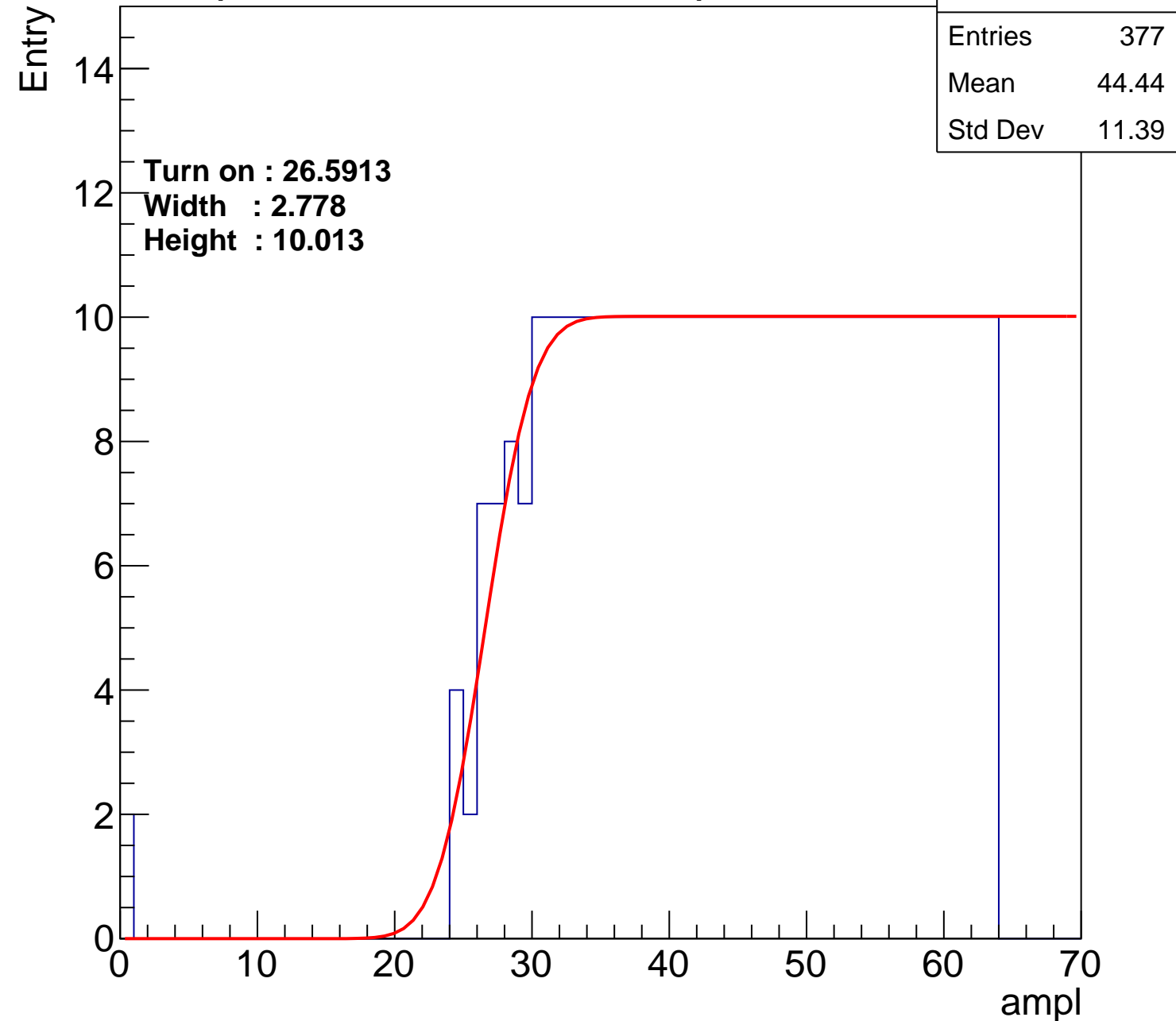
Width : 2.778

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch95

calib_packv5_042523_0143.root, FC#4, port A2

Entries	402
Mean	43.17
Std Dev	12.1

Turn on : 24.3033

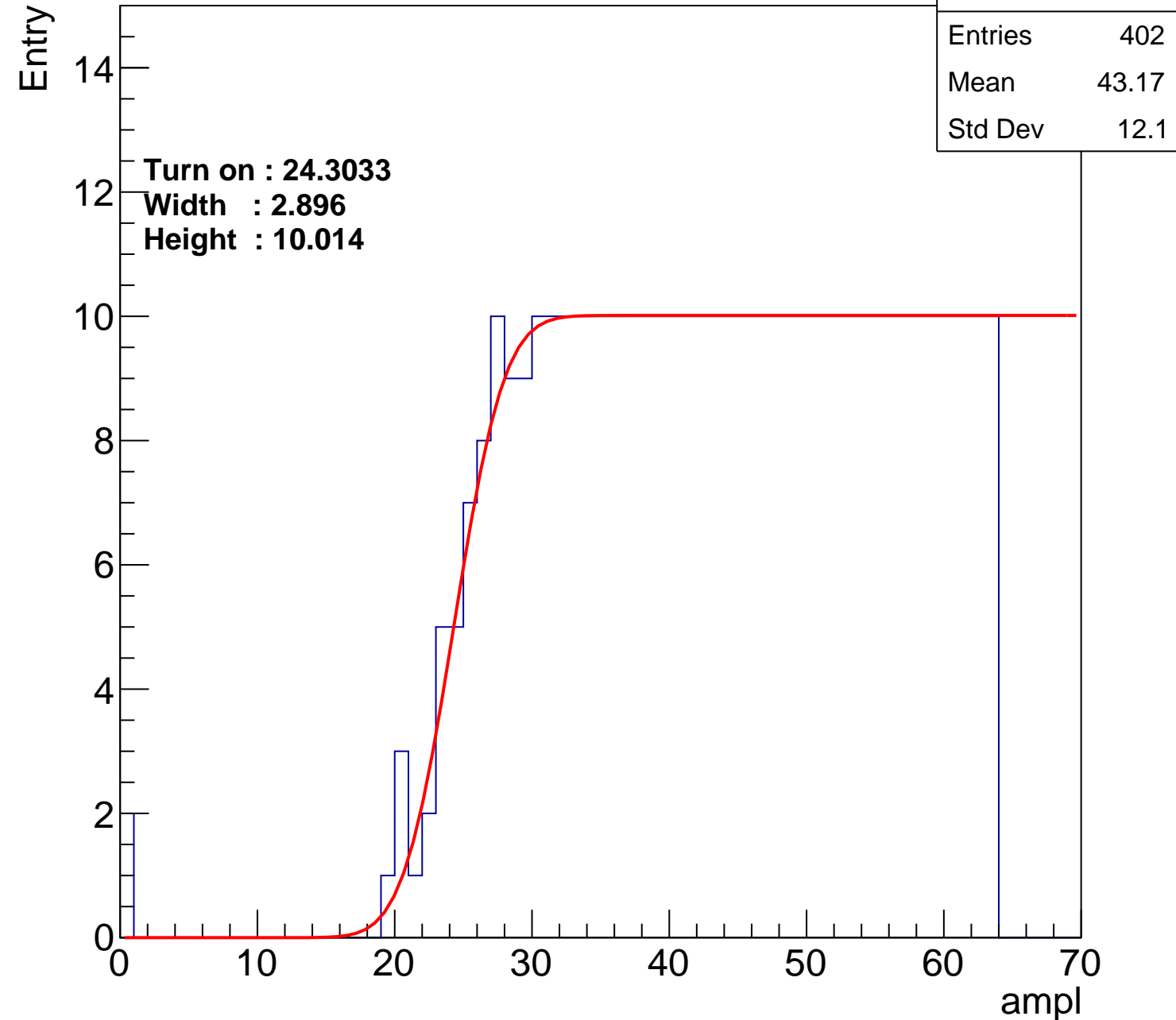
Width : 2.896

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch96

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.39
Std Dev	10.77

Turn on : 28.1843

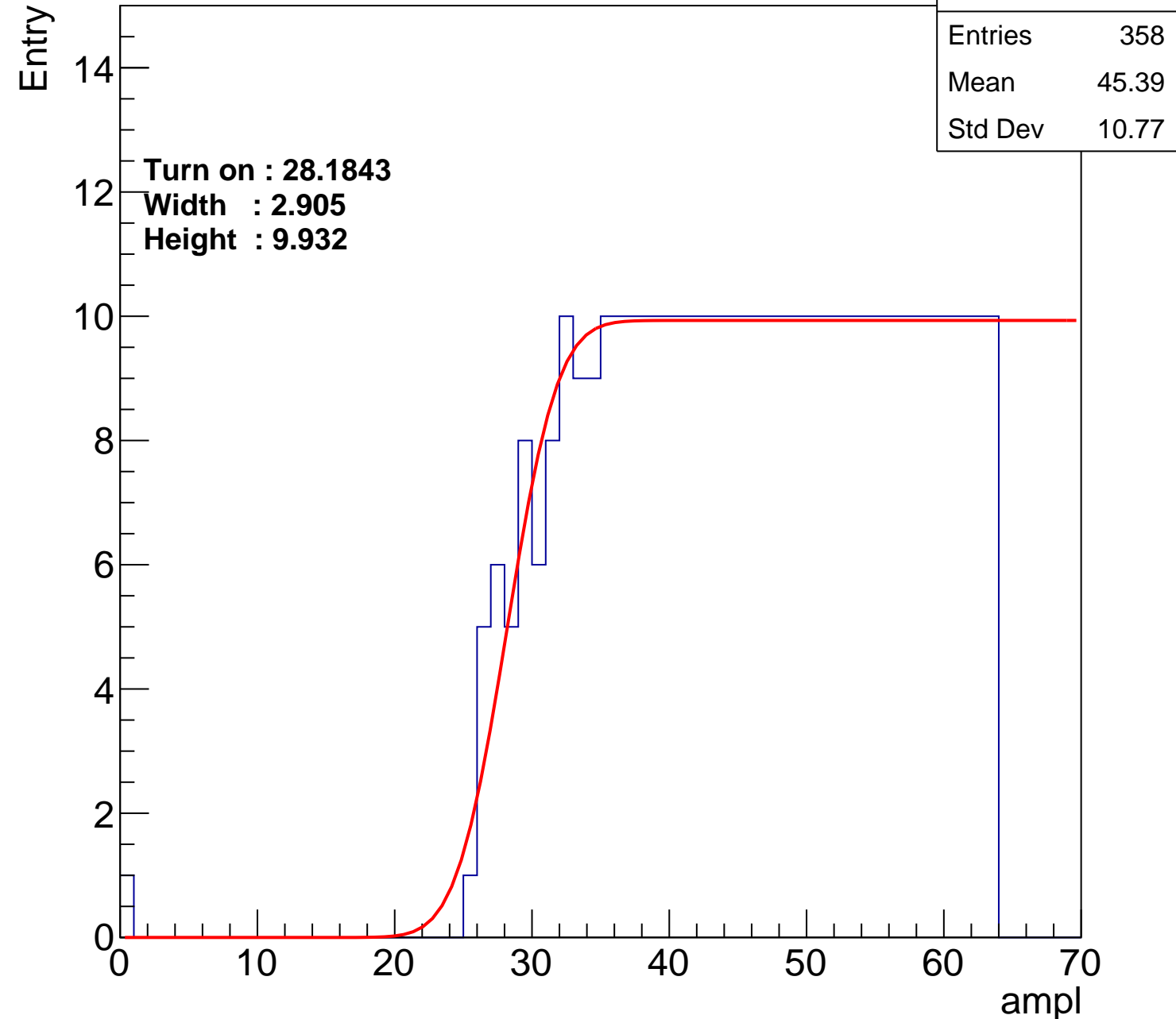
Width : 2.905

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch97

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.18
Std Dev	11.65

Turn on : 26.6164

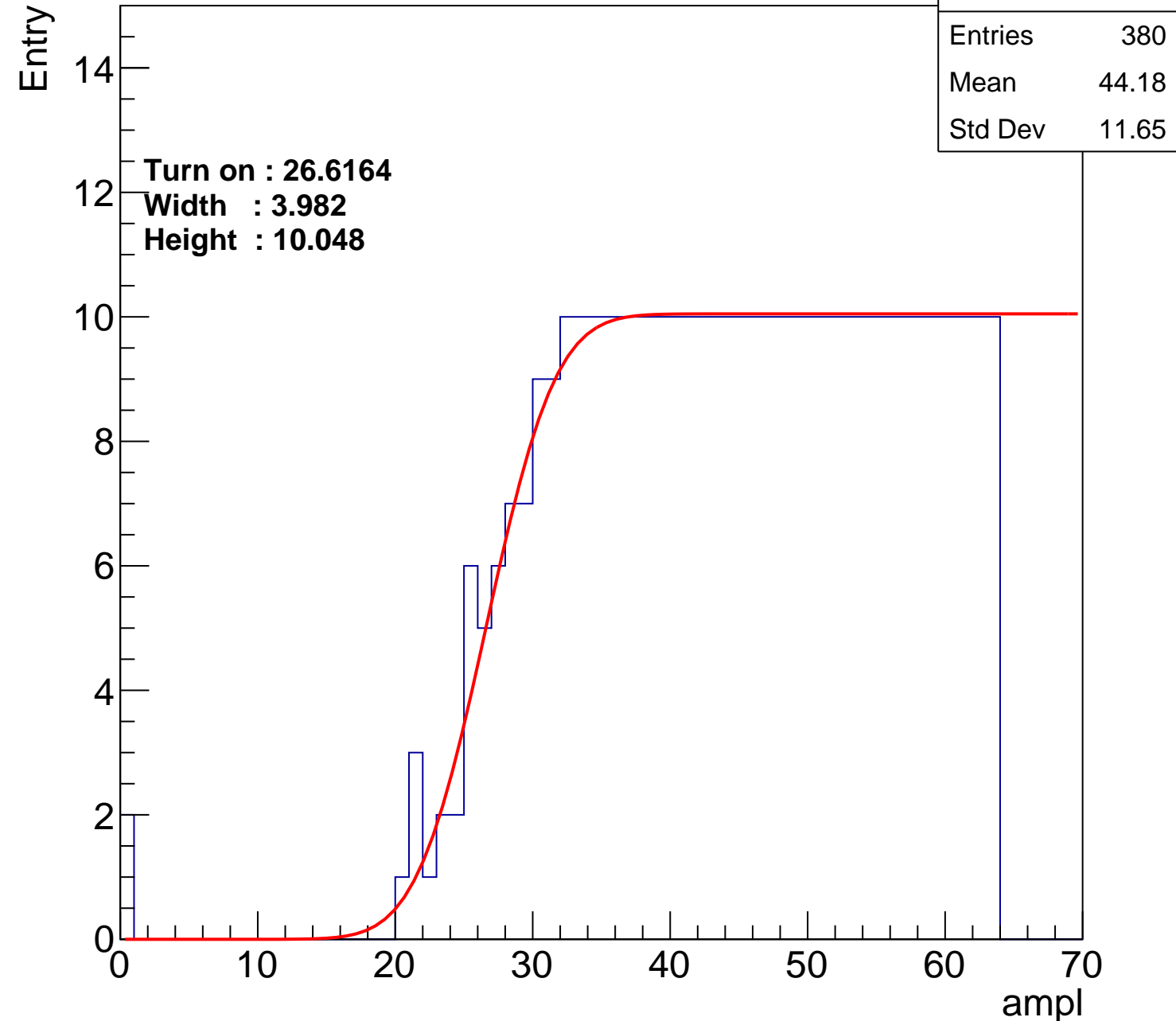
Width : 3.982

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch98

calib_packv5_042523_0143.root, FC#4, port A2

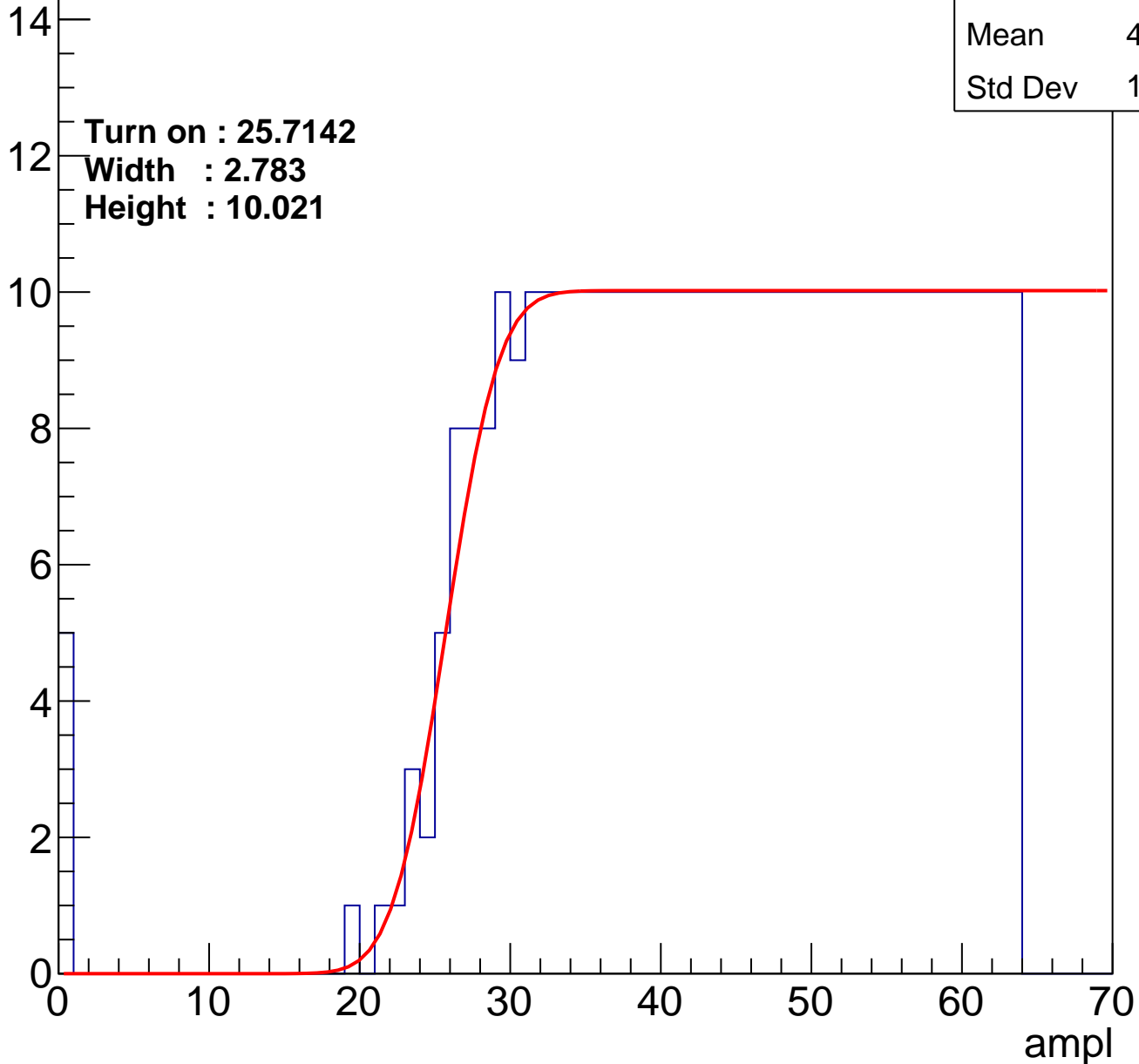
Entries	391
Mean	43.53
Std Dev	12.29

Turn on : 25.7142

Width : 2.783

Height : 10.021

Entry



B1L100S, U25-ch99

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 26.2206

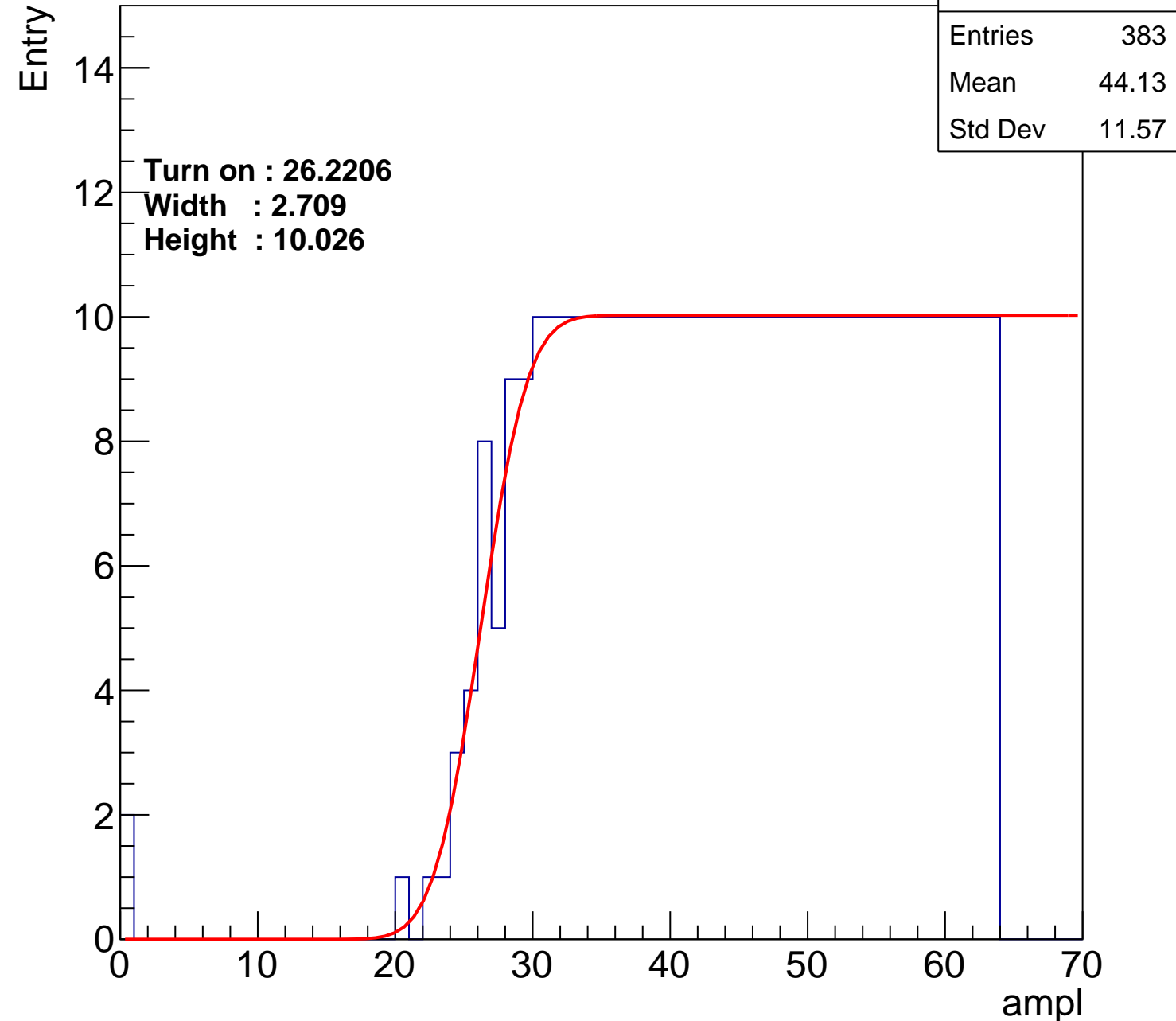
Width : 2.709

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch100

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.12
Std Dev	10.89

Turn on : 27.5521

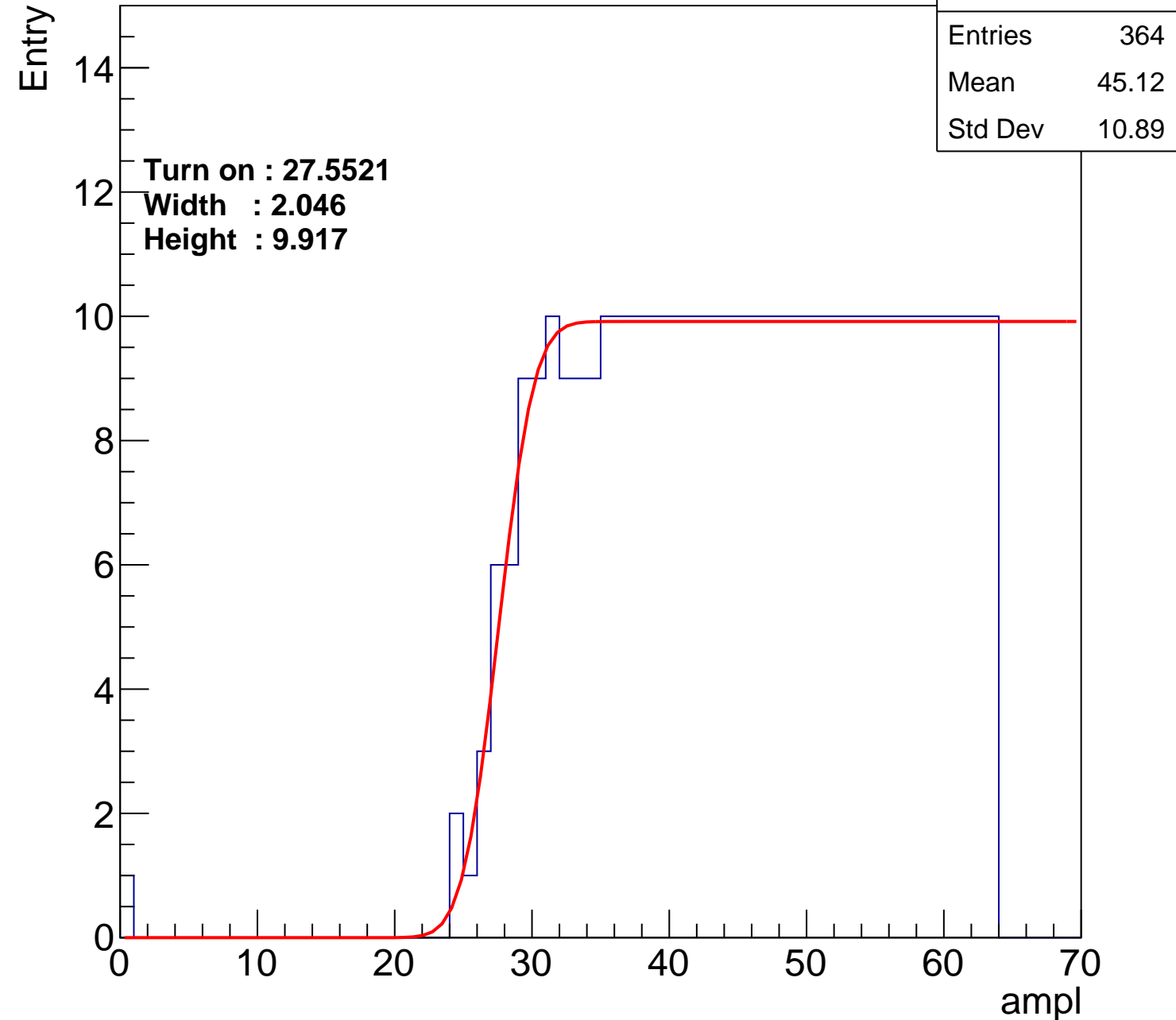
Width : 2.046

Height : 9.917

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch101

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.48
Std Dev	10.9

Turn on : 28.7062

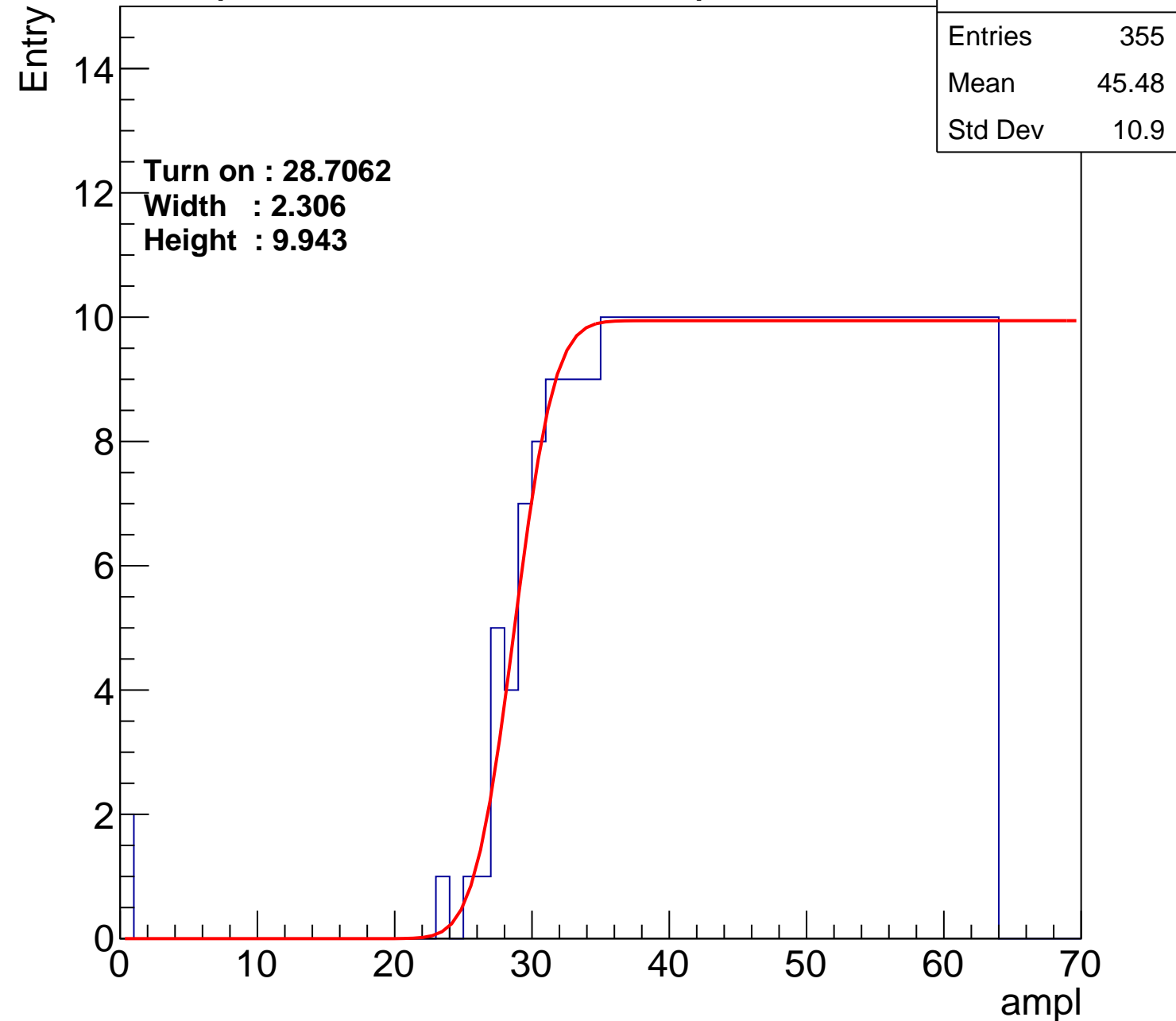
Width : 2.306

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch102

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.2
Std Dev	11.56

Turn on : 26.3944

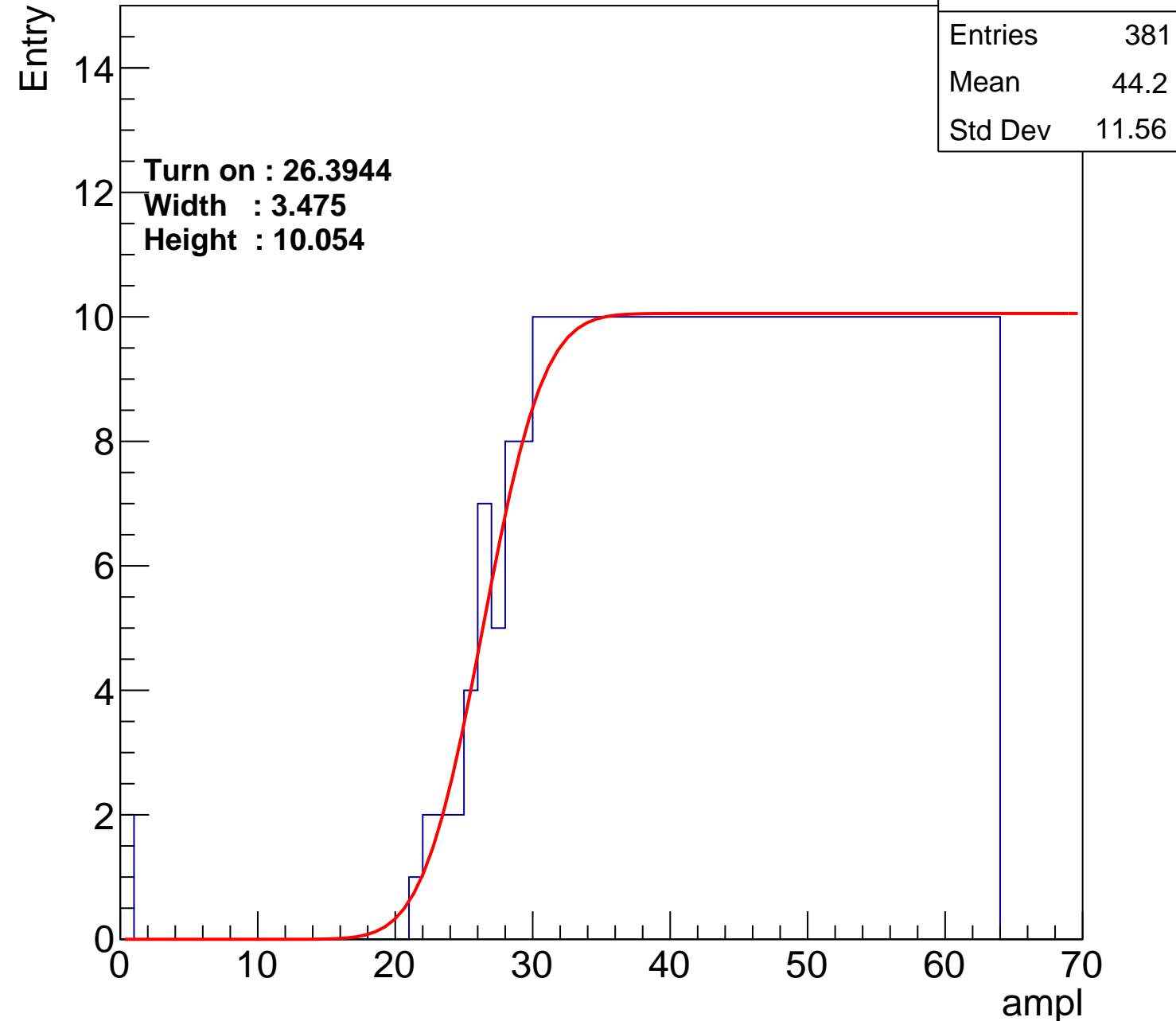
Width : 3.475

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch103

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.92
Std Dev	11.57

Turn on : 25.4258

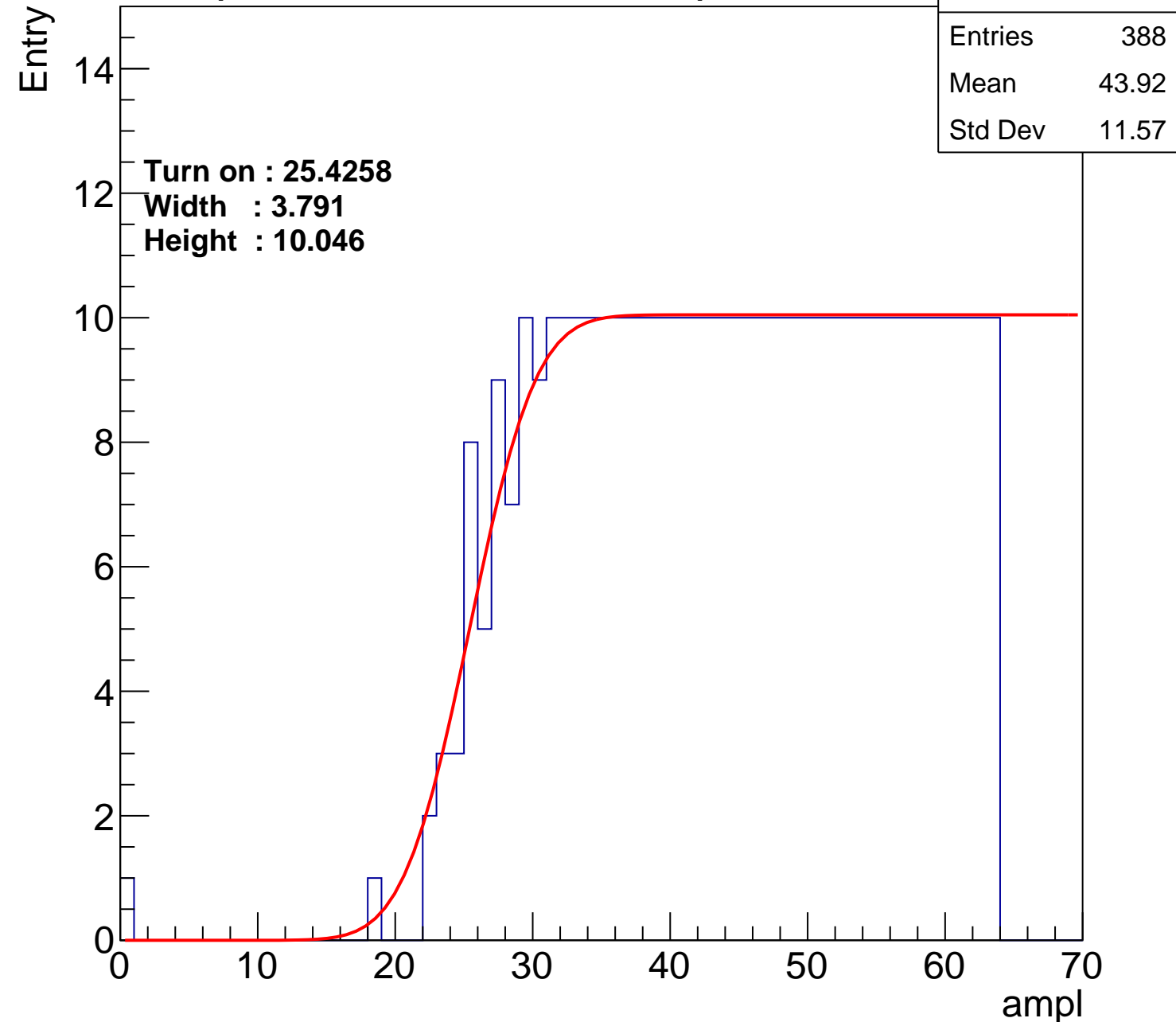
Width : 3.791

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch104

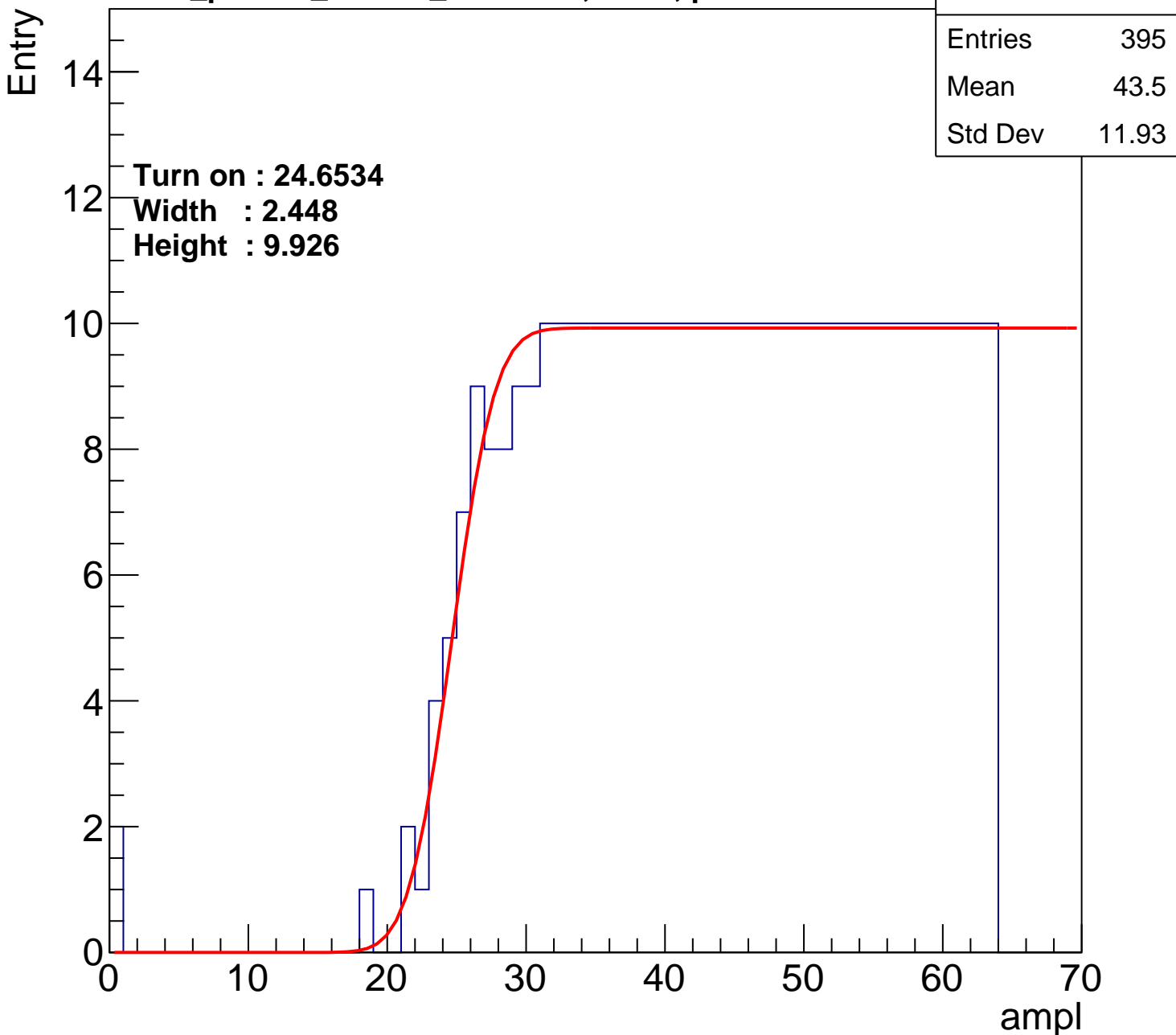
calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 24.6534

Width : 2.448

Height : 9.926

Entries	395
Mean	43.5
Std Dev	11.93



B1L100S, U25-ch105

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.4
Std Dev	10.76

Turn on : 28.0661

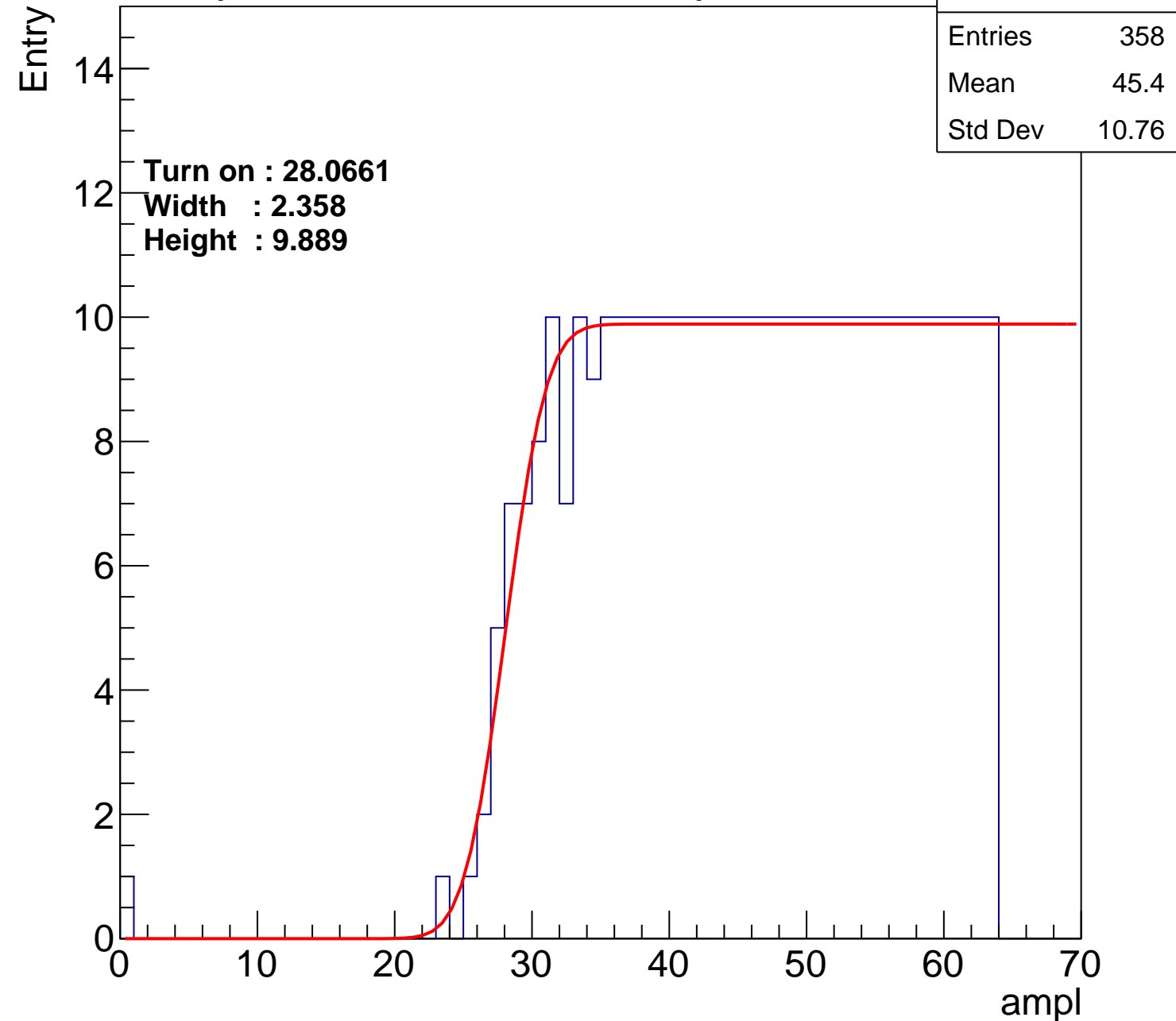
Width : 2.358

Height : 9.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch106

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	44
Std Dev	11.59

Turn on : 24.6482

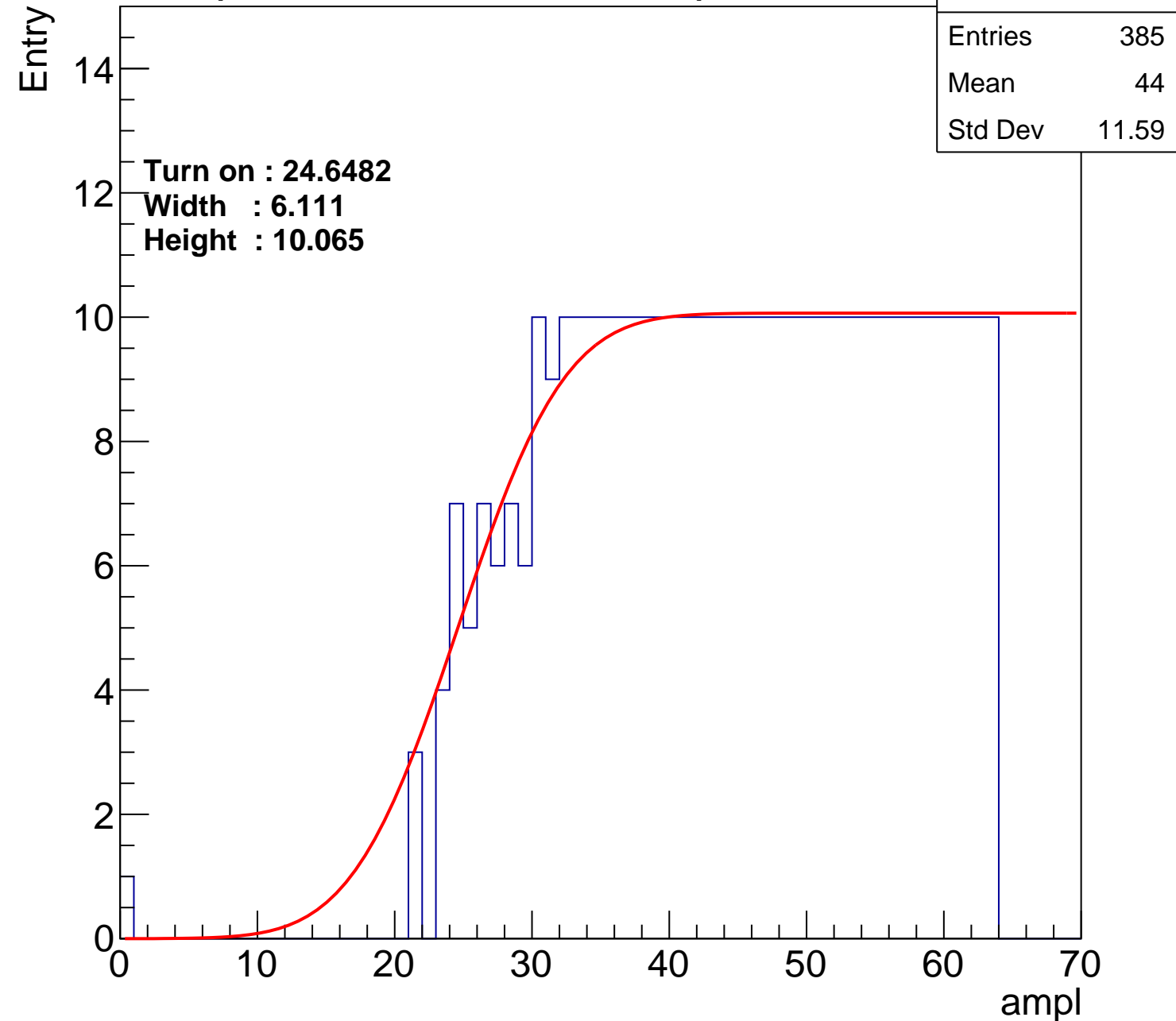
Width : 6.111

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch107

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.0931

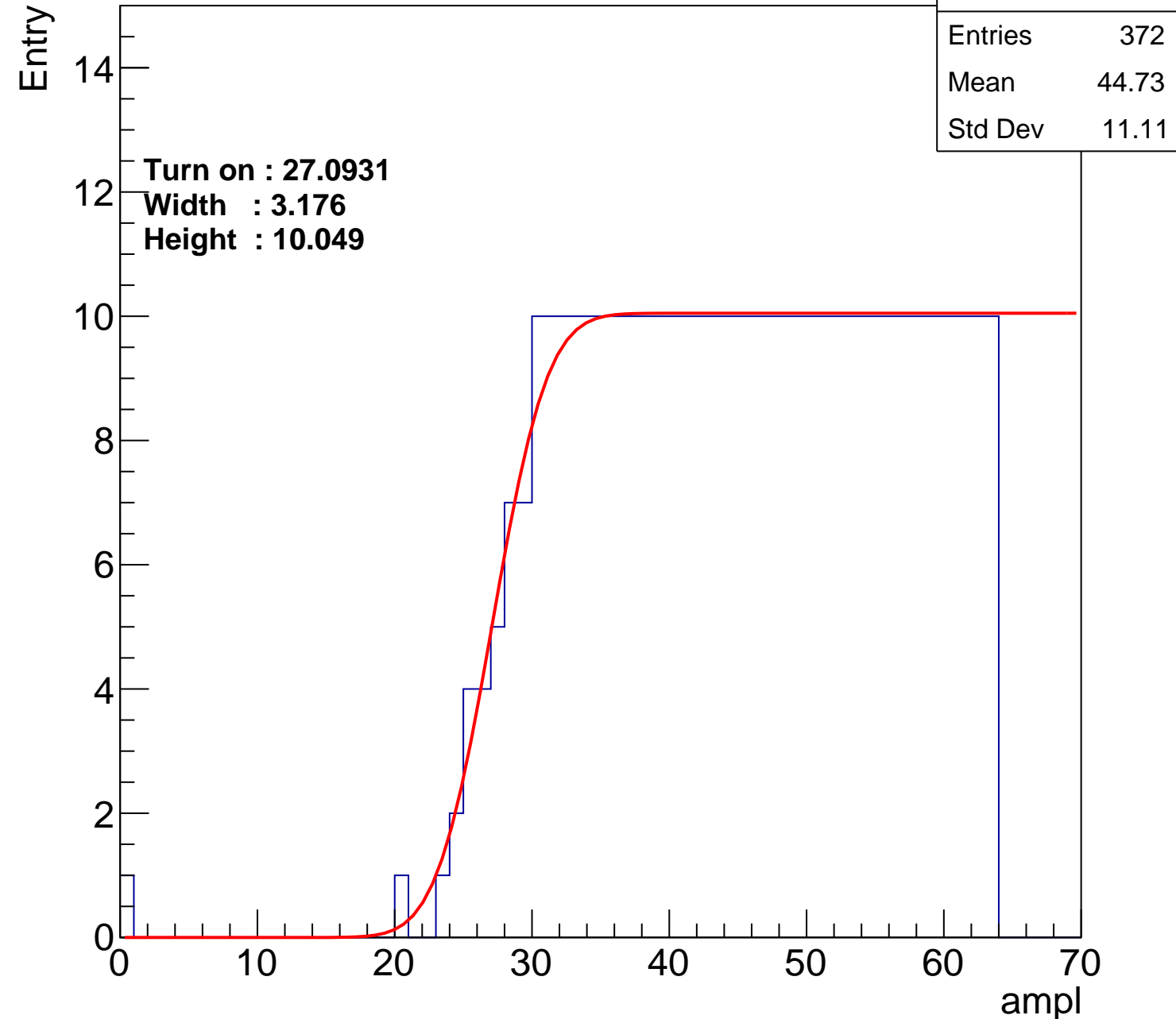
Width : 3.176

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch108

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.57
Std Dev	12.63

Turn on : 26.0340

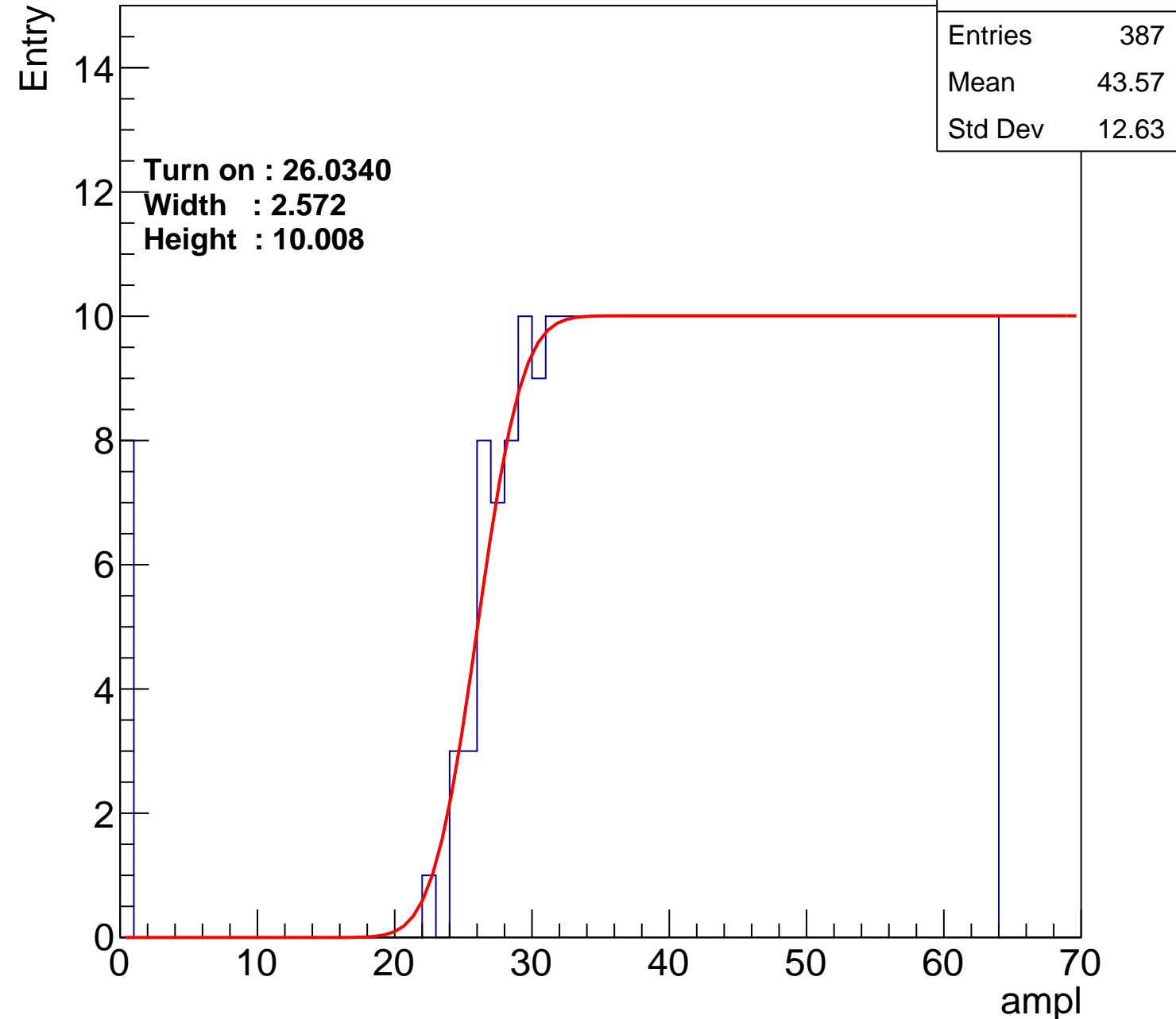
Width : 2.572

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch109

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.62
Std Dev	11.37

Turn on : 27.7130

Width : 3.022

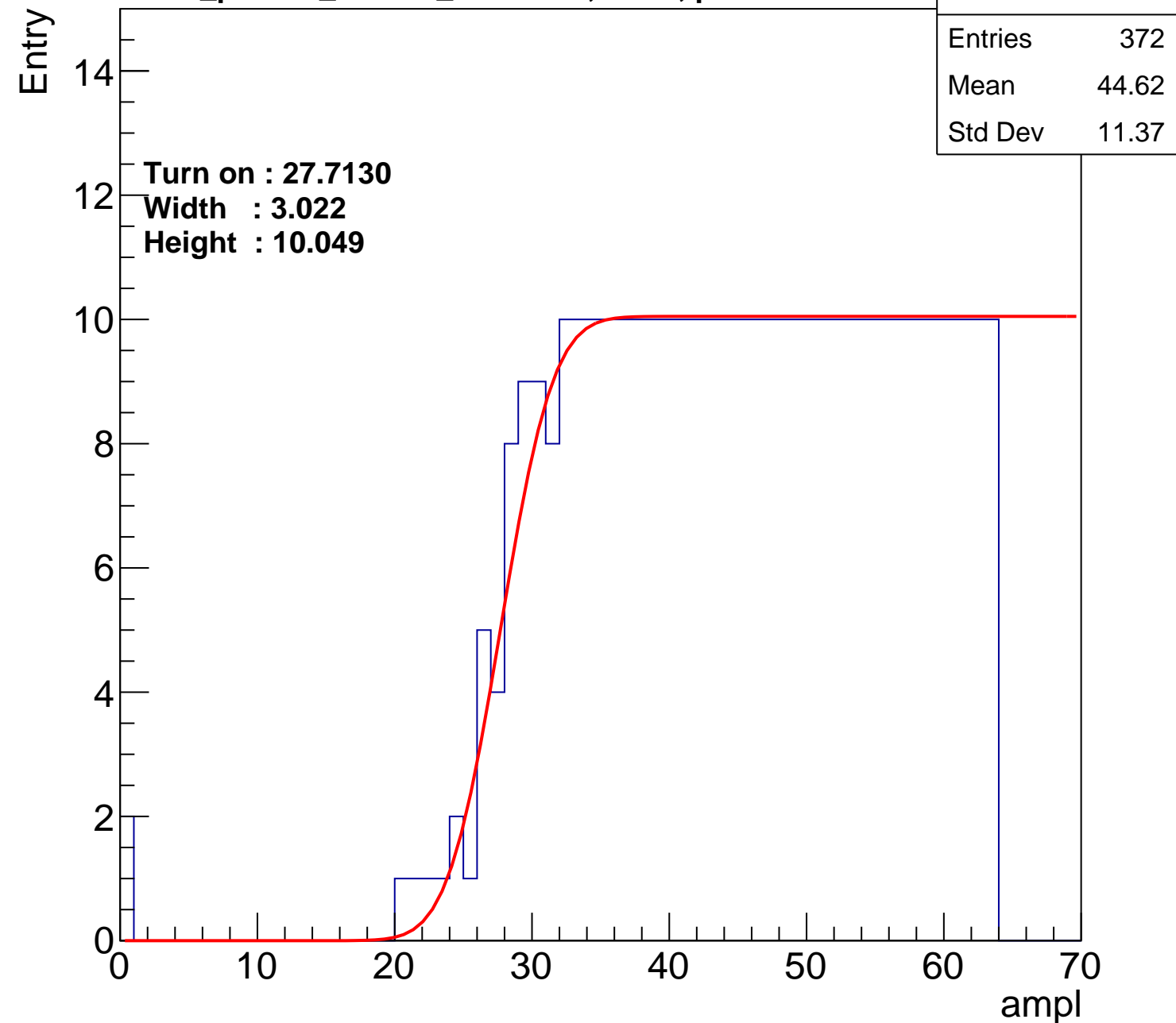
Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch110

calib_packv5_042523_0143.root, FC#4, port A2

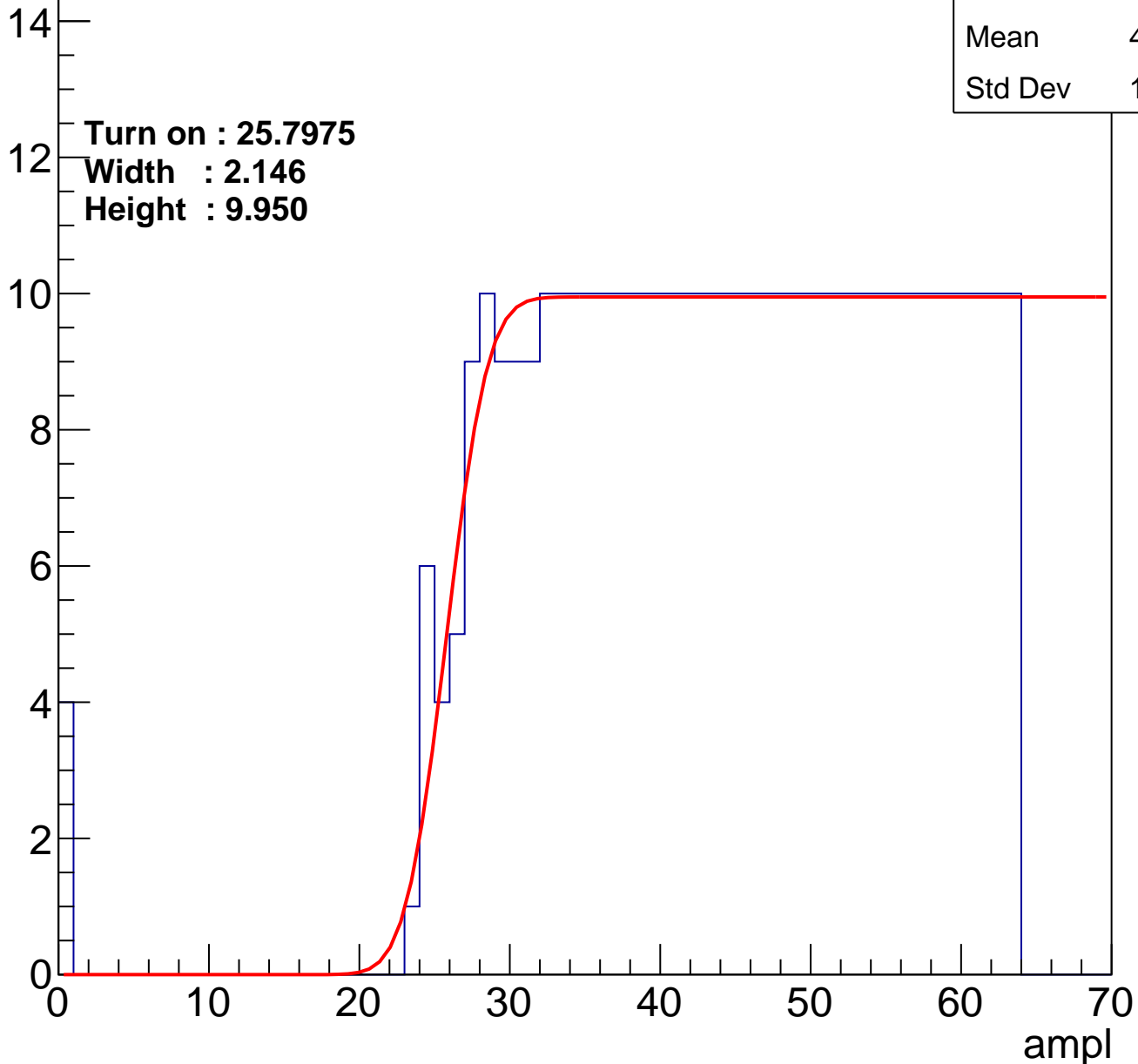
Entries	386
Mean	43.86
Std Dev	11.97

Turn on : 25.7975

Width : 2.146

Height : 9.950

Entry



B1L100S, U25-ch111

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.49
Std Dev	11.41

Turn on : 26.9422

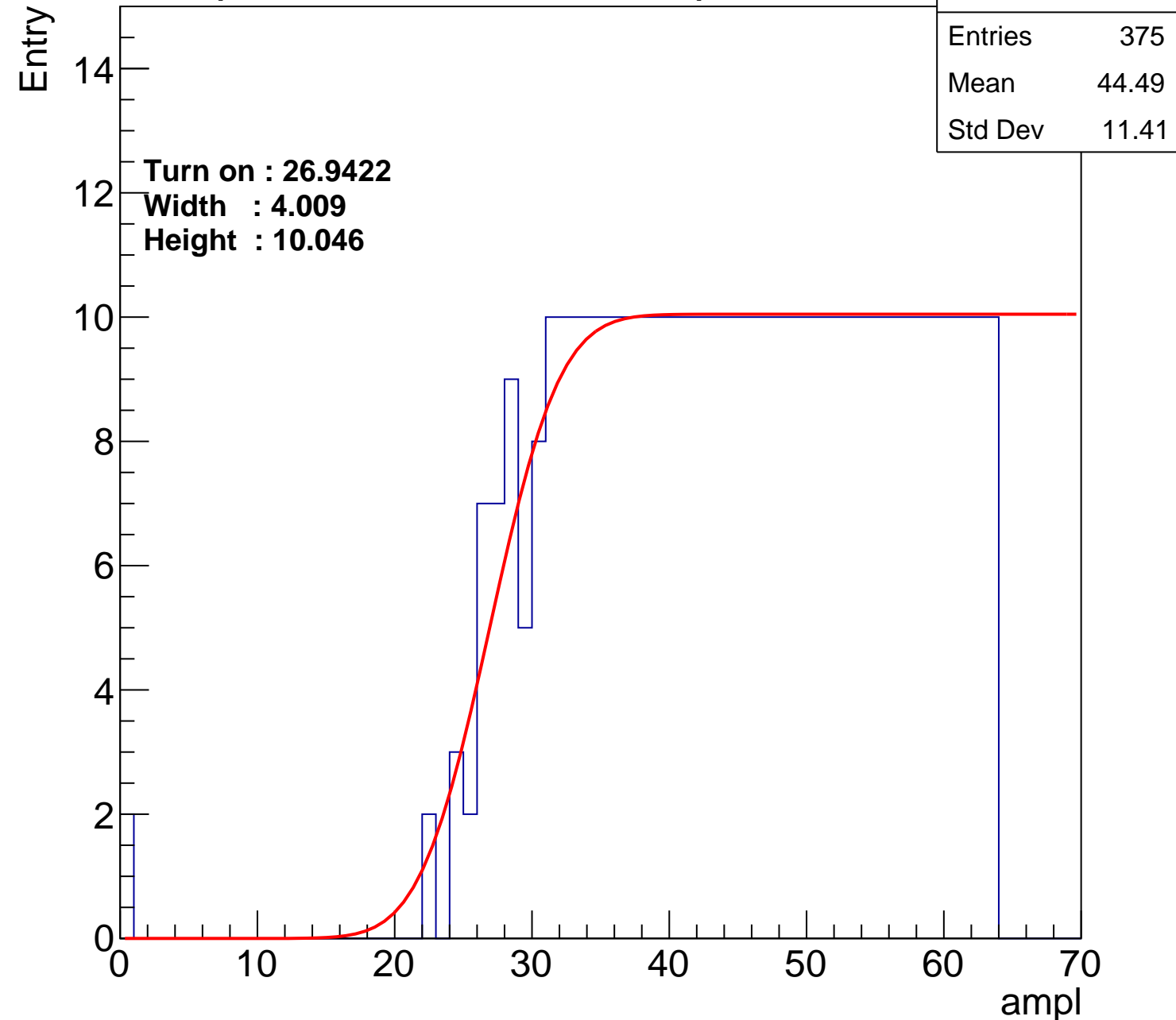
Width : 4.009

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch112

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.82
Std Dev	11.87

Turn on : 25.6042

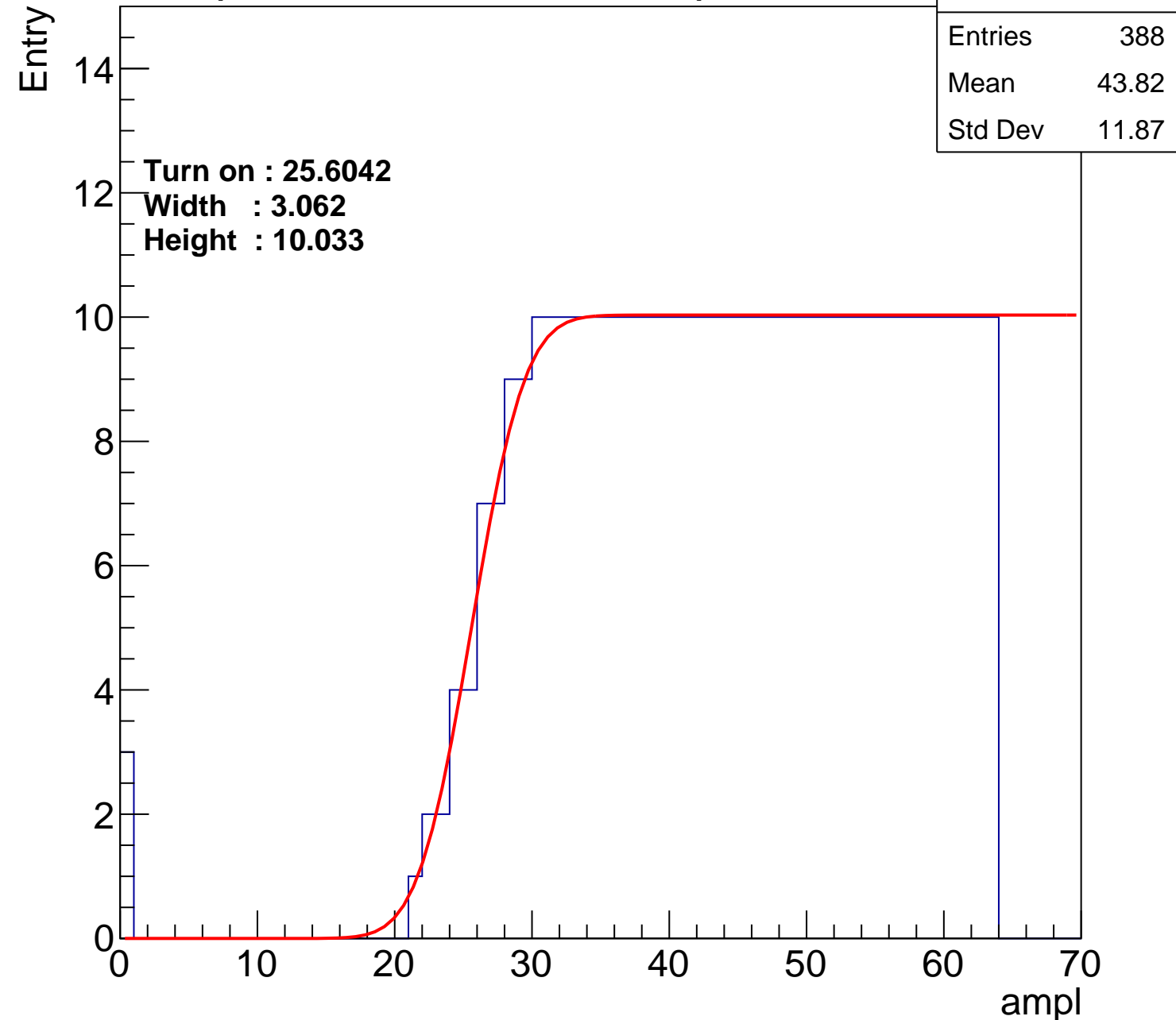
Width : 3.062

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch113

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.51
Std Dev	11.71

Turn on : 27.2715

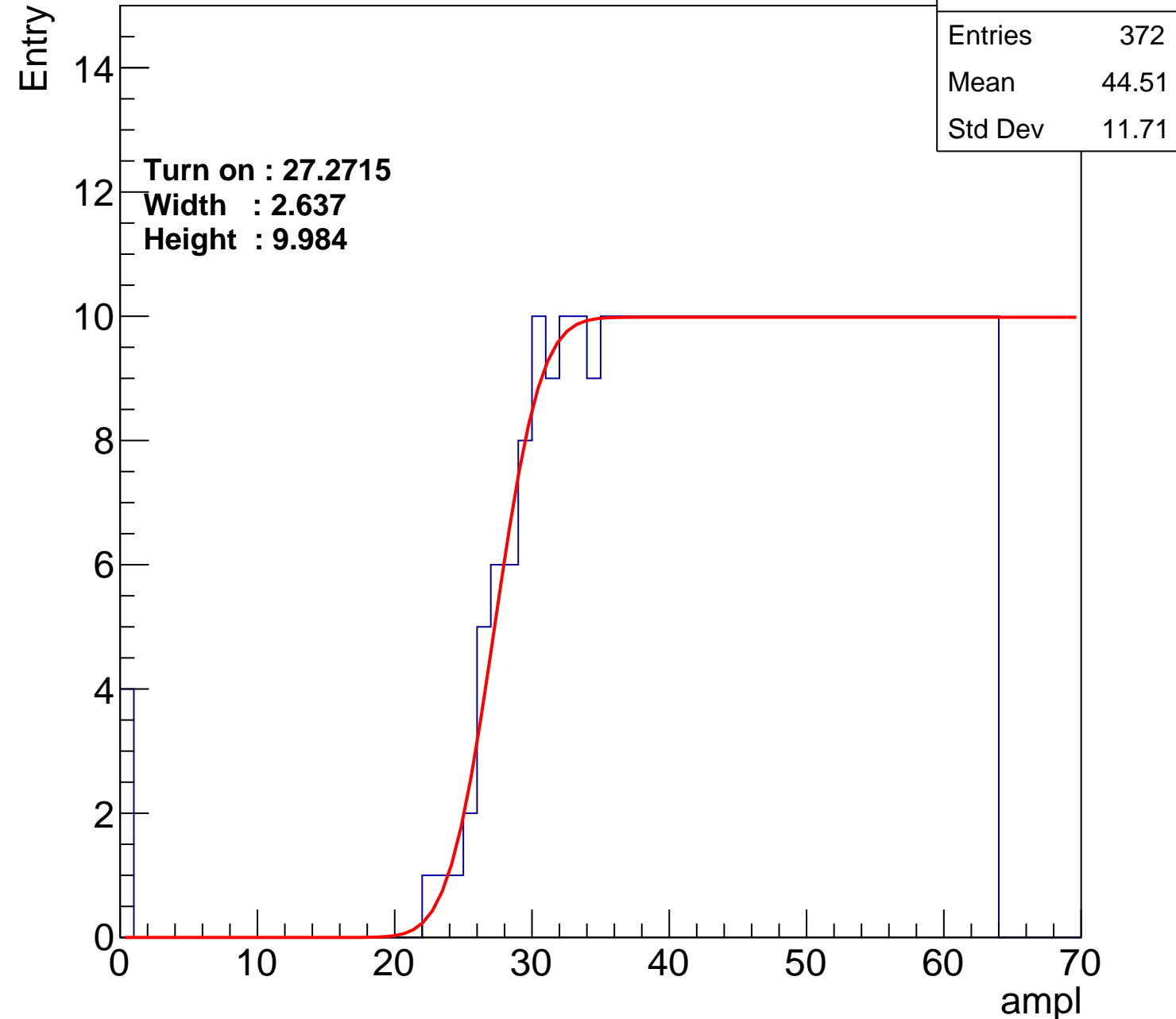
Width : 2.637

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch114

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.77
Std Dev	11.29

Turn on : 27.0563

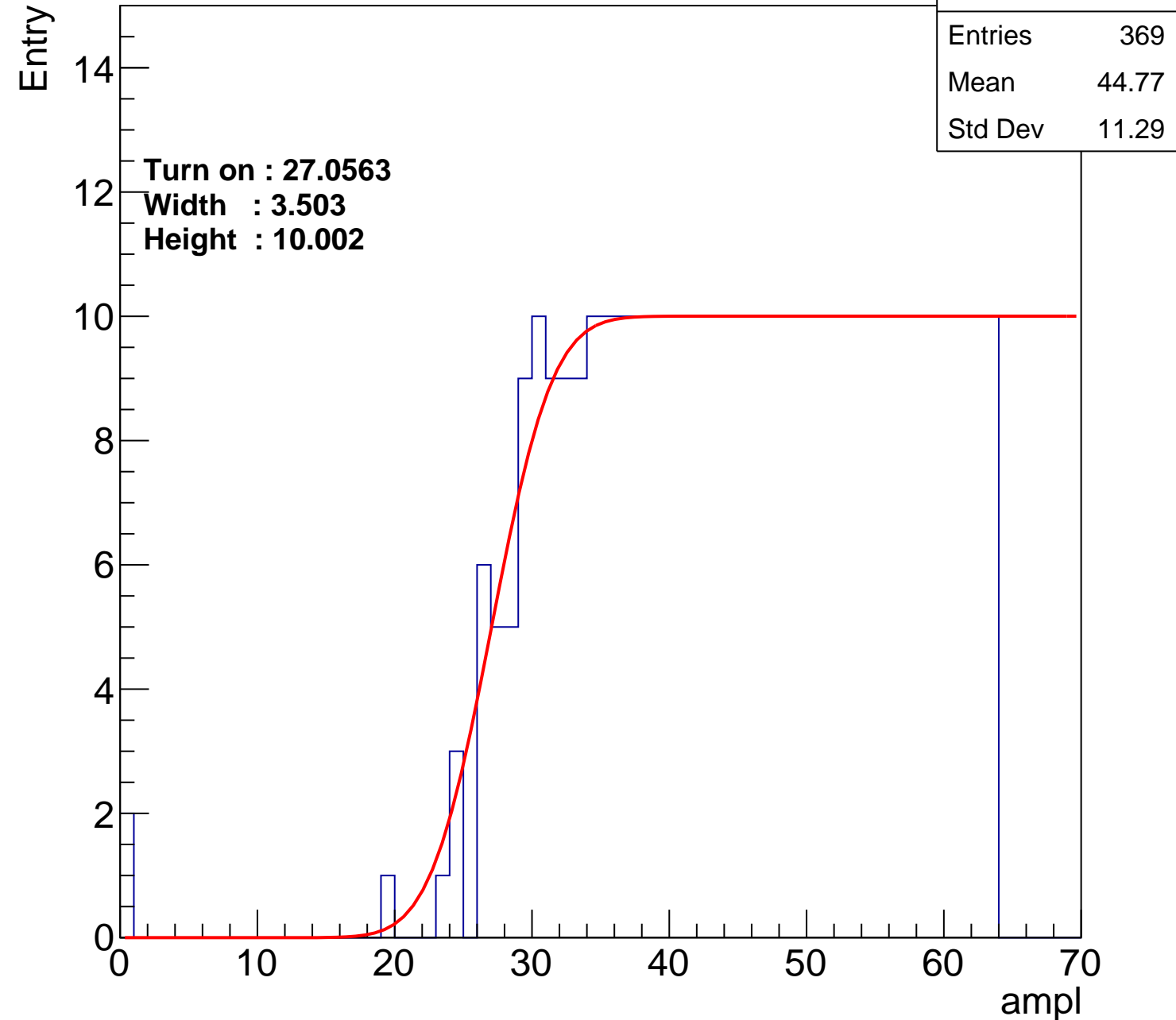
Width : 3.503

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch115

calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	45.26
Std Dev	11.03

Turn on : 27.8587

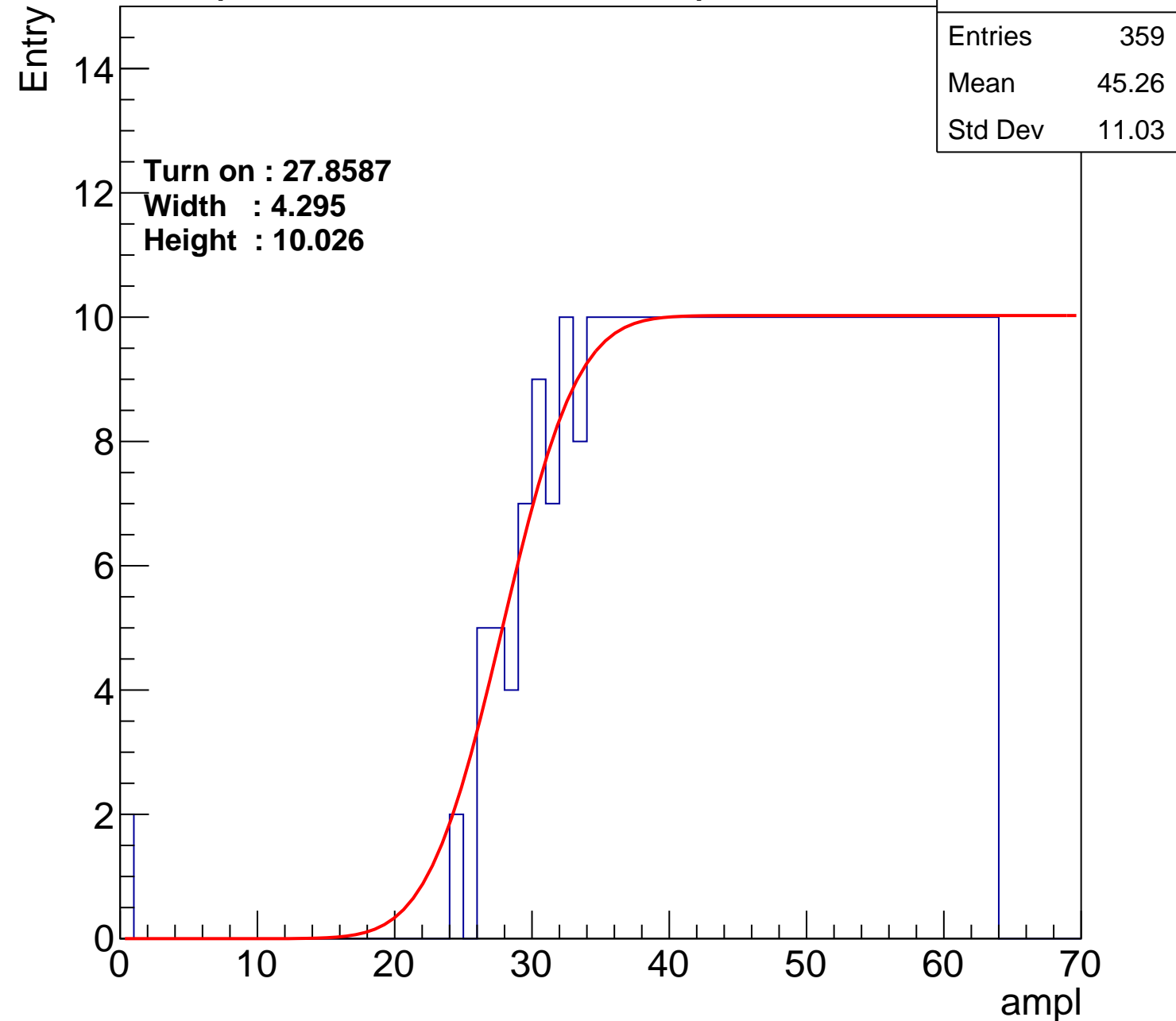
Width : 4.295

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch116

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.74
Std Dev	11.94

Turn on : 25.8163

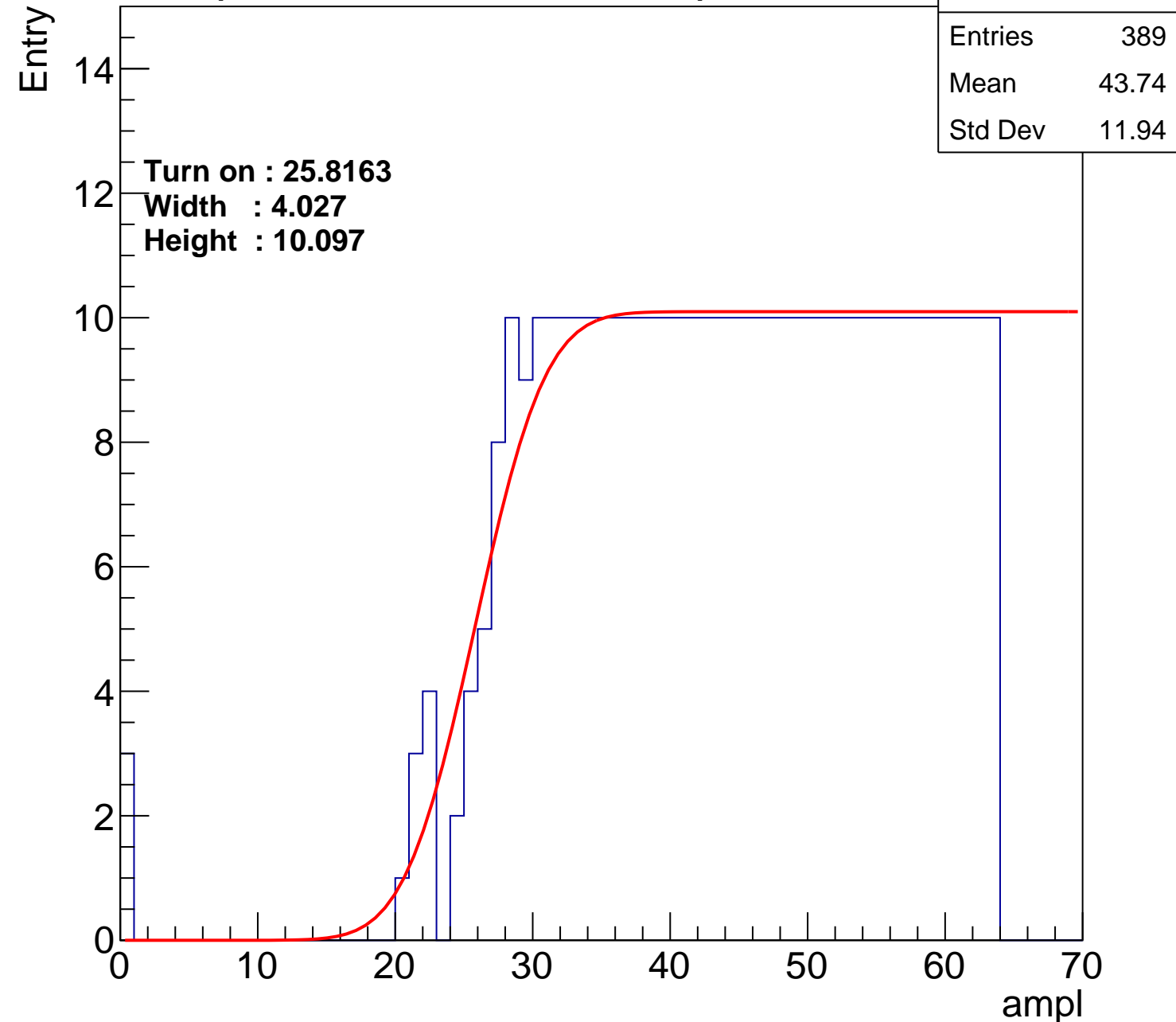
Width : 4.027

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch117

calib_packv5_042523_0143.root, FC#4, port A2

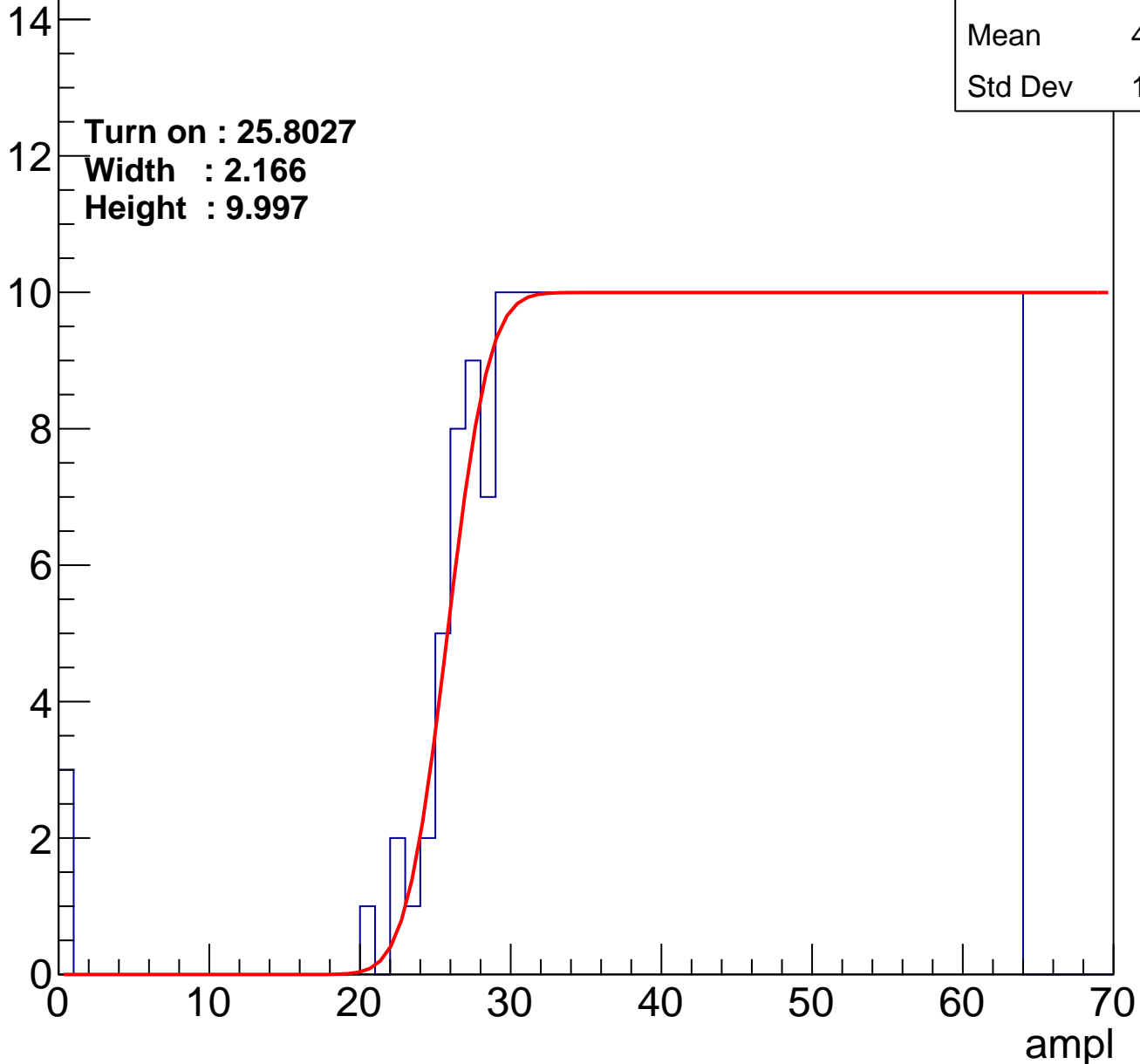
Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.8027

Width : 2.166

Height : 9.997

Entry



B1L100S, U25-ch118

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.42
Std Dev	11.34

Turn on : 26.6152

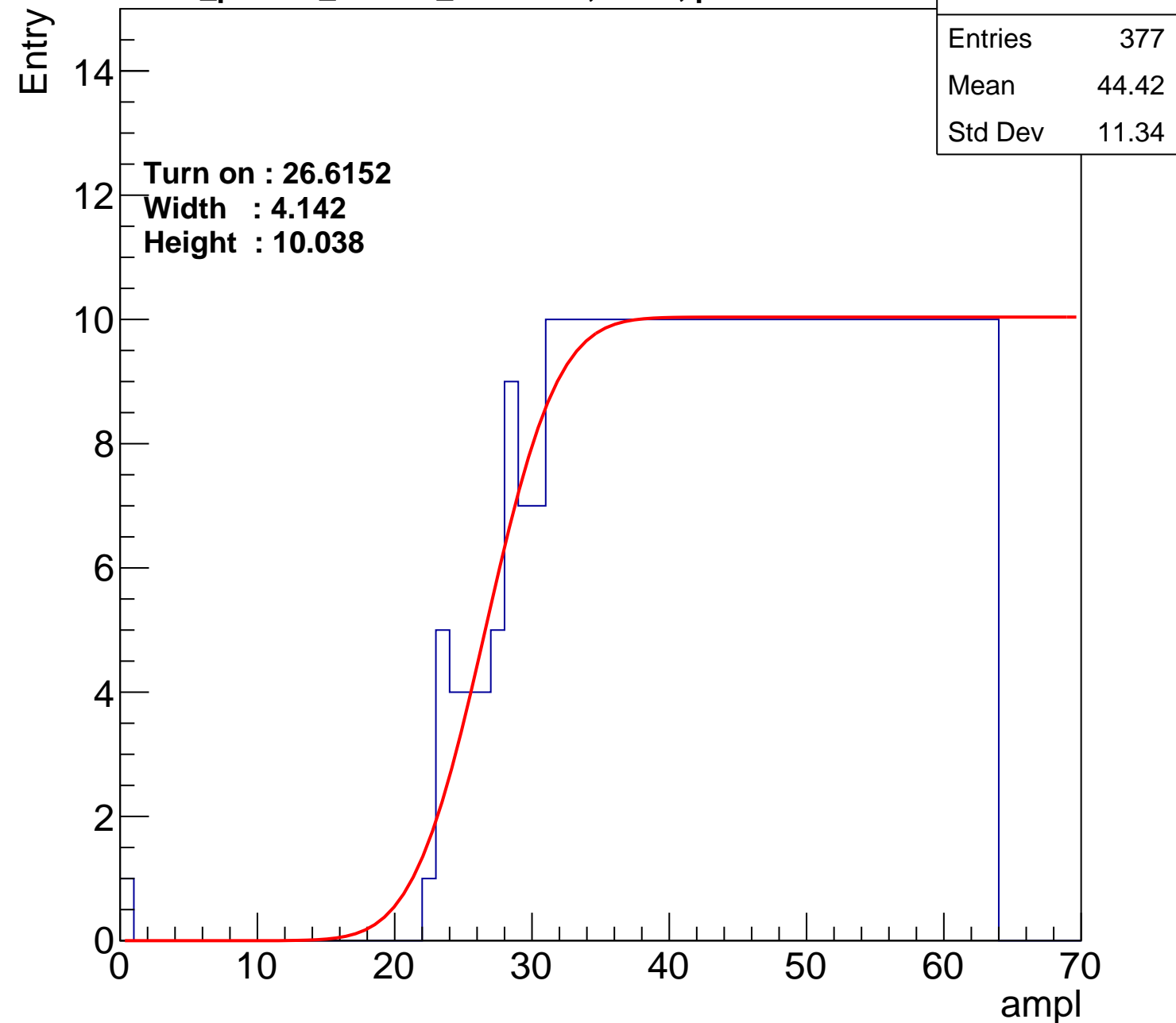
Width : 4.142

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch119

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	44.71
Std Dev	11.92

Turn on : 28.3900

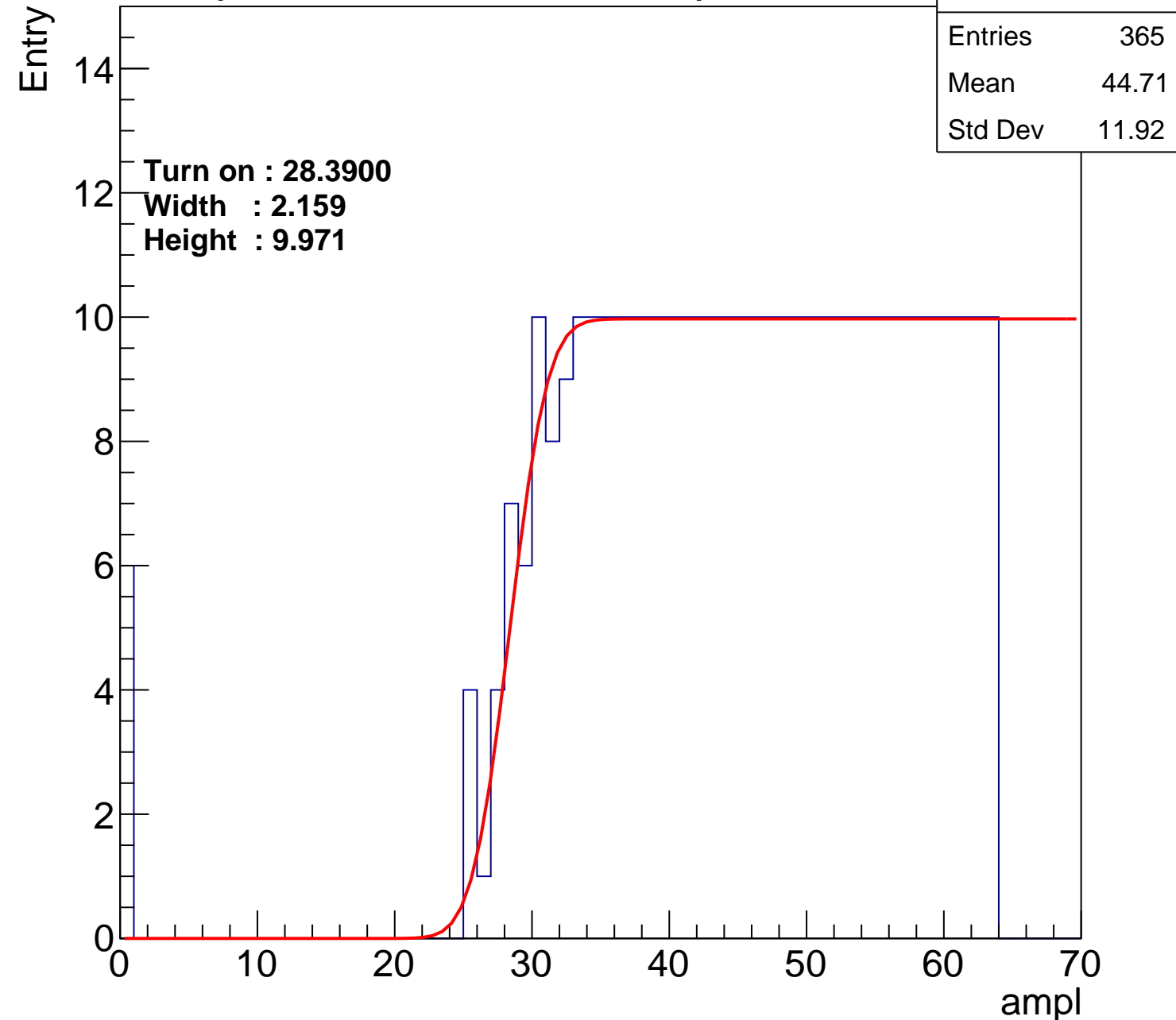
Width : 2.159

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch120

calib_packv5_042523_0143.root, FC#4, port A2

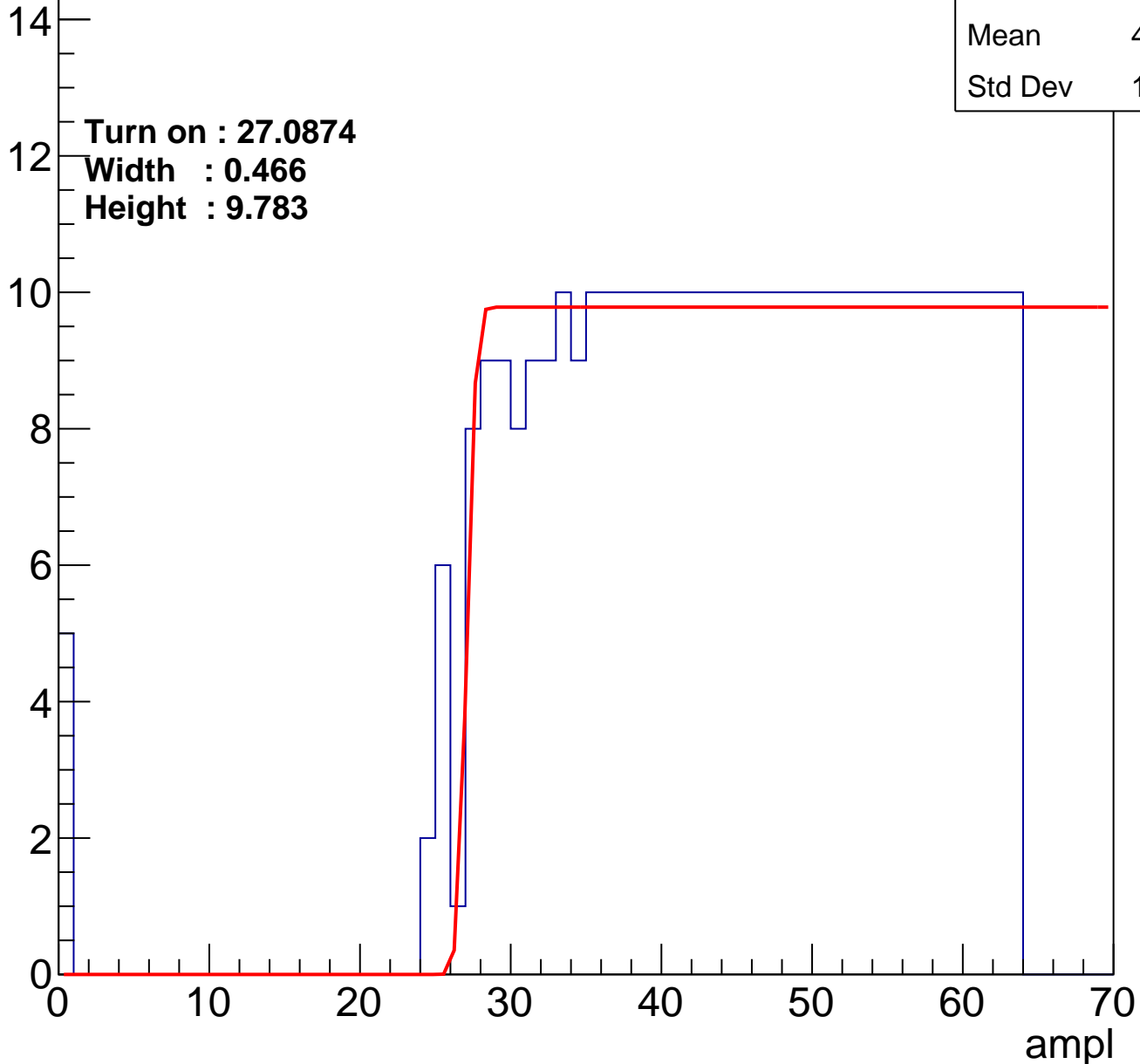
Entries	375
Mean	44.28
Std Dev	11.96

Turn on : 27.0874

Width : 0.466

Height : 9.783

Entry



B1L100S, U25-ch121

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.28
Std Dev	11.85

Turn on : 27.1017

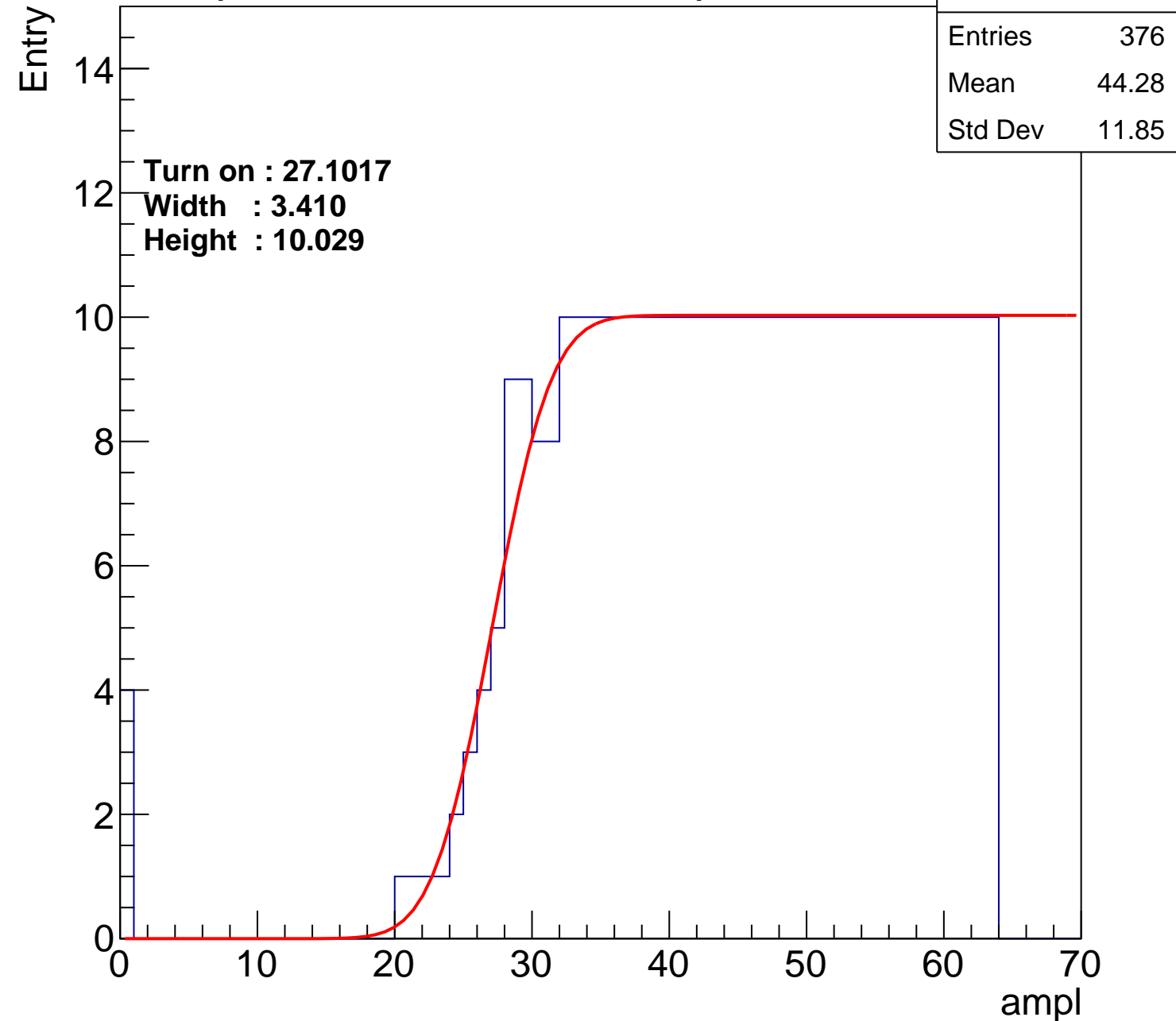
Width : 3.410

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch122

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.14
Std Dev	11.79

Turn on : 26.6023

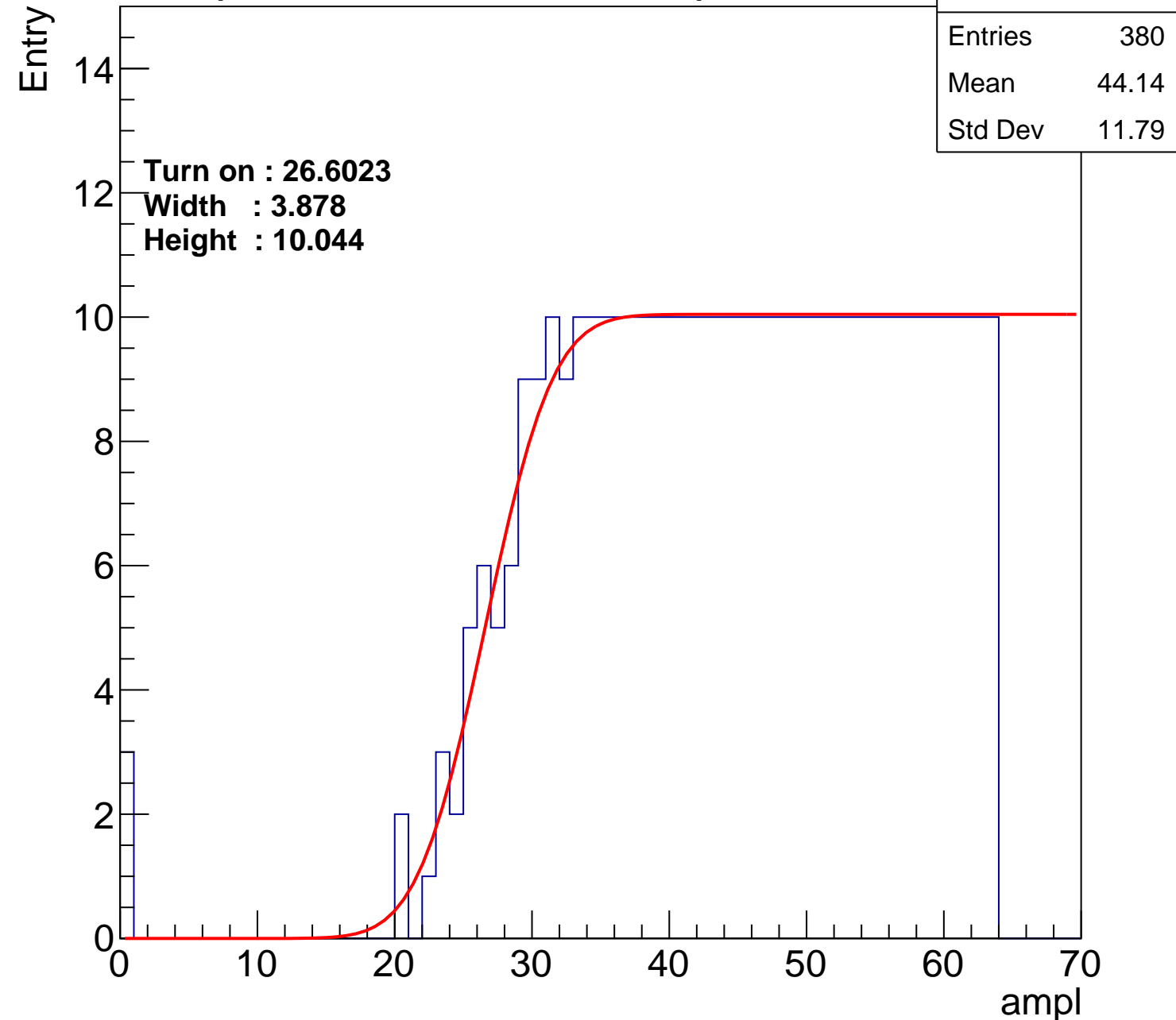
Width : 3.878

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch123

calib_packv5_042523_0143.root, FC#4, port A2

Entries	357
Mean	45.3
Std Dev	11.16

Turn on : 28.5683

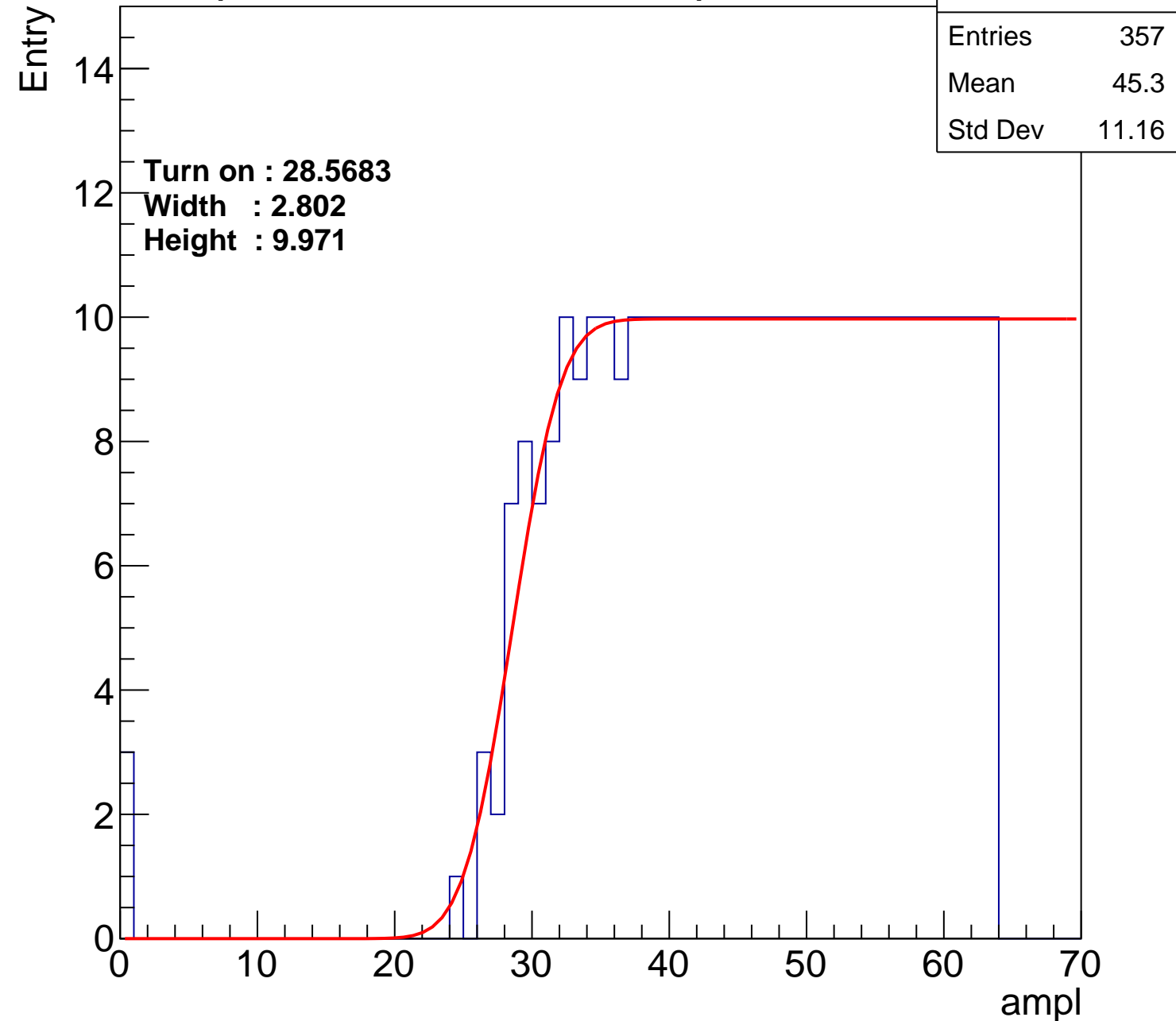
Width : 2.802

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch124

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.56
Std Dev	11.55

Turn on : 27.4576

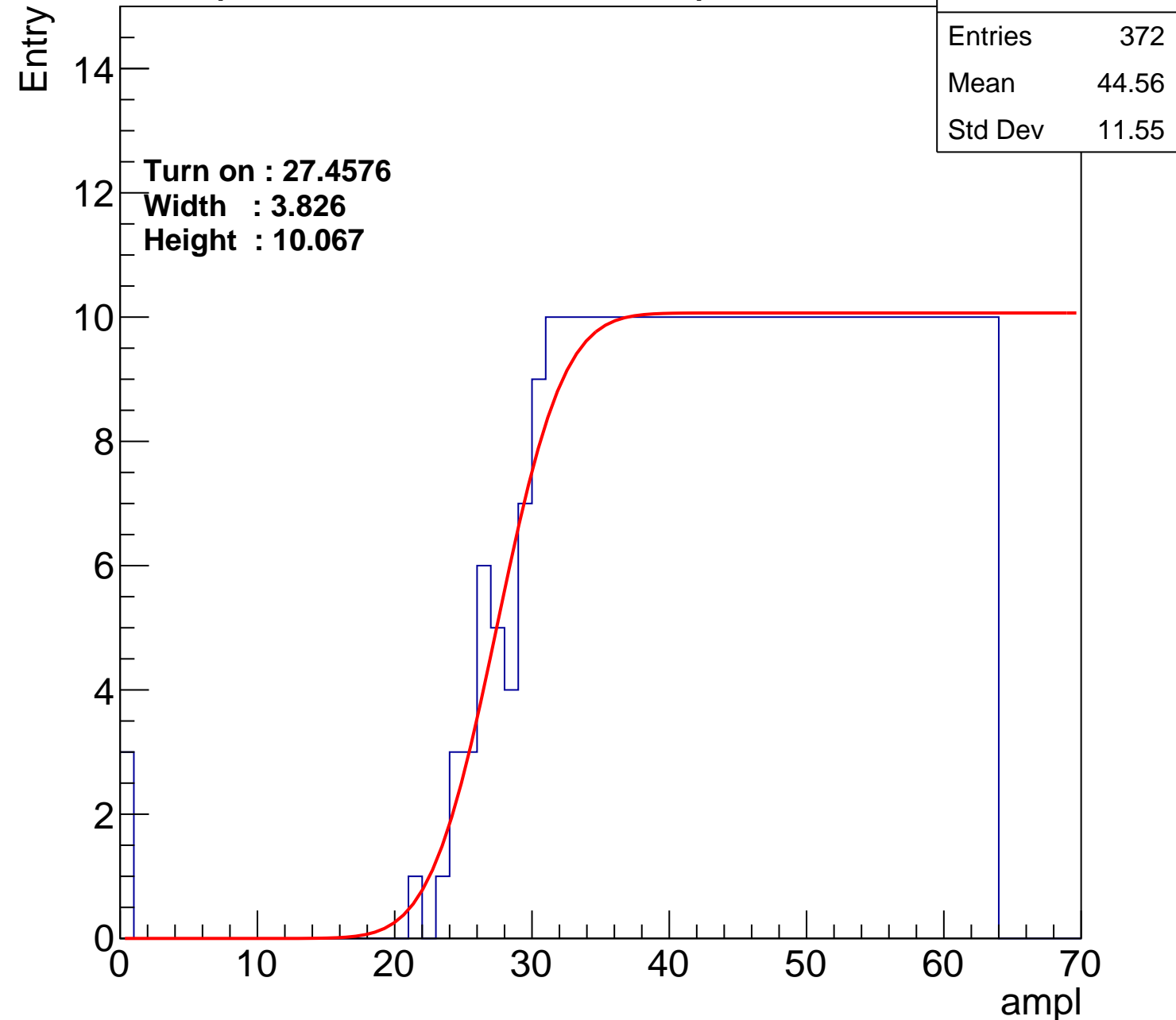
Width : 3.826

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch125

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.12
Std Dev	12.01

Turn on : 27.0706

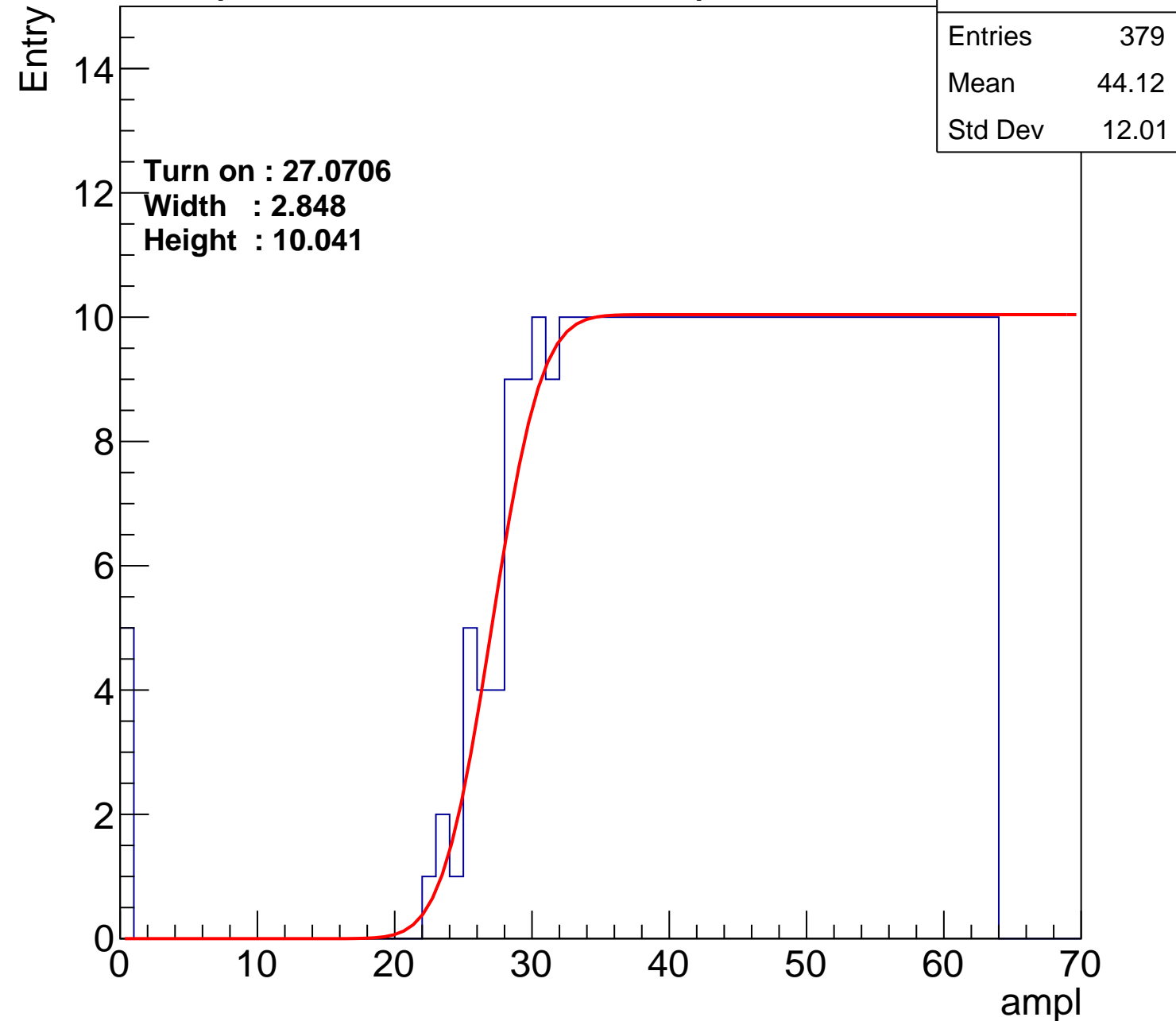
Width : 2.848

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch126

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.82
Std Dev	11.08

Turn on : 28.2107

Width : 0.566

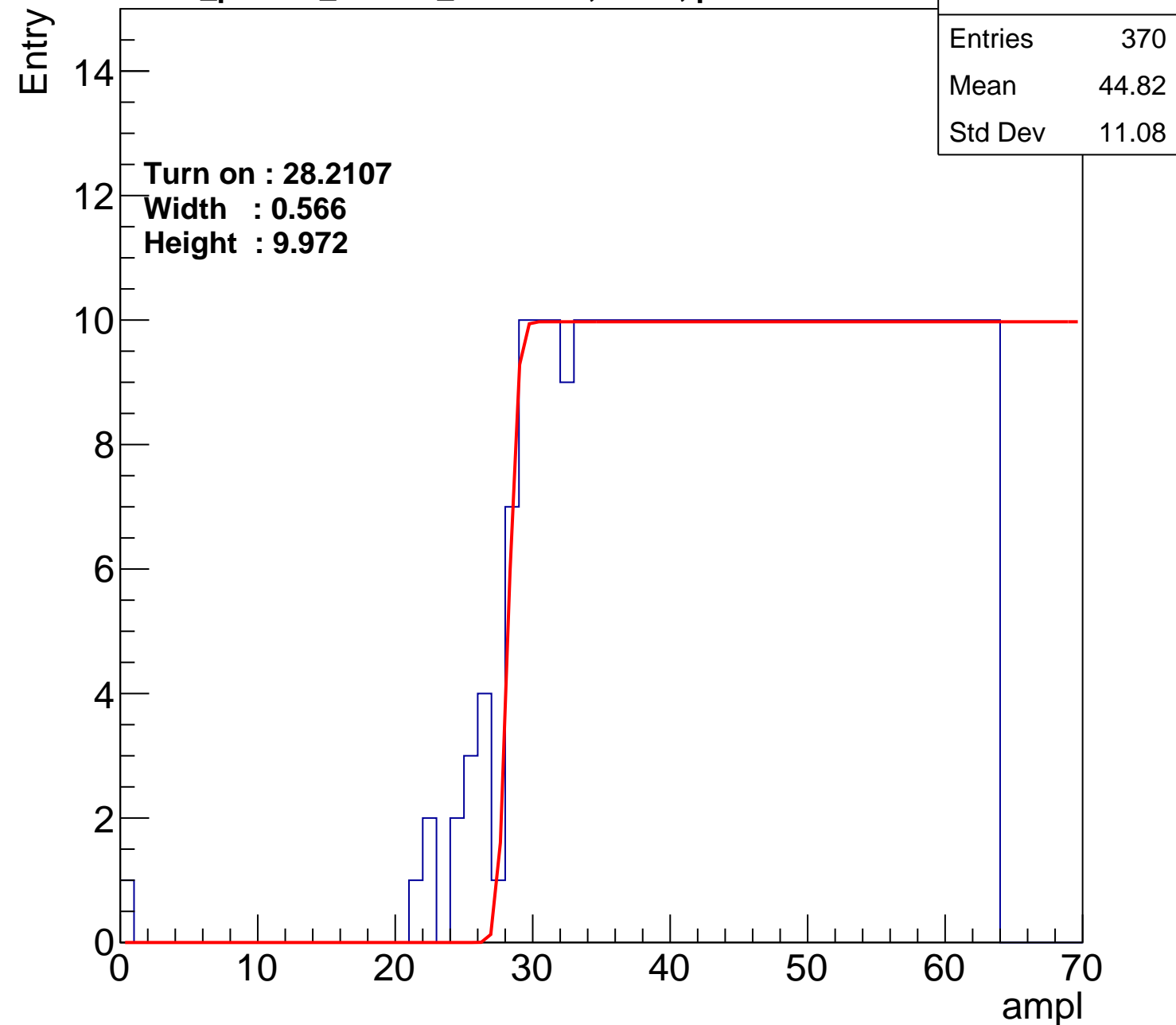
Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U25-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 26.5417

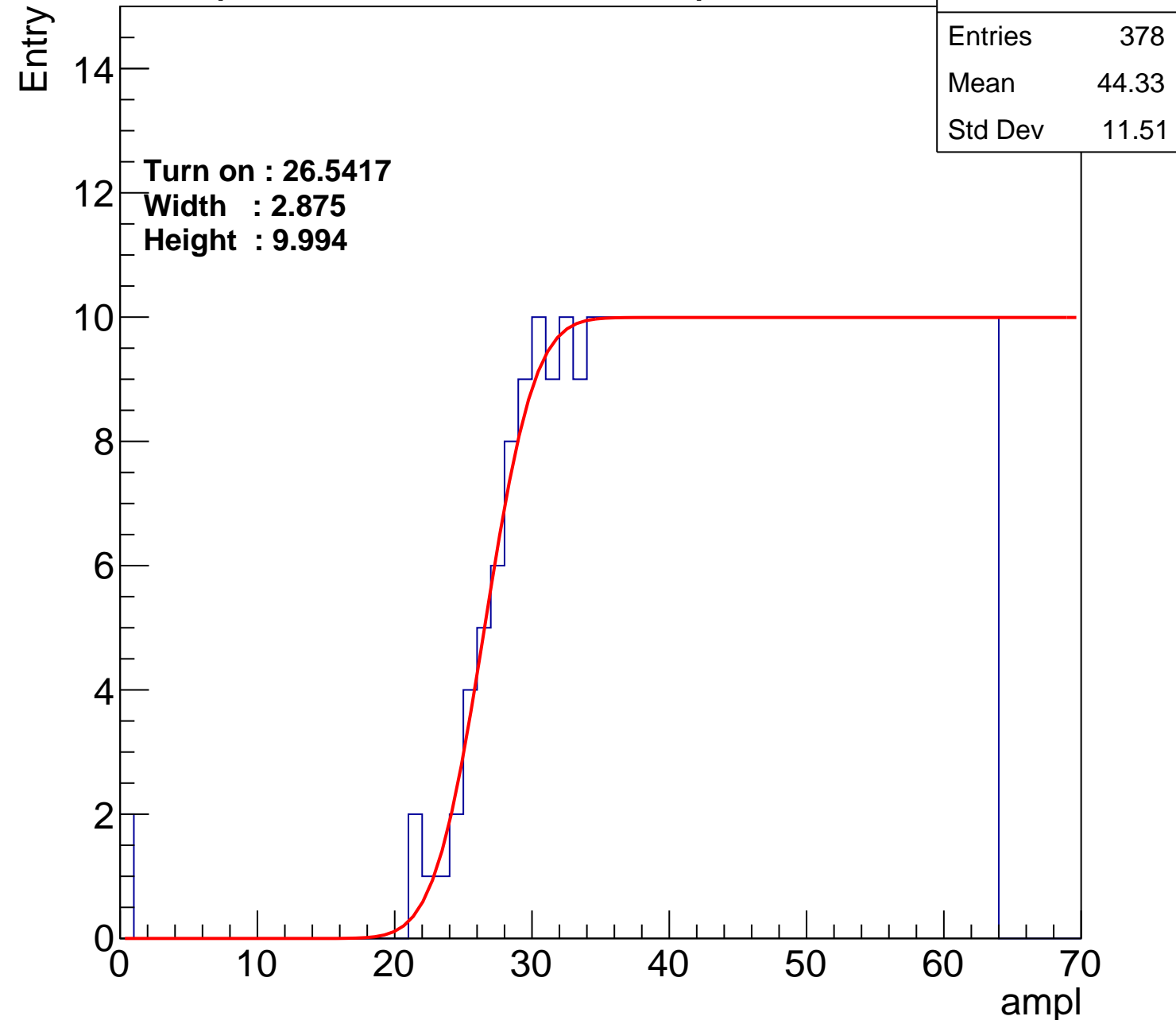
Width : 2.875

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U25-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 26.5417

Width : 2.875

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl

