

B1L003S, U20-ch0

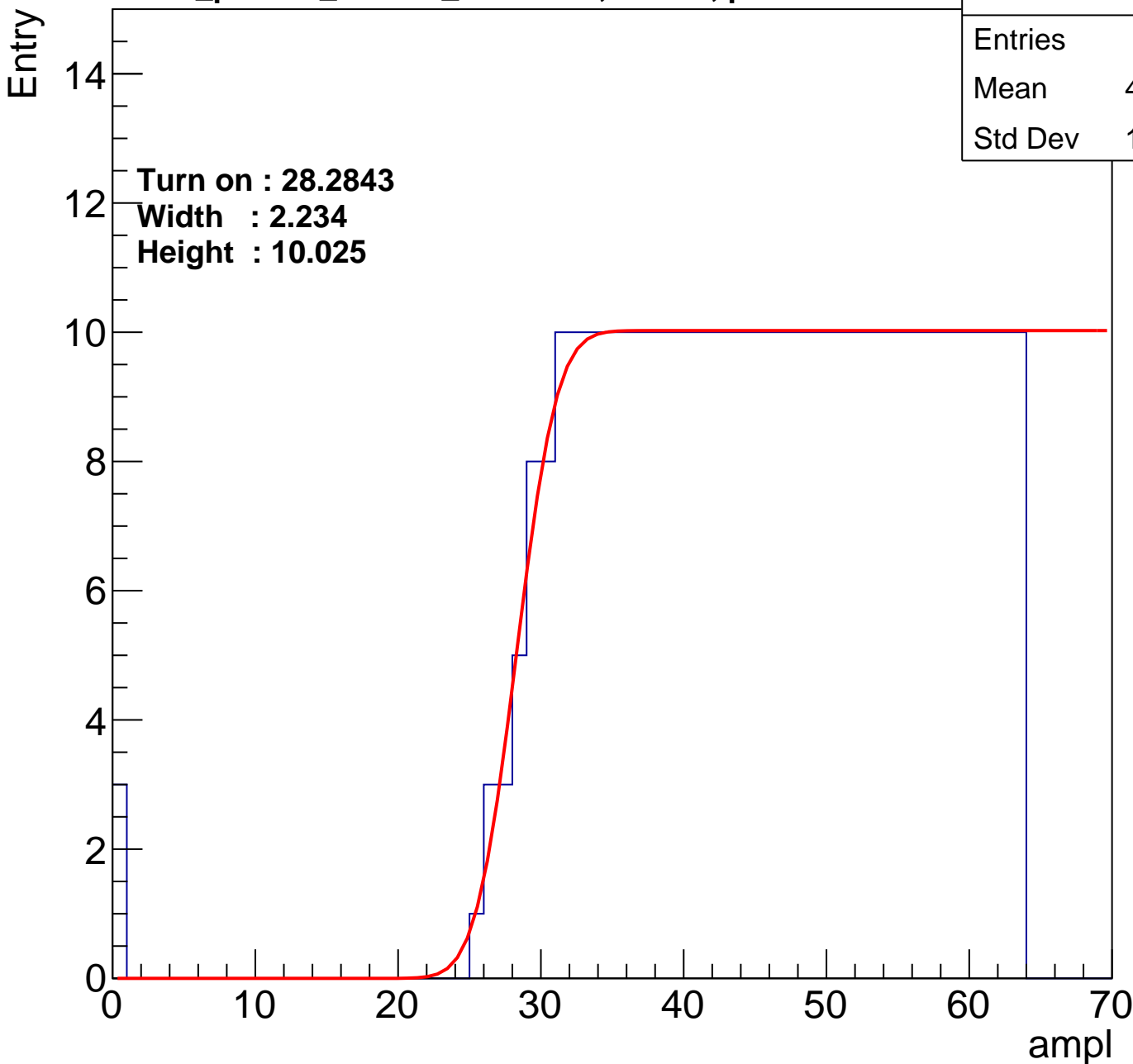
calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.17
Std Dev	11.17

Turn on : 28.2843

Width : 2.234

Height : 10.025



B1L003S, U20-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.35
Std Dev	11.79

Turn on : 27.2243

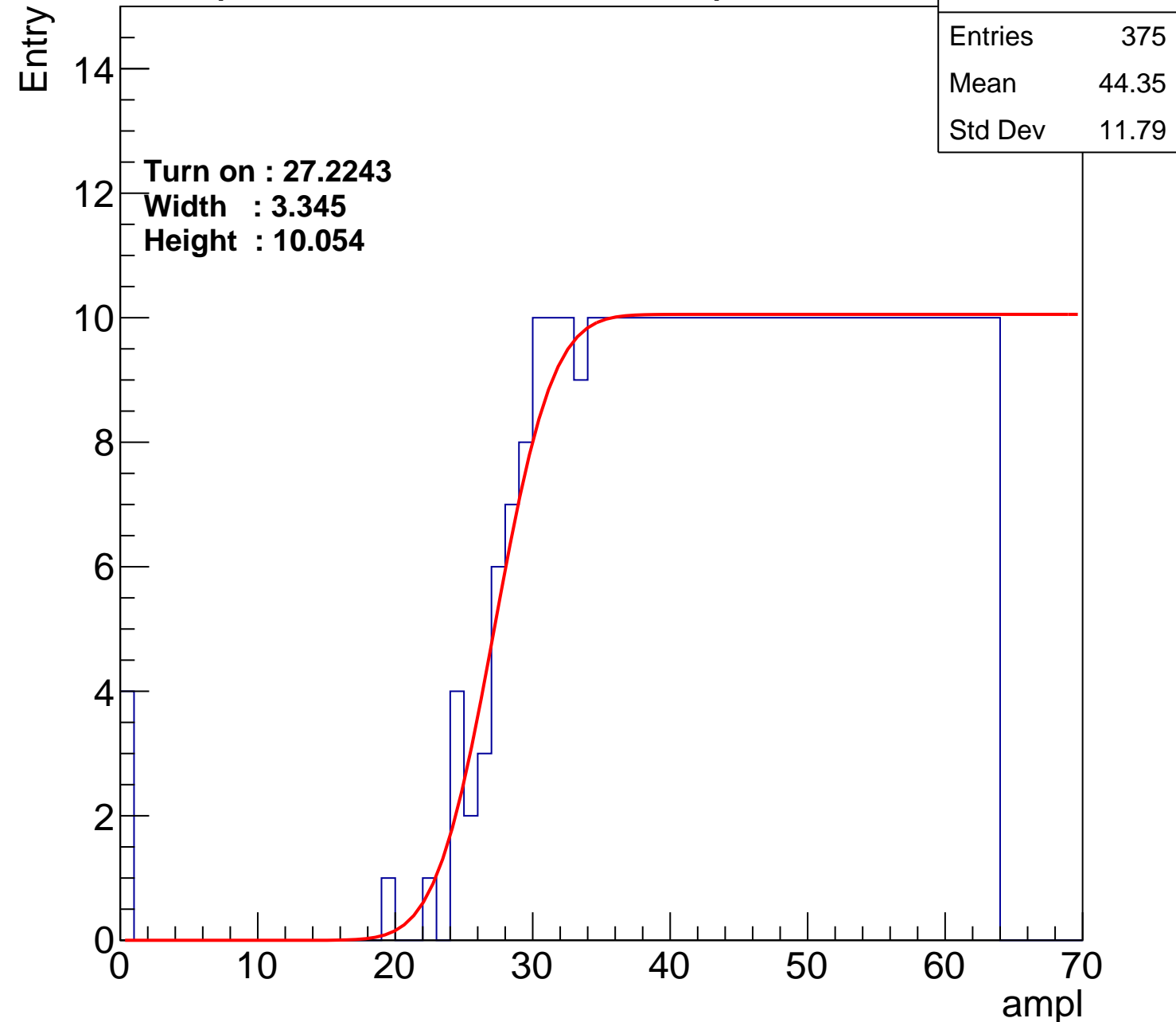
Width : 3.345

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.17
Std Dev	11.44

Turn on : 26.3699

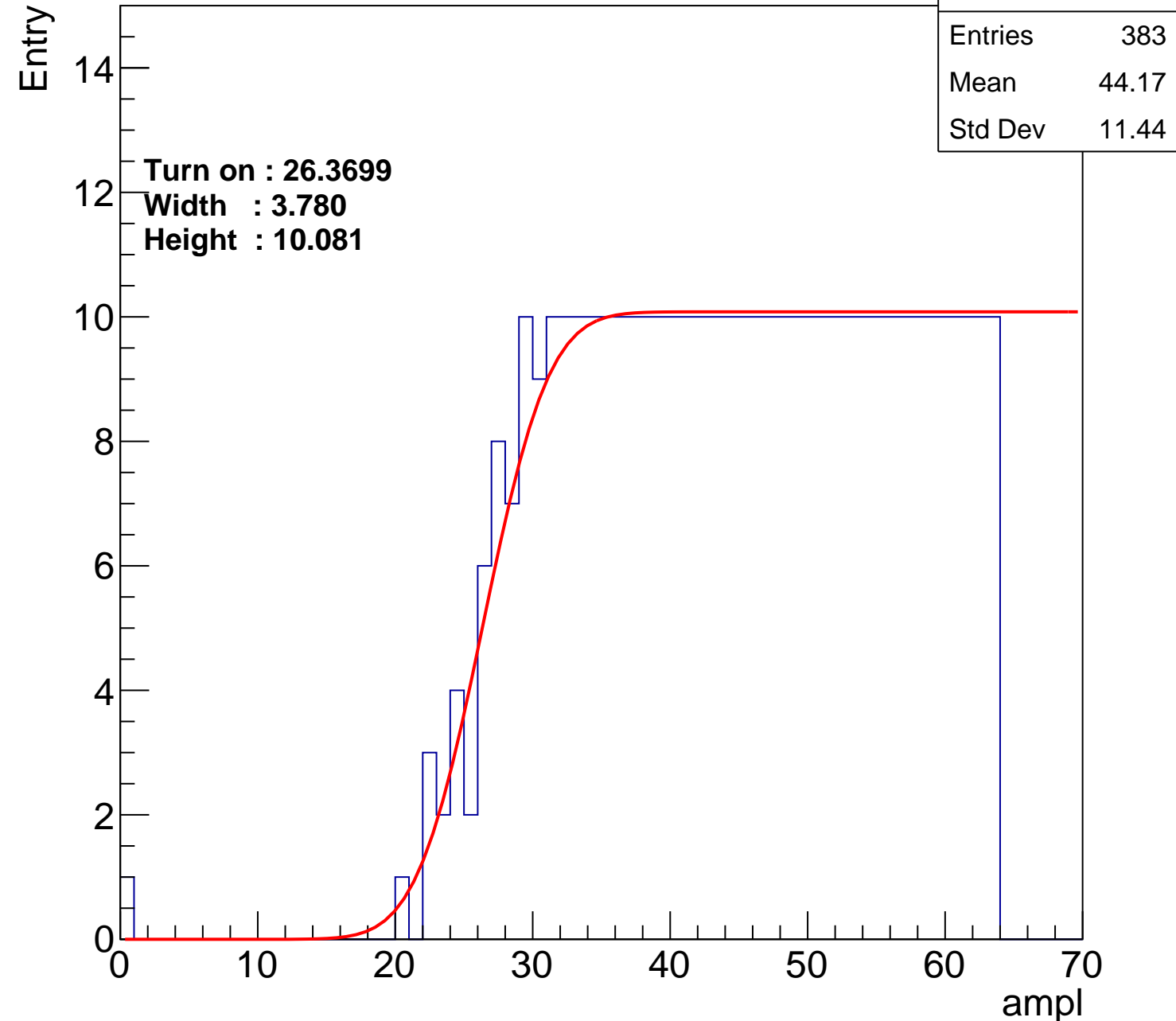
Width : 3.780

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch3

calib_packv5_042523_0143.root, FC#13, port D2

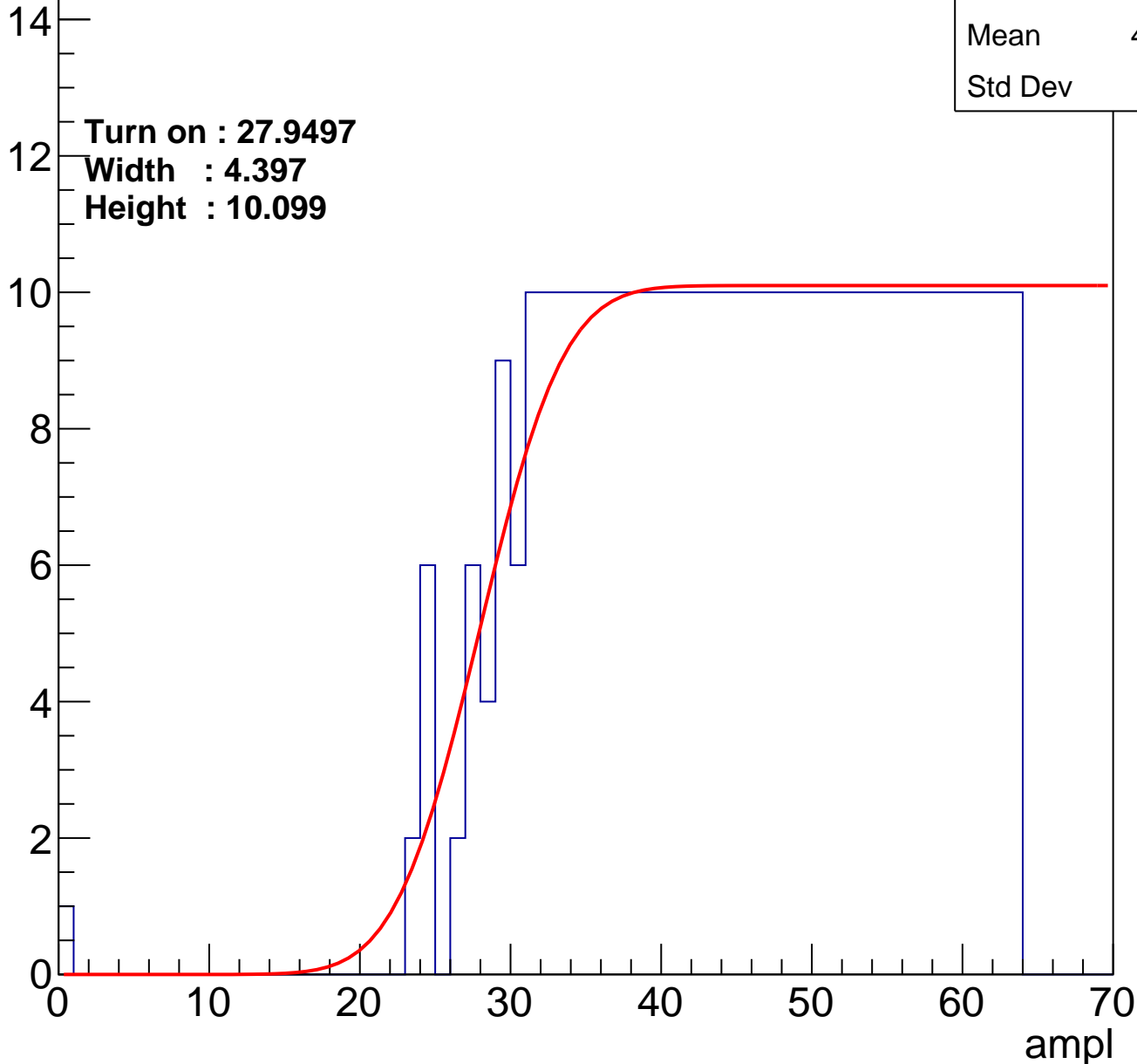
Entries	366
Mean	44.99
Std Dev	11.01

Turn on : 27.9497

Width : 4.397

Height : 10.099

Entry



B1L003S, U20-ch4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.75
Std Dev	11.07

Turn on : 26.8060

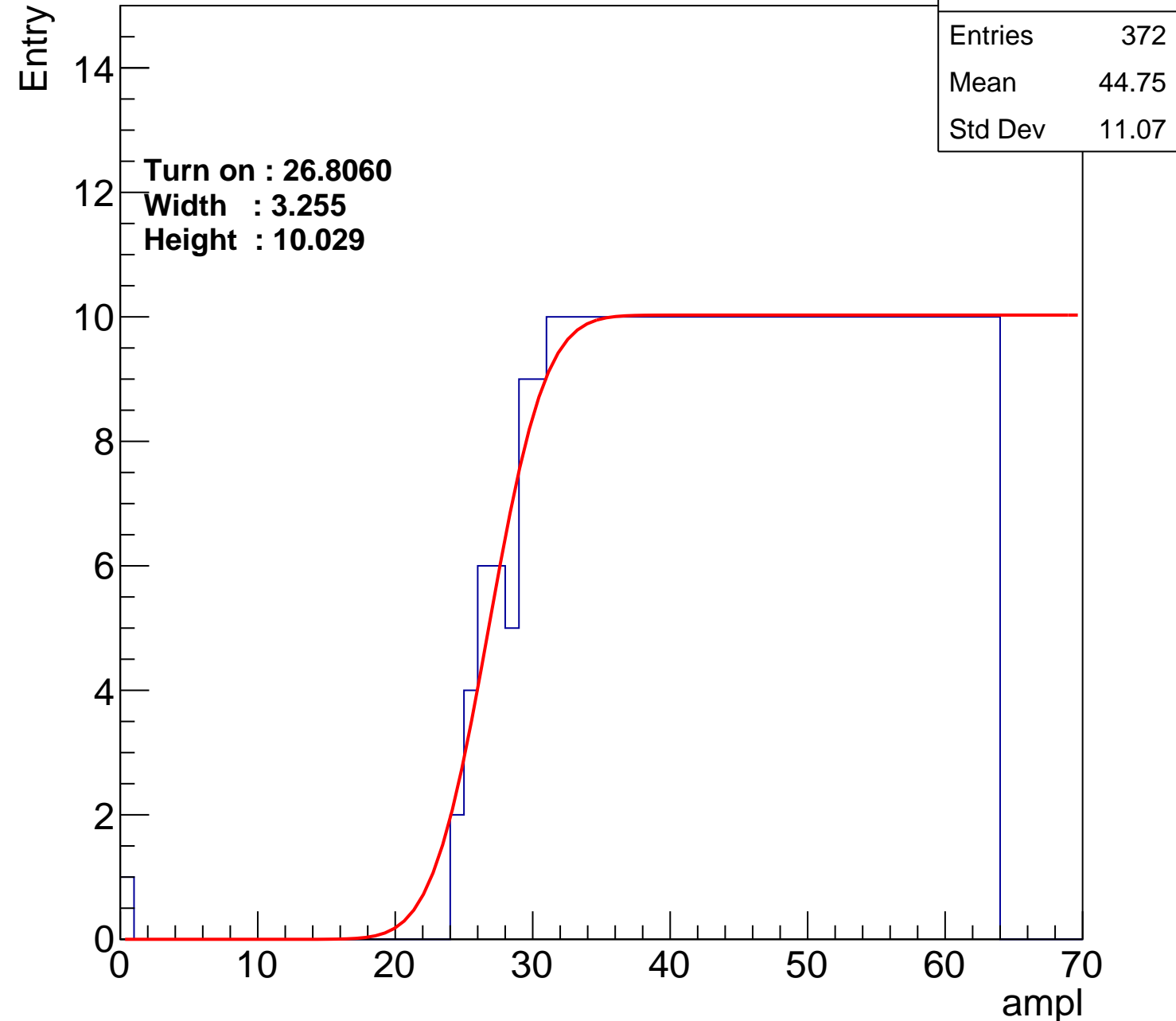
Width : 3.255

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.41
Std Dev	11.64

Turn on : 27.6751

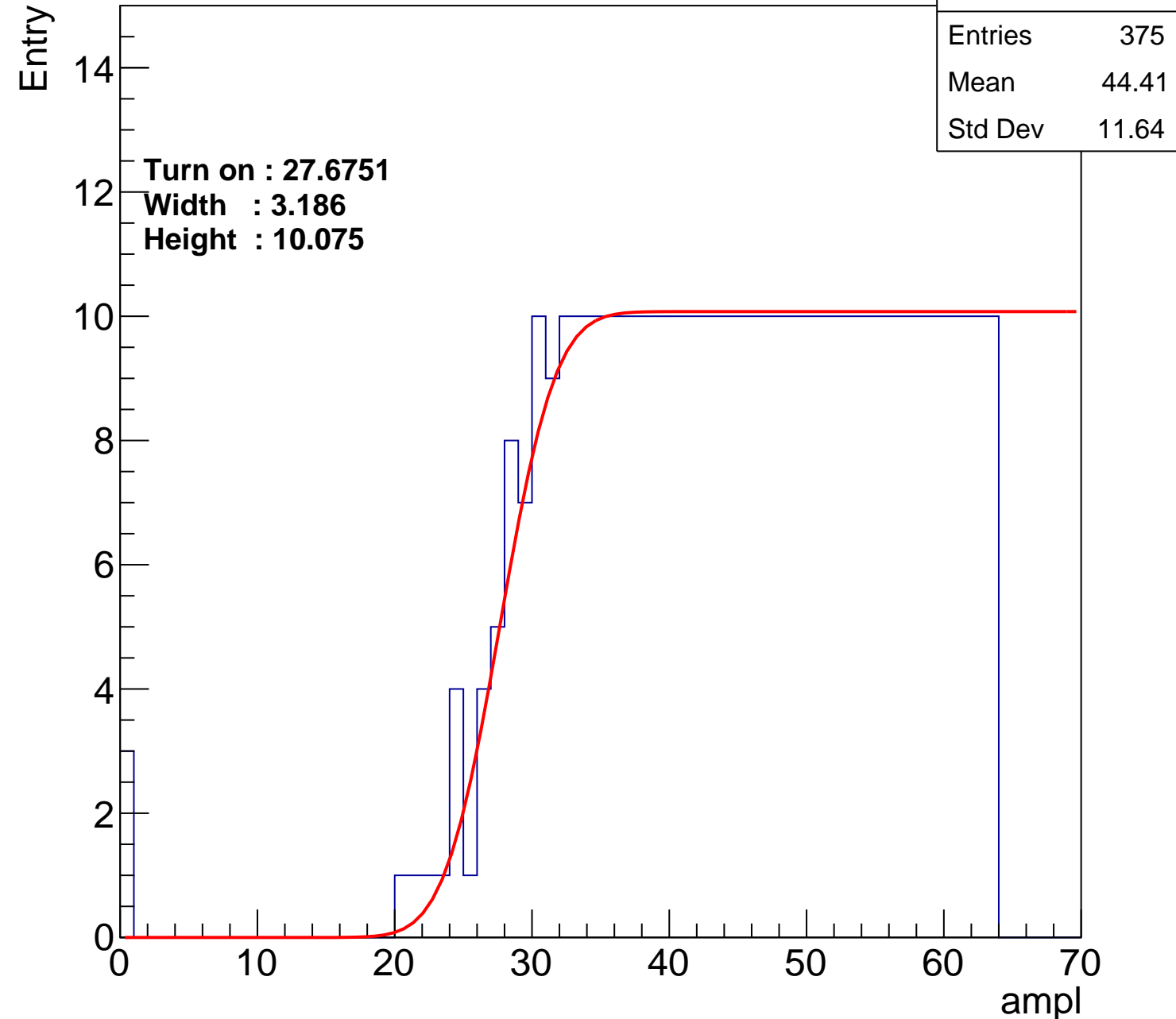
Width : 3.186

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.72
Std Dev	11.59

Turn on : 27.3871

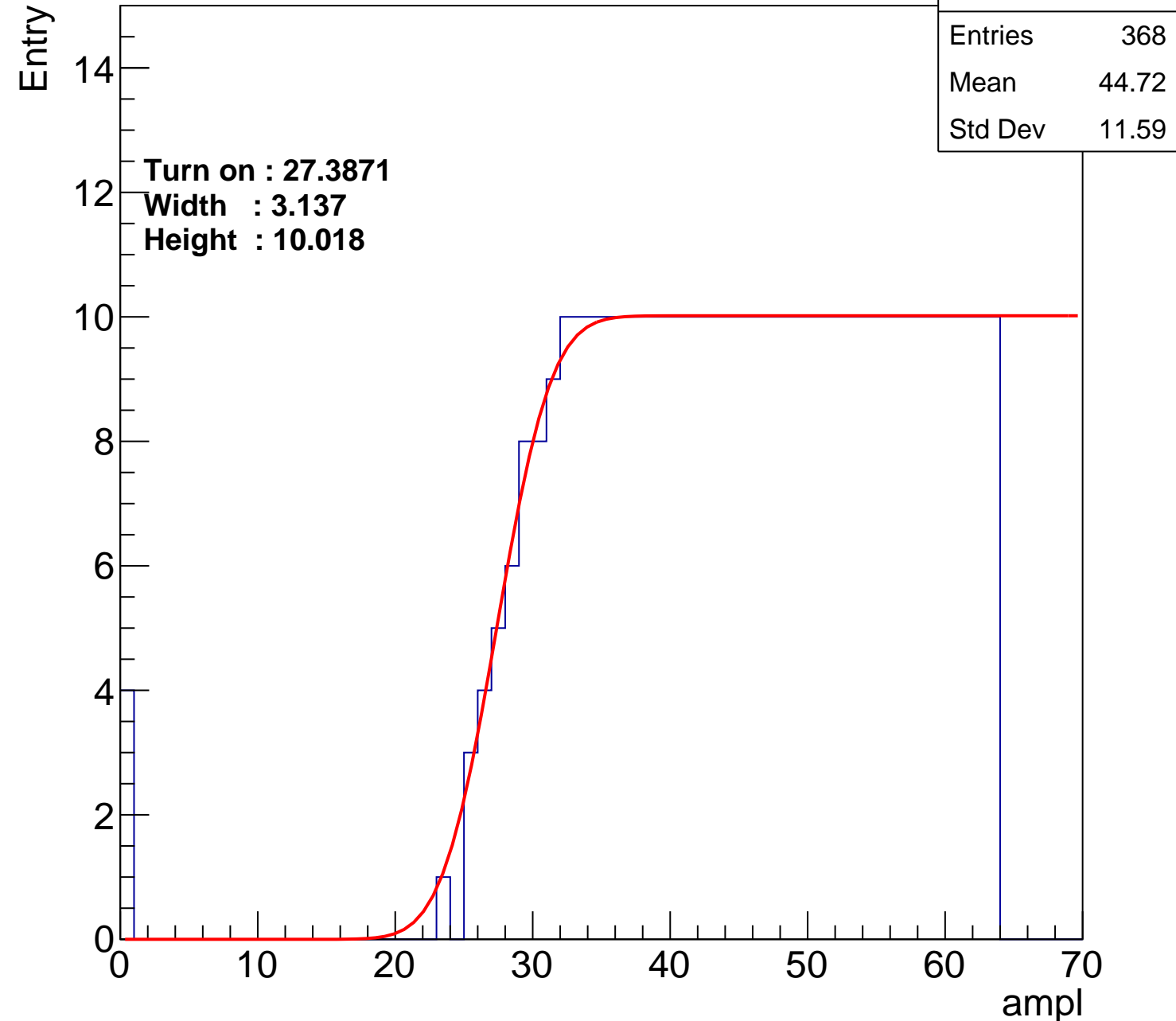
Width : 3.137

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Turn on : 26.7907

Width : 2.637

Height : 10.054

Entries	377
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Mean	44.45
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Std Dev	11.37
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B1L003S, U20-ch8

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 25.9834

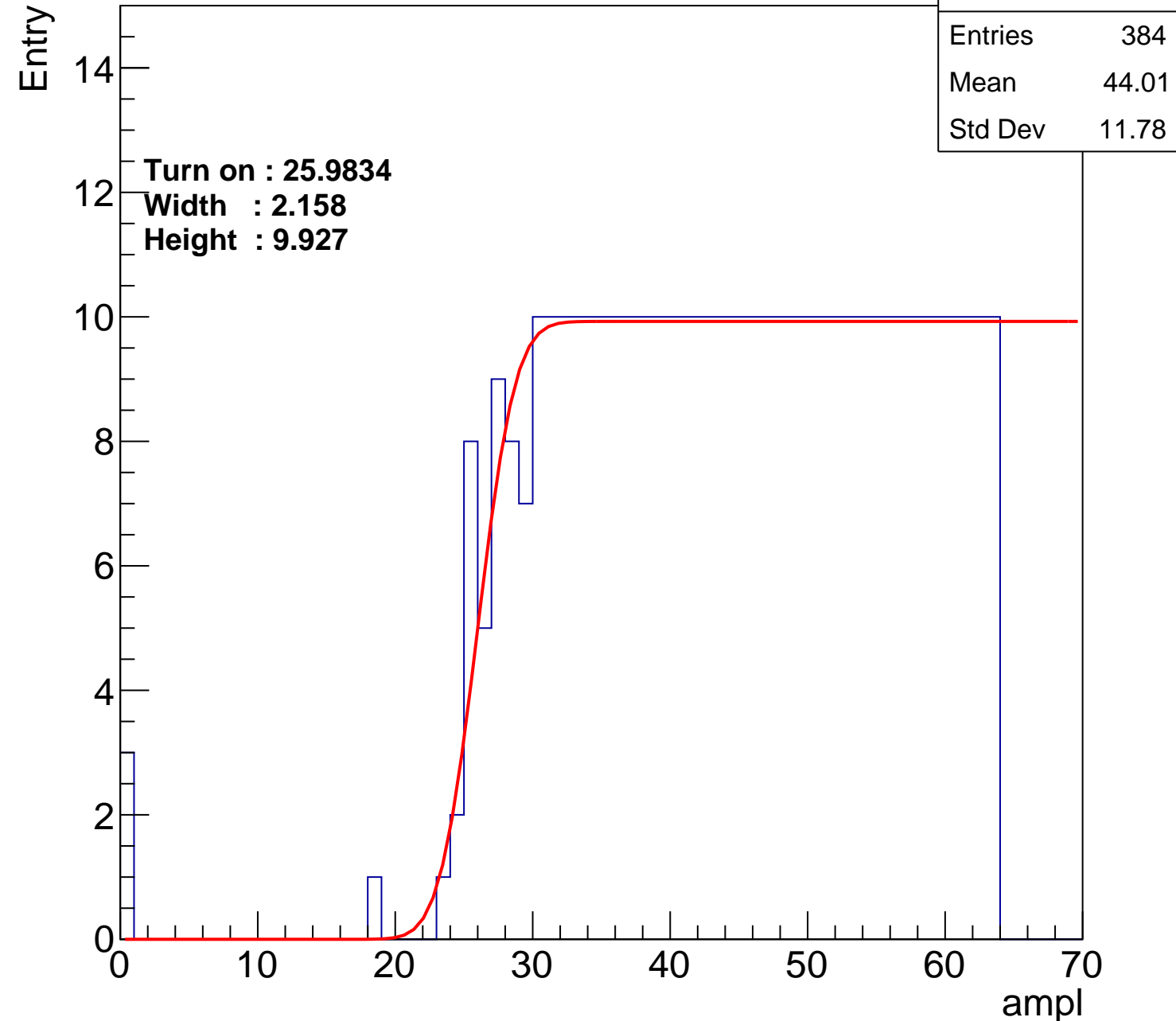
Width : 2.158

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	345
Mean	45.91
Std Dev	10.86

Turn on : 29.8543

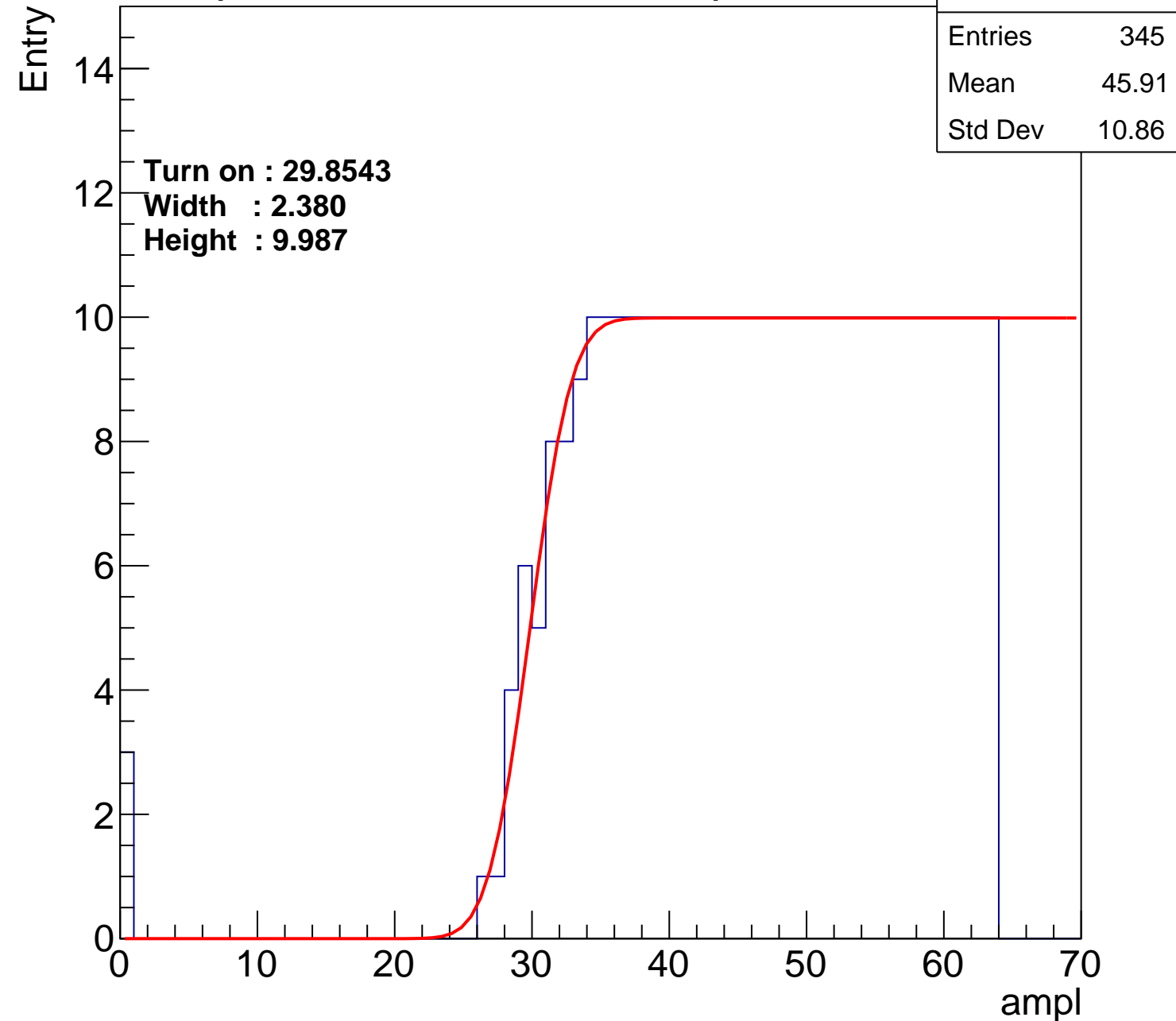
Width : 2.380

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch10

calib_packv5_042523_0143.root, FC#13, port D2

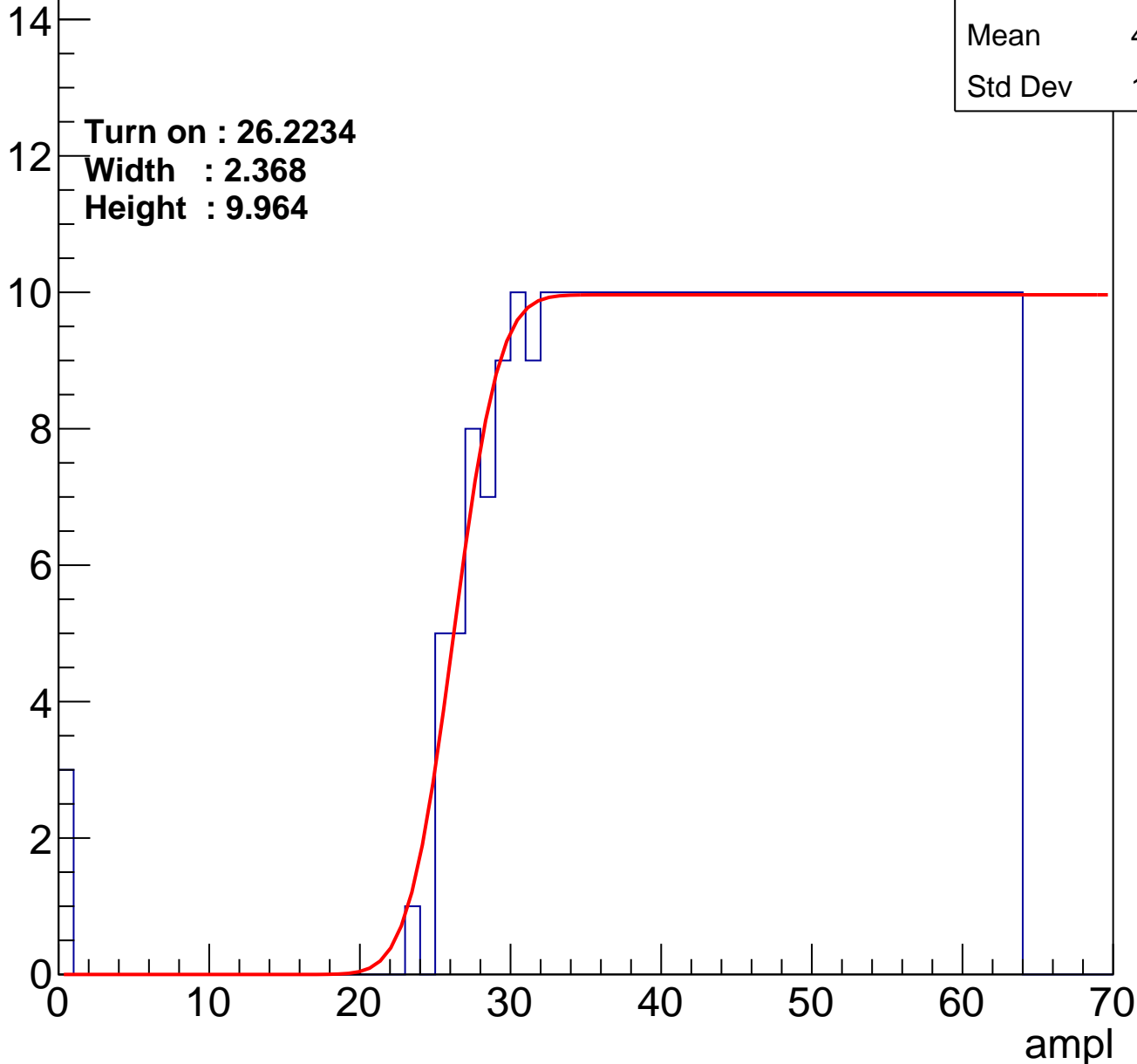
Entry

Entries	377
Mean	44.38
Std Dev	11.57

Turn on : 26.2234

Width : 2.368

Height : 9.964



B1L003S, U20-ch11

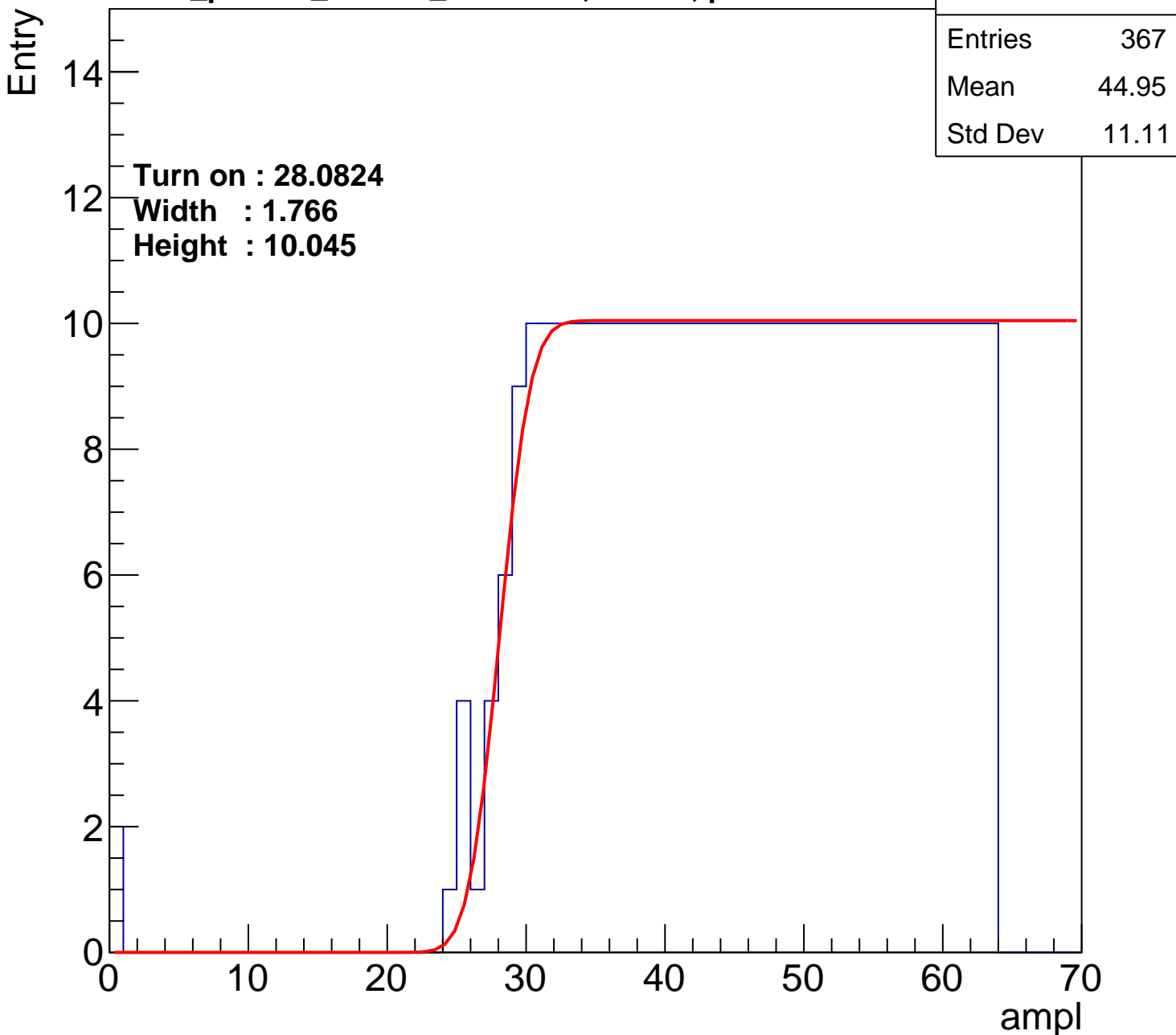
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 28.0824

Width : 1.766

Height : 10.045

Entries	367
Mean	44.95
Std Dev	11.11



B1L003S, U20-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 26.5371

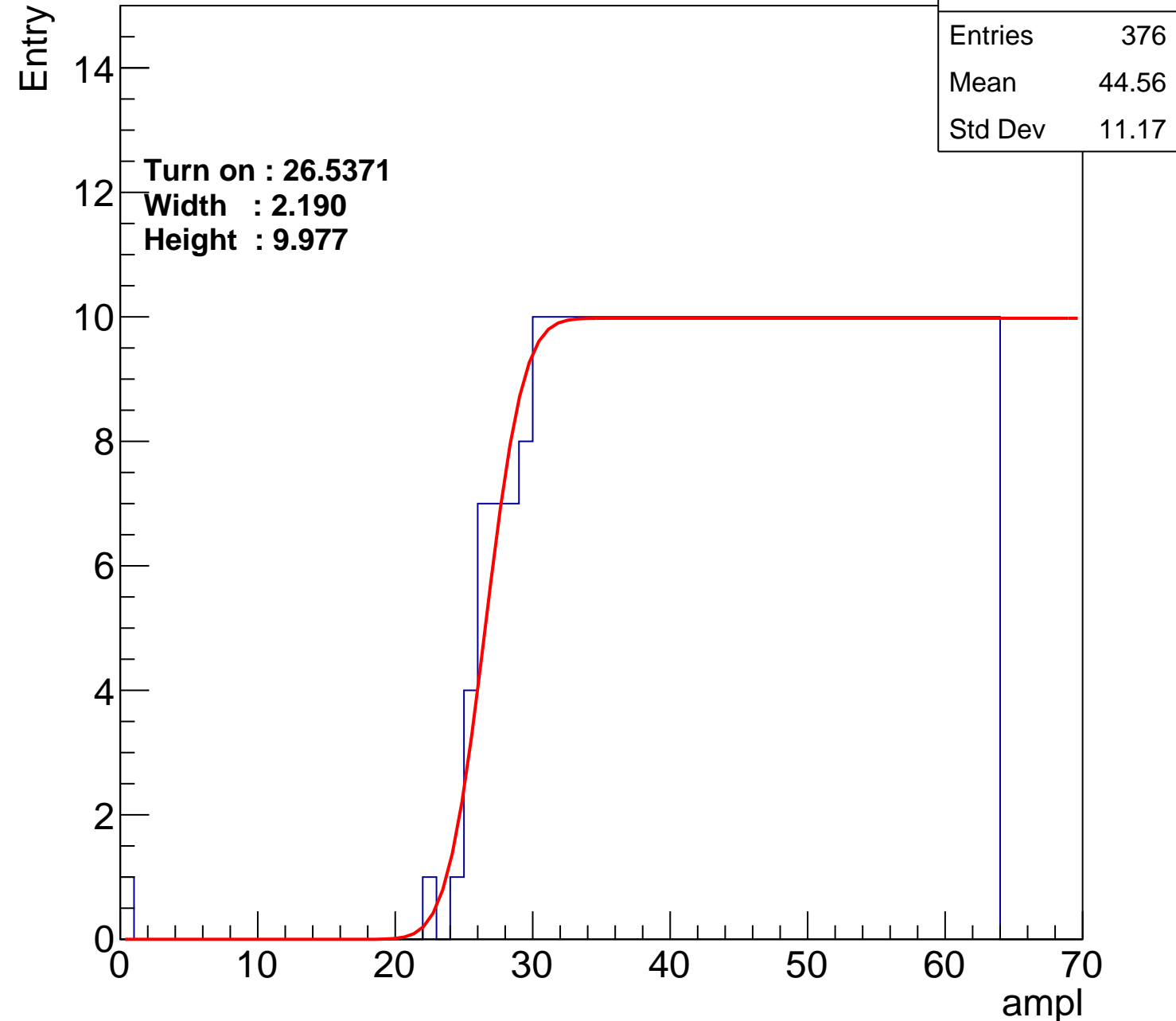
Width : 2.190

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch13

calib_packv5_042523_0143.root, FC#13, port D2

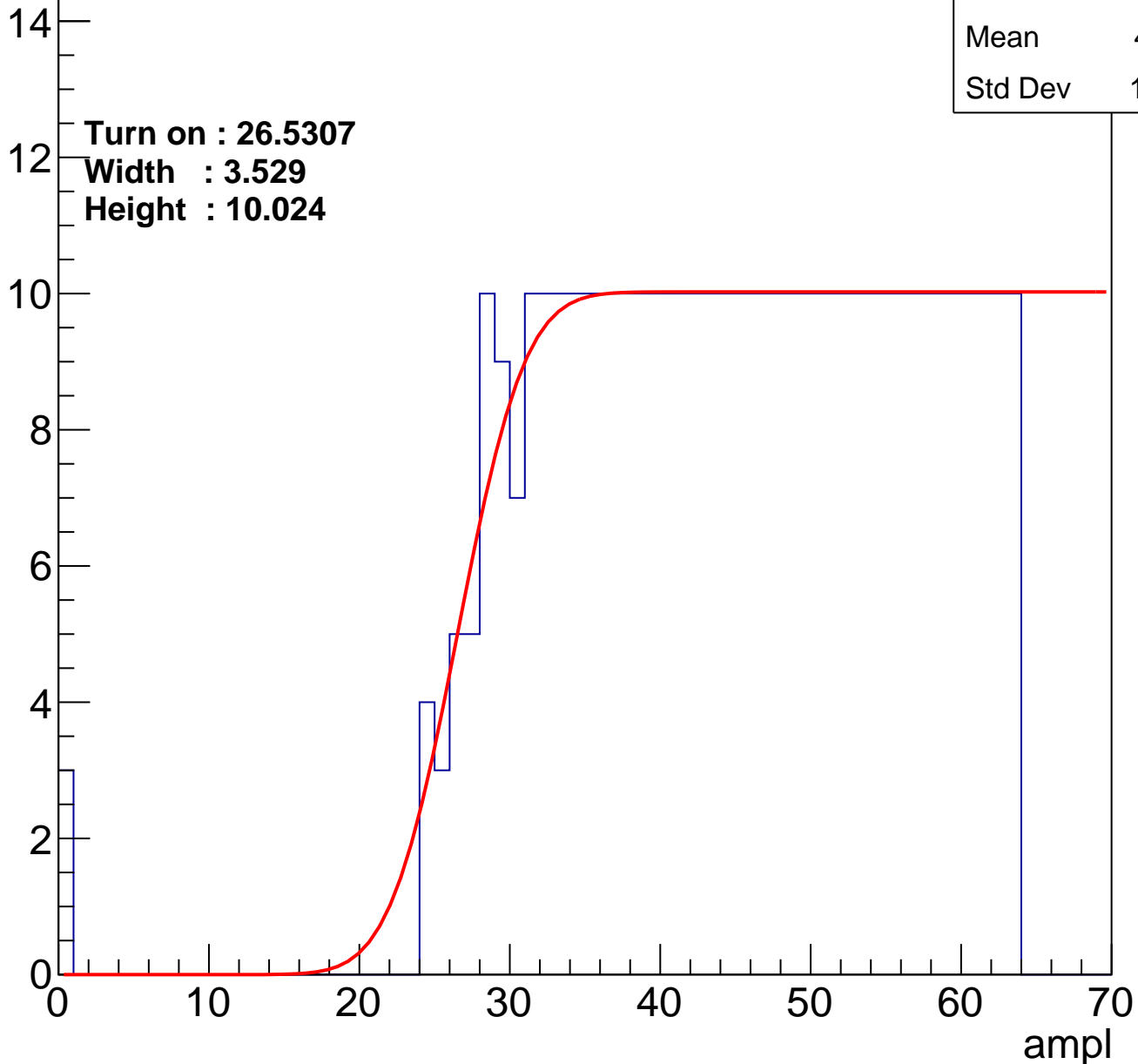
Entries	376
Mean	44.41
Std Dev	11.57

Turn on : 26.5307

Width : 3.529

Height : 10.024

Entry



B1L003S, U20-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entries	348
Mean	45.66
Std Dev	11.18

Turn on : 29.8011

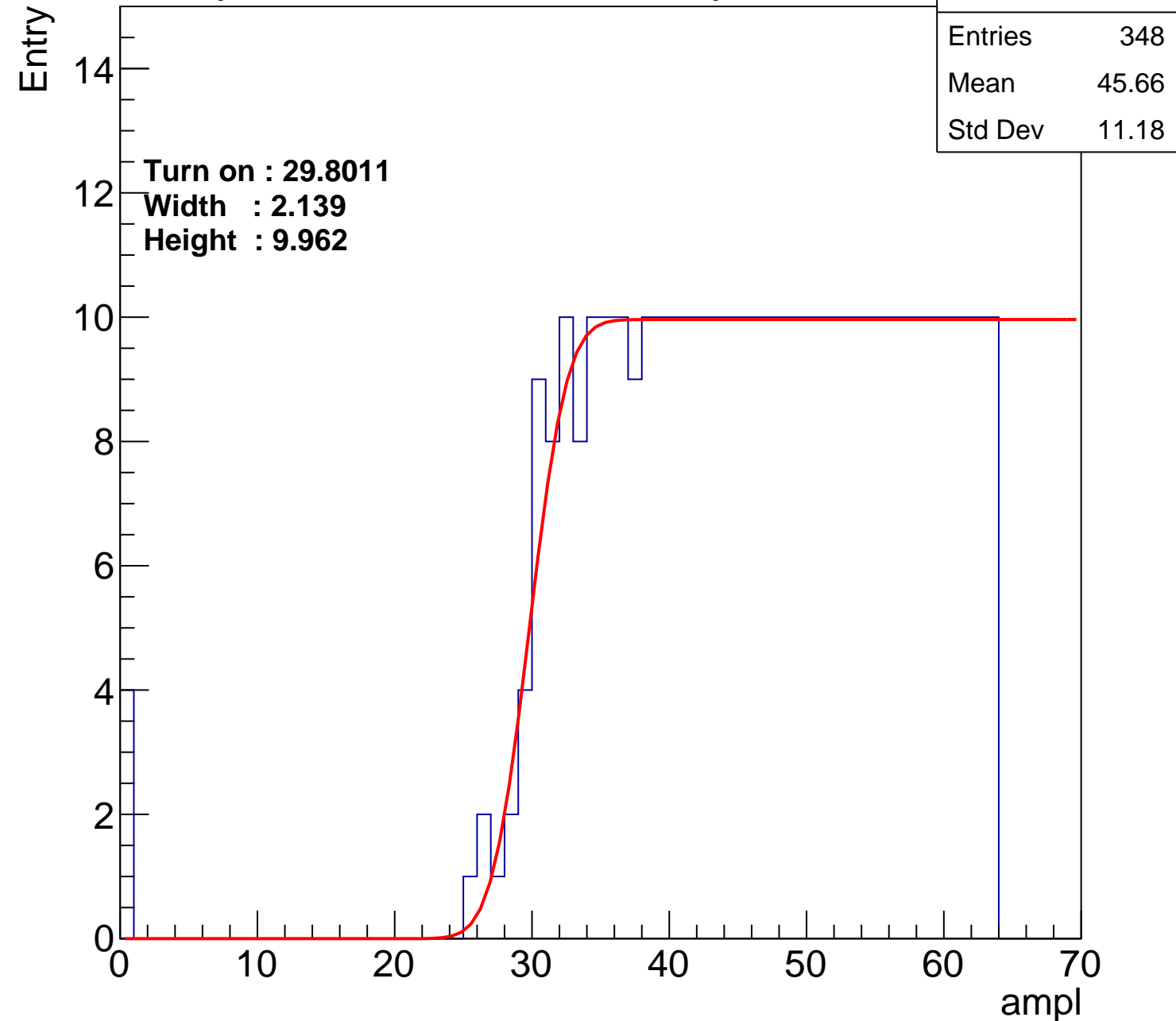
Width : 2.139

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.5587
Width : 3.142
Height : 9.985



B1L003S, U20-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.66
Std Dev	12.25

Turn on : 26.0066

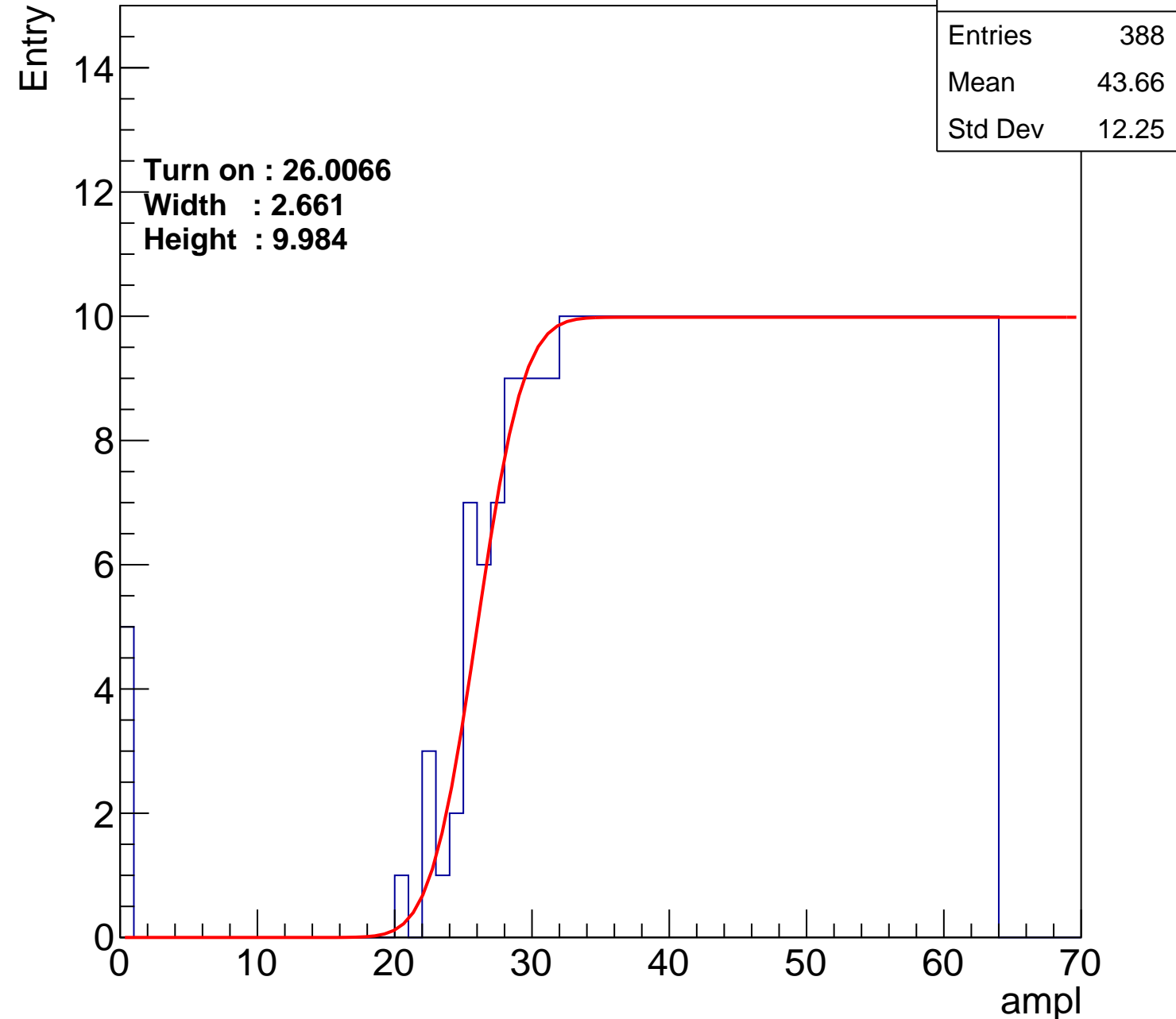
Width : 2.661

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch17

calib_packv5_042523_0143.root, FC#13, port D2

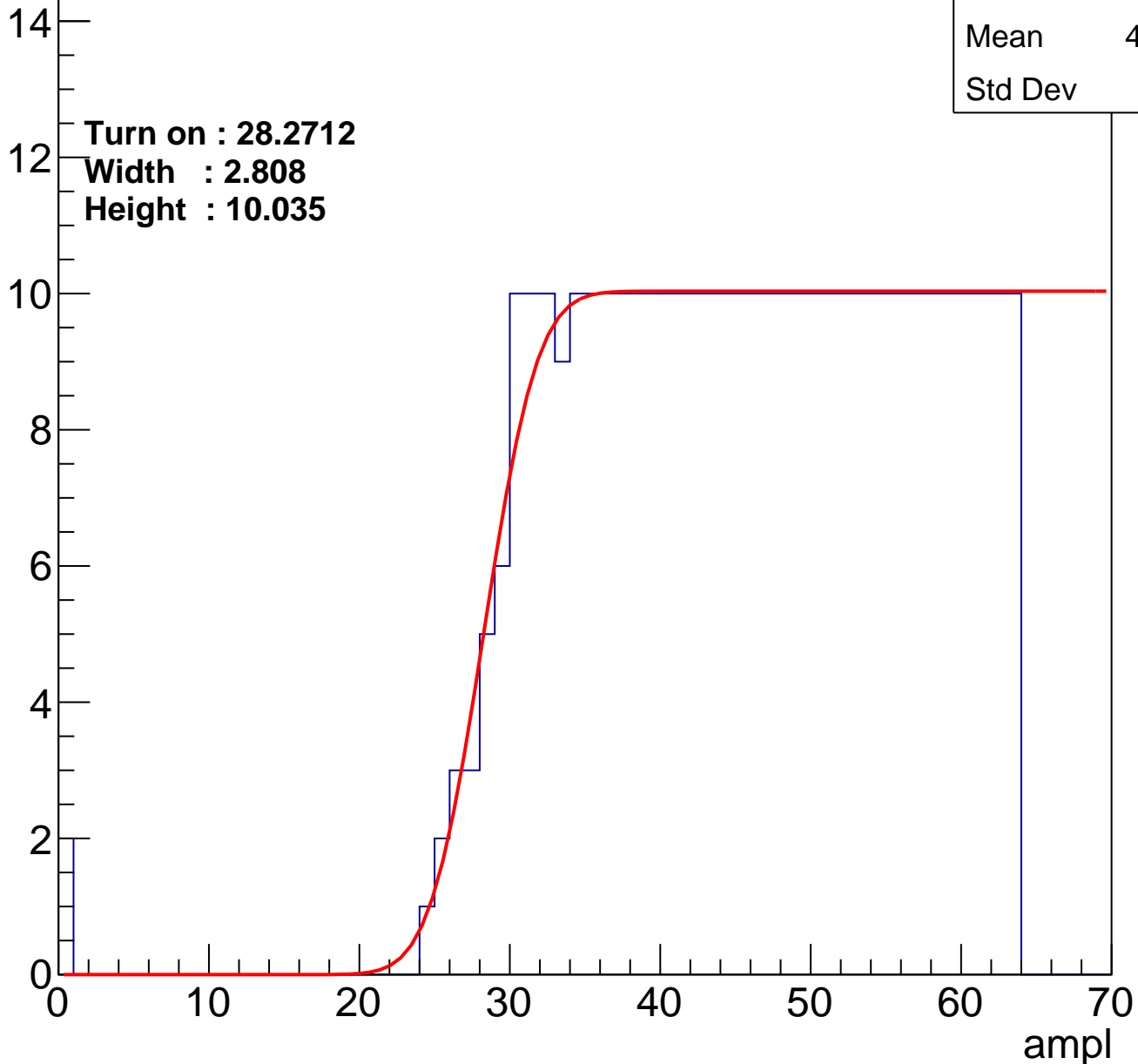
Entries	361
Mean	45.22
Std Dev	11

Turn on : 28.2712

Width : 2.808

Height : 10.035

Entry



B1L003S, U20-ch18

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.01
Std Dev	11.33

Turn on : 28.3448

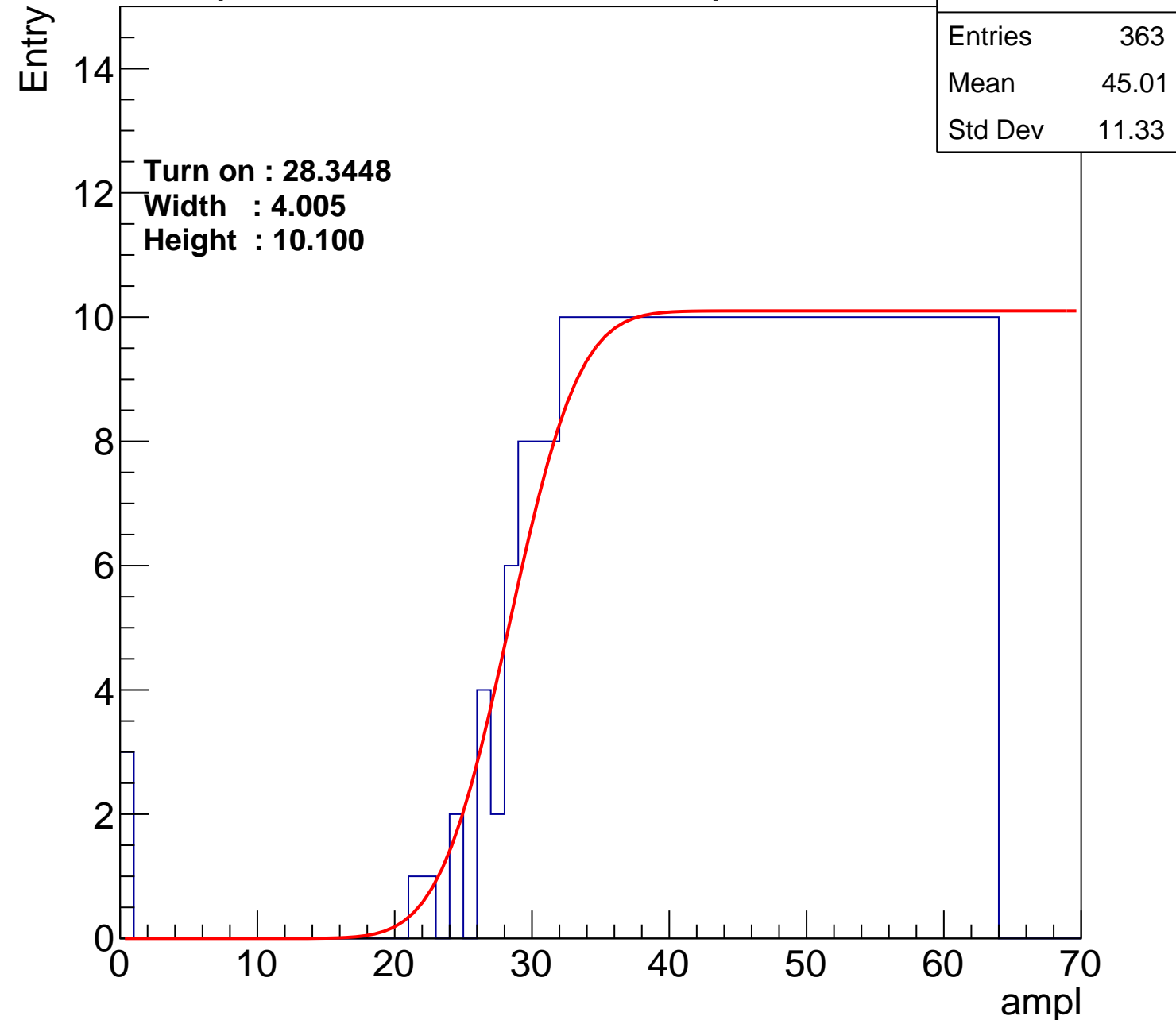
Width : 4.005

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch19

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.9560

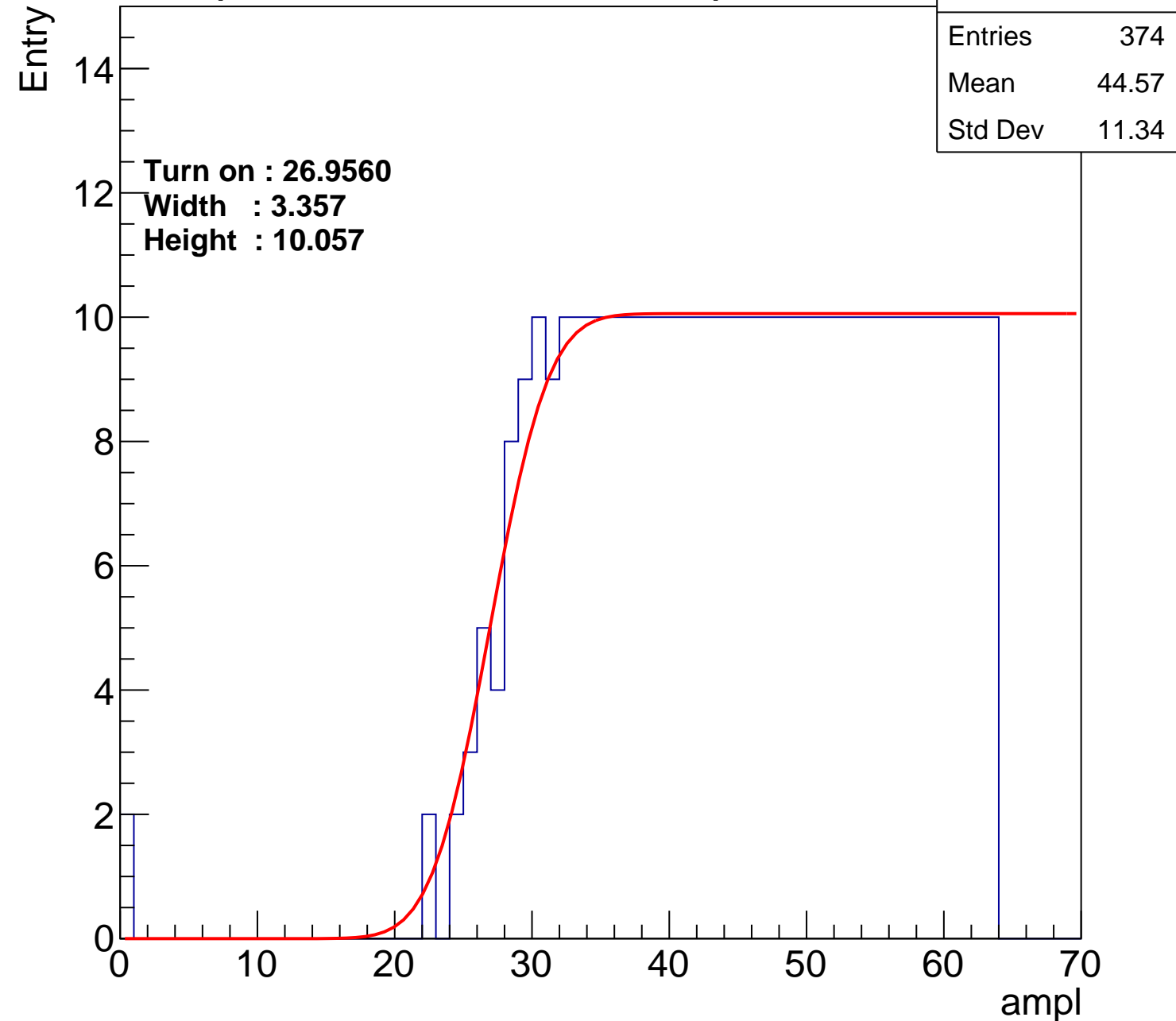
Width : 3.357

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch20

calib_packv5_042523_0143.root, FC#13, port D2

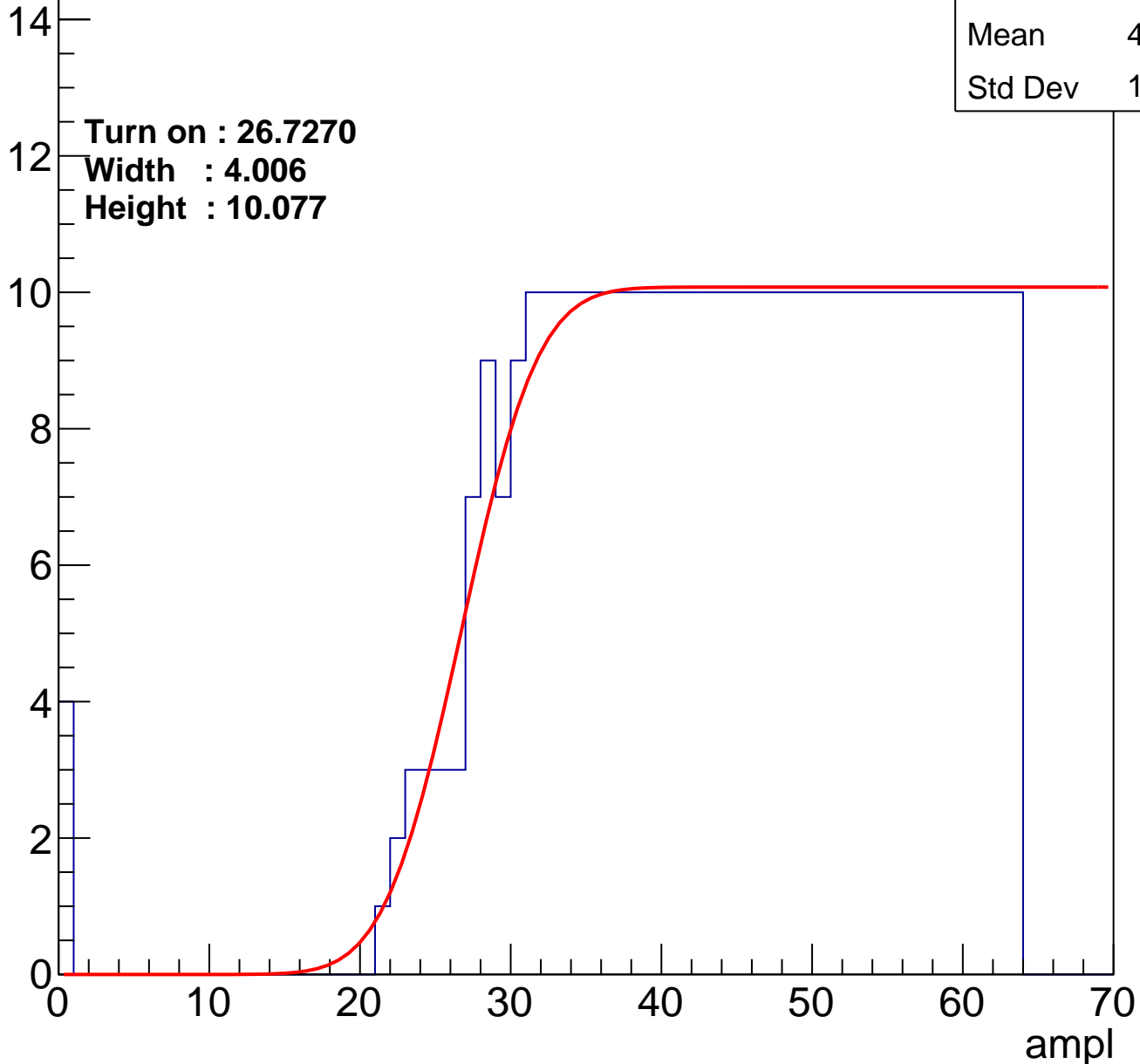
Entries	381
Mean	44.05
Std Dev	11.95

Turn on : 26.7270

Width : 4.006

Height : 10.077

Entry



B1L003S, U20-ch21

calib_packv5_042523_0143.root, FC#13, port D2

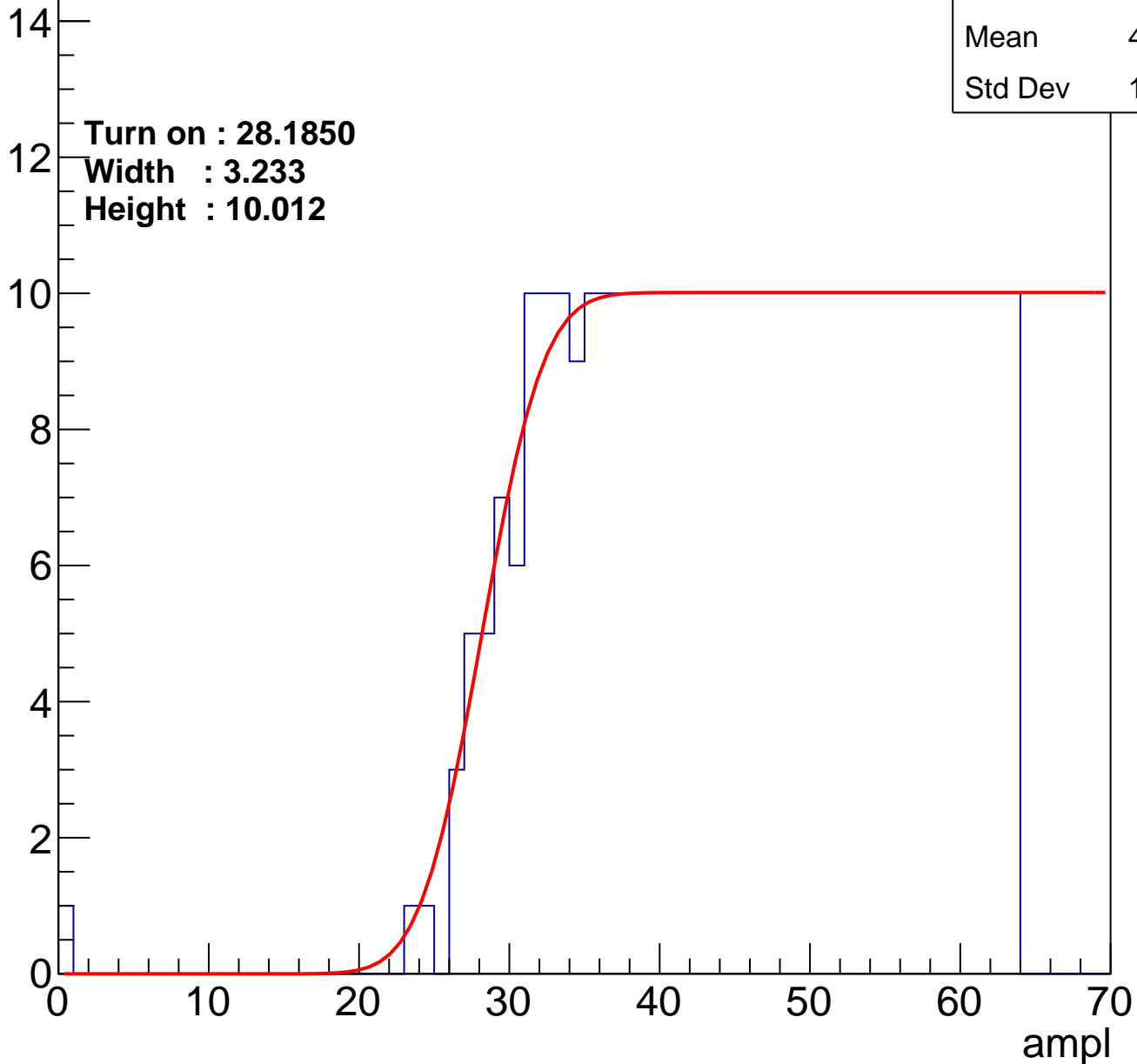
Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 28.1850

Width : 3.233

Height : 10.012

Entry



B1L003S, U20-ch22

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 25.5915

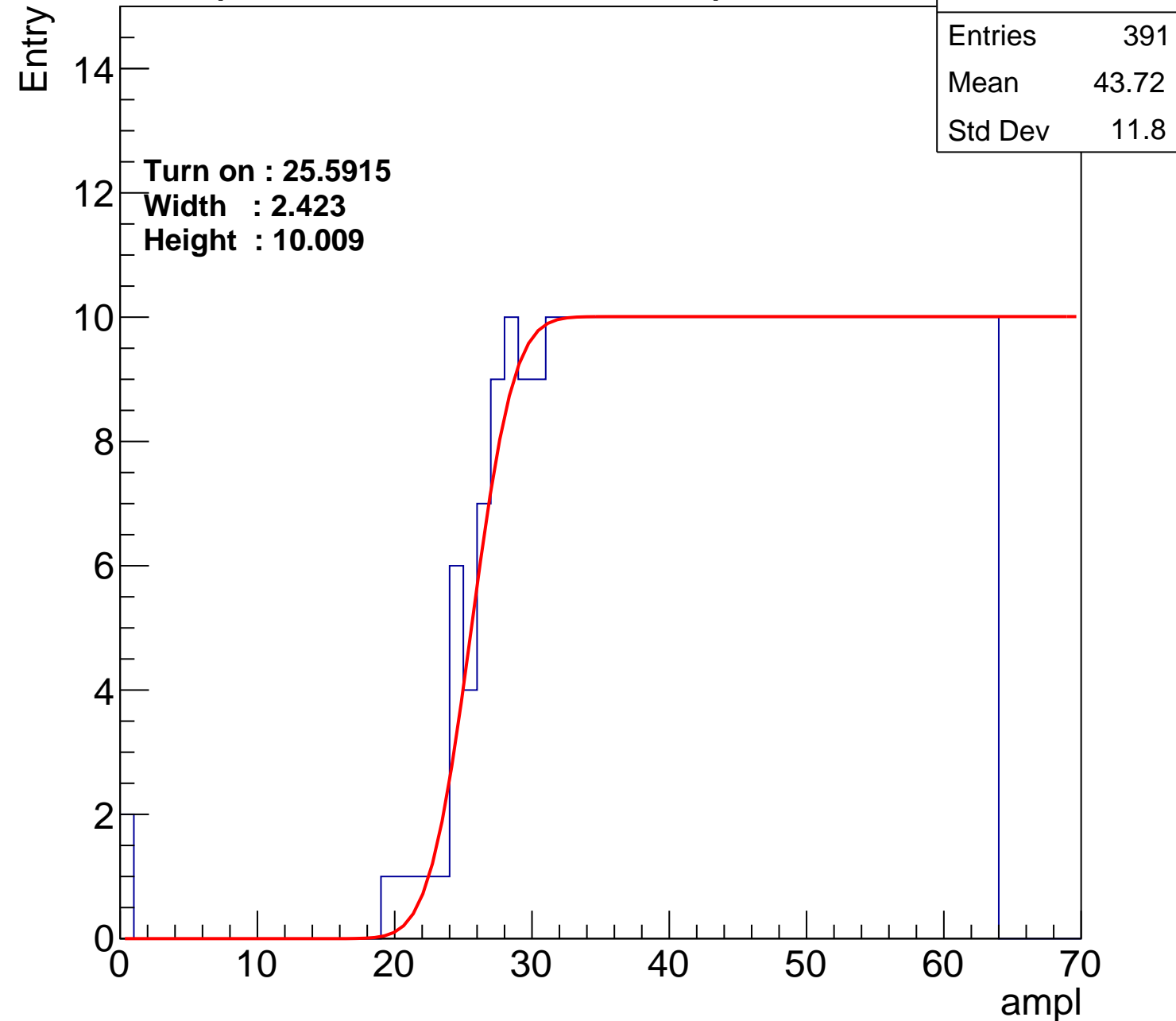
Width : 2.423

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.48
Std Dev	11.25

Turn on : 26.5273

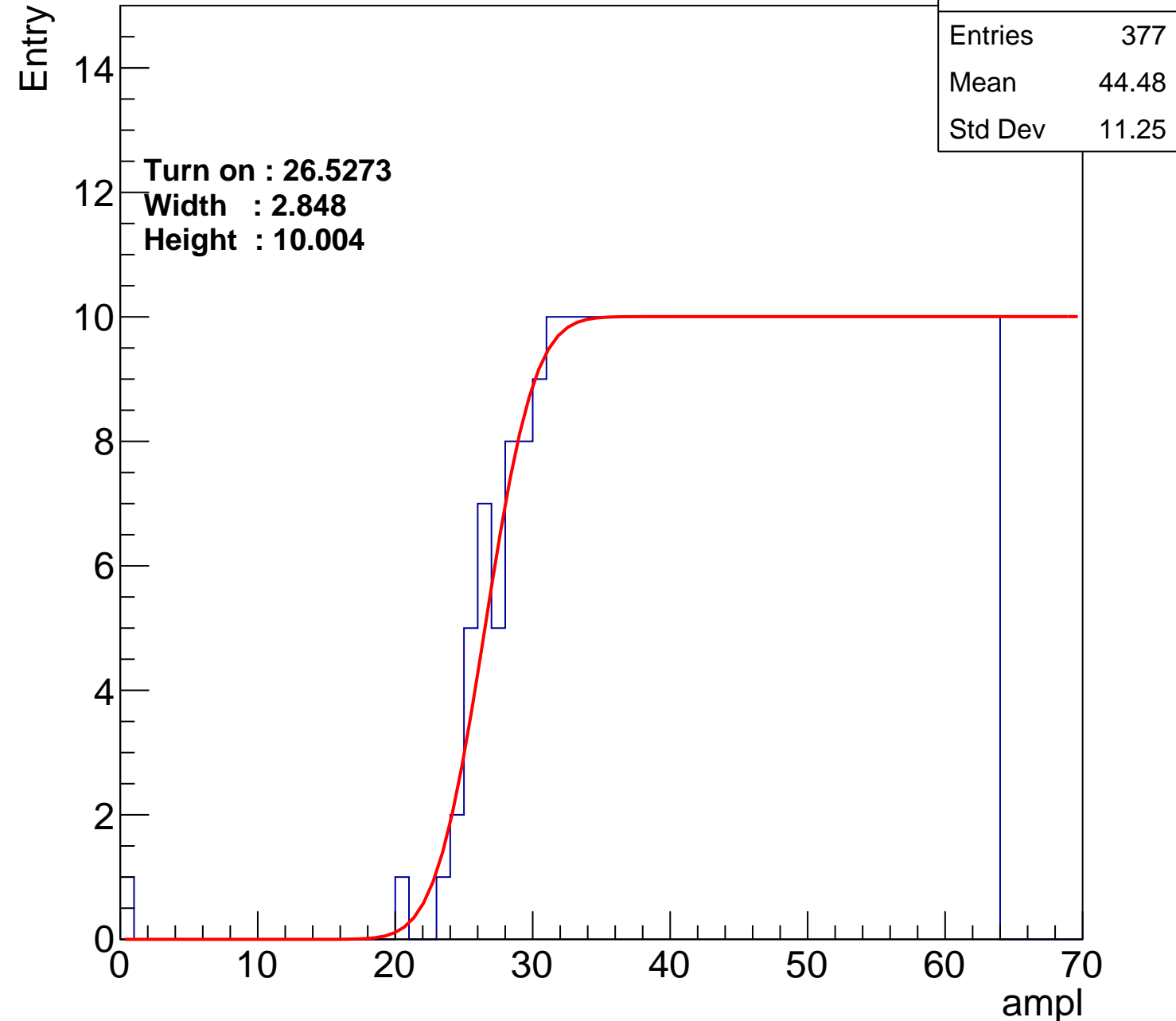
Width : 2.848

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch24

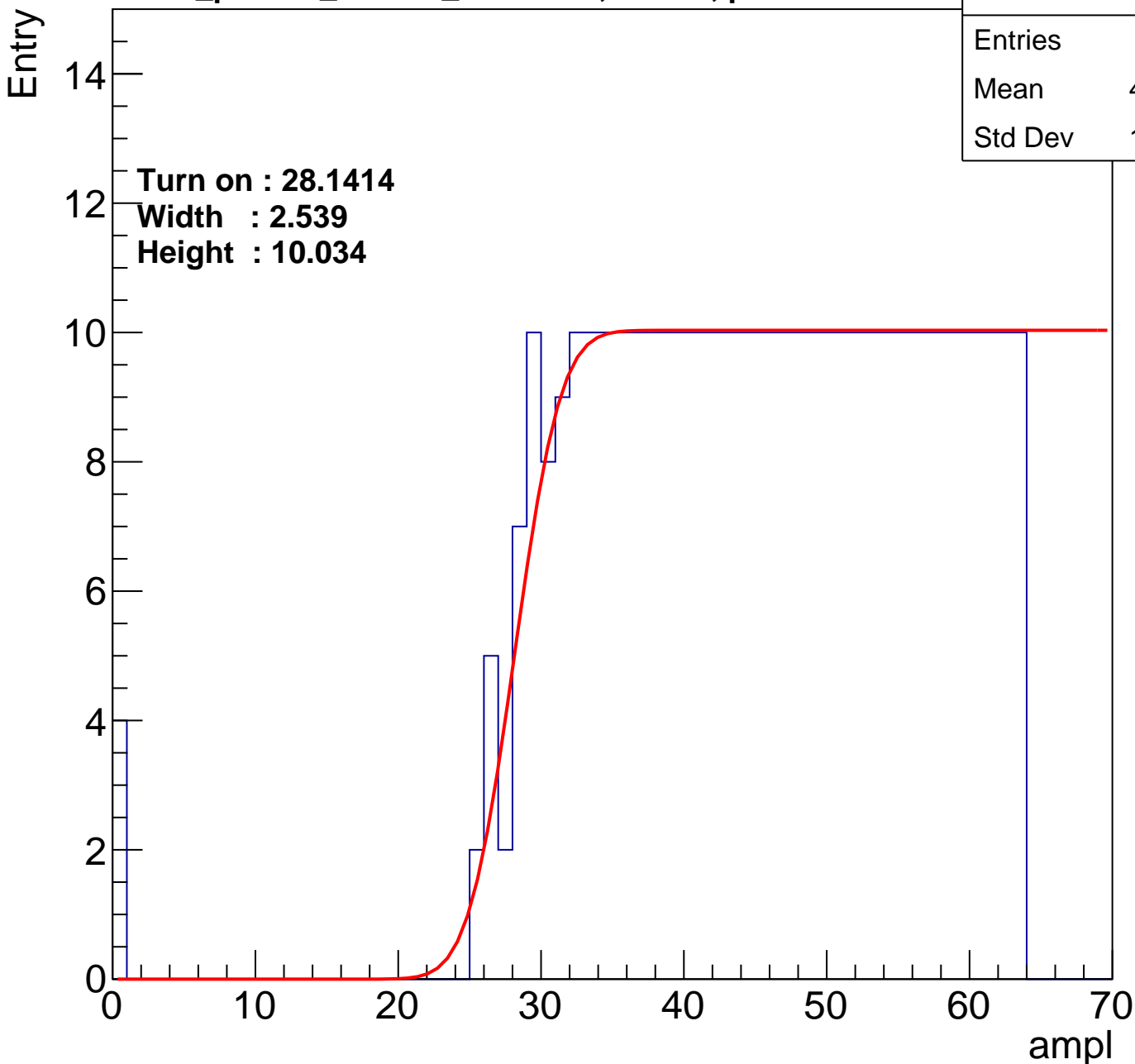
calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.79
Std Dev	11.53

Turn on : 28.1414

Width : 2.539

Height : 10.034



B1L003S, U20-ch25

calib_packv5_042523_0143.root, FC#13, port D2

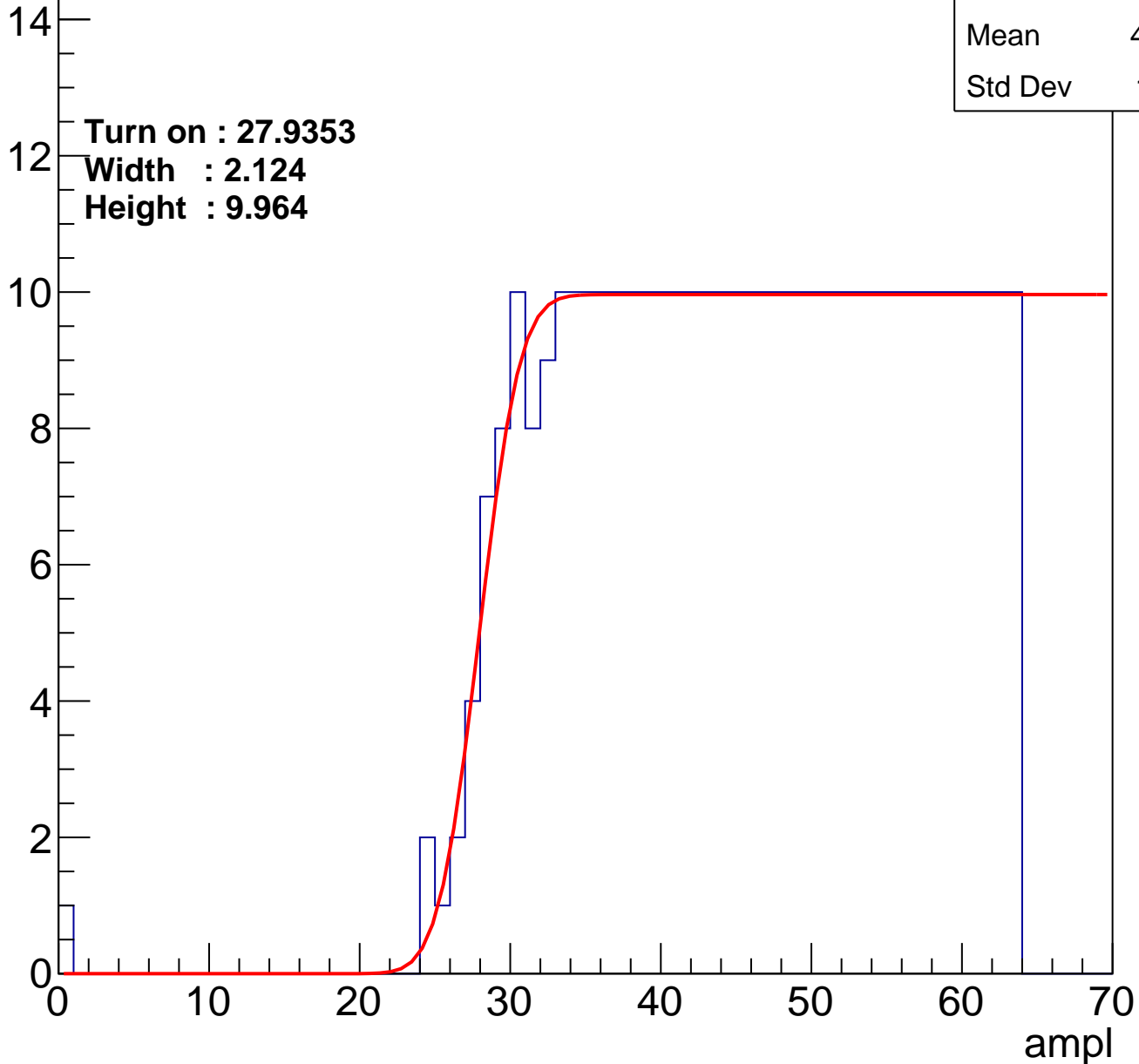
Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 27.9353

Width : 2.124

Height : 9.964

Entry



B1L003S, U20-ch26

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.37
Std Dev	10.71

Turn on : 28.1648

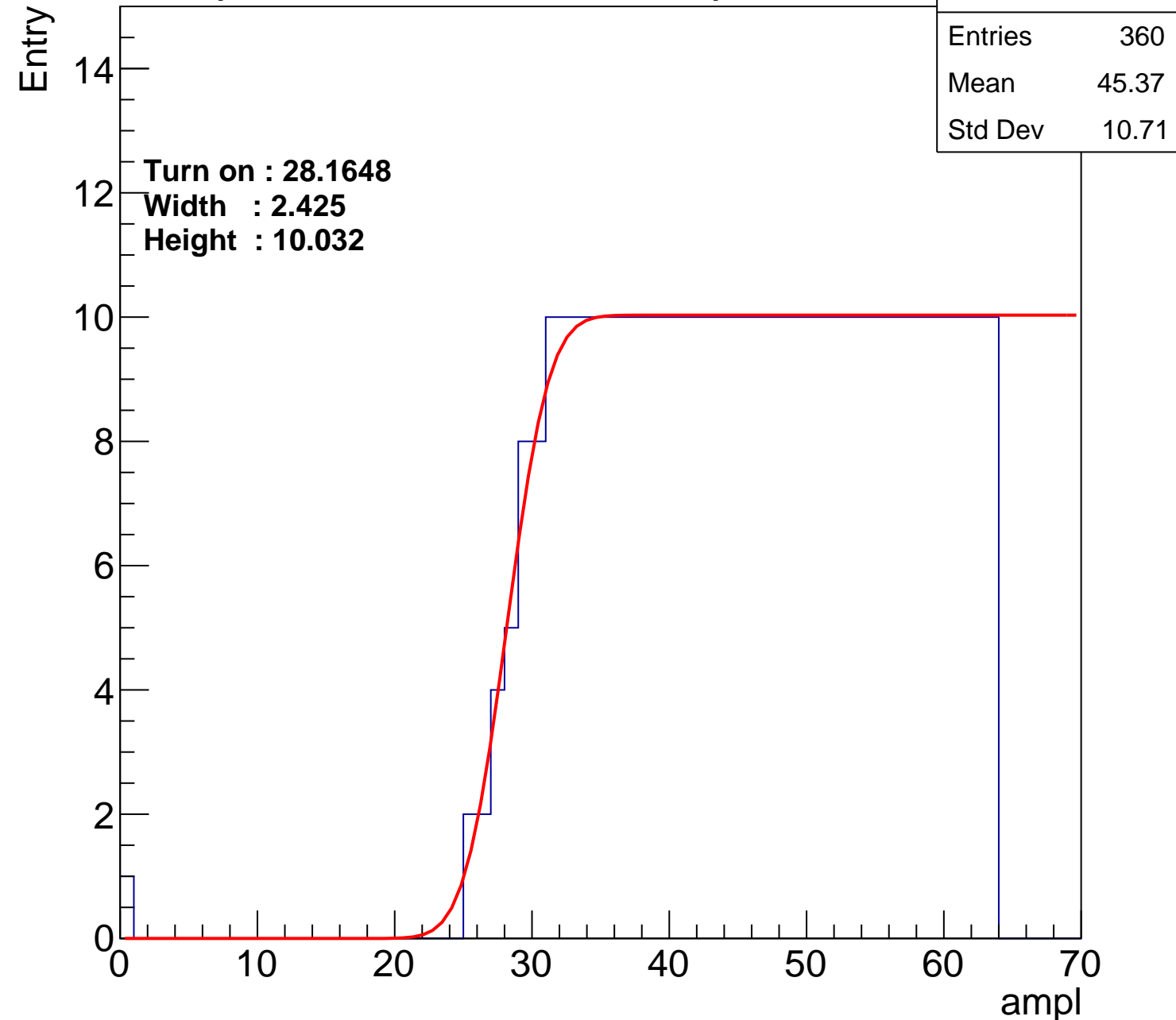
Width : 2.425

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 26.1454

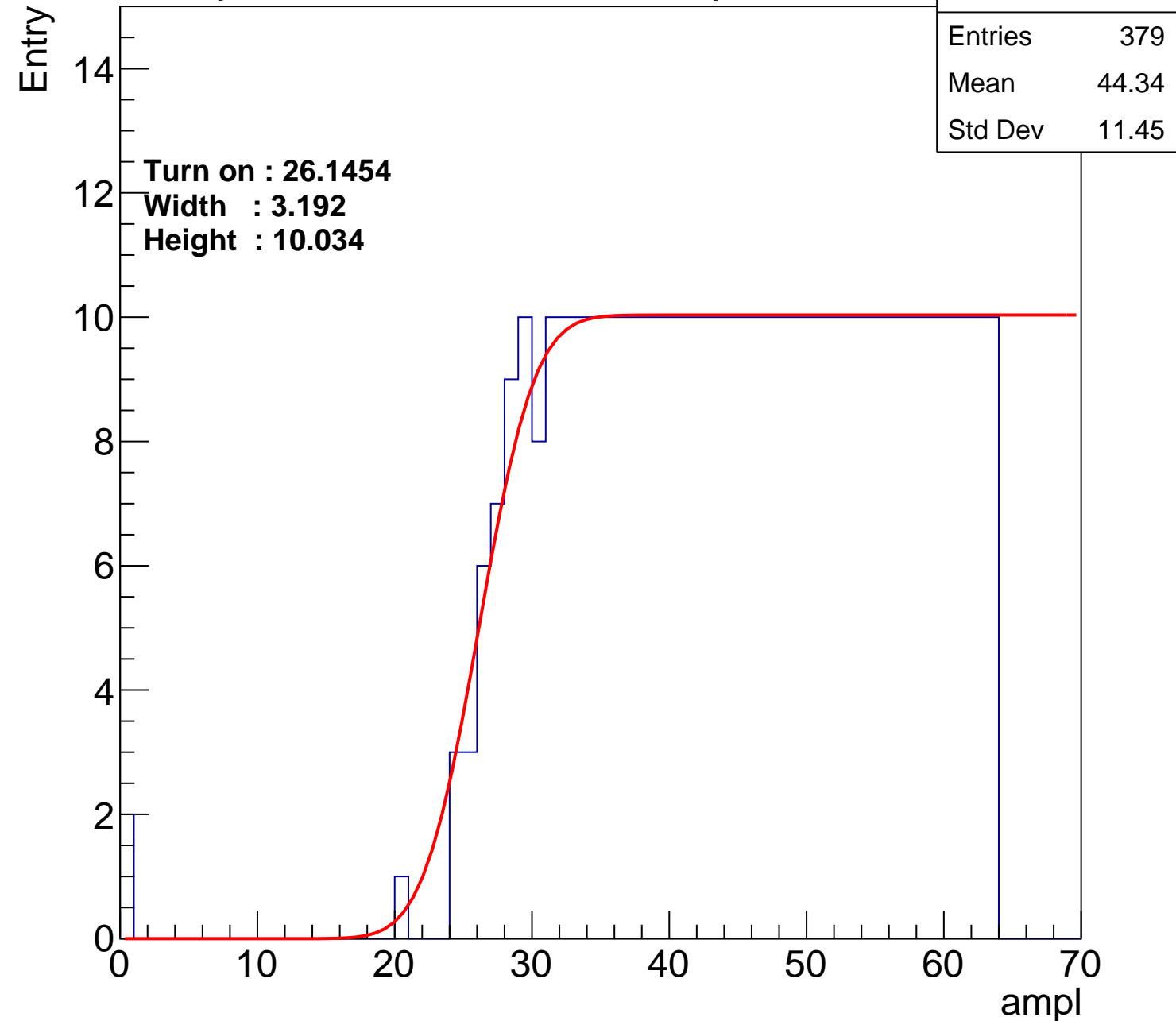
Width : 3.192

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch28

calib_packv5_042523_0143.root, FC#13, port D2

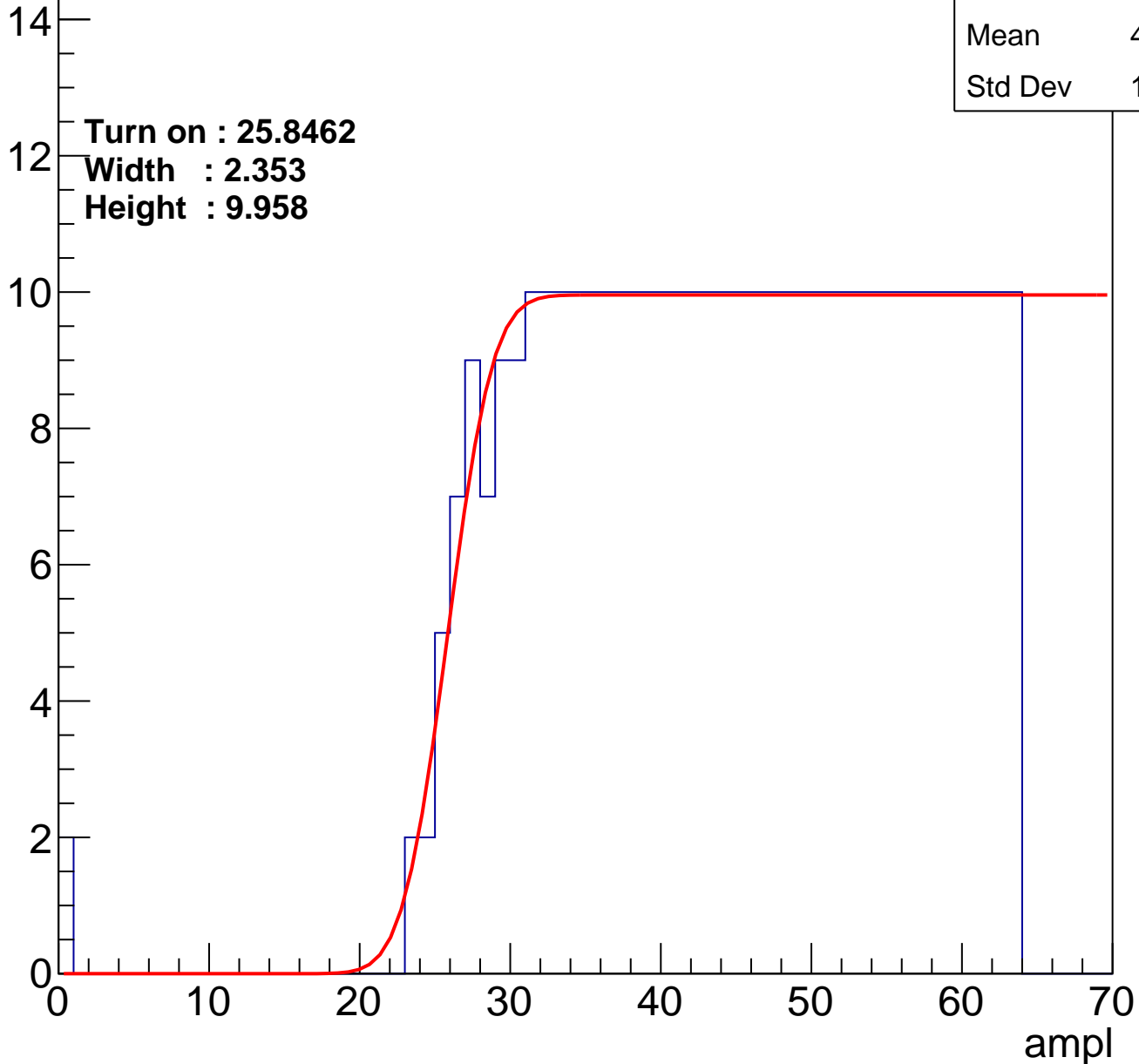
Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 25.8462

Width : 2.353

Height : 9.958

Entry



B1L003S, U20-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 26.8663

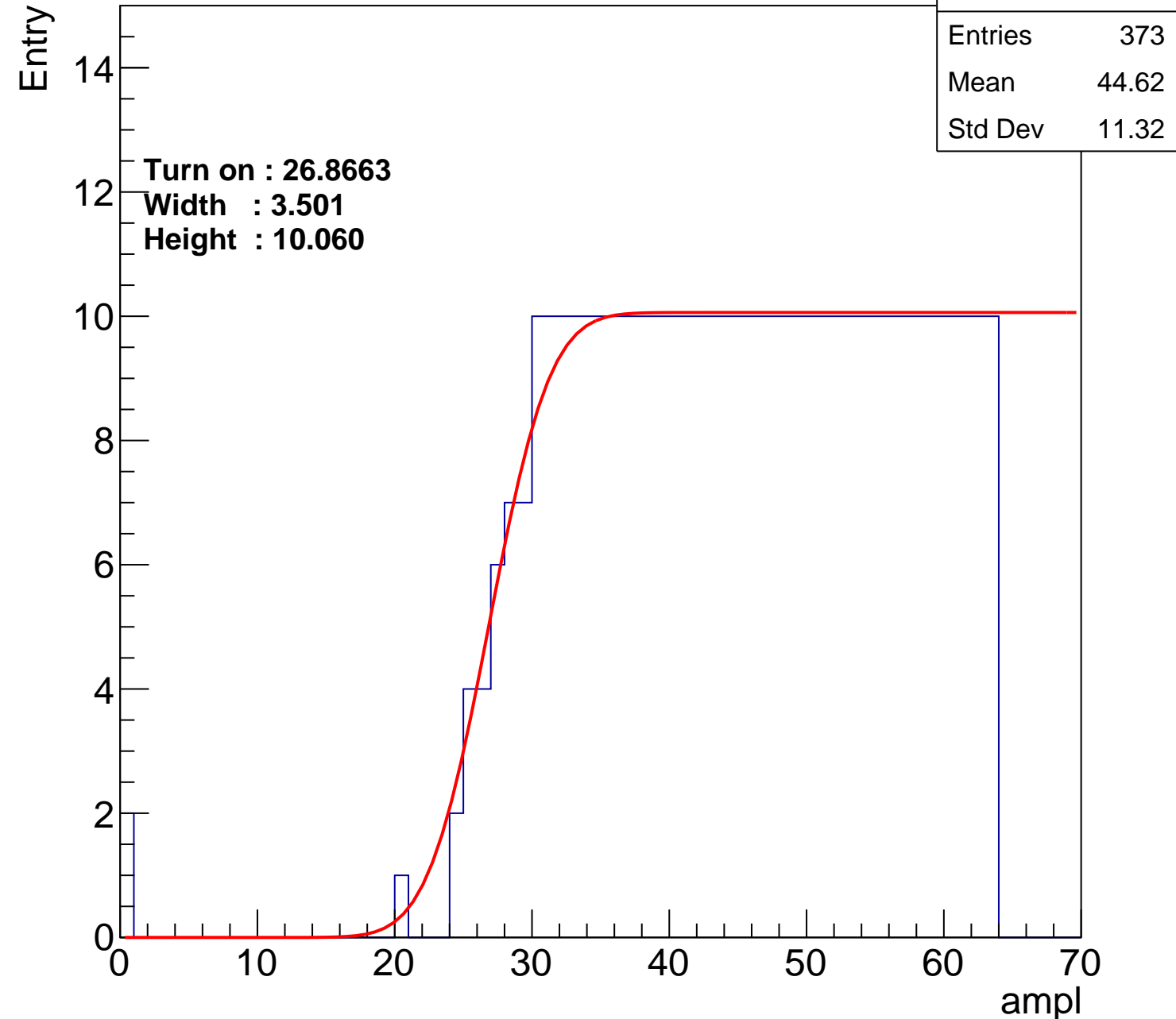
Width : 3.501

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch30

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 26.5339

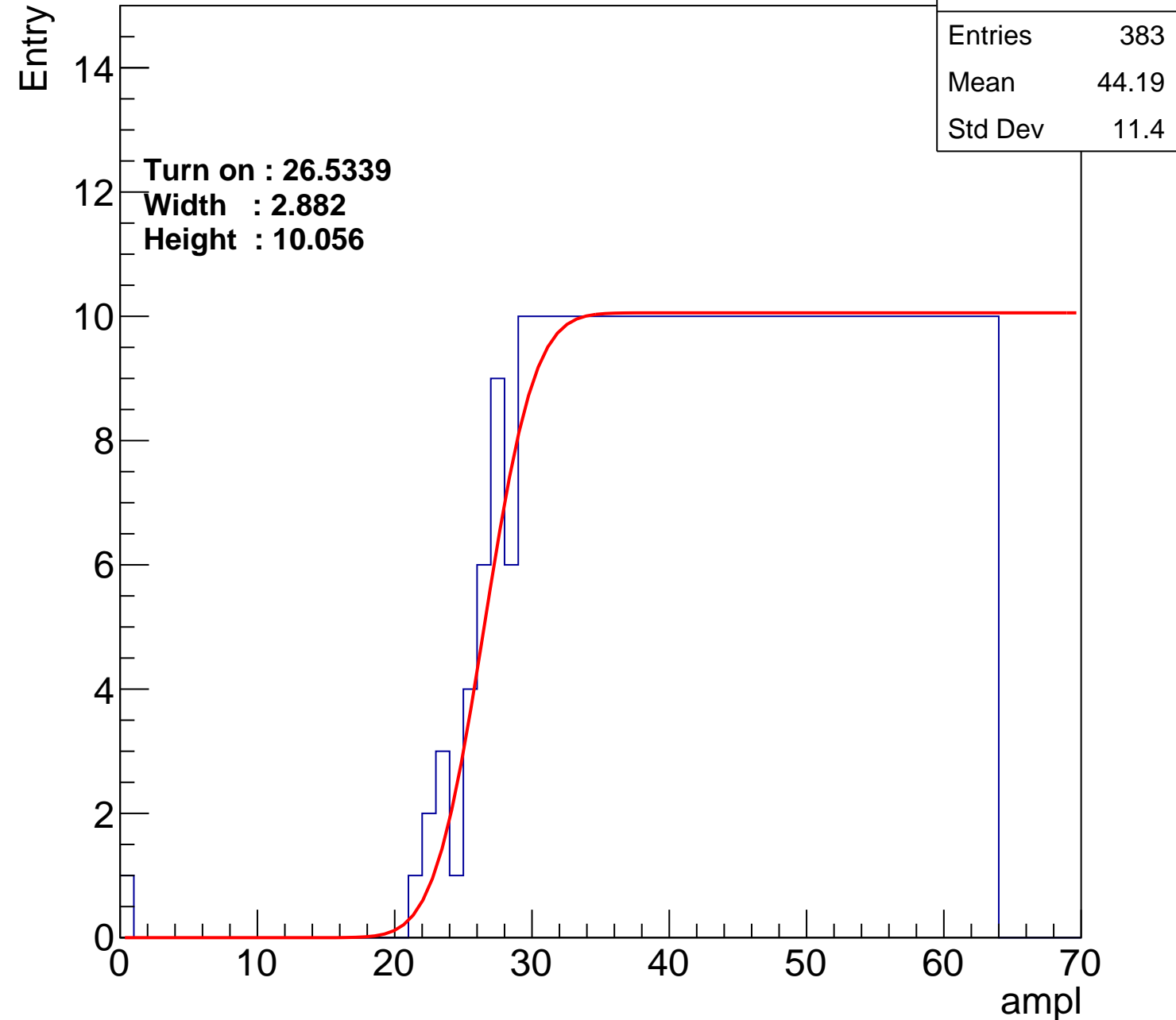
Width : 2.882

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.18
Std Dev	11.59

Turn on : 26.2740

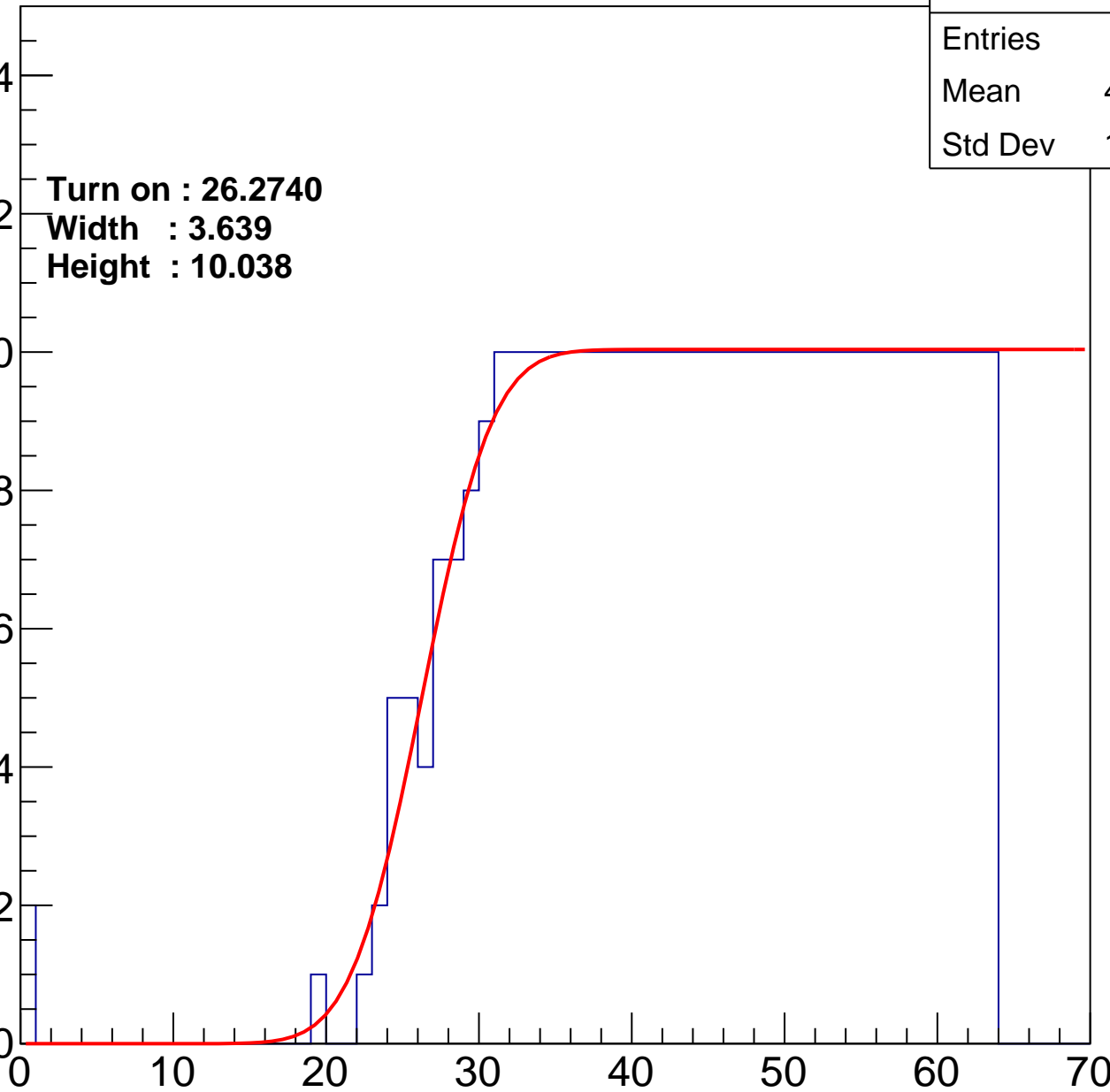
Width : 3.639

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch32

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.66
Std Dev	11.18

Turn on : 26.8597

Width : 3.382

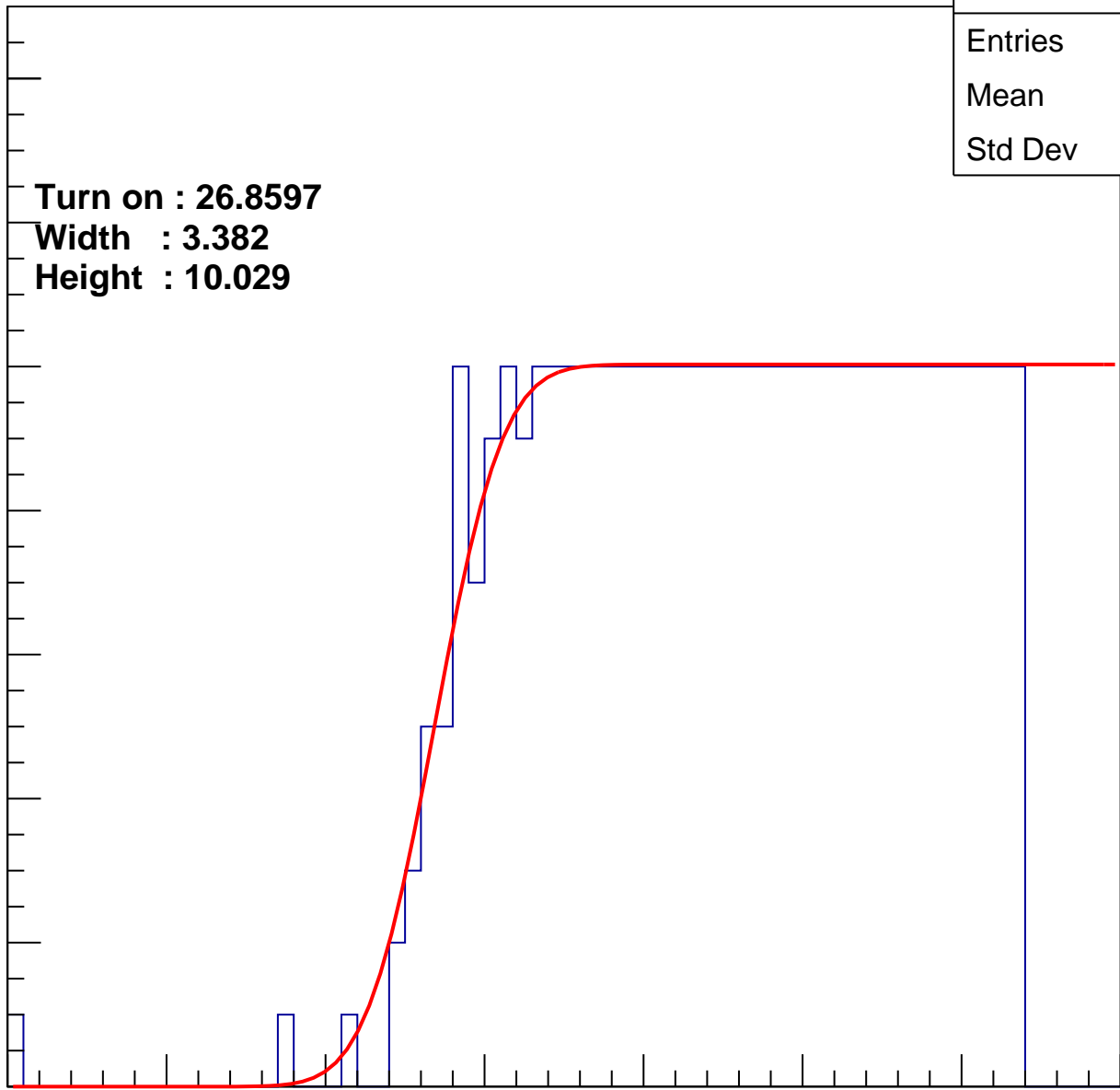
Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch33

calib_packv5_042523_0143.root, FC#13, port D2

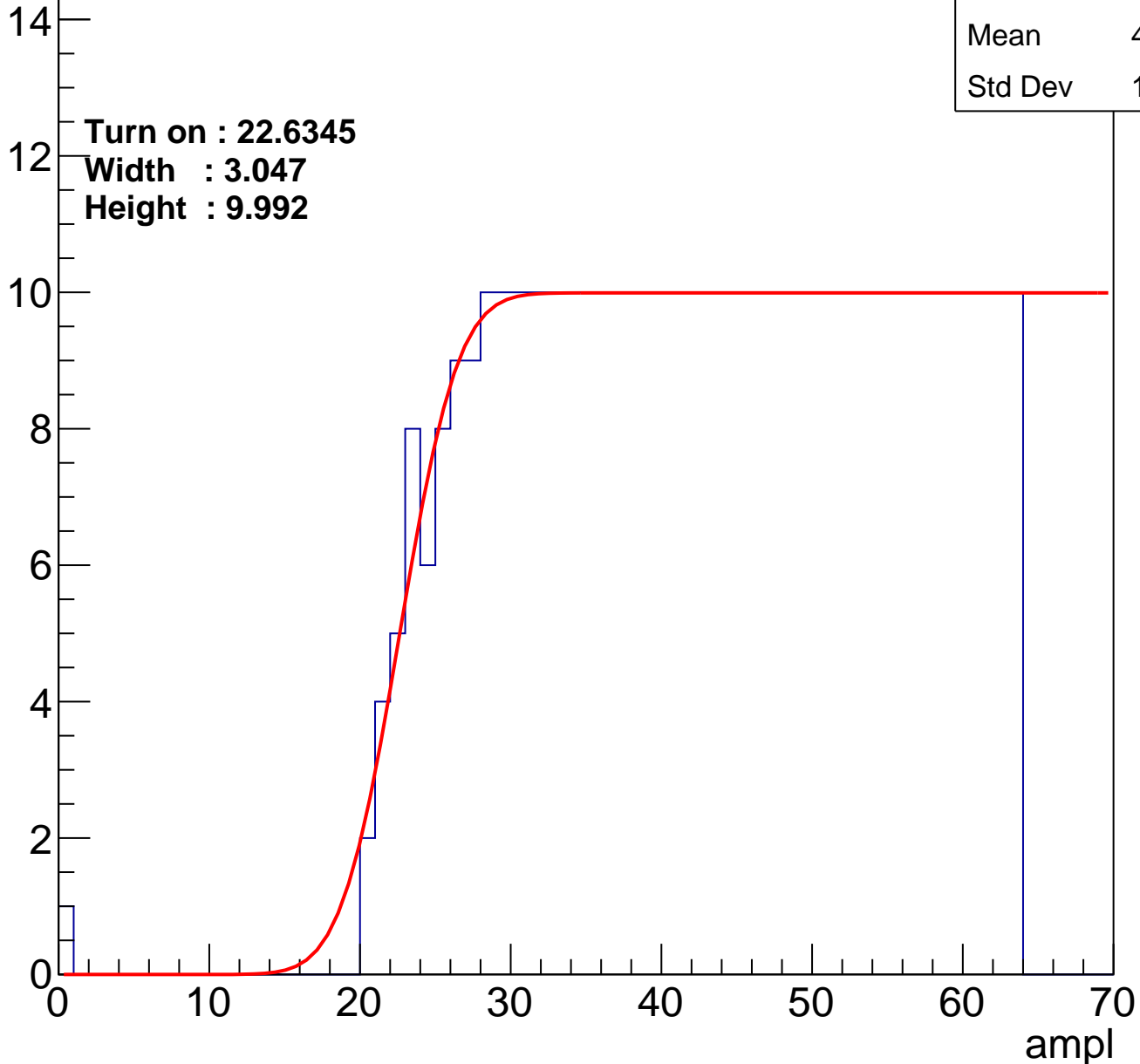
Entries	412
Mean	42.76
Std Dev	12.17

Turn on : 22.6345

Width : 3.047

Height : 9.992

Entry



B1L003S, U20-ch34

calib_packv5_042523_0143.root, FC#13, port D2

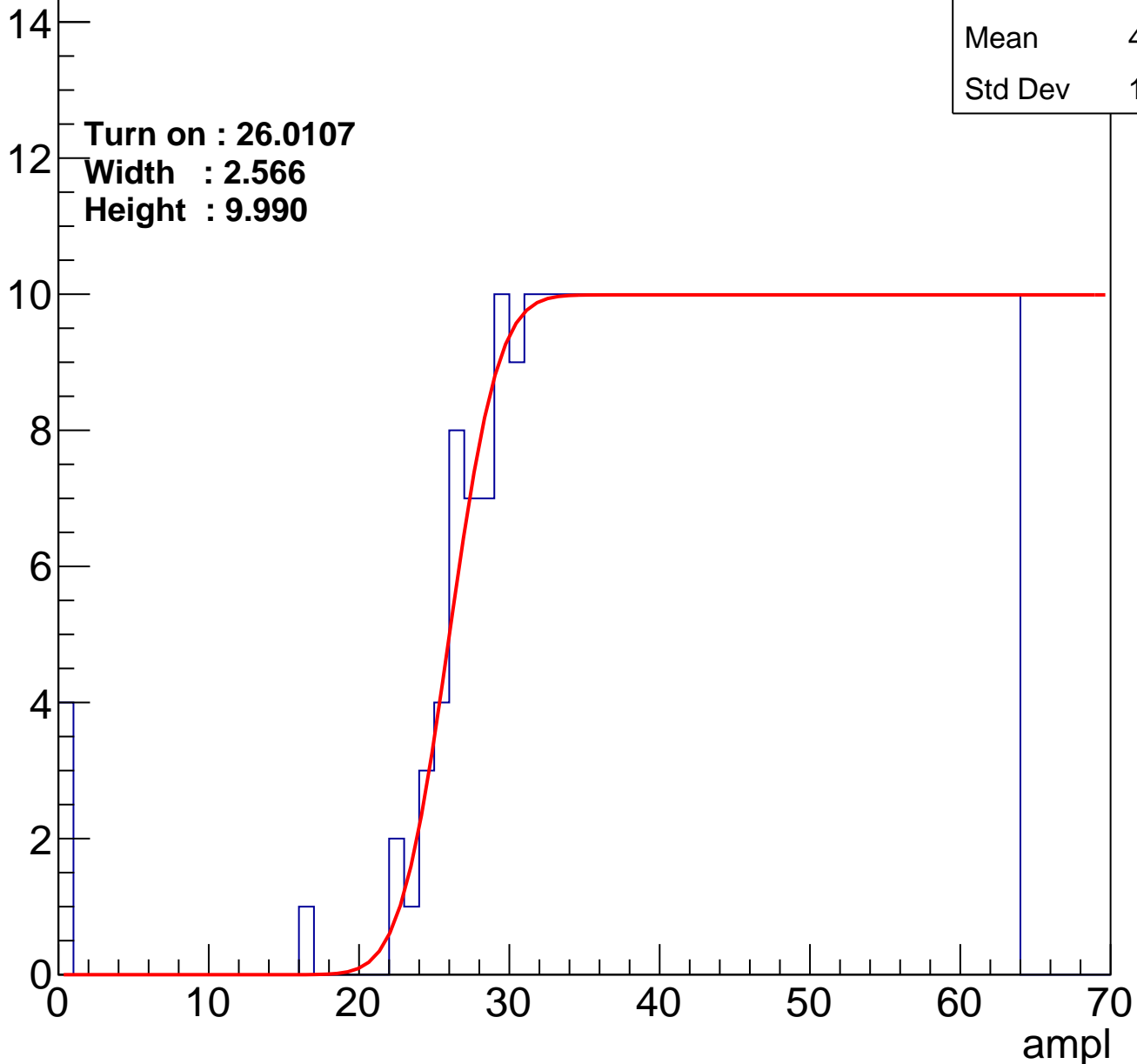
Entries	386
Mean	43.83
Std Dev	12.03

Turn on : 26.0107

Width : 2.566

Height : 9.990

Entry



B1L003S, U20-ch35

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.76
Std Dev	12.04

Turn on : 25.6624

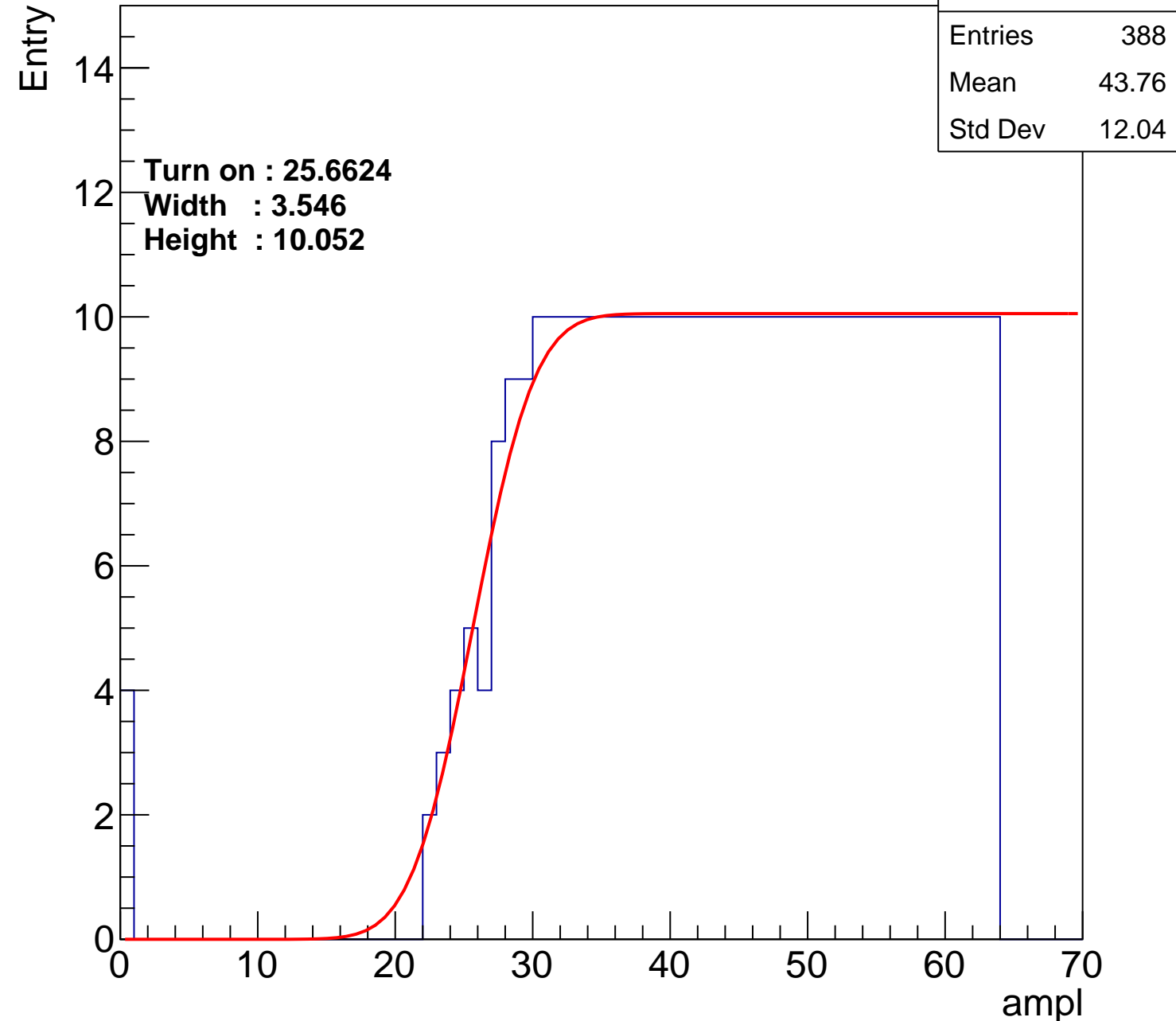
Width : 3.546

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch36

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.26
Std Dev	11.65

Turn on : 26.0895

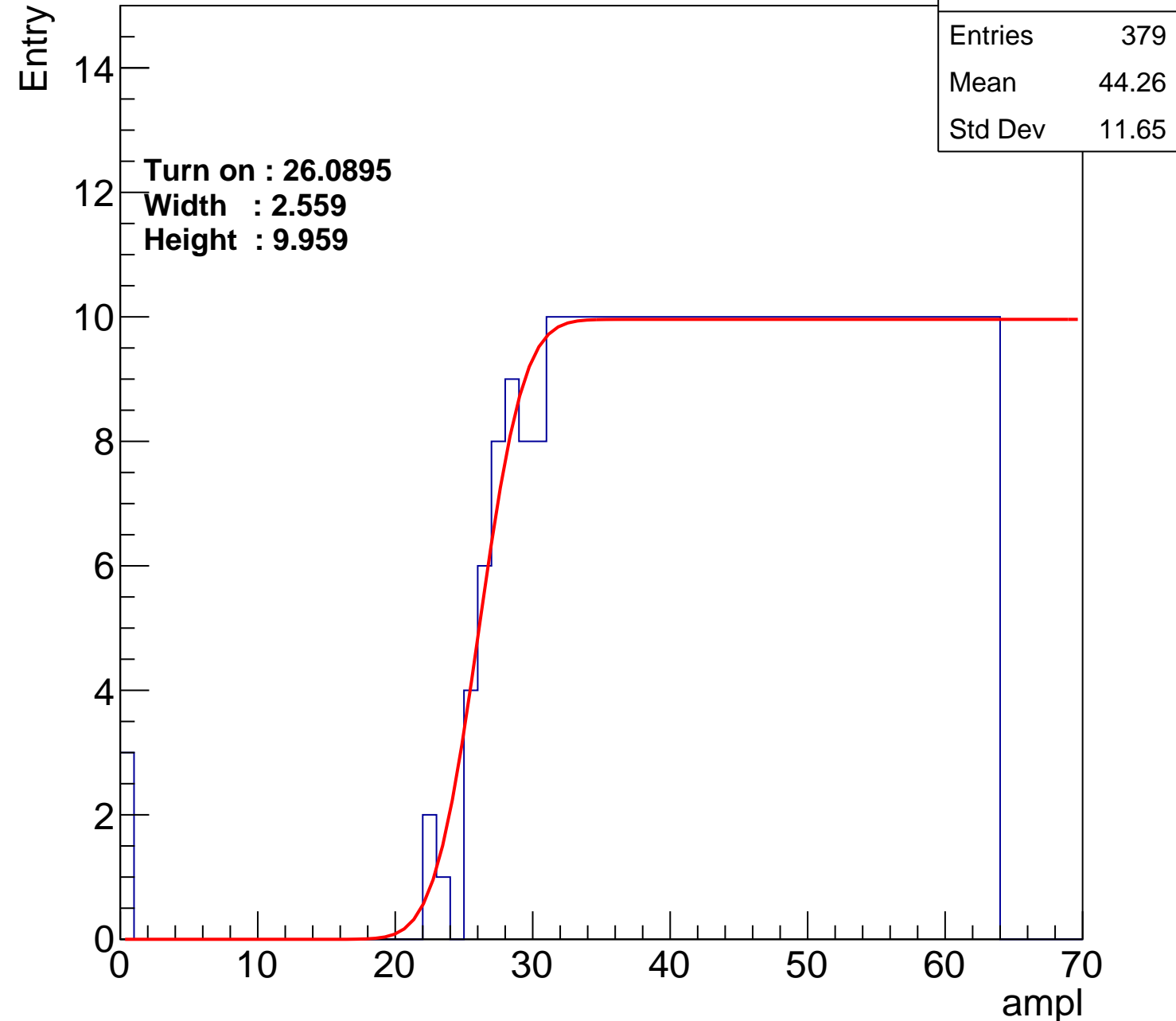
Width : 2.559

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.9
Std Dev	11.19

Turn on : 27.5938

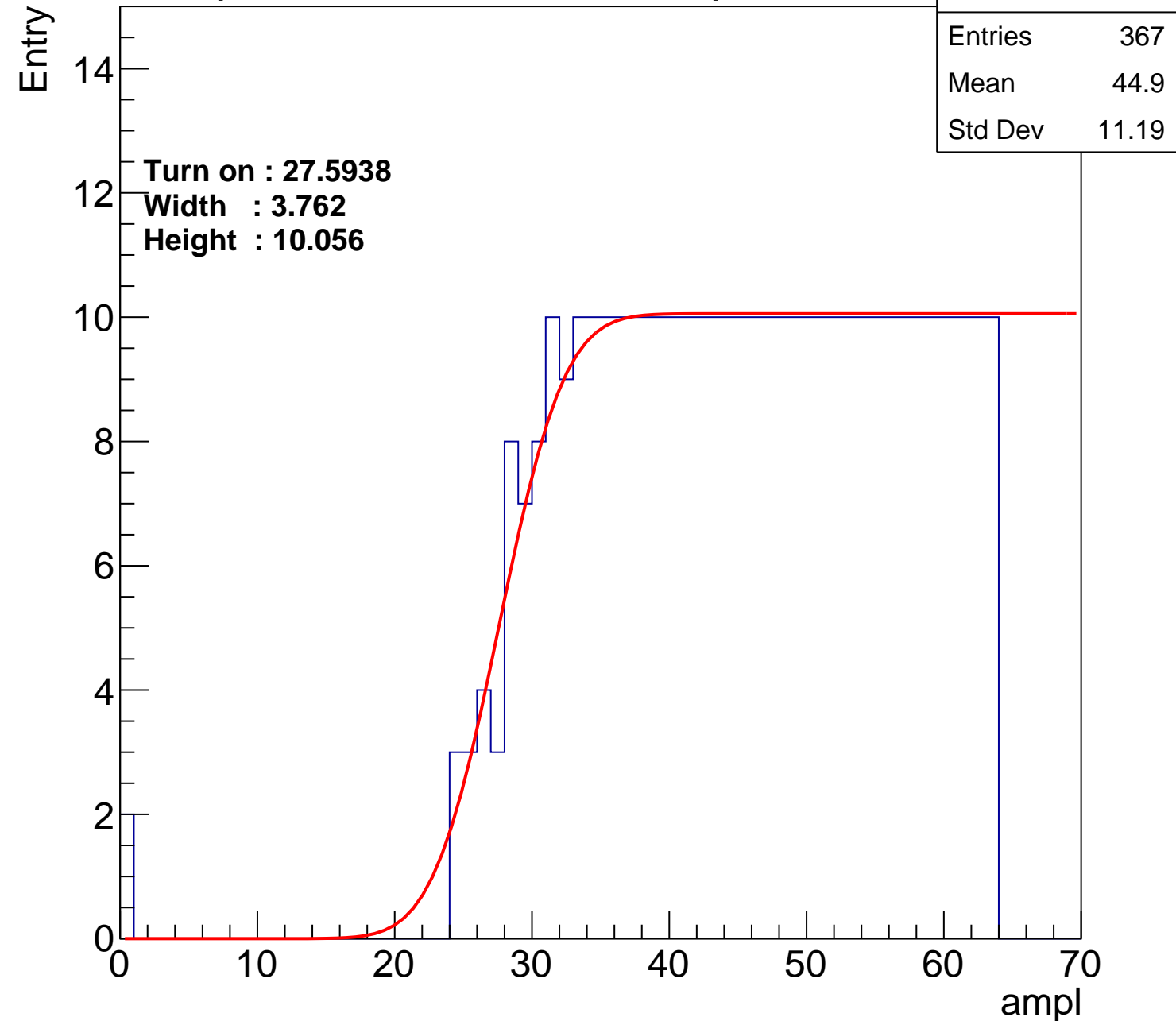
Width : 3.762

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch38

calib_packv5_042523_0143.root, FC#13, port D2

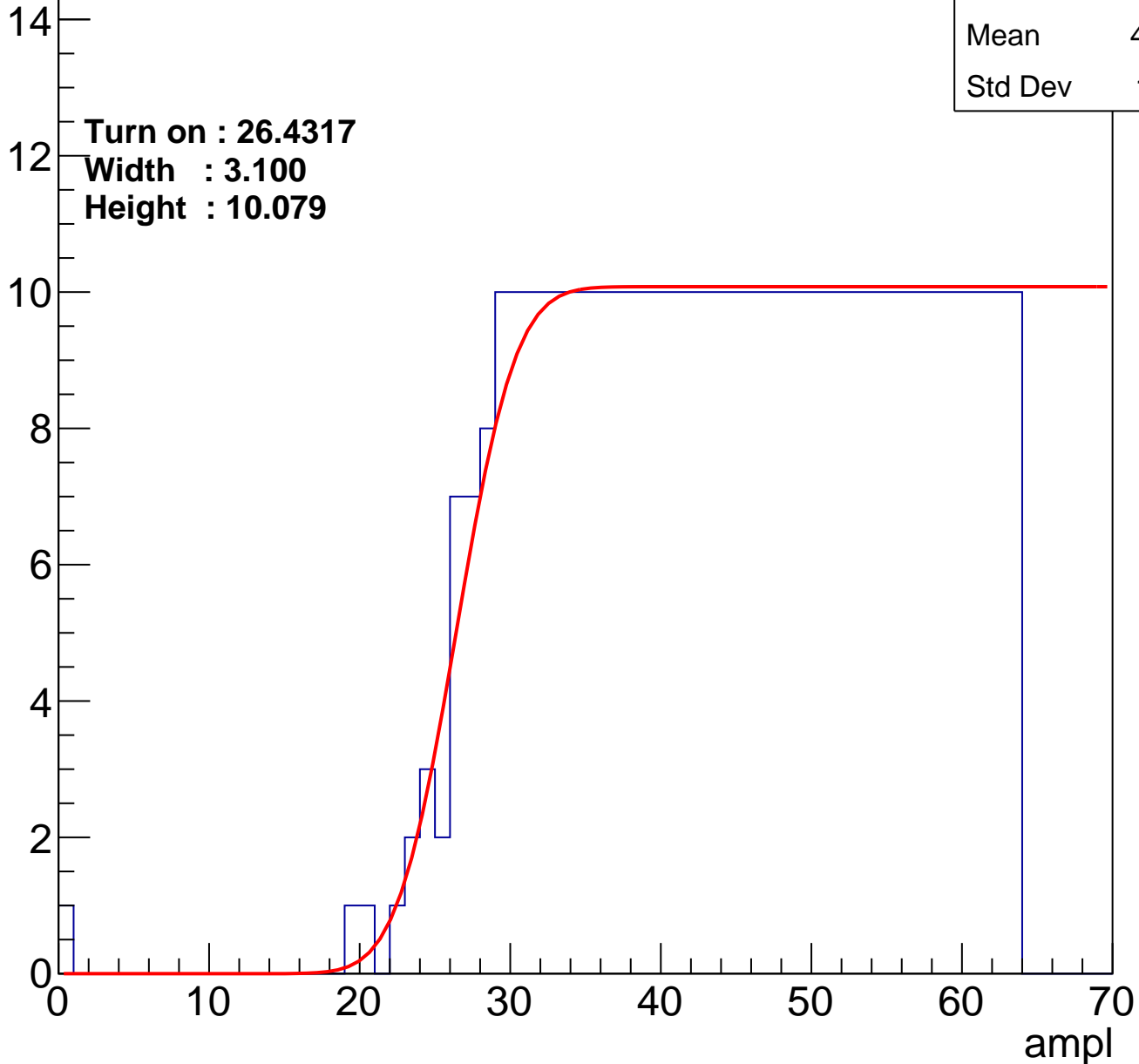
Entries	383
Mean	44.19
Std Dev	11.41

Turn on : 26.4317

Width : 3.100

Height : 10.079

Entry



B1L003S, U20-ch39

calib_packv5_042523_0143.root, FC#13, port D2

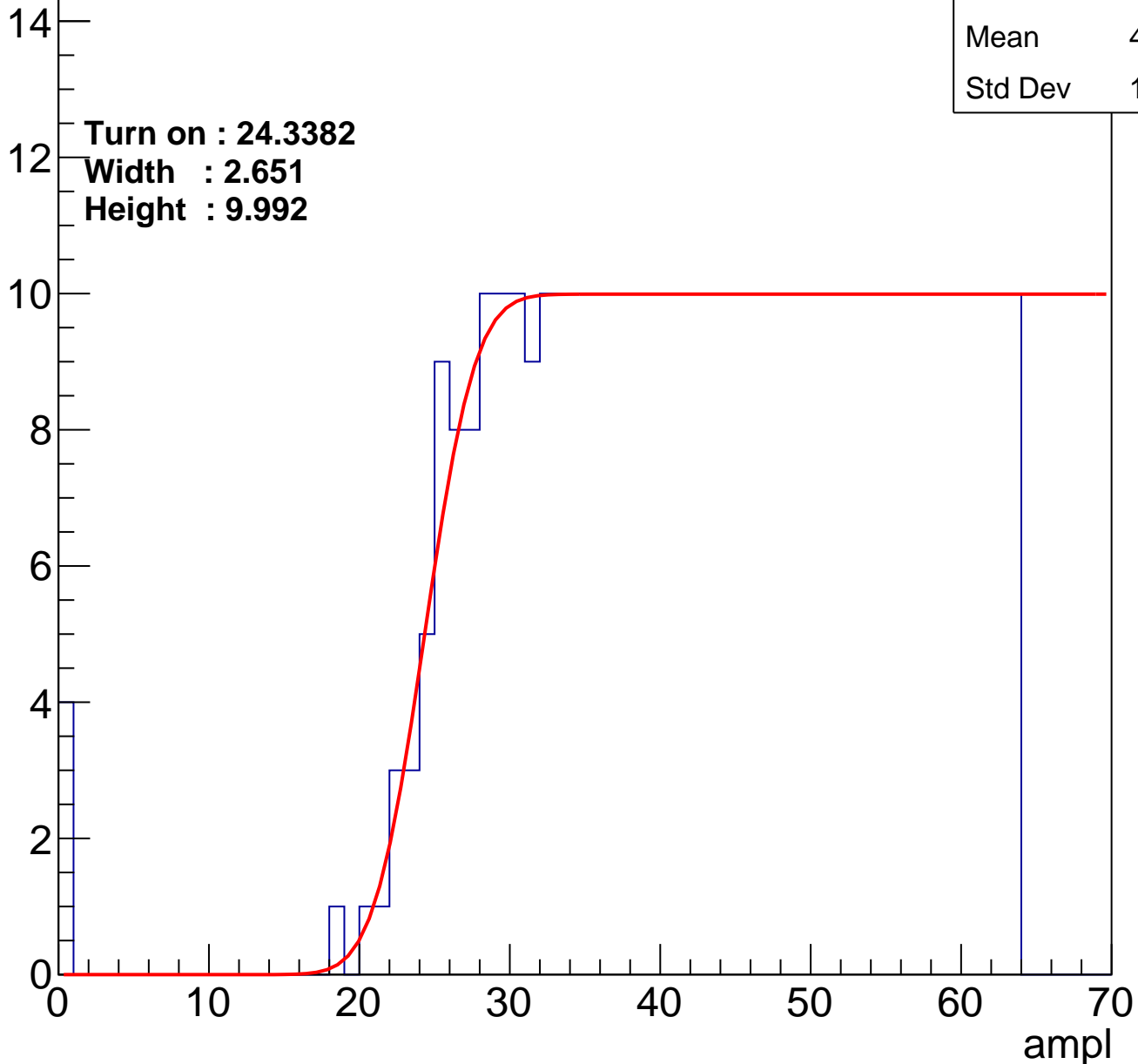
Entries	402
Mean	43.06
Std Dev	12.38

Turn on : 24.3382

Width : 2.651

Height : 9.992

Entry



B1L003S, U20-ch40

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.31
Std Dev	11.32

Turn on : 28.9632

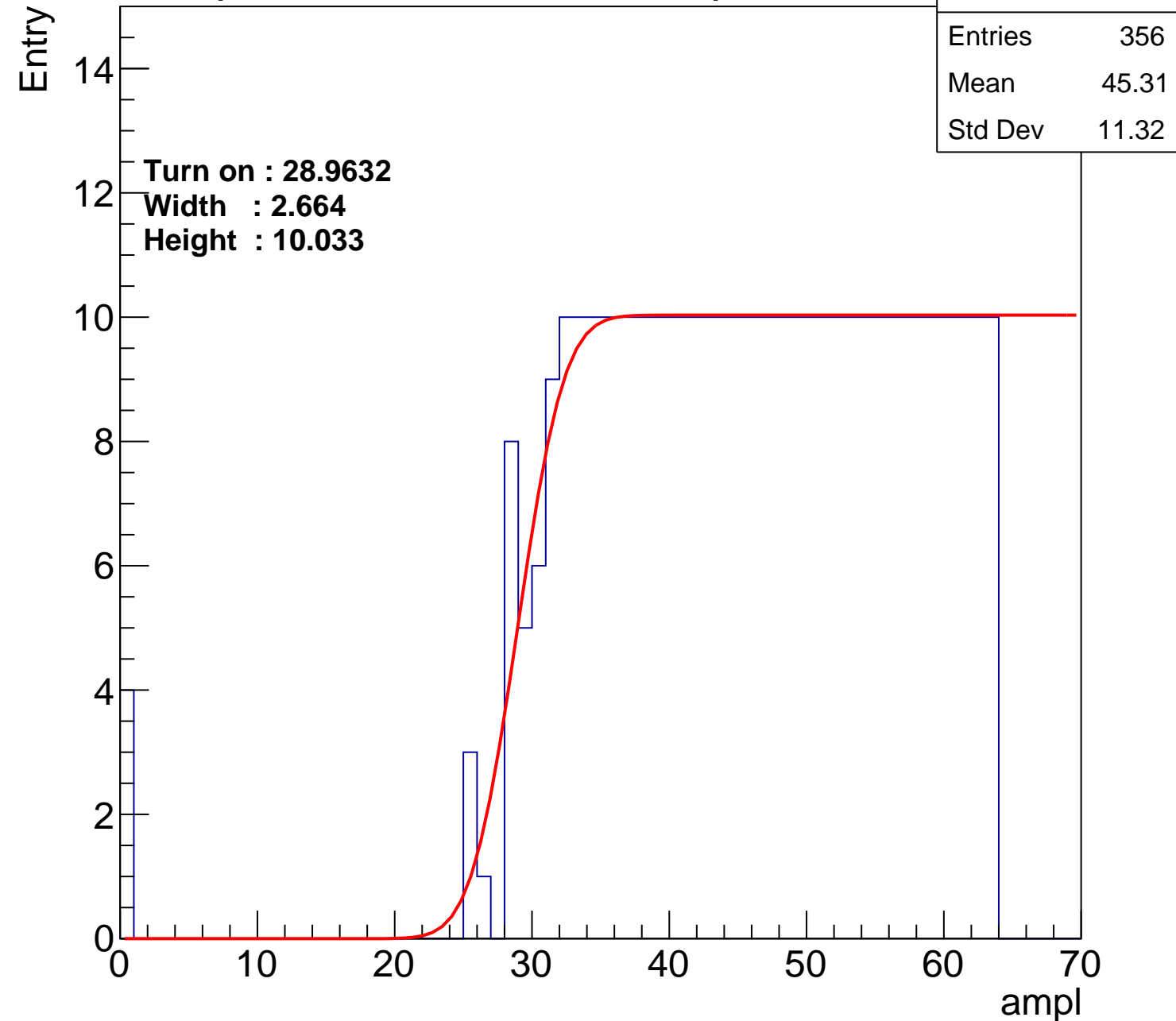
Width : 2.664

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch41

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
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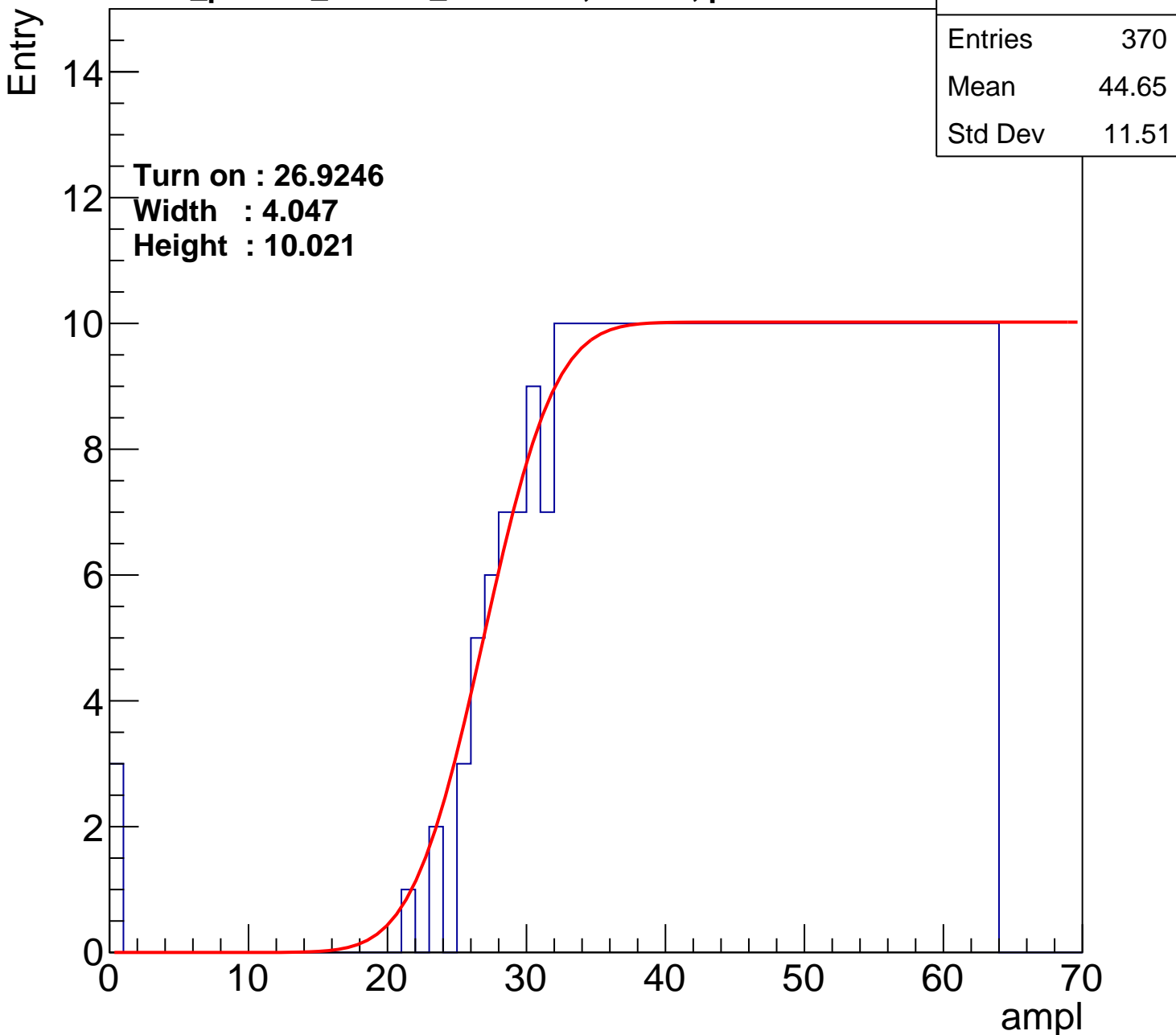
Mean	44.65
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Std Dev	11.51
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Turn on : 26.9246

Width : 4.047

Height : 10.021



B1L003S, U20-ch42

calib_packv5_042523_0143.root, FC#13, port D2

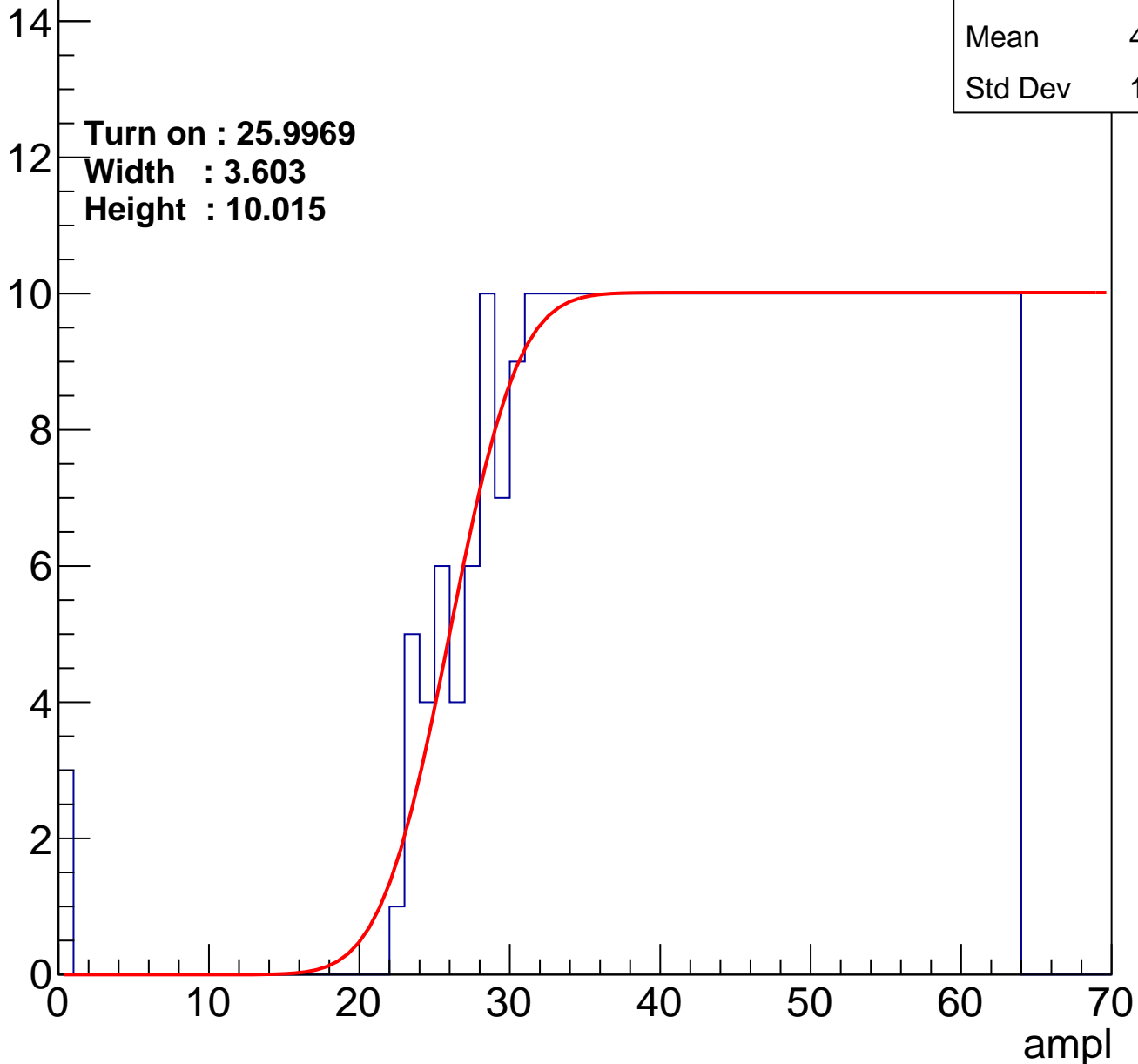
Entries	385
Mean	43.93
Std Dev	11.85

Turn on : 25.9969

Width : 3.603

Height : 10.015

Entry



B1L003S, U20-ch43

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.85
Std Dev	11.25

Turn on : 27.8987

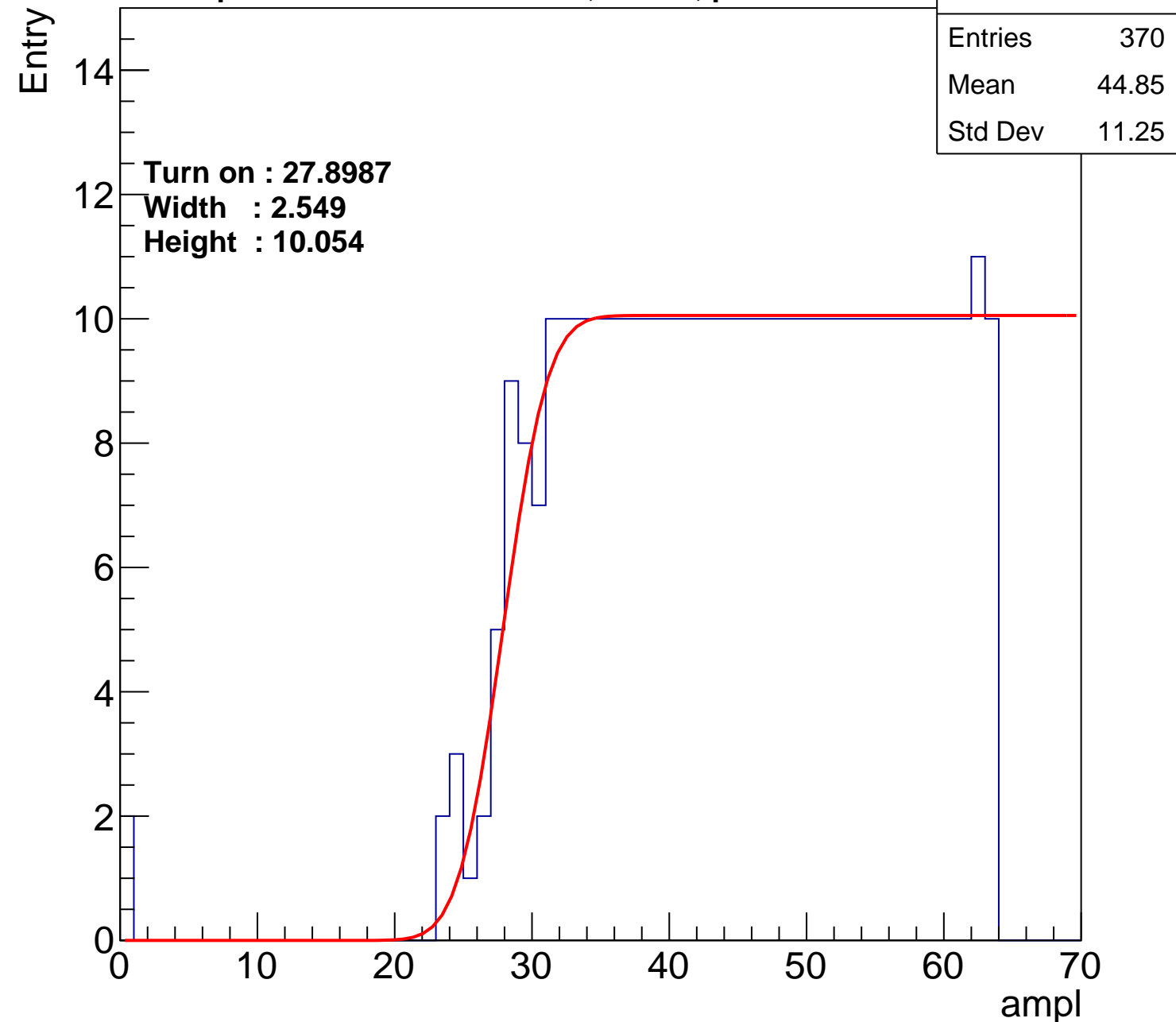
Width : 2.549

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch44

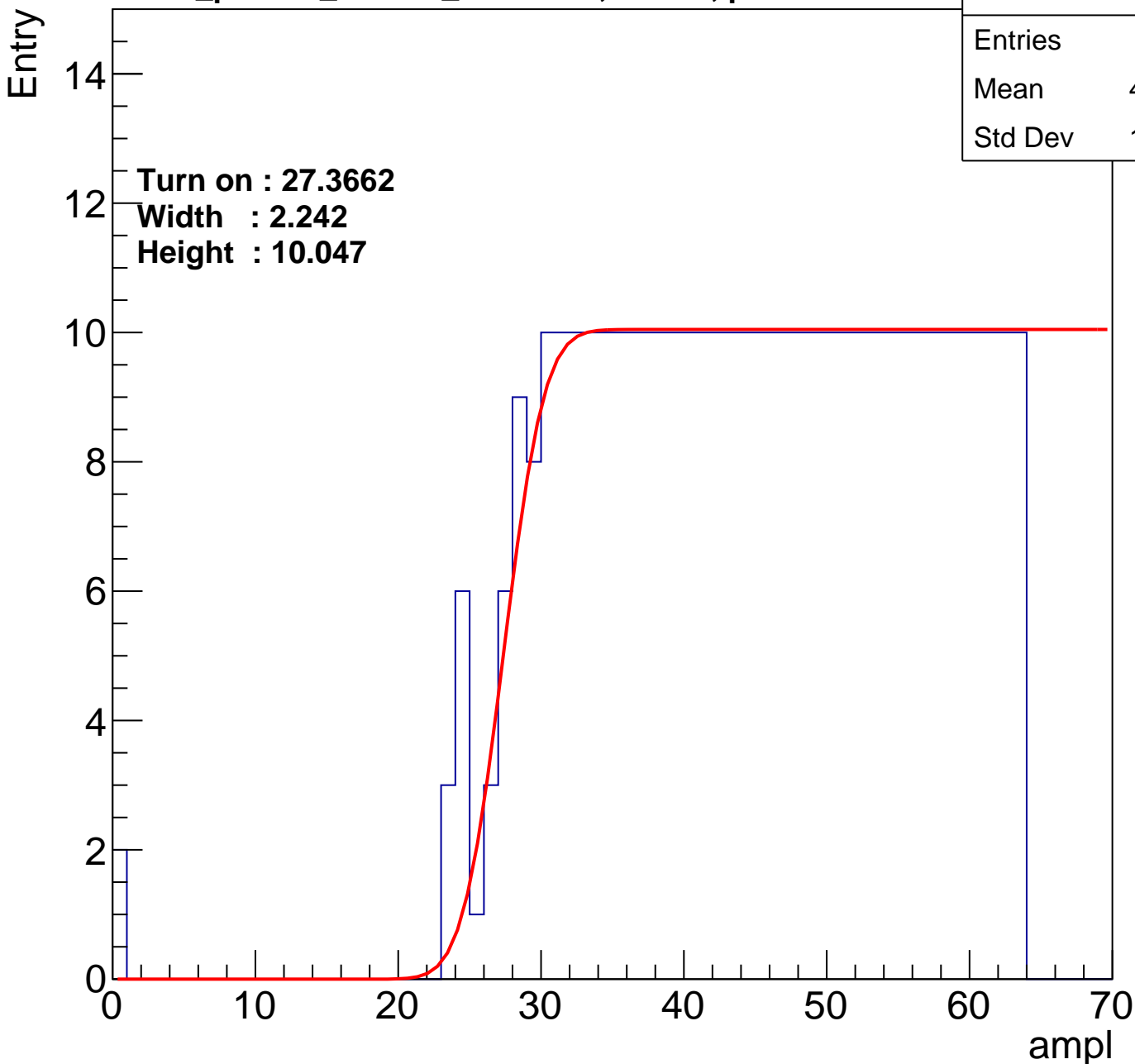
calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 27.3662

Width : 2.242

Height : 10.047



B1L003S, U20-ch45

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.56
Std Dev	11.81

Turn on : 28.0959

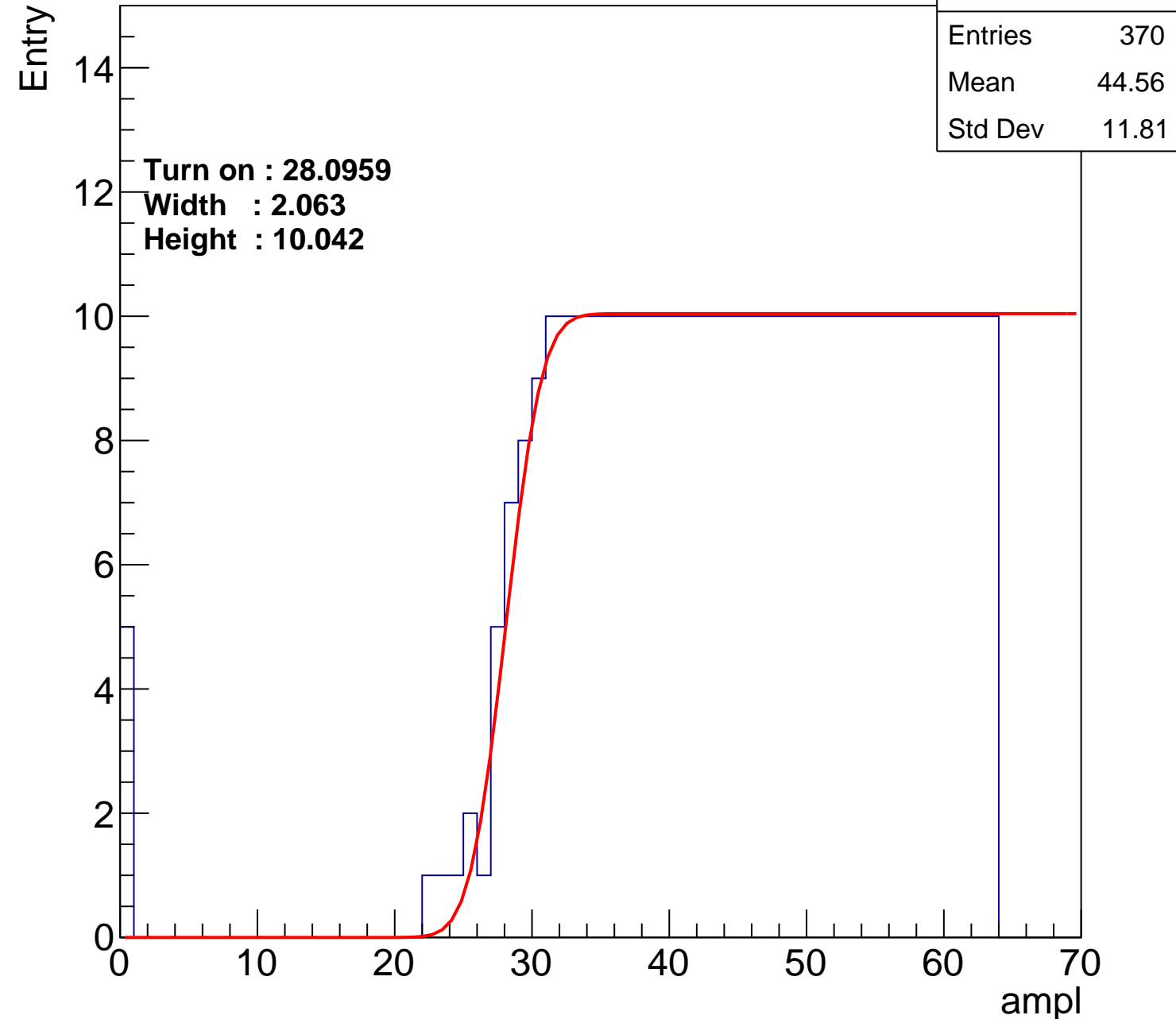
Width : 2.063

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch46

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.43
Std Dev	11.08

Turn on : 28.7871

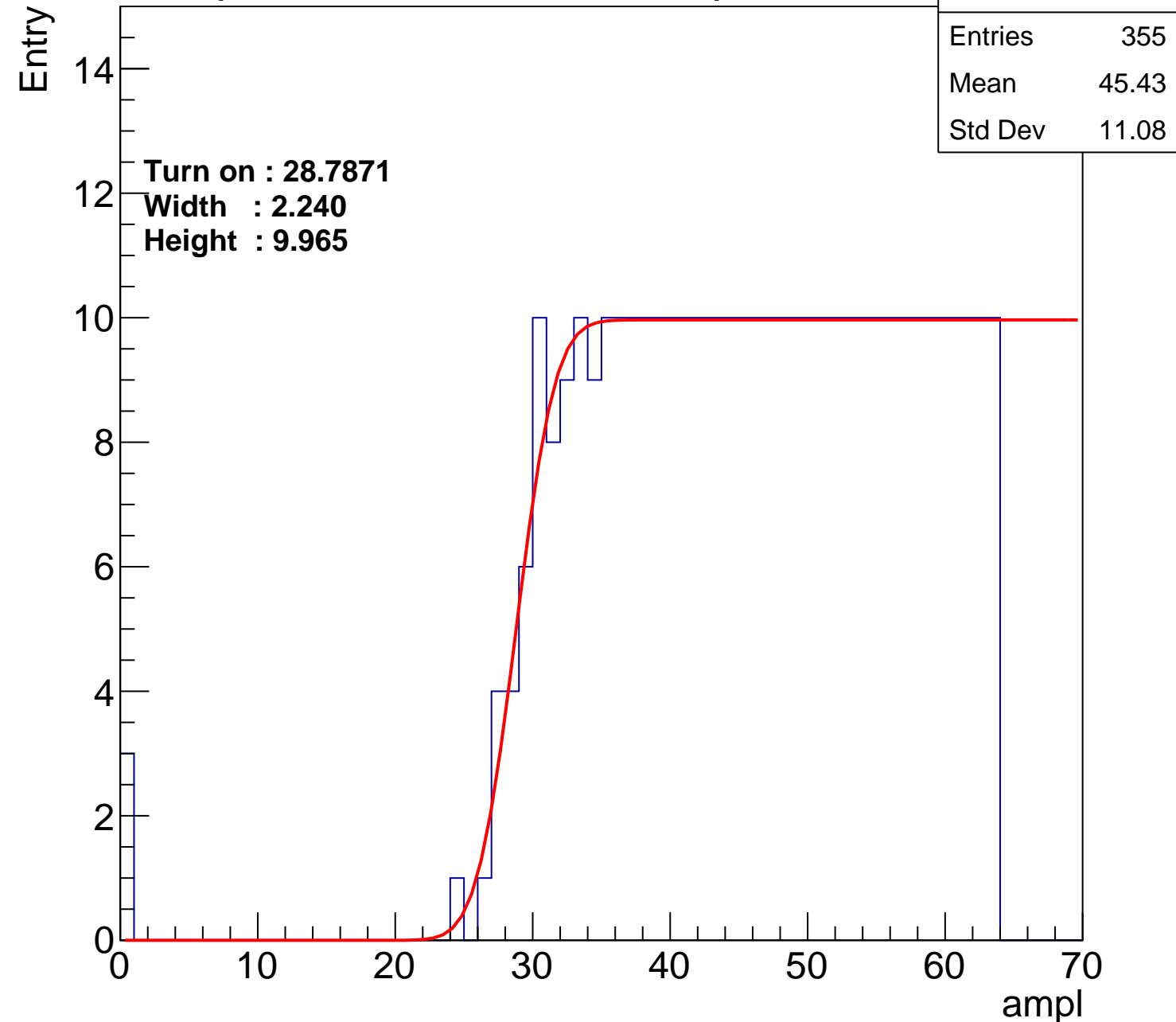
Width : 2.240

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch47

calib_packv5_042523_0143.root, FC#13, port D2

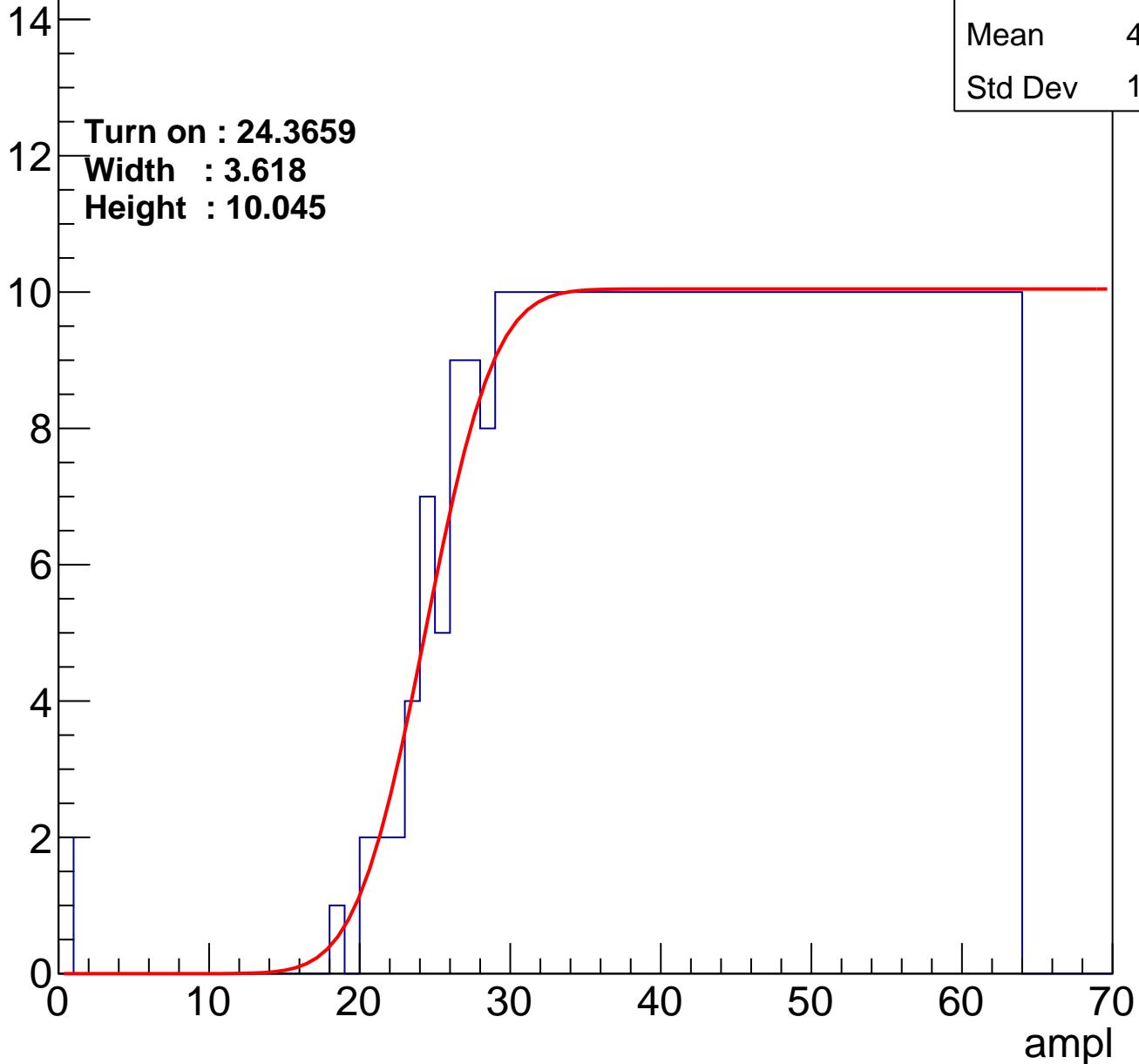
Entries	401
Mean	43.22
Std Dev	12.08

Turn on : 24.3659

Width : 3.618

Height : 10.045

Entry

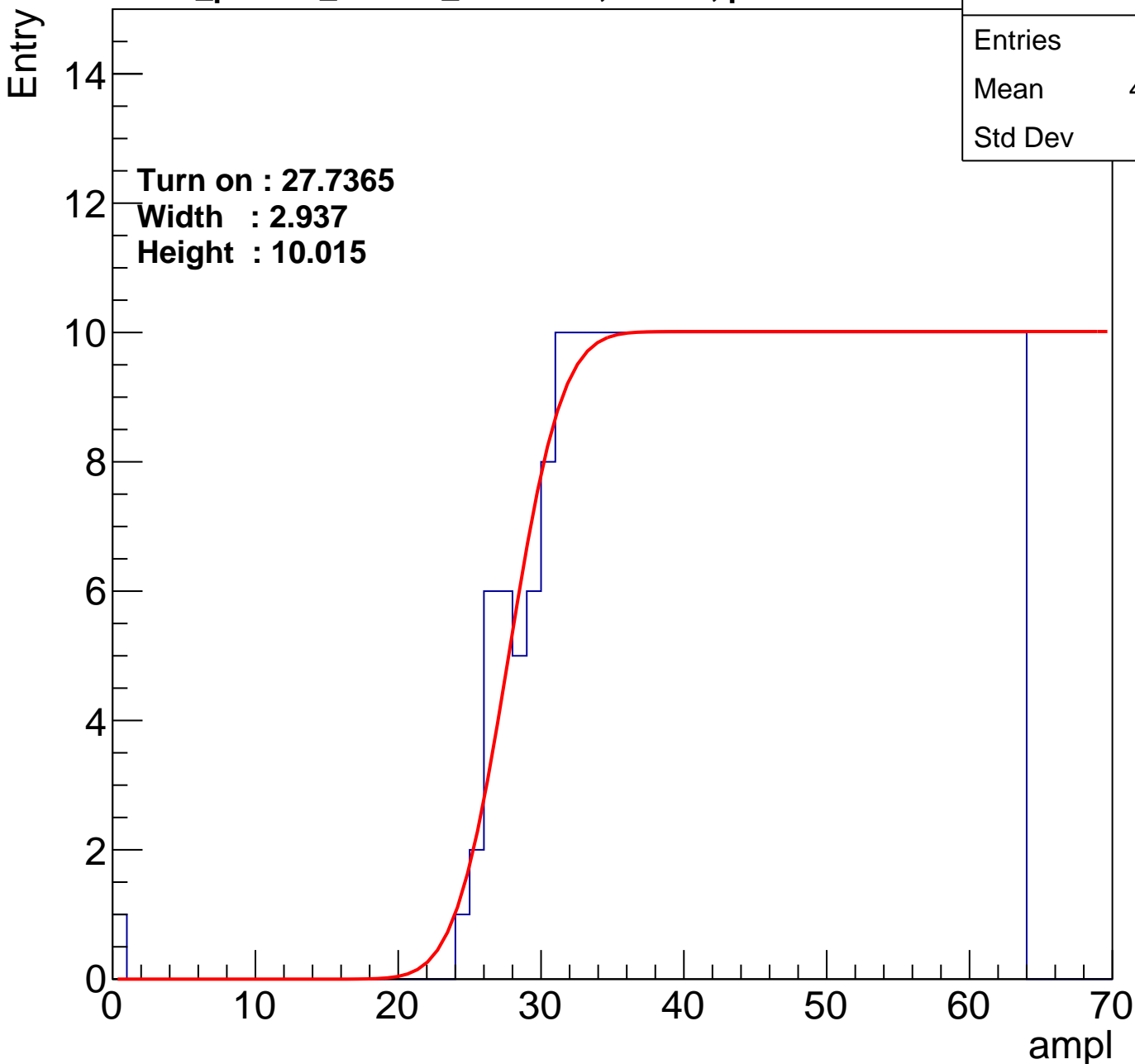


calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.08
Std Dev	10.9

Height : 10.015



B1L003S, U20-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.85
Std Dev	11.55

Turn on : 27.9662

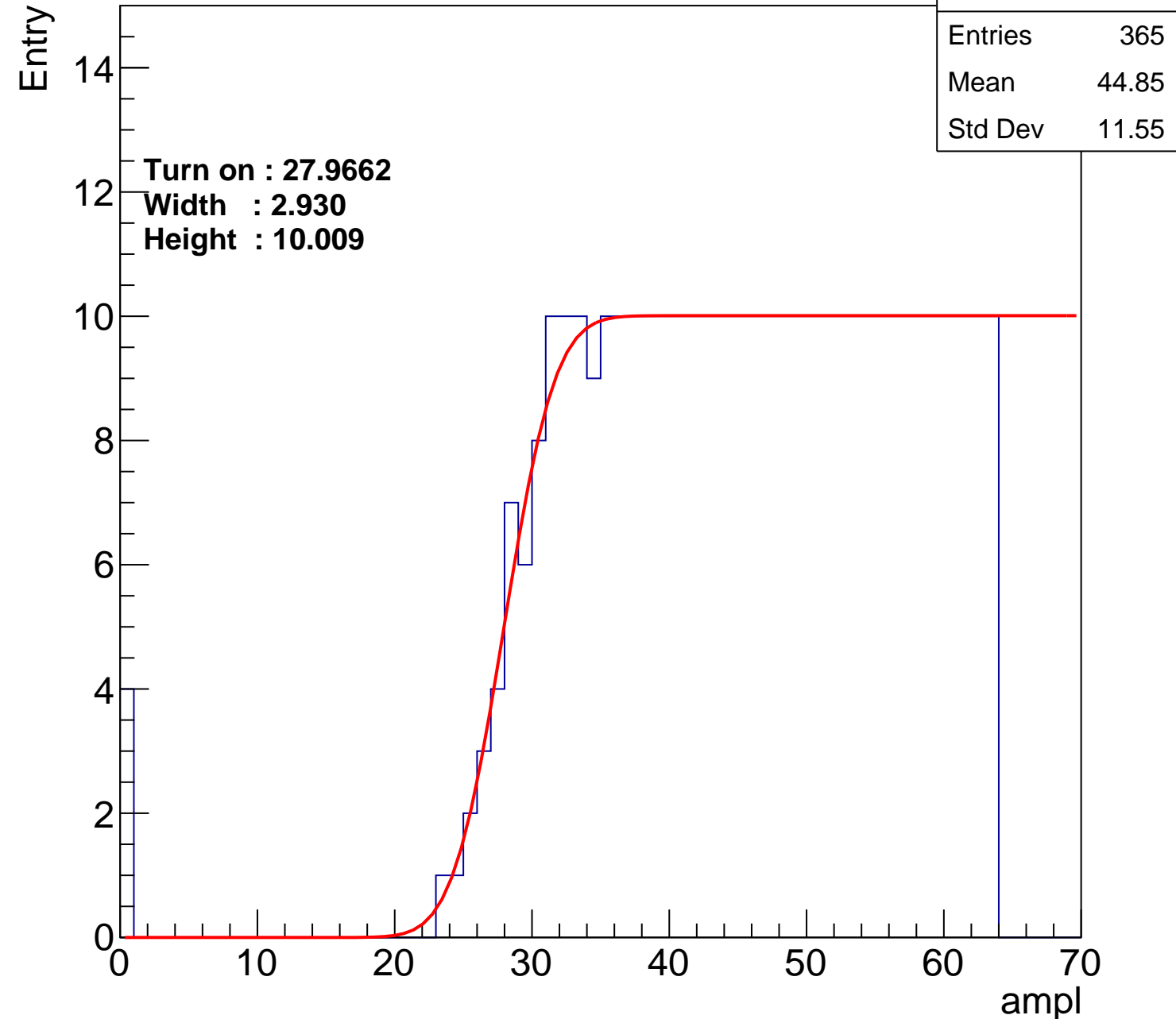
Width : 2.930

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch50

calib_packv5_042523_0143.root, FC#13, port D2

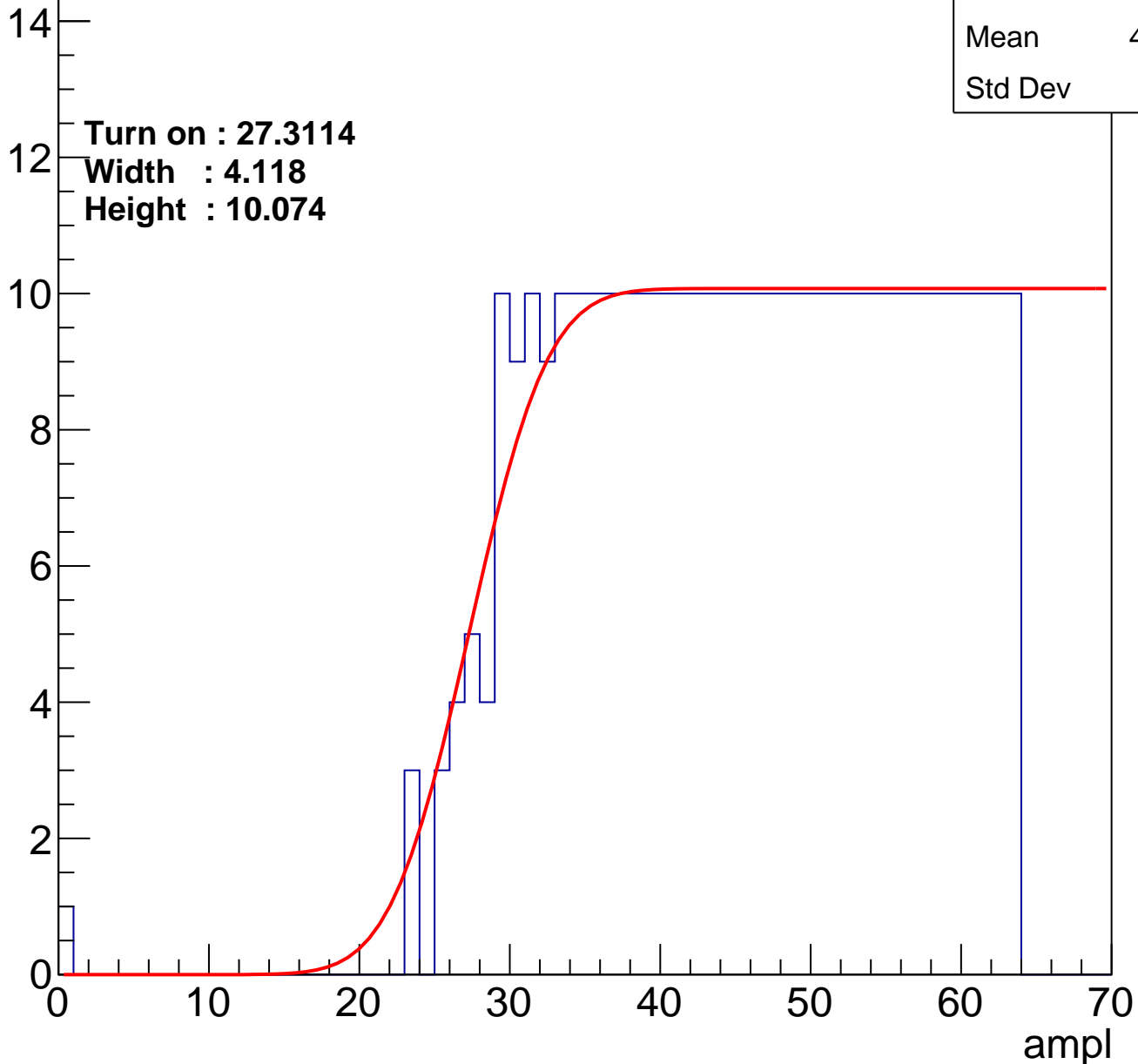
Entries	368
Mean	44.93
Std Dev	11

Turn on : 27.3114

Width : 4.118

Height : 10.074

Entry



B1L003S, U20-ch51

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.07
Std Dev	11.99

Turn on : 26.4938

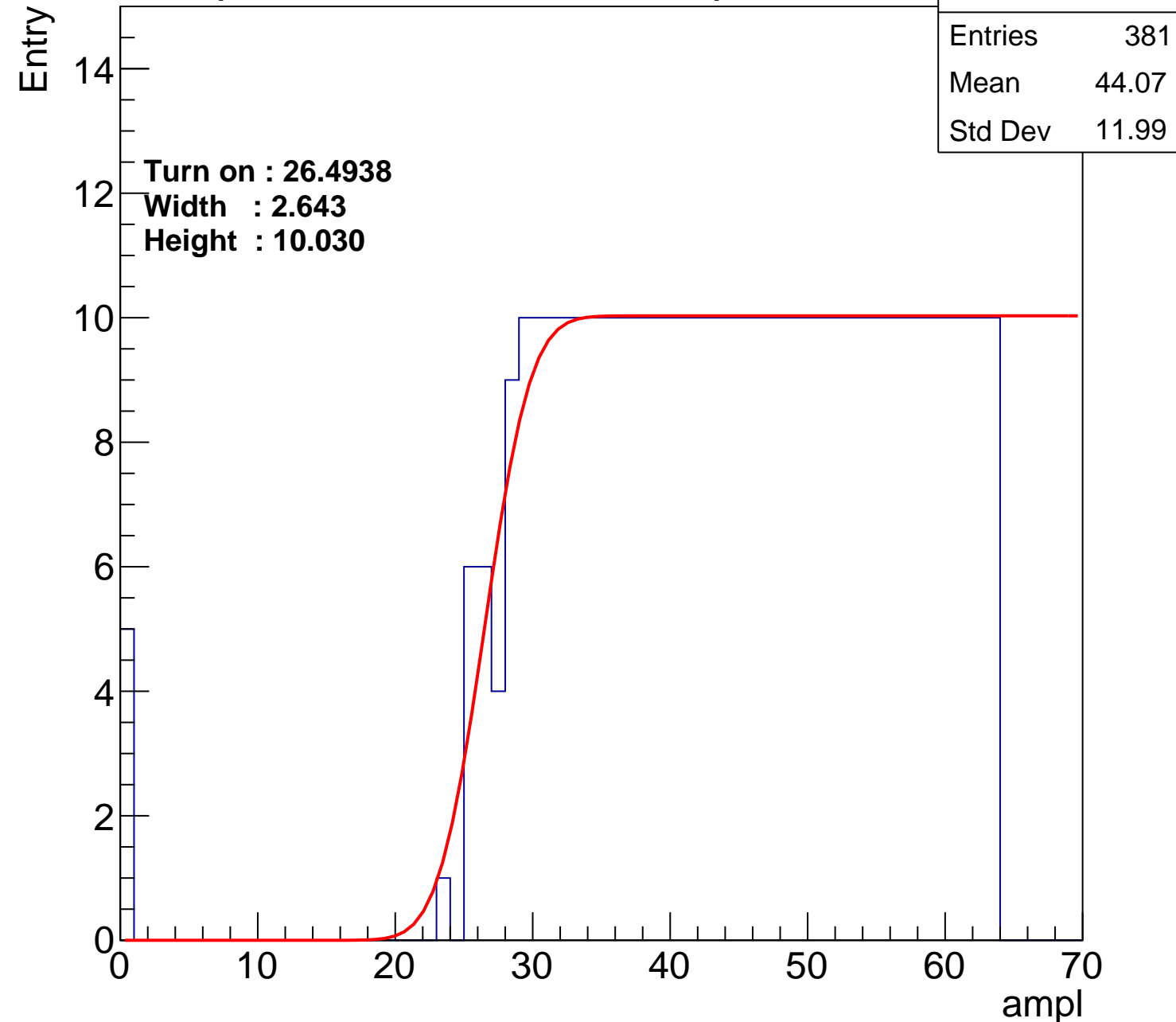
Width : 2.643

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch52

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.98
Std Dev	11.12

Turn on : 27.9405

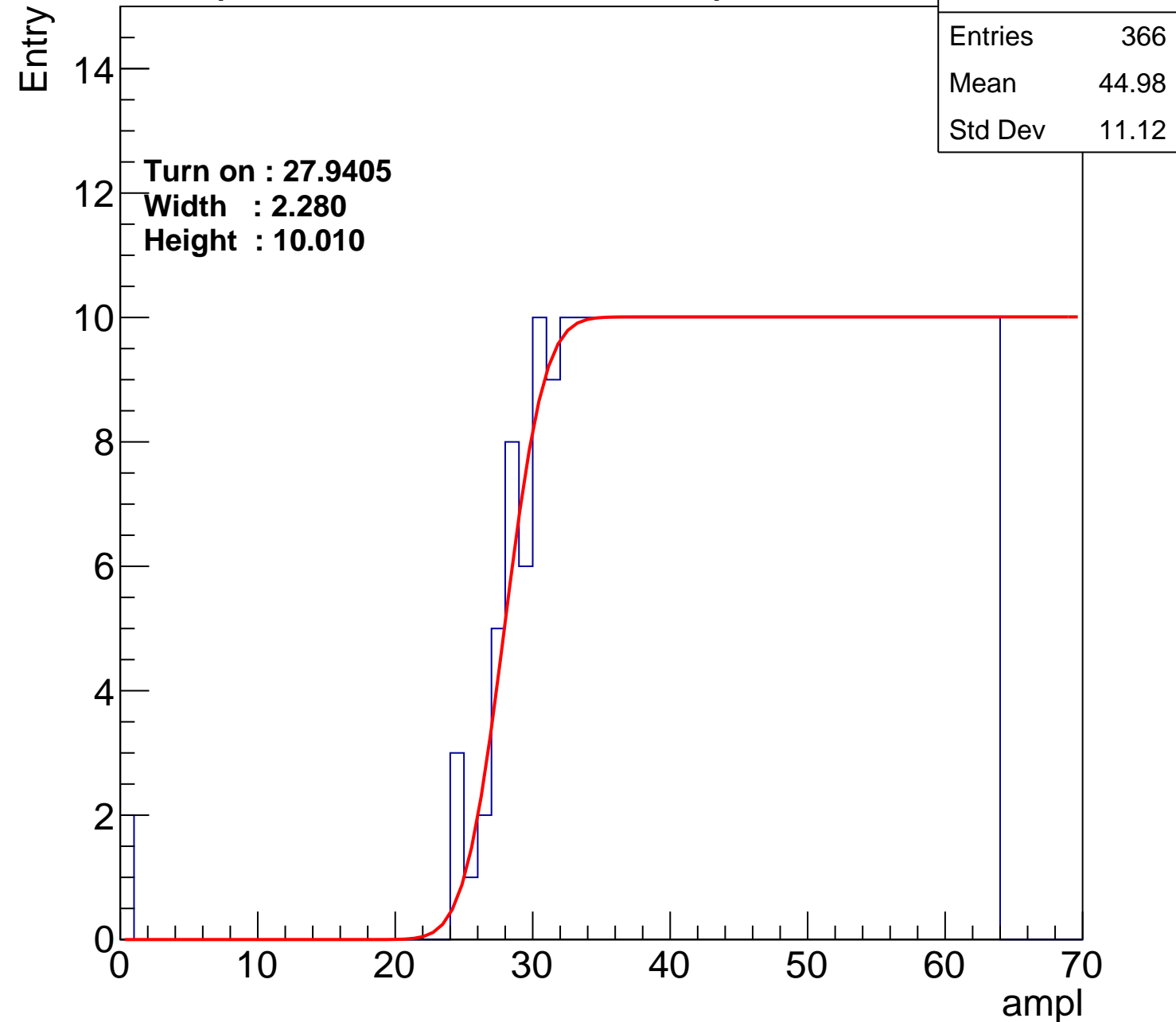
Width : 2.280

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	401
Mean	42.93
Std Dev	12.81

Turn on : 25.0042

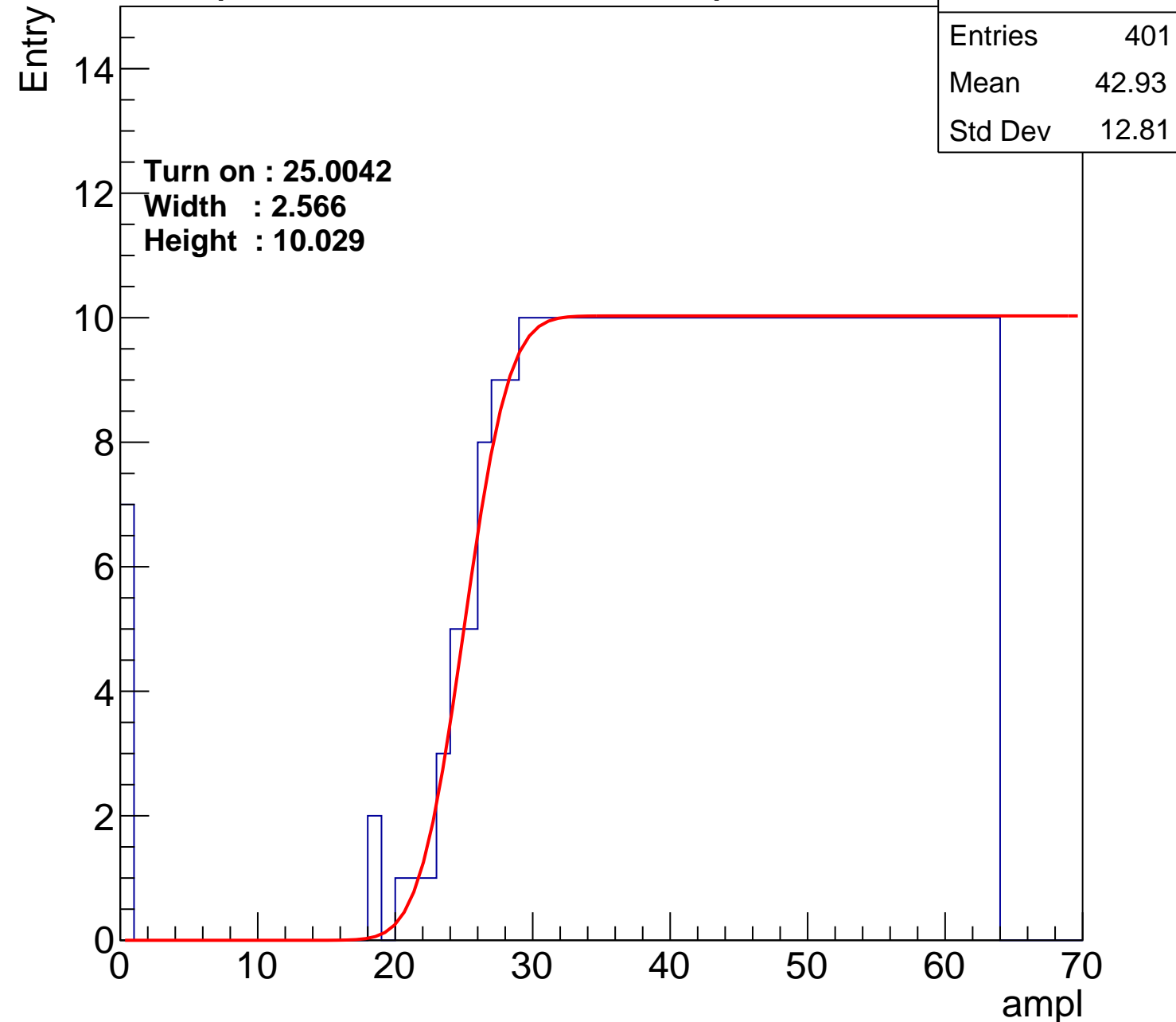
Width : 2.566

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 26.3923

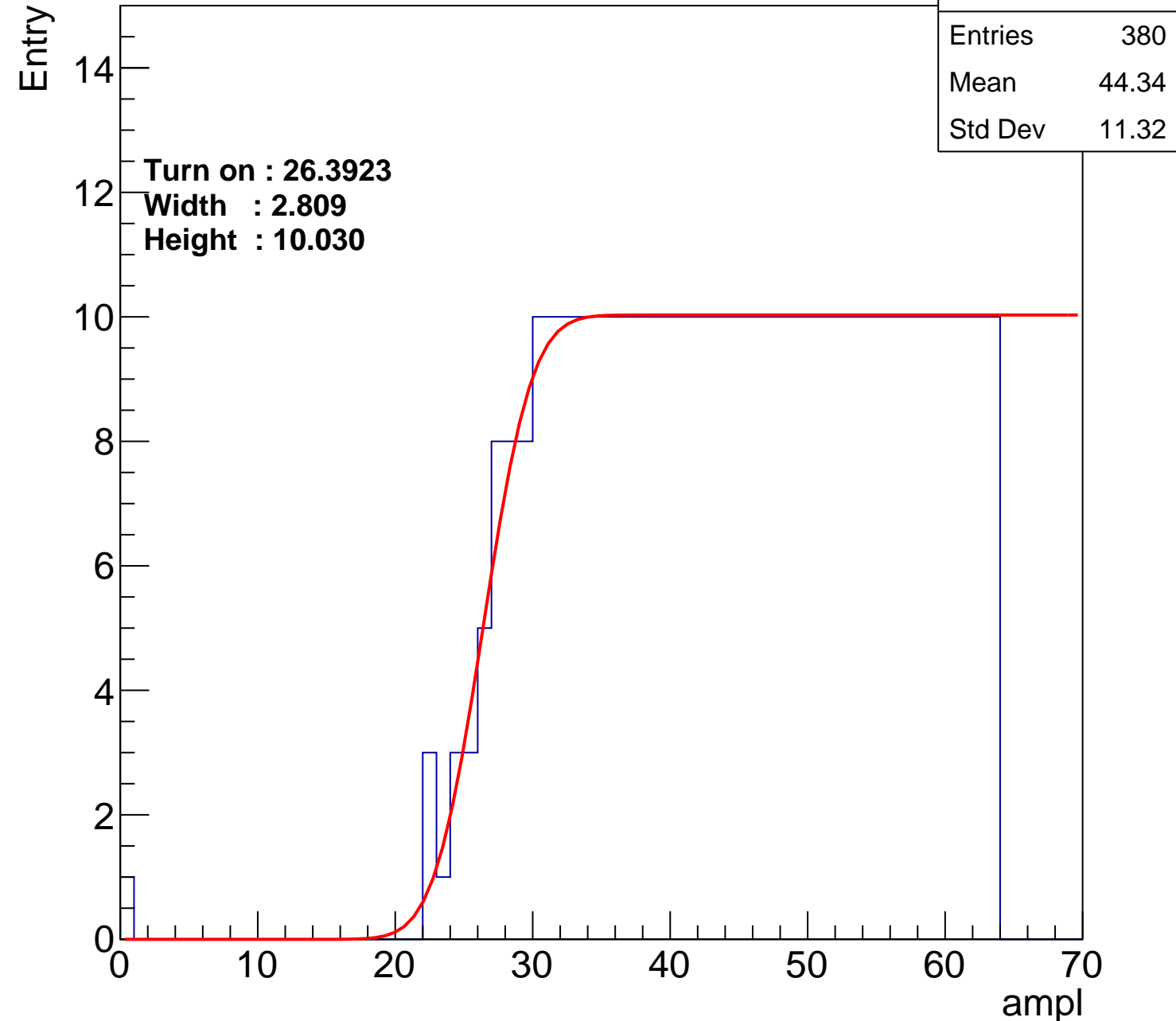
Width : 2.809

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch55

calib_packv5_042523_0143.root, FC#13, port D2

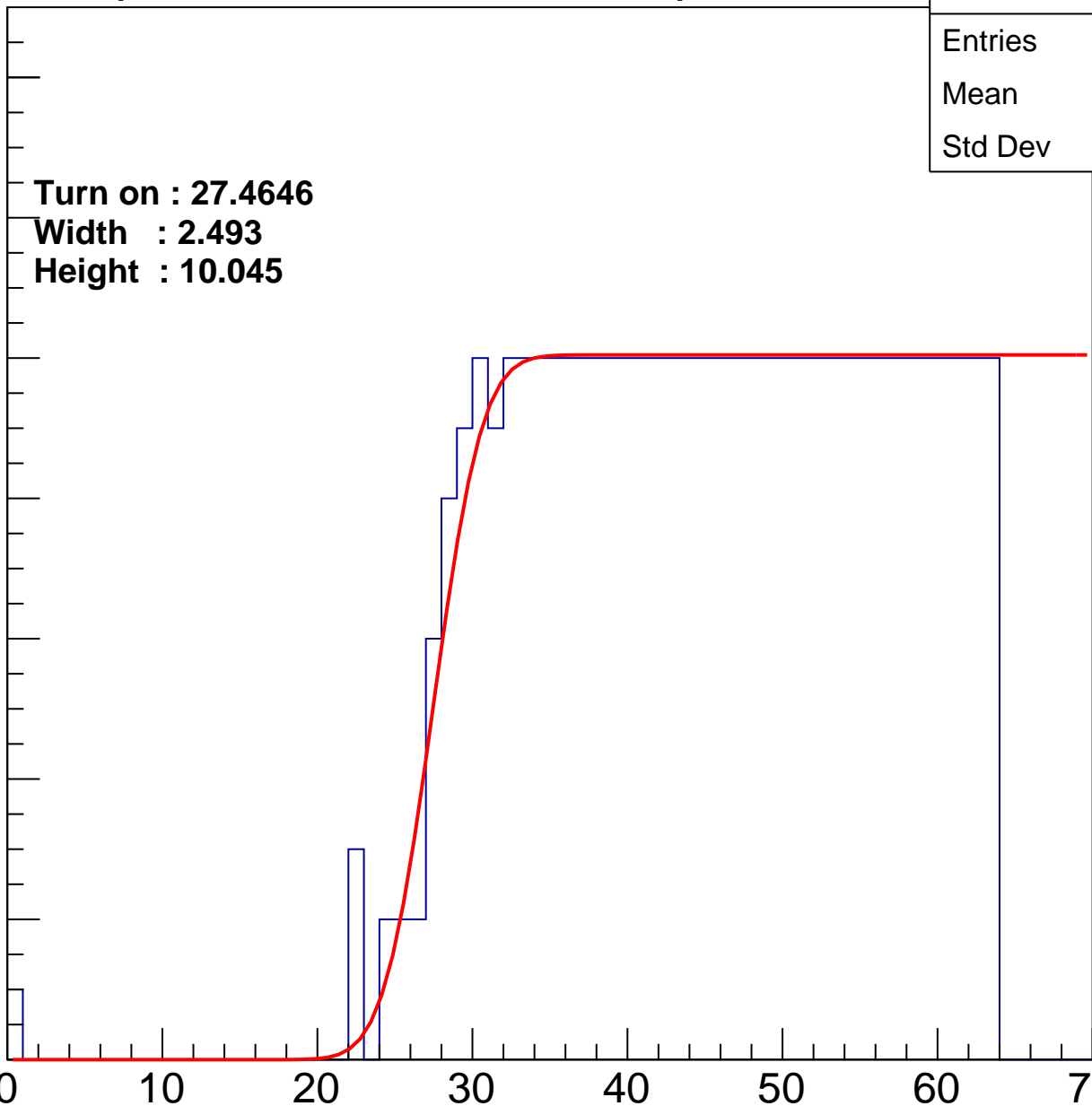
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4646
Width : 2.493
Height : 10.045

Entries	372
Mean	44.74
Std Dev	11.1

ampl



B1L003S, U20-ch56

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.93
Std Dev	11.18

Turn on : 27.4839

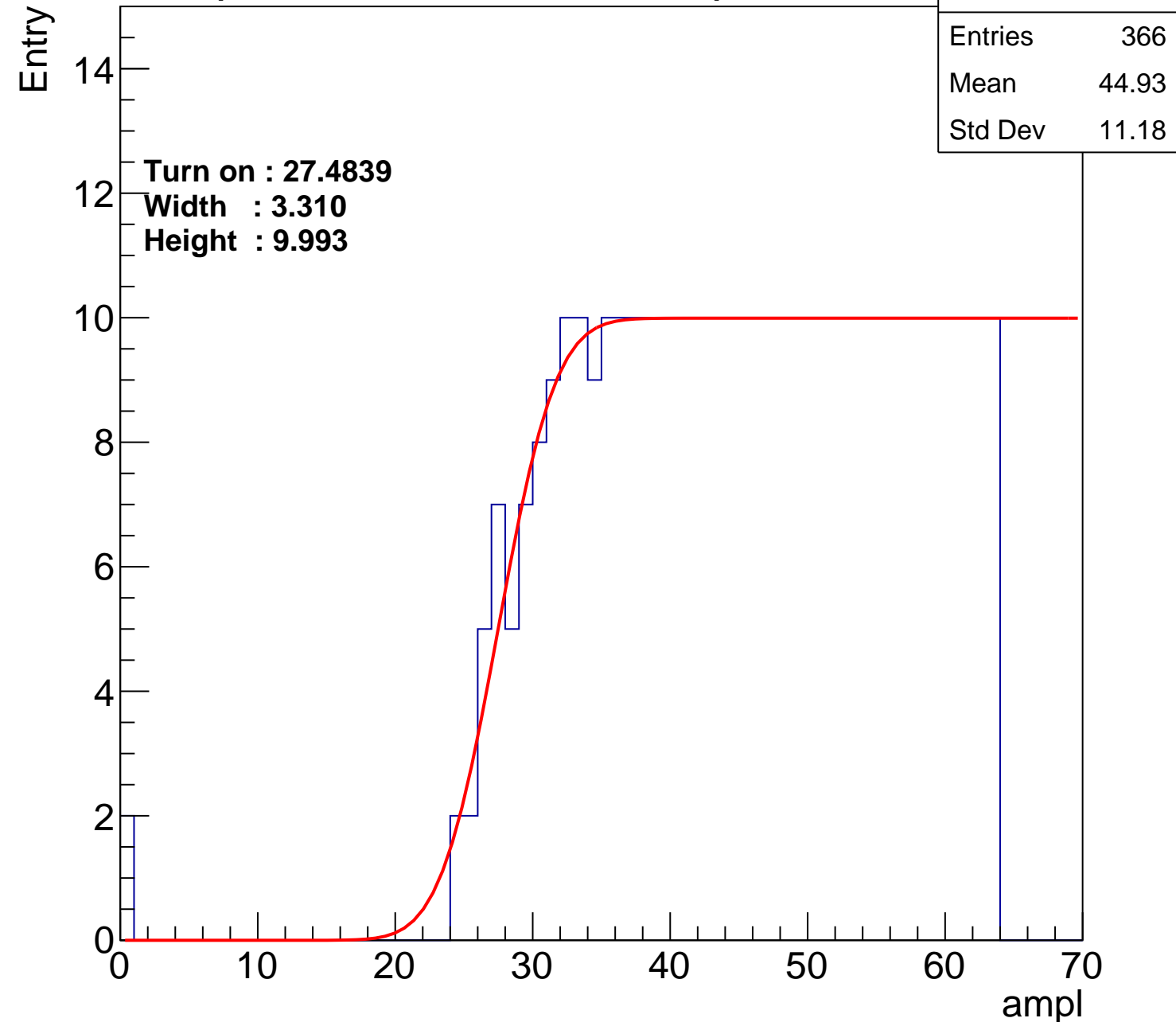
Width : 3.310

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.04
Std Dev	11.14

Turn on : 28.1410

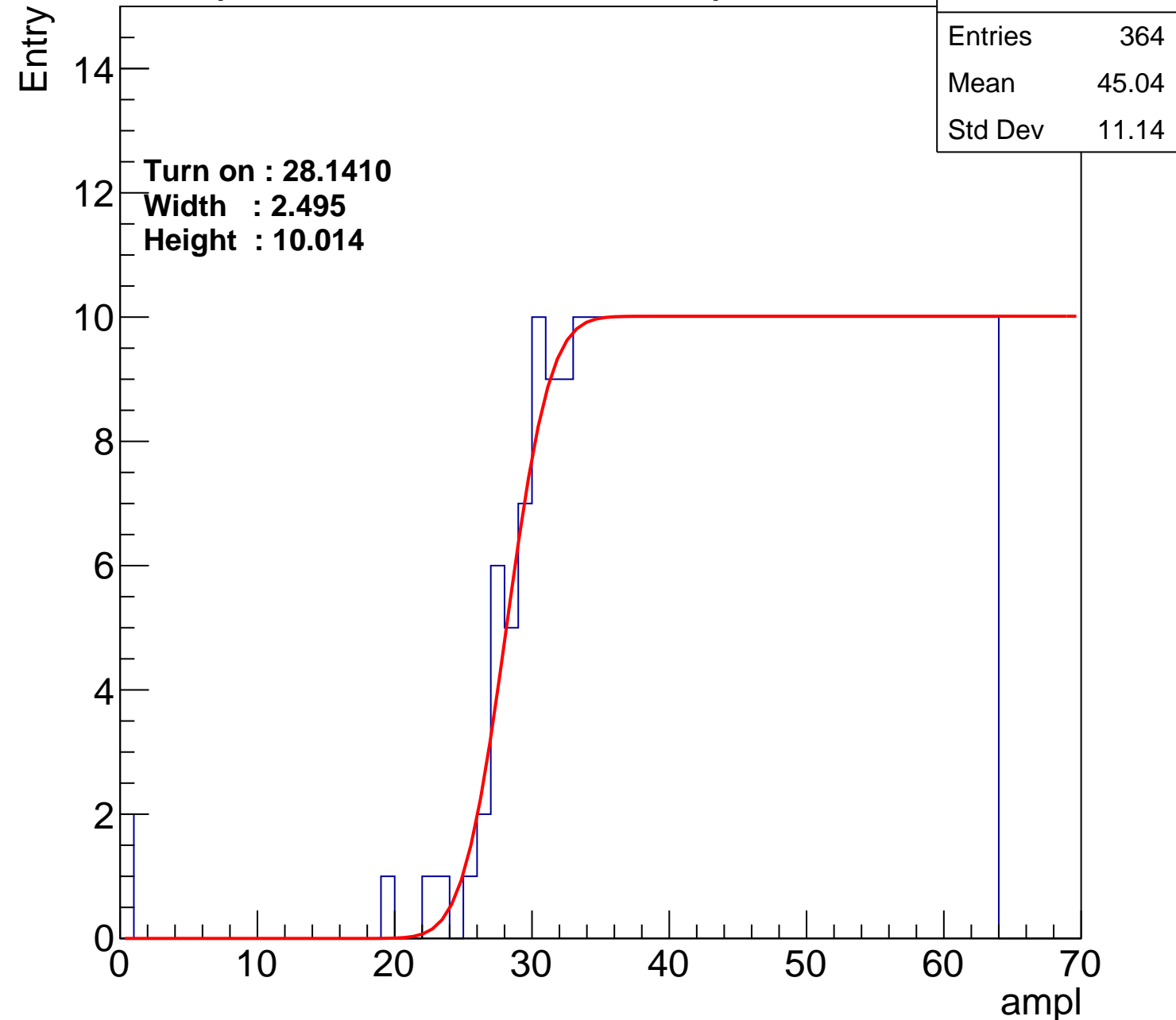
Width : 2.495

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch58

calib_packv5_042523_0143.root, FC#13, port D2

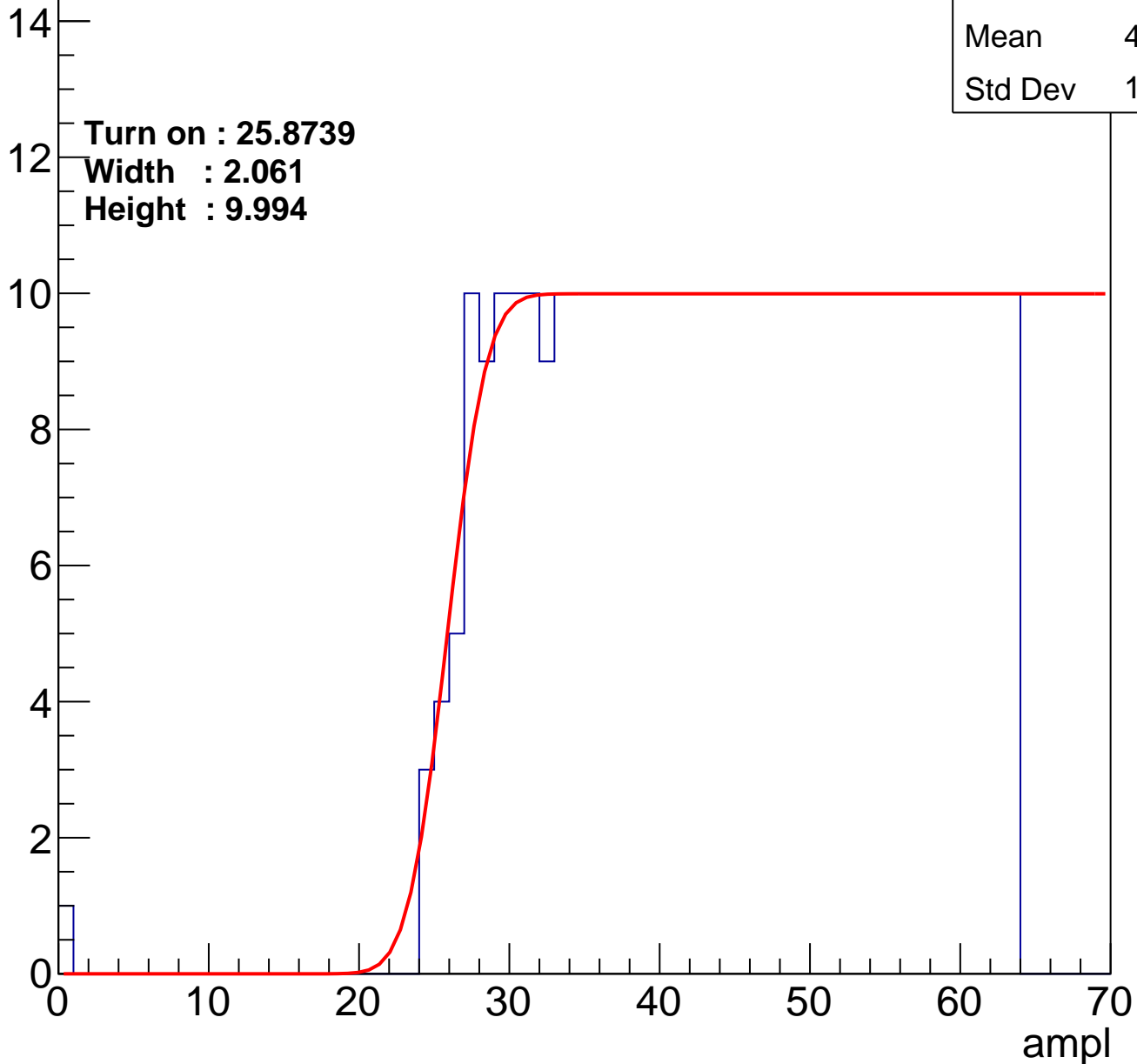
Entries	381
Mean	44.34
Std Dev	11.26

Turn on : 25.8739

Width : 2.061

Height : 9.994

Entry



B1L003S, U20-ch59

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 26.1909

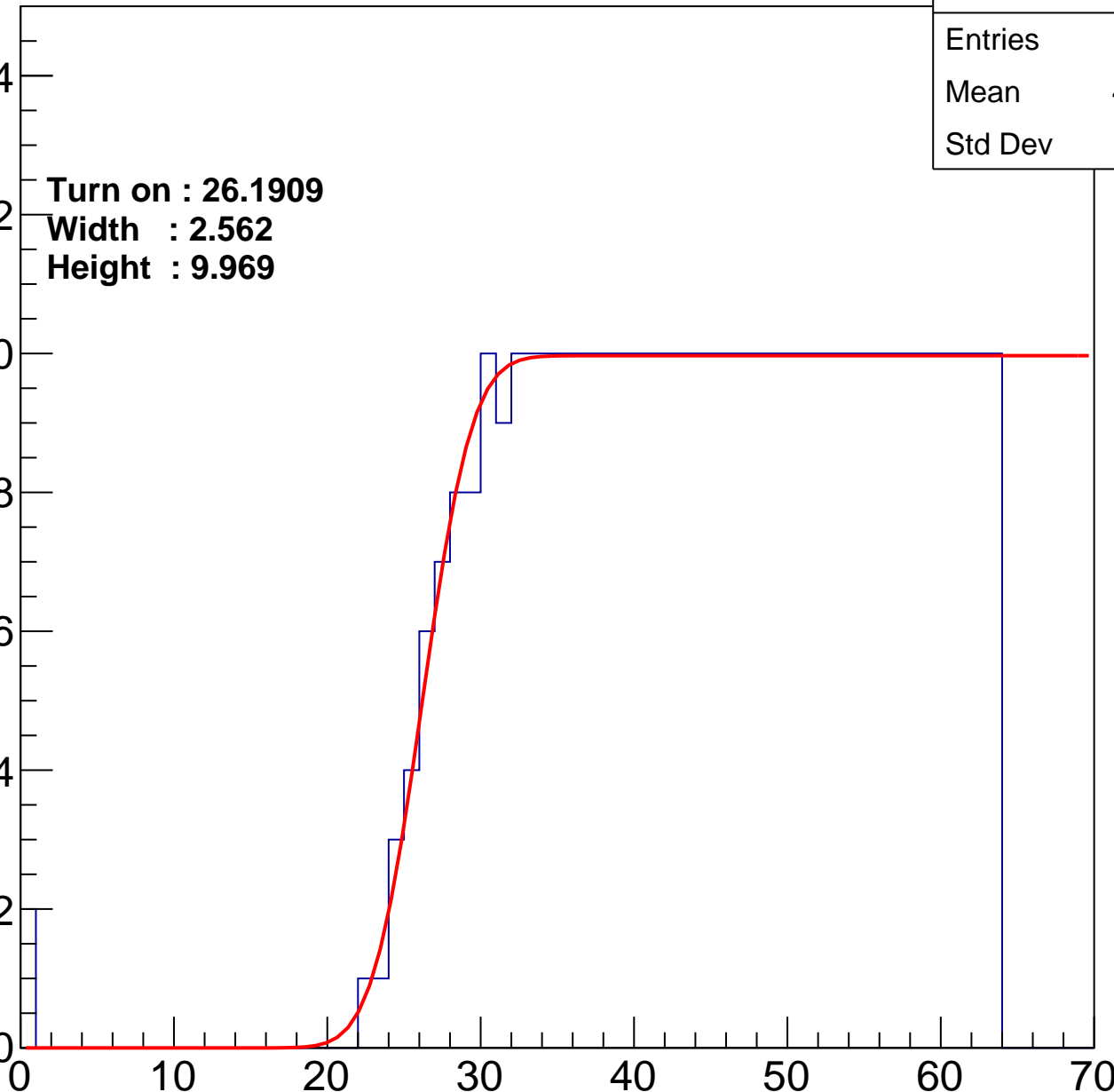
Width : 2.562

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.2309

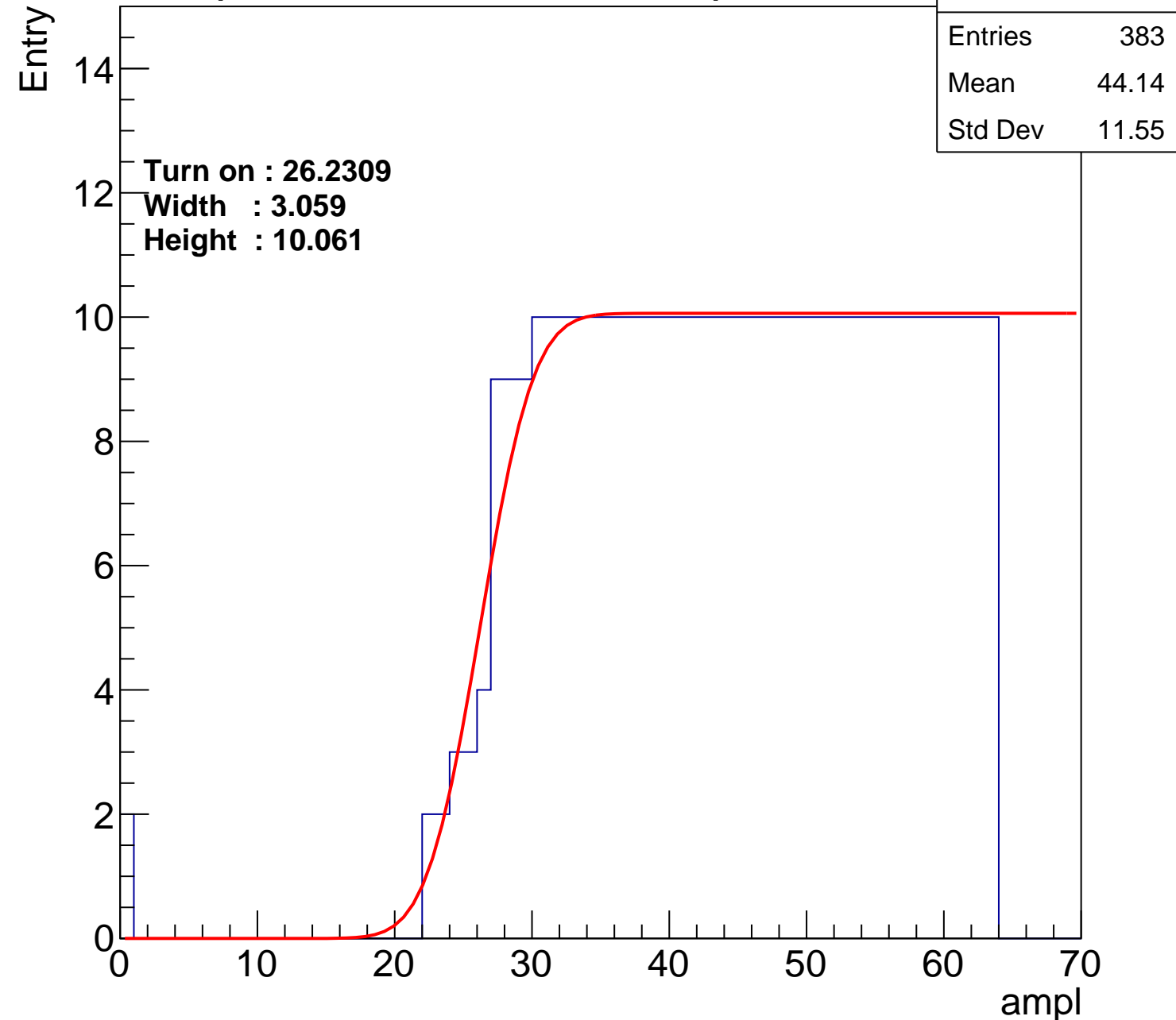
Width : 3.059

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch61

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 25.8658

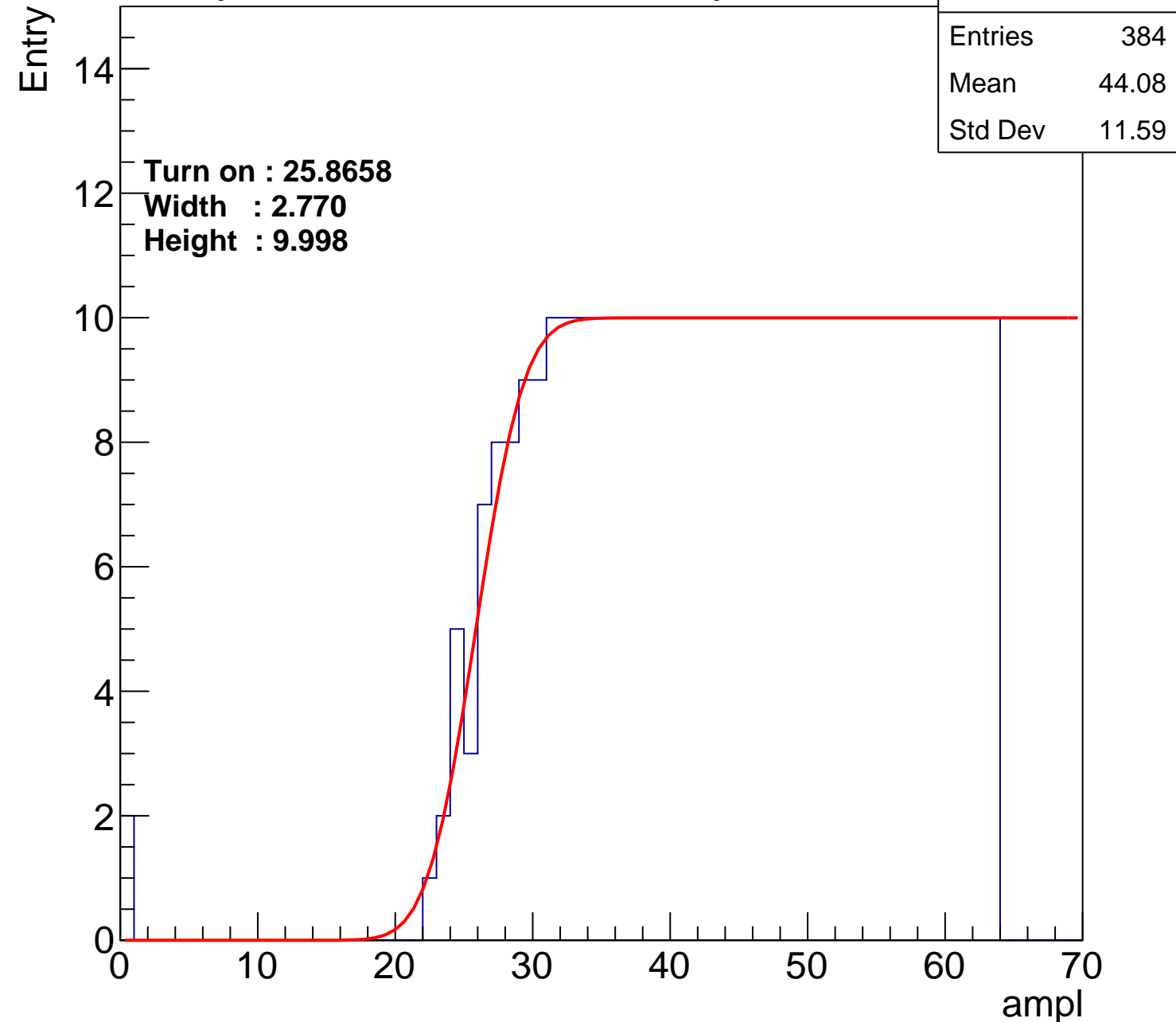
Width : 2.770

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch62

calib_packv5_042523_0143.root, FC#13, port D2

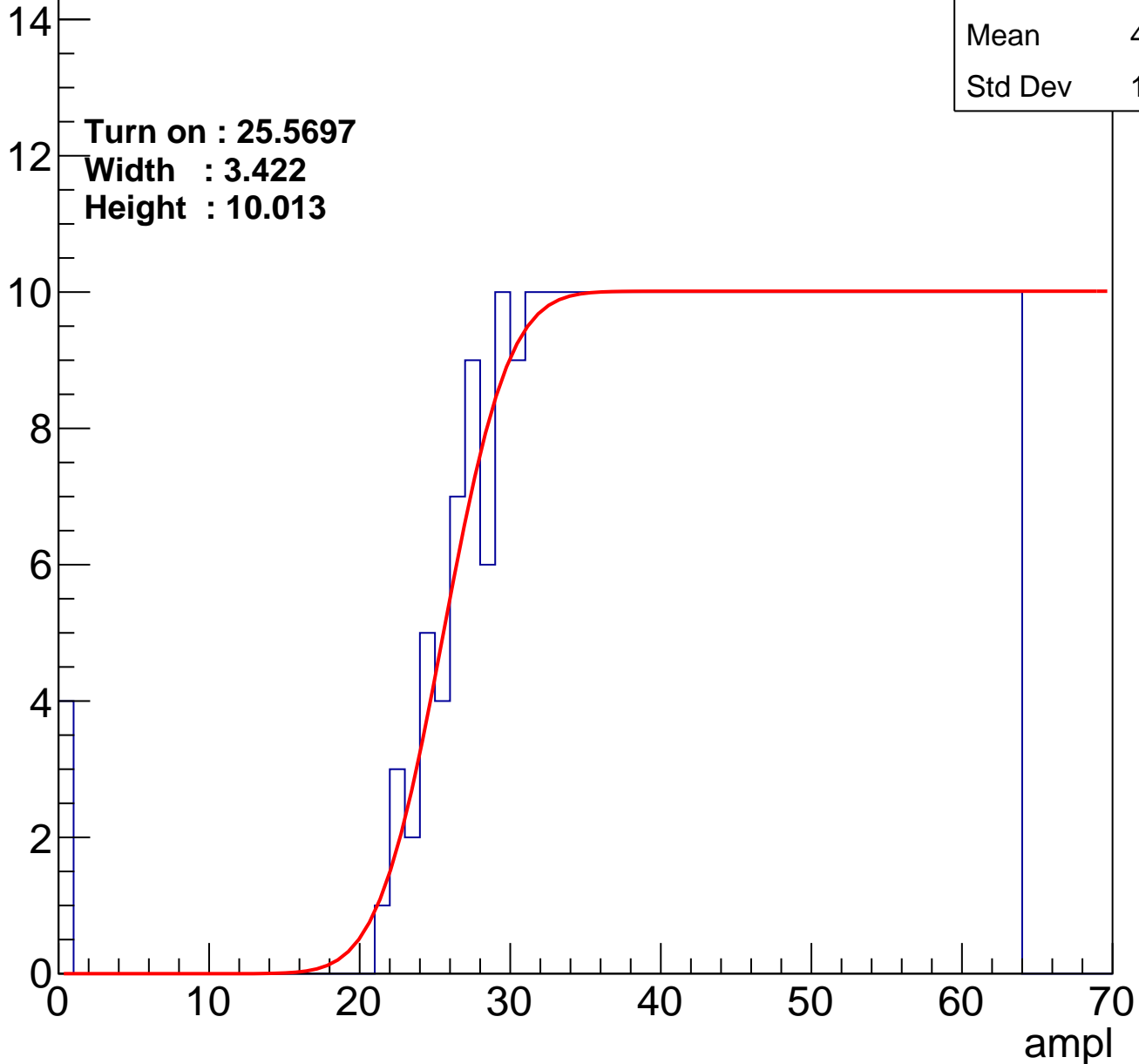
Entries	390
Mean	43.63
Std Dev	12.12

Turn on : 25.5697

Width : 3.422

Height : 10.013

Entry



B1L003S, U20-ch63

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.61
Std Dev	11.66

Turn on : 27.7578

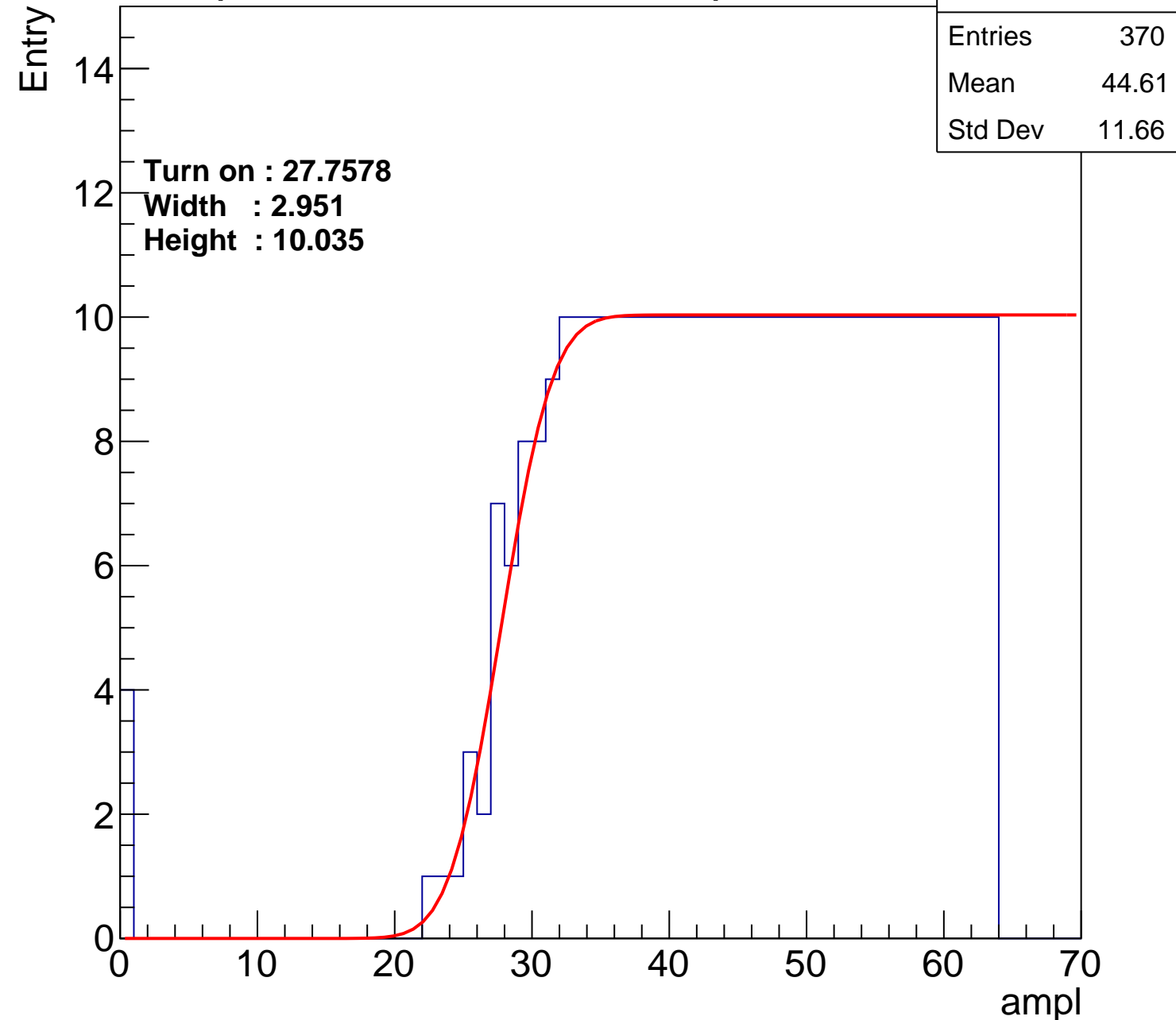
Width : 2.951

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch64

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.04
Std Dev	11.13

Turn on : 28.1279

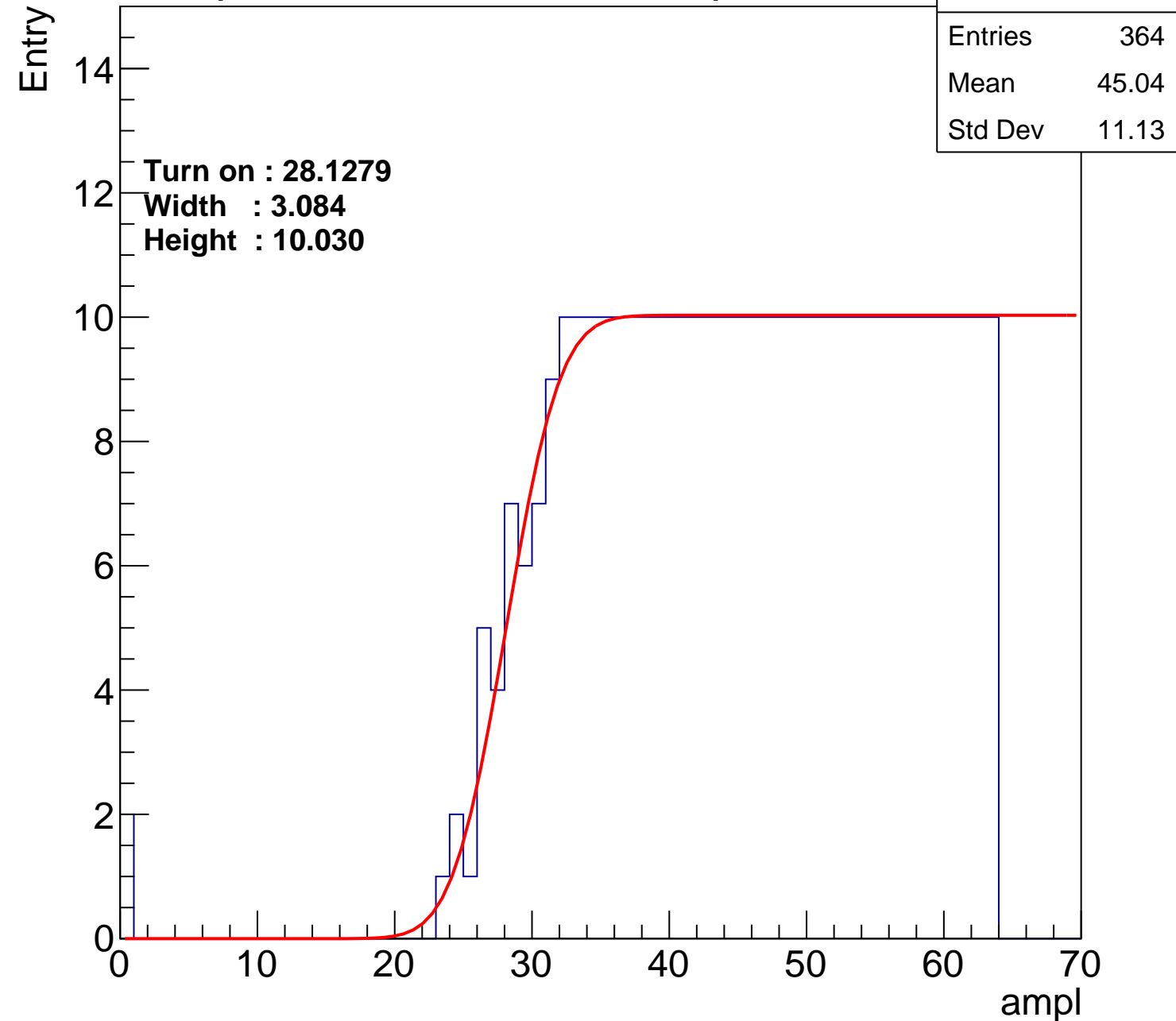
Width : 3.084

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.79
Std Dev	11.27

Turn on : 27.6996

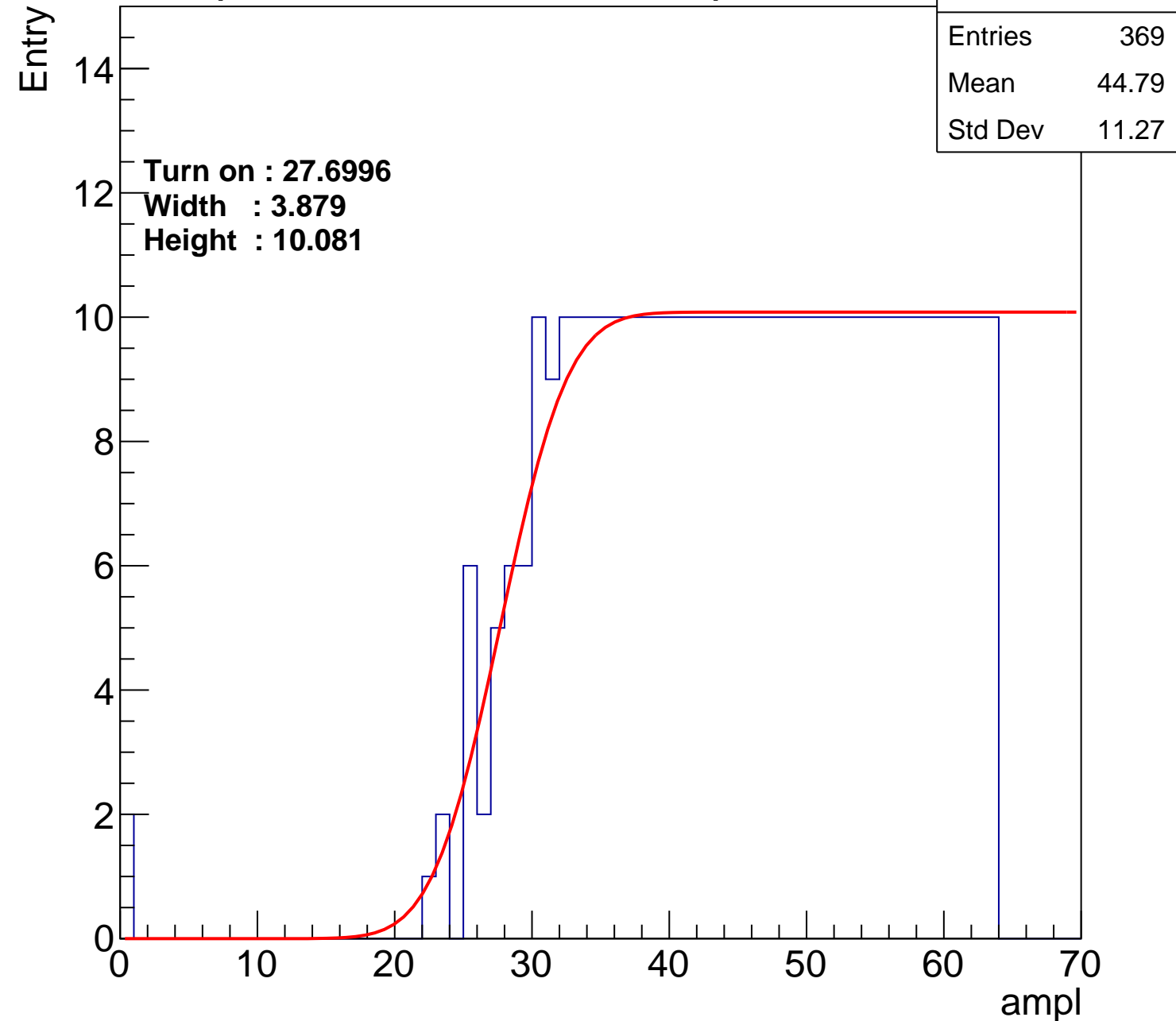
Width : 3.879

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch66

calib_packv5_042523_0143.root, FC#13, port D2

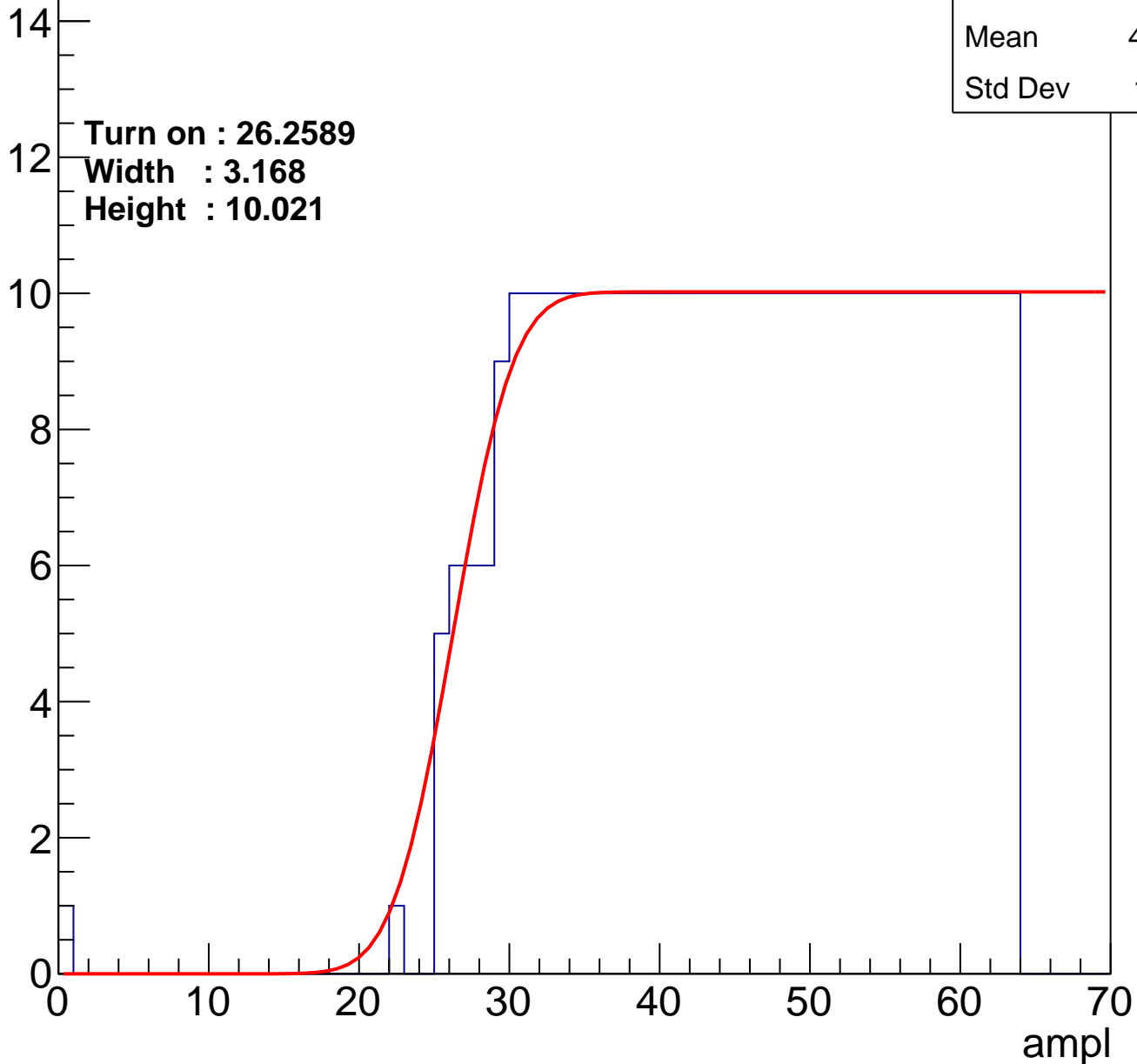
Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 26.2589

Width : 3.168

Height : 10.021

Entry



B1L003S, U20-ch67

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.38
Std Dev	11.25

Turn on : 26.4843

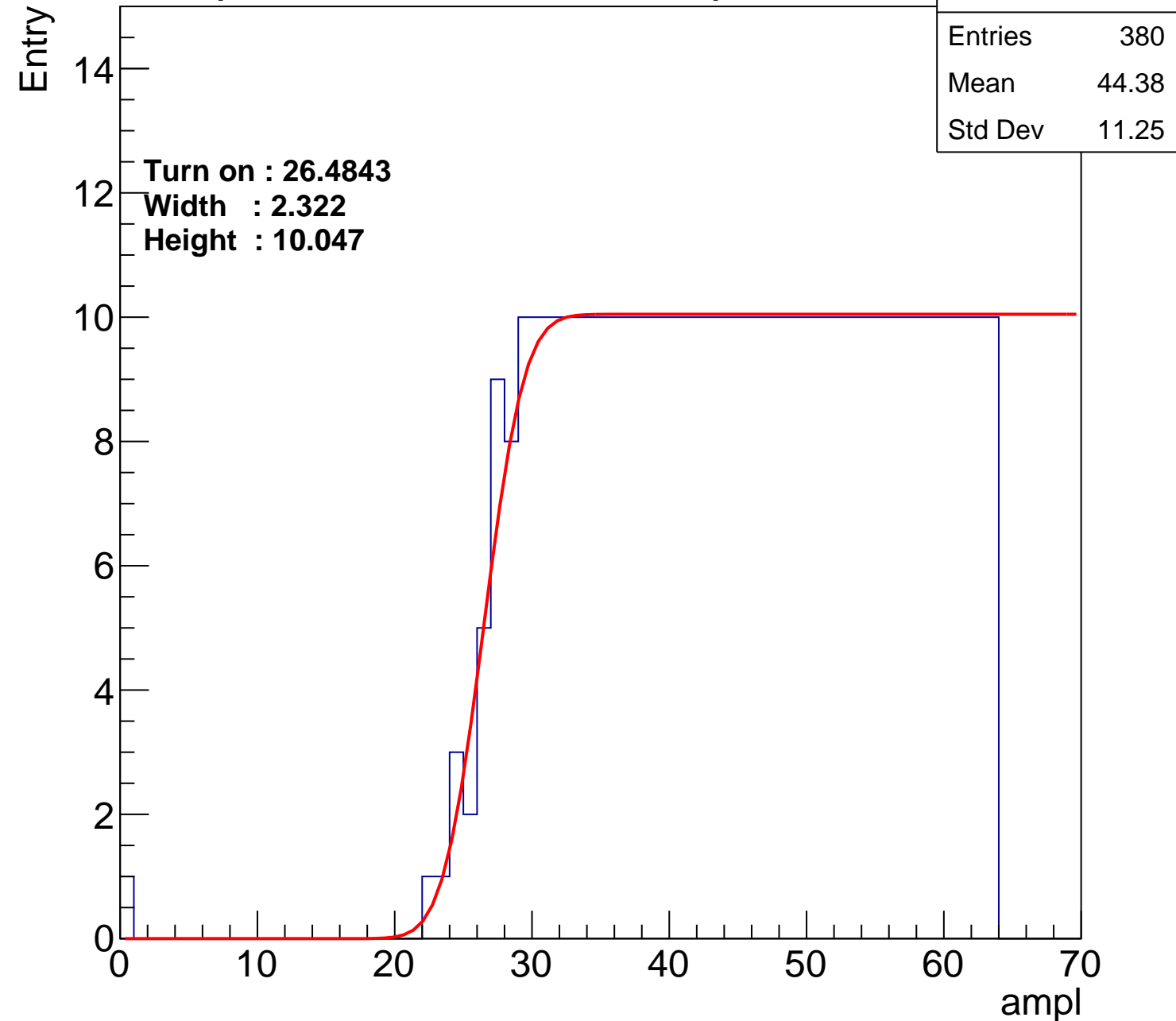
Width : 2.322

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch68

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.67
Std Dev	11.94

Turn on : 25.1632

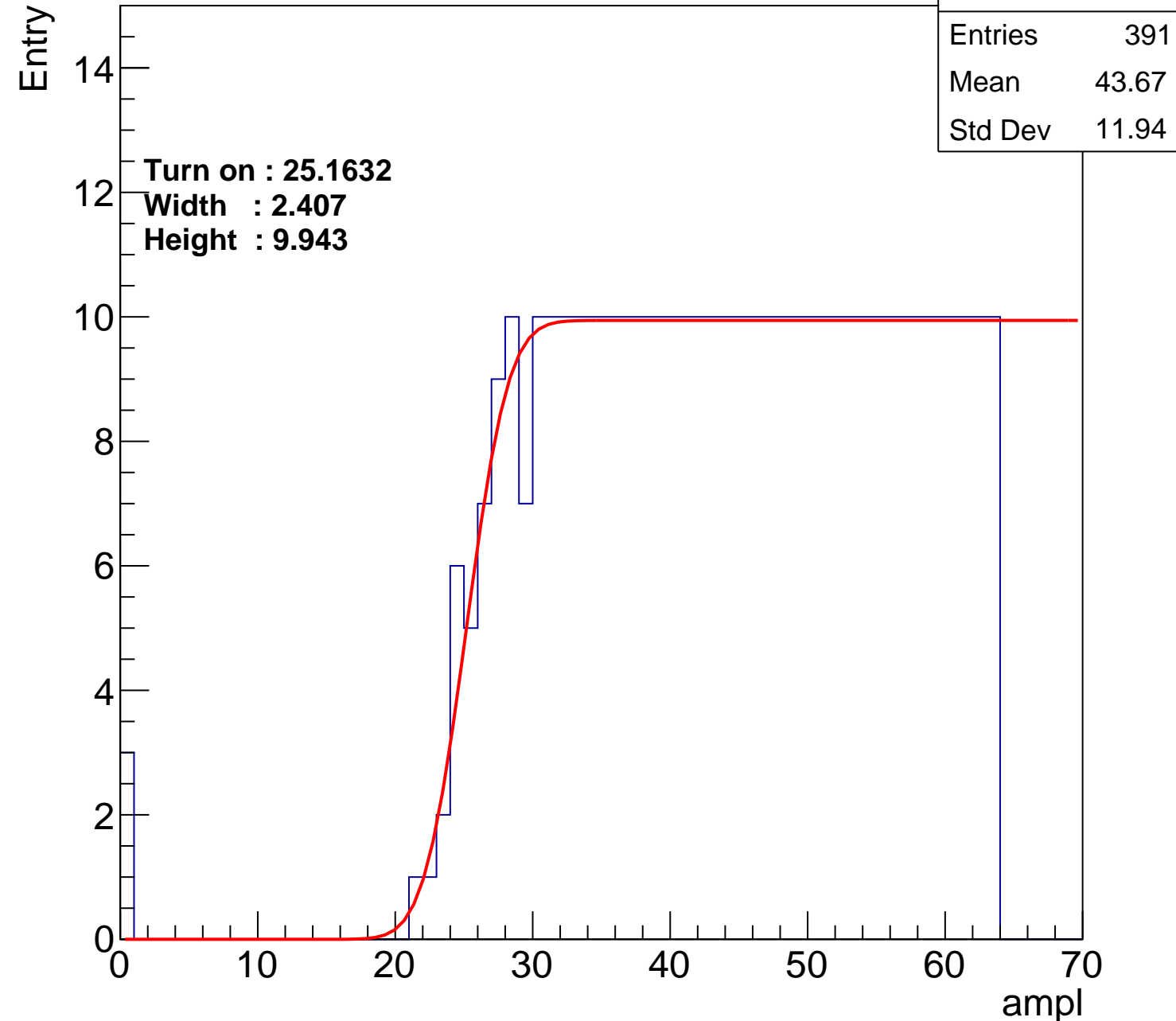
Width : 2.407

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.5
Std Dev	11.57

Turn on : 26.8570

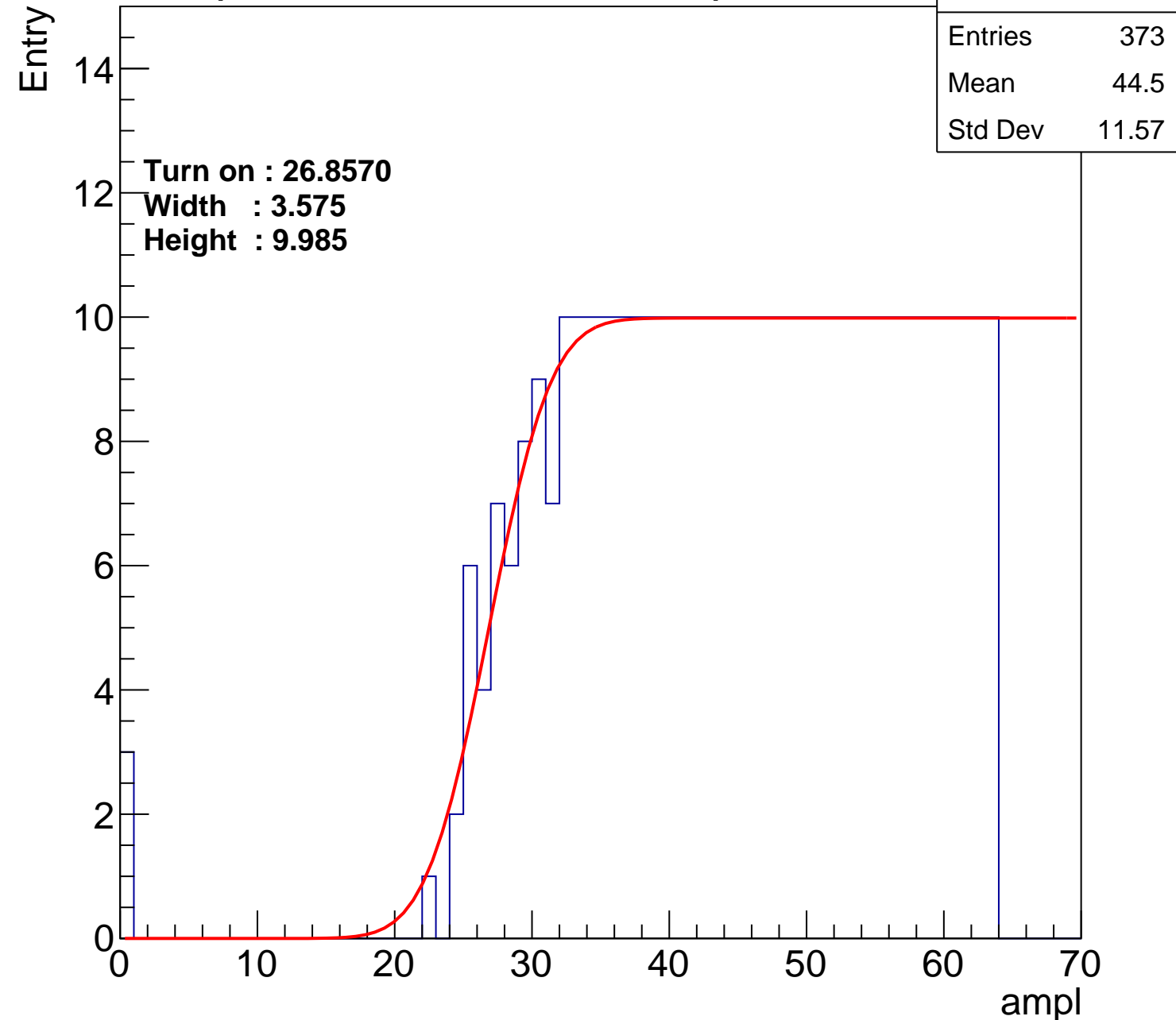
Width : 3.575

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch70

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.94
Std Dev	11.18

Turn on : 27.9580

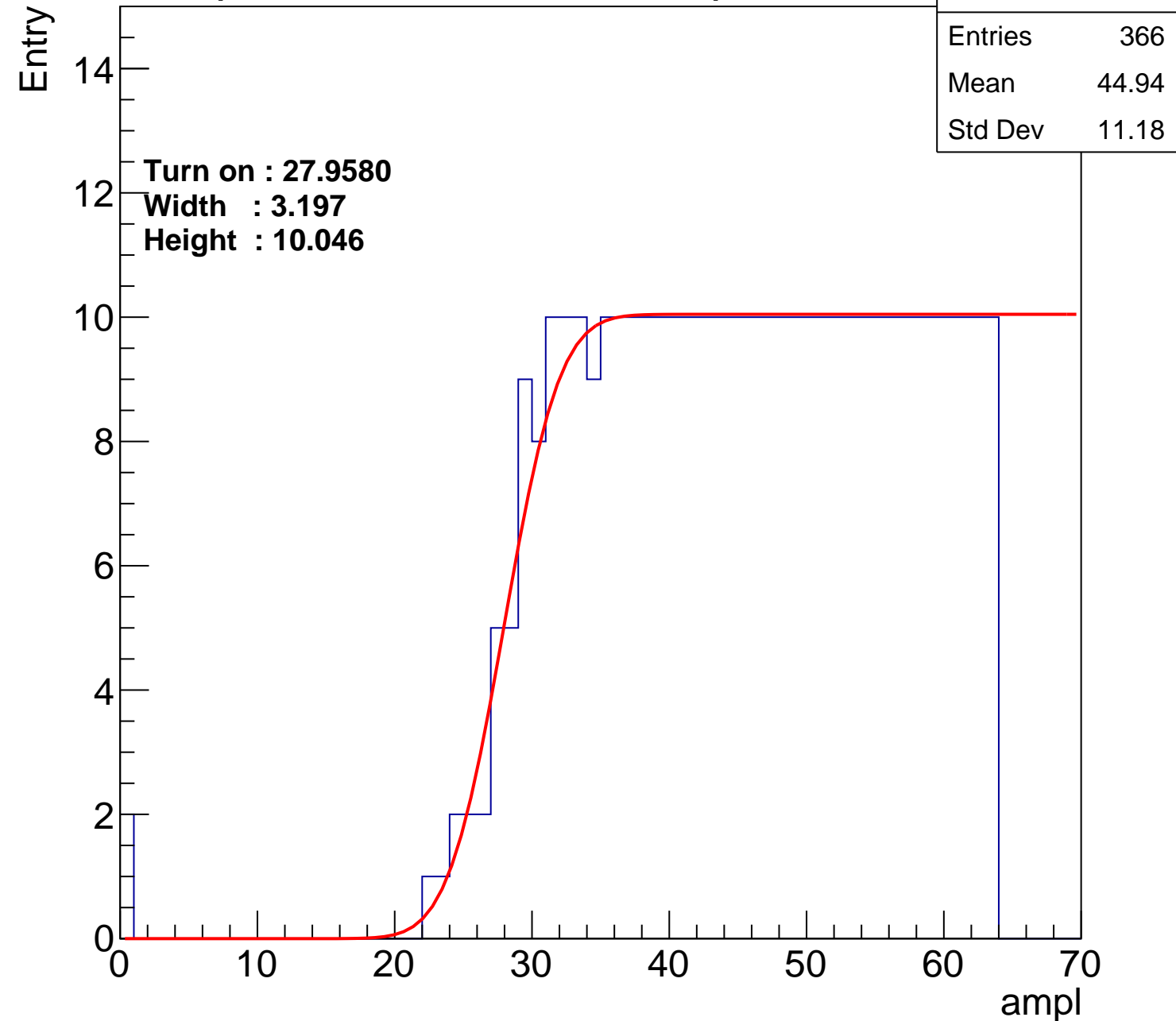
Width : 3.197

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch71

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.27
Std Dev	10.81

Turn on : 27.7301

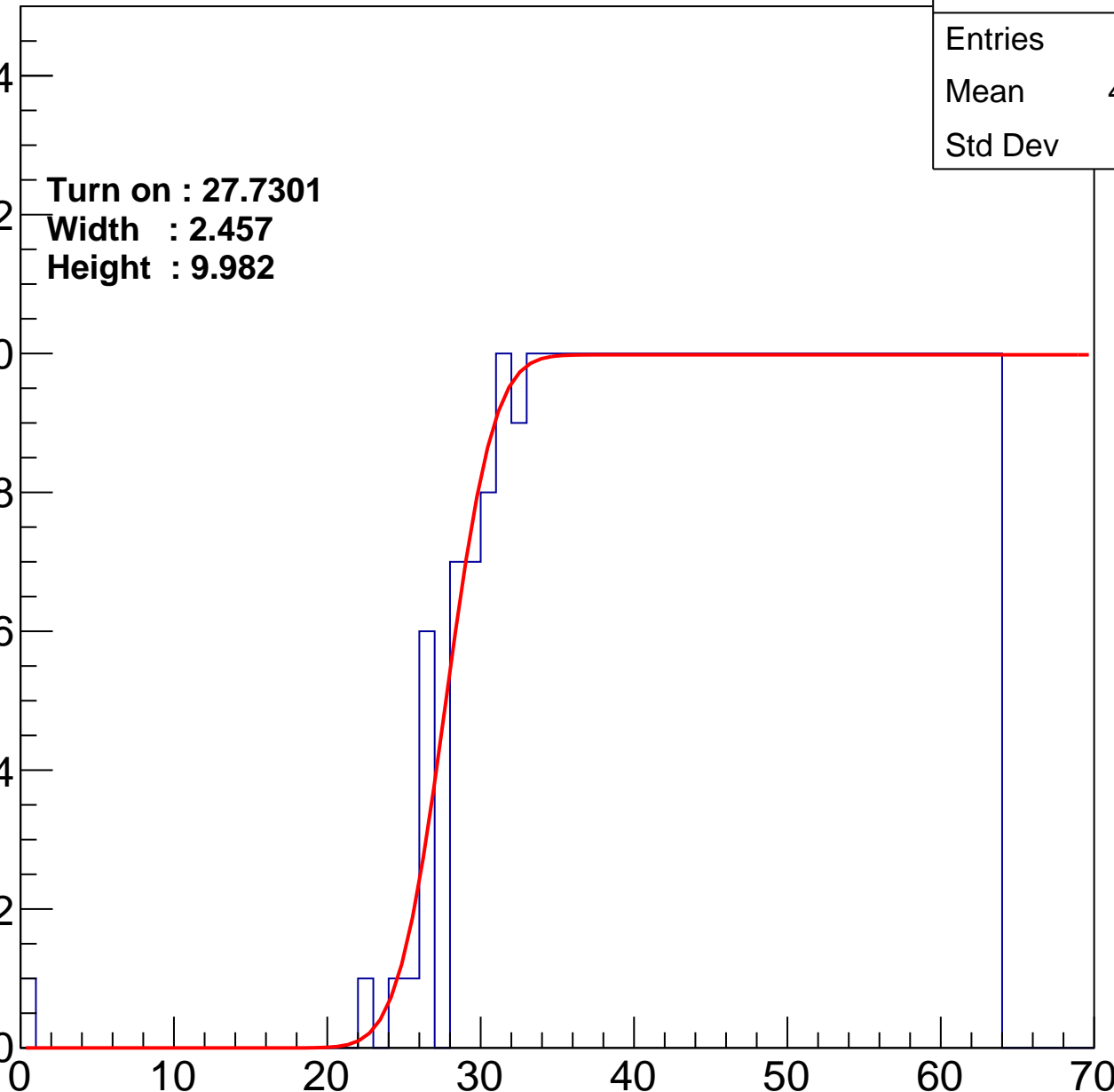
Width : 2.457

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch72

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.57
Std Dev	10.82

Turn on : 28.4905

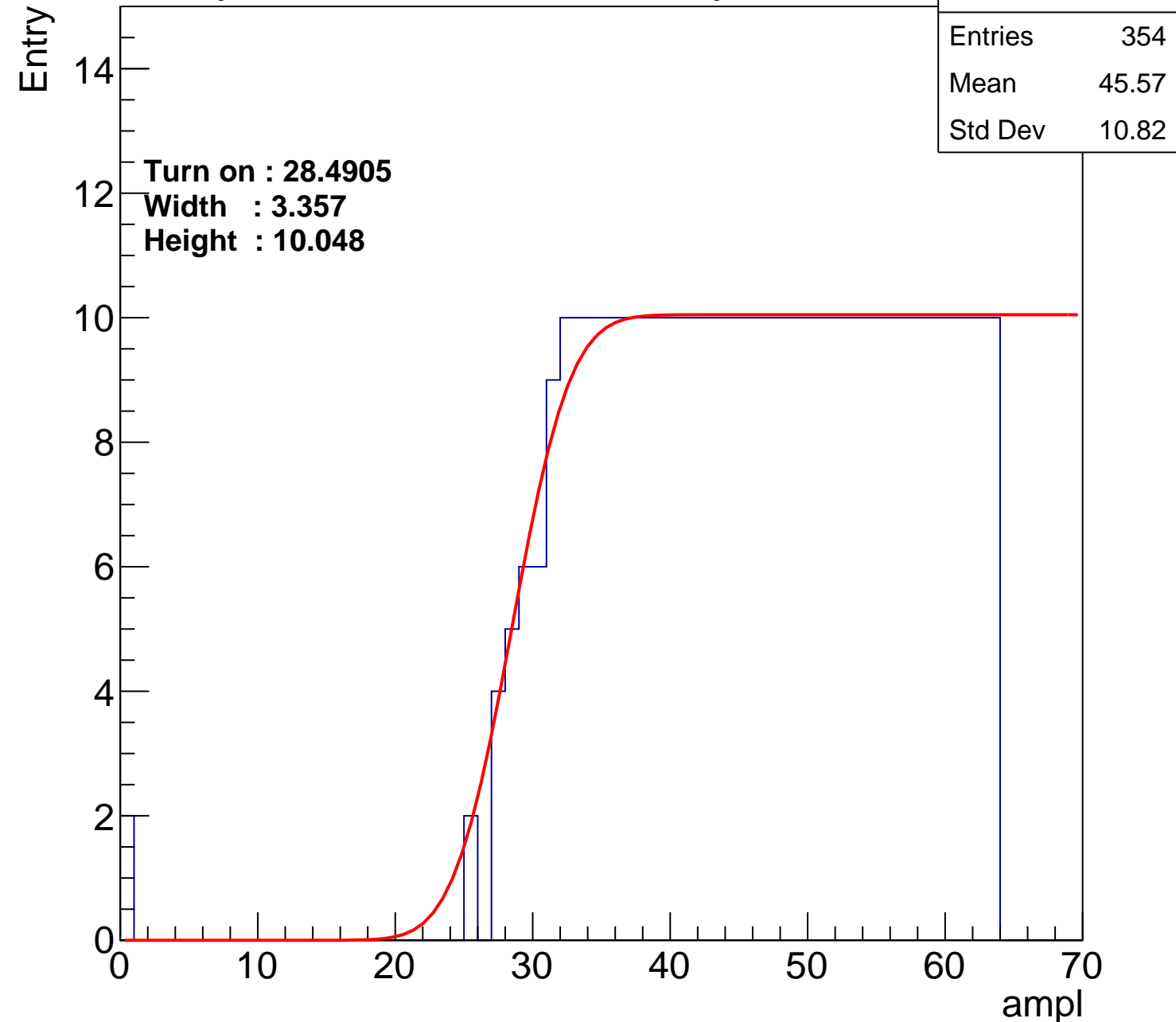
Width : 3.357

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch73

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.19
Std Dev	11.39

Turn on : 28.5914

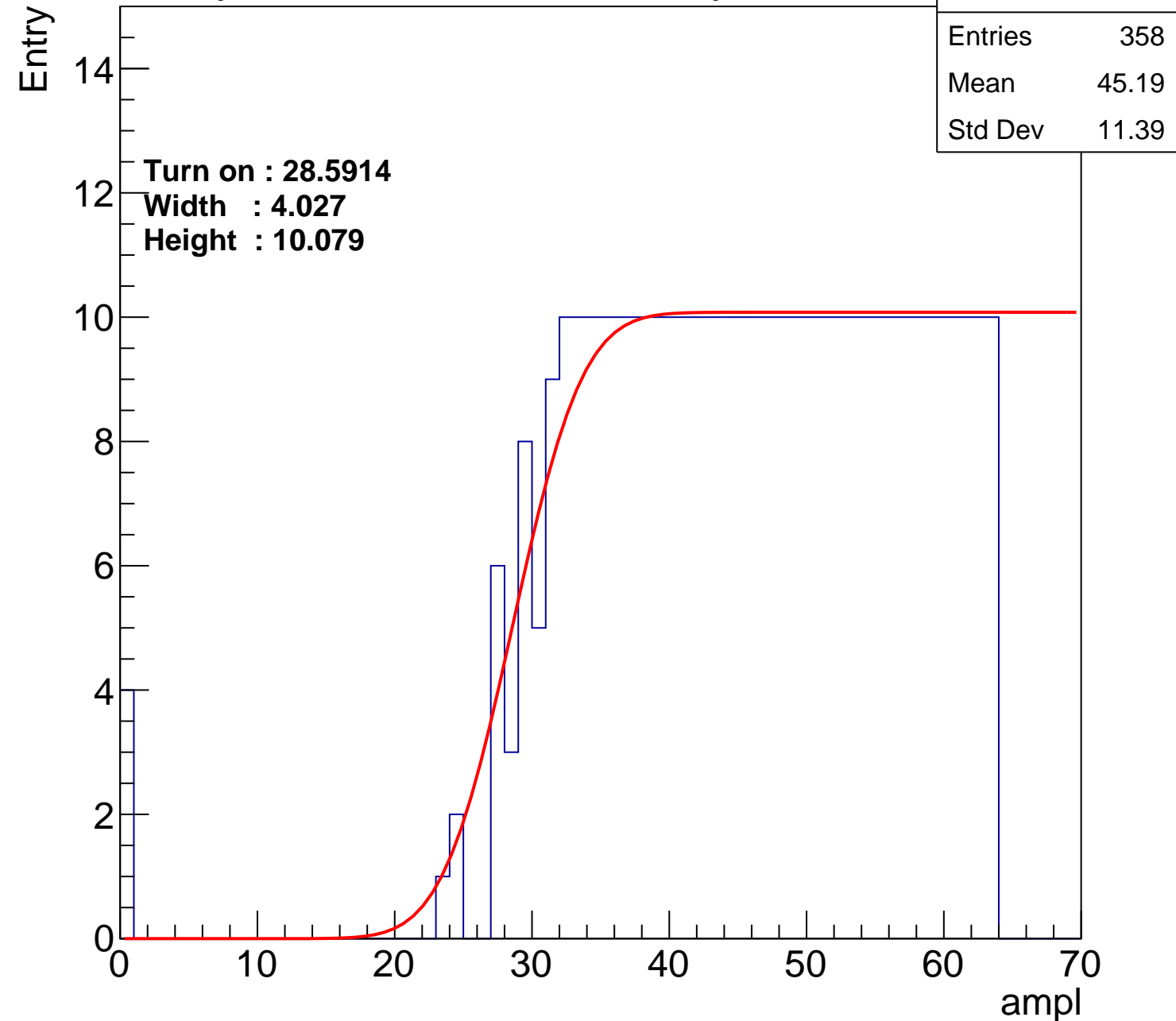
Width : 4.027

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.44
Std Dev	11.38

Turn on : 27.0002

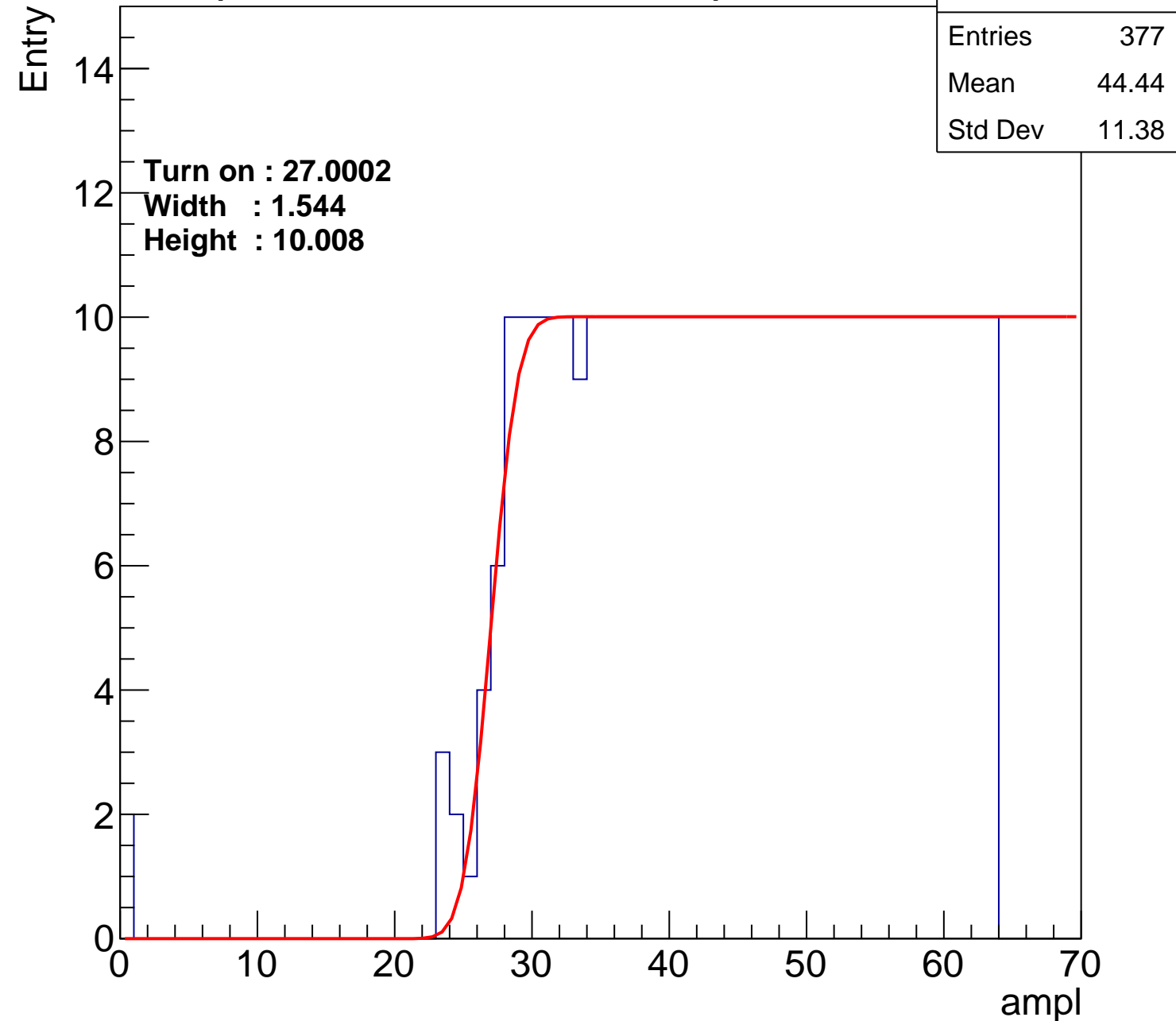
Width : 1.544

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch75

calib_packv5_042523_0143.root, FC#13, port D2

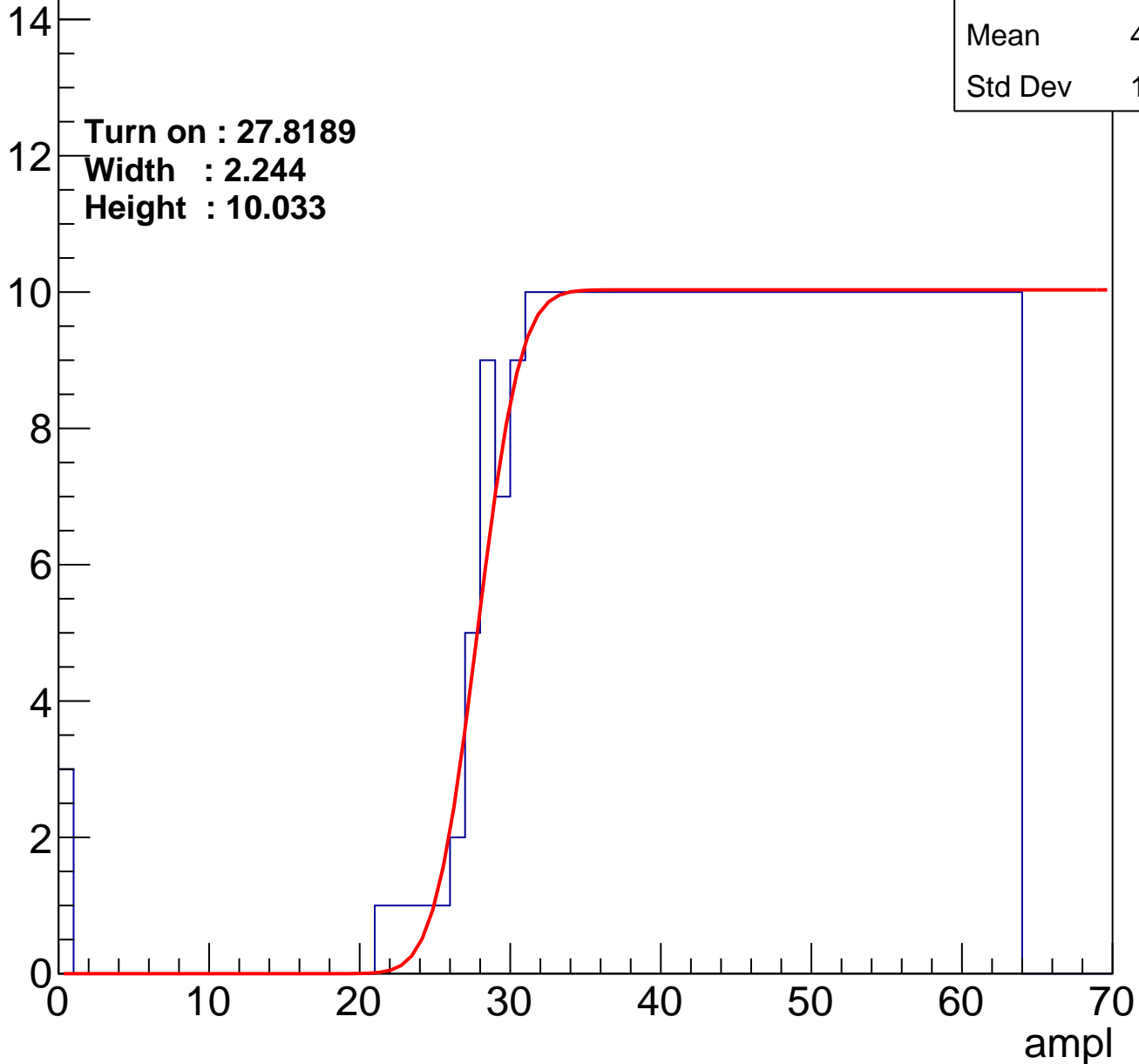
Entries	370
Mean	44.69
Std Dev	11.45

Turn on : 27.8189

Width : 2.244

Height : 10.033

Entry



B1L003S, U20-ch76

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.06
Std Dev	11.75

Turn on : 26.0748

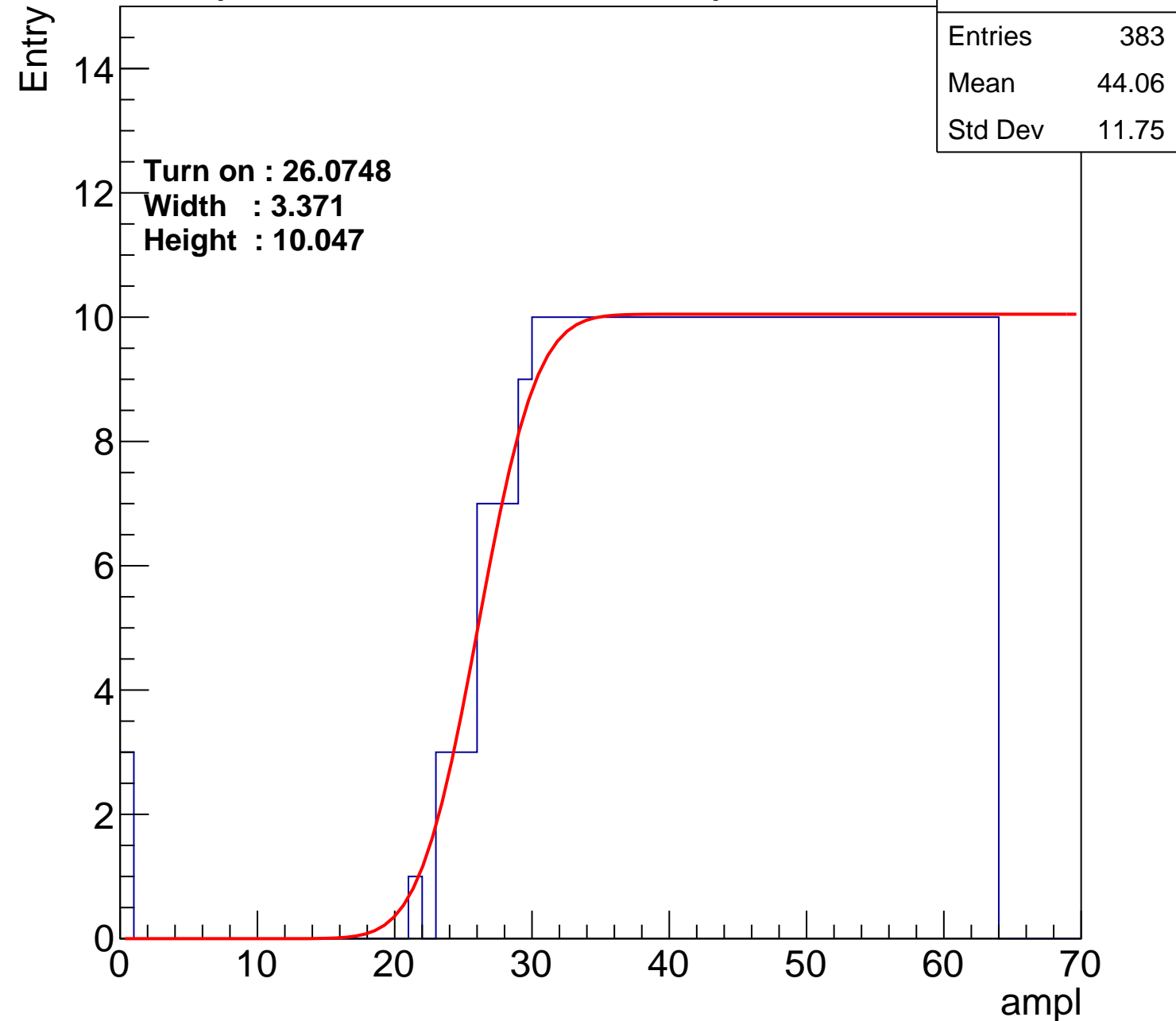
Width : 3.371

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch77

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.29
Std Dev	12.03

Turn on : 27.5473

Width : 4.437

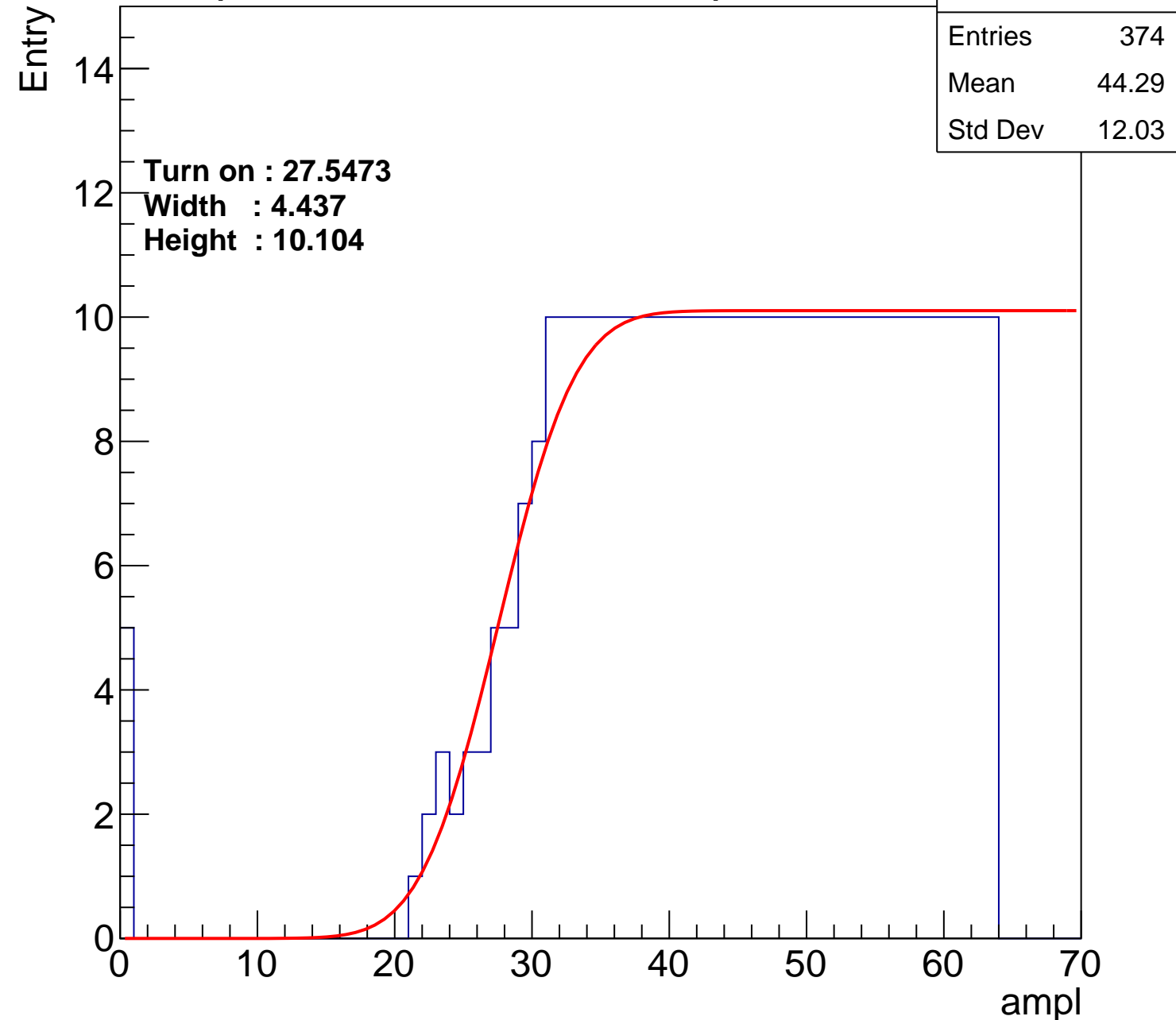
Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U20-ch78

calib_packv5_042523_0143.root, FC#13, port D2

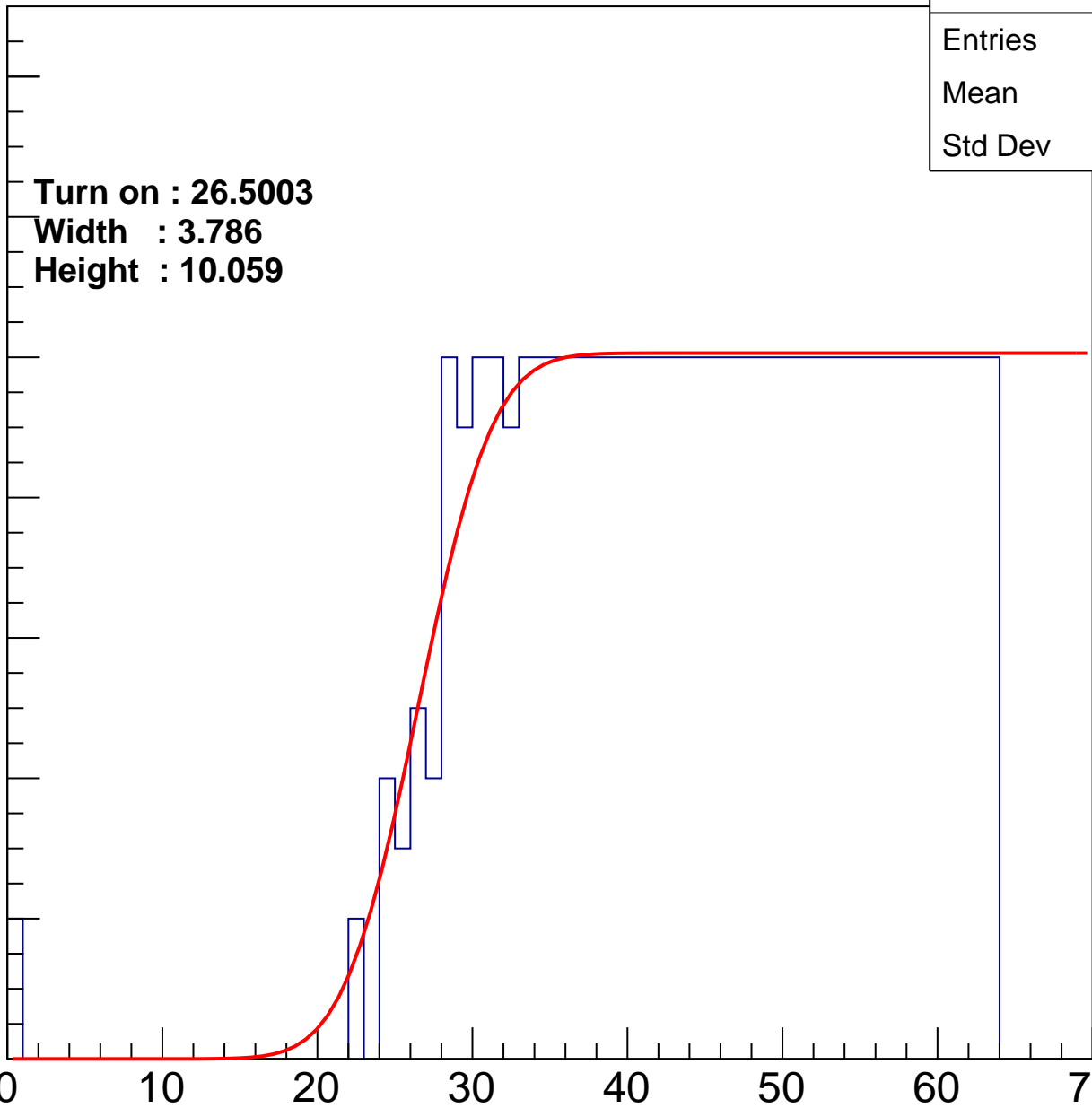
Entry

14
12
10
8
6
4
2
0

Turn on : 26.5003
Width : 3.786
Height : 10.059

Entries	378
Mean	44.37
Std Dev	11.45

ampl



B1L003S, U20-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.09
Std Dev	11.98

Turn on : 26.6073

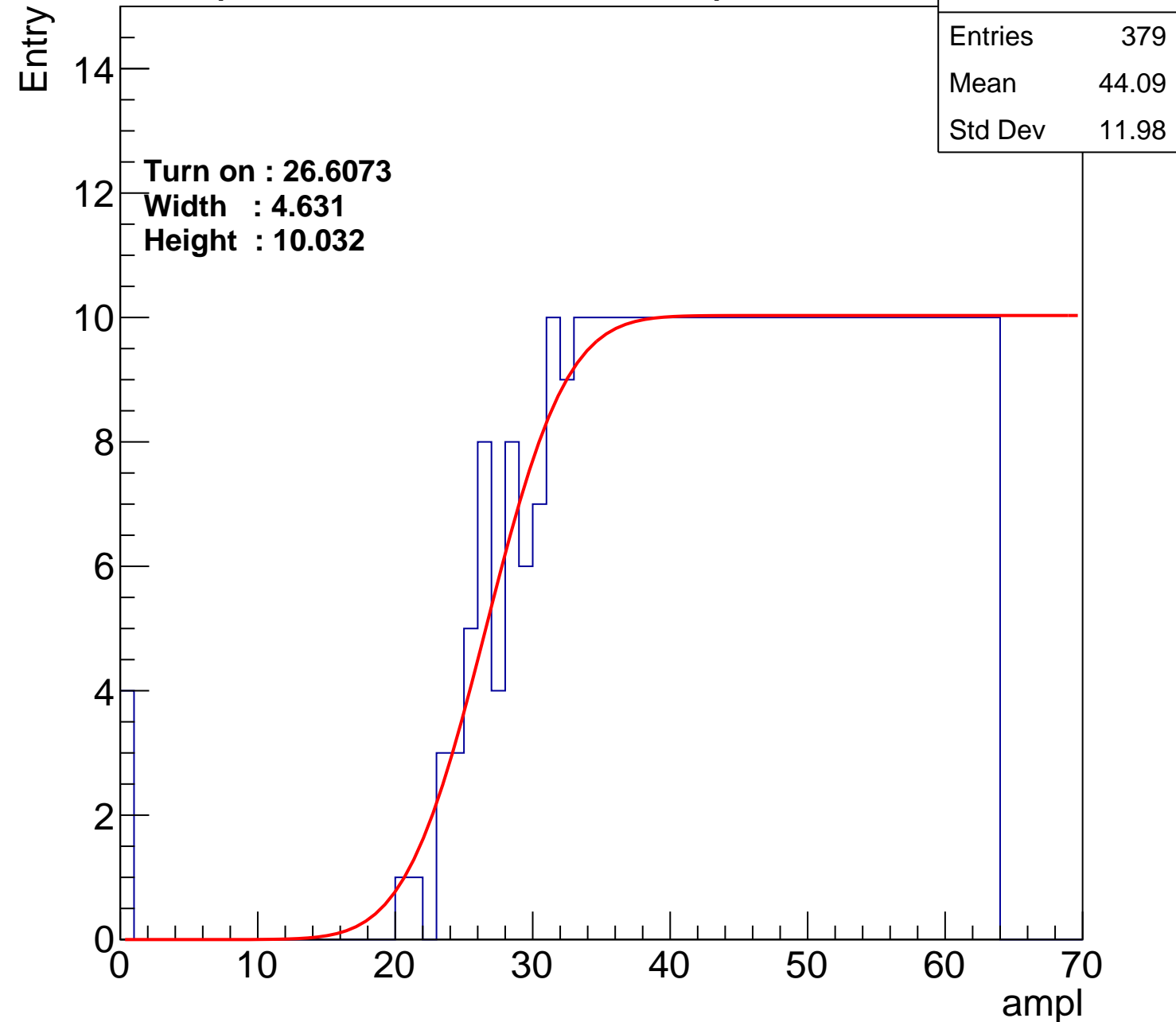
Width : 4.631

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch80

calib_packv5_042523_0143.root, FC#13, port D2

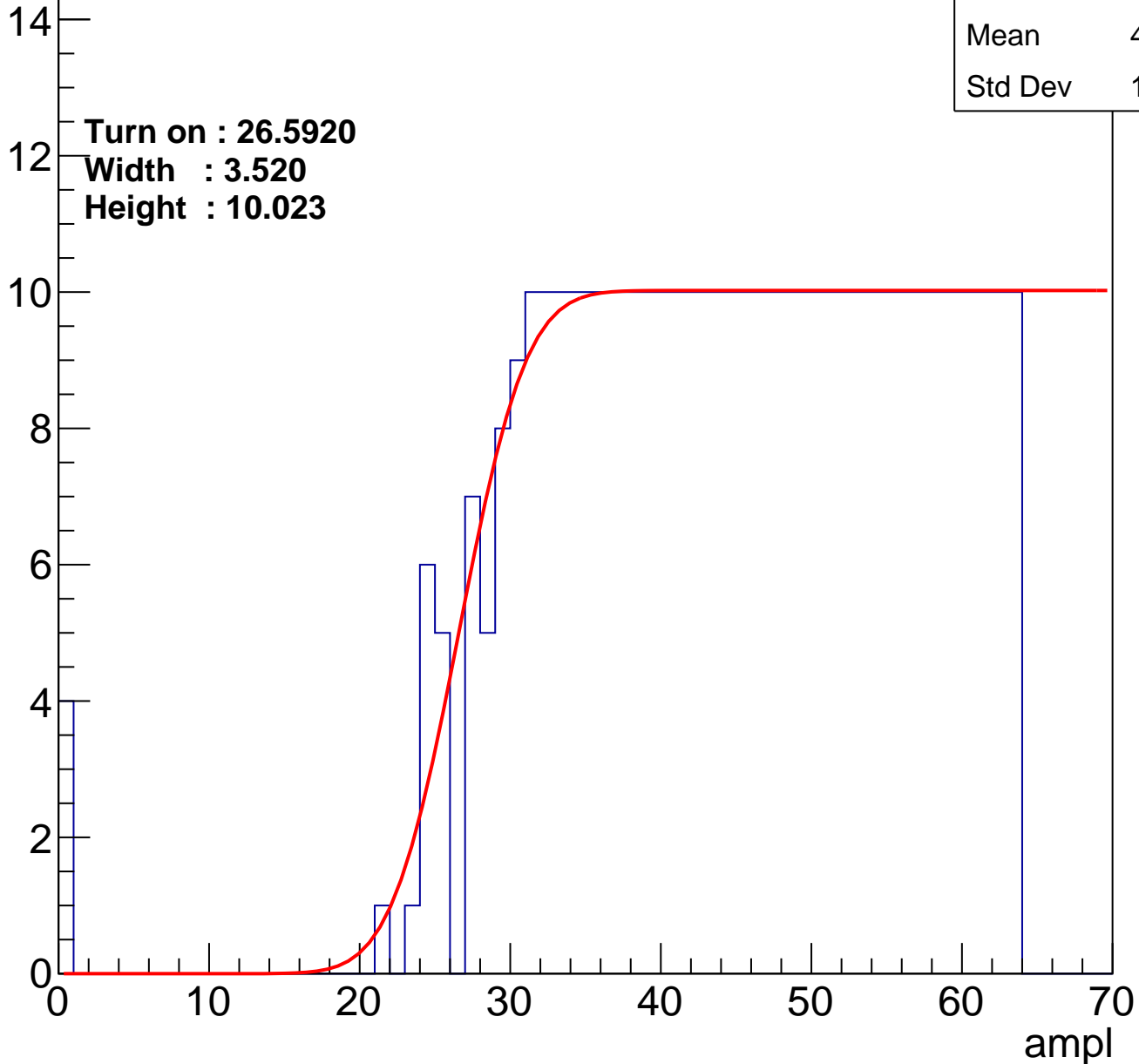
Entries	376
Mean	44.29
Std Dev	11.83

Turn on : 26.5920

Width : 3.520

Height : 10.023

Entry



B1L003S, U20-ch81

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.01
Std Dev	11.26

Turn on : 27.9561

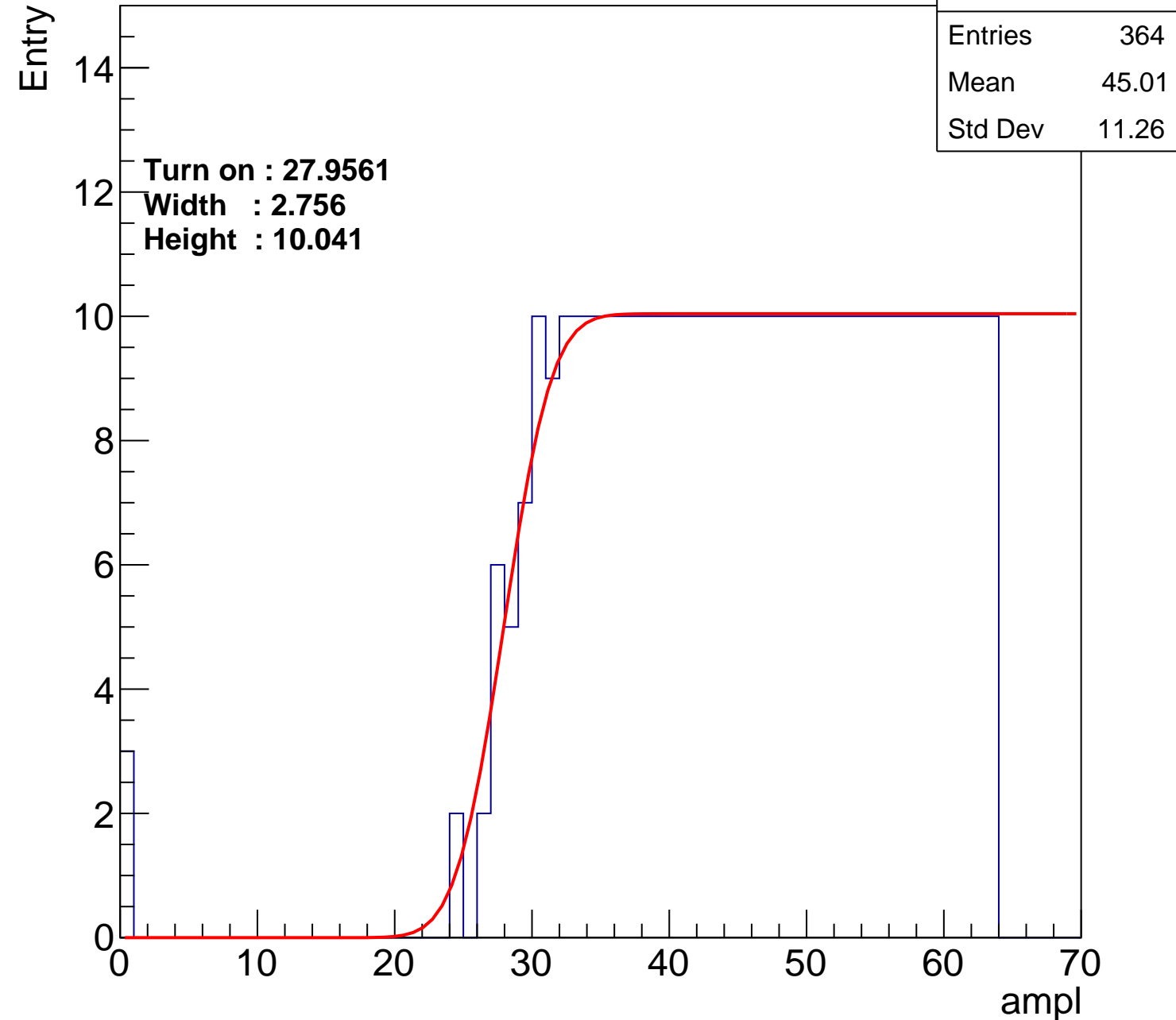
Width : 2.756

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch82

calib_packv5_042523_0143.root, FC#13, port D2

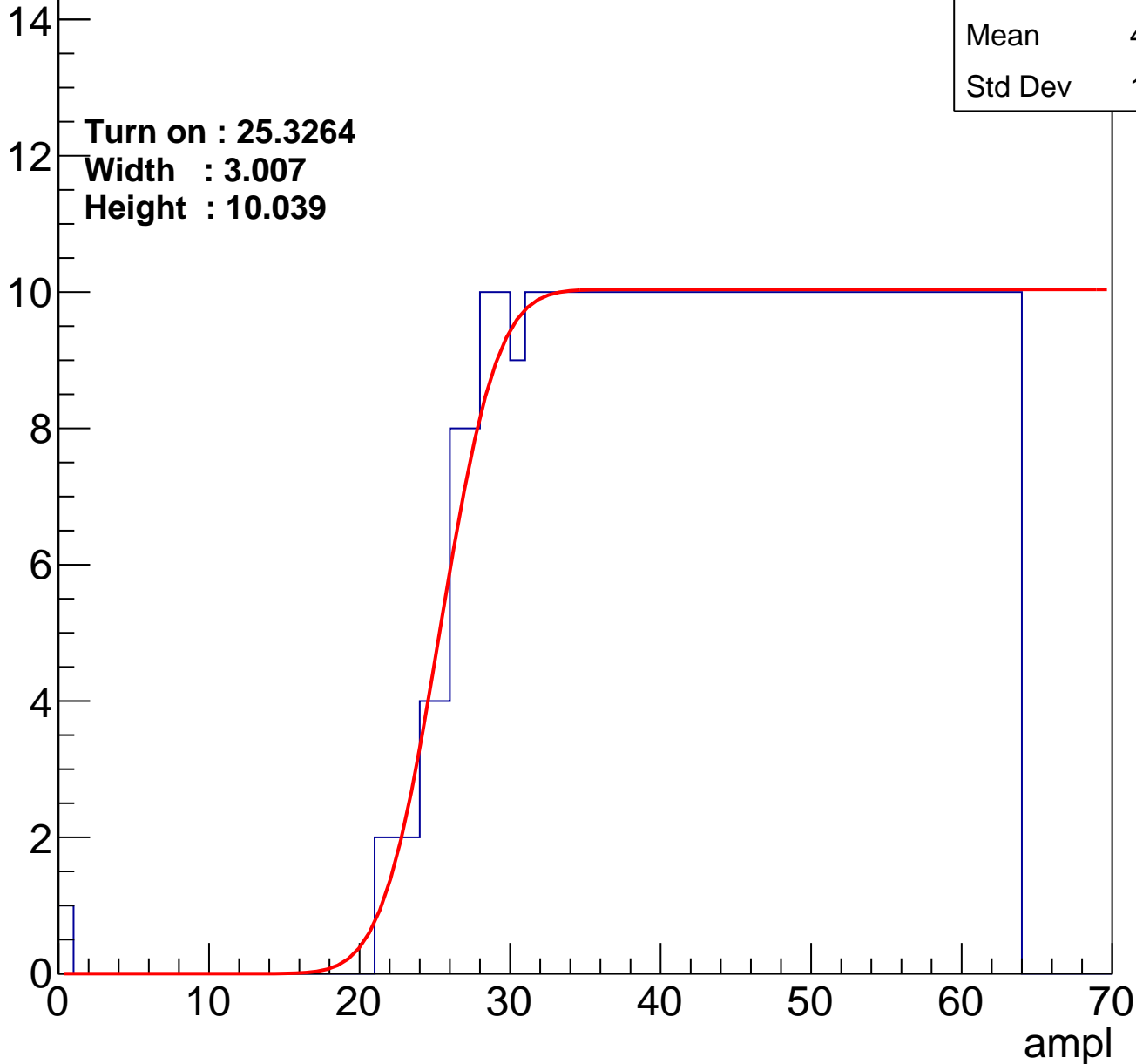
Entries	390
Mean	43.85
Std Dev	11.57

Turn on : 25.3264

Width : 3.007

Height : 10.039

Entry



B1L003S, U20-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.9
Std Dev	11.82

Turn on : 25.8213

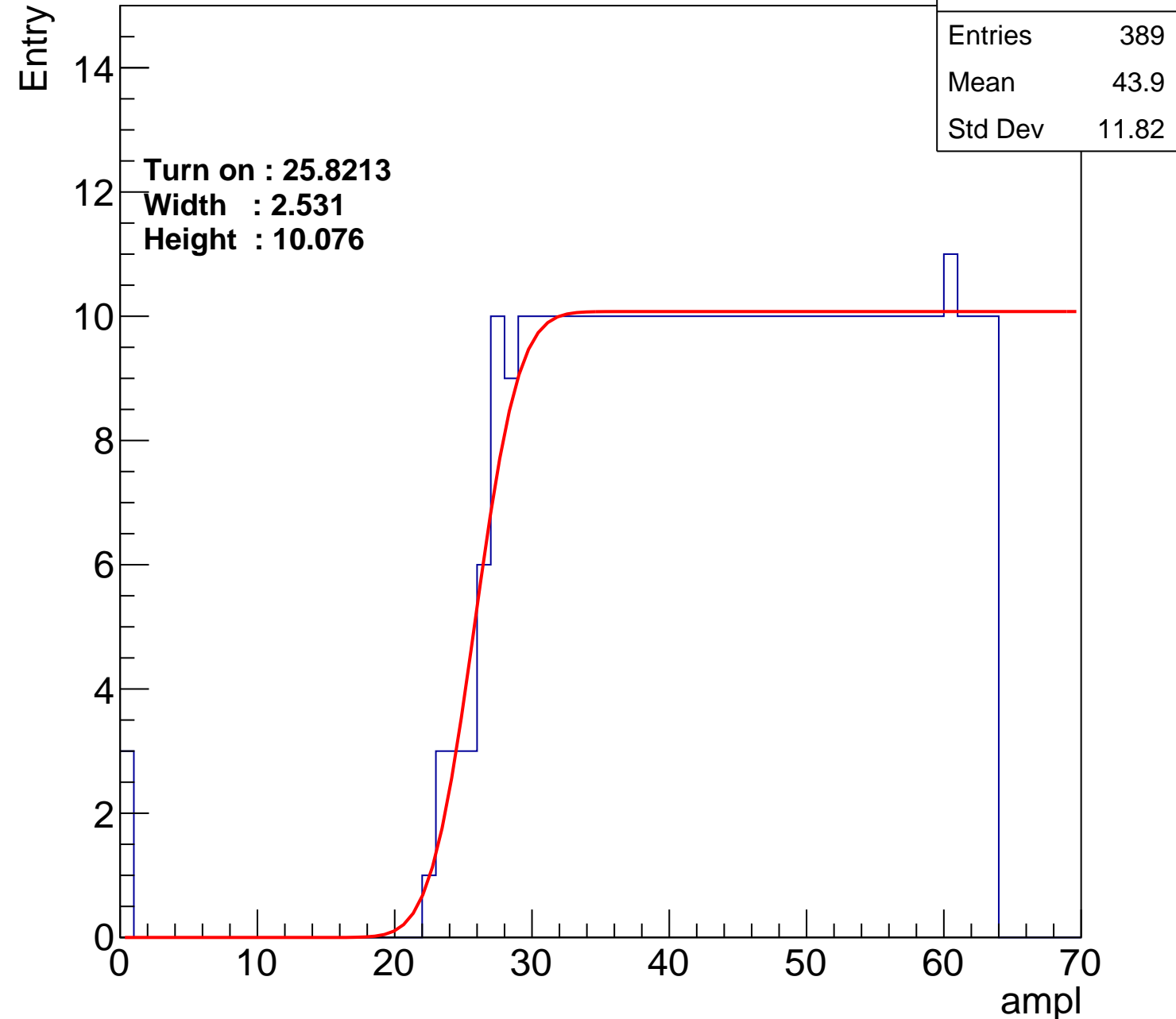
Width : 2.531

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch84

calib_packv5_042523_0143.root, FC#13, port D2

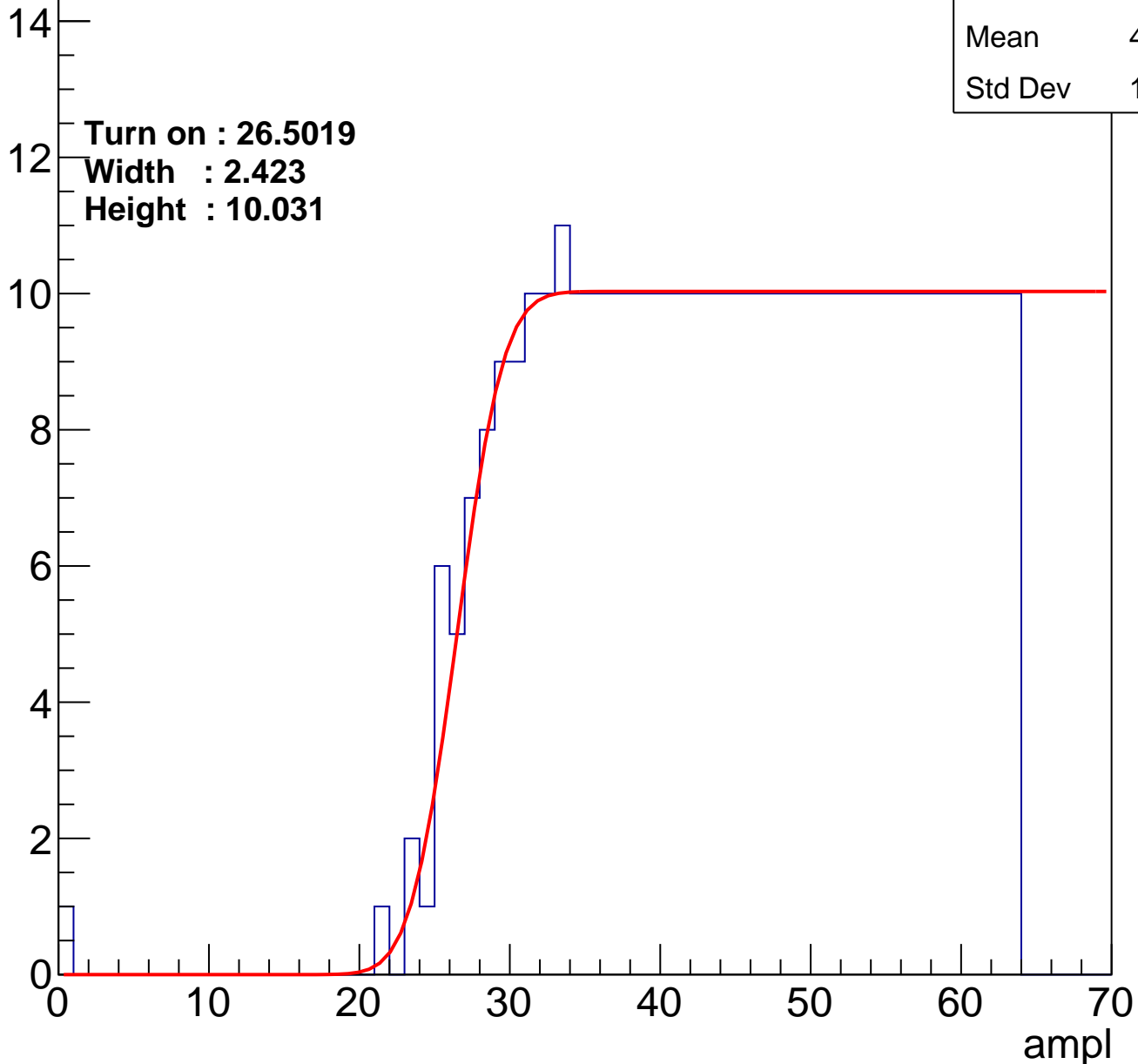
Entries	380
Mean	44.36
Std Dev	11.28

Turn on : 26.5019

Width : 2.423

Height : 10.031

Entry



B1L003S, U20-ch85

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.8762

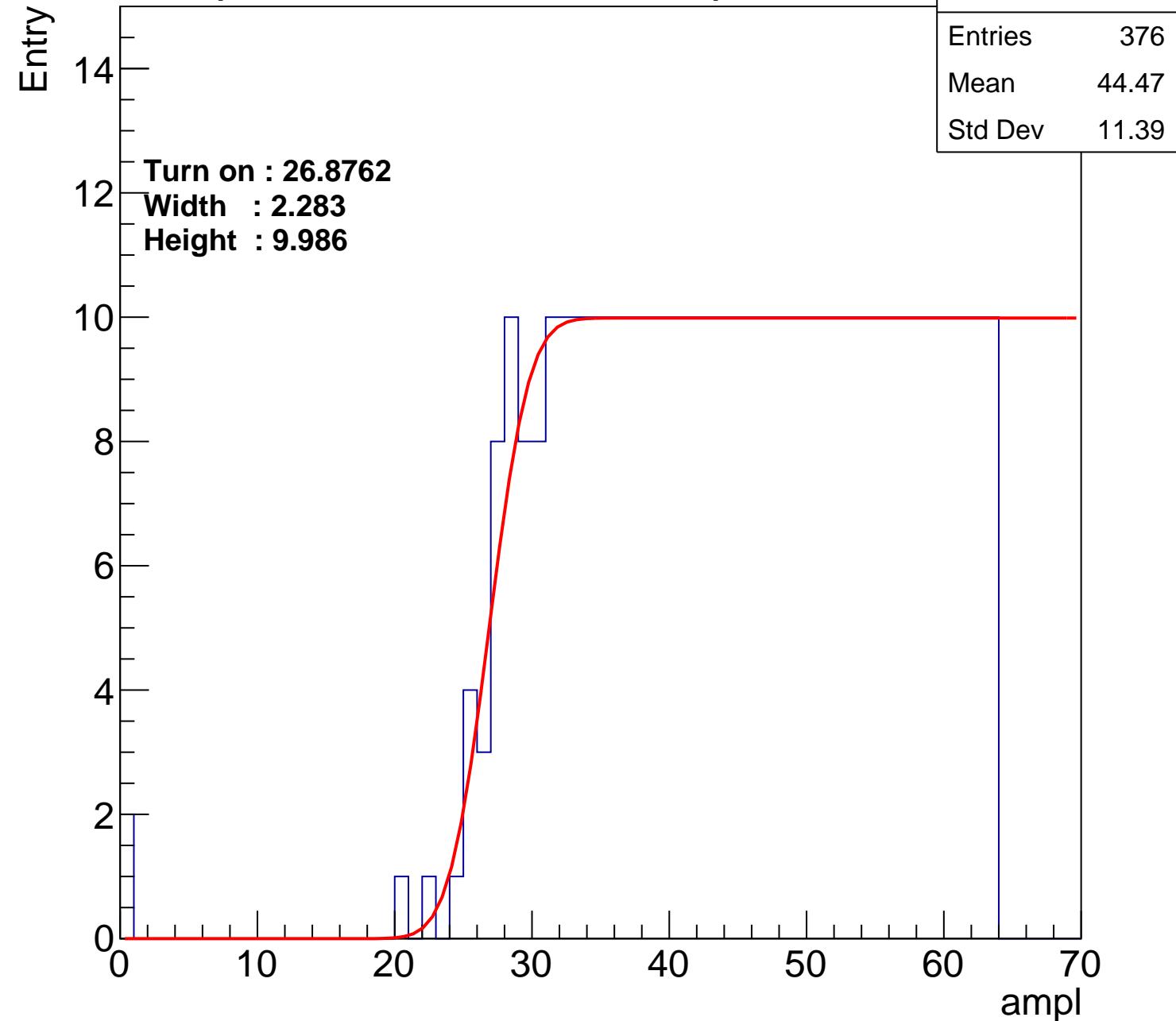
Width : 2.283

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch86

calib_packv5_042523_0143.root, FC#13, port D2

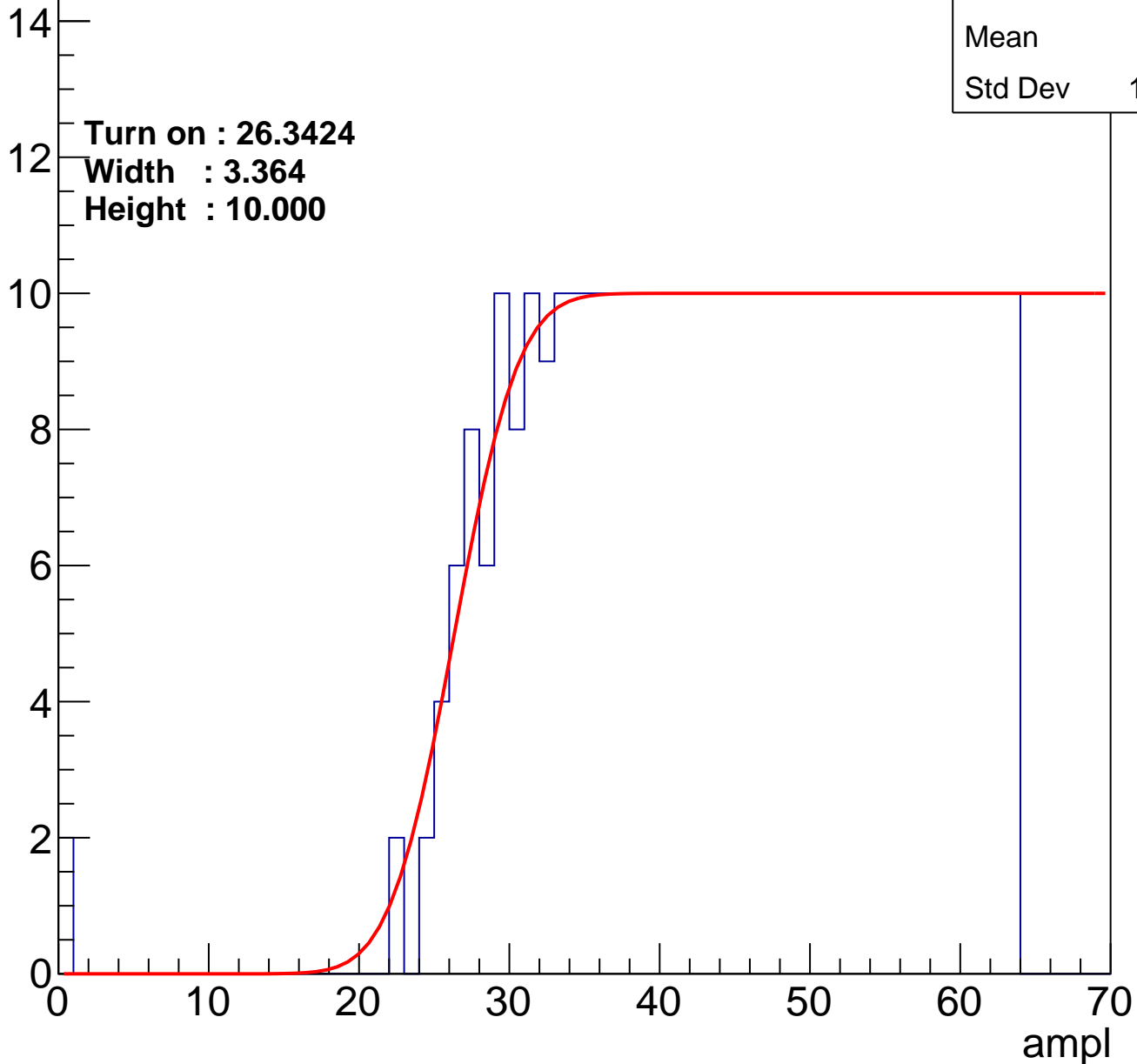
Entries	377
Mean	44.4
Std Dev	11.44

Turn on : 26.3424

Width : 3.364

Height : 10.000

Entry



B1L003S, U20-ch87

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 25.9410

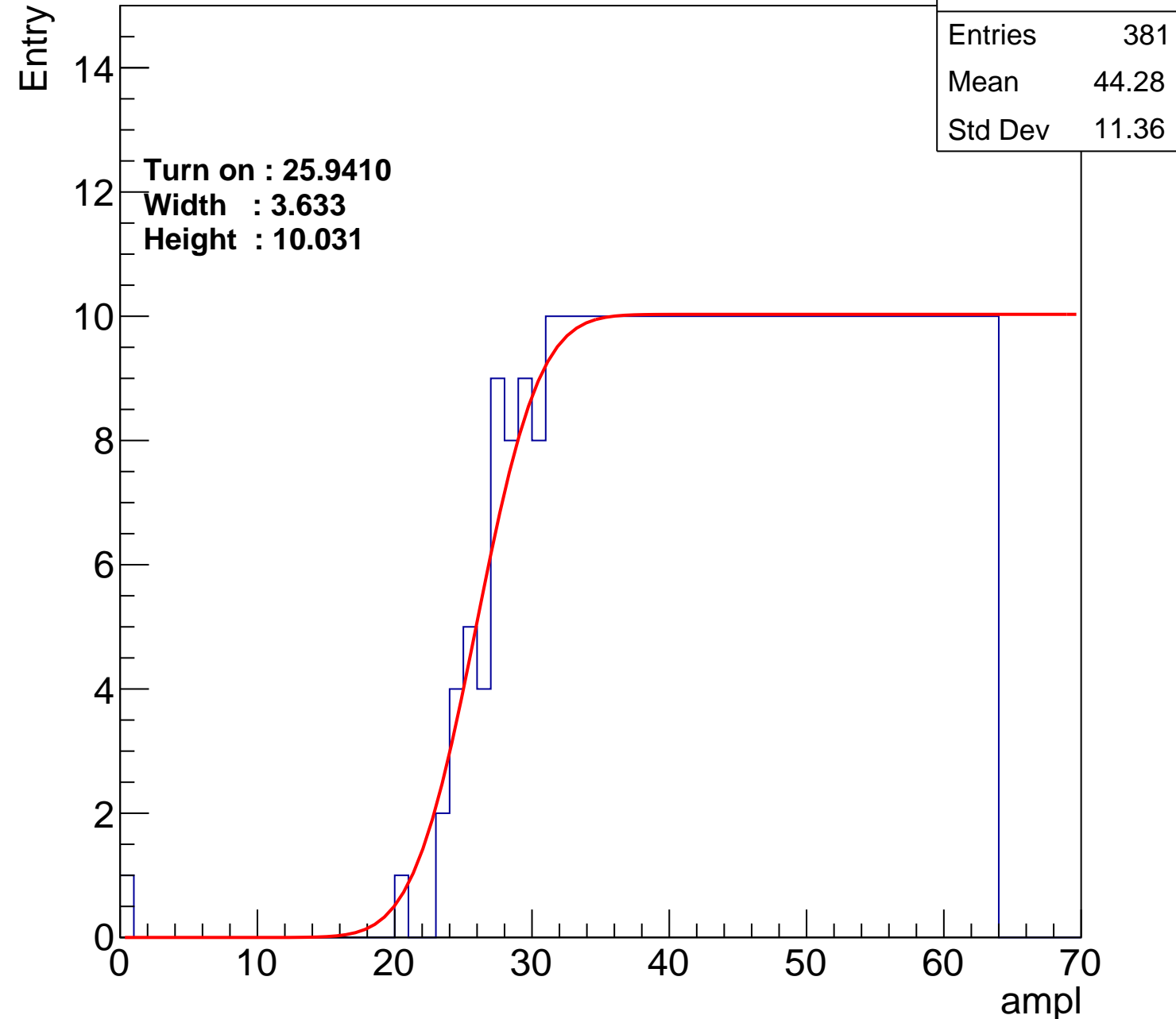
Width : 3.633

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.1587
Width : 3.123
Height : 10.045



B1L003S, U20-ch89

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.19
Std Dev	11.33

Turn on : 28.2977

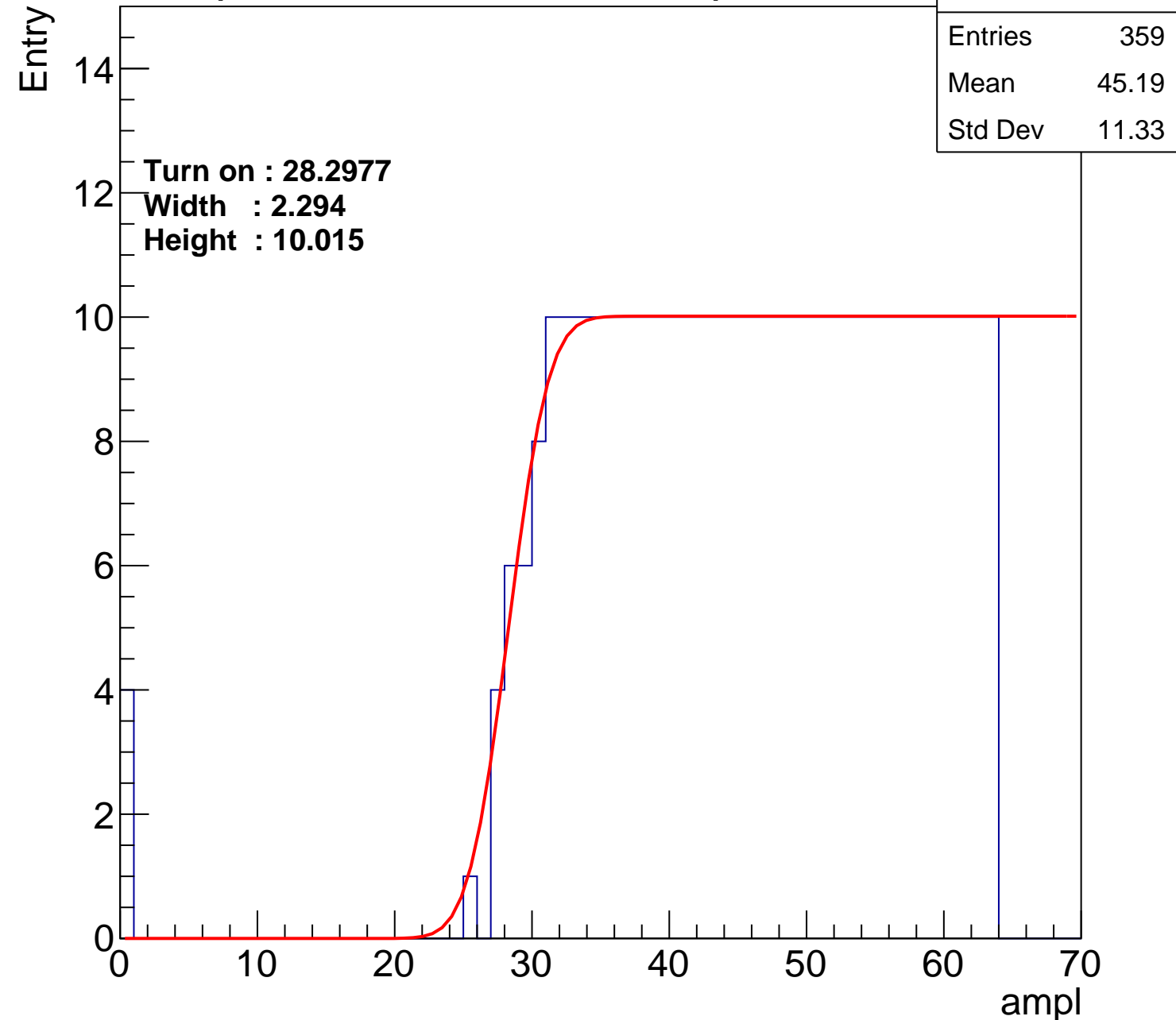
Width : 2.294

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.85
Std Dev	11.41

Turn on : 27.8736

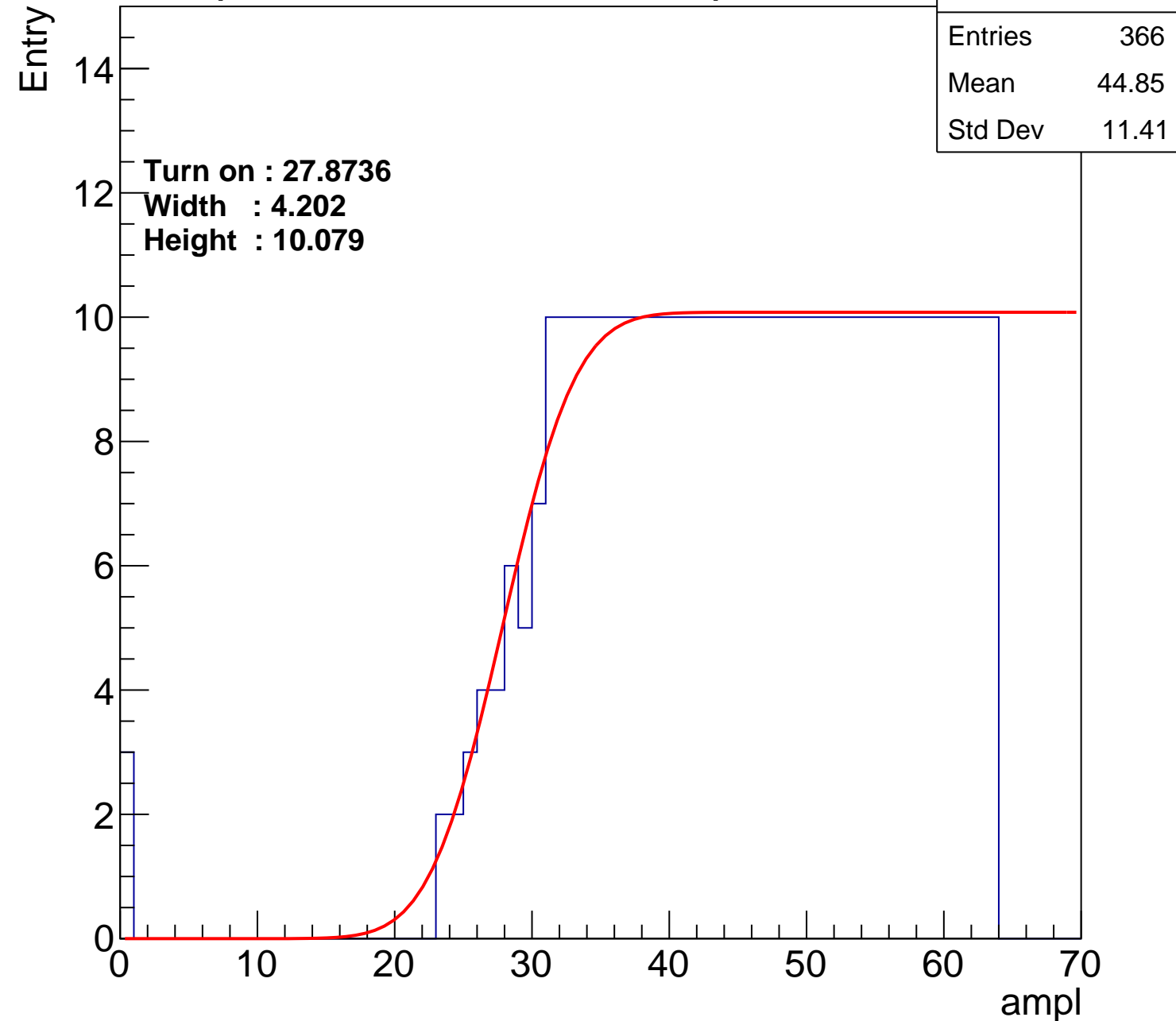
Width : 4.202

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch91

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.58
Std Dev	11.47

Turn on : 27.1480

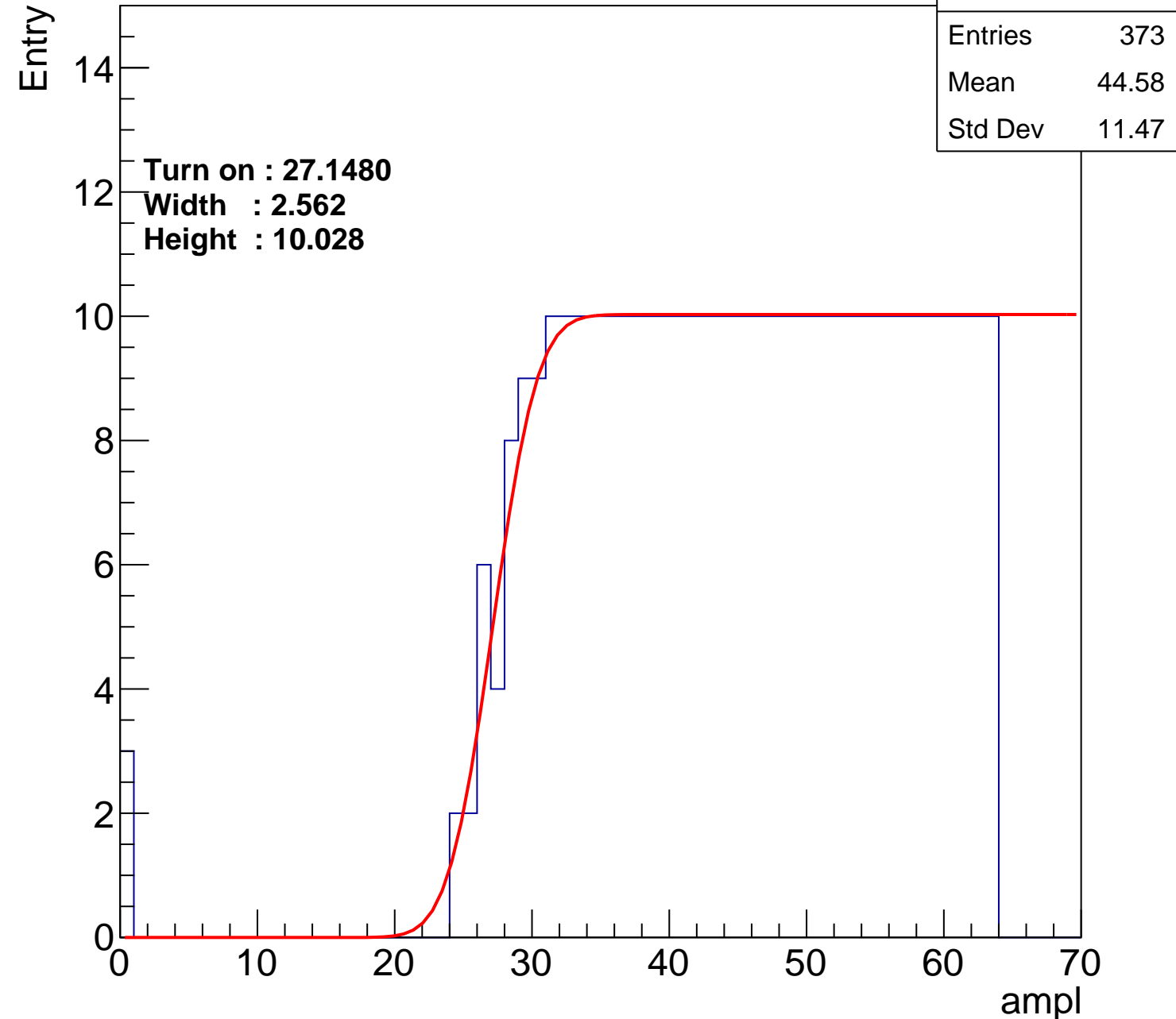
Width : 2.562

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch92

calib_packv5_042523_0143.root, FC#13, port D2

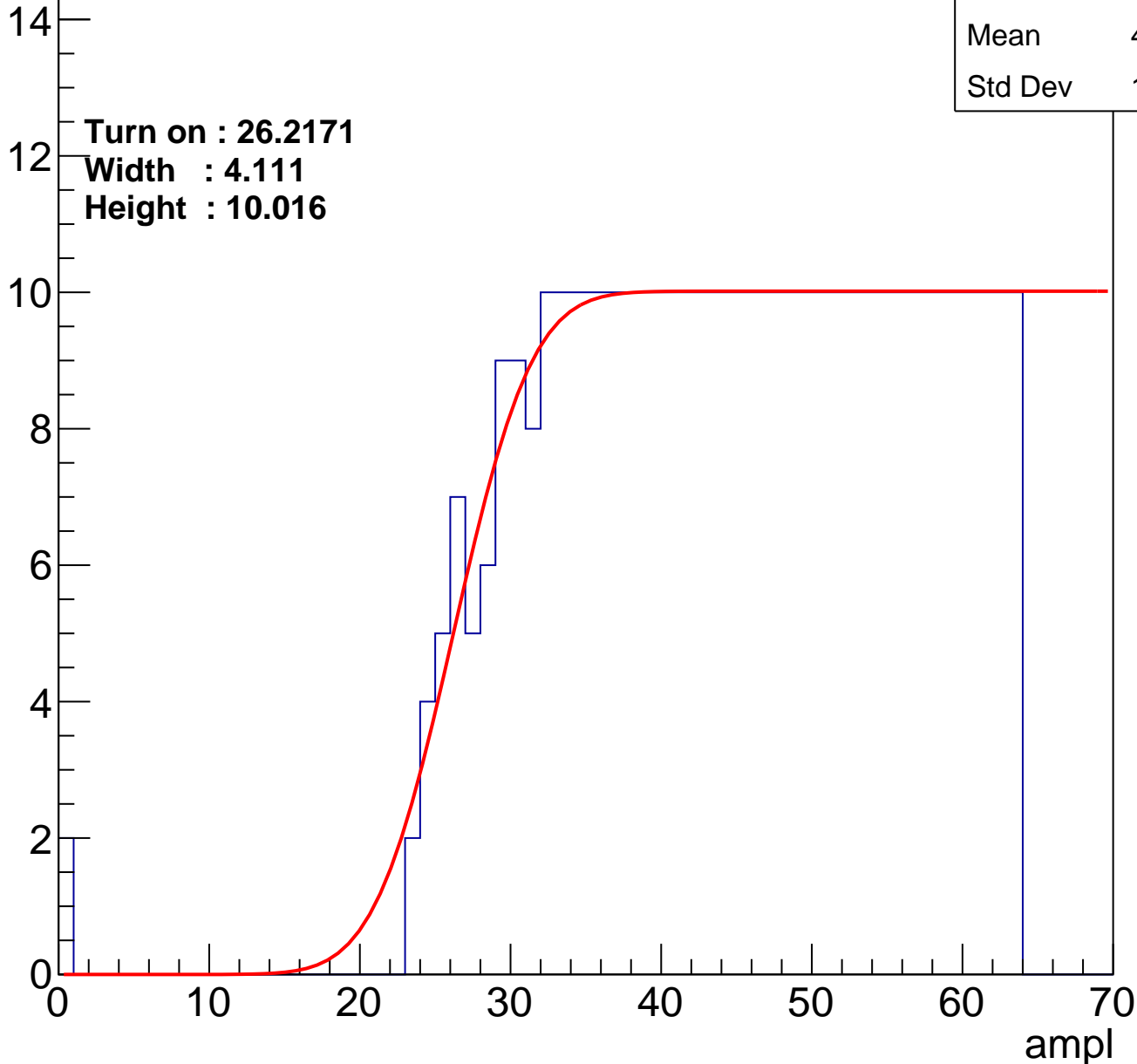
Entries	377
Mean	44.38
Std Dev	11.48

Turn on : 26.2171

Width : 4.111

Height : 10.016

Entry



B1L003S, U20-ch93

calib_packv5_042523_0143.root, FC#13, port D2

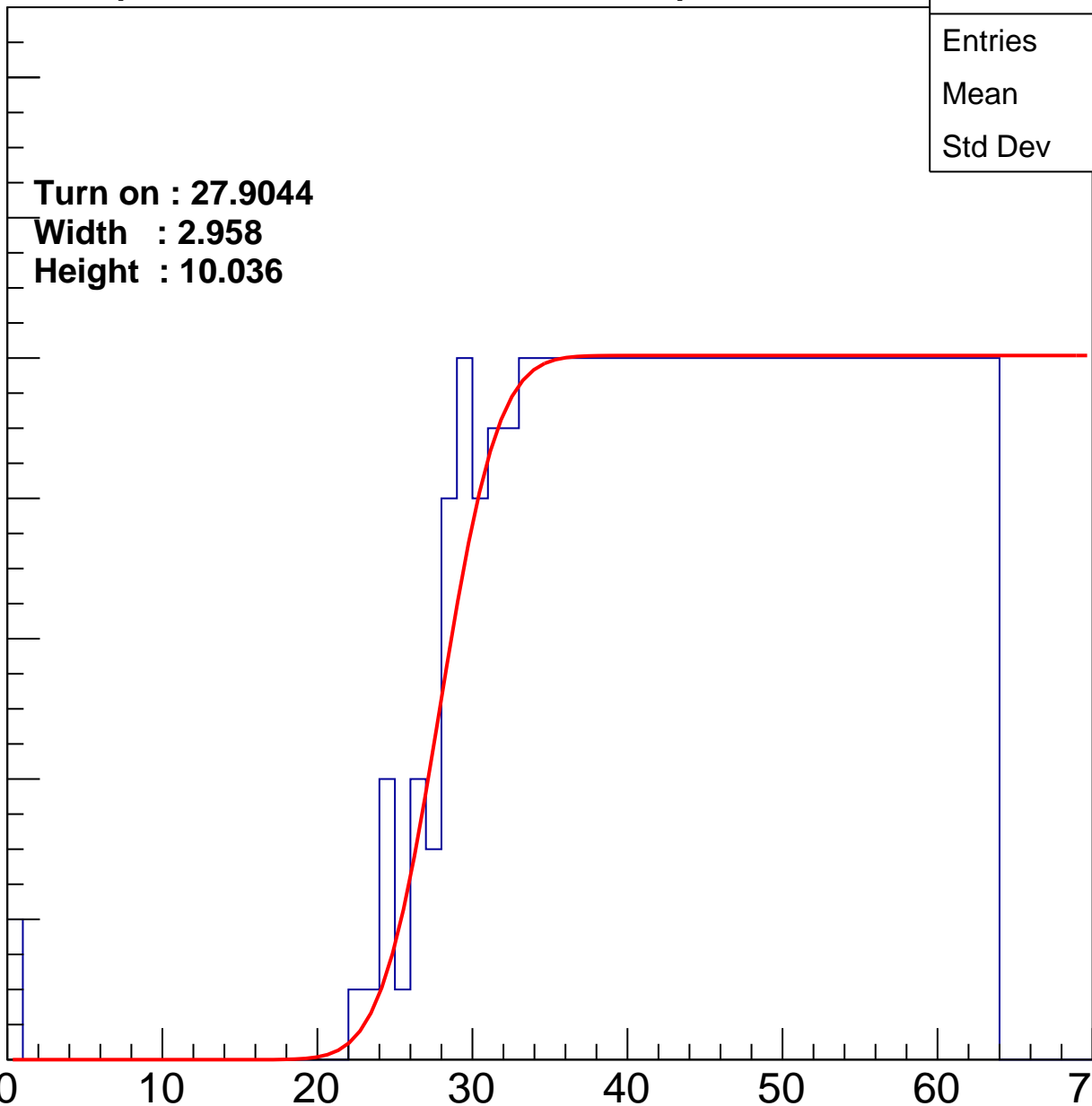
Entry

14
12
10
8
6
4
2
0

Turn on : 27.9044
Width : 2.958
Height : 10.036

Entries	370
Mean	44.74
Std Dev	11.29

ampl



B1L003S, U20-ch94

calib_packv5_042523_0143.root, FC#13, port D2

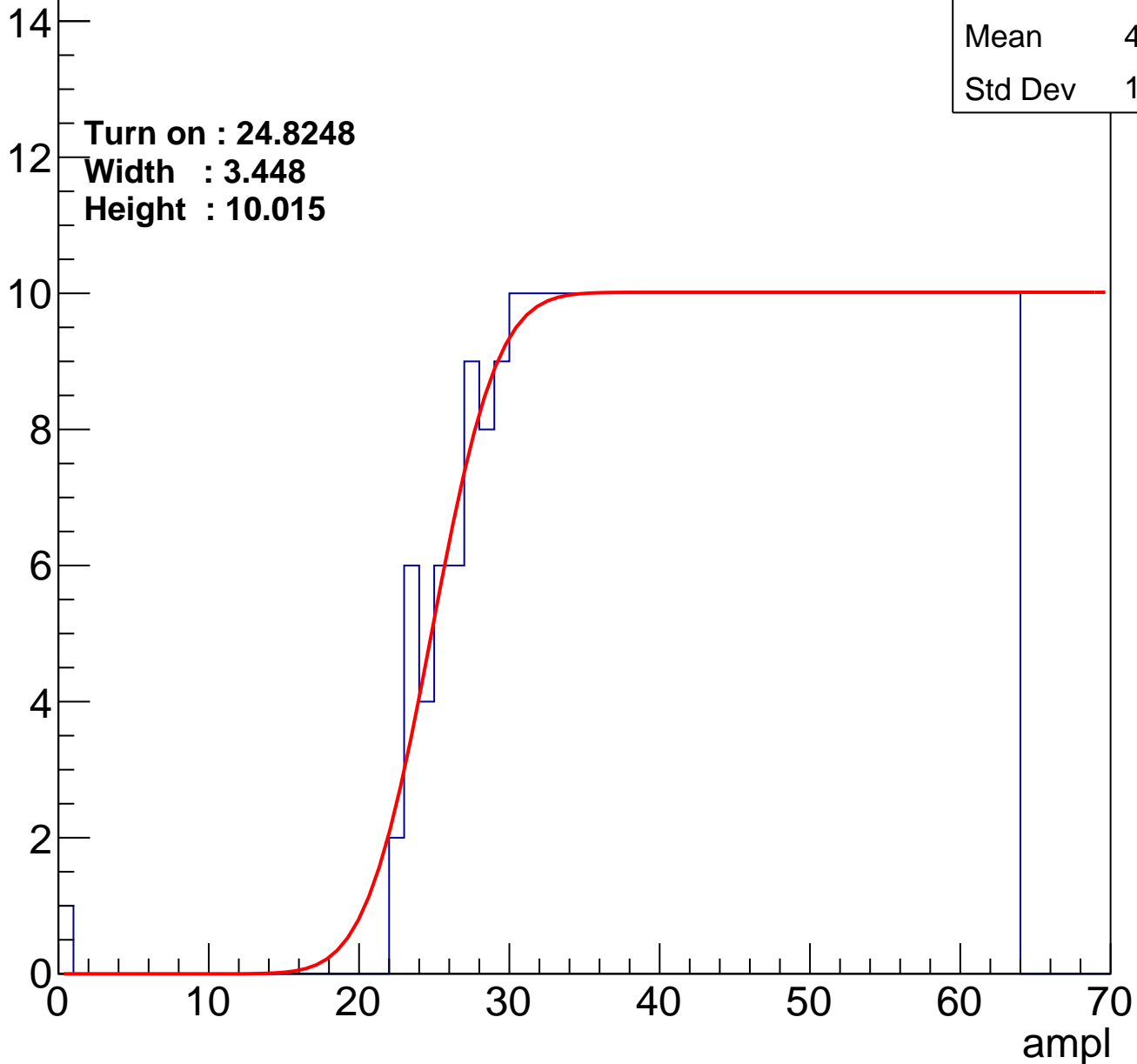
Entries	391
Mean	43.79
Std Dev	11.62

Turn on : 24.8248

Width : 3.448

Height : 10.015

Entry



B1L003S, U20-ch95

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.22
Std Dev	11.69

Turn on : 26.3172

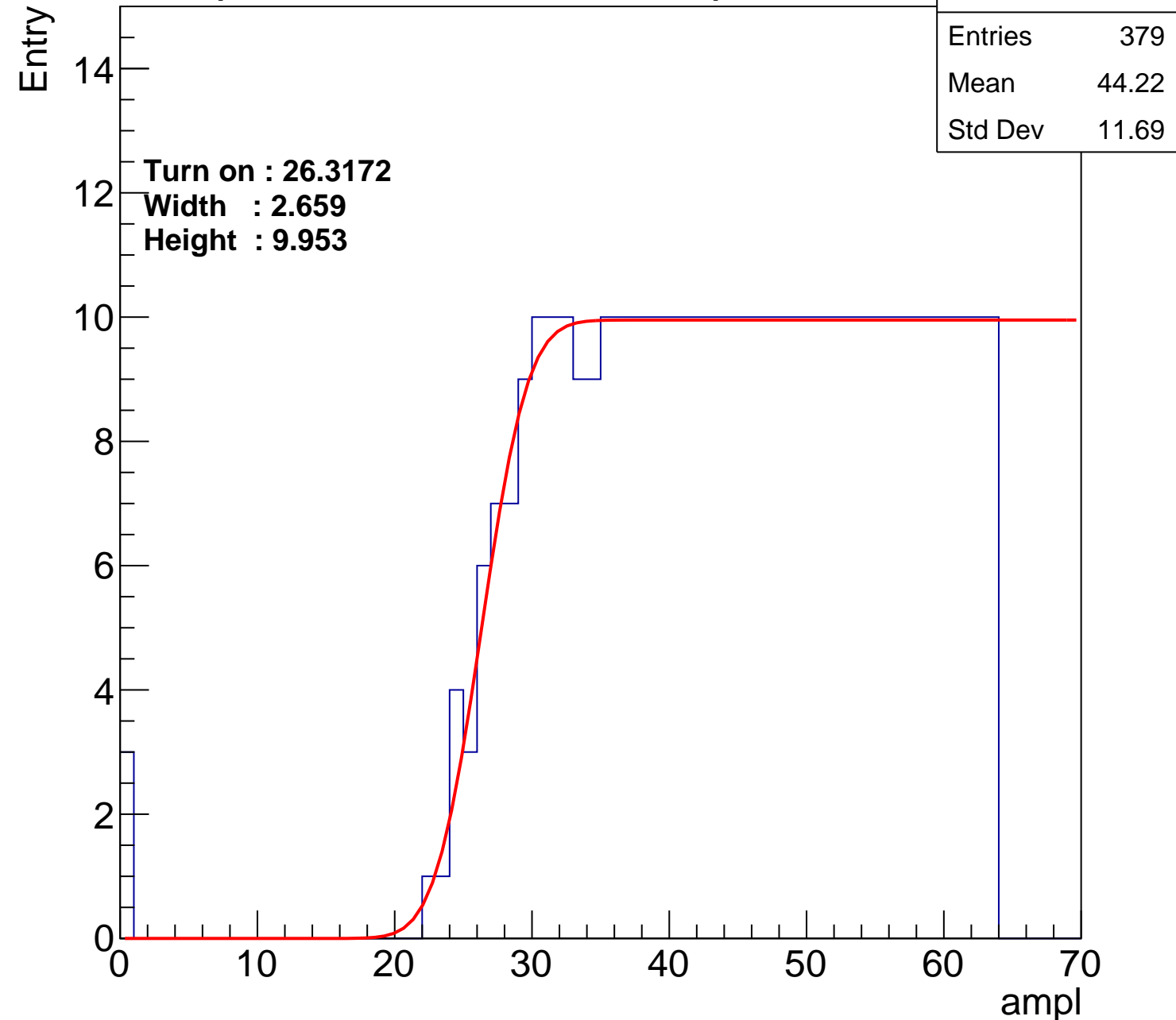
Width : 2.659

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.8
Std Dev	11.1

Turn on : 27.2632

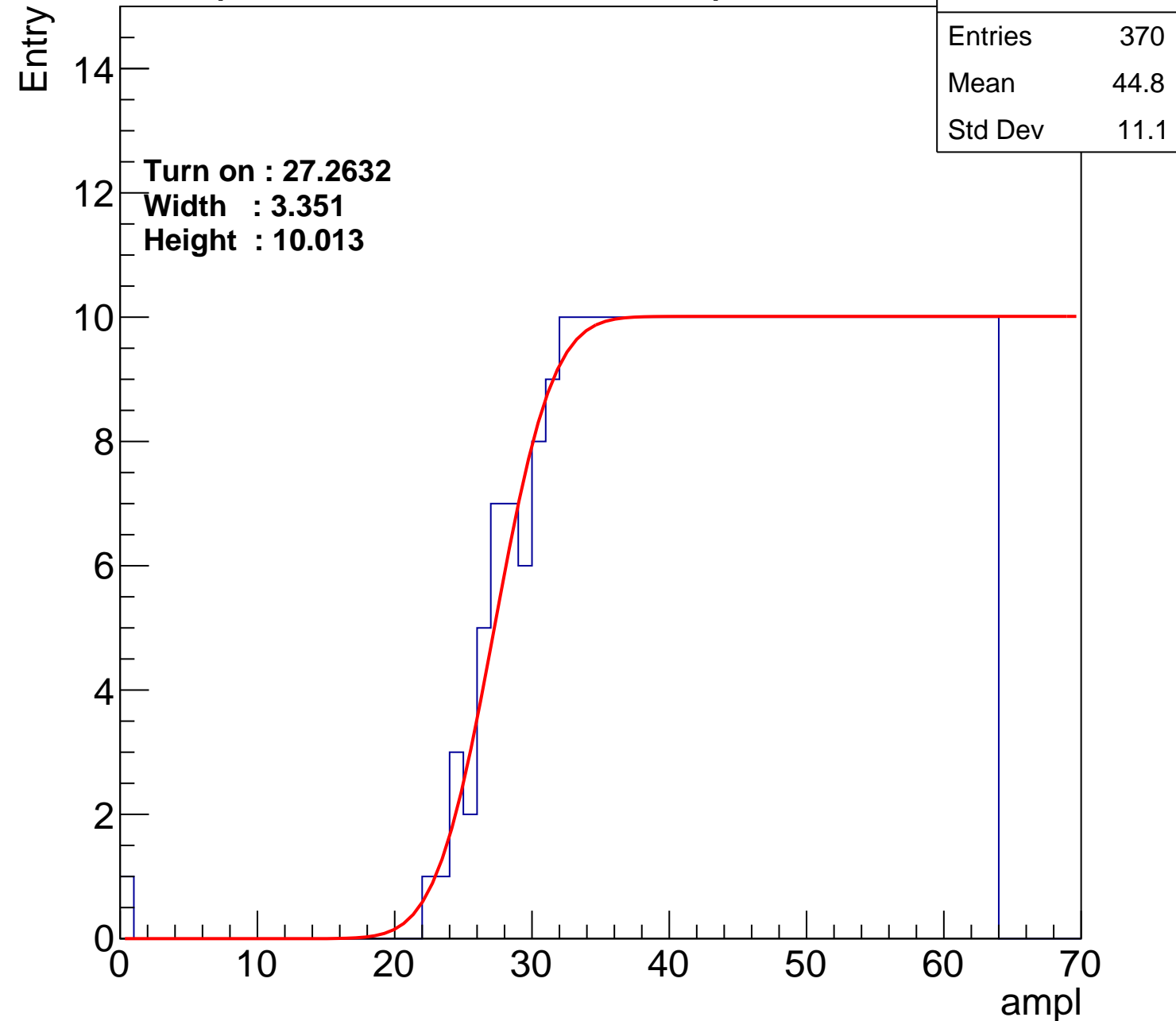
Width : 3.351

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch97

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.53
Std Dev	10.67

Turn on : 28.8576

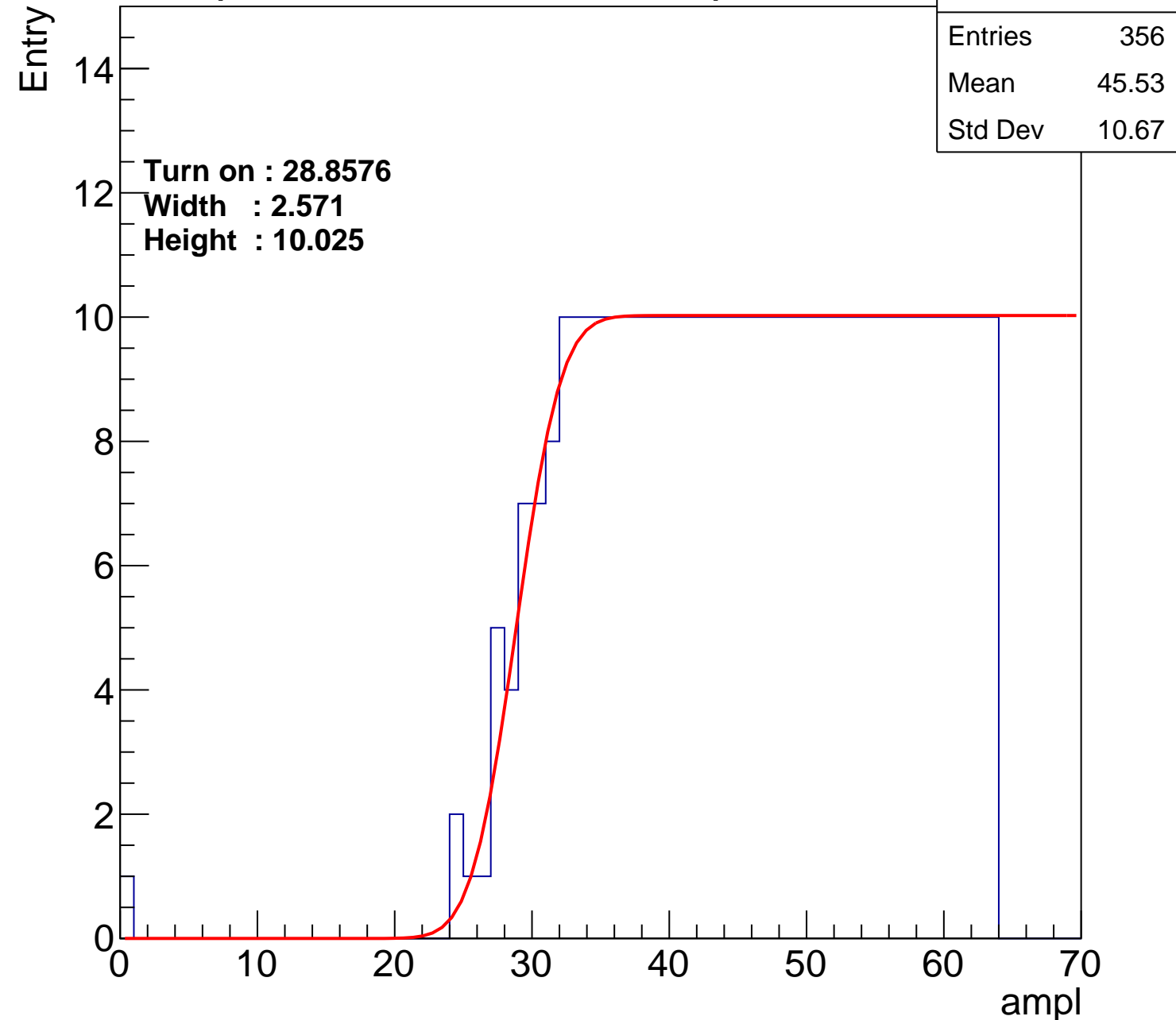
Width : 2.571

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.22
Std Dev	11.41

Turn on : 26.2783

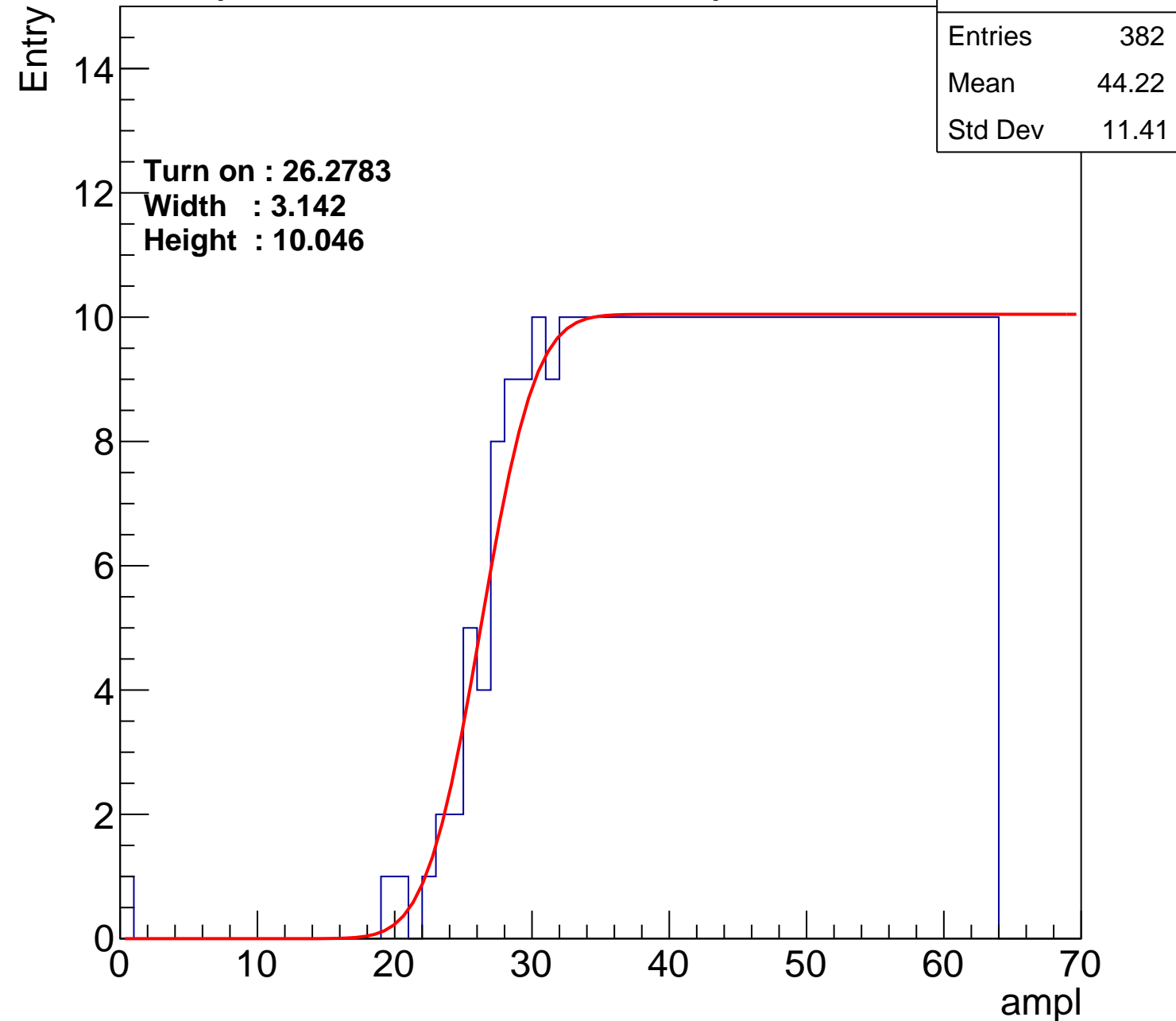
Width : 3.142

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch99

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.73
Std Dev	11.57

Turn on : 27.8193

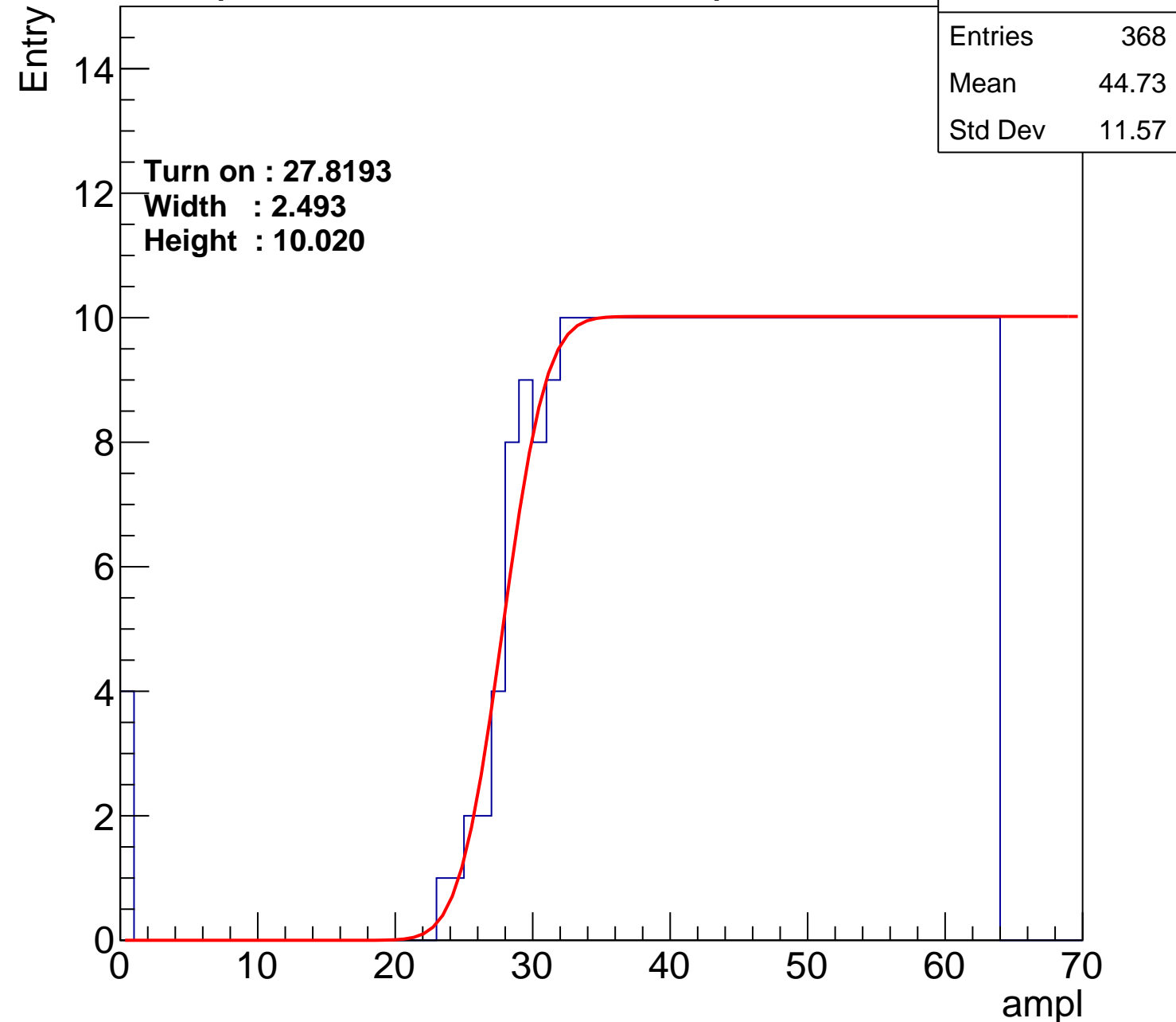
Width : 2.493

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch100

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.58
Std Dev	11.52

Turn on : 27.3784

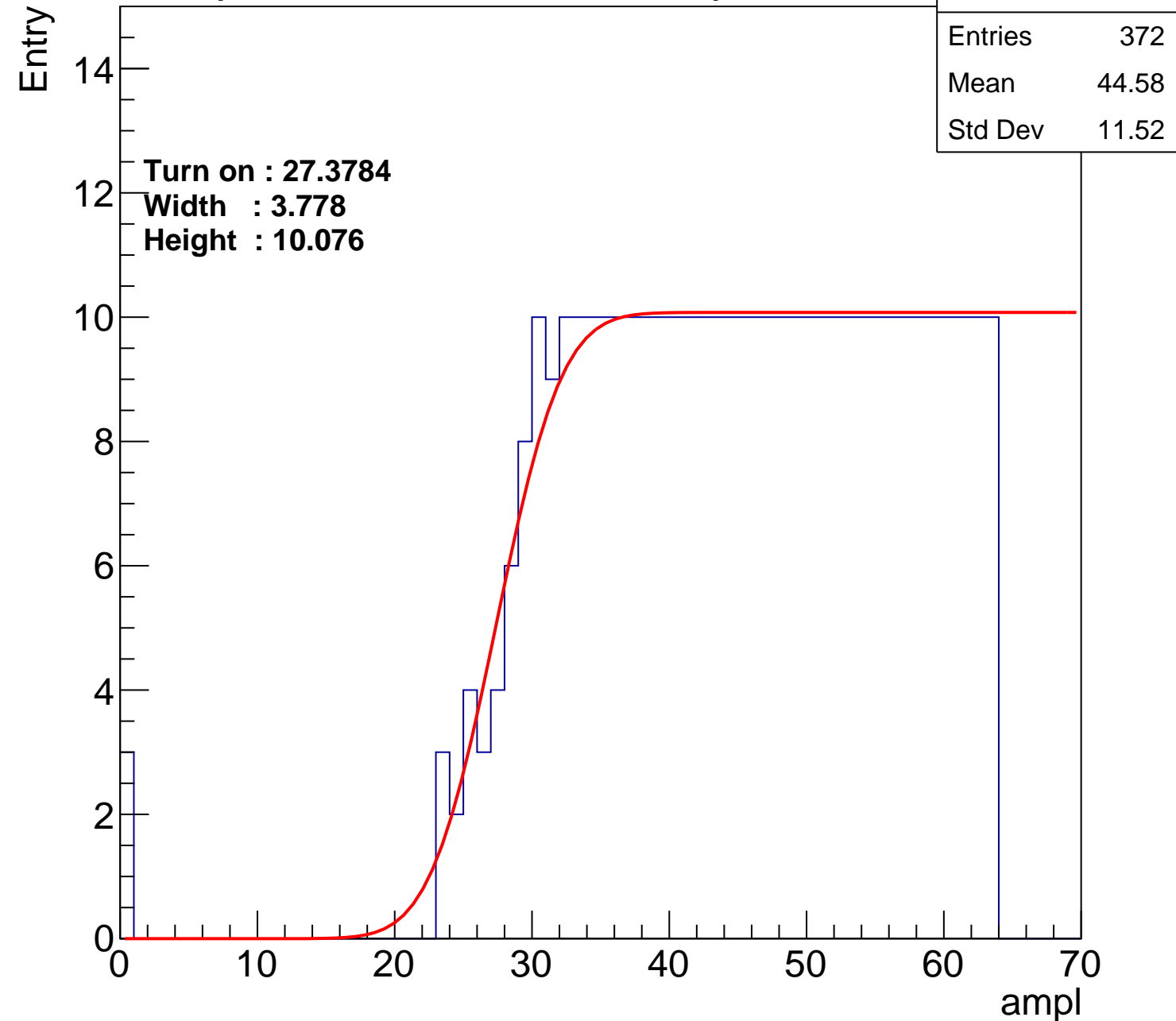
Width : 3.778

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch101

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 25.4443

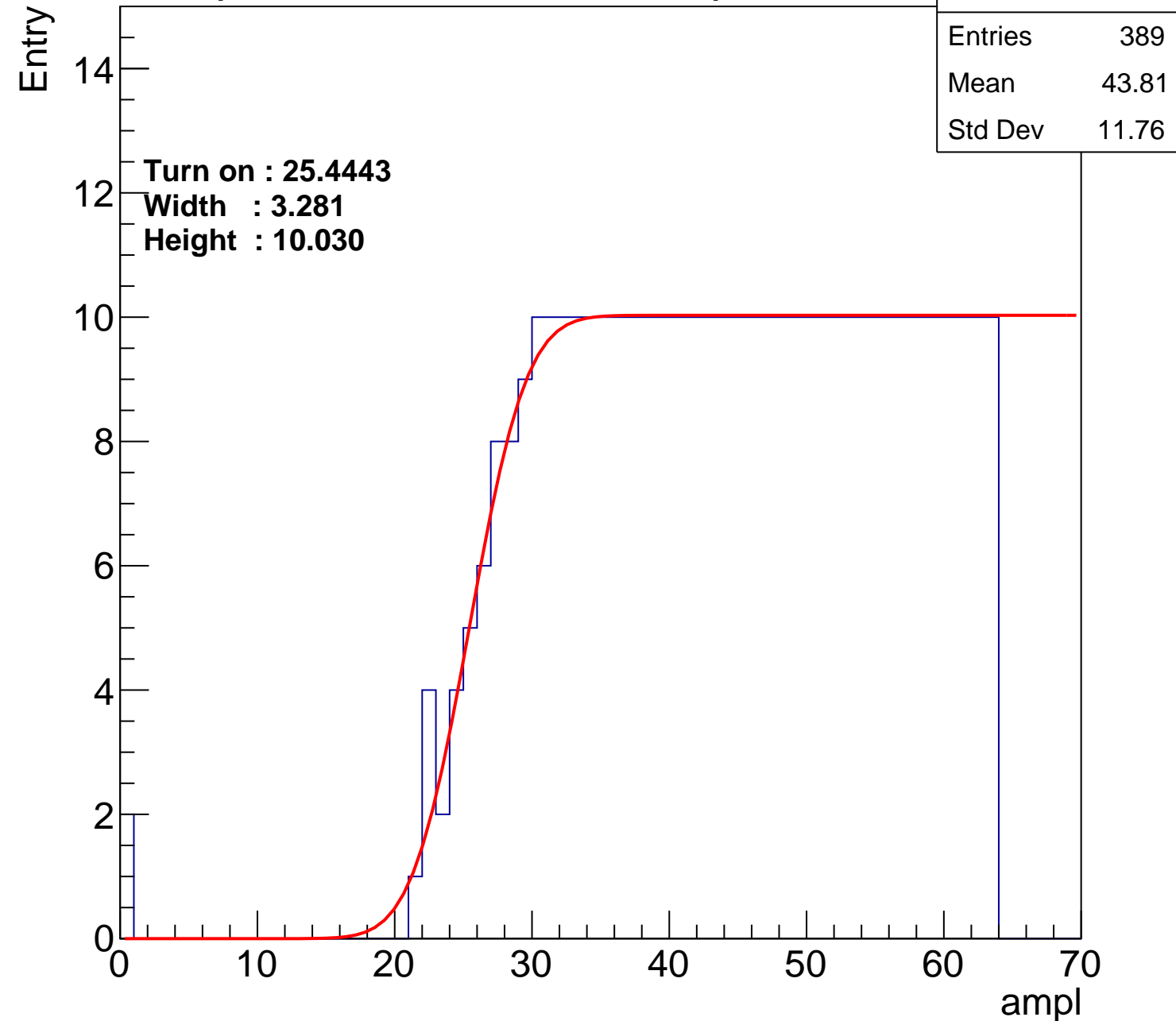
Width : 3.281

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch102

calib_packv5_042523_0143.root, FC#13, port D2

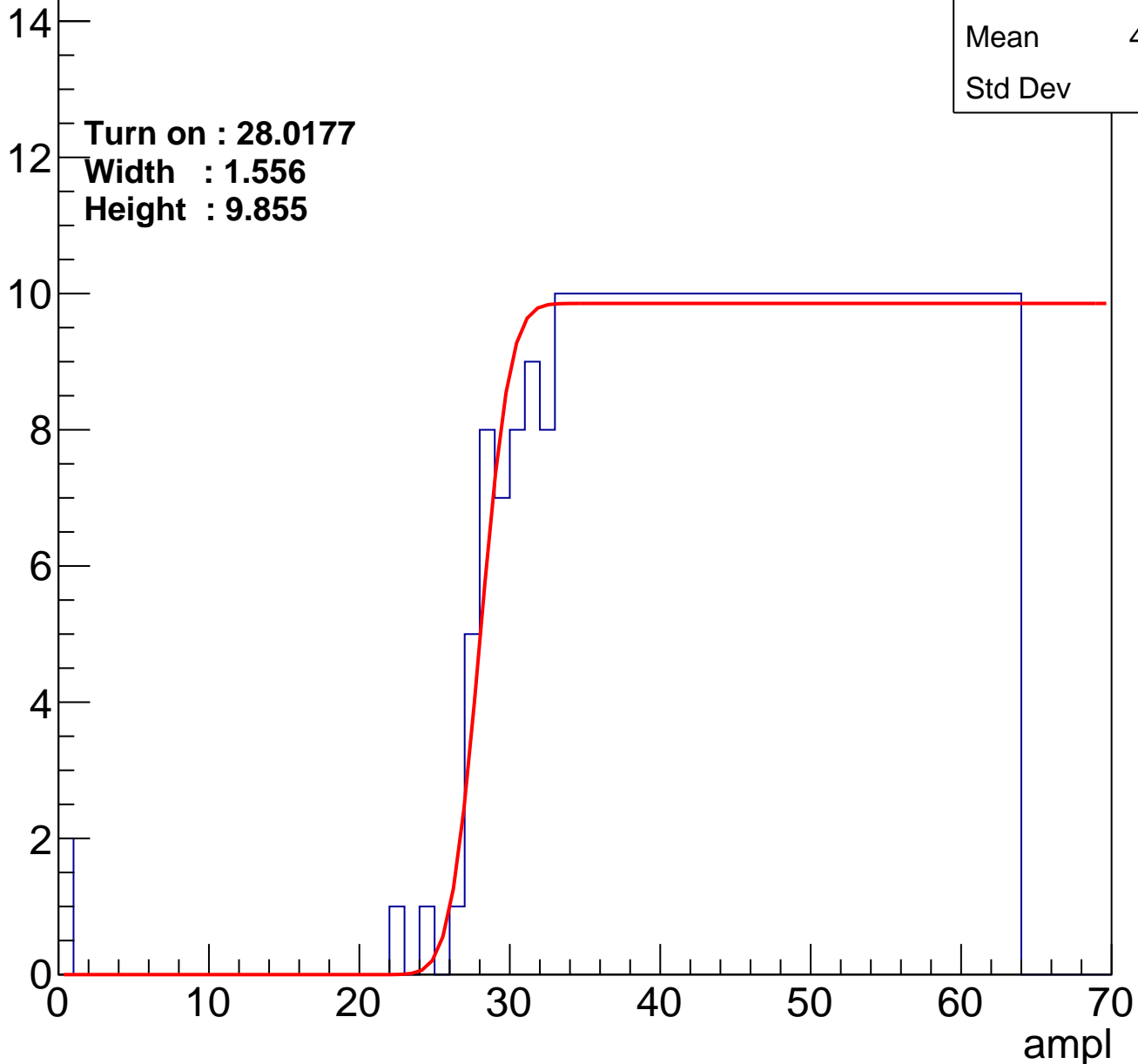
Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.0177

Width : 1.556

Height : 9.855

Entry



B1L003S, U20-ch103

calib_packv5_042523_0143.root, FC#13, port D2

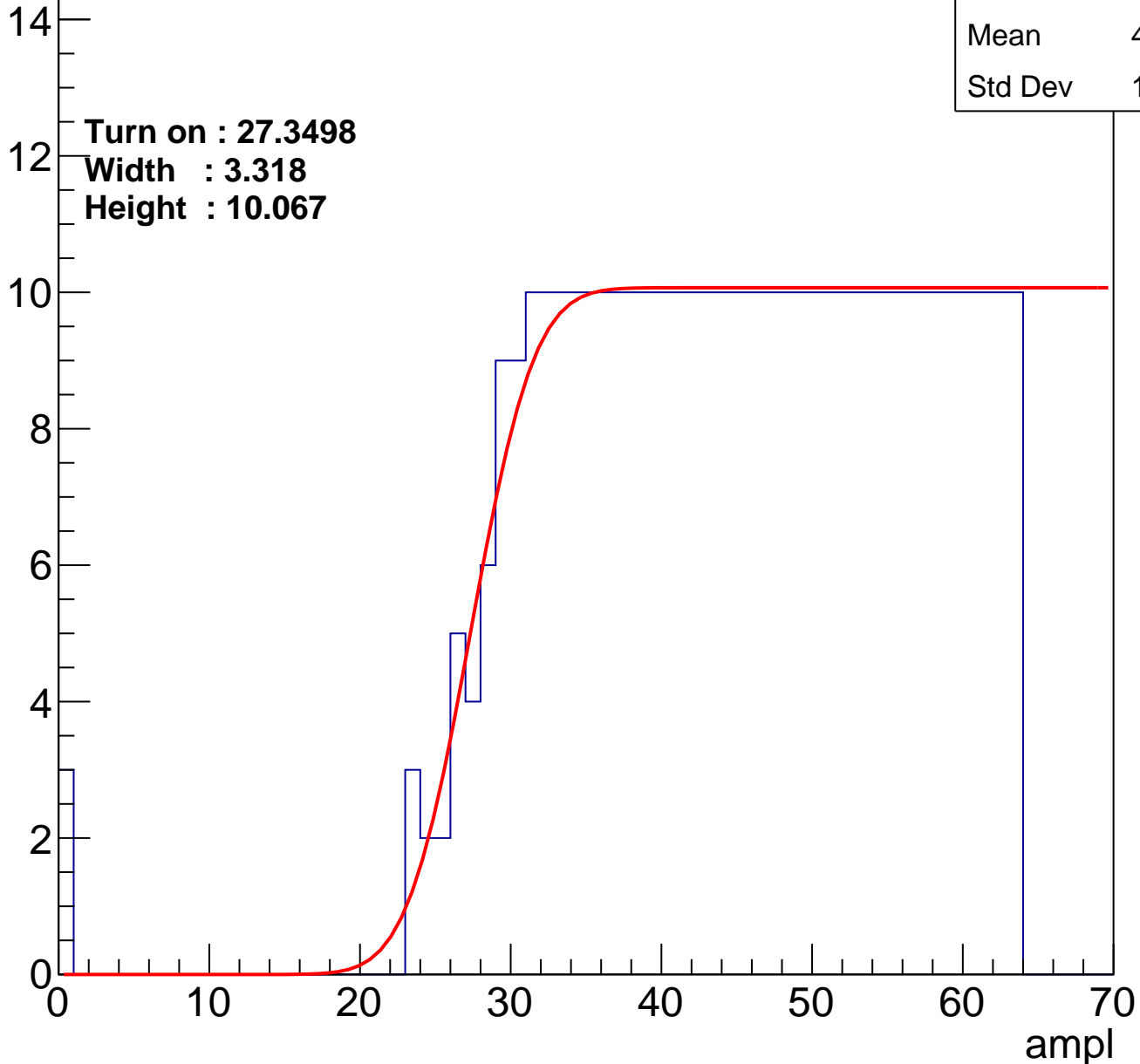
Entries	373
Mean	44.54
Std Dev	11.52

Turn on : 27.3498

Width : 3.318

Height : 10.067

Entry



B1L003S, U20-ch104

calib_packv5_042523_0143.root, FC#13, port D2

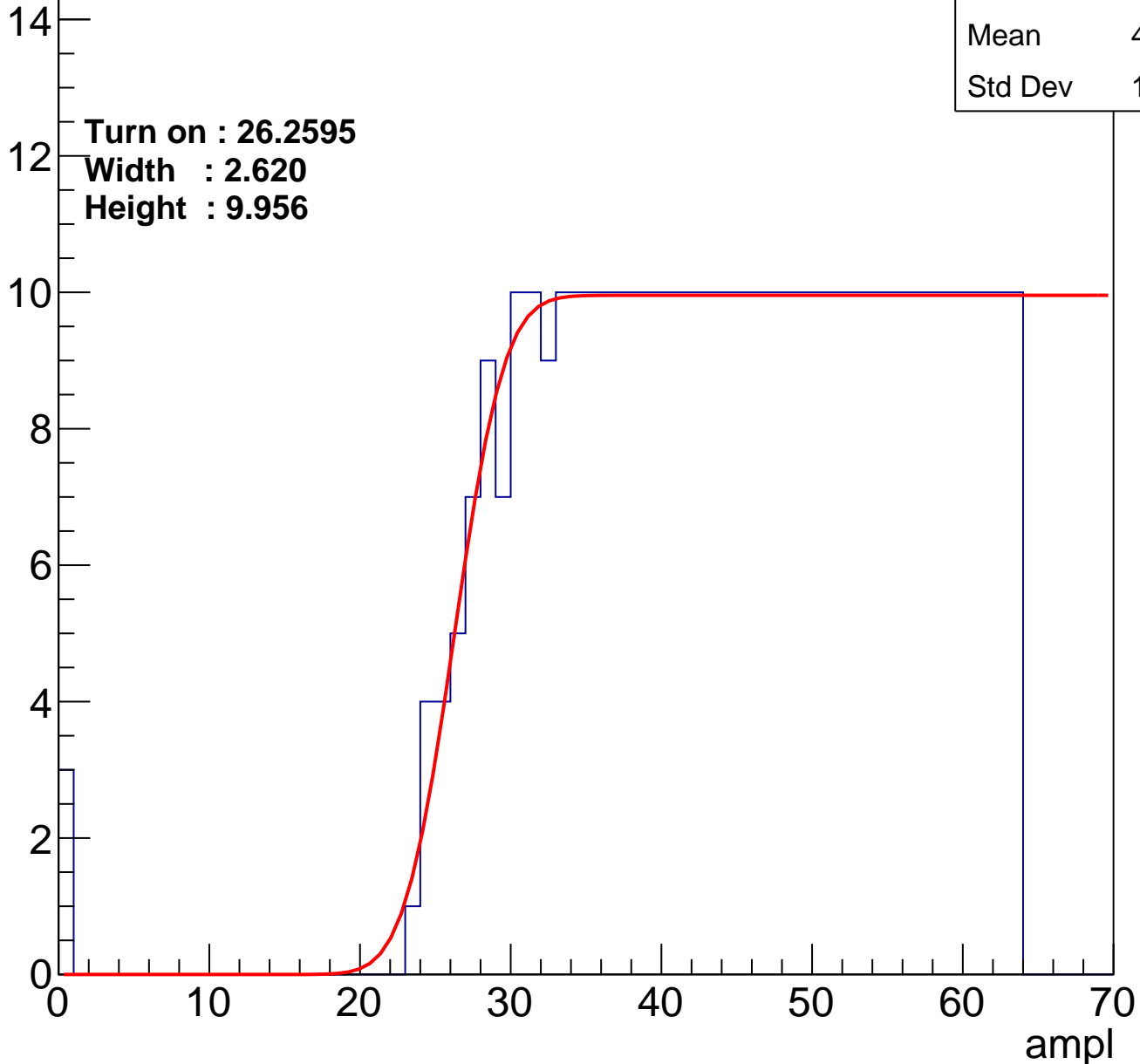
Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.2595

Width : 2.620

Height : 9.956

Entry



B1L003S, U20-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.39
Std Dev	10.93

Turn on : 28.5067

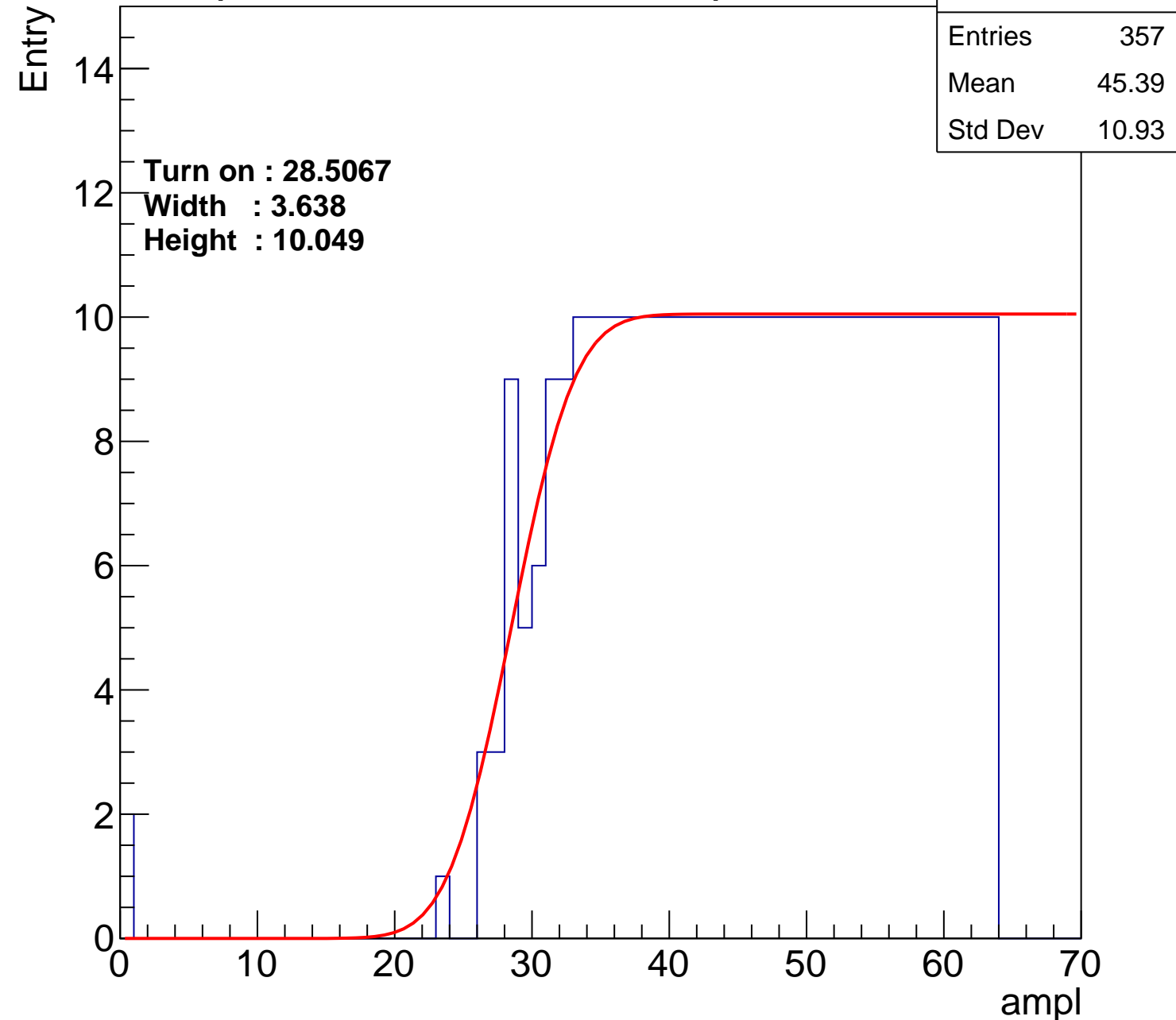
Width : 3.638

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch106

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.57
Std Dev	11.22

Turn on : 26.8161

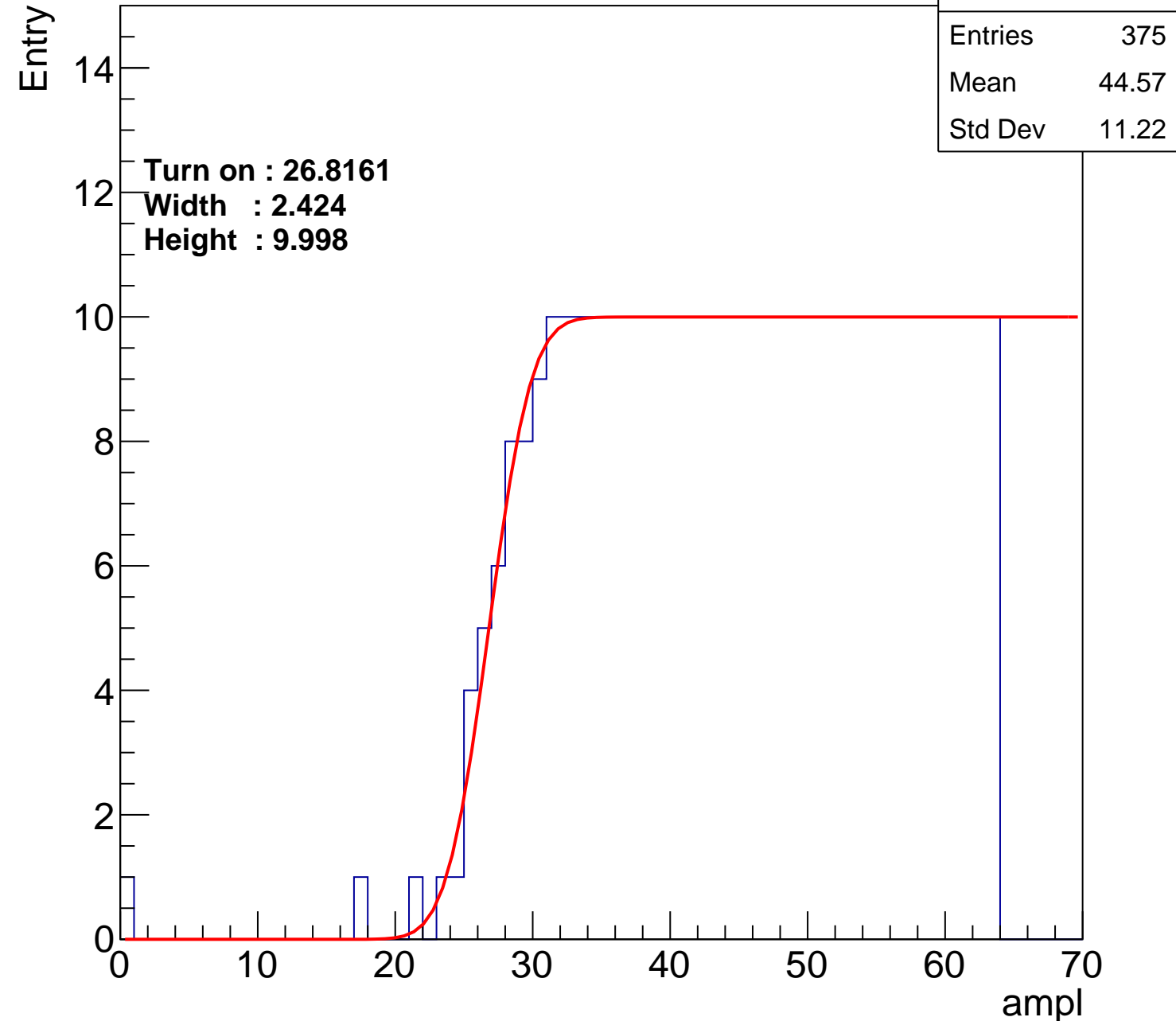
Width : 2.424

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.04
Std Dev	10.93

Turn on : 27.6644

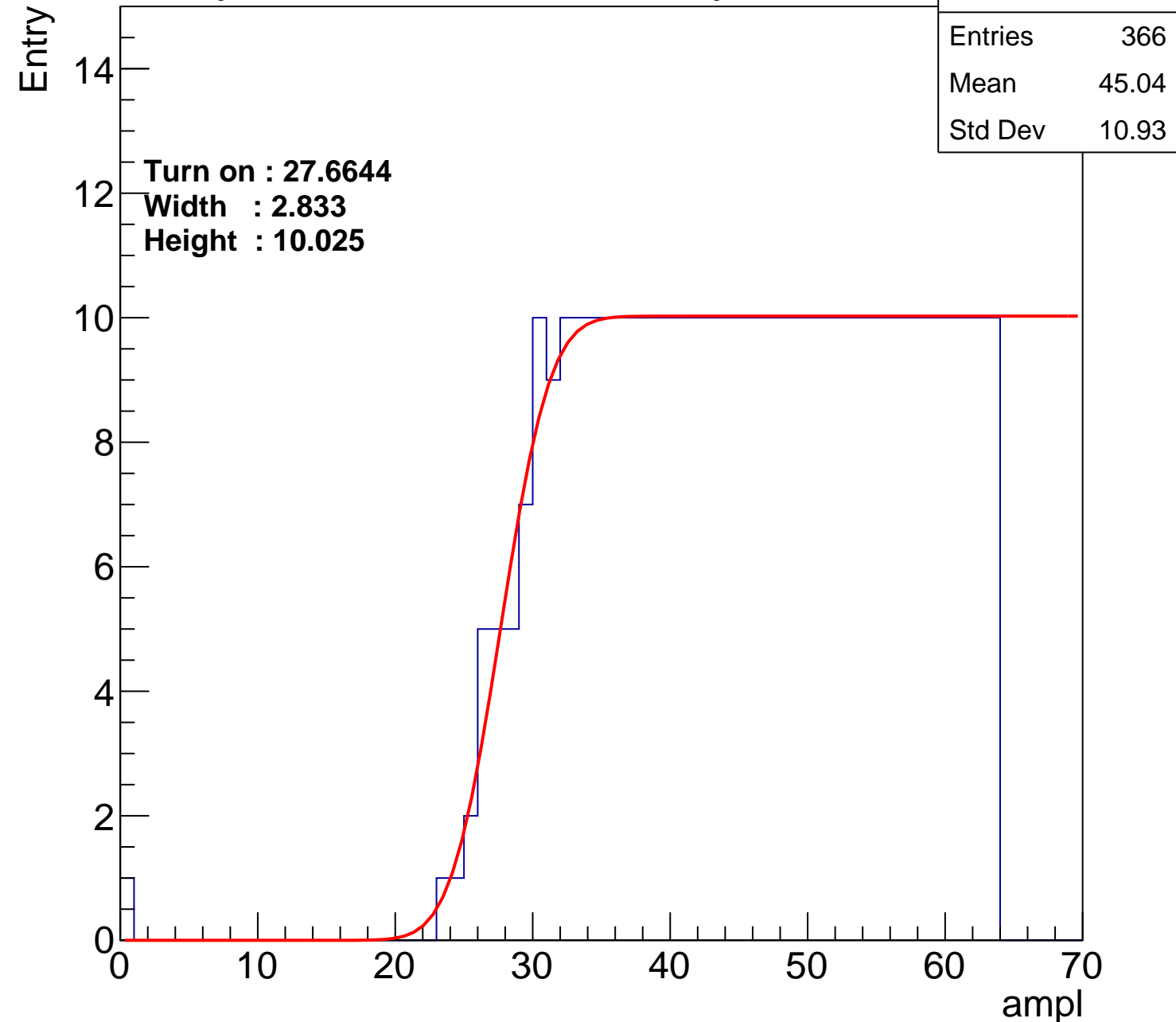
Width : 2.833

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.77
Std Dev	11.42

Turn on : 27.1787

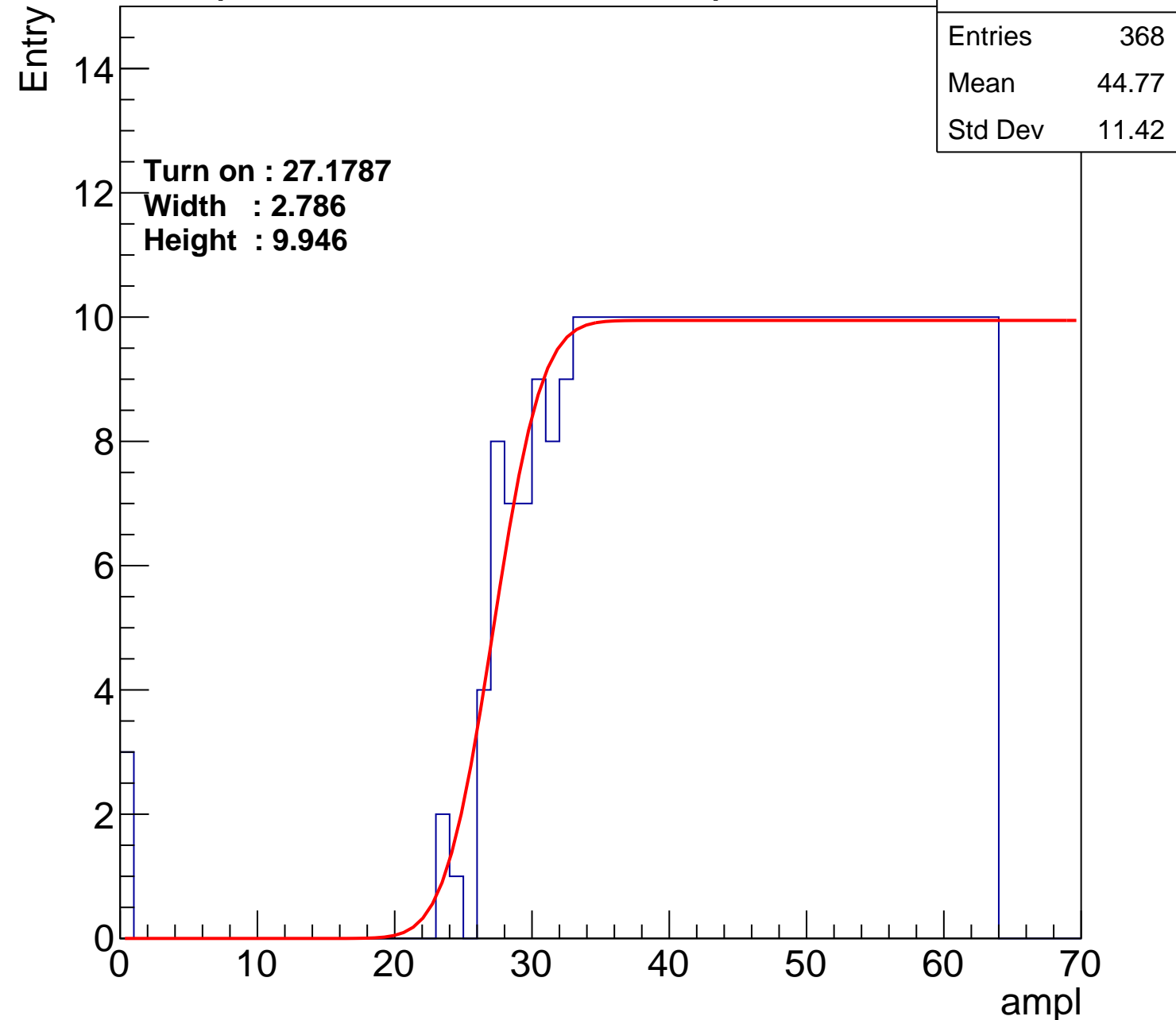
Width : 2.786

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 27.1808

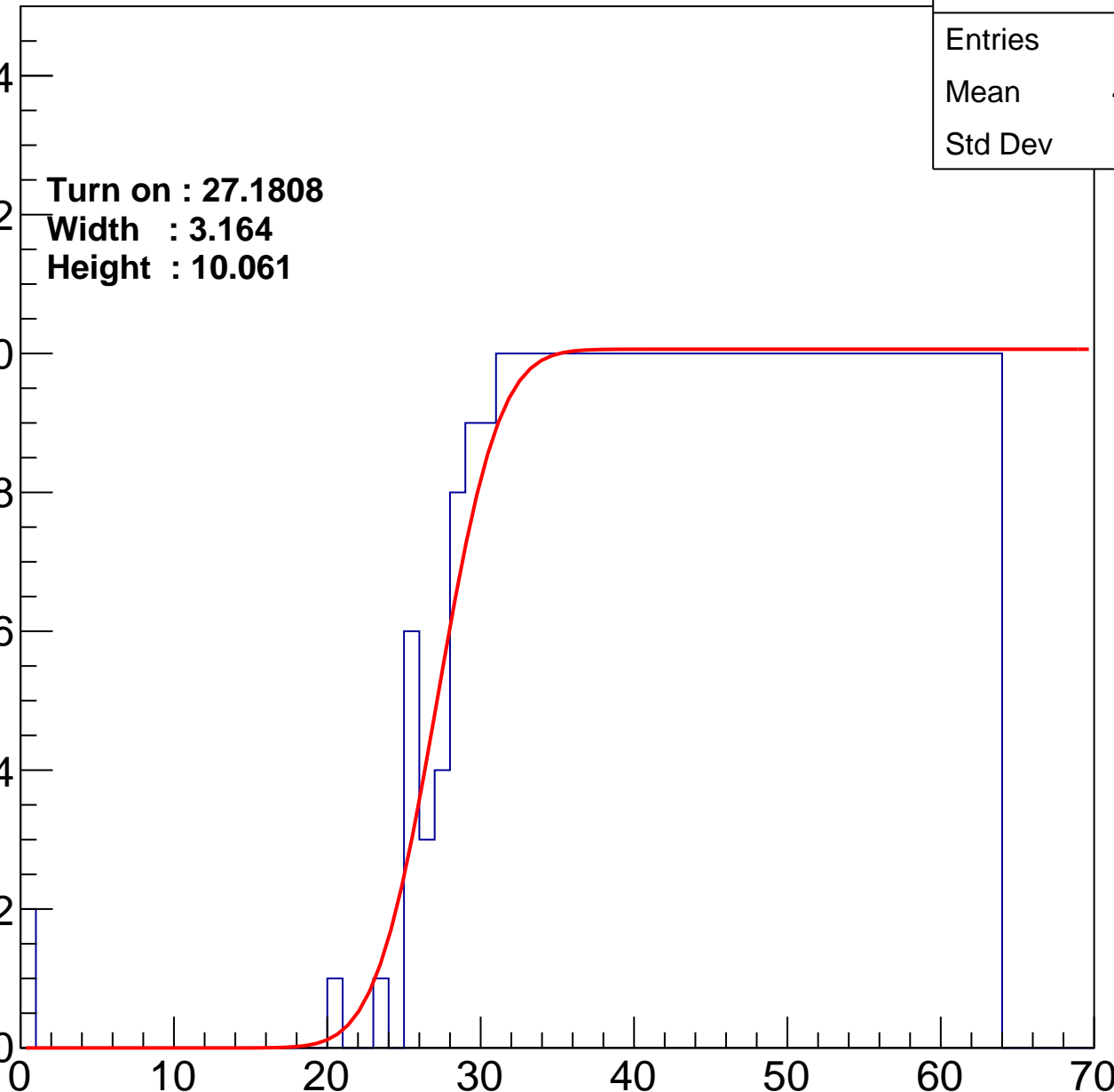
Width : 3.164

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.76
Std Dev	11.92

Turn on : 25.9257

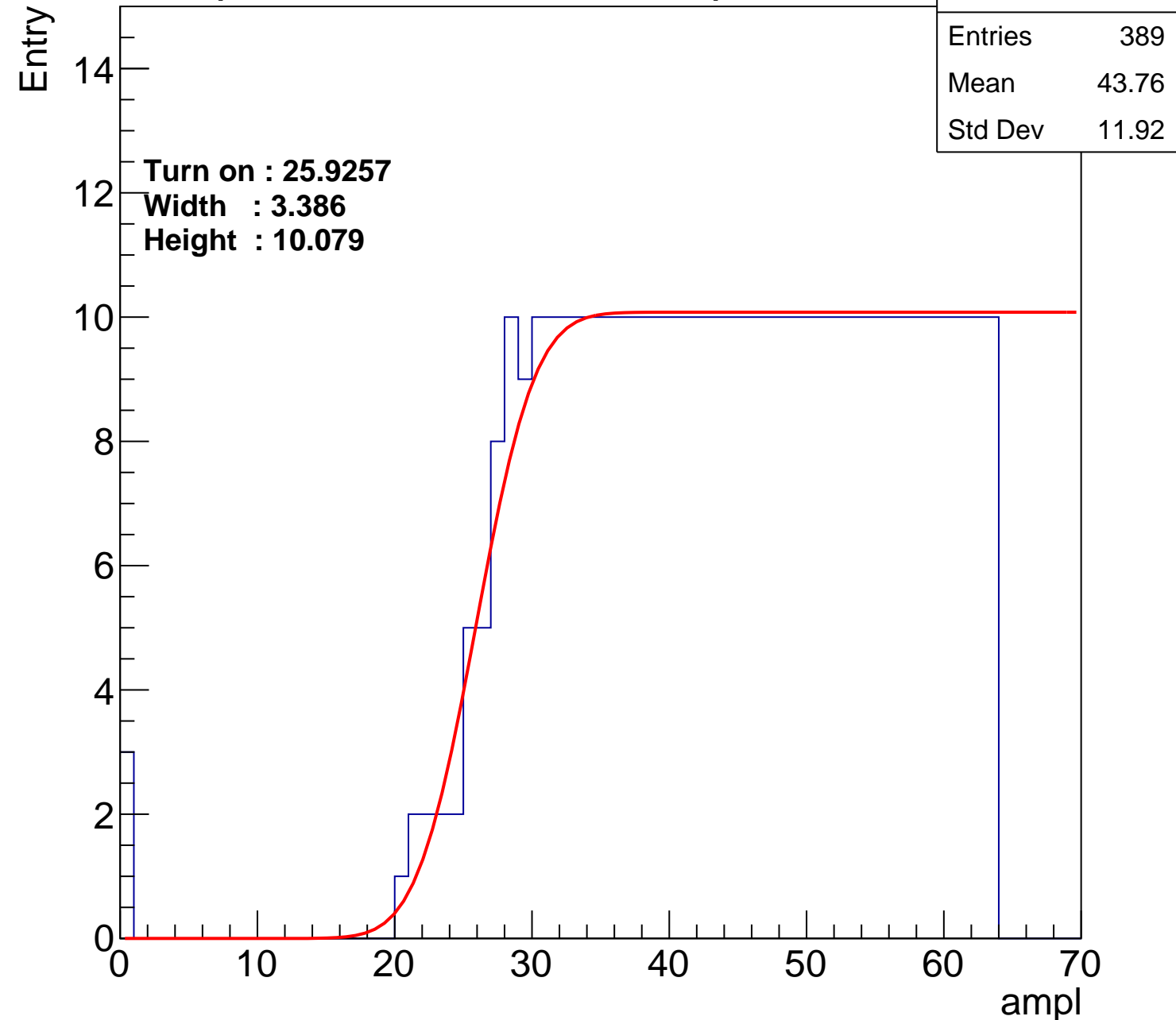
Width : 3.386

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.5
Std Dev	10.87

Turn on : 28.7883

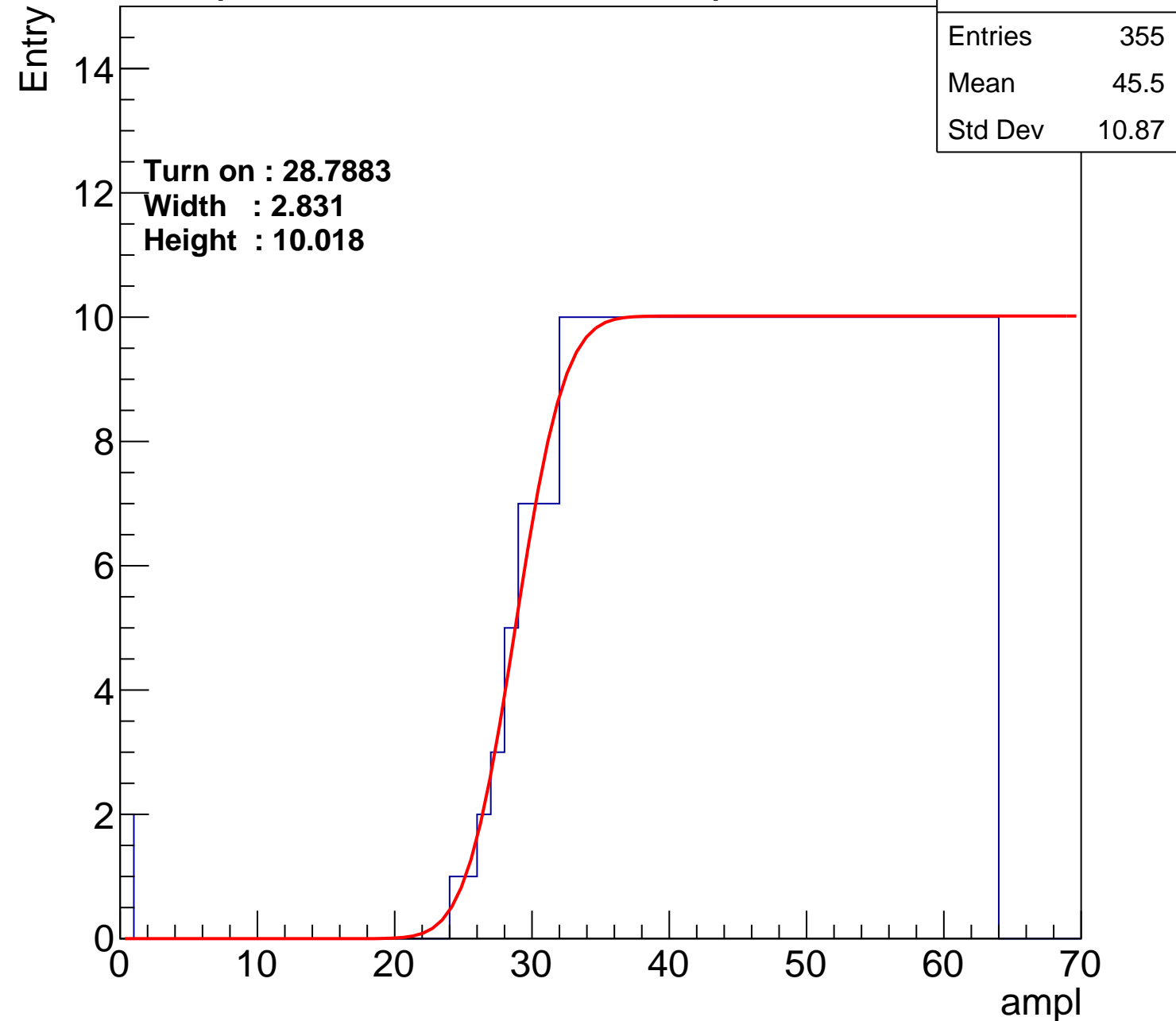
Width : 2.831

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.18
Std Dev	12.03

Turn on : 26.9420

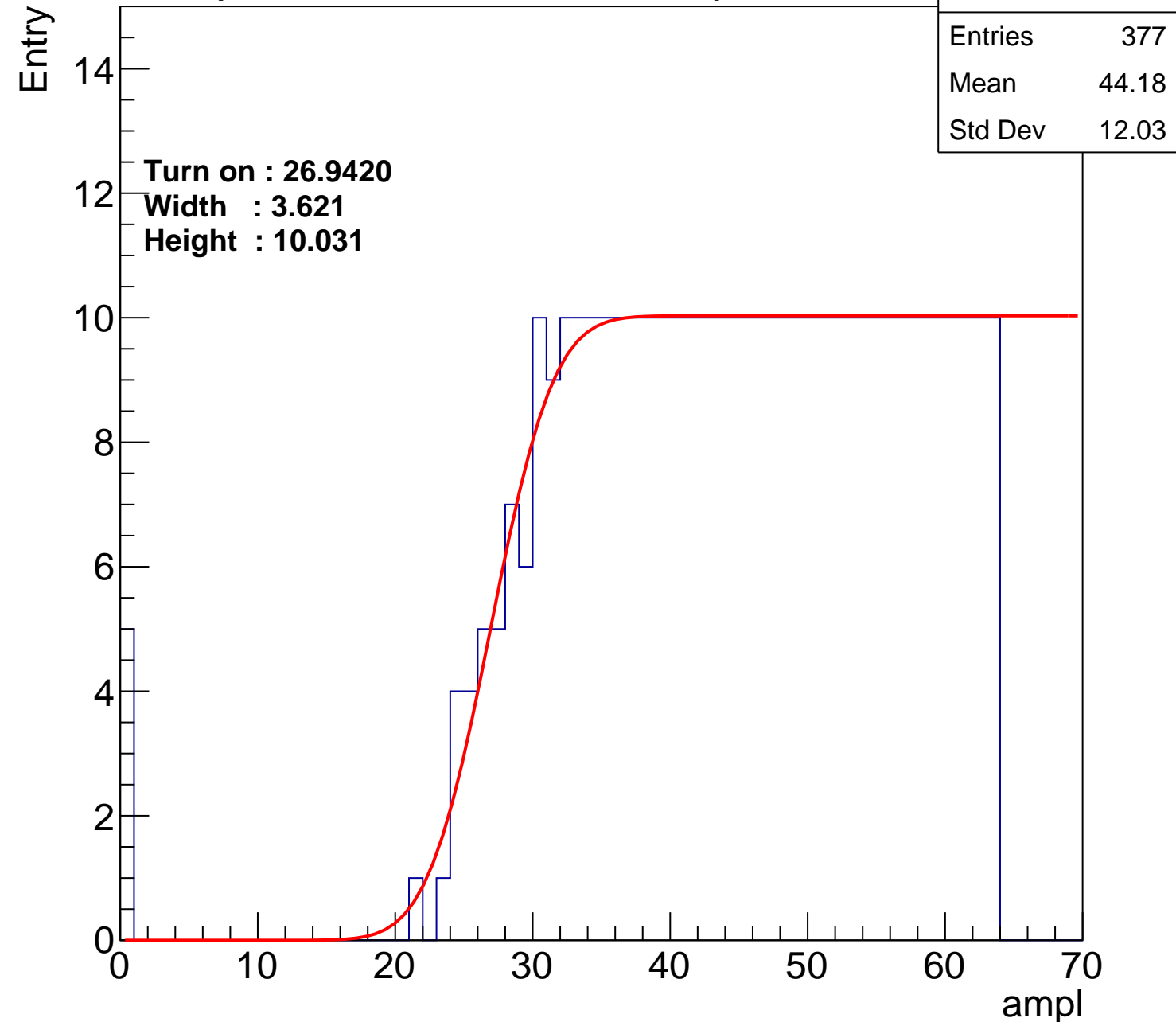
Width : 3.621

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch113

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.8978

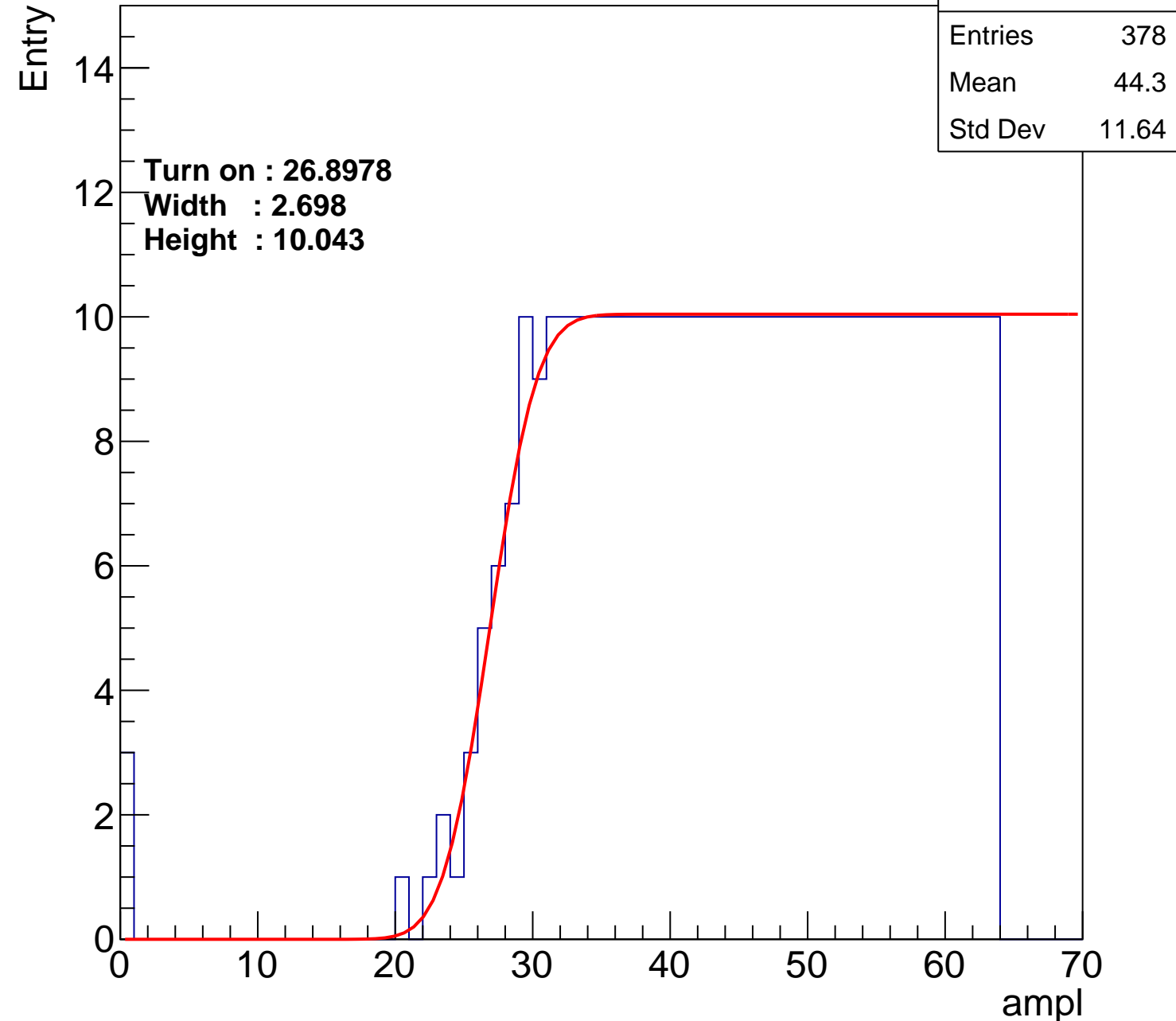
Width : 2.698

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch114

calib_packv5_042523_0143.root, FC#13, port D2

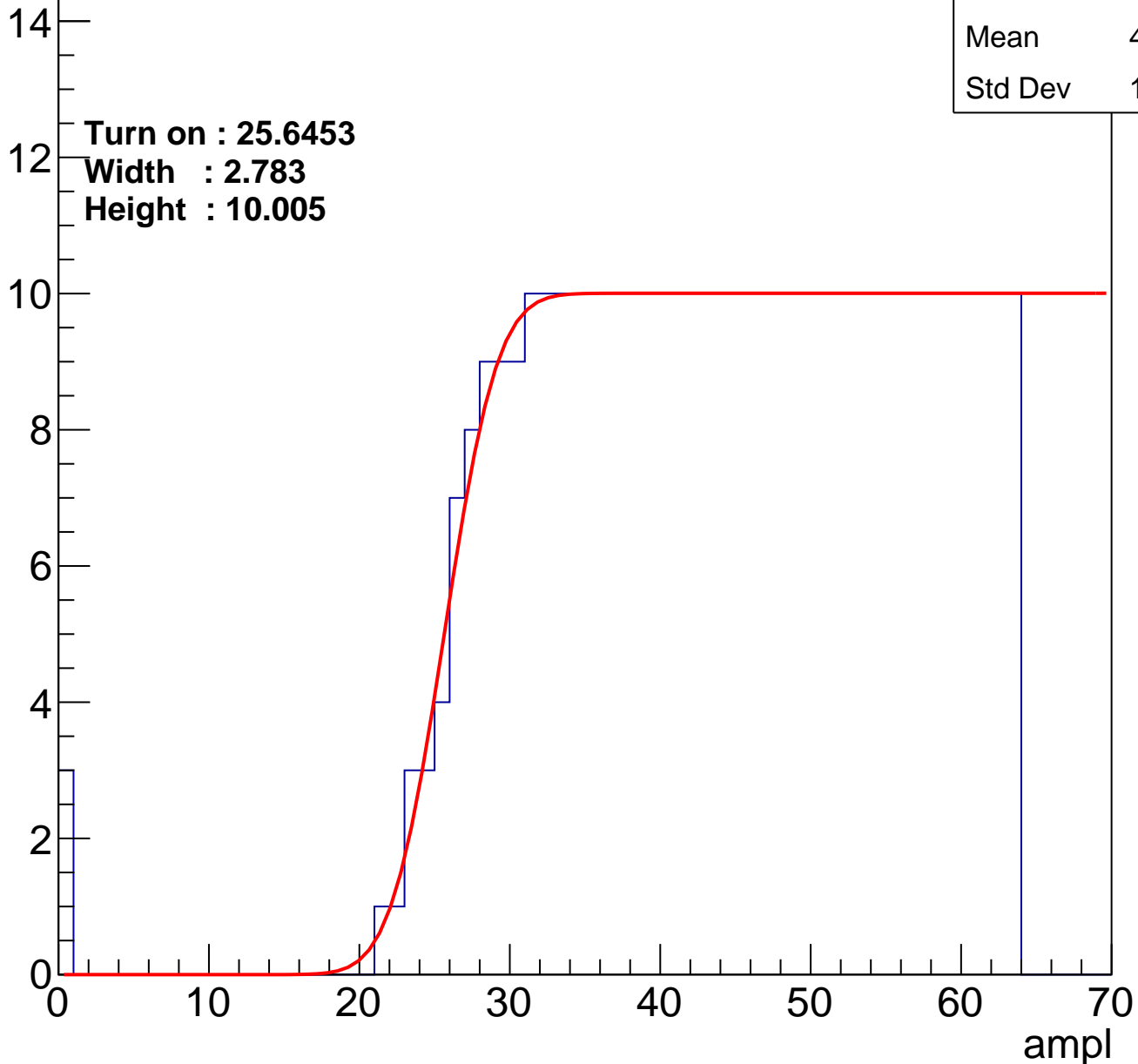
Entries	387
Mean	43.86
Std Dev	11.85

Turn on : 25.6453

Width : 2.783

Height : 10.005

Entry



B1L003S, U20-ch115

calib_packv5_042523_0143.root, FC#13, port D2

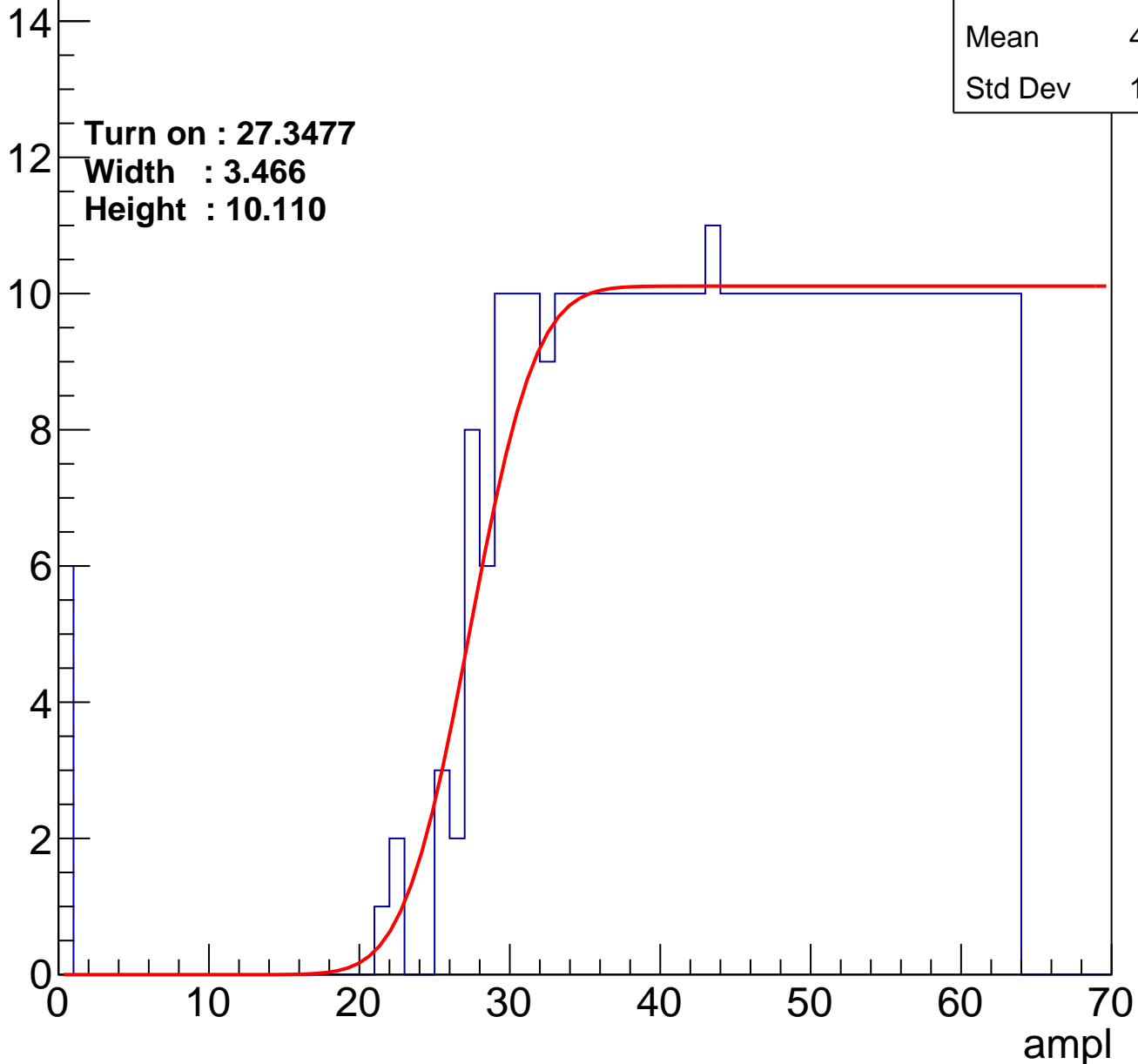
Entries	378
Mean	44.15
Std Dev	12.13

Turn on : 27.3477

Width : 3.466

Height : 10.110

Entry



B1L003S, U20-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.67
Std Dev	11.82

Turn on : 24.9305

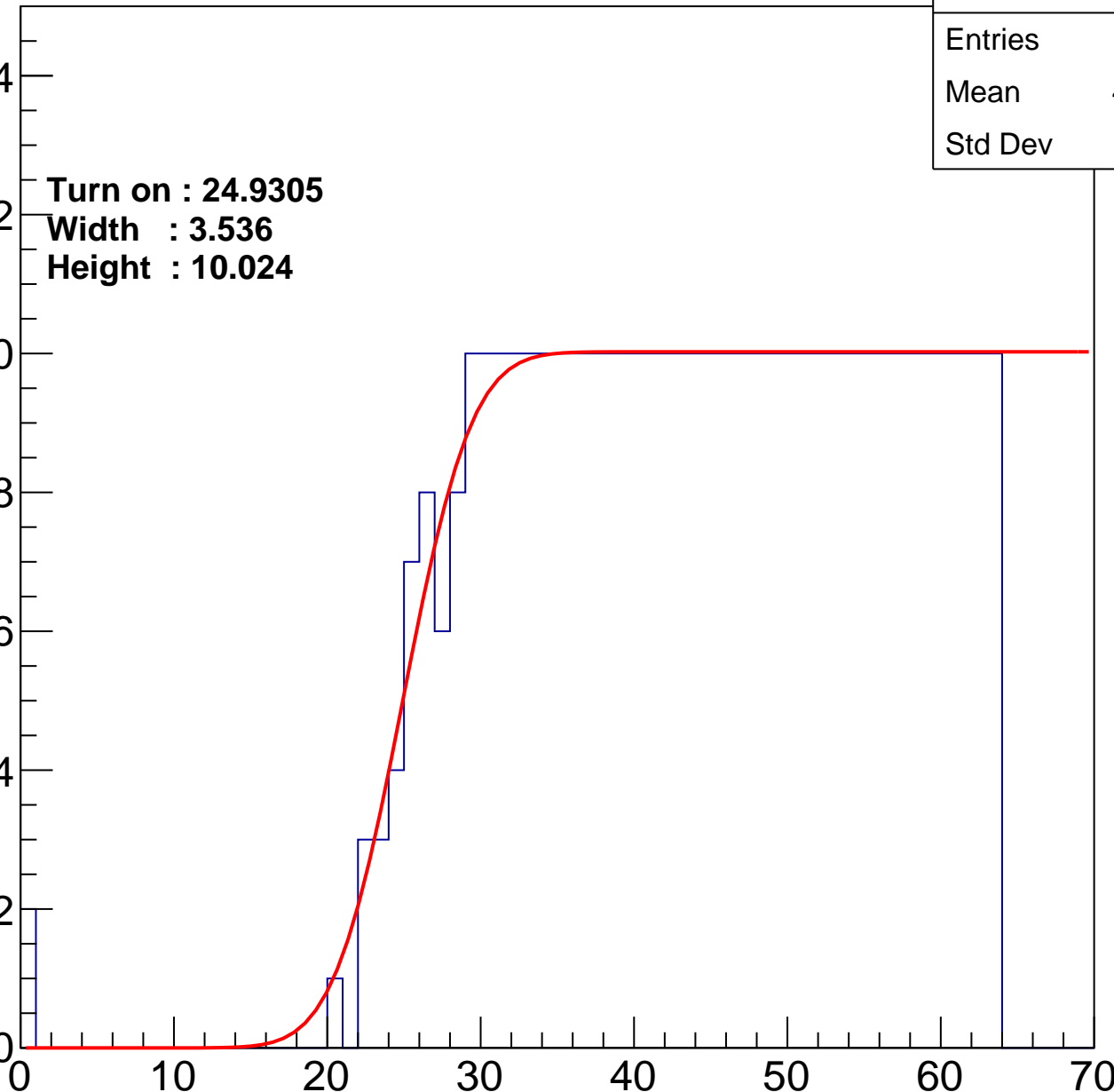
Width : 3.536

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch117

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.2
Std Dev	11.69

Turn on : 26.5856

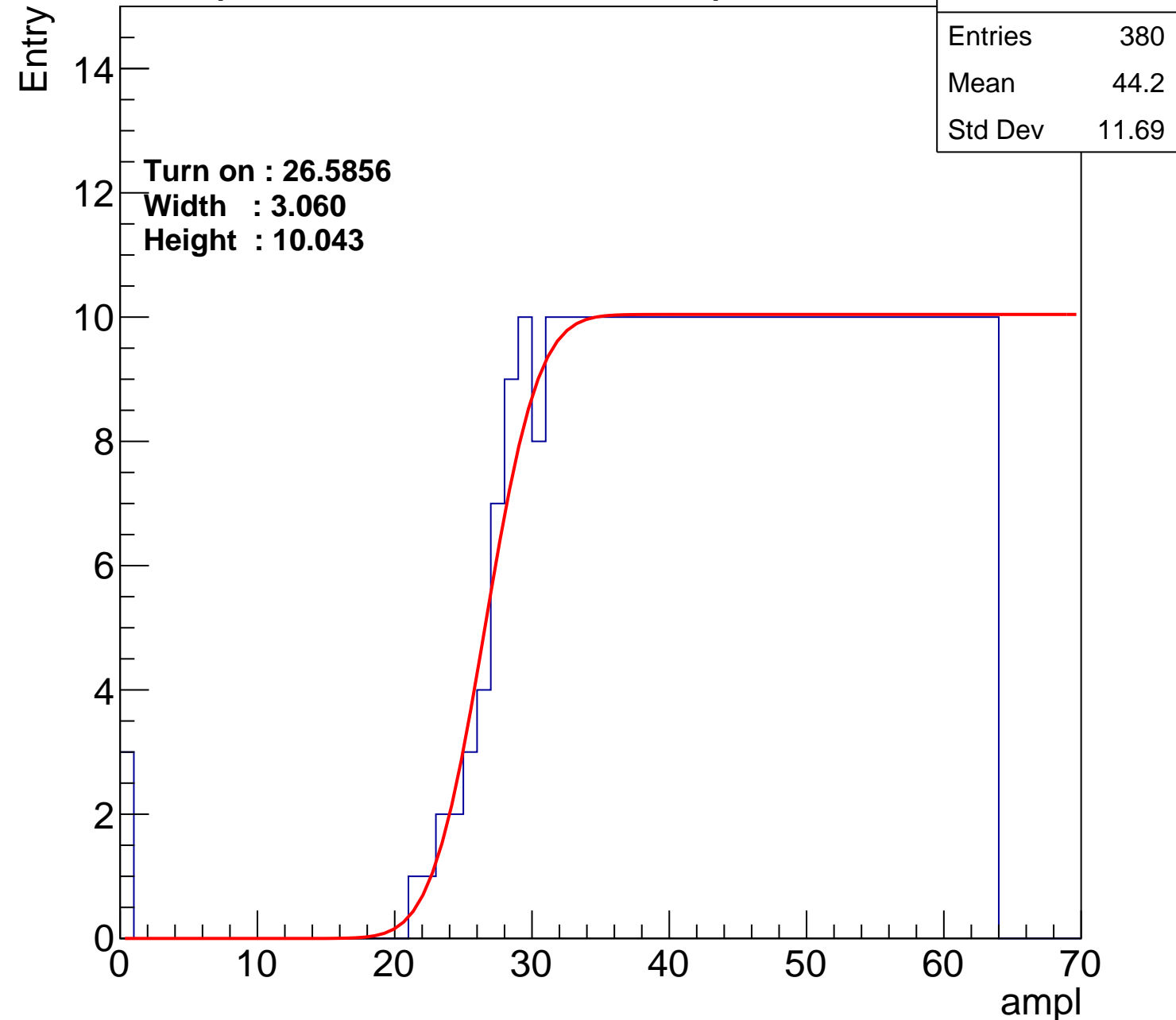
Width : 3.060

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	43.92
Std Dev	12.03

Turn on : 26.6887

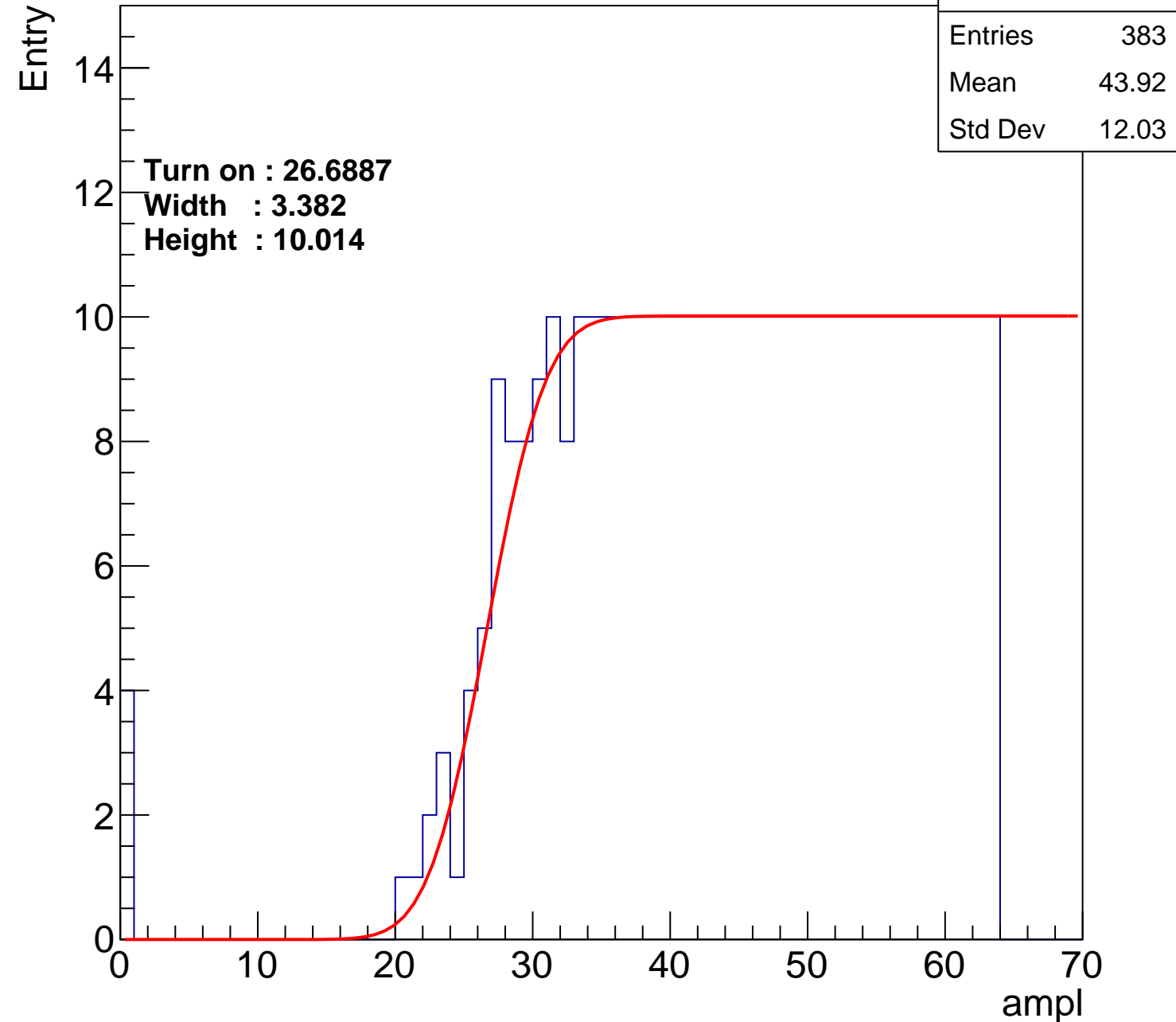
Width : 3.382

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch119

calib_packv5_042523_0143.root, FC#13, port D2

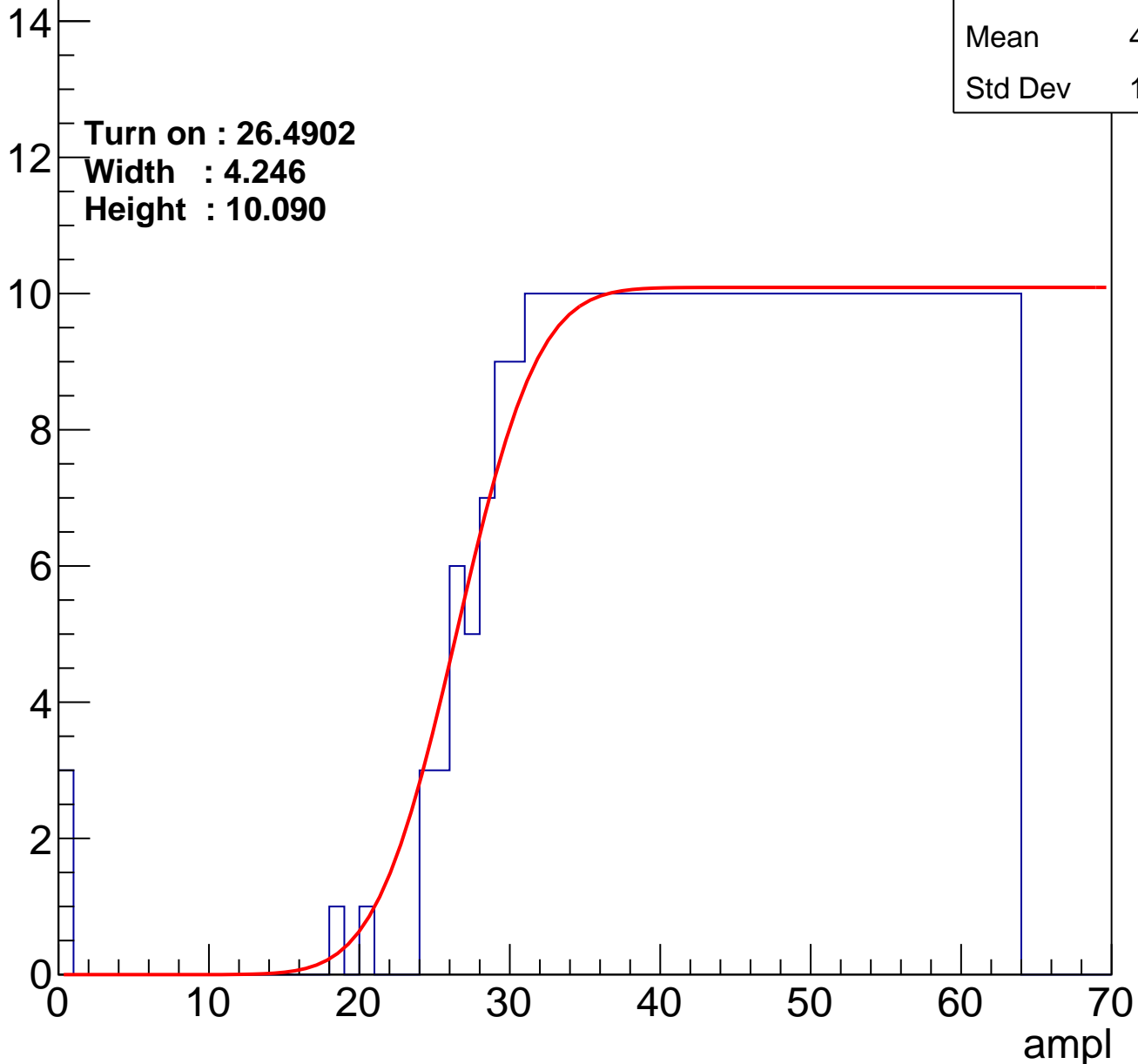
Entries	377
Mean	44.33
Std Dev	11.65

Turn on : 26.4902

Width : 4.246

Height : 10.090

Entry



B1L003S, U20-ch120

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 26.2712

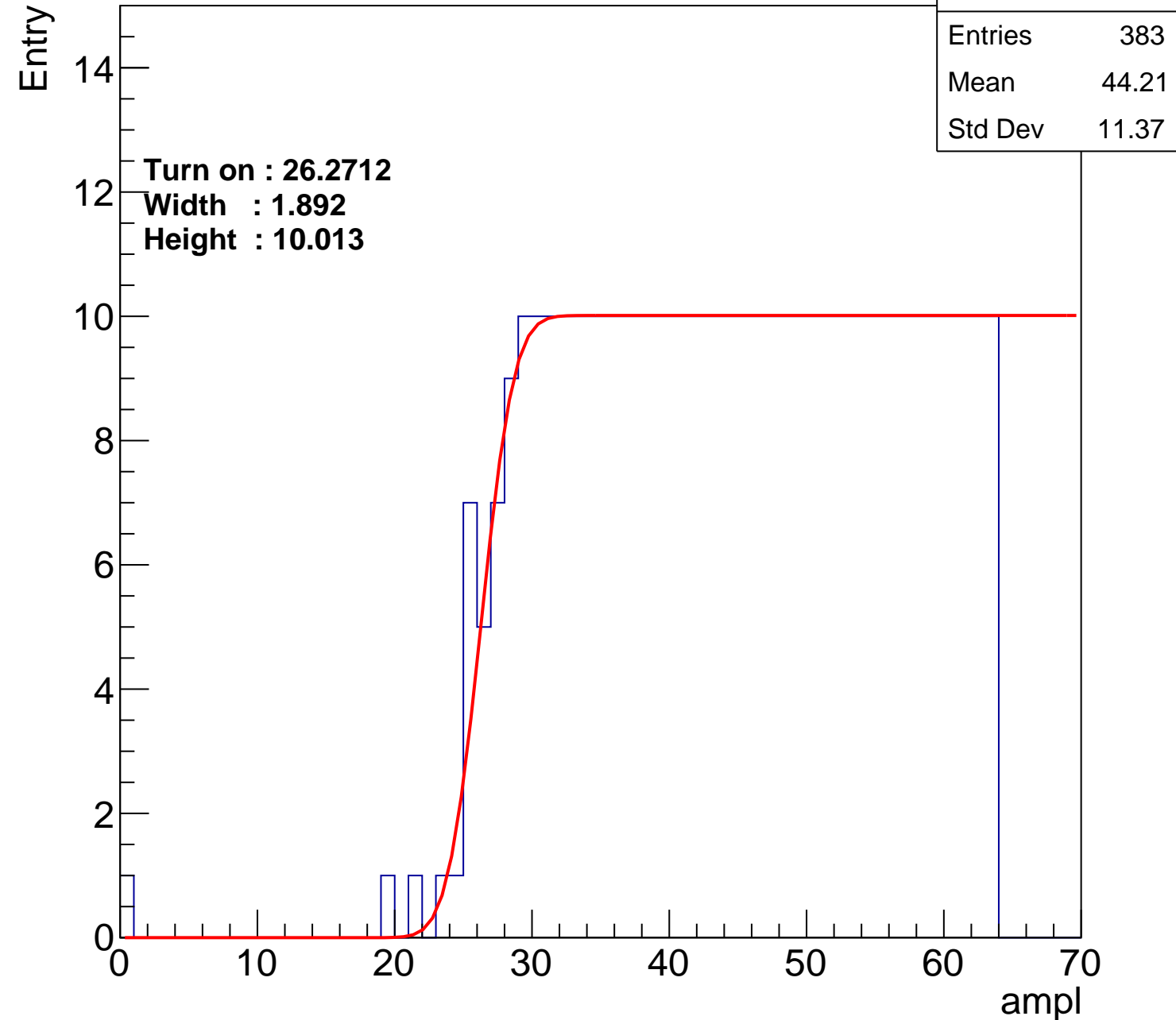
Width : 1.892

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch121

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.48
Std Dev	10.96

Turn on : 28.7683

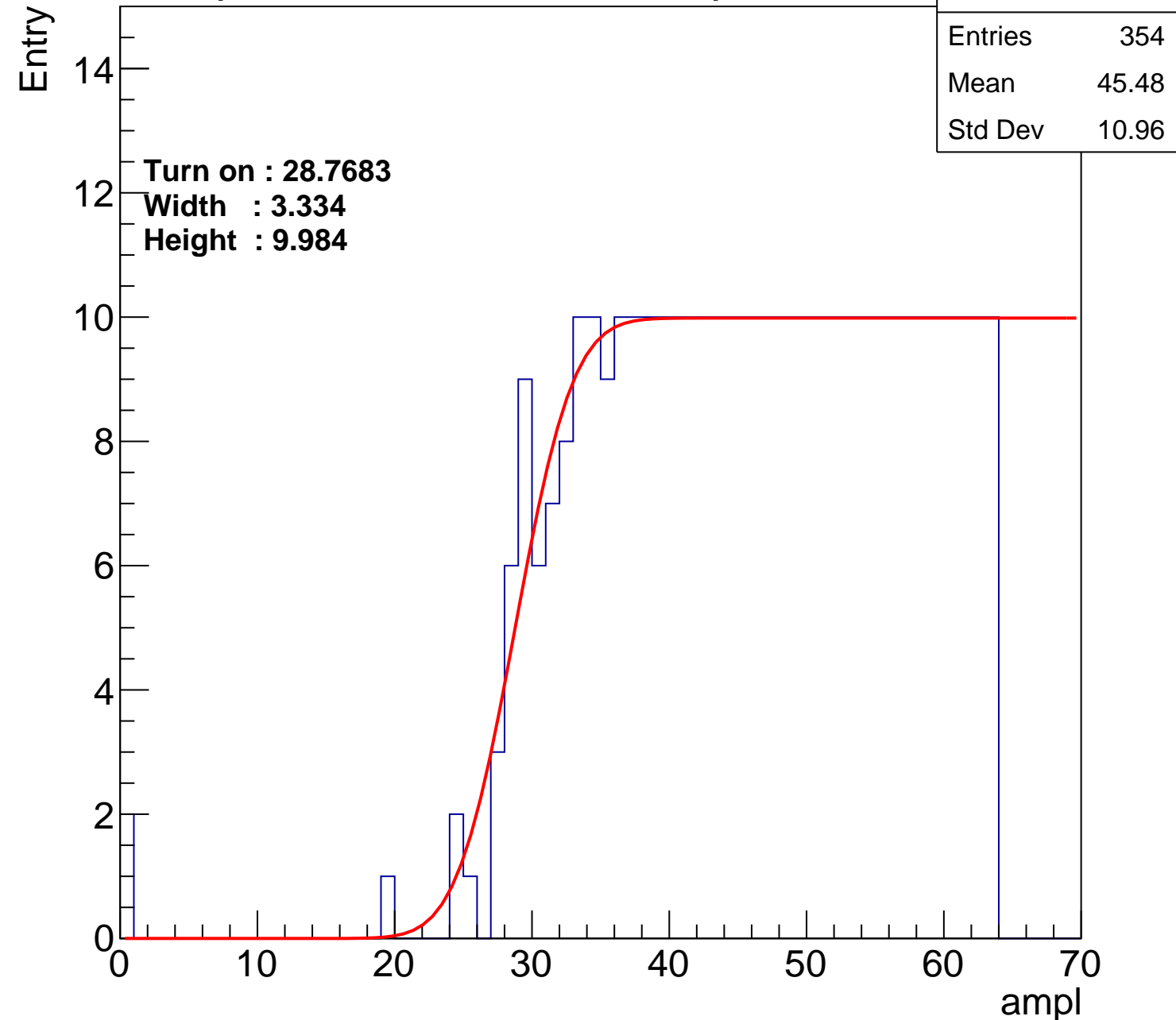
Width : 3.334

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch122

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.22
Std Dev	11.54

Turn on : 29.0538

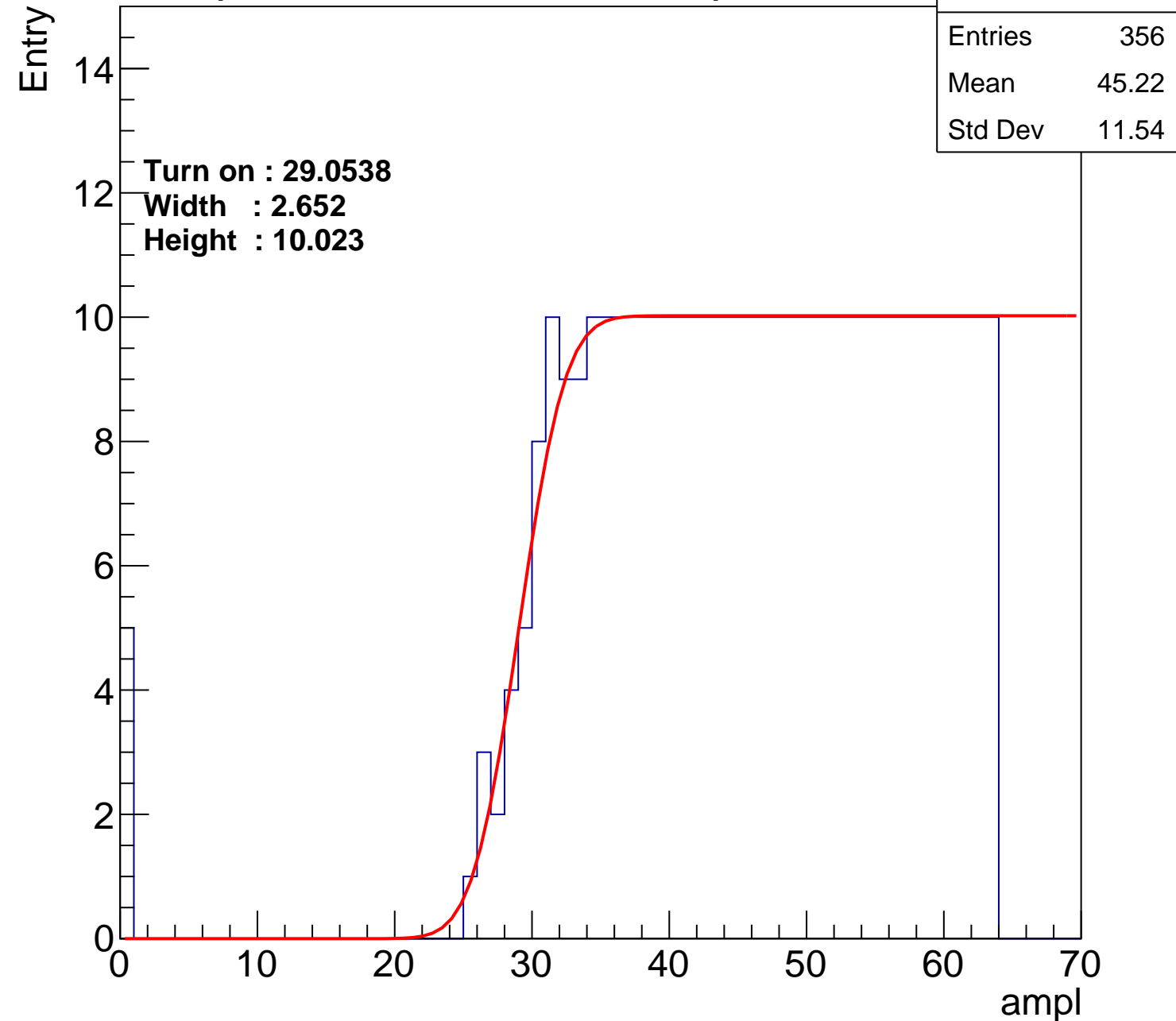
Width : 2.652

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.88
Std Dev	12.06

Turn on : 27.0432

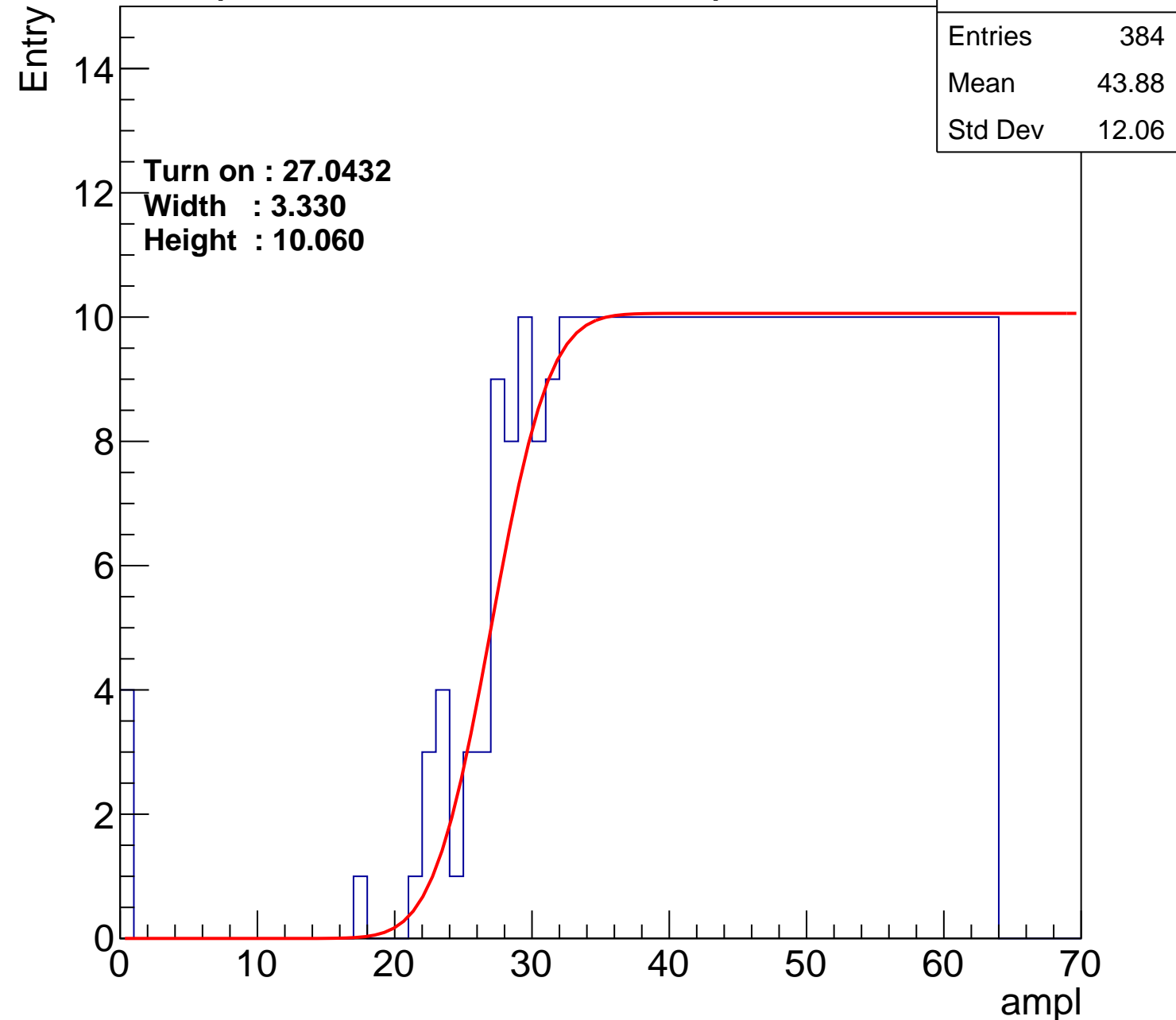
Width : 3.330

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.5699

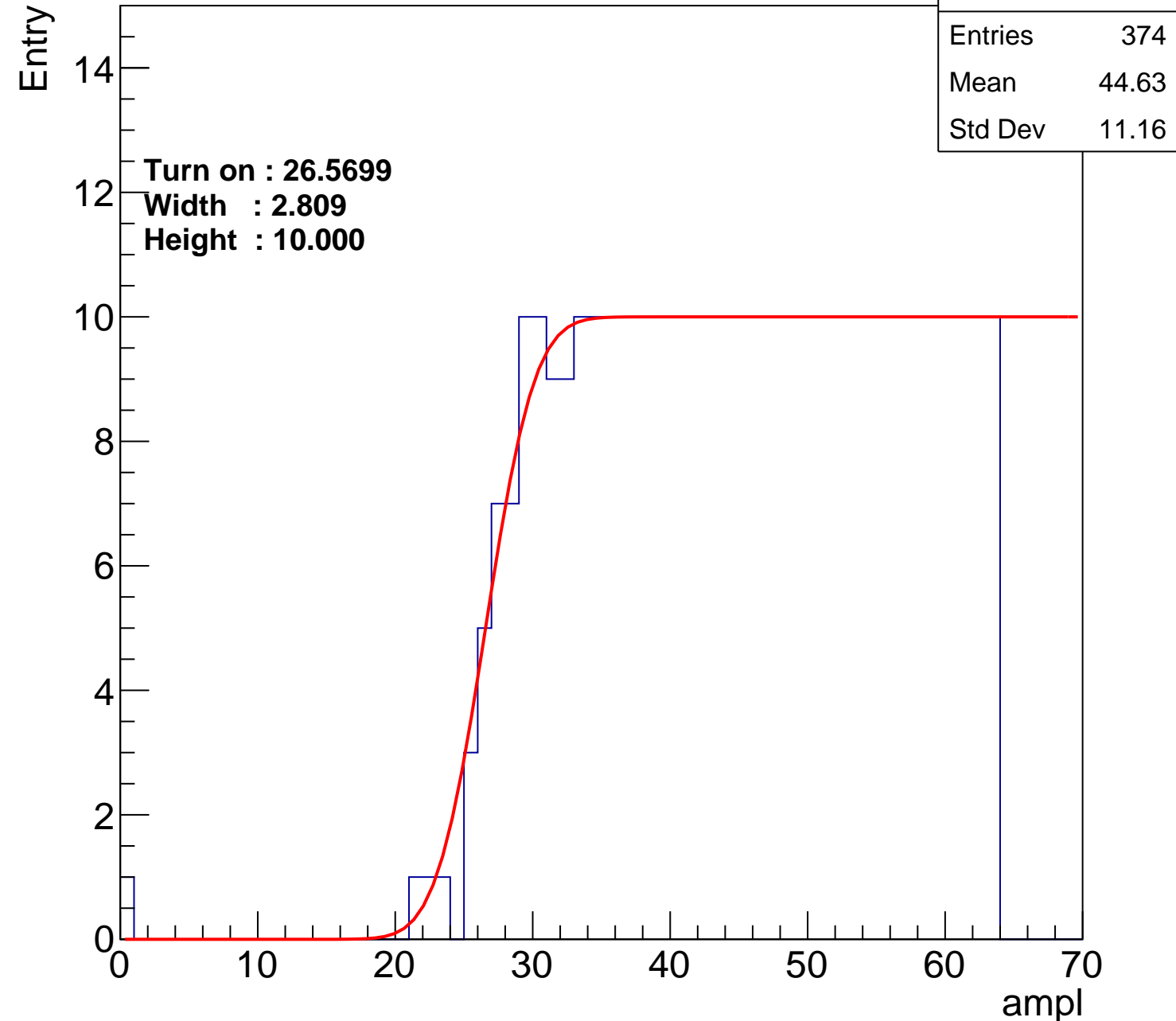
Width : 2.809

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch125

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.05
Std Dev	11.26

Turn on : 28.1382

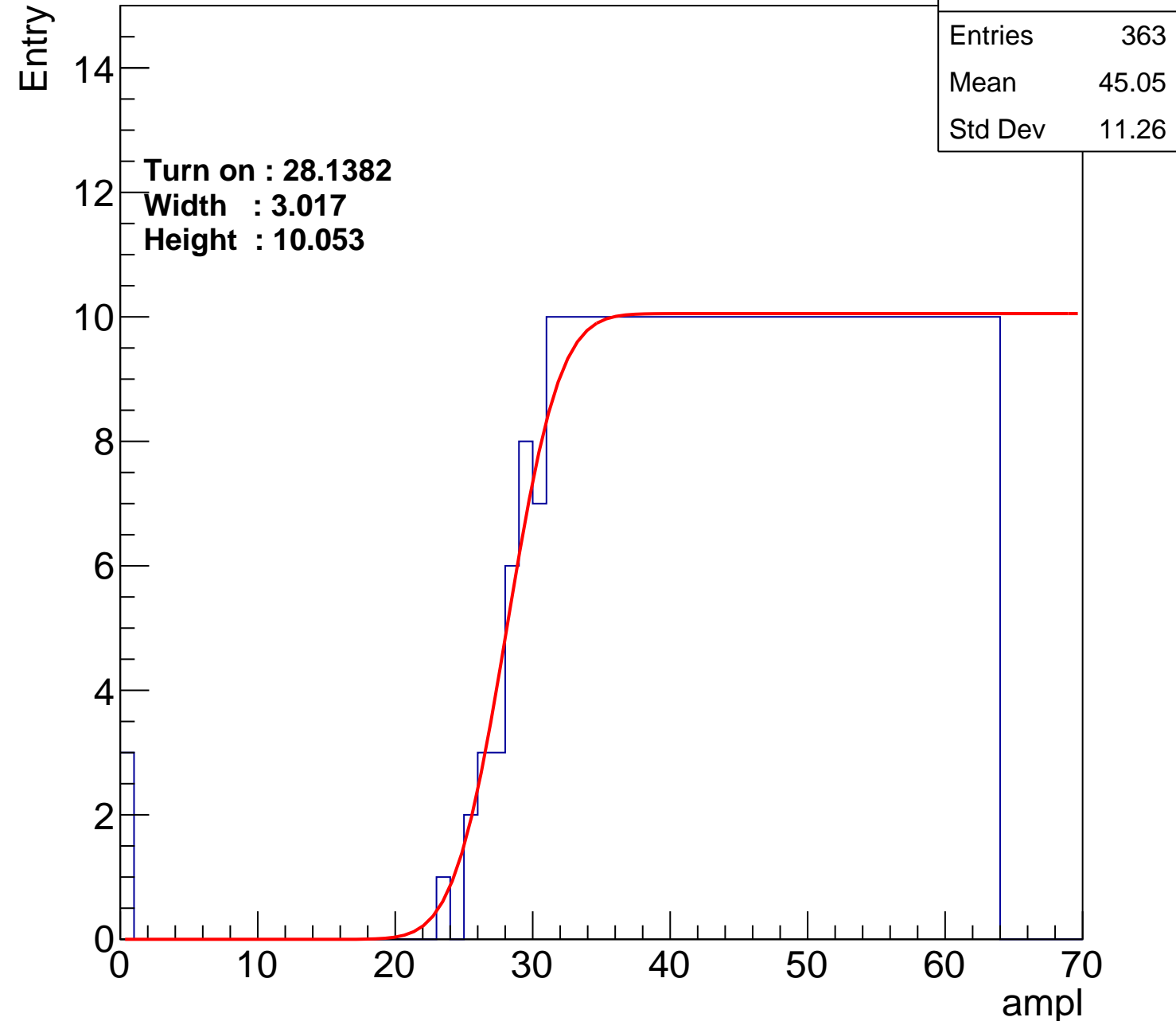
Width : 3.017

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.05
Std Dev	11.44

Turn on : 28.1890

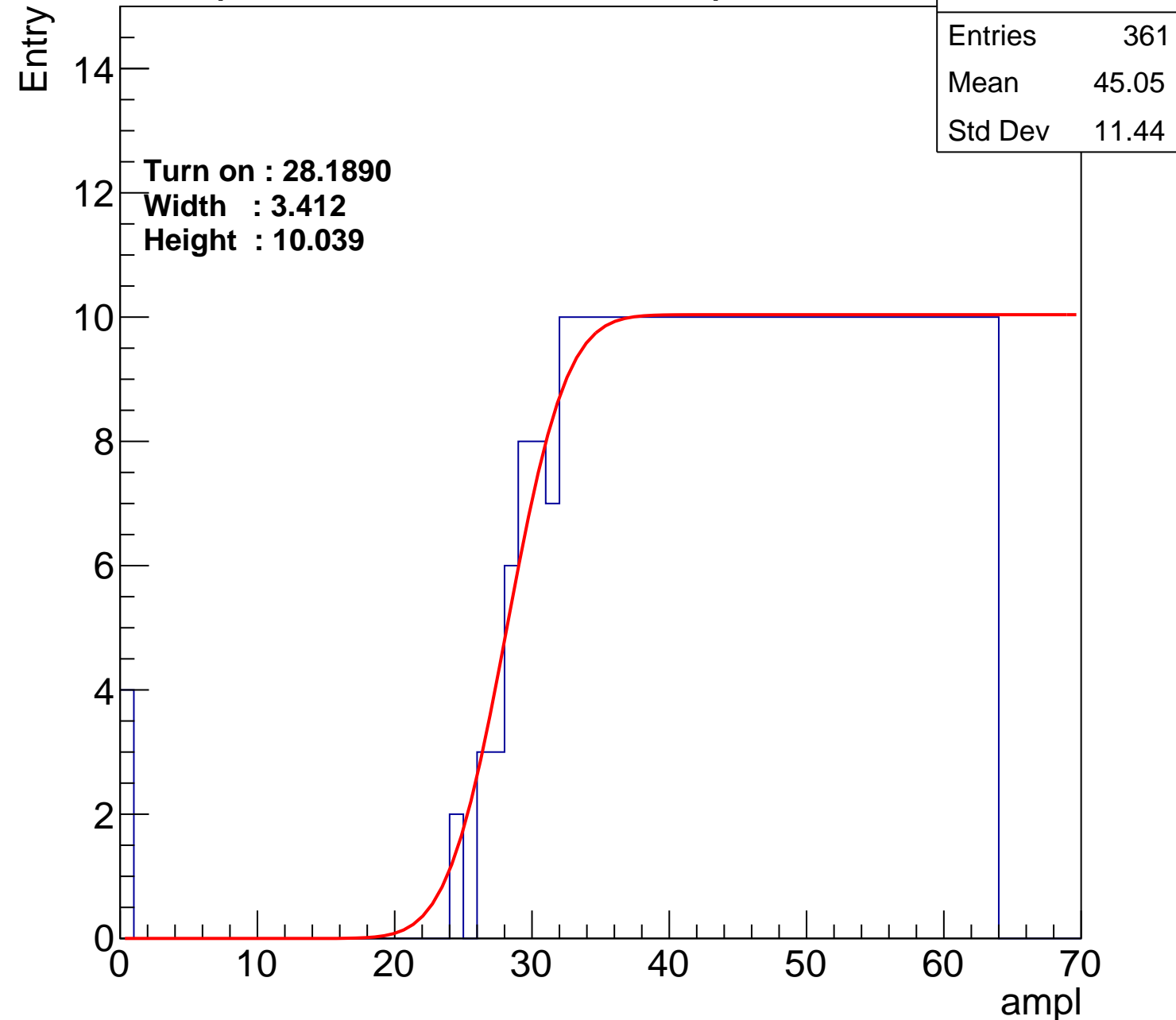
Width : 3.412

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 27.1742

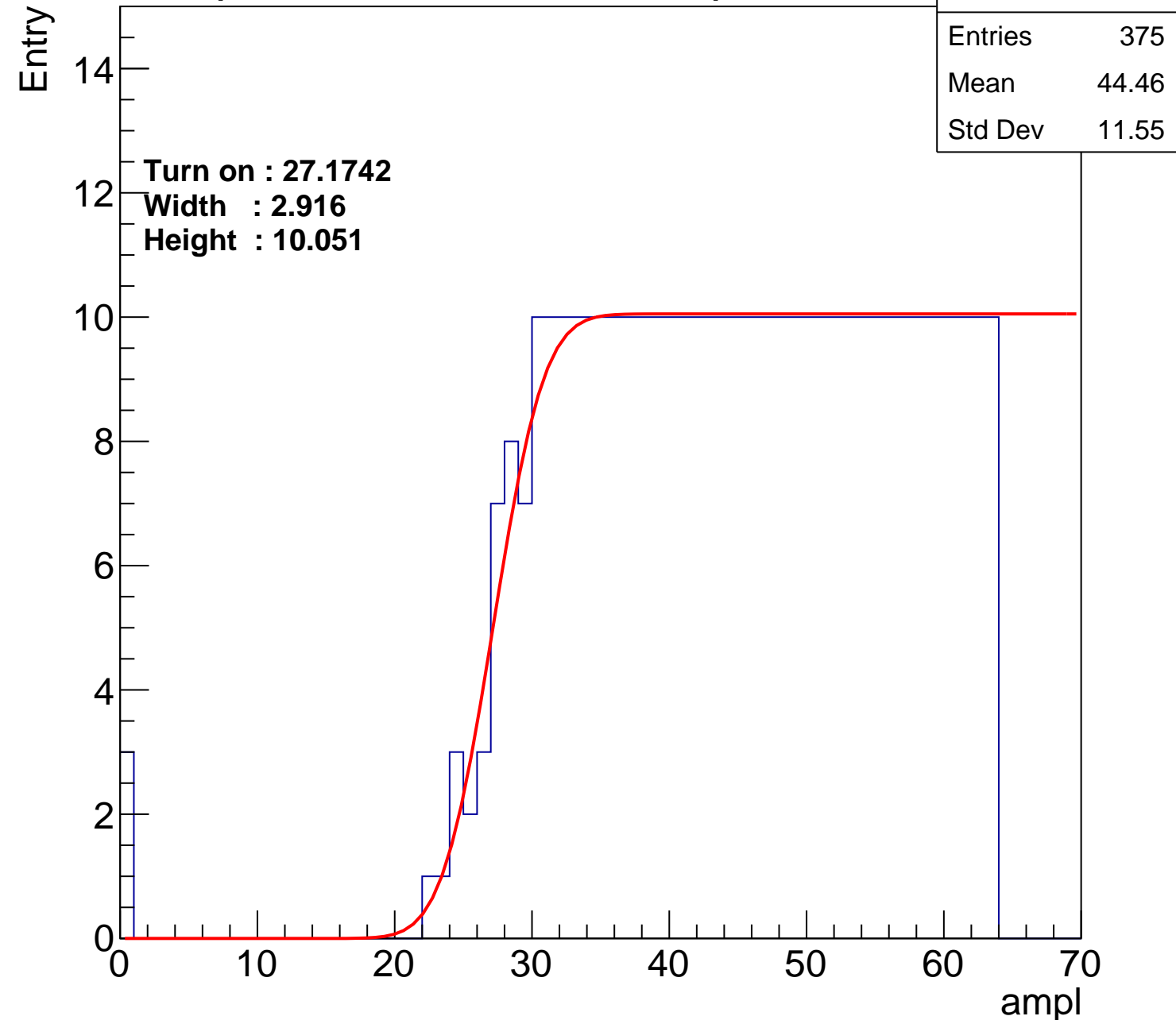
Width : 2.916

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U20-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 27.1742

Width : 2.916

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl

