

B1L103S, U21-ch0, adc0

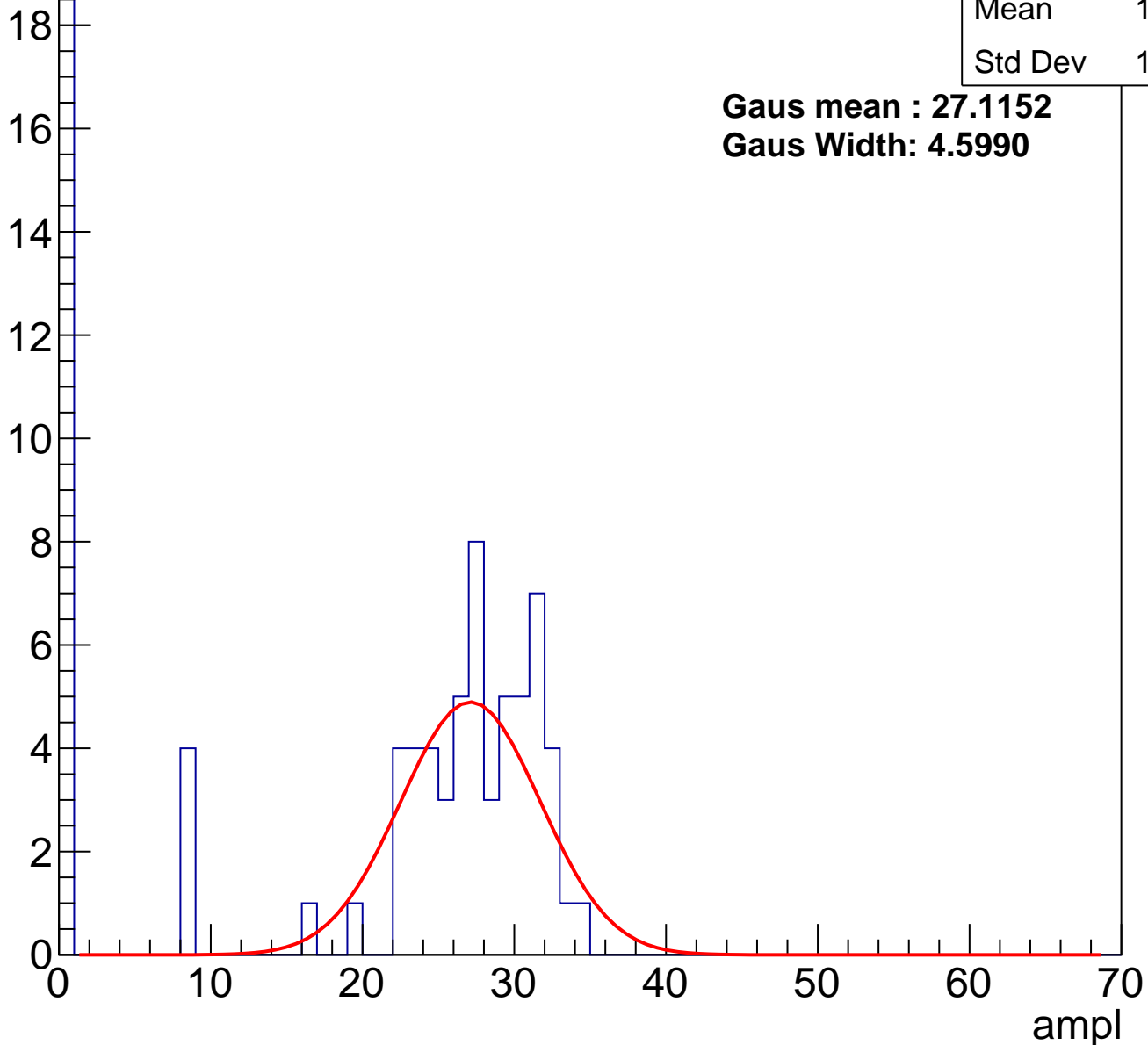
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	19.68
Std Dev	12.24

Gaus mean : 27.1152

Gaus Width: 4.5990

Entry



B1L103S, U21-ch0, adc1

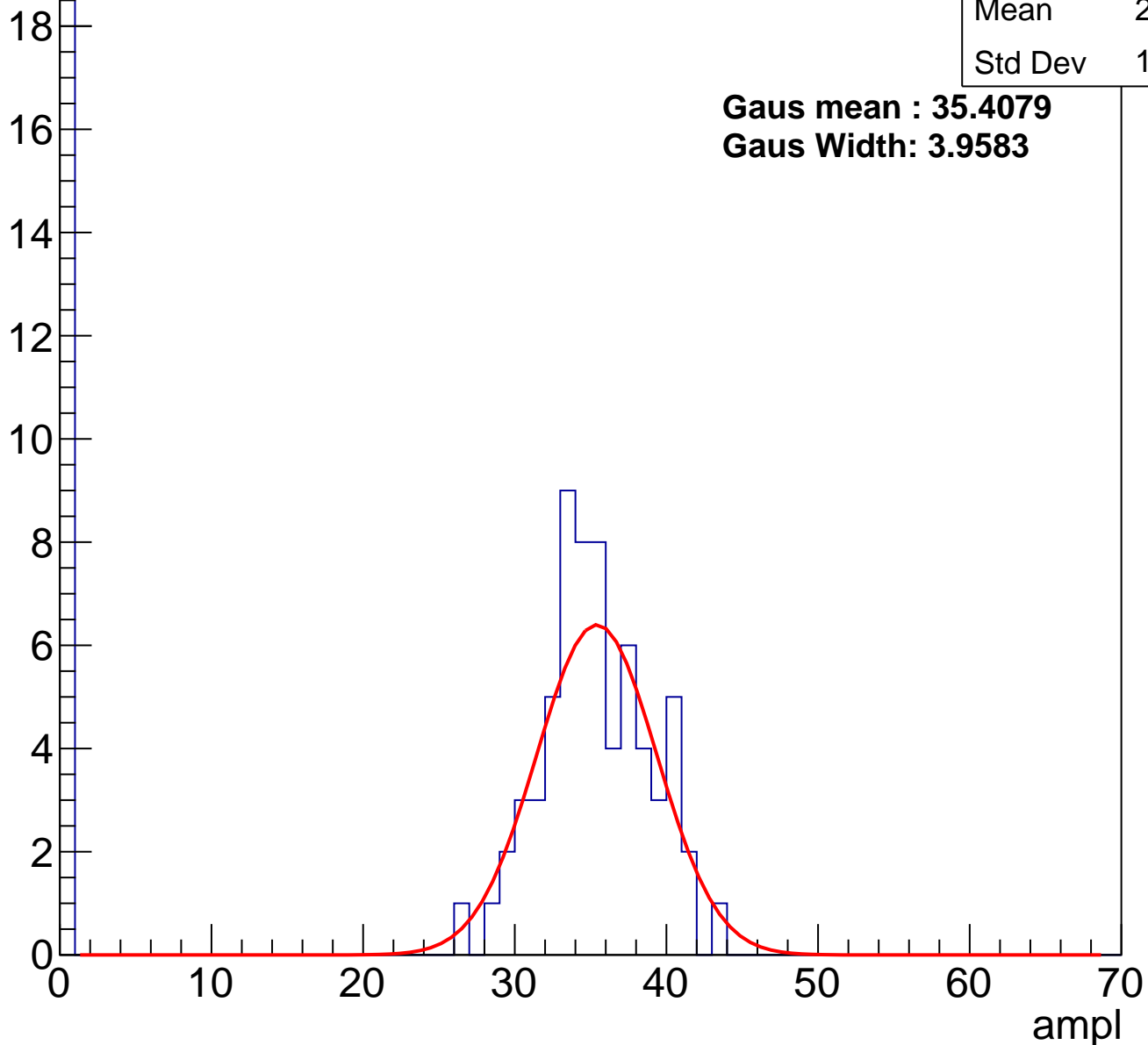
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	26.95
Std Dev	14.89

Gaus mean : 35.4079

Gaus Width: 3.9583

Entry



B1L103S, U21-ch0, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	39.96
Std Dev	8.655

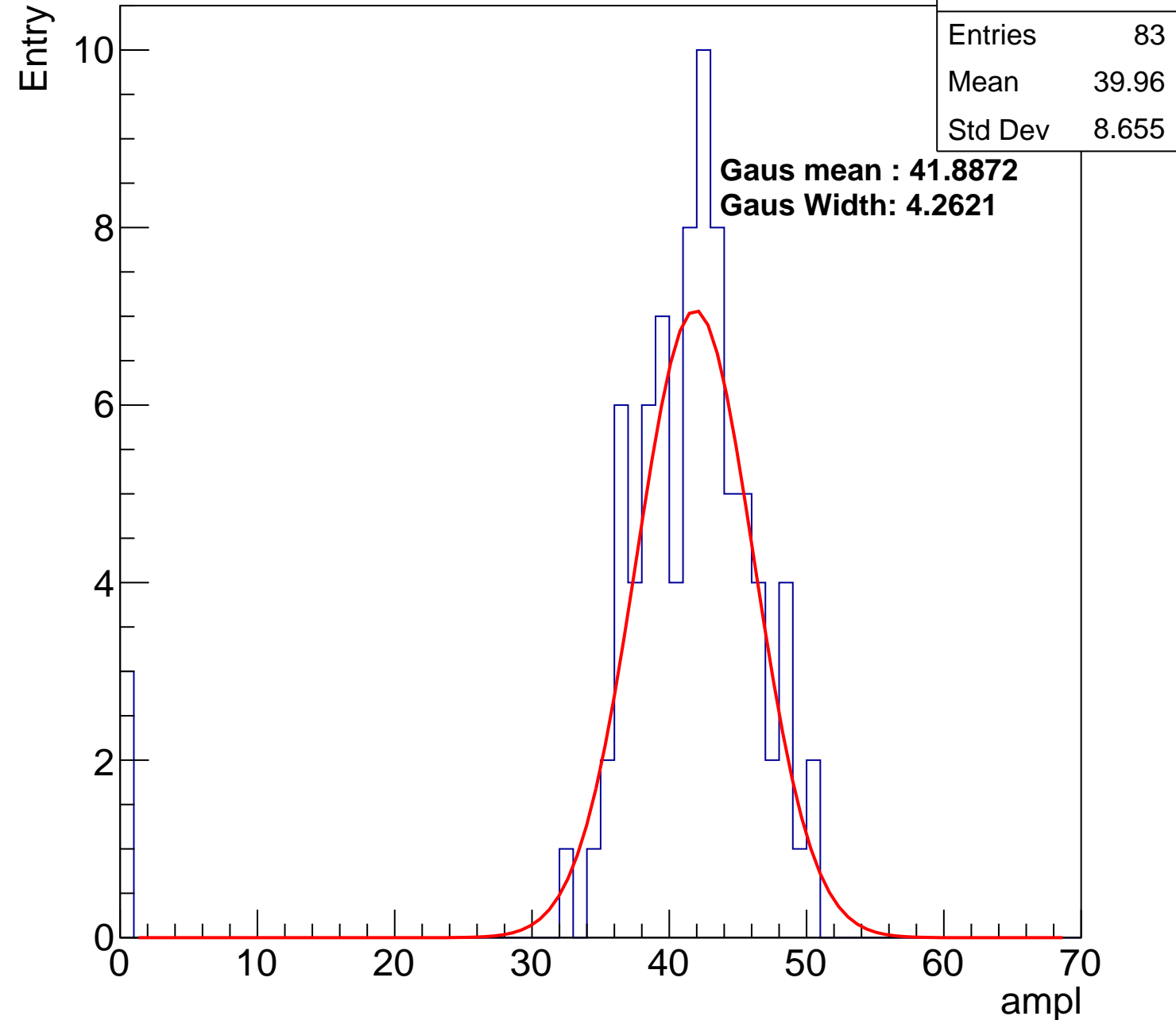
Gaus mean : 41.8872

Gaus Width: 4.2621

Entry

10
8
6
4
2
0

ampl

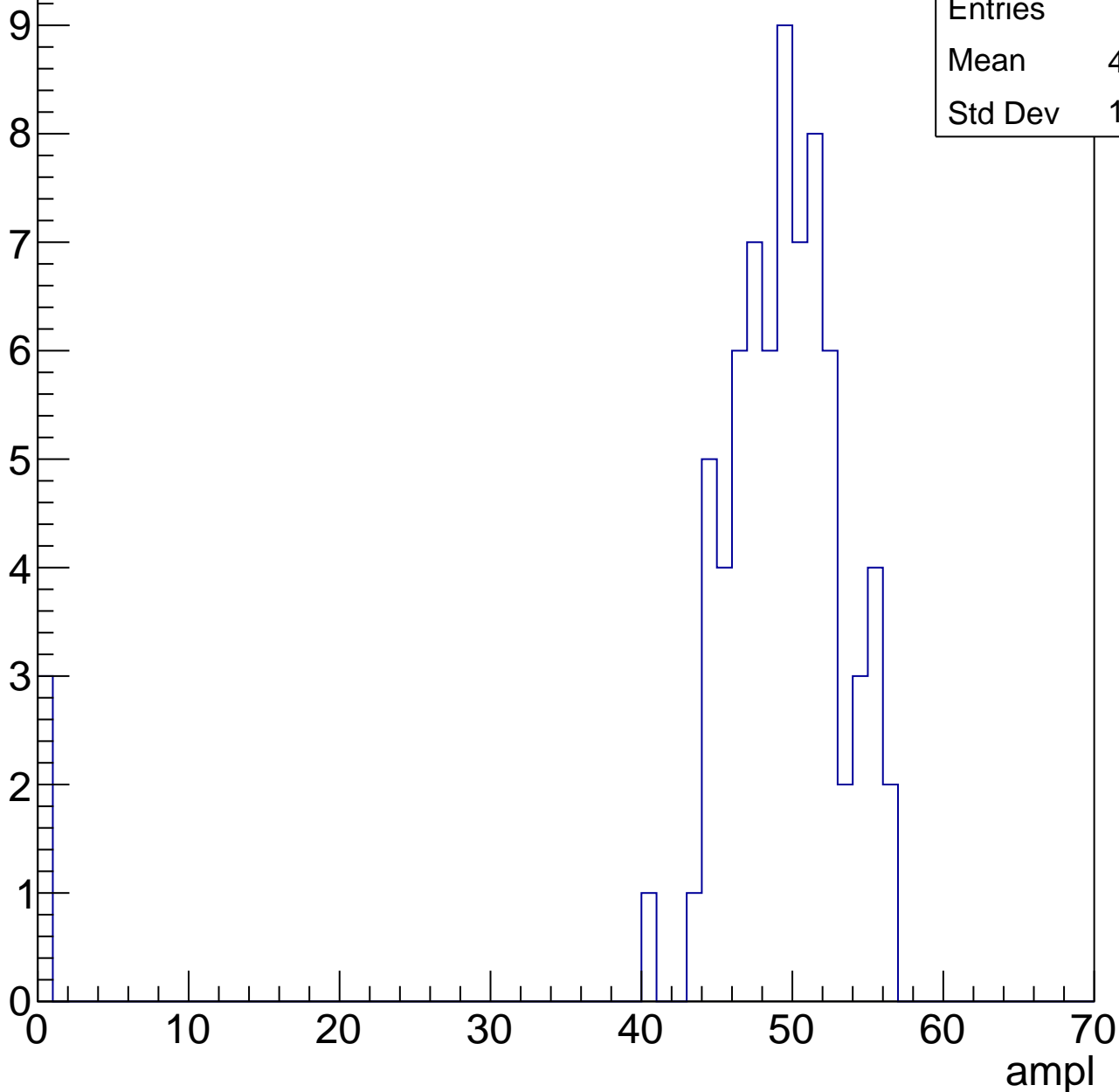


B1L103S, U21-ch0, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	47.12
Std Dev	10.25

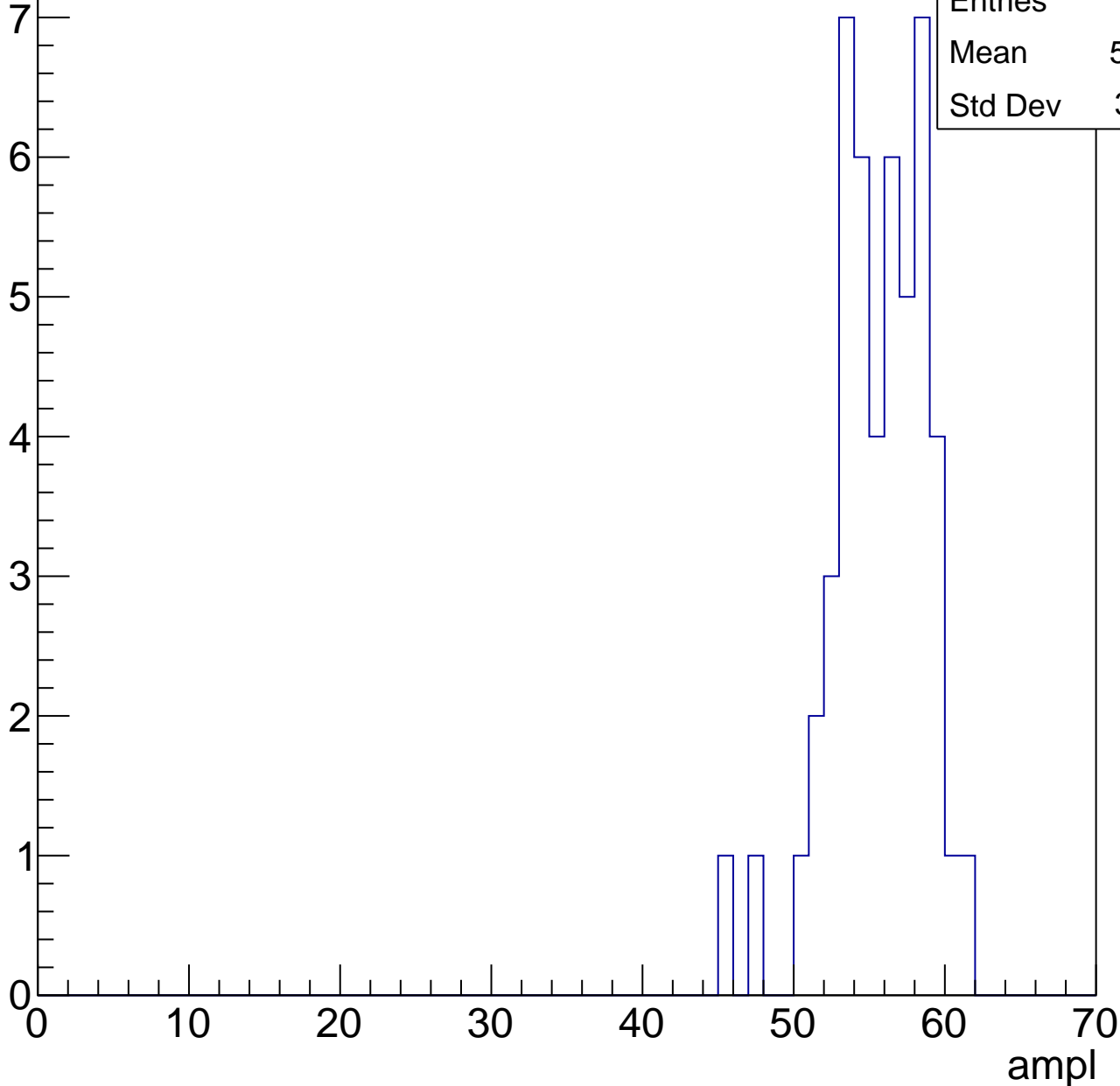


B1L103S, U21-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.08
Std Dev	3.181

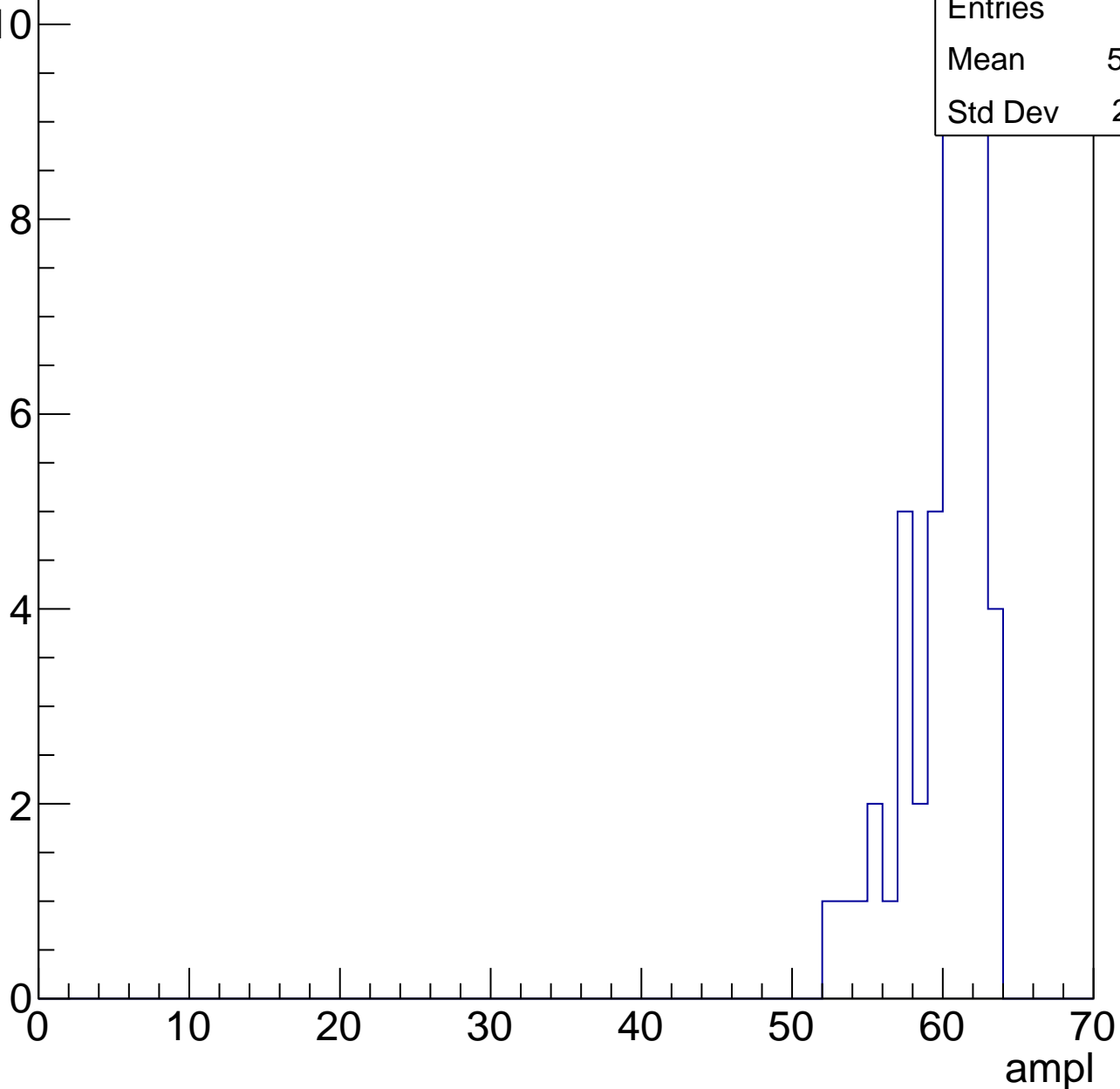


B1L103S, U21-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

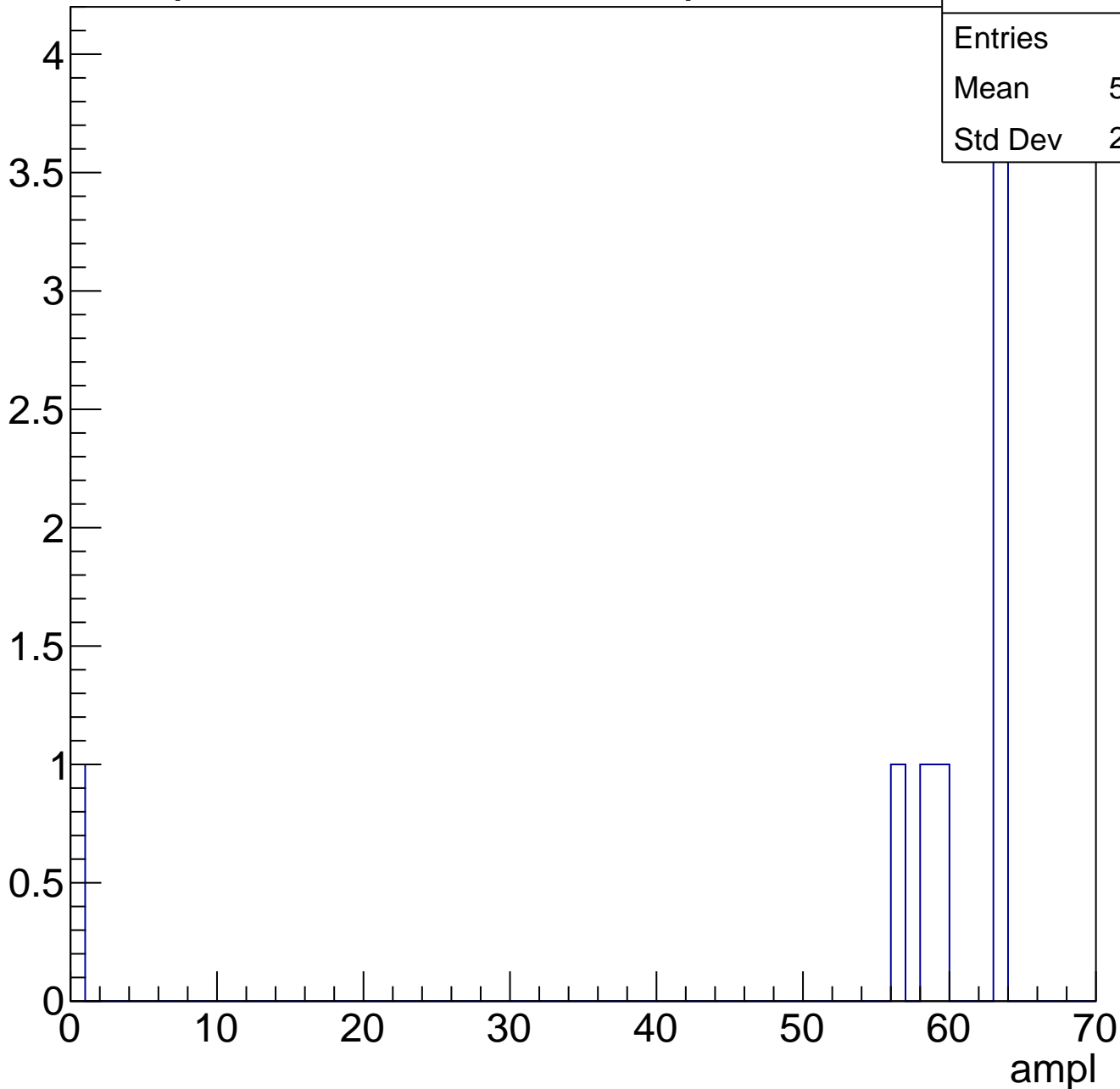
Entries	50
Mean	59.64
Std Dev	2.651



B1L103S, U21-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch1, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	23.18
Std Dev	11.67

Gaus mean : 28.7122

Gaus Width: 3.3996

Entry

12

10

8

6

4

2

0

0

10

20

30

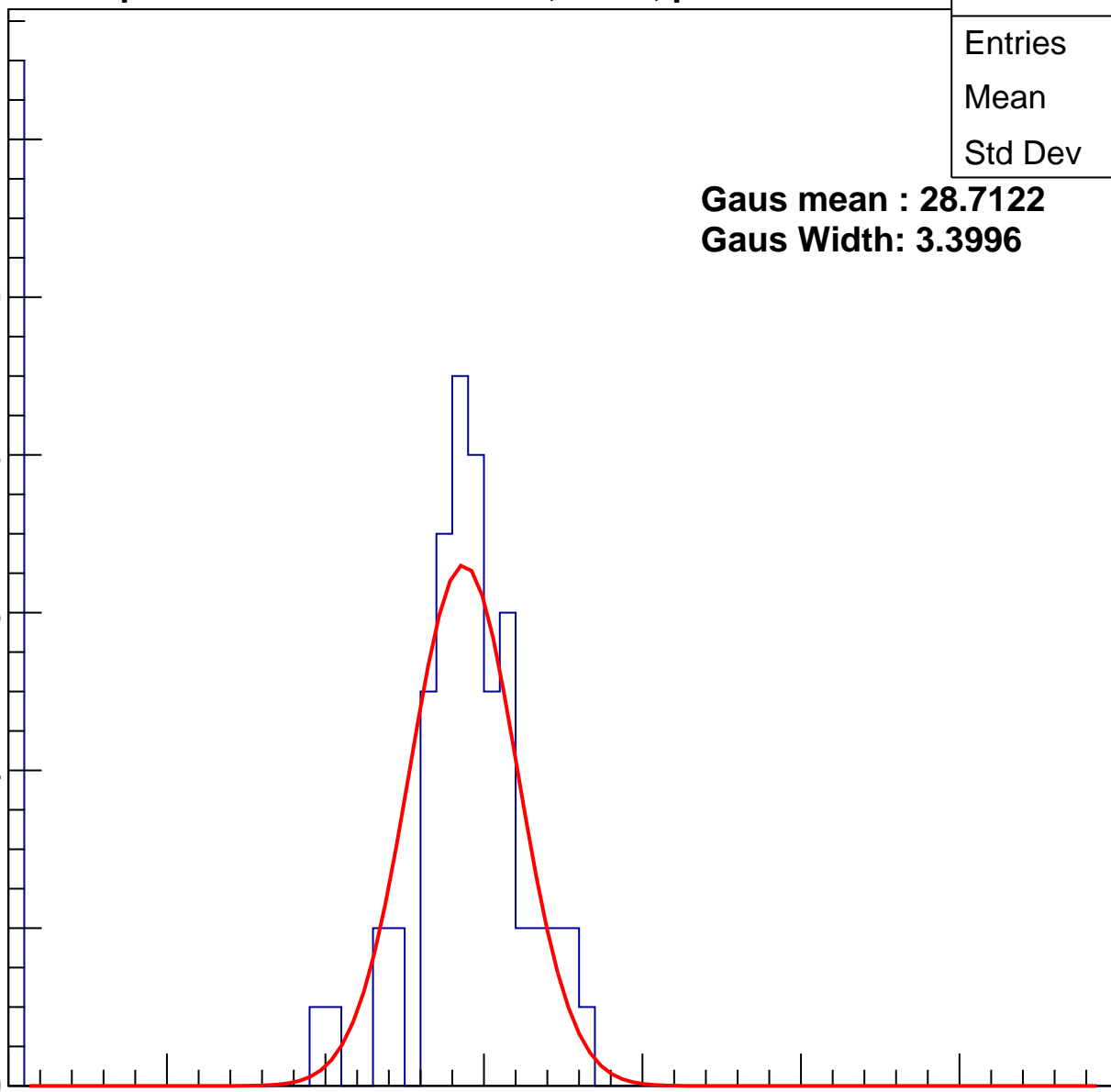
40

50

60

70

ampl



B1L103S, U21-ch1, adc1

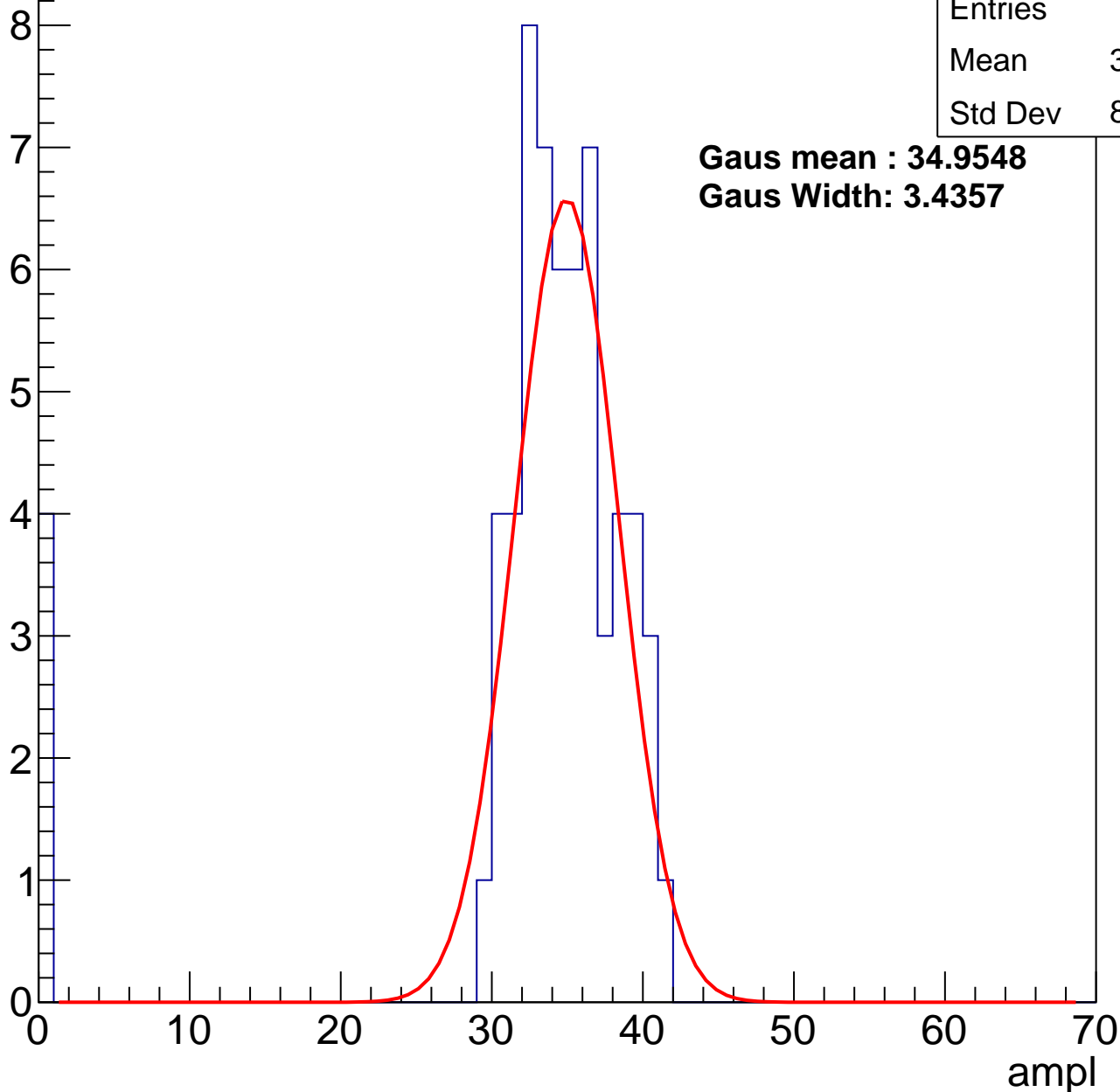
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.35
Std Dev	8.982

Gaus mean : 34.9548

Gaus Width: 3.4357



B1L103S, U21-ch1, adc2

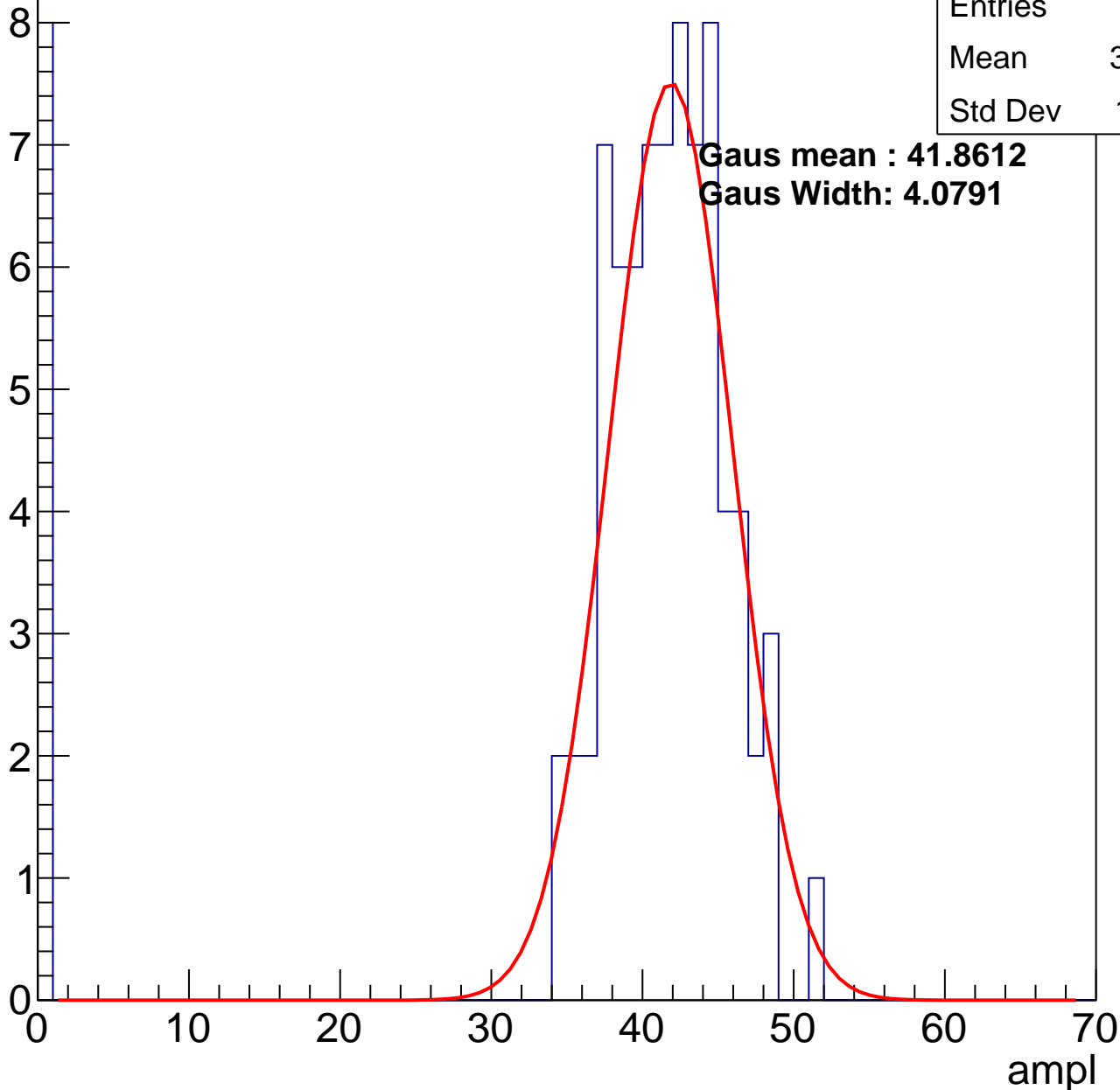
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	37.38
Std Dev	12.61

Gaus mean : 41.8612

Gaus Width: 4.0791

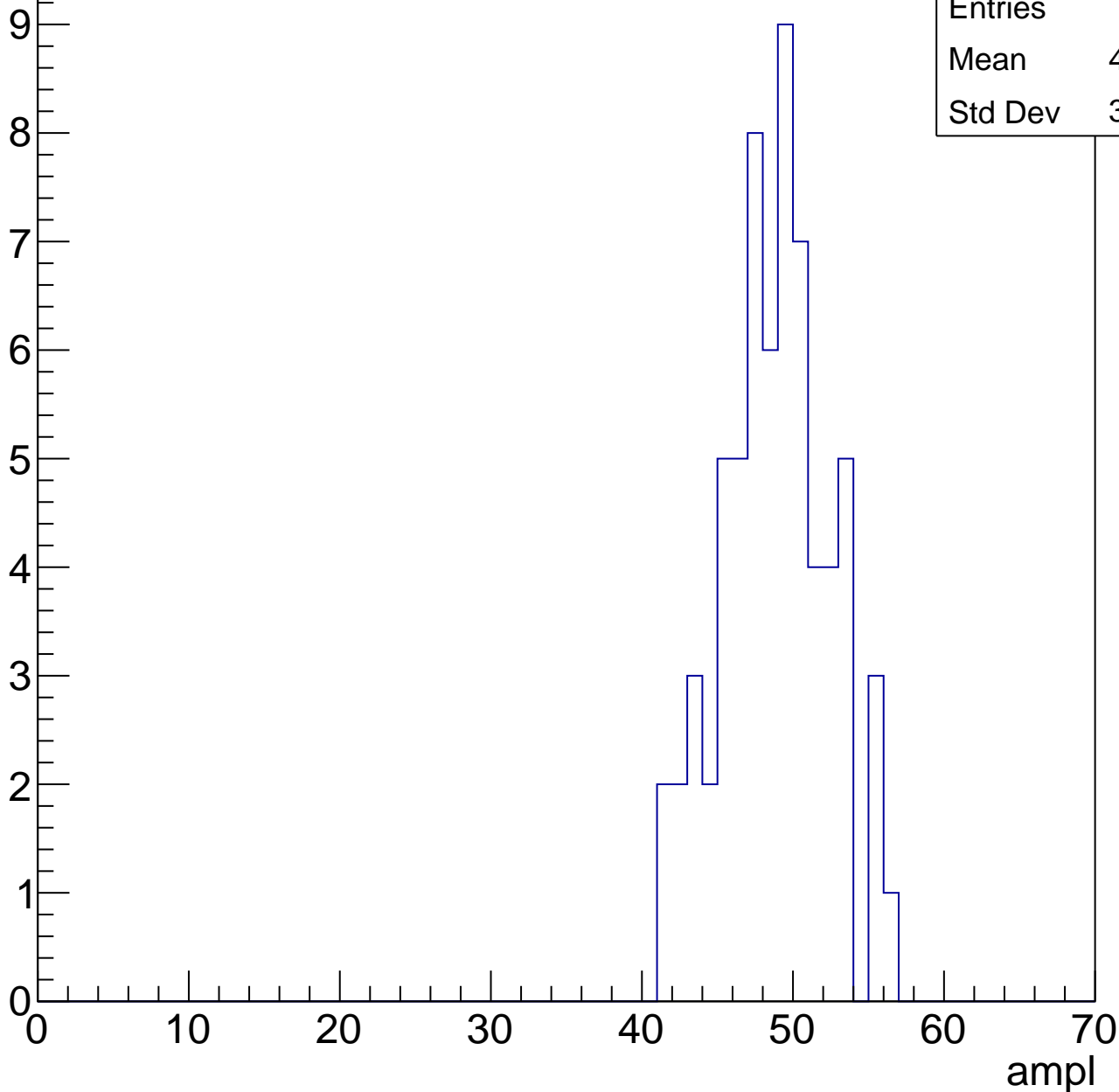


B1L103S, U21-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	48.35
Std Dev	3.514

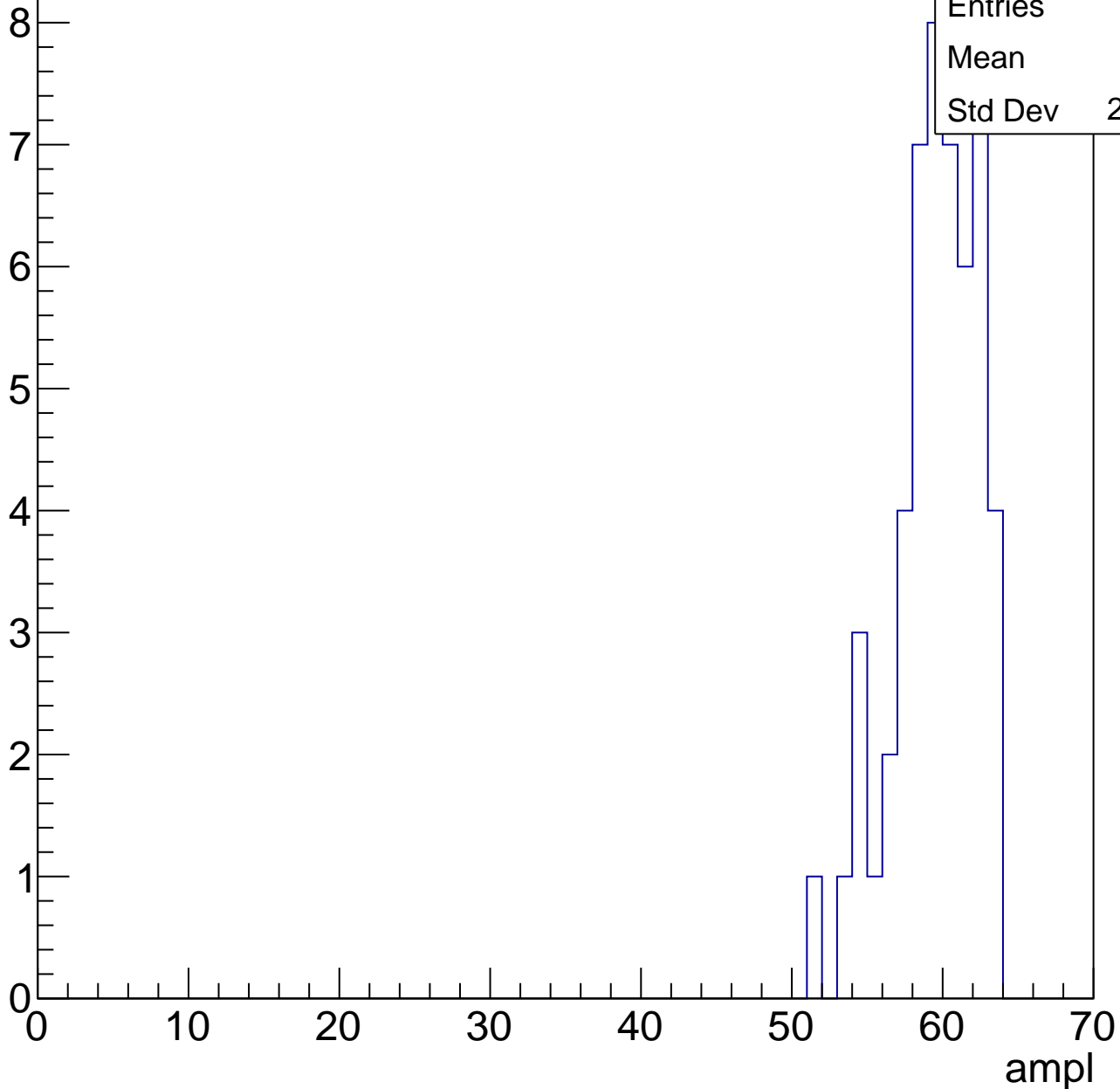


B1L103S, U21-ch1, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	59.1
Std Dev	2.768

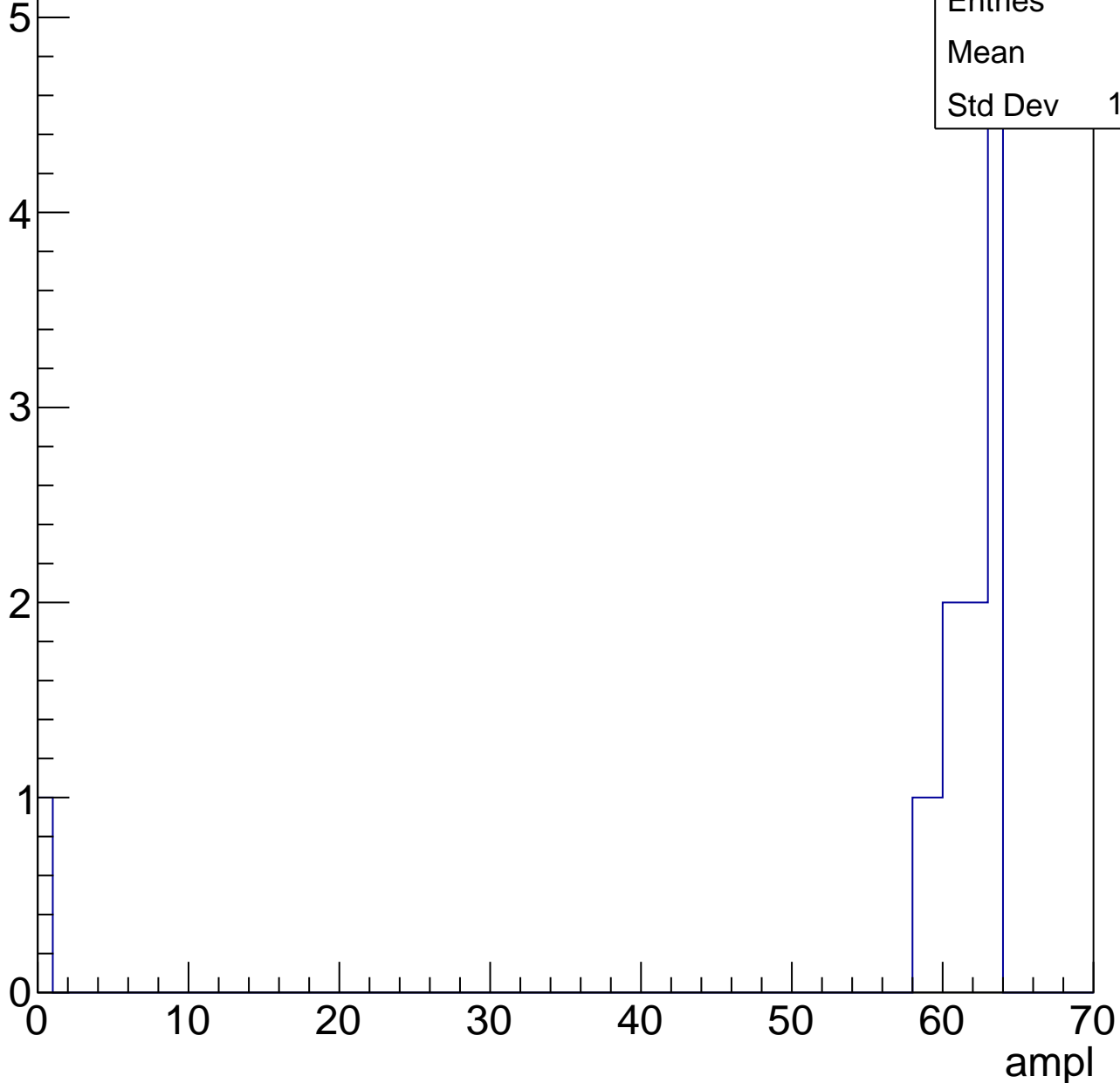


B1L103S, U21-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57
Std Dev	15.89



B1L103S, U21-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U21-ch2, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	23
Std Dev	10.03

Gaus mean : 27.1358

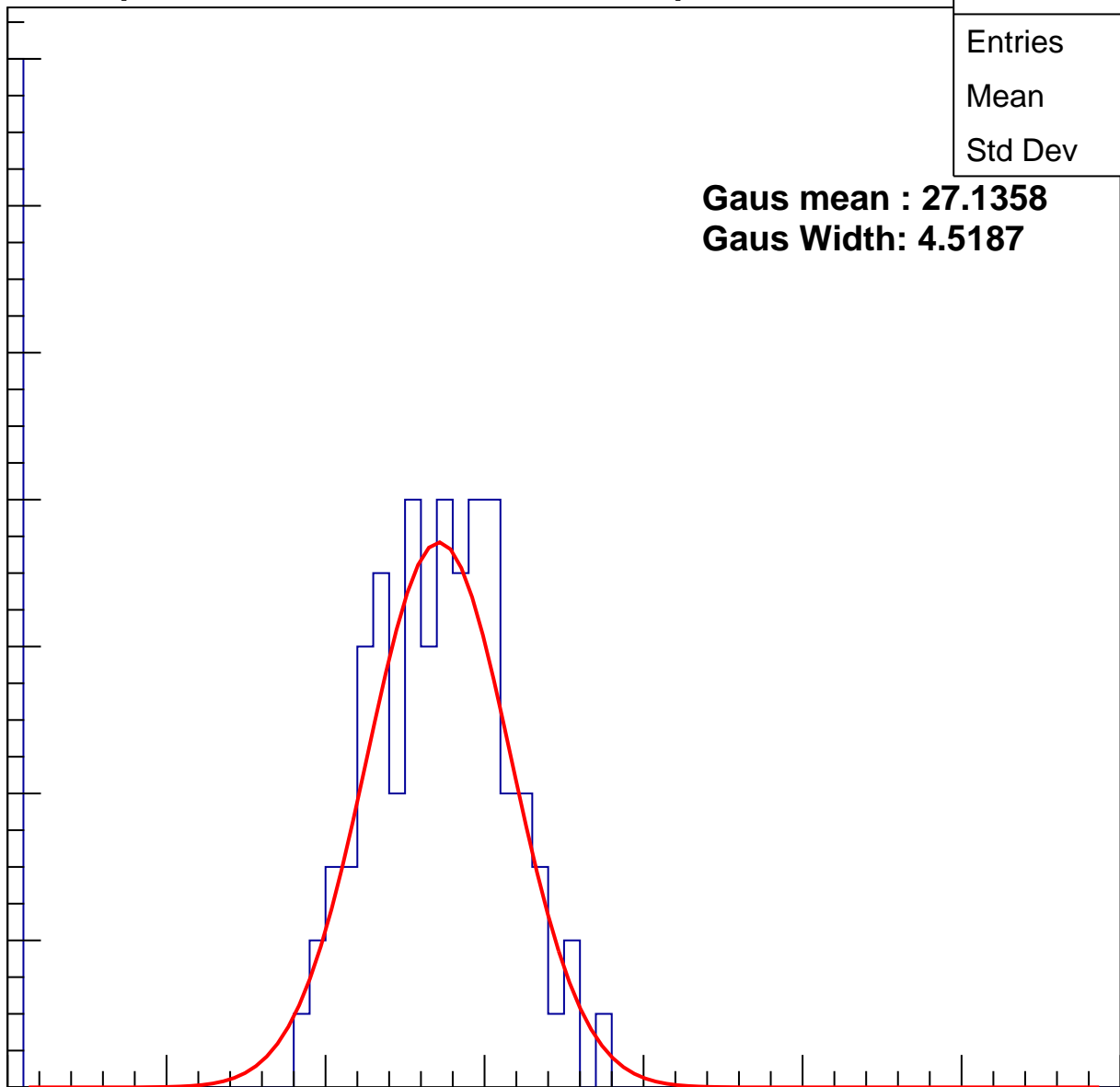
Gaus Width: 4.5187

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch2, adc1

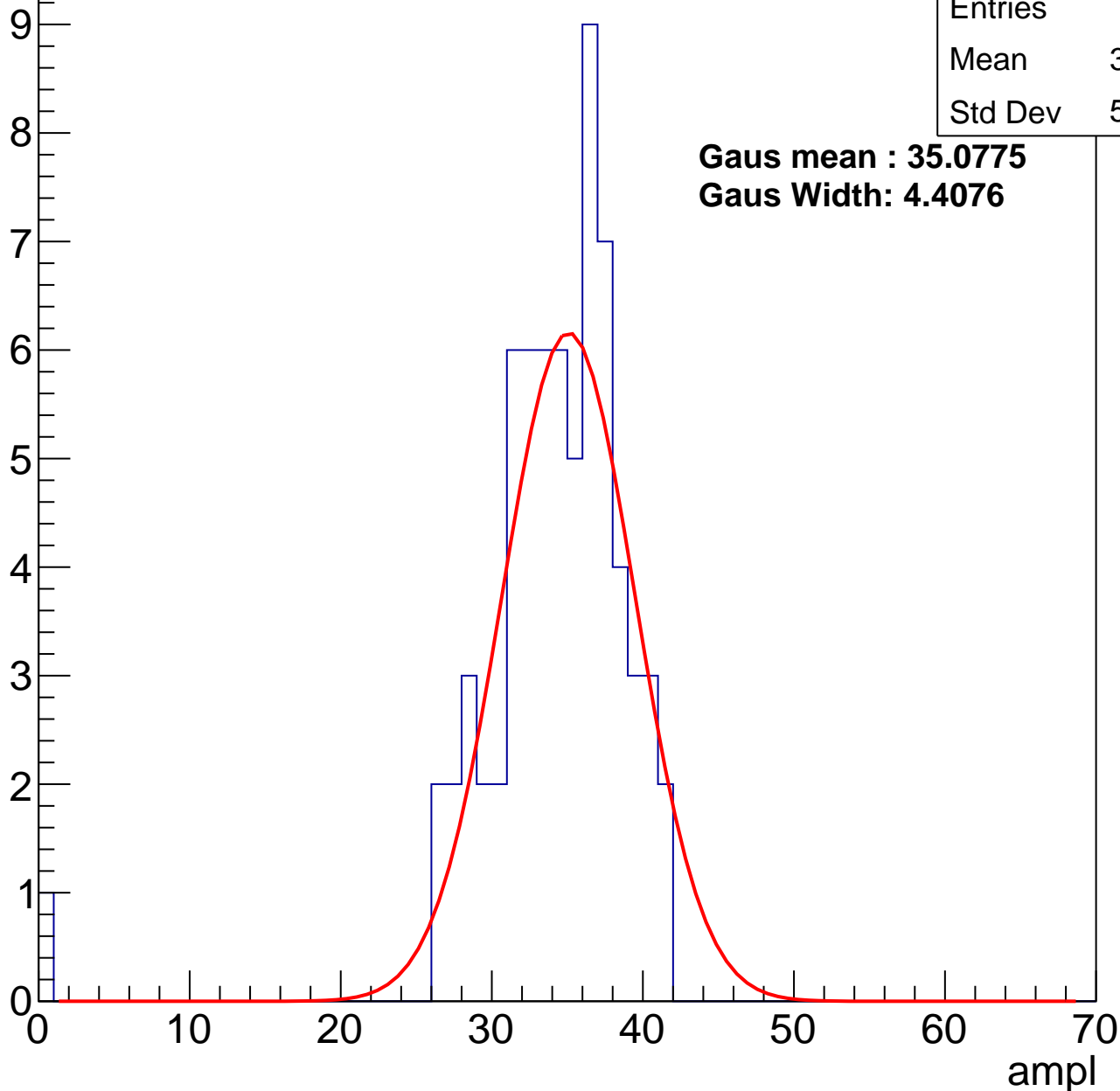
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	33.58
Std Dev	5.507

Gaus mean : 35.0775

Gaus Width: 4.4076



B1L103S, U21-ch2, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	36.74
Std Dev	14.22

Gaus mean : 42.9480

Gaus Width: 4.2267

Entry

10

8

6

4

2

0

0

10

20

30

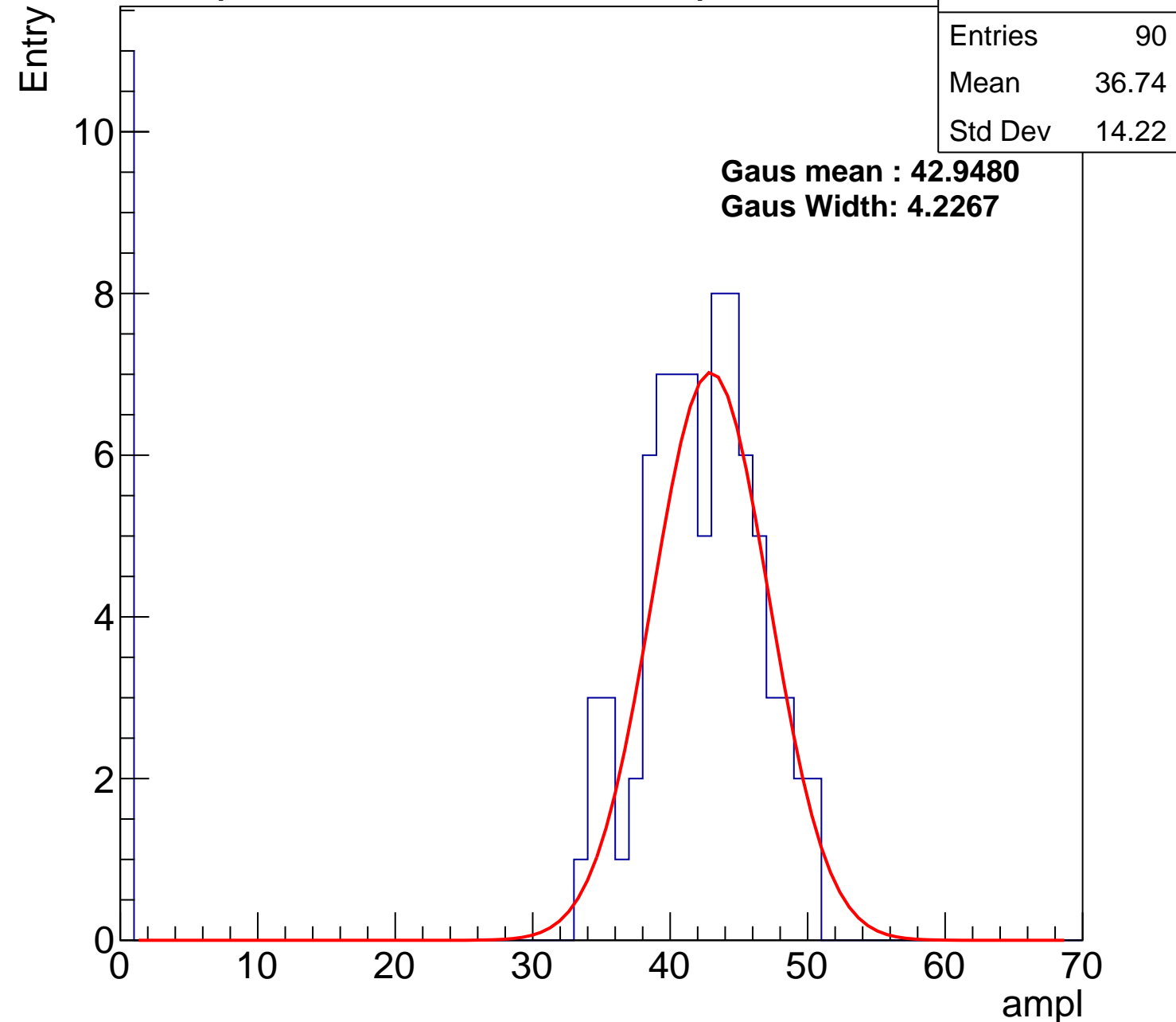
40

50

60

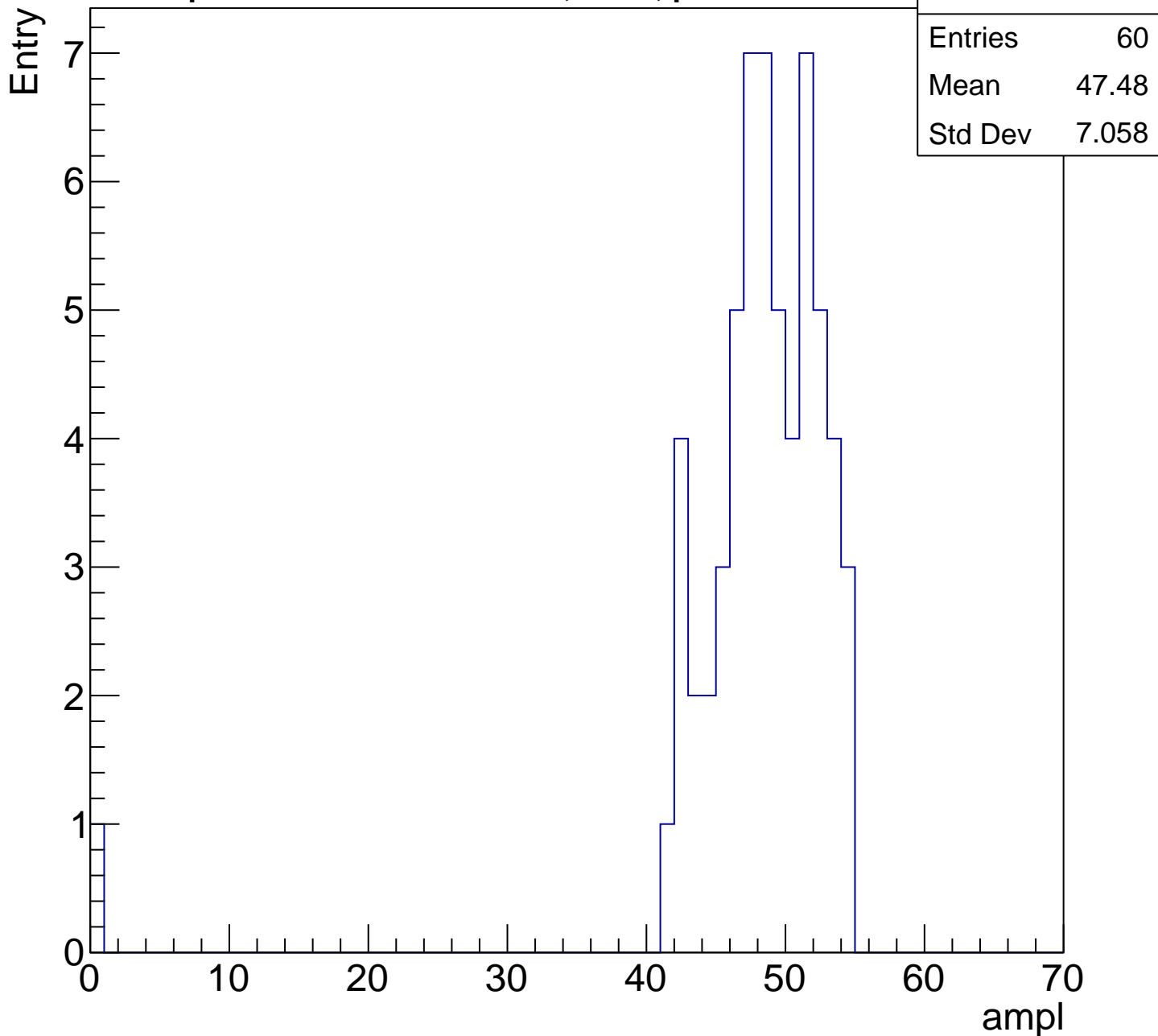
70

ampl



B1L103S, U21-ch2, adc3

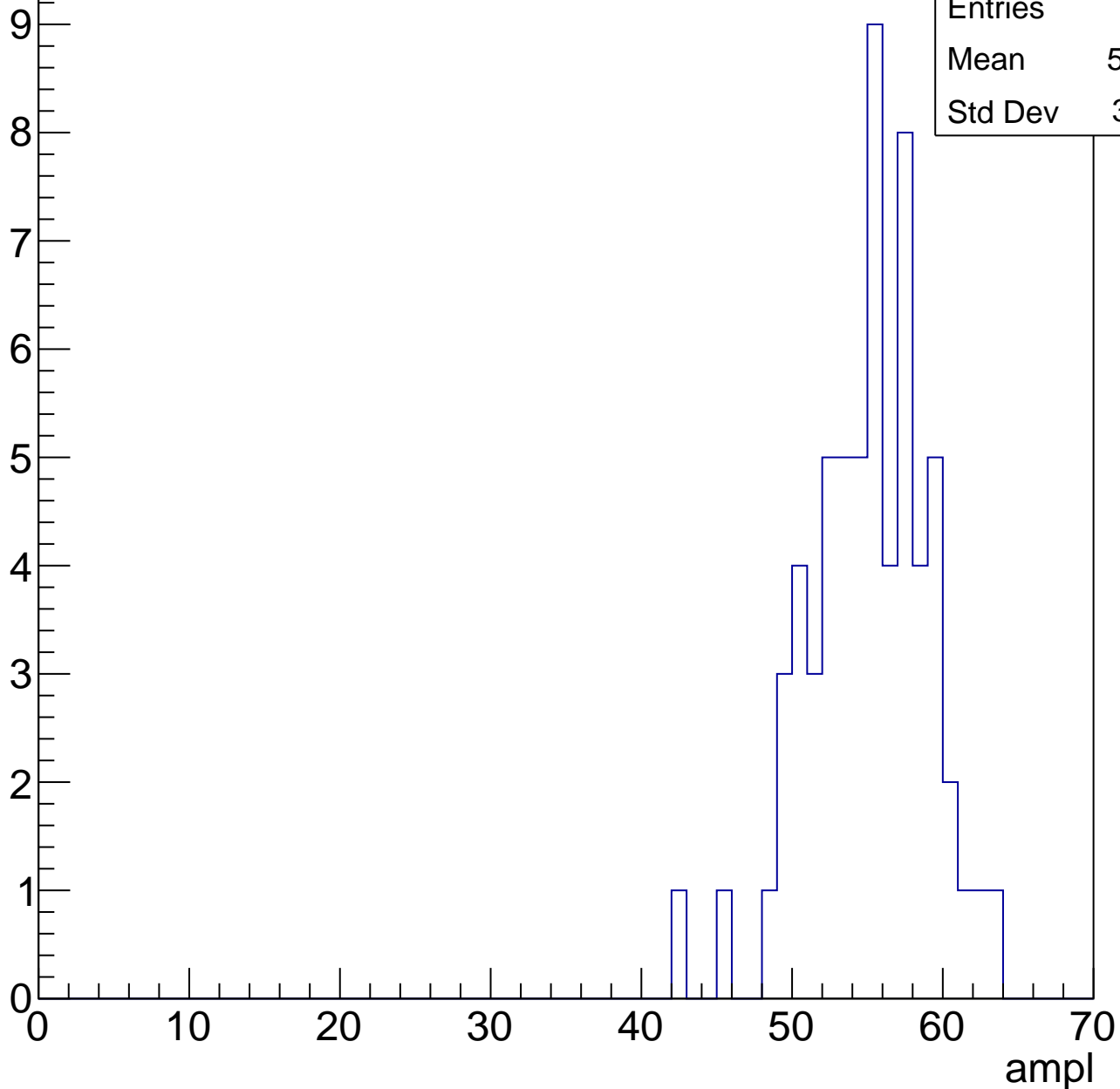
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U21-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

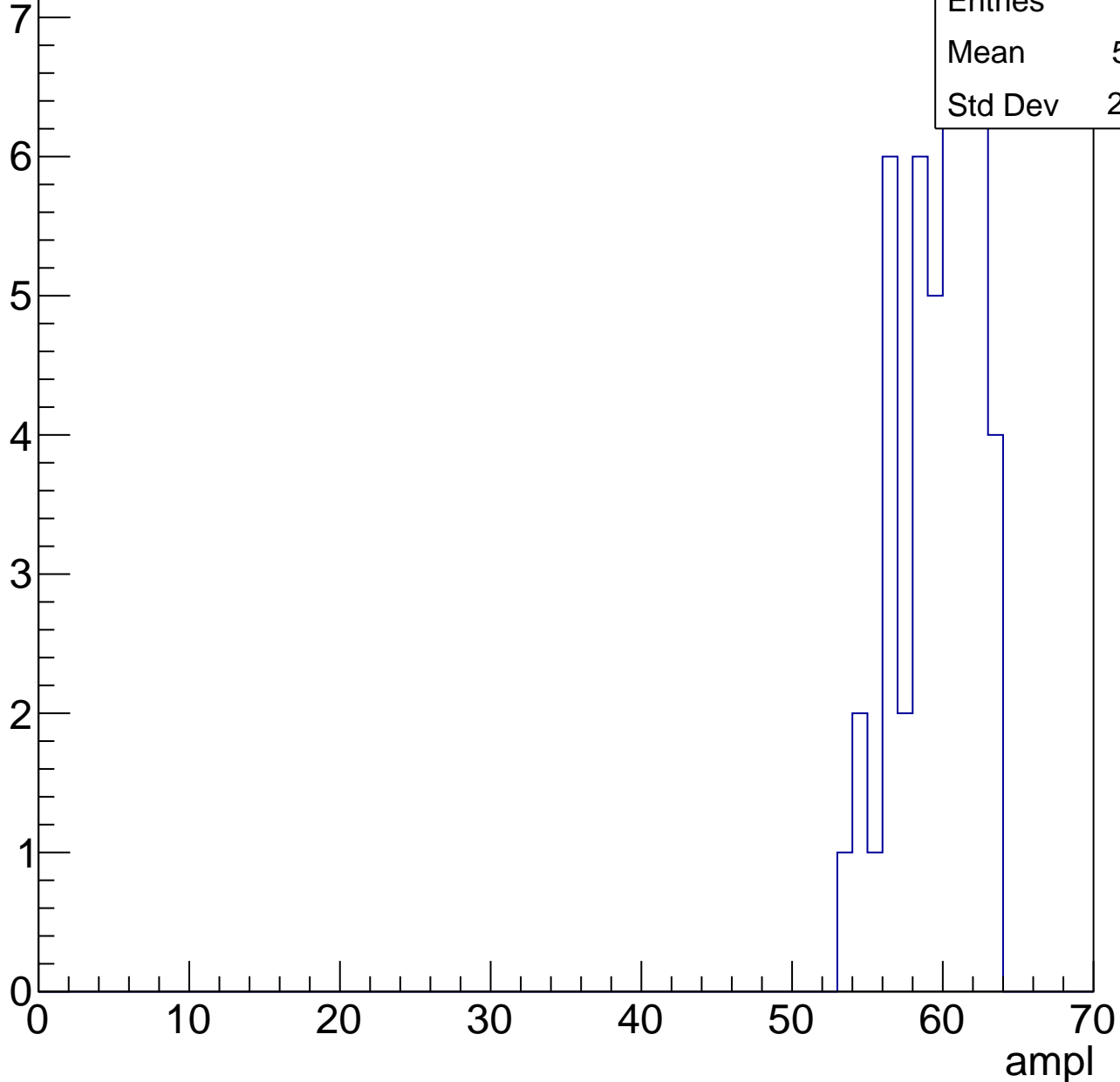
Entry



B1L103S, U21-ch2, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

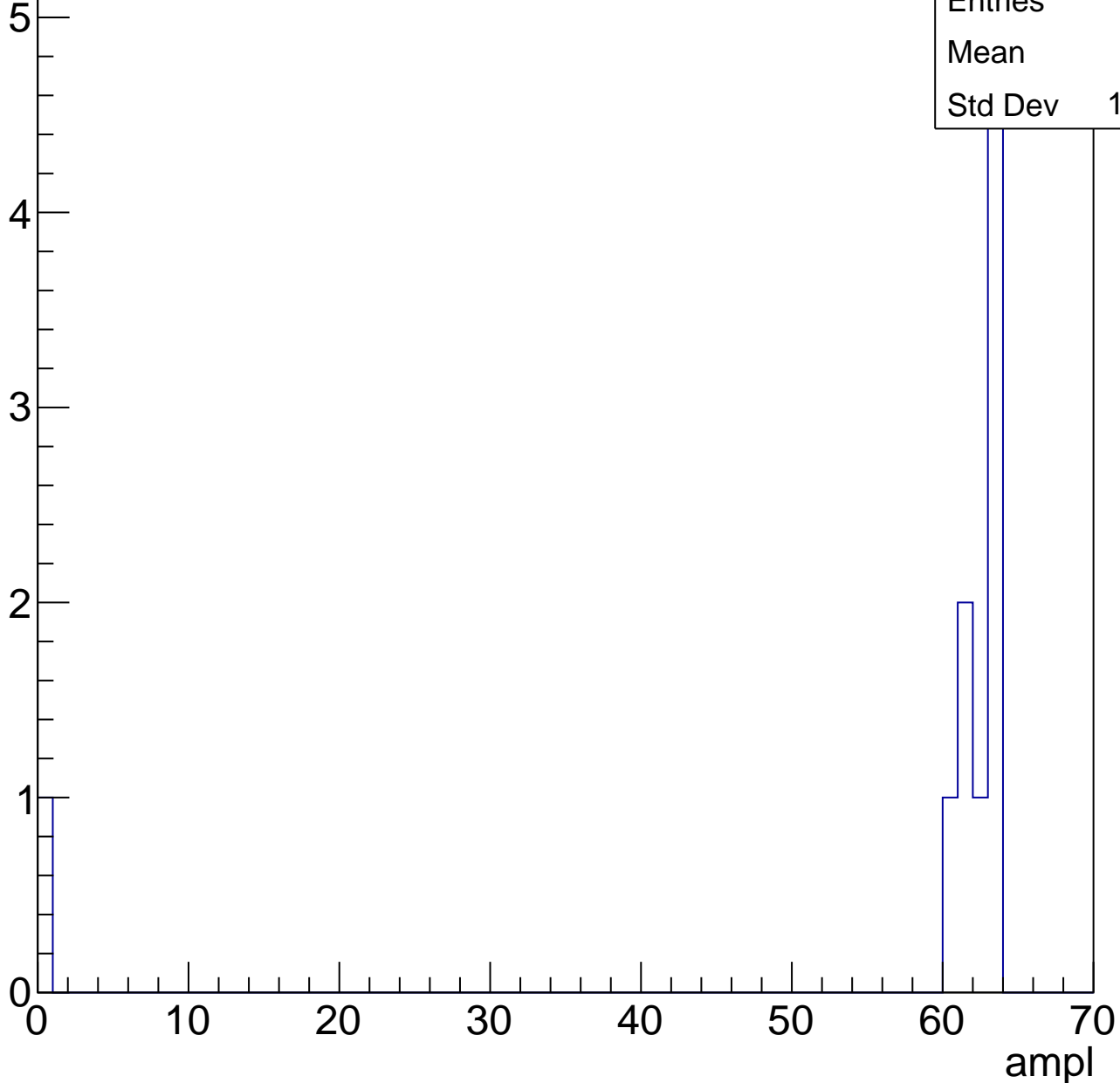


B1L103S, U21-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

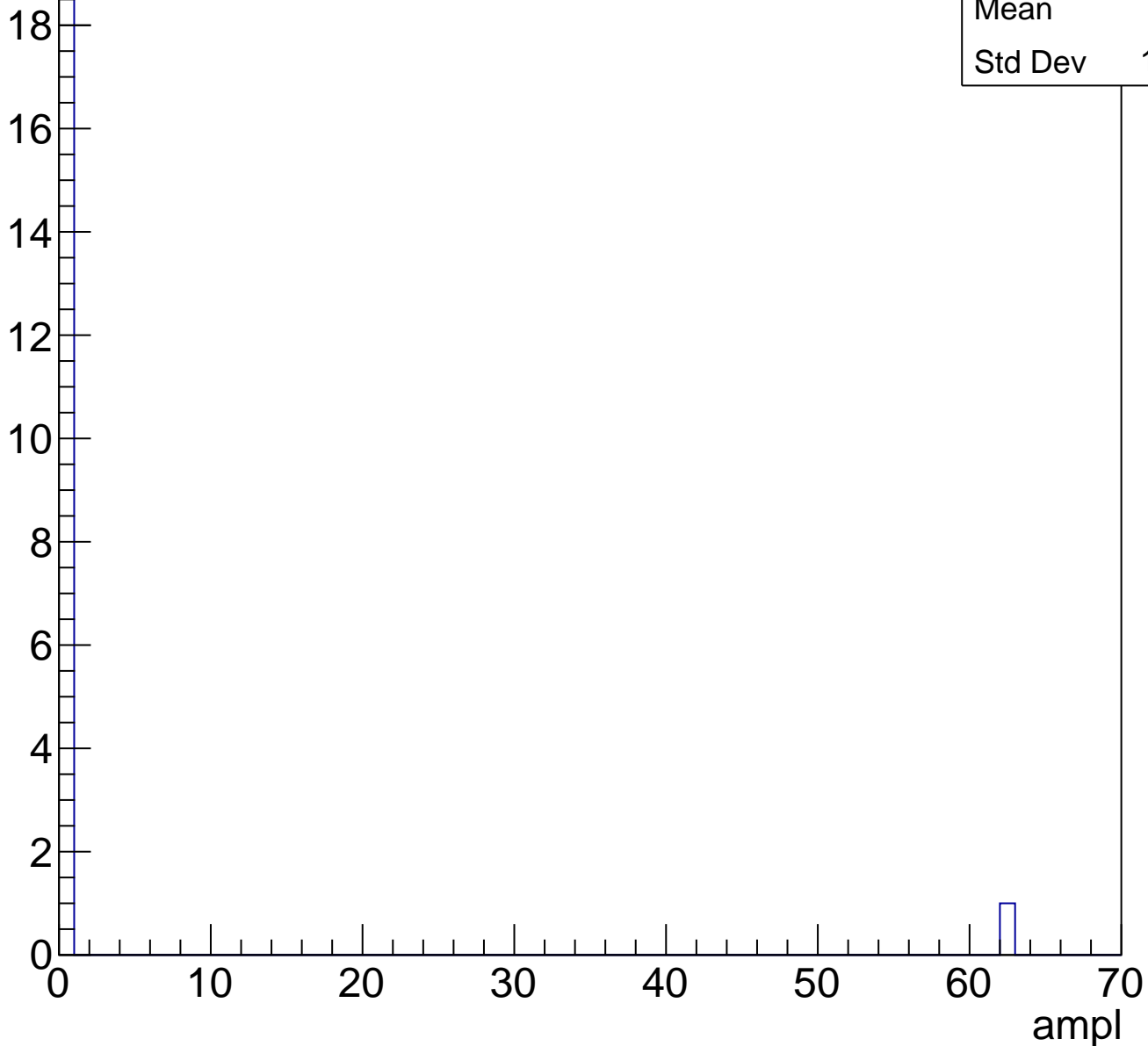
Entries	10
Mean	55.9
Std Dev	18.66



B1L103S, U21-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.1
Std Dev	13.51

B1L103S, U21-ch3, adc0

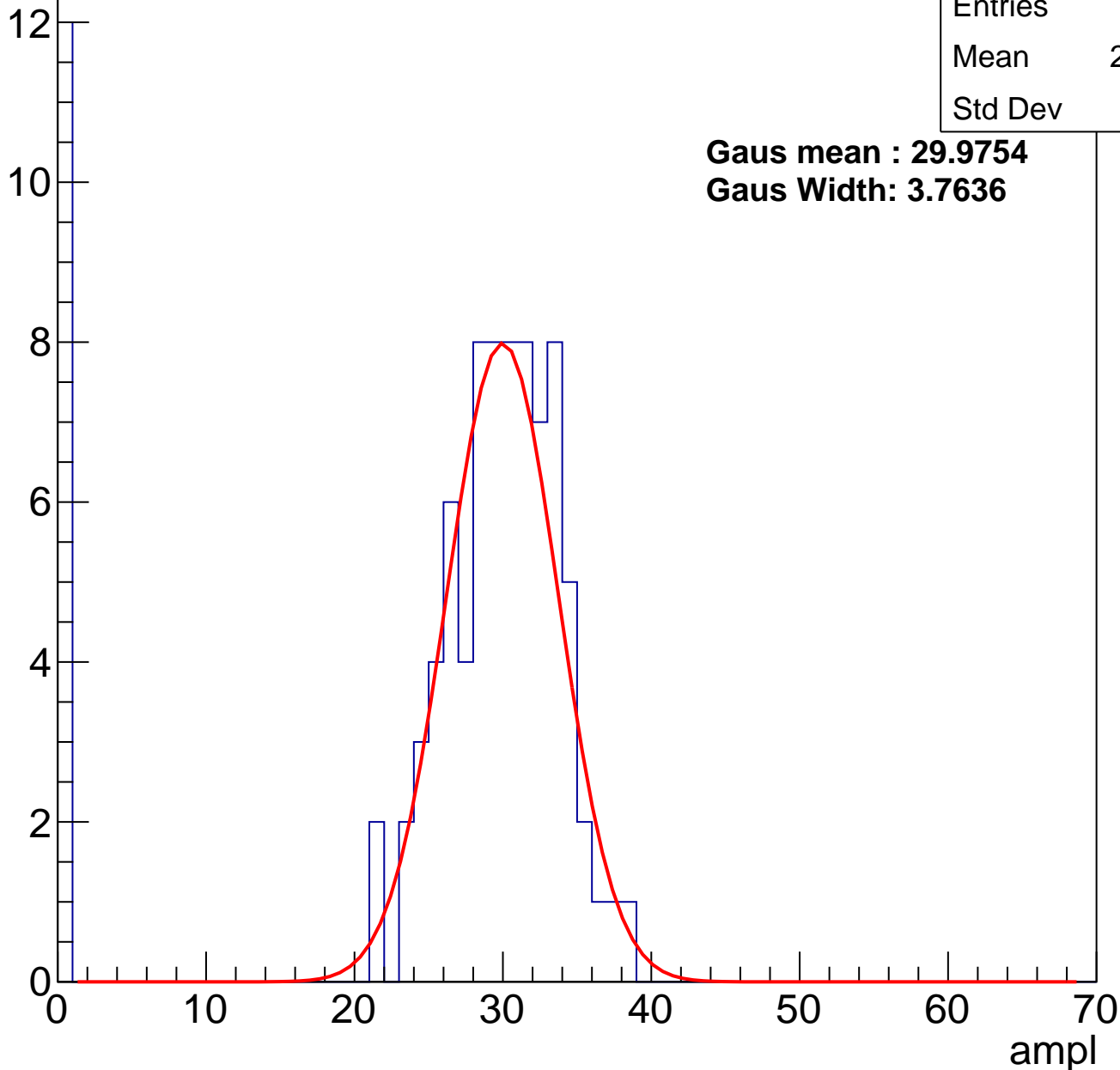
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	25.63
Std Dev	10.6

Gaus mean : 29.9754

Gaus Width: 3.7636

Entry



B1L103S, U21-ch3, adc1

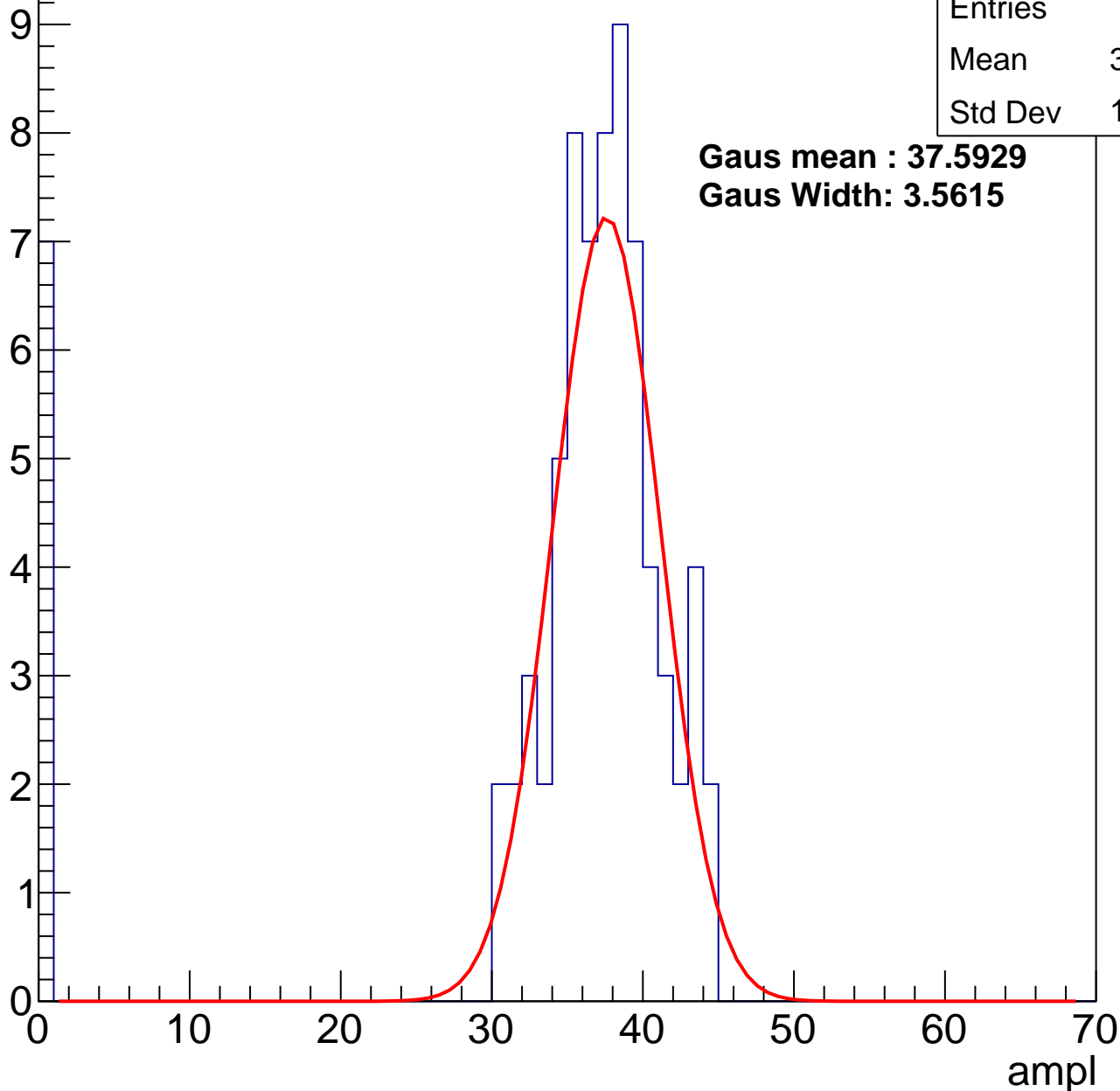
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	33.65
Std Dev	11.26

Gaus mean : 37.5929

Gaus Width: 3.5615



B1L103S, U21-ch3, adc2

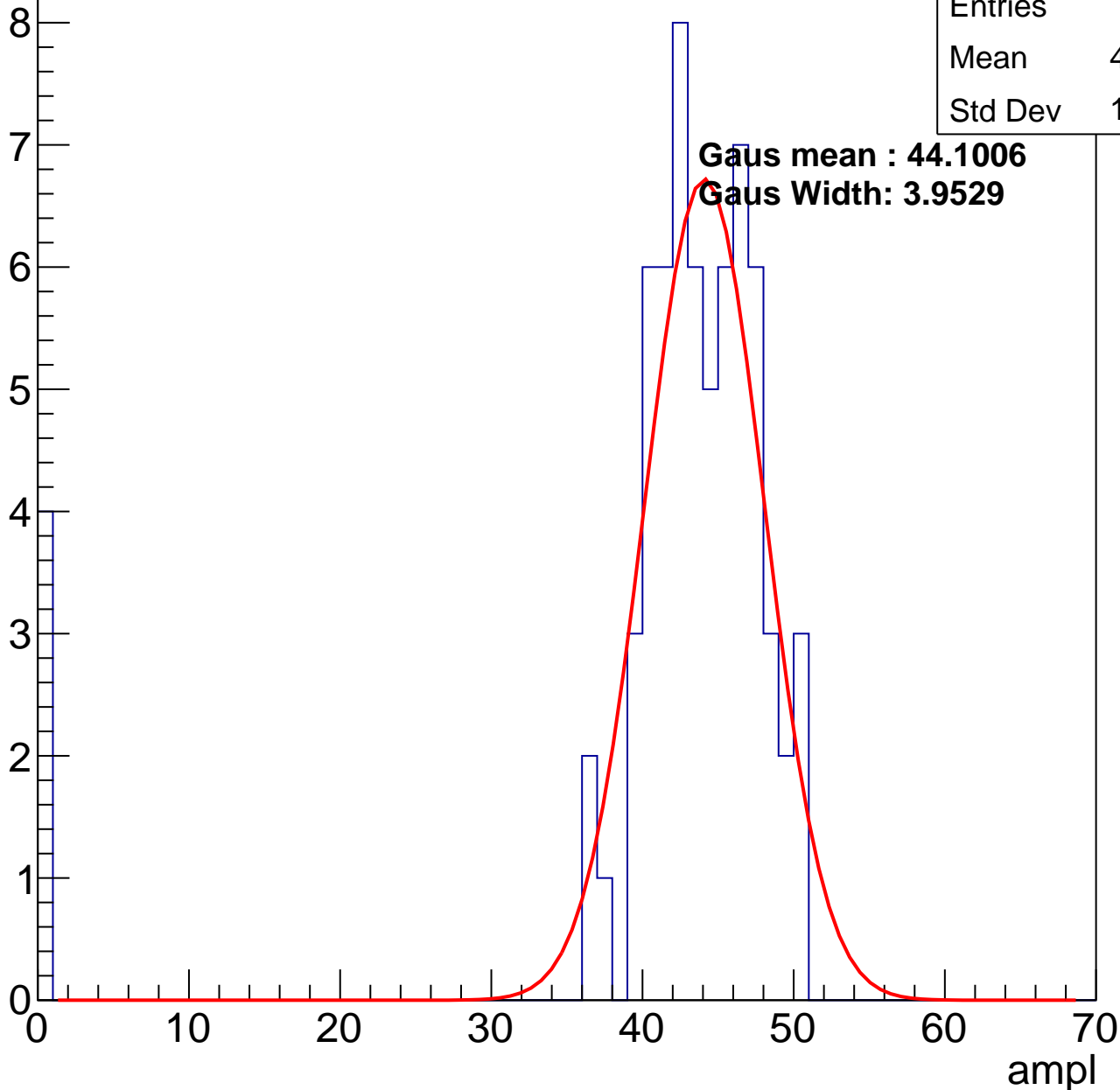
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.06
Std Dev	10.77

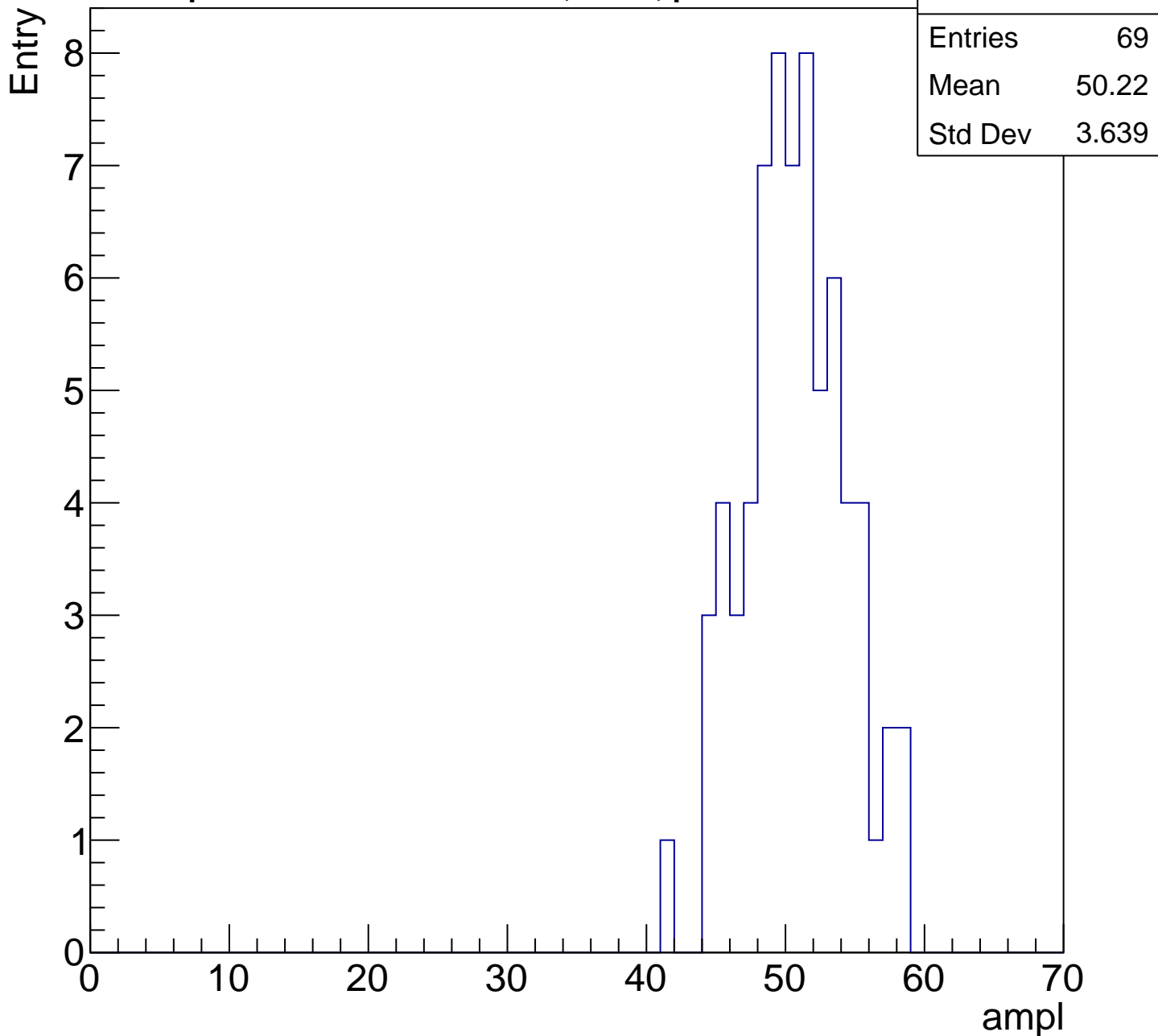
Gaus mean : 44.1006

Gaus Width: 3.9529



B1L103S, U21-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

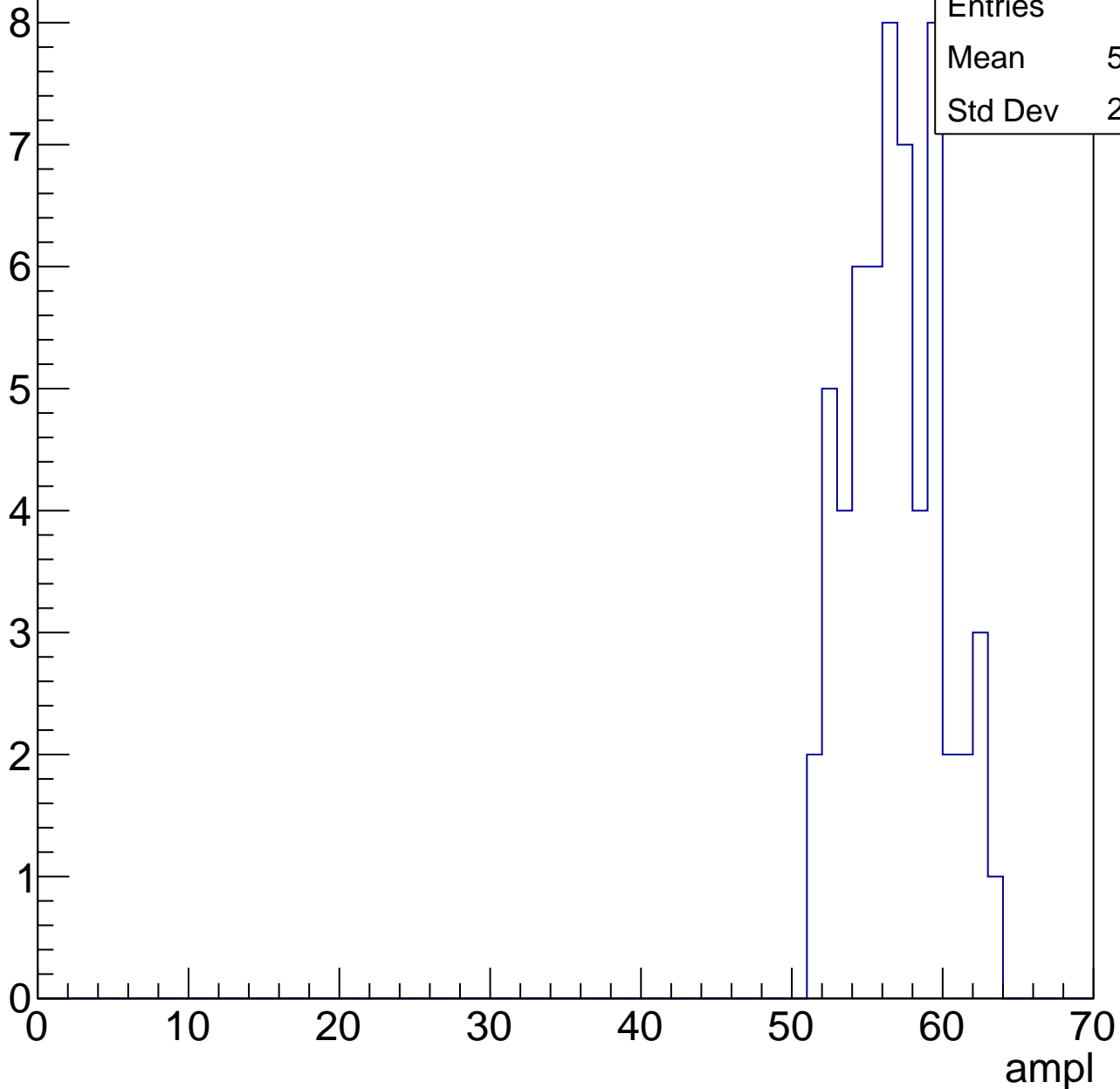


B1L103S, U21-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.38
Std Dev	2.999



B1L103S, U21-ch3, adc5

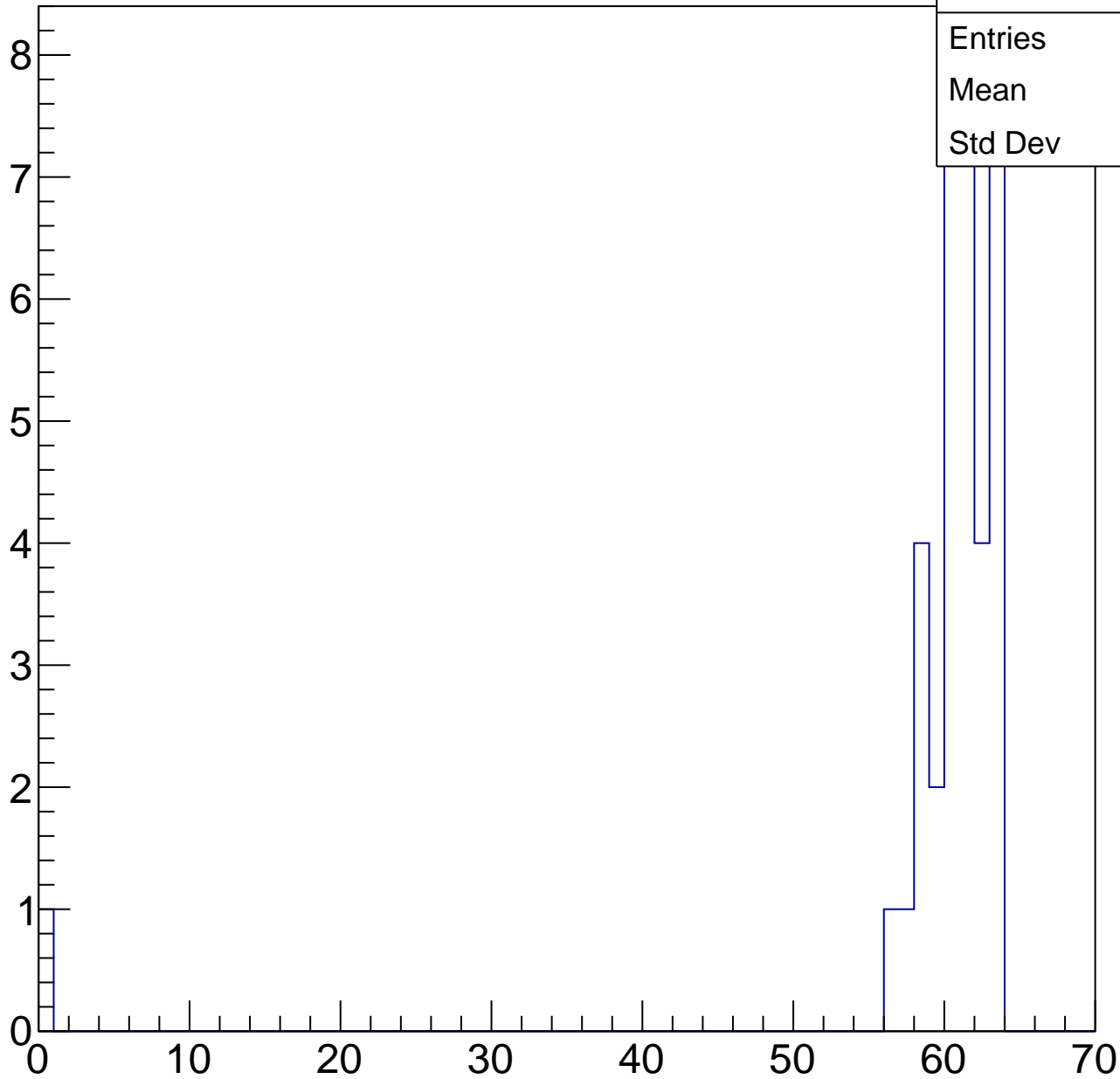
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	59
Std Dev	10

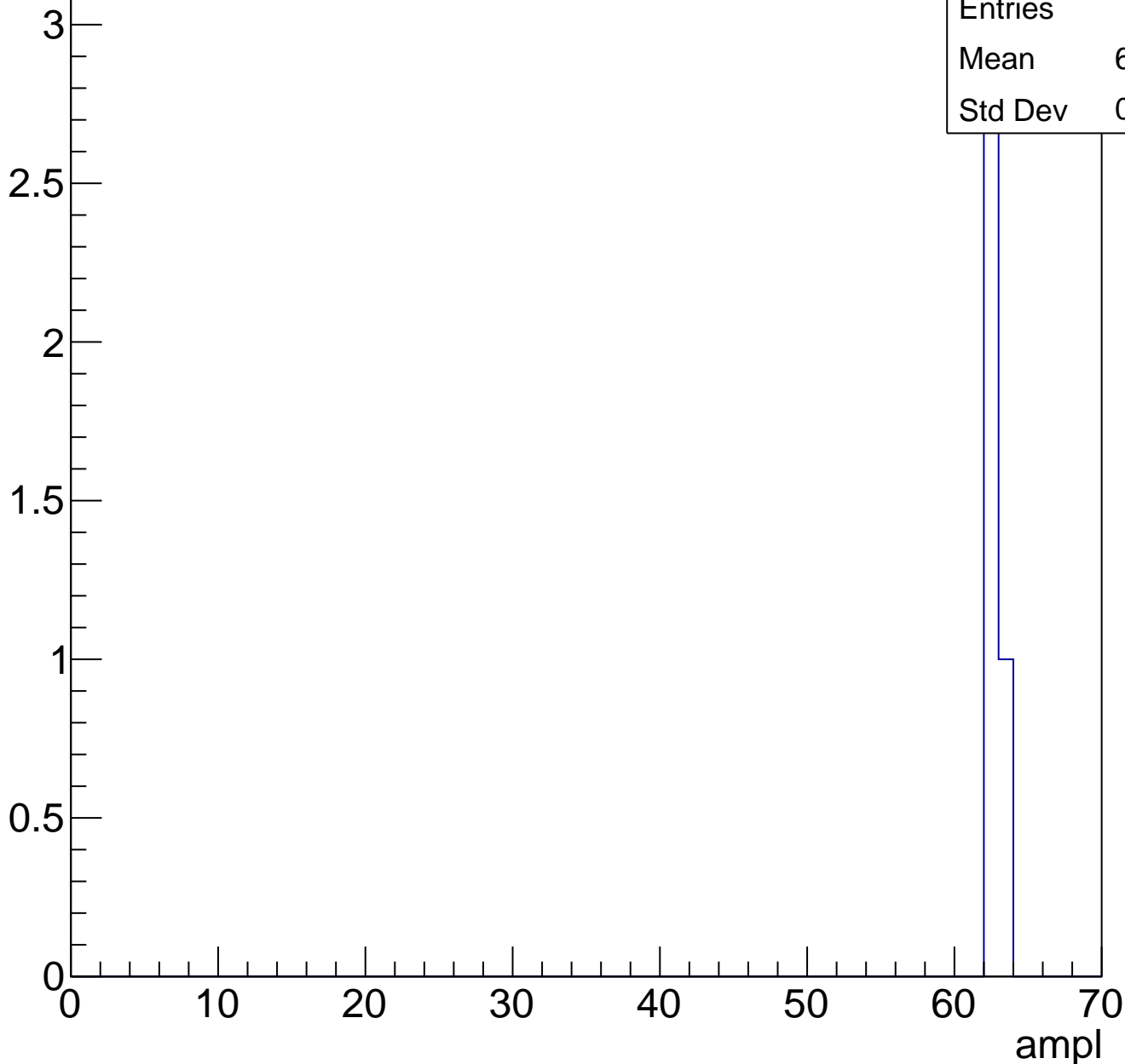
ampl



B1L103S, U21-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch4, adc0

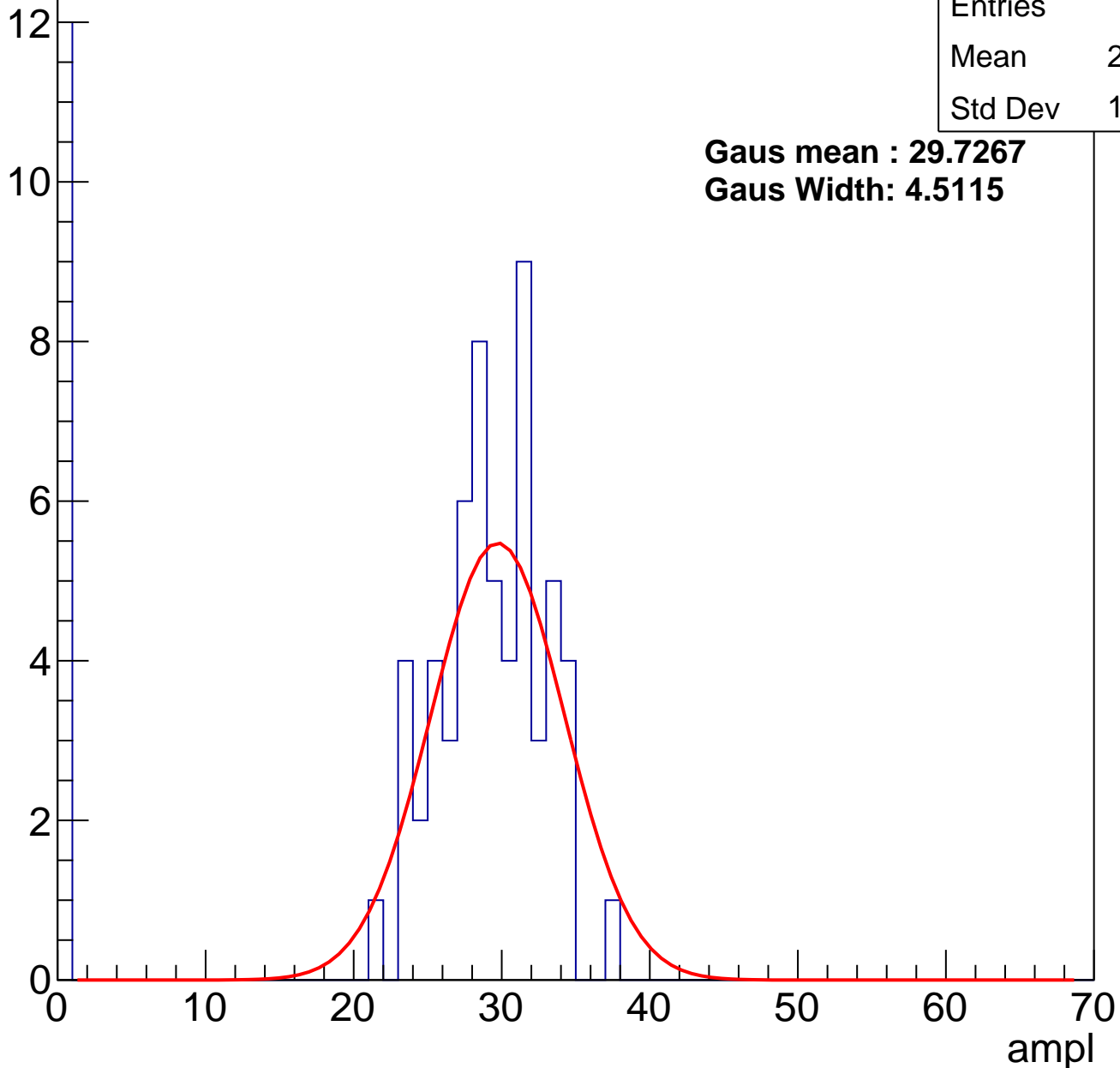
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	23.99
Std Dev	11.26

Gaus mean : 29.7267

Gaus Width: 4.5115

Entry



B1L103S, U21-ch4, adc1

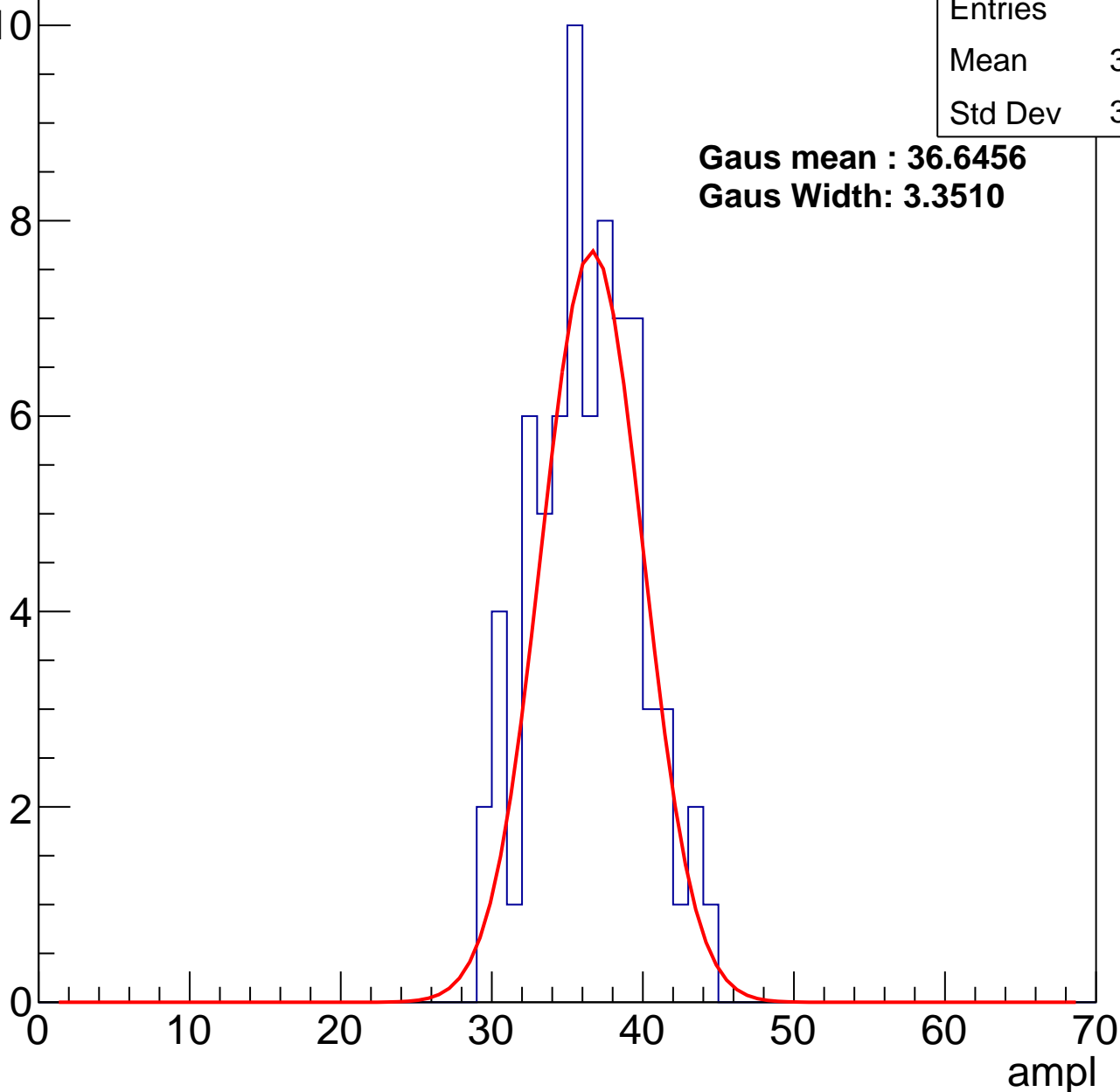
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.92
Std Dev	3.459

Gaus mean : 36.6456

Gaus Width: 3.3510



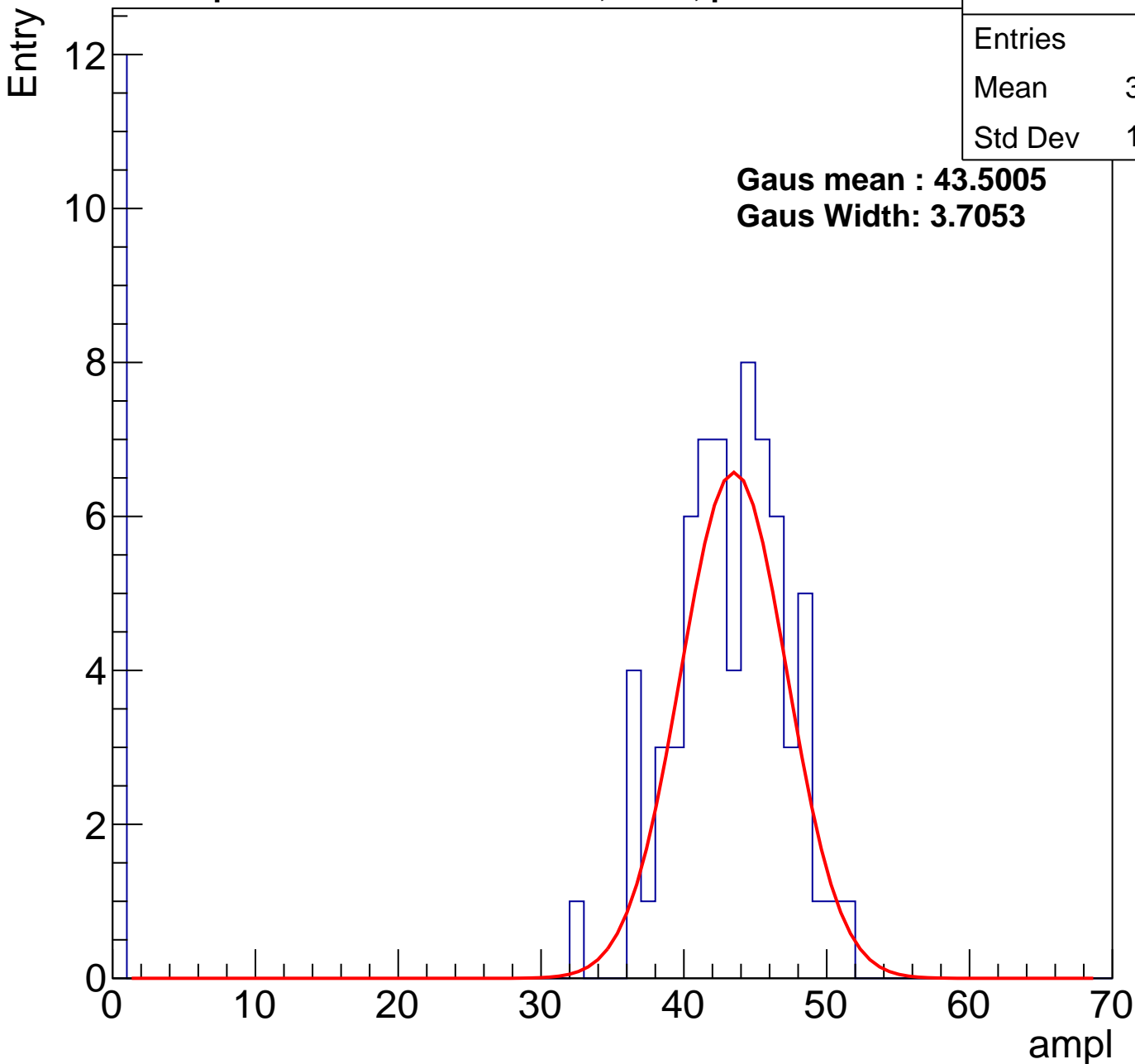
B1L103S, U21-ch4, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	36.39
Std Dev	15.68

Gaus mean : 43.5005

Gaus Width: 3.7053

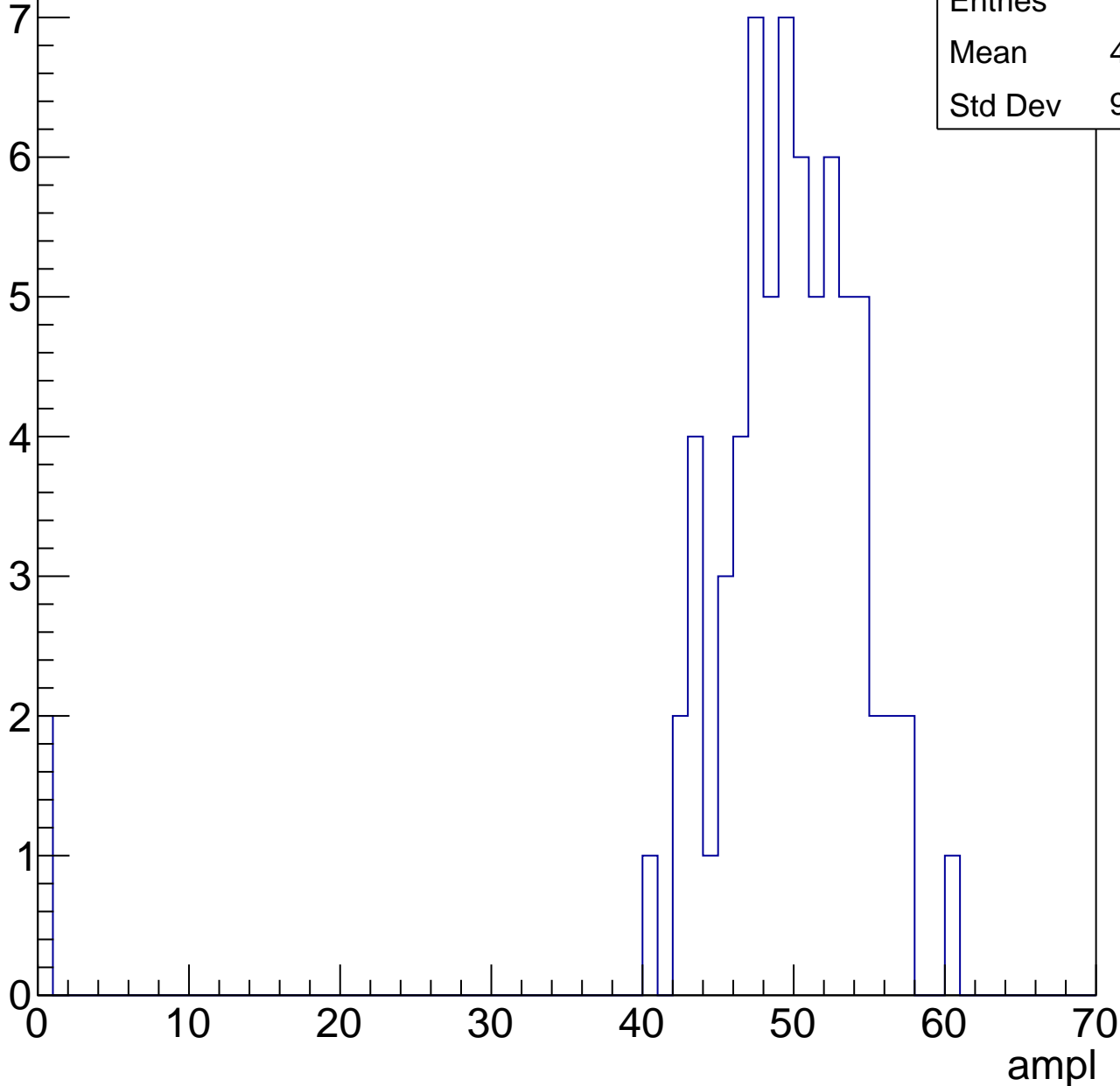


B1L103S, U21-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	48.13
Std Dev	9.195

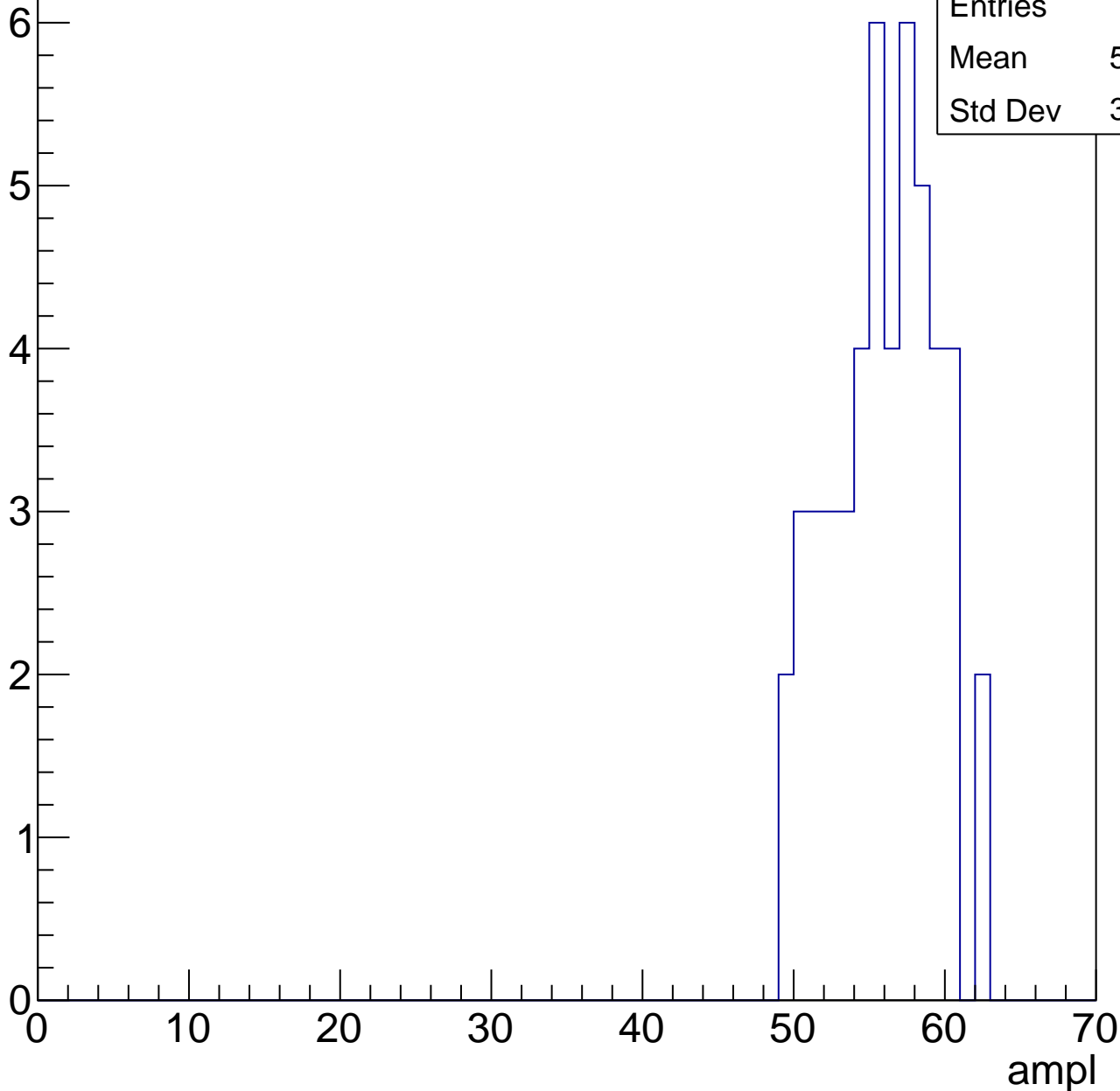


B1L103S, U21-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.47
Std Dev	3.387

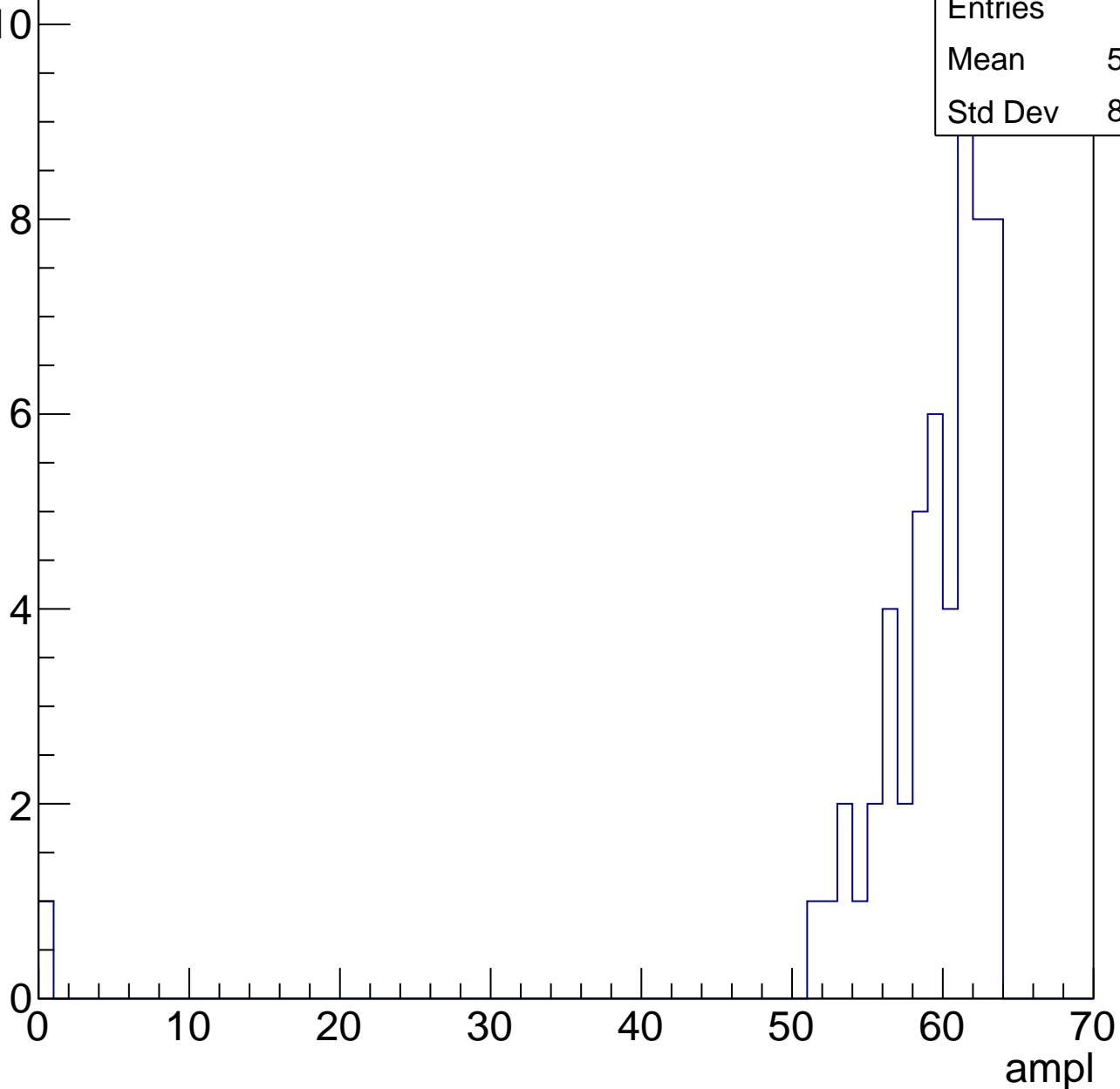


B1L103S, U21-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	58.27
Std Dev	8.514



B1L103S, U21-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch5, adc0

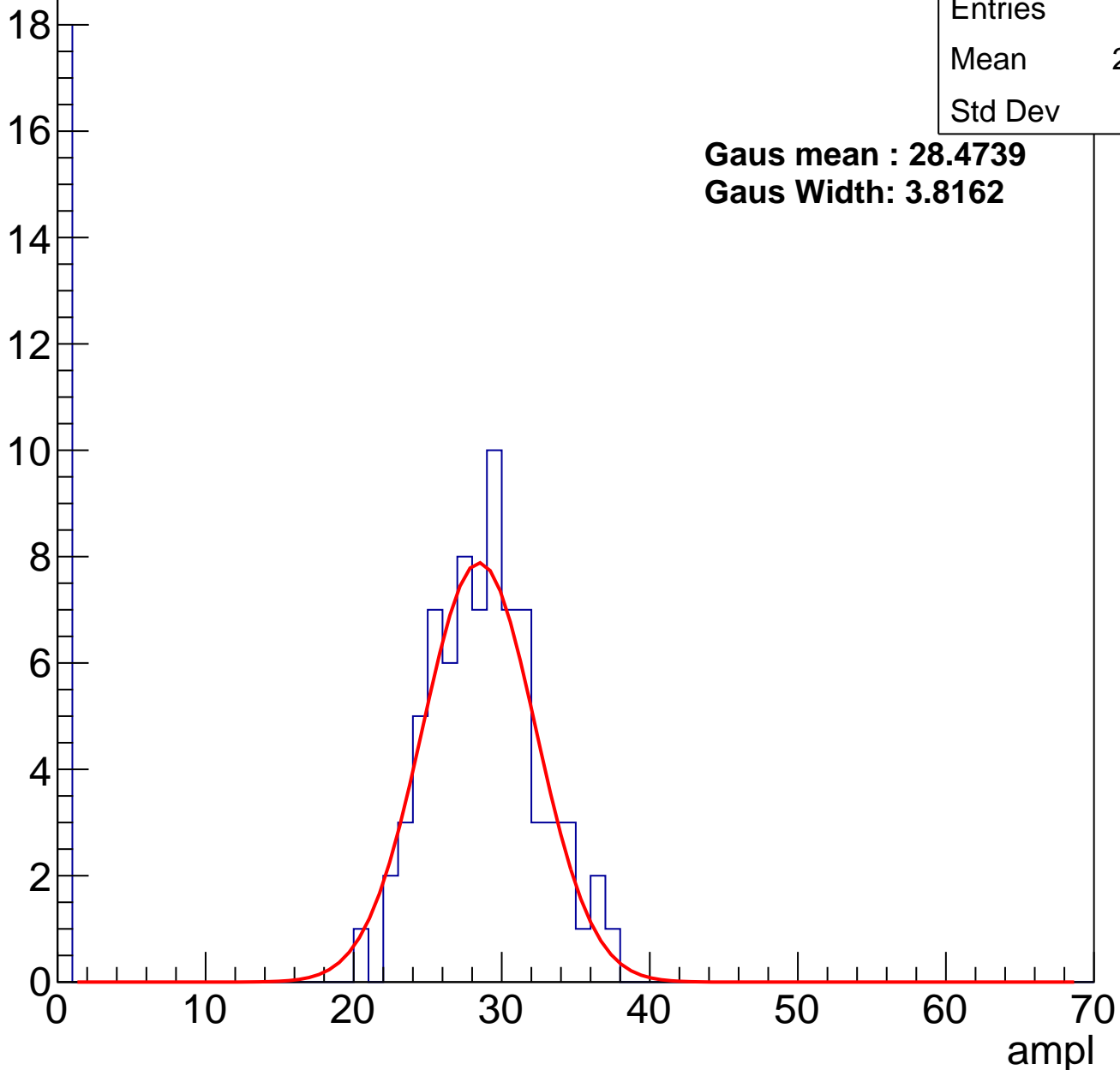
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	22.91
Std Dev	11.6

Gaus mean : 28.4739

Gaus Width: 3.8162

Entry



B1L103S, U21-ch5, adc1

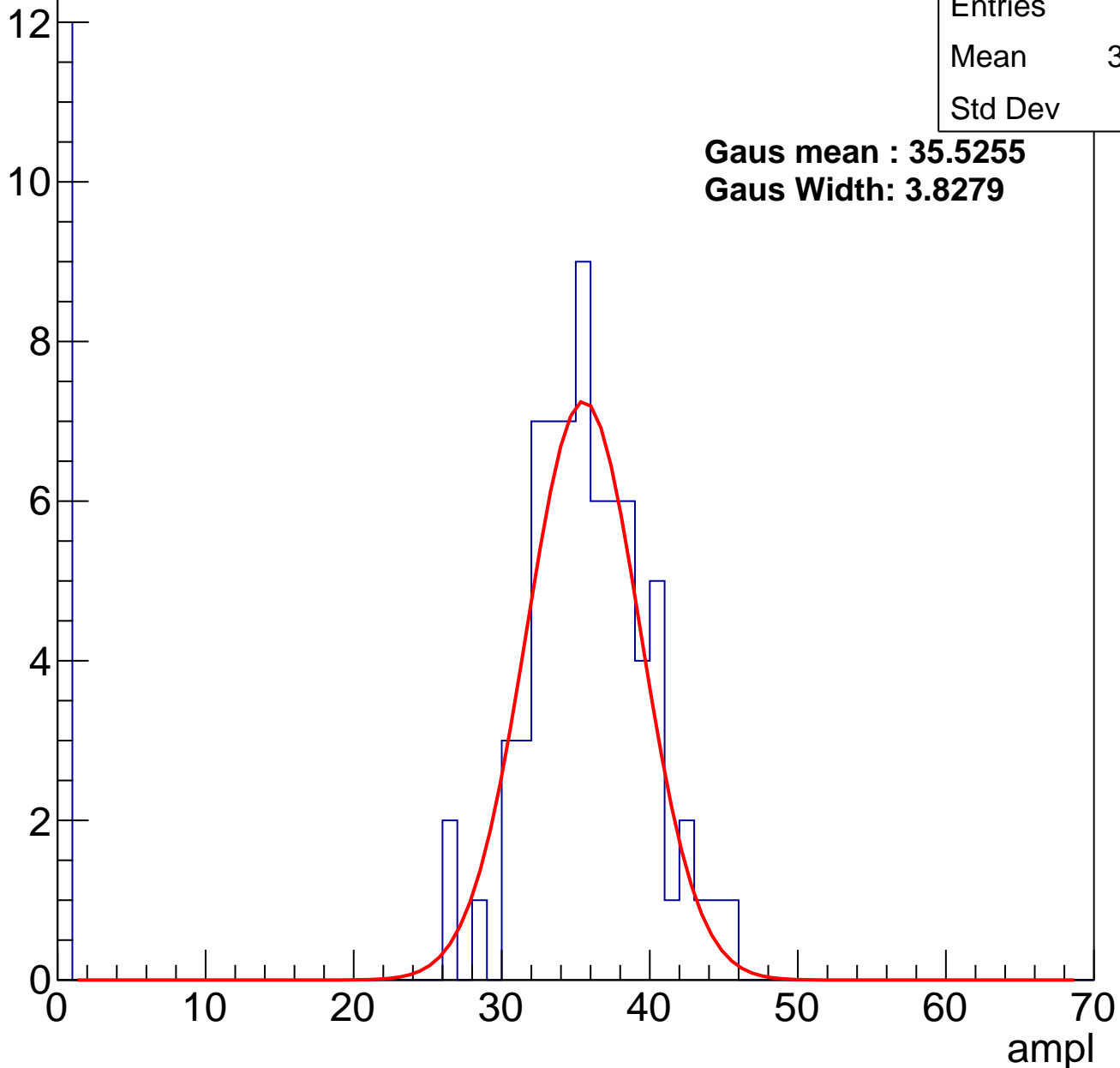
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	30.36
Std Dev	12.9

Gaus mean : 35.5255

Gaus Width: 3.8279

Entry



B1L103S, U21-ch5, adc2

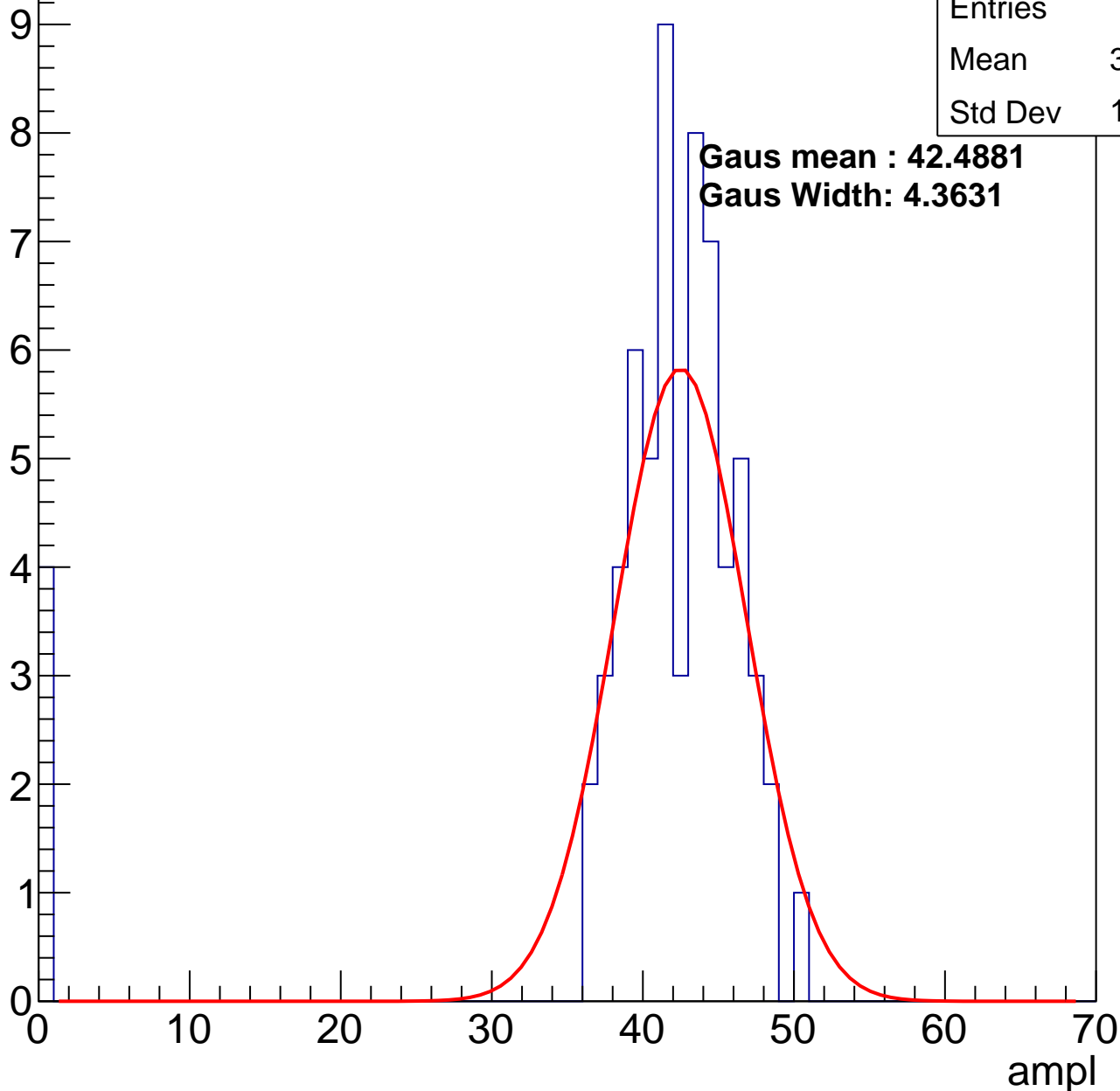
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	39.59
Std Dev	10.54

Gaus mean : 42.4881

Gaus Width: 4.3631

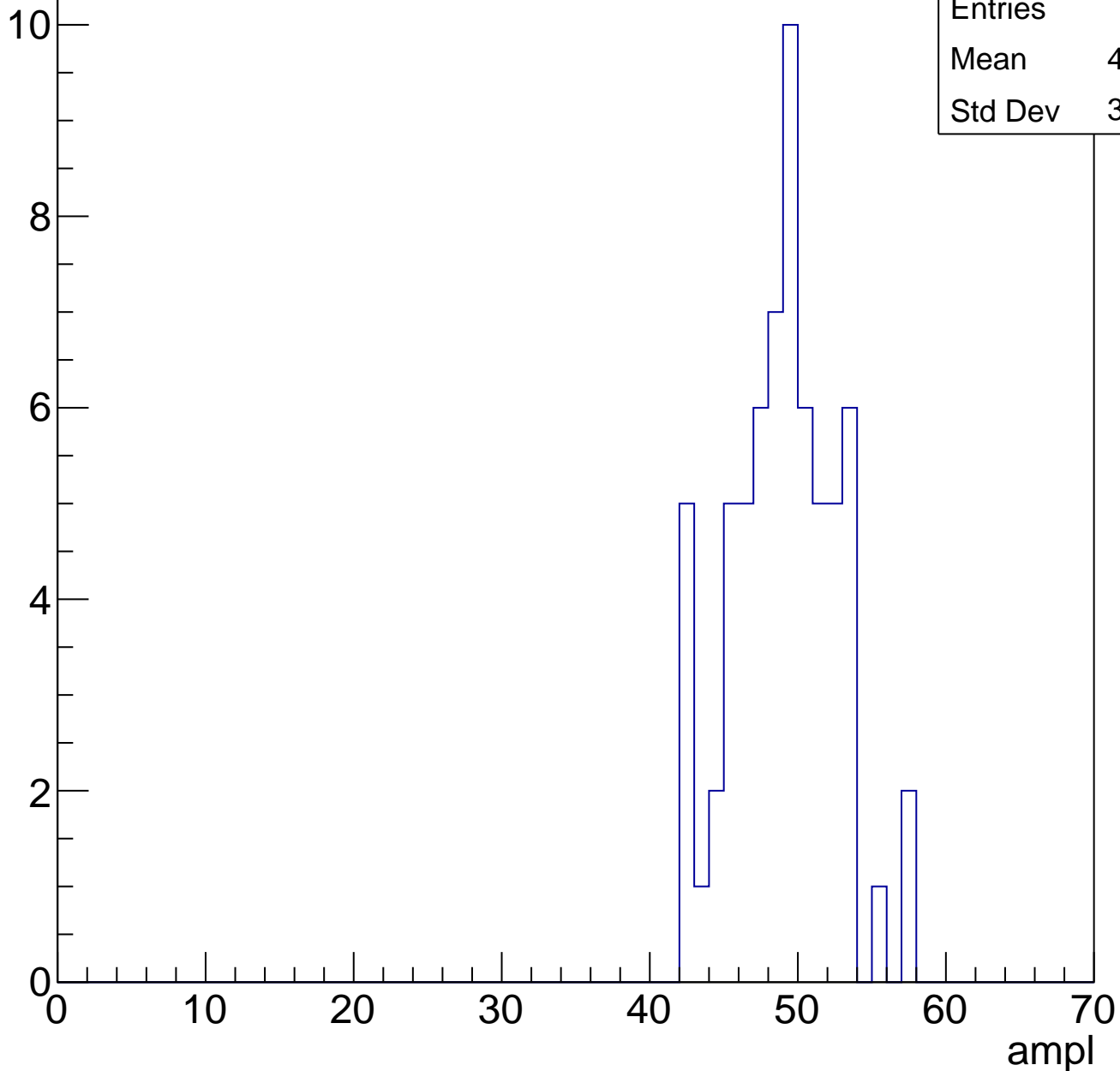


B1L103S, U21-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	48.58
Std Dev	3.495

Entry

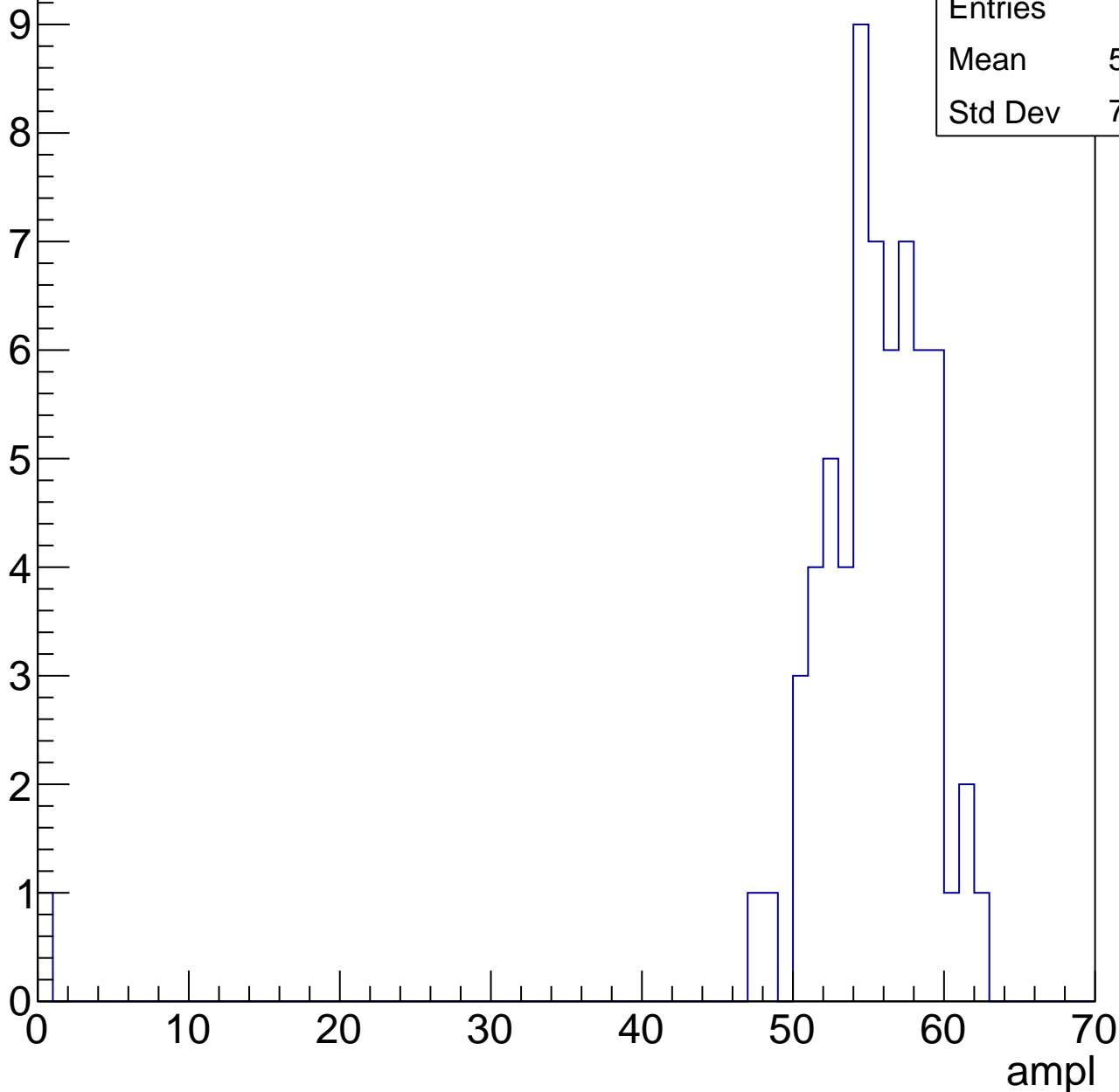


B1L103S, U21-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.27
Std Dev	7.544

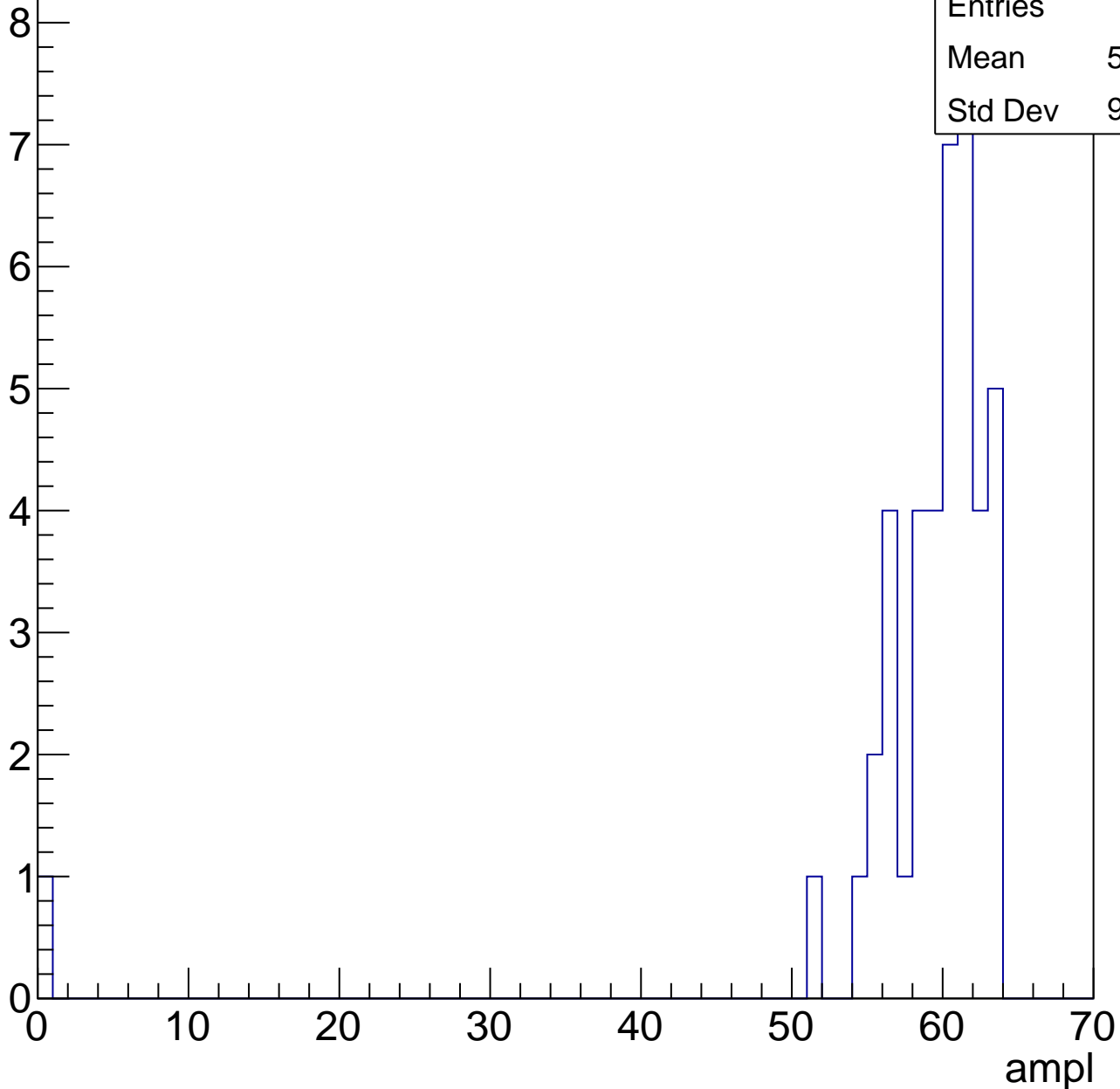


B1L103S, U21-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	57.98
Std Dev	9.458

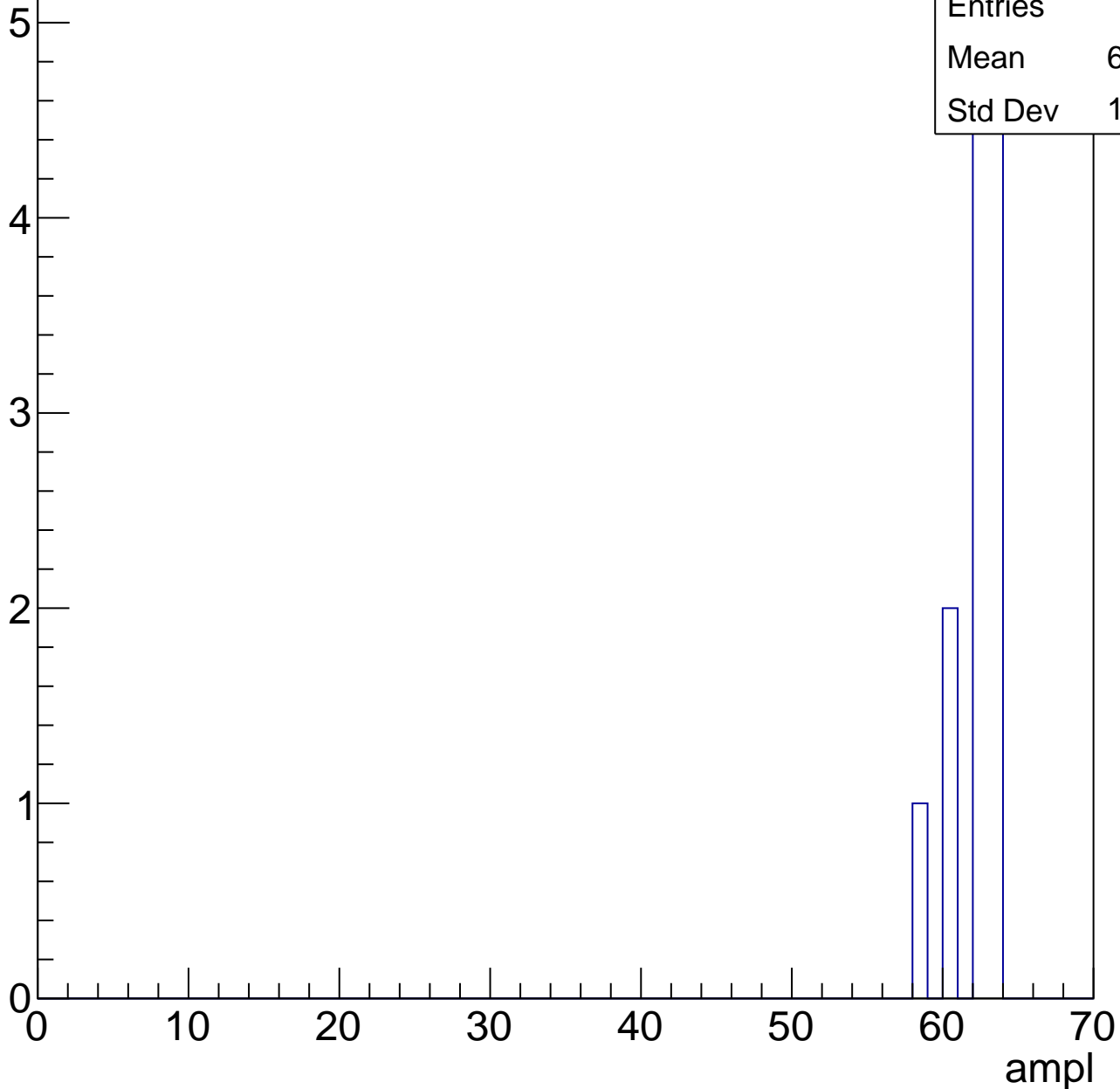


B1L103S, U21-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.77
Std Dev	1.476



B1L103S, U21-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch6, adc0

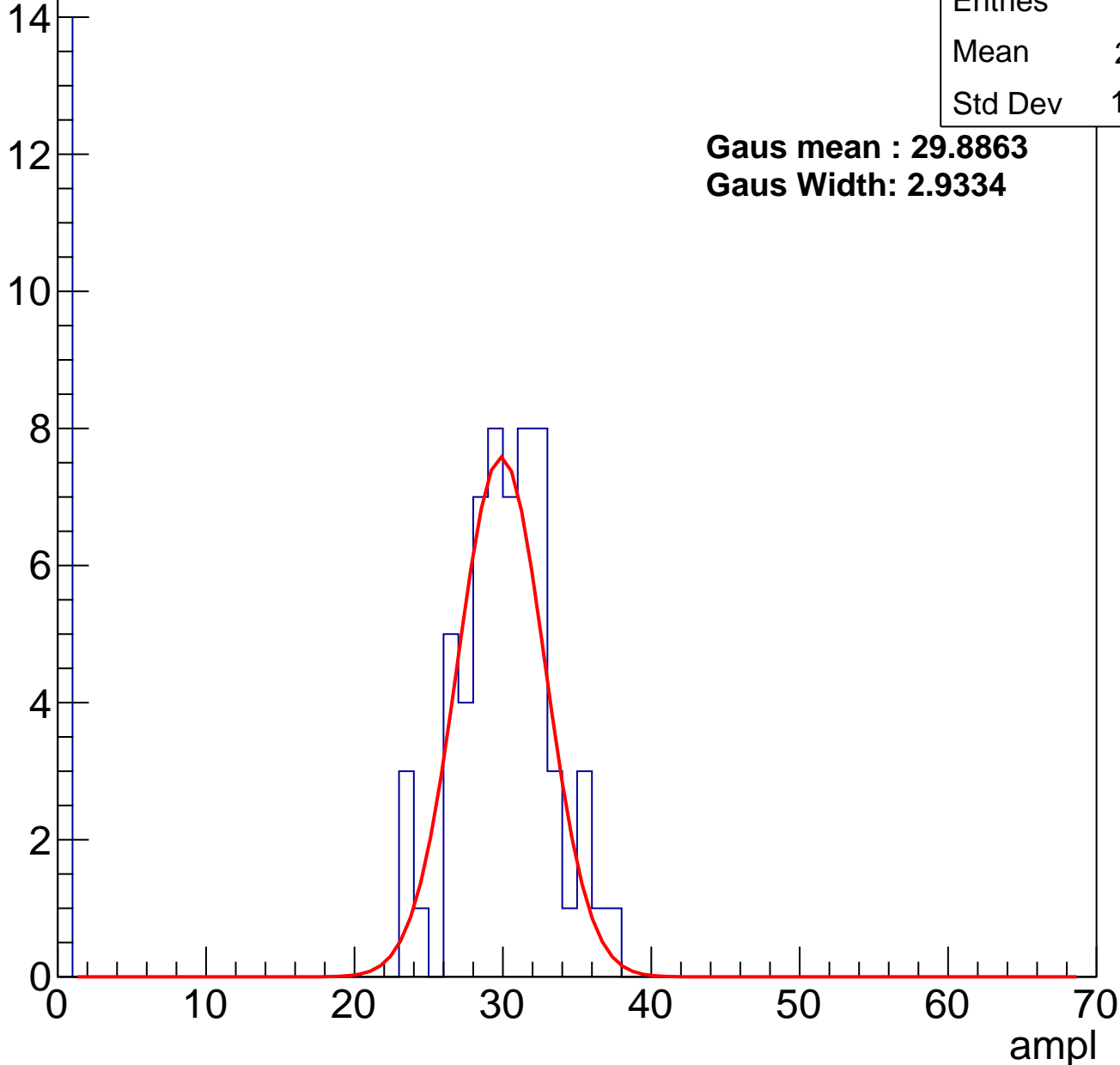
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	24.11
Std Dev	11.97

Gaus mean : 29.8863

Gaus Width: 2.9334

Entry



B1L103S, U21-ch6, adc1

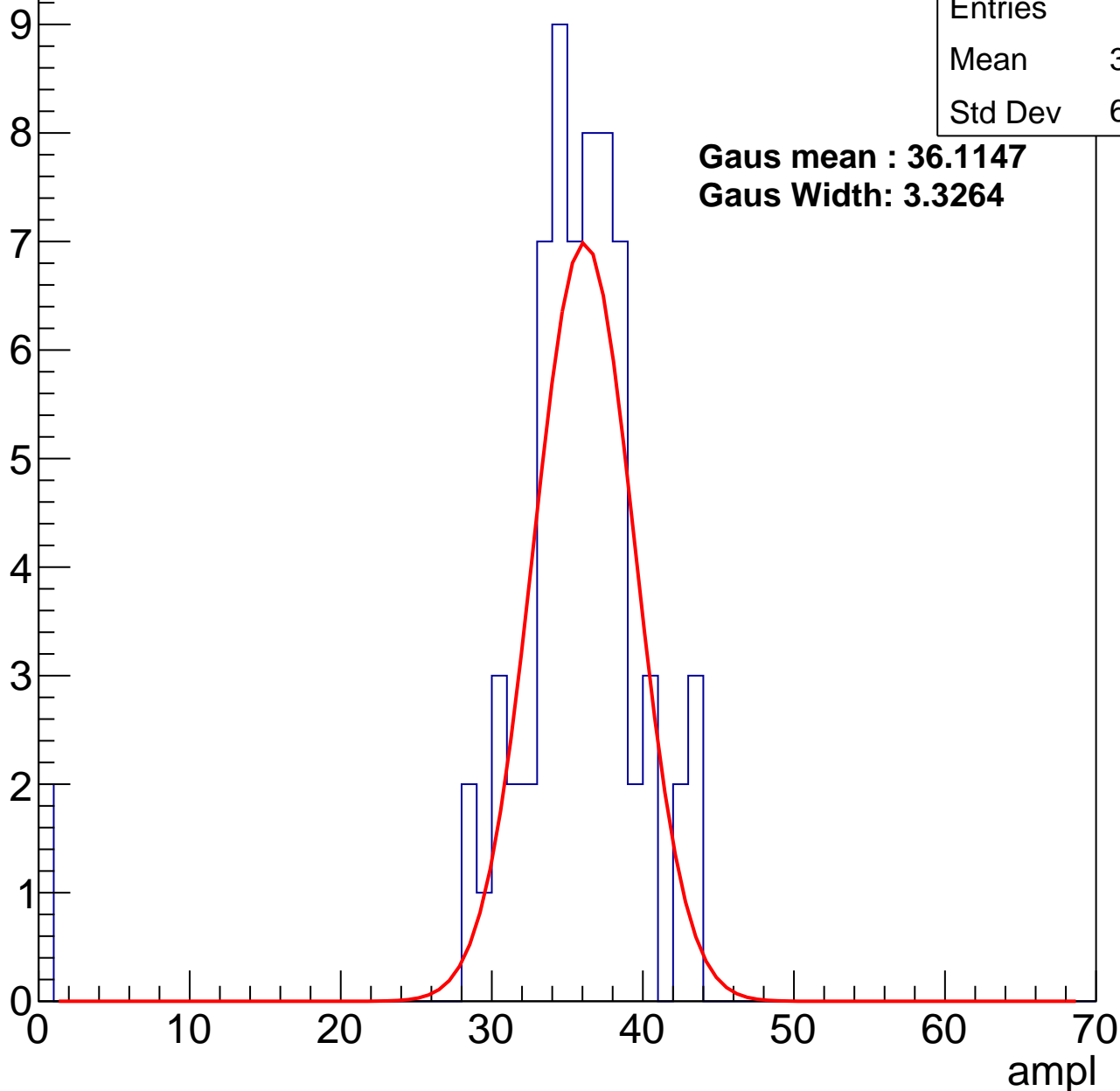
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.47
Std Dev	6.889

Gaus mean : 36.1147

Gaus Width: 3.3264



B1L103S, U21-ch6, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	35.21
Std Dev	16.3

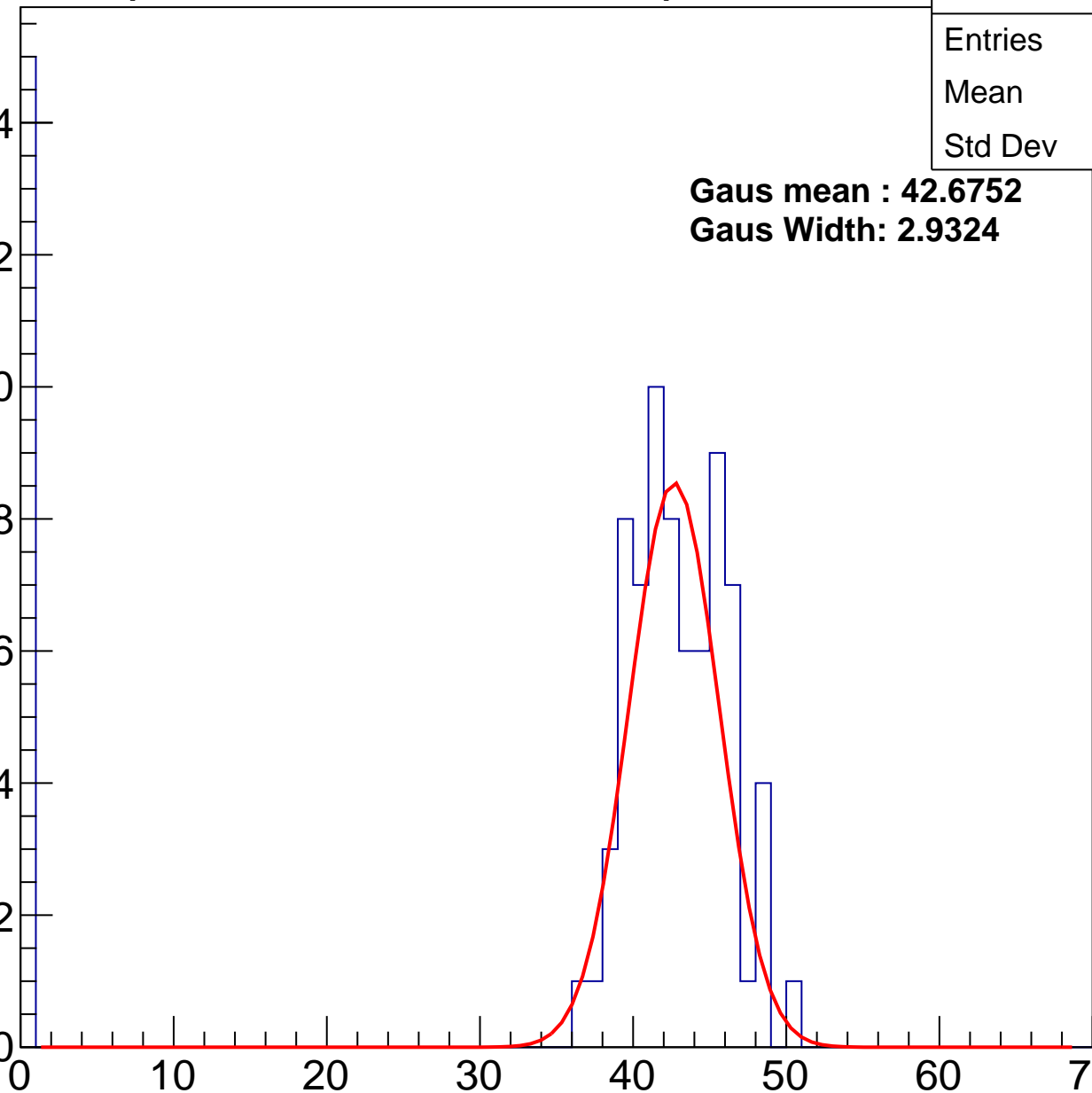
Gaus mean : 42.6752

Gaus Width: 2.9324

Entry

14
12
10
8
6
4
2
0

ampl

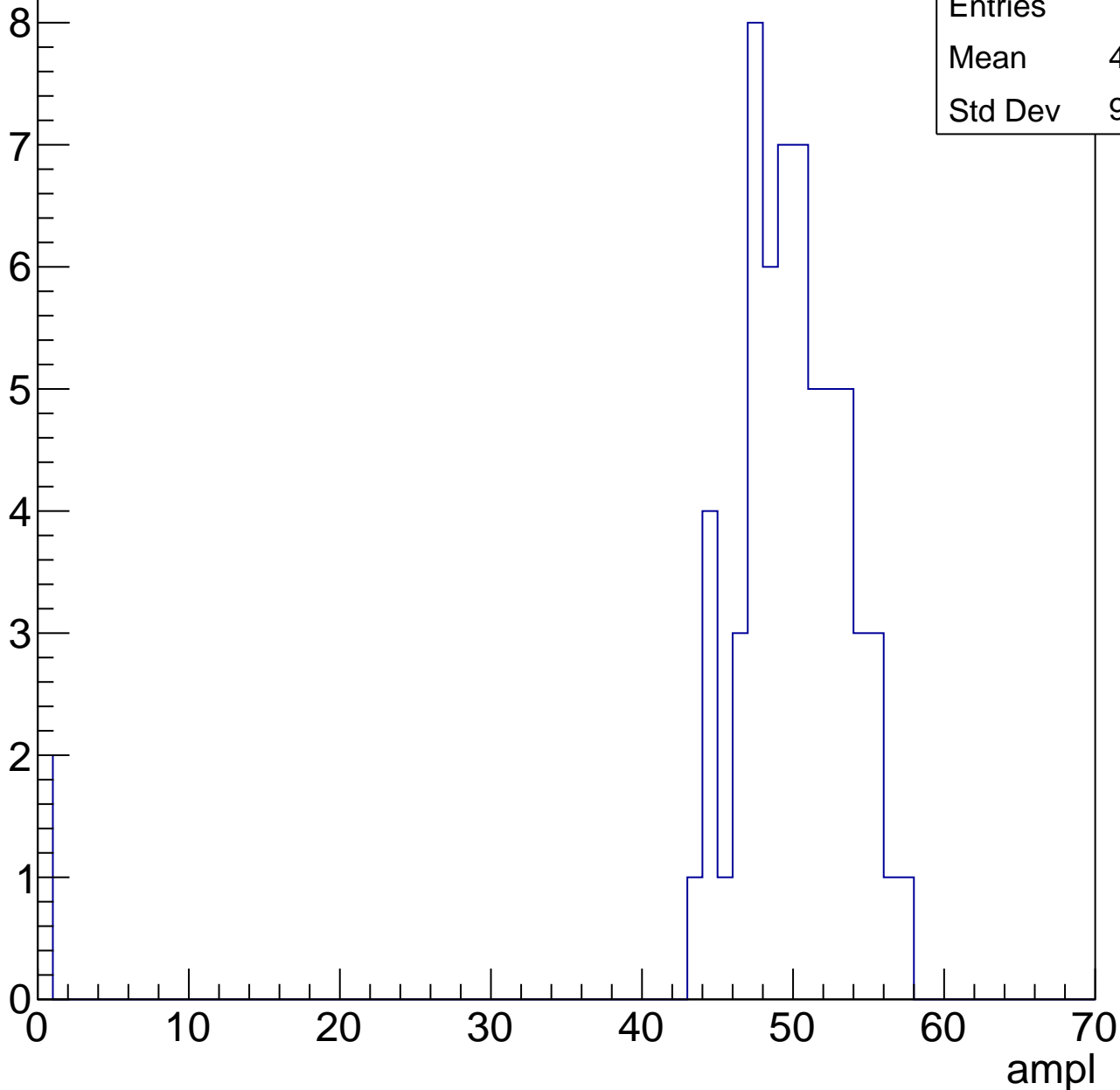


B1L103S, U21-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.05
Std Dev	9.342

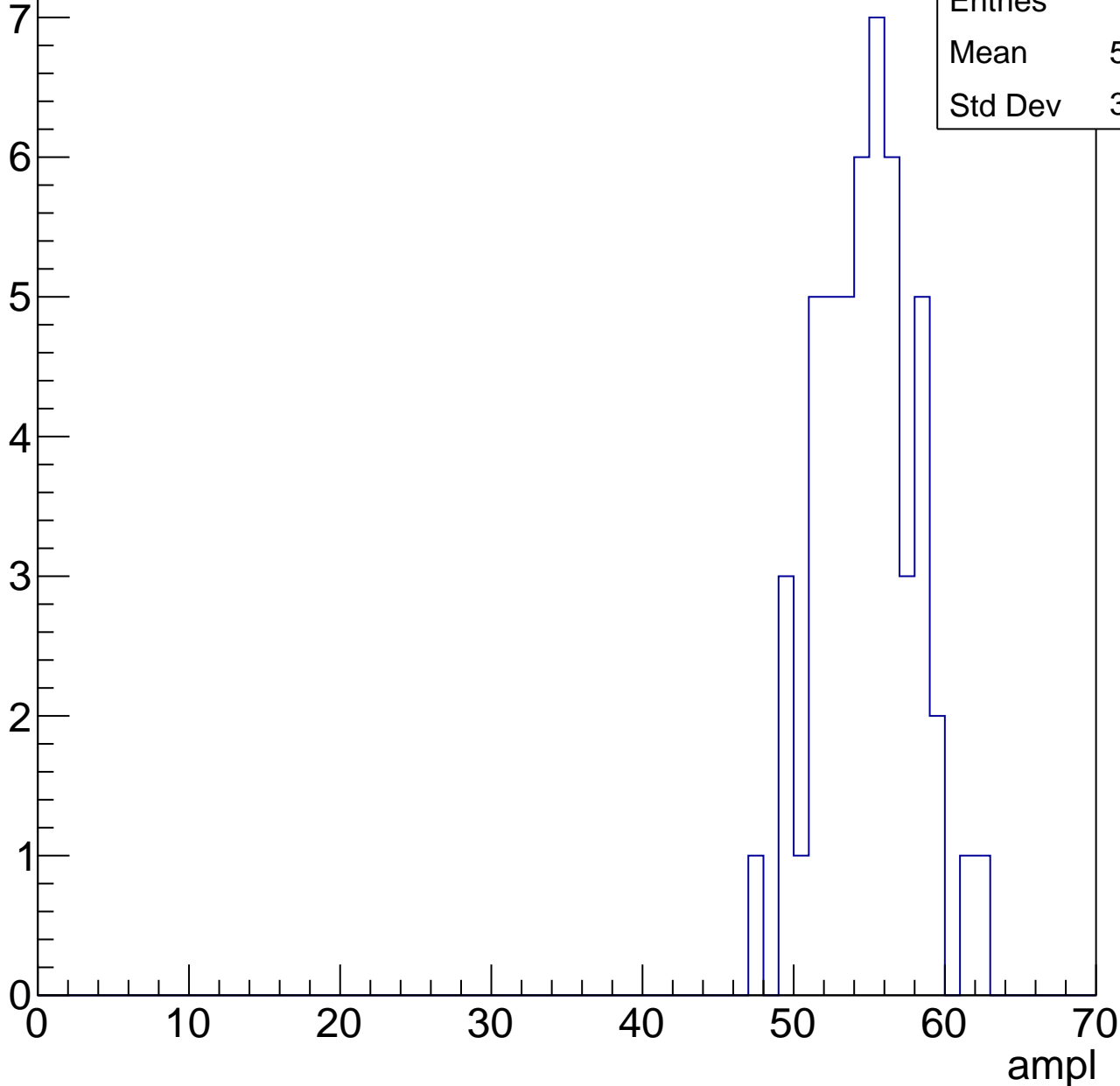


B1L103S, U21-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	54.33
Std Dev	3.148

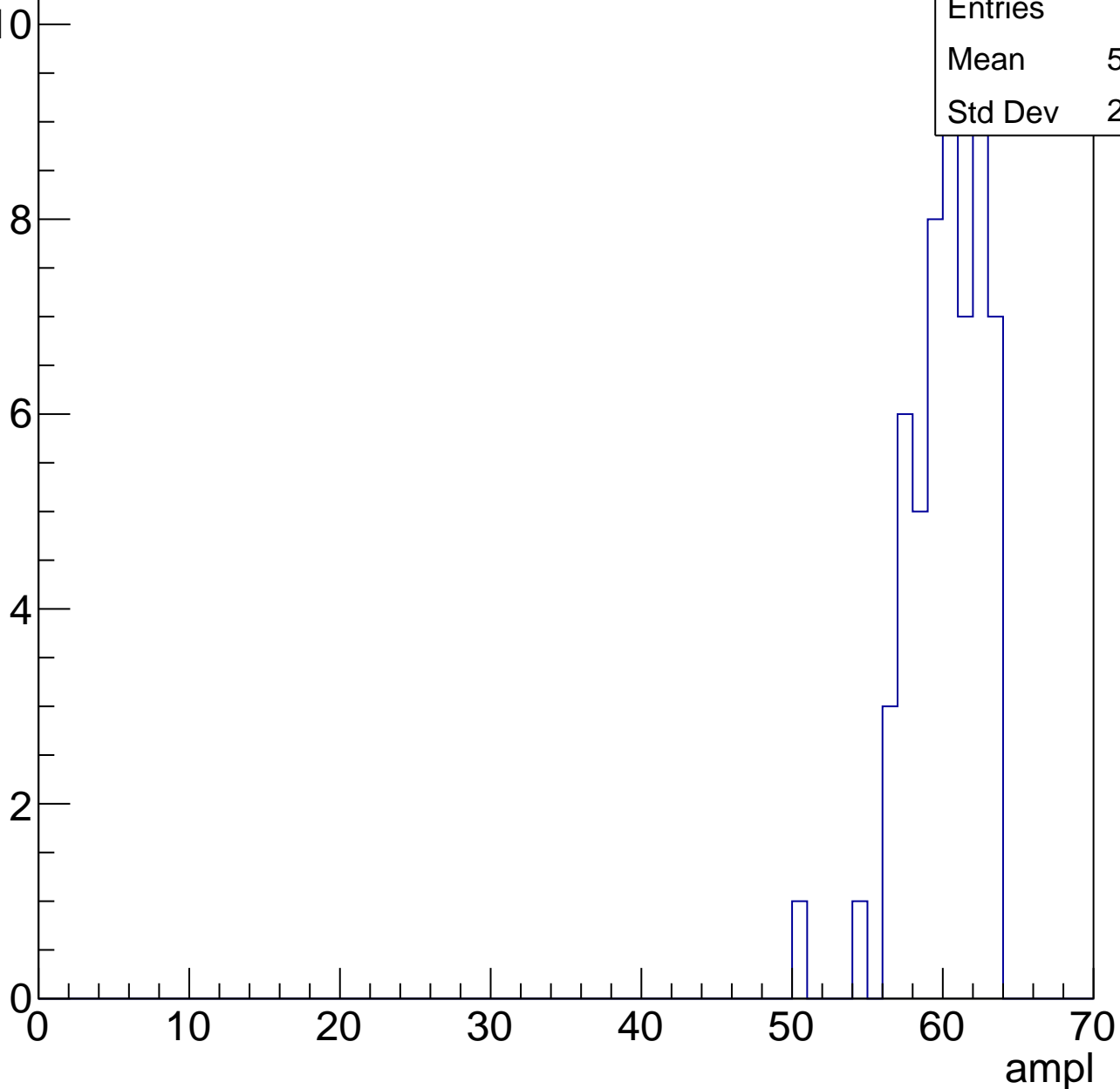


B1L103S, U21-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

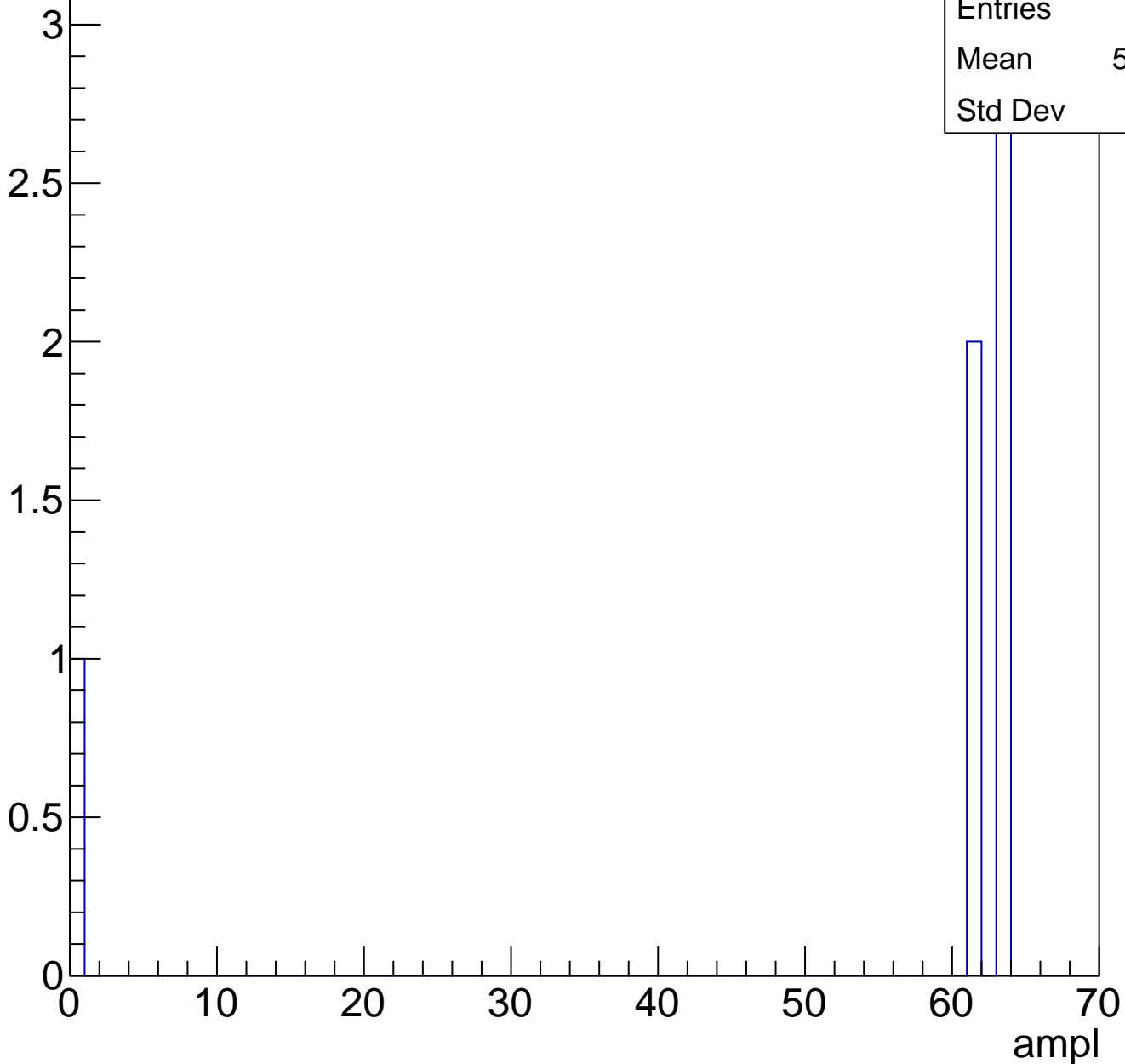
Entries	57
Mean	59.68
Std Dev	2.535



B1L103S, U21-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

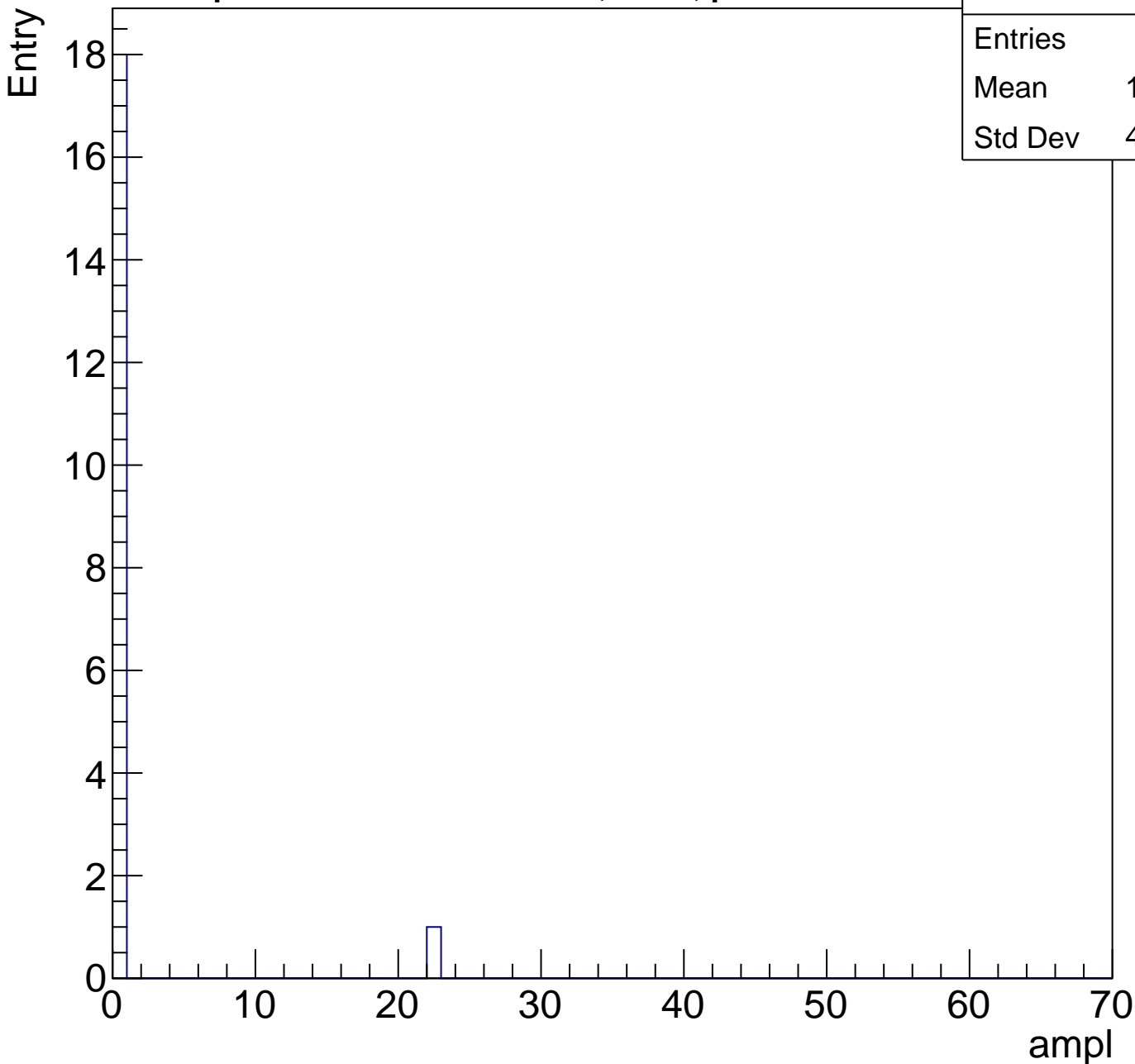
Entry



B1L103S, U21-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913



B1L103S, U21-ch7, adc0

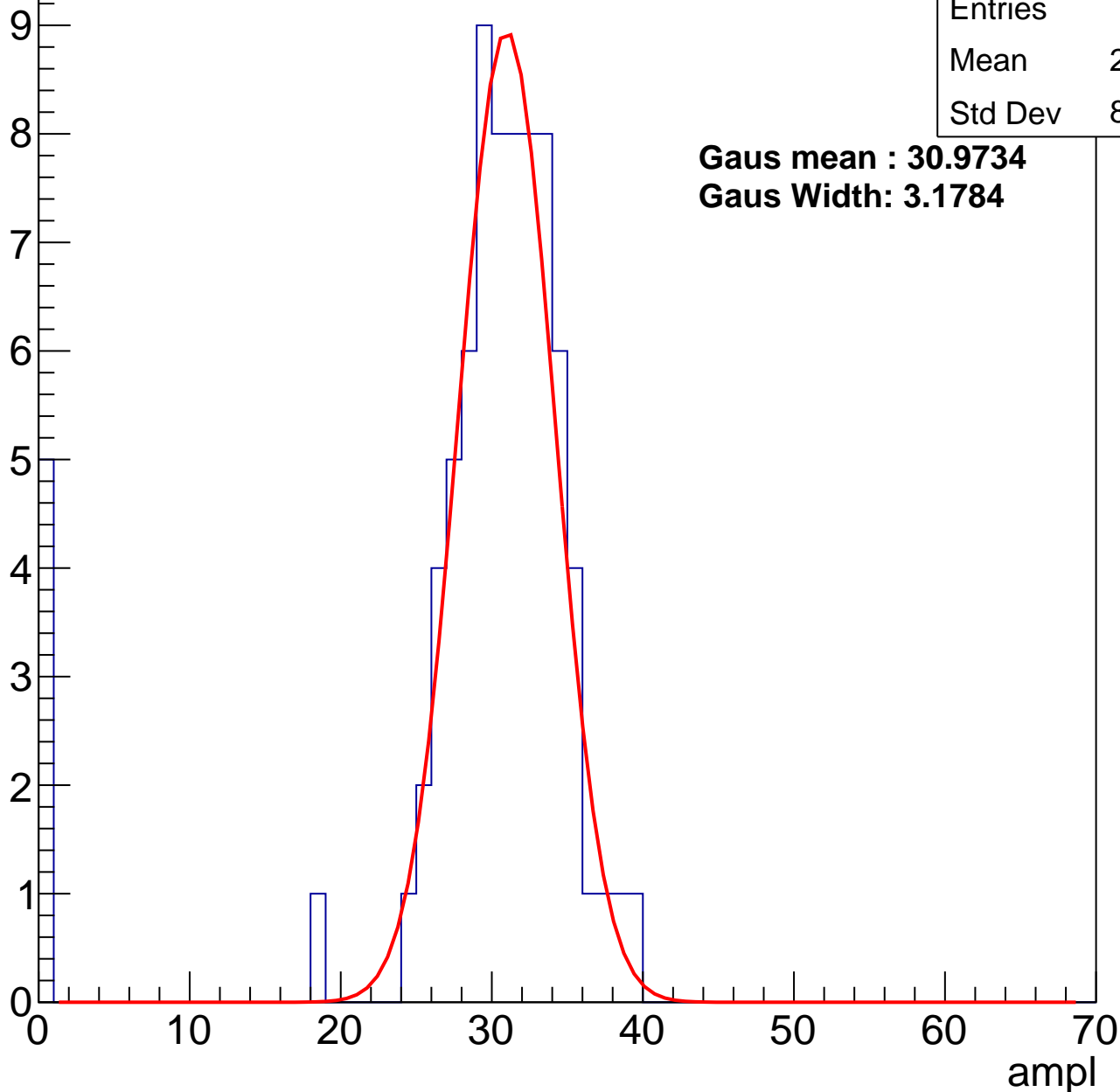
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	28.63
Std Dev	8.162

Gaus mean : 30.9734

Gaus Width: 3.1784



B1L103S, U21-ch7, adc1

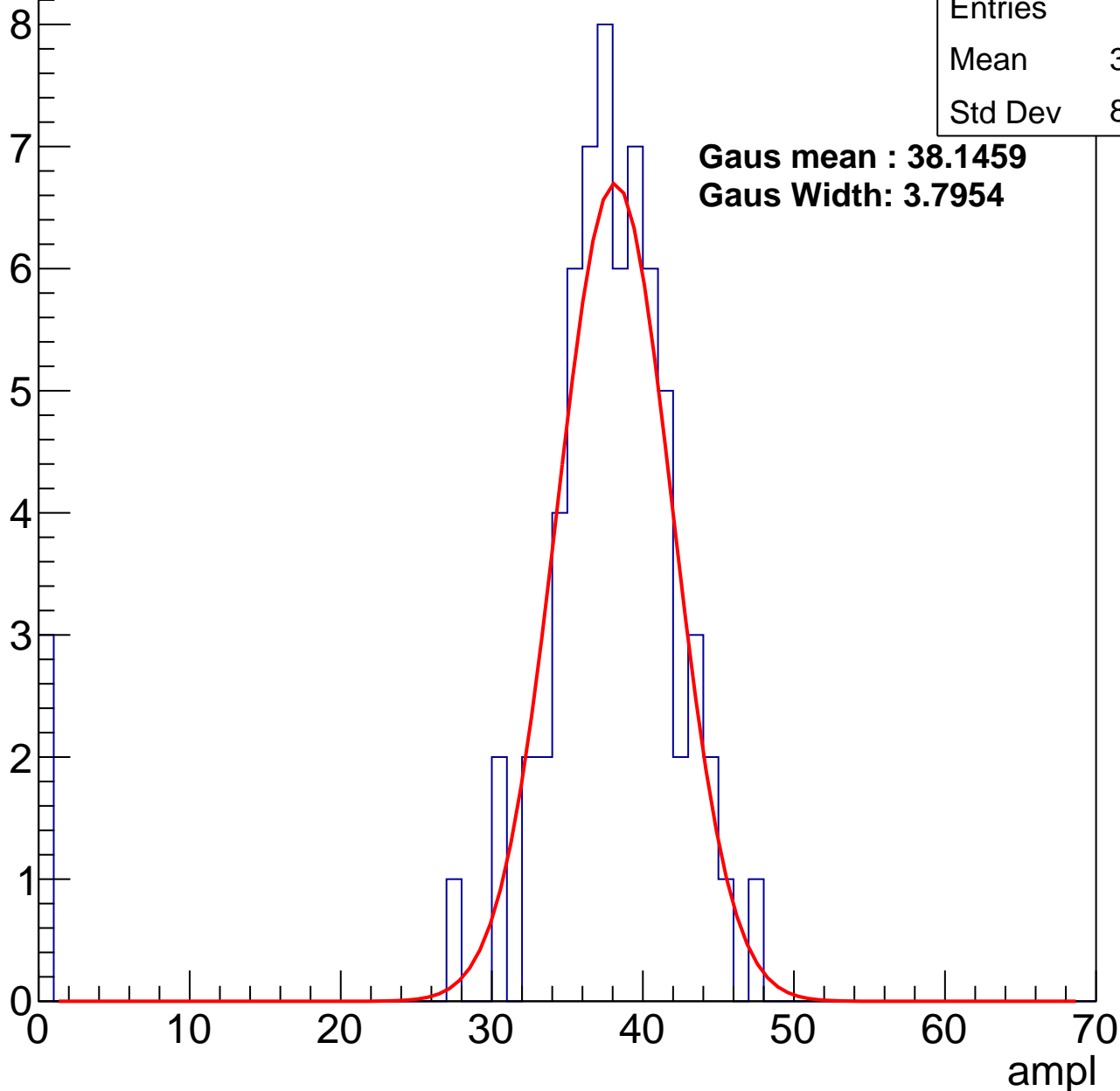
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	36.03
Std Dev	8.556

Gaus mean : 38.1459

Gaus Width: 3.7954



B1L103S, U21-ch7, adc2

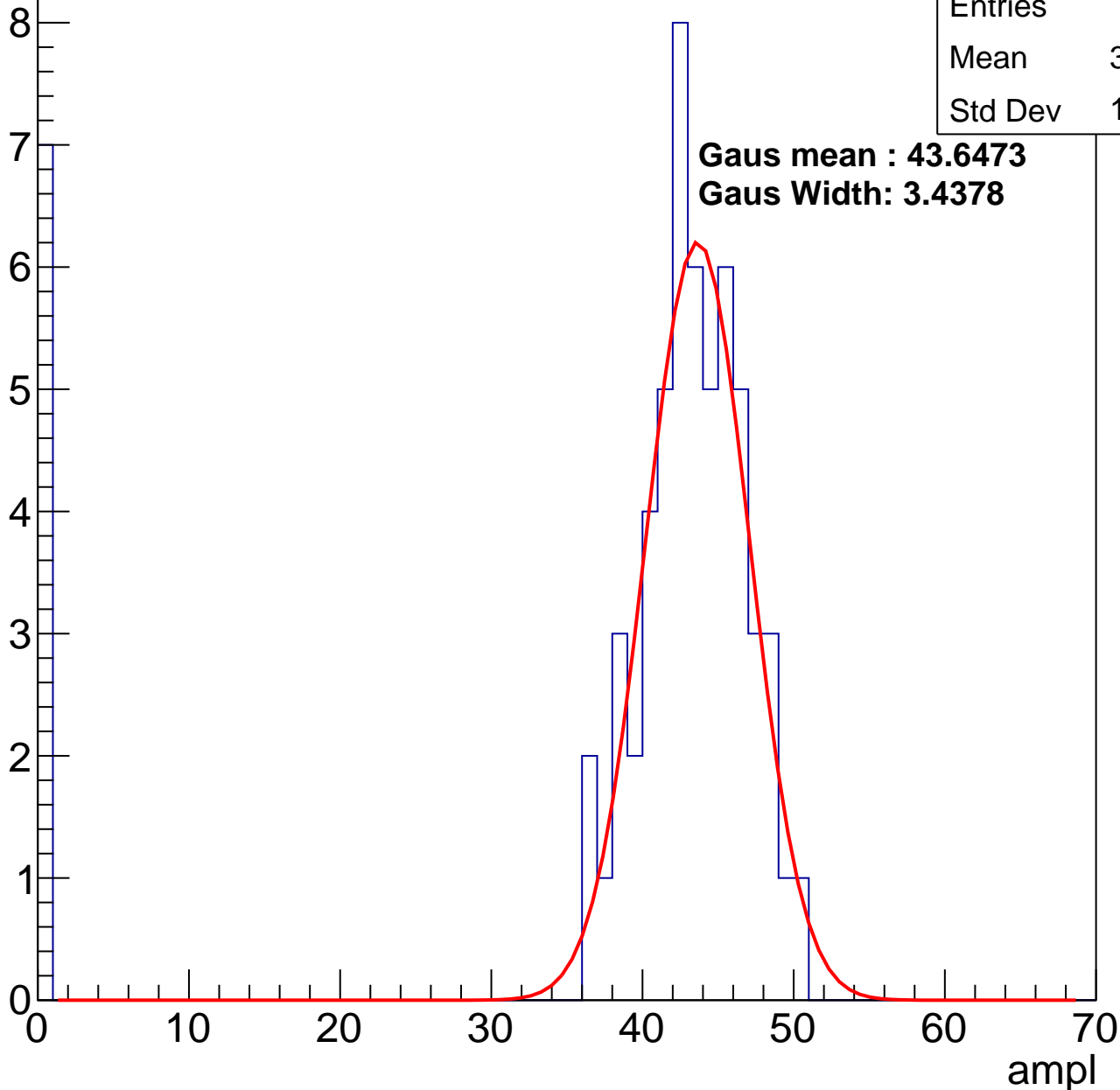
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	38.13
Std Dev	13.95

Gaus mean : 43.6473

Gaus Width: 3.4378

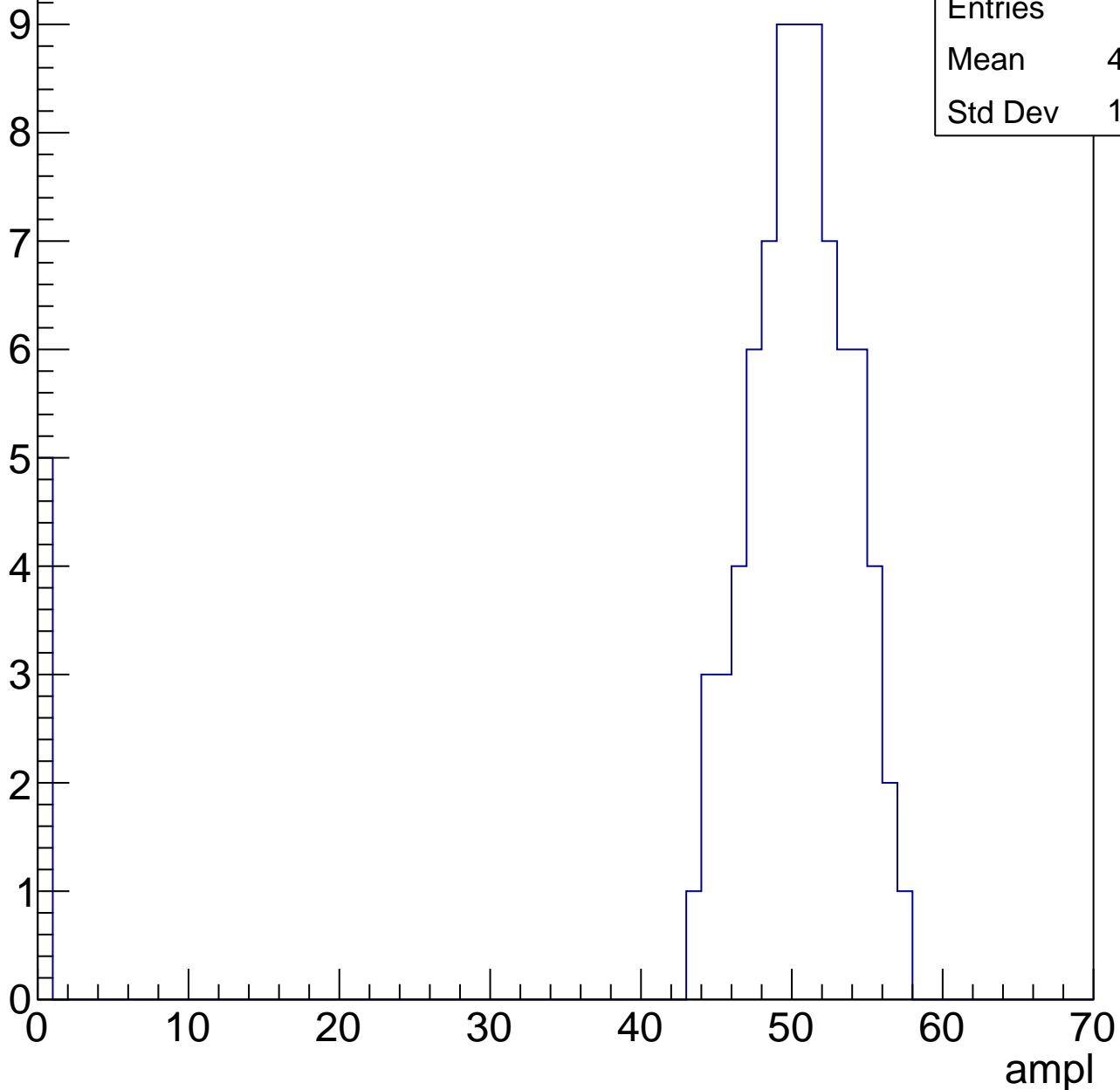


B1L103S, U21-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	47.04
Std Dev	12.38

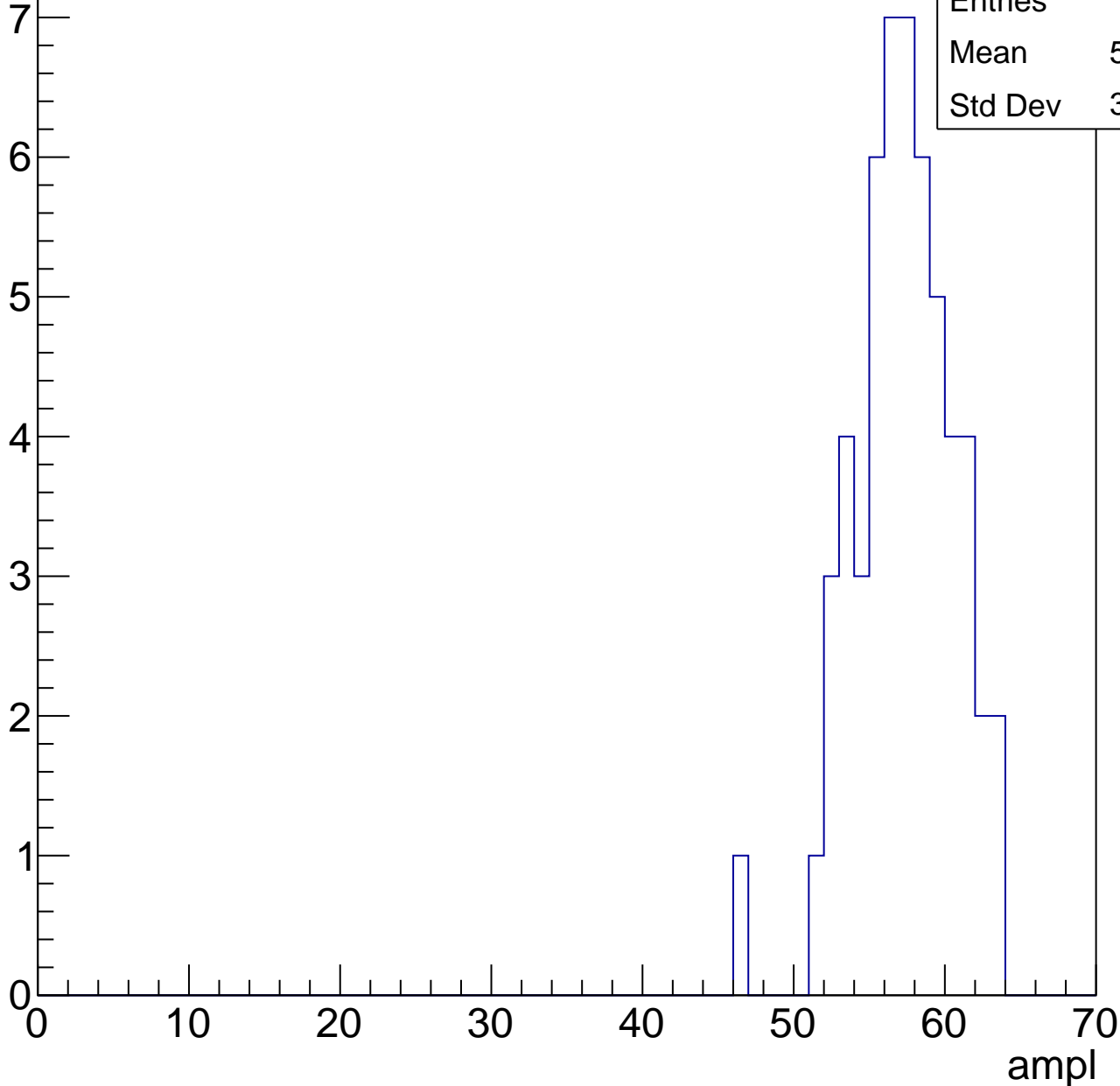


B1L103S, U21-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	56.82
Std Dev	3.303

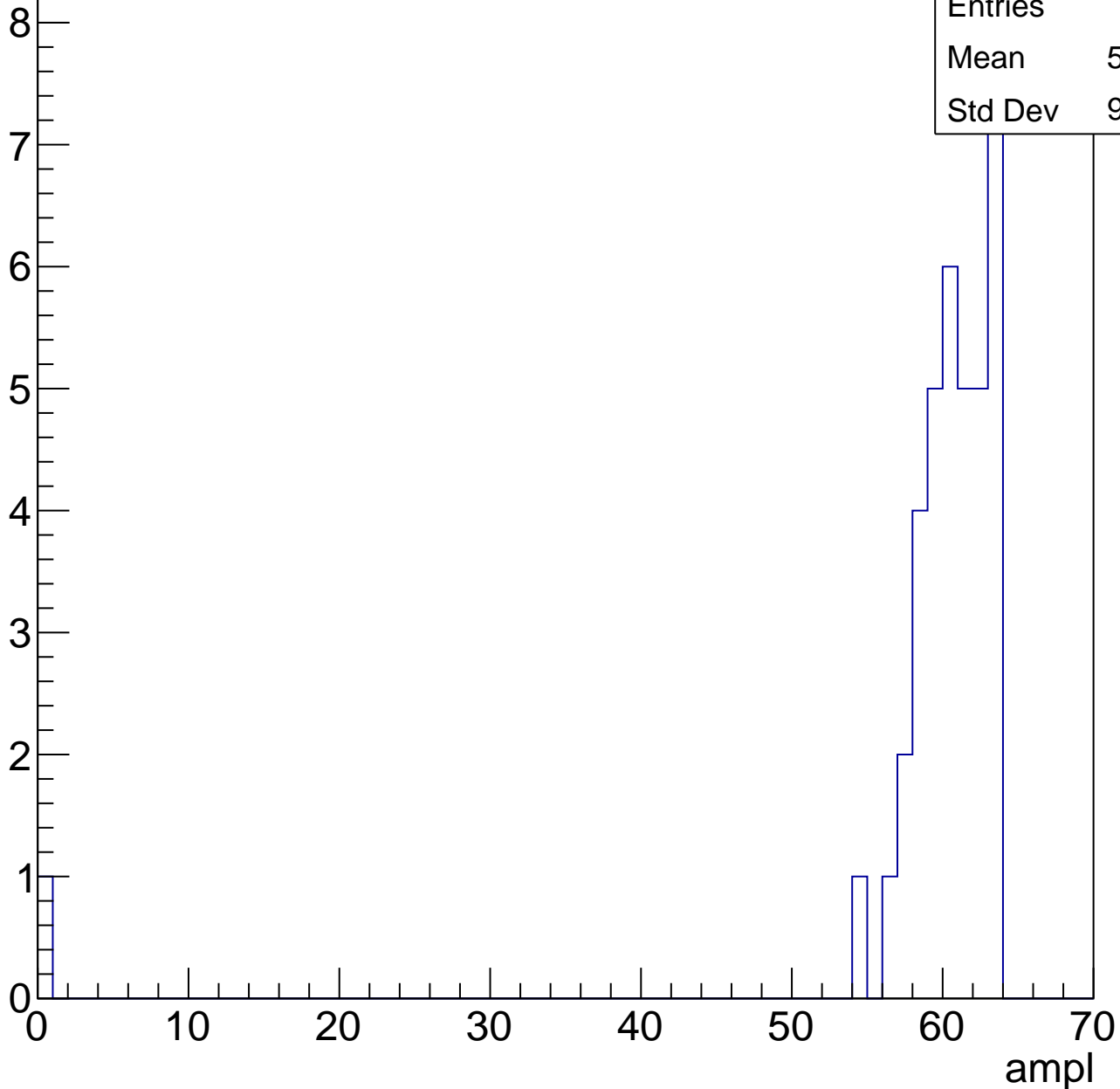


B1L103S, U21-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

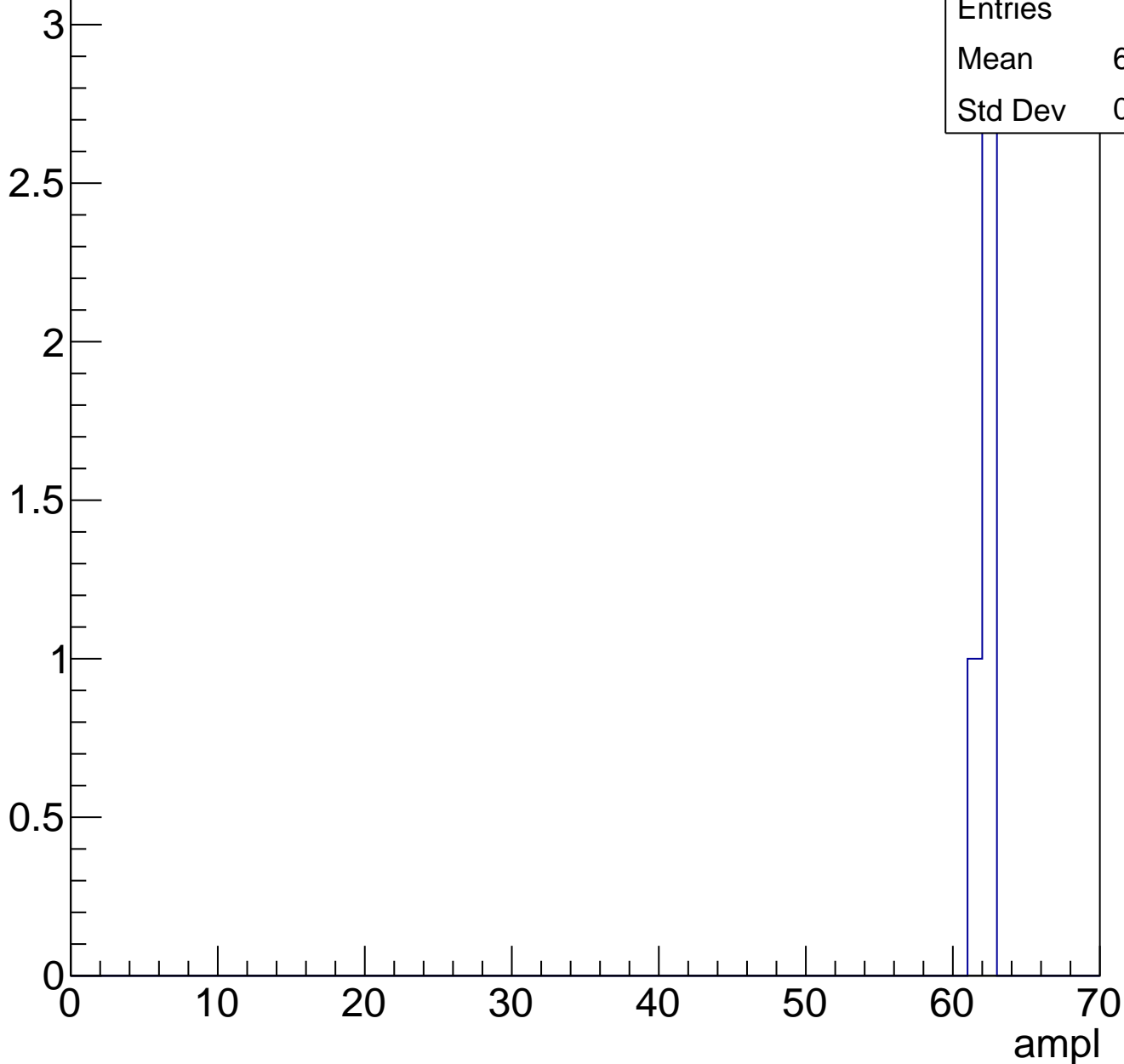
Entries	38
Mean	58.68
Std Dev	9.897



B1L103S, U21-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch8, adc0

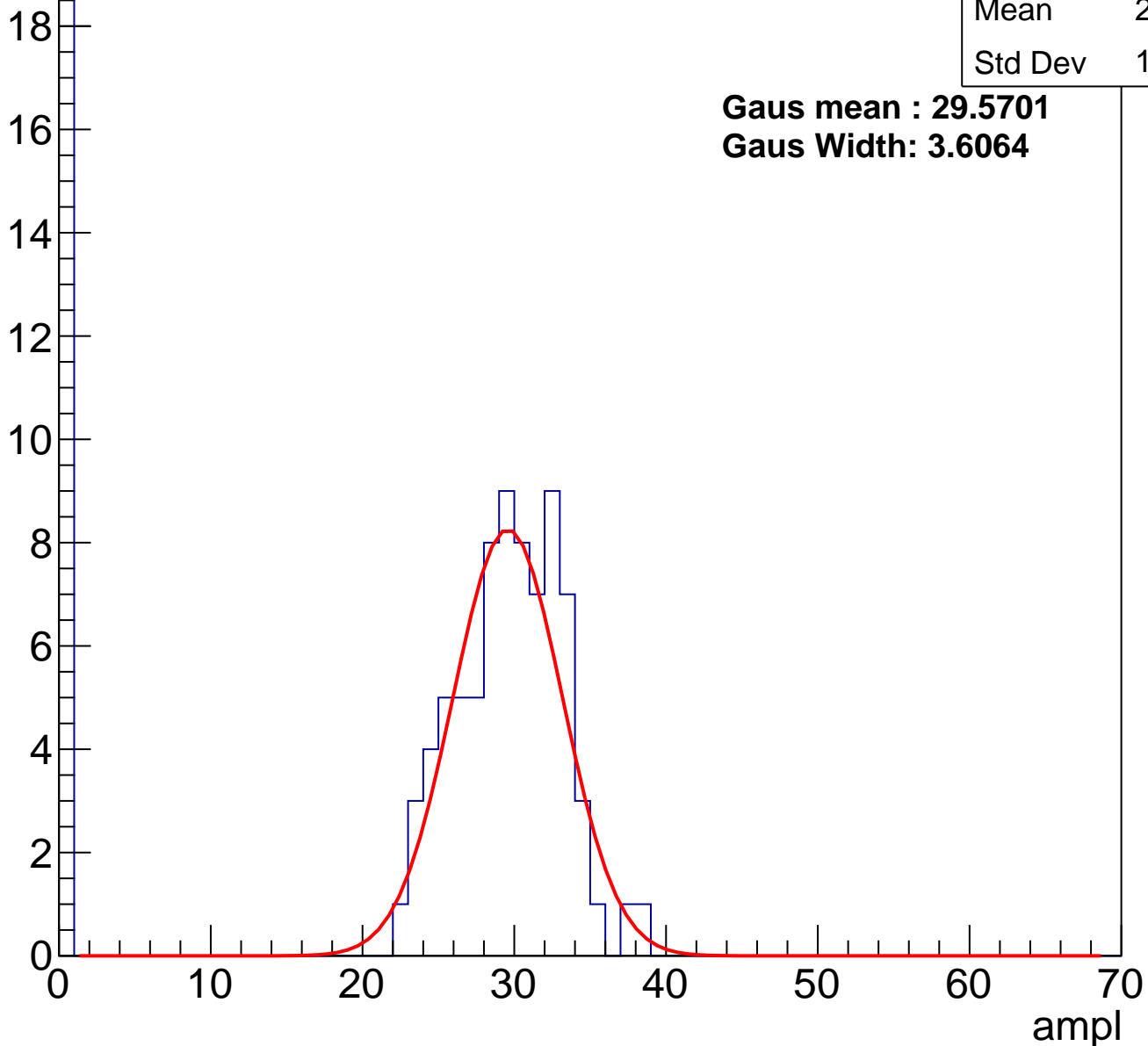
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	23.44
Std Dev	12.03

Gaus mean : 29.5701

Gaus Width: 3.6064

Entry



B1L103S, U21-ch8, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	29.21
Std Dev	14.25

Gaus mean : 36.7336

Gaus Width: 3.0693

Entry

12

10

8

6

4

2

0

0

10

20

30

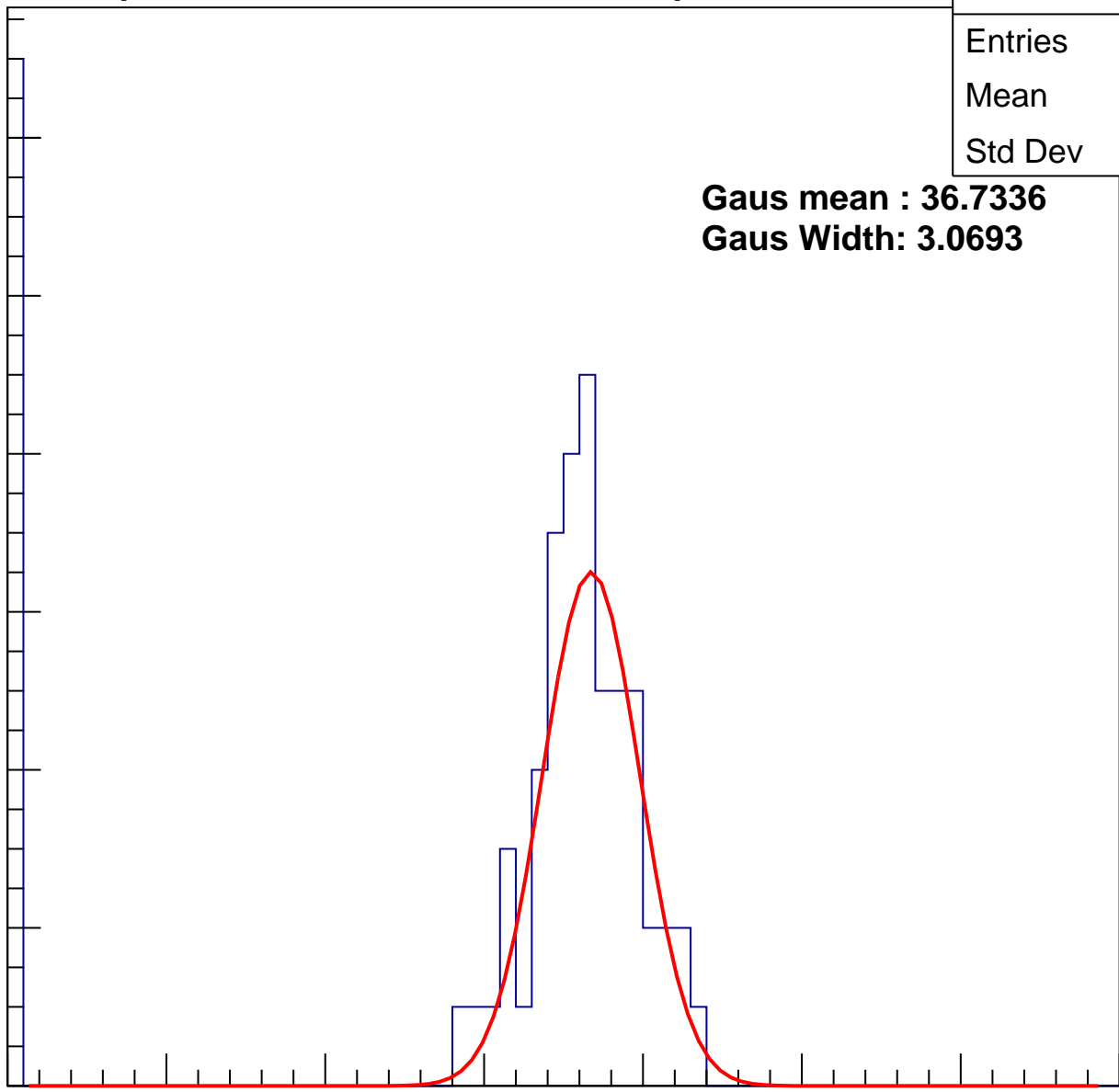
40

50

60

70

ampl



B1L103S, U21-ch8, adc2

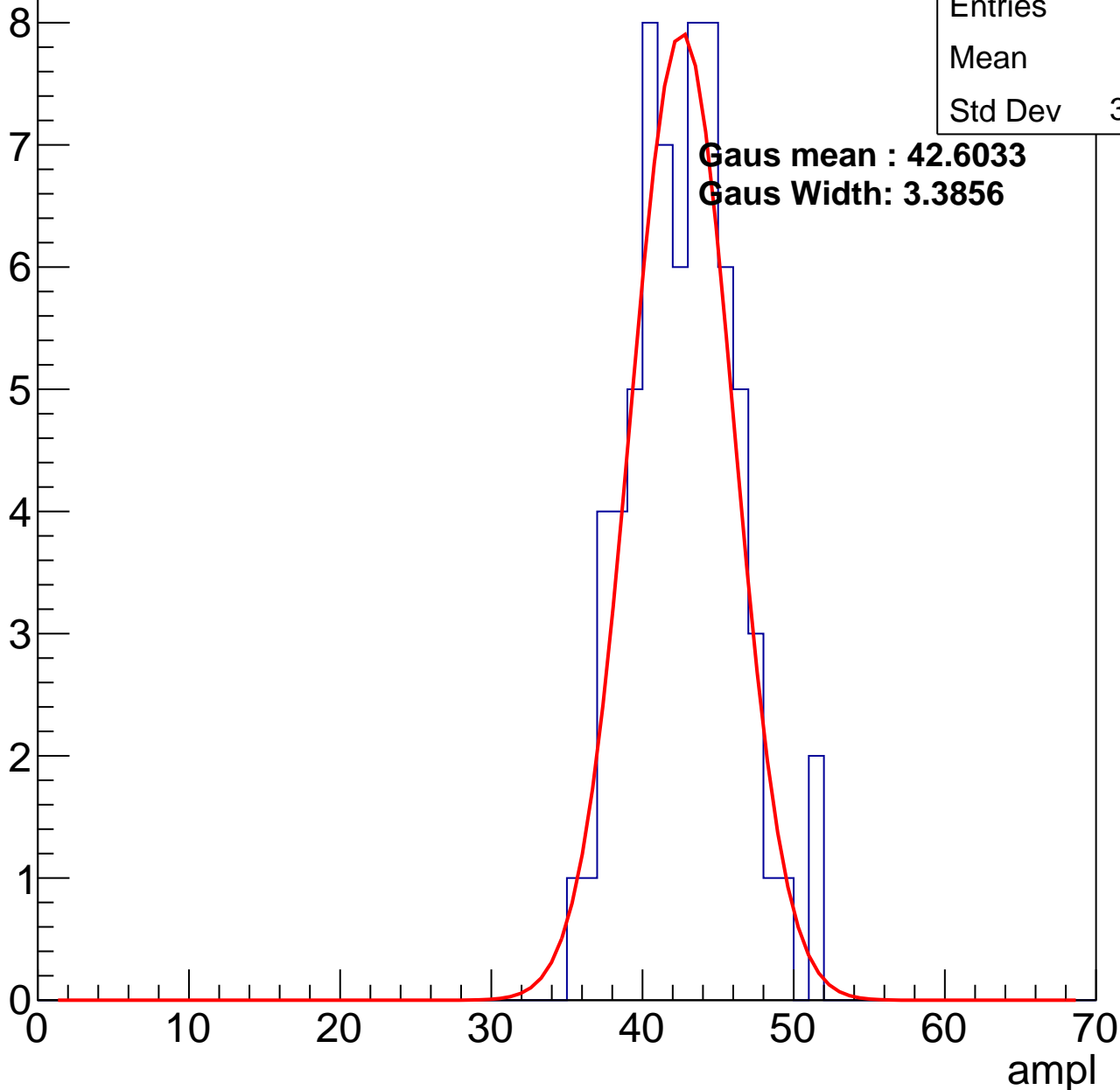
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	42.3
Std Dev	3.428

Gaus mean : 42.6033

Gaus Width: 3.3856

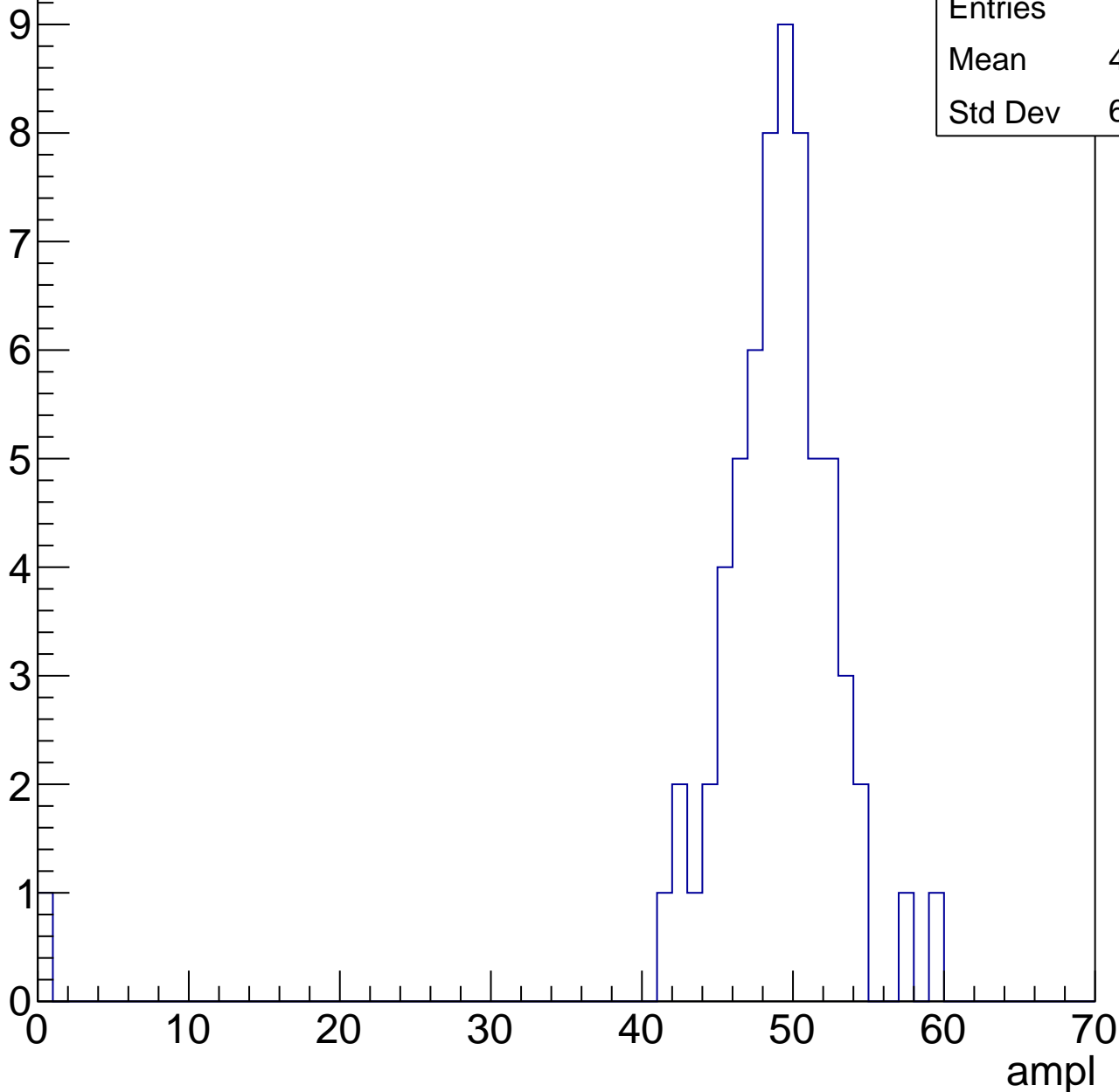


B1L103S, U21-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	47.98
Std Dev	6.914

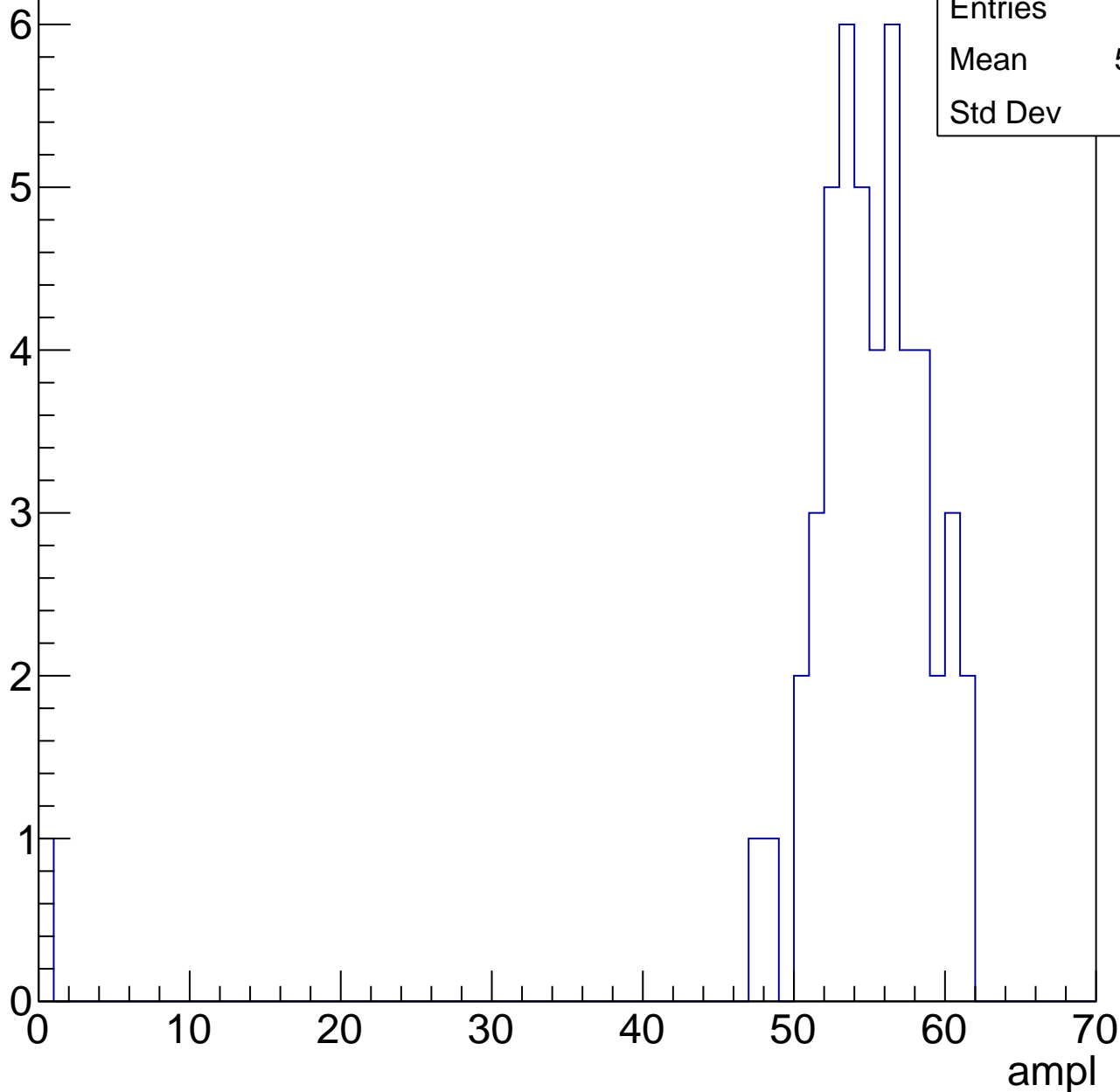


B1L103S, U21-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	53.71
Std Dev	8.41

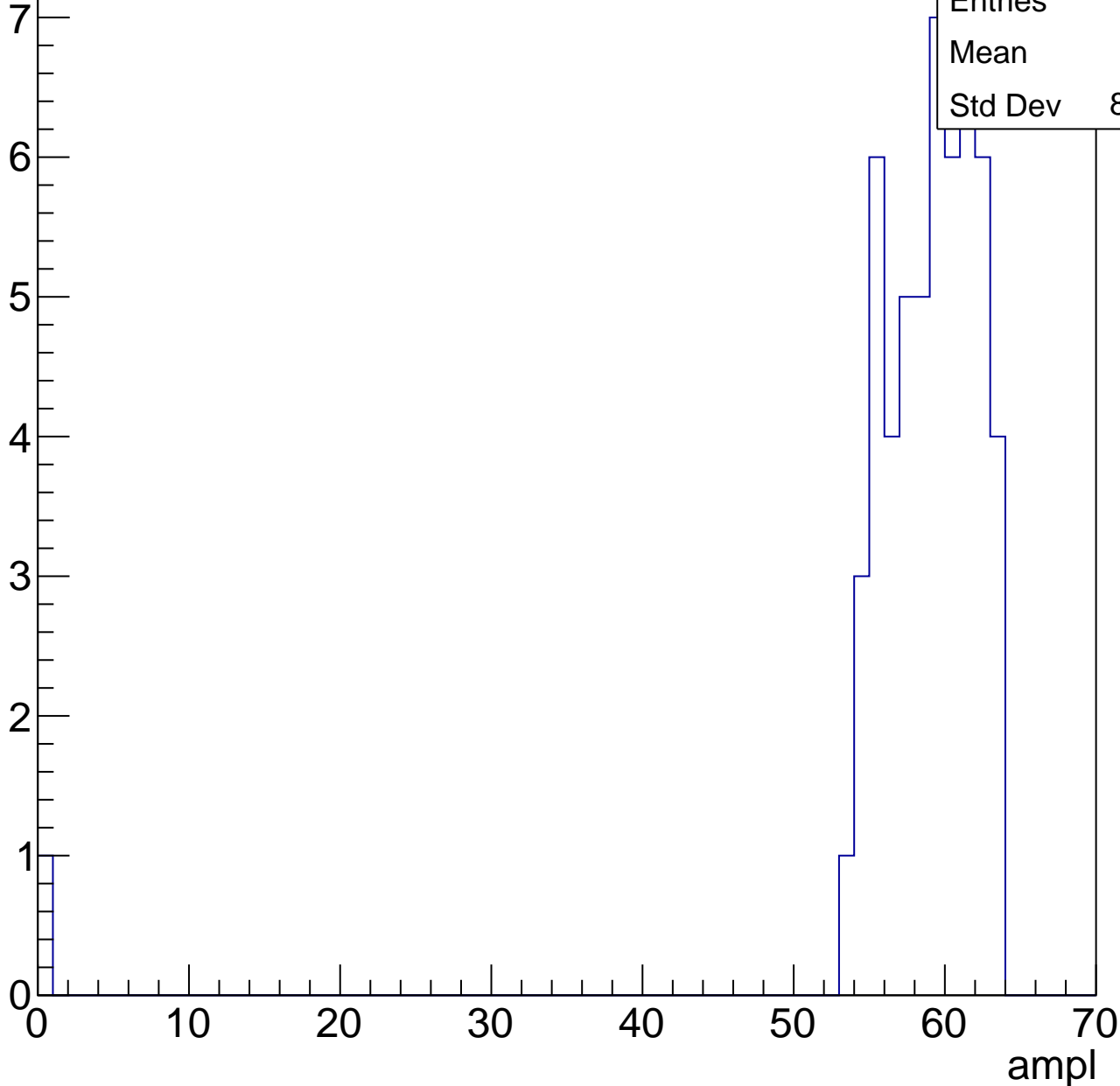


B1L103S, U21-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.6
Std Dev	8.305

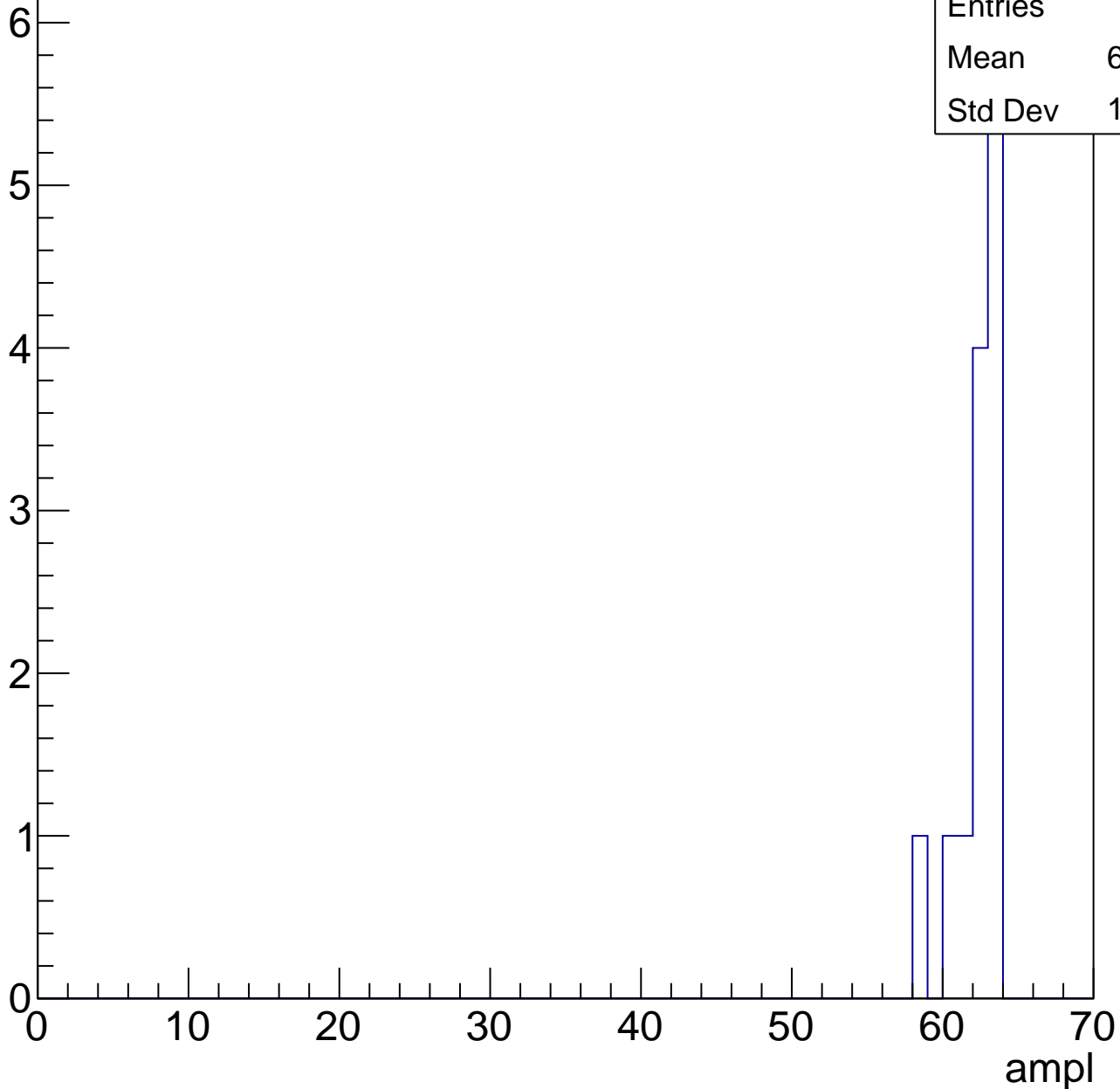


B1L103S, U21-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.92
Std Dev	1.439



B1L103S, U21-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch9, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	24.21
Std Dev	11.01

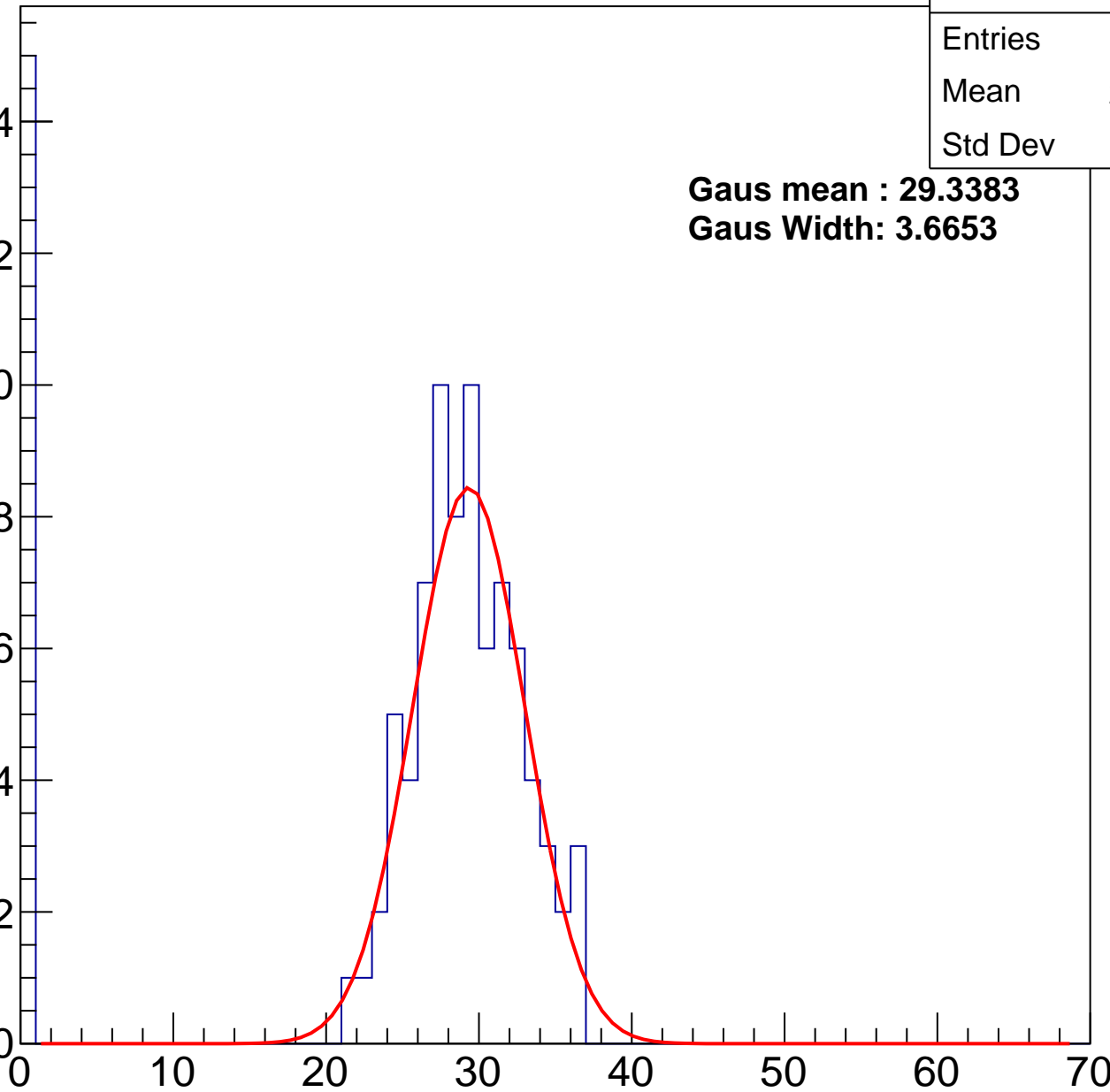
Gaus mean : 29.3383

Gaus Width: 3.6653

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch9, adc1

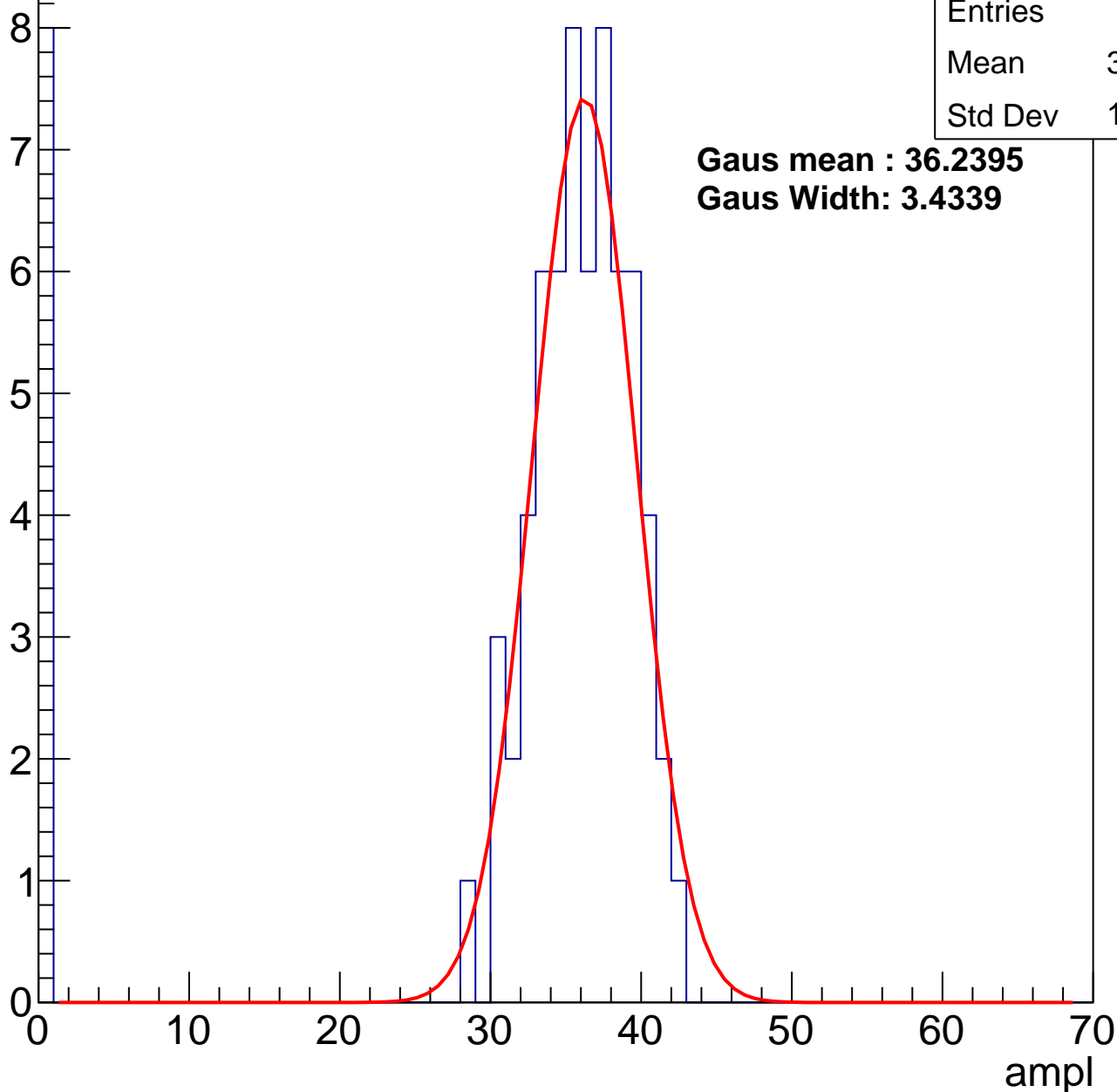
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	31.66
Std Dev	11.65

Gaus mean : 36.2395

Gaus Width: 3.4339



B1L103S, U21-ch9, adc2

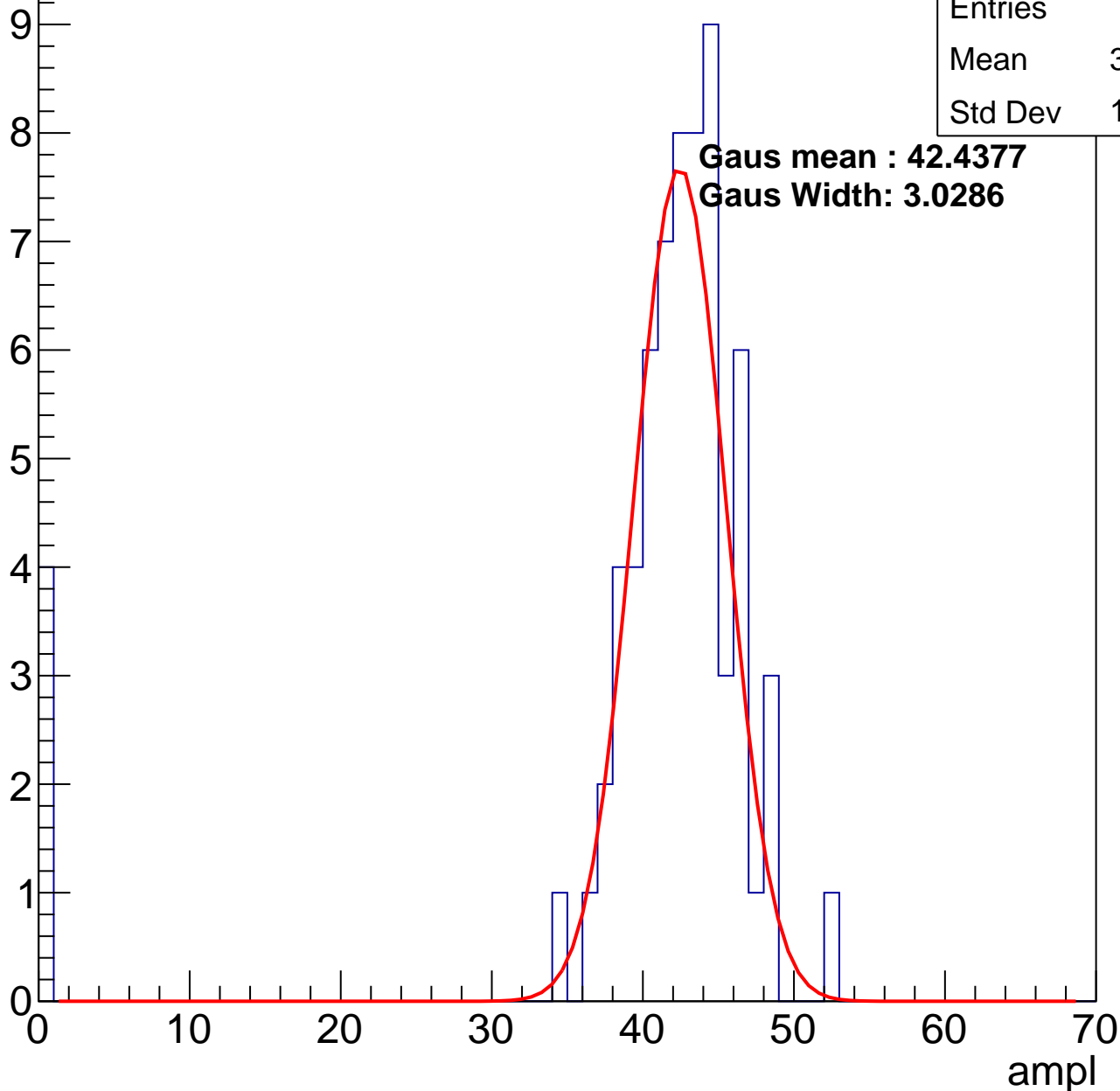
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	39.84
Std Dev	10.45

Gaus mean : 42.4377

Gaus Width: 3.0286

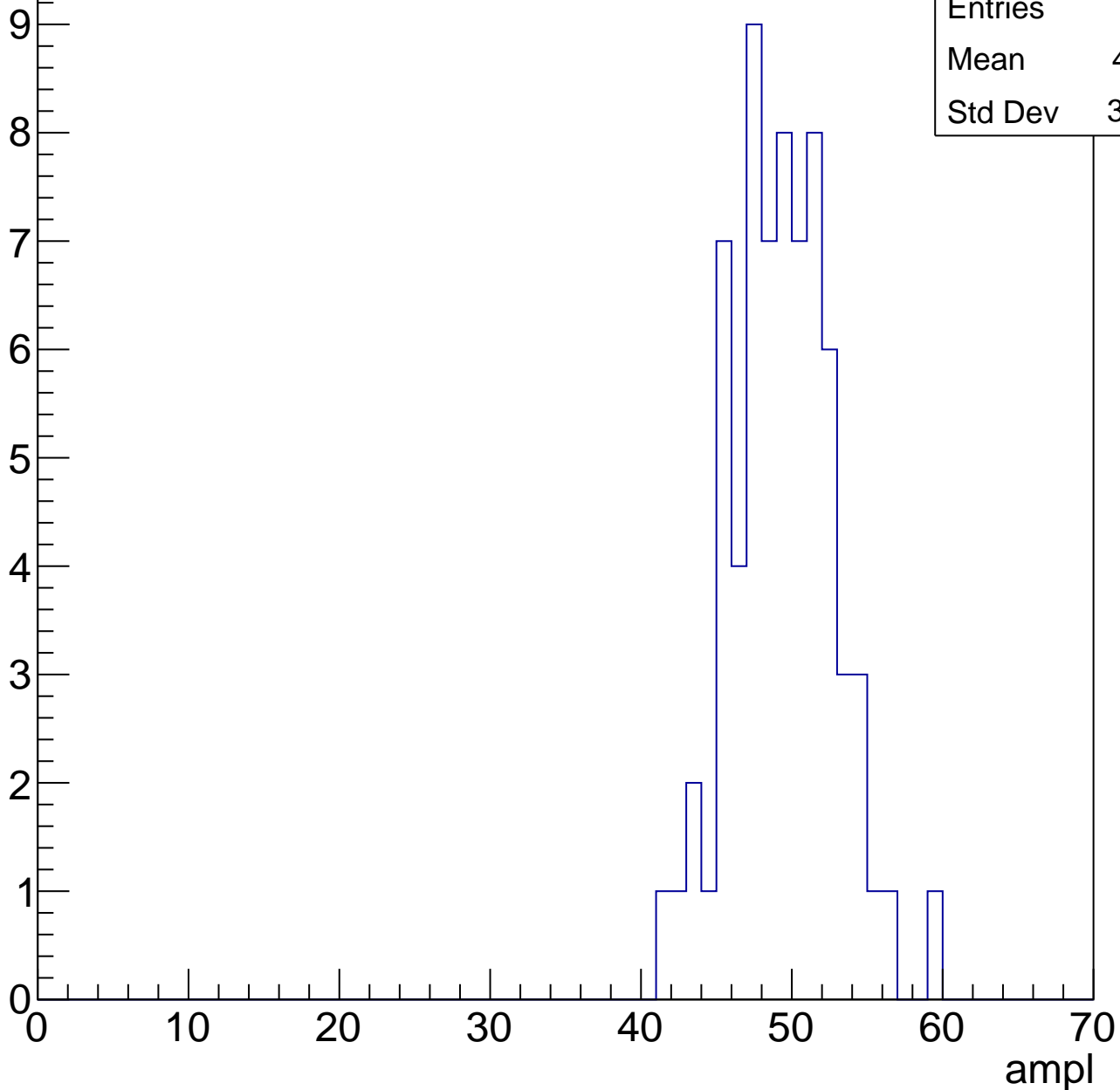


B1L103S, U21-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

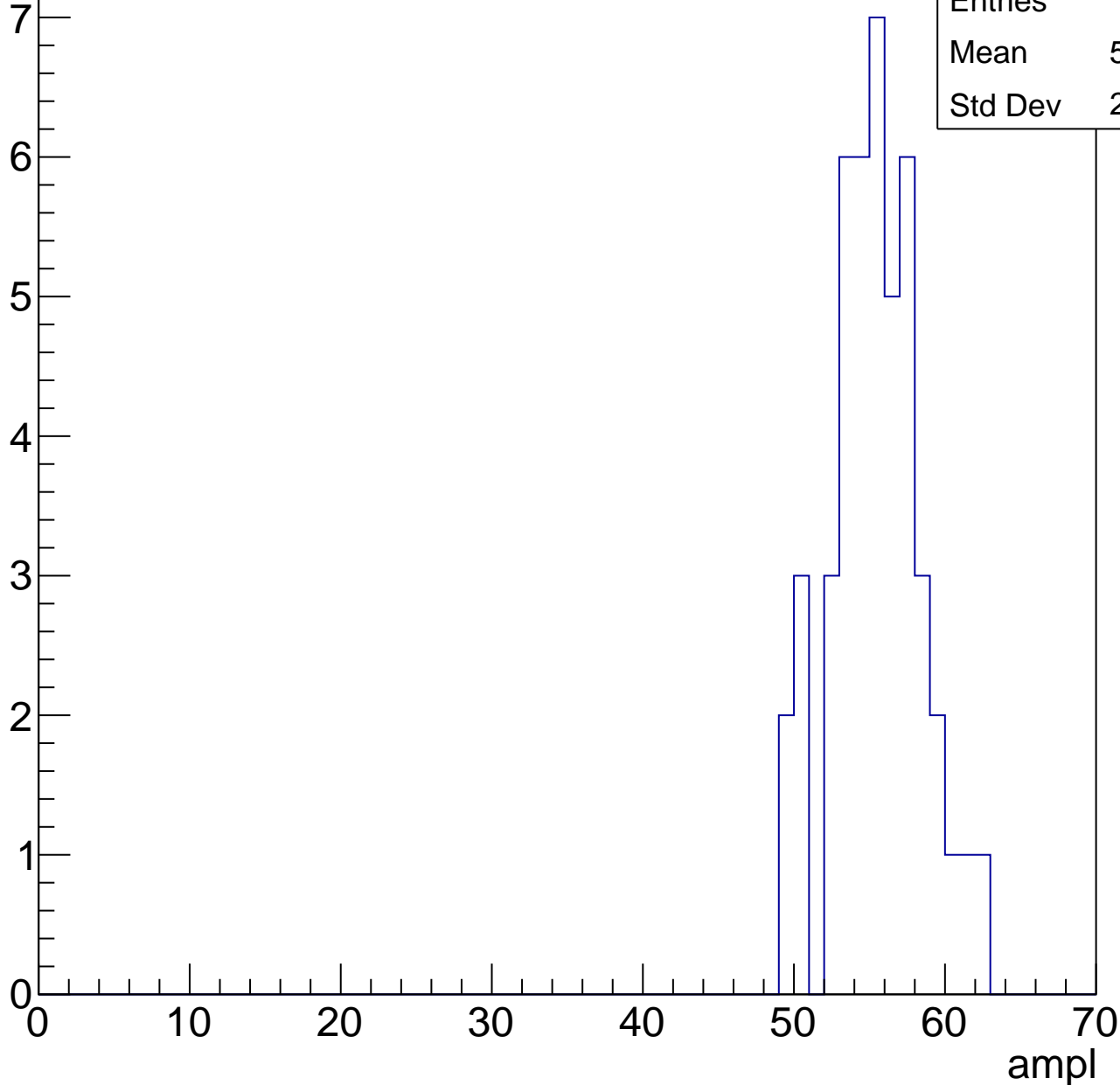
Entries	70
Mean	48.91
Std Dev	3.384



B1L103S, U21-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

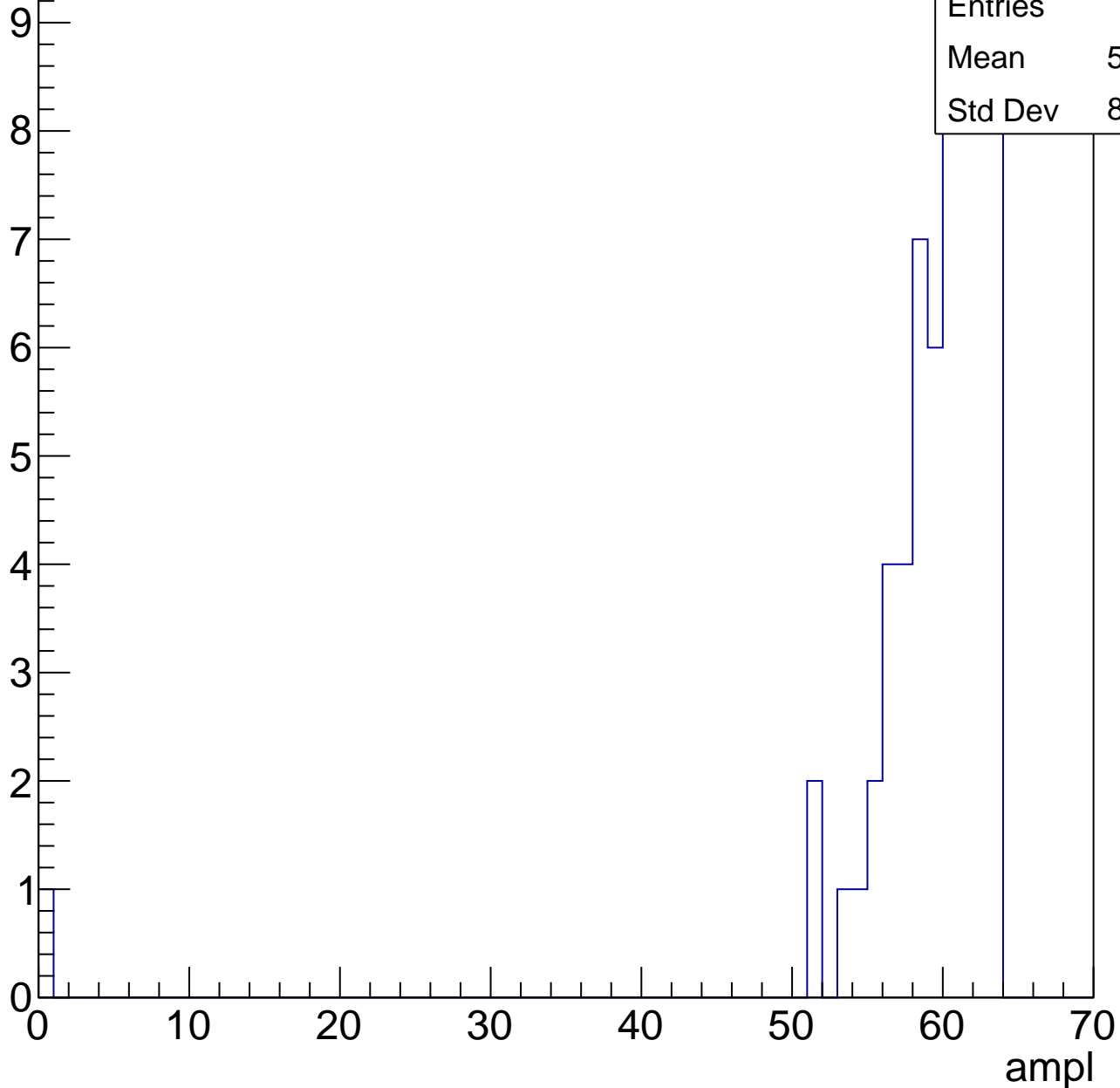


Entries	46
Mean	54.96
Std Dev	2.956

B1L103S, U21-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch10, adc0

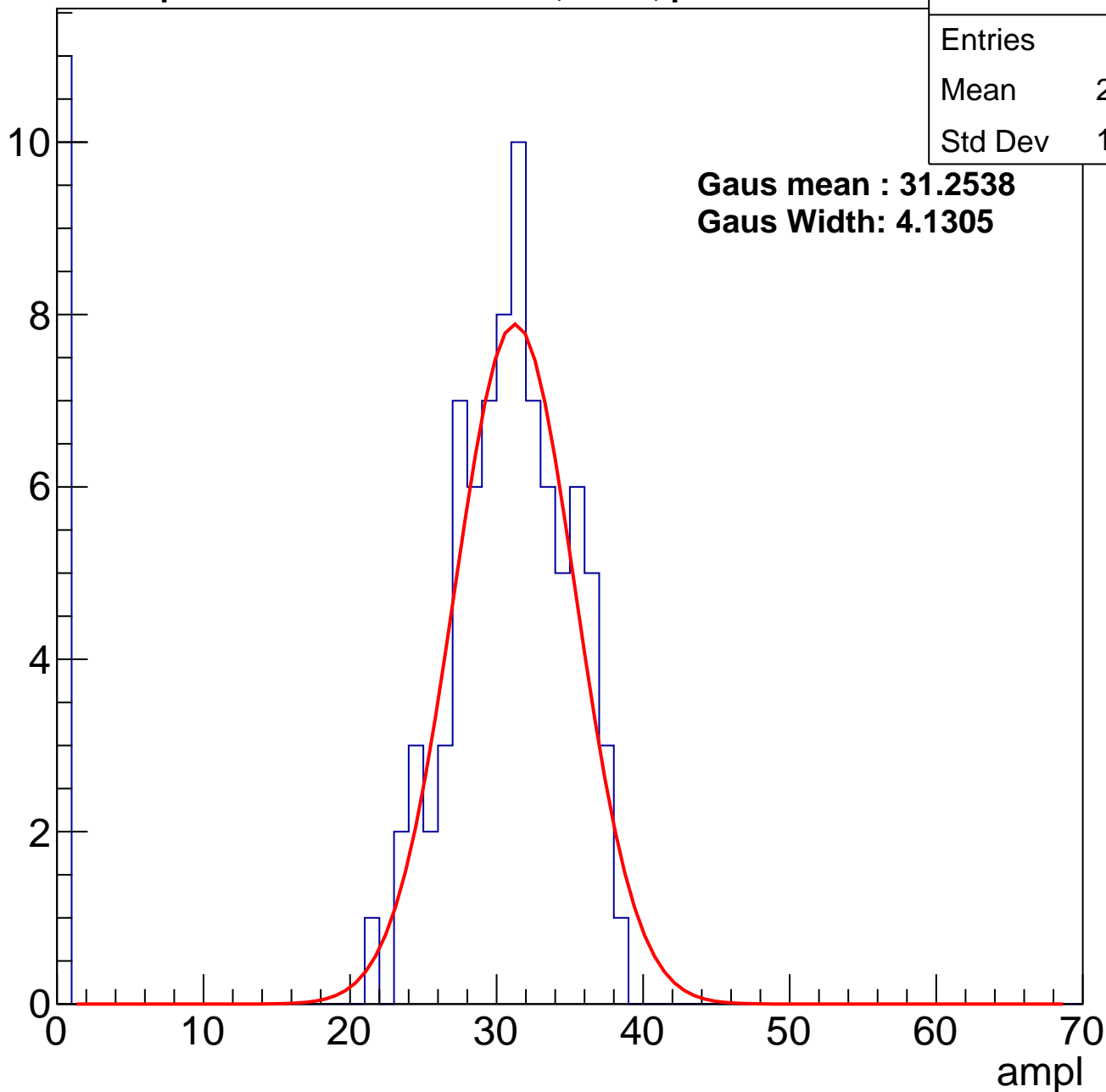
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	26.97
Std Dev	10.49

Gaus mean : 31.2538

Gaus Width: 4.1305

Entry



B1L103S, U21-ch10, adc1

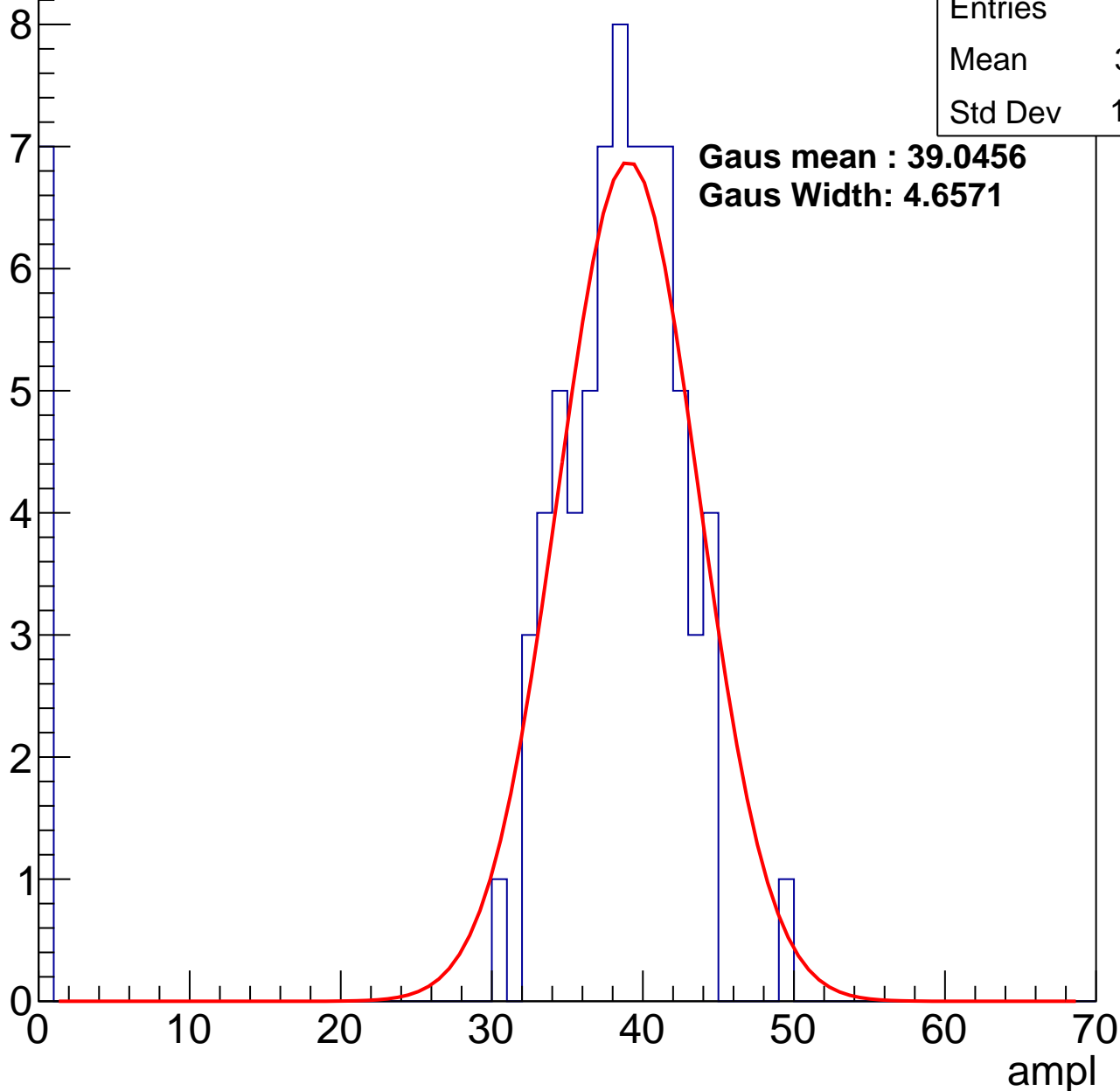
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.81
Std Dev	11.46

Gaus mean : 39.0456

Gaus Width: 4.6571



B1L103S, U21-ch10, adc2

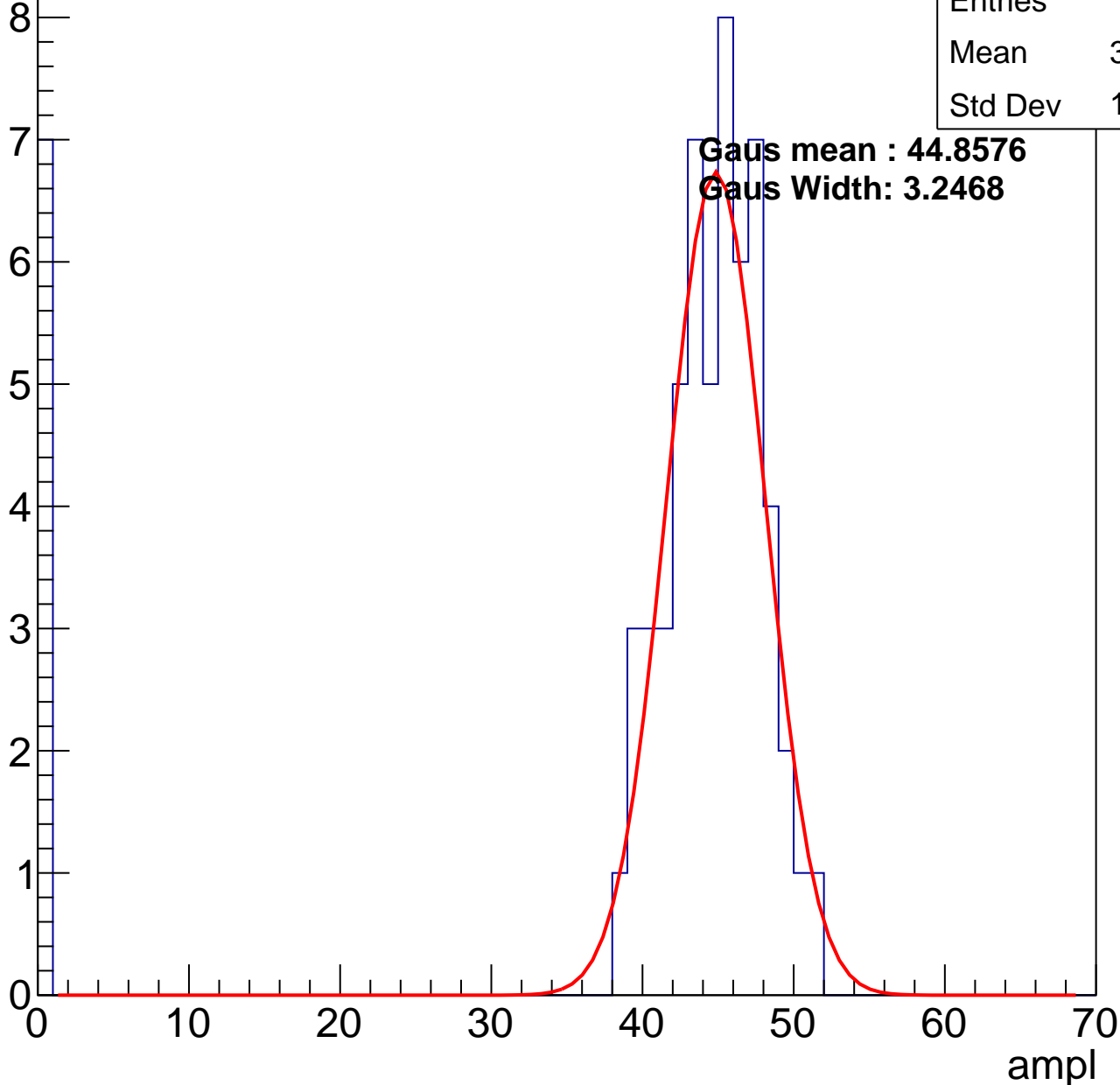
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	39.44
Std Dev	14.23

Gaus mean : 44.8576

Gaus Width: 3.2468

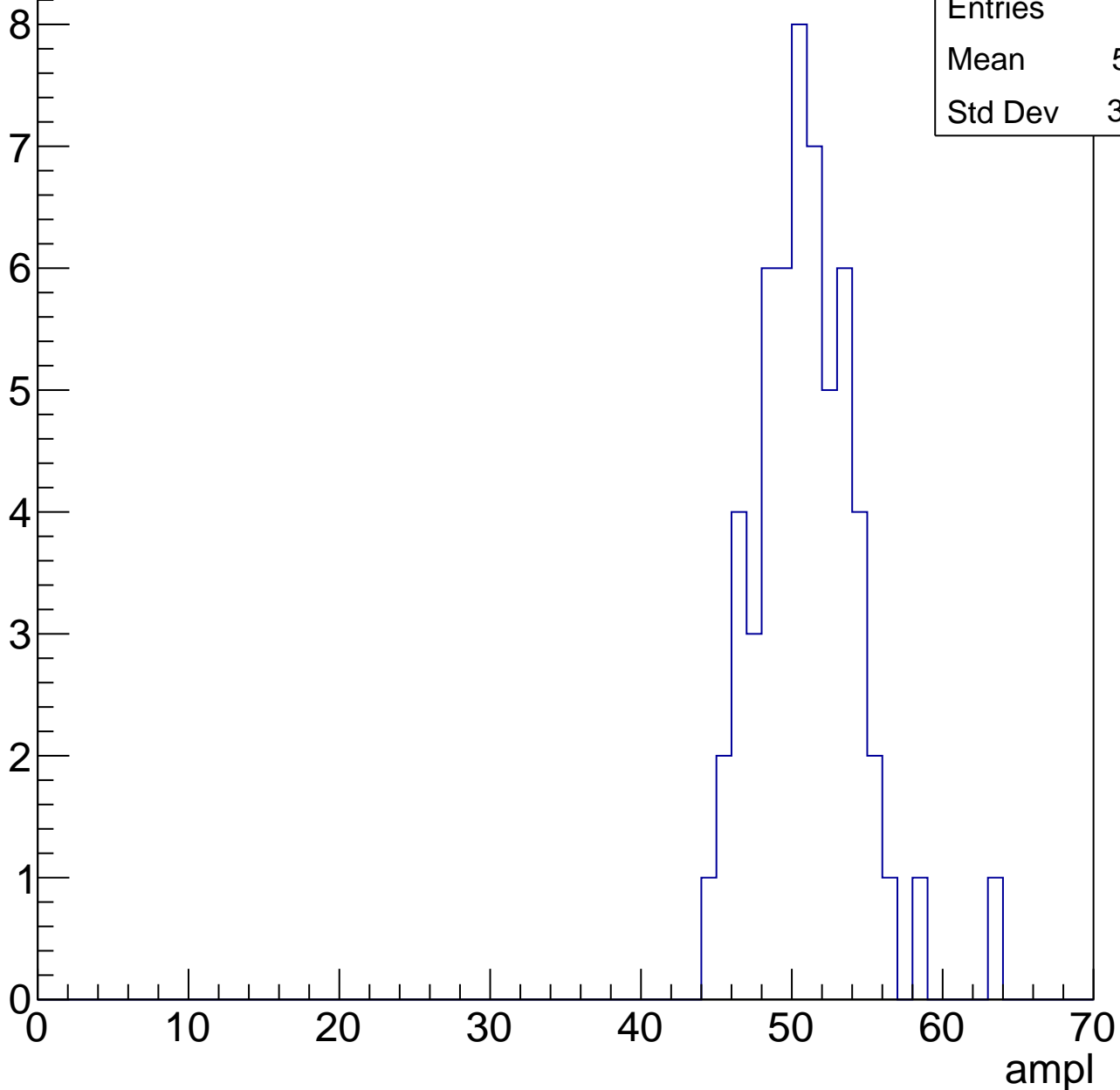


B1L103S, U21-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

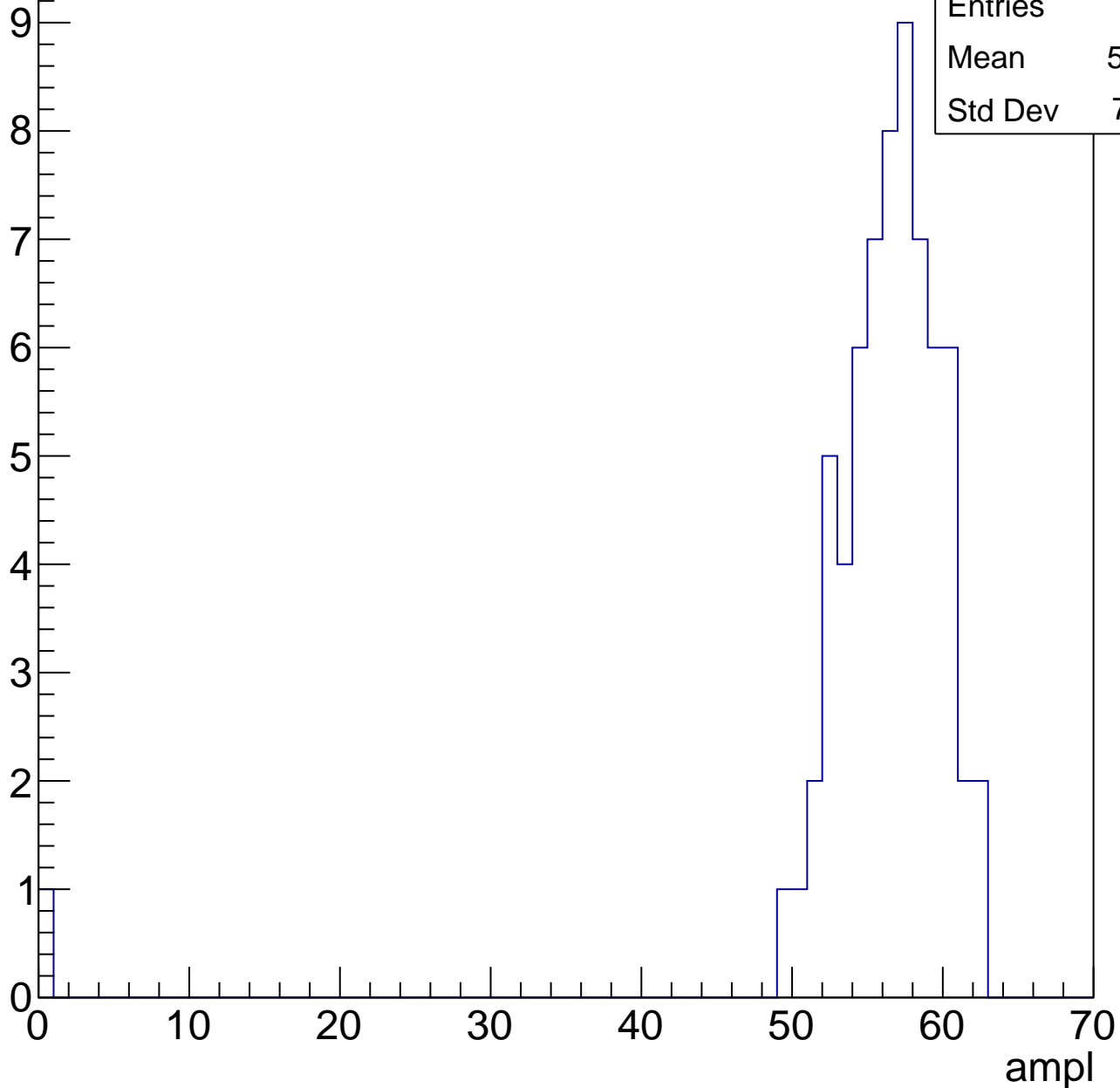
Entries	57
Mean	50.51
Std Dev	3.393



B1L103S, U21-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

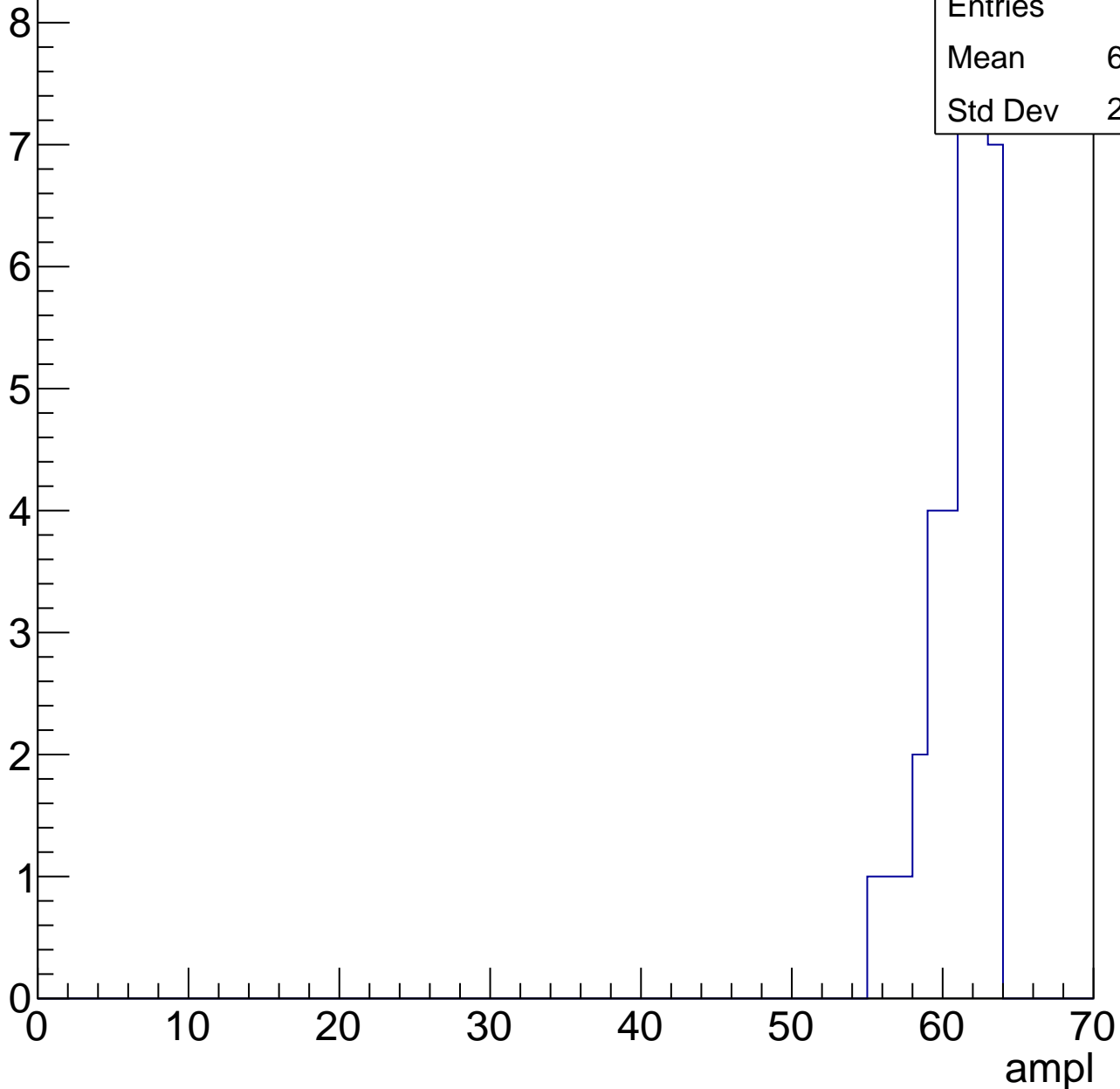


B1L103S, U21-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	60.69
Std Dev	2.025



B1L103S, U21-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L103S, U21-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch11, adc0

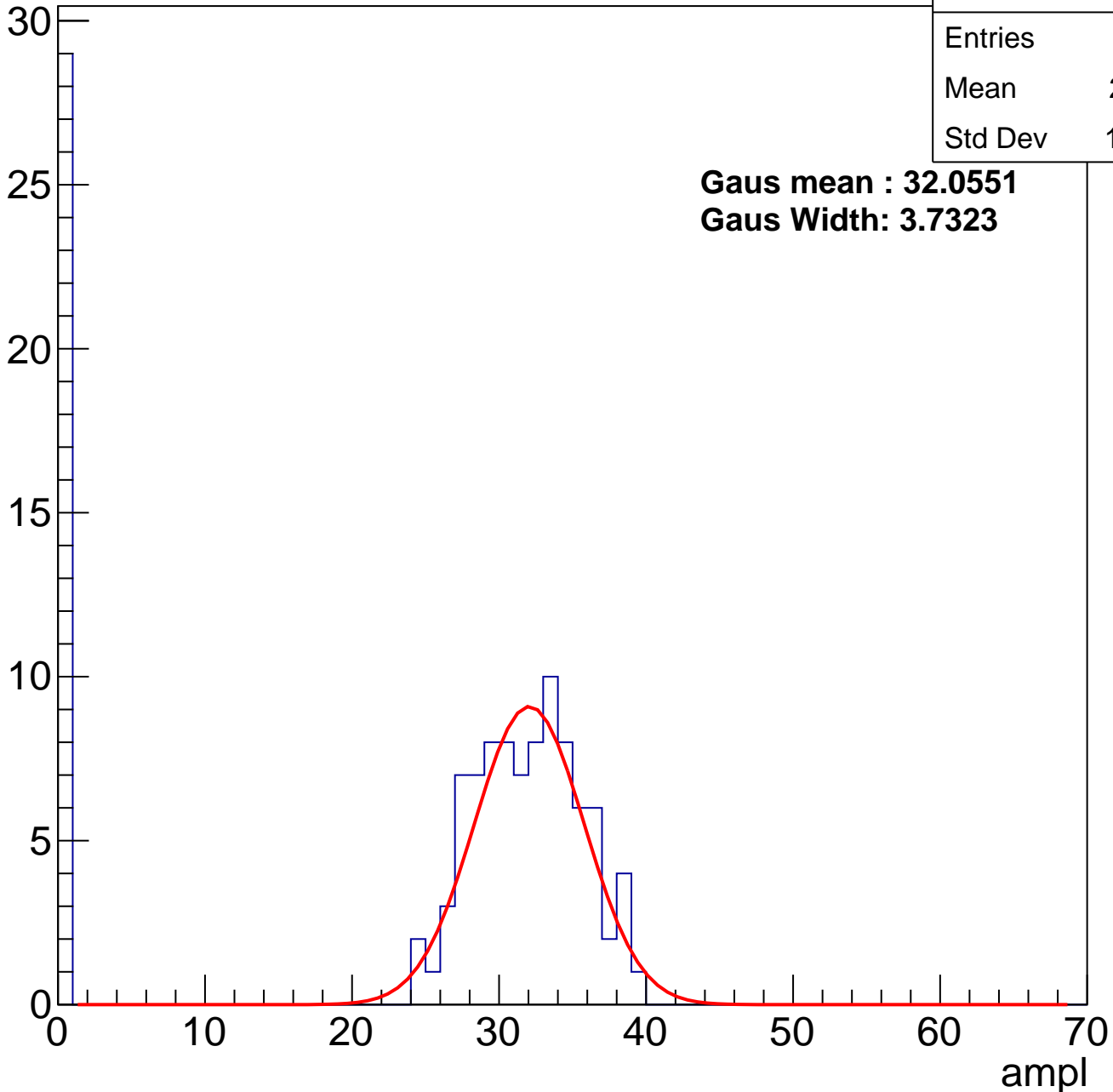
calib_packv5_041523_1651.root, FC#0, port C2

Entries	117
Mean	23.71
Std Dev	13.95

Gaus mean : 32.0551

Gaus Width: 3.7323

Entry



B1L103S, U21-ch11, adc1

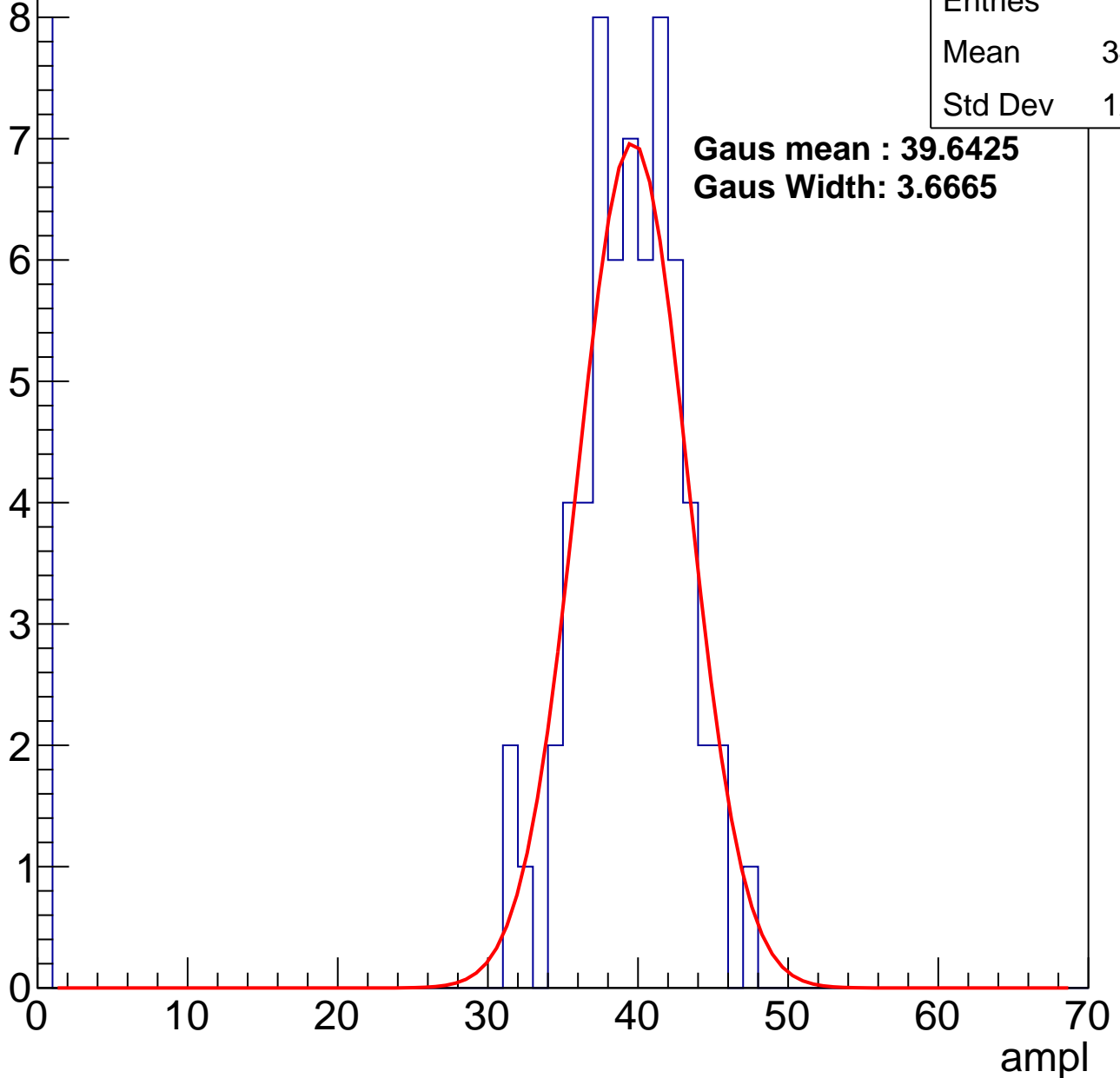
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.65
Std Dev	12.74

Gaus mean : 39.6425

Gaus Width: 3.6665



B1L103S, U21-ch11, adc2

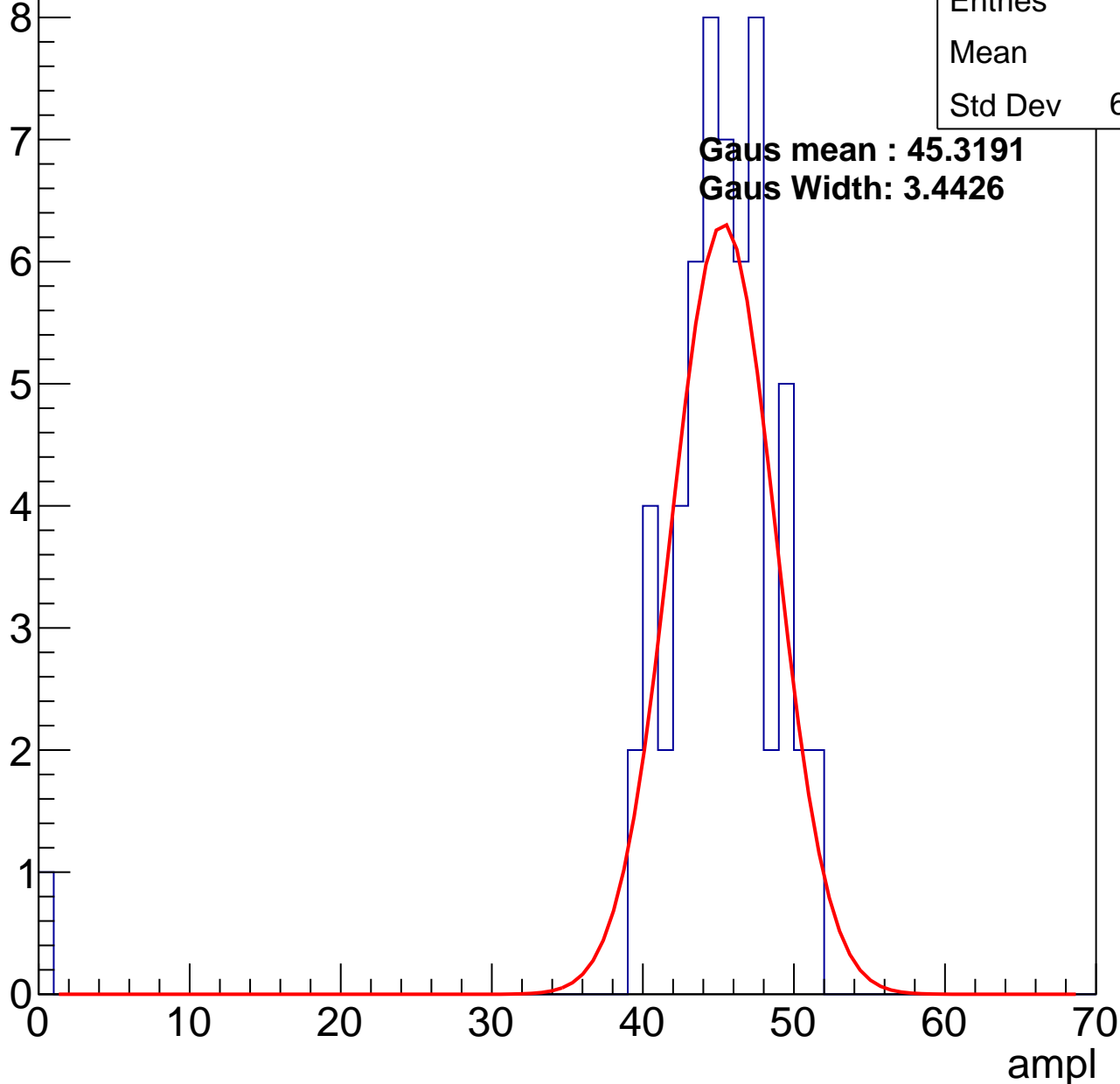
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	44.2
Std Dev	6.532

Gaus mean : 45.3191

Gaus Width: 3.4426

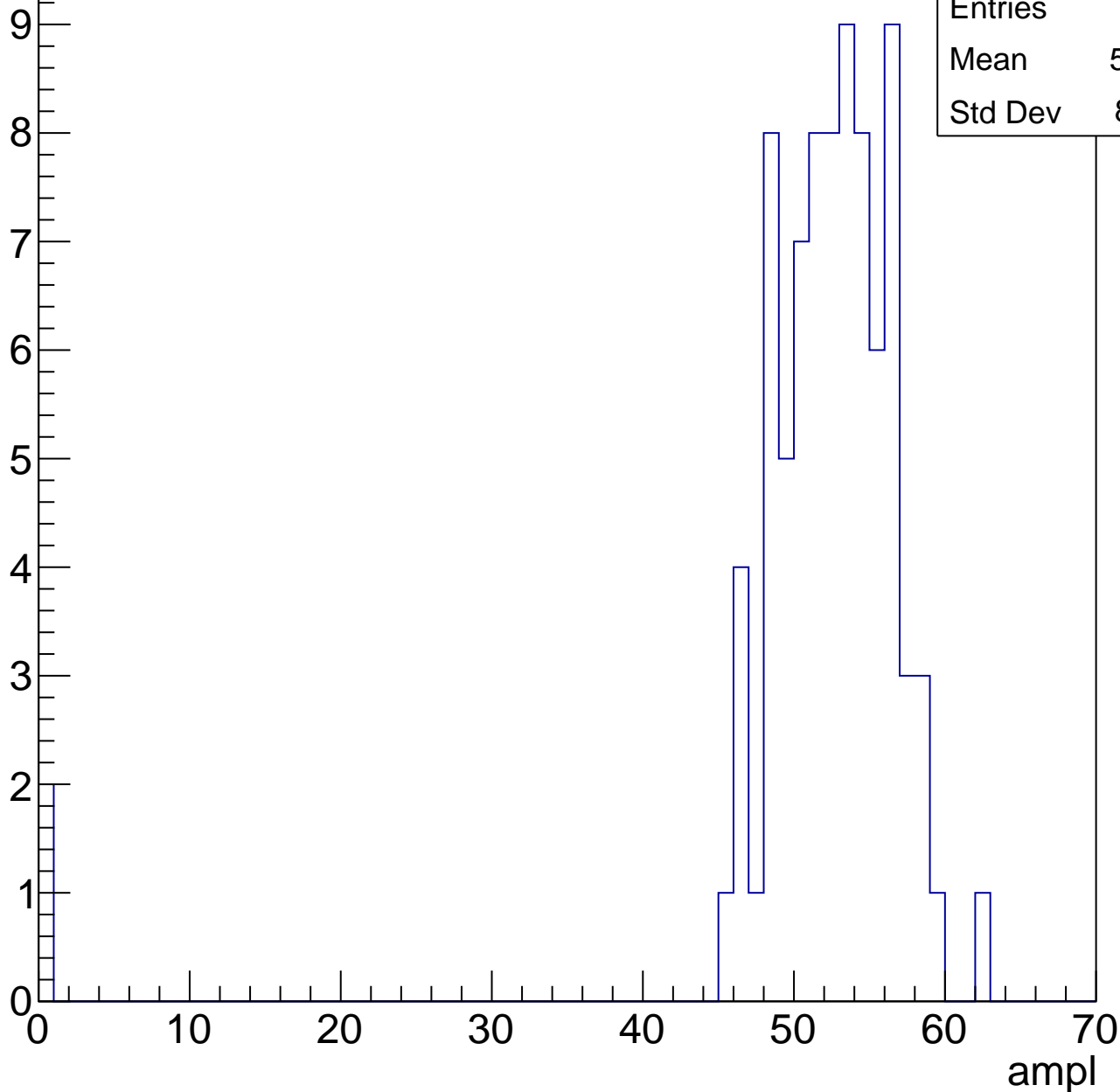


B1L103S, U21-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	51.05
Std Dev	8.681

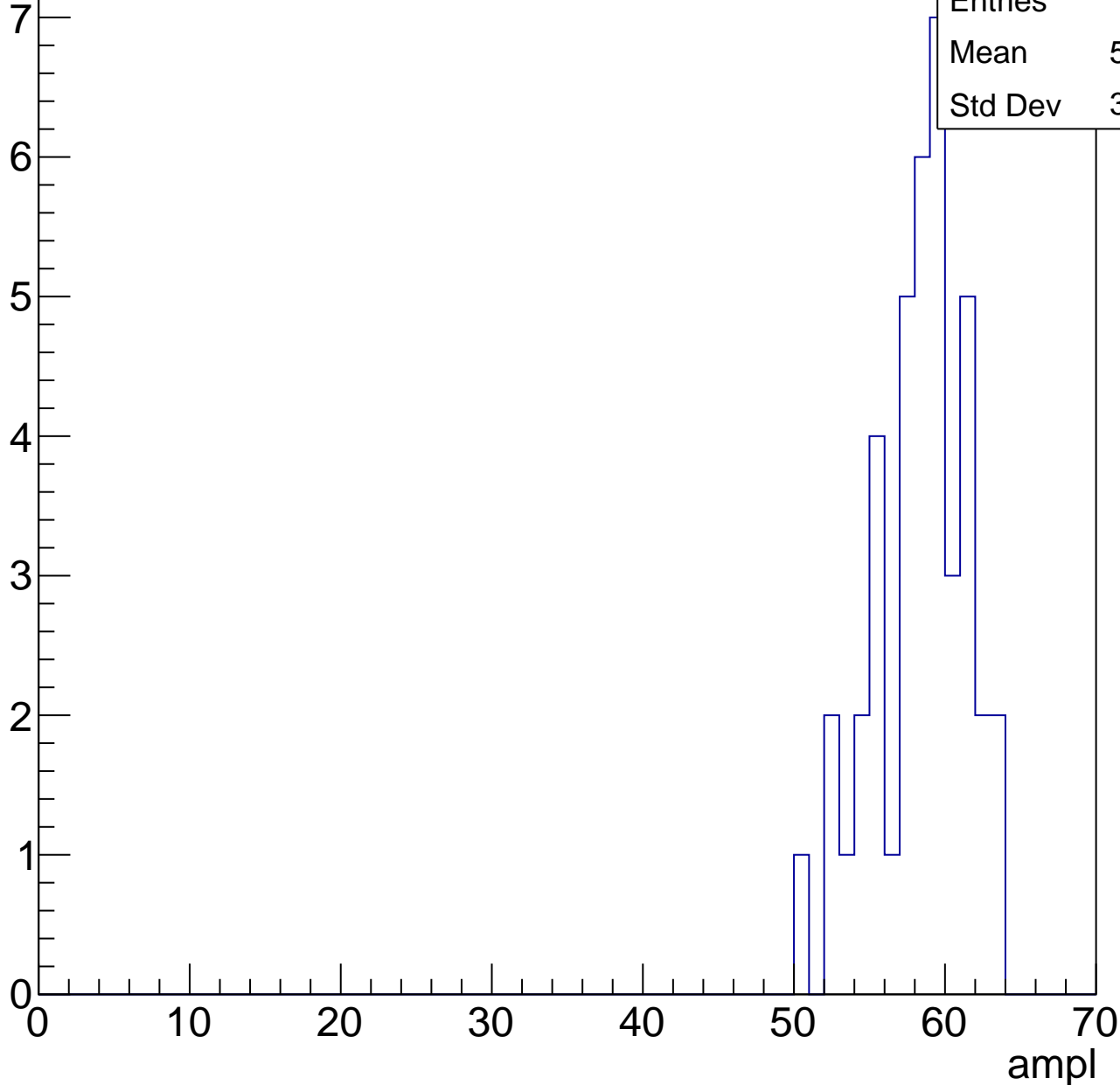


B1L103S, U21-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

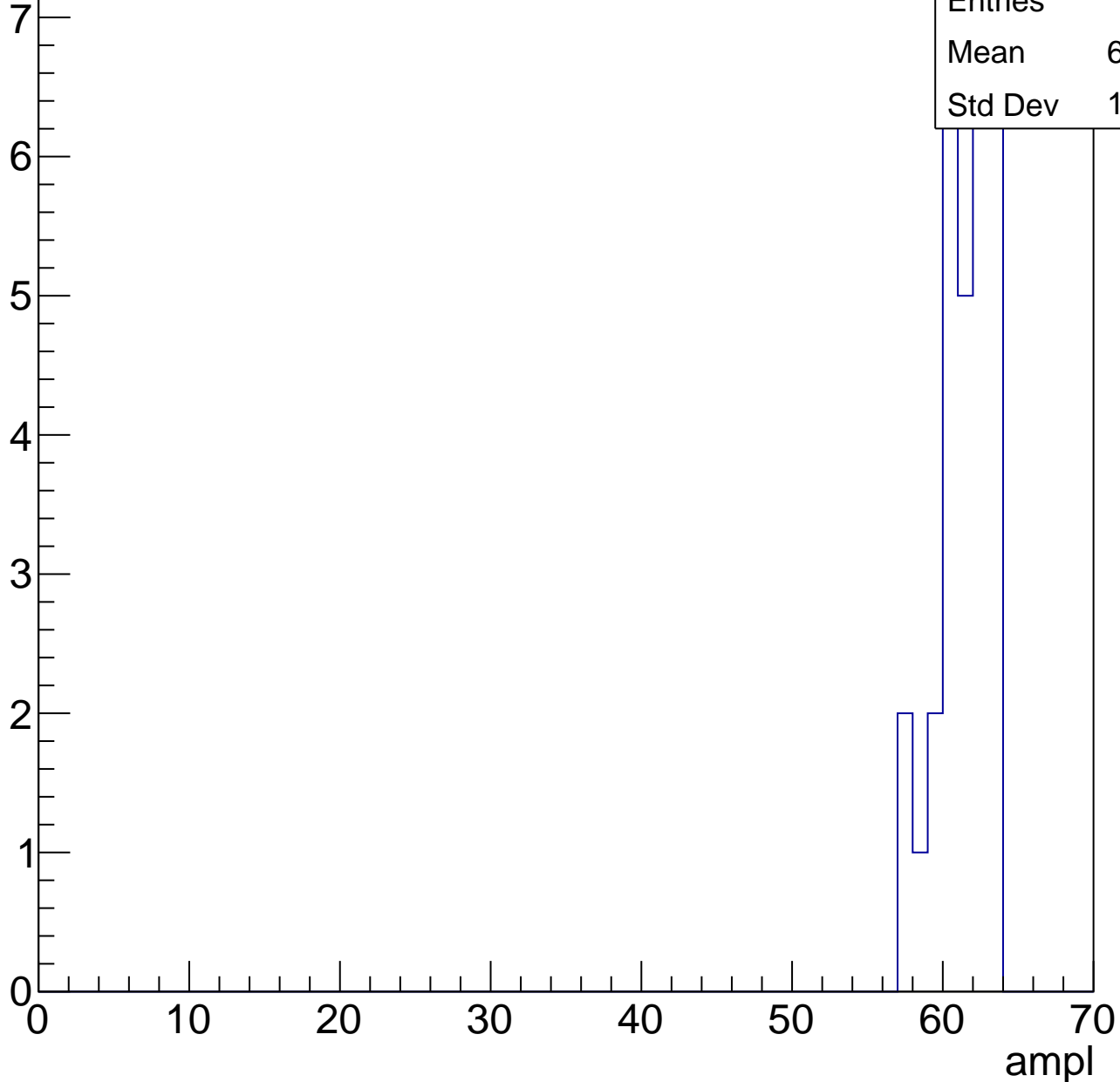
Entries	41
Mean	57.85
Std Dev	3.057



B1L103S, U21-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch12, adc0

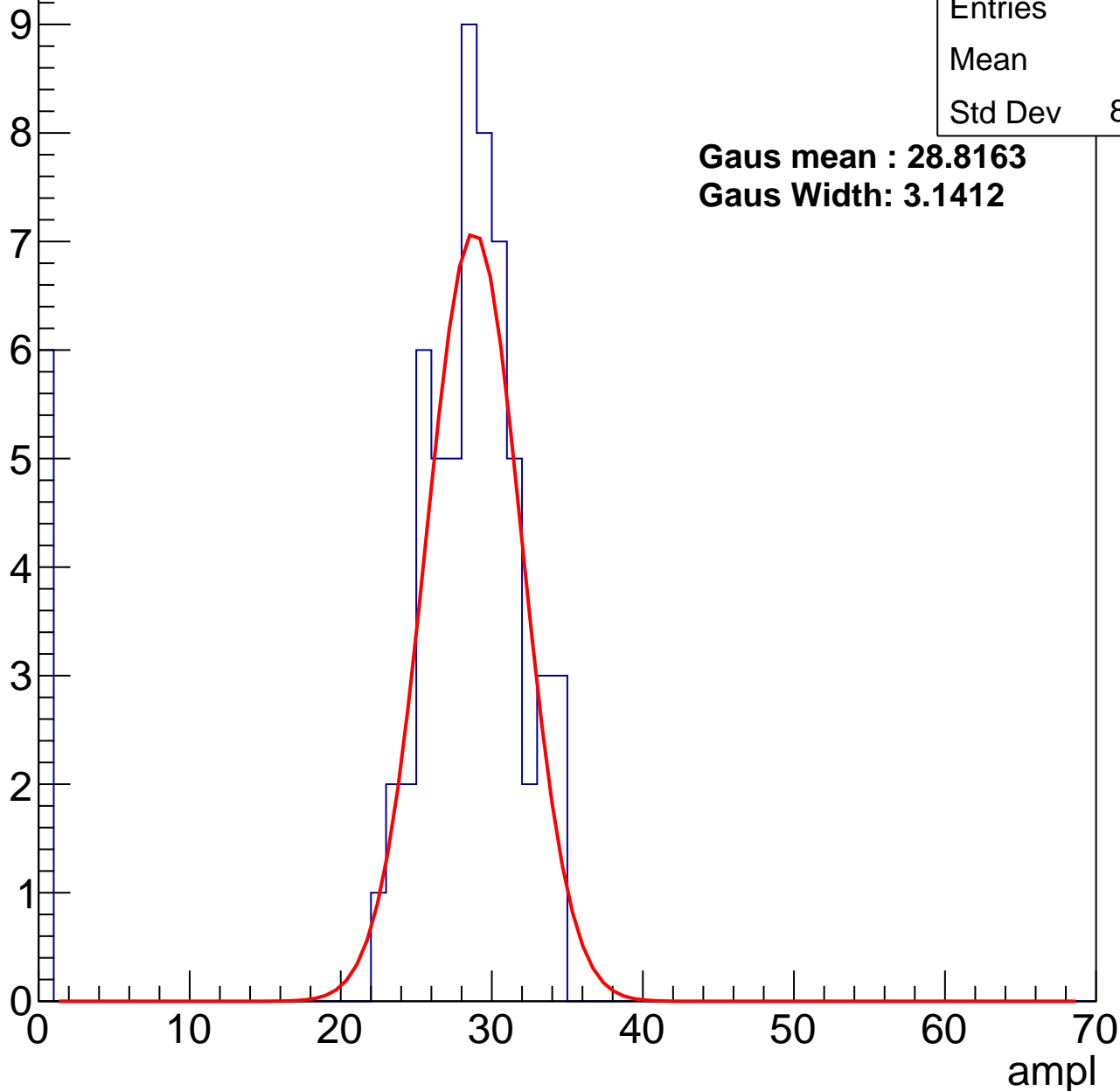
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	25.7
Std Dev	8.714

Gaus mean : 28.8163

Gaus Width: 3.1412



B1L103S, U21-ch12, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	35.43
Std Dev	3.216

Gaus mean : 35.5007

Gaus Width: 3.4035

10

8

6

4

2

0

0

10

20

30

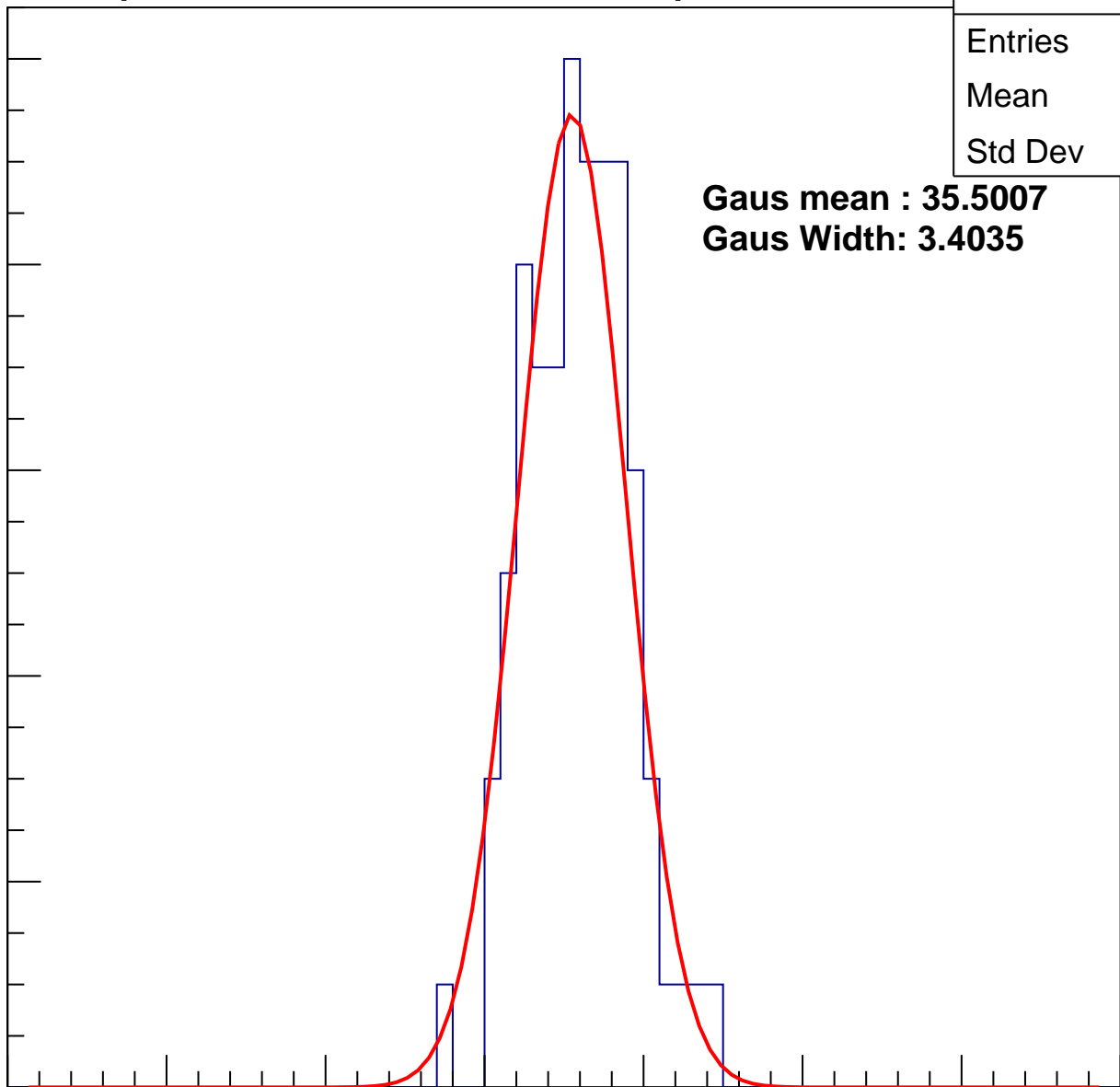
40

50

60

70

ampl



B1L103S, U21-ch12, adc2

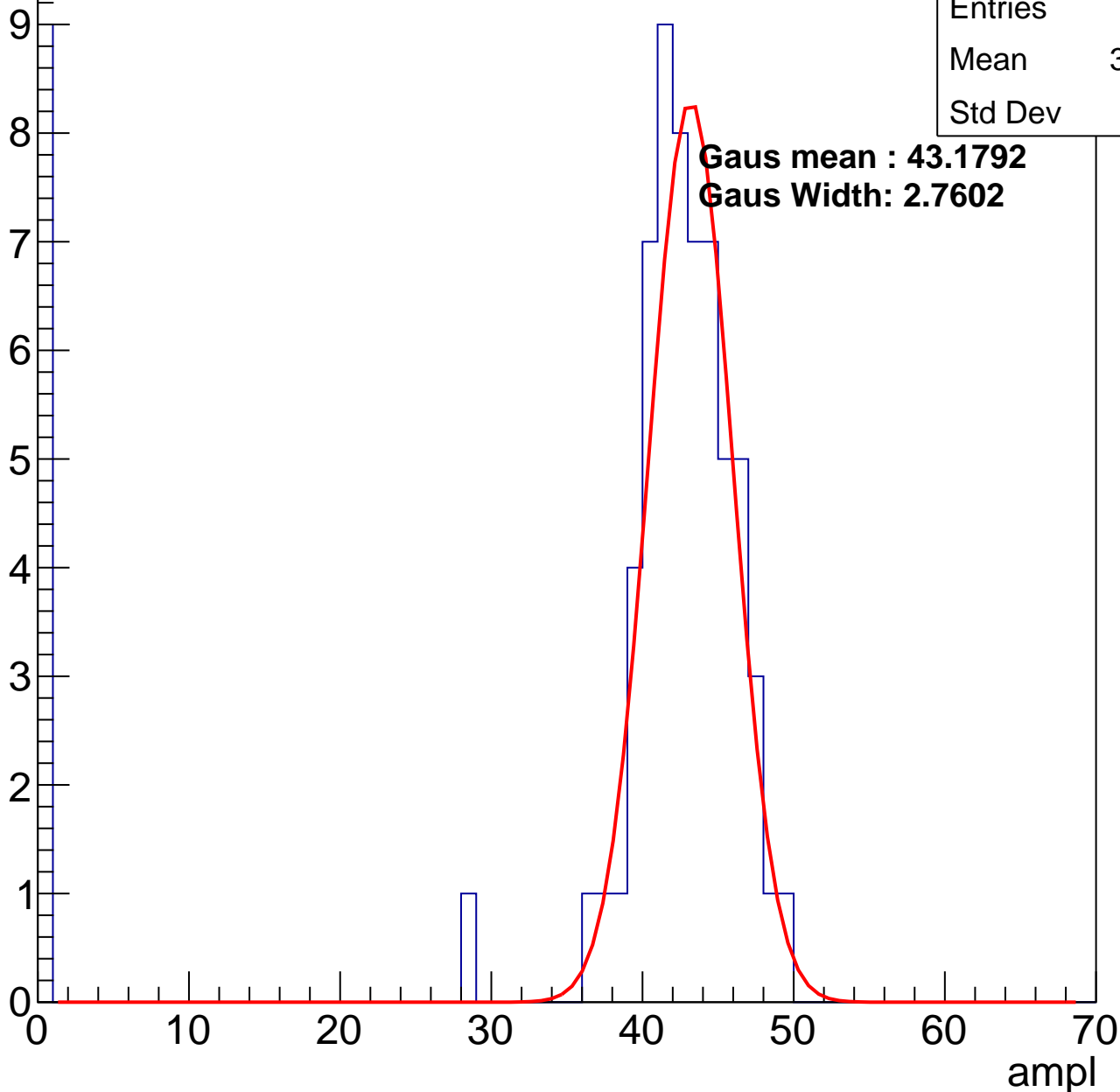
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.89
Std Dev	14.5

Gaus mean : 43.1792

Gaus Width: 2.7602



B1L103S, U21-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	46.68
Std Dev	11.44

Entry

10

8

6

4

2

0

0

10

20

30

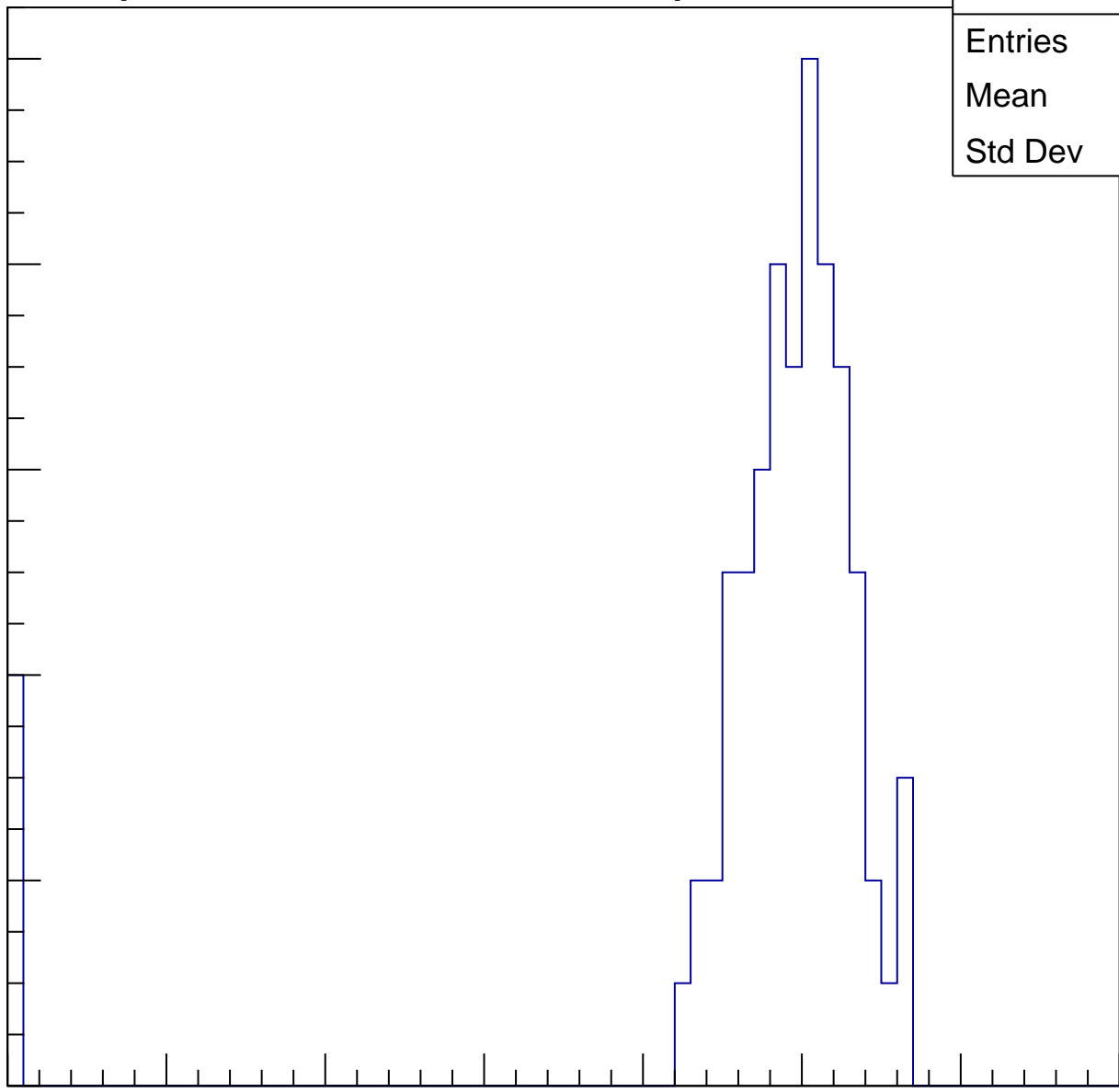
40

50

60

70

ampl

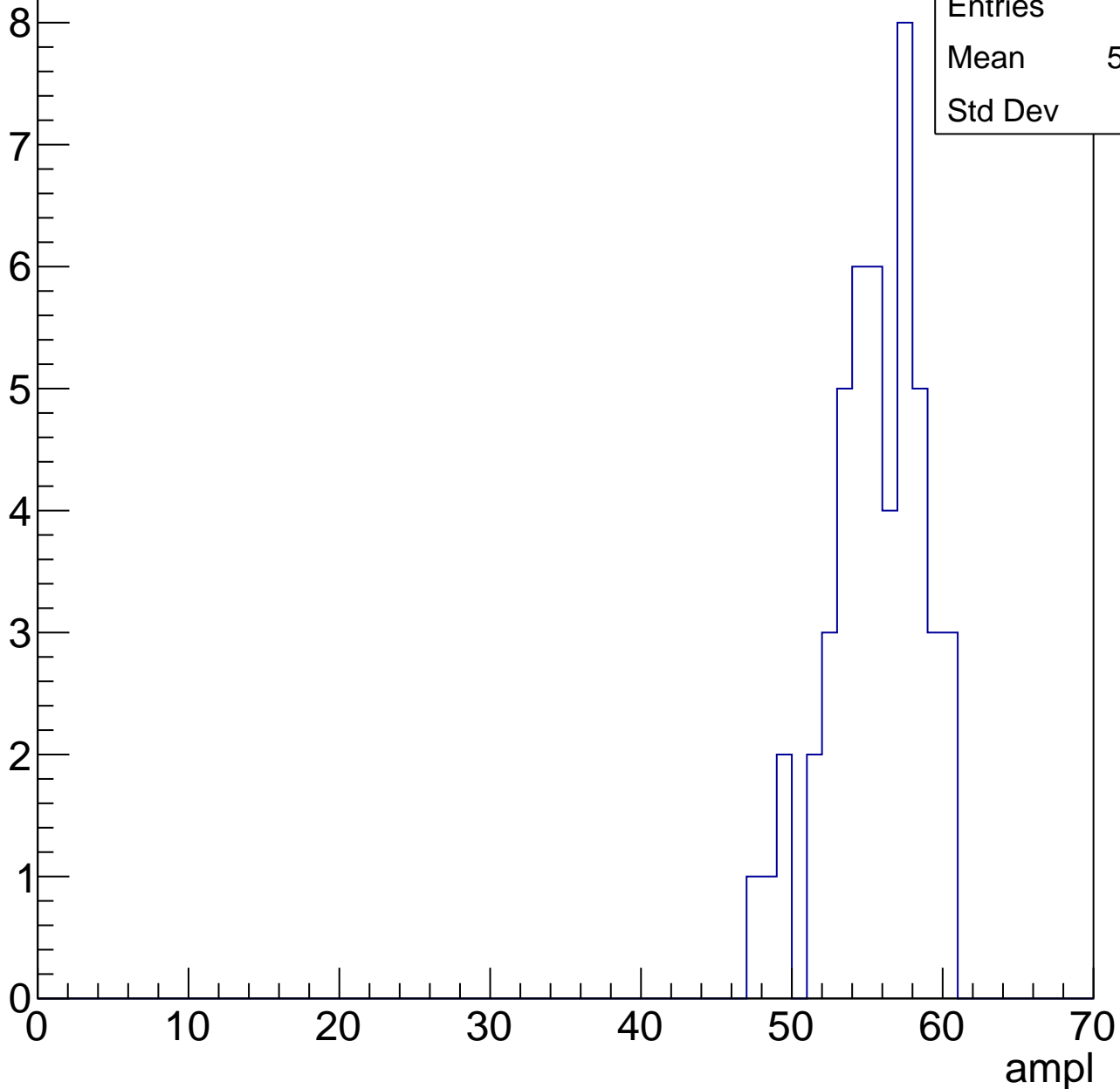


B1L103S, U21-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.04
Std Dev	3.11

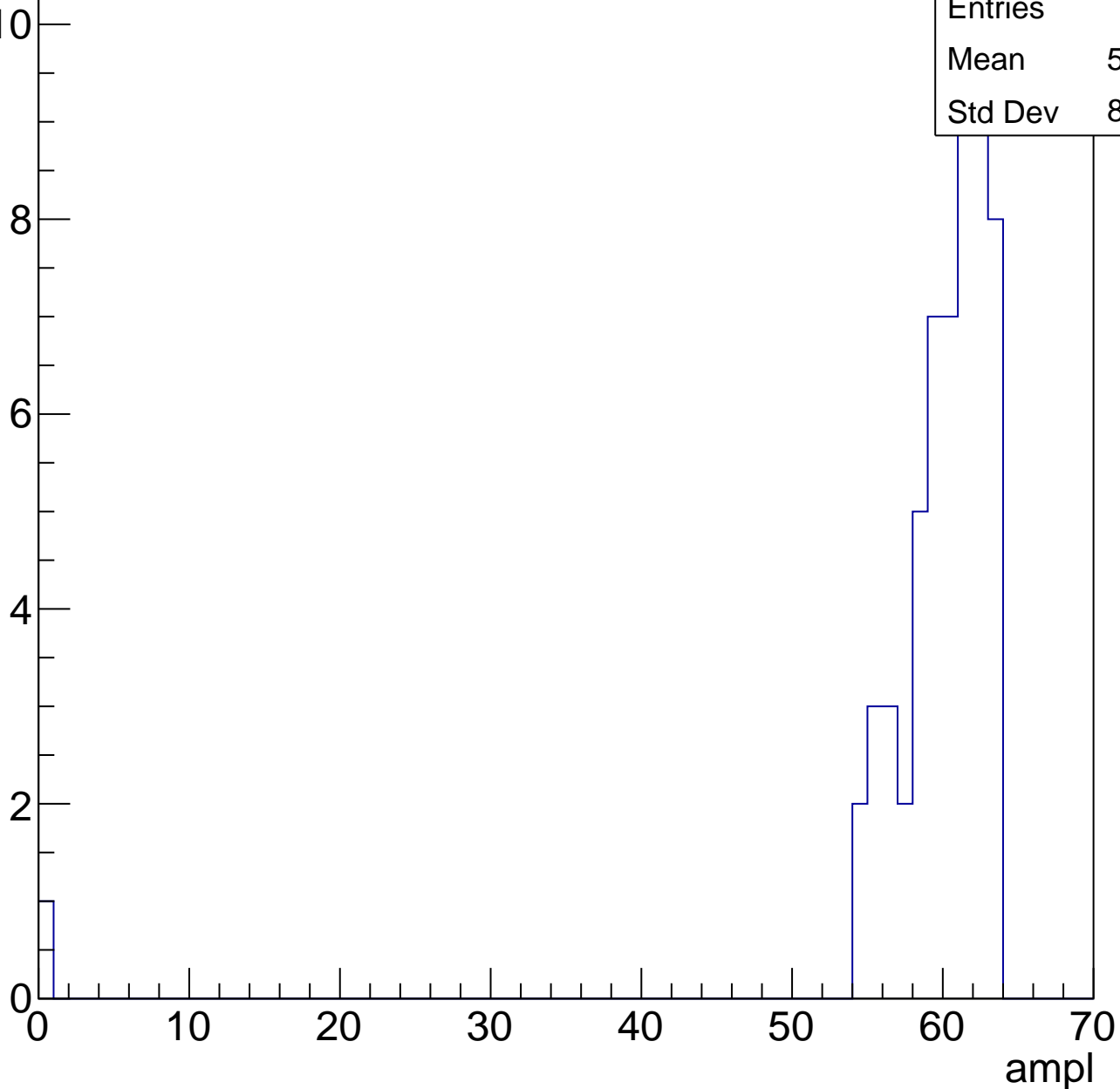


B1L103S, U21-ch12, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.77
Std Dev	8.242



B1L103S, U21-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

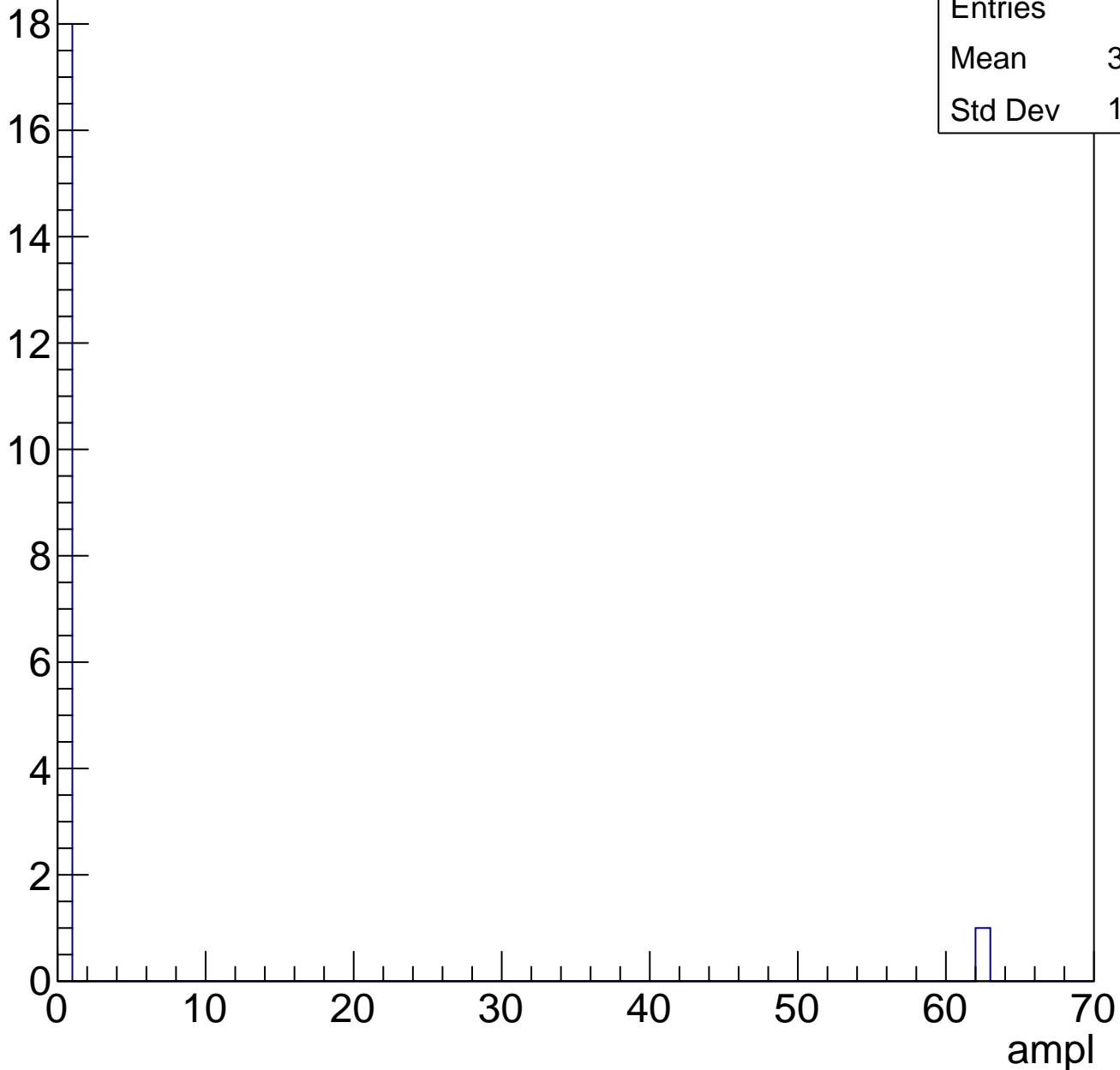


B1L103S, U21-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U21-ch13, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	26.19
Std Dev	12.15

Gaus mean : 32.3276

Gaus Width: 2.4301

Entry

10

8

6

4

2

0

0

10

20

30

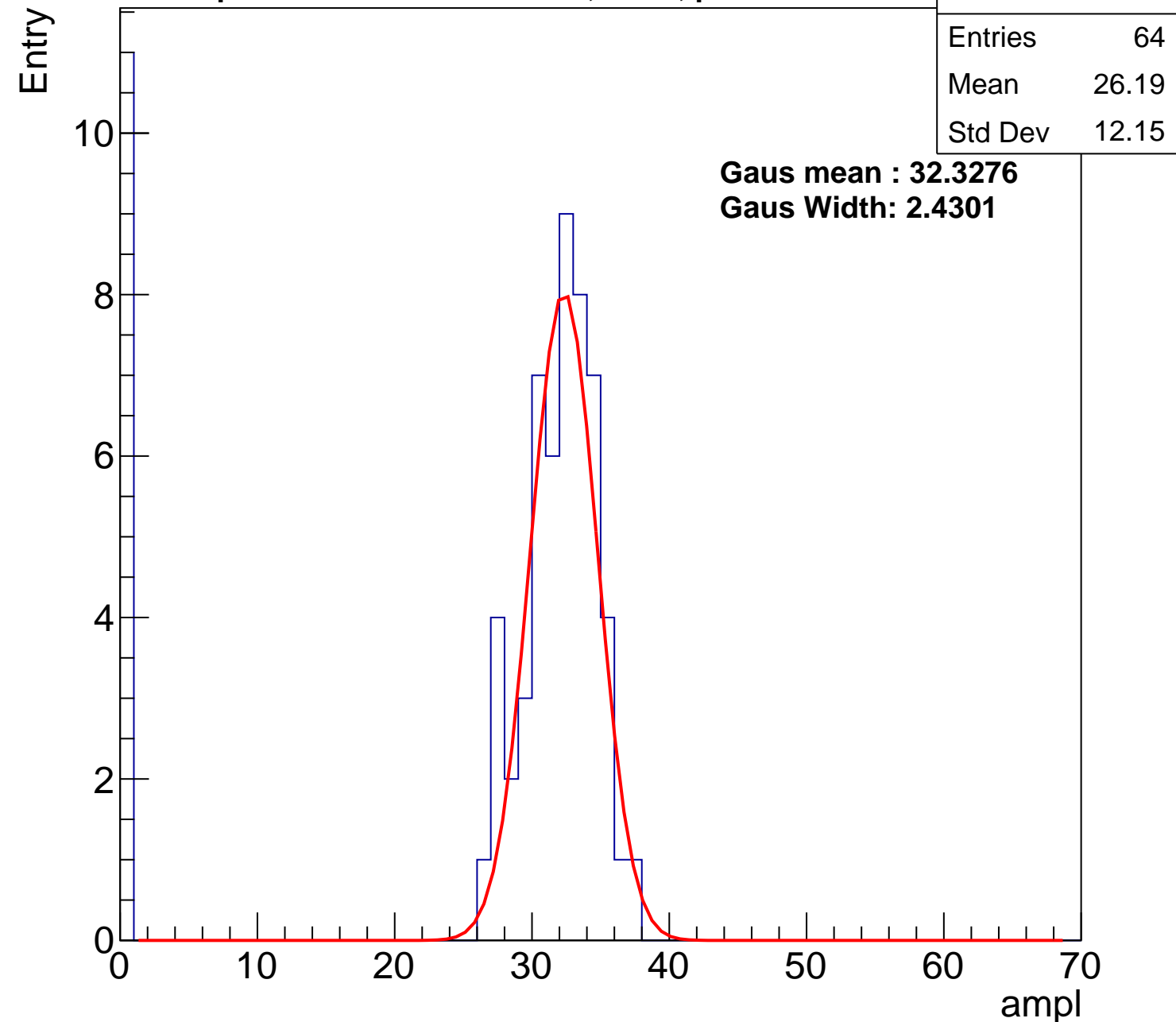
40

50

60

70

ampl



B1L103S, U21-ch13, adc1

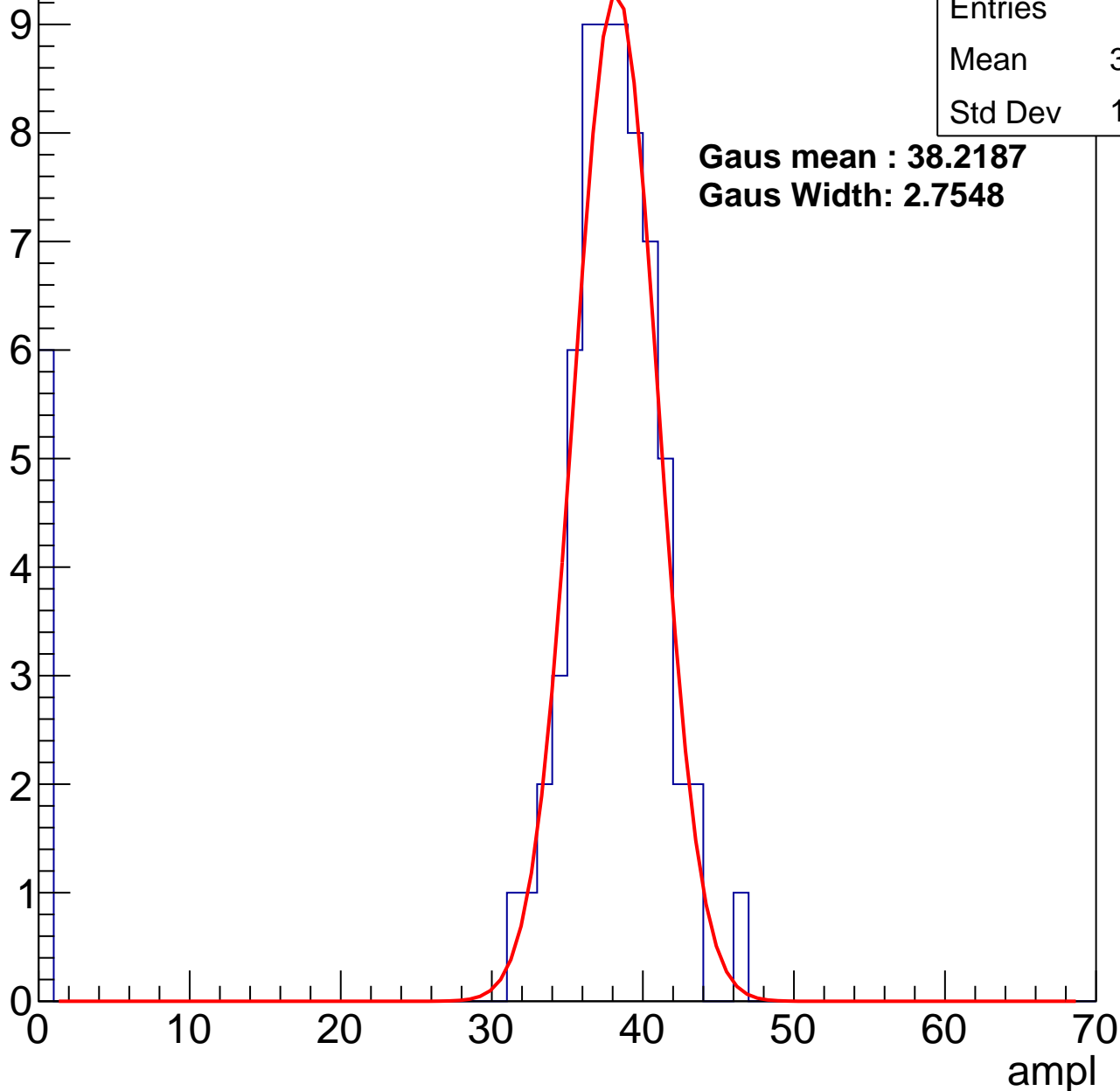
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.55
Std Dev	10.83

Gaus mean : 38.2187

Gaus Width: 2.7548



B1L103S, U21-ch13, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	40.51
Std Dev	13.48

Gaus mean : 45.2477

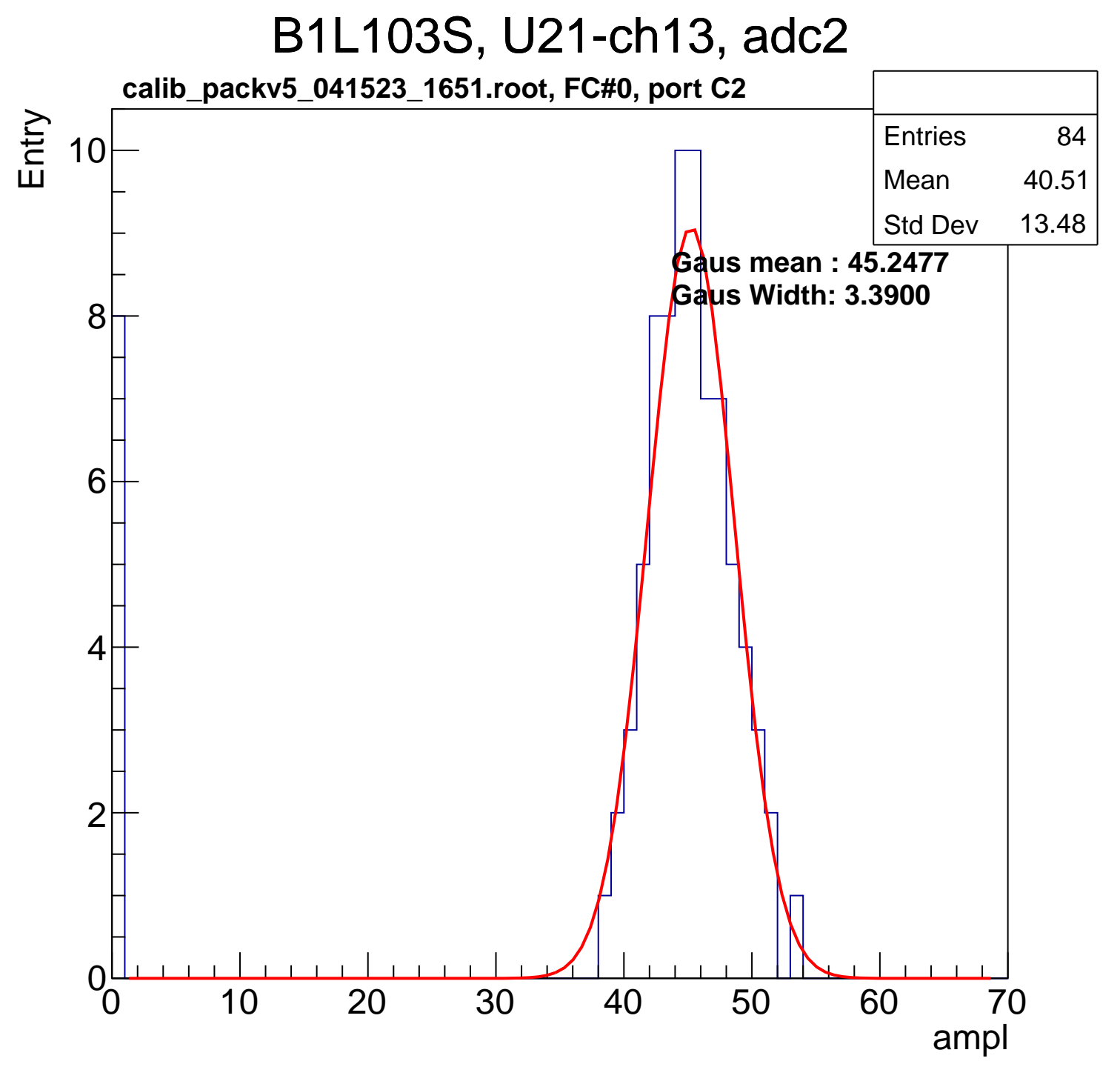
Gaus Width: 3.3900

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

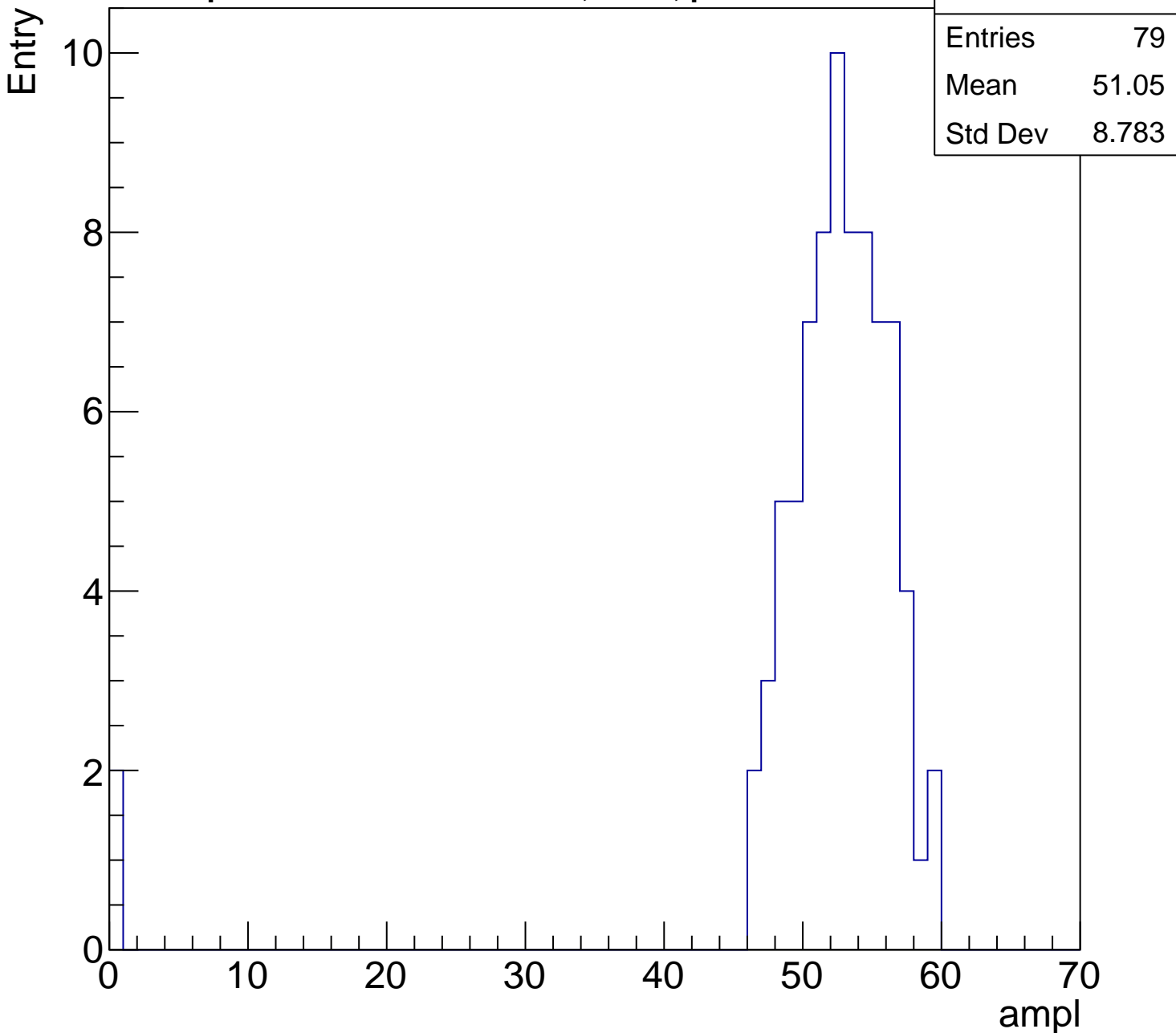
Entries	79
Mean	51.05
Std Dev	8.783

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 47

Mean 57.21

Std Dev 8.92

0

10

20

30

40

50

60

70

ampl

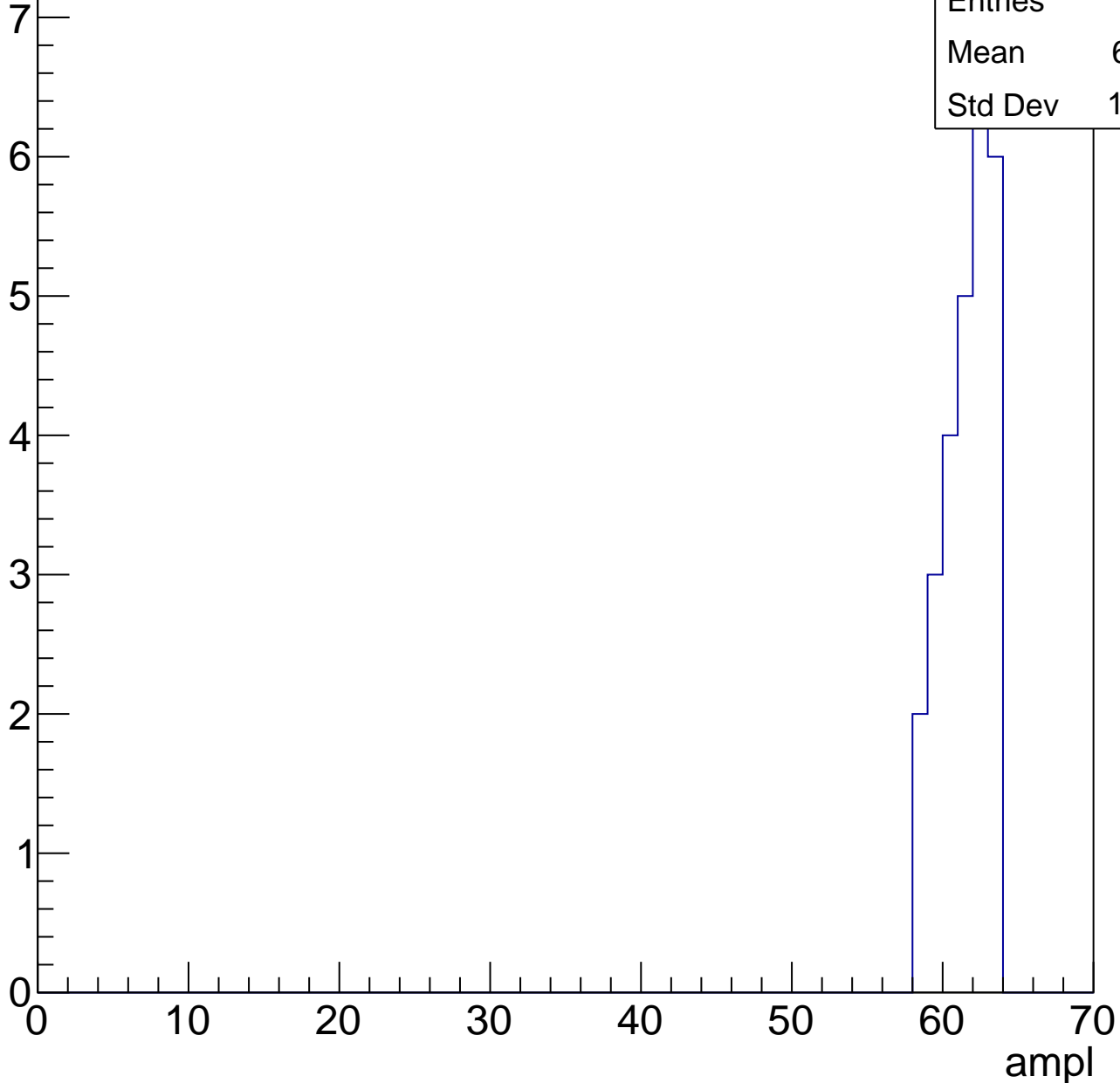
0

B1L103S, U21-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	61.11
Std Dev	1.548



B1L103S, U21-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



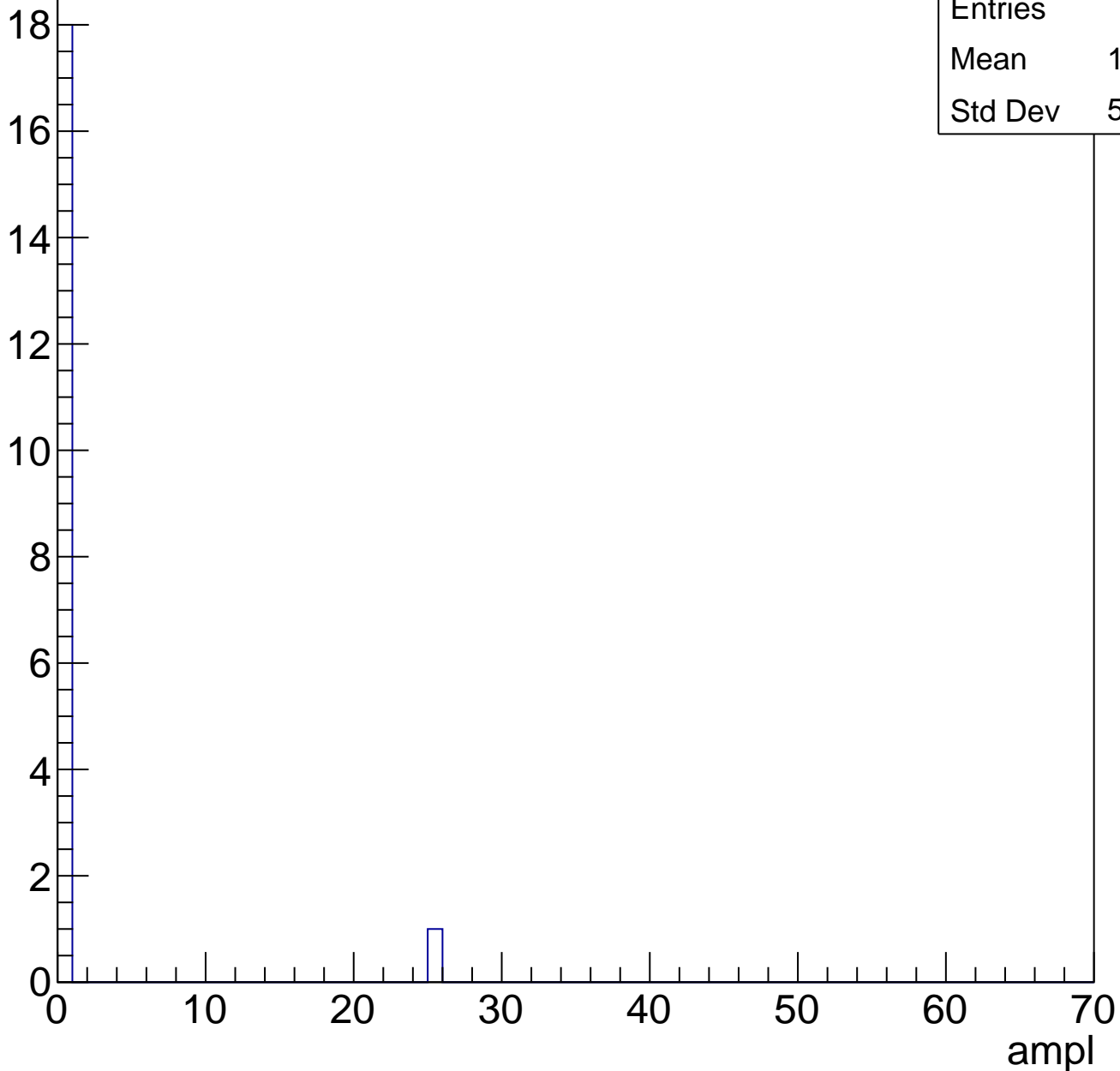
Entries	0
Mean	0
Std Dev	0

B1L103S, U21-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.316
Std Dev	5.582

Entry



B1L103S, U21-ch14, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	27.42
Std Dev	6.489

Gaus mean : 28.4263

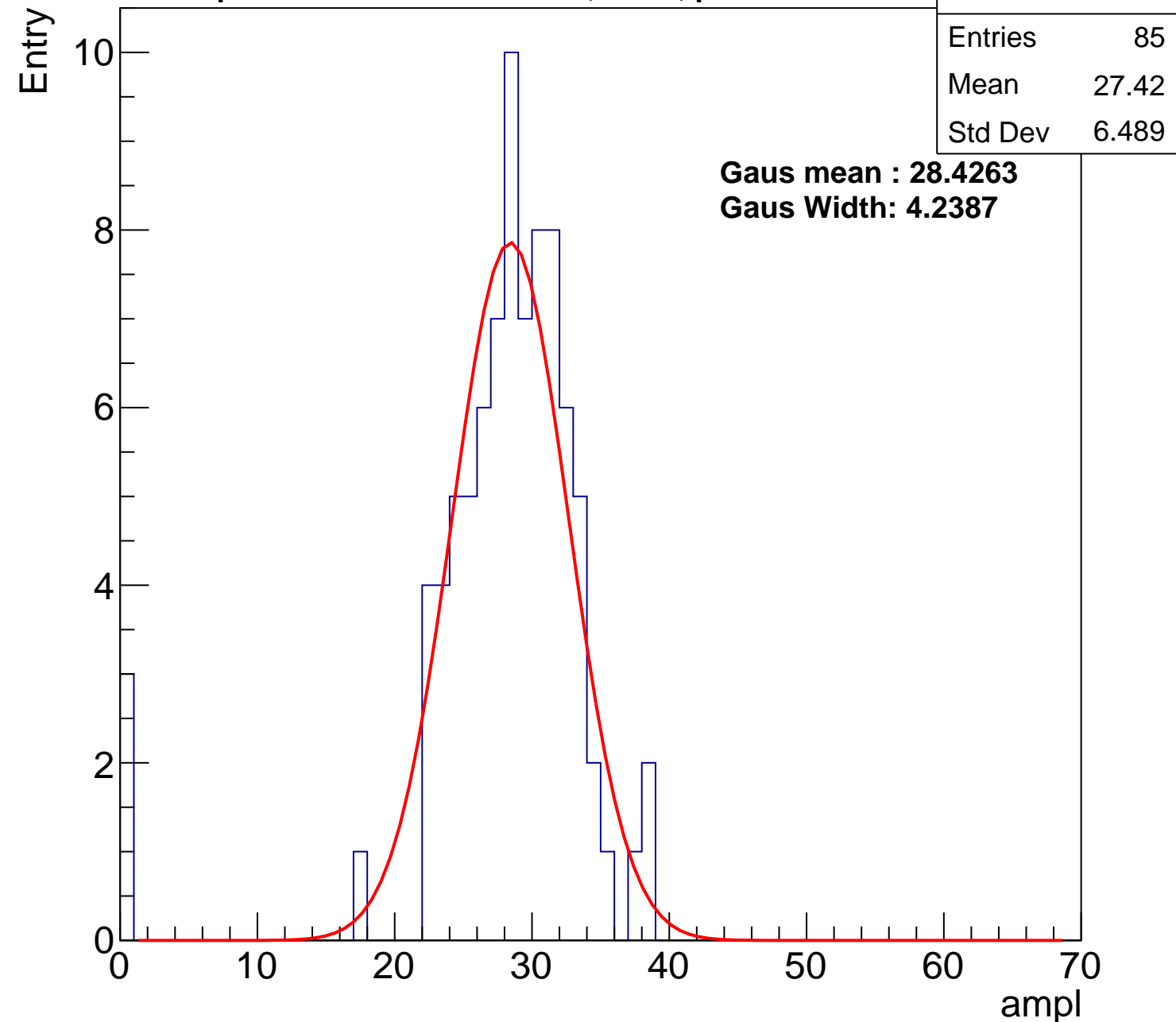
Gaus Width: 4.2387

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch14, adc1

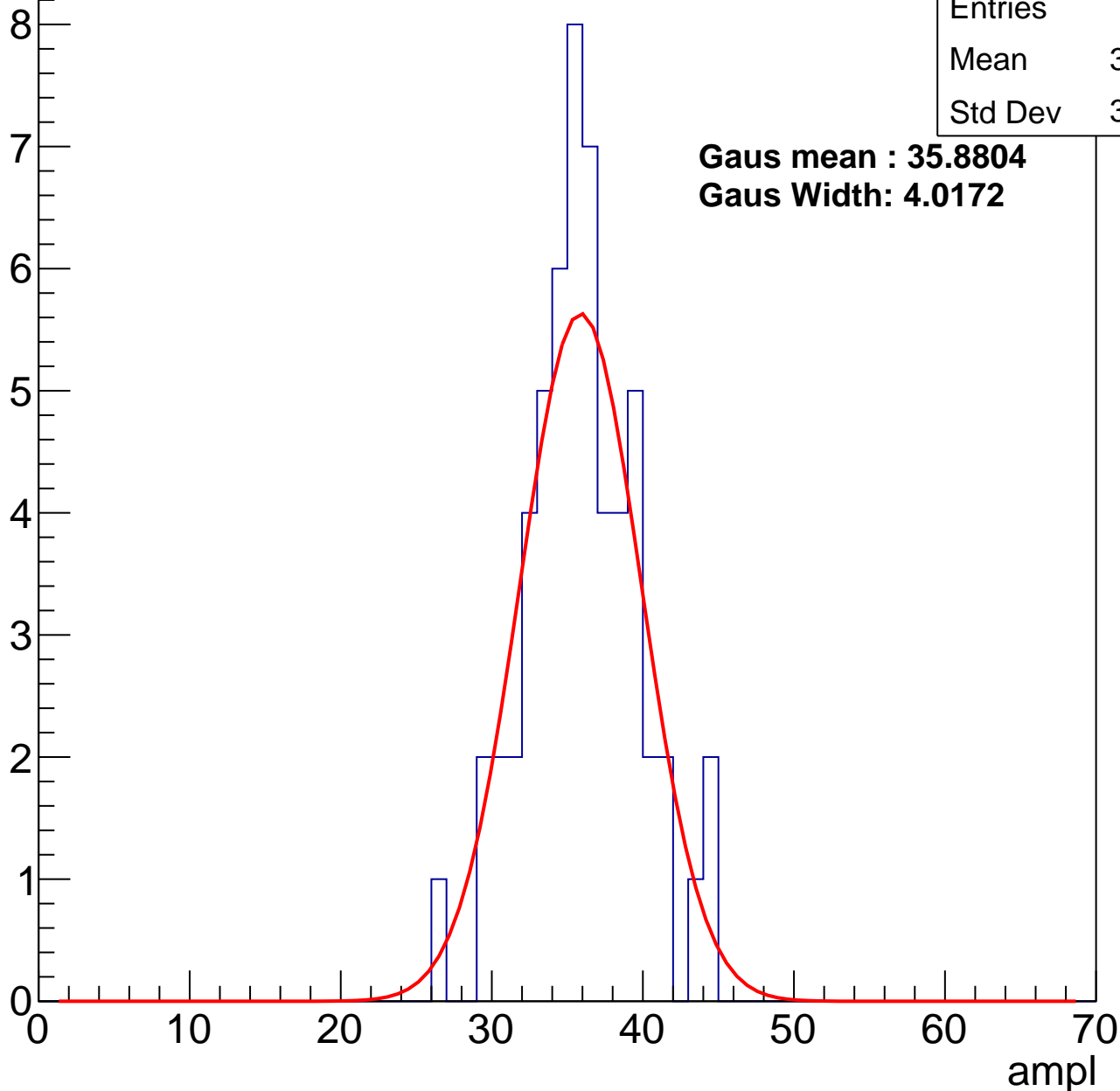
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	35.49
Std Dev	3.676

Gaus mean : 35.8804

Gaus Width: 4.0172



B1L103S, U21-ch14, adc2

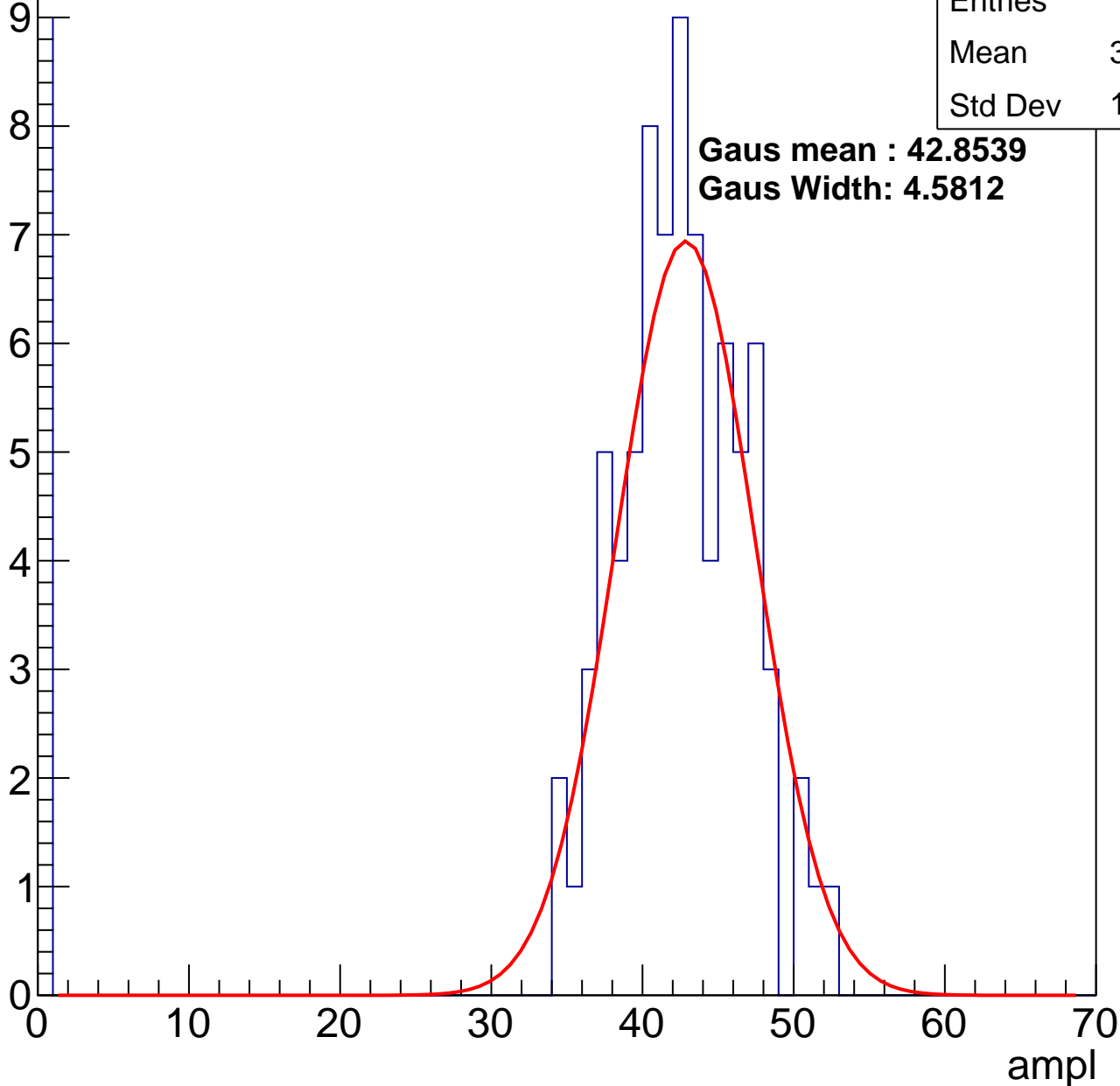
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	37.89
Std Dev	13.34

Gaus mean : 42.8539

Gaus Width: 4.5812



B1L103S, U21-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	48.85
Std Dev	3.366

Entry

10

8

6

4

2

0

0

10

20

30

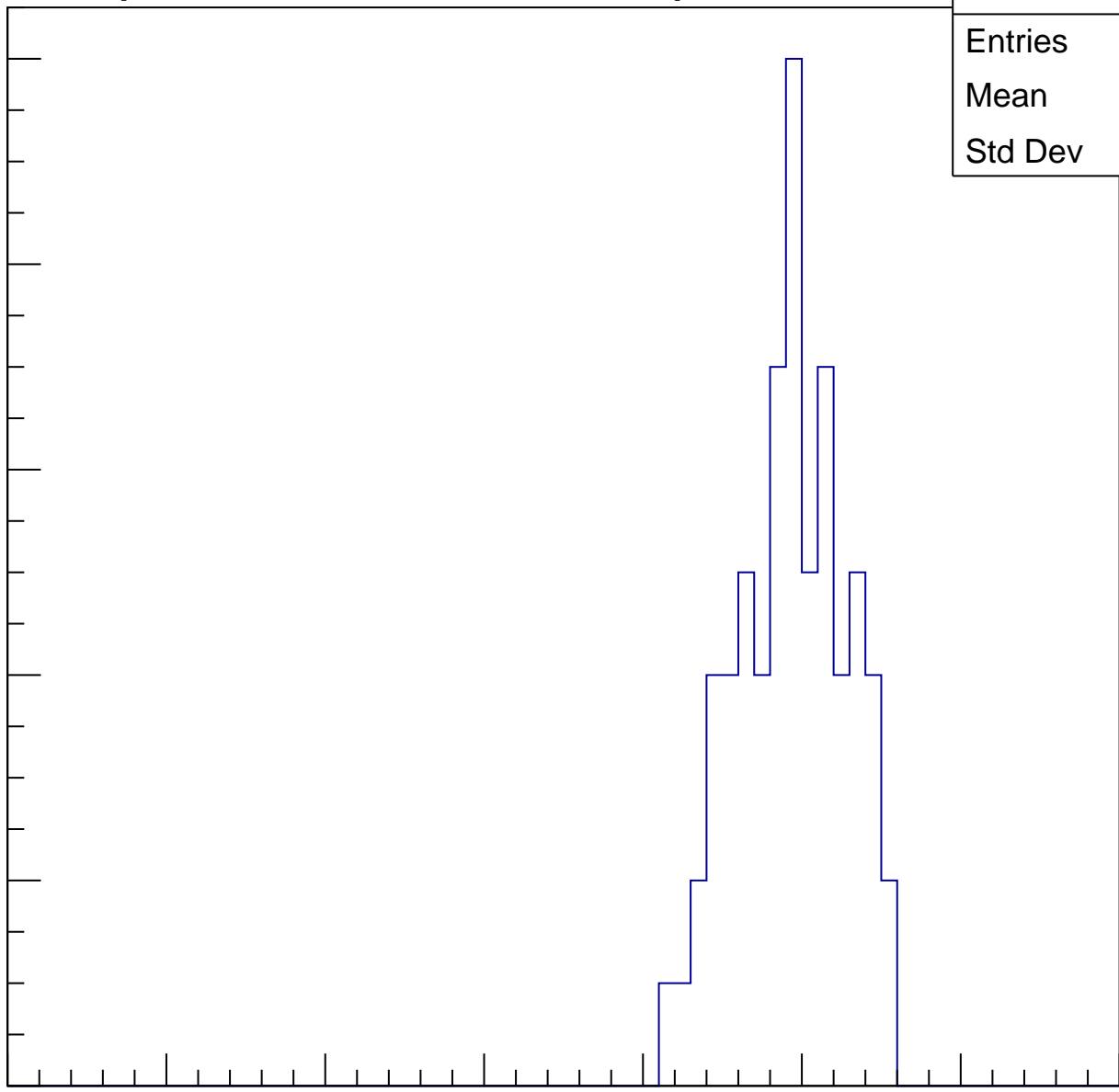
40

50

60

70

ampl

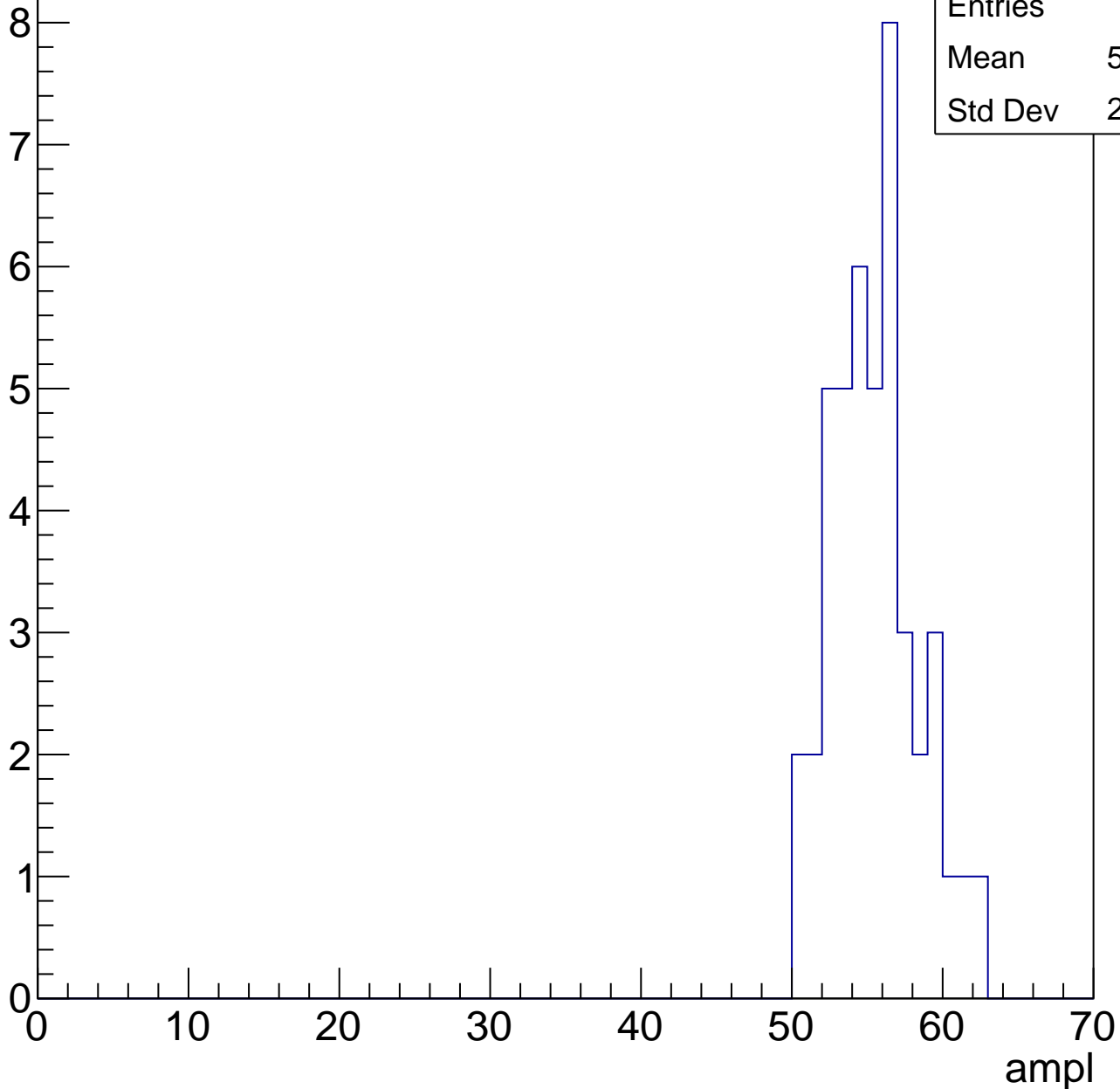


B1L103S, U21-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	55.02
Std Dev	2.816

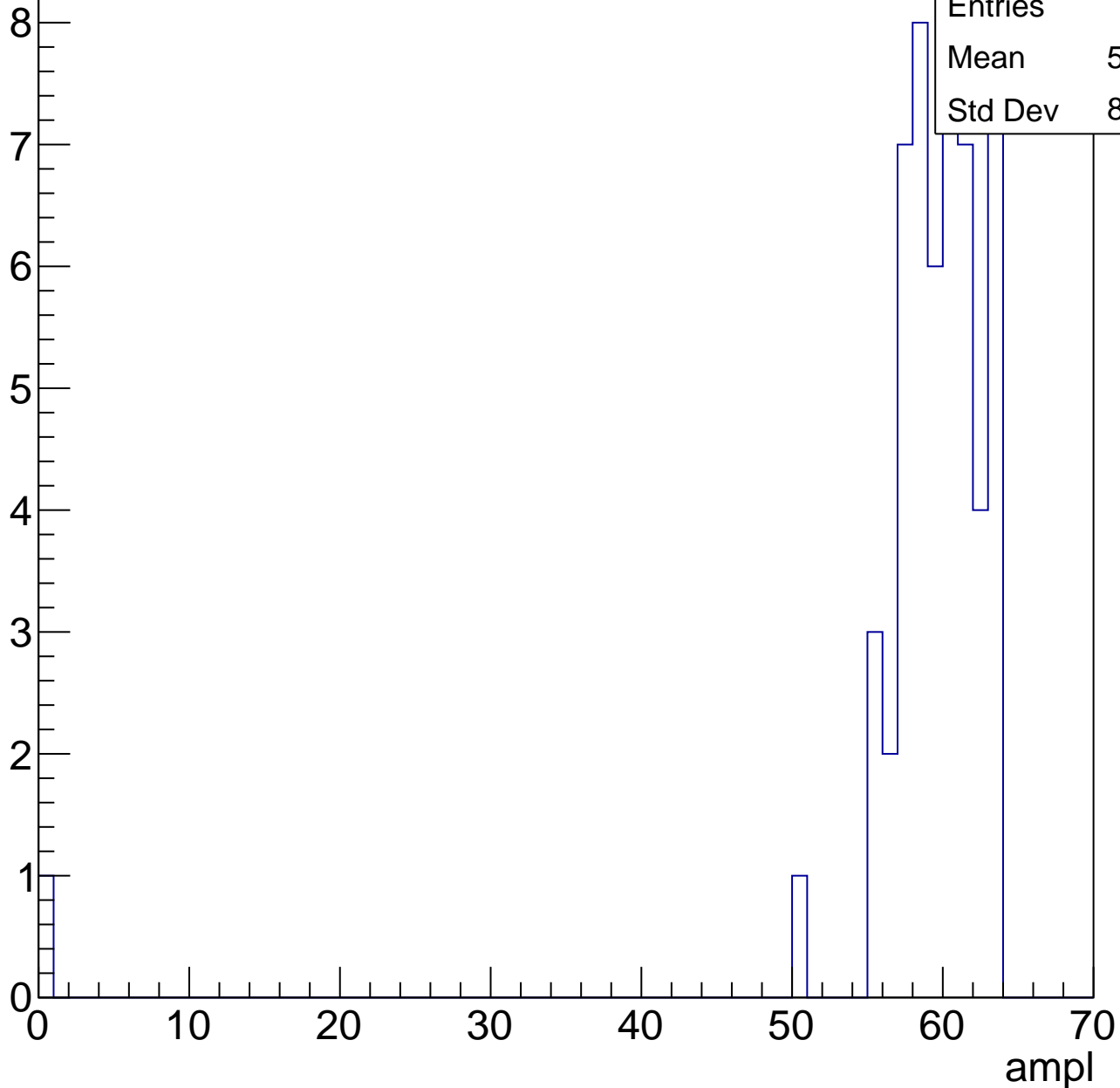


B1L103S, U21-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	58.24
Std Dev	8.347

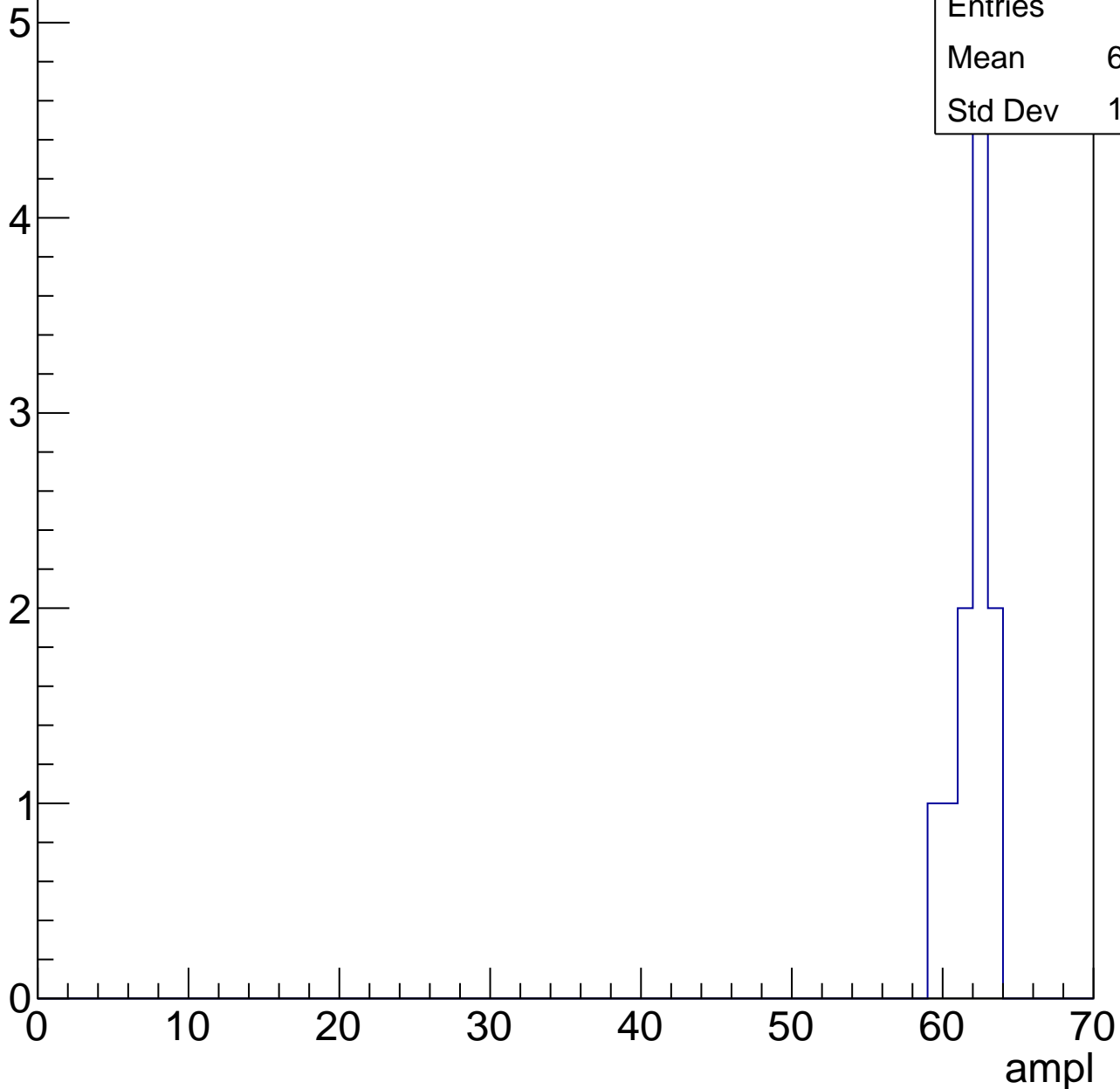


B1L103S, U21-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	61.55
Std Dev	1.157



B1L103S, U21-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U21-ch15, adc0

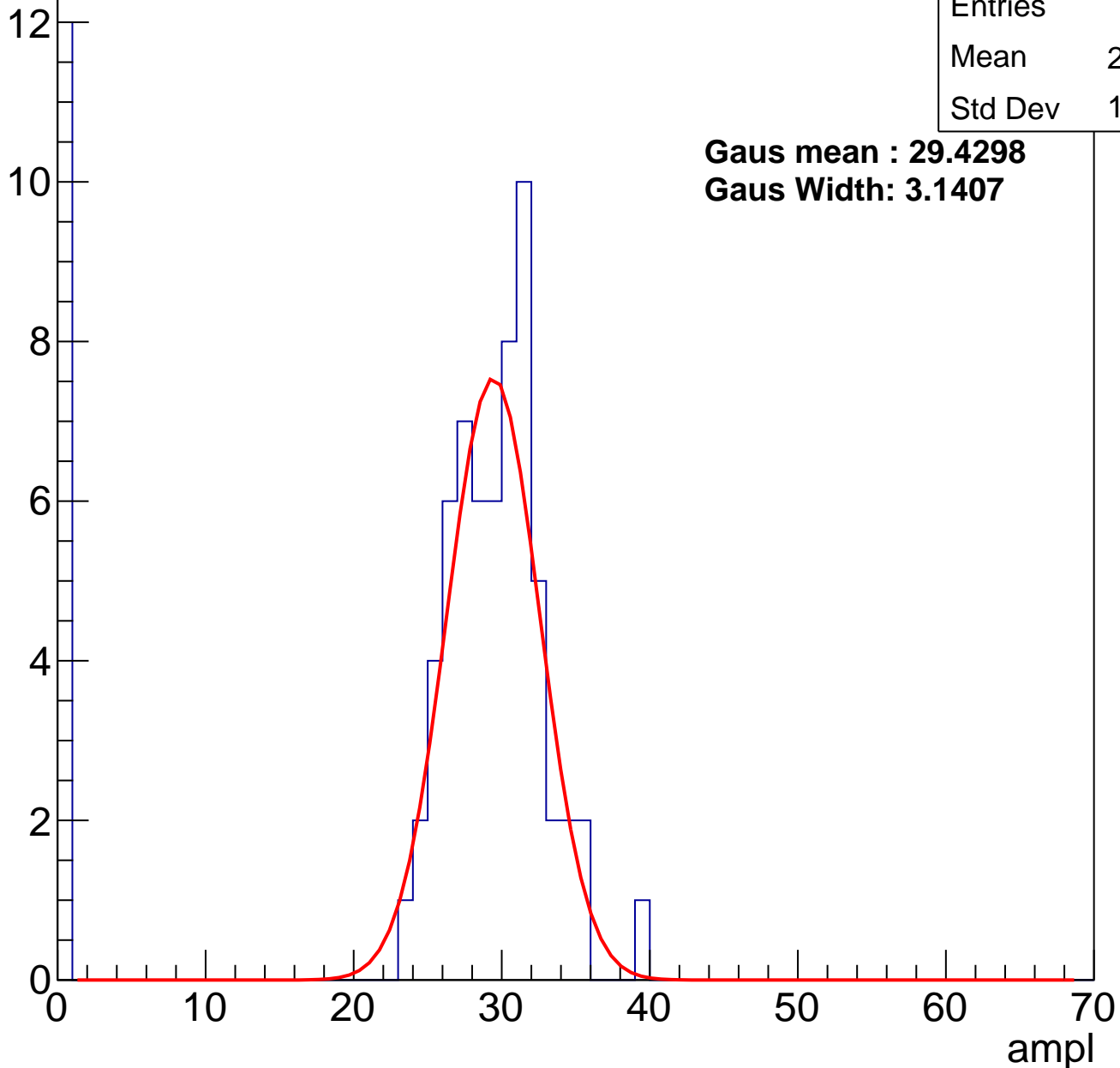
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	24.47
Std Dev	11.13

Gaus mean : 29.4298

Gaus Width: 3.1407

Entry



B1L103S, U21-ch15, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	35.04
Std Dev	6.769

Gaus mean : 36.4687

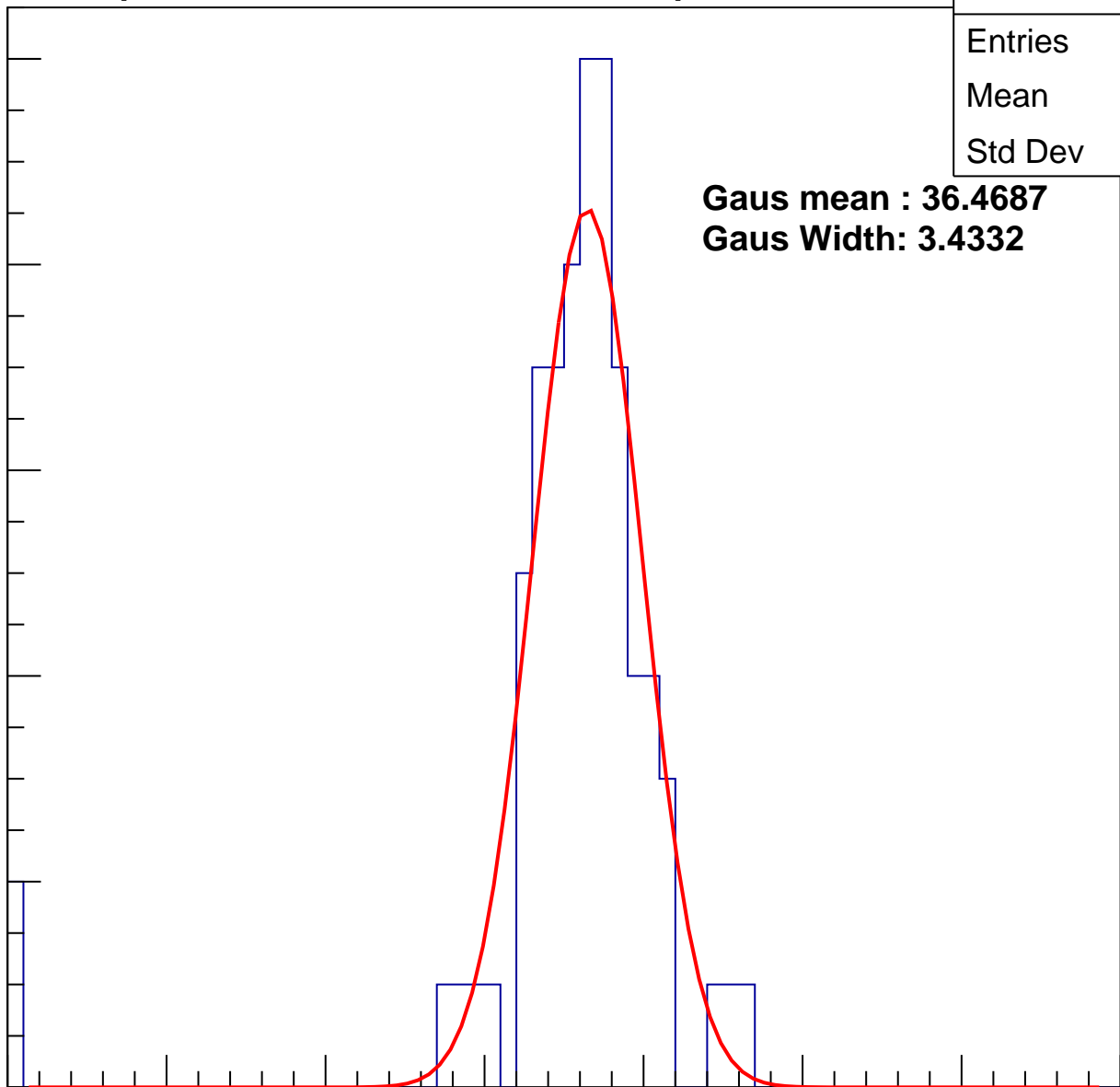
Gaus Width: 3.4332

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch15, adc2

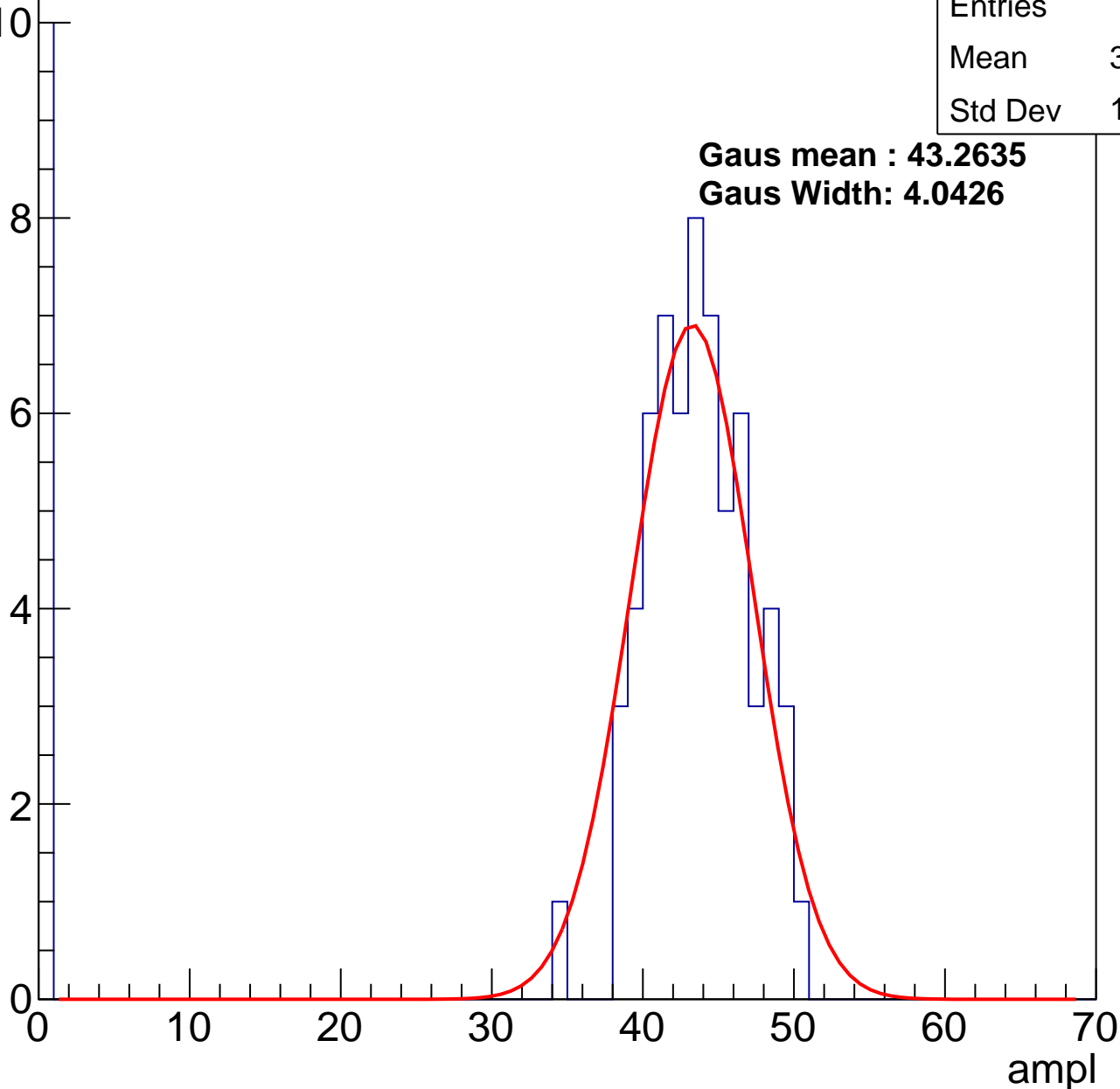
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	37.38
Std Dev	15.09

Gaus mean : 43.2635

Gaus Width: 4.0426

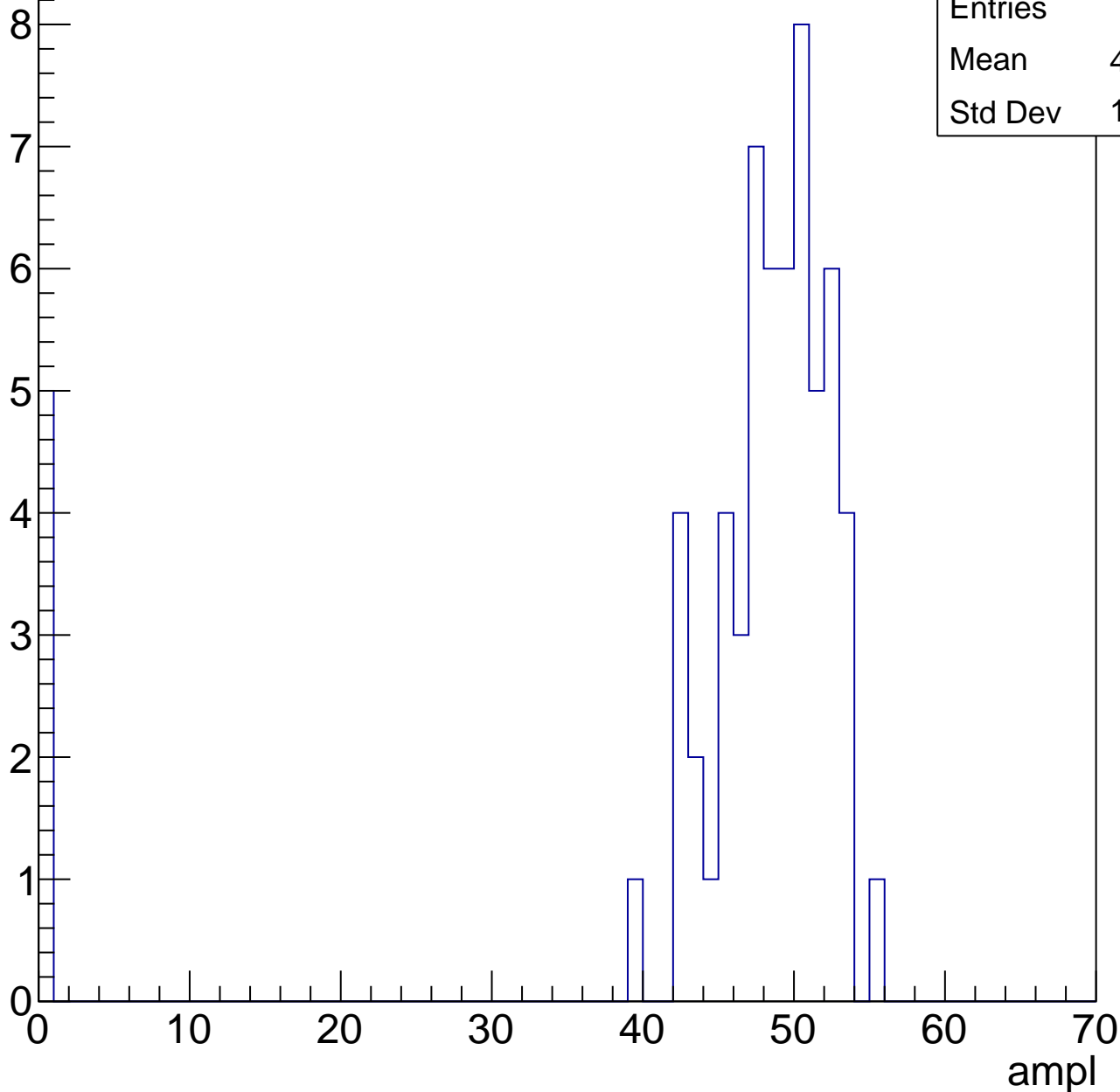


B1L103S, U21-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	44.44
Std Dev	13.45

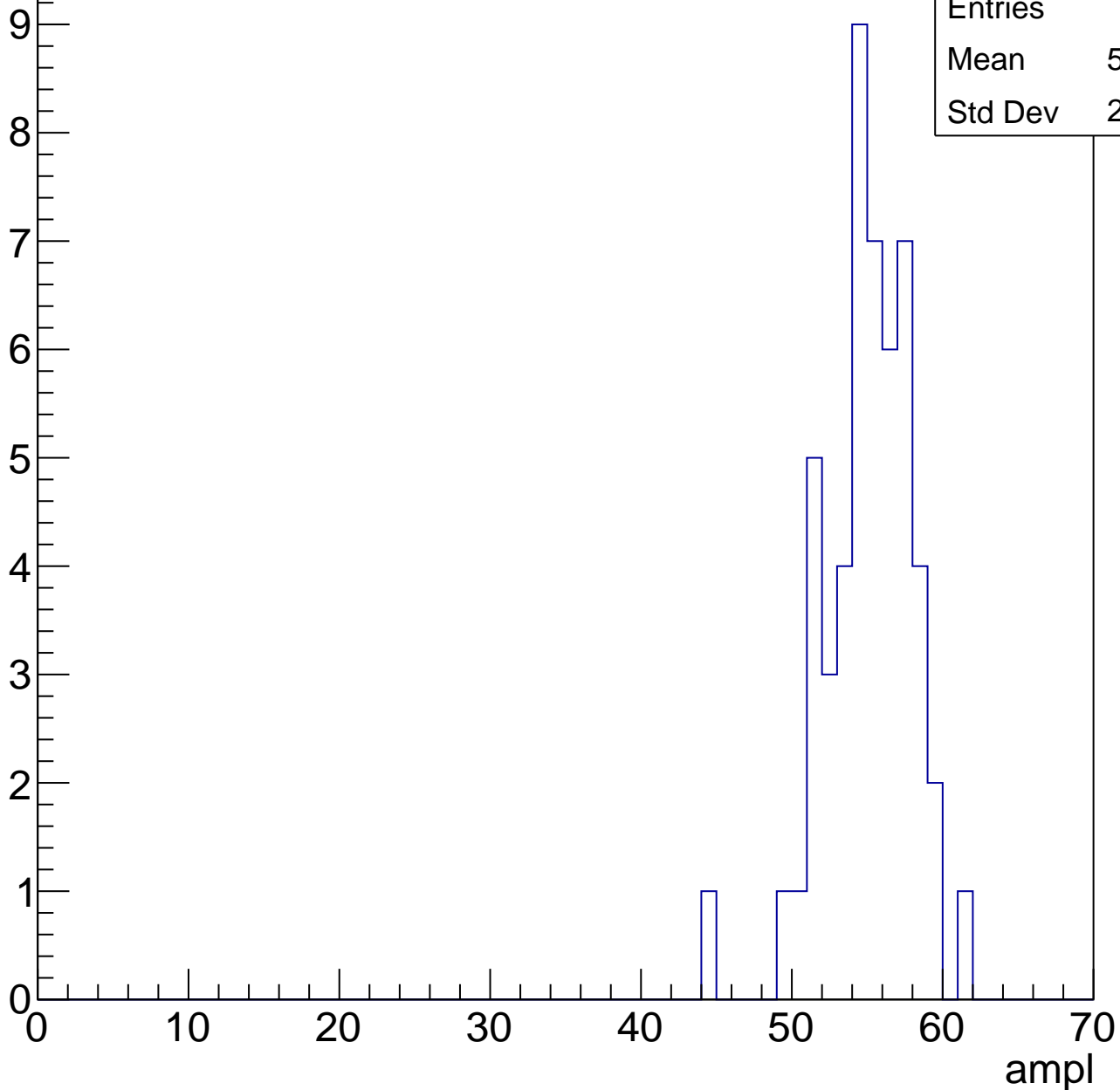


B1L103S, U21-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	54.57
Std Dev	2.946

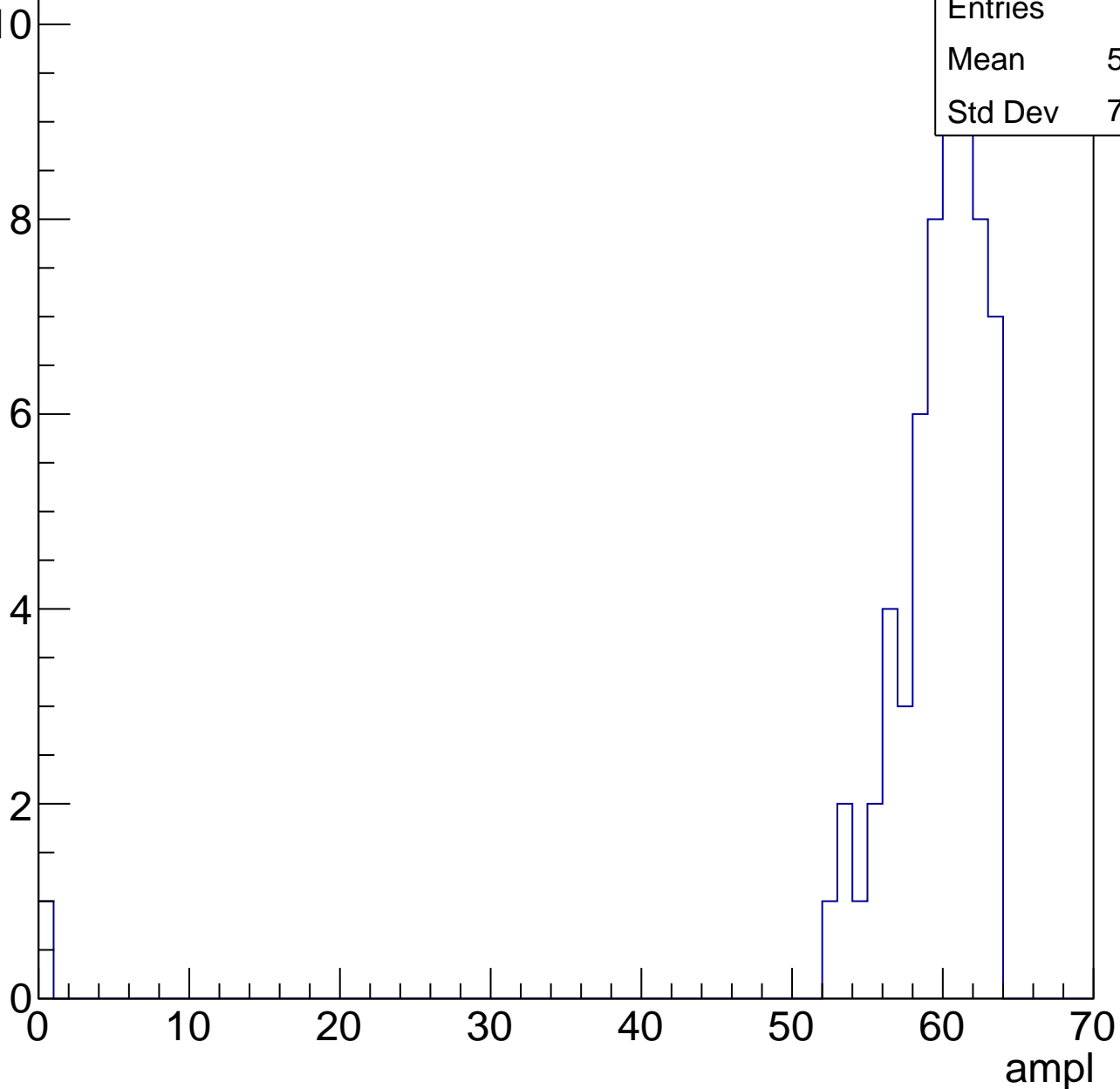


B1L103S, U21-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	58.44
Std Dev	7.955



B1L103S, U21-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136

Entry



B1L103S, U21-ch16, adc0

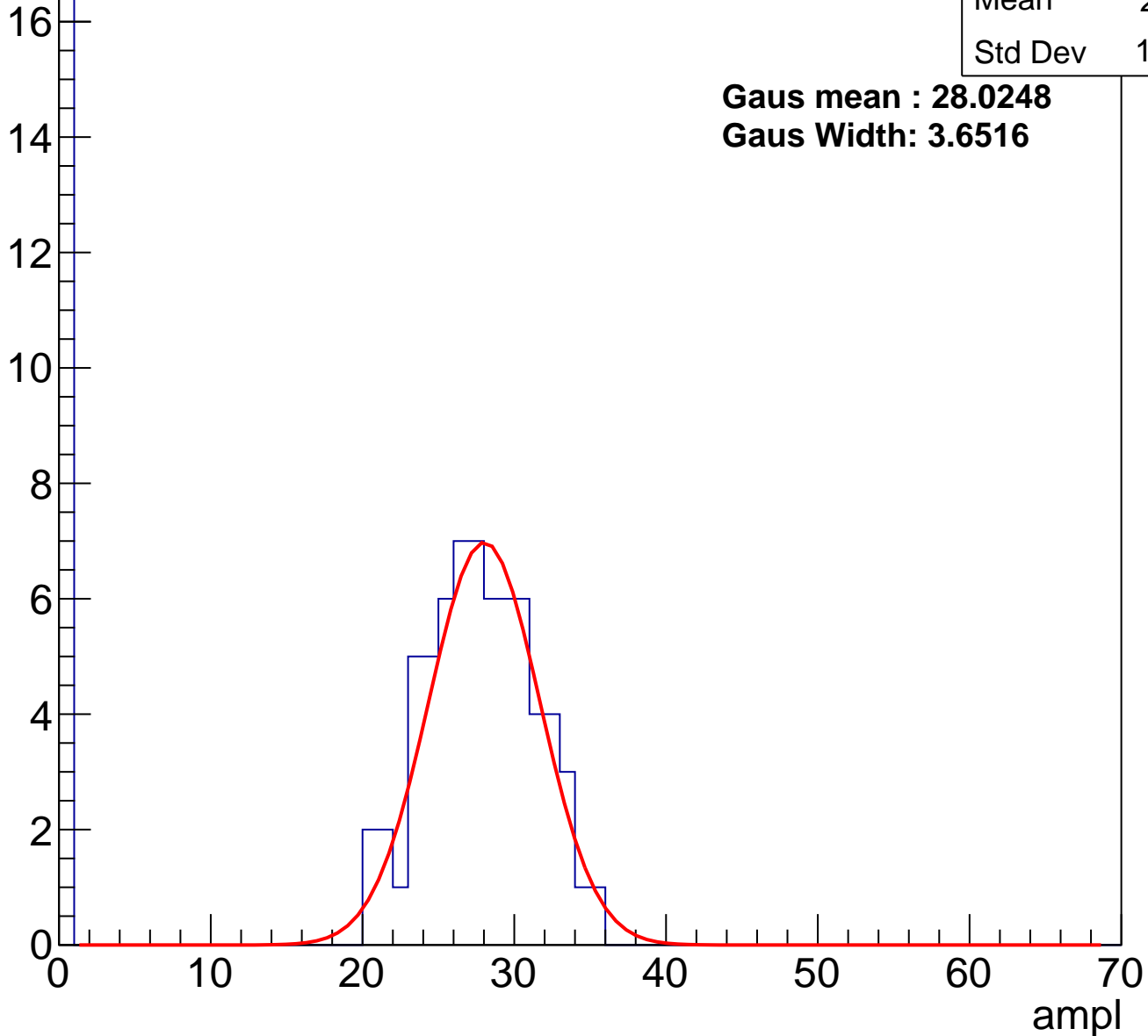
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	21.71
Std Dev	11.46

Gaus mean : 28.0248

Gaus Width: 3.6516

Entry



B1L103S, U21-ch16, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	30.52
Std Dev	11.87

Gaus mean : 35.3282

Gaus Width: 4.1753

Entry

12

10

8

6

4

2

0

0

10

20

30

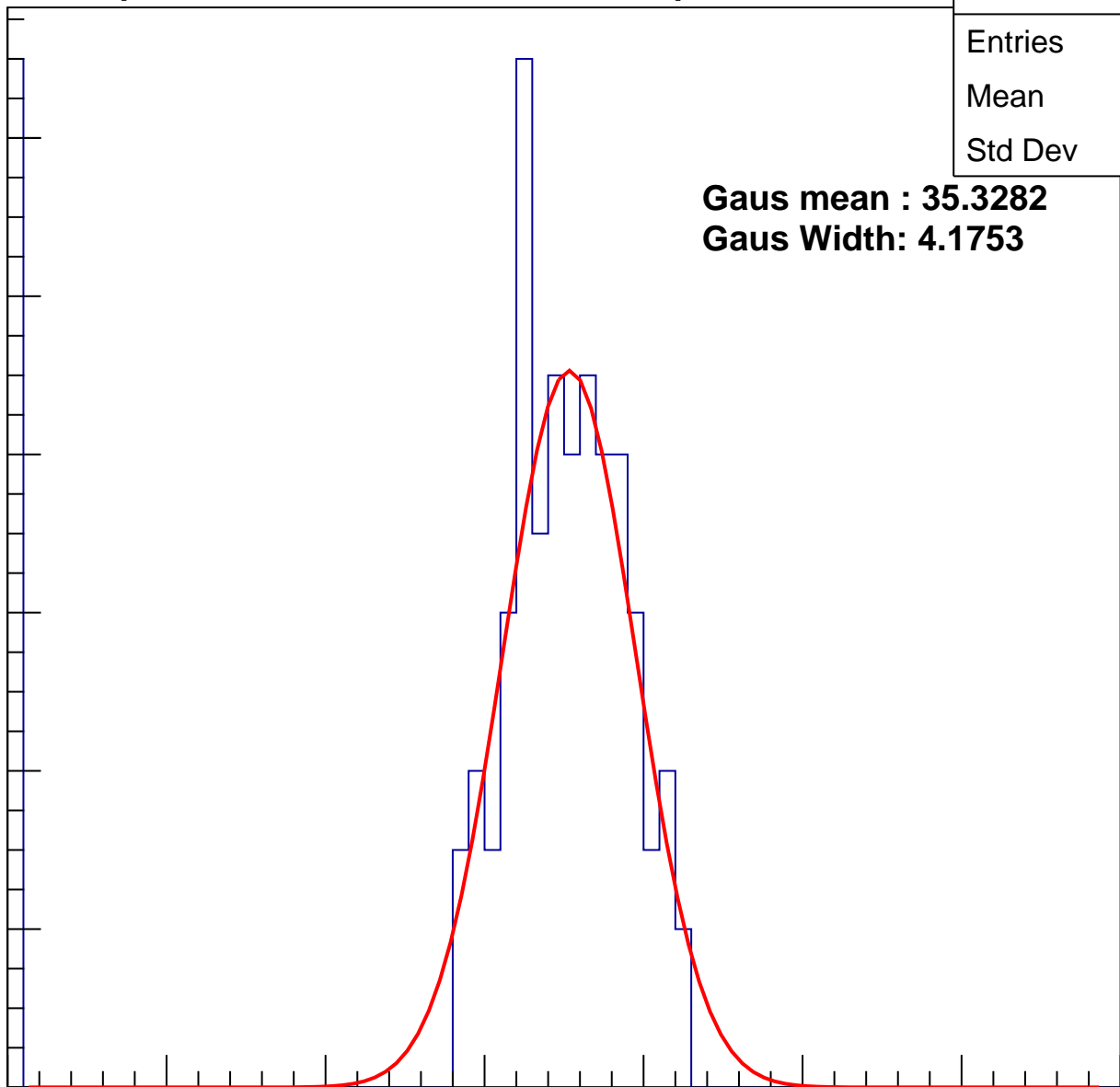
40

50

60

70

ampl



B1L103S, U21-ch16, adc2

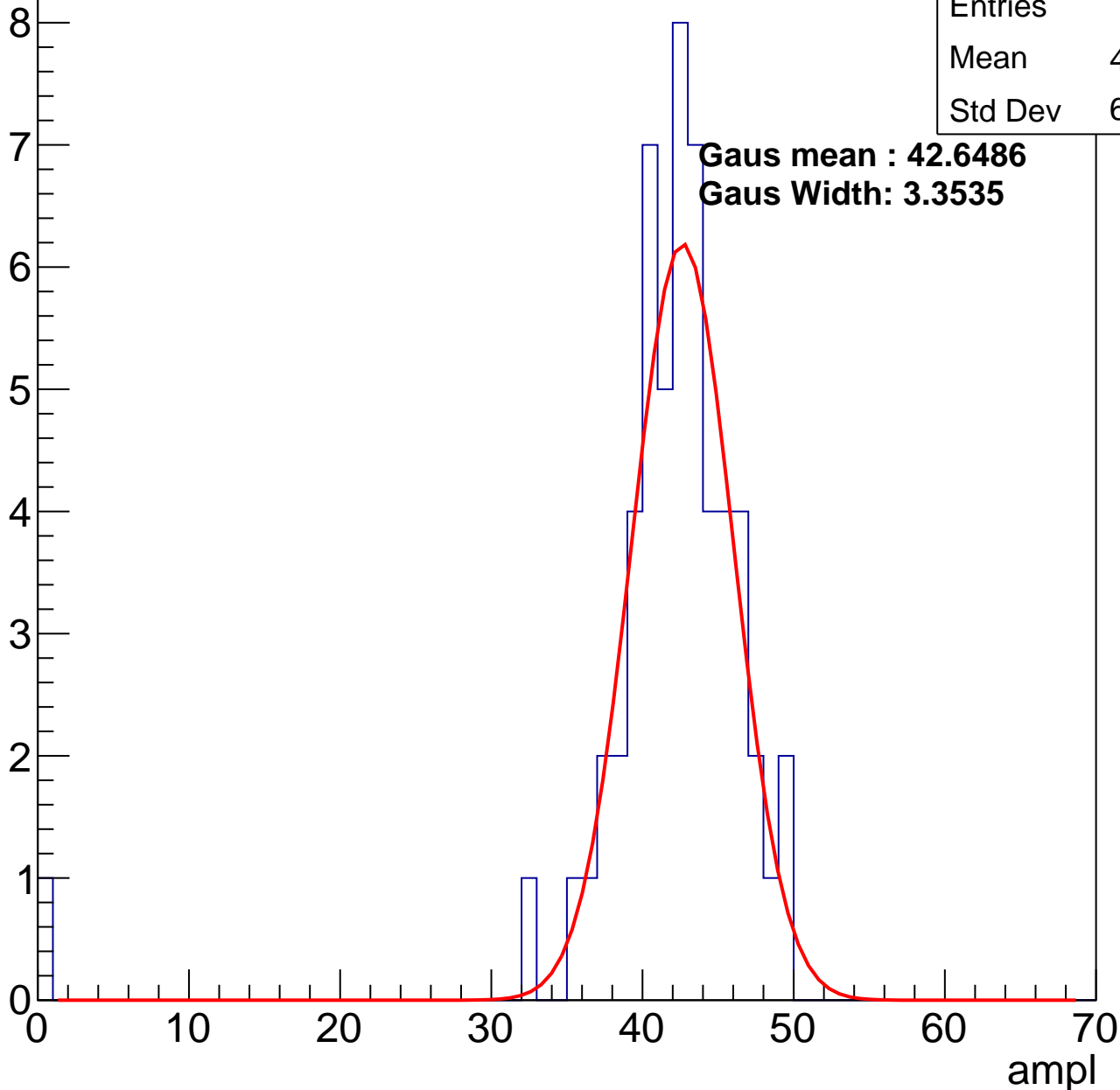
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	41.27
Std Dev	6.515

Gaus mean : 42.6486

Gaus Width: 3.3535

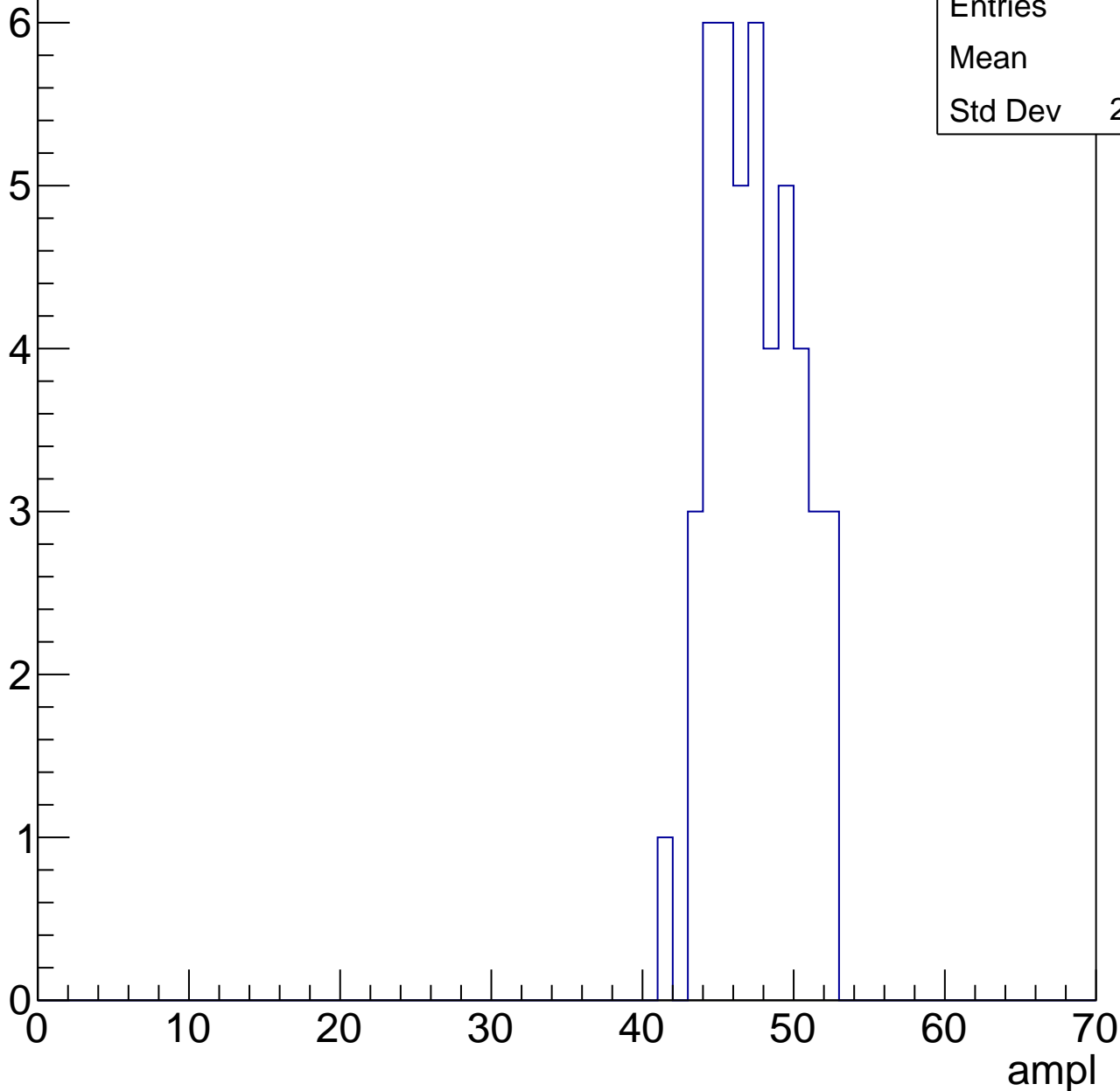


B1L103S, U21-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	47
Std Dev	2.758



B1L103S, U21-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	52.53
Std Dev	7.052

Entry

10

8

6

4

2

0

0

10

20

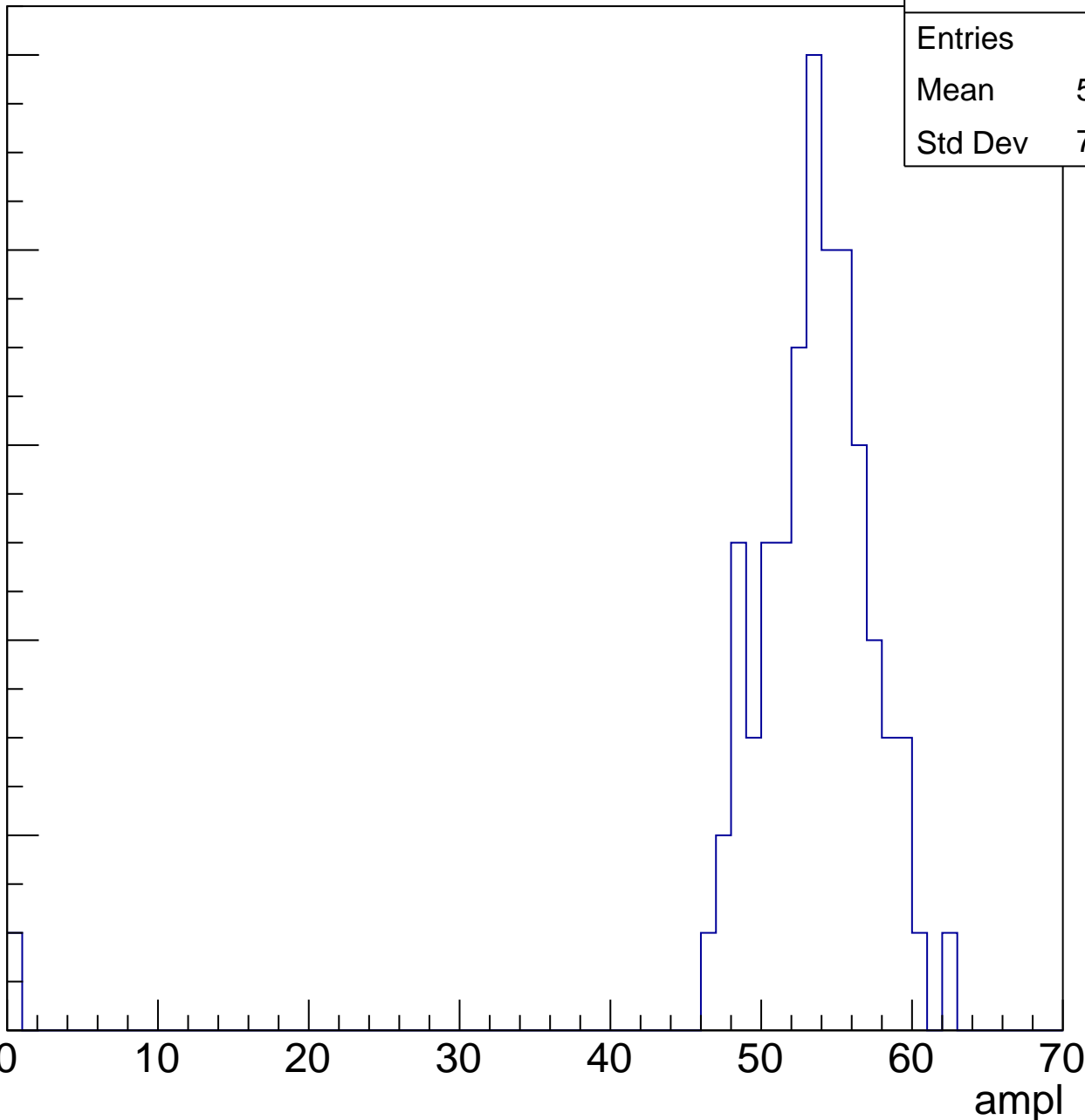
30

40

50

60

ampl

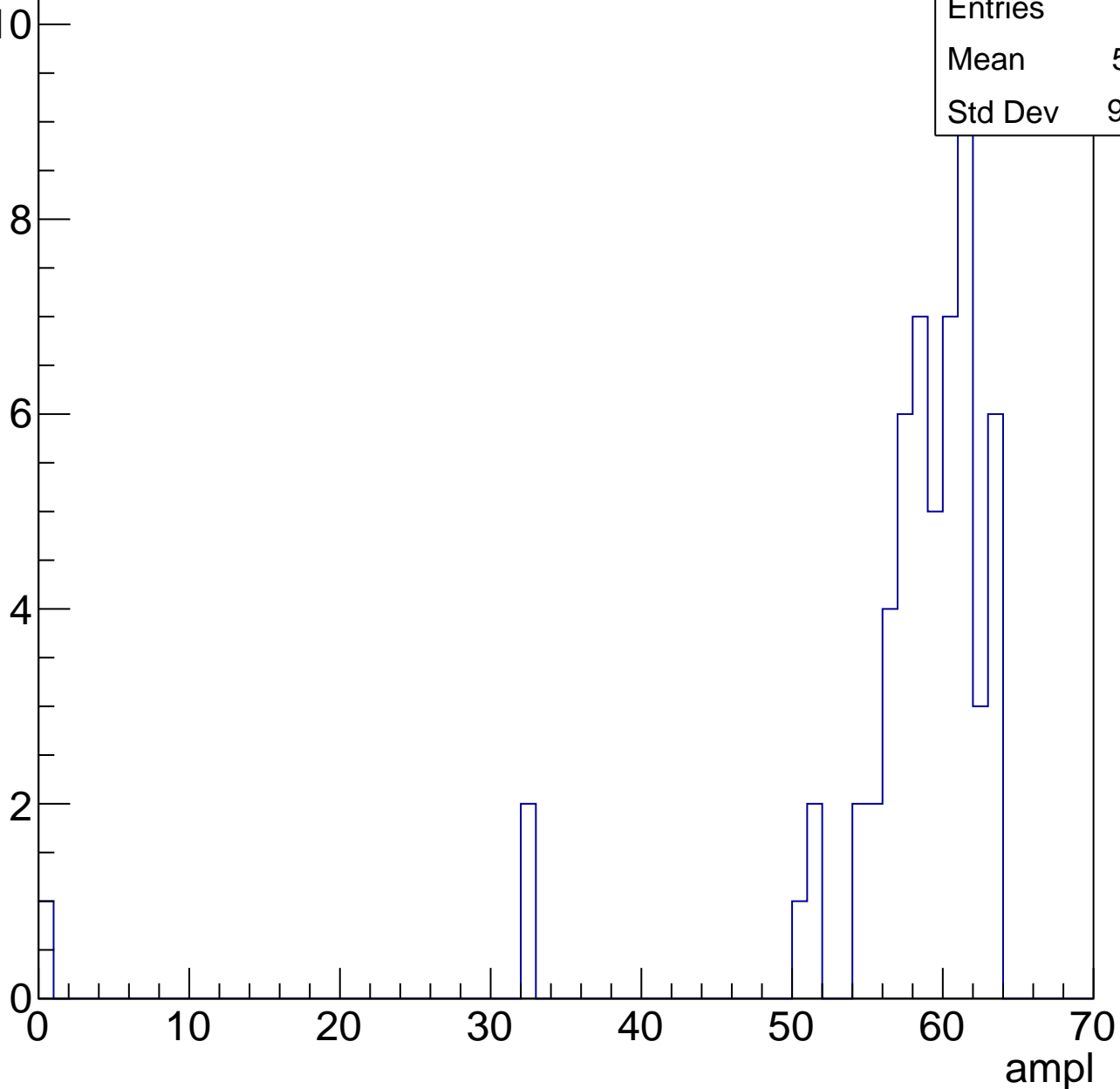


B1L103S, U21-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.81
Std Dev	9.459

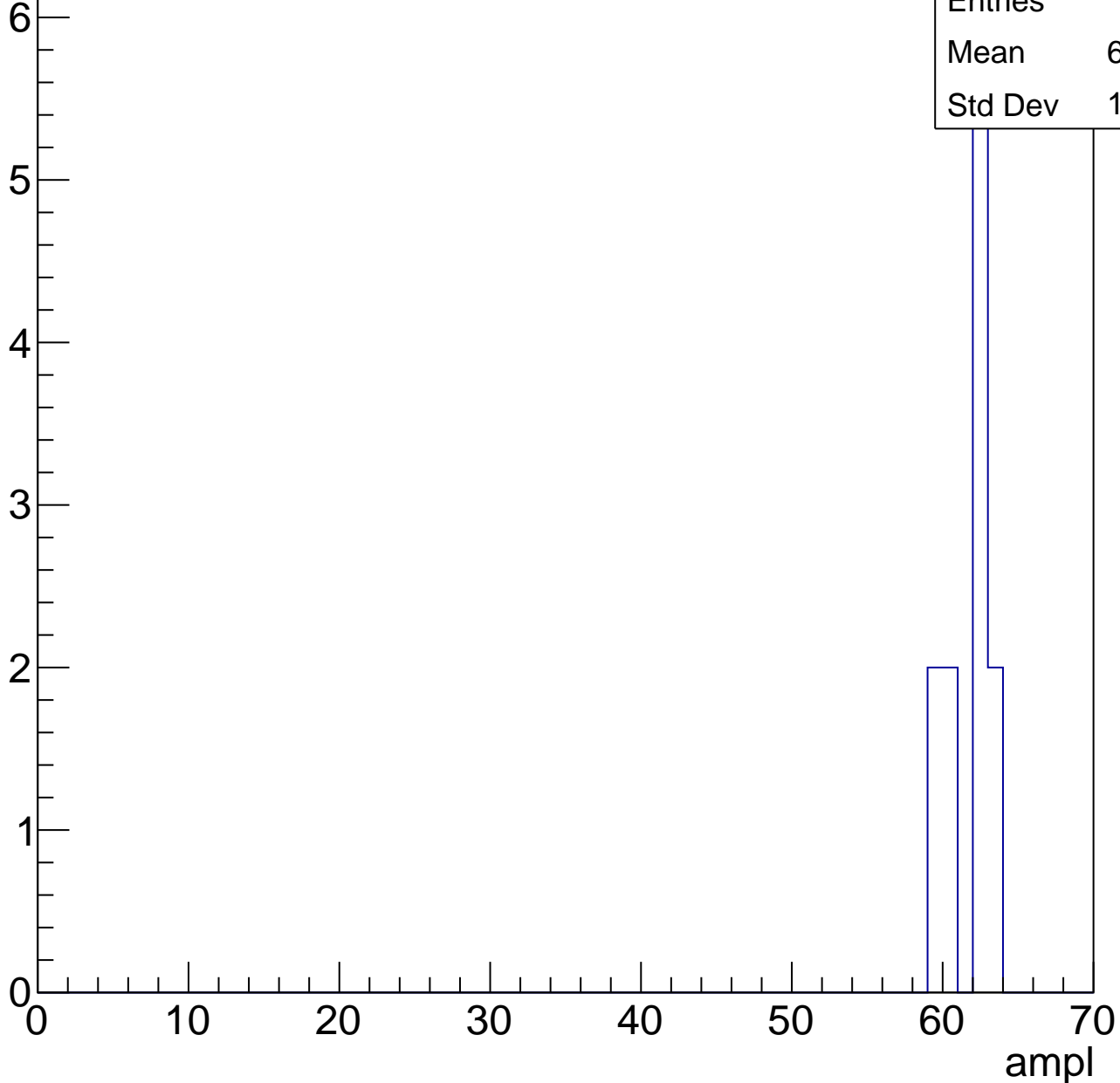


B1L103S, U21-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.33
Std Dev	1.374



B1L103S, U21-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U21-ch17, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	23.73
Std Dev	10.7

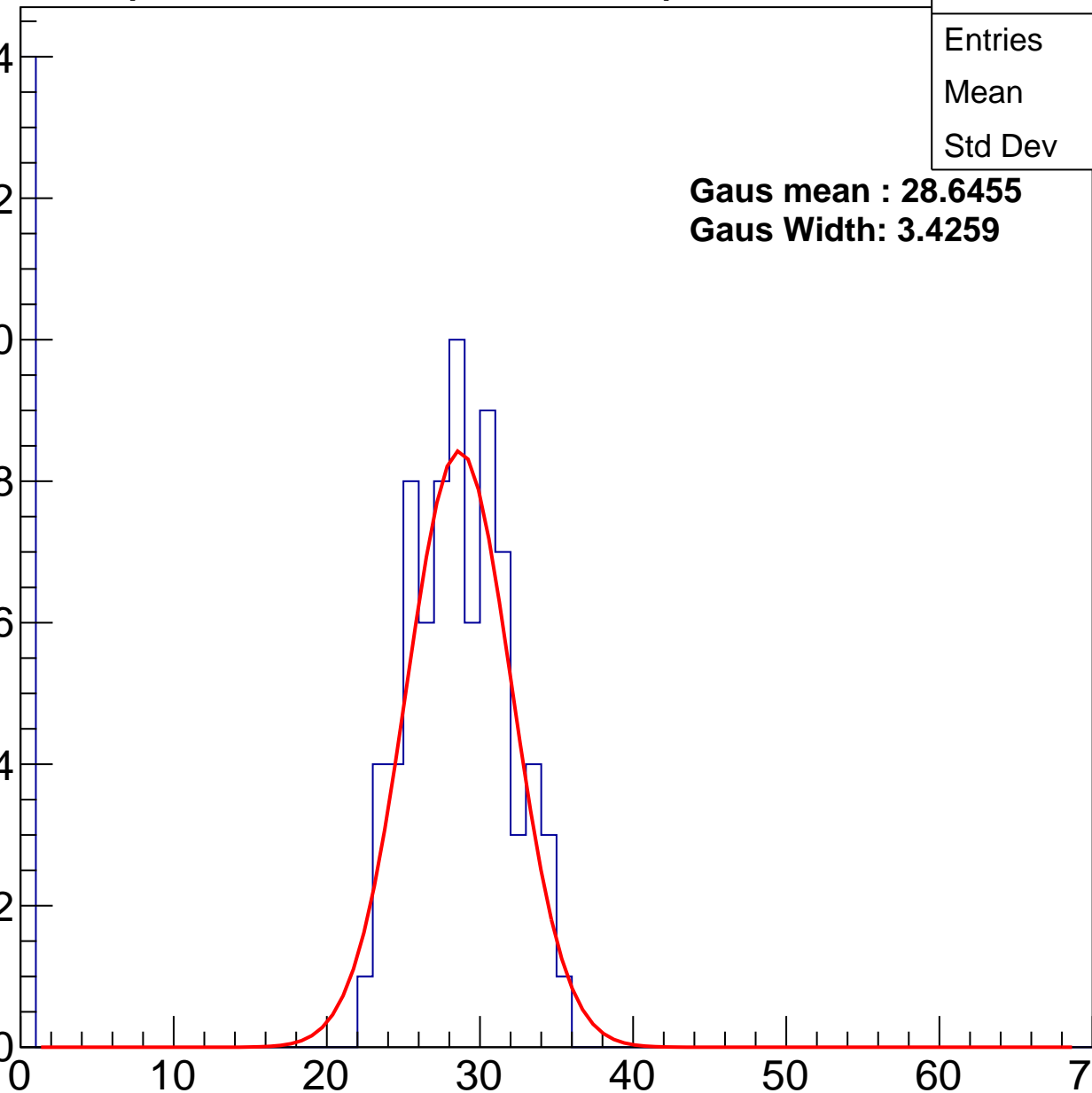
Gaus mean : 28.6455

Gaus Width: 3.4259

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch17, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	34.68
Std Dev	5.126

Gaus mean : 35.6809

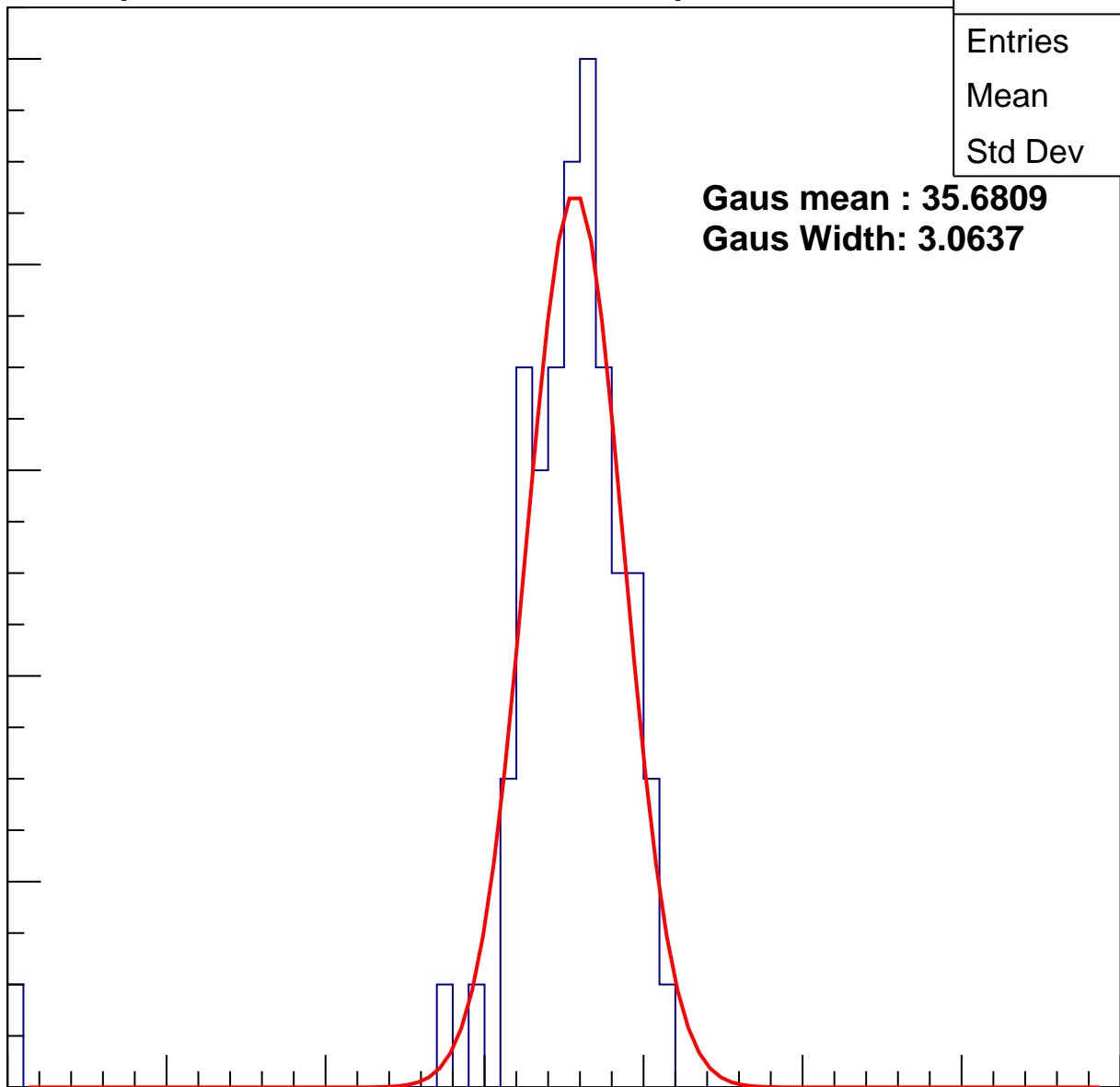
Gaus Width: 3.0637

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch17, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	37.35
Std Dev	14.65

Gaus mean : 42.9294

Gaus Width: 3.8752

Entry

10

8

6

4

2

0

0

10

20

30

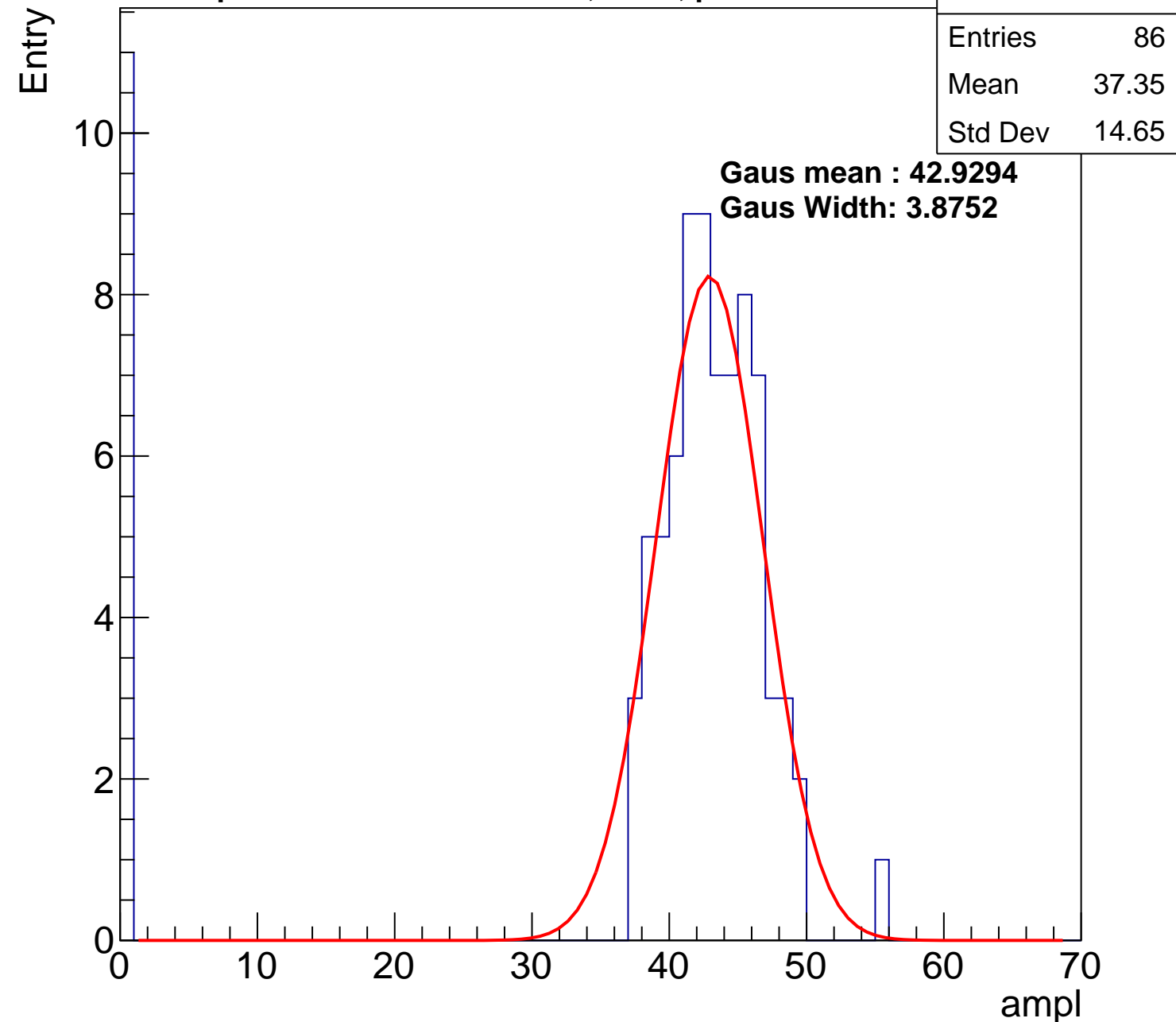
40

50

60

70

ampl

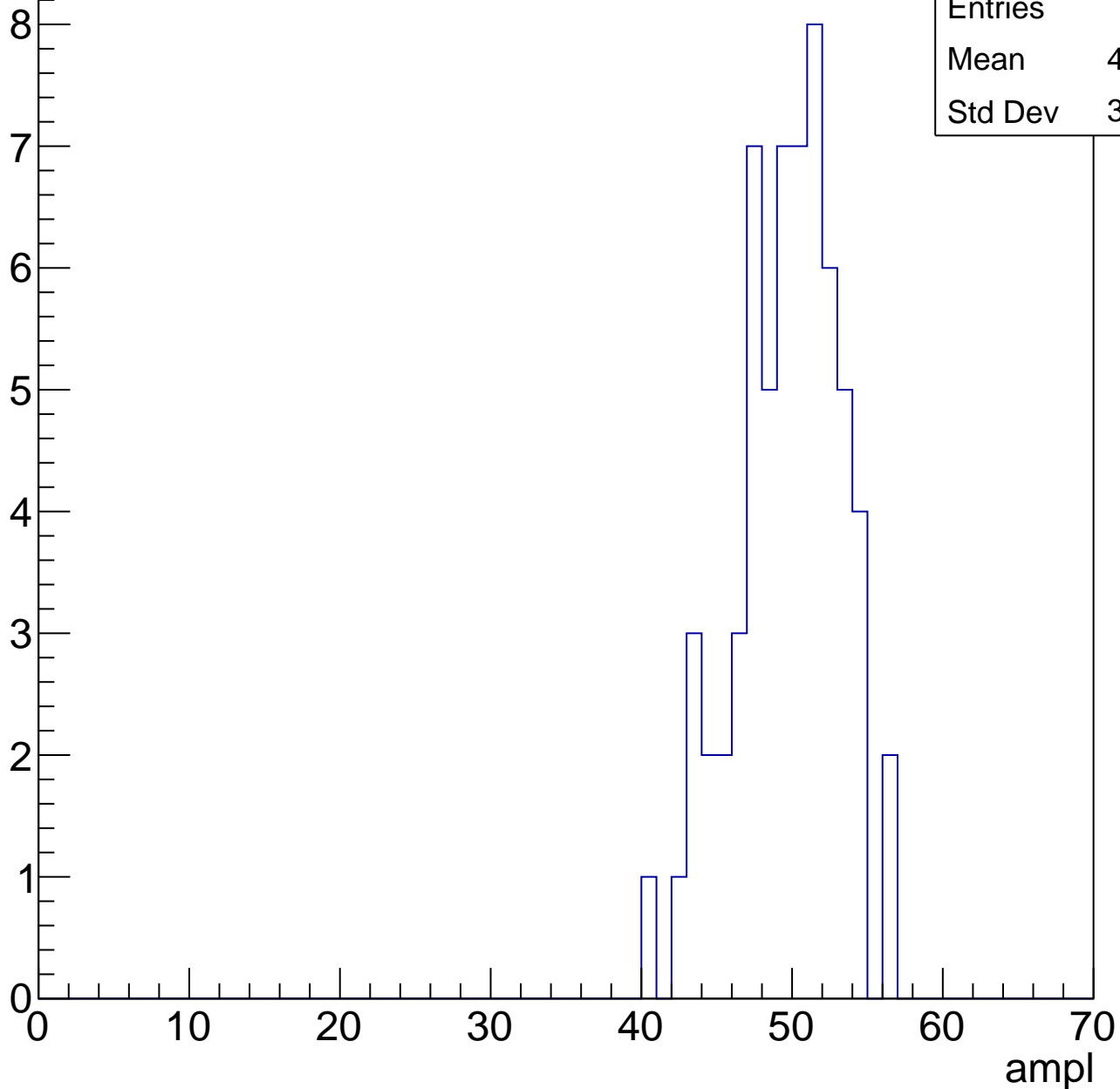


B1L103S, U21-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.24
Std Dev	3.444

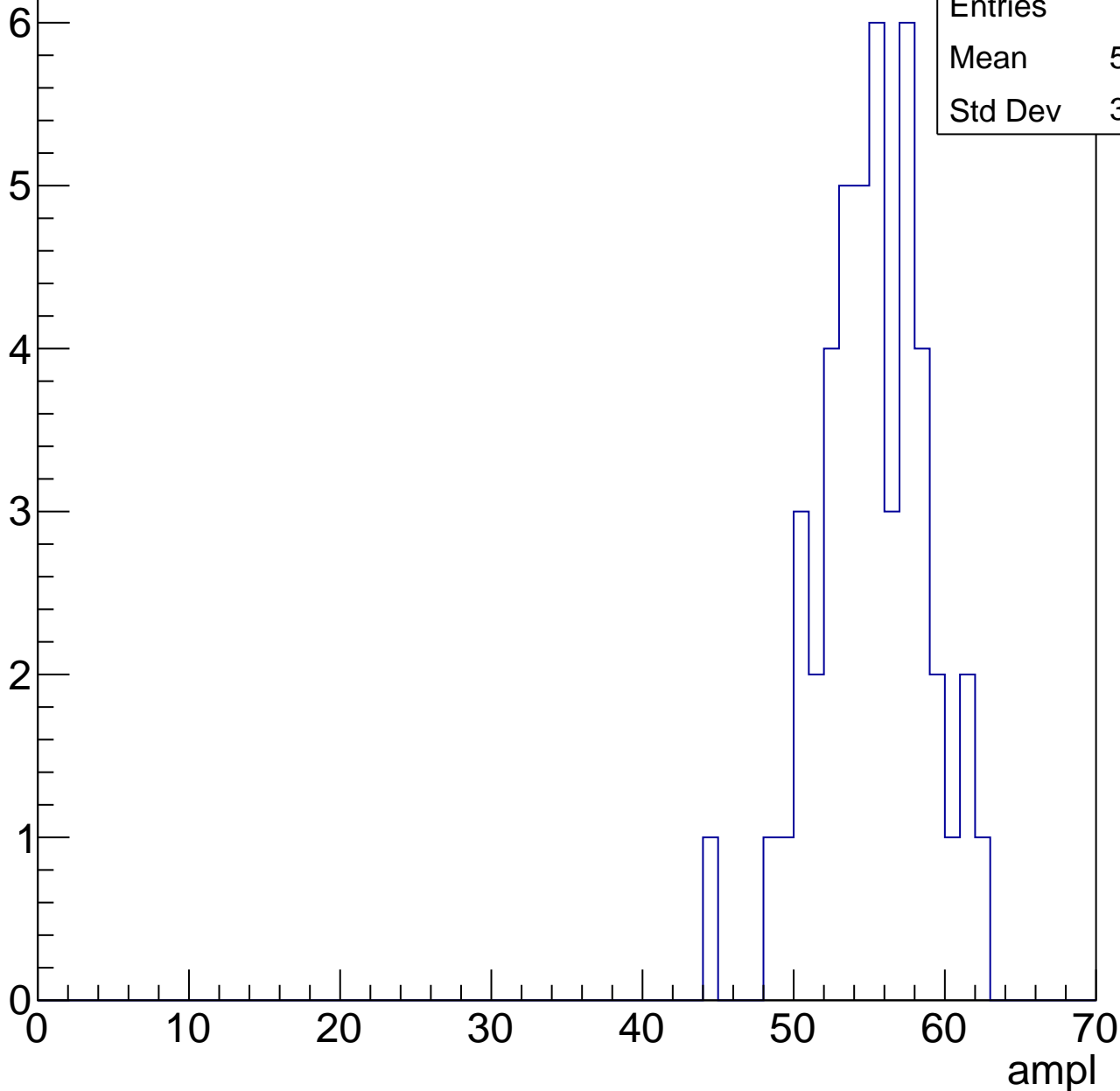


B1L103S, U21-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	54.68
Std Dev	3.603

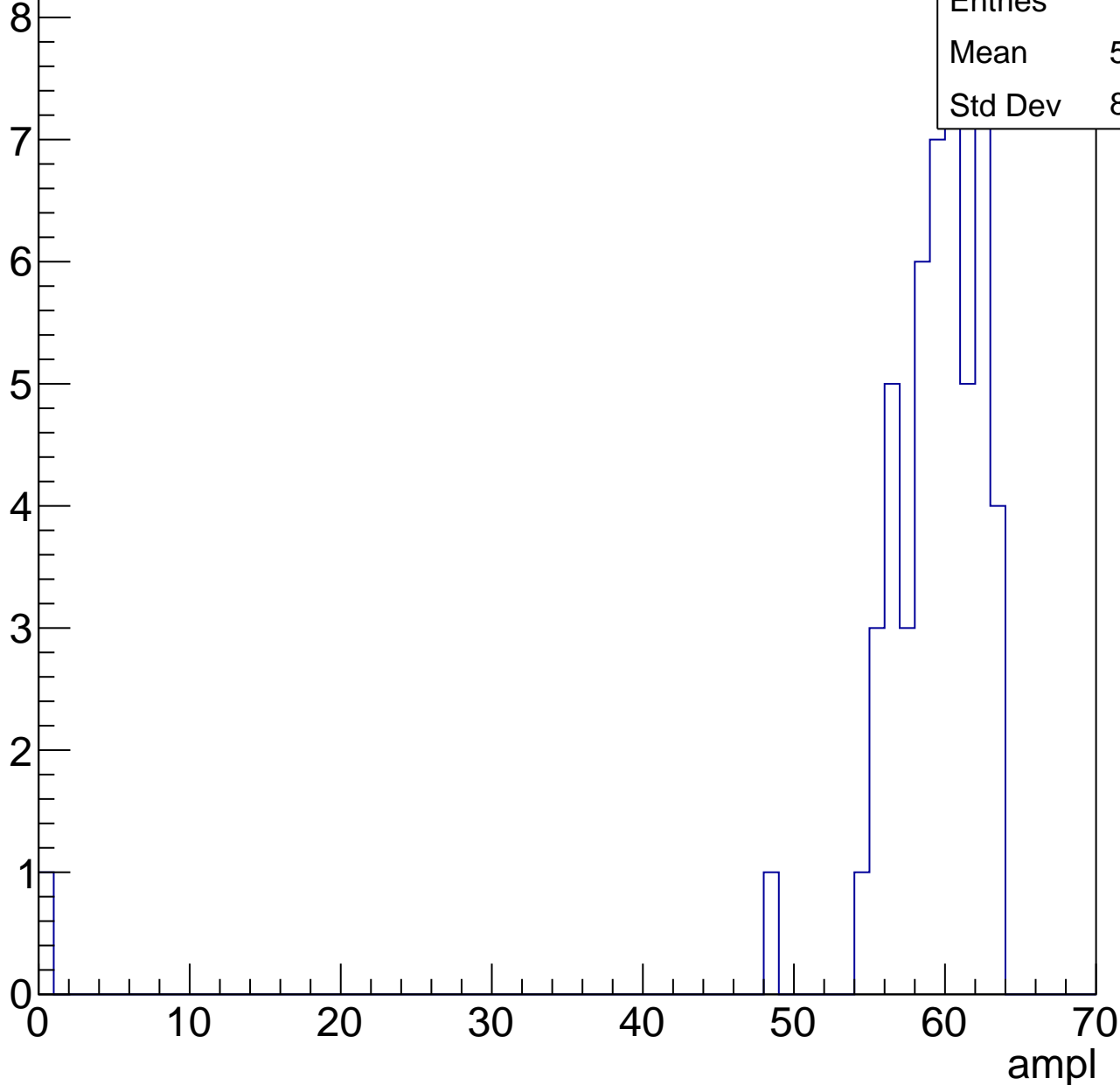


B1L103S, U21-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	57.92
Std Dev	8.595

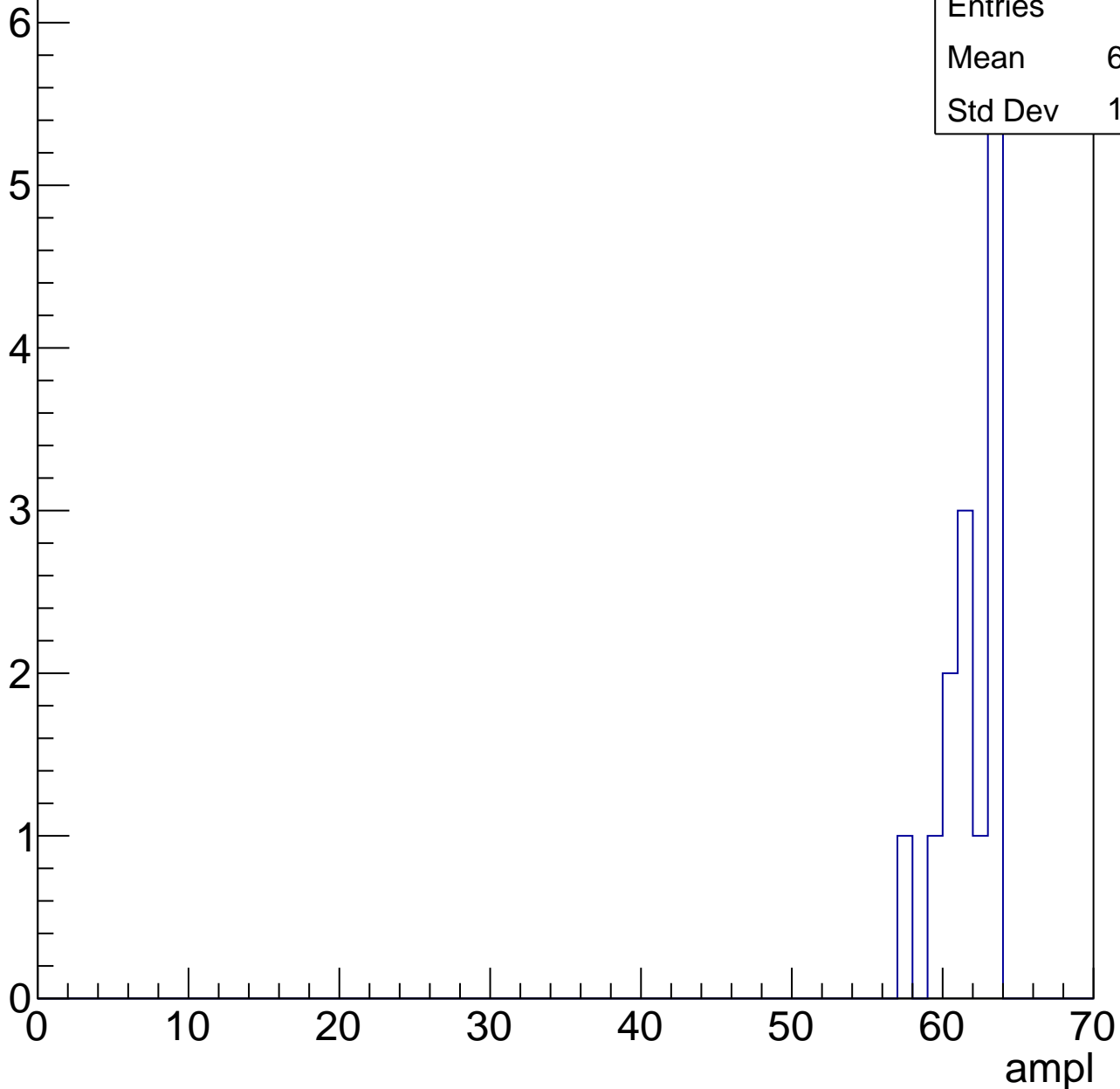


B1L103S, U21-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.36
Std Dev	1.797



B1L103S, U21-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch18, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	23.99
Std Dev	12.13

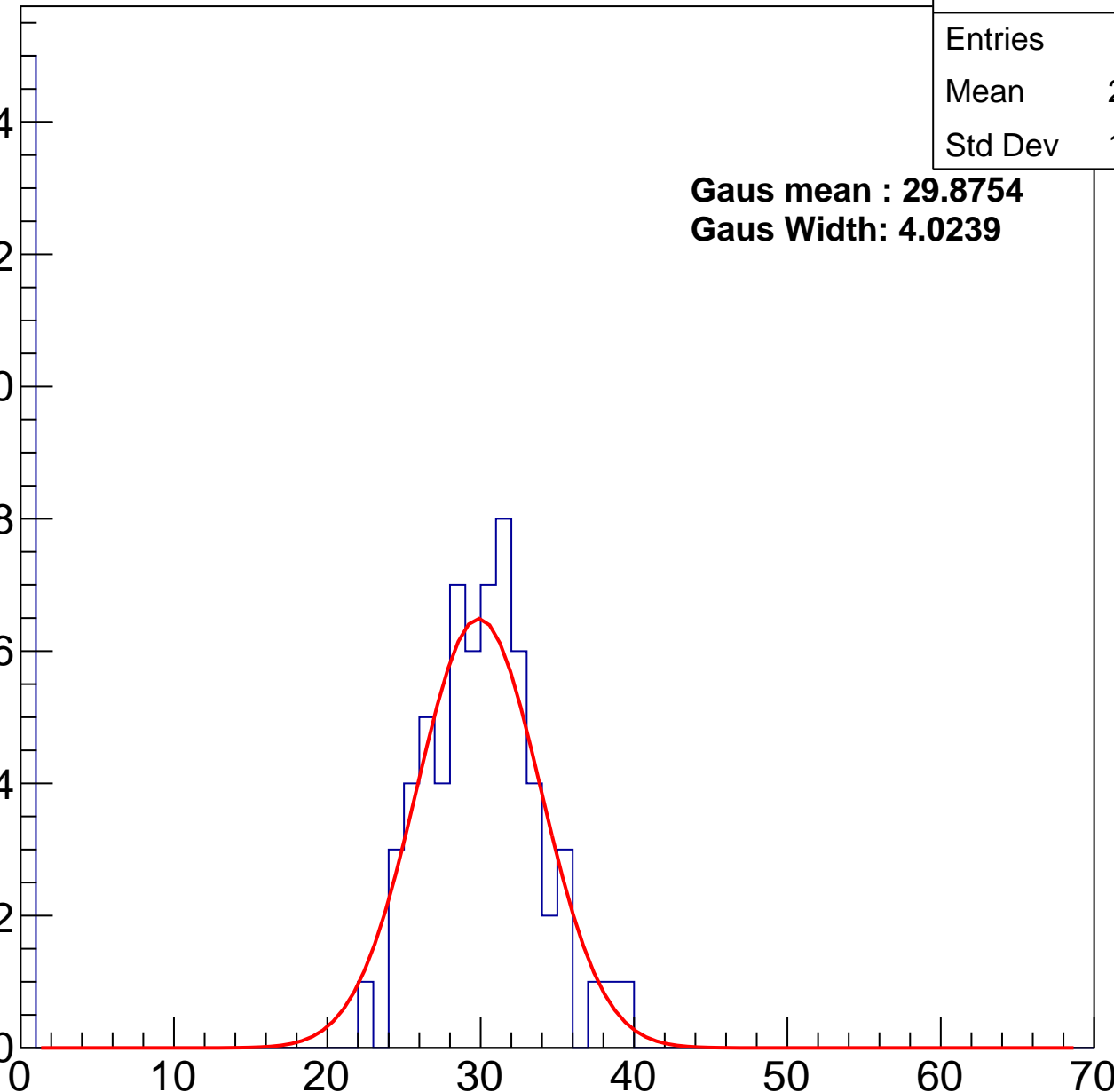
Gaus mean : 29.8754

Gaus Width: 4.0239

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch18, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	31.14
Std Dev	12.97

Gaus mean : 36.6975

Gaus Width: 3.6753

Entry

10

8

6

4

2

0

0

10

20

30

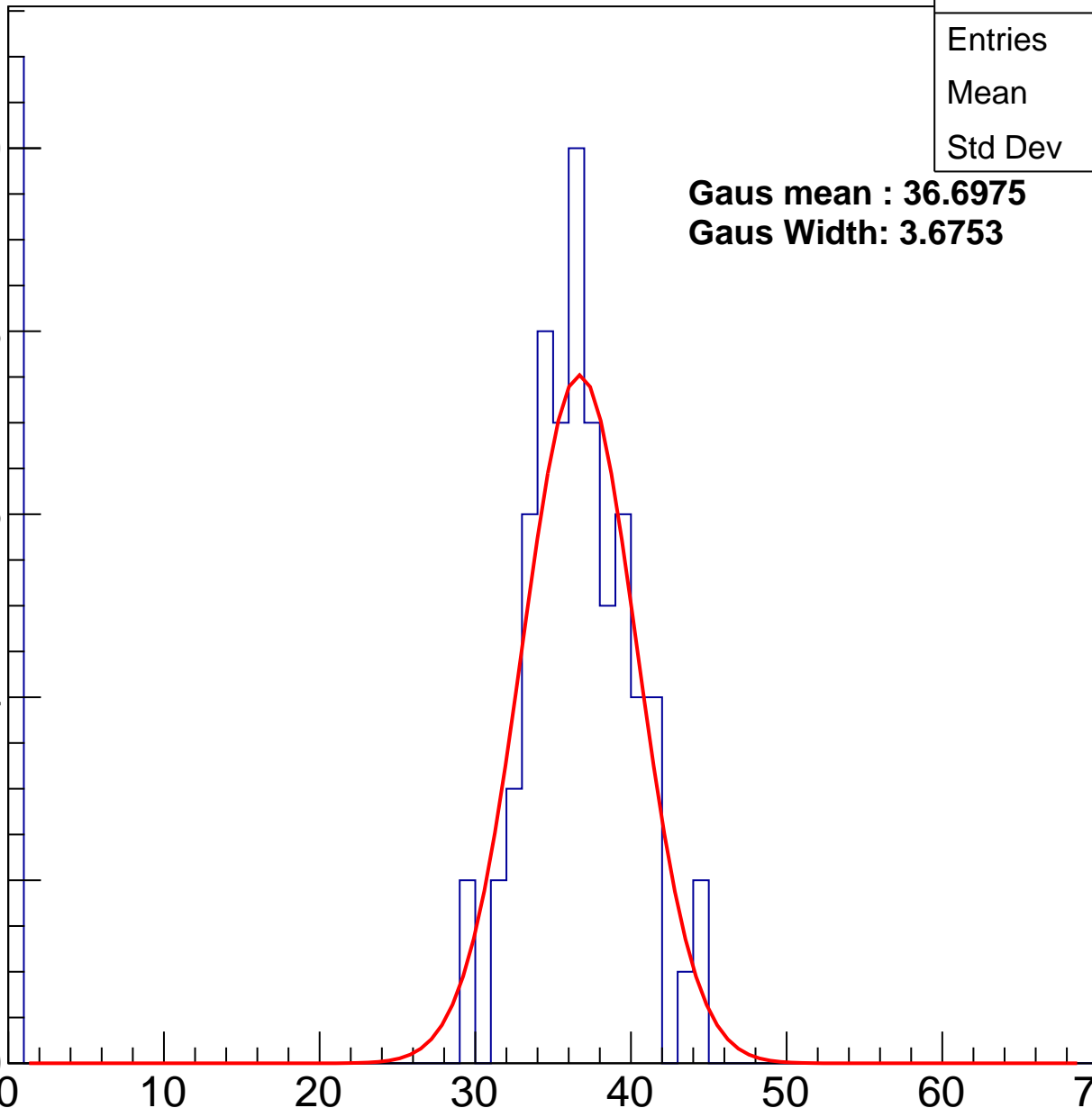
40

50

60

70

ampl



B1L103S, U21-ch18, adc2

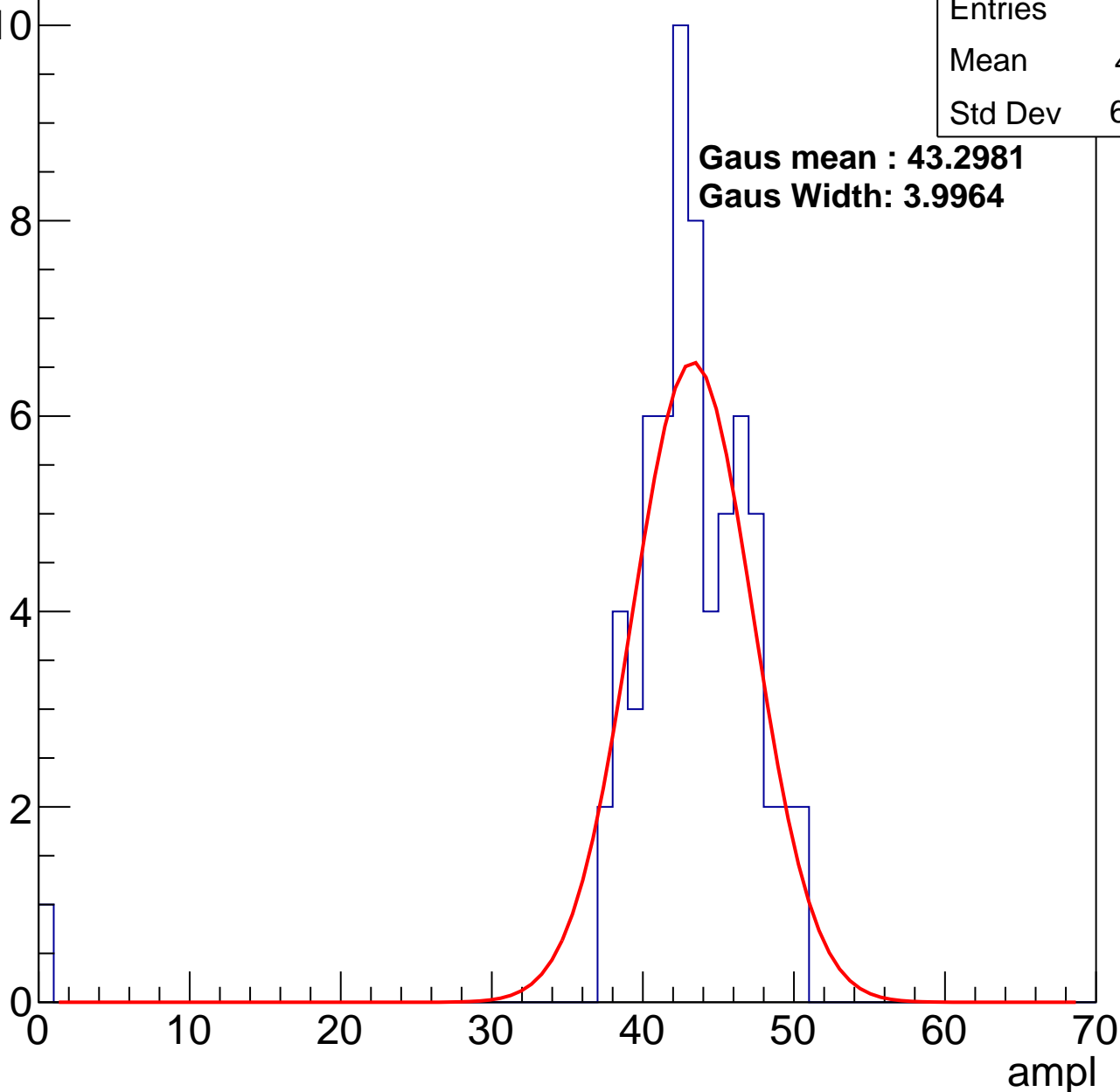
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.41
Std Dev	6.169

Gaus mean : 43.2981

Gaus Width: 3.9964

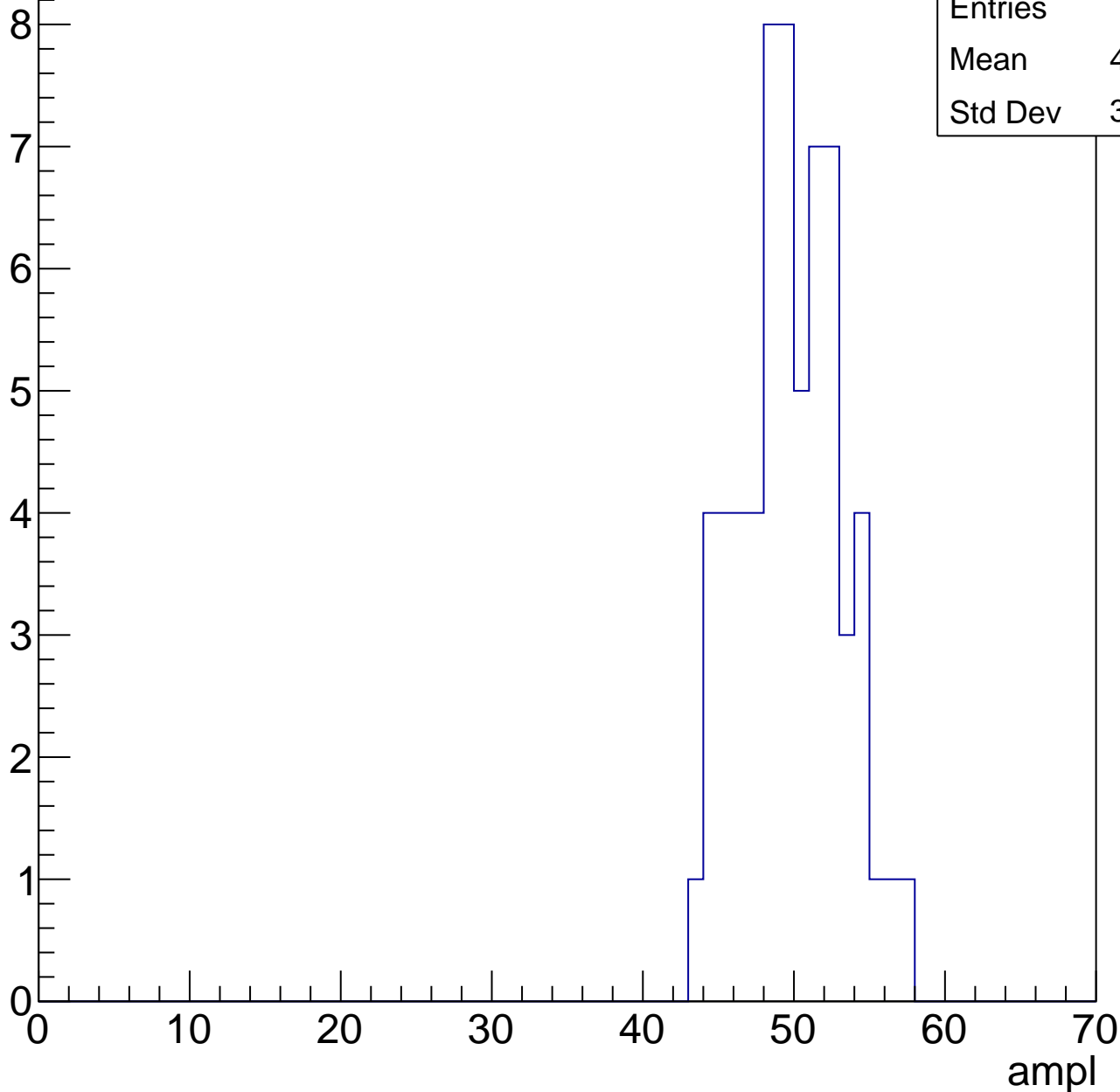


B1L103S, U21-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	49.37
Std Dev	3.224

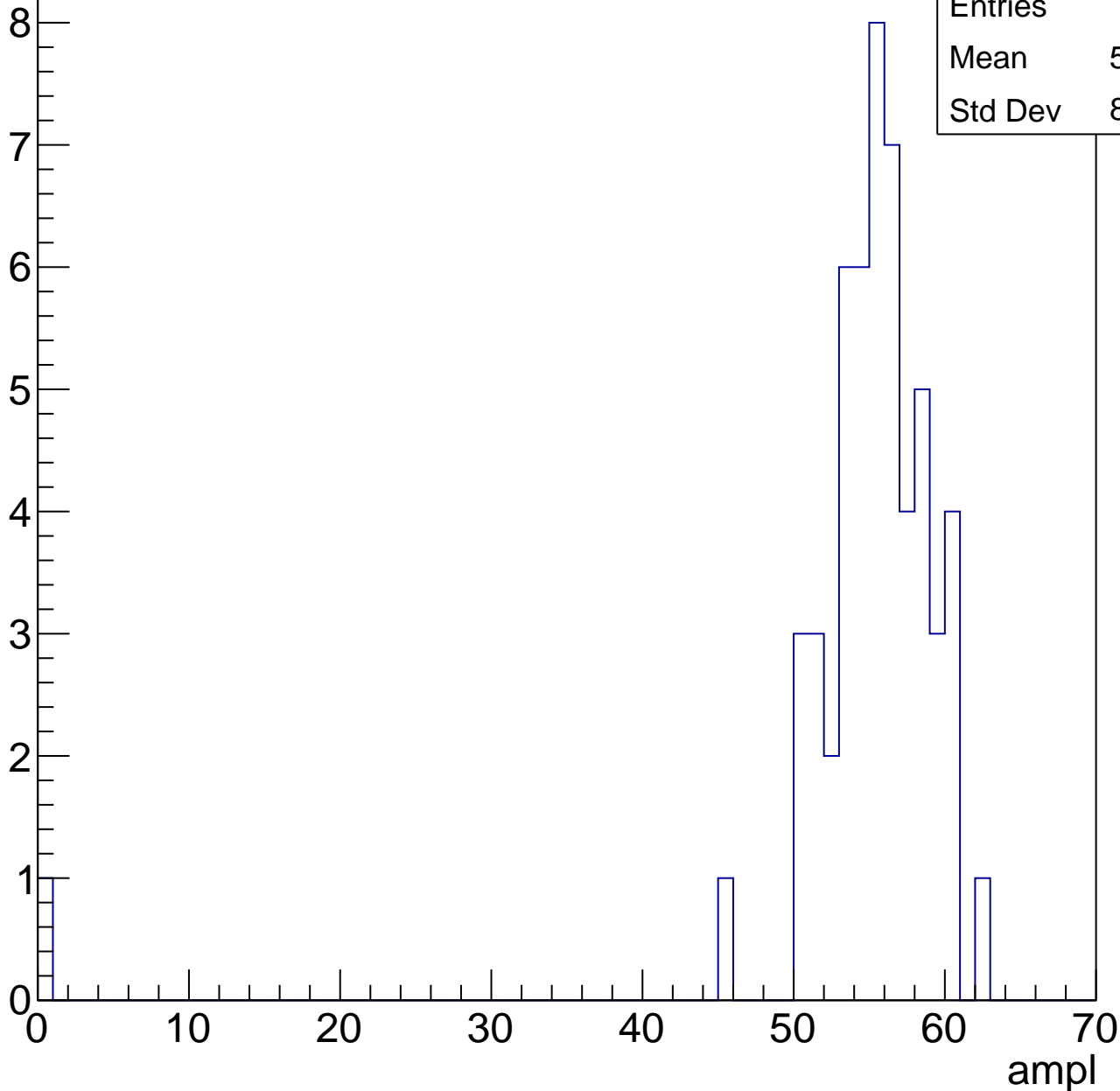


B1L103S, U21-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	54.13
Std Dev	8.074

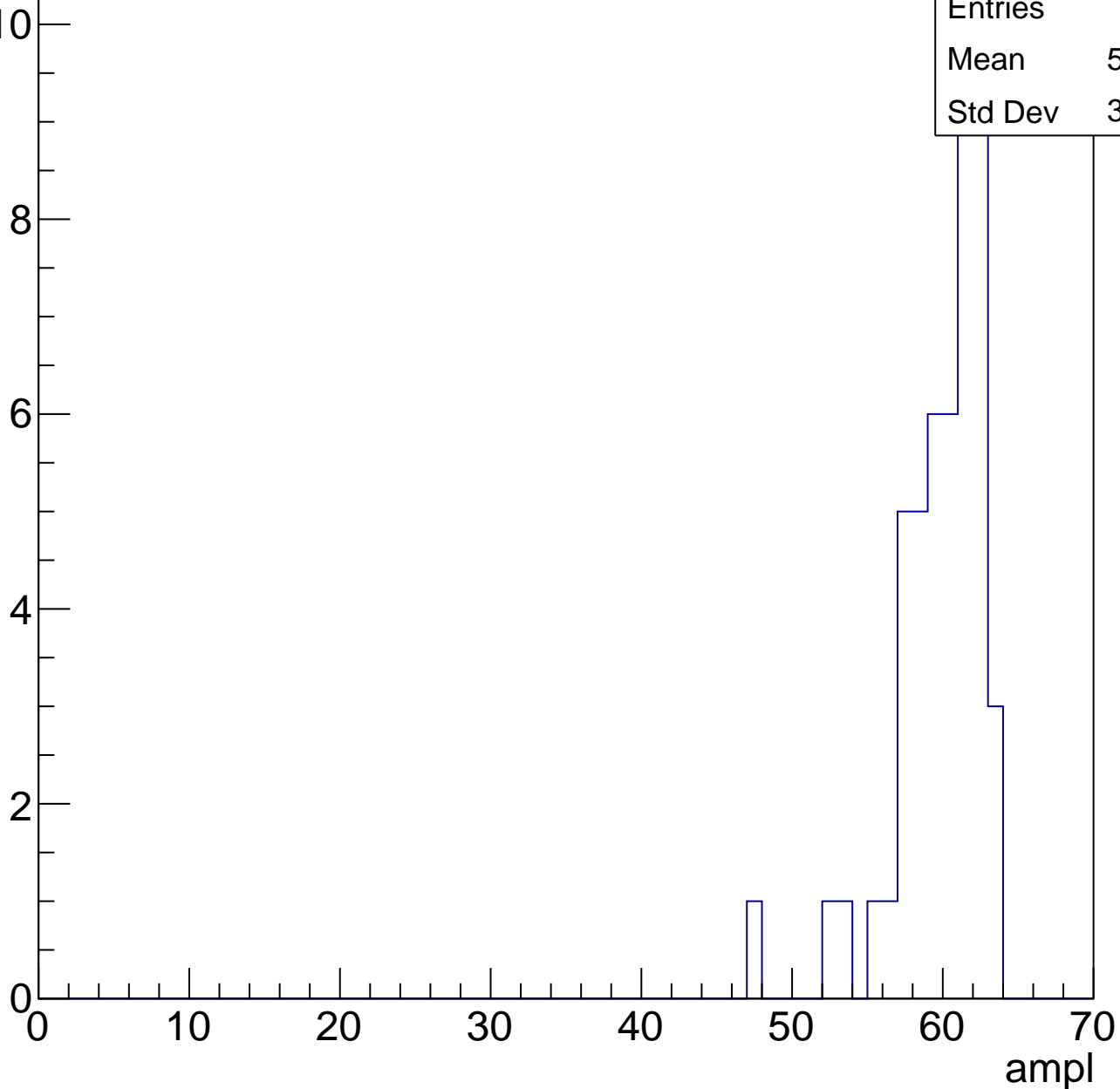


B1L103S, U21-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	59.37
Std Dev	3.022

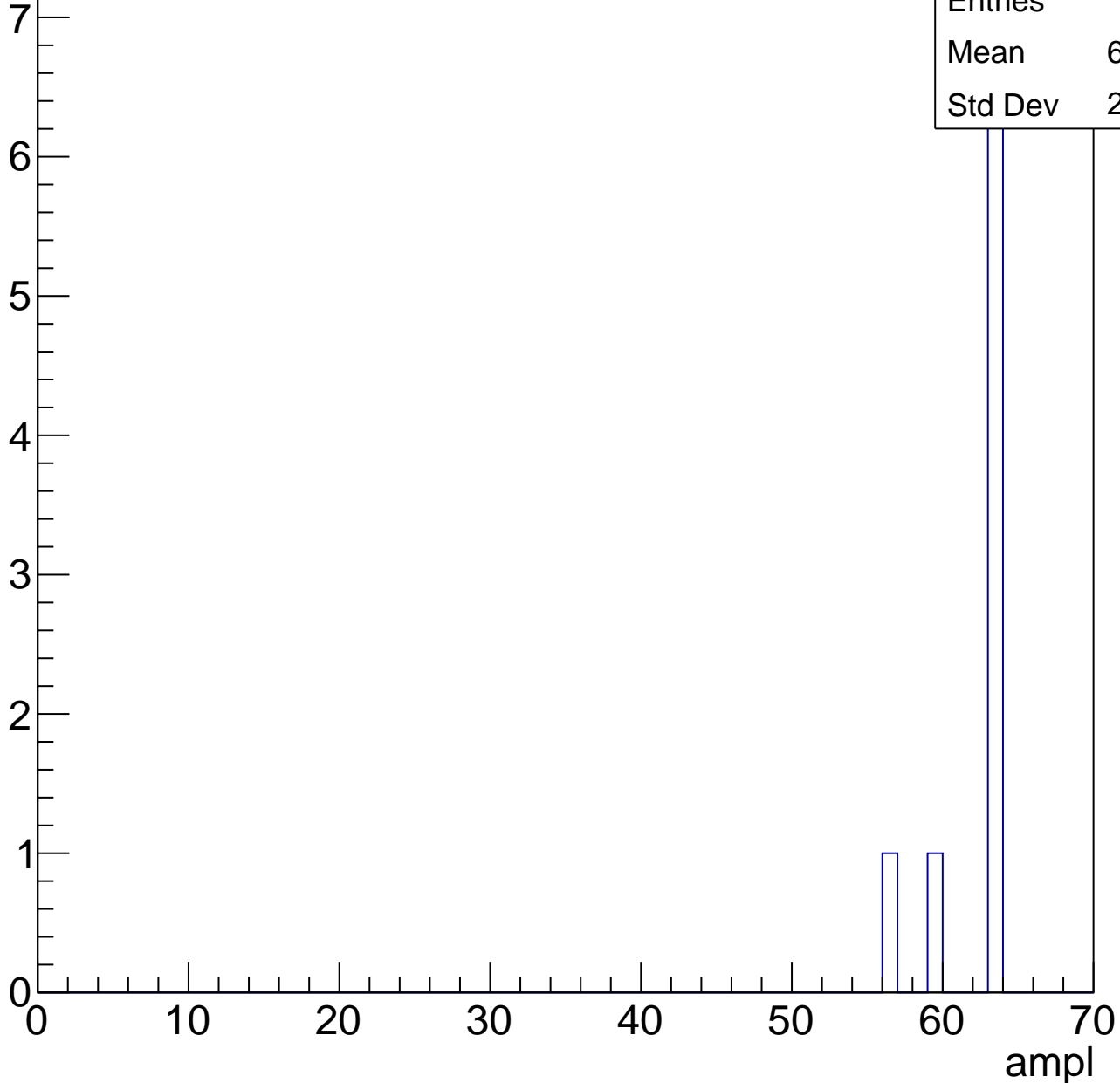


B1L103S, U21-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	61.78
Std Dev	2.393



B1L103S, U21-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

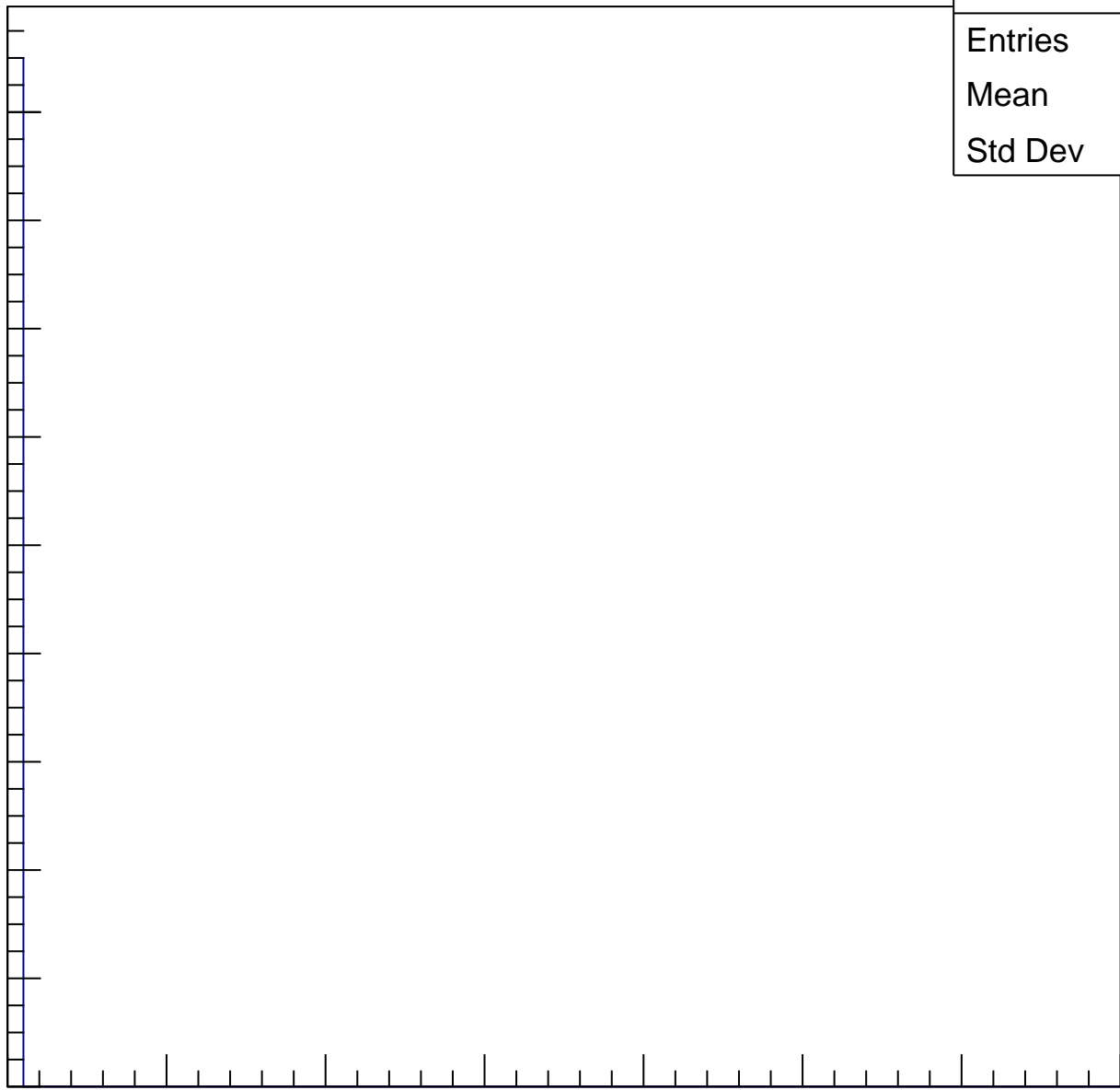
Entries	19
Mean	0
Std Dev	0

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch19, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	22.4
Std Dev	11.8

Gaus mean : 29.1174

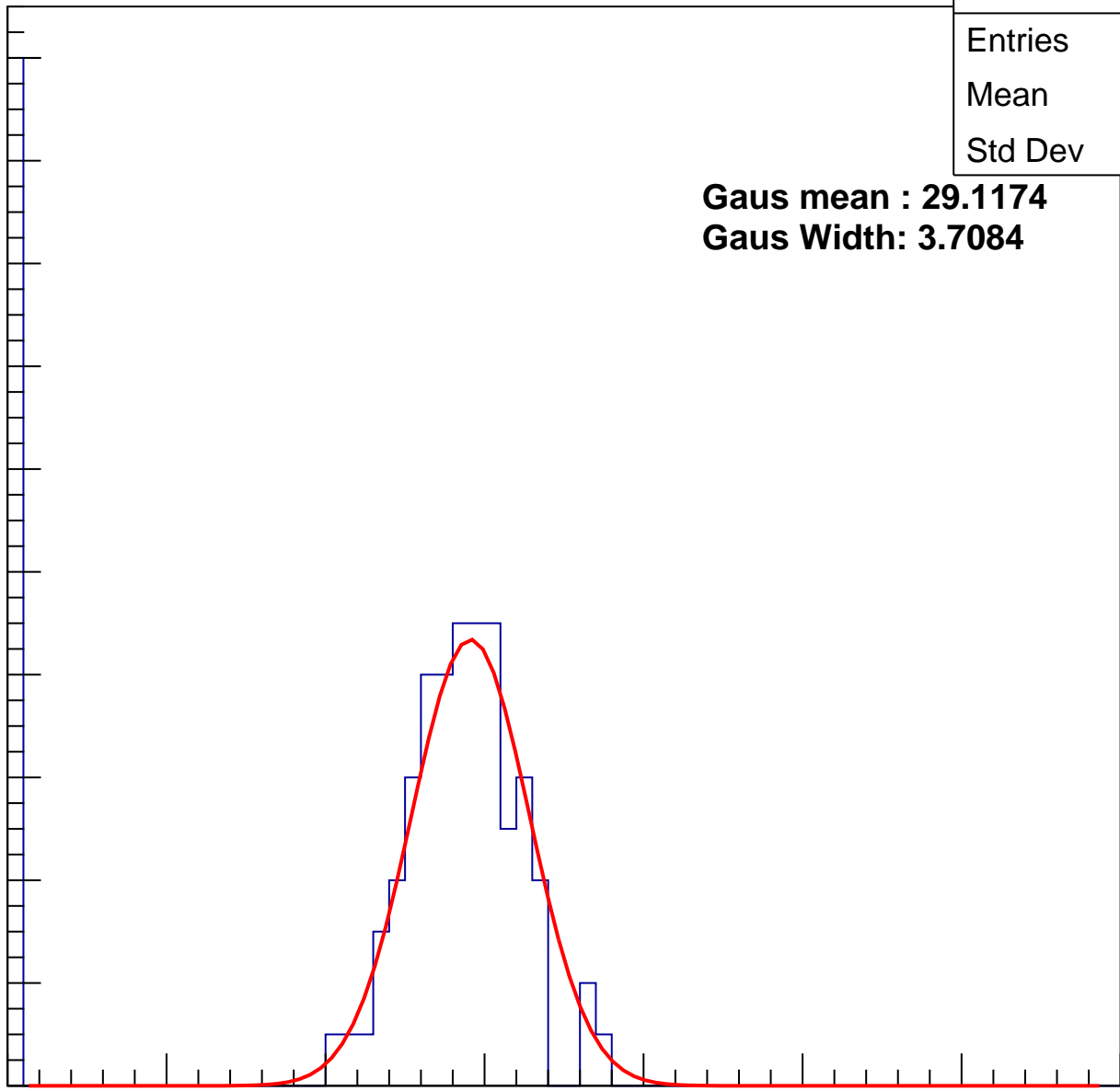
Gaus Width: 3.7084

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch19, adc1

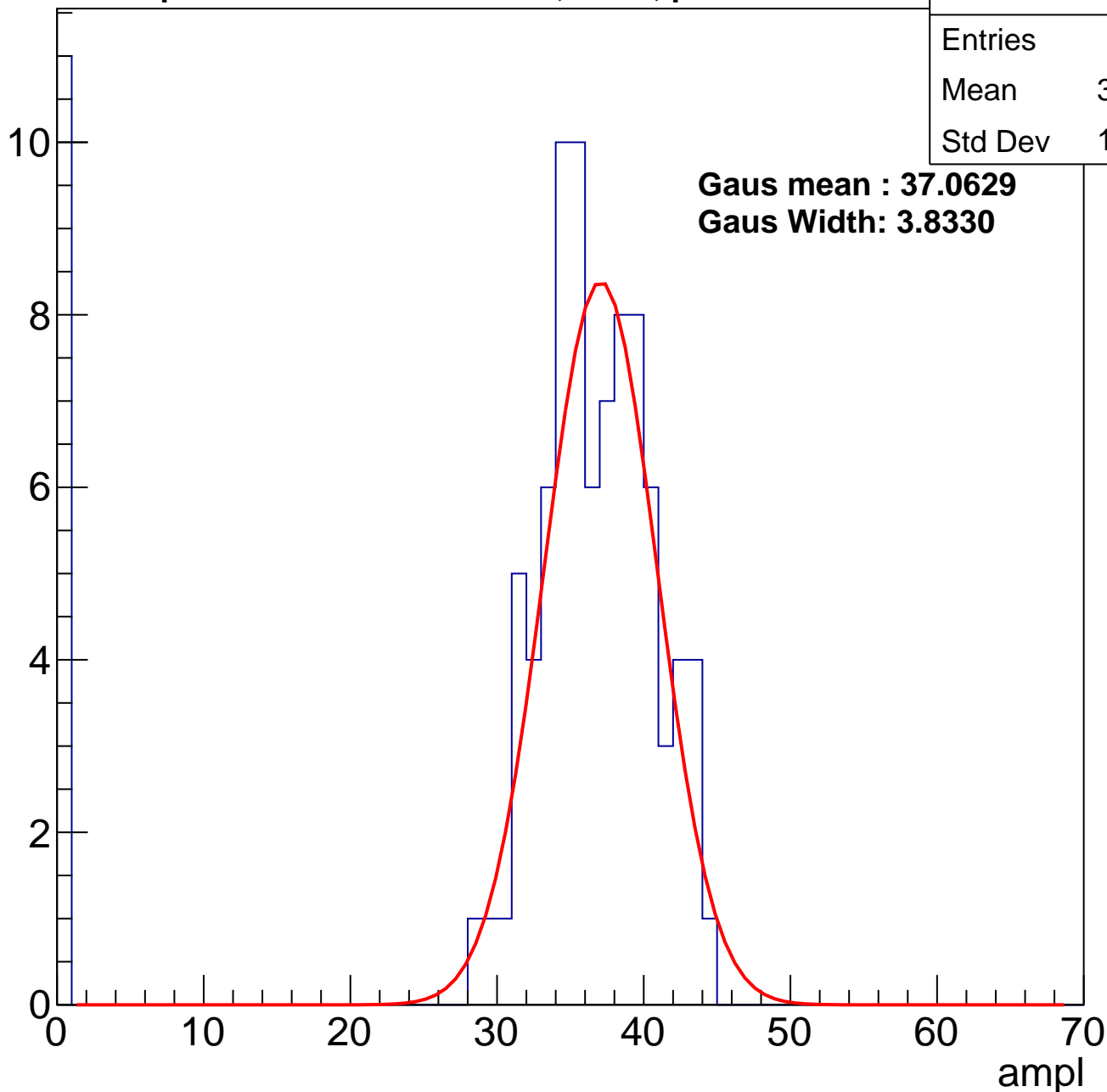
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	32.25
Std Dev	12.09

Gaus mean : 37.0629

Gaus Width: 3.8330

Entry



B1L103S, U21-ch19, adc2

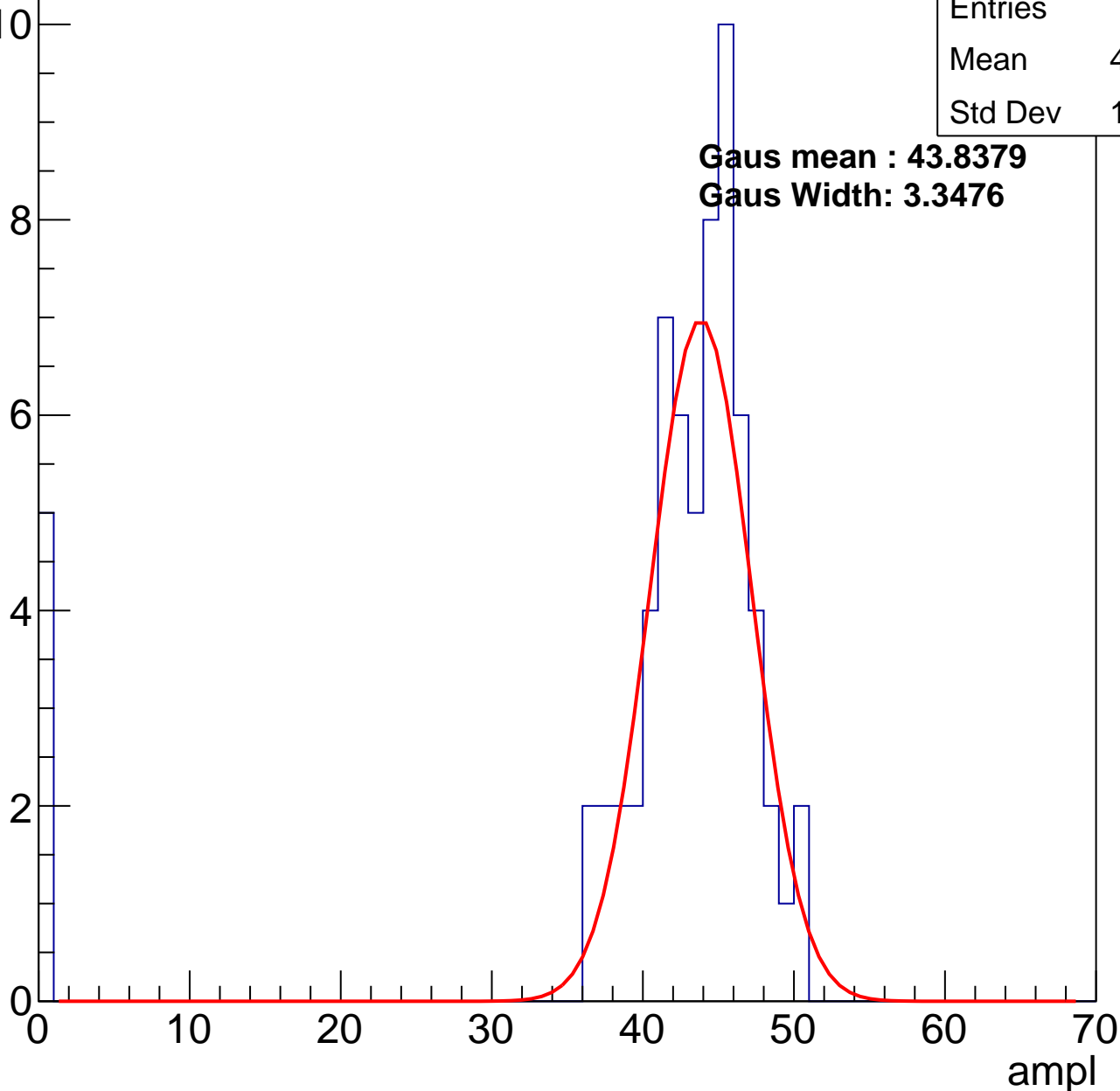
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	40.07
Std Dev	11.72

Gaus mean : 43.8379

Gaus Width: 3.3476

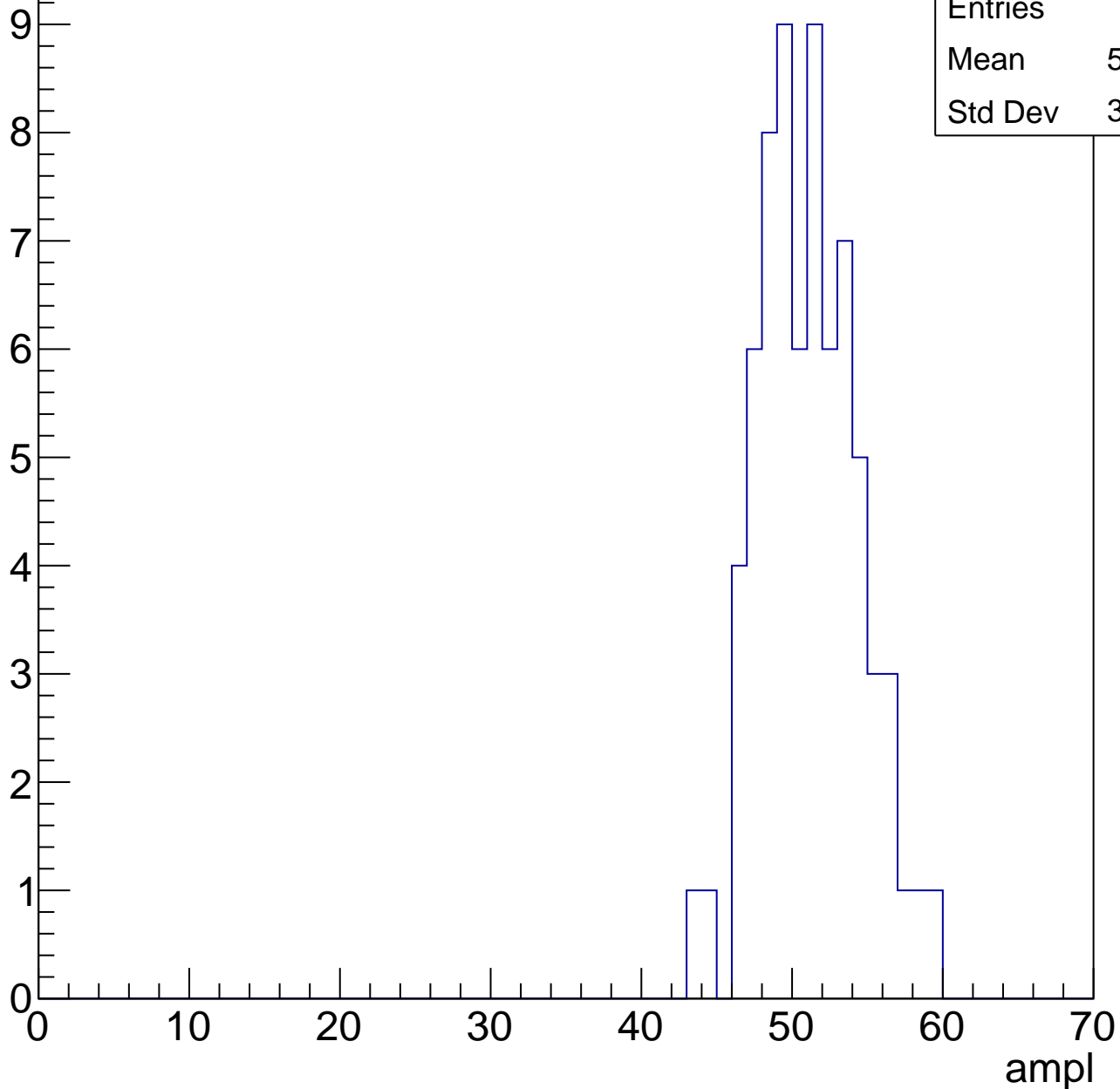
Entry



B1L103S, U21-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



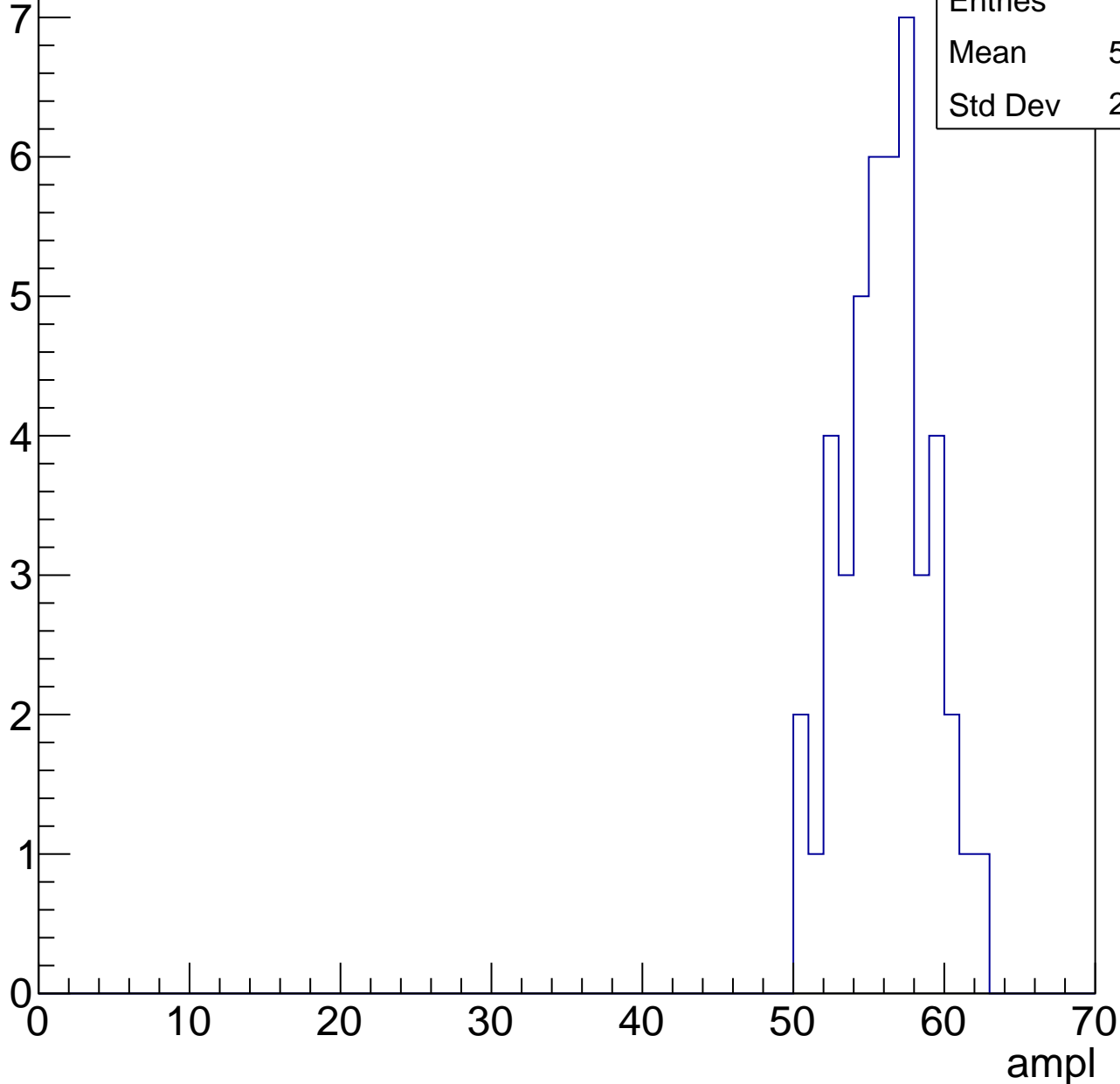
Entries	71
Mean	50.66
Std Dev	3.284

B1L103S, U21-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	55.69
Std Dev	2.819

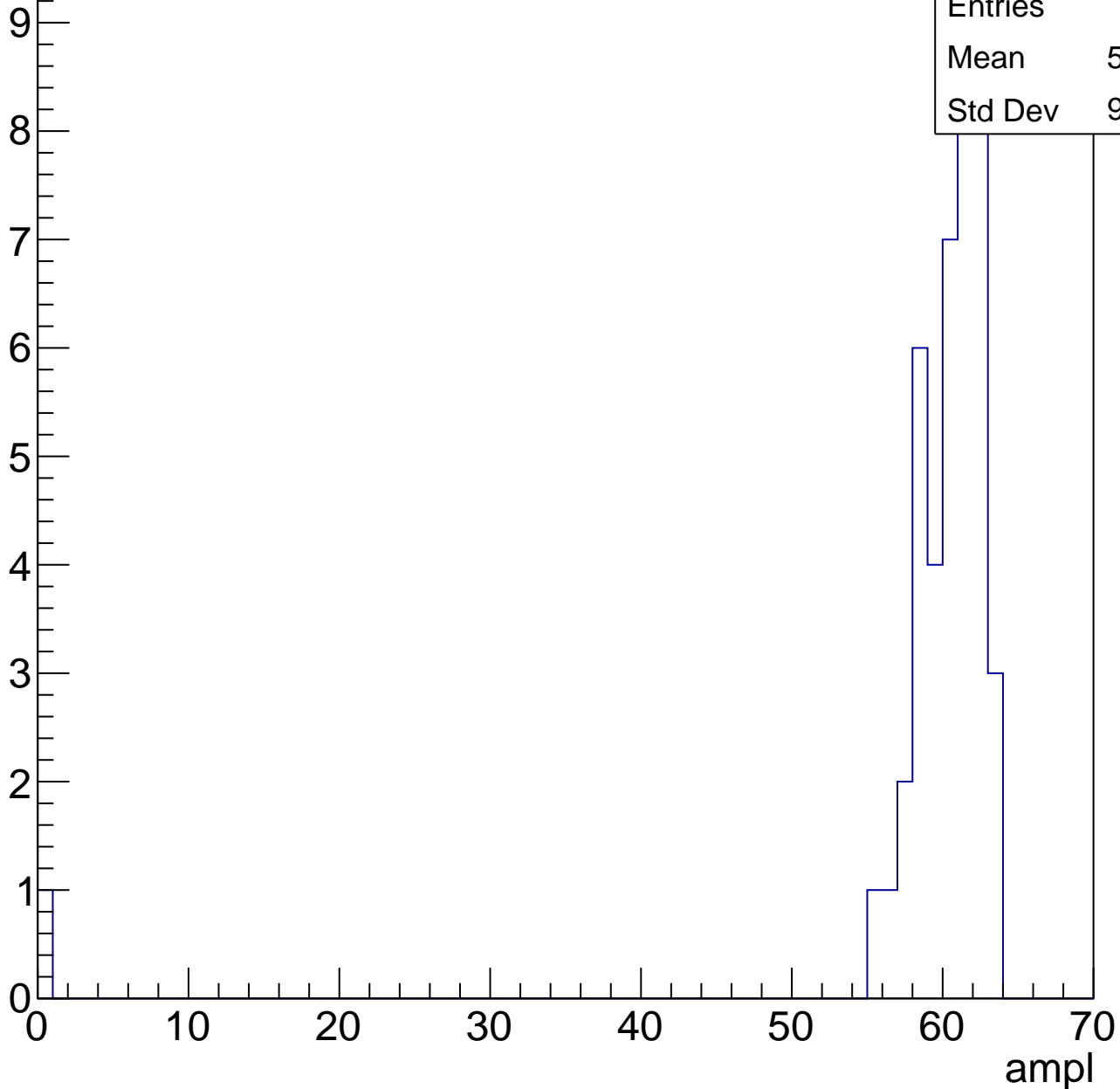


B1L103S, U21-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.67
Std Dev	9.365

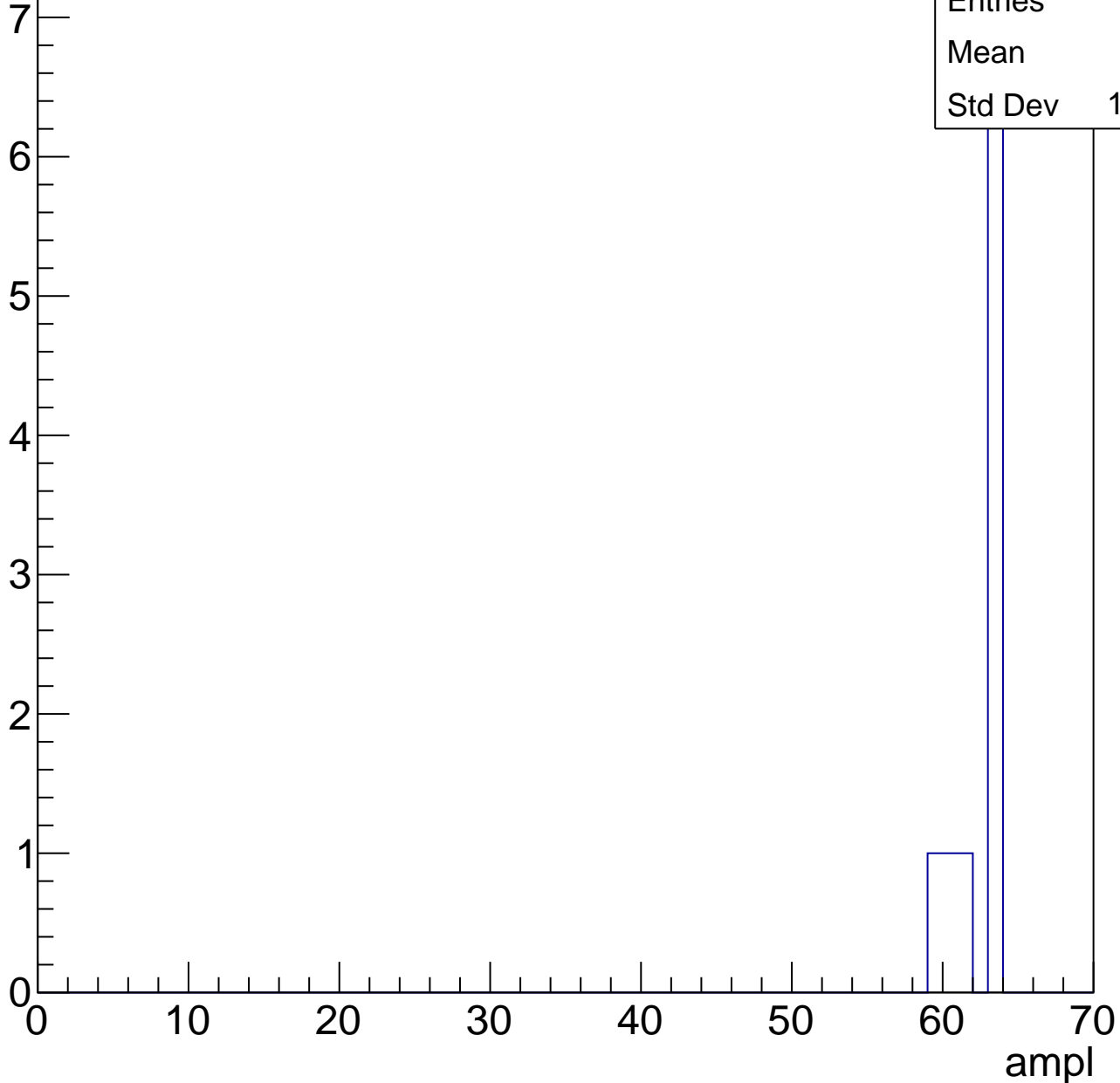


B1L103S, U21-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	62.1
Std Dev	1.446



B1L103S, U21-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch20, adc0

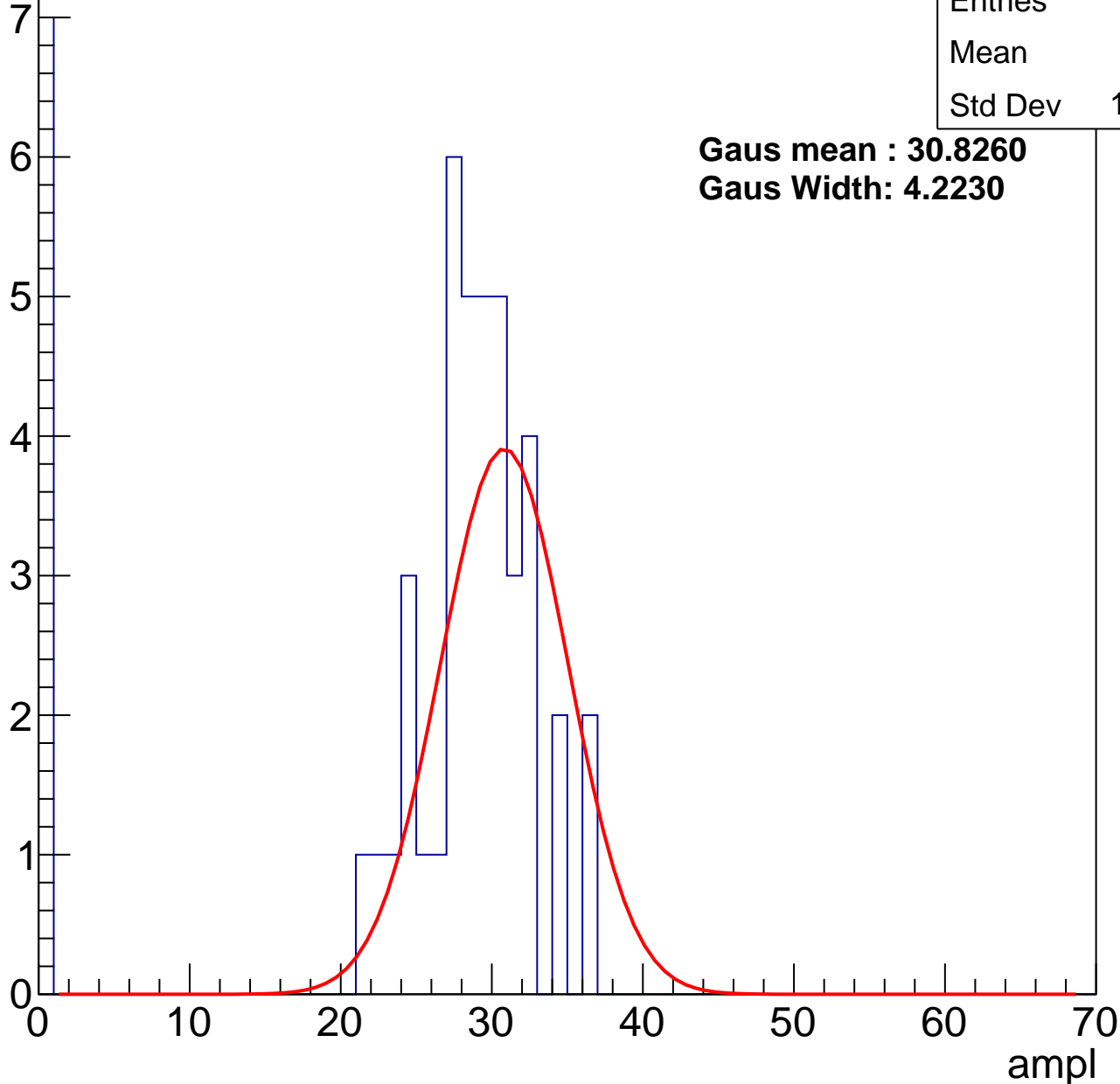
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	24.4
Std Dev	10.69

Gaus mean : 30.8260

Gaus Width: 4.2230



B1L103S, U21-ch20, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	32.21
Std Dev	9.458

Gaus mean : 35.1221

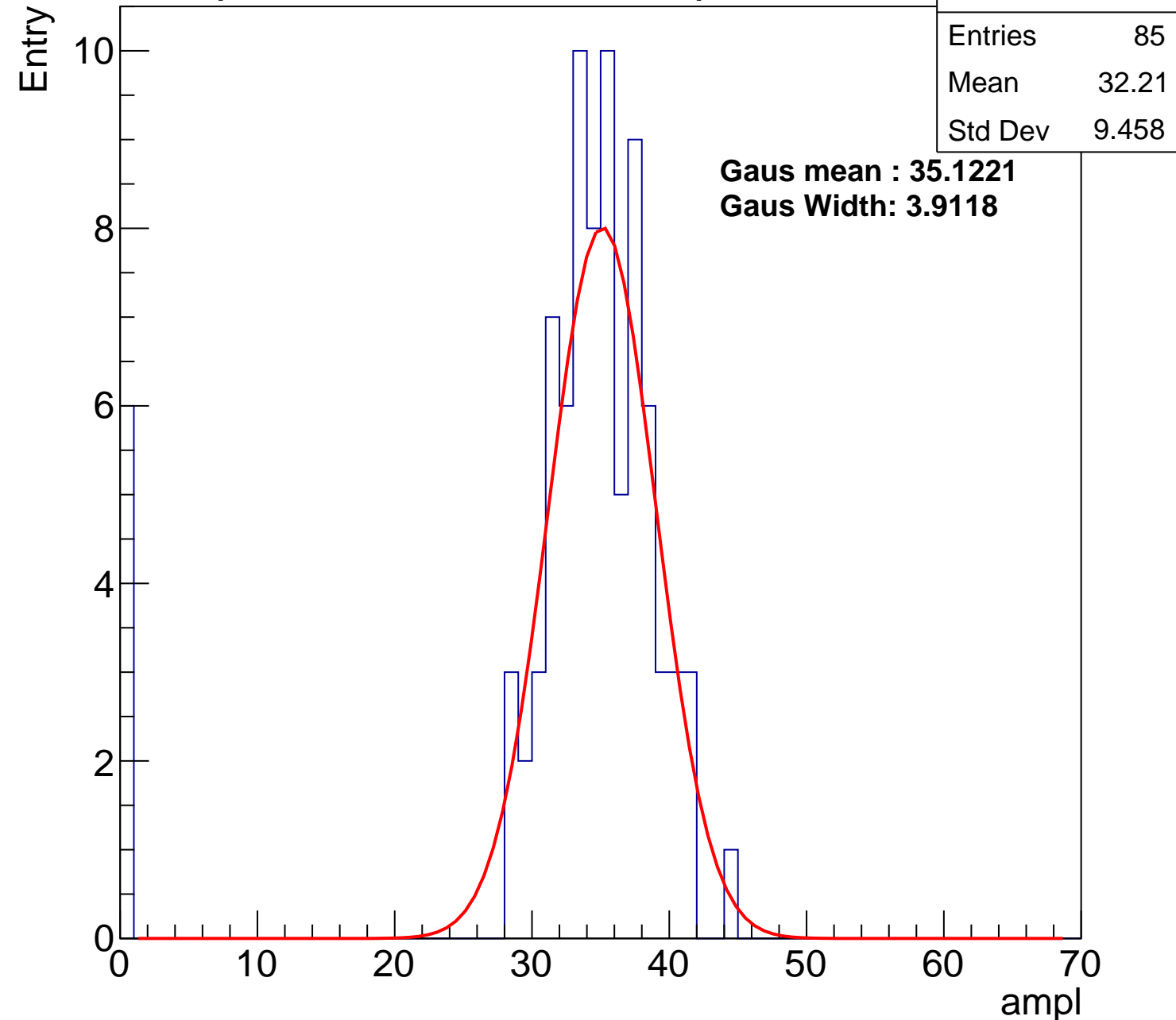
Gaus Width: 3.9118

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch20, adc2

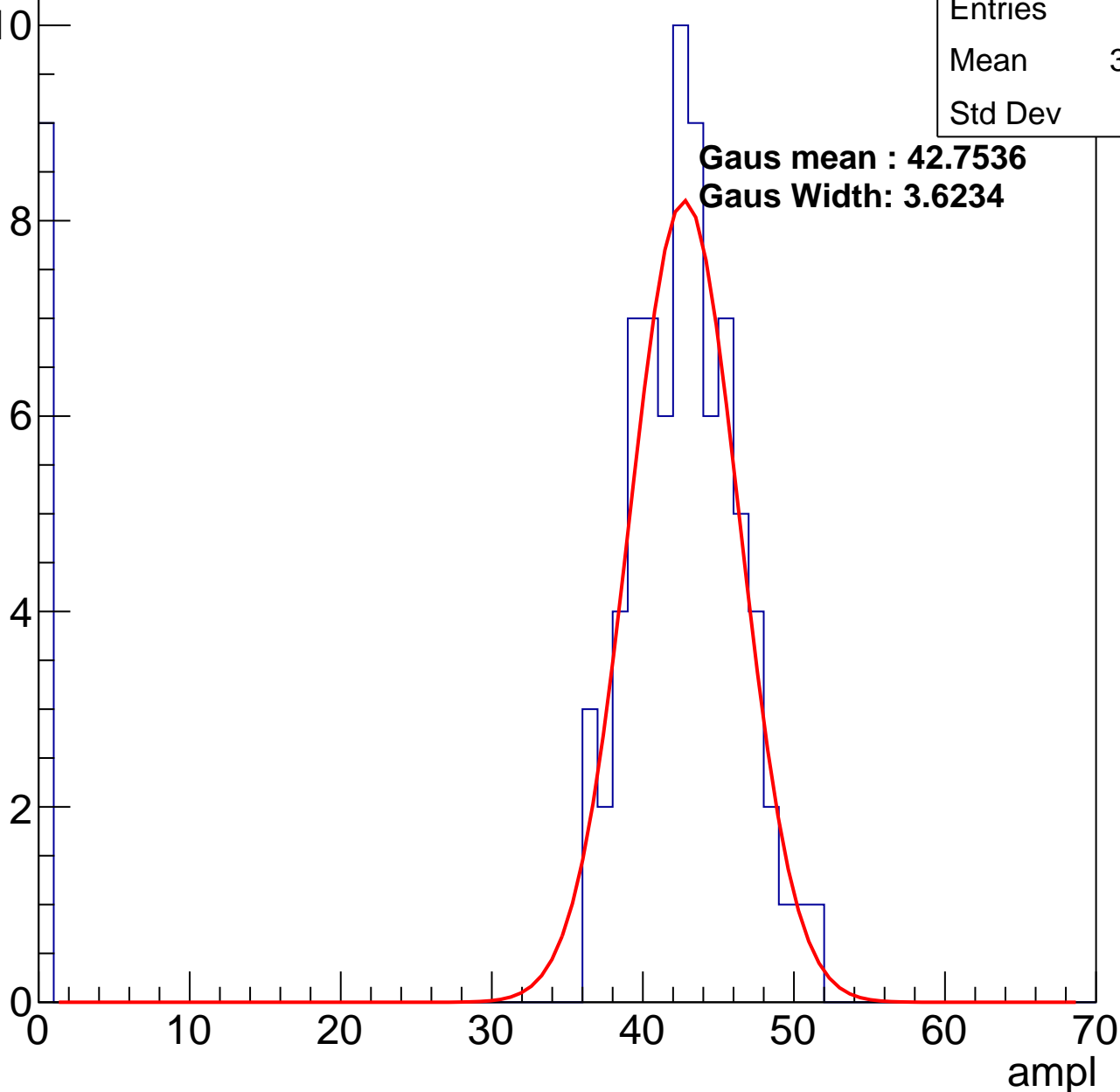
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	37.89
Std Dev	13.5

Gaus mean : 42.7536

Gaus Width: 3.6234

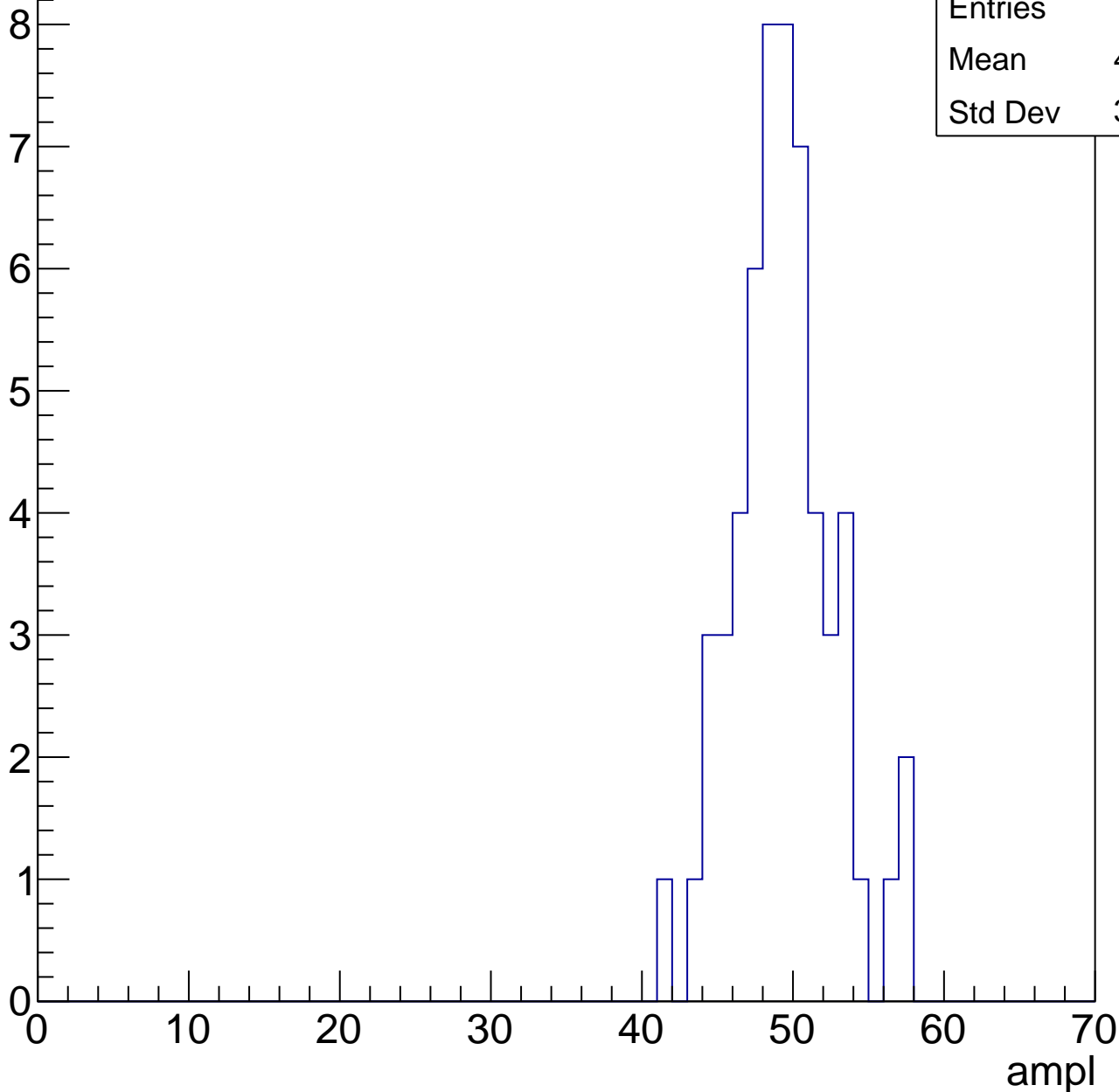


B1L103S, U21-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

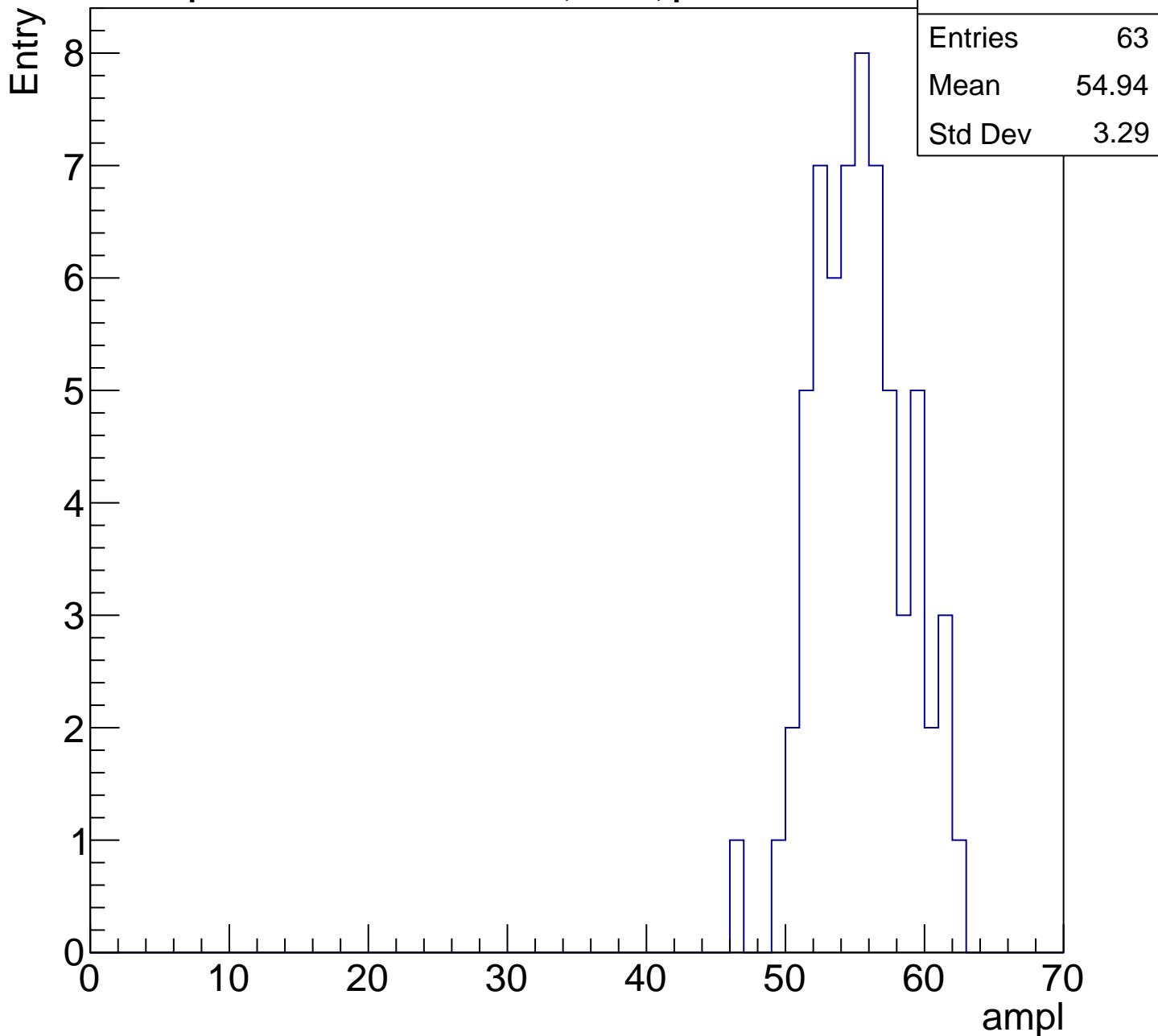
Entry

Entries	56
Mean	48.91
Std Dev	3.291



B1L103S, U21-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

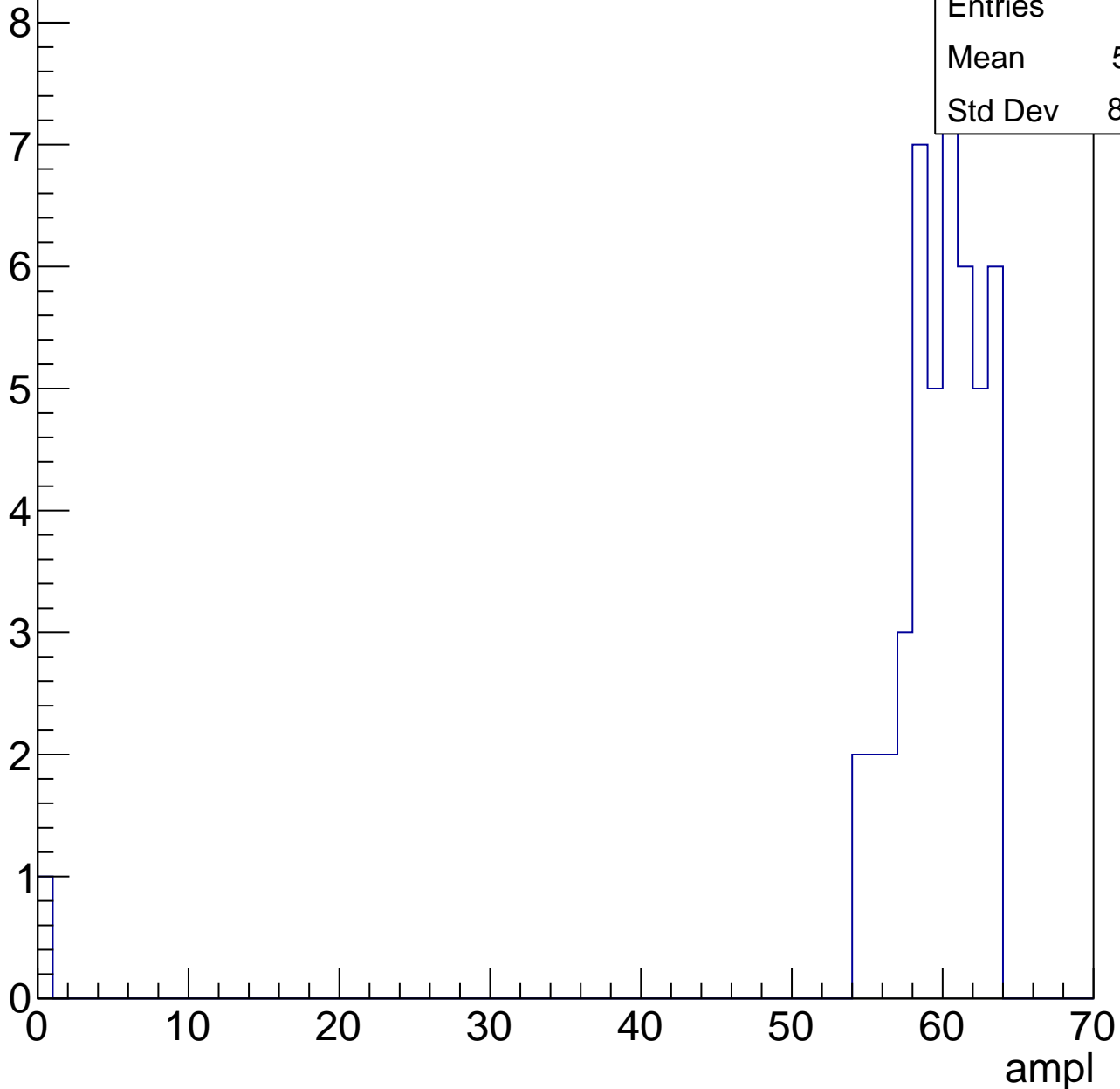


B1L103S, U21-ch20, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

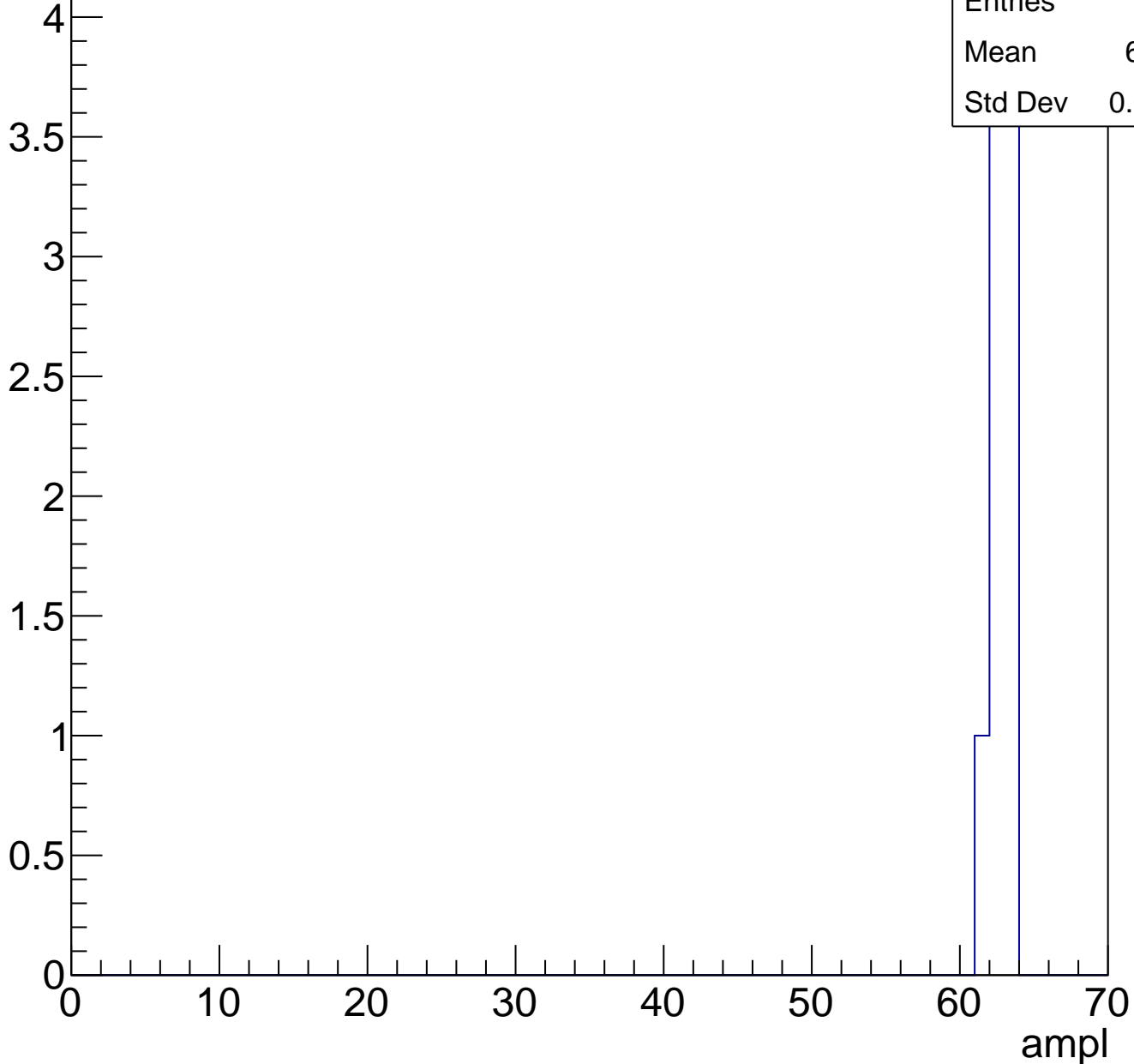
Entries	47
Mean	58.21
Std Dev	8.925



B1L103S, U21-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U21-ch21, adc0

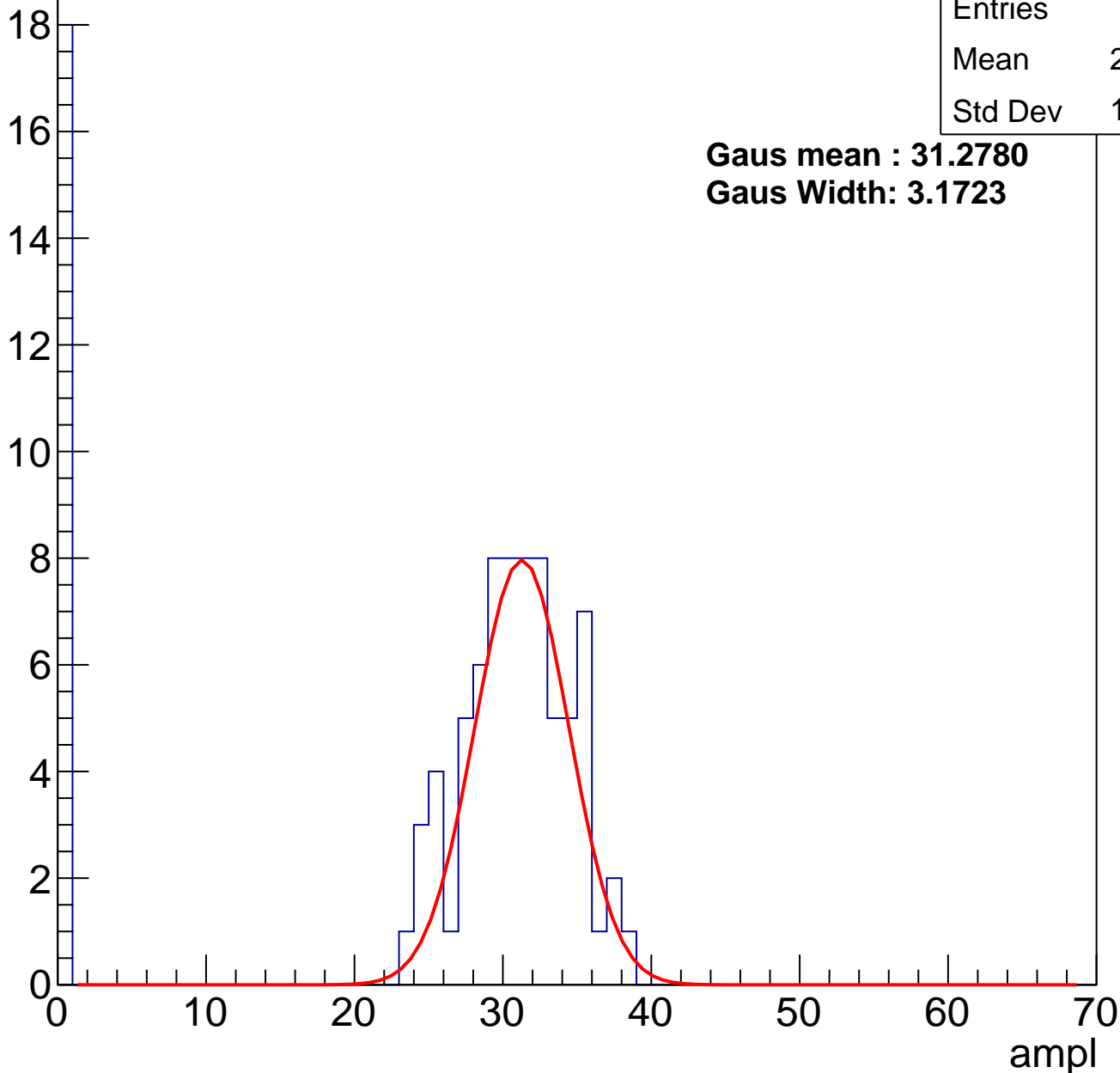
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	24.48
Std Dev	12.54

Gaus mean : 31.2780

Gaus Width: 3.1723

Entry



B1L103S, U21-ch21, adc1

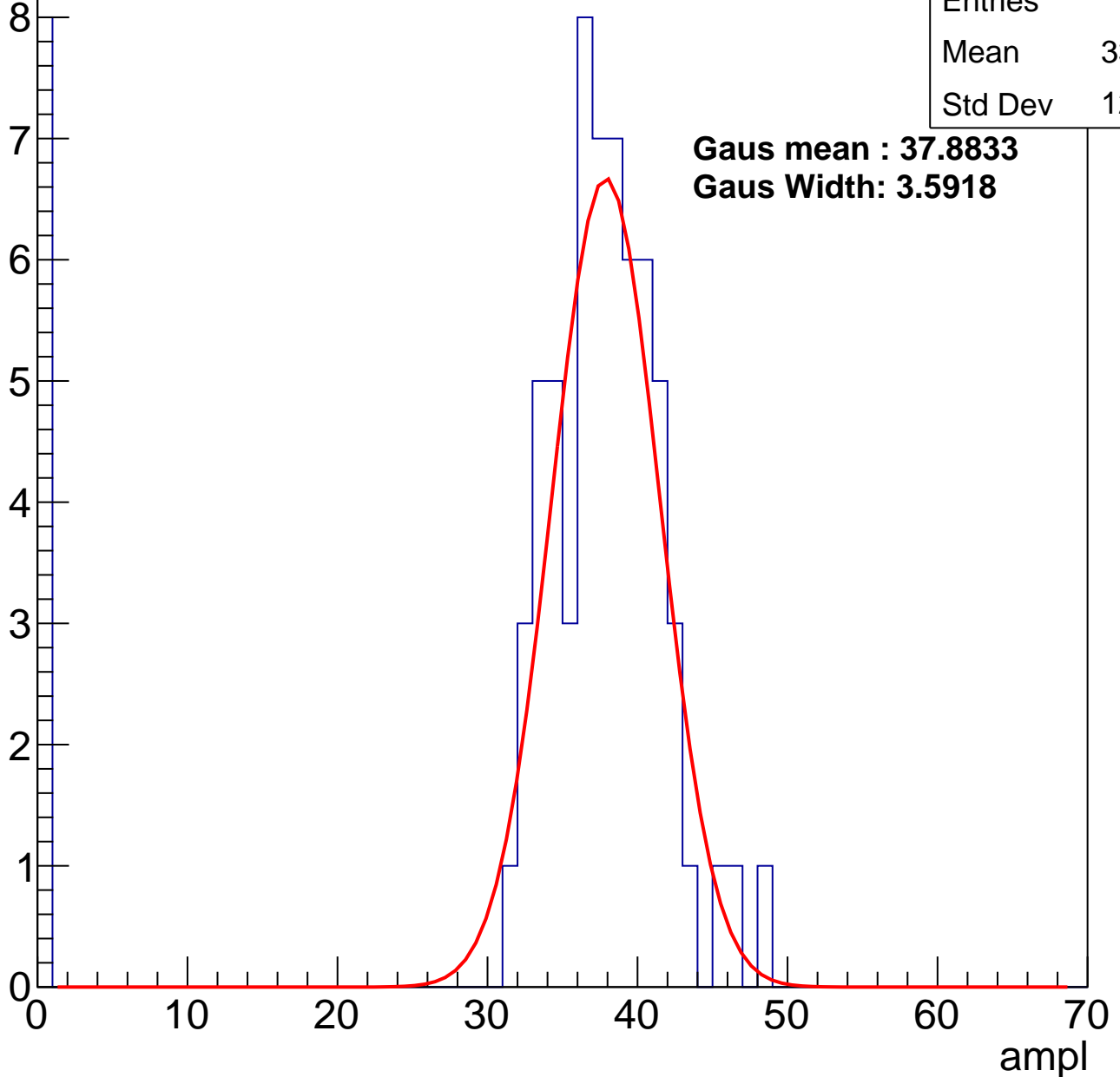
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	33.34
Std Dev	12.33

Gaus mean : 37.8833

Gaus Width: 3.5918



B1L103S, U21-ch21, adc2

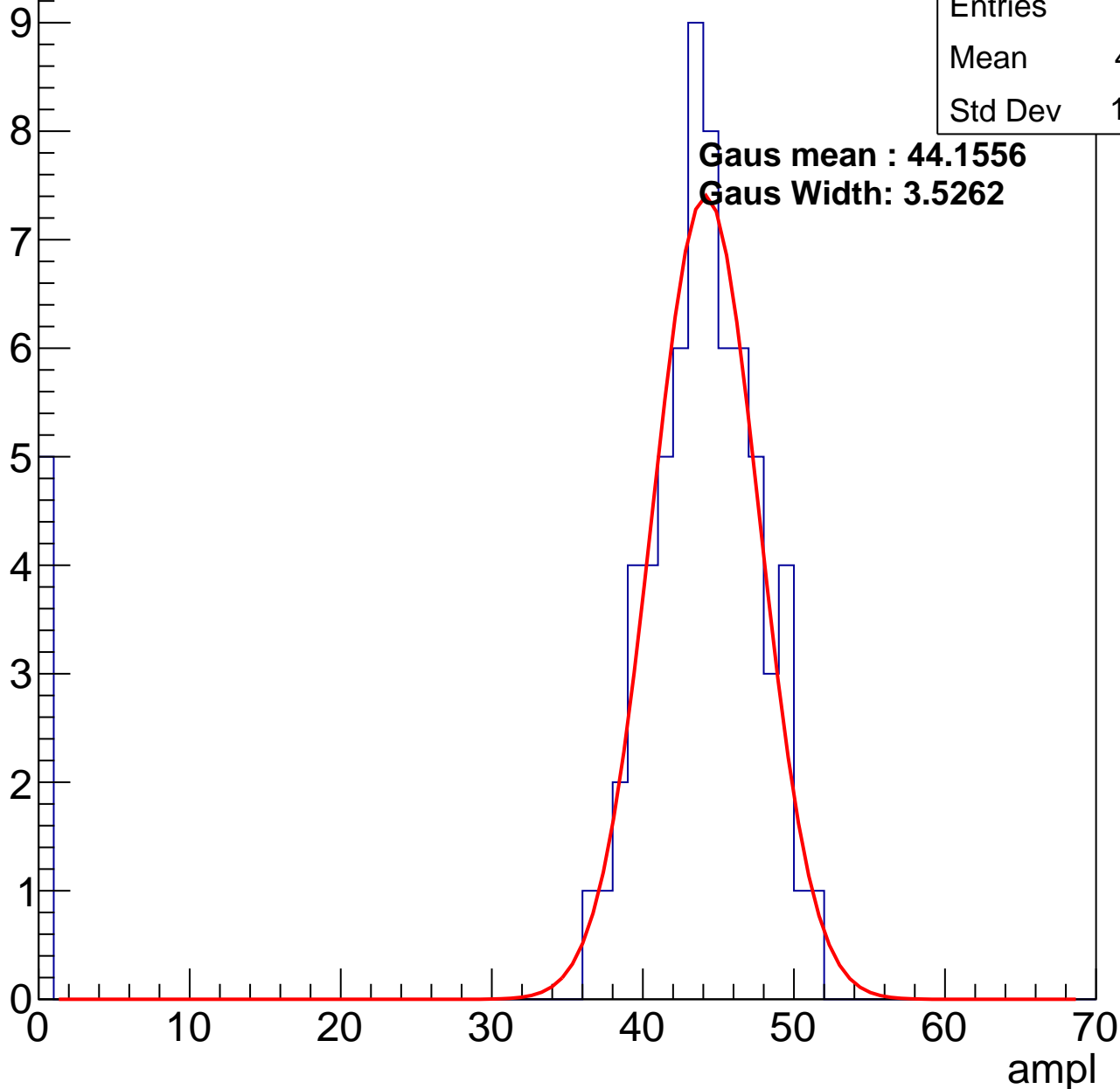
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	40.61
Std Dev	11.63

Gaus mean : 44.1556

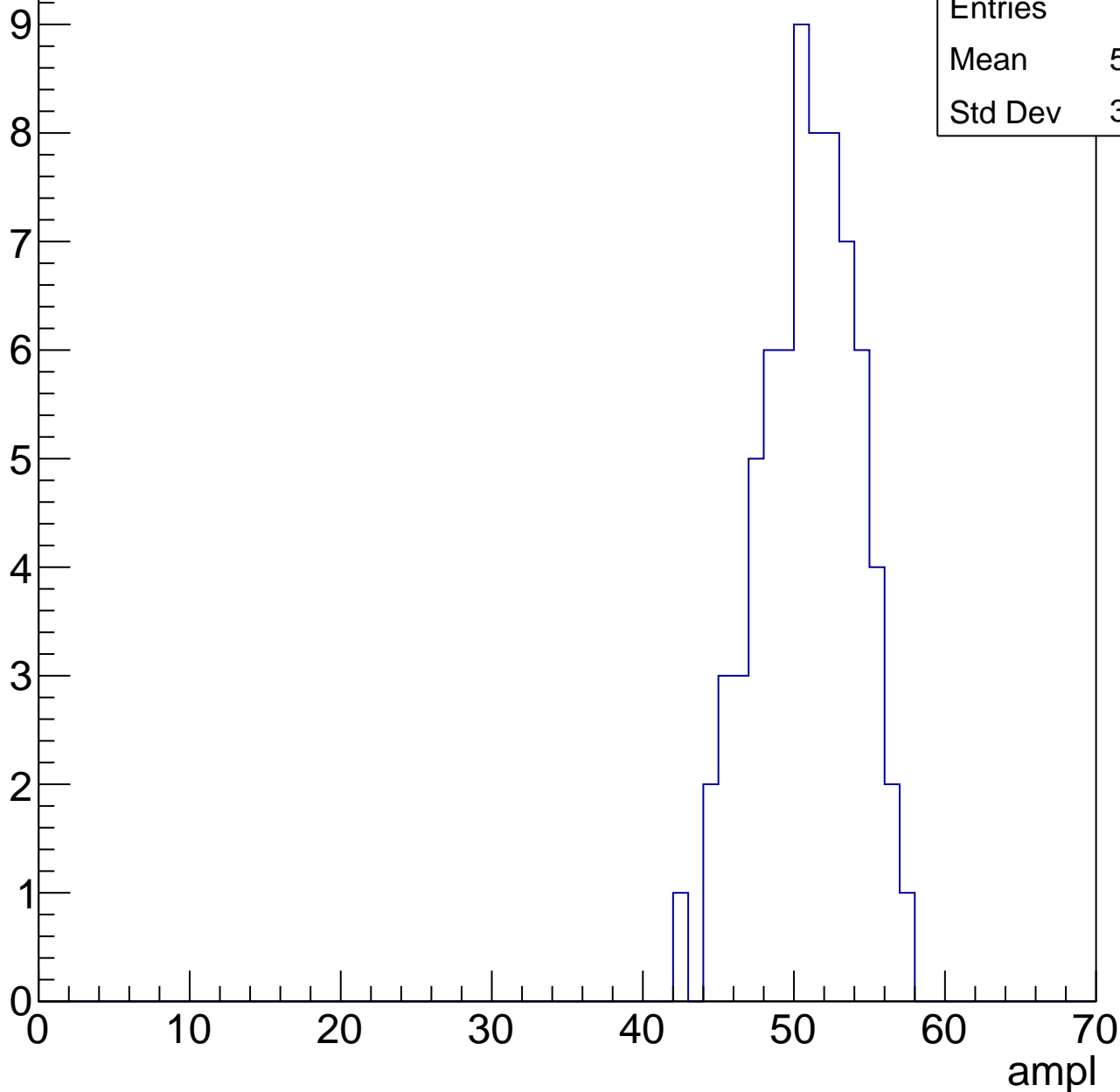
Gaus Width: 3.5262



B1L103S, U21-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



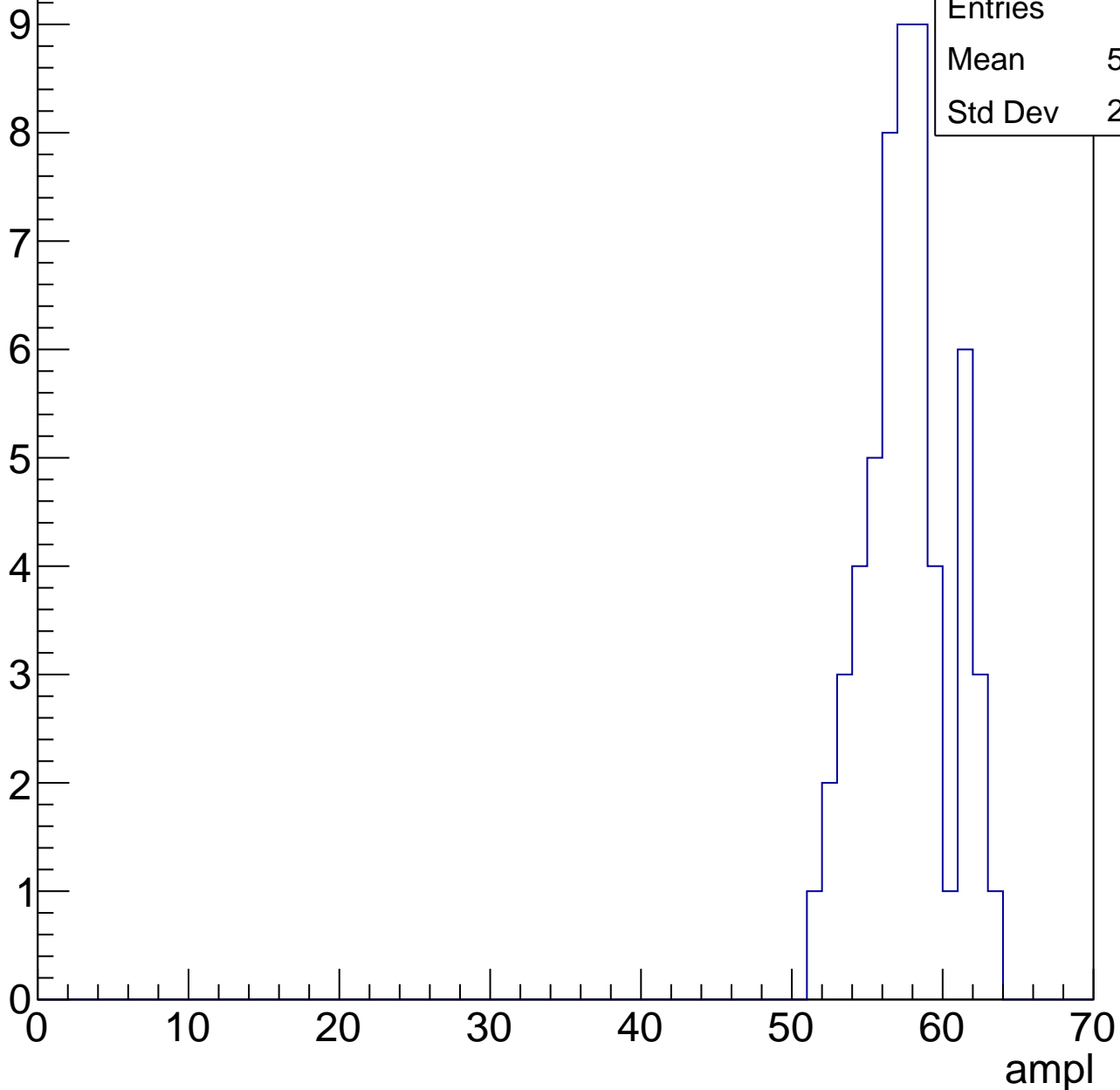
Entries	71
Mean	50.39
Std Dev	3.235

B1L103S, U21-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	57.12
Std Dev	2.797

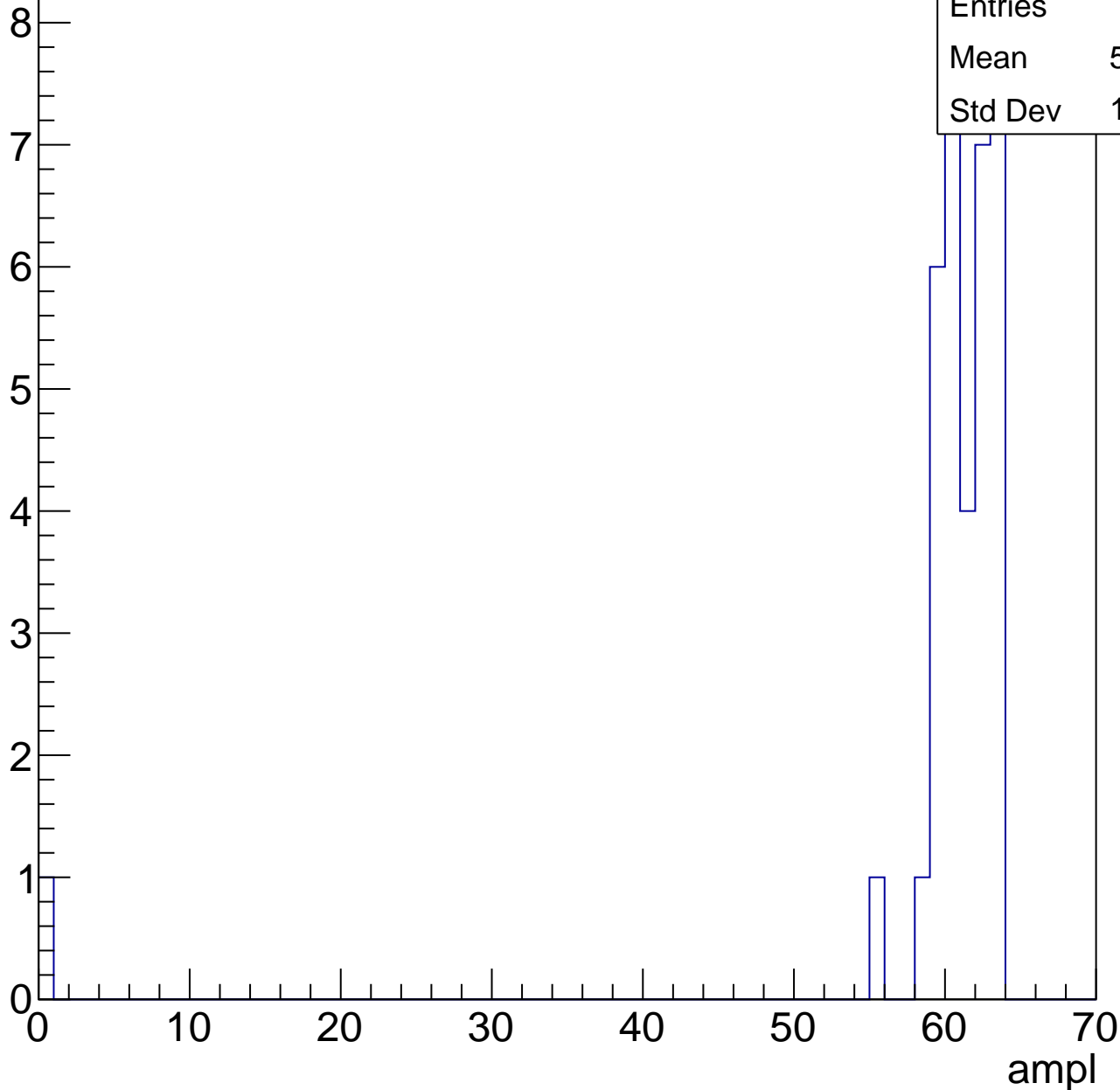


B1L103S, U21-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	59.14
Std Dev	10.15



B1L103S, U21-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch22, adc0

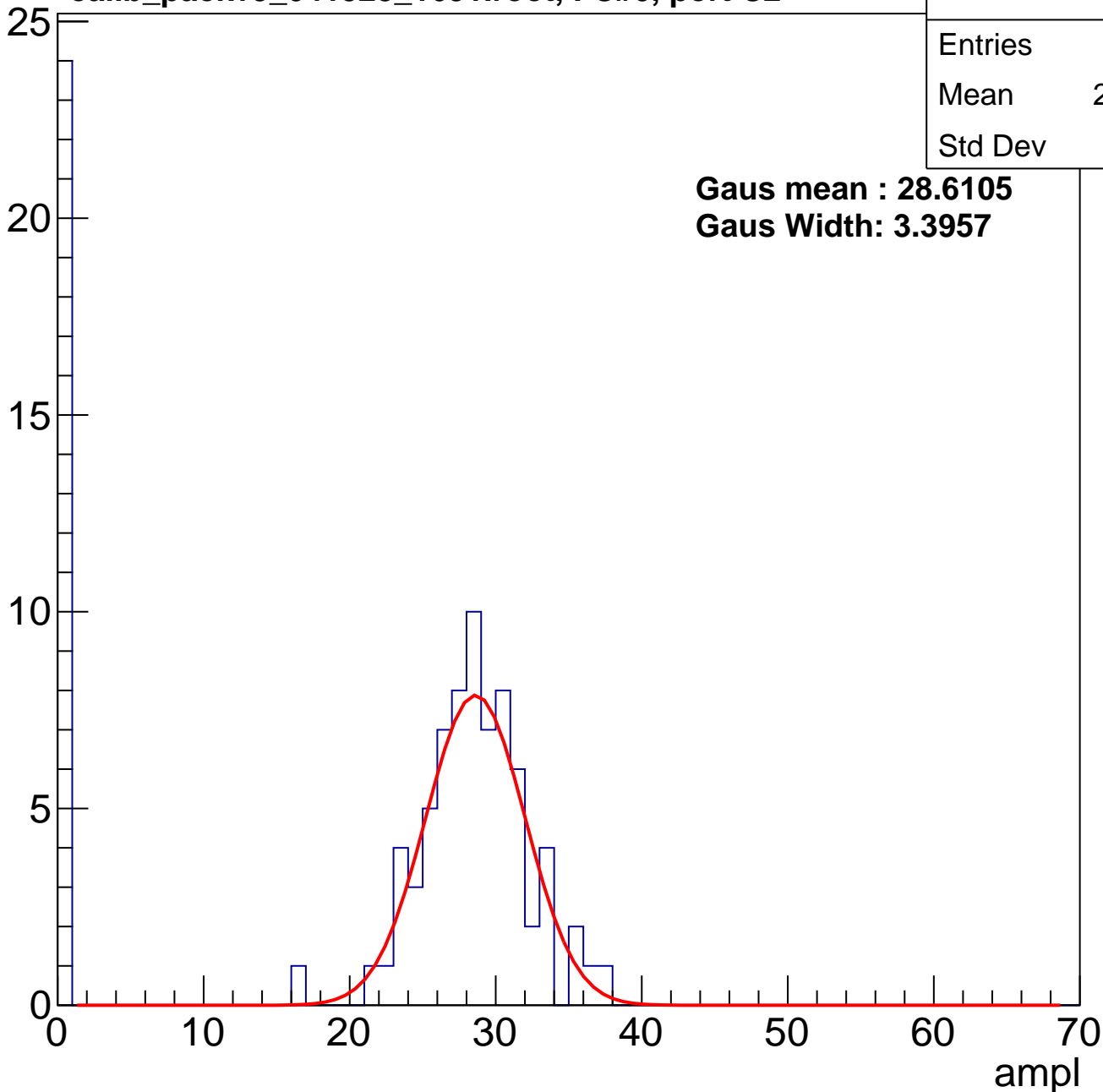
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	20.99
Std Dev	12.6

Gaus mean : 28.6105

Gaus Width: 3.3957

Entry



B1L103S, U21-ch22, adc1

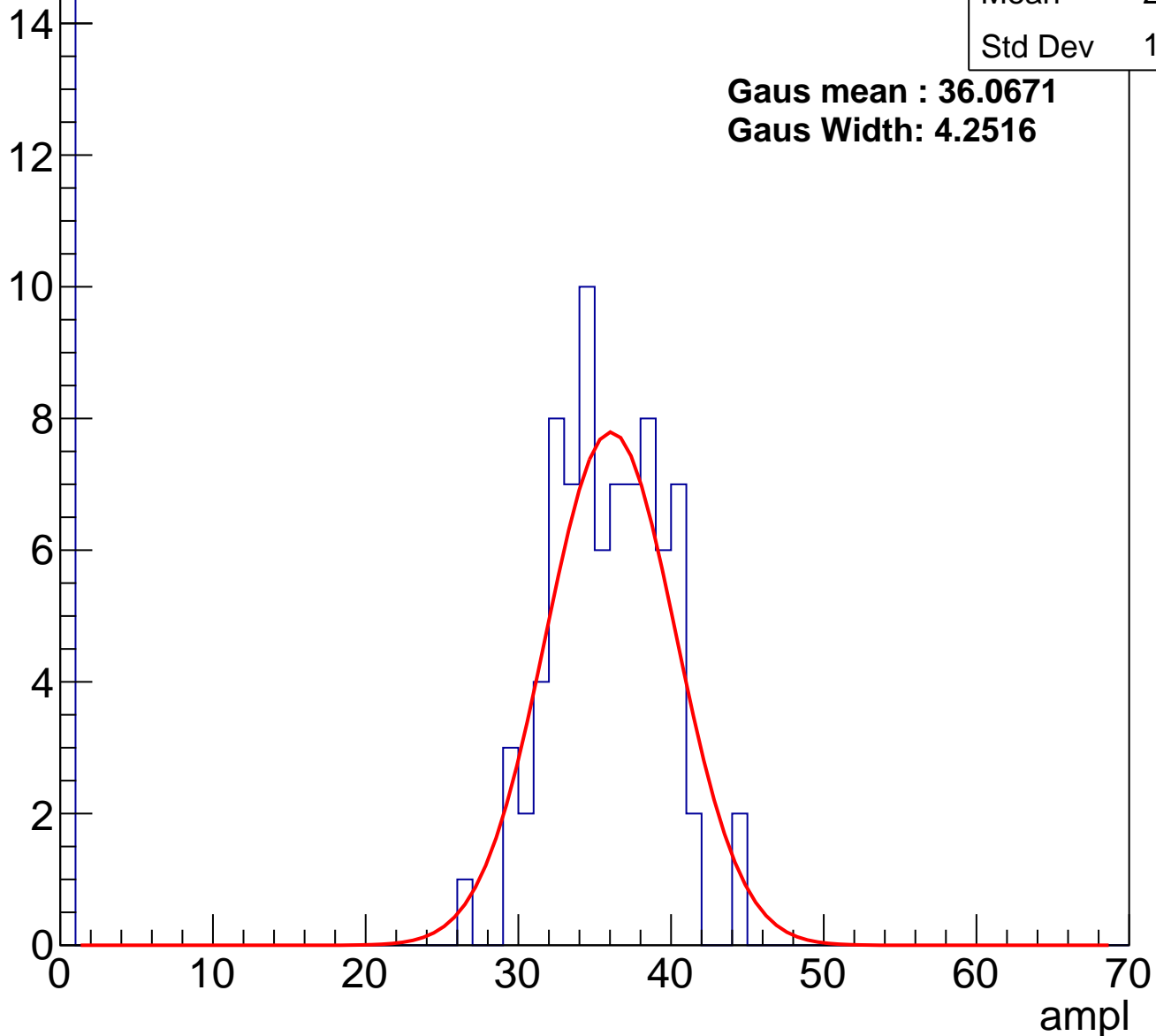
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	29.82
Std Dev	13.32

Gaus mean : 36.0671

Gaus Width: 4.2516

Entry



B1L103S, U21-ch22, adc2

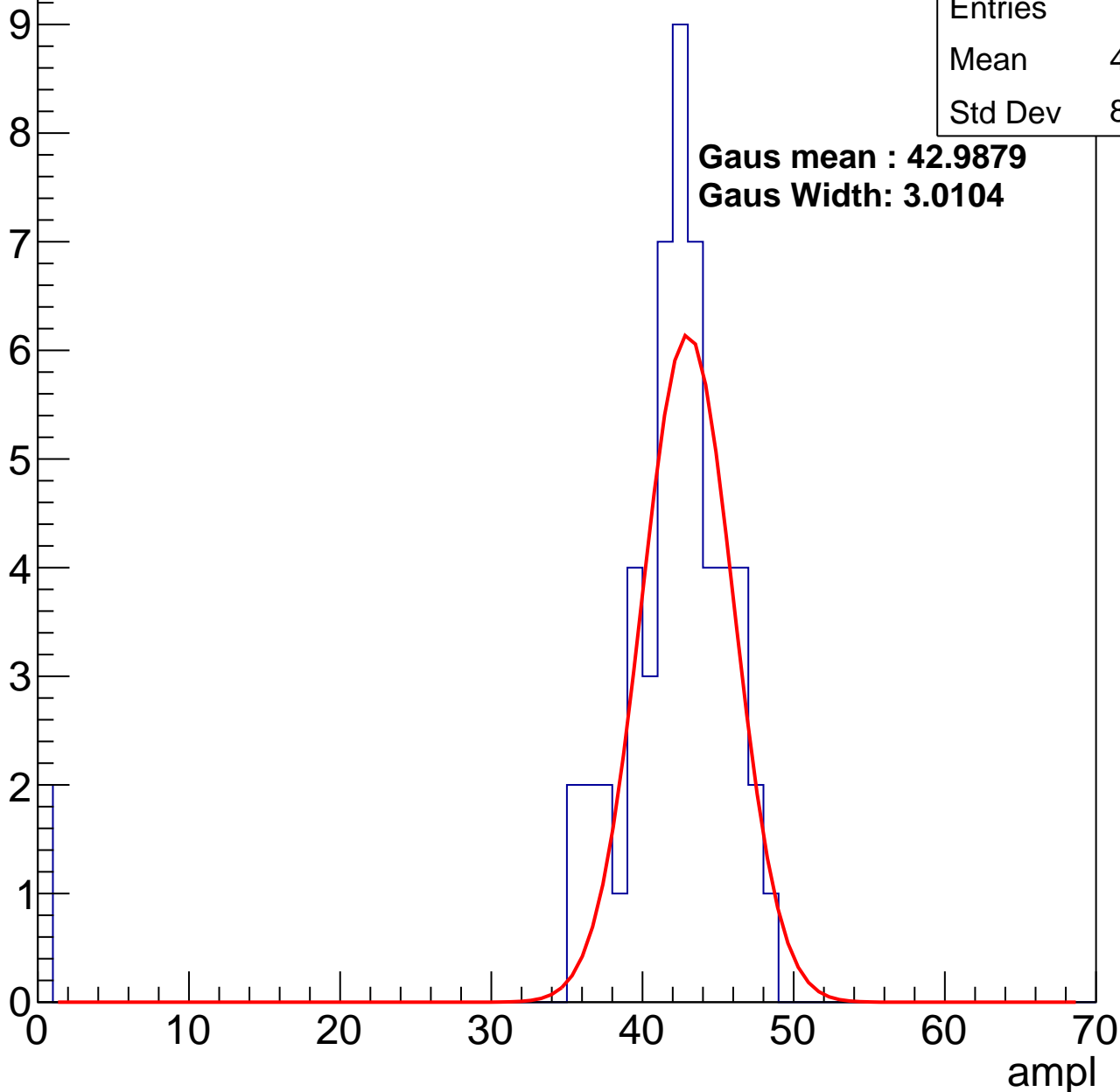
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	40.33
Std Dev	8.474

Gaus mean : 42.9879

Gaus Width: 3.0104

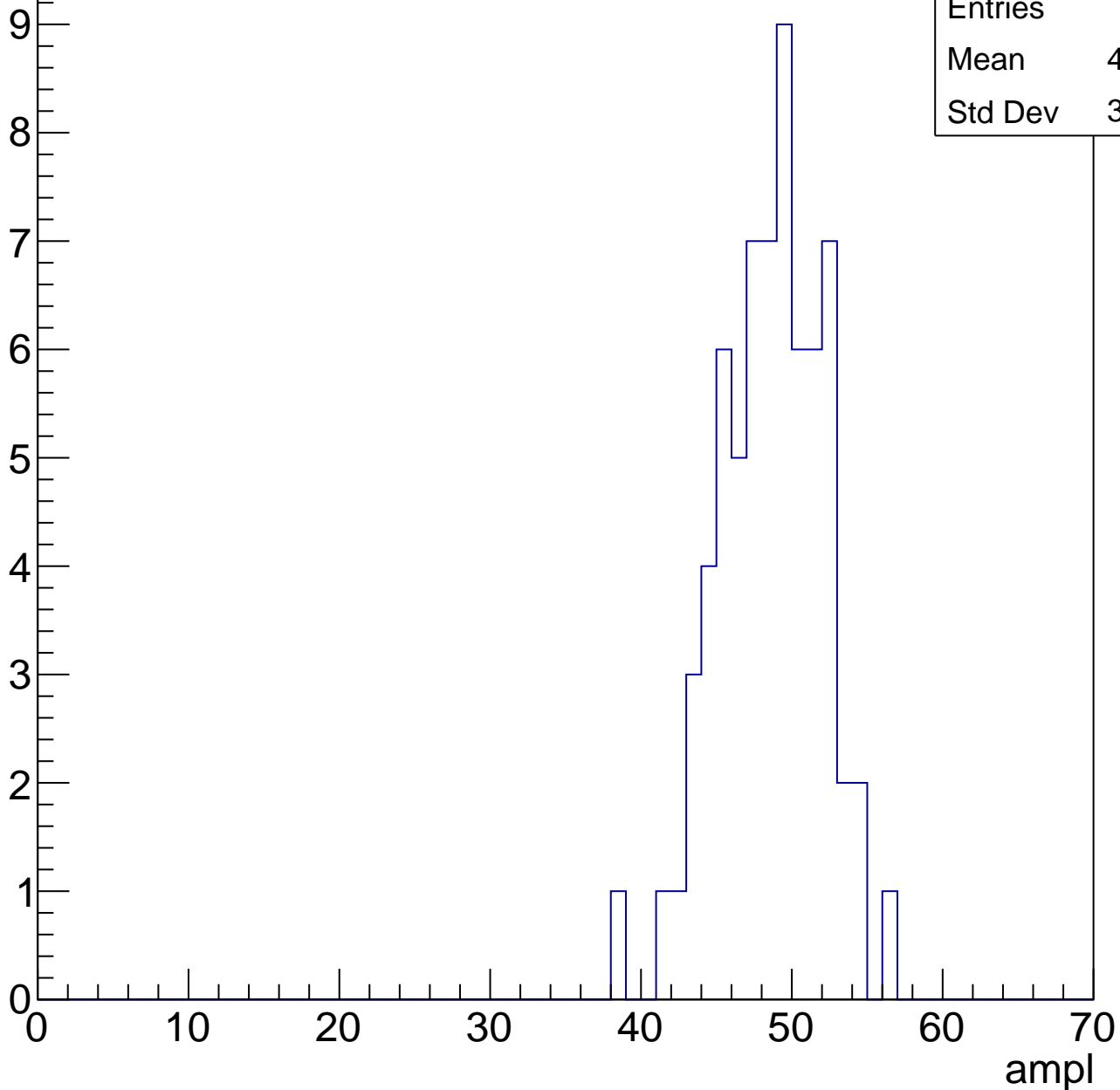


B1L103S, U21-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	48.12
Std Dev	3.406

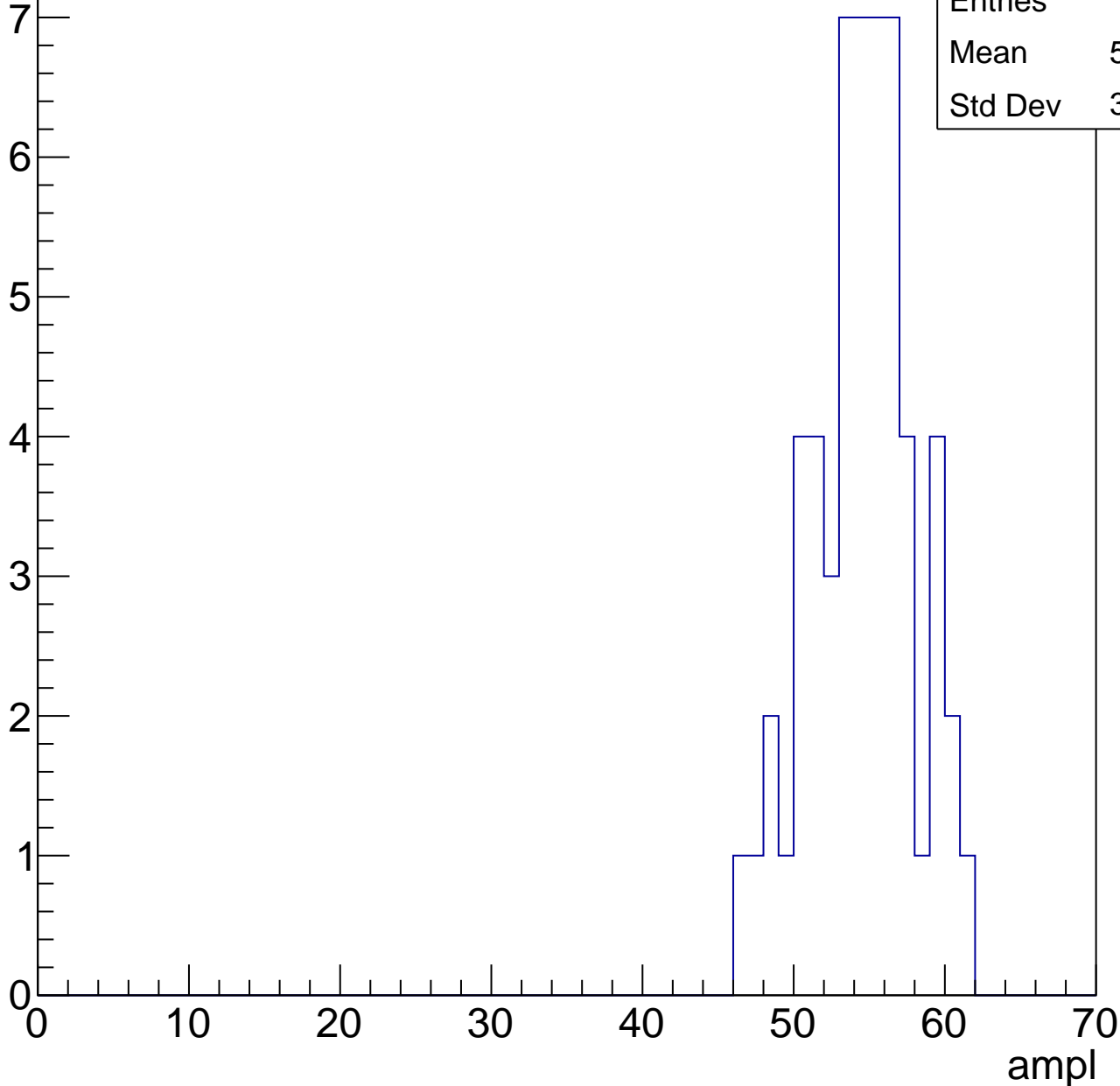


B1L103S, U21-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

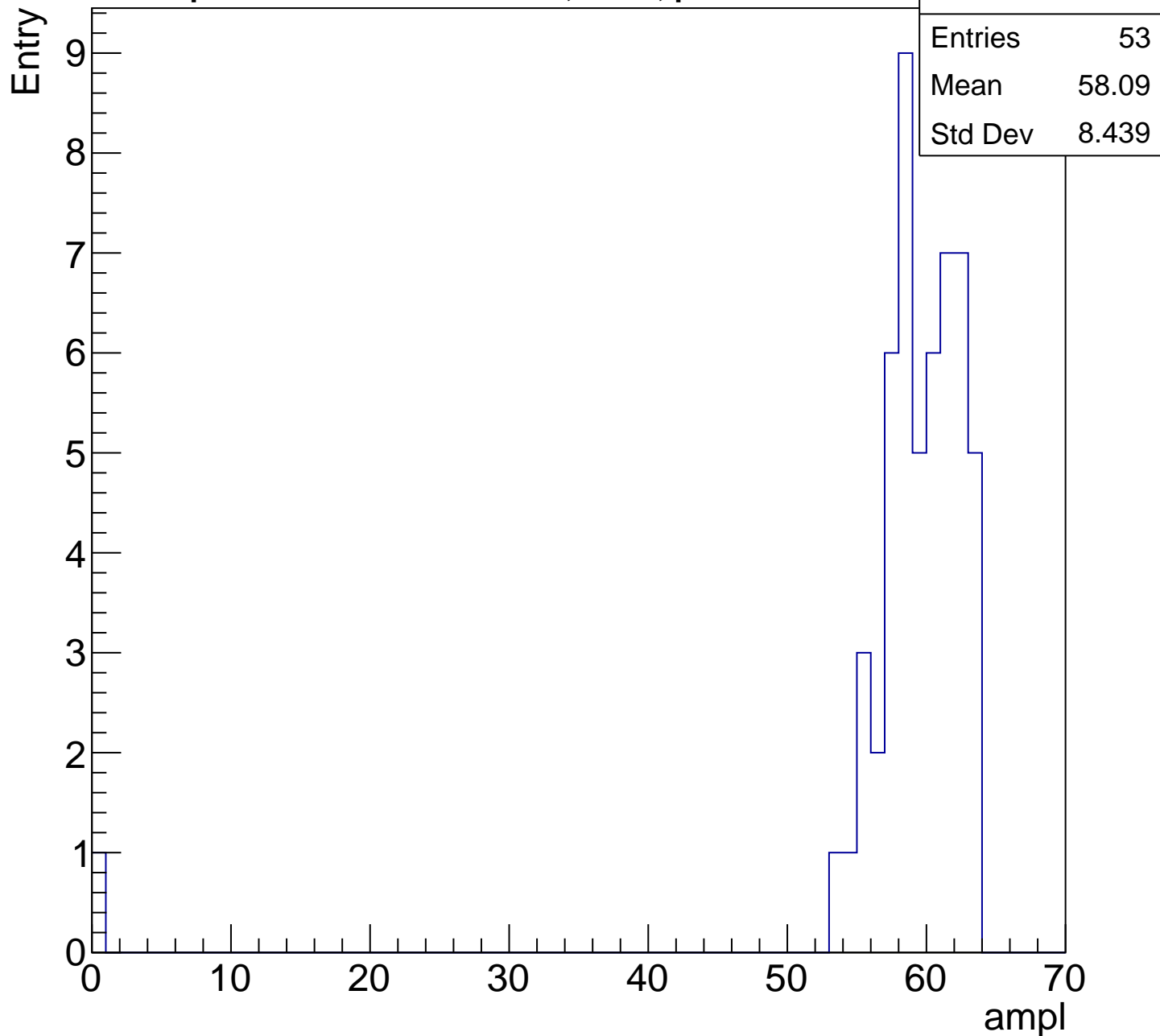
Entry

Entries	56
Mean	54.05
Std Dev	3.372



B1L103S, U21-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

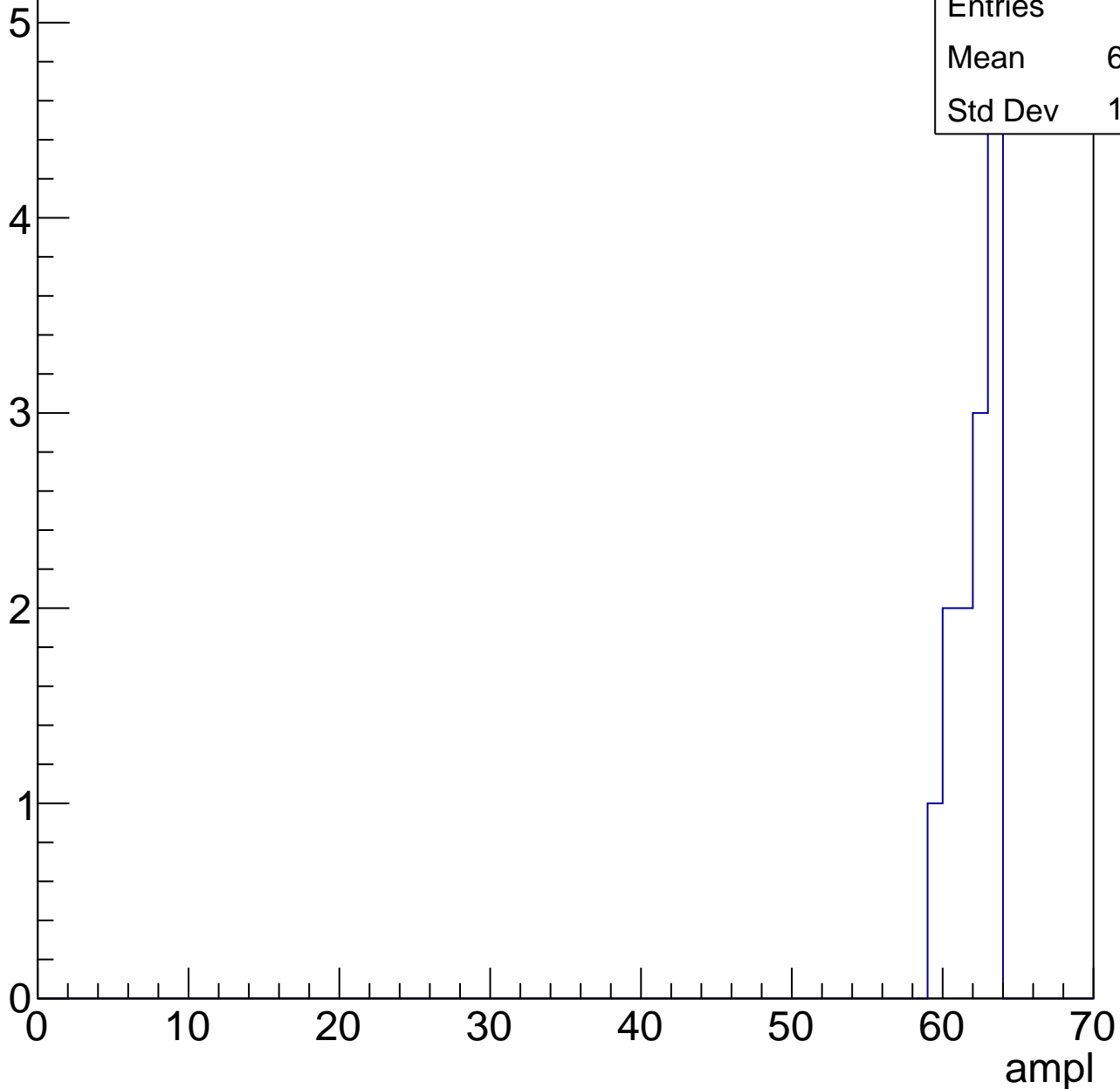


B1L103S, U21-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.69
Std Dev	1.323



B1L103S, U21-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch23, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	27.07
Std Dev	7.746

Gaus mean : 29.4667

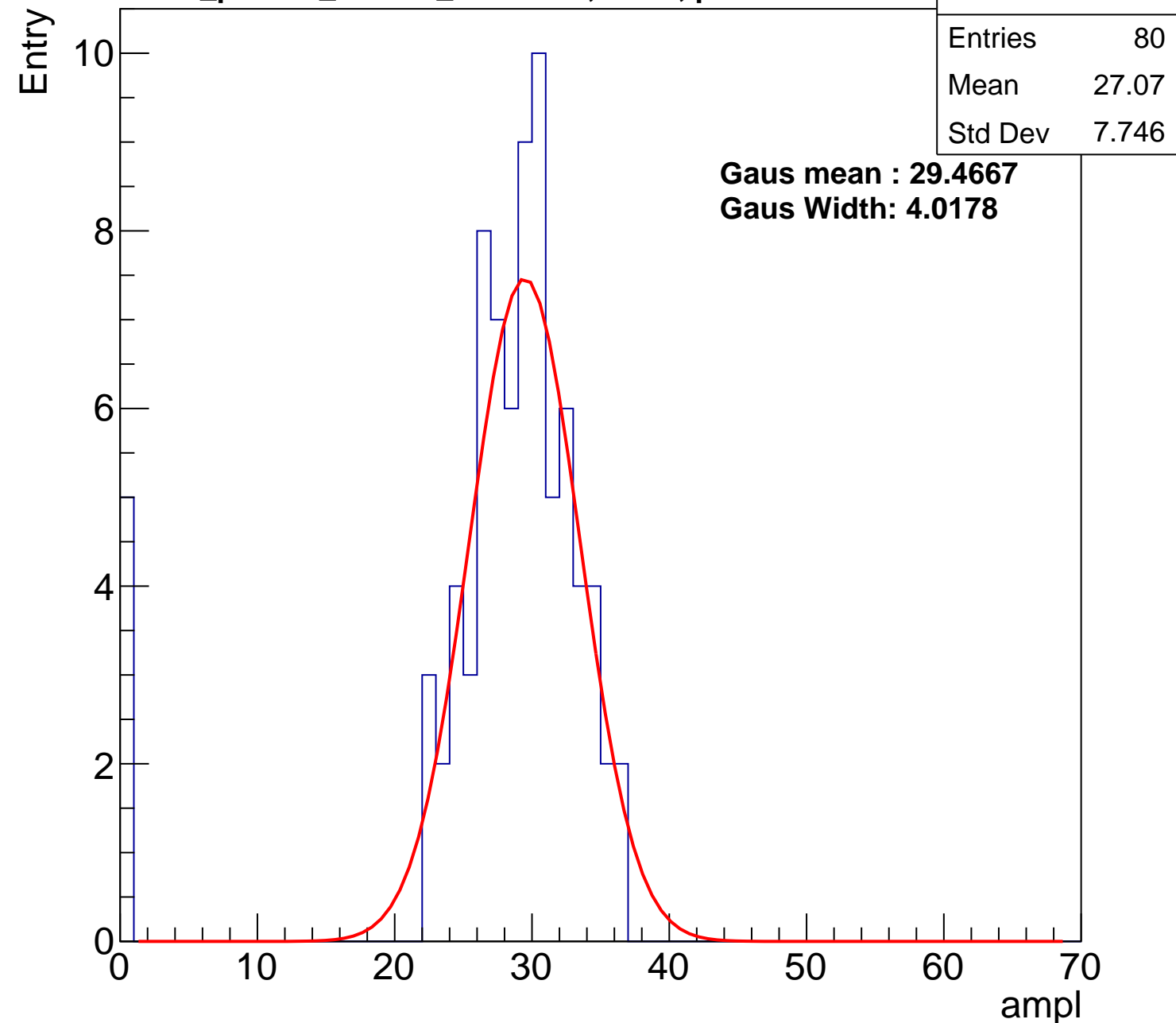
Gaus Width: 4.0178

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch23, adc1

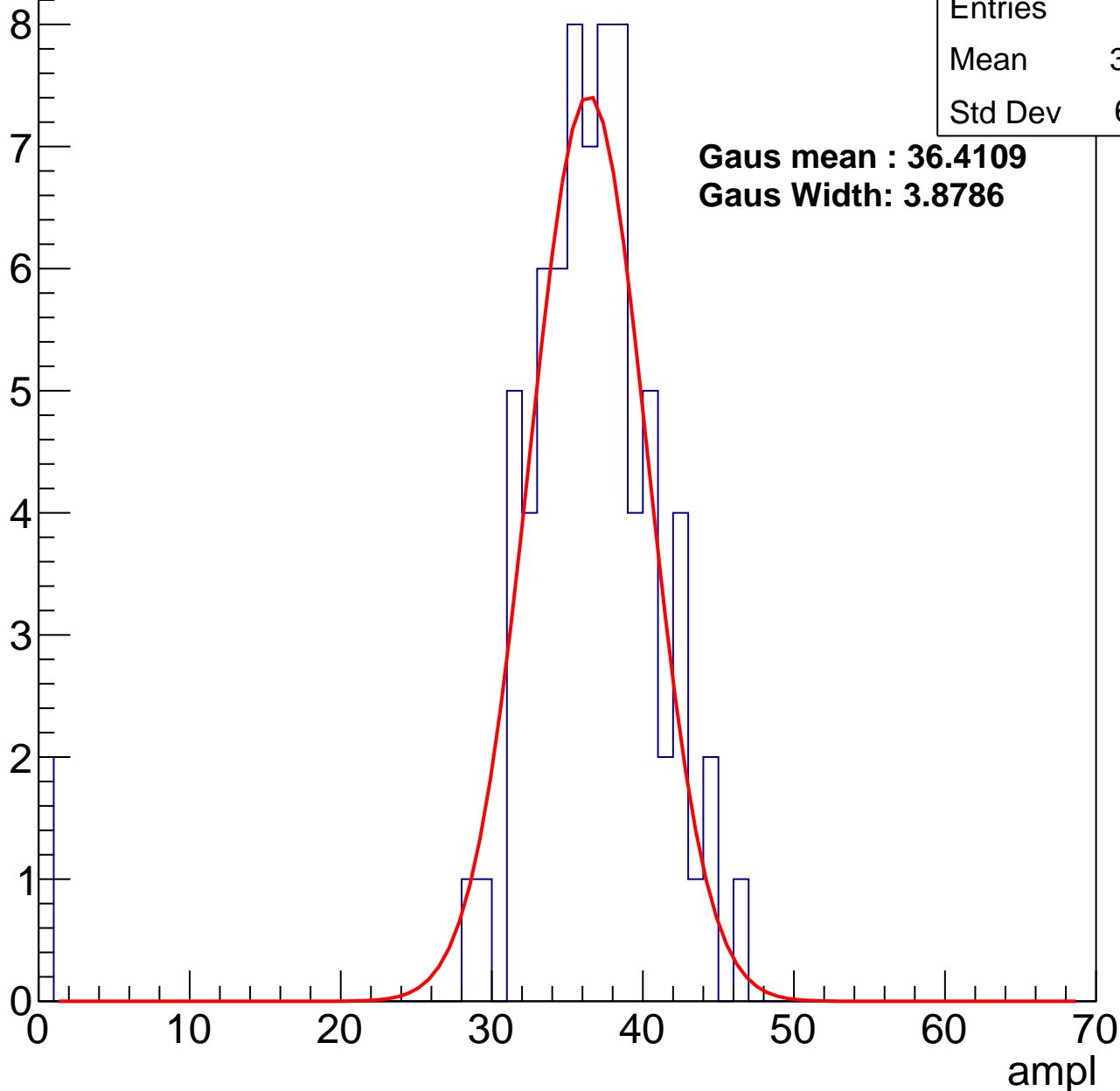
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	35.43
Std Dev	6.911

Gaus mean : 36.4109

Gaus Width: 3.8786



B1L103S, U21-ch23, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	37.9
Std Dev	14.87

Gaus mean : 43.8023

Gaus Width: 3.7577

Entry

10

8

6

4

2

0

0

10

20

30

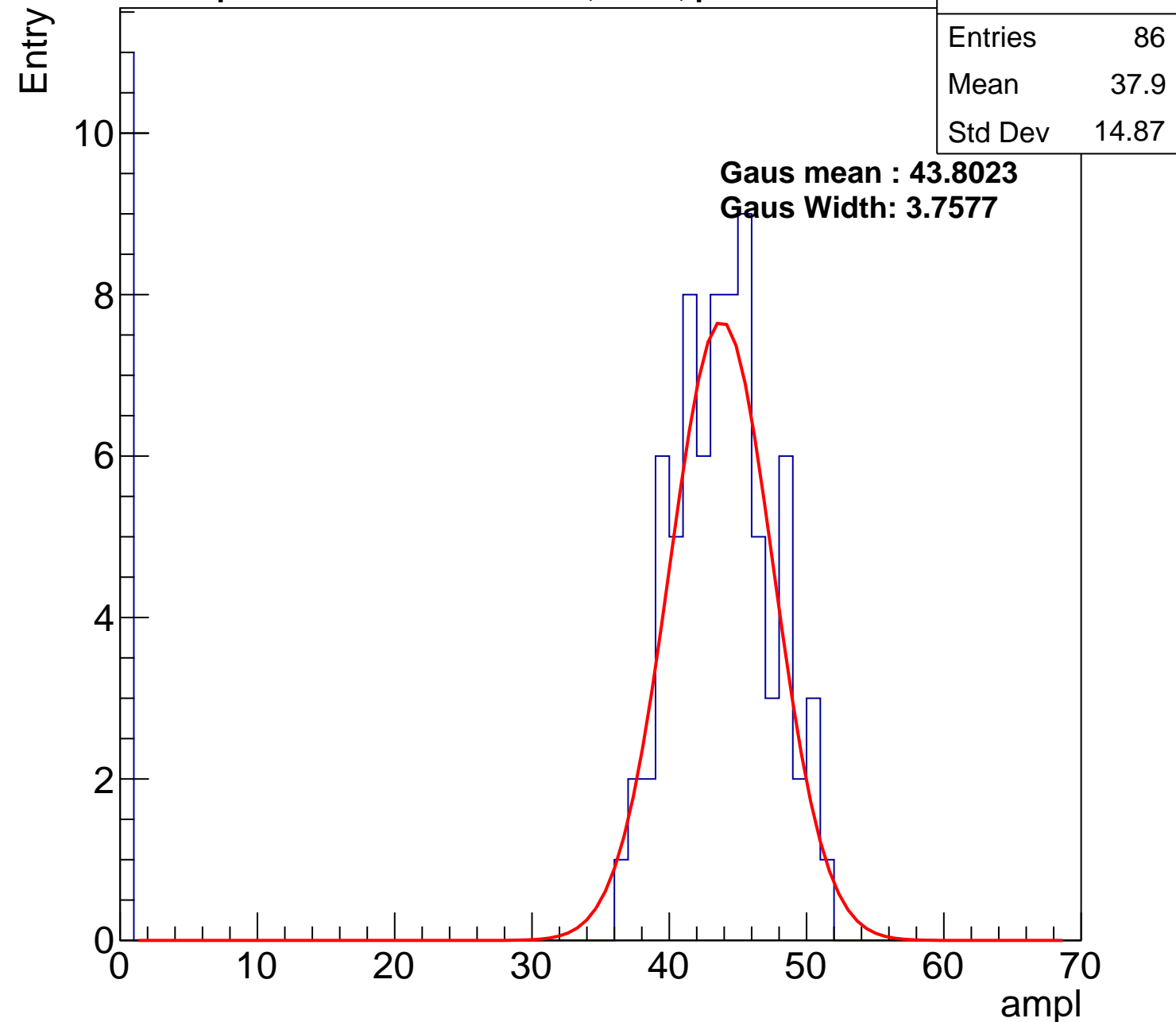
40

50

60

70

ampl

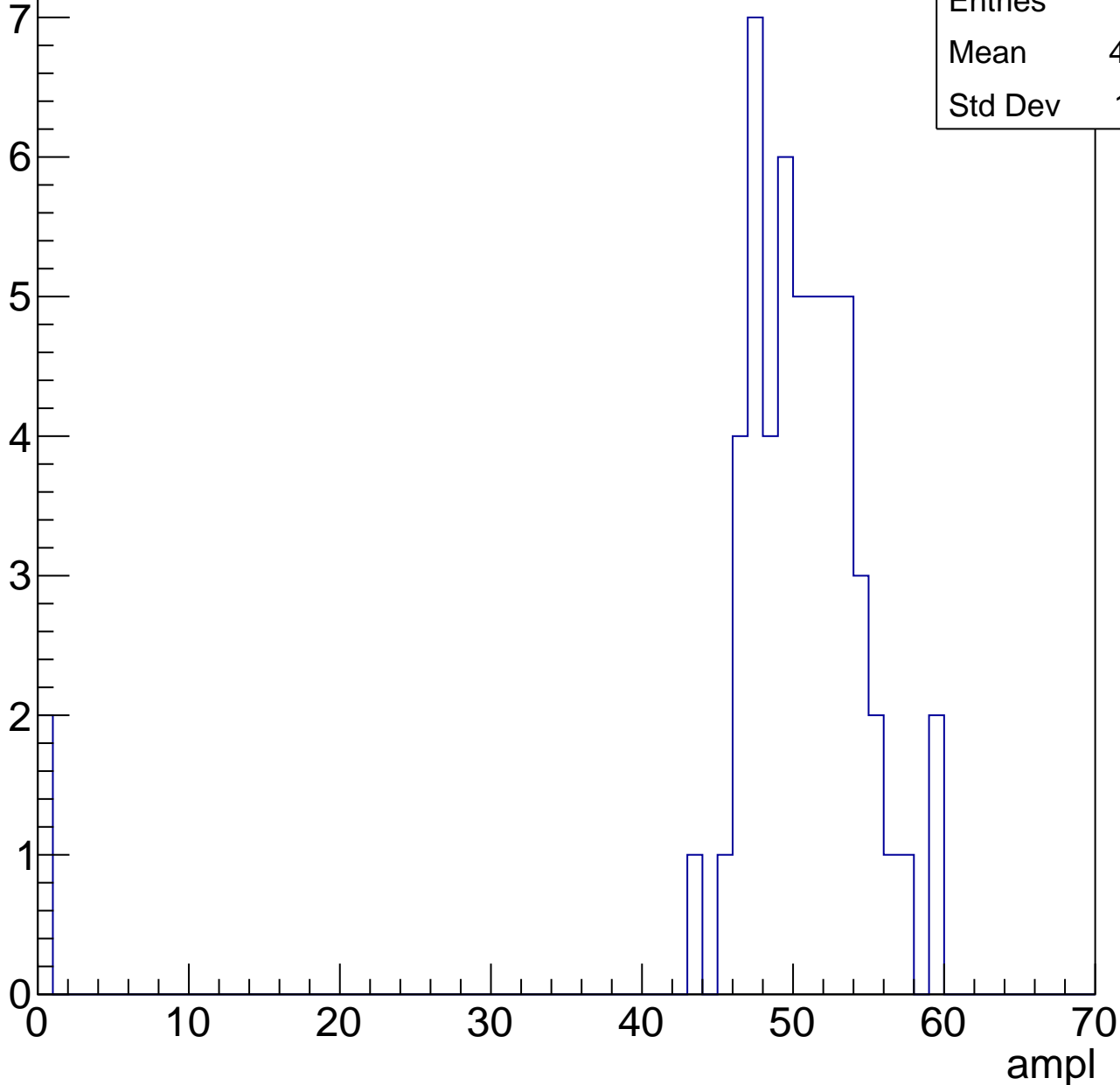


B1L103S, U21-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	48.52
Std Dev	10.11

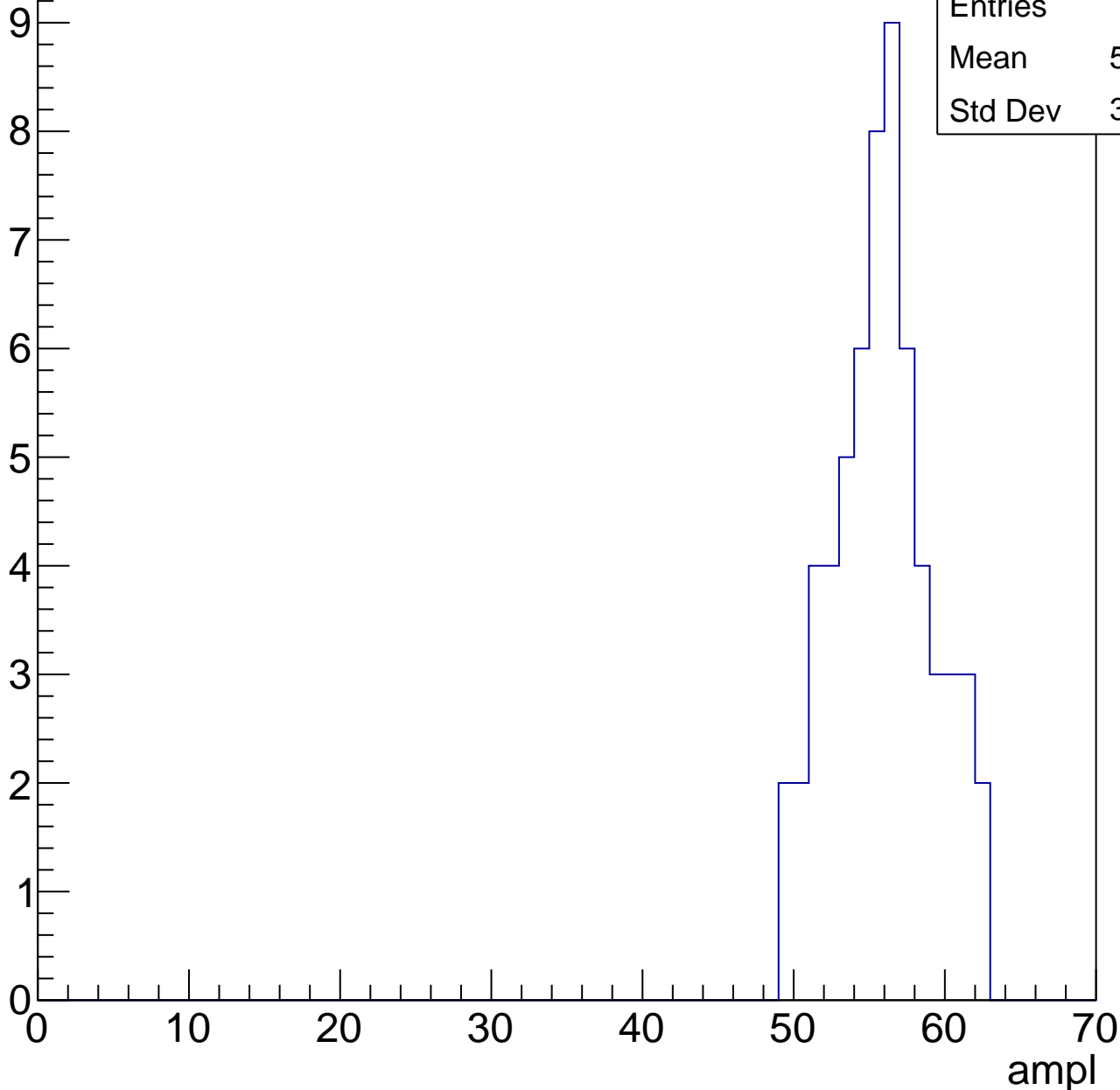


B1L103S, U21-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55.43
Std Dev	3.226

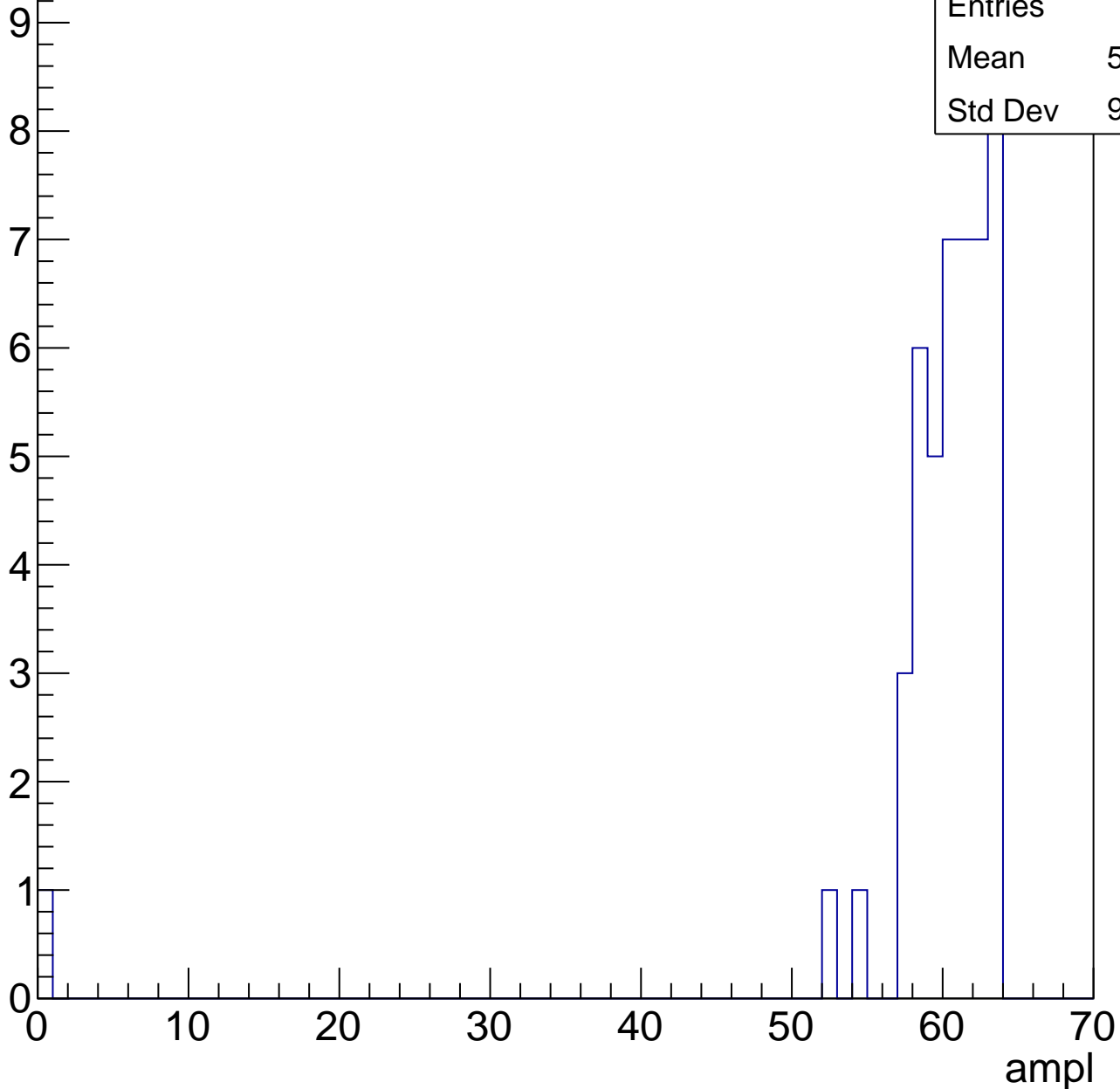


B1L103S, U21-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.89
Std Dev	9.009



B1L103S, U21-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

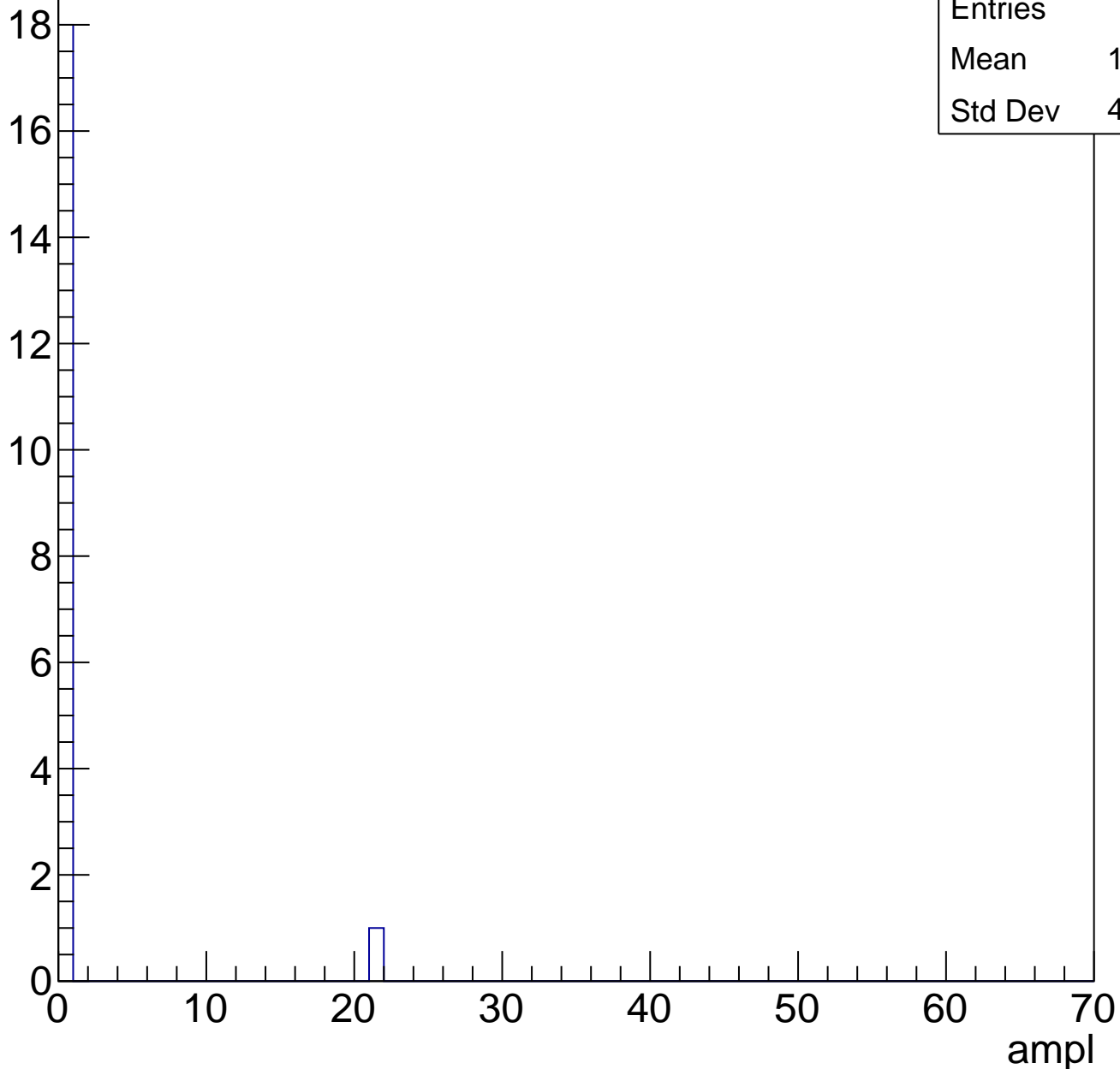


B1L103S, U21-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U21-ch24, adc0

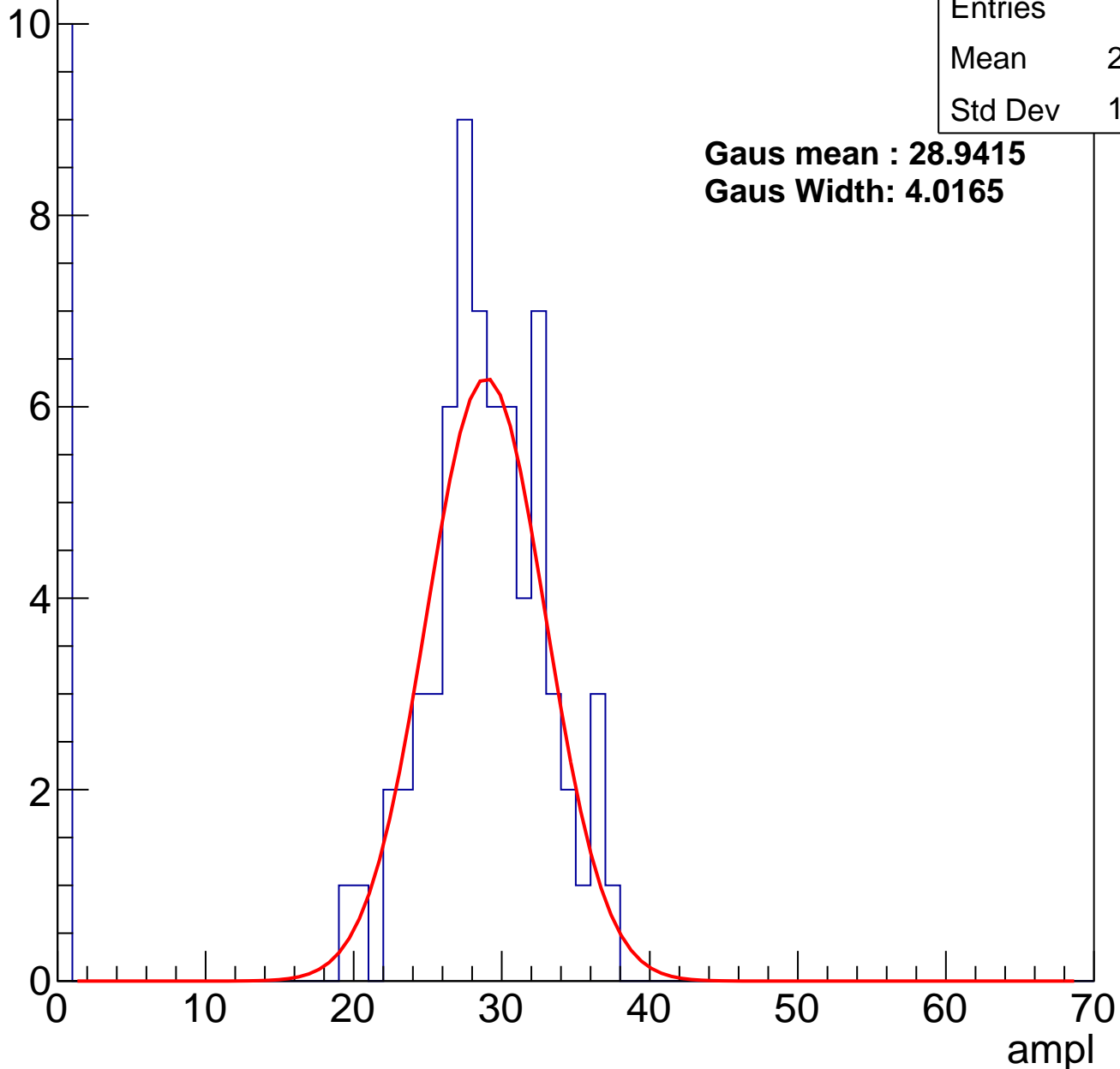
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	24.94
Std Dev	10.28

Gaus mean : 28.9415

Gaus Width: 4.0165

Entry



B1L103S, U21-ch24, adc1

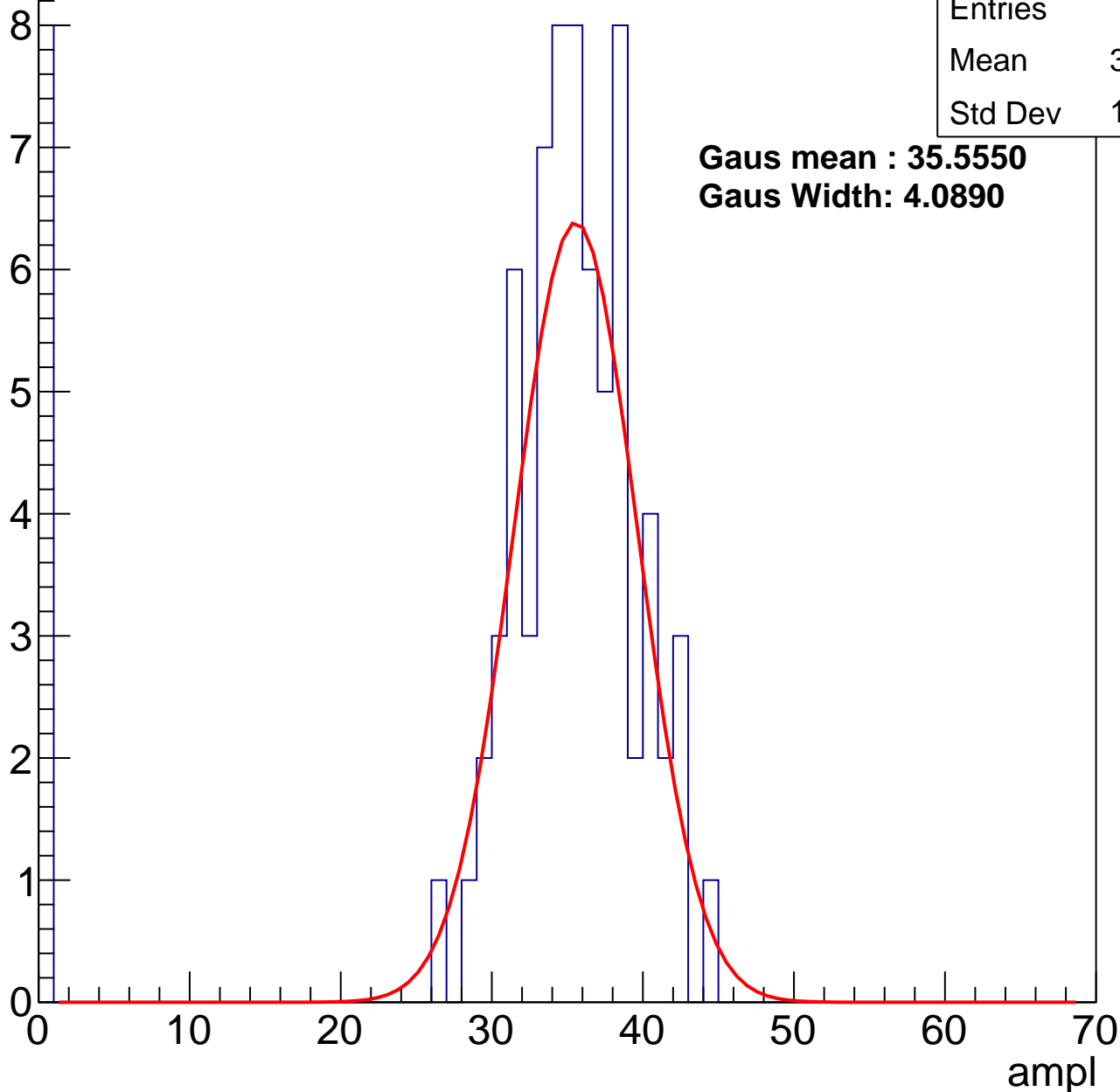
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	31.56
Std Dev	11.23

Gaus mean : 35.5550

Gaus Width: 4.0890



B1L103S, U21-ch24, adc2

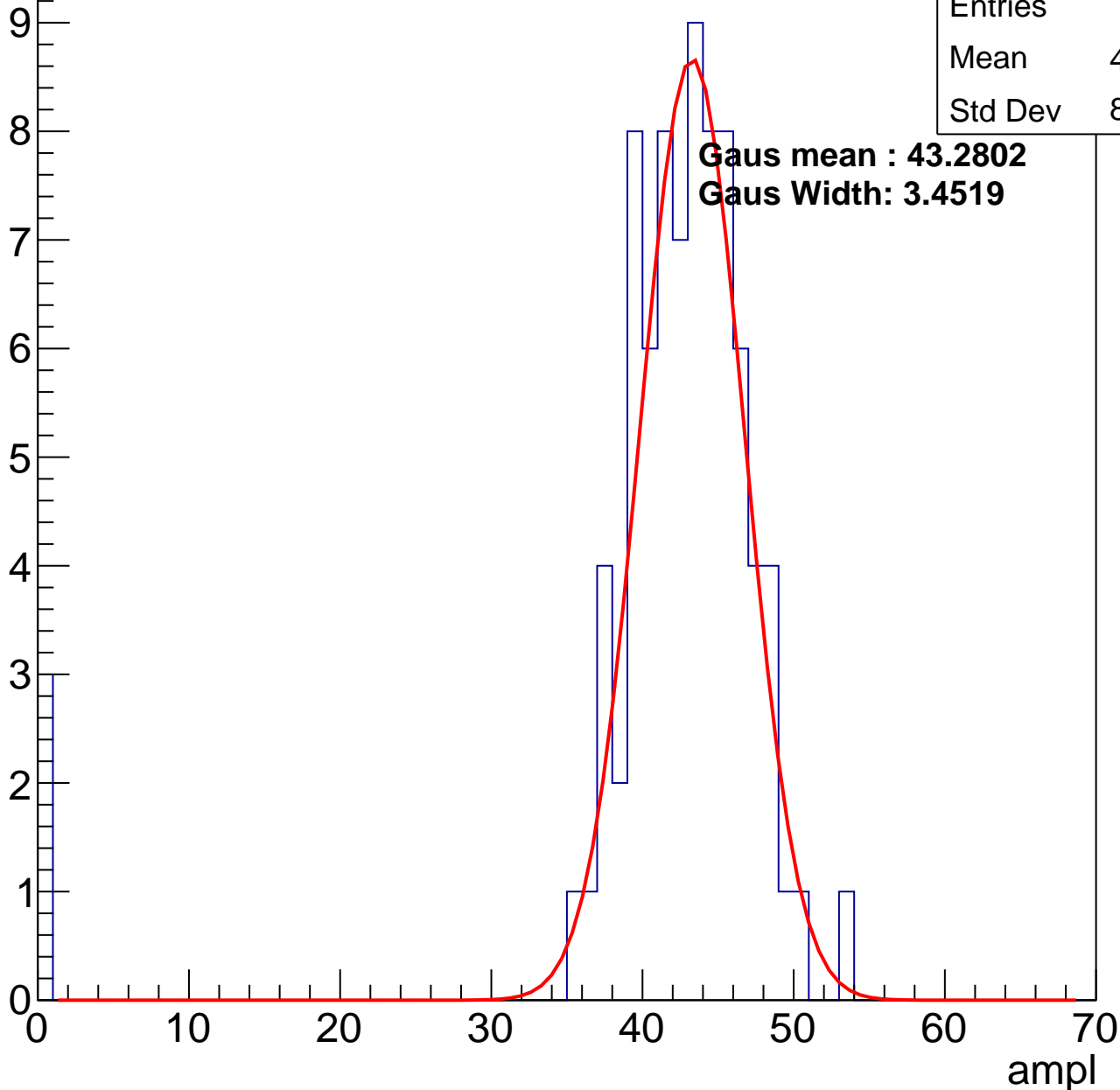
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	41.17
Std Dev	8.726

Gaus mean : 43.2802

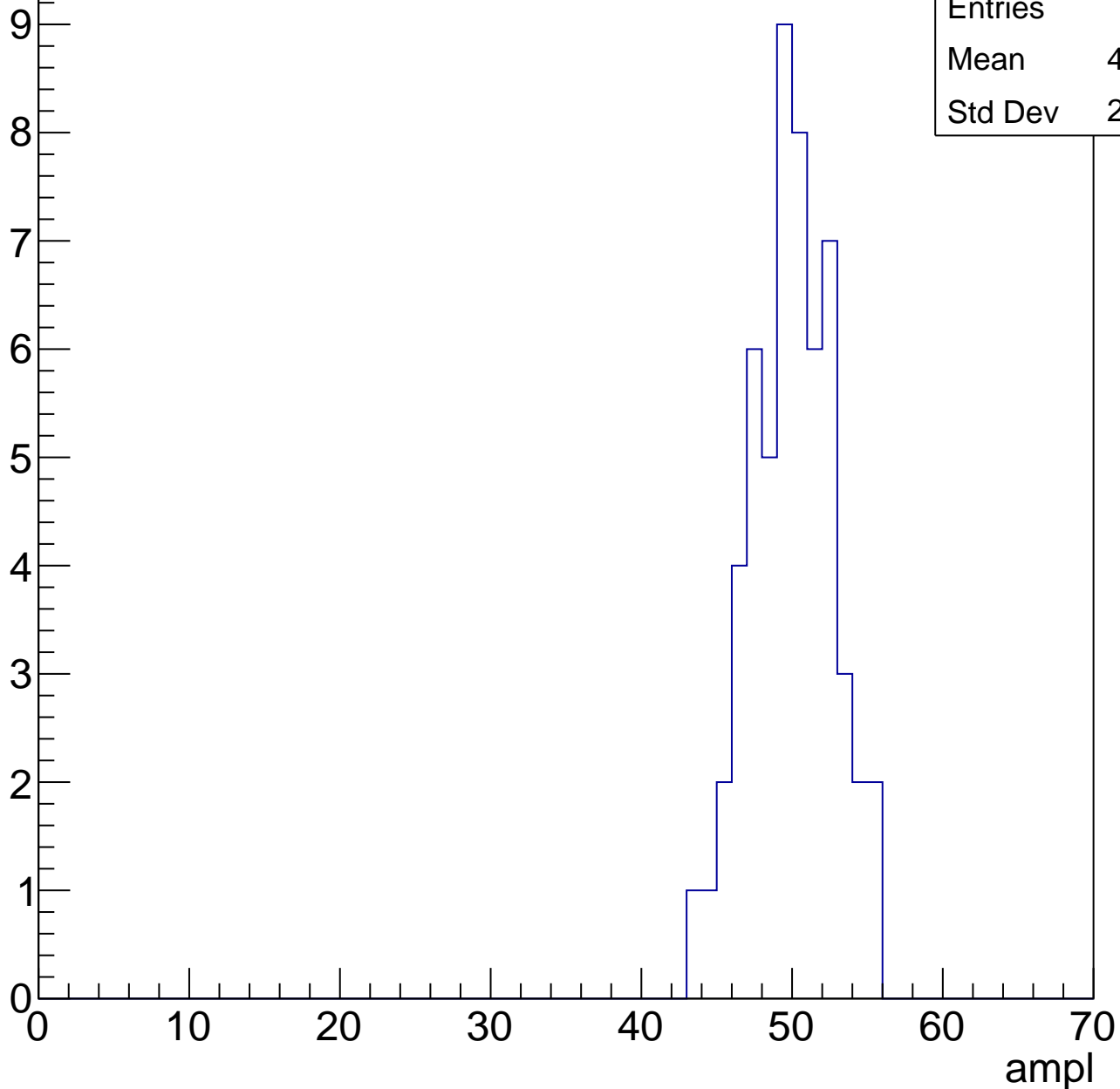
Gaus Width: 3.4519



B1L103S, U21-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

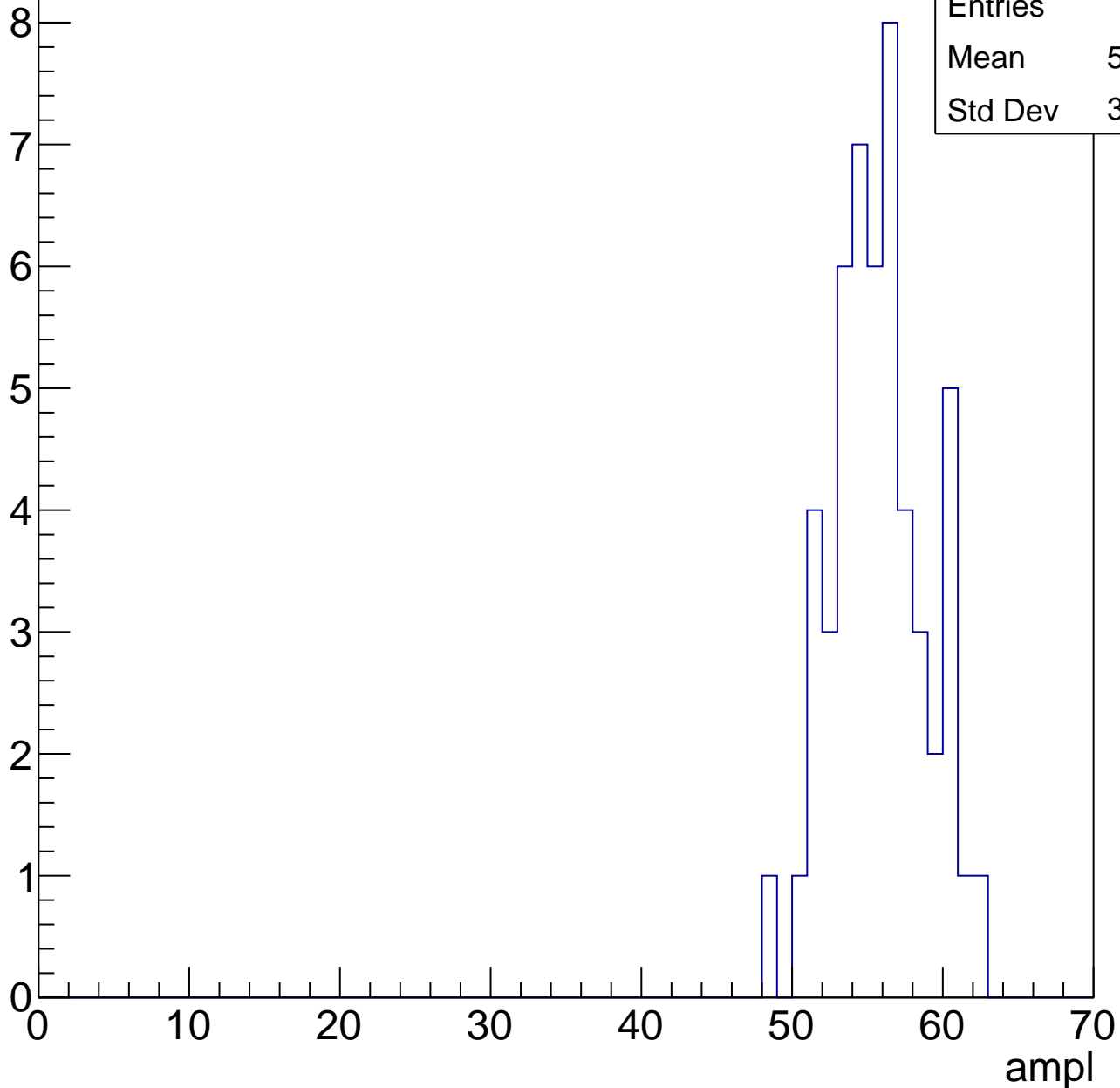


B1L103S, U21-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	55.29
Std Dev	3.053

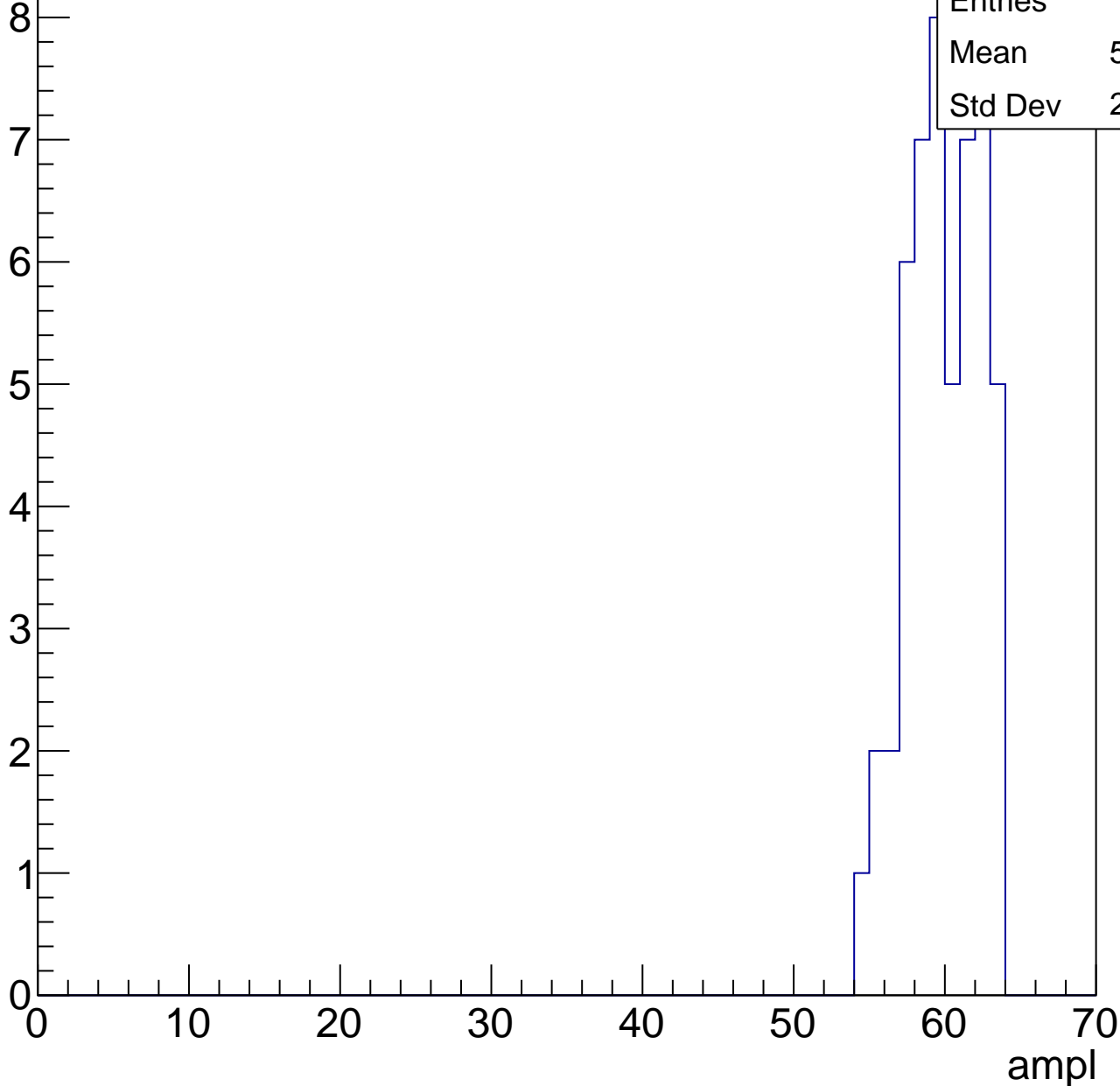


B1L103S, U21-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	59.49
Std Dev	2.338

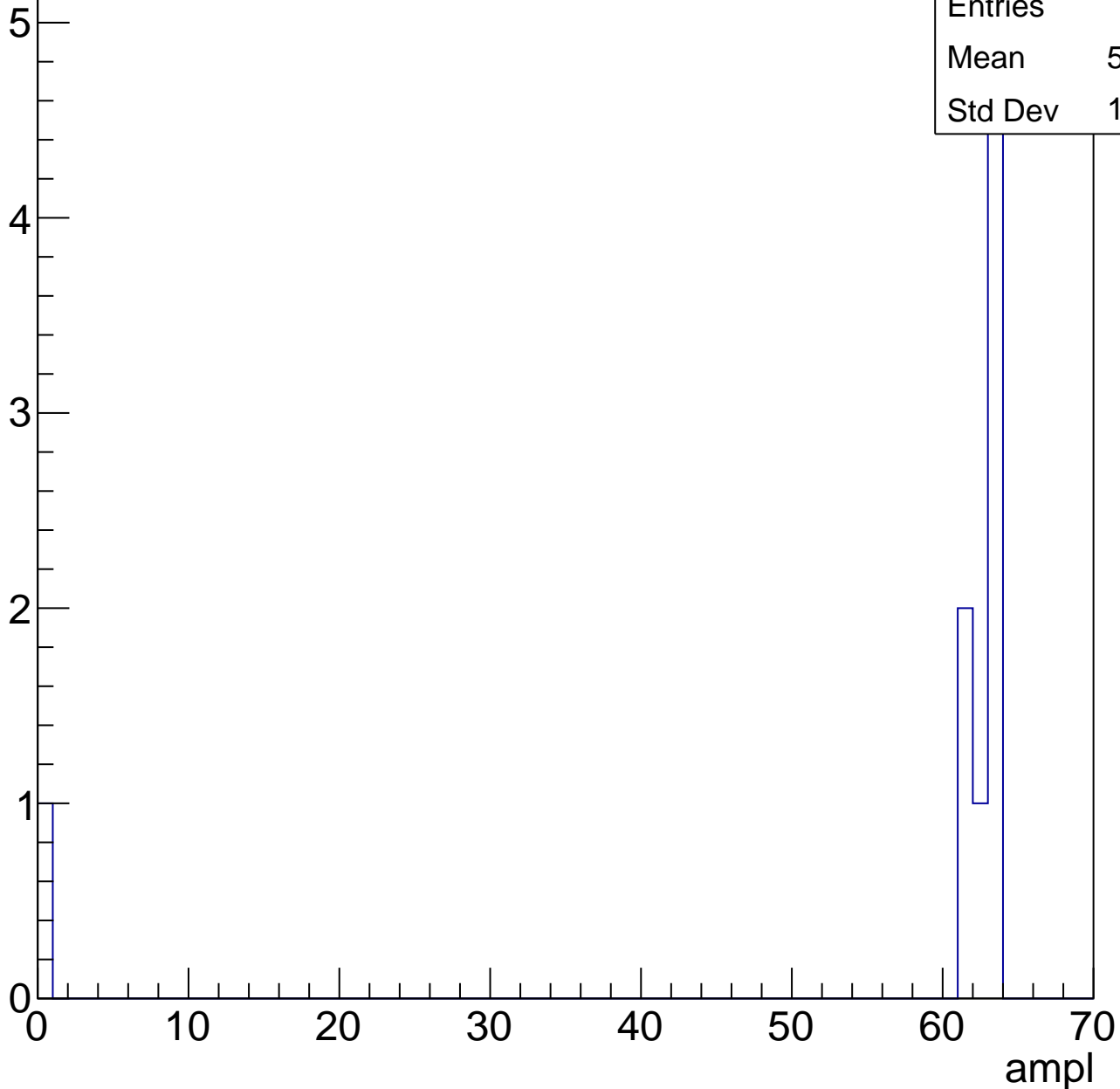


B1L103S, U21-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	55.44
Std Dev	19.62



B1L103S, U21-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U21-ch25, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	20.92
Std Dev	14.1

Gaus mean : 30.4425

Gaus Width: 3.9036

Entry

25

20

15

10

5

0

0

10

20

30

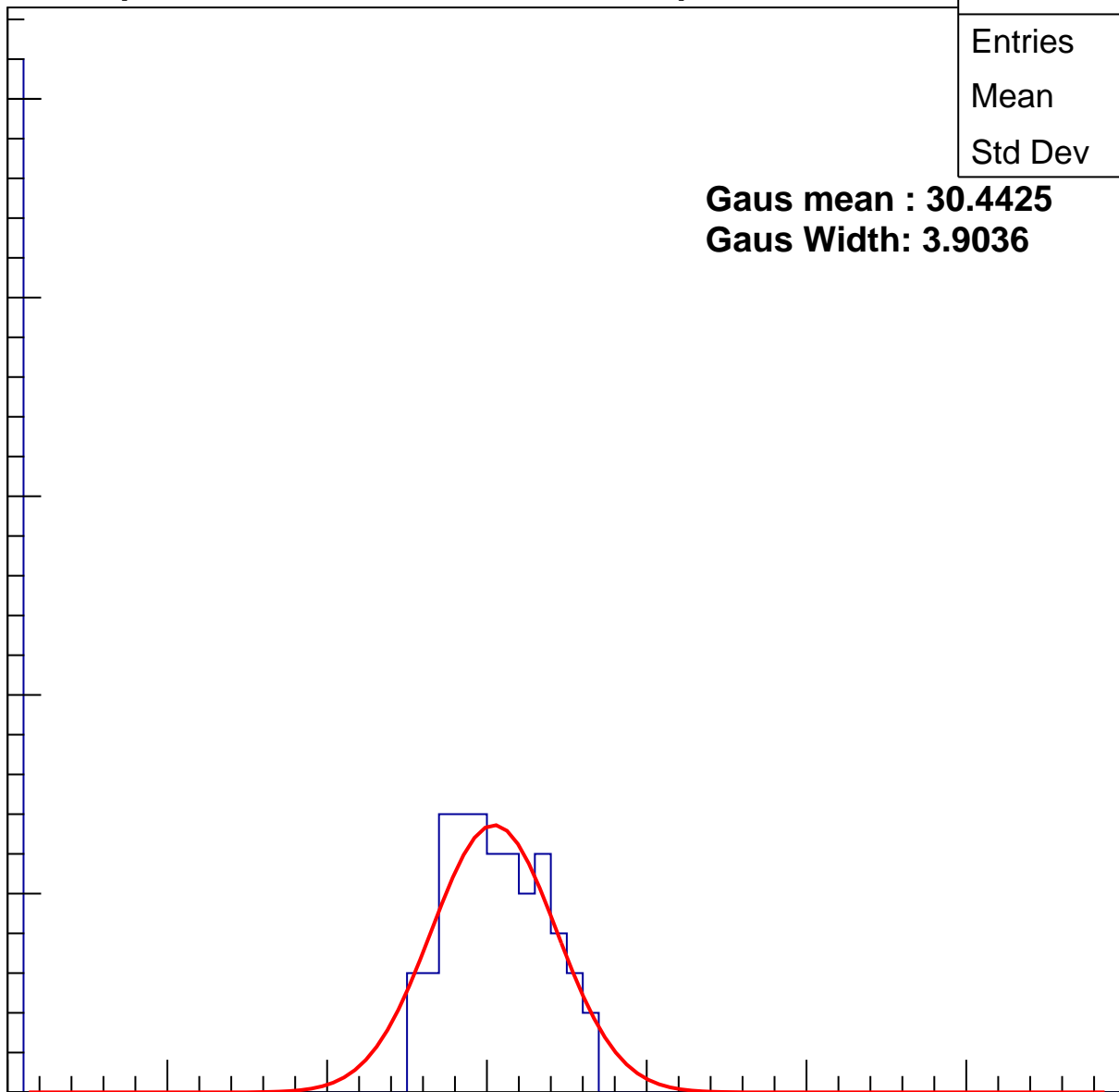
40

50

60

70

ampl



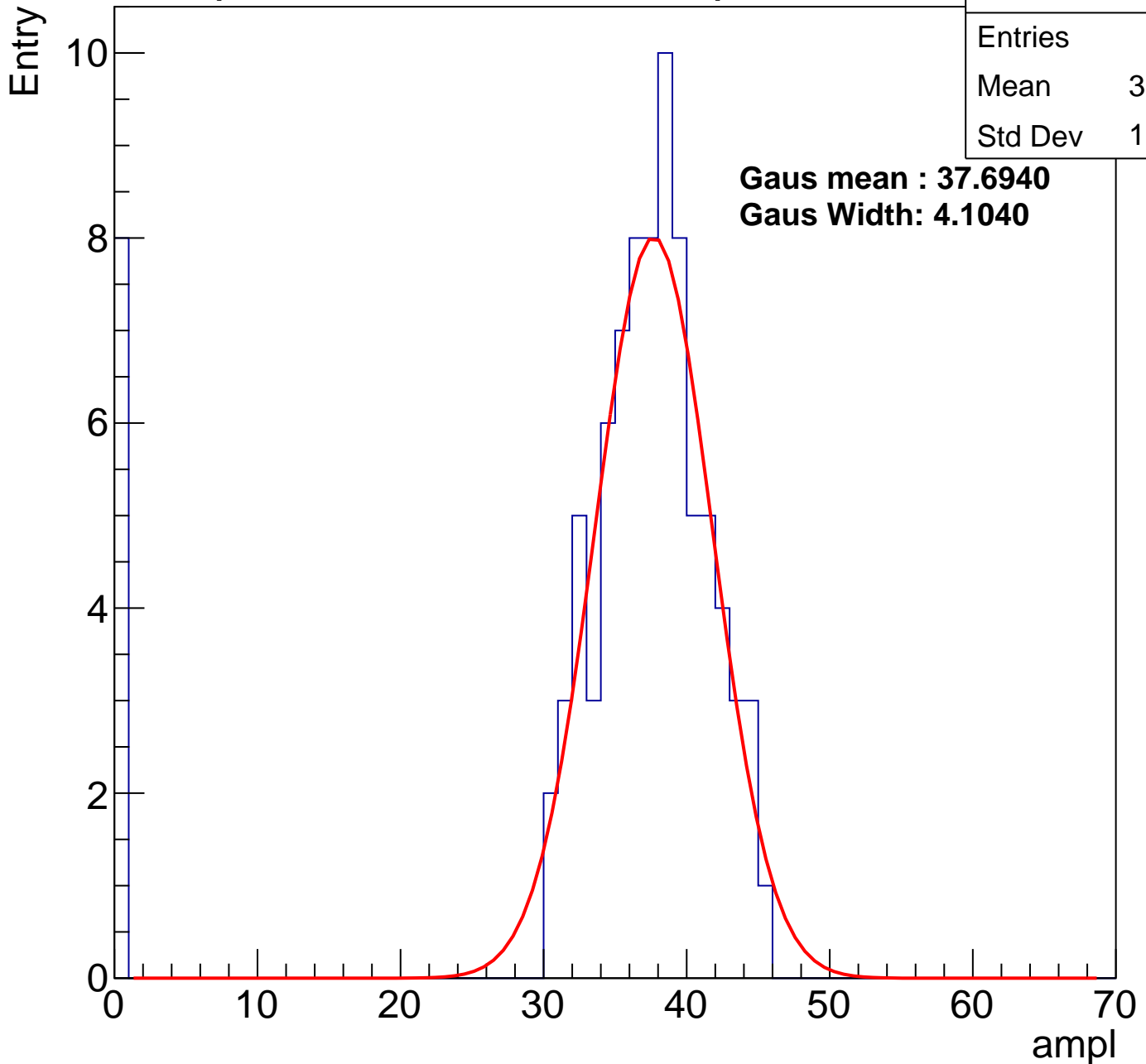
B1L103S, U21-ch25, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	33.89
Std Dev	11.19

Gaus mean : 37.6940

Gaus Width: 4.1040



B1L103S, U21-ch25, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	38.05
Std Dev	15.86

Gaus mean : 44.4836

Gaus Width: 3.3814

Entry

10

8

6

4

2

0

0

10

20

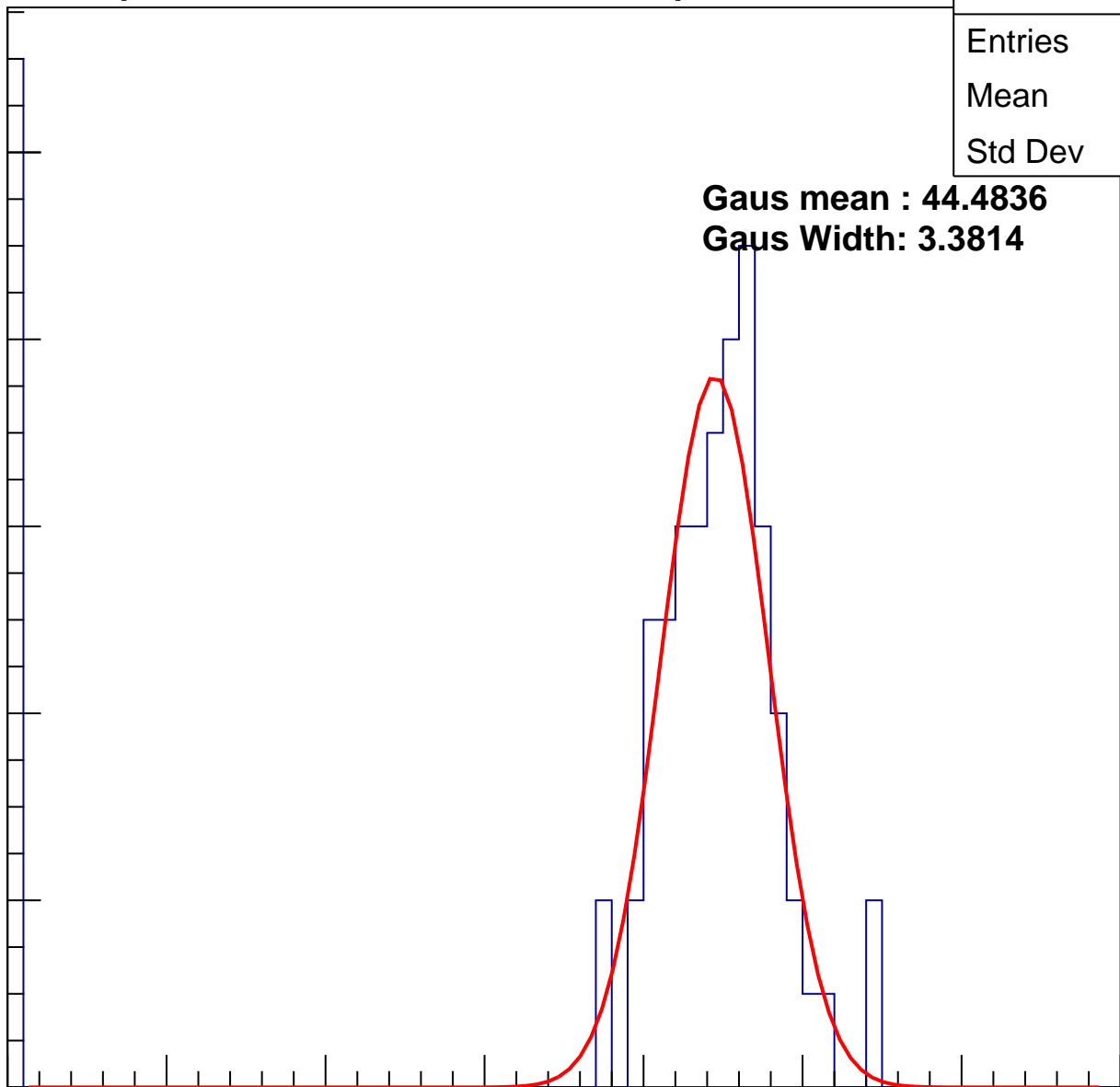
30

40

50

60

ampl

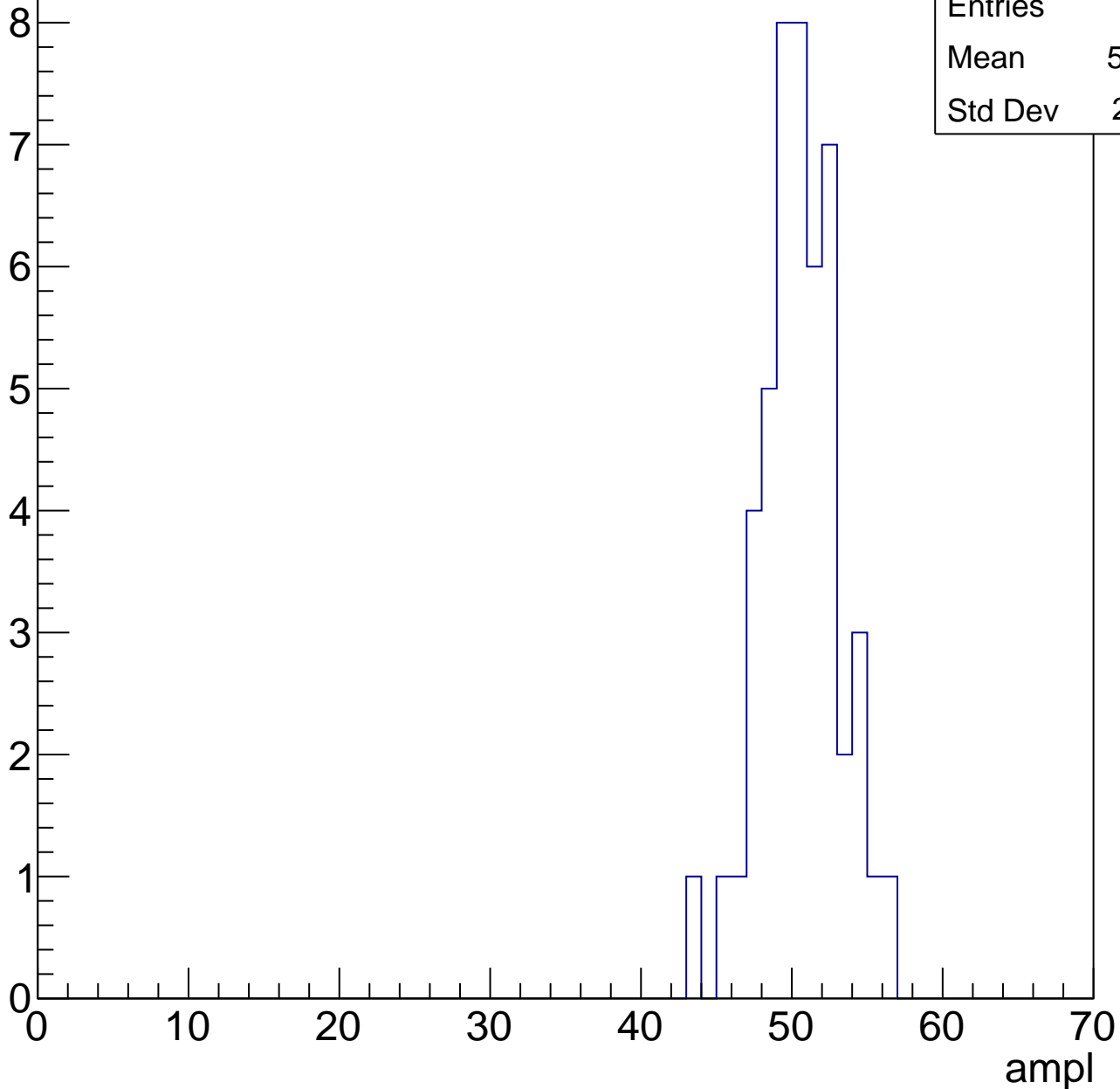


B1L103S, U21-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

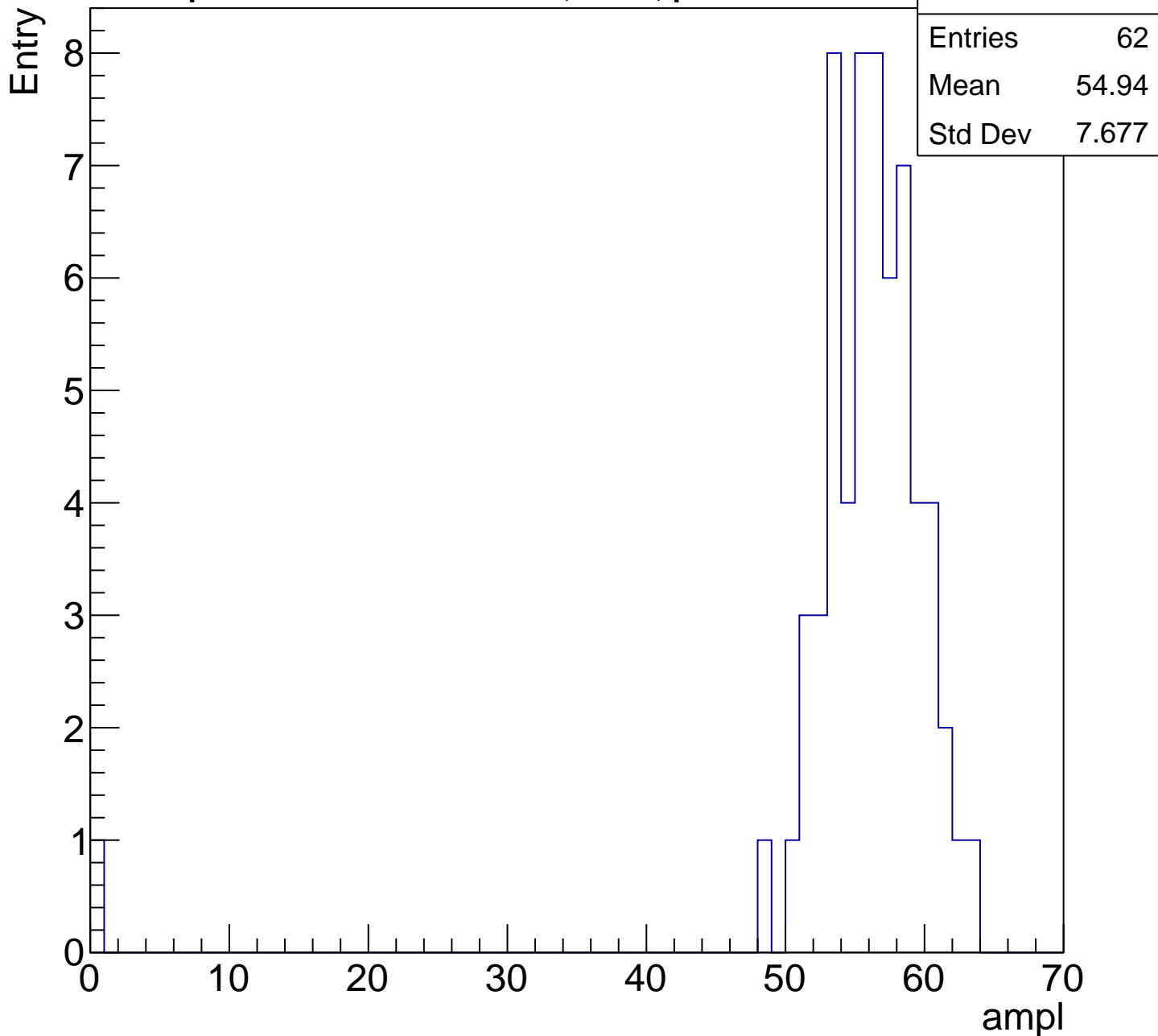
Entry

Entries	48
Mean	50.06
Std Dev	2.561



B1L103S, U21-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

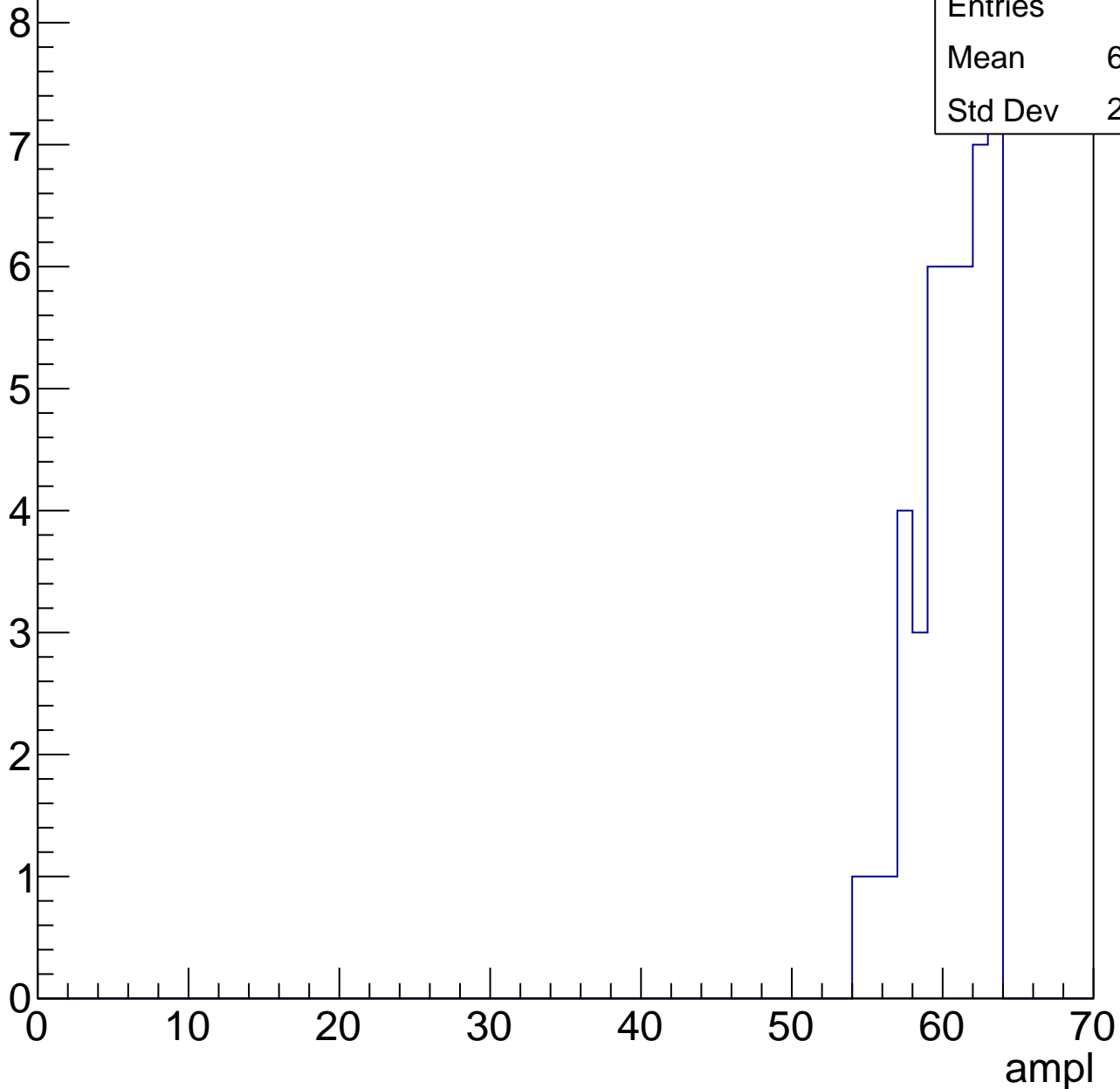


B1L103S, U21-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

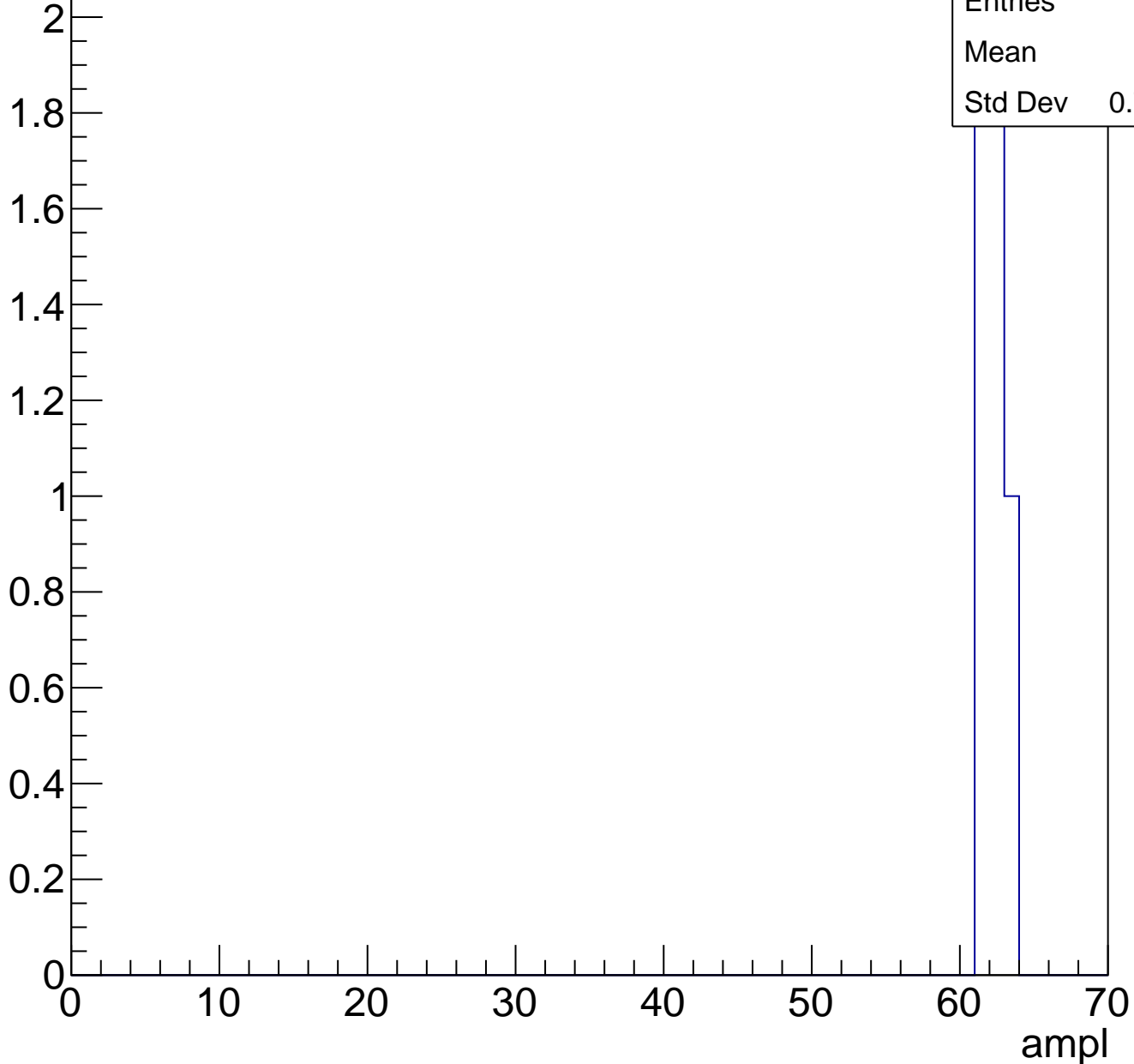
Entries	43
Mean	60.12
Std Dev	2.345



B1L103S, U21-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

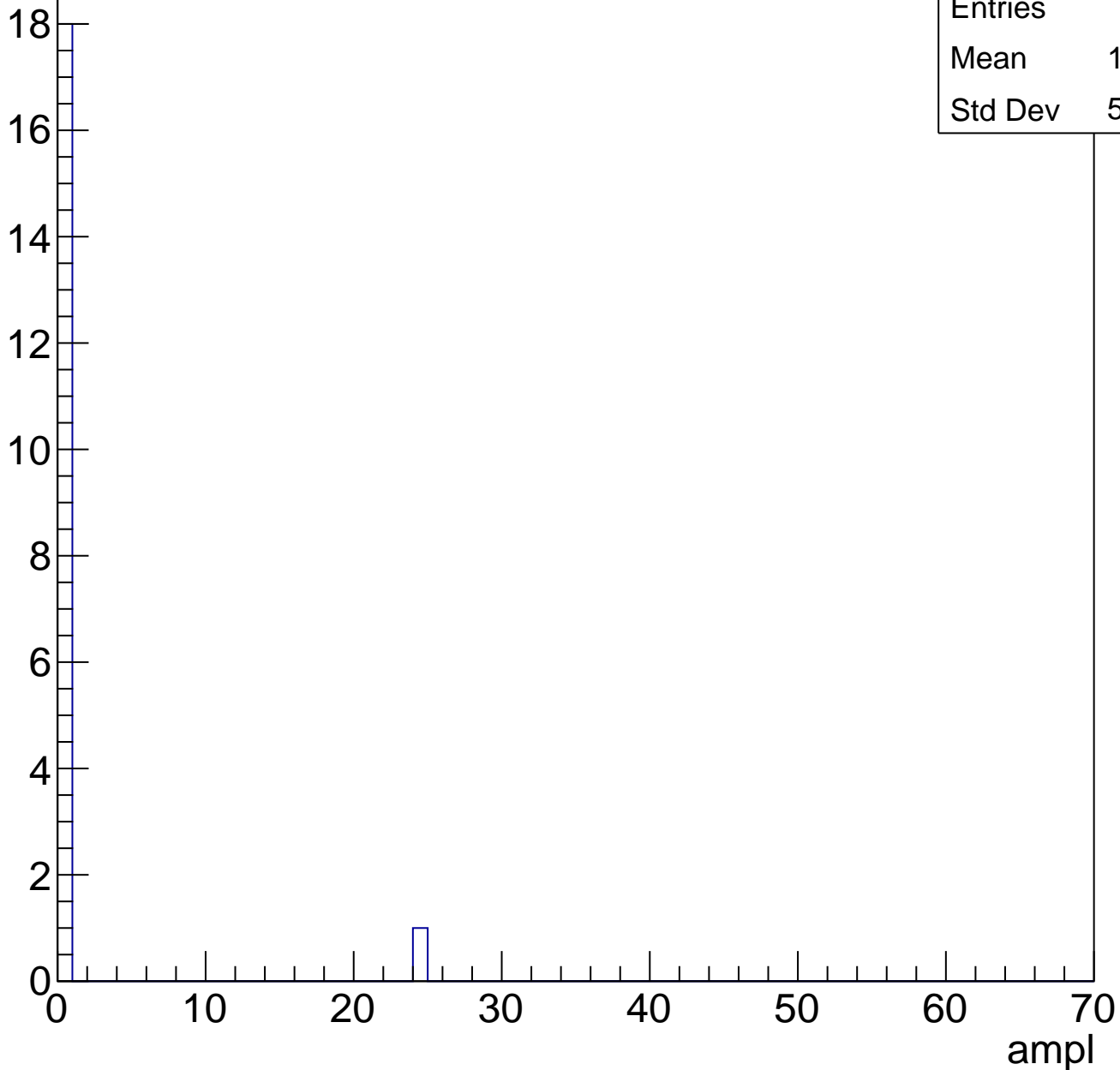


B1L103S, U21-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.263
Std Dev	5.359

Entry



B1L103S, U21-ch26, adc0

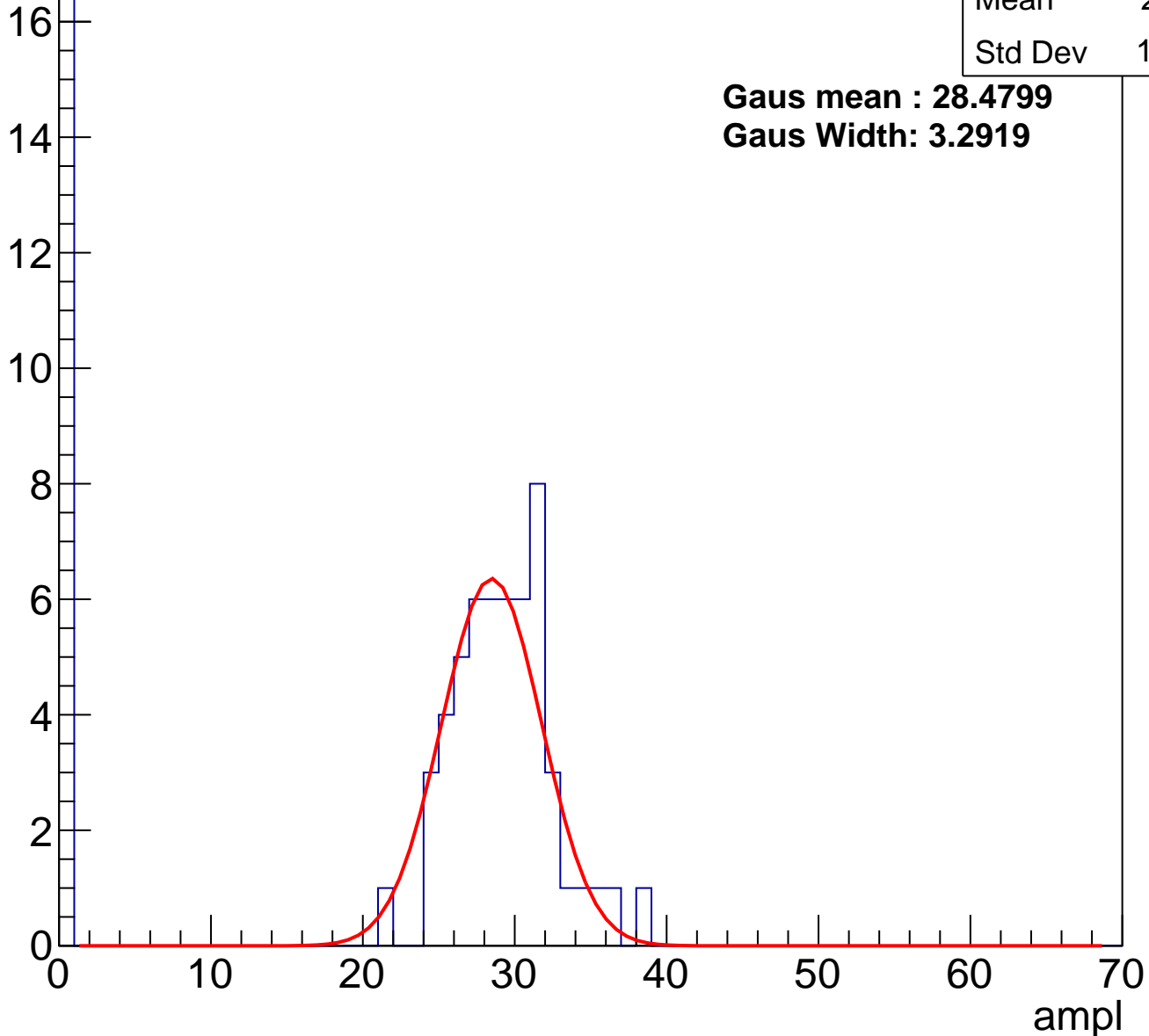
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	21.81
Std Dev	12.67

Gaus mean : 28.4799

Gaus Width: 3.2919

Entry



B1L103S, U21-ch26, adc1

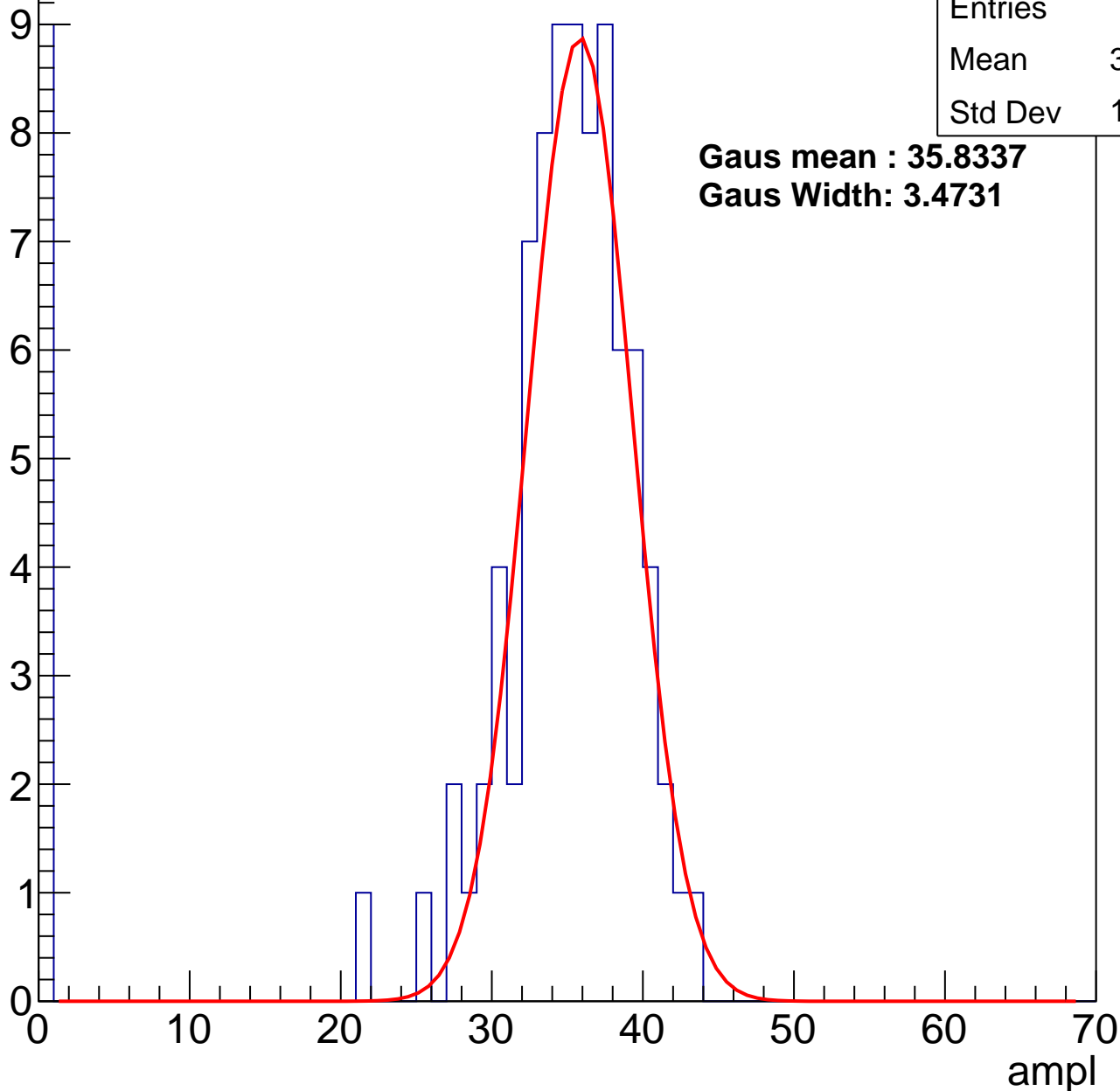
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	92
Mean	31.38
Std Dev	10.97

Gaus mean : 35.8337

Gaus Width: 3.4731



B1L103S, U21-ch26, adc2

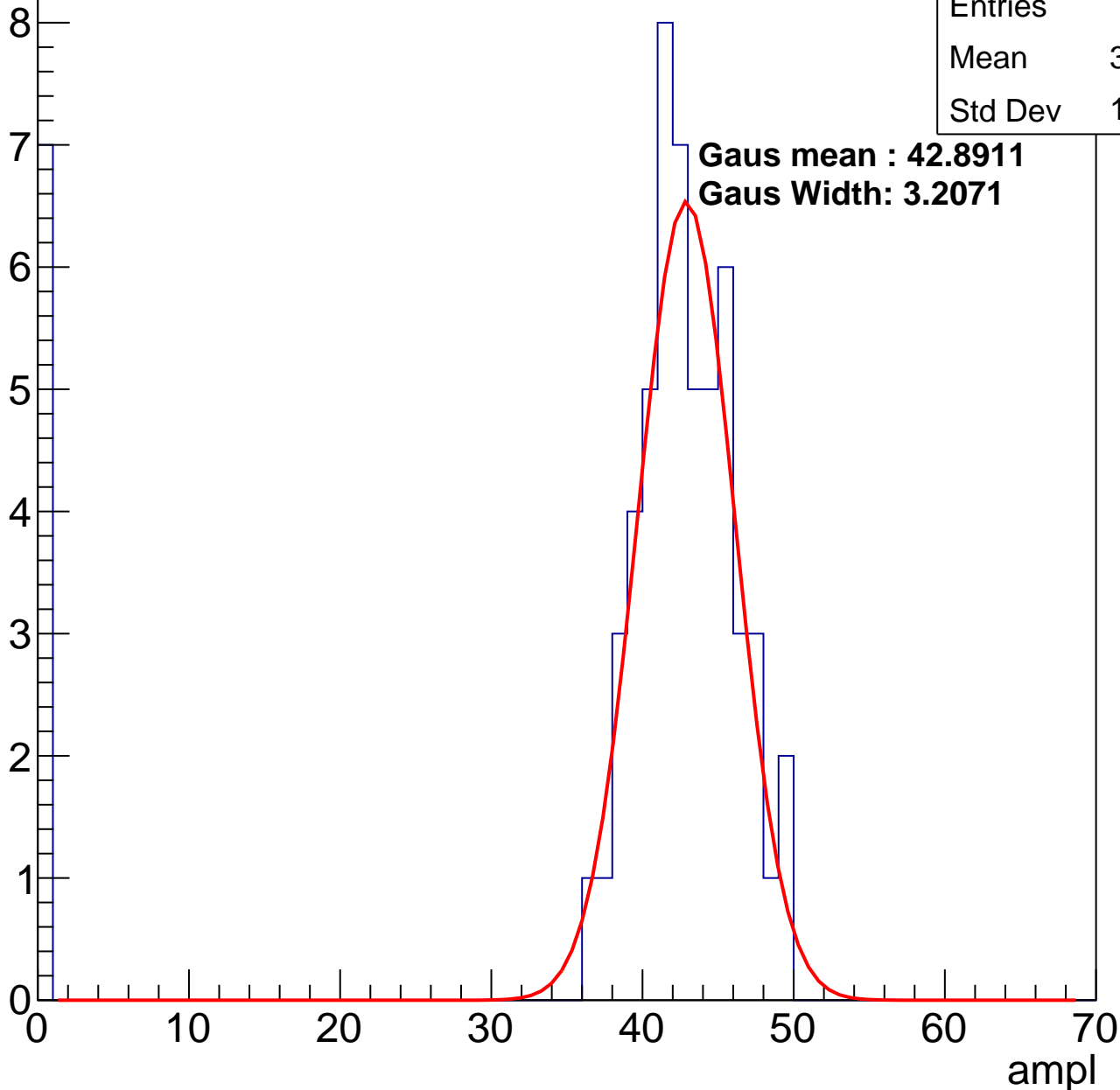
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	37.62
Std Dev	13.84

Gaus mean : 42.8911

Gaus Width: 3.2071



B1L103S, U21-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

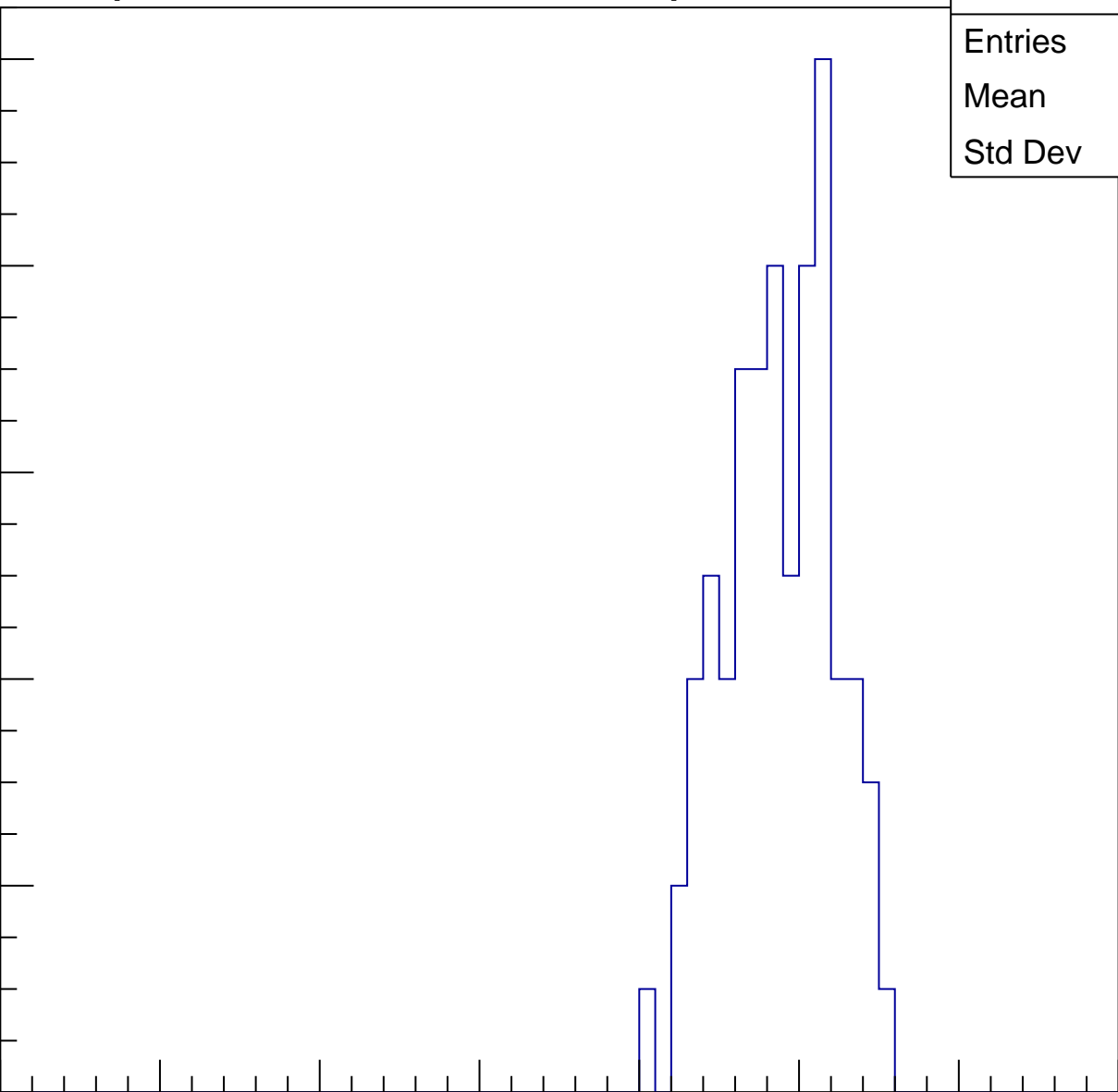
Entries	73
Mean	48.26
Std Dev	3.372

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

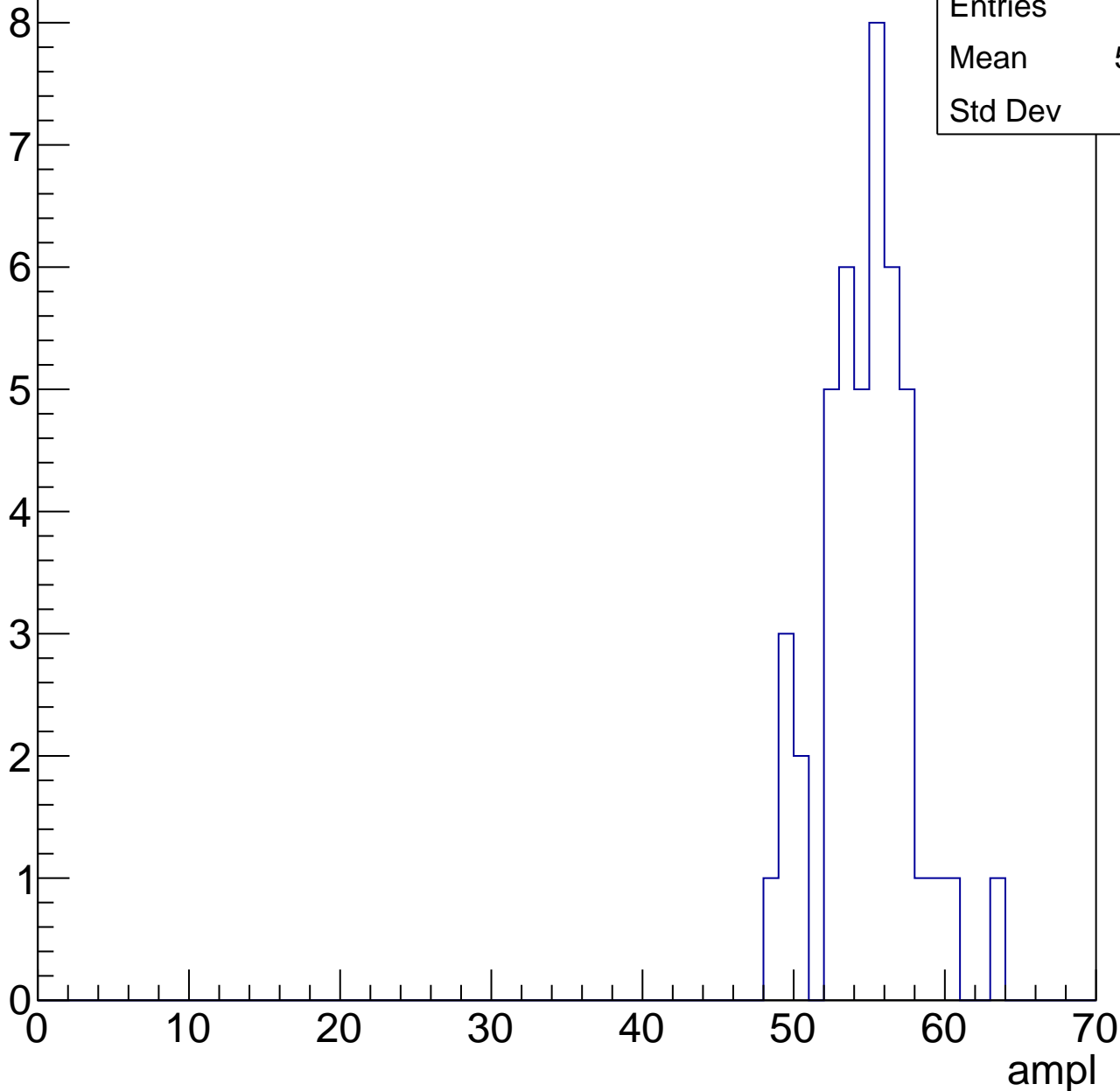


B1L103S, U21-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

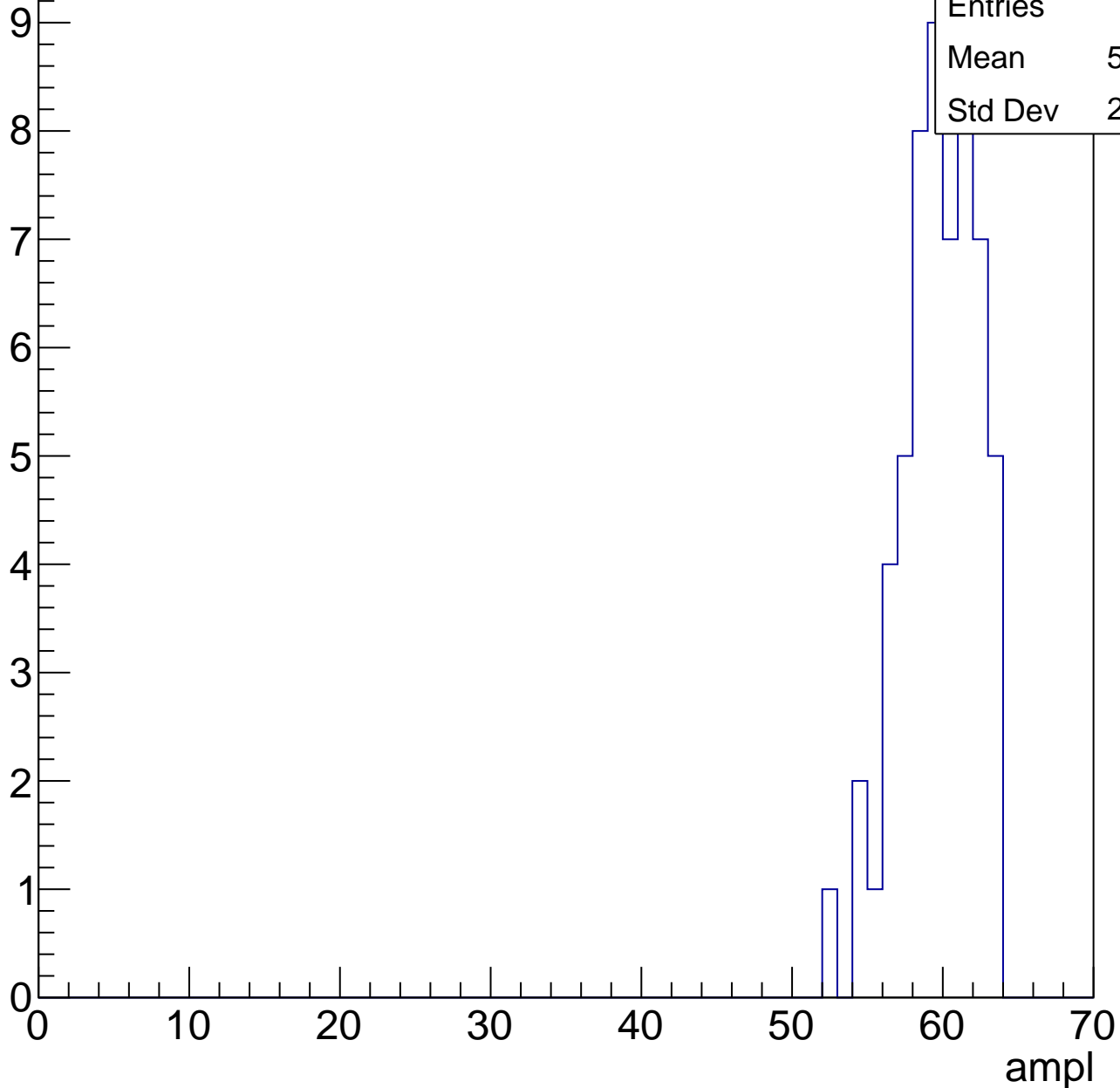
Entries	45
Mean	54.31
Std Dev	2.98



B1L103S, U21-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

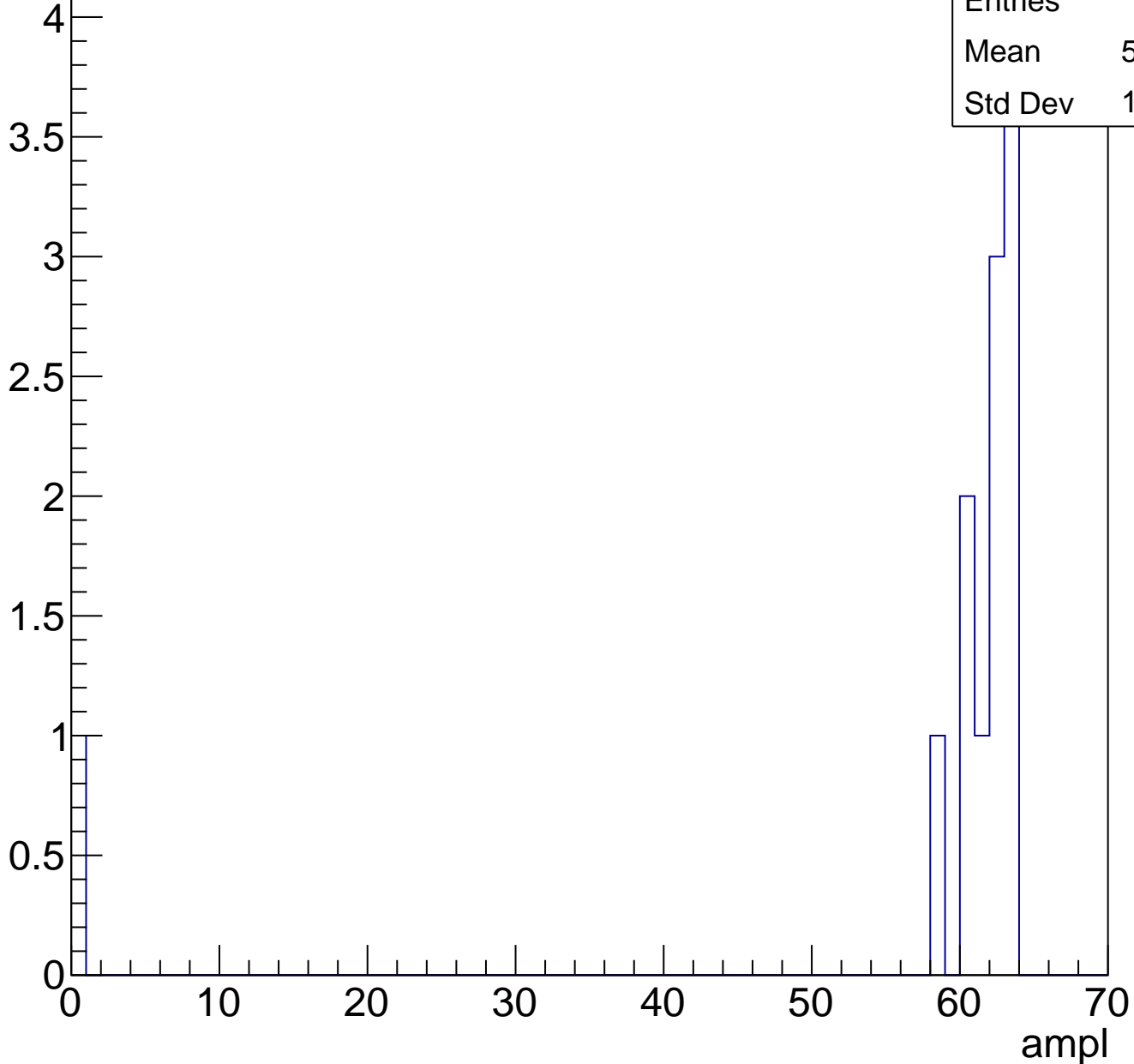
Entry



B1L103S, U21-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch27, adc0

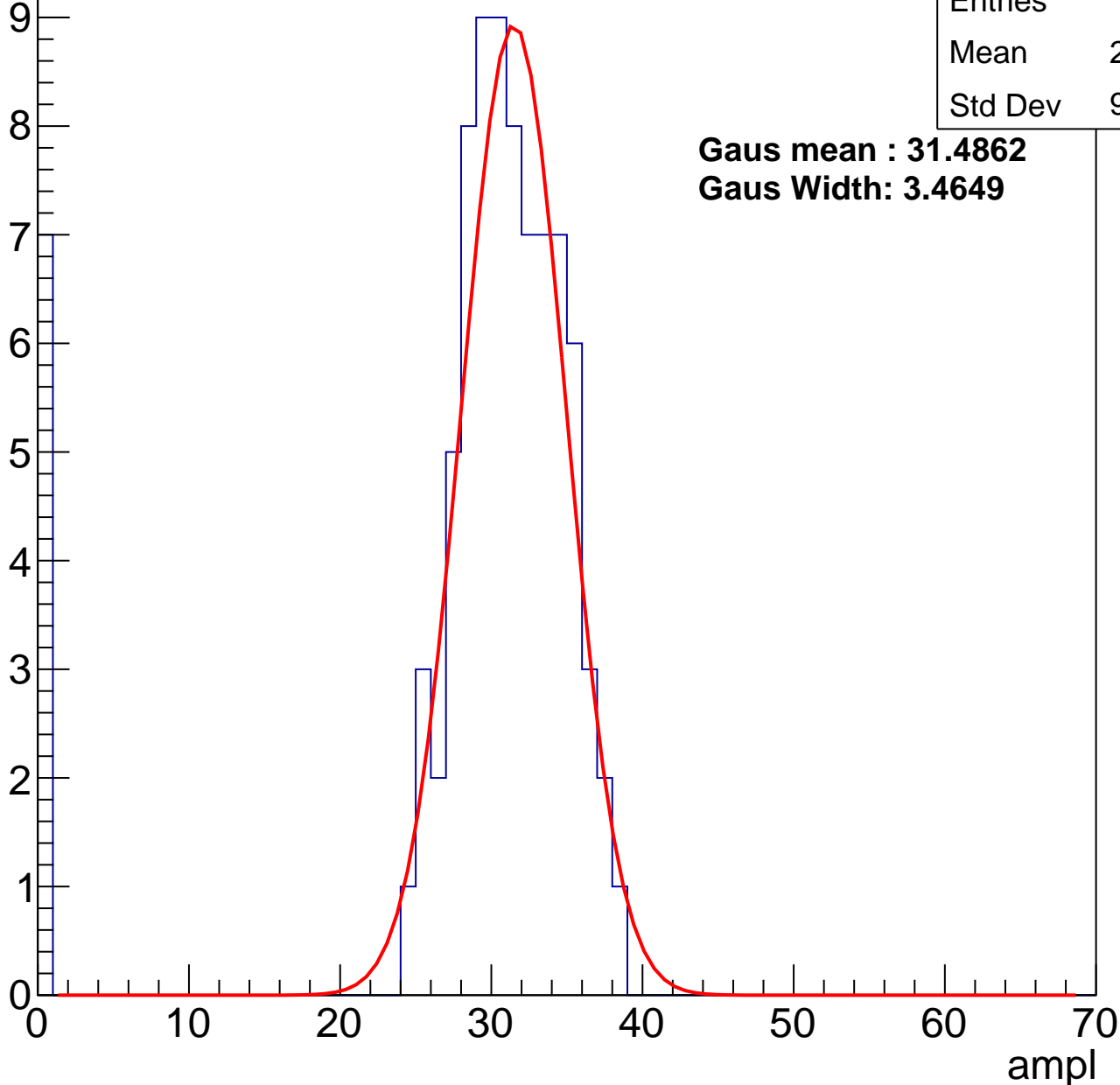
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	28.38
Std Dev	9.033

Gaus mean : 31.4862

Gaus Width: 3.4649



B1L103S, U21-ch27, adc1

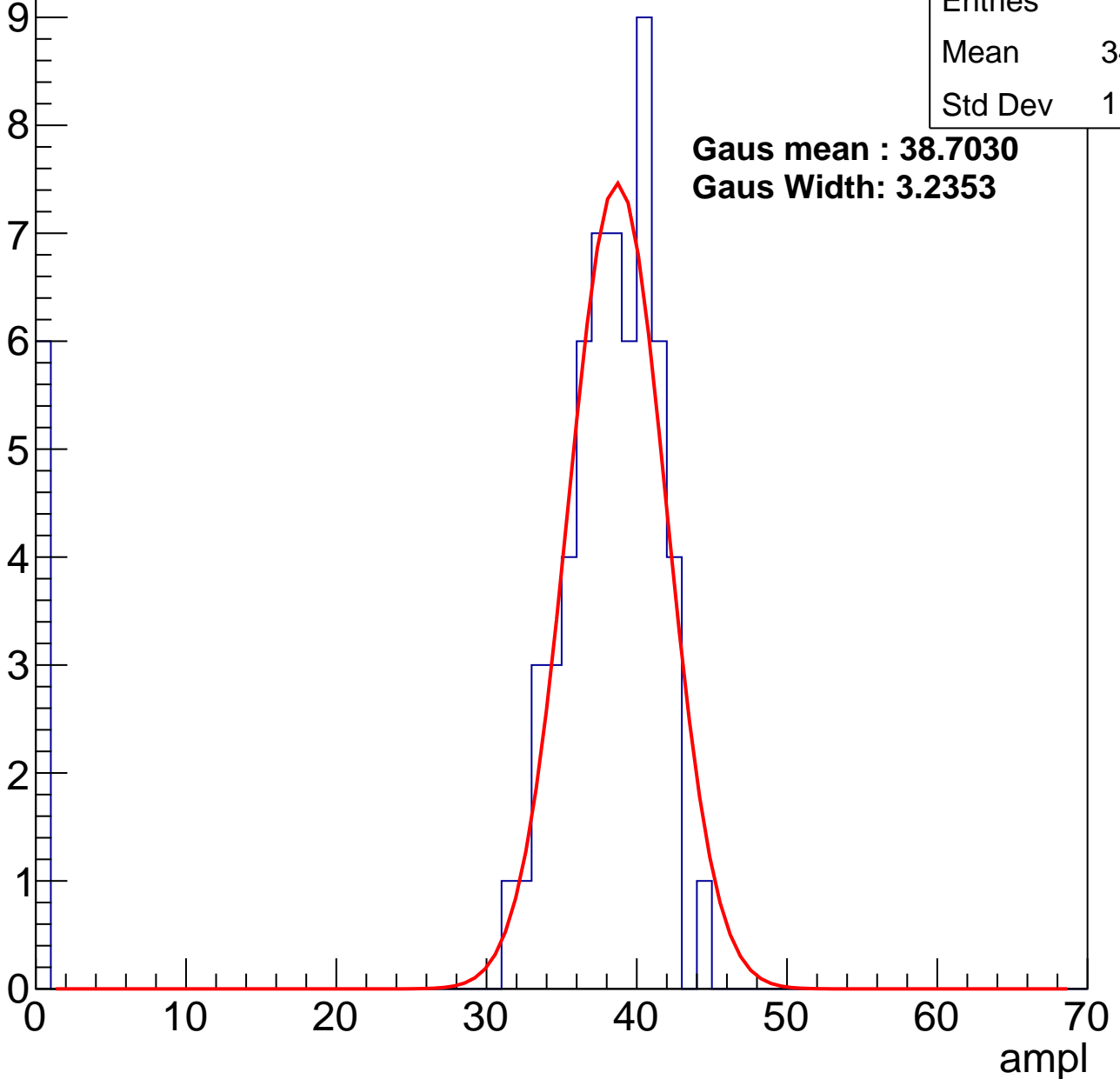
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.33
Std Dev	11.37

Gaus mean : 38.7030

Gaus Width: 3.2353



B1L103S, U21-ch27, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	40.86
Std Dev	13.08

Gaus mean : 45.2705

Gaus Width: 3.2697

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

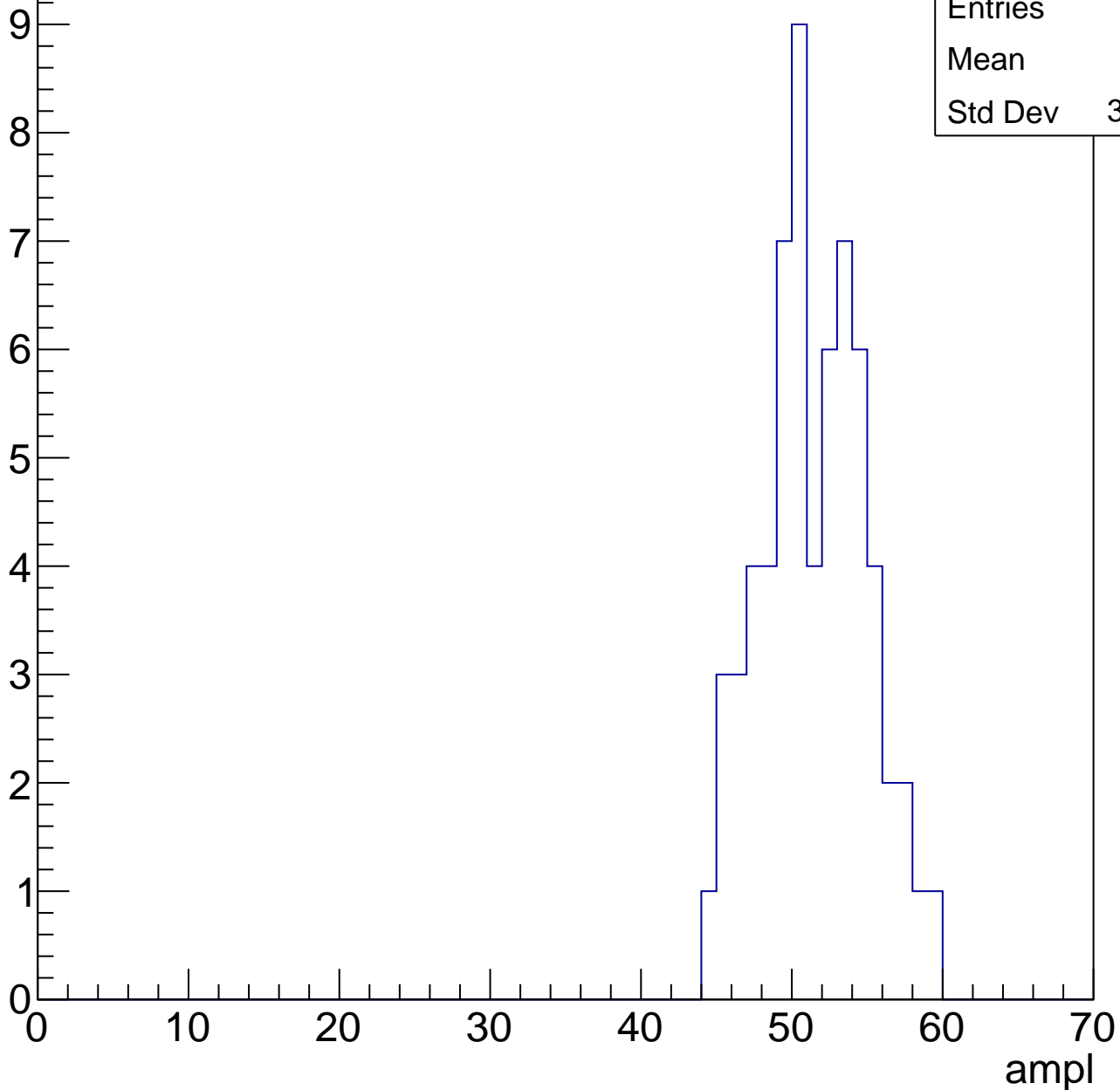
70

B1L103S, U21-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	51
Std Dev	3.437

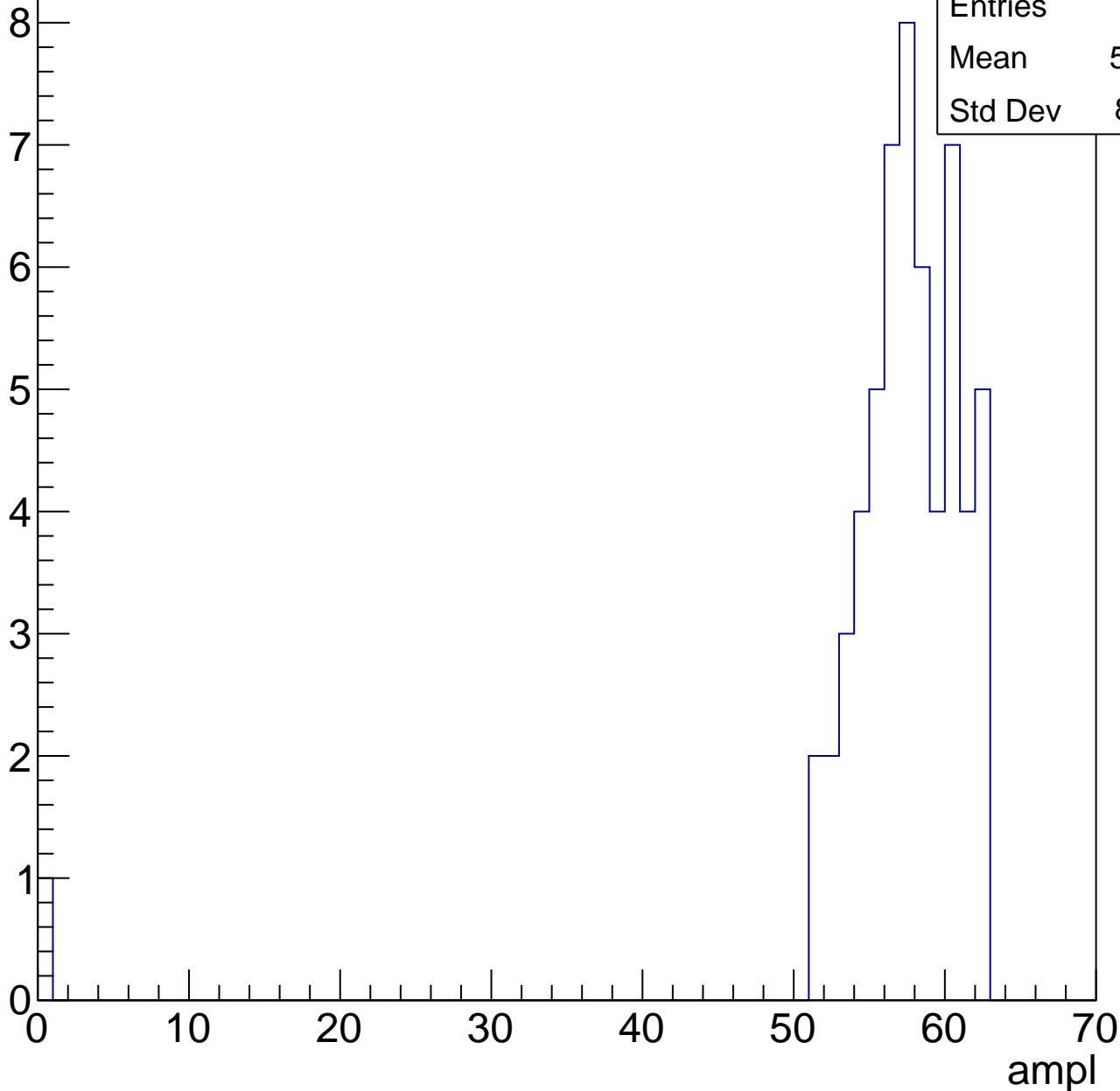


B1L103S, U21-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.24
Std Dev	8.011

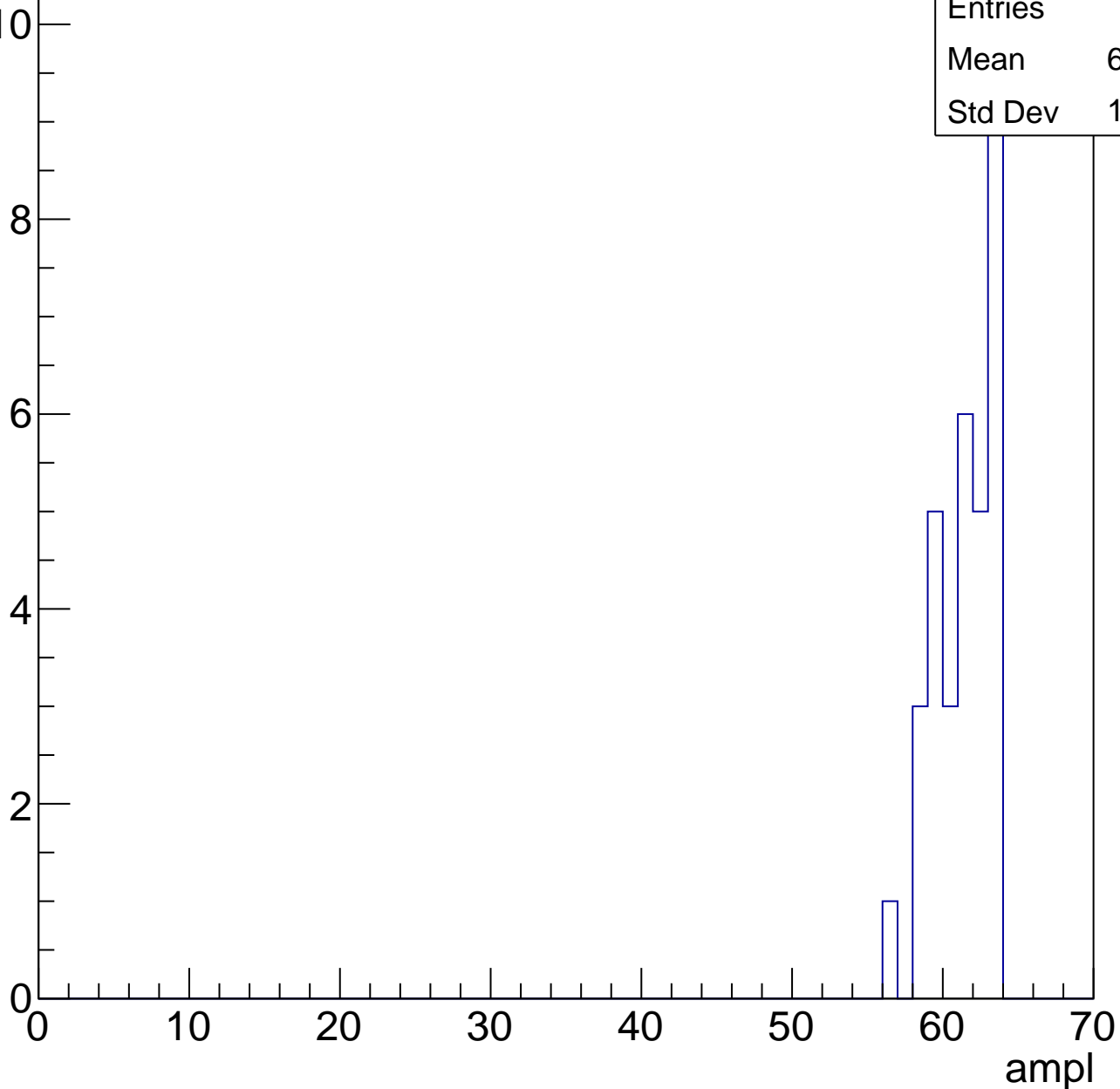


B1L103S, U21-ch27, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	60.94
Std Dev	1.906



B1L103S, U21-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch28, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	23.31
Std Dev	11.2

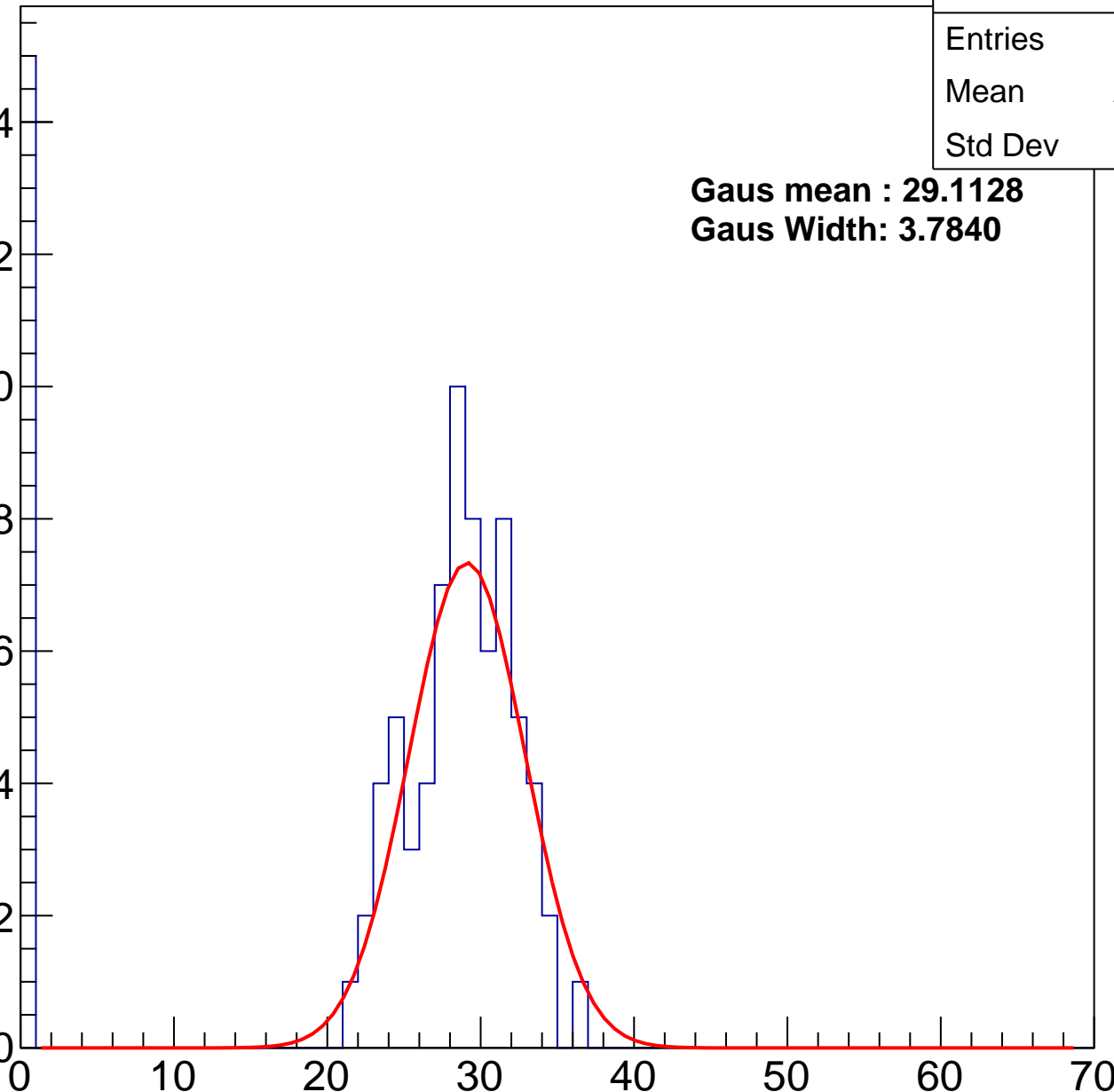
Gaus mean : 29.1128

Gaus Width: 3.7840

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch28, adc1

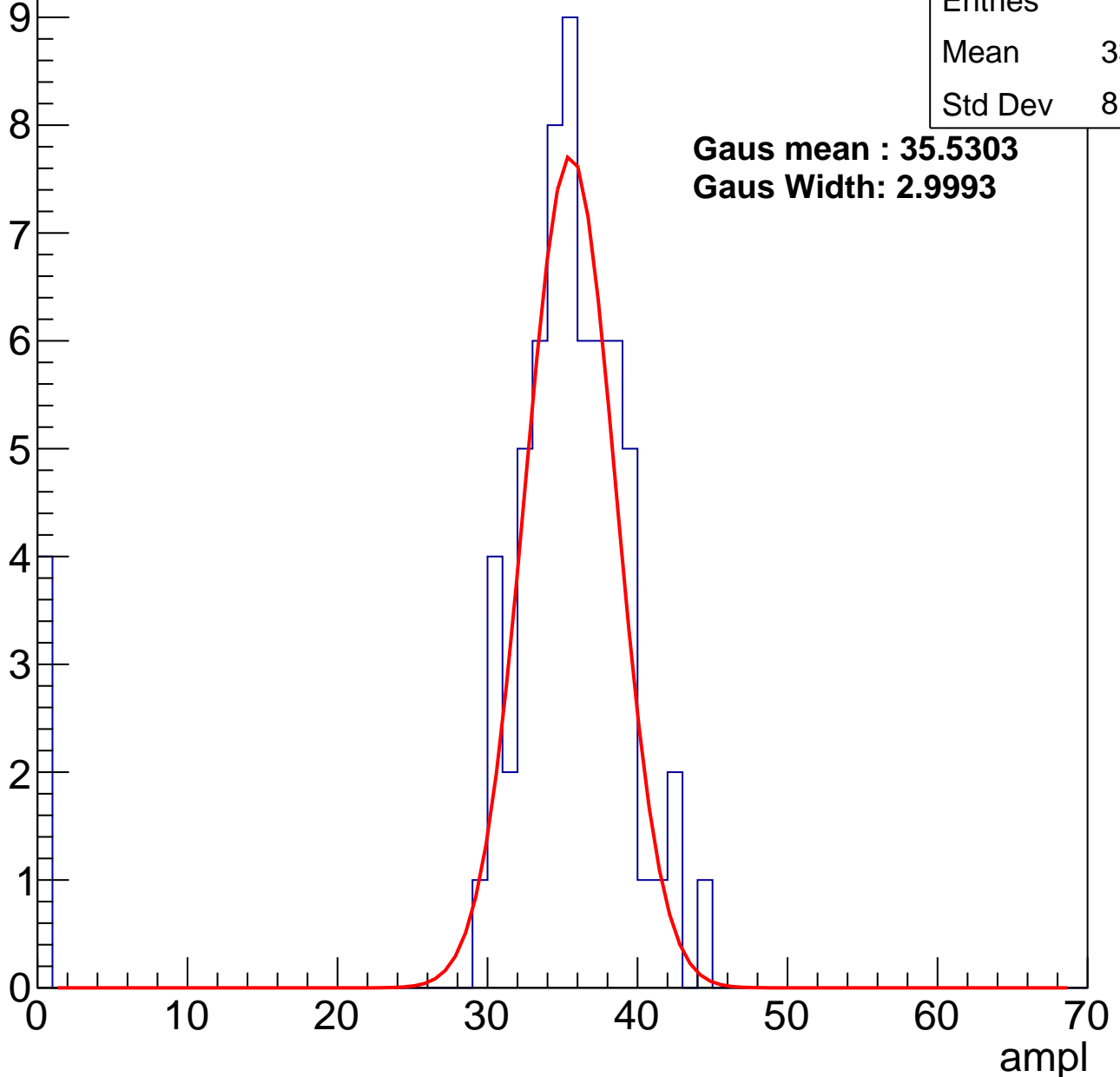
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.22
Std Dev	8.926

Gaus mean : 35.5303

Gaus Width: 2.9993



B1L103S, U21-ch28, adc2

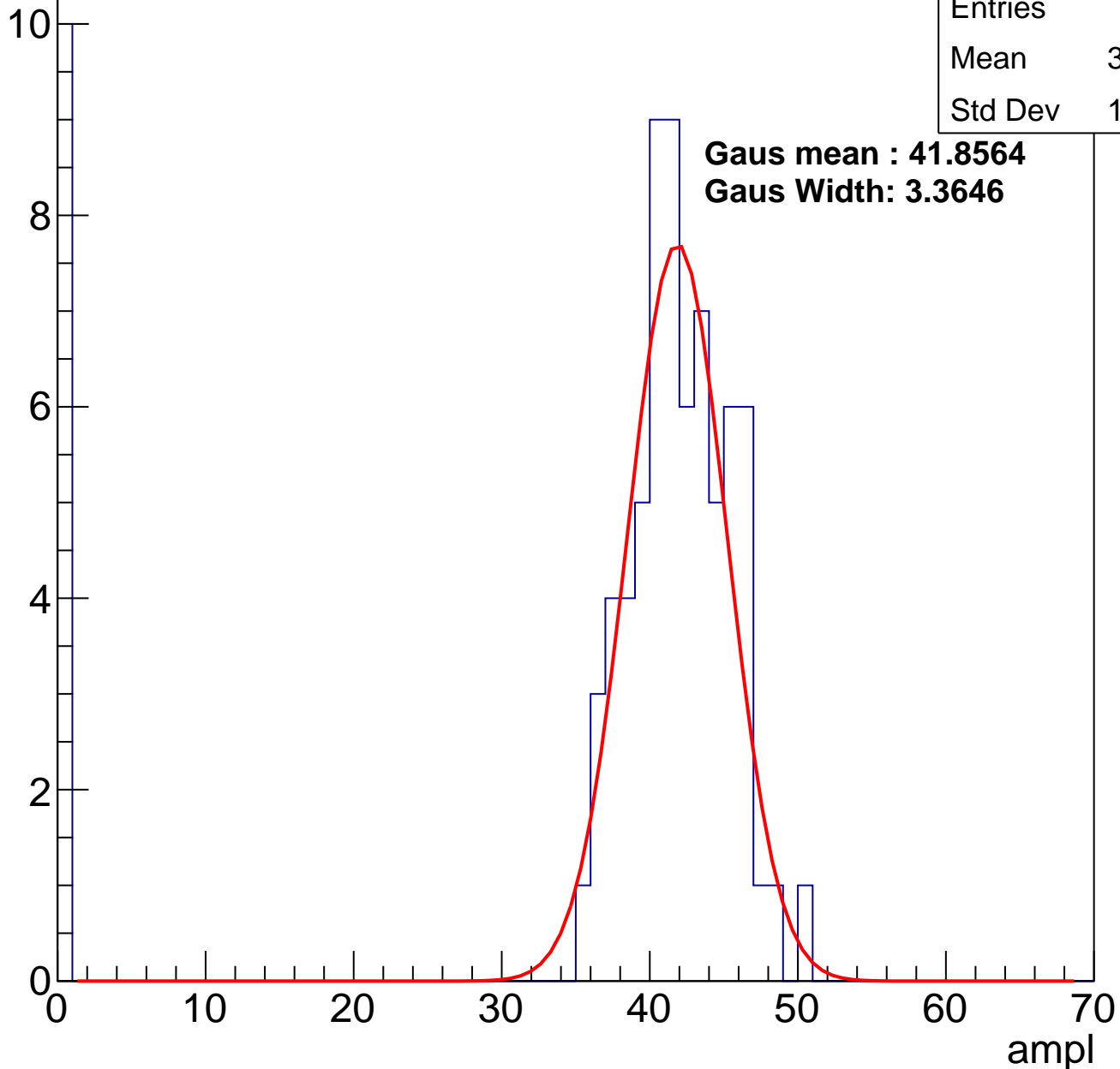
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	36.29
Std Dev	14.24

Gaus mean : 41.8564

Gaus Width: 3.3646

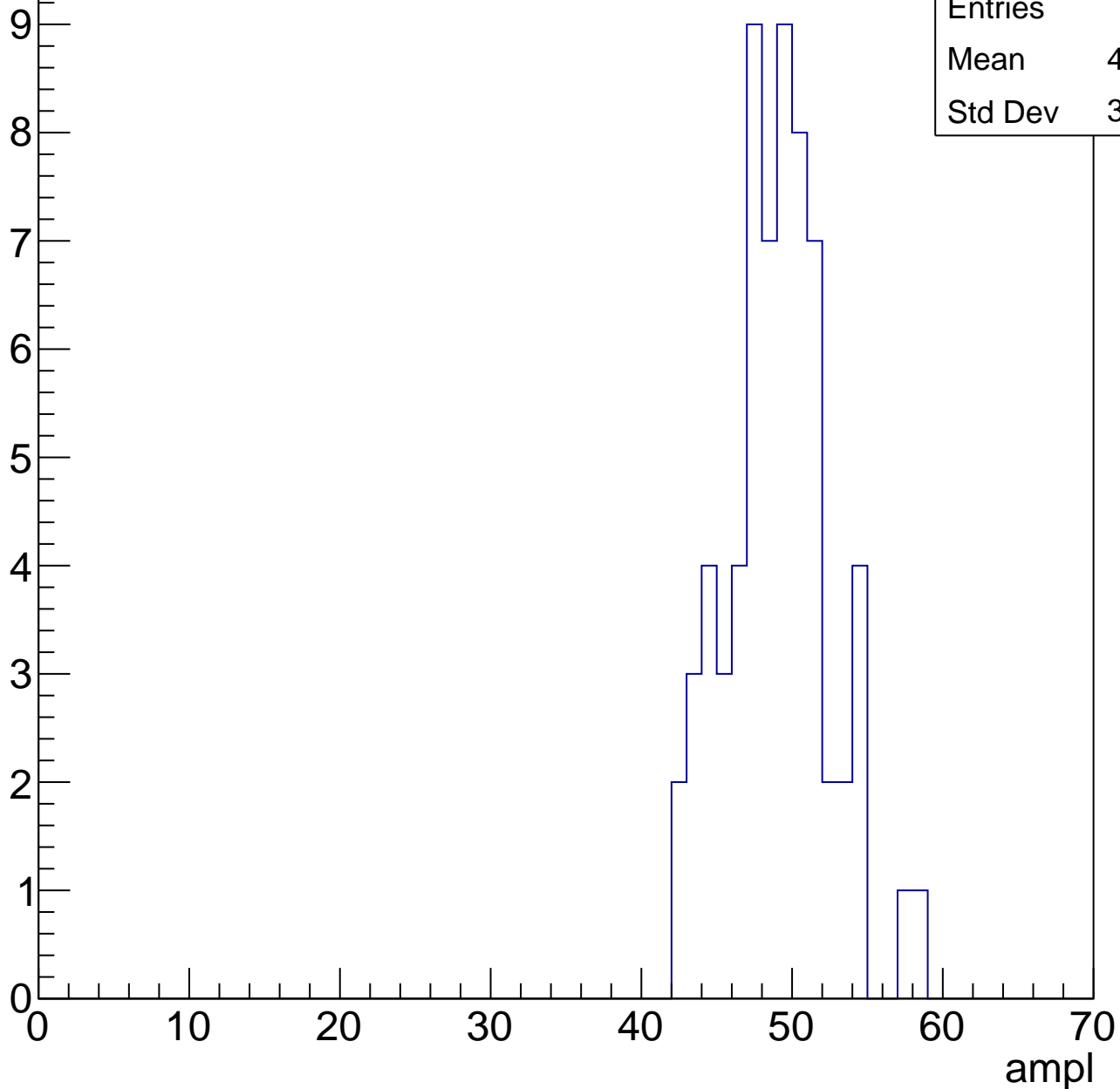
Entry



B1L103S, U21-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



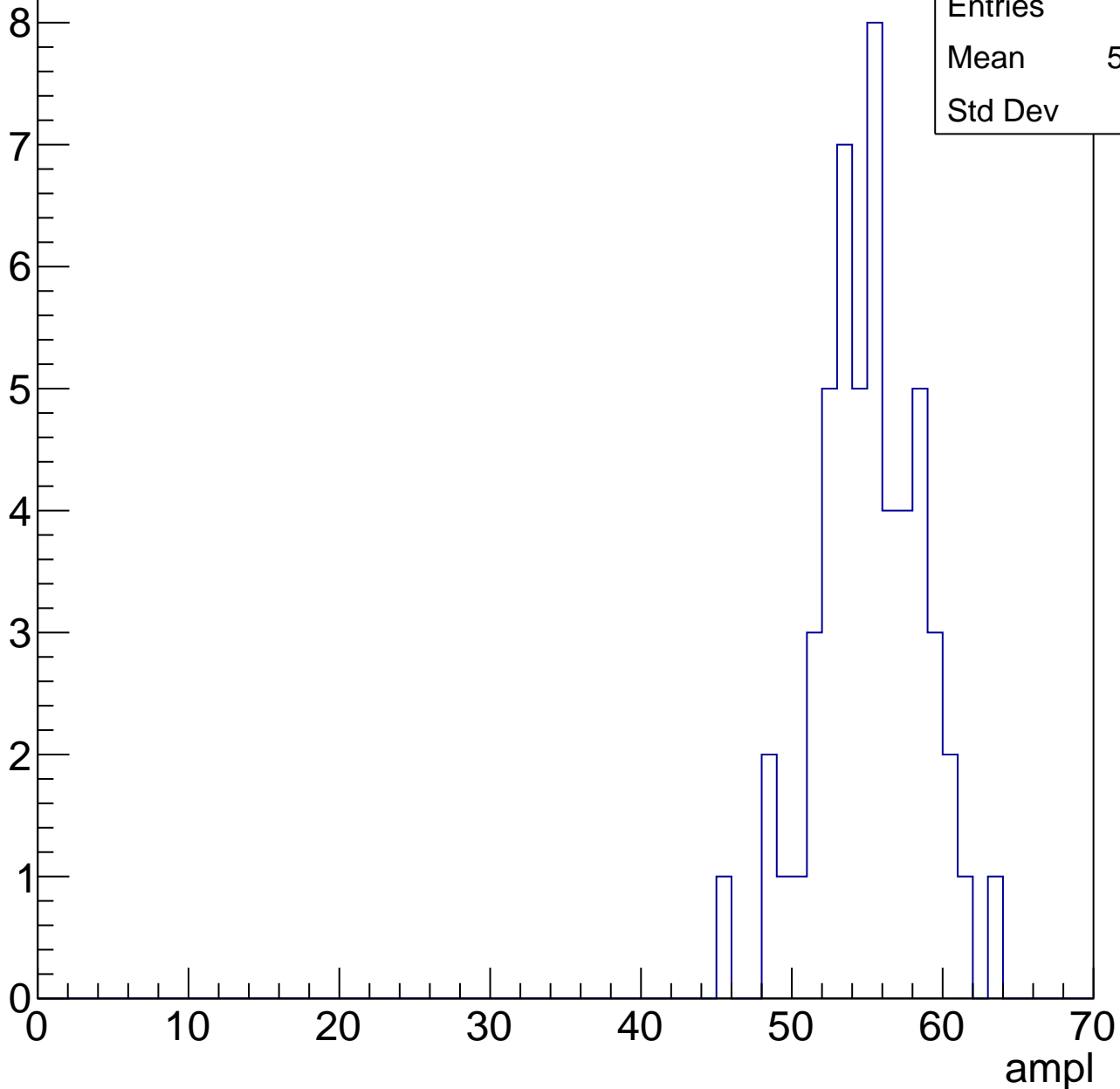
Entries	66
Mean	48.58
Std Dev	3.376

B1L103S, U21-ch28, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

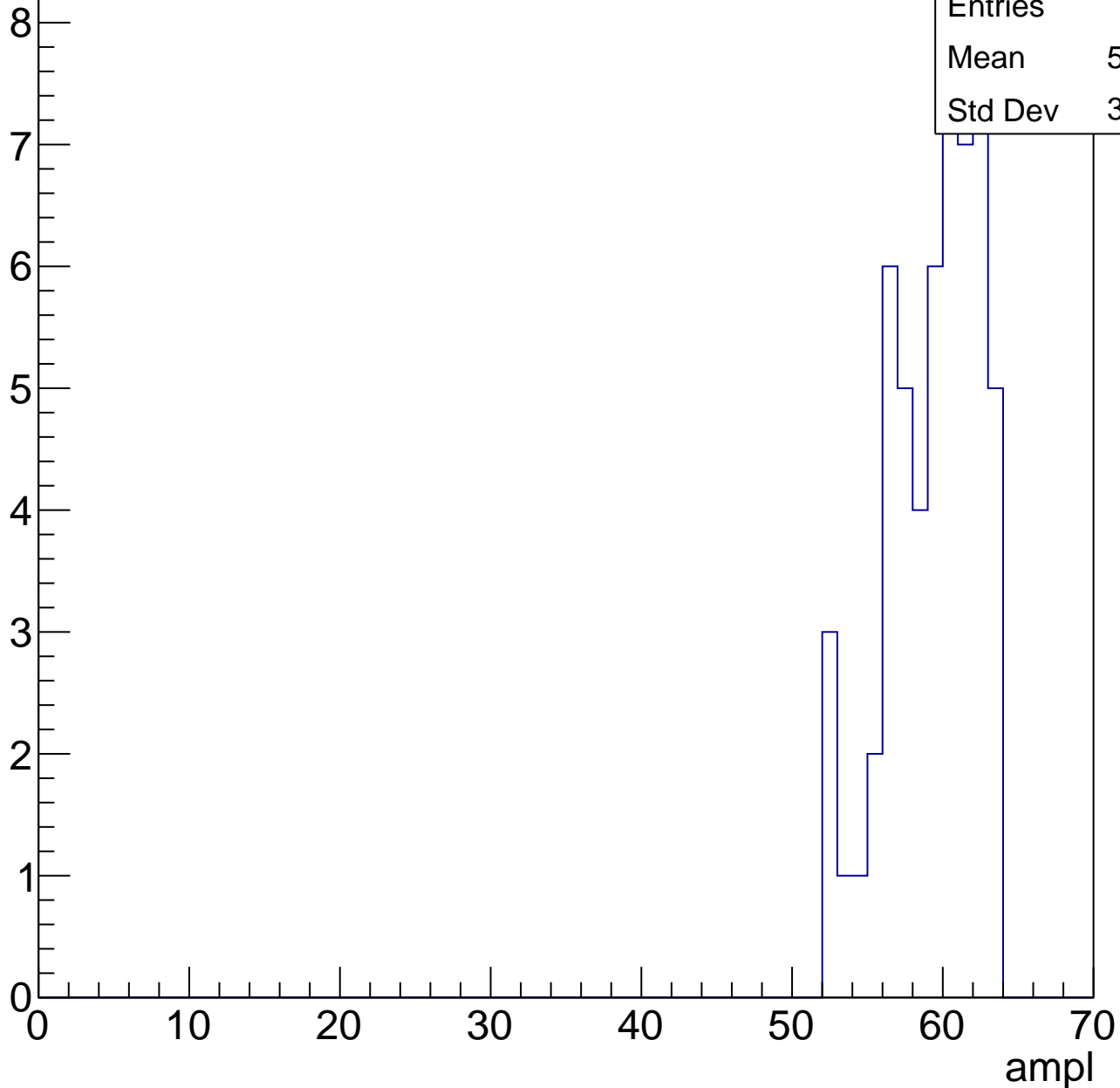
Entries	53
Mean	54.66
Std Dev	3.48



B1L103S, U21-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

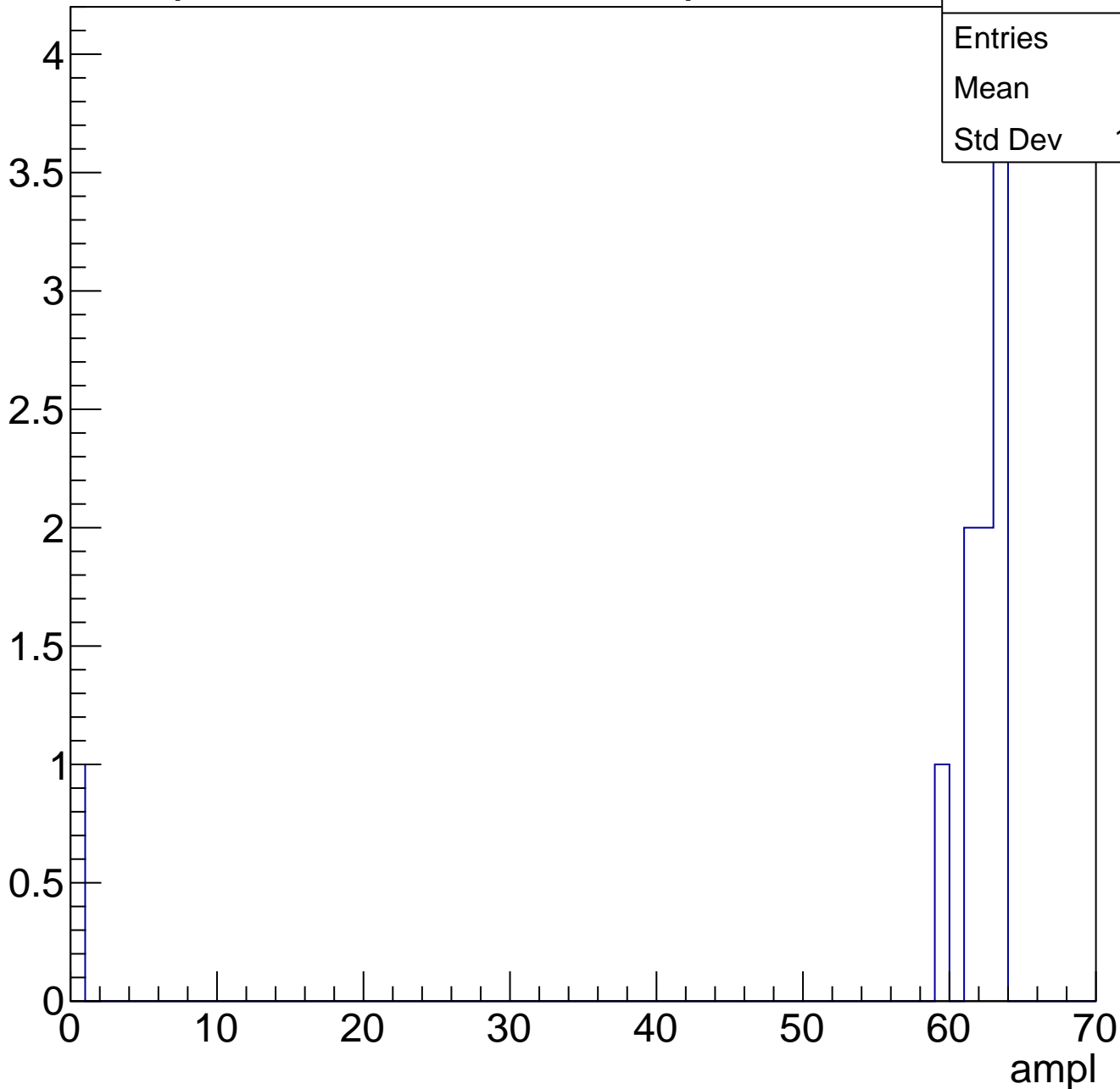
Entry



B1L103S, U21-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch29, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	26.99
Std Dev	8.044

Gaus mean : 29.5333

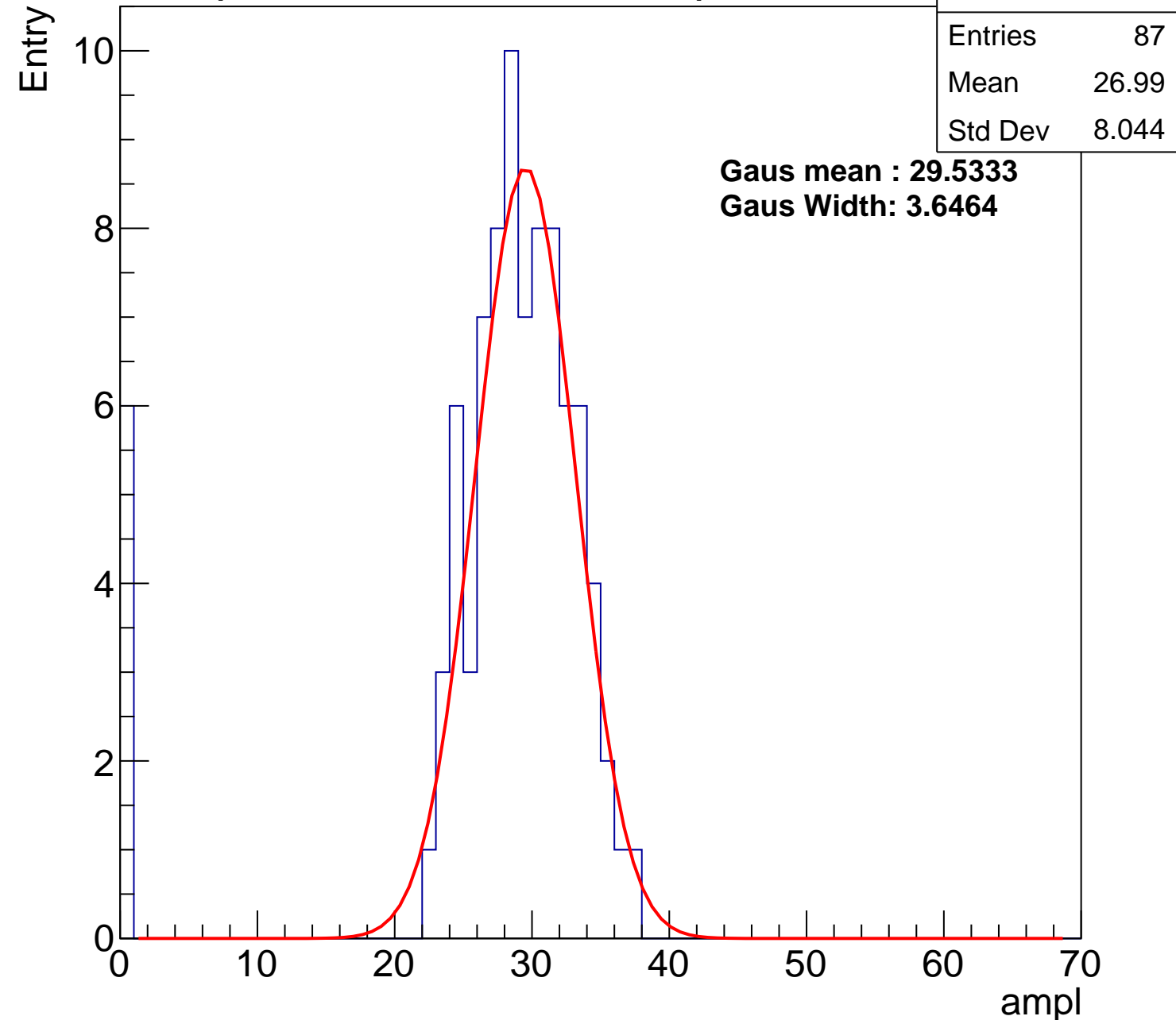
Gaus Width: 3.6464

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch29, adc1

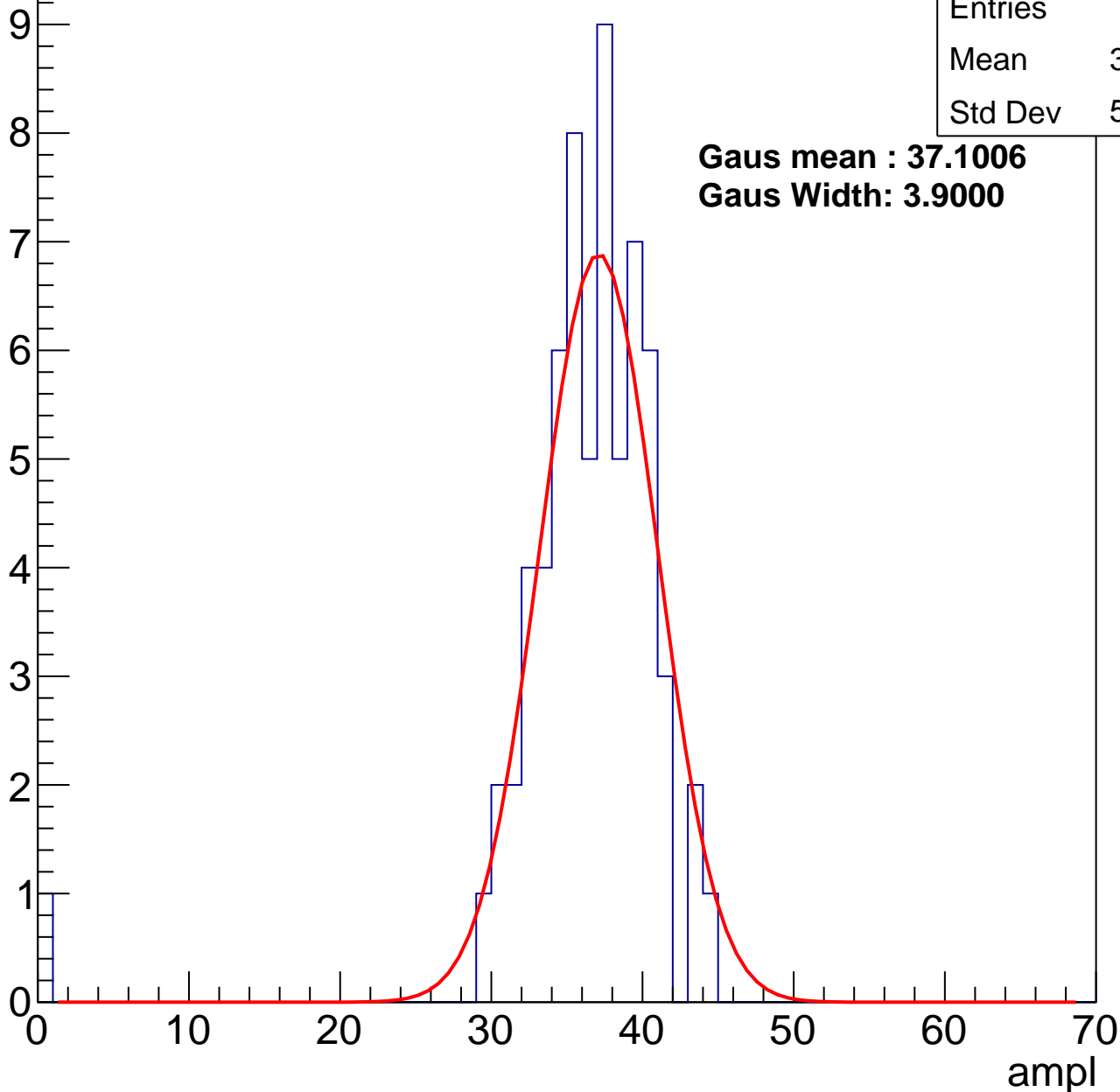
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	35.82
Std Dev	5.535

Gaus mean : 37.1006

Gaus Width: 3.9000



B1L103S, U21-ch29, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	33.72
Std Dev	16.77

Gaus mean : 39.8751

Gaus Width: 5.9864

10

8

6

4

2

0

0

10

20

30

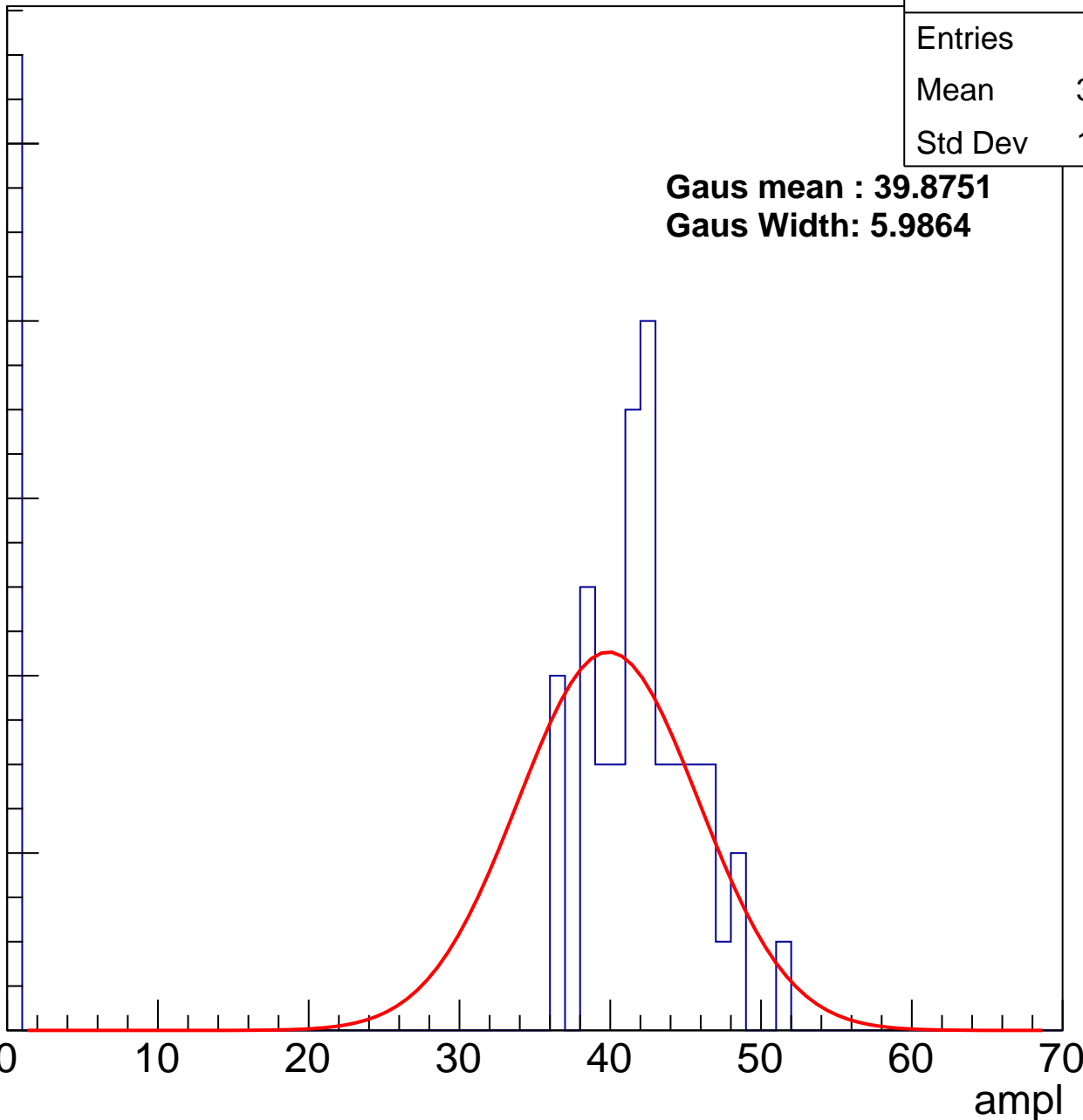
40

50

60

70

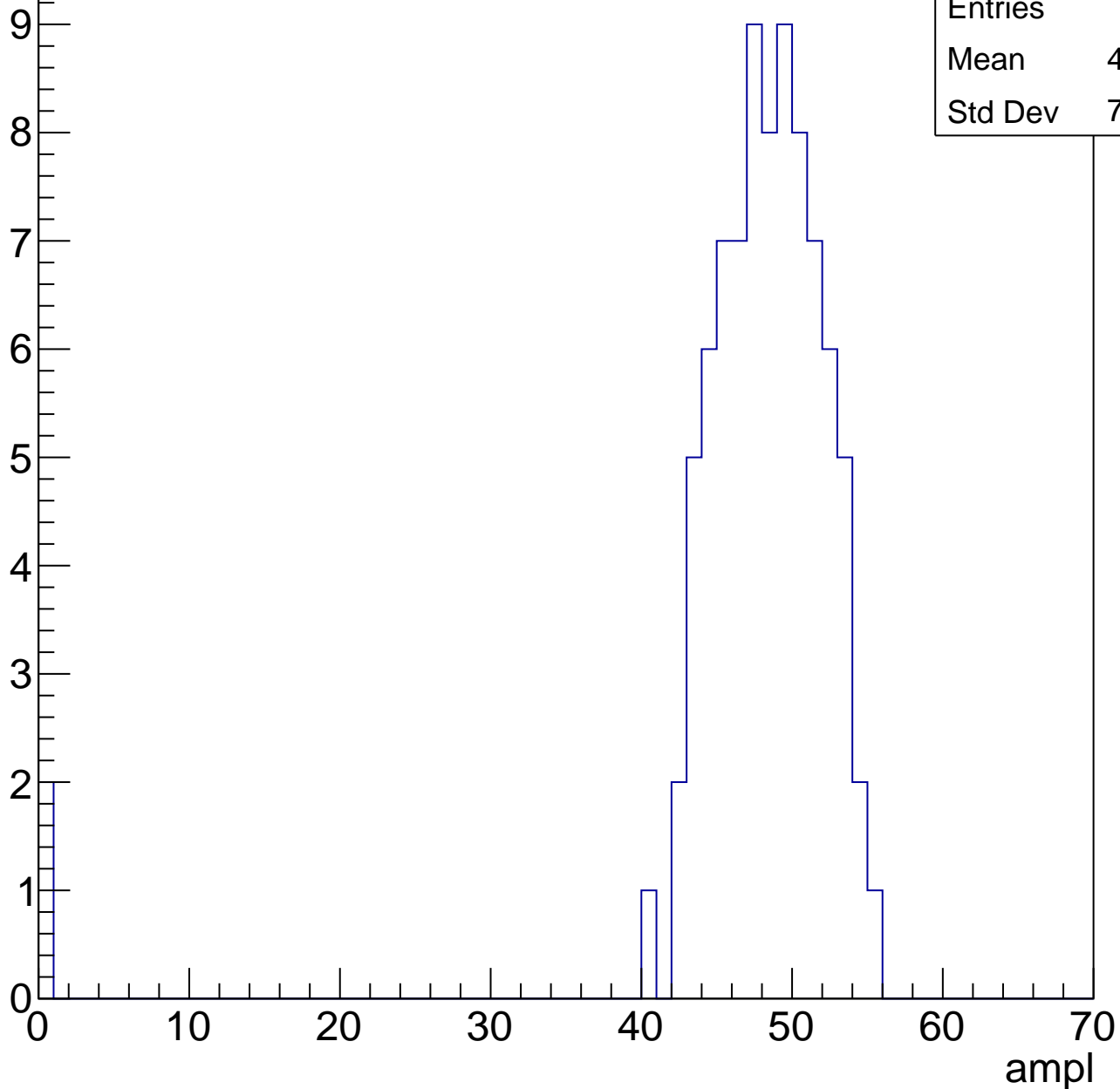
ampl



B1L103S, U21-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

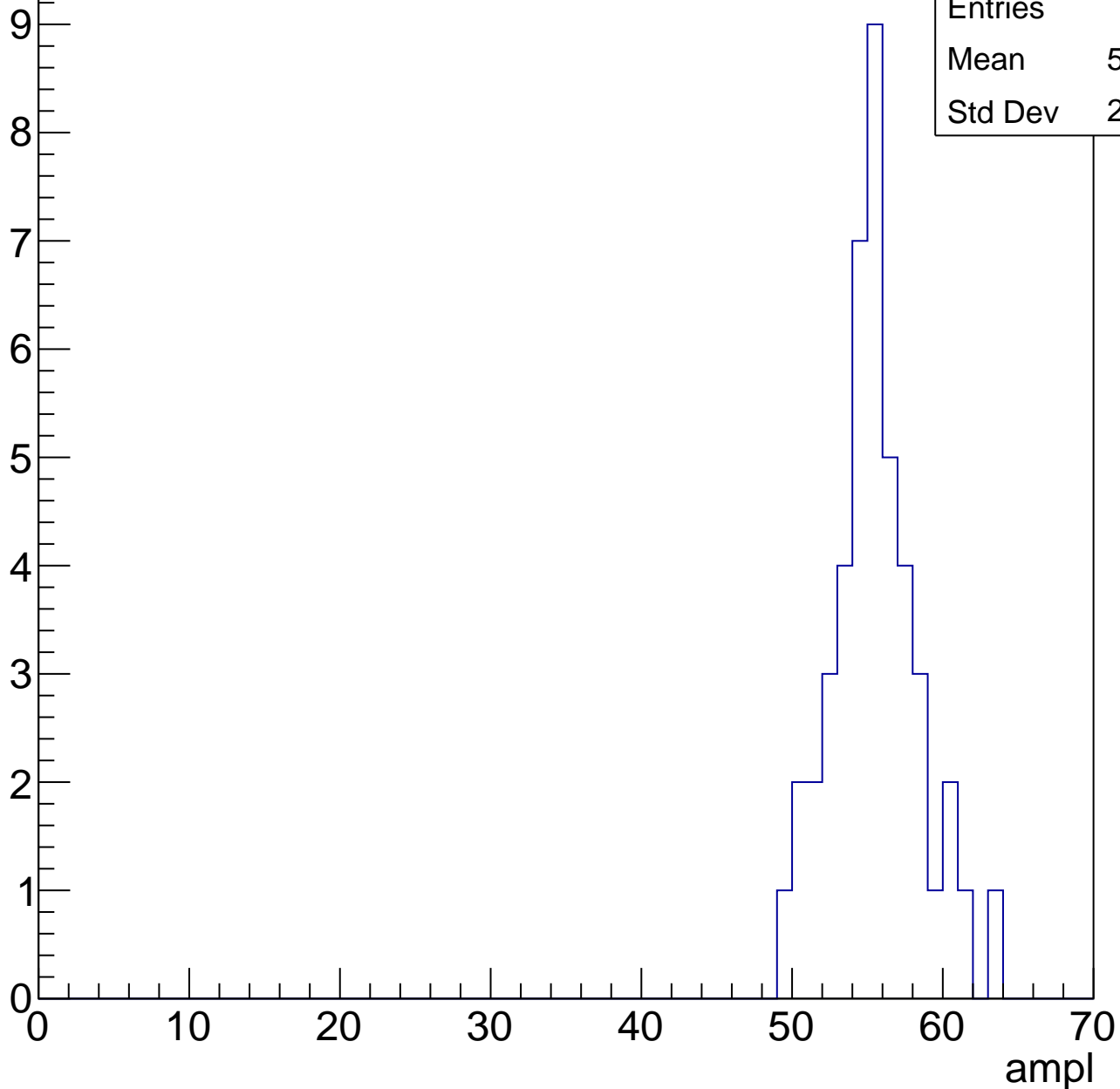


Entries	85
Mean	46.88
Std Dev	7.974

B1L103S, U21-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

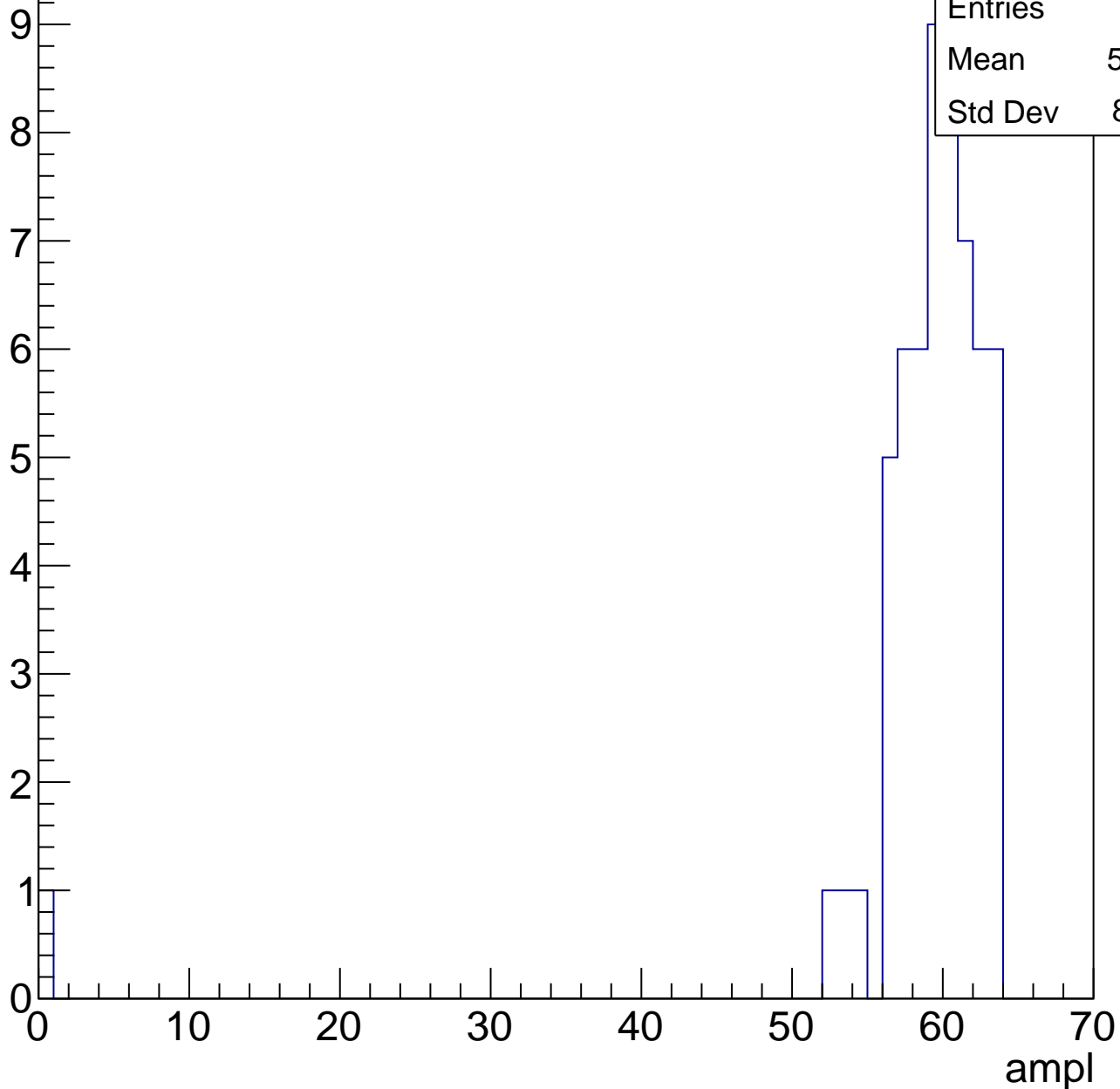


Entries	45
Mean	55.04
Std Dev	2.913

B1L103S, U21-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

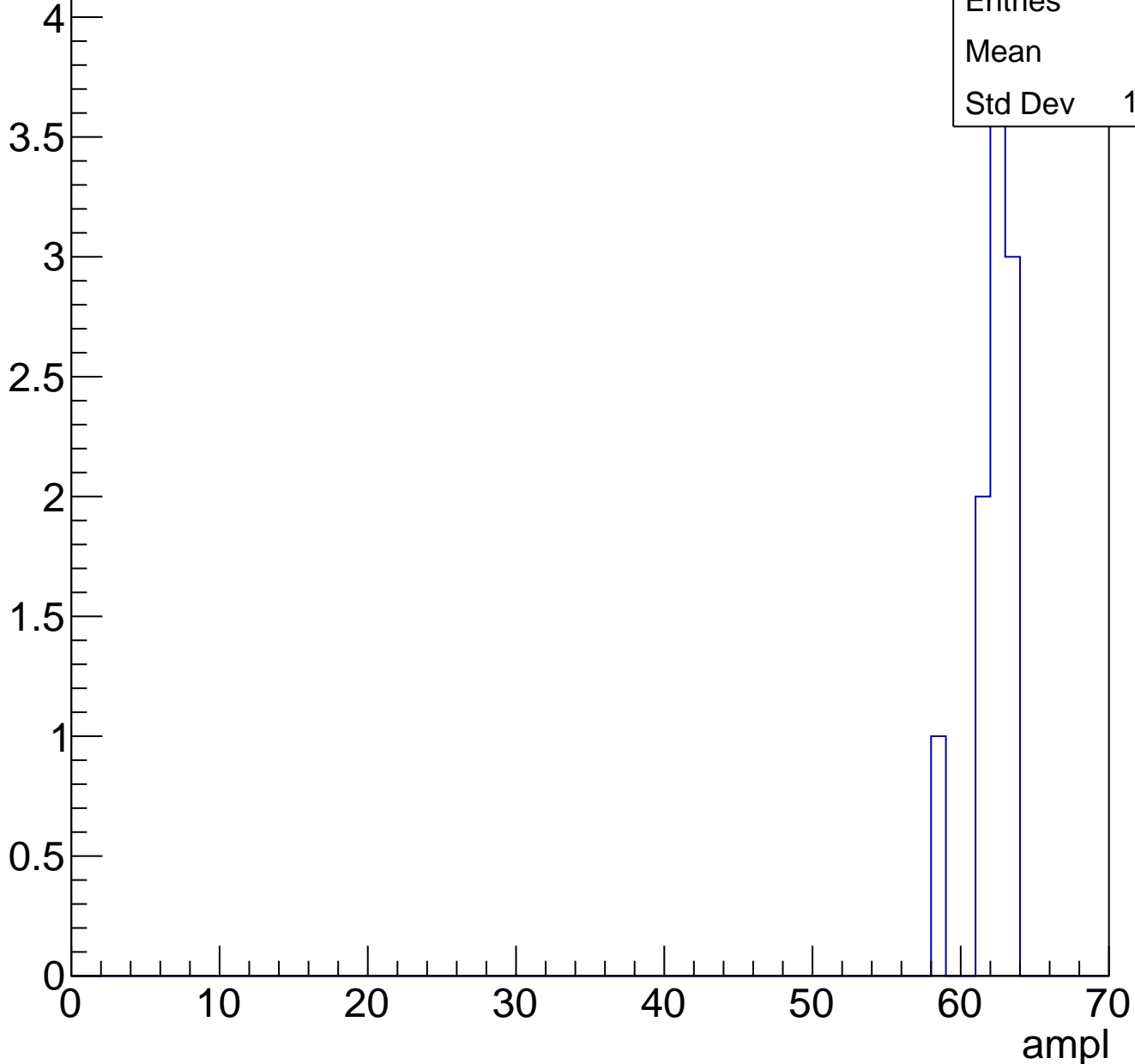
Entry



B1L103S, U21-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	10
Mean	61.7
Std Dev	1.418

B1L103S, U21-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch30, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	23.16
Std Dev	11.02

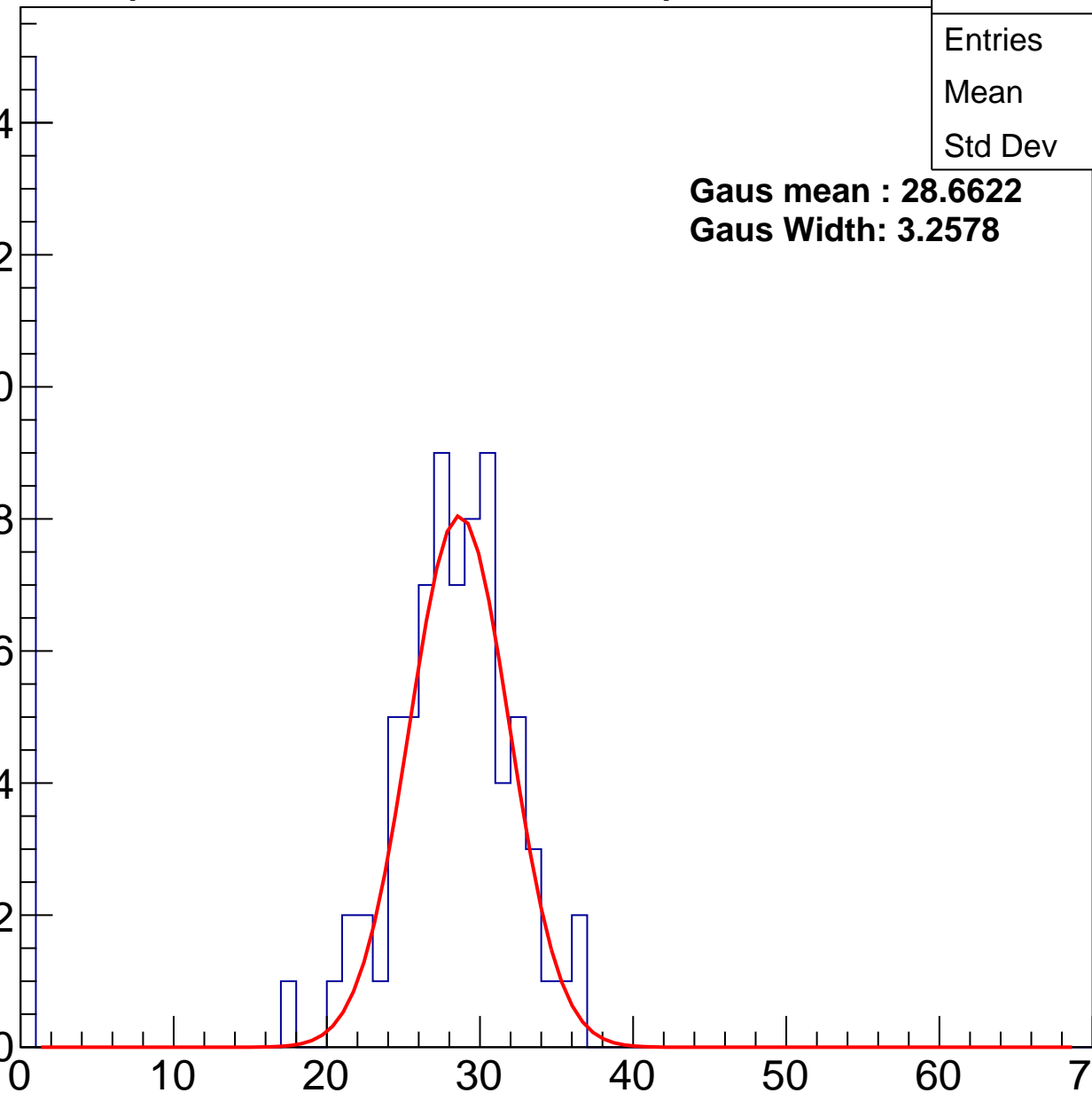
Gaus mean : 28.6622

Gaus Width: 3.2578

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch30, adc1

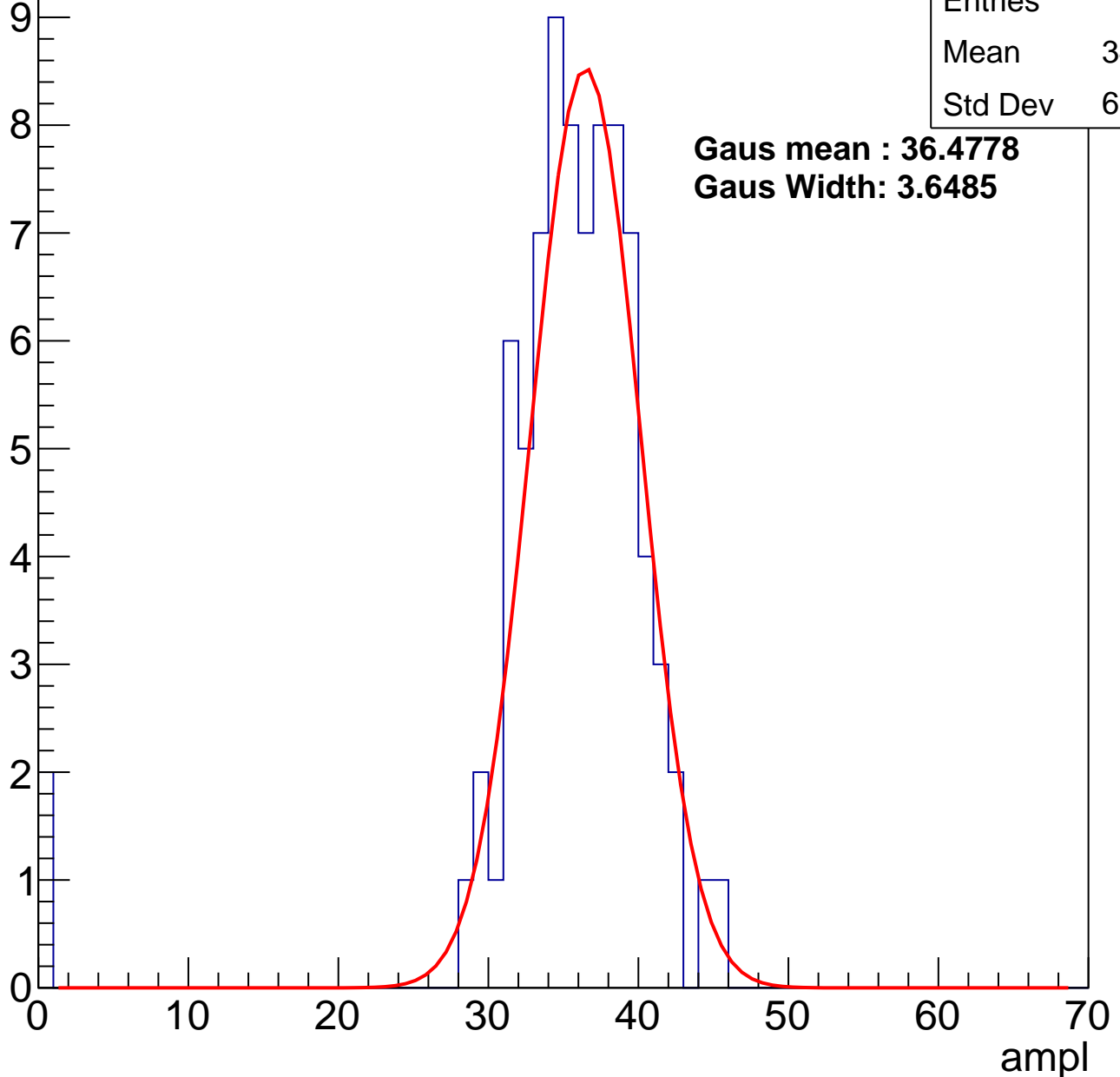
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	34.88
Std Dev	6.517

Gaus mean : 36.4778

Gaus Width: 3.6485



B1L103S, U21-ch30, adc2

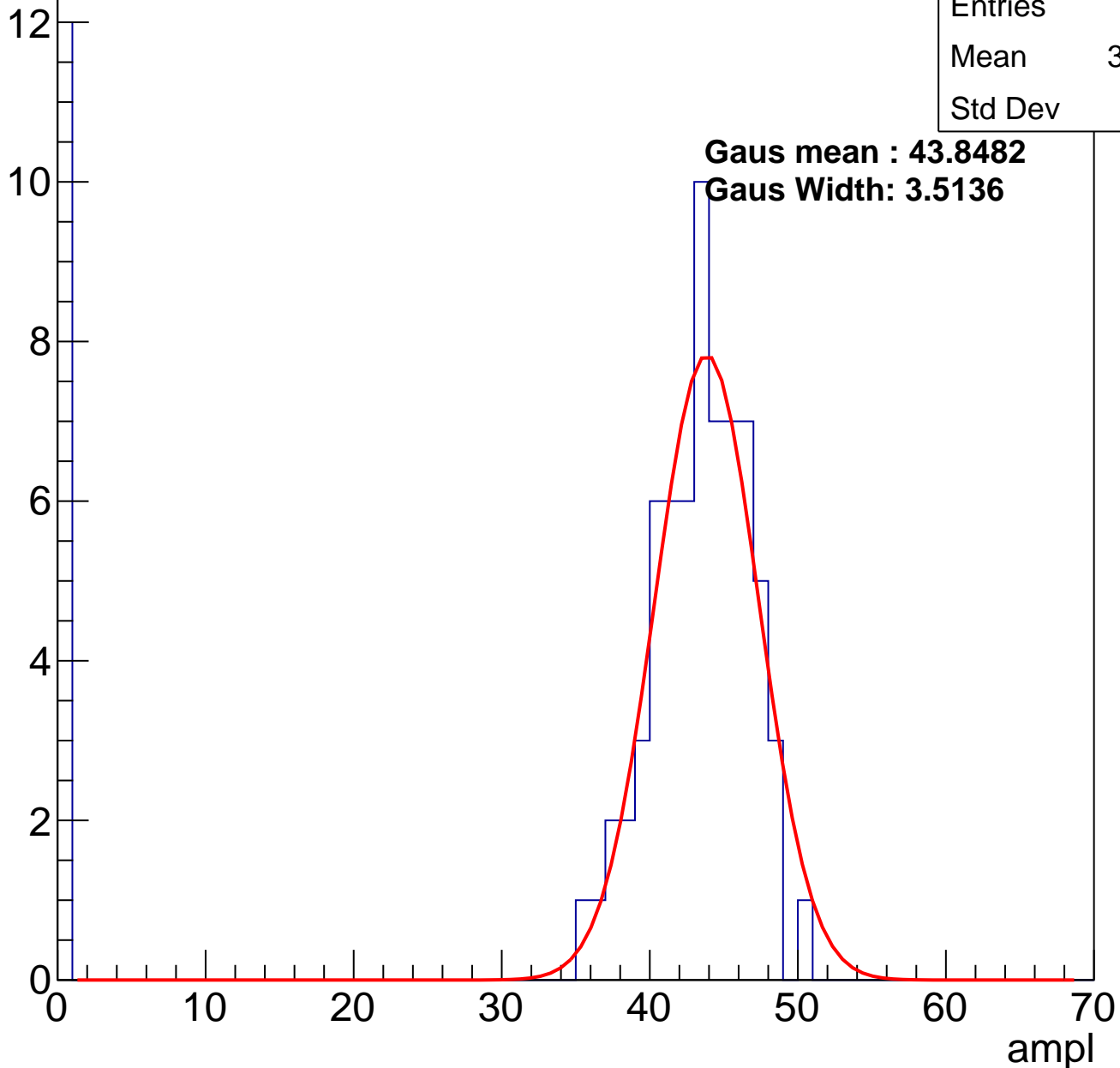
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	36.46
Std Dev	15.7

Gaus mean : 43.8482

Gaus Width: 3.5136

Entry

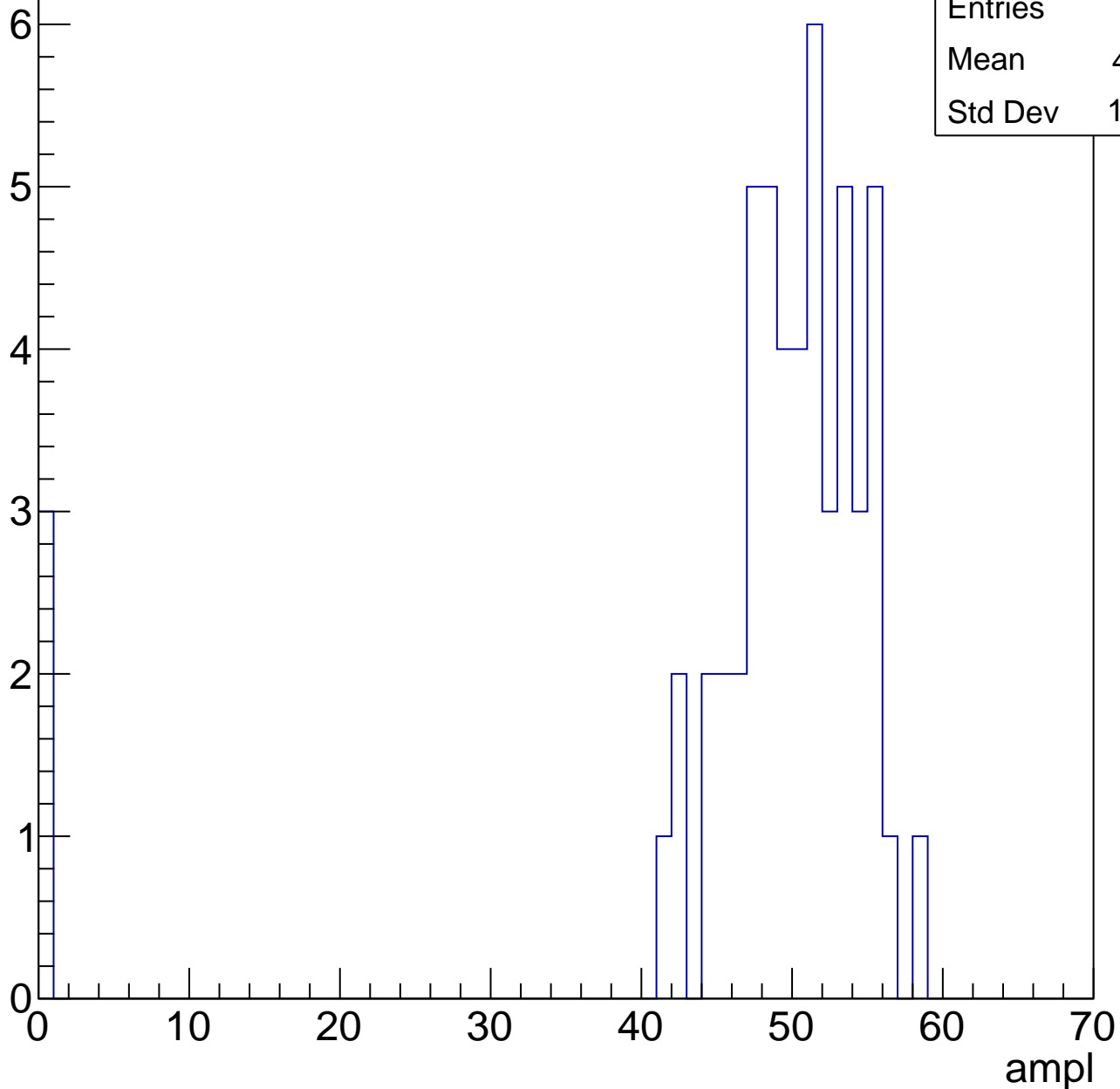


B1L103S, U21-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	47.11
Std Dev	12.03

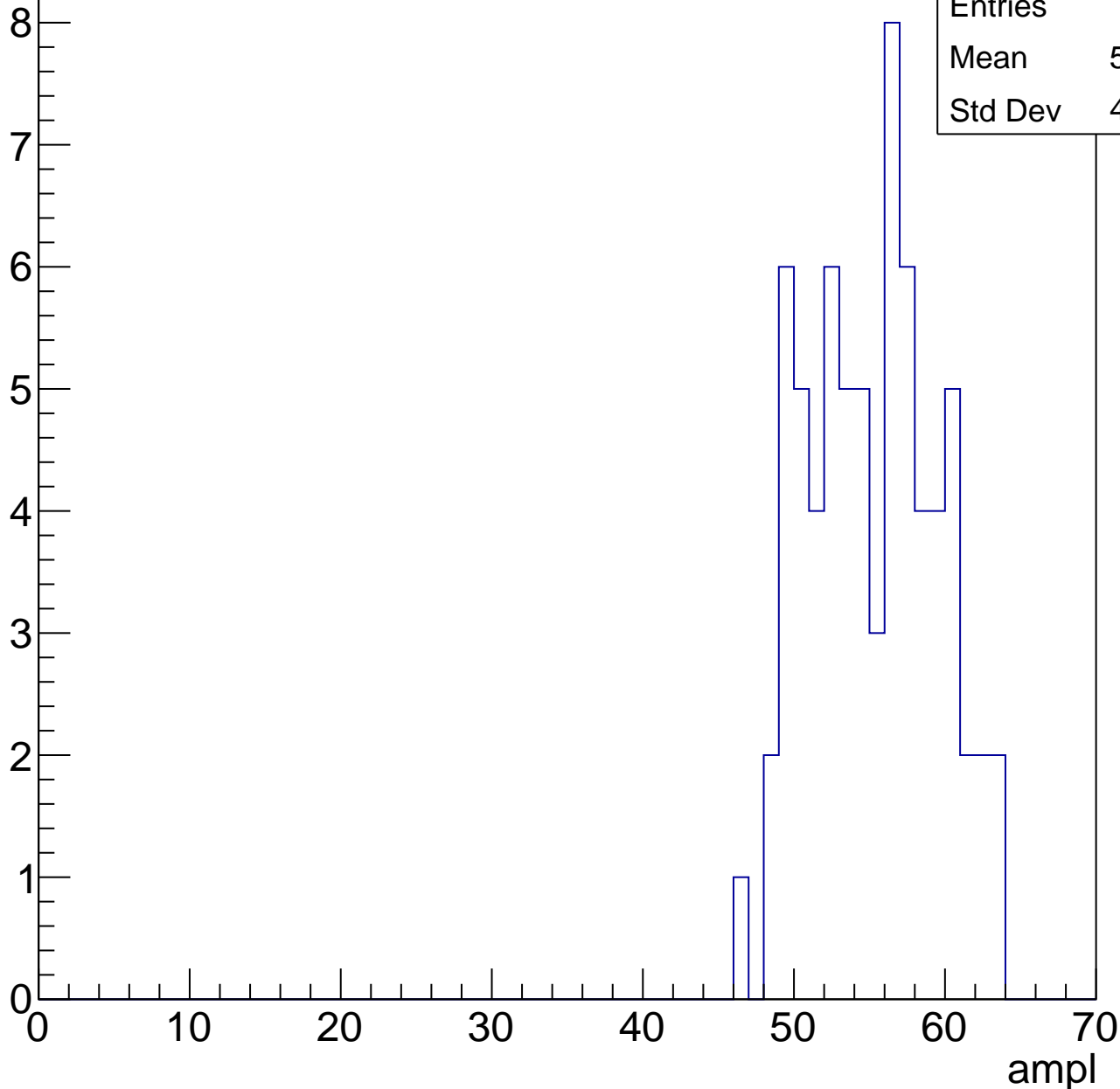


B1L103S, U21-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	54.74
Std Dev	4.163

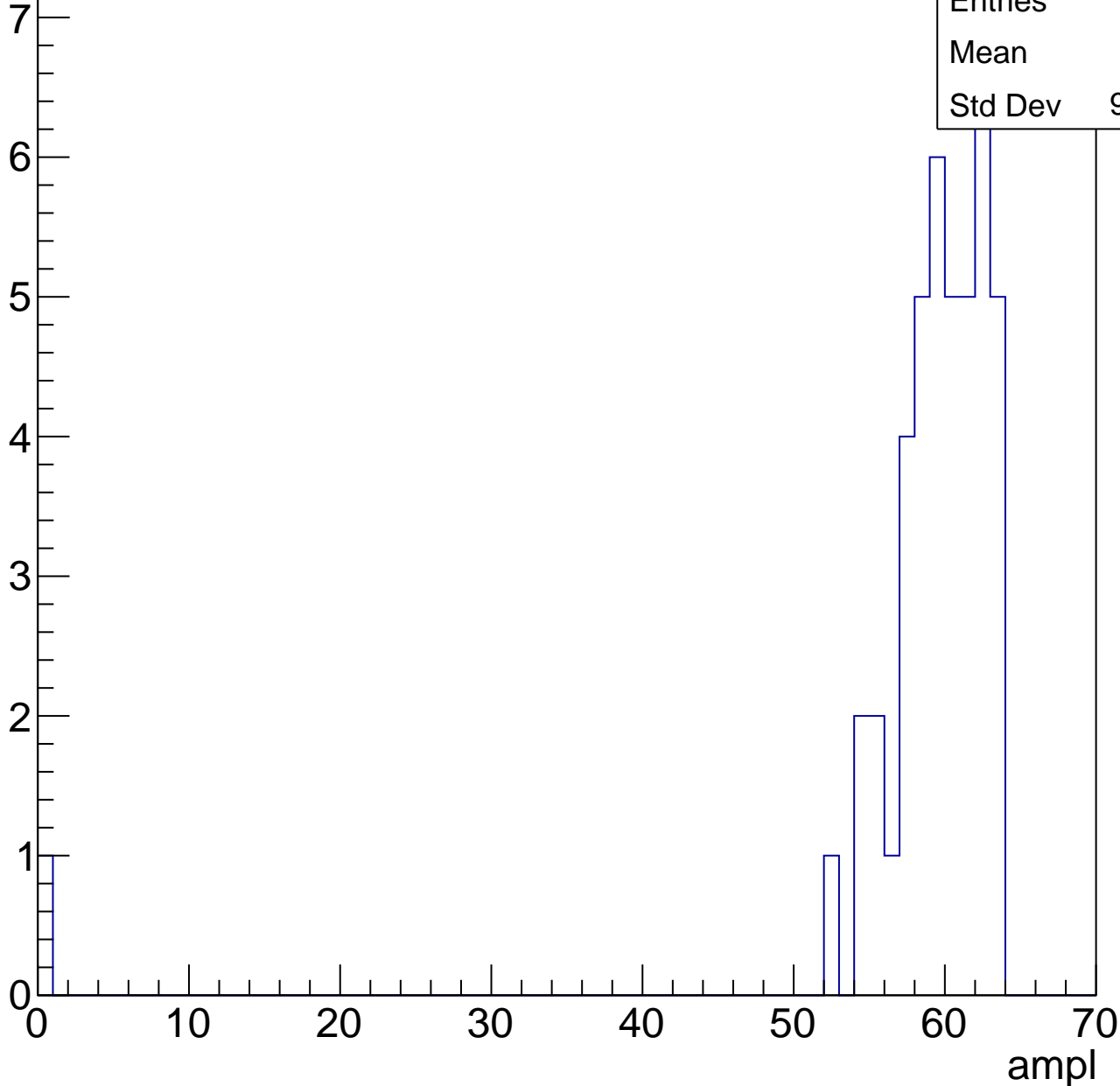


B1L103S, U21-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

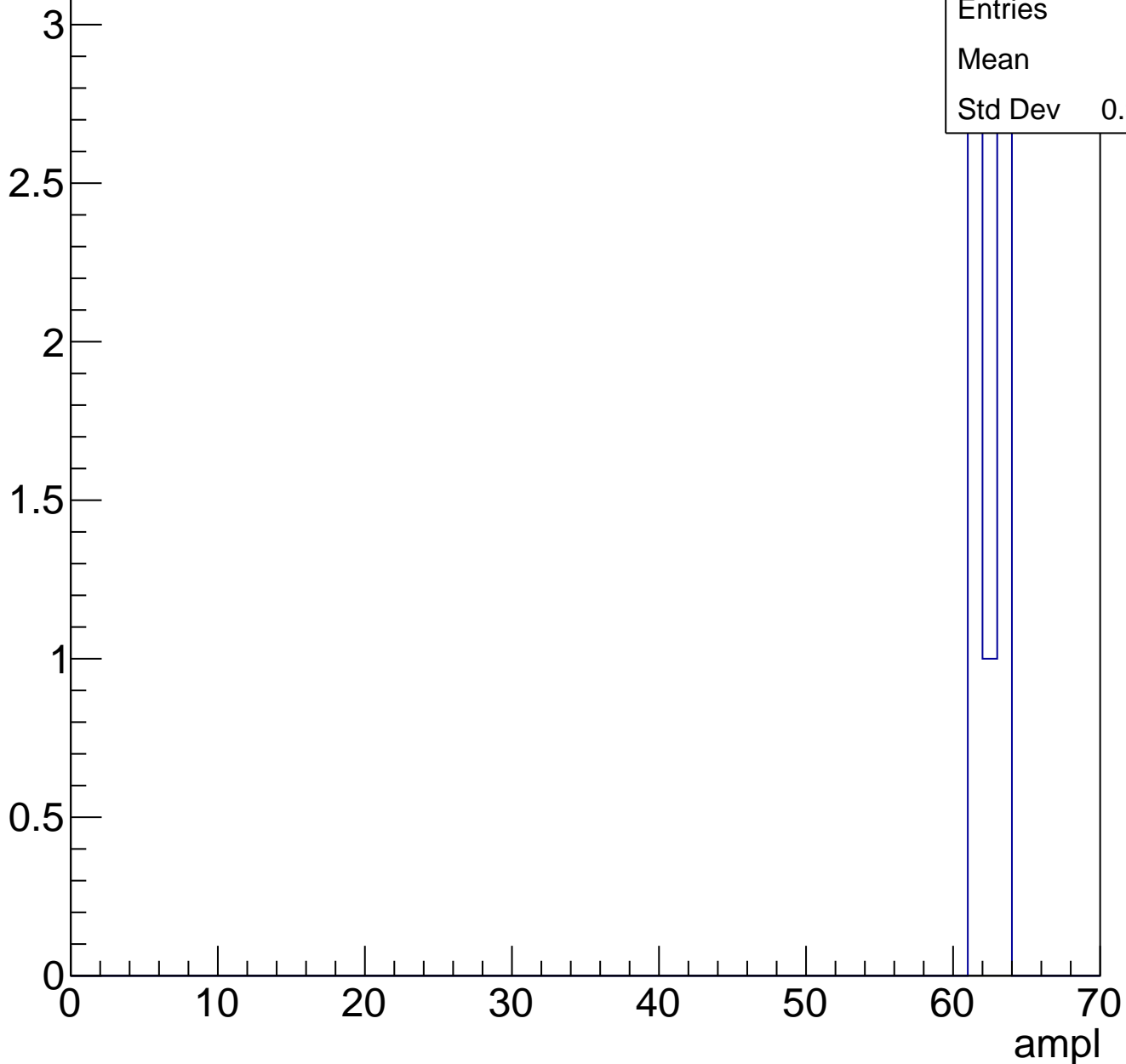
Entries	44
Mean	58
Std Dev	9.252



B1L103S, U21-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch31, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	25.53
Std Dev	11.93

Gaus mean : 30.9540

Gaus Width: 3.3982

Entry

12

10

8

6

4

2

0

0

10

20

30

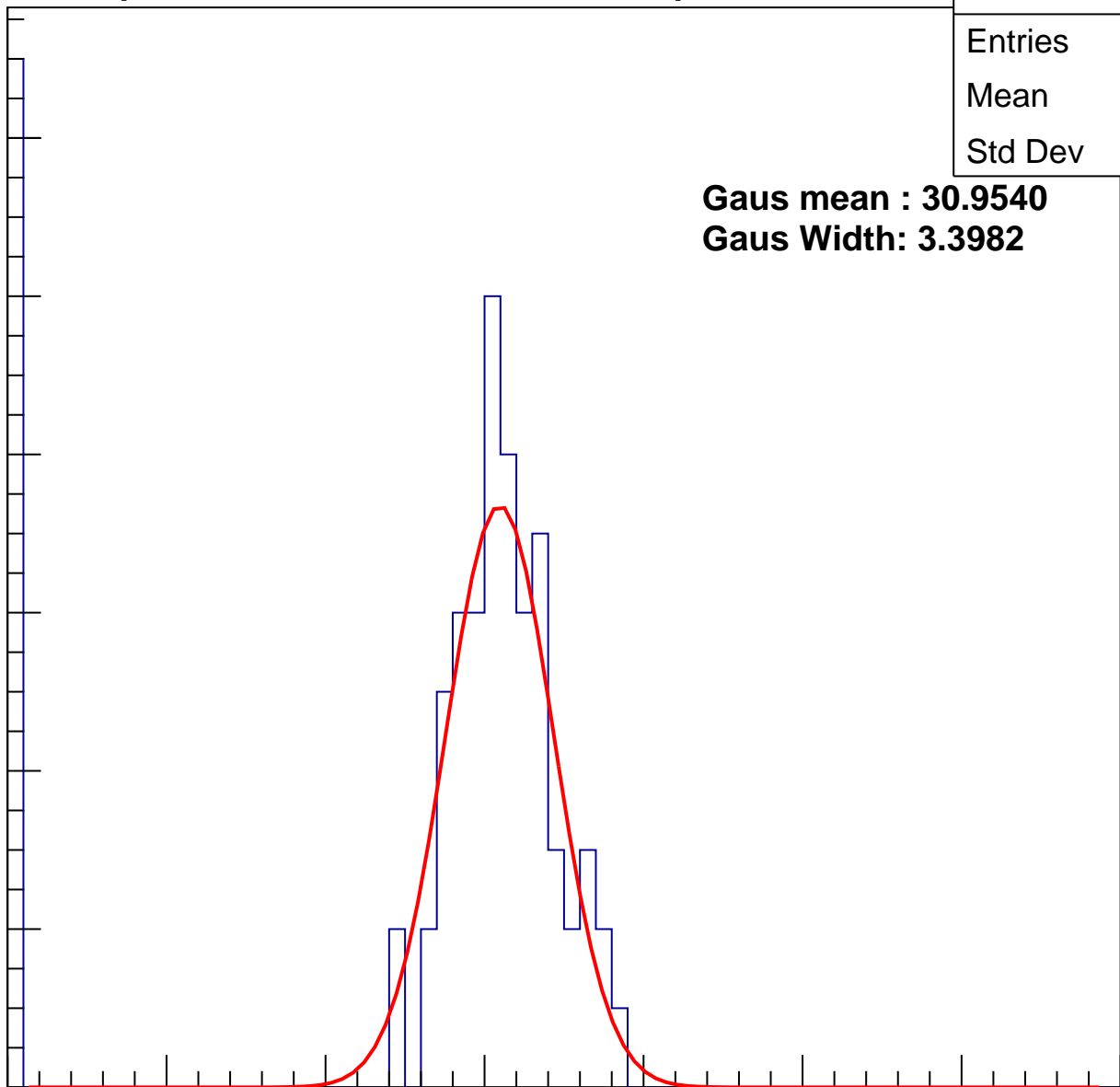
40

50

60

70

ampl



B1L103S, U21-ch31, adc1

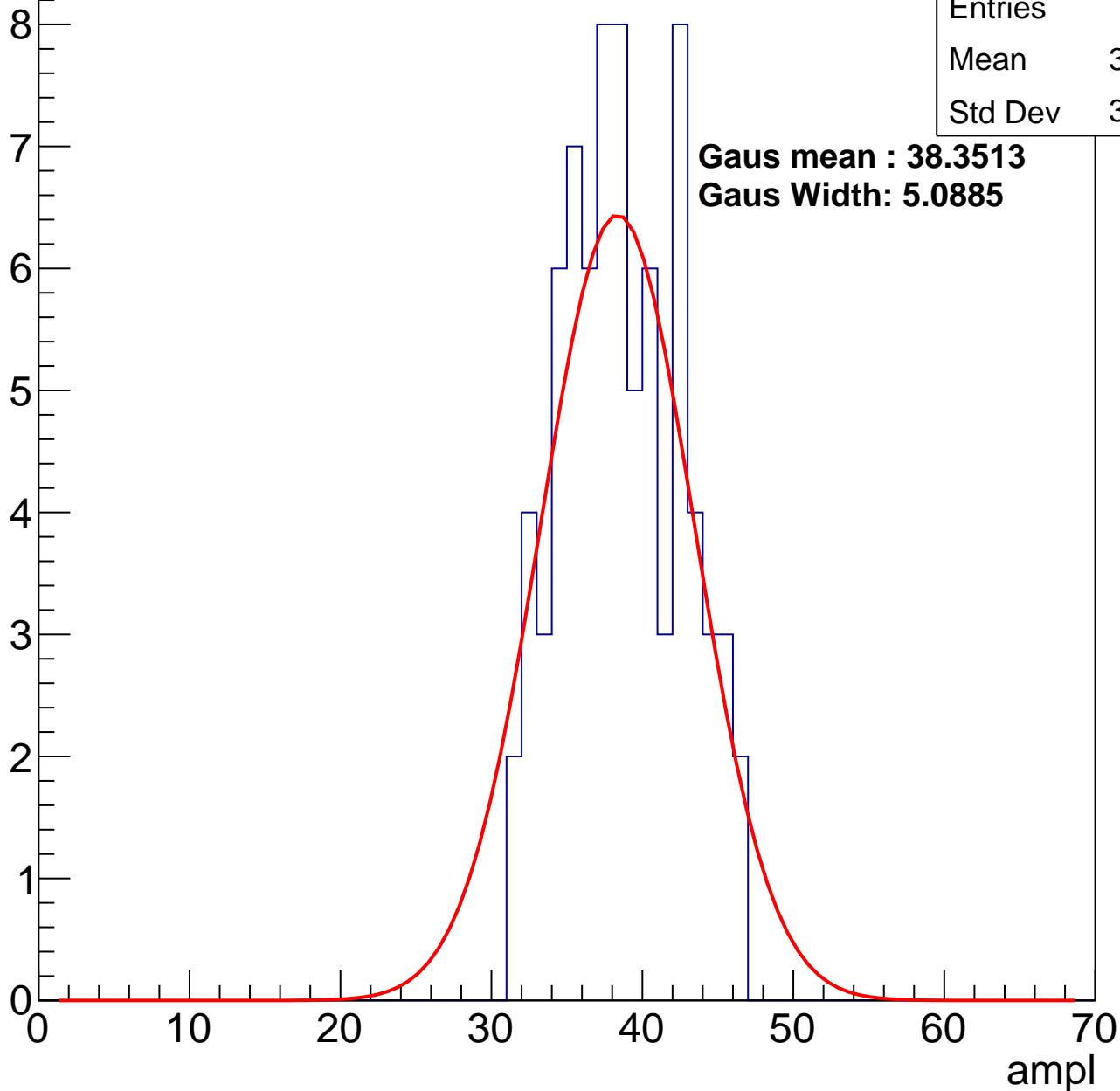
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	38.19
Std Dev	3.876

Gaus mean : 38.3513

Gaus Width: 5.0885



B1L103S, U21-ch31, adc2

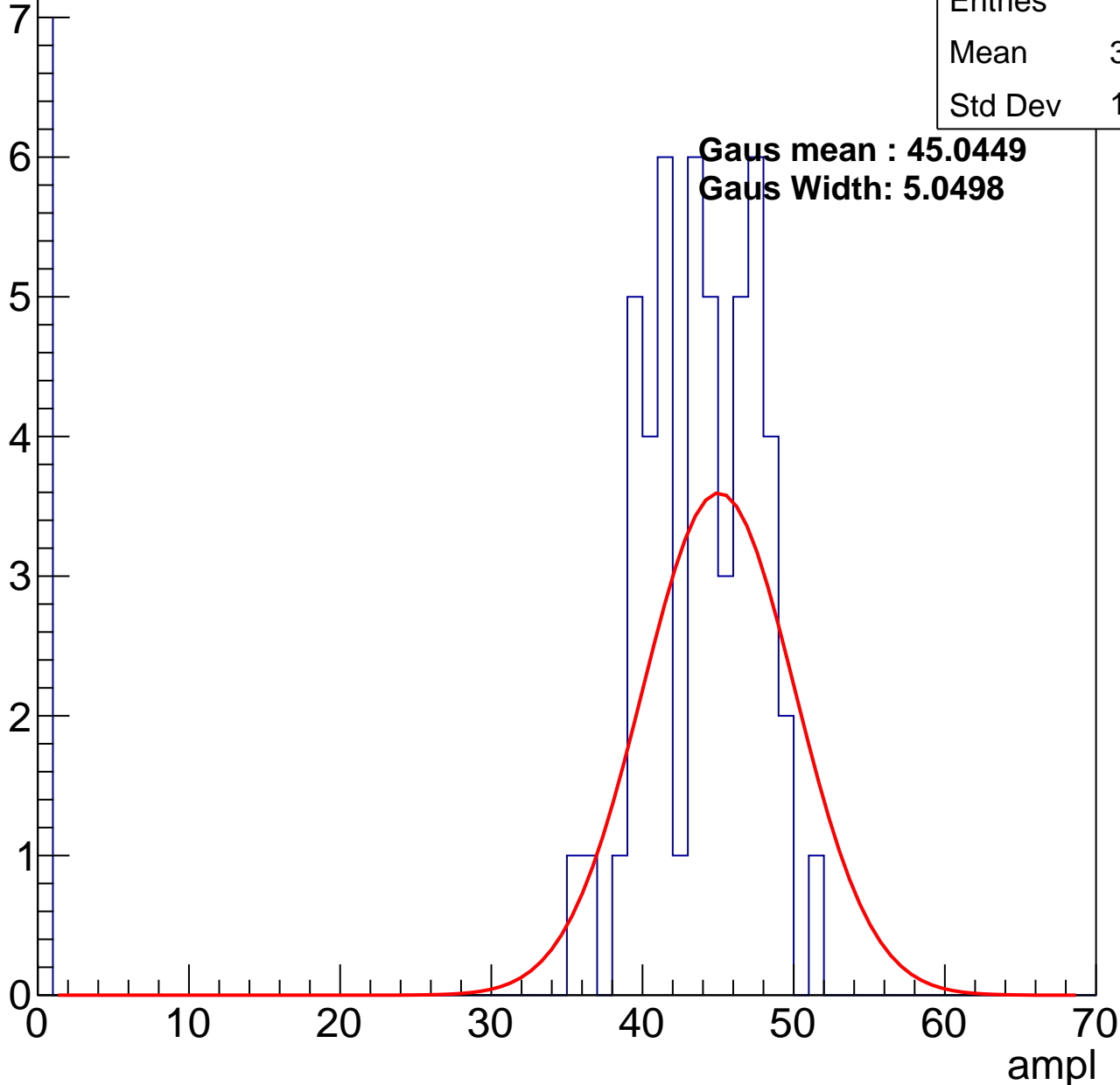
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	38.24
Std Dev	14.56

Gaus mean : 45.0449

Gaus Width: 5.0498



B1L103S, U21-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

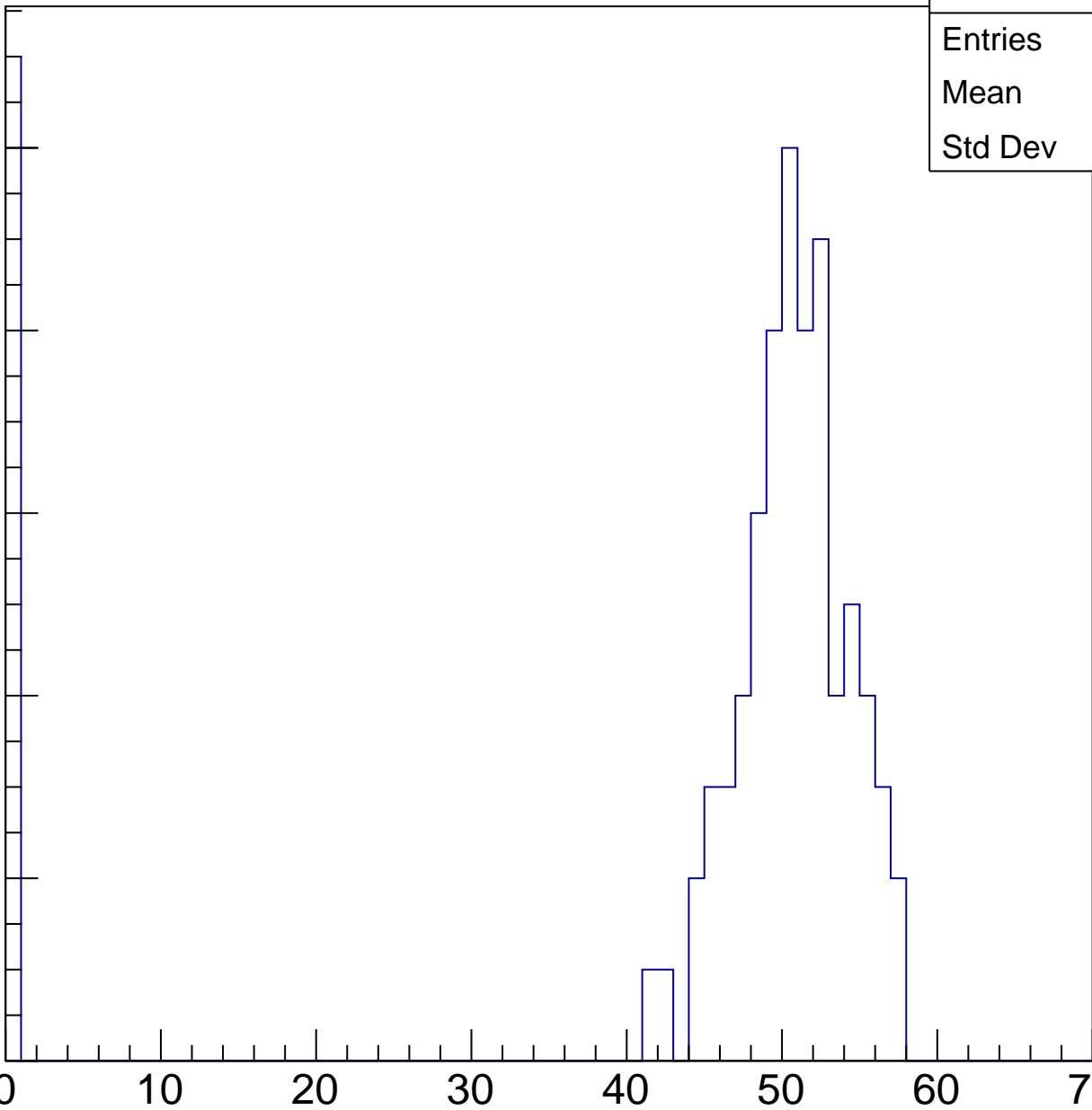
4

2

0

Entries	84
Mean	43.71
Std Dev	17.27

ampl

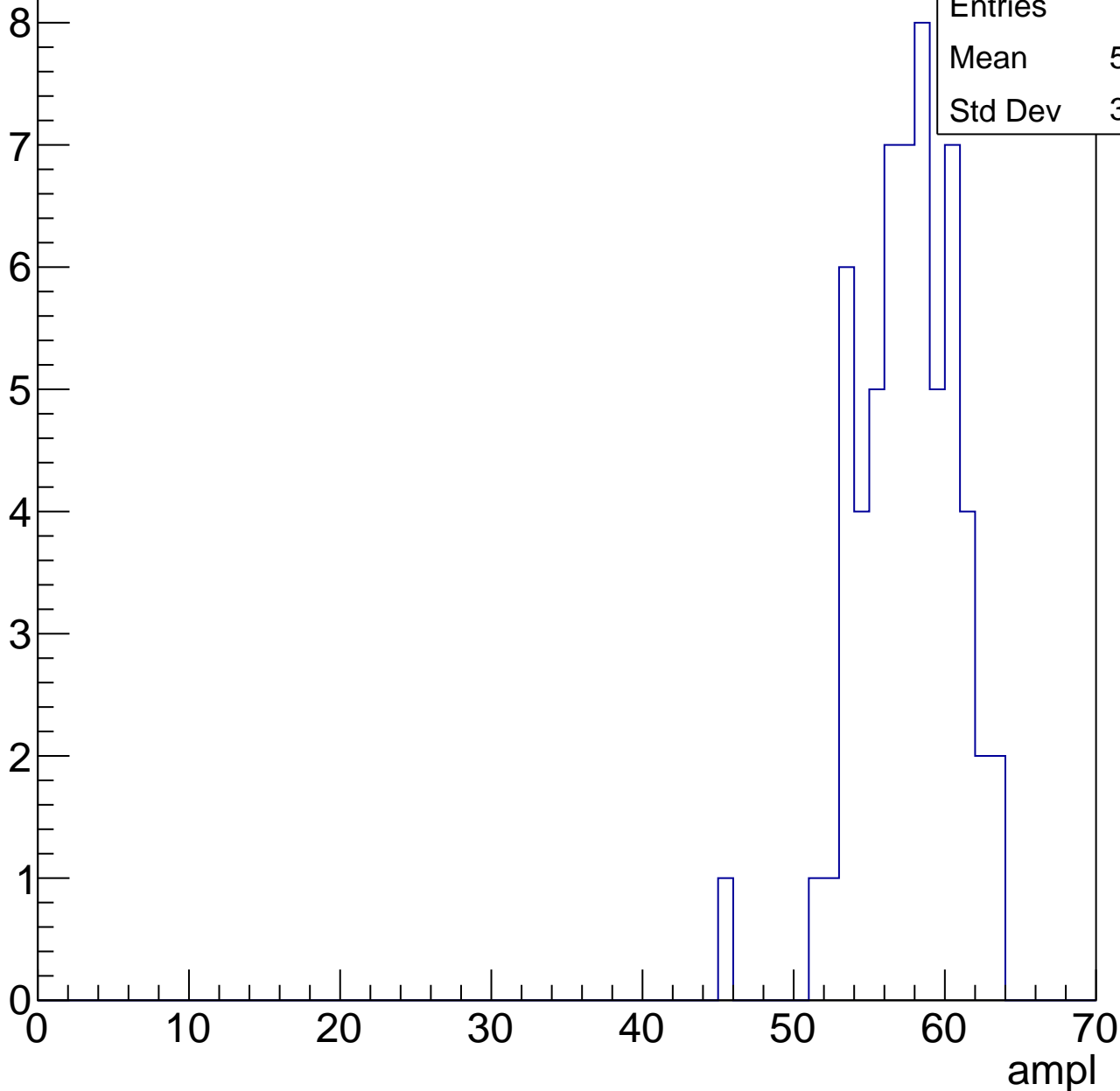


B1L103S, U21-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.02
Std Dev	3.268

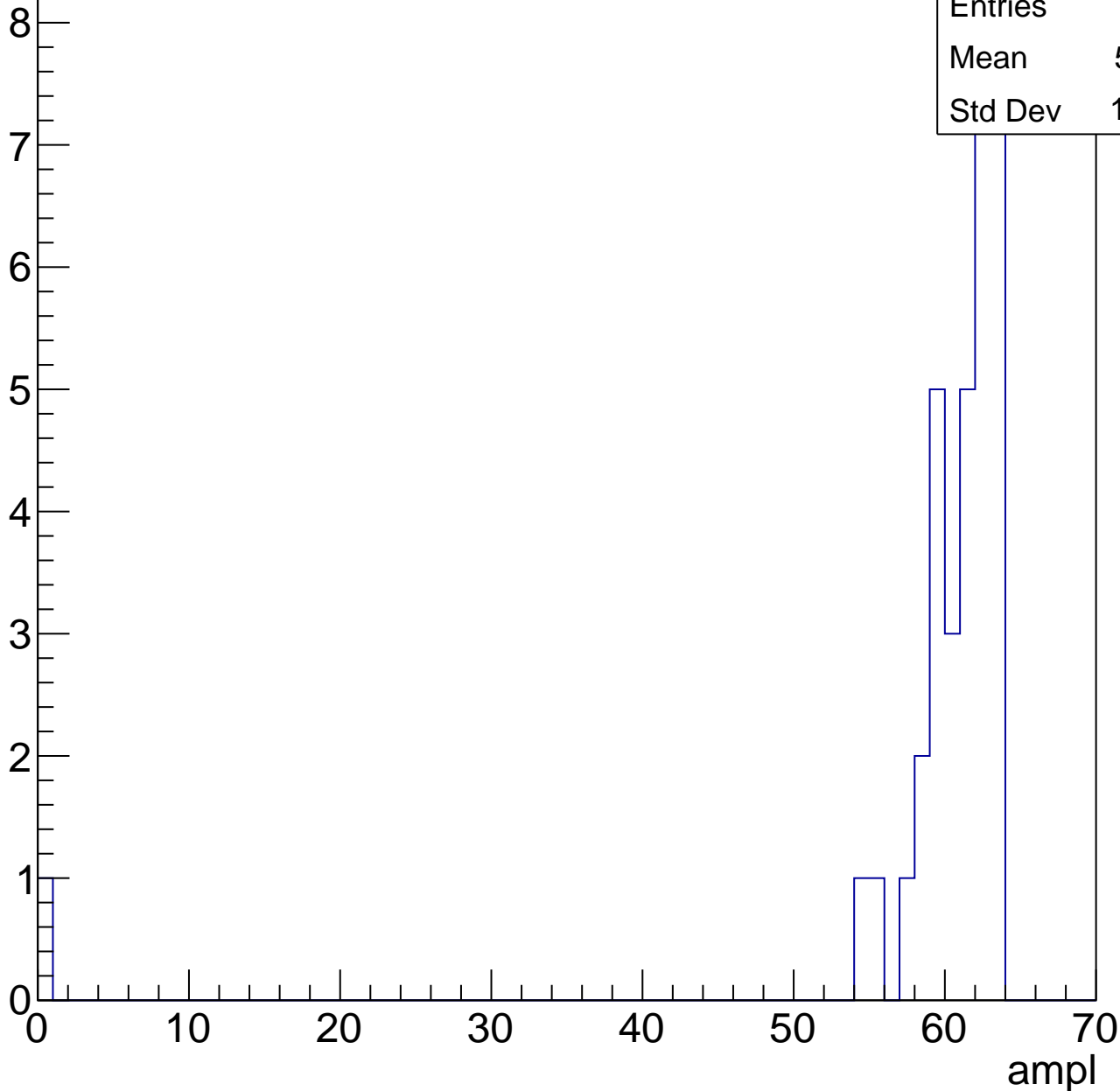


B1L103S, U21-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	58.91
Std Dev	10.35



B1L103S, U21-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch32, adc0

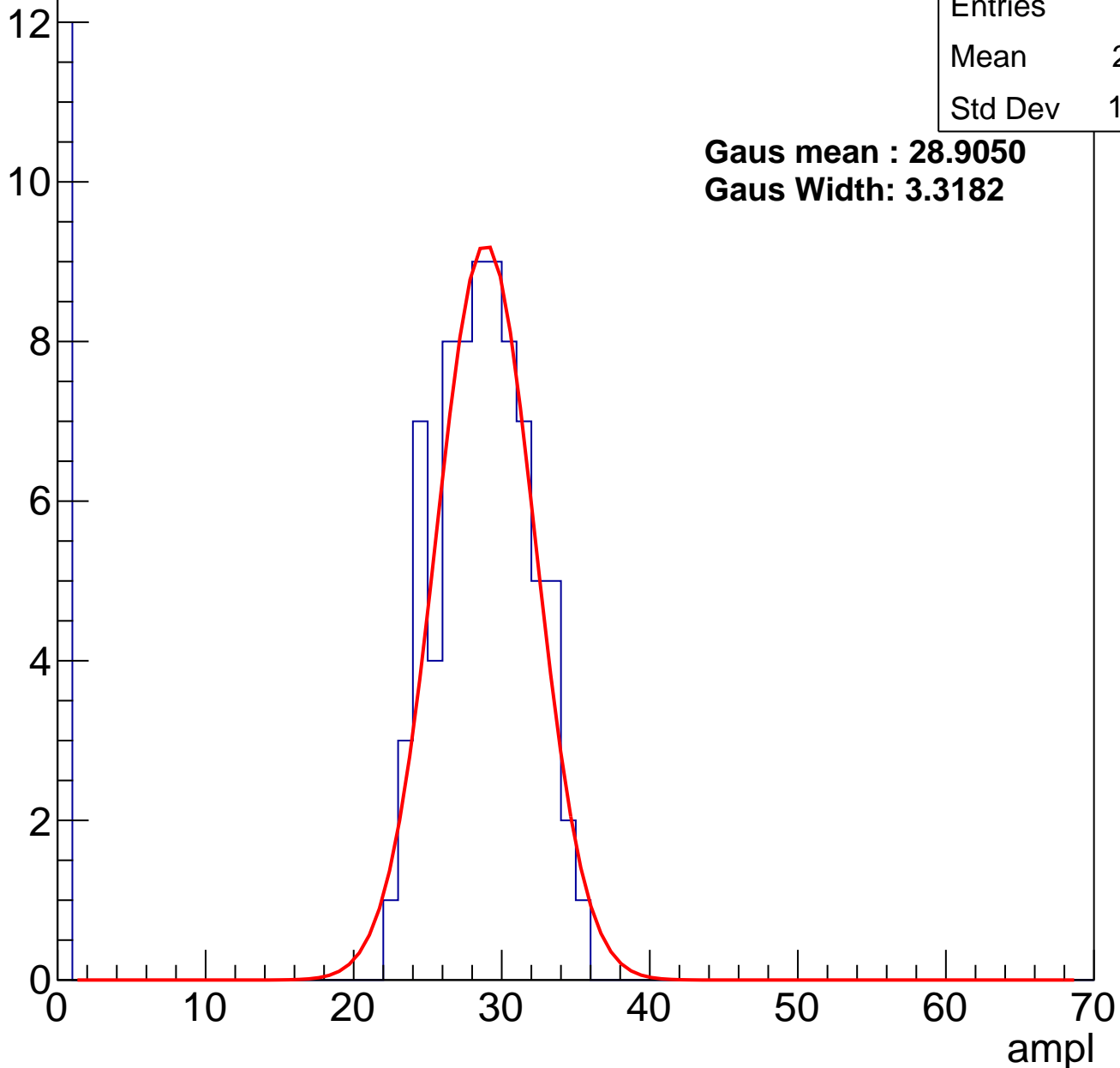
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	24.51
Std Dev	10.08

Gaus mean : 28.9050

Gaus Width: 3.3182

Entry



B1L103S, U21-ch32, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	35.52
Std Dev	5.41

Gaus mean : 36.3048

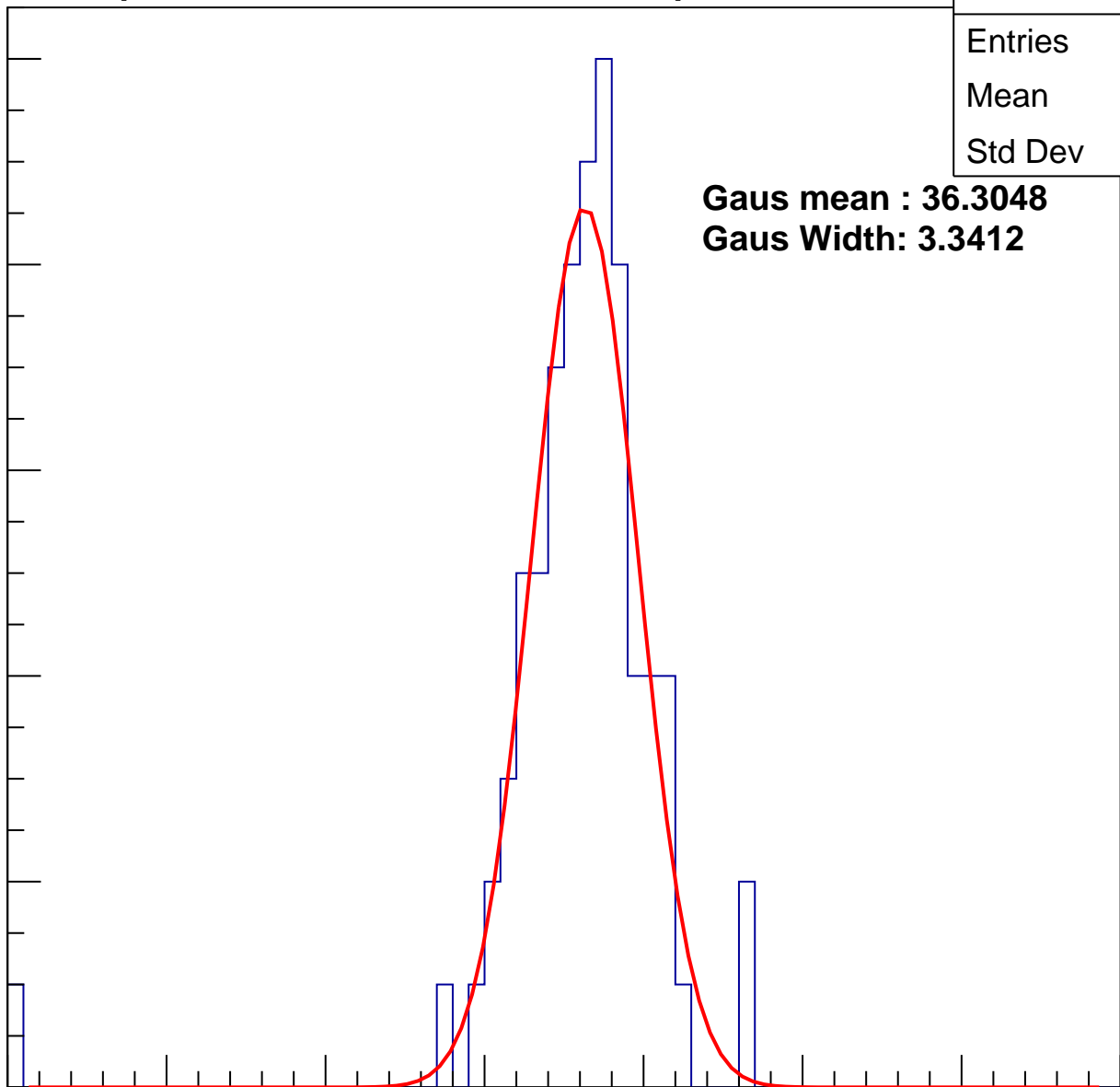
Gaus Width: 3.3412

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch32, adc2

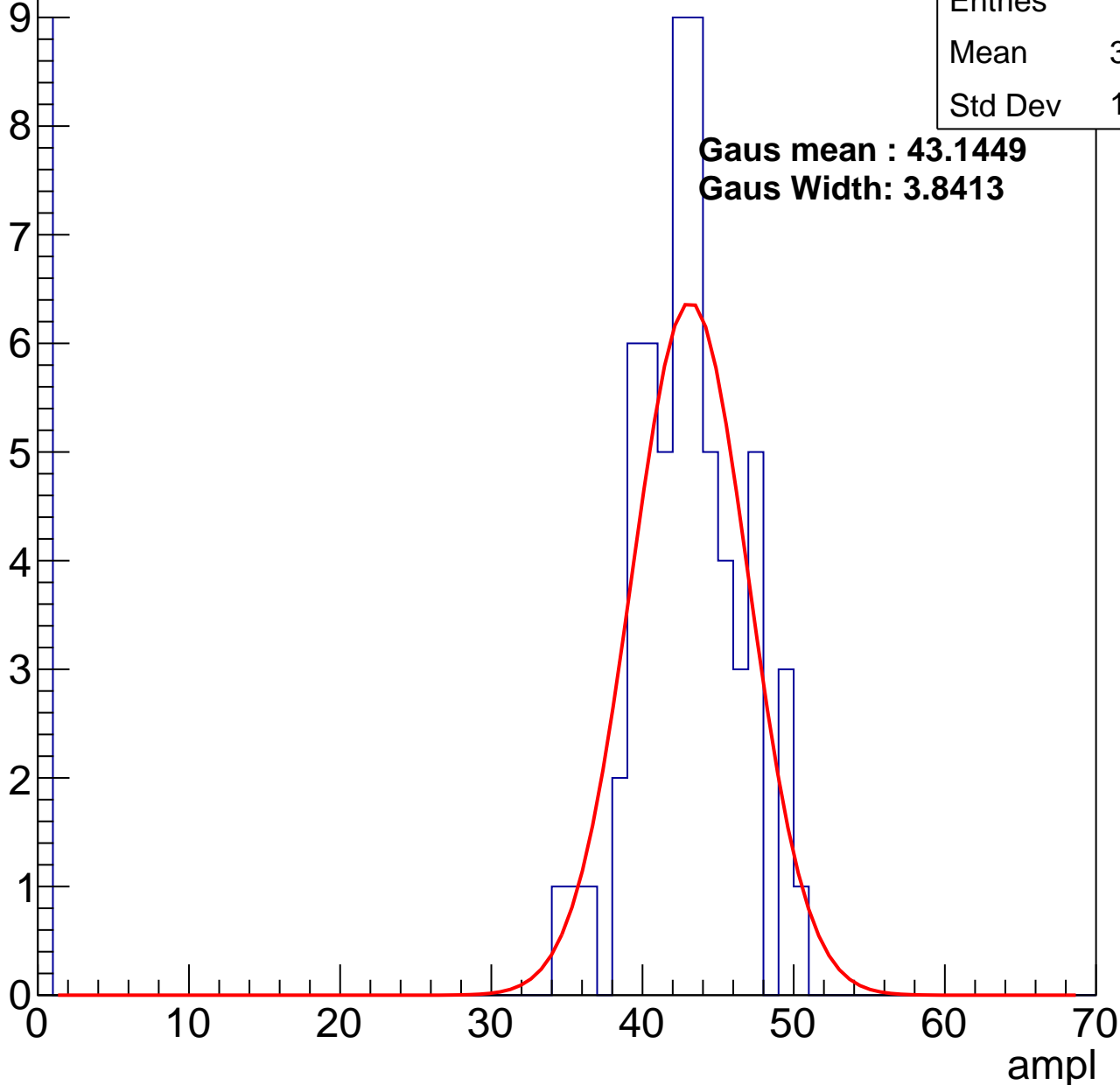
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	37.07
Std Dev	14.58

Gaus mean : 43.1449

Gaus Width: 3.8413



B1L103S, U21-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

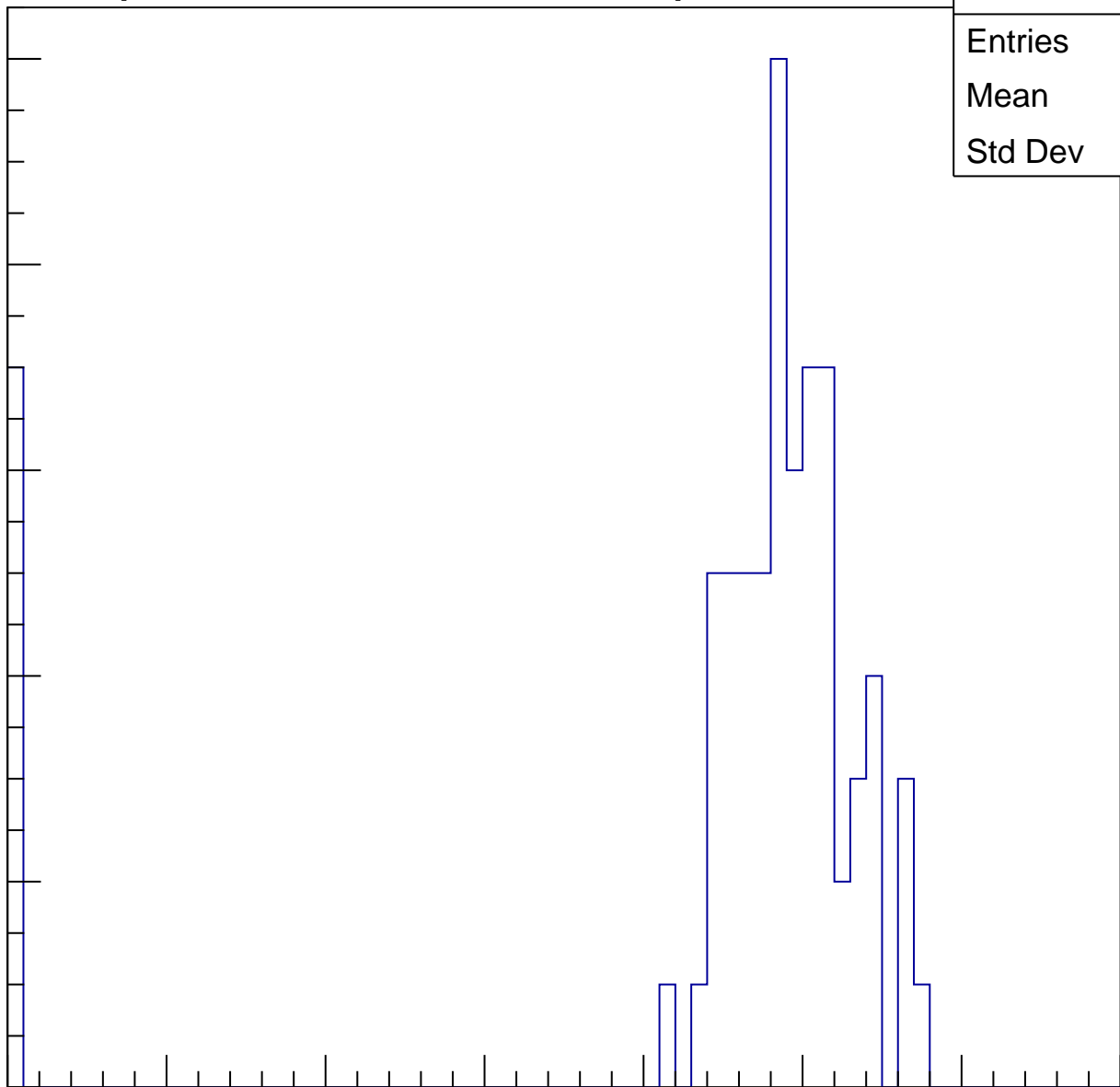
Entries	72
Mean	44.15
Std Dev	14.86

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

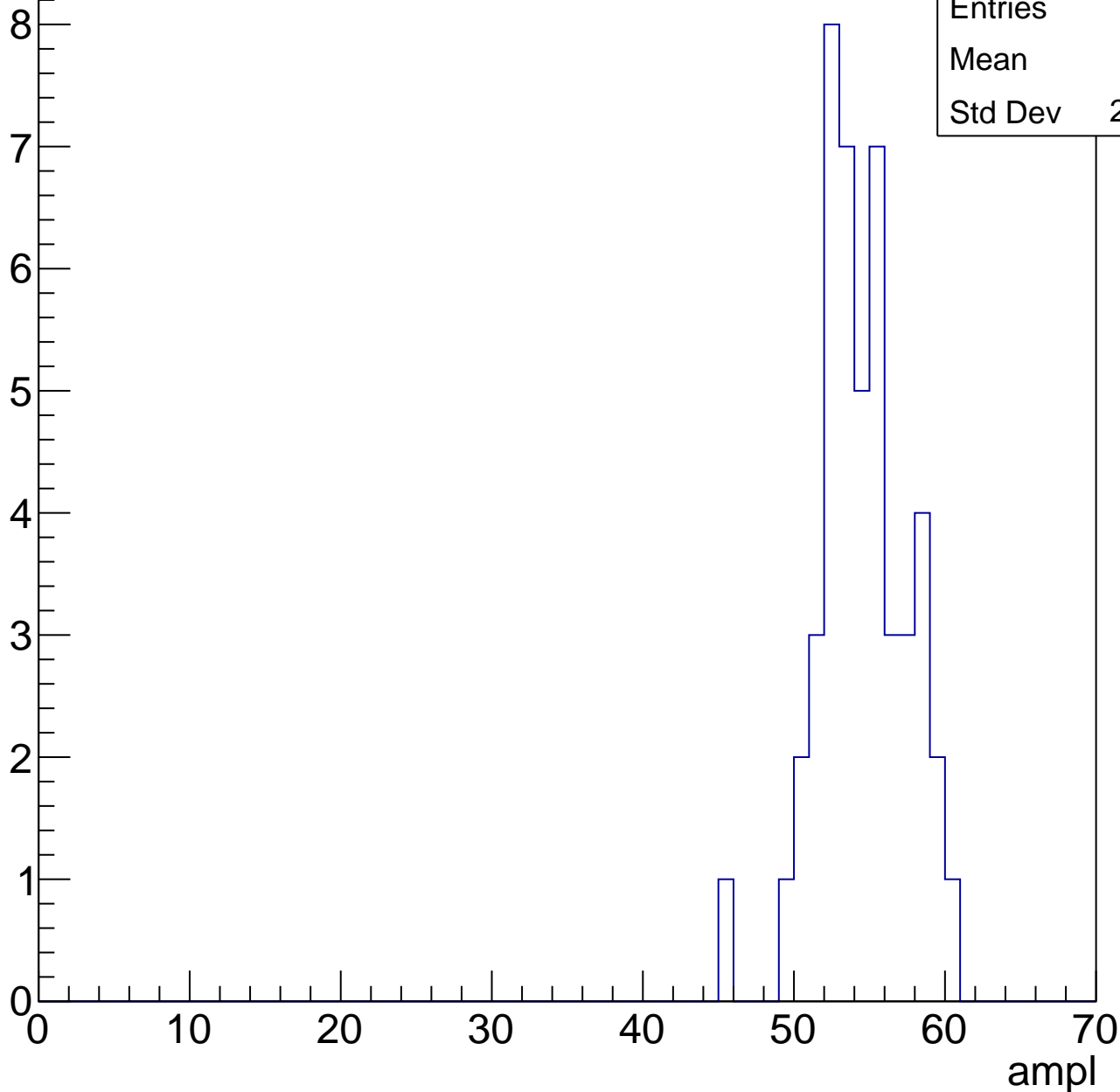


B1L103S, U21-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	54
Std Dev	2.917



B1L103S, U21-ch32, adc5

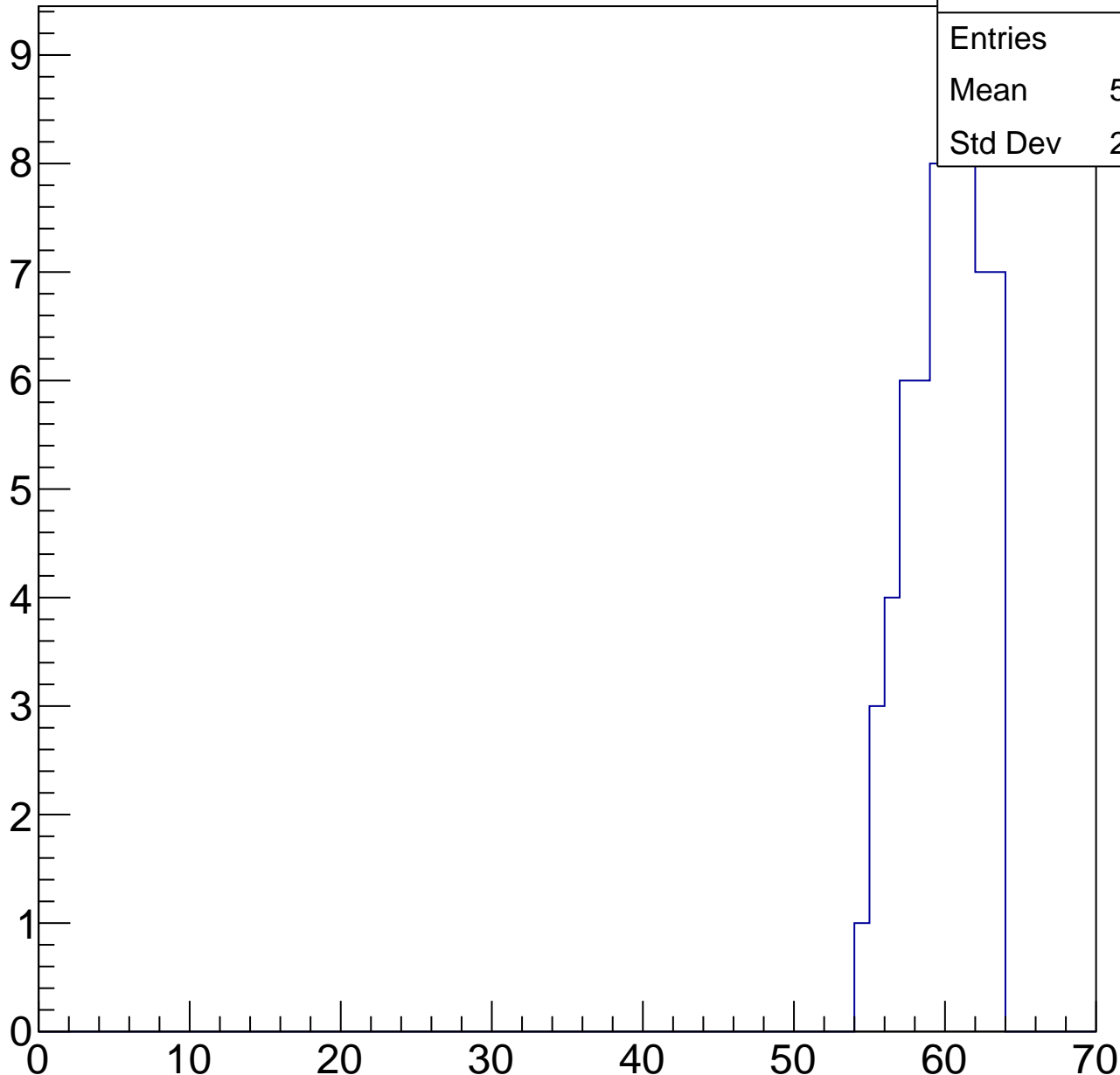
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	59.47
Std Dev	2.417

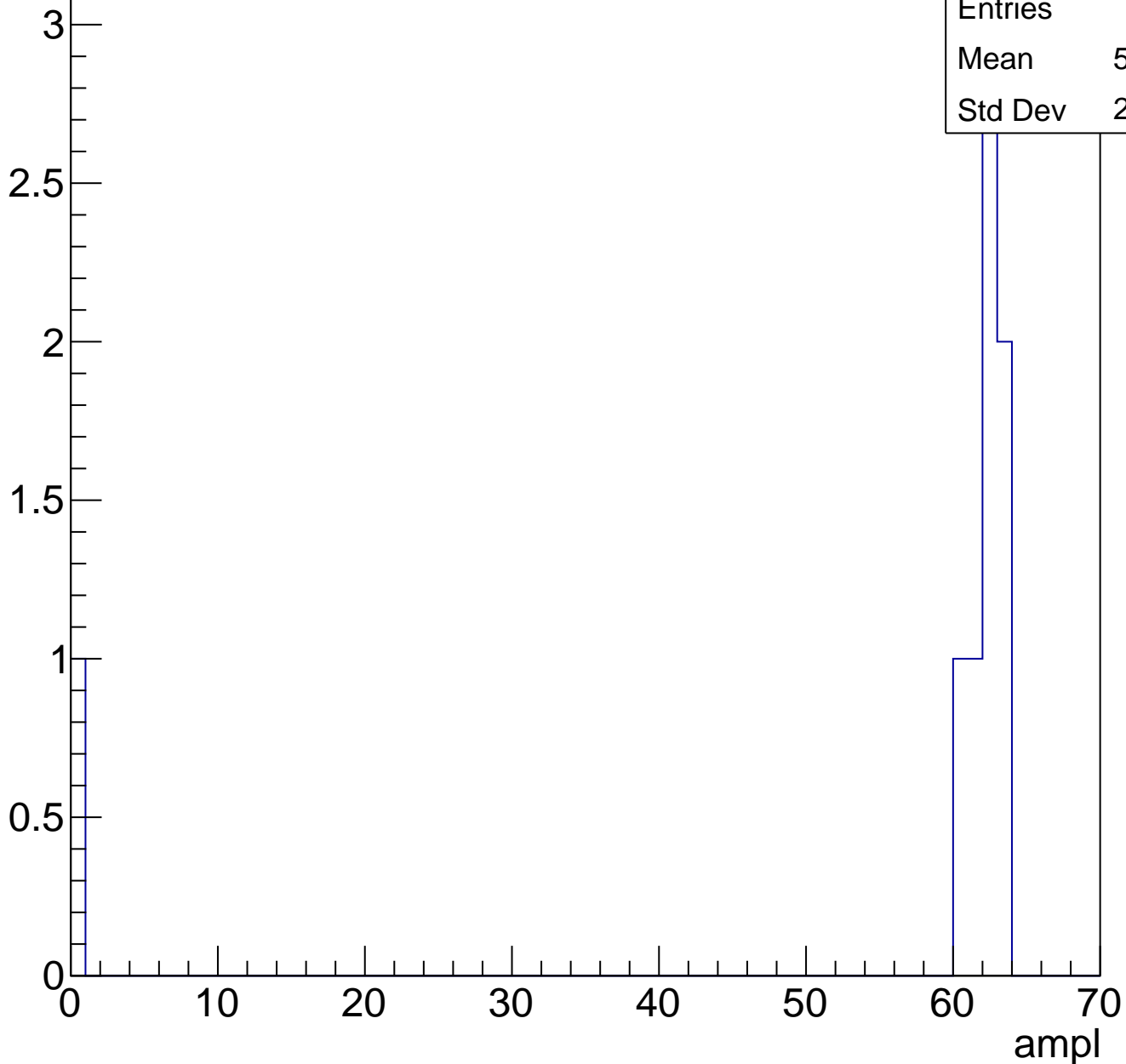
ampl



B1L103S, U21-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

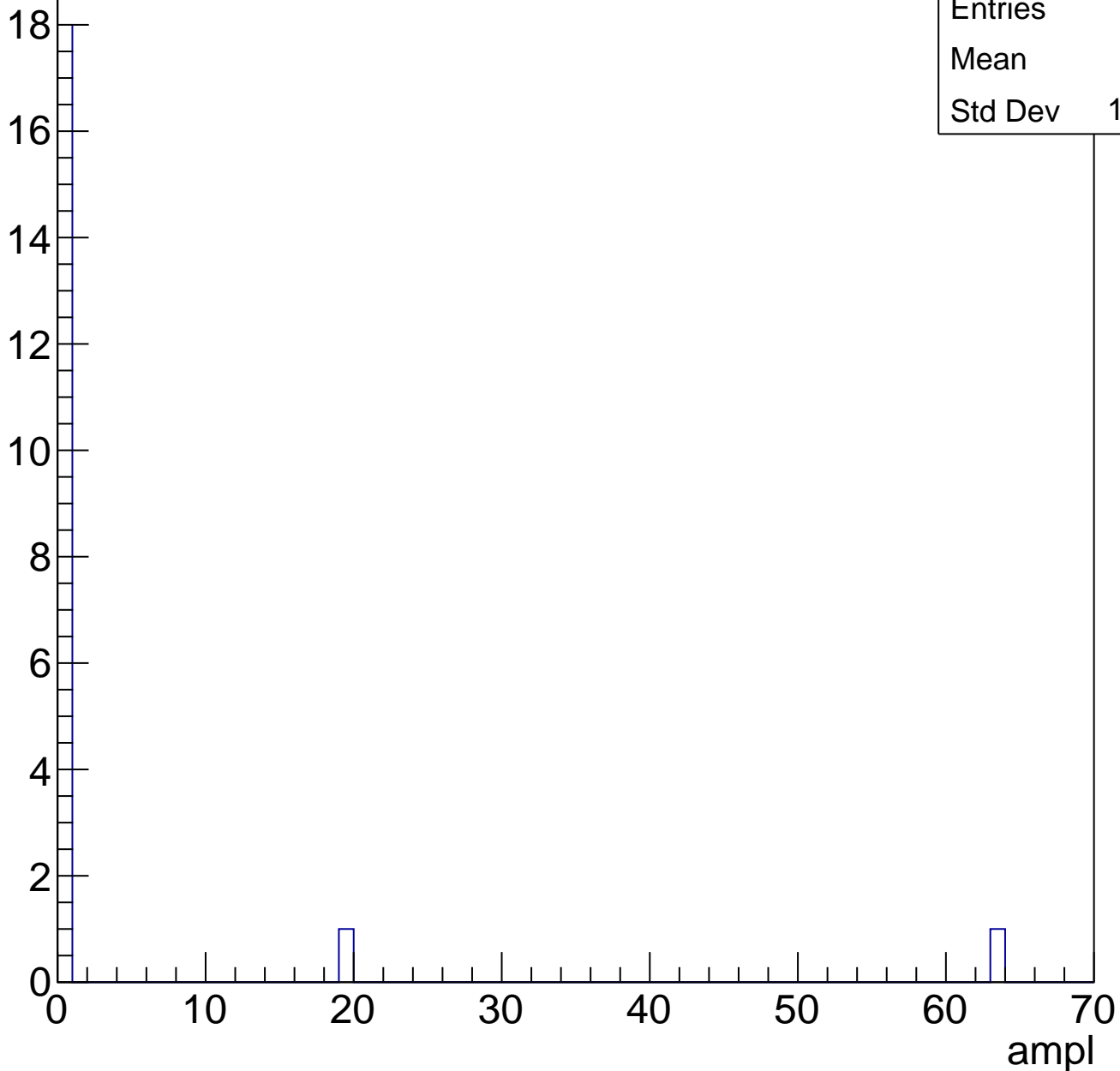


B1L103S, U21-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.1
Std Dev	14.13

Entry



B1L103S, U21-ch33, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	26.35
Std Dev	8.669

Gaus mean : 29.3564

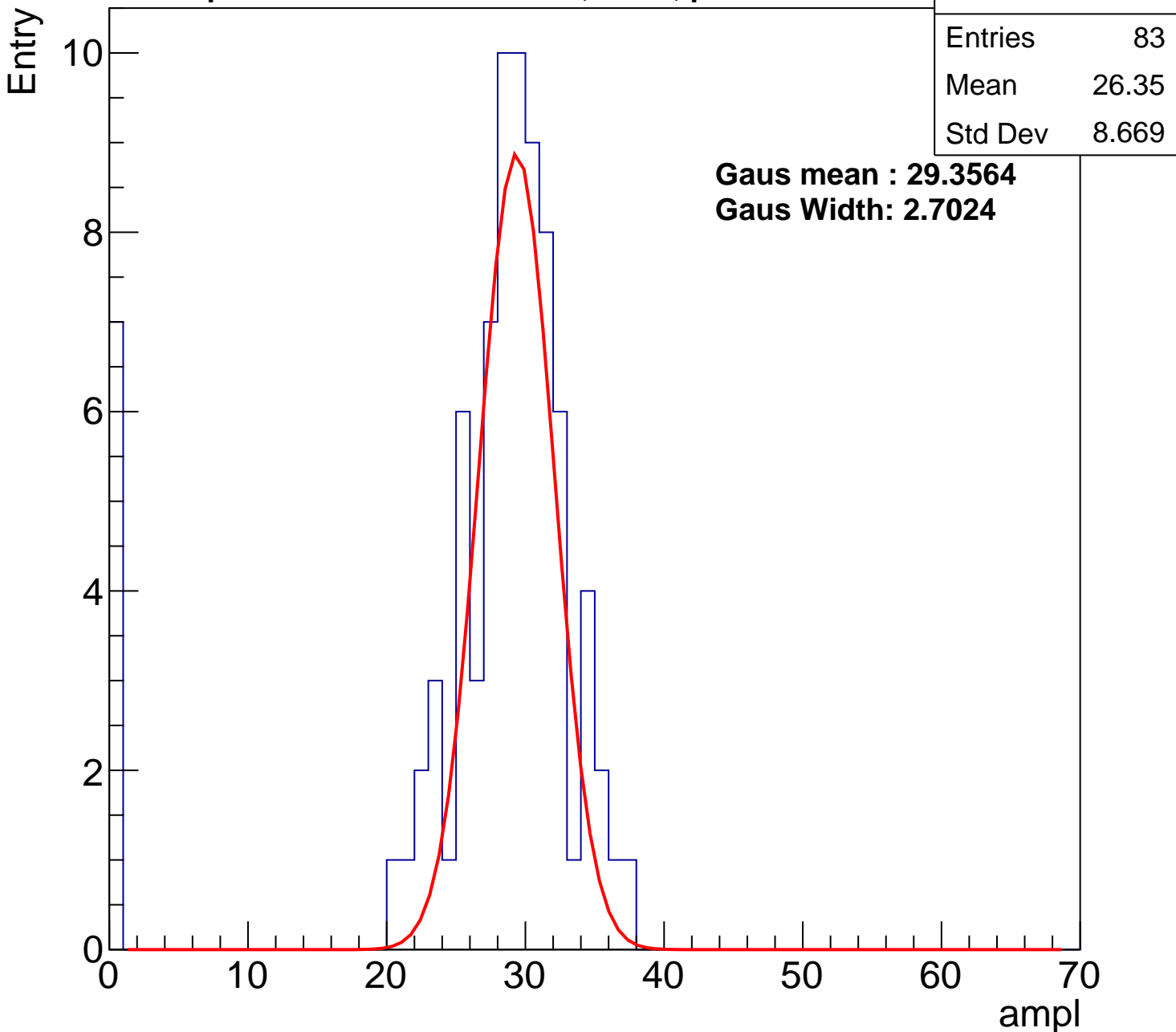
Gaus Width: 2.7024

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch33, adc1

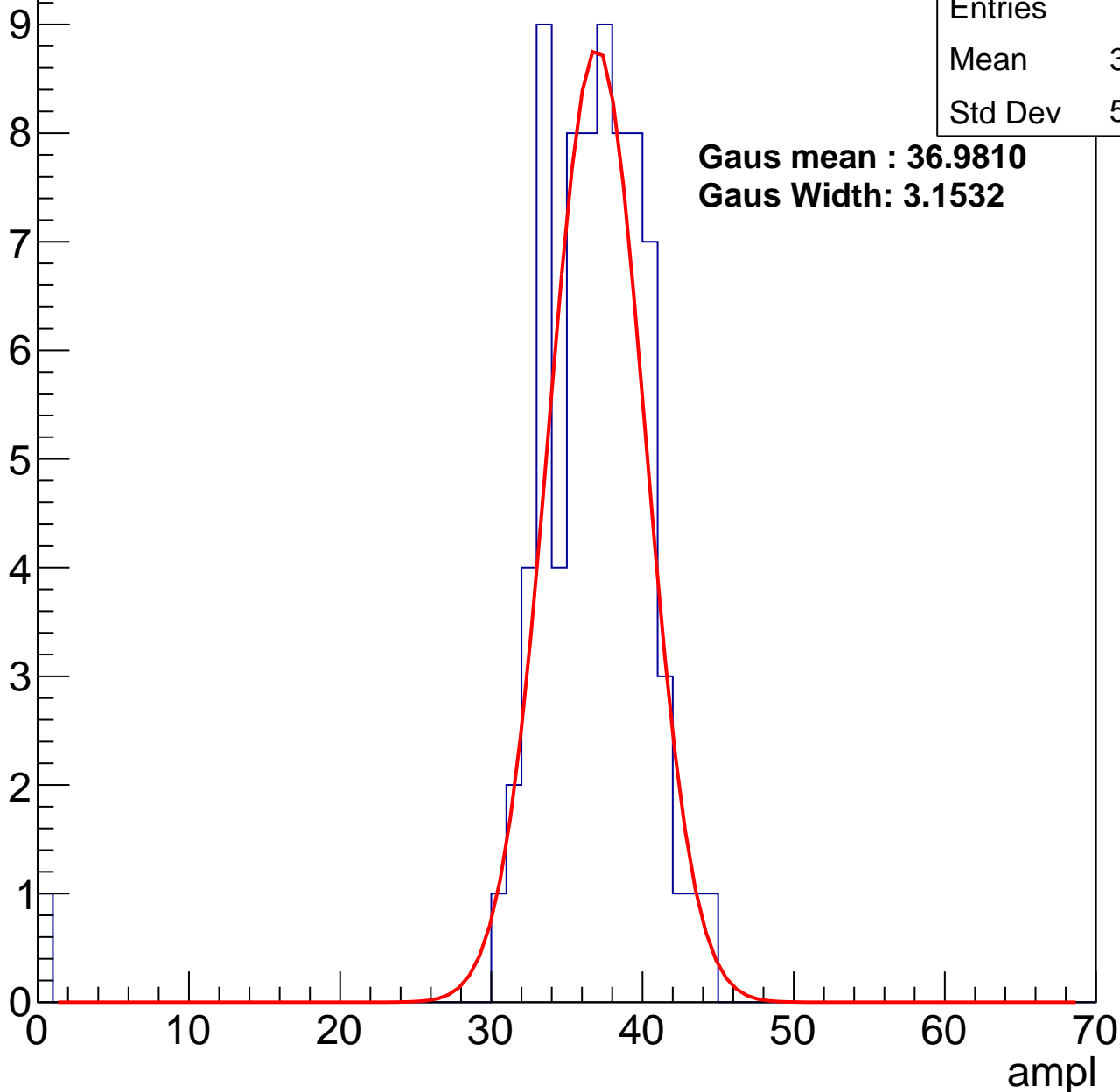
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	36.03
Std Dev	5.164

Gaus mean : 36.9810

Gaus Width: 3.1532



B1L103S, U21-ch33, adc2

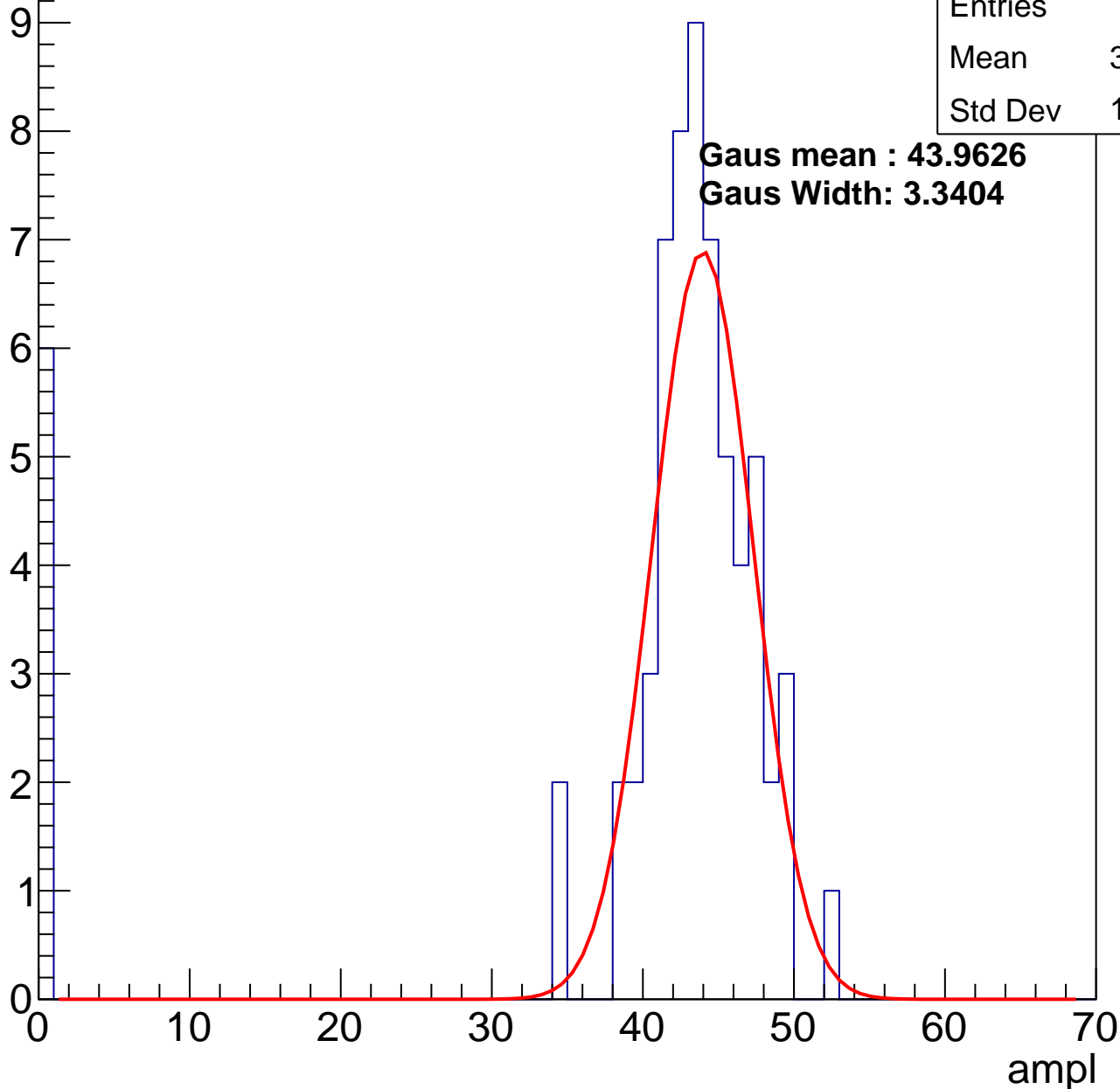
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	39.38
Std Dev	12.86

Gaus mean : 43.9626

Gaus Width: 3.3404

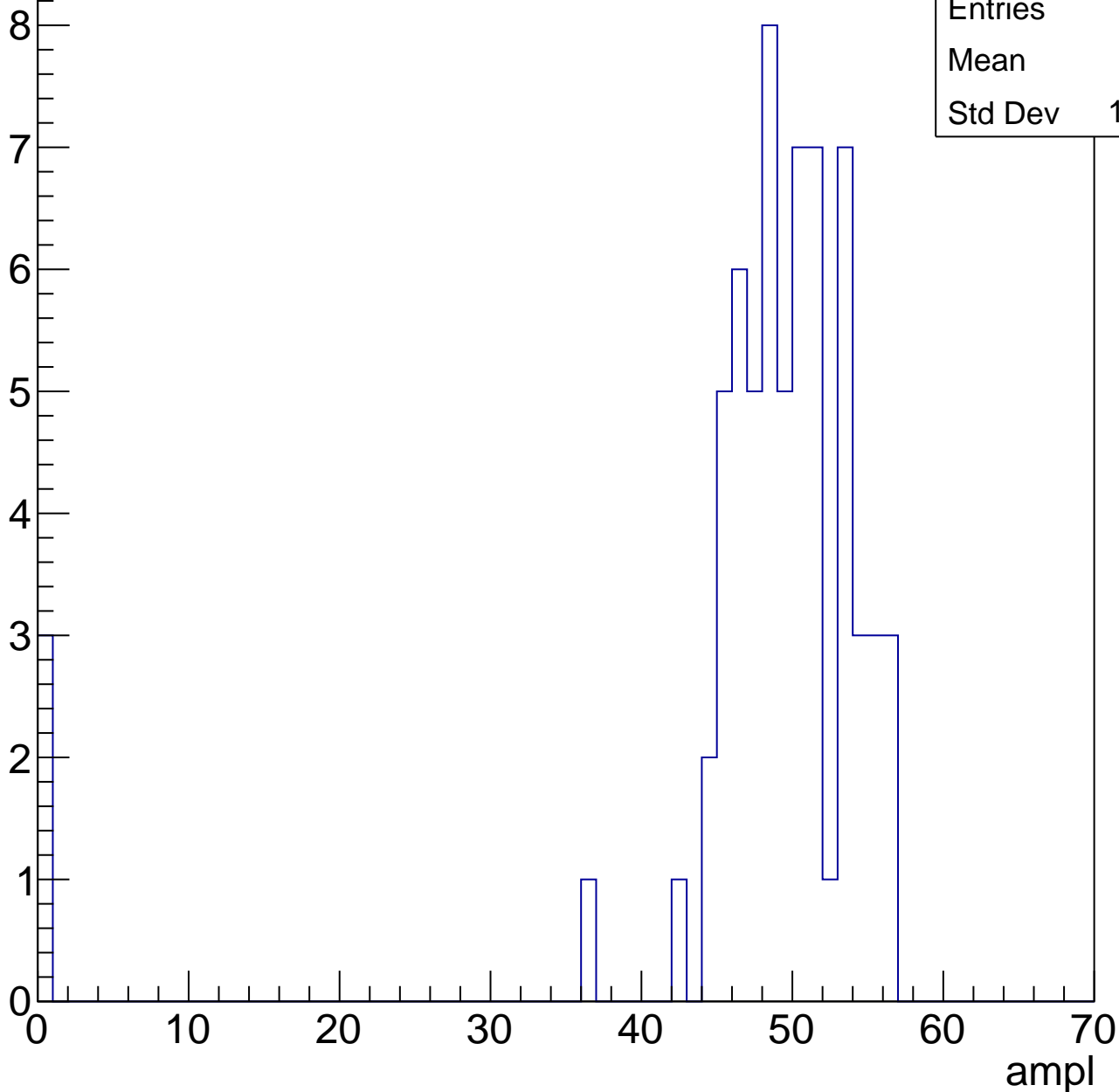


B1L103S, U21-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.1
Std Dev	10.84

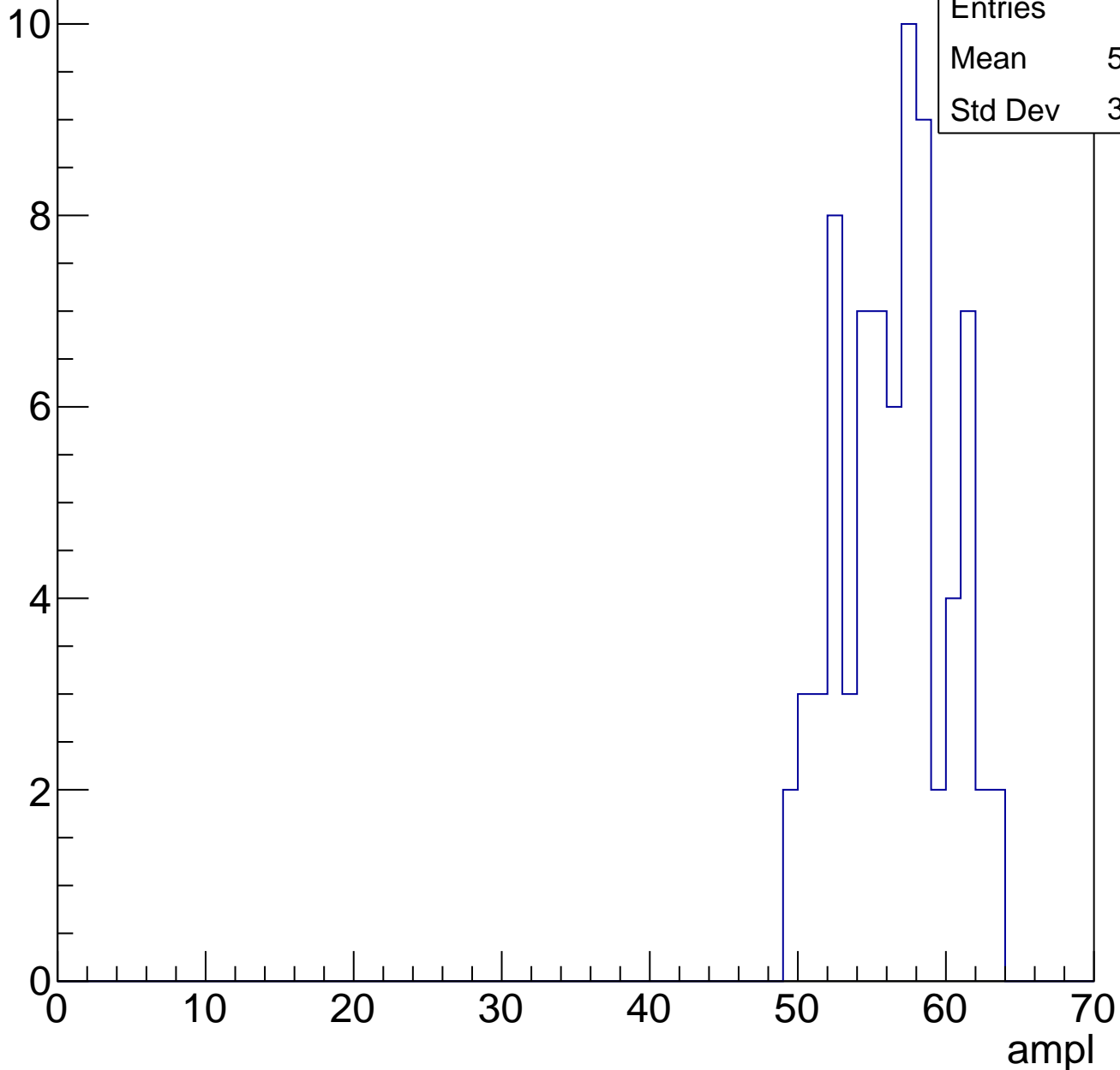


B1L103S, U21-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	56.03
Std Dev	3.548

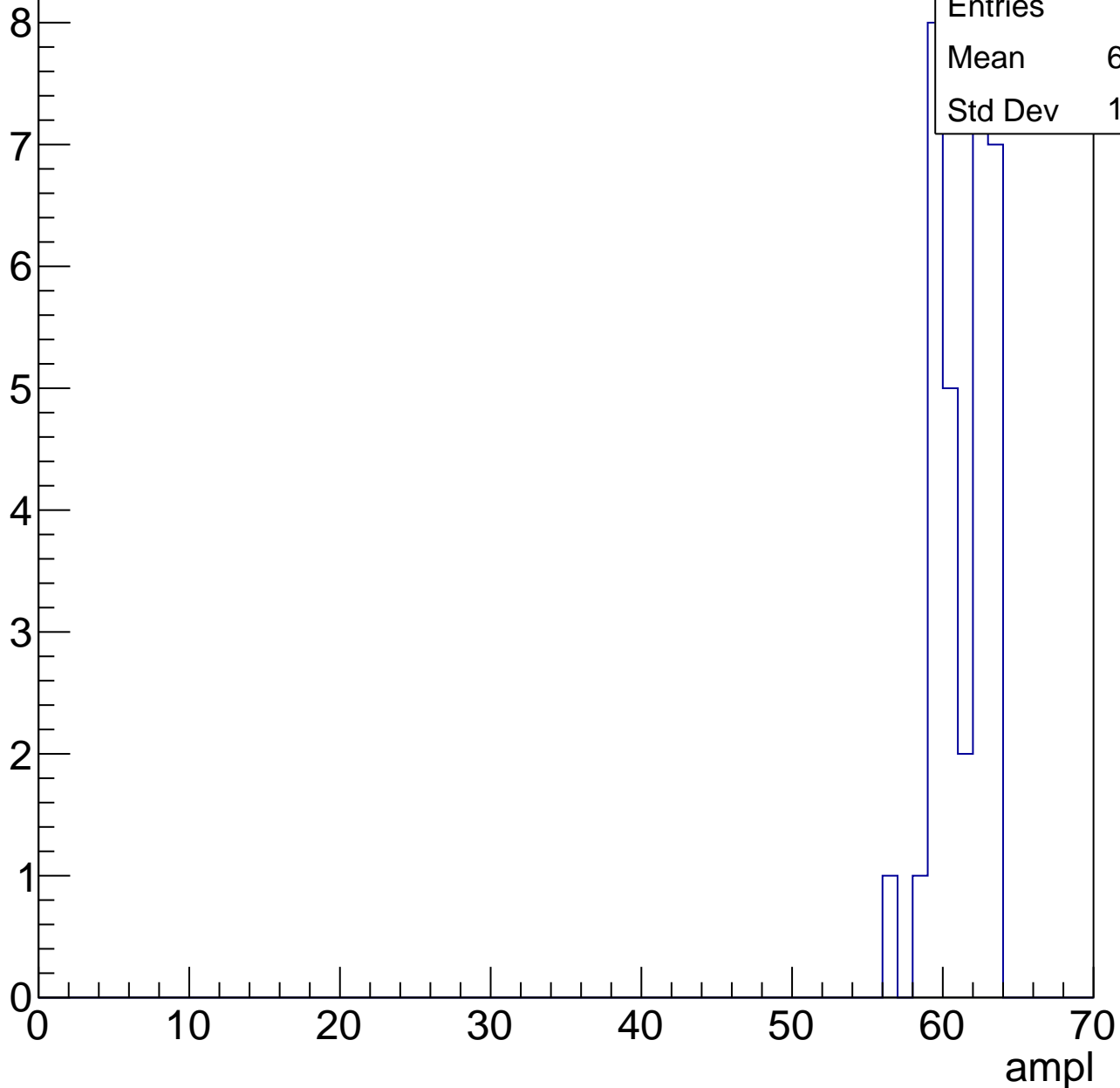
Entry



B1L103S, U21-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

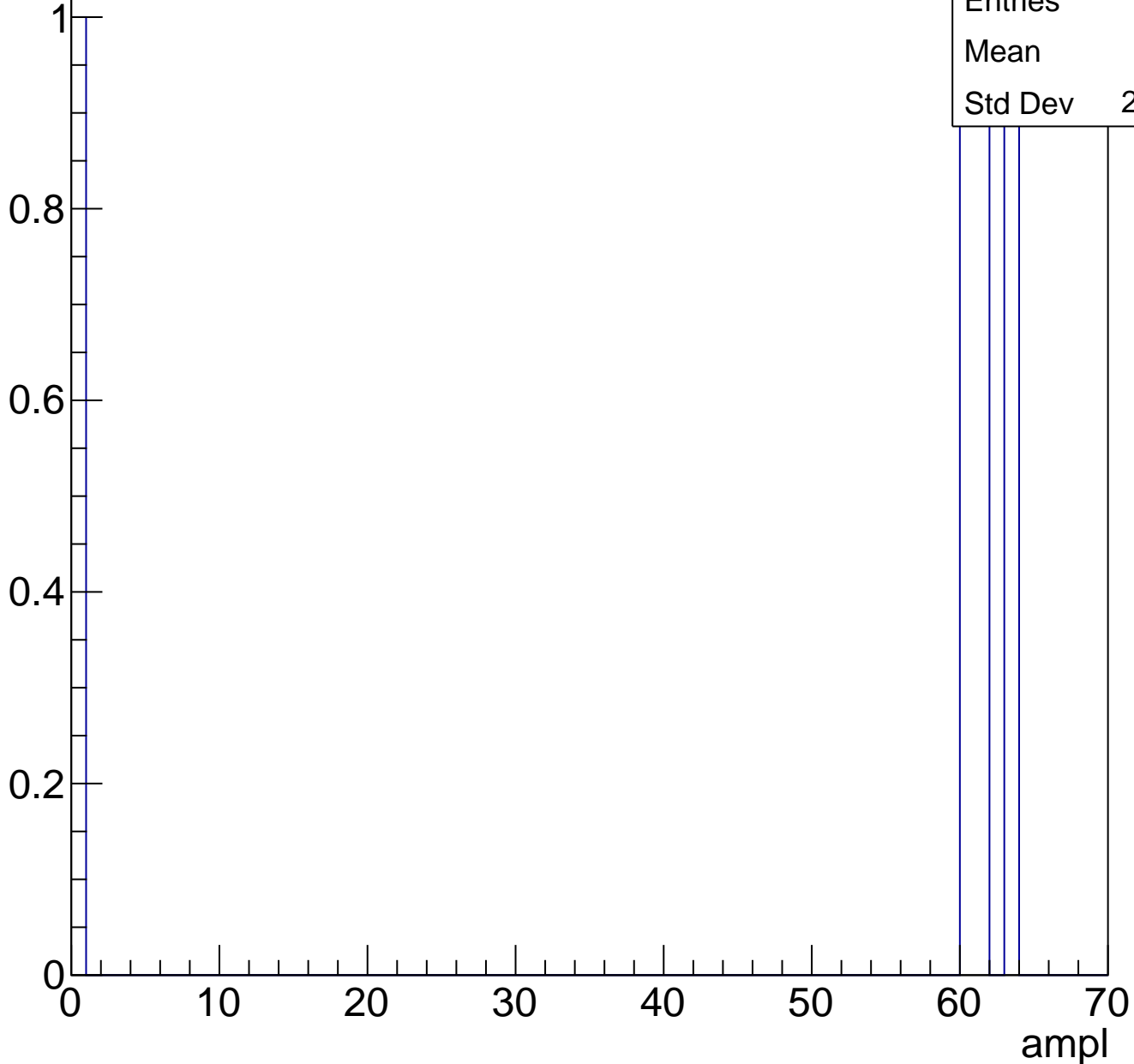
Entry



B1L103S, U21-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch34, adc0

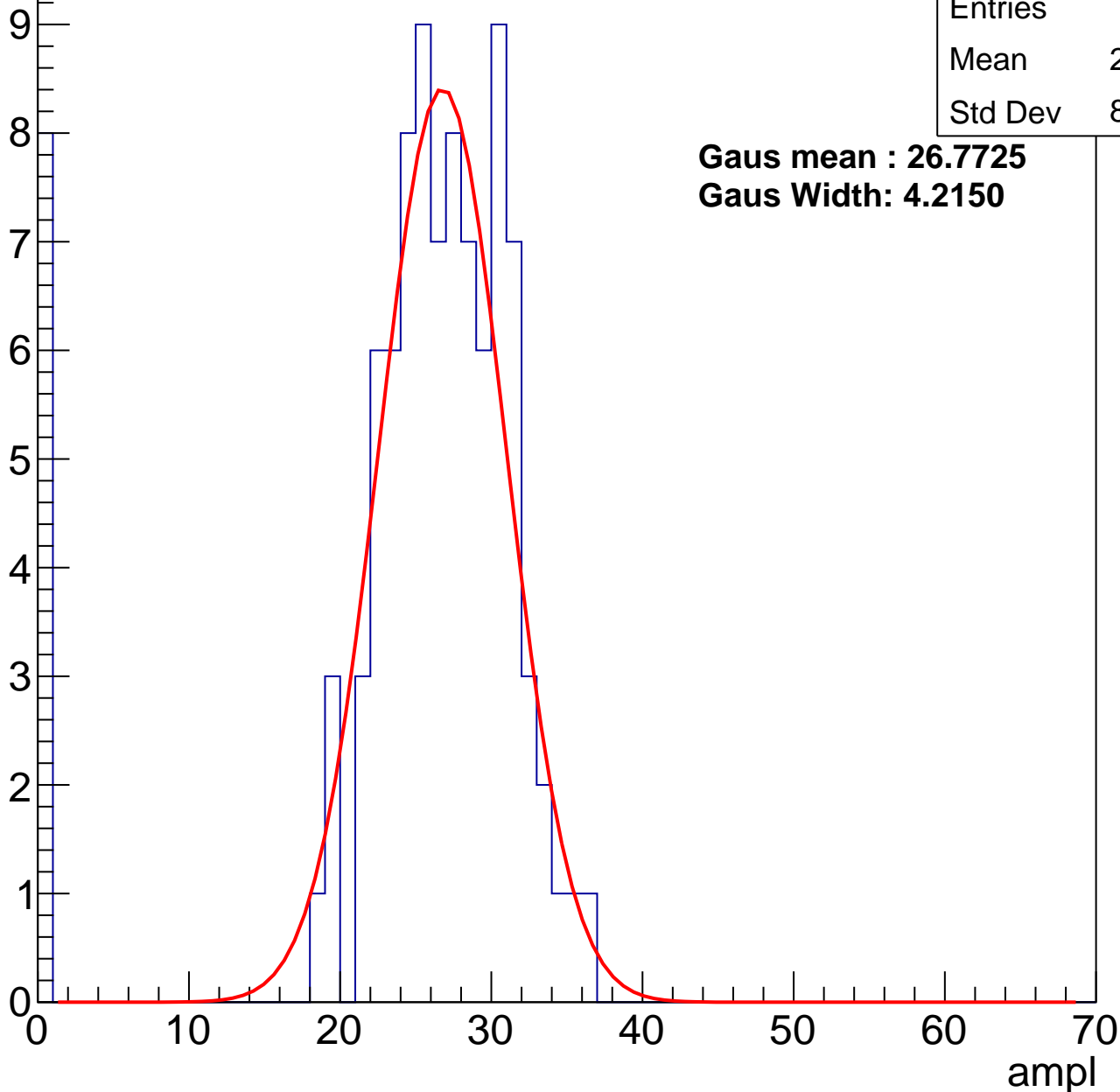
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	96
Mean	24.45
Std Dev	8.237

Gaus mean : 26.7725

Gaus Width: 4.2150



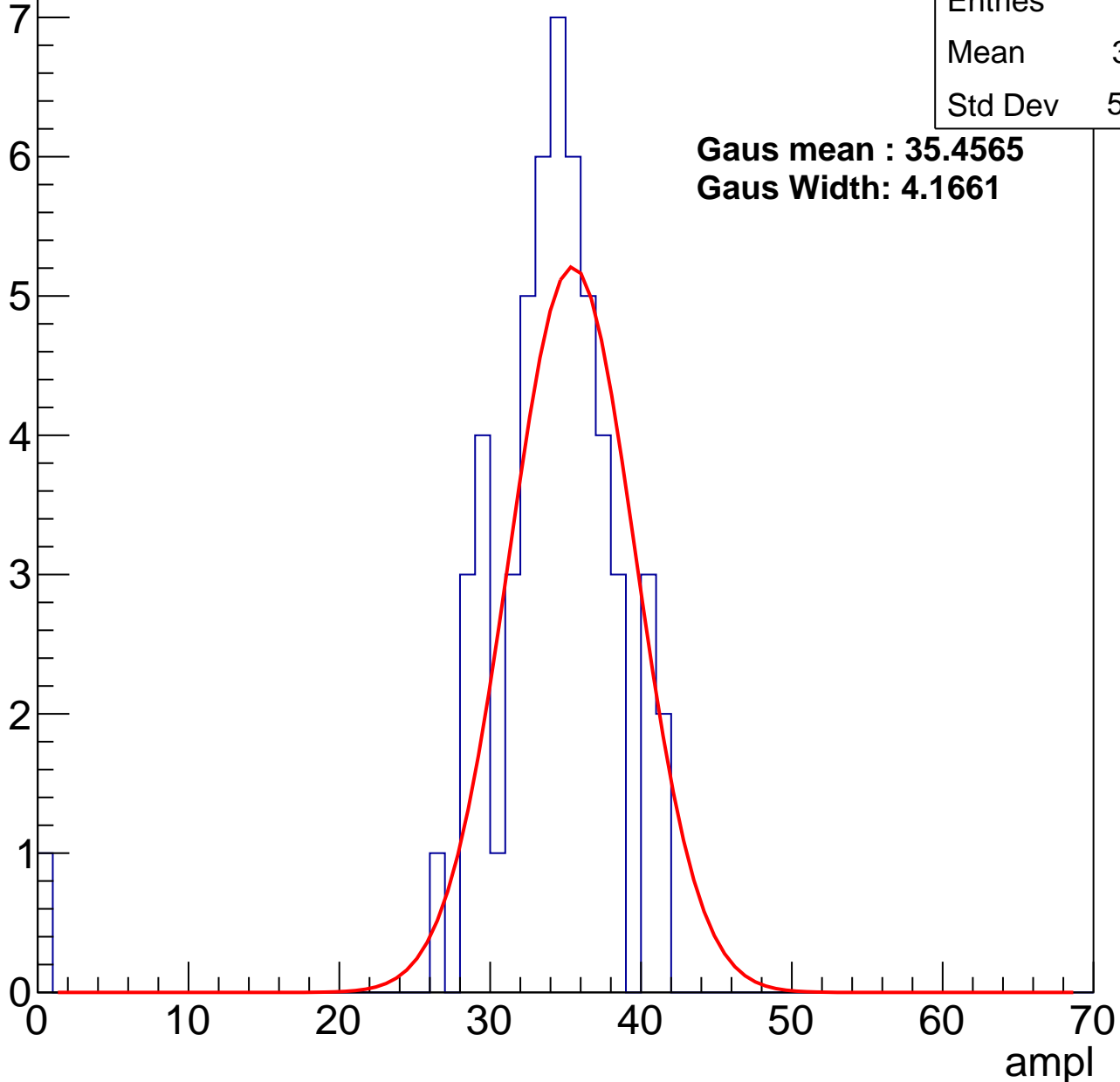
B1L103S, U21-ch34, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	33.31
Std Dev	5.754

Gaus mean : 35.4565
Gaus Width: 4.1661



B1L103S, U21-ch34, adc2

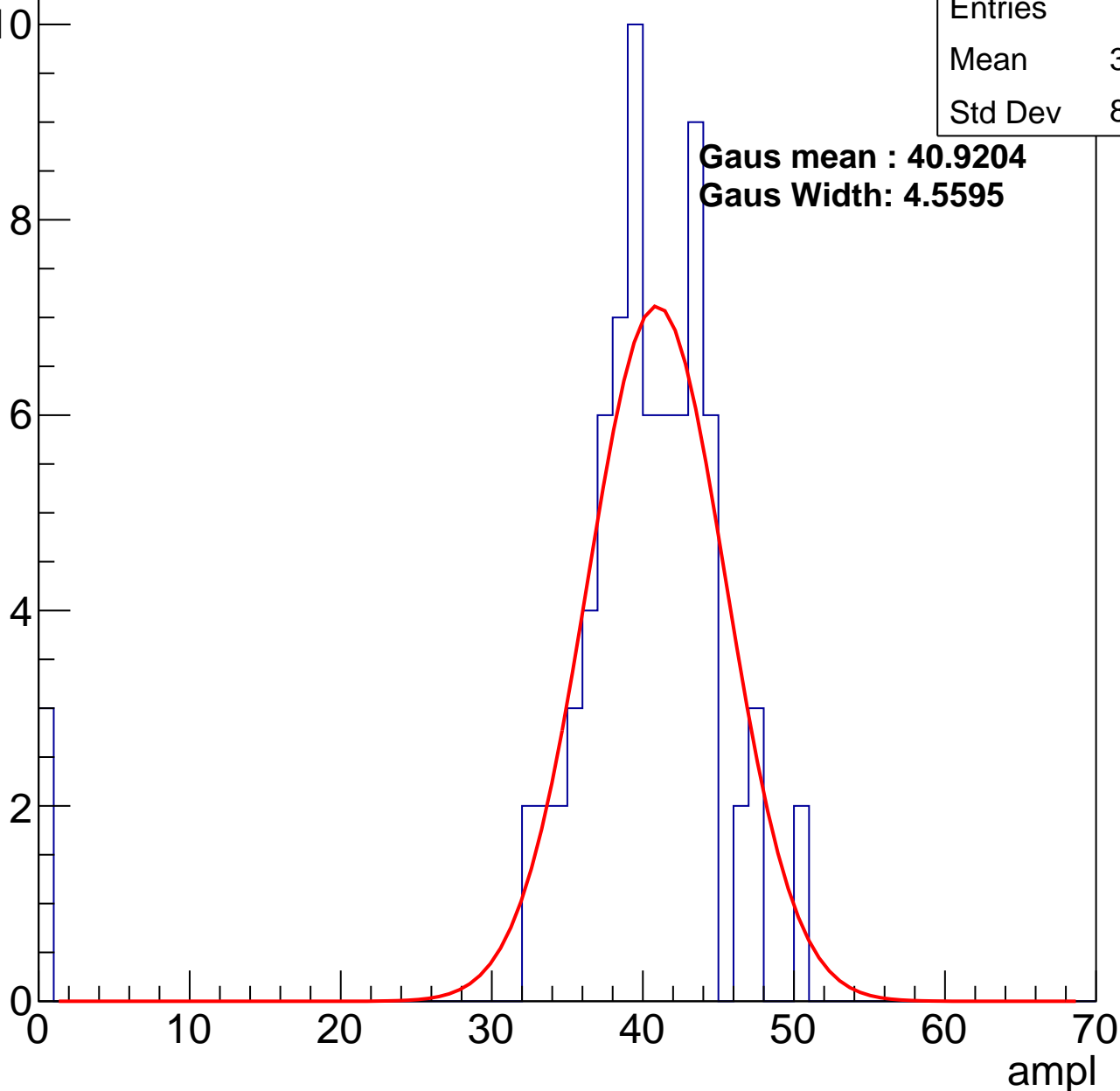
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	38.57
Std Dev	8.564

Gaus mean : 40.9204

Gaus Width: 4.5595



B1L103S, U21-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

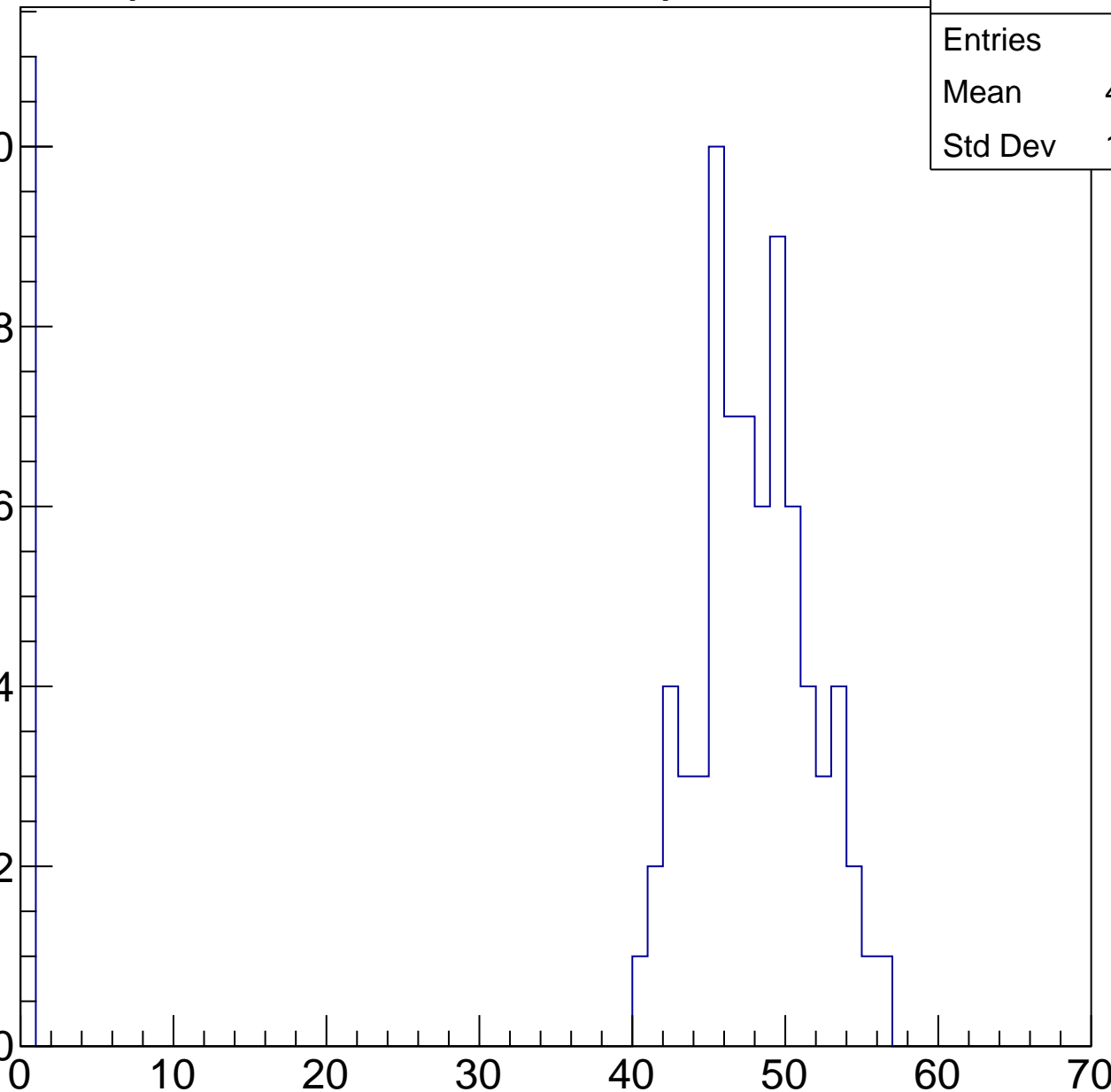
0

Entries 84

Mean 41.33

Std Dev 16.39

ampl

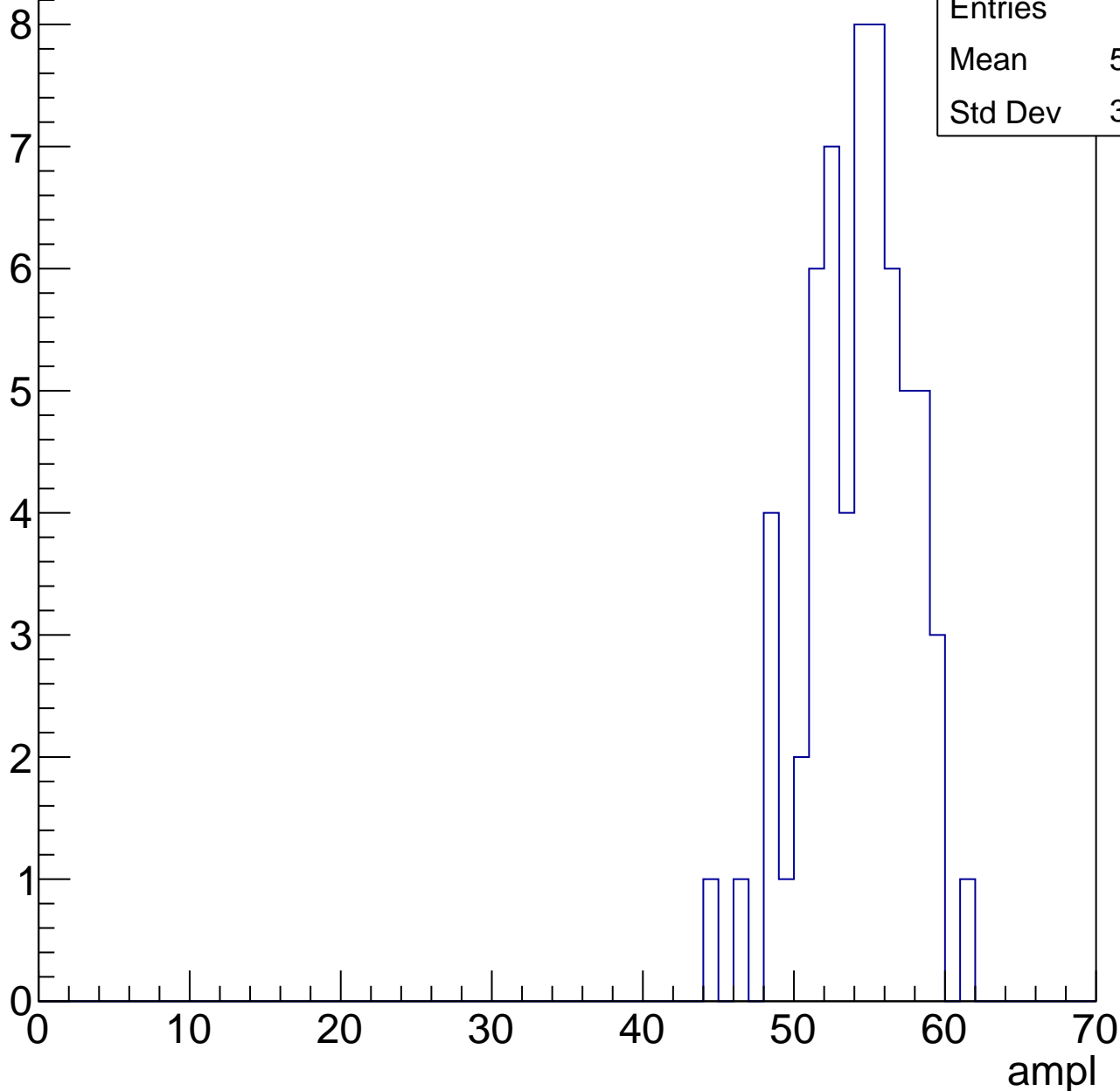


B1L103S, U21-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	53.77
Std Dev	3.429



B1L103S, U21-ch34, adc5

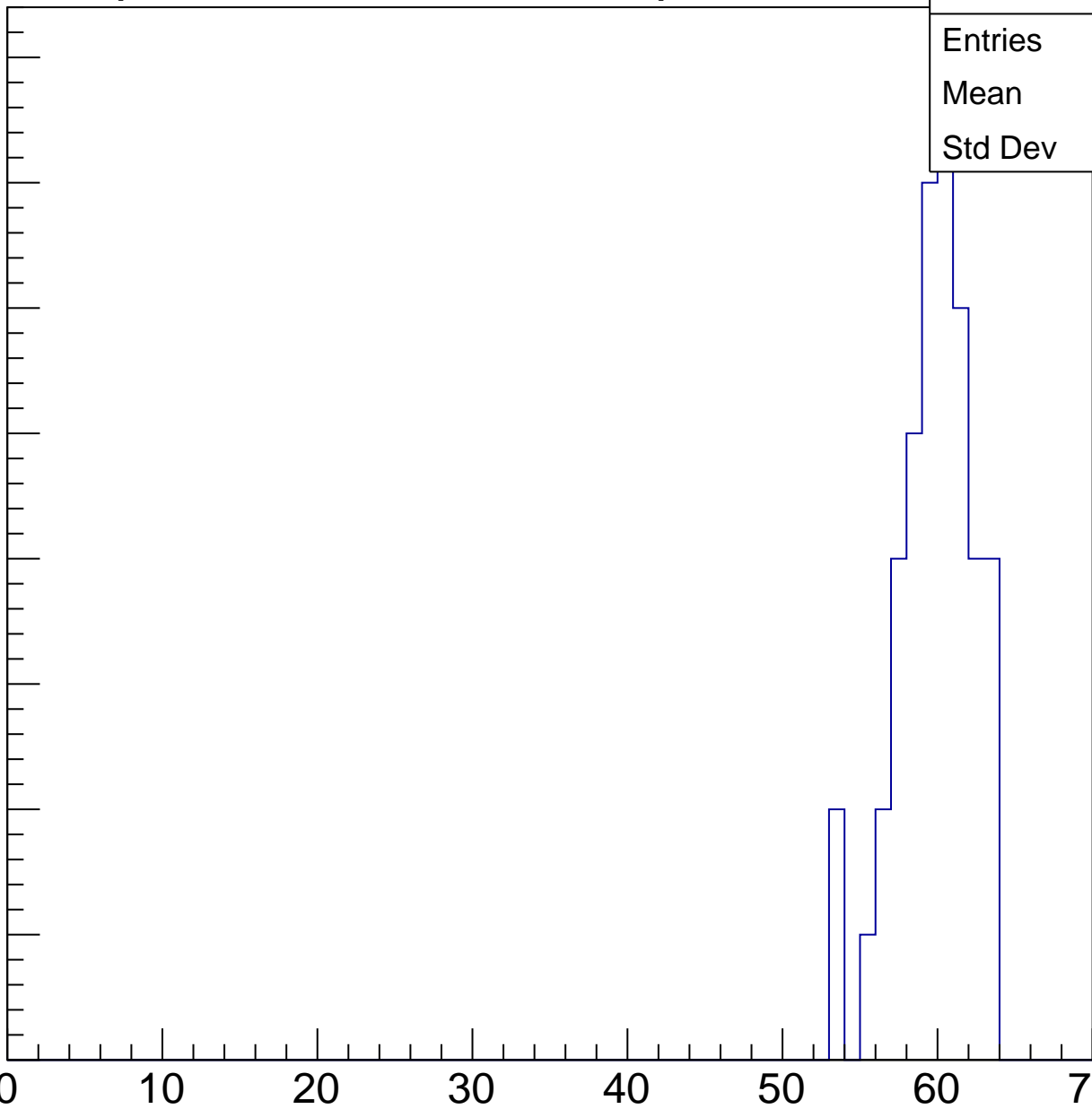
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.3
Std Dev	2.436

ampl

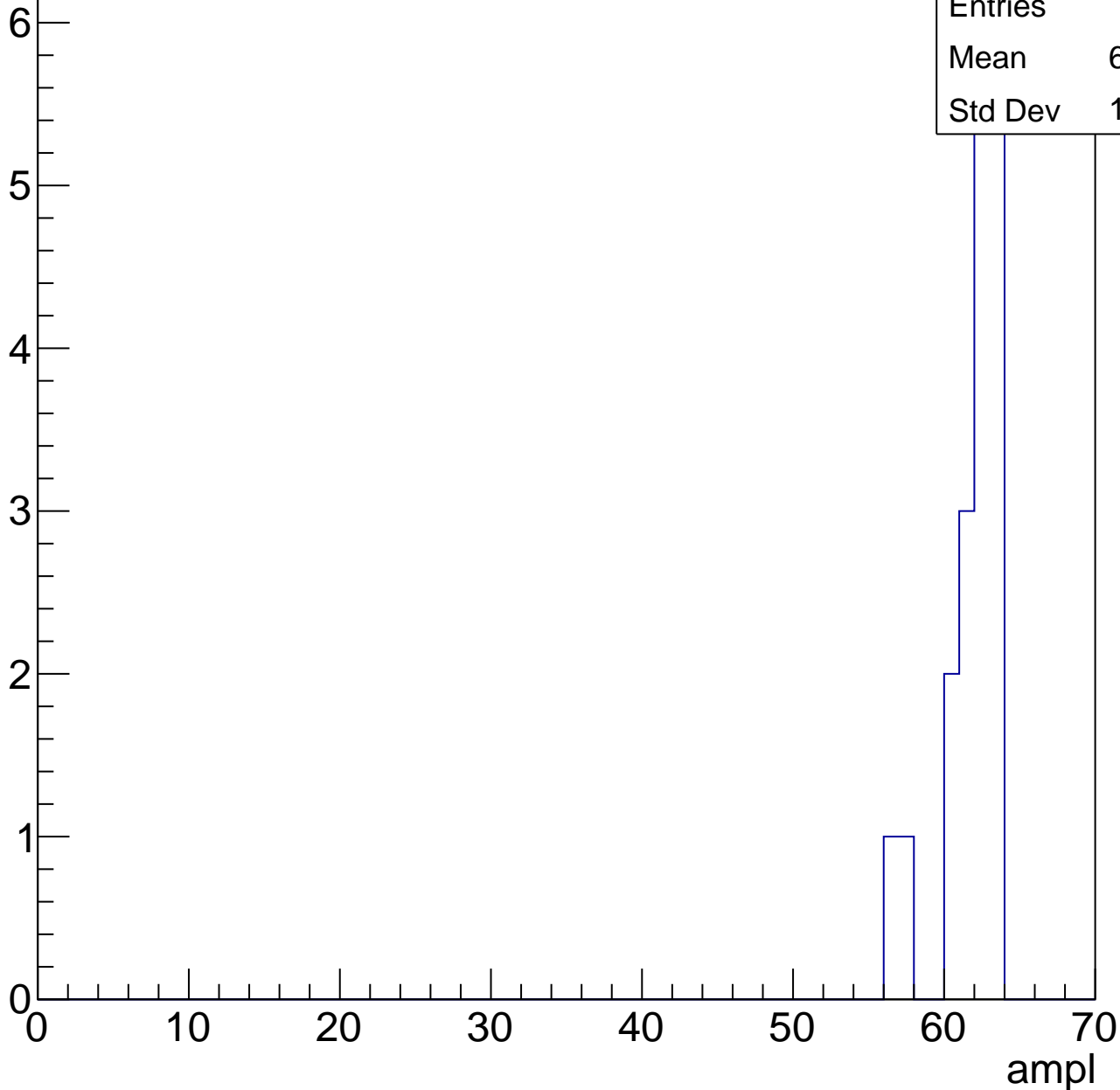


B1L103S, U21-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61.37
Std Dev	1.925



B1L103S, U21-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch35, adc0

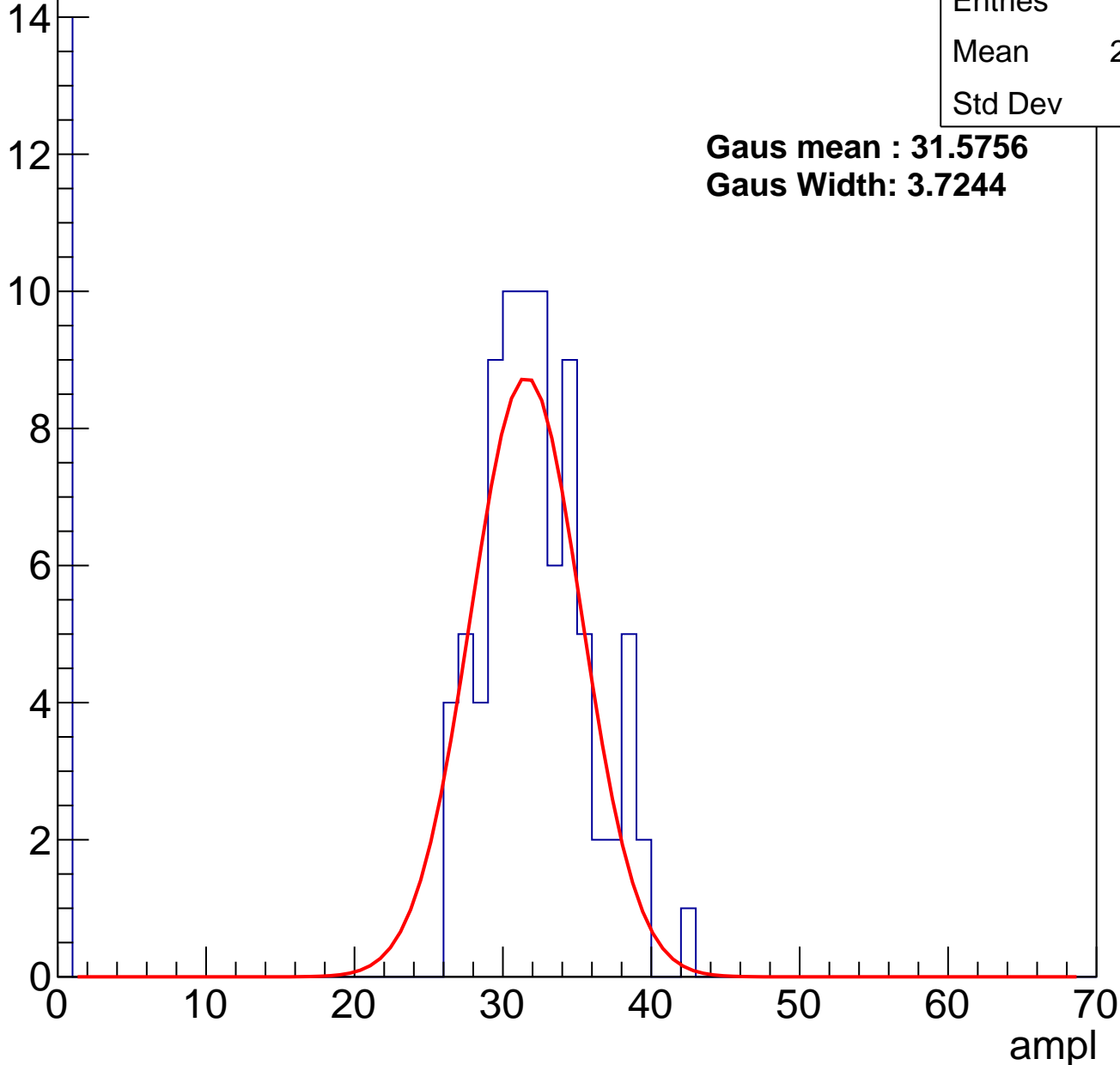
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	27.32
Std Dev	11.6

Gaus mean : 31.5756

Gaus Width: 3.7244

Entry



B1L103S, U21-ch35, adc1

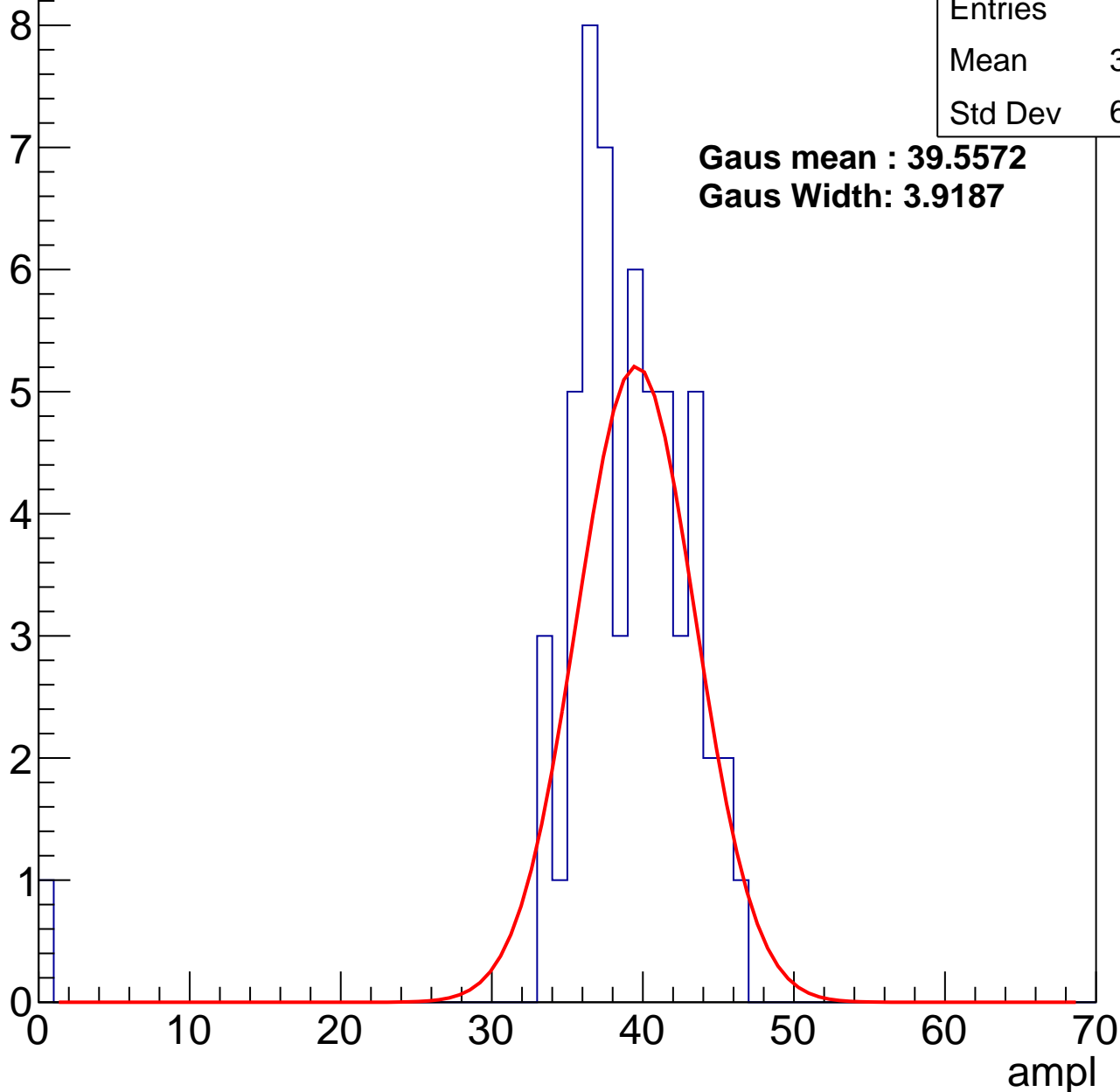
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	38.12
Std Dev	6.064

Gaus mean : 39.5572

Gaus Width: 3.9187



B1L103S, U21-ch35, adc2

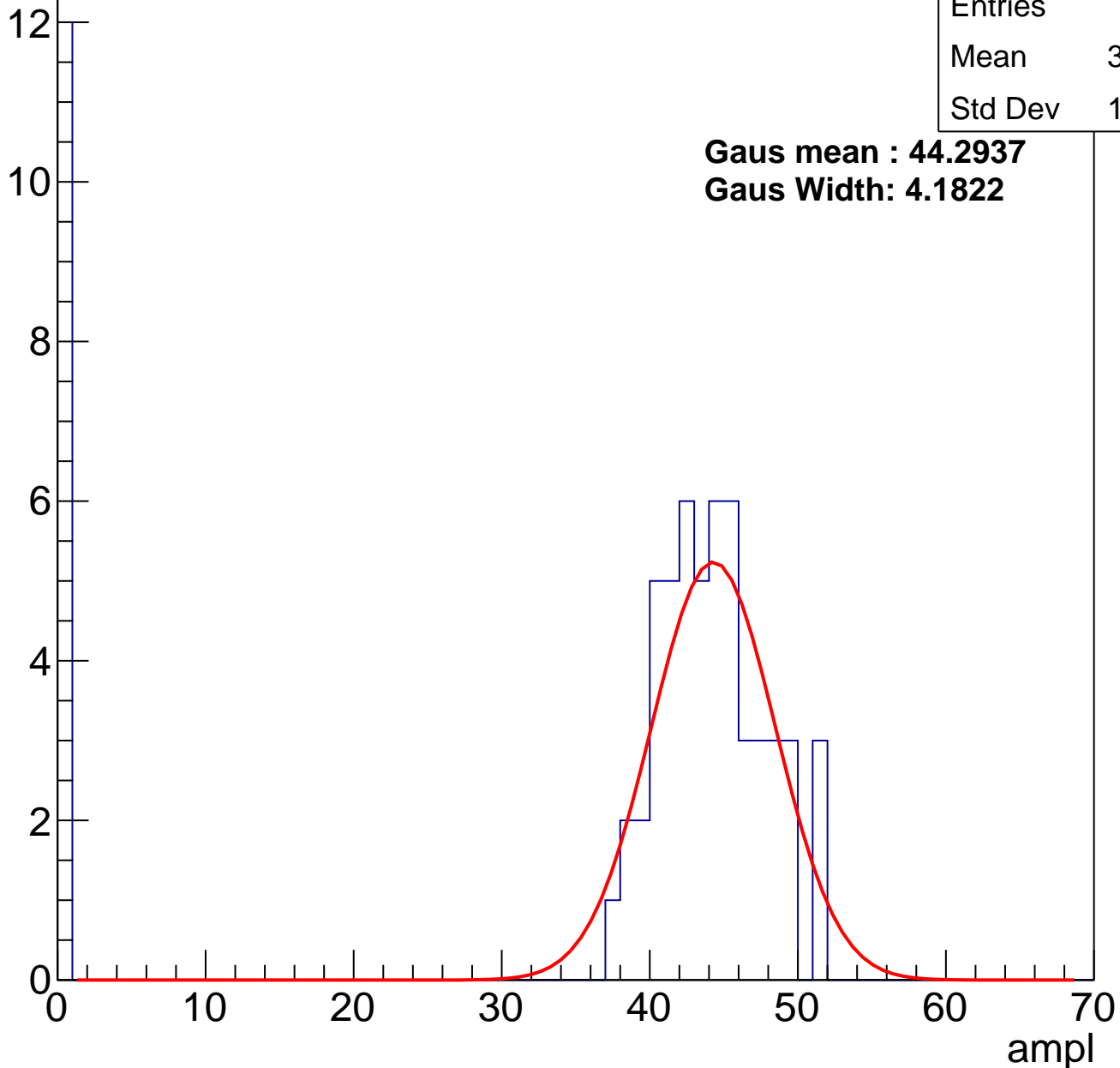
calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	35.69
Std Dev	17.27

Gaus mean : 44.2937

Gaus Width: 4.1822

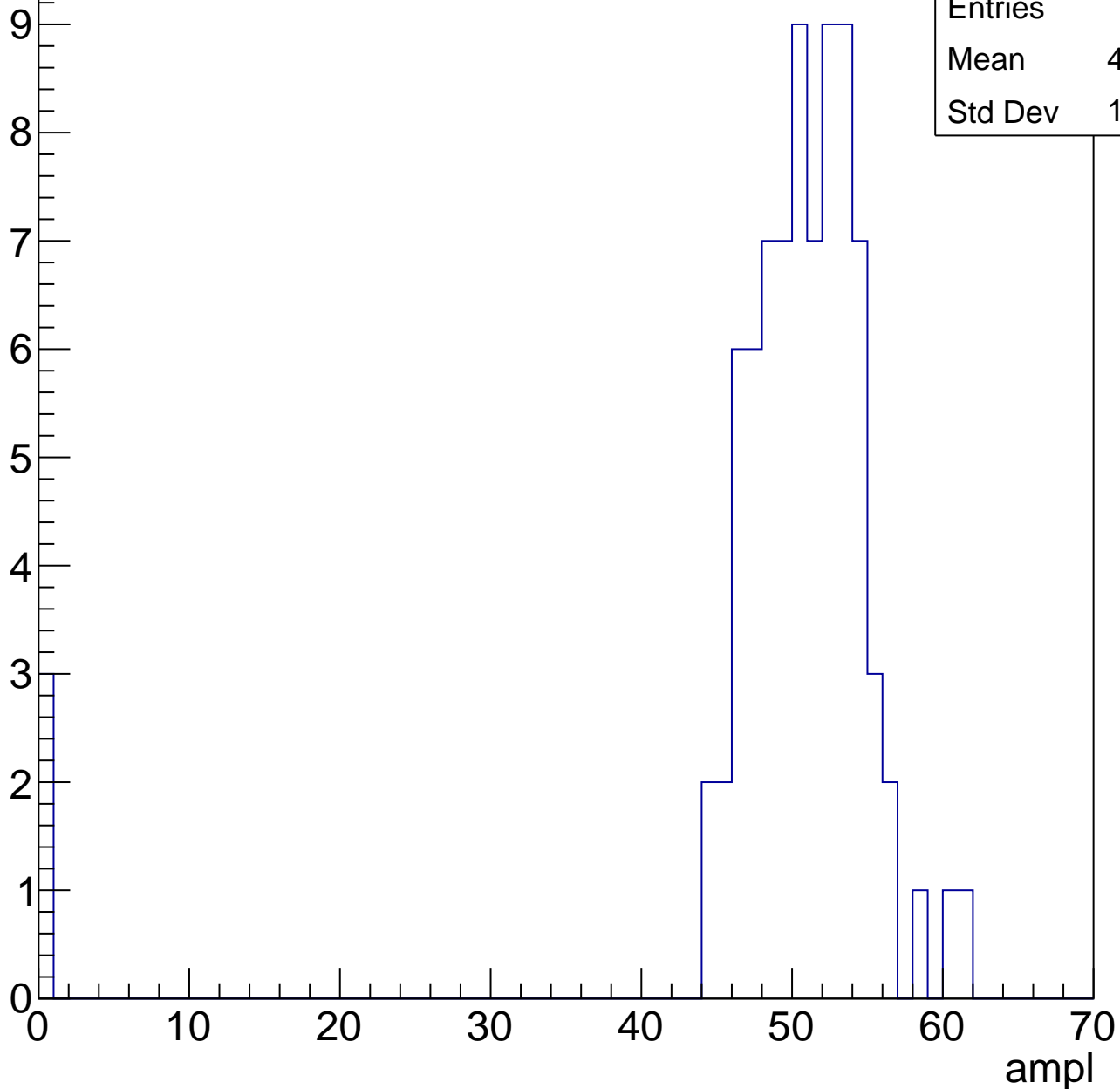
Entry



B1L103S, U21-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	82
Mean	48.79
Std Dev	10.09

B1L103S, U21-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

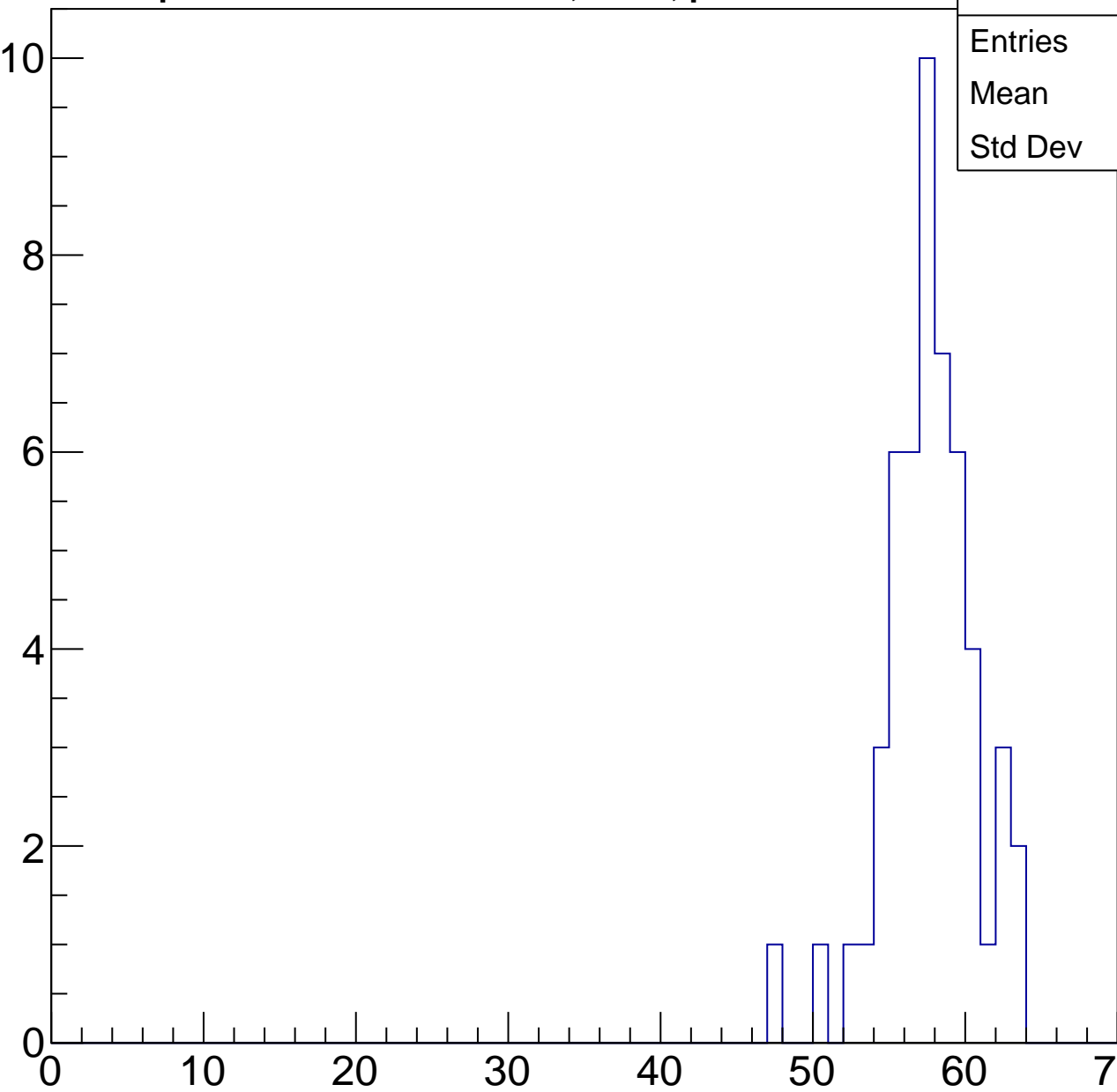
Entries	52
Mean	57.17
Std Dev	3.024

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

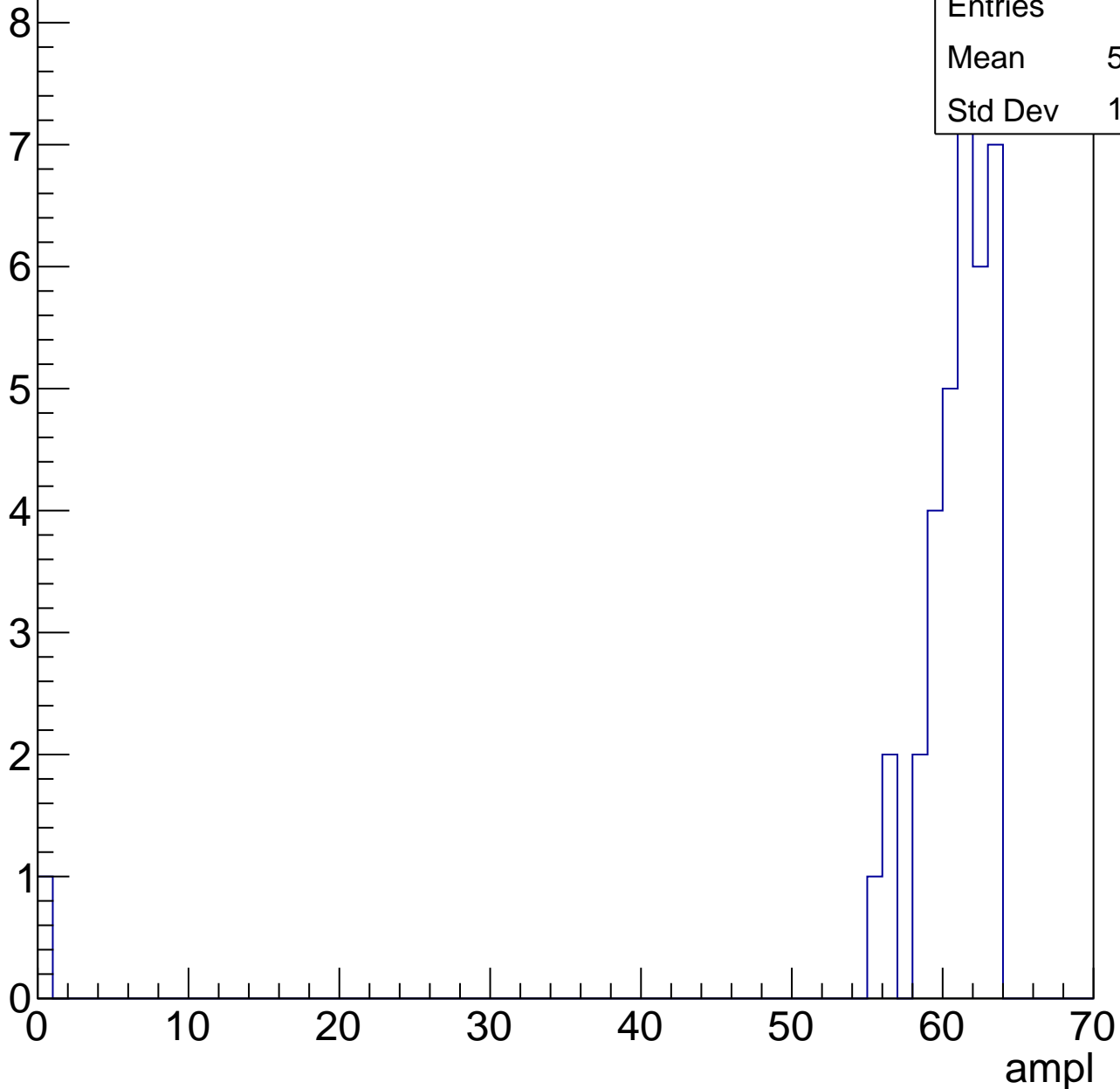


B1L103S, U21-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.89
Std Dev	10.16



B1L103S, U21-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch36, adc0

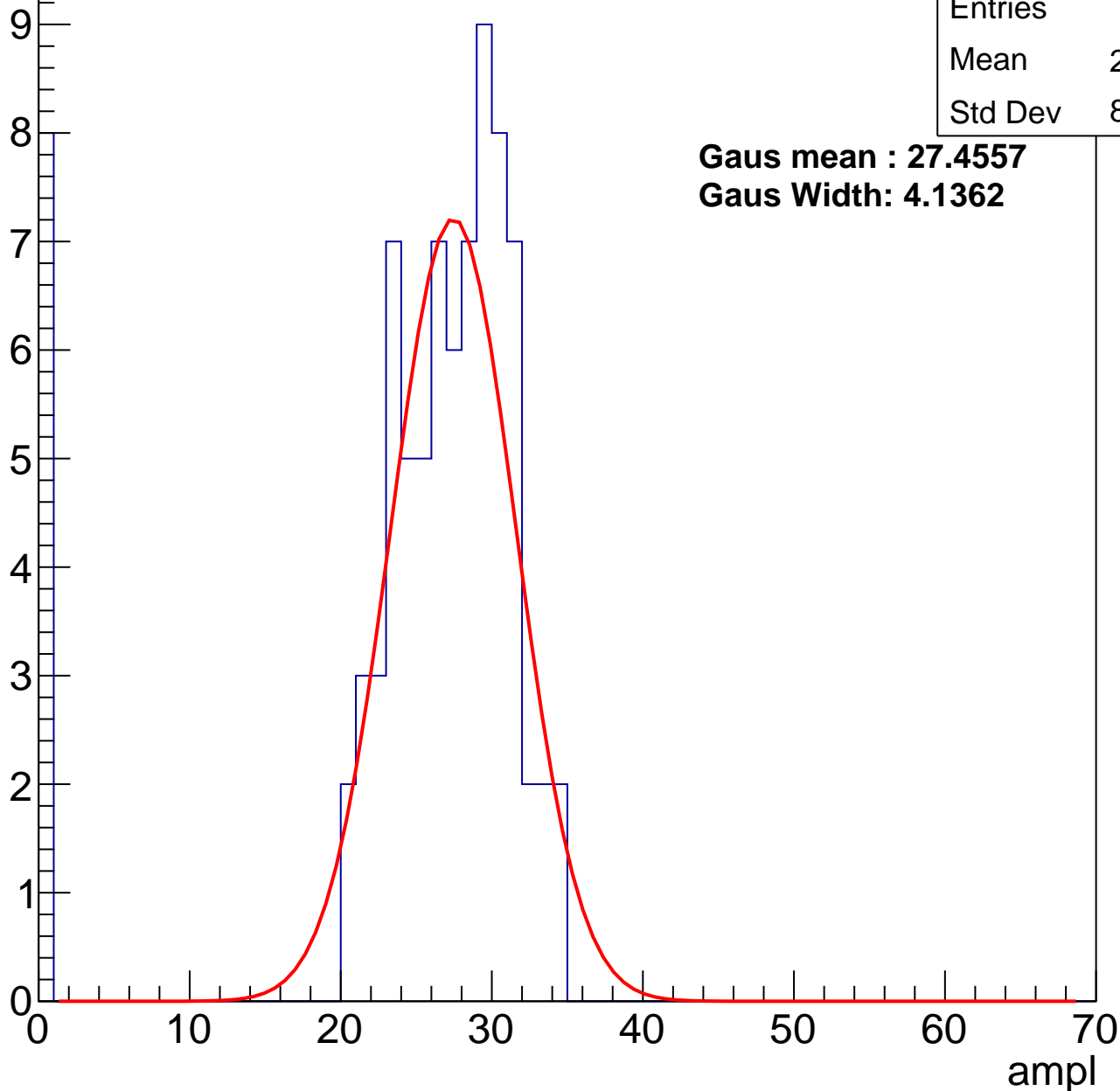
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	24.47
Std Dev	8.652

Gaus mean : 27.4557

Gaus Width: 4.1362



B1L103S, U21-ch36, adc1

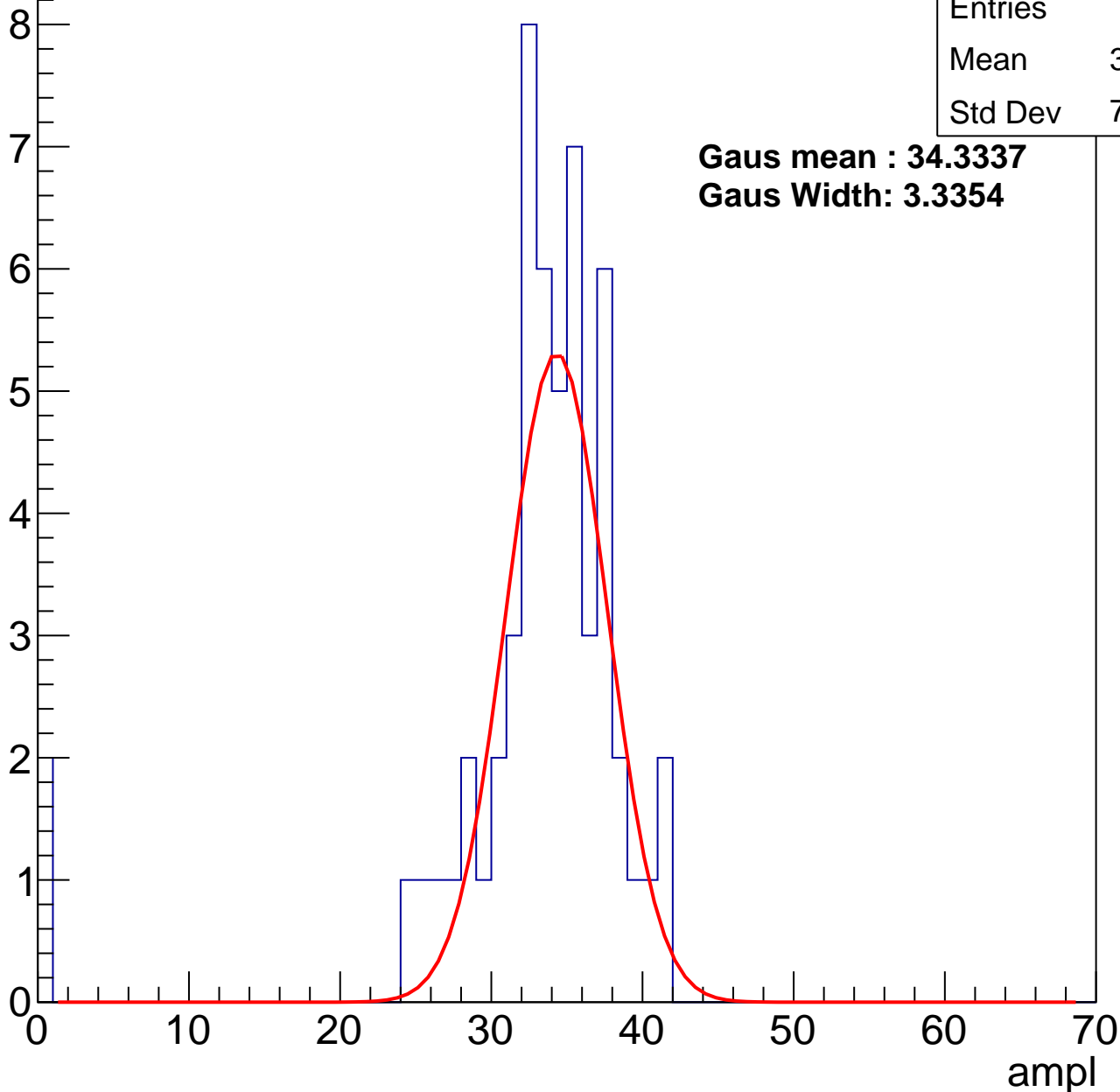
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	32.29
Std Dev	7.266

Gaus mean : 34.3337

Gaus Width: 3.3354



B1L103S, U21-ch36, adc2

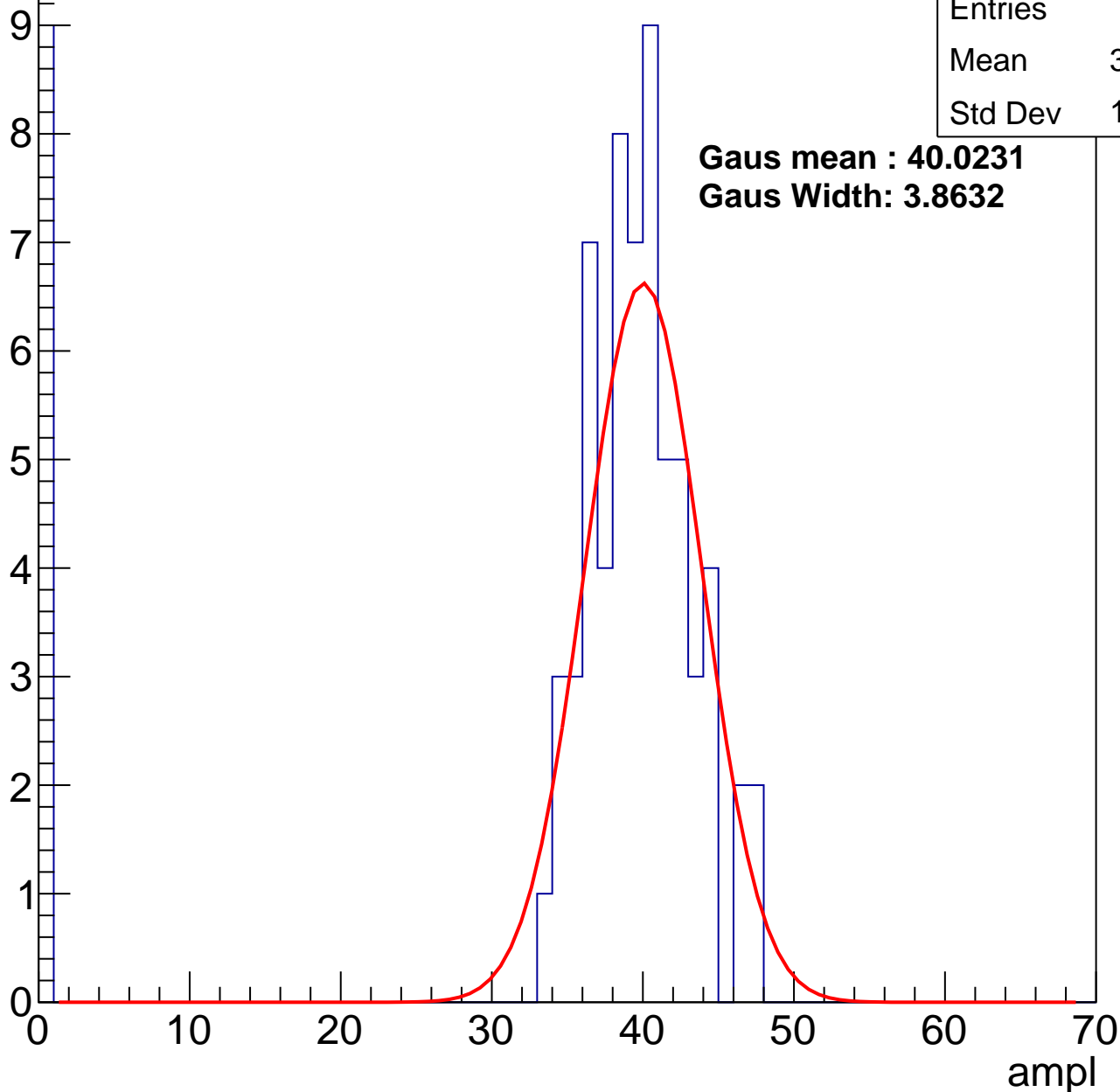
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	34.49
Std Dev	13.39

Gaus mean : 40.0231

Gaus Width: 3.8632

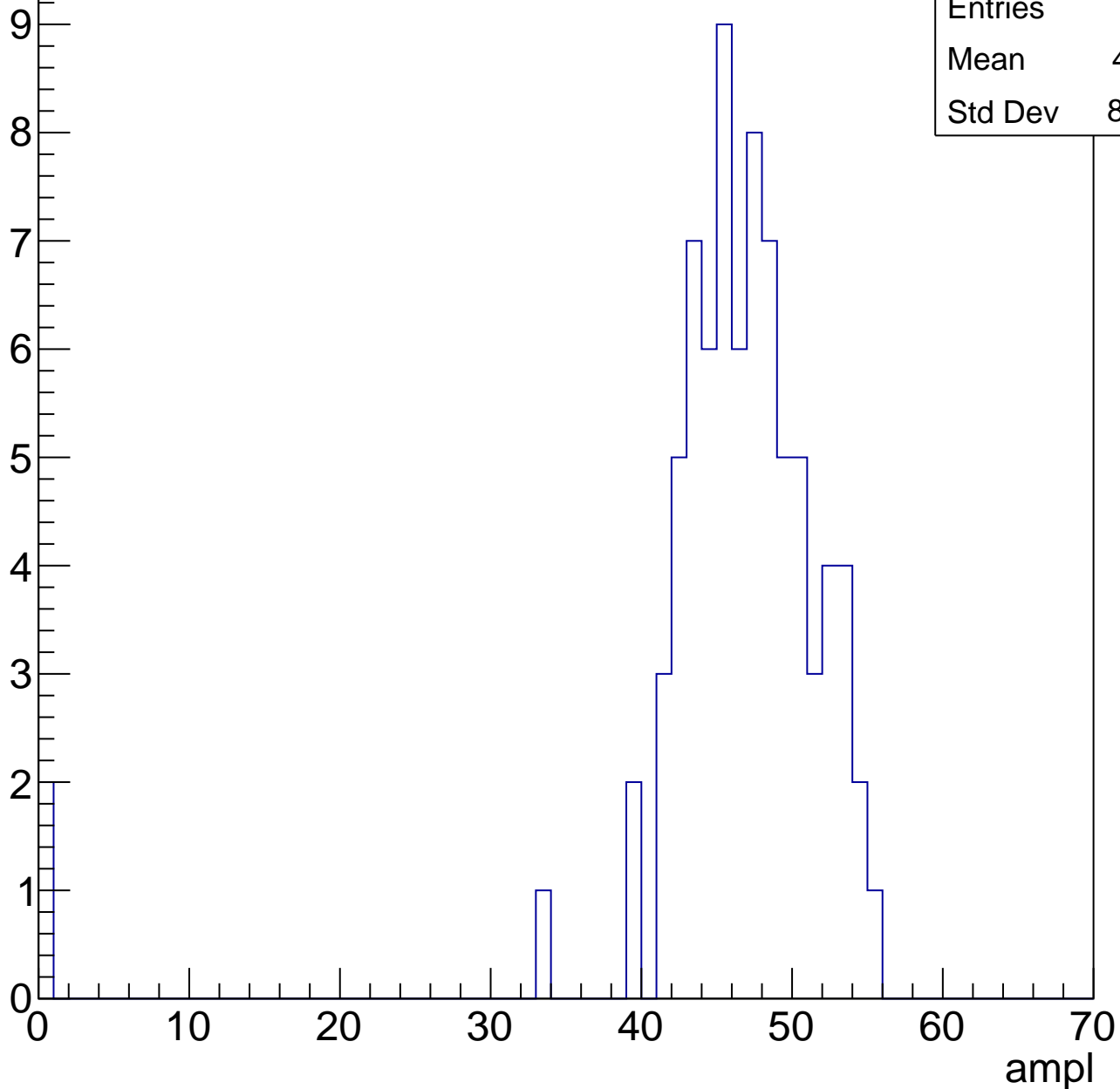


B1L103S, U21-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	45.41
Std Dev	8.297

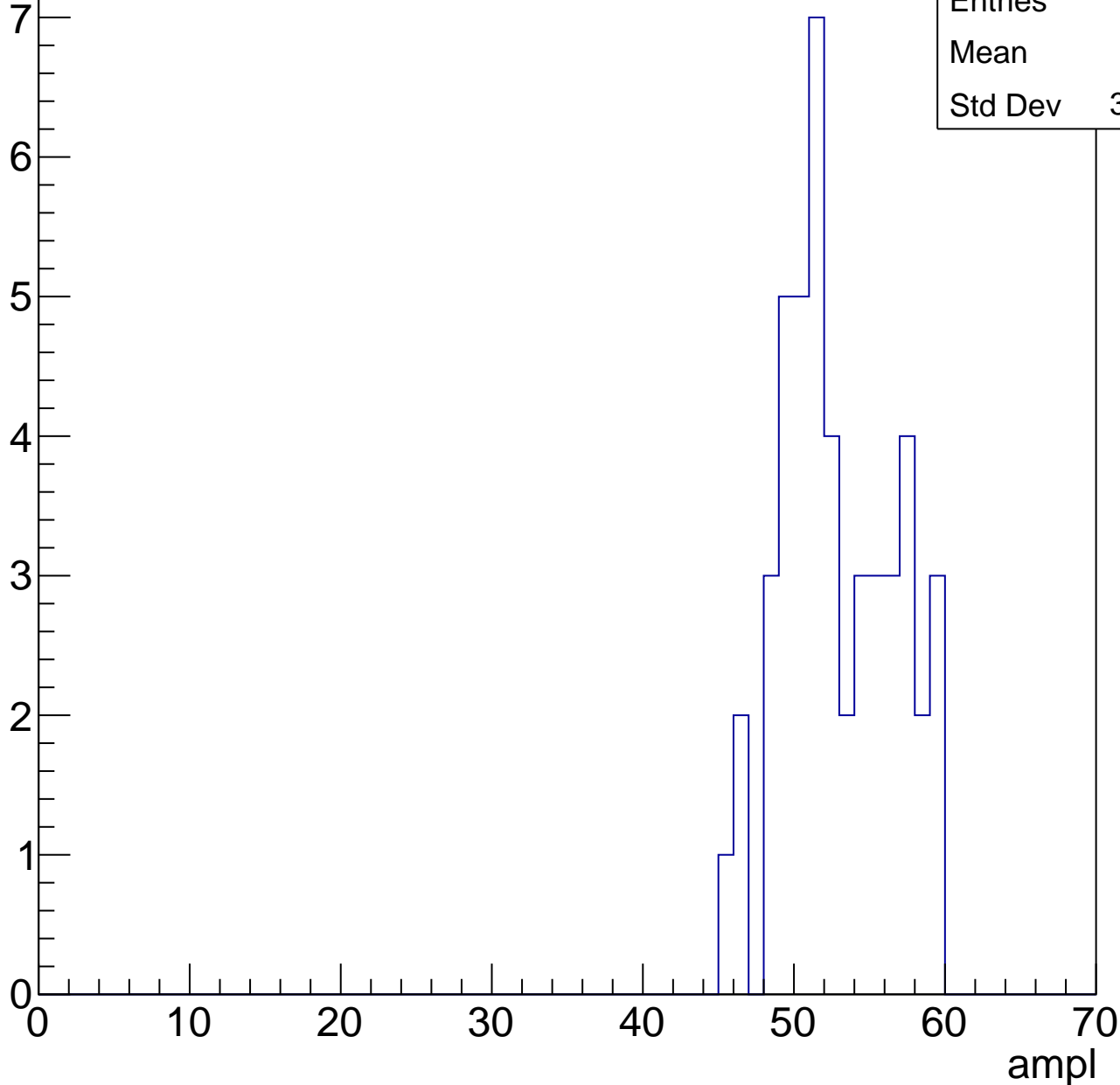


B1L103S, U21-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

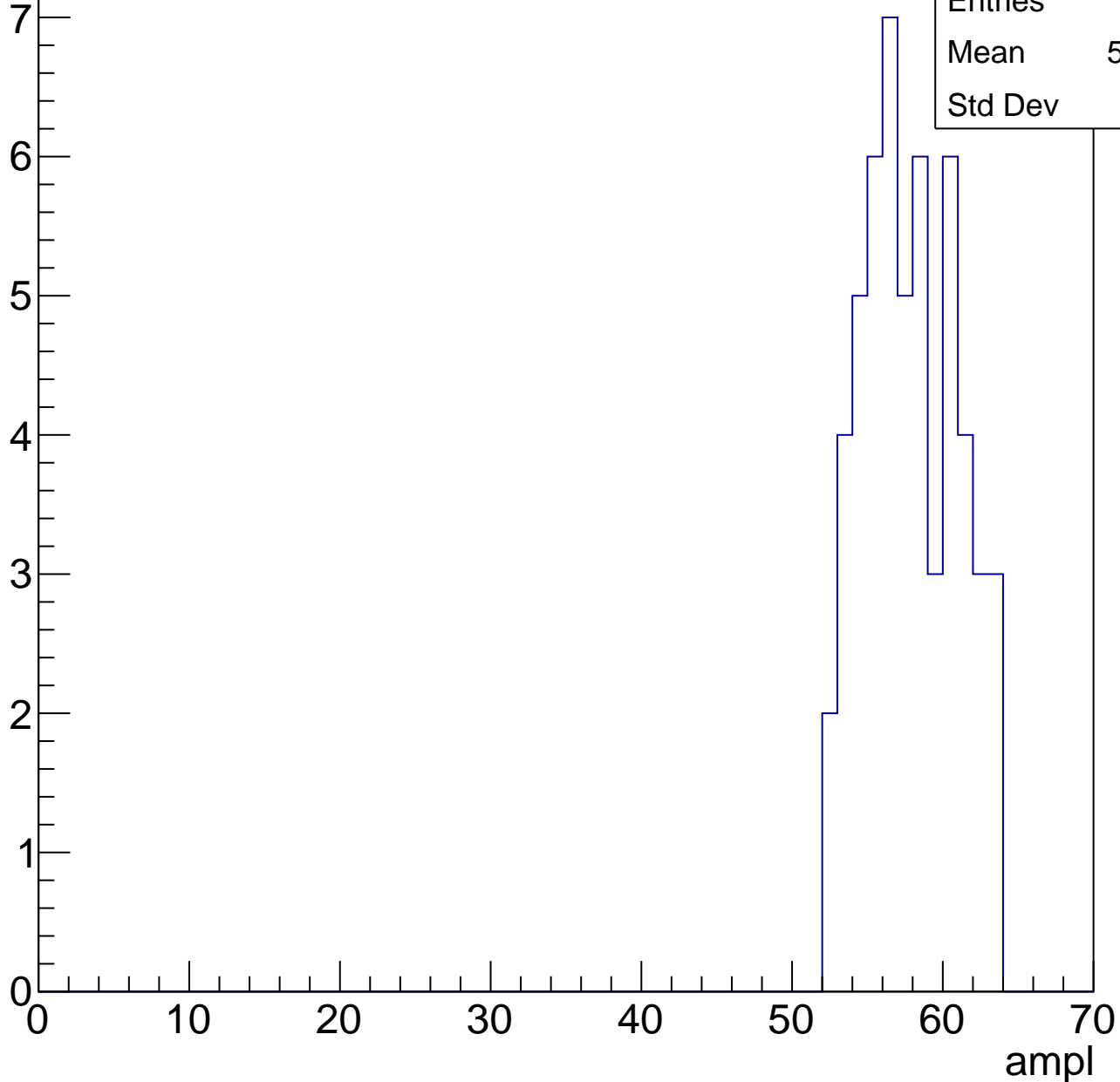
Entries	47
Mean	52.4
Std Dev	3.694



B1L103S, U21-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



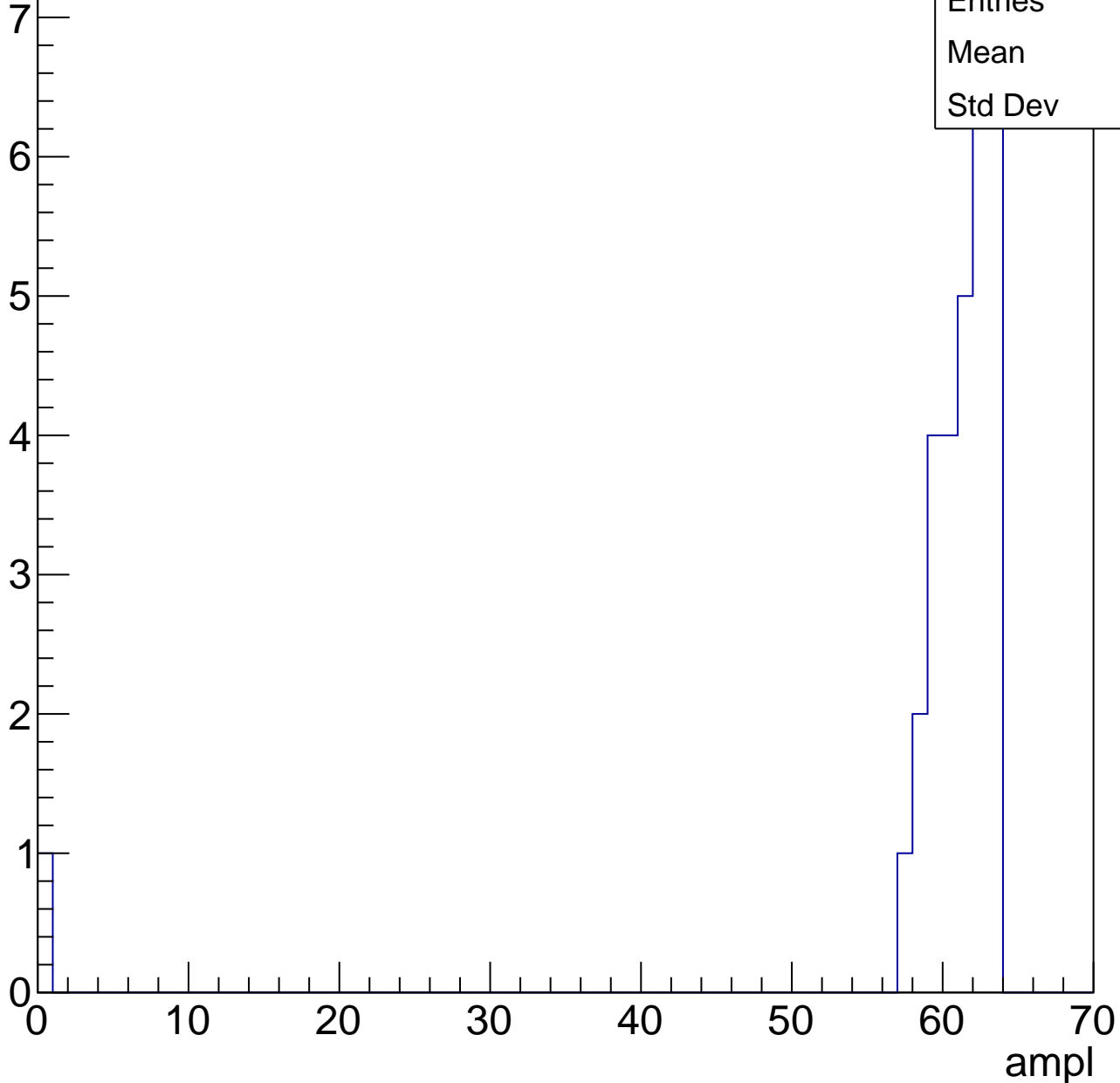
Entries	54
Mean	57.35
Std Dev	3.05

B1L103S, U21-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	59
Std Dev	10.9

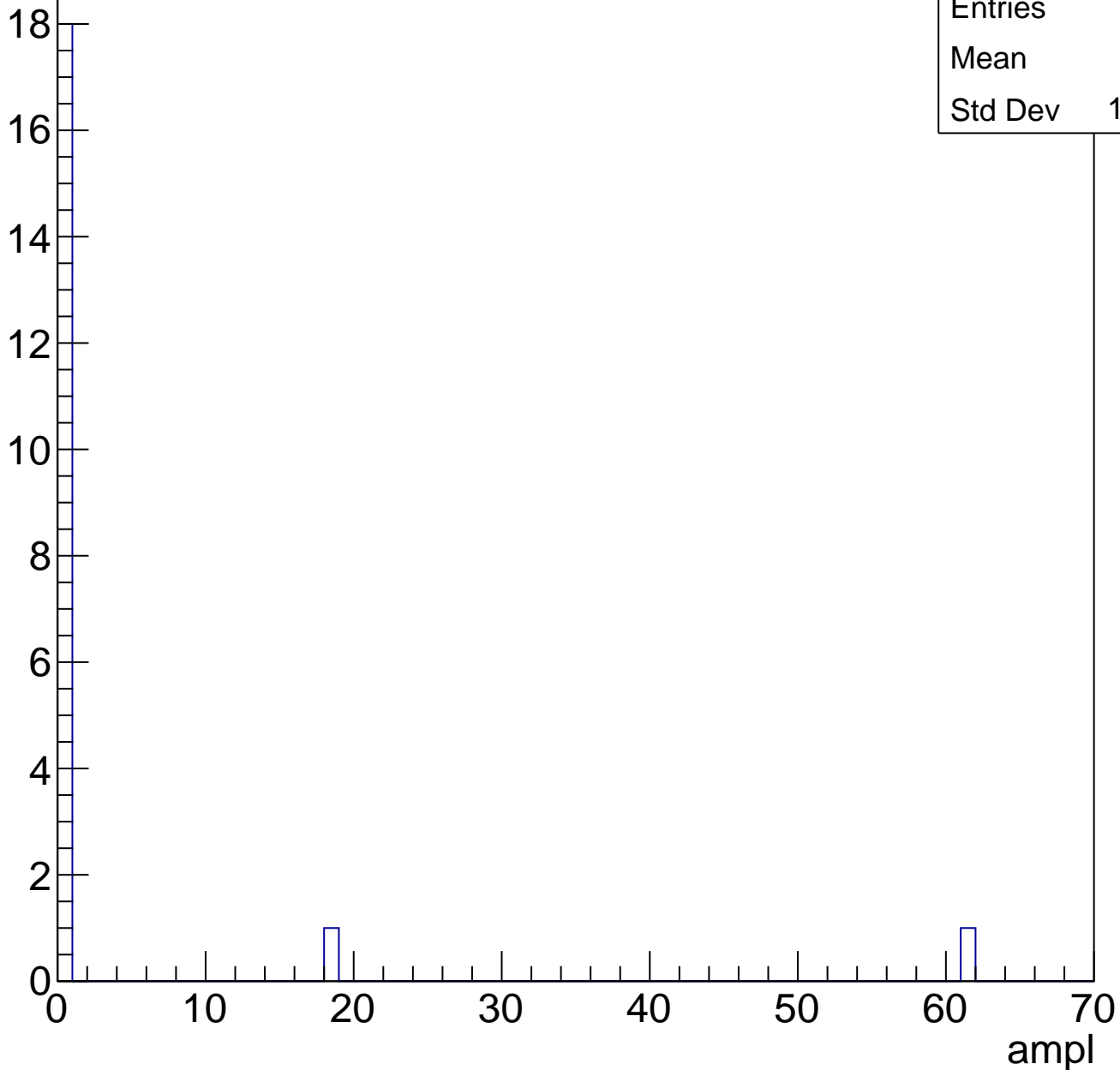


B1L103S, U21-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.95
Std Dev	13.66

Entry



B1L103S, U21-ch37, adc0

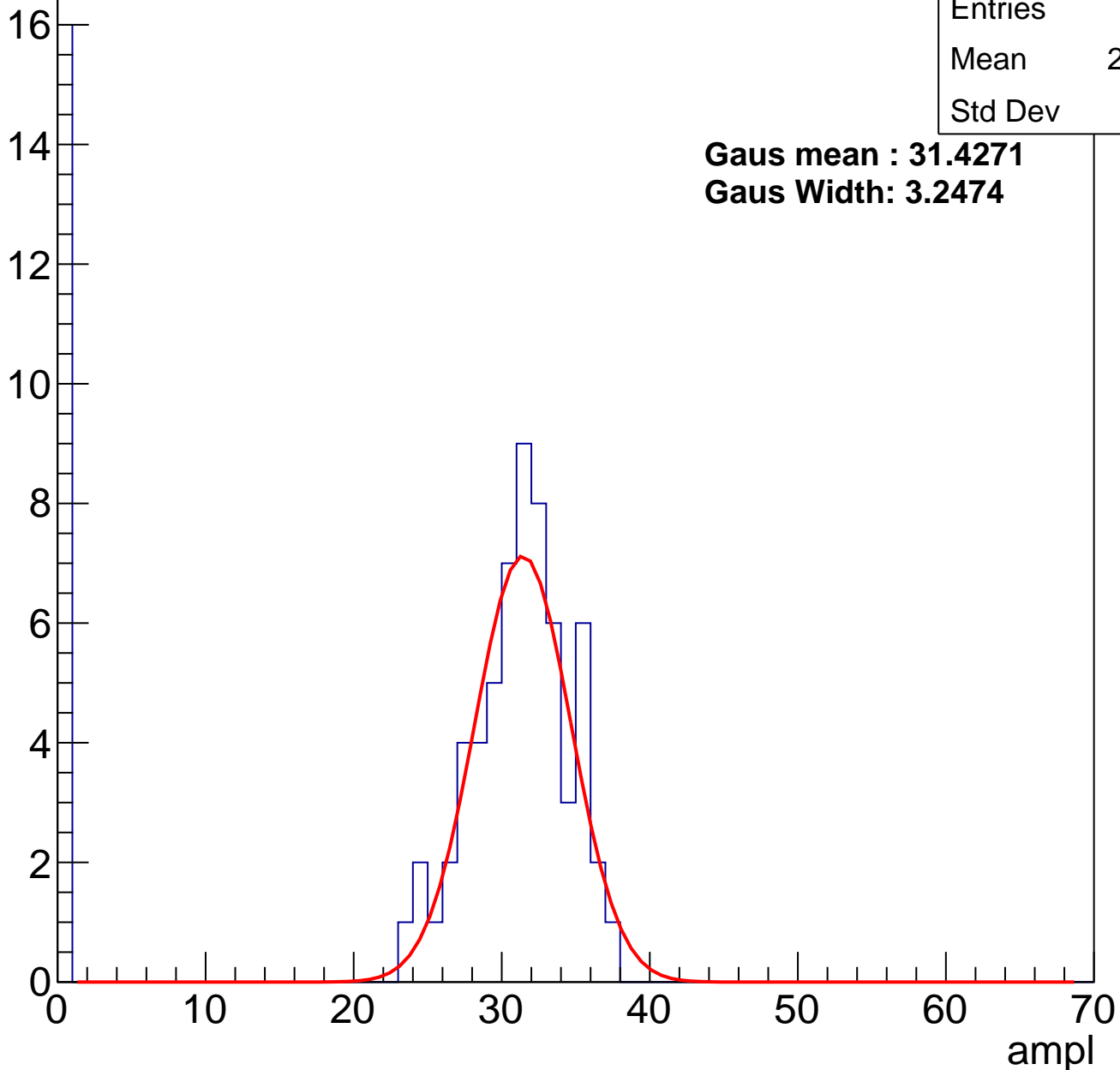
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	24.38
Std Dev	12.8

Gaus mean : 31.4271

Gaus Width: 3.2474

Entry



B1L103S, U21-ch37, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	35.74
Std Dev	9.936

Gaus mean : 38.4233

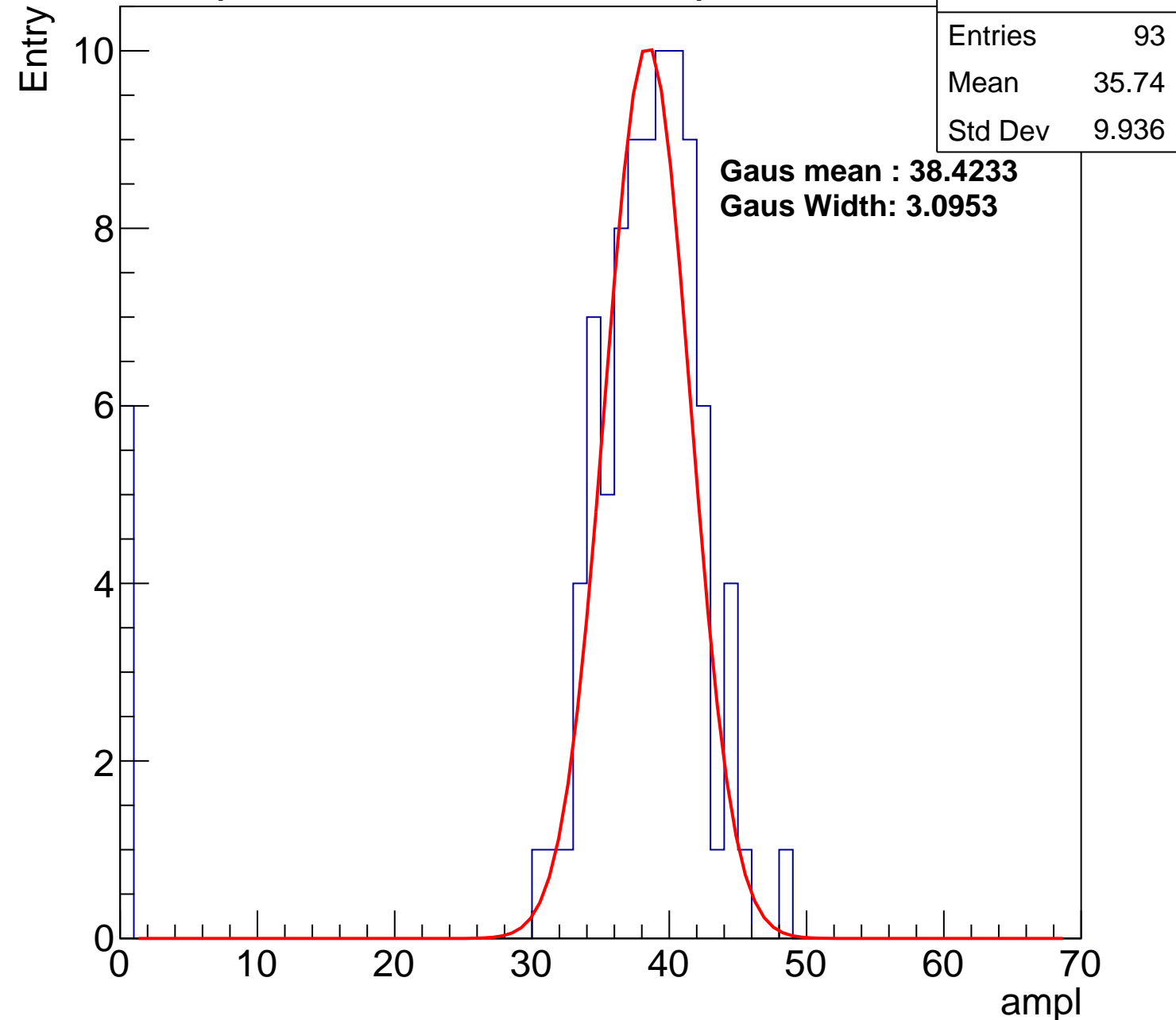
Gaus Width: 3.0953

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch37, adc2

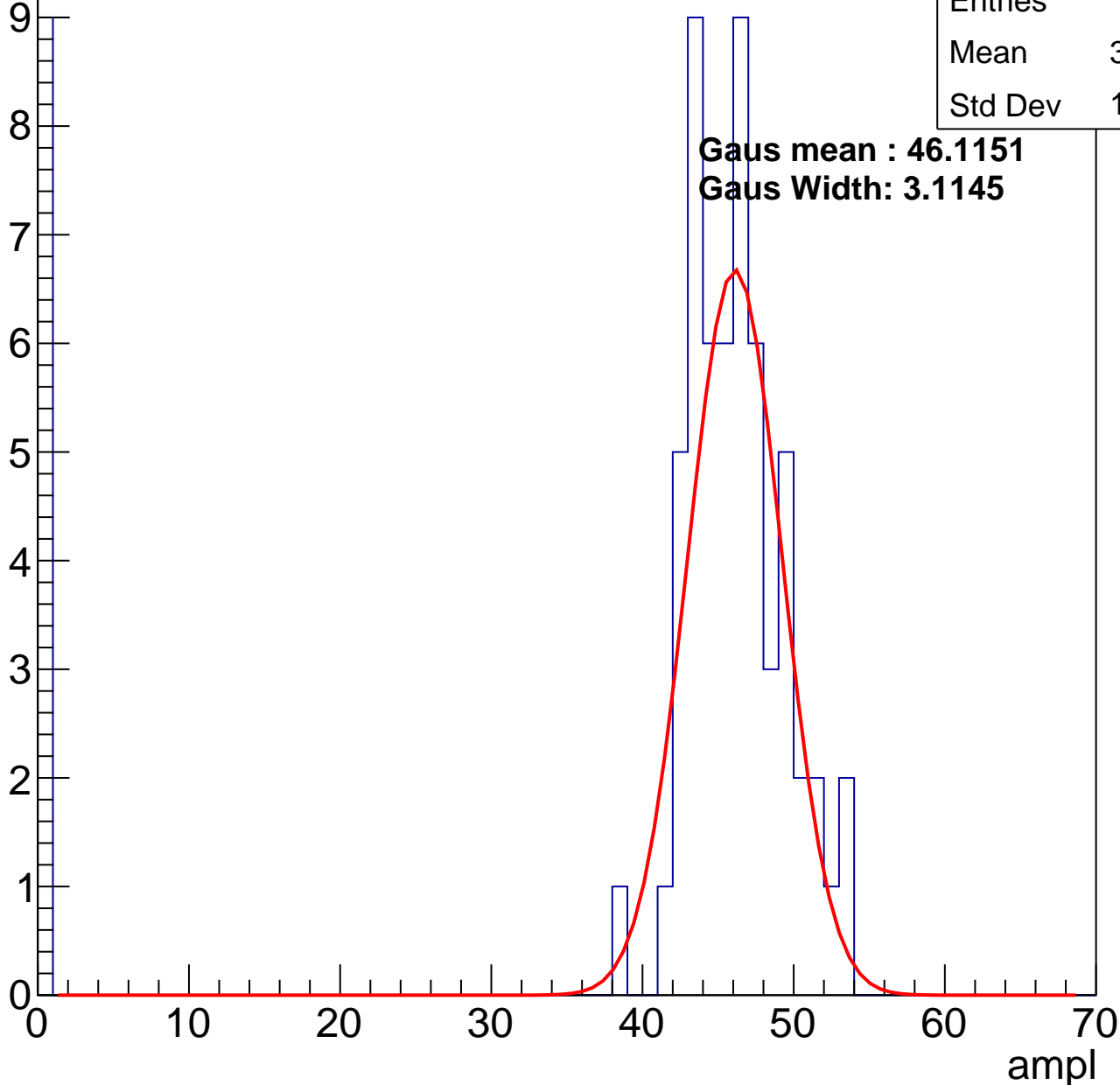
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	39.63
Std Dev	15.87

Gaus mean : 46.1151

Gaus Width: 3.1145

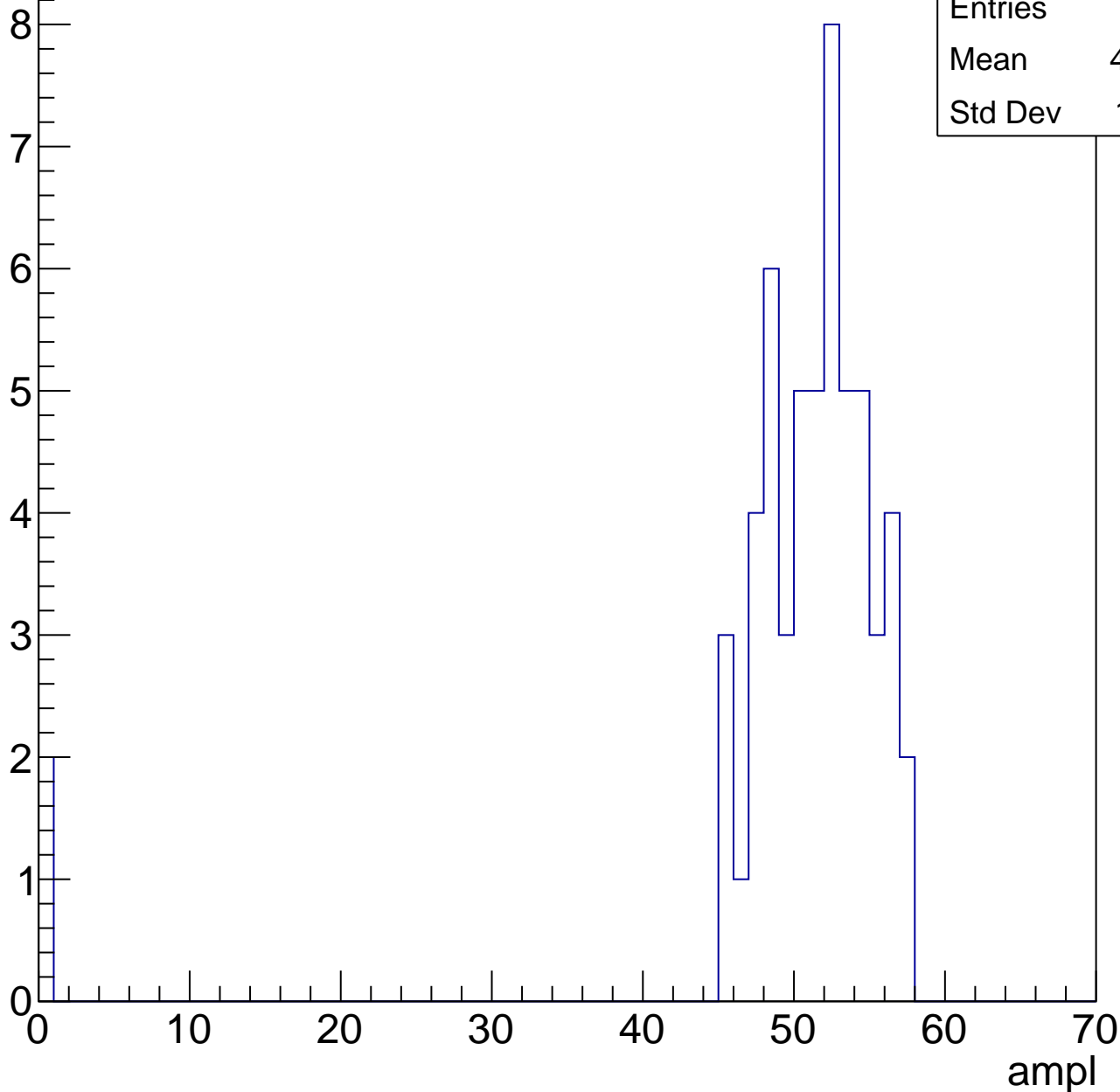


B1L103S, U21-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	49.34
Std Dev	10.01

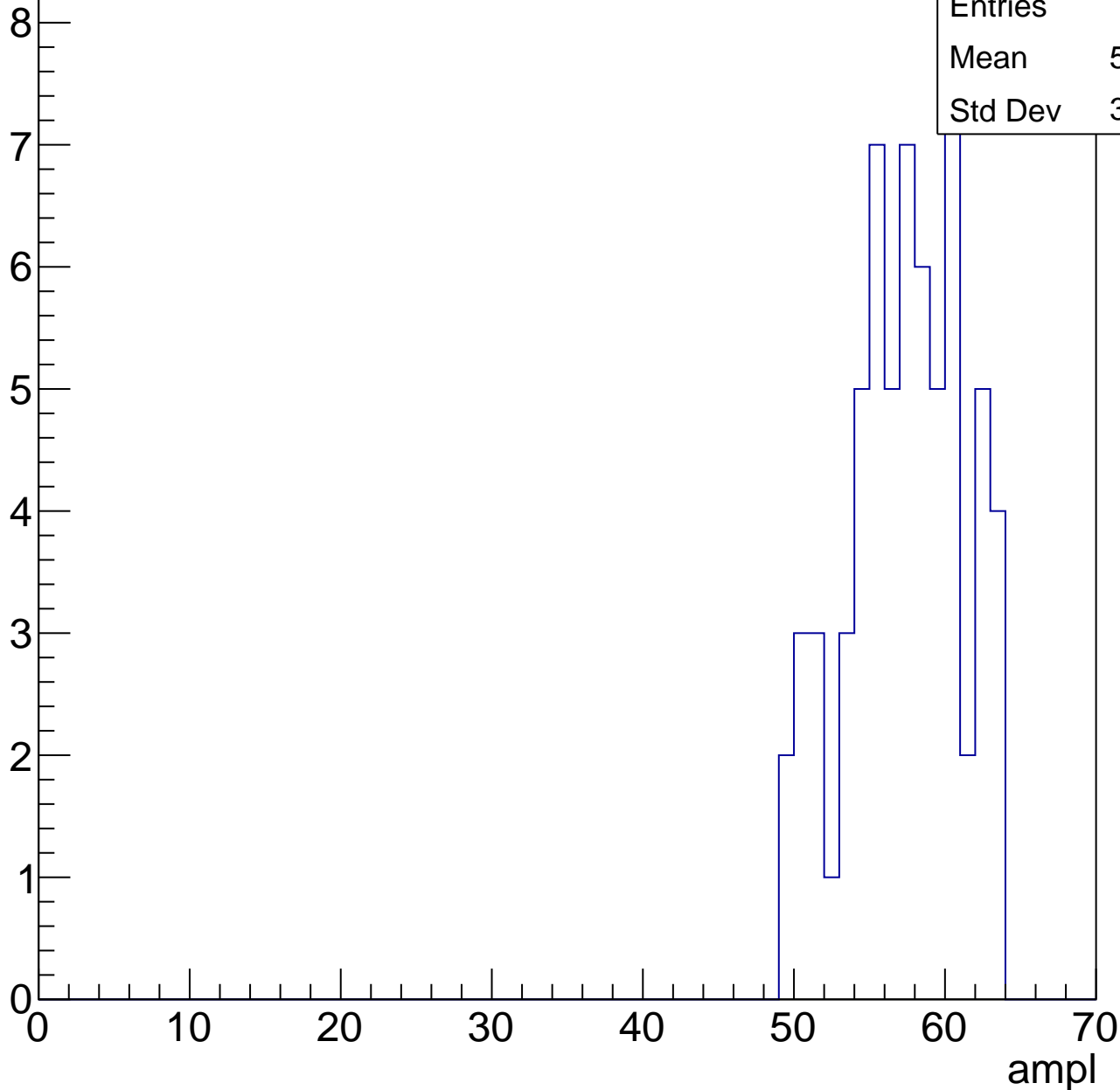


B1L103S, U21-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	56.86
Std Dev	3.757

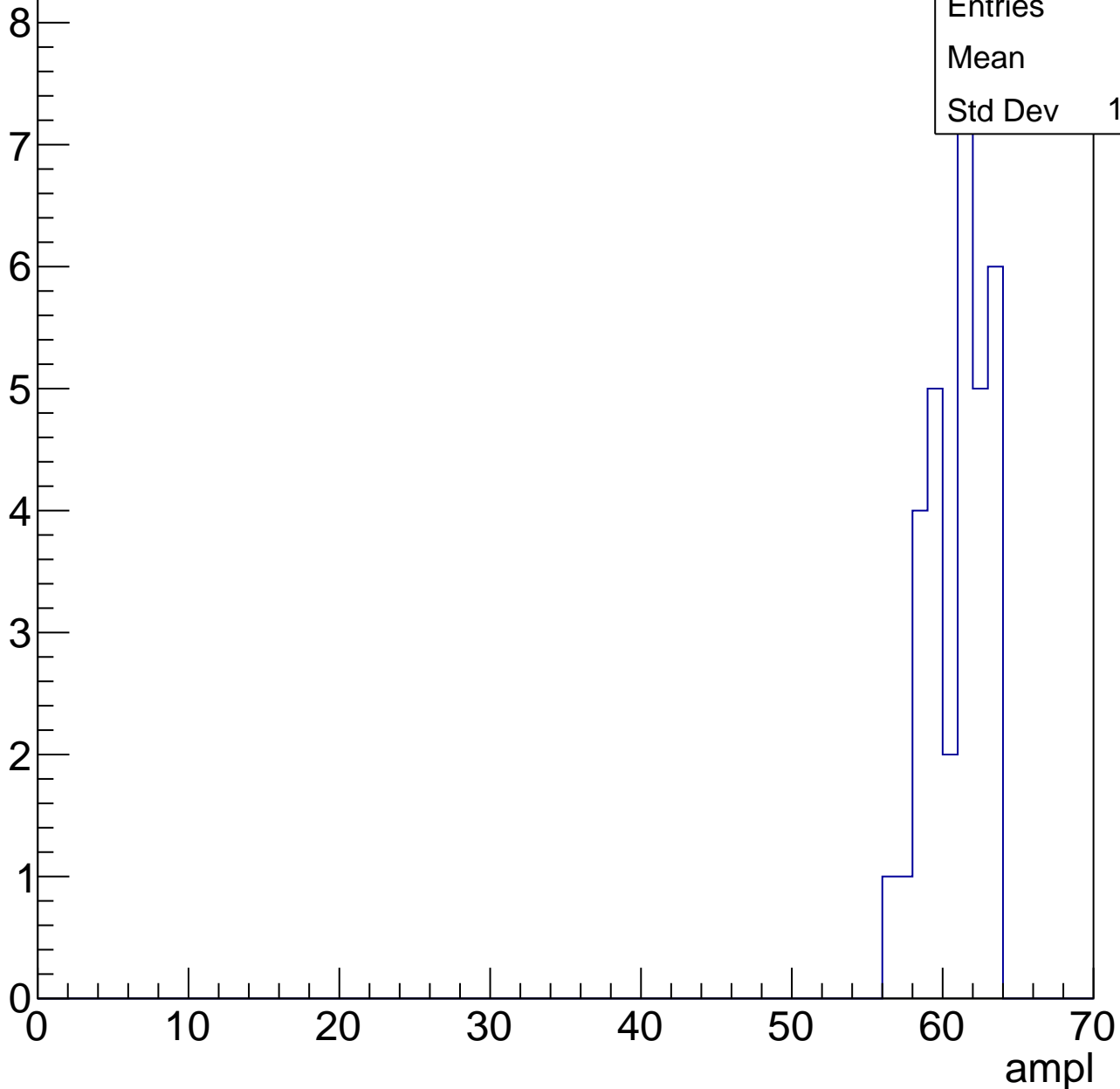


B1L103S, U21-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

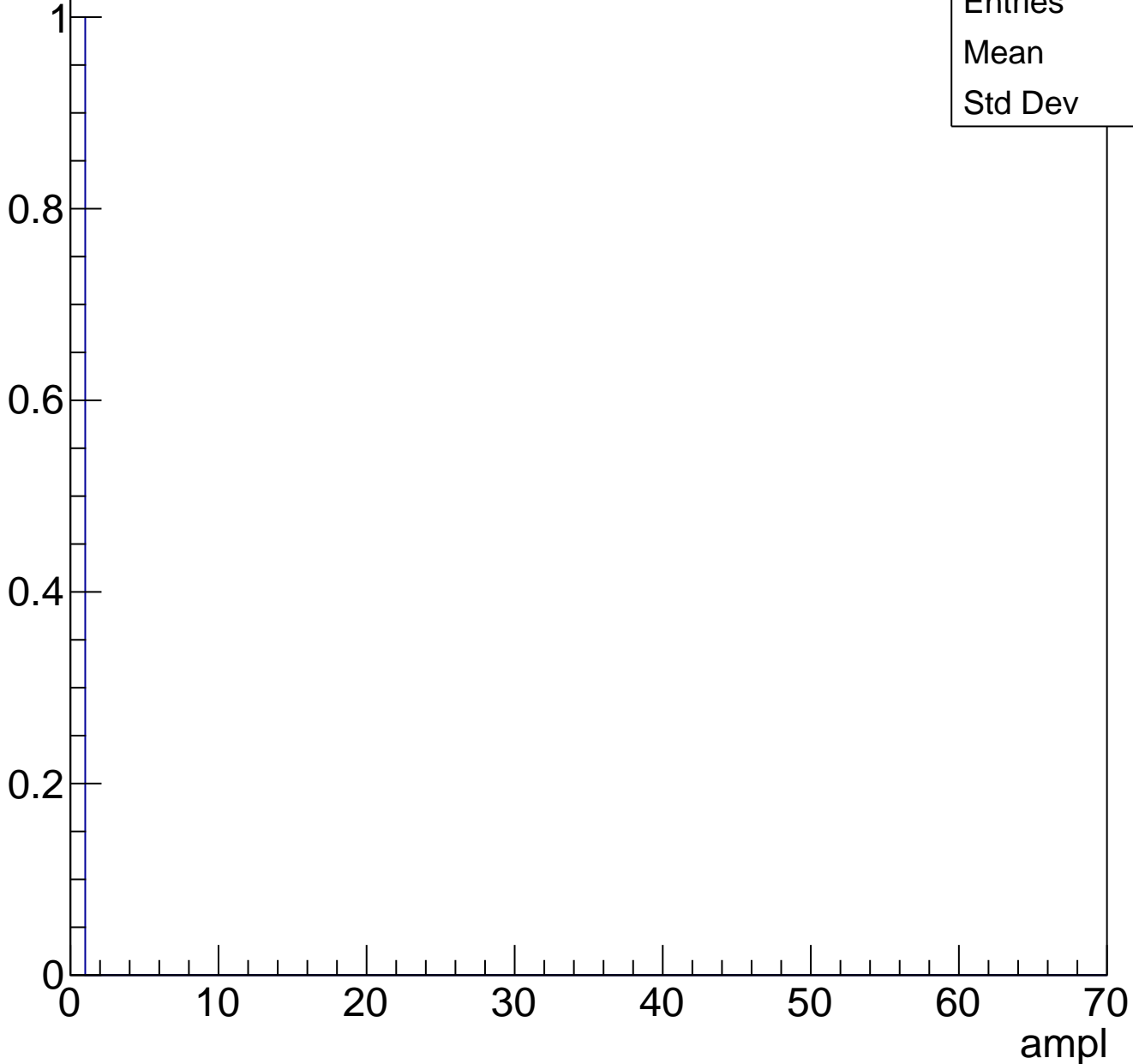
Entries	32
Mean	60.5
Std Dev	1.936



B1L103S, U21-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch38, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	23.97
Std Dev	9.694

Gaus mean : 27.5413

Gaus Width: 4.3683

Entry

12

10

8

6

4

2

0

0

10

20

30

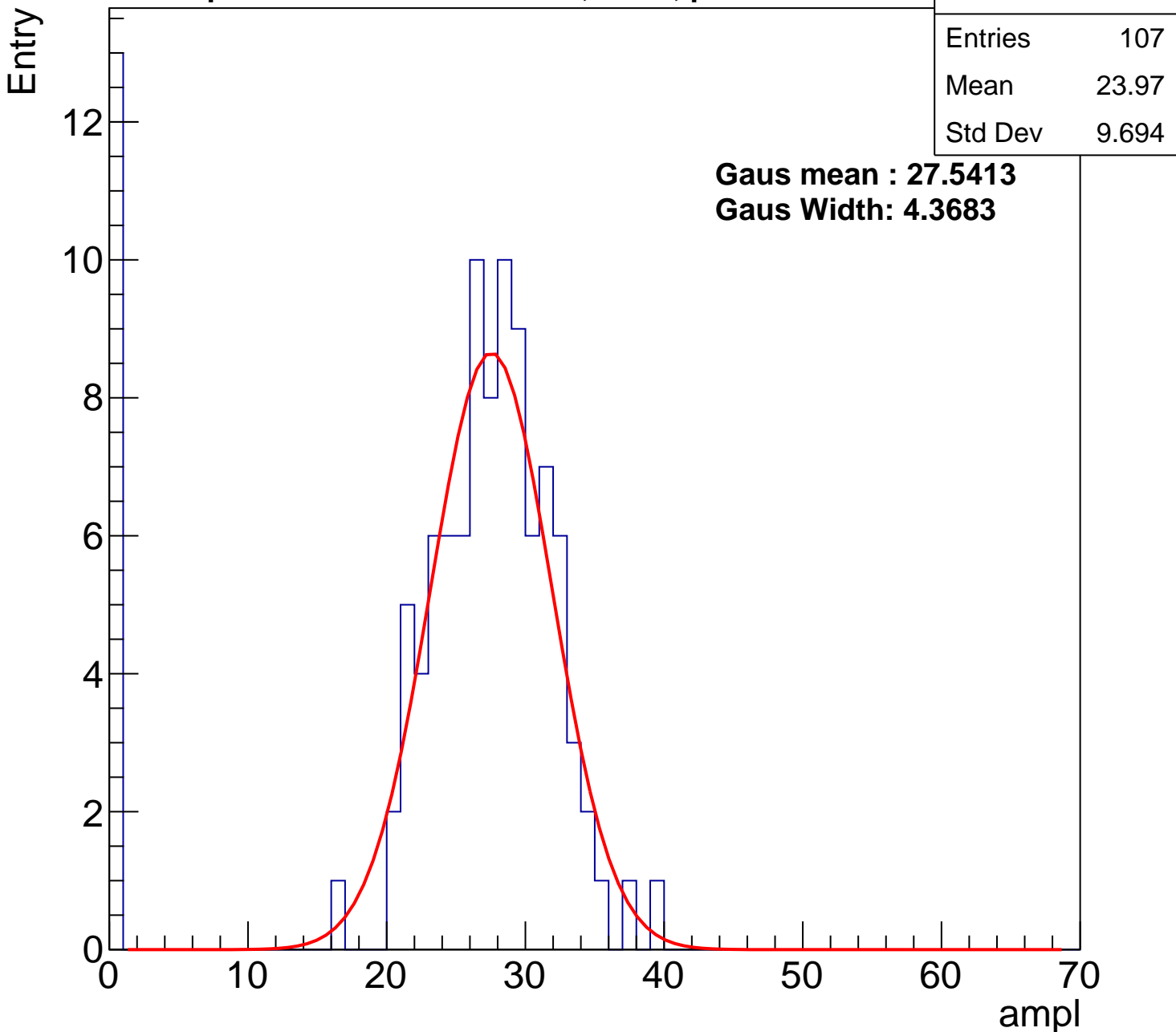
40

50

60

70

ampl



B1L103S, U21-ch38, adc1

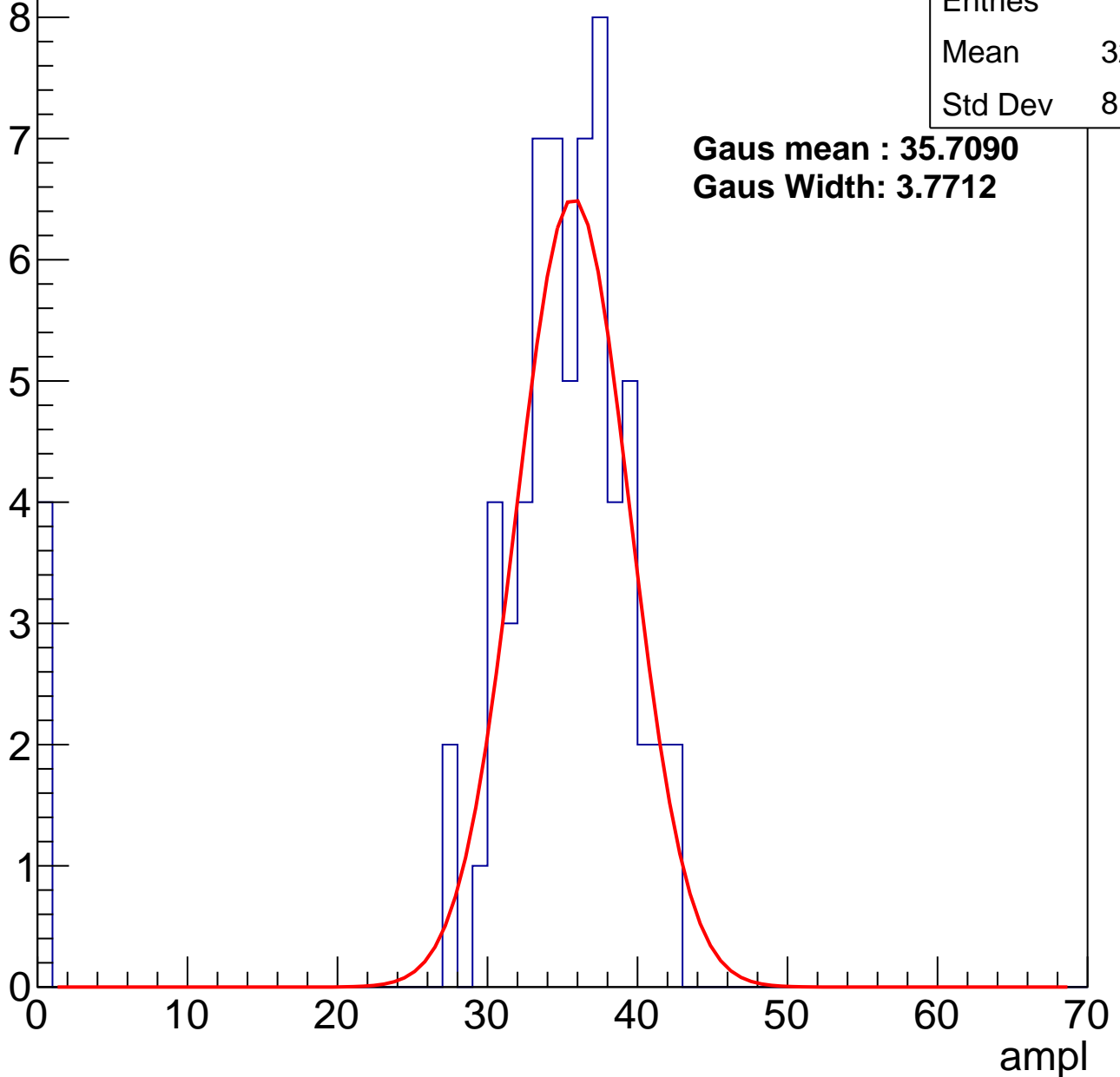
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.97
Std Dev	8.963

Gaus mean : 35.7090

Gaus Width: 3.7712



B1L103S, U21-ch38, adc2

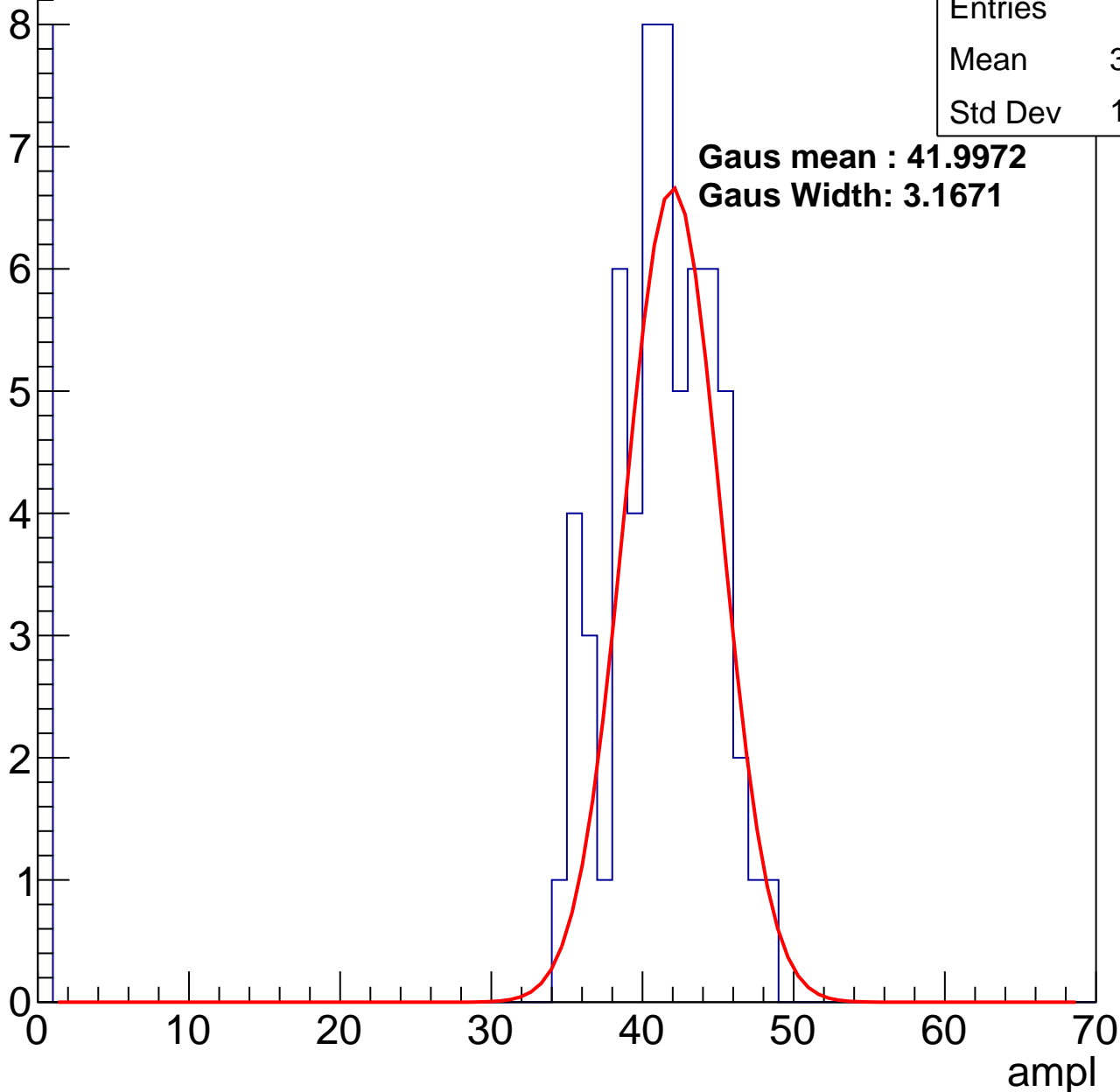
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	36.16
Std Dev	13.45

Gaus mean : 41.9972

Gaus Width: 3.1671

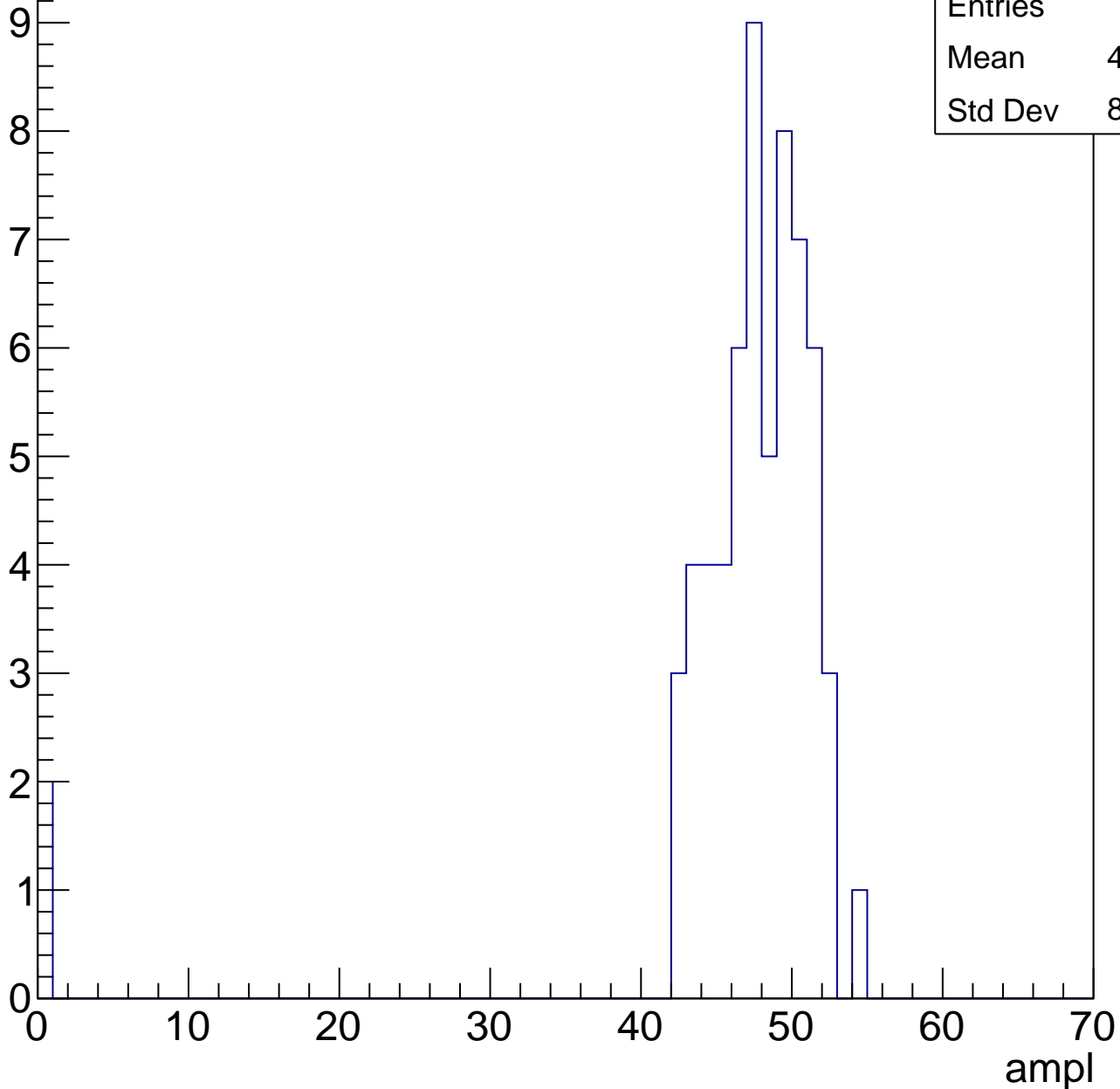


B1L103S, U21-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	45.98
Std Dev	8.864



B1L103S, U21-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	53.2
Std Dev	3.353

Entry

10

8

6

4

2

0

0

10

20

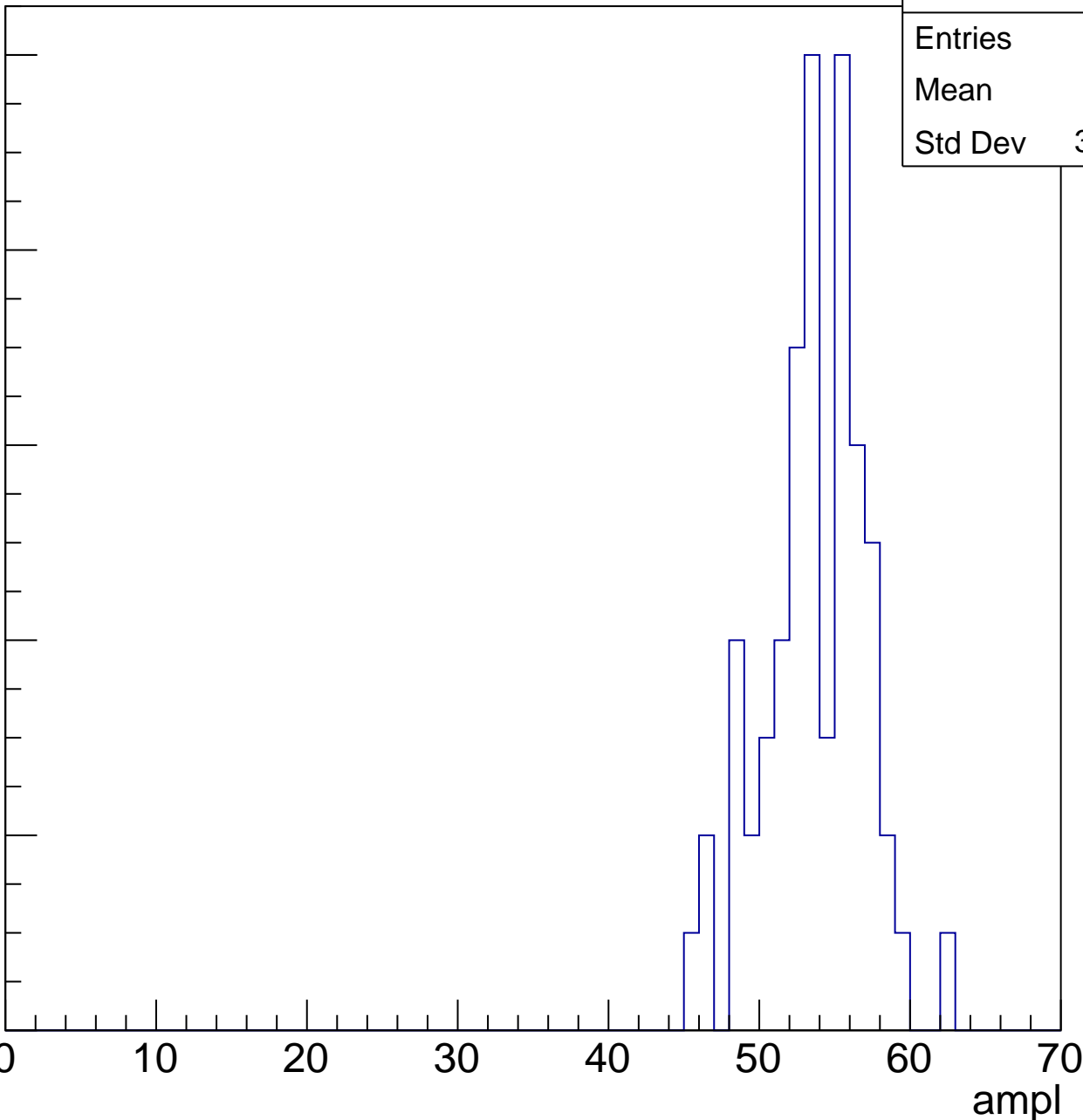
30

40

50

60

ampl

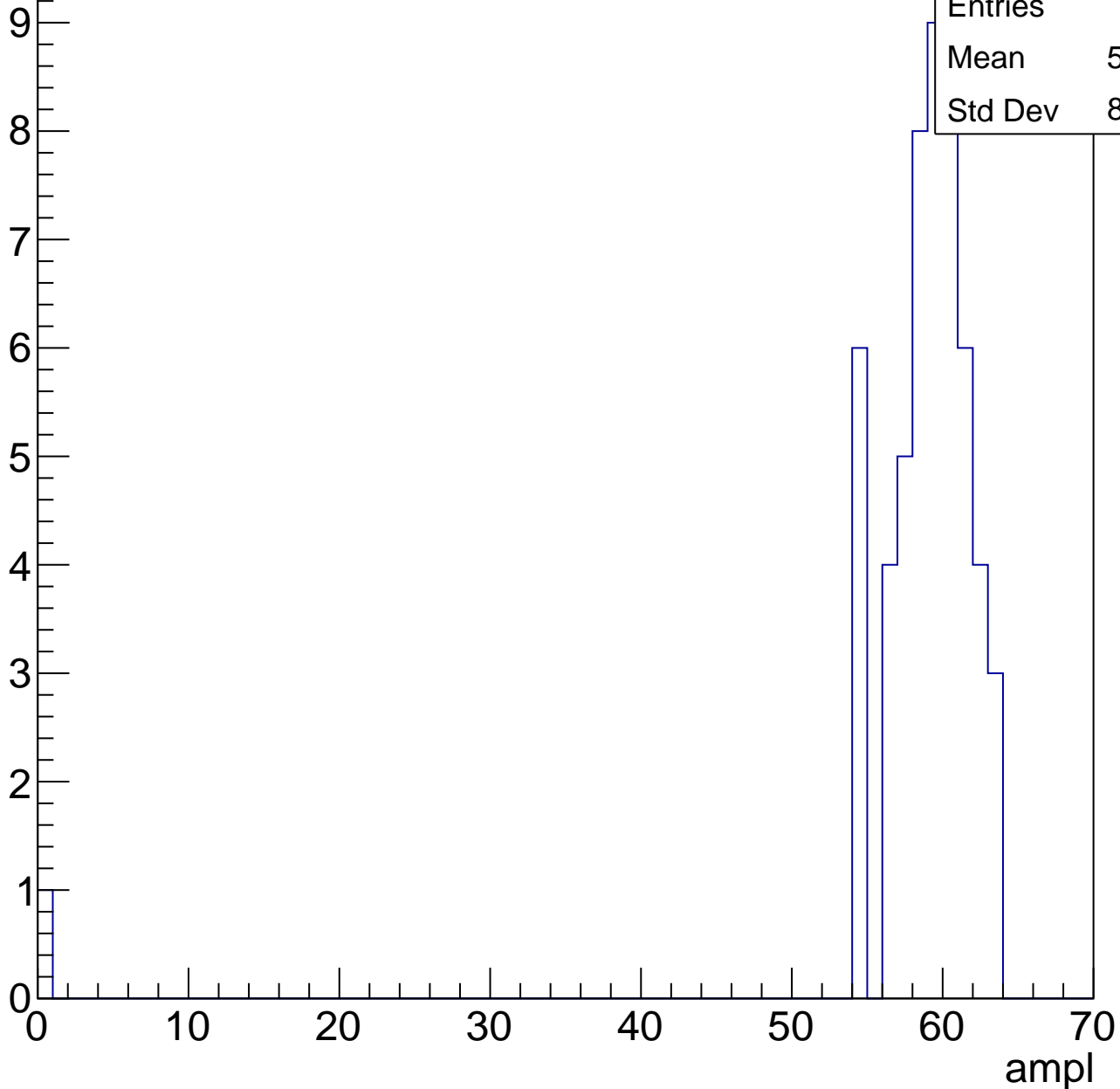


B1L103S, U21-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.65
Std Dev	8.216

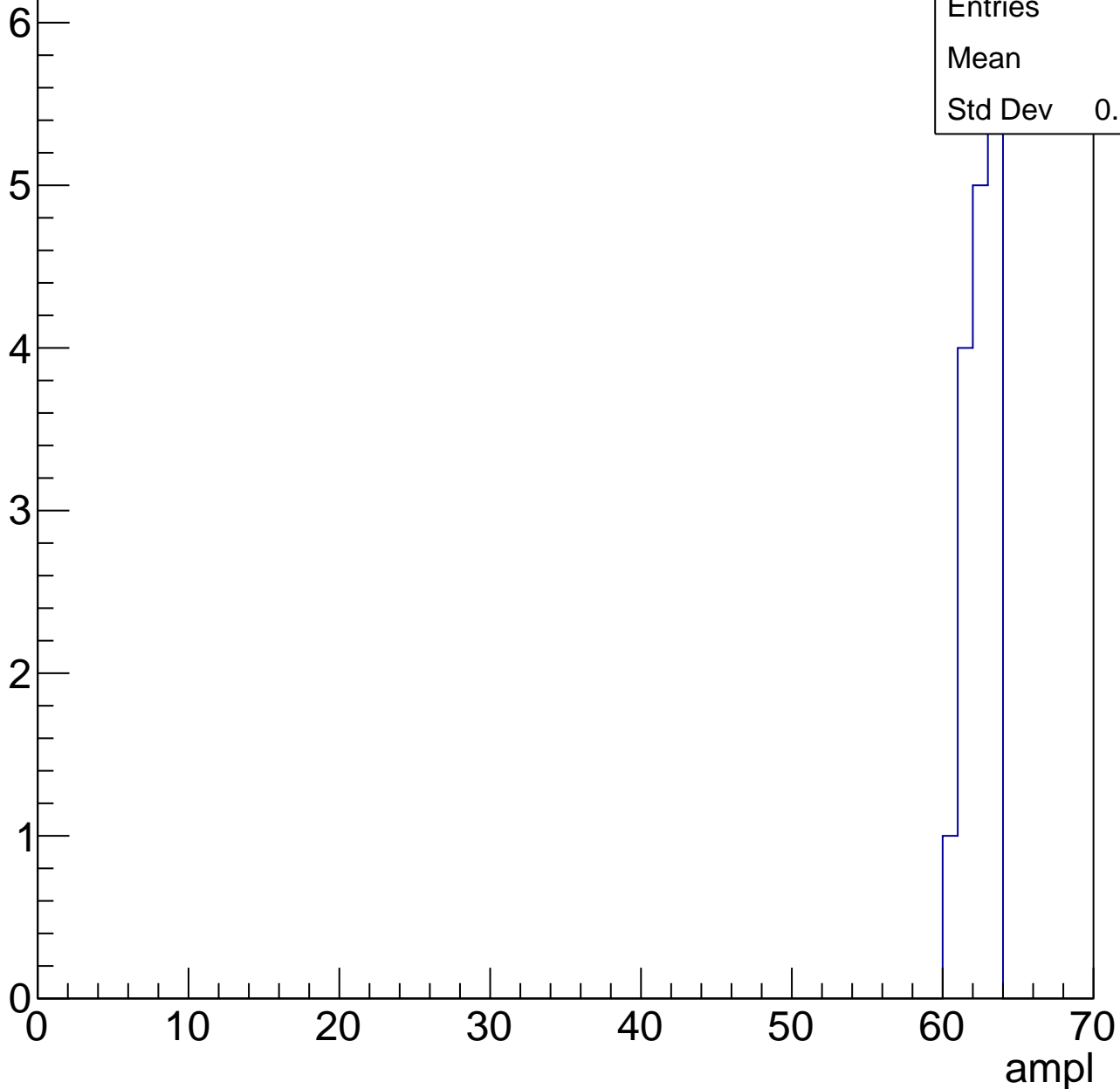


B1L103S, U21-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	62
Std Dev	0.9354



B1L103S, U21-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U21-ch39, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	21.96
Std Dev	11.6

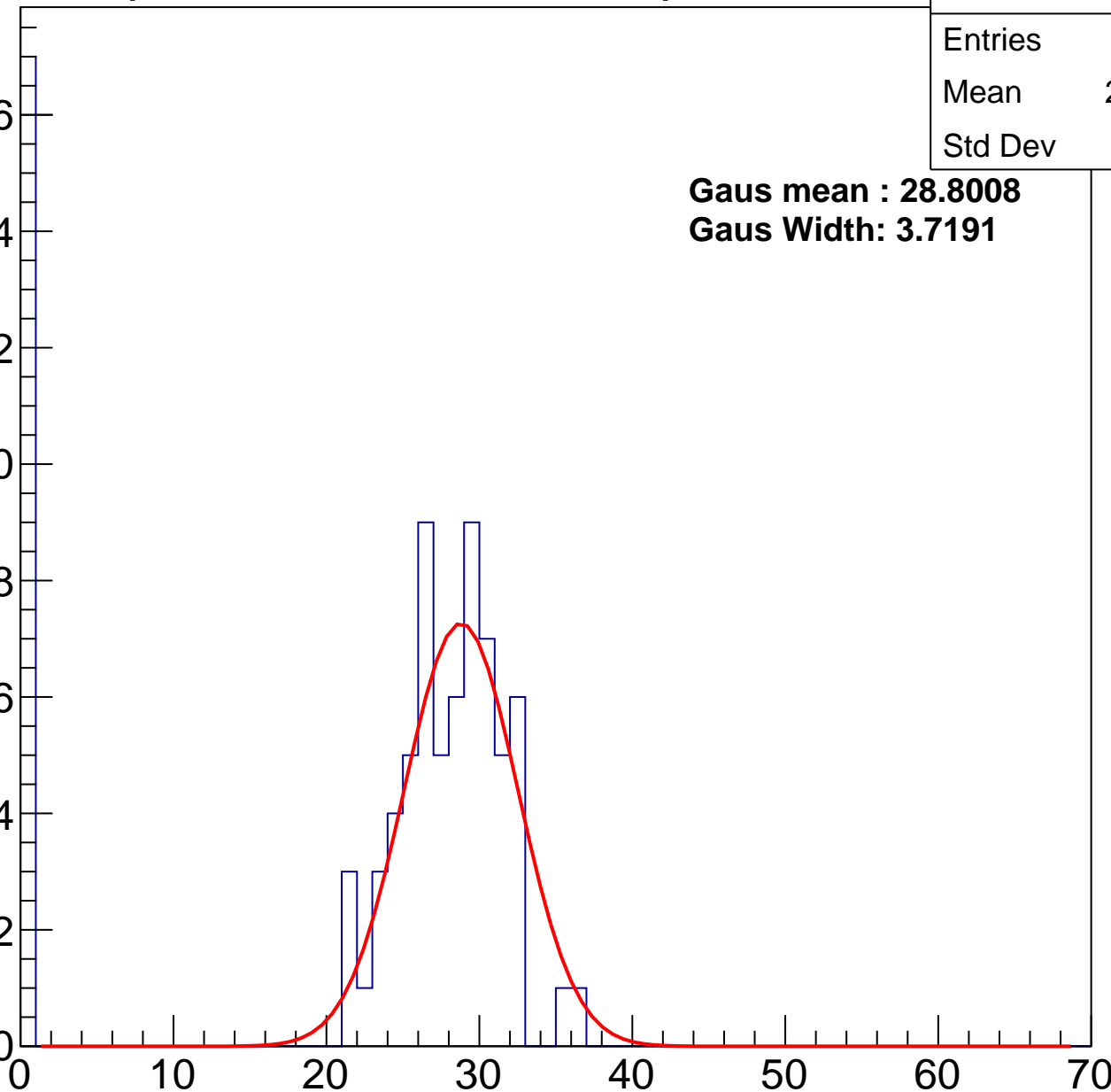
Gaus mean : 28.8008

Gaus Width: 3.7191

Entry

16
14
12
10
8
6
4
2
0

ampl



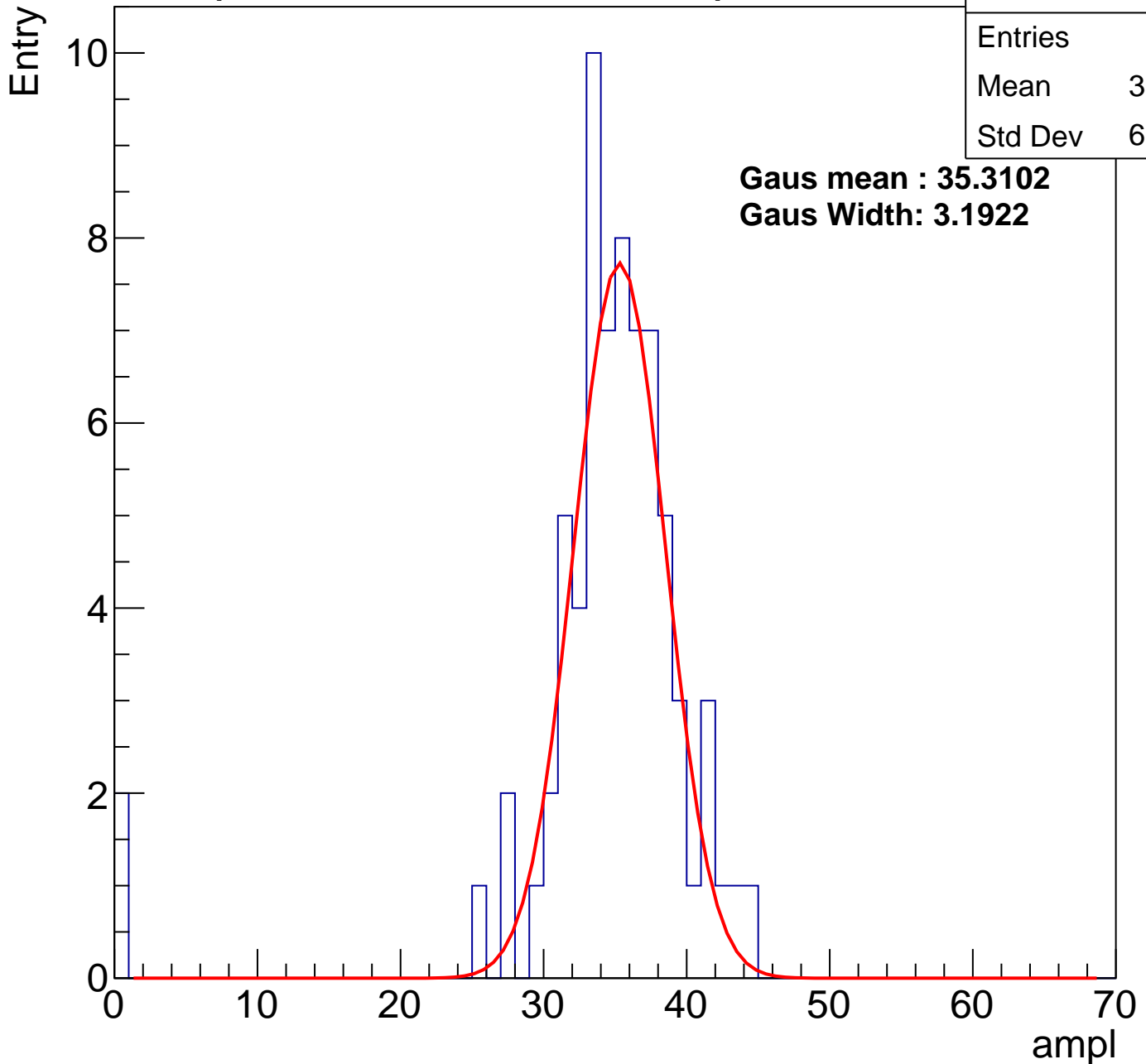
B1L103S, U21-ch39, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	33.93
Std Dev	6.818

Gaus mean : 35.3102

Gaus Width: 3.1922



B1L103S, U21-ch39, adc2

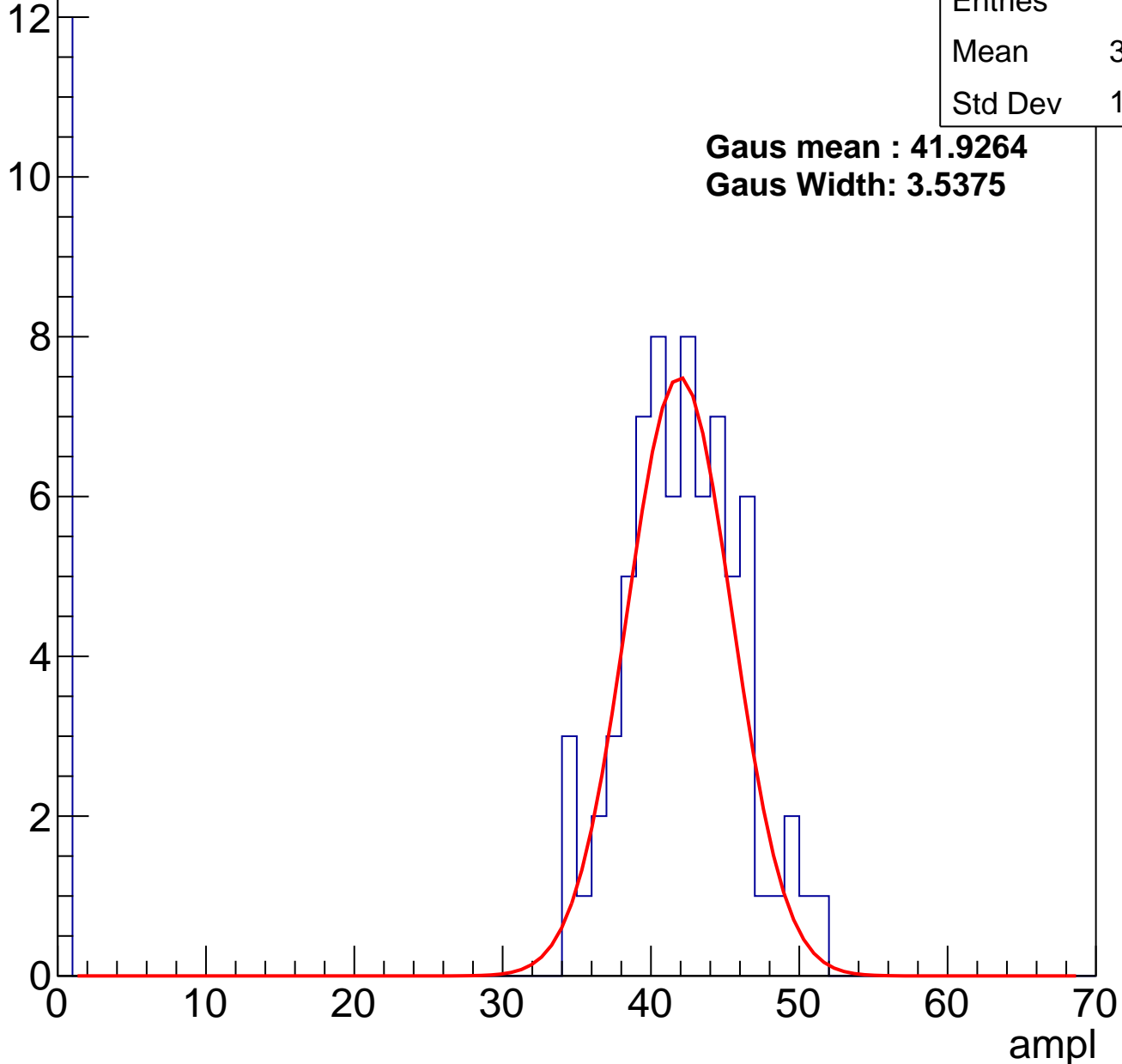
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	35.84
Std Dev	14.95

Gaus mean : 41.9264

Gaus Width: 3.5375

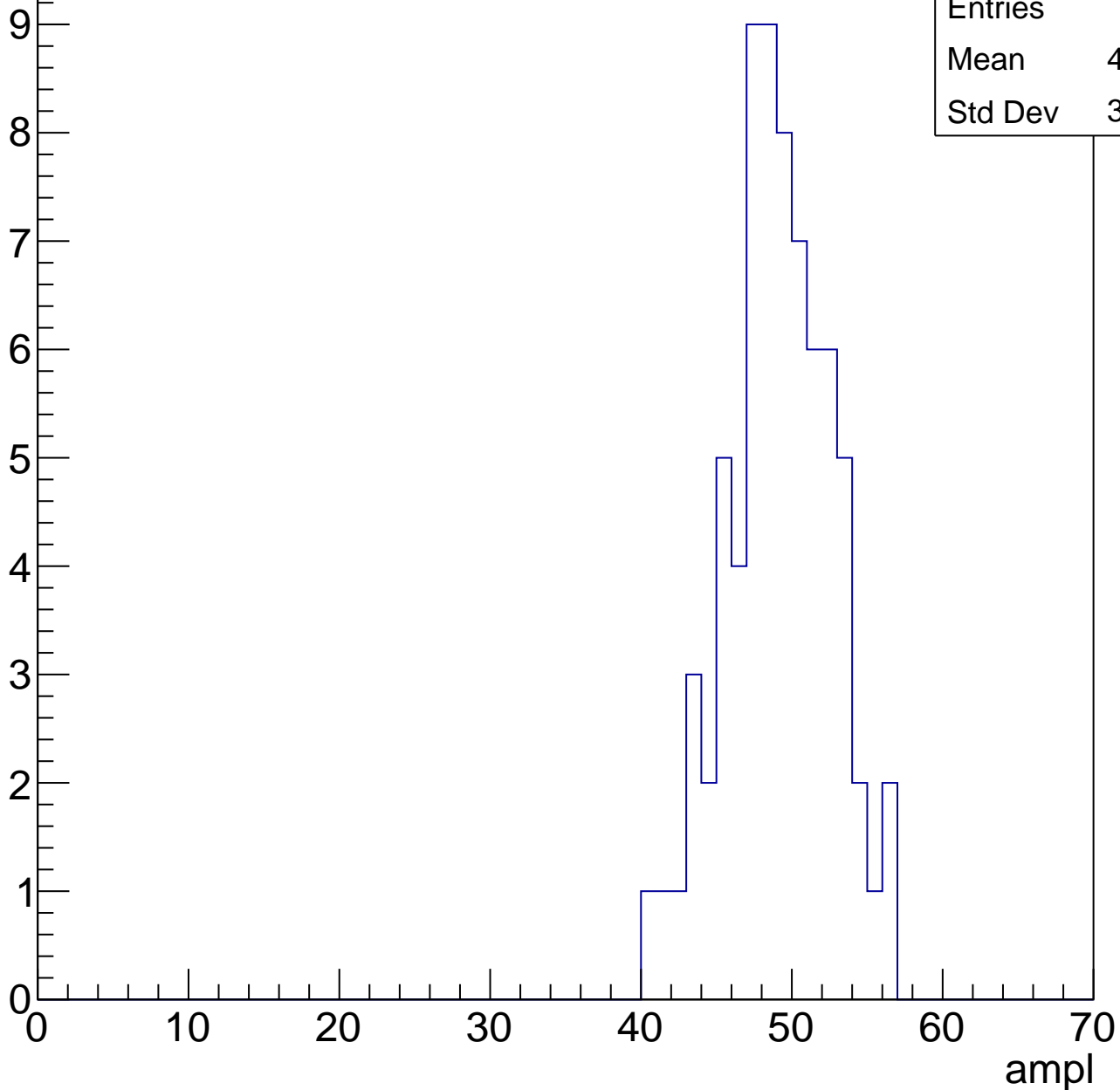
Entry



B1L103S, U21-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

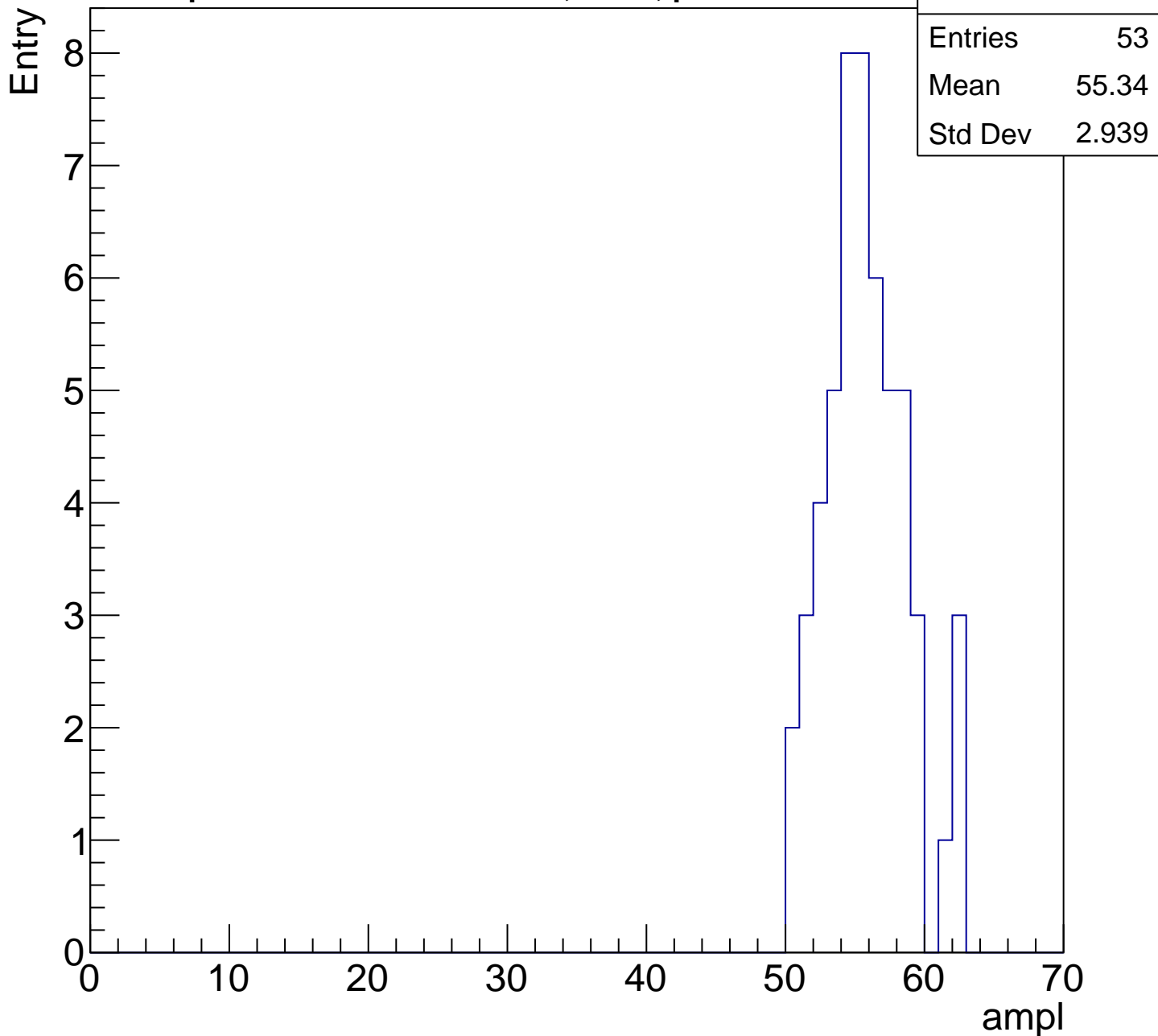
Entry



Entries	72
Mean	48.67
Std Dev	3.444

B1L103S, U21-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

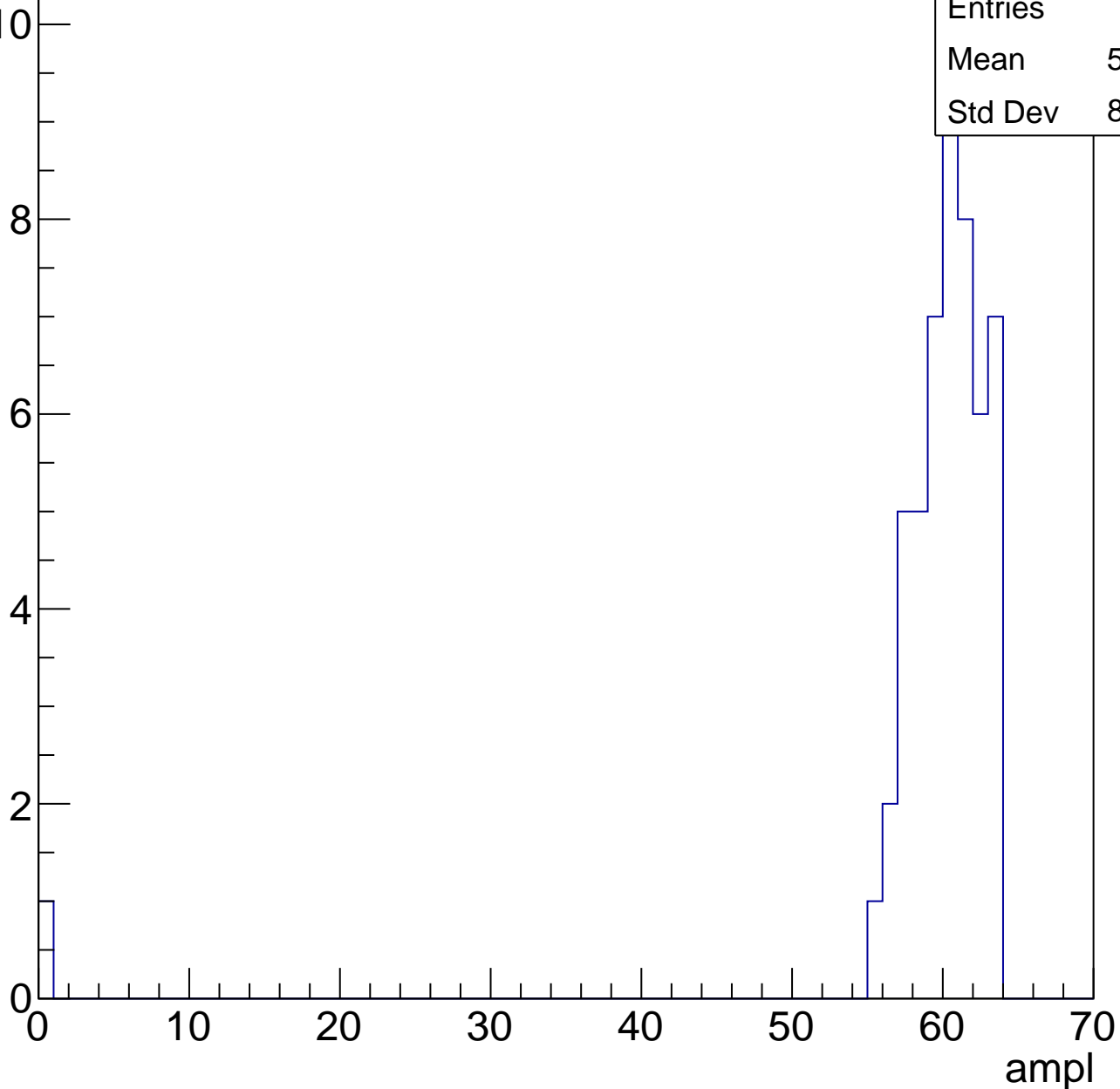


B1L103S, U21-ch39, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.77
Std Dev	8.487



B1L103S, U21-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	62.4
Std Dev	0.8

B1L103S, U21-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch40, adc0

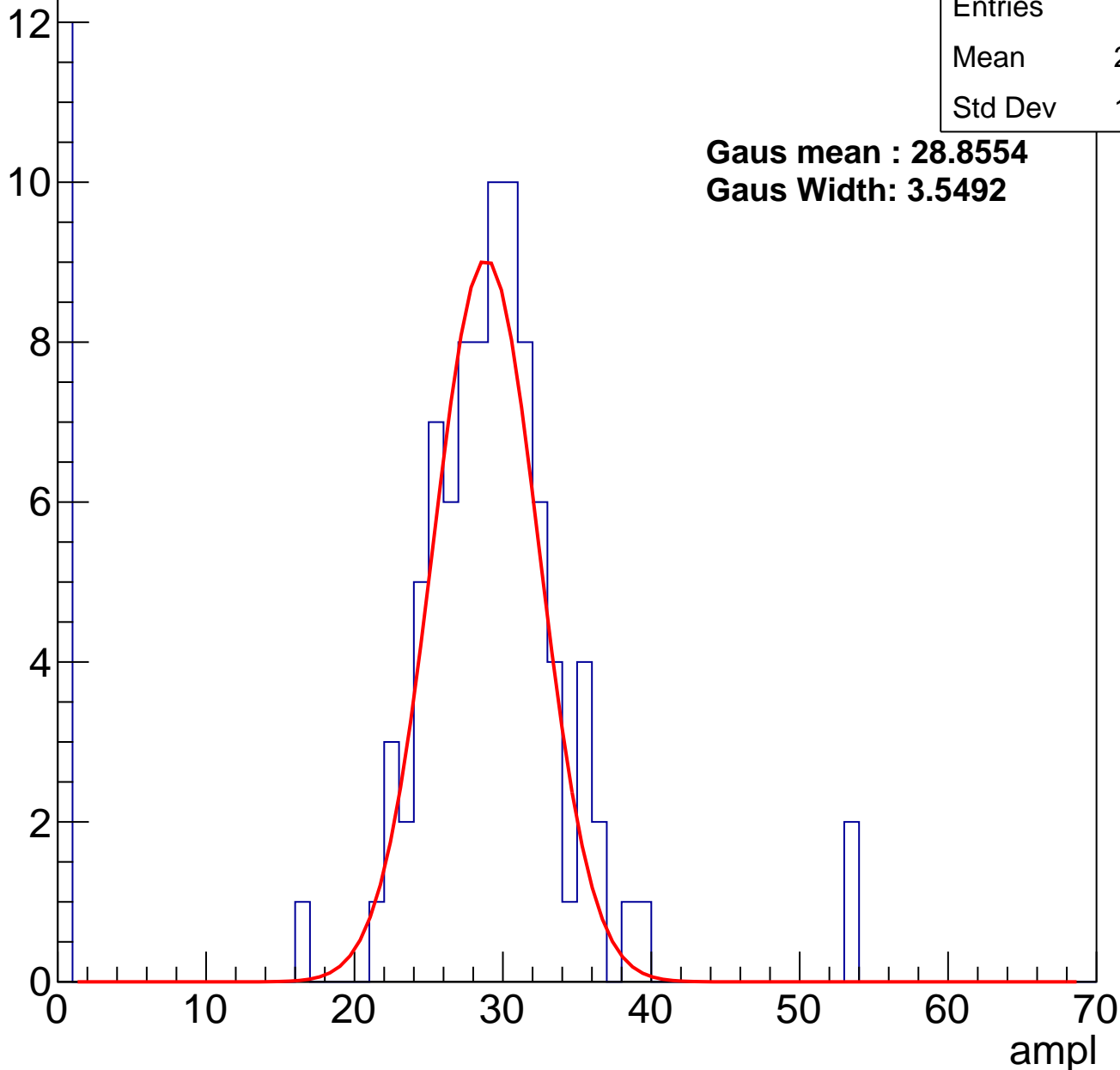
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	25.79
Std Dev	10.66

Gaus mean : 28.8554

Gaus Width: 3.5492

Entry



B1L103S, U21-ch40, adc1

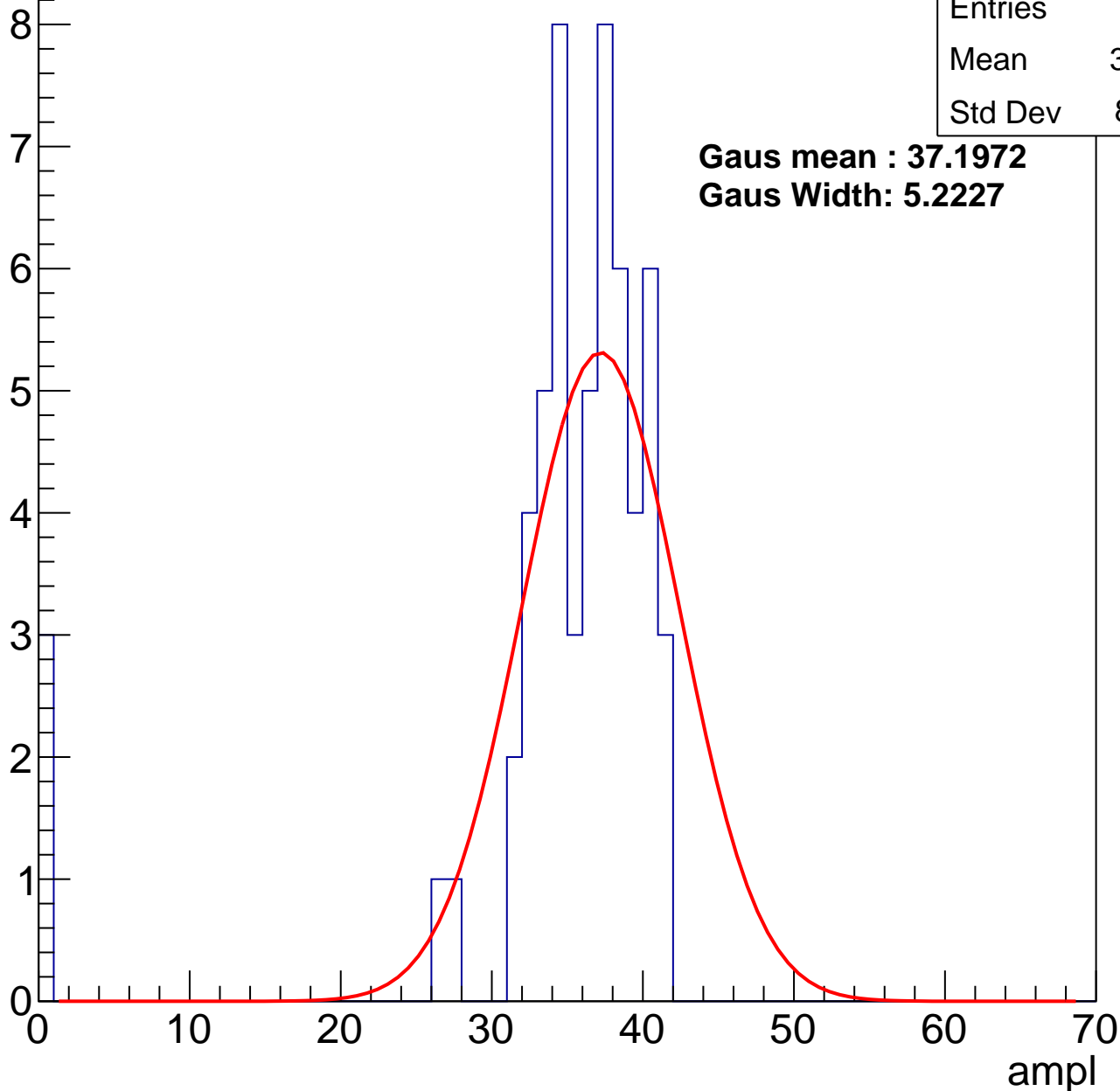
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	34.03
Std Dev	8.511

Gaus mean : 37.1972

Gaus Width: 5.2227



B1L103S, U21-ch40, adc2

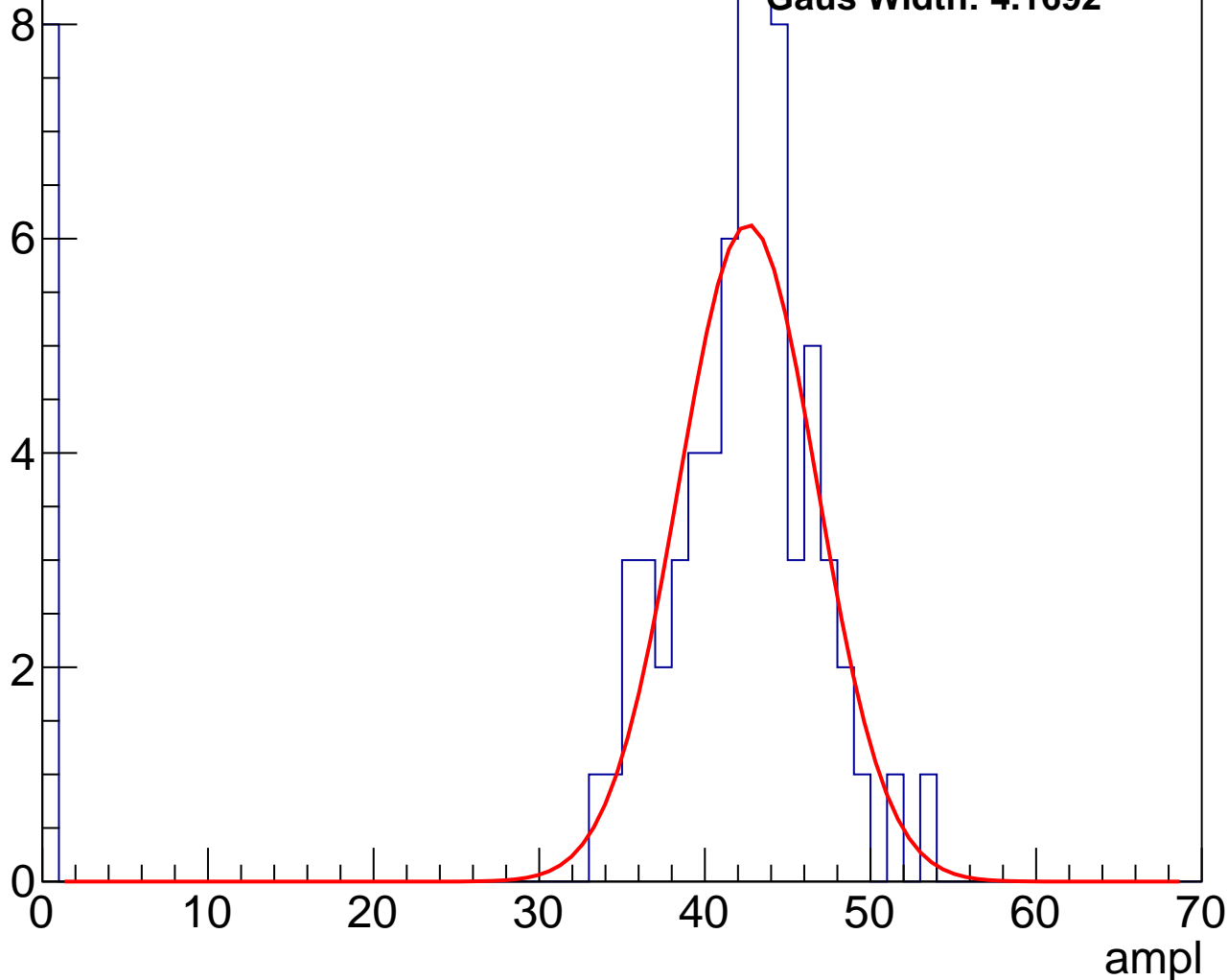
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	37.74
Std Dev	13.3

Gaus mean : 42.6072

Gaus Width: 4.1692

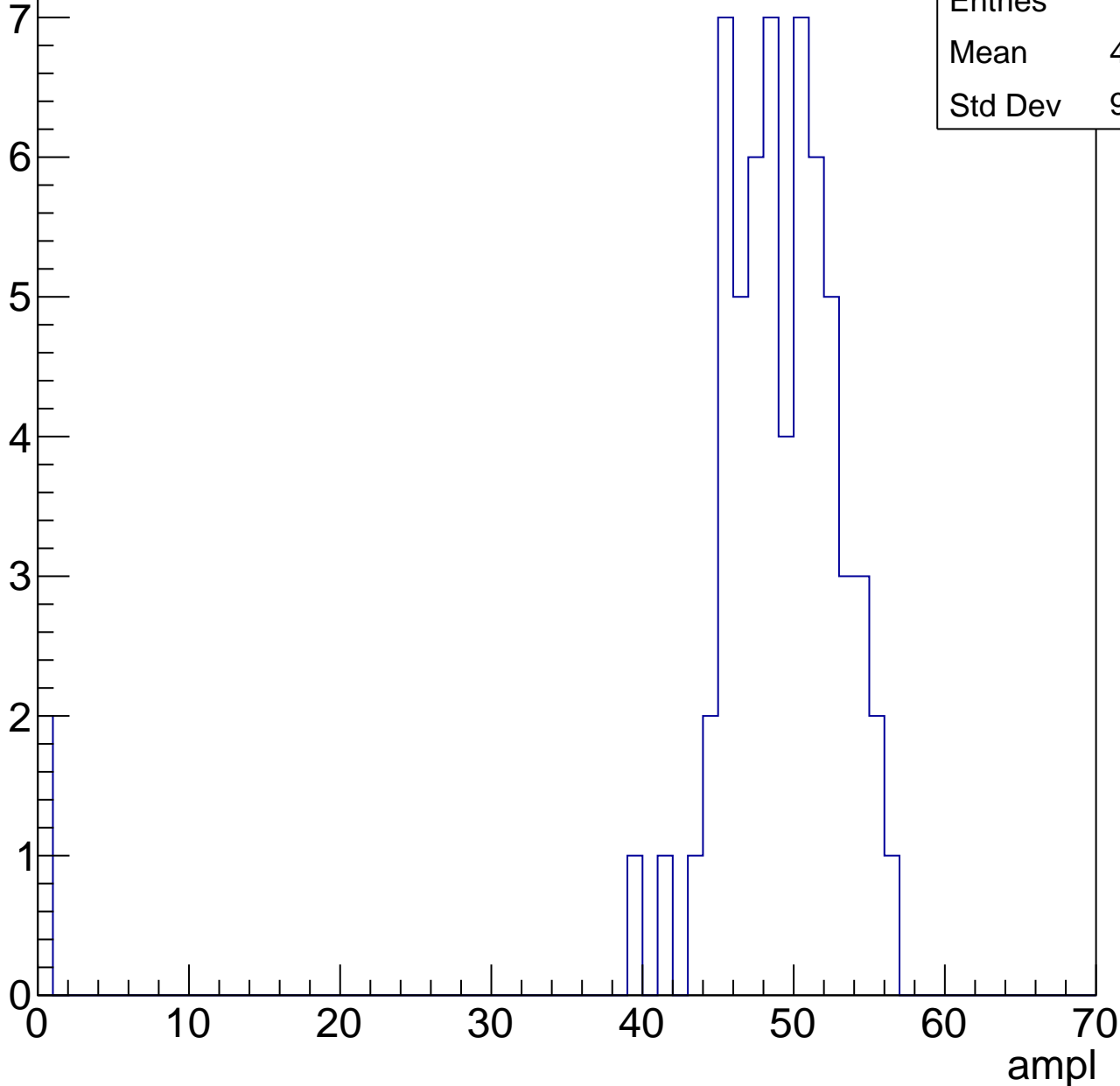


B1L103S, U21-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.19
Std Dev	9.218

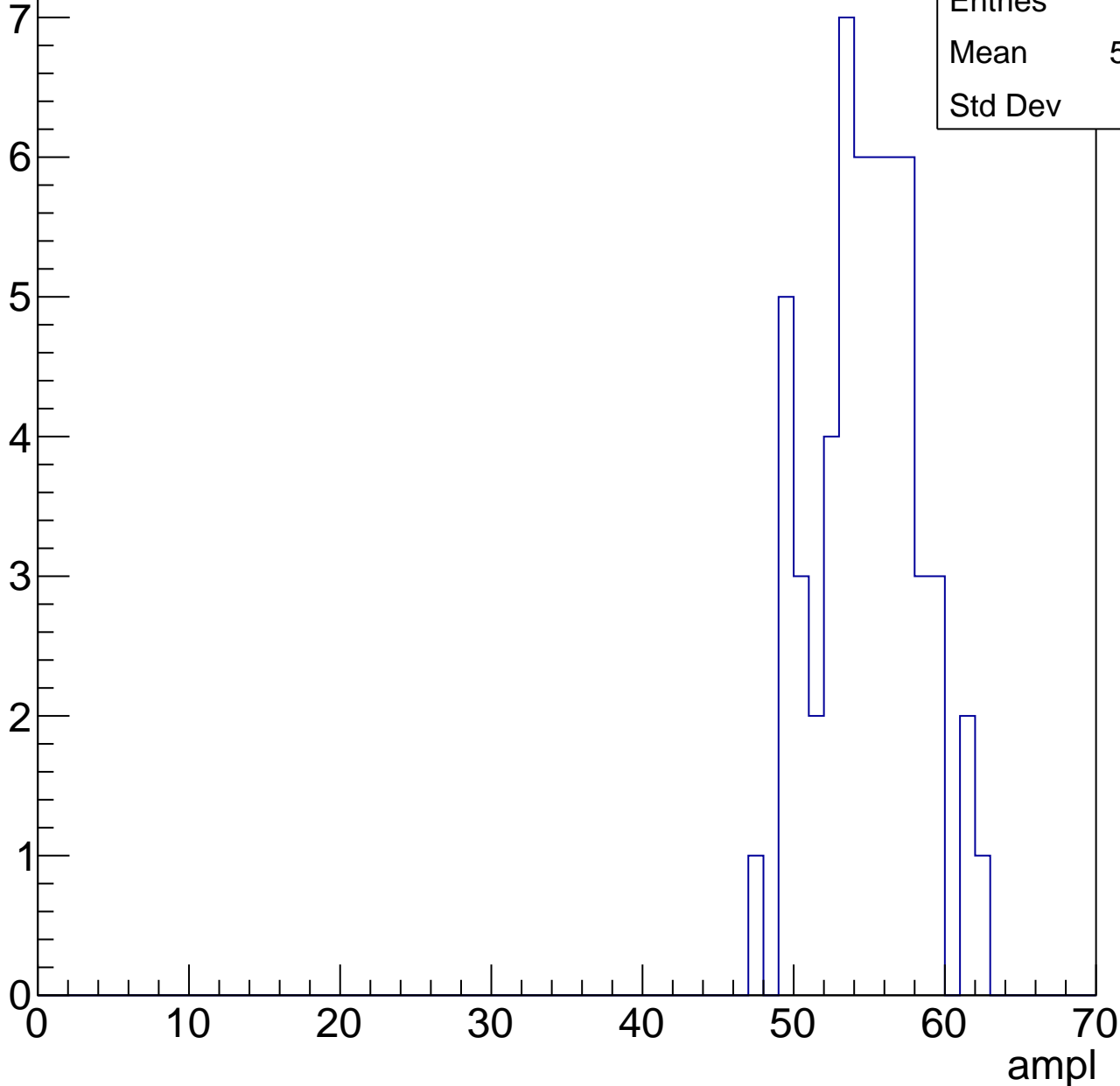


B1L103S, U21-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.36
Std Dev	3.37

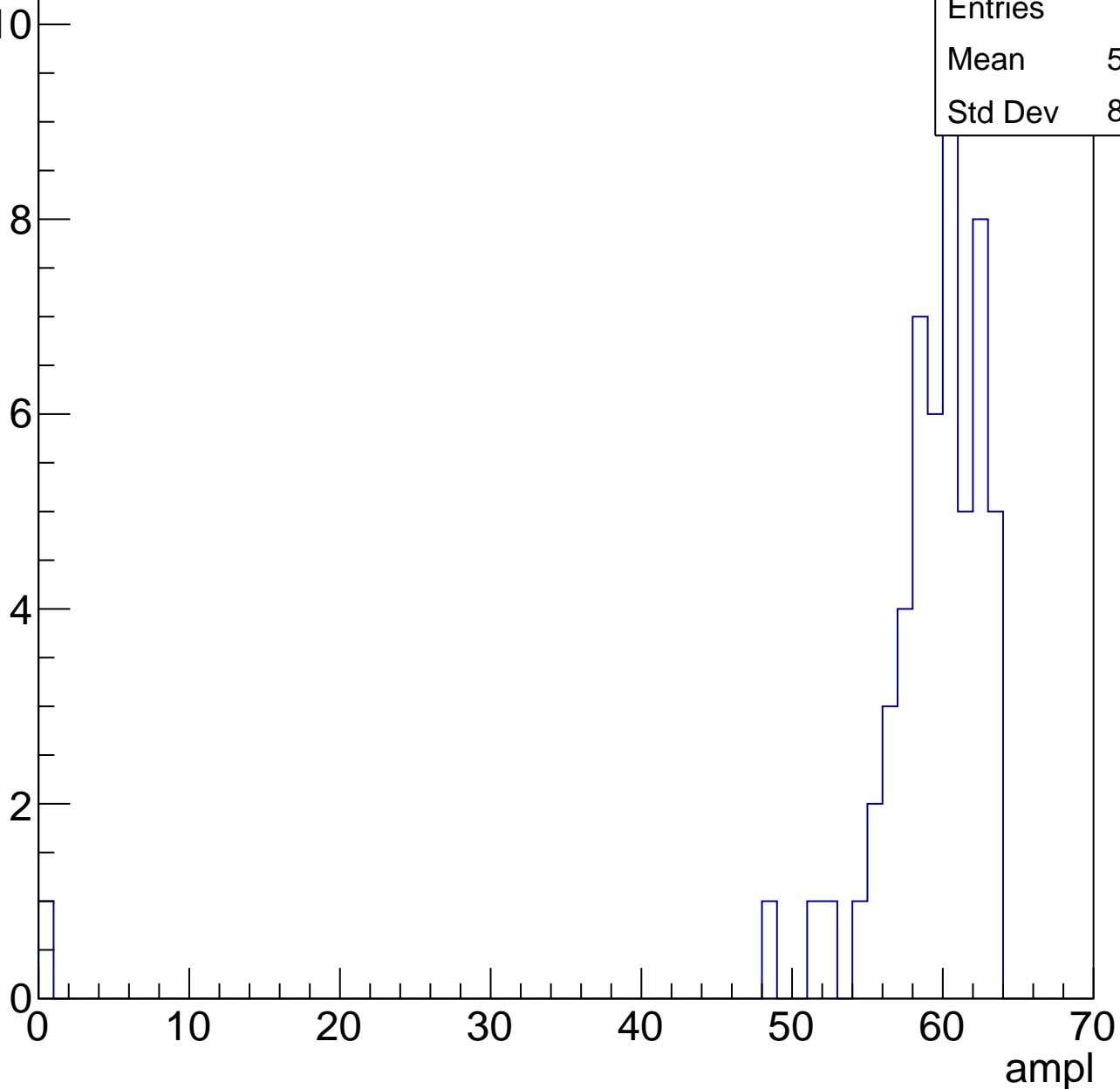


B1L103S, U21-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.95
Std Dev	8.467

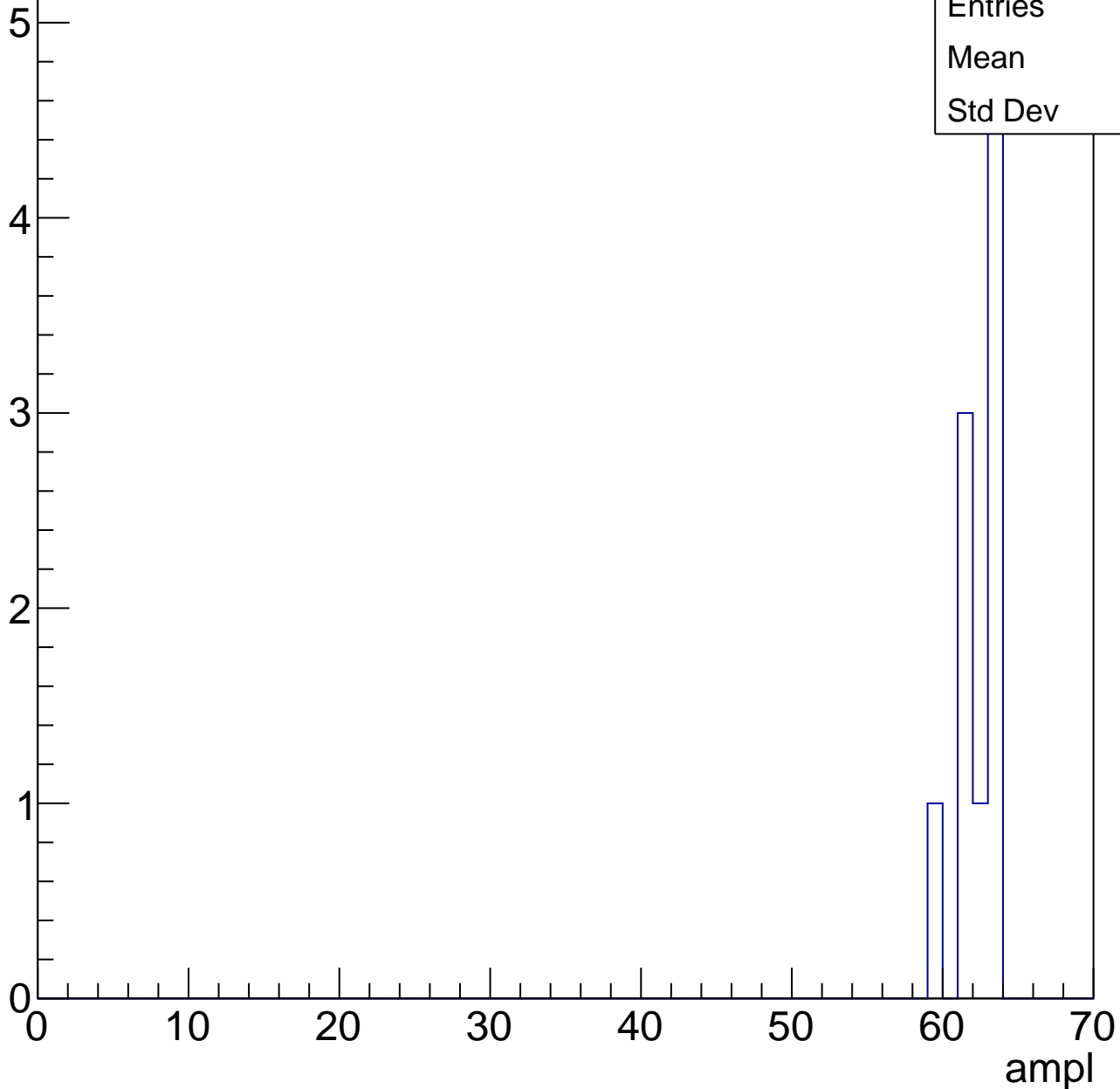


B1L103S, U21-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	61.9
Std Dev	1.3



B1L103S, U21-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch41, adc0

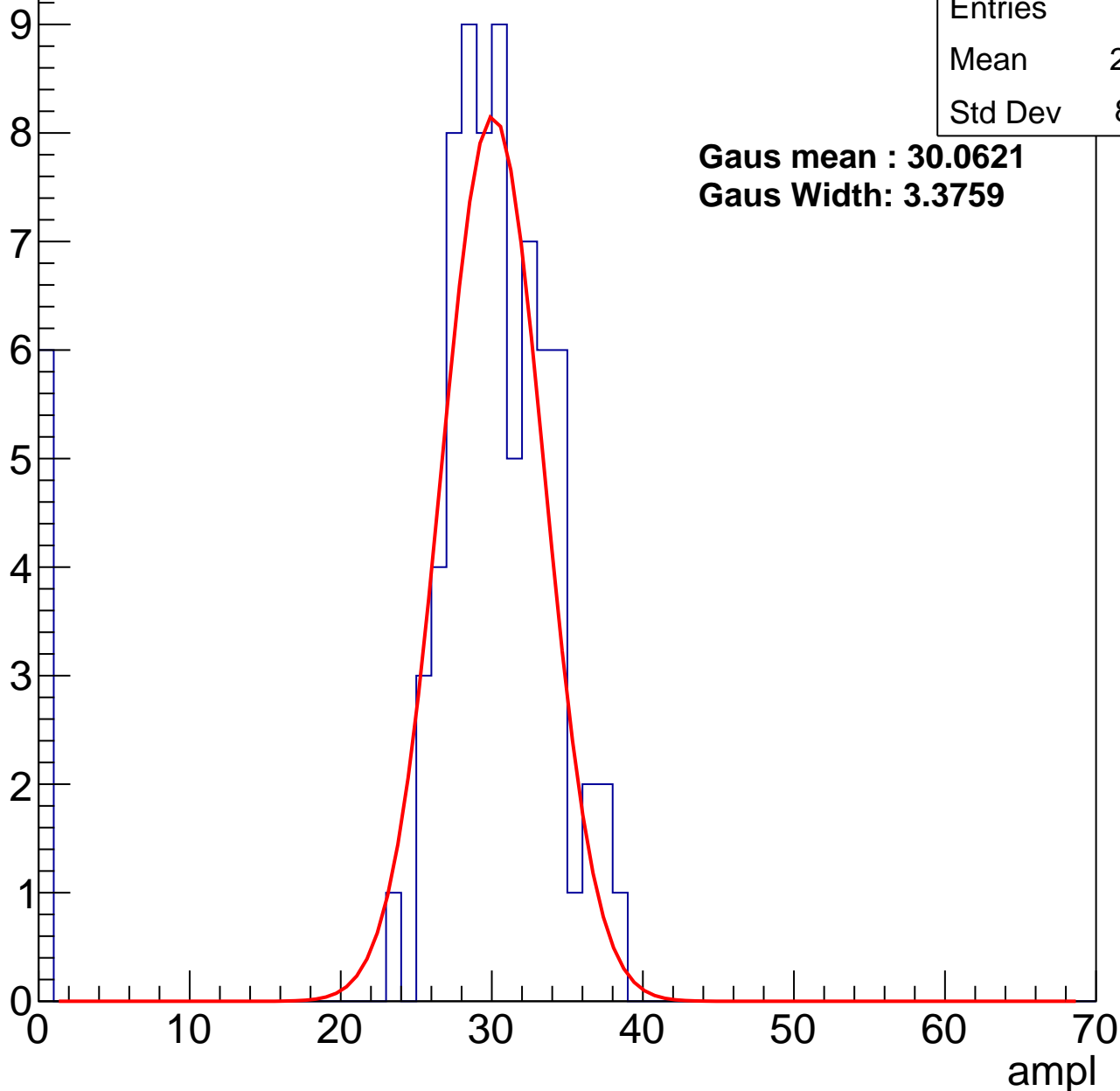
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	27.85
Std Dev	8.611

Gaus mean : 30.0621

Gaus Width: 3.3759



B1L103S, U21-ch41, adc1

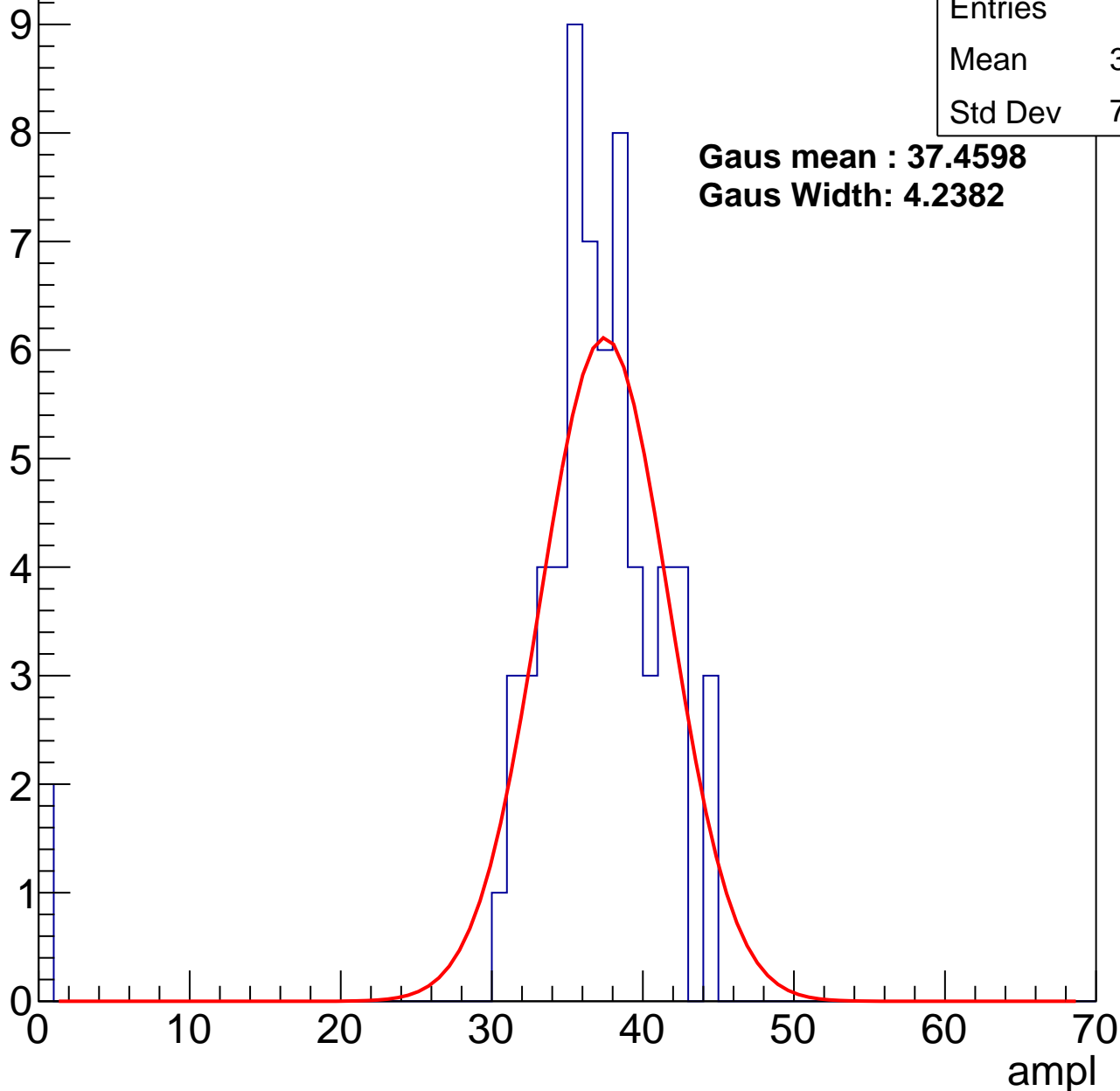
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	35.69
Std Dev	7.194

Gaus mean : 37.4598

Gaus Width: 4.2382



B1L103S, U21-ch41, adc2

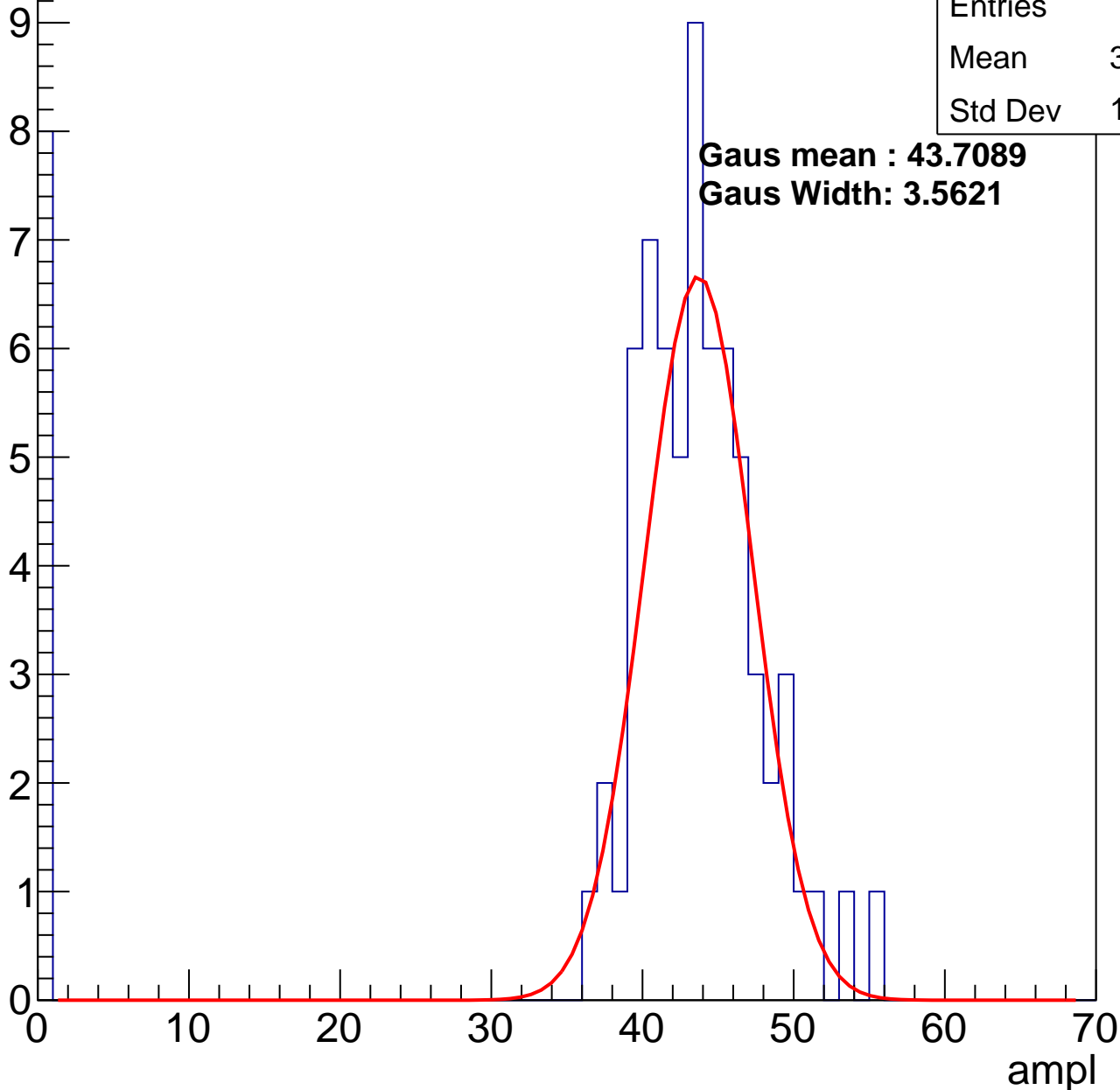
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	38.68
Std Dev	13.94

Gaus mean : 43.7089

Gaus Width: 3.5621

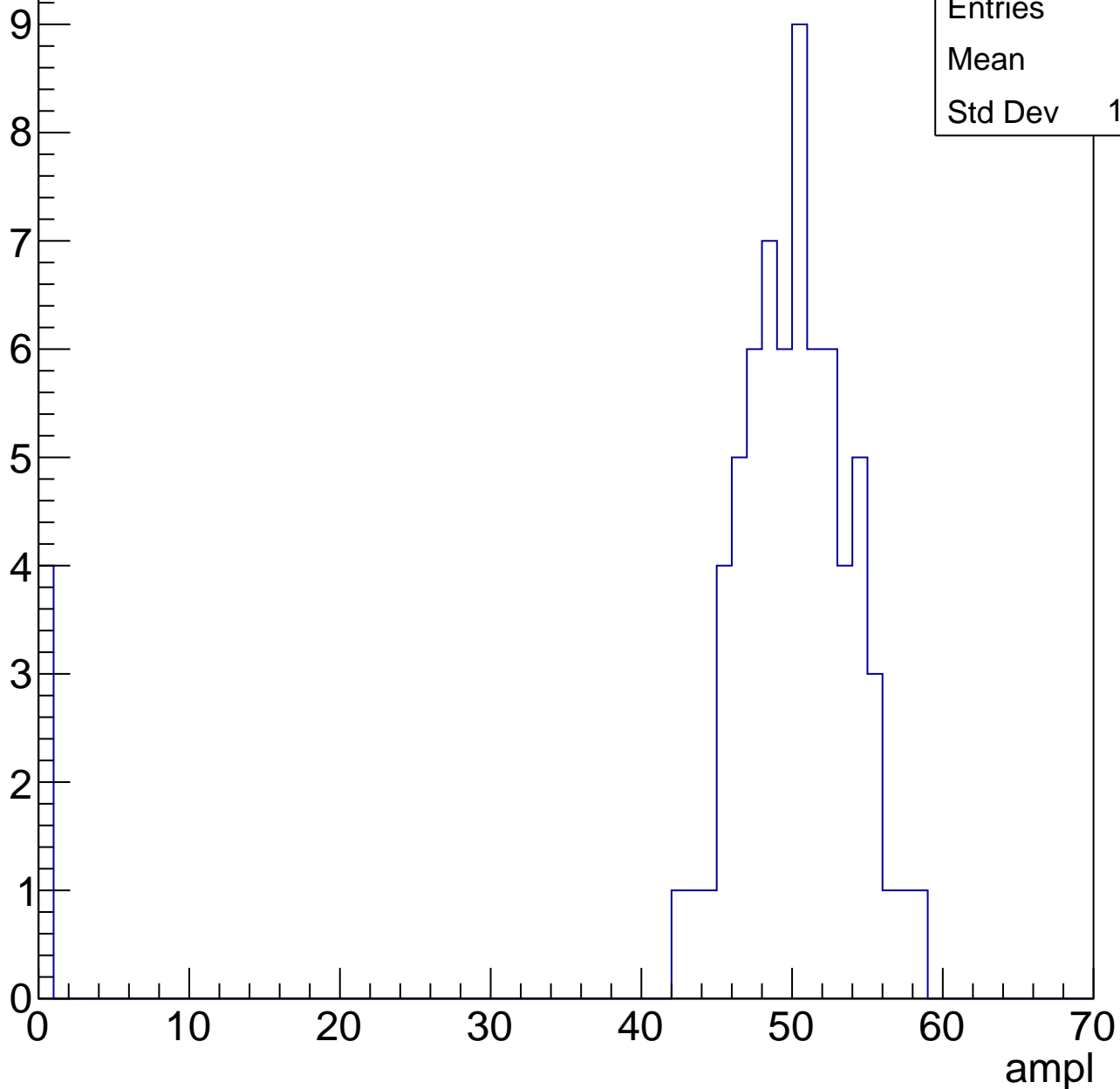


B1L103S, U21-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	47
Std Dev	11.96

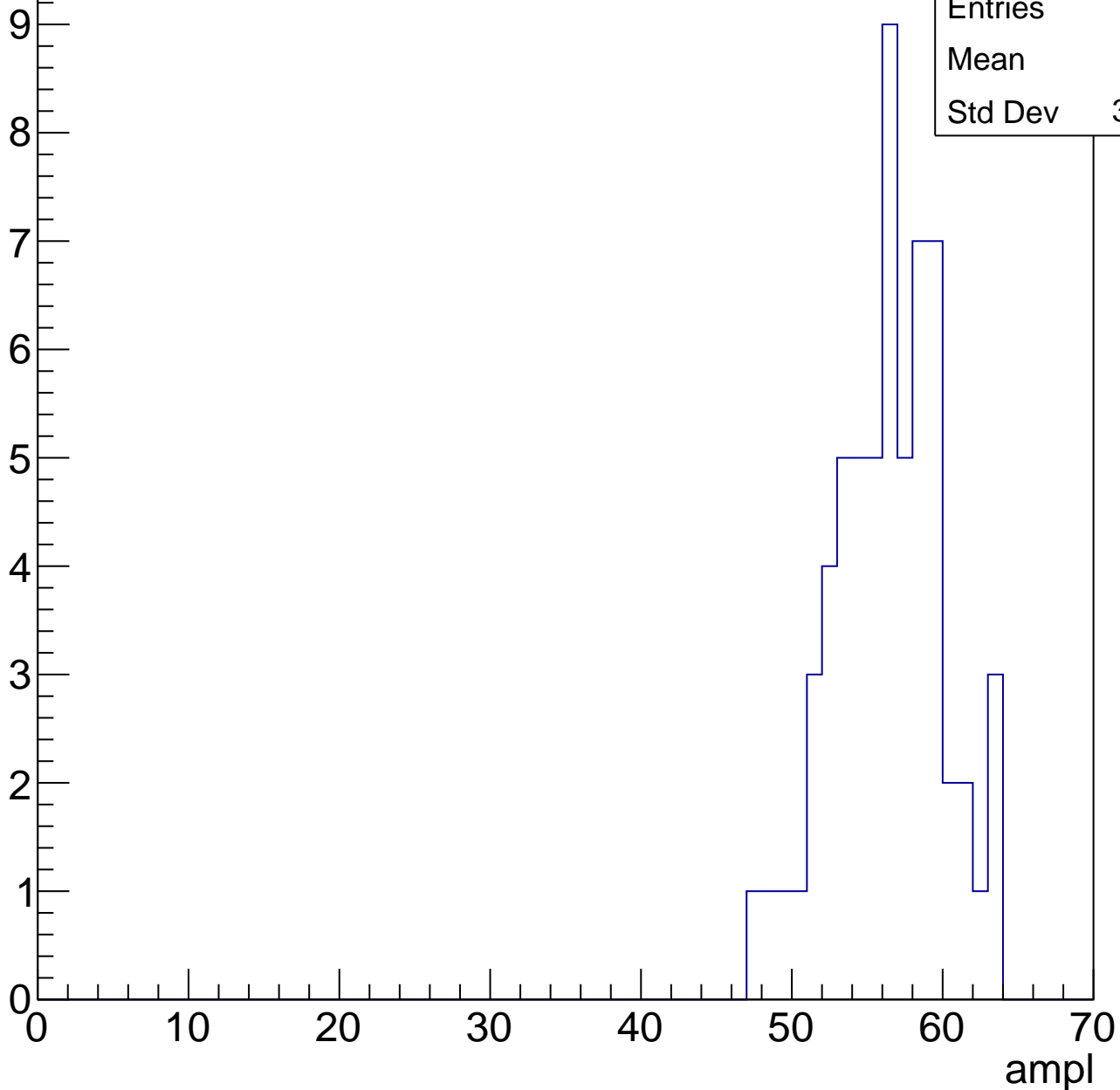


B1L103S, U21-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	55.9
Std Dev	3.591

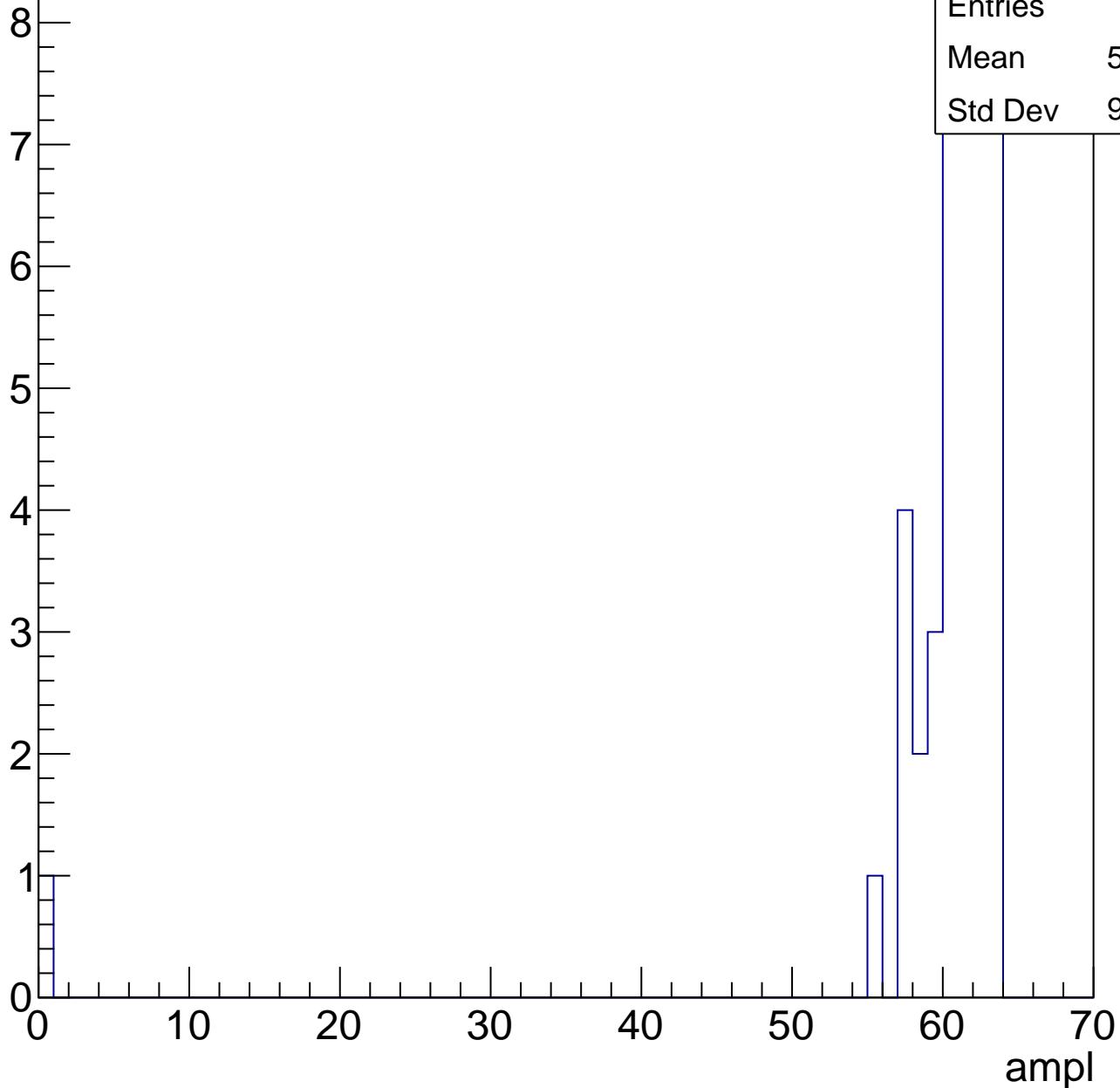


B1L103S, U21-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	59.16
Std Dev	9.343



B1L103S, U21-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U21-ch42, adc0

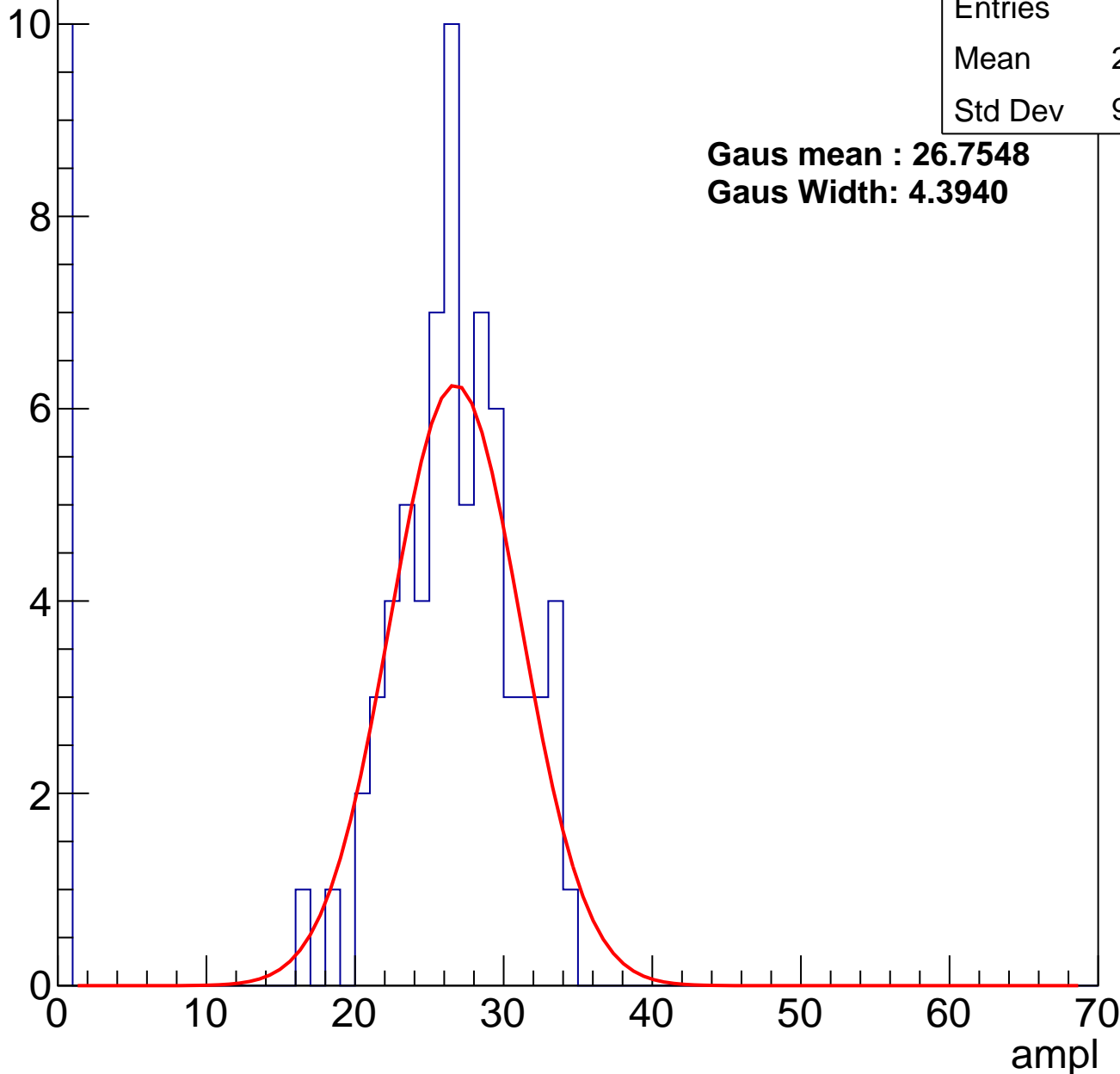
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	23.05
Std Dev	9.476

Gaus mean : 26.7548

Gaus Width: 4.3940

Entry



B1L103S, U21-ch42, adc1

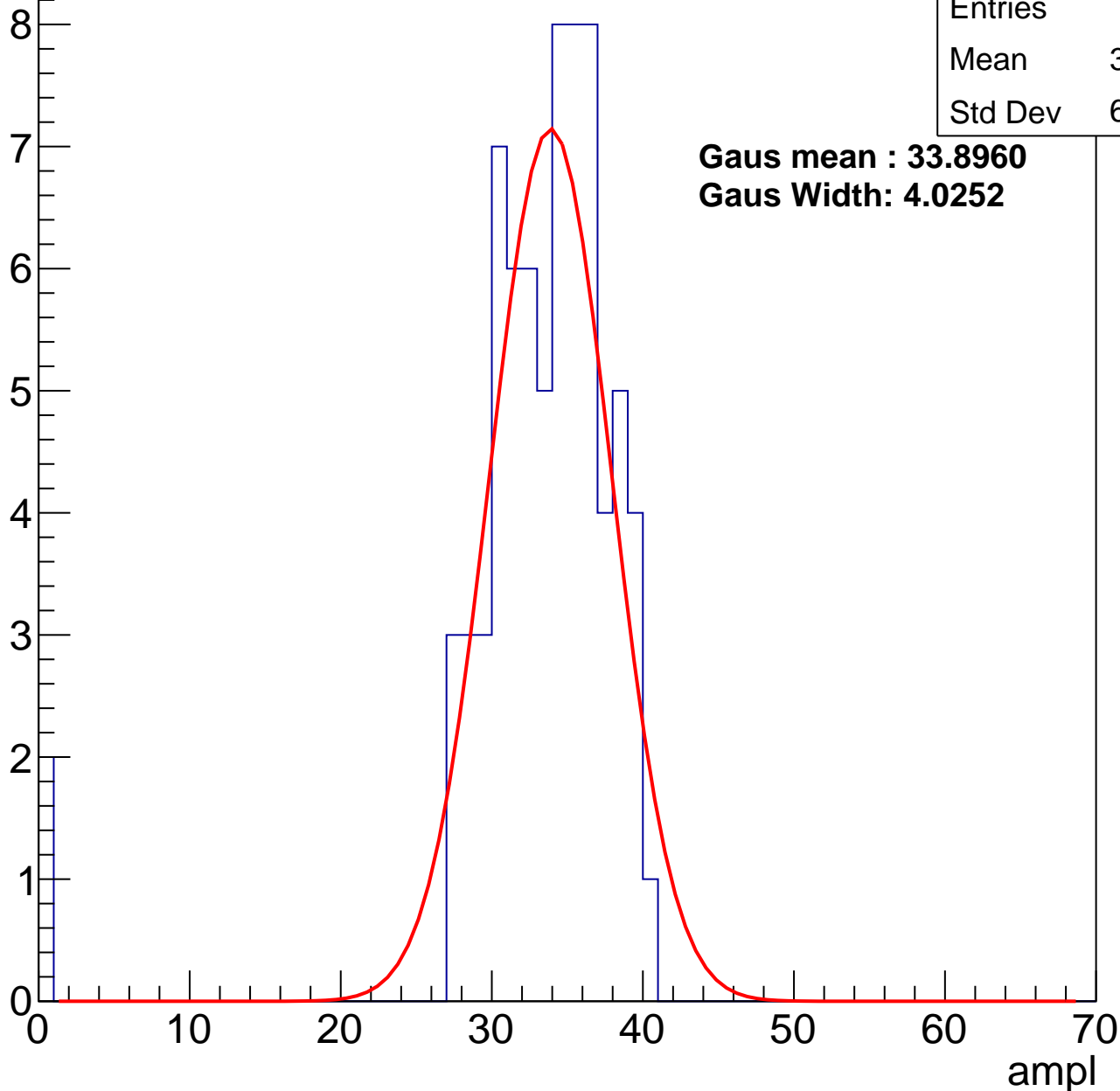
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.59
Std Dev	6.393

Gaus mean : 33.8960

Gaus Width: 4.0252



B1L103S, U21-ch42, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	34.08
Std Dev	14.86

Gaus mean : 40.9306

Gaus Width: 3.8279

Entry

10

8

6

4

2

0

0

10

20

30

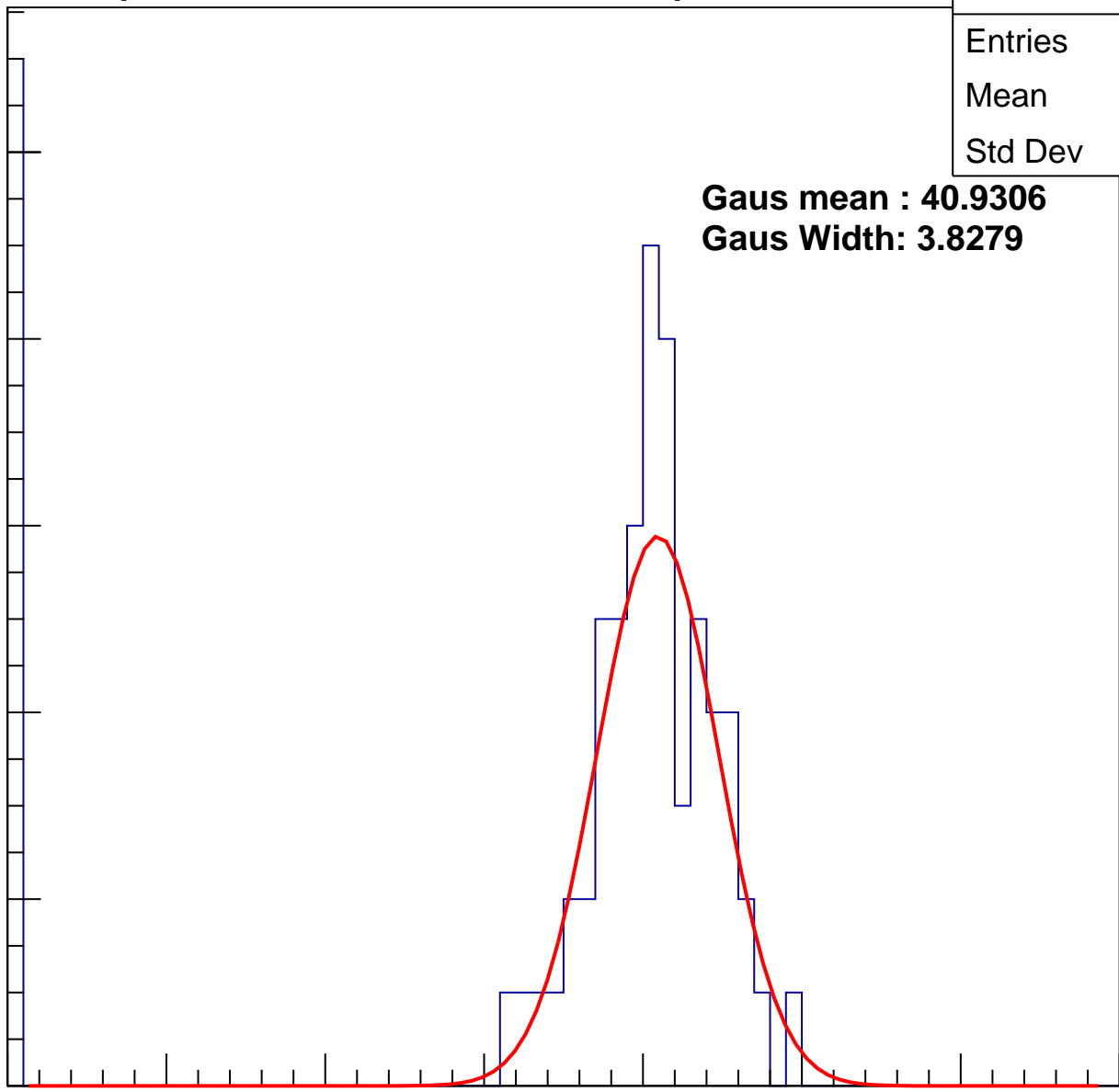
40

50

60

70

ampl

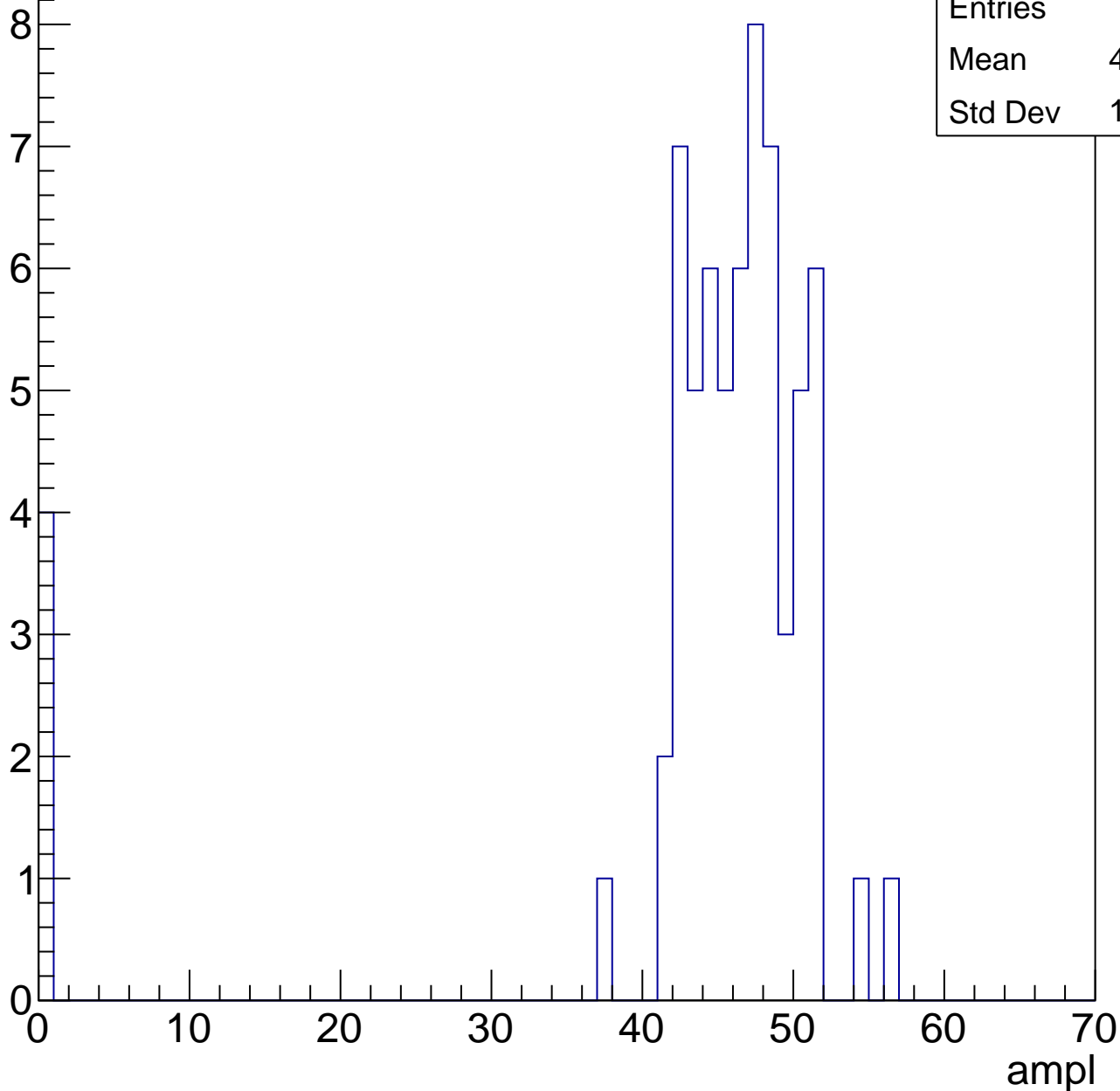


B1L103S, U21-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	43.55
Std Dev	11.48



B1L103S, U21-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	52.56
Std Dev	3.328

Entry

10

8

6

4

2

0

0

10

20

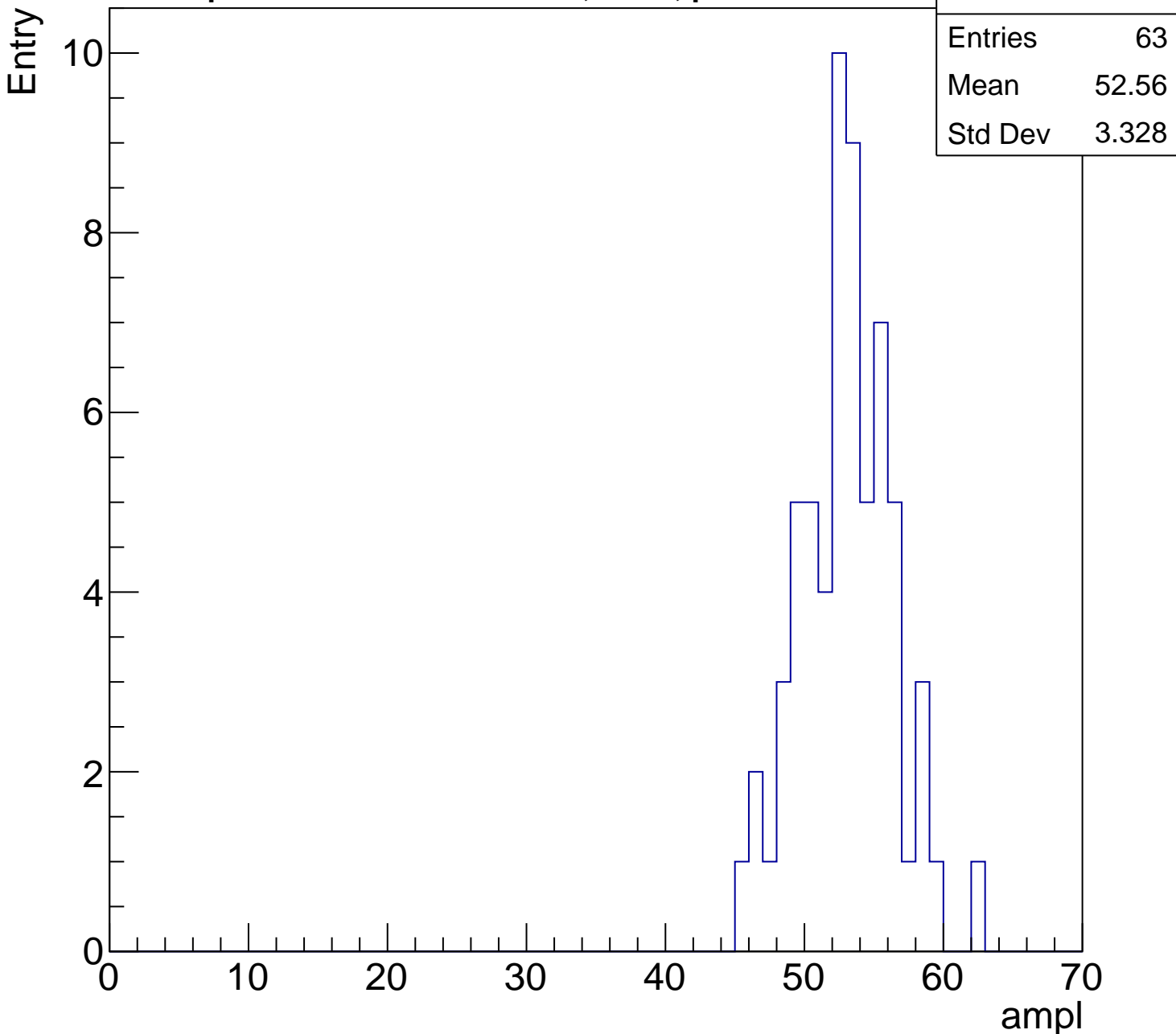
30

40

50

60

ampl

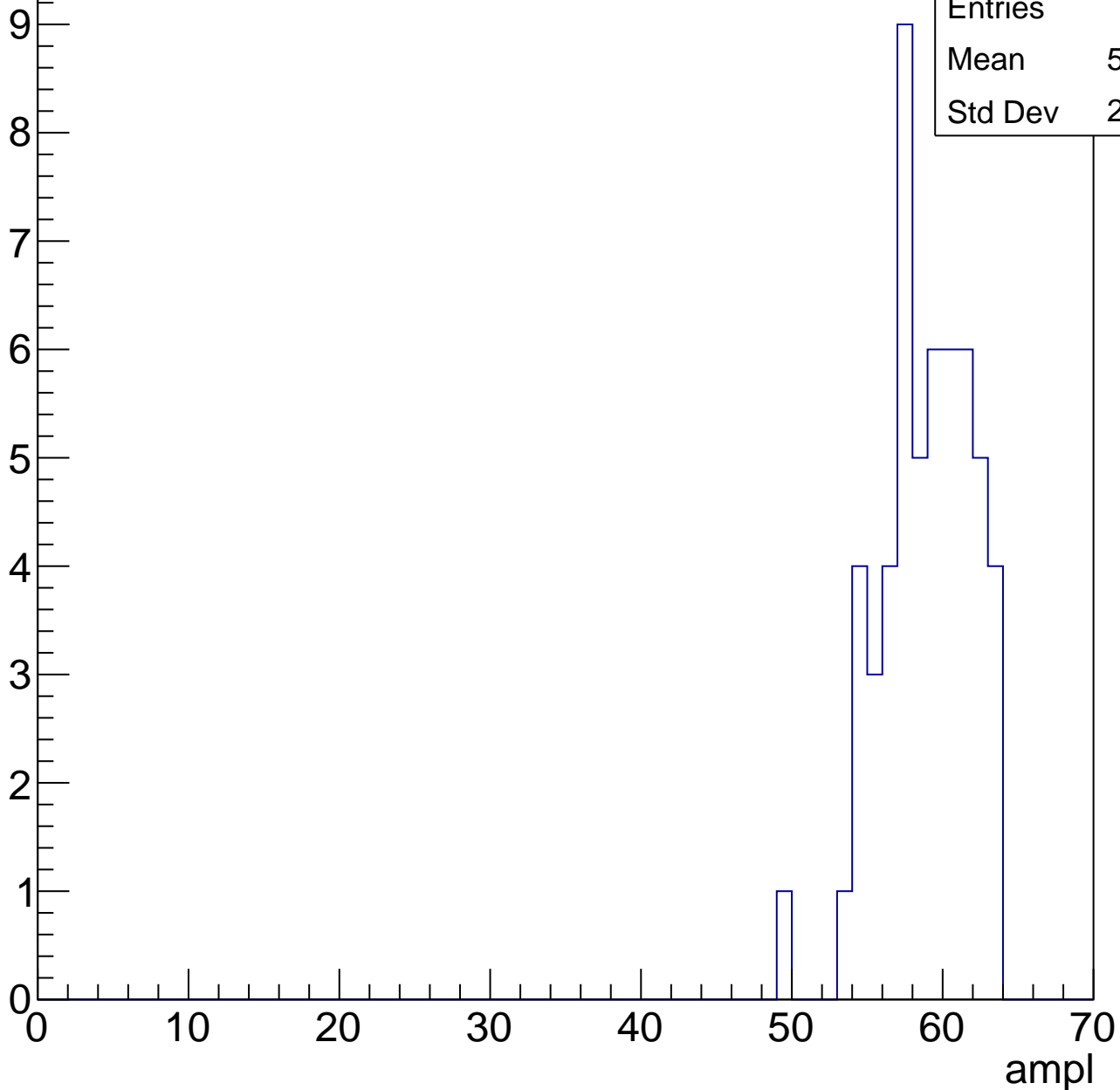


B1L103S, U21-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	58.37
Std Dev	2.977

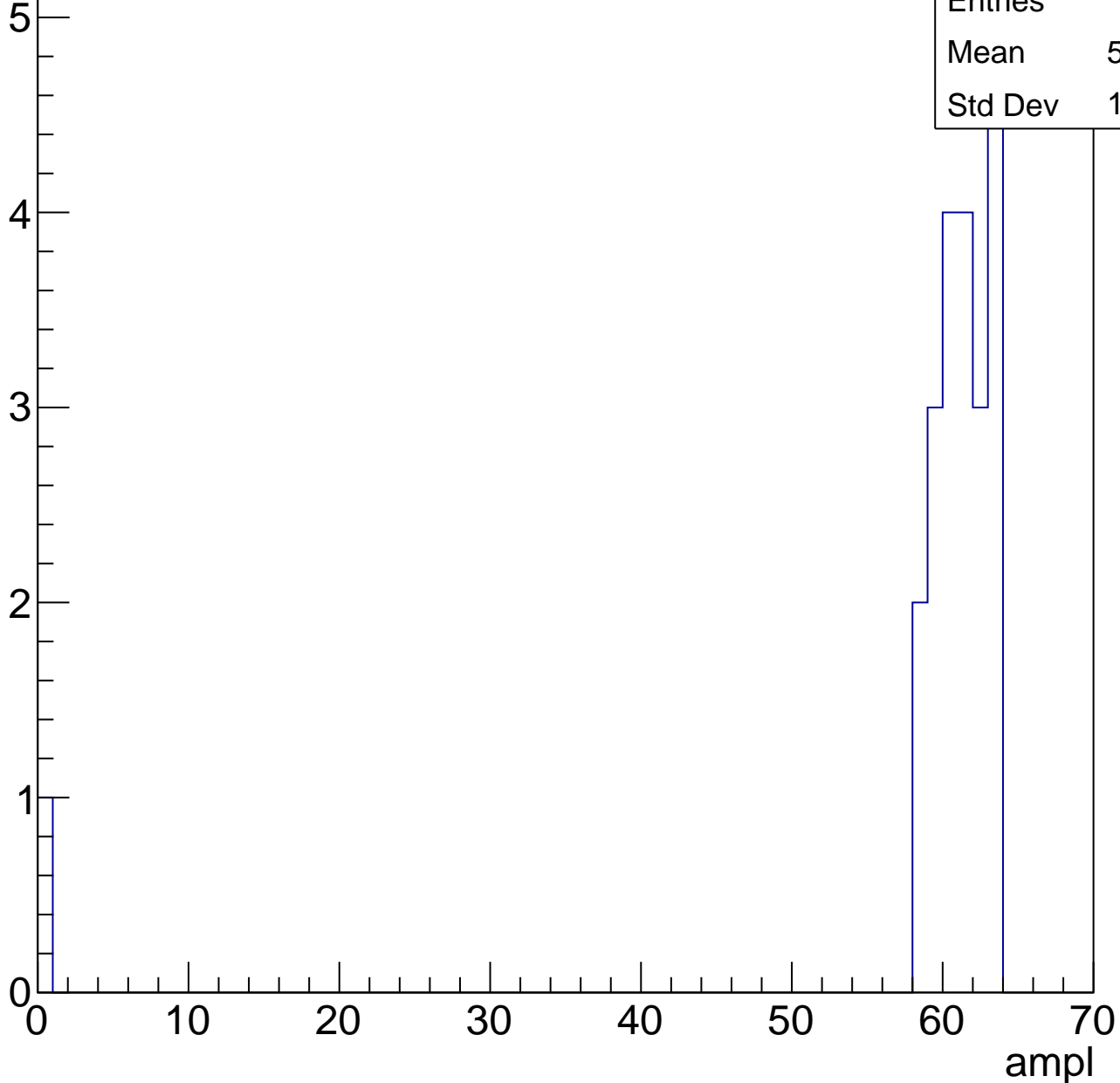


B1L103S, U21-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.09
Std Dev	12.78



B1L103S, U21-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U21-ch43, adc0

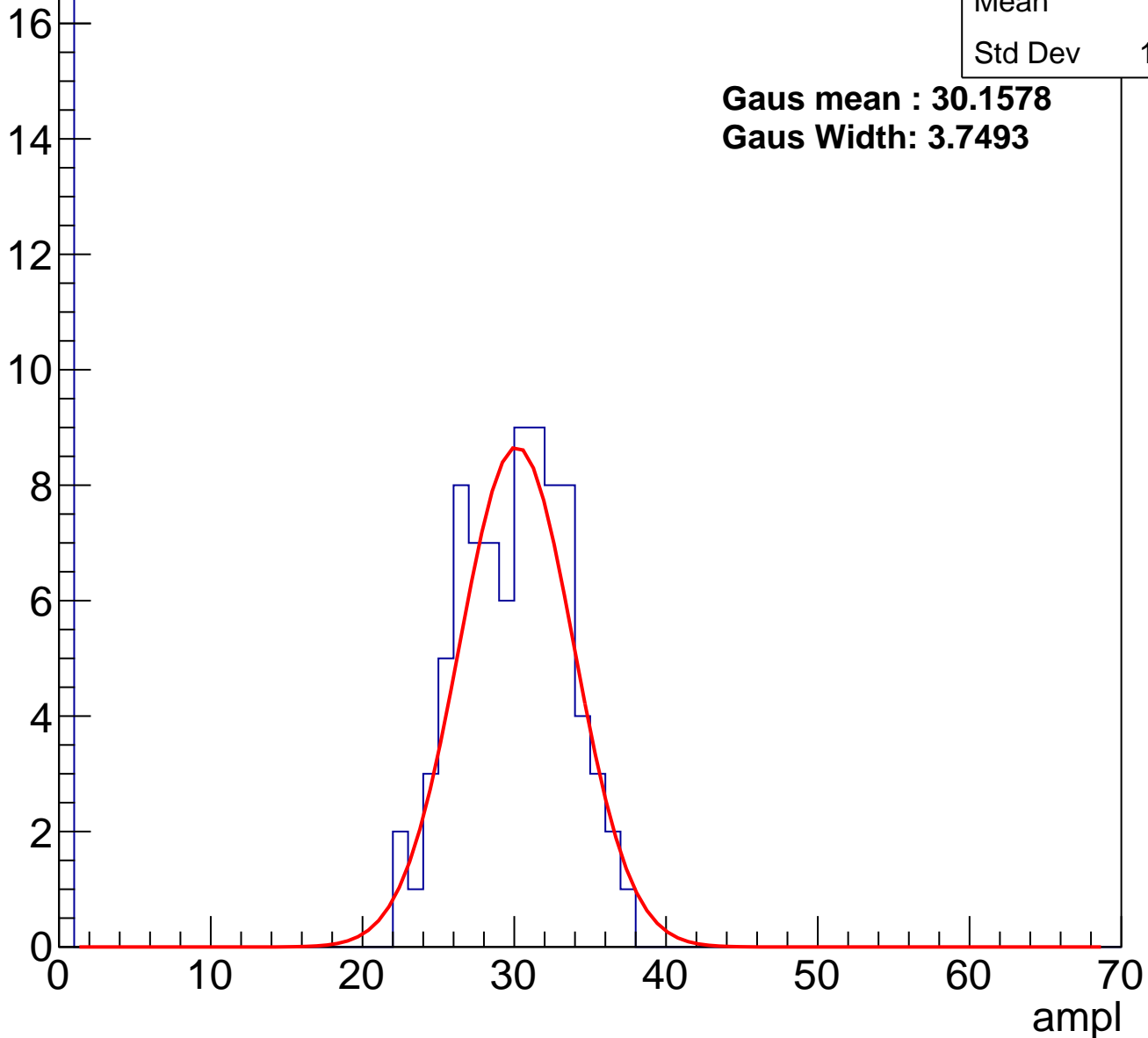
calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	24.5
Std Dev	11.53

Gaus mean : 30.1578

Gaus Width: 3.7493

Entry



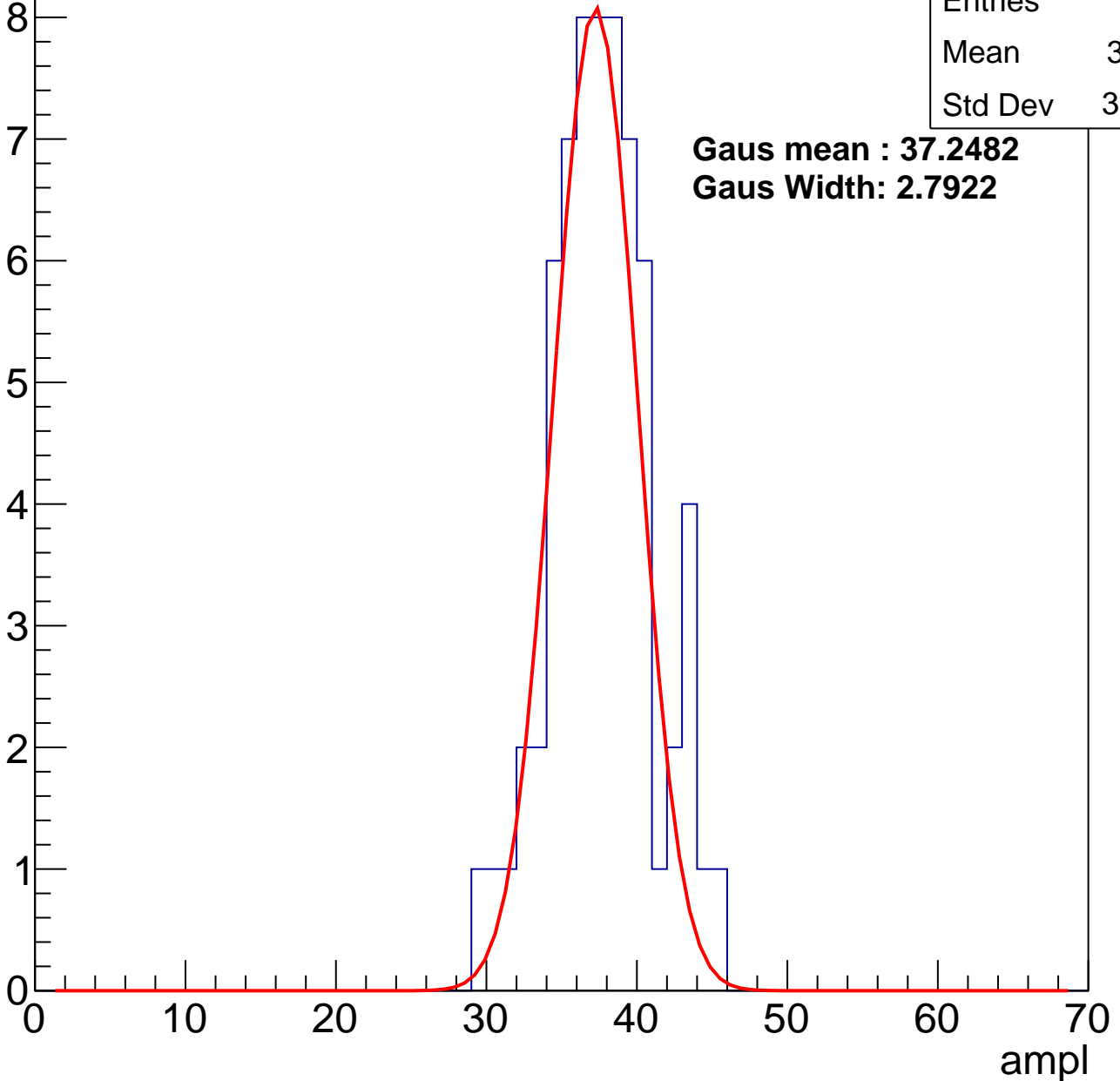
B1L103S, U21-ch43, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	37.21
Std Dev	3.328

Gaus mean : 37.2482
Gaus Width: 2.7922



B1L103S, U21-ch43, adc2

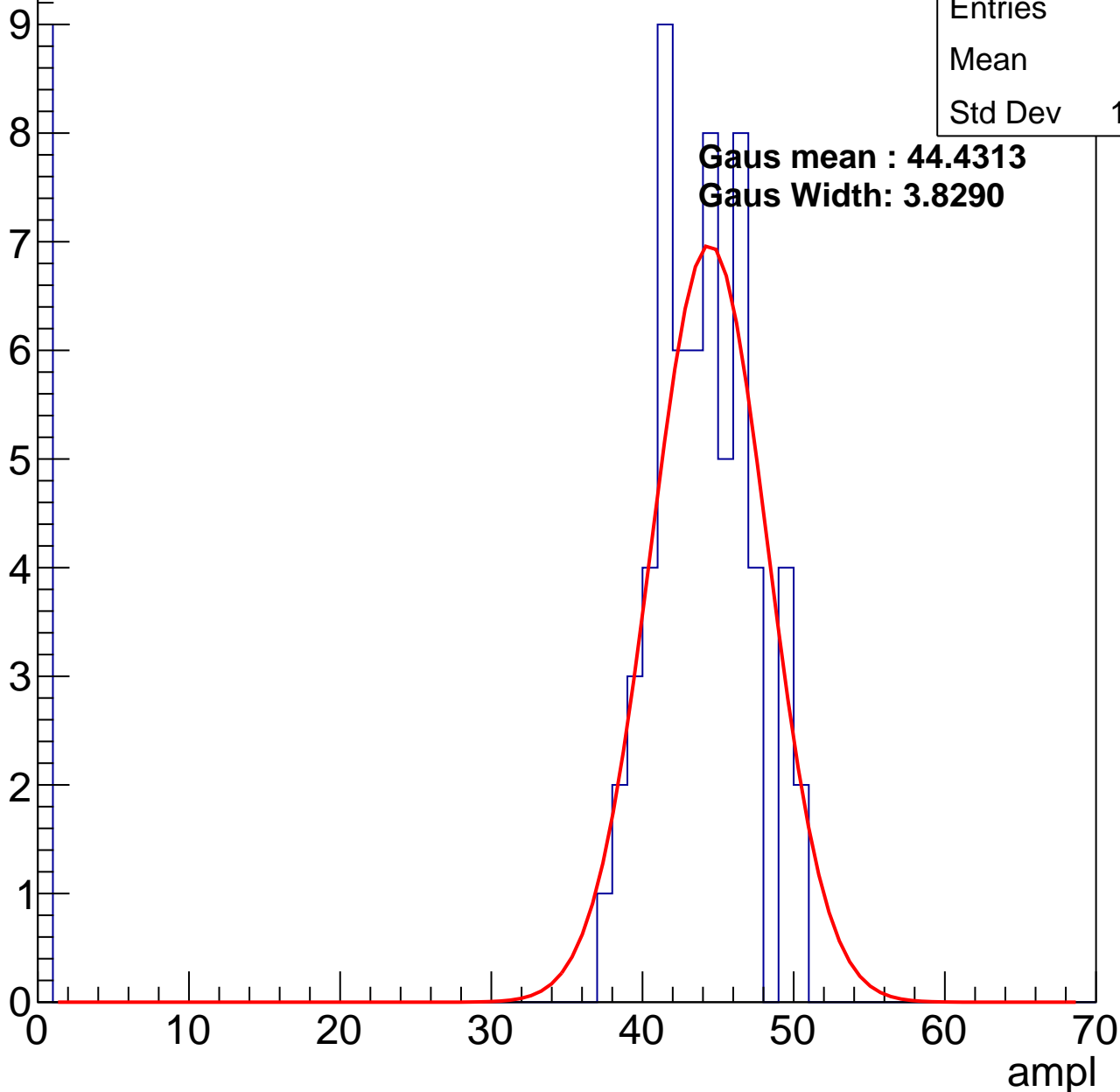
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	38
Std Dev	14.77

Gaus mean : 44.4313

Gaus Width: 3.8290

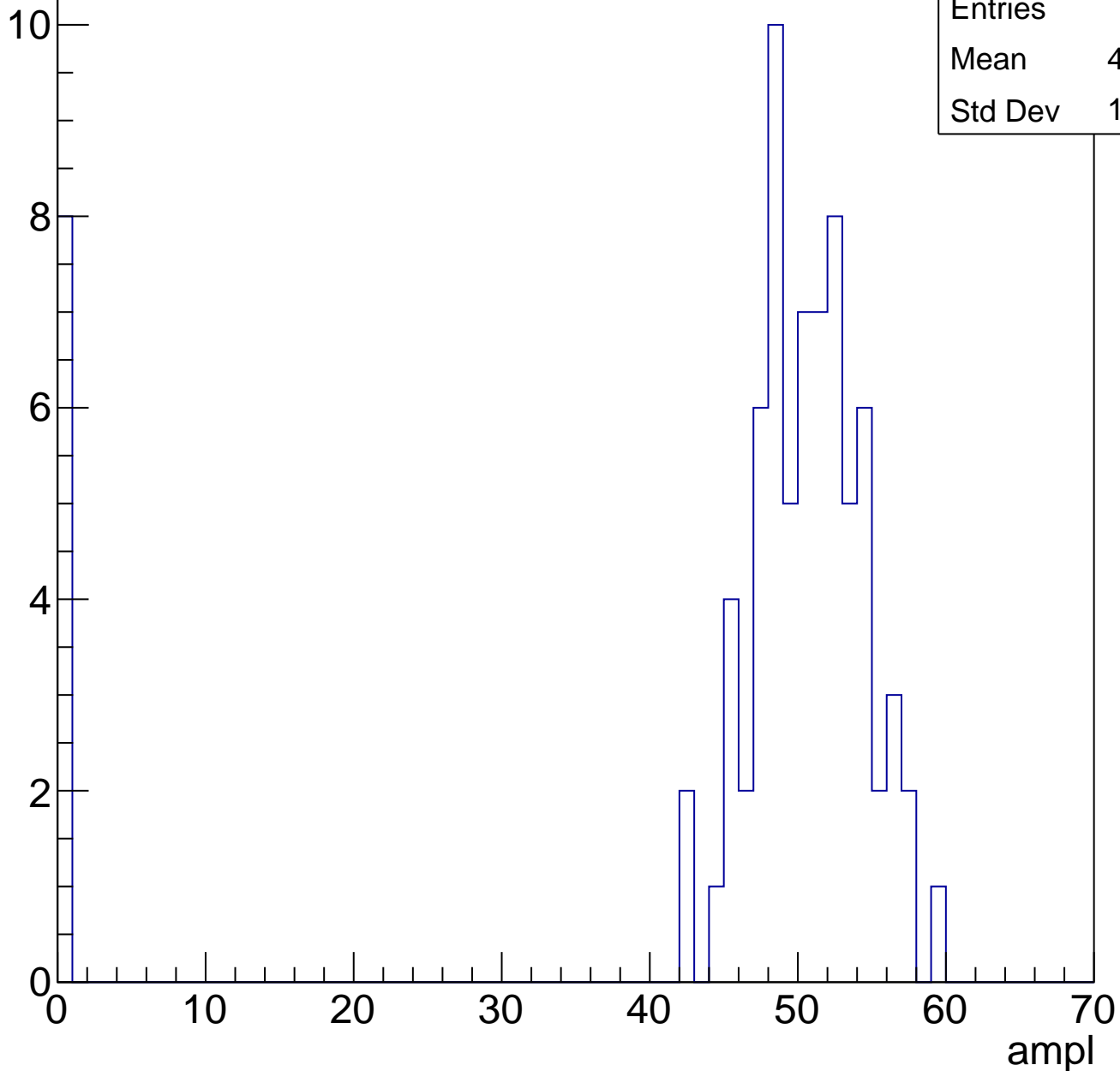


B1L103S, U21-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	45.19
Std Dev	15.54

Entry

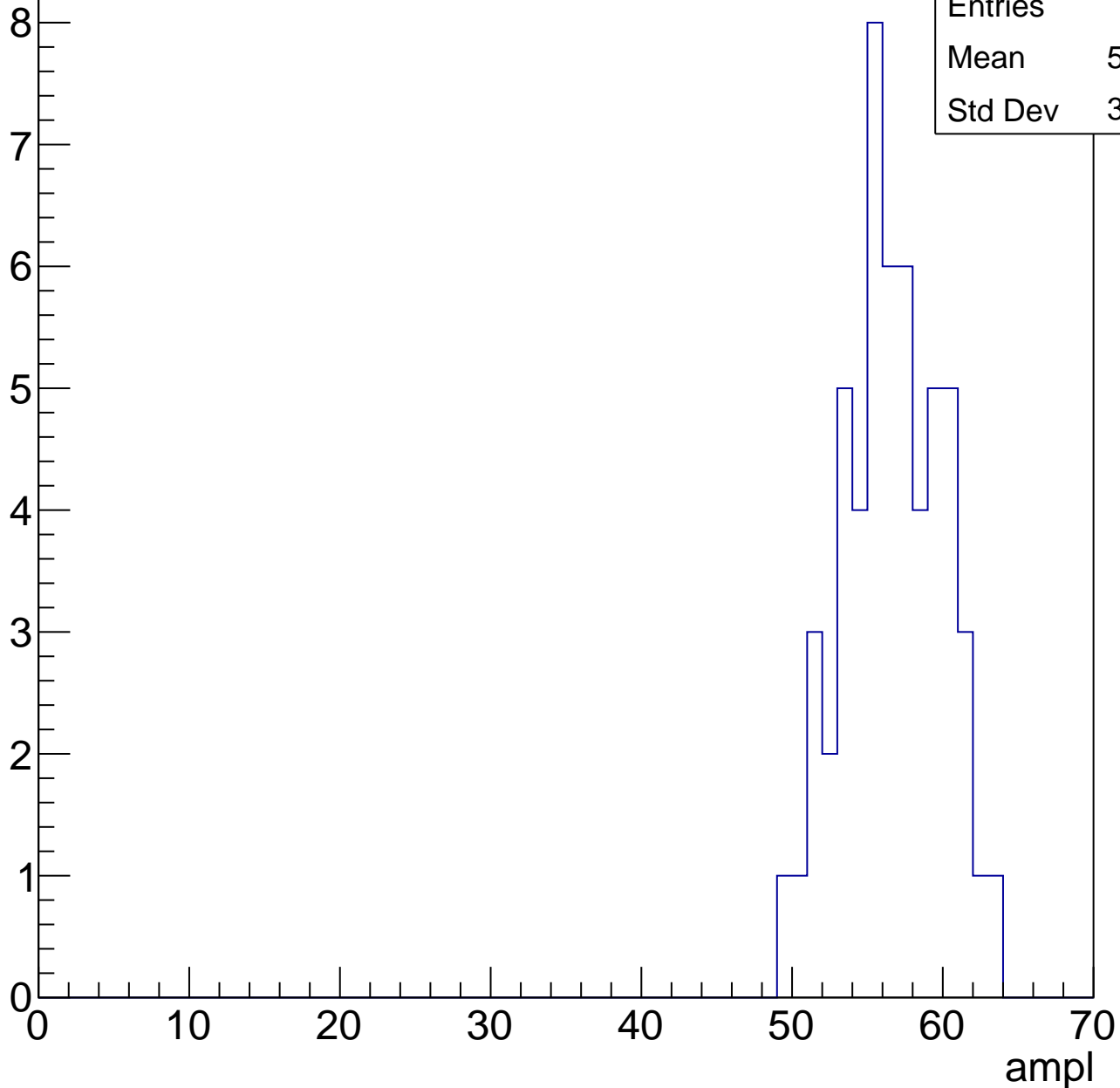


B1L103S, U21-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	56.18
Std Dev	3.208

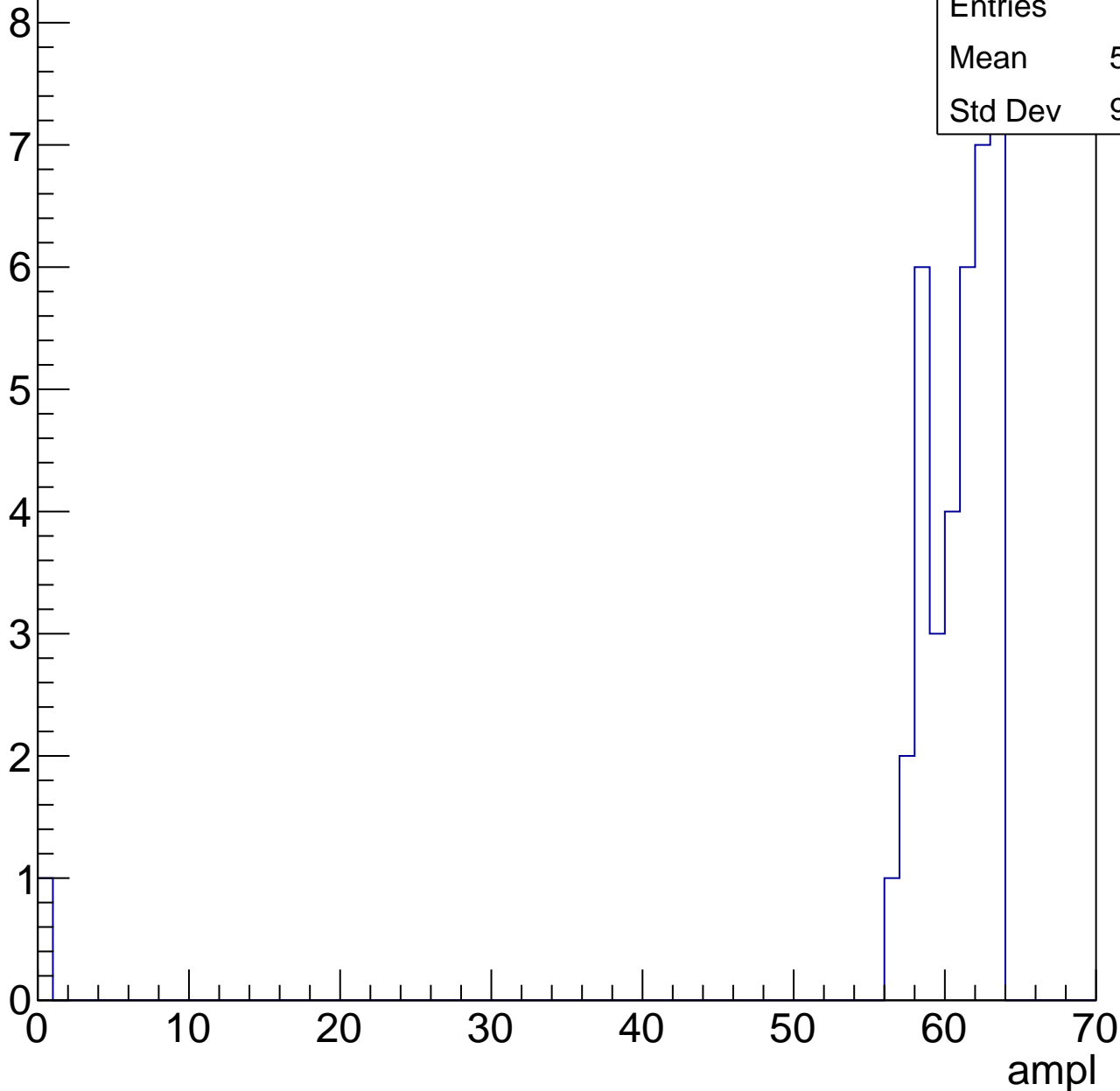


B1L103S, U21-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.92
Std Dev	9.898



B1L103S, U21-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

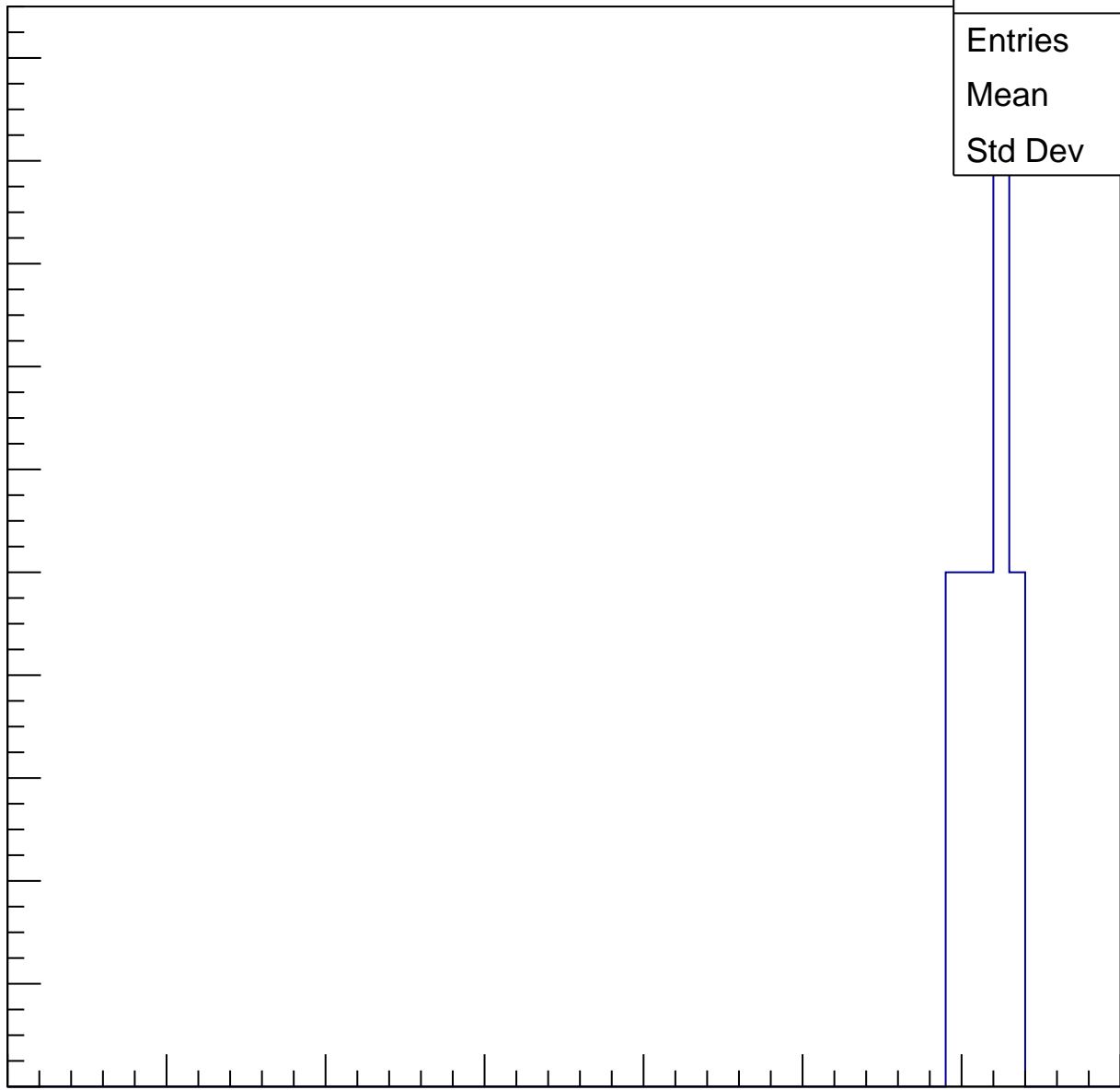
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.344

ampl

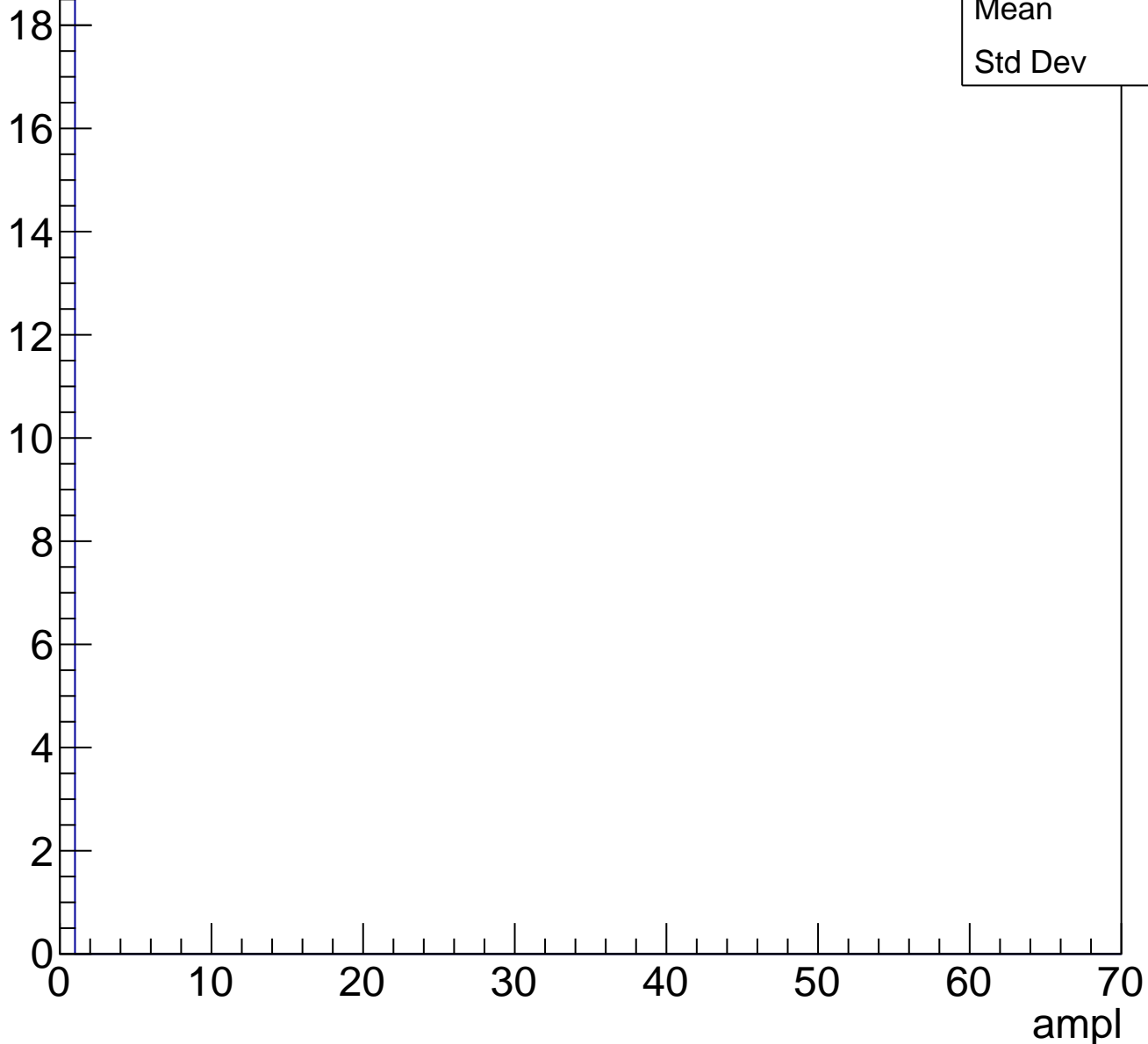
0 10 20 30 40 50 60 70



B1L103S, U21-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch44, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	23.17
Std Dev	9.066

Gaus mean : 26.8631

Gaus Width: 4.5969

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U21-ch44, adc1

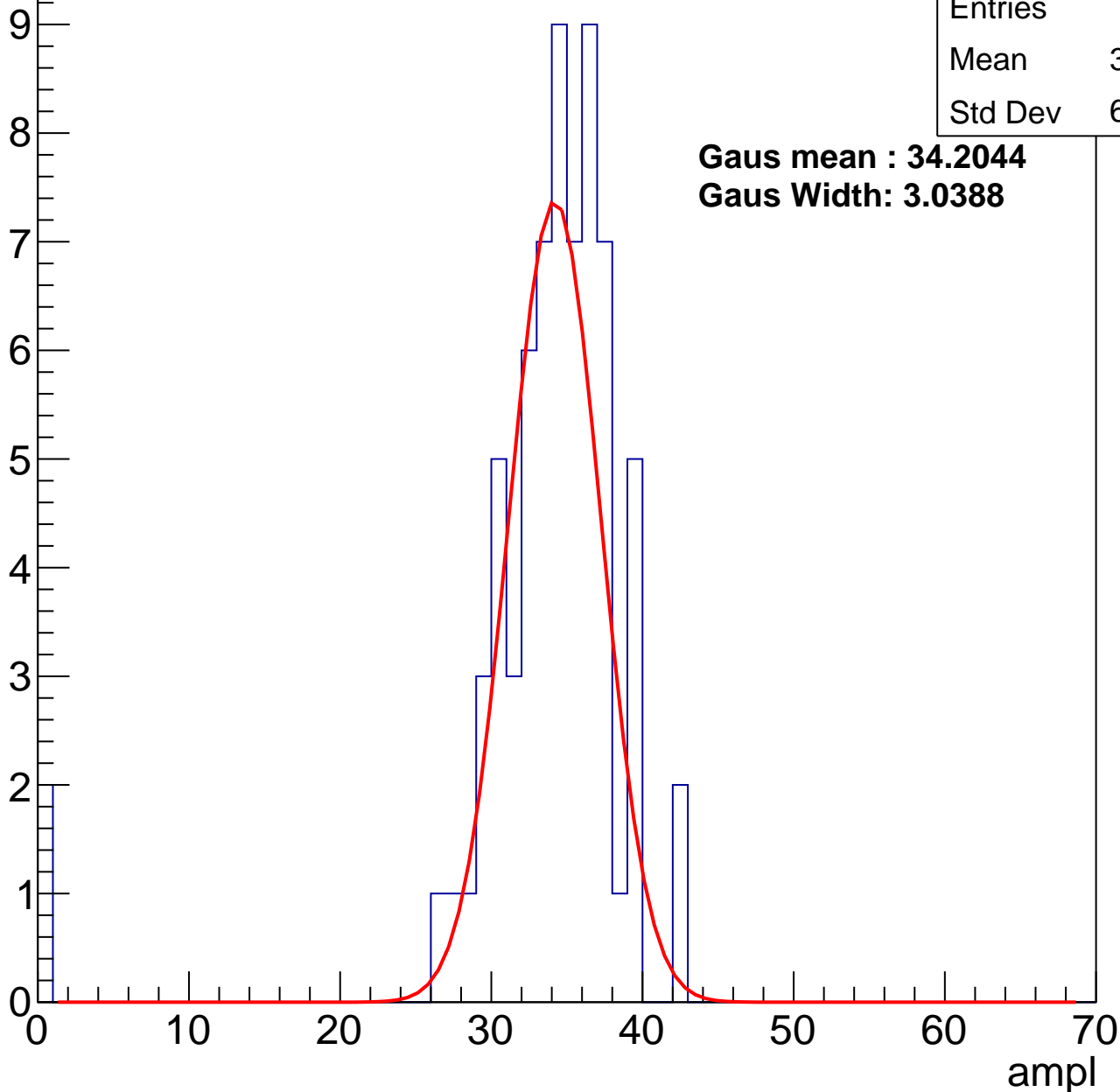
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	33.12
Std Dev	6.588

Gaus mean : 34.2044

Gaus Width: 3.0388



B1L103S, U21-ch44, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	33.73
Std Dev	15.65

Gaus mean : 41.1844

Gaus Width: 3.4349

Entry

10

8

6

4

2

0

0

10

20

30

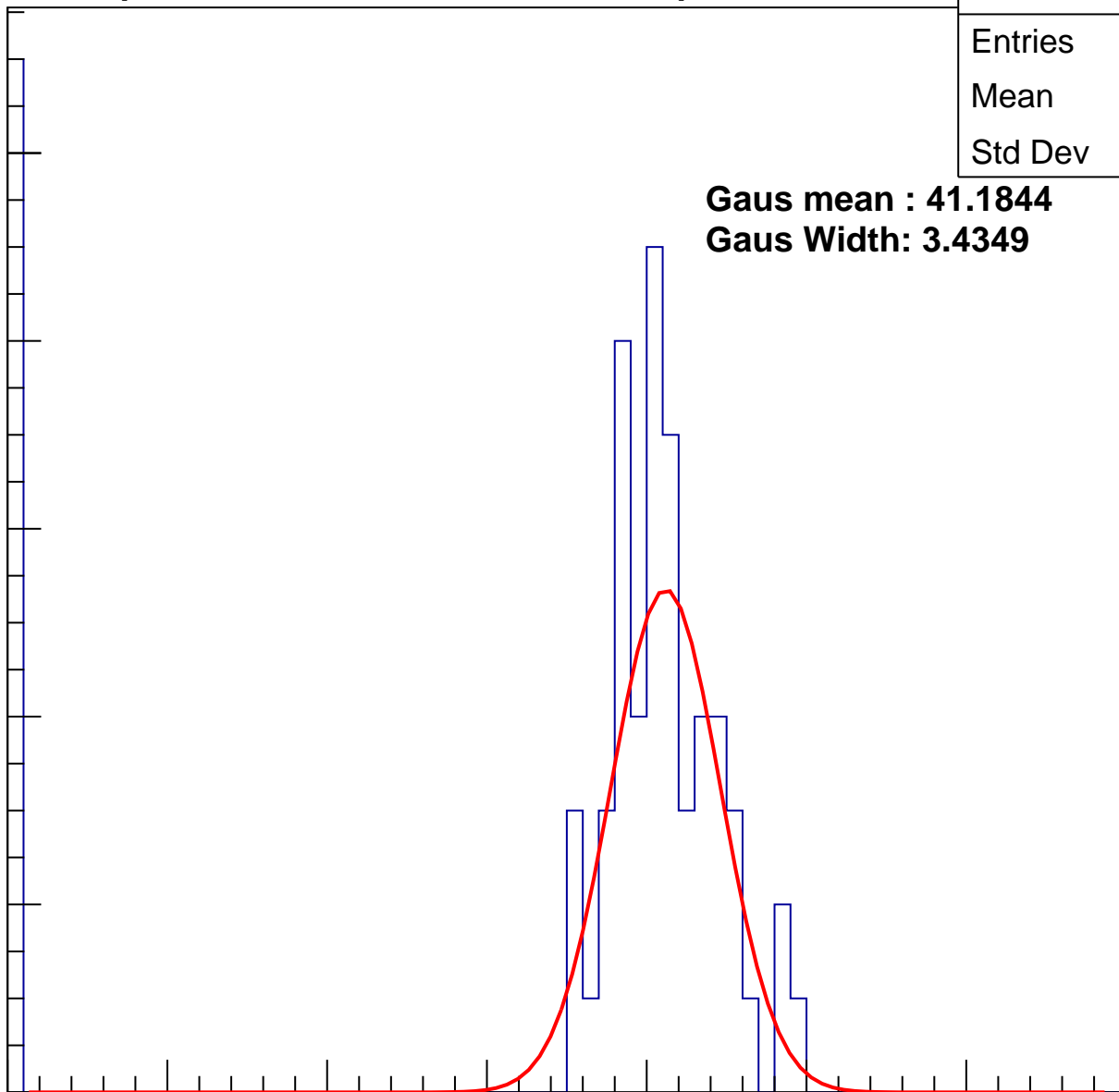
40

50

60

70

ampl

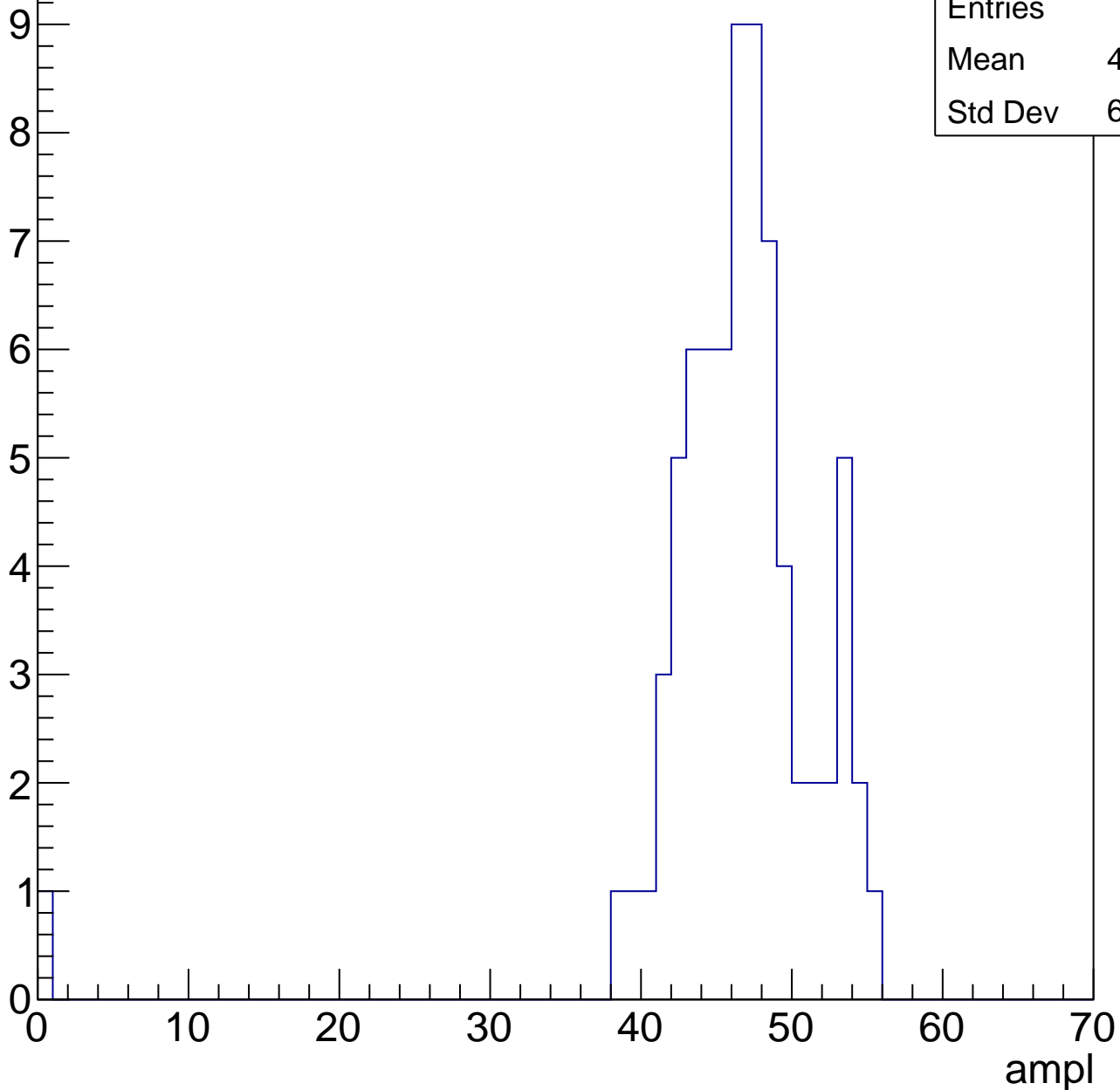


B1L103S, U21-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	45.82
Std Dev	6.603

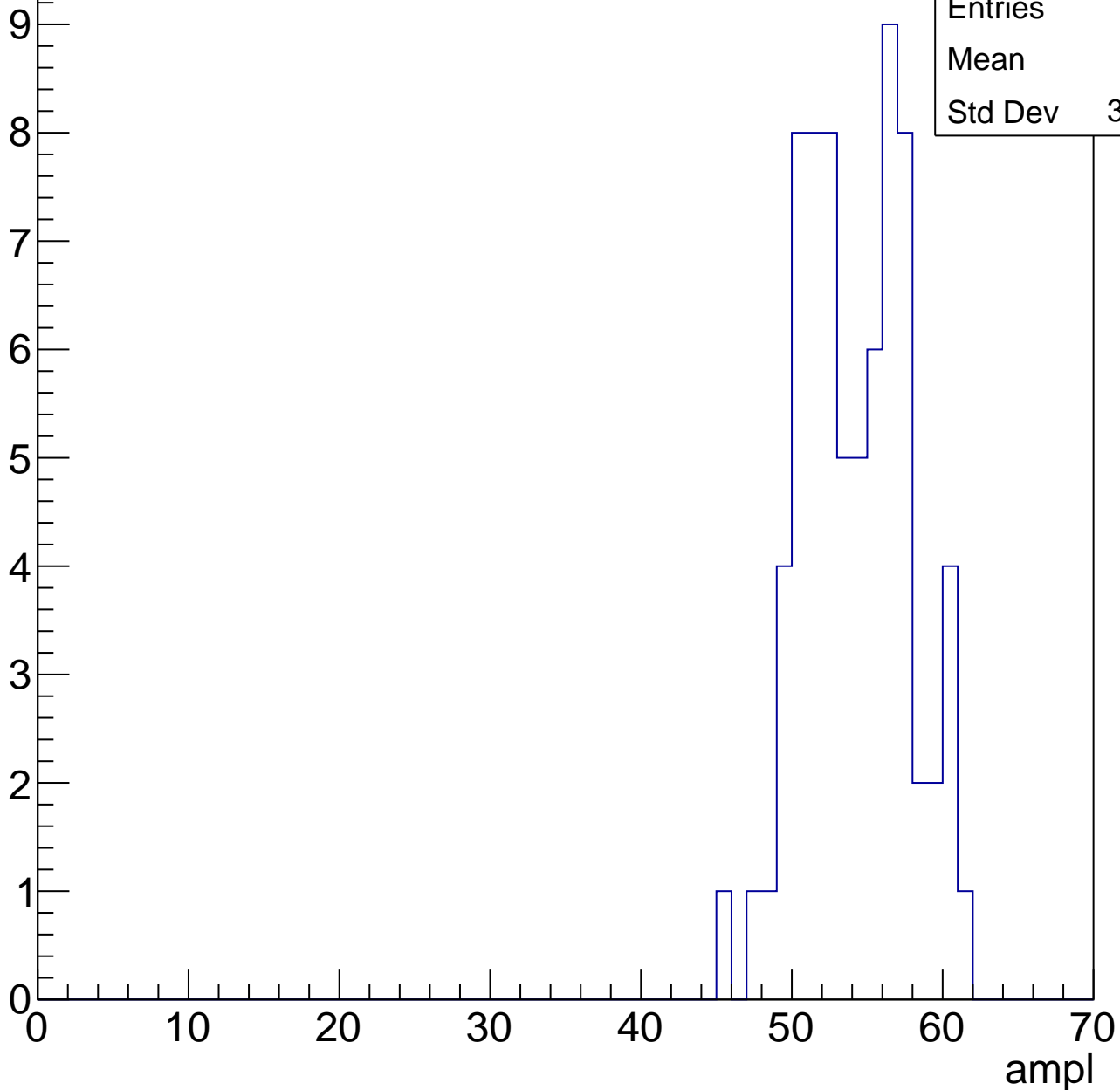


B1L103S, U21-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

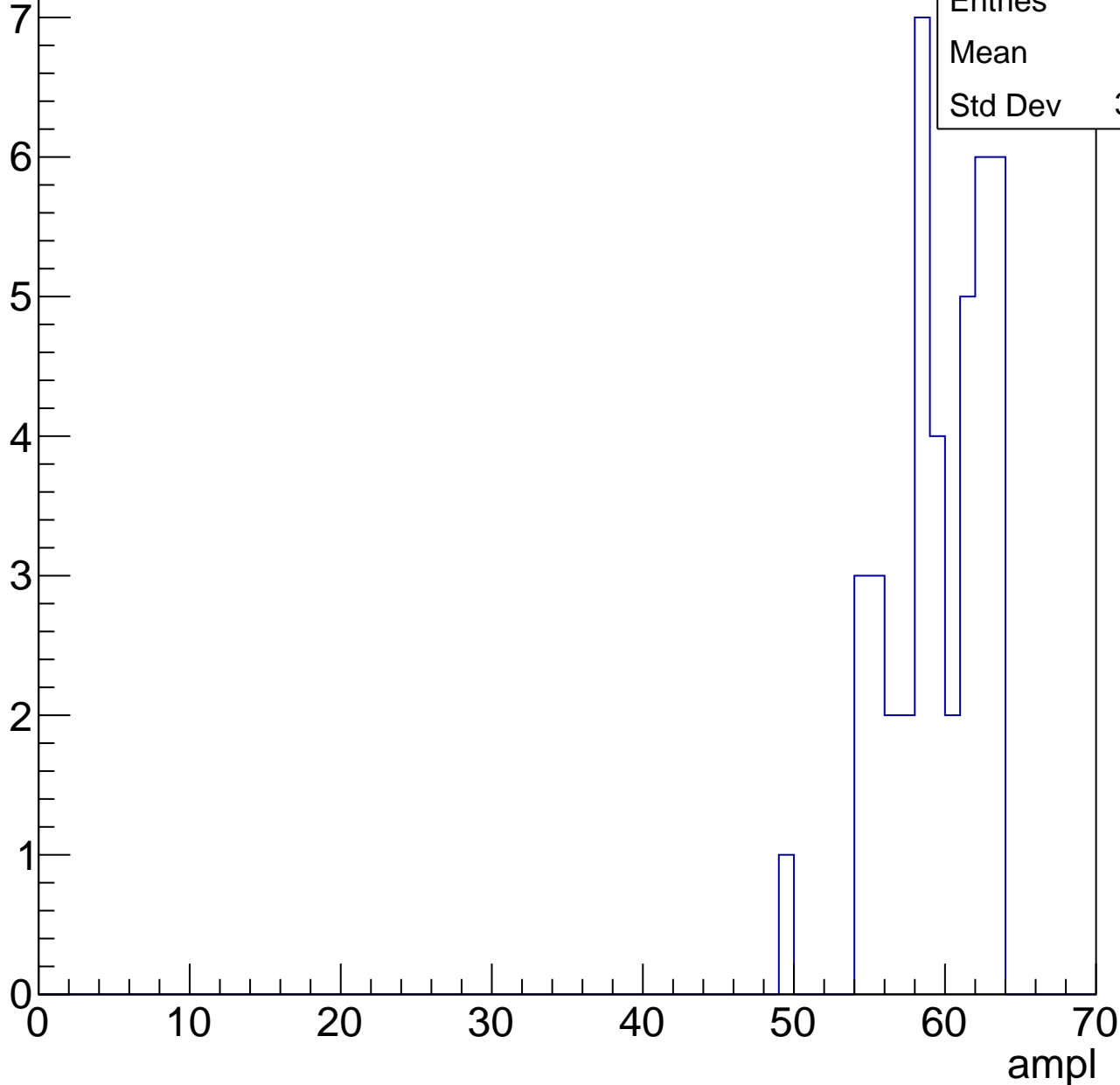
Entries	73
Mean	53.7
Std Dev	3.483



B1L103S, U21-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

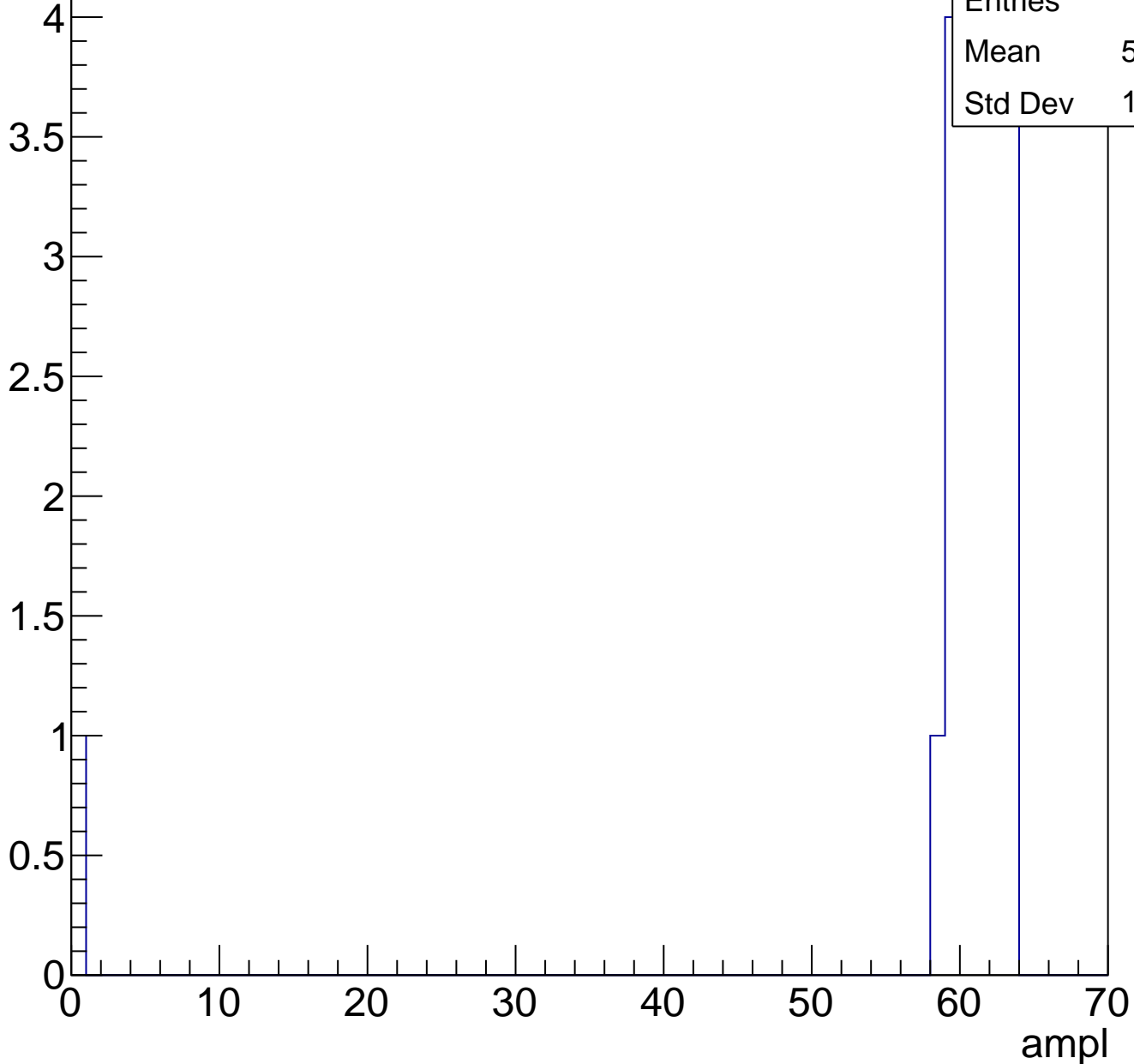
Entry



B1L103S, U21-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch45, adc0

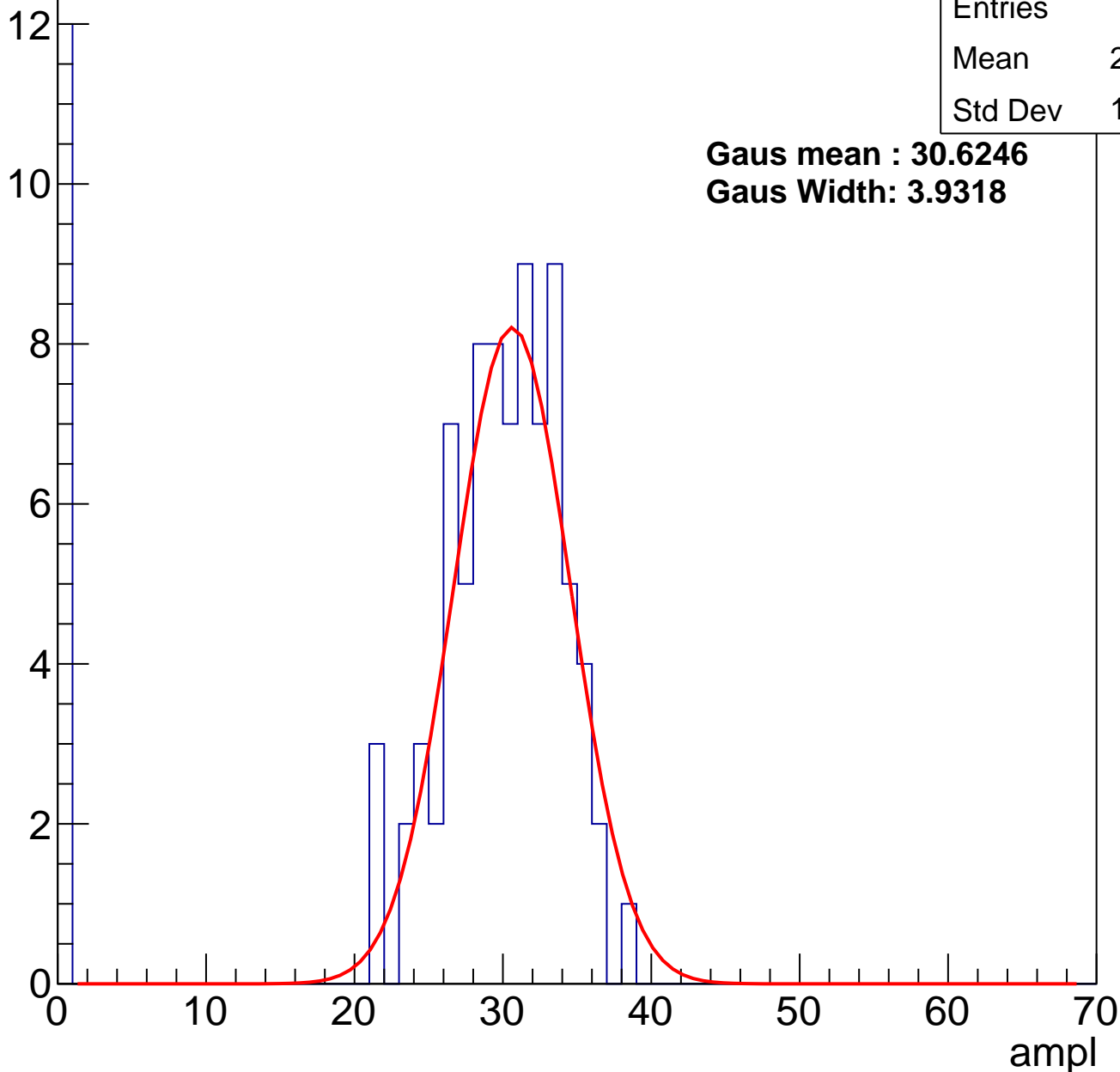
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	25.89
Std Dev	10.49

Gaus mean : 30.6246

Gaus Width: 3.9318

Entry



B1L103S, U21-ch45, adc1

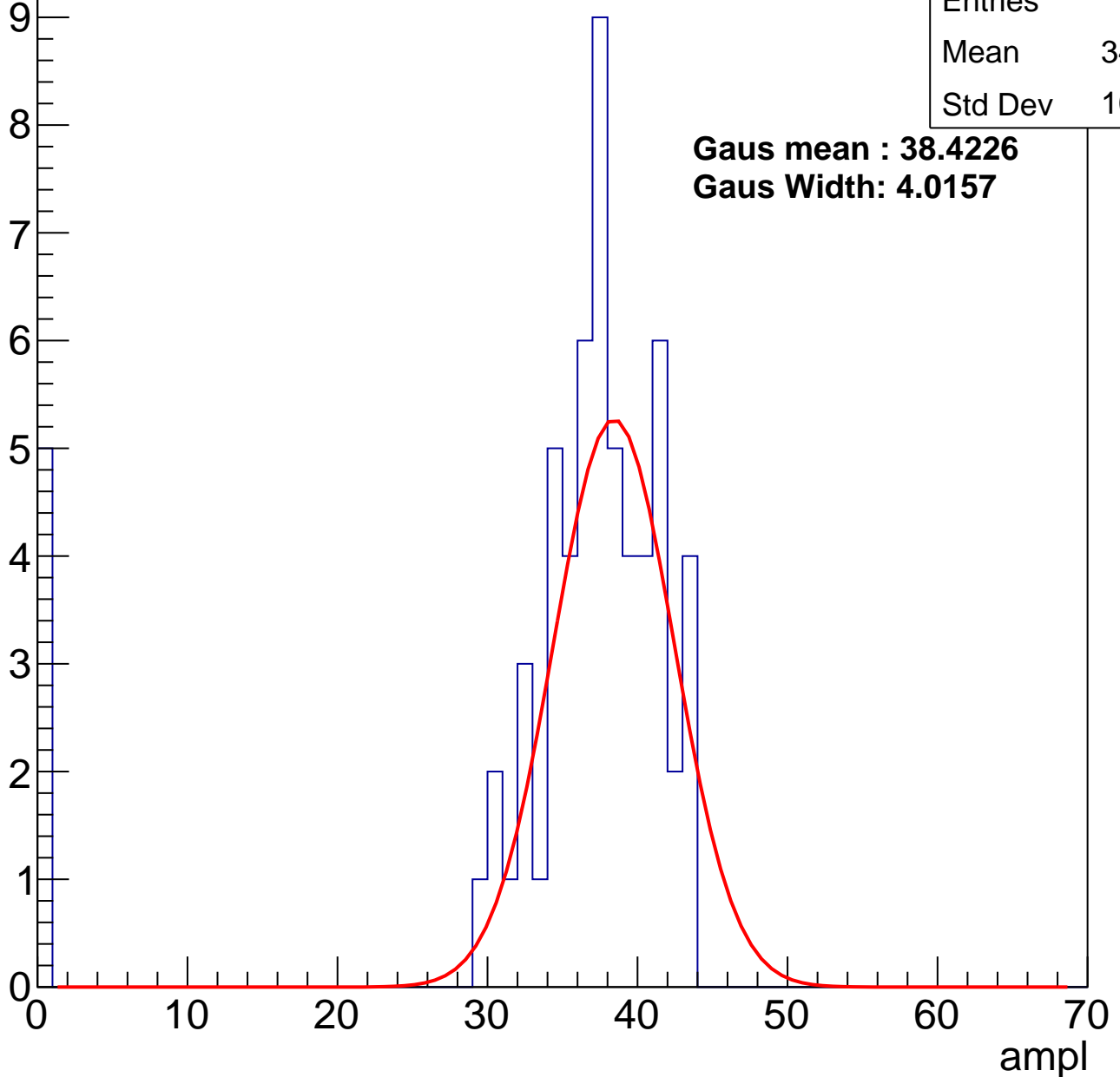
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.13
Std Dev	10.65

Gaus mean : 38.4226

Gaus Width: 4.0157



B1L103S, U21-ch45, adc2

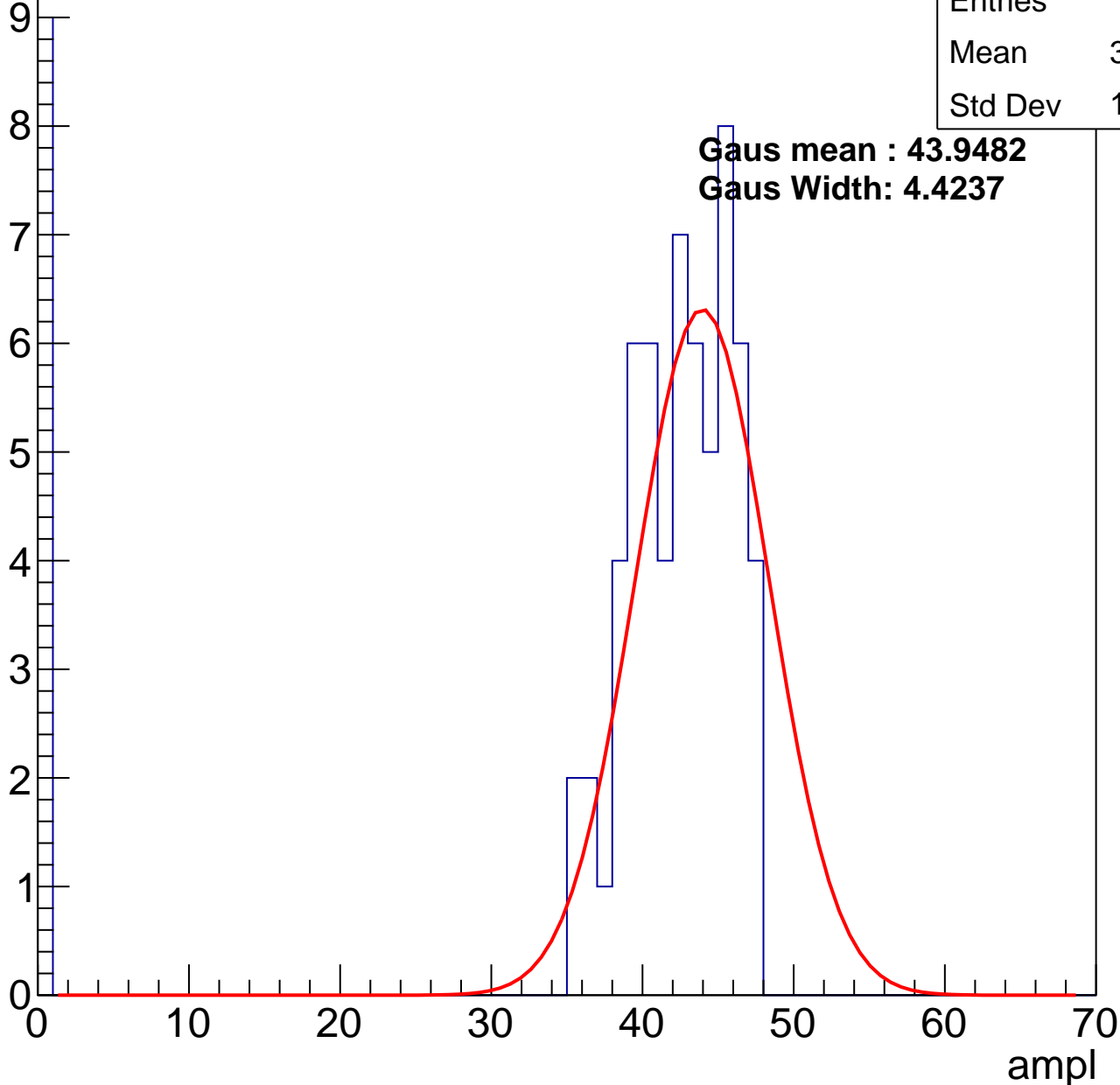
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.64
Std Dev	14.39

Gaus mean : 43.9482

Gaus Width: 4.4237

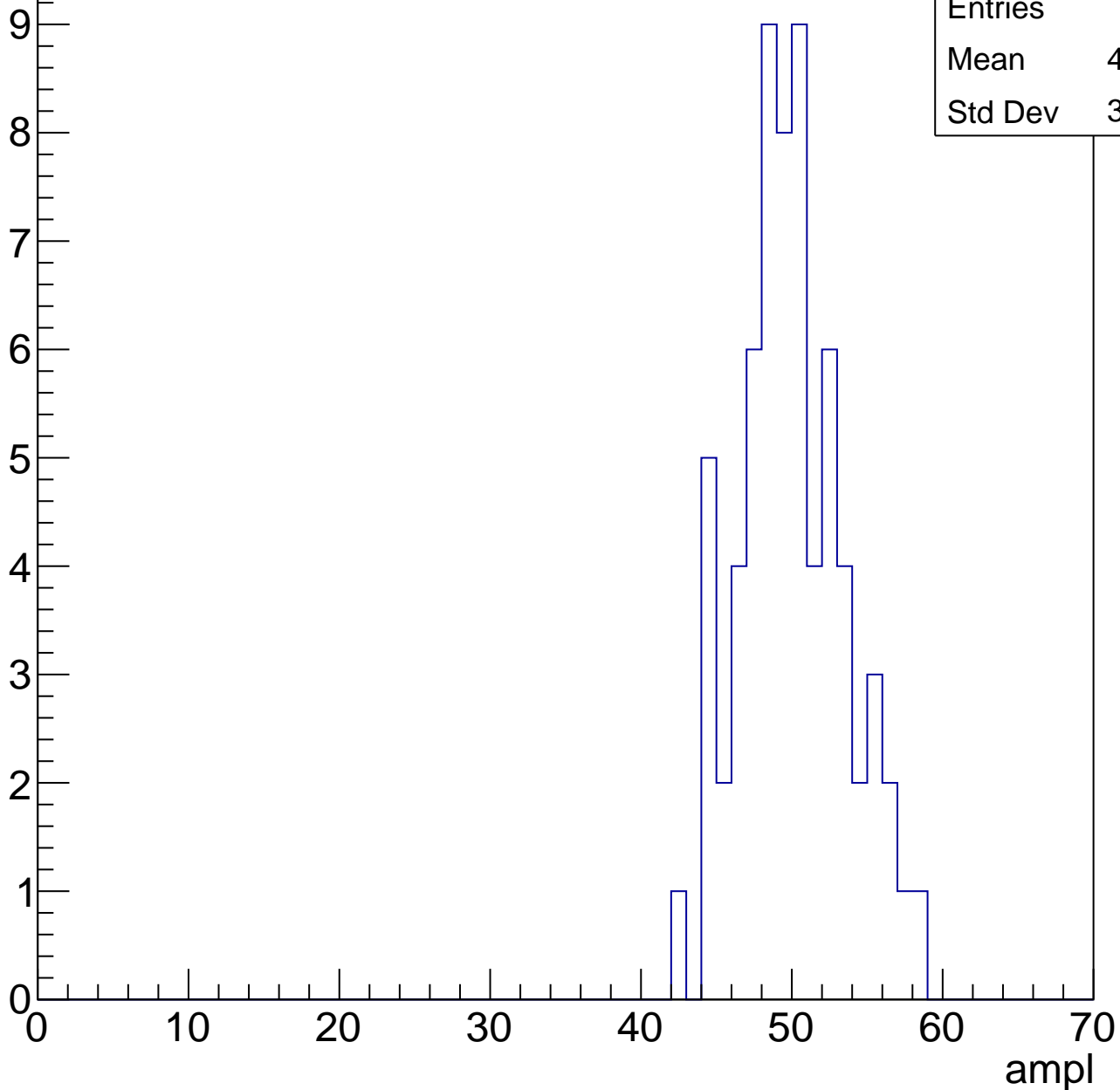


B1L103S, U21-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

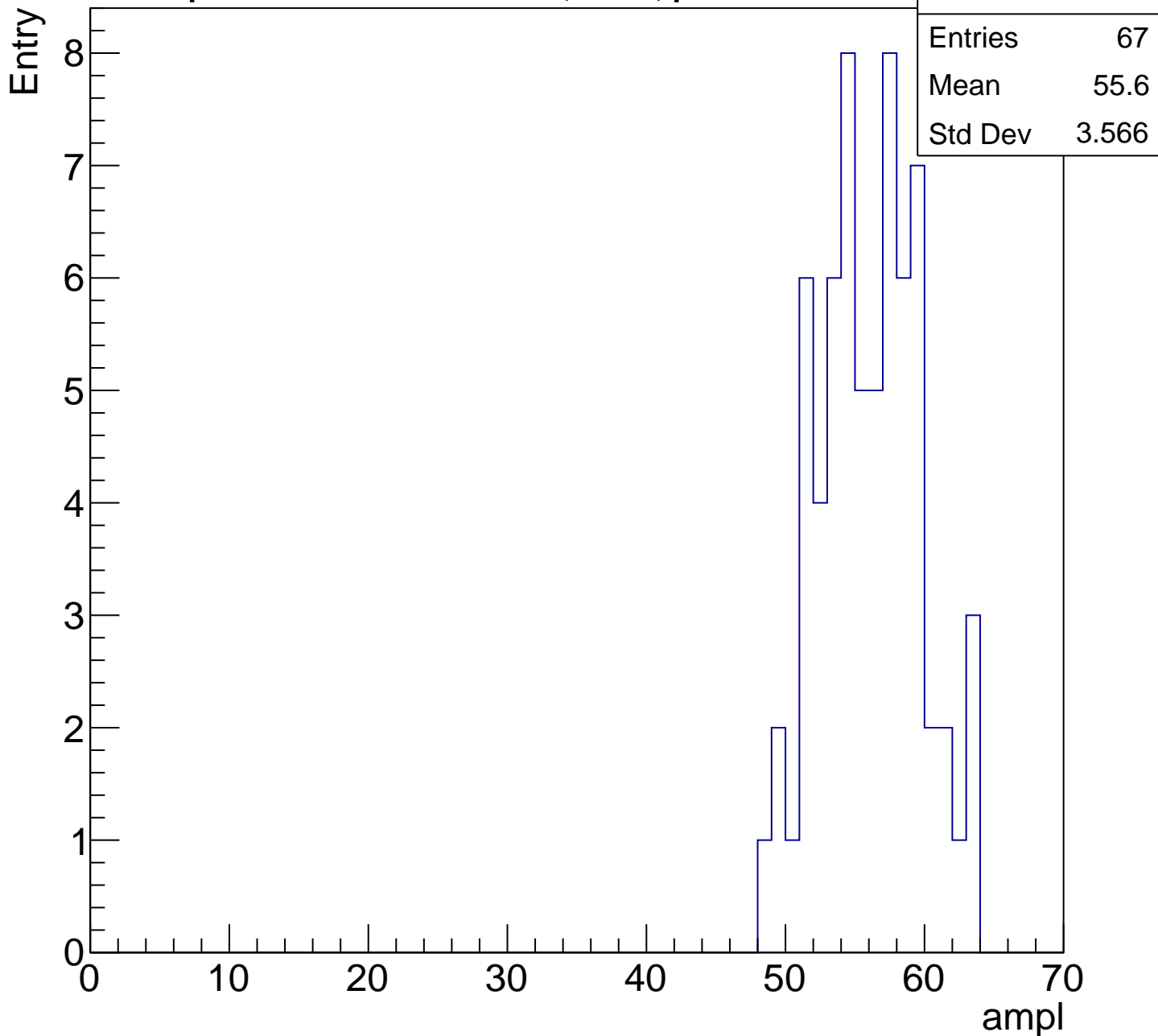
Entry

Entries	67
Mean	49.55
Std Dev	3.452



B1L103S, U21-ch45, adc4

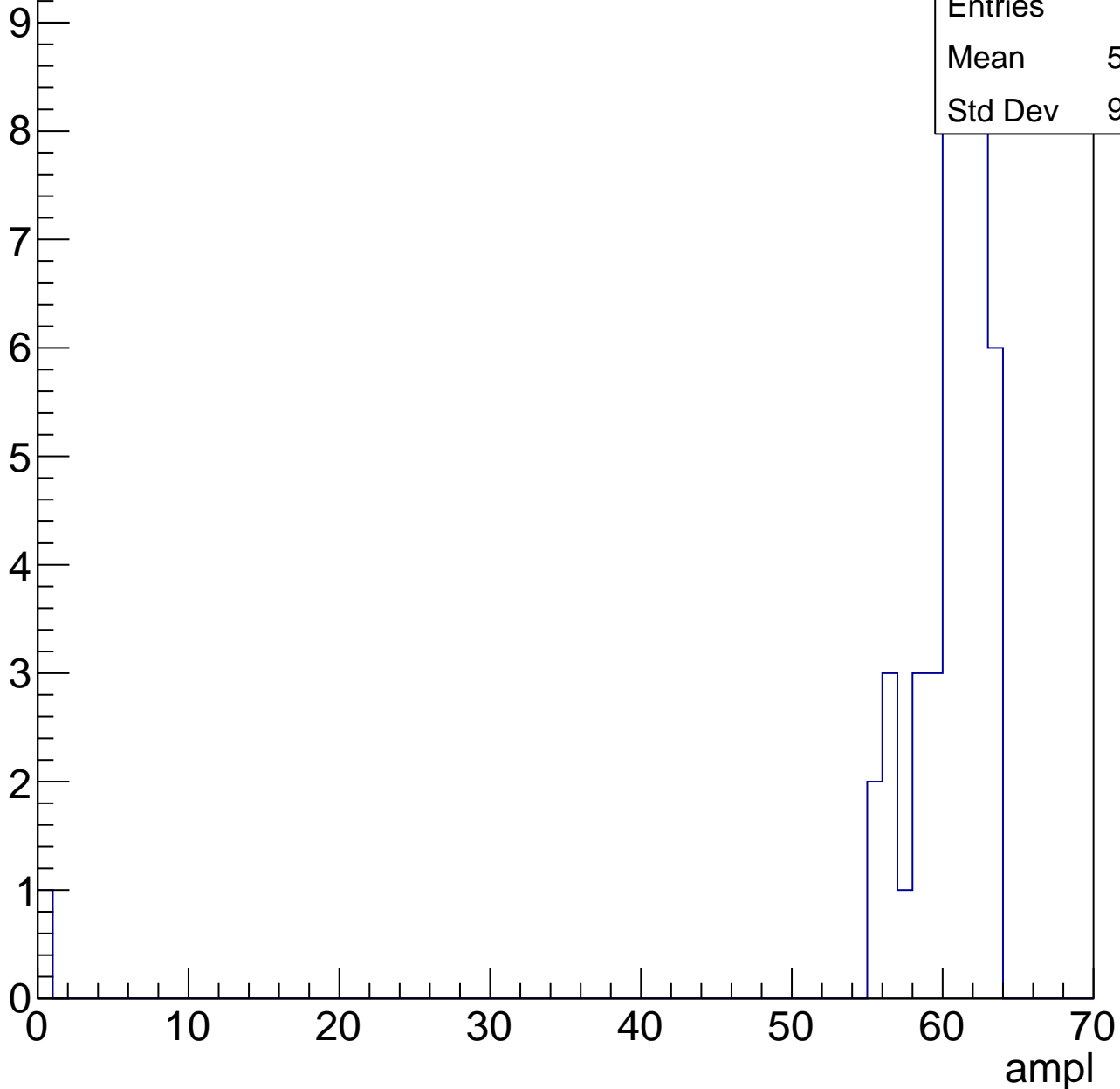
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U21-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch46, adc0

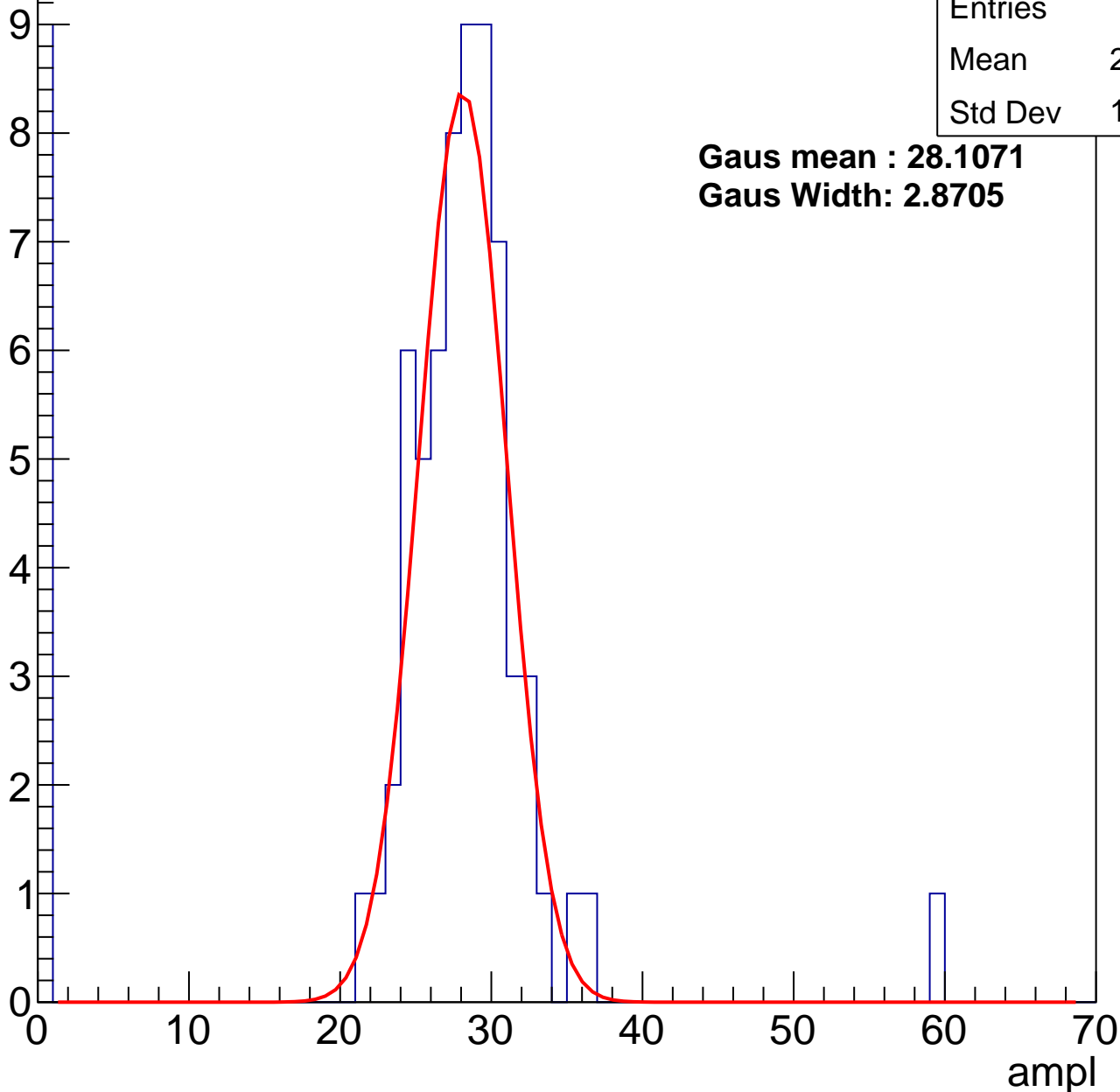
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	24.73
Std Dev	10.33

Gaus mean : 28.1071

Gaus Width: 2.8705



B1L103S, U21-ch46, adc1

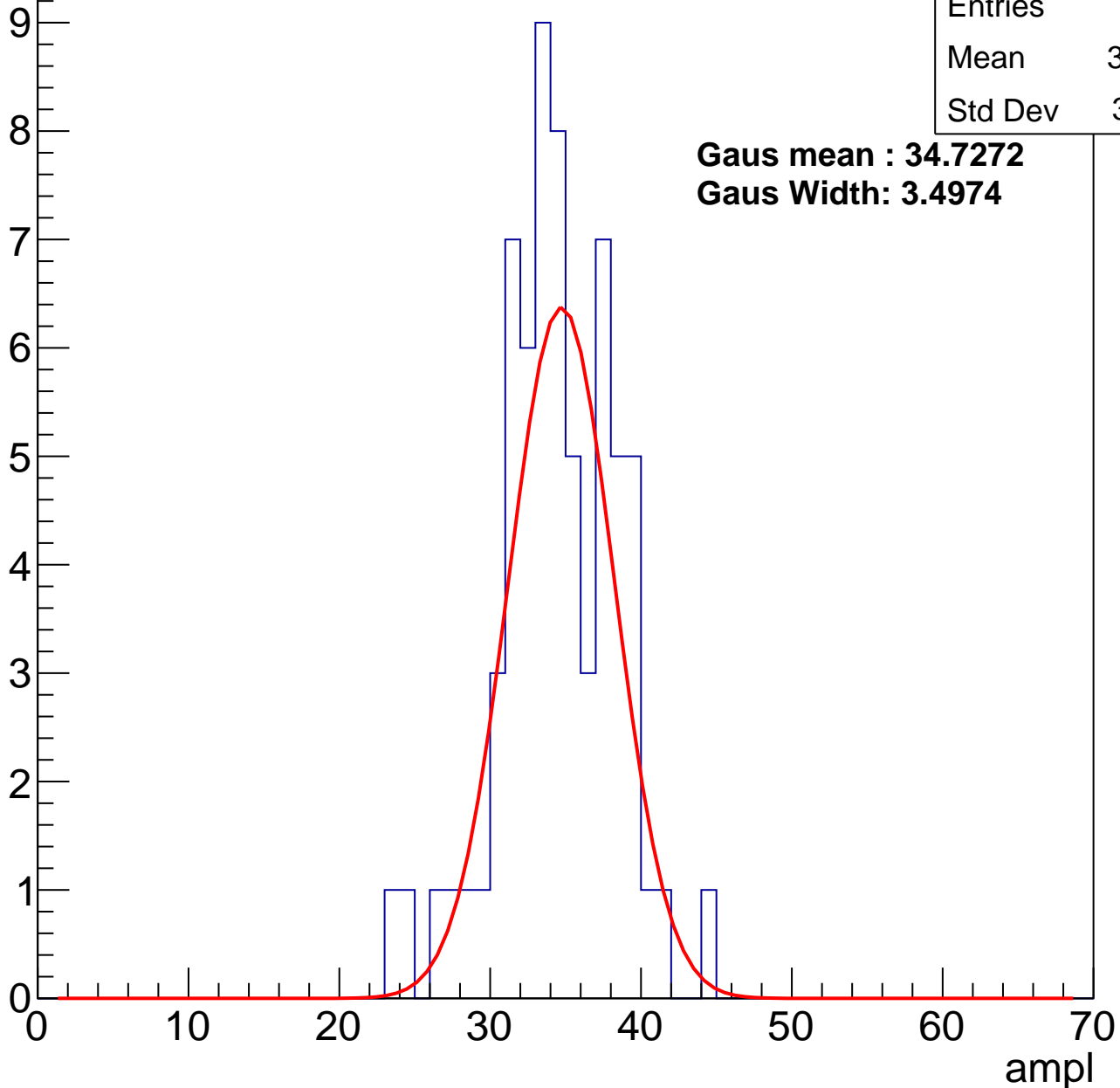
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.99
Std Dev	3.881

Gaus mean : 34.7272

Gaus Width: 3.4974



B1L103S, U21-ch46, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	38.21
Std Dev	11.64

Gaus mean : 41.7609

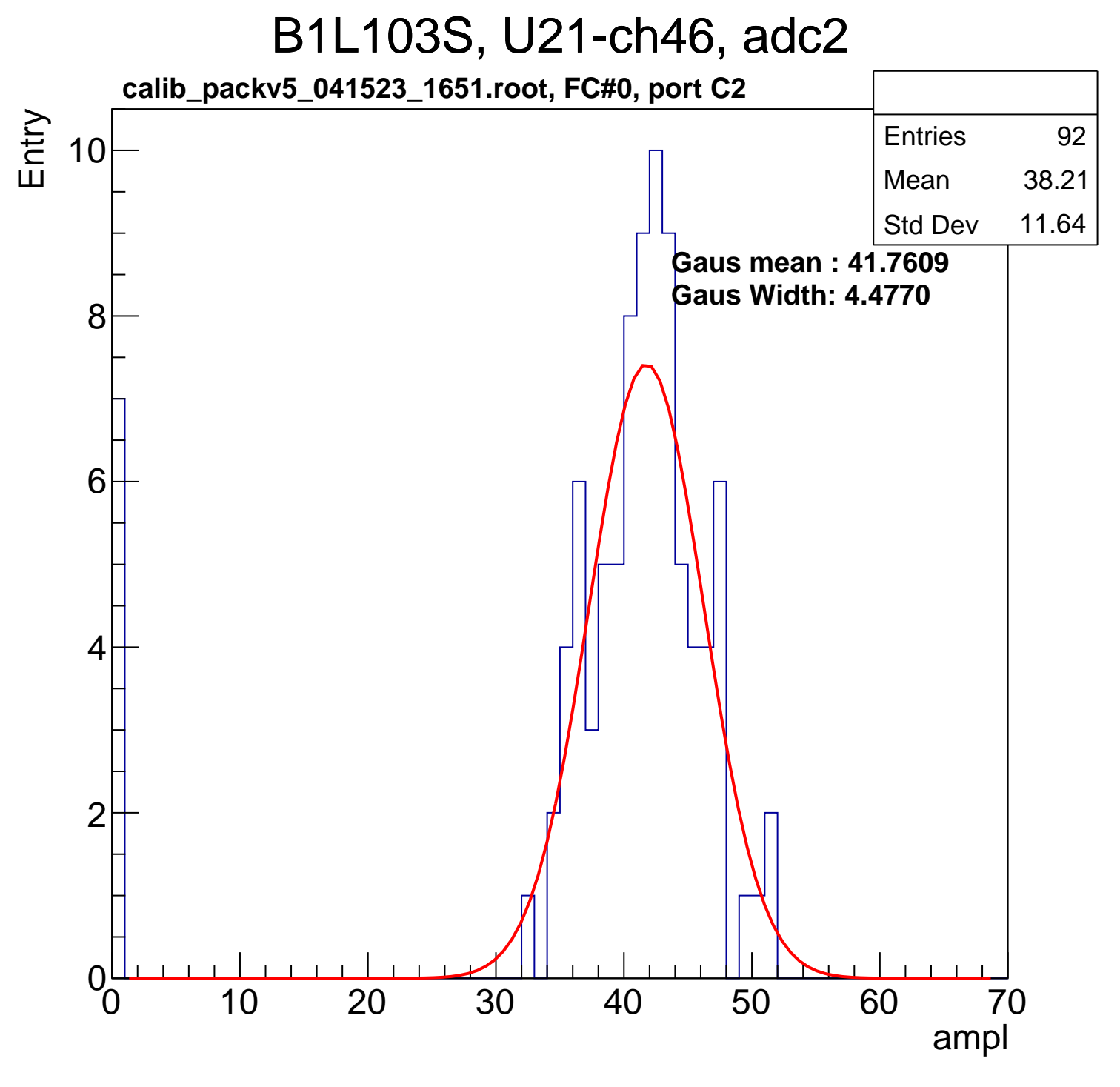
Gaus Width: 4.4770

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

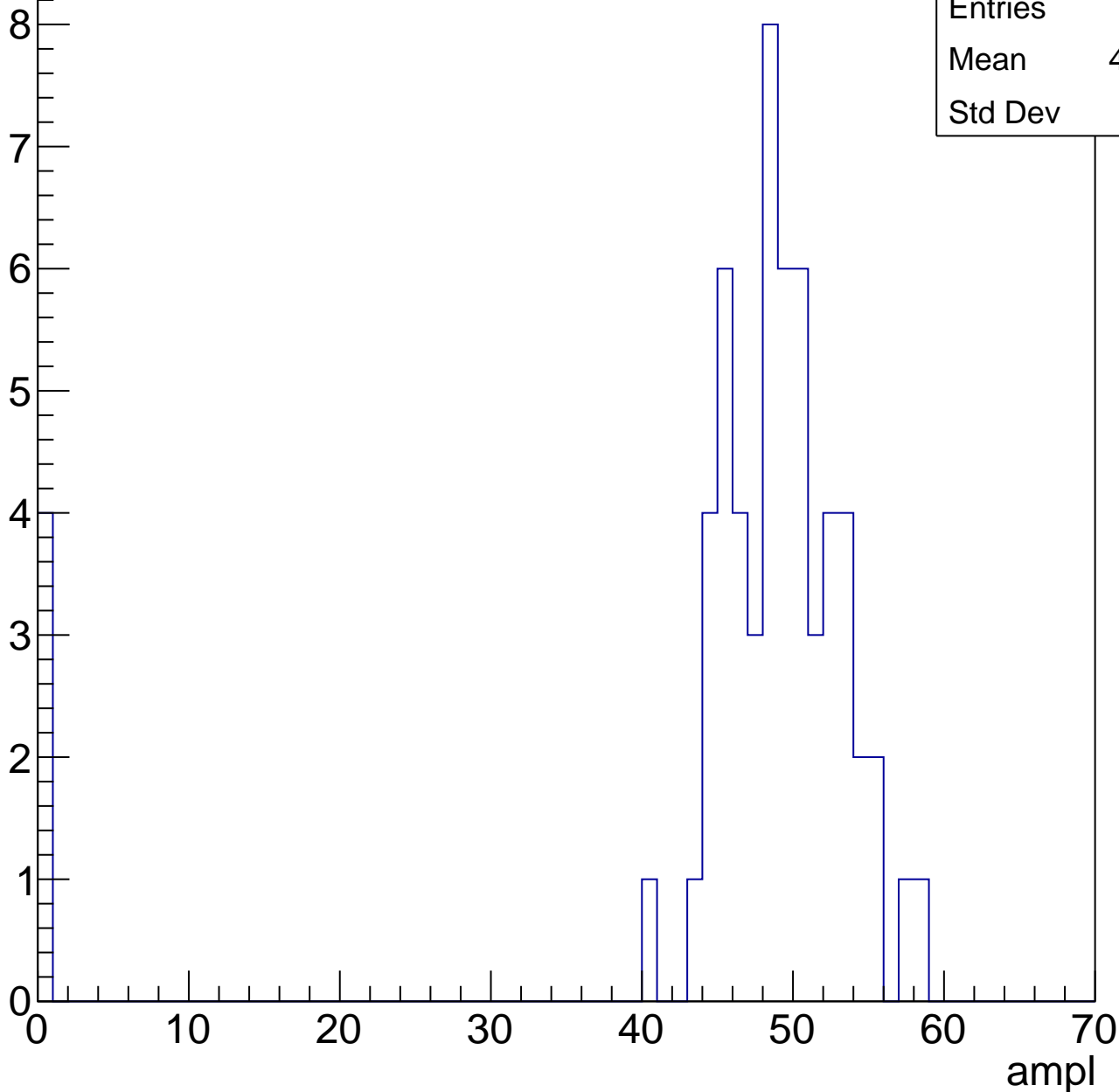


B1L103S, U21-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	45.63
Std Dev	12.7

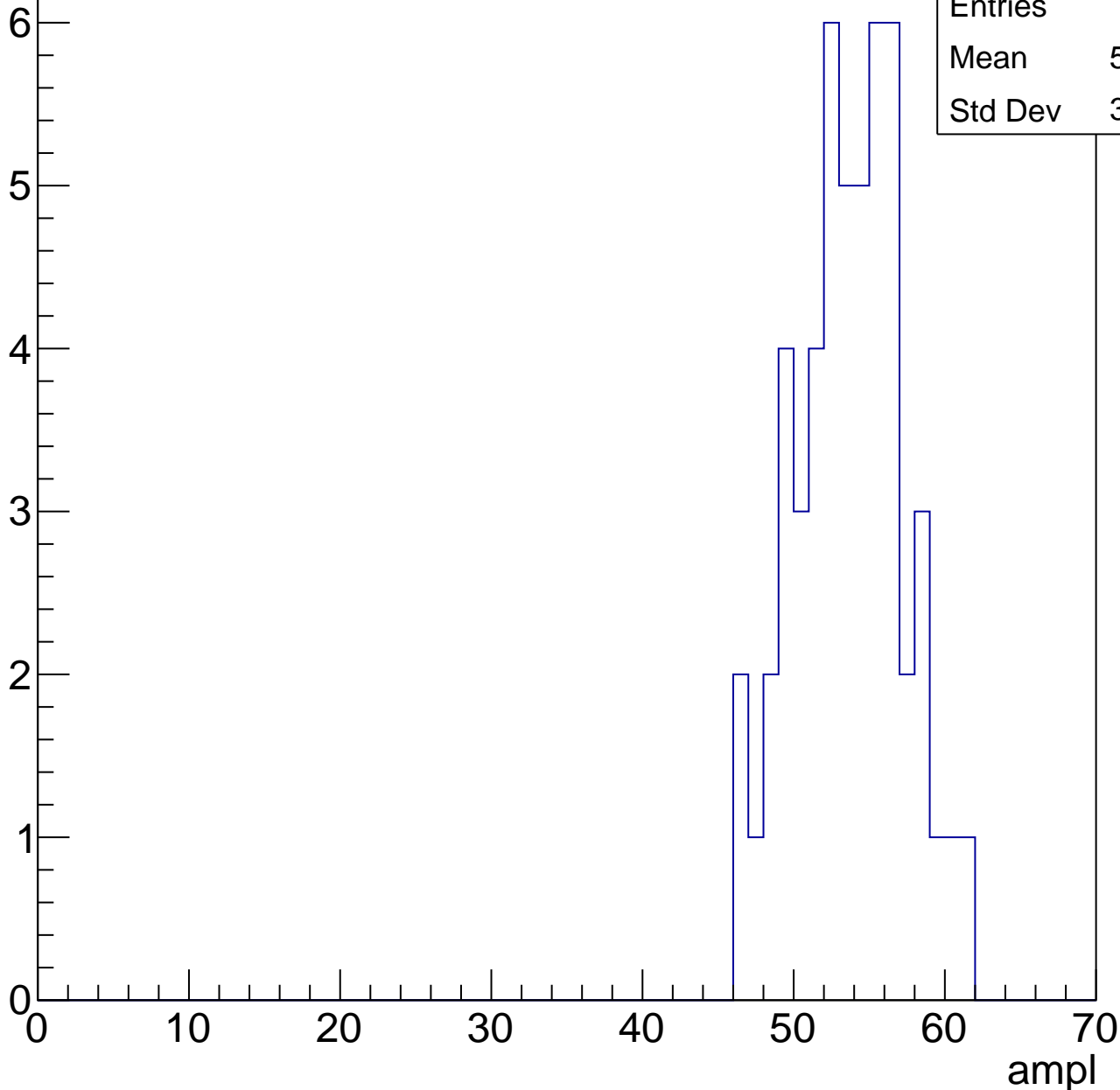


B1L103S, U21-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

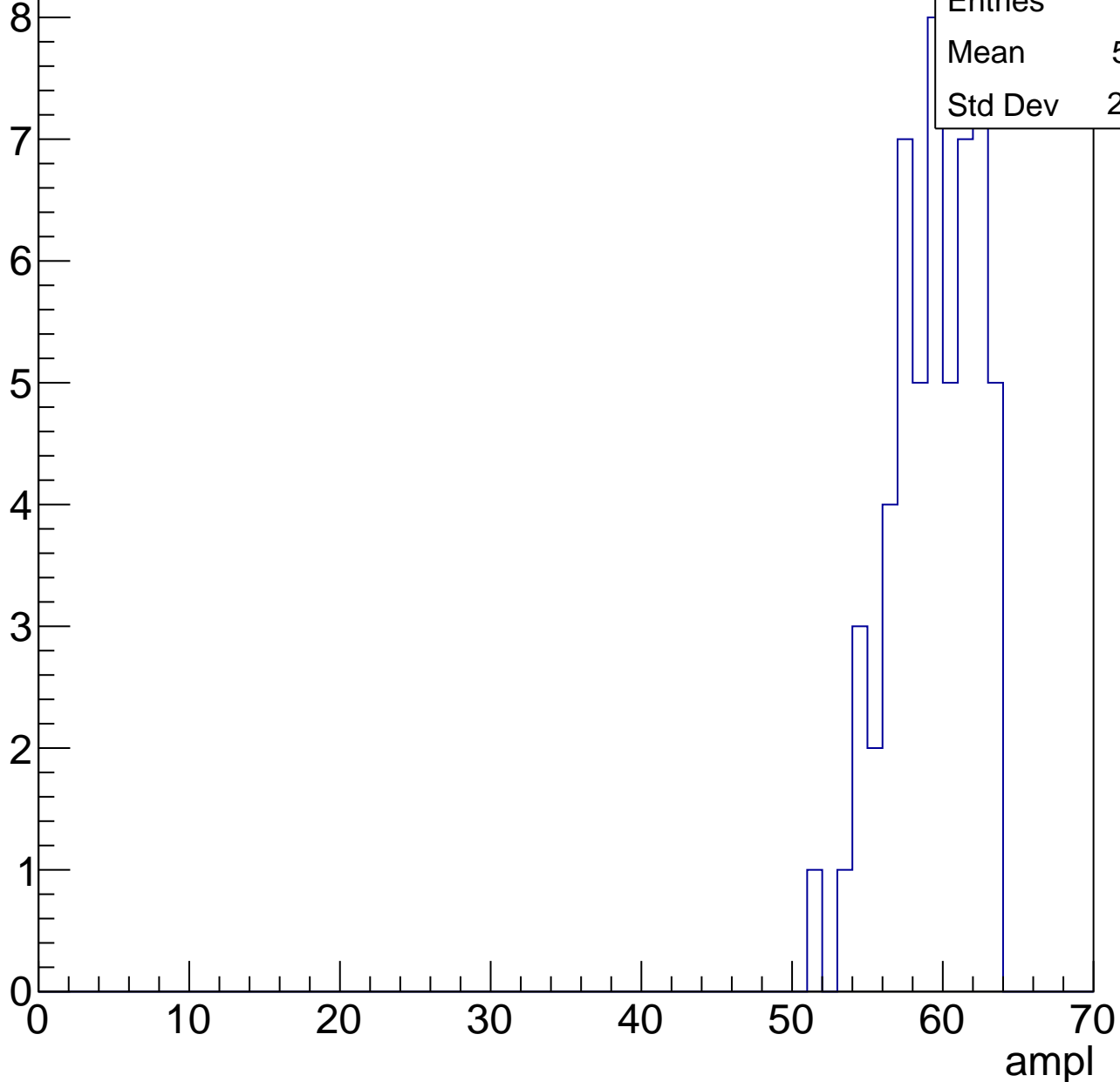
Entries	52
Mean	53.19
Std Dev	3.492



B1L103S, U21-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

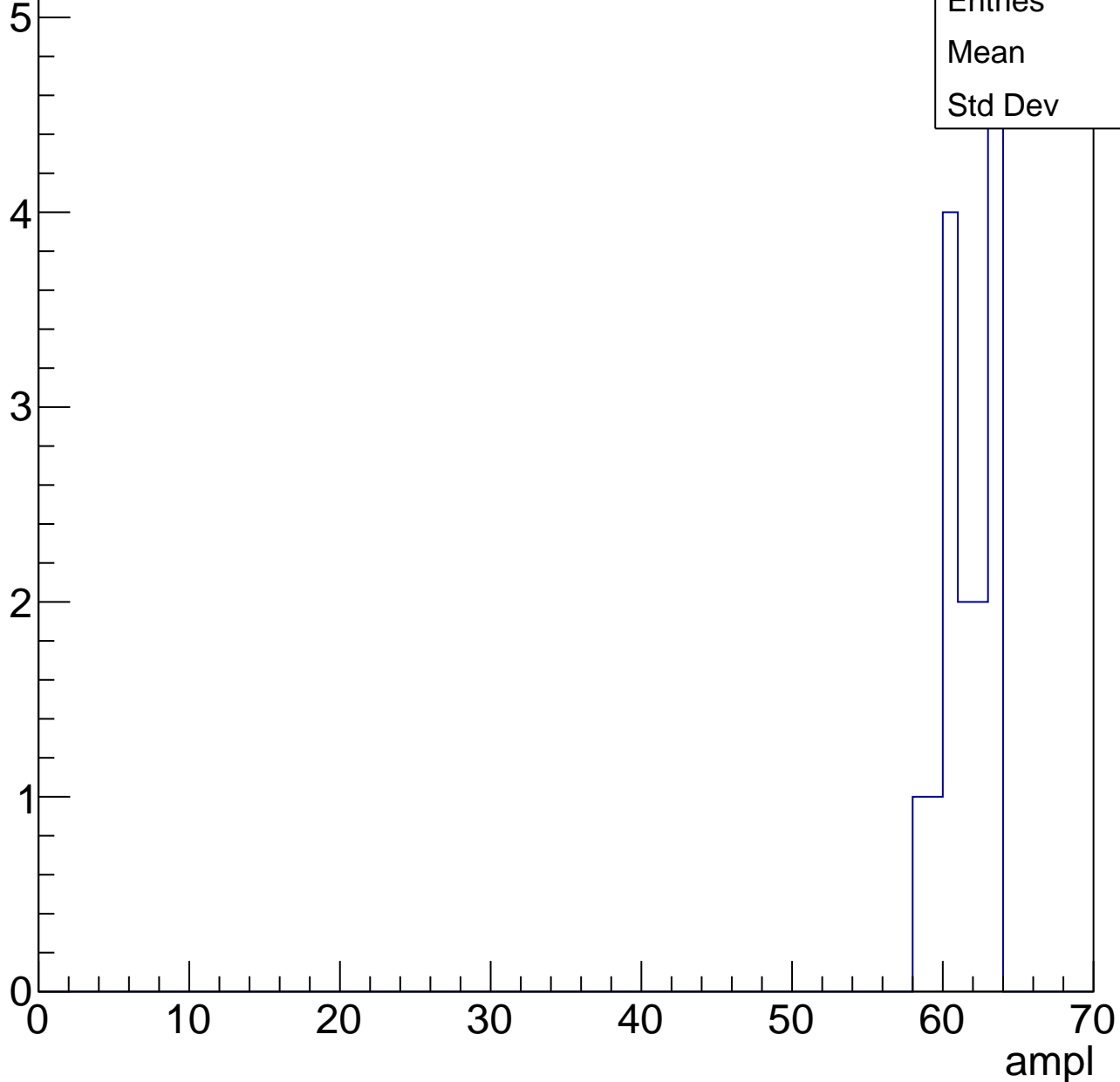
Entry



B1L103S, U21-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



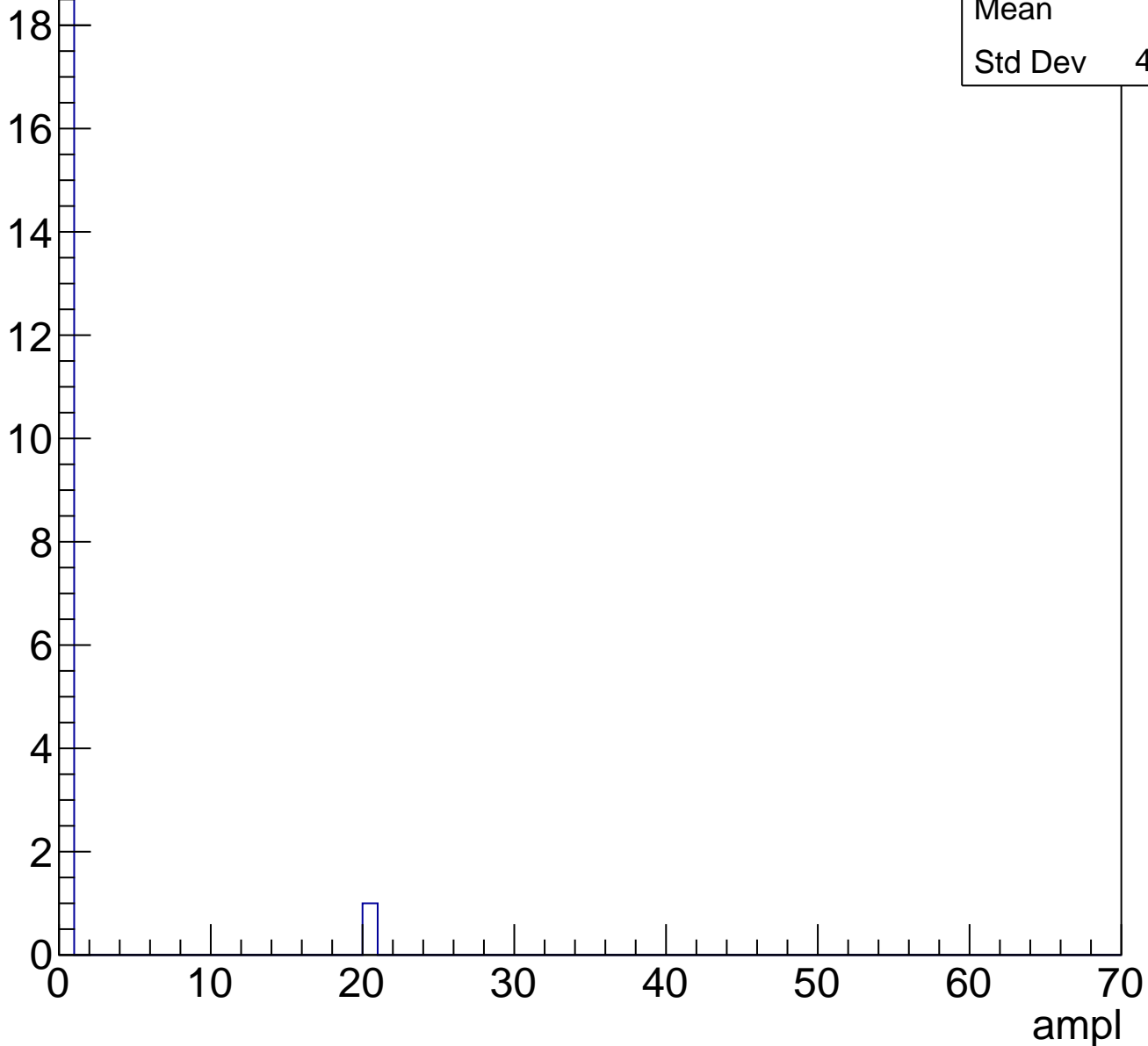
Entries	15
Mean	61.2
Std Dev	1.6

B1L103S, U21-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	1
Std Dev	4.359

Entry



B1L103S, U21-ch47, adc0

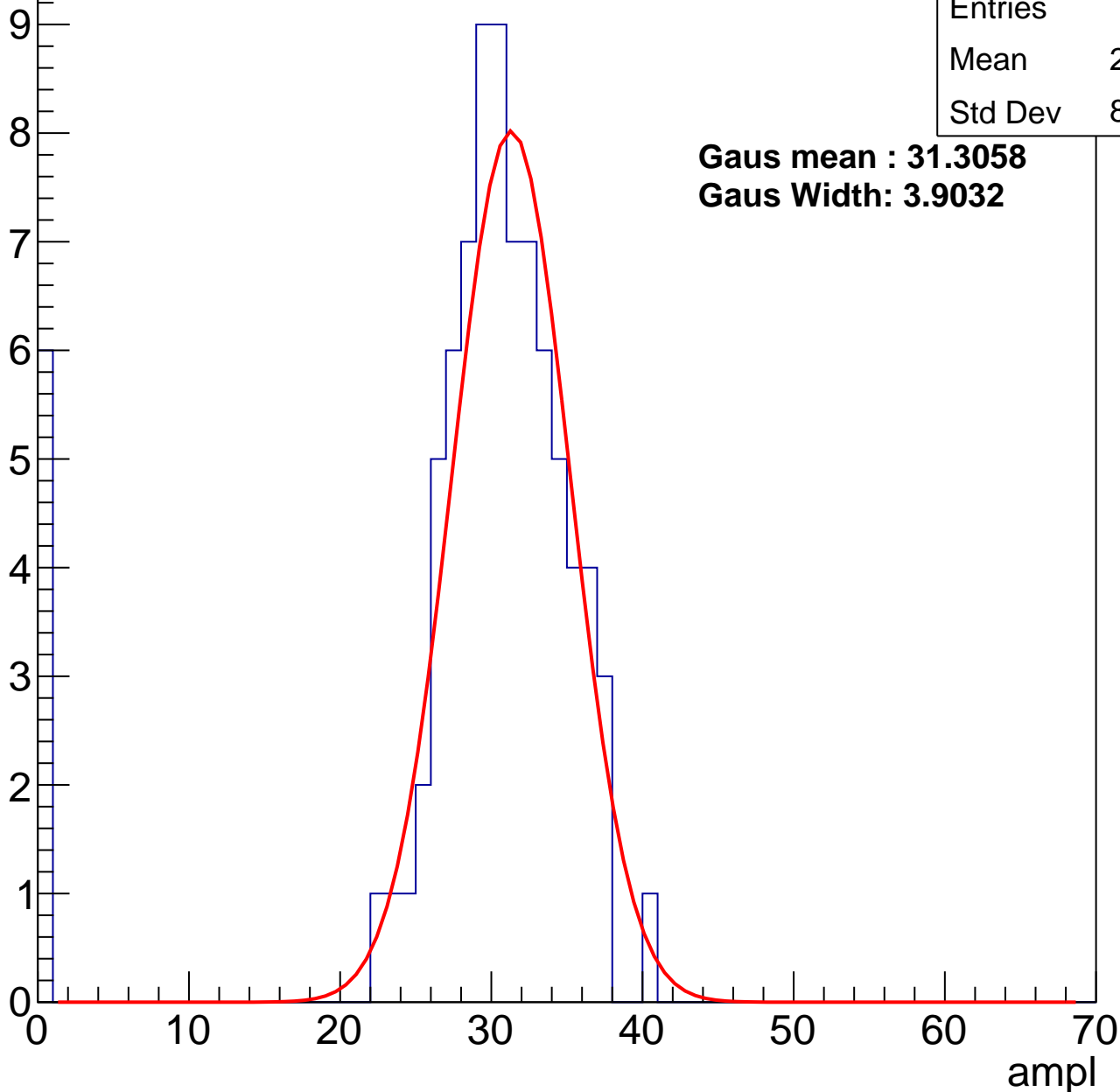
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	28.36
Std Dev	8.594

Gaus mean : 31.3058

Gaus Width: 3.9032



B1L103S, U21-ch47, adc1

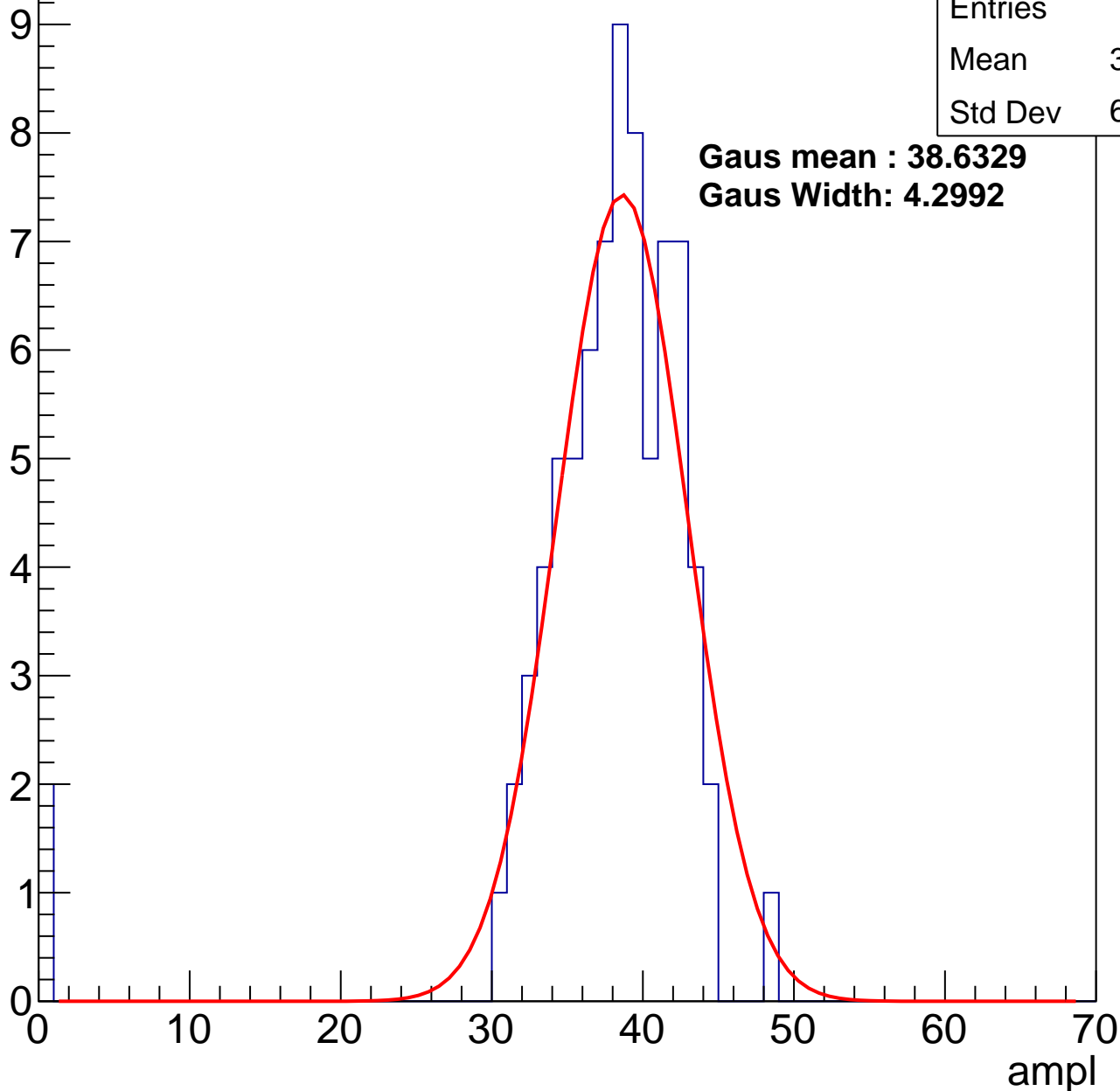
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	36.96
Std Dev	6.984

Gaus mean : 38.6329

Gaus Width: 4.2992



B1L103S, U21-ch47, adc2

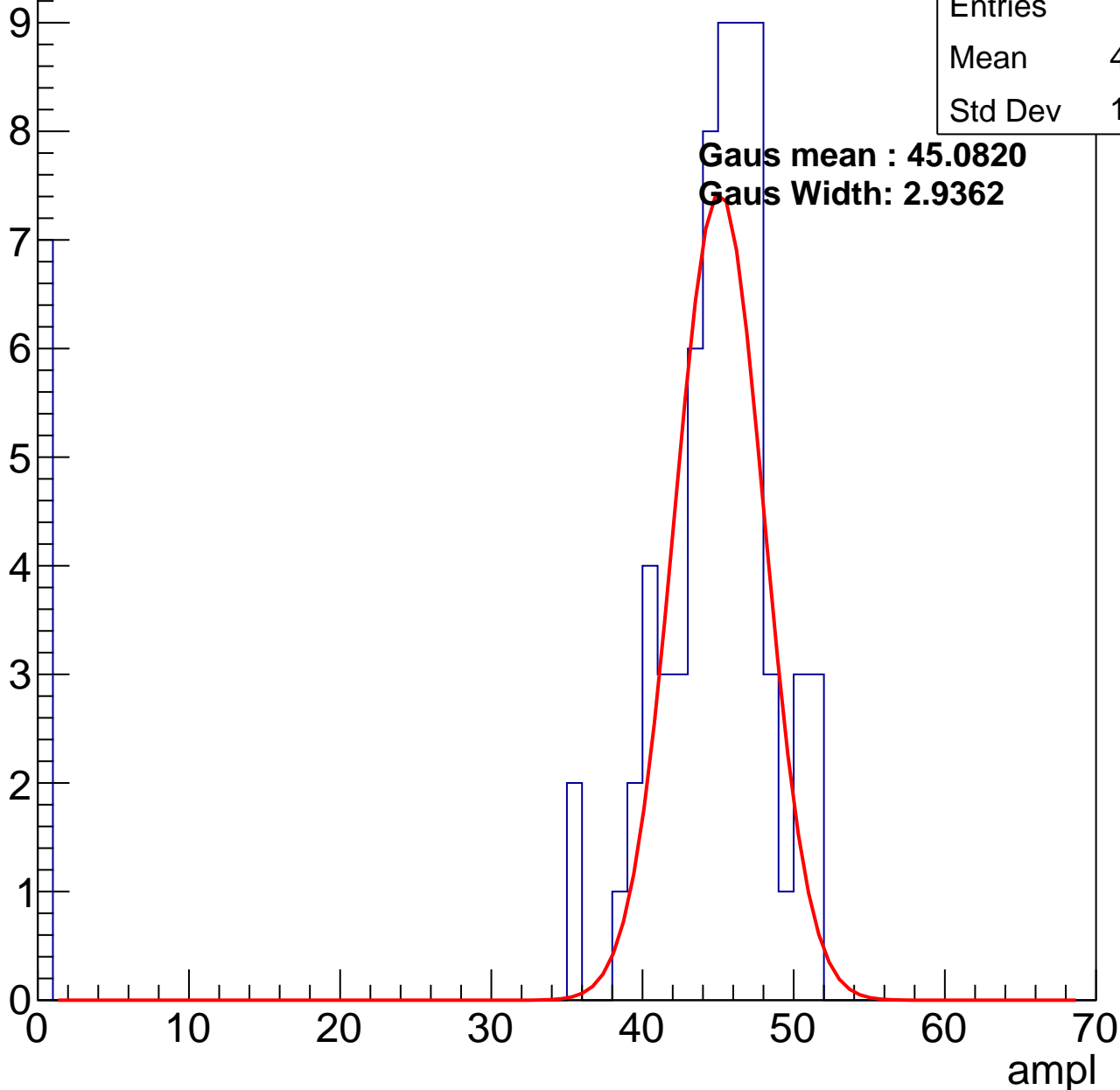
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.32
Std Dev	13.53

Gaus mean : 45.0820

Gaus Width: 2.9362

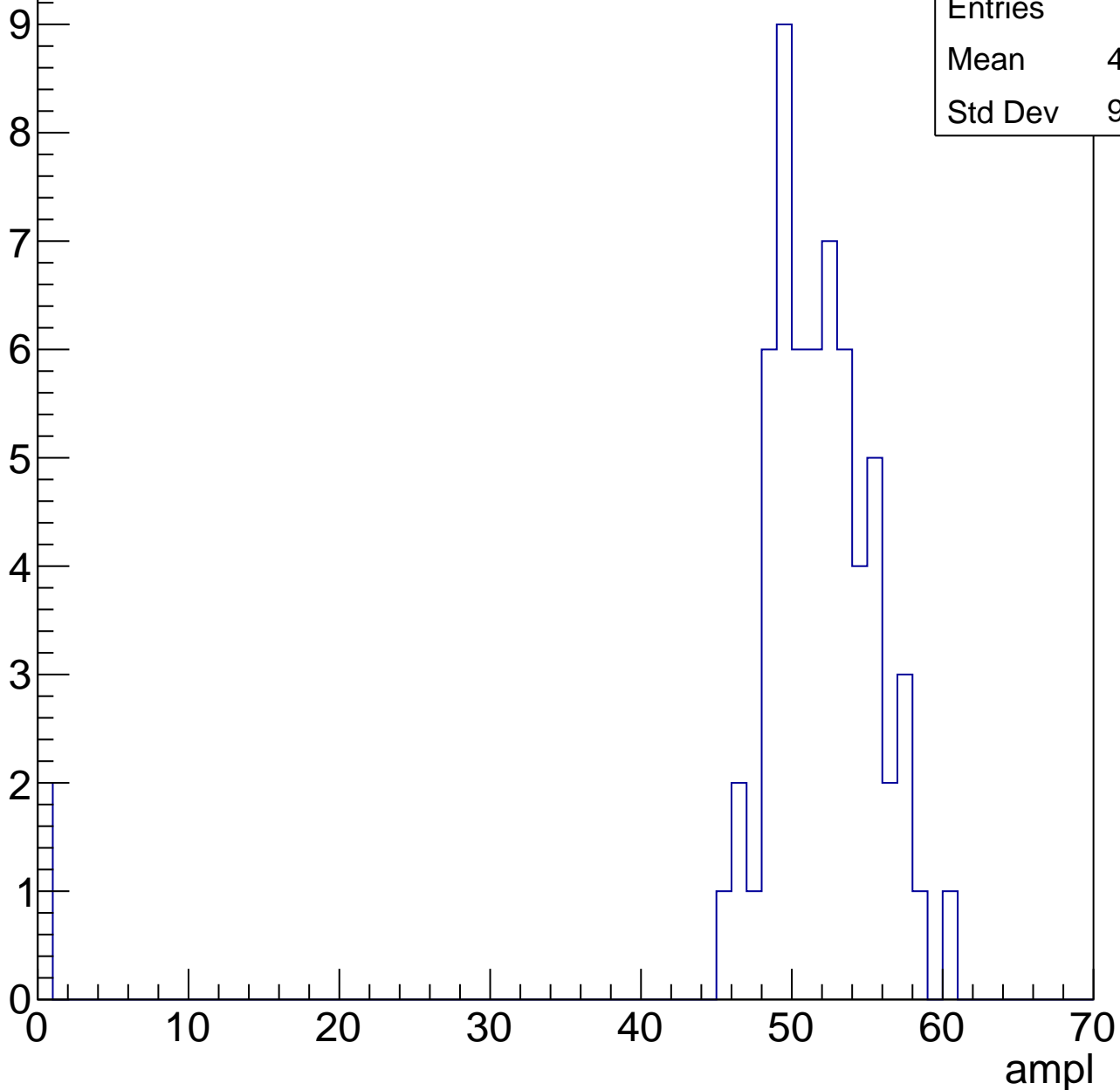


B1L103S, U21-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

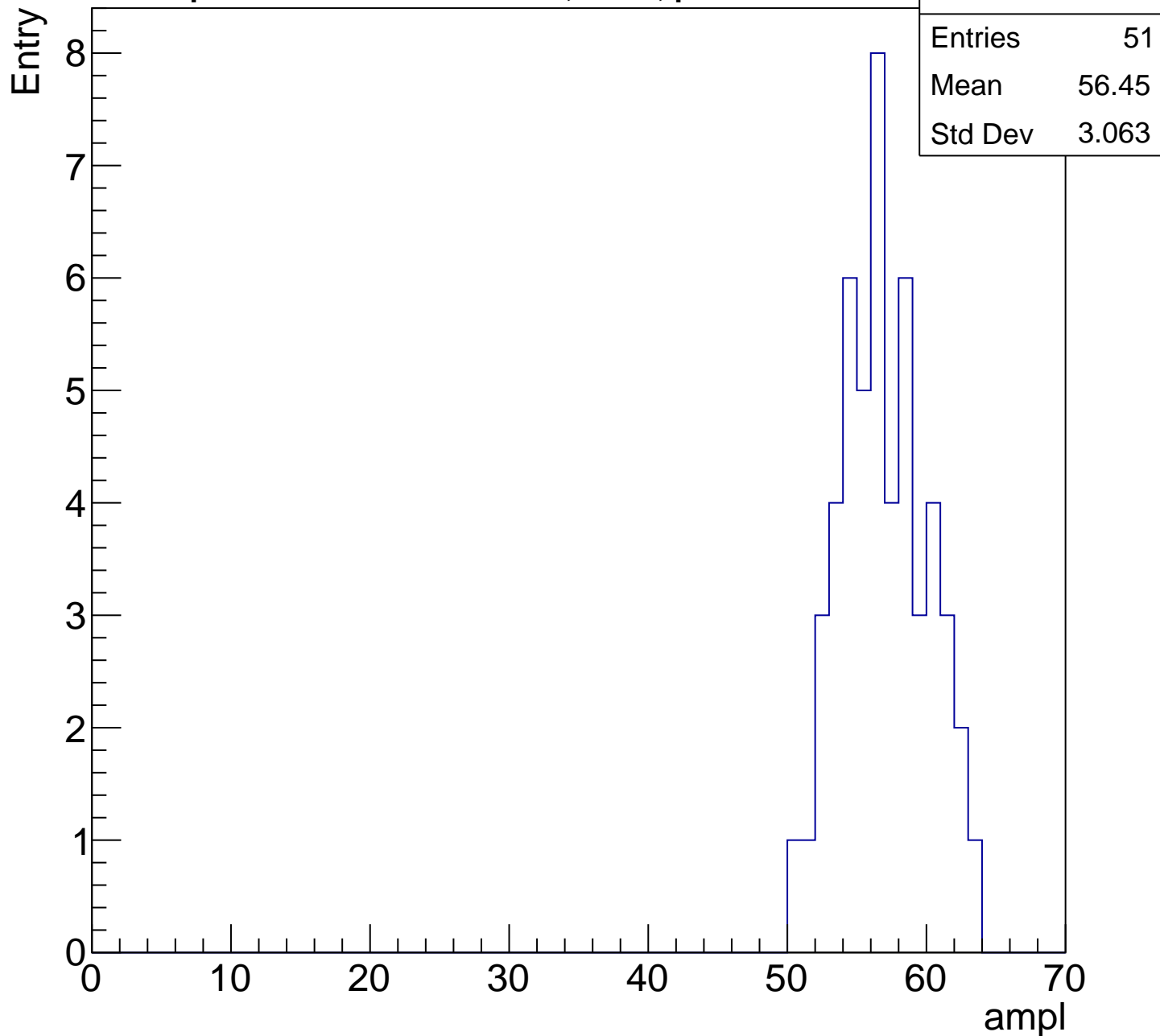
Entry

Entries	62
Mean	49.89
Std Dev	9.637



B1L103S, U21-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

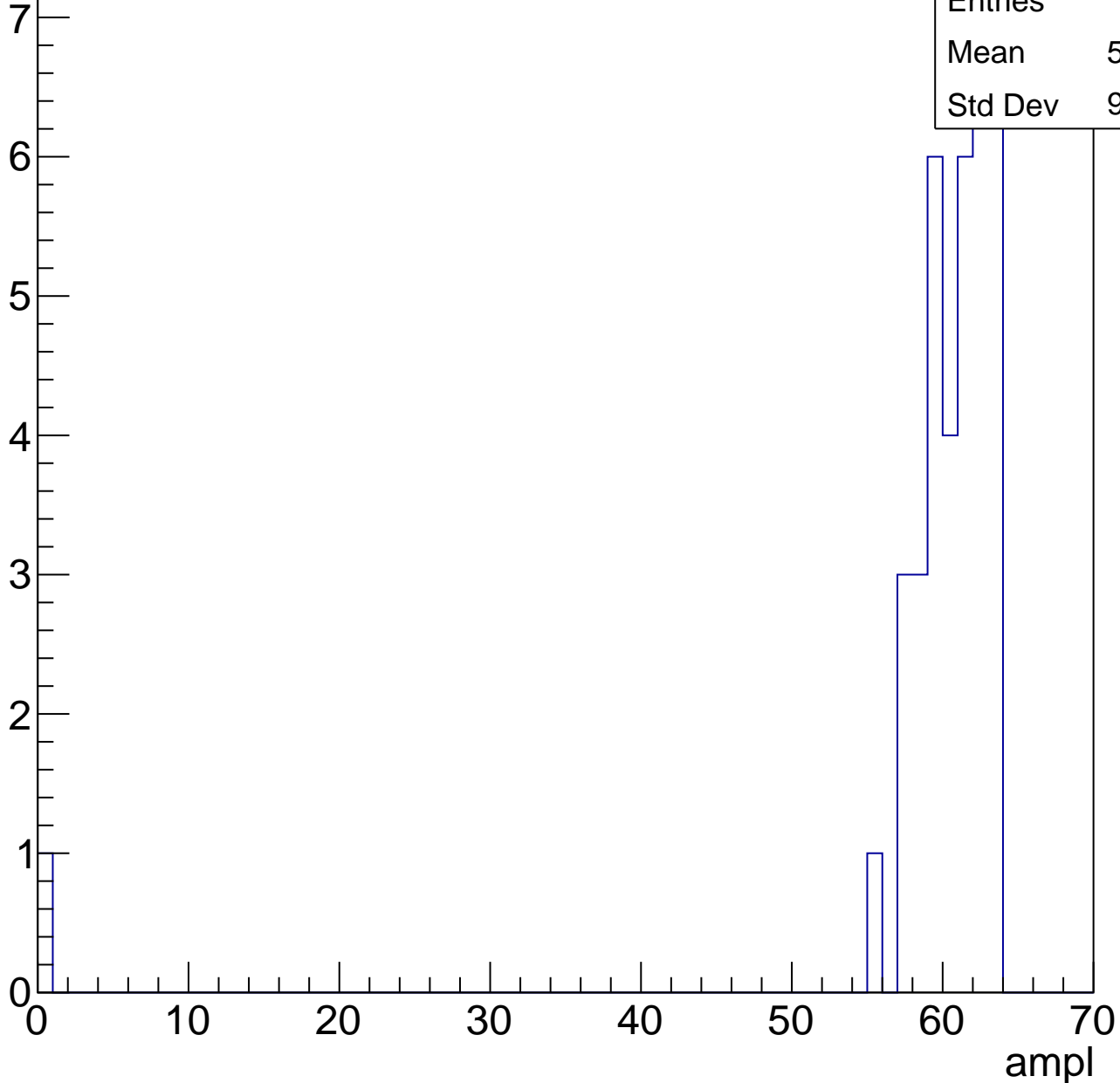


B1L103S, U21-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.82
Std Dev	9.886



B1L103S, U21-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

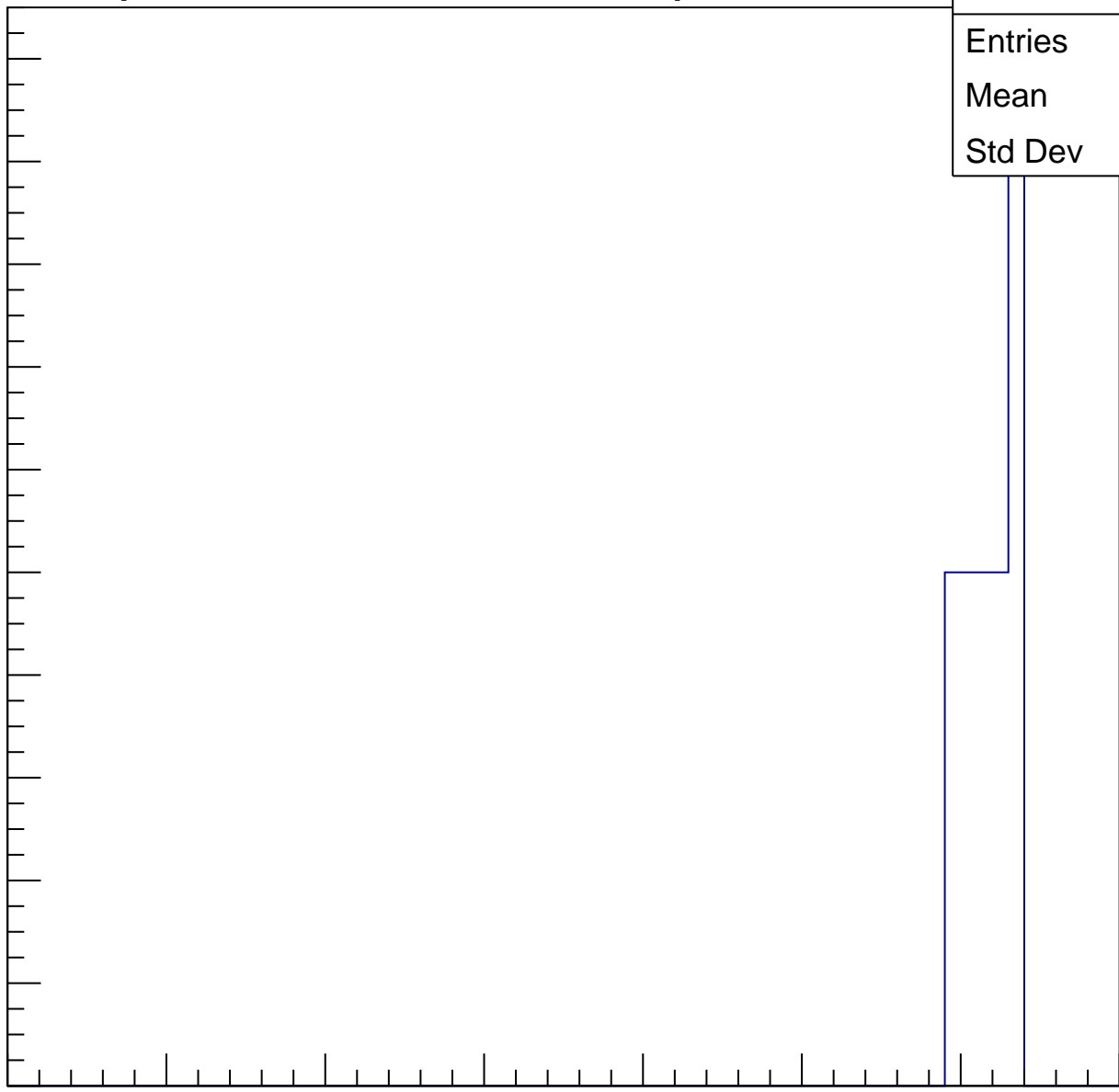
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.491

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry

18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U21-ch48, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	21.7
Std Dev	10.83

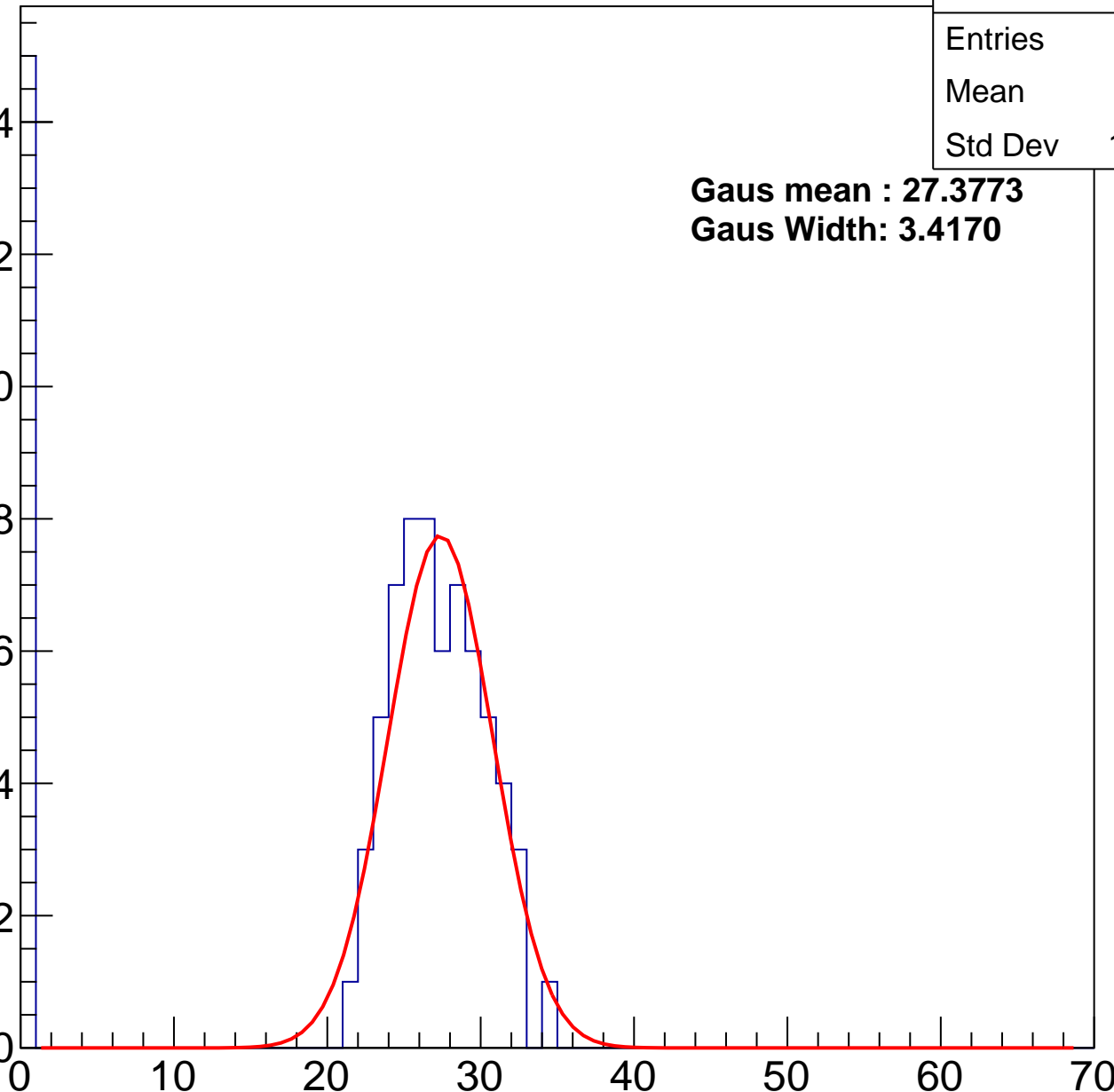
Gaus mean : 27.3773

Gaus Width: 3.4170

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch48, adc1

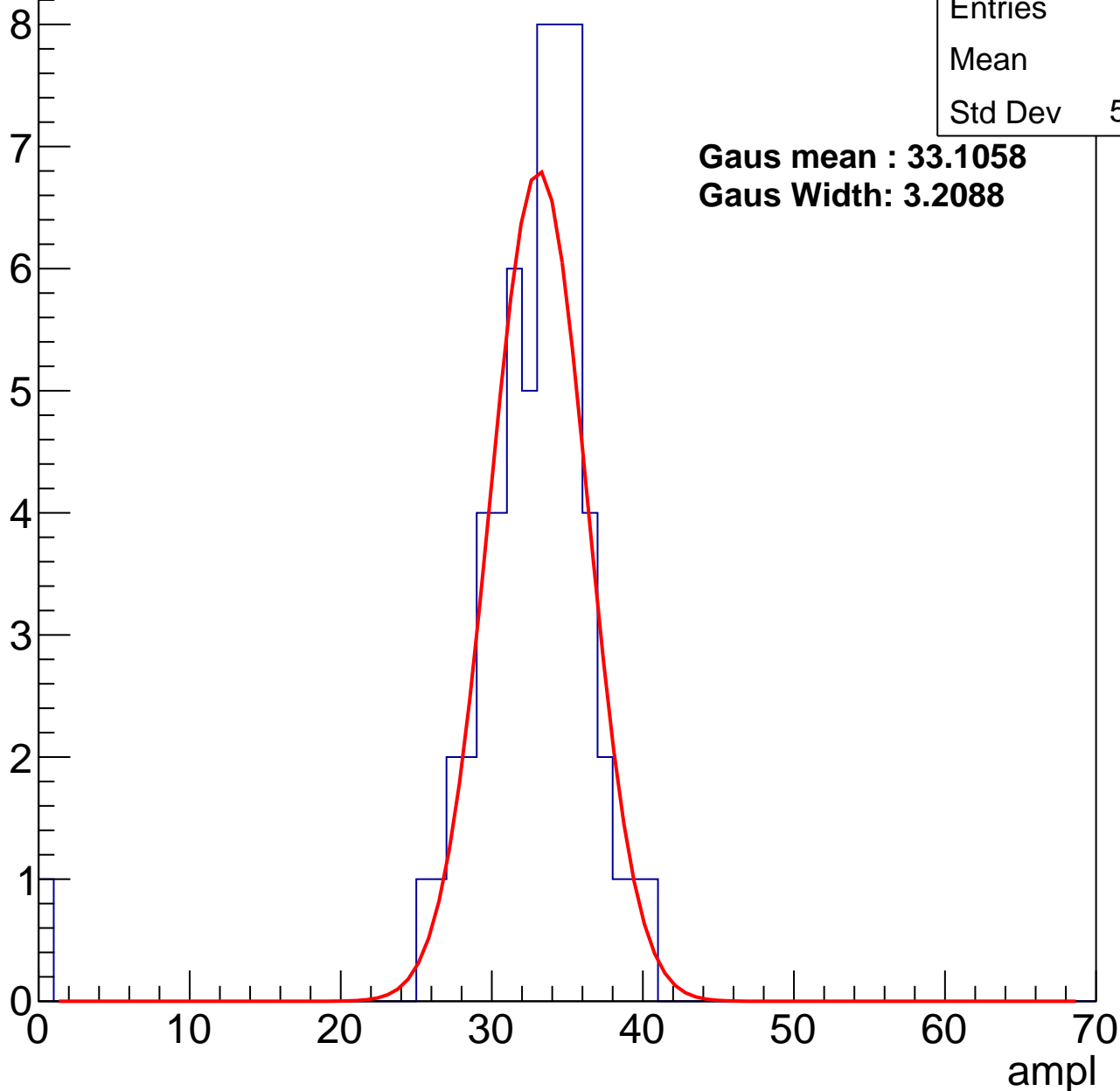
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	32.1
Std Dev	5.245

Gaus mean : 33.1058

Gaus Width: 3.2088



B1L103S, U21-ch48, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	38.99
Std Dev	3.271

Gaus mean : 40.0006

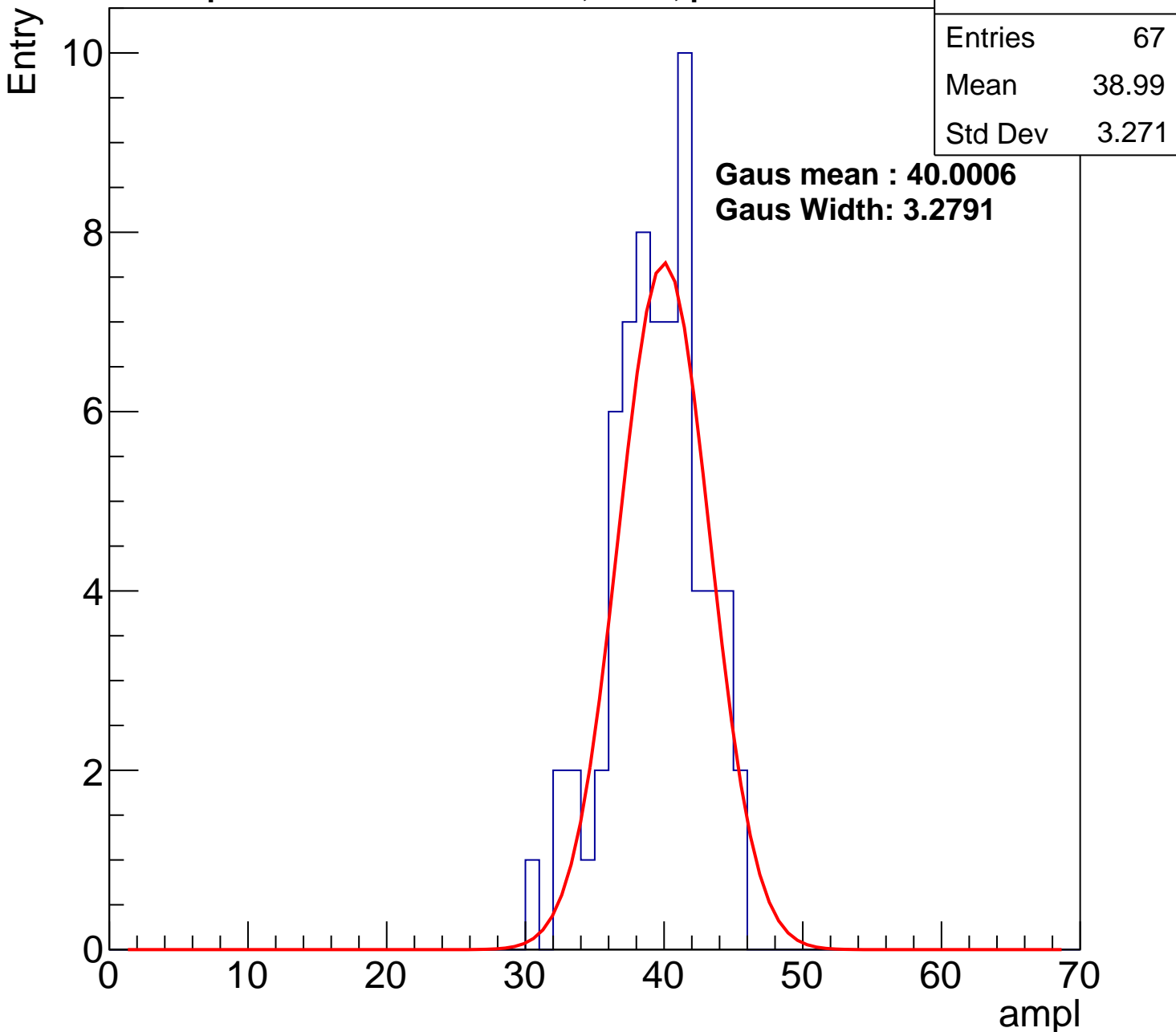
Gaus Width: 3.2791

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	37.96
Std Dev	17.39

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

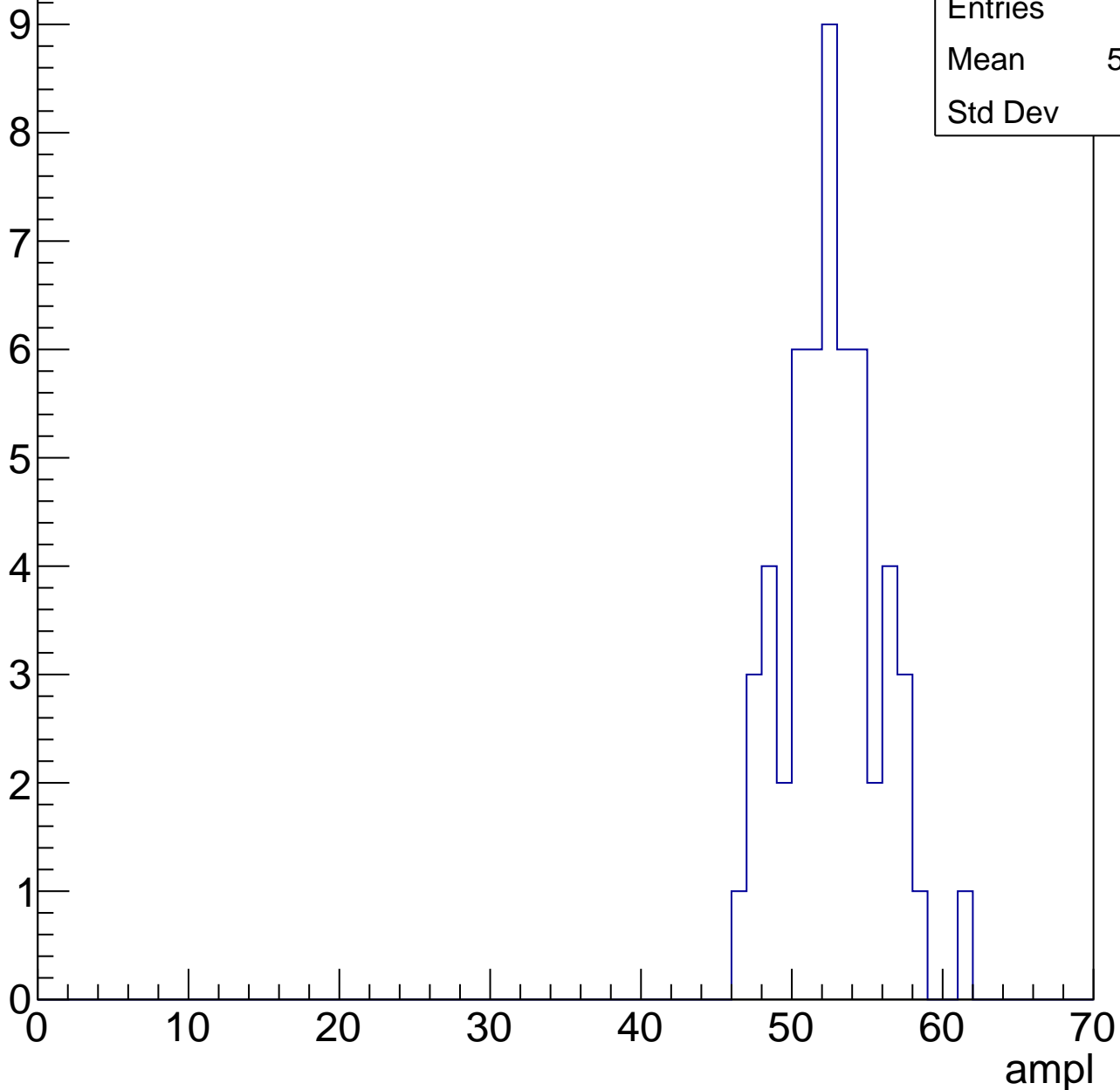
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B1L103S, U21-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	52.17
Std Dev	3.12



B1L103S, U21-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	56.92
Std Dev	7.425

Entry

10

8

6

4

2

0

0

10

20

30

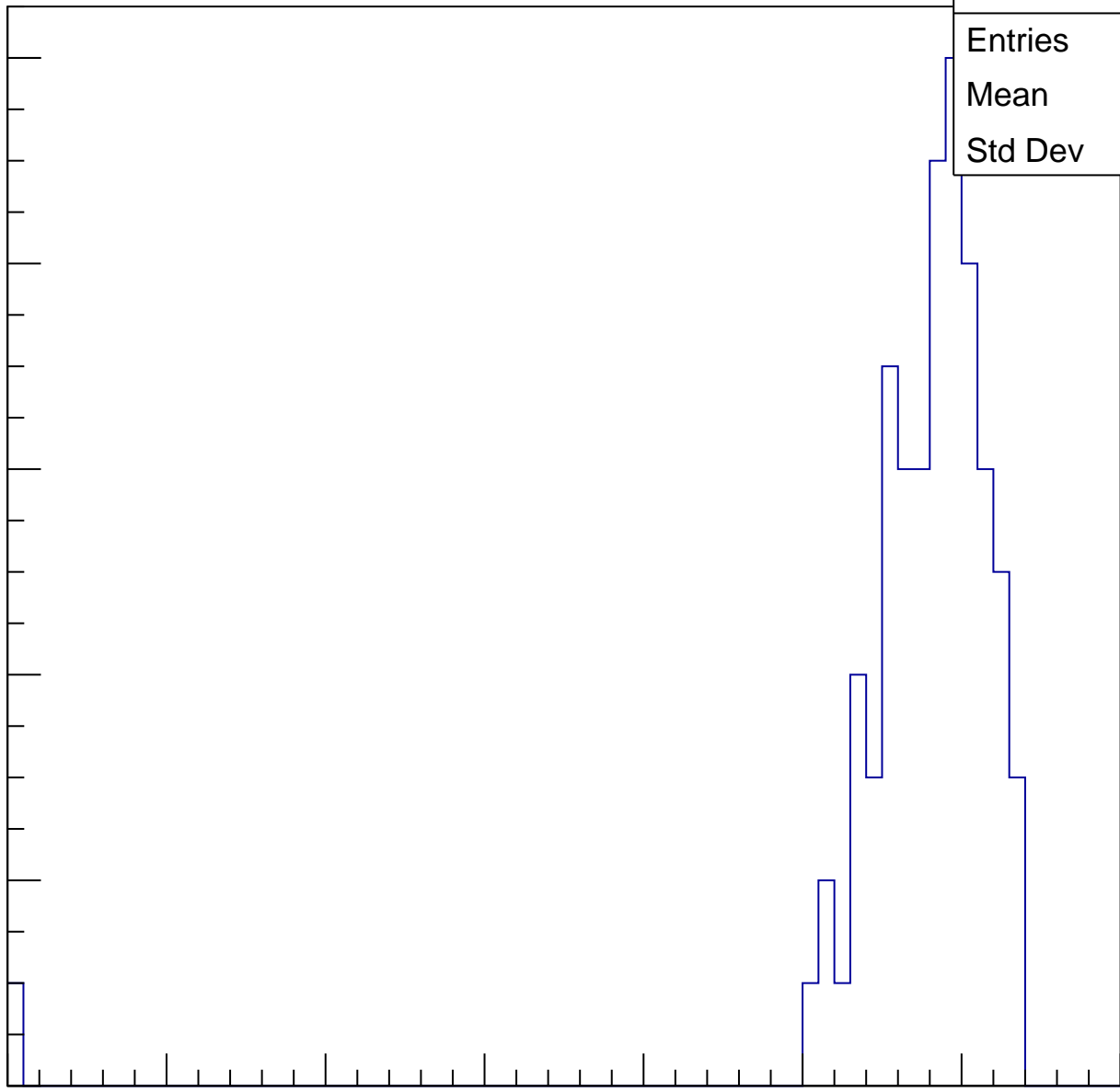
40

50

60

70

ampl

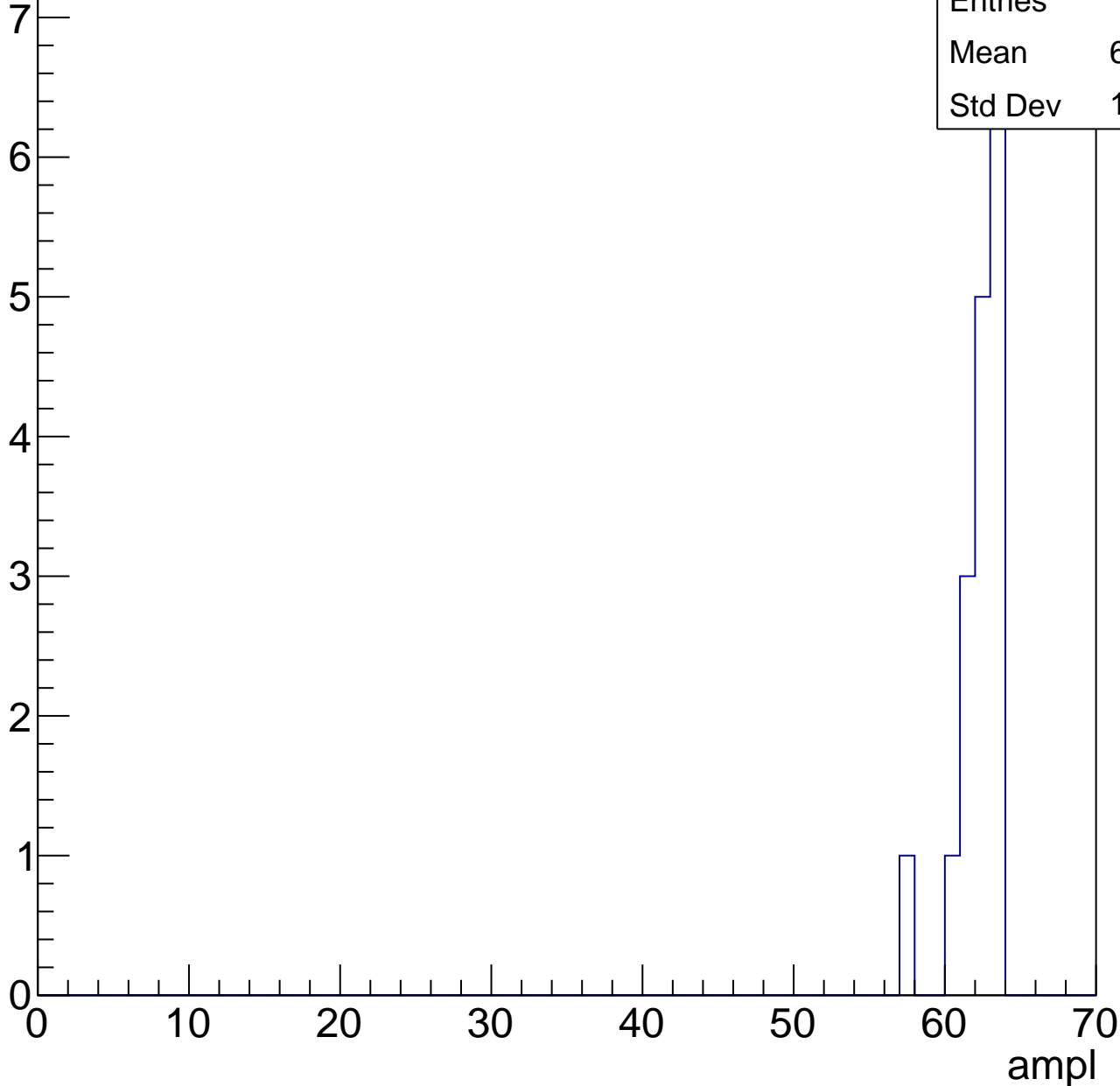


B1L103S, U21-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.82
Std Dev	1.504

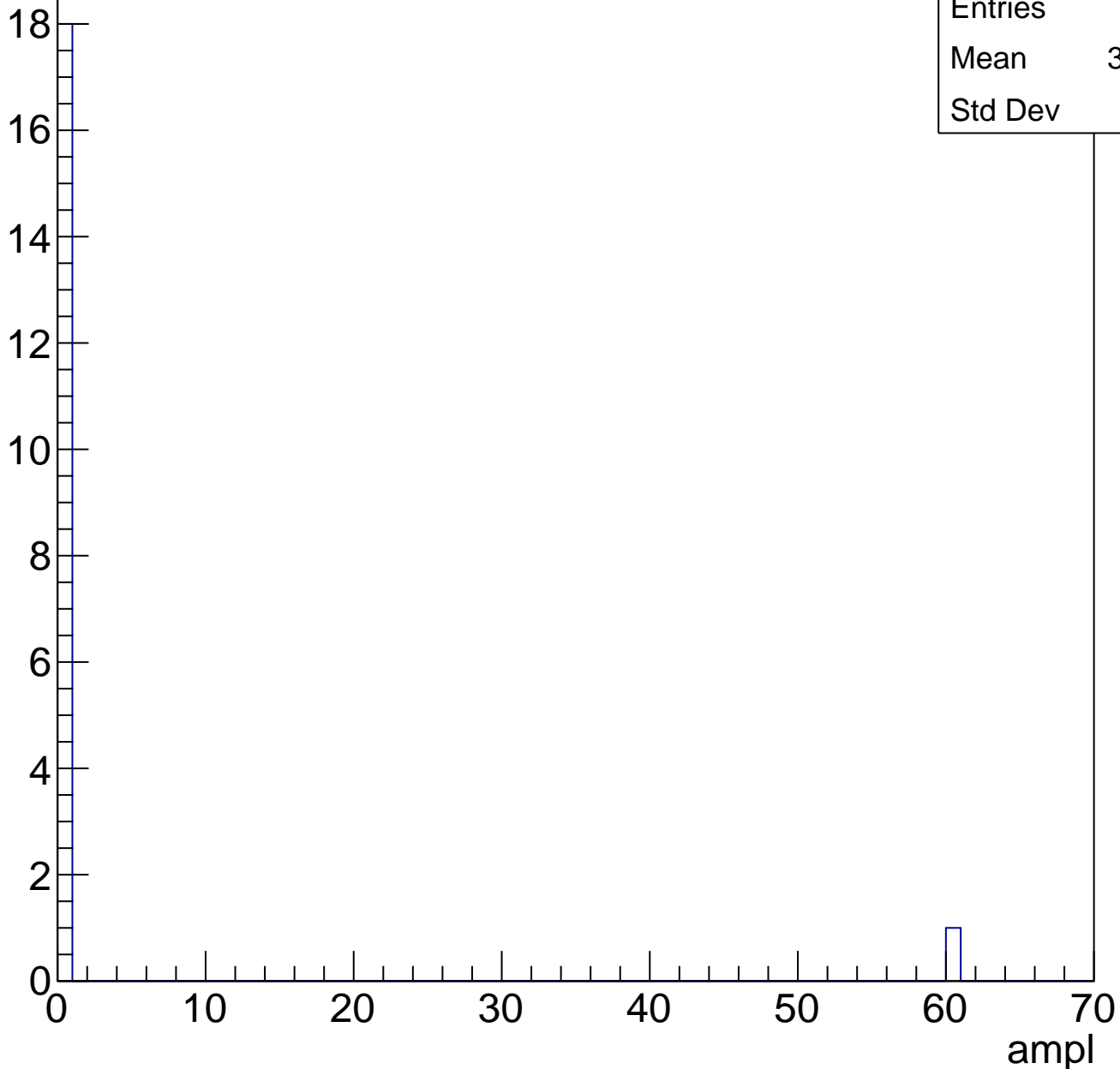


B1L103S, U21-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.158
Std Dev	13.4

Entry



B1L103S, U21-ch49, adc0

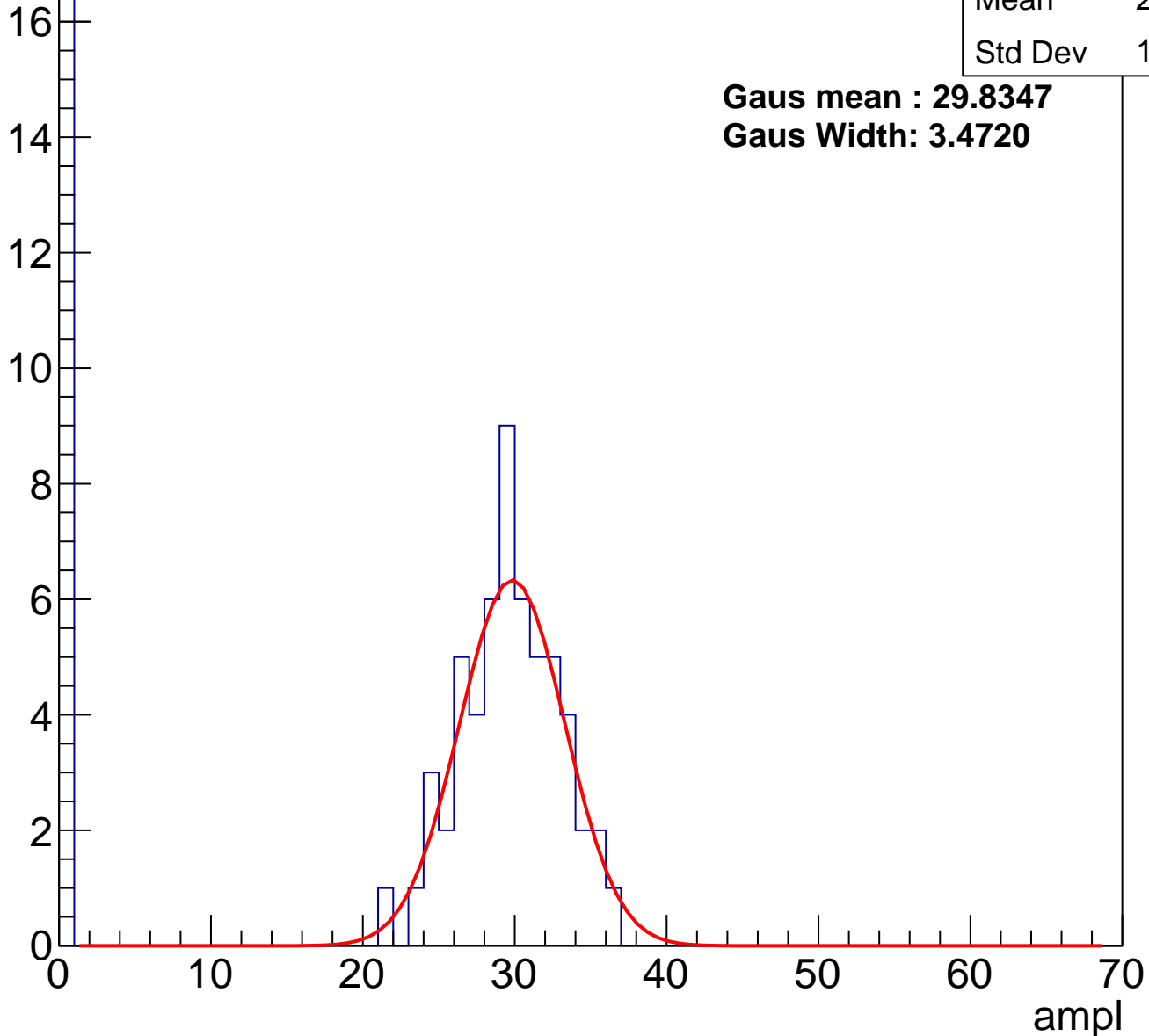
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	22.38
Std Dev	12.65

Gaus mean : 29.8347

Gaus Width: 3.4720

Entry



B1L103S, U21-ch49, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	29.8
Std Dev	13

Gaus mean : 35.7646

Gaus Width: 3.8702

Entry

12

10

8

6

4

2

0

0

10

20

30

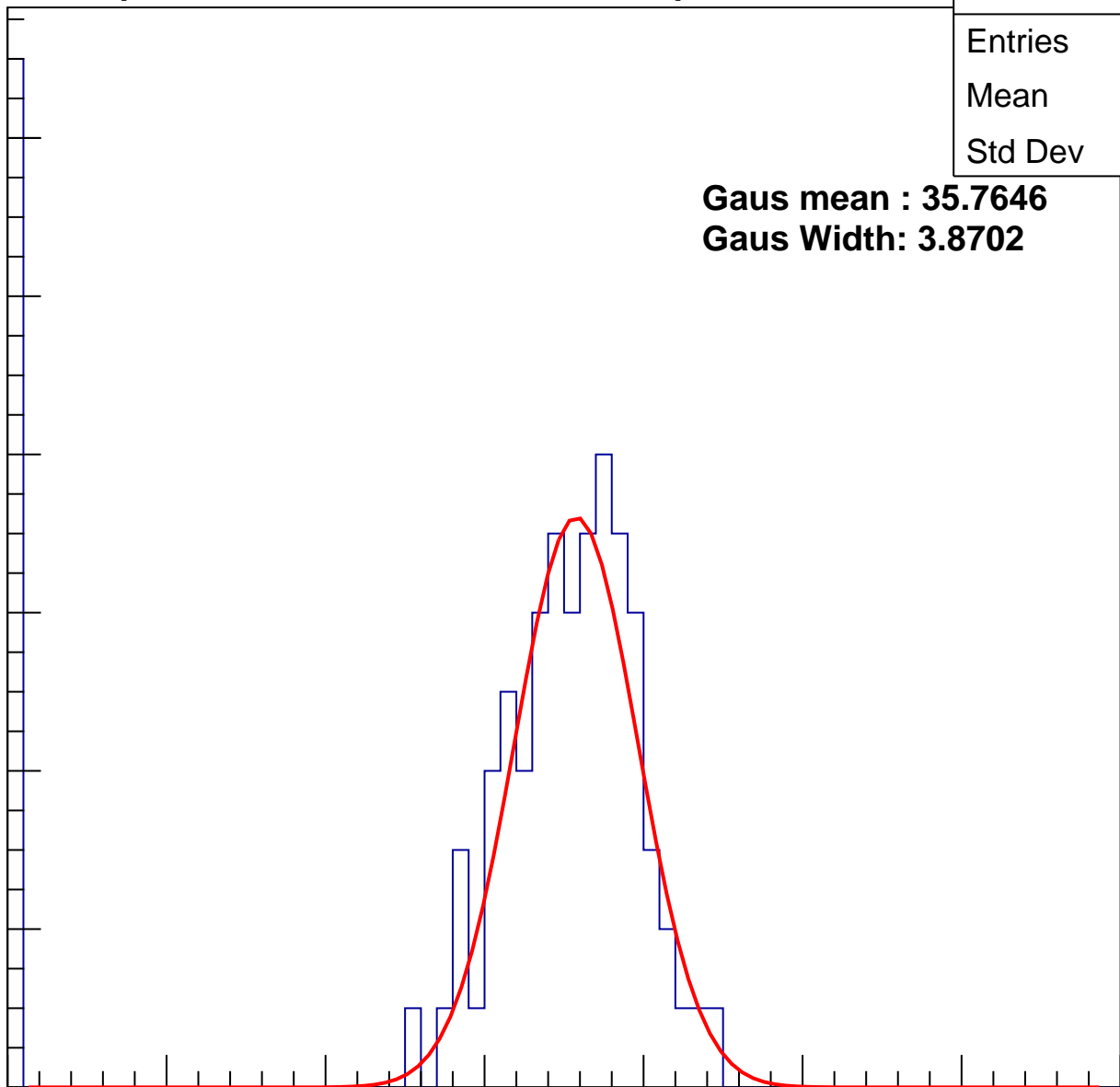
40

50

60

70

ampl



B1L103S, U21-ch49, adc2

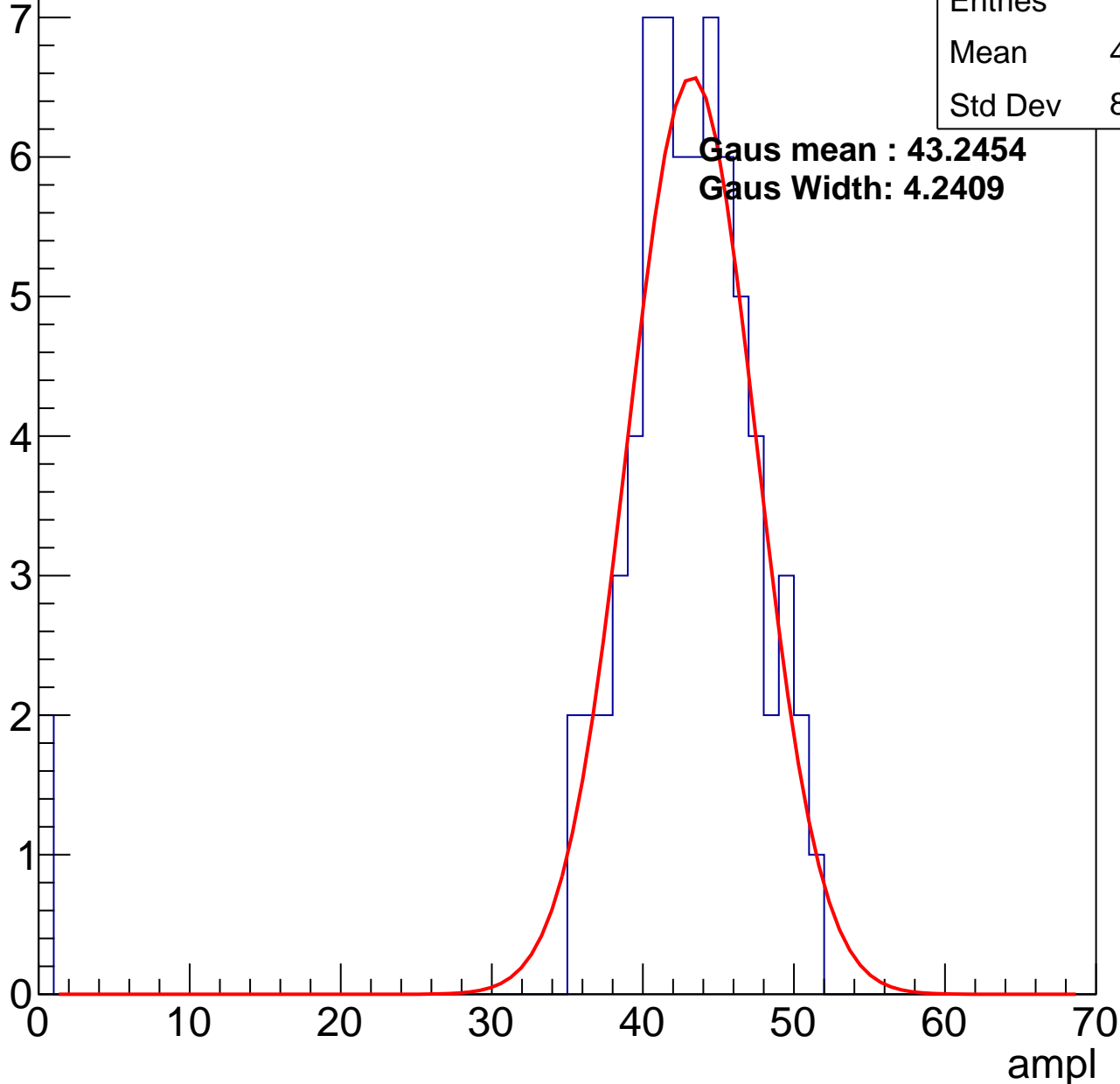
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	41.59
Std Dev	8.006

Gaus mean : 43.2454

Gaus Width: 4.2409

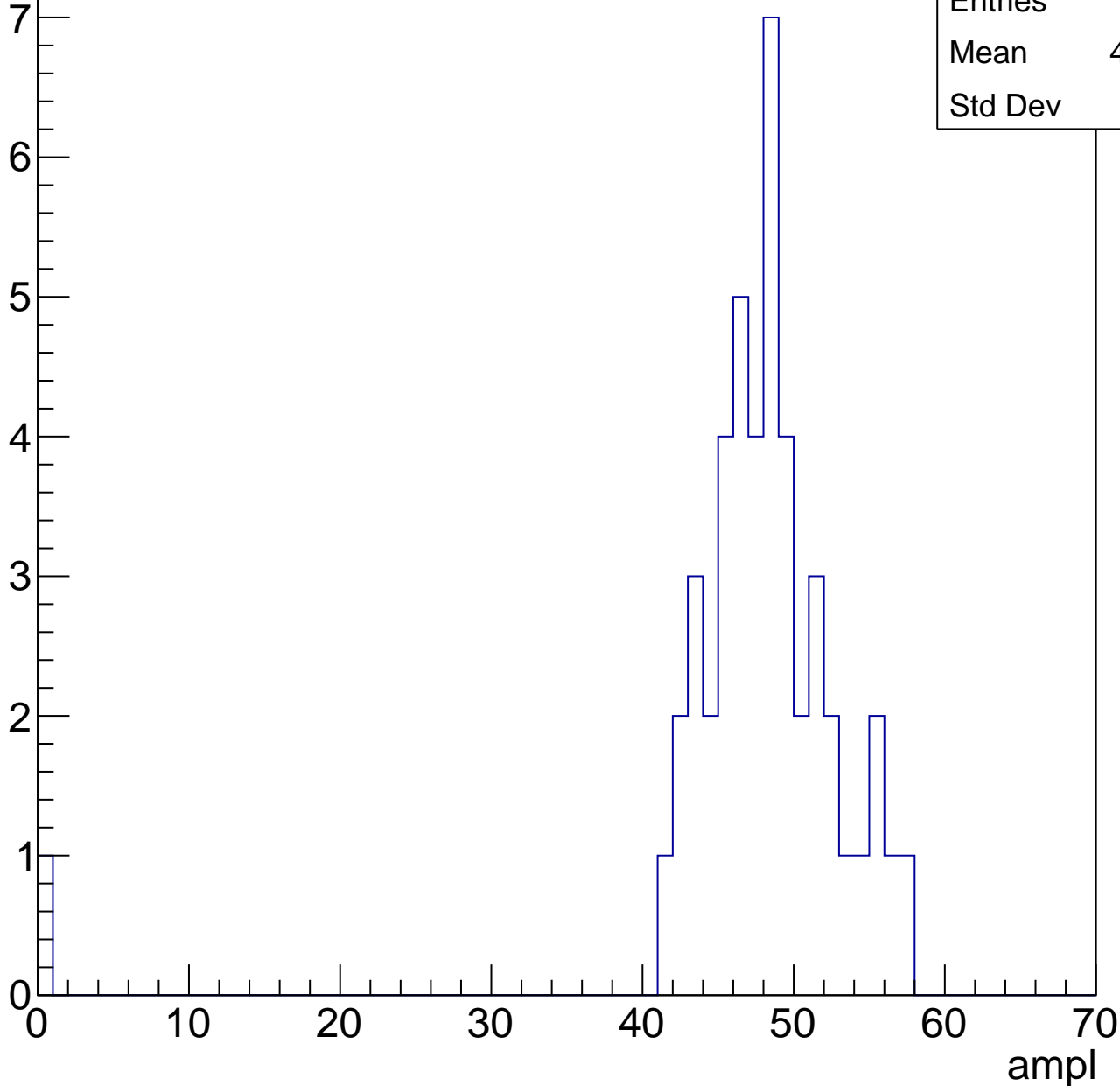


B1L103S, U21-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	46.93
Std Dev	7.96

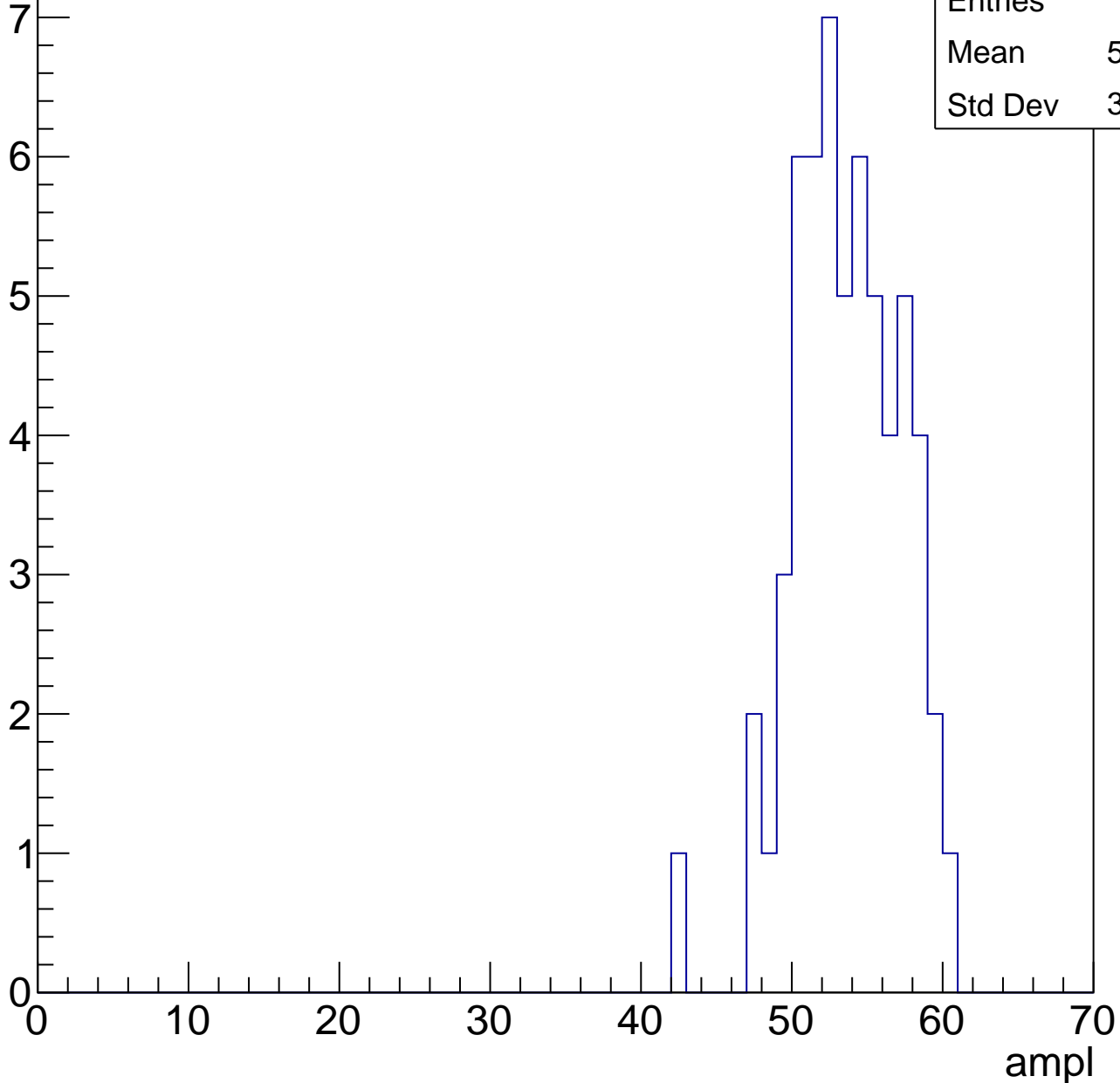


B1L103S, U21-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.17
Std Dev	3.504

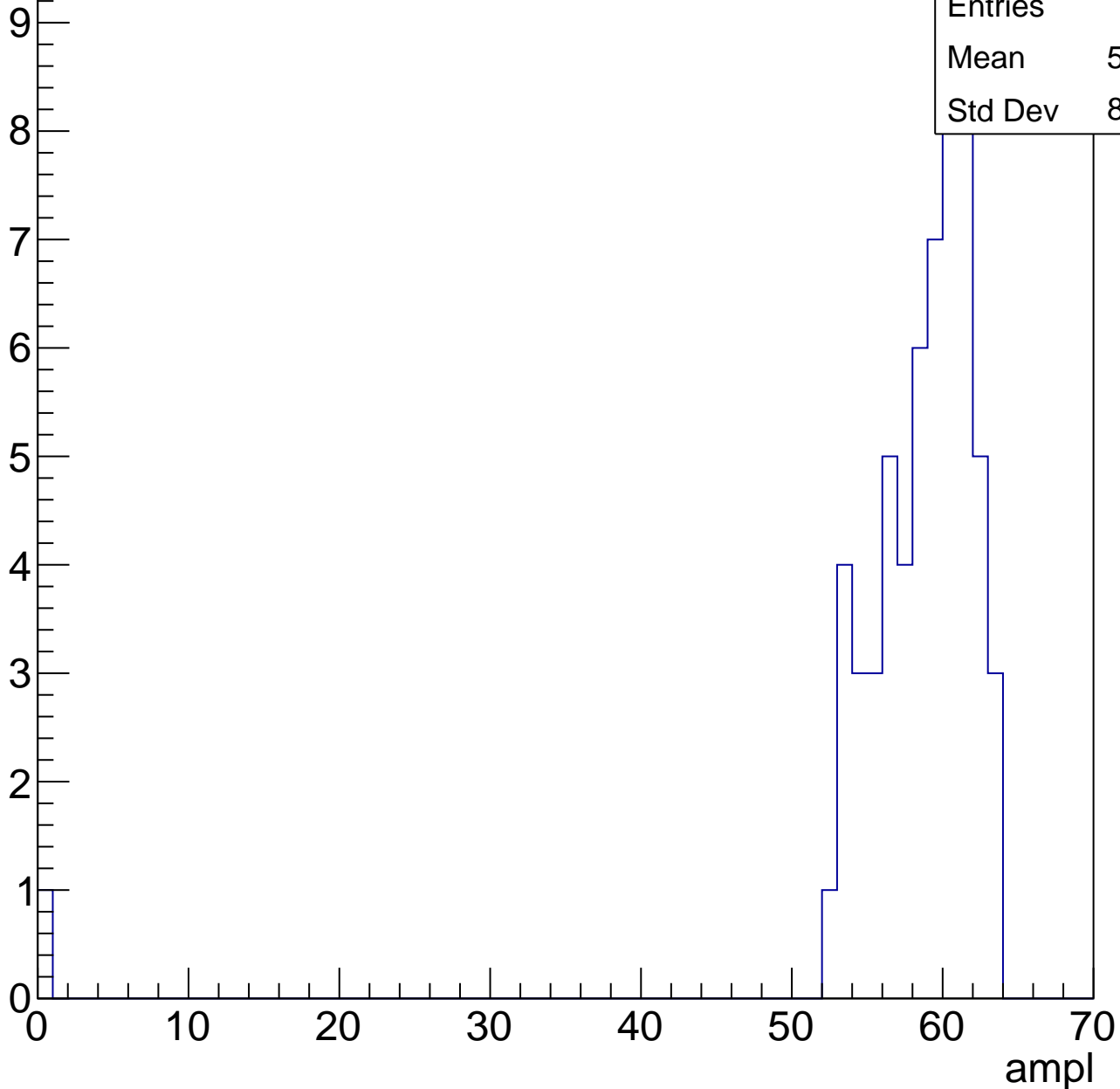


B1L103S, U21-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.42
Std Dev	8.083

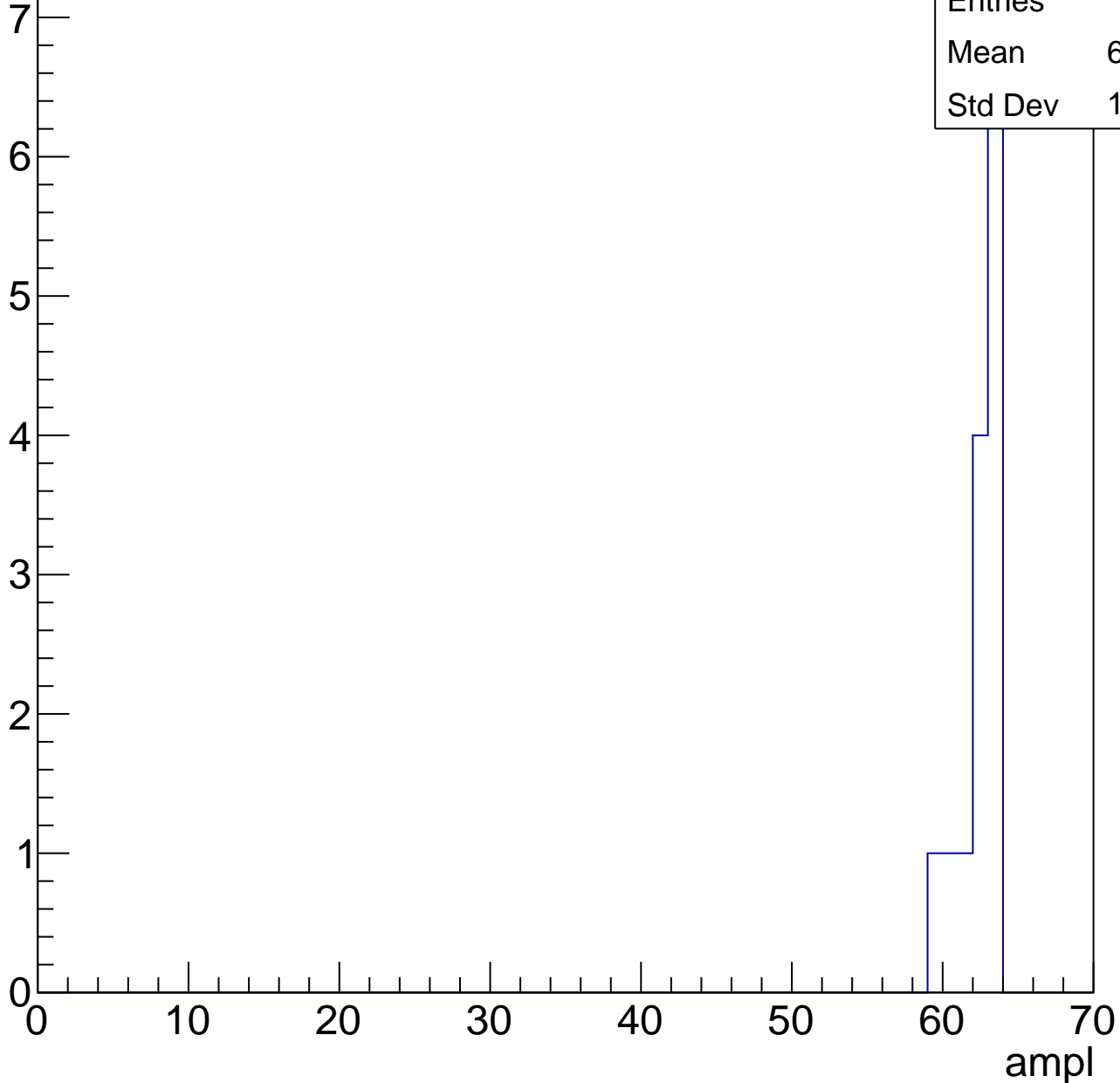


B1L103S, U21-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	62.07
Std Dev	1.223



B1L103S, U21-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

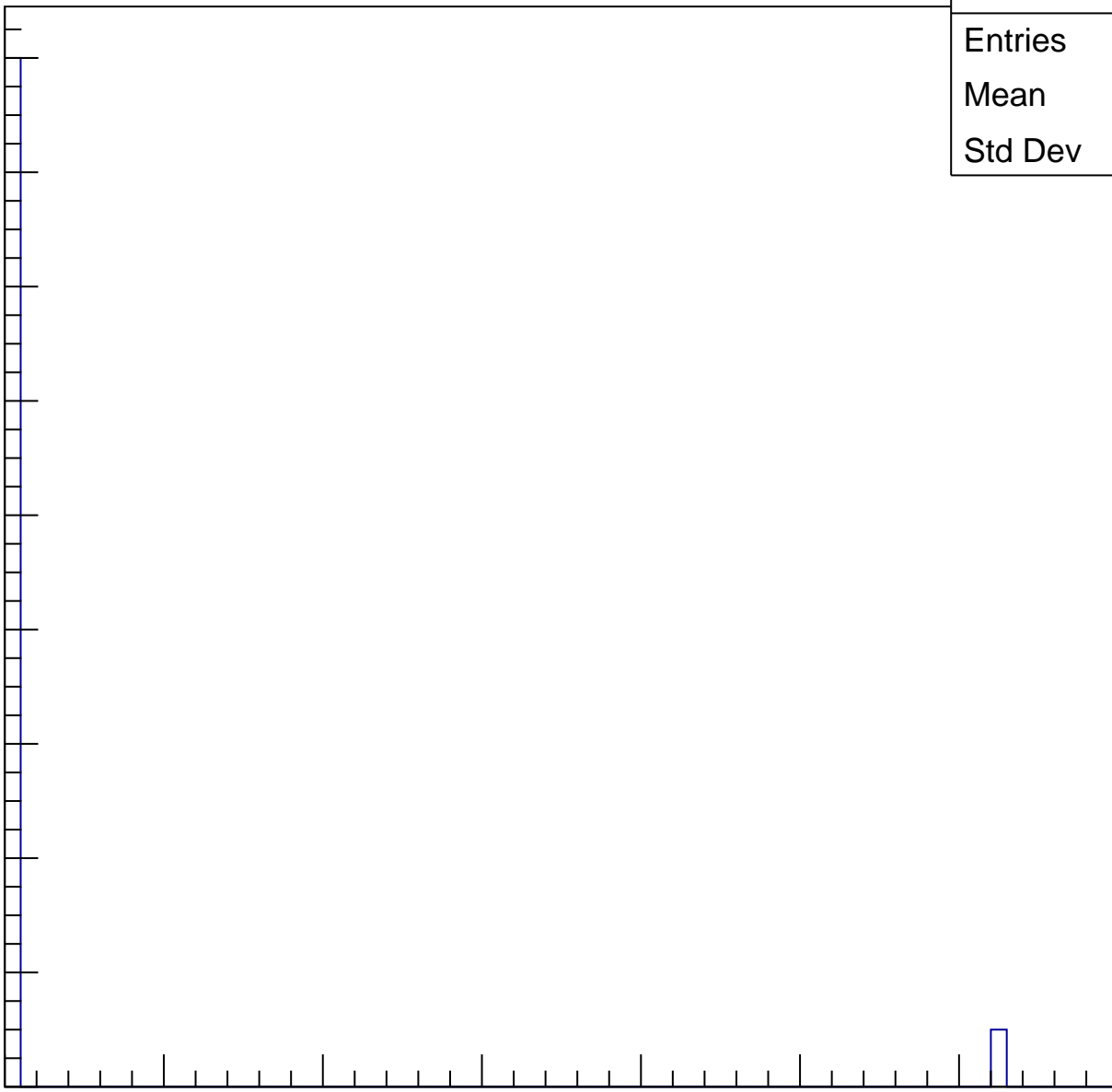
Entries	19
Mean	3.263
Std Dev	13.84

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch50, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	24.97
Std Dev	10.1

Gaus mean : 28.8611

Gaus Width: 4.3551

Entry

10

8

6

4

2

0

0

10

20

30

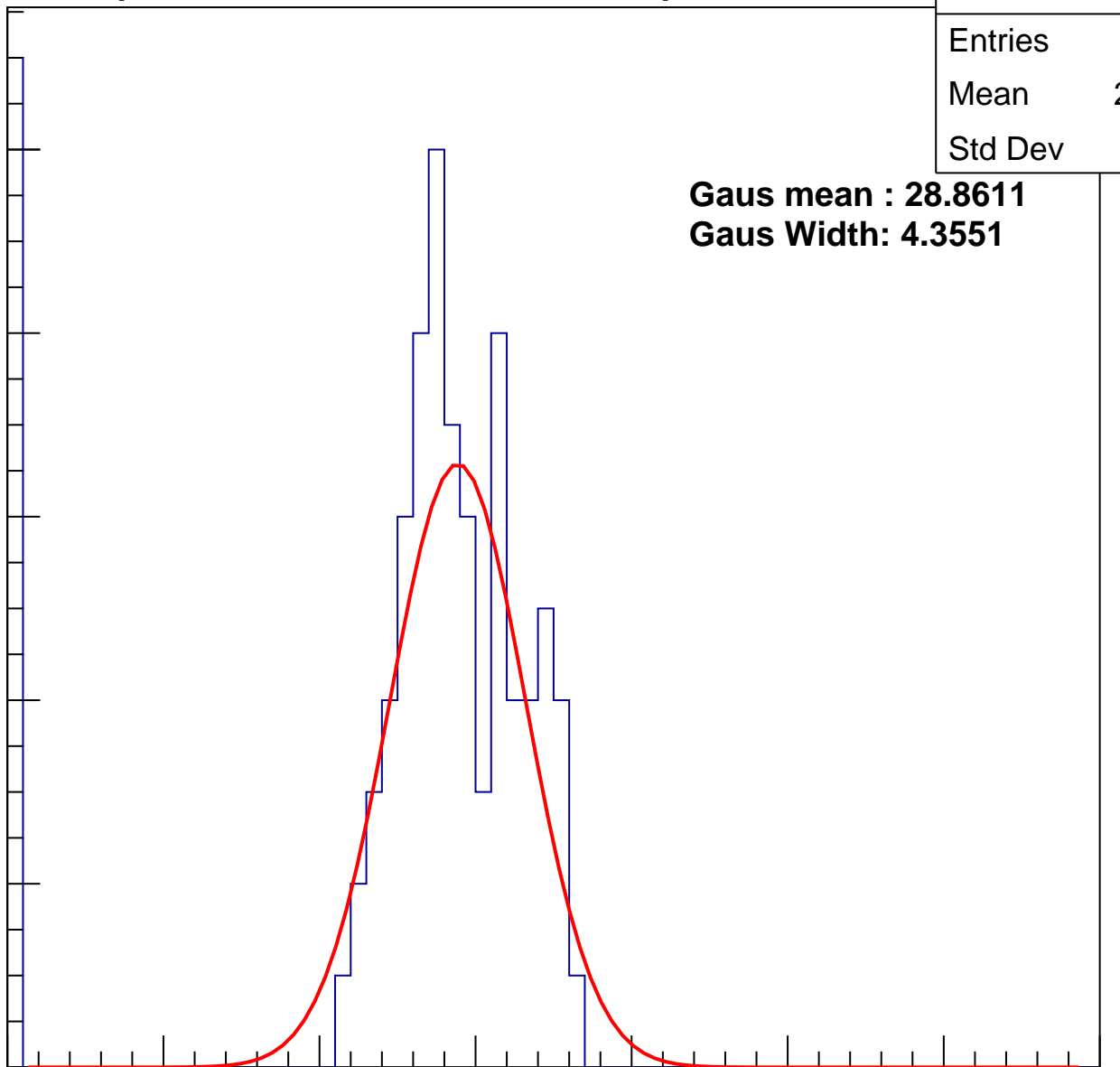
40

50

60

70

ampl



B1L103S, U21-ch50, adc1

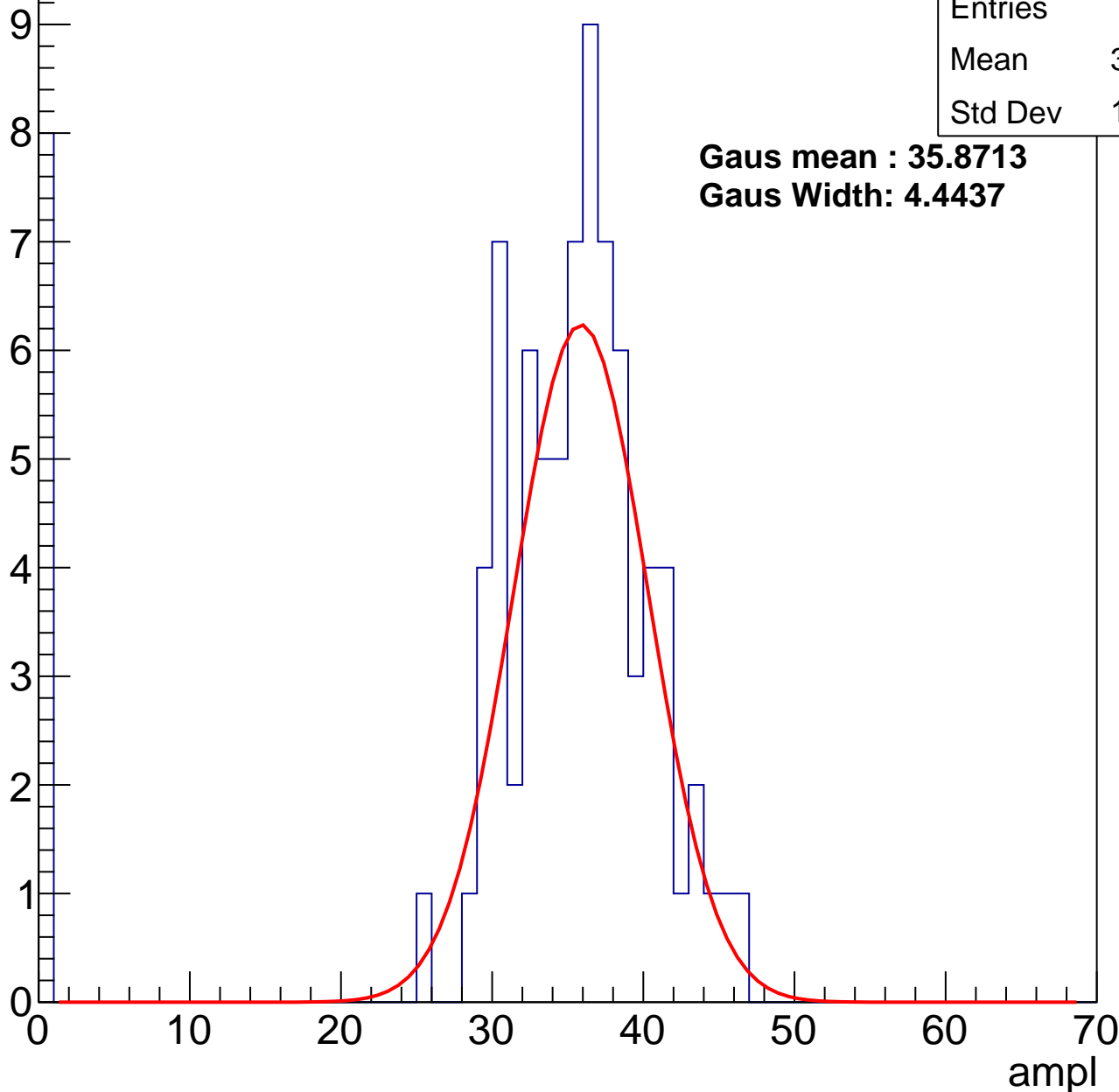
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	32.09
Std Dev	11.13

Gaus mean : 35.8713

Gaus Width: 4.4437



B1L103S, U21-ch50, adc2

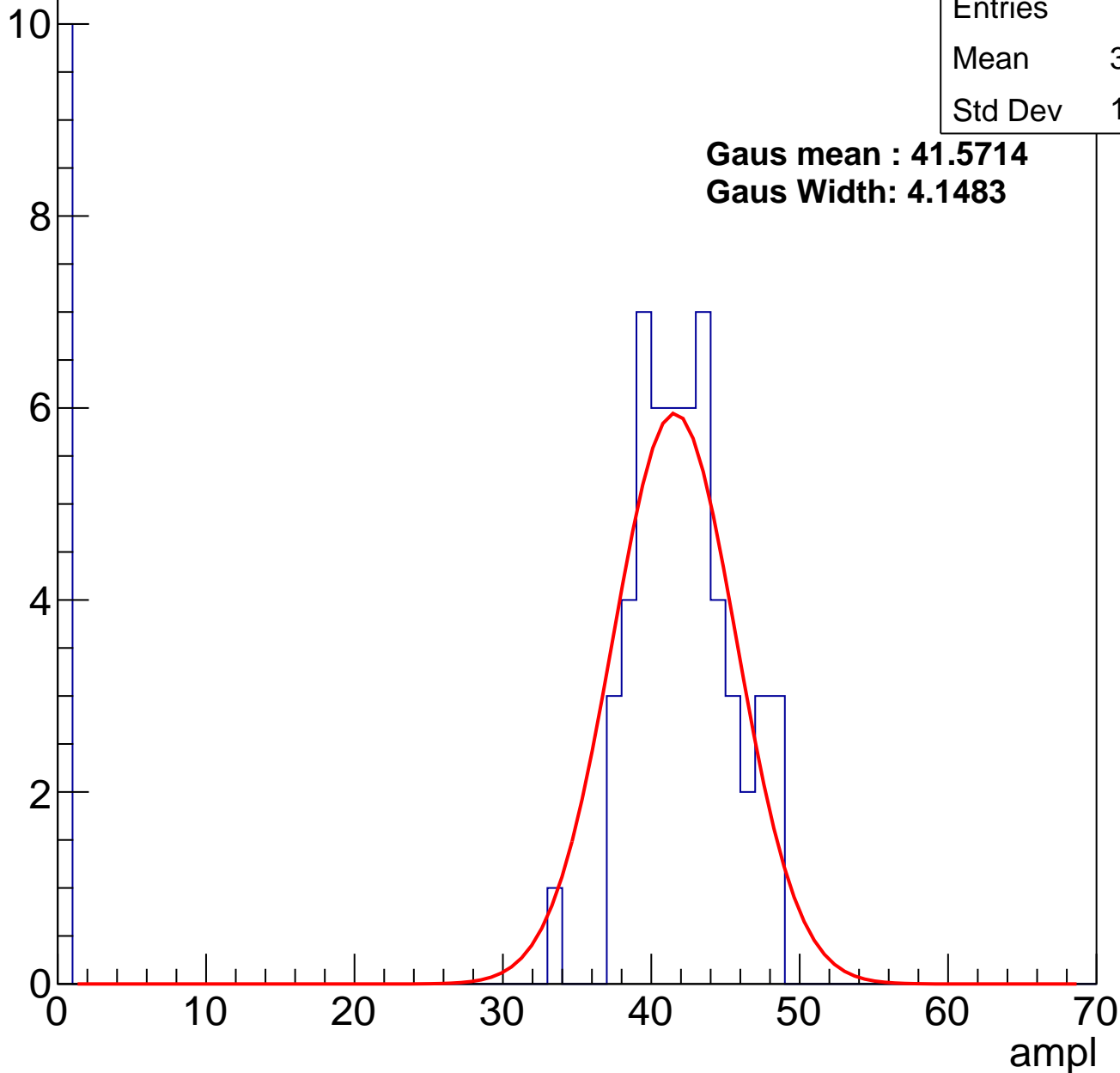
calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	35.32
Std Dev	15.35

Gaus mean : 41.5714

Gaus Width: 4.1483

Entry

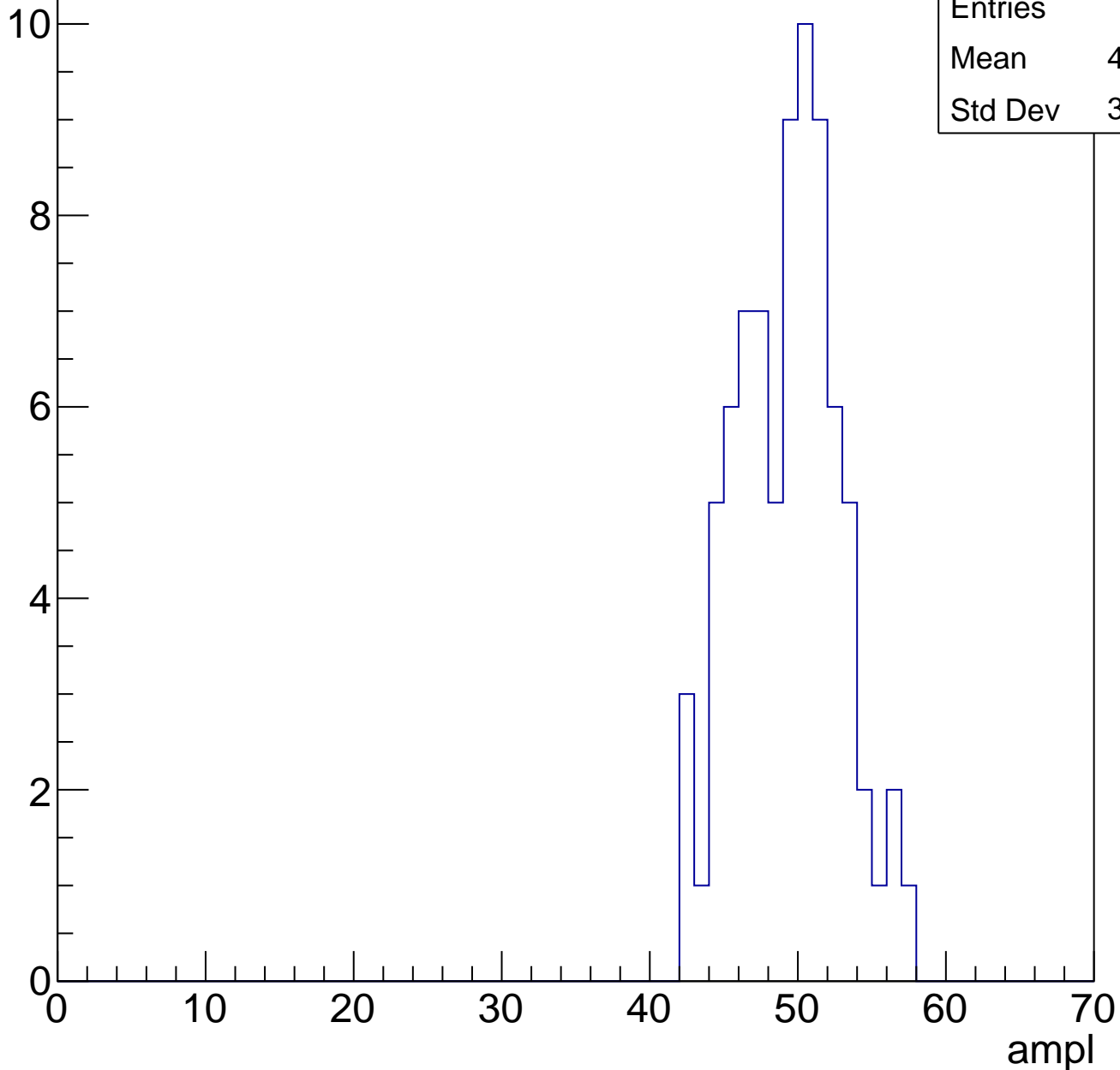


B1L103S, U21-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	48.85
Std Dev	3.424

Entry

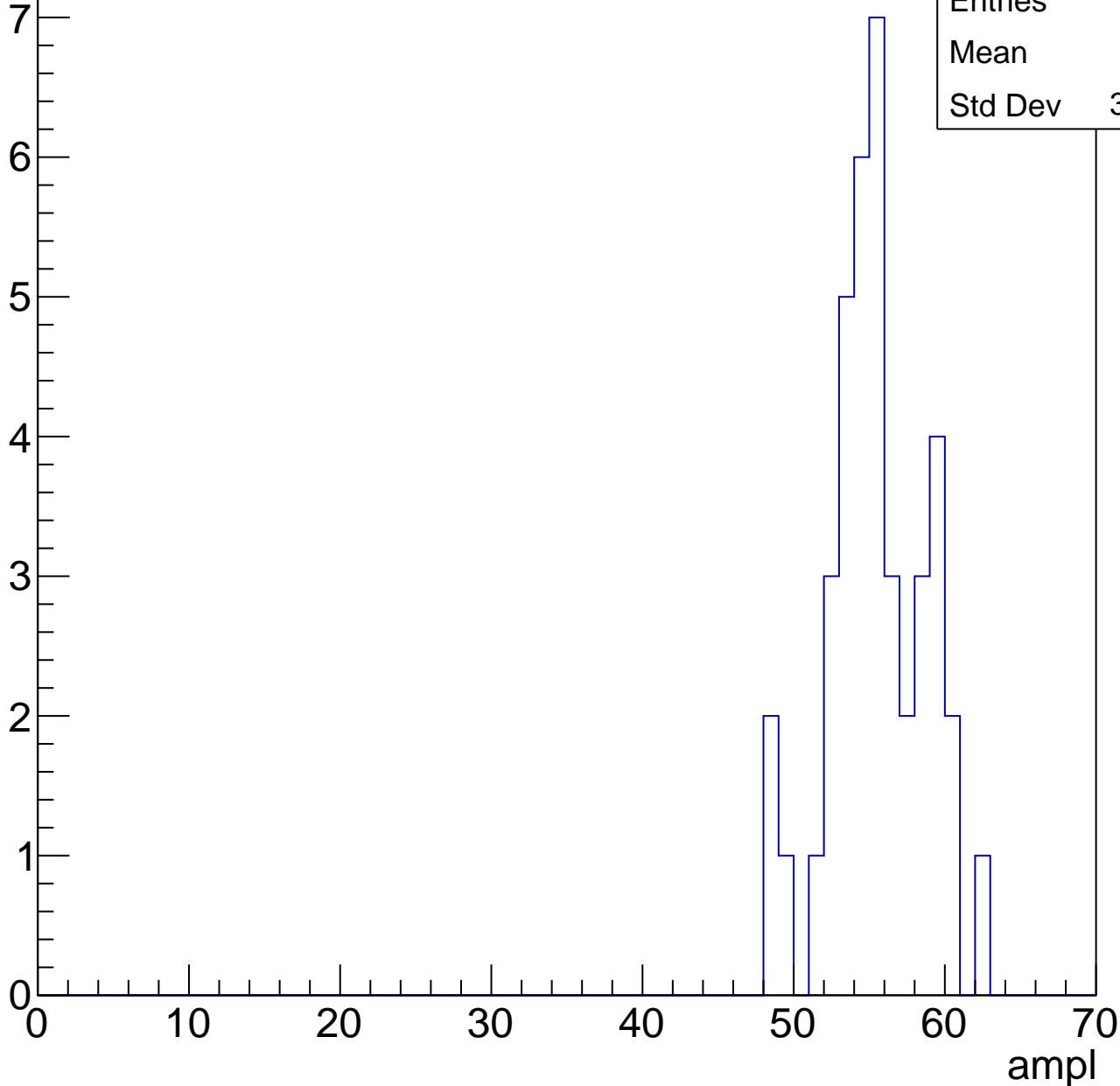


B1L103S, U21-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	55
Std Dev	3.178

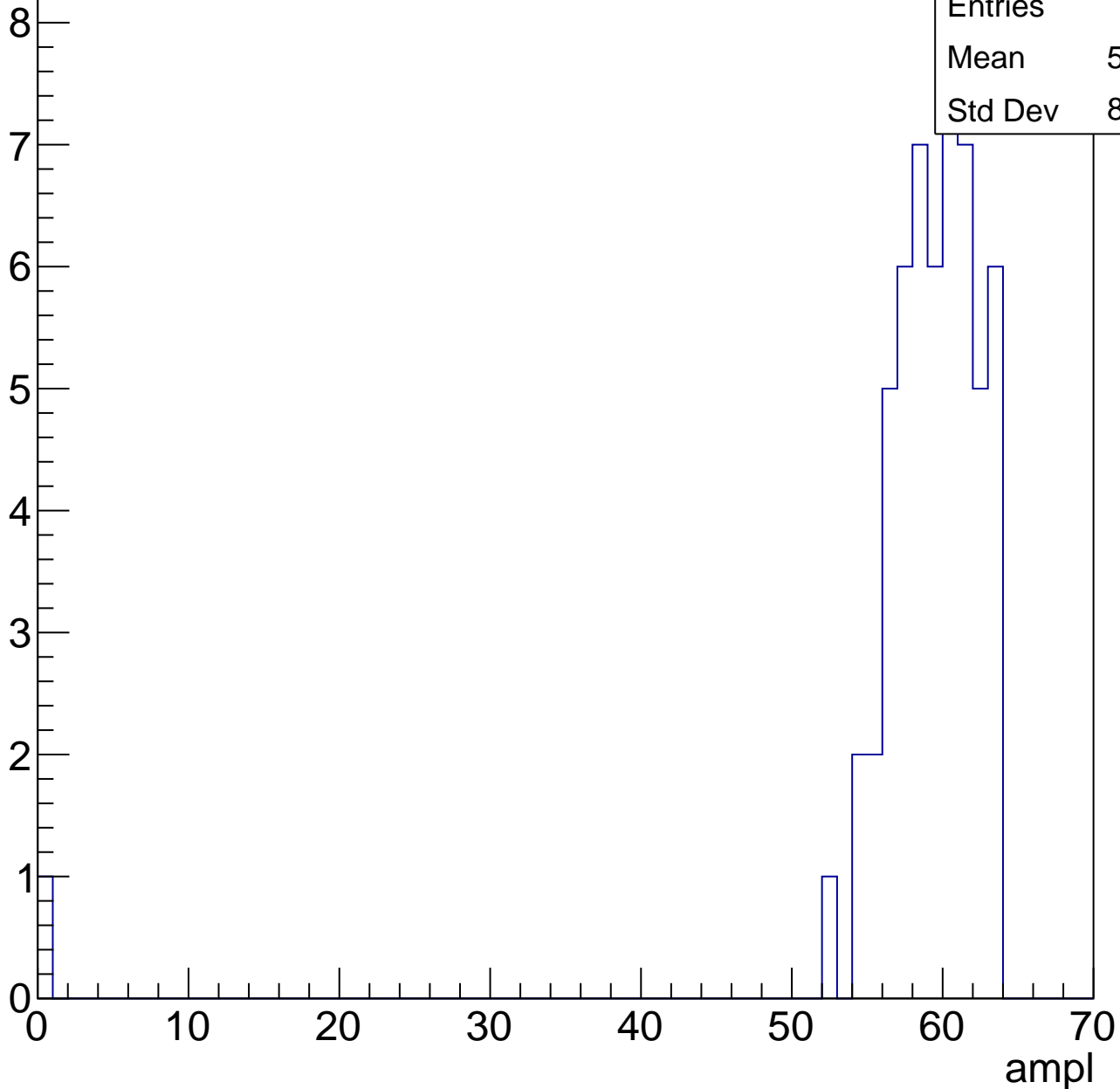


B1L103S, U21-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

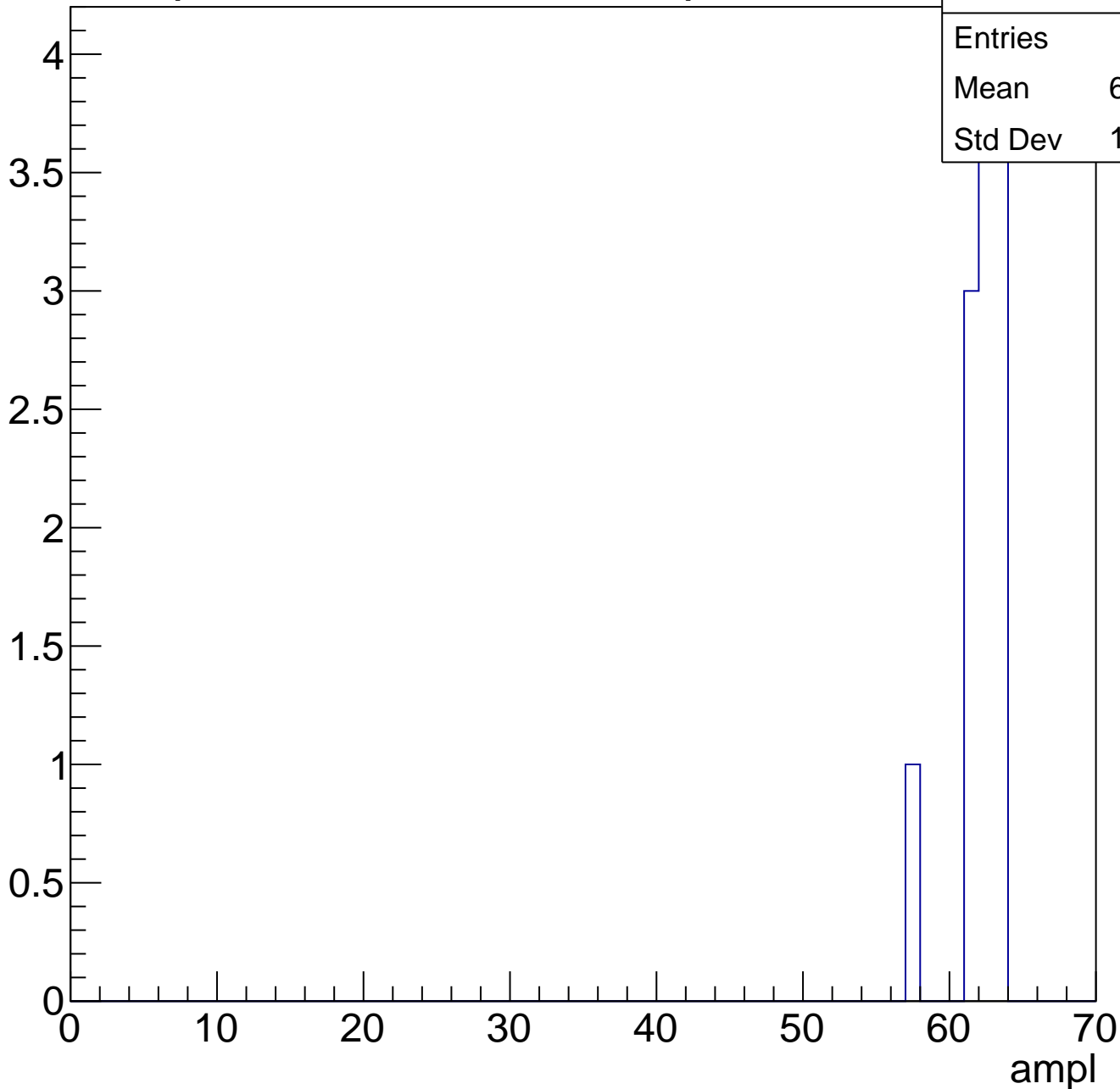
Entries	56
Mean	57.98
Std Dev	8.245



B1L103S, U21-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

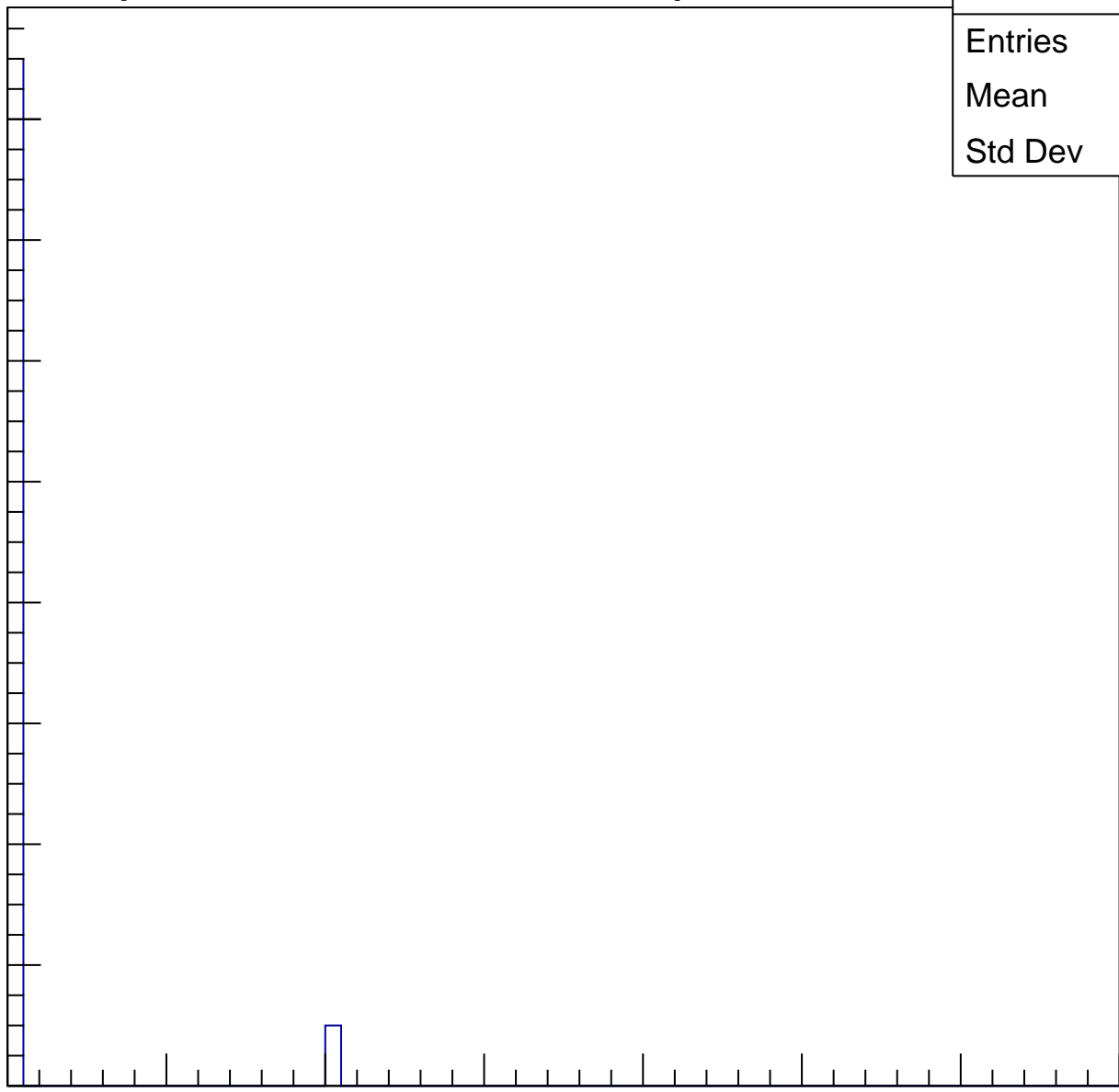
Entries	18
Mean	1.111
Std Dev	4.581

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch51, adc0

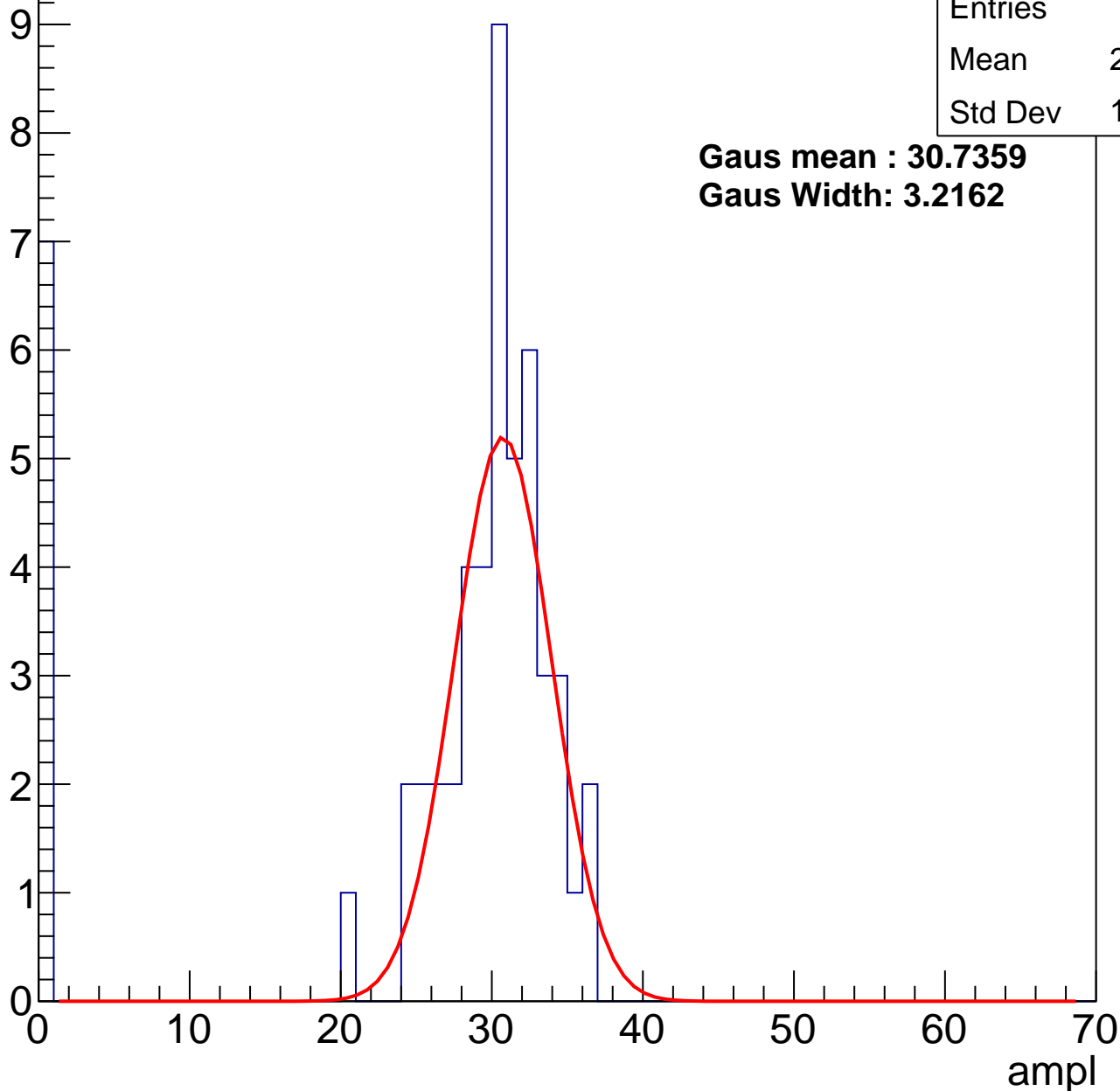
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	25.98
Std Dev	10.58

Gaus mean : 30.7359

Gaus Width: 3.2162



B1L103S, U21-ch51, adc1

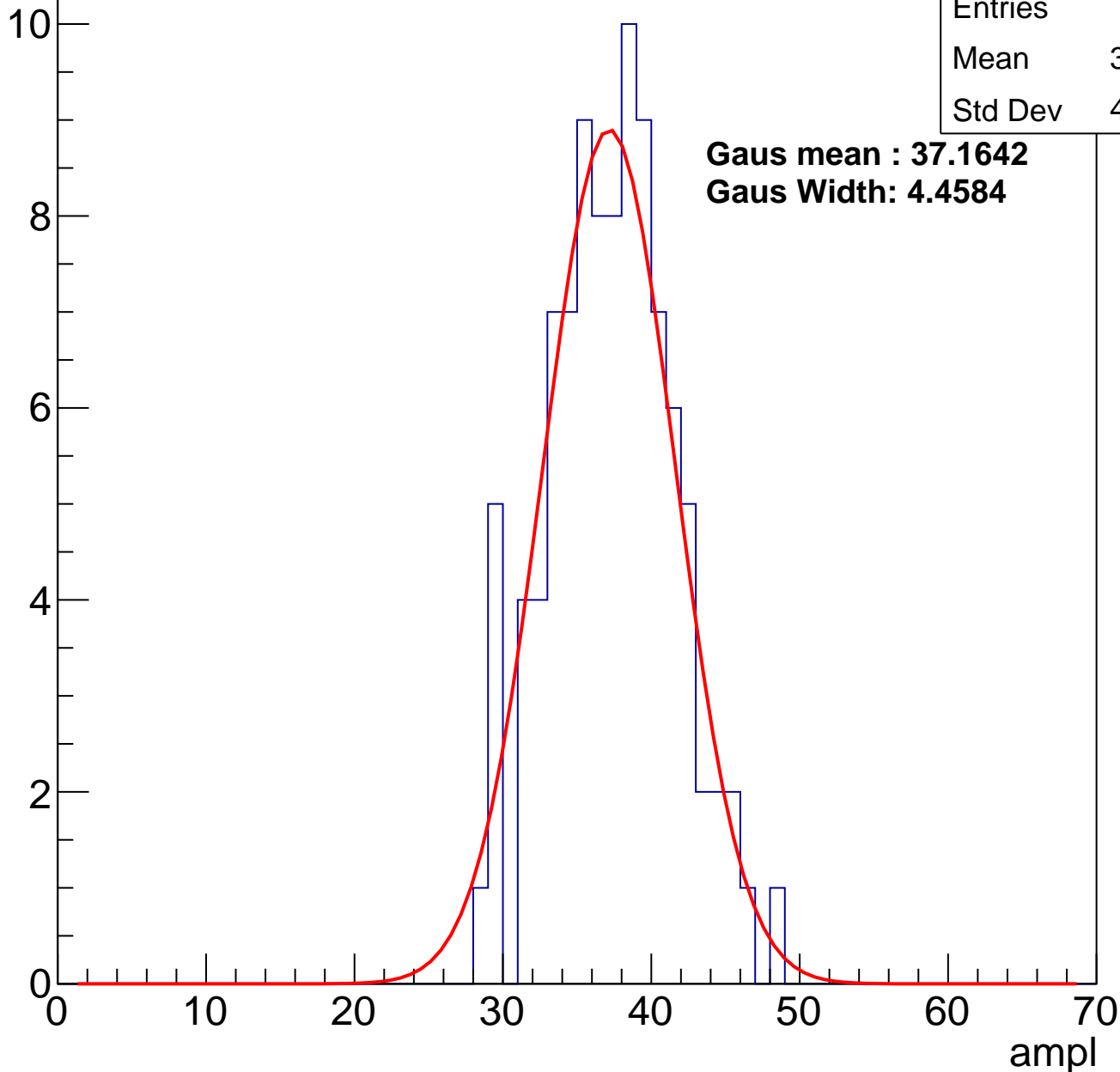
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	36.92
Std Dev	4.157

Gaus mean : 37.1642

Gaus Width: 4.4584

Entry



B1L103S, U21-ch51, adc2

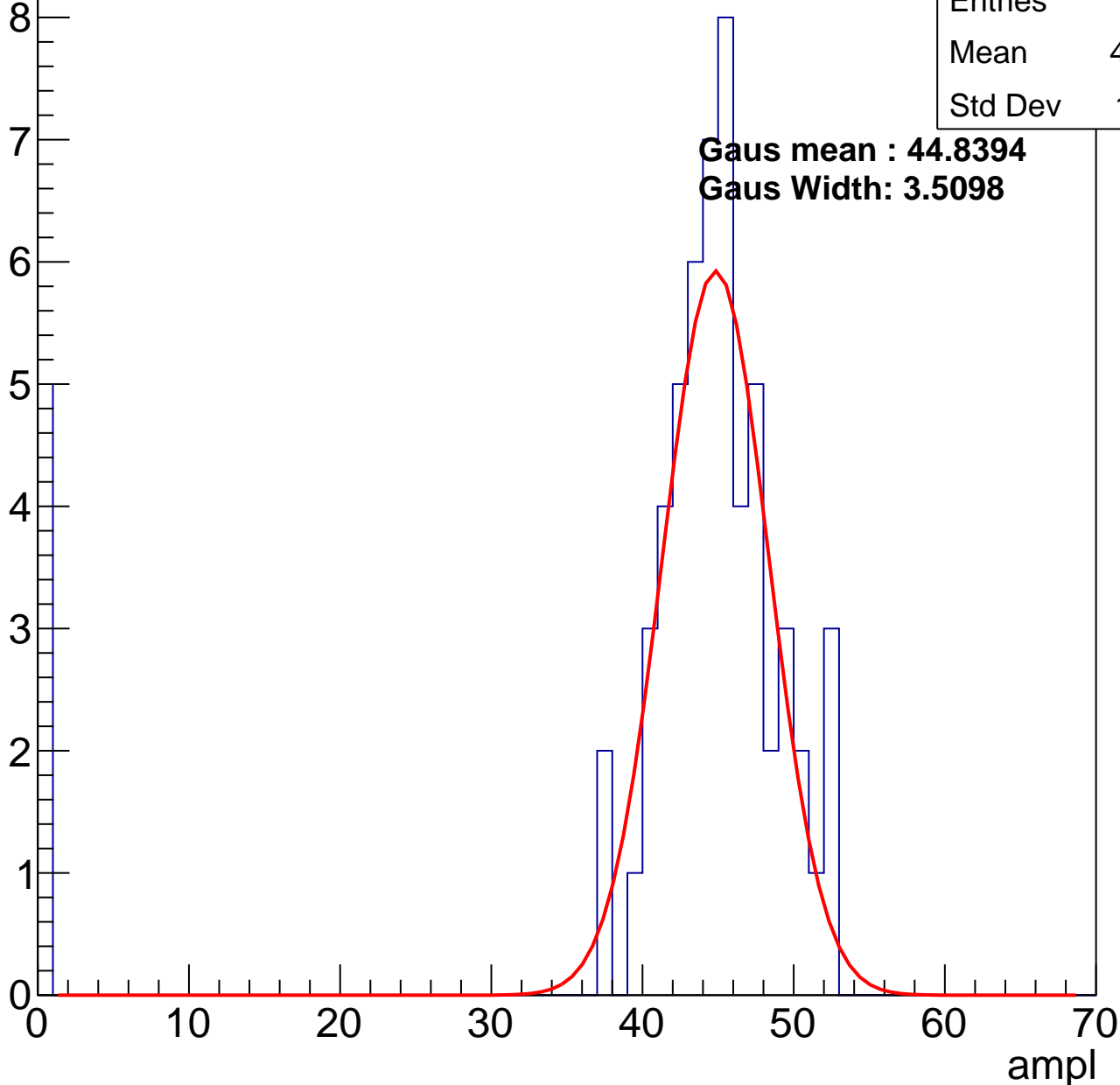
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	41.02
Std Dev	12.71

Gaus mean : 44.8394

Gaus Width: 3.5098

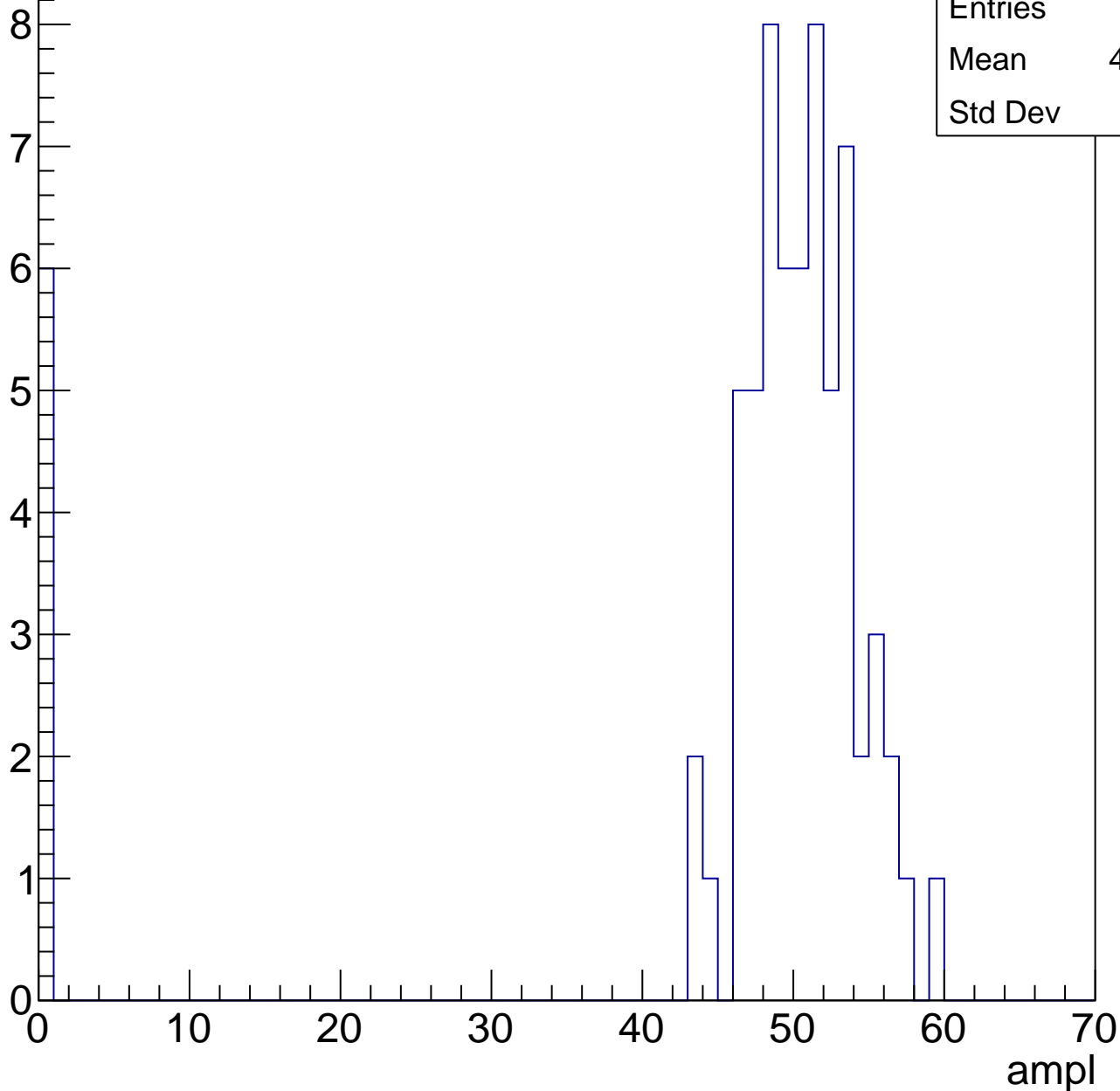


B1L103S, U21-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	45.78
Std Dev	14.6

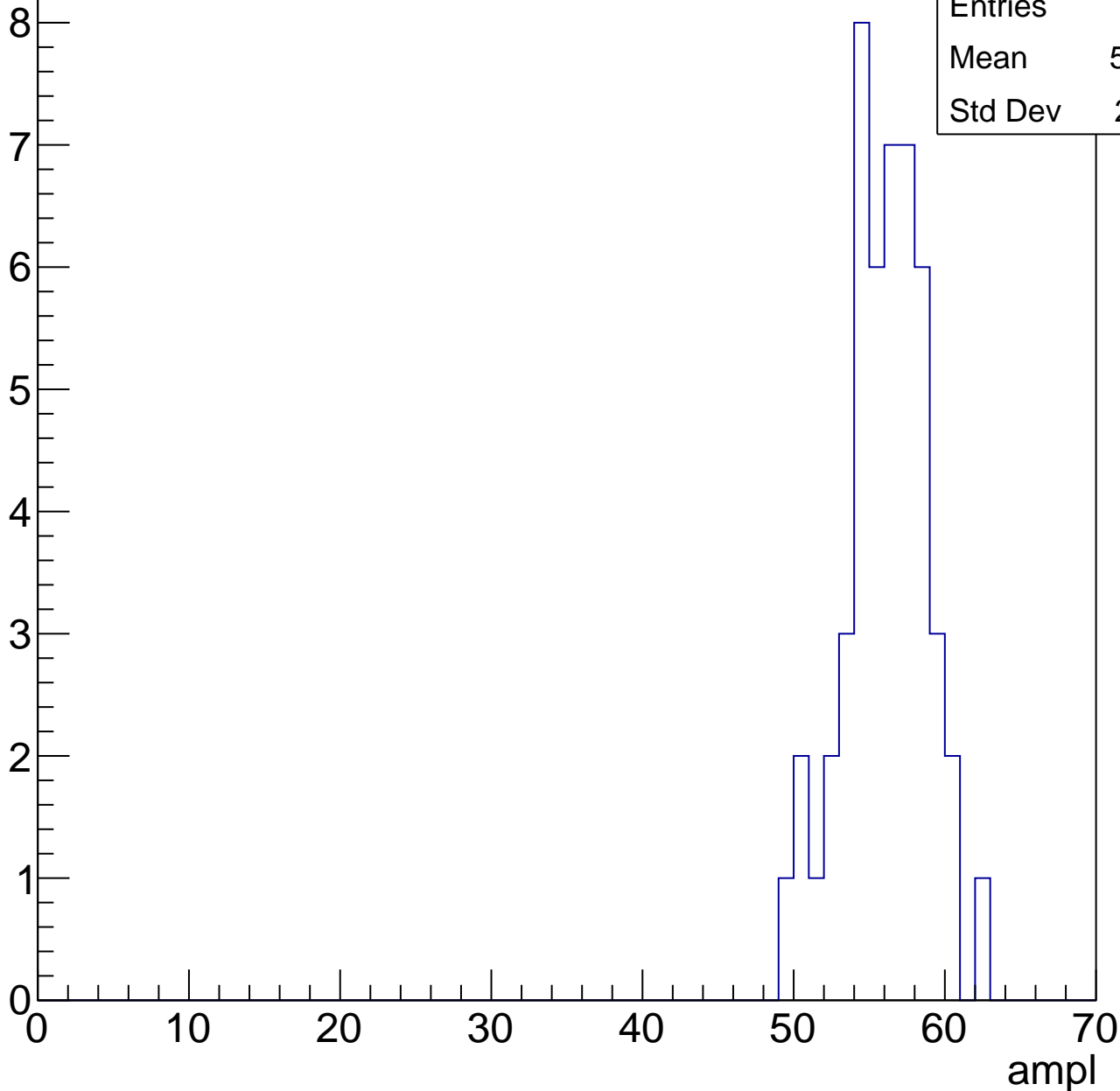


B1L103S, U21-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

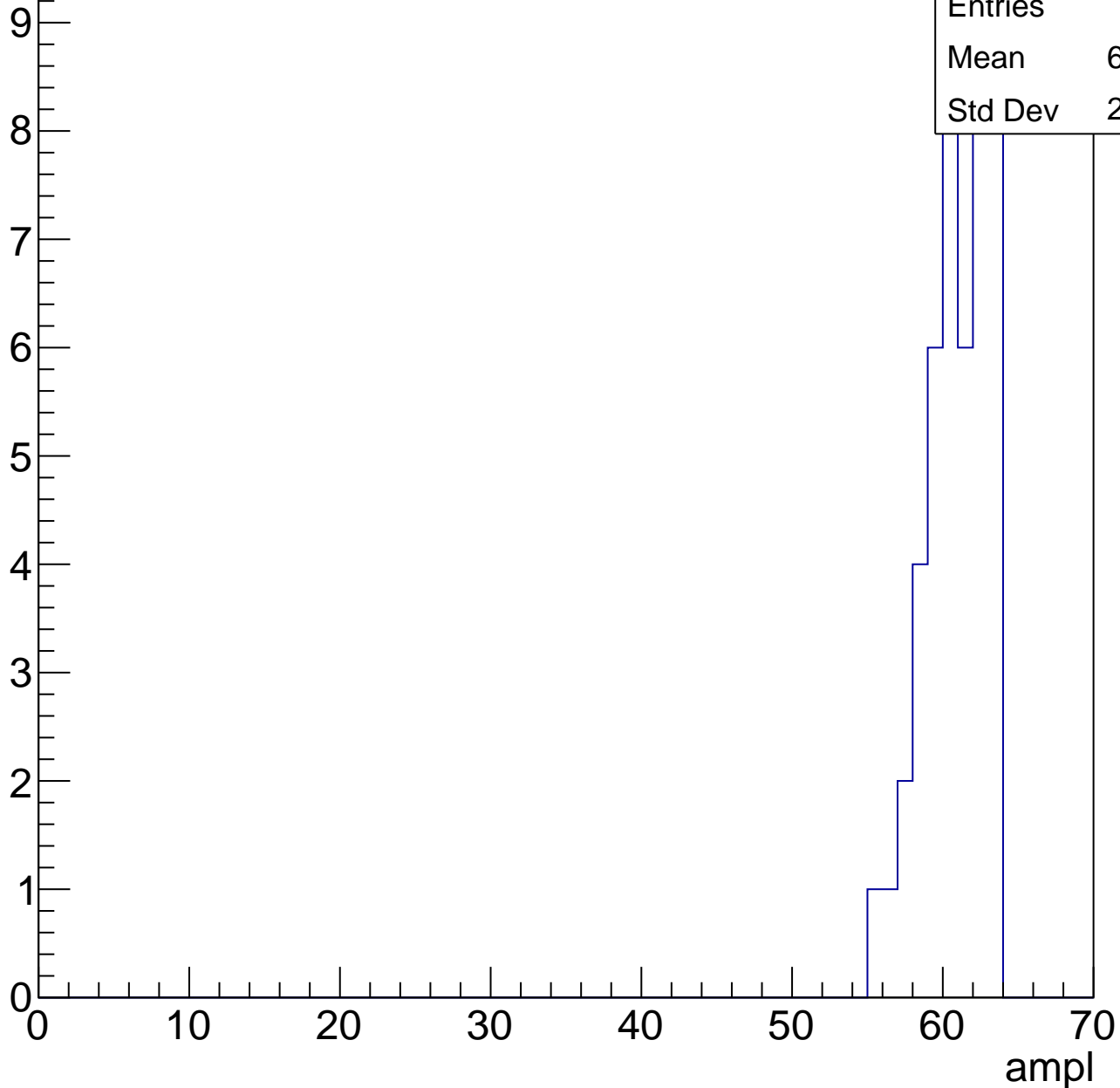
Entries	49
Mean	55.57
Std Dev	2.711



B1L103S, U21-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

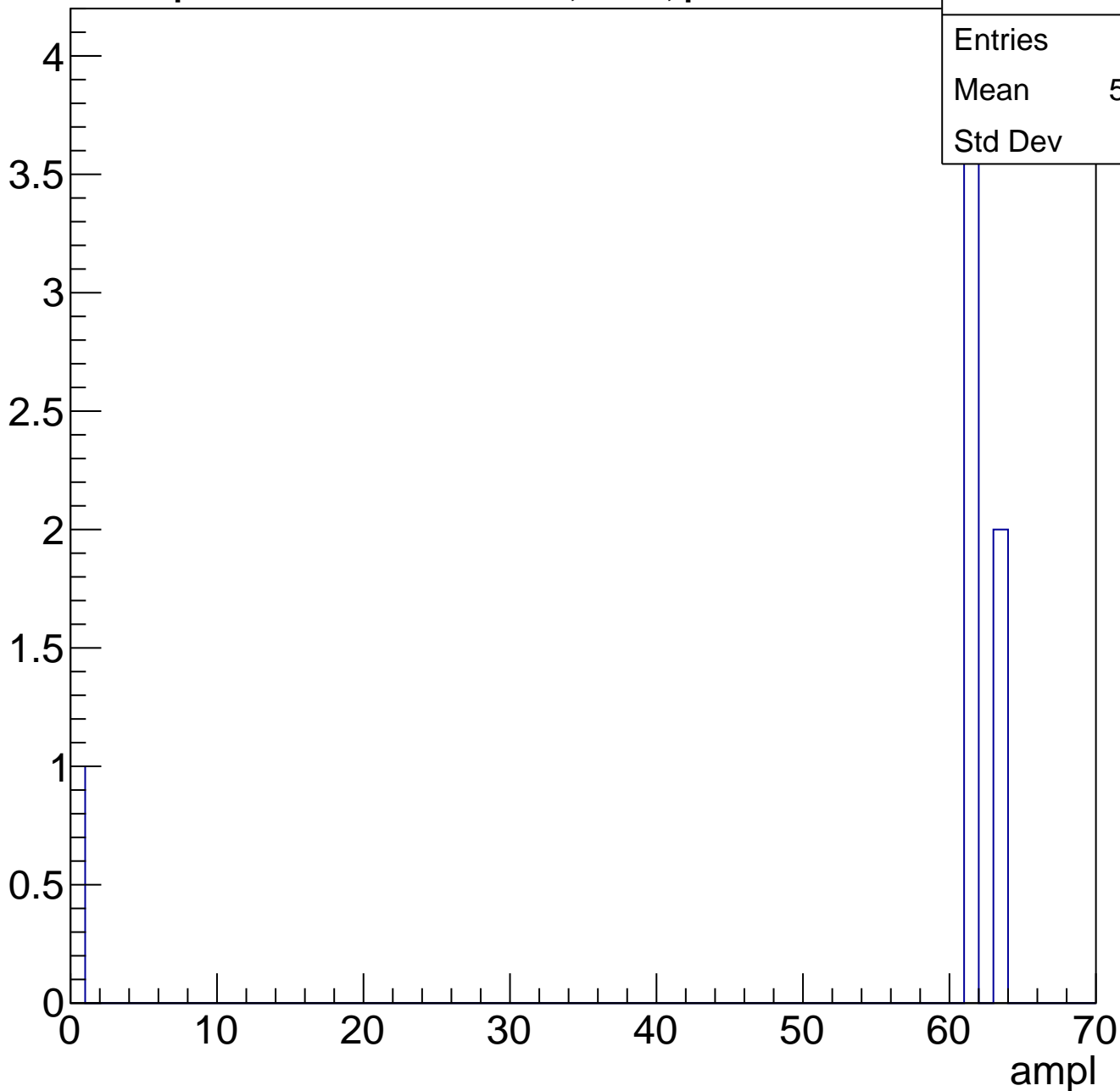
Entry



B1L103S, U21-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

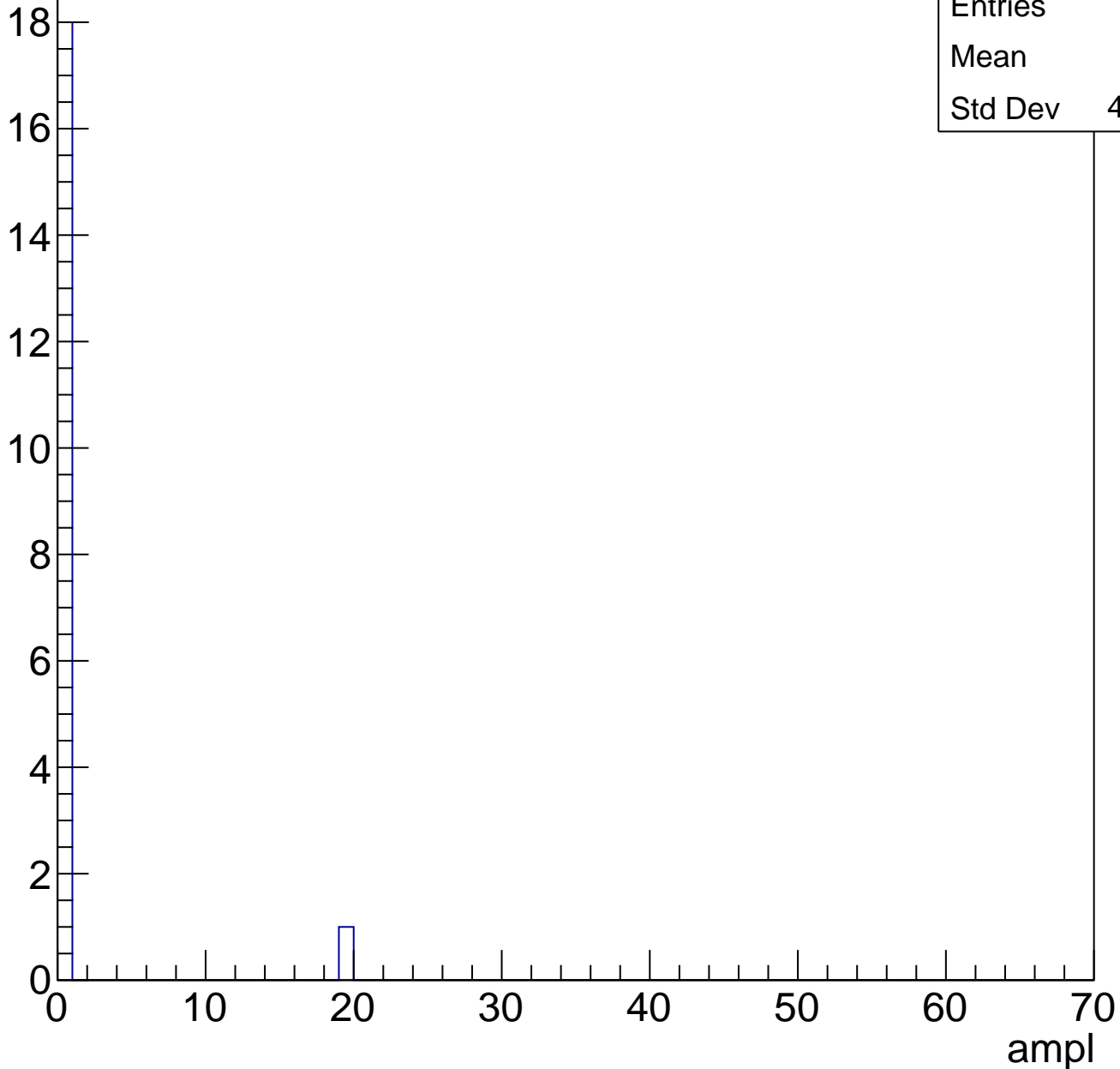


B1L103S, U21-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1
Std Dev	4.243

Entry



B1L103S, U21-ch52, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	24.55
Std Dev	10.18

Gaus mean : 28.4902

Gaus Width: 4.5670

Entry

10

8

6

4

2

0

0

10

20

30

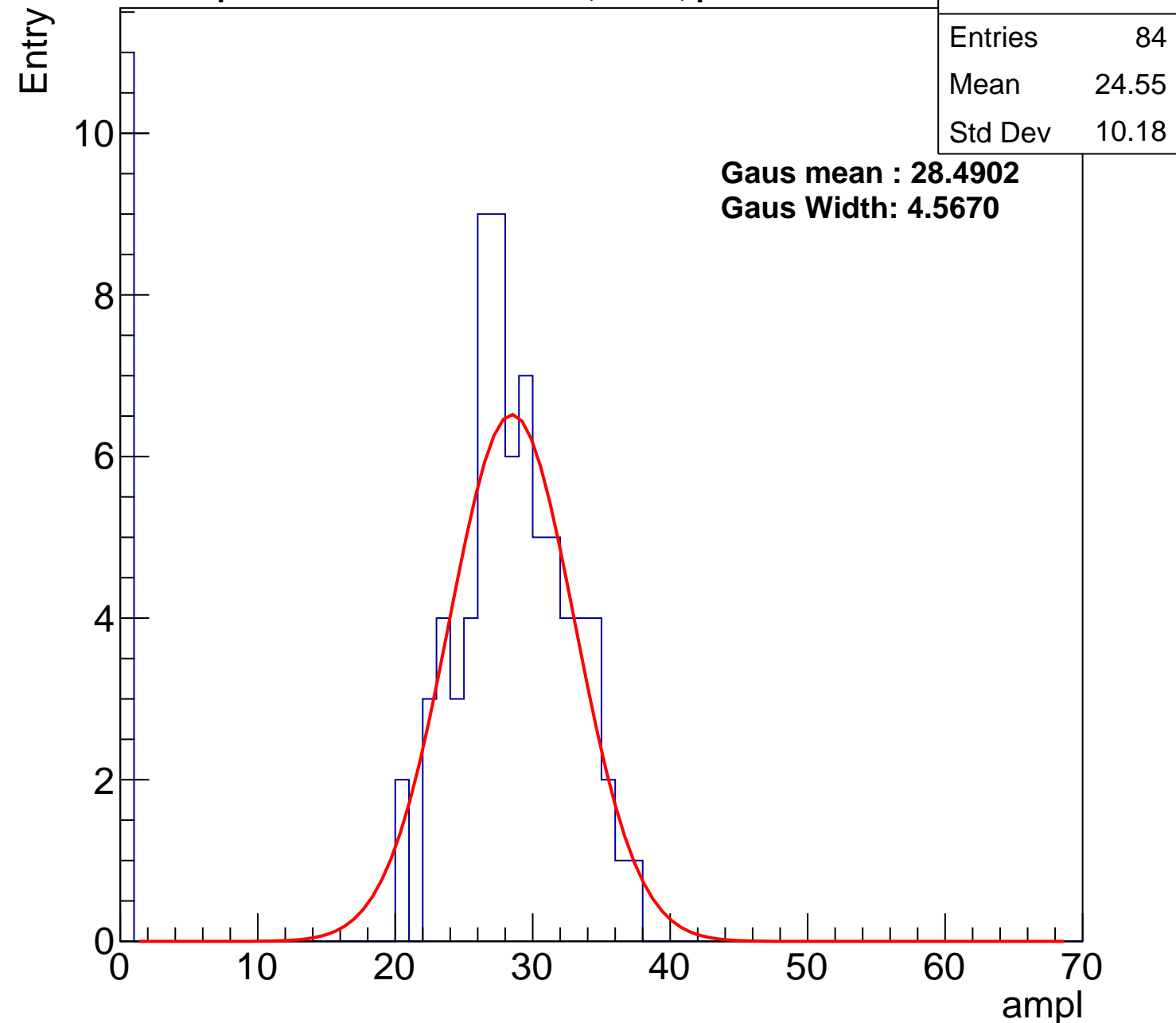
40

50

60

70

ampl



B1L103S, U21-ch52, adc1

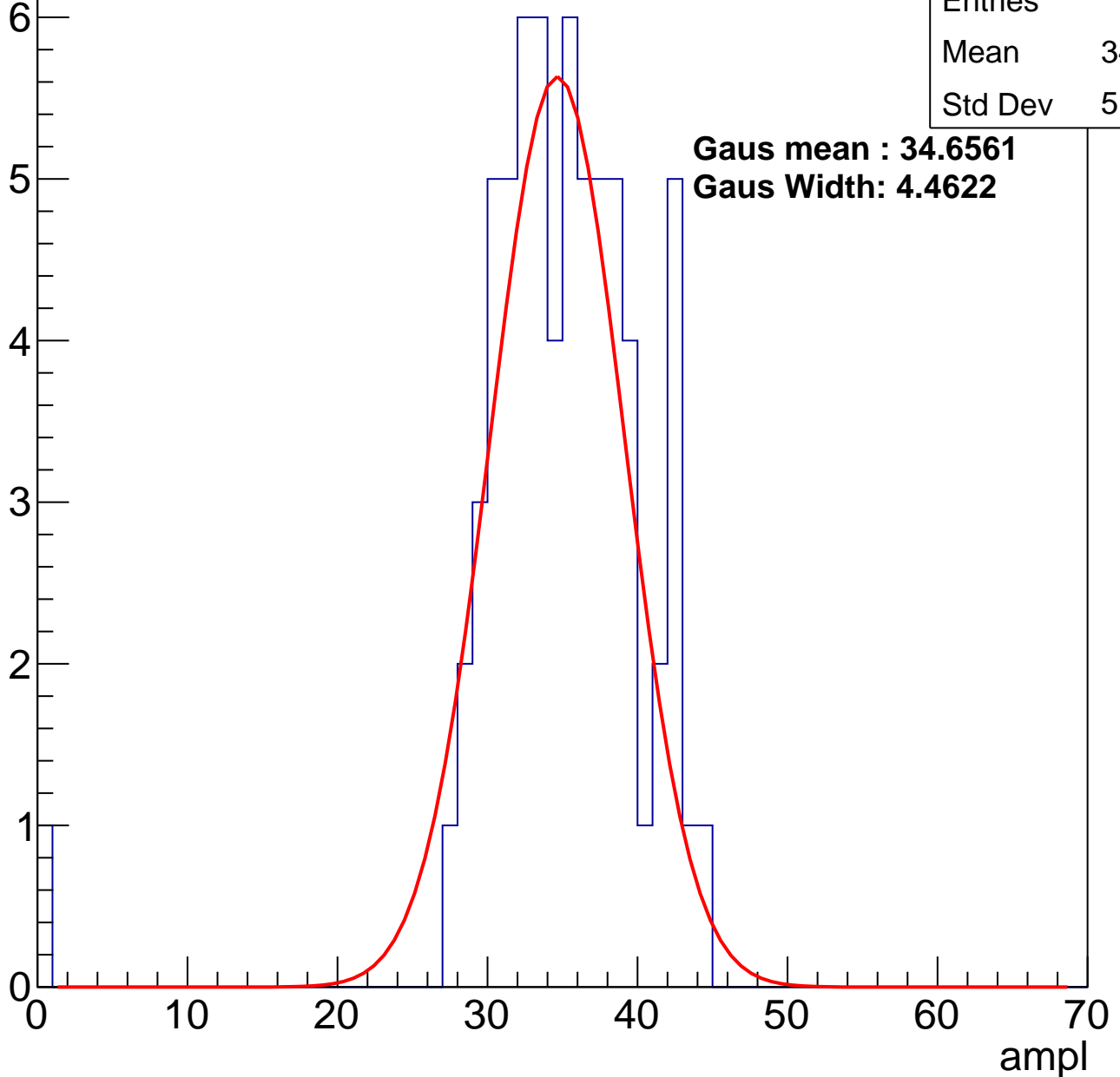
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.43
Std Dev	5.917

Gaus mean : 34.6561

Gaus Width: 4.4622



B1L103S, U21-ch52, adc2

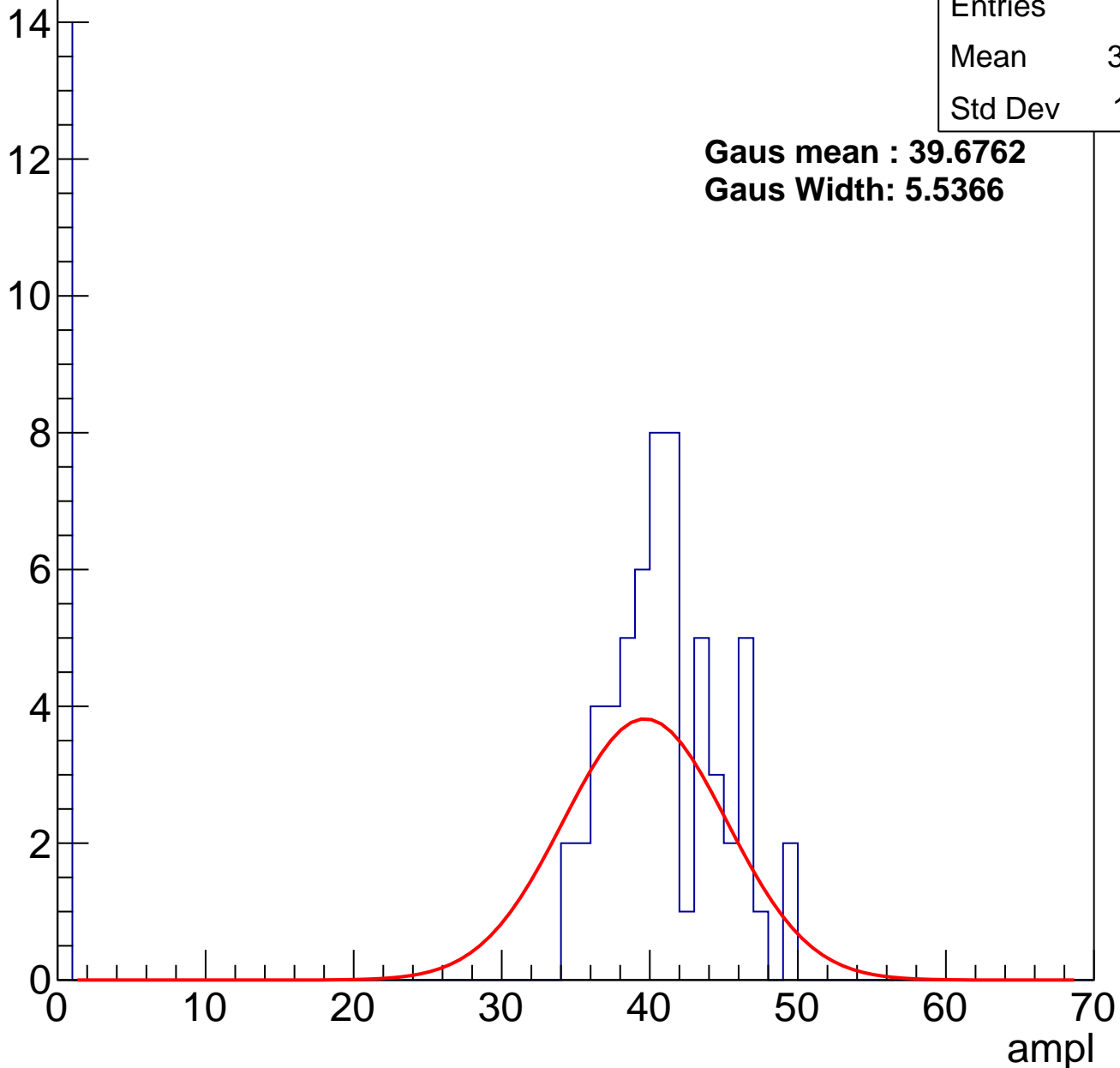
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	32.72
Std Dev	16.41

Gaus mean : 39.6762

Gaus Width: 5.5366

Entry

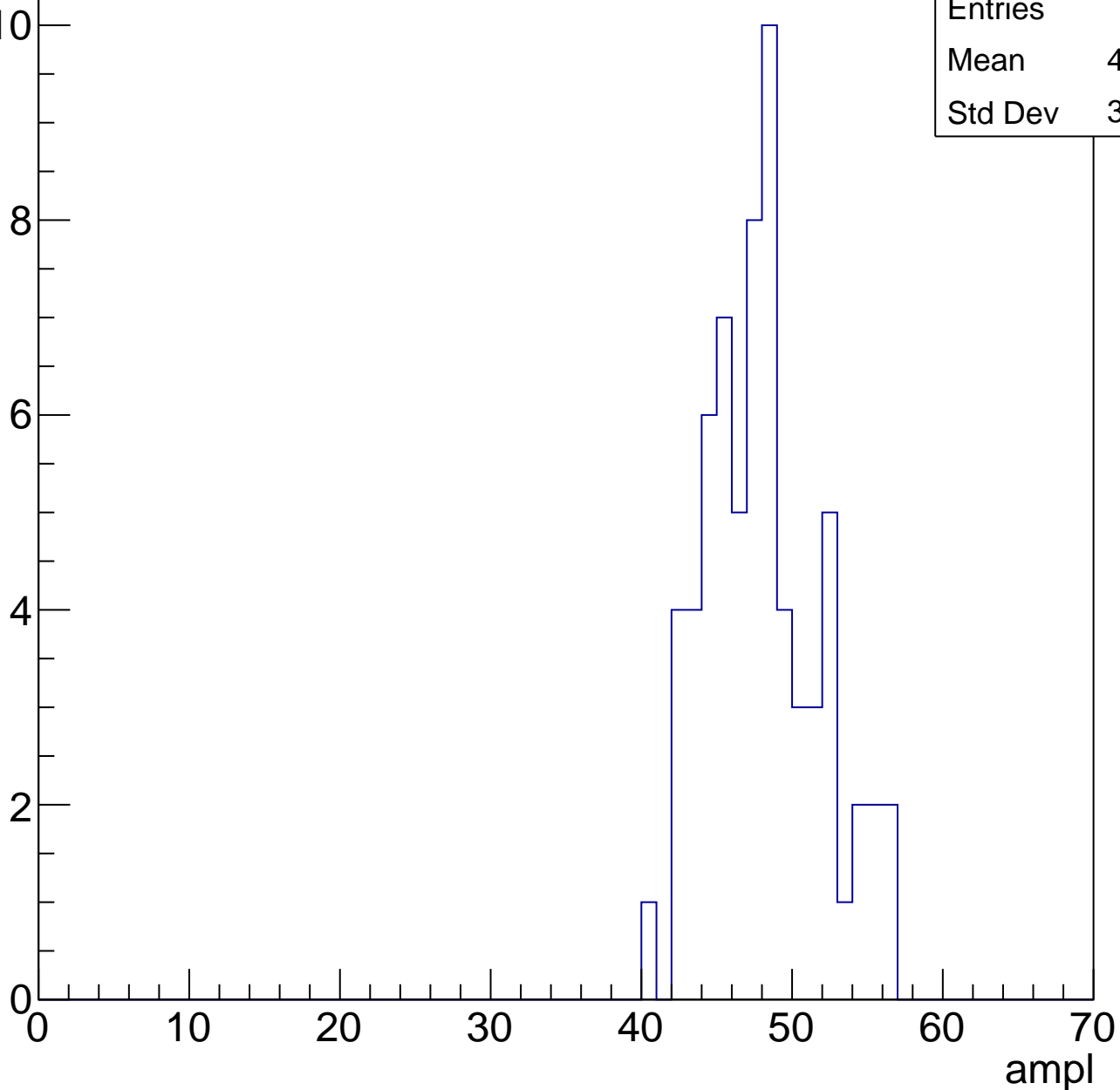


B1L103S, U21-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.57
Std Dev	3.723

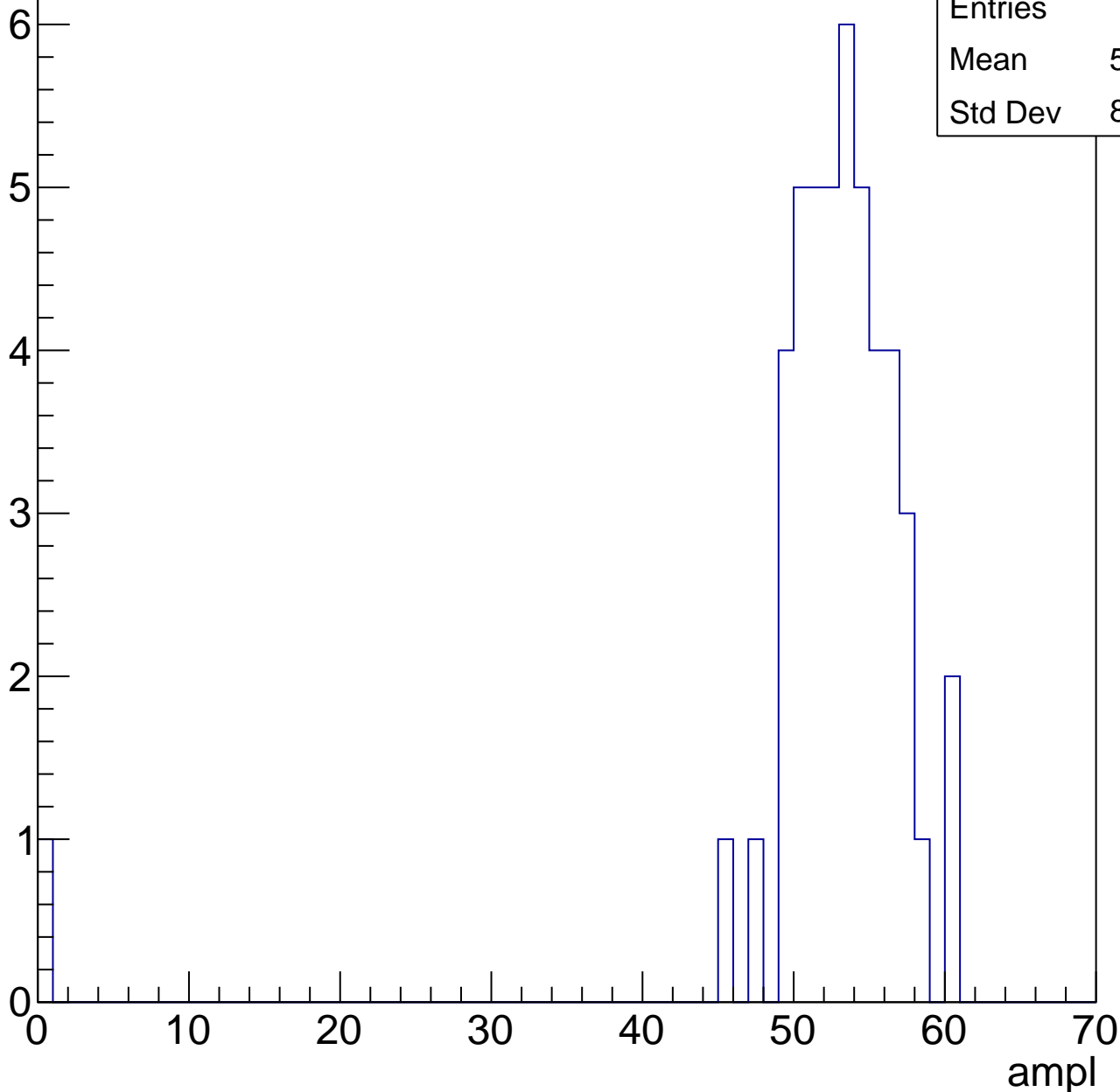


B1L103S, U21-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	51.79
Std Dev	8.254



B1L103S, U21-ch52, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

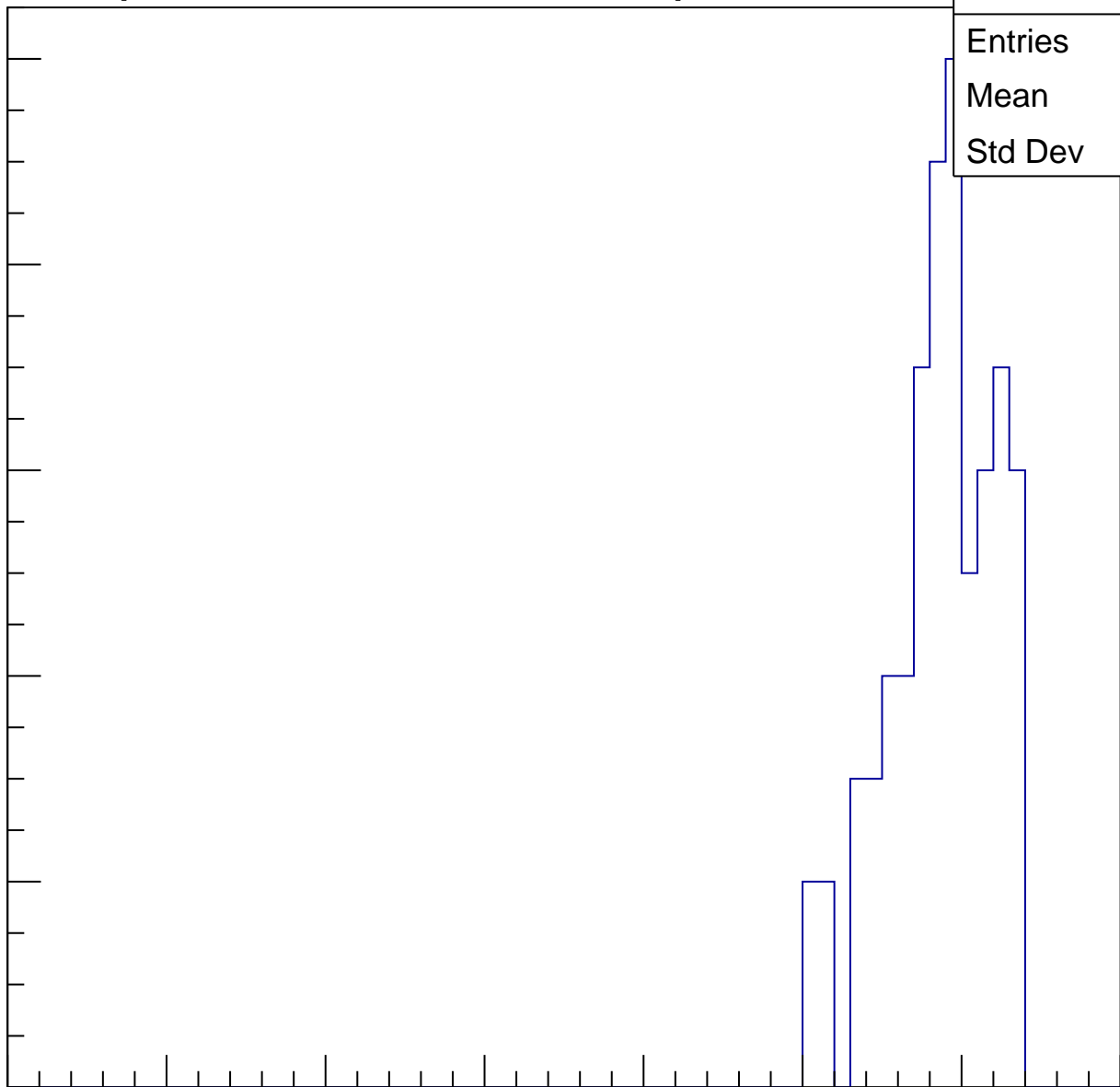
40

50

60

ampl

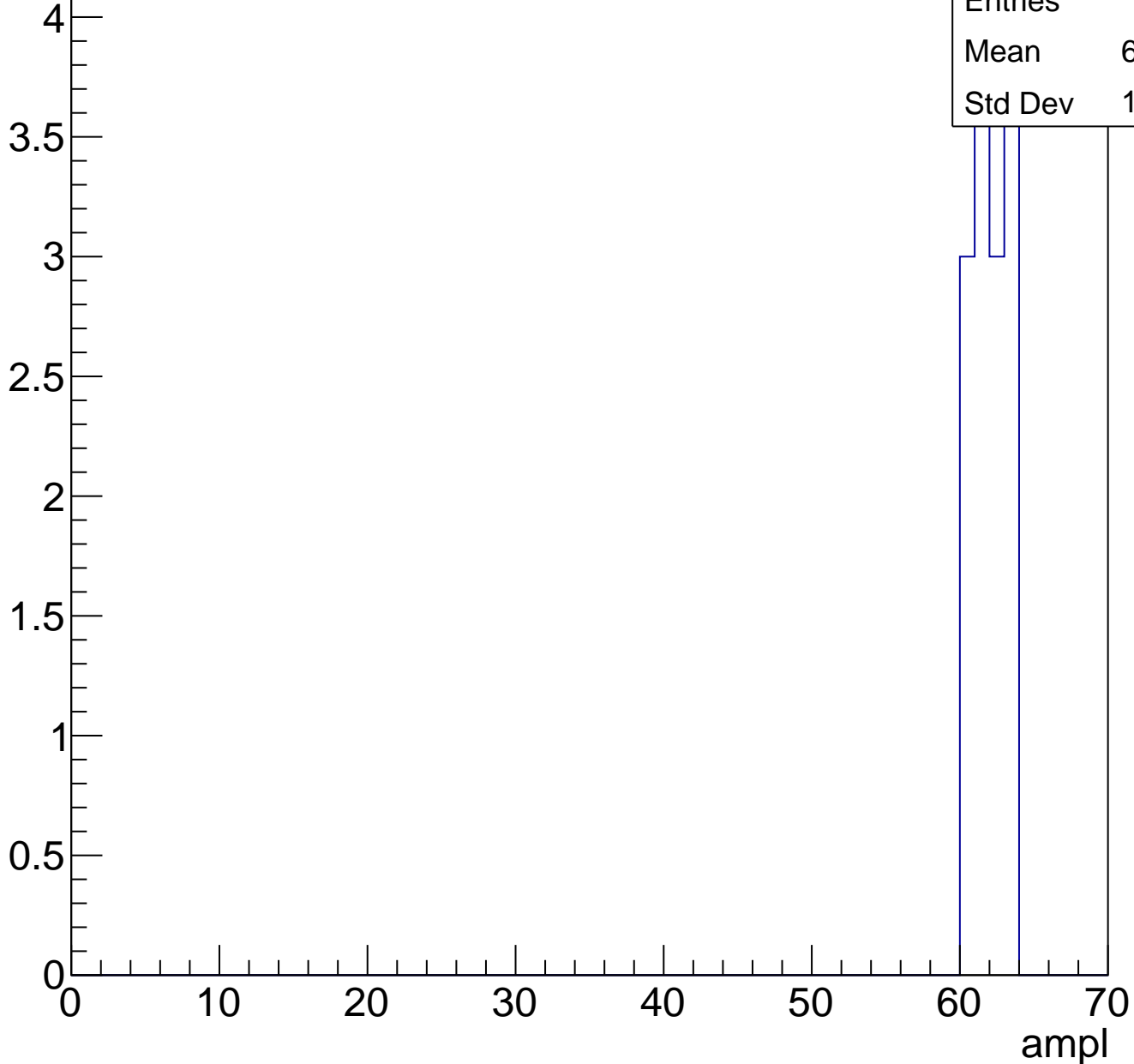
Entries	68
Mean	58.18
Std Dev	3.325



B1L103S, U21-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch53, adc0

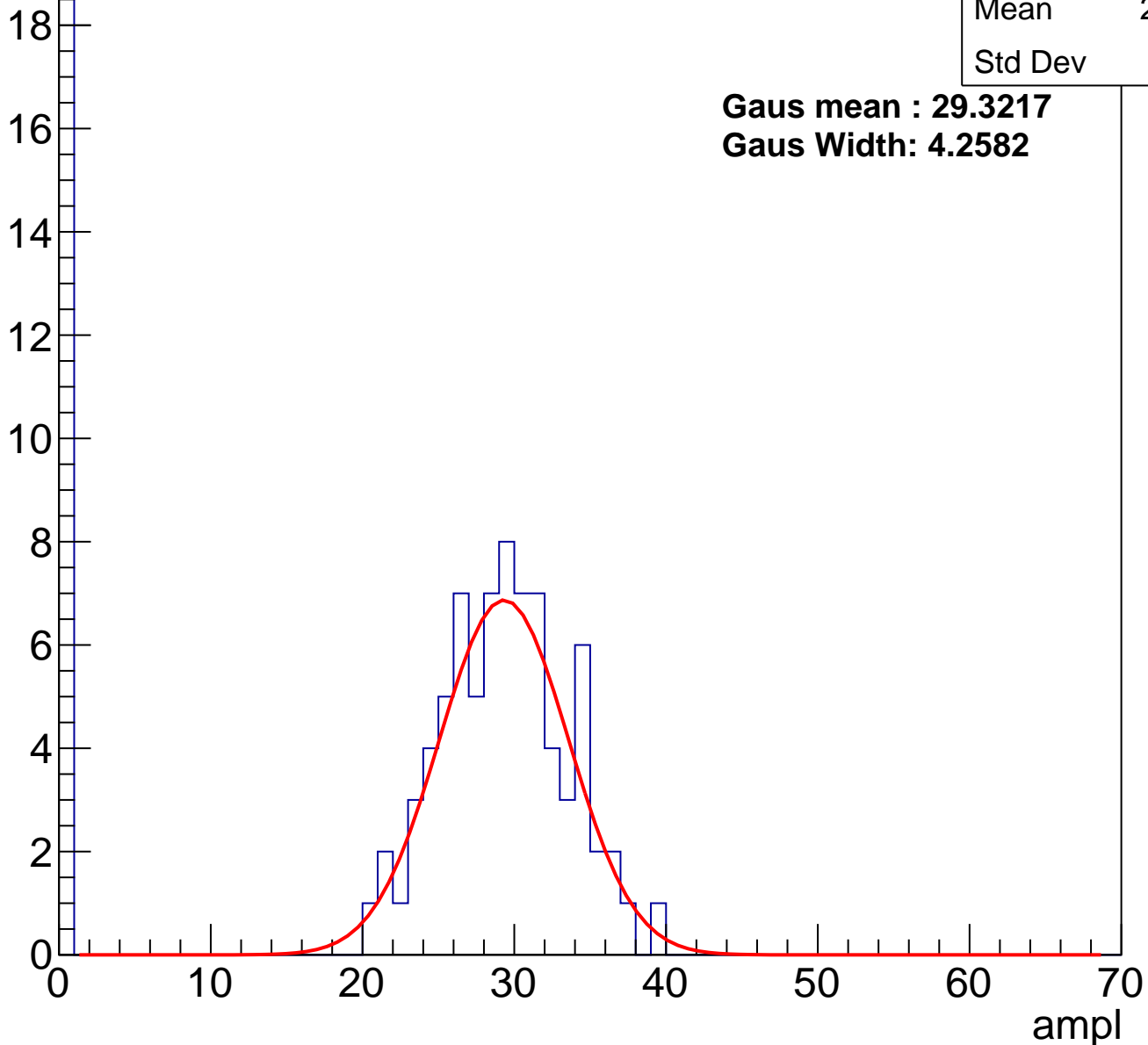
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	23.11
Std Dev	12.1

Gaus mean : 29.3217

Gaus Width: 4.2582

Entry



B1L103S, U21-ch53, adc1

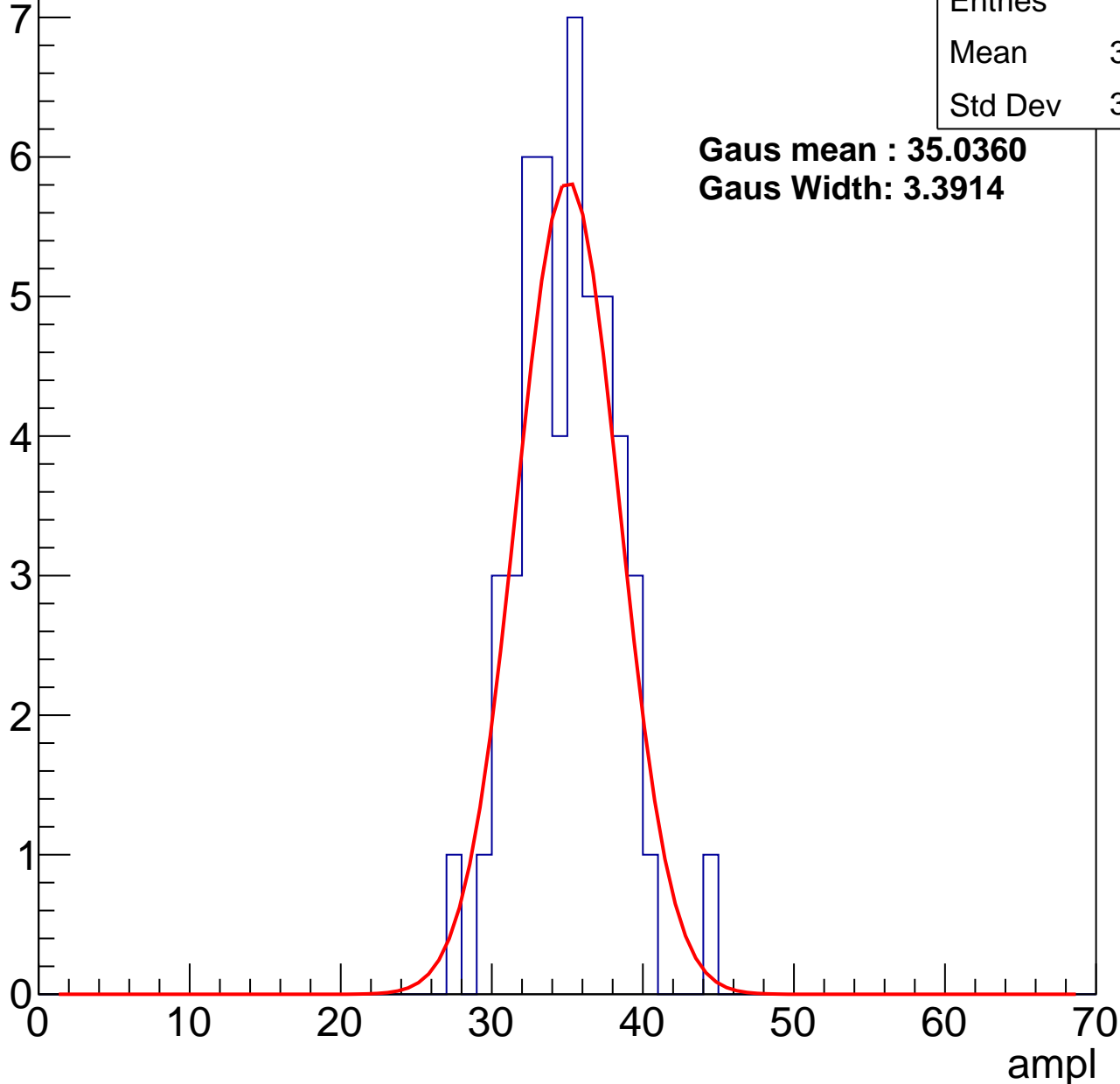
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	34.56
Std Dev	3.195

Gaus mean : 35.0360

Gaus Width: 3.3914



B1L103S, U21-ch53, adc2

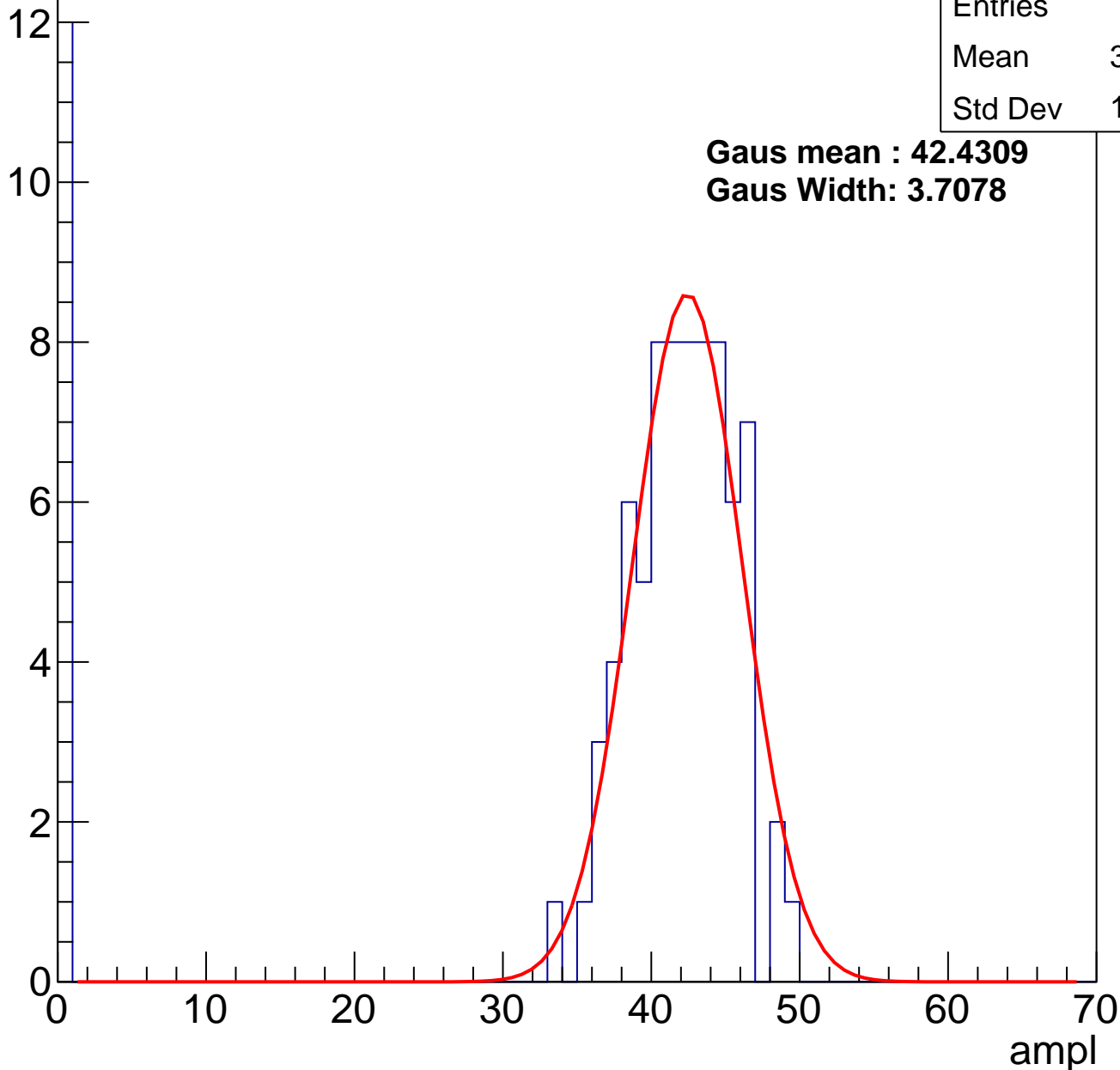
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	35.95
Std Dev	14.62

Gaus mean : 42.4309

Gaus Width: 3.7078

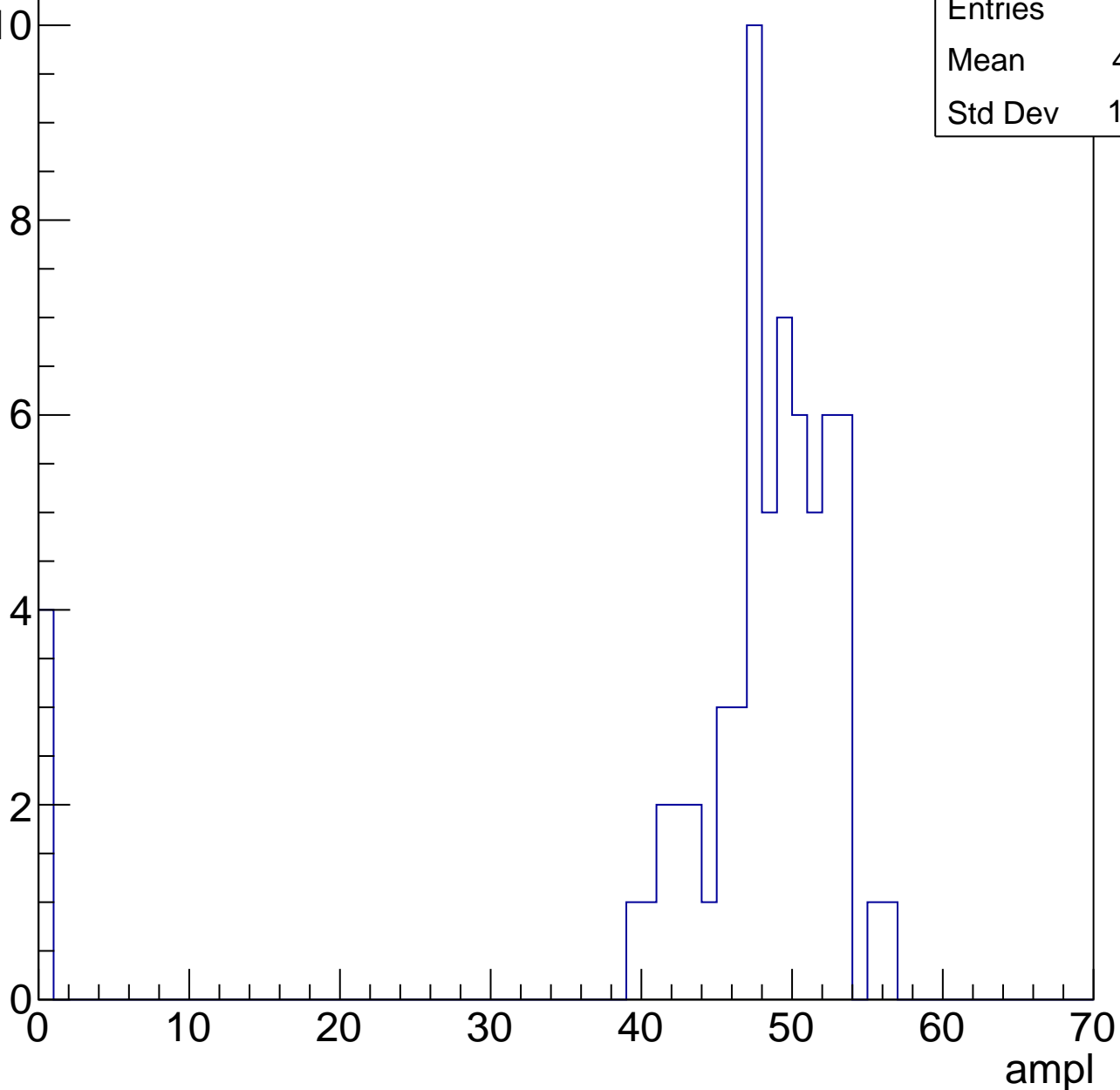


B1L103S, U21-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2

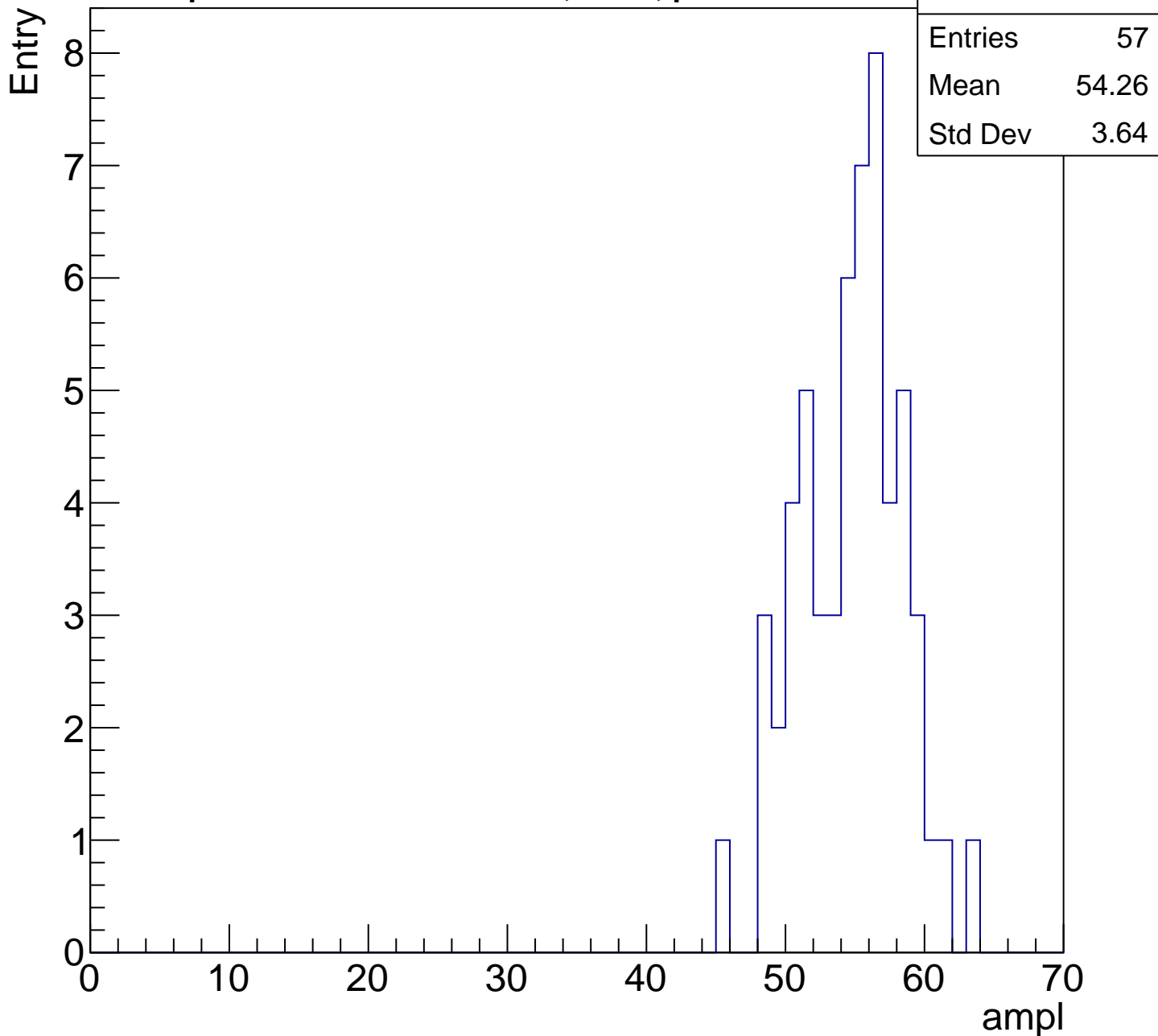
Entry

Entries	66
Mean	45.41
Std Dev	12.08



B1L103S, U21-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

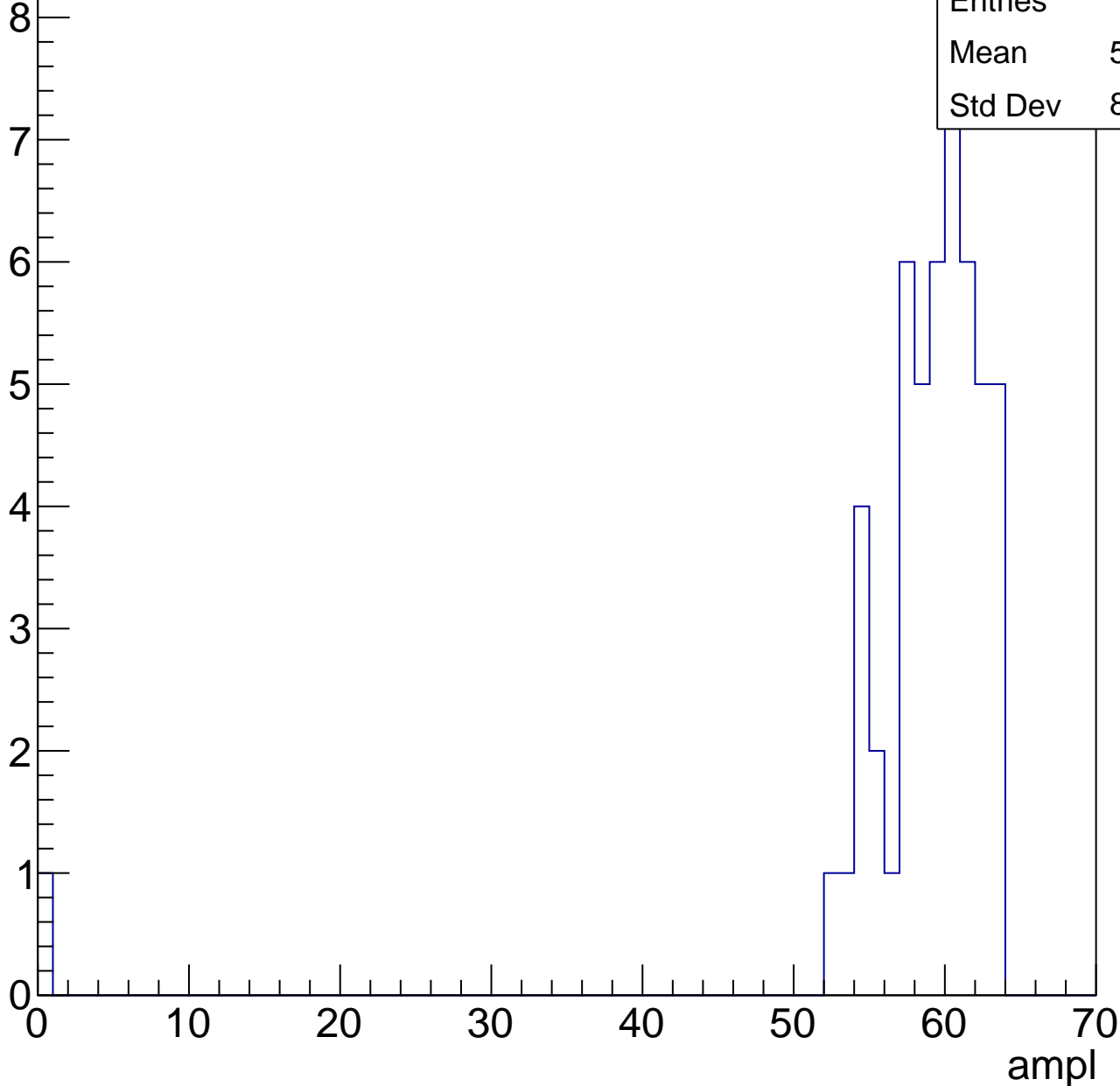


B1L103S, U21-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.73
Std Dev	8.643

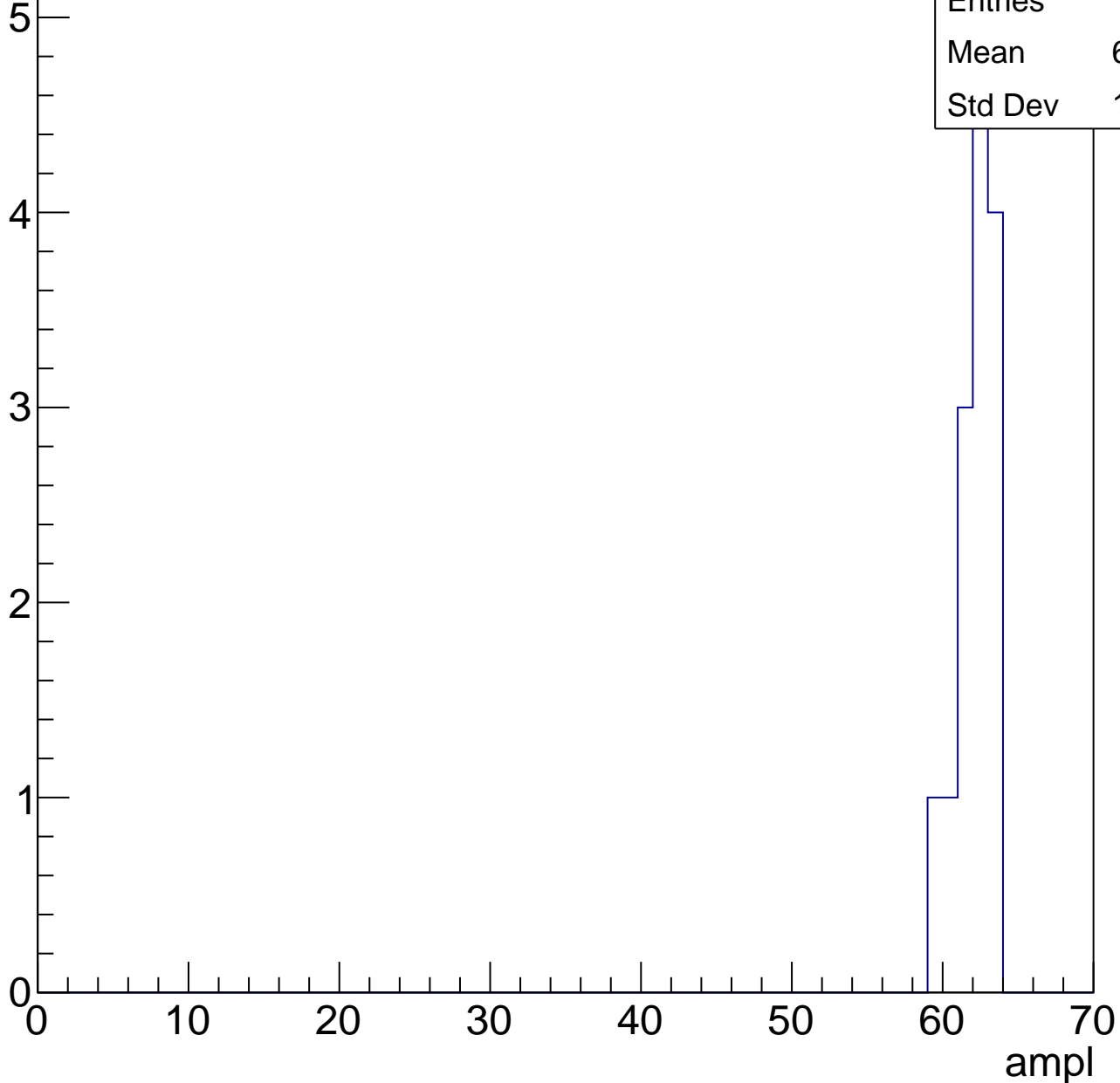


B1L103S, U21-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.71
Std Dev	1.161



B1L103S, U21-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U21-ch54, adc0

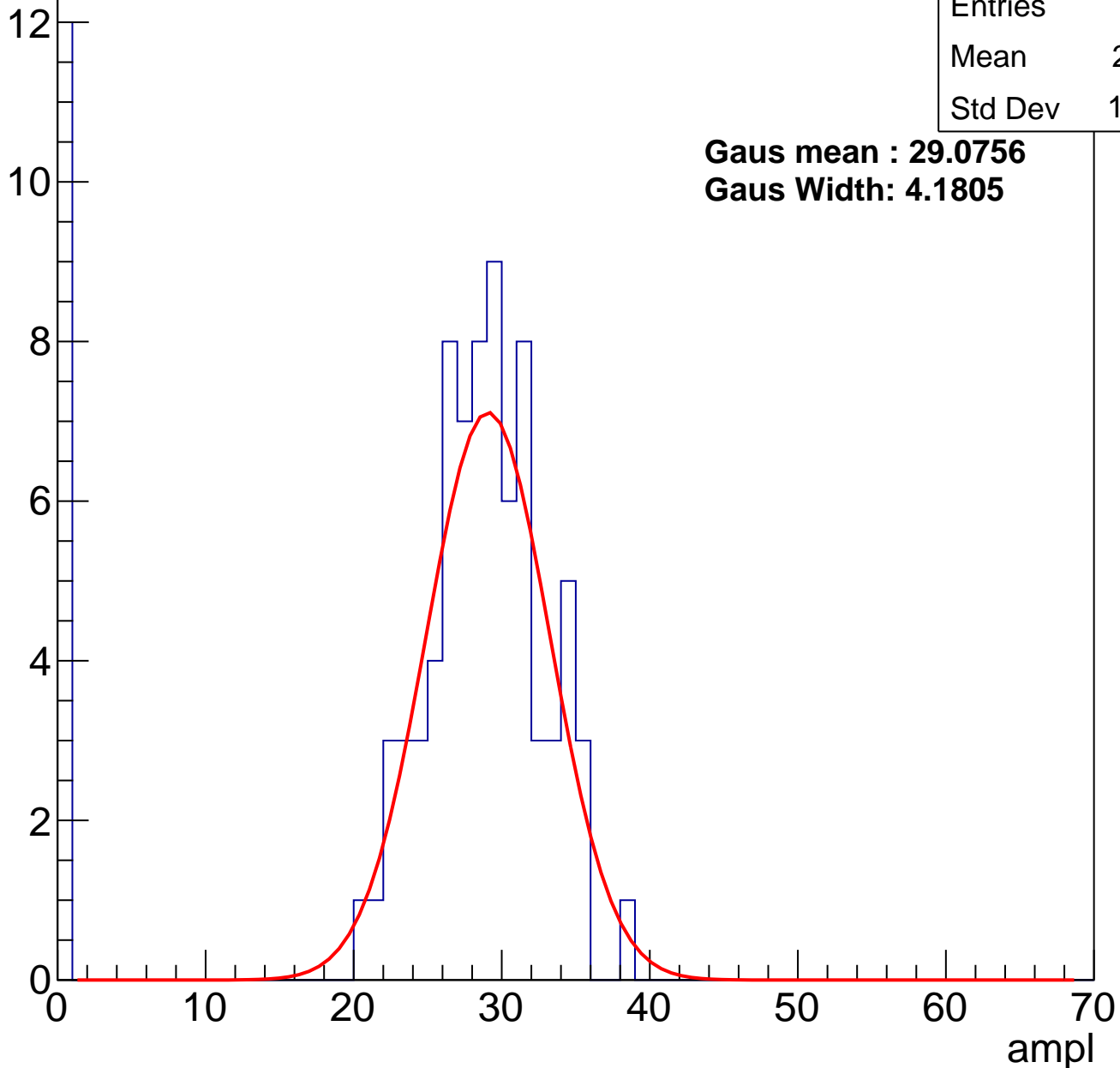
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	24.61
Std Dev	10.38

Gaus mean : 29.0756

Gaus Width: 4.1805

Entry



B1L103S, U21-ch54, adc1

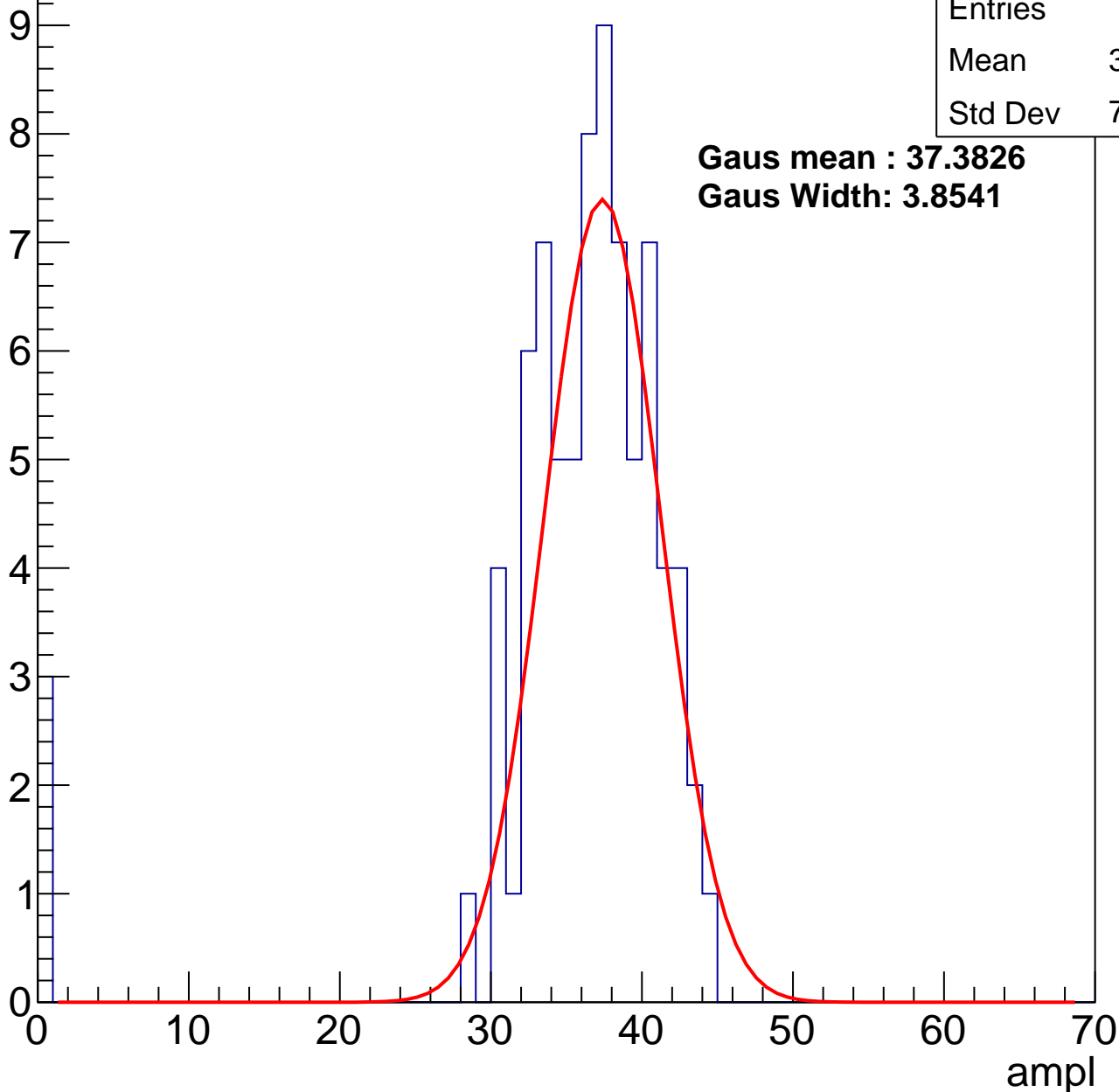
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	35.08
Std Dev	7.825

Gaus mean : 37.3826

Gaus Width: 3.8541



B1L103S, U21-ch54, adc2

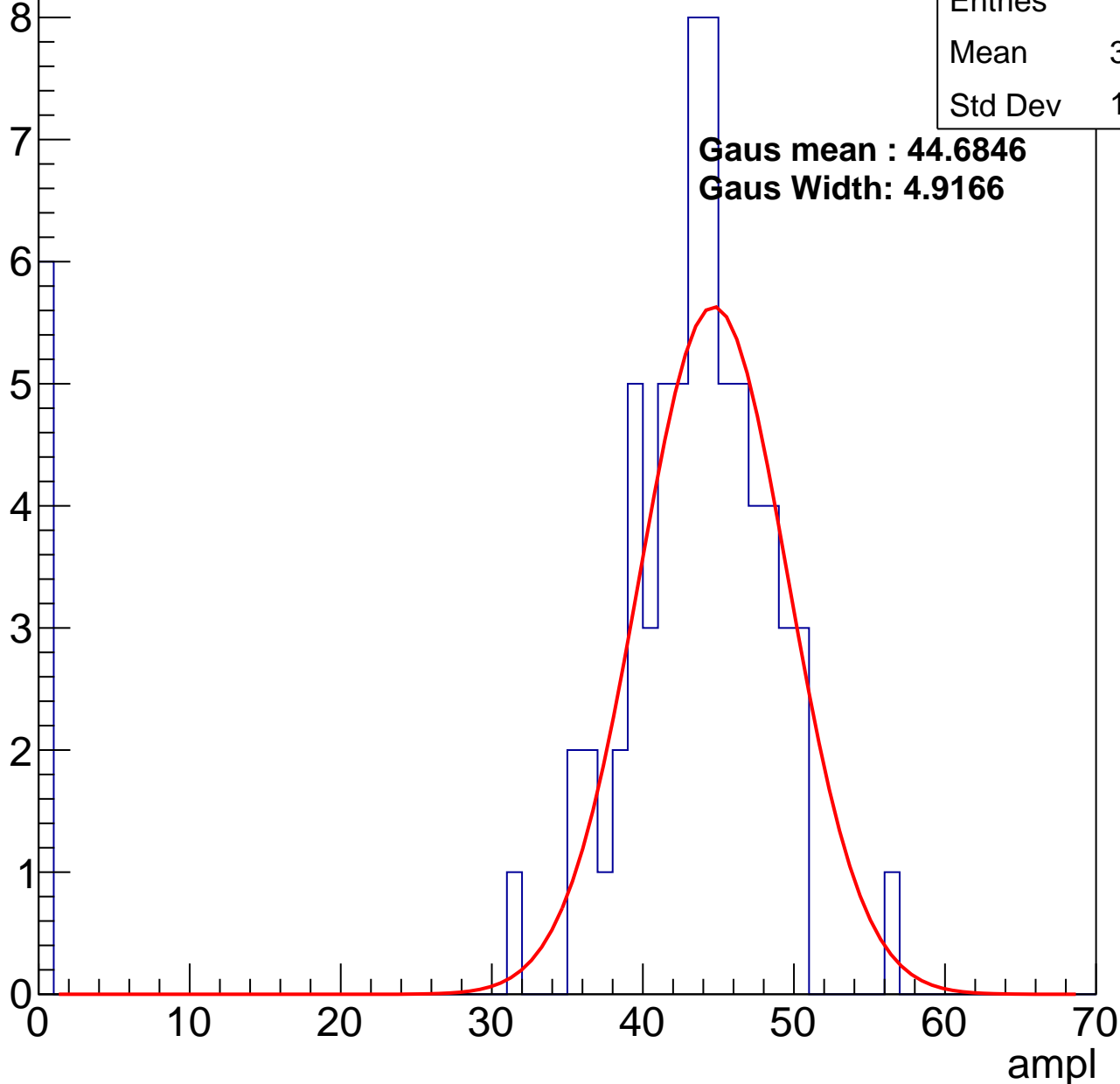
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	39.73
Std Dev	12.58

Gaus mean : 44.6846

Gaus Width: 4.9166

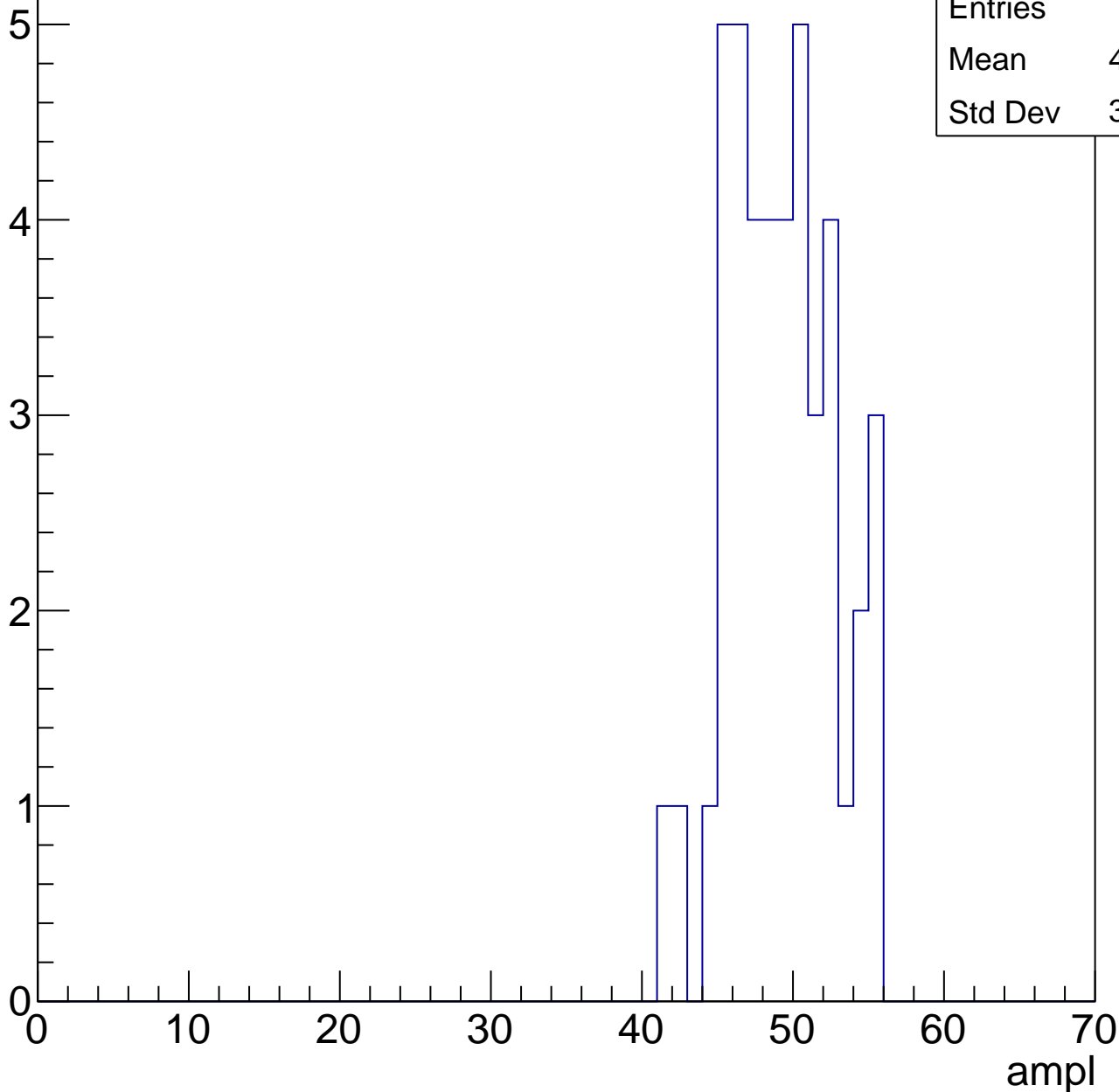


B1L103S, U21-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	48.72
Std Dev	3.433

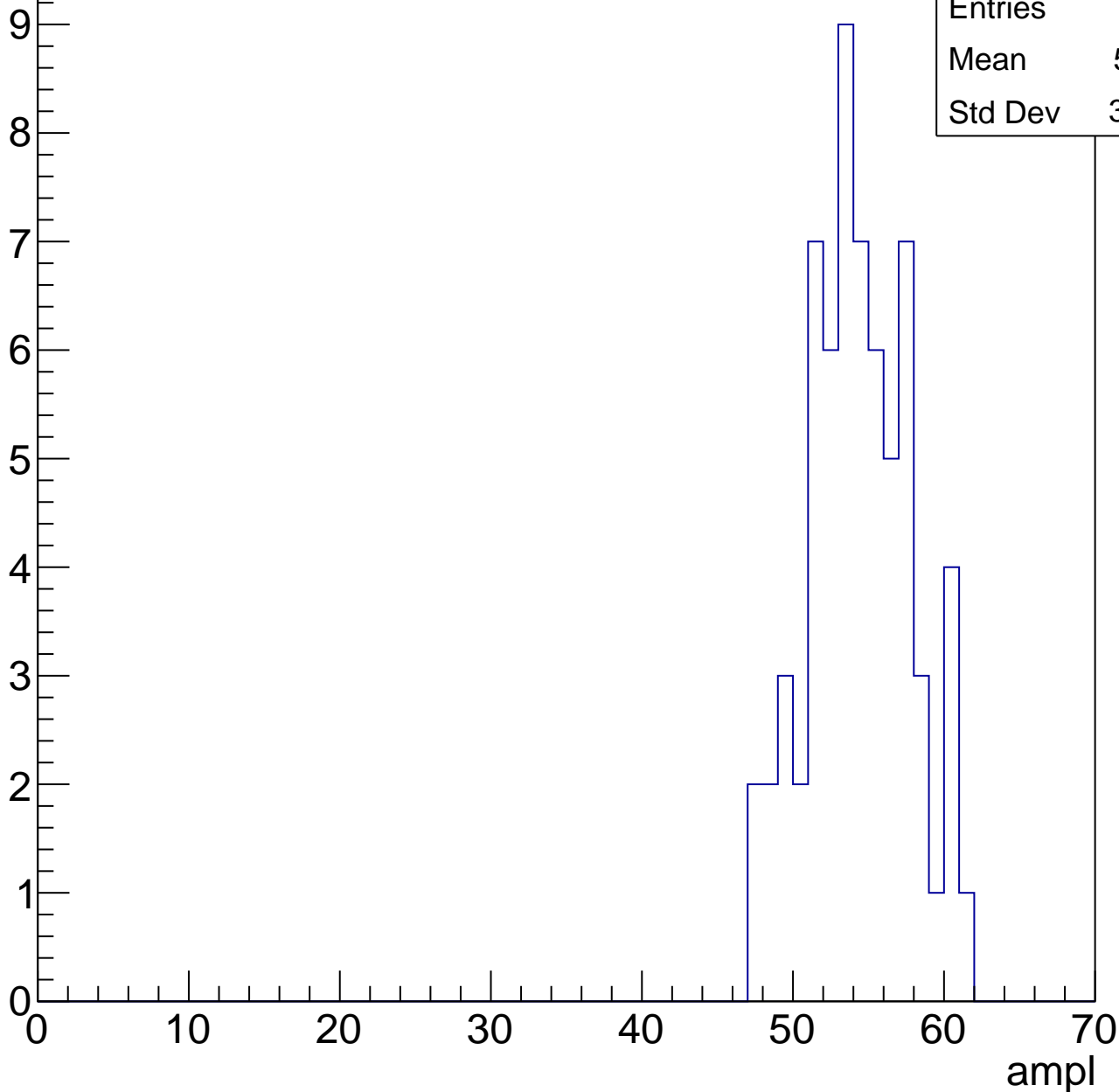


B1L103S, U21-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

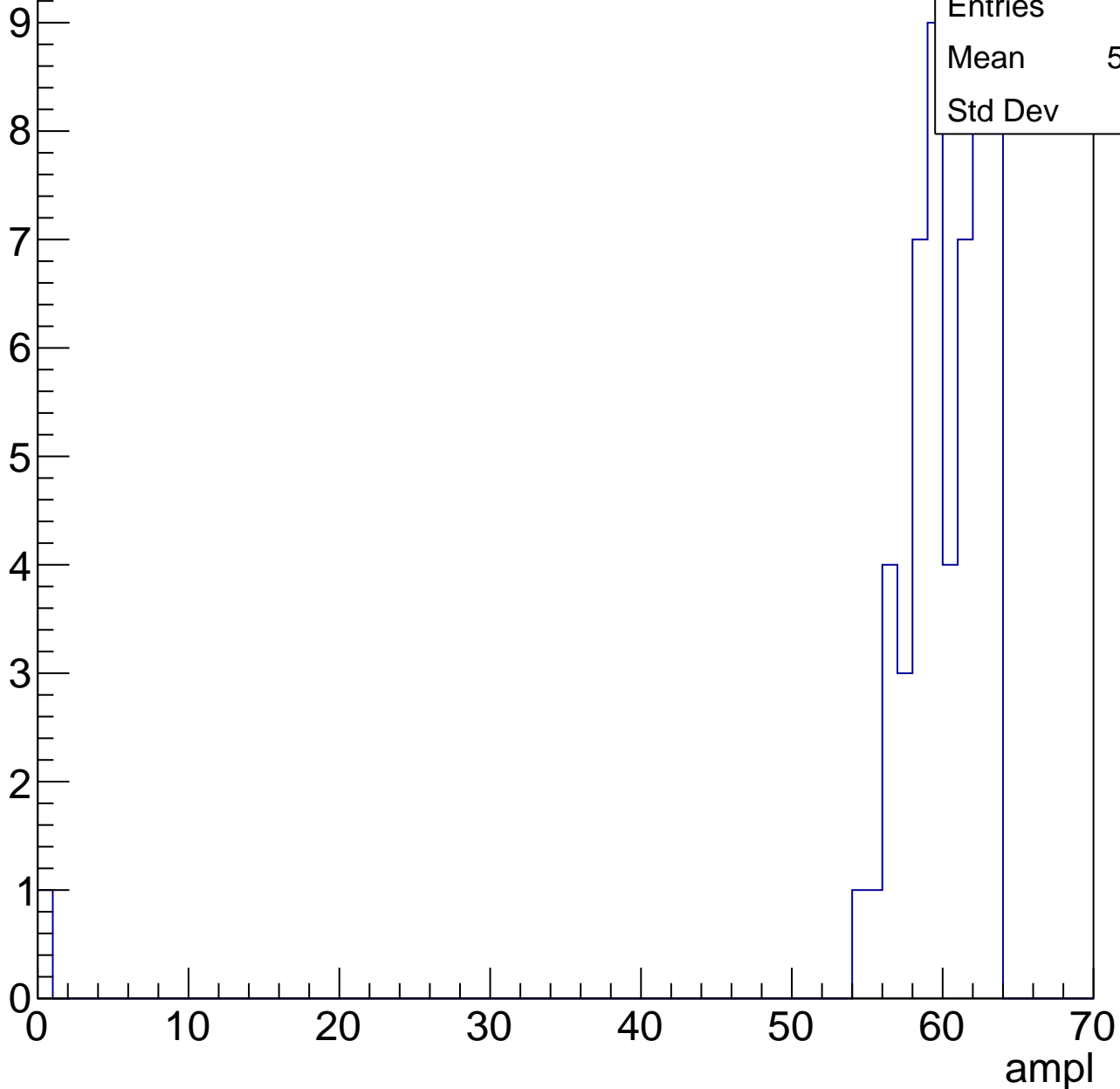
Entries	65
Mean	53.91
Std Dev	3.345



B1L103S, U21-ch54, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	8
Mean	61.5
Std Dev	1.118

B1L103S, U21-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch55, adc0

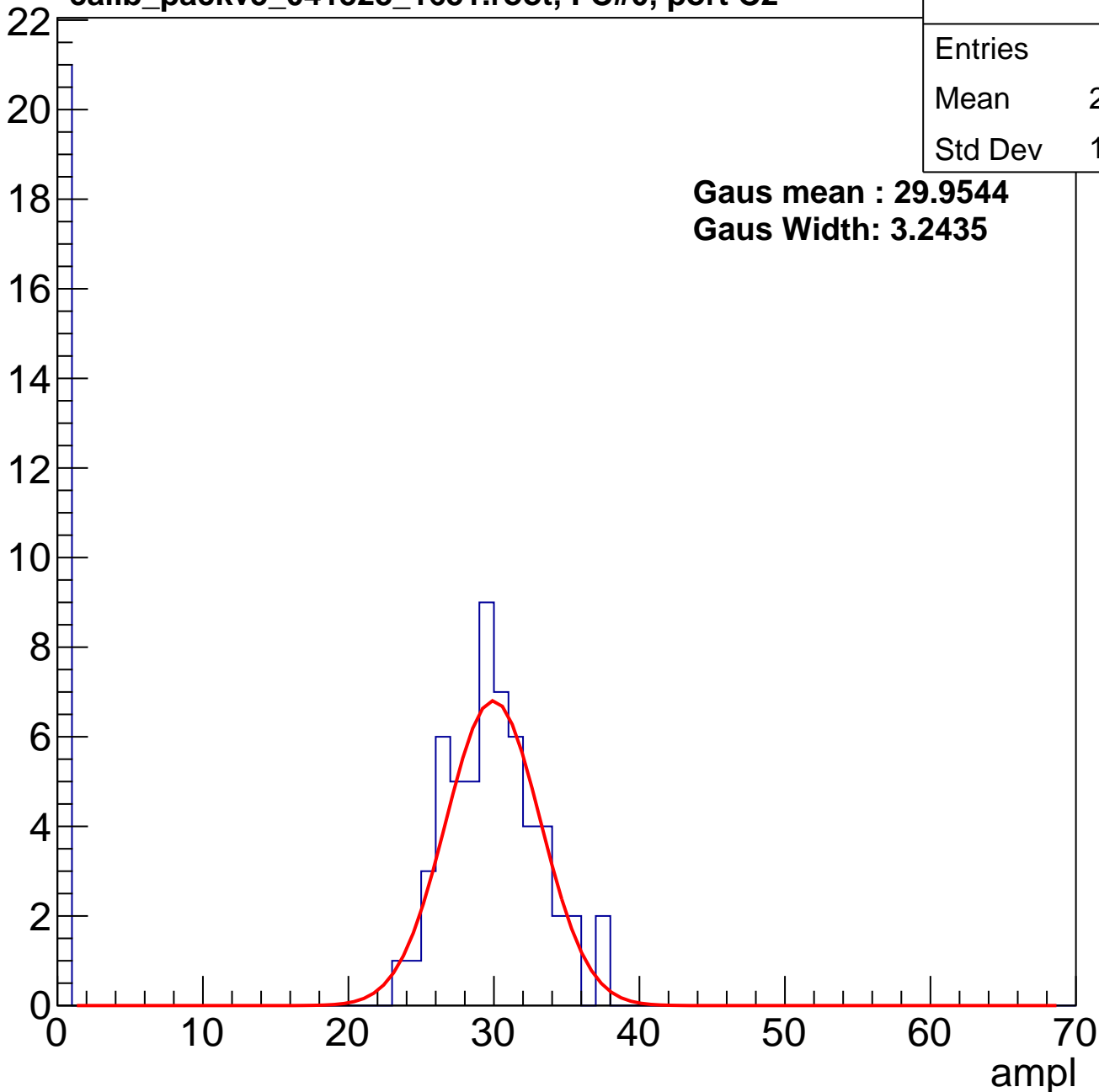
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	21.56
Std Dev	13.36

Gaus mean : 29.9544

Gaus Width: 3.2435

Entry



B1L103S, U21-ch55, adc1

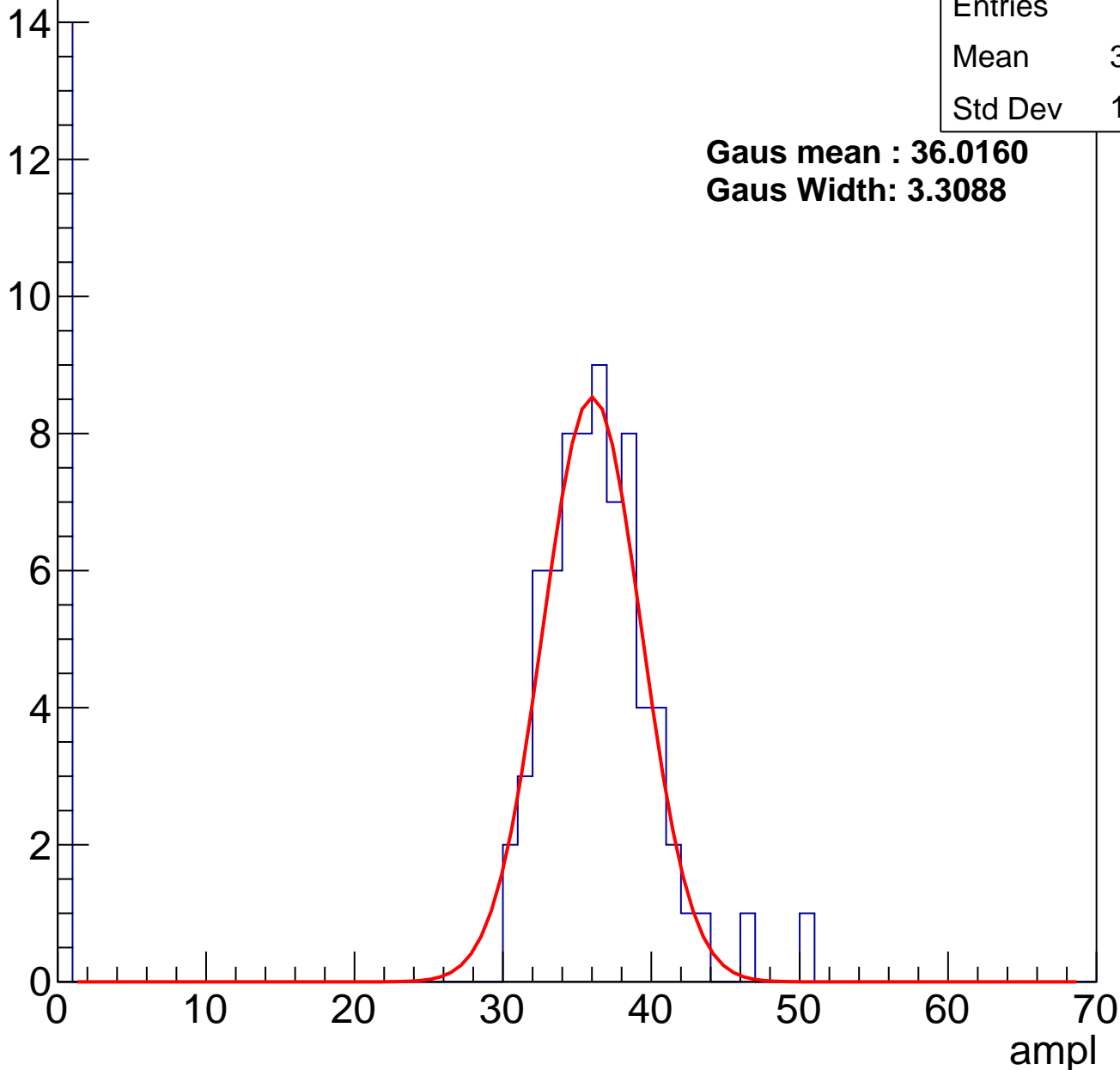
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	30.13
Std Dev	13.77

Gaus mean : 36.0160

Gaus Width: 3.3088

Entry



B1L103S, U21-ch55, adc2

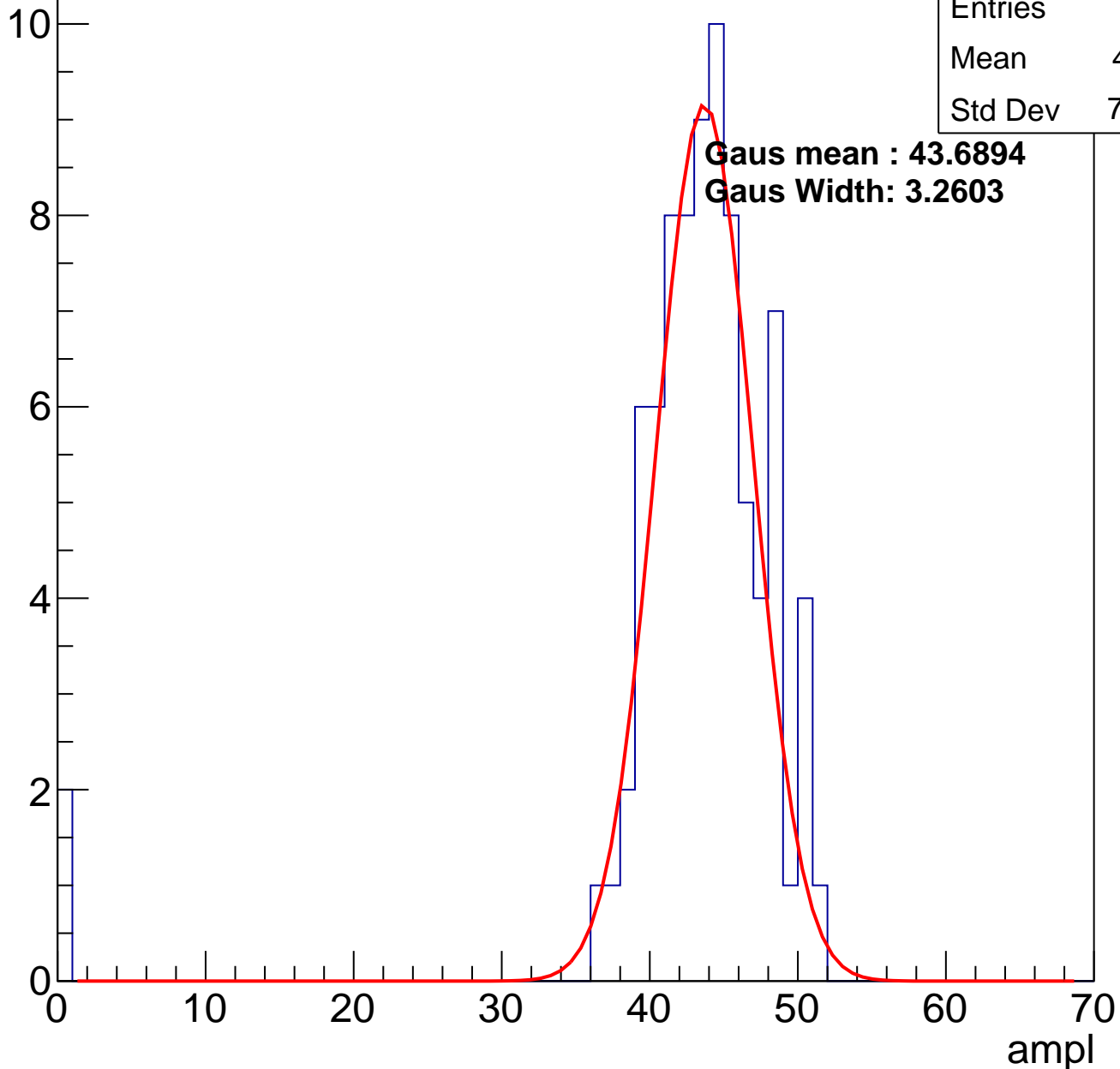
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	42.51
Std Dev	7.465

Gaus mean : 43.6894

Gaus Width: 3.2603

Entry

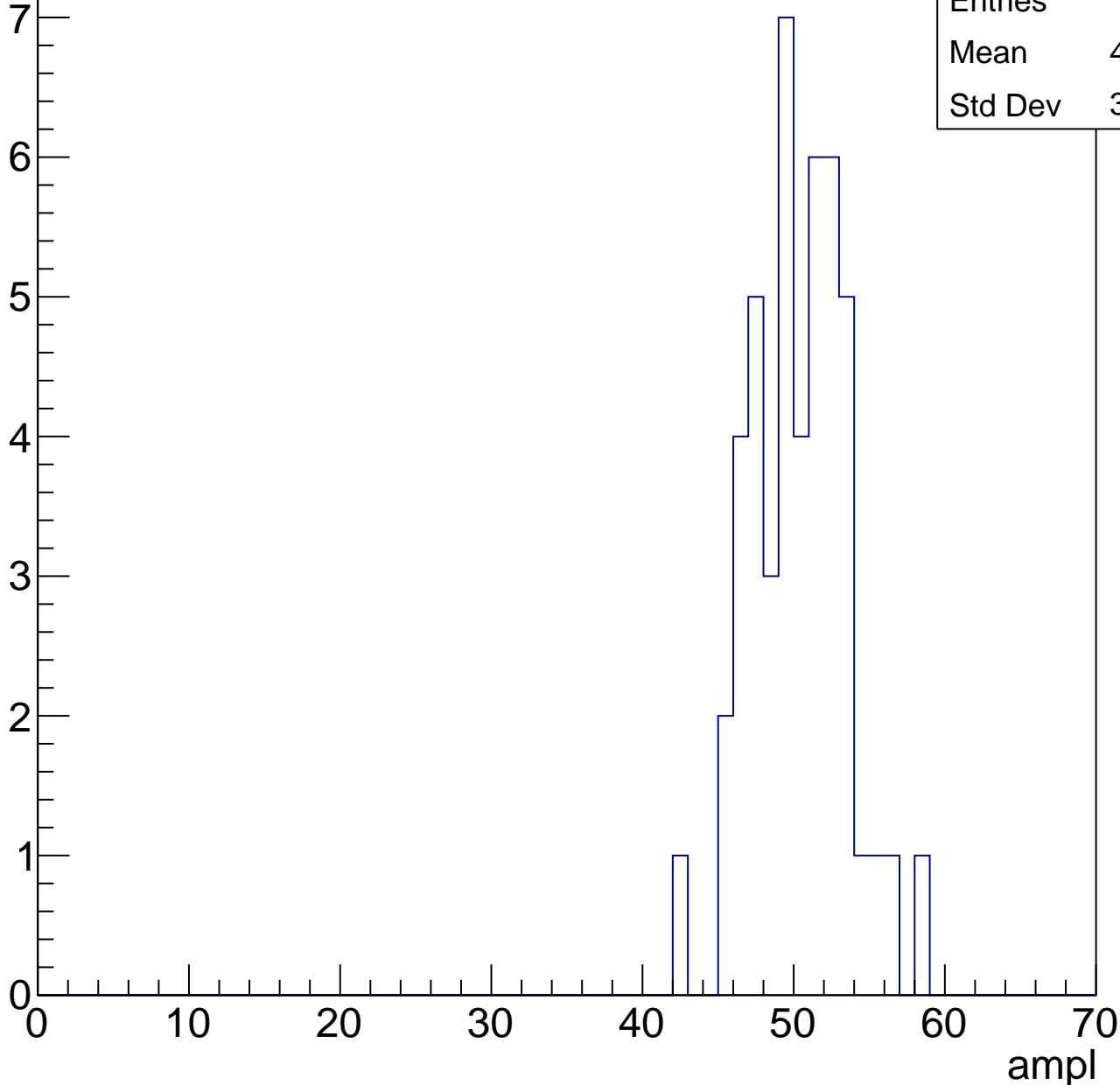


B1L103S, U21-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	49.87
Std Dev	3.119

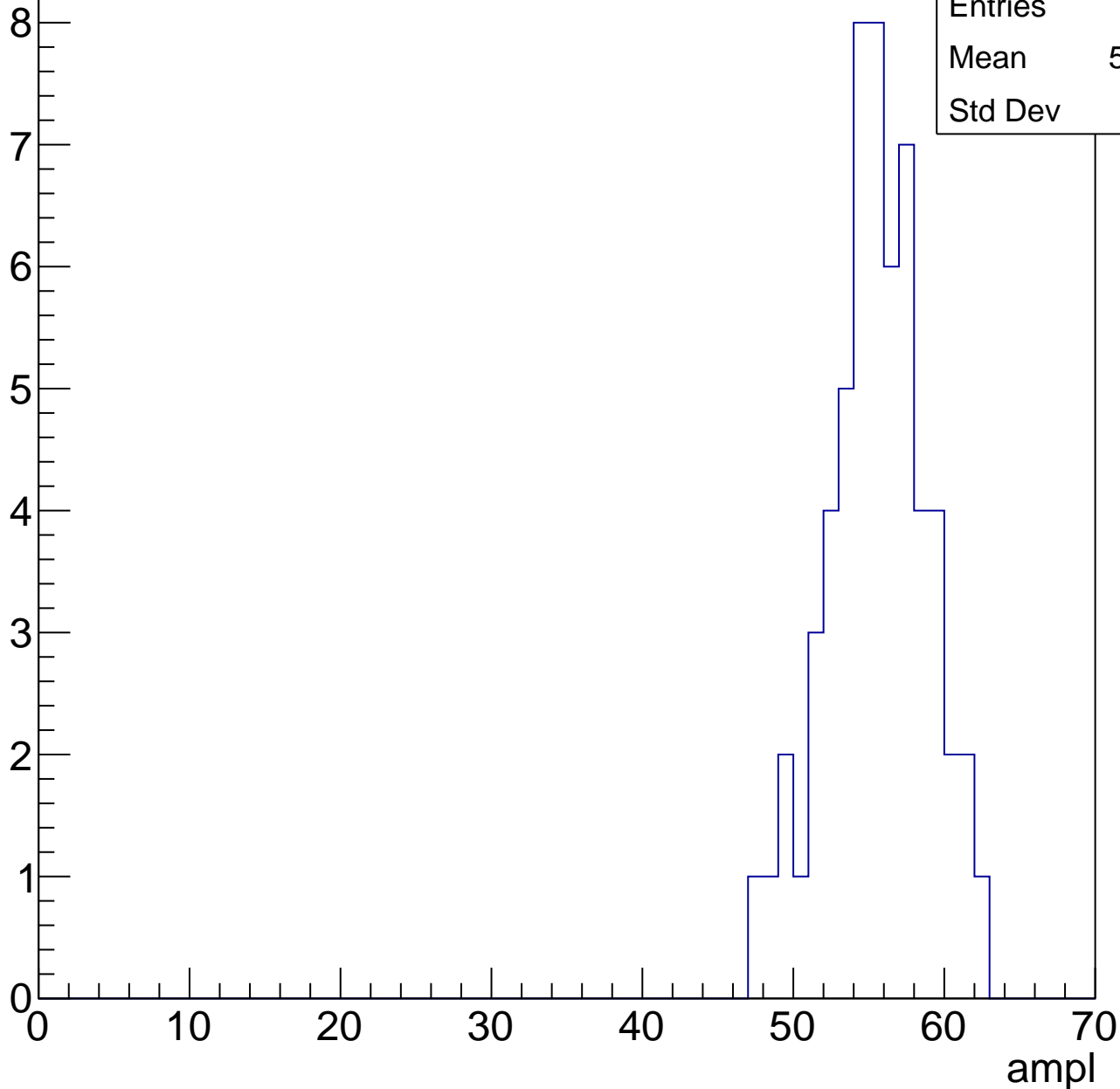


B1L103S, U21-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	55.05
Std Dev	3.26

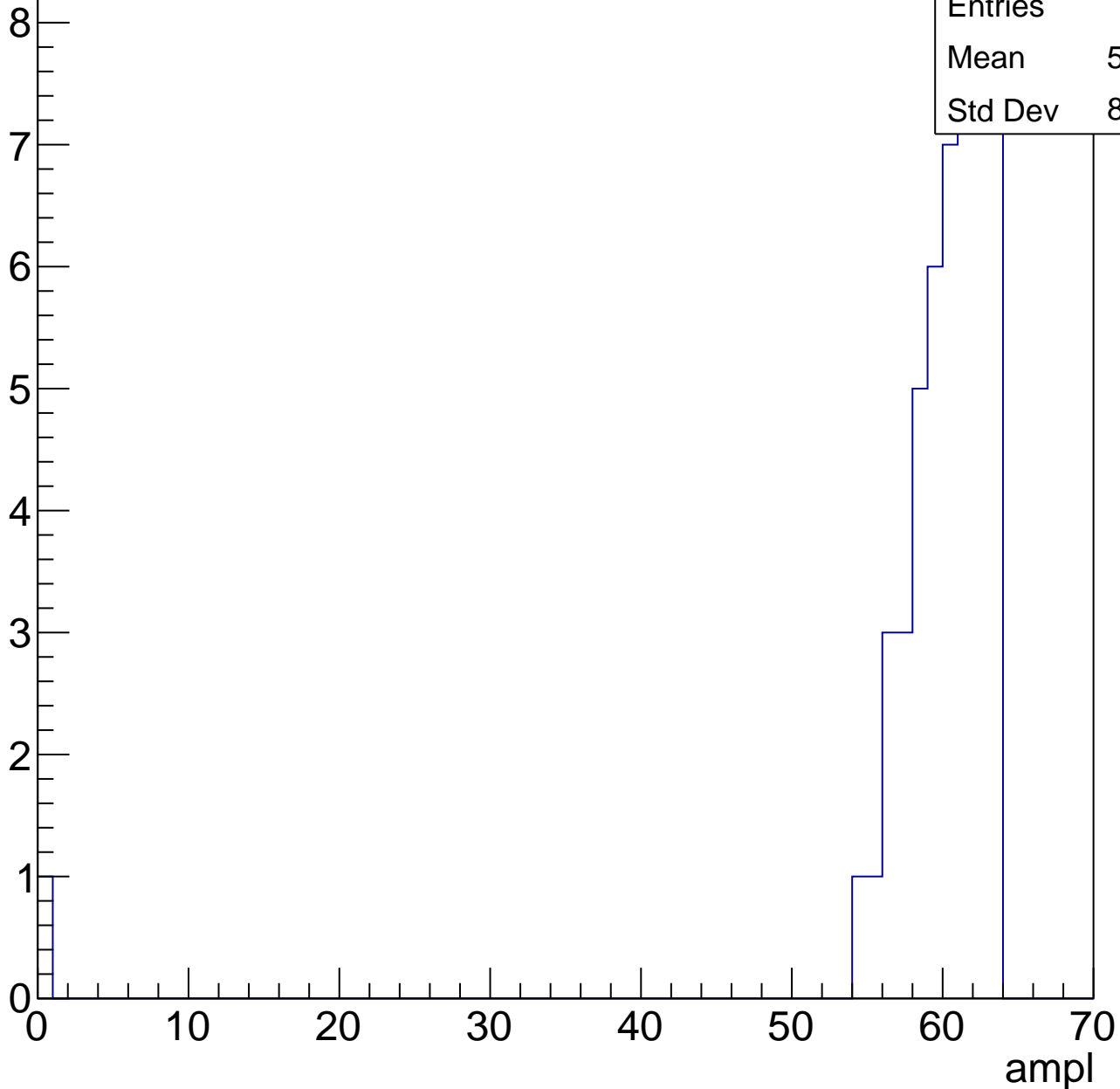


B1L103S, U21-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.82
Std Dev	8.636



B1L103S, U21-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch56, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	21.23
Std Dev	10.79

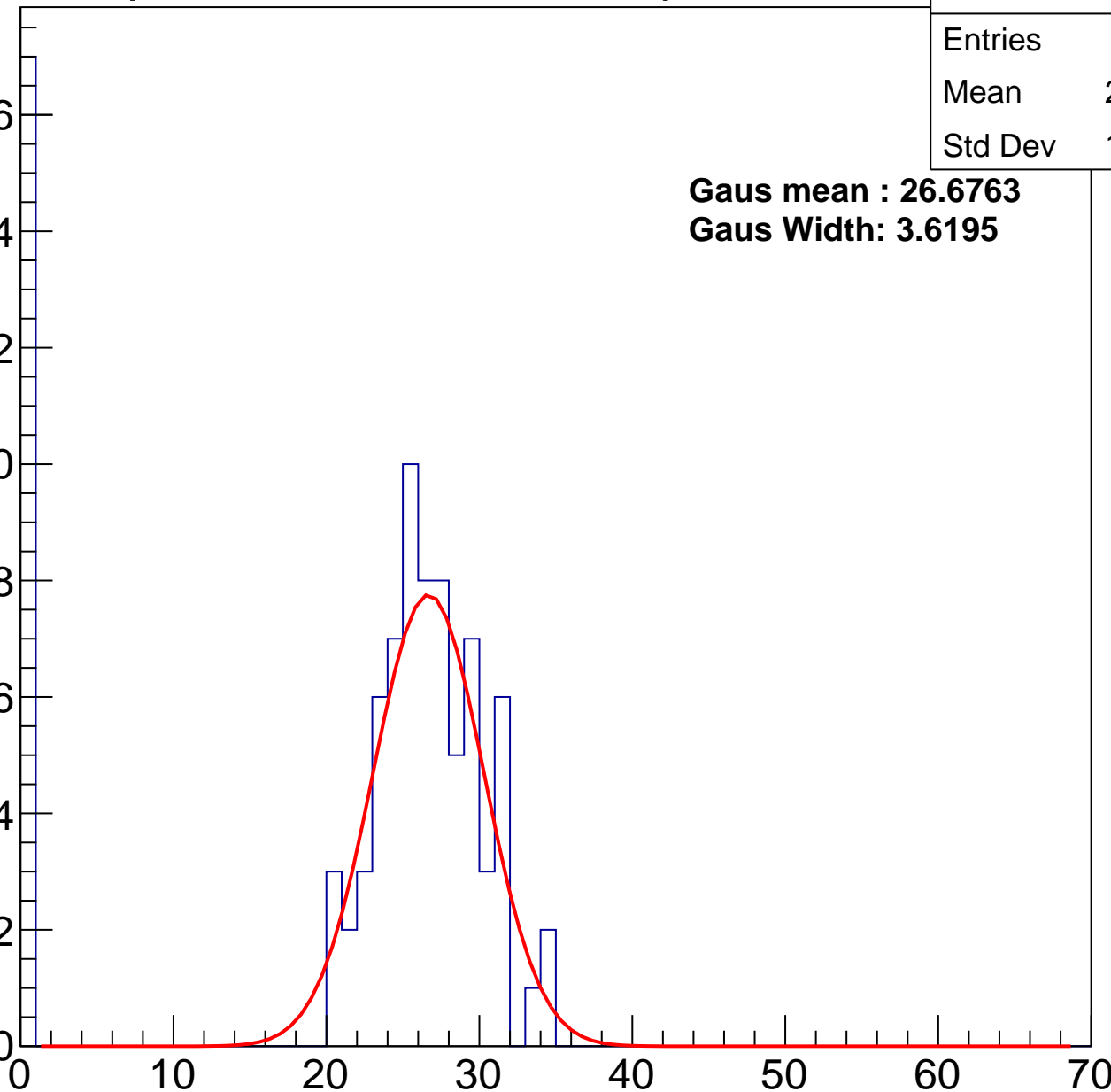
Gaus mean : 26.6763

Gaus Width: 3.6195

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch56, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	33.75
Std Dev	3.727

Gaus mean : 34.0877

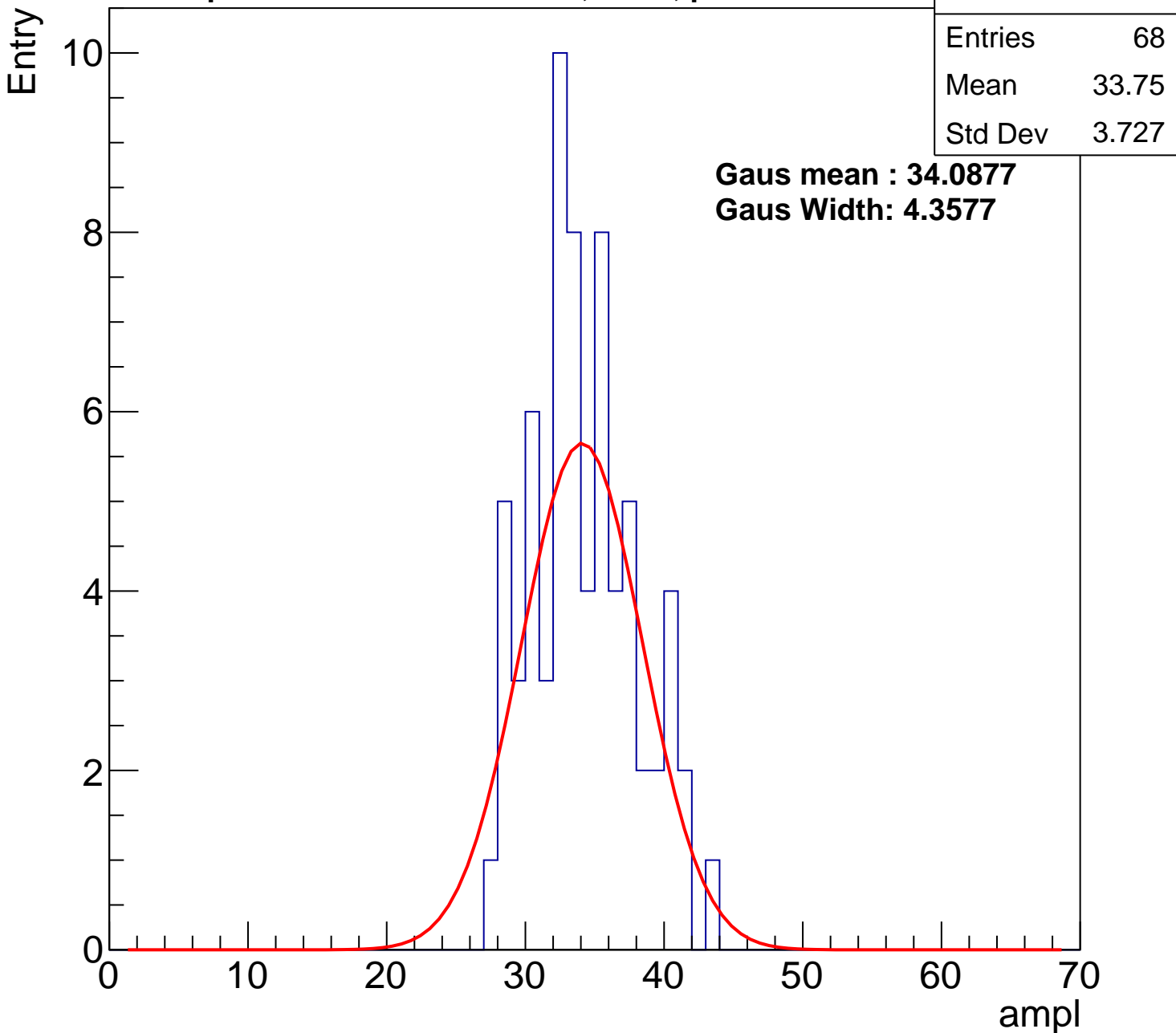
Gaus Width: 4.3577

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch56, adc2

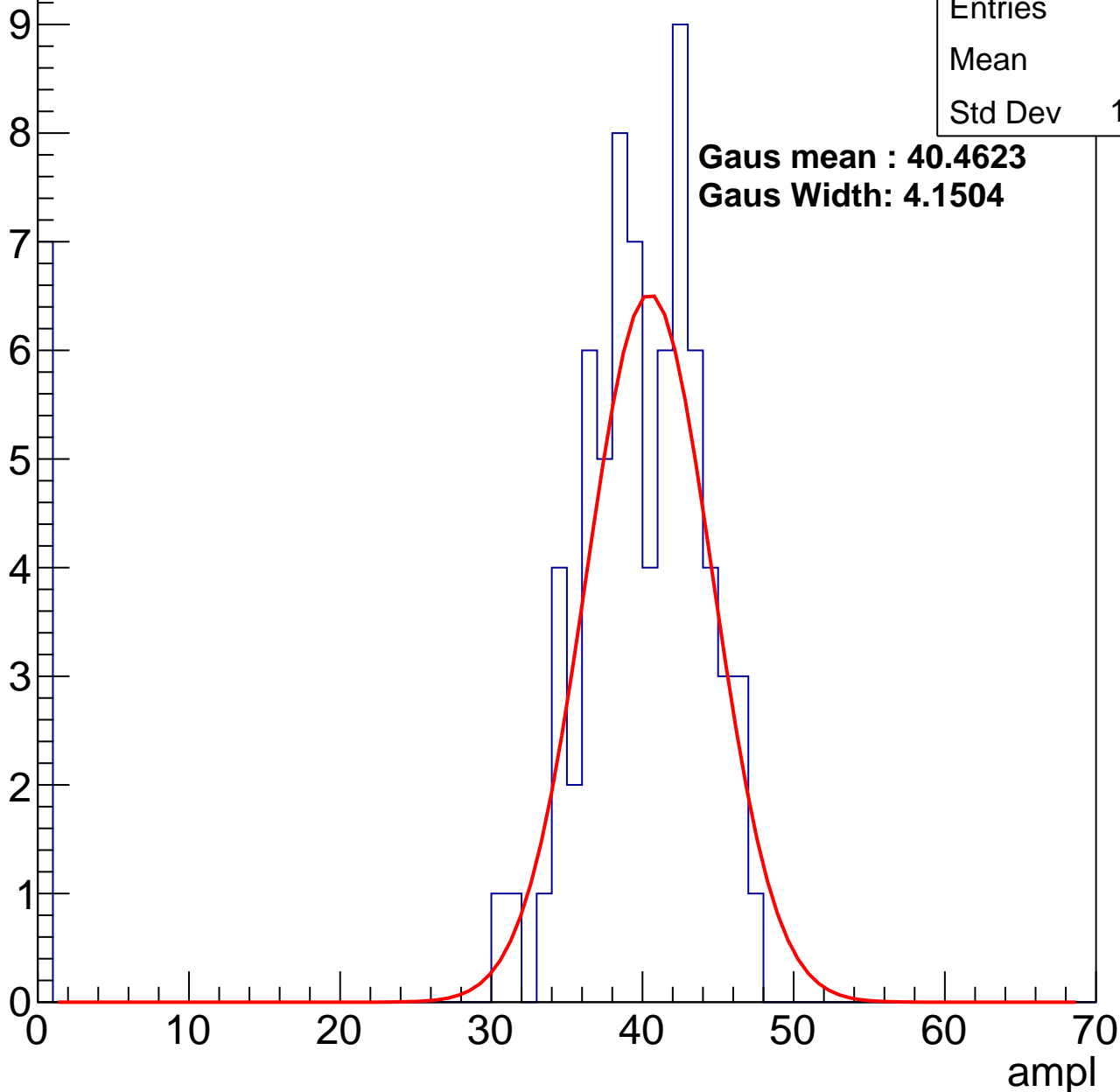
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	36.1
Std Dev	11.88

Gaus mean : 40.4623

Gaus Width: 4.1504



B1L103S, U21-ch56, adc3

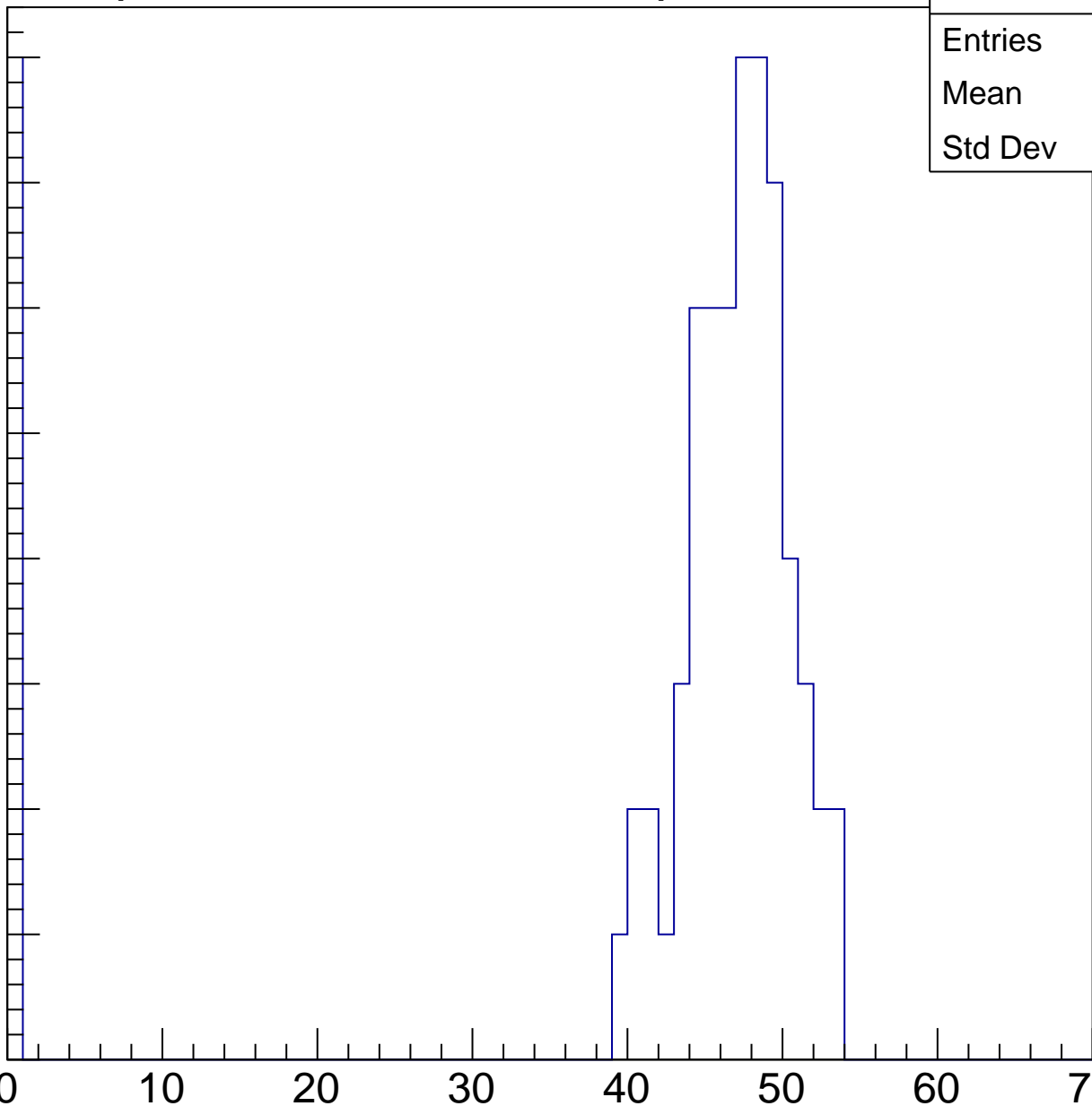
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	69
Mean	41.28
Std Dev	15.25

ampl

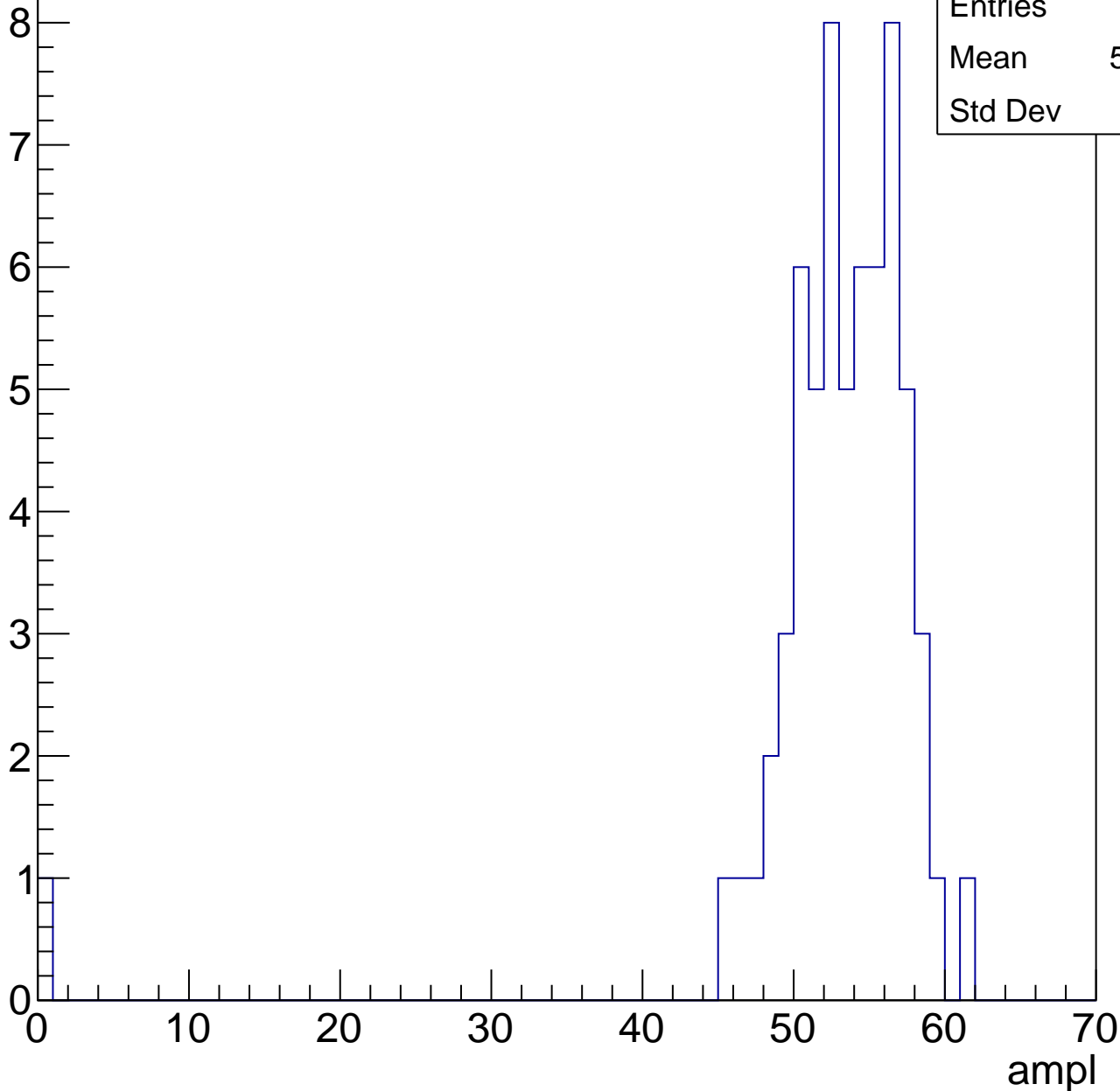


B1L103S, U21-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.35
Std Dev	7.42

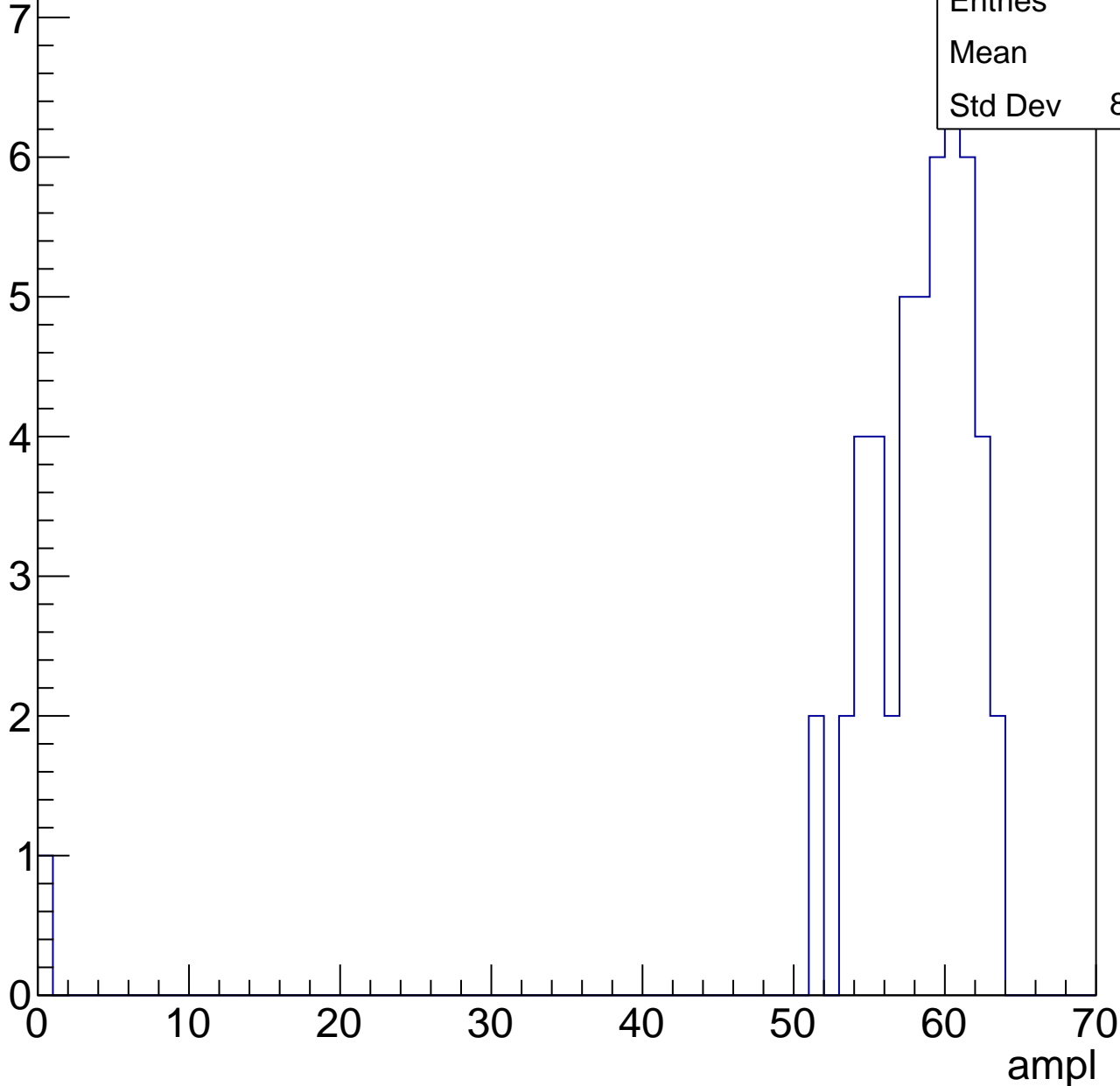


B1L103S, U21-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	56.9
Std Dev	8.679

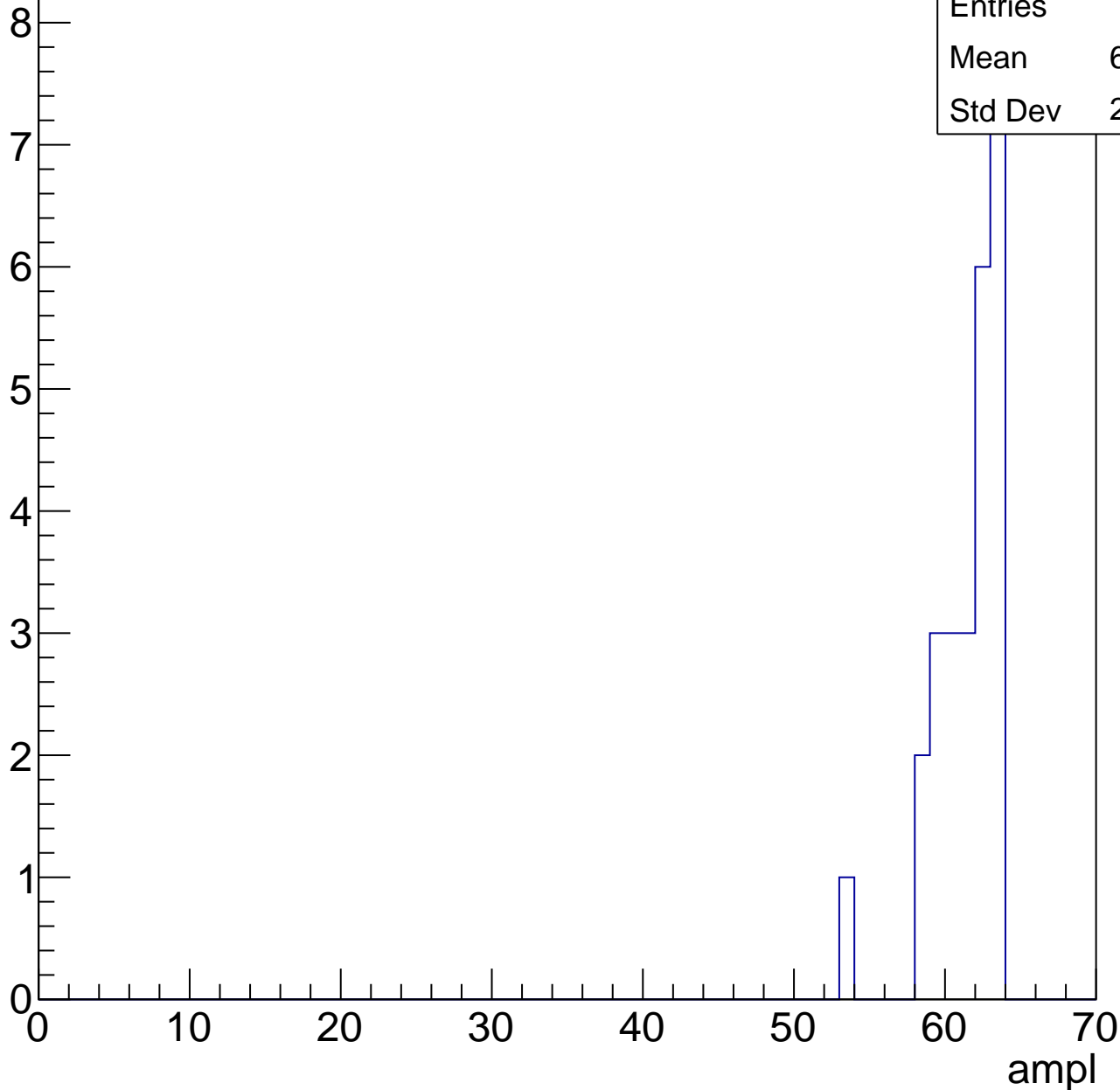


B1L103S, U21-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	60.96
Std Dev	2.278



B1L103S, U21-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch57, adc0

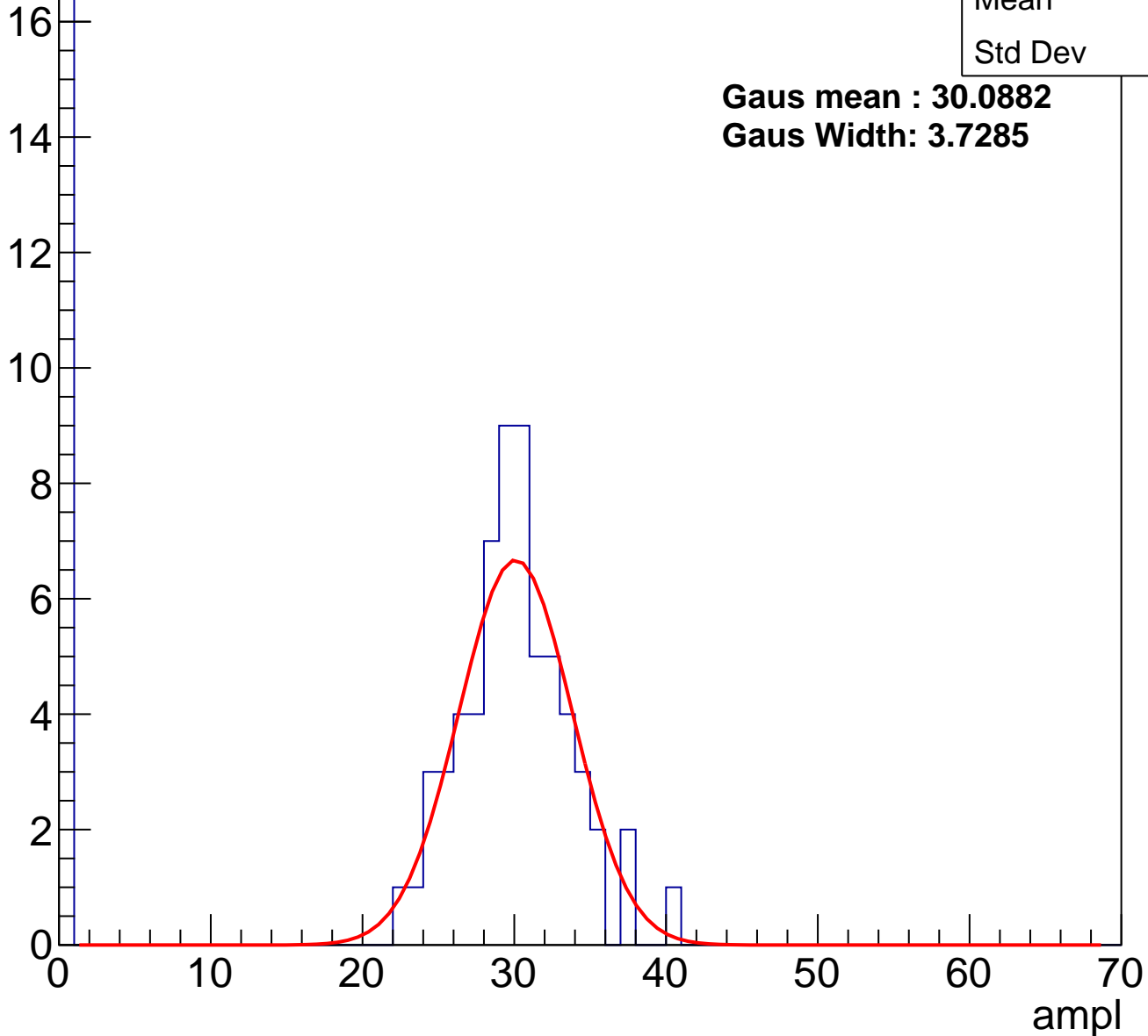
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	23.3
Std Dev	12.5

Gaus mean : 30.0882

Gaus Width: 3.7285

Entry



B1L103S, U21-ch57, adc1

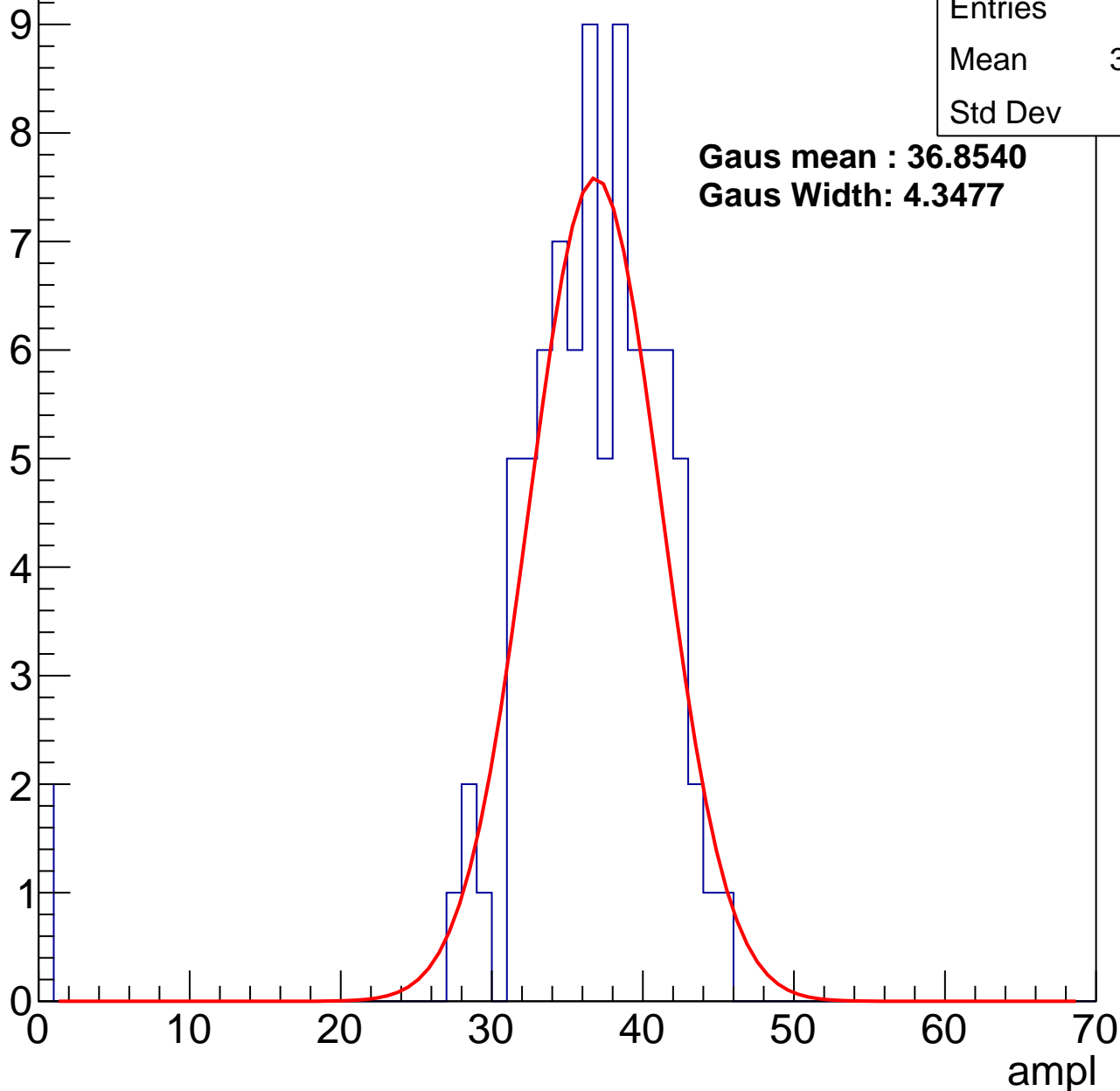
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	35.64
Std Dev	6.77

Gaus mean : 36.8540

Gaus Width: 4.3477



B1L103S, U21-ch57, adc2

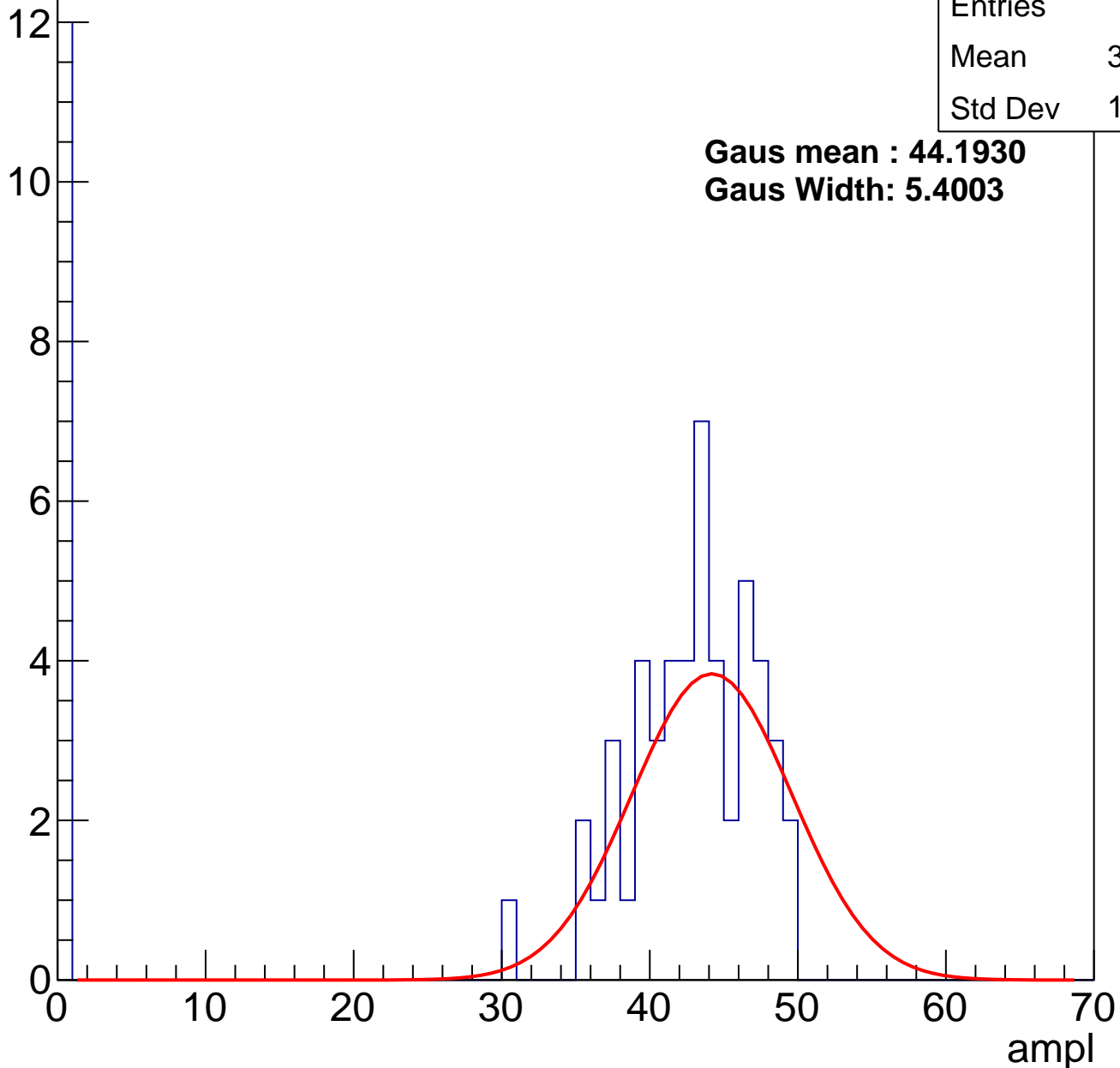
calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	34.19
Std Dev	17.15

Gaus mean : 44.1930

Gaus Width: 5.4003

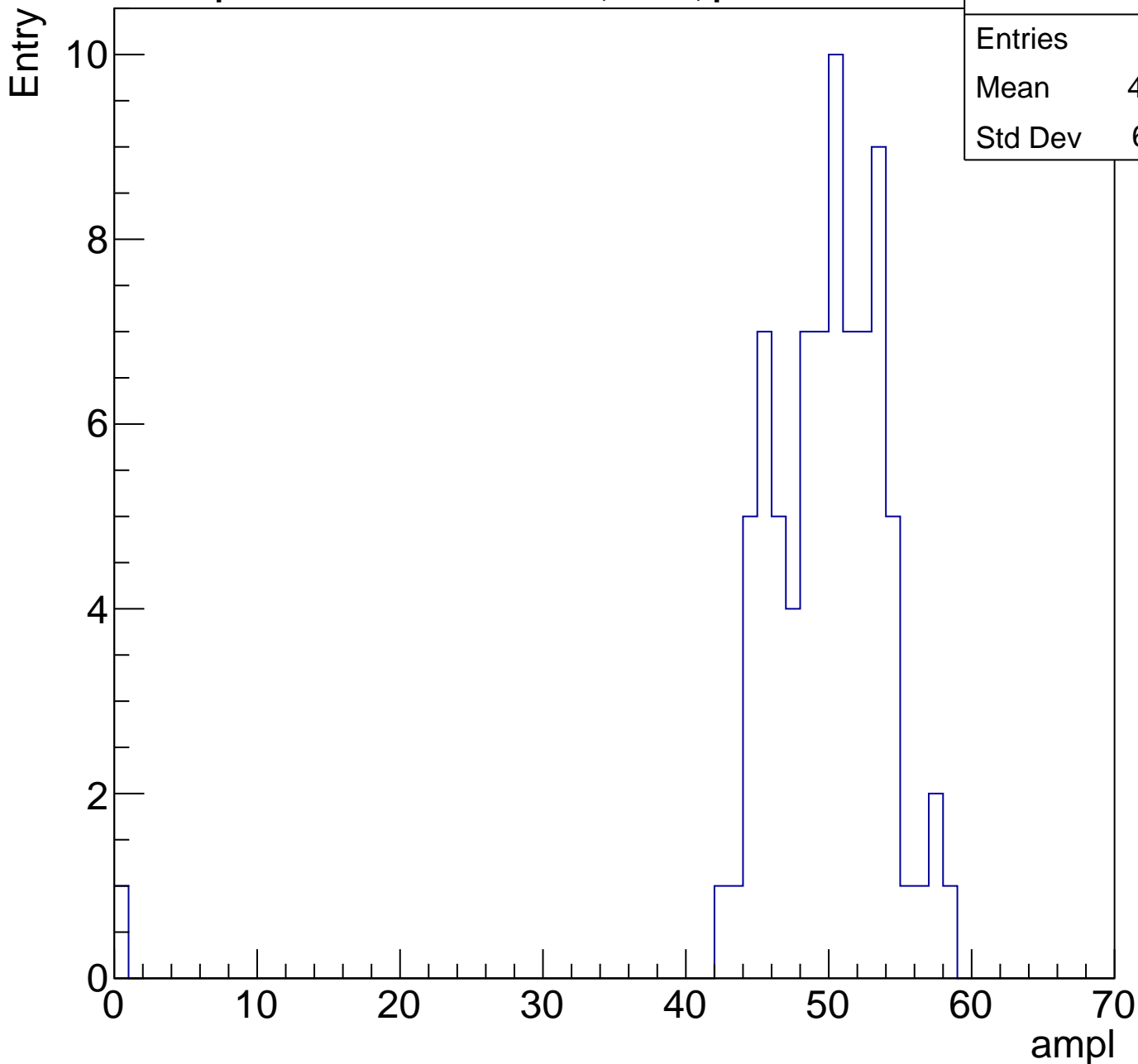
Entry



B1L103S, U21-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	48.99
Std Dev	6.531

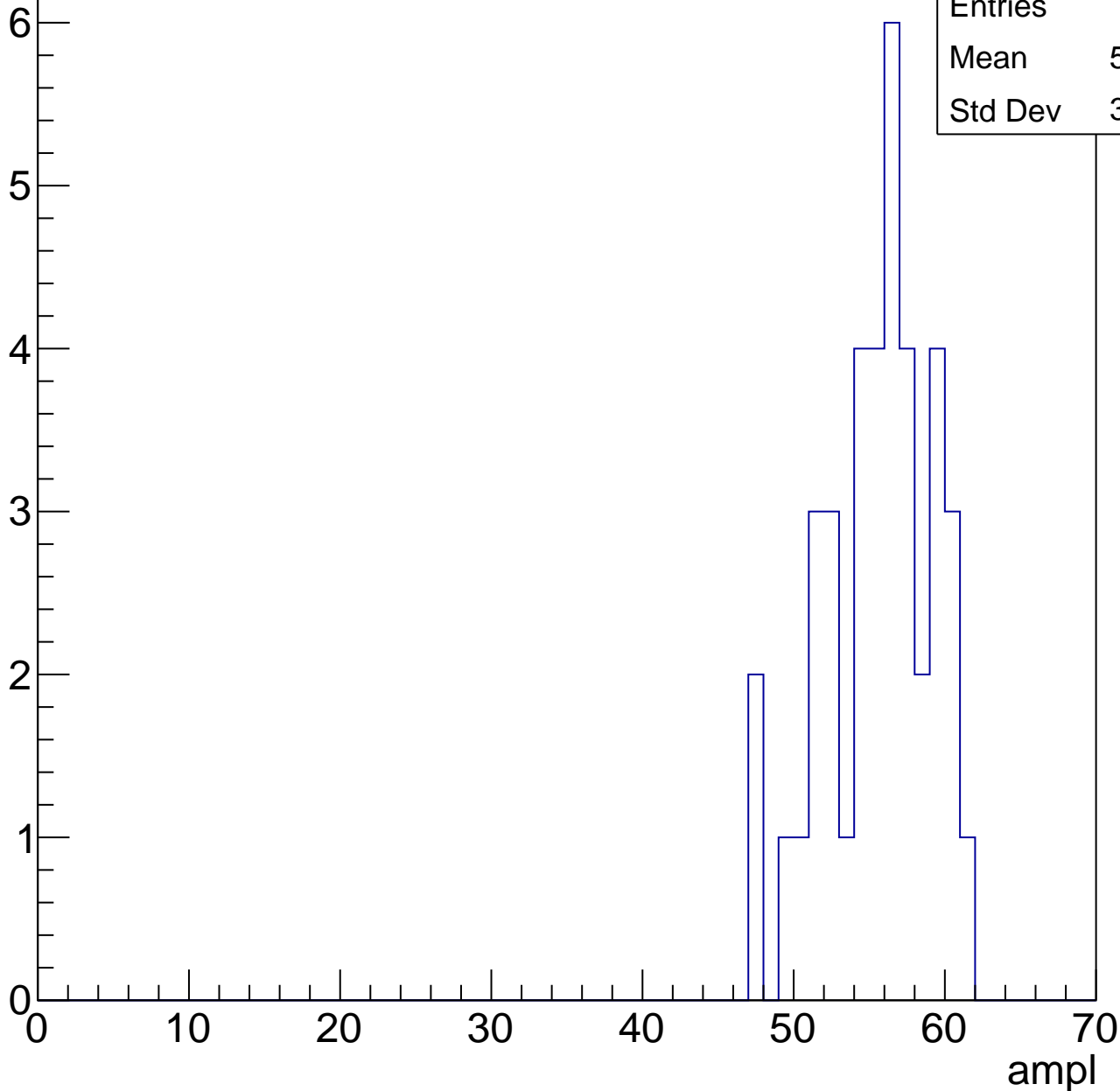


B1L103S, U21-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	55.08
Std Dev	3.533

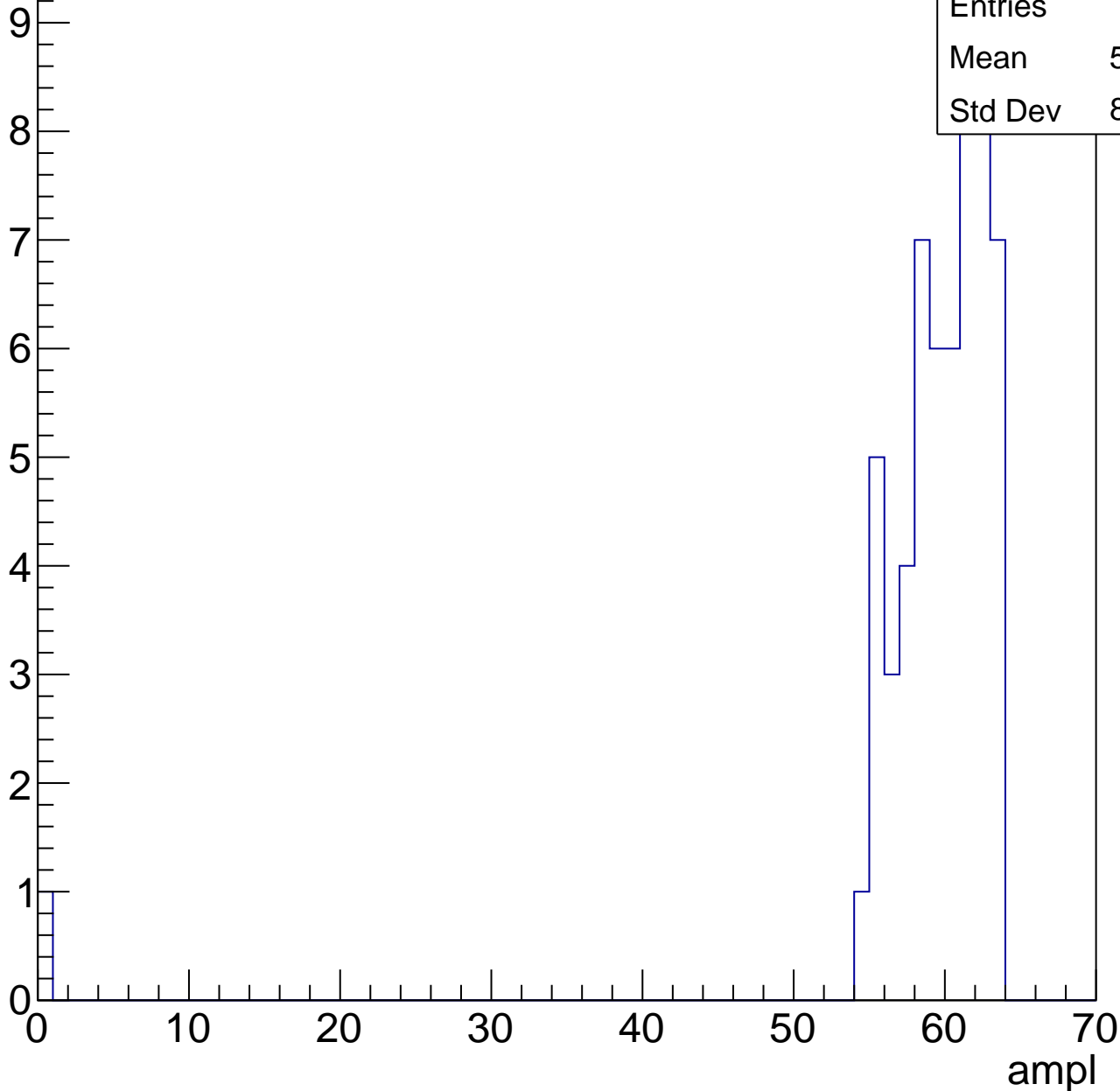


B1L103S, U21-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.46
Std Dev	8.219



B1L103S, U21-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

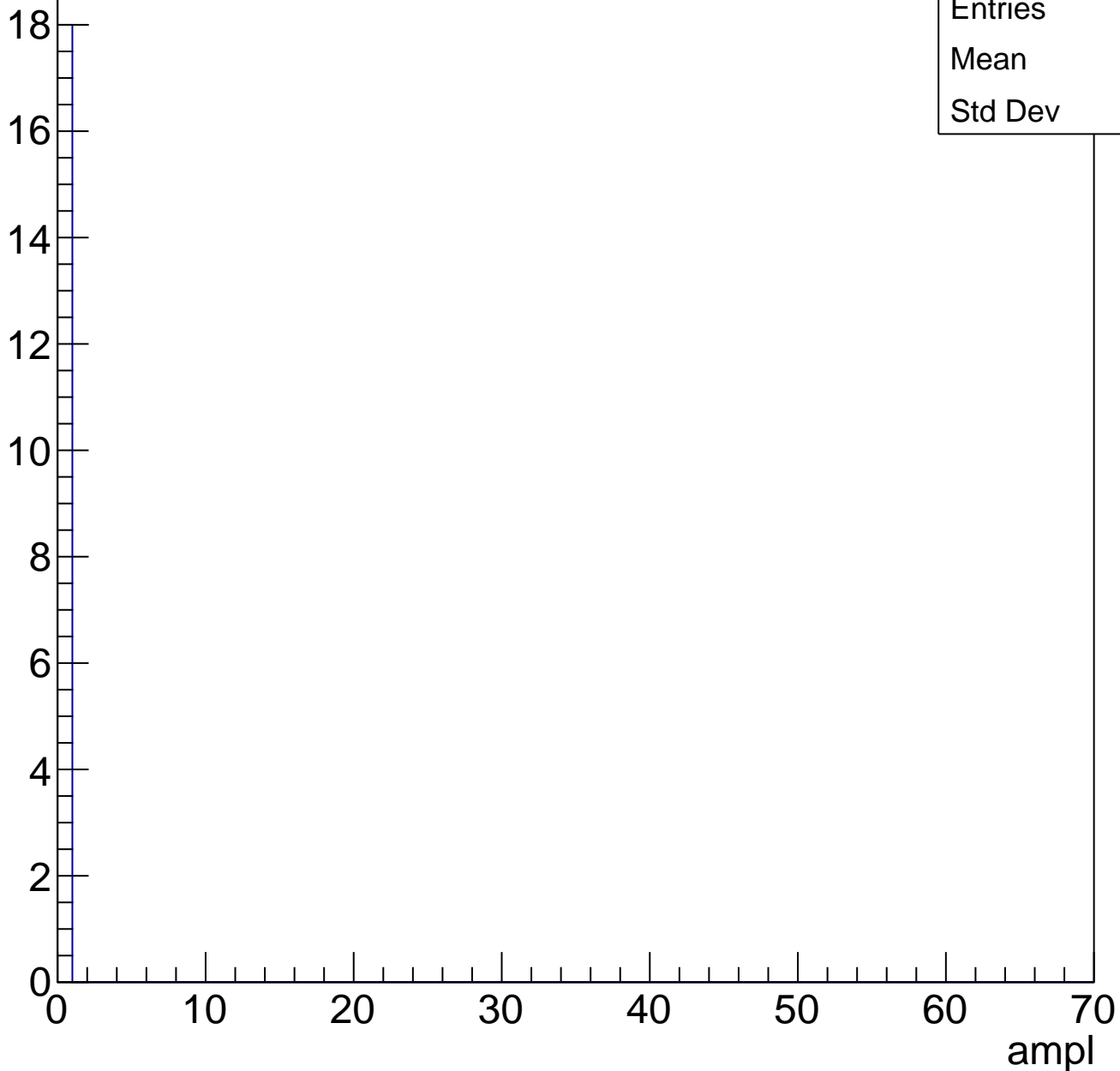


B1L103S, U21-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0

Entry



B1L103S, U21-ch58, adc0

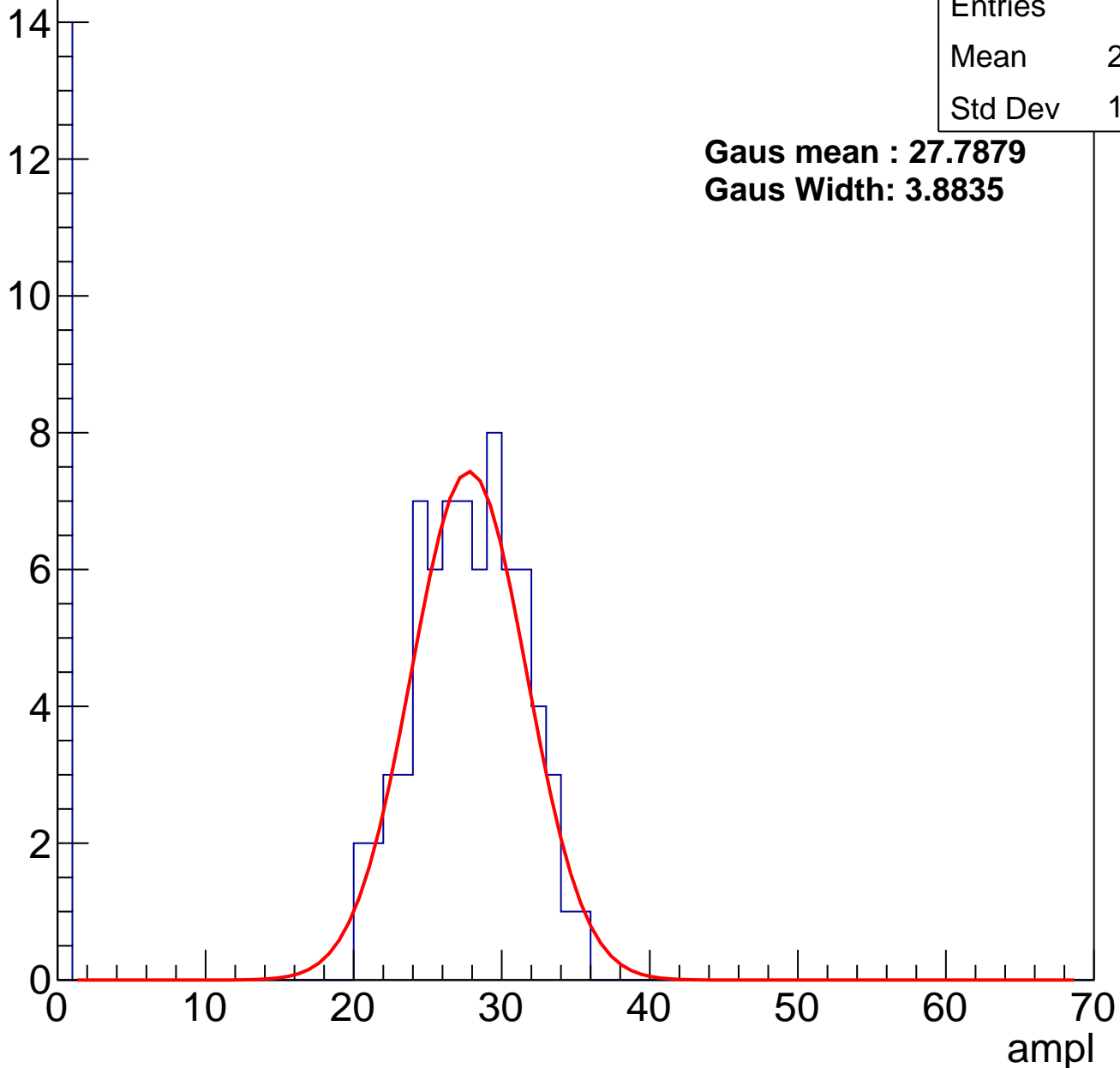
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	22.88
Std Dev	10.59

Gaus mean : 27.7879

Gaus Width: 3.8835

Entry



B1L103S, U21-ch58, adc1

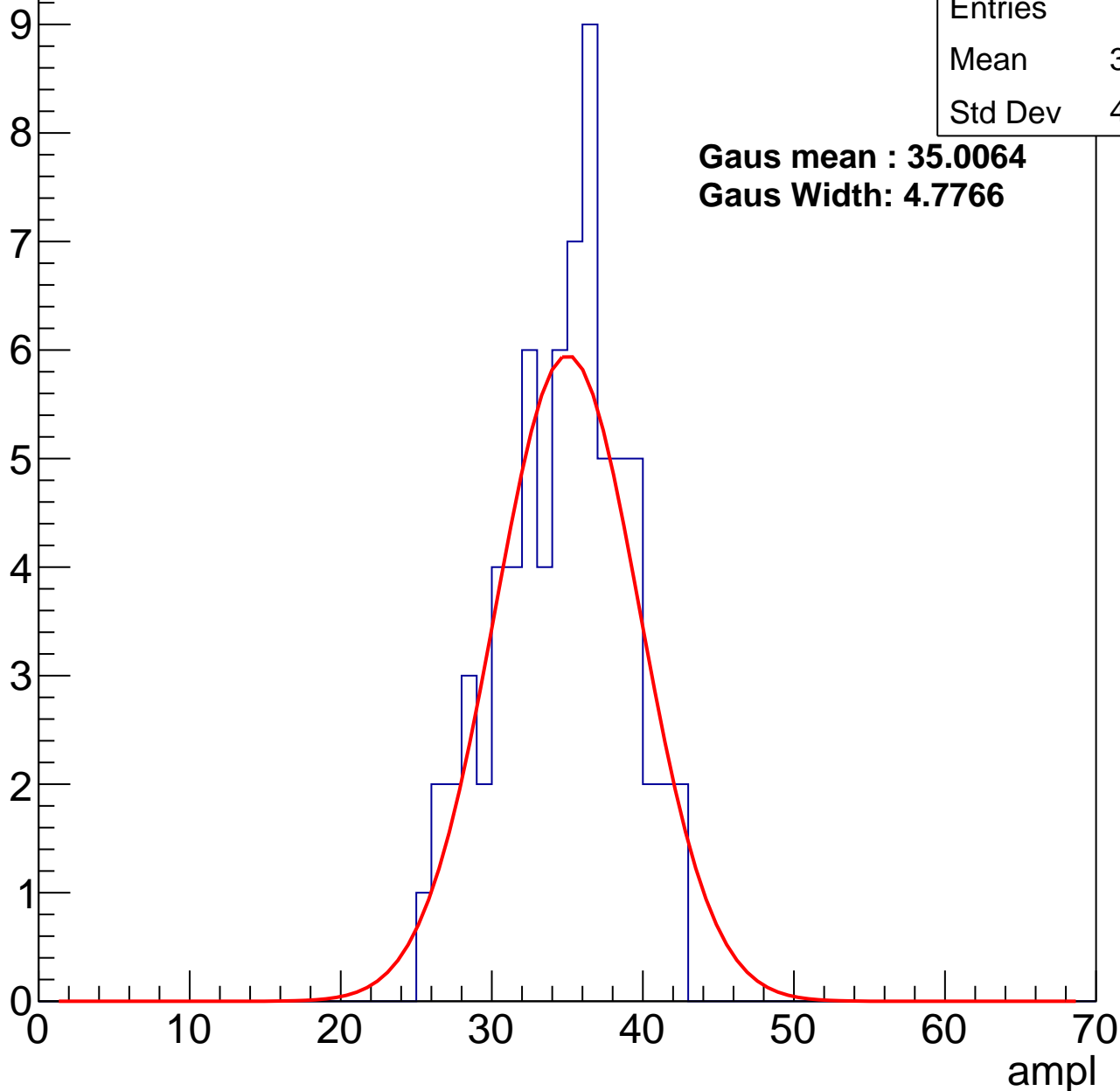
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.23
Std Dev	4.077

Gaus mean : 35.0064

Gaus Width: 4.7766



B1L103S, U21-ch58, adc2

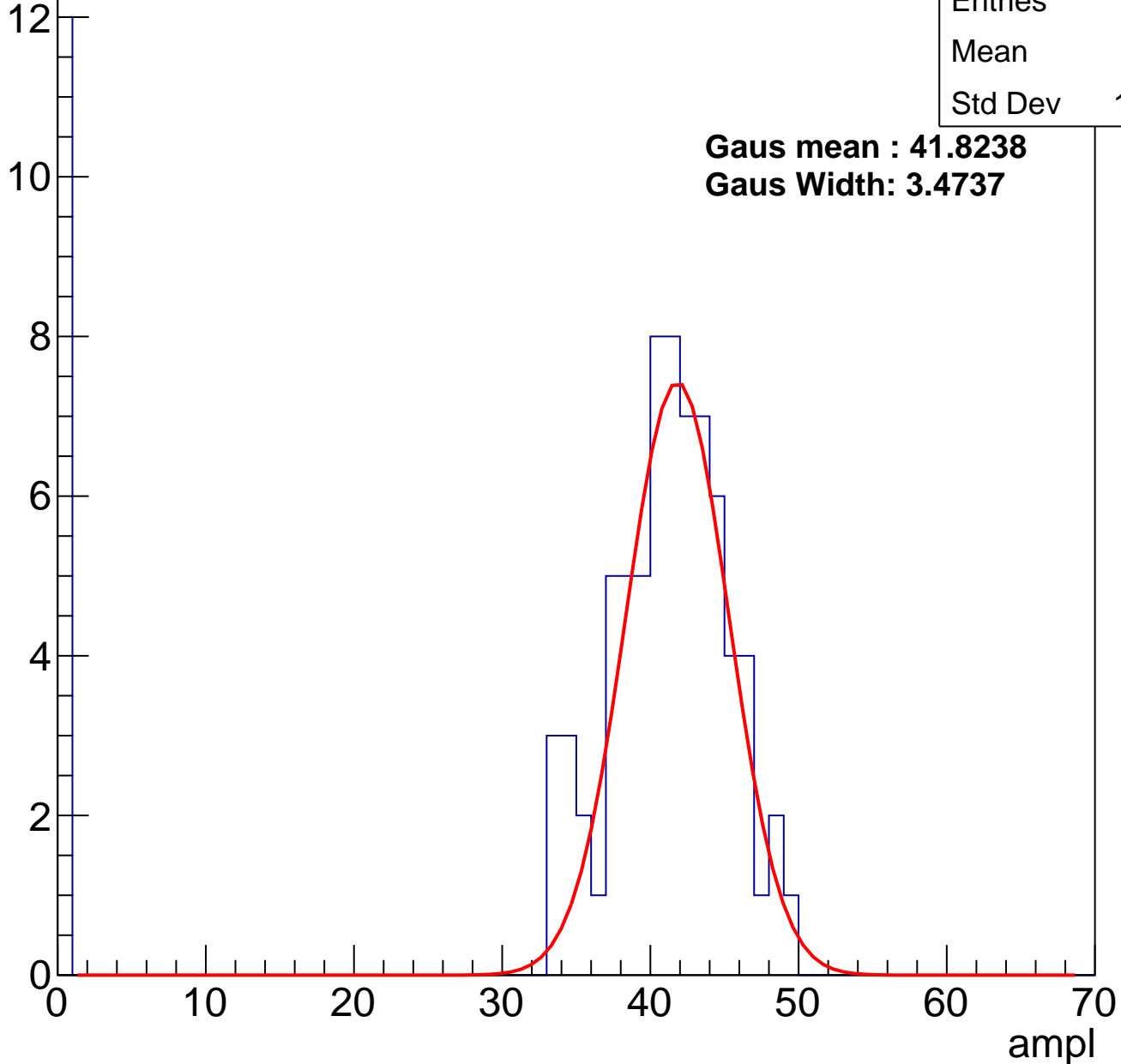
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	35
Std Dev	14.71

Gaus mean : 41.8238

Gaus Width: 3.4737

Entry

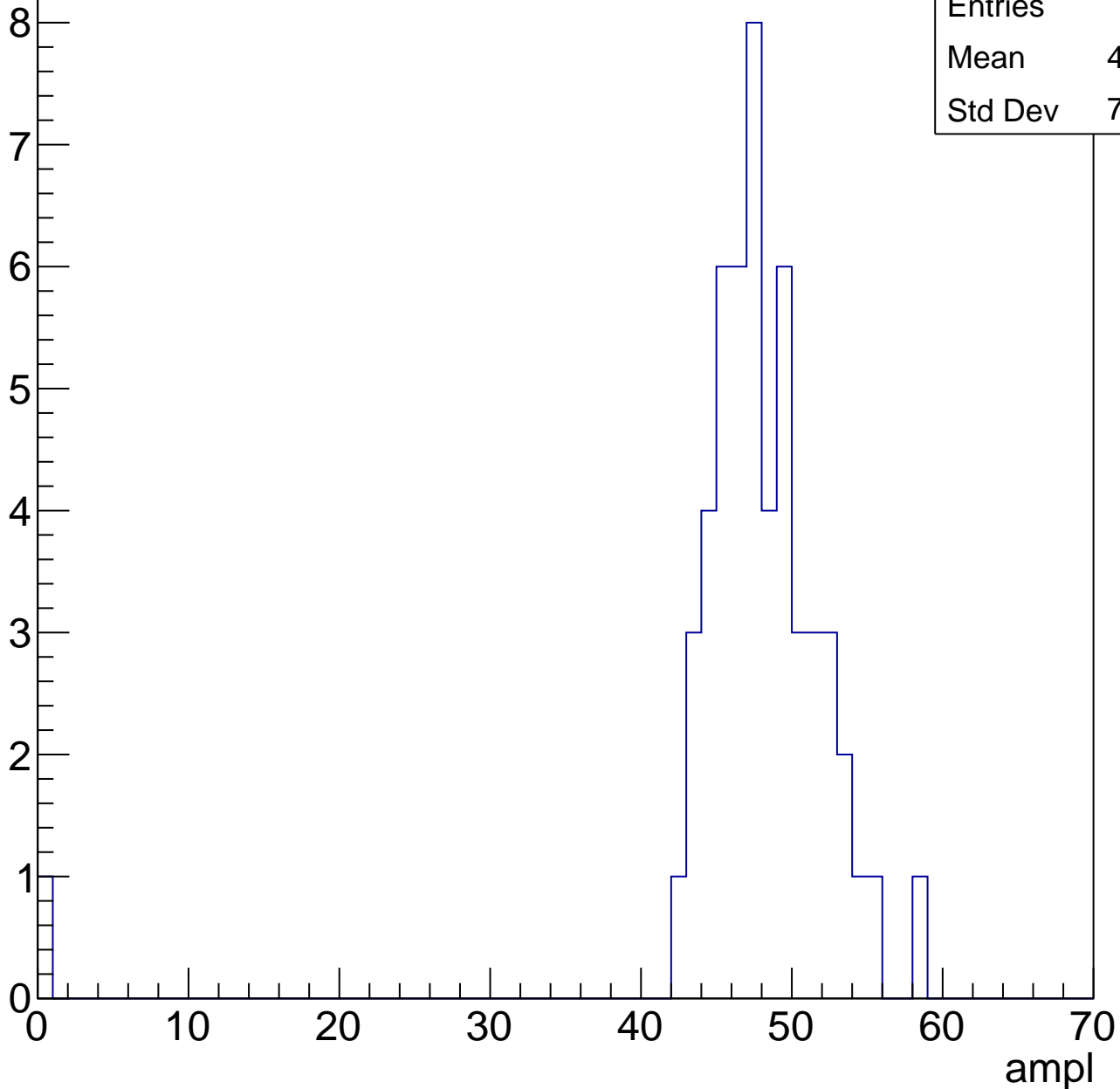


B1L103S, U21-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

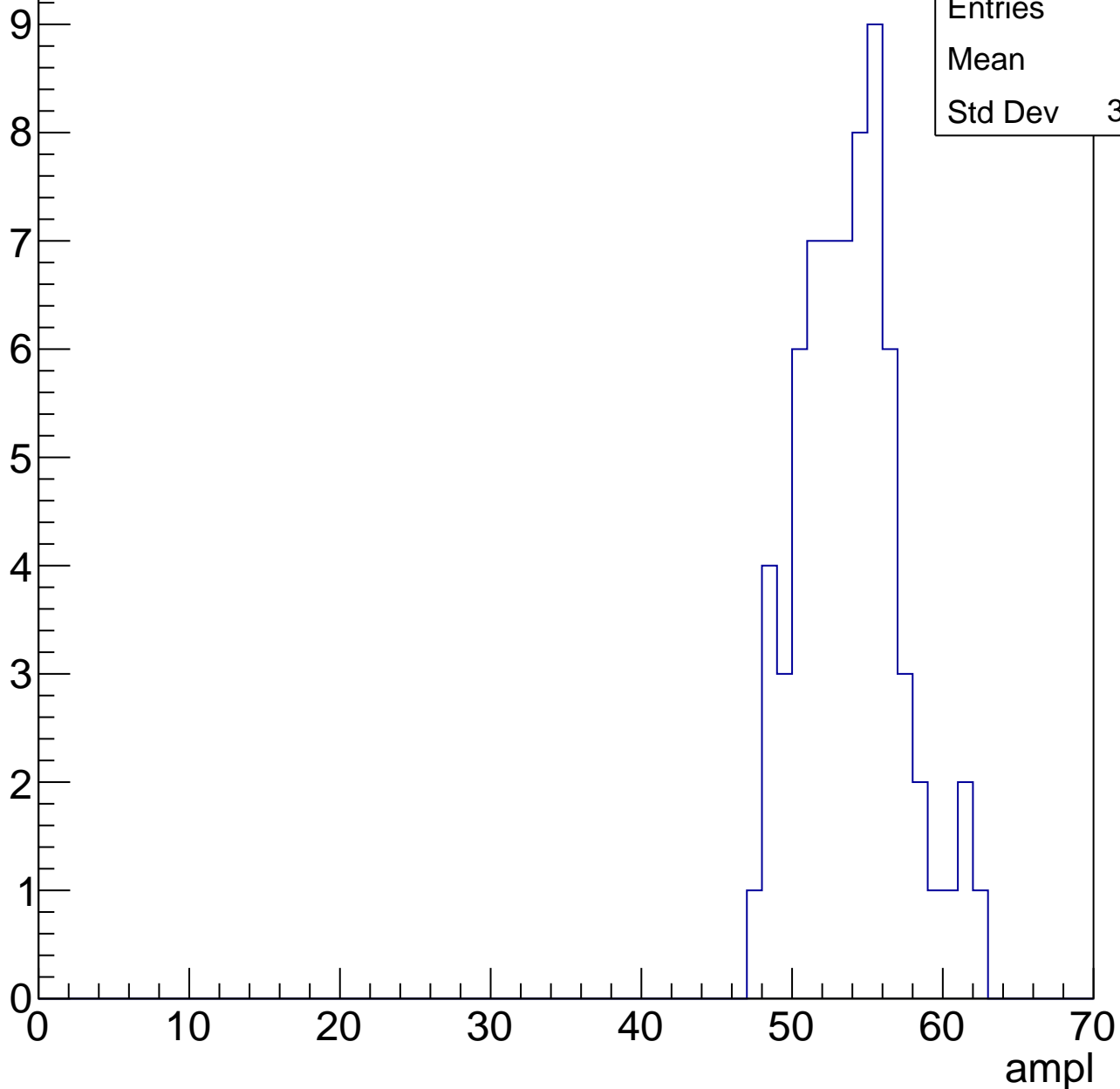
Entries	53
Mean	46.92
Std Dev	7.309



B1L103S, U21-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

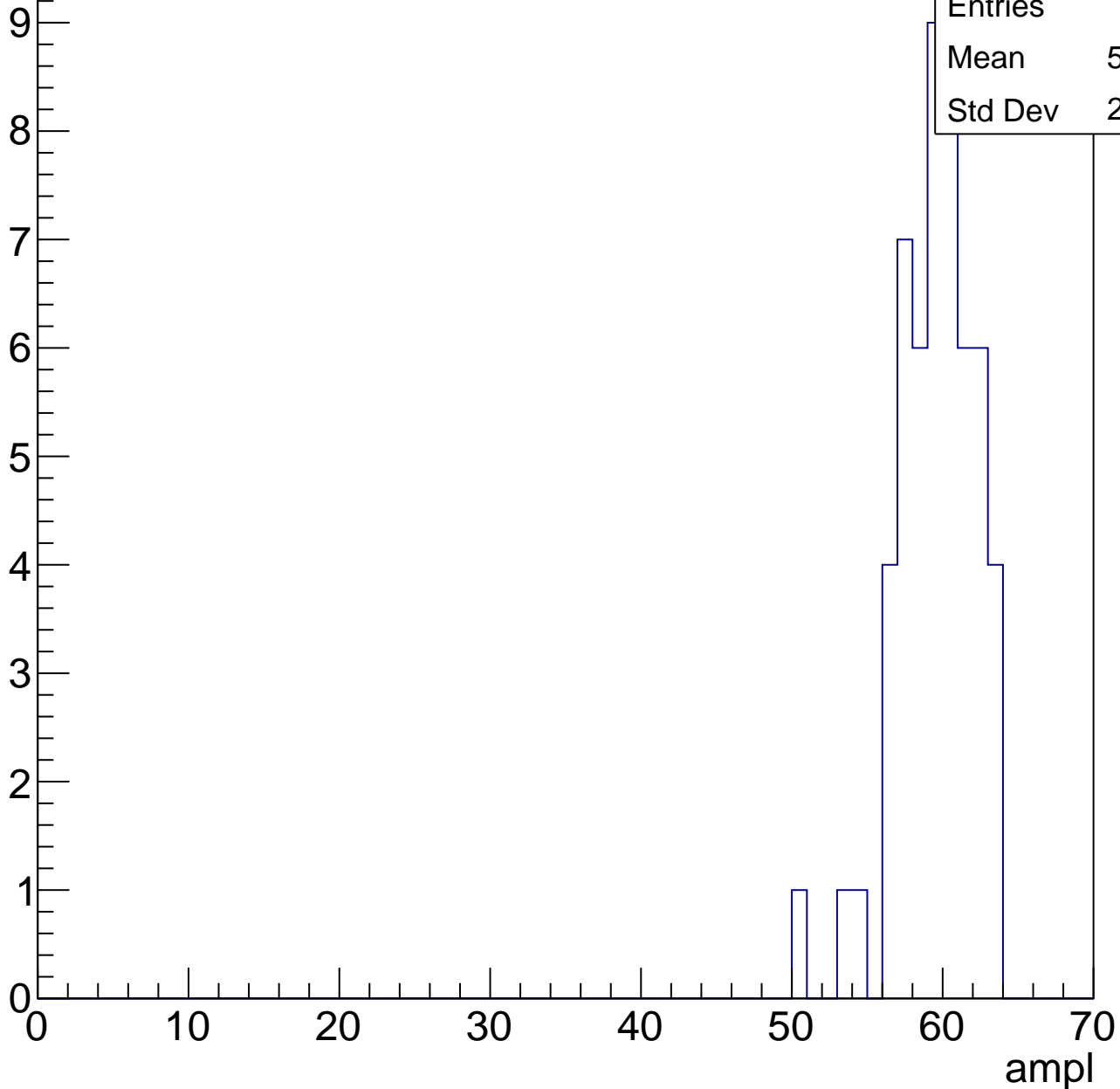


B1L103S, U21-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	59.04
Std Dev	2.613

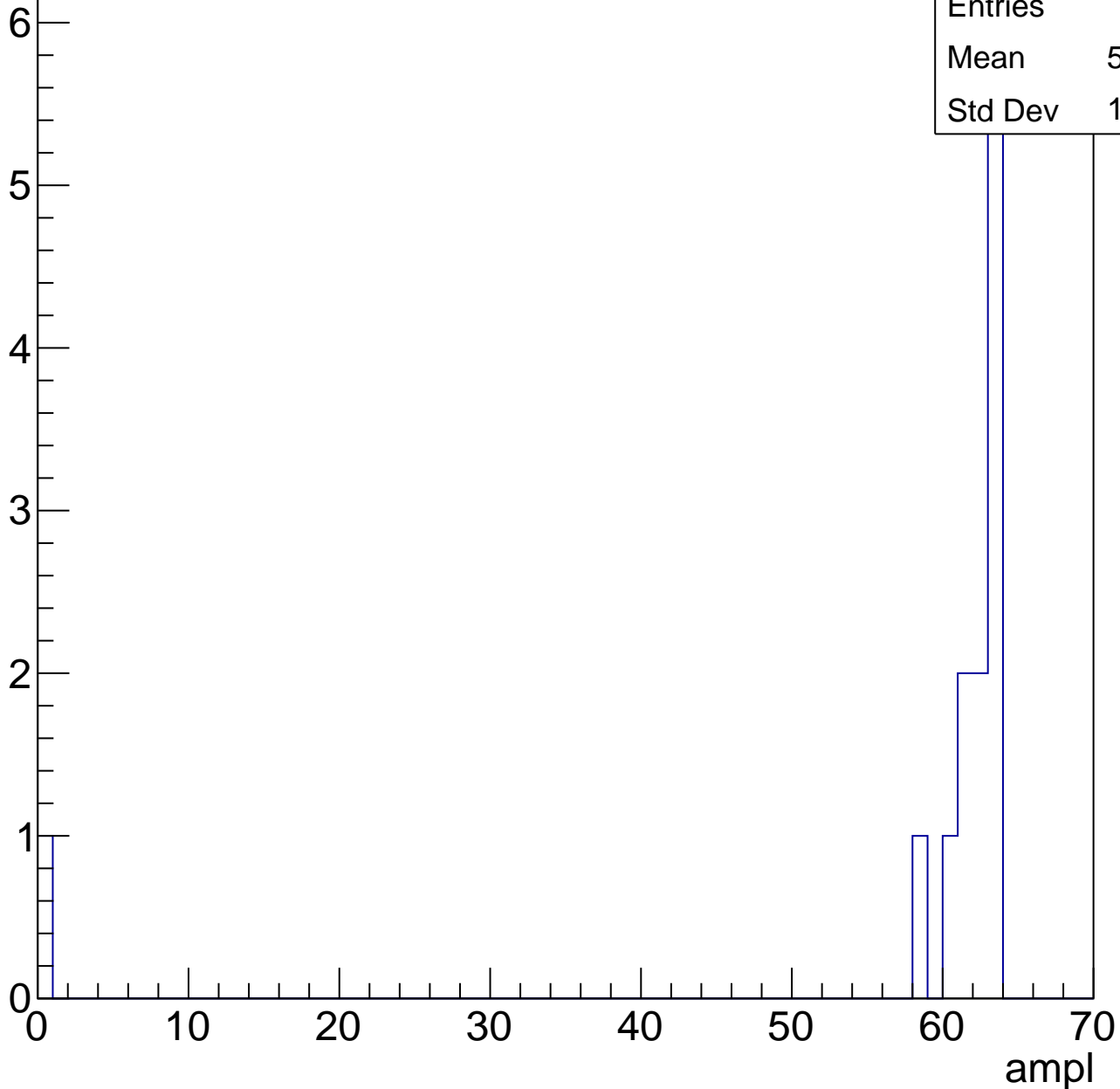


B1L103S, U21-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.08
Std Dev	16.54



B1L103S, U21-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U21-ch59, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	22.45
Std Dev	12.26

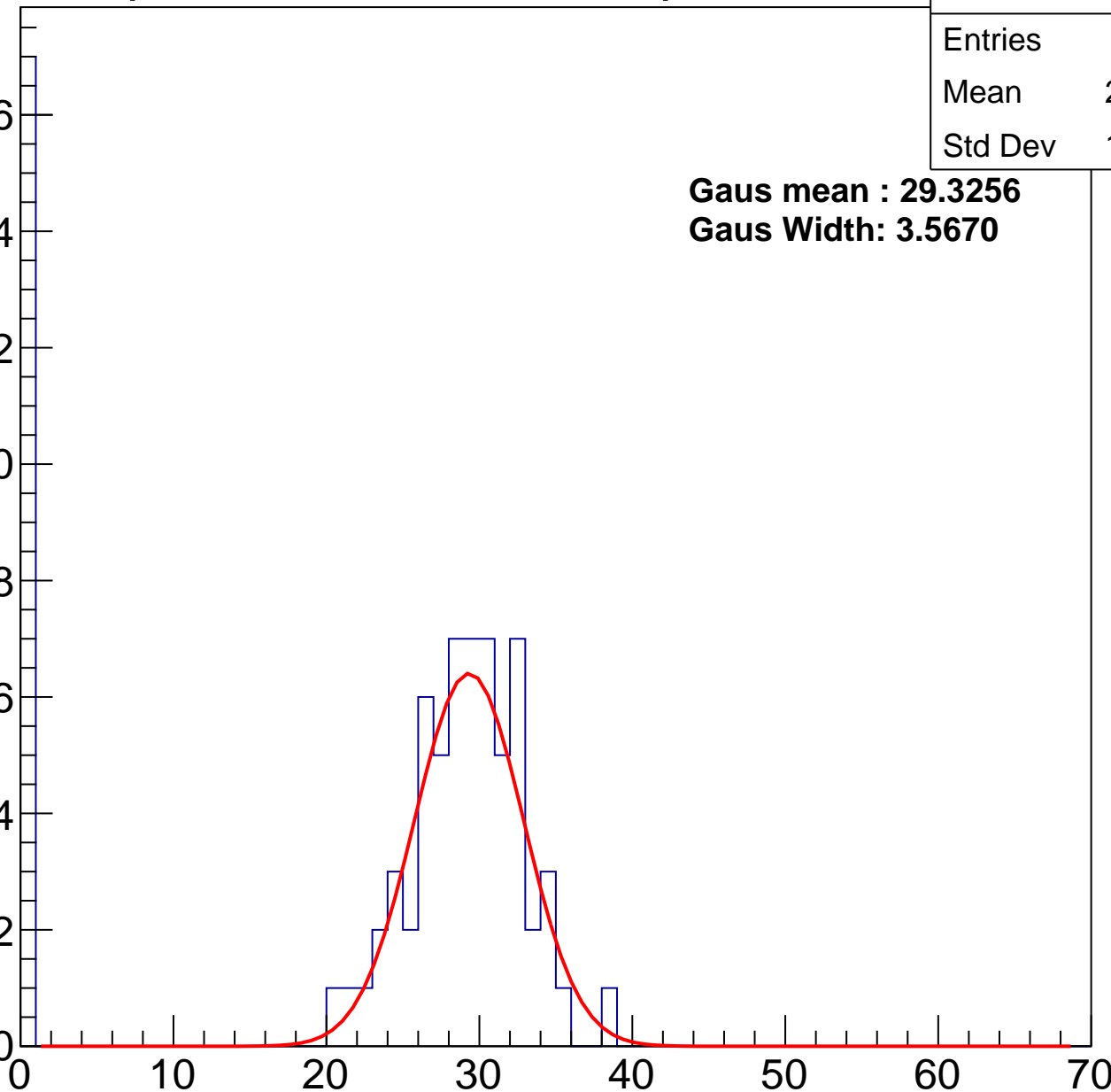
Gaus mean : 29.3256

Gaus Width: 3.5670

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch59, adc1

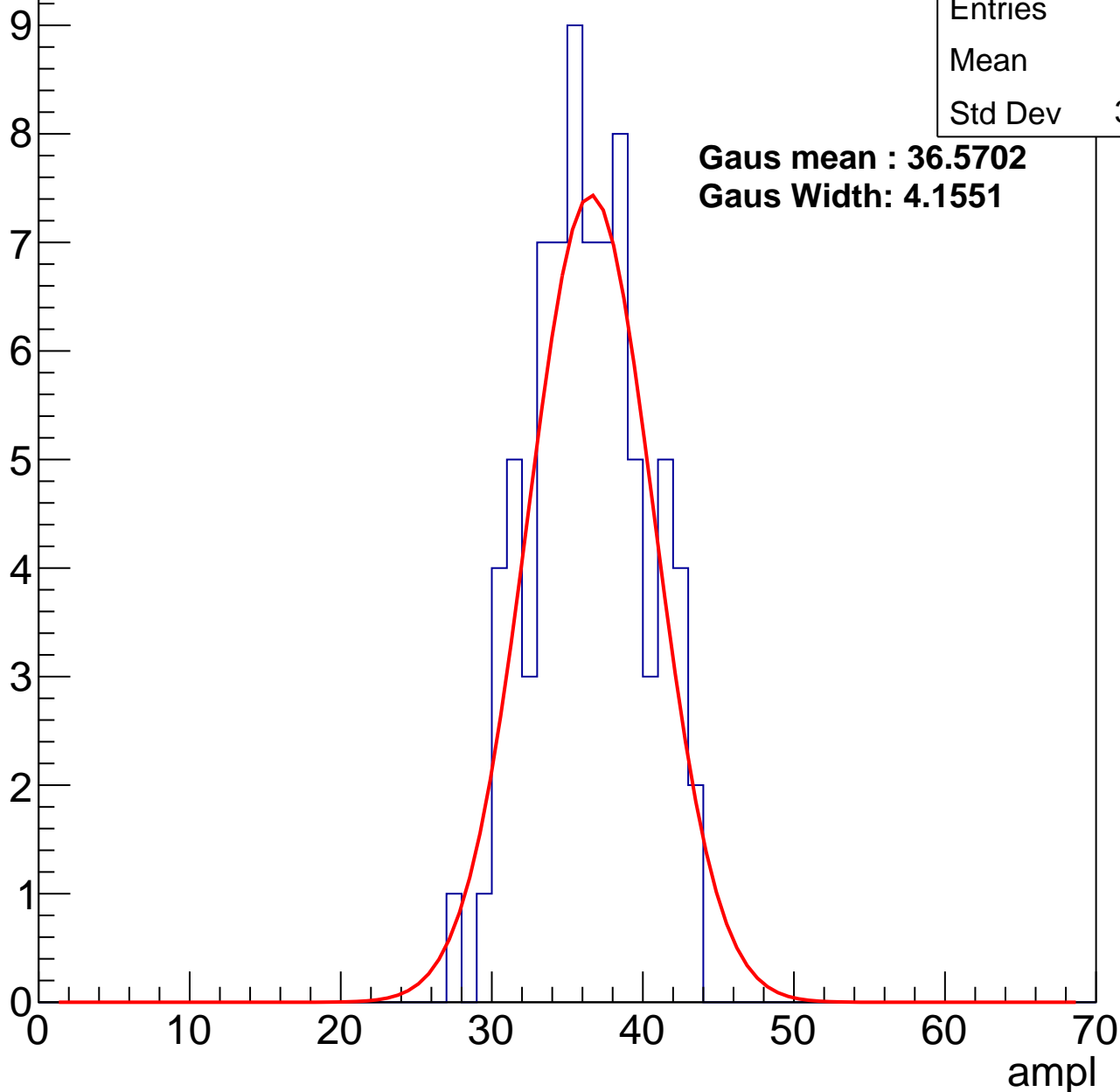
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	35.9
Std Dev	3.661

Gaus mean : 36.5702

Gaus Width: 4.1551

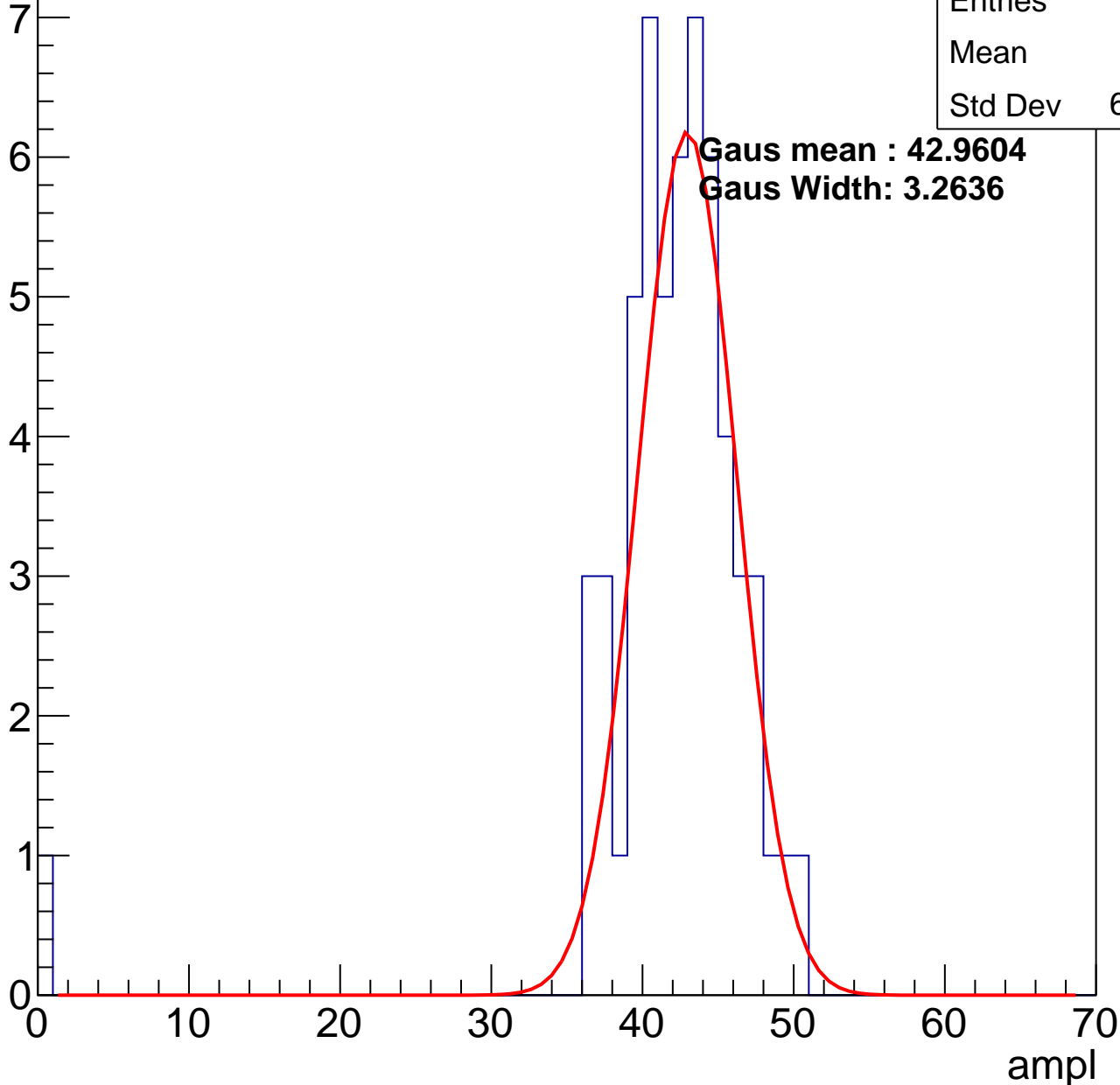


B1L103S, U21-ch59, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	41.4
Std Dev	6.434



B1L103S, U21-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	41.54
Std Dev	18.02

Entry

12

10

8

6

4

2

0

0

10

20

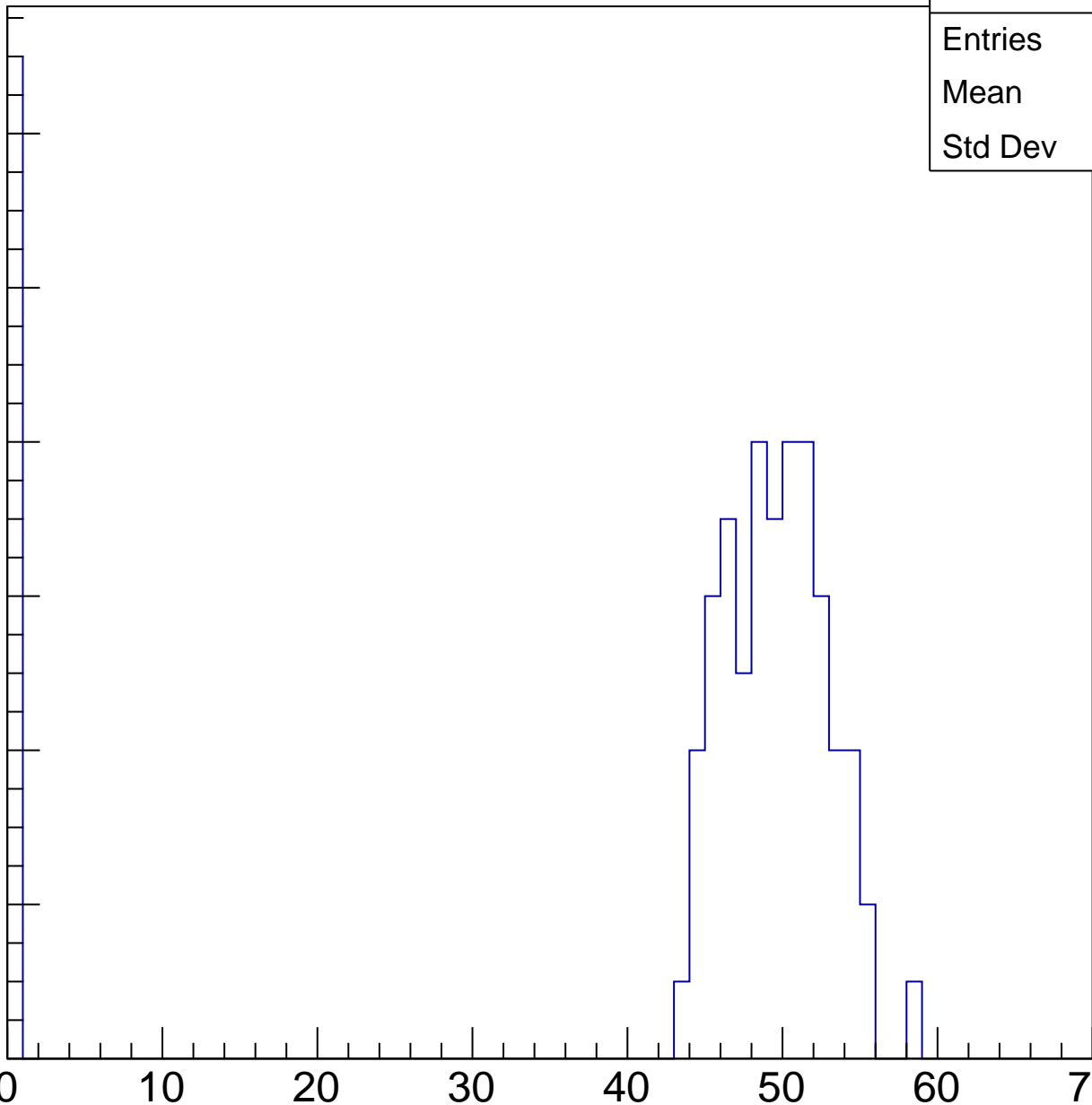
30

40

50

60

ampl

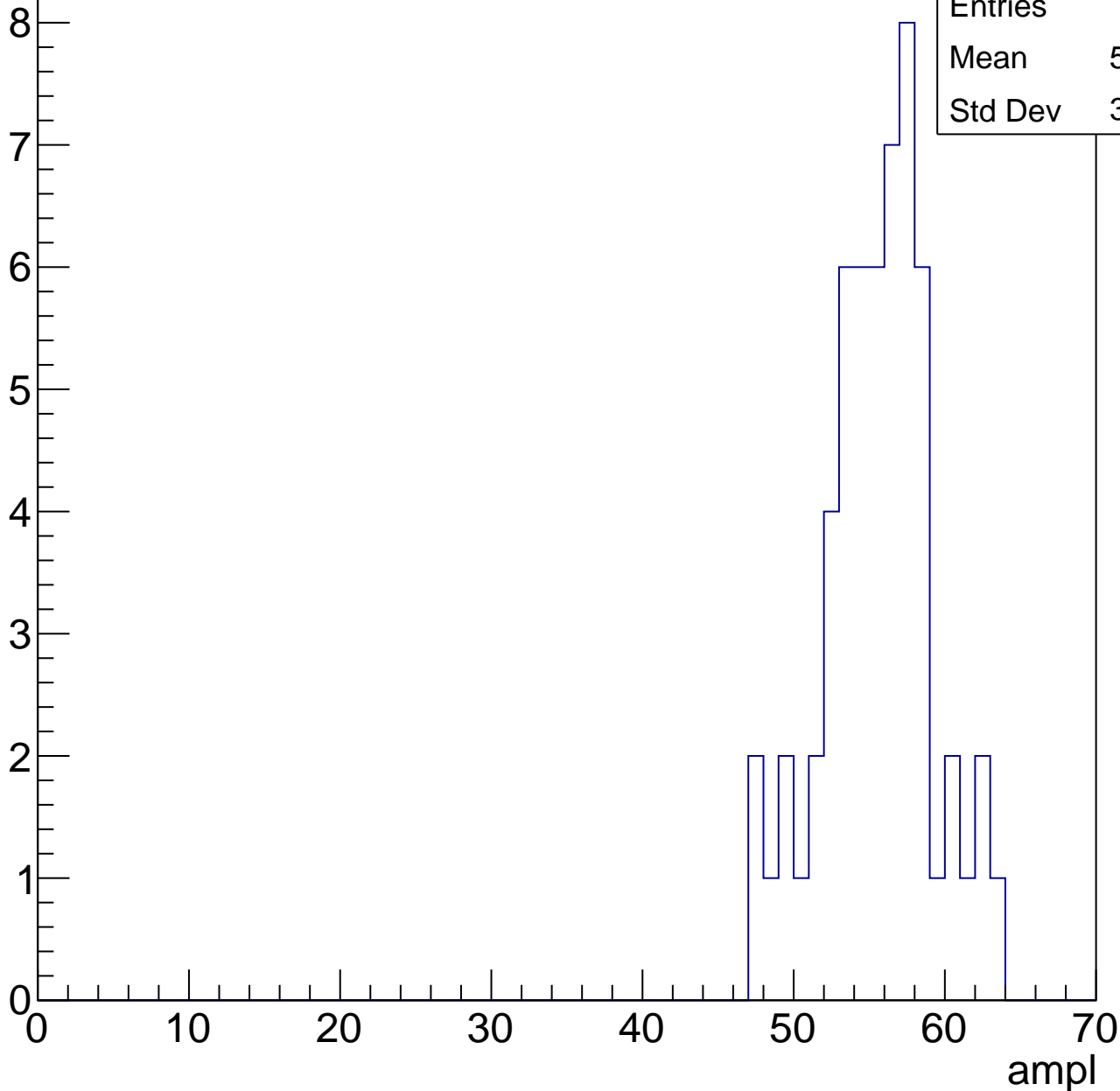


B1L103S, U21-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

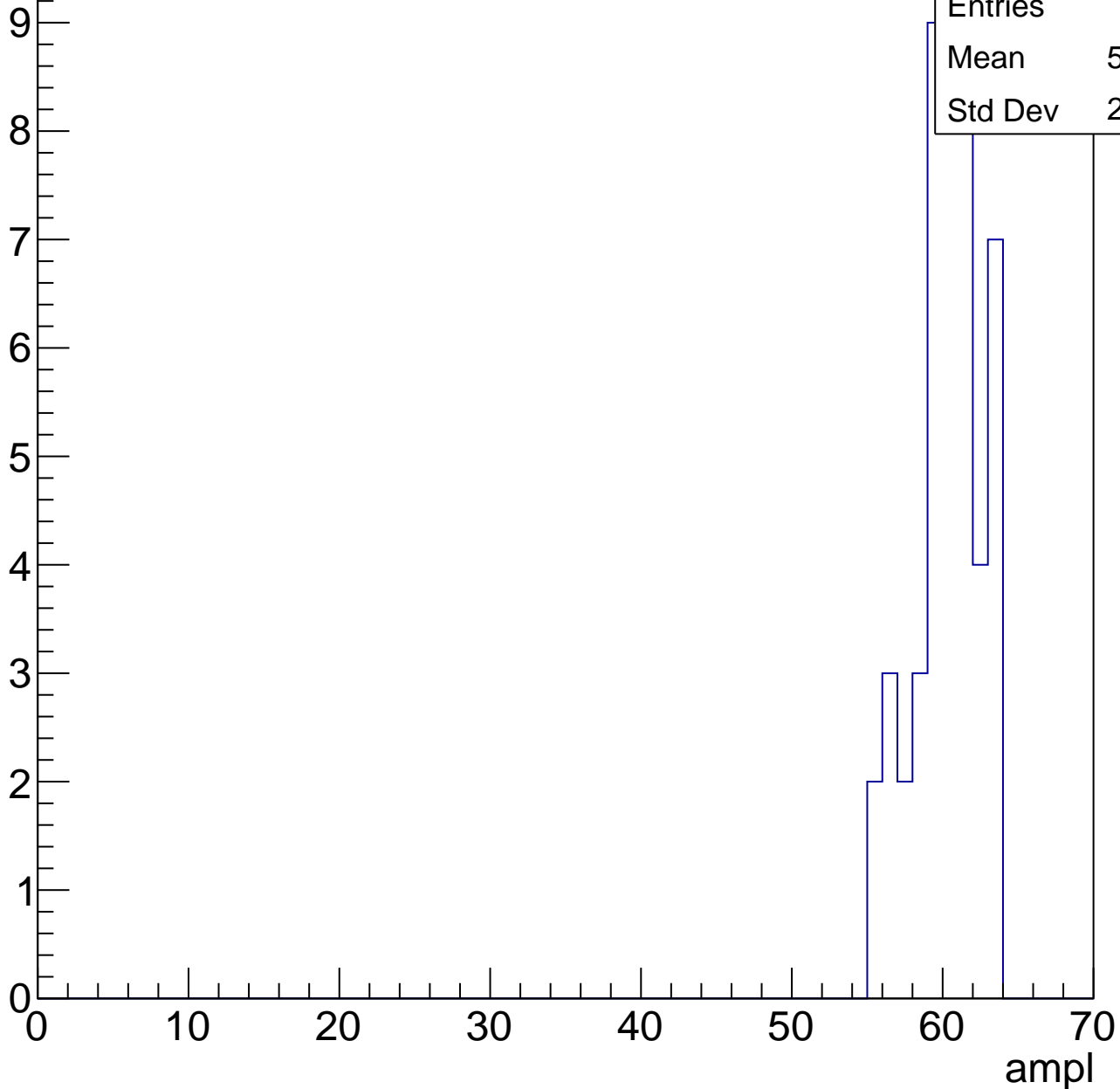
Entries	58
Mean	55.09
Std Dev	3.544



B1L103S, U21-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

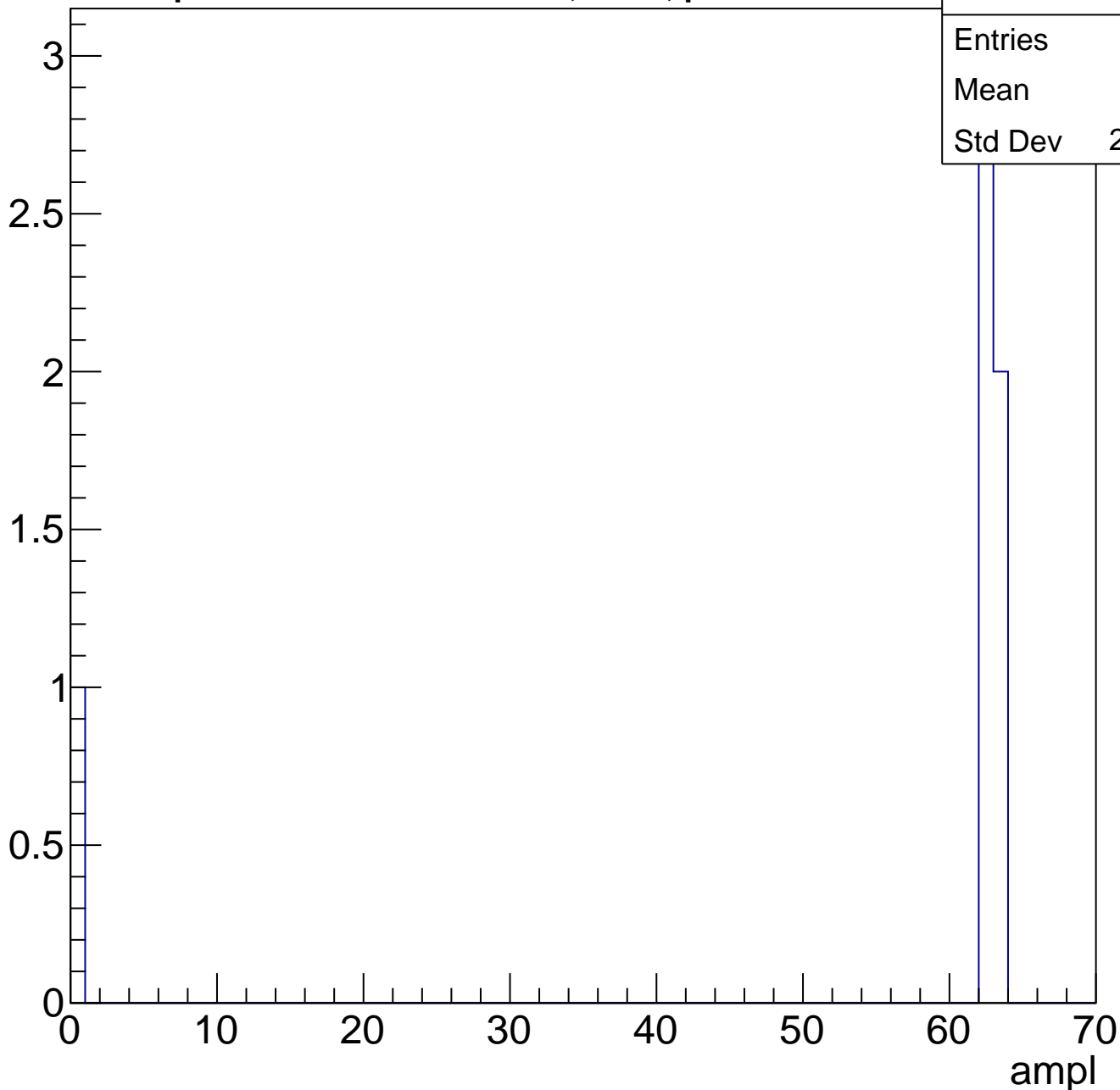


Entries	47
Mean	59.89
Std Dev	2.185

B1L103S, U21-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U21-ch60, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	21.99
Std Dev	11.1

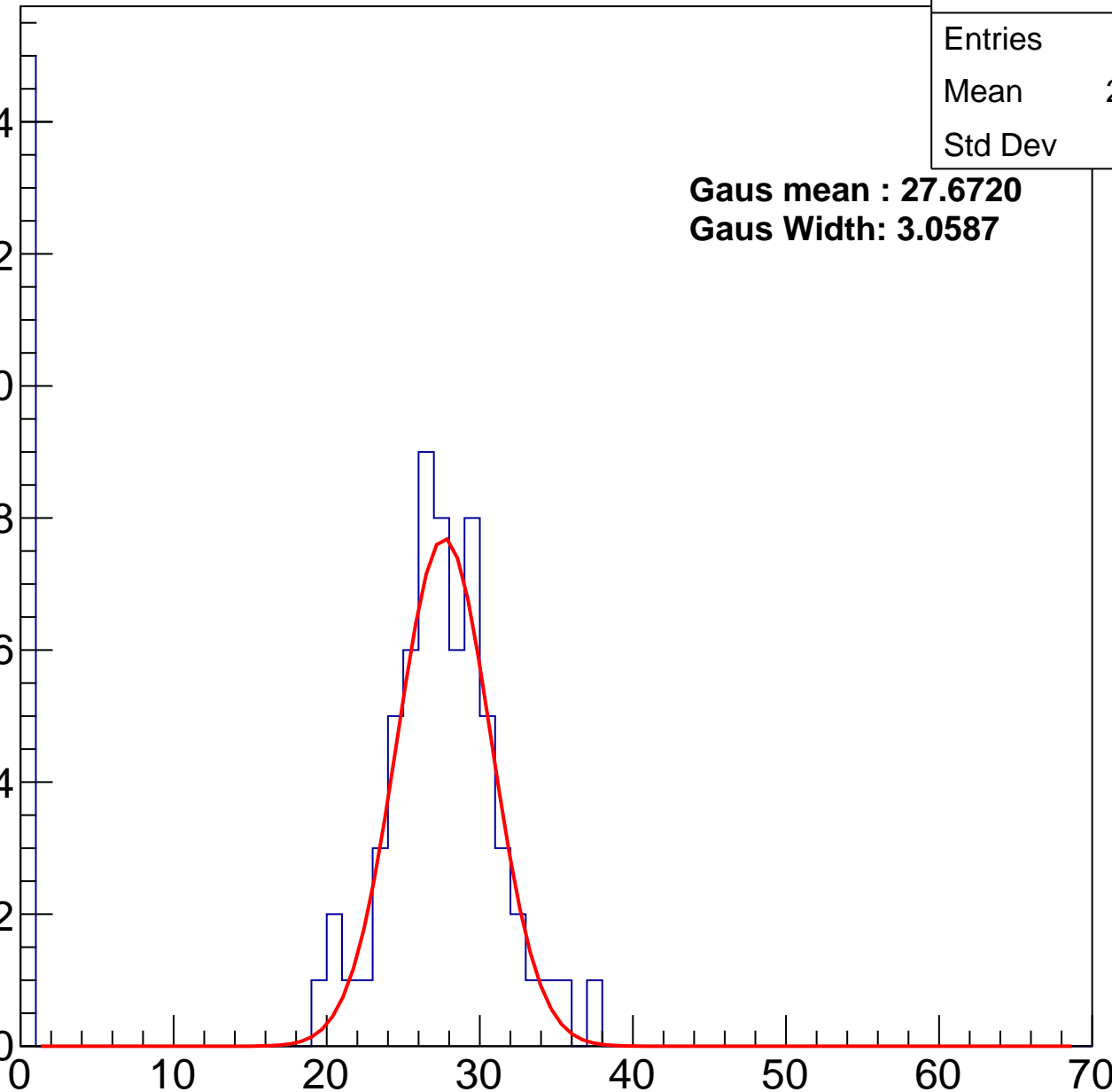
Gaus mean : 27.6720

Gaus Width: 3.0587

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch60, adc1

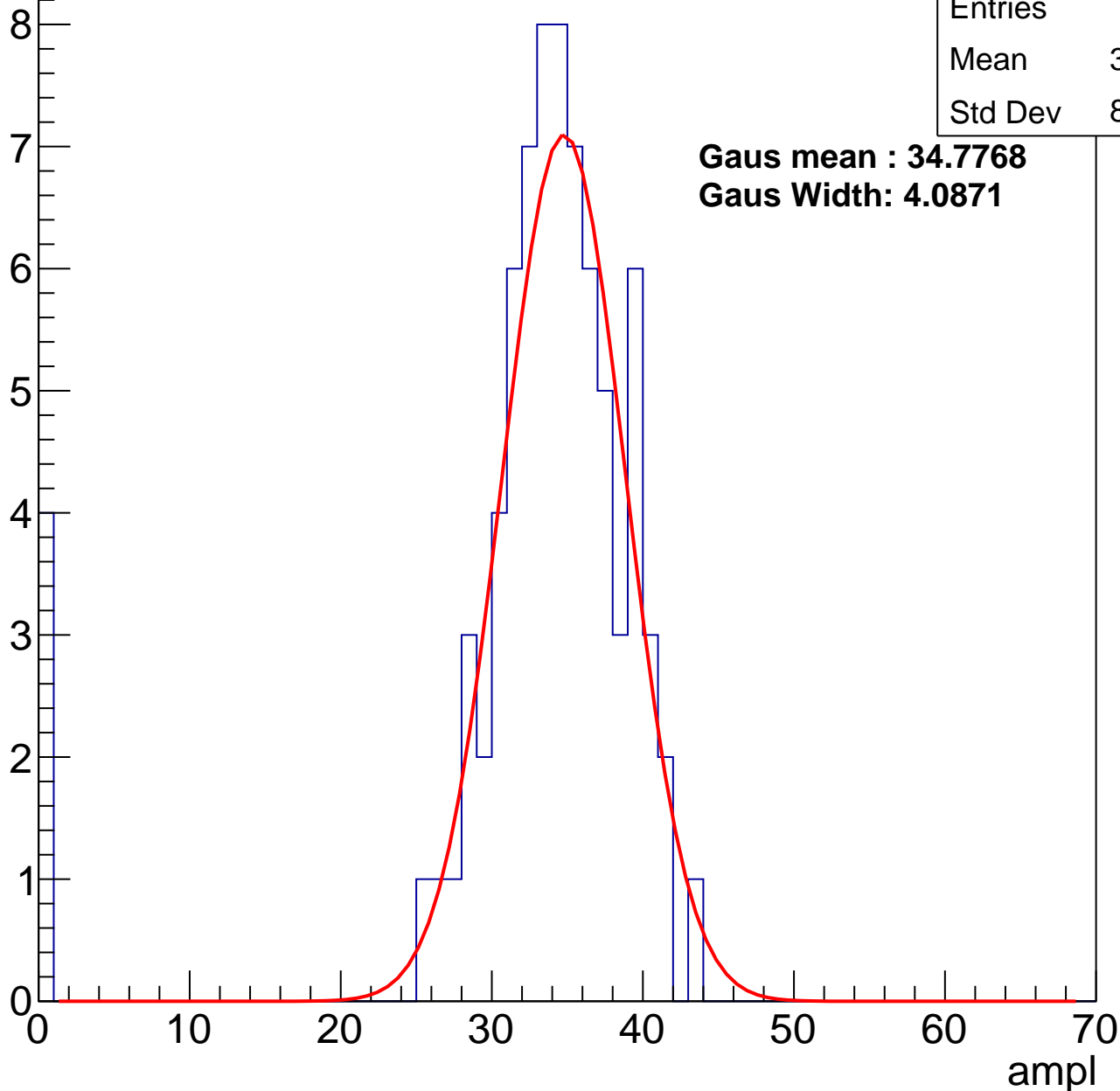
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	32.37
Std Dev	8.385

Gaus mean : 34.7768

Gaus Width: 4.0871



B1L103S, U21-ch60, adc2

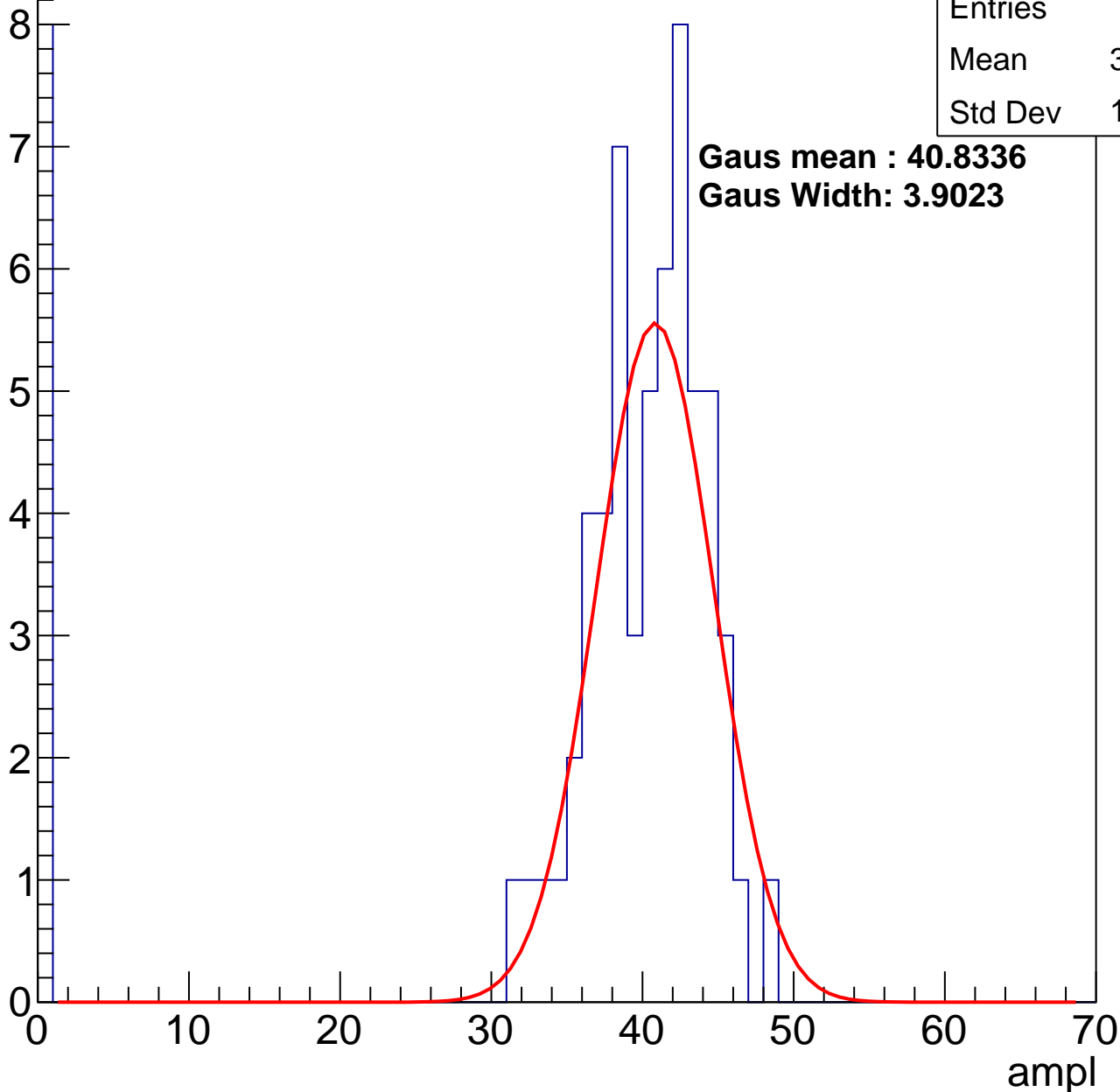
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	35.17
Std Dev	13.49

Gaus mean : 40.8336

Gaus Width: 3.9023



B1L103S, U21-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

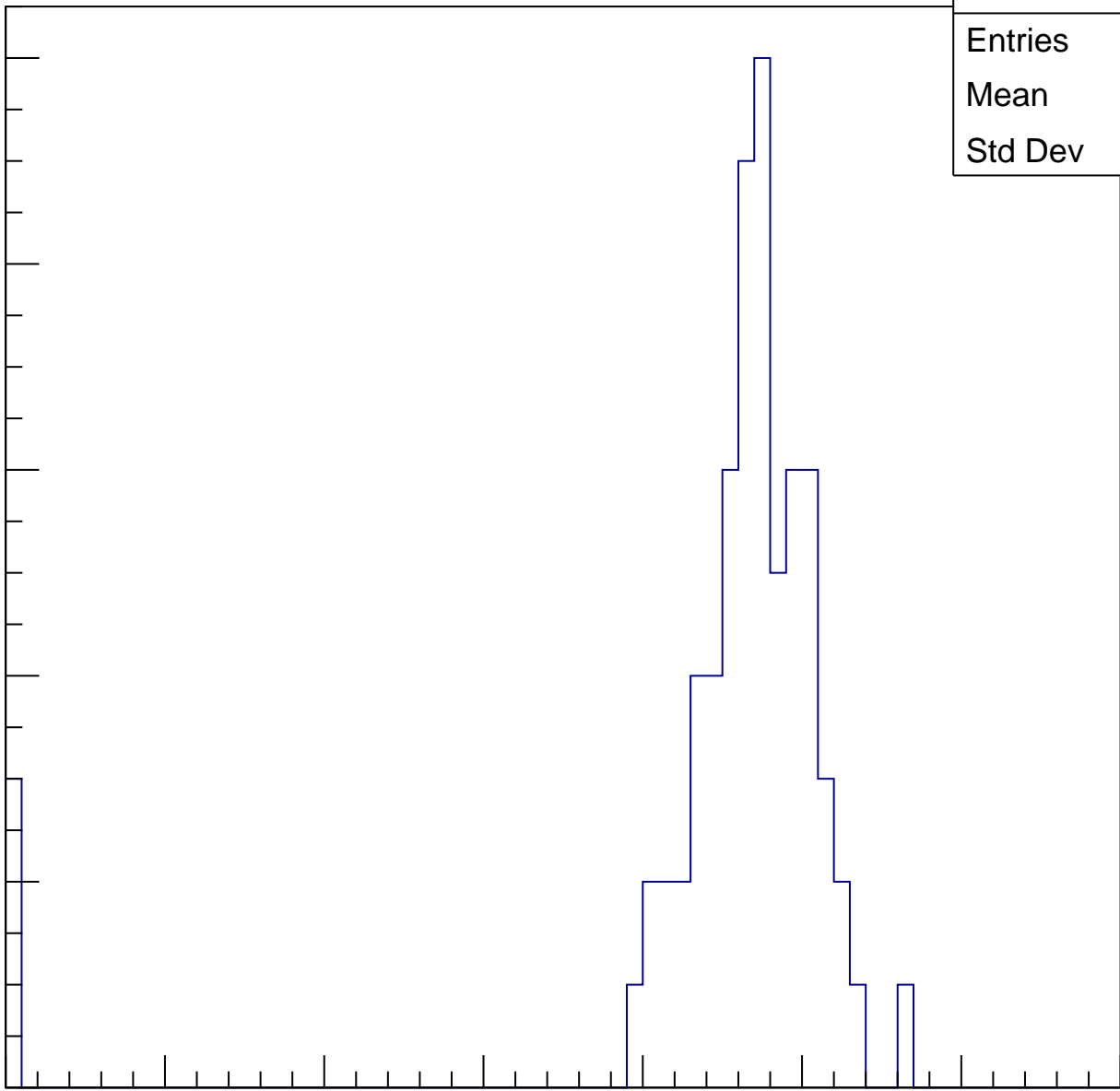
Entries	67
Mean	44.58
Std Dev	10.19

Entry

10
8
6
4
2
0

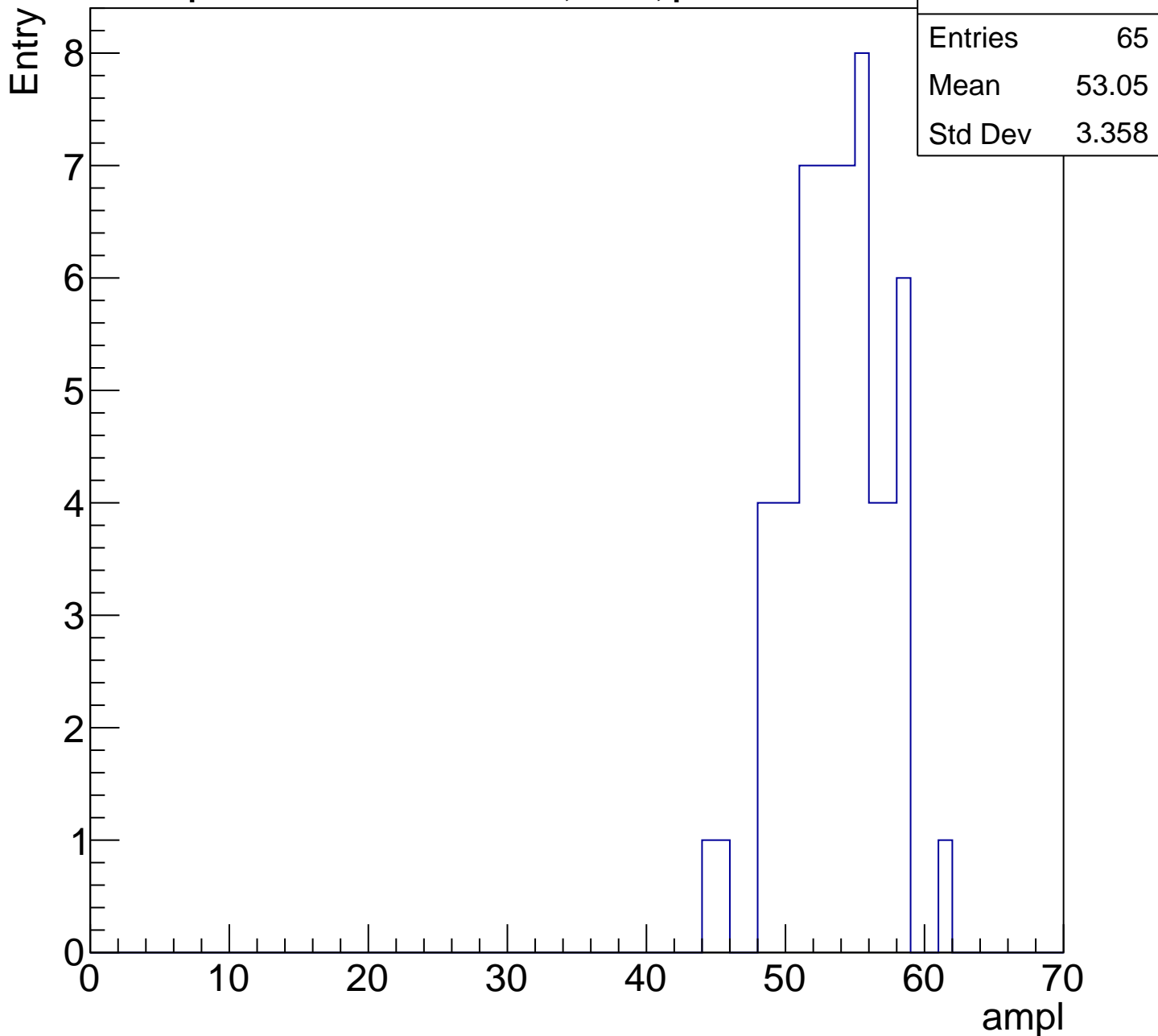
ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch60, adc4

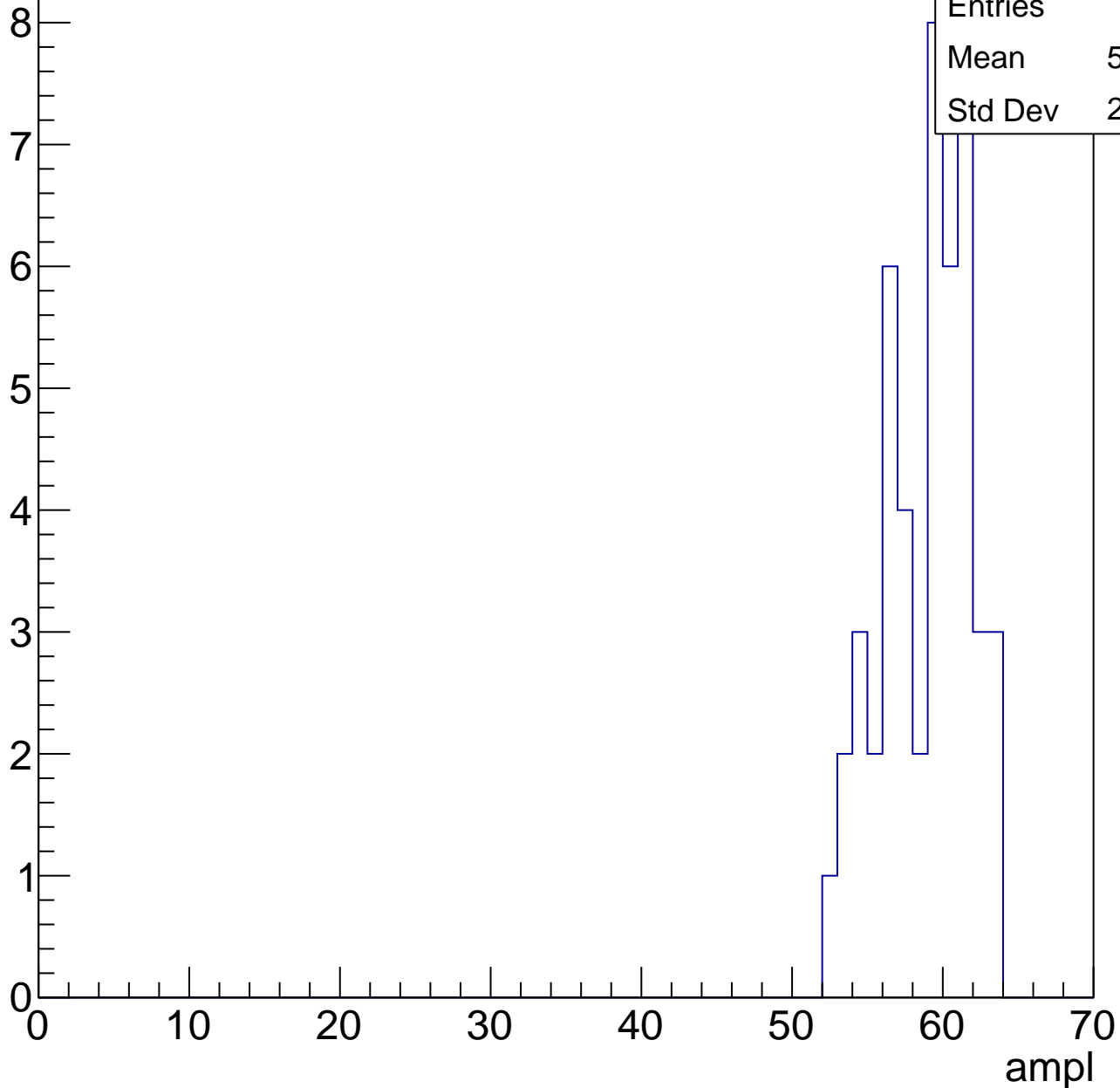
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U21-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

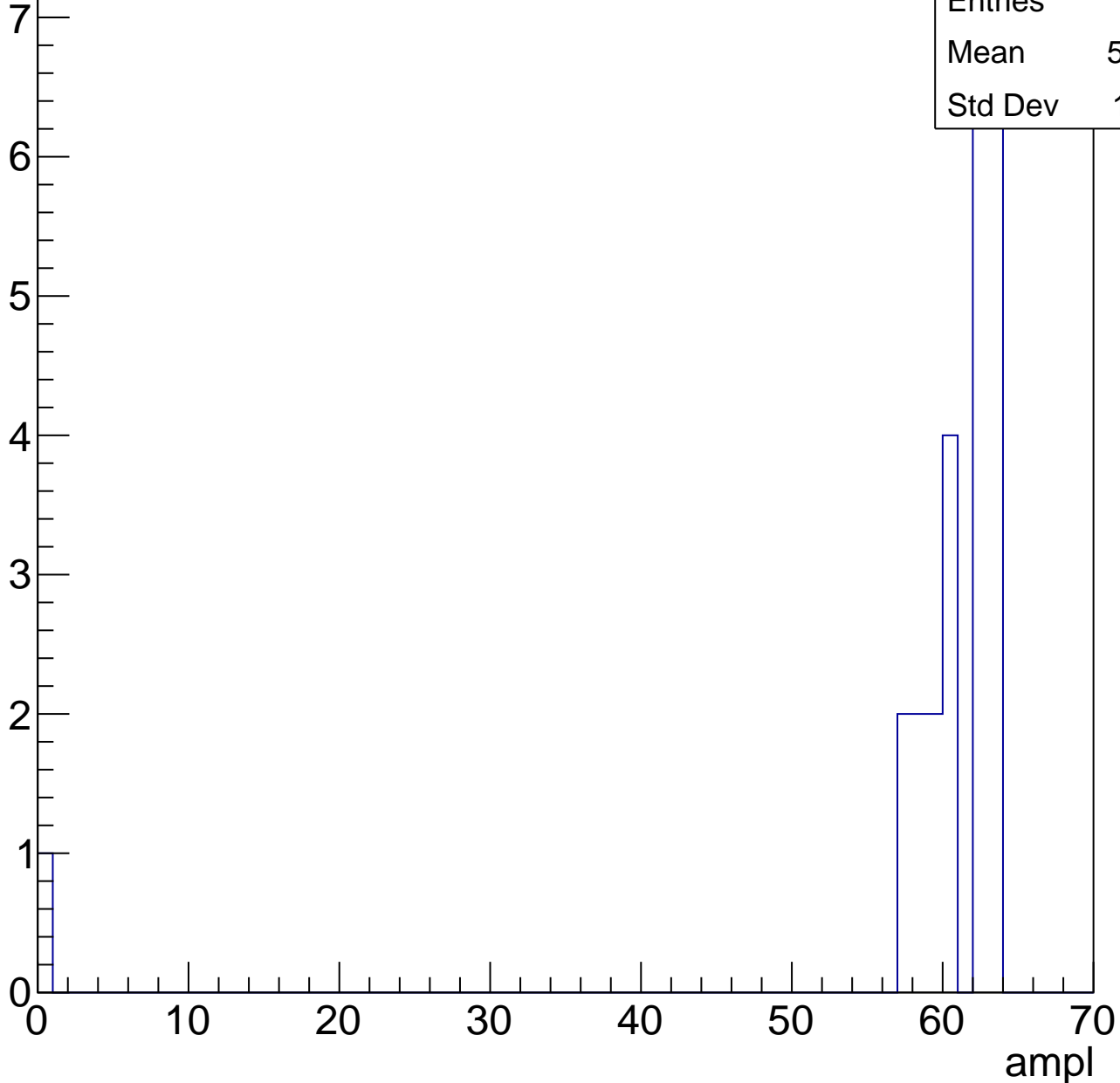


B1L103S, U21-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

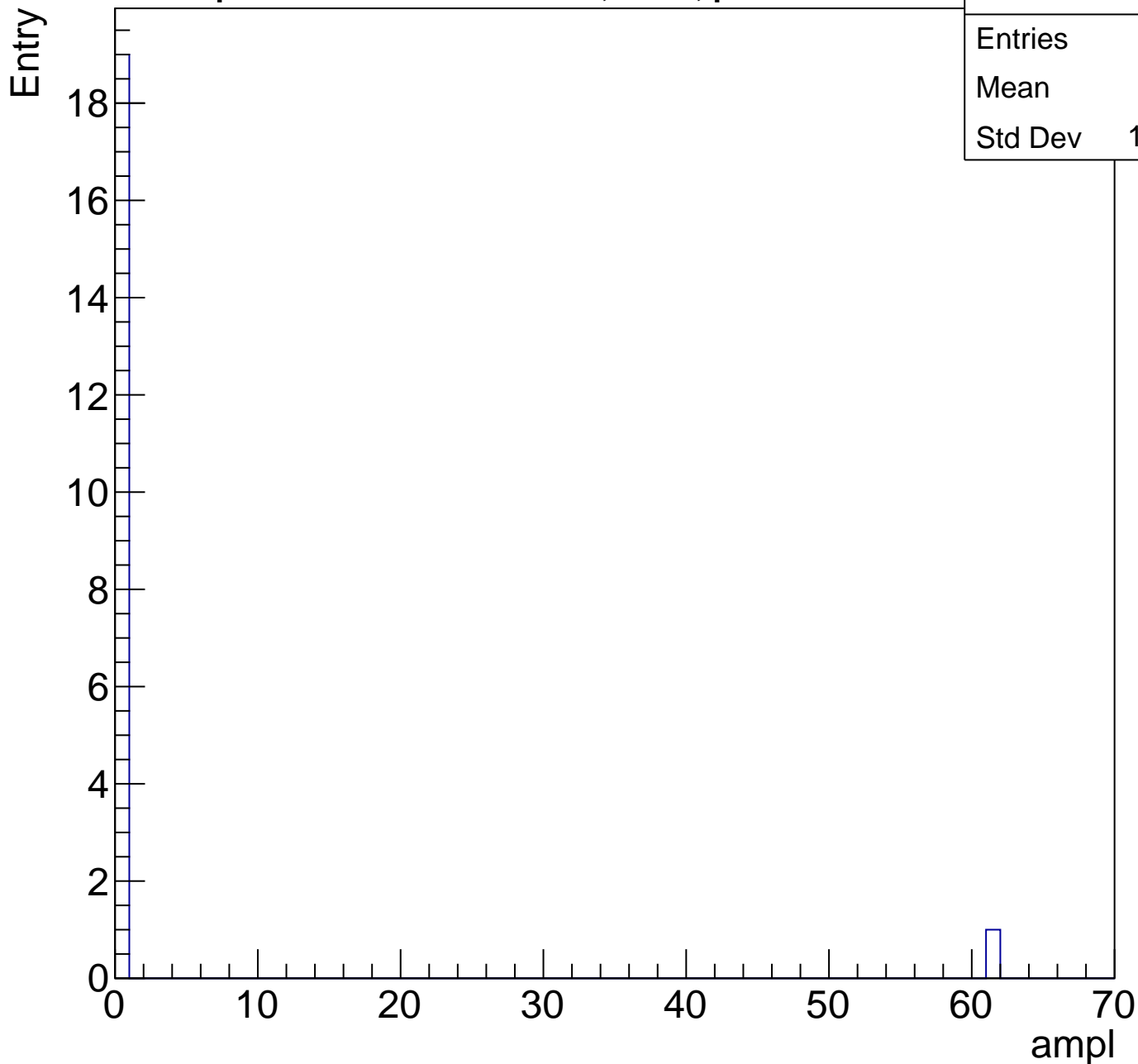
Entries	25
Mean	58.52
Std Dev	12.11



B1L103S, U21-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29



B1L103S, U21-ch61, adc0

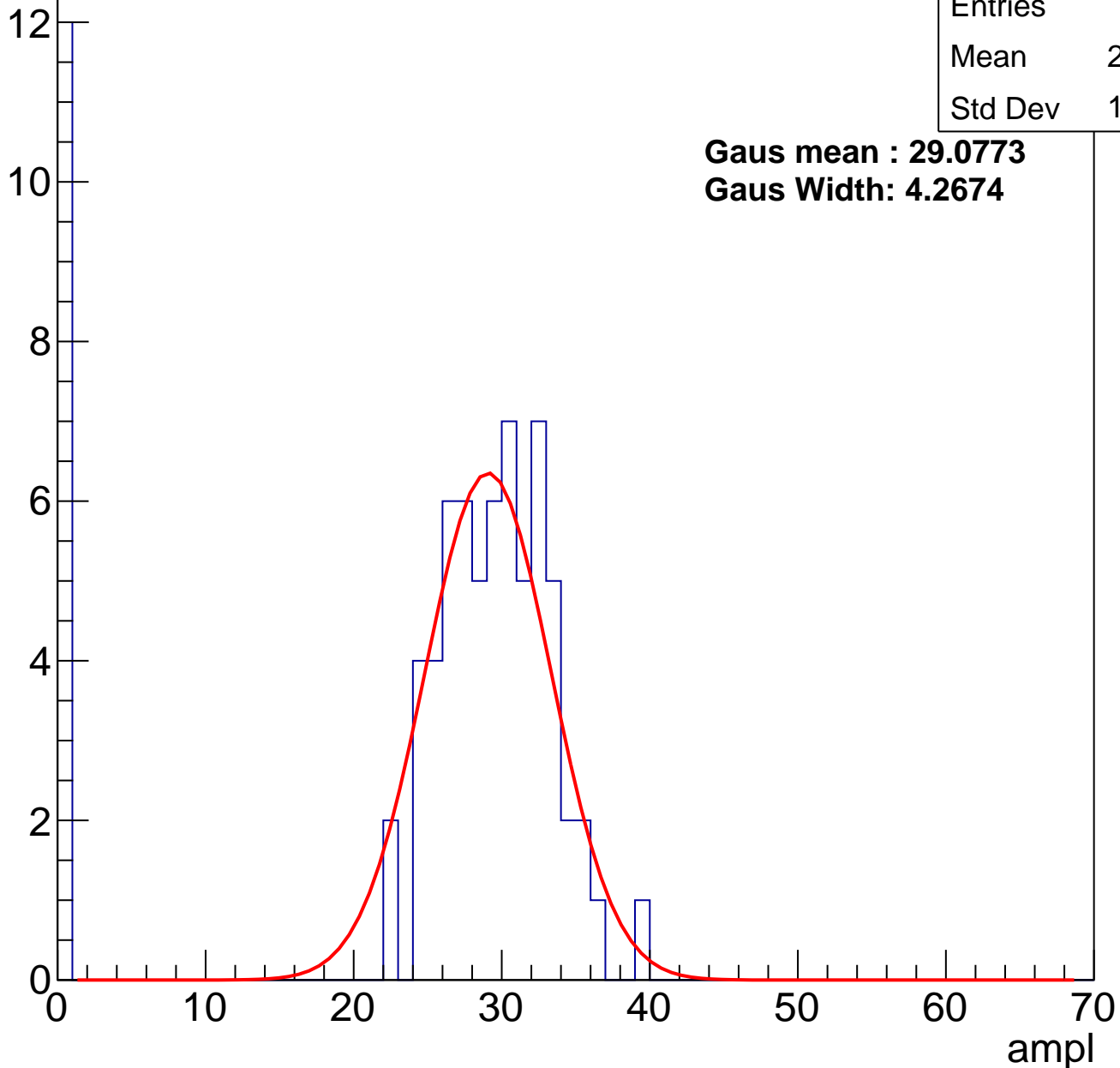
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	24.52
Std Dev	11.18

Gaus mean : 29.0773

Gaus Width: 4.2674

Entry



B1L103S, U21-ch61, adc1

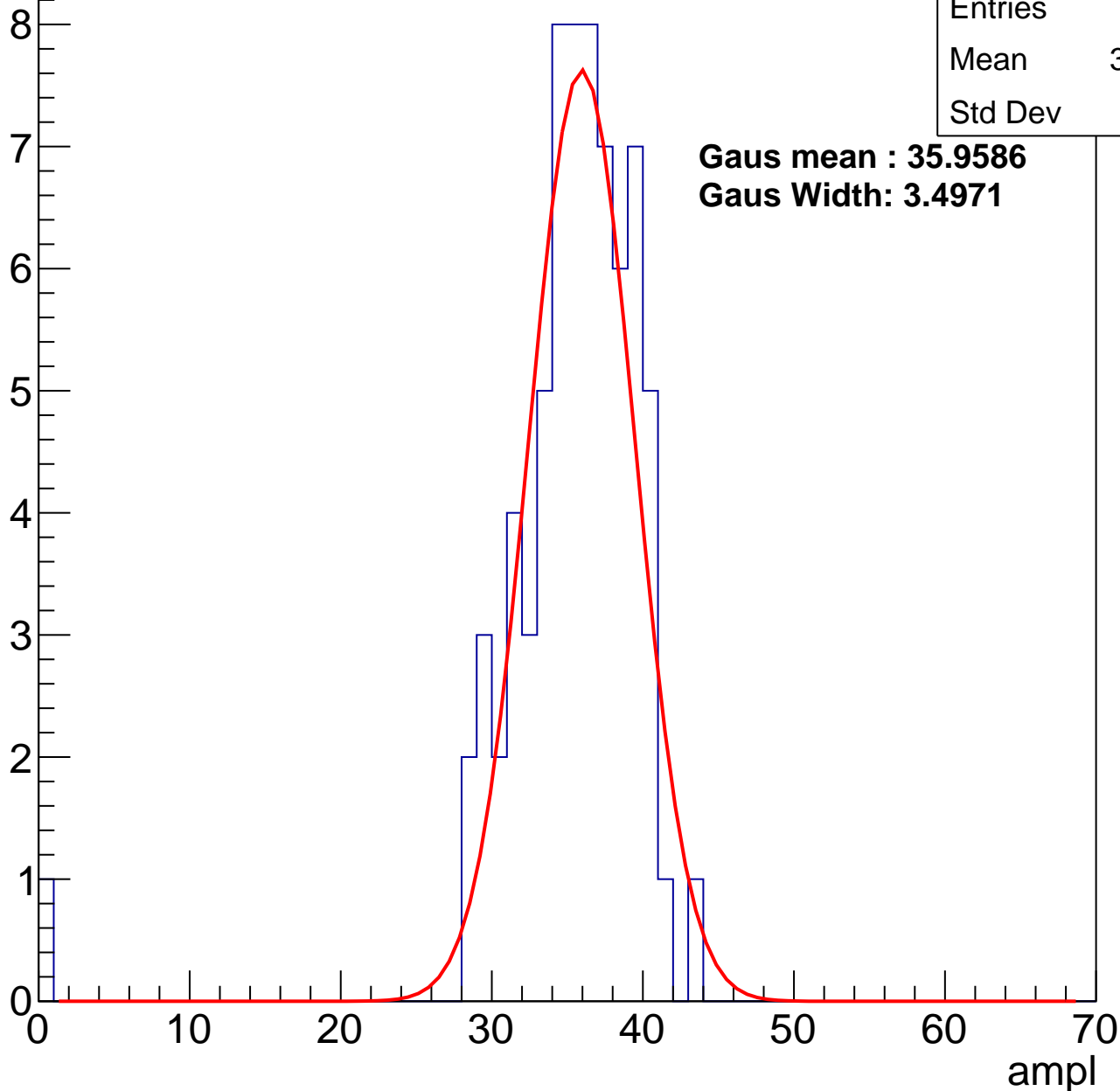
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.82
Std Dev	5.34

Gaus mean : 35.9586

Gaus Width: 3.4971



B1L103S, U21-ch61, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	36.2
Std Dev	15.72

Gaus mean : 43.3951

Gaus Width: 3.3479

Entry

12

10

8

6

4

2

0

0

10

20

30

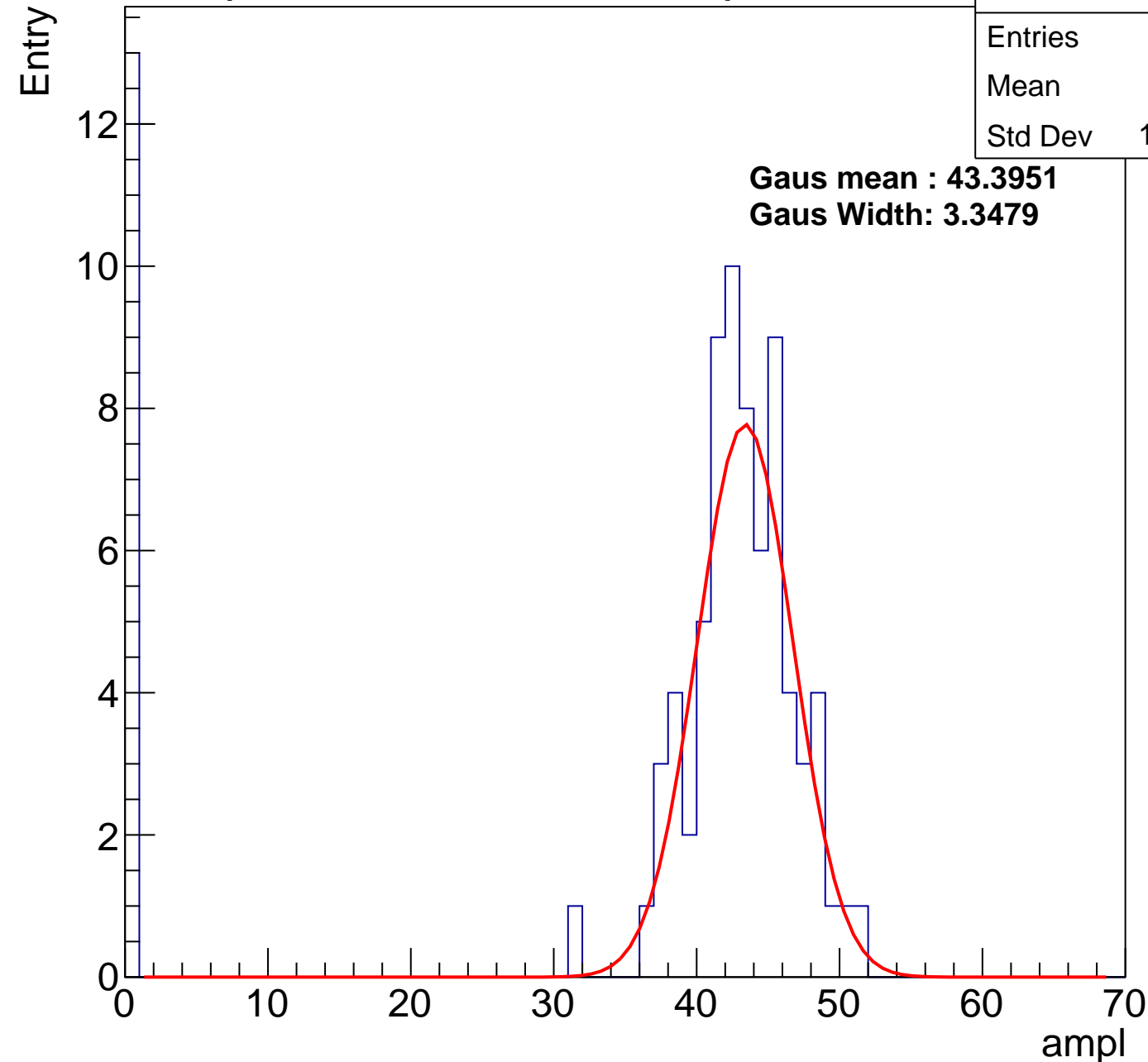
40

50

60

70

ampl

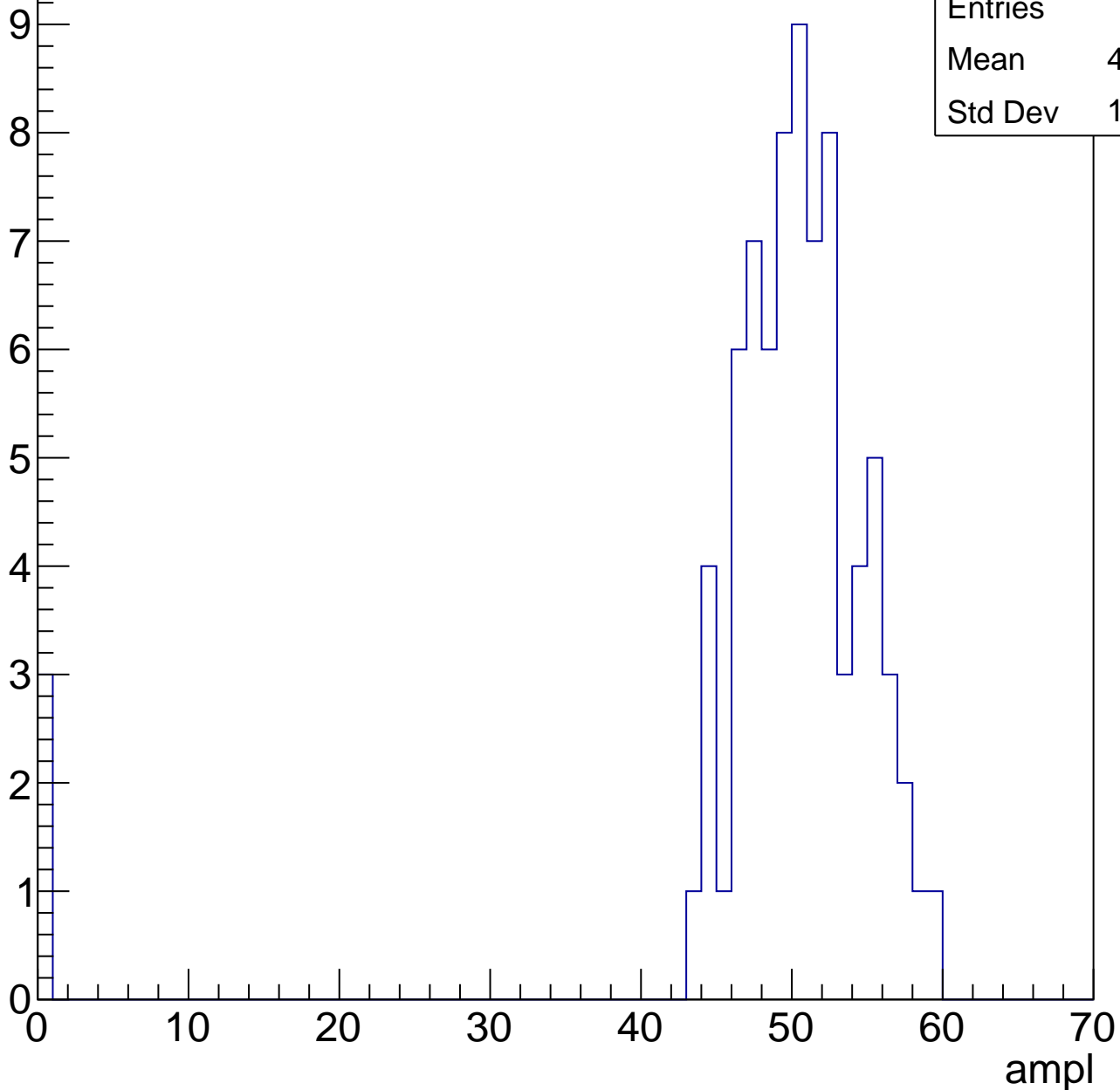


B1L103S, U21-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	48.37
Std Dev	10.25

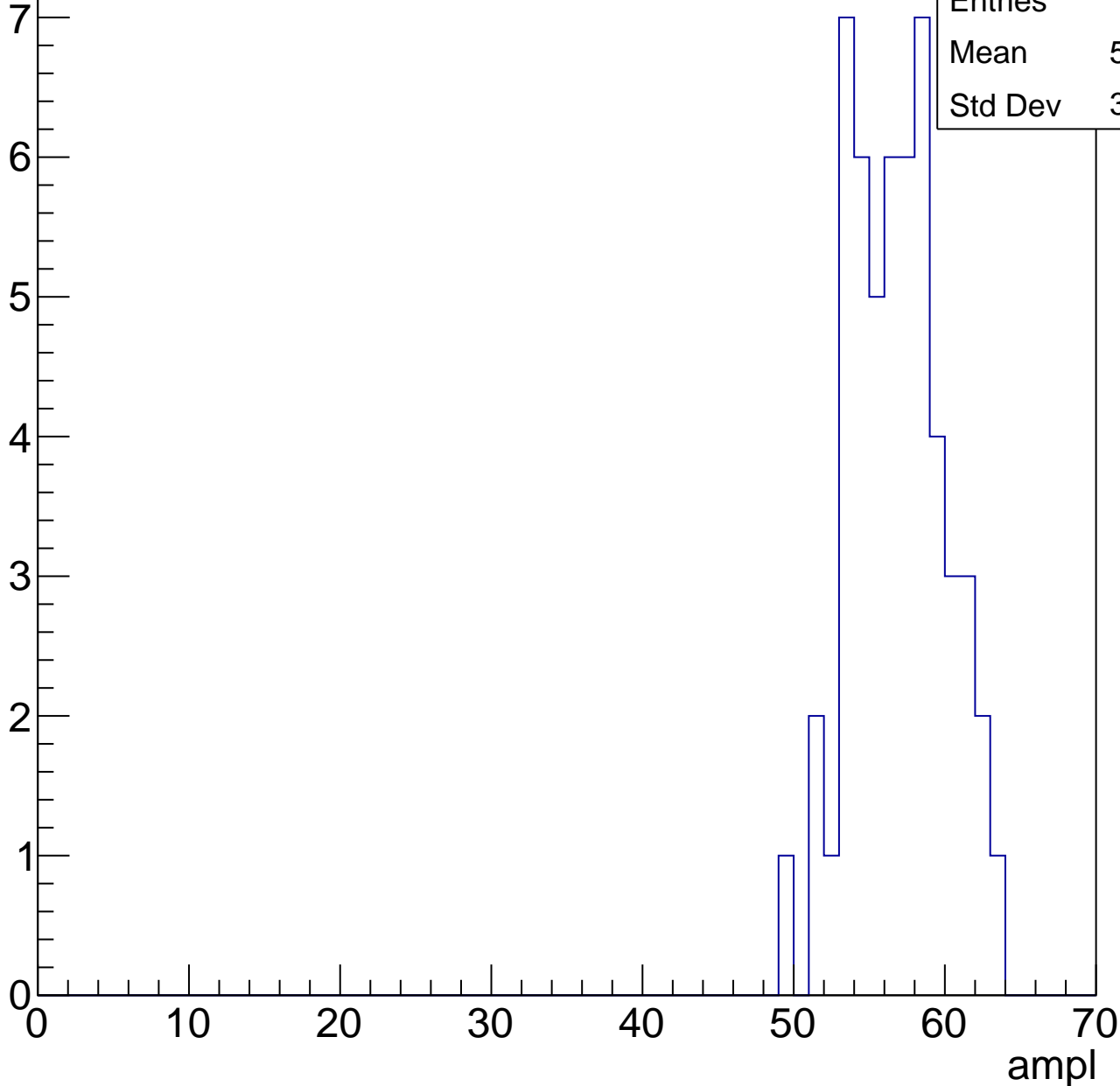


B1L103S, U21-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.35
Std Dev	3.086

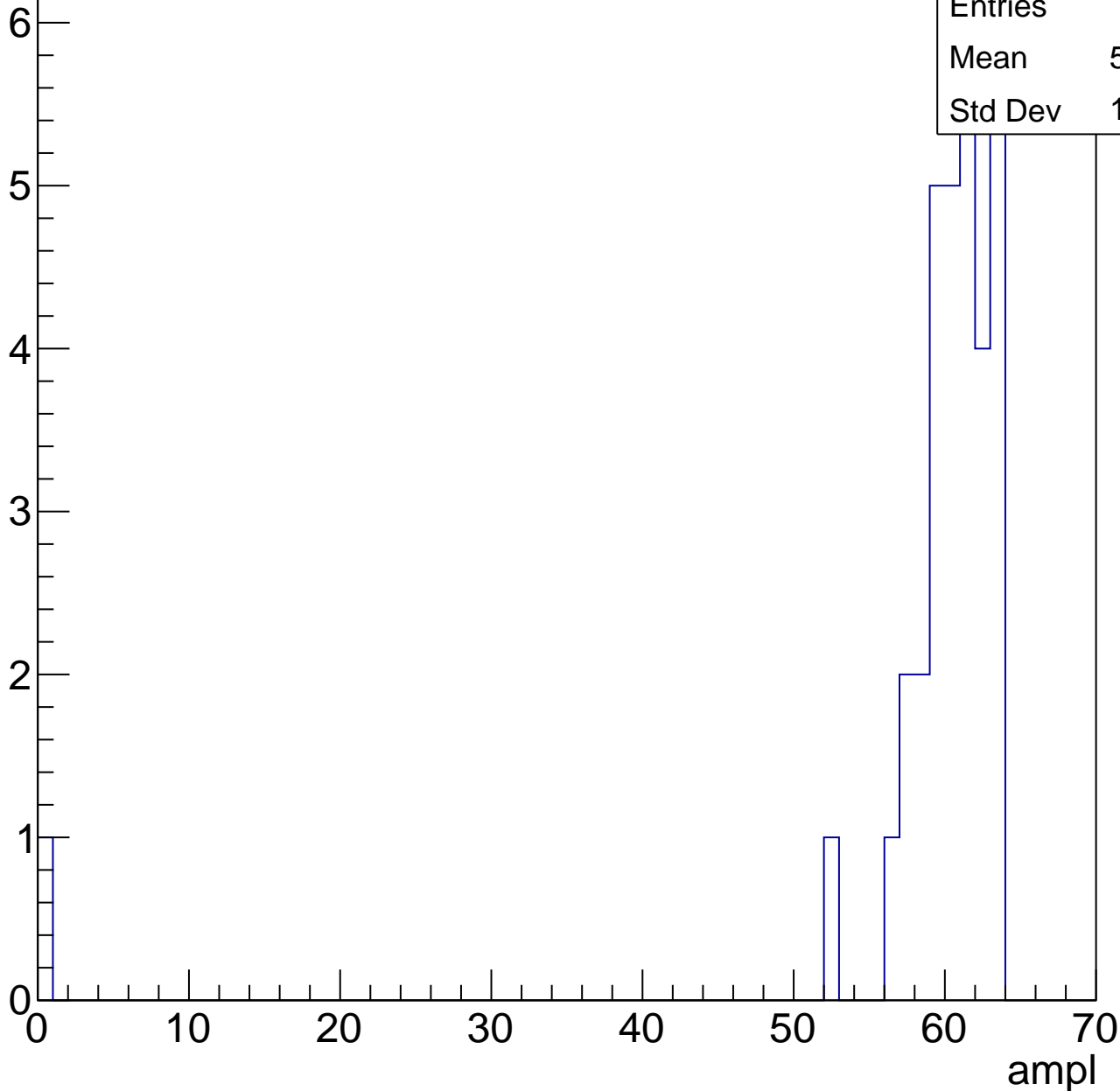


B1L103S, U21-ch61, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

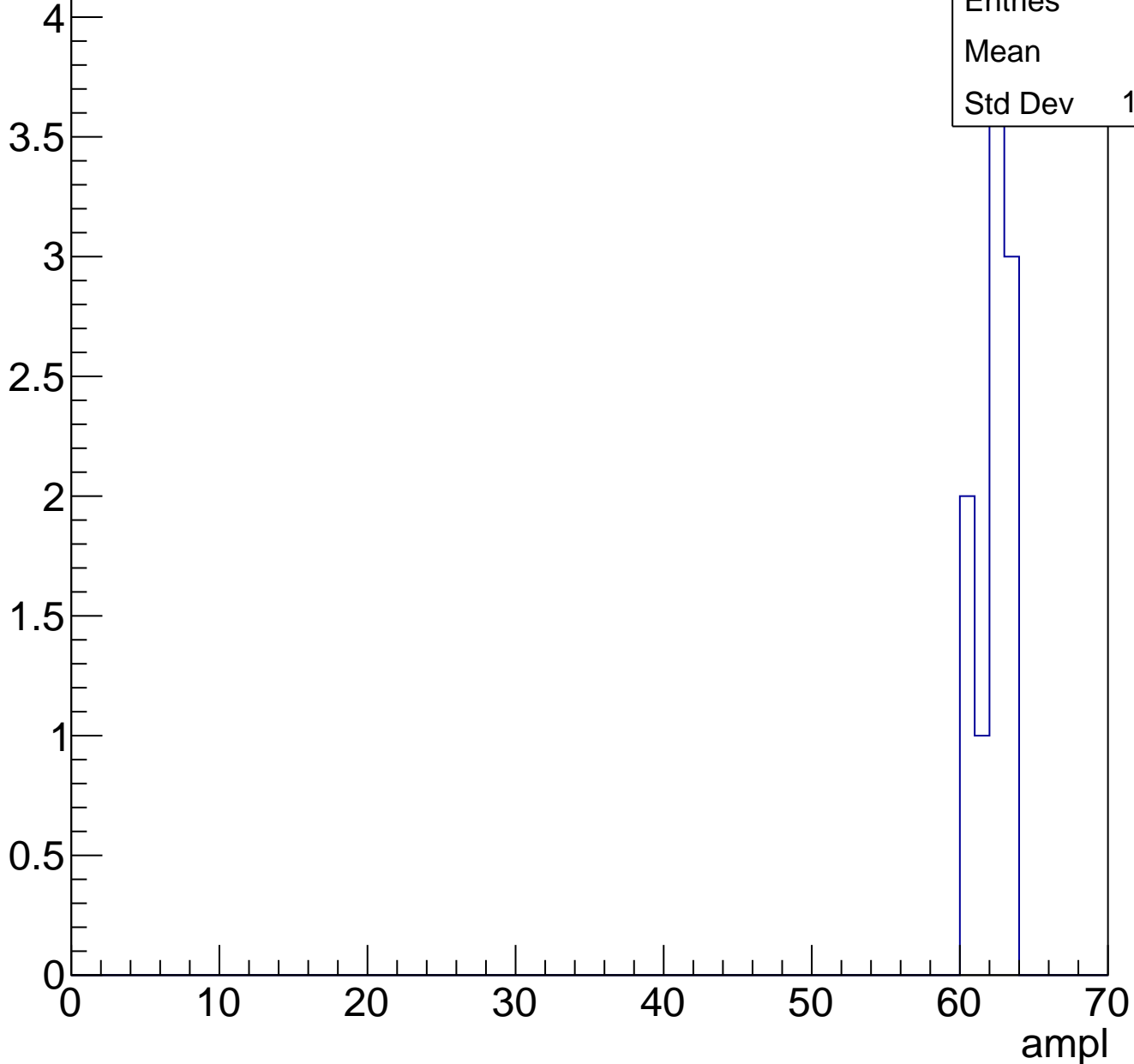
Entries	33
Mean	58.33
Std Dev	10.58



B1L103S, U21-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

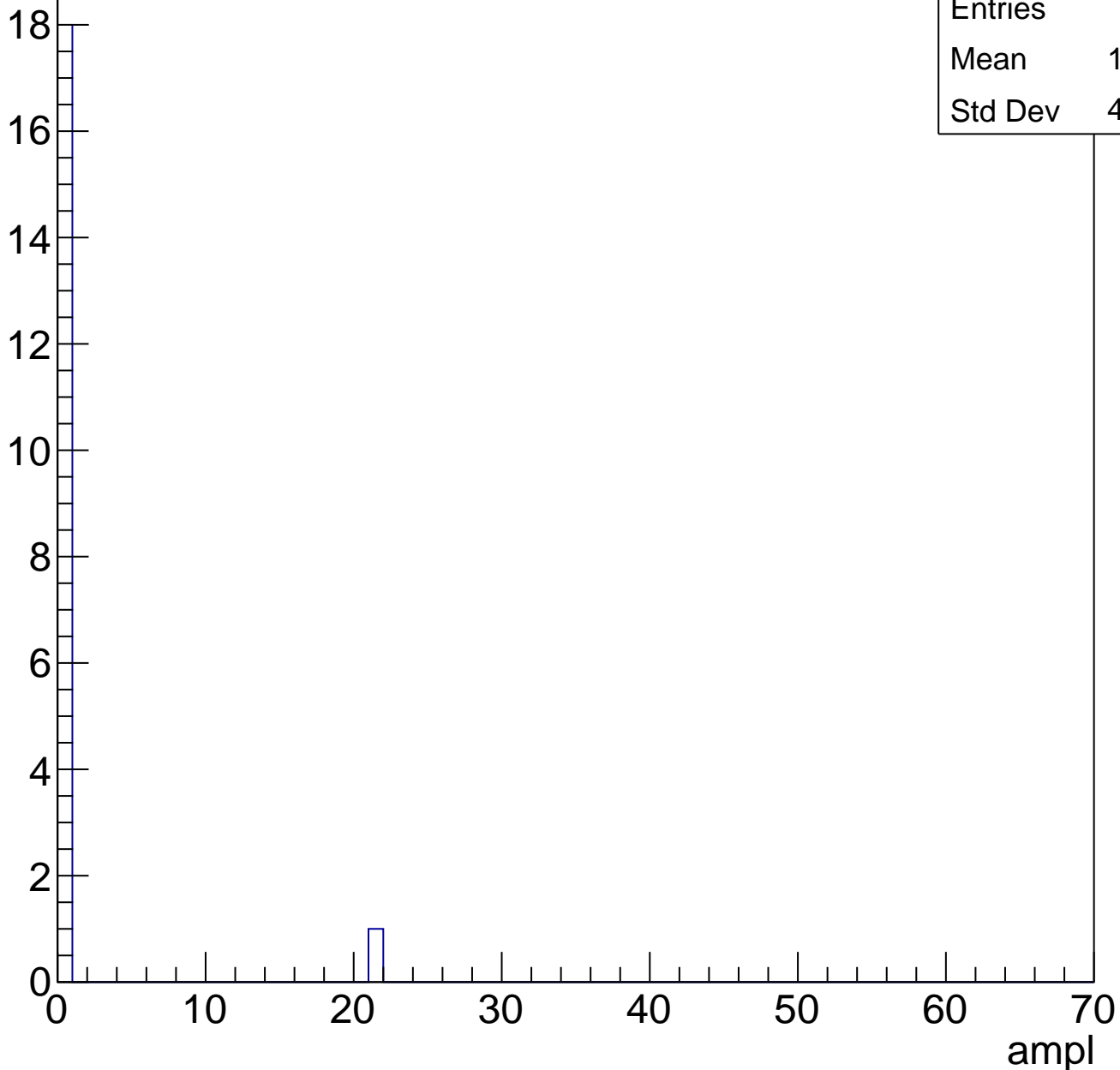


B1L103S, U21-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U21-ch62, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	24.07
Std Dev	10.6

Gaus mean : 28.2782

Gaus Width: 3.2708

Entry

10

8

6

4

2

0

0

10

20

30

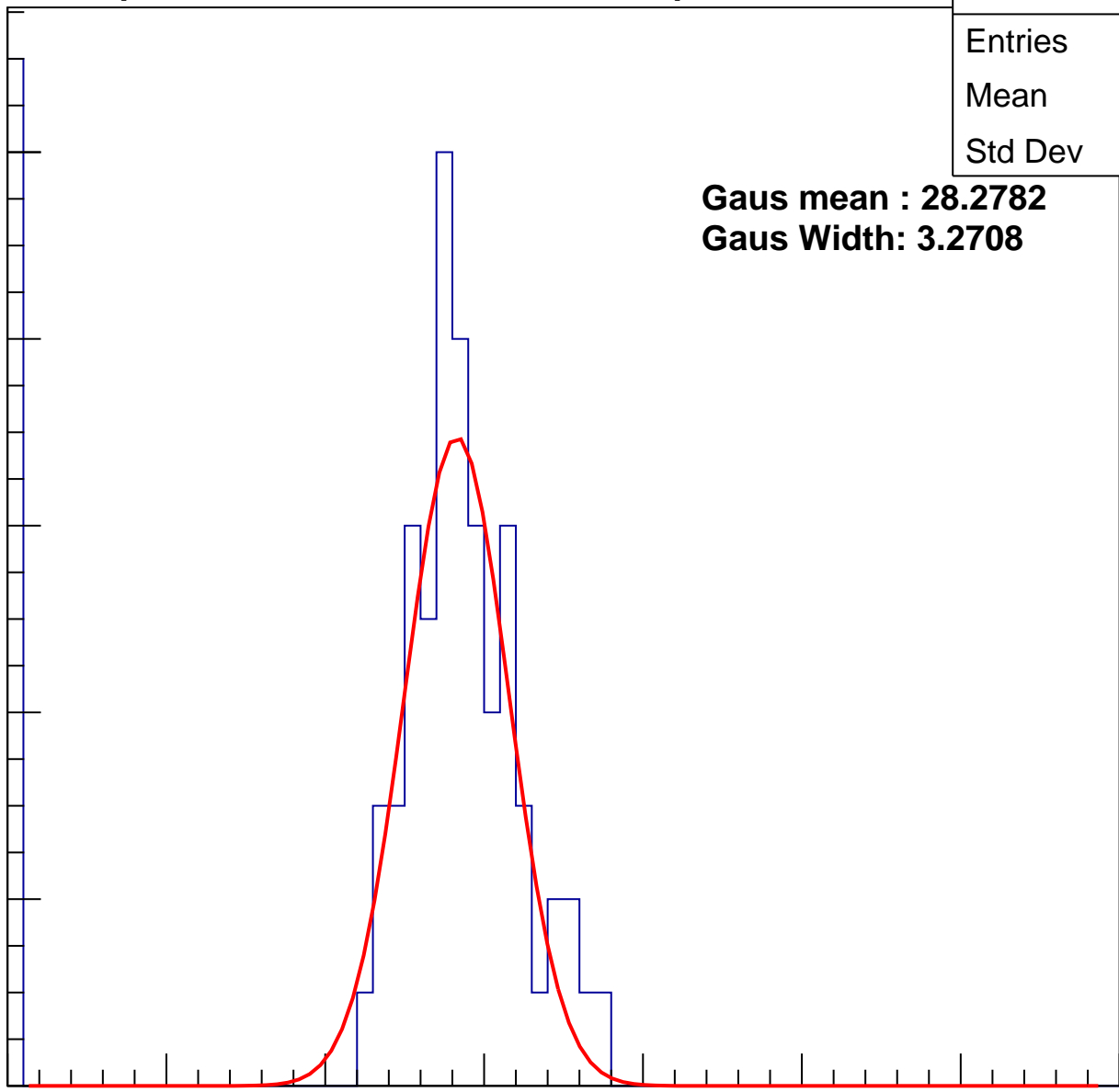
40

50

60

70

ampl



B1L103S, U21-ch62, adc1

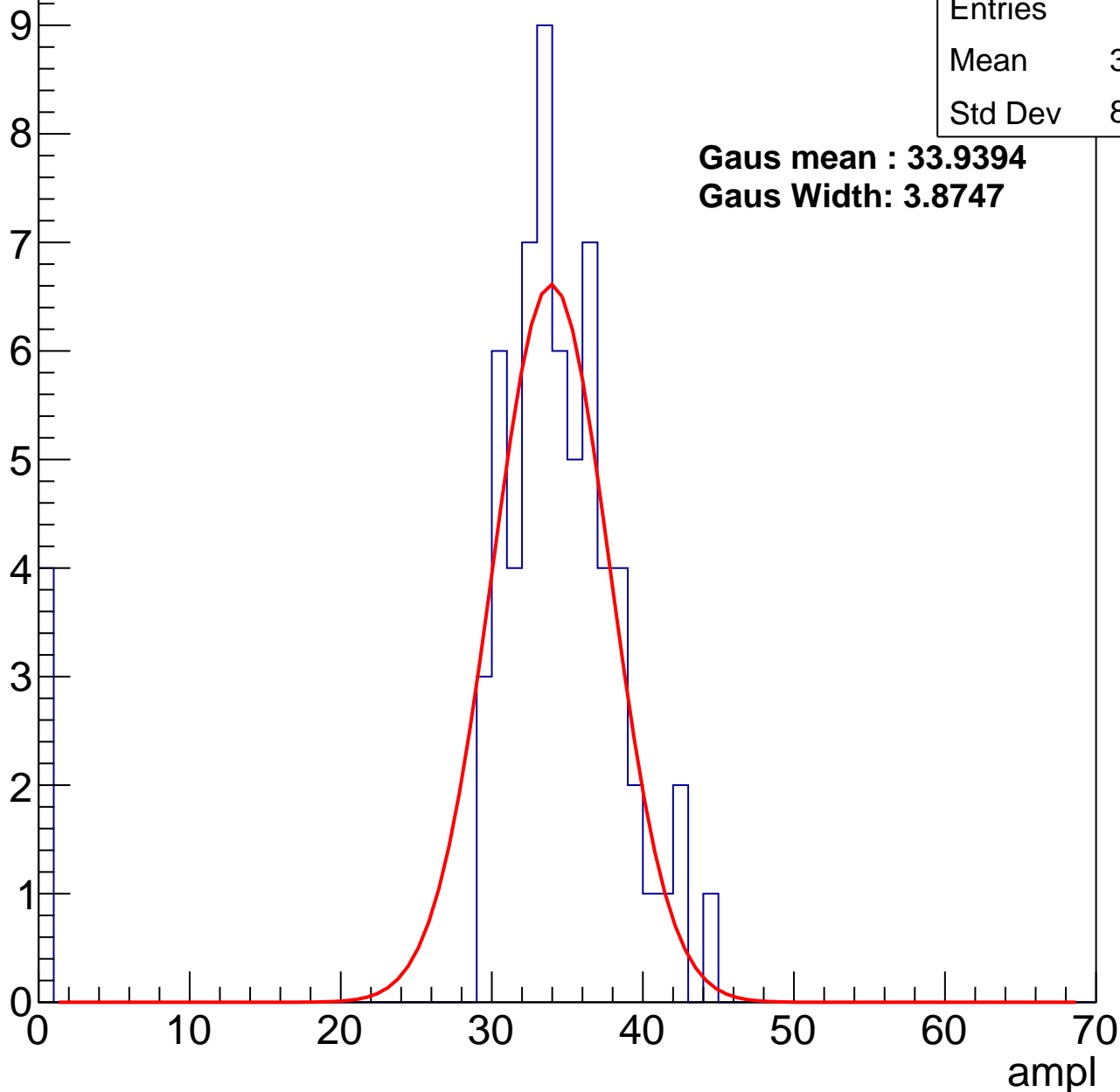
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	32.27
Std Dev	8.848

Gaus mean : 33.9394

Gaus Width: 3.8747



B1L103S, U21-ch62, adc2

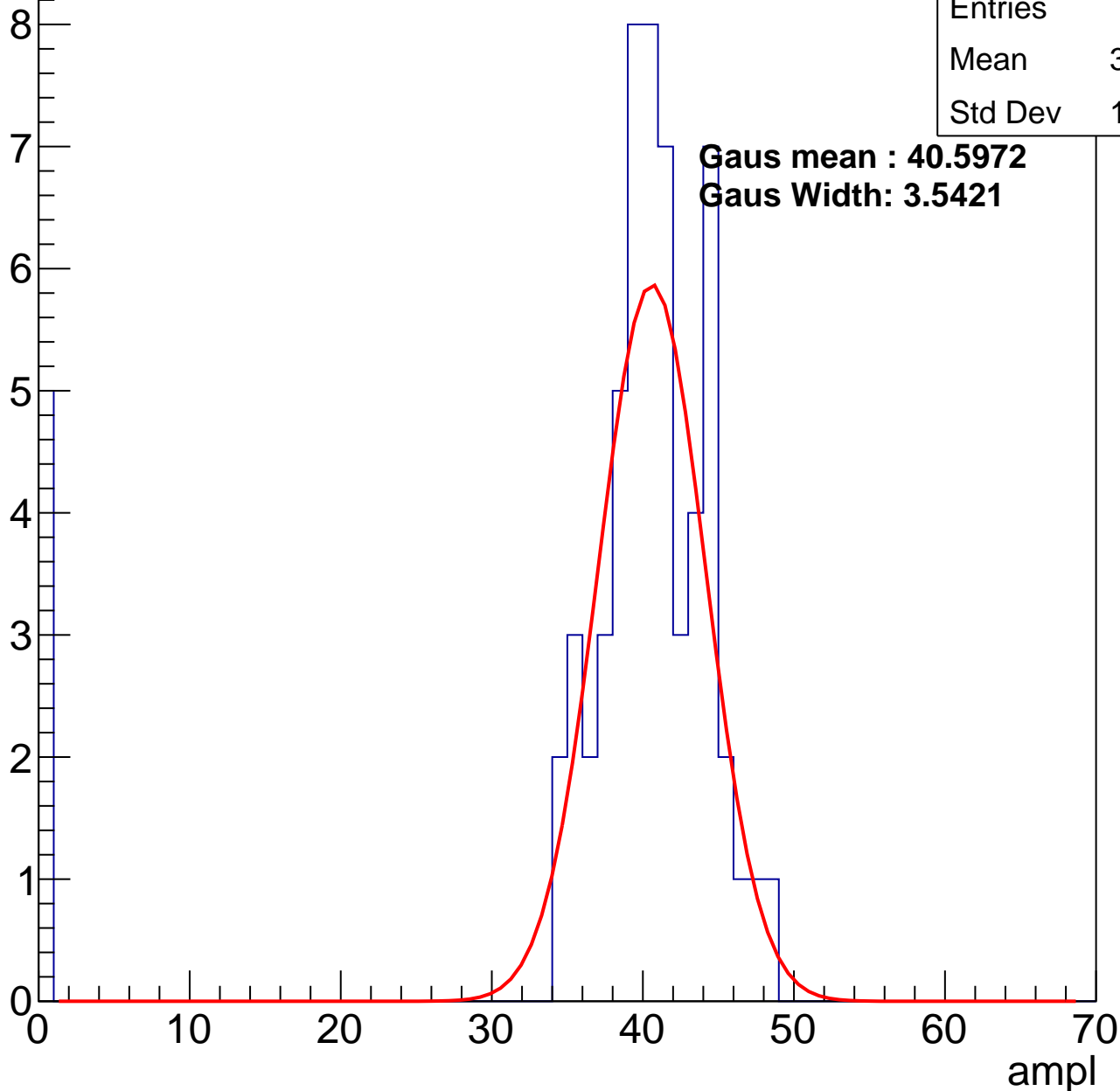
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	37.13
Std Dev	11.42

Gaus mean : 40.5972

Gaus Width: 3.5421

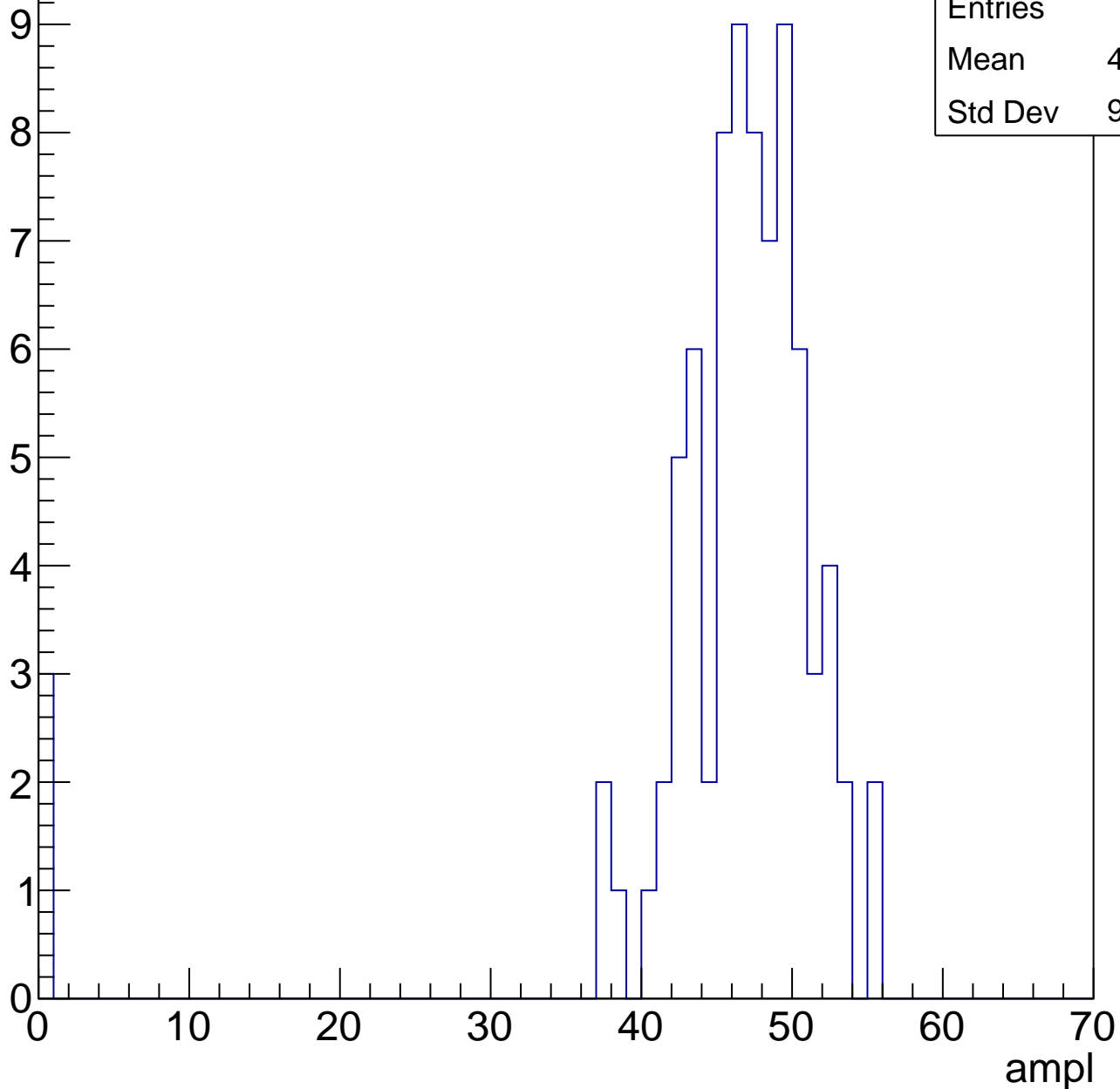


B1L103S, U21-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	44.92
Std Dev	9.624

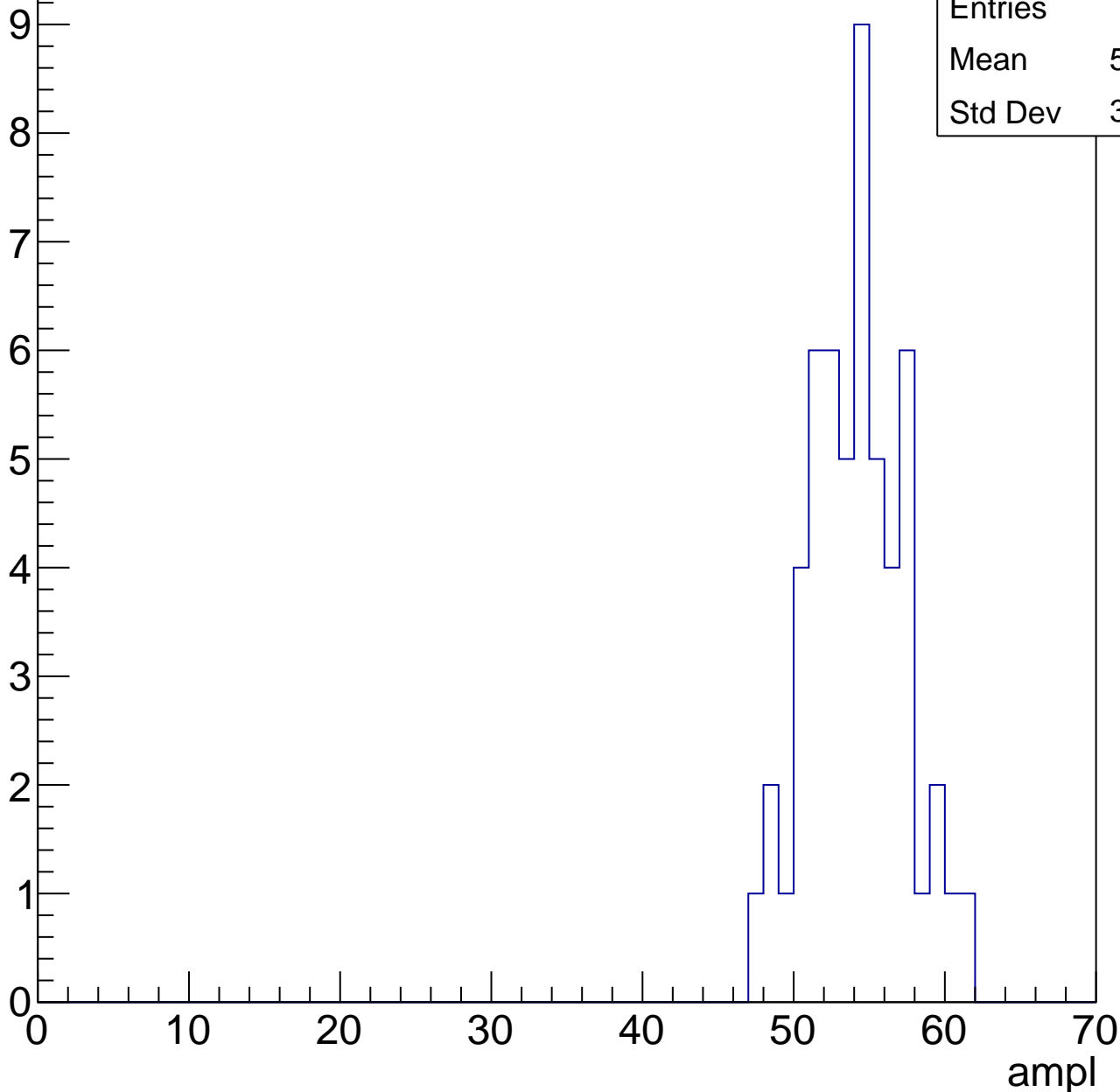


B1L103S, U21-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	53.69
Std Dev	3.084

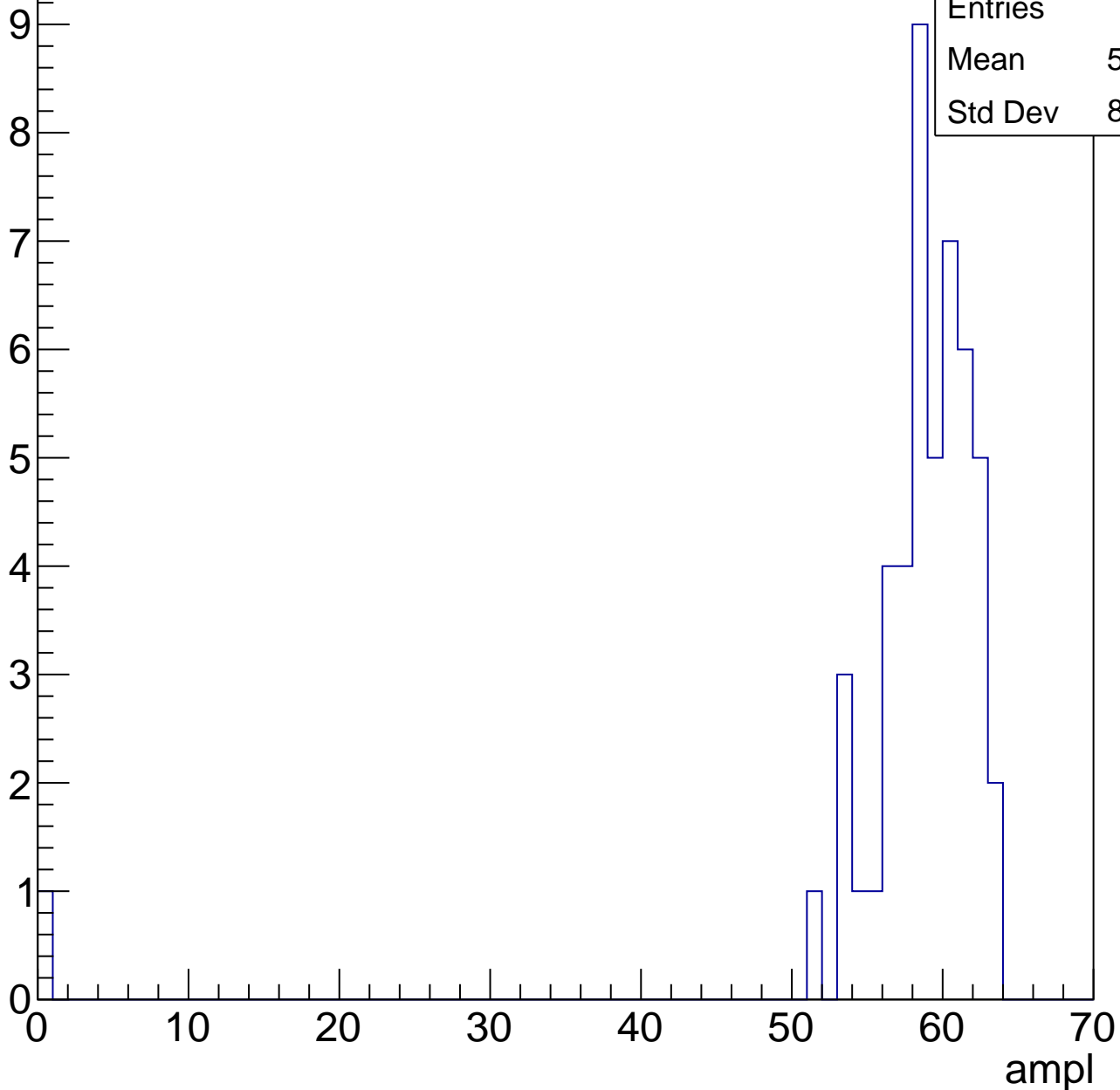


B1L103S, U21-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.35
Std Dev	8.724

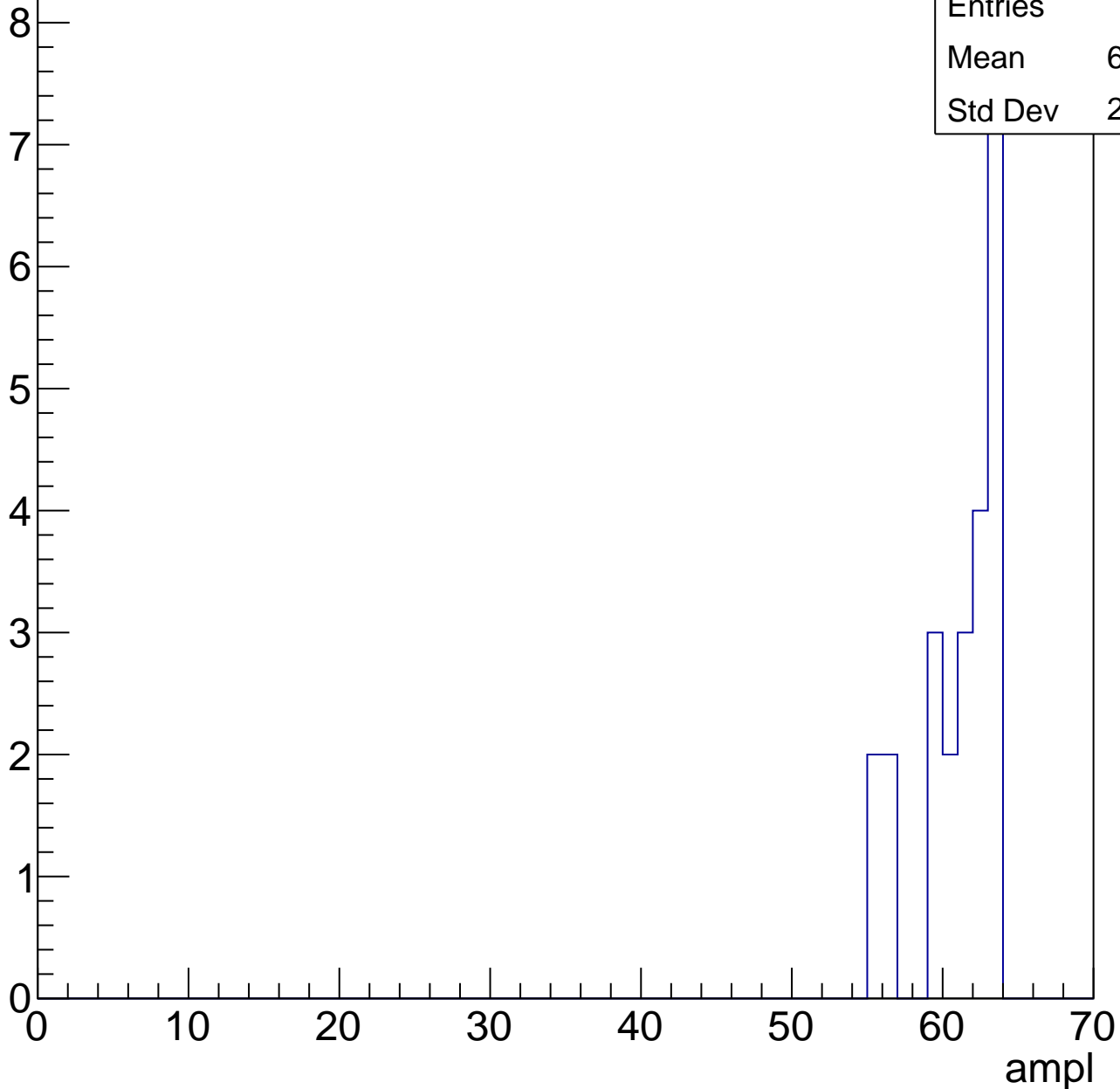


B1L103S, U21-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	60.58
Std Dev	2.644

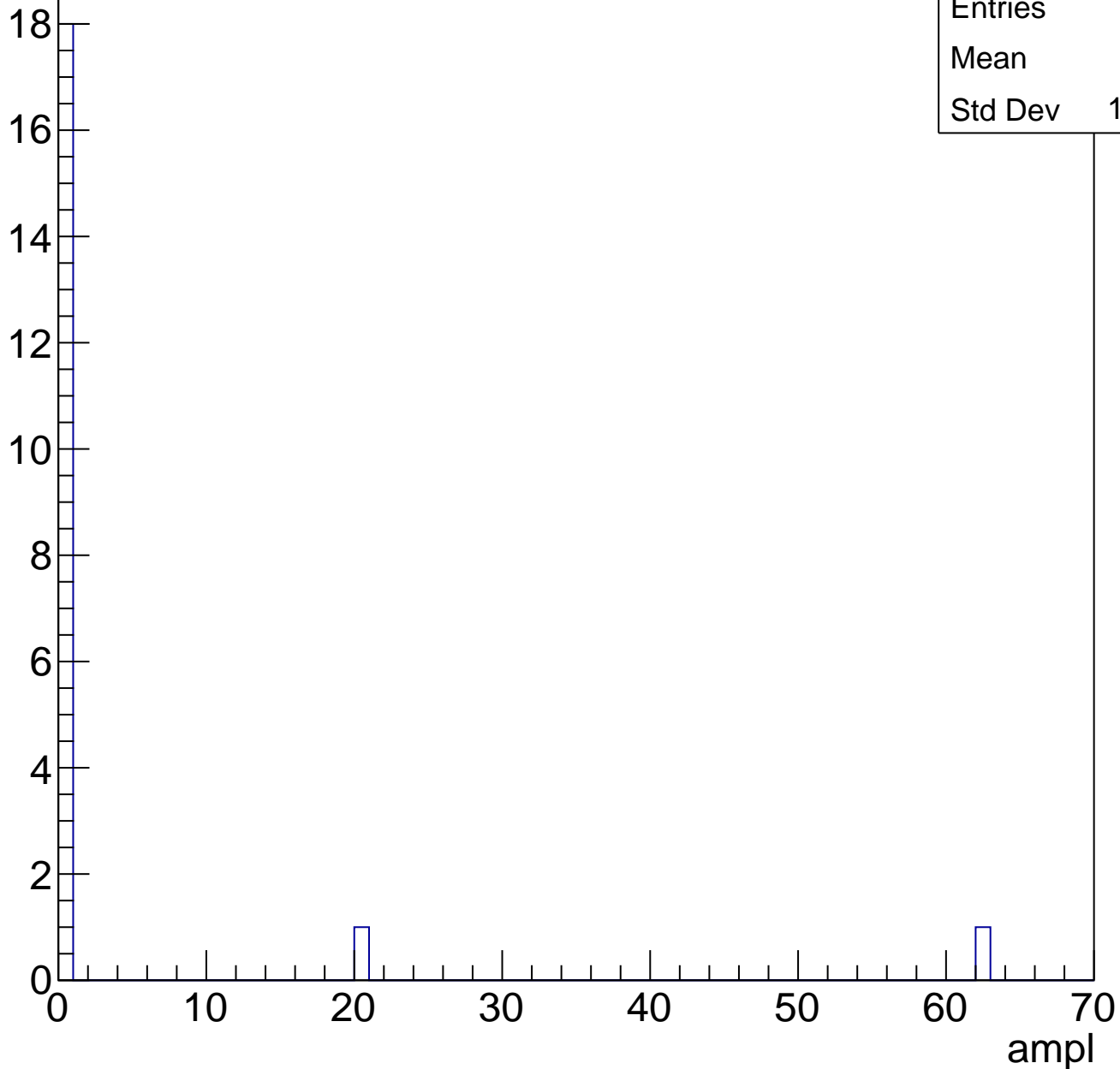


B1L103S, U21-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.1
Std Dev	13.98

Entry



B1L103S, U21-ch63, adc0

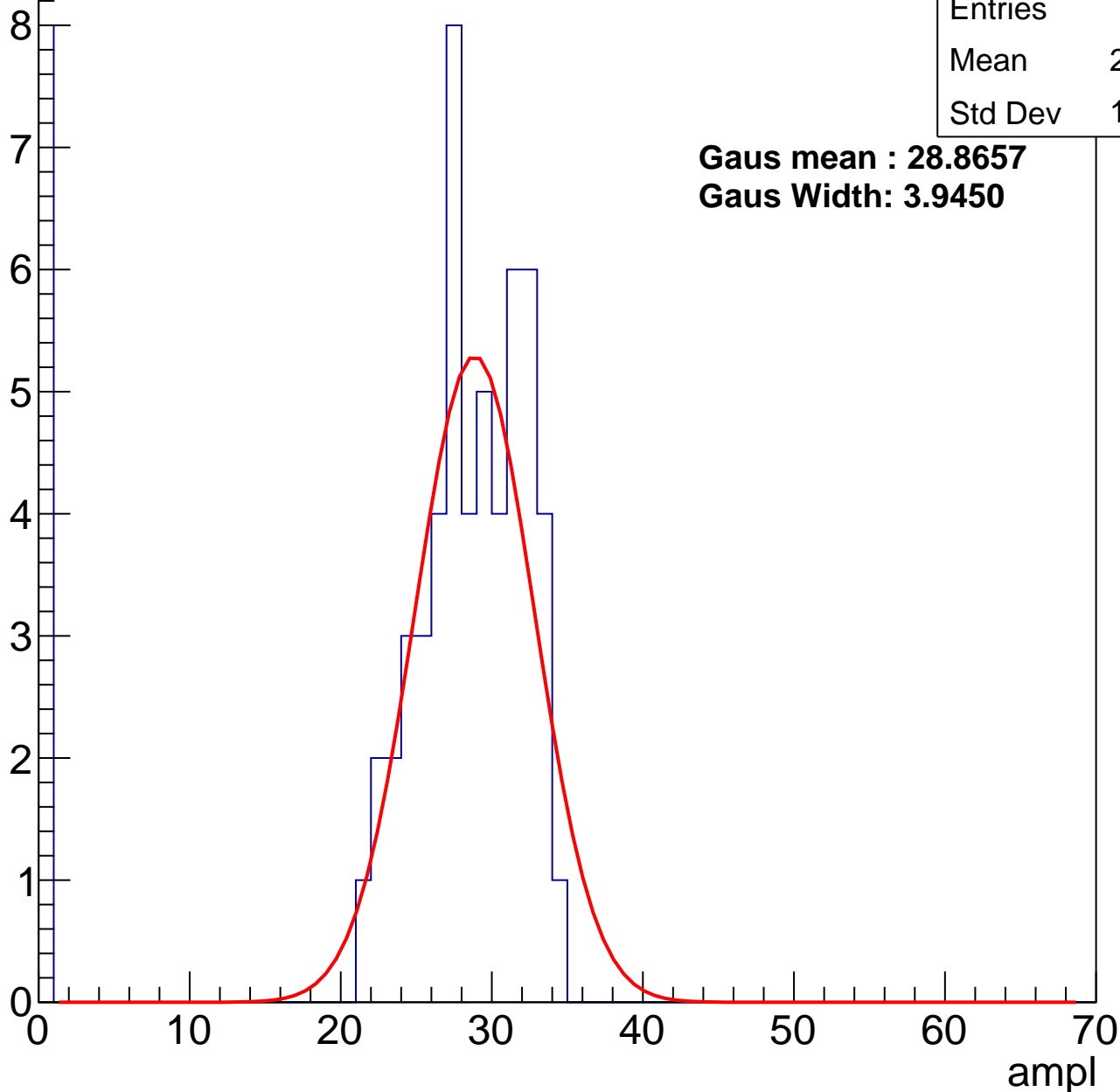
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	24.57
Std Dev	10.03

Gaus mean : 28.8657

Gaus Width: 3.9450

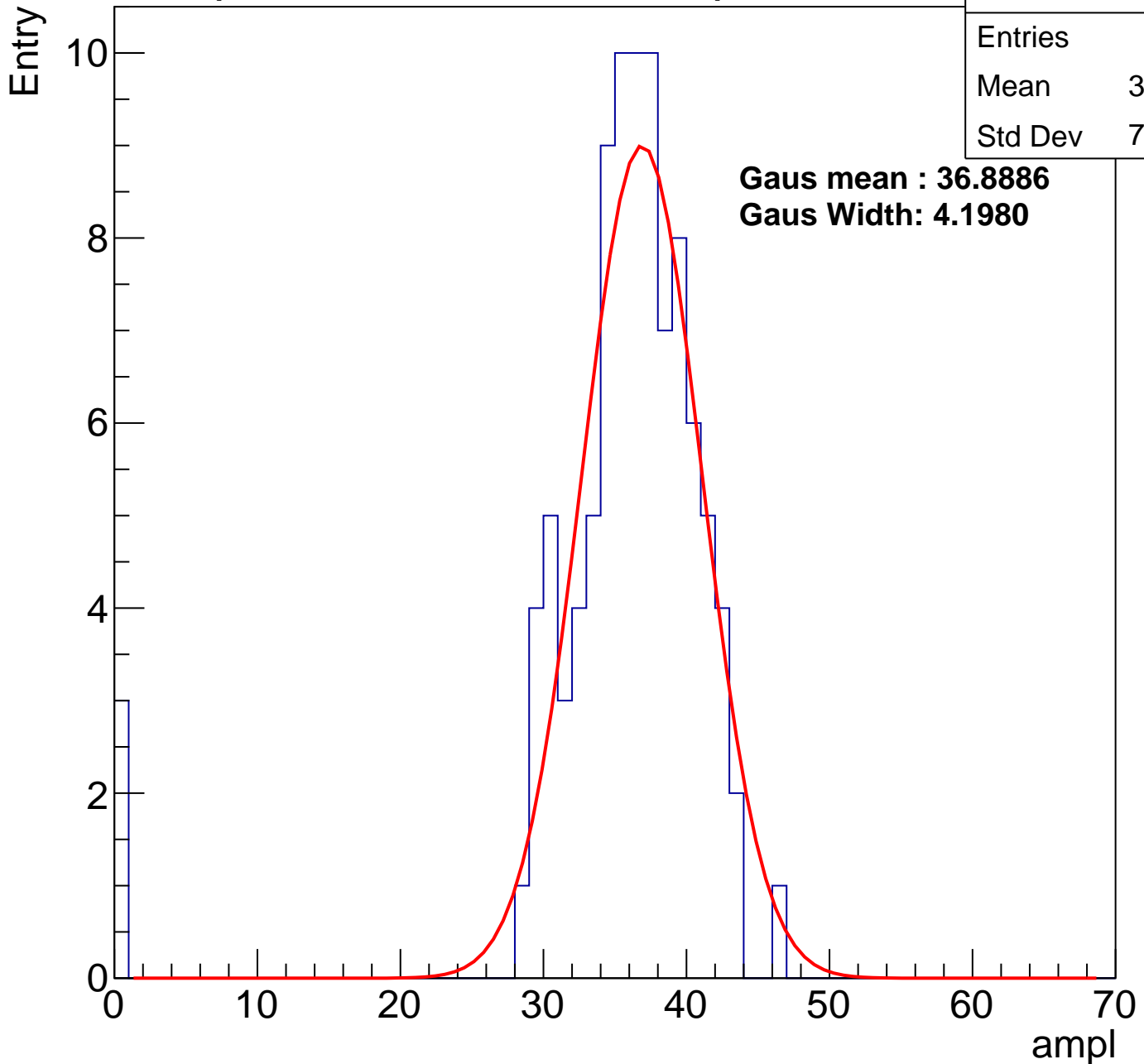


B1L103S, U21-ch63, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	34.94
Std Dev	7.266

Gaus mean : 36.8886
Gaus Width: 4.1980



B1L103S, U21-ch63, adc2

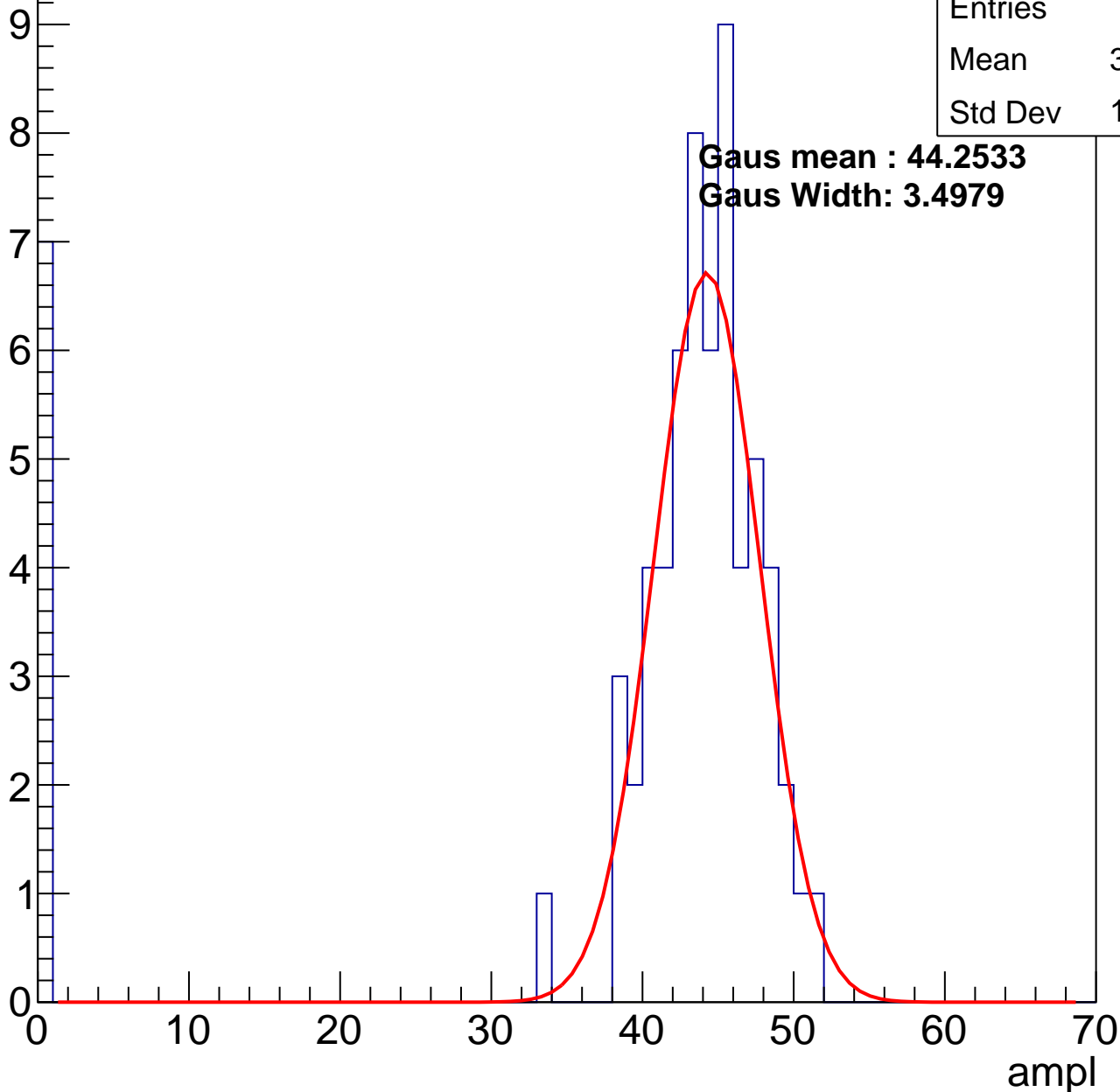
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	39.16
Std Dev	13.75

Gaus mean : 44.2533

Gaus Width: 3.4979

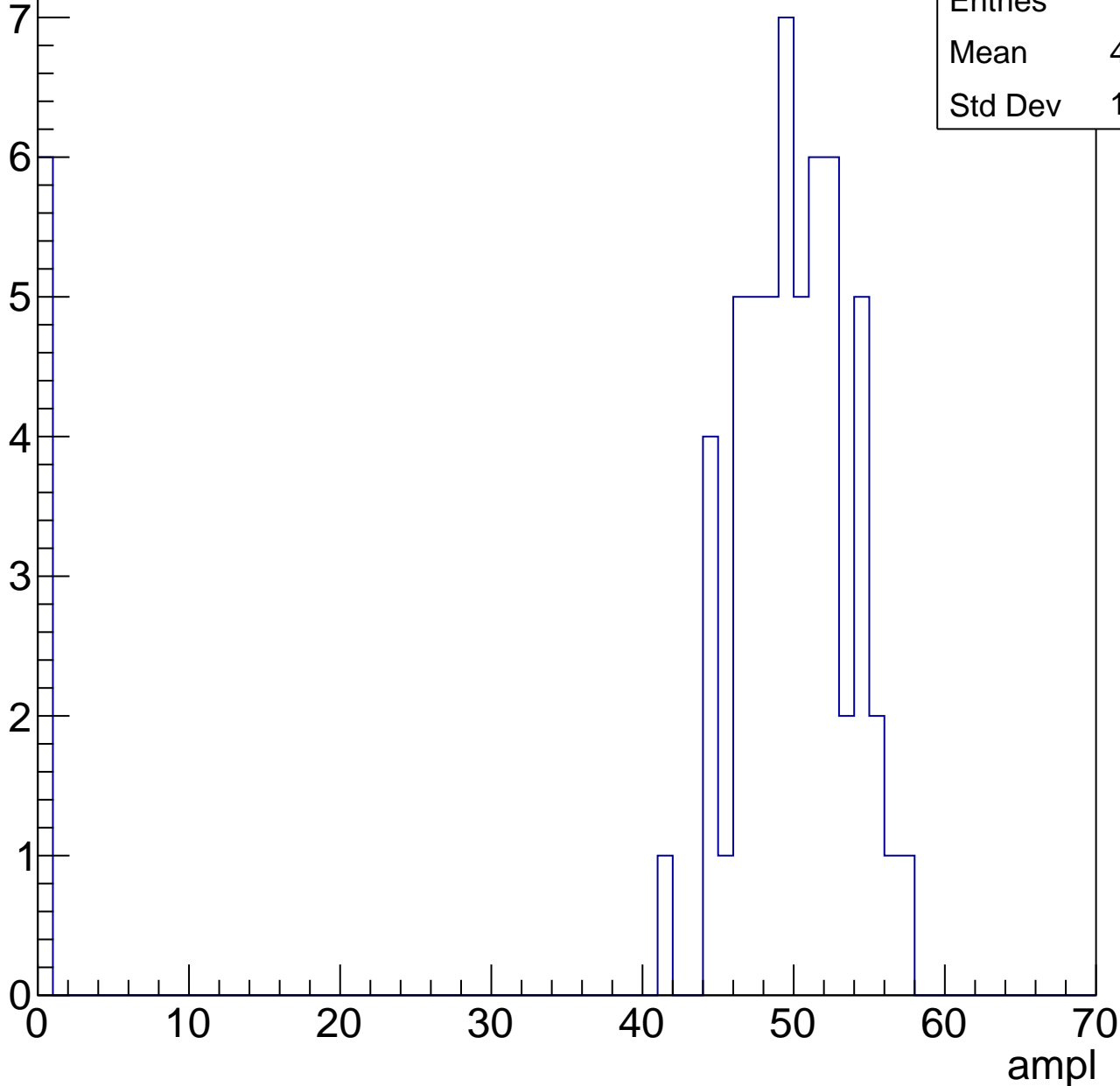


B1L103S, U21-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	44.79
Std Dev	15.02

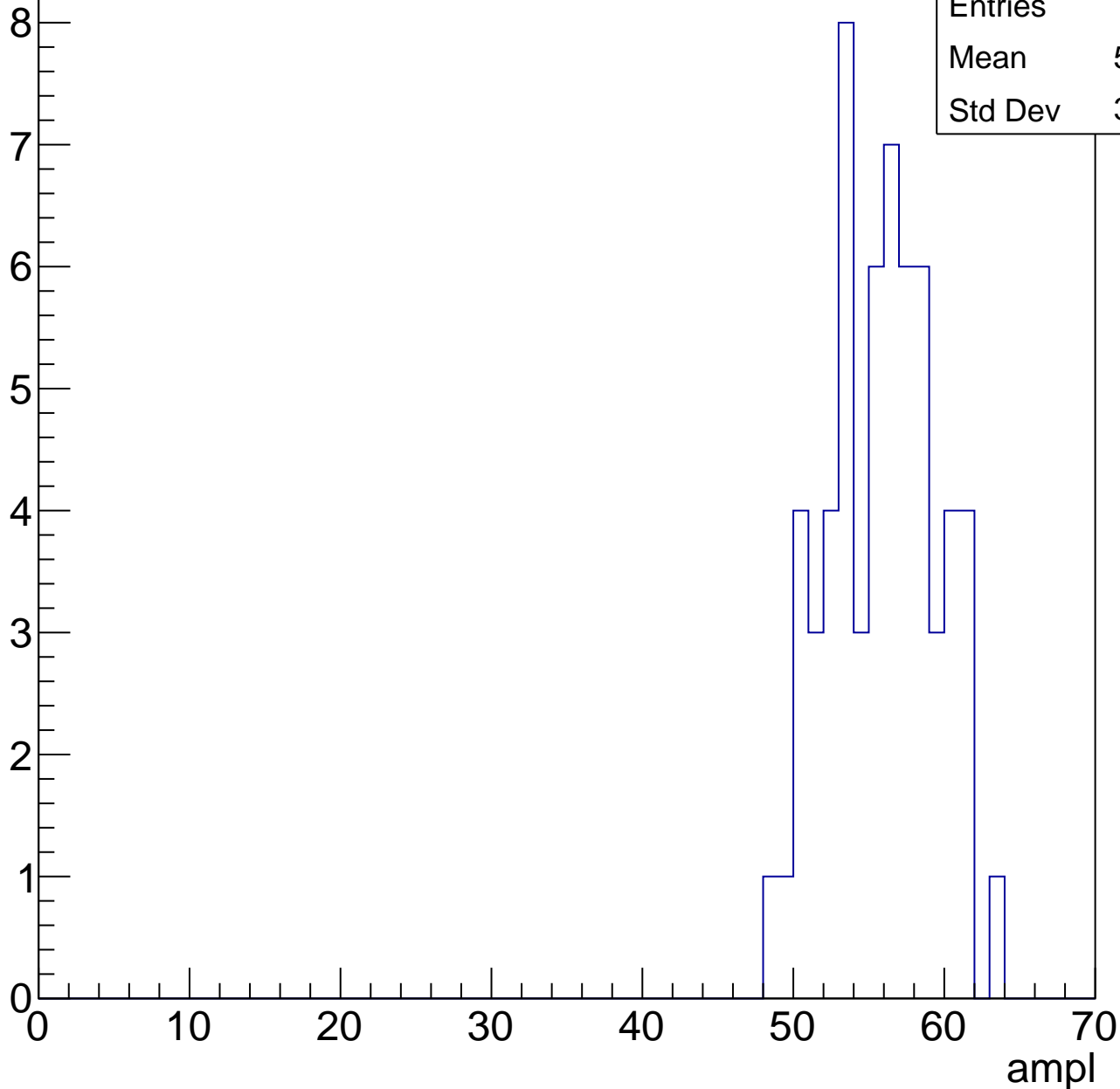


B1L103S, U21-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55.41
Std Dev	3.471

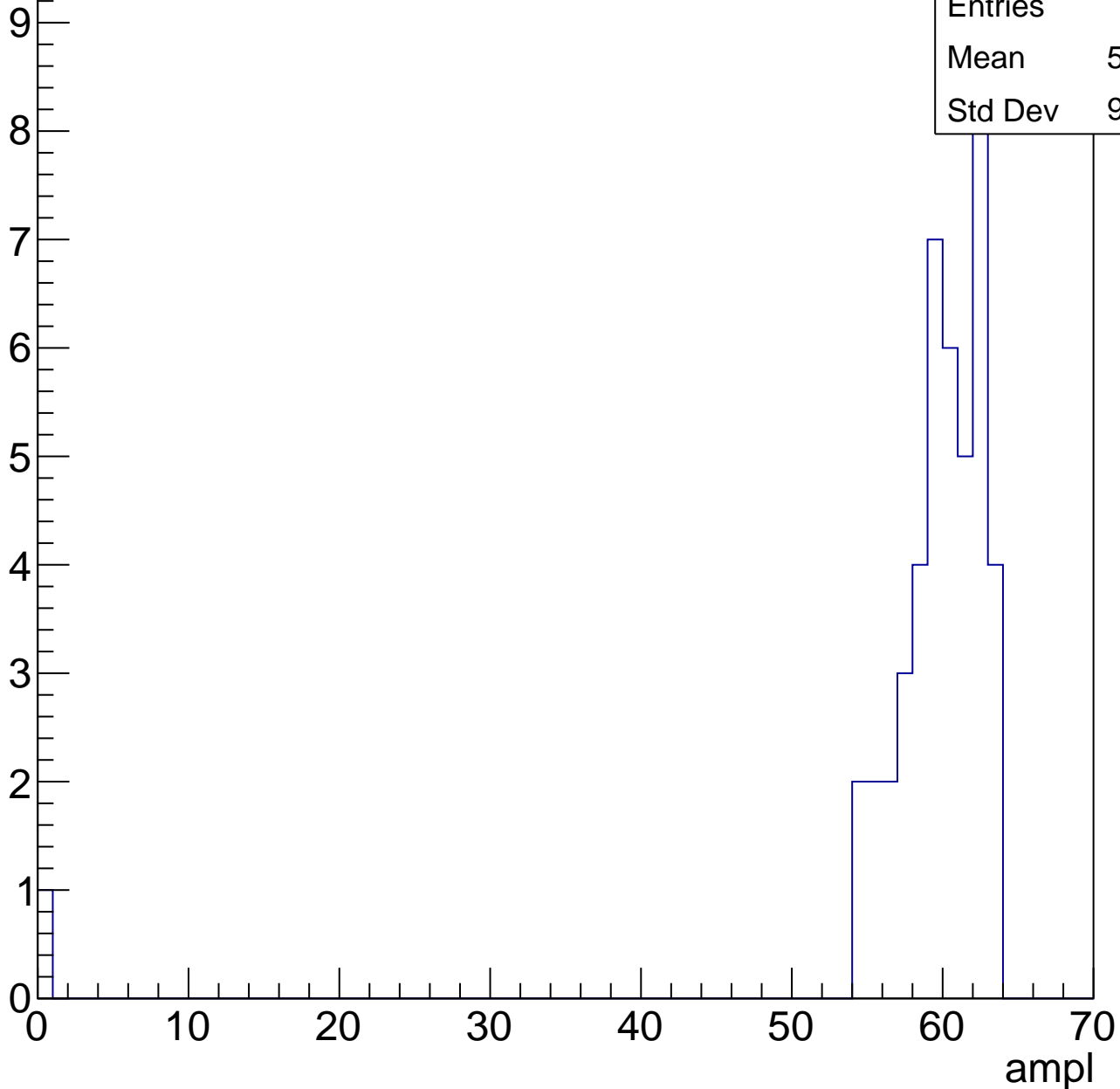


B1L103S, U21-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.24
Std Dev	9.119



B1L103S, U21-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

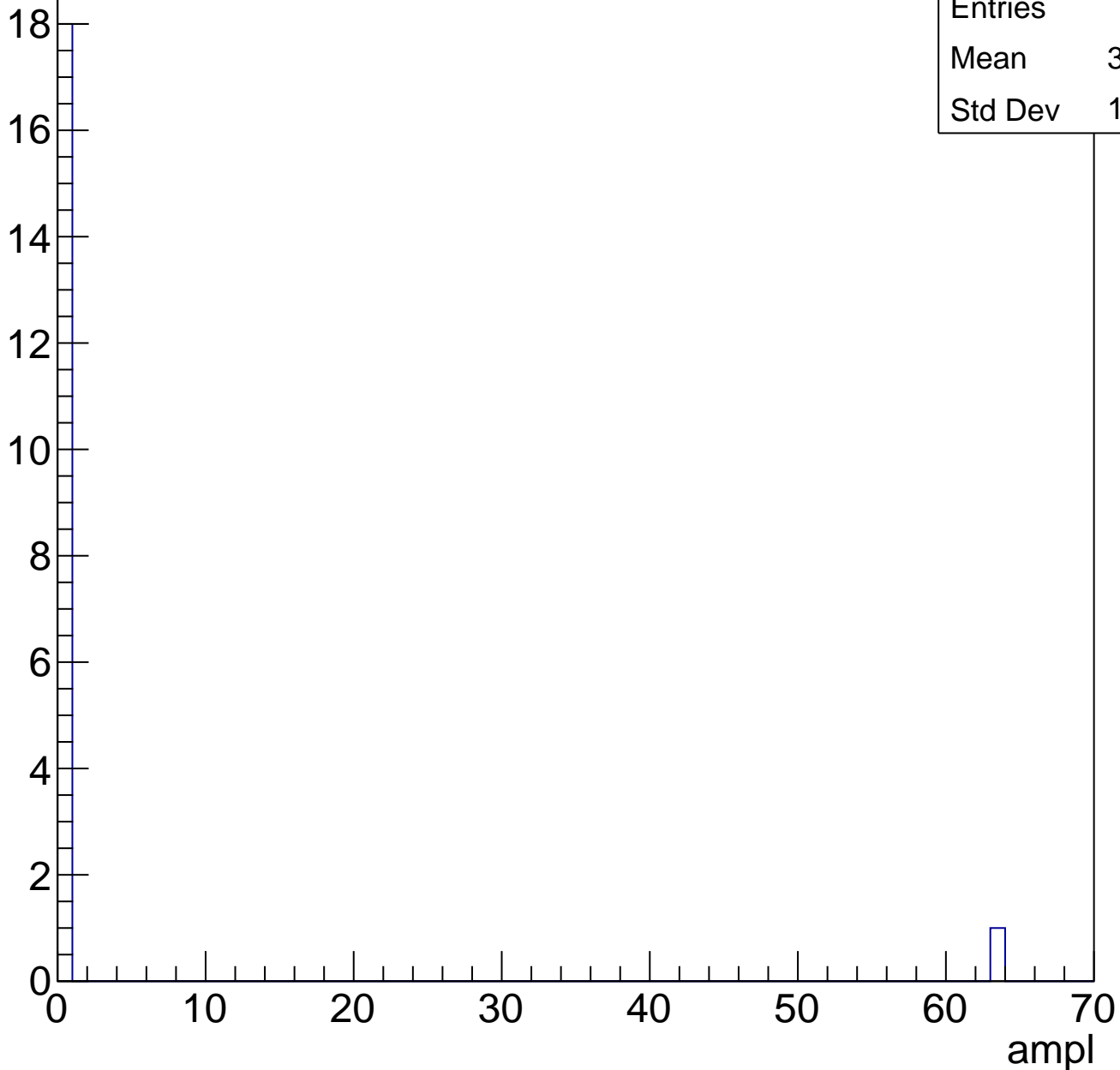


B1L103S, U21-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U21-ch64, adc0

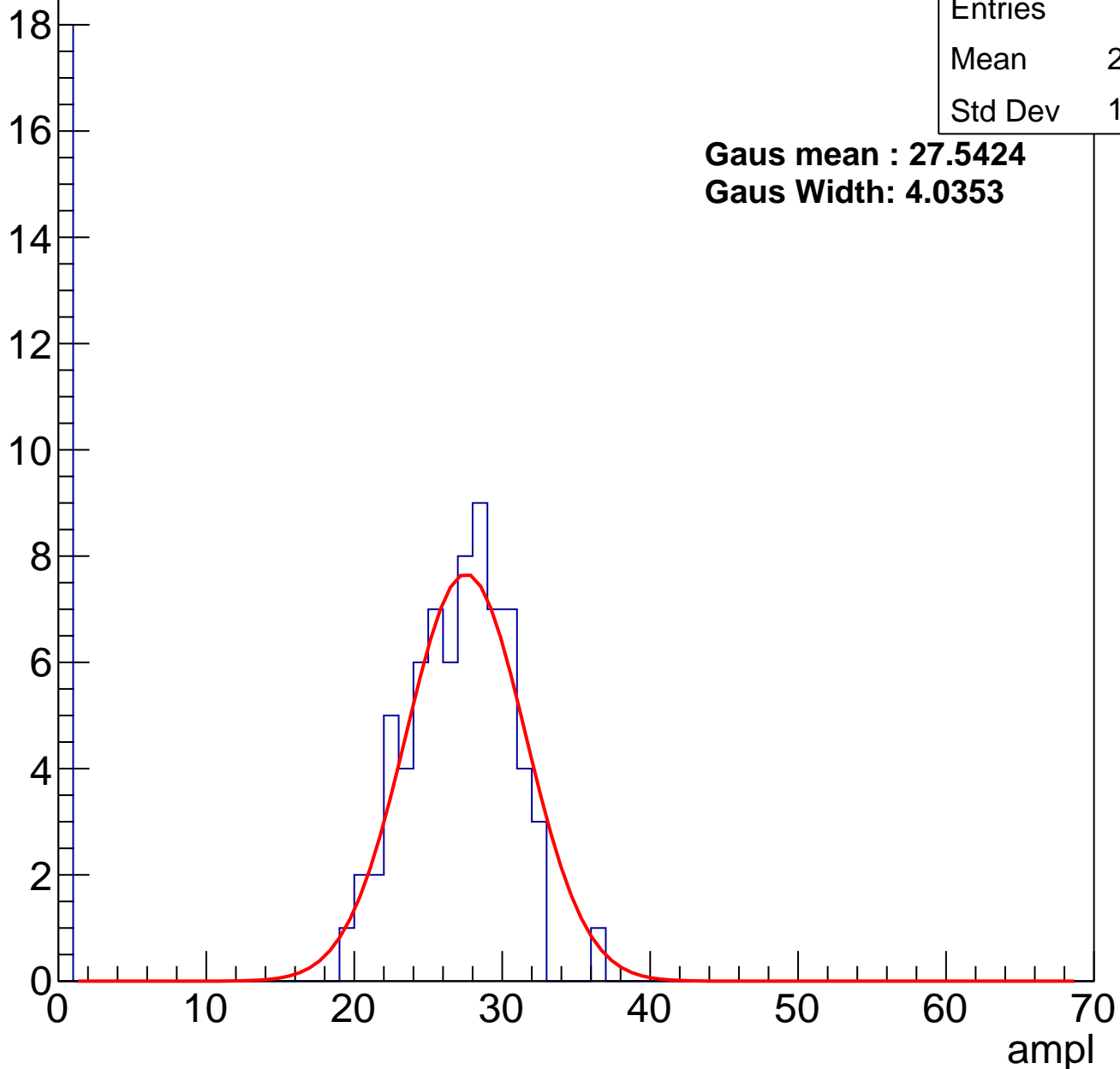
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	21.28
Std Dev	11.06

Gaus mean : 27.5424

Gaus Width: 4.0353

Entry



B1L103S, U21-ch64, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	32.7
Std Dev	6.658

Gaus mean : 34.4725

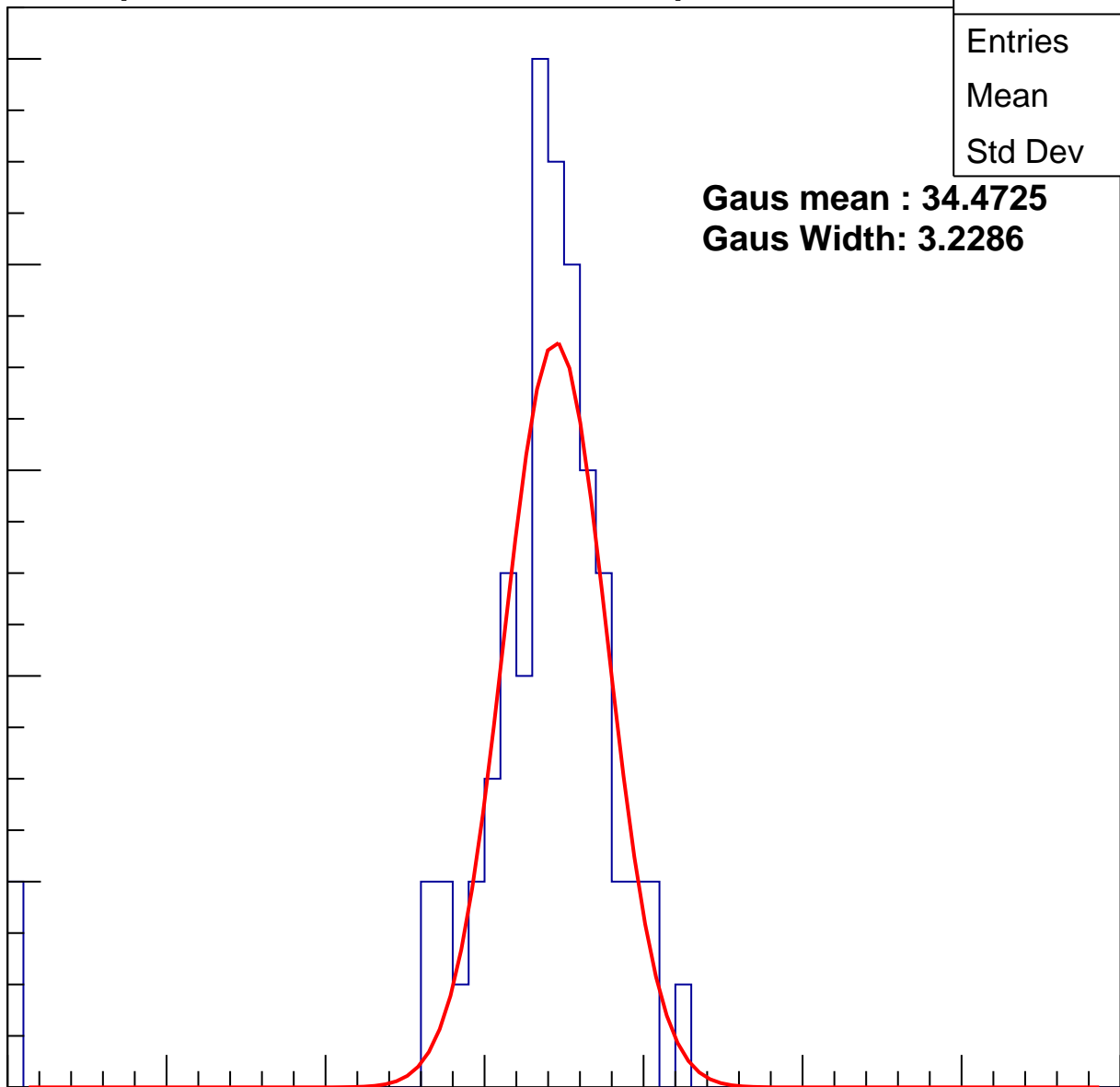
Gaus Width: 3.2286

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch64, adc2

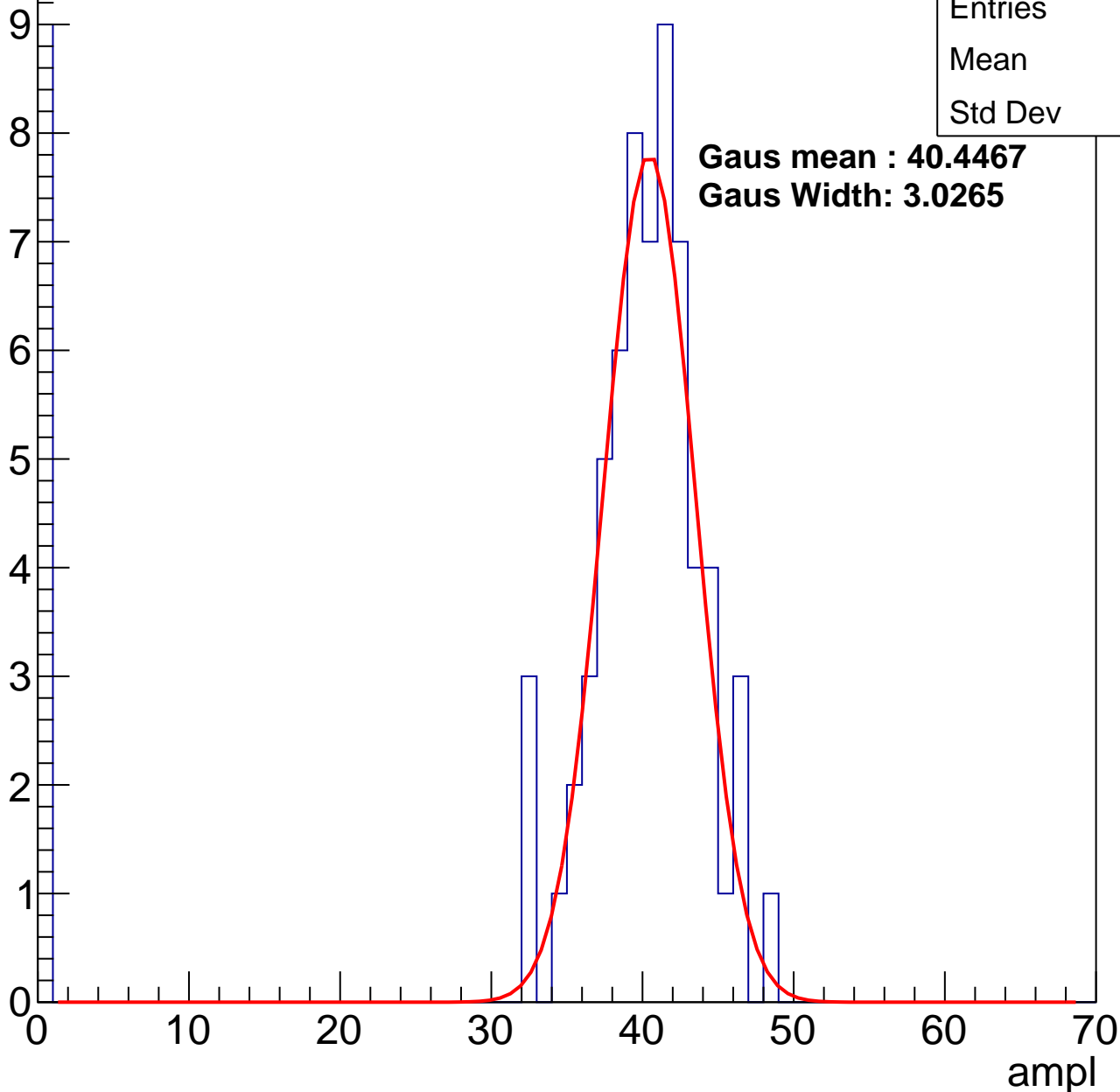
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35
Std Dev	13.5

Gaus mean : 40.4467

Gaus Width: 3.0265

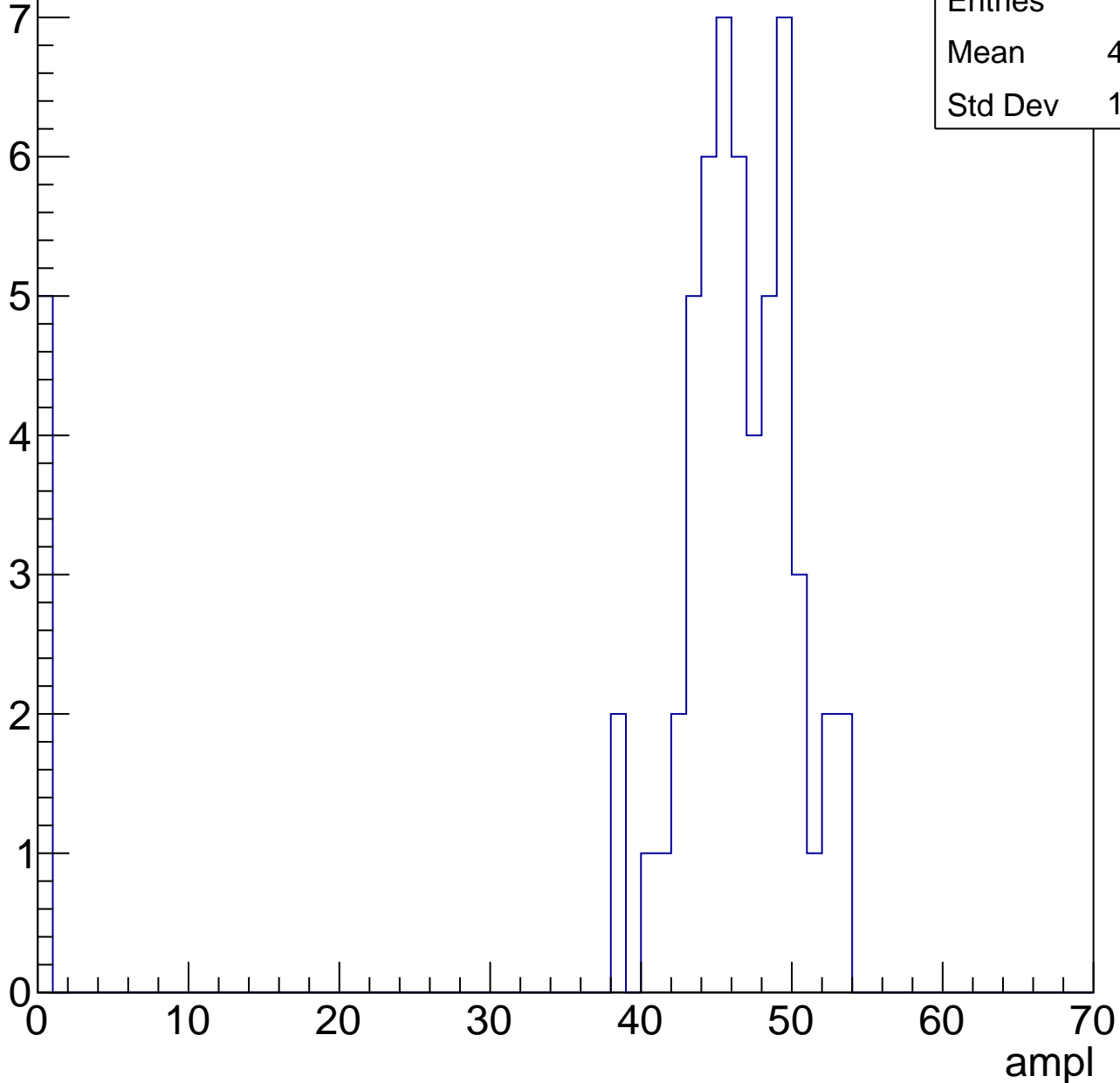


B1L103S, U21-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42.25
Std Dev	13.26

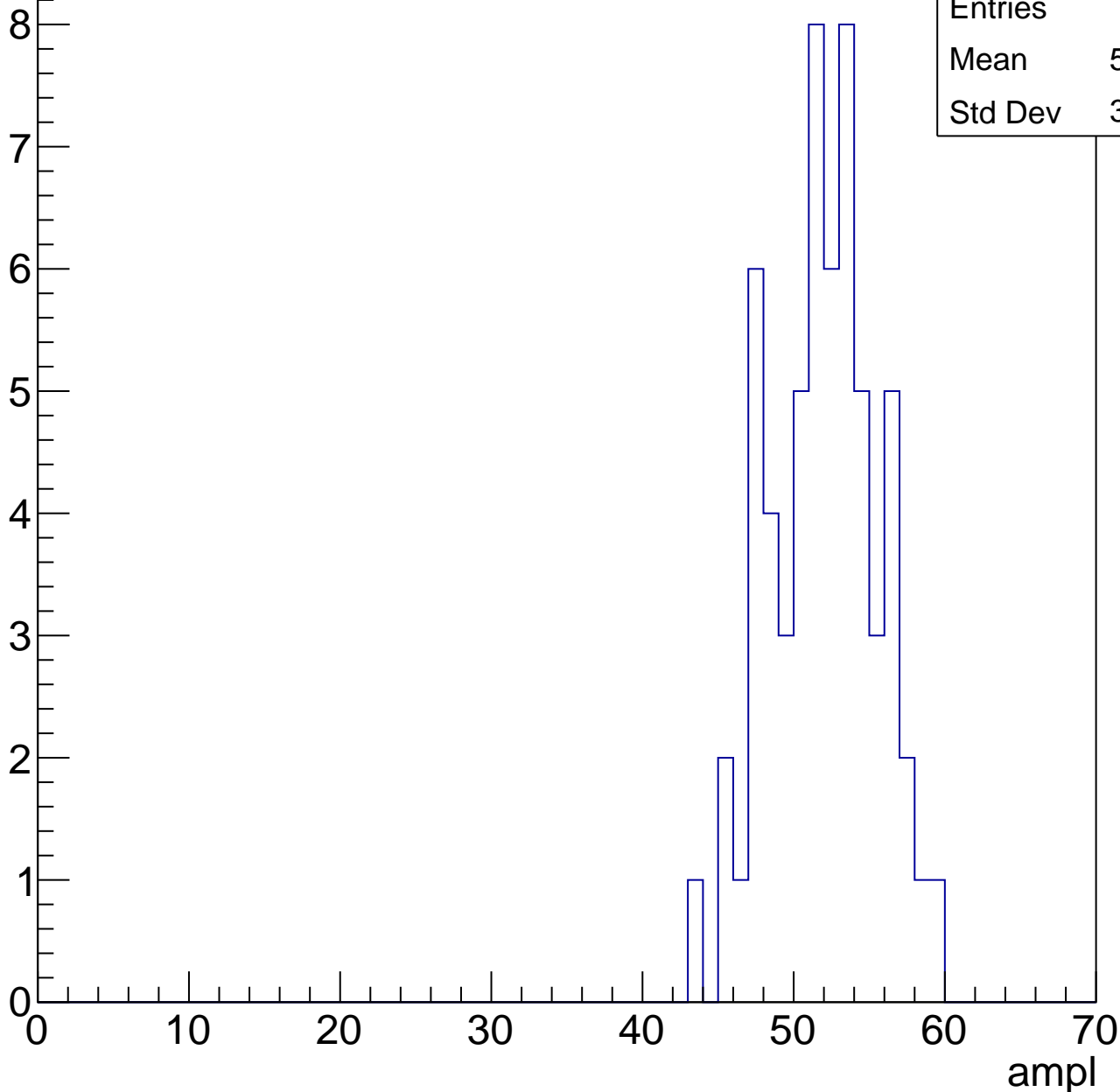


B1L103S, U21-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	51.48
Std Dev	3.476

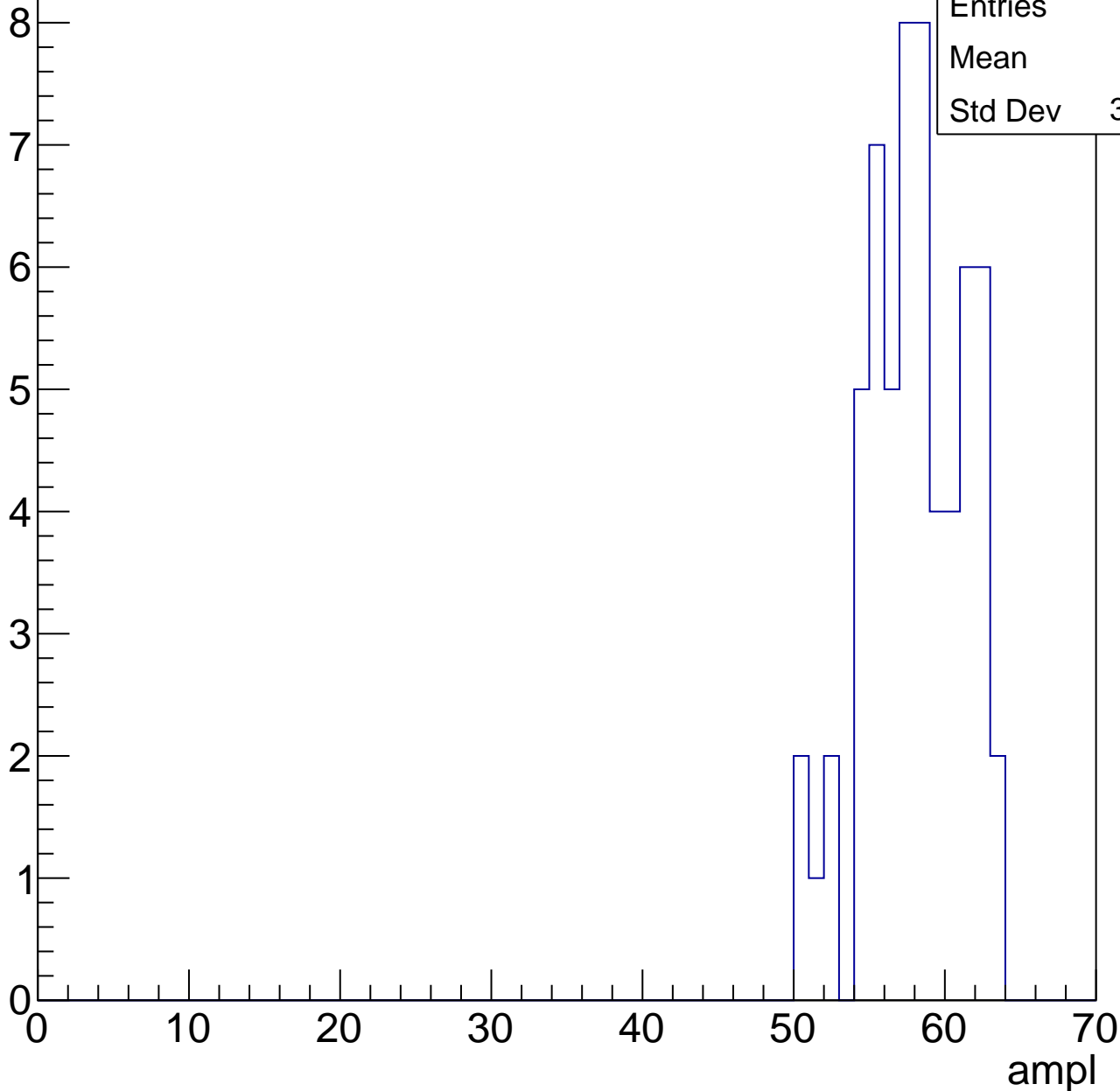


B1L103S, U21-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.5
Std Dev	3.227

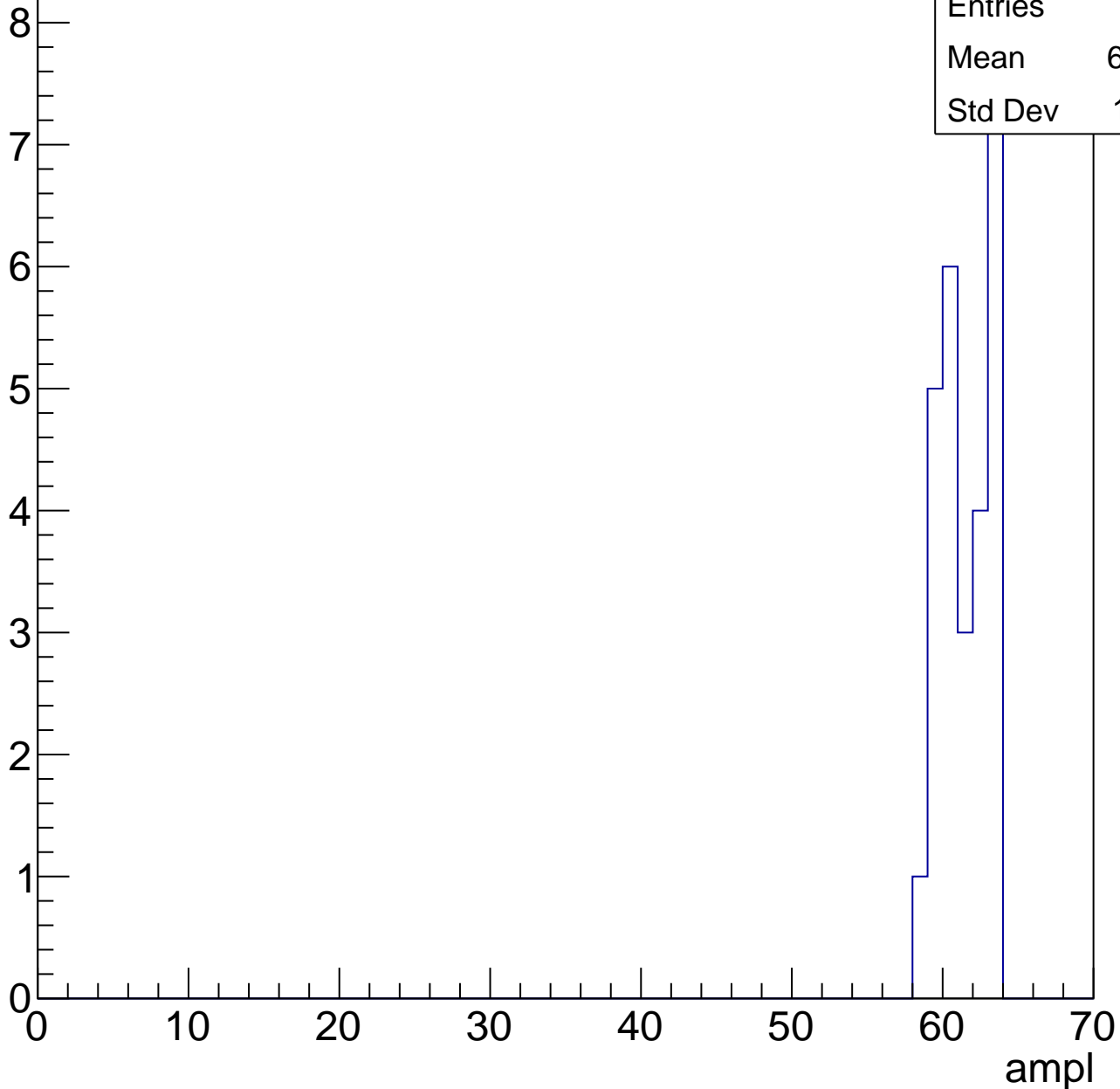


B1L103S, U21-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

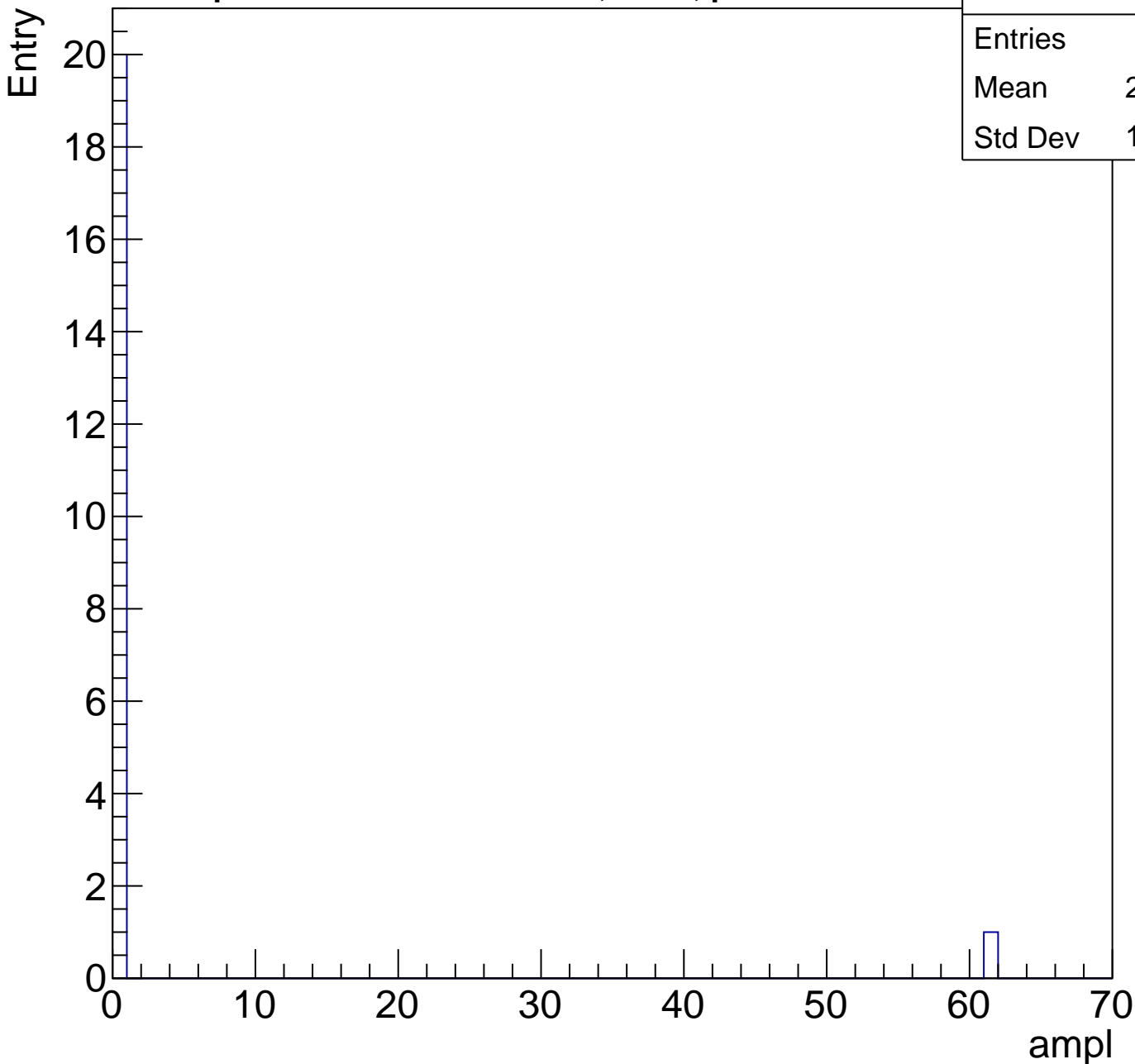
Entries	27
Mean	61.04
Std Dev	1.621



B1L103S, U21-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	2.905
Std Dev	12.99



B1L103S, U21-ch65, adc0

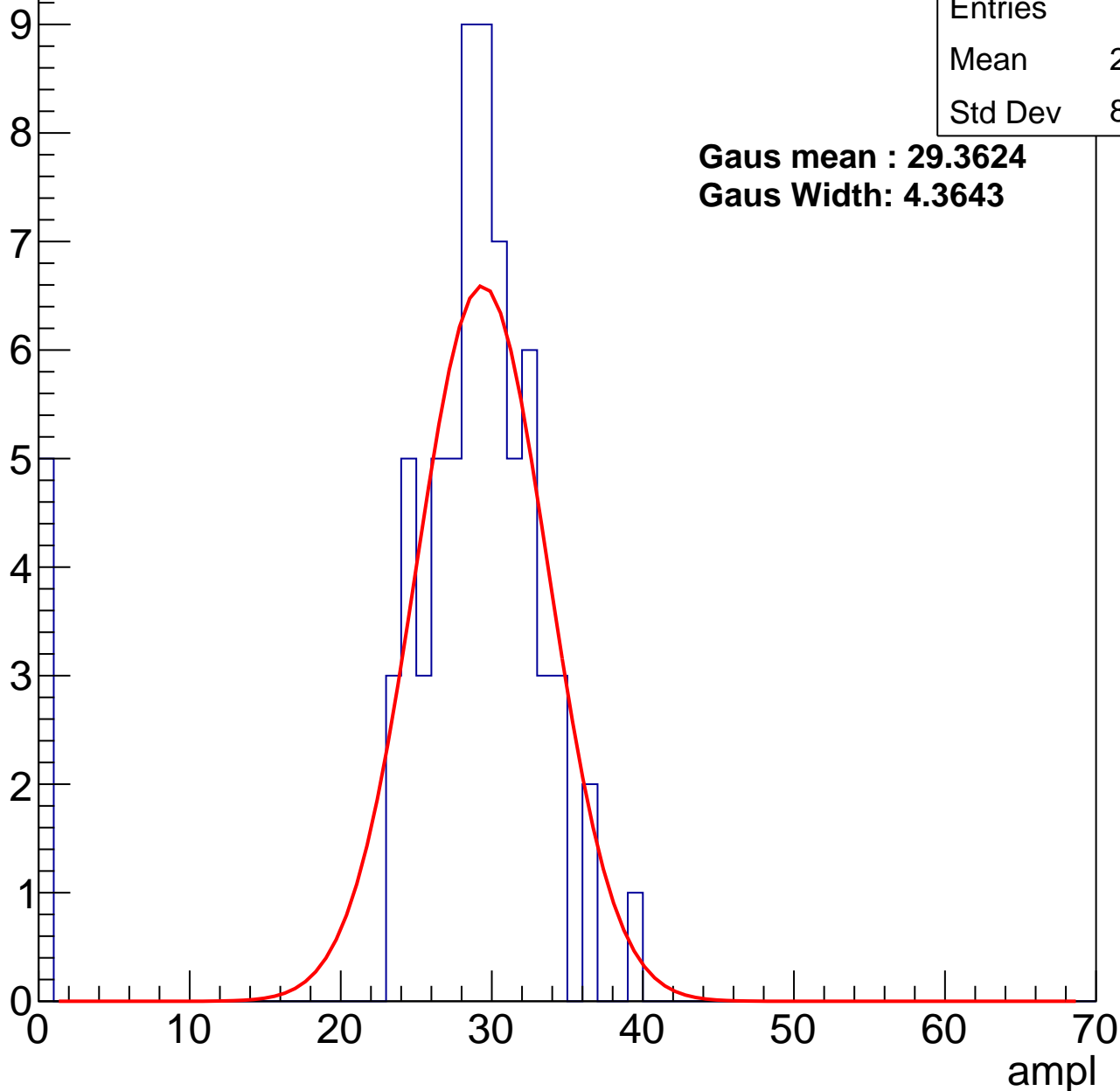
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	26.92
Std Dev	8.096

Gaus mean : 29.3624

Gaus Width: 4.3643



B1L103S, U21-ch65, adc1

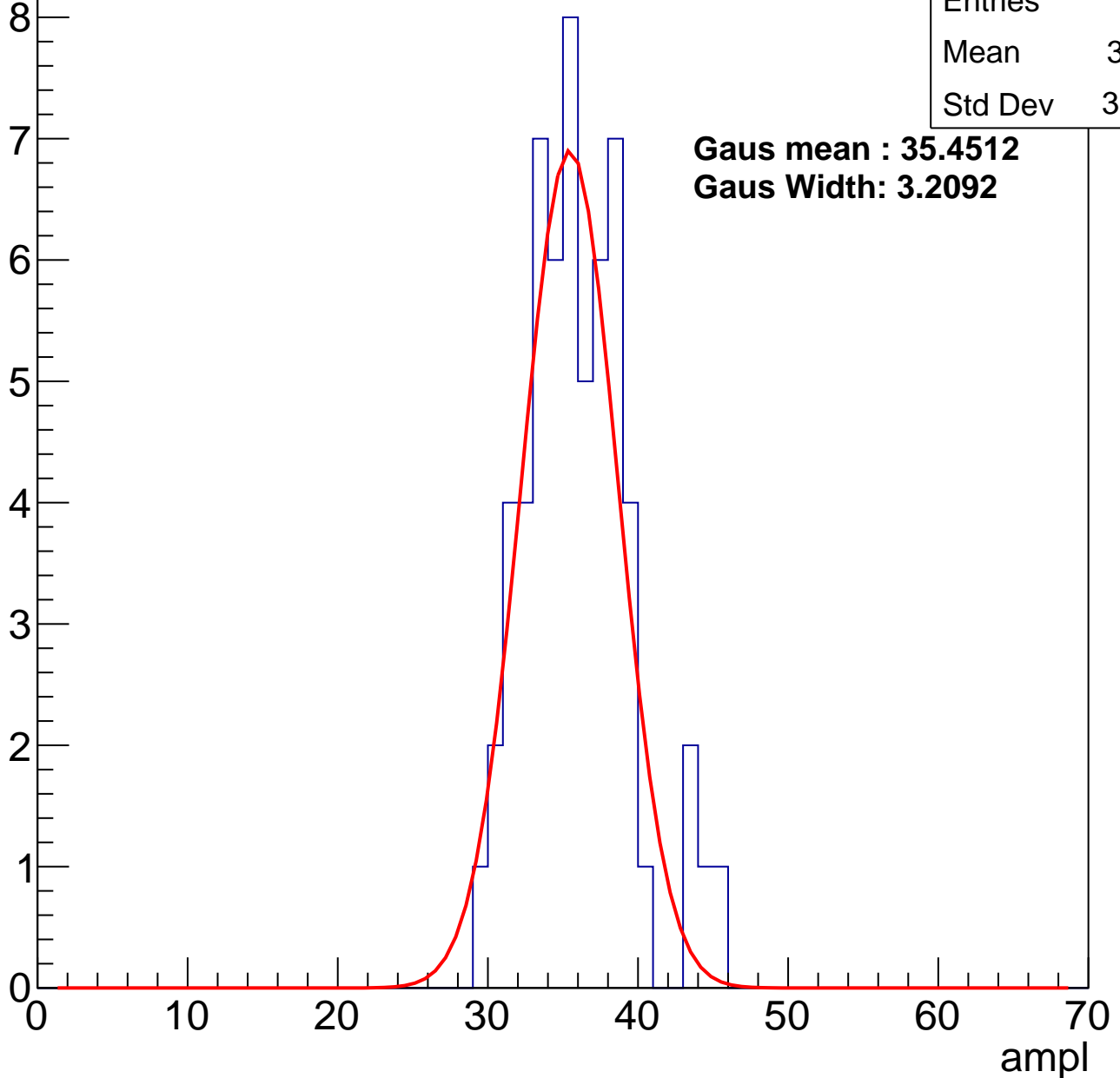
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.51
Std Dev	3.432

Gaus mean : 35.4512

Gaus Width: 3.2092



B1L103S, U21-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	35.49
Std Dev	15.1

Gaus mean : 41.7109
Gaus Width: 3.7297

Entry

10

8

6

4

2

0

0

10

20

30

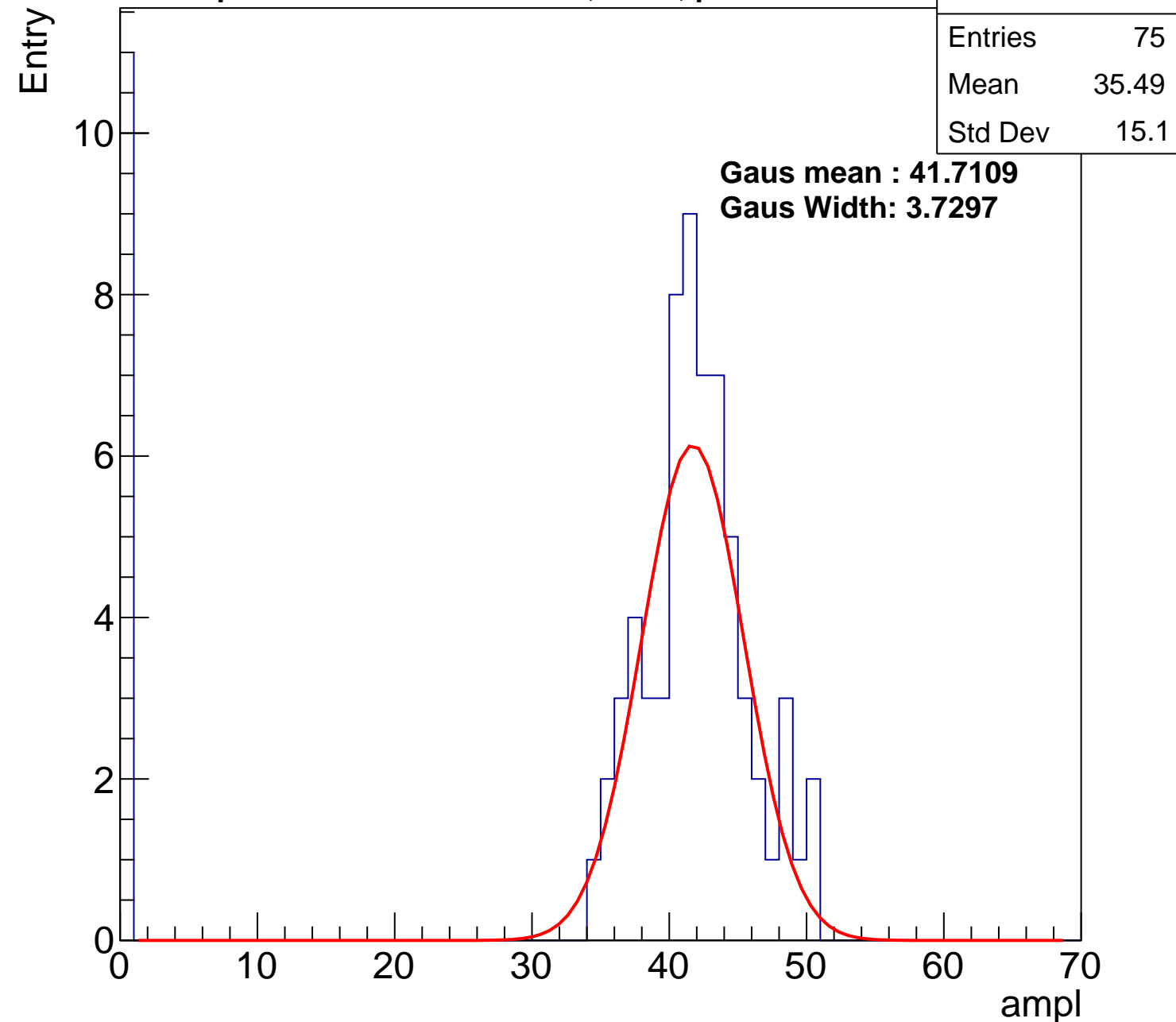
40

50

60

70

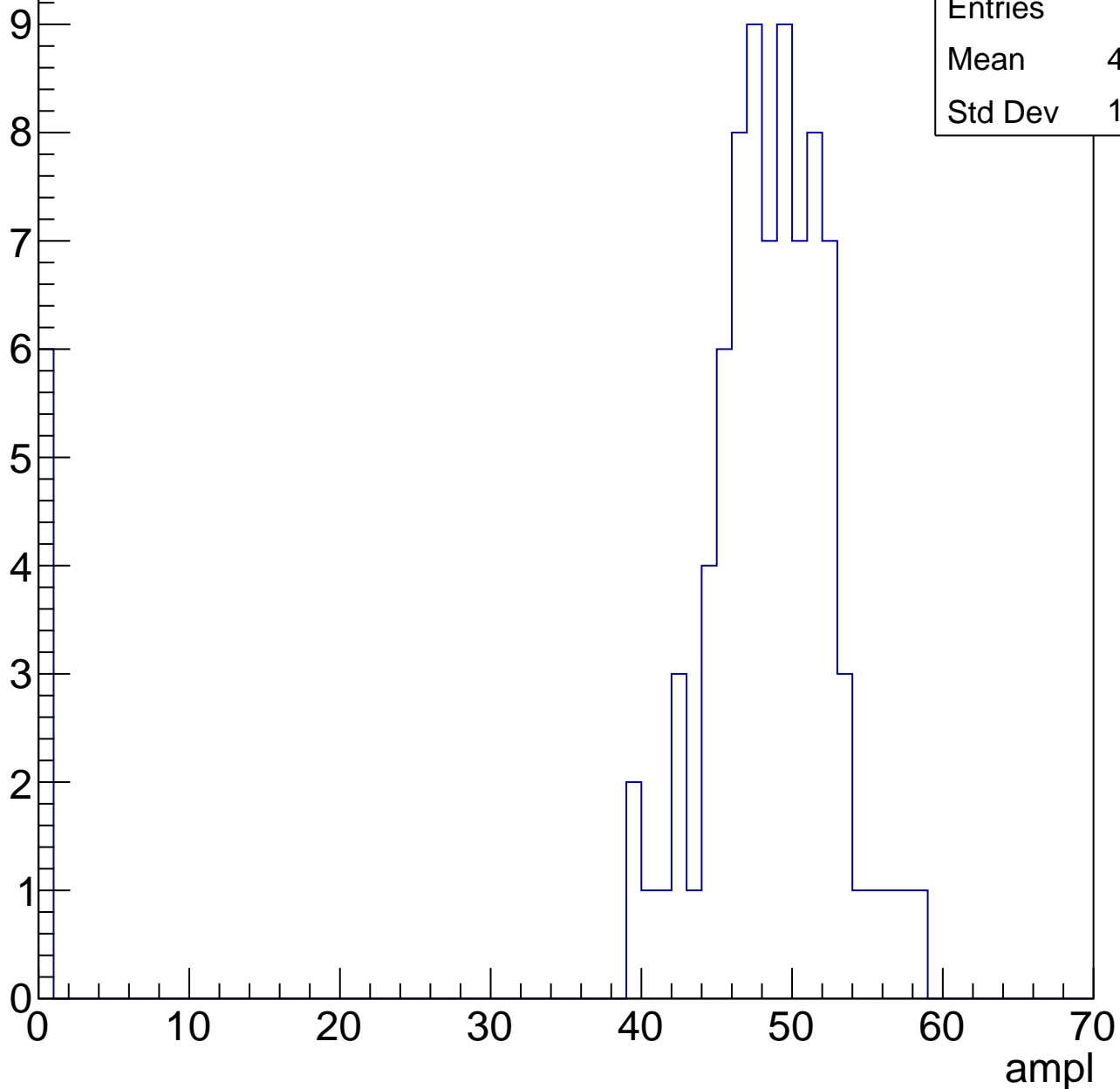
ampl



B1L103S, U21-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

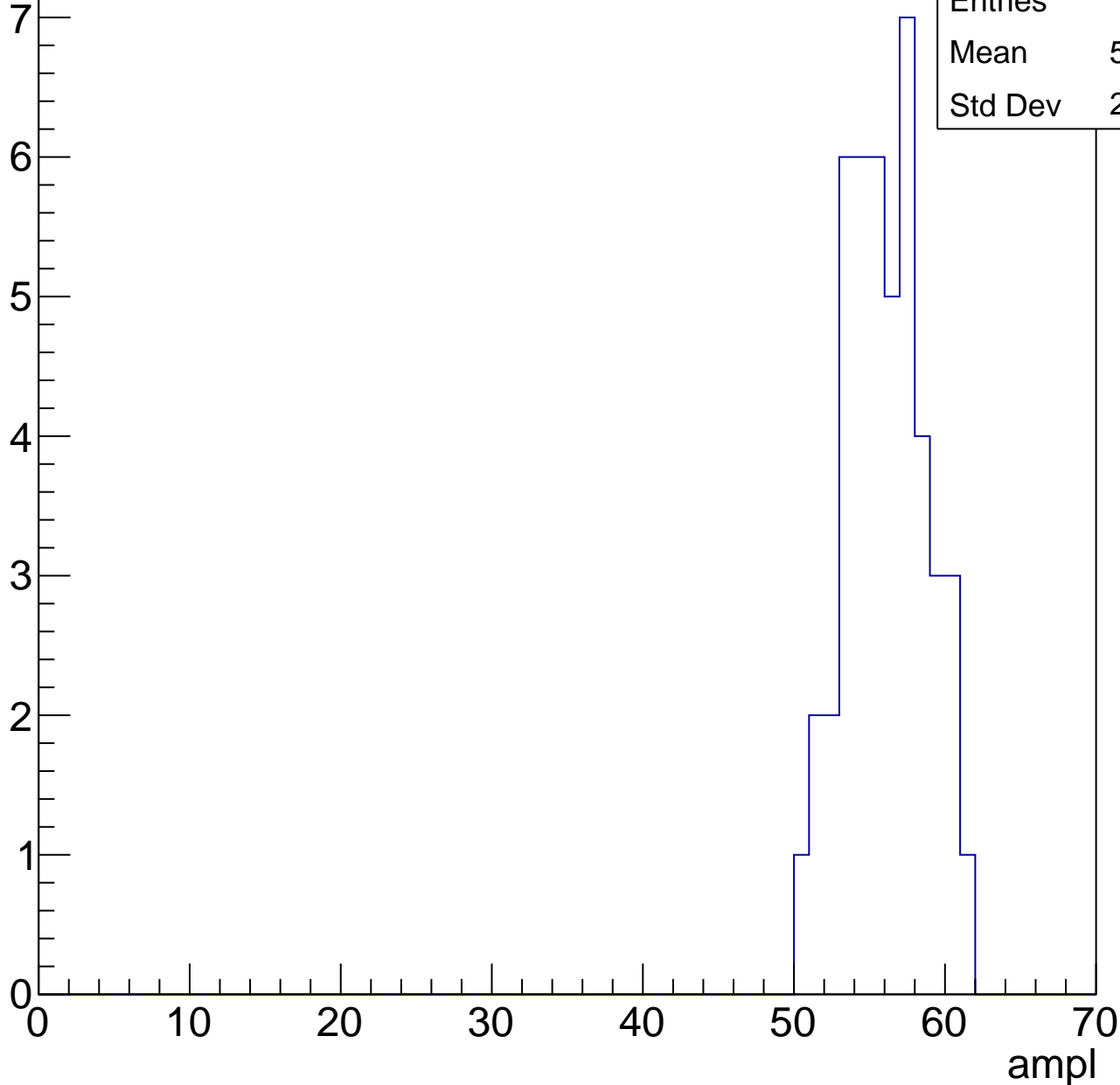
Entry



B1L103S, U21-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



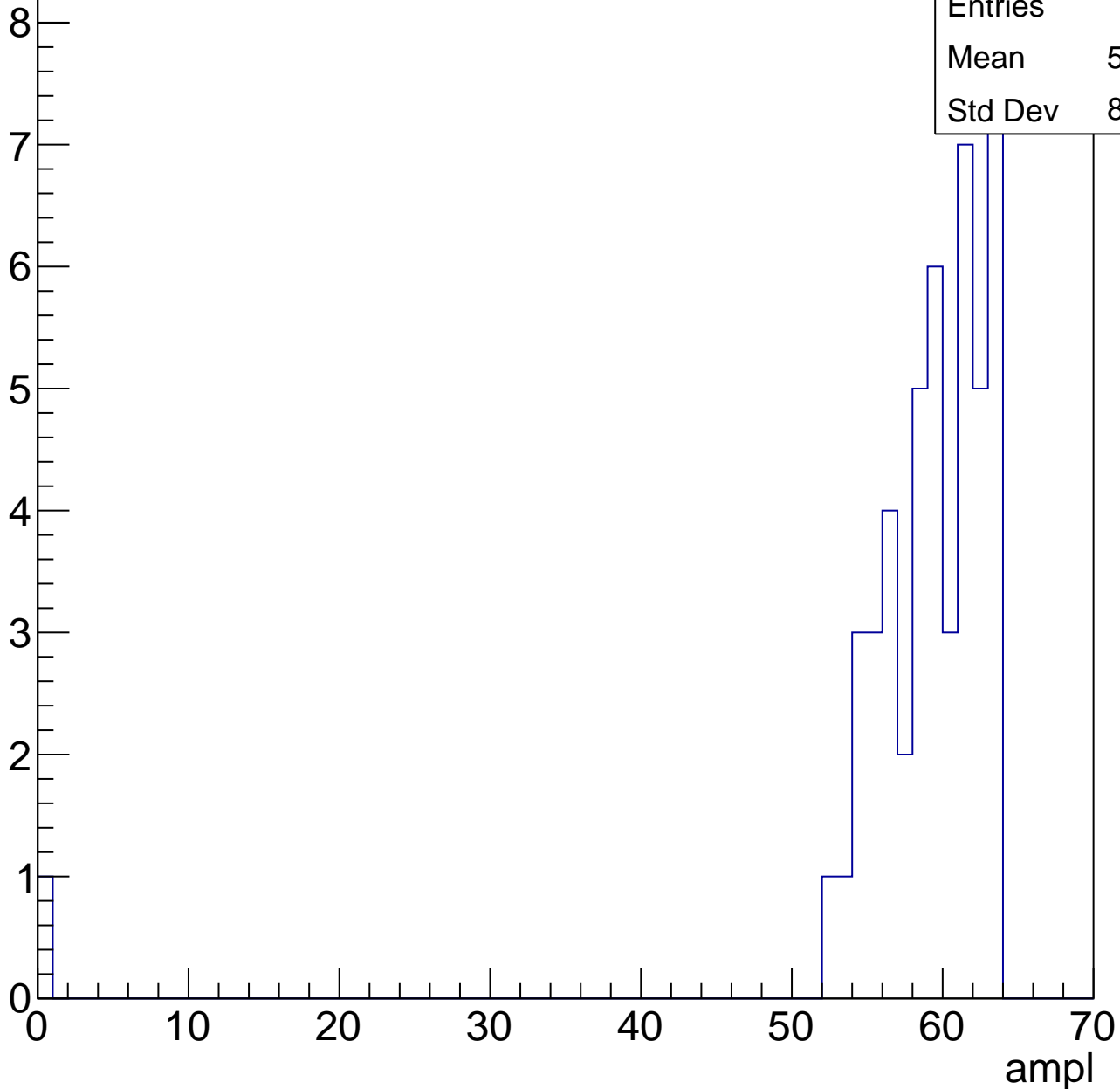
Entries	46
Mean	55.59
Std Dev	2.626

B1L103S, U21-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.86
Std Dev	8.896

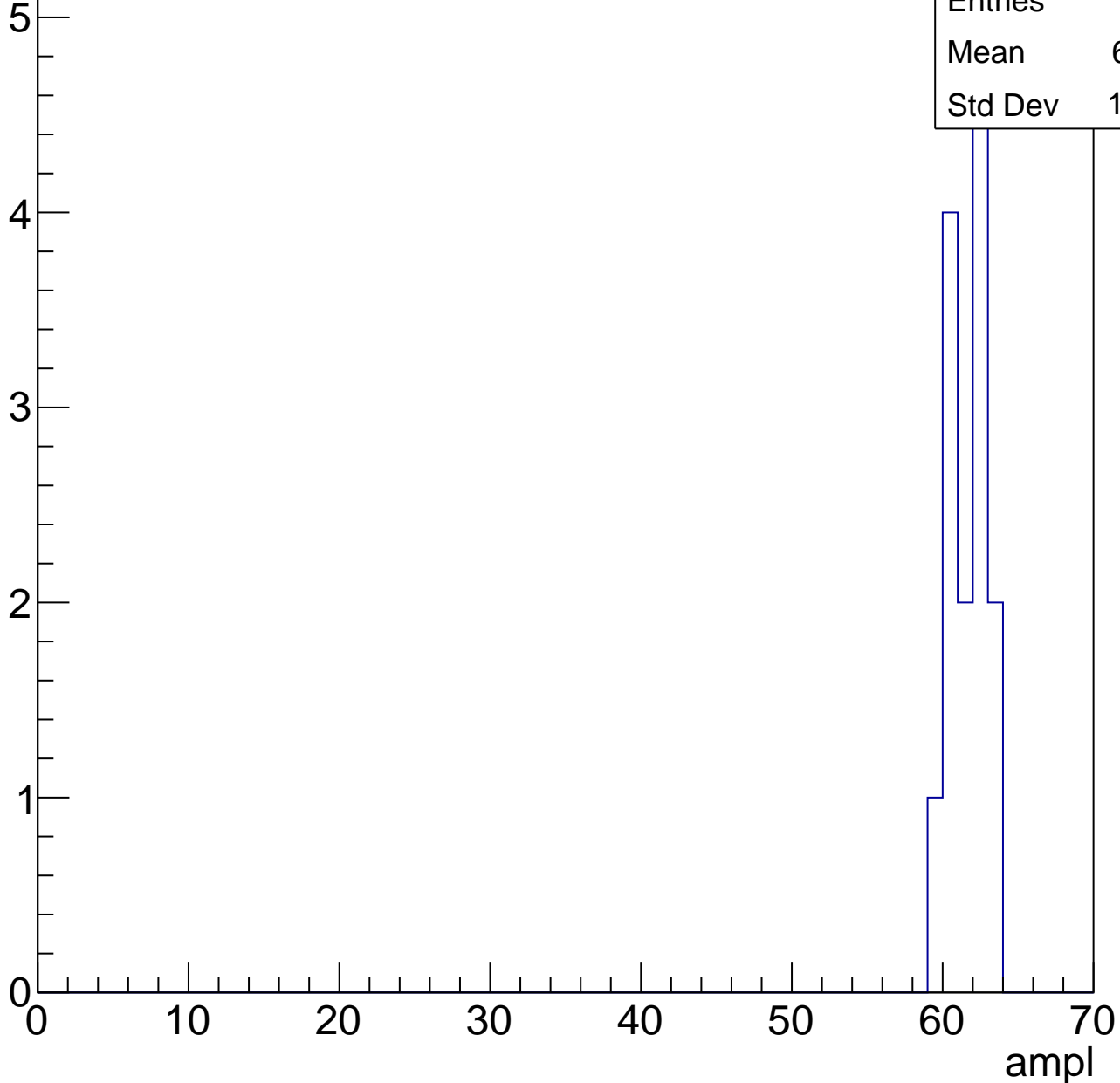


B1L103S, U21-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.21
Std Dev	1.206



B1L103S, U21-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch66, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	24.69
Std Dev	9.534

Gaus mean : 27.9127

Gaus Width: 3.6930

Entry

10

8

6

4

2

0

0

10

20

30

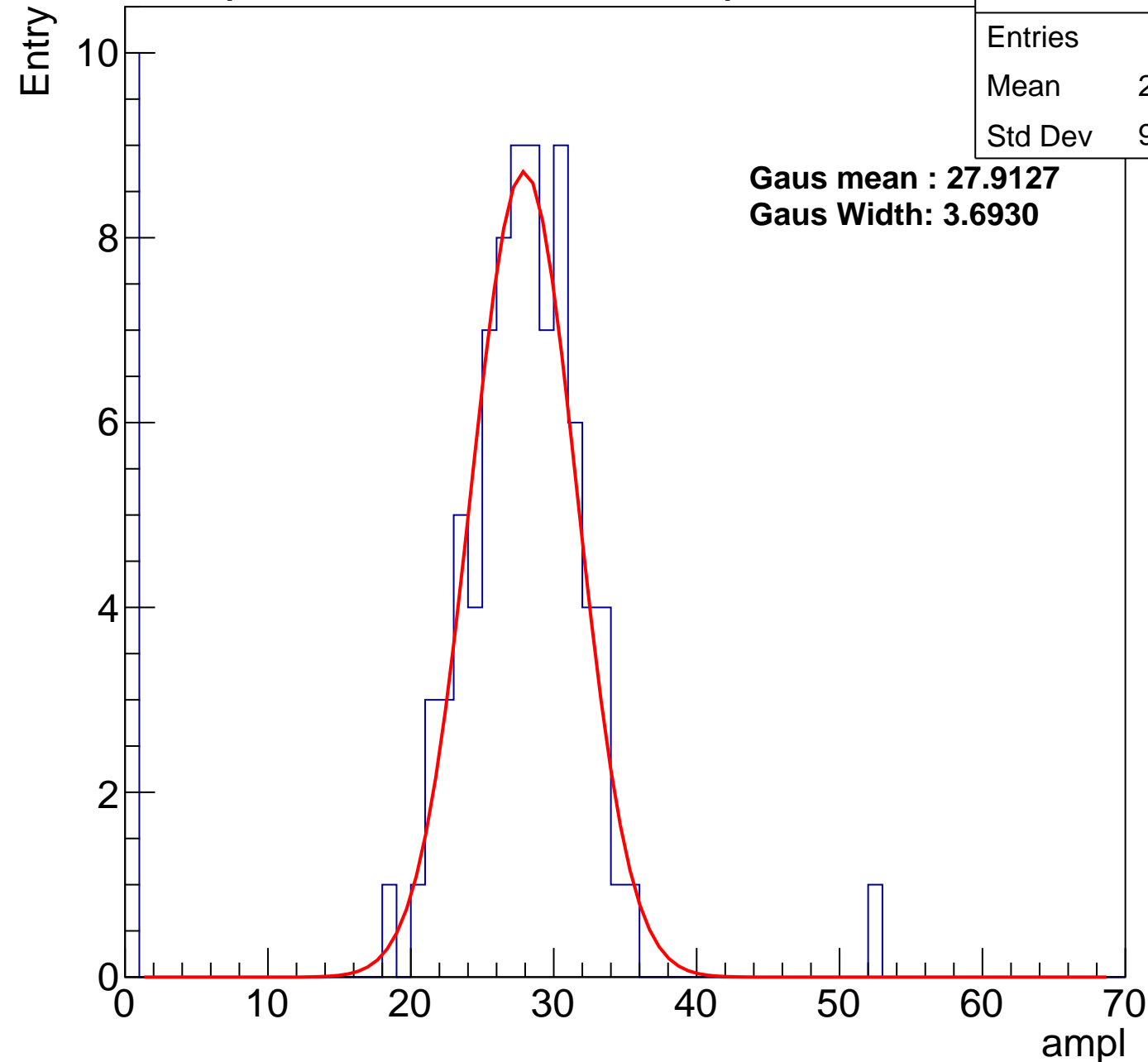
40

50

60

70

ampl



B1L103S, U21-ch66, adc1

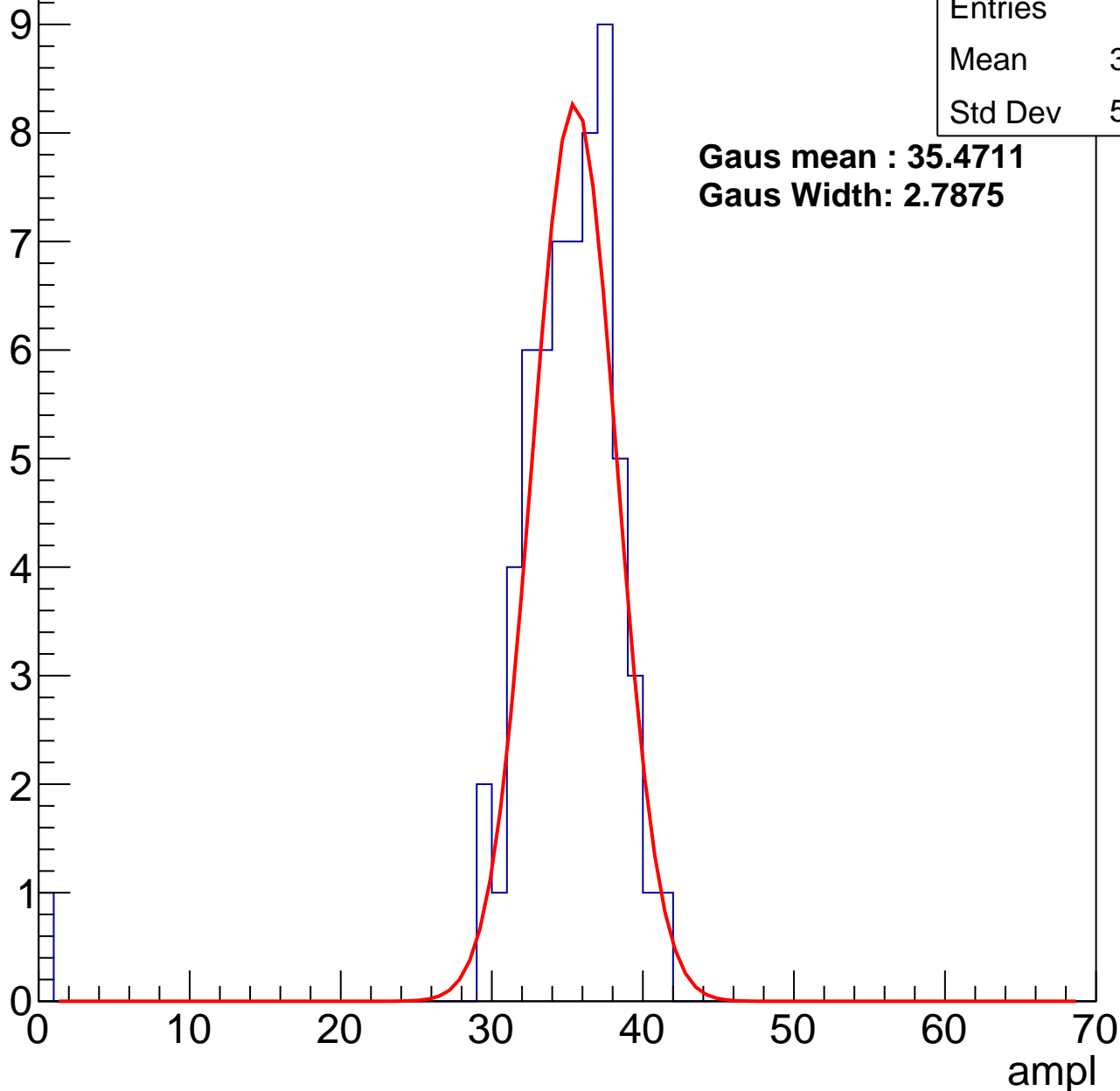
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.33
Std Dev	5.187

Gaus mean : 35.4711

Gaus Width: 2.7875



B1L103S, U21-ch66, adc2

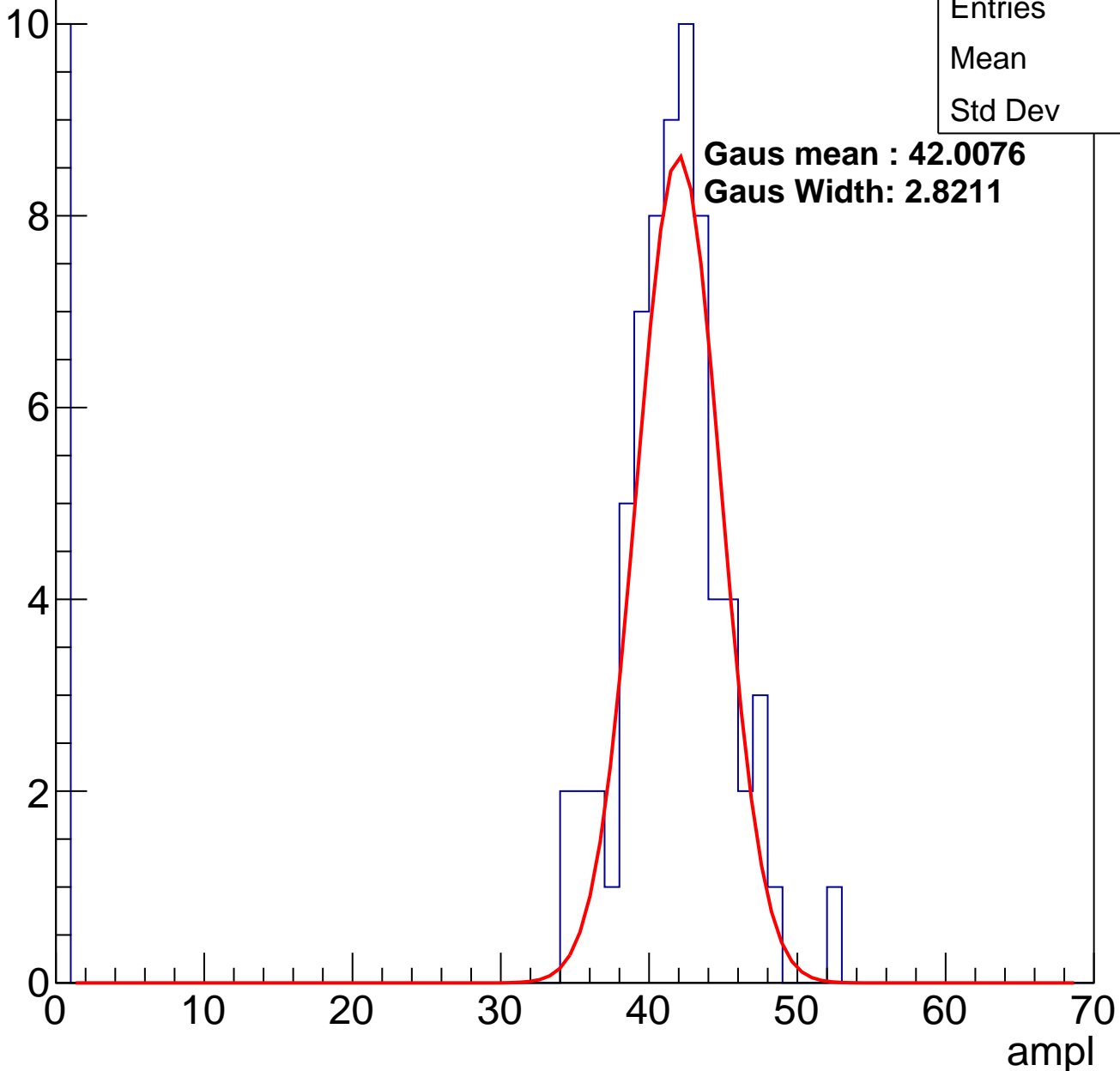
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	36.1
Std Dev	14.1

Gaus mean : 42.0076

Gaus Width: 2.8211

Entry

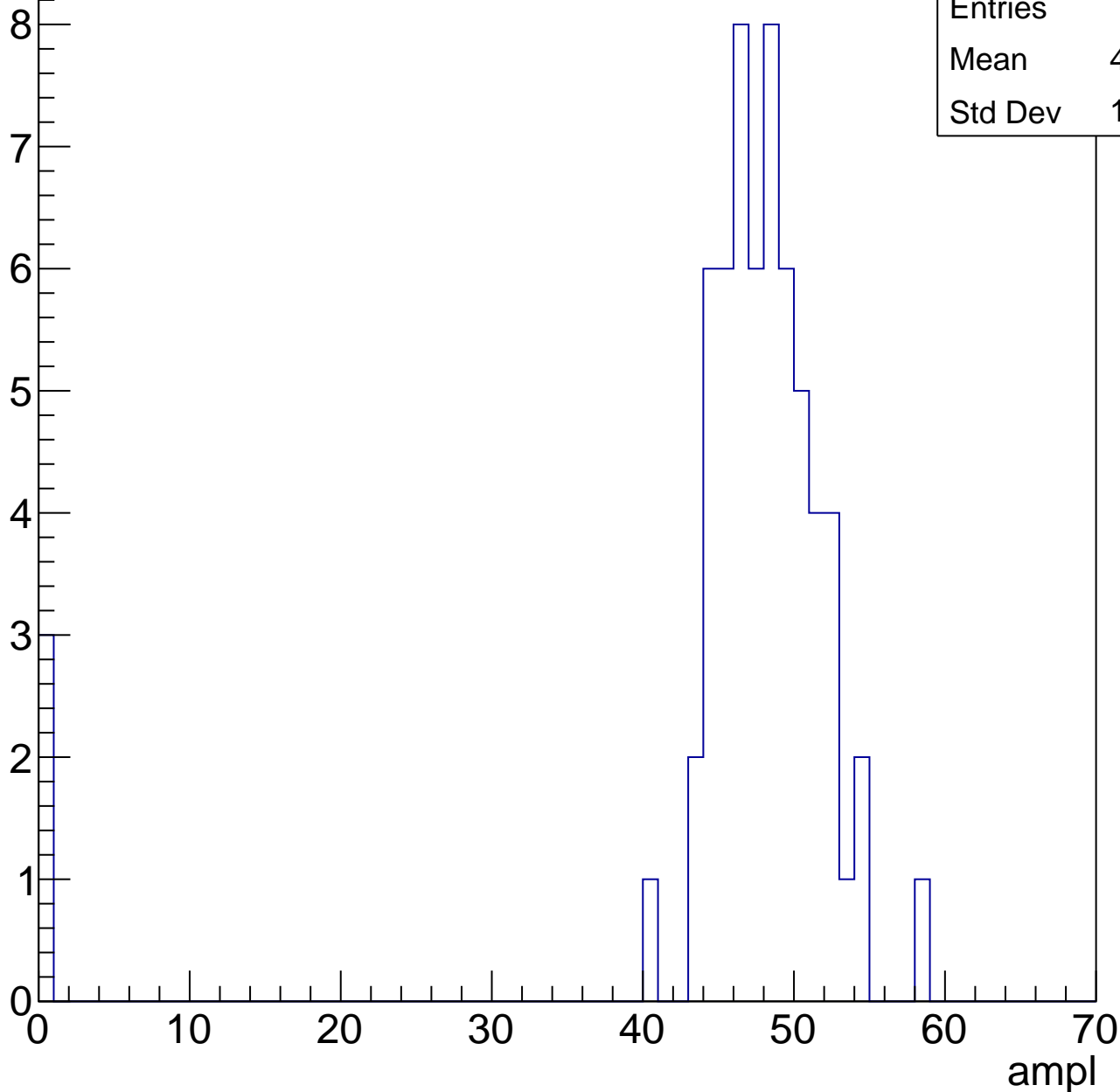


B1L103S, U21-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2

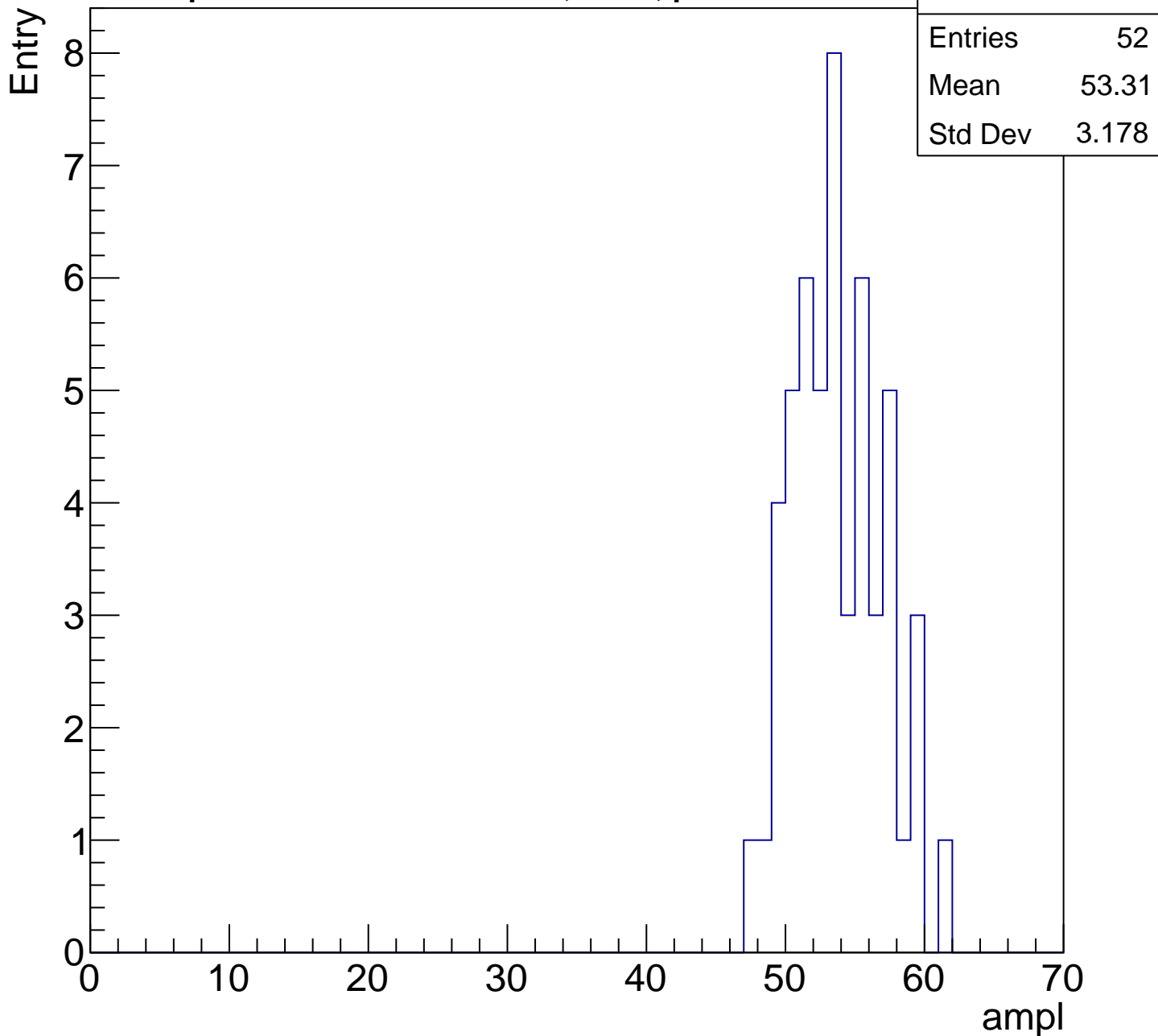
Entry

Entries	63
Mean	45.54
Std Dev	10.66



B1L103S, U21-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

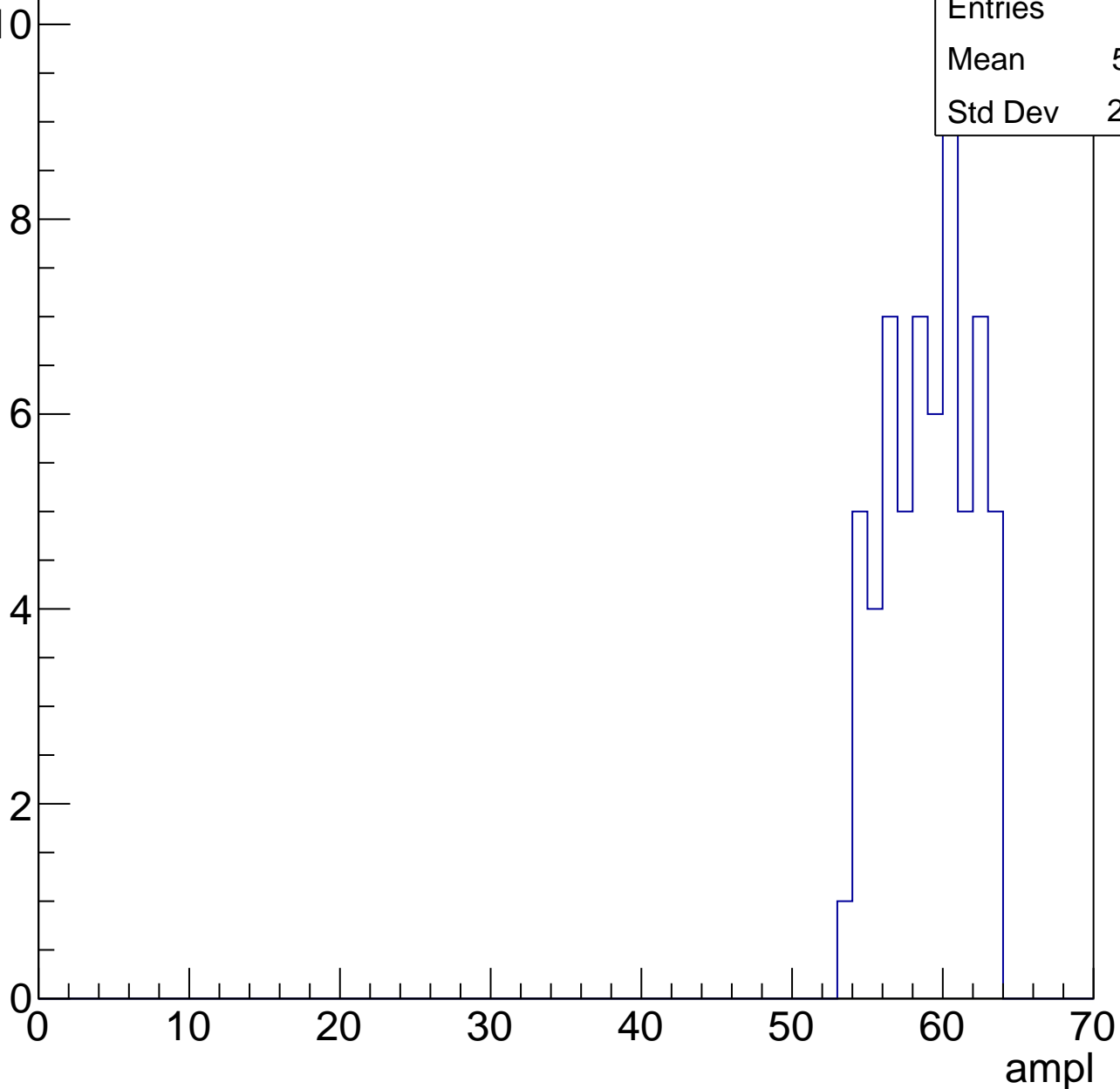


B1L103S, U21-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	58.61
Std Dev	2.779

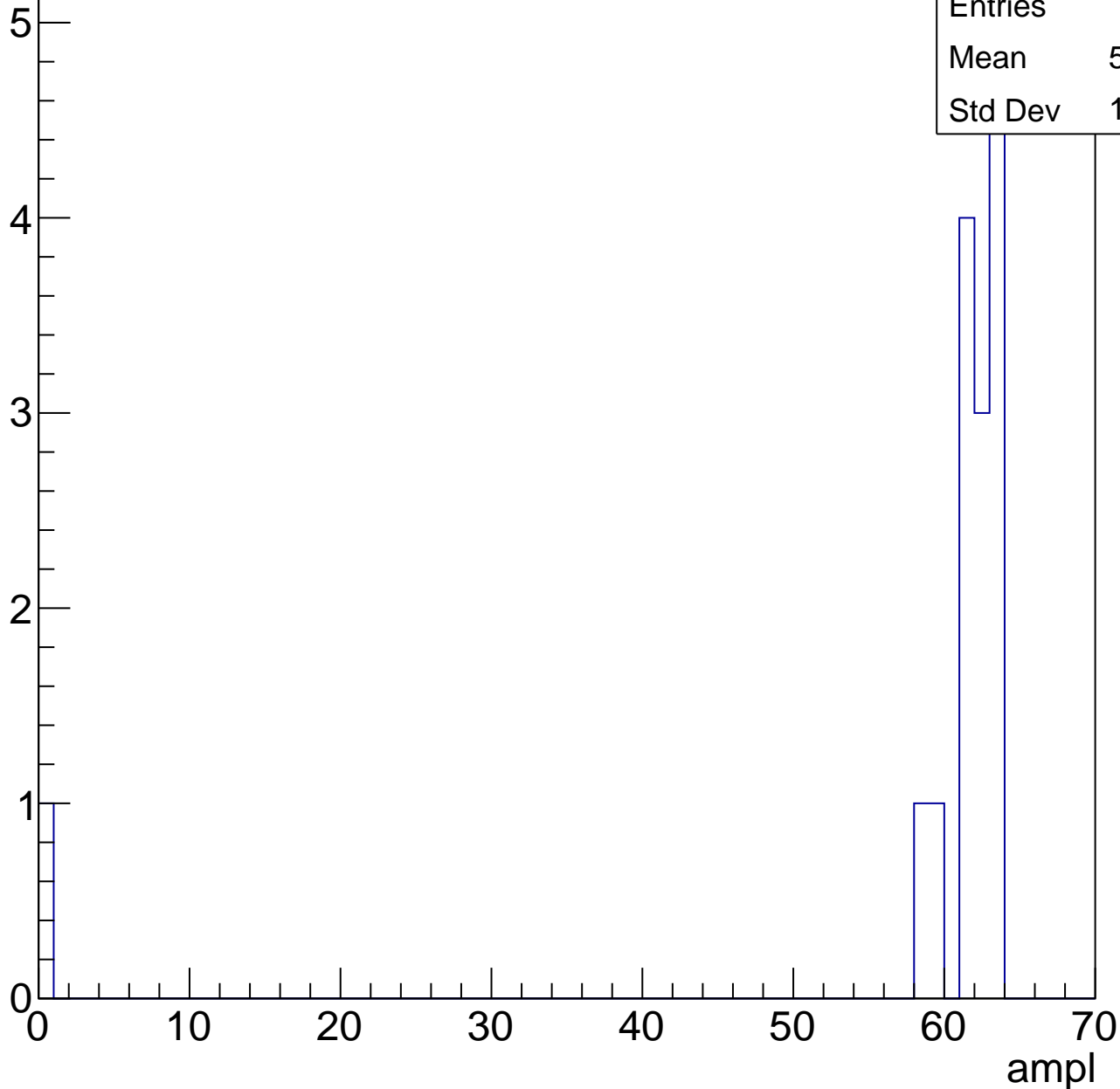


B1L103S, U21-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.47
Std Dev	15.43



B1L103S, U21-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch67, adc0

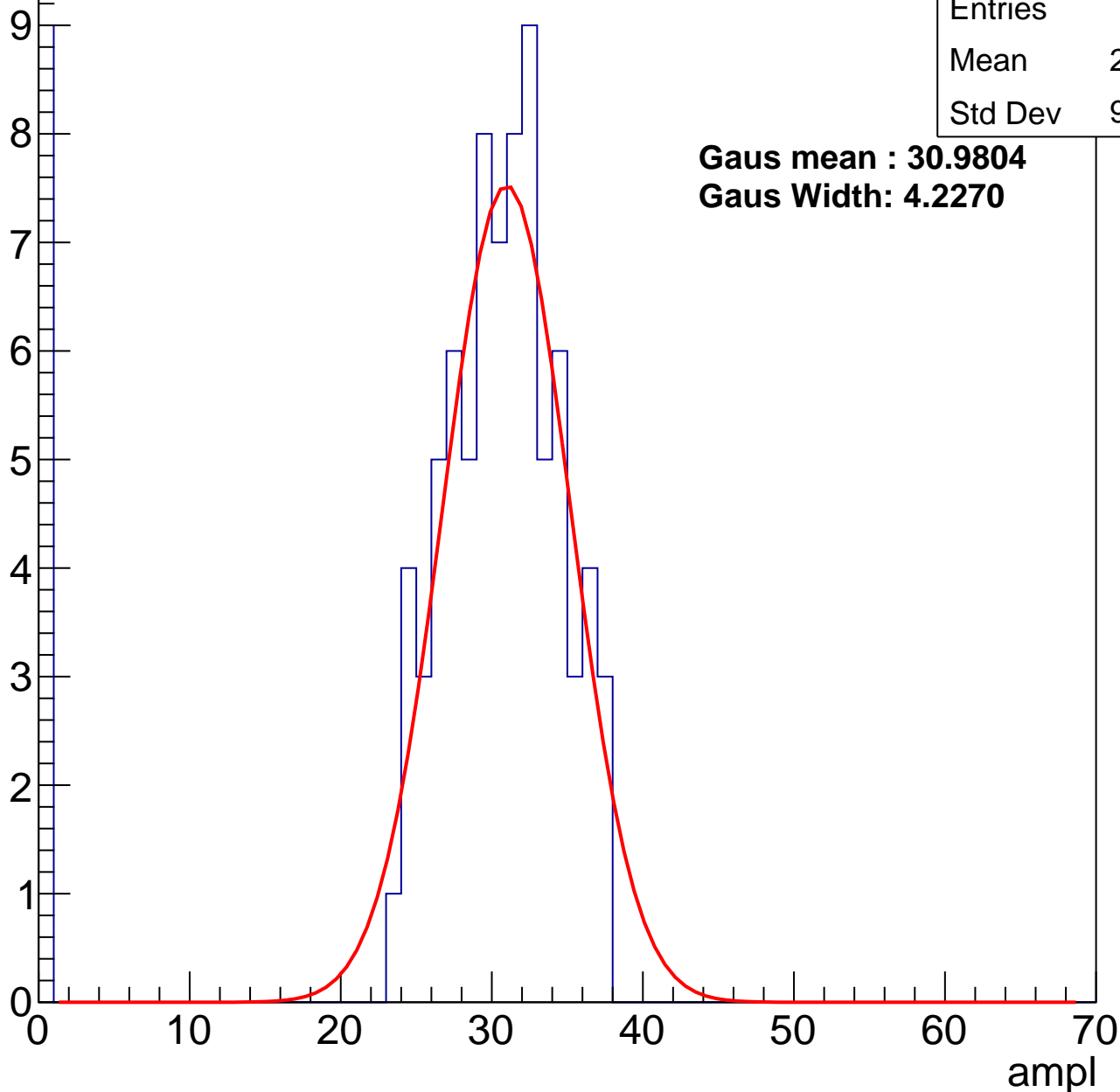
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	27.13
Std Dev	9.866

Gaus mean : 30.9804

Gaus Width: 4.2270



B1L103S, U21-ch67, adc1

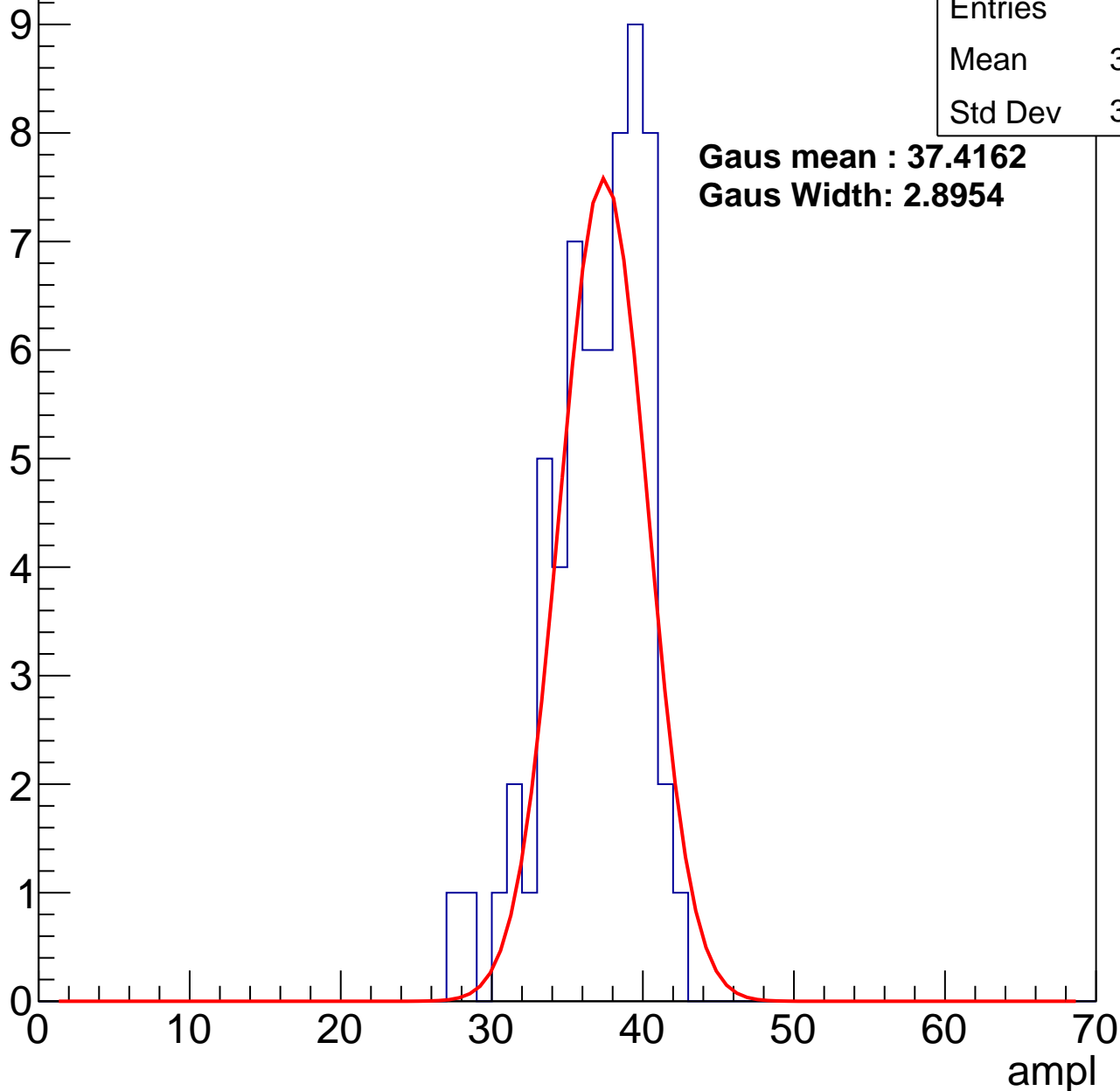
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	36.48
Std Dev	3.196

Gaus mean : 37.4162

Gaus Width: 2.8954



B1L103S, U21-ch67, adc2

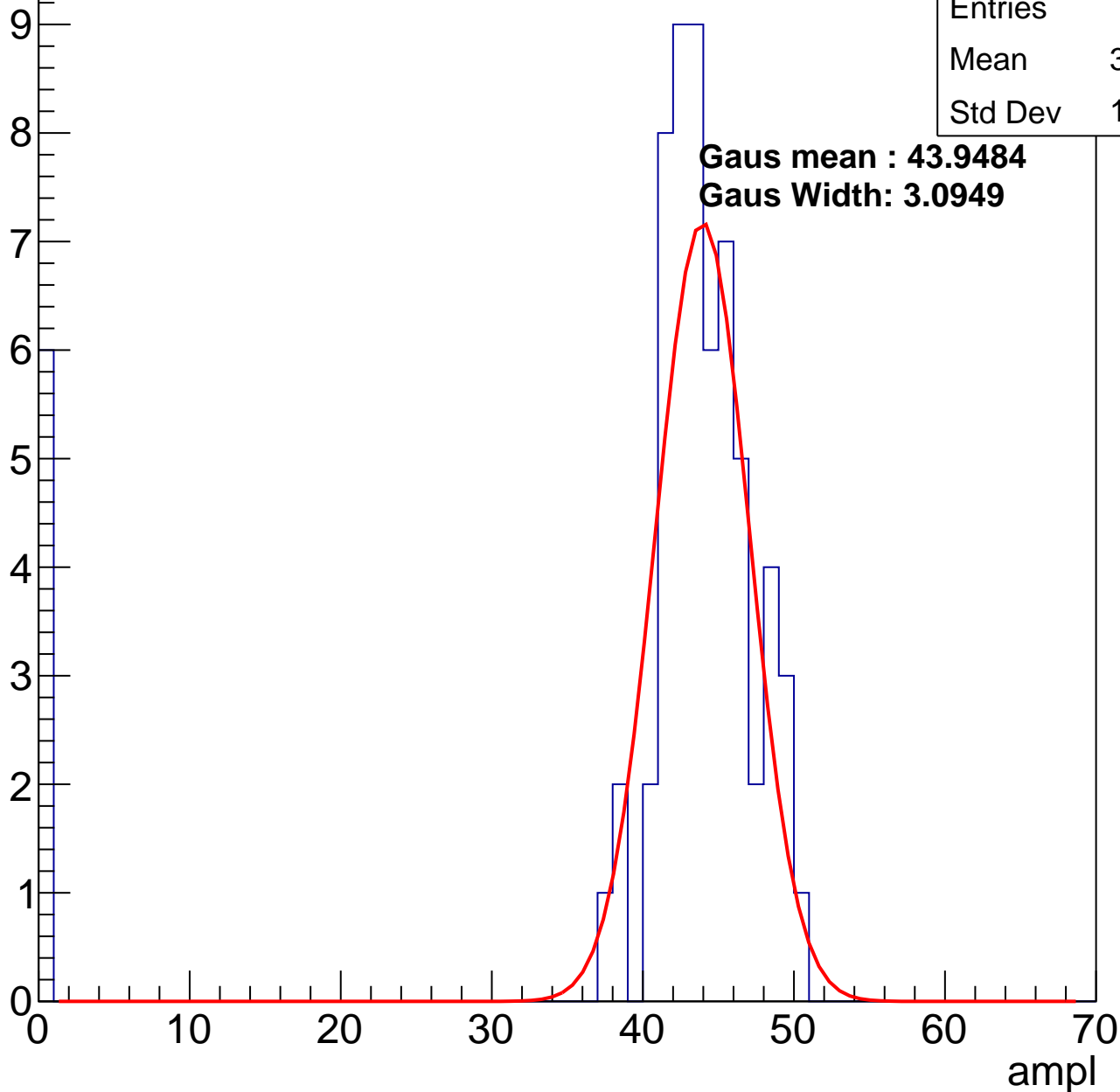
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	39.66
Std Dev	12.94

Gaus mean : 43.9484

Gaus Width: 3.0949

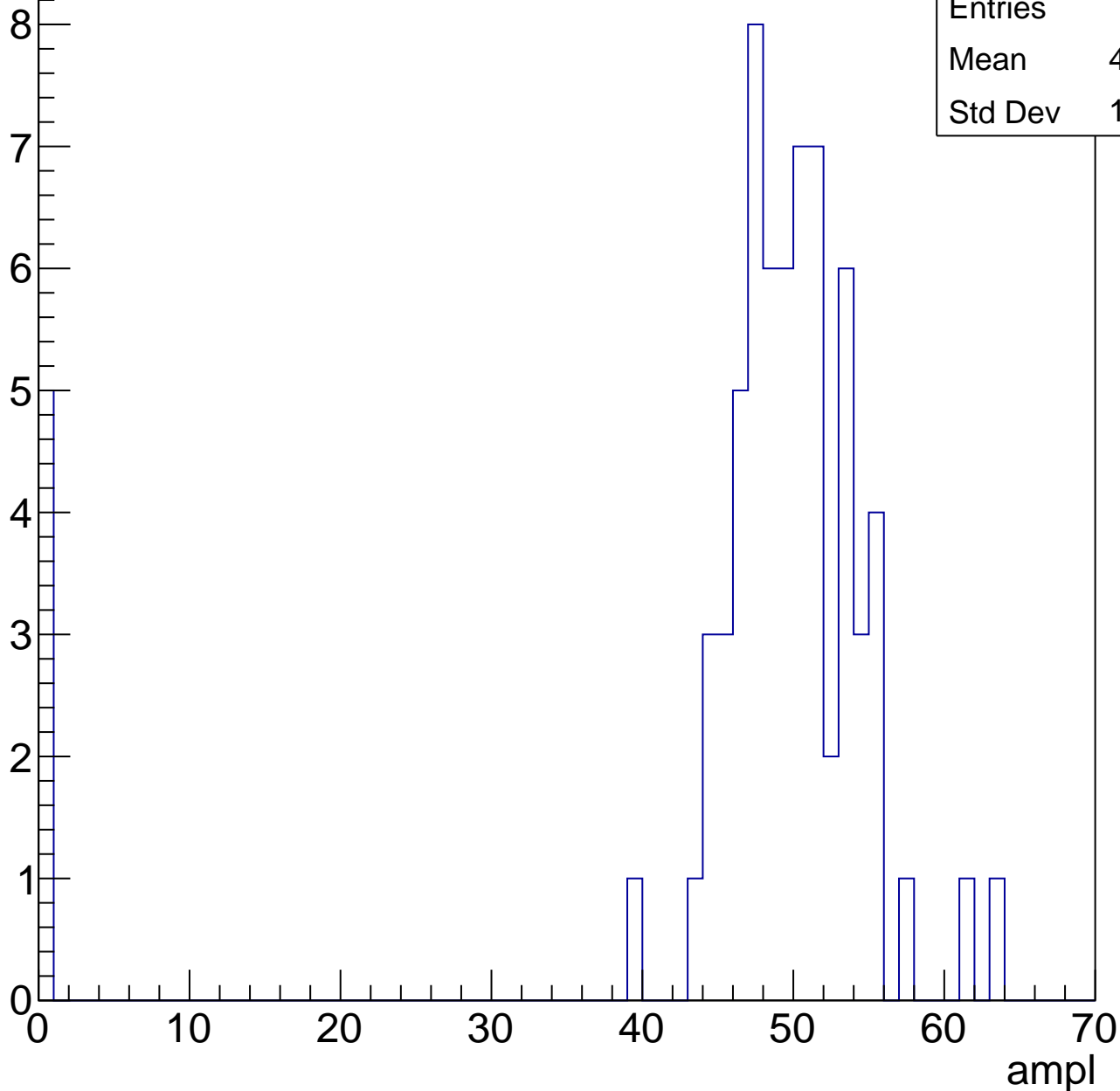


B1L103S, U21-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	46.13
Std Dev	13.39

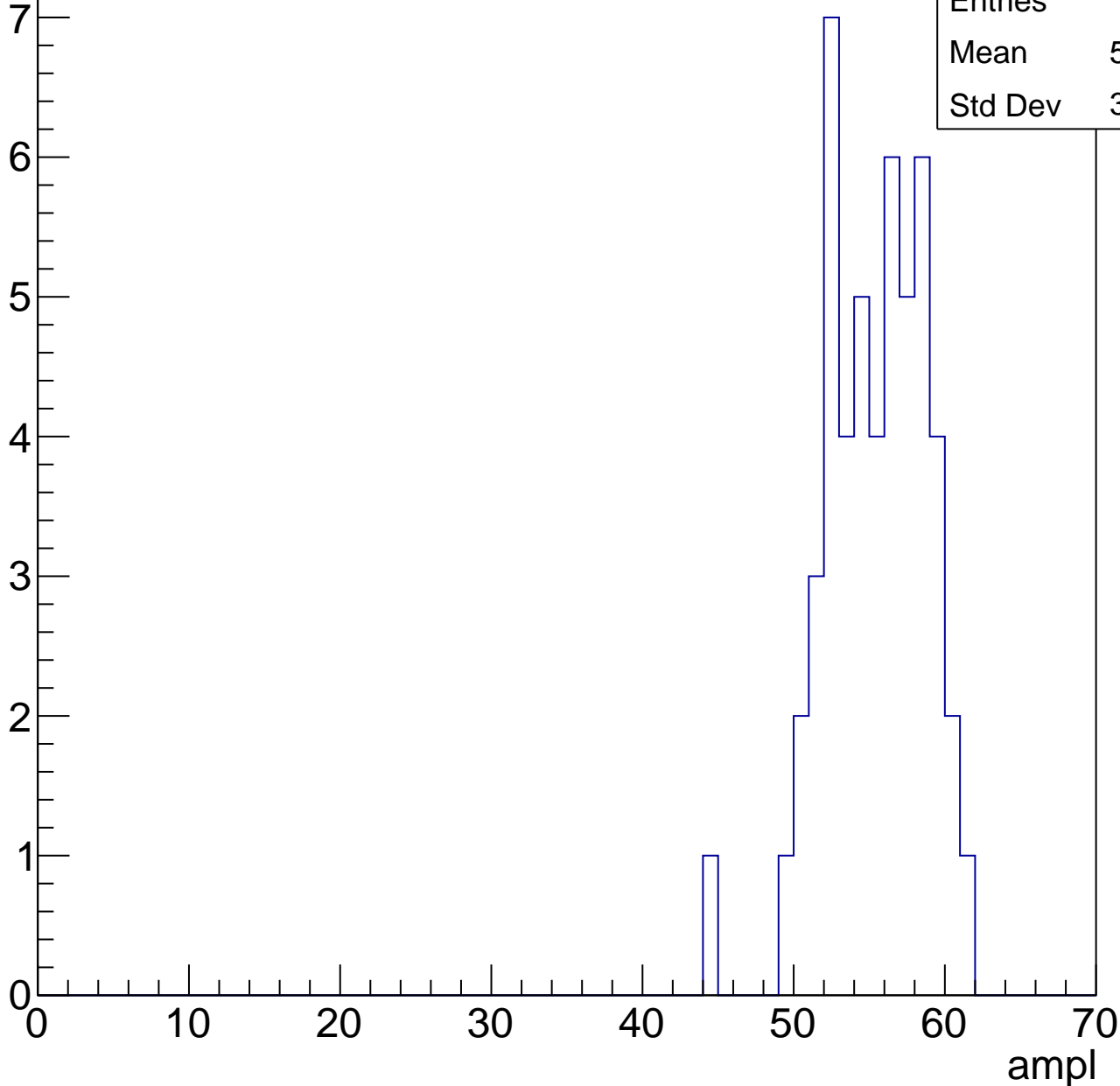


B1L103S, U21-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	54.86
Std Dev	3.337



B1L103S, U21-ch67, adc5

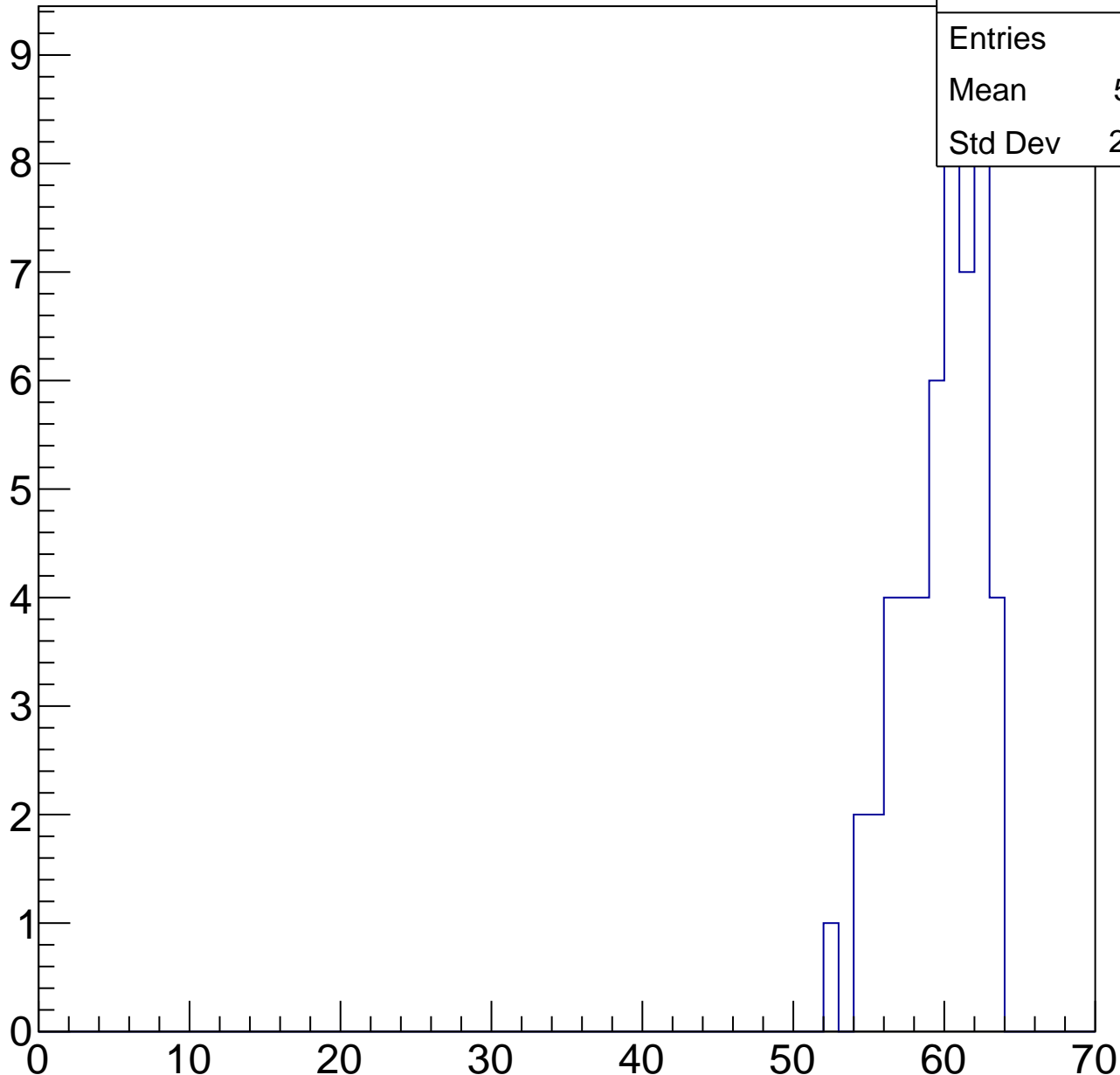
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.31
Std Dev	2.668

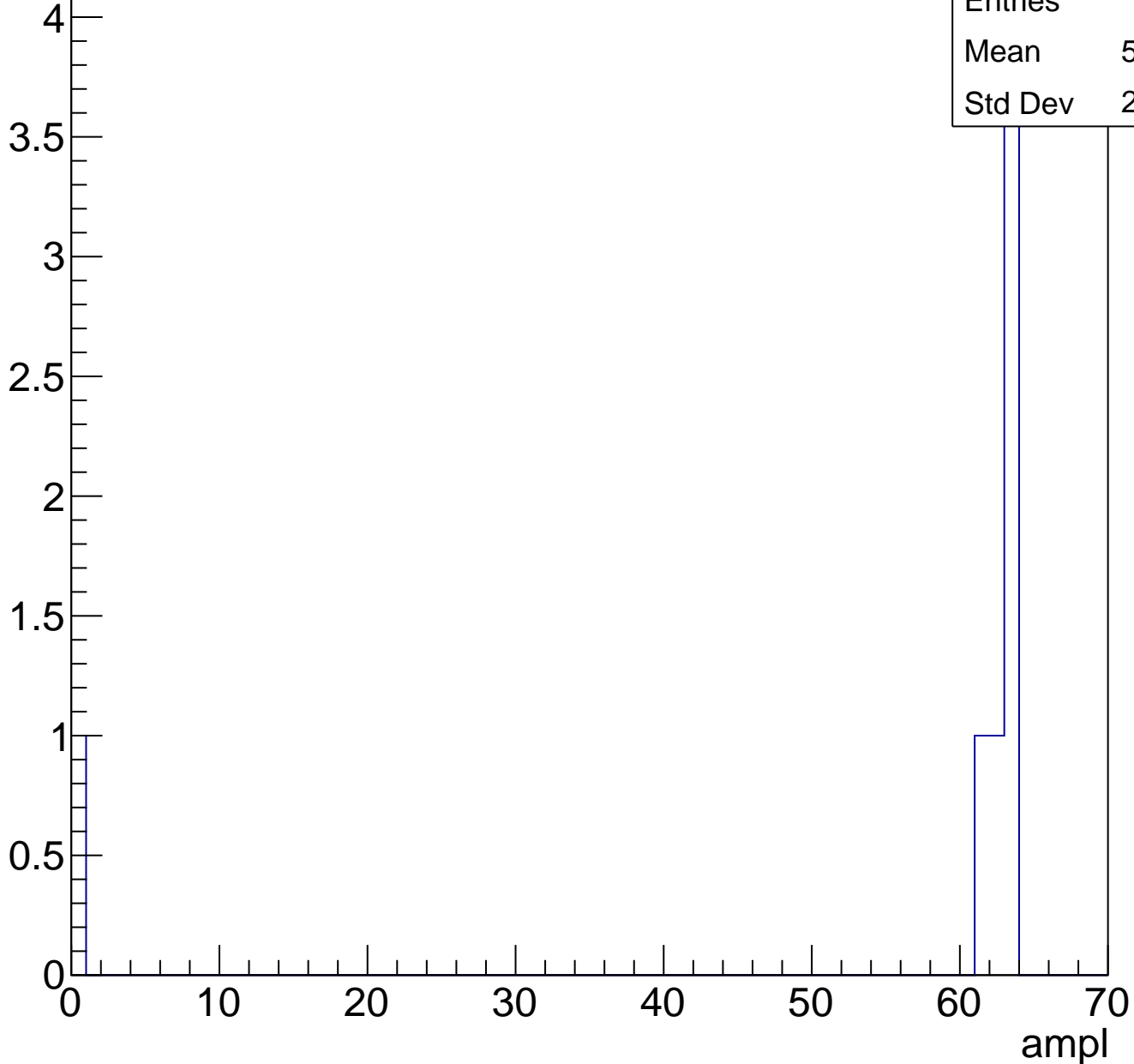
ampl



B1L103S, U21-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

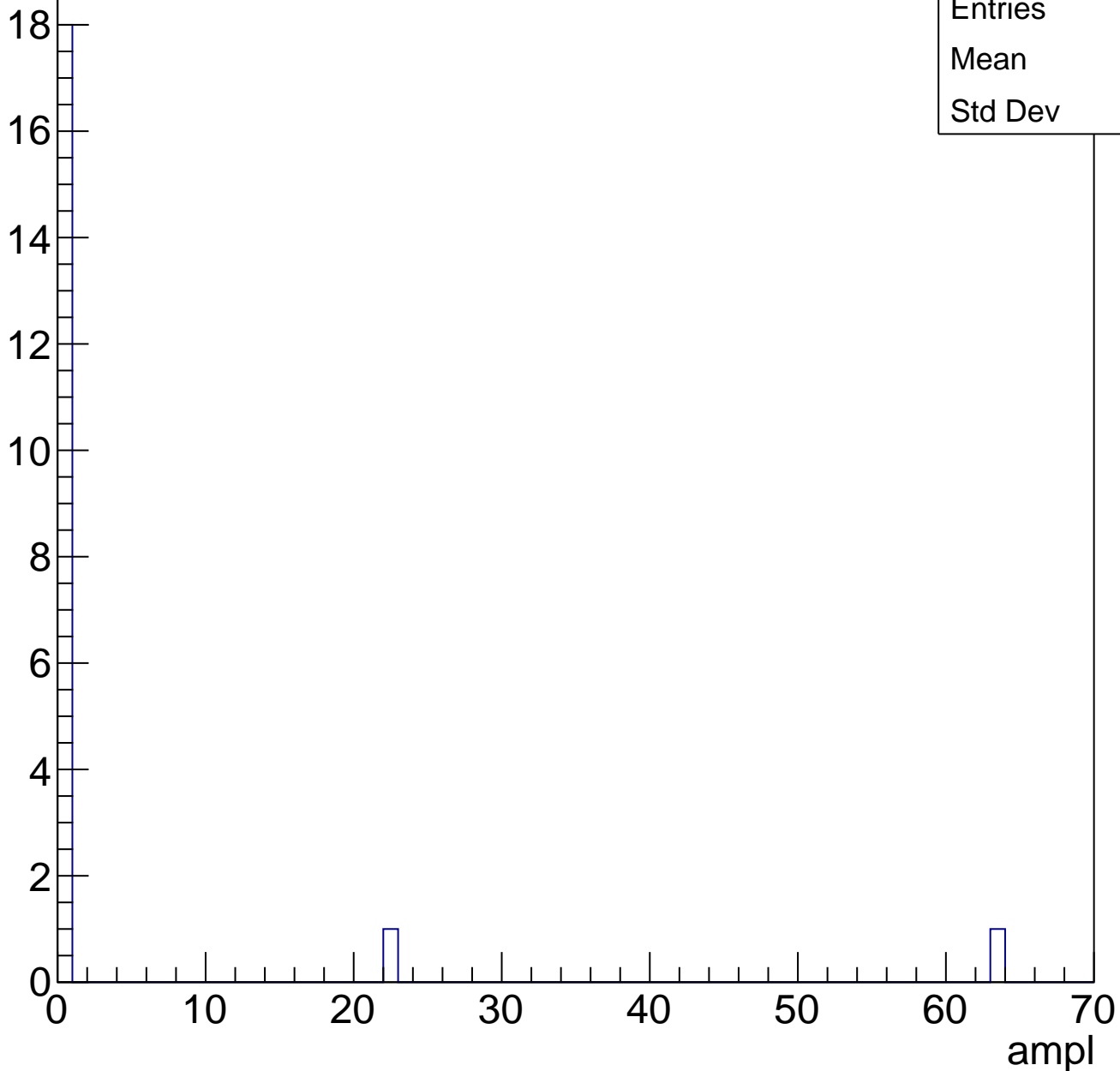


B1L103S, U21-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.25
Std Dev	14.3

Entry



B1L103S, U21-ch68, adc0

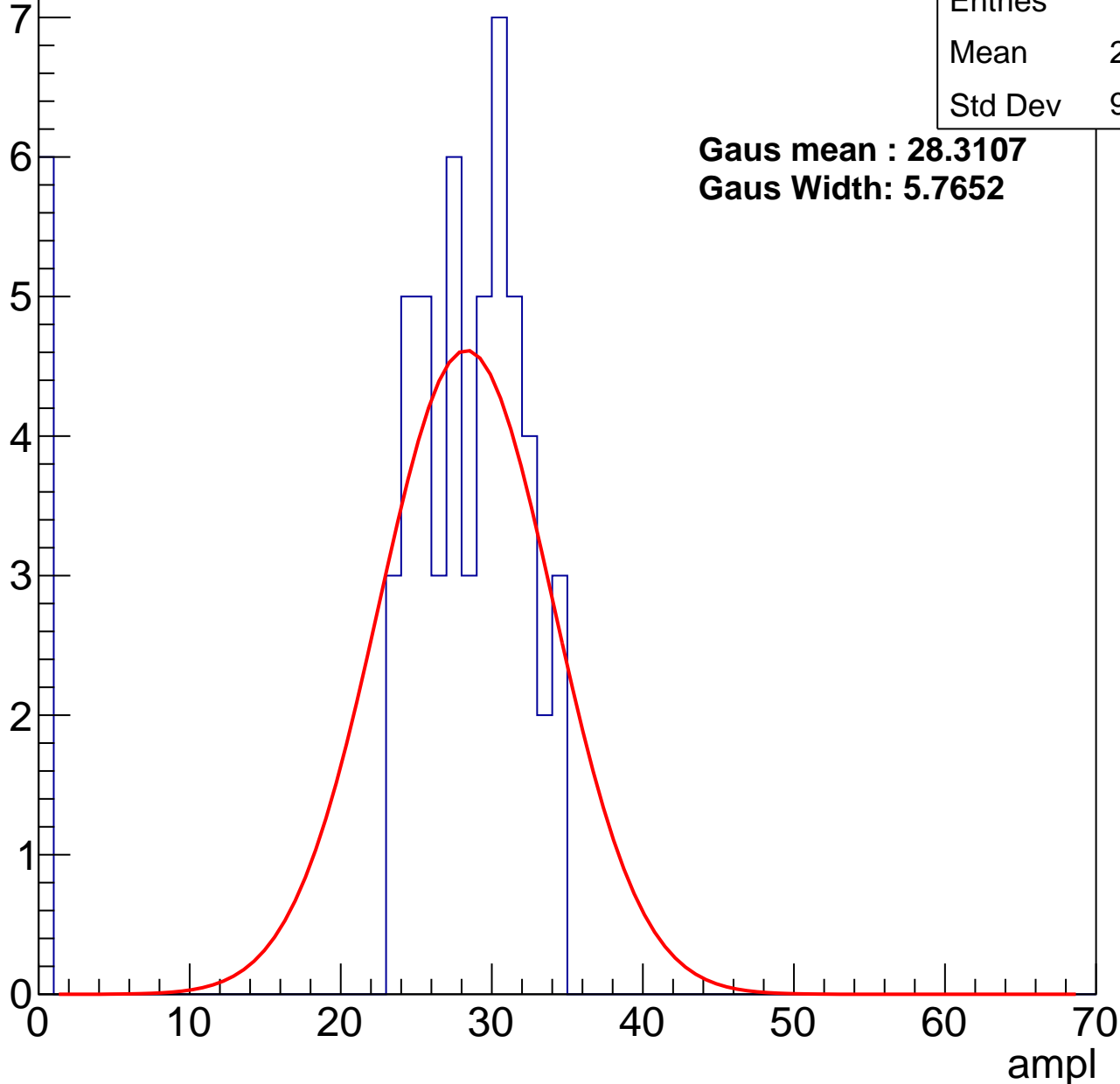
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	25.33
Std Dev	9.193

Gaus mean : 28.3107

Gaus Width: 5.7652



B1L103S, U21-ch68, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	30.18
Std Dev	11.28

Gaus mean : 33.5400

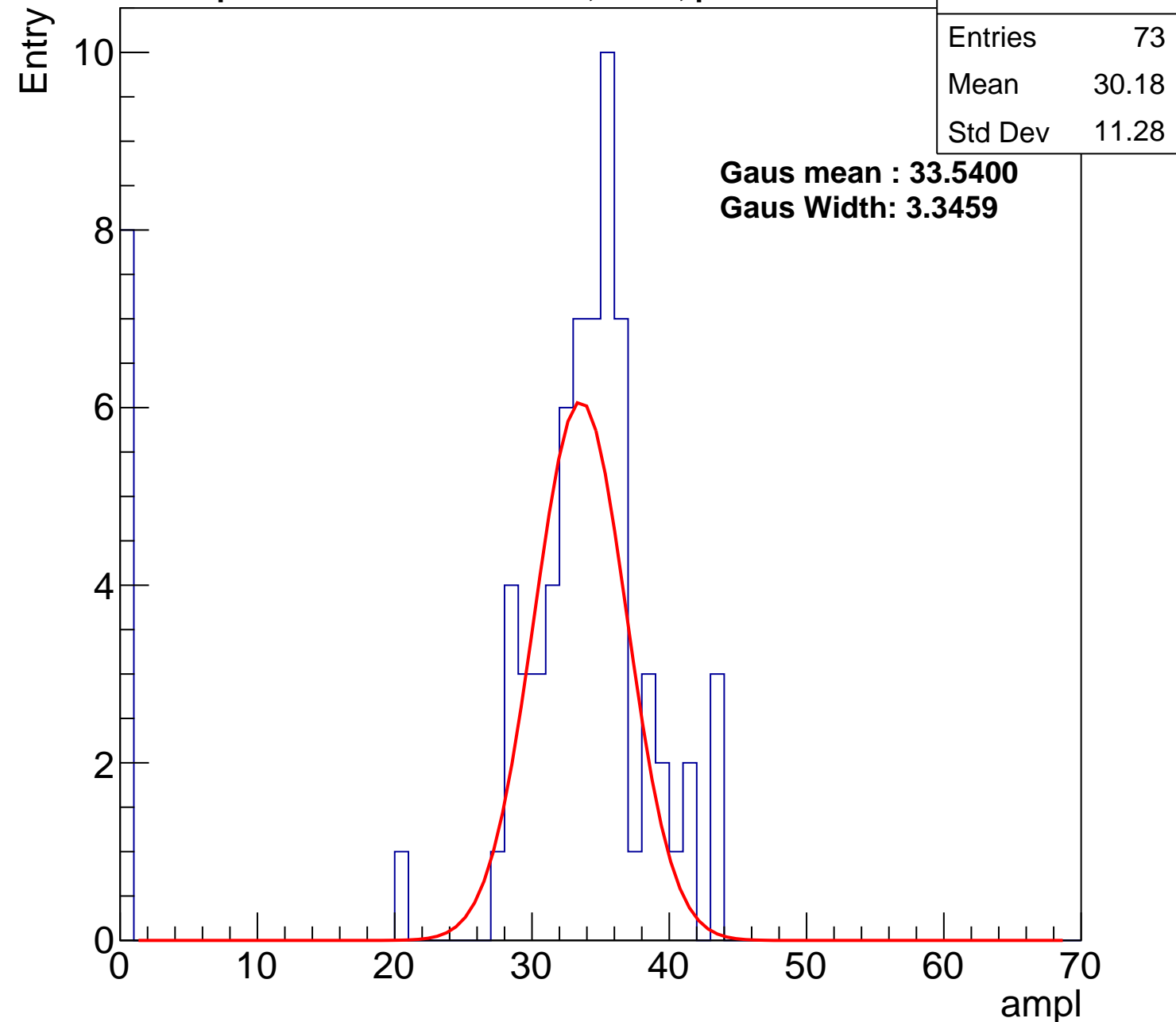
Gaus Width: 3.3459

Entry

10
8
6
4
2
0

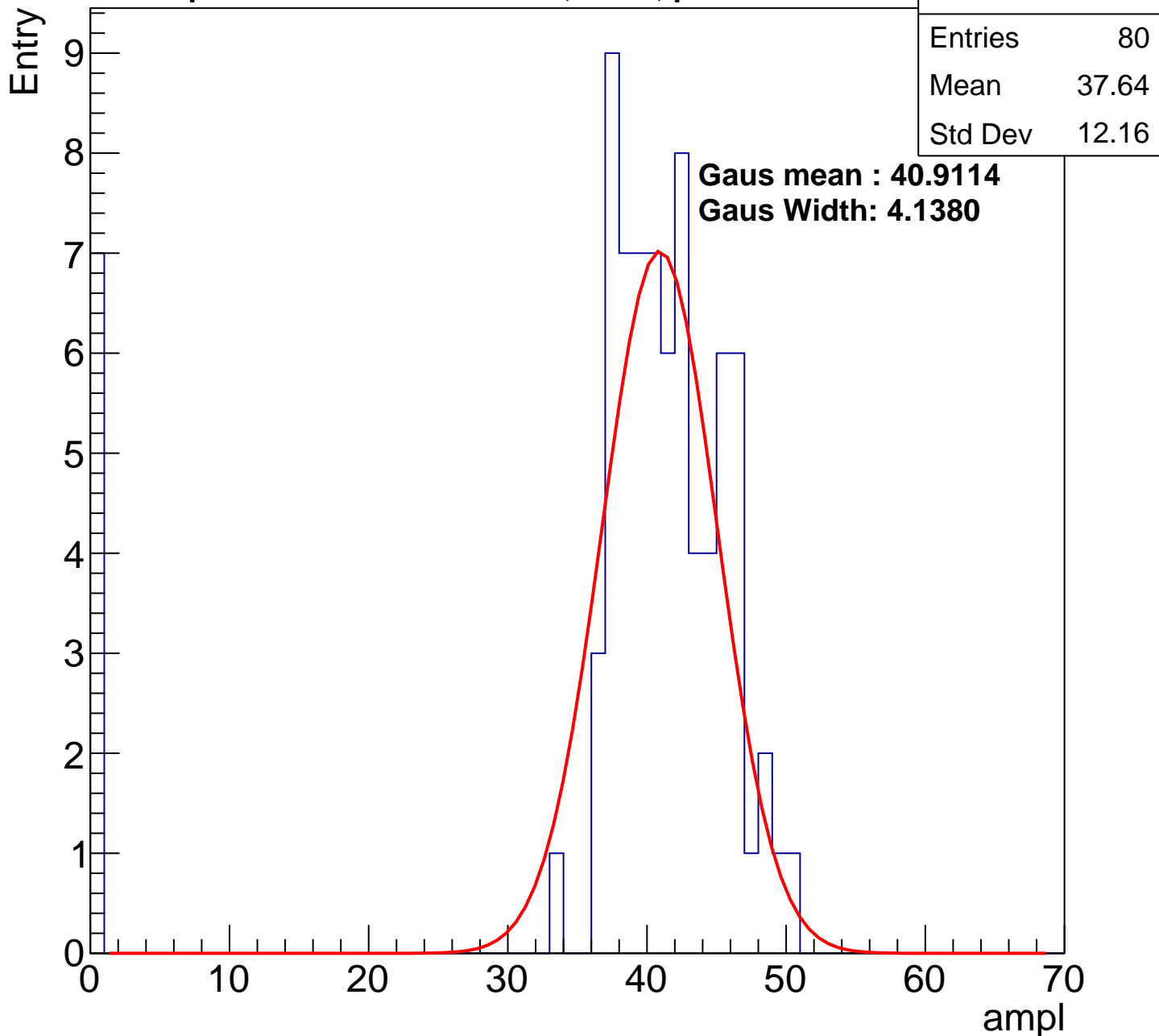
ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch68, adc2

calib_packv5_041523_1651.root, FC#0, port C2

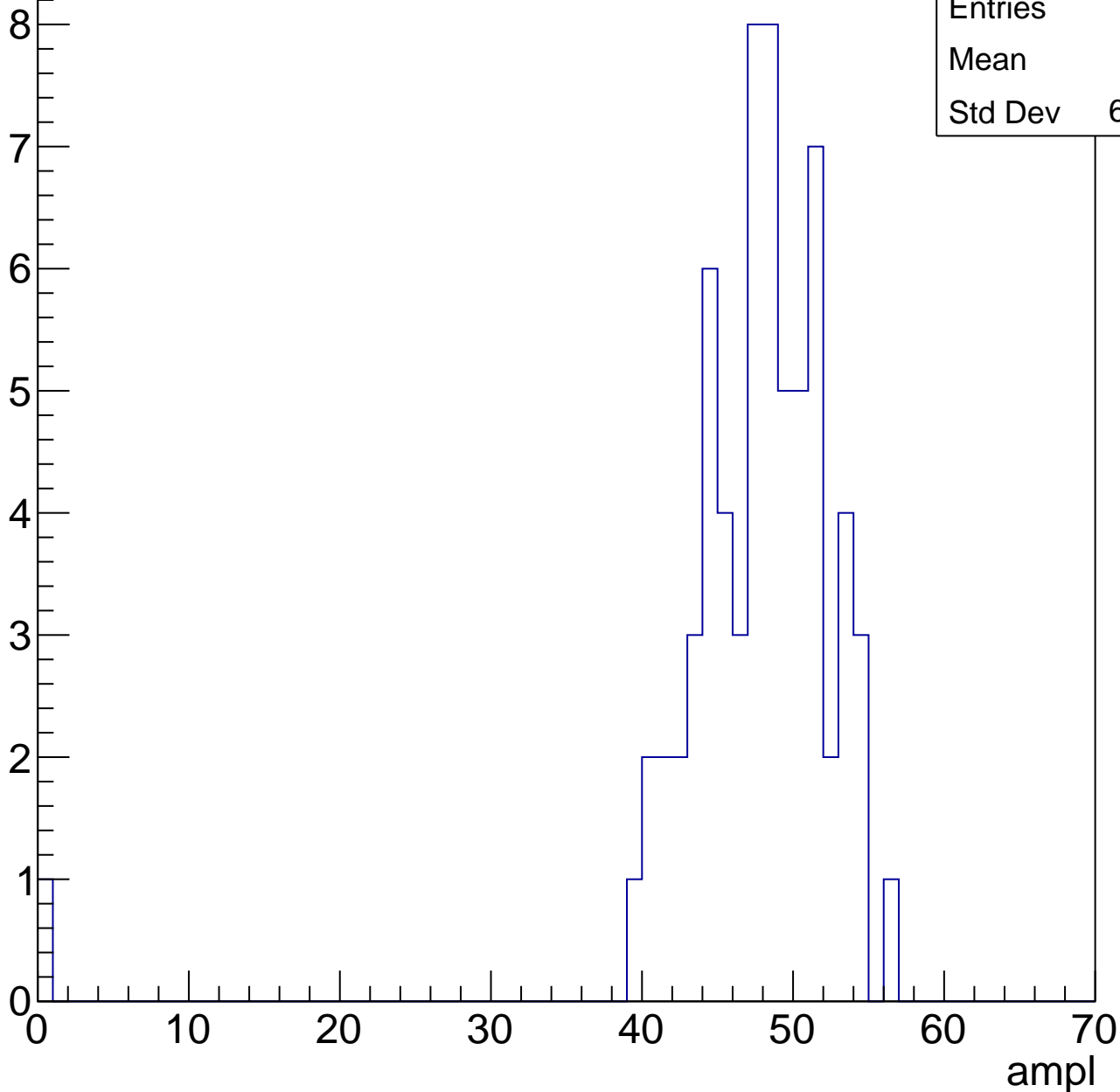


B1L103S, U21-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	46.9
Std Dev	6.933

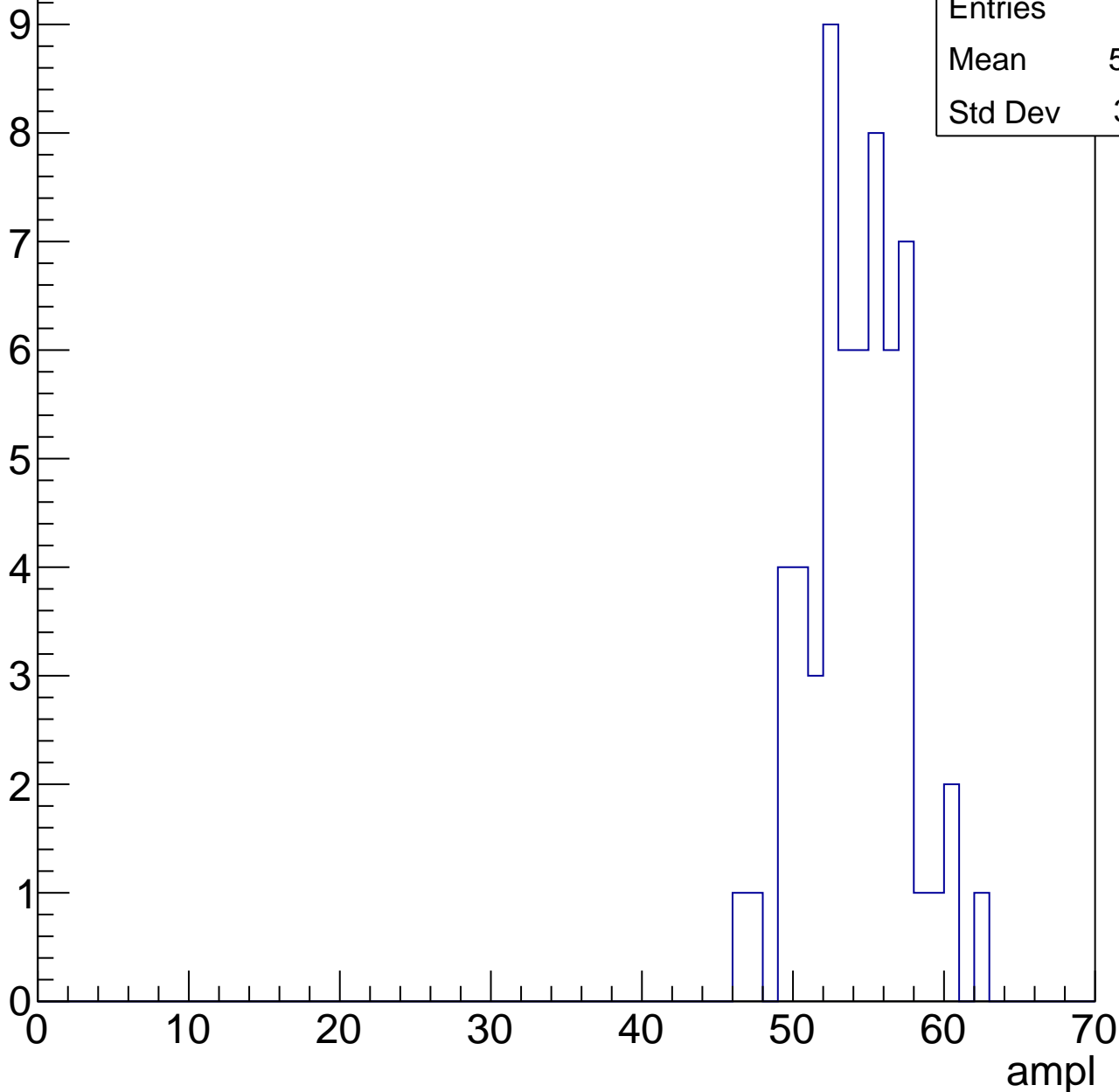


B1L103S, U21-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

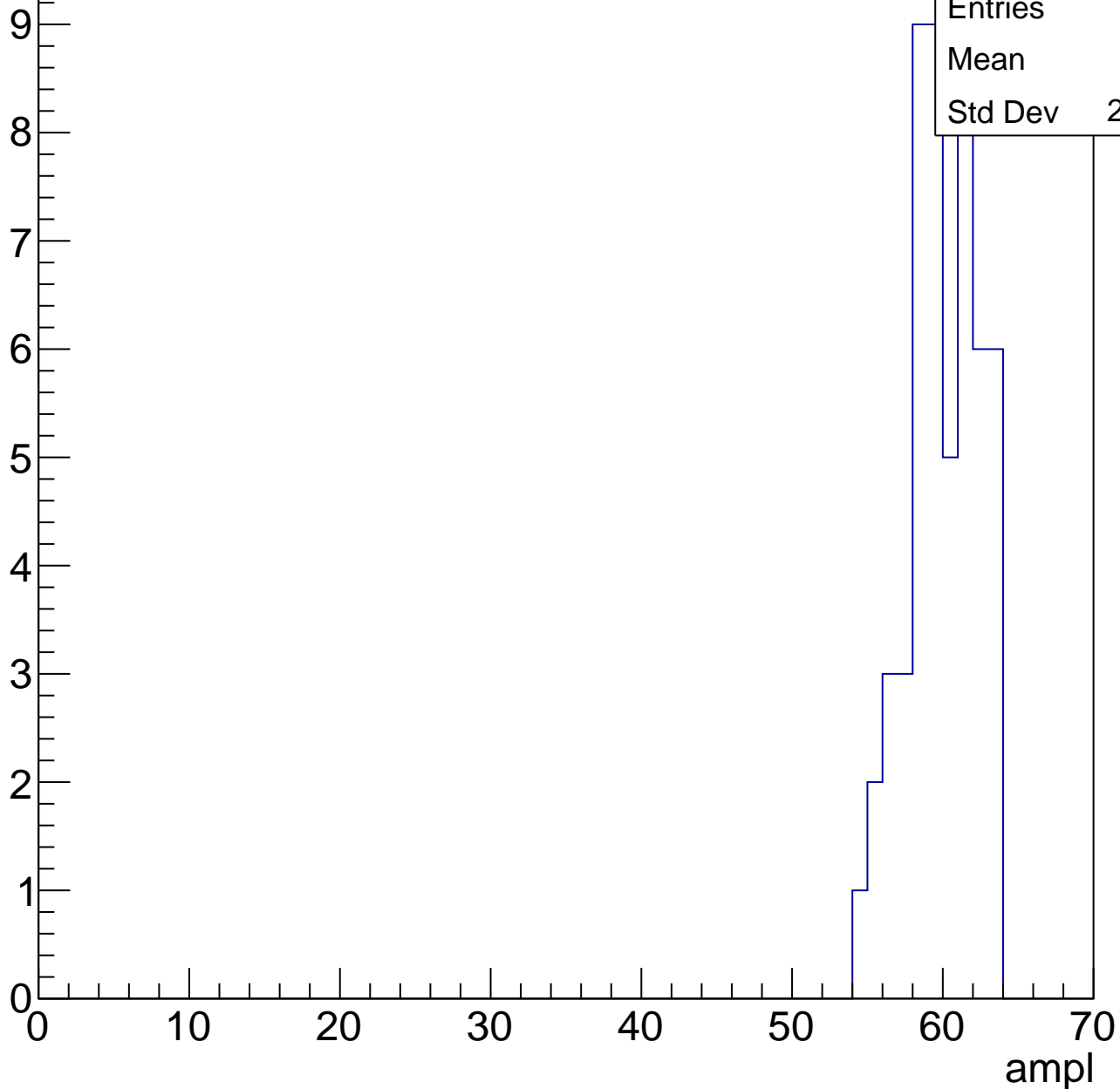
Entries	60
Mean	53.77
Std Dev	3.201



B1L103S, U21-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

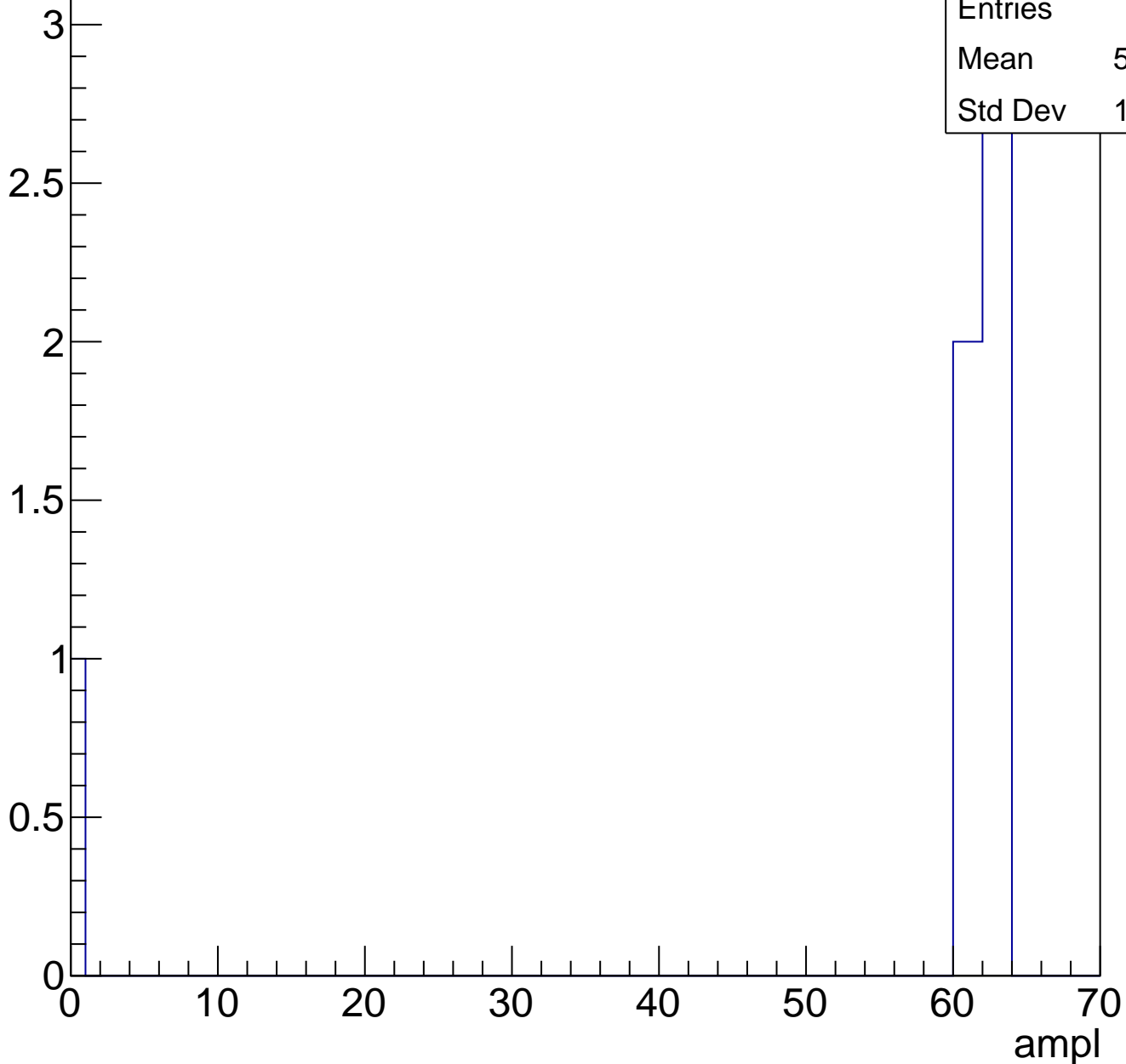


Entries	52
Mean	59.5
Std Dev	2.316

B1L103S, U21-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

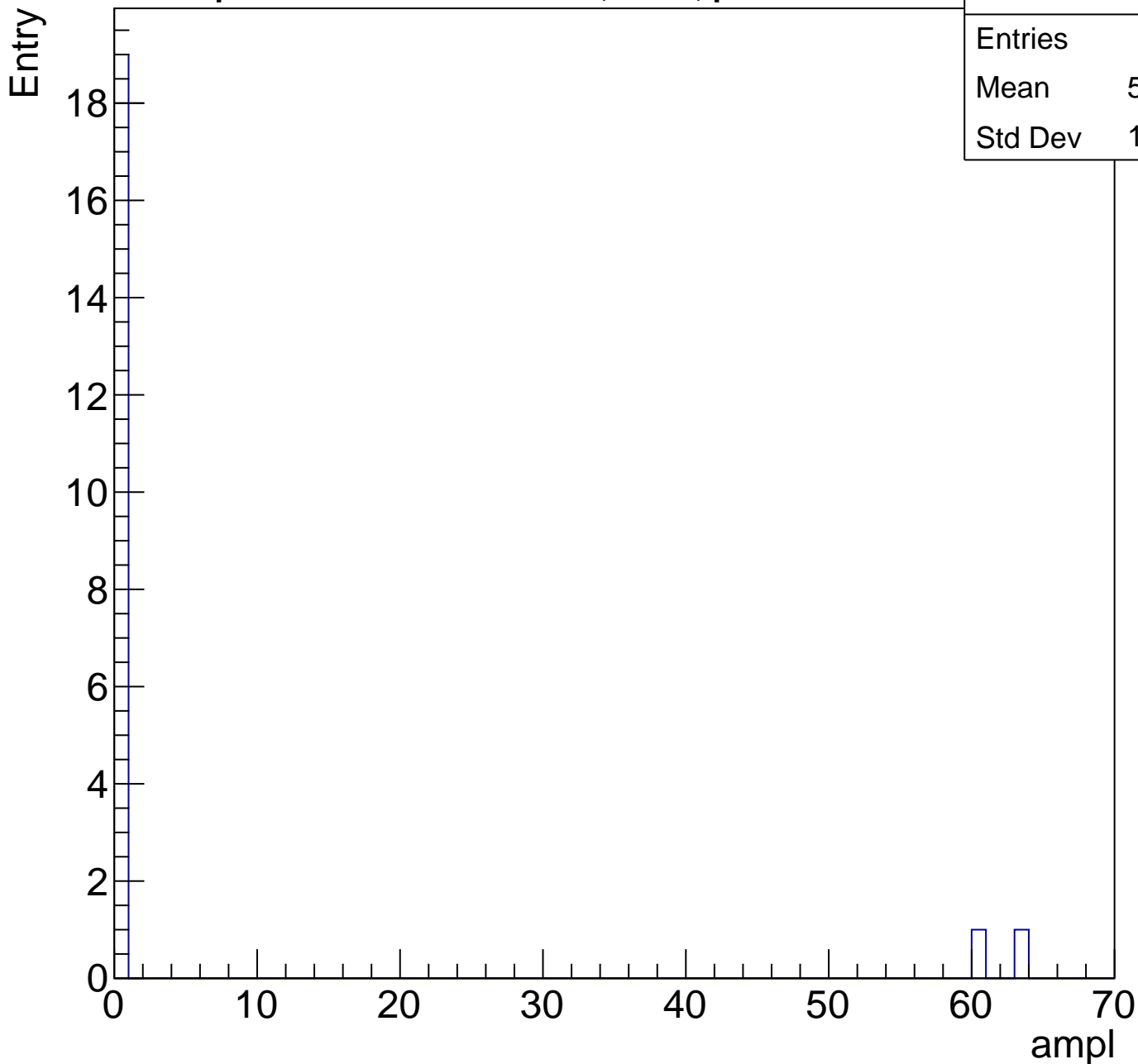
Entry



B1L103S, U21-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.857
Std Dev	18.06



B1L103S, U21-ch69, adc0

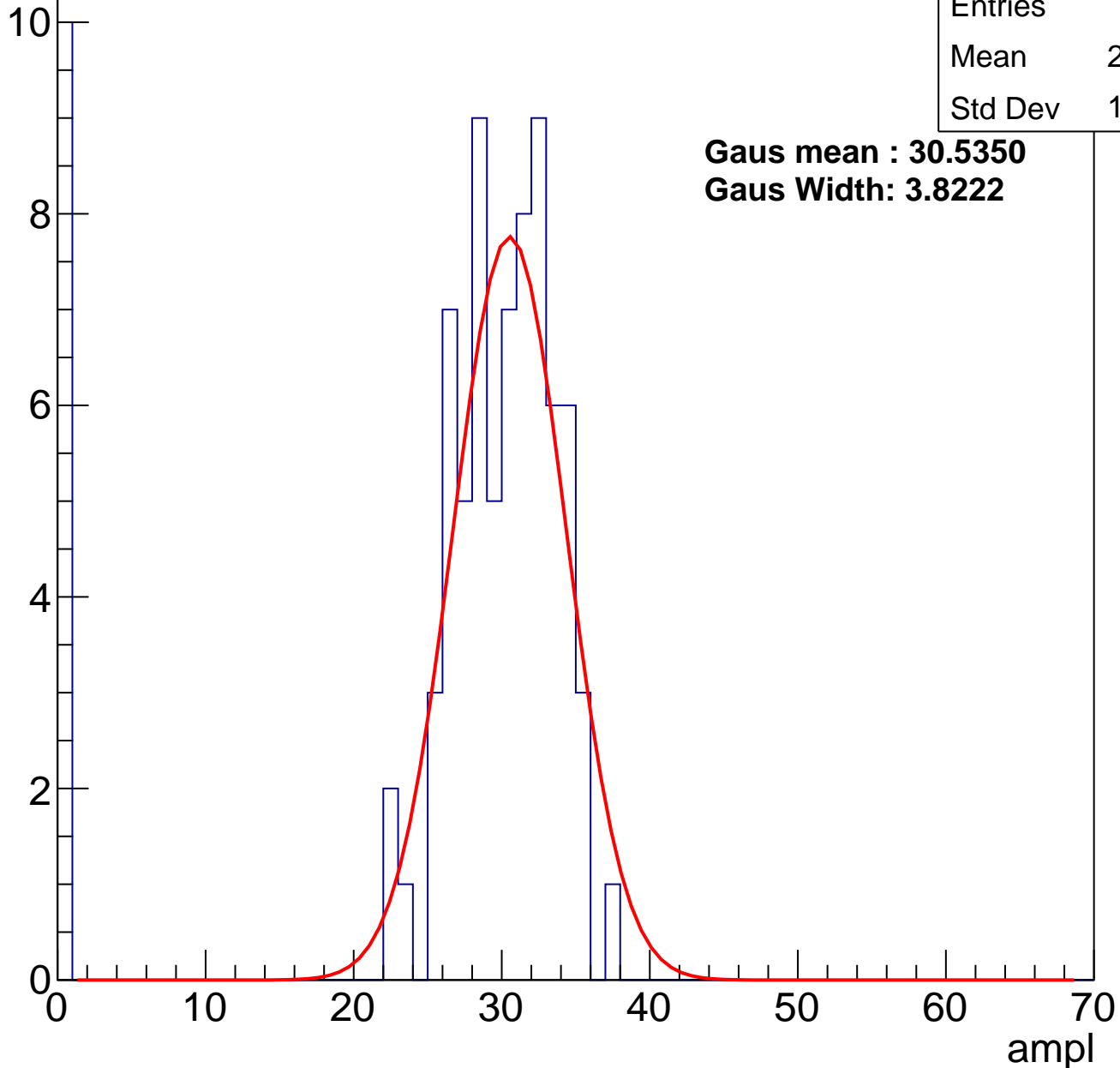
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	26.17
Std Dev	10.22

Gaus mean : 30.5350

Gaus Width: 3.8222

Entry



B1L103S, U21-ch69, adc1

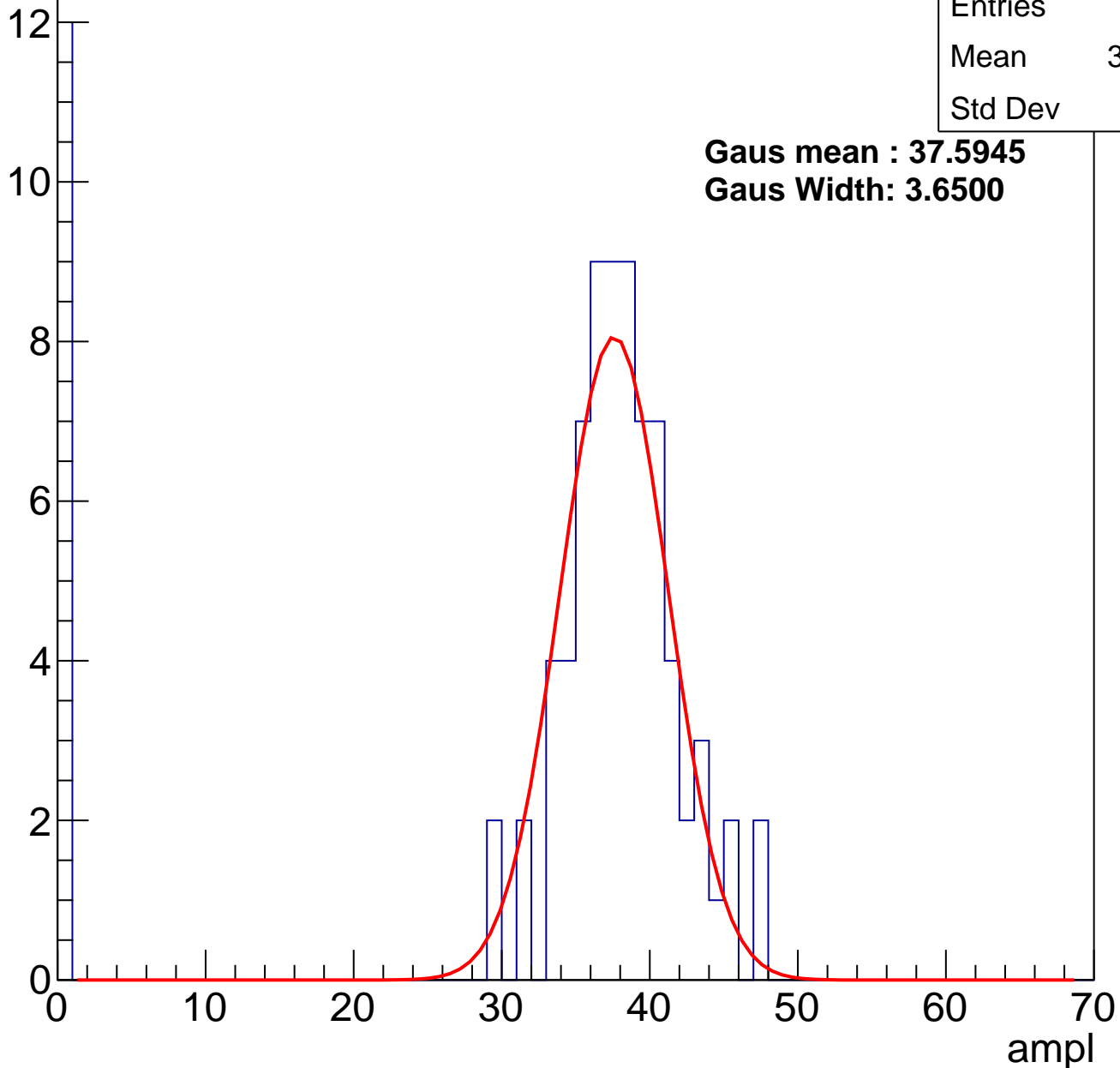
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	32.44
Std Dev	13.5

Gaus mean : 37.5945

Gaus Width: 3.6500

Entry



B1L103S, U21-ch69, adc2

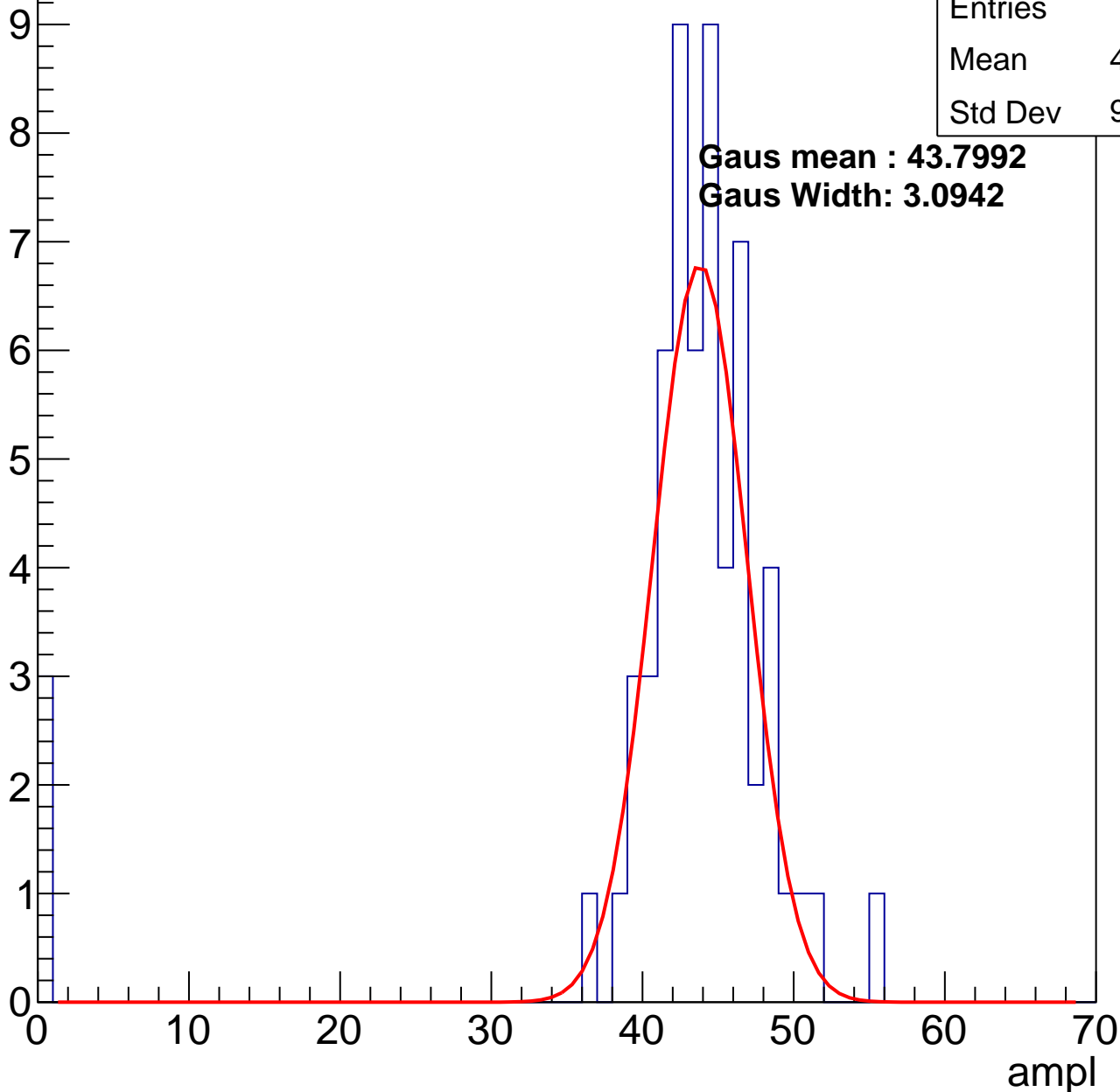
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	41.65
Std Dev	9.942

Gaus mean : 43.7992

Gaus Width: 3.0942

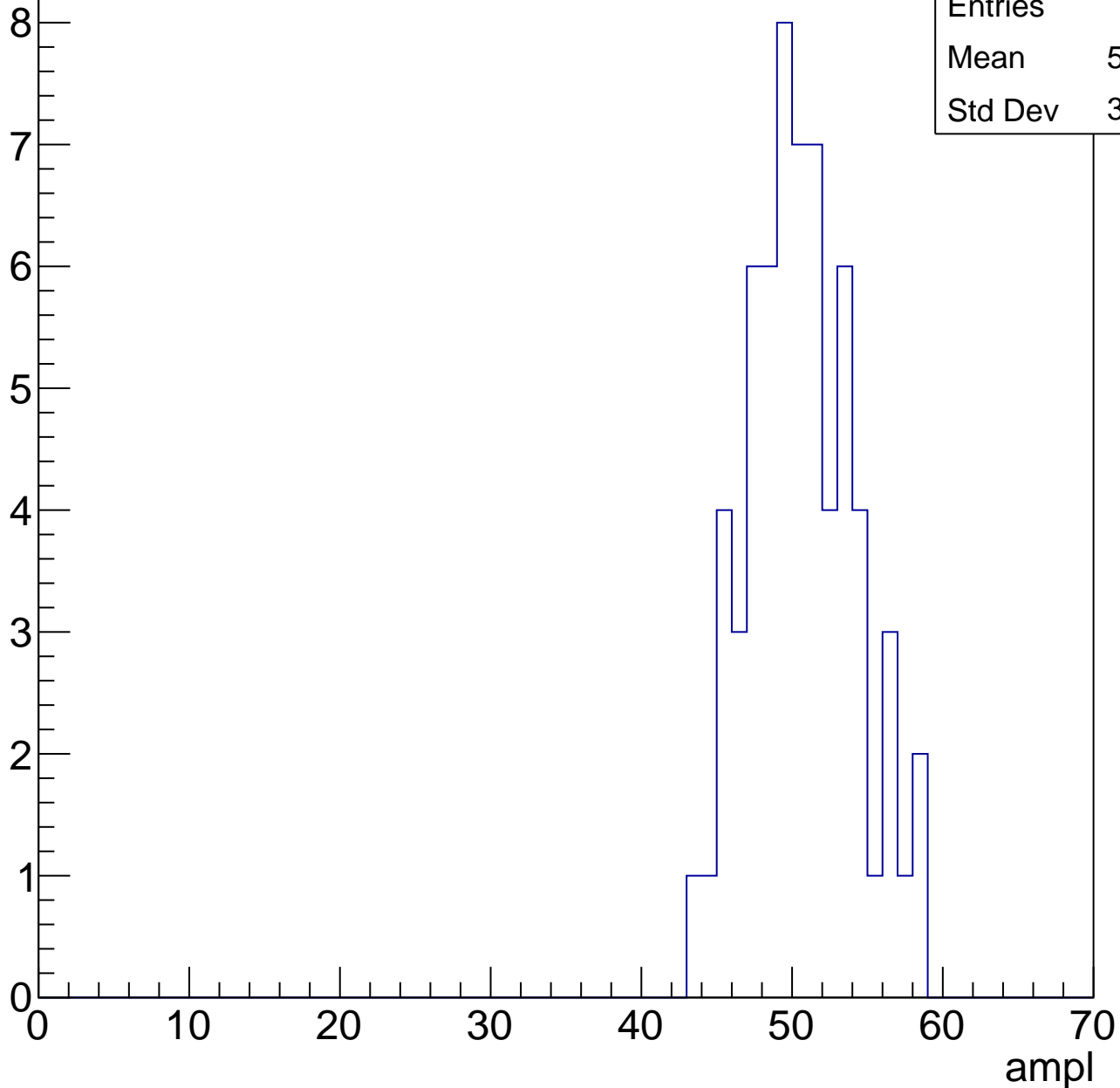


B1L103S, U21-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	50.19
Std Dev	3.464

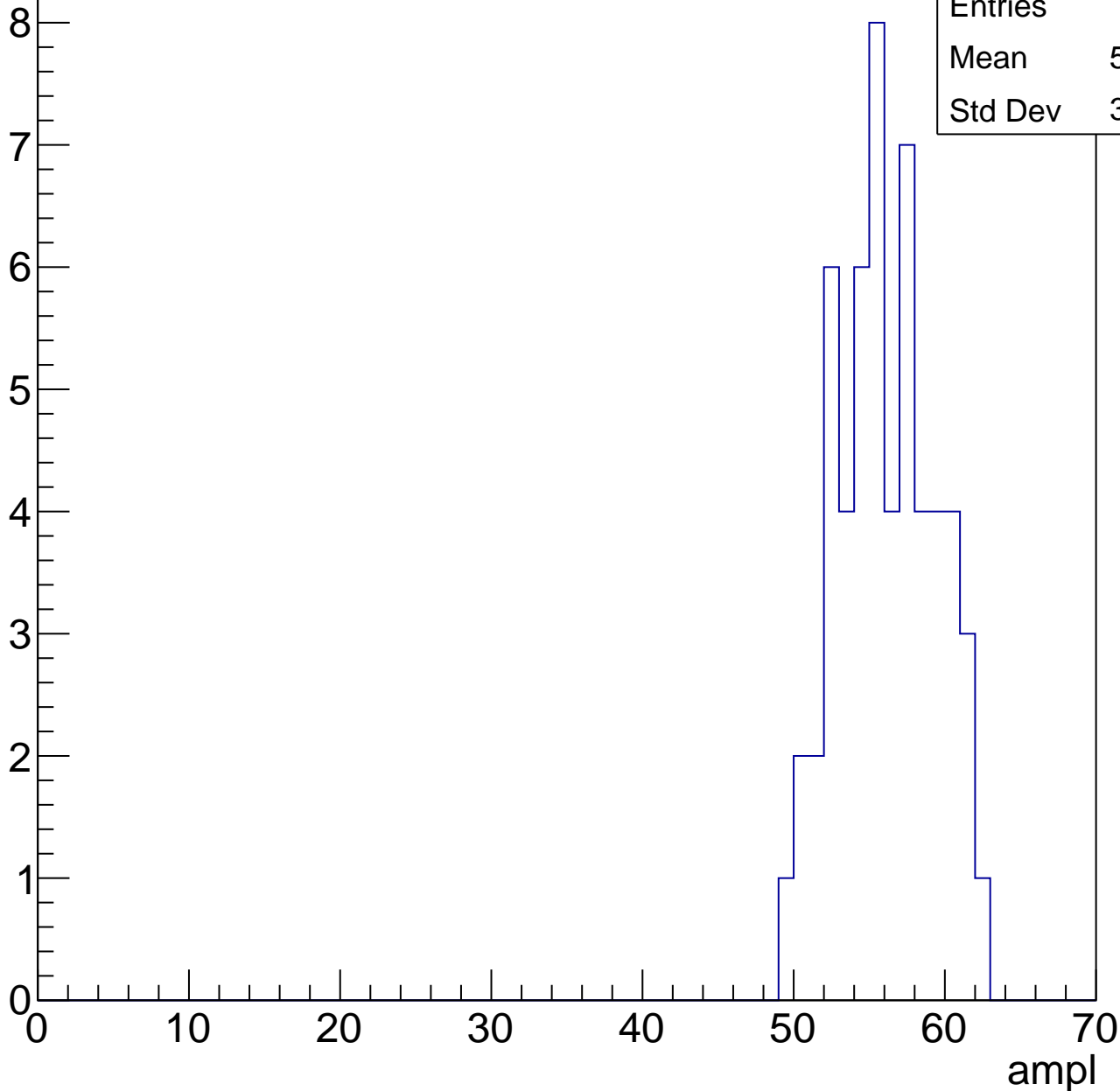


B1L103S, U21-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

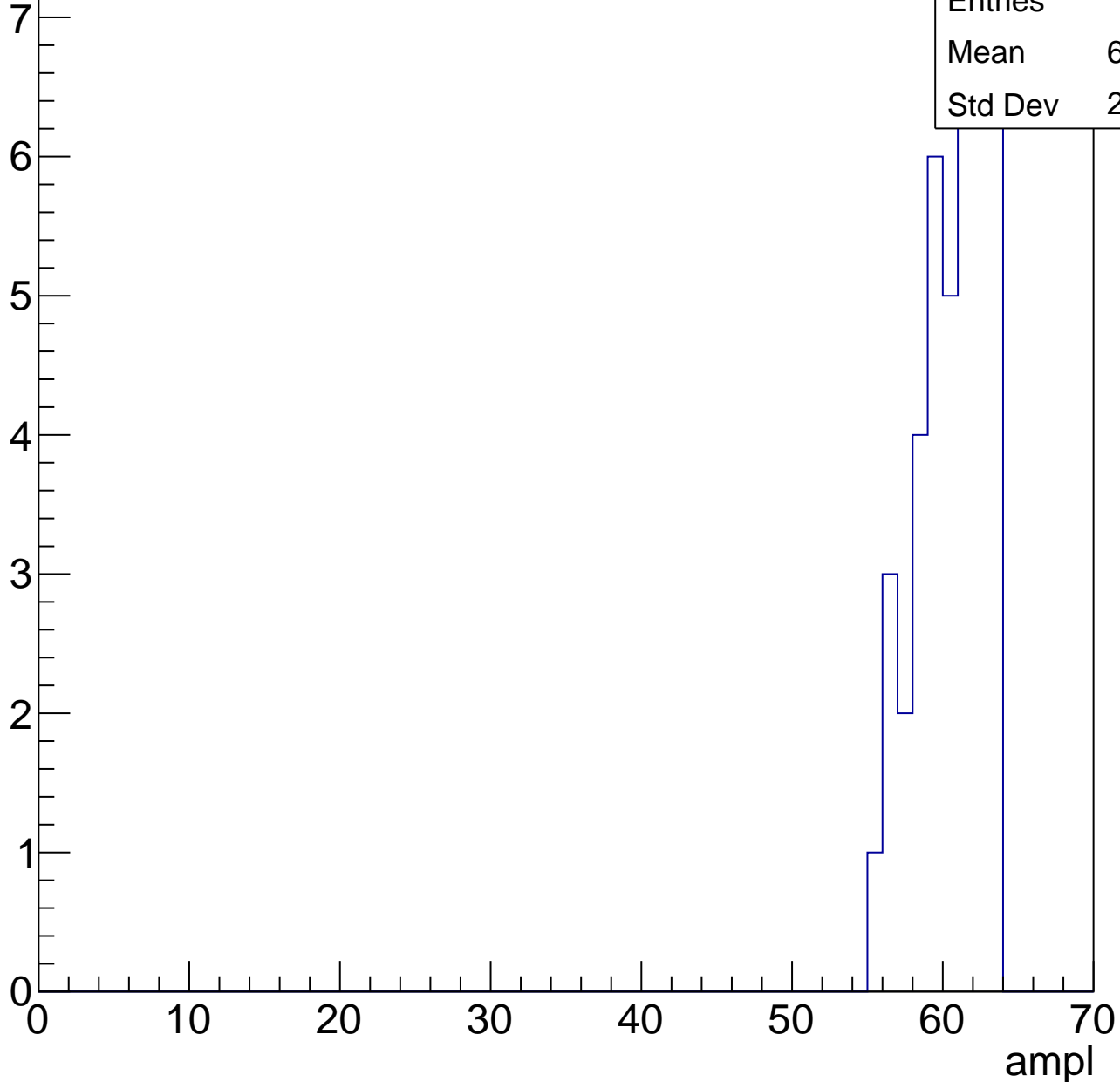
Entries	56
Mean	55.62
Std Dev	3.165



B1L103S, U21-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

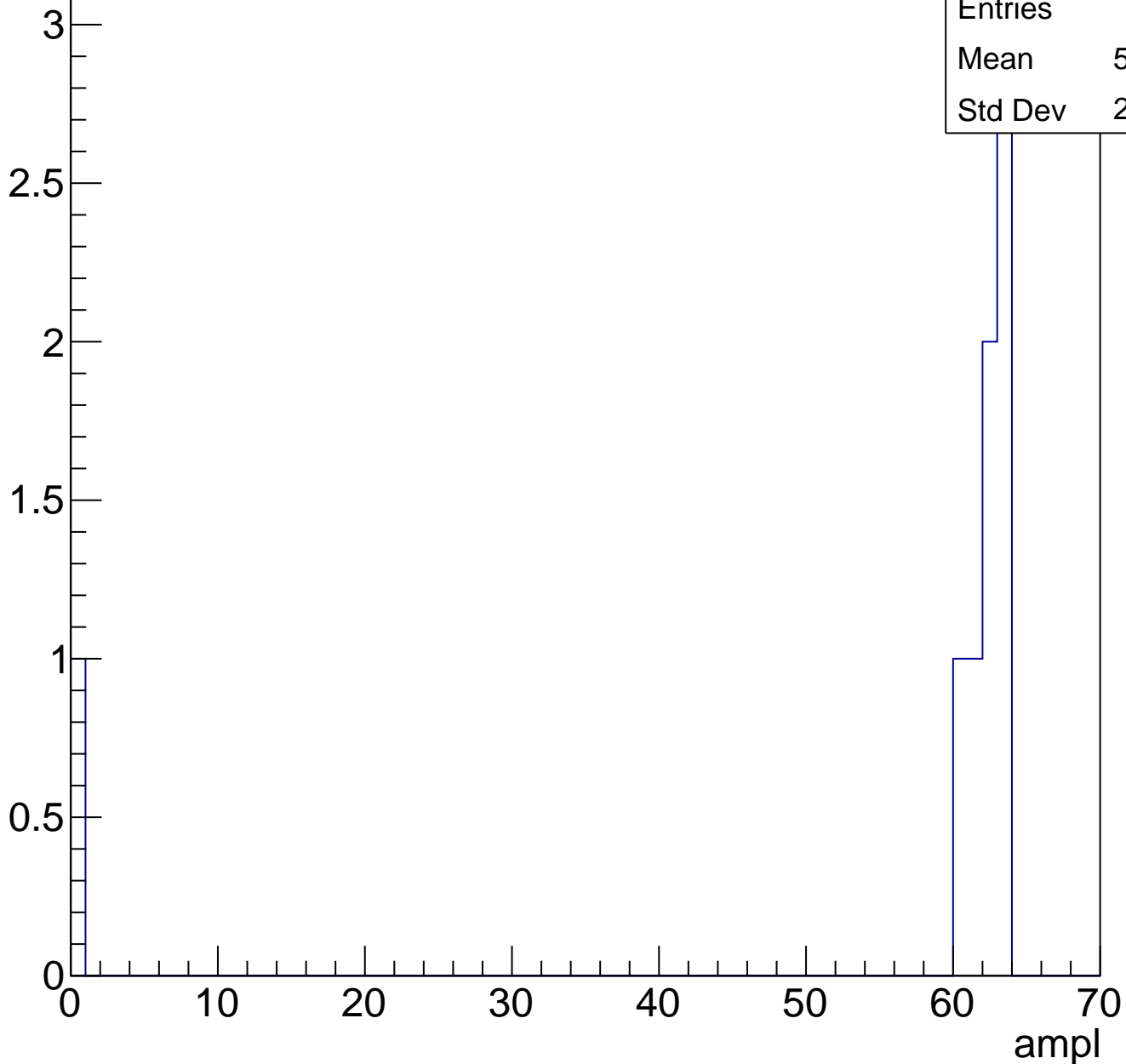
Entry



B1L103S, U21-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	8
Mean	54.25
Std Dev	20.53

B1L103S, U21-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch70, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	23.07
Std Dev	11.63

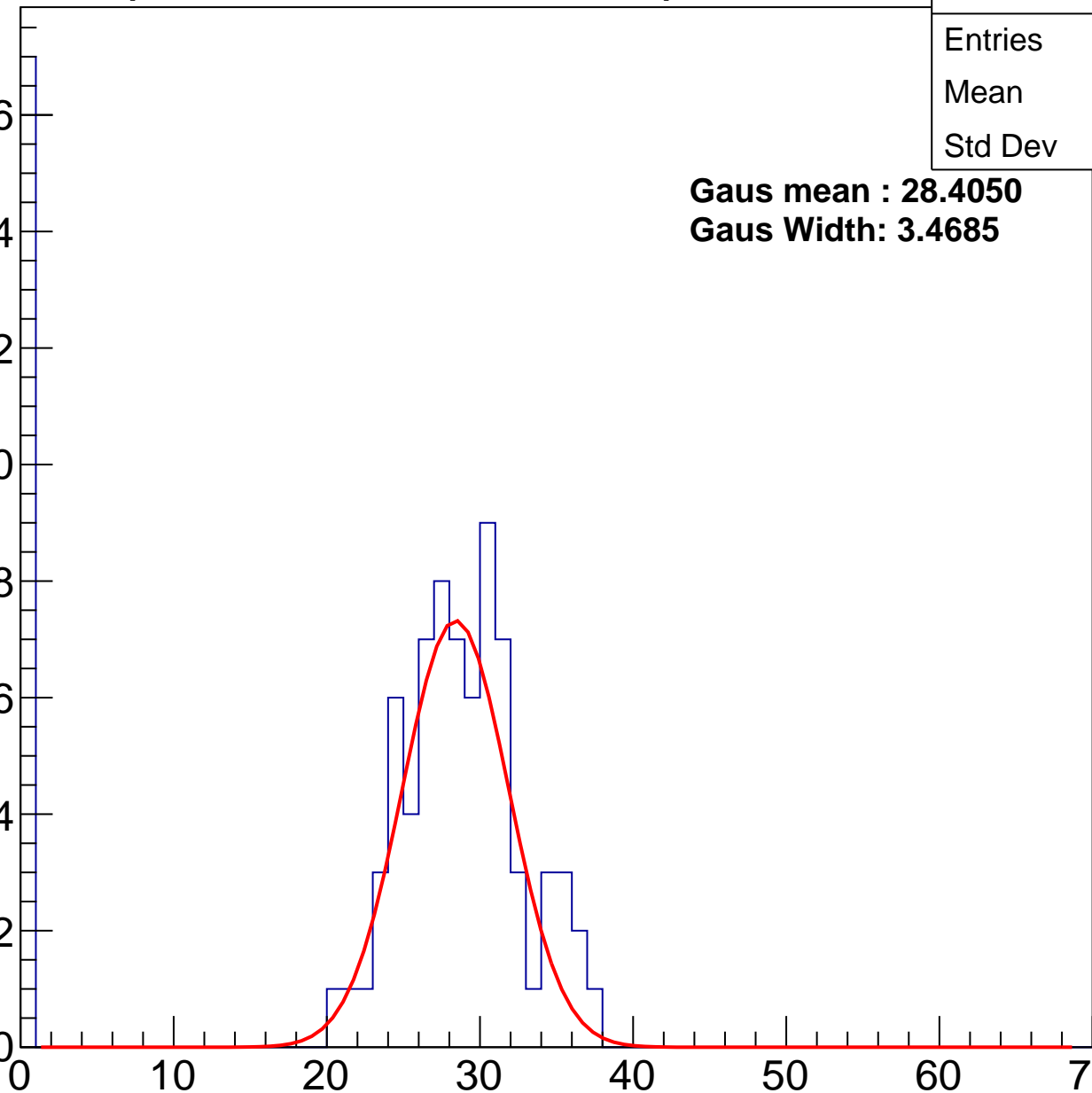
Gaus mean : 28.4050

Gaus Width: 3.4685

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch70, adc1

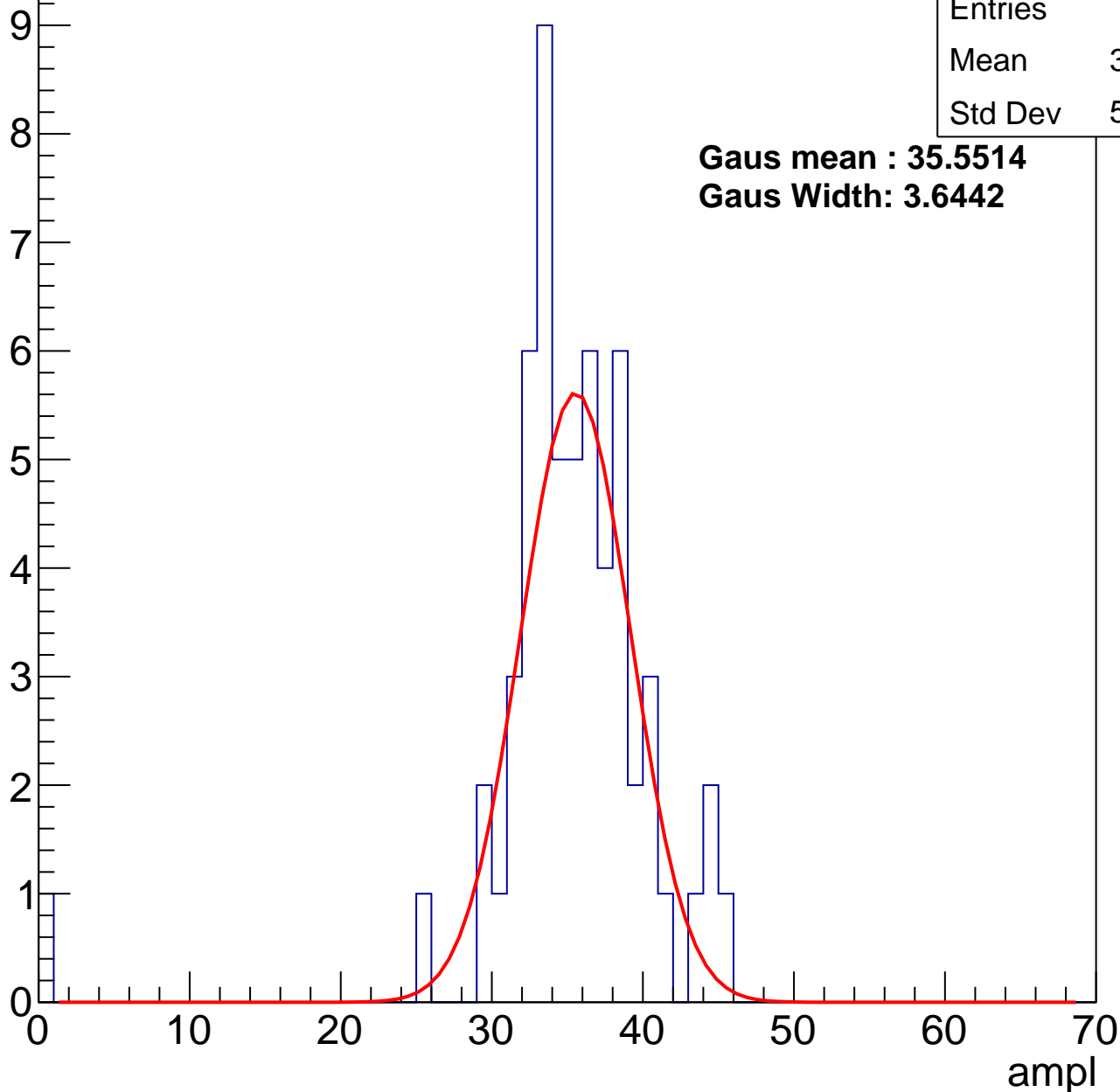
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	34.69
Std Dev	5.972

Gaus mean : 35.5514

Gaus Width: 3.6442



B1L103S, U21-ch70, adc2

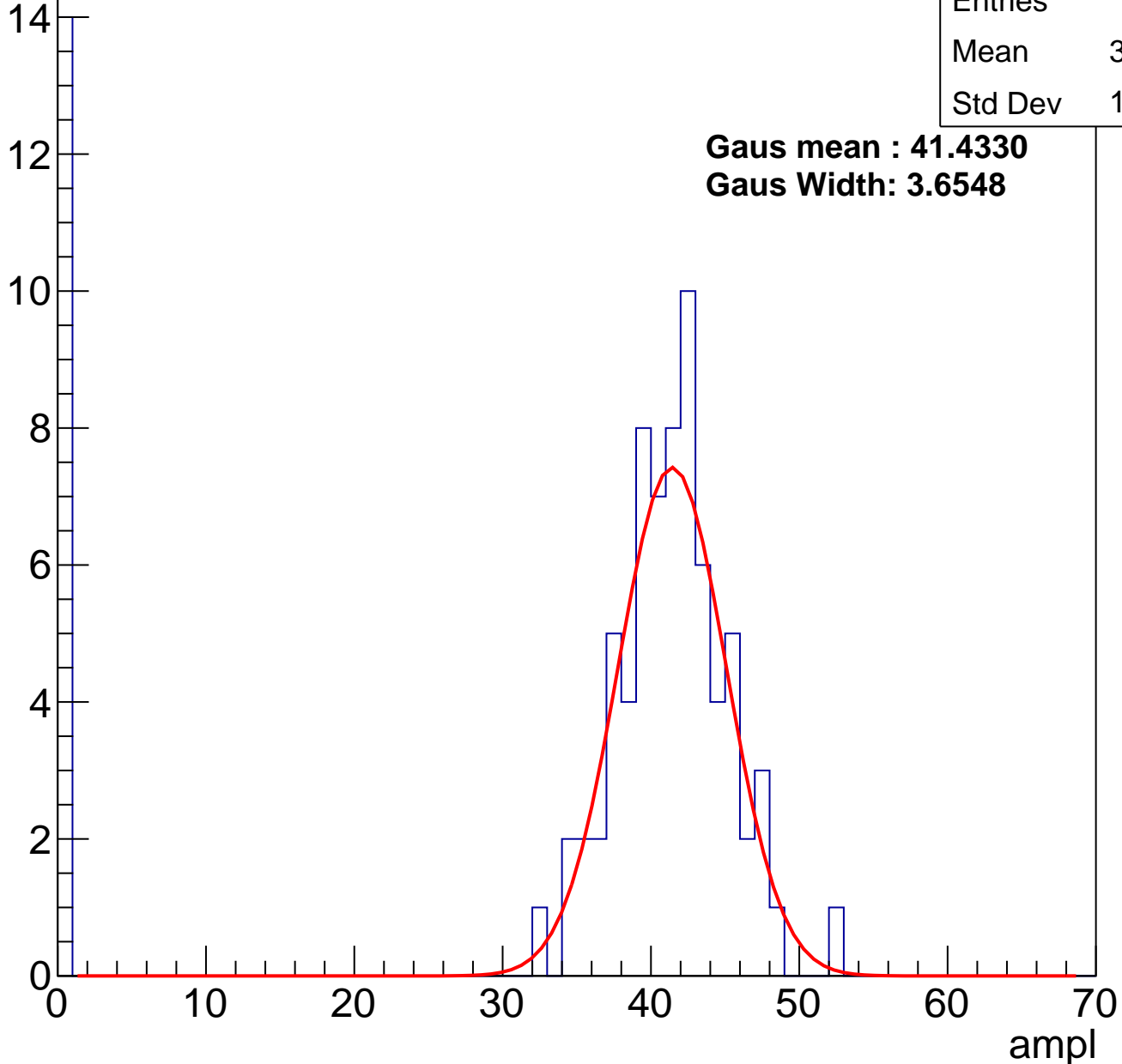
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	34.25
Std Dev	15.56

Gaus mean : 41.4330

Gaus Width: 3.6548

Entry

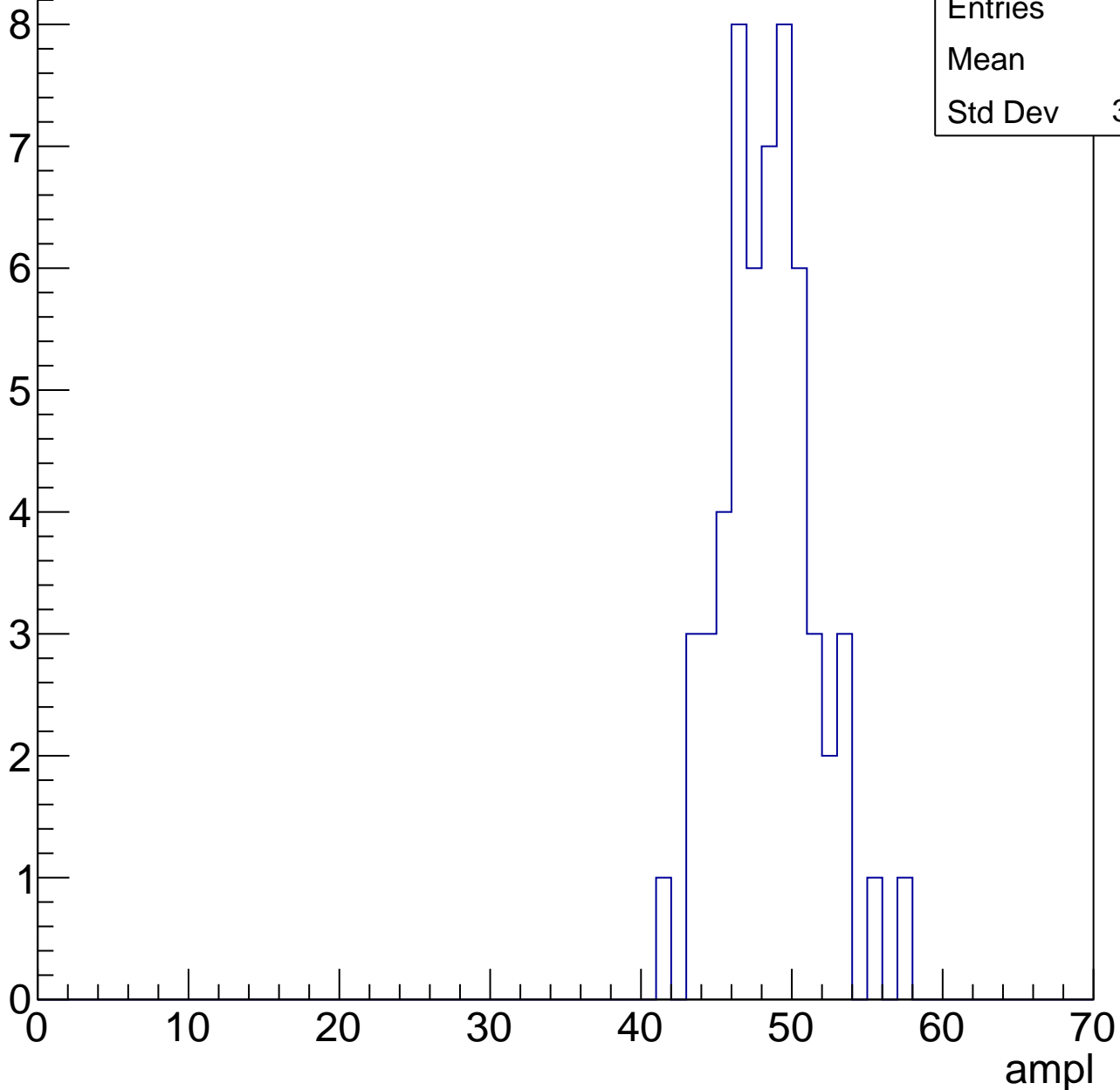


B1L103S, U21-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	48
Std Dev	3.111

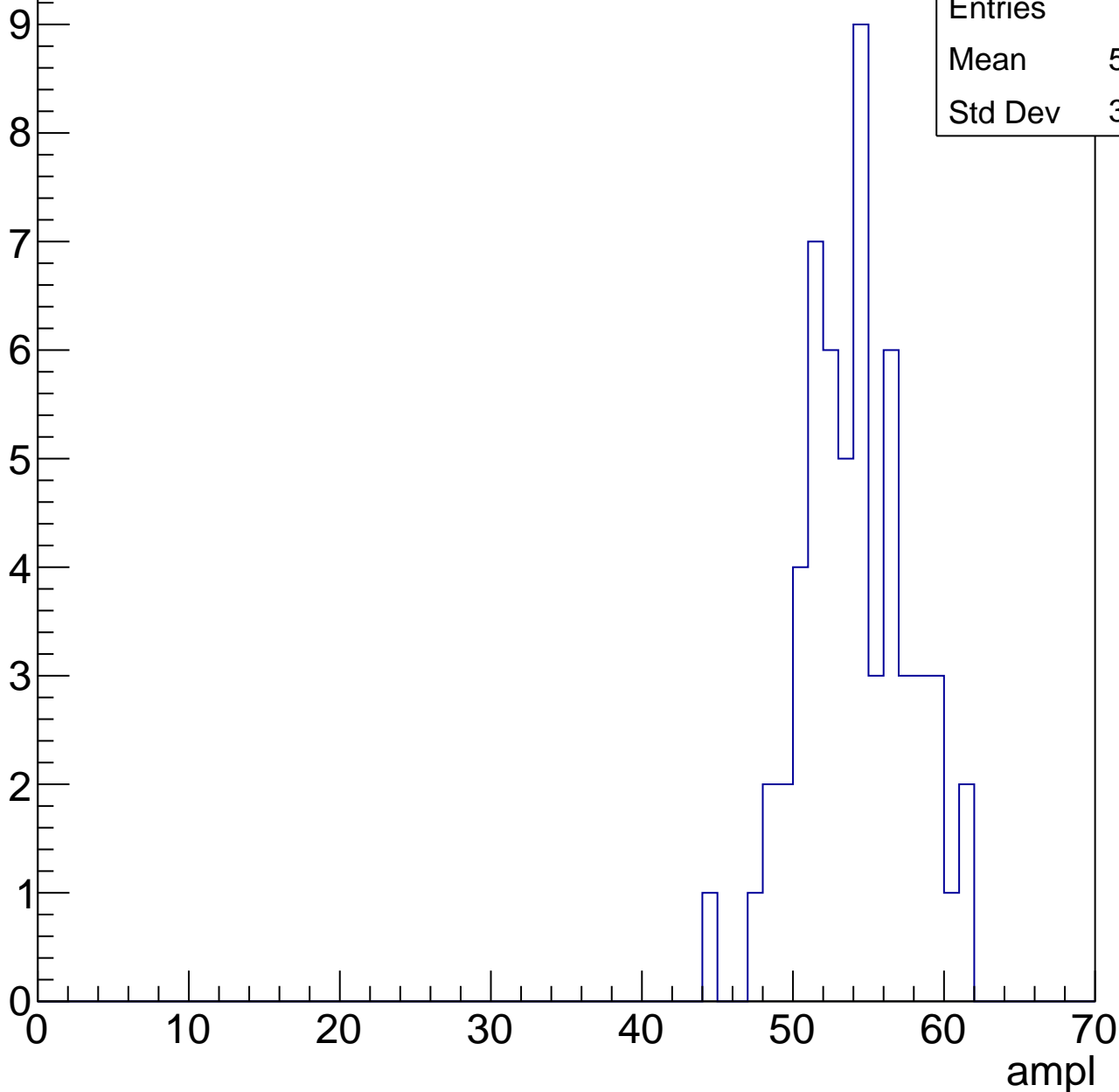


B1L103S, U21-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.62
Std Dev	3.547

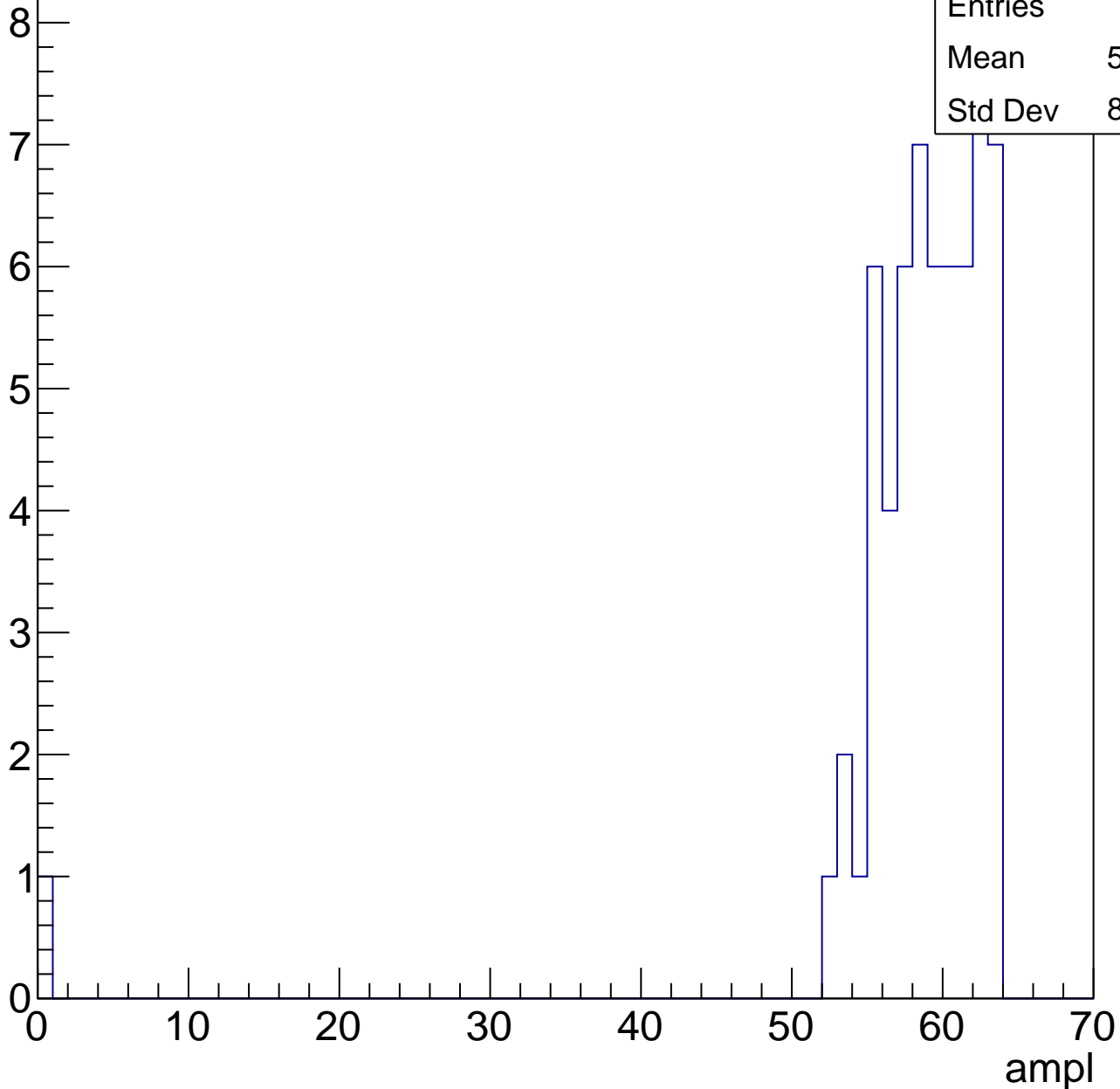


B1L103S, U21-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.89
Std Dev	8.025



B1L103S, U21-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

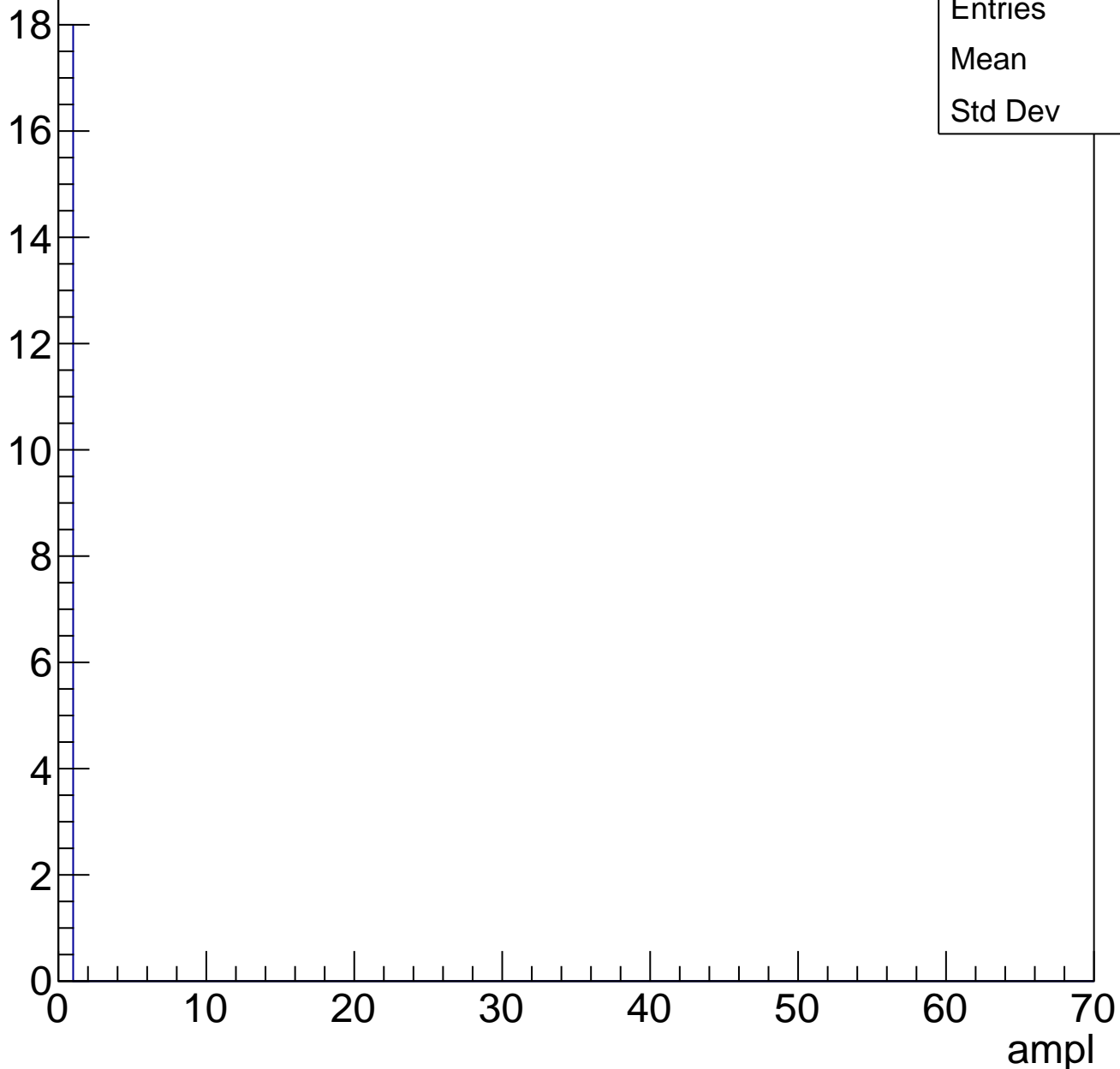
ampl

Entries	11
Mean	61.27
Std Dev	1.355

B1L103S, U21-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U21-ch71, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	23.95
Std Dev	11.5

Gaus mean : 29.4037

Gaus Width: 3.4179

Entry

12

10

8

6

4

2

0

0

10

20

30

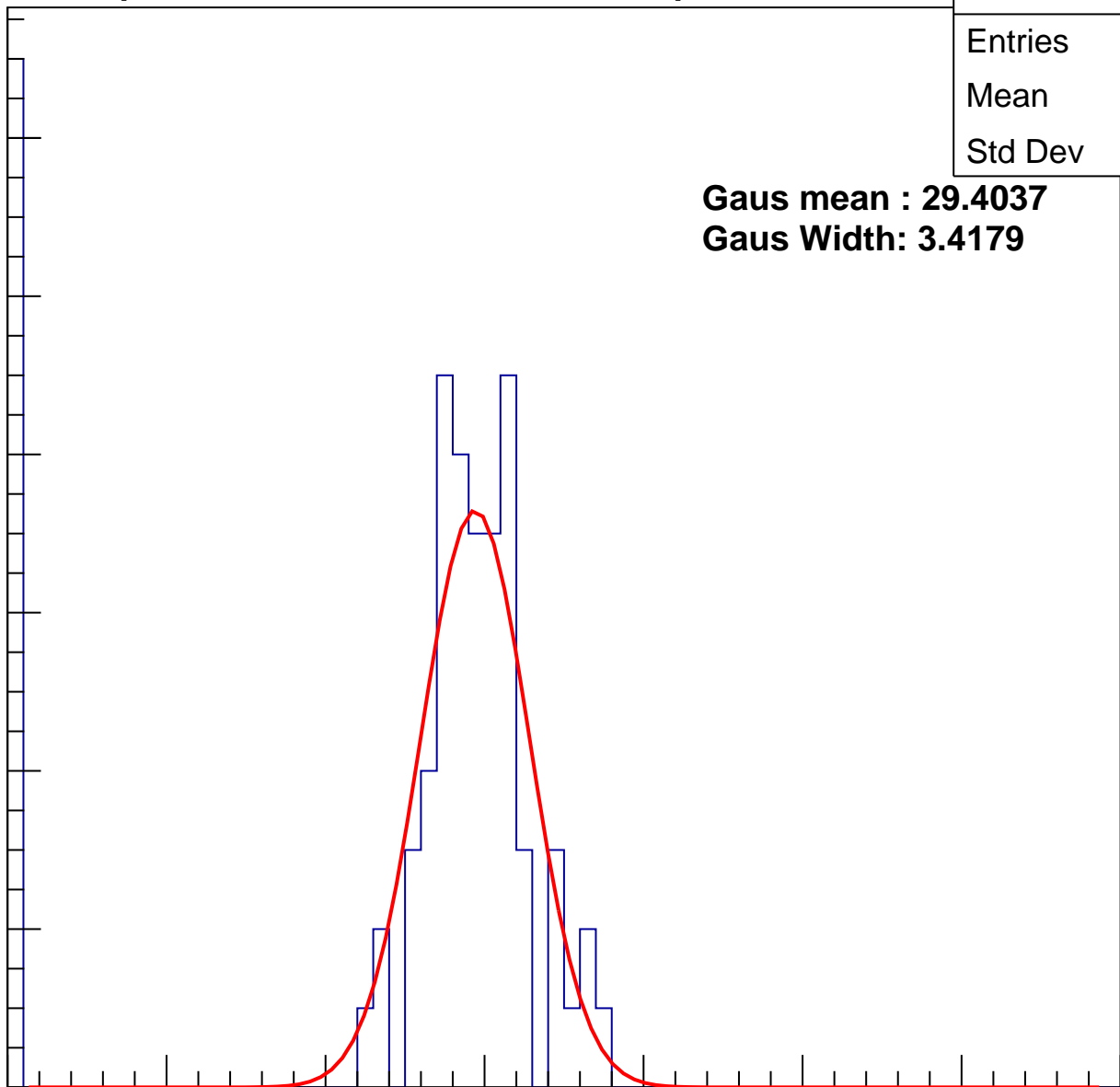
40

50

60

70

ampl



B1L103S, U21-ch71, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	33.34
Std Dev	8.783

Gaus mean : 36.0719

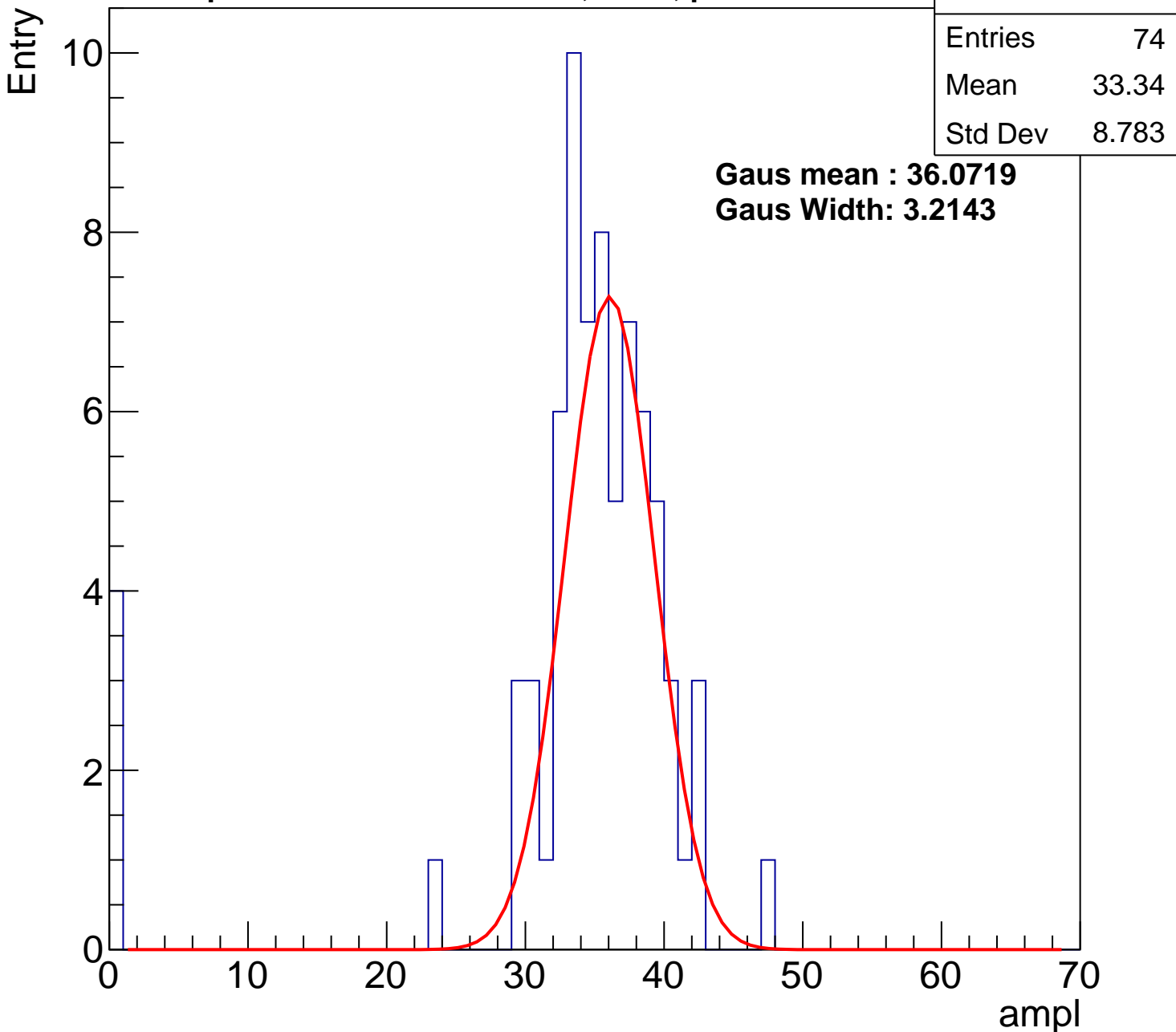
Gaus Width: 3.2143

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch71, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	36.84
Std Dev	14.54

Gaus mean : 42.6816

Gaus Width: 3.4137

Entry

10

8

6

4

2

0

0

10

20

30

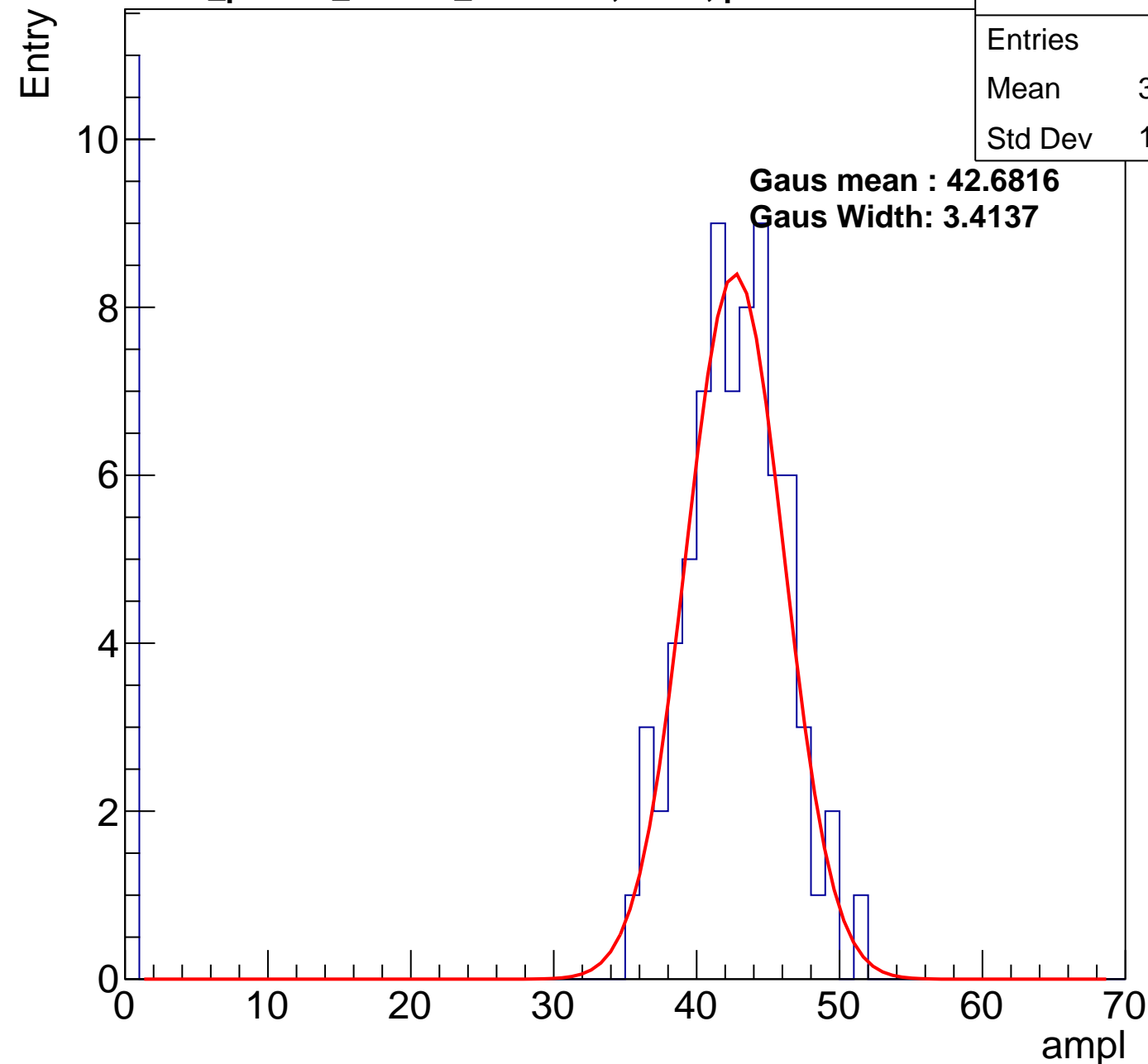
40

50

60

70

ampl

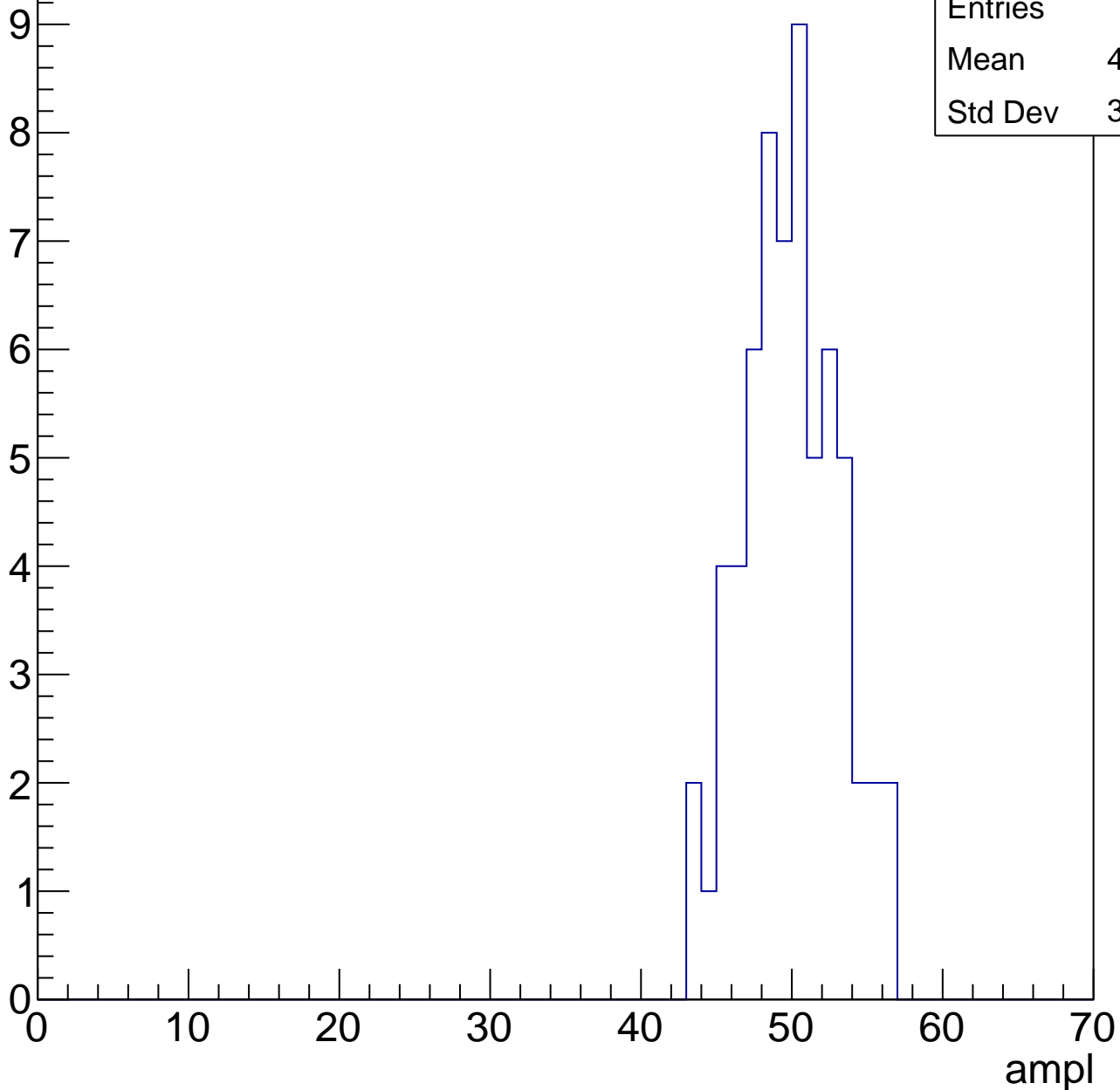


B1L103S, U21-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.44
Std Dev	3.085

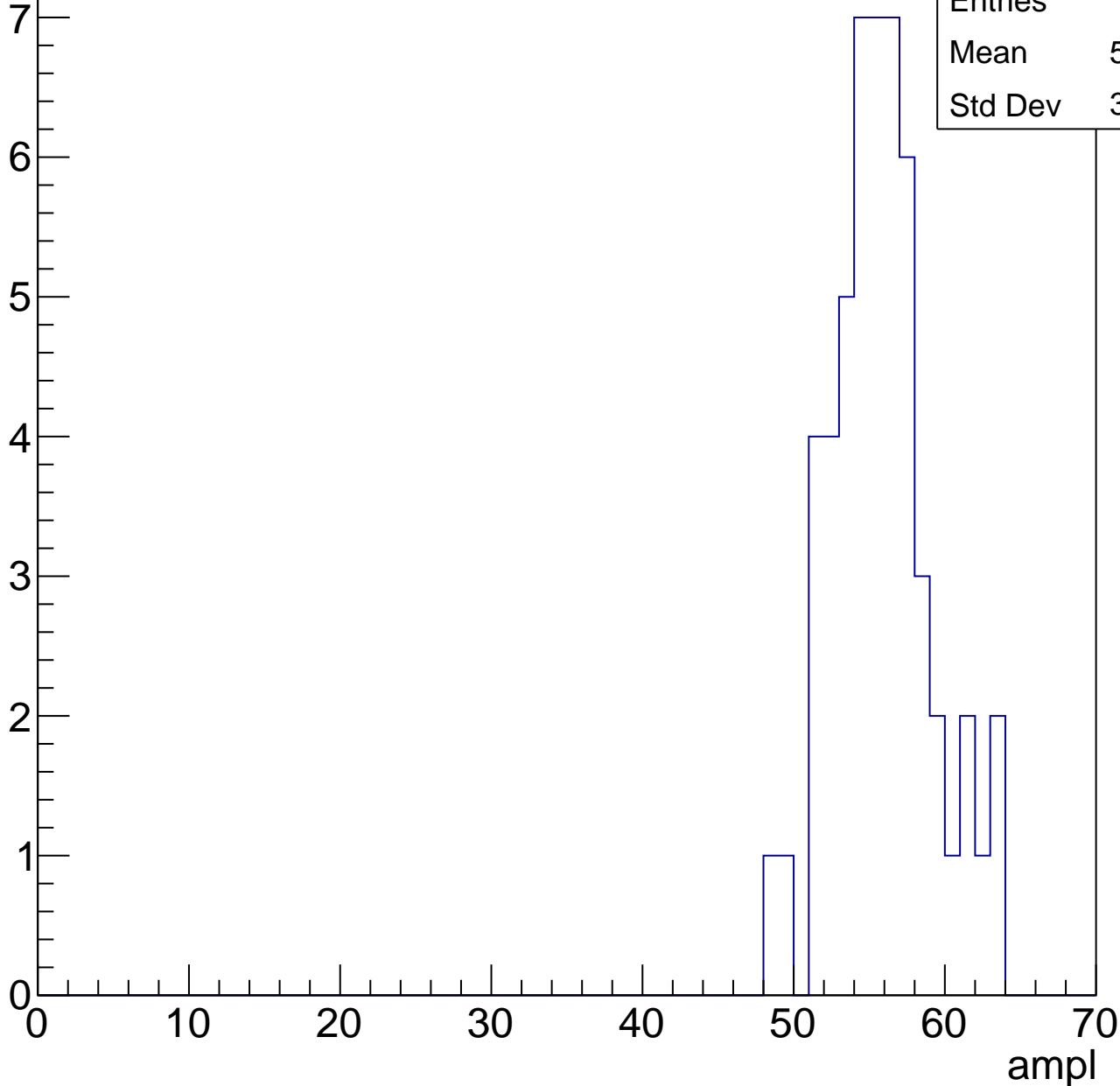


B1L103S, U21-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	55.34
Std Dev	3.279



B1L103S, U21-ch71, adc5

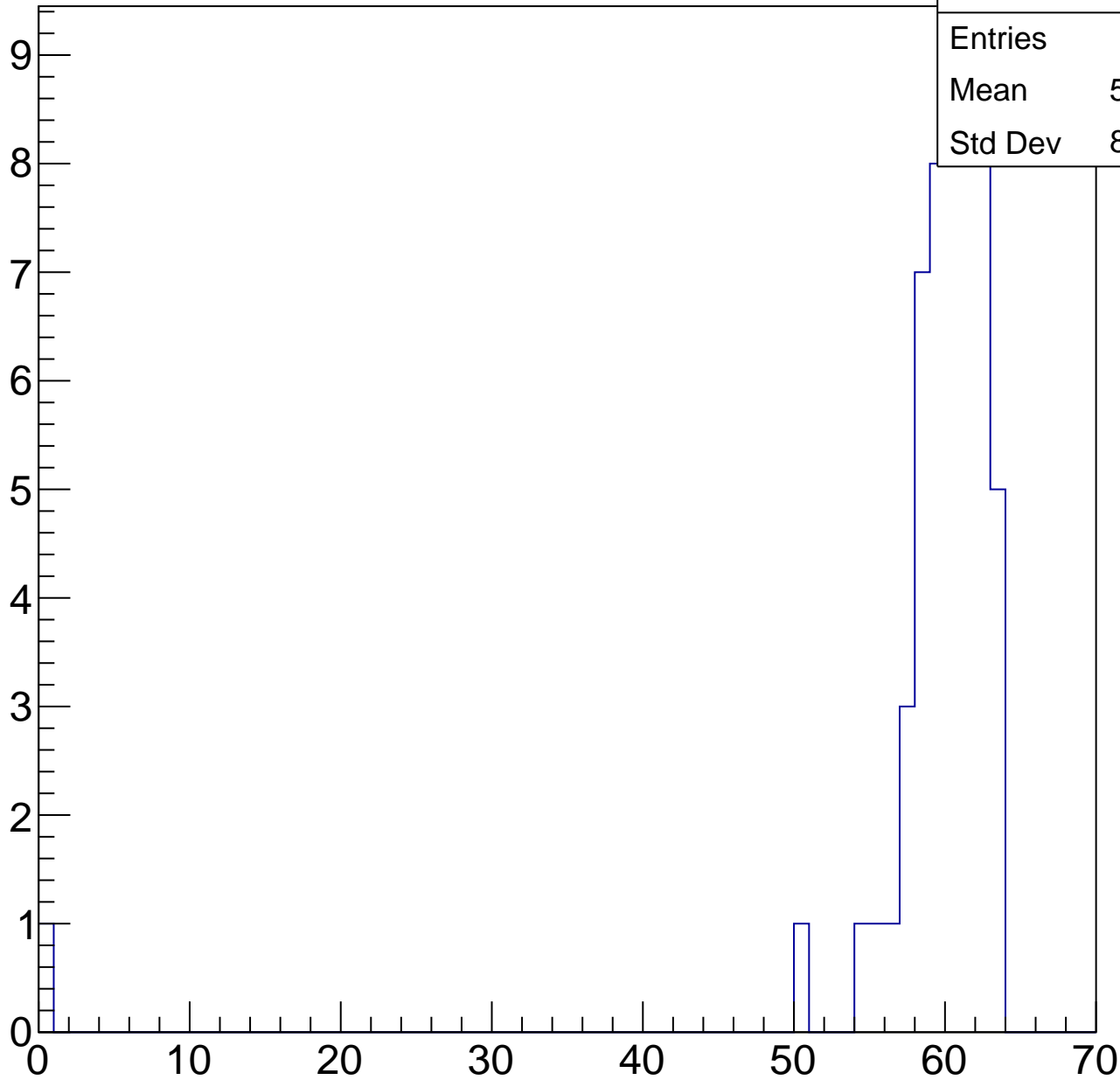
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.58
Std Dev	8.493

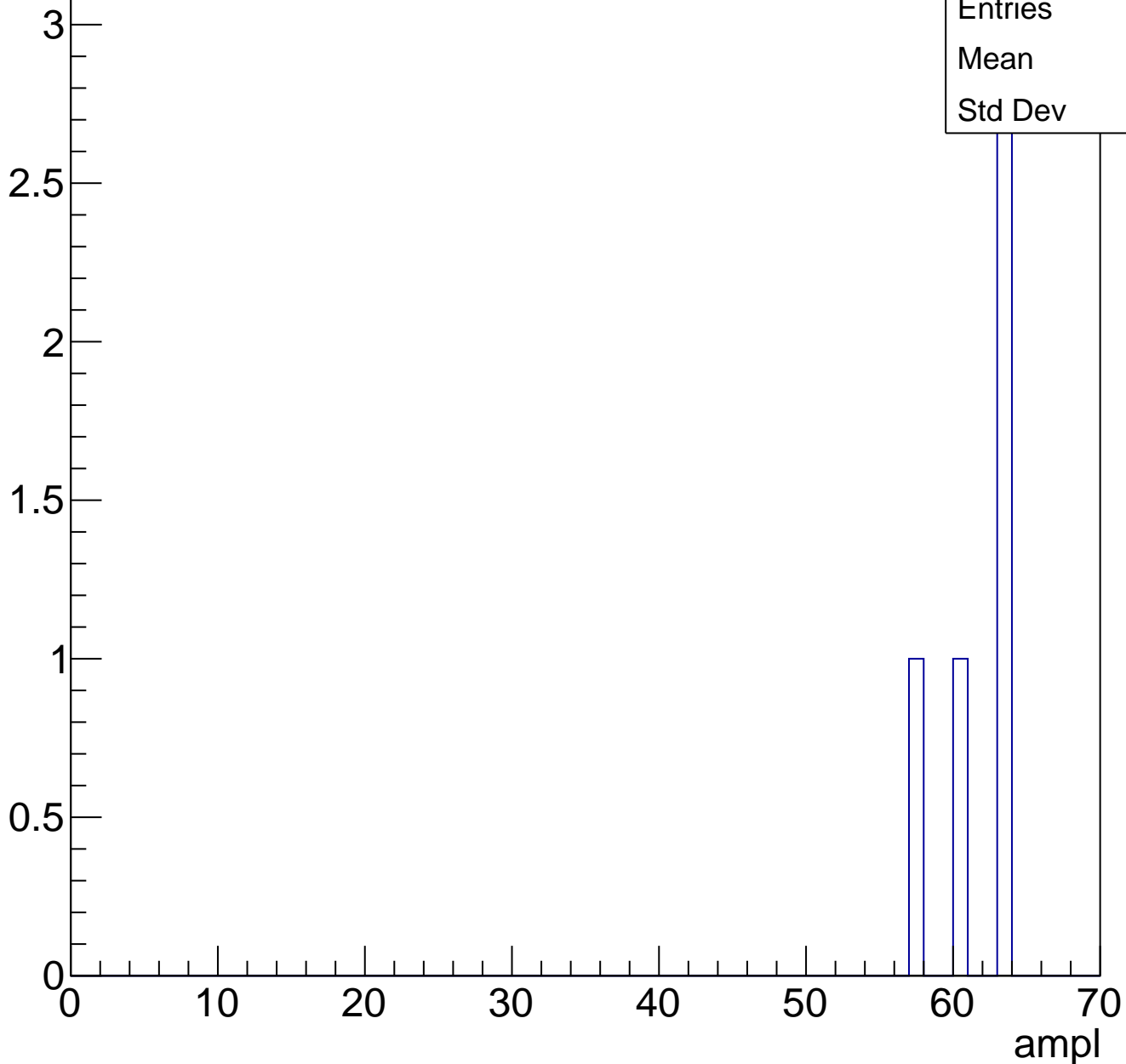
ampl



B1L103S, U21-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch72, adc0

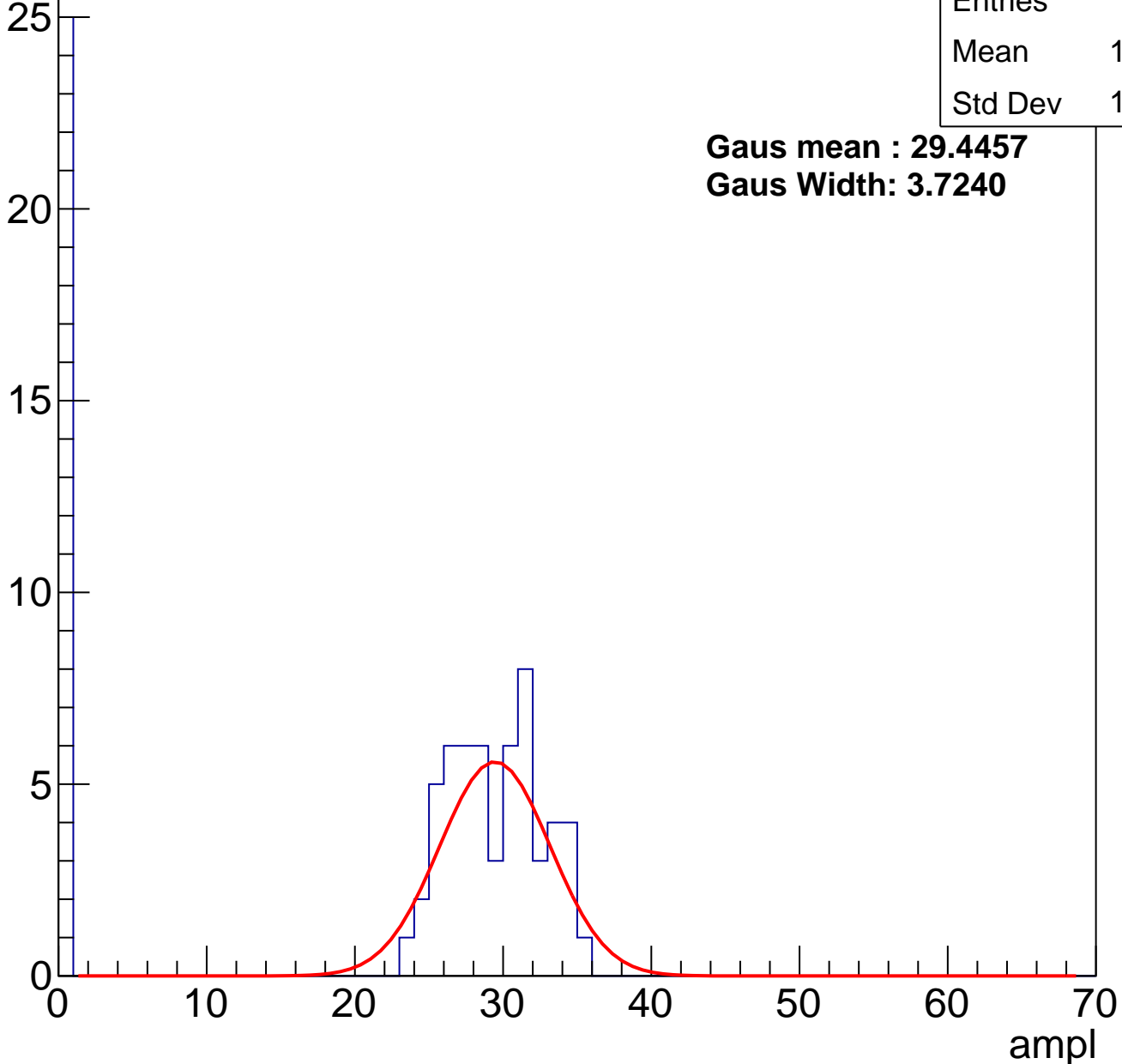
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	19.95
Std Dev	13.69

Gaus mean : 29.4457

Gaus Width: 3.7240

Entry



B1L103S, U21-ch72, adc1

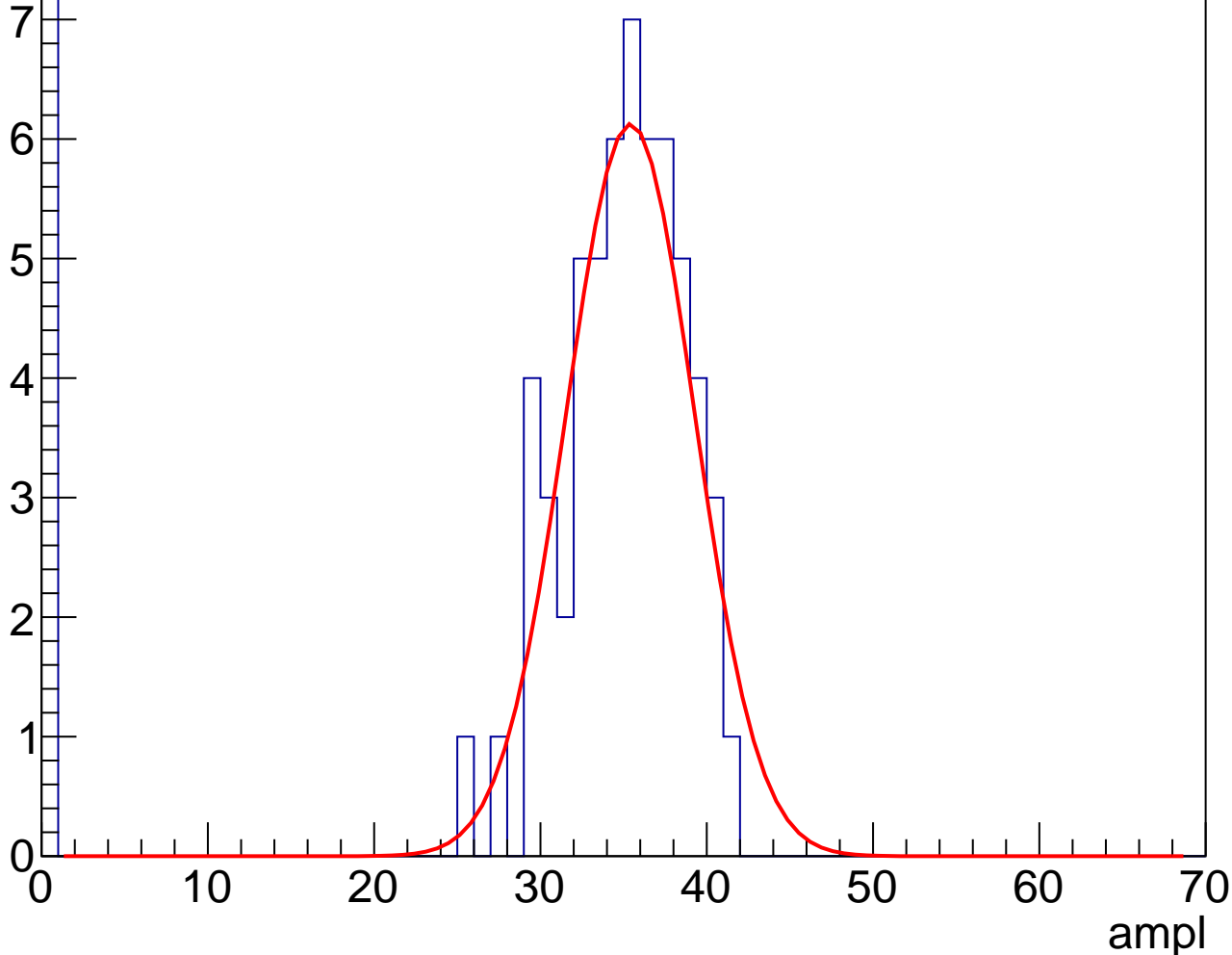
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	29.99
Std Dev	12.16

Gaus mean : 35.4087

Gaus Width: 3.8552



B1L103S, U21-ch72, adc2

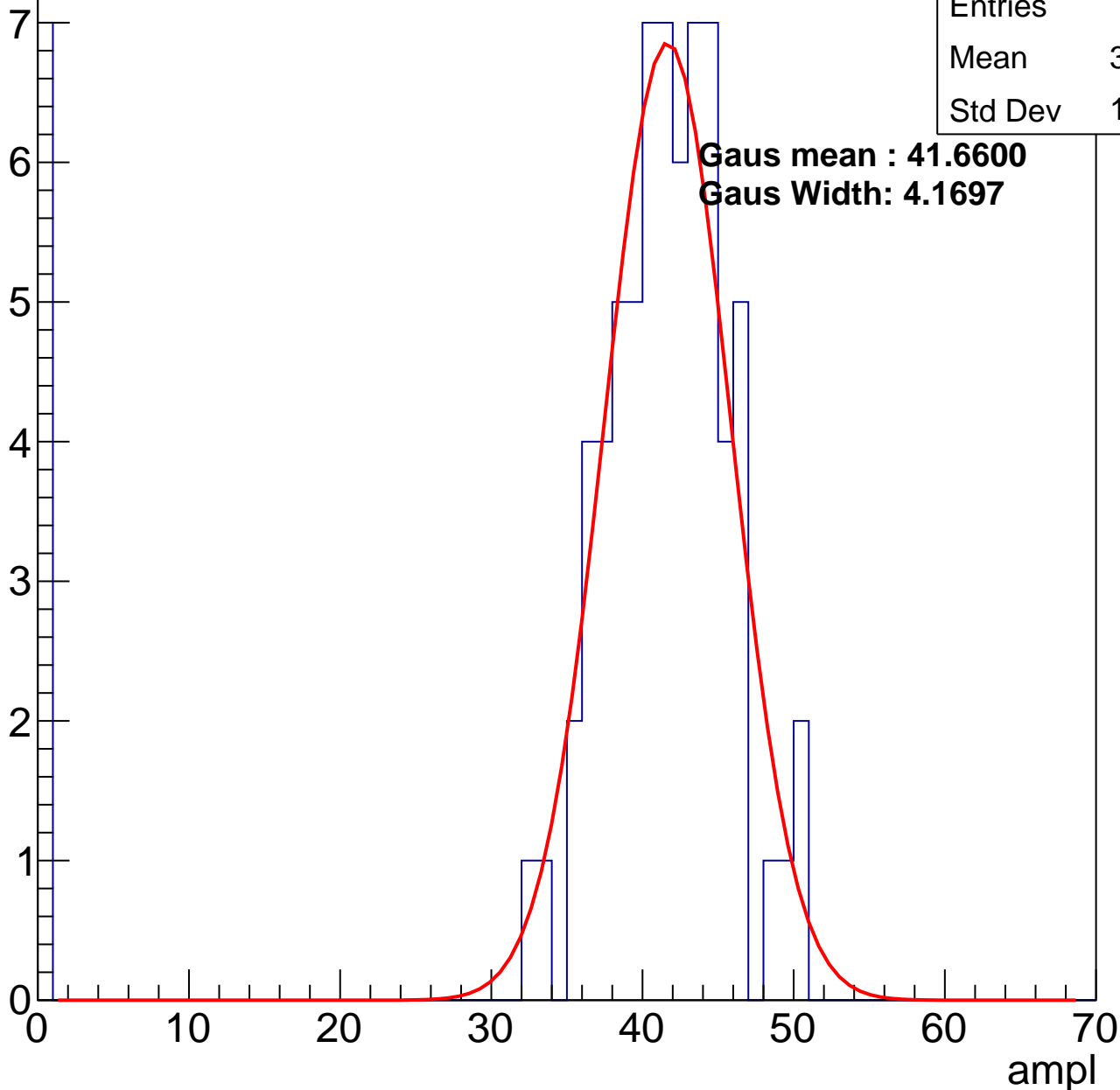
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	37.46
Std Dev	12.48

Gaus mean : 41.6600

Gaus Width: 4.1697

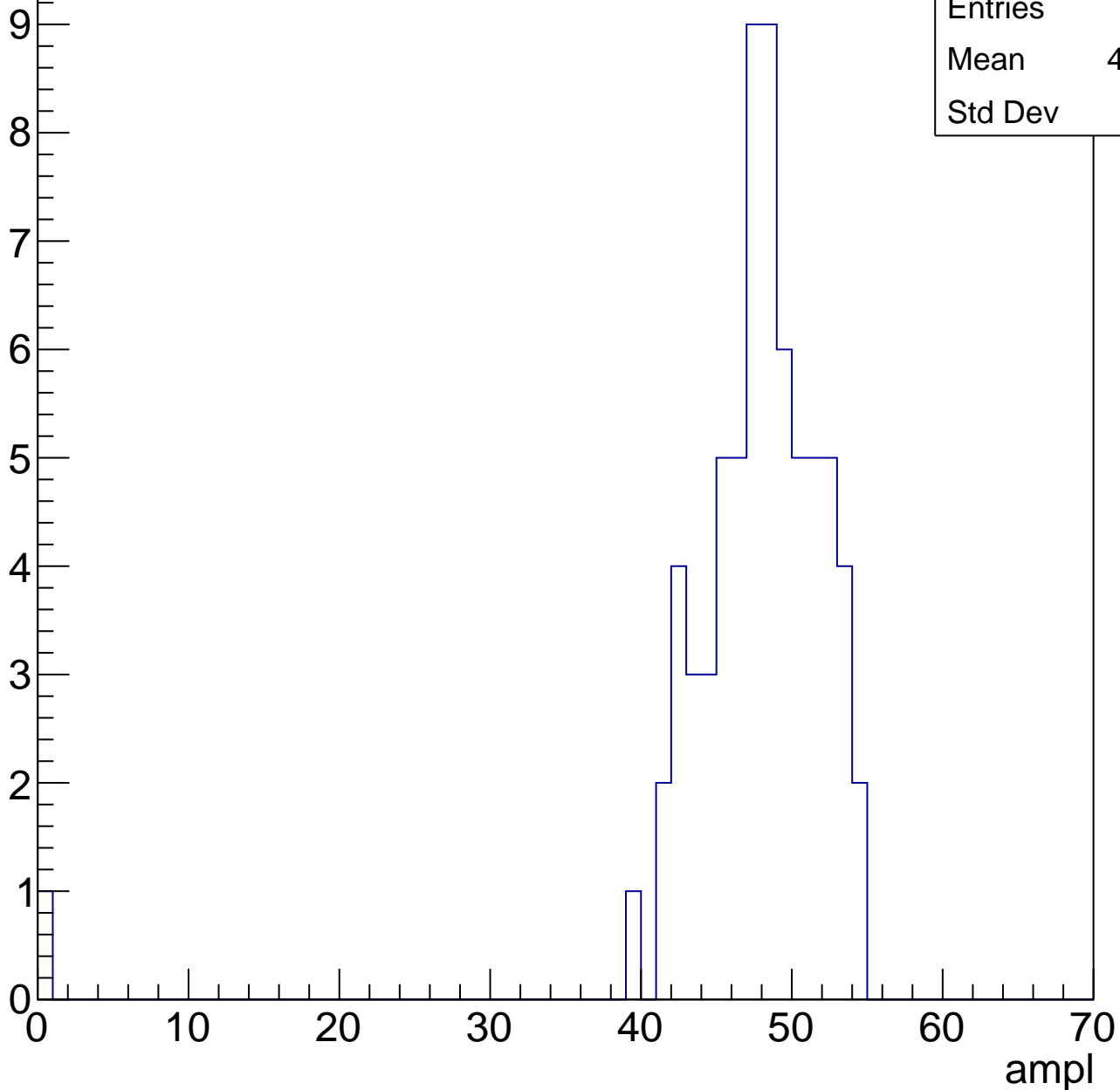


B1L103S, U21-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	46.94
Std Dev	6.67

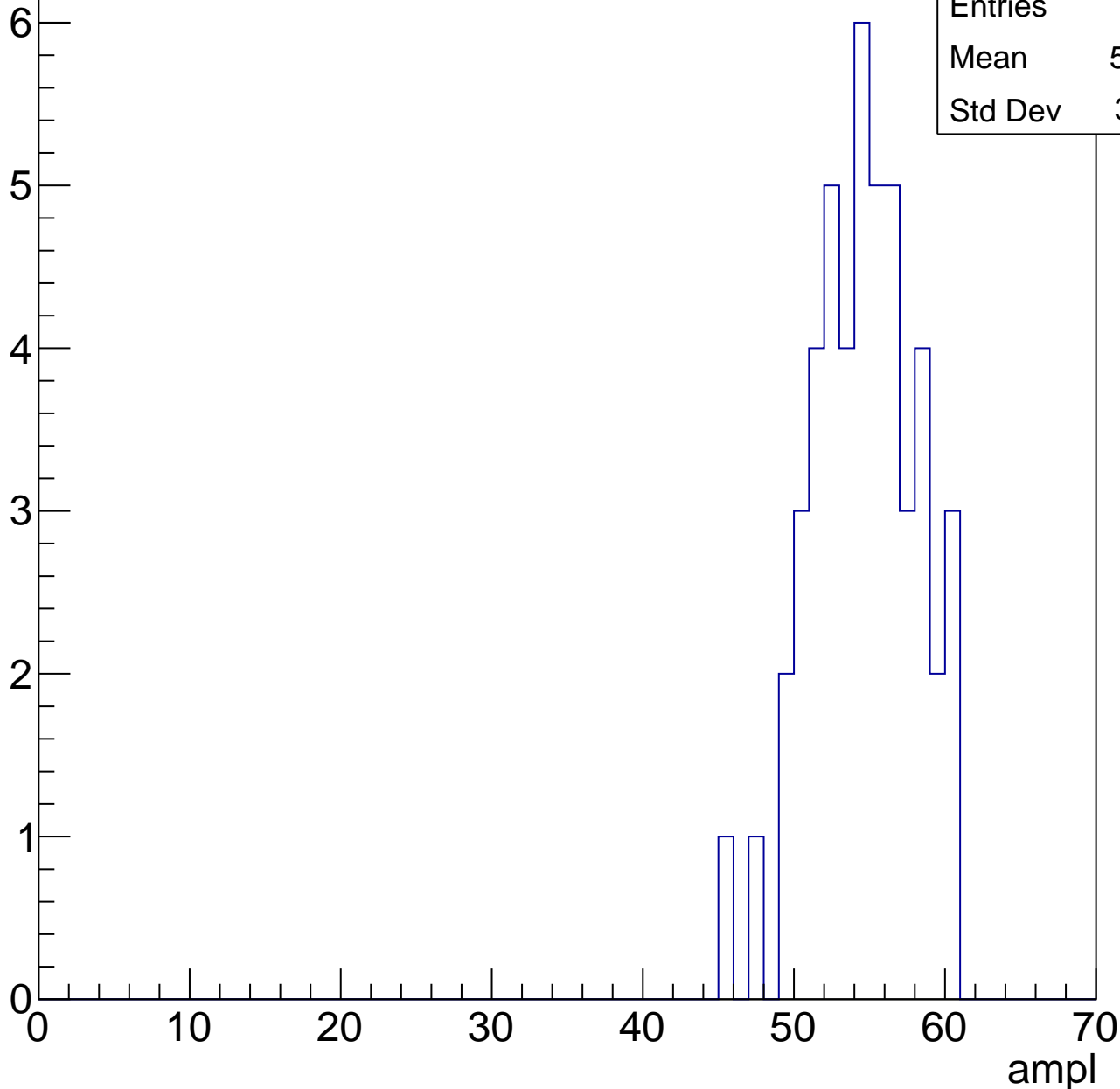


B1L103S, U21-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	54.08
Std Dev	3.421



B1L103S, U21-ch72, adc5

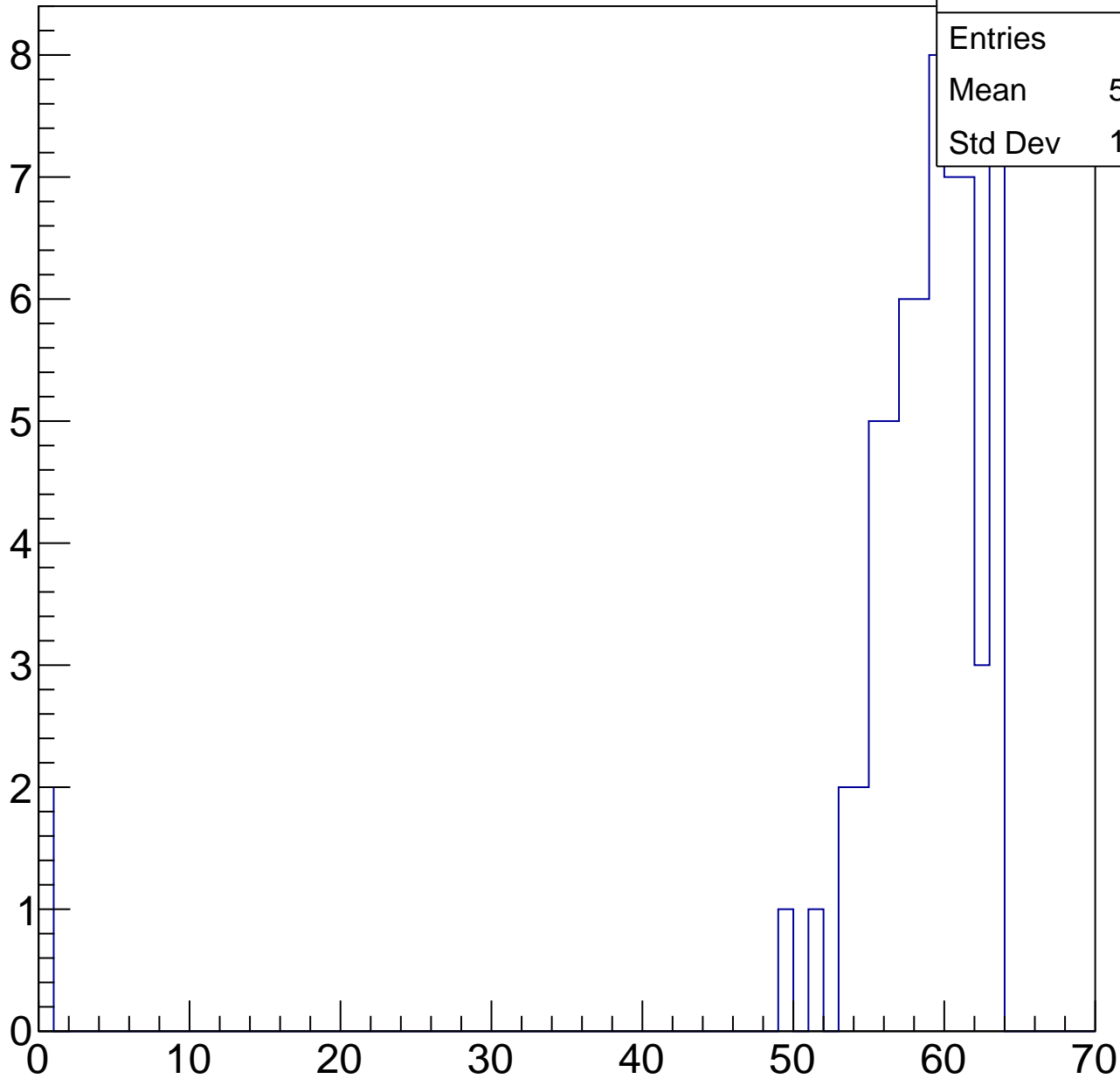
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	63
Mean	56.63
Std Dev	10.72

ampl

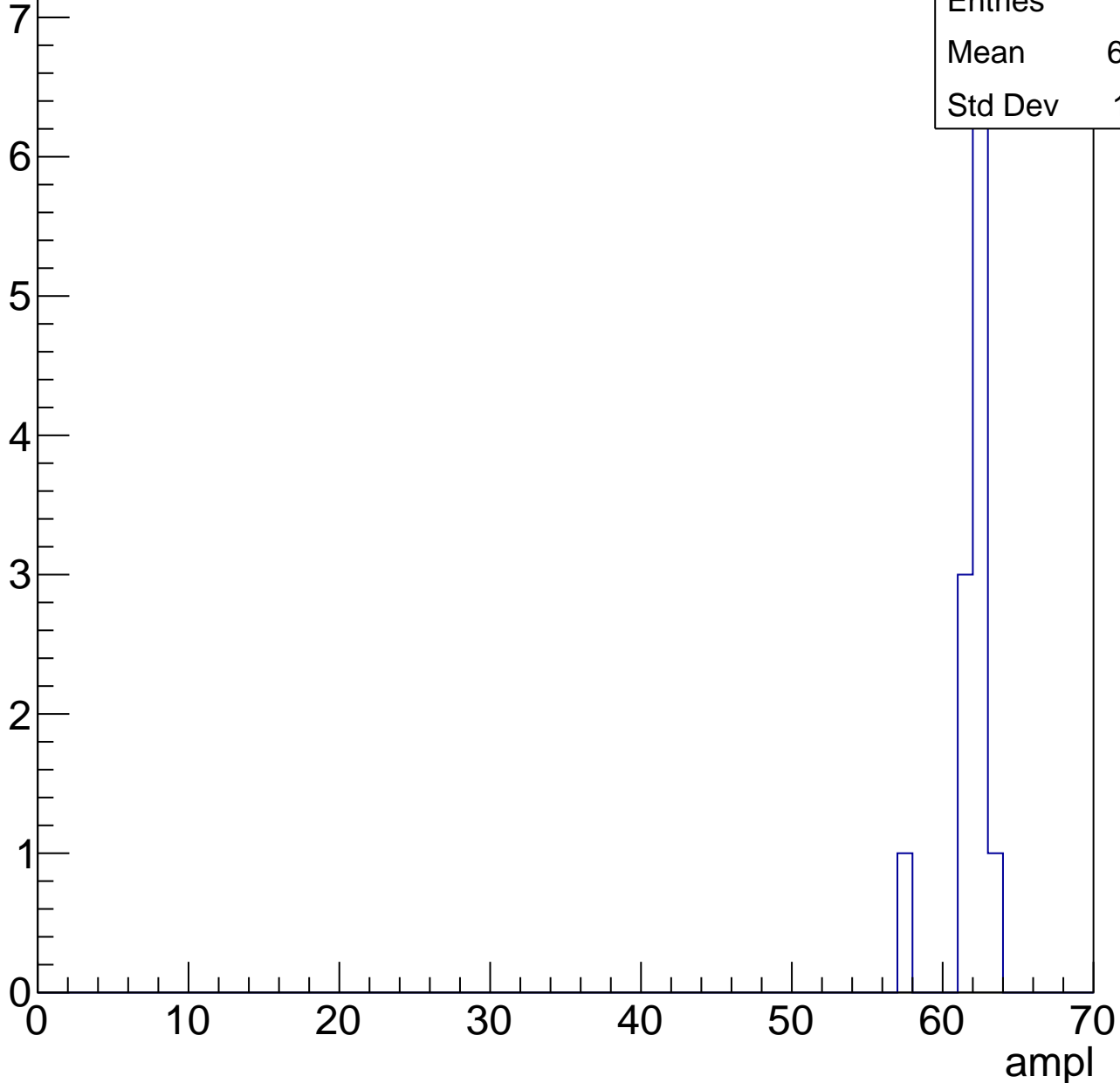


B1L103S, U21-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.42
Std Dev	1.441



B1L103S, U21-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U21-ch73, adc0

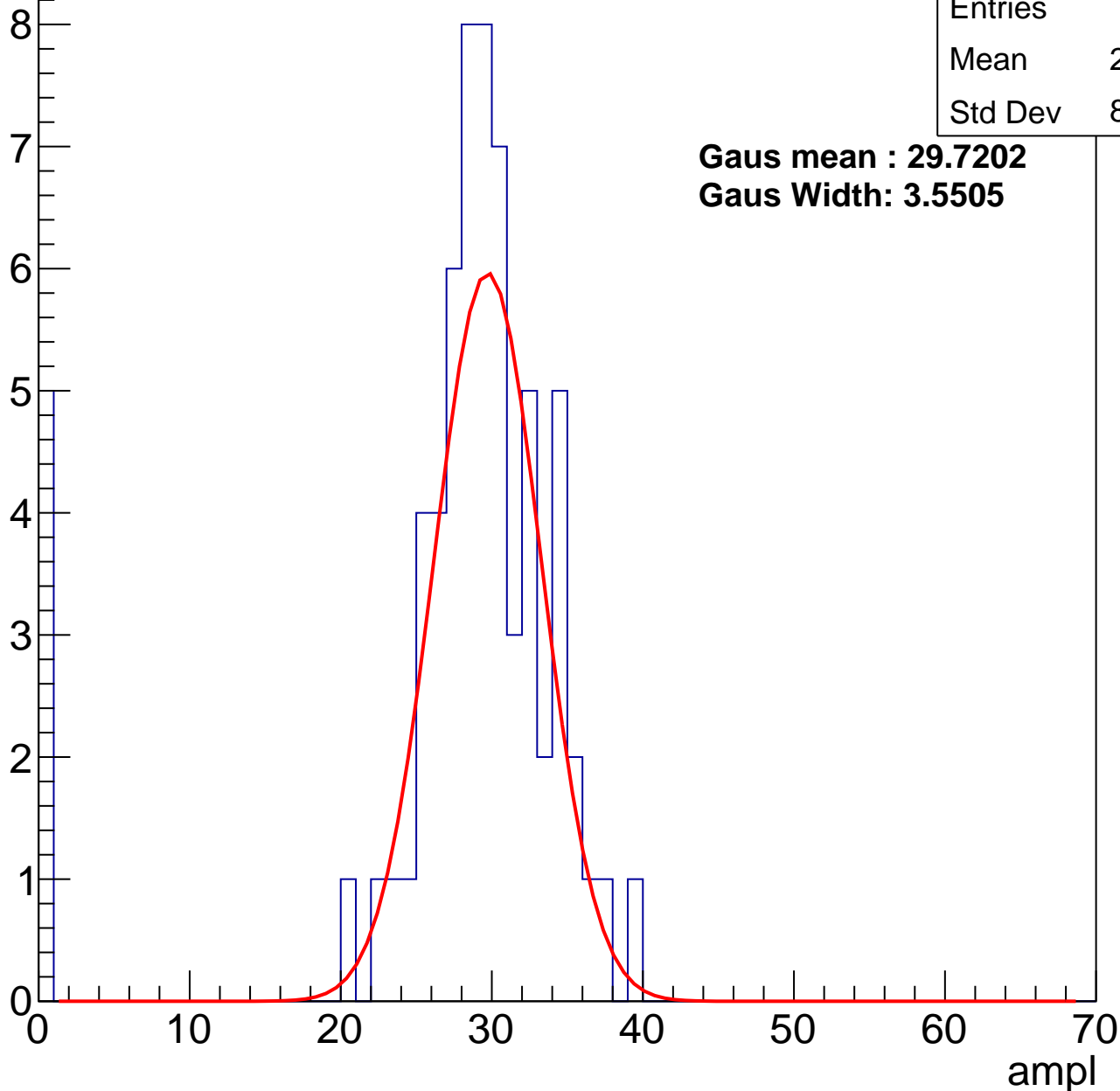
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	27.15
Std Dev	8.536

Gaus mean : 29.7202

Gaus Width: 3.5505



B1L103S, U21-ch73, adc1

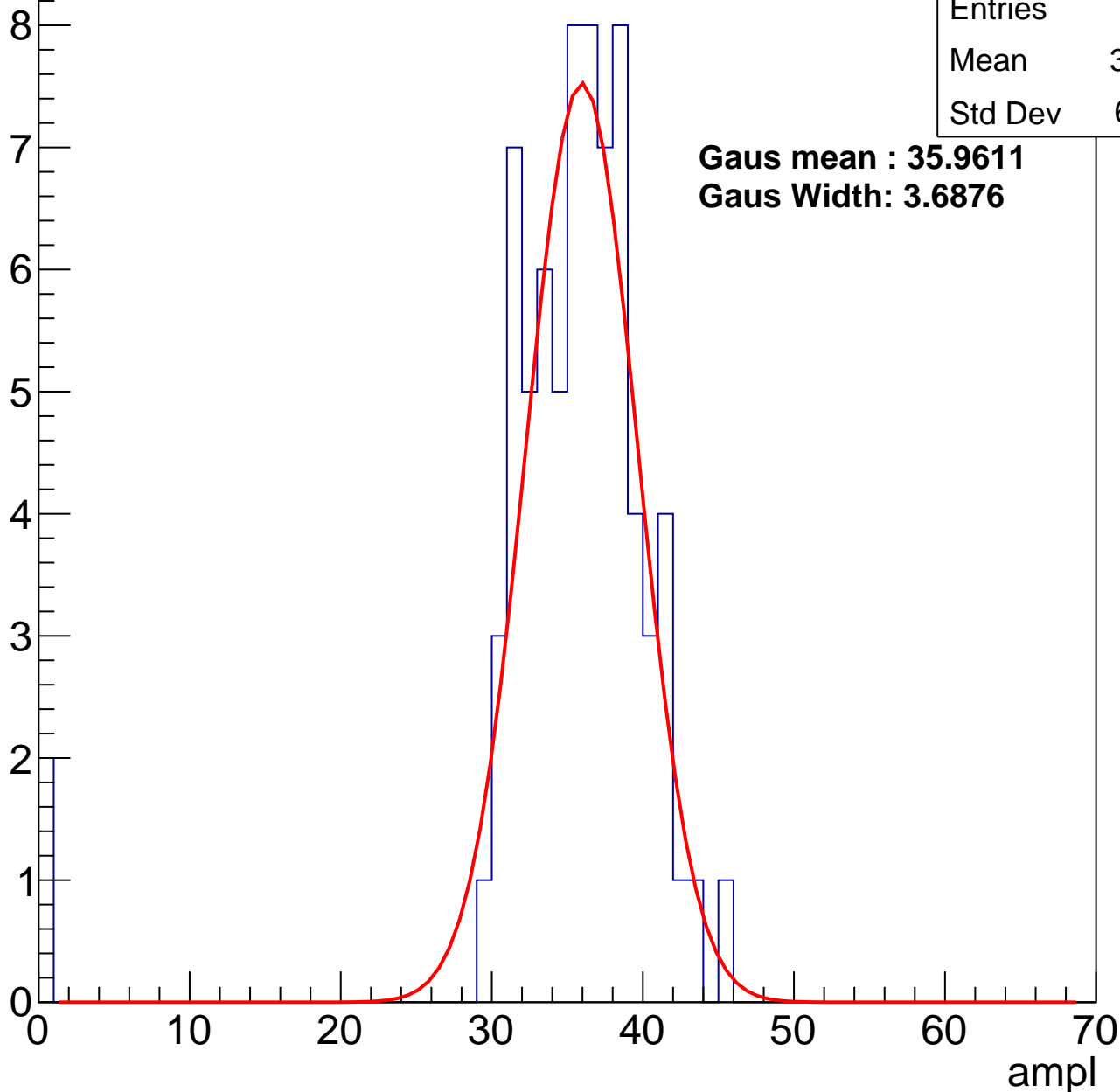
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	34.66
Std Dev	6.721

Gaus mean : 35.9611

Gaus Width: 3.6876



B1L103S, U21-ch73, adc2

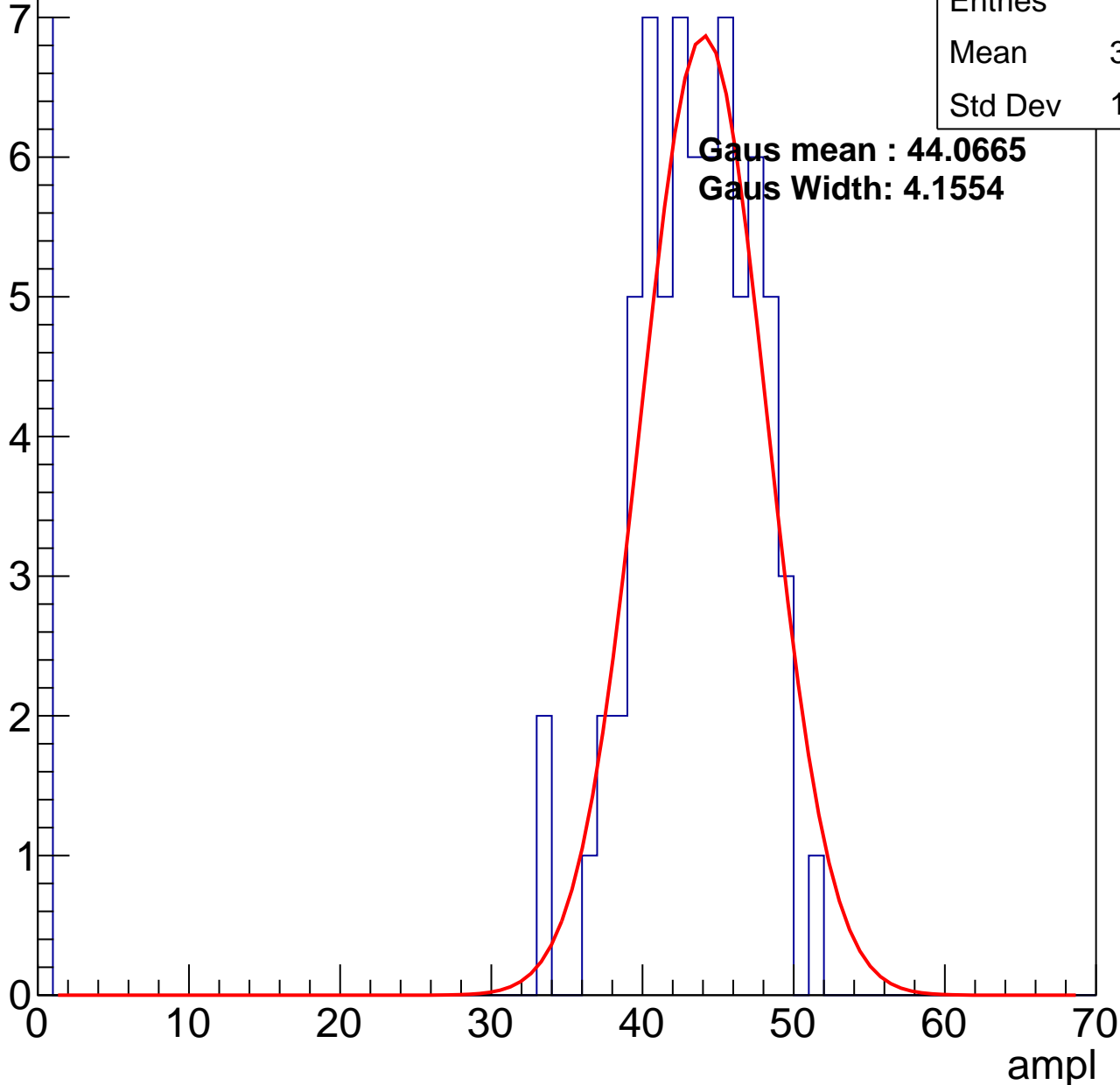
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	39.13
Std Dev	12.89

Gaus mean : 44.0665

Gaus Width: 4.1554

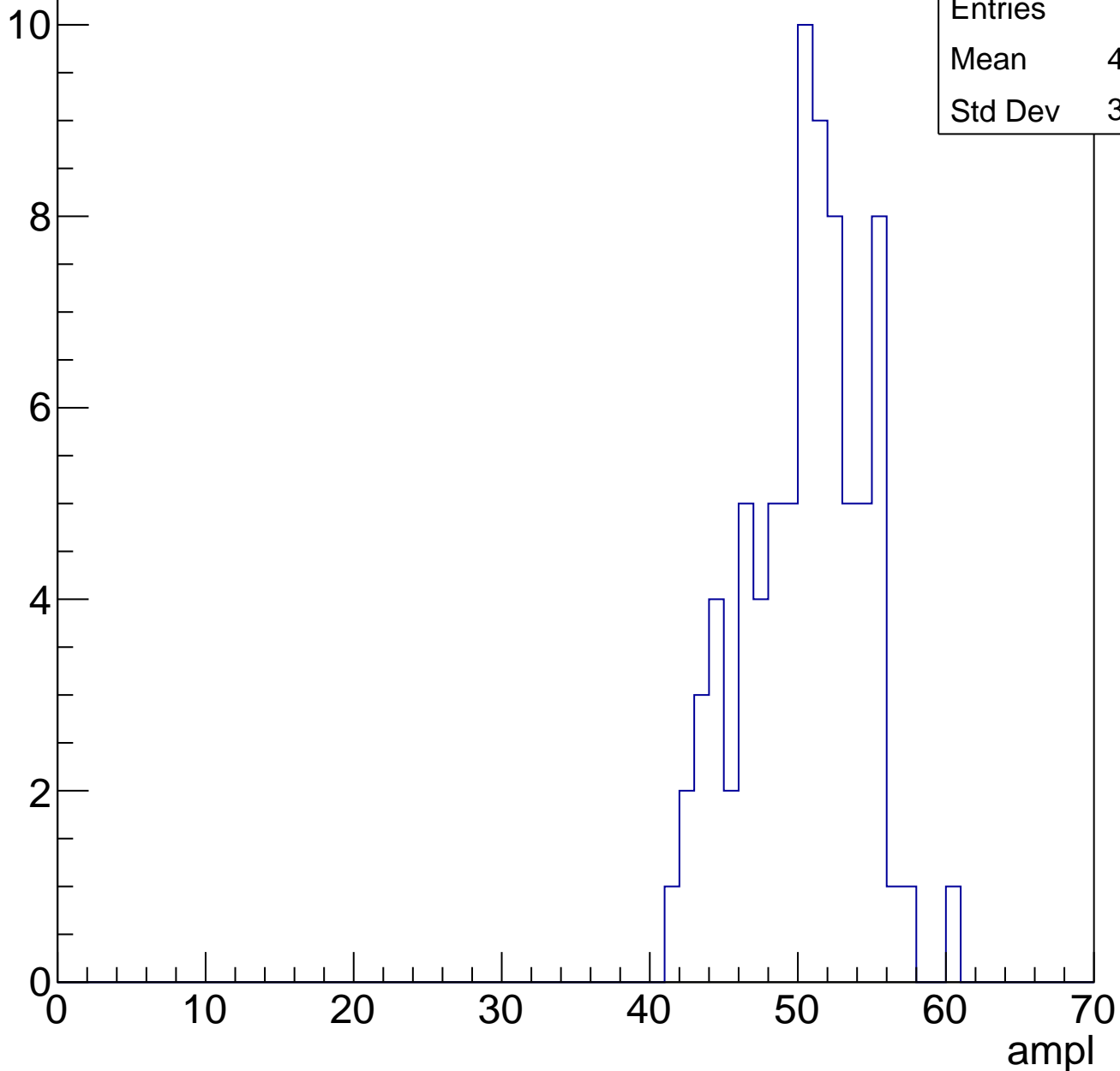


B1L103S, U21-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	49.95
Std Dev	3.965

Entry

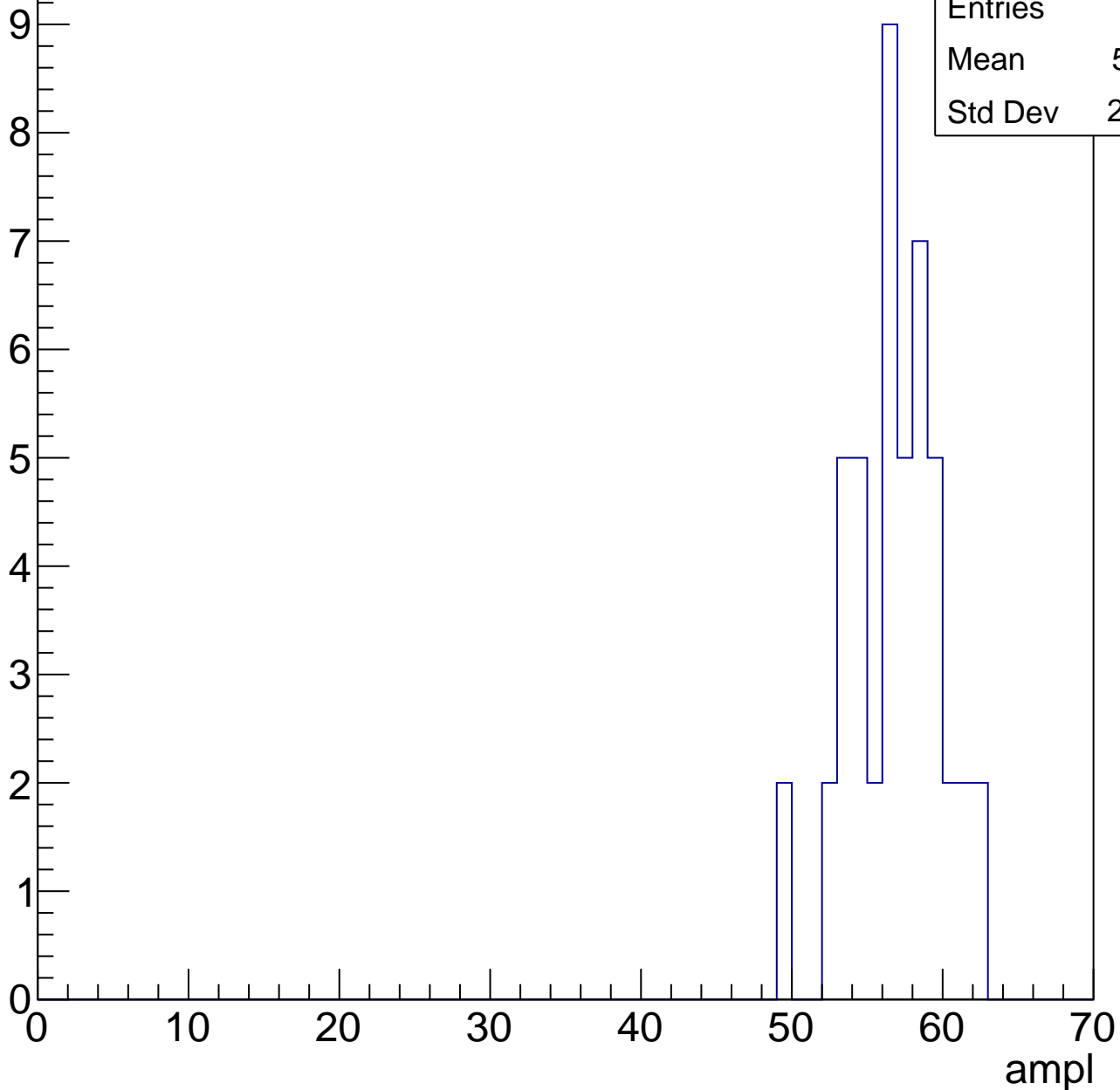


B1L103S, U21-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	56.31
Std Dev	2.973

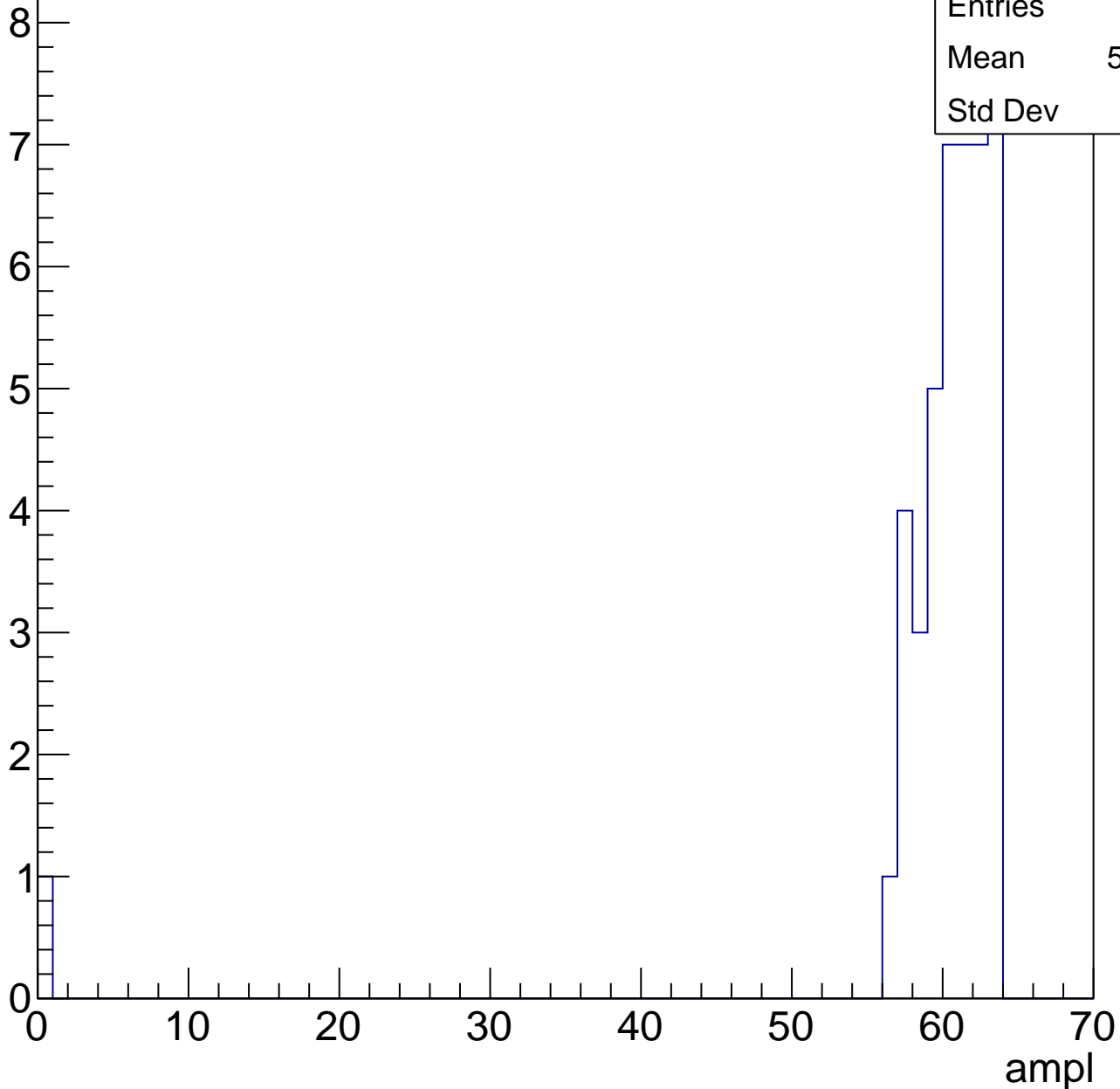


B1L103S, U21-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	59.02
Std Dev	9.32



B1L103S, U21-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



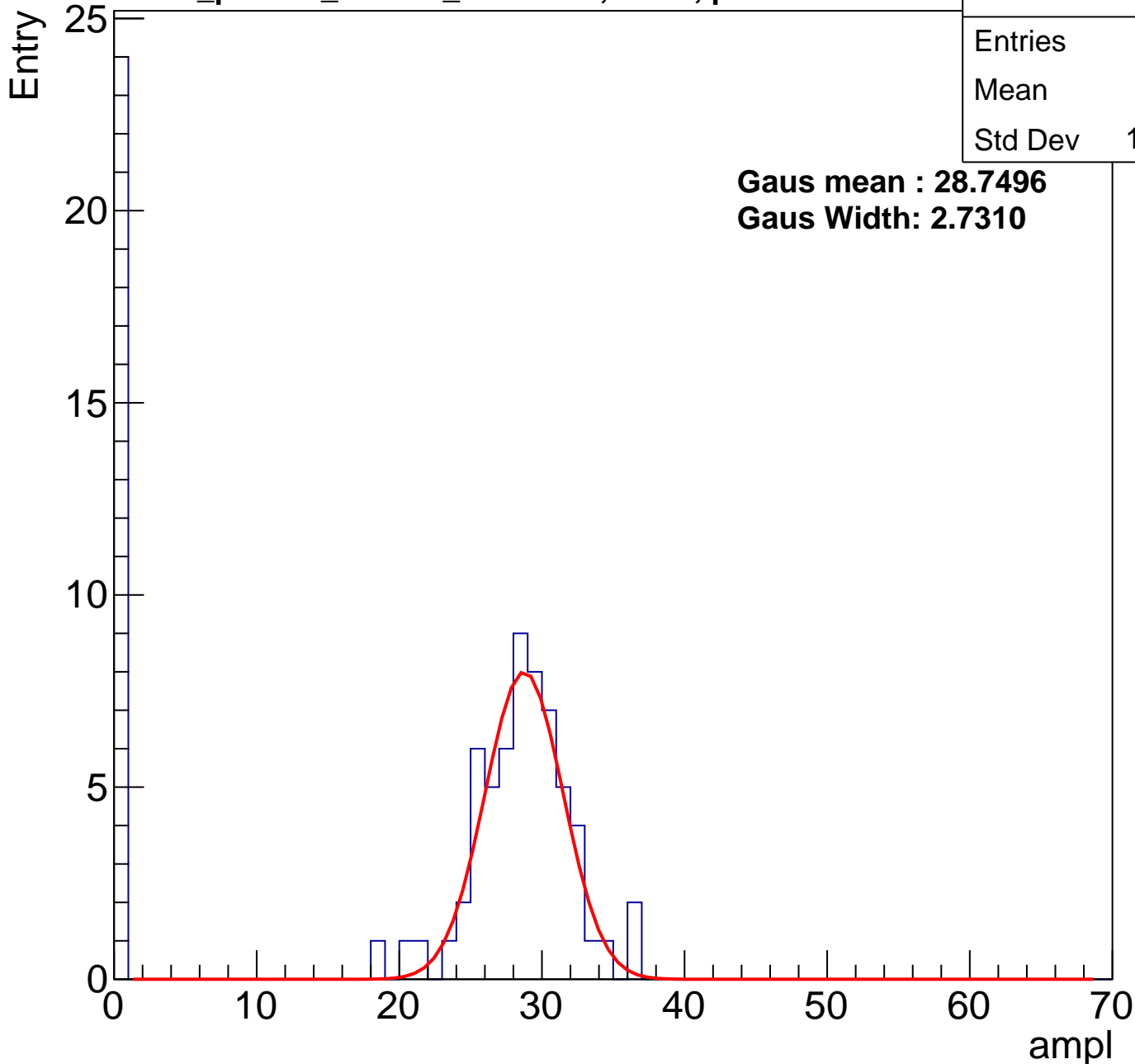
B1L103S, U21-ch74, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	20.1
Std Dev	13.03

Gaus mean : 28.7496

Gaus Width: 2.7310



B1L103S, U21-ch74, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	30.9
Std Dev	11.89

Gaus mean : 35.4680

Gaus Width: 3.7615

Entry

10

8

6

4

2

0

0

10

20

30

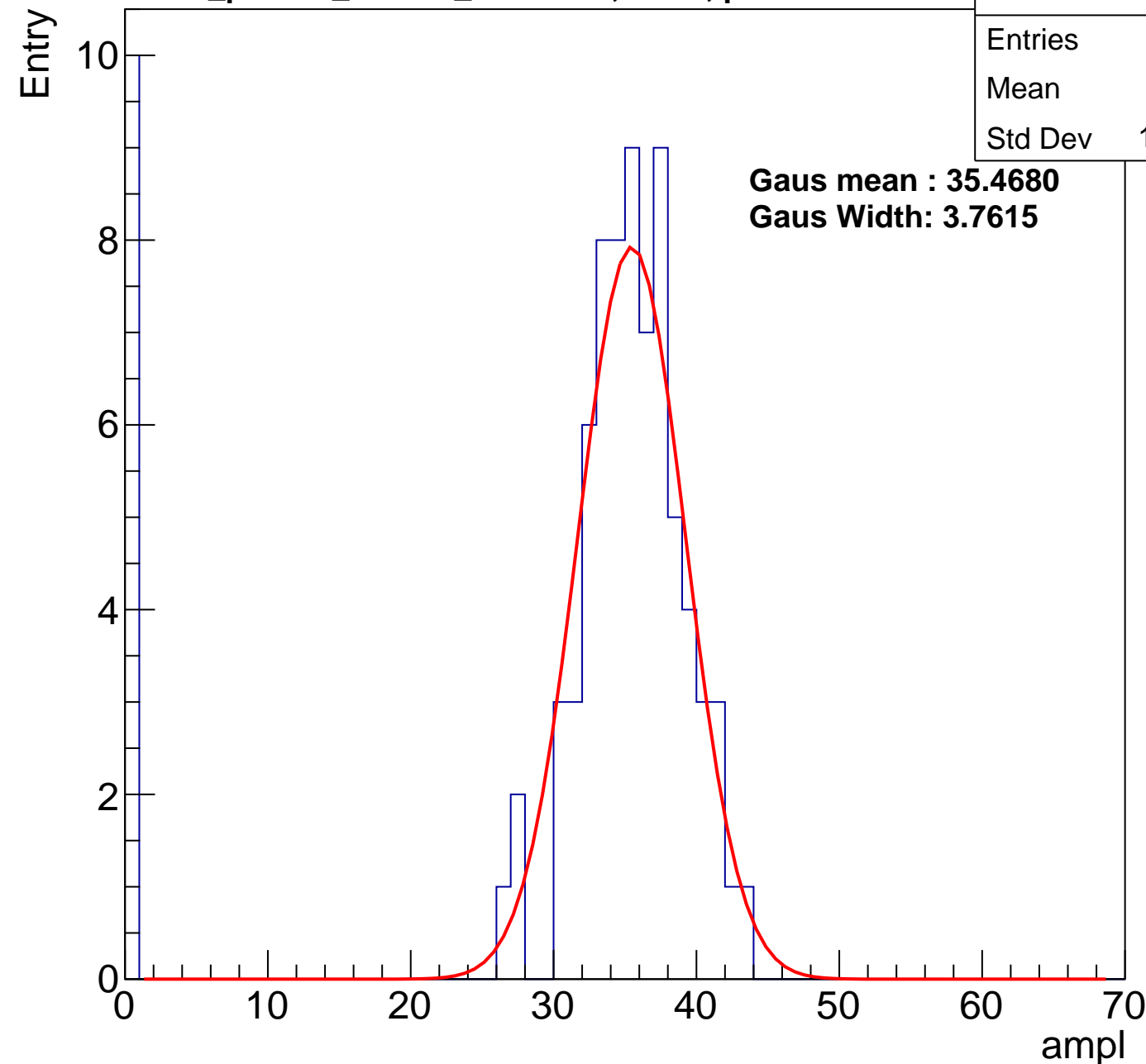
40

50

60

70

ampl



B1L103S, U21-ch74, adc2

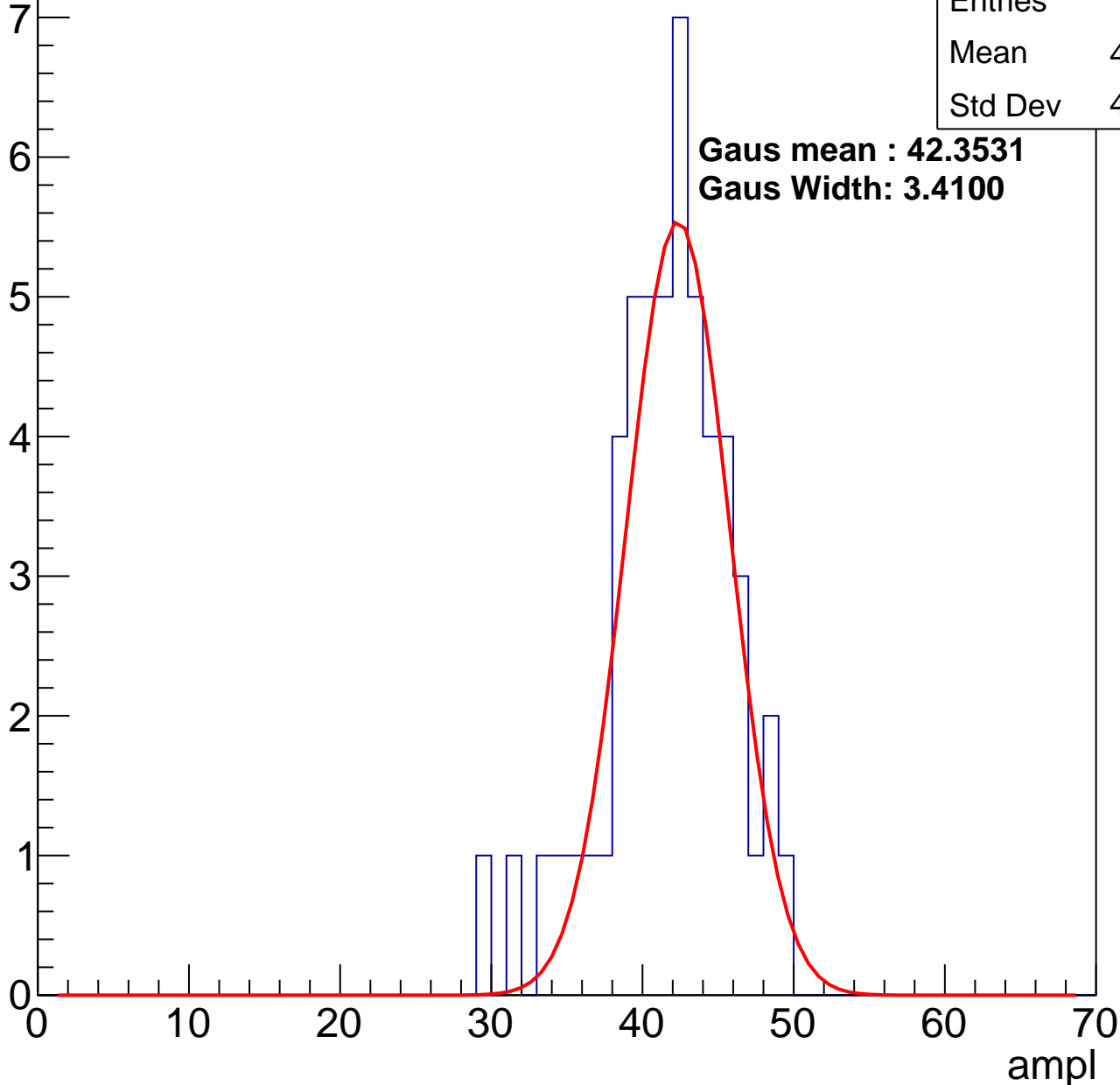
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	41.17
Std Dev	4.106

Gaus mean : 42.3531

Gaus Width: 3.4100

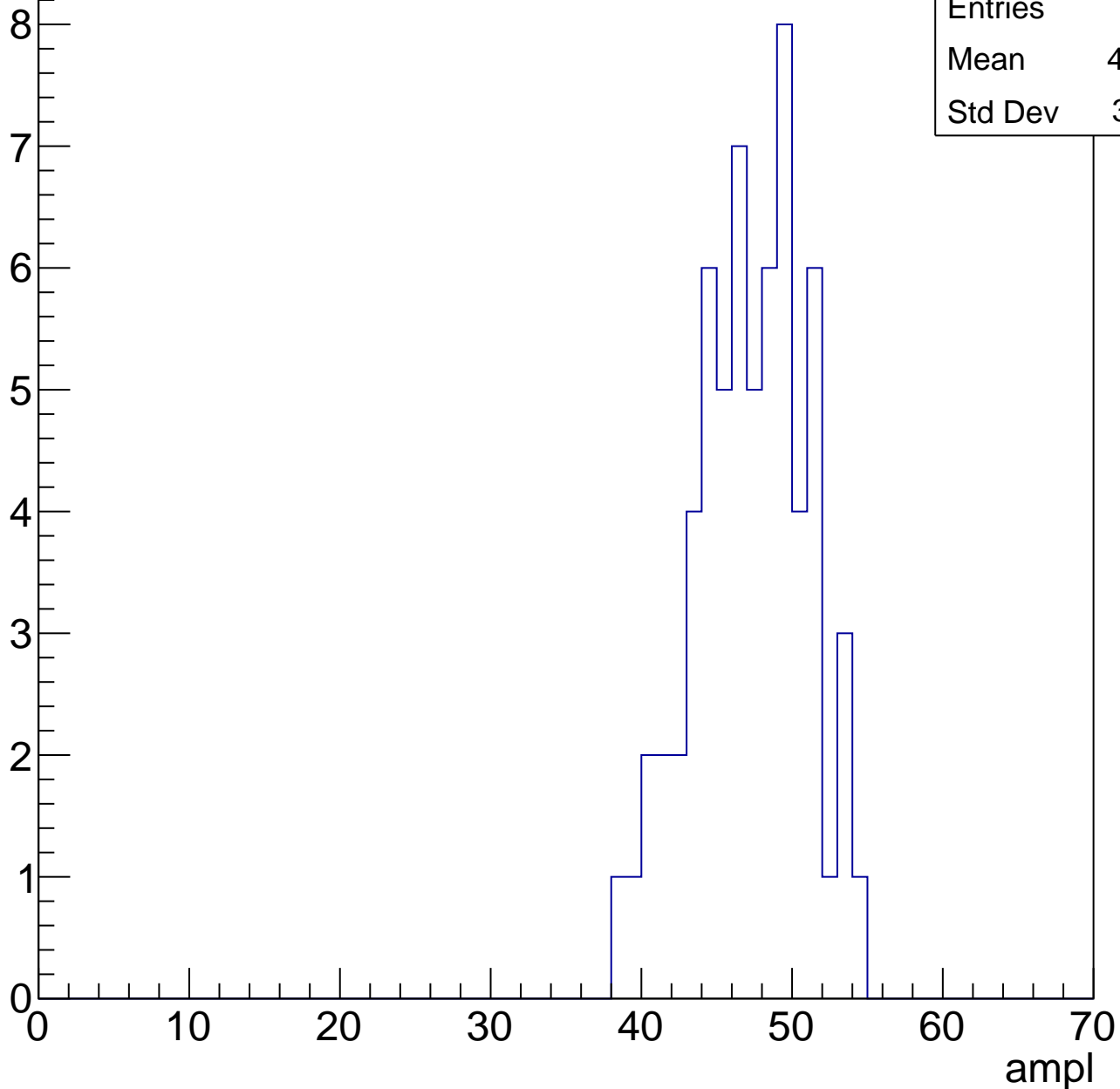


B1L103S, U21-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	46.75
Std Dev	3.661

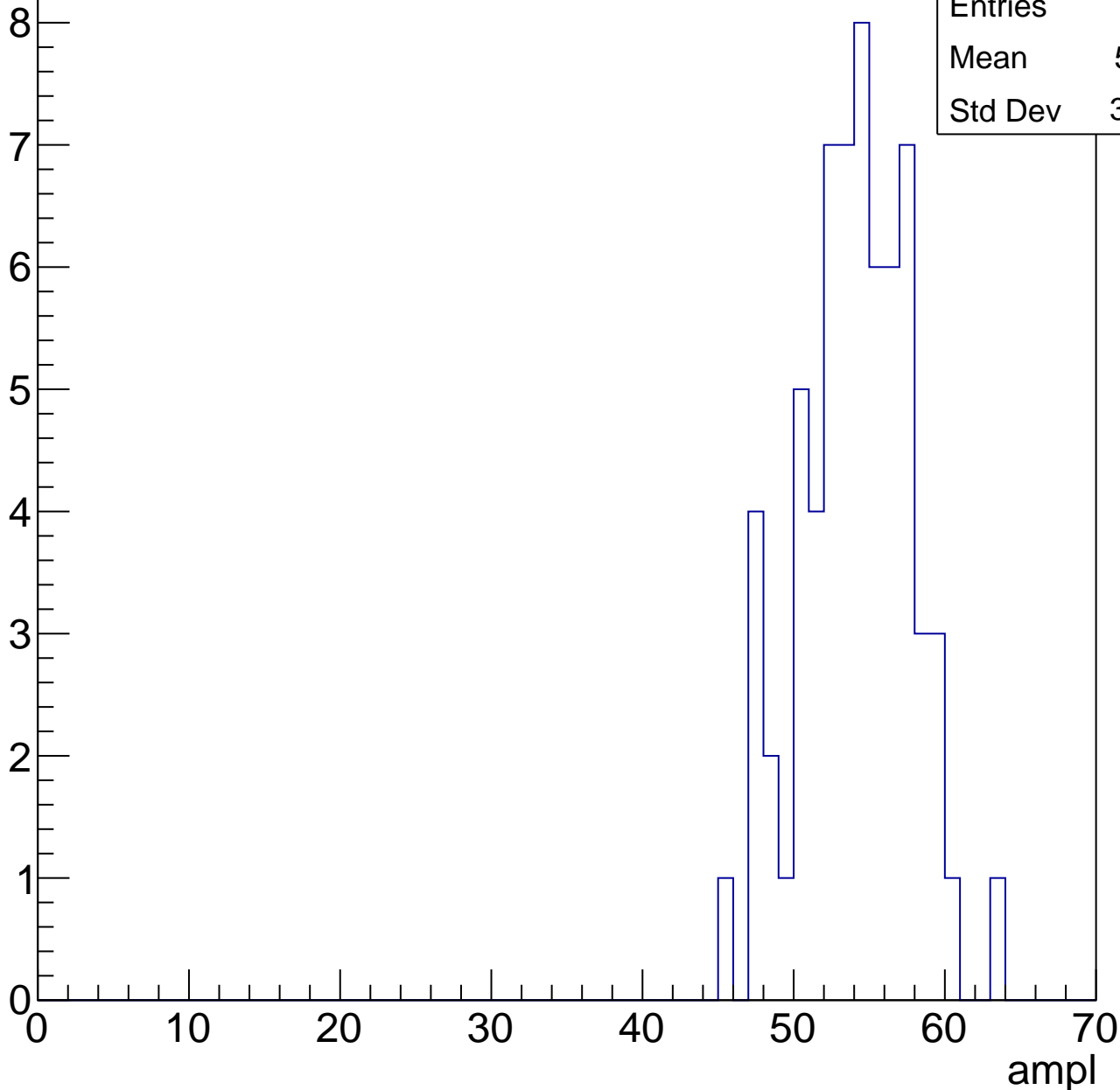


B1L103S, U21-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	53.61
Std Dev	3.584

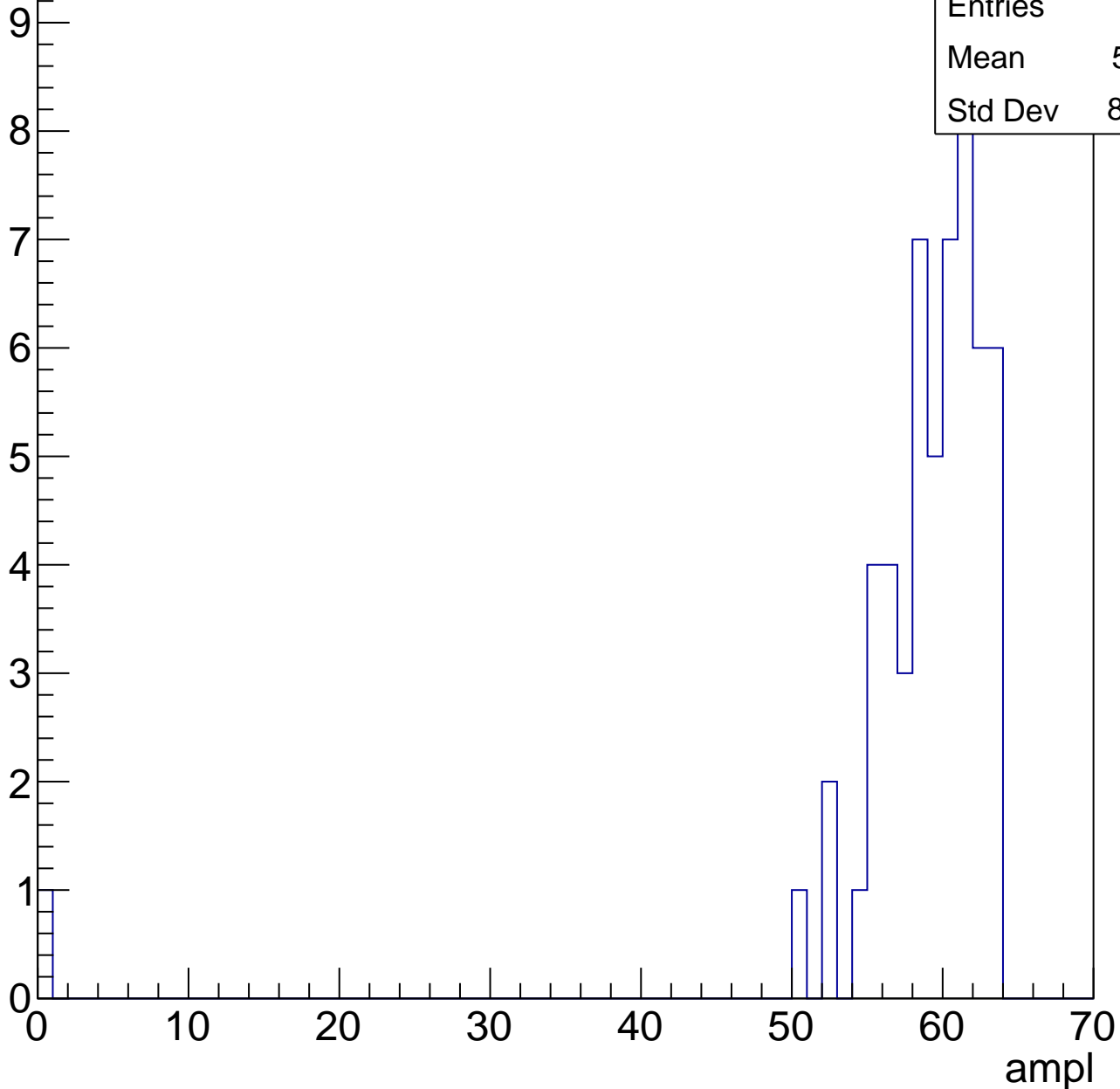


B1L103S, U21-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

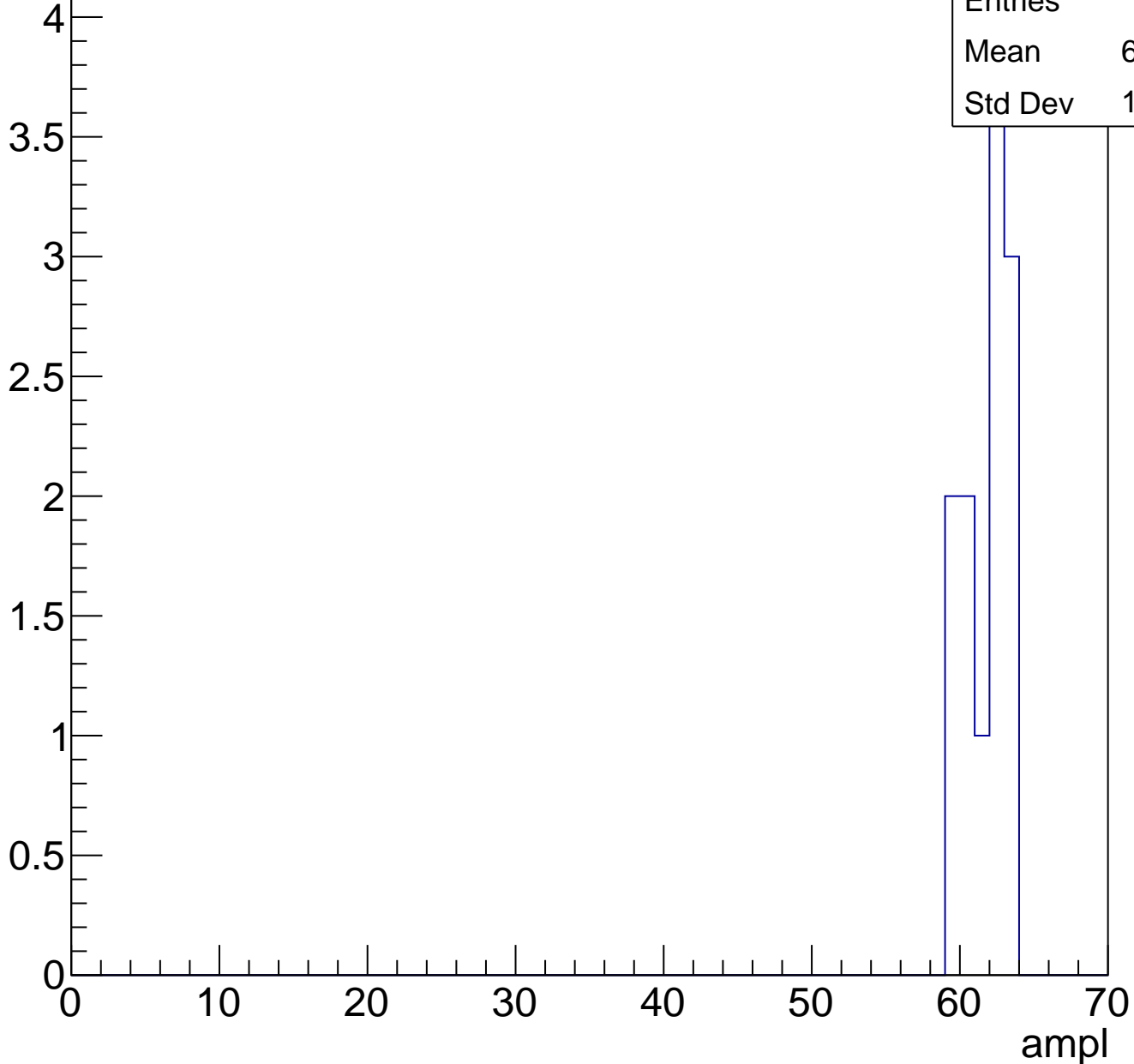
Entries	56
Mean	57.91
Std Dev	8.378



B1L103S, U21-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch75, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	24.58
Std Dev	11.17

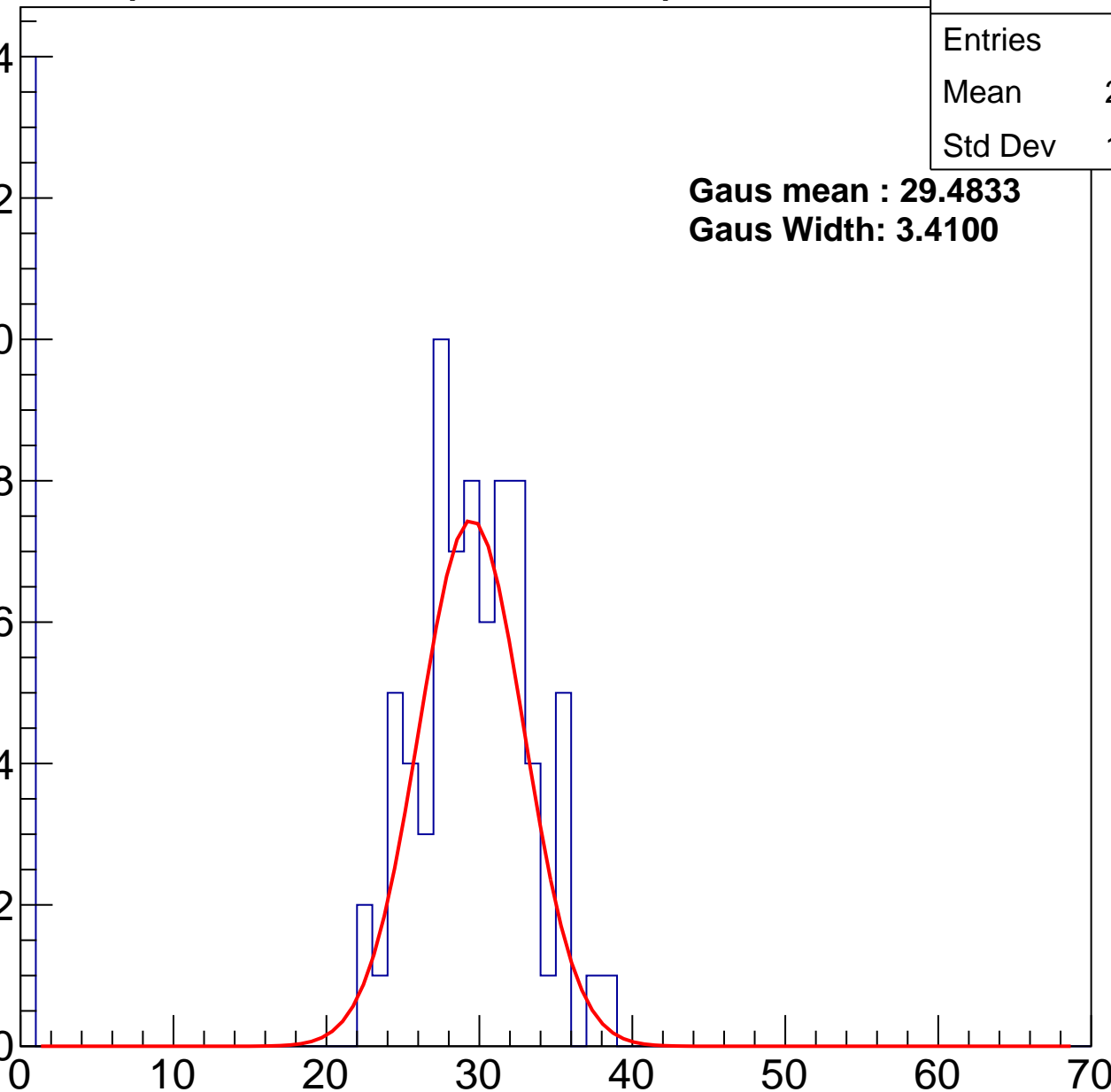
Gaus mean : 29.4833

Gaus Width: 3.4100

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch75, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	30.99
Std Dev	13.36

Gaus mean : 37.0220

Gaus Width: 3.9295

Entry

12

10

8

6

4

2

0

0

10

20

30

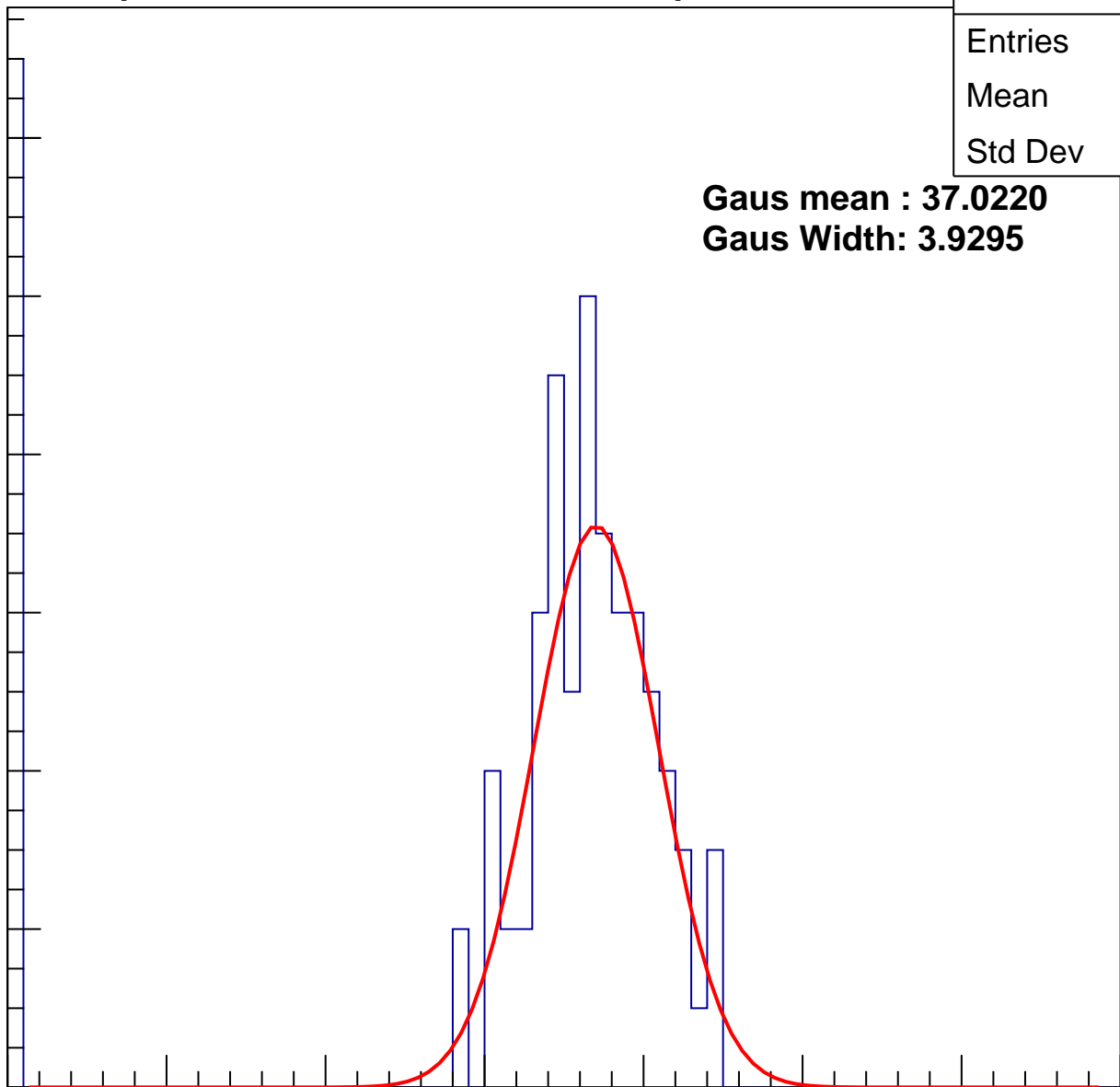
40

50

60

70

ampl



B1L103S, U21-ch75, adc2

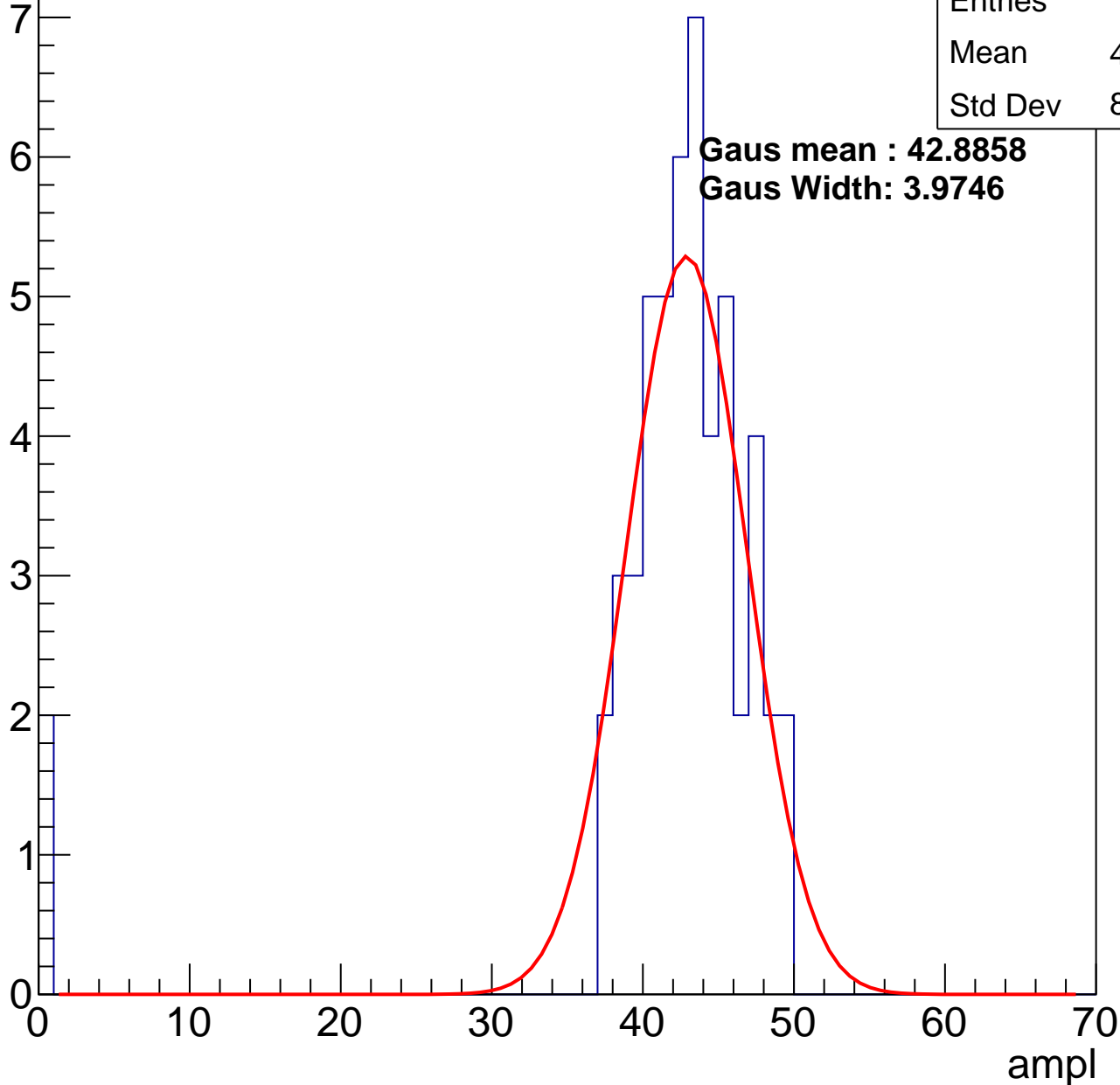
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	41.12
Std Dev	8.779

Gaus mean : 42.8858

Gaus Width: 3.9746



B1L103S, U21-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	48.96
Std Dev	3.912

Entry

10

8

6

4

2

0

0

10

20

30

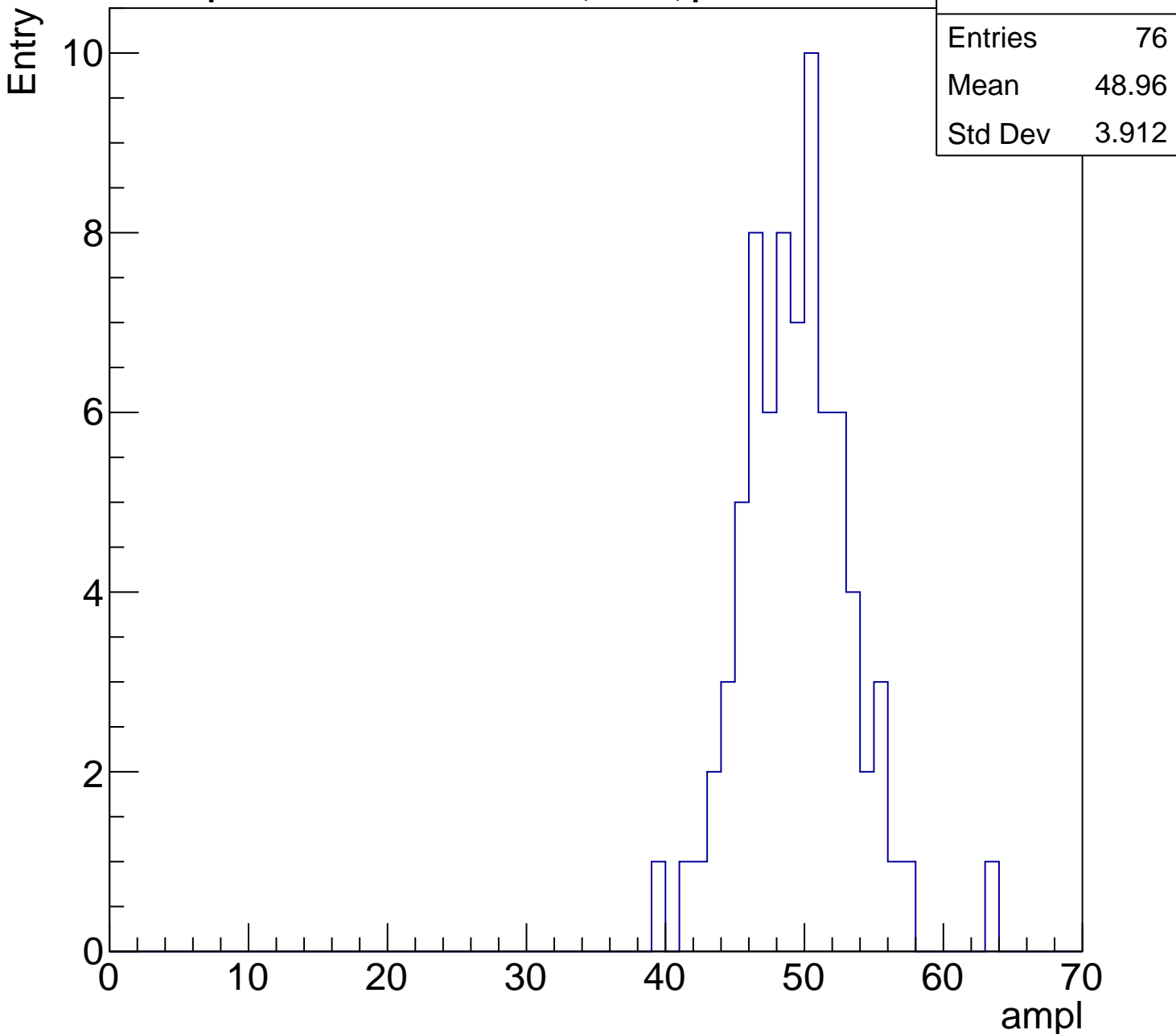
40

50

60

ampl

70

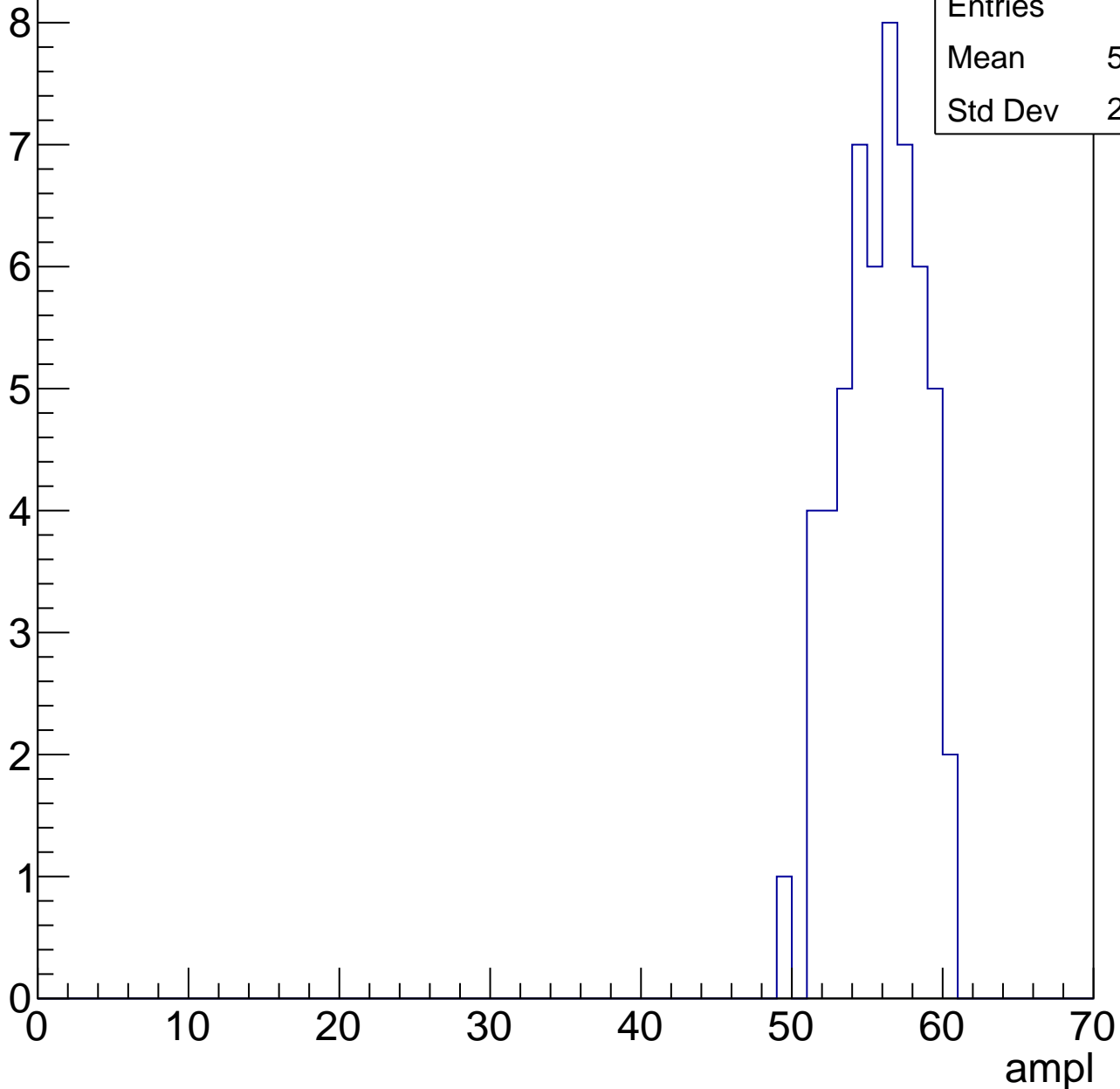


B1L103S, U21-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.35
Std Dev	2.616

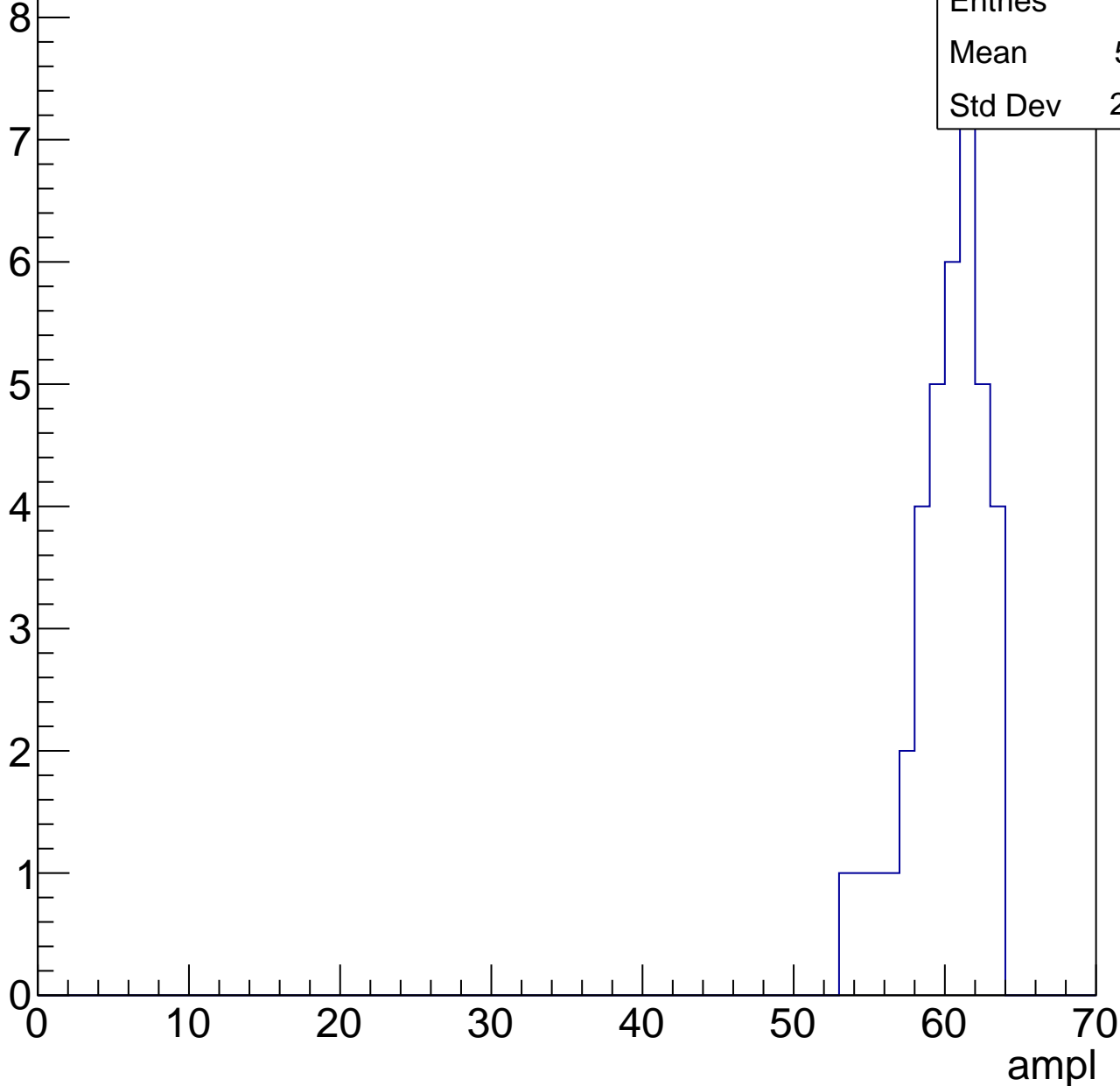


B1L103S, U21-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	59.71
Std Dev	2.438

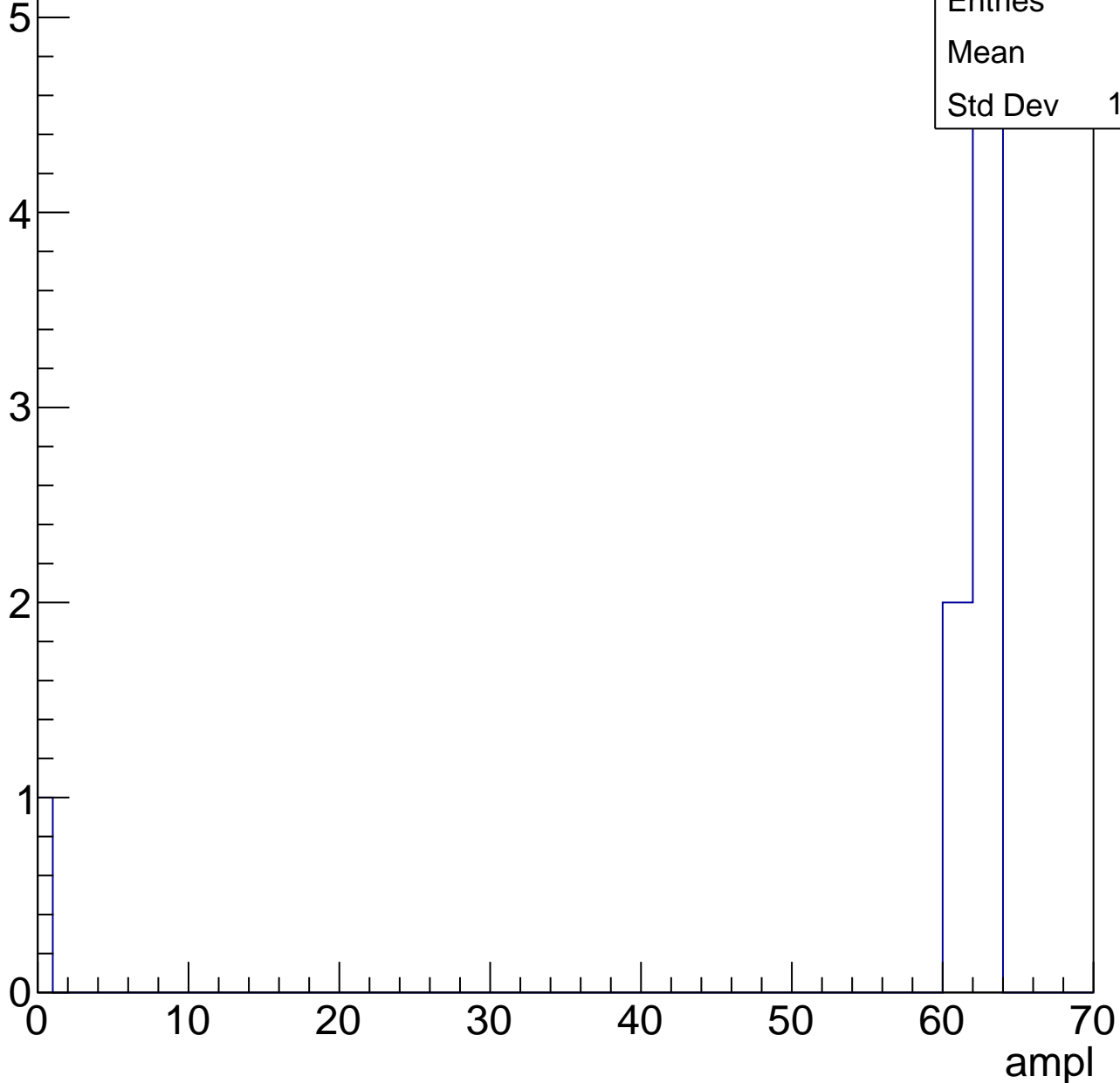


B1L103S, U21-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.8
Std Dev	15.48



B1L103S, U21-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U21-ch76, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	22.55
Std Dev	10.72

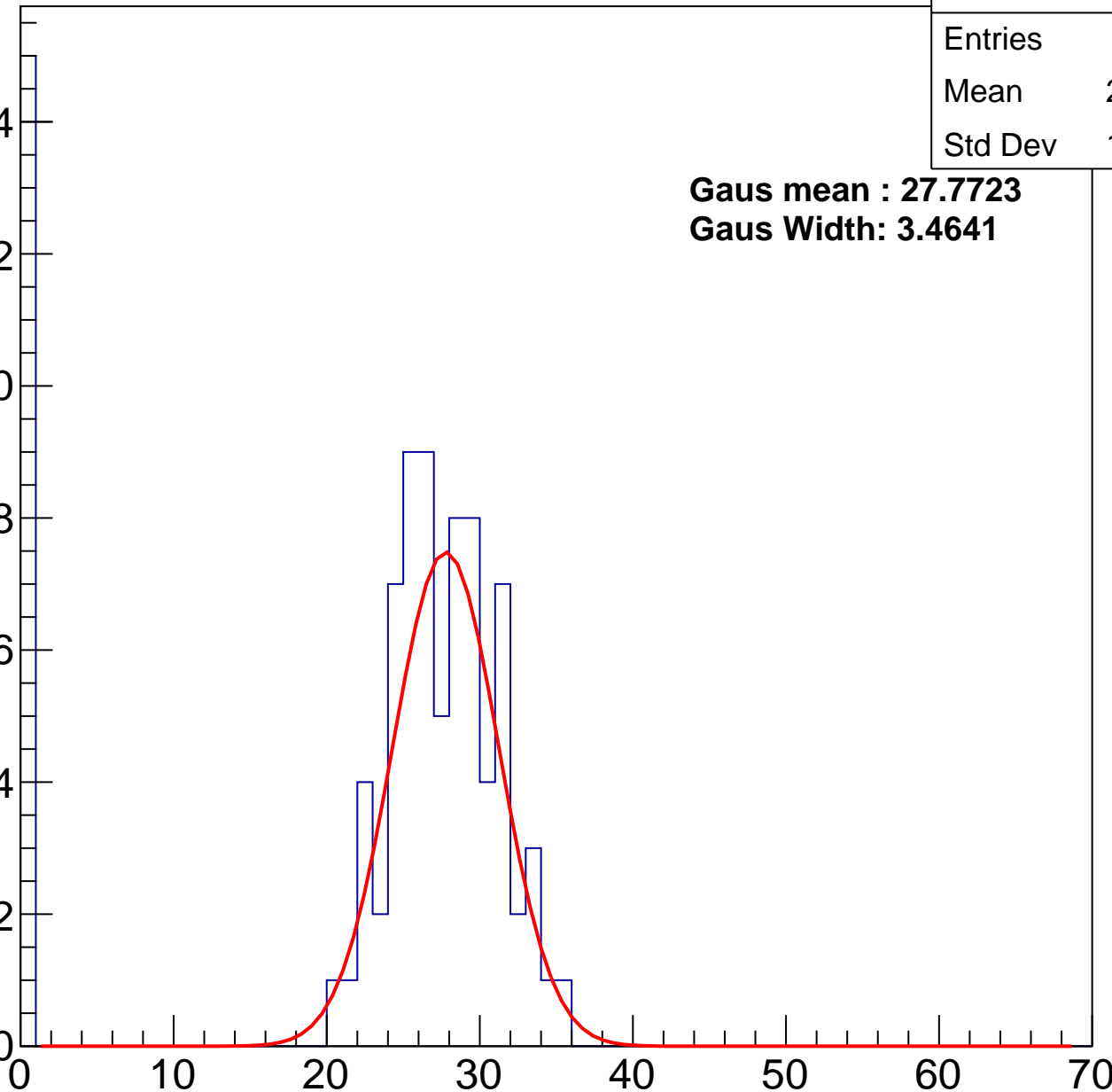
Gaus mean : 27.7723

Gaus Width: 3.4641

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch76, adc1

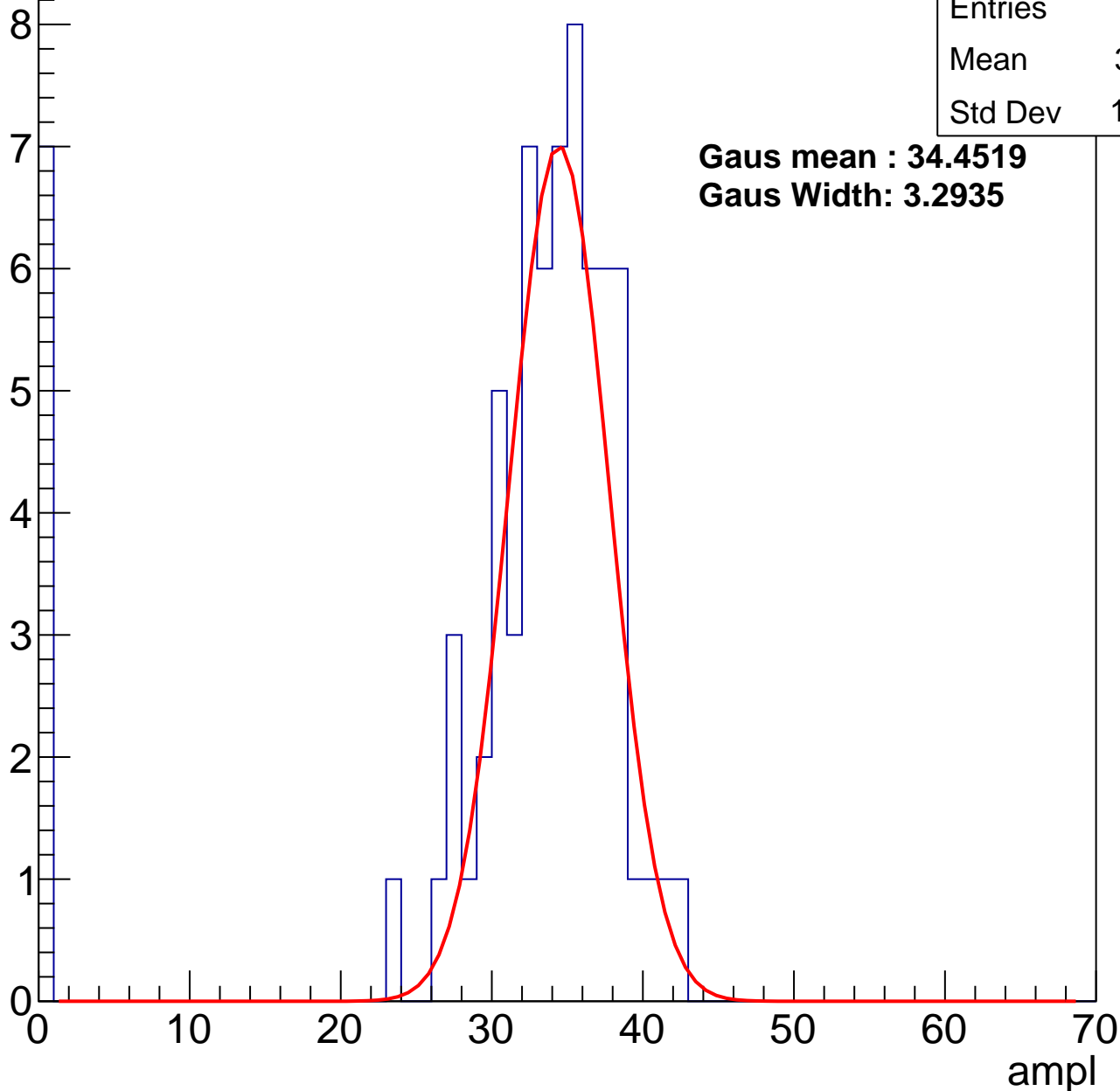
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	30.51
Std Dev	10.54

Gaus mean : 34.4519

Gaus Width: 3.2935



B1L103S, U21-ch76, adc2

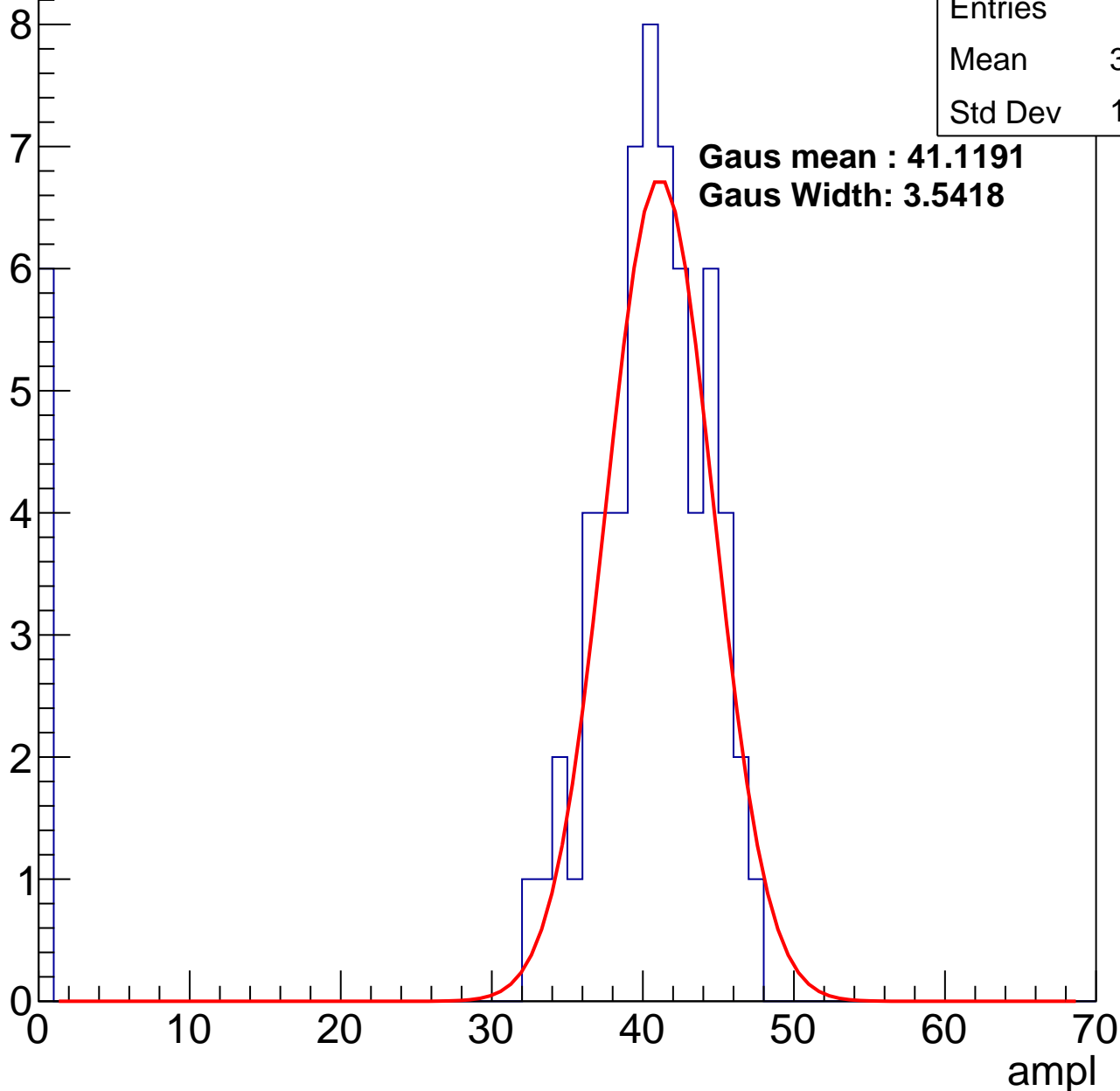
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	36.75
Std Dev	11.88

Gaus mean : 41.1191

Gaus Width: 3.5418

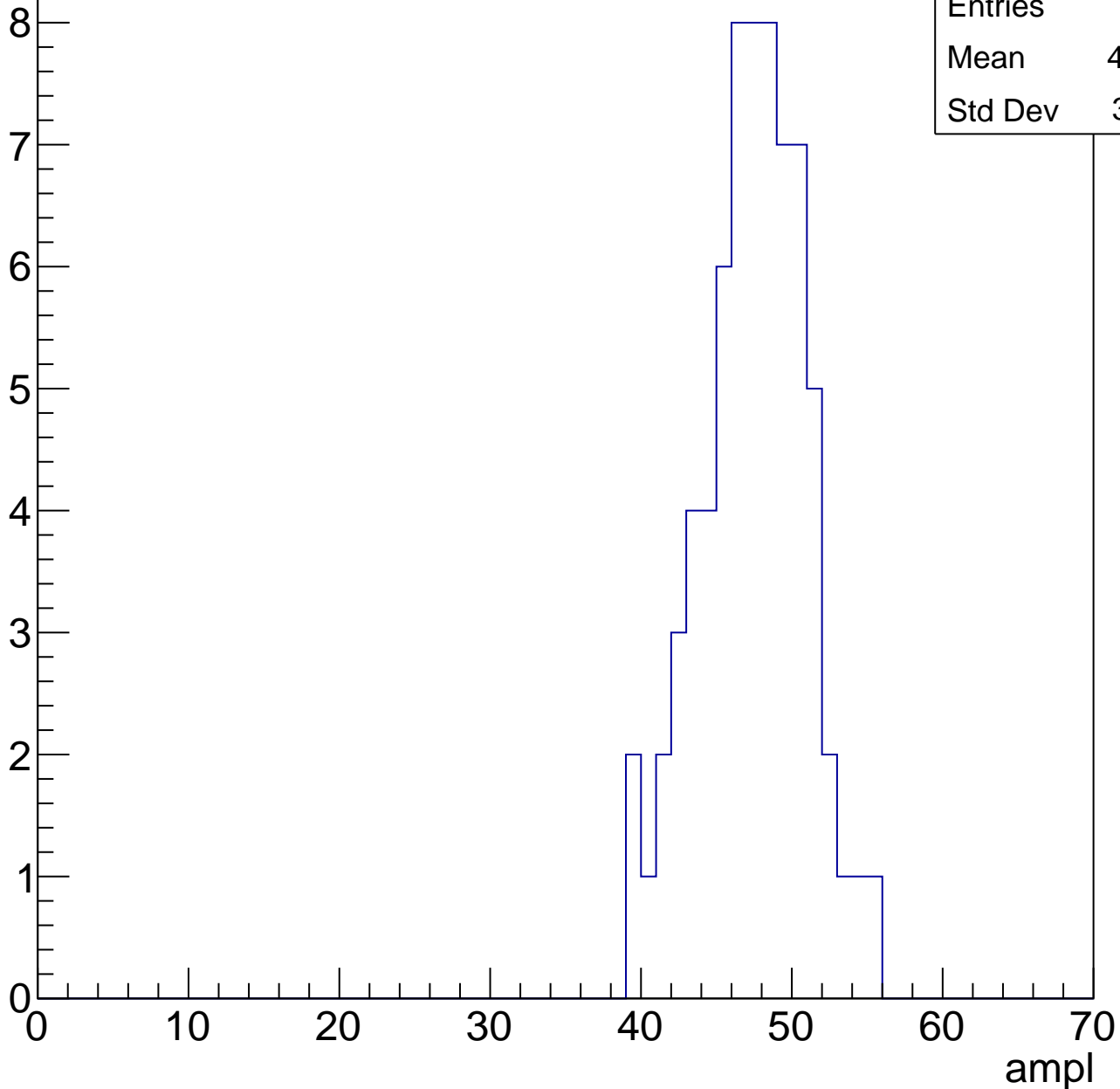


B1L103S, U21-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	46.94
Std Dev	3.451

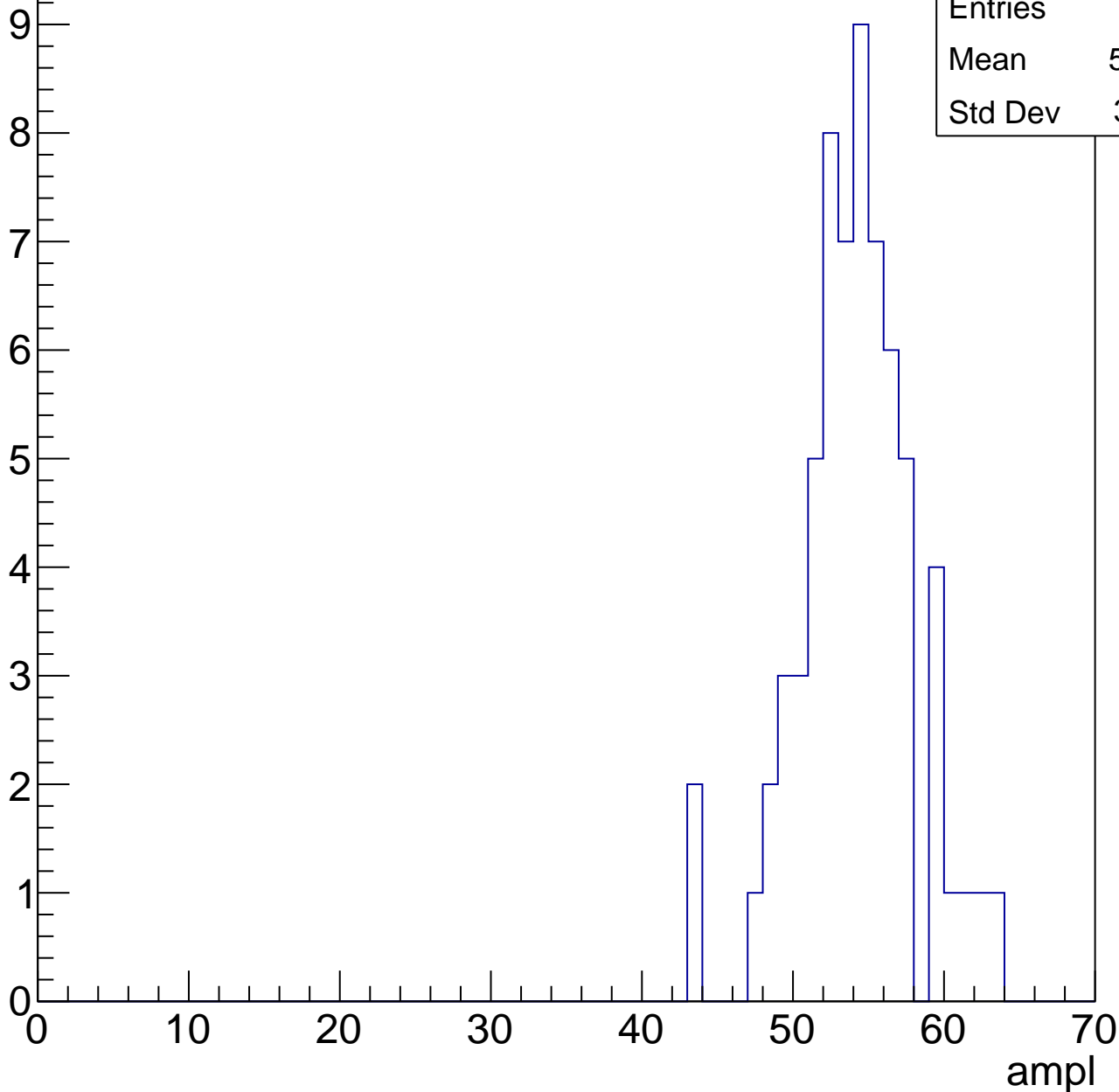


B1L103S, U21-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	53.67
Std Dev	3.831

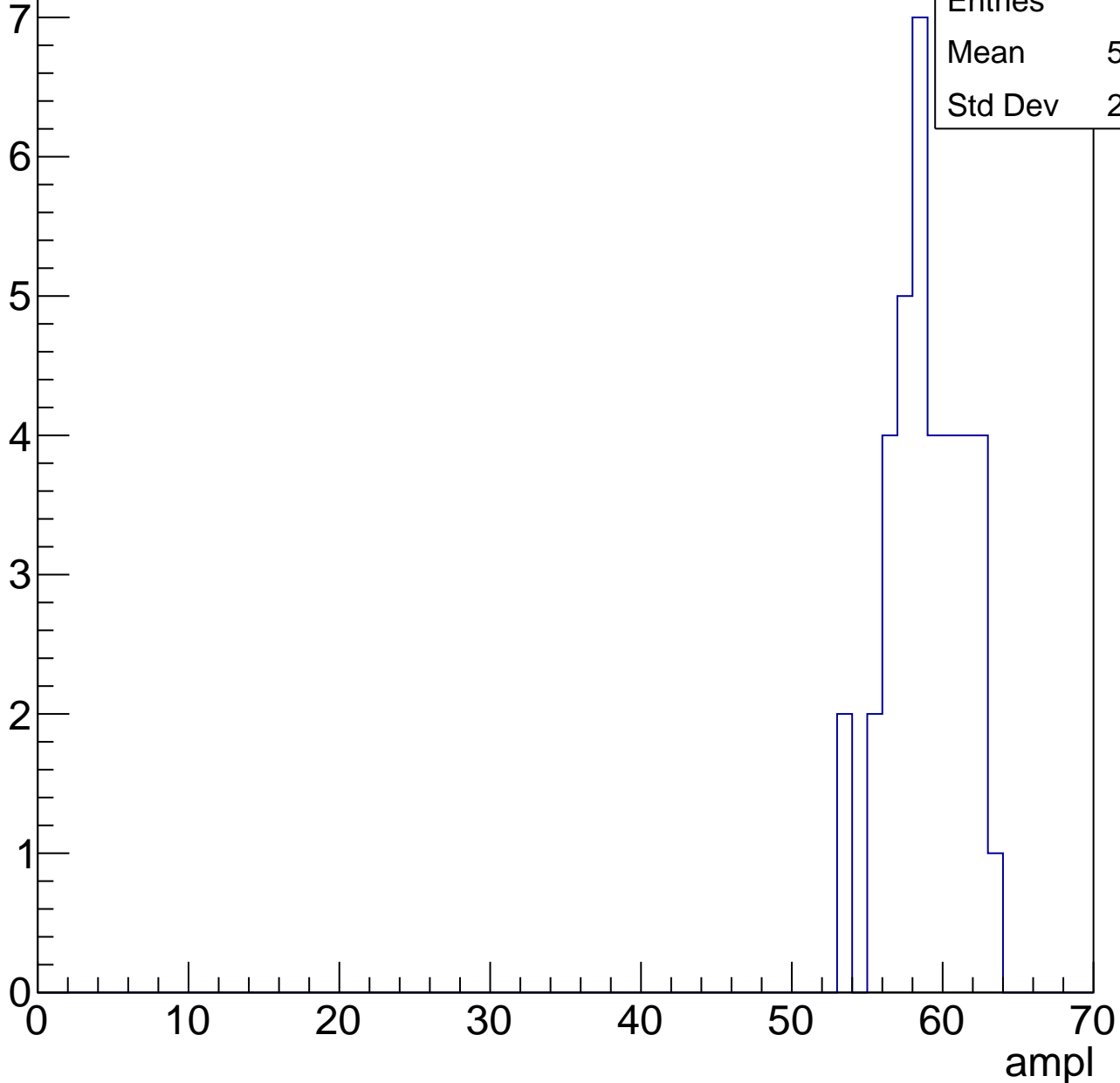


B1L103S, U21-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	58.43
Std Dev	2.477

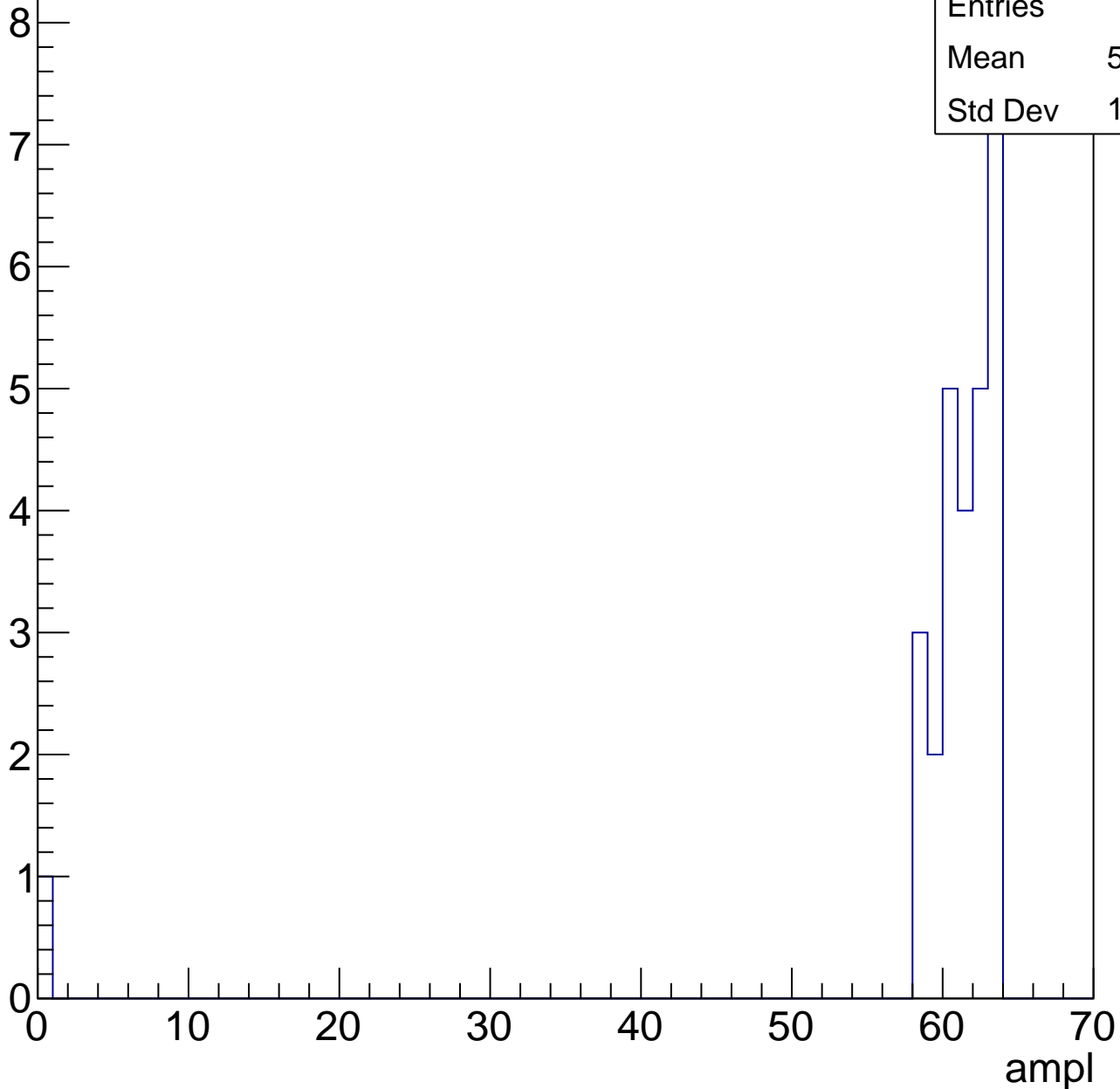


B1L103S, U21-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.93
Std Dev	11.46

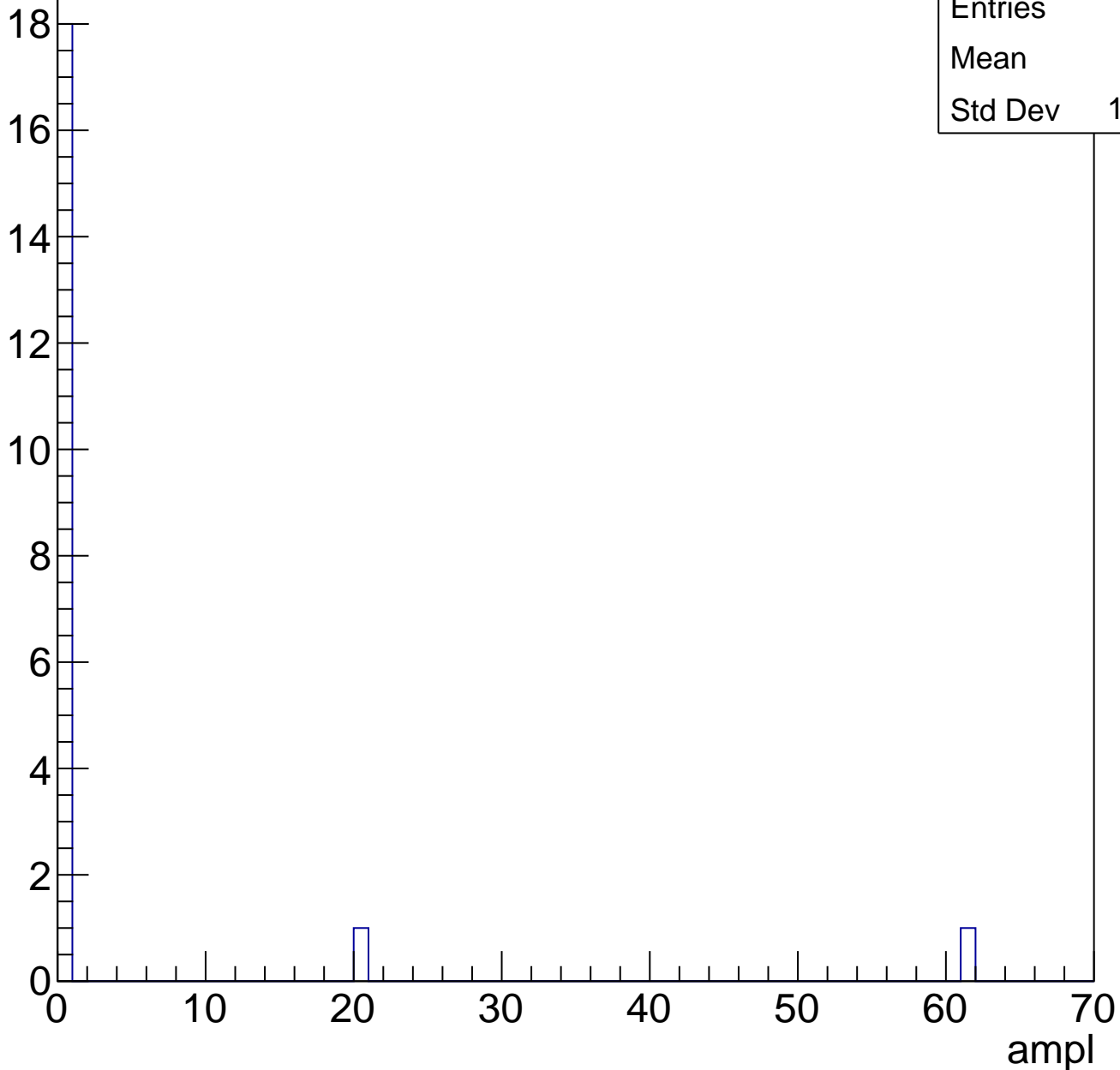


B1L103S, U21-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.05
Std Dev	13.77

Entry



B1L103S, U21-ch77, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	20.1
Std Dev	13.74

Gaus mean : 29.9874

Gaus Width: 3.4428

Entry

25

20

15

10

5

0

0

10

20

30

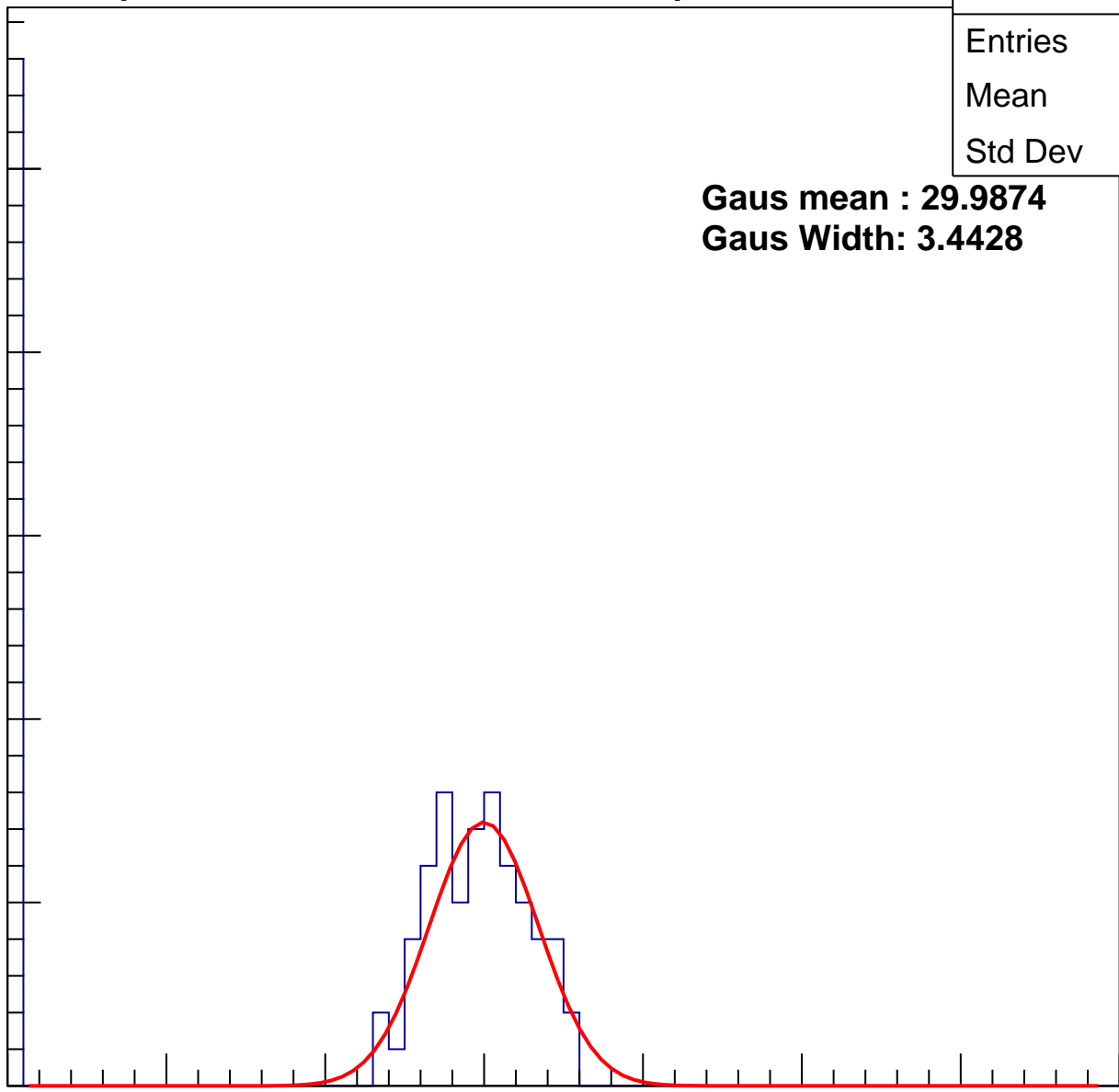
40

50

60

70

ampl



B1L103S, U21-ch77, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	33.44
Std Dev	8.814

Gaus mean : 36.0842

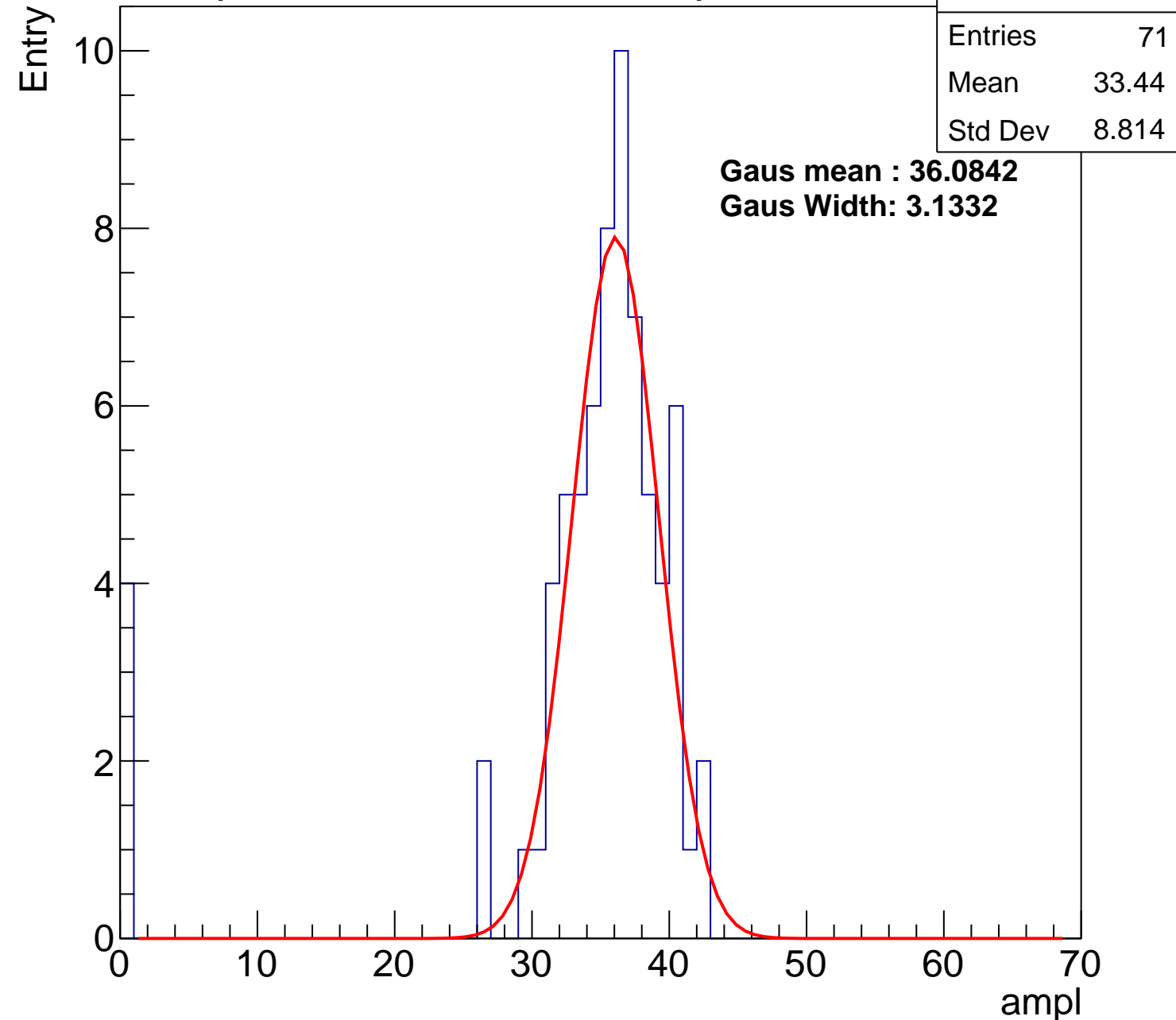
Gaus Width: 3.1332

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch77, adc2

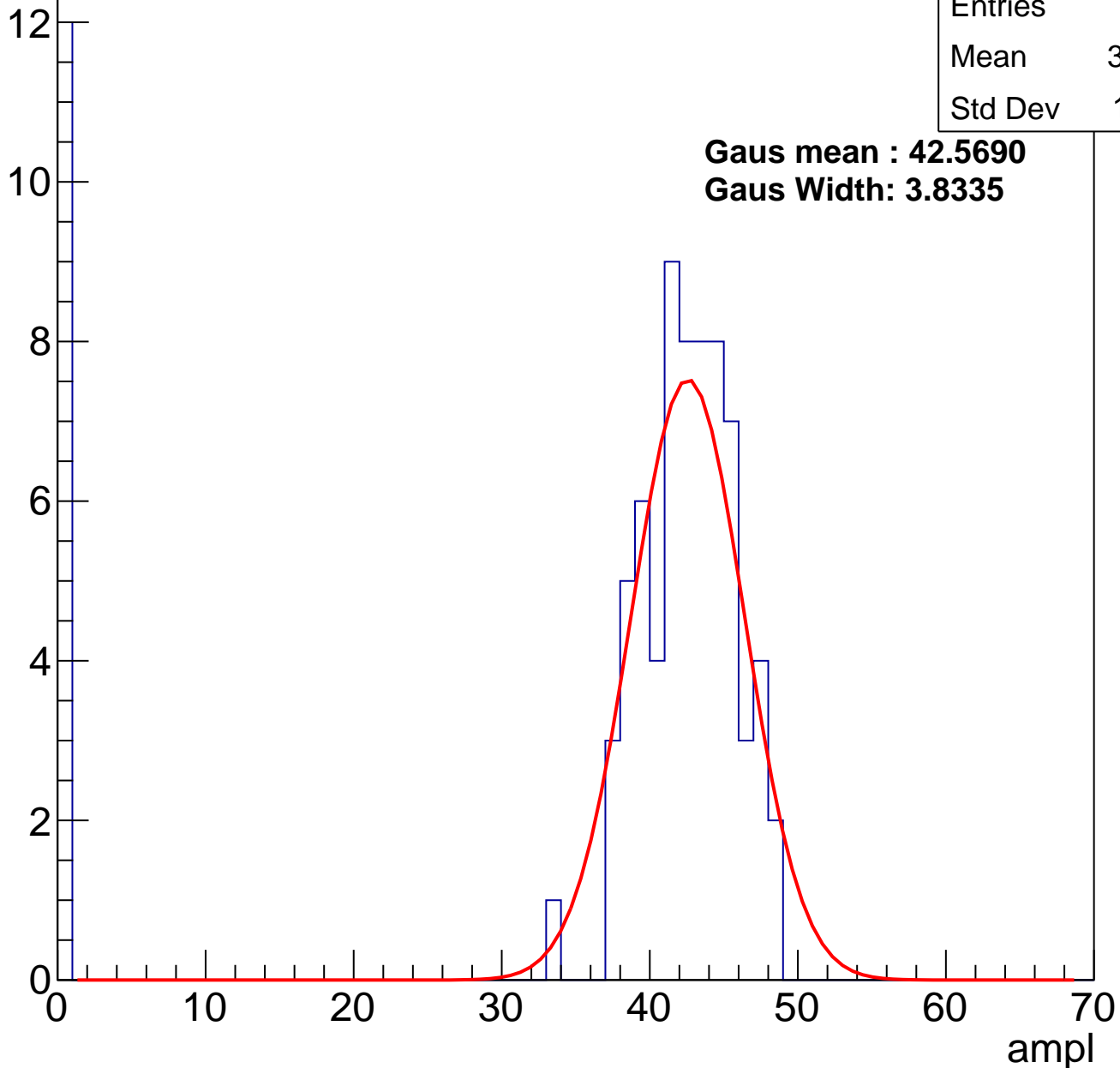
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	35.83
Std Dev	15.31

Gaus mean : 42.5690

Gaus Width: 3.8335

Entry

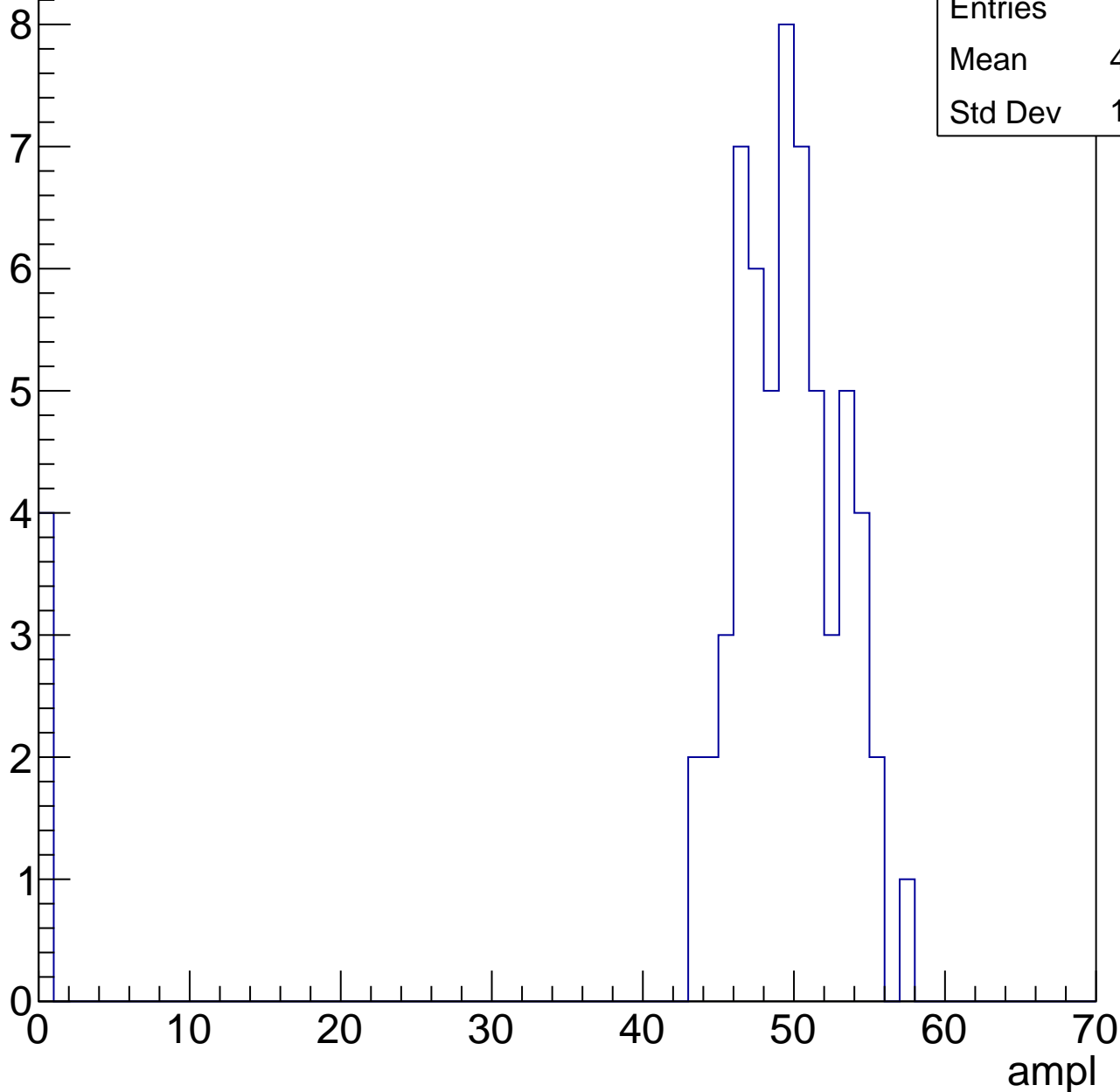


B1L103S, U21-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	46.16
Std Dev	12.32

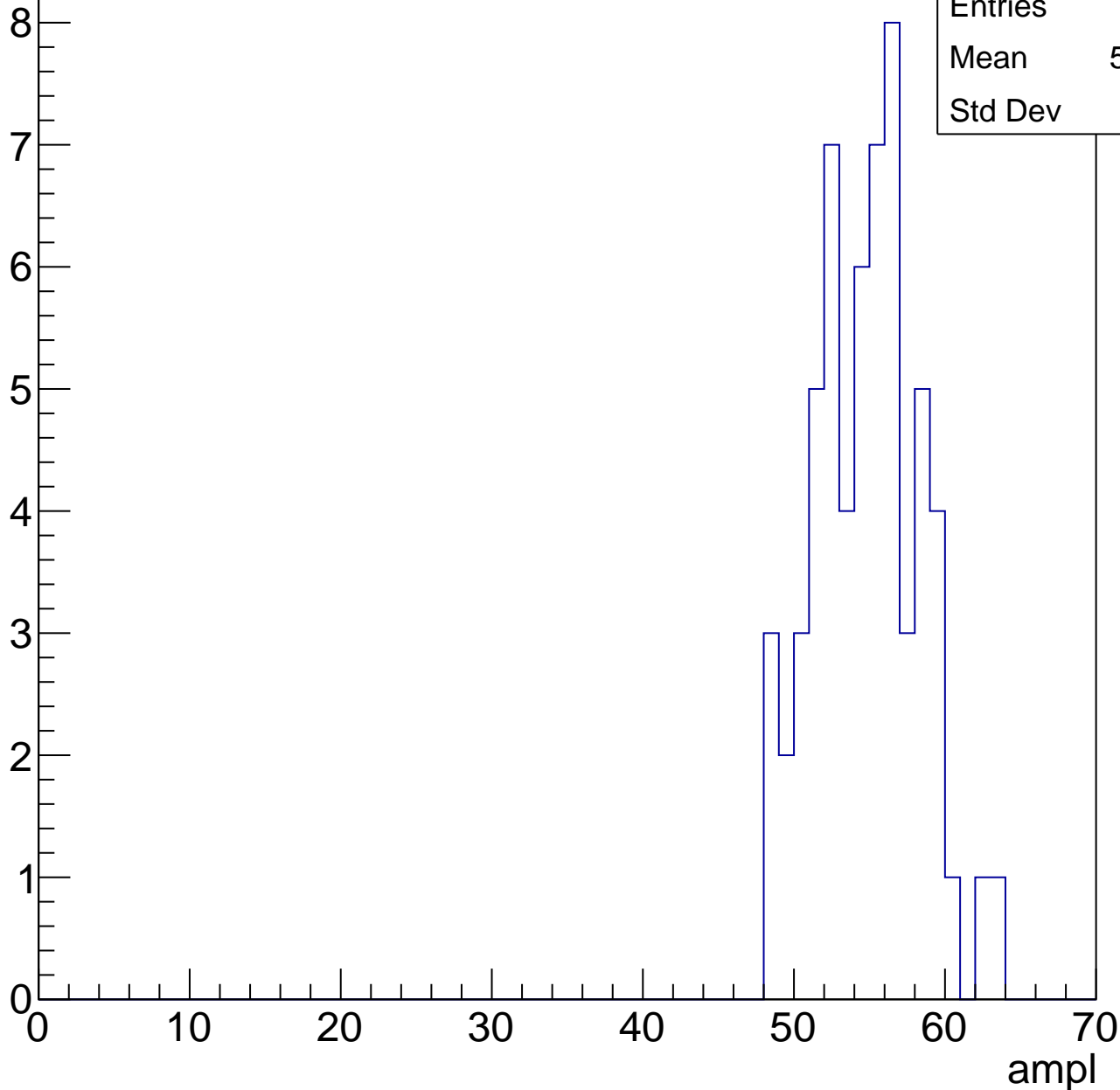


B1L103S, U21-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.37
Std Dev	3.42

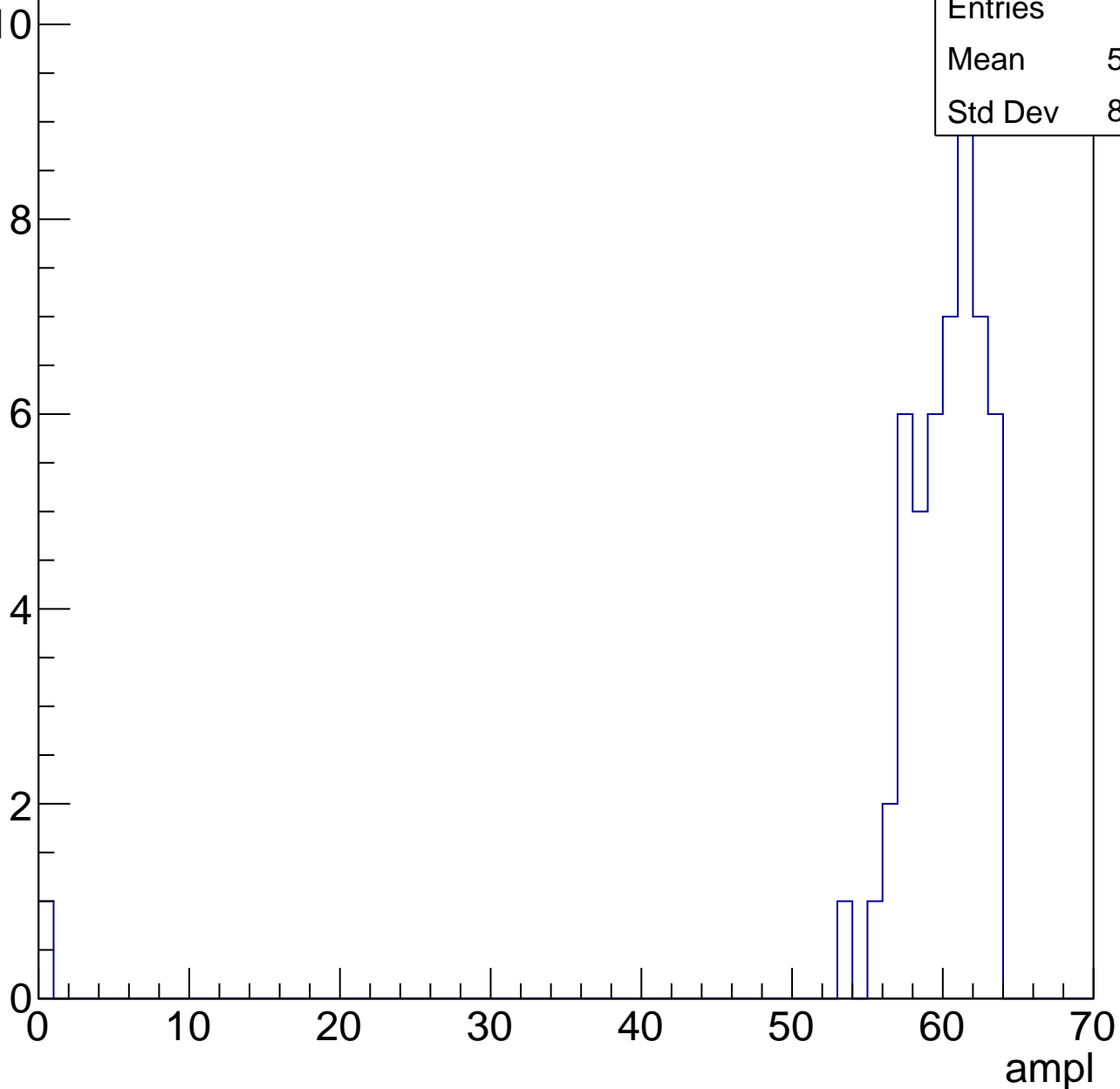


B1L103S, U21-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.62
Std Dev	8.524



B1L103S, U21-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

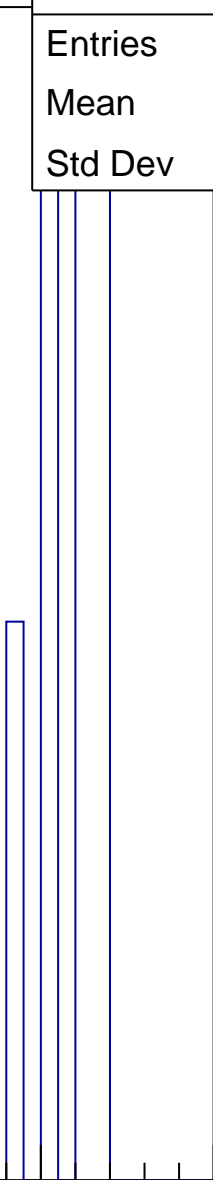
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.14
Std Dev	1.726

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

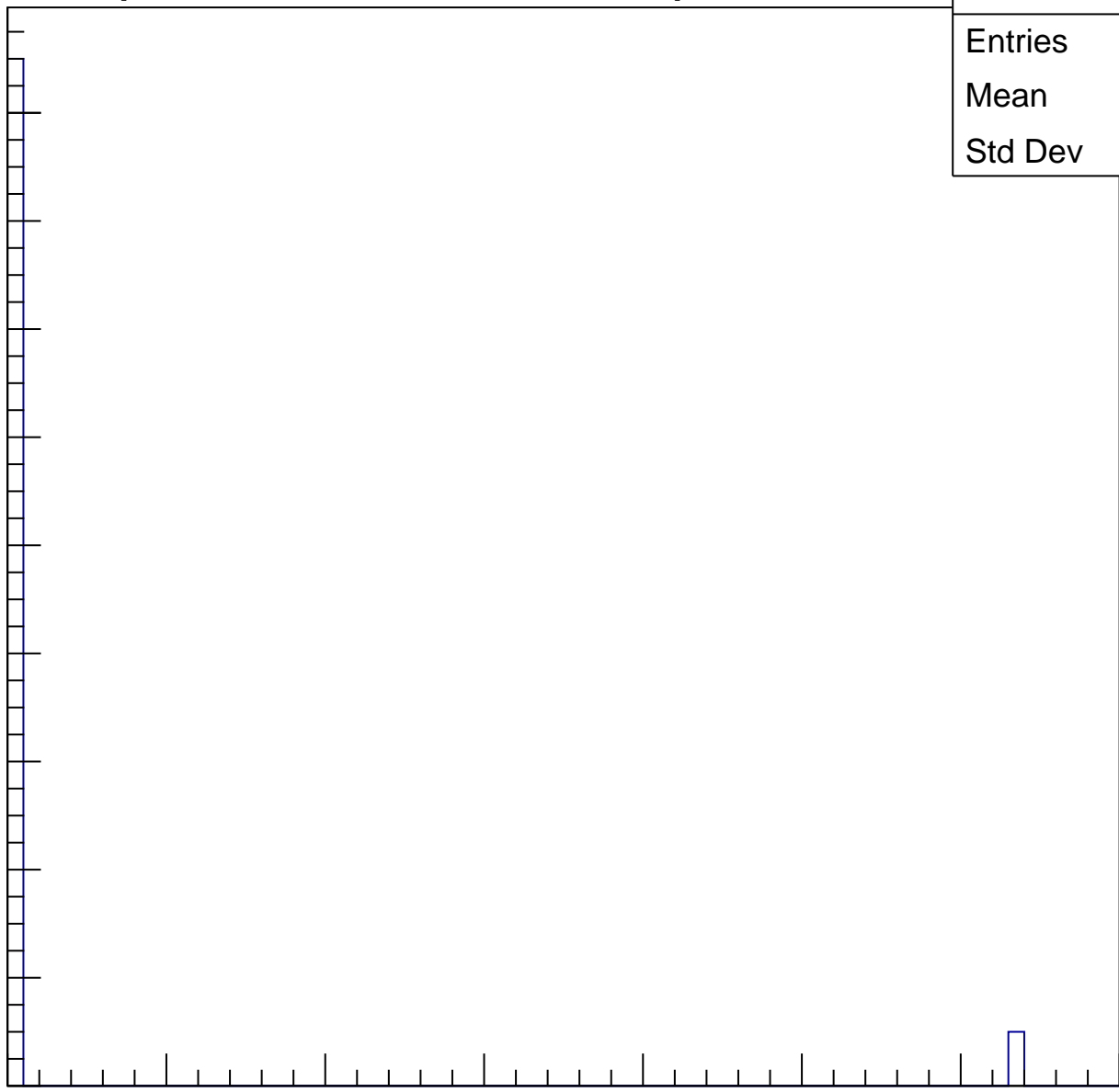
Entries	20
Mean	3.15
Std Dev	13.73

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch78, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	23.74
Std Dev	10.47

Gaus mean : 28.5026

Gaus Width: 3.5784

Entry

12

10

8

6

4

2

0

0

10

20

30

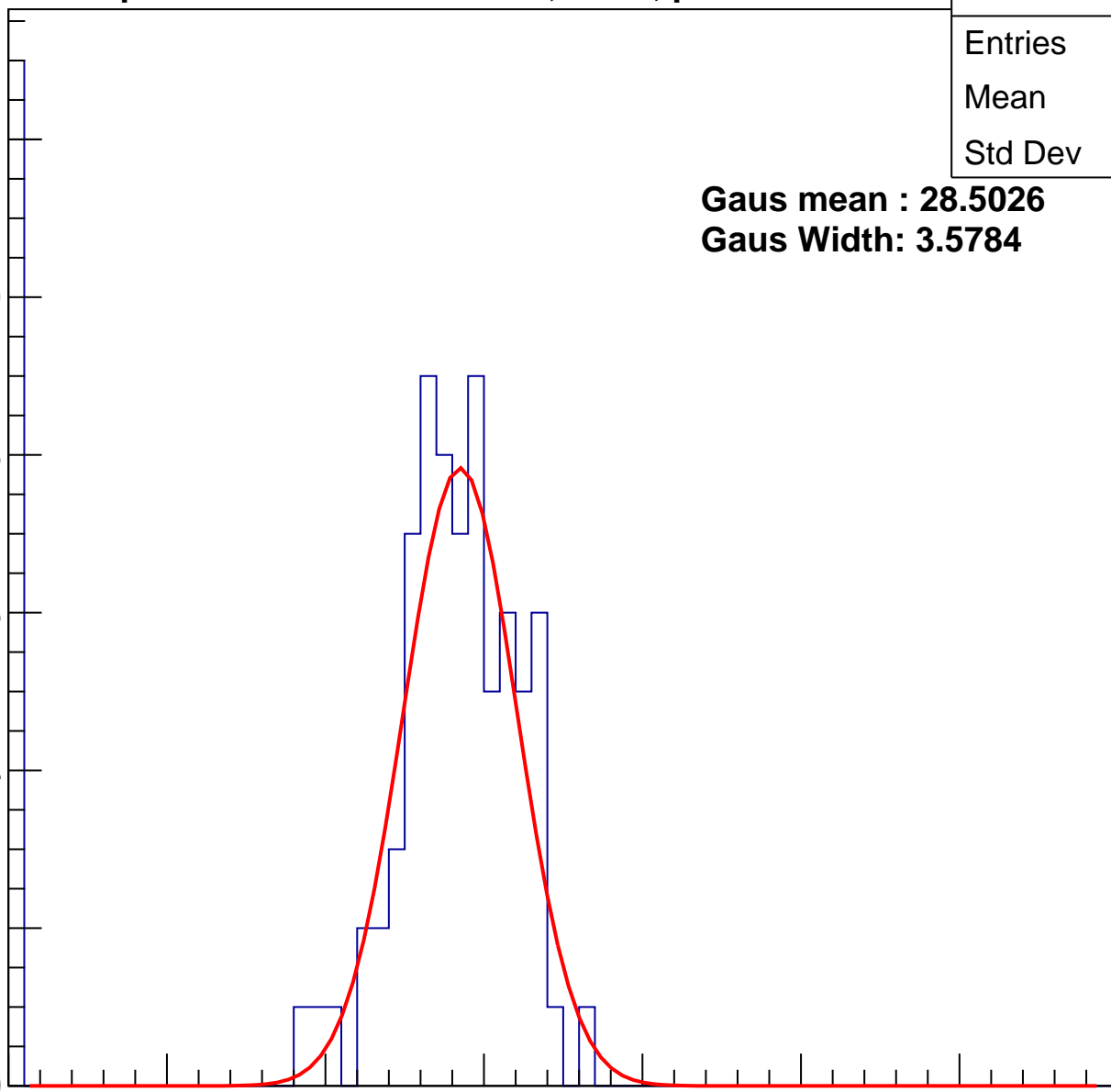
40

50

60

70

ampl



B1L103S, U21-ch78, adc1

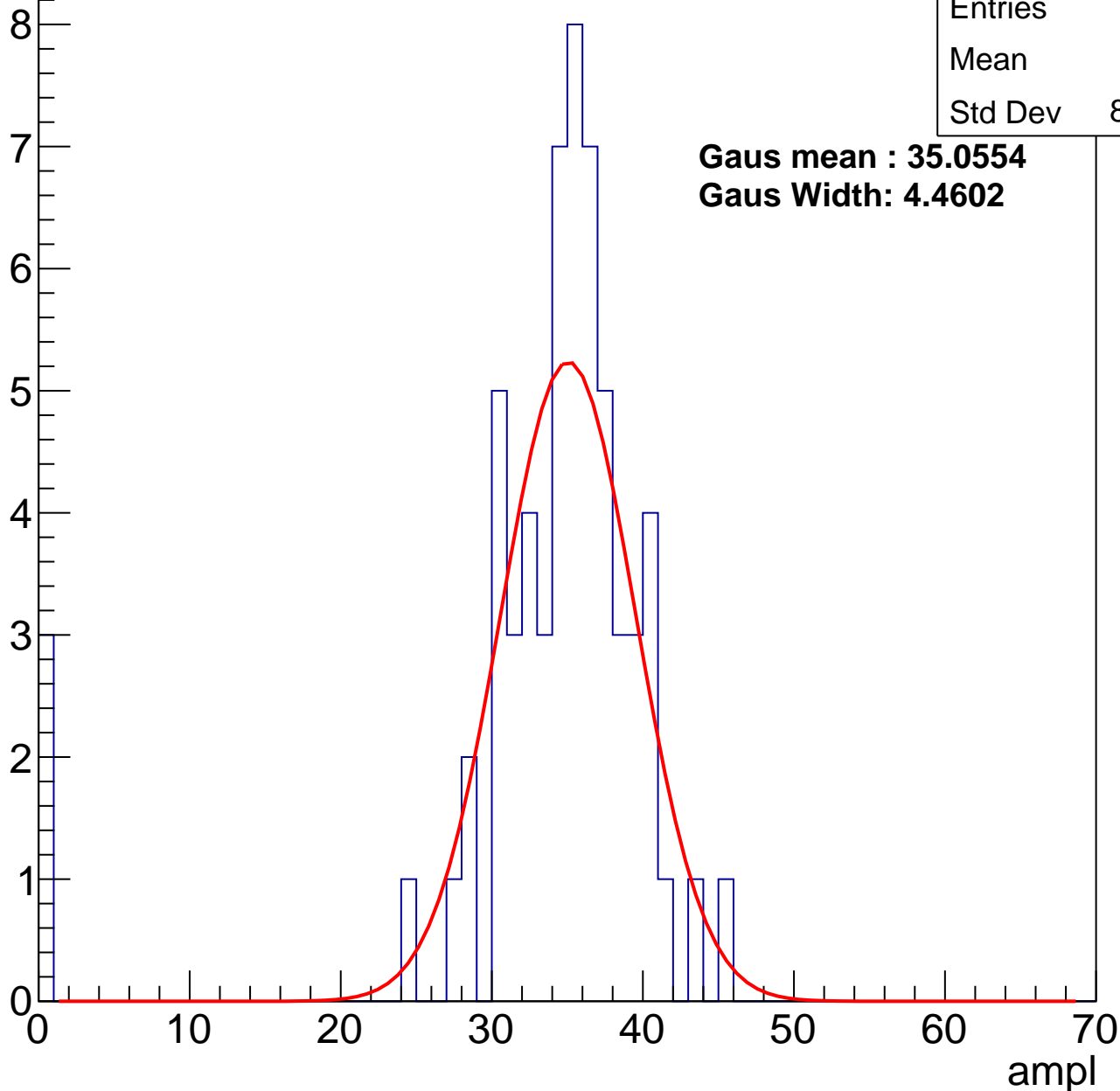
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	33.1
Std Dev	8.389

Gaus mean : 35.0554

Gaus Width: 4.4602



B1L103S, U21-ch78, adc2

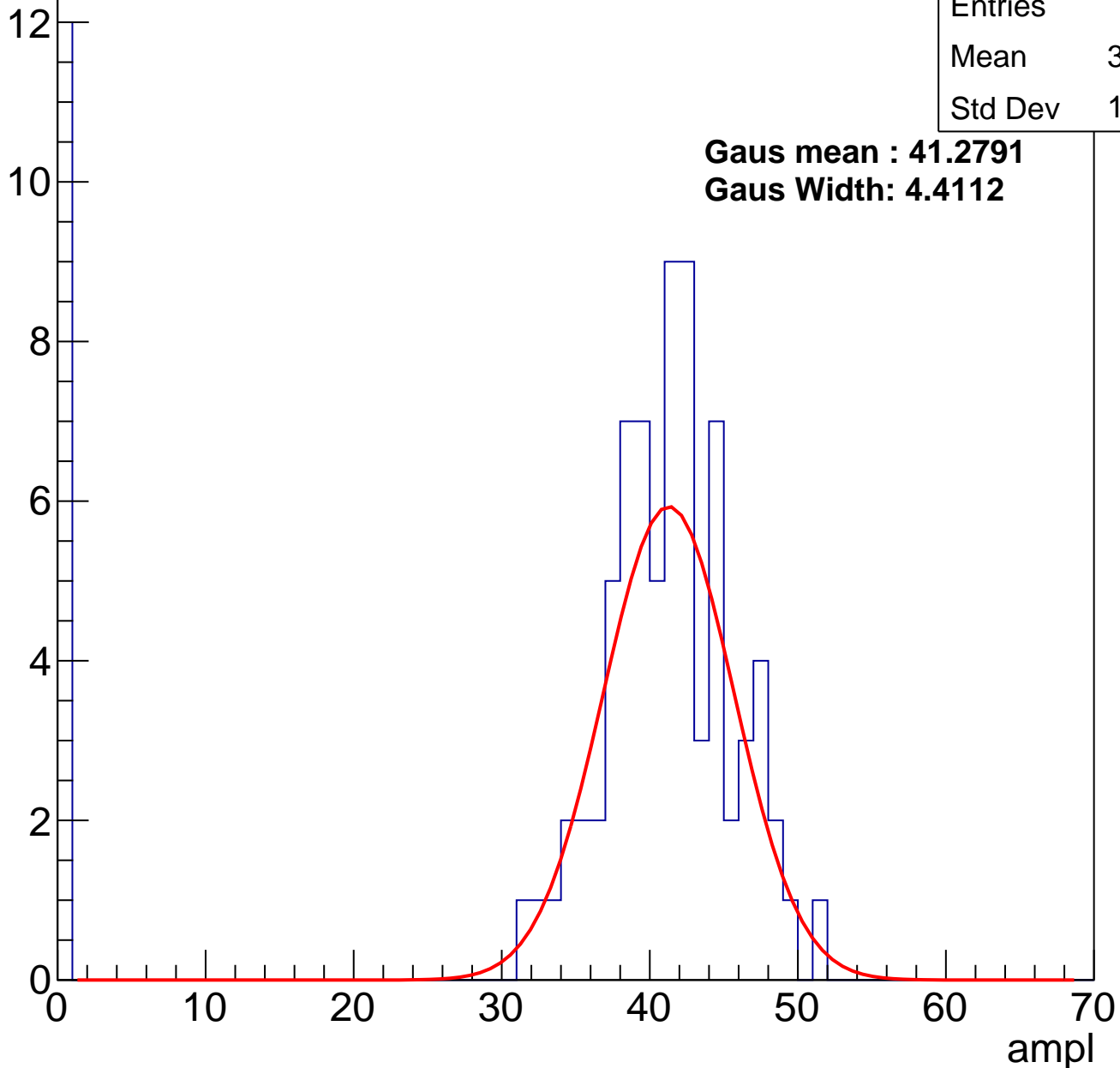
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	35.19
Std Dev	14.67

Gaus mean : 41.2791

Gaus Width: 4.4112

Entry

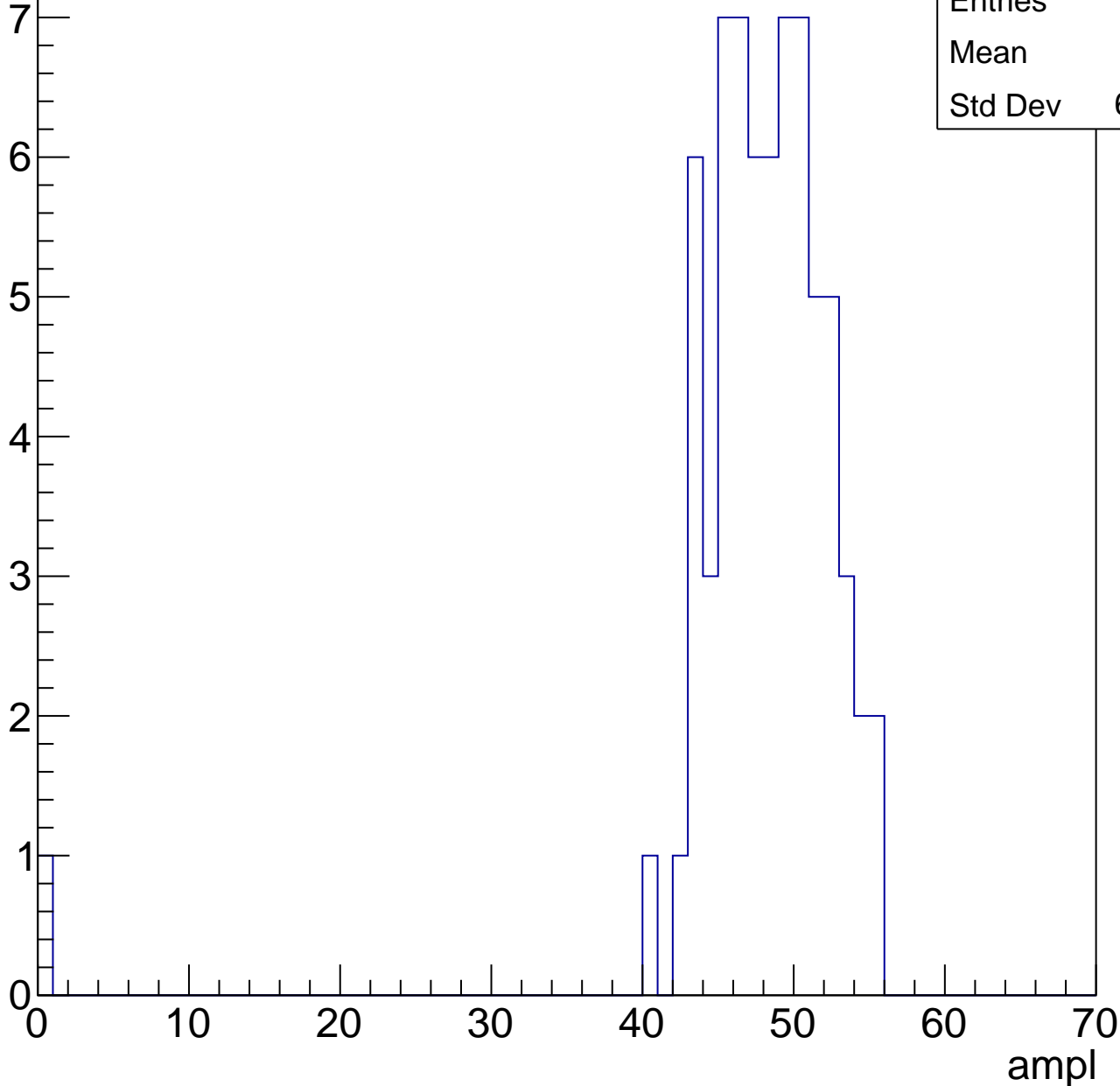


B1L103S, U21-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	47.3
Std Dev	6.671

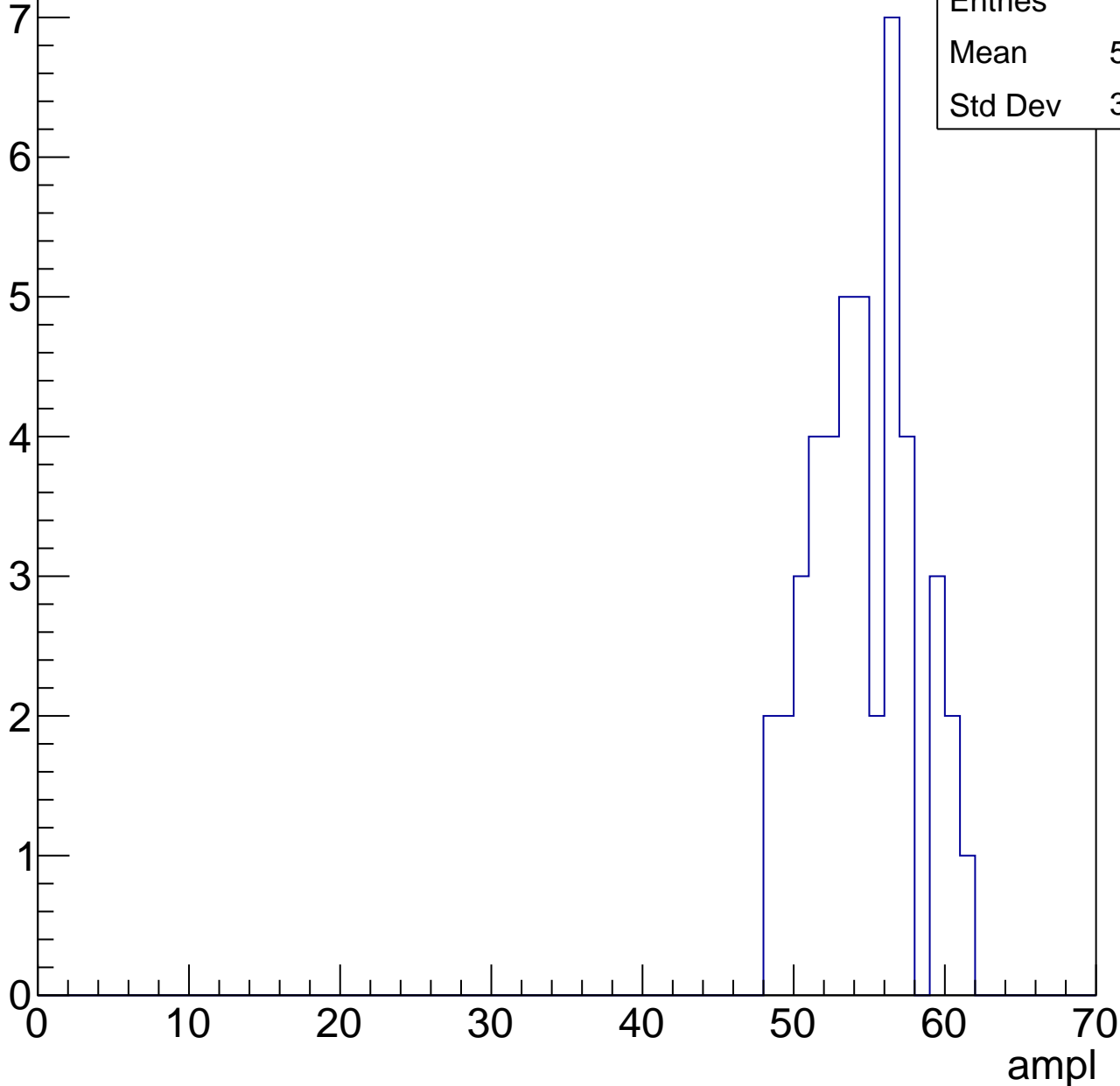


B1L103S, U21-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	54.07
Std Dev	3.333



B1L103S, U21-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	57.49
Std Dev	7.931

Entry

10

8

6

4

2

0

0

10

20

30

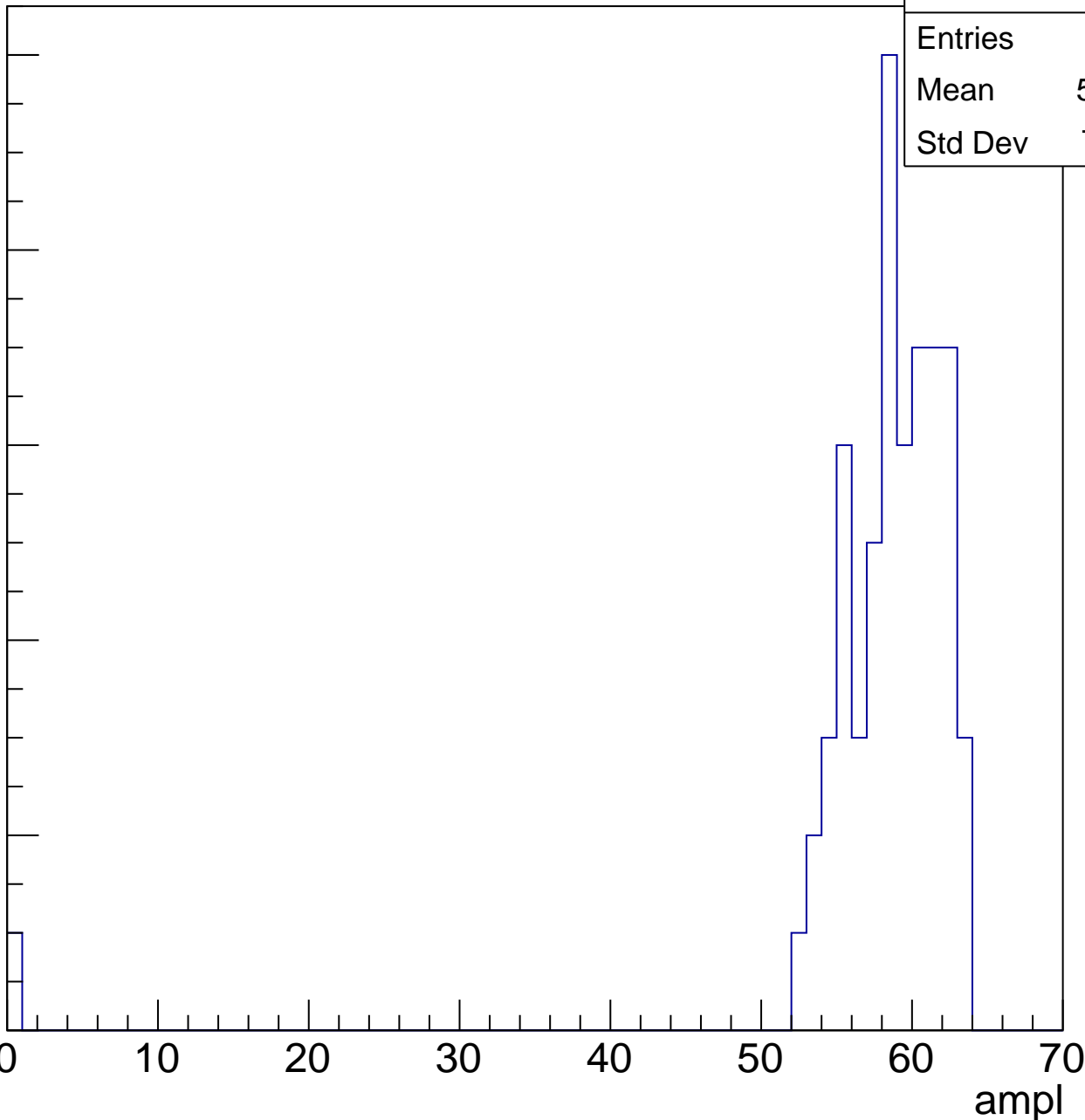
40

50

60

70

ampl

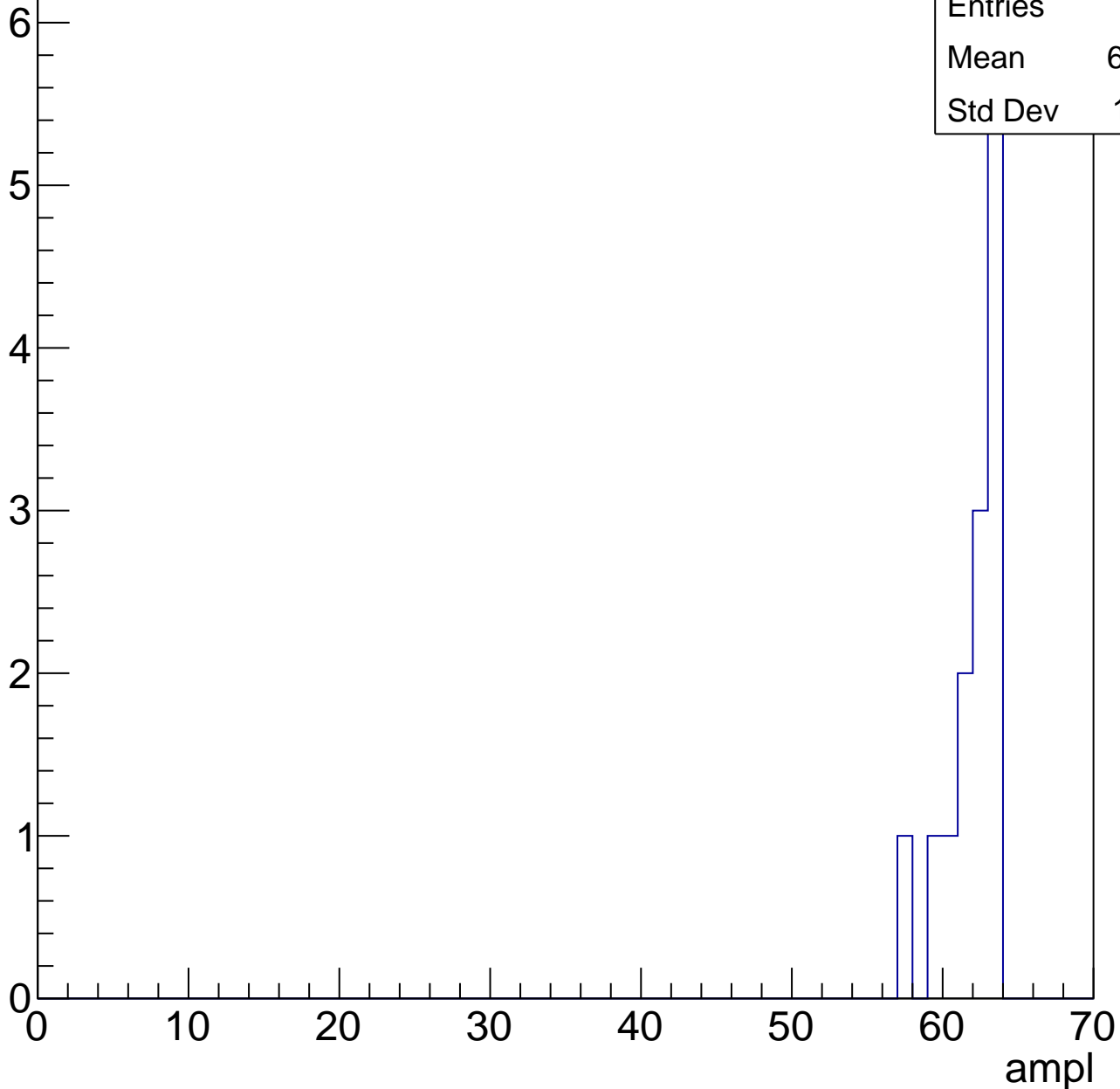


B1L103S, U21-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.57
Std Dev	1.761



B1L103S, U21-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U21-ch79, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	60
Mean	23.9
Std Dev	11.76

Gaus mean : 29.7644

Gaus Width: 3.5586

Entry

10

8

6

4

2

0

0

10

20

30

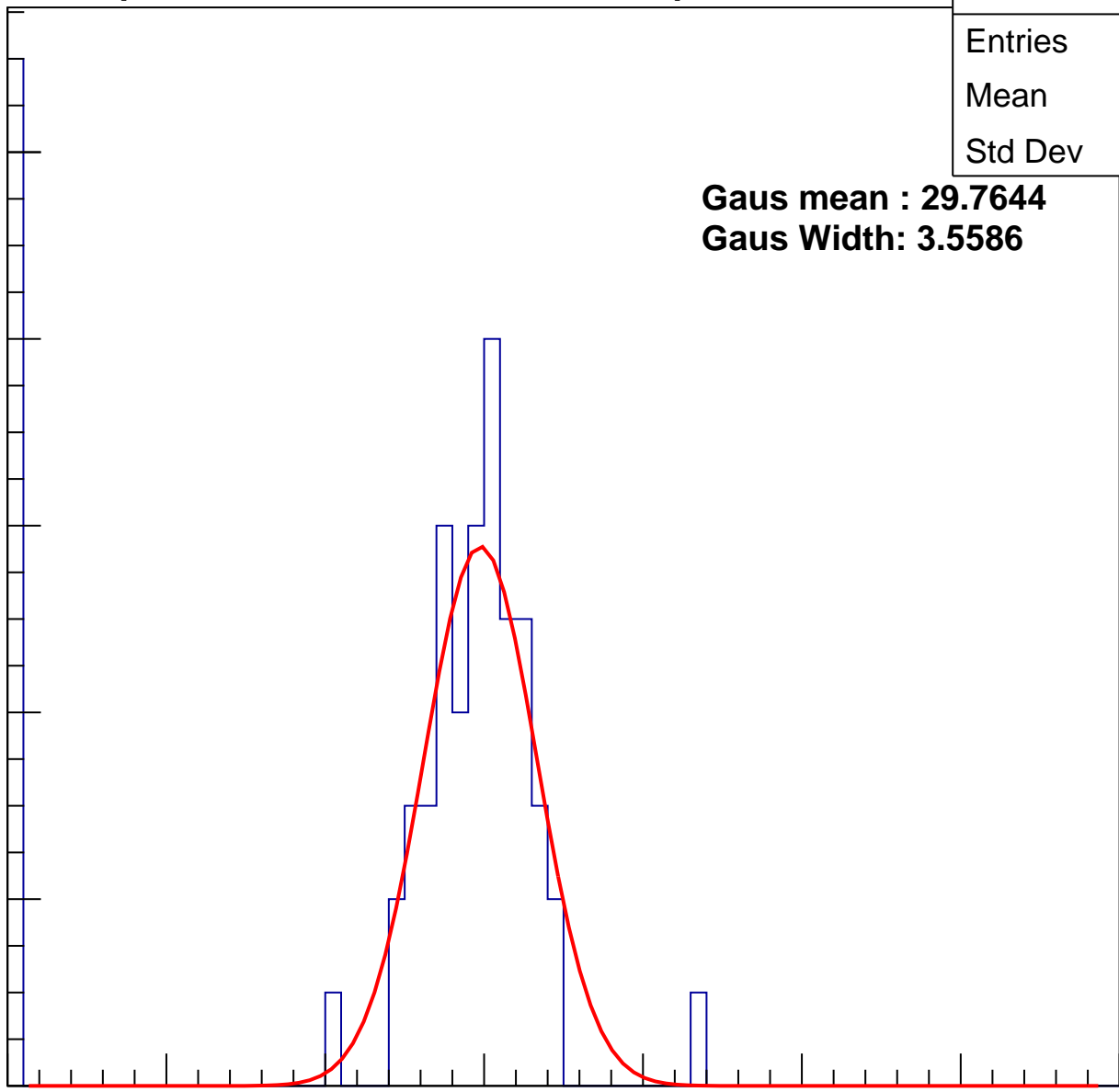
40

50

60

70

ampl



B1L103S, U21-ch79, adc1

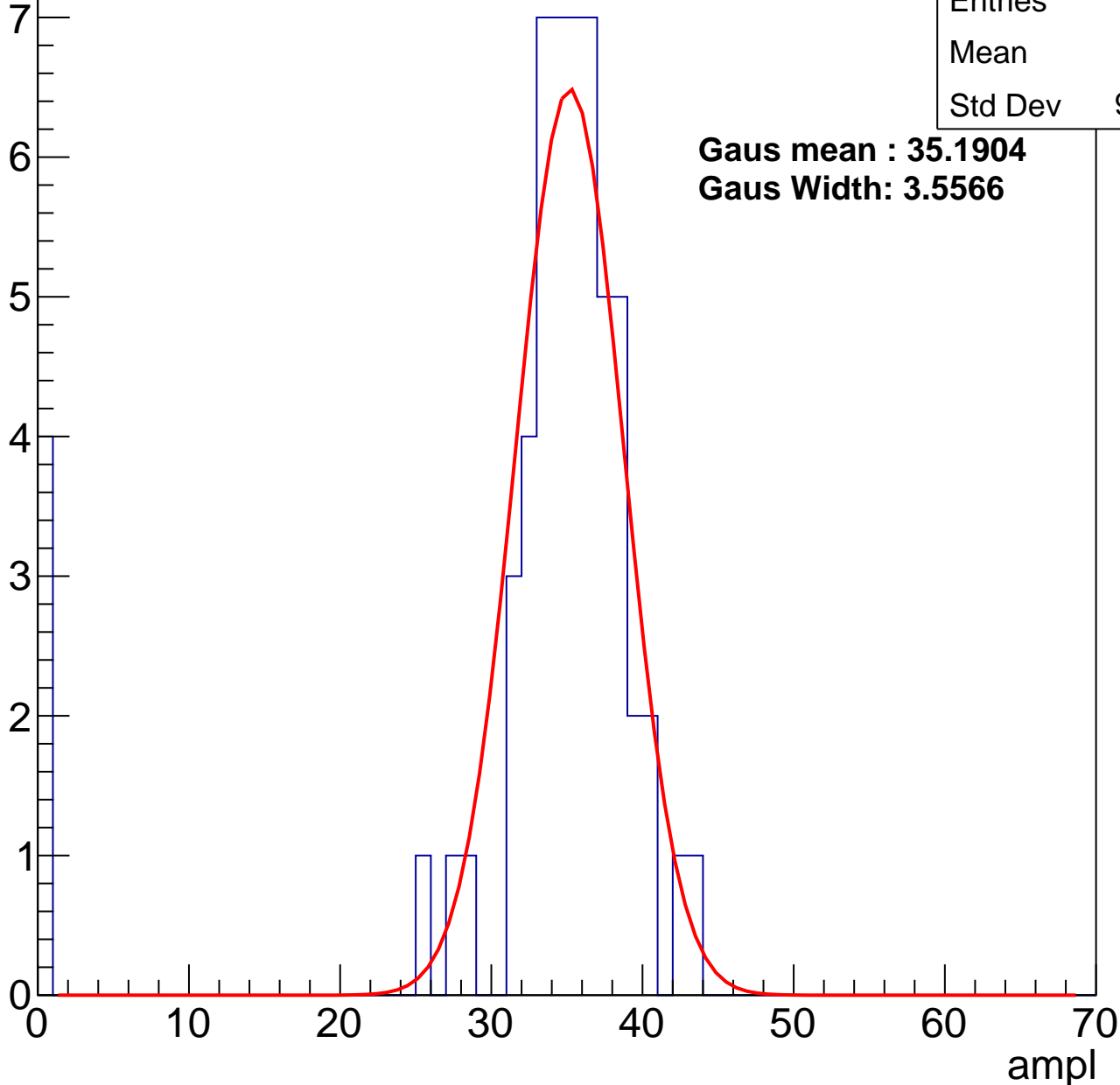
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	32.5
Std Dev	9.411

Gaus mean : 35.1904

Gaus Width: 3.5566



B1L103S, U21-ch79, adc2

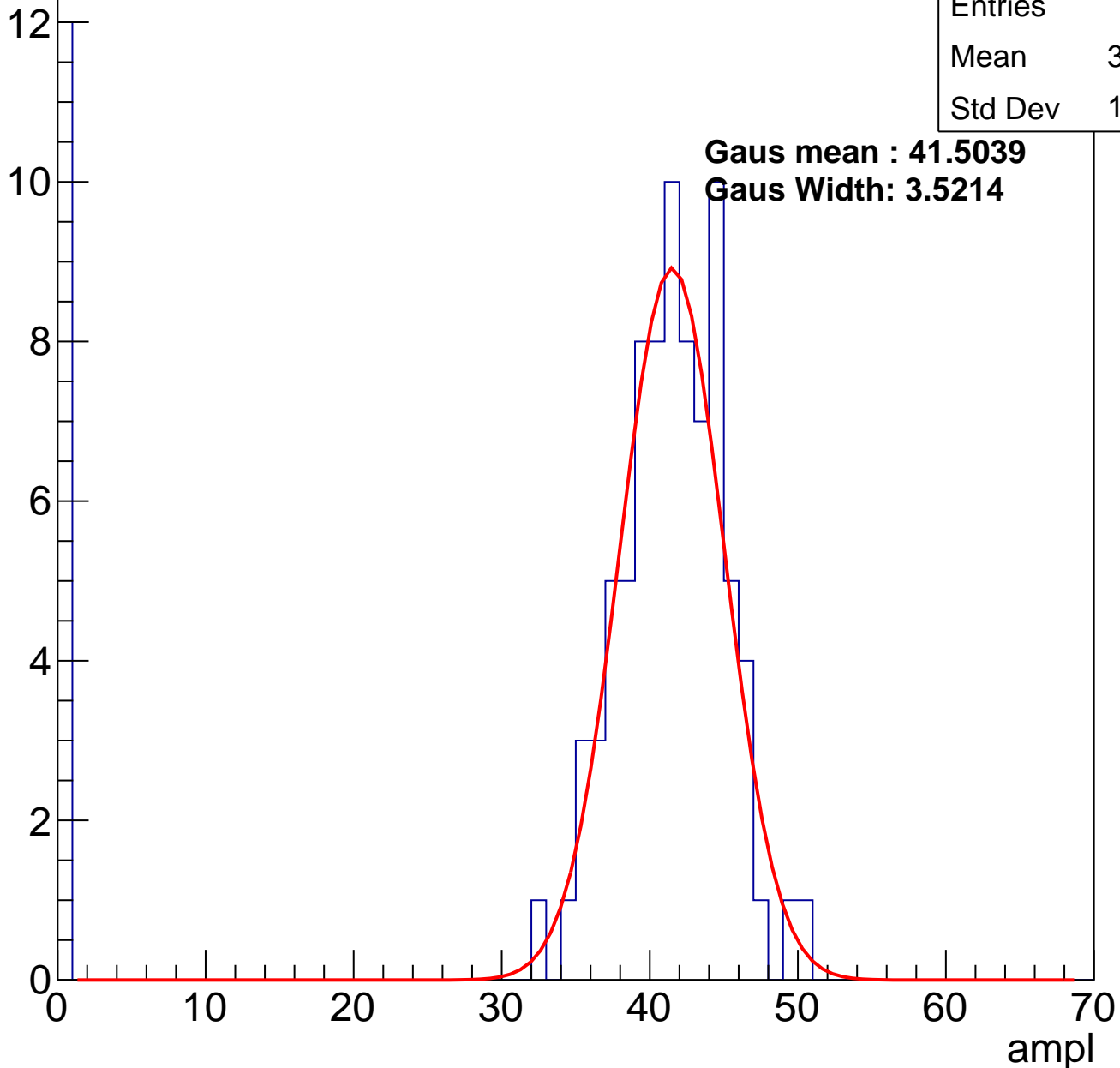
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	35.78
Std Dev	14.15

Gaus mean : 41.5039

Gaus Width: 3.5214

Entry

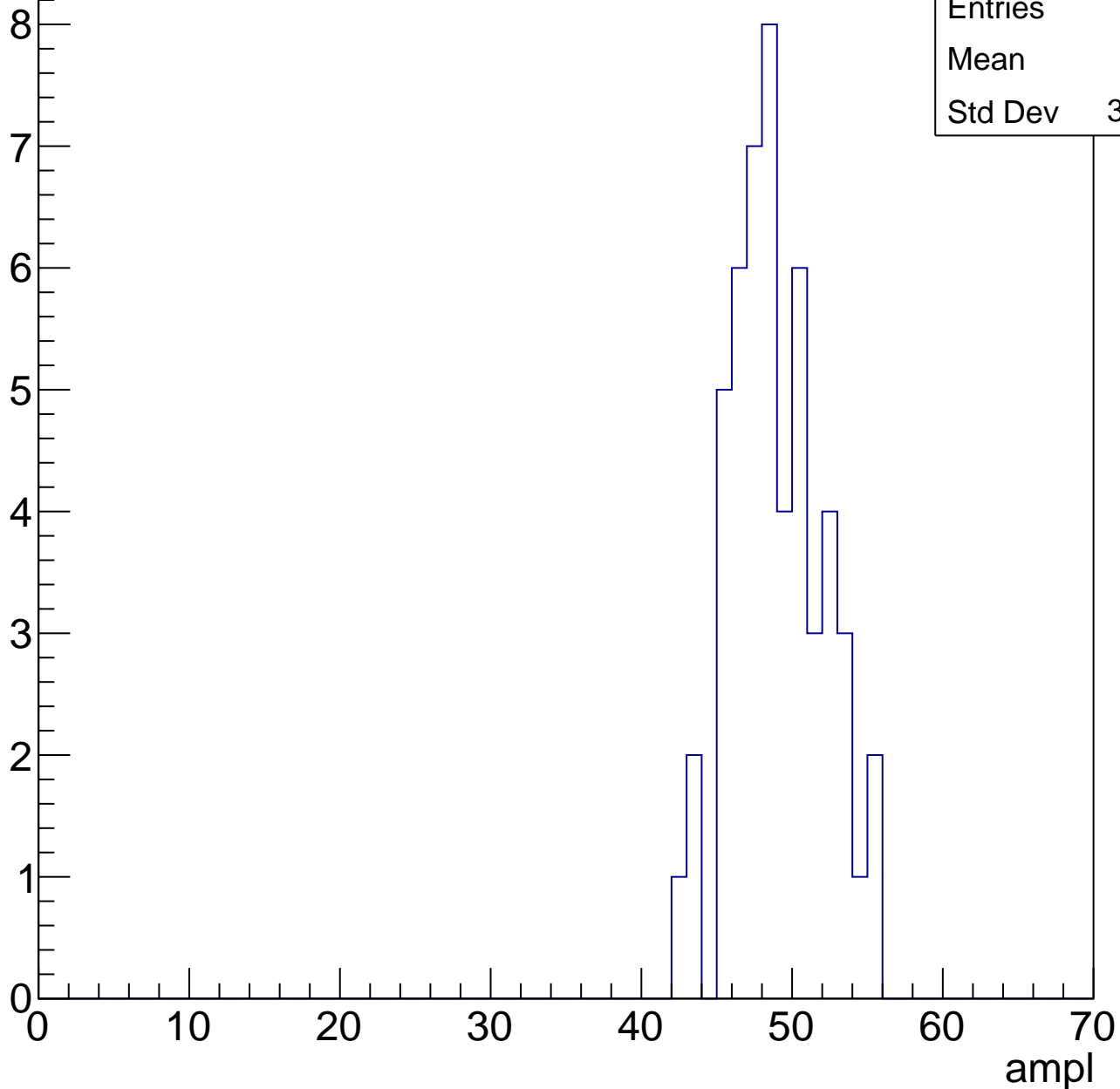


B1L103S, U21-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	48.5
Std Dev	3.029

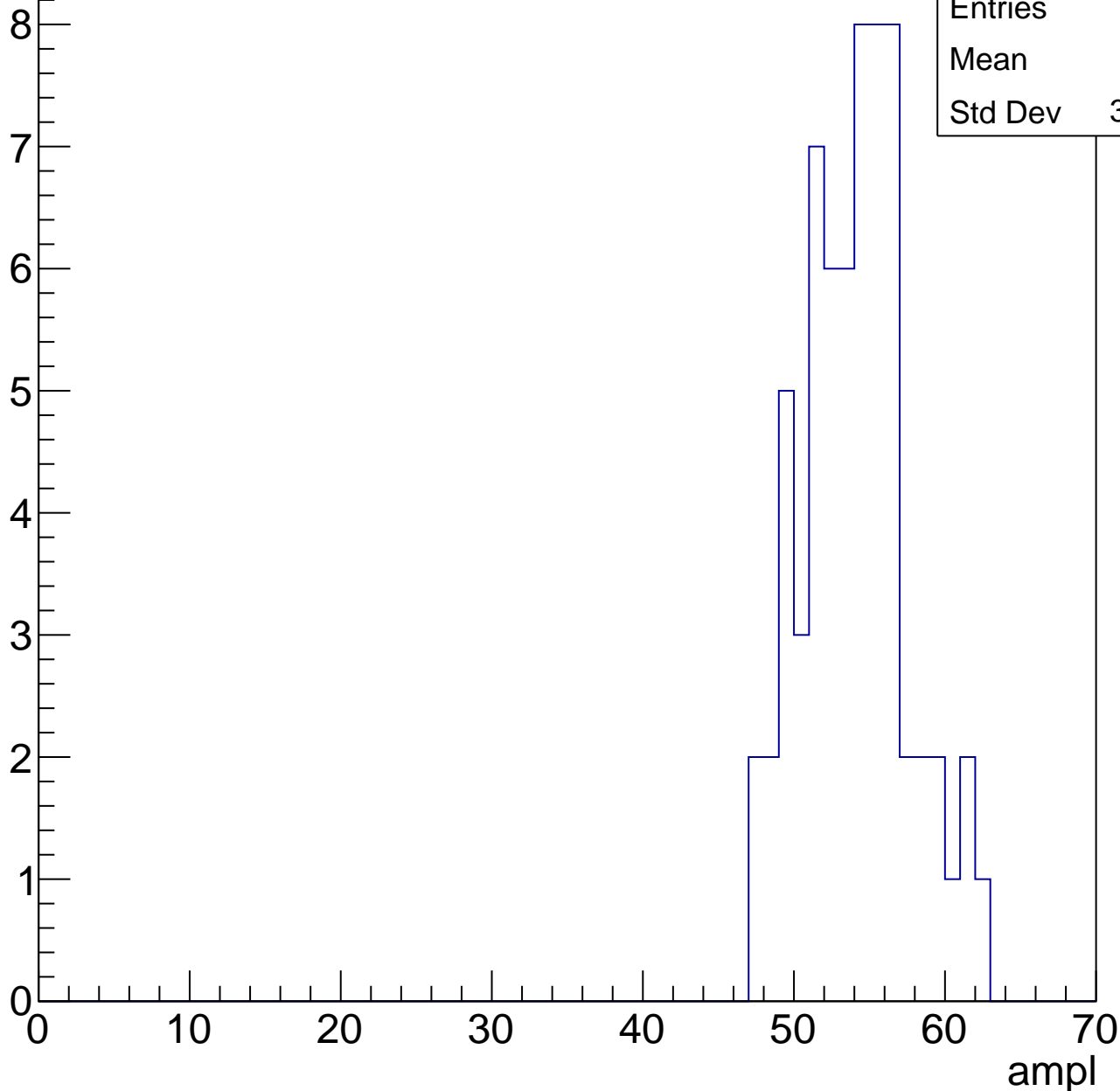


B1L103S, U21-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	53.6
Std Dev	3.427

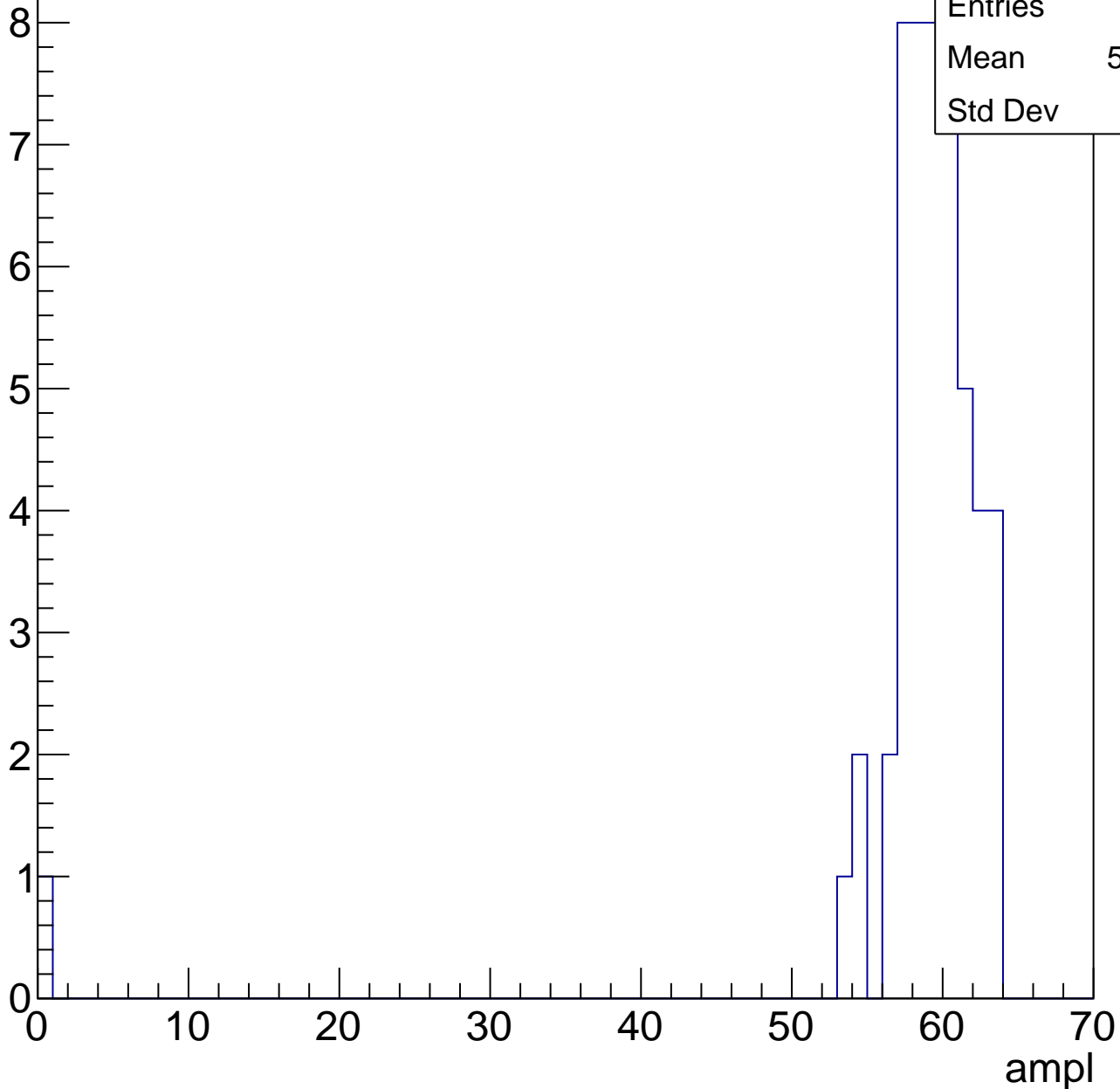


B1L103S, U21-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.84
Std Dev	8.5

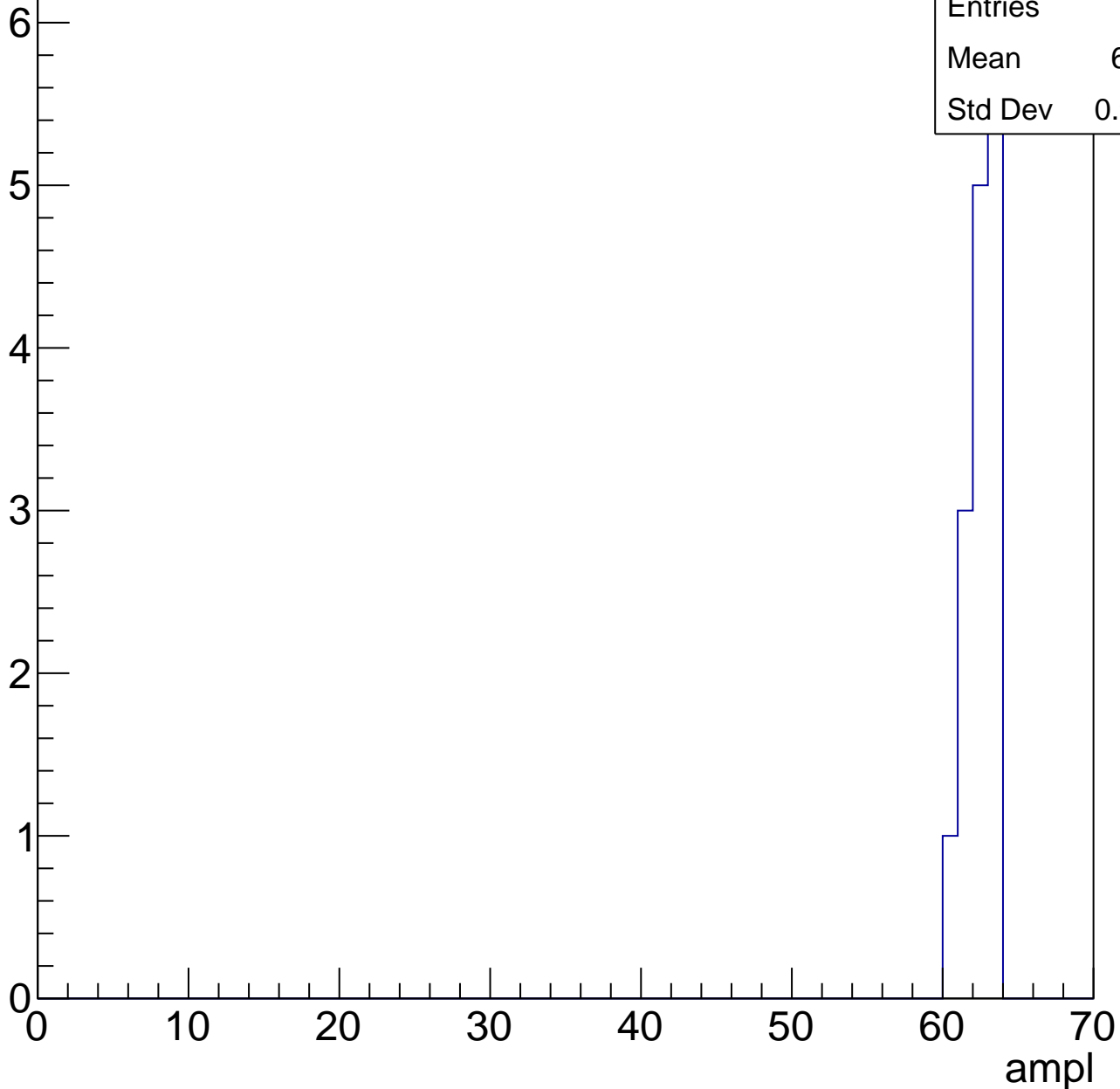


B1L103S, U21-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	62.07
Std Dev	0.9286



B1L103S, U21-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch80, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	22.33
Std Dev	10.34

Gaus mean : 27.3730

Gaus Width: 3.2316

Entry

10

8

6

4

2

0

0

10

20

30

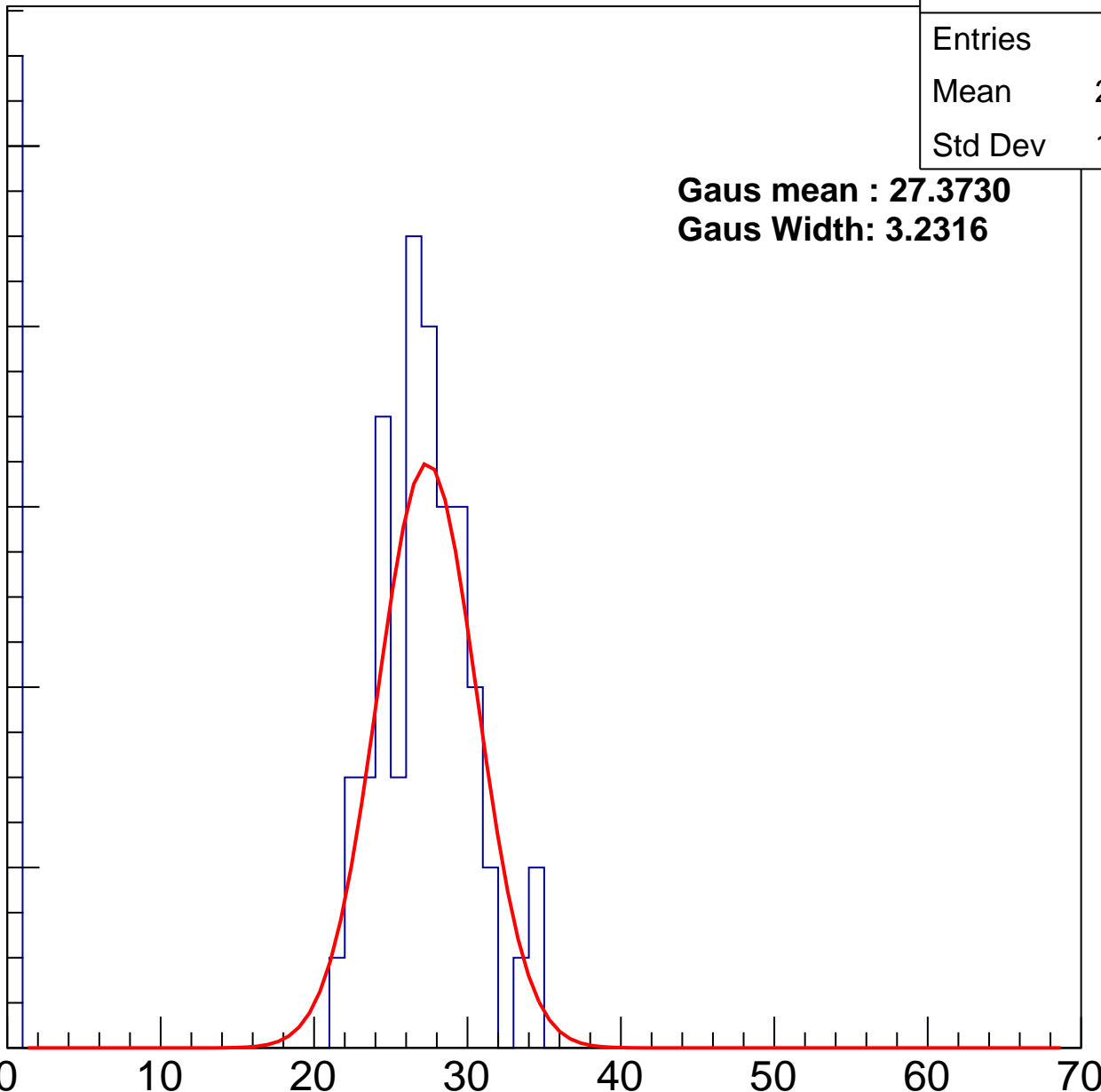
40

50

60

70

ampl



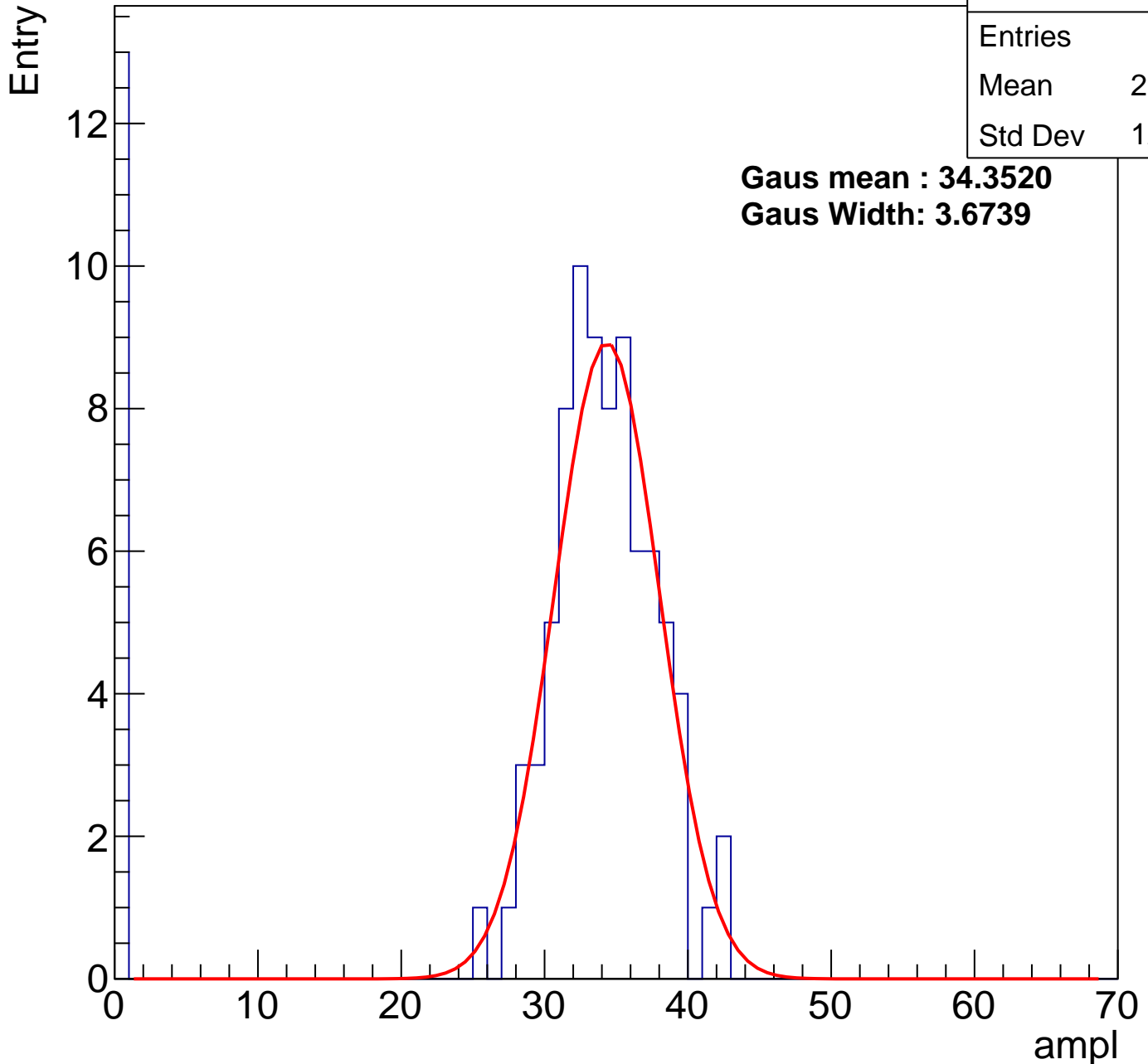
B1L103S, U21-ch80, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	29.09
Std Dev	12.08

Gaus mean : 34.3520

Gaus Width: 3.6739



B1L103S, U21-ch80, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	40.1
Std Dev	6.115

Gaus mean : 40.9641

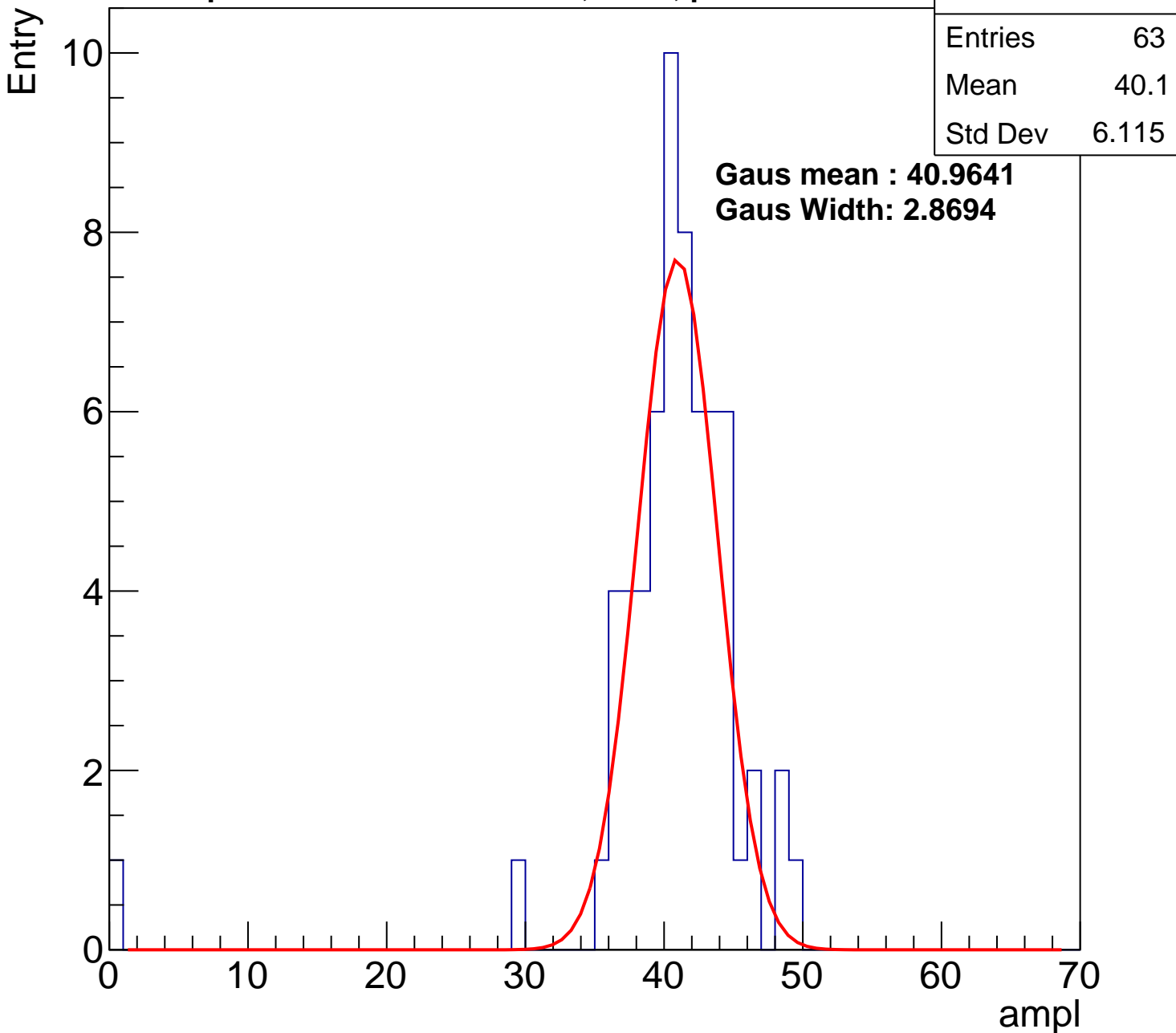
Gaus Width: 2.8694

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	47.1
Std Dev	3.156

Entry

10

8

6

4

2

0

0

10

20

30

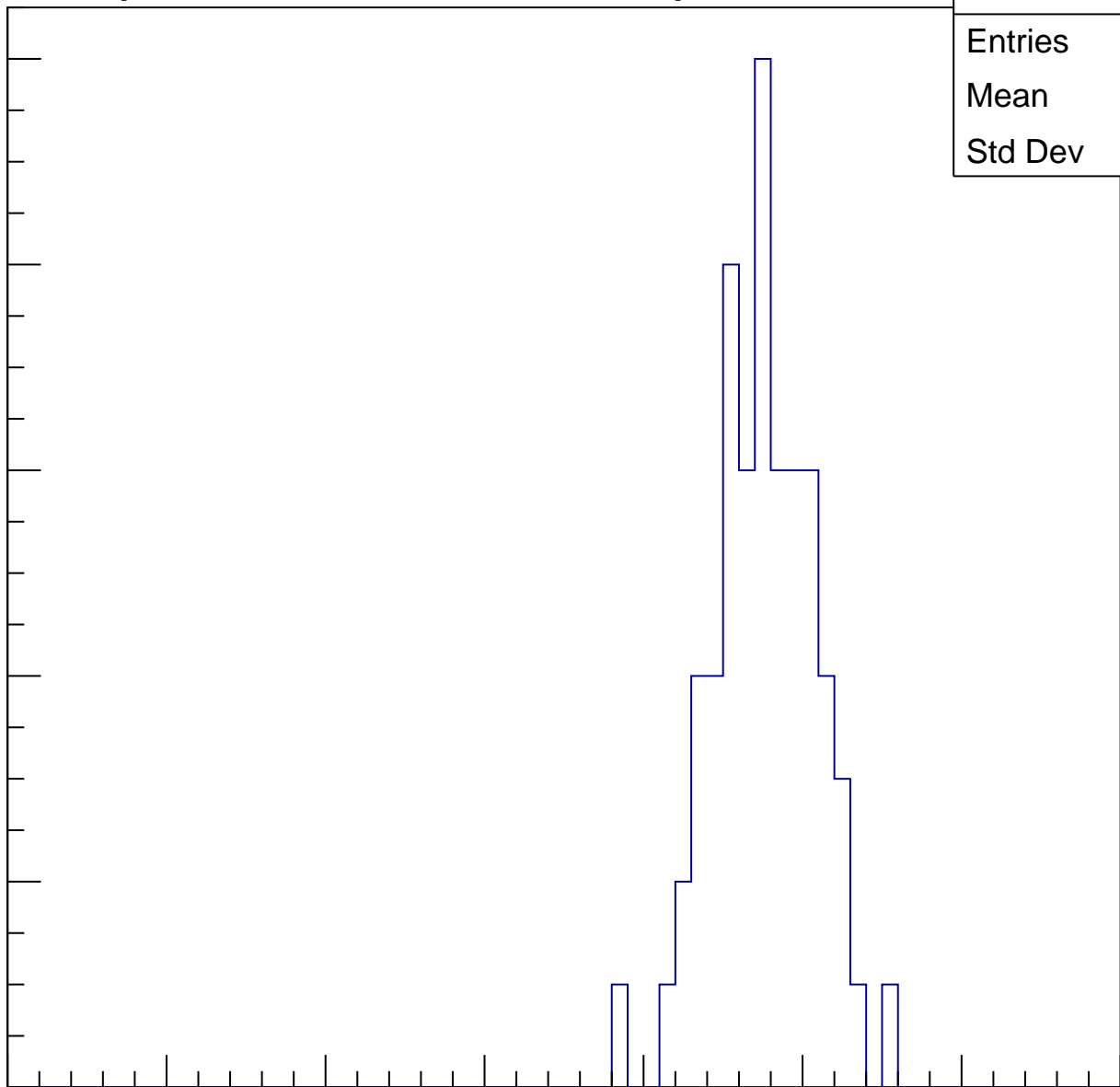
40

50

60

70

ampl

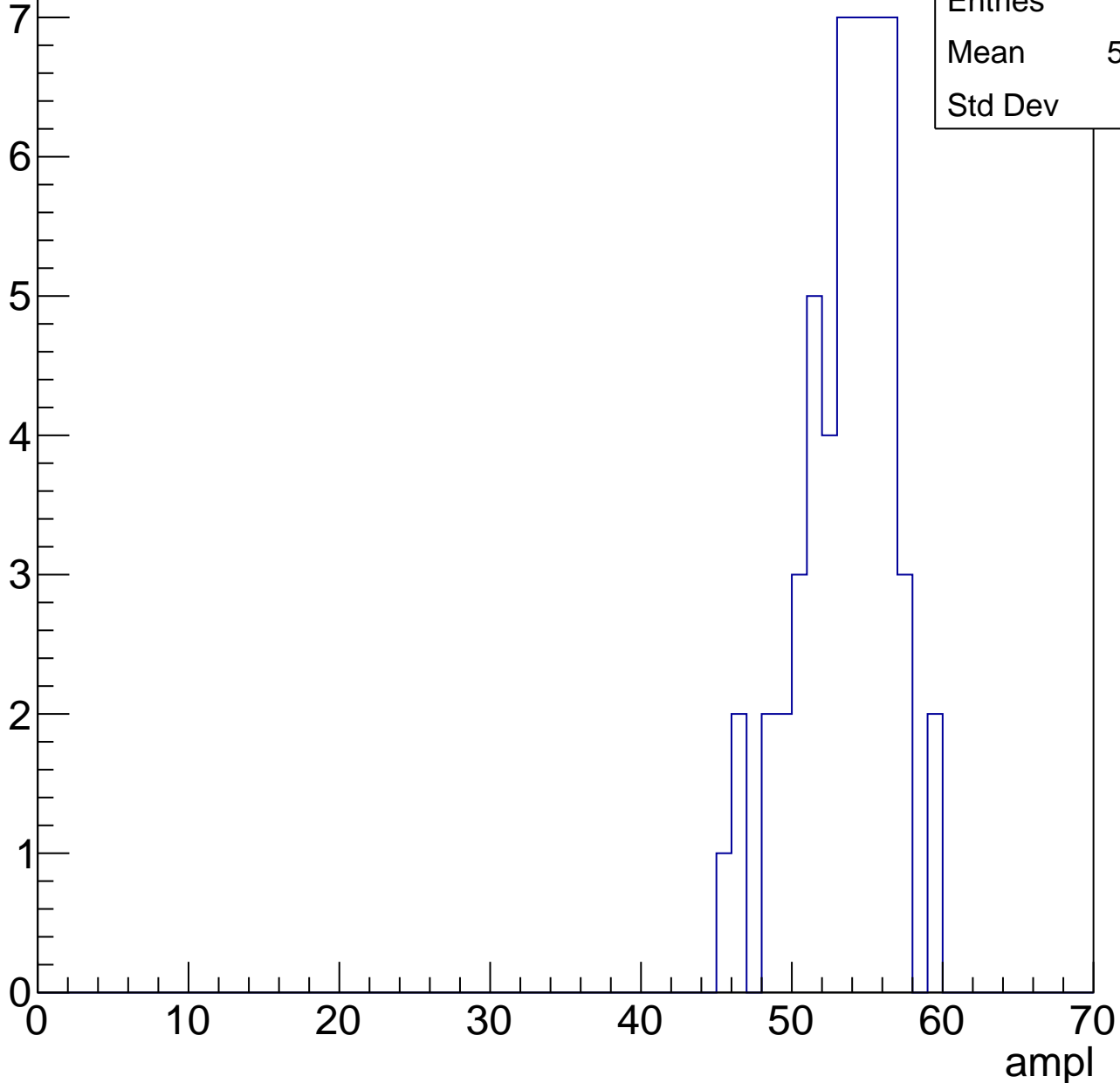


B1L103S, U21-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.06
Std Dev	3.14



B1L103S, U21-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 57

Mean 57.82

Std Dev 3.414

8

6

4

2

0

0

10

20

30

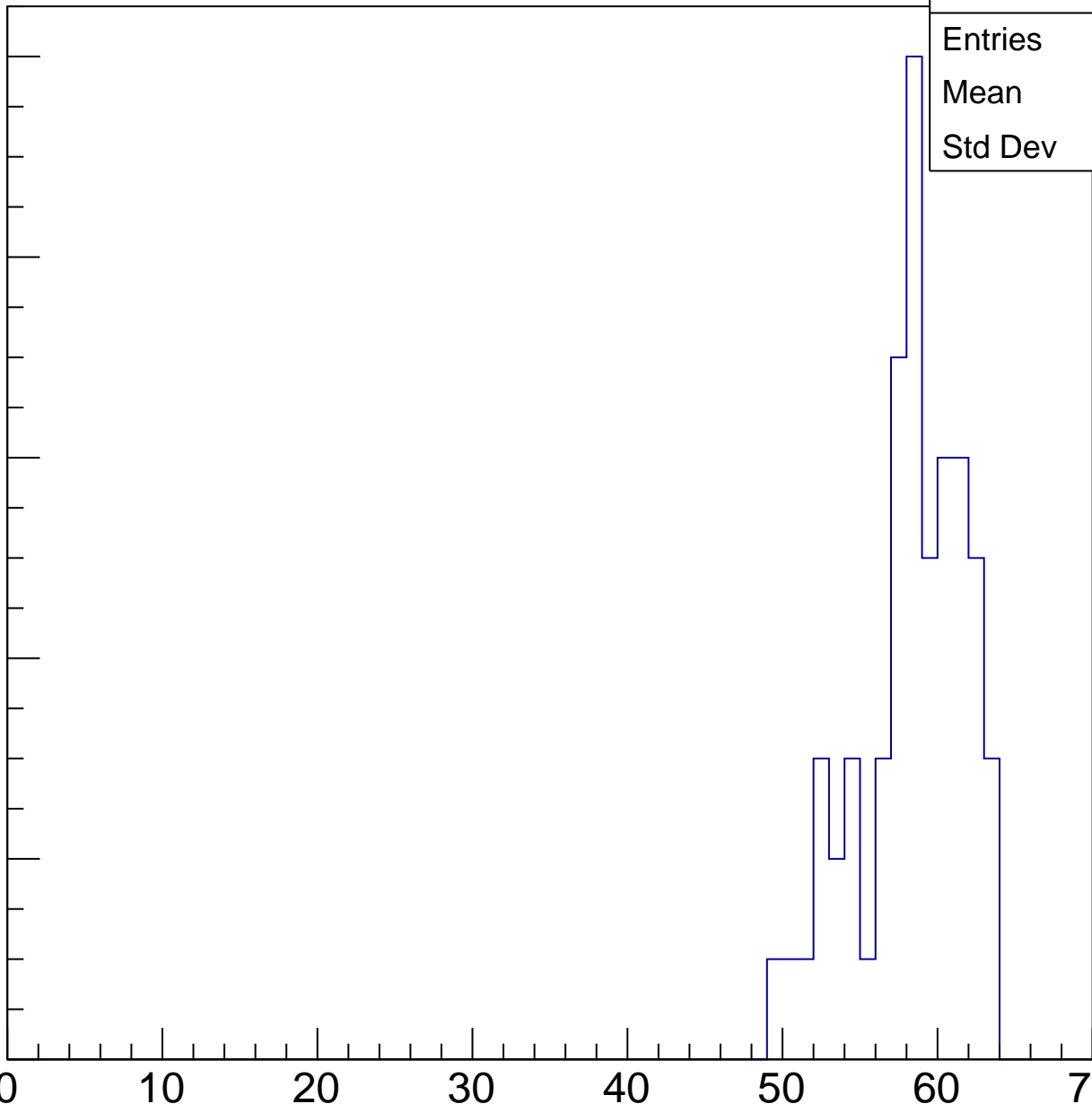
40

50

60

70

ampl

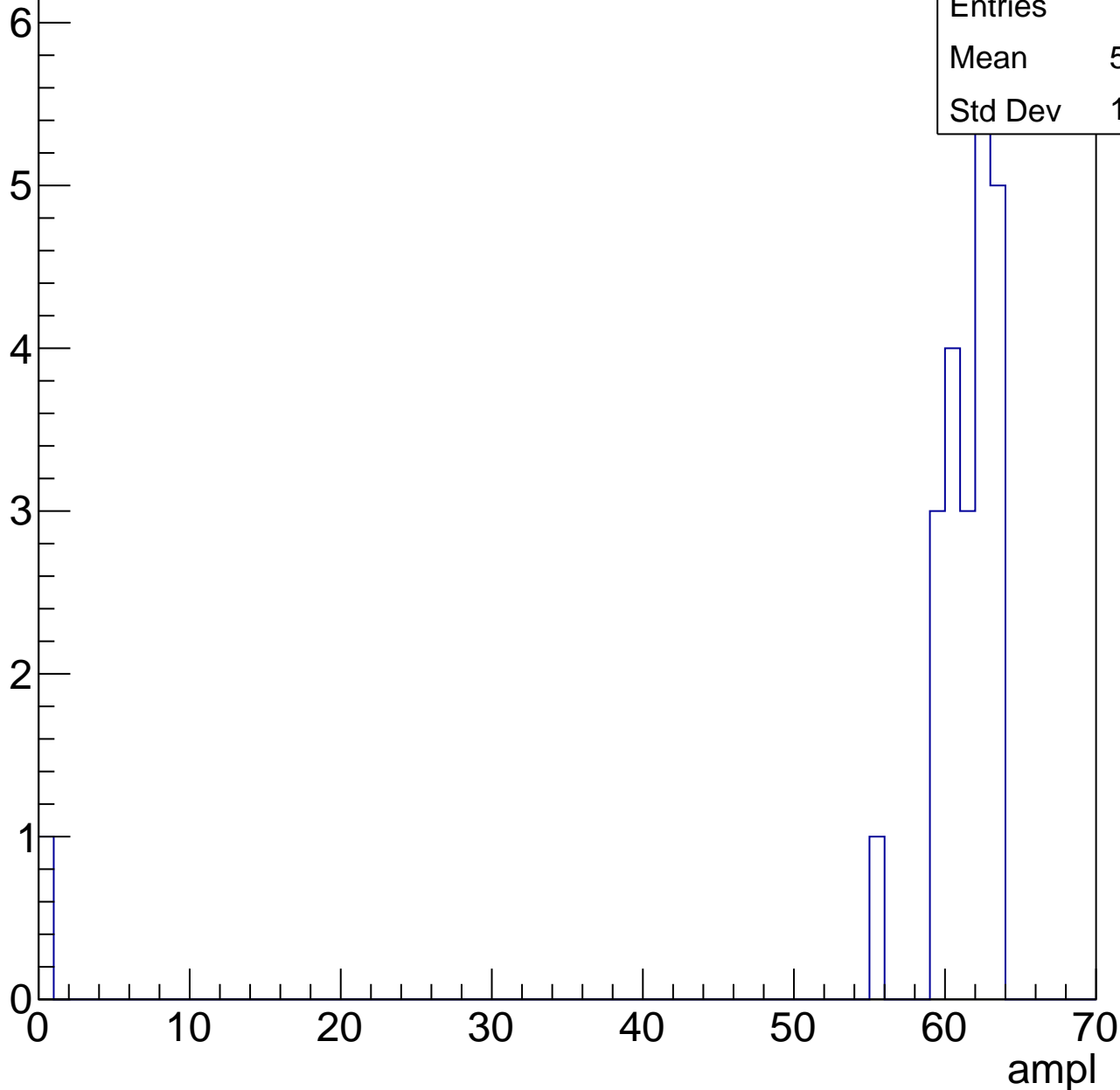


B1L103S, U21-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

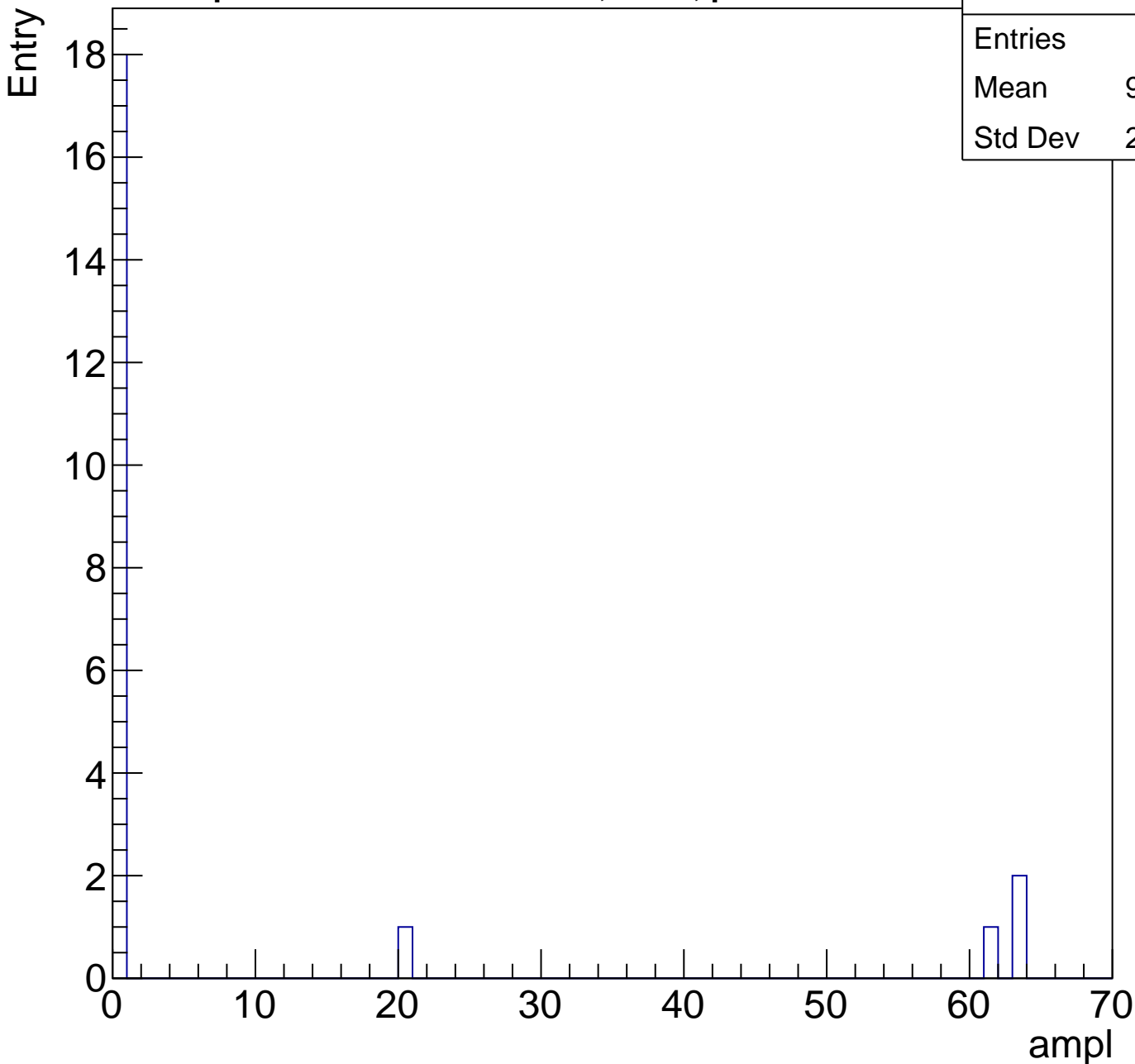
Entries	23
Mean	58.35
Std Dev	12.58



B1L103S, U21-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	9.409
Std Dev	21.44



B1L103S, U21-ch81, adc0

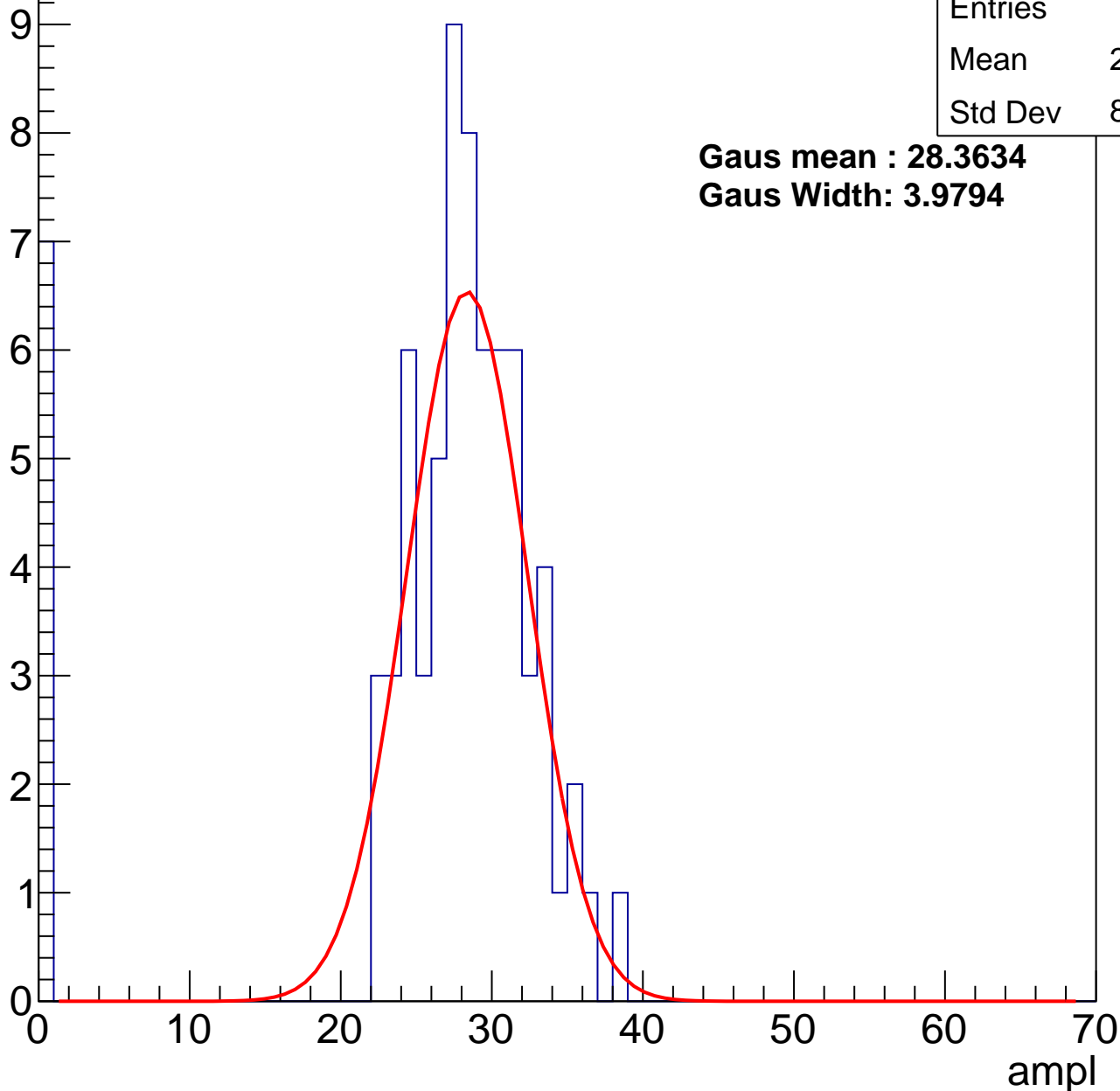
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	25.64
Std Dev	8.959

Gaus mean : 28.3634

Gaus Width: 3.9794



B1L103S, U21-ch81, adc1

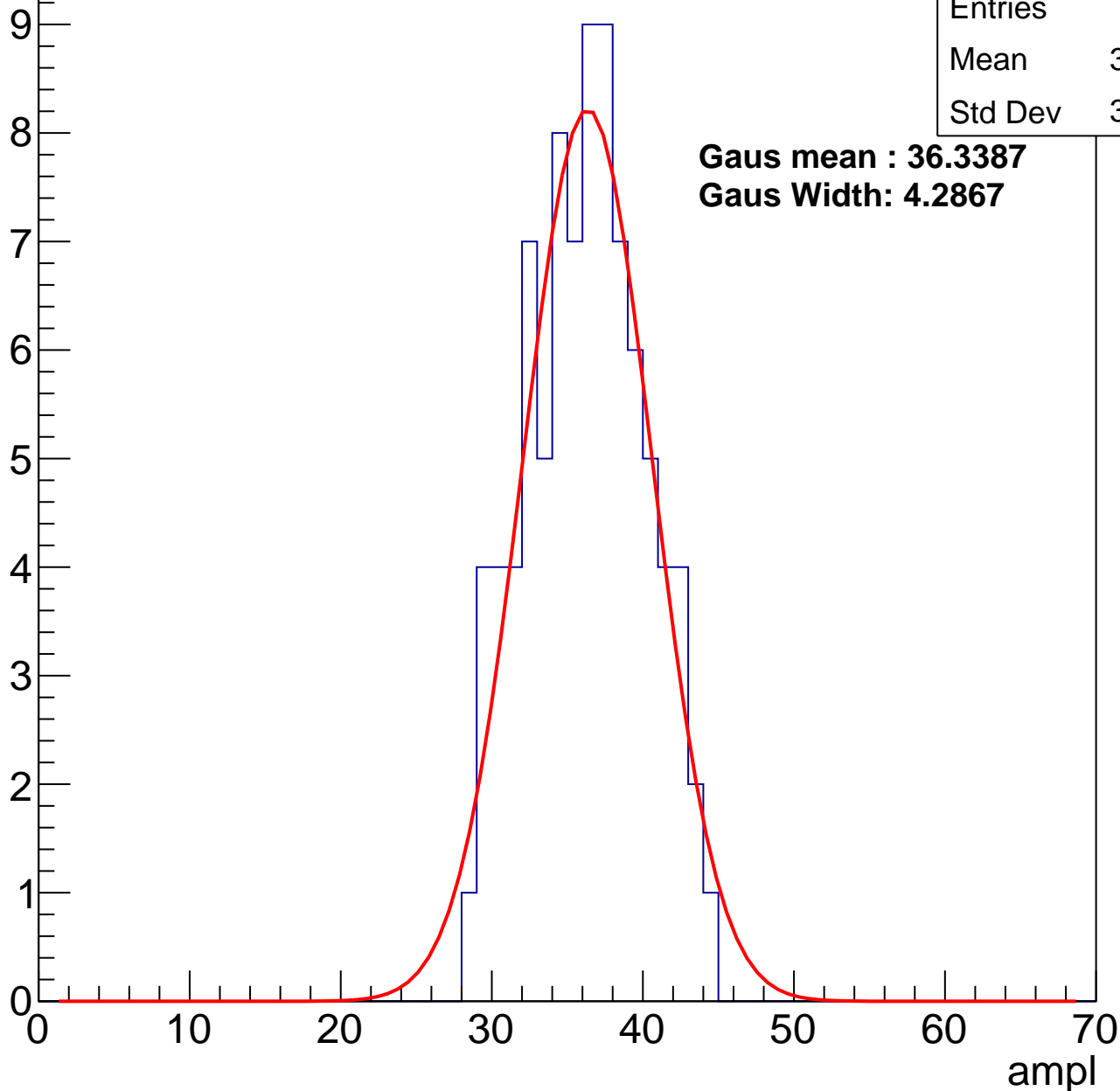
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	35.78
Std Dev	3.825

Gaus mean : 36.3387

Gaus Width: 4.2867



B1L103S, U21-ch81, adc2

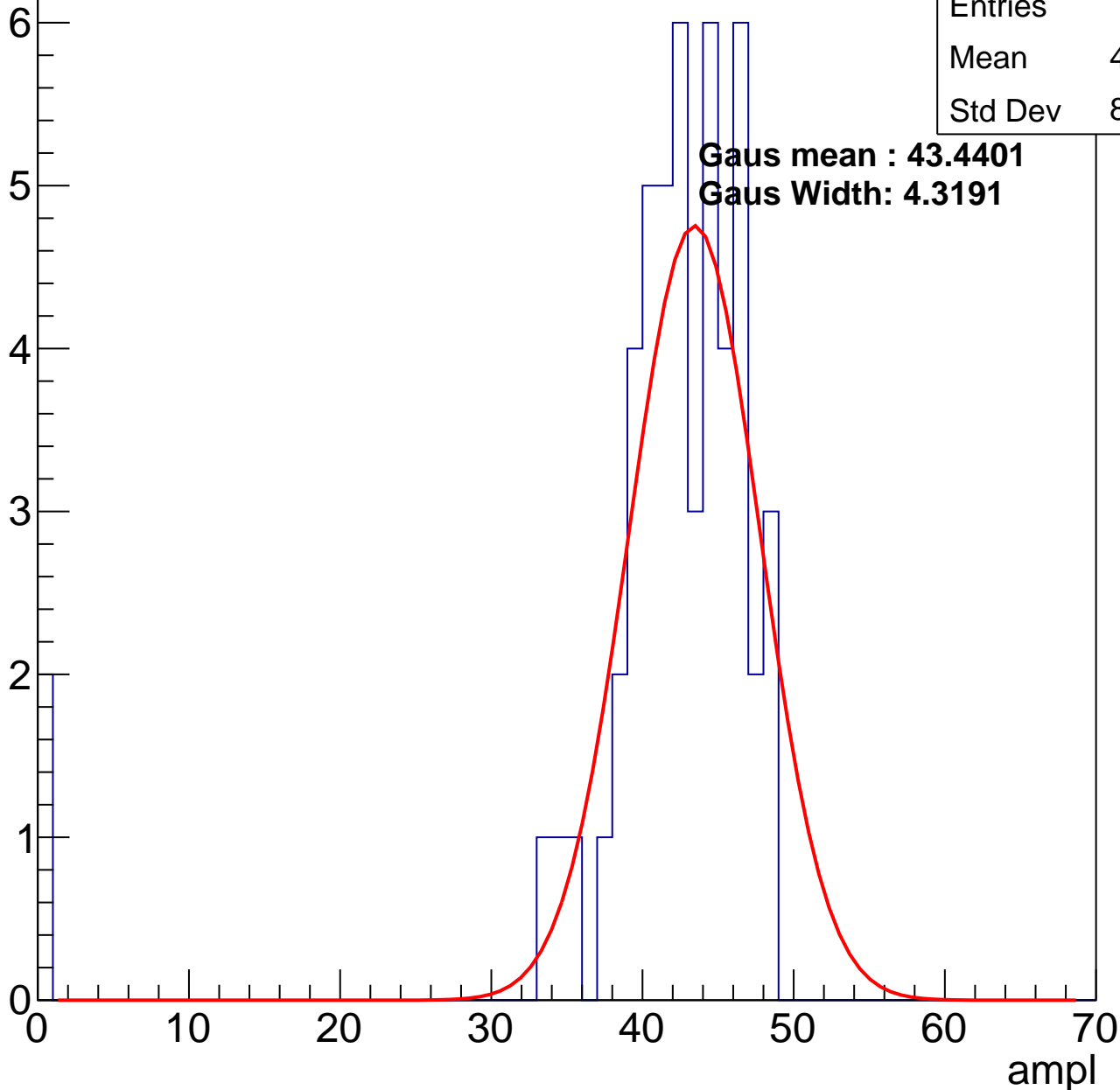
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	40.67
Std Dev	8.842

Gaus mean : 43.4401

Gaus Width: 4.3191

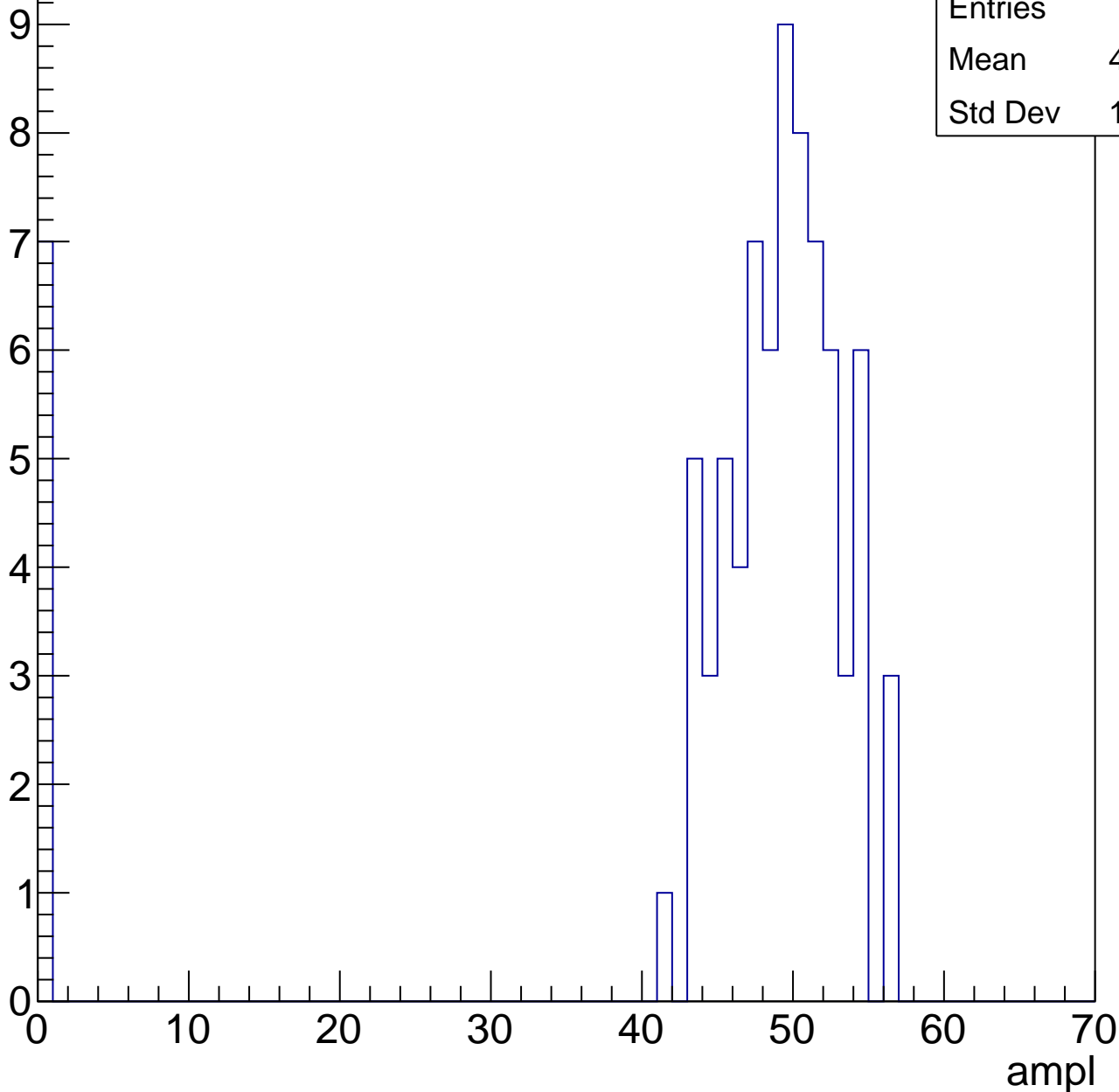


B1L103S, U21-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	44.69
Std Dev	14.24

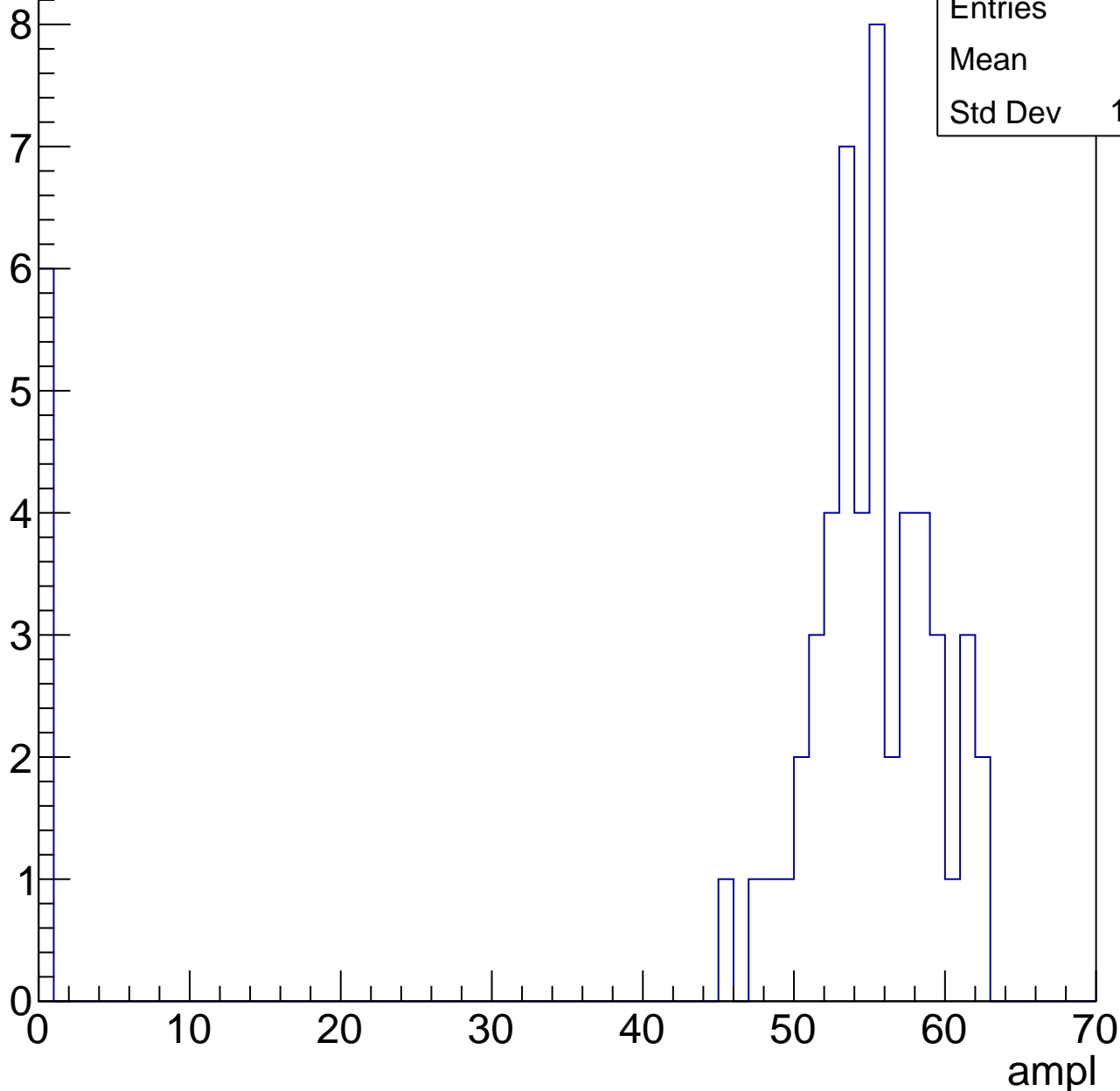


B1L103S, U21-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

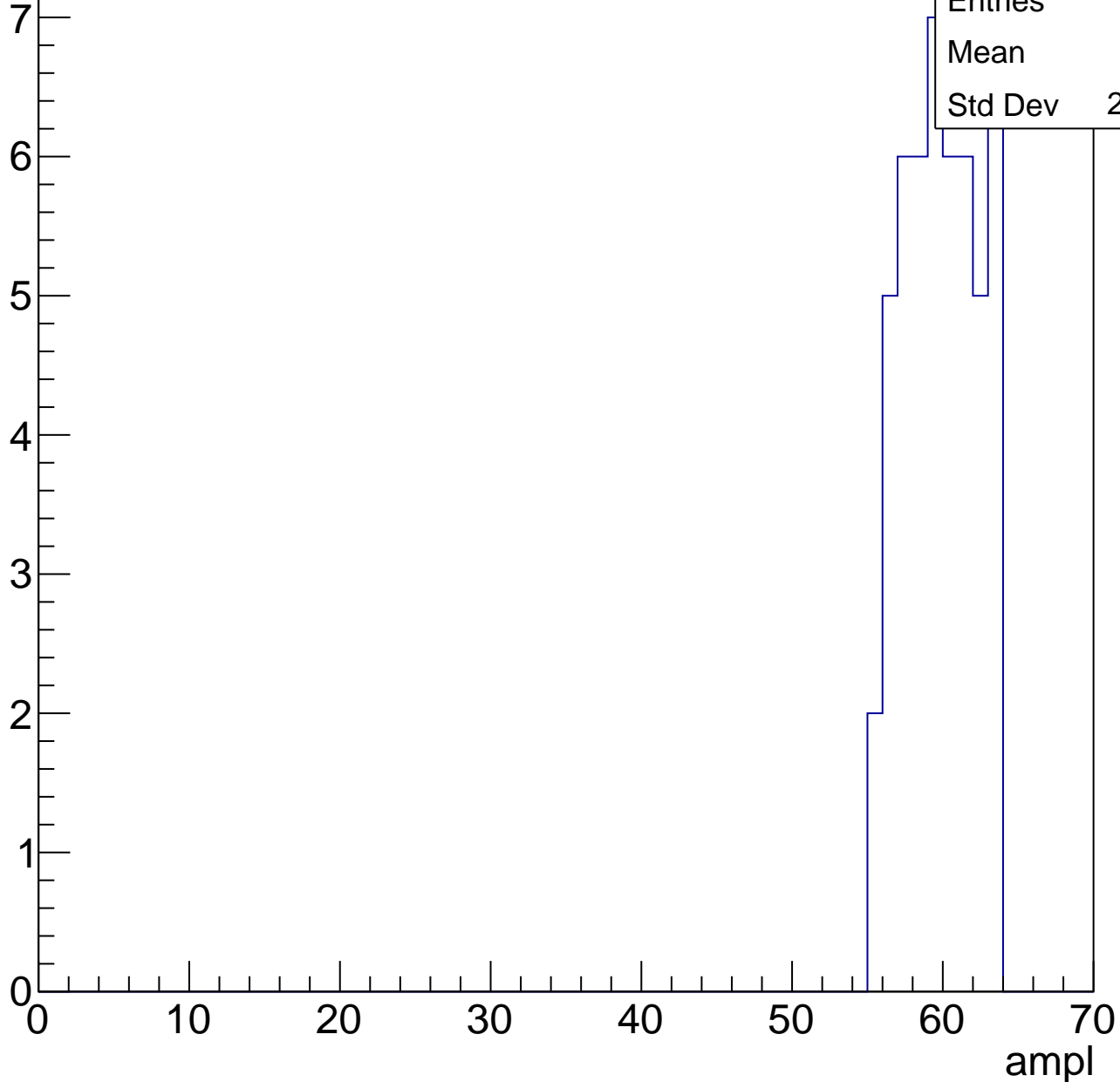
Entries	57
Mean	49
Std Dev	17.19



B1L103S, U21-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

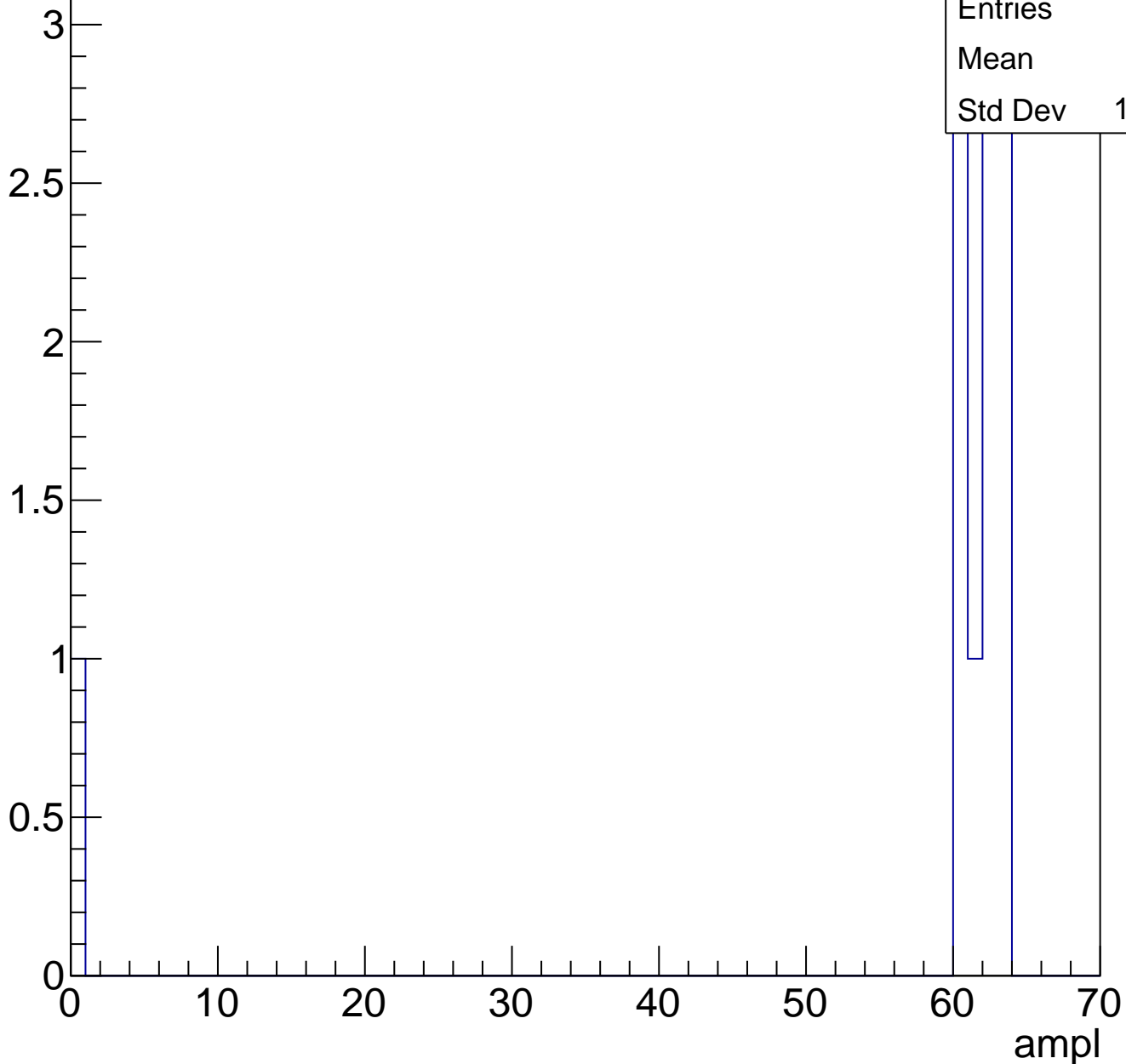
Entry



B1L103S, U21-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



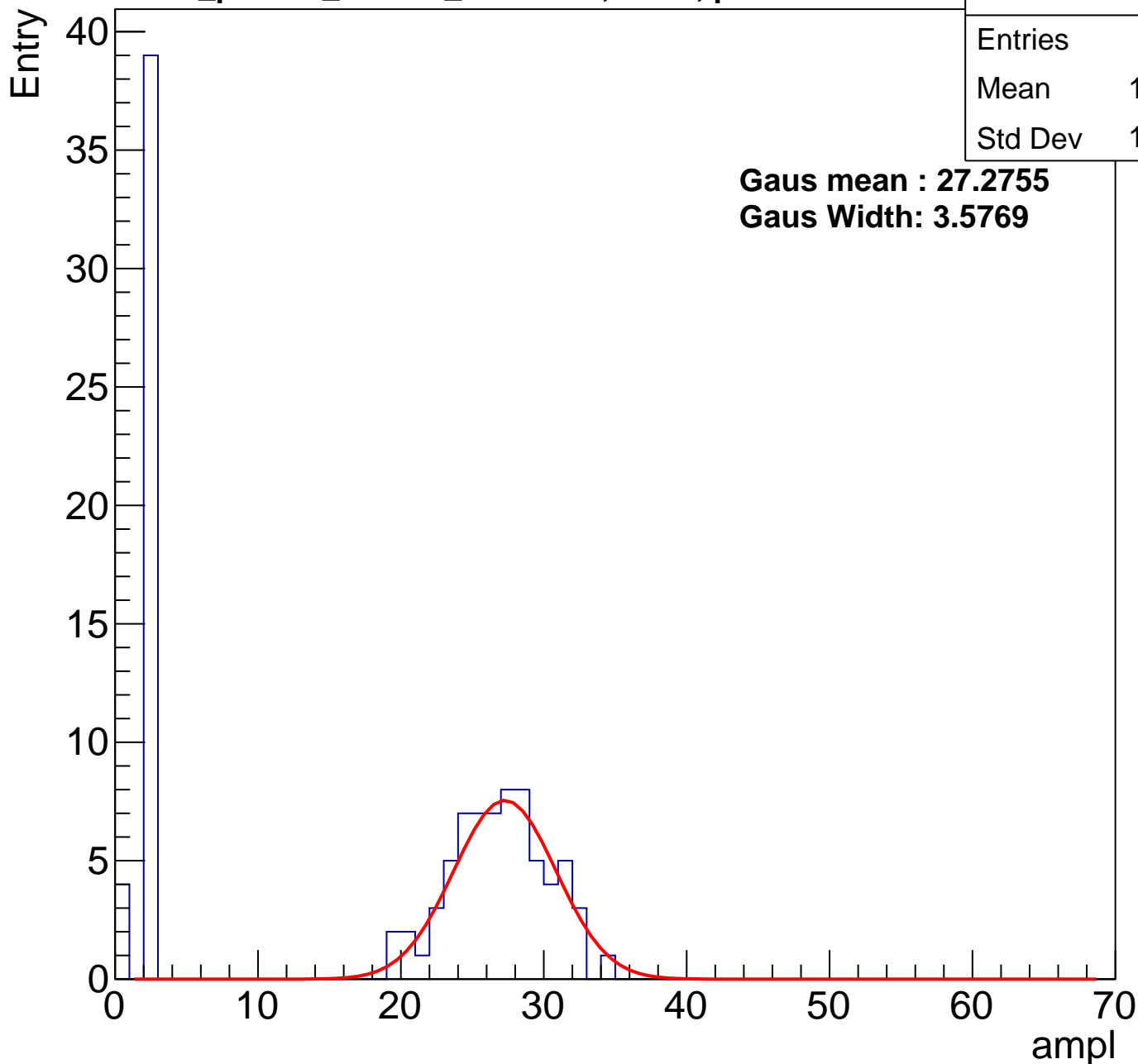
B1L103S, U21-ch82, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	111
Mean	16.87
Std Dev	12.26

Gaus mean : 27.2755

Gaus Width: 3.5769



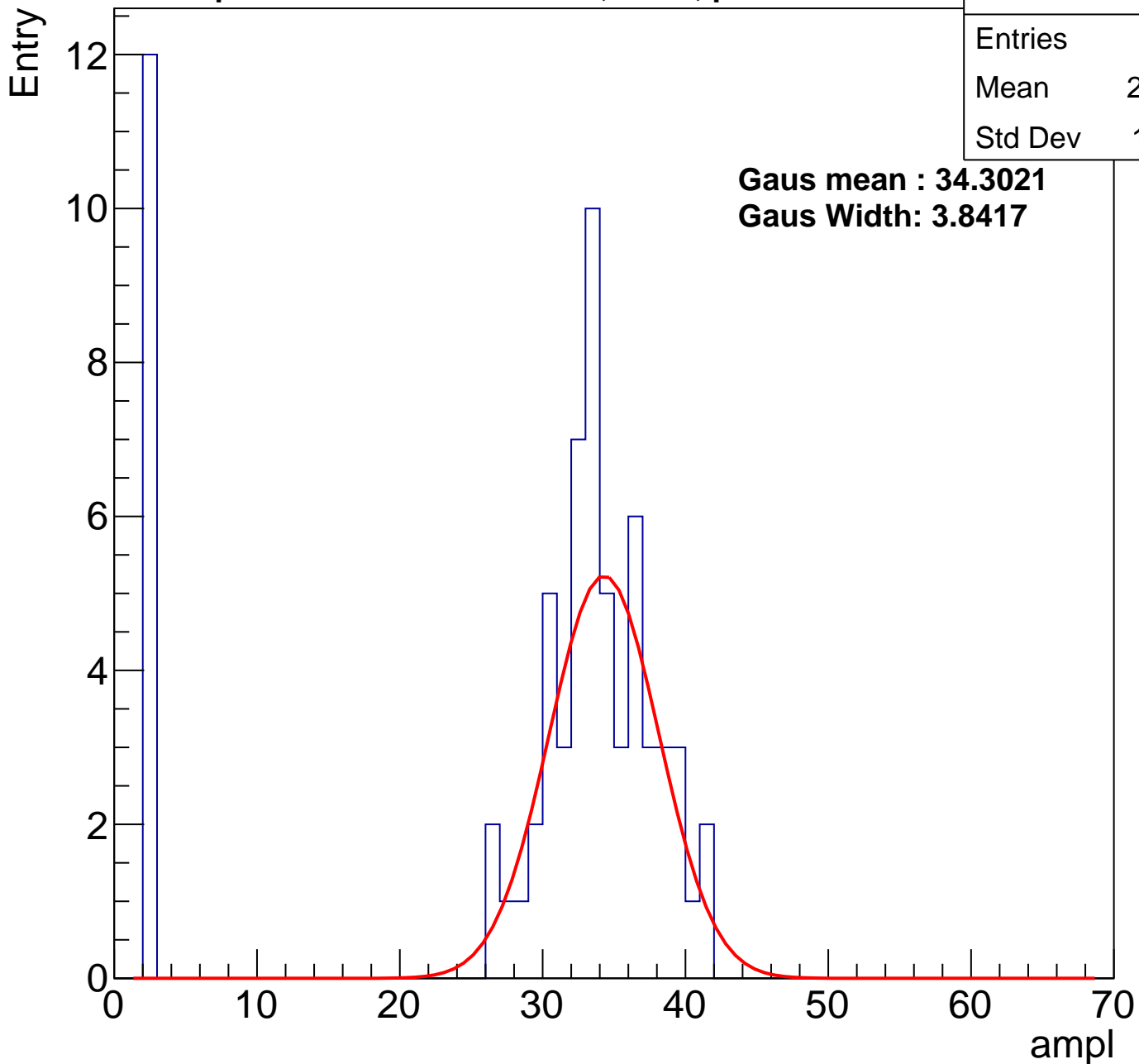
B1L103S, U21-ch82, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	28.13
Std Dev	12.41

Gaus mean : 34.3021

Gaus Width: 3.8417



B1L103S, U21-ch82, adc2

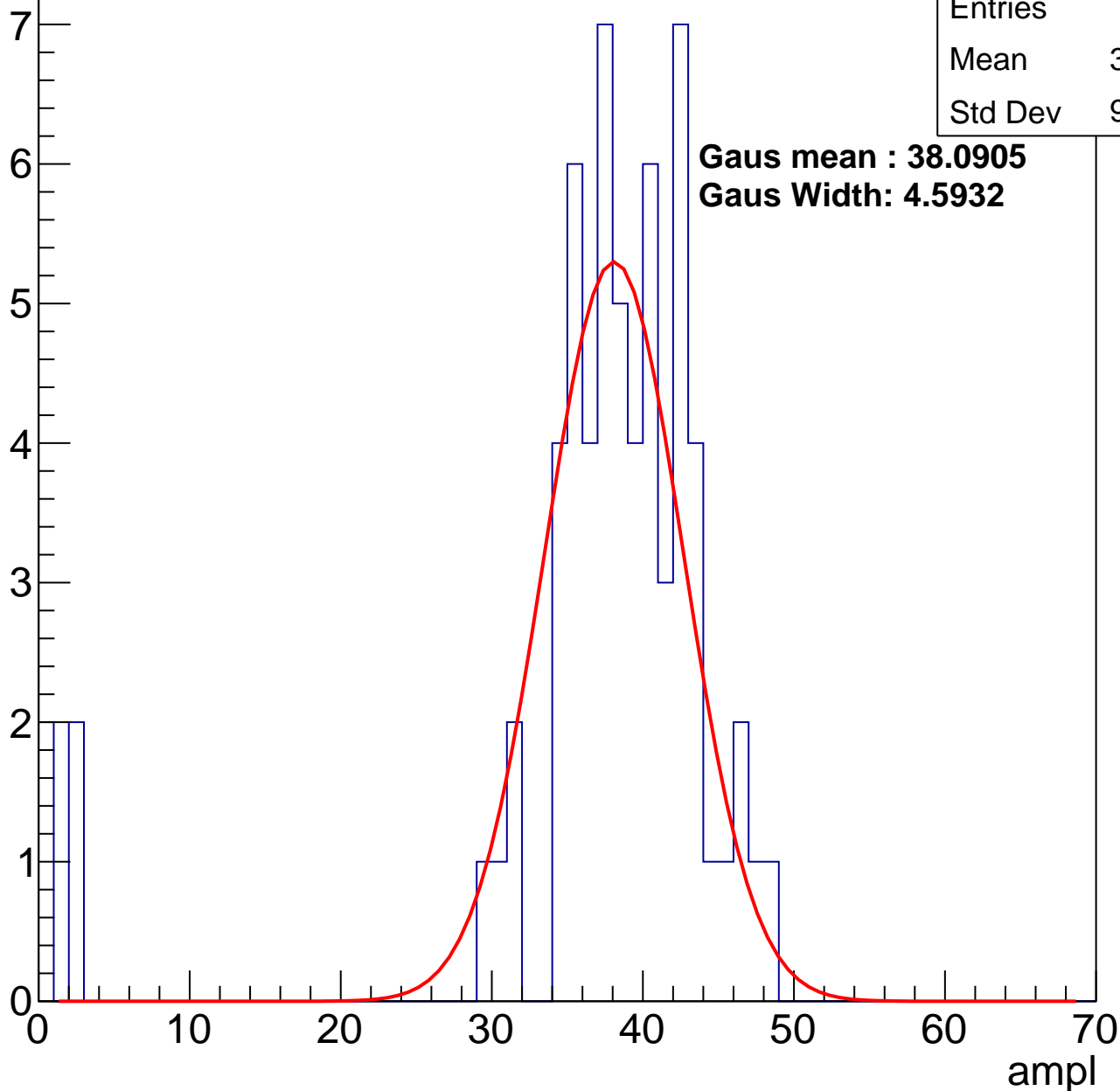
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.33
Std Dev	9.958

Gaus mean : 38.0905

Gaus Width: 4.5932

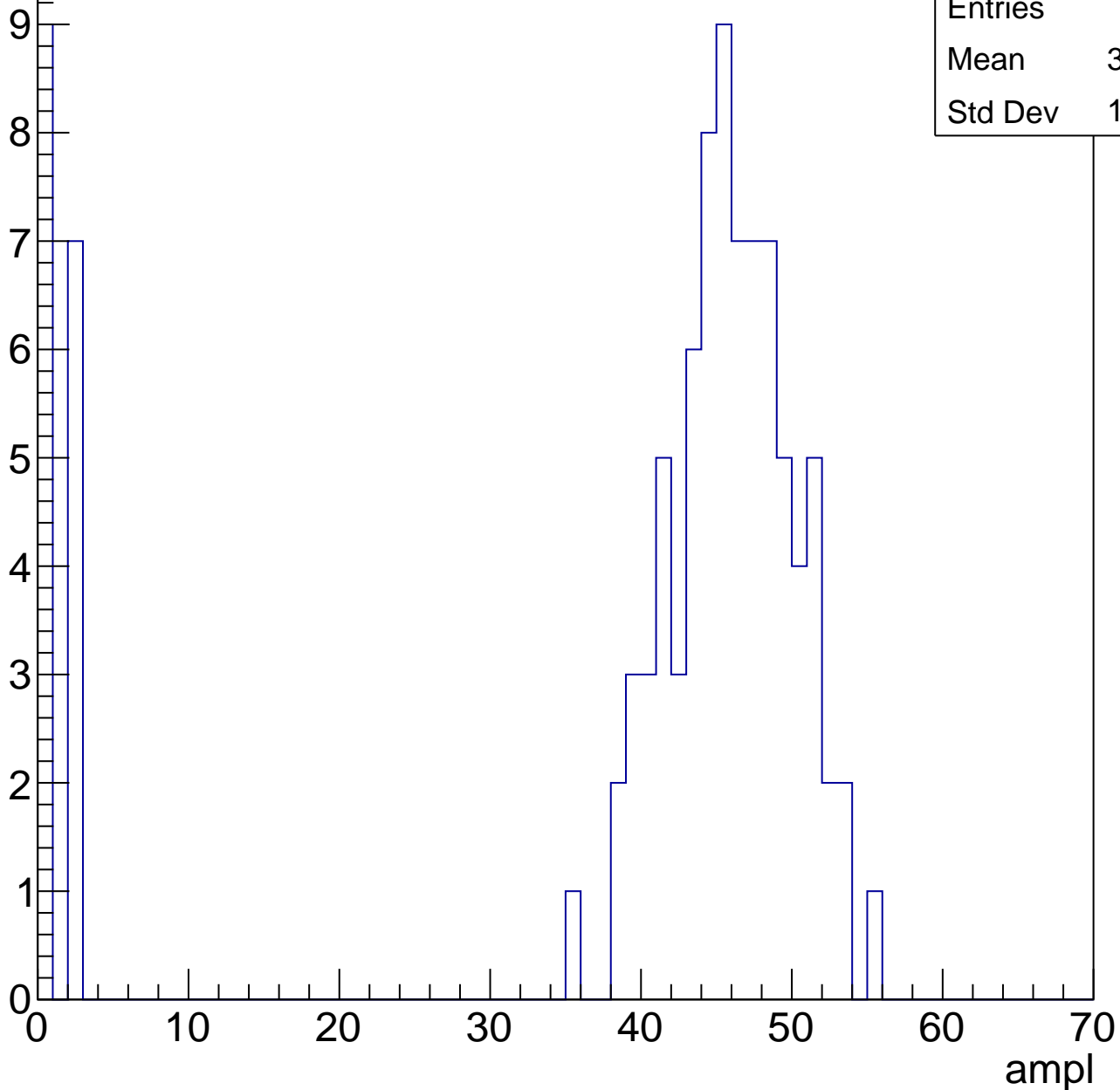


B1L103S, U21-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	96
Mean	38.12
Std Dev	17.06

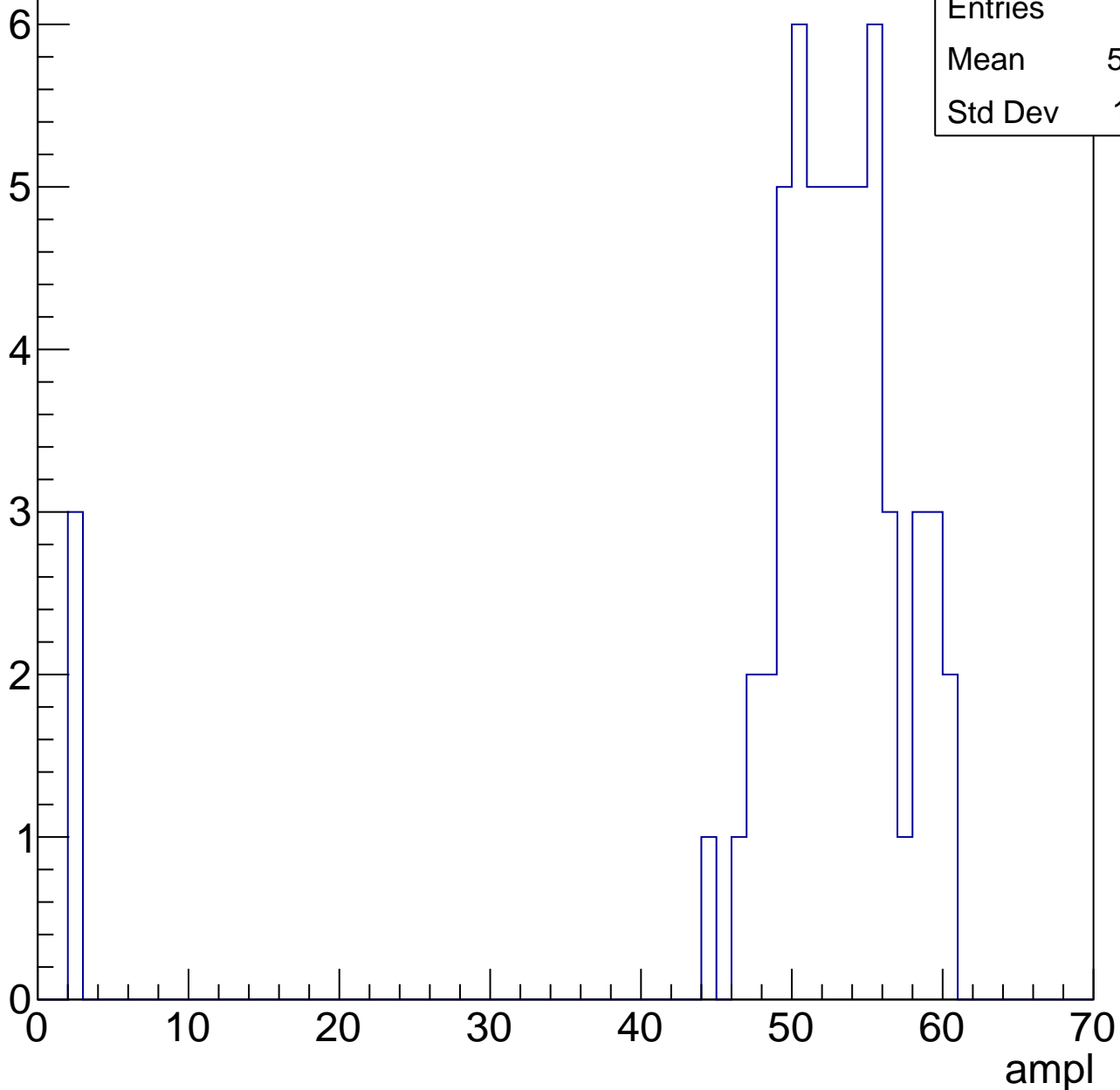


B1L103S, U21-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	50.12
Std Dev	11.81

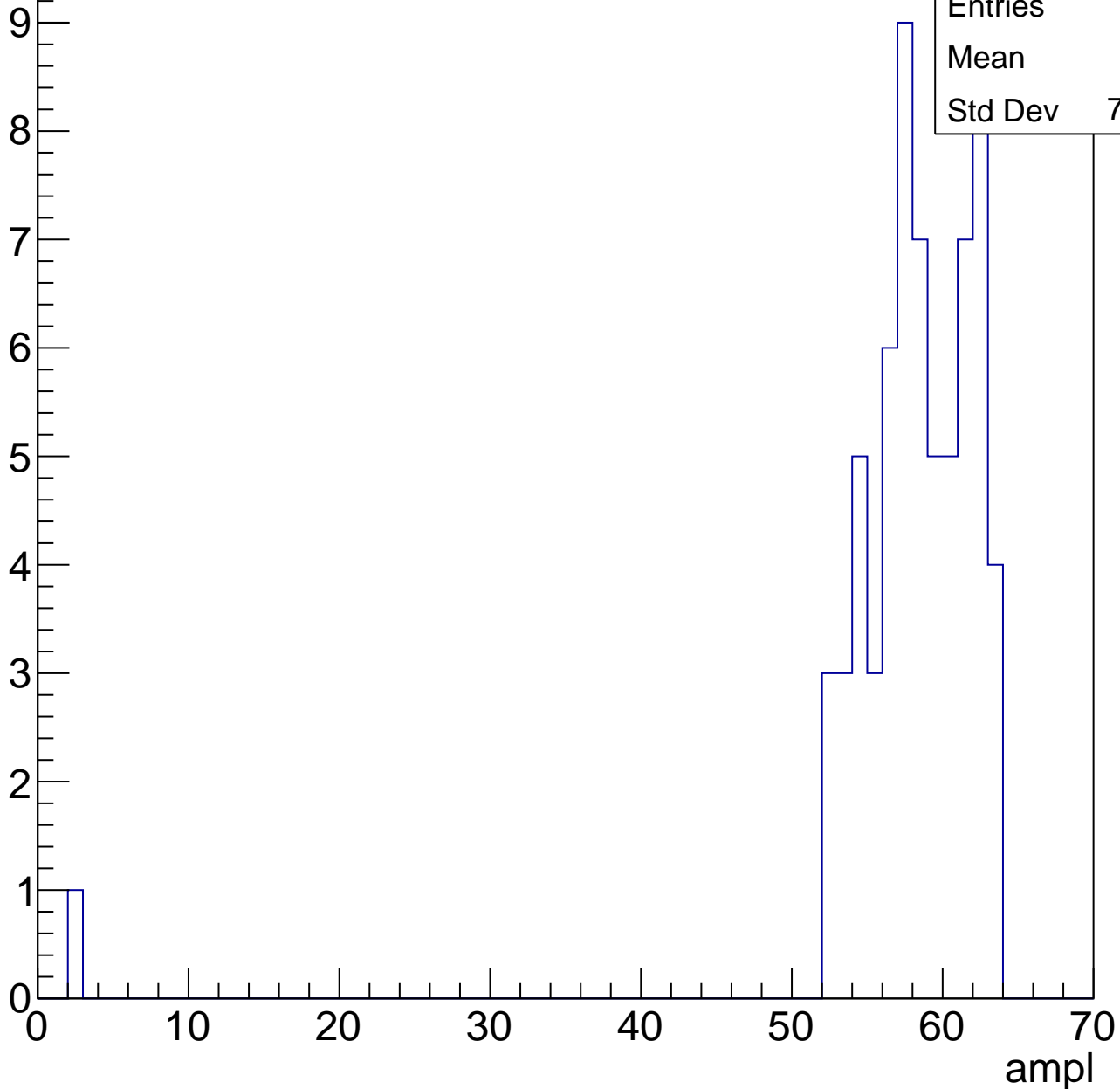


B1L103S, U21-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	57.3
Std Dev	7.489

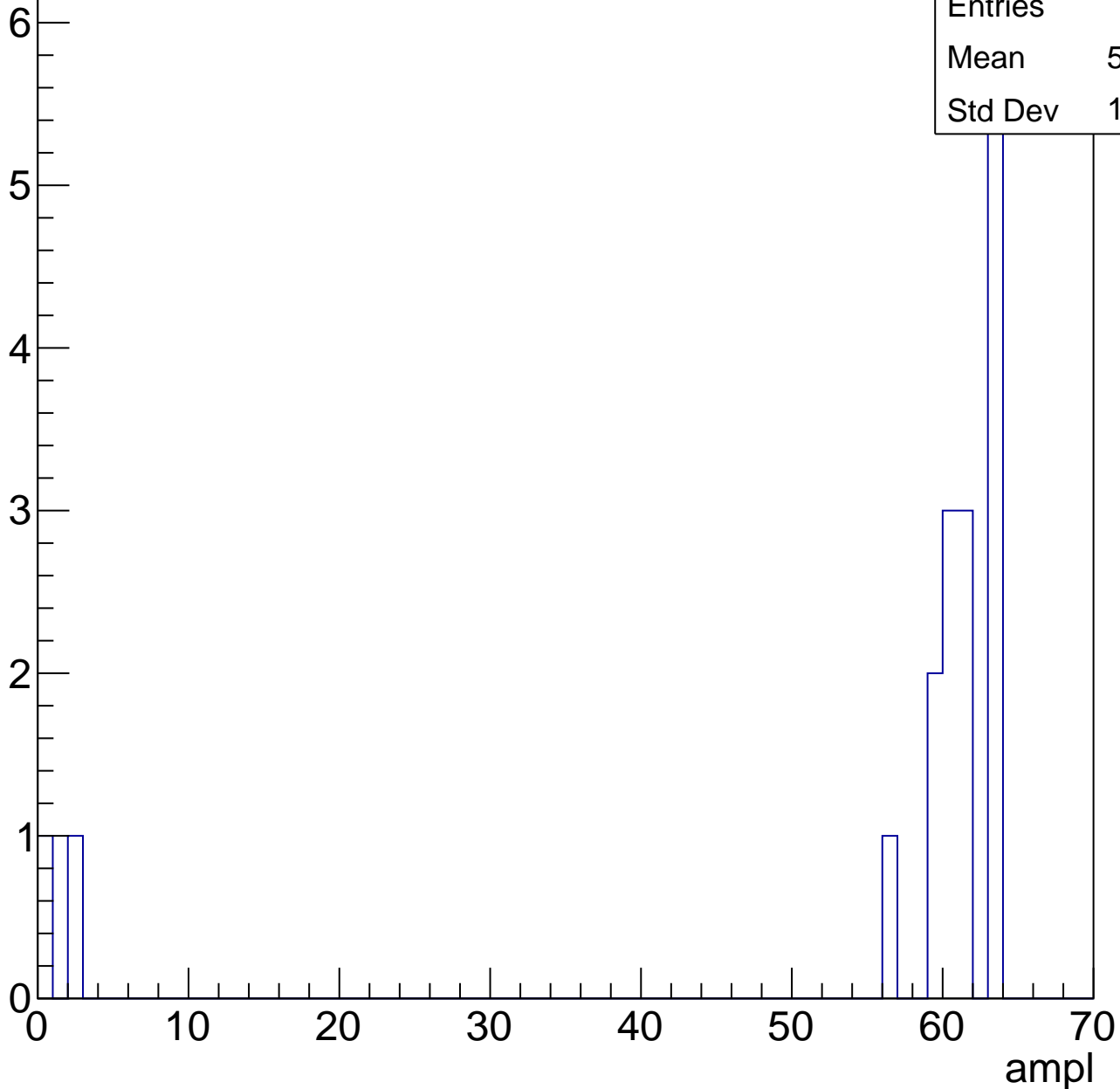


B1L103S, U21-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

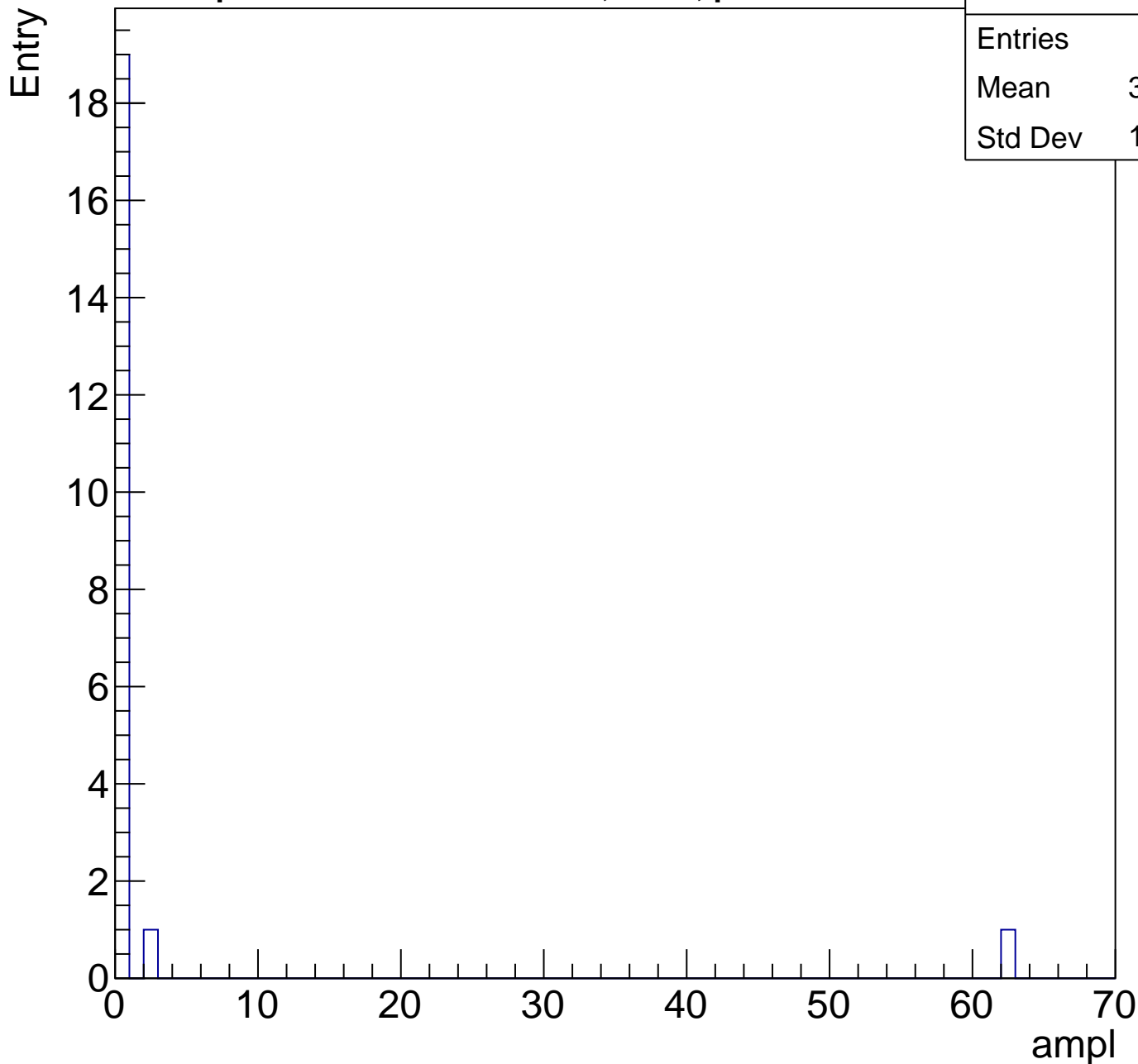
Entries	17
Mean	53.94
Std Dev	19.43



B1L103S, U21-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	3.048
Std Dev	13.19



B1L103S, U21-ch83, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24.11
Std Dev	12.67

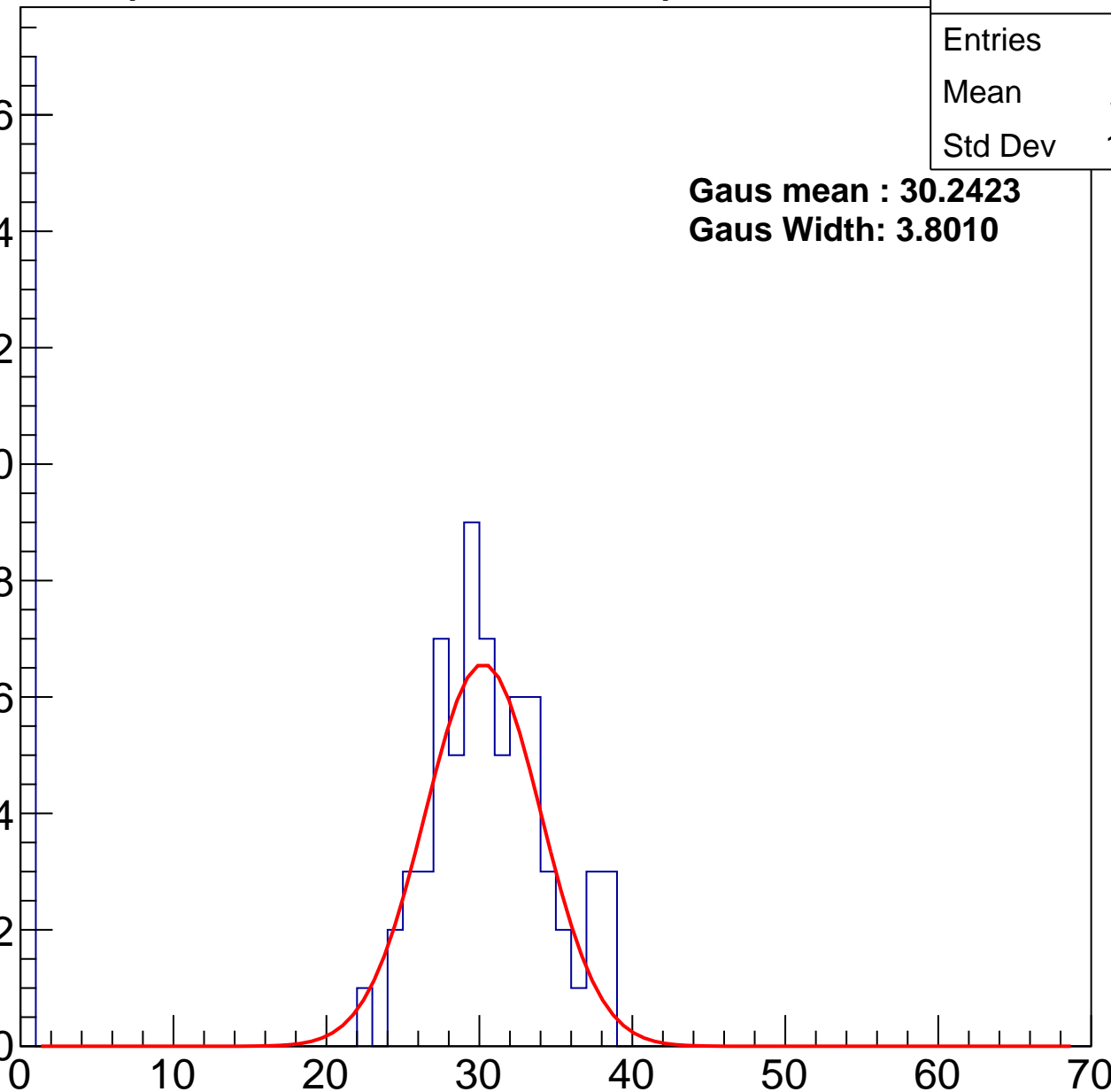
Gaus mean : 30.2423

Gaus Width: 3.8010

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch83, adc1

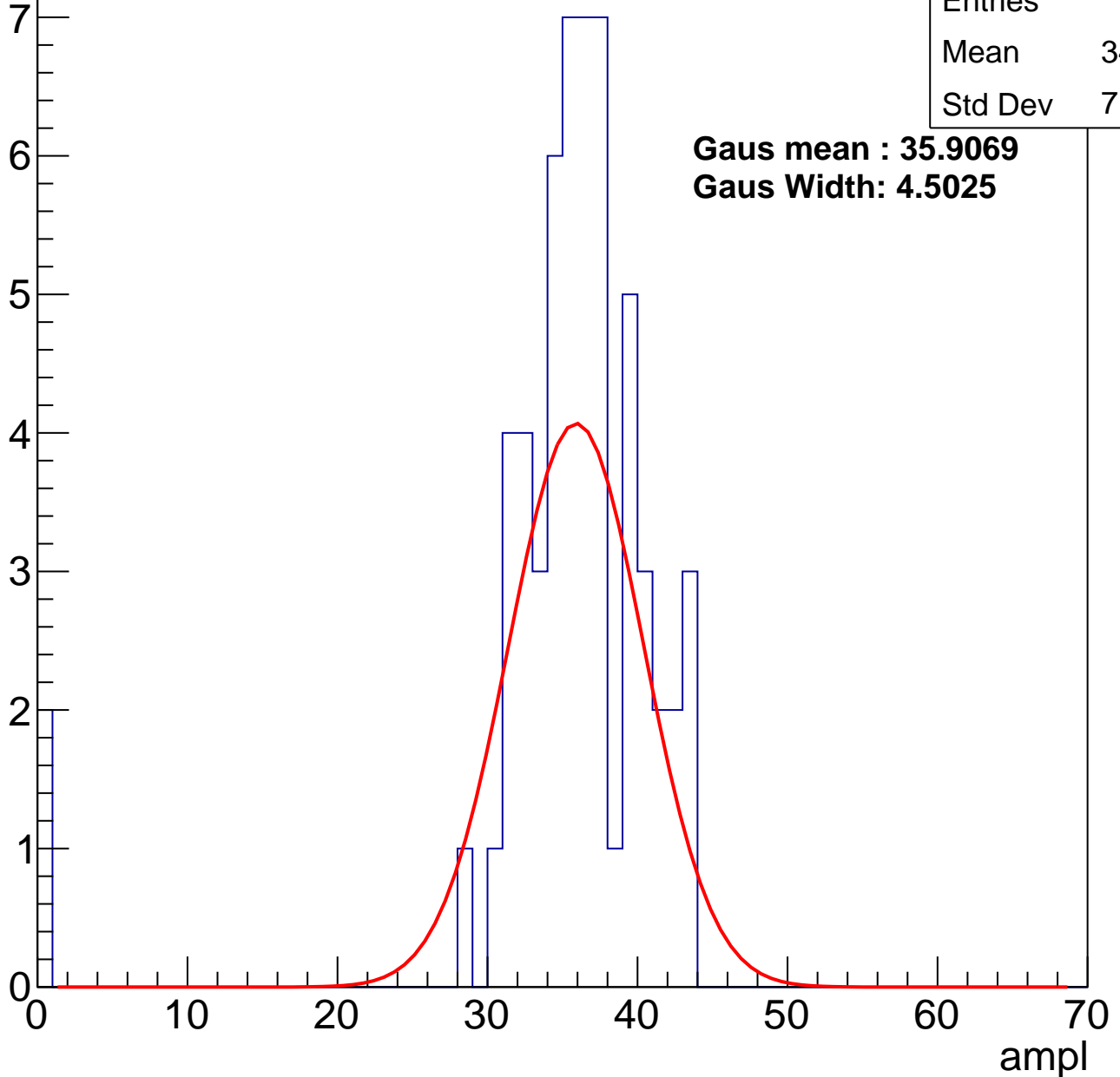
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	34.78
Std Dev	7.428

Gaus mean : 35.9069

Gaus Width: 4.5025



B1L103S, U21-ch83, adc2

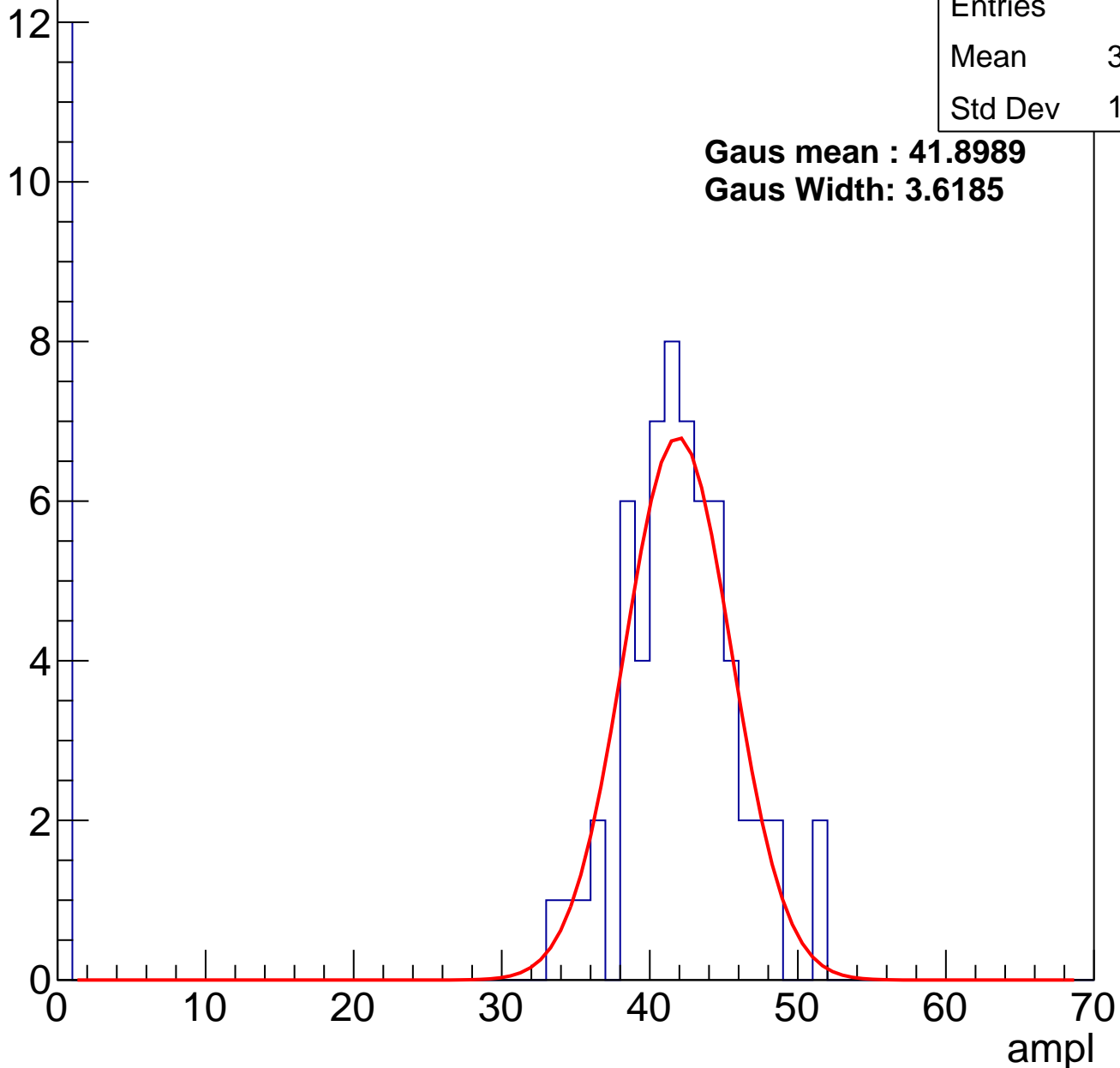
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	34.88
Std Dev	15.82

Gaus mean : 41.8989

Gaus Width: 3.6185

Entry

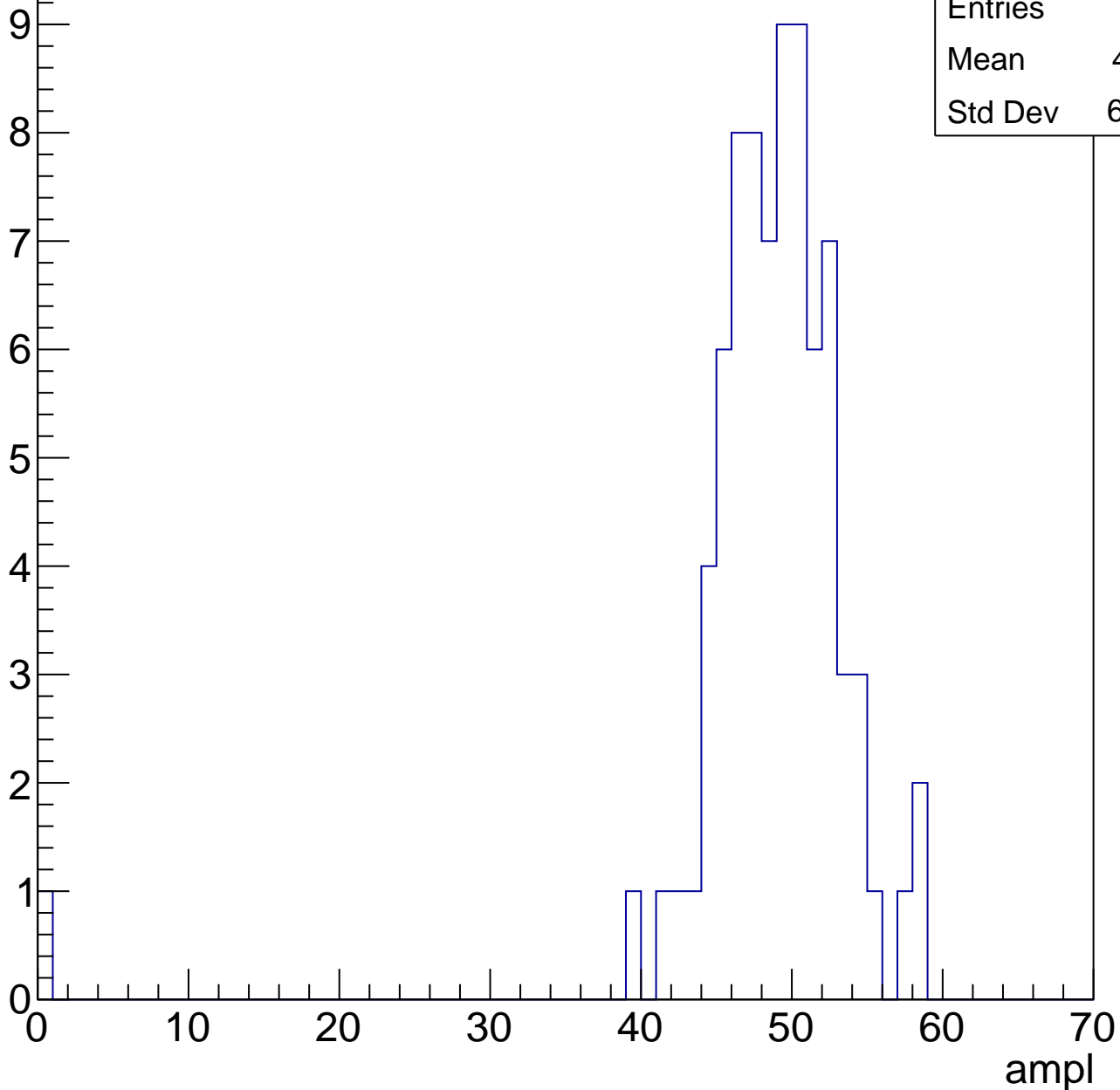


B1L103S, U21-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	48.11
Std Dev	6.537

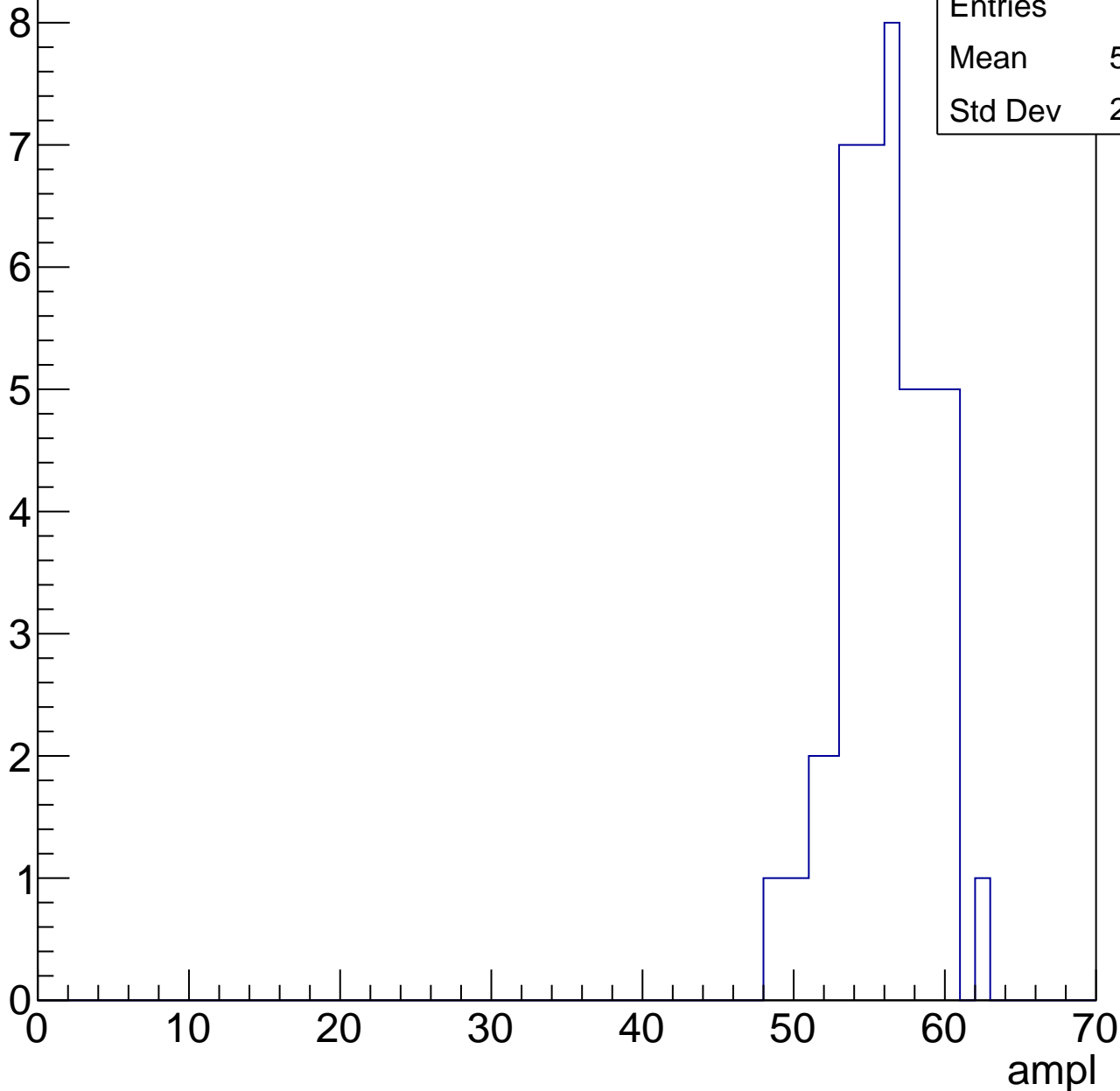


B1L103S, U21-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.56
Std Dev	2.974

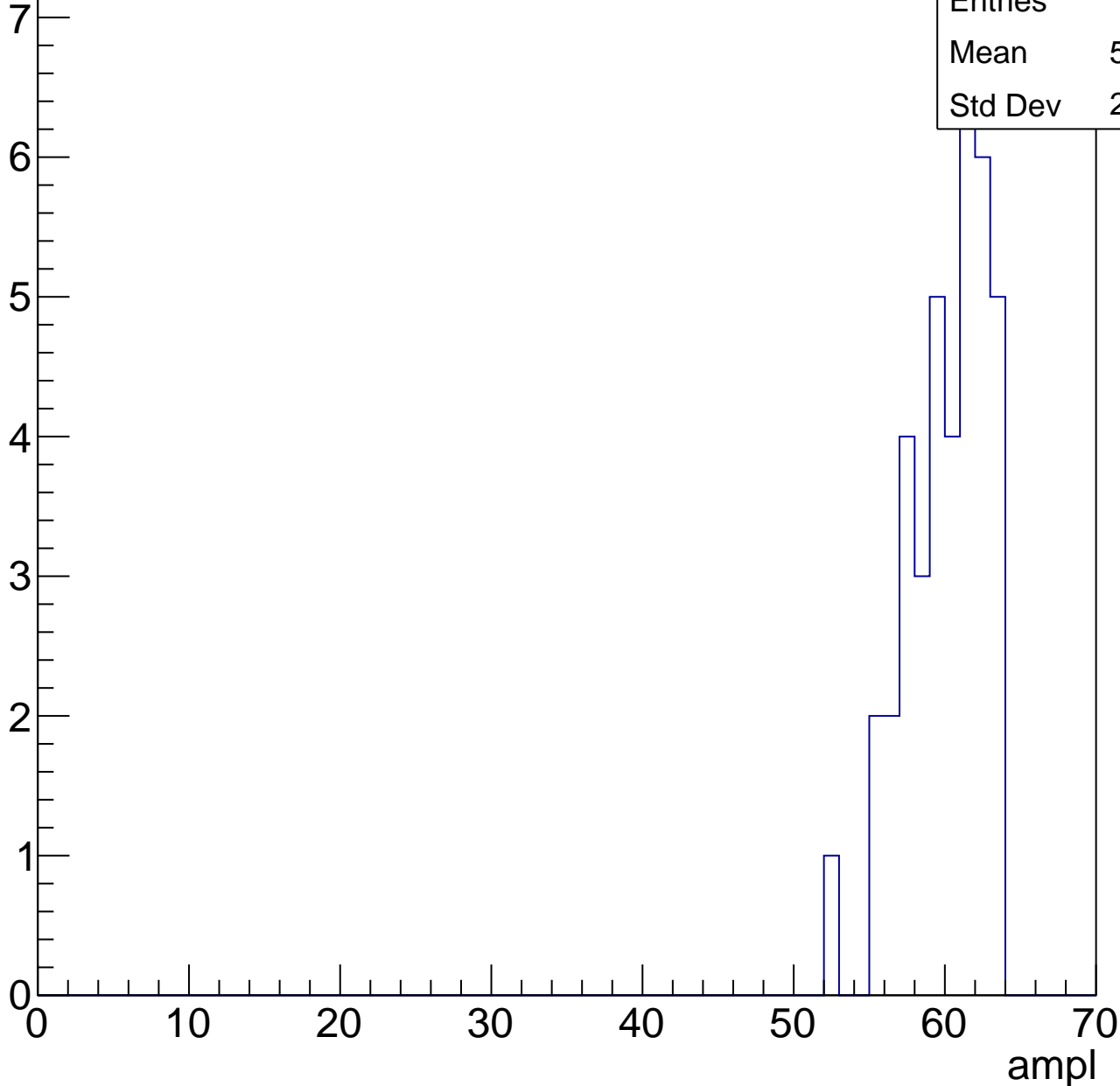


B1L103S, U21-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	59.62
Std Dev	2.627

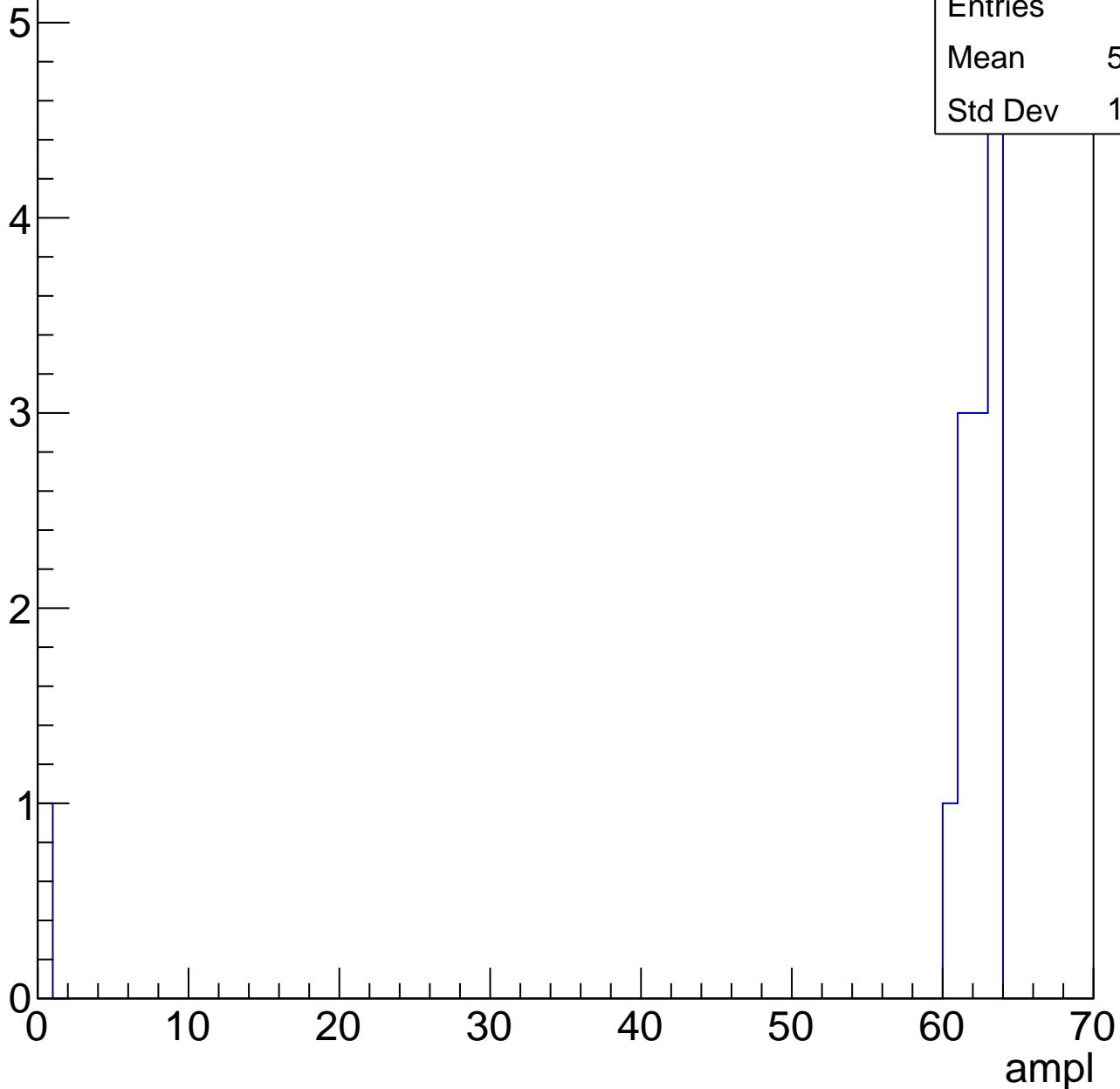


B1L103S, U21-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.23
Std Dev	16.55



B1L103S, U21-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch84, adc0

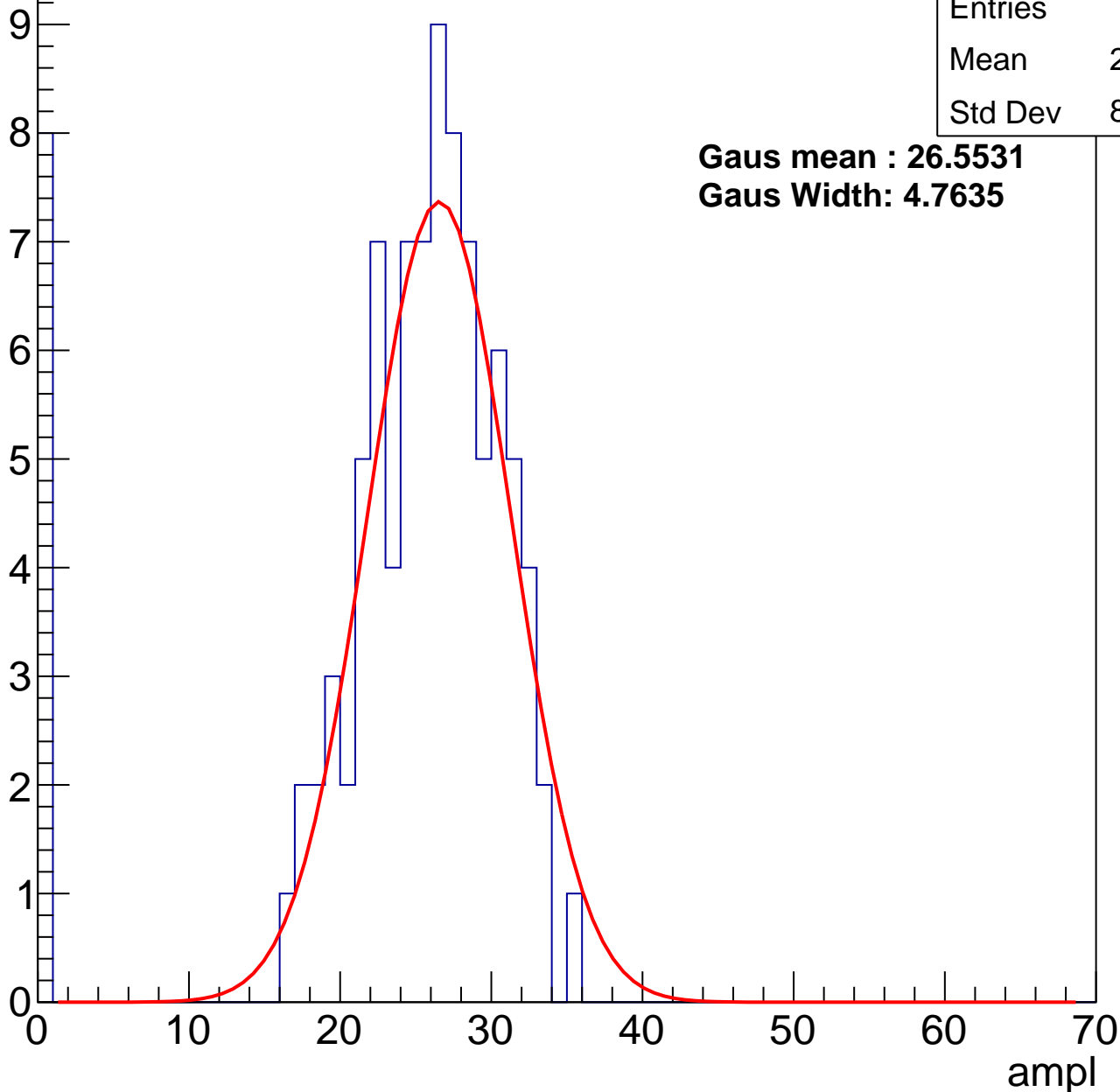
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	95
Mean	23.49
Std Dev	8.178

Gaus mean : 26.5531

Gaus Width: 4.7635



B1L103S, U21-ch84, adc1

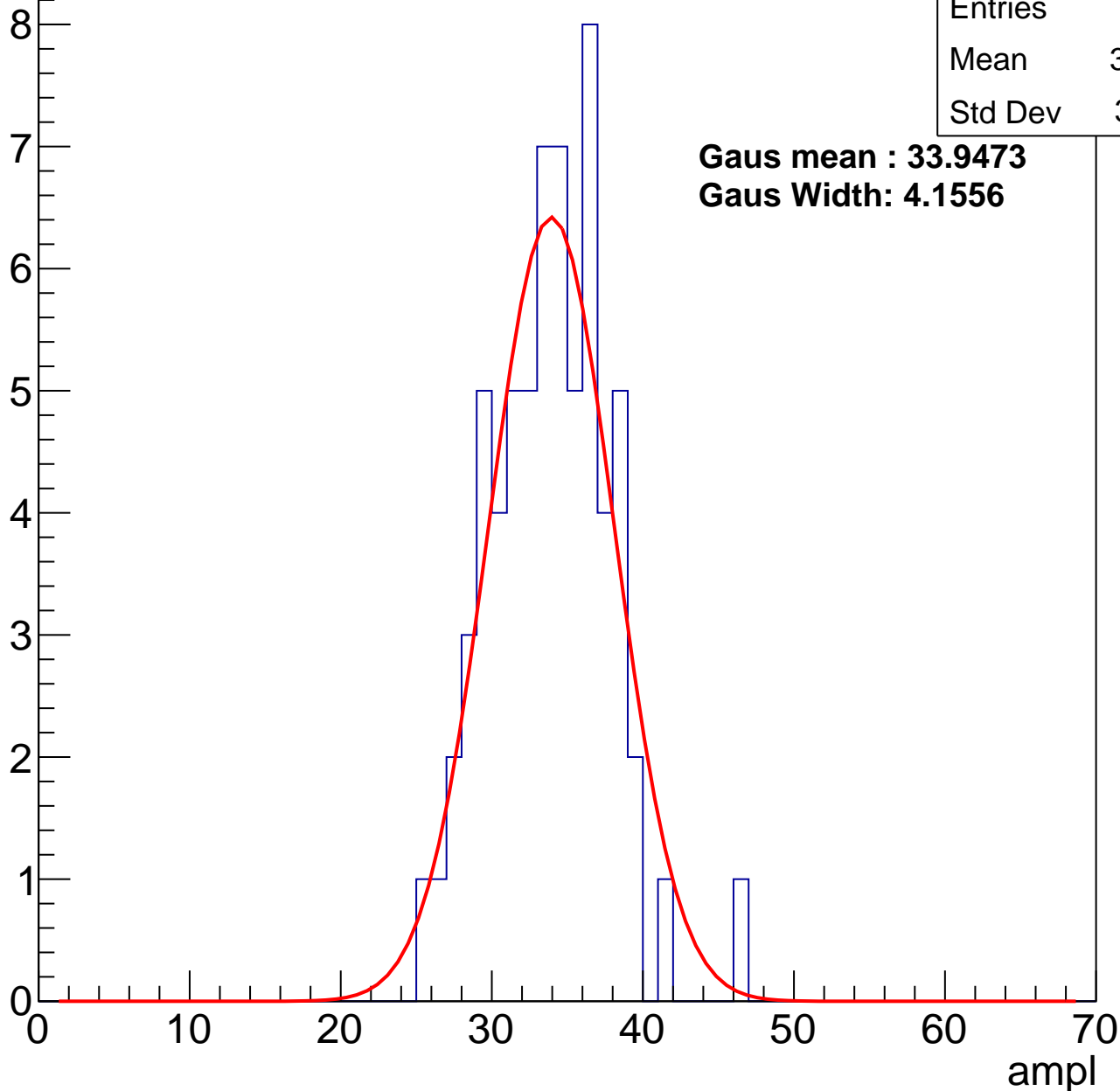
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	33.39
Std Dev	3.861

Gaus mean : 33.9473

Gaus Width: 4.1556



B1L103S, U21-ch84, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	38.95
Std Dev	8.565

Gaus mean : 40.9119

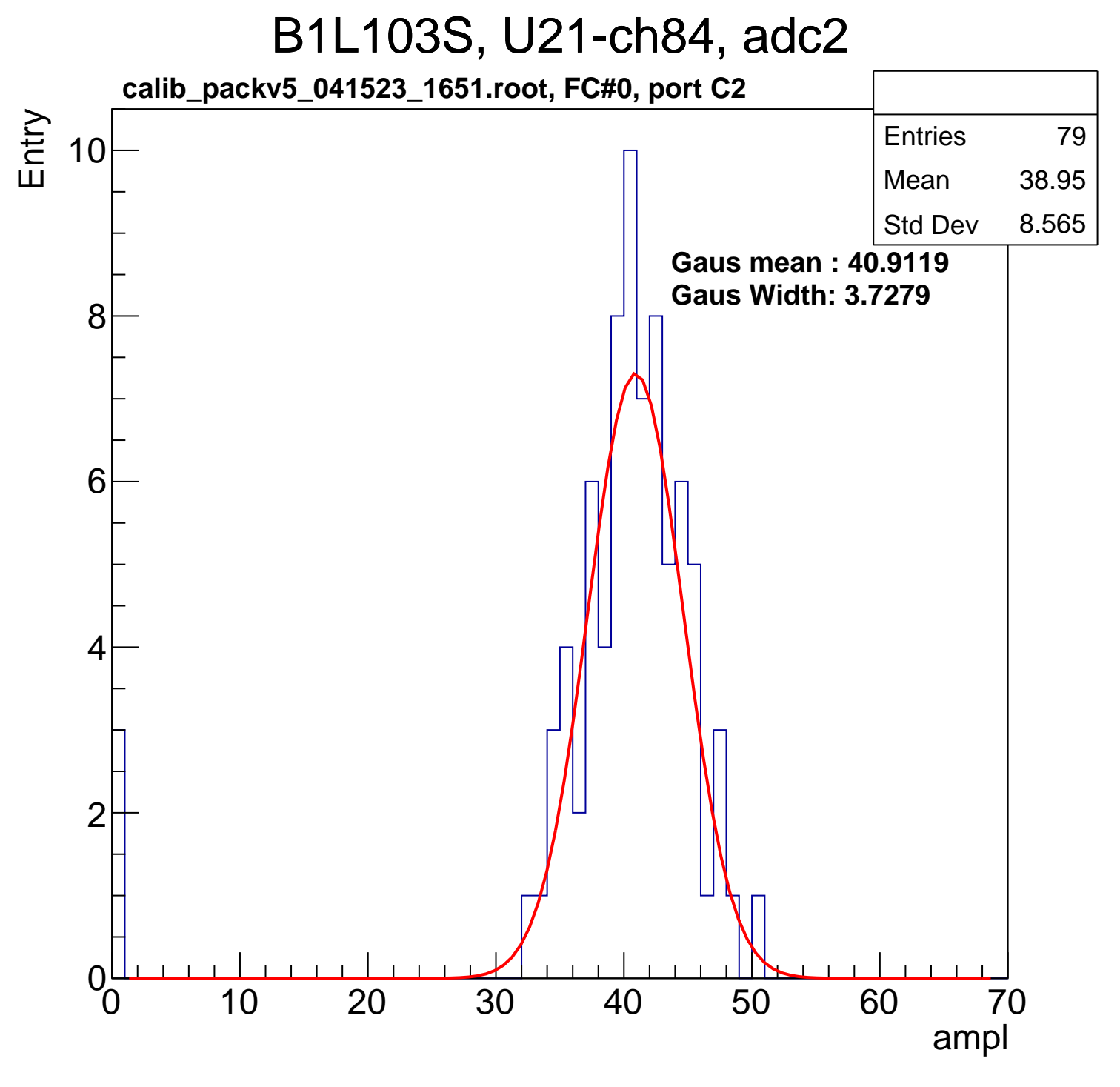
Gaus Width: 3.7279

Entry

10
8
6
4
2
0

ampl

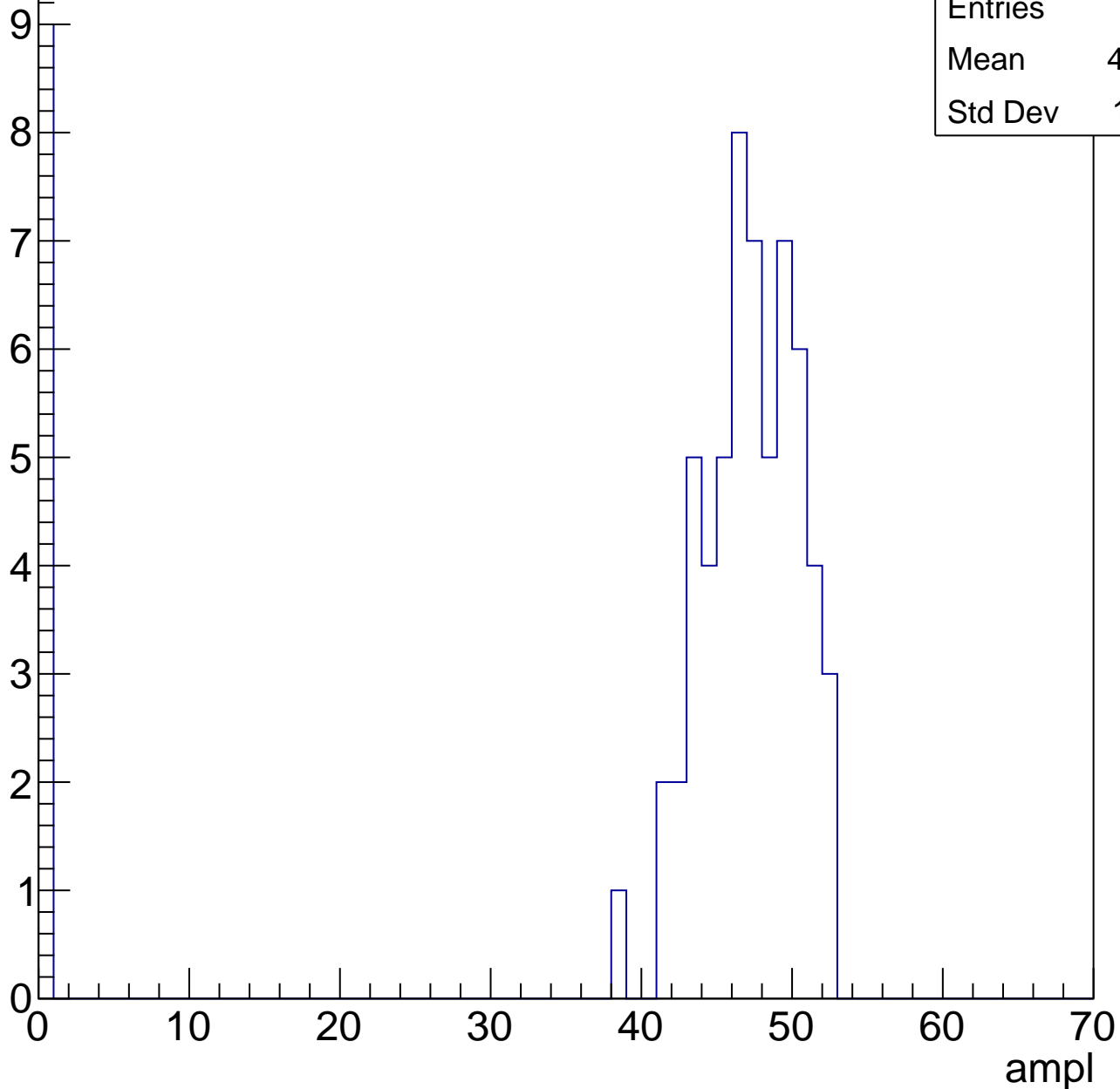
0 10 20 30 40 50 60 70



B1L103S, U21-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

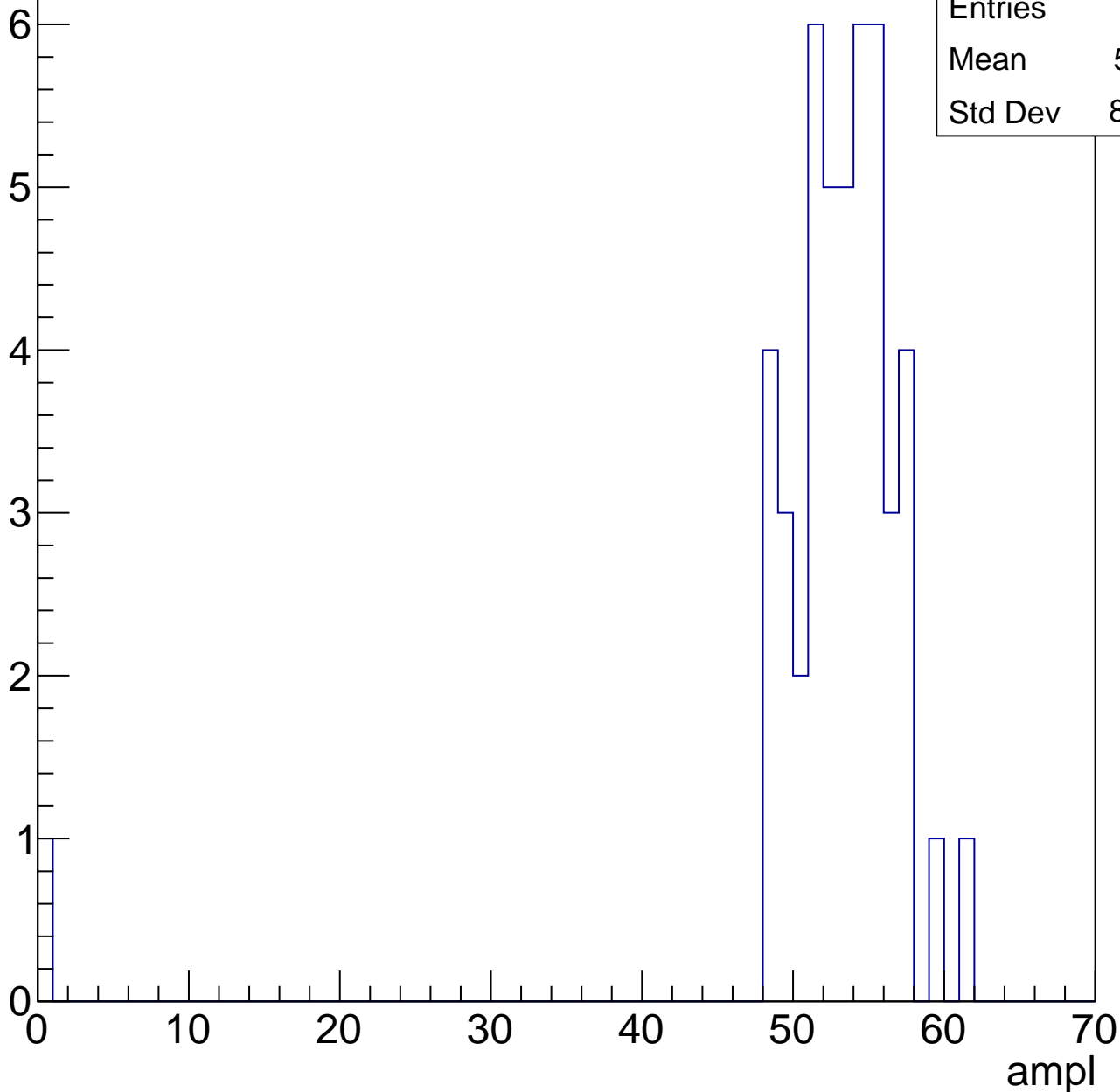


B1L103S, U21-ch84, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

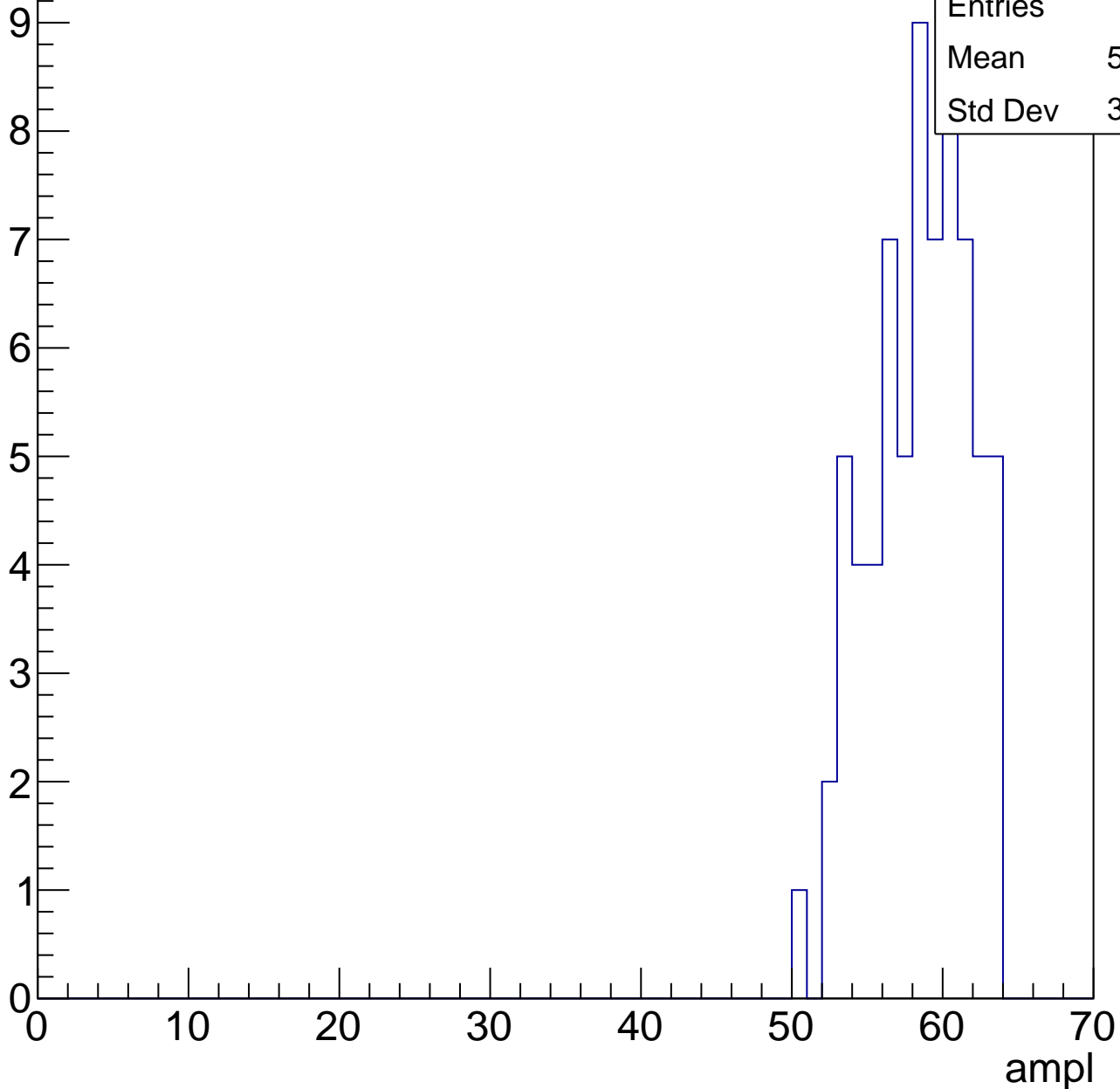
Entries	47
Mean	51.91
Std Dev	8.212



B1L103S, U21-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

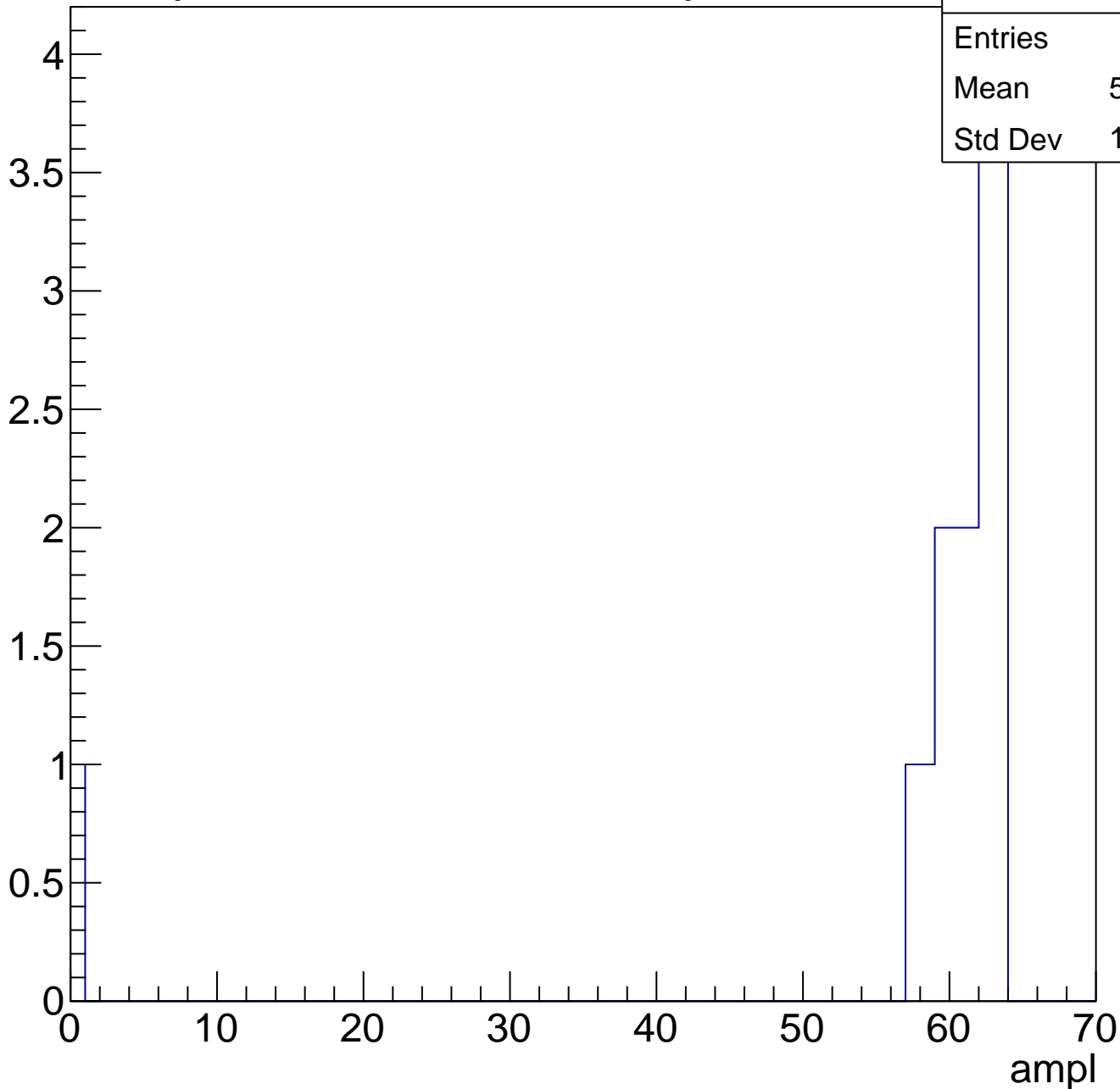
Entry



B1L103S, U21-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	7.05
Std Dev	18.81

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U21-ch85, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	22.24
Std Dev	11.85

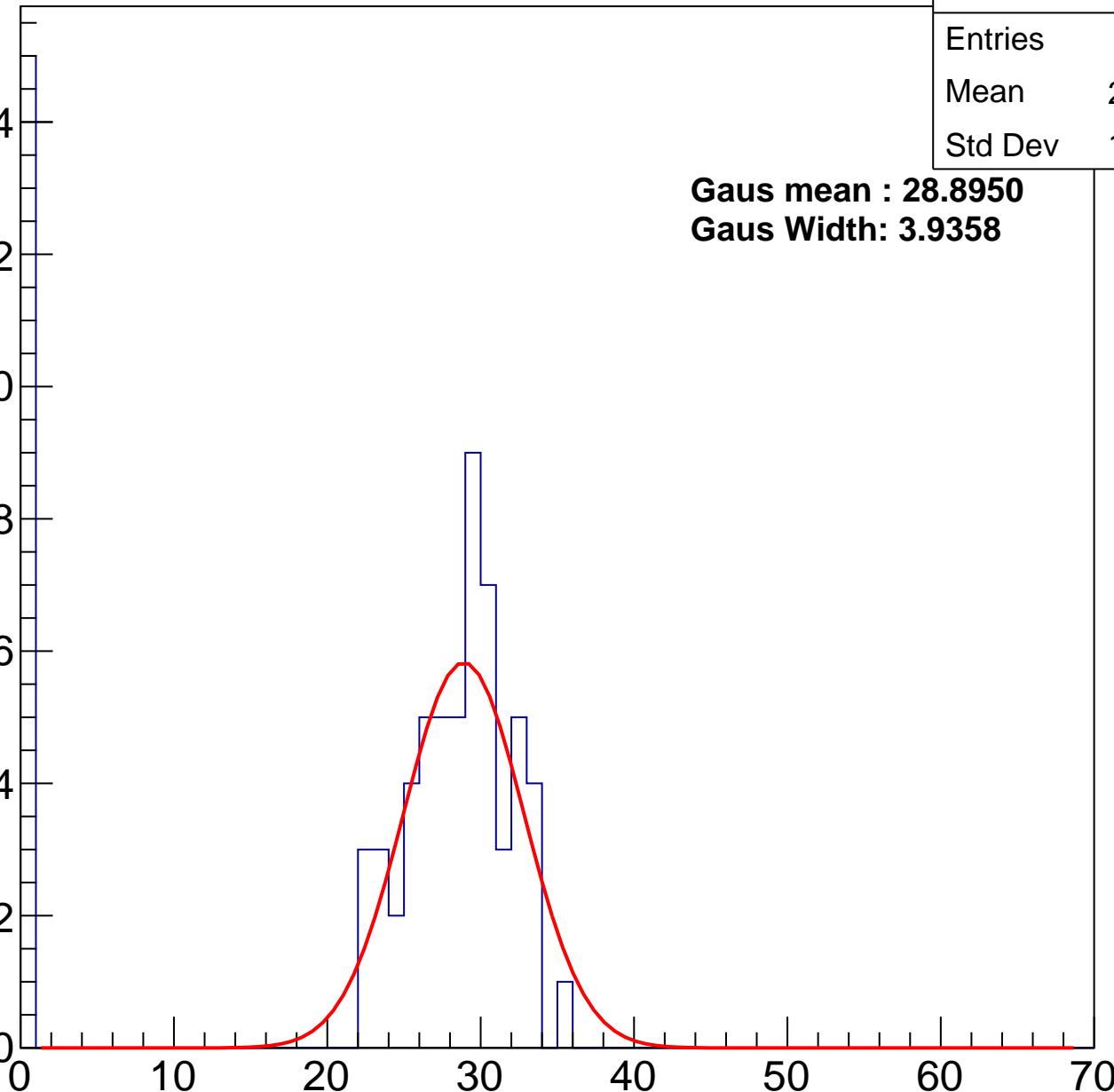
Gaus mean : 28.8950

Gaus Width: 3.9358

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch85, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	35.45
Std Dev	5.407

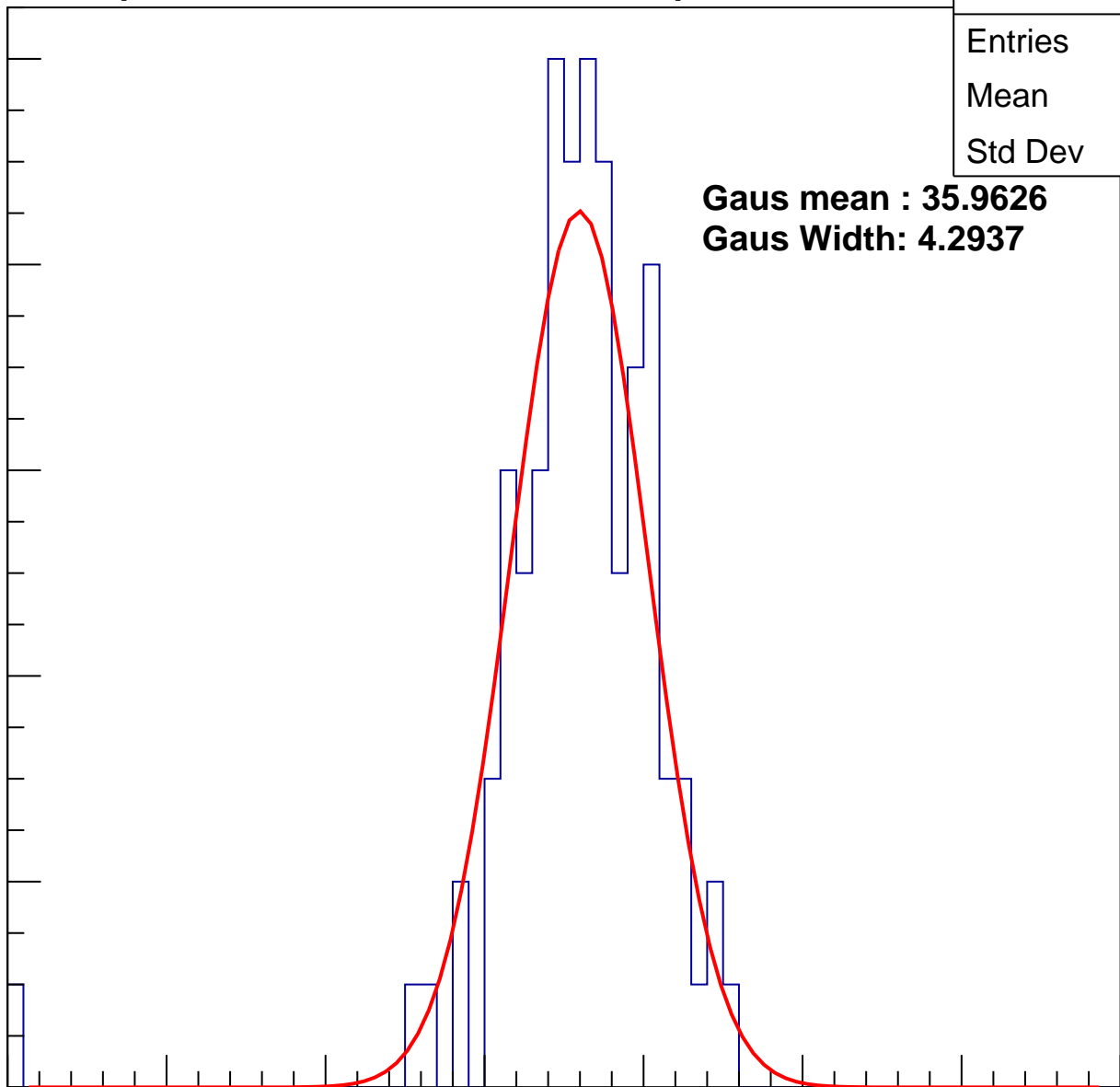
Gaus mean : 35.9626
Gaus Width: 4.2937

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch85, adc2

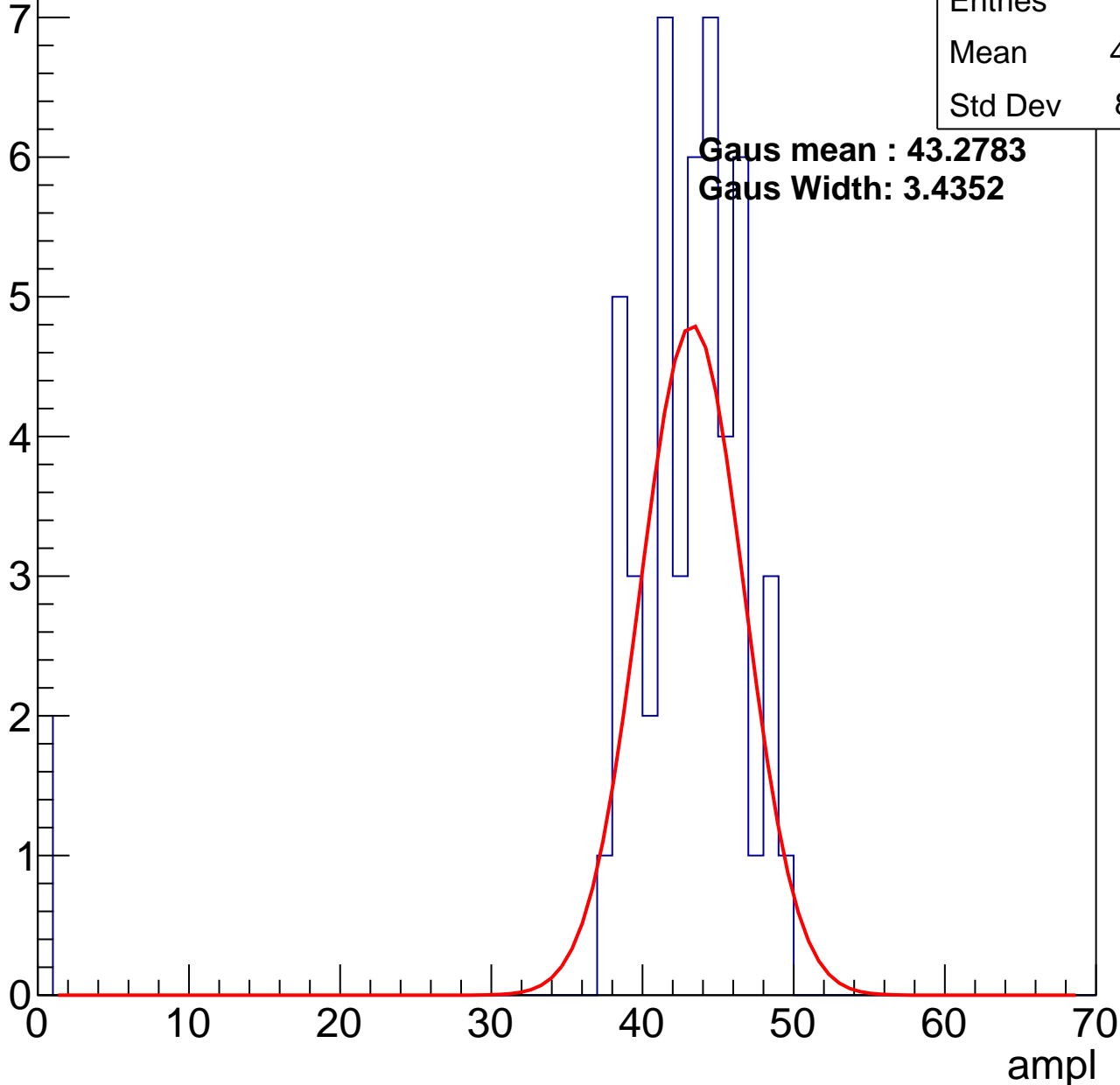
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	41.16
Std Dev	8.841

Gaus mean : 43.2783

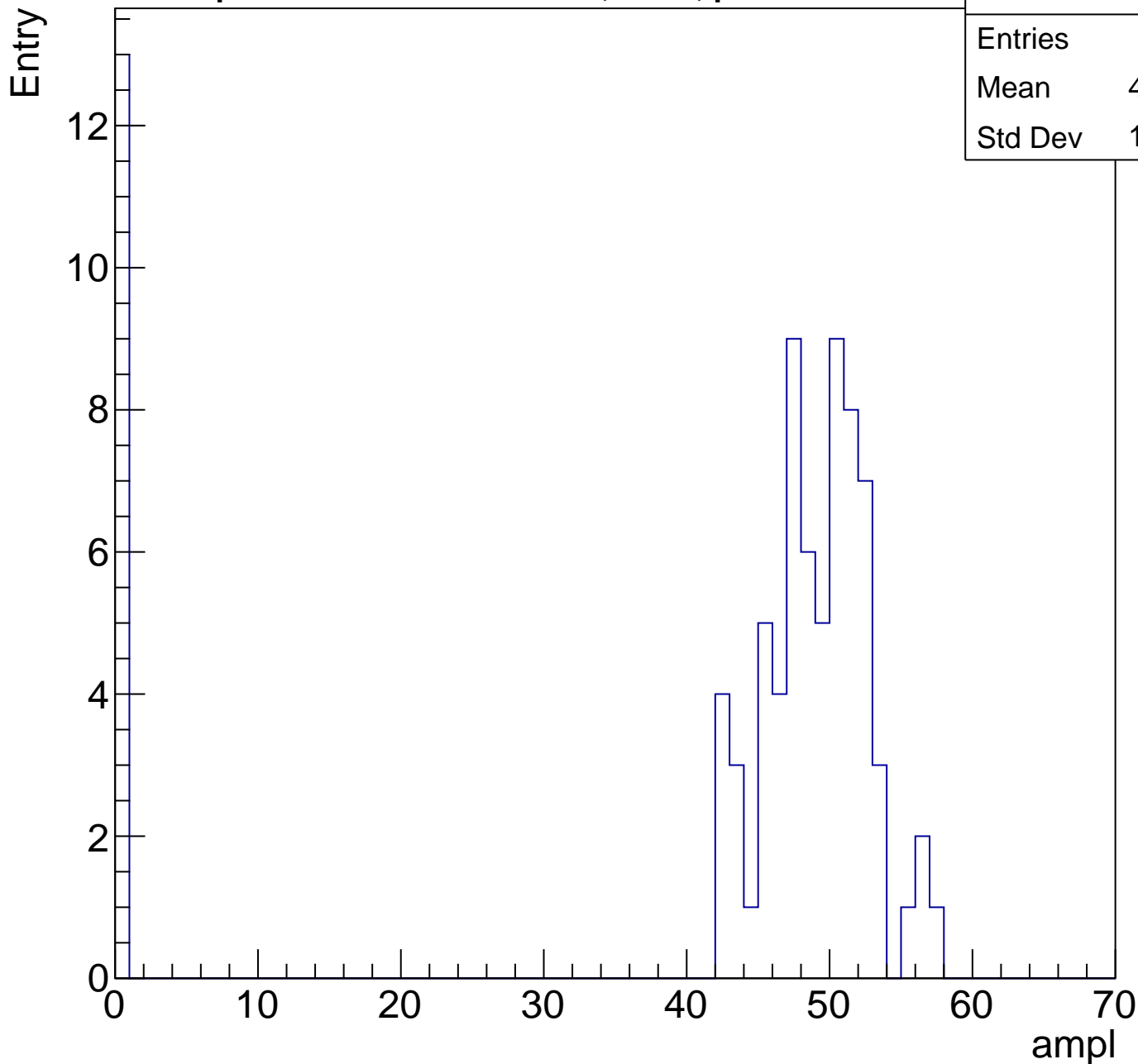
Gaus Width: 3.4352



B1L103S, U21-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	40.88
Std Dev	18.16

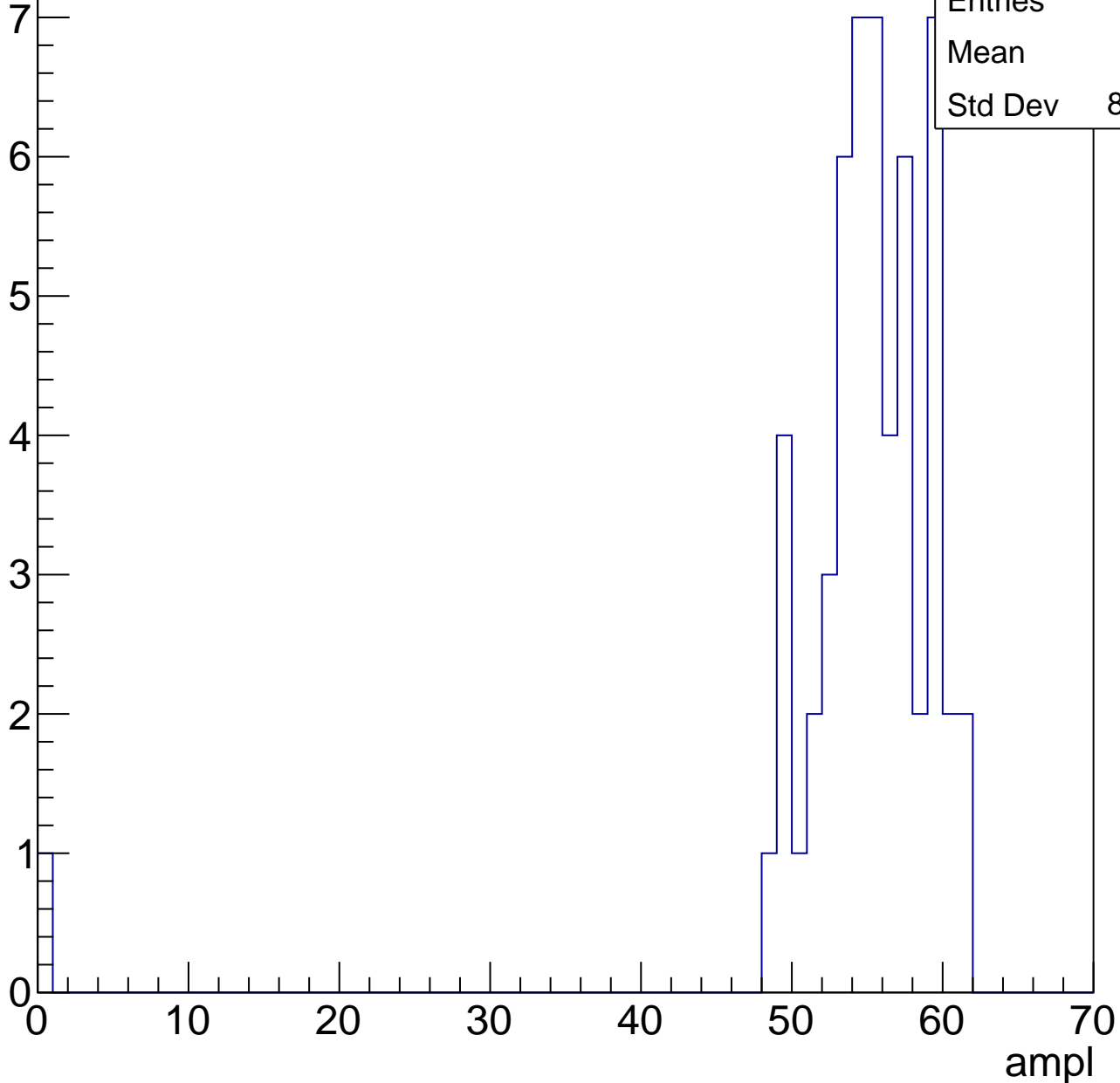


B1L103S, U21-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54
Std Dev	8.043

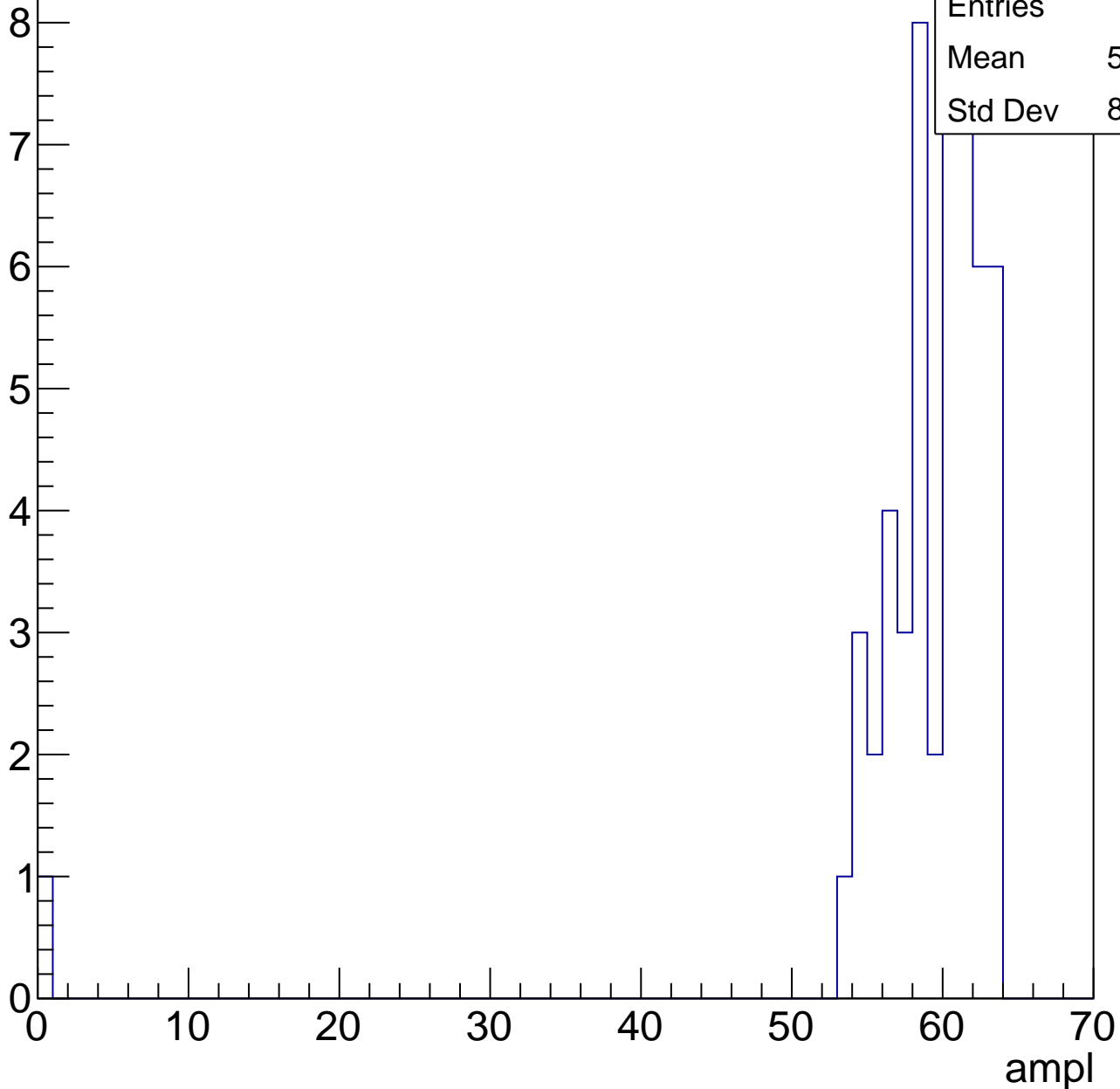


B1L103S, U21-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

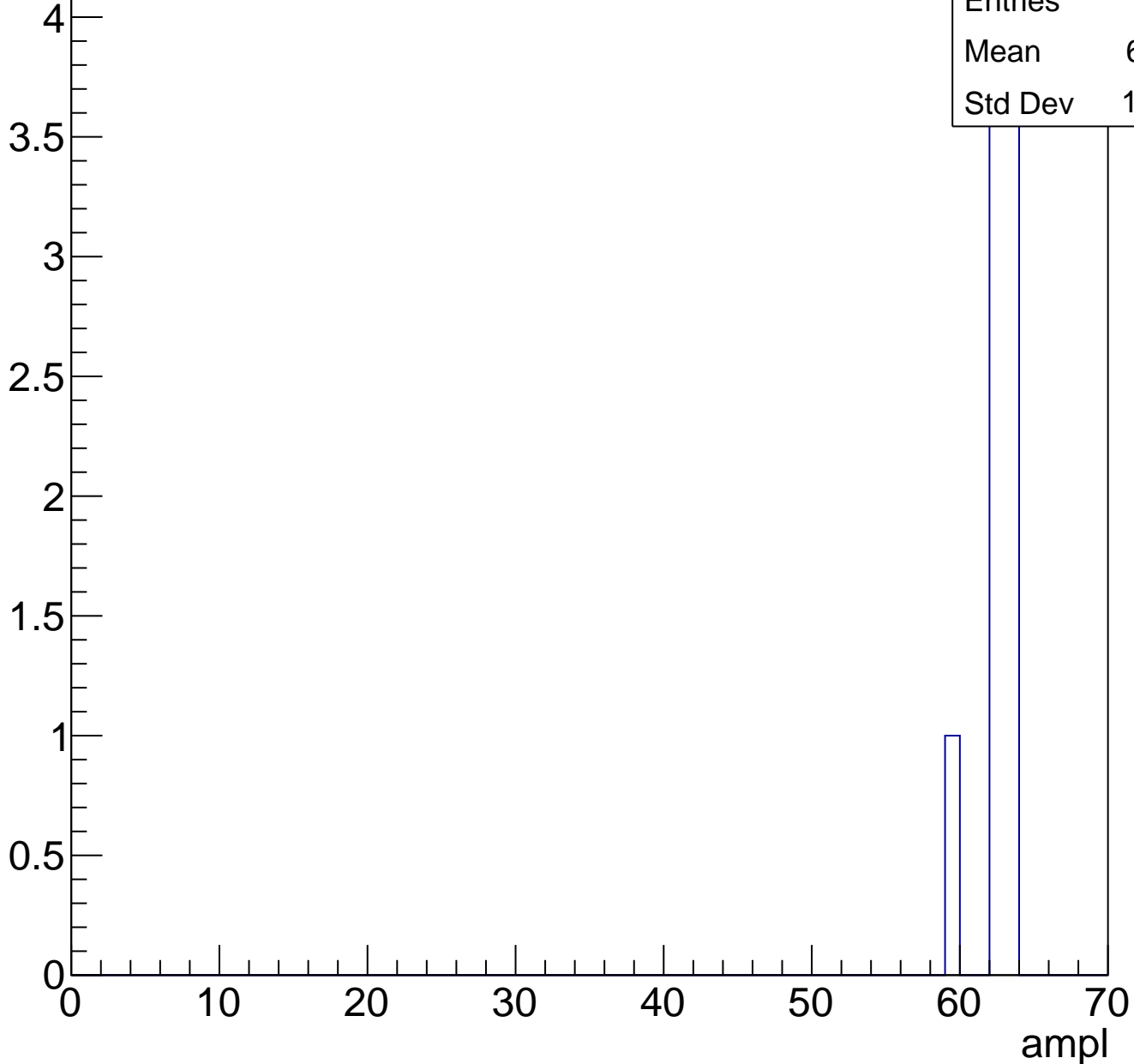
Entries	52
Mean	58.08
Std Dev	8.577



B1L103S, U21-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

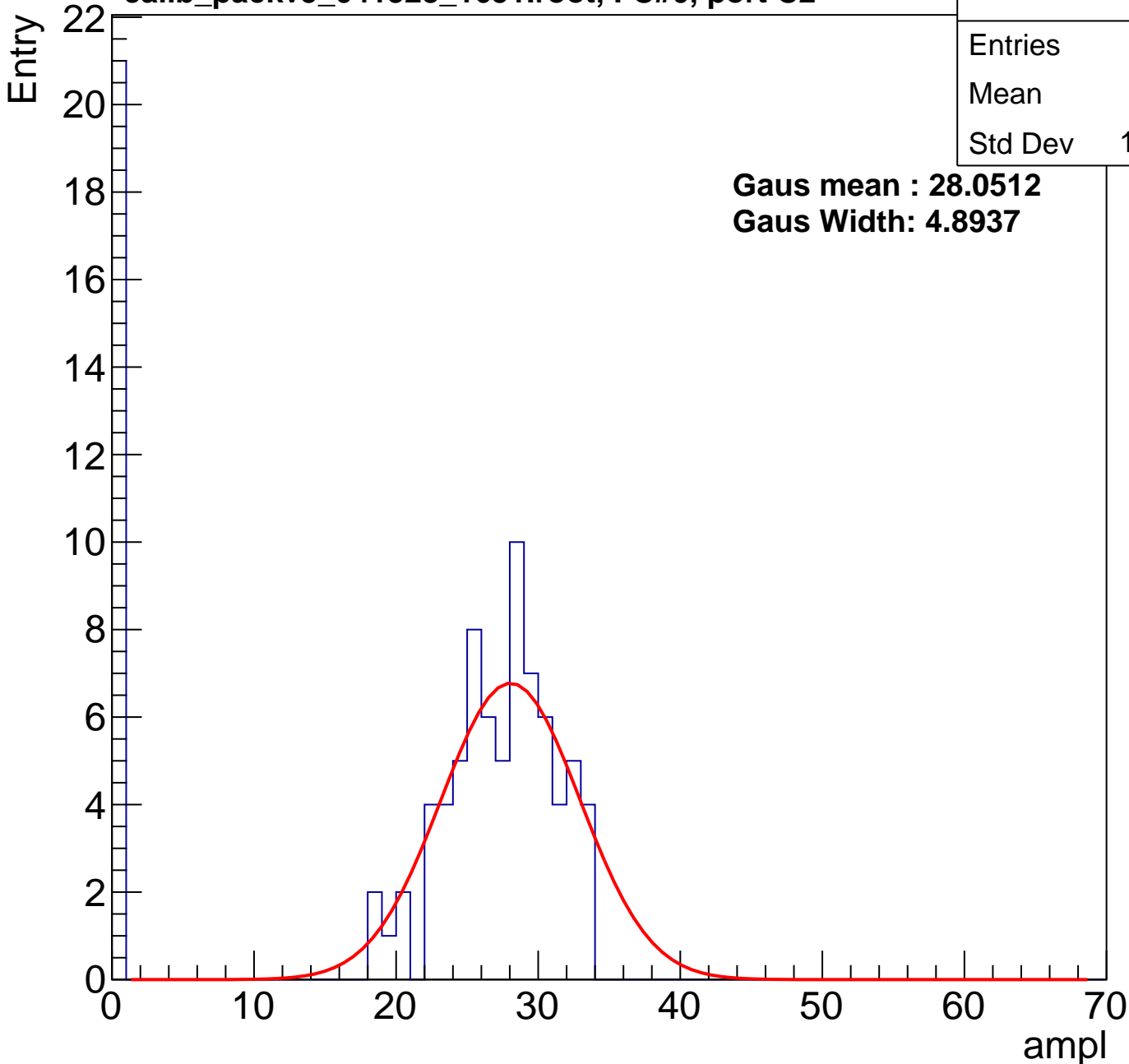
B1L103S, U21-ch86, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	20.9
Std Dev	11.67

Gaus mean : 28.0512

Gaus Width: 4.8937



B1L103S, U21-ch86, adc1

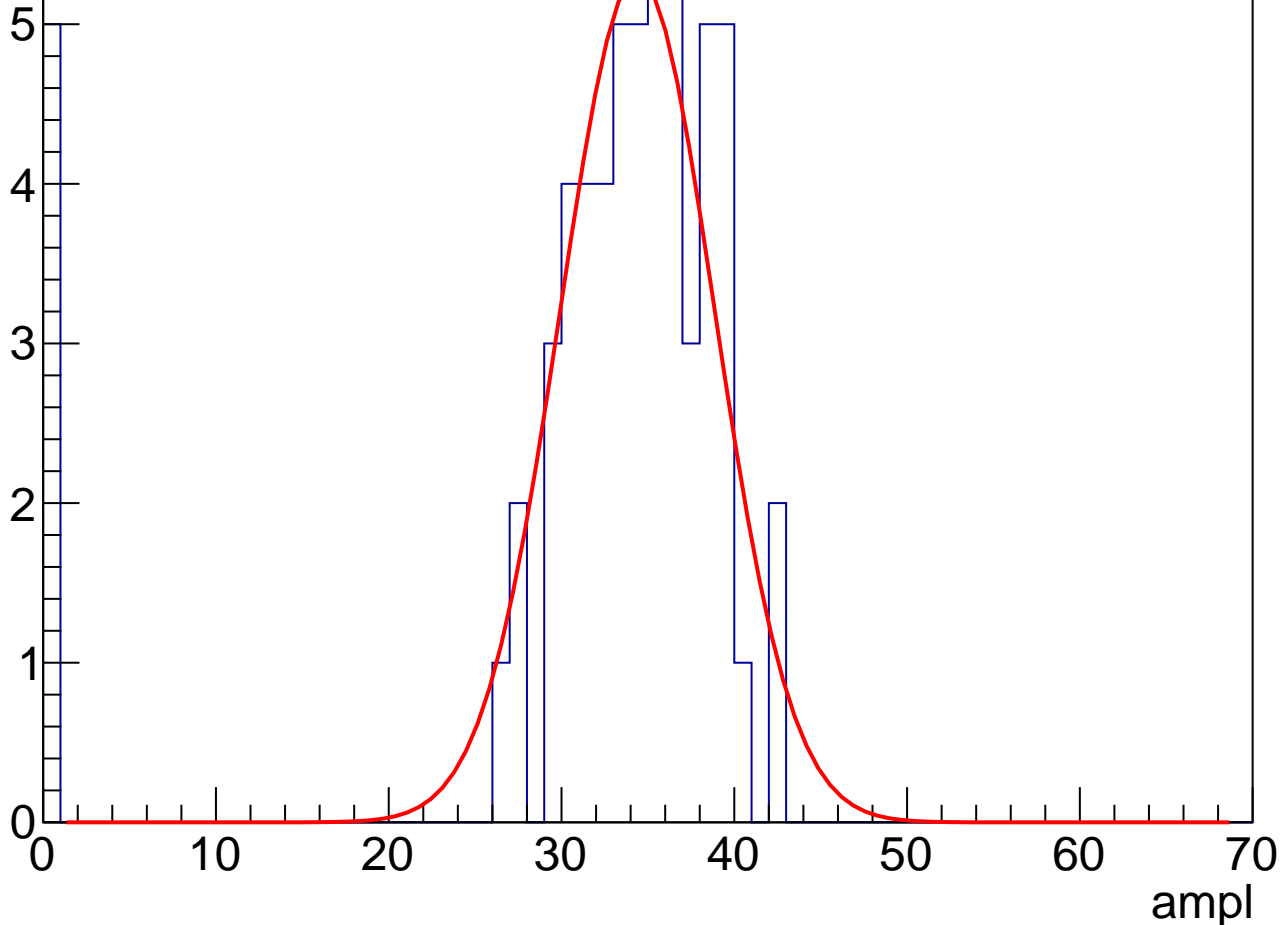
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	31.55
Std Dev	9.996

Gaus mean : 34.3961

Gaus Width: 4.4636



B1L103S, U21-ch86, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	34.76
Std Dev	14.06

Gaus mean : 40.7301

Gaus Width: 4.6812

Entry

10

8

6

4

2

0

0

10

20

30

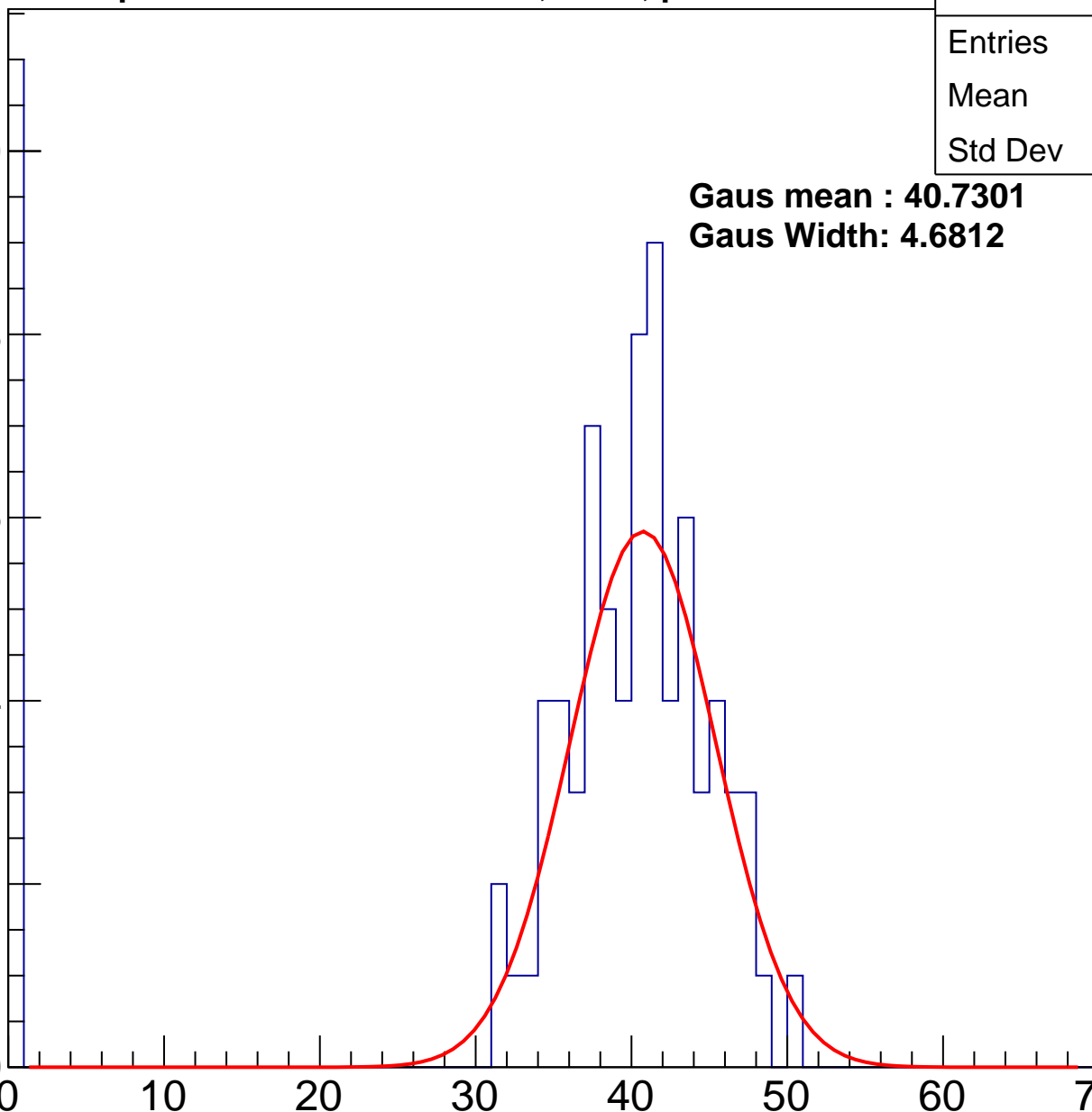
40

50

60

70

ampl

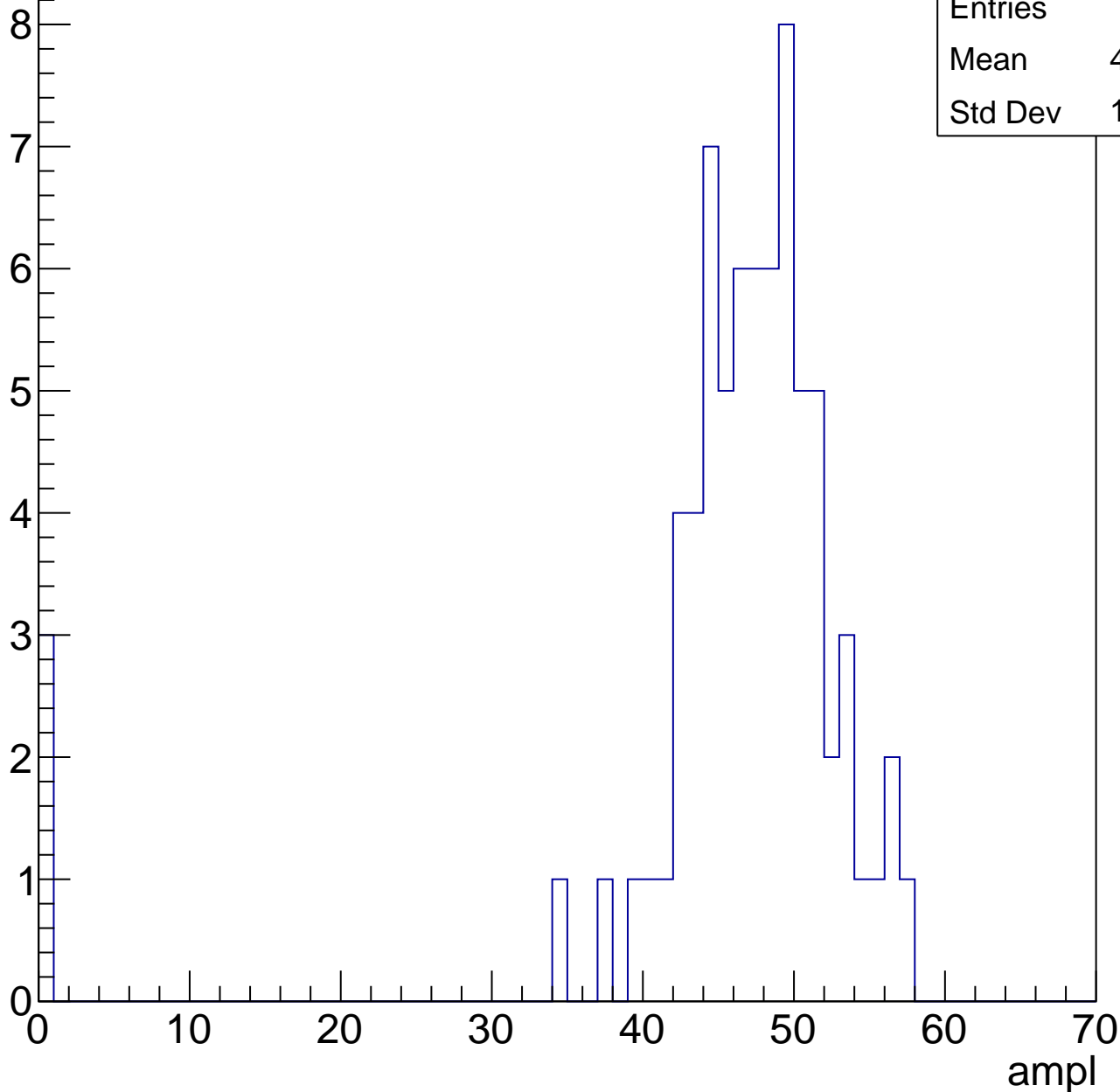


B1L103S, U21-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	45.24
Std Dev	10.24

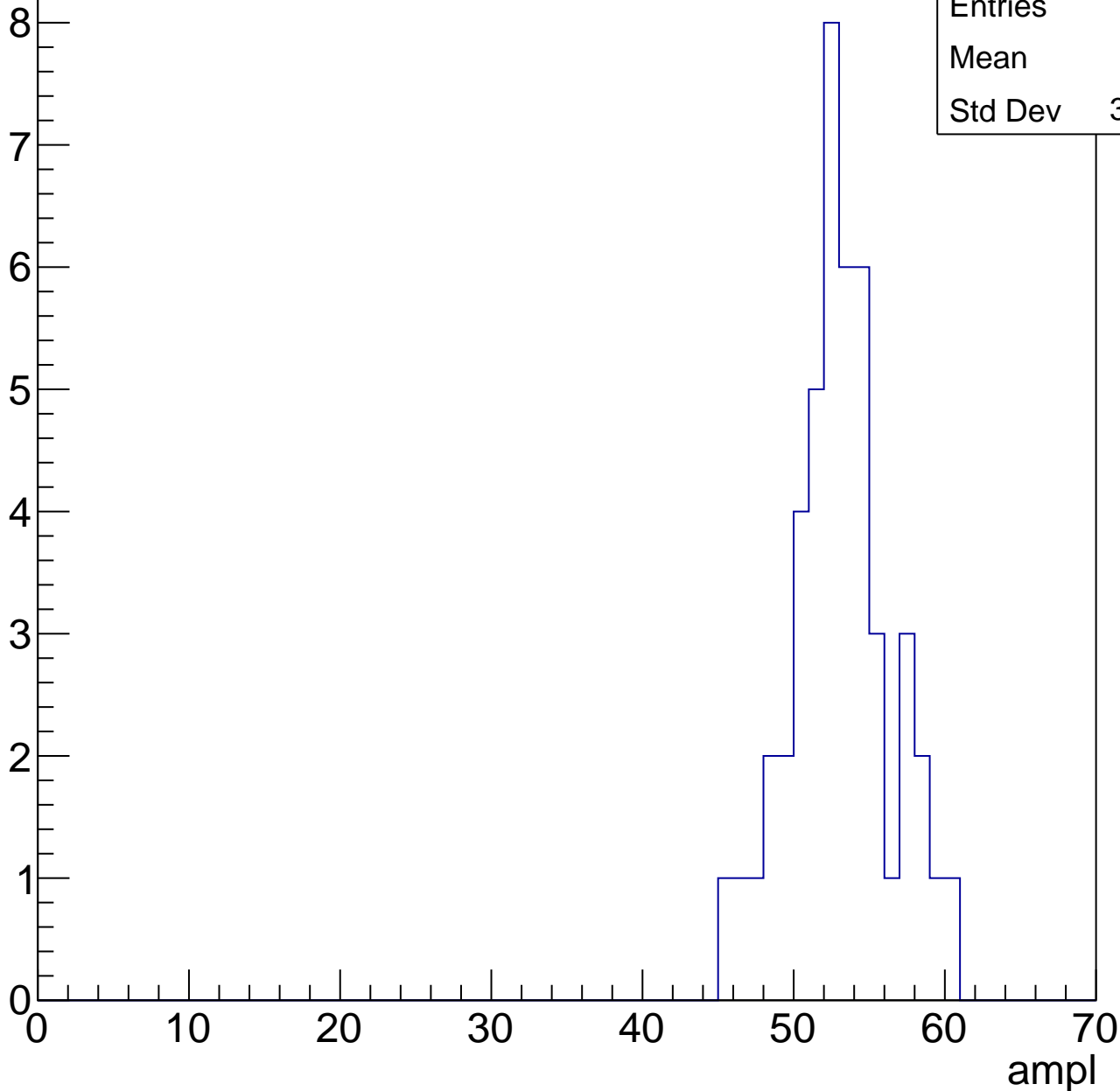


B1L103S, U21-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	52.6
Std Dev	3.253

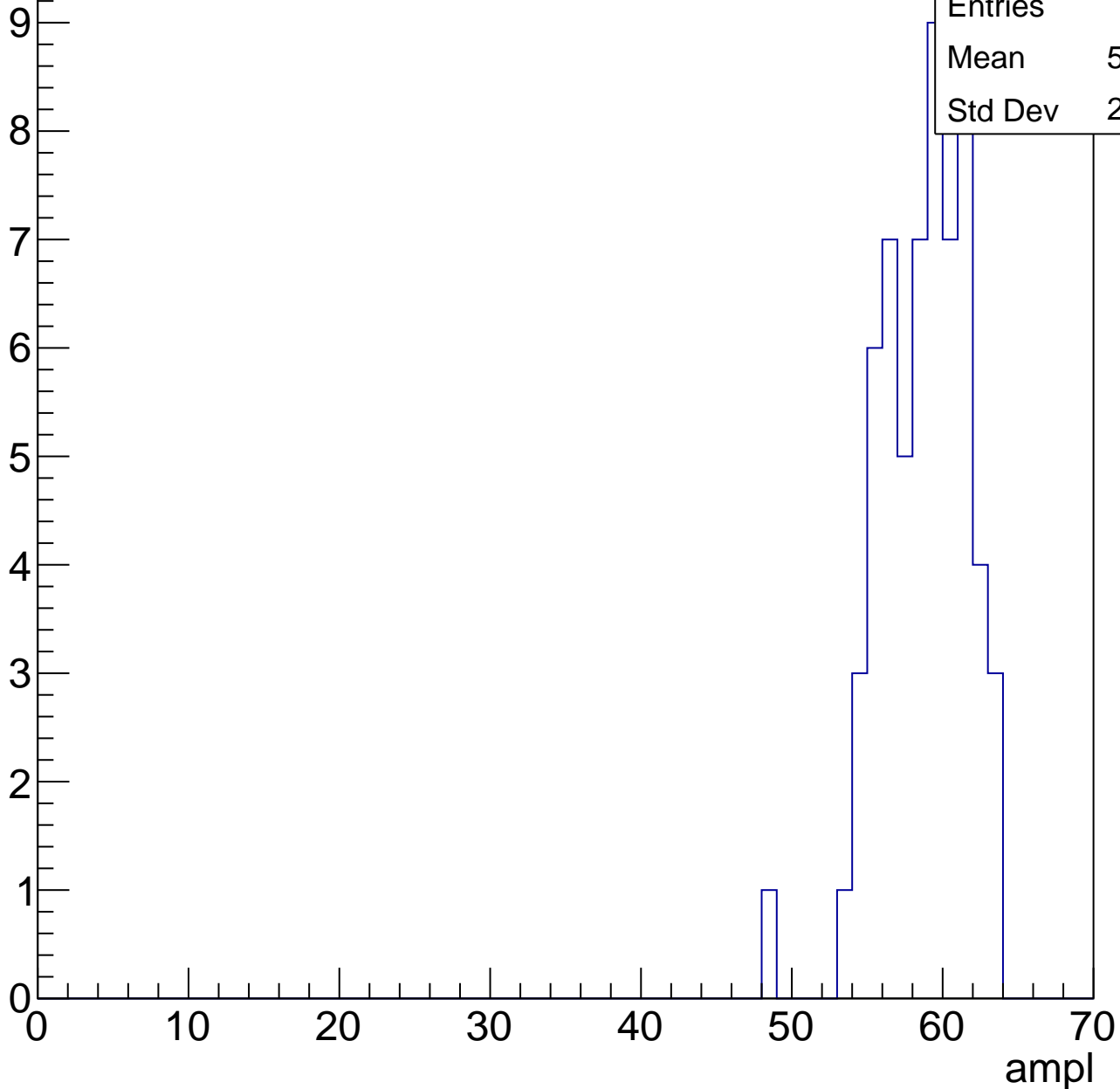


B1L103S, U21-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	58.23
Std Dev	2.877

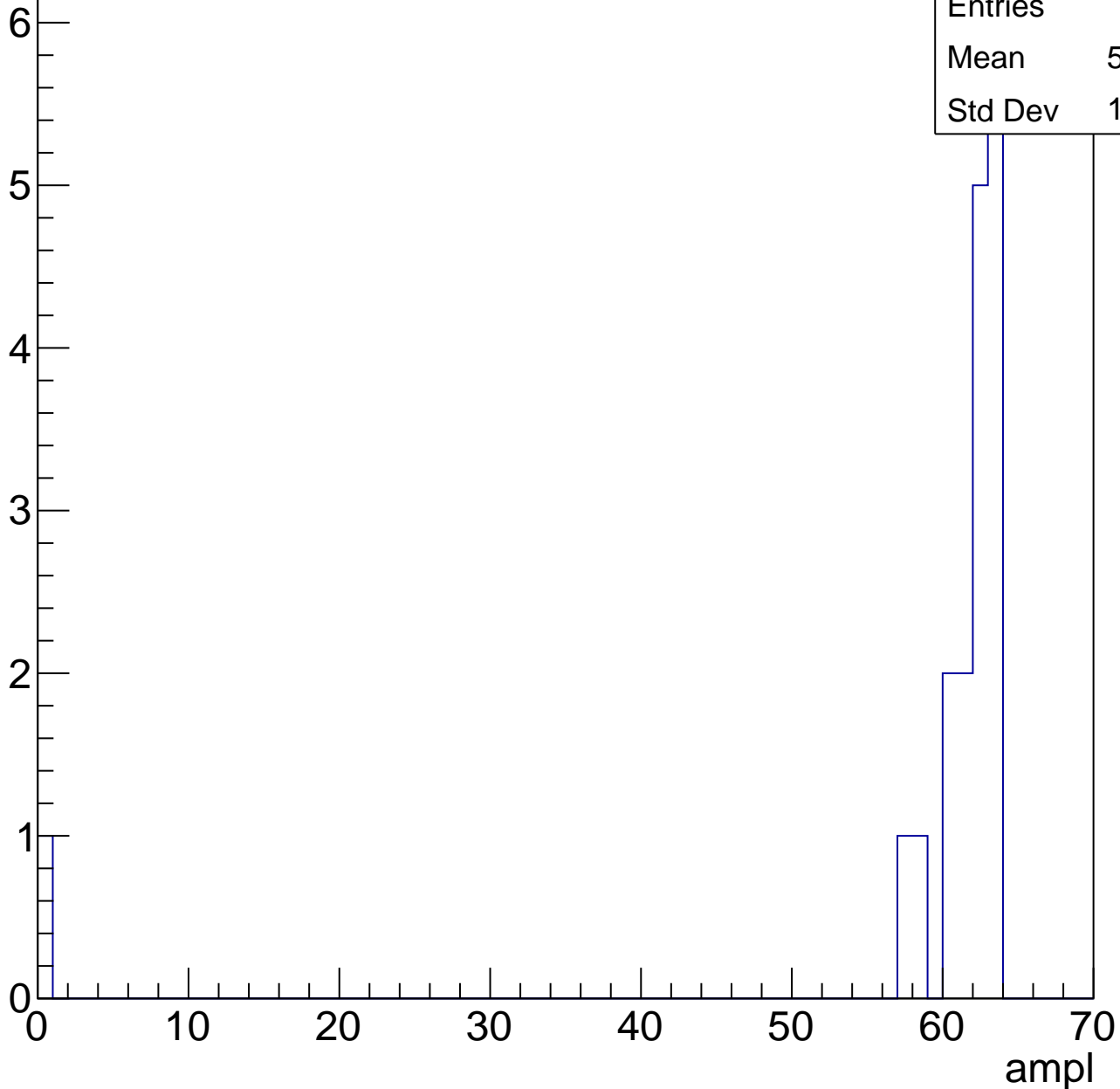


B1L103S, U21-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	58.06
Std Dev	14.18

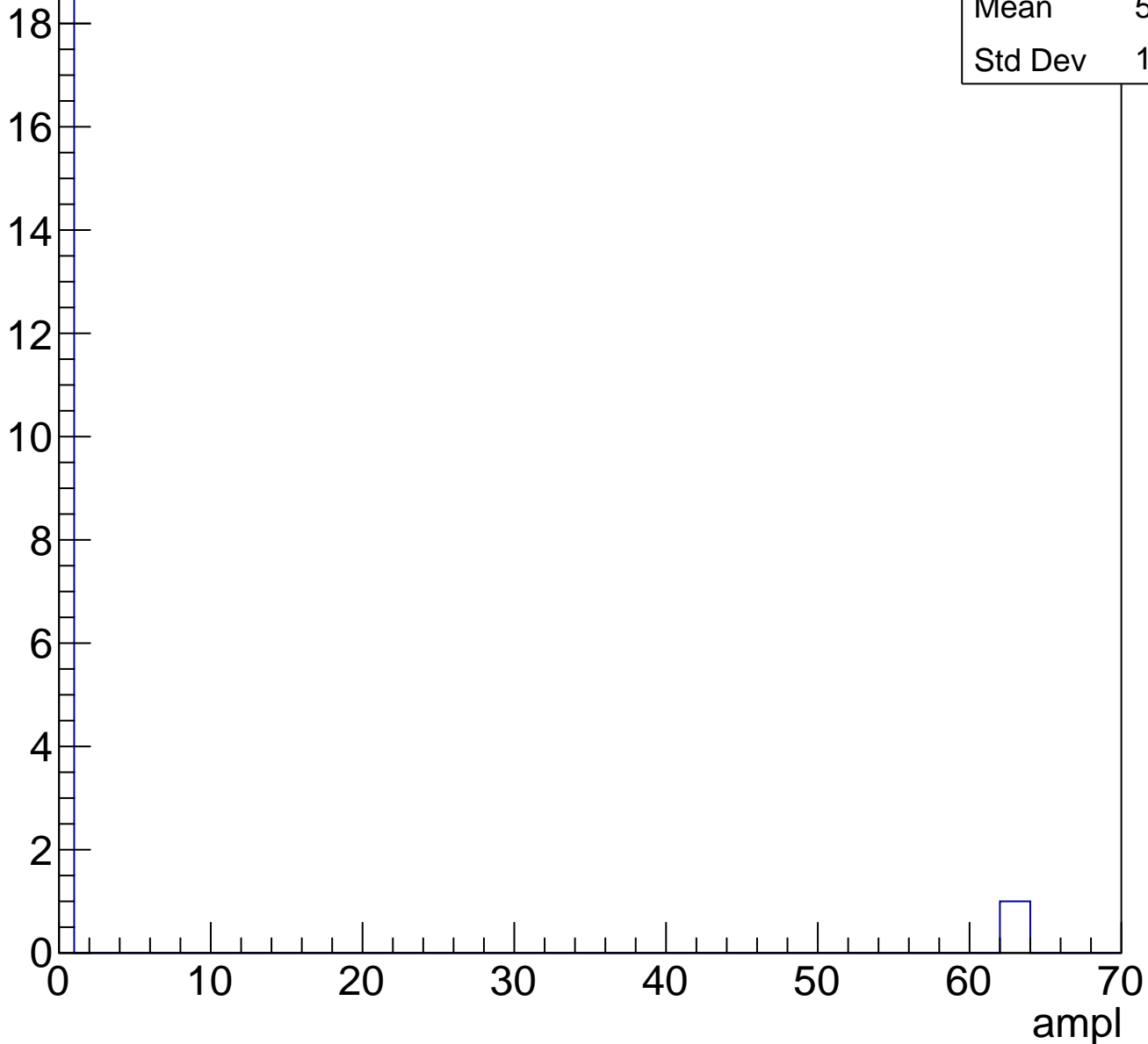


B1L103S, U21-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U21-ch87, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	22.16
Std Dev	9.987

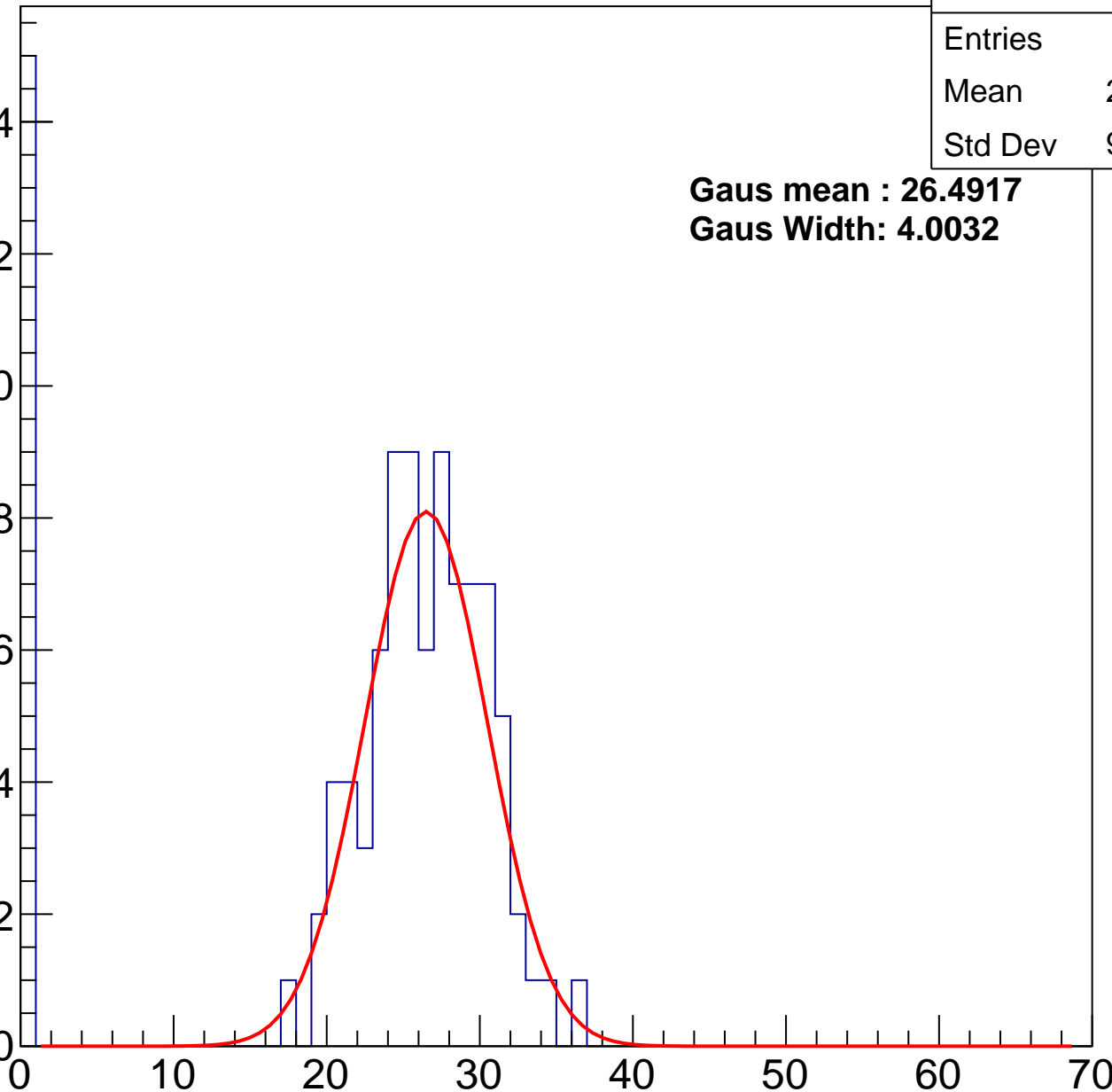
Gaus mean : 26.4917

Gaus Width: 4.0032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch87, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	31.71
Std Dev	8.996

Gaus mean : 34.3085

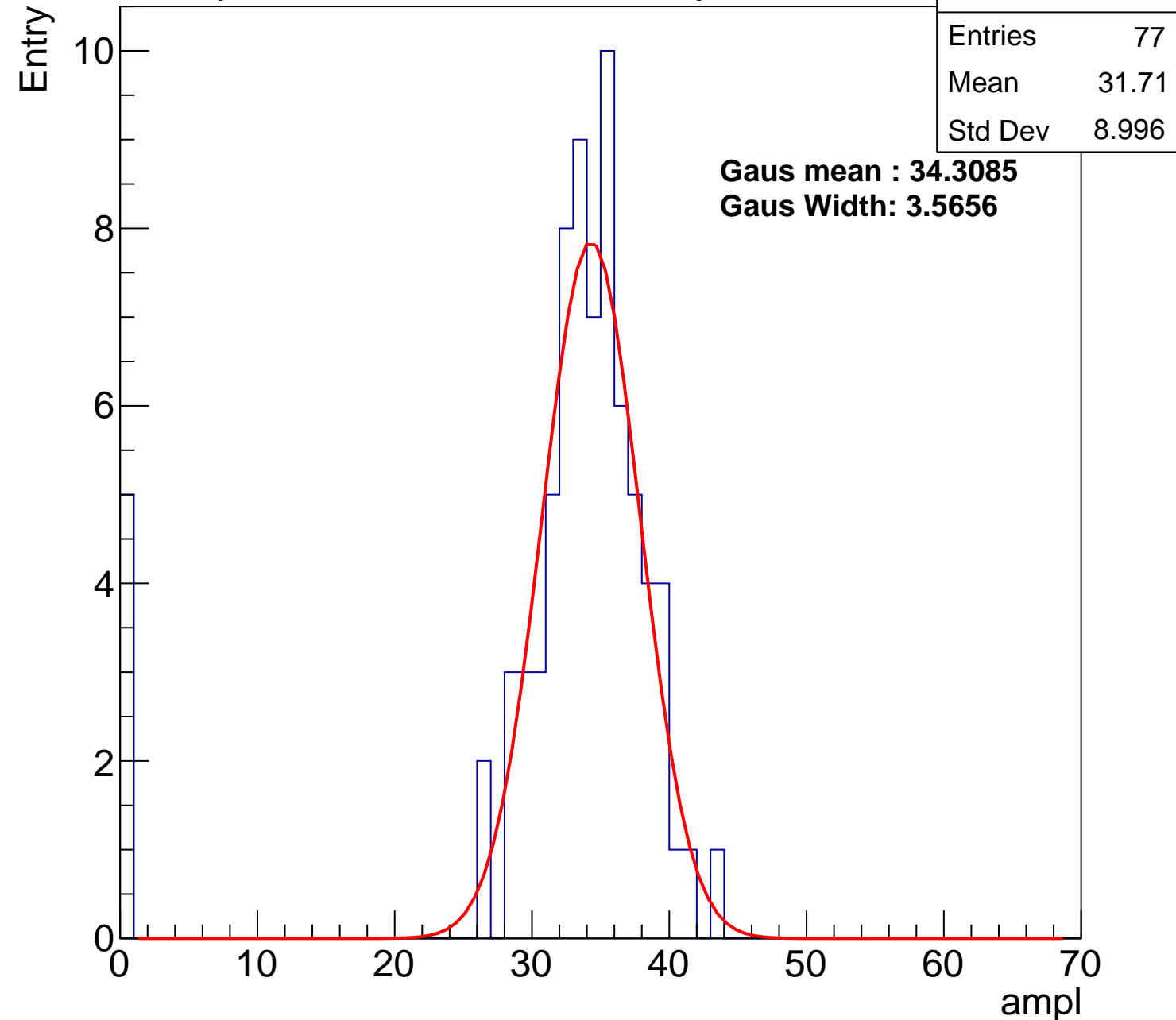
Gaus Width: 3.5656

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch87, adc2

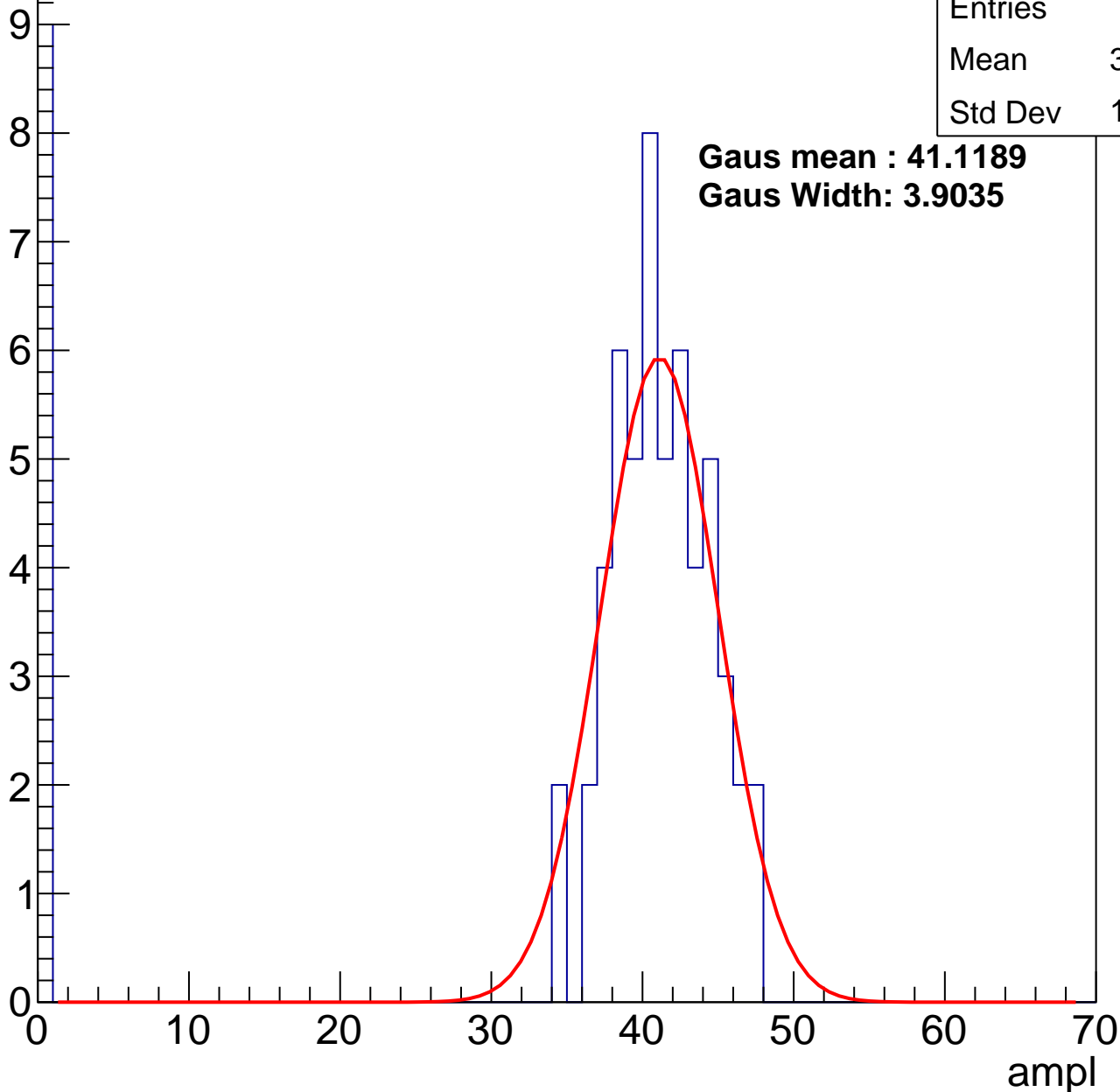
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.94
Std Dev	14.55

Gaus mean : 41.1189

Gaus Width: 3.9035

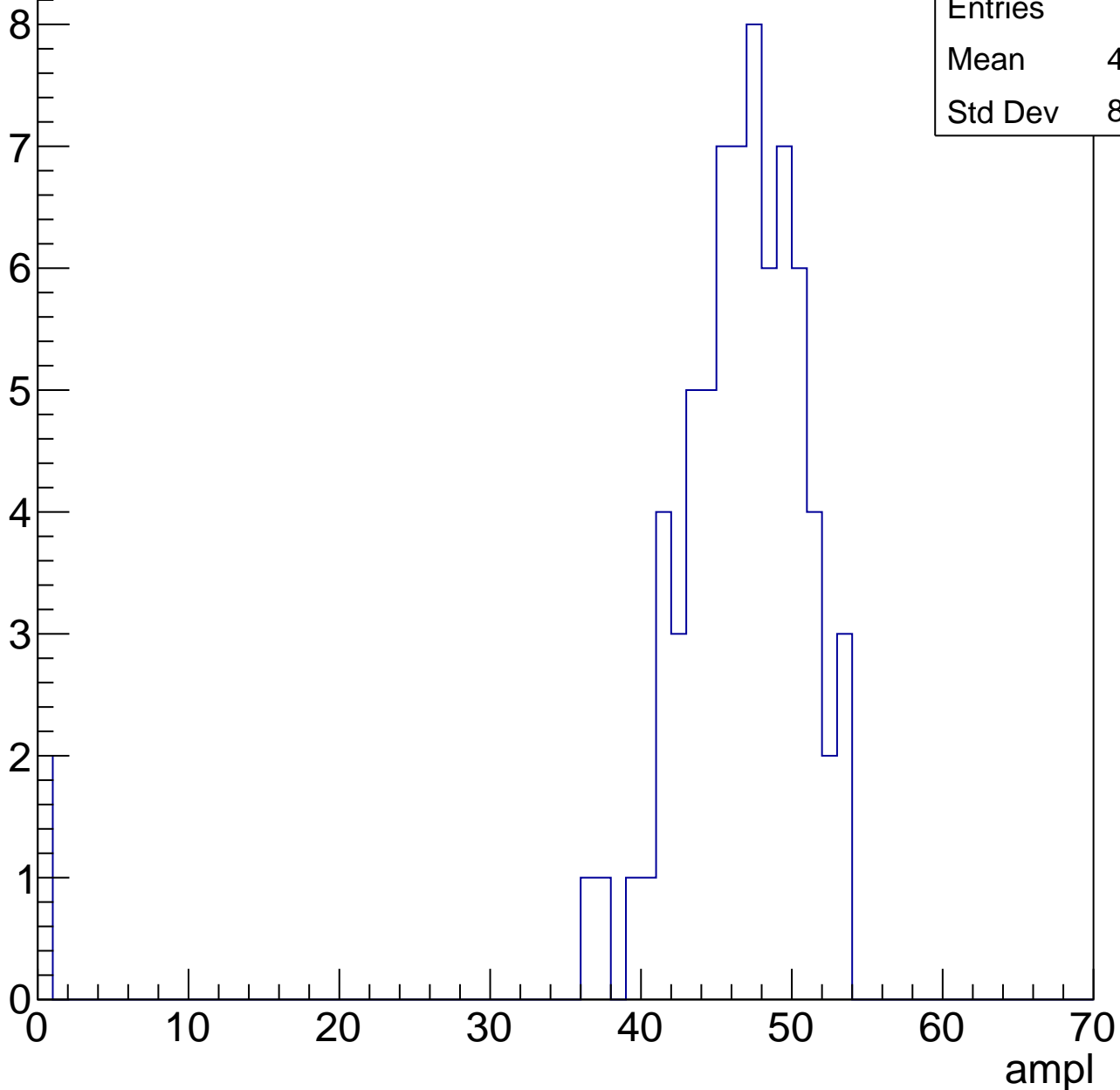


B1L103S, U21-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	45.04
Std Dev	8.408

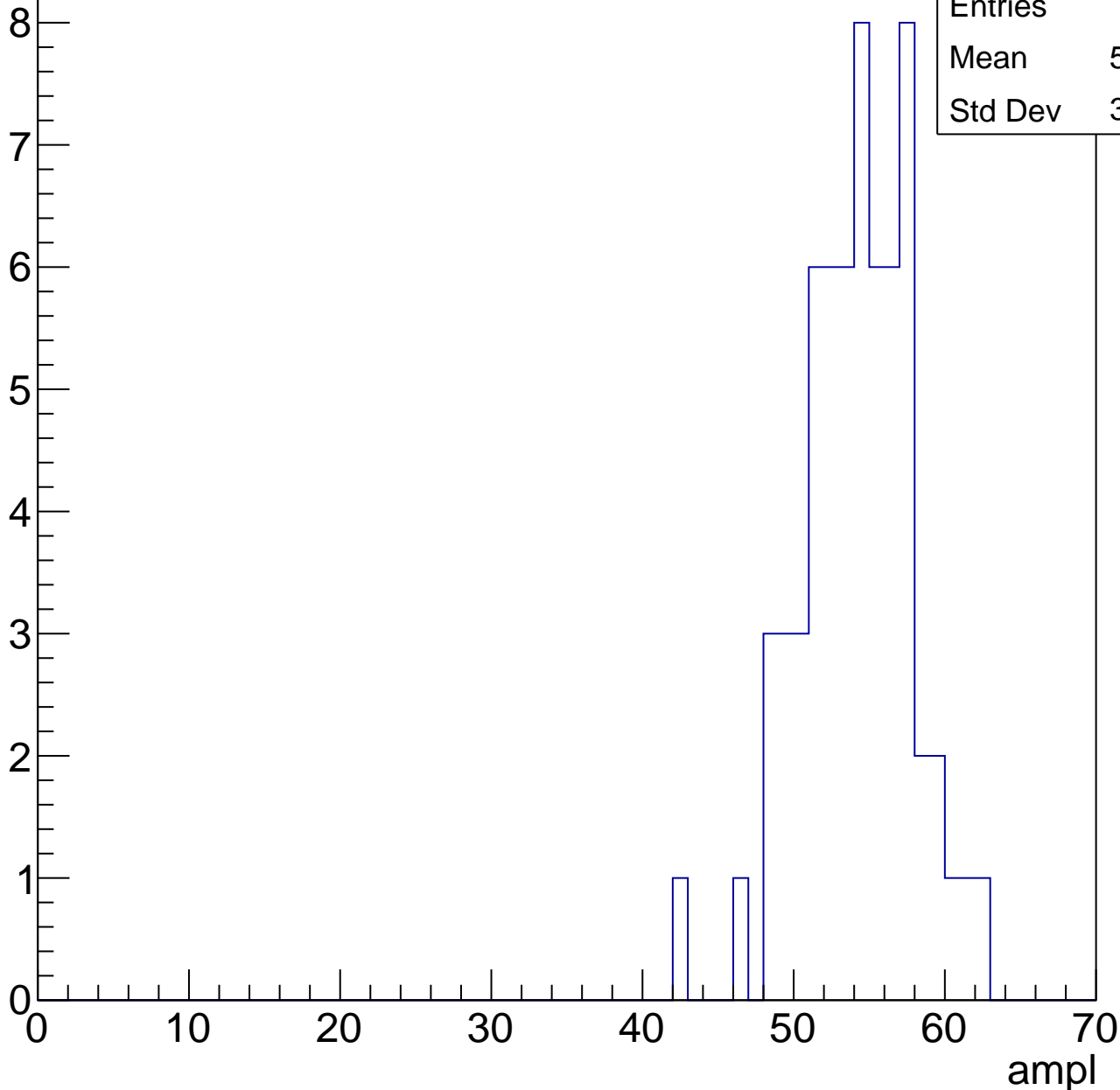


B1L103S, U21-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	53.69
Std Dev	3.644

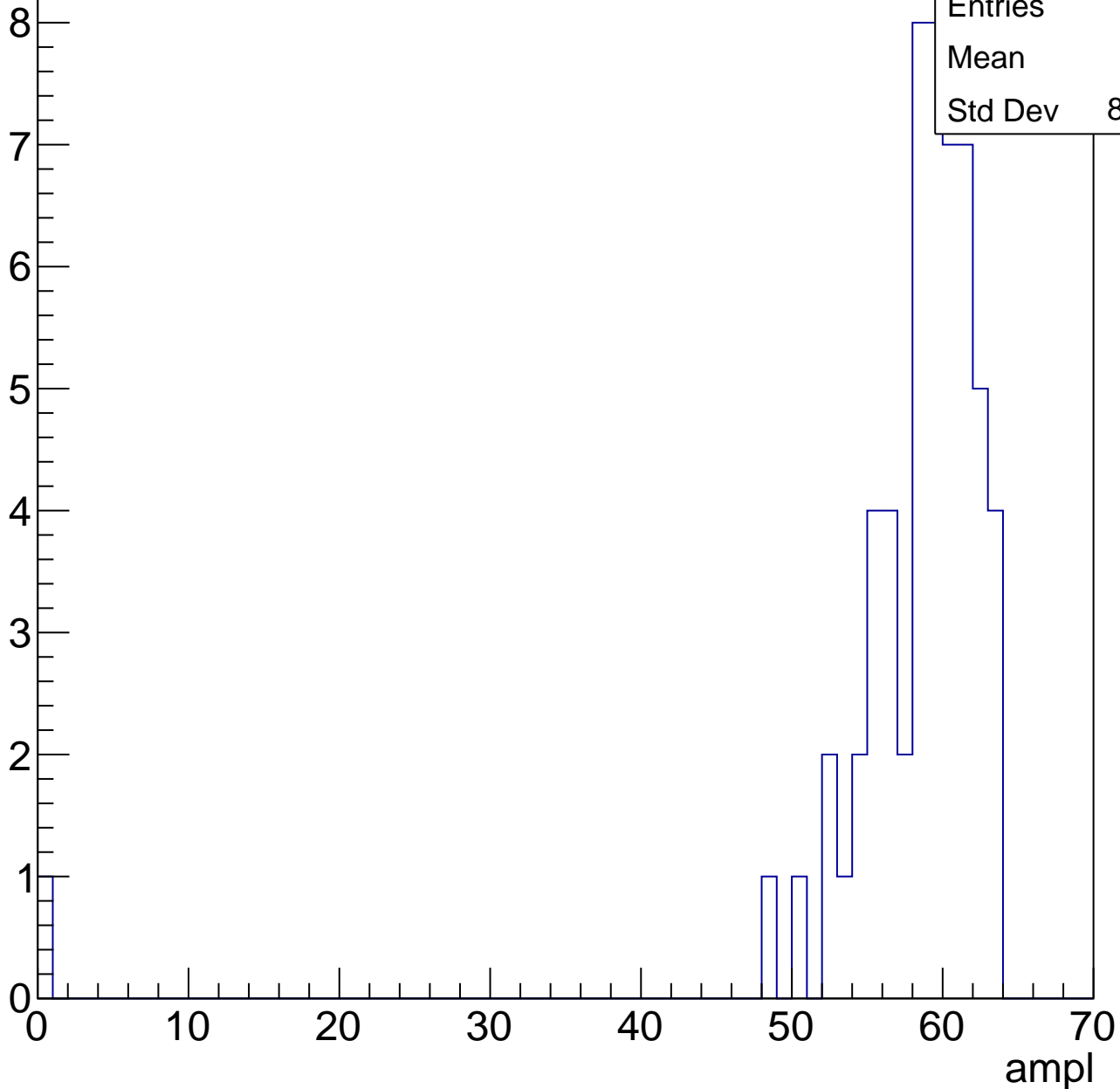


B1L103S, U21-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	57.3
Std Dev	8.337

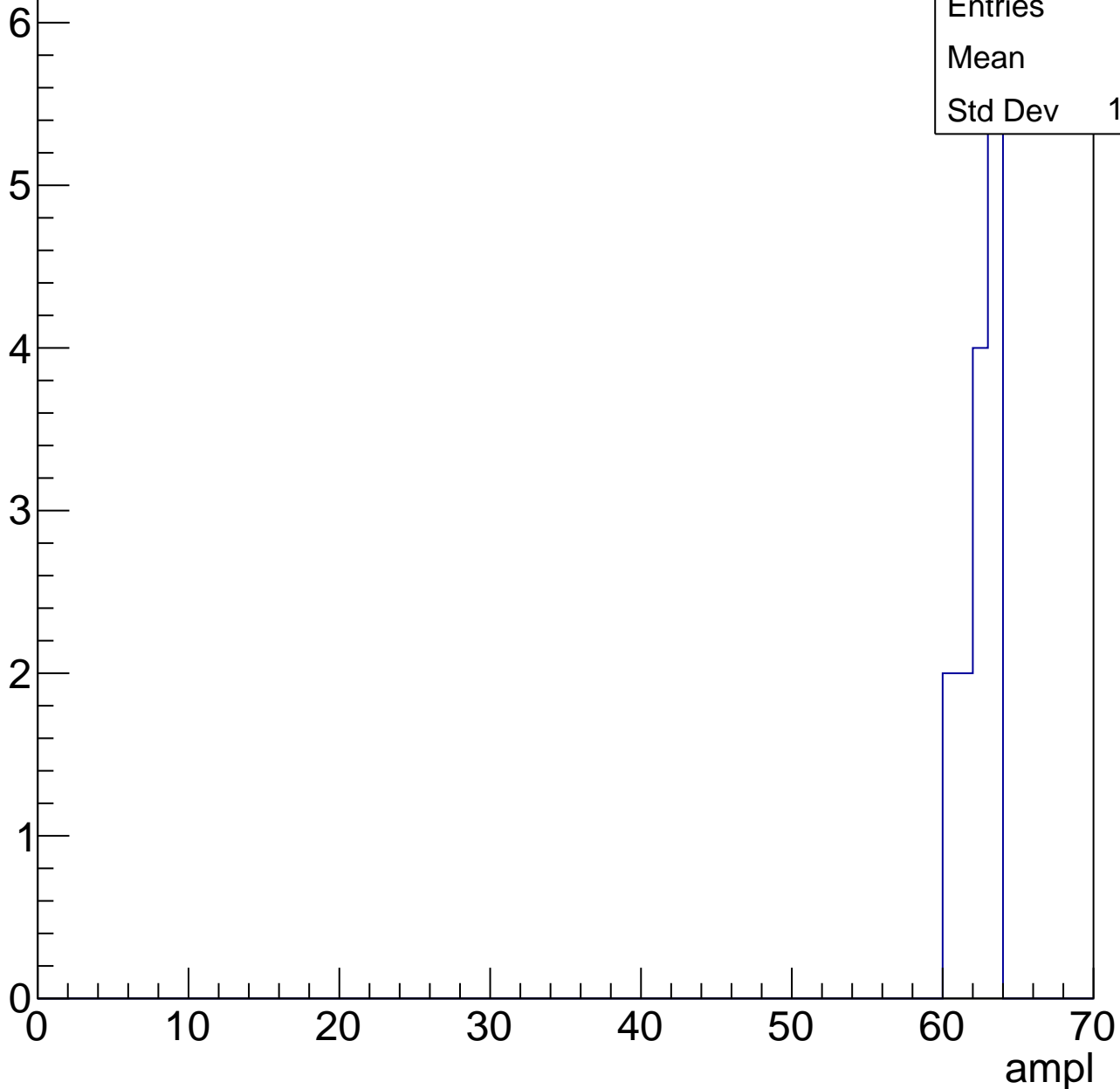


B1L103S, U21-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	62
Std Dev	1.069



B1L103S, U21-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

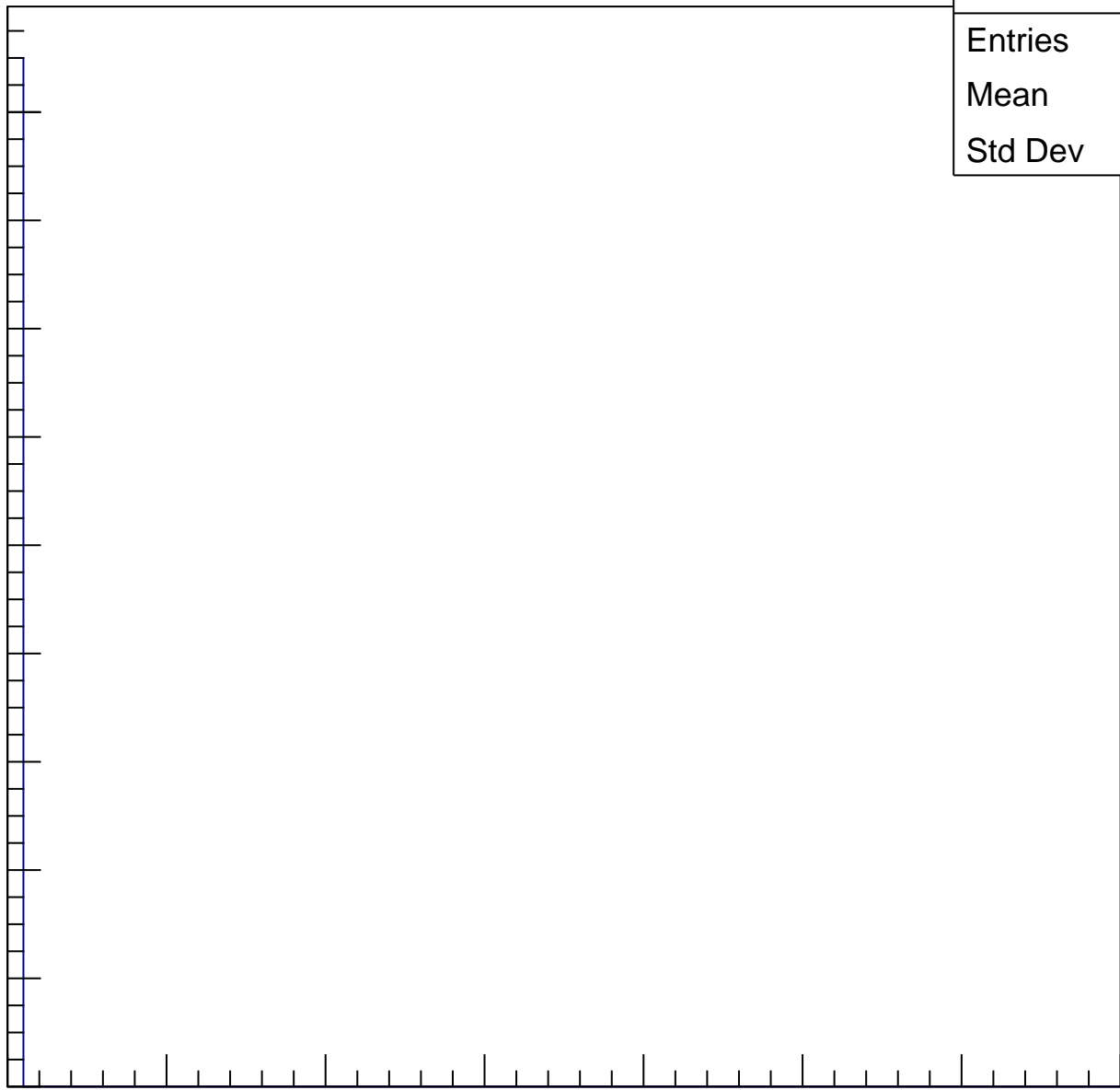
Entries	19
Mean	0
Std Dev	0

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch88, adc0

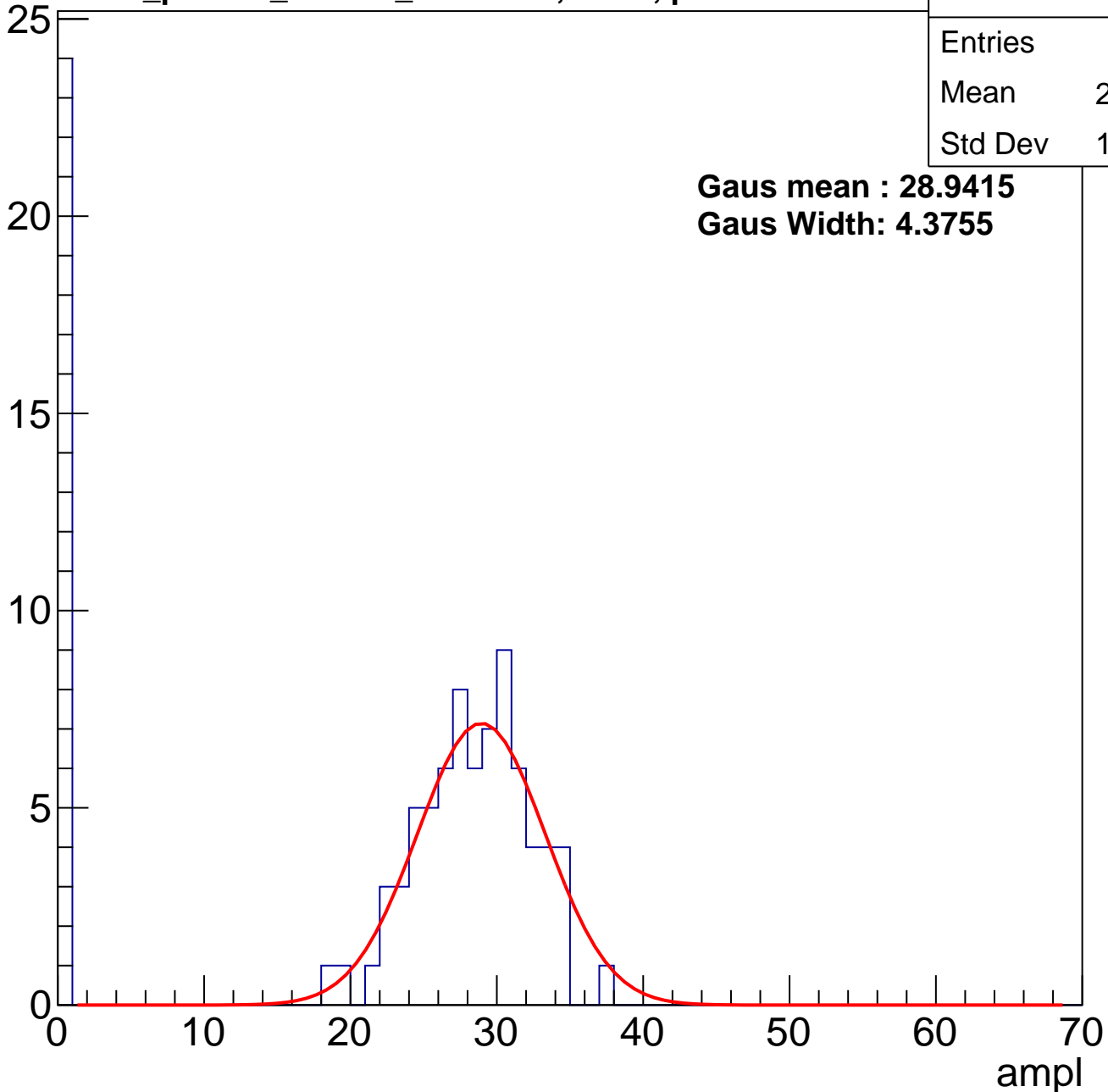
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	21.12
Std Dev	12.47

Gaus mean : 28.9415

Gaus Width: 4.3755

Entry



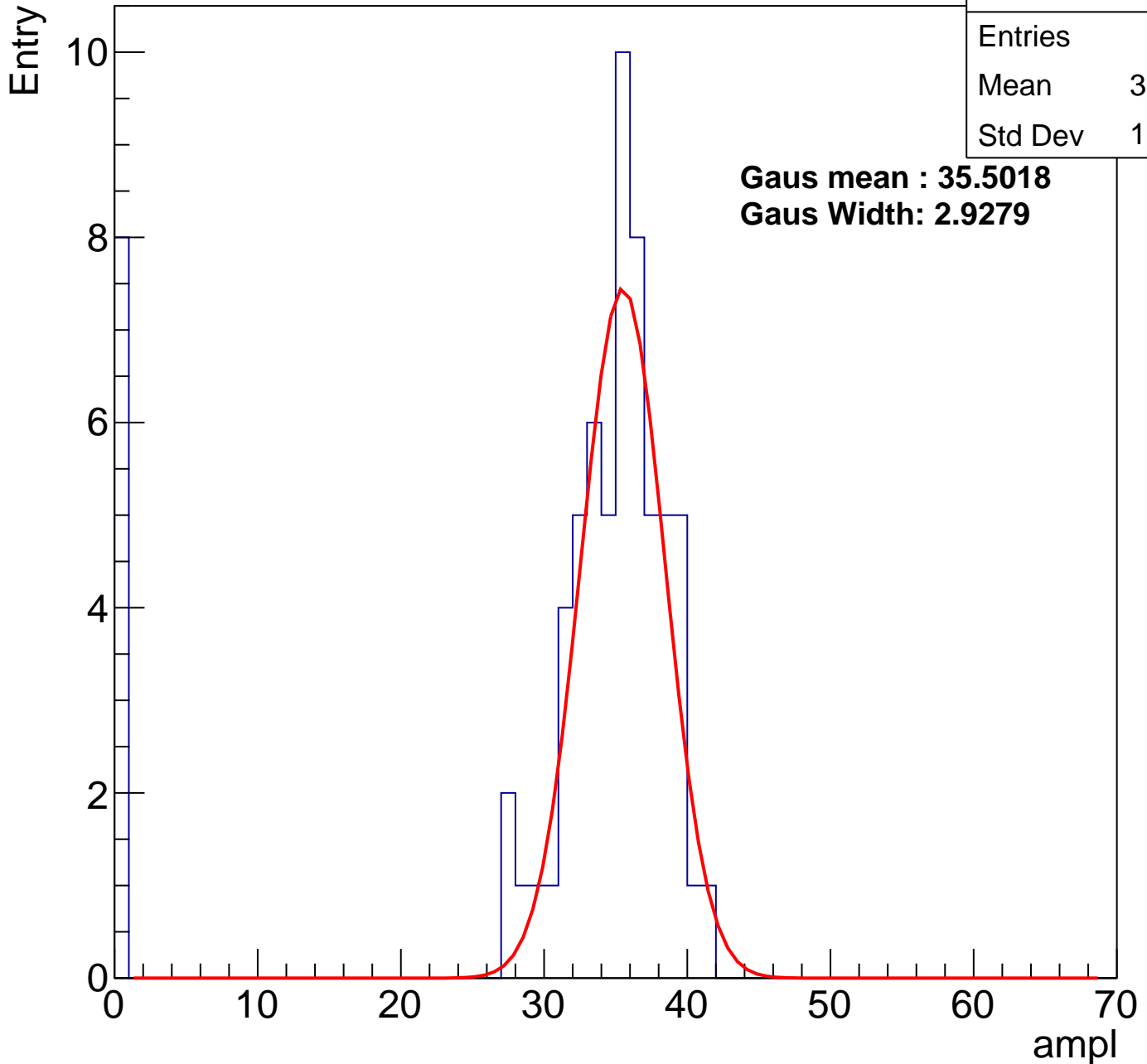
B1L103S, U21-ch88, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	30.62
Std Dev	11.56

Gaus mean : 35.5018

Gaus Width: 2.9279



B1L103S, U21-ch88, adc2

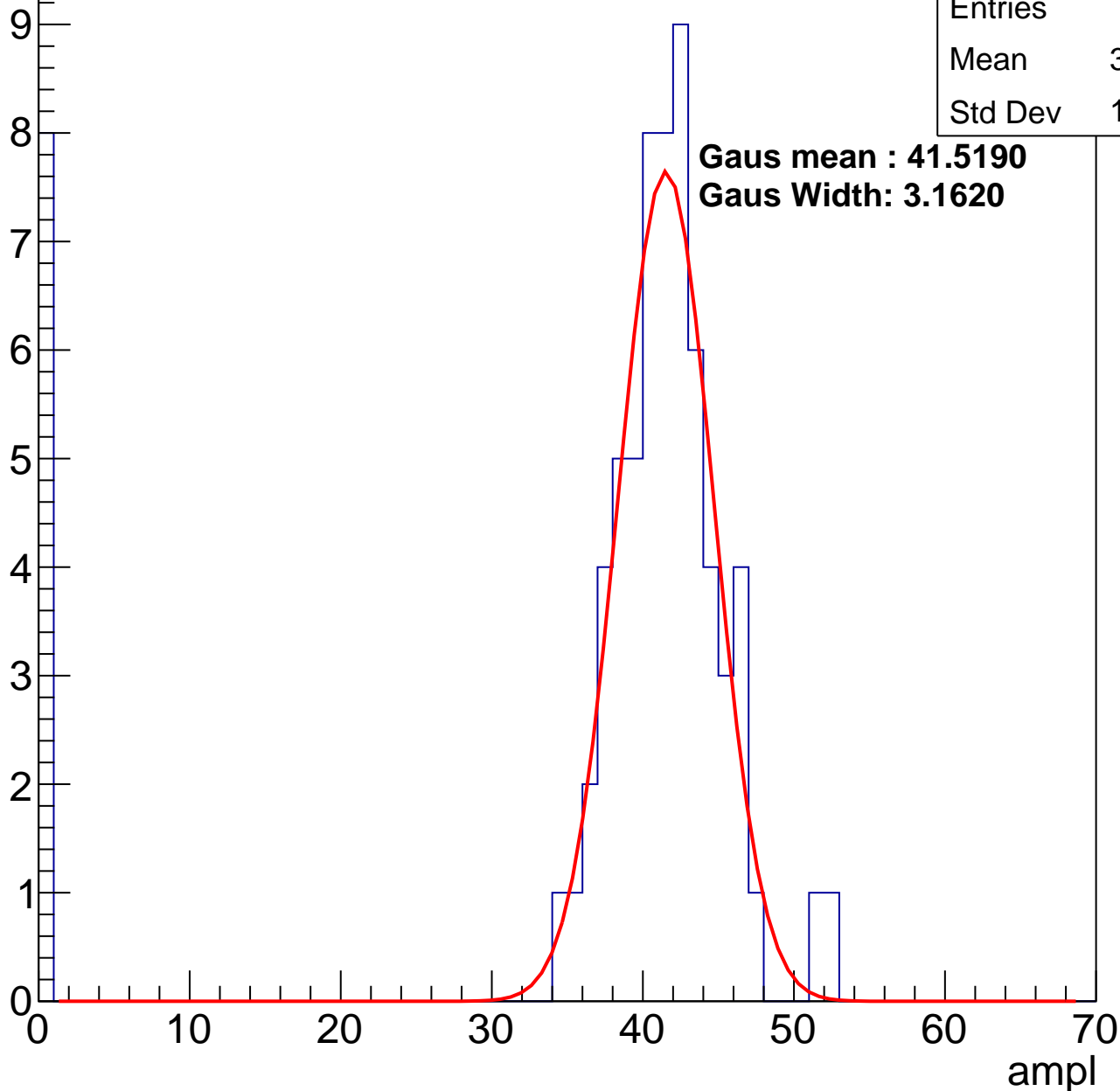
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	36.66
Std Dev	13.46

Gaus mean : 41.5190

Gaus Width: 3.1620

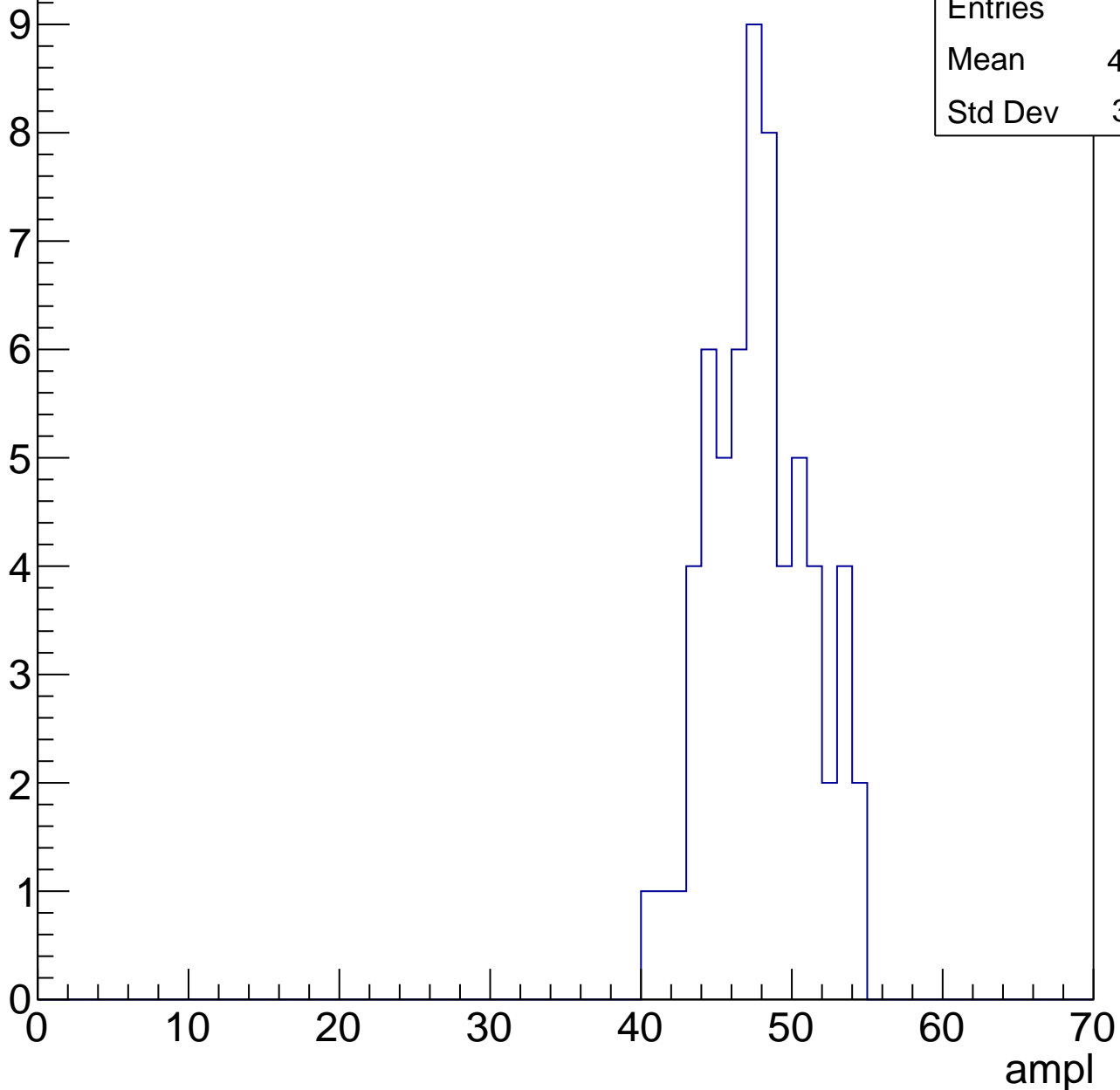


B1L103S, U21-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	47.44
Std Dev	3.281

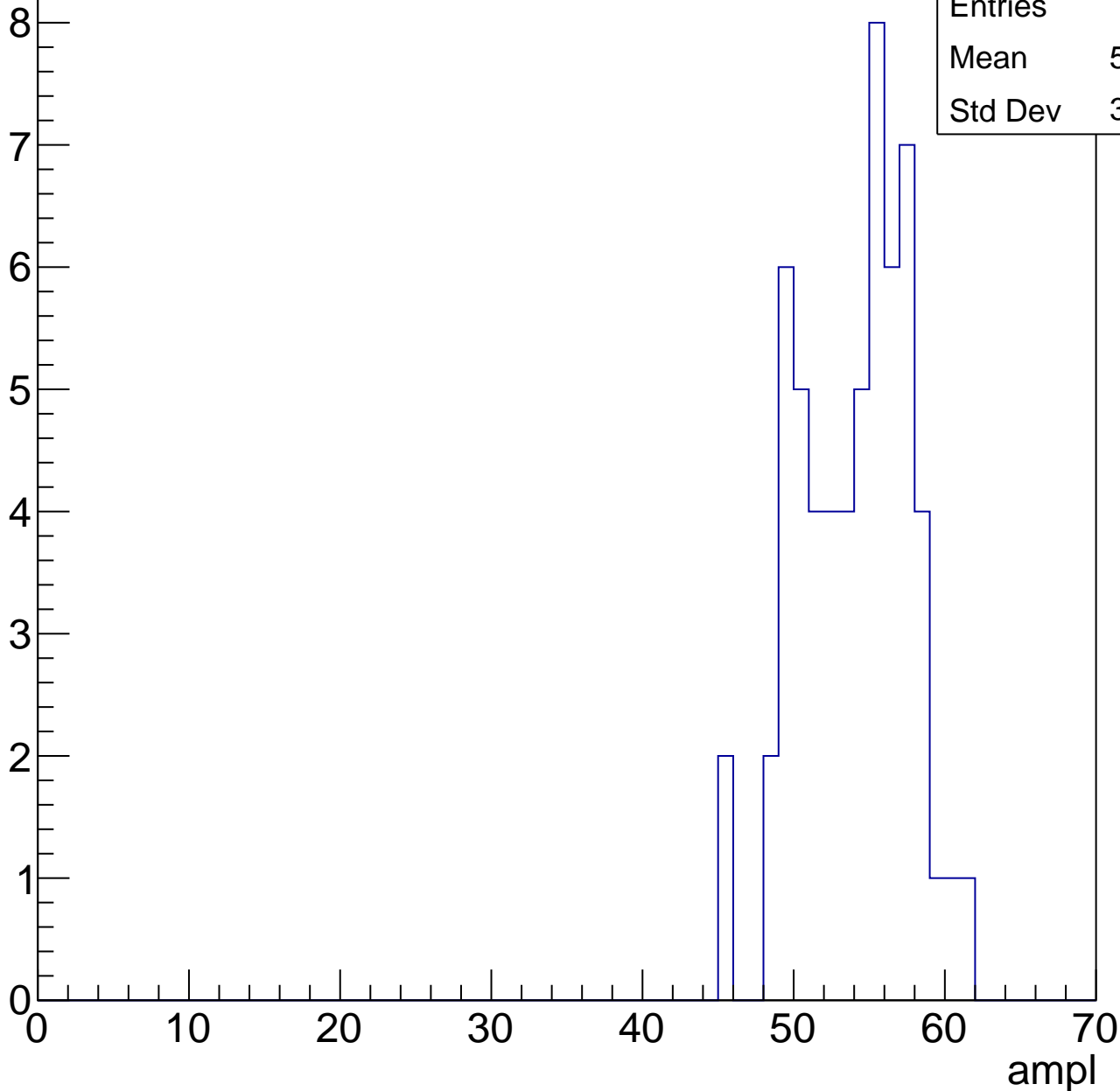


B1L103S, U21-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	53.52
Std Dev	3.594

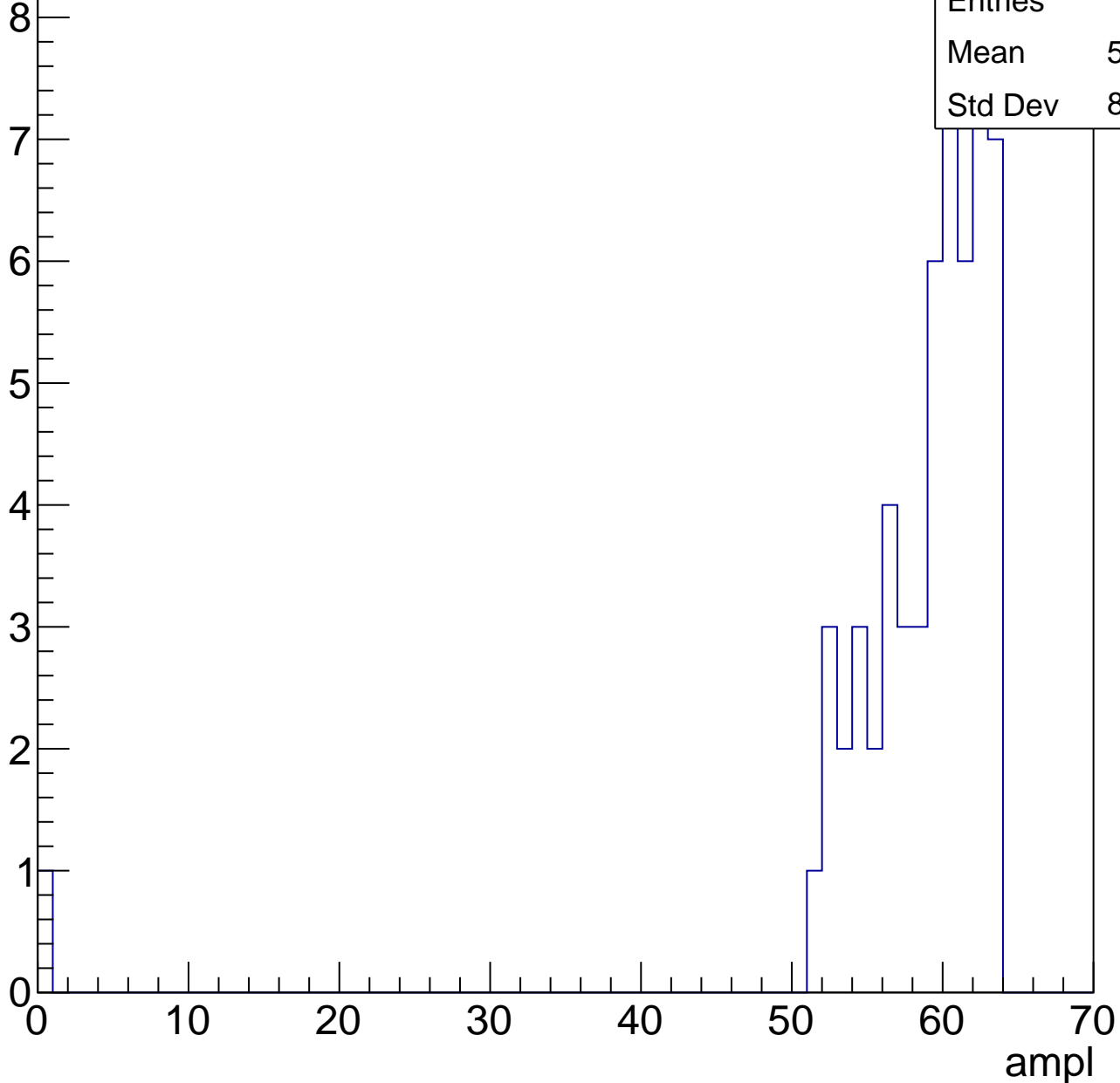


B1L103S, U21-ch88, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	57.74
Std Dev	8.426



B1L103S, U21-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	14
Mean	60.21
Std Dev	1.739

B1L103S, U21-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



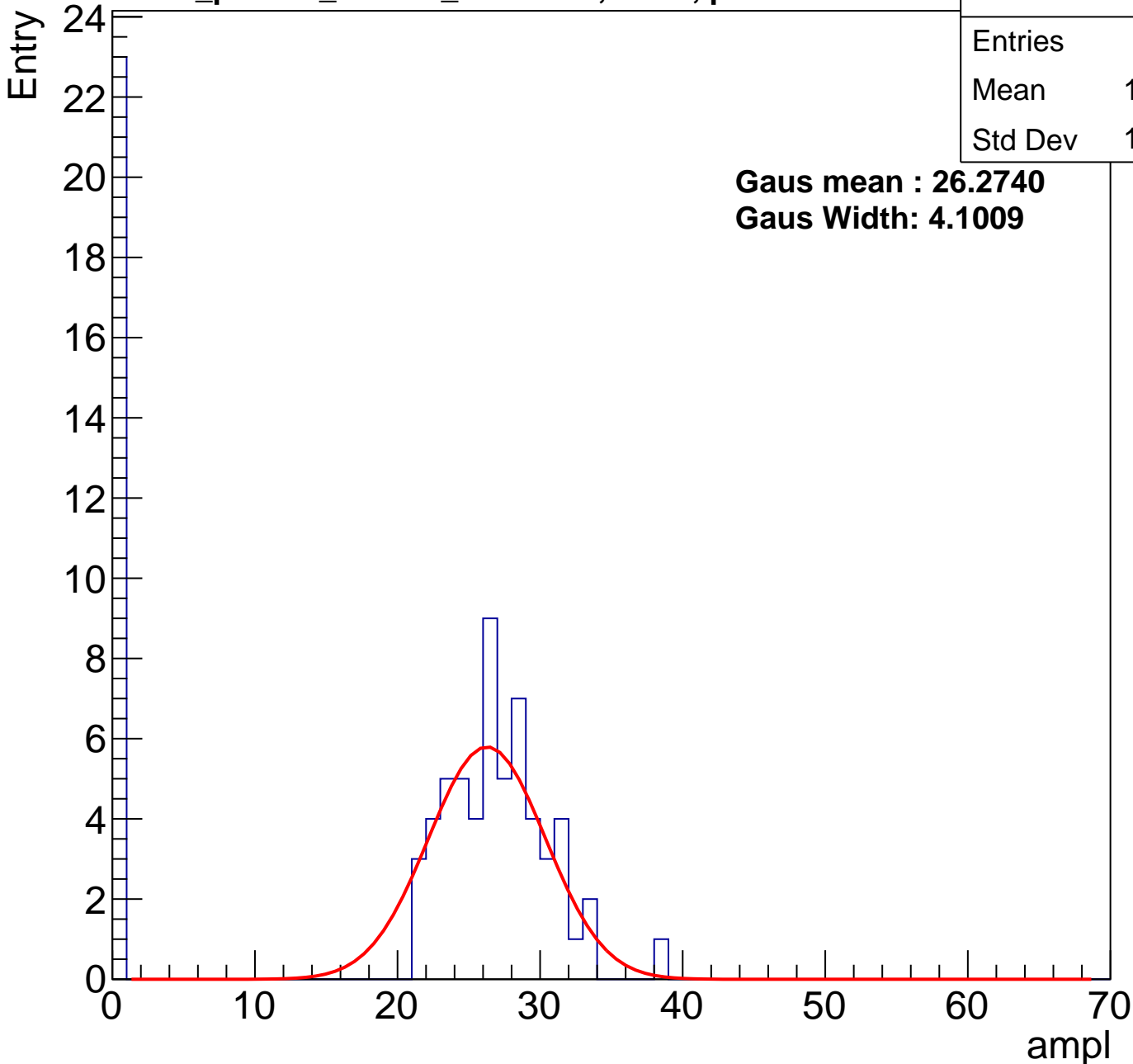
B1L103S, U21-ch89, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	18.96
Std Dev	12.39

Gaus mean : 26.2740

Gaus Width: 4.1009



B1L103S, U21-ch89, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	32.82
Std Dev	5.133

Gaus mean : 33.6297

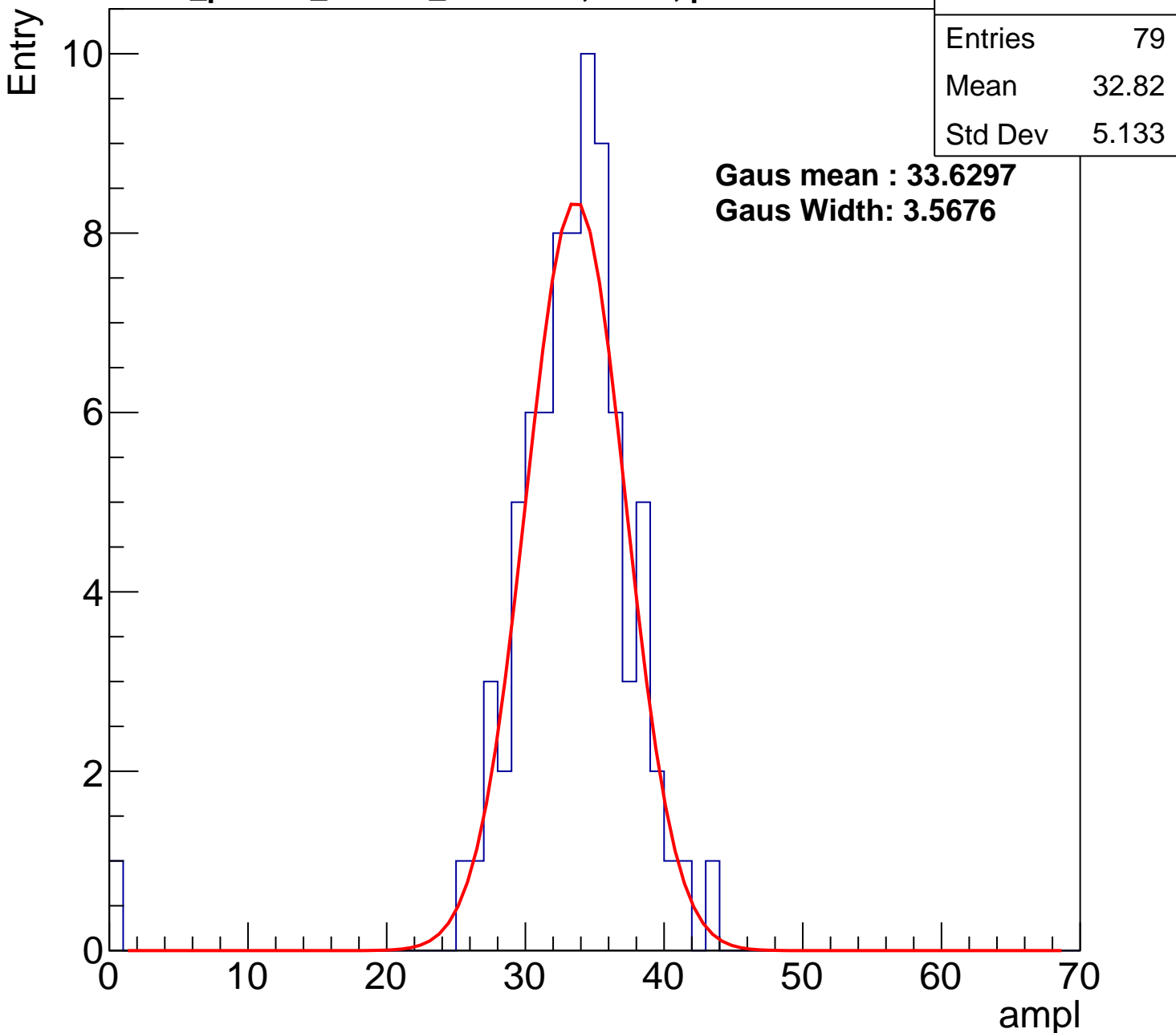
Gaus Width: 3.5676

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch89, adc2

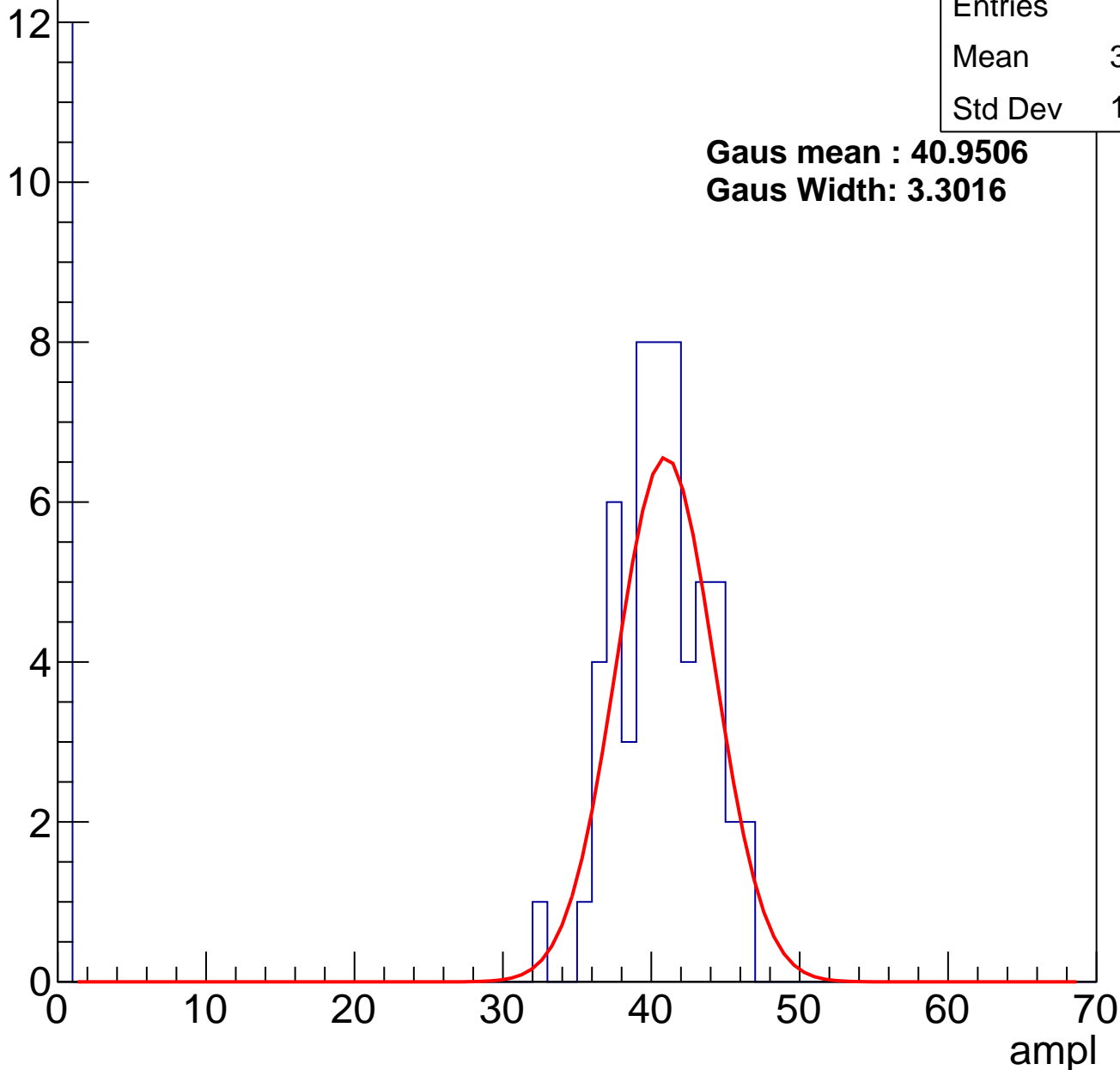
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	33.22
Std Dev	15.47

Gaus mean : 40.9506

Gaus Width: 3.3016

Entry

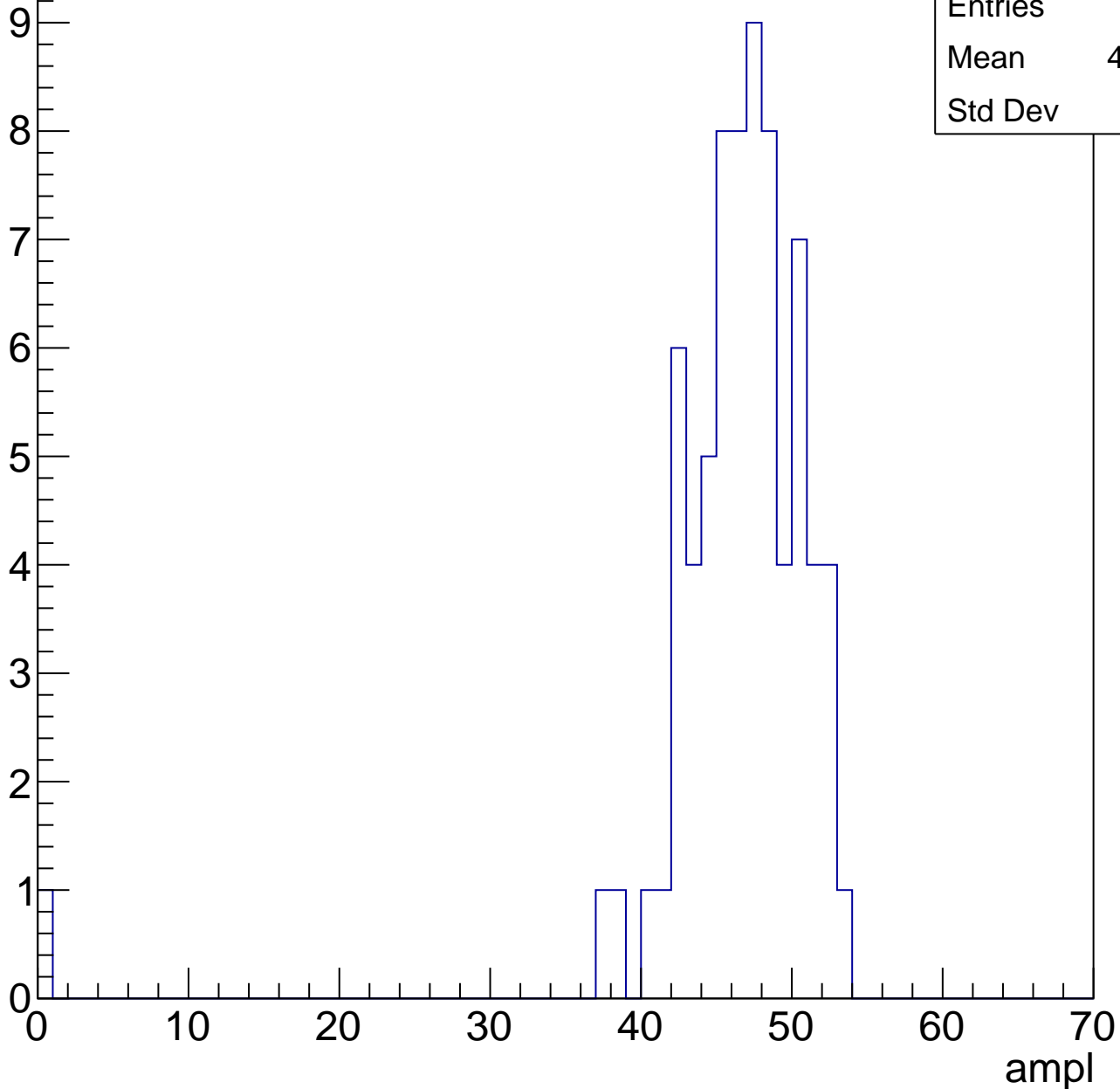


B1L103S, U21-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	45.84
Std Dev	6.37

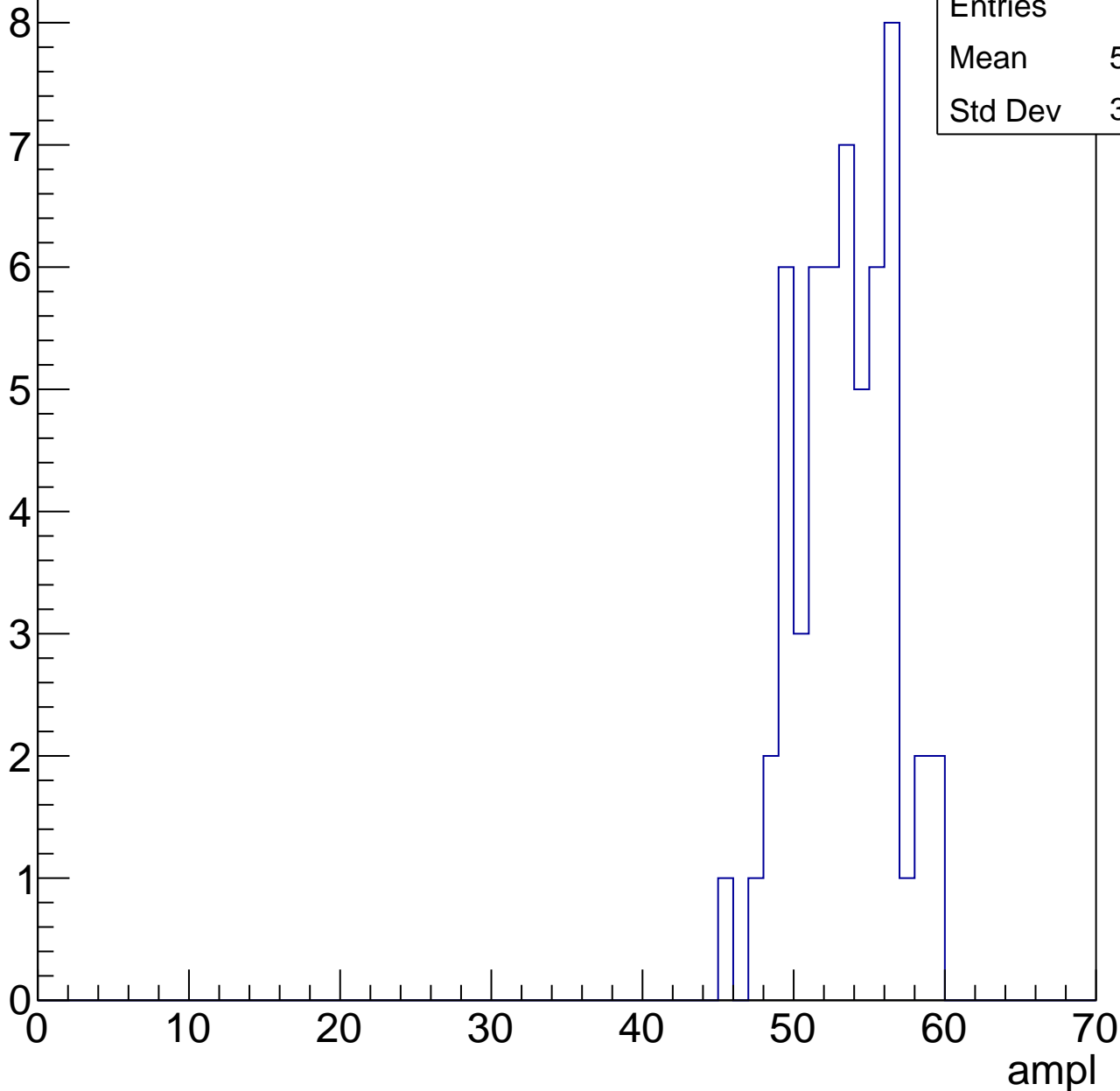


B1L103S, U21-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

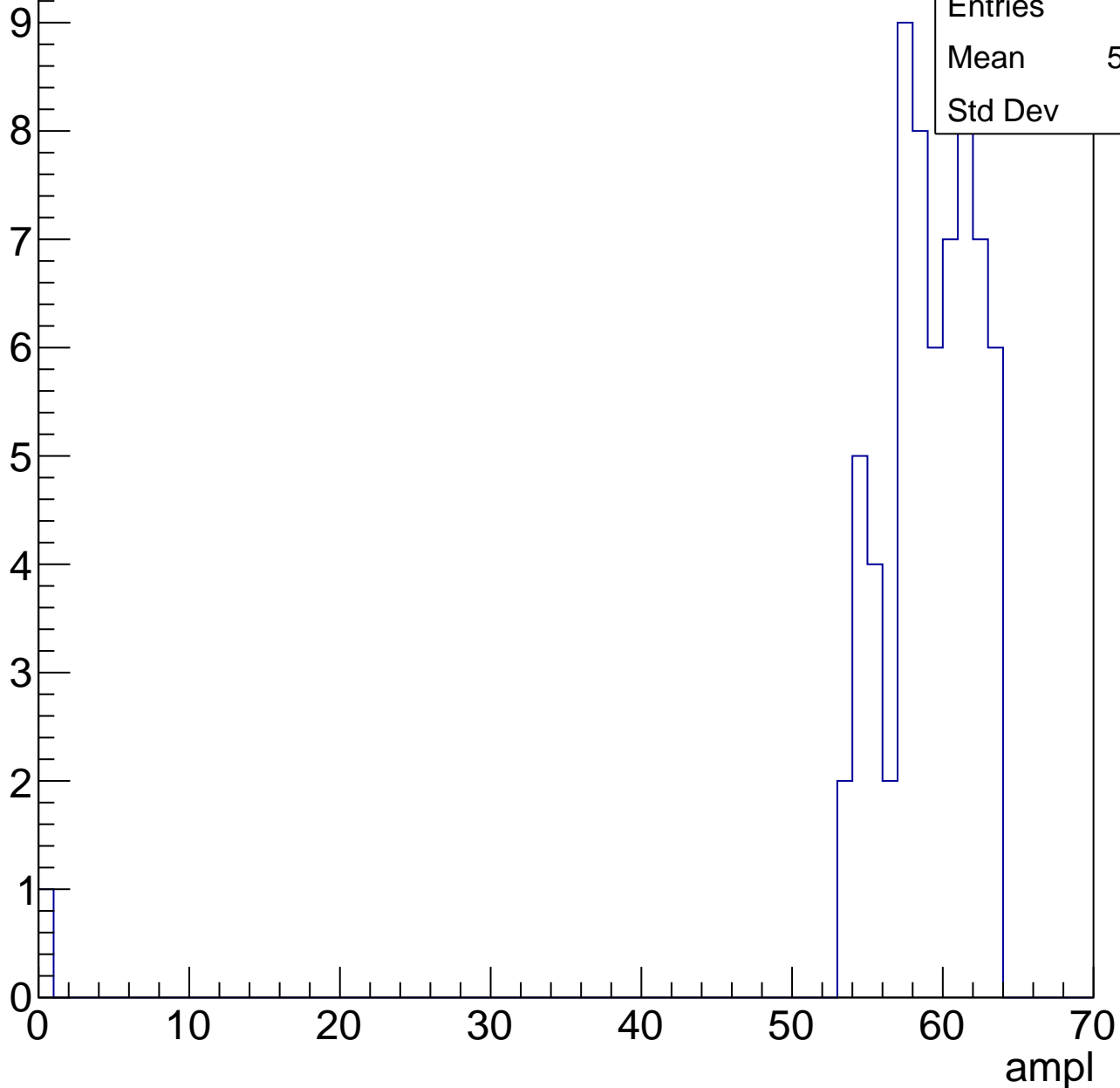
Entries	56
Mean	52.86
Std Dev	3.108



B1L103S, U21-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

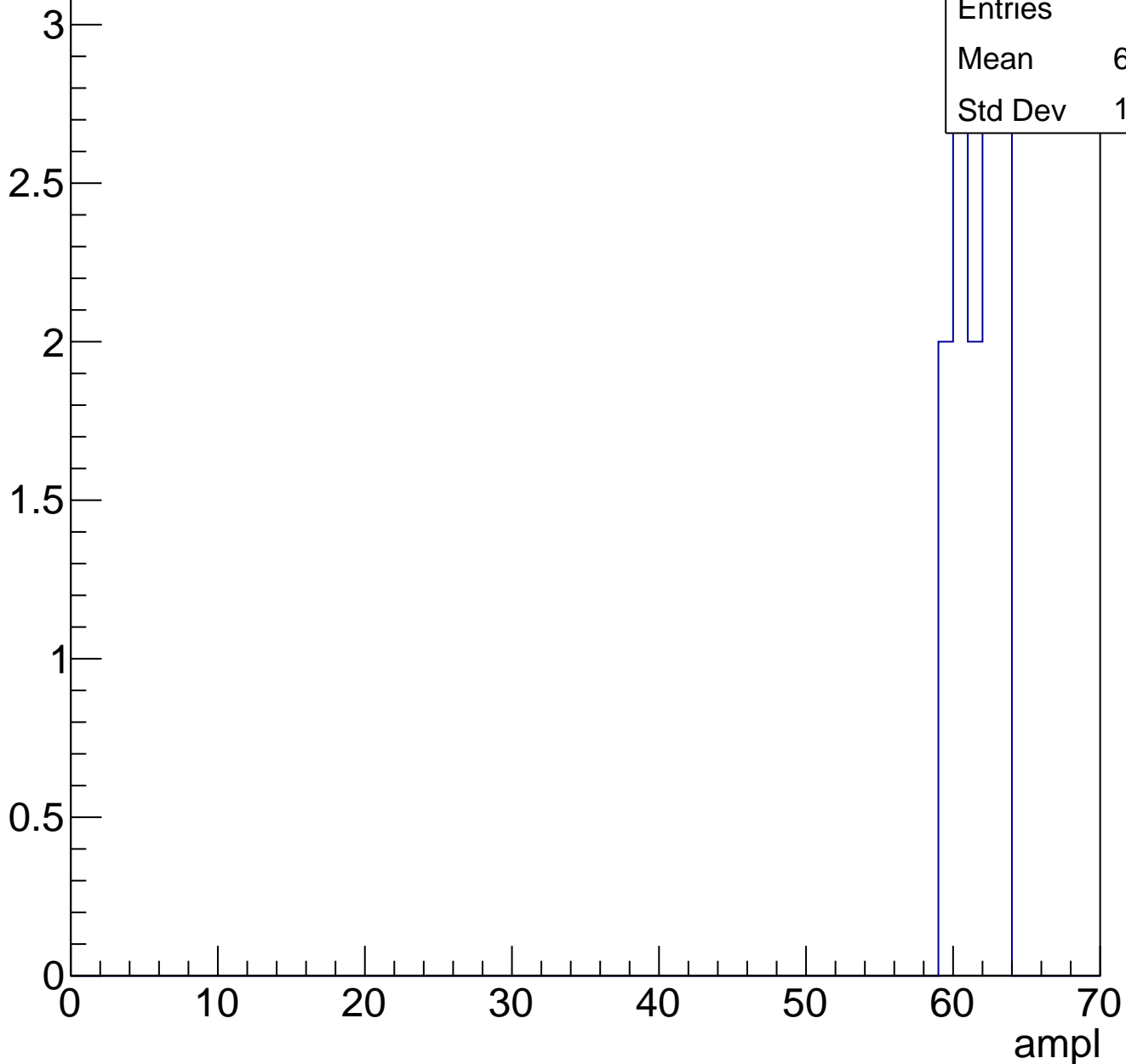
Entry



B1L103S, U21-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

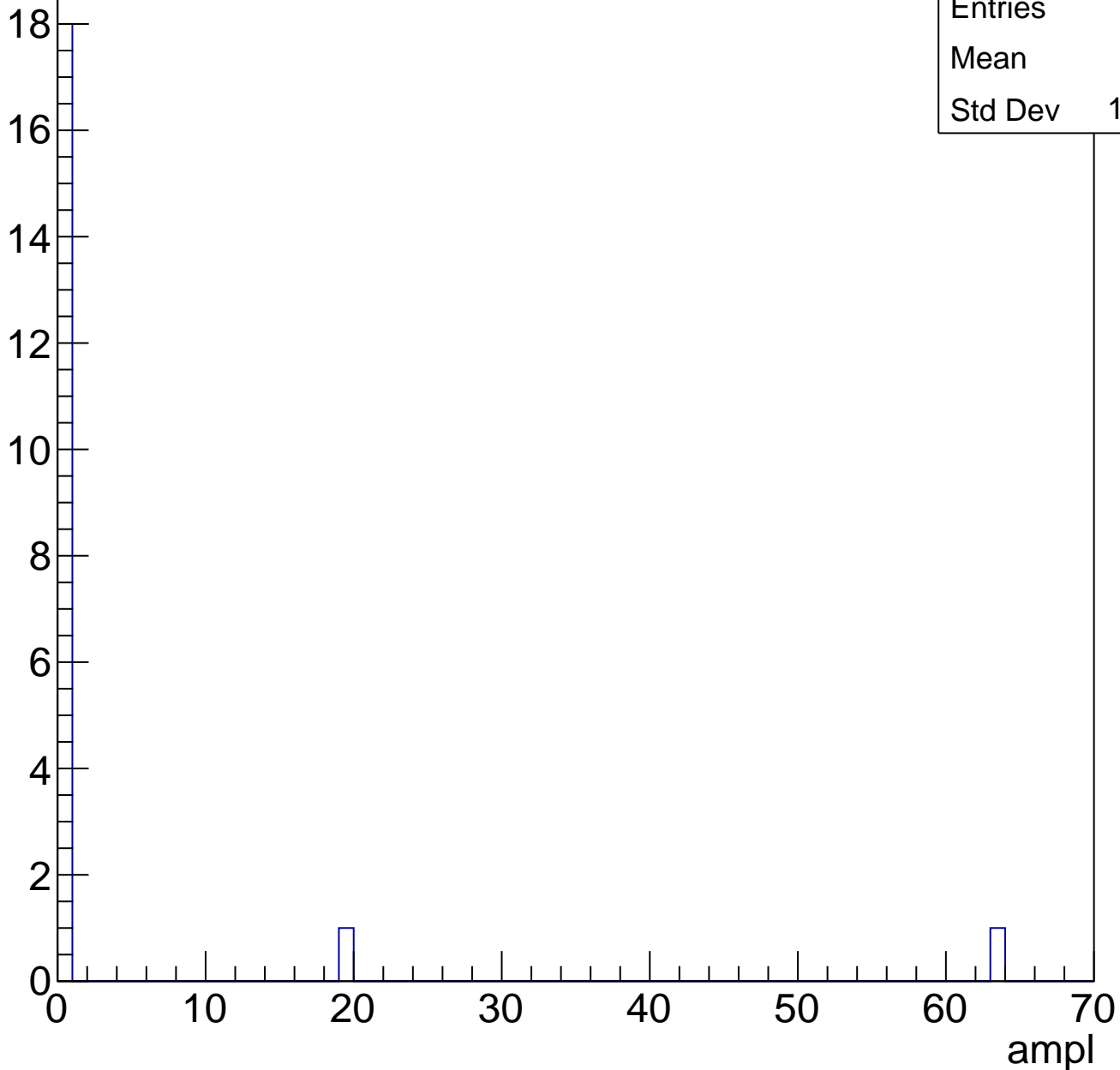


B1L103S, U21-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.1
Std Dev	14.13

Entry



B1L103S, U21-ch90, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	20.13
Std Dev	10.19

Gaus mean : 25.7547

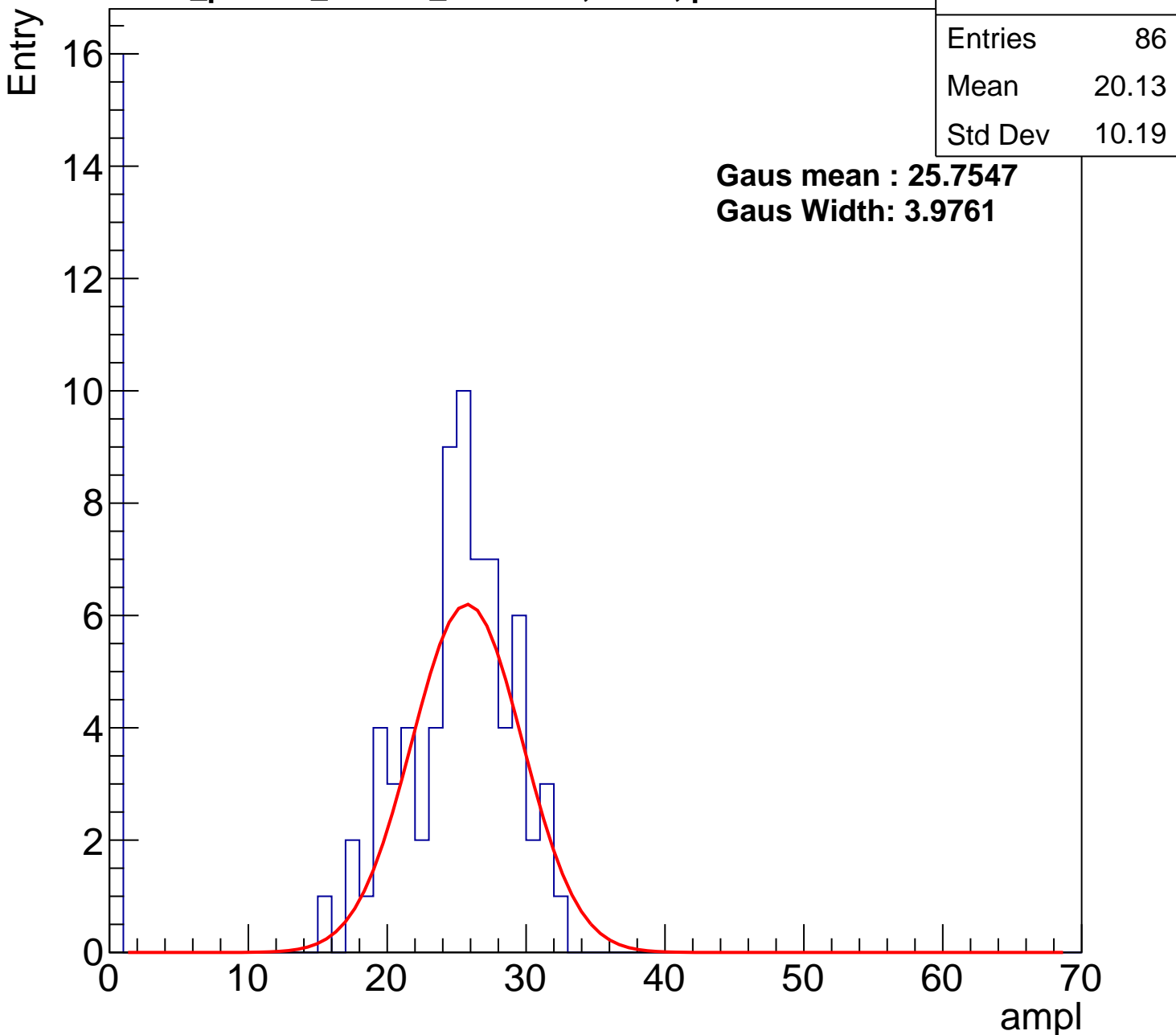
Gaus Width: 3.9761

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch90, adc1

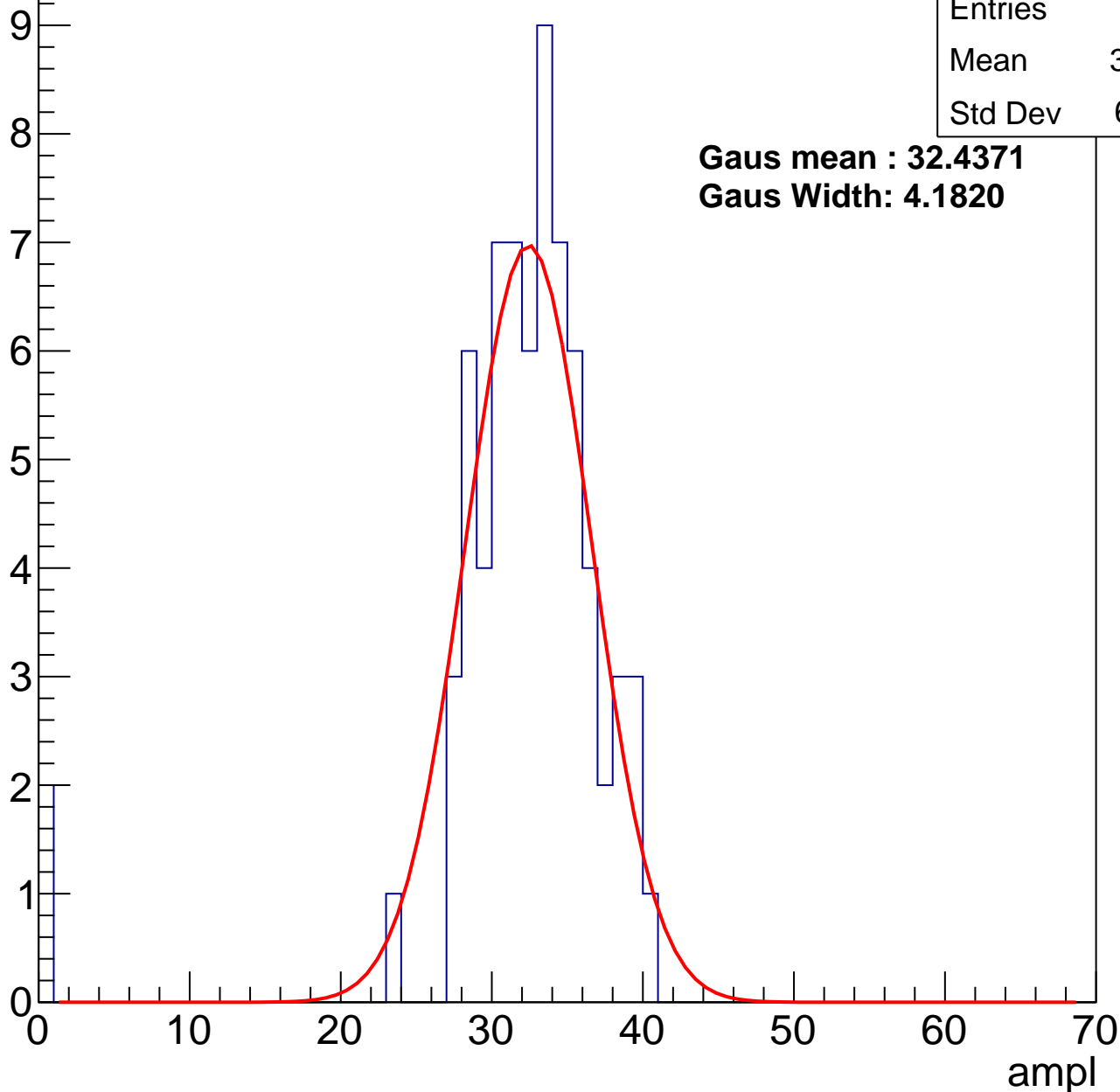
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	31.56
Std Dev	6.371

Gaus mean : 32.4371

Gaus Width: 4.1820



B1L103S, U21-ch90, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	33.7
Std Dev	13.91

Gaus mean : 39.6838

Gaus Width: 3.9171

Entry

10

8

6

4

2

0

0

10

20

30

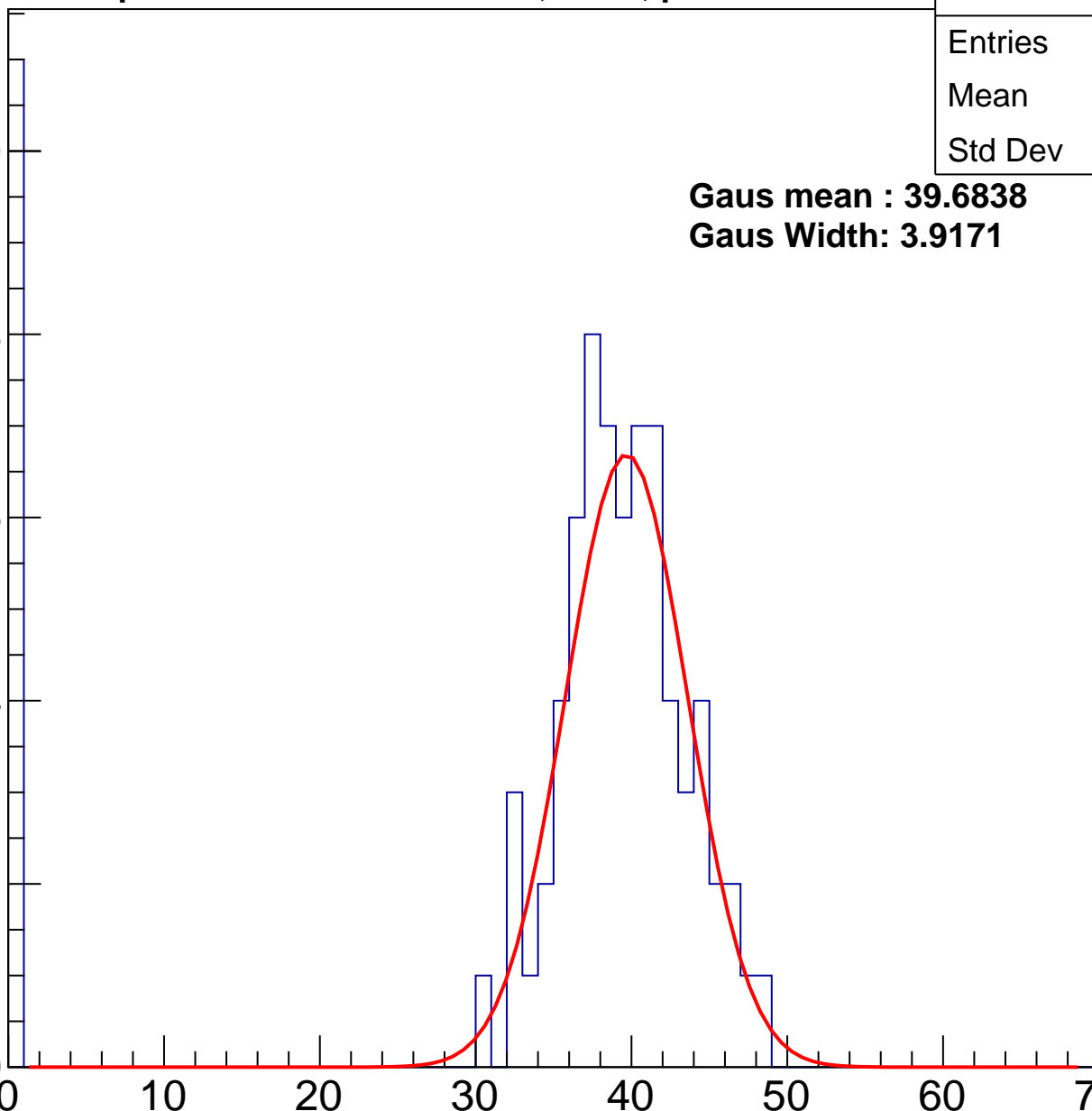
40

50

60

70

ampl

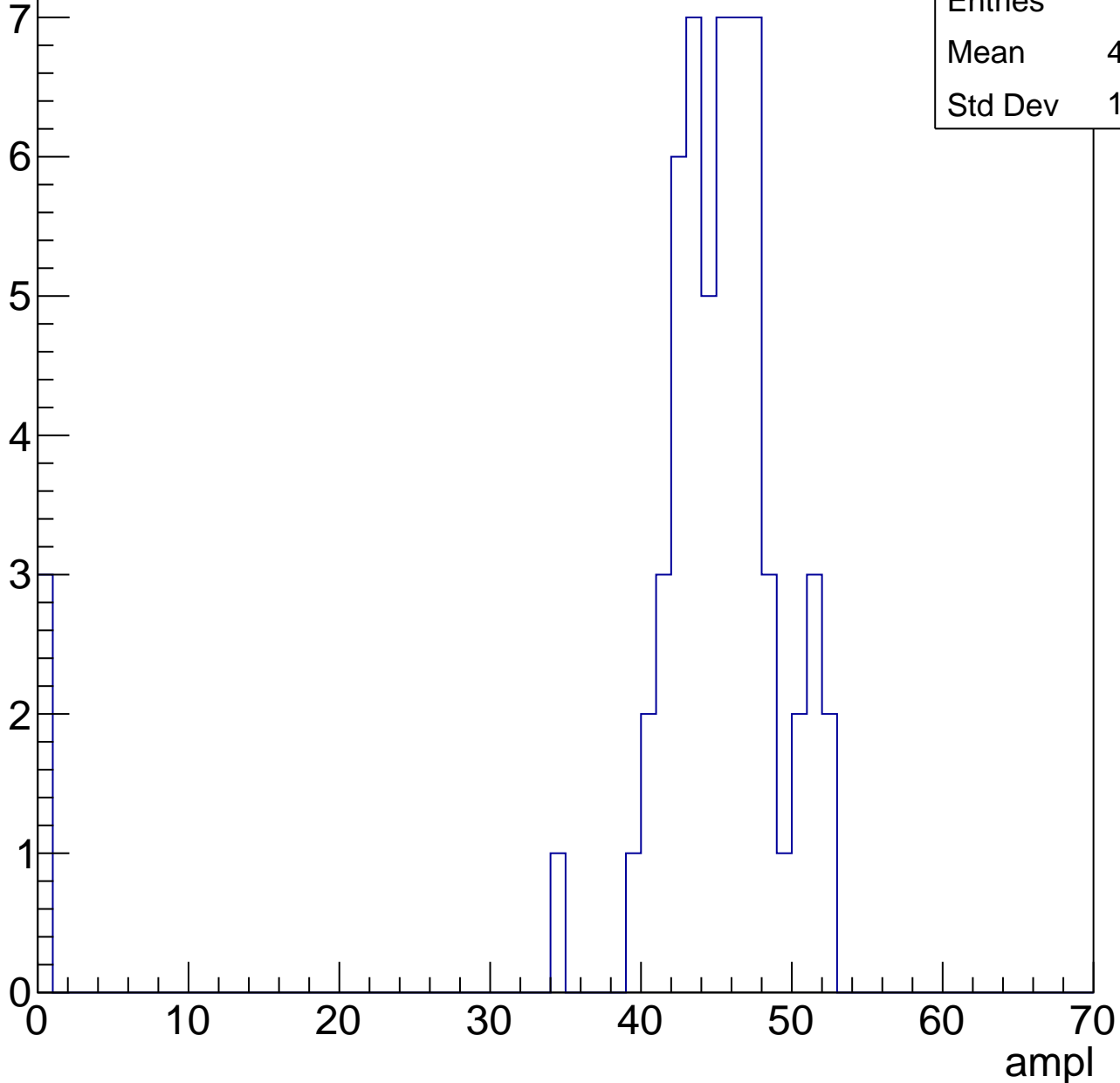


B1L103S, U21-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	42.75
Std Dev	10.37

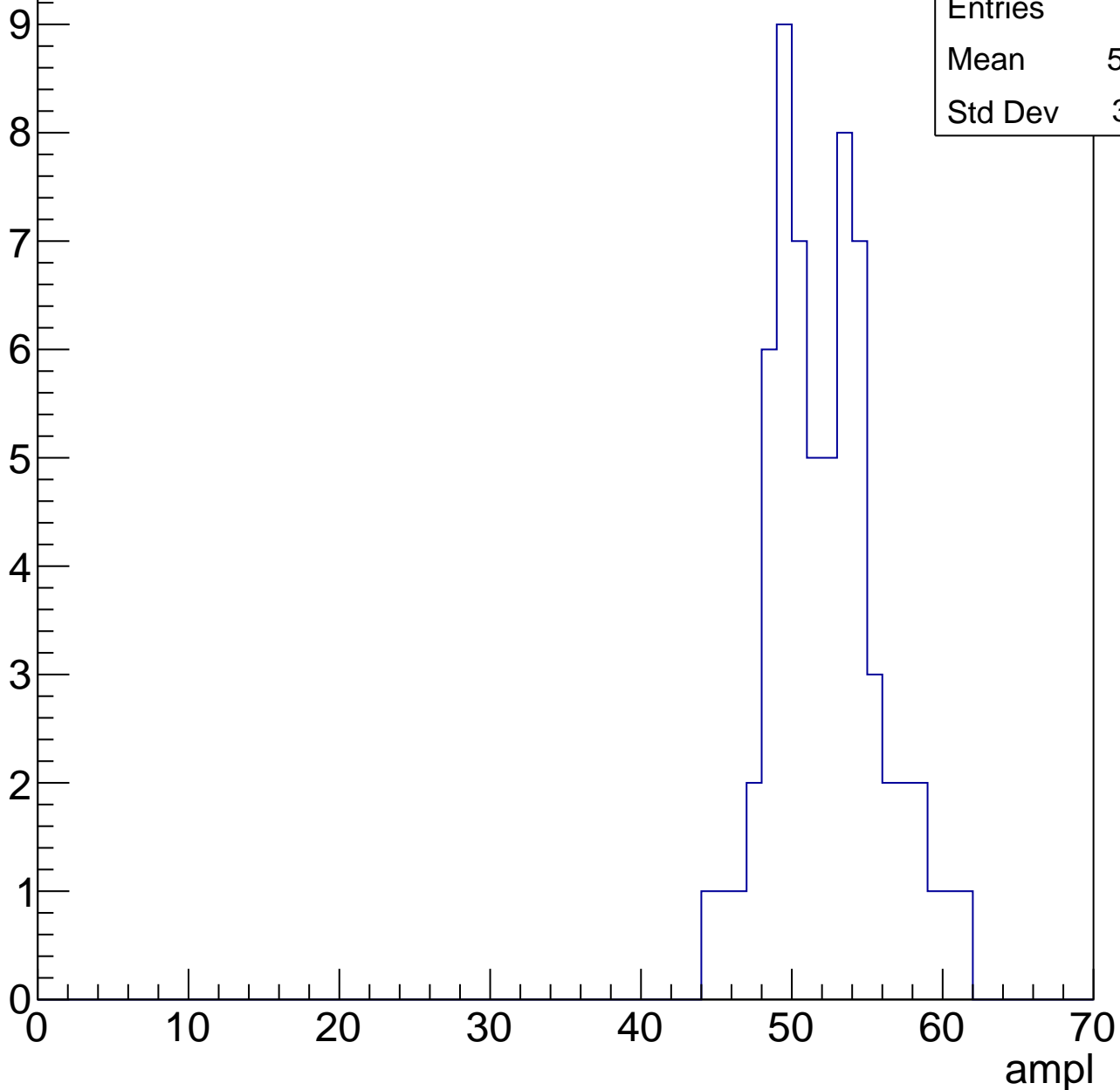


B1L103S, U21-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	51.75
Std Dev	3.571

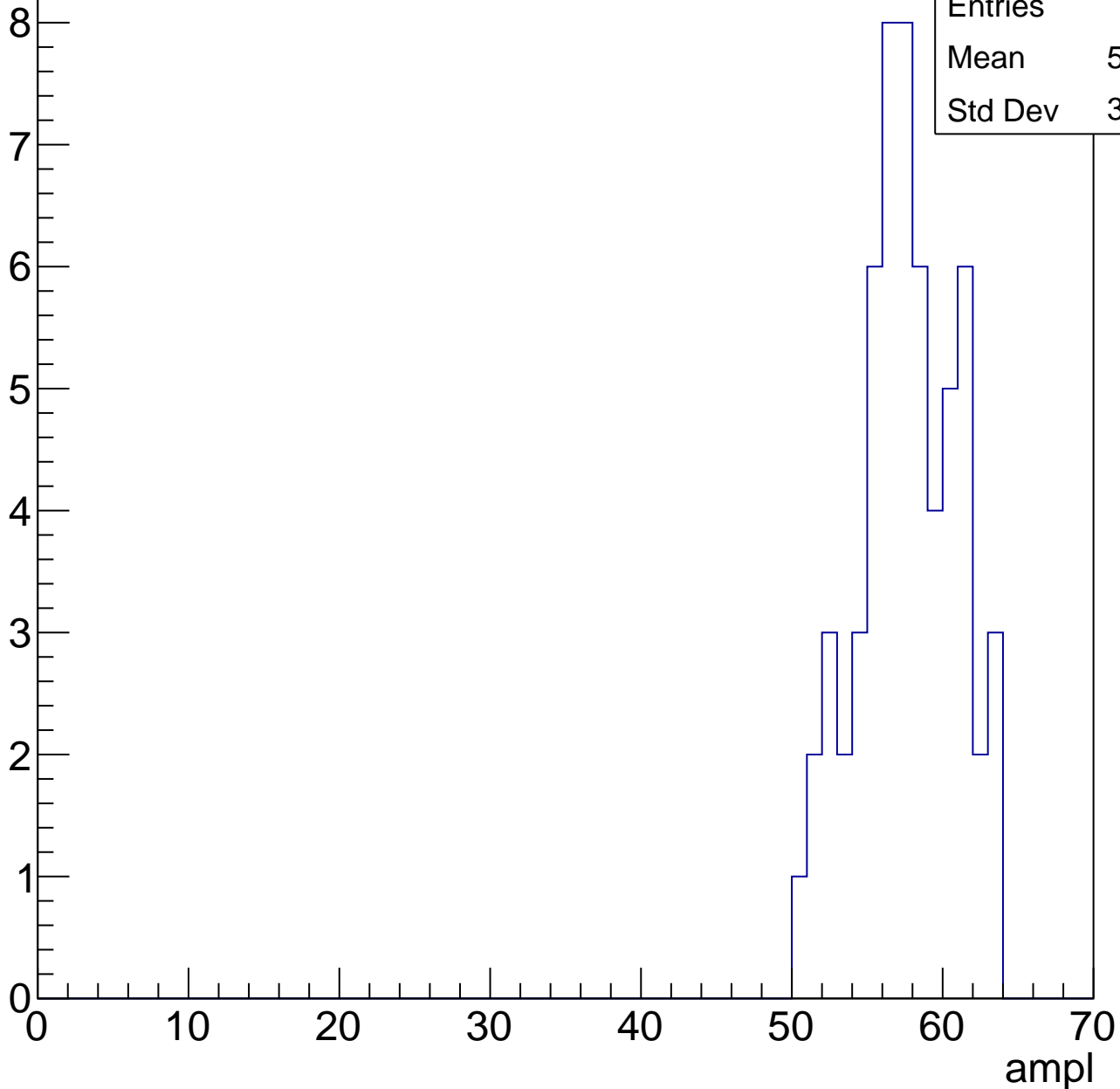


B1L103S, U21-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.17
Std Dev	3.206

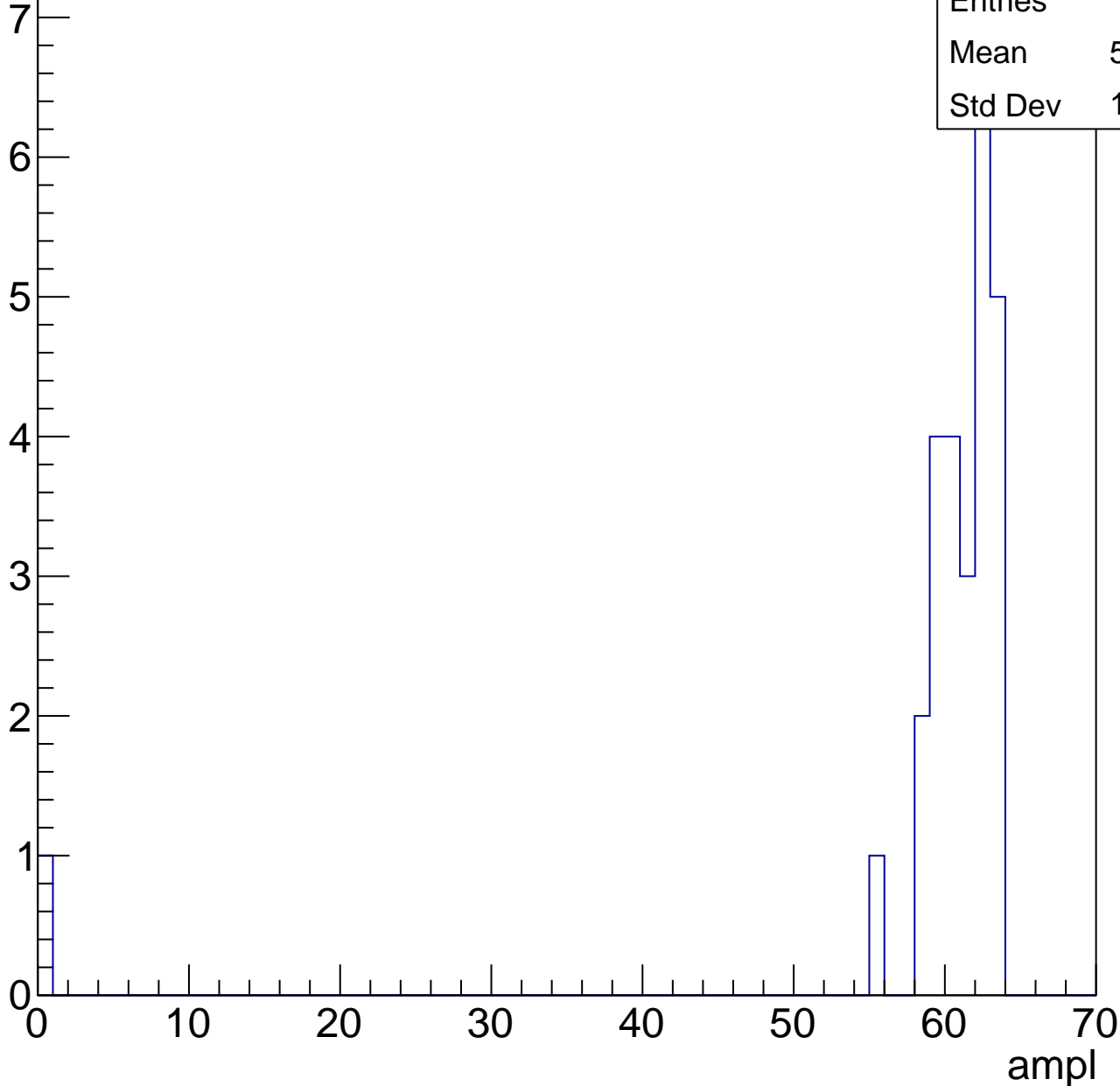


B1L103S, U21-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	58.48
Std Dev	11.63

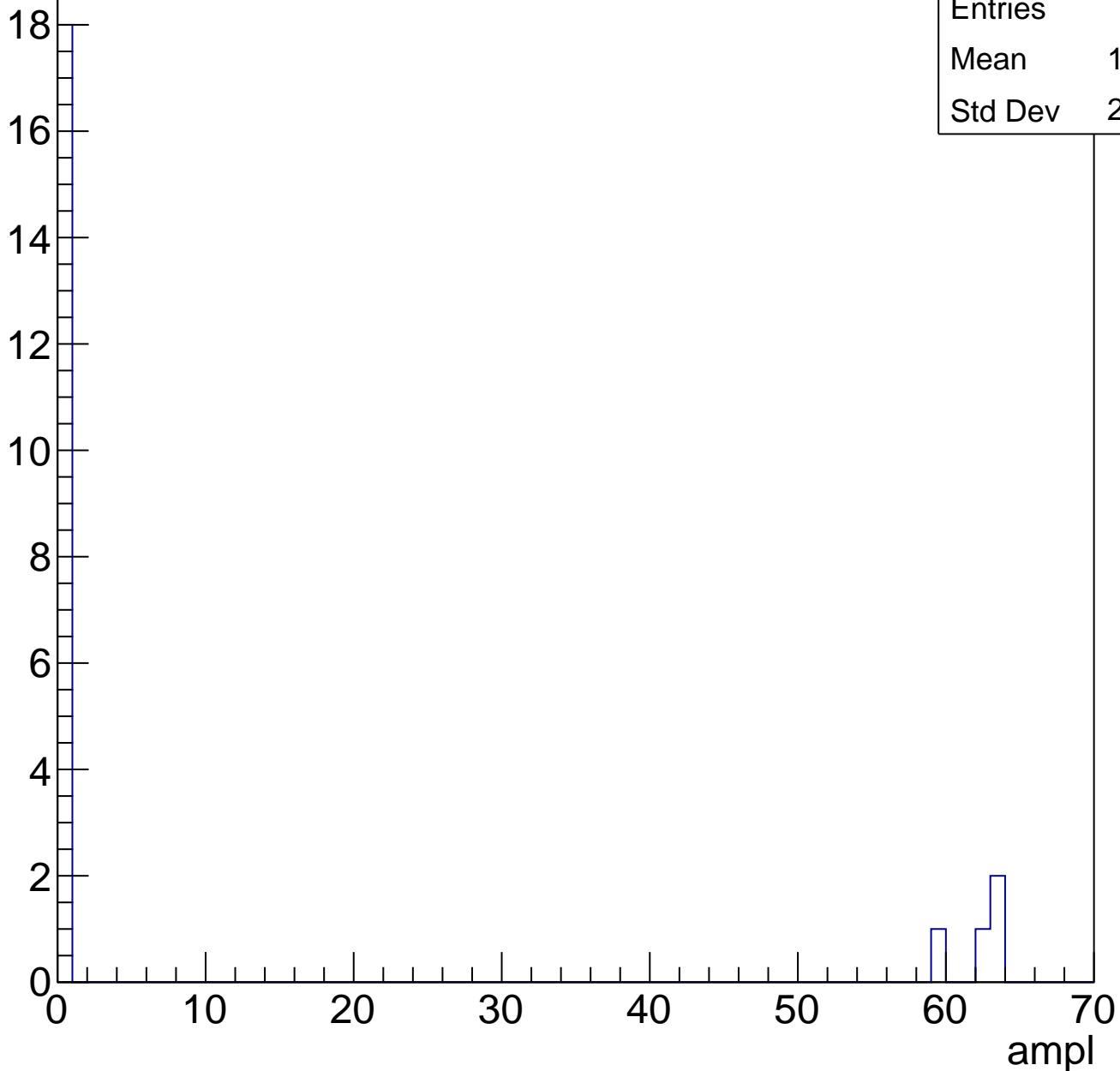


B1L103S, U21-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	11.23
Std Dev	23.83

Entry



B1L103S, U21-ch91, adc0

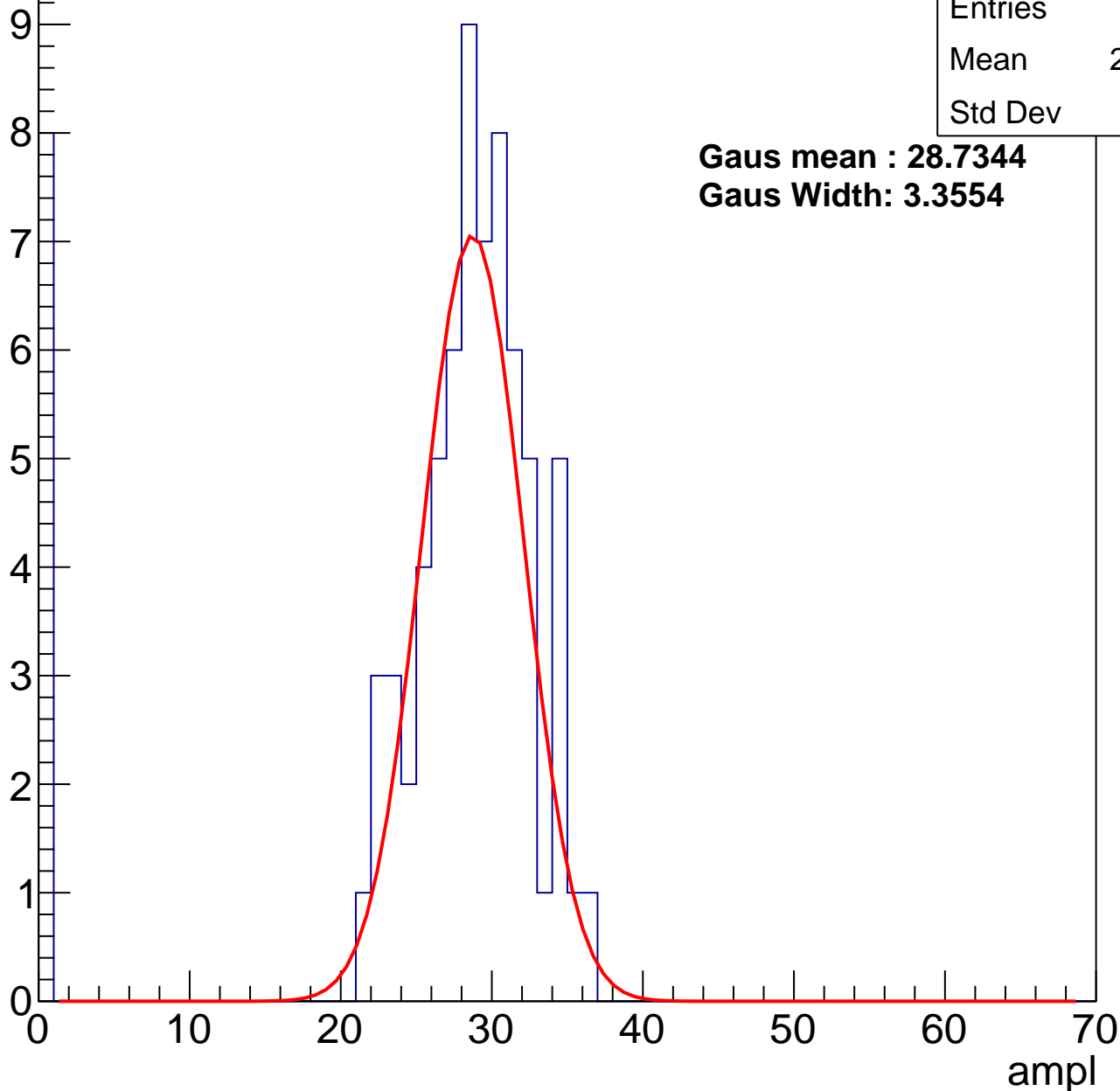
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	25.48
Std Dev	9.39

Gaus mean : 28.7344

Gaus Width: 3.3554



B1L103S, U21-ch91, adc1

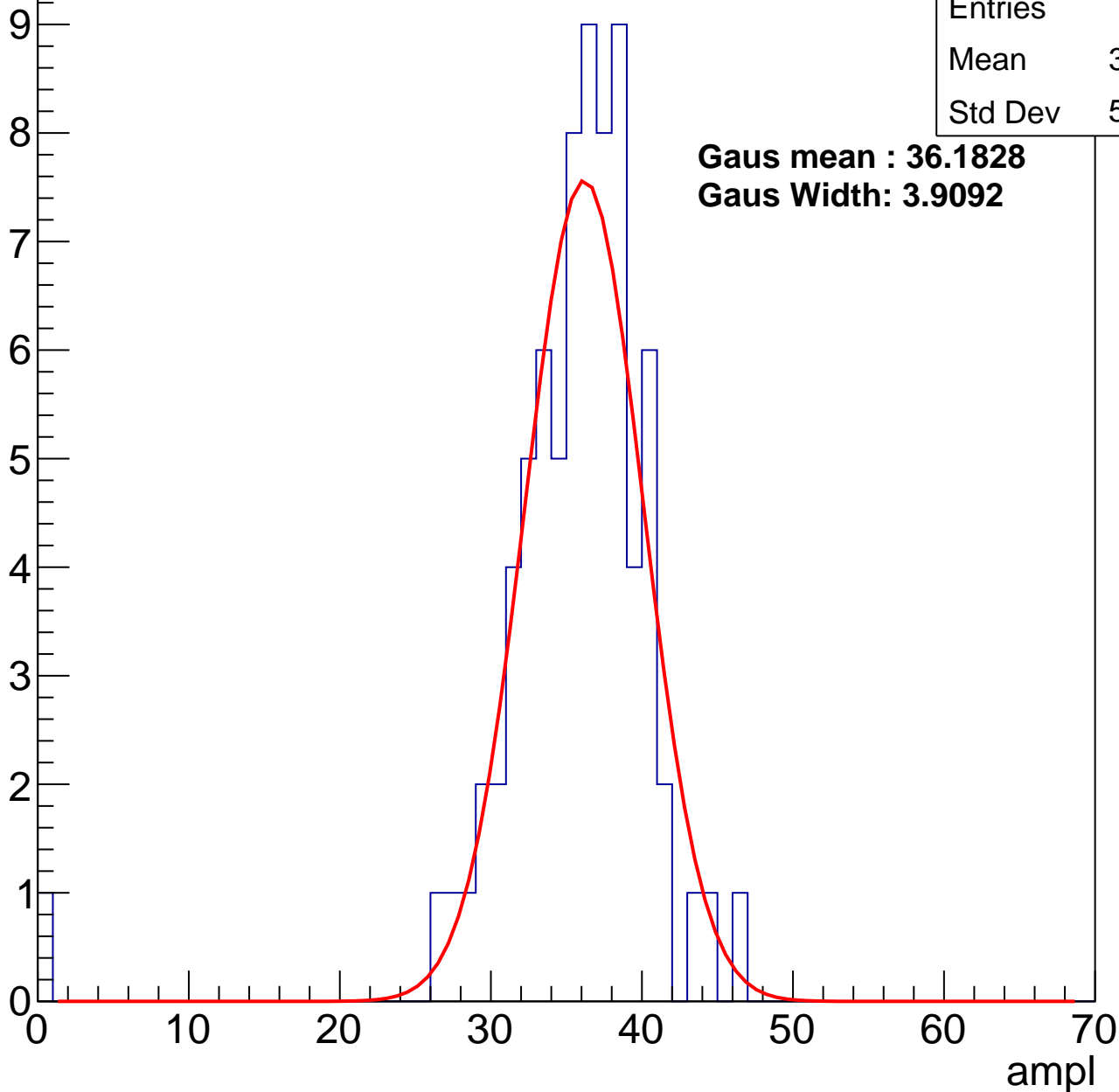
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	35.12
Std Dev	5.527

Gaus mean : 36.1828

Gaus Width: 3.9092



B1L103S, U21-ch91, adc2

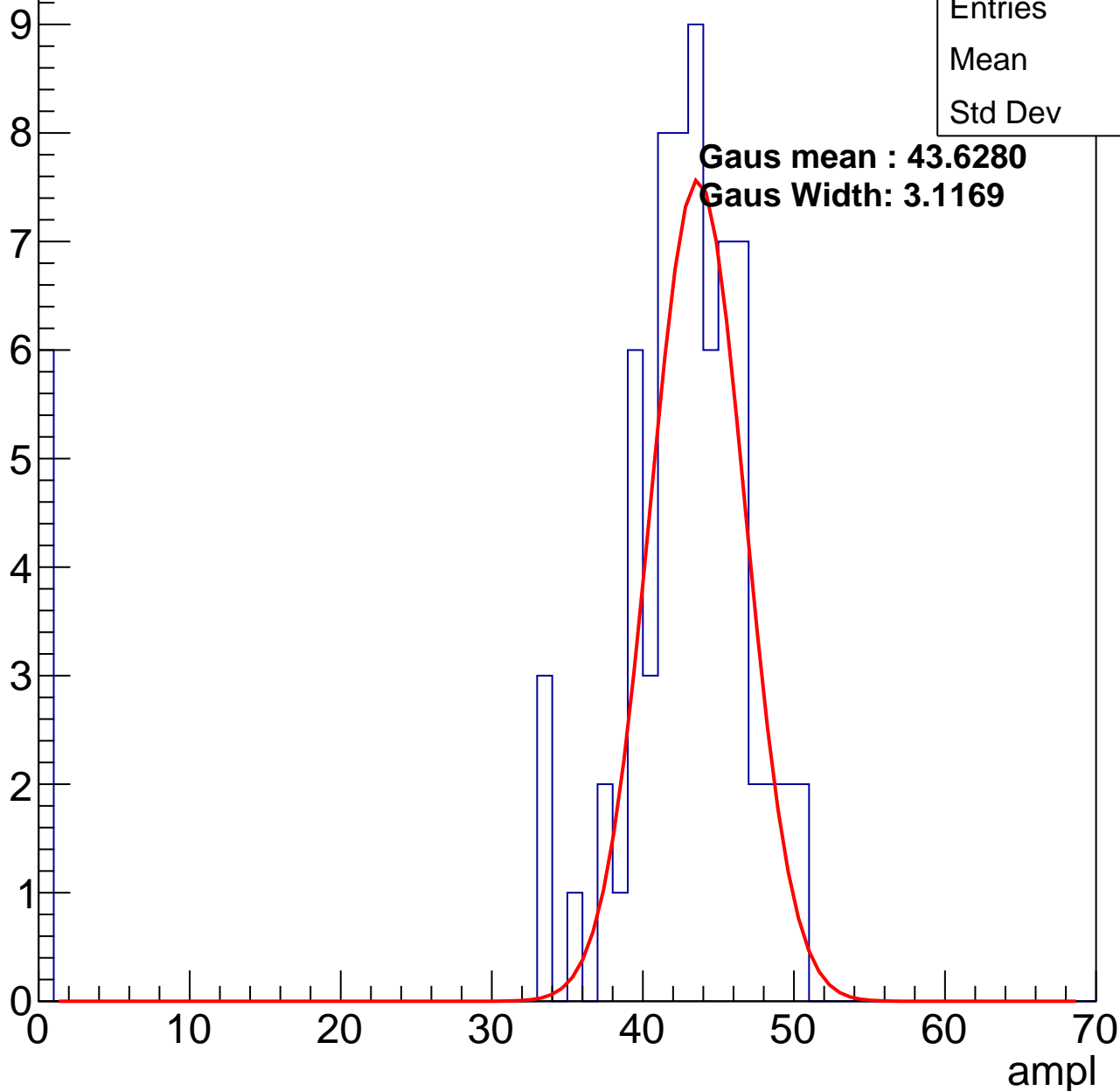
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	39.2
Std Dev	12.1

Gaus mean : 43.6280

Gaus Width: 3.1169

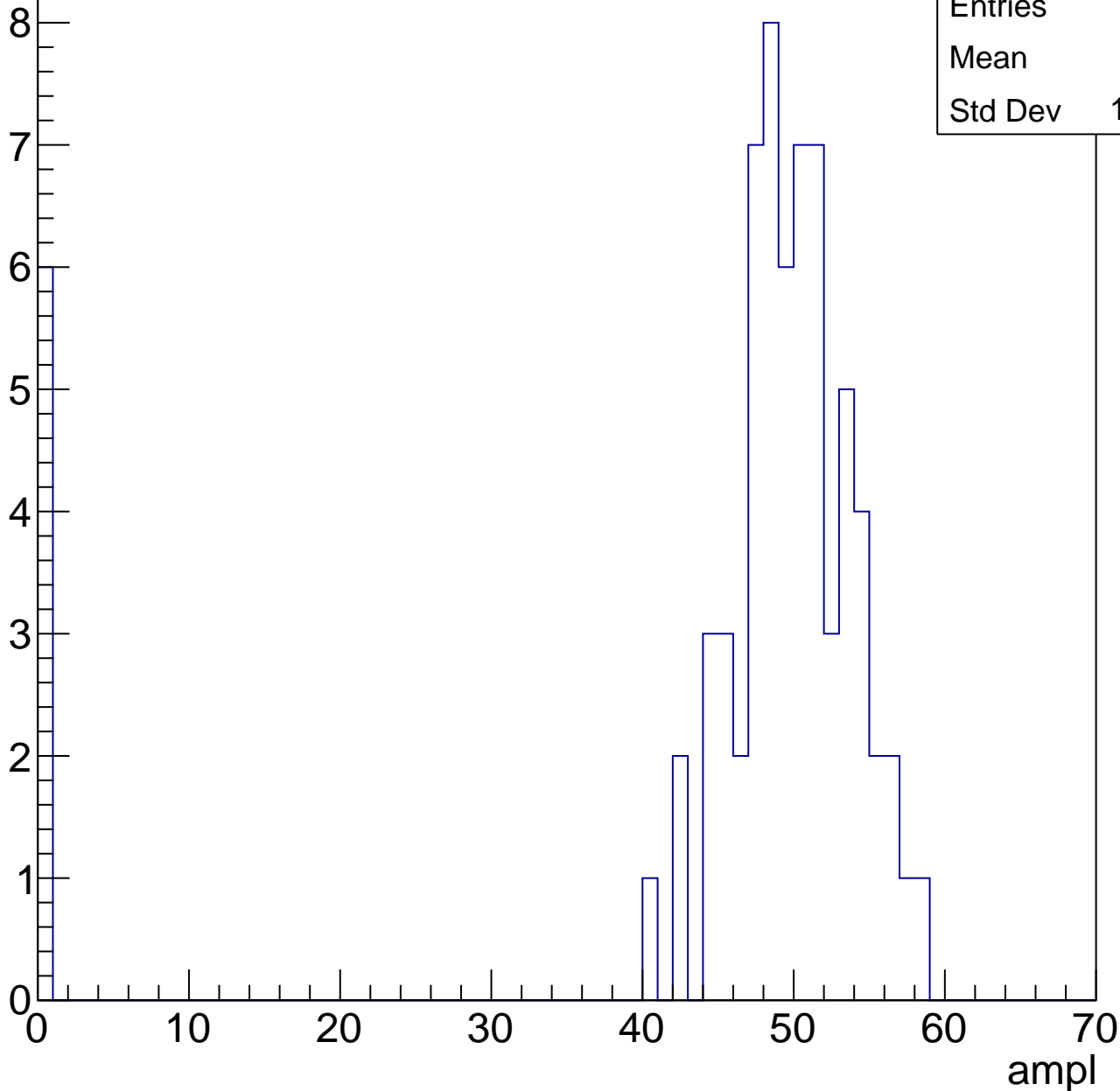


B1L103S, U21-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	45.3
Std Dev	14.32

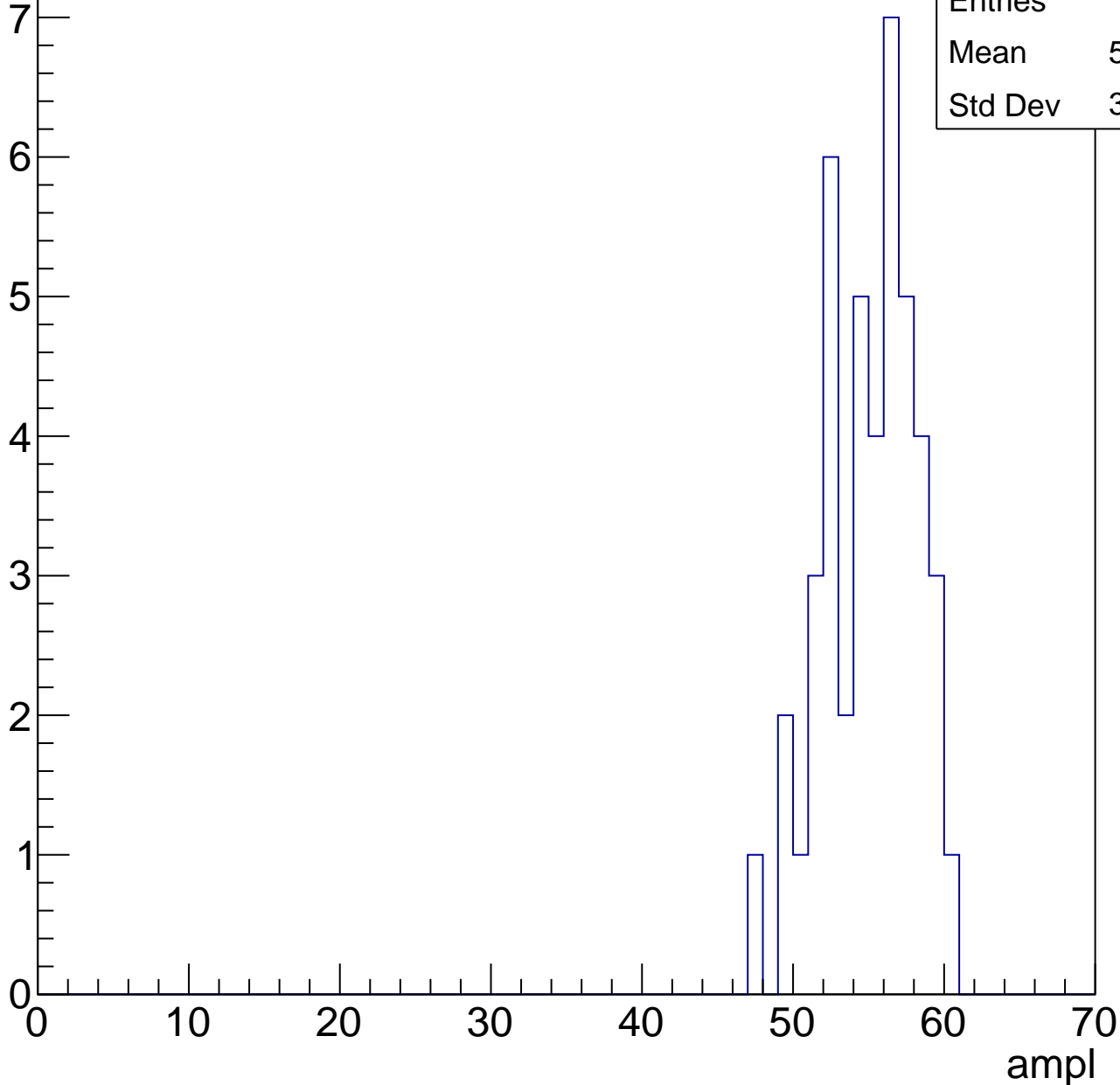


B1L103S, U21-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

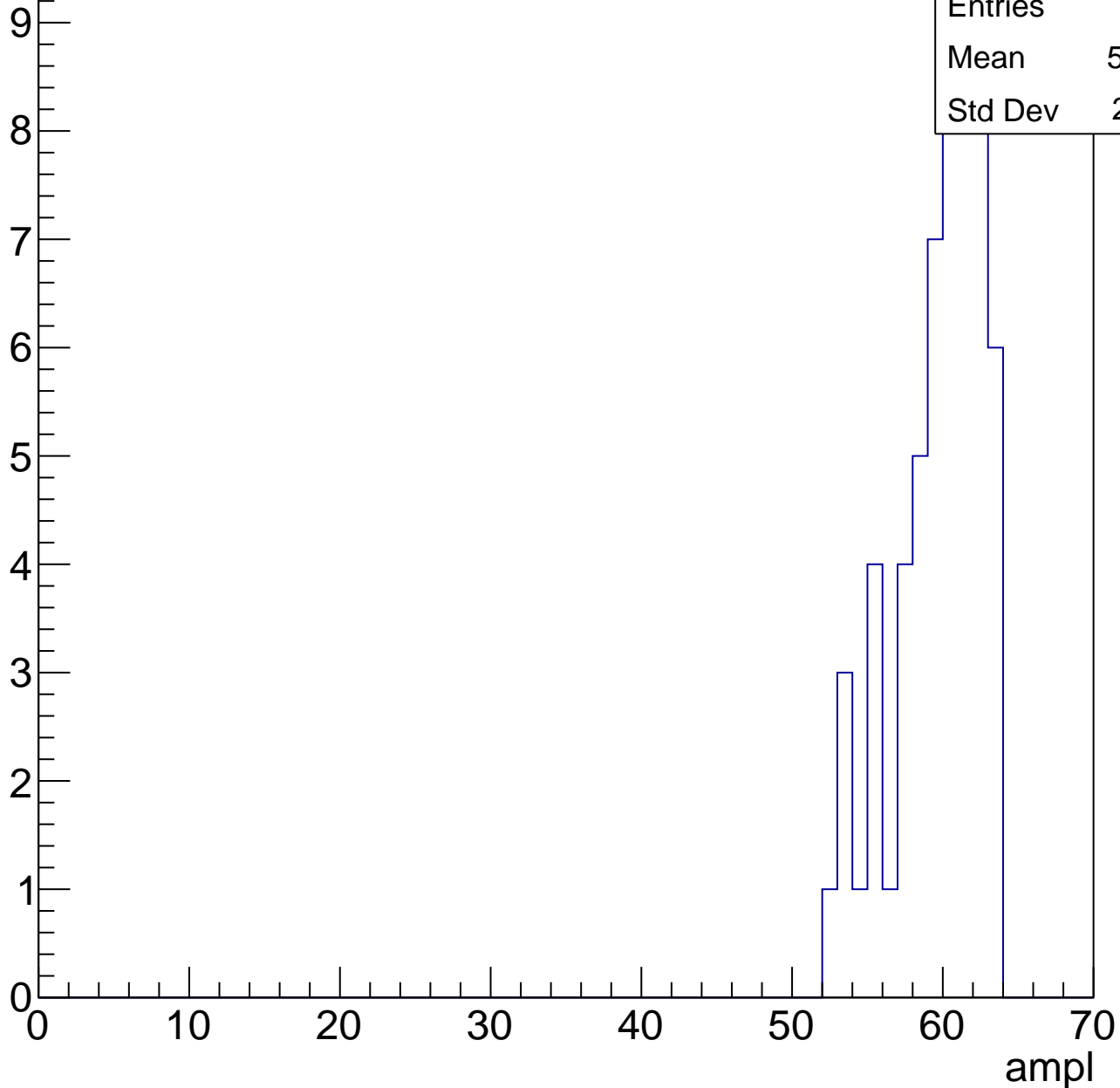
Entries	44
Mean	54.59
Std Dev	3.033



B1L103S, U21-ch91, adc5

calib_packv5_041523_1651.root, FC#0, port C2

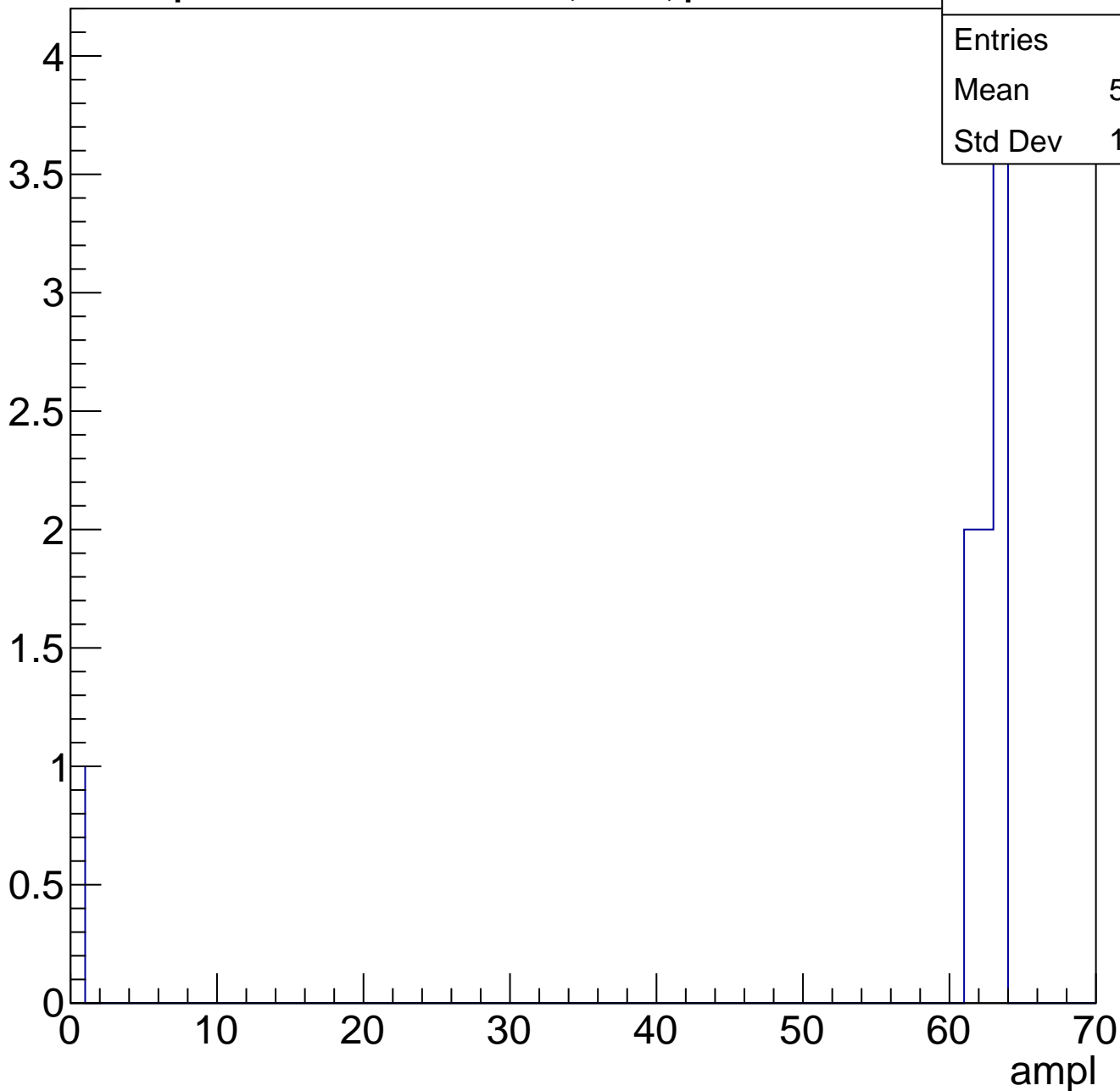
Entry



B1L103S, U21-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	9
Mean	55.33
Std Dev	19.58

B1L103S, U21-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch92, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	22.91
Std Dev	10.26

Gaus mean : 27.6374

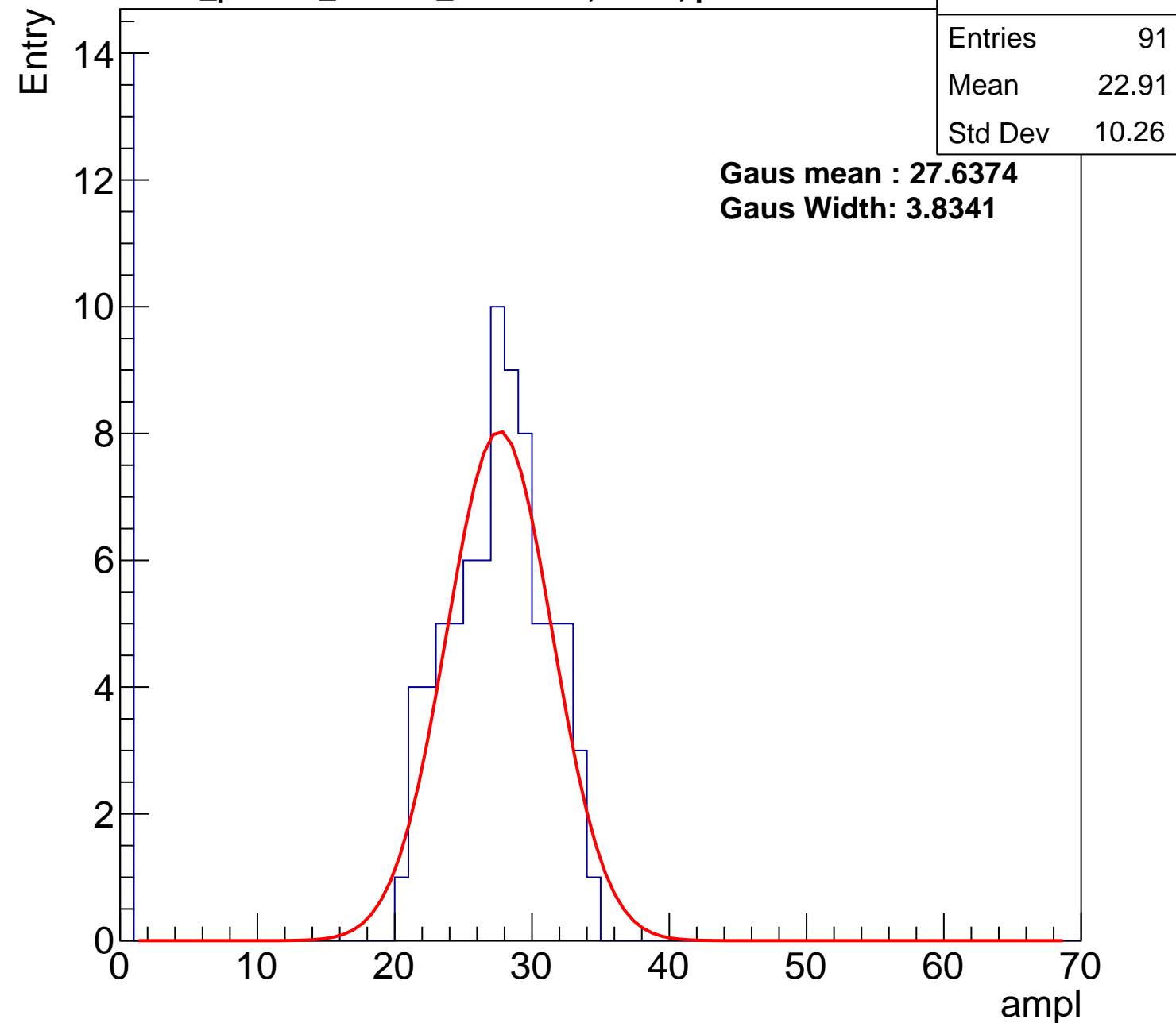
Gaus Width: 3.8341

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch92, adc1

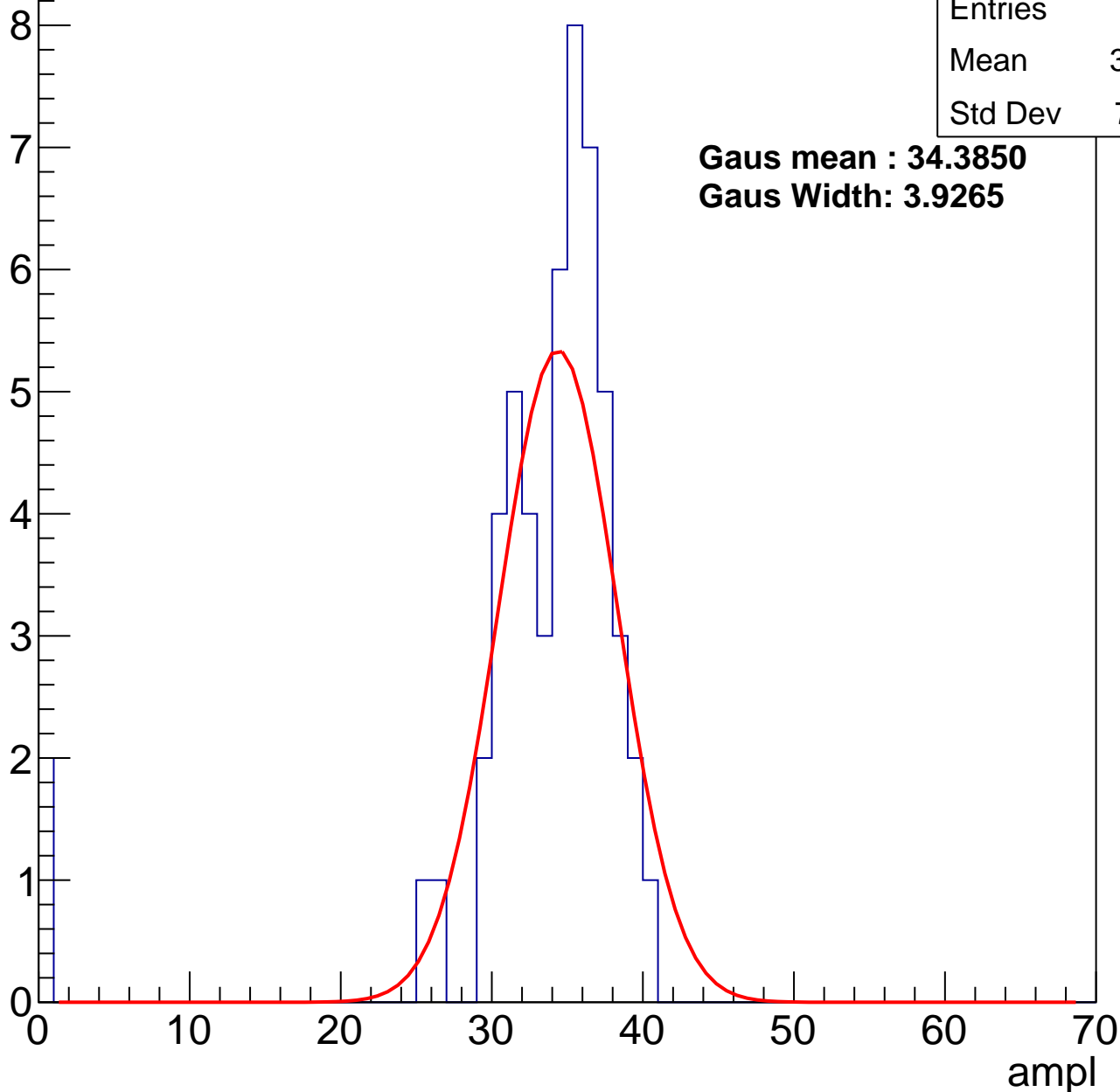
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	32.67
Std Dev	7.141

Gaus mean : 34.3850

Gaus Width: 3.9265



B1L103S, U21-ch92, adc2

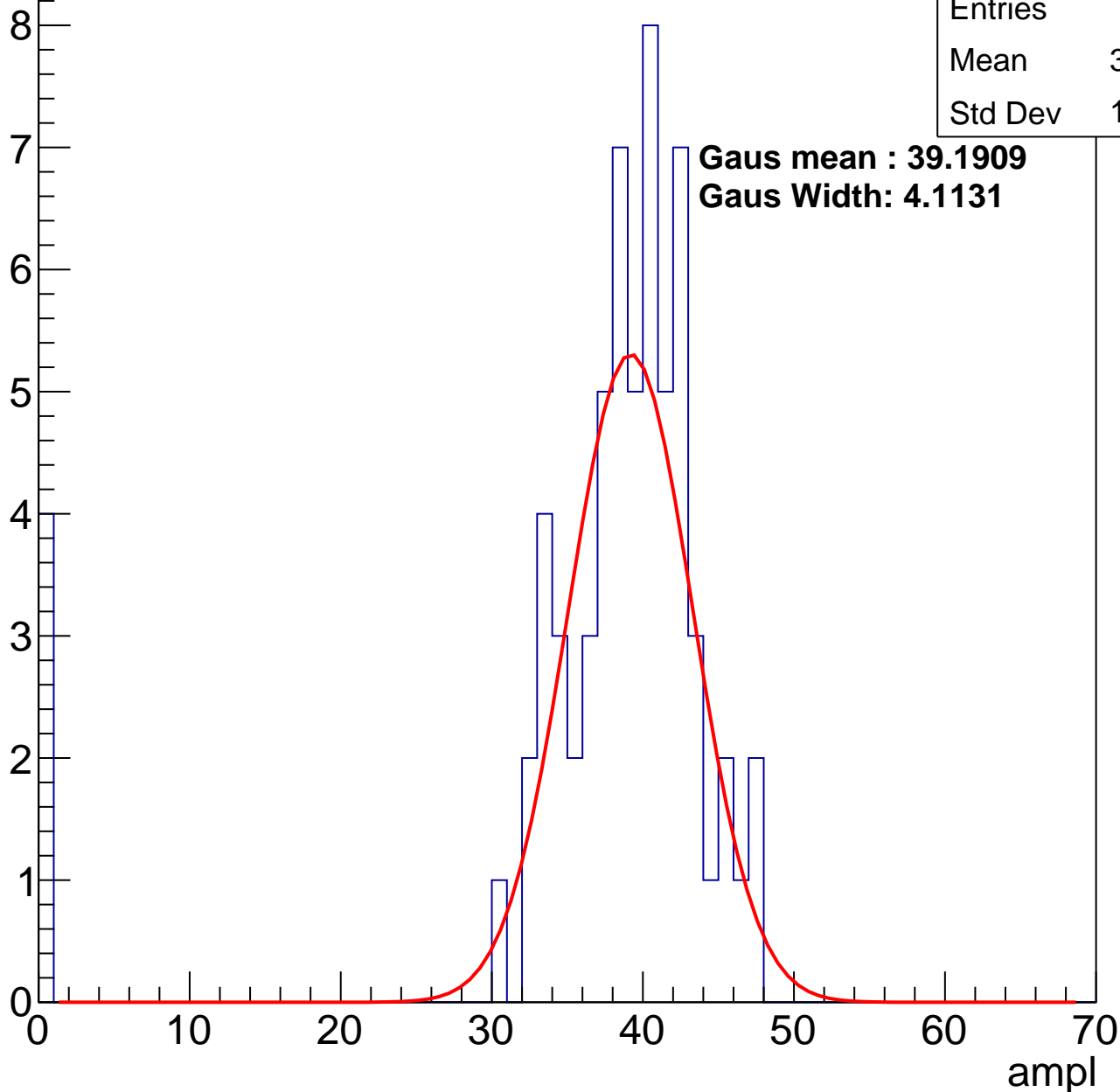
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.52
Std Dev	10.06

Gaus mean : 39.1909

Gaus Width: 4.1131

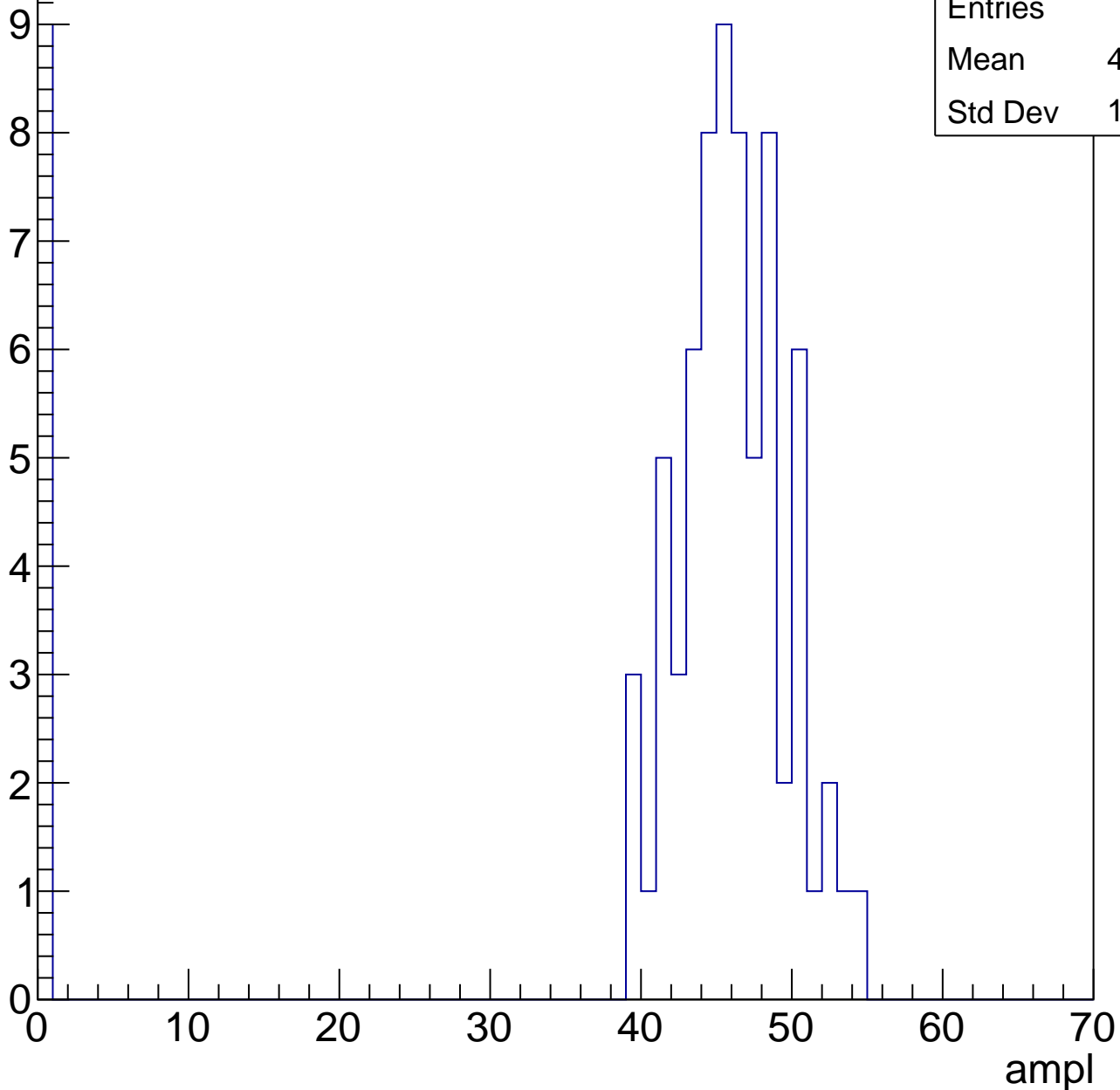


B1L103S, U21-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	40.38
Std Dev	14.93

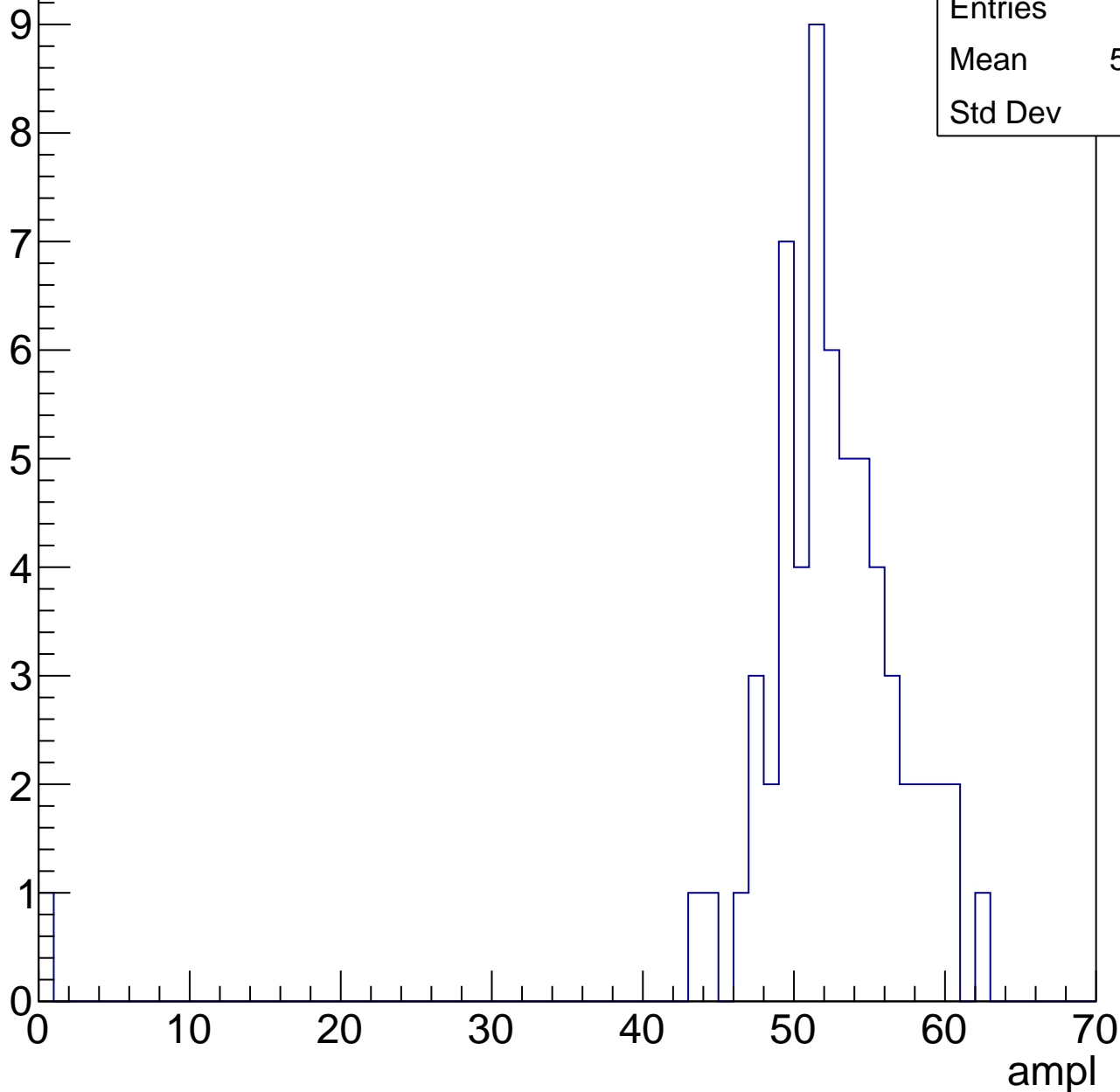


B1L103S, U21-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	51.43
Std Dev	7.7

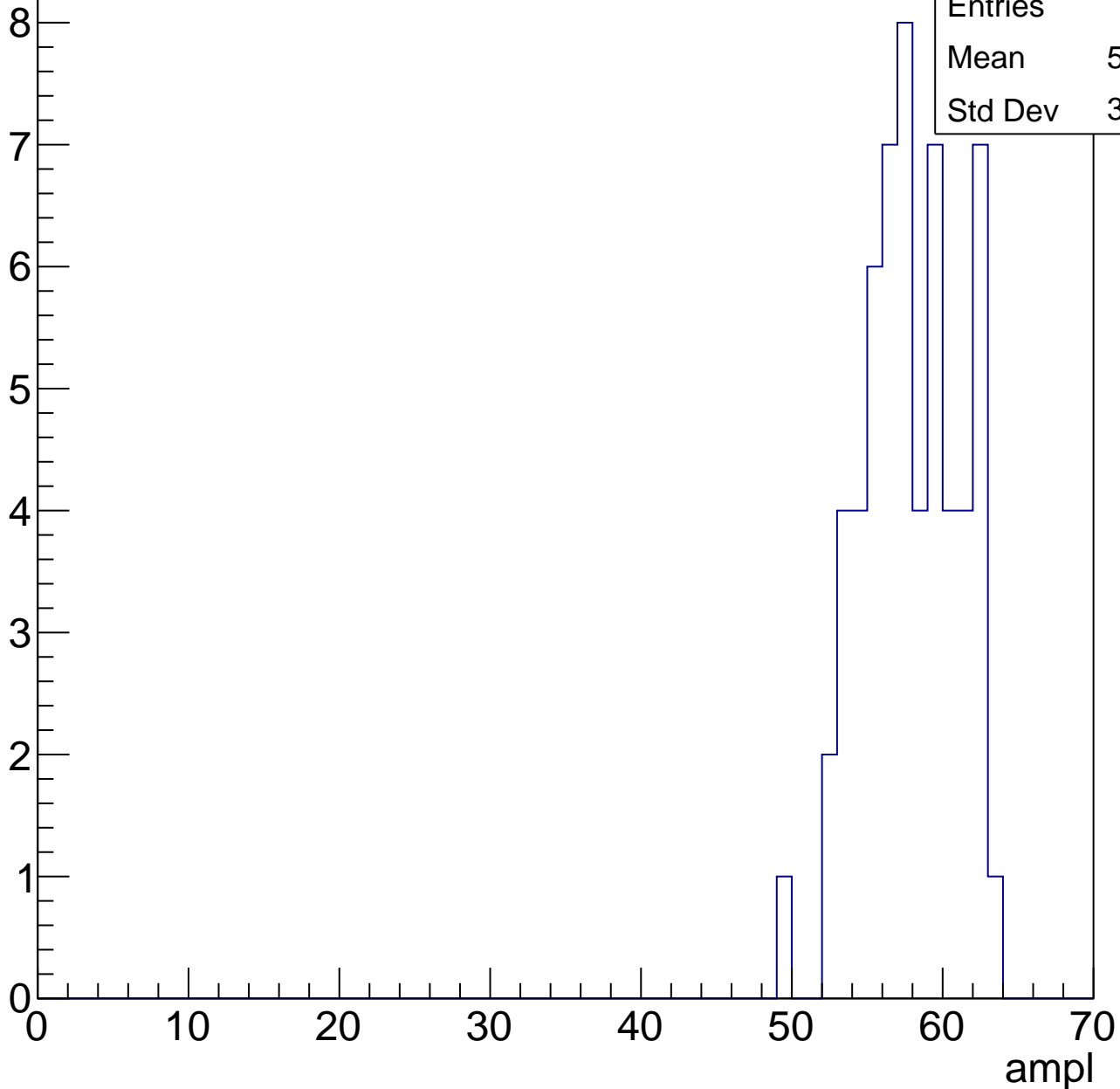


B1L103S, U21-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.37
Std Dev	3.135

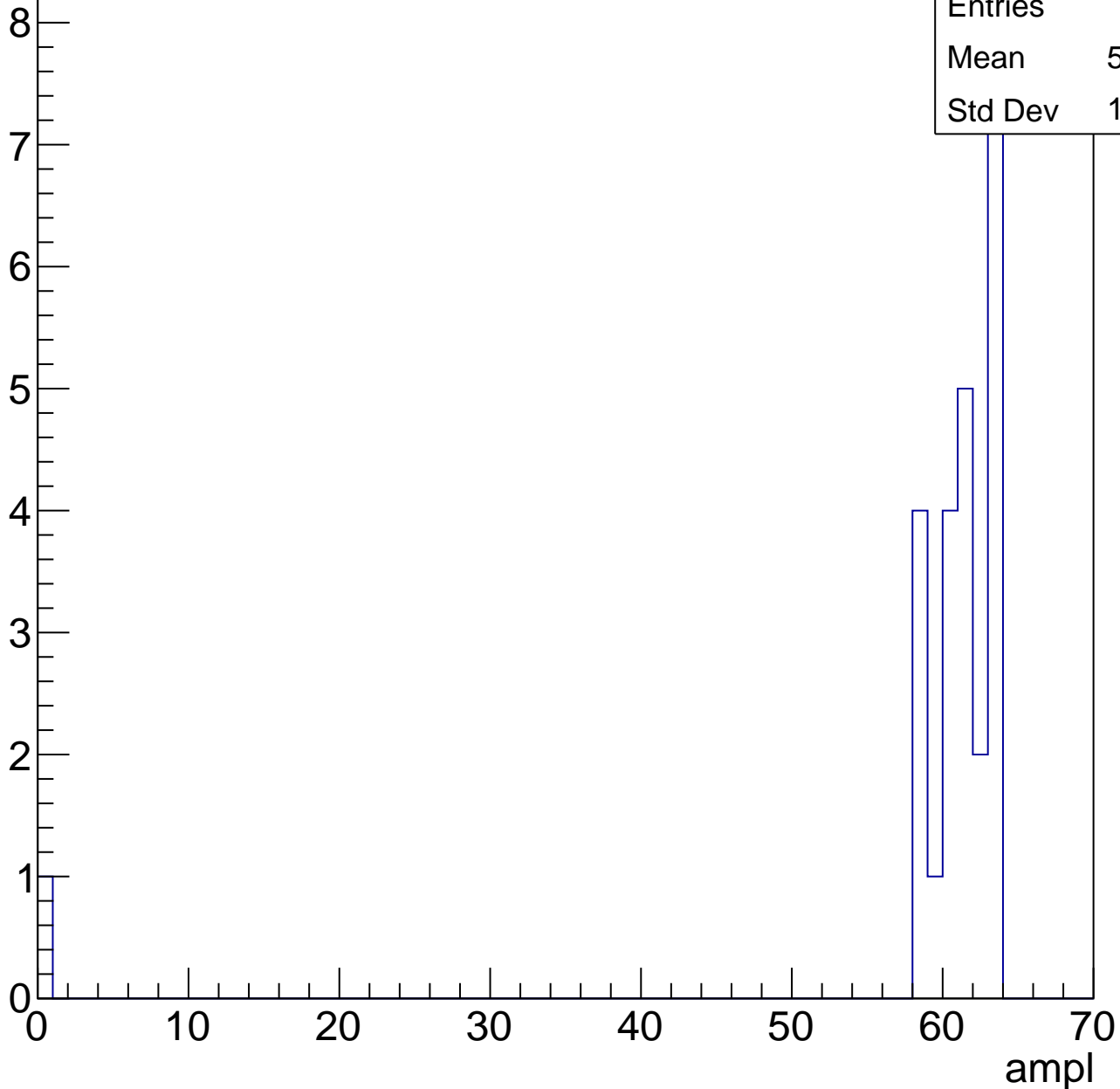


B1L103S, U21-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	58.56
Std Dev	12.08

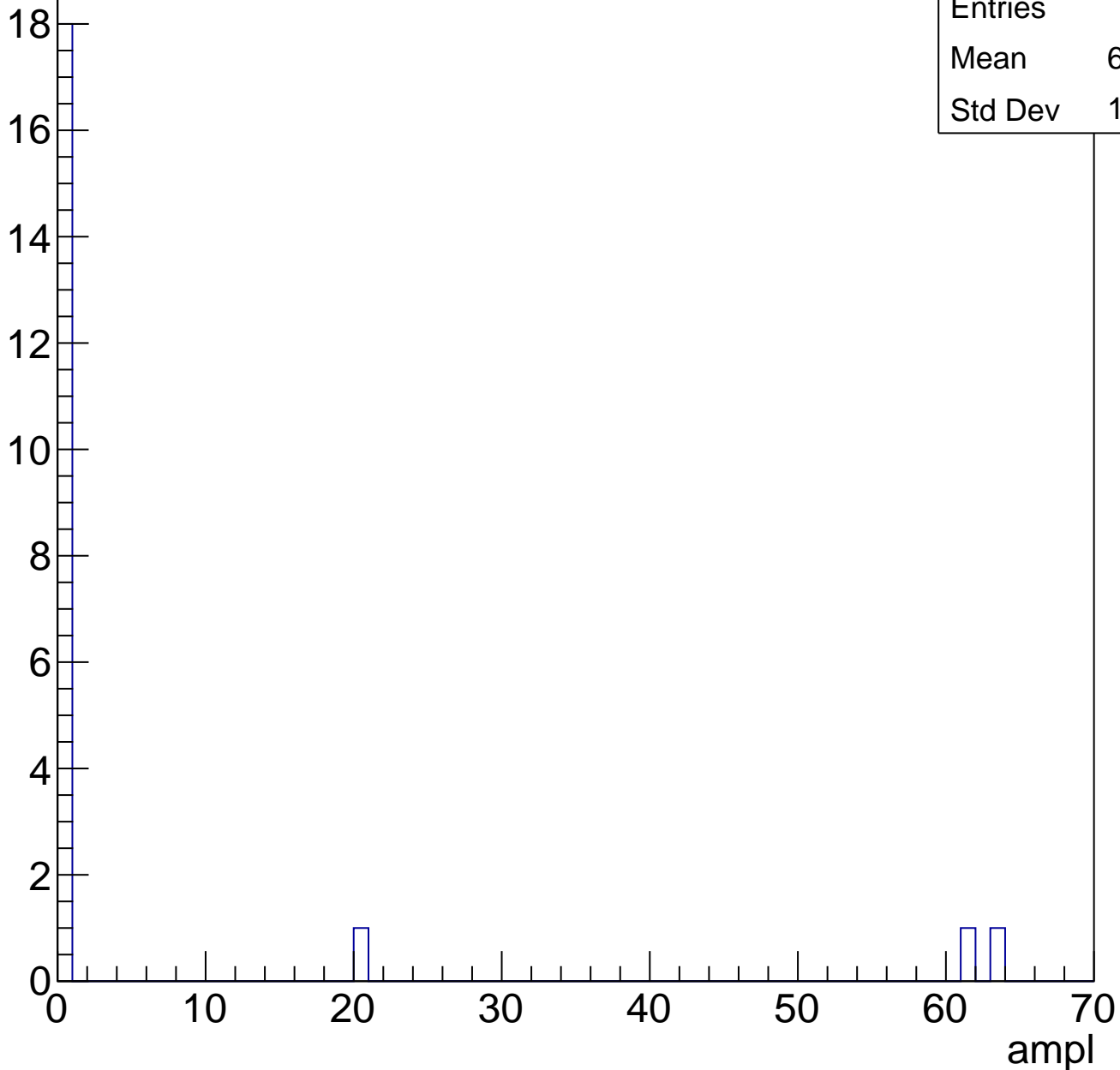


B1L103S, U21-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.857
Std Dev	18.39

Entry



B1L103S, U21-ch93, adc0

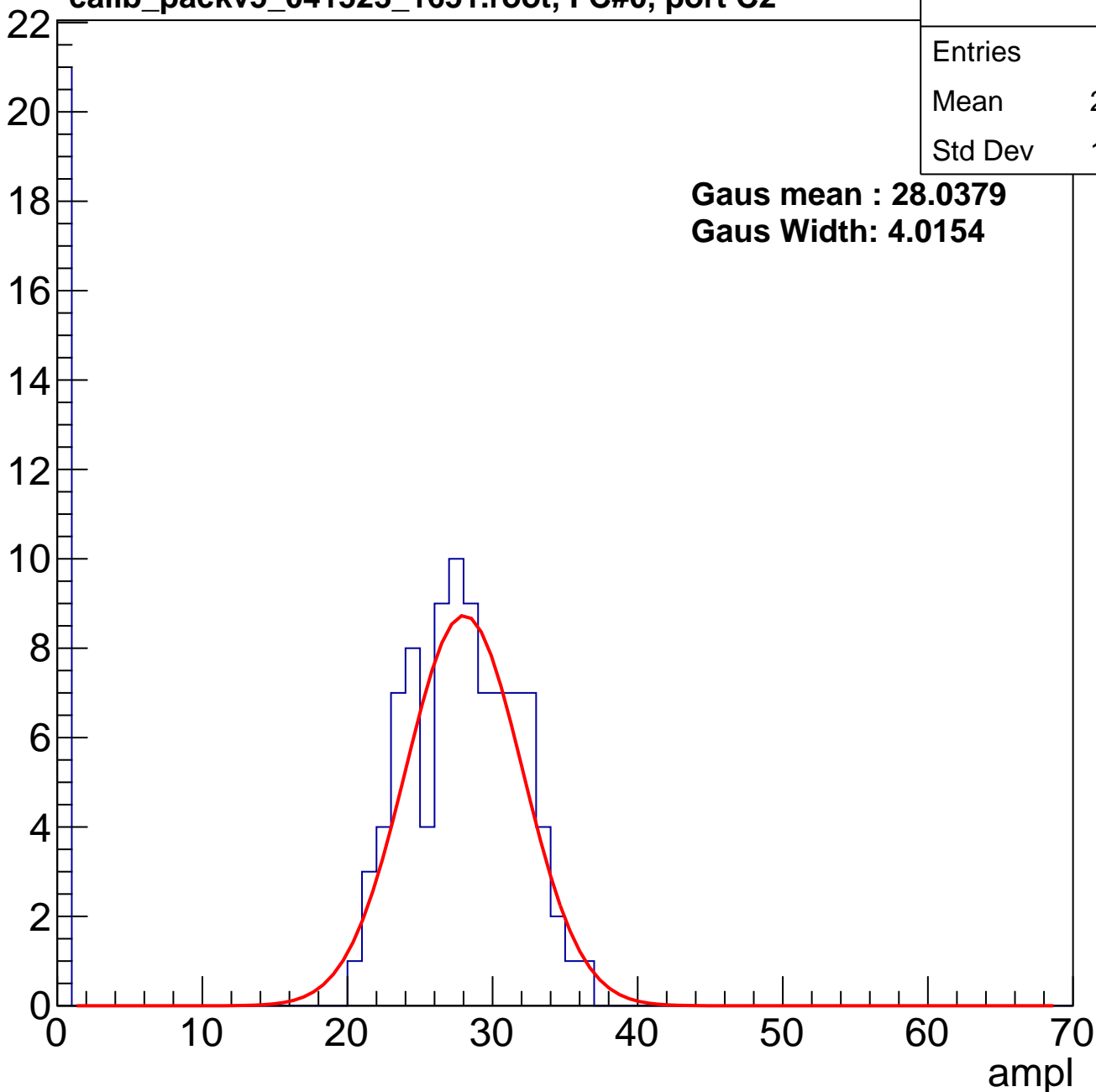
calib_packv5_041523_1651.root, FC#0, port C2

Entries	112
Mean	22.37
Std Dev	11.24

Gaus mean : 28.0379

Gaus Width: 4.0154

Entry



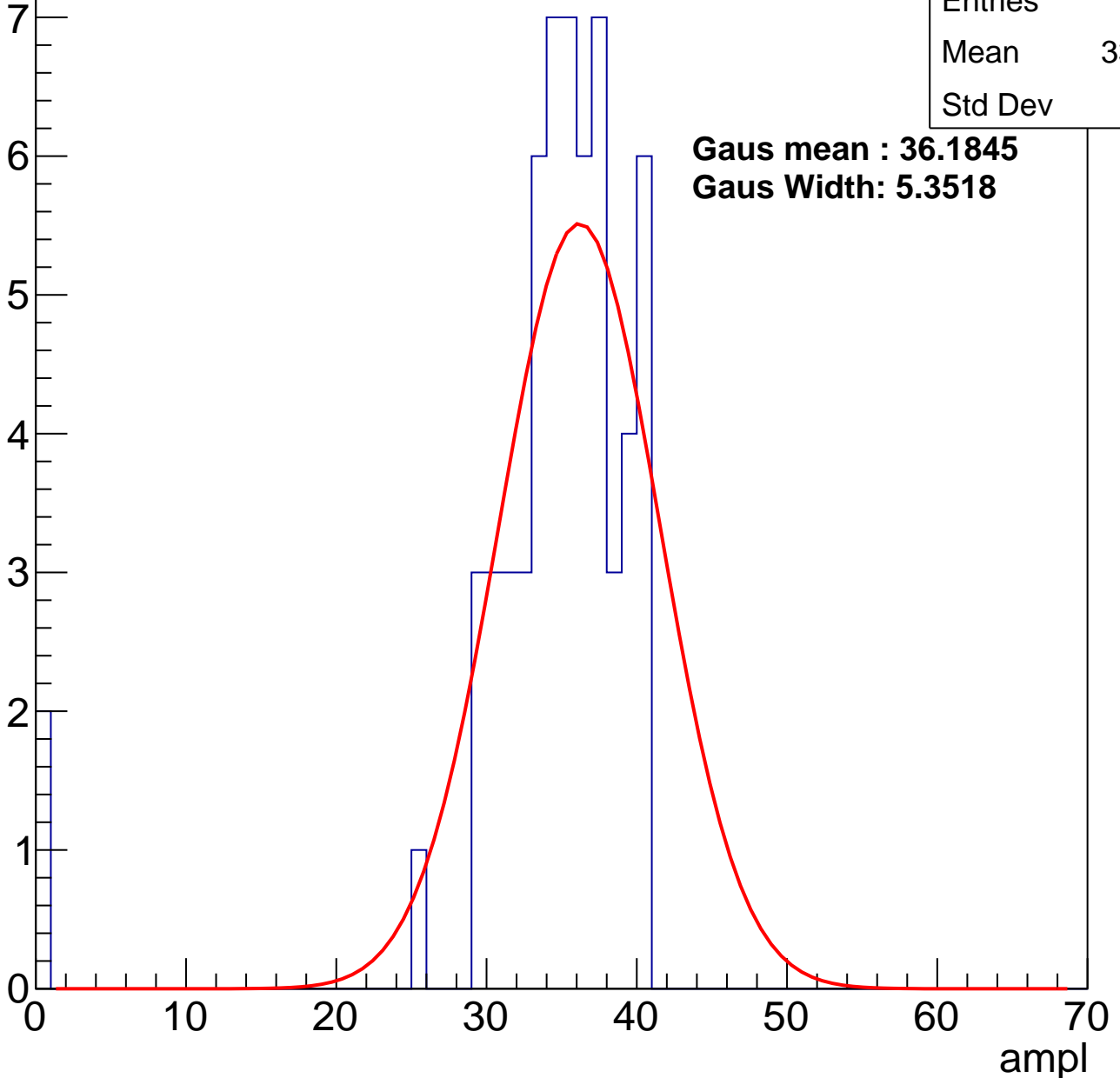
B1L103S, U21-ch93, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	33.72
Std Dev	7.03

Gaus mean : 36.1845
Gaus Width: 5.3518



B1L103S, U21-ch93, adc2

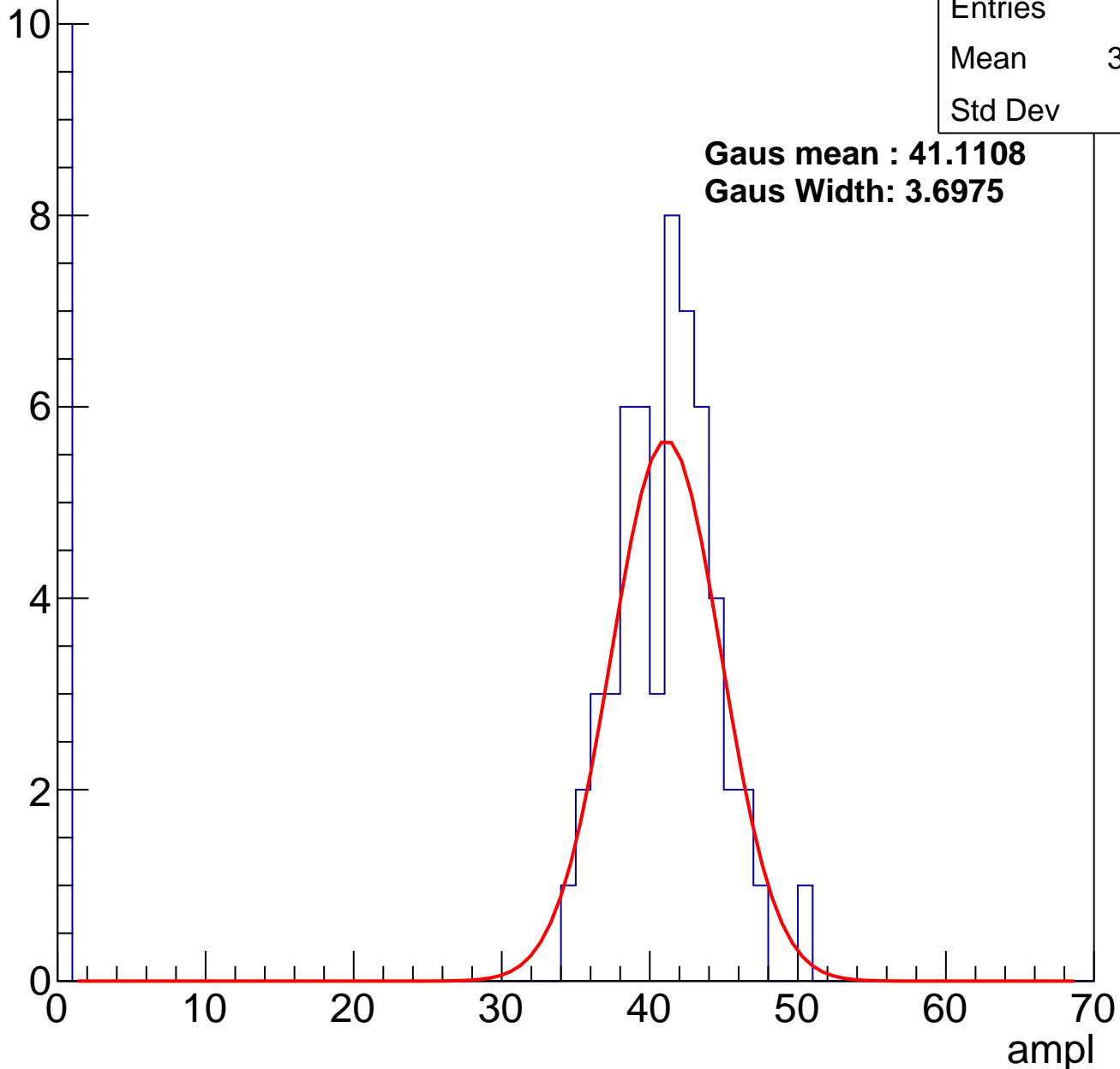
calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	34.46
Std Dev	15

Gaus mean : 41.1108

Gaus Width: 3.6975

Entry

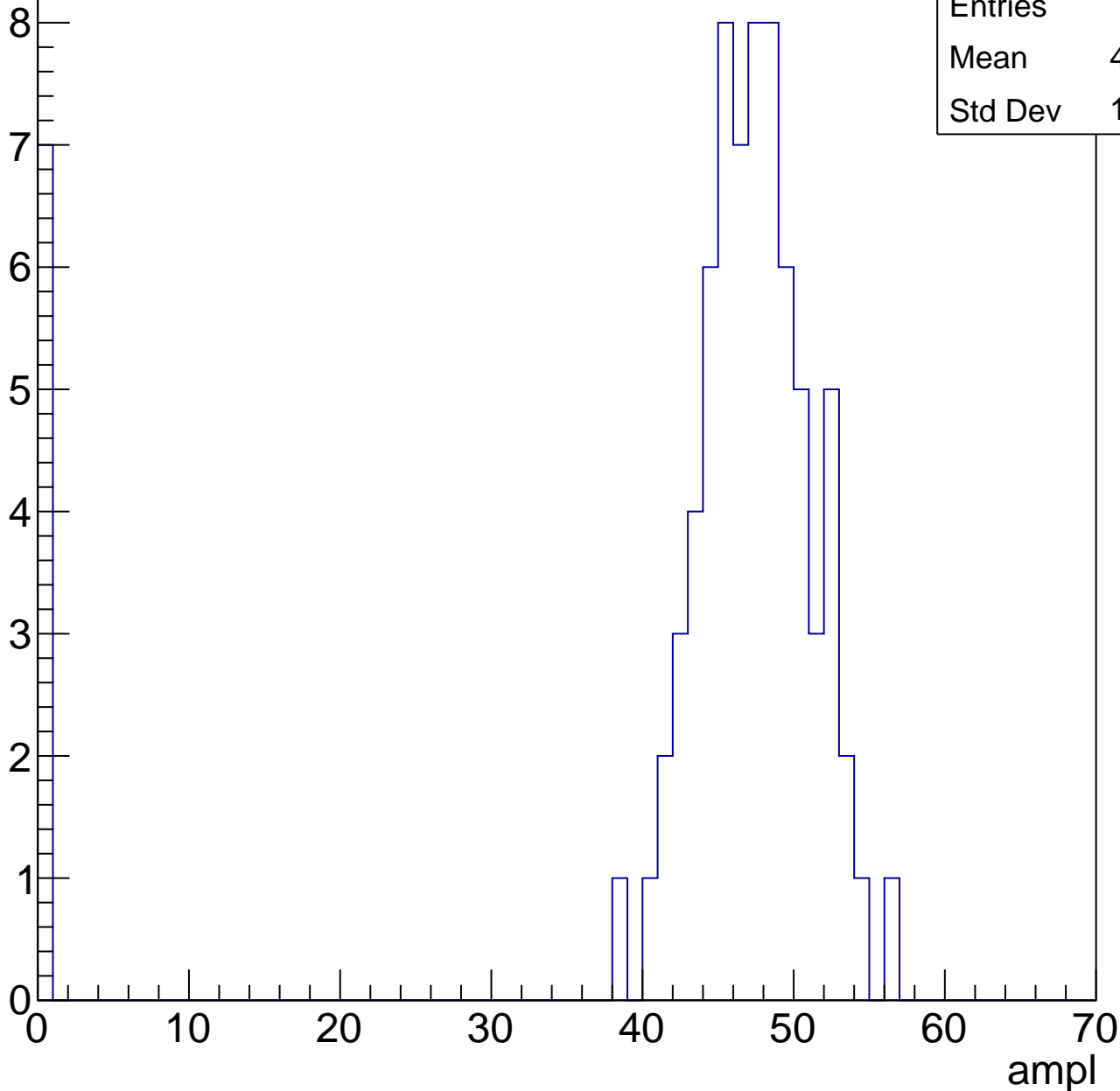


B1L103S, U21-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	42.78
Std Dev	13.85

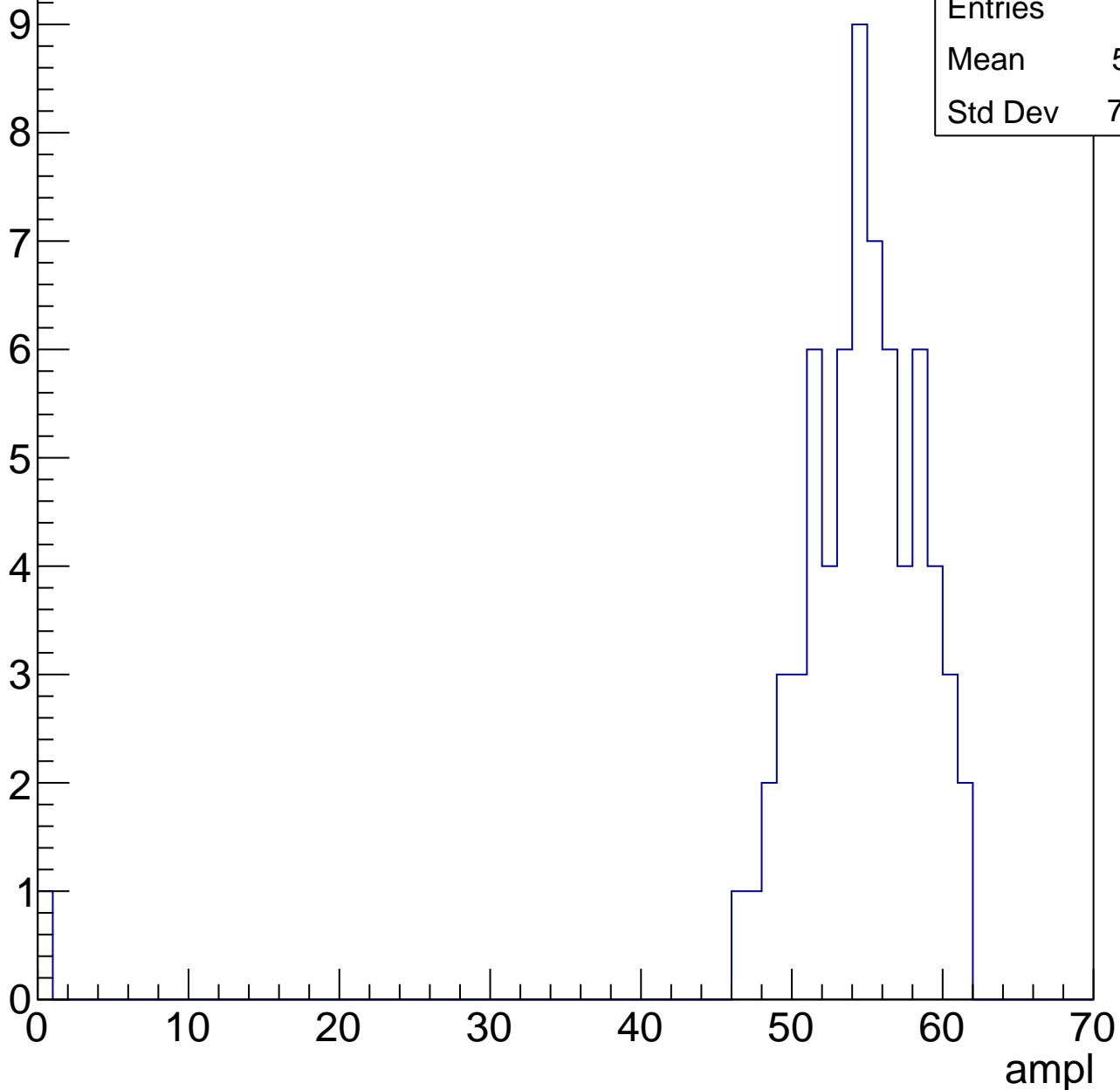


B1L103S, U21-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	53.51
Std Dev	7.429



B1L103S, U21-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries

49

Mean

58.31

Std Dev

3.442

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

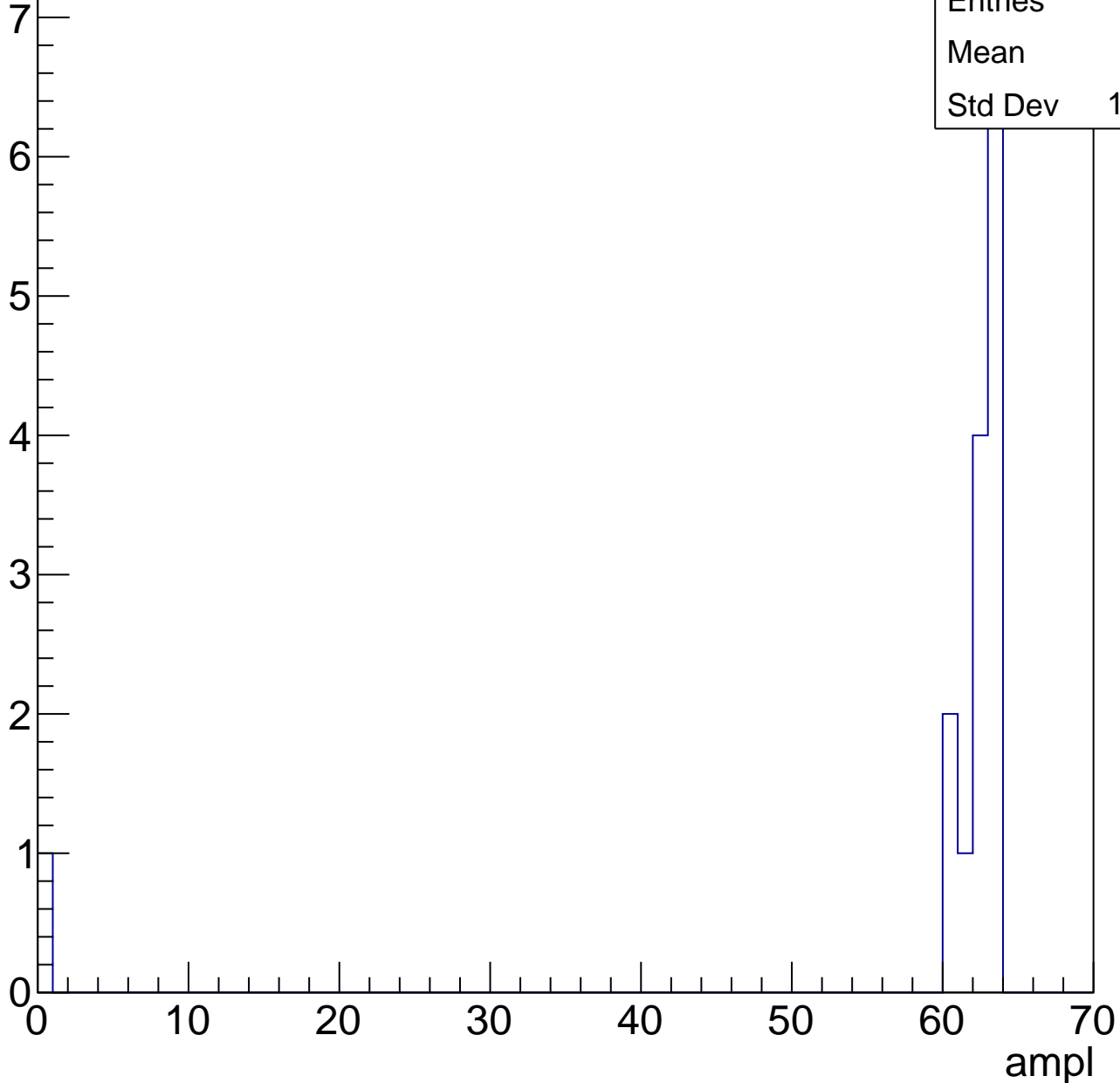
7

B1L103S, U21-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	58
Std Dev	15.53



B1L103S, U21-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U21-ch94, adc0

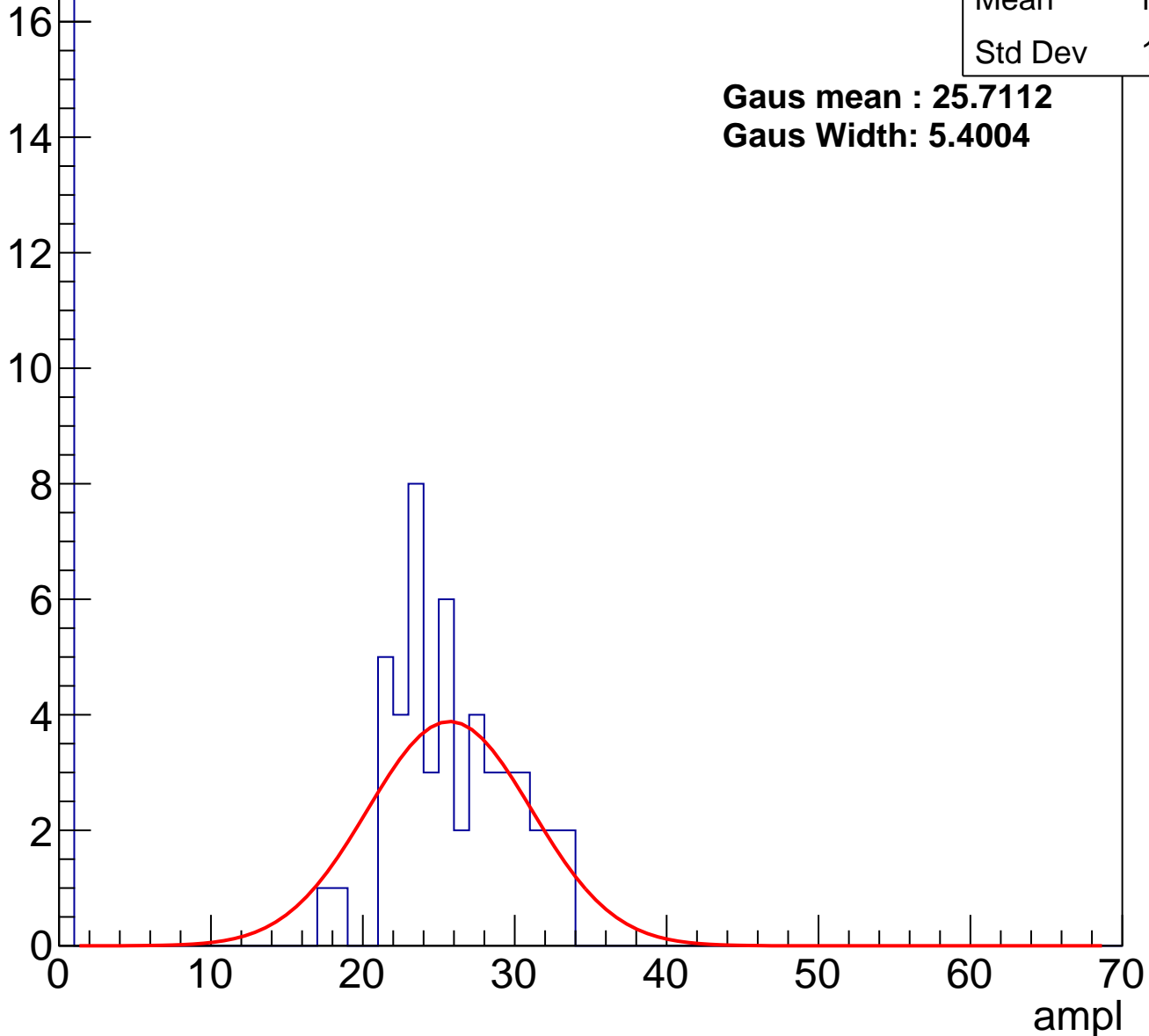
calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	18.89
Std Dev	11.61

Gaus mean : 25.7112

Gaus Width: 5.4004

Entry



B1L103S, U21-ch94, adc1

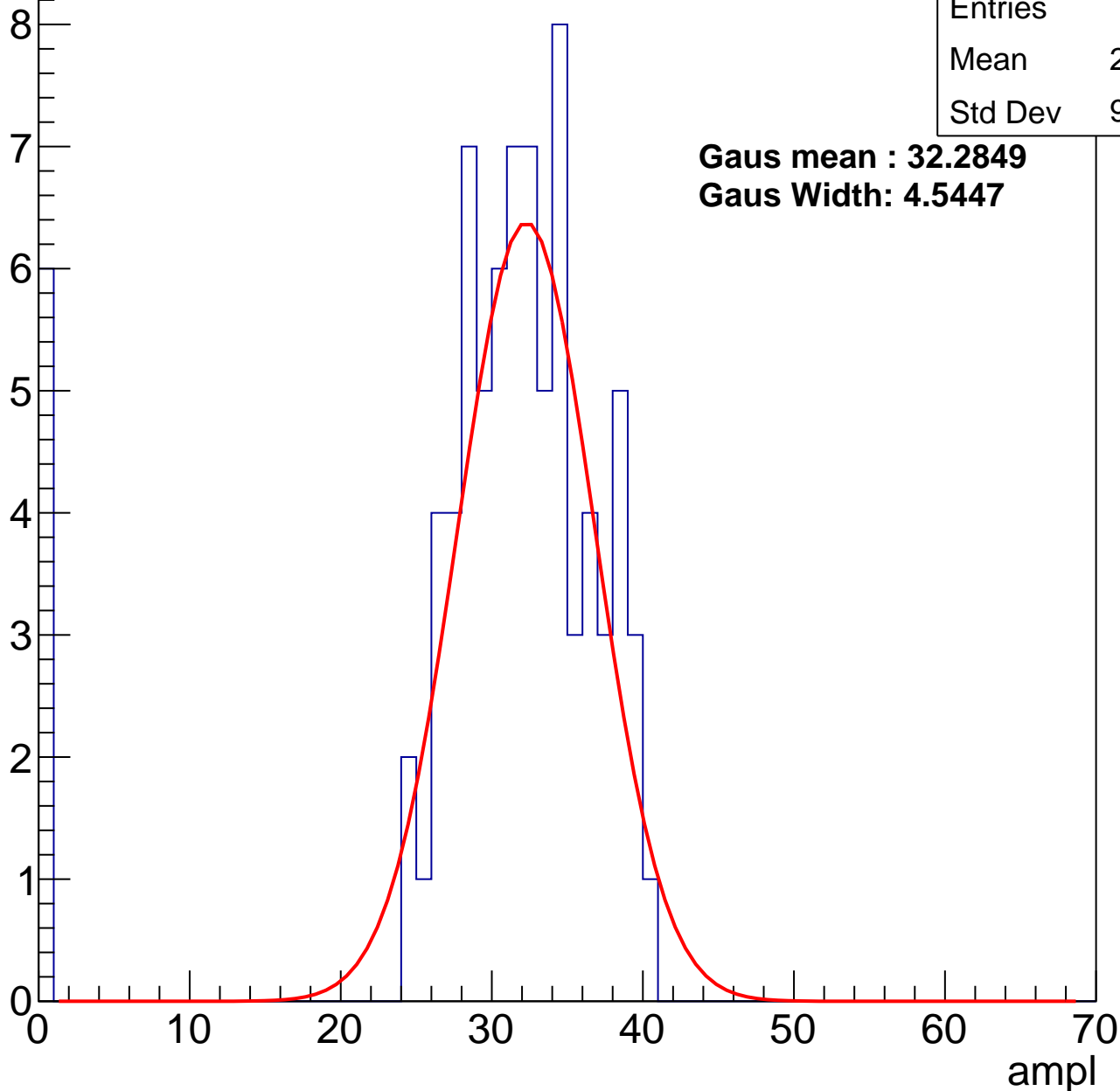
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	29.52
Std Dev	9.195

Gaus mean : 32.2849

Gaus Width: 4.5447



B1L103S, U21-ch94, adc2

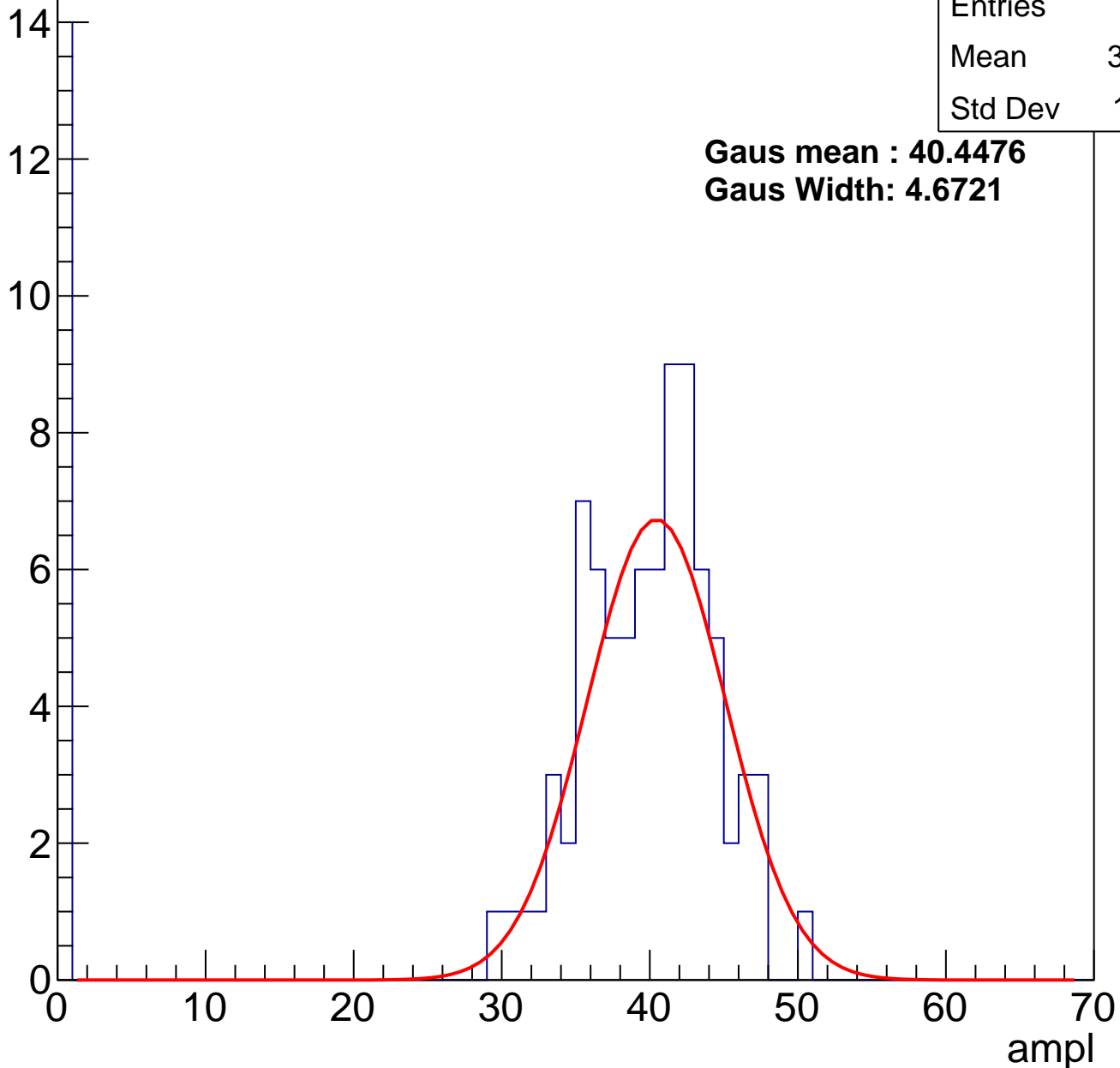
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	33.78
Std Dev	14.51

Gaus mean : 40.4476

Gaus Width: 4.6721

Entry

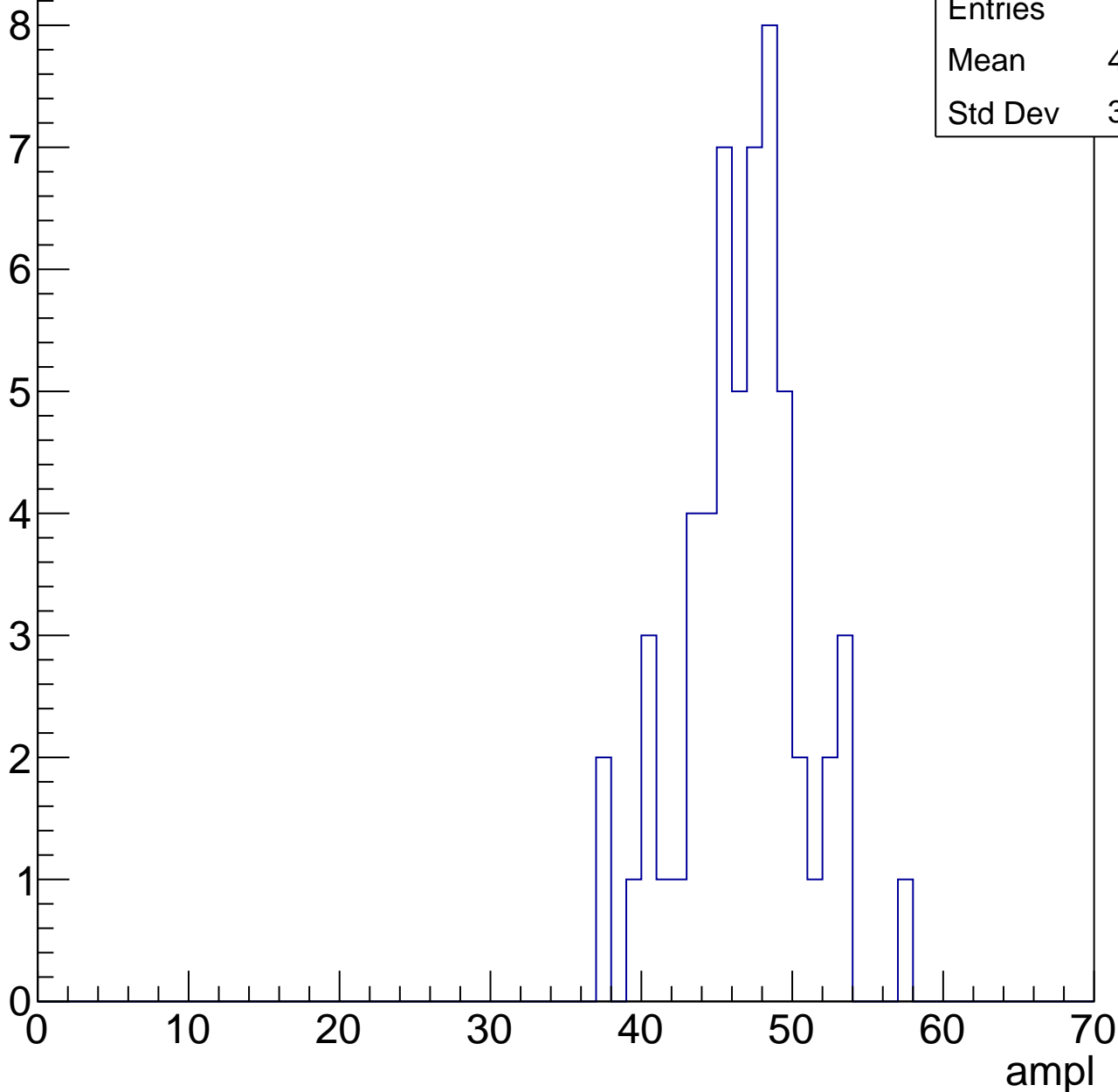


B1L103S, U21-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	46.28
Std Dev	3.964

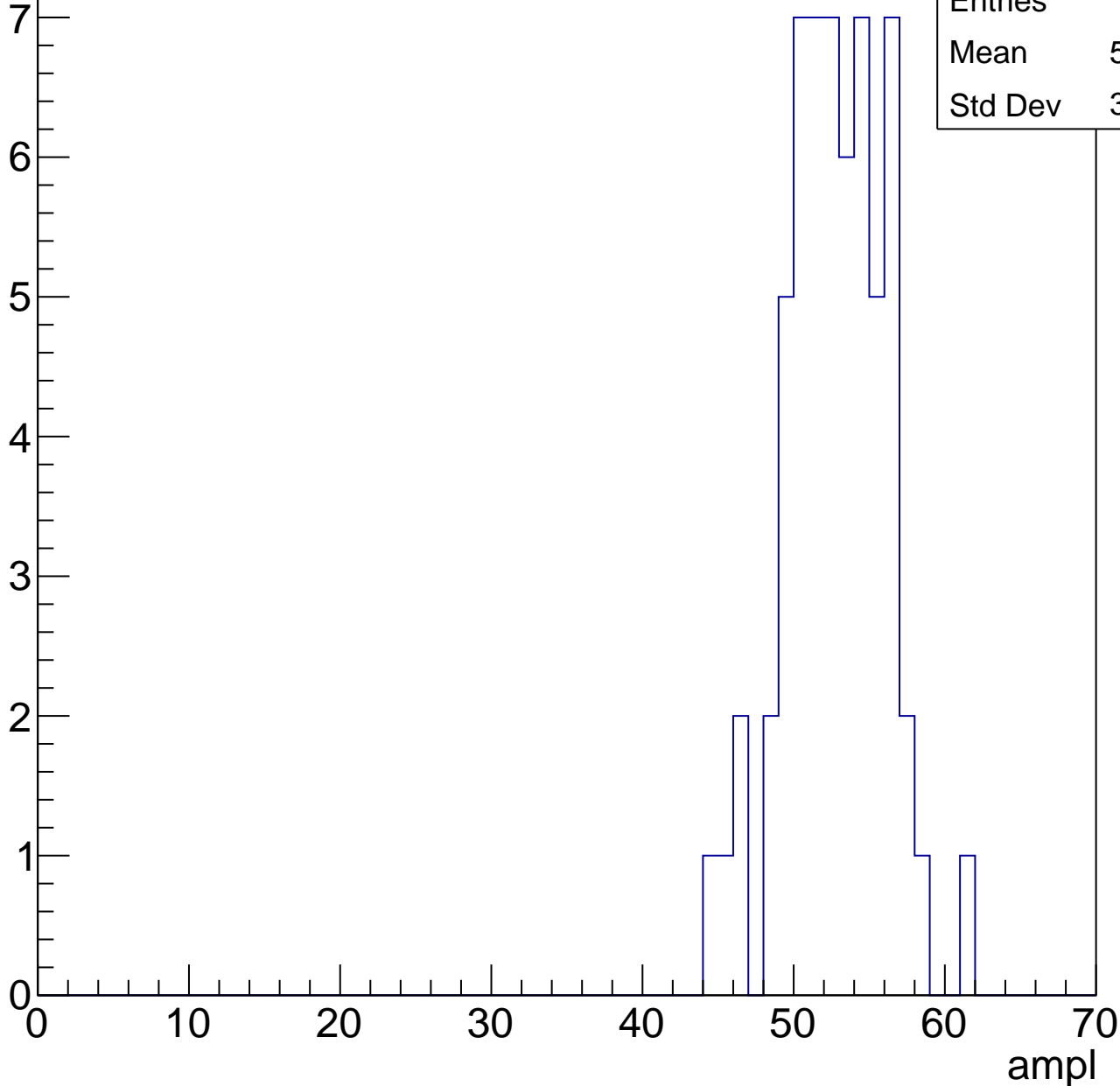


B1L103S, U21-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

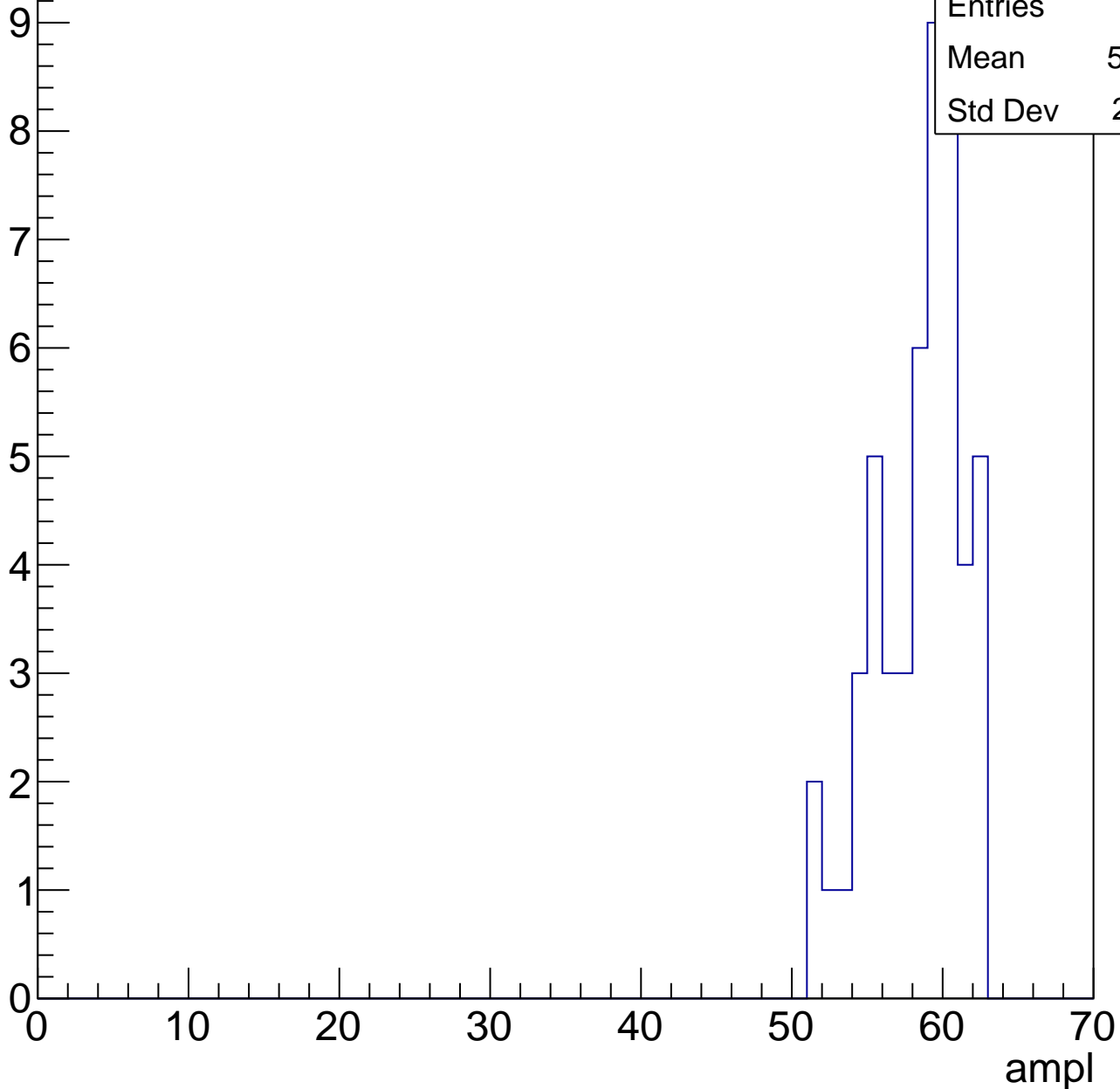
Entries	61
Mean	52.28
Std Dev	3.265



B1L103S, U21-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



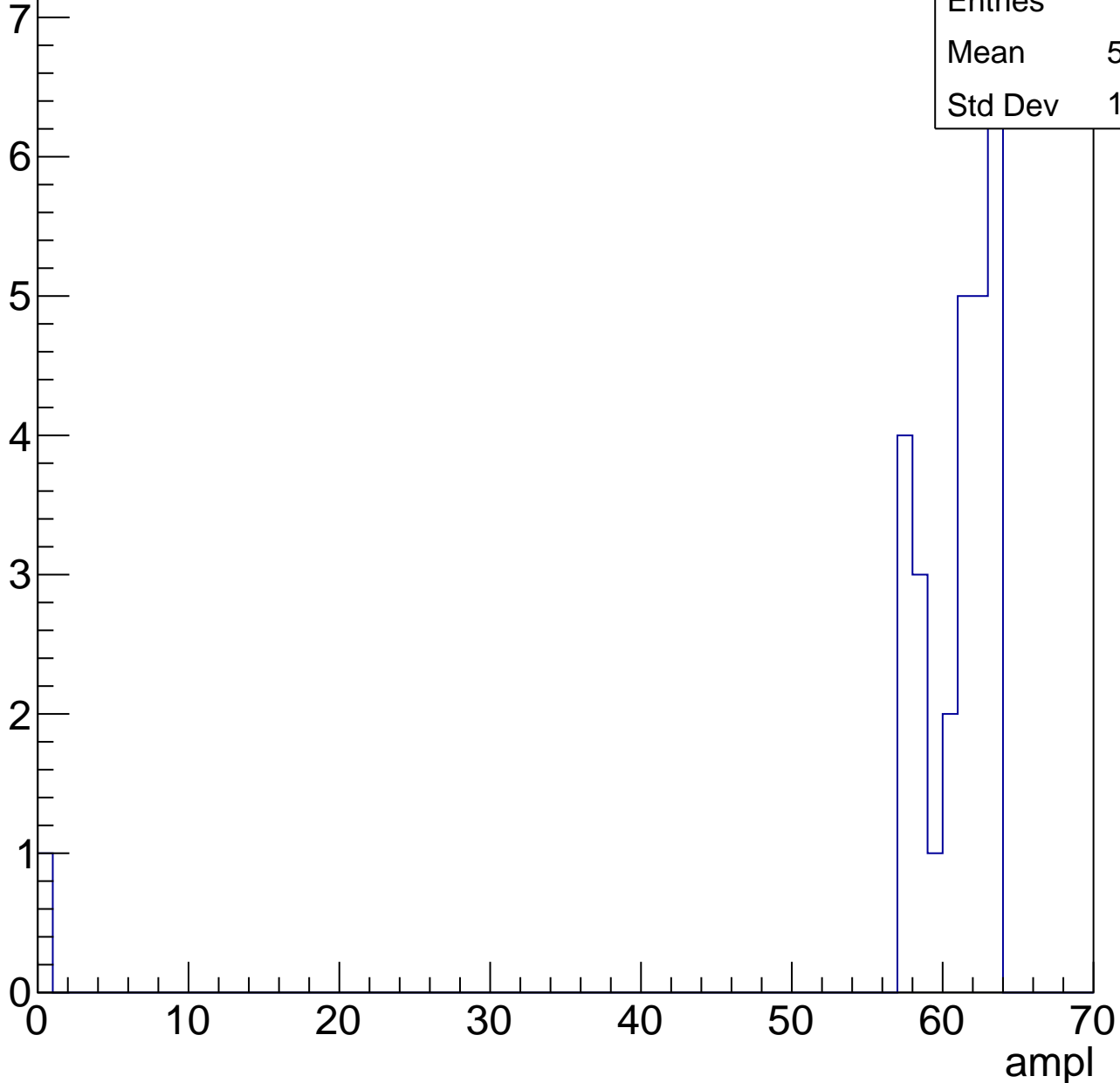
Entries	50
Mean	57.92
Std Dev	2.911

B1L103S, U21-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.46
Std Dev	11.45

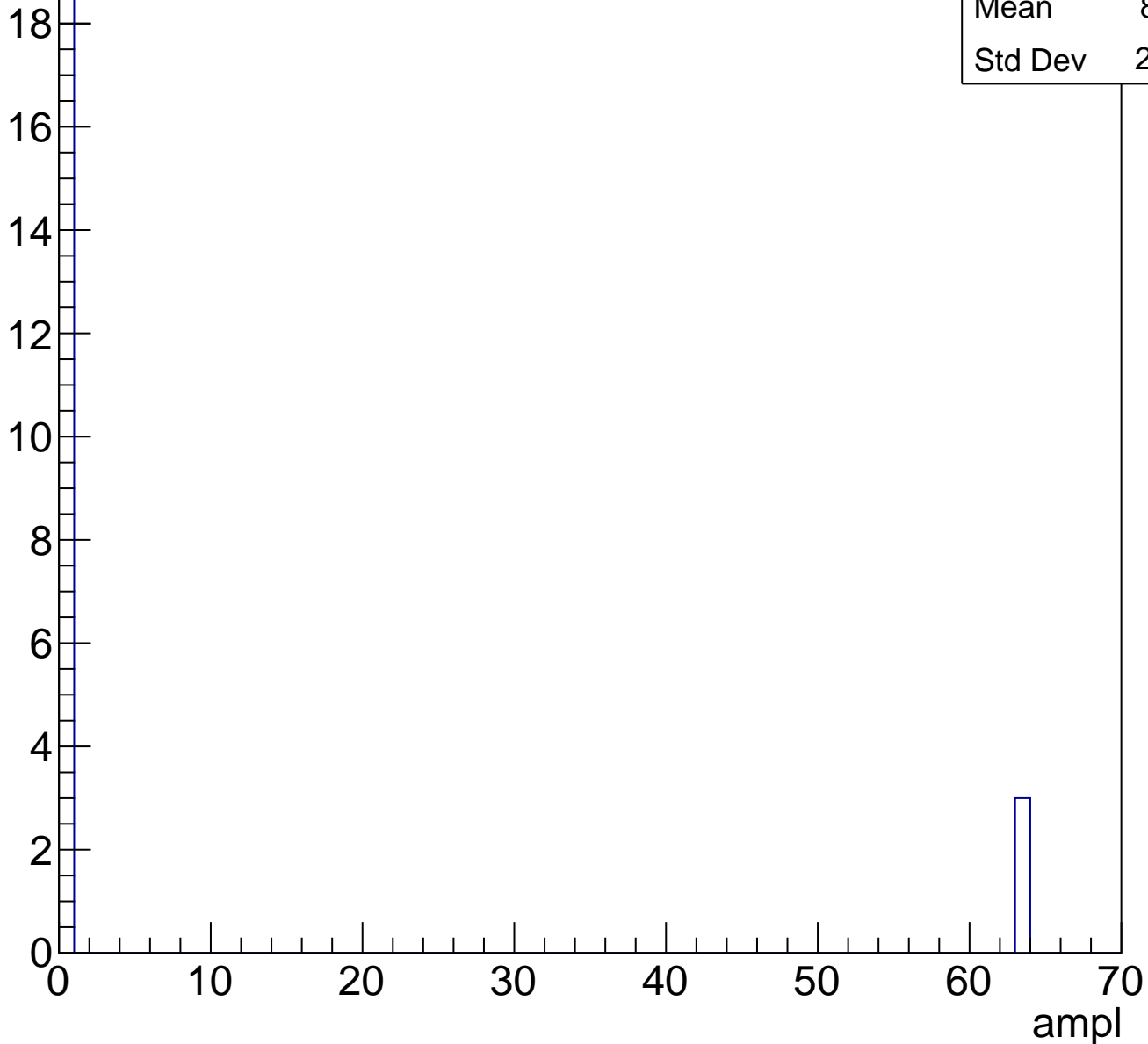


B1L103S, U21-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.591
Std Dev	21.62

Entry



B1L103S, U21-ch95, adc0

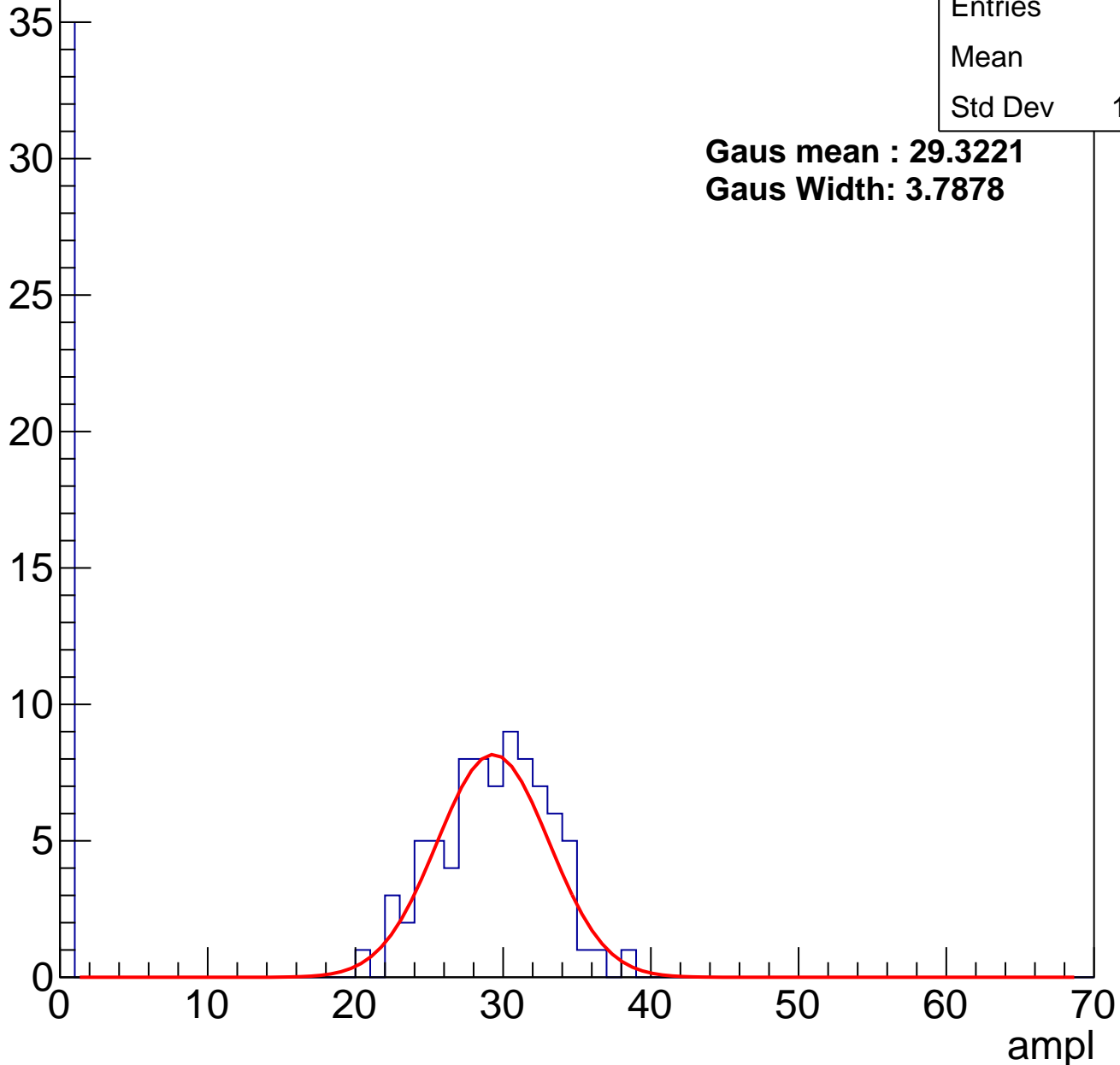
calib_packv5_041523_1651.root, FC#0, port C2

Entries	116
Mean	20.2
Std Dev	13.62

Gaus mean : 29.3221

Gaus Width: 3.7878

Entry



B1L103S, U21-ch95, adc1

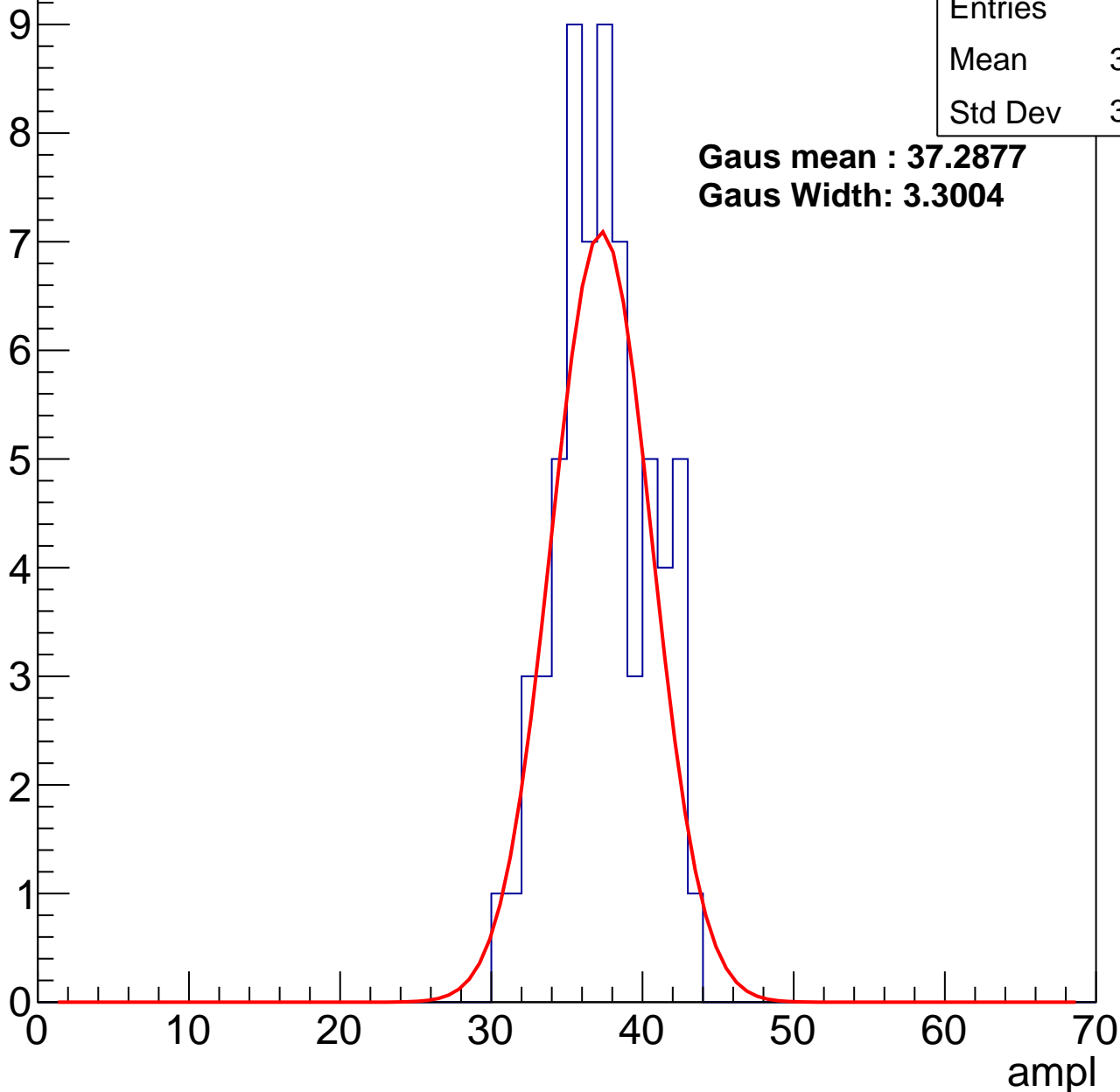
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.92
Std Dev	3.046

Gaus mean : 37.2877

Gaus Width: 3.3004



B1L103S, U21-ch95, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	37.44
Std Dev	14.88

Gaus mean : 43.7268

Gaus Width: 3.4862

10

8

6

4

2

0

0

10

20

30

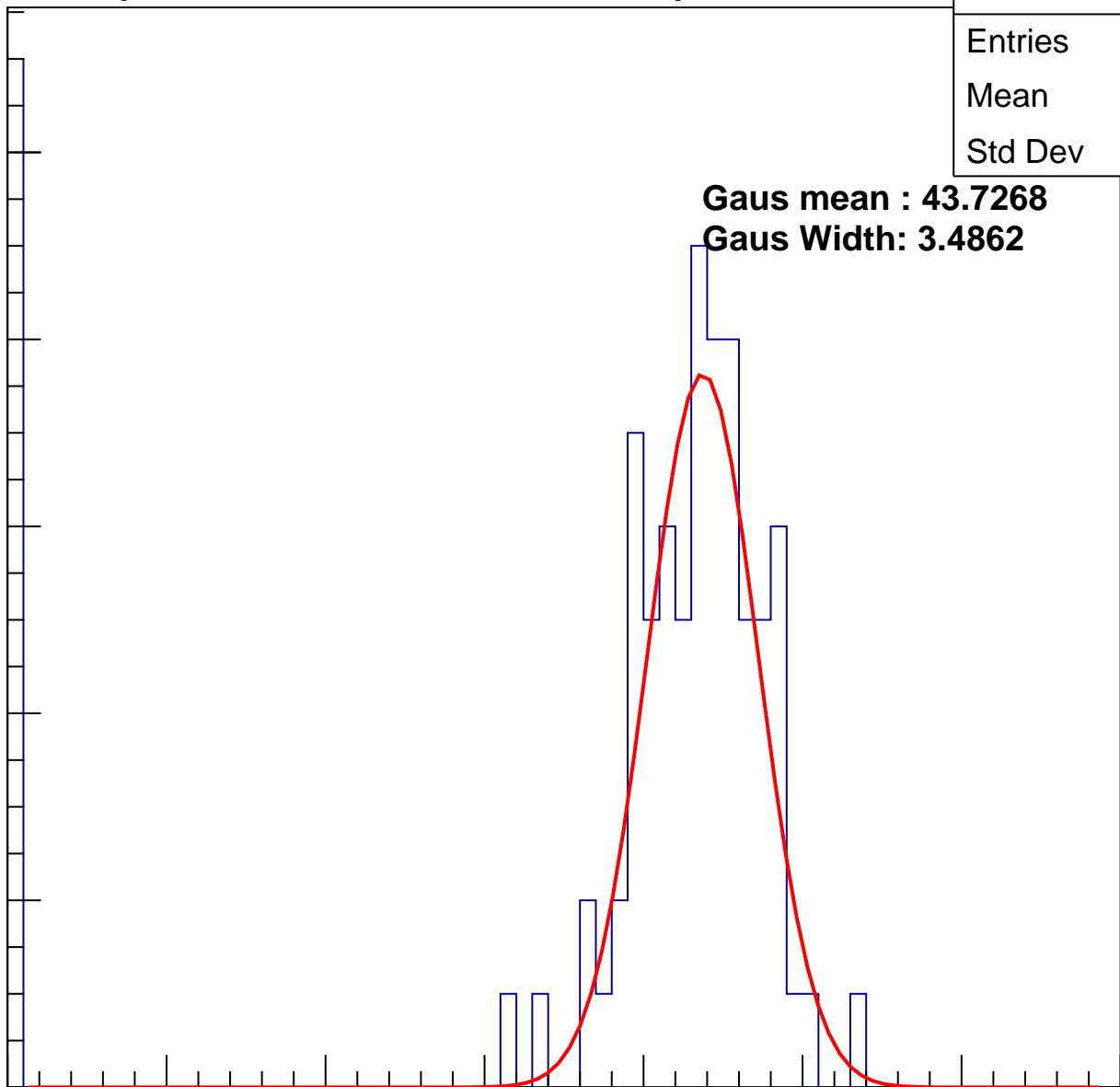
40

50

60

70

ampl

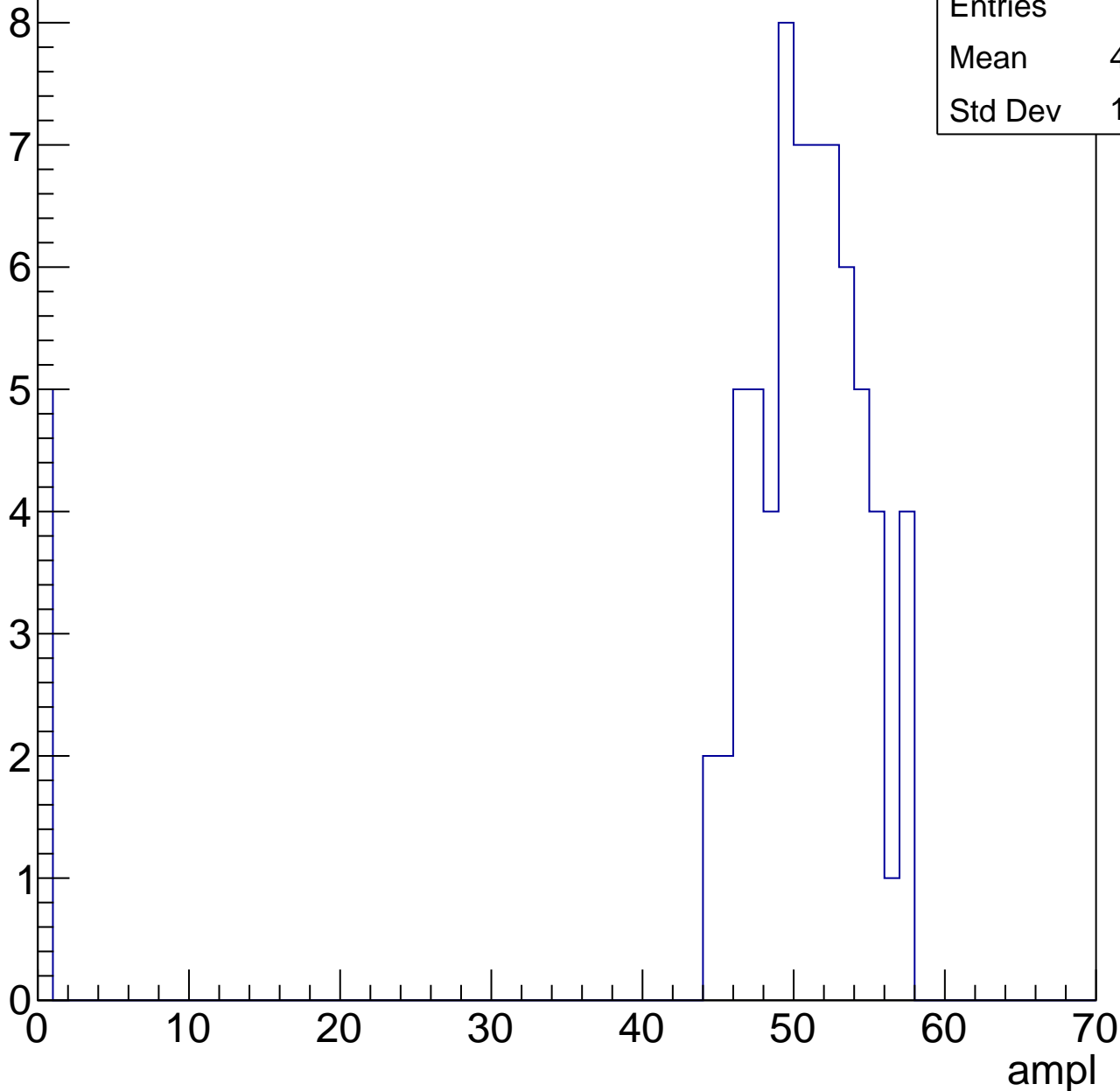


B1L103S, U21-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

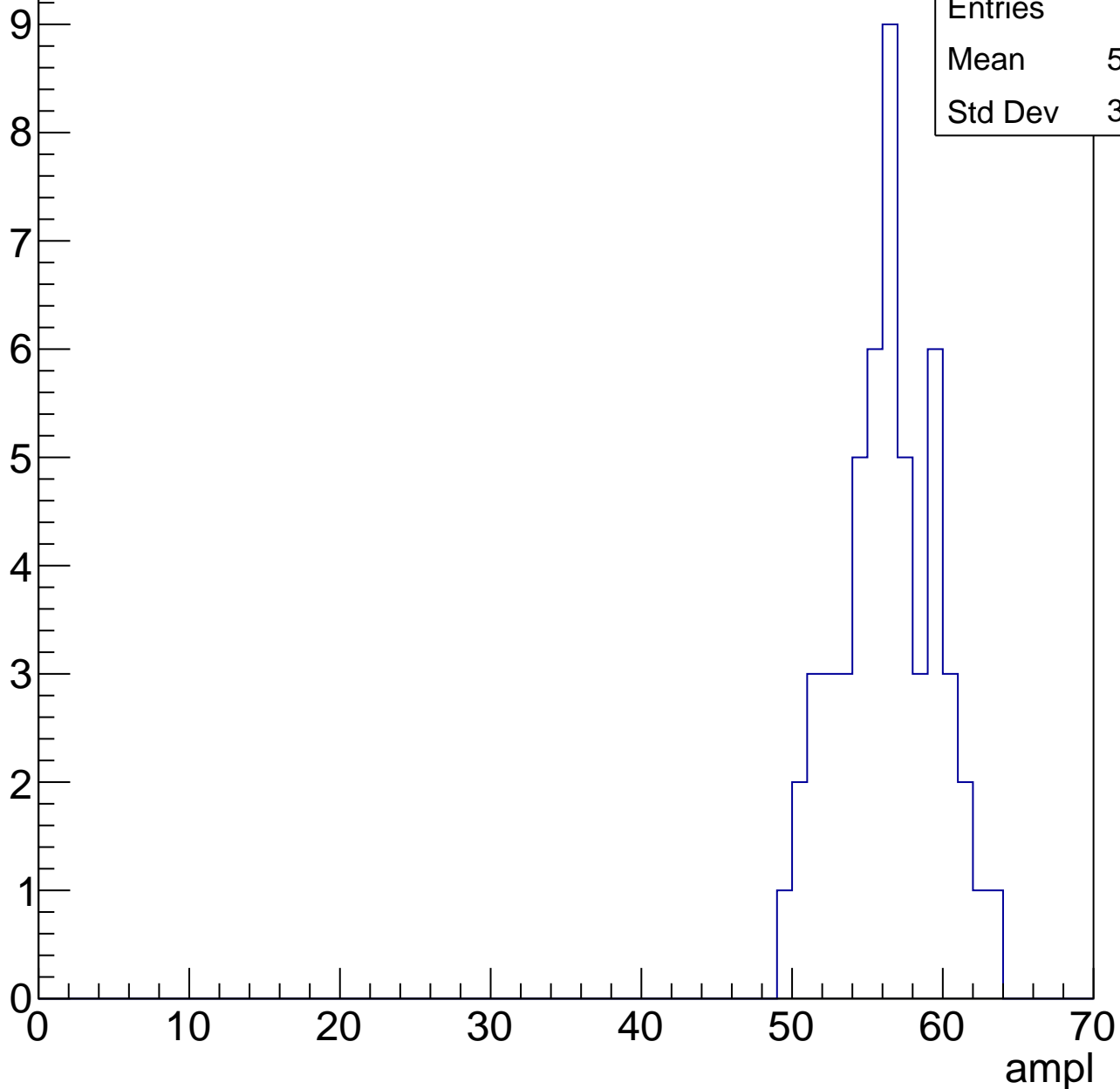
Entries	72
Mean	47.08
Std Dev	13.26



B1L103S, U21-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



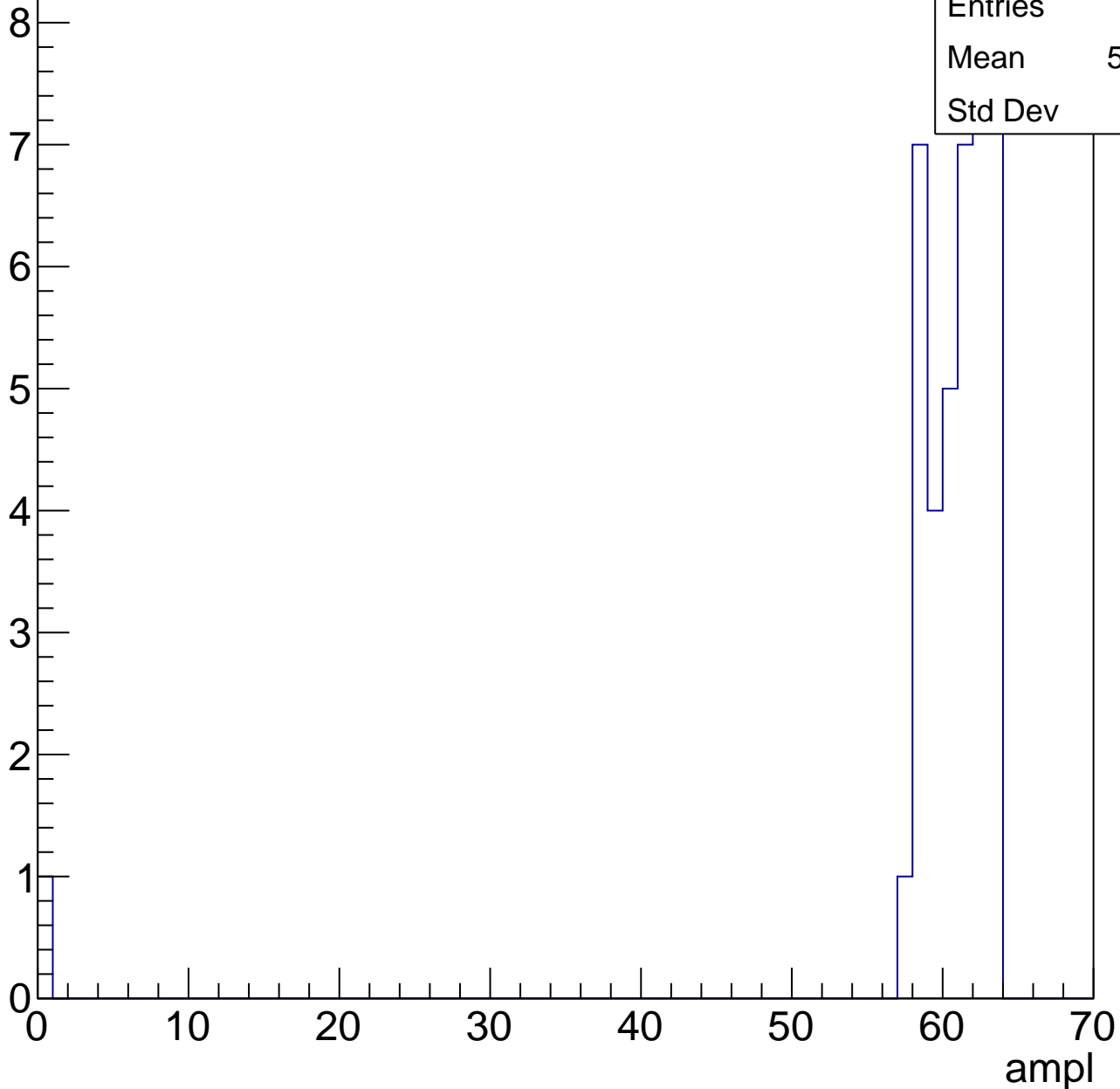
Entries	53
Mean	55.87
Std Dev	3.222

B1L103S, U21-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	59.17
Std Dev	9.53



B1L103S, U21-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

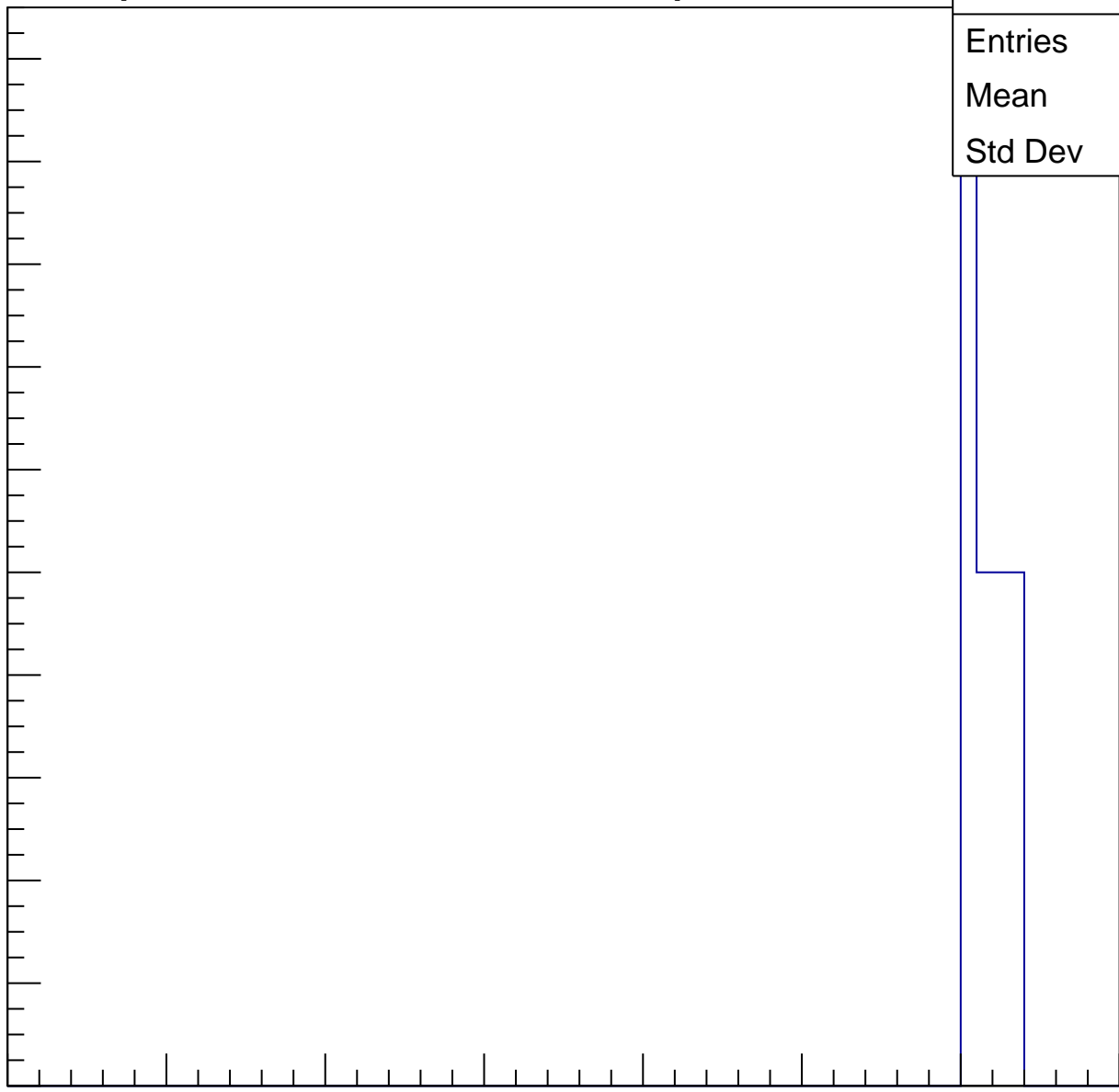
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch96, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	21.91
Std Dev	9.468

Gaus mean : 25.9867

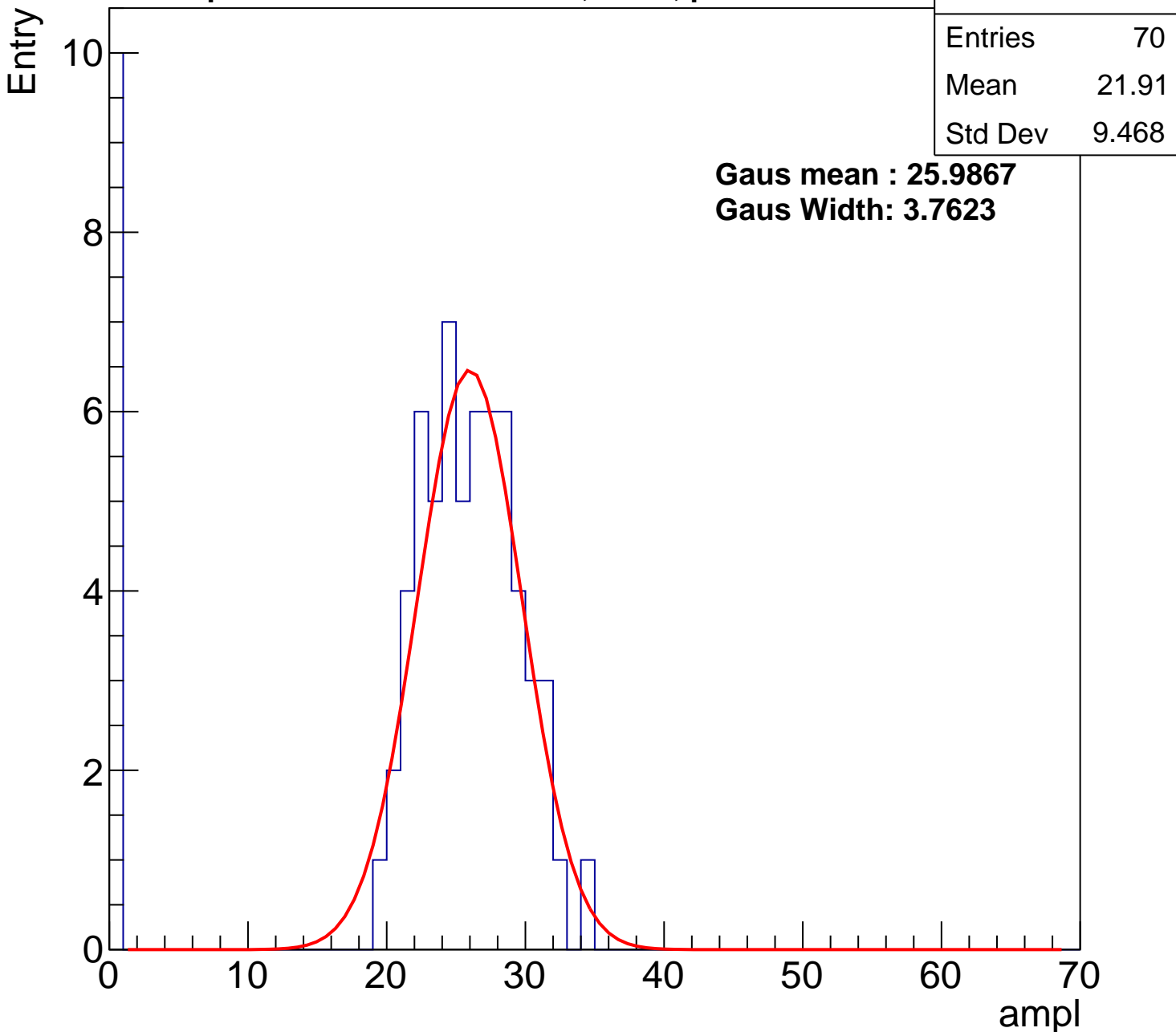
Gaus Width: 3.7623

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch96, adc1

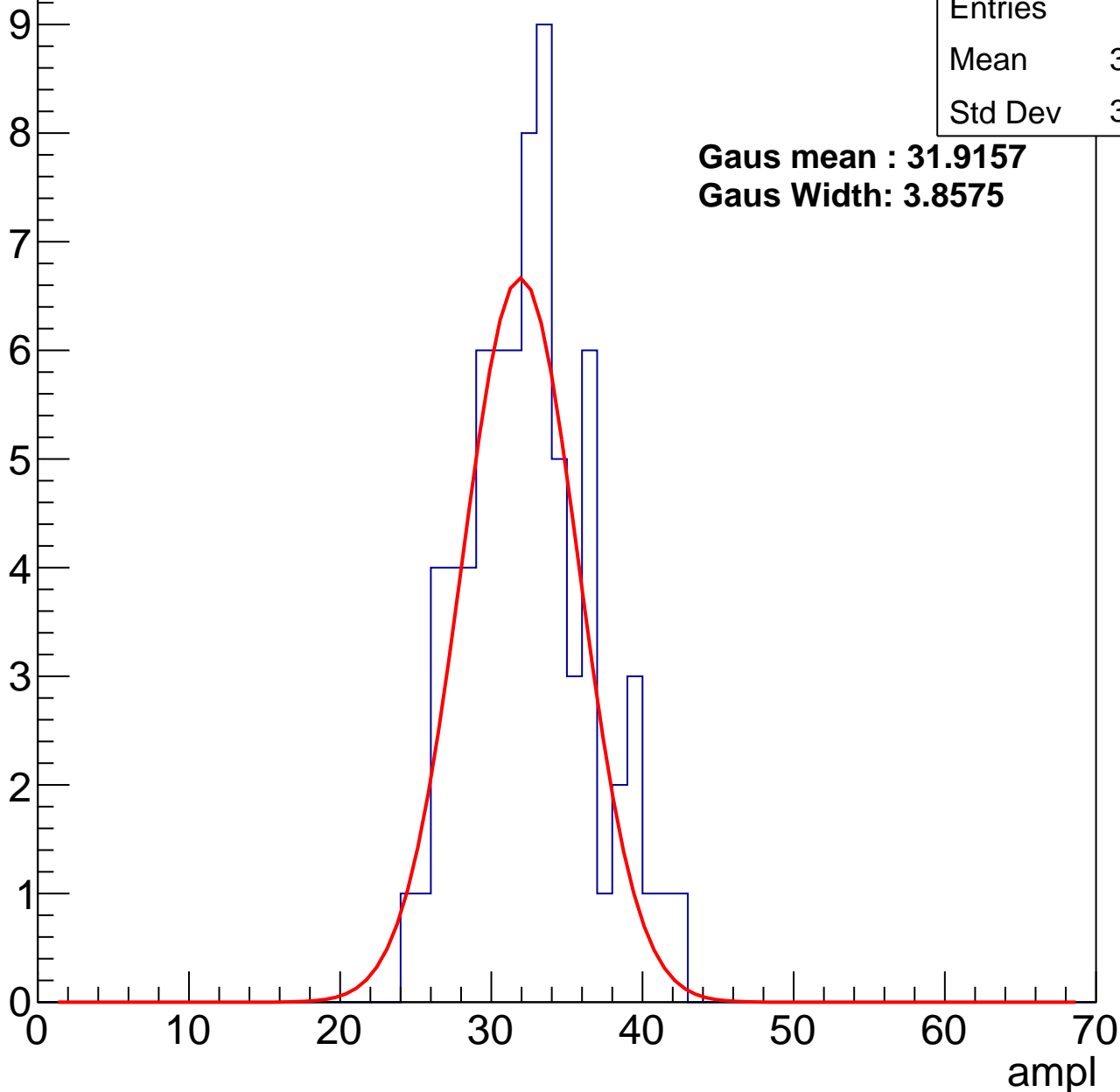
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	32.08
Std Dev	3.989

Gaus mean : 31.9157

Gaus Width: 3.8575



B1L103S, U21-ch96, adc2

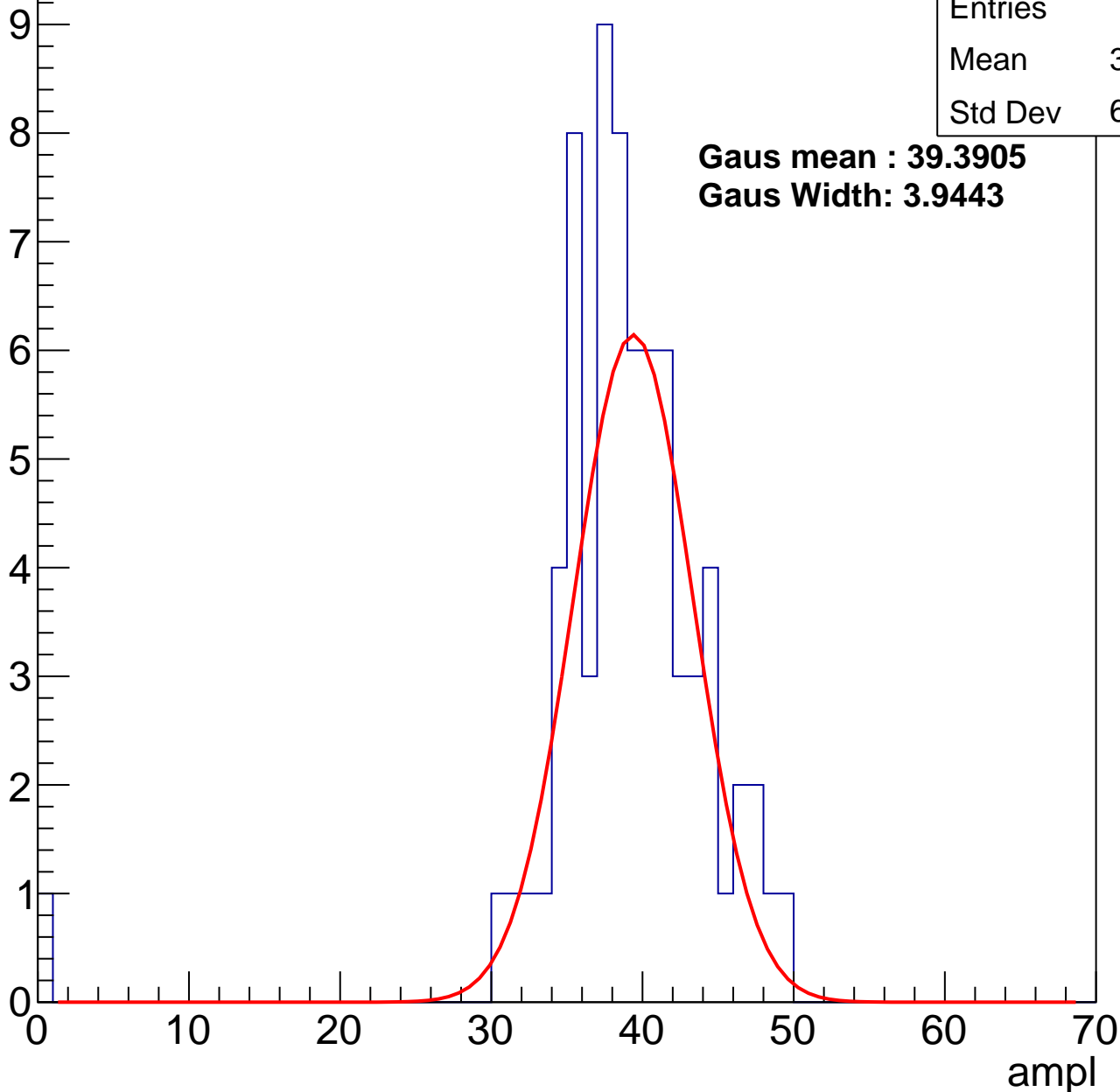
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	38.42
Std Dev	6.094

Gaus mean : 39.3905

Gaus Width: 3.9443



B1L103S, U21-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	38.27
Std Dev	16.7

Entry

10

8

6

4

2

0

0

10

20

30

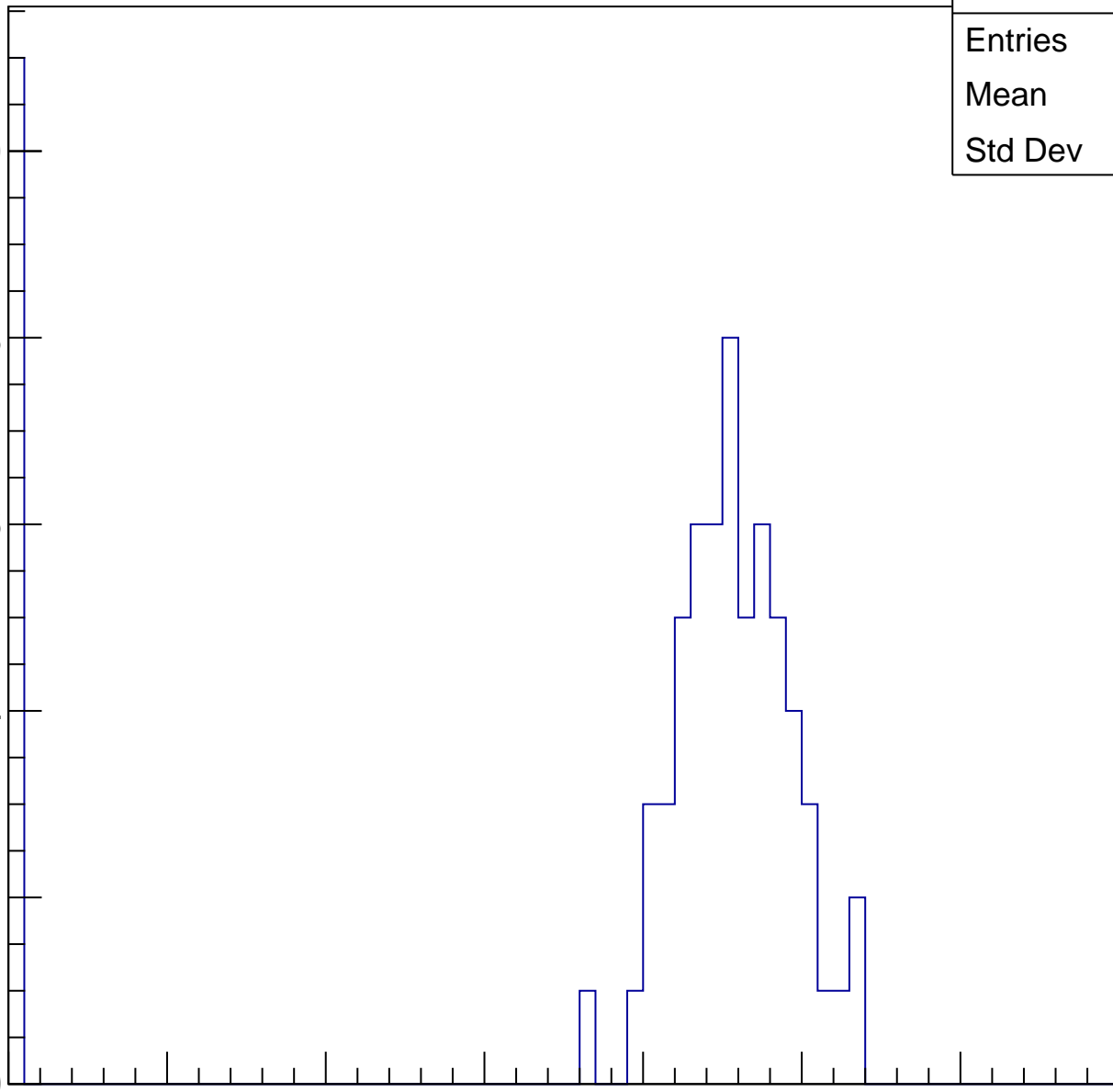
40

50

60

70

ampl

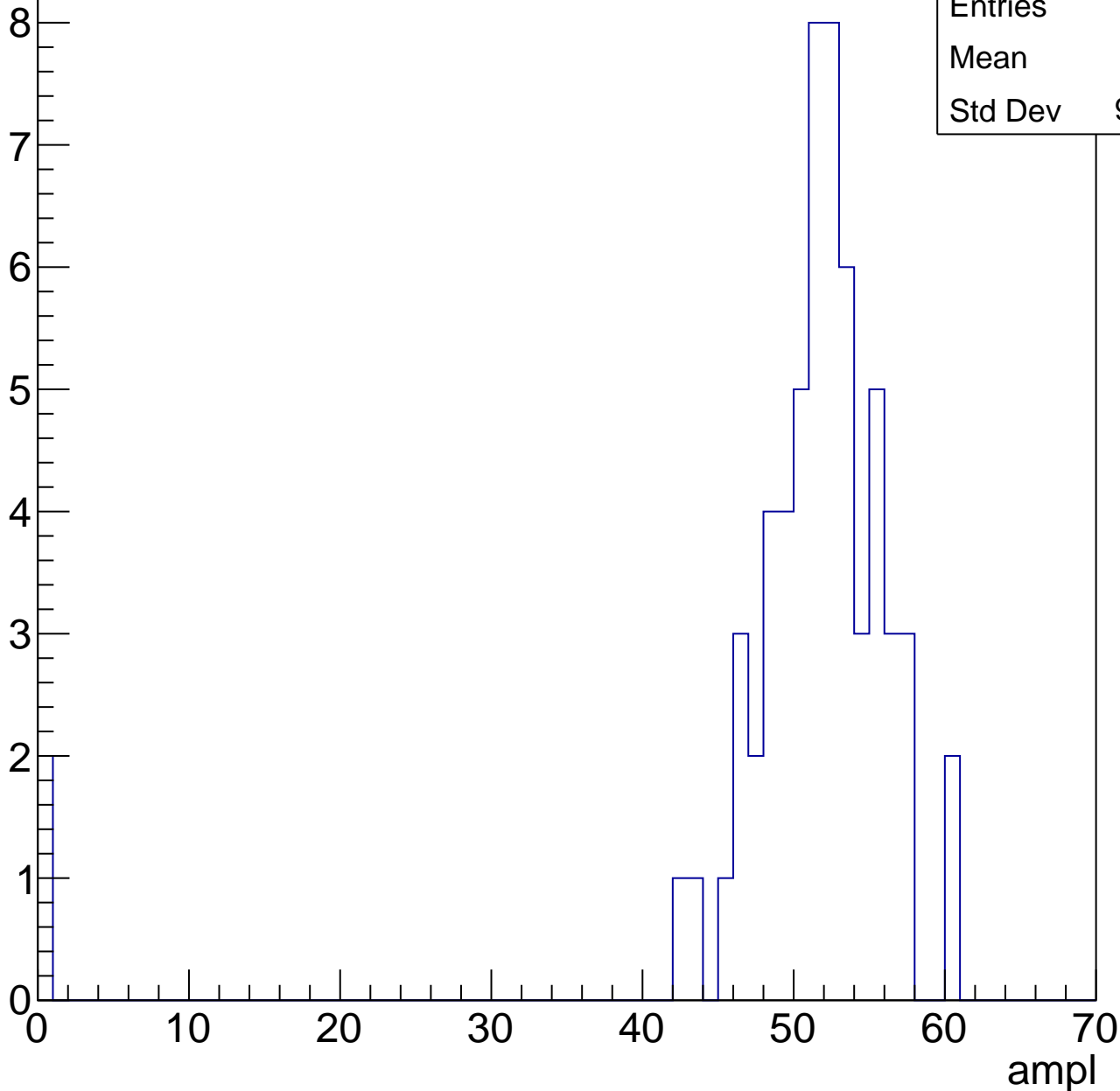


B1L103S, U21-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.8
Std Dev	9.871

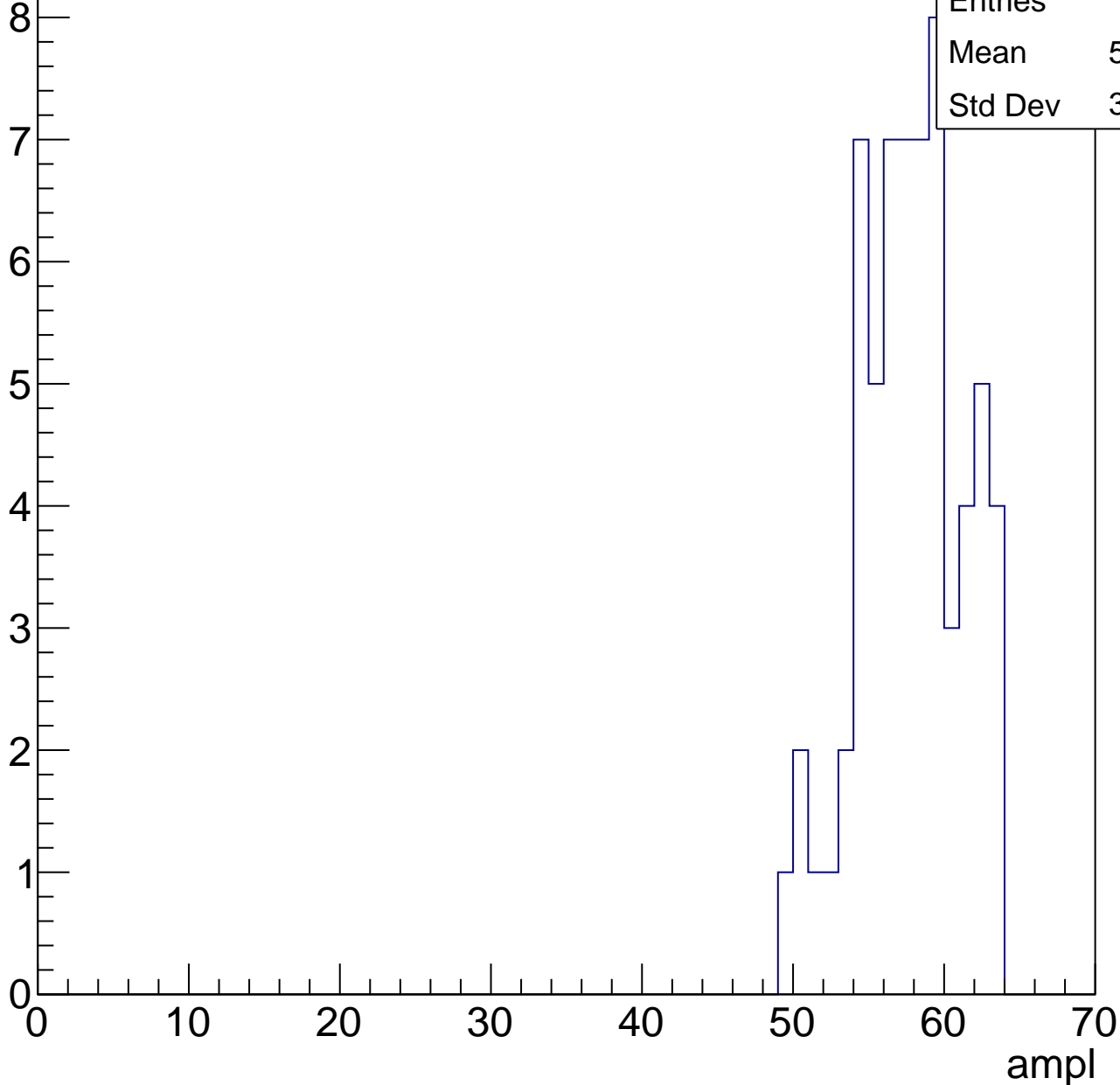


B1L103S, U21-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	57.28
Std Dev	3.398

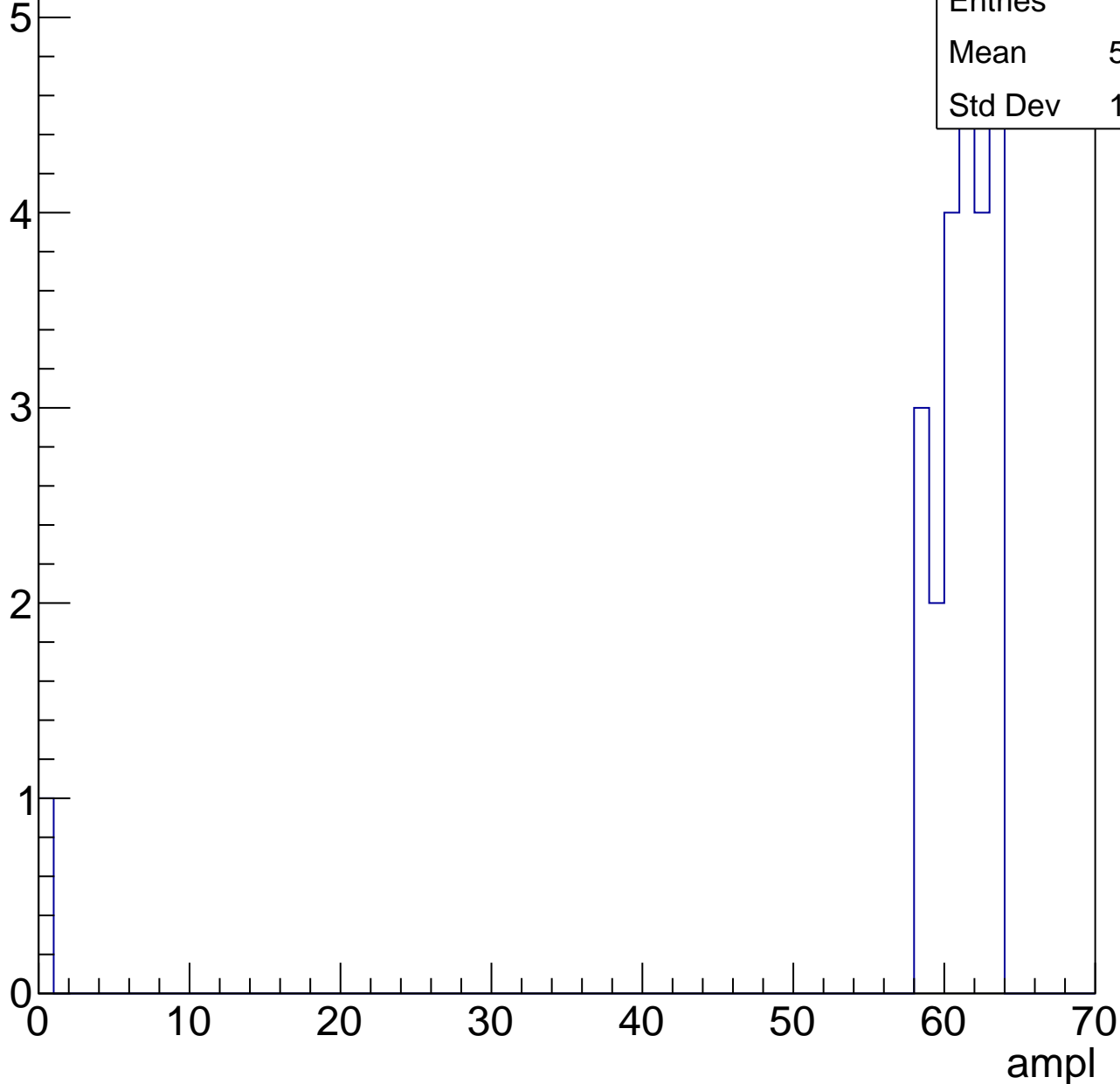


B1L103S, U21-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.33
Std Dev	12.27

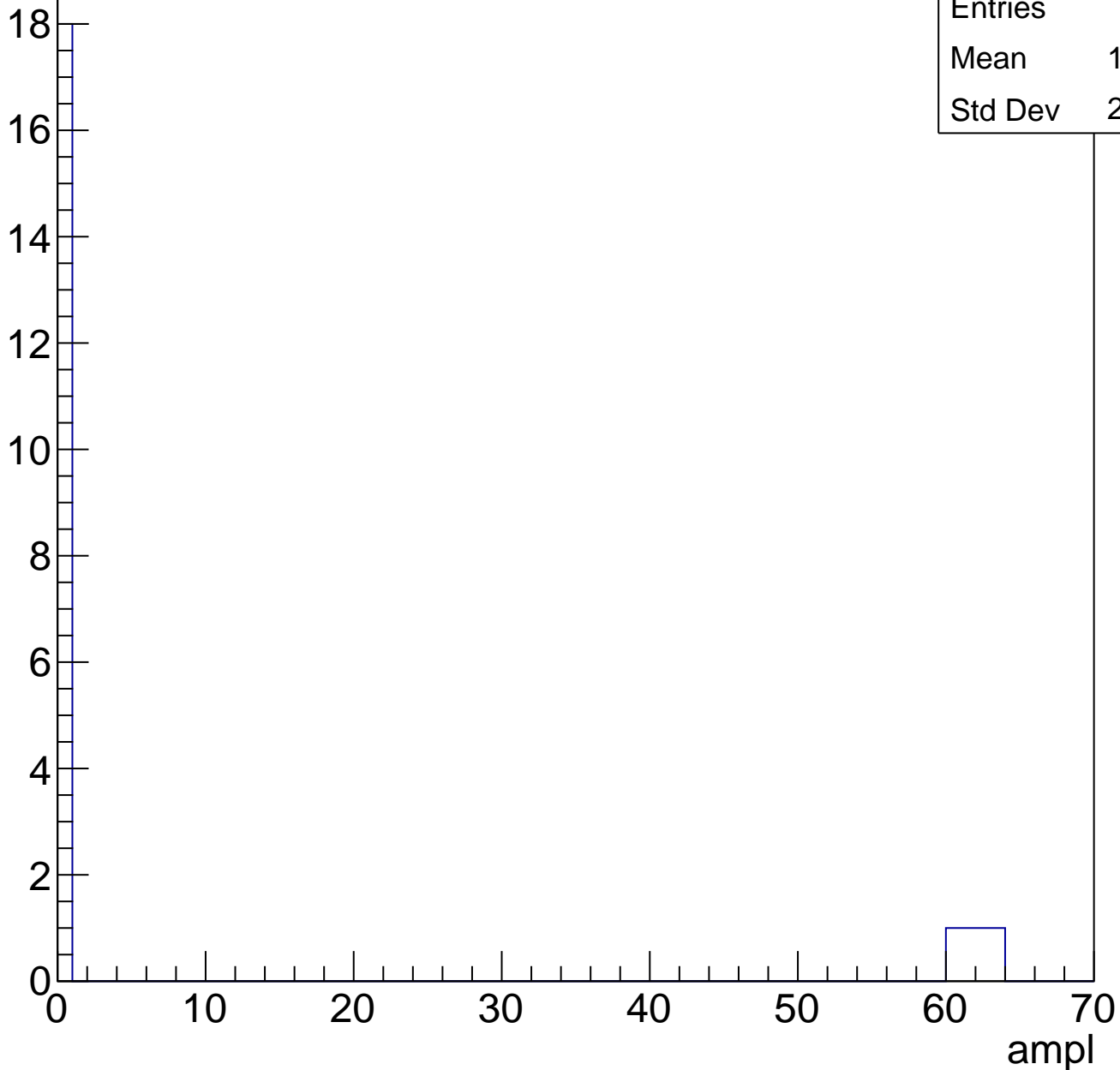


B1L103S, U21-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	11.18
Std Dev	23.73

Entry



B1L103S, U21-ch97, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	22.88
Std Dev	10.51

Gaus mean : 28.0699

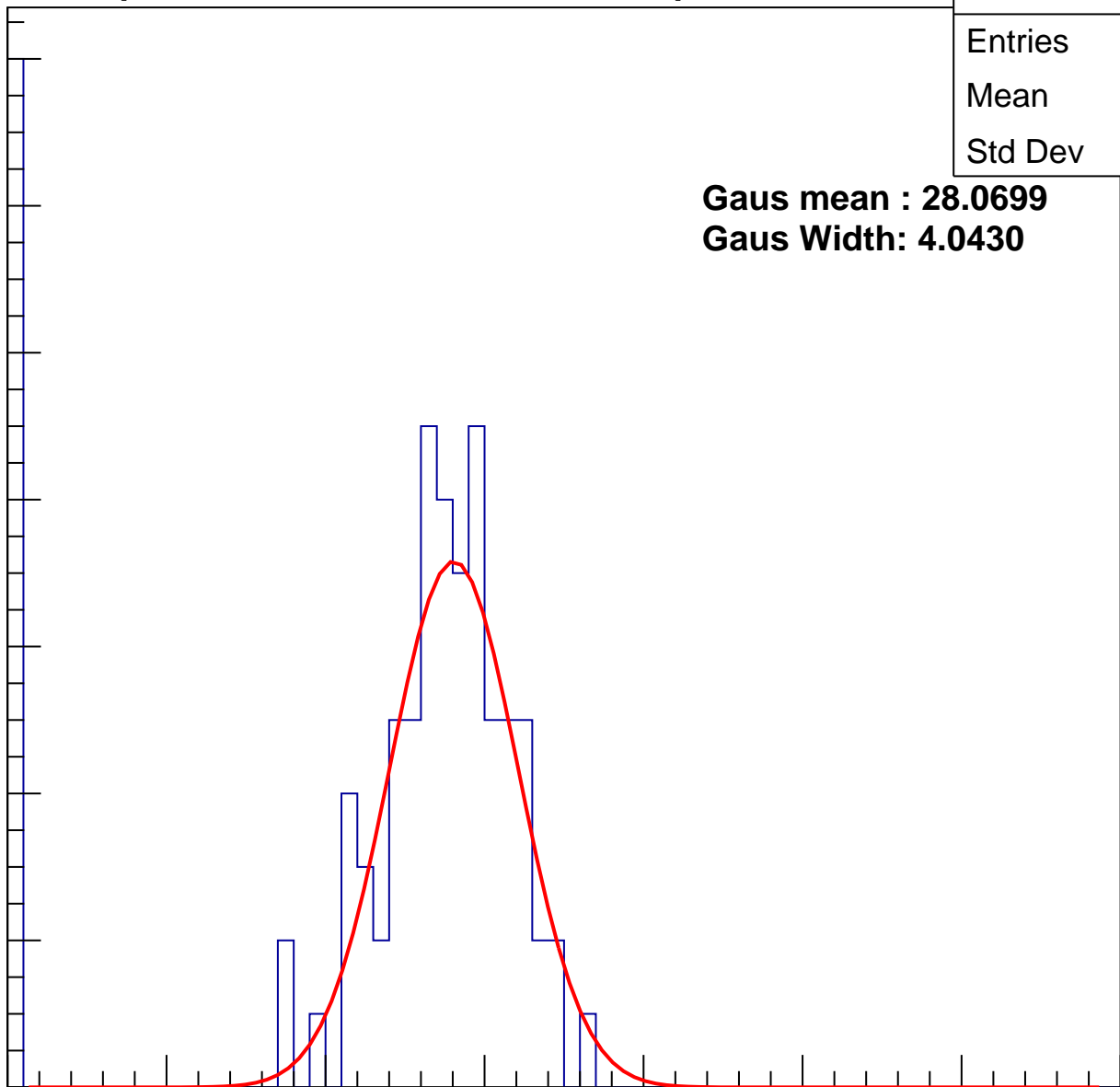
Gaus Width: 4.0430

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch97, adc1

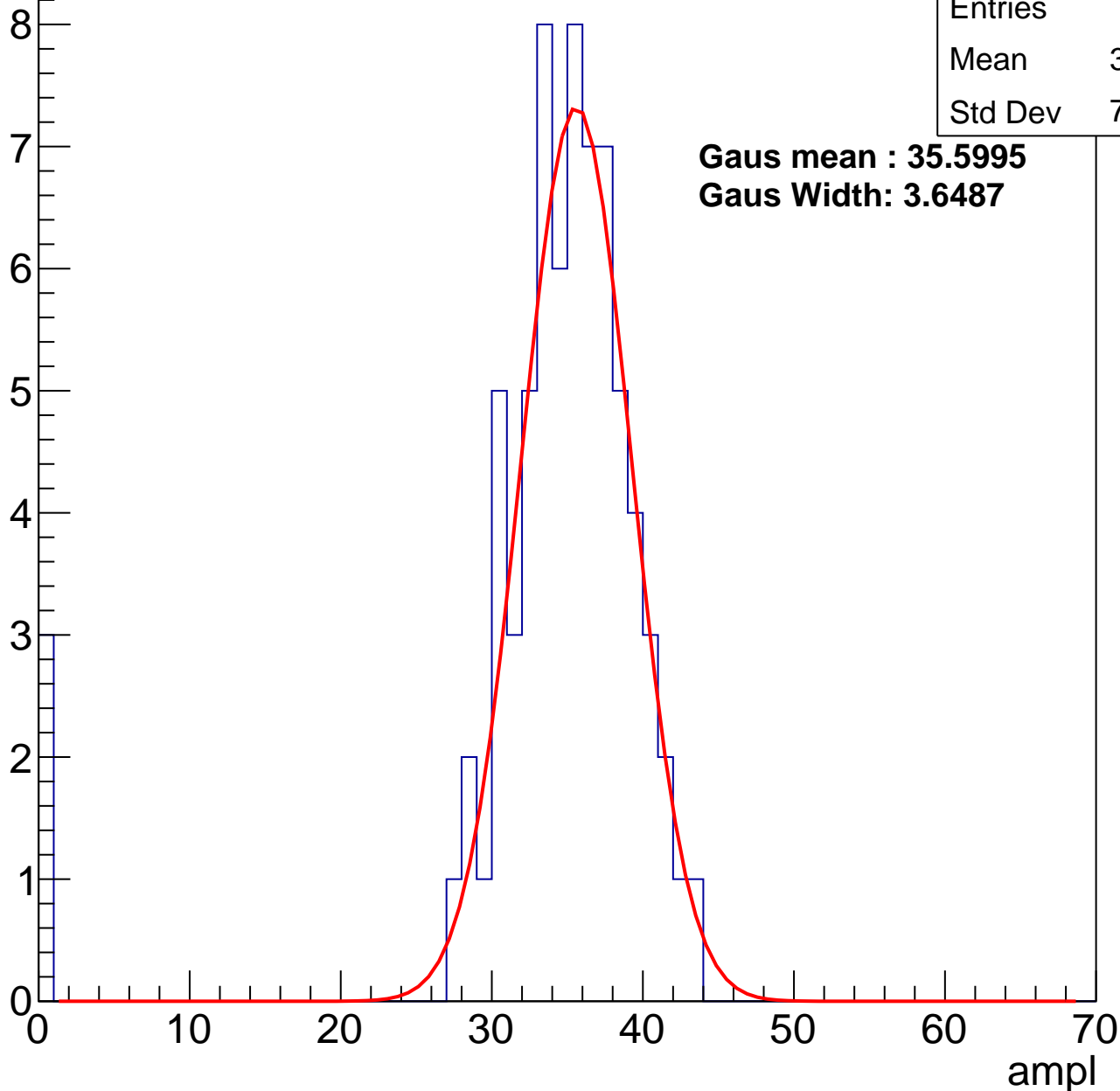
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	33.43
Std Dev	7.778

Gaus mean : 35.5995

Gaus Width: 3.6487



B1L103S, U21-ch97, adc2

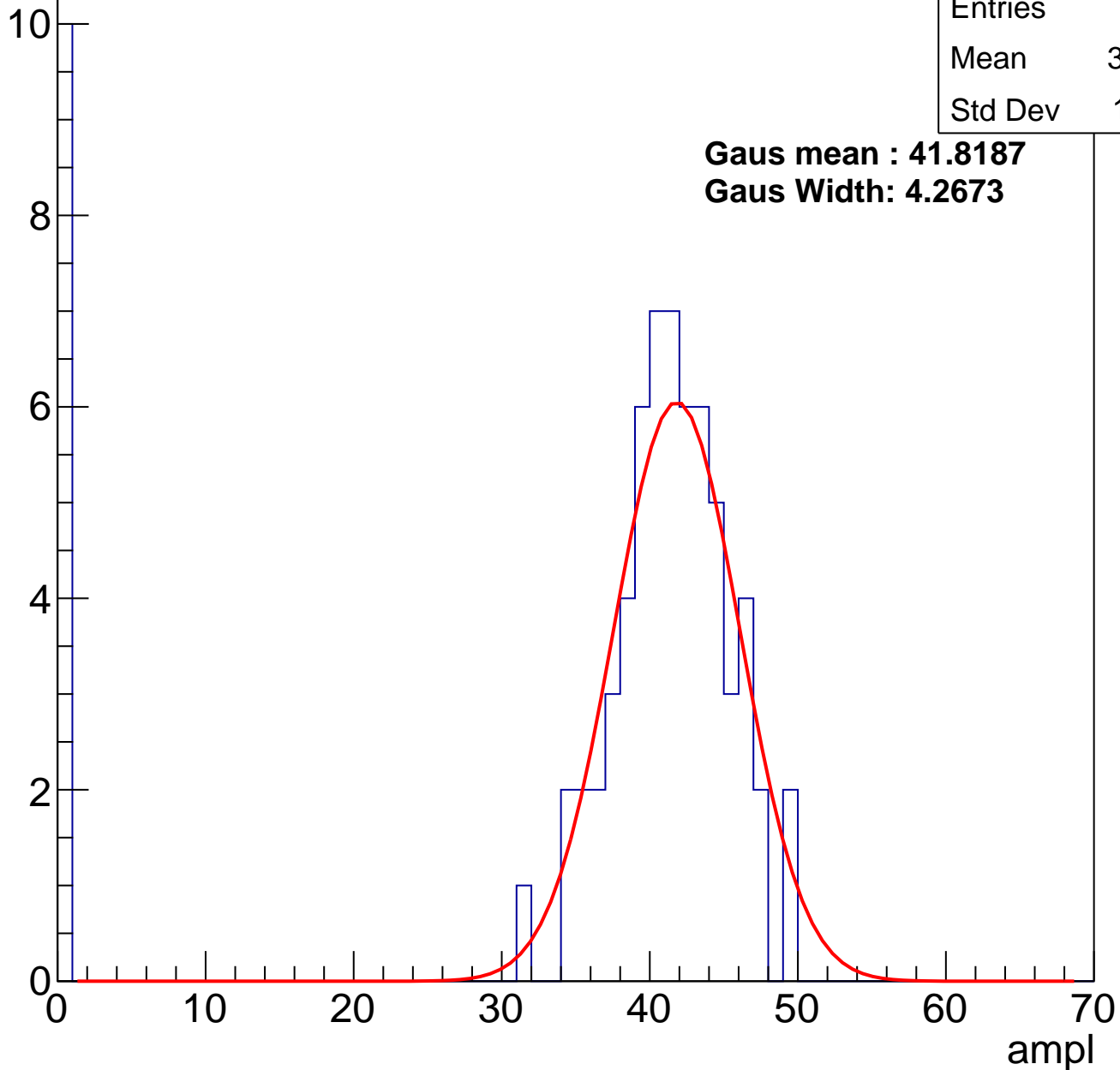
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	35.36
Std Dev	14.61

Gaus mean : 41.8187

Gaus Width: 4.2673

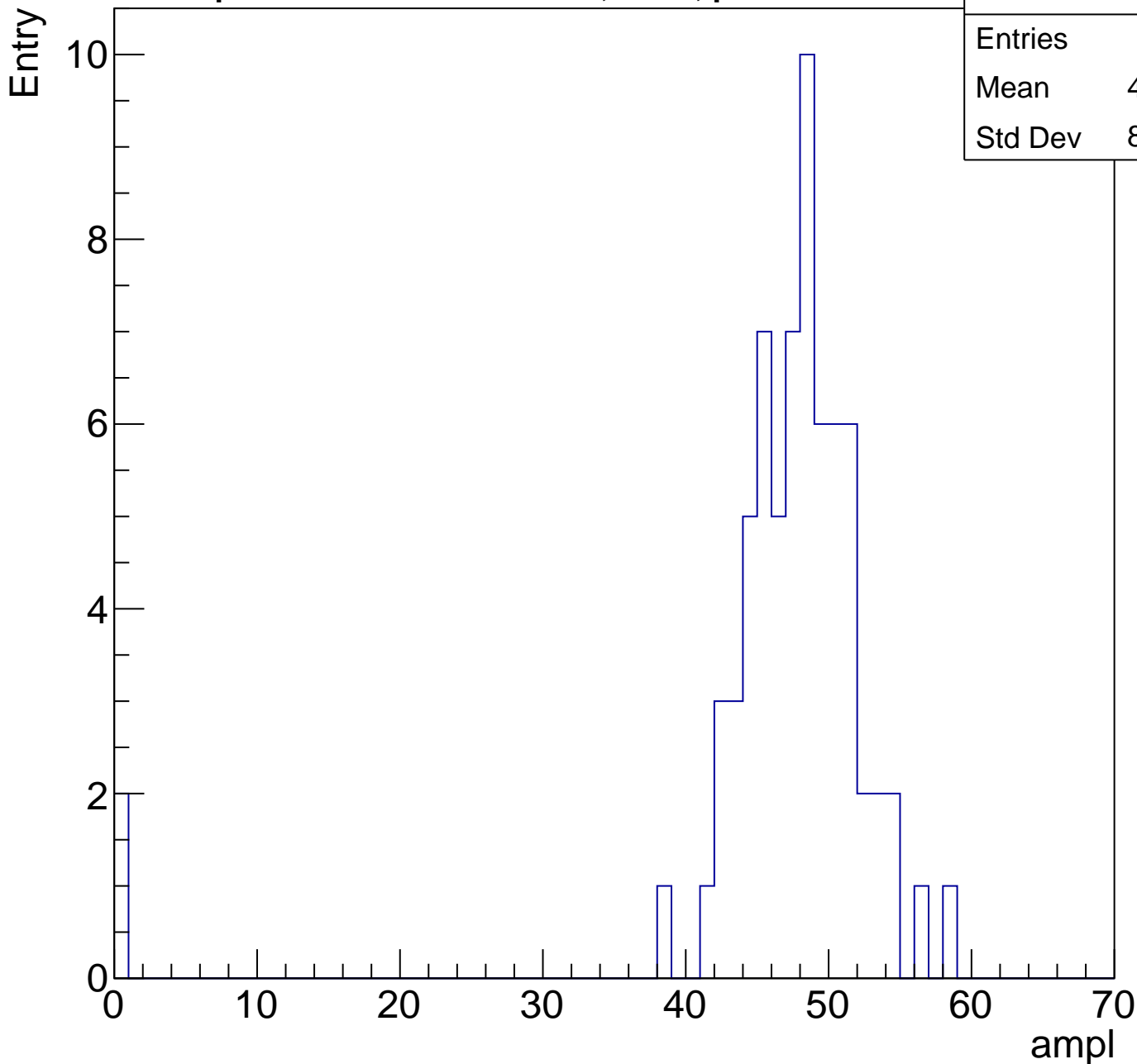
Entry



B1L103S, U21-ch97, adc3

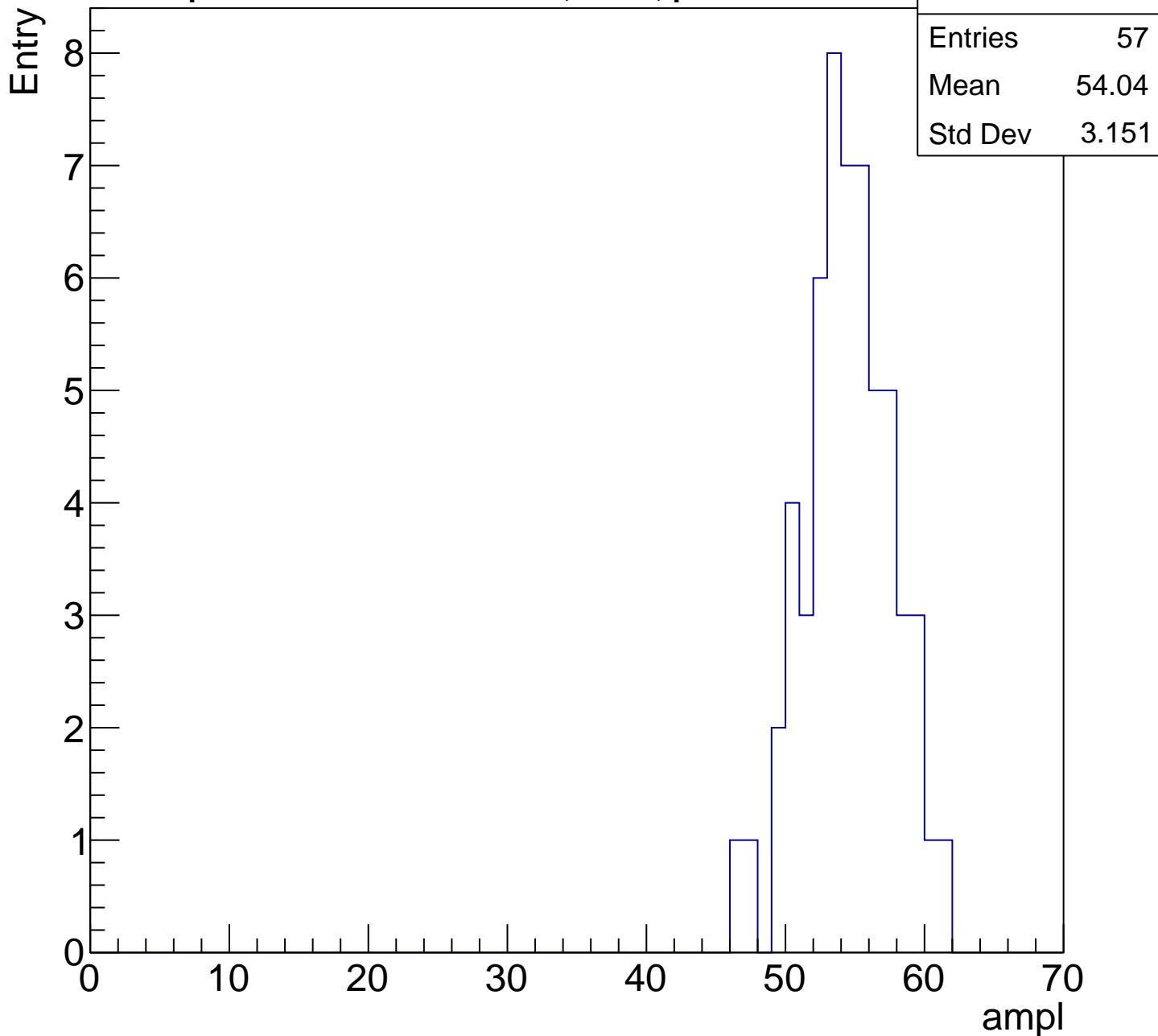
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	46.29
Std Dev	8.707



B1L103S, U21-ch97, adc4

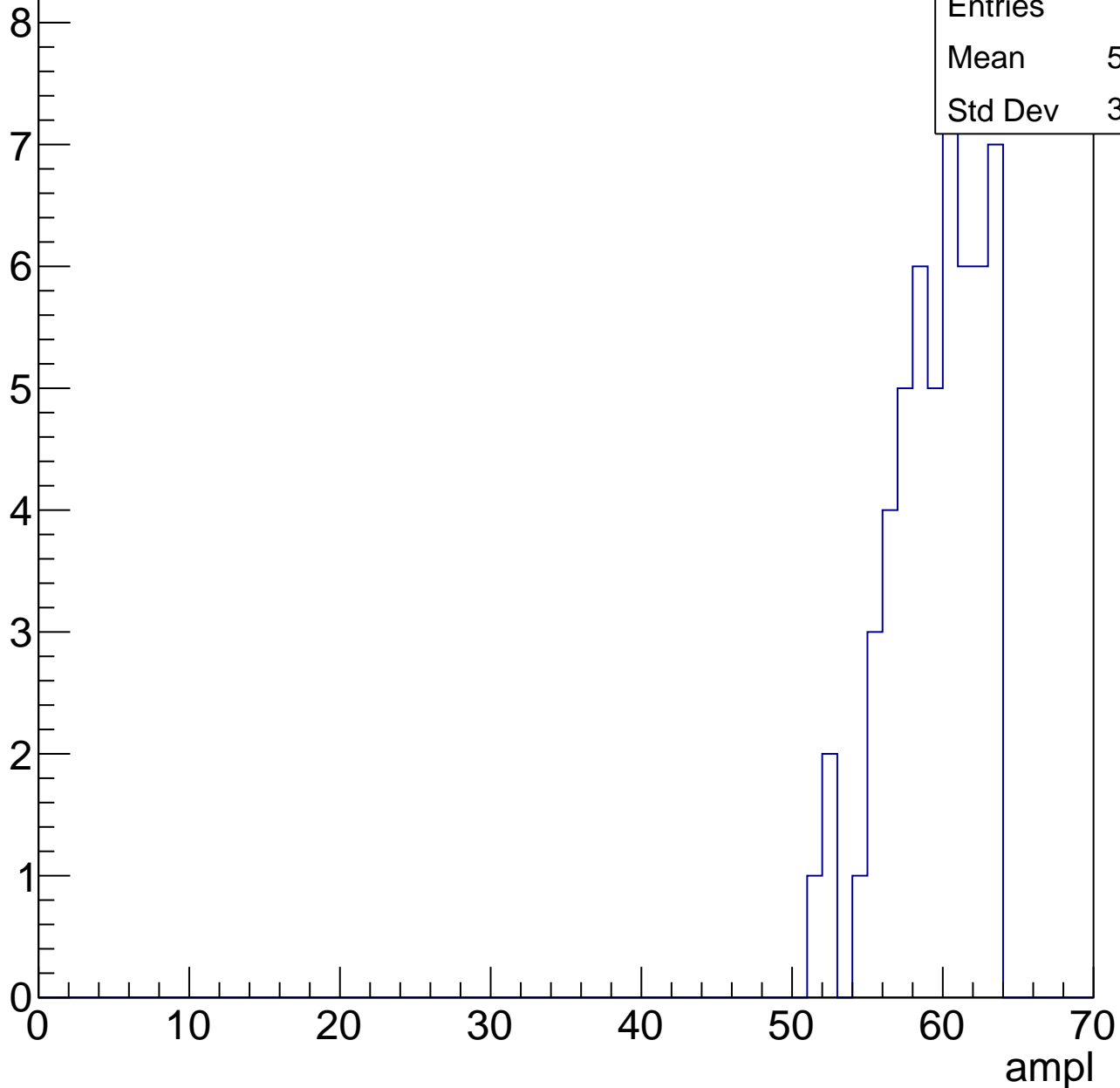
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U21-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

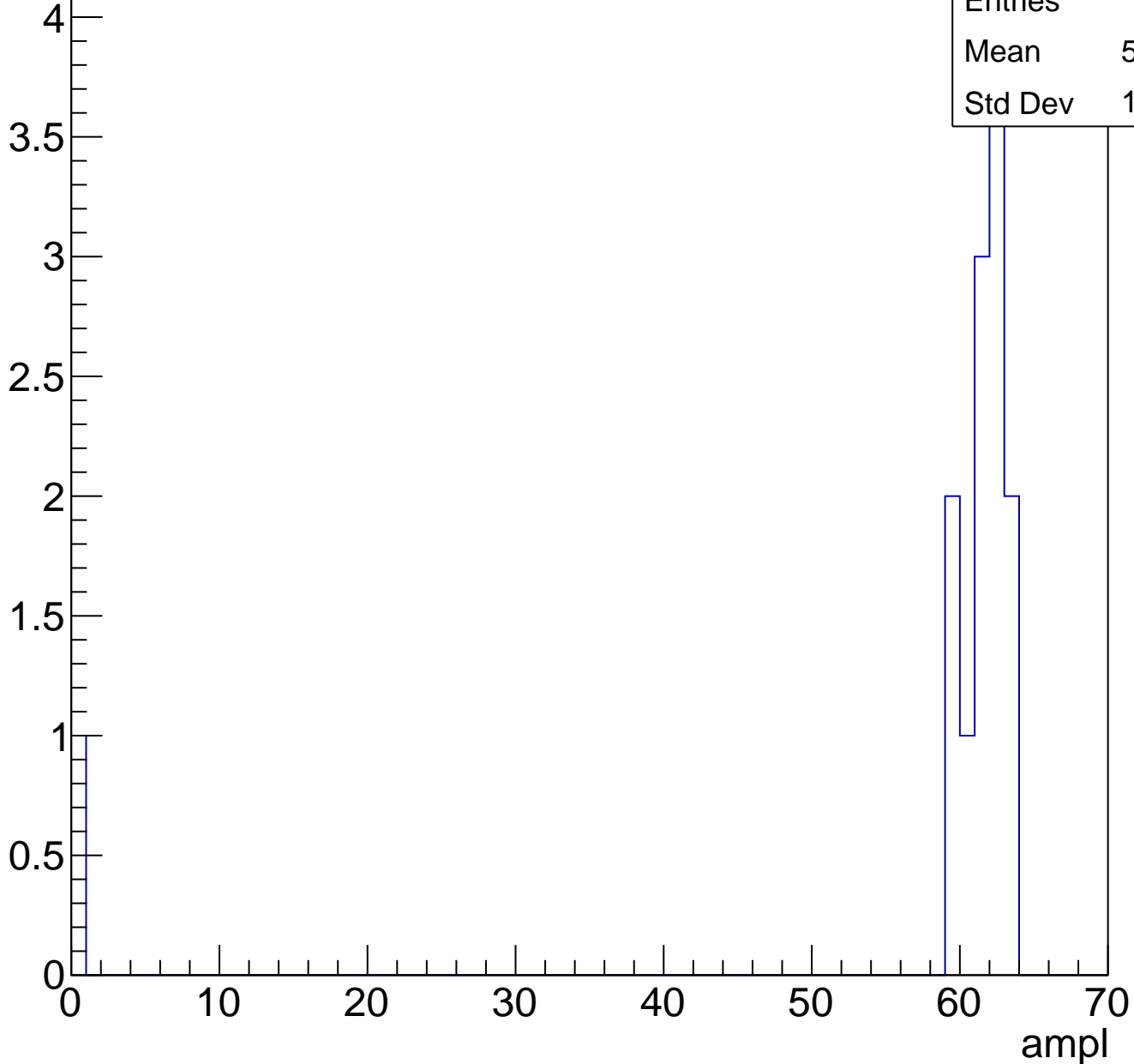
Entry



B1L103S, U21-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U21-ch98, adc0

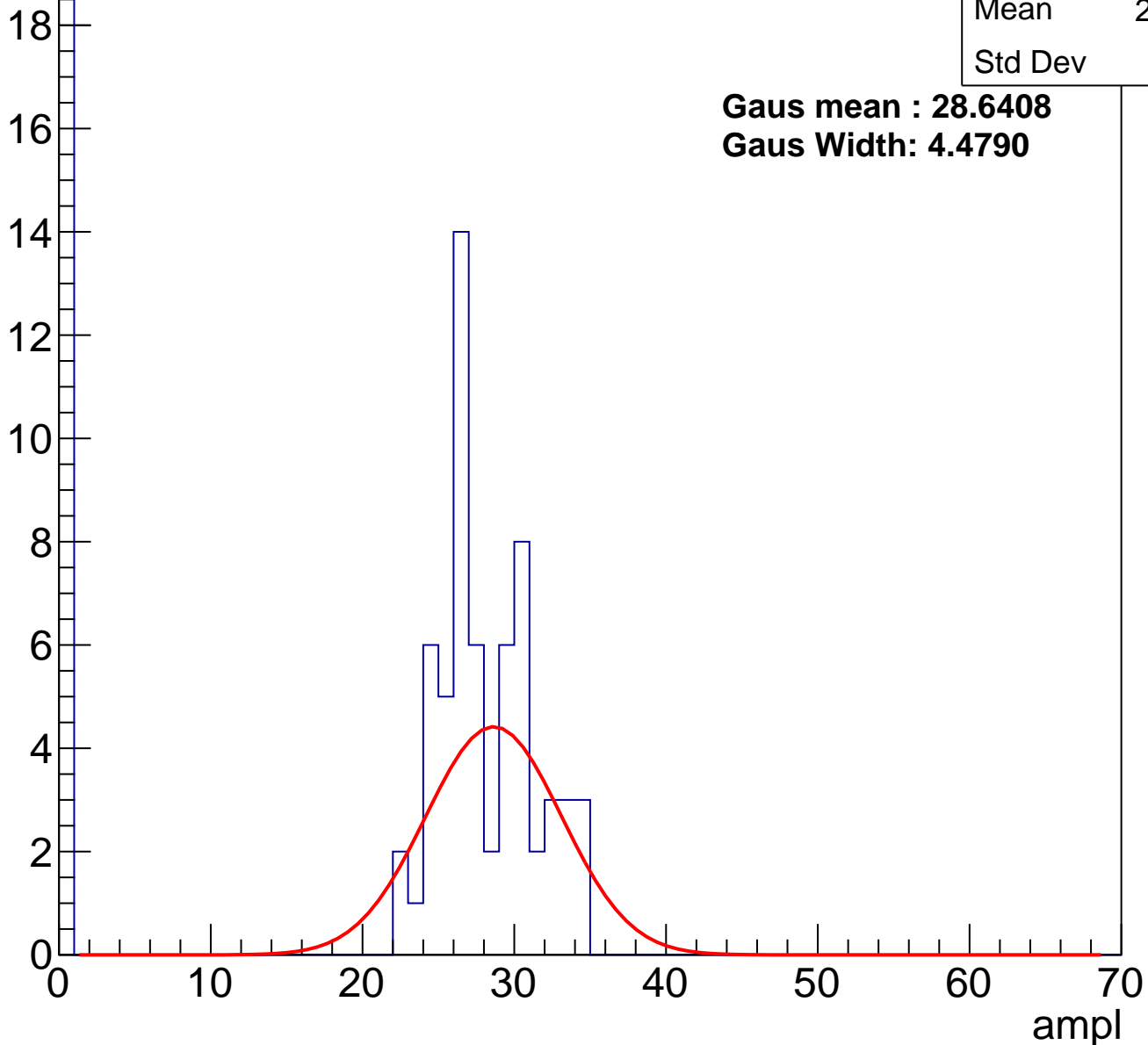
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	21.14
Std Dev	12.1

Gaus mean : 28.6408

Gaus Width: 4.4790

Entry



B1L103S, U21-ch98, adc1

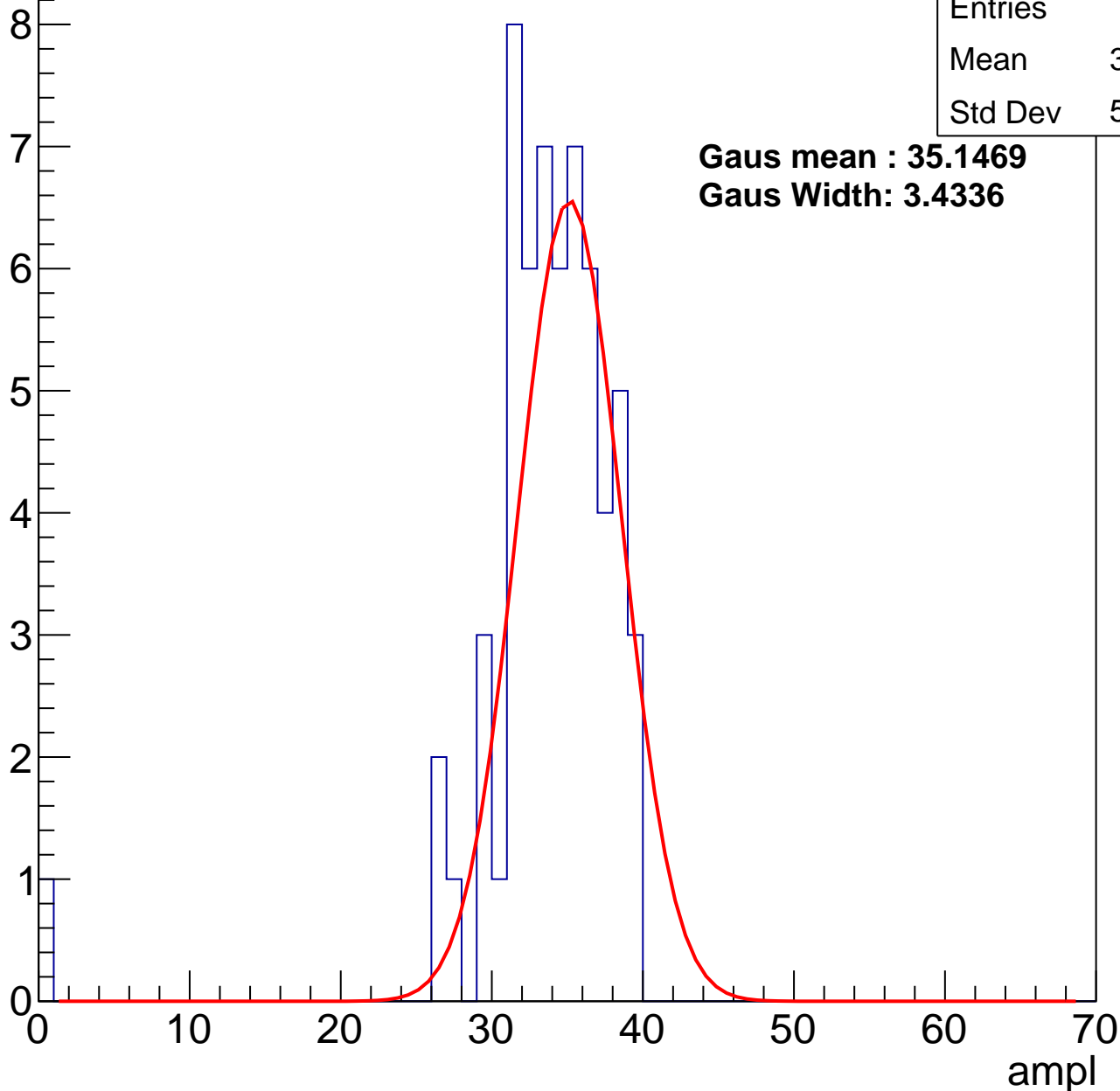
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	33.12
Std Dev	5.326

Gaus mean : 35.1469

Gaus Width: 3.4336



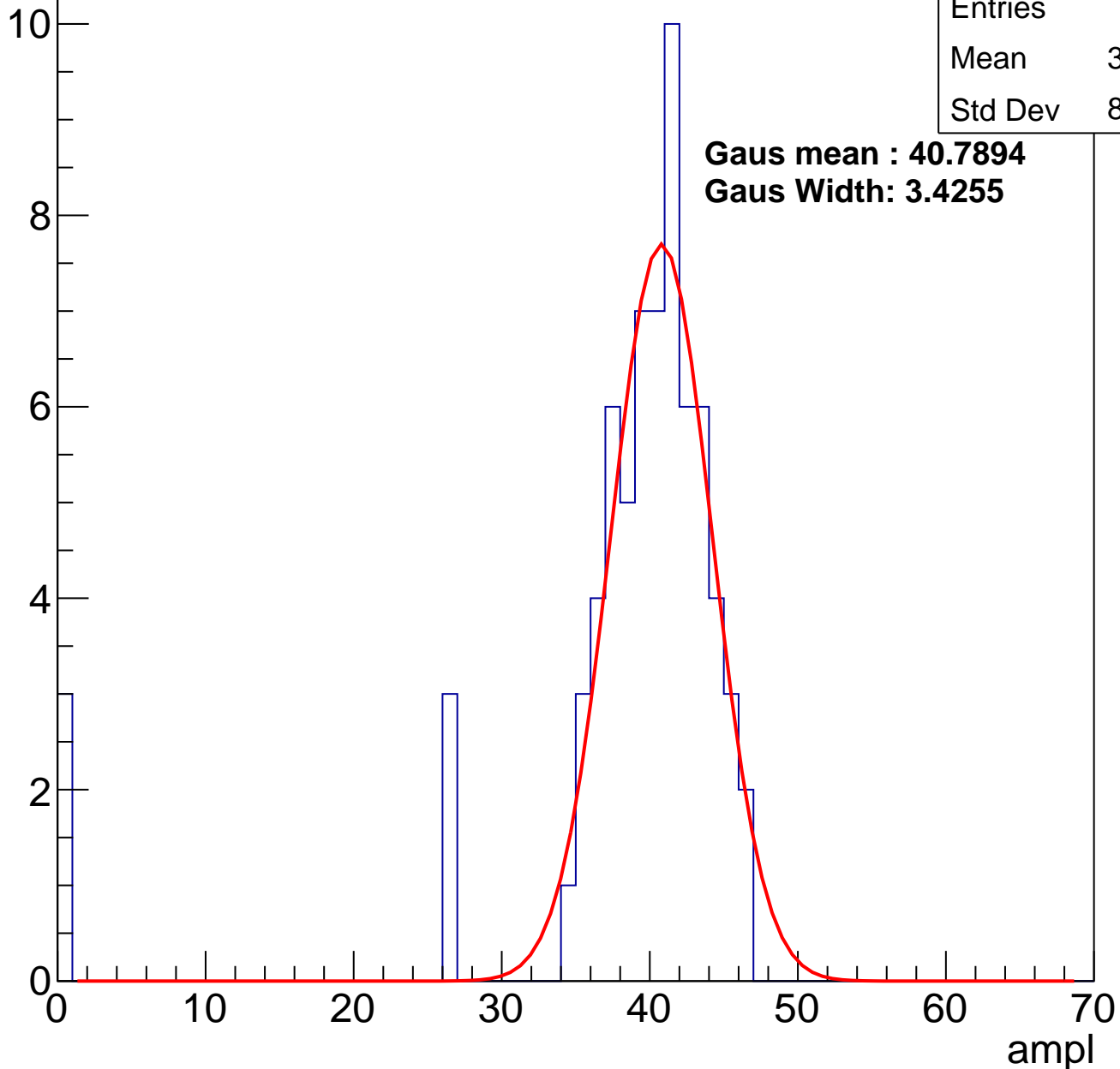
B1L103S, U21-ch98, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	37.84
Std Dev	8.958

Gaus mean : 40.7894
Gaus Width: 3.4255

Entry



B1L103S, U21-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	38.38
Std Dev	17.82

Entry

12

10

8

6

4

2

0

0

10

20

30

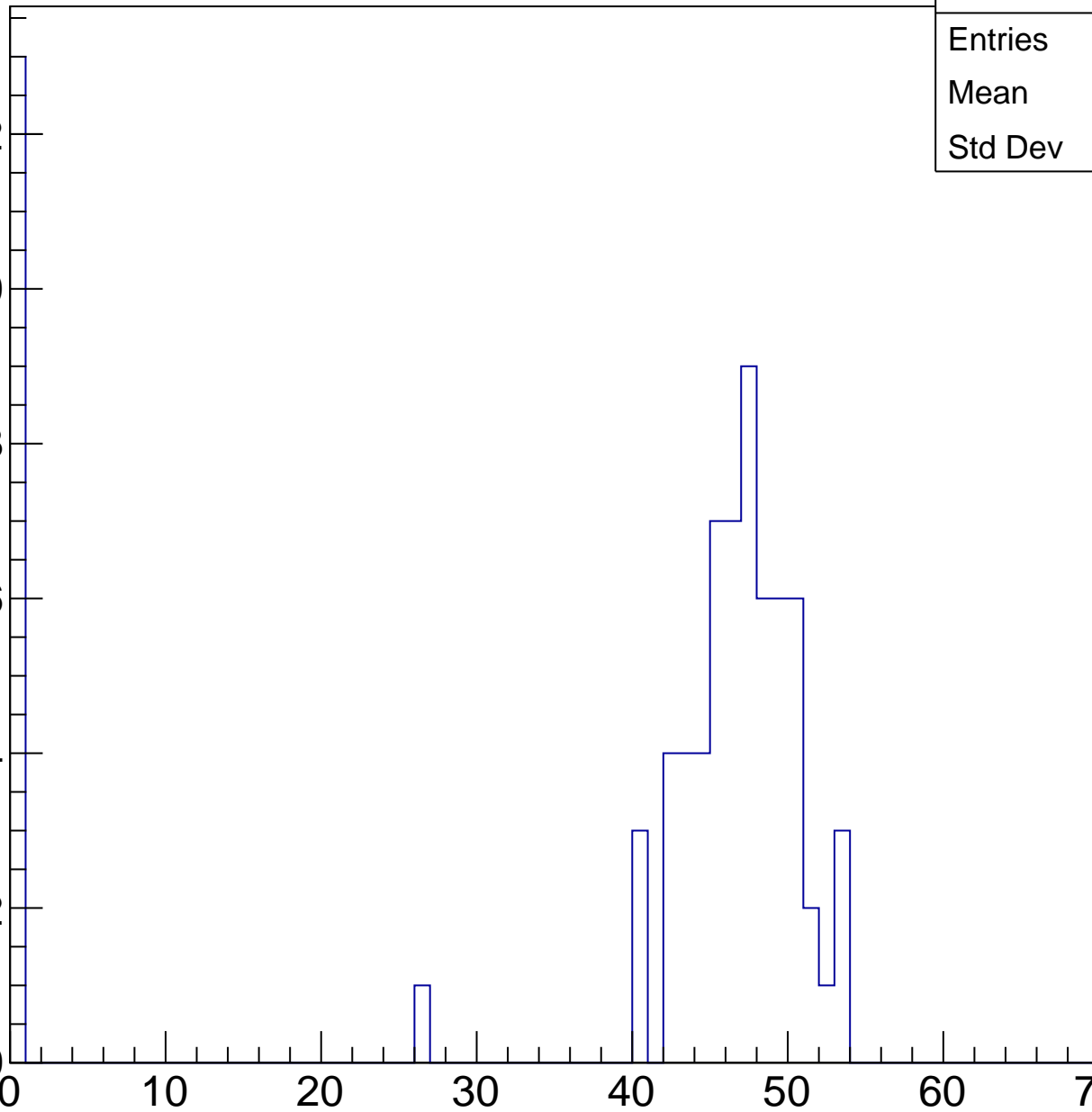
40

50

60

70

ampl

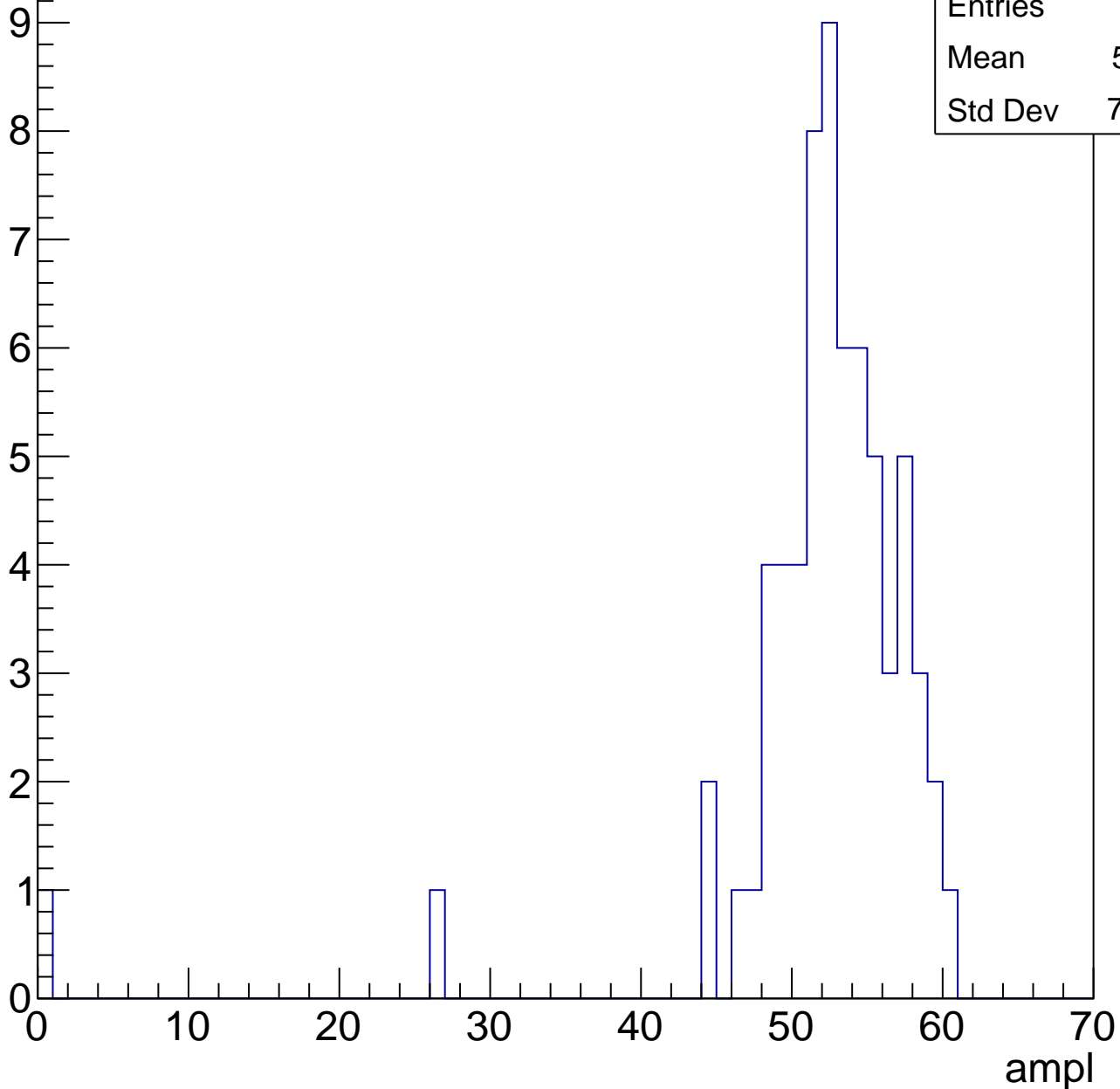


B1L103S, U21-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

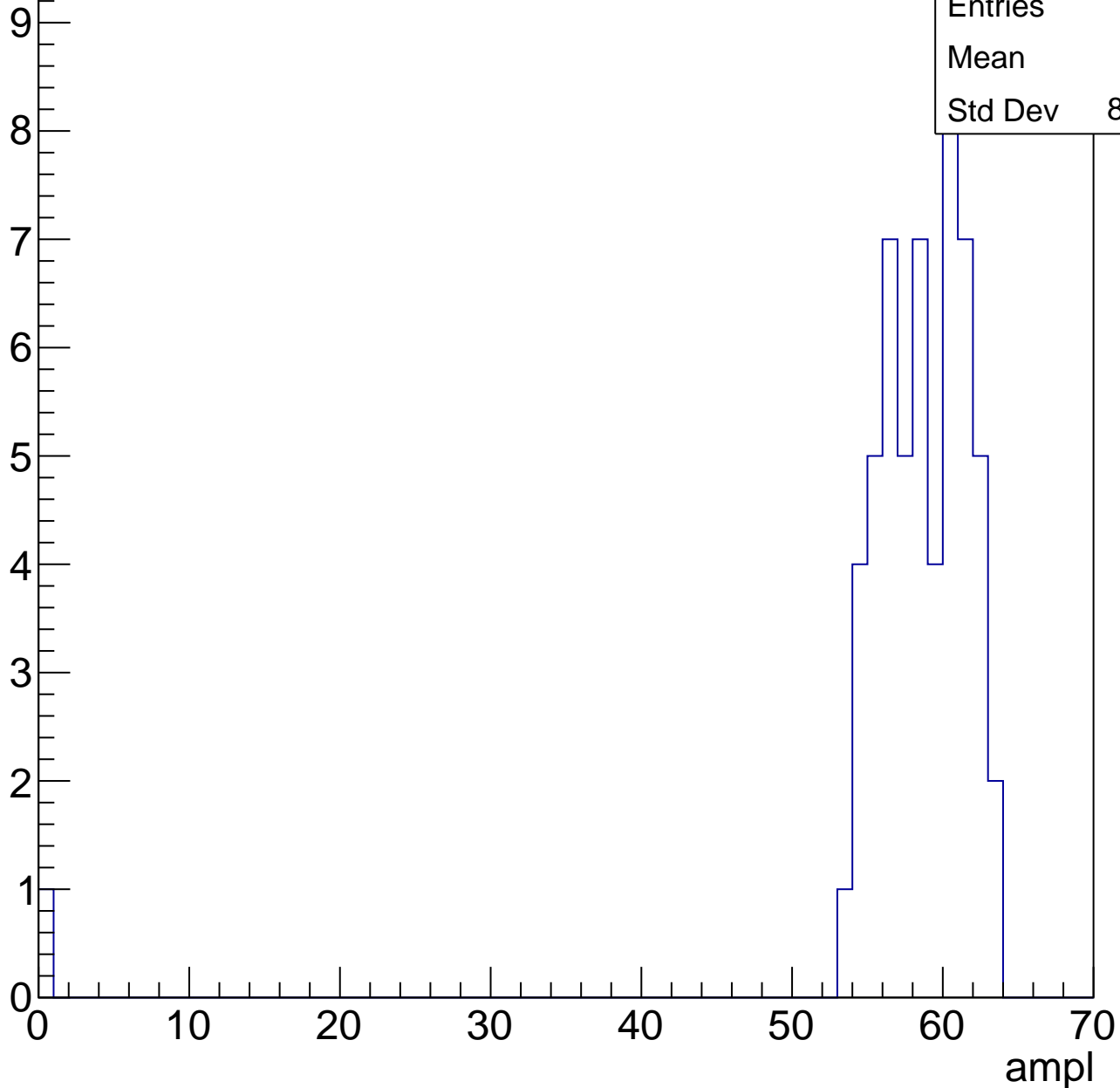
Entries	66
Mean	51.41
Std Dev	7.966



B1L103S, U21-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

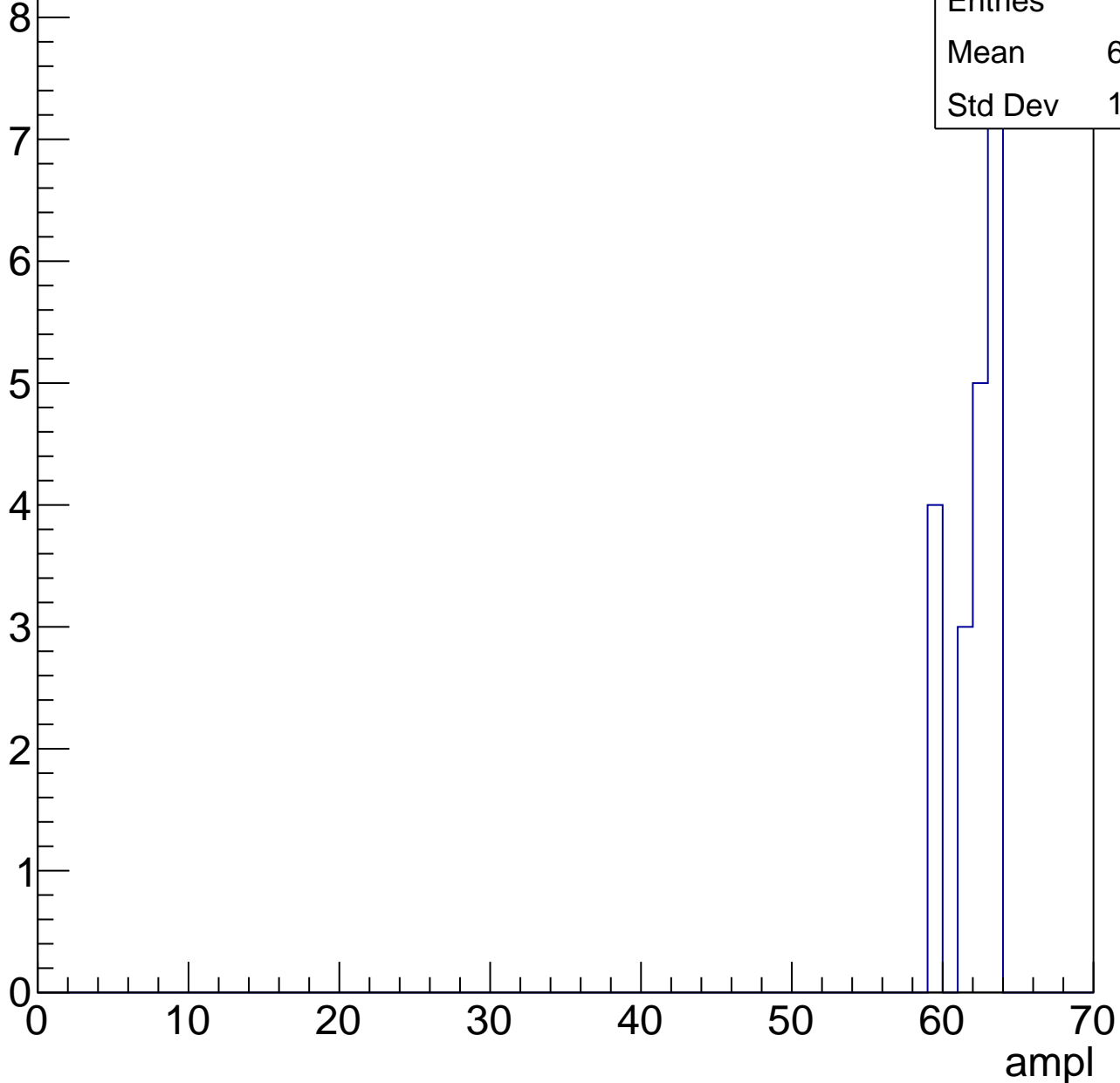


B1L103S, U21-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	61.65
Std Dev	1.492

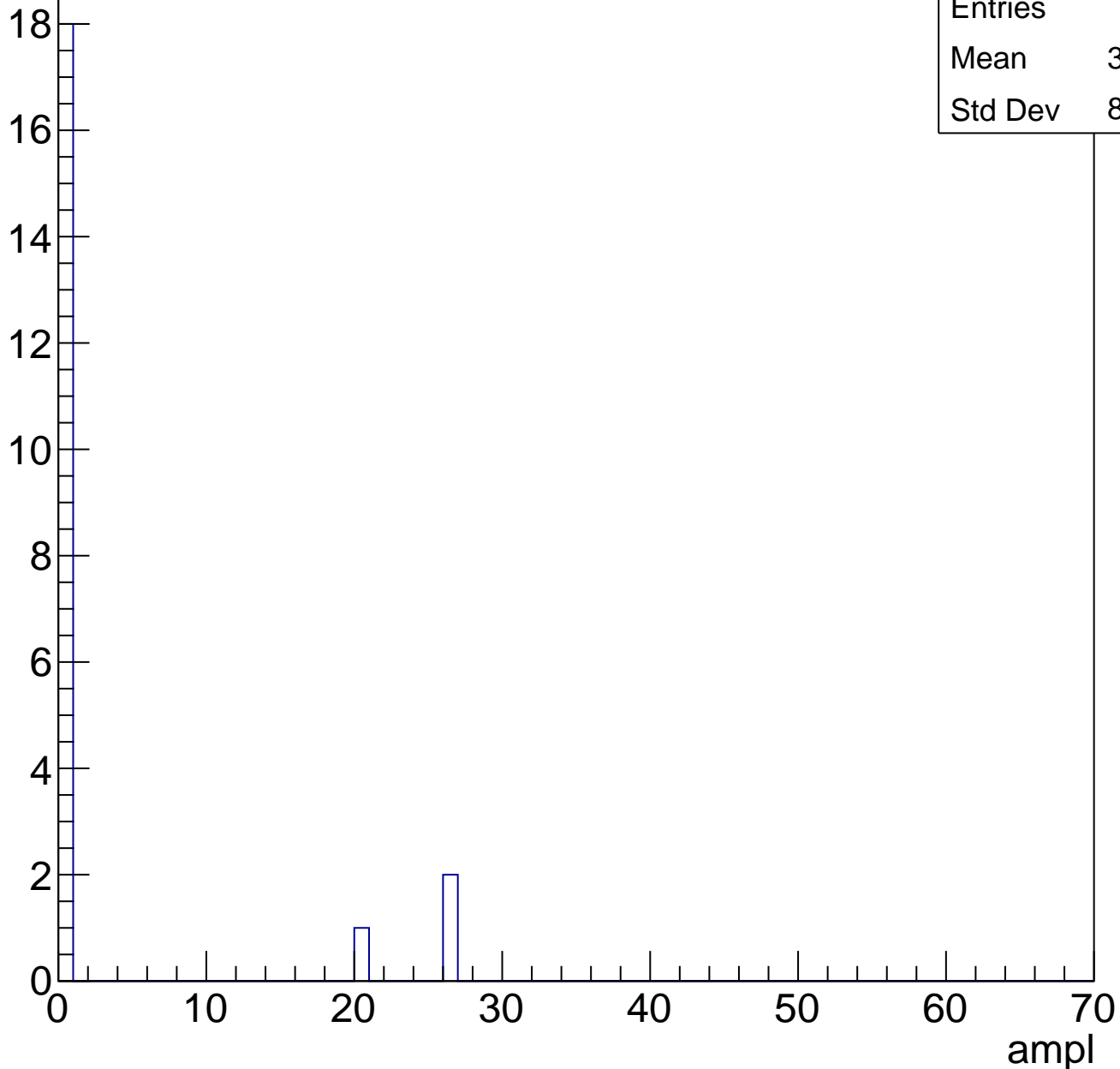


B1L103S, U21-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	3.429
Std Dev	8.466

Entry



B1L103S, U21-ch99, adc0

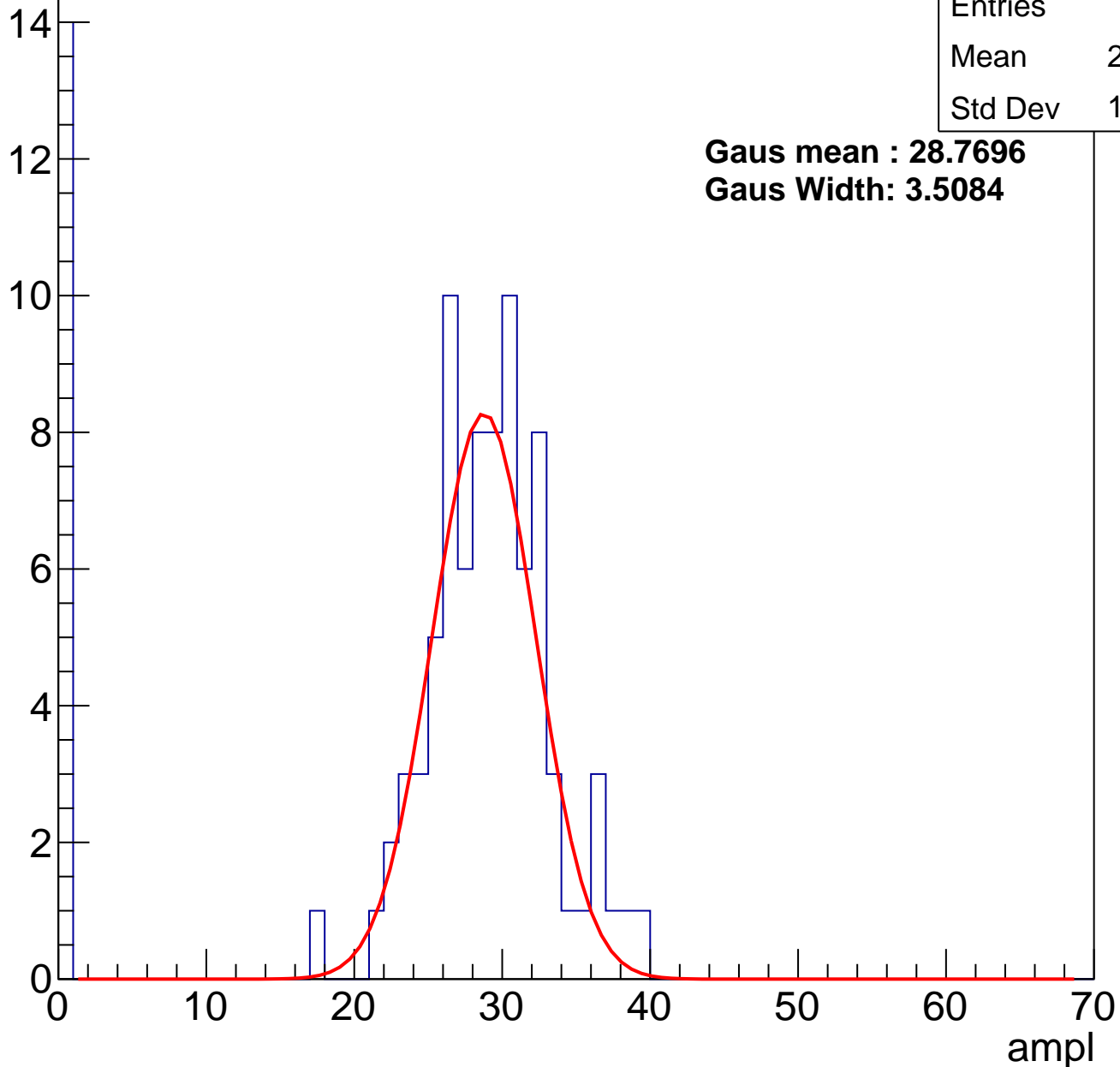
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	24.56
Std Dev	10.79

Gaus mean : 28.7696

Gaus Width: 3.5084

Entry



B1L103S, U21-ch99, adc1

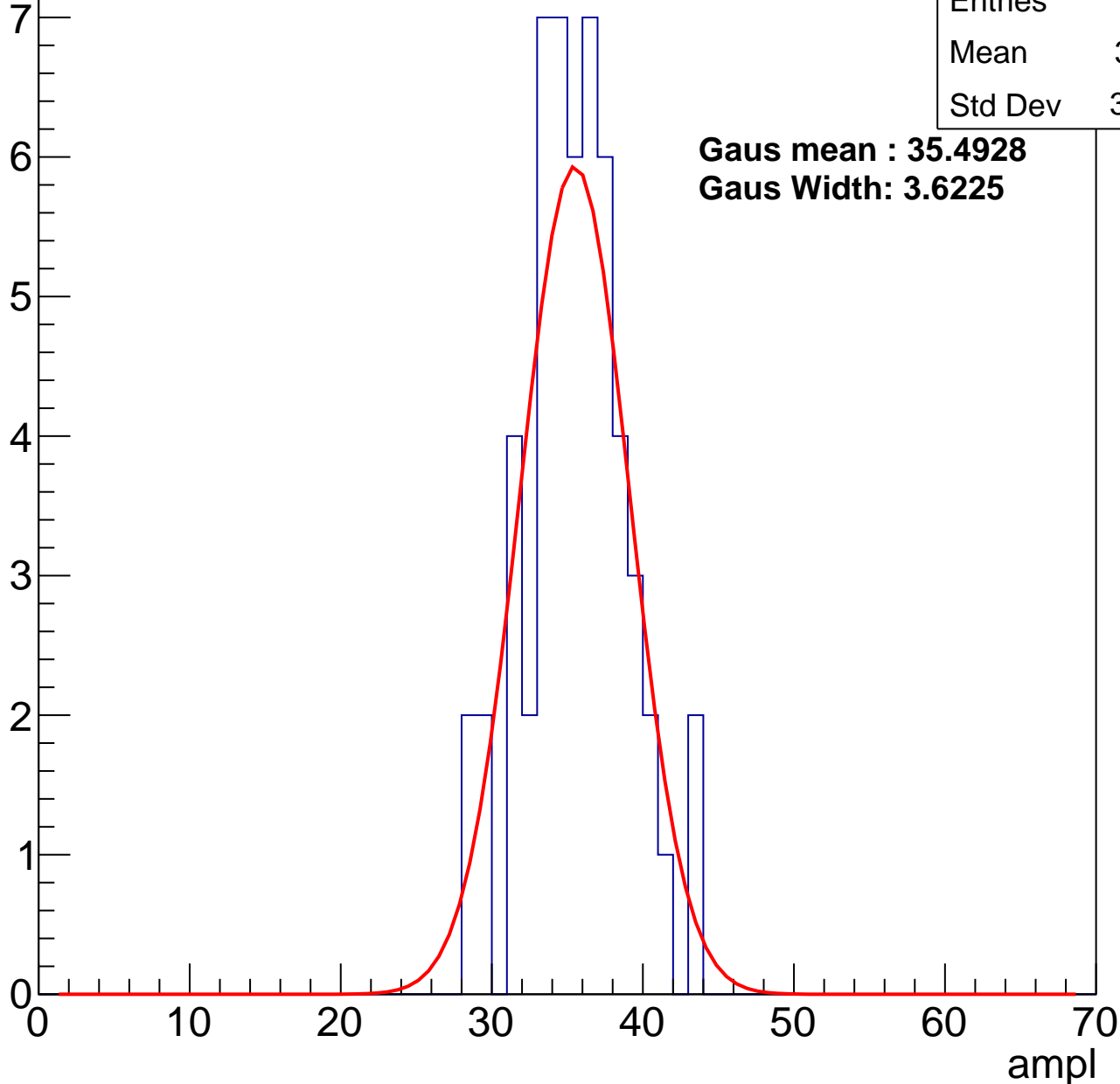
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	35.11
Std Dev	3.345

Gaus mean : 35.4928

Gaus Width: 3.6225



B1L103S, U21-ch99, adc2

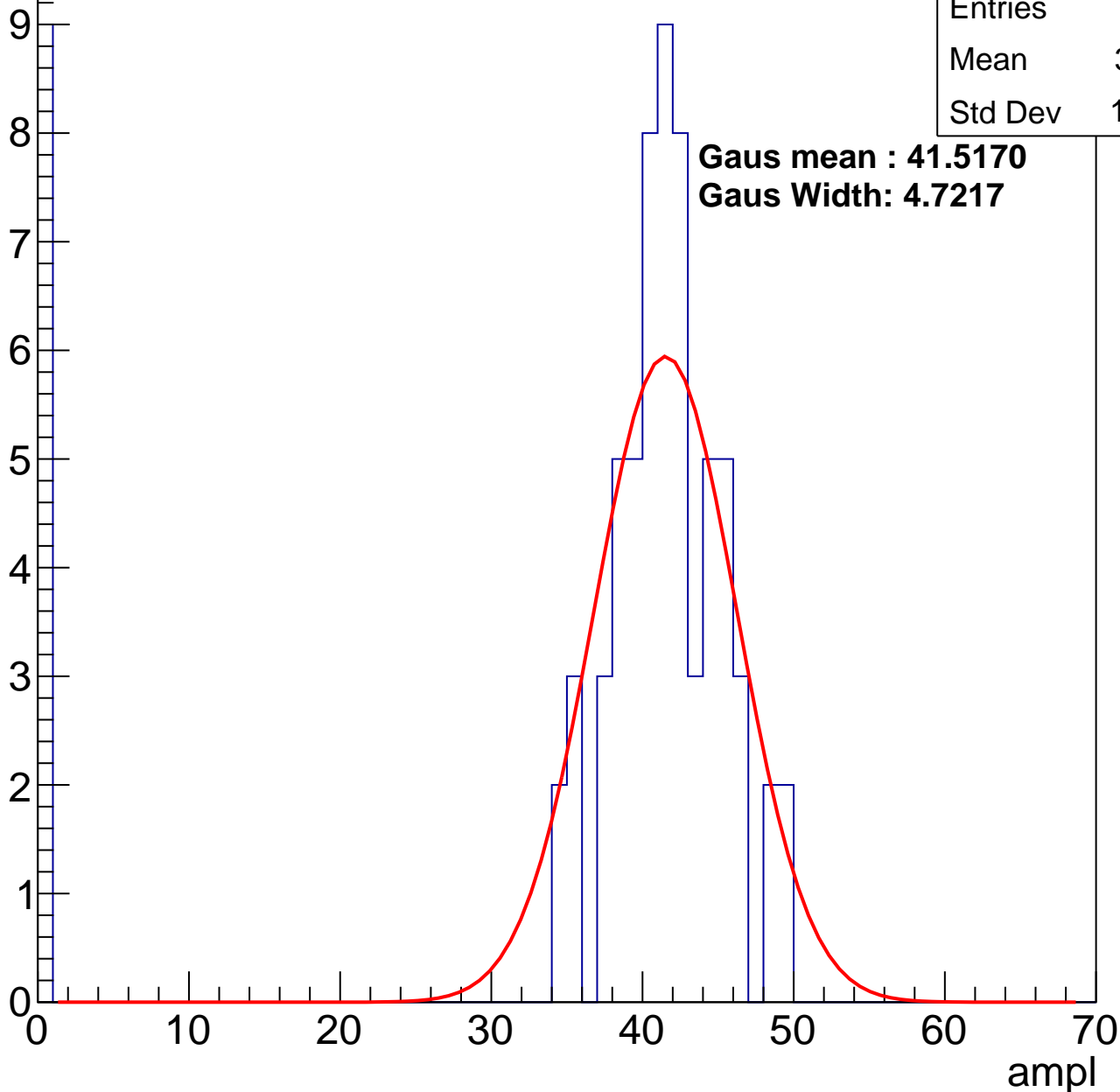
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	36.11
Std Dev	14.03

Gaus mean : 41.5170

Gaus Width: 4.7217



B1L103S, U21-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	46.1
Std Dev	9.891

Entry

10

8

6

4

2

0

0

10

20

30

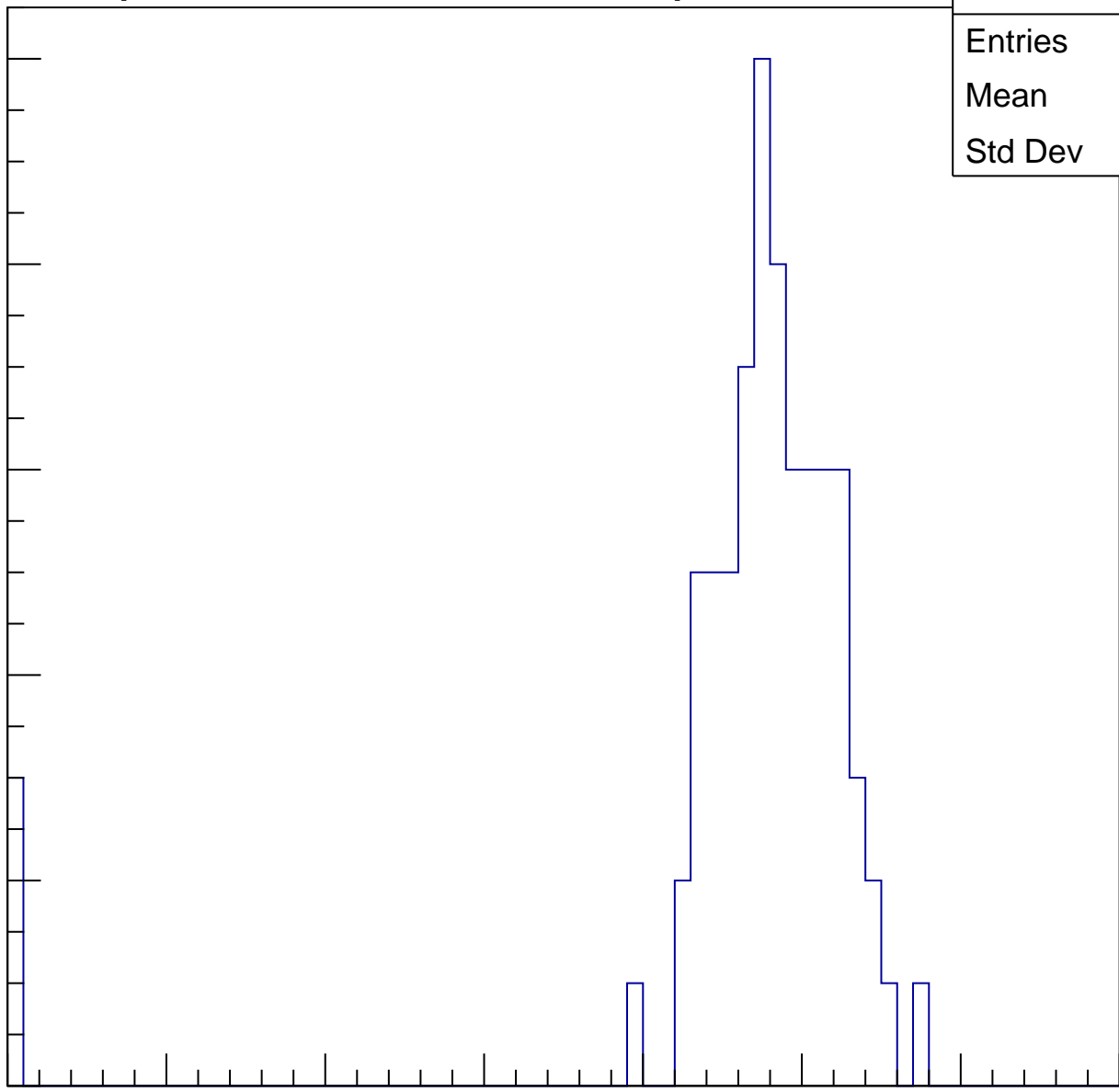
40

50

60

70

ampl

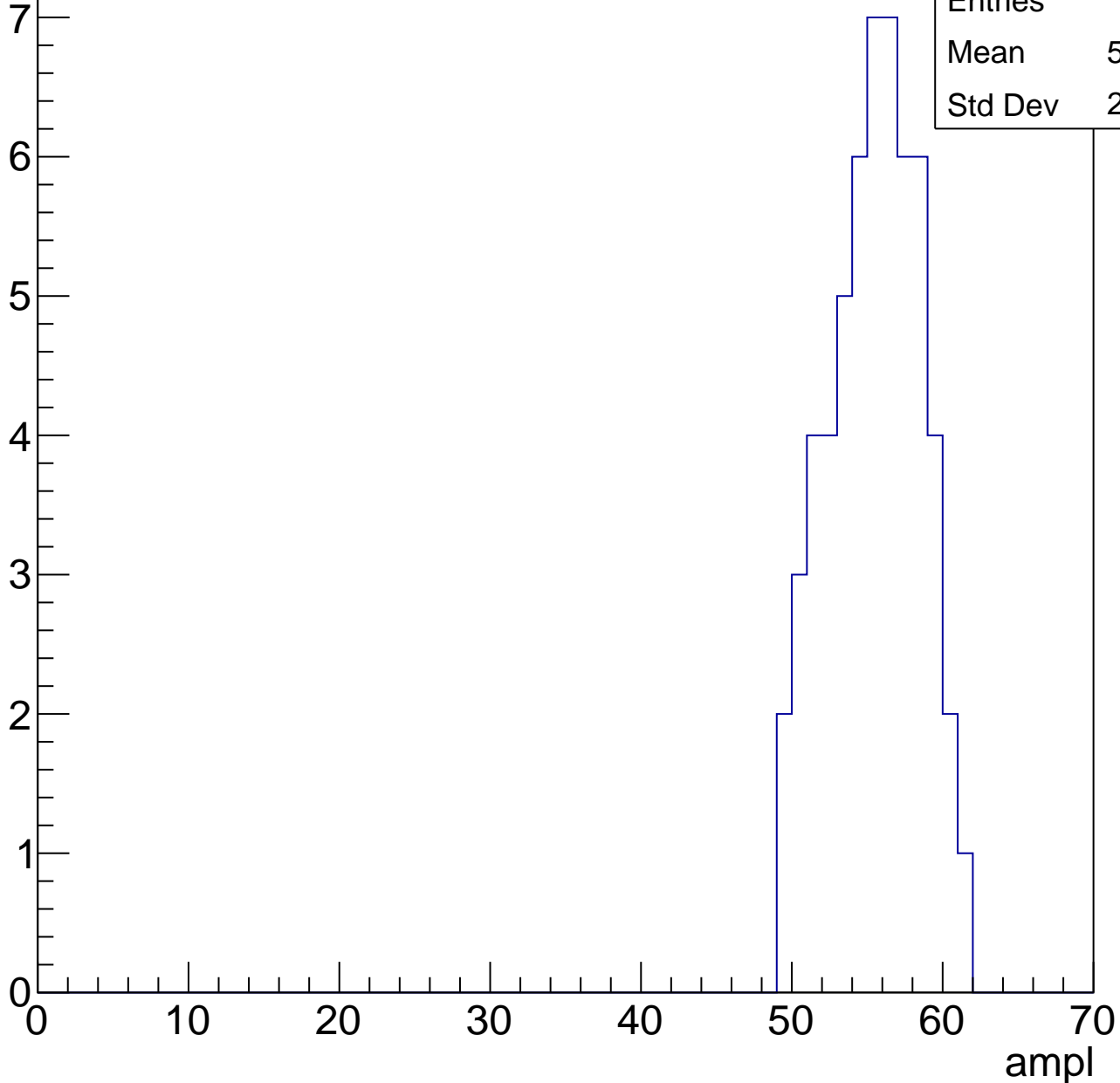


B1L103S, U21-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.96
Std Dev	2.985

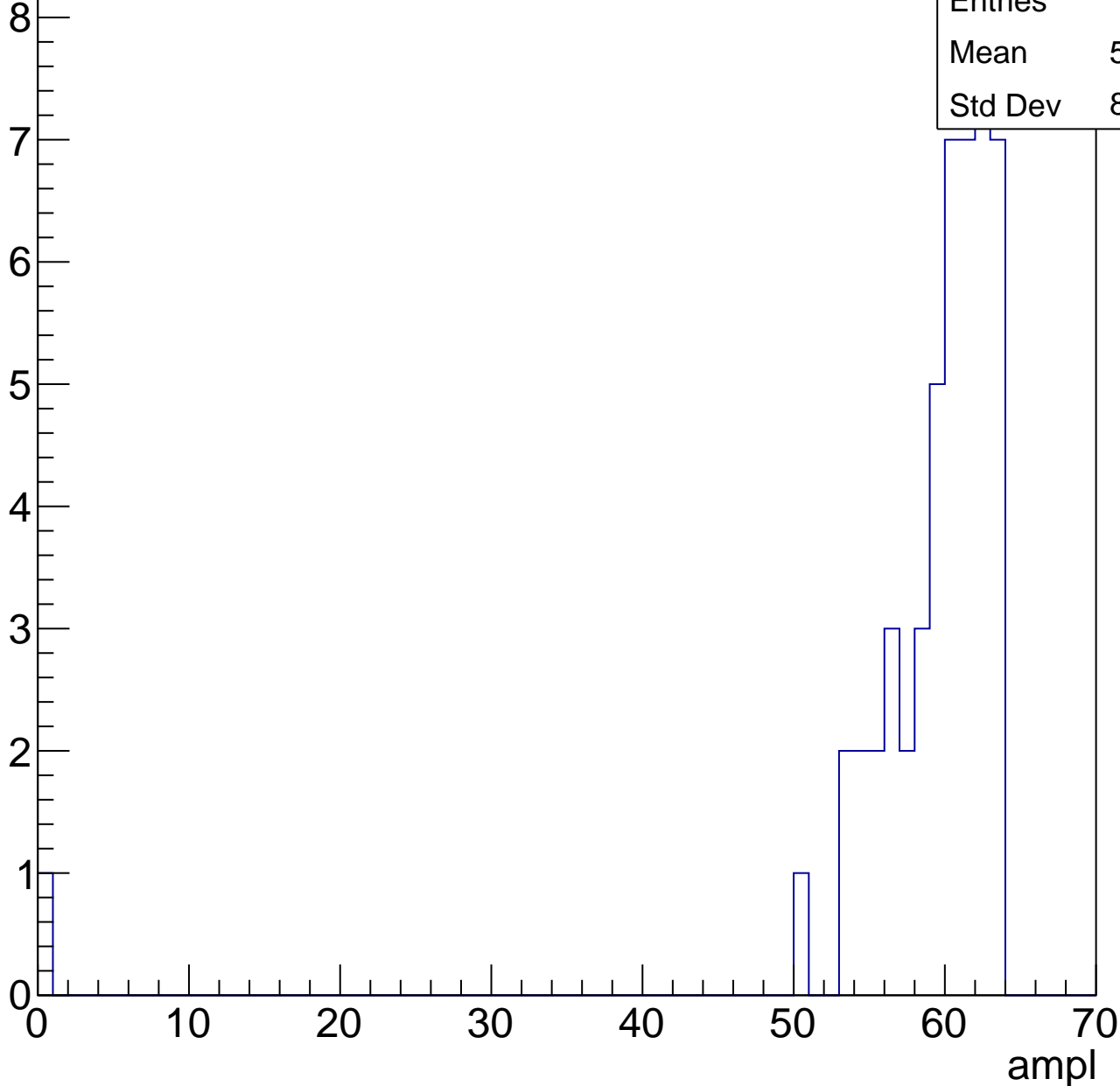


B1L103S, U21-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

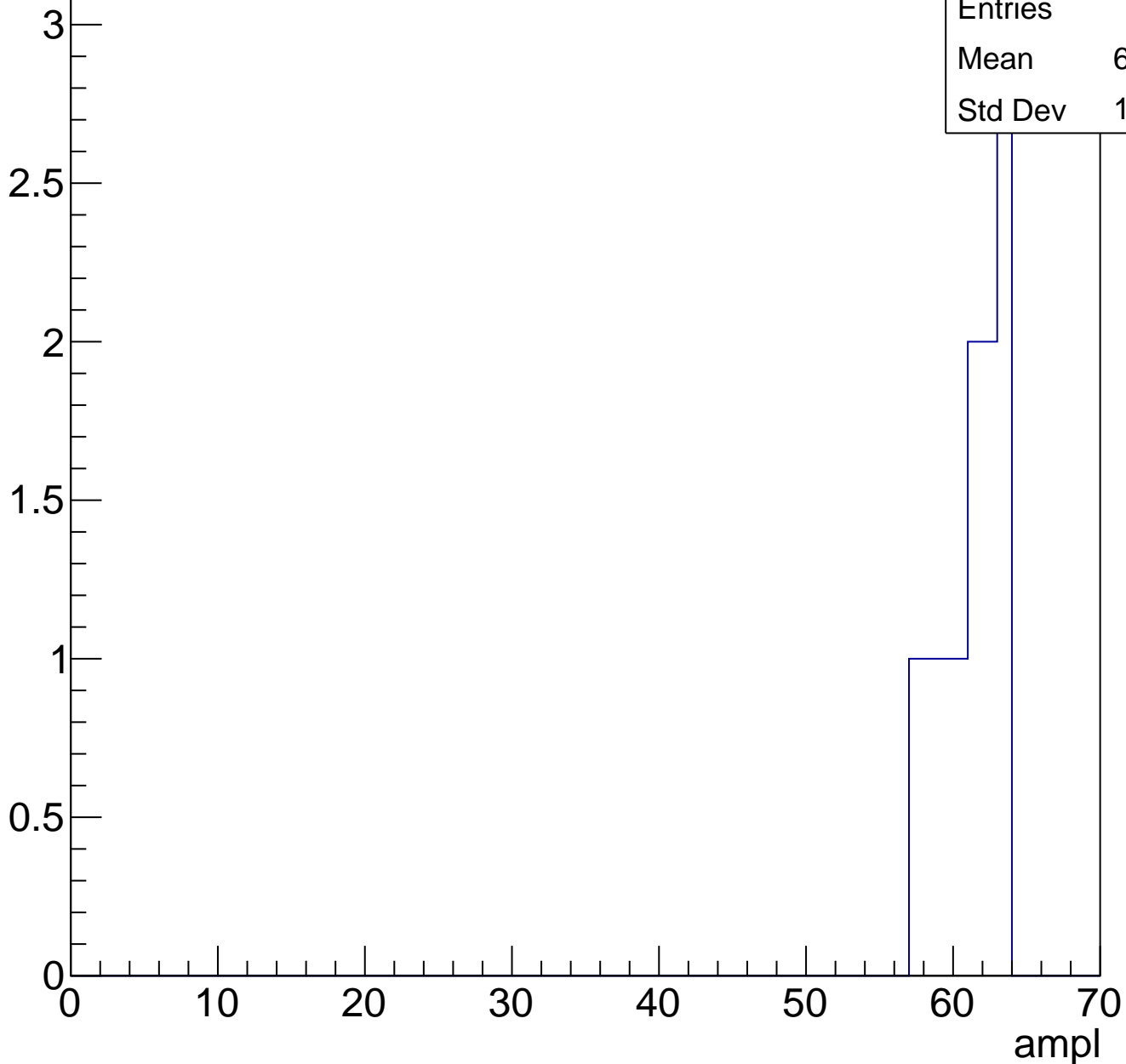
Entries	50
Mean	58.18
Std Dev	8.876



B1L103S, U21-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

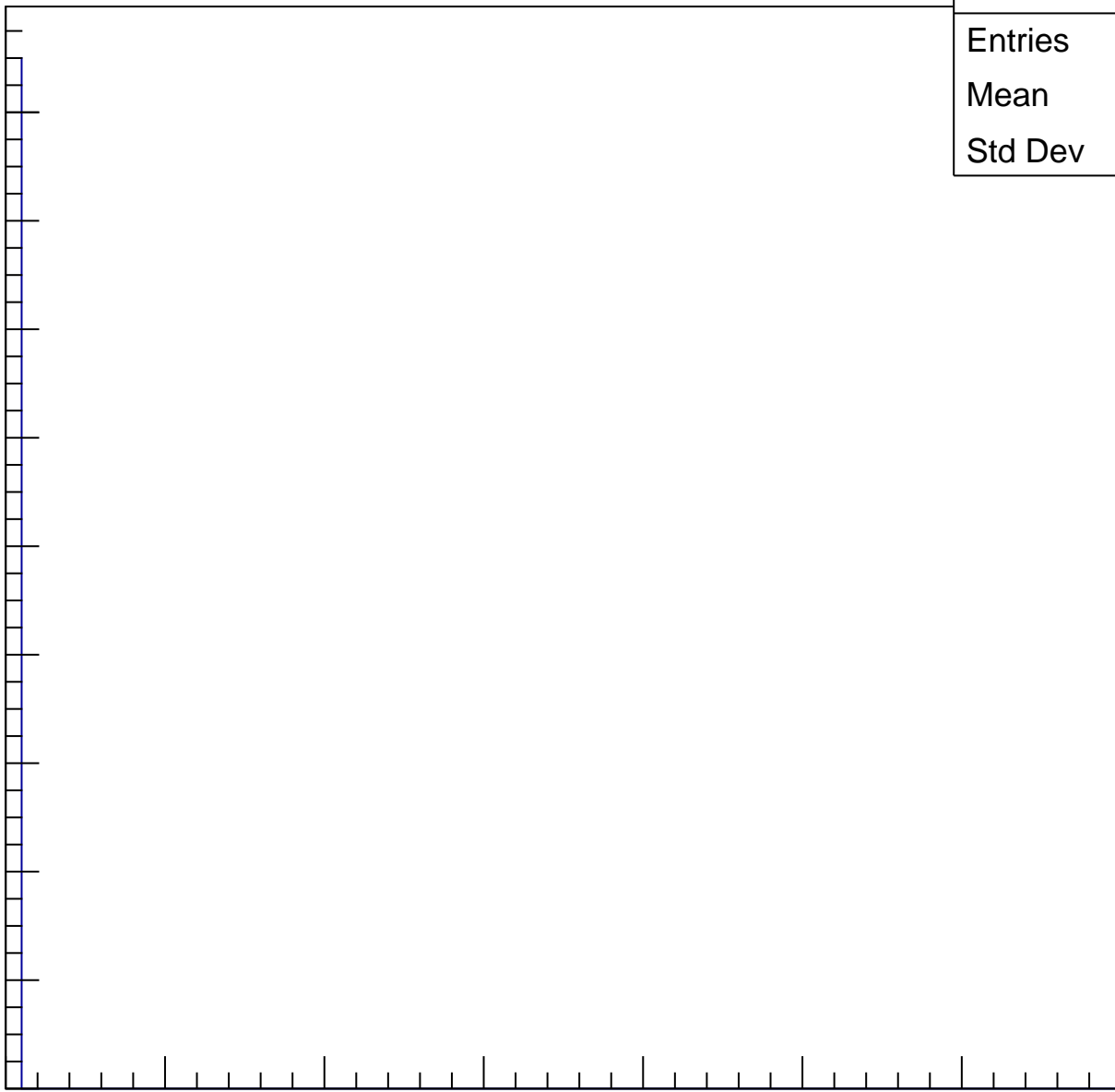
Entries	19
Mean	0
Std Dev	0

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch100, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	21.61
Std Dev	10.75

Gaus mean : 26.6779

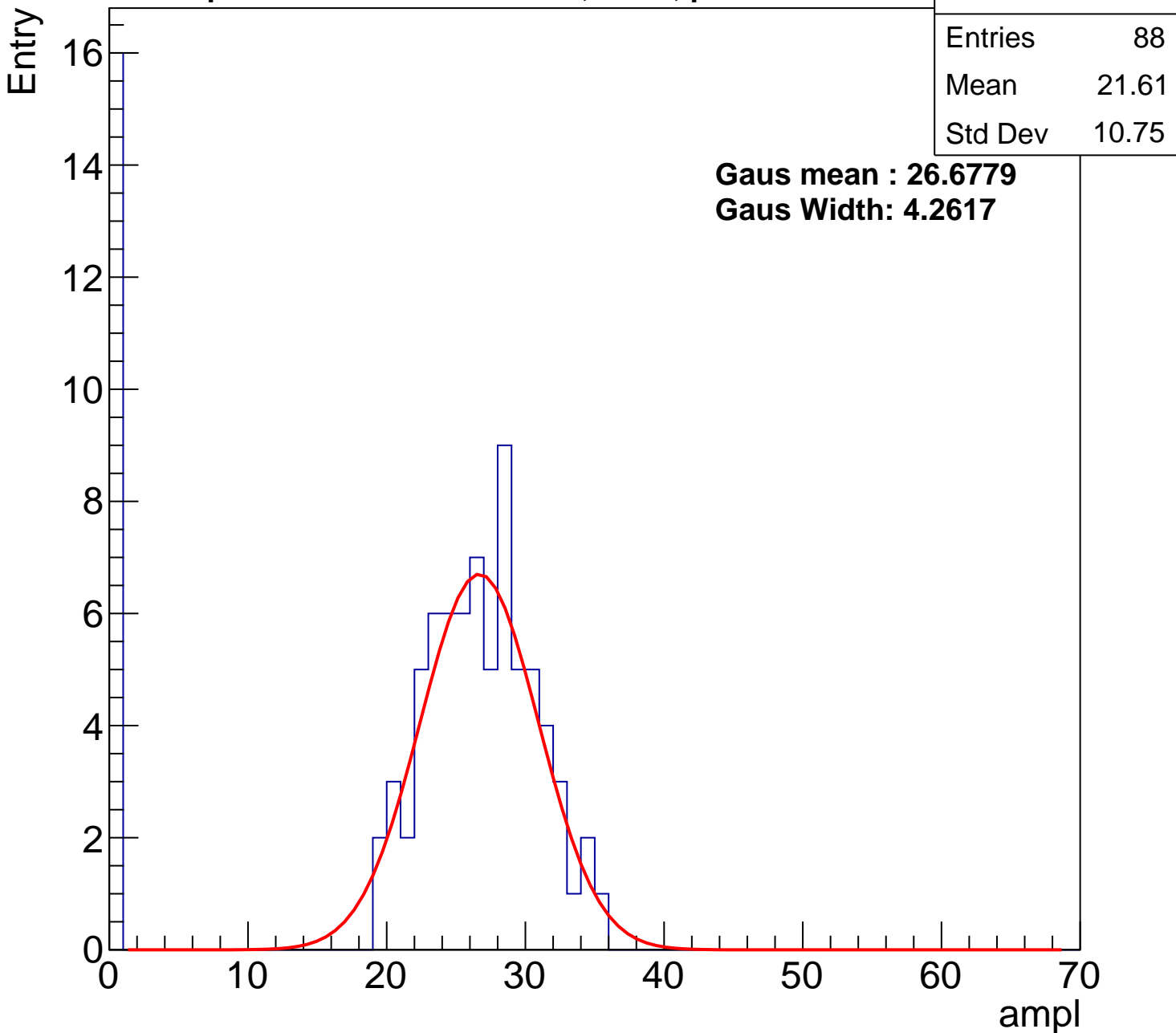
Gaus Width: 4.2617

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch100, adc1

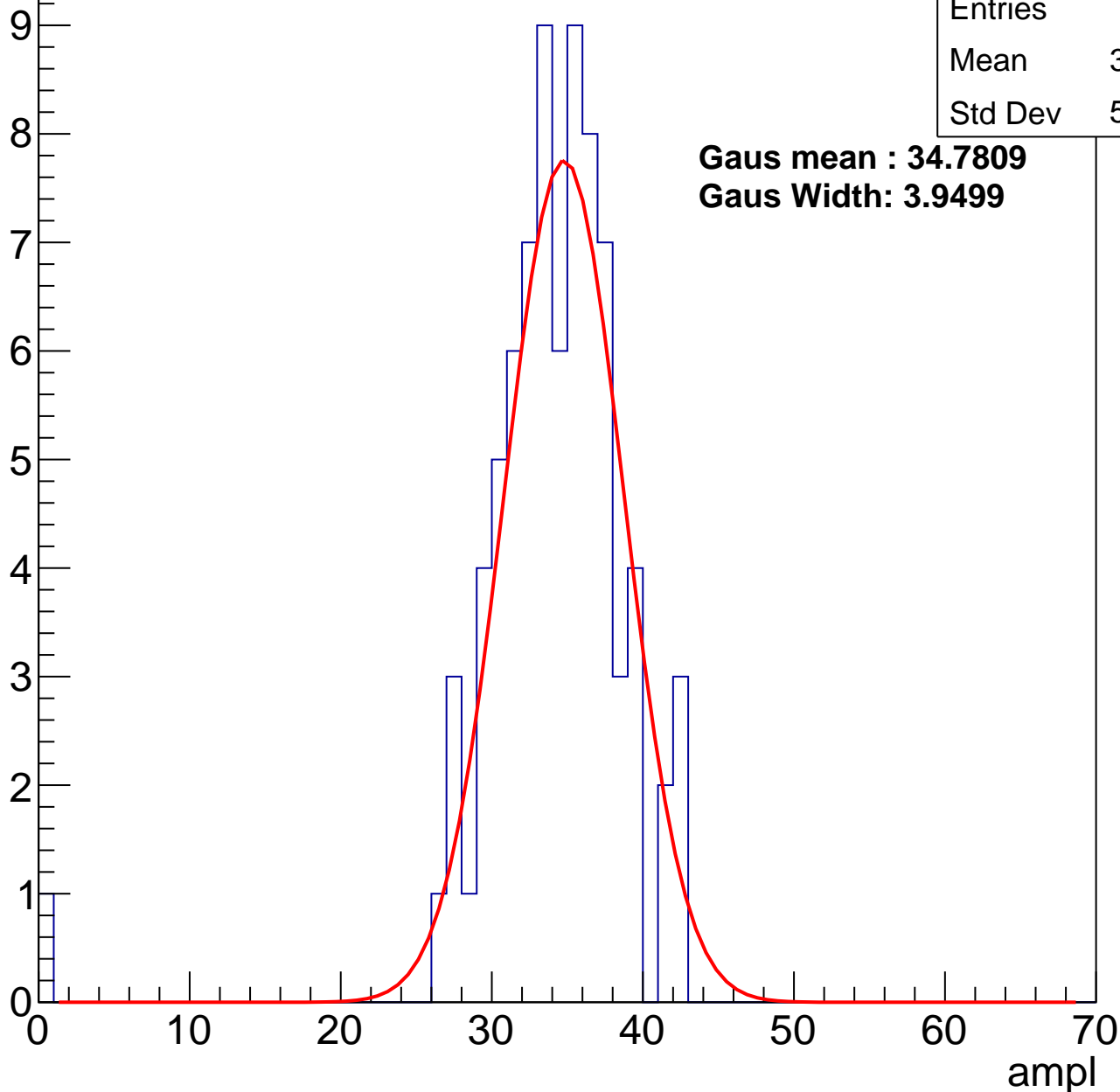
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	33.57
Std Dev	5.279

Gaus mean : 34.7809

Gaus Width: 3.9499



B1L103S, U21-ch100, adc2

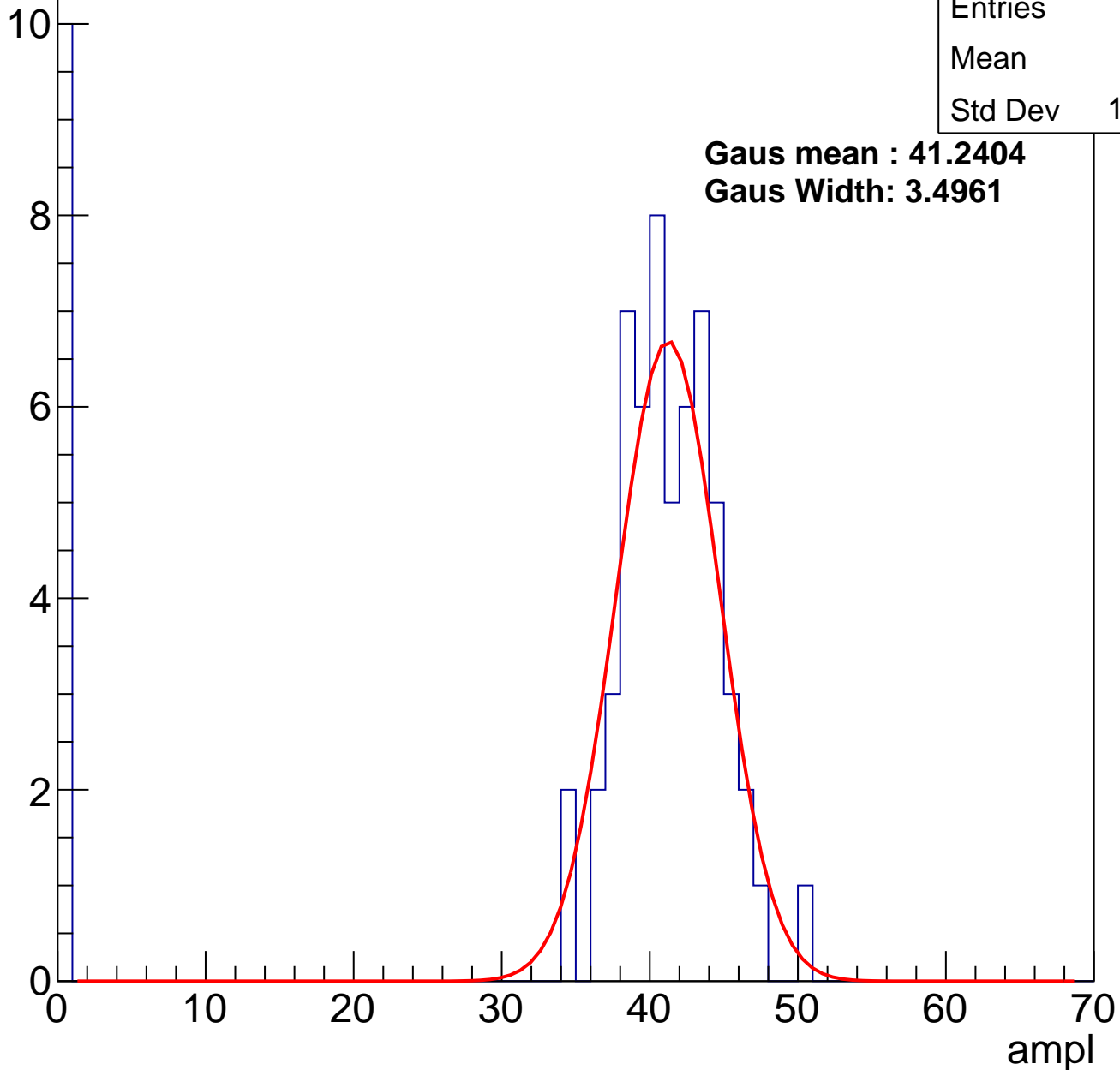
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	34.9
Std Dev	14.78

Gaus mean : 41.2404

Gaus Width: 3.4961

Entry

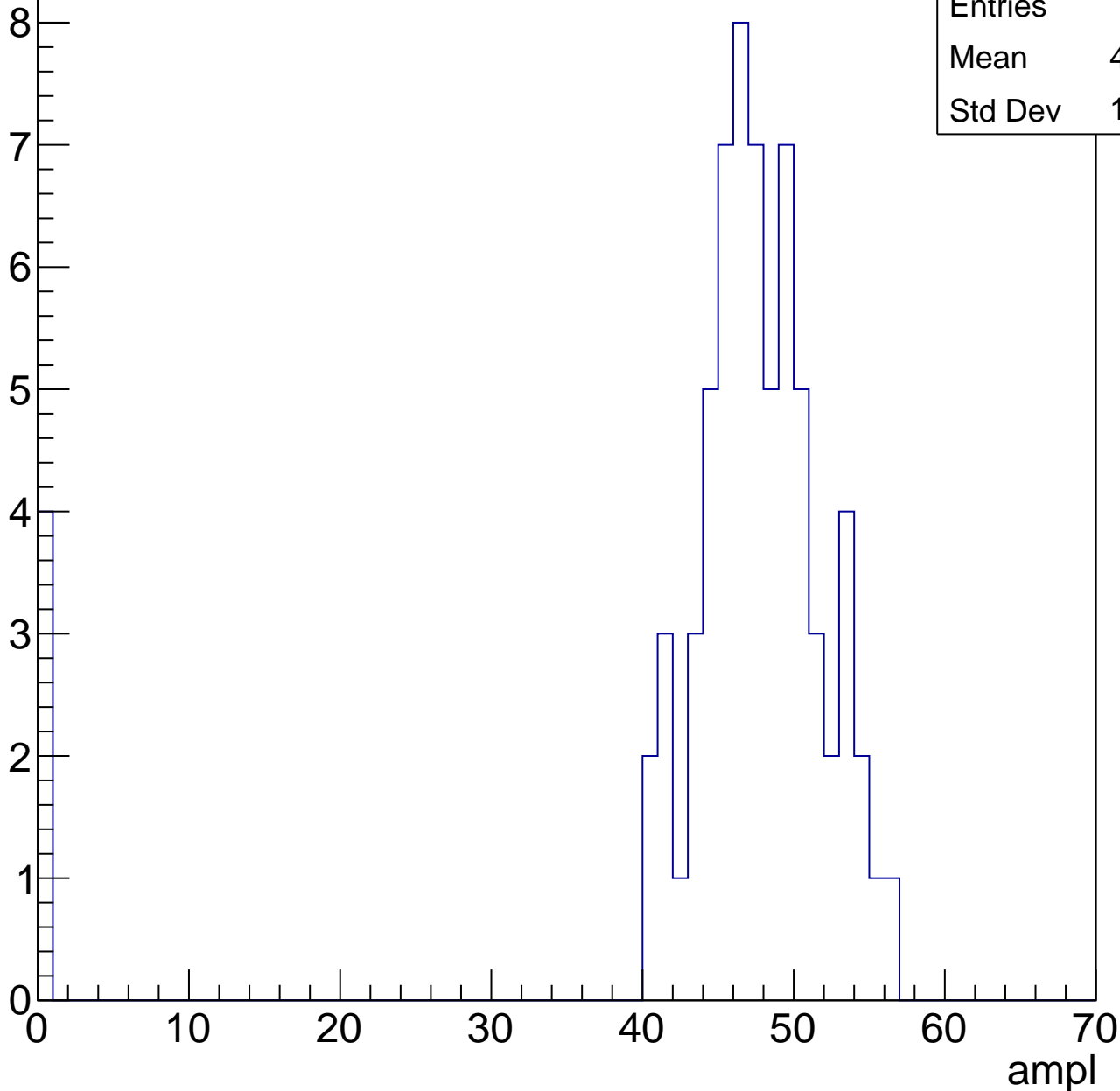


B1L103S, U21-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	44.67
Std Dev	11.58

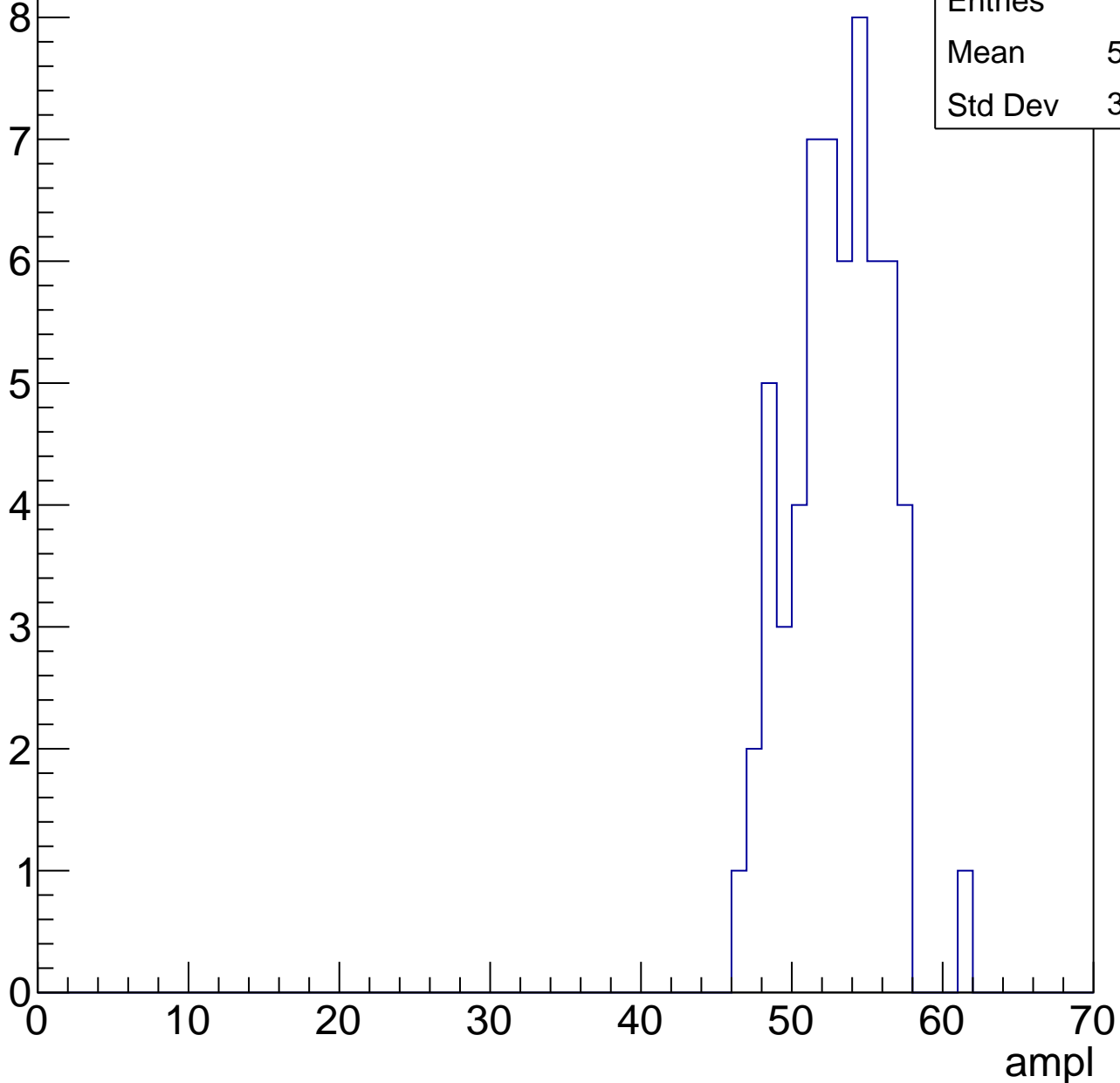


B1L103S, U21-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	52.55
Std Dev	3.074

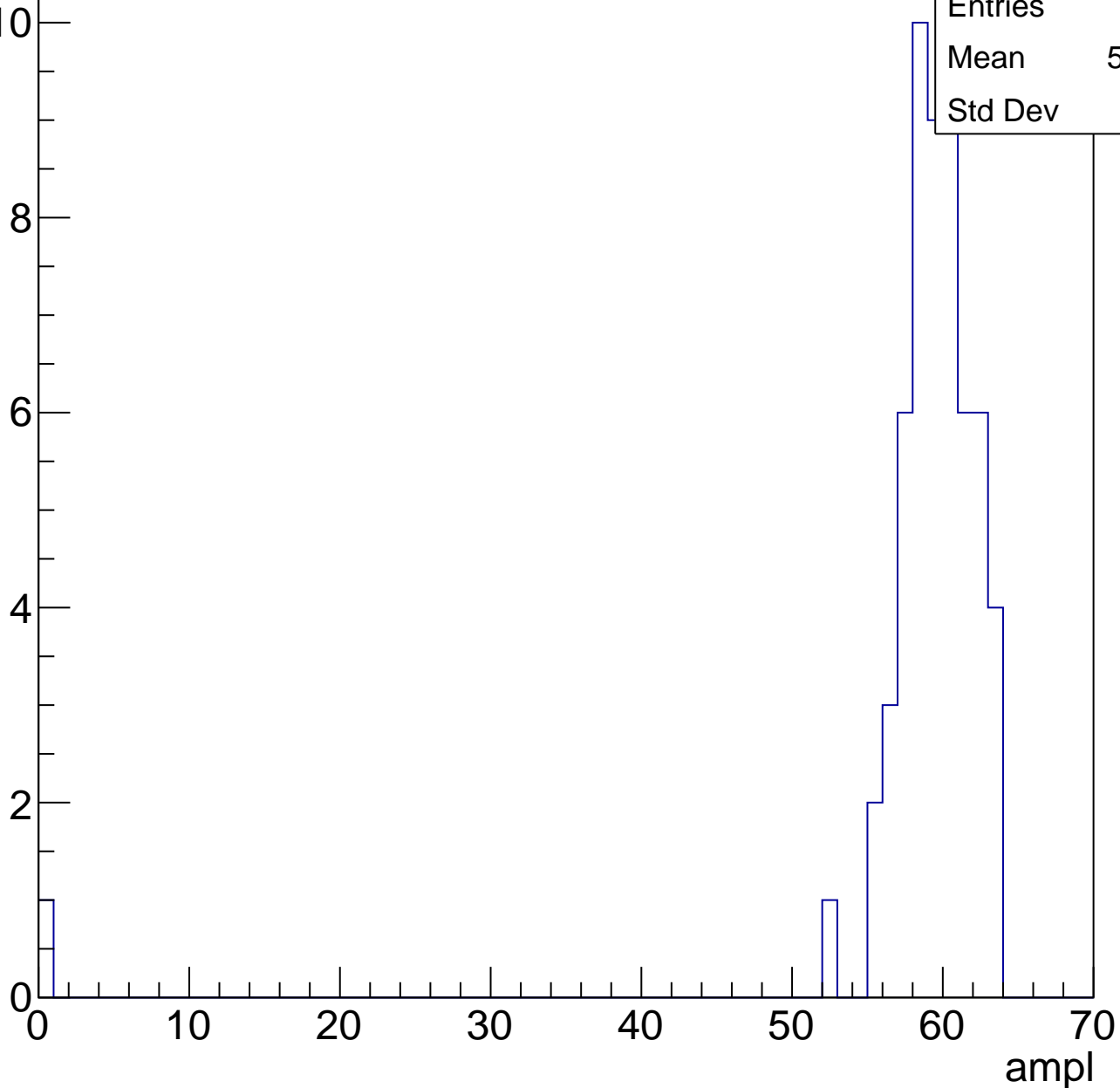


B1L103S, U21-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.12
Std Dev	8.09

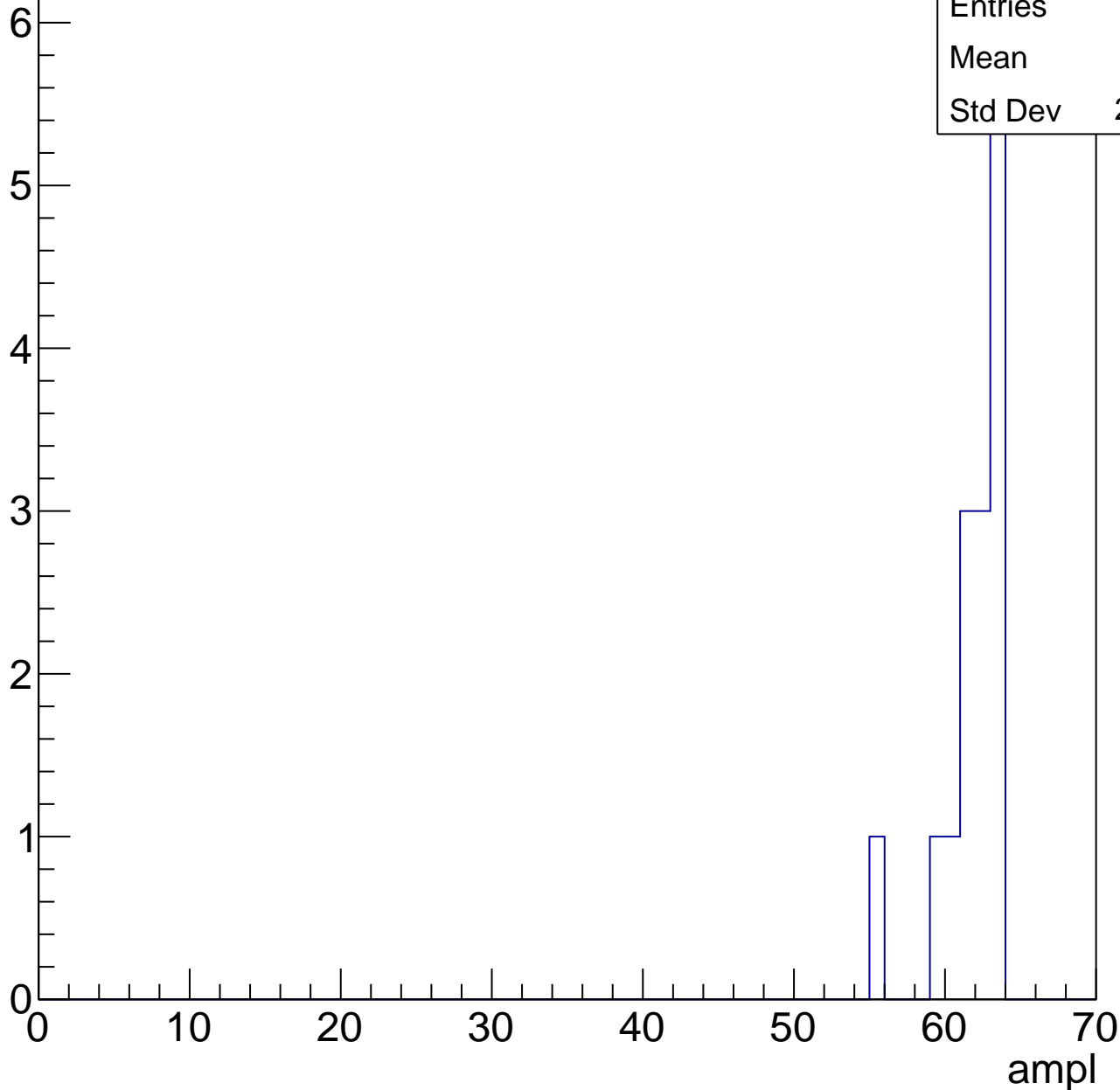


B1L103S, U21-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.4
Std Dev	2.091



B1L103S, U21-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U21-ch101, adc0

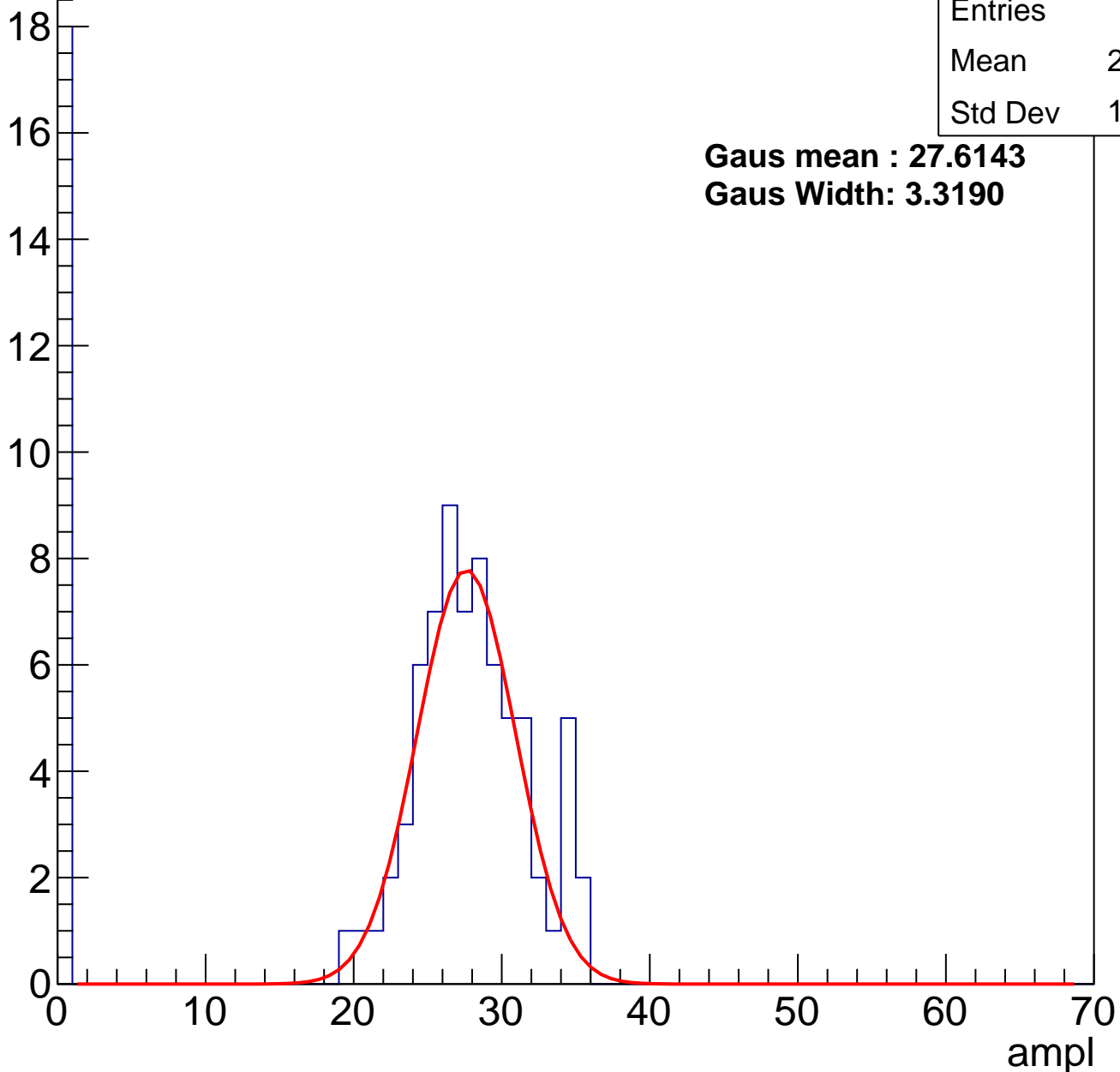
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	21.97
Std Dev	11.52

Gaus mean : 27.6143

Gaus Width: 3.3190

Entry



B1L103S, U21-ch101, adc1

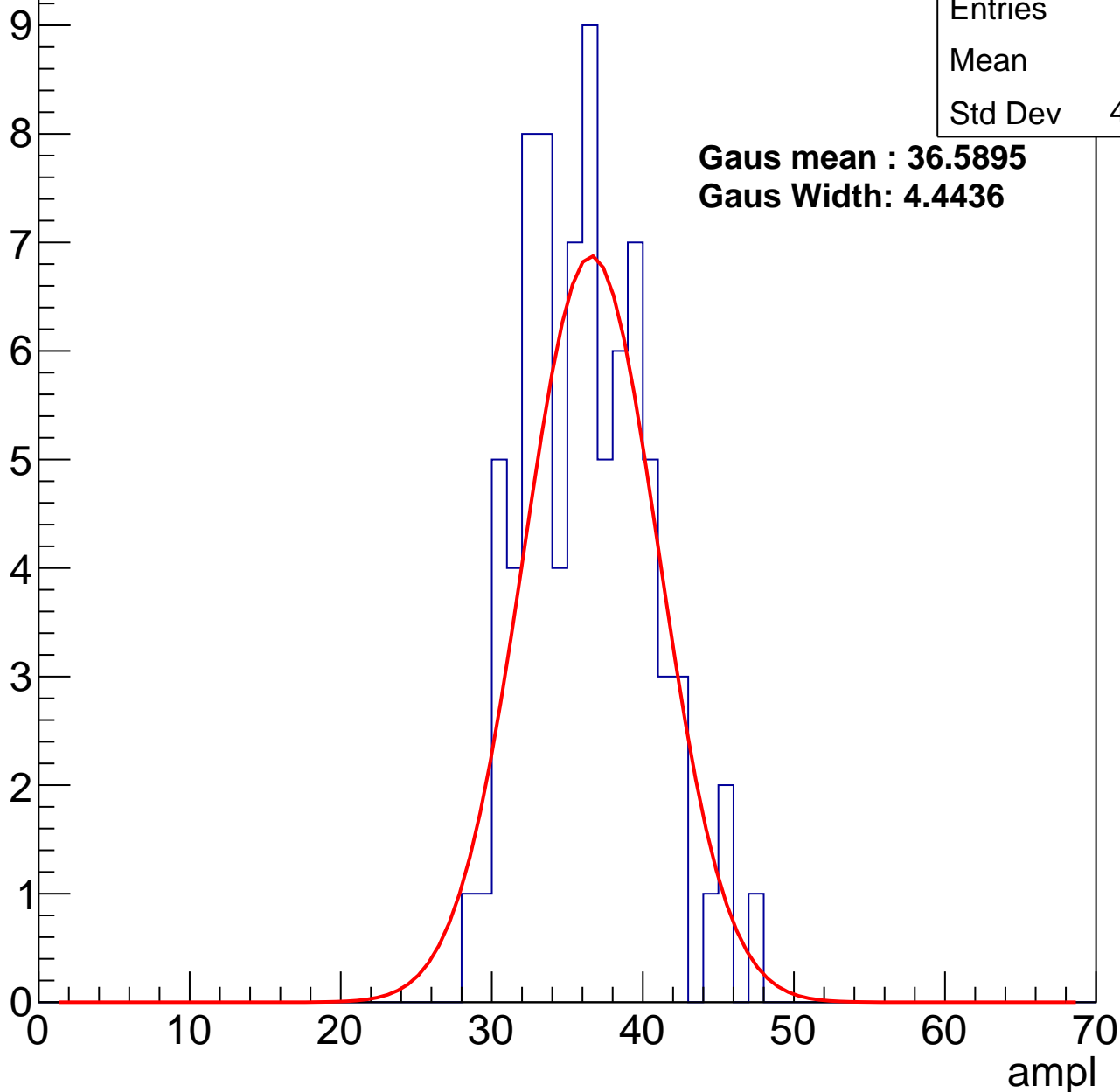
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	35.9
Std Dev	4.058

Gaus mean : 36.5895

Gaus Width: 4.4436



B1L103S, U21-ch101, adc2

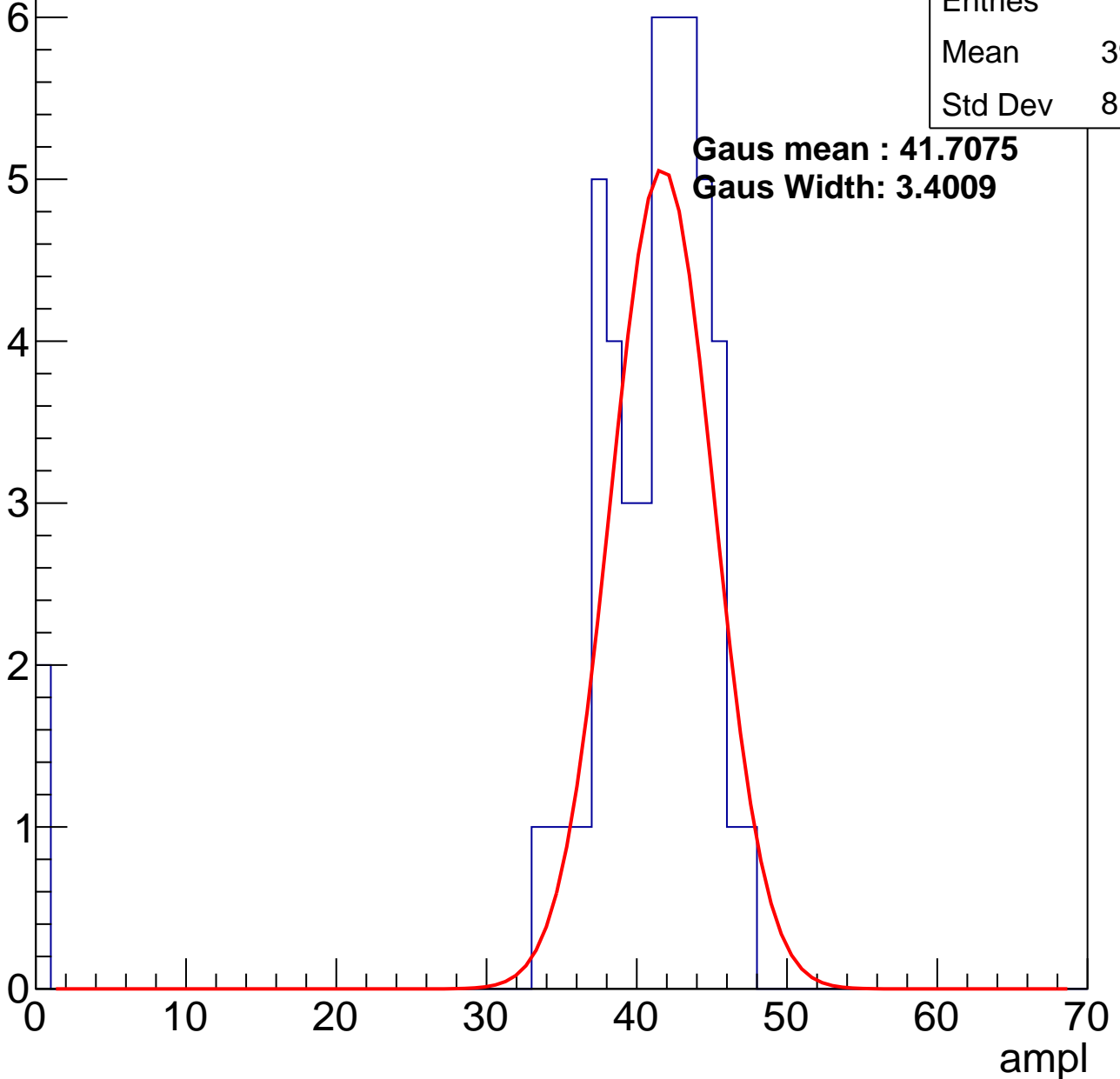
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	39.22
Std Dev	8.612

Gaus mean : 41.7075

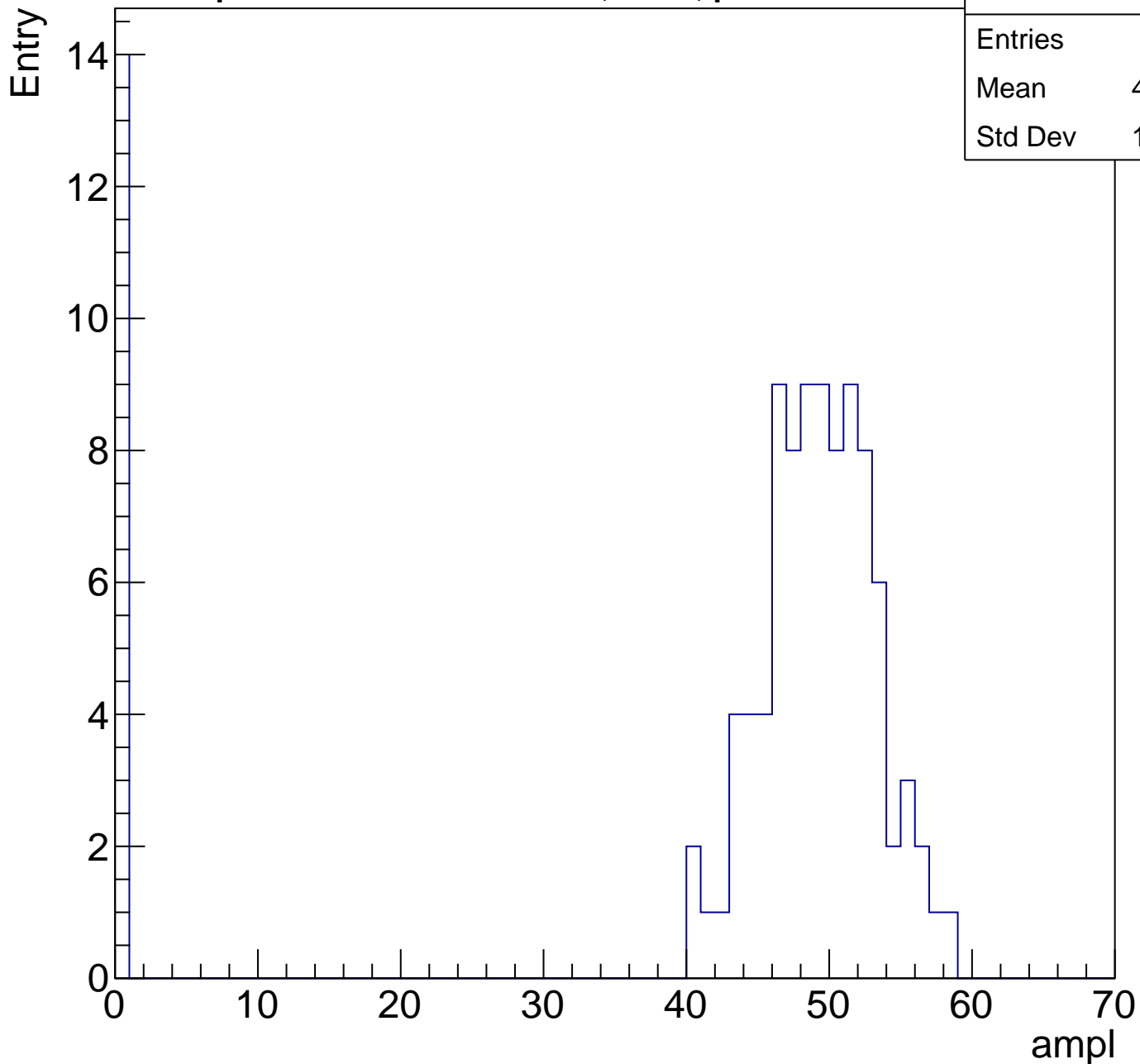
Gaus Width: 3.4009



B1L103S, U21-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	42.35
Std Dev	16.99

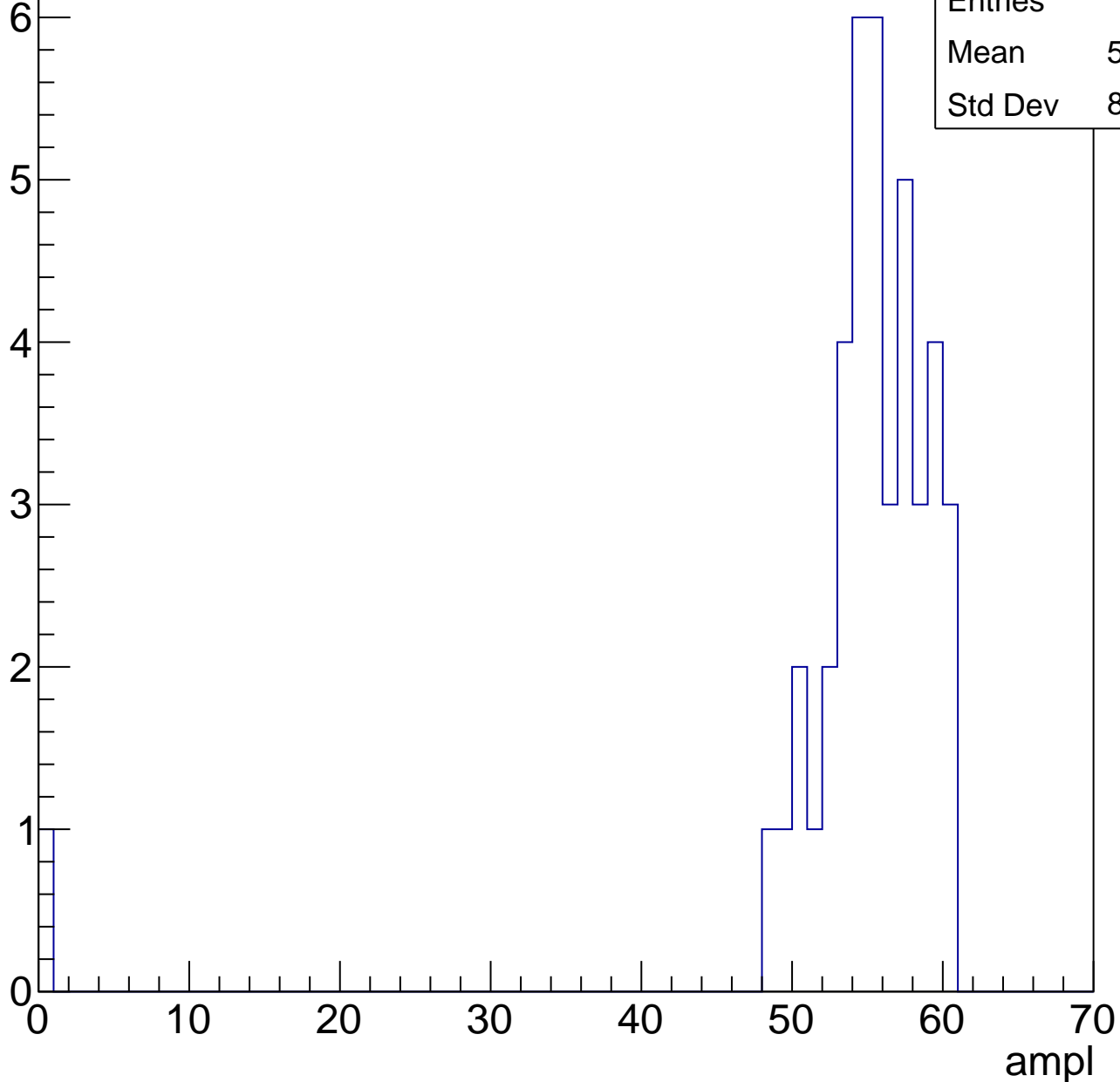


B1L103S, U21-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	53.83
Std Dev	8.928

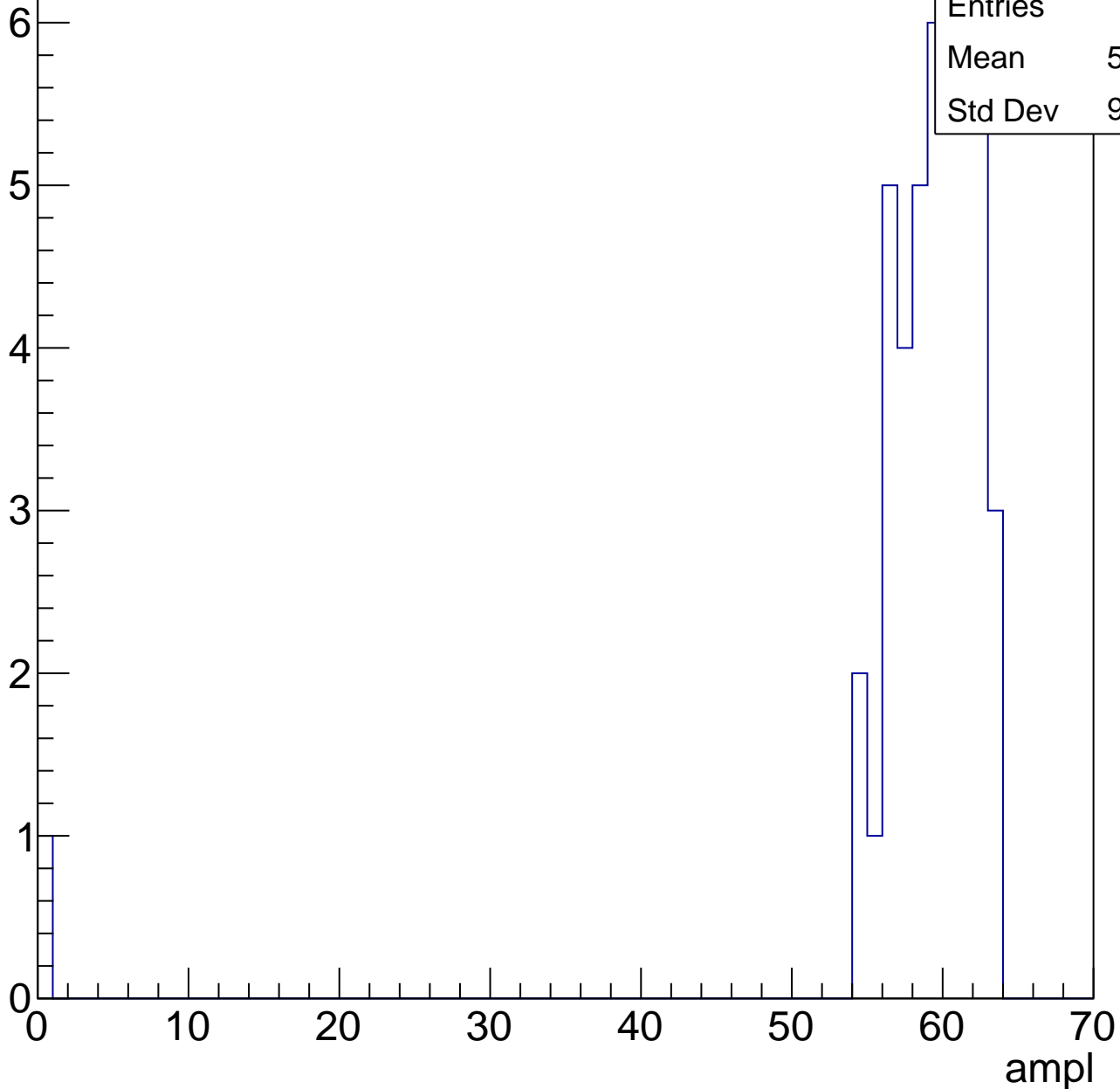


B1L103S, U21-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	57.82
Std Dev	9.046

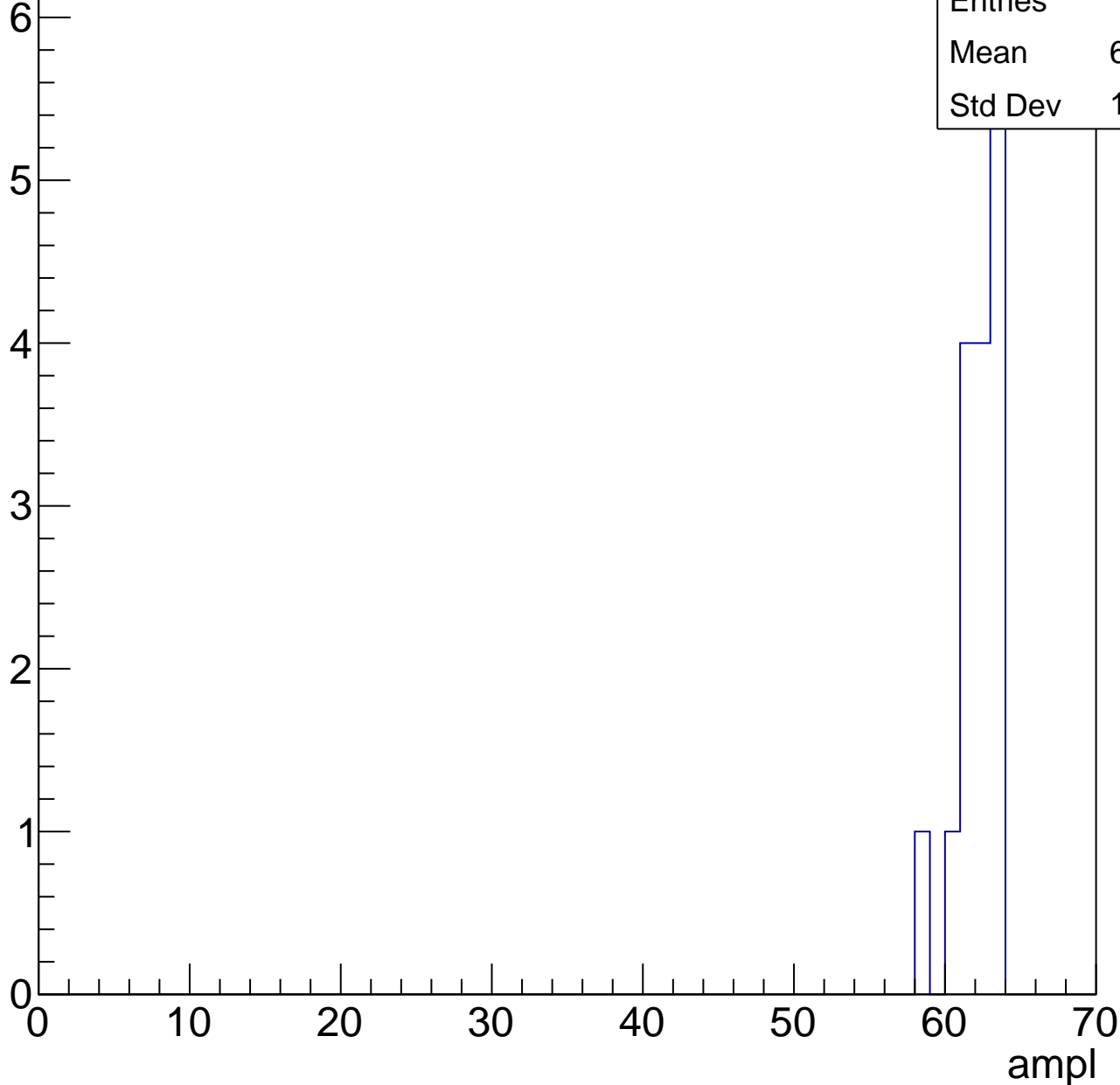


B1L103S, U21-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.75
Std Dev	1.346



B1L103S, U21-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U21-ch102, adc0

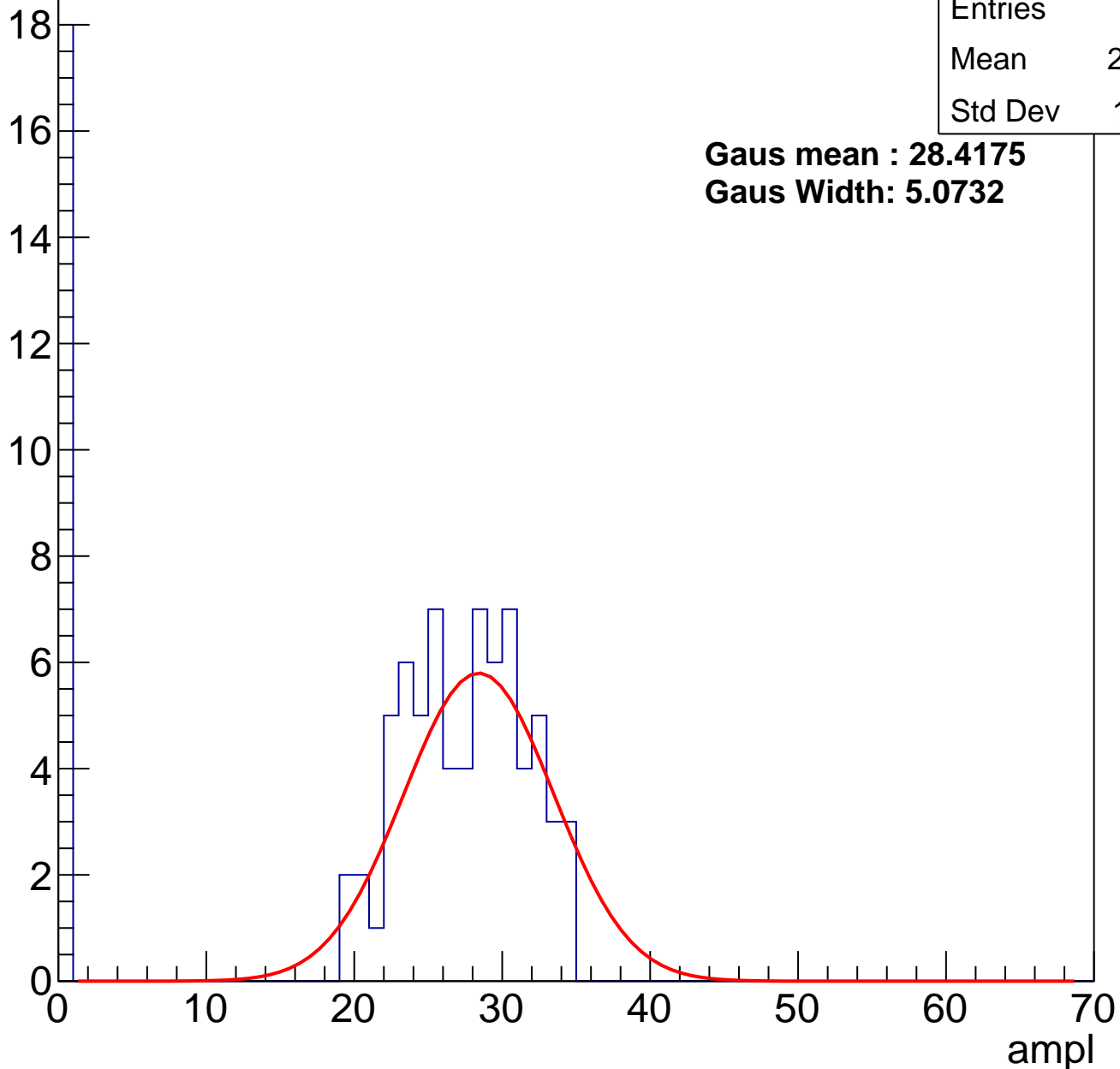
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	21.56
Std Dev	11.41

Gaus mean : 28.4175

Gaus Width: 5.0732

Entry



B1L103S, U21-ch102, adc1

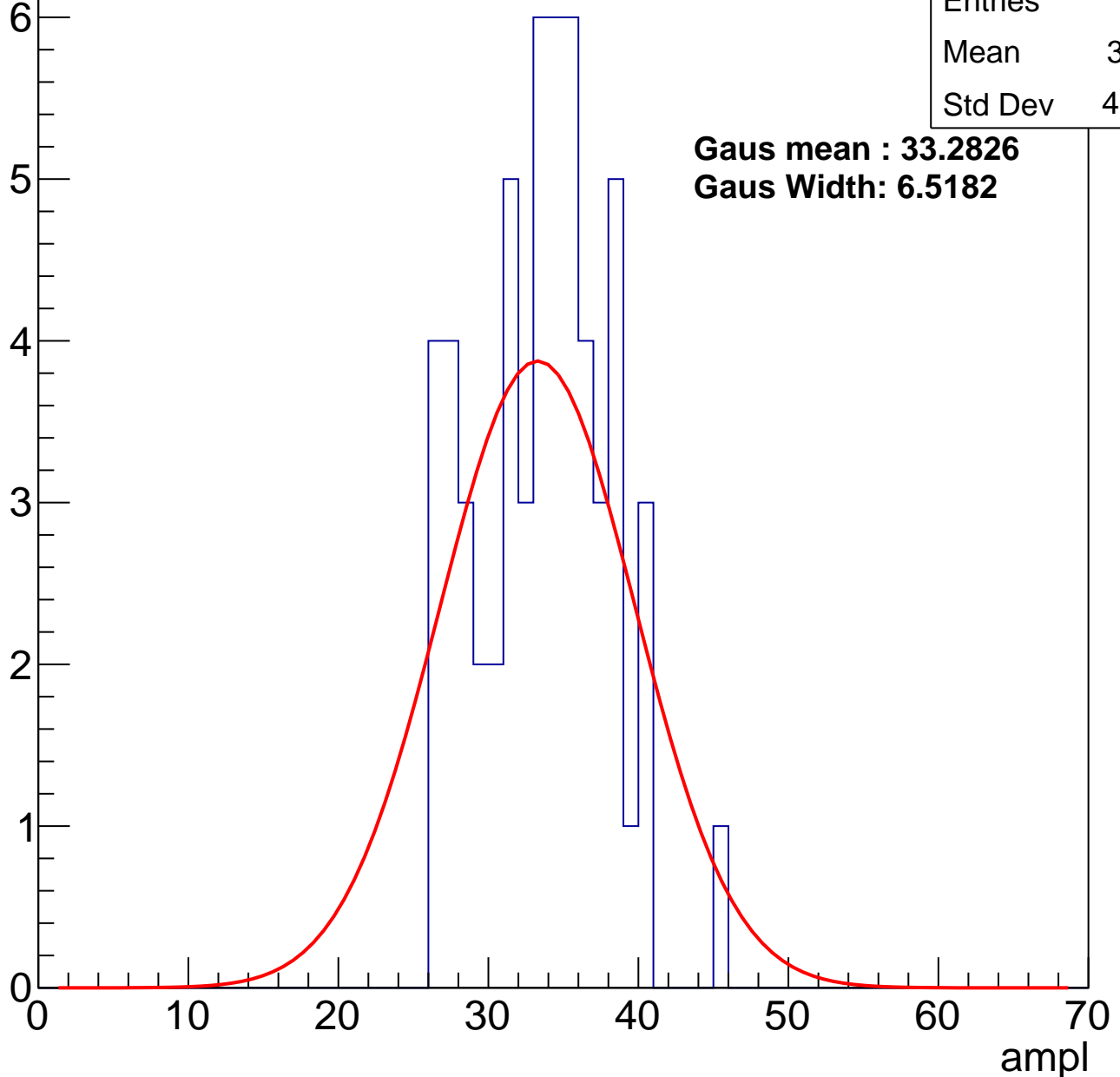
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	33.21
Std Dev	4.258

Gaus mean : 33.2826

Gaus Width: 6.5182



B1L103S, U21-ch102, adc2

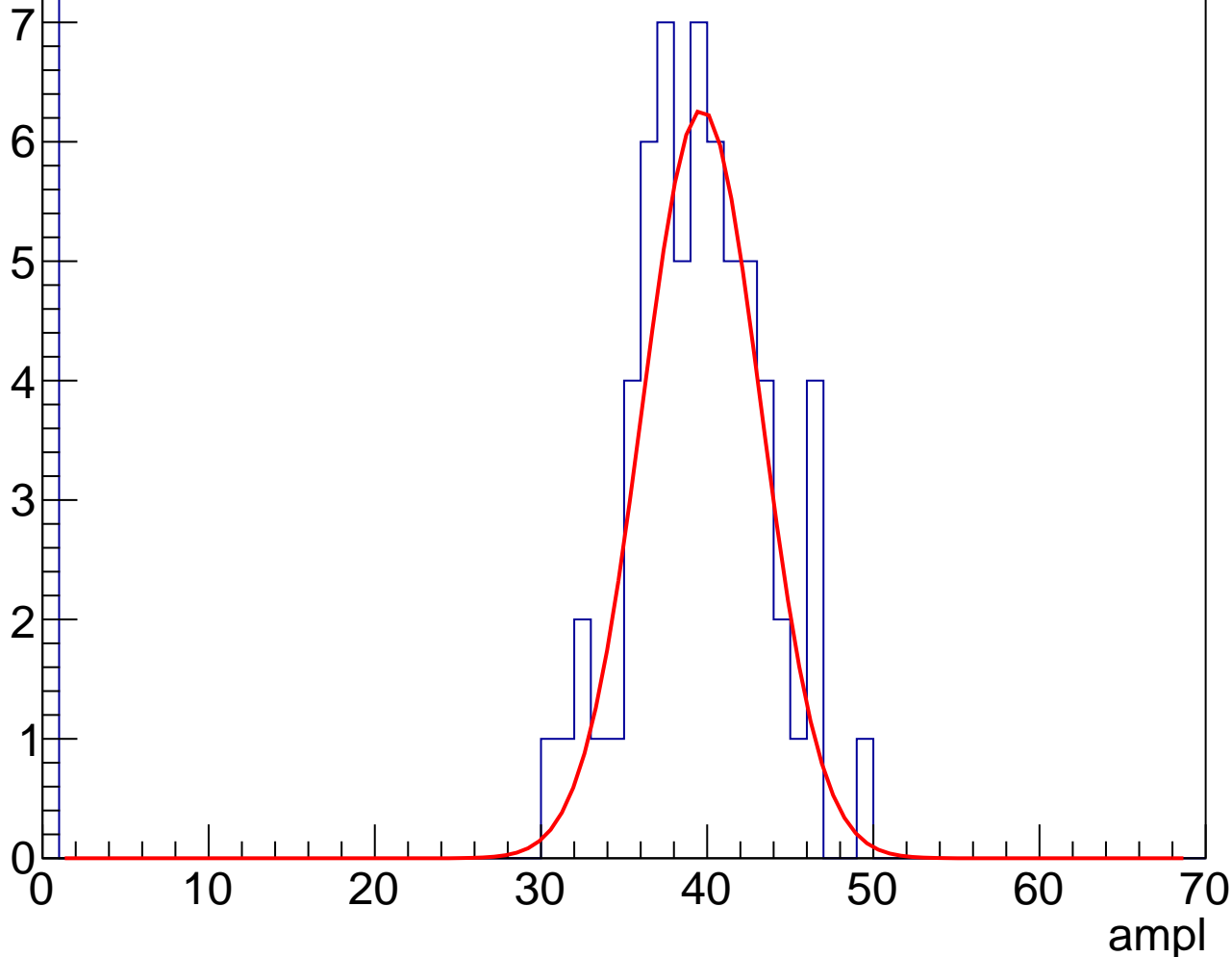
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	34.21
Std Dev	13.44

Gaus mean : 39.6702

Gaus Width: 3.5563

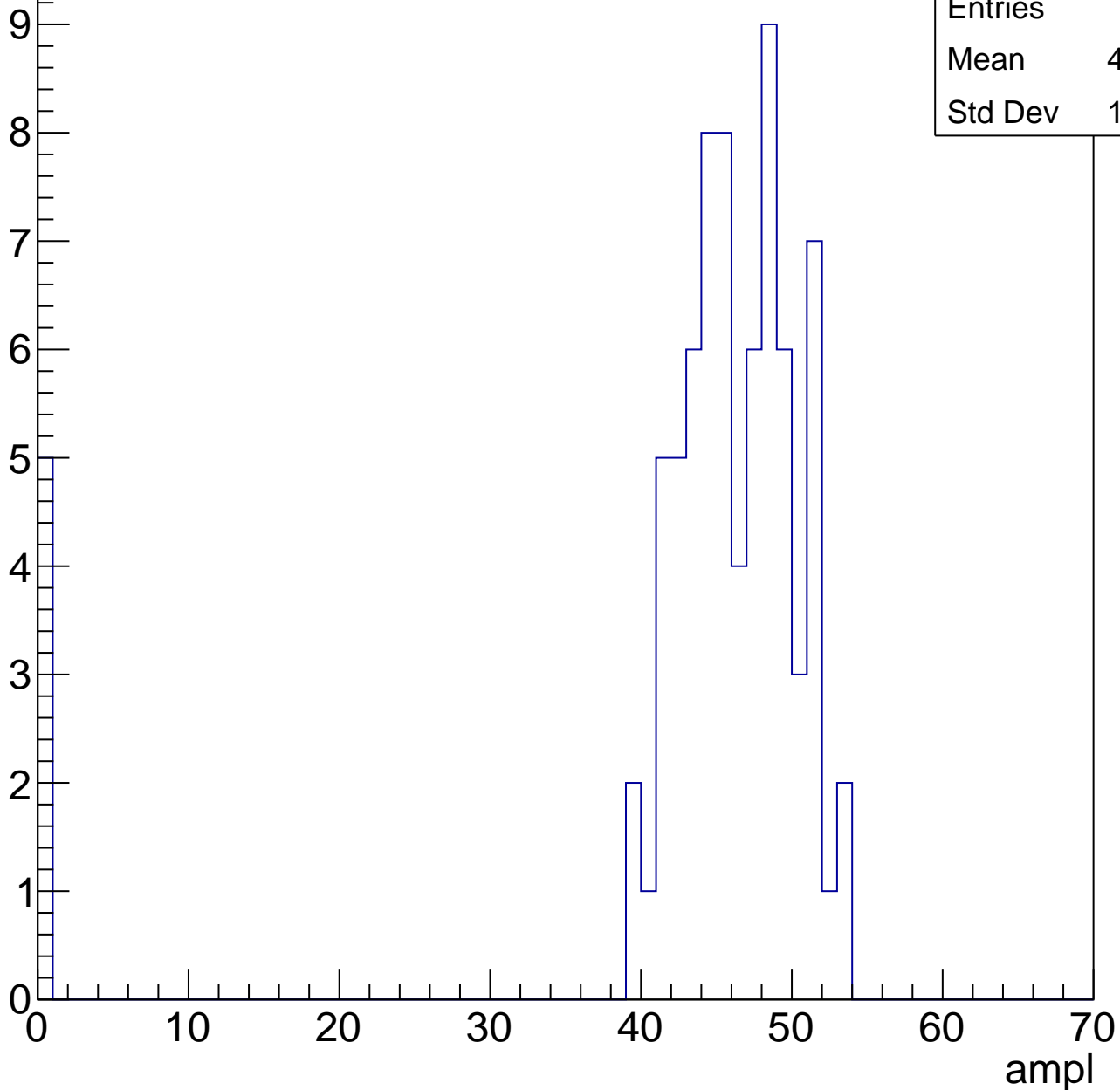


B1L103S, U21-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	43.08
Std Dev	11.77

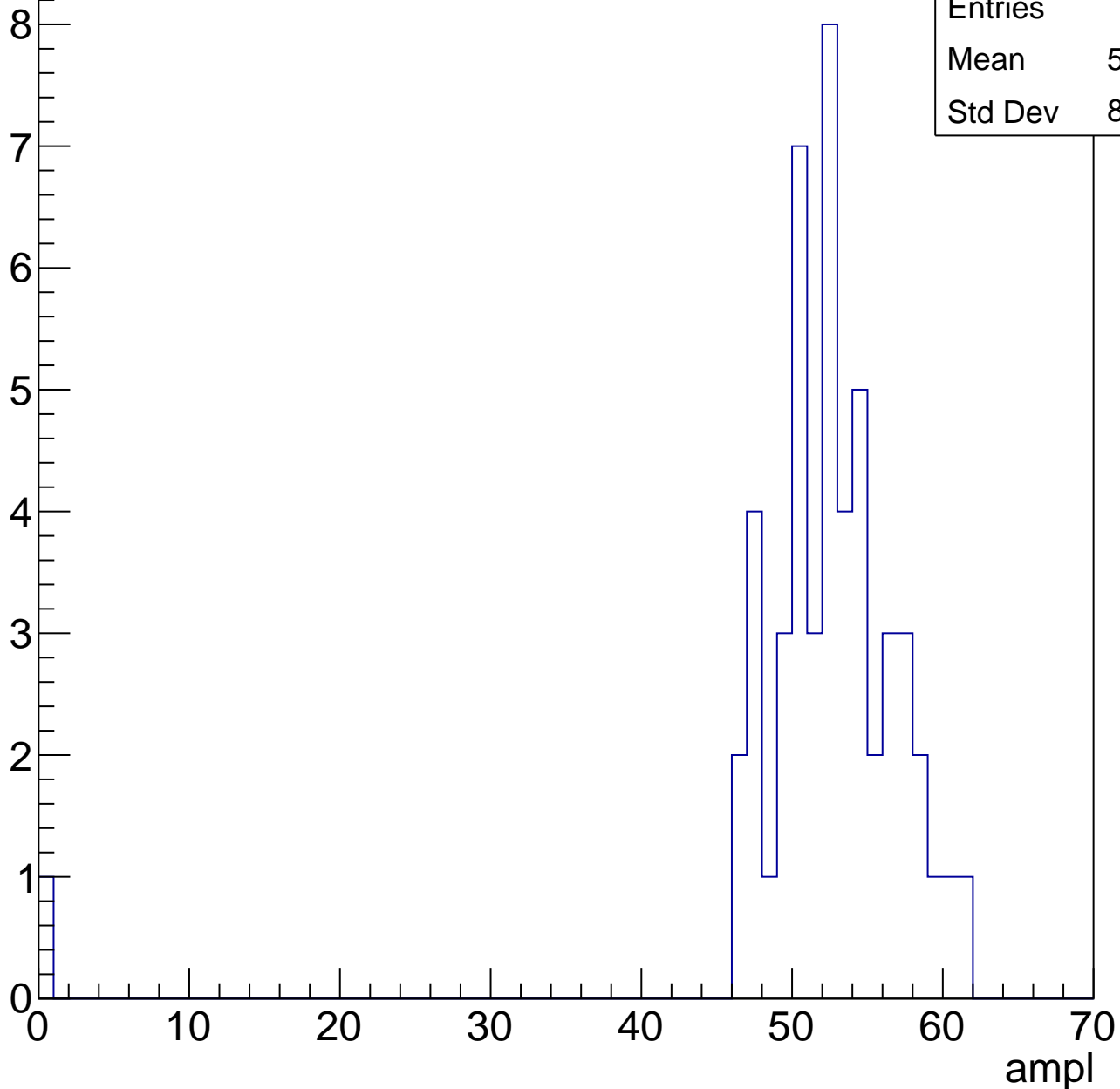


B1L103S, U21-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	51.39
Std Dev	8.119

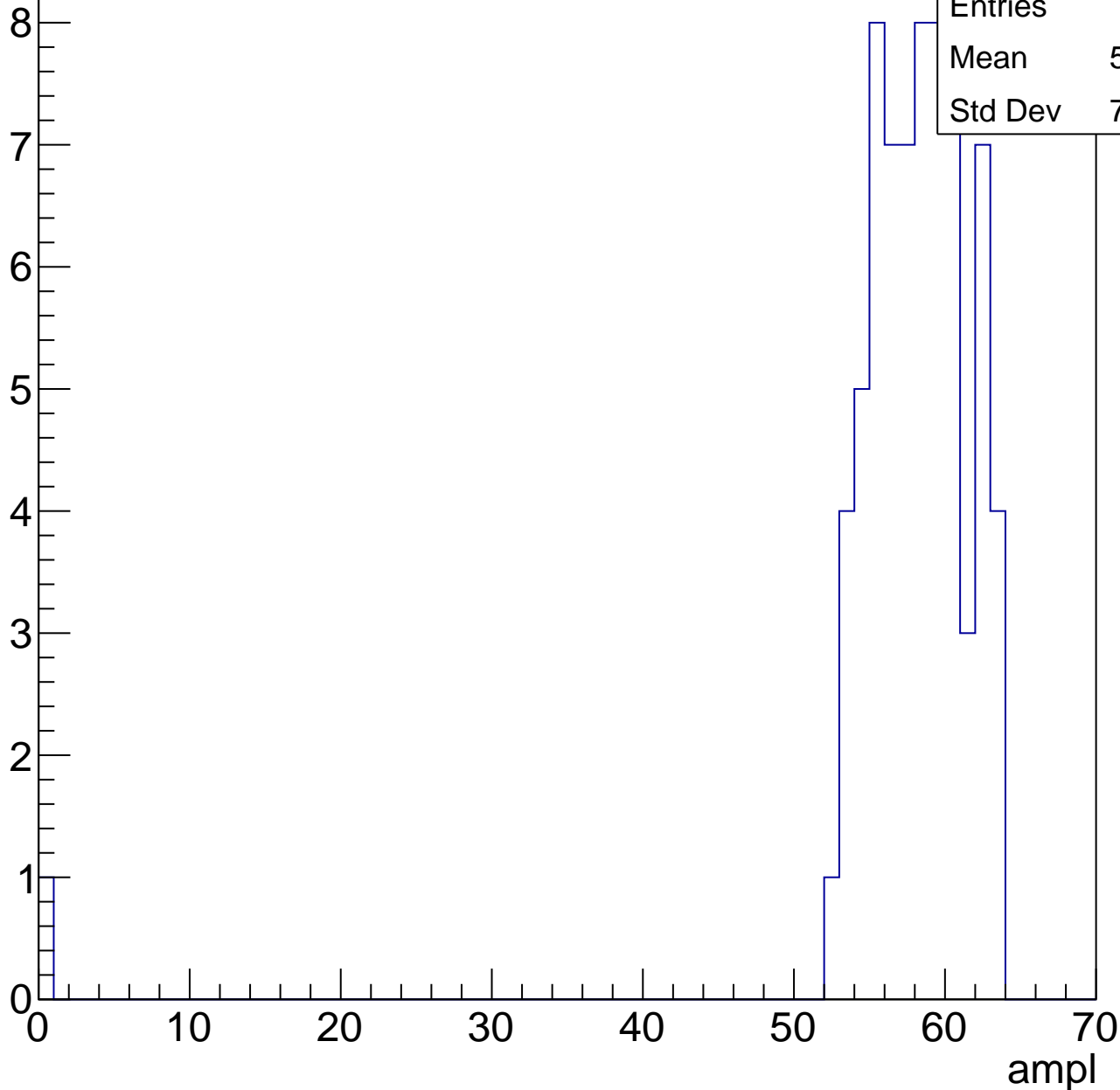


B1L103S, U21-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	57.04
Std Dev	7.412

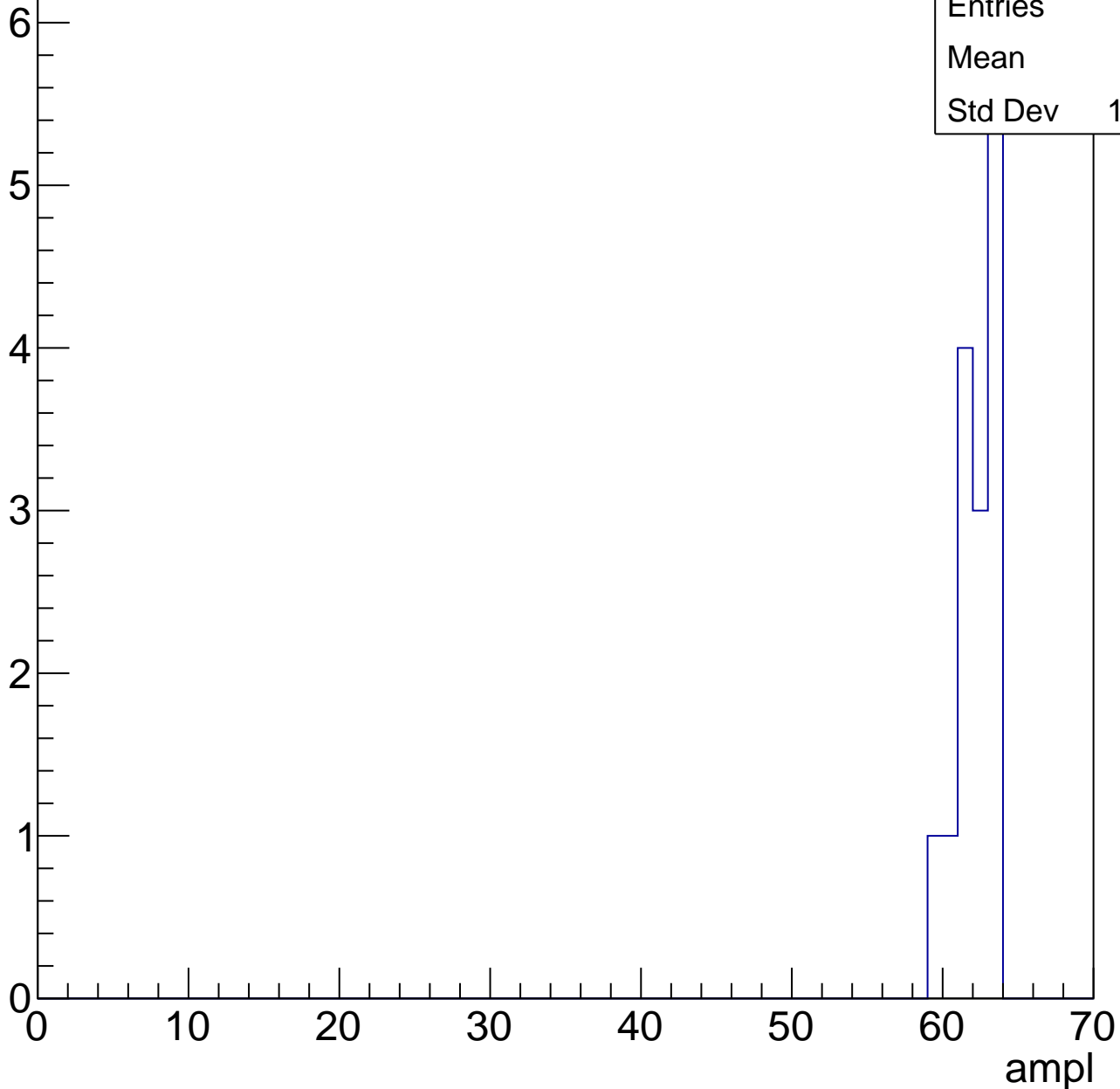


B1L103S, U21-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

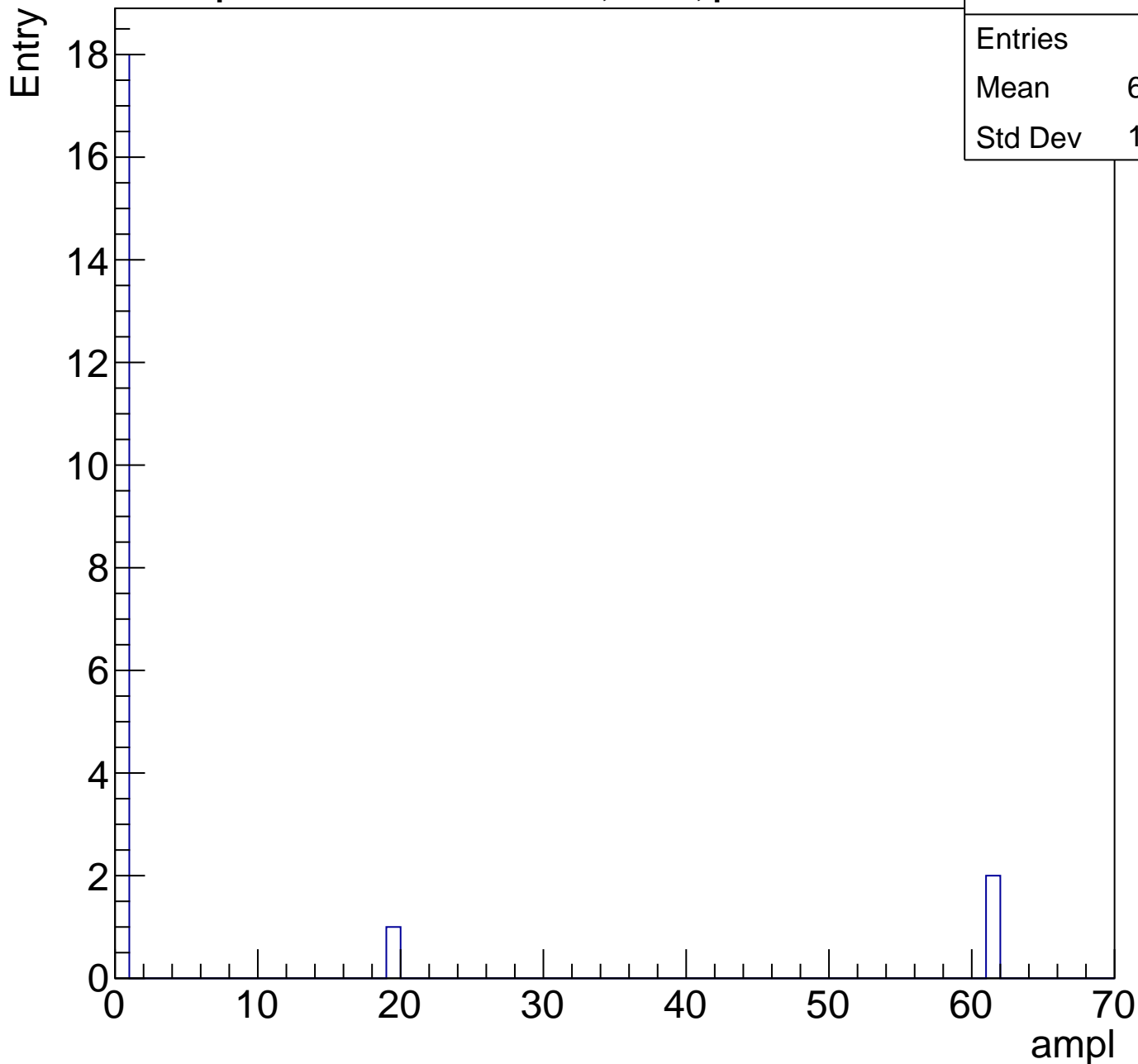
Entries	15
Mean	61.8
Std Dev	1.222



B1L103S, U21-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.714
Std Dev	18.07



B1L103S, U21-ch103, adc0

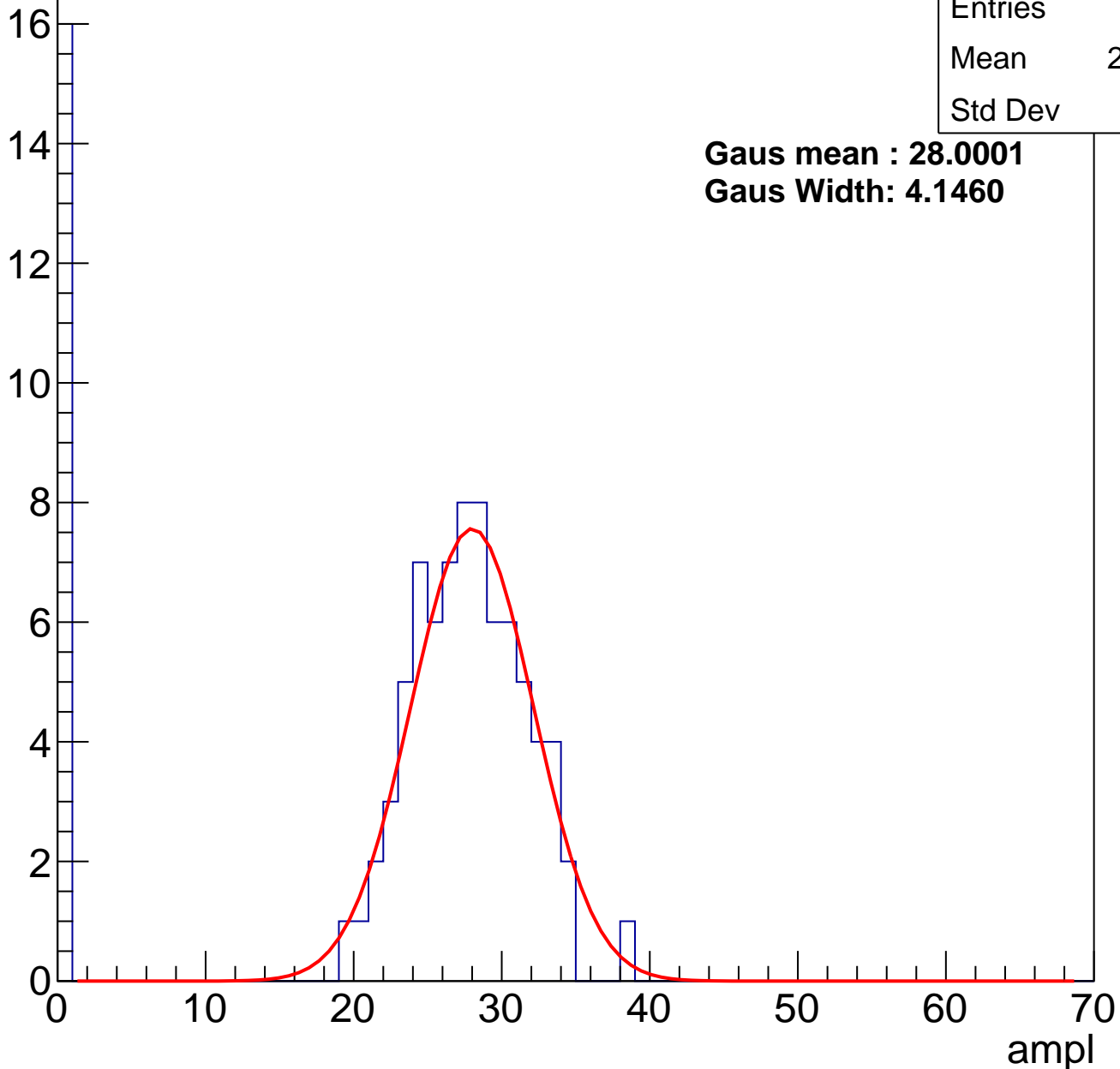
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	22.58
Std Dev	10.9

Gaus mean : 28.0001

Gaus Width: 4.1460

Entry



B1L103S, U21-ch103, adc1

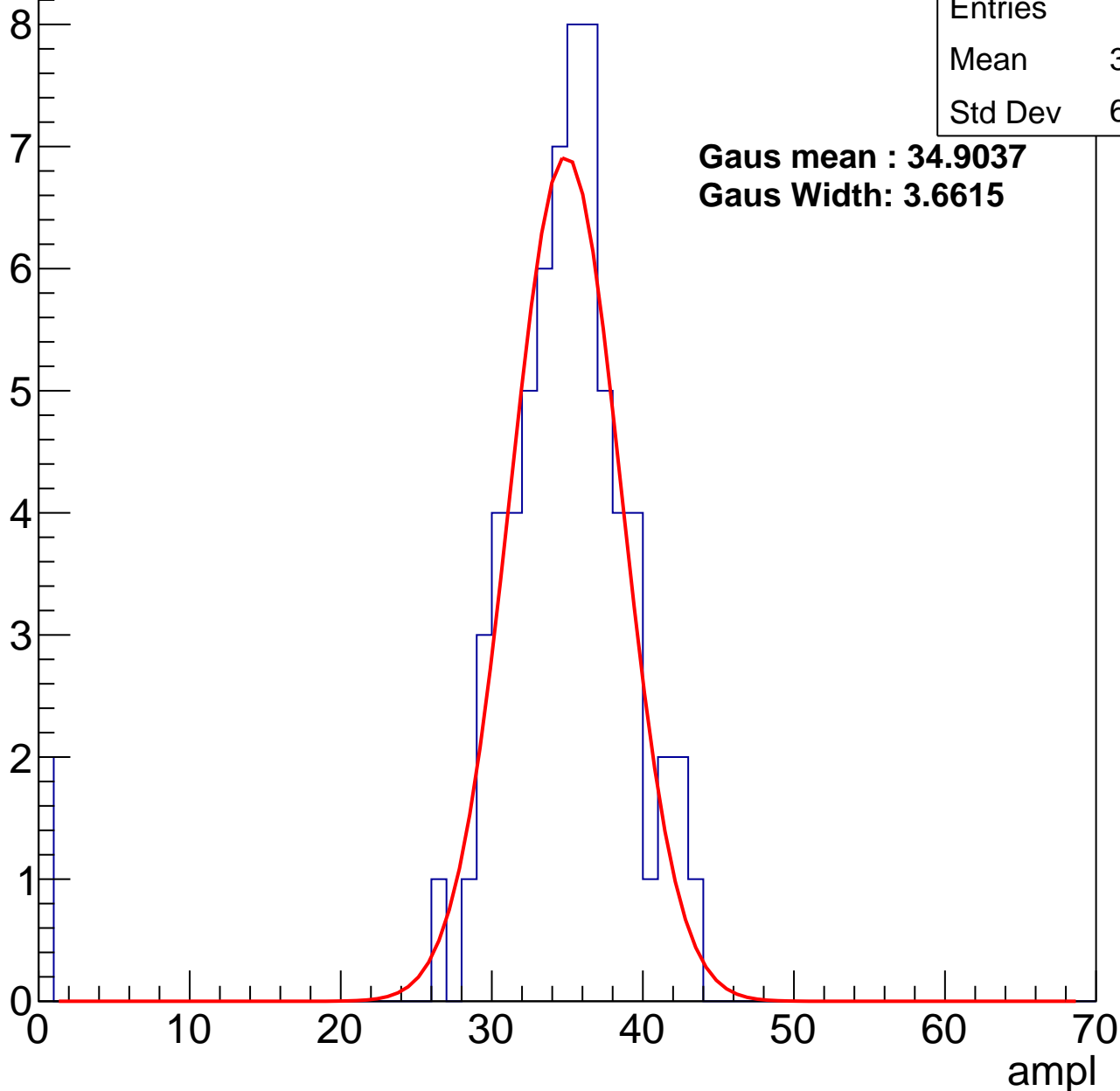
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	33.69
Std Dev	6.858

Gaus mean : 34.9037

Gaus Width: 3.6615



B1L103S, U21-ch103, adc2

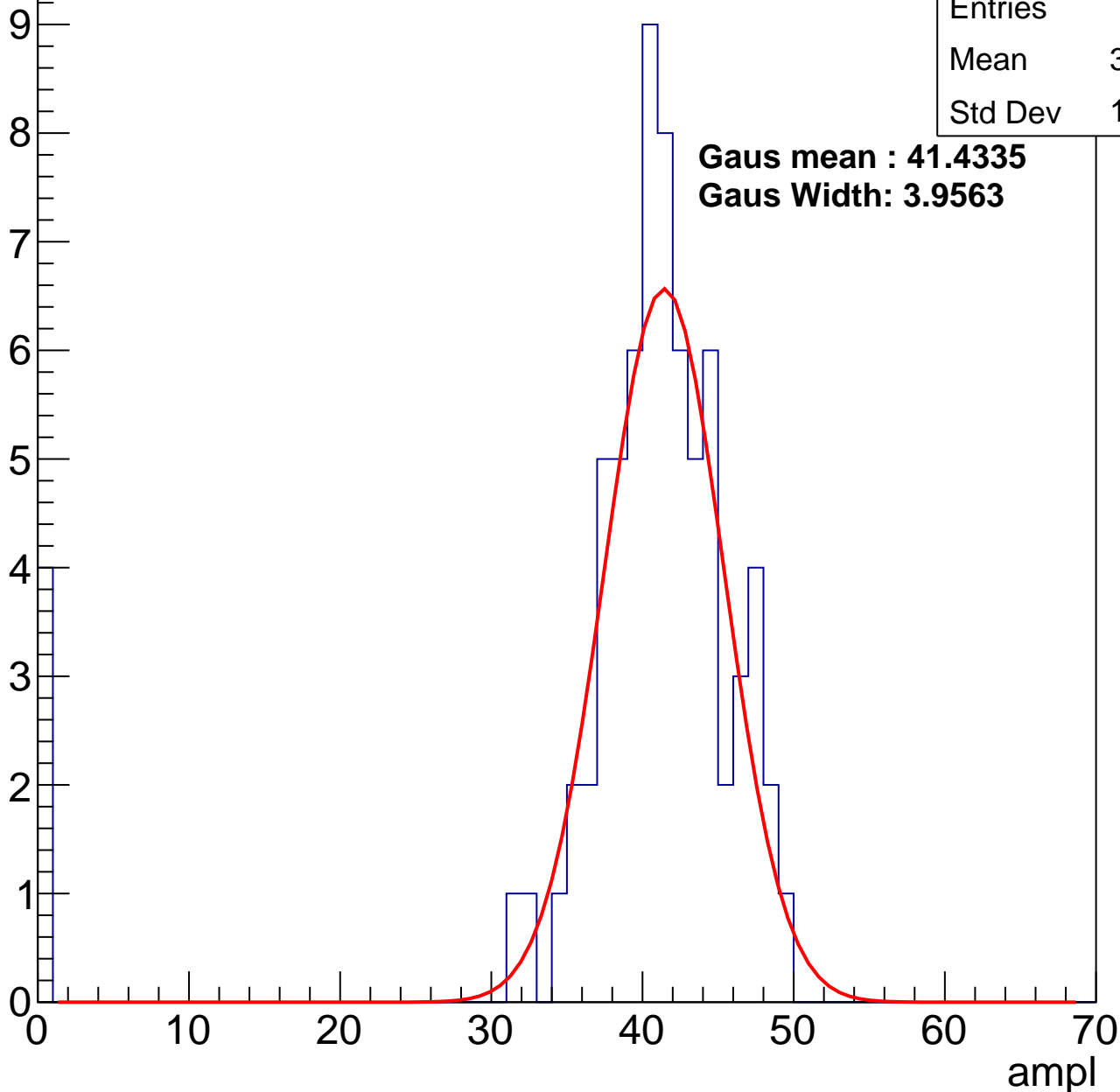
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	38.74
Std Dev	10.04

Gaus mean : 41.4335

Gaus Width: 3.9563

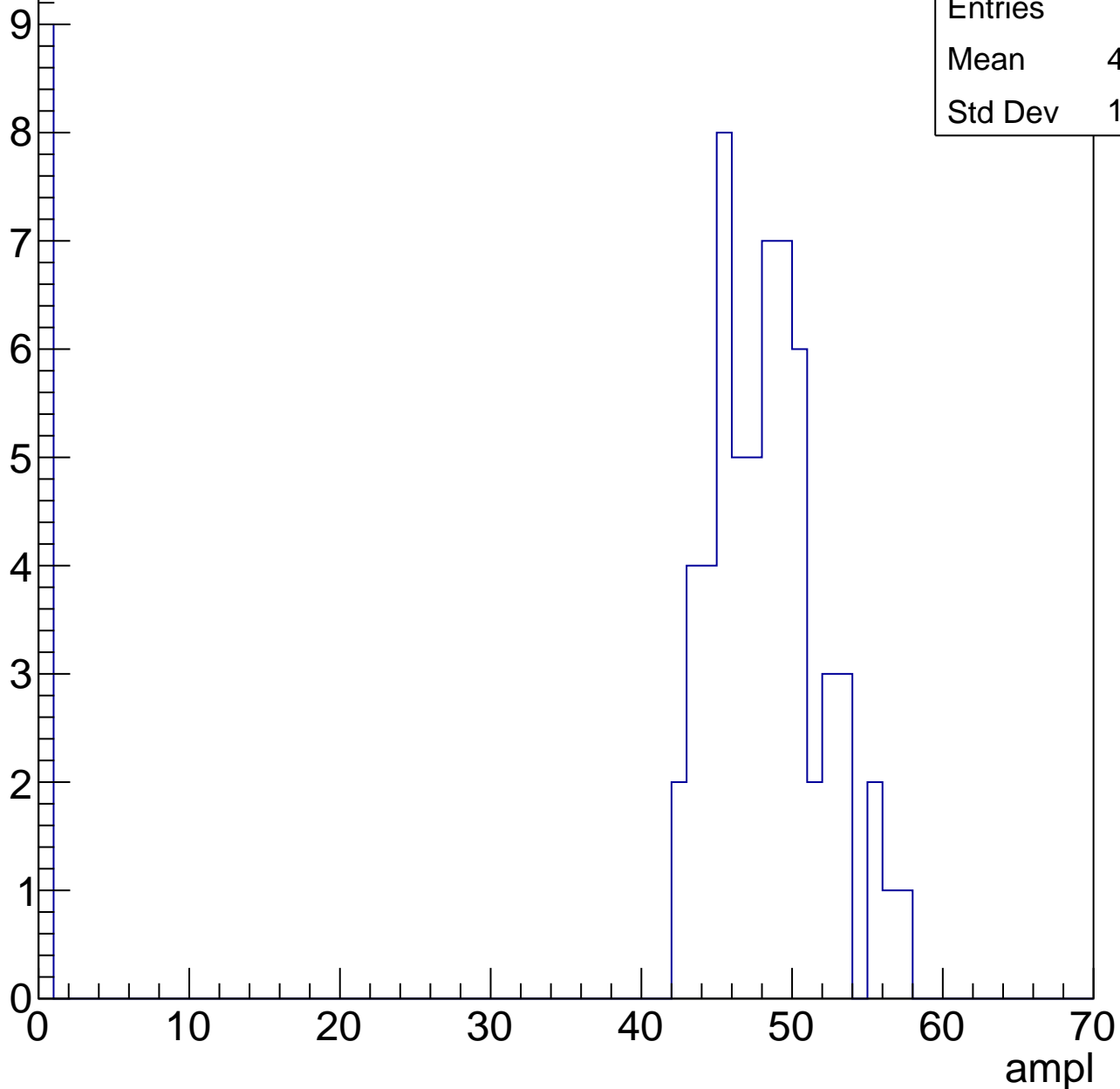


B1L103S, U21-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	41.68
Std Dev	16.48

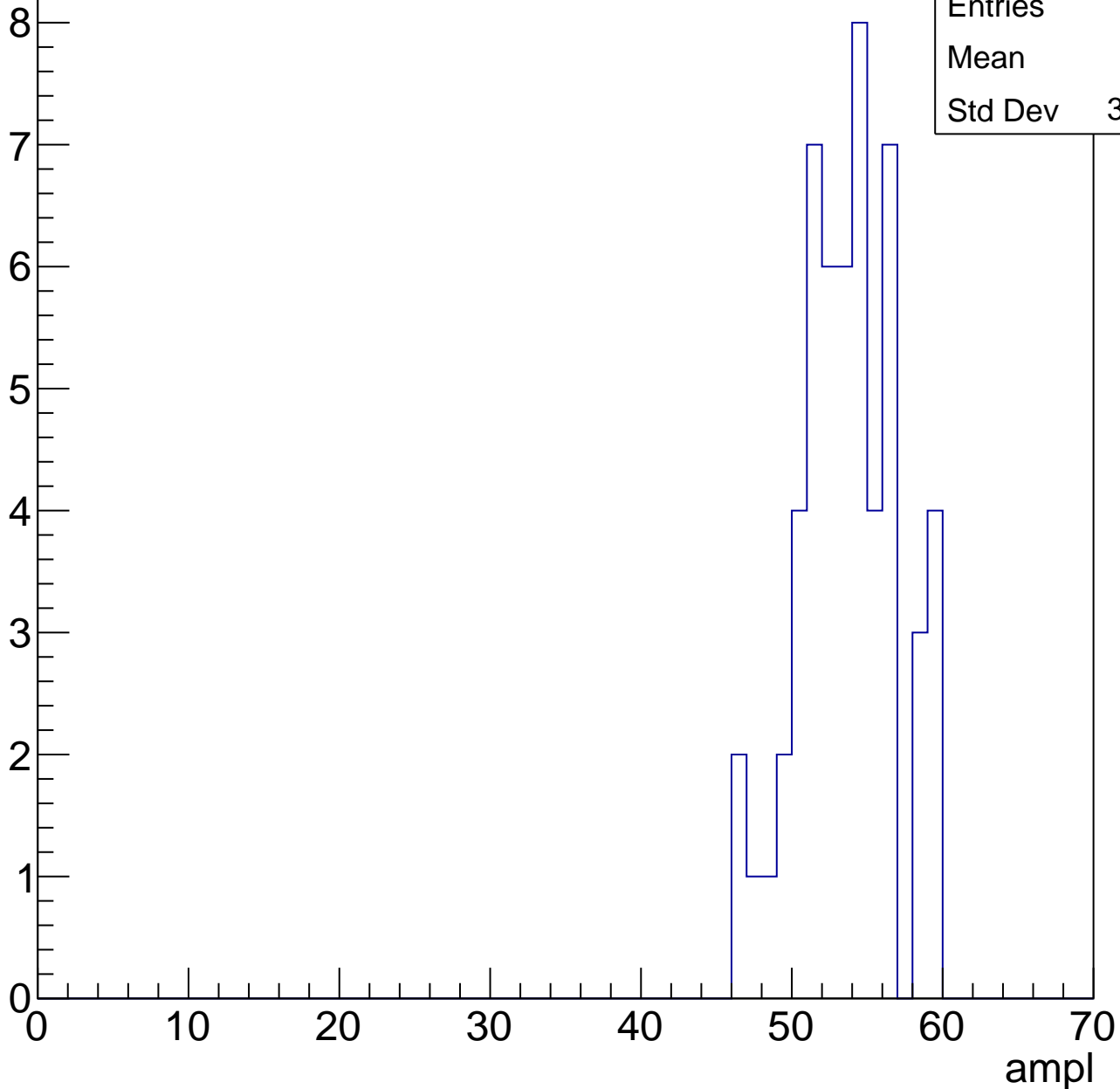


B1L103S, U21-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.2
Std Dev	3.205

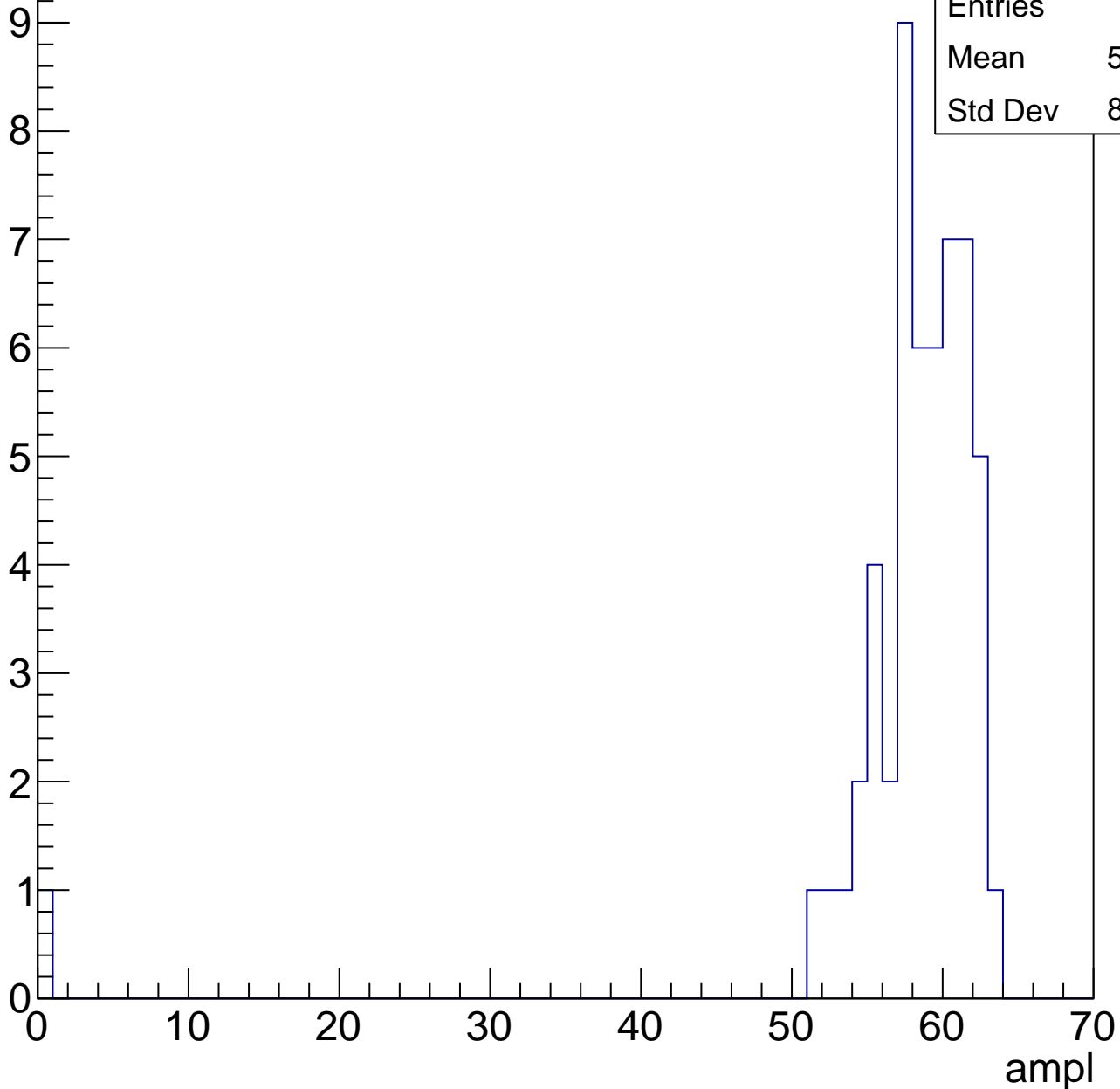


B1L103S, U21-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.19
Std Dev	8.385

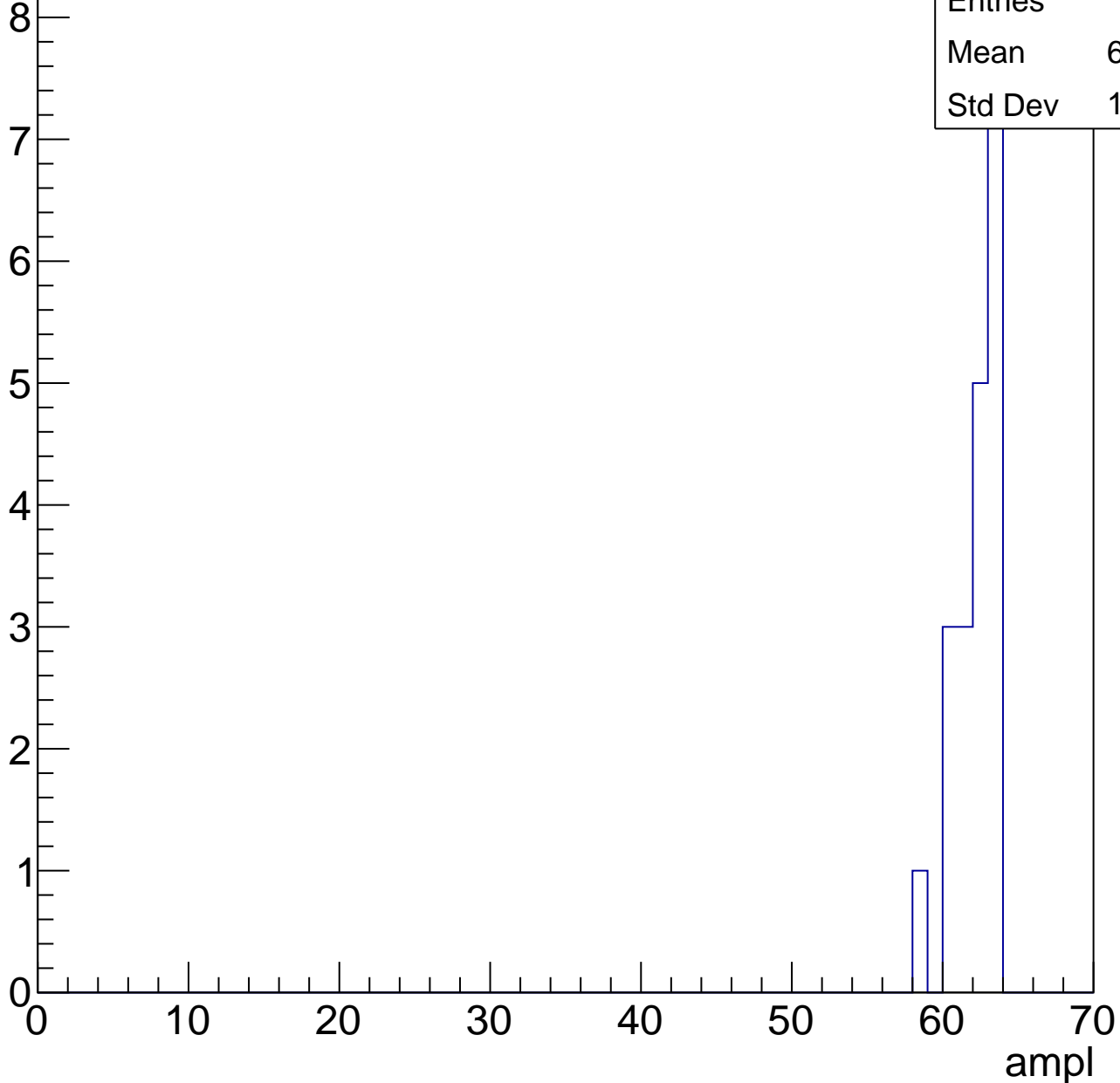


B1L103S, U21-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	61.75
Std Dev	1.374



B1L103S, U21-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U21-ch104, adc0

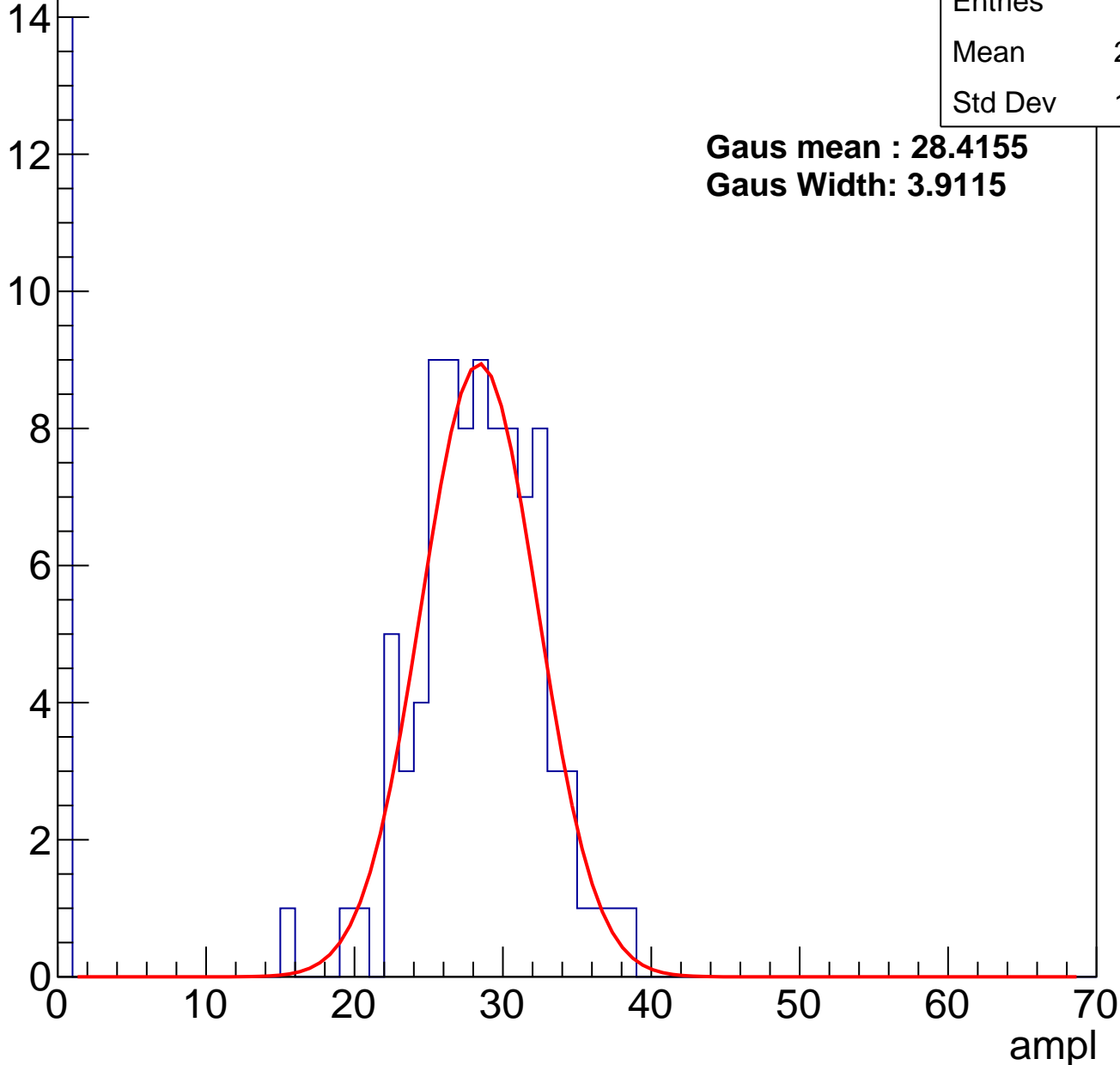
calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	24.27
Std Dev	10.23

Gaus mean : 28.4155

Gaus Width: 3.9115

Entry



B1L103S, U21-ch104, adc1

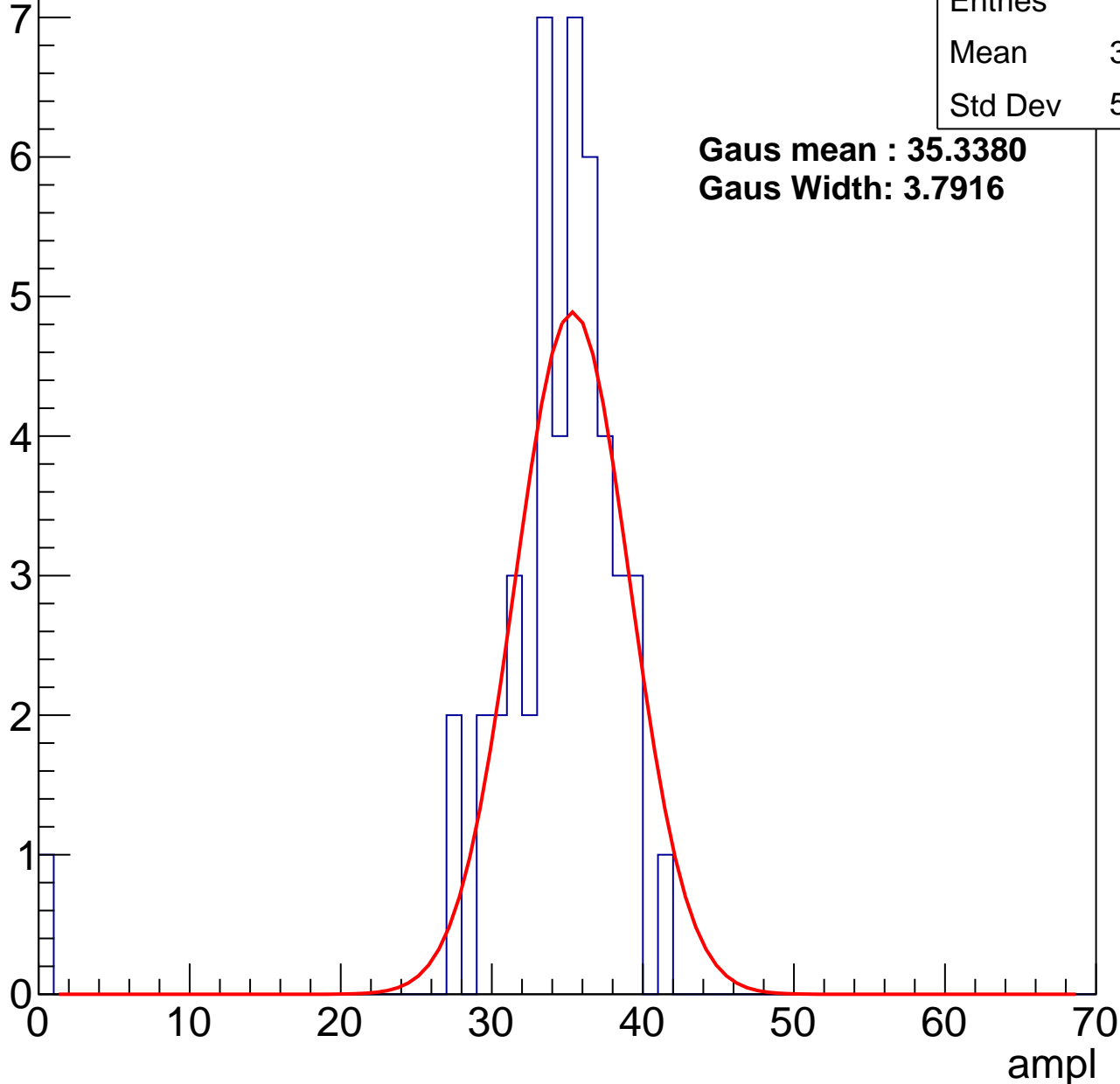
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	33.55
Std Dev	5.848

Gaus mean : 35.3380

Gaus Width: 3.7916



B1L103S, U21-ch104, adc2

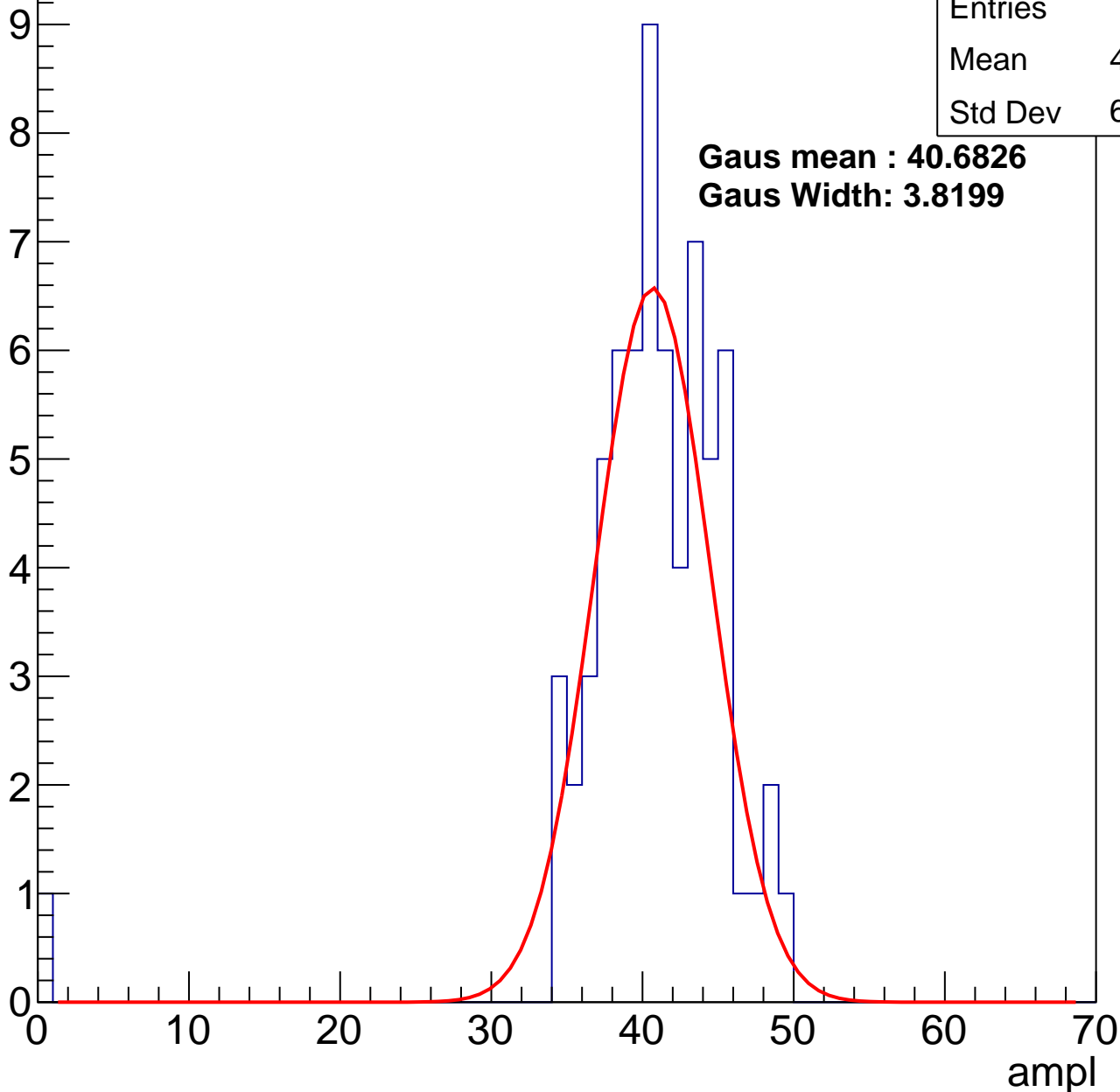
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.15
Std Dev	6.049

Gaus mean : 40.6826

Gaus Width: 3.8199

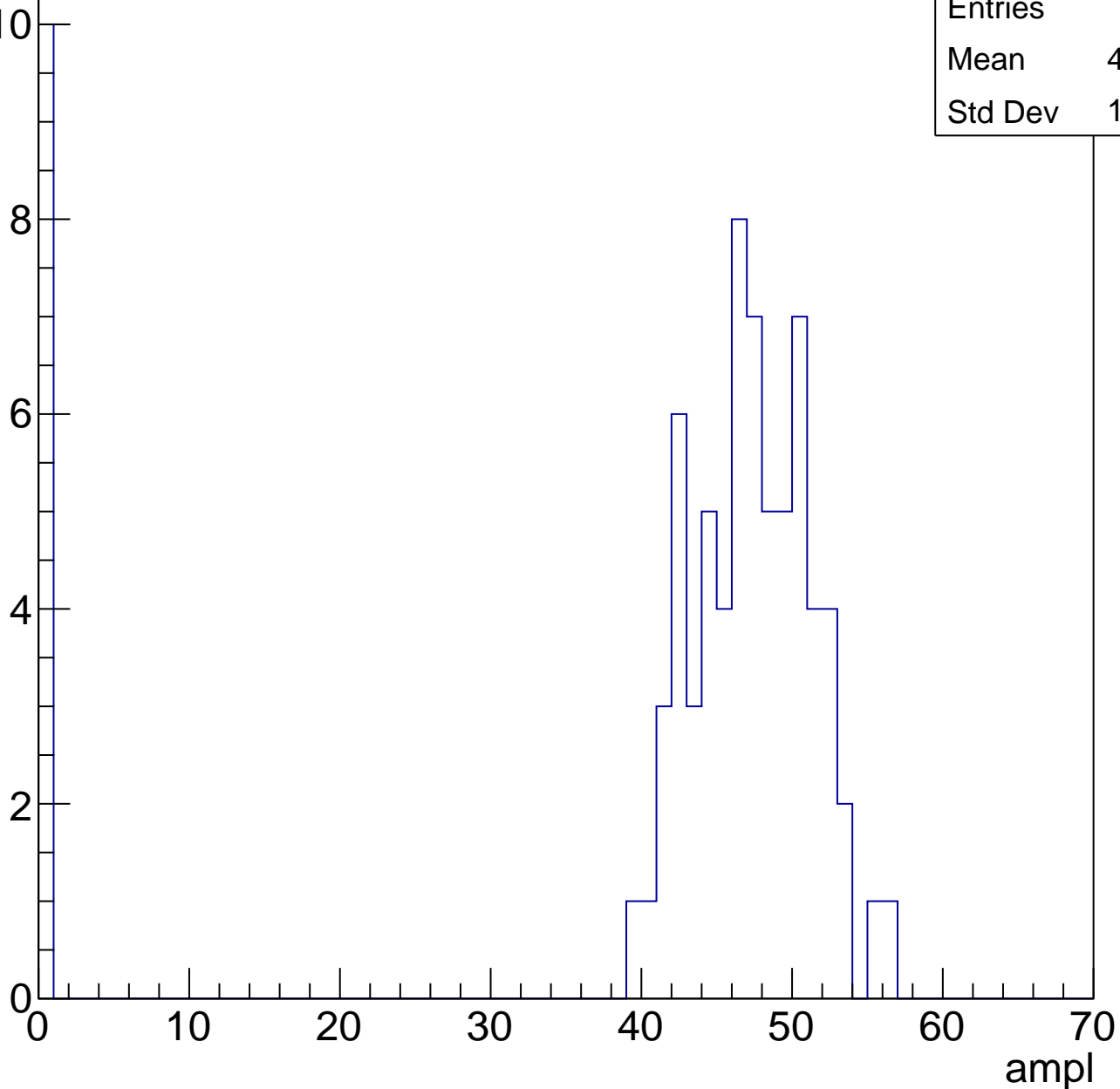


B1L103S, U21-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	40.83
Std Dev	16.16

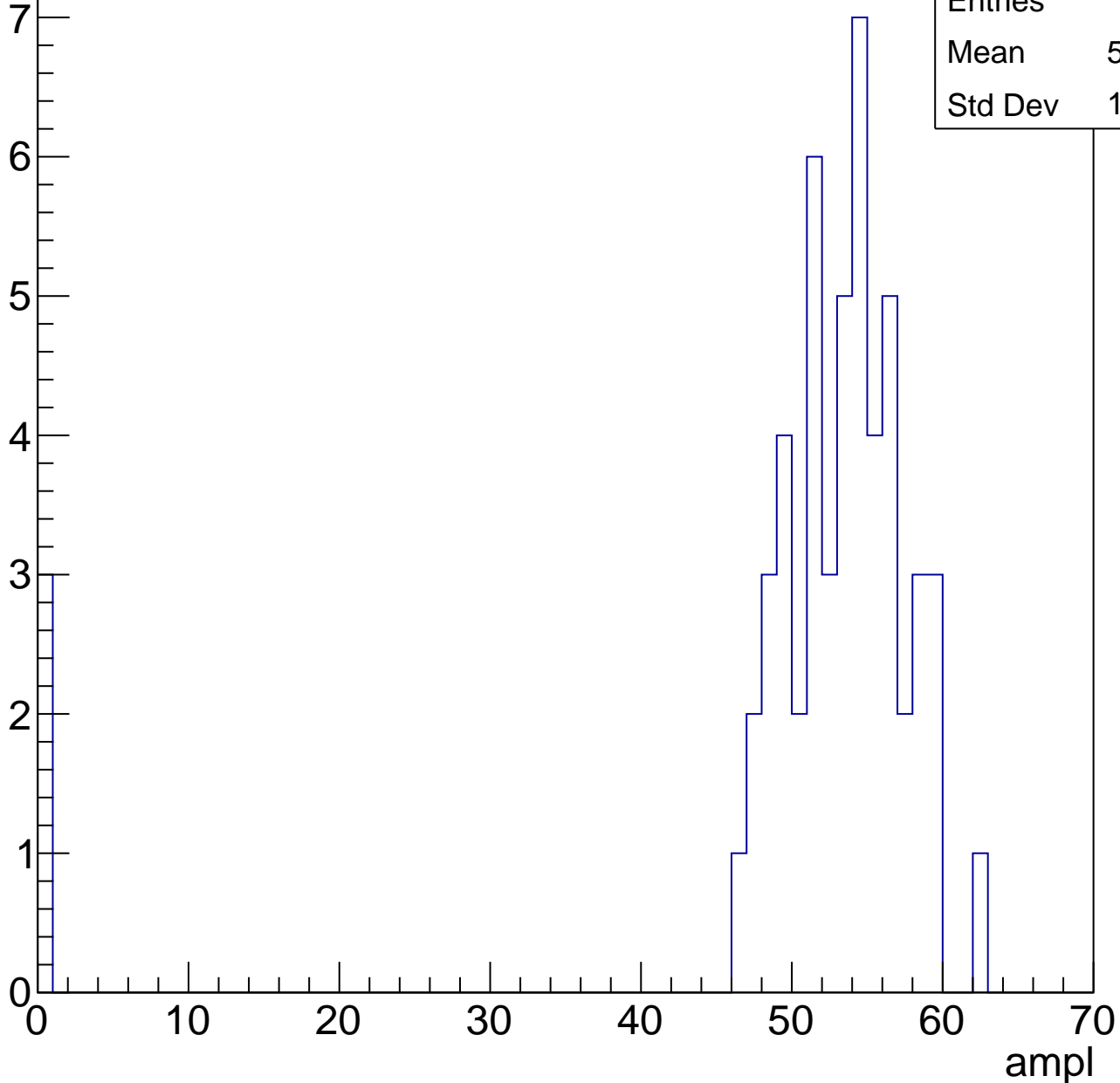


B1L103S, U21-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	50.22
Std Dev	12.68

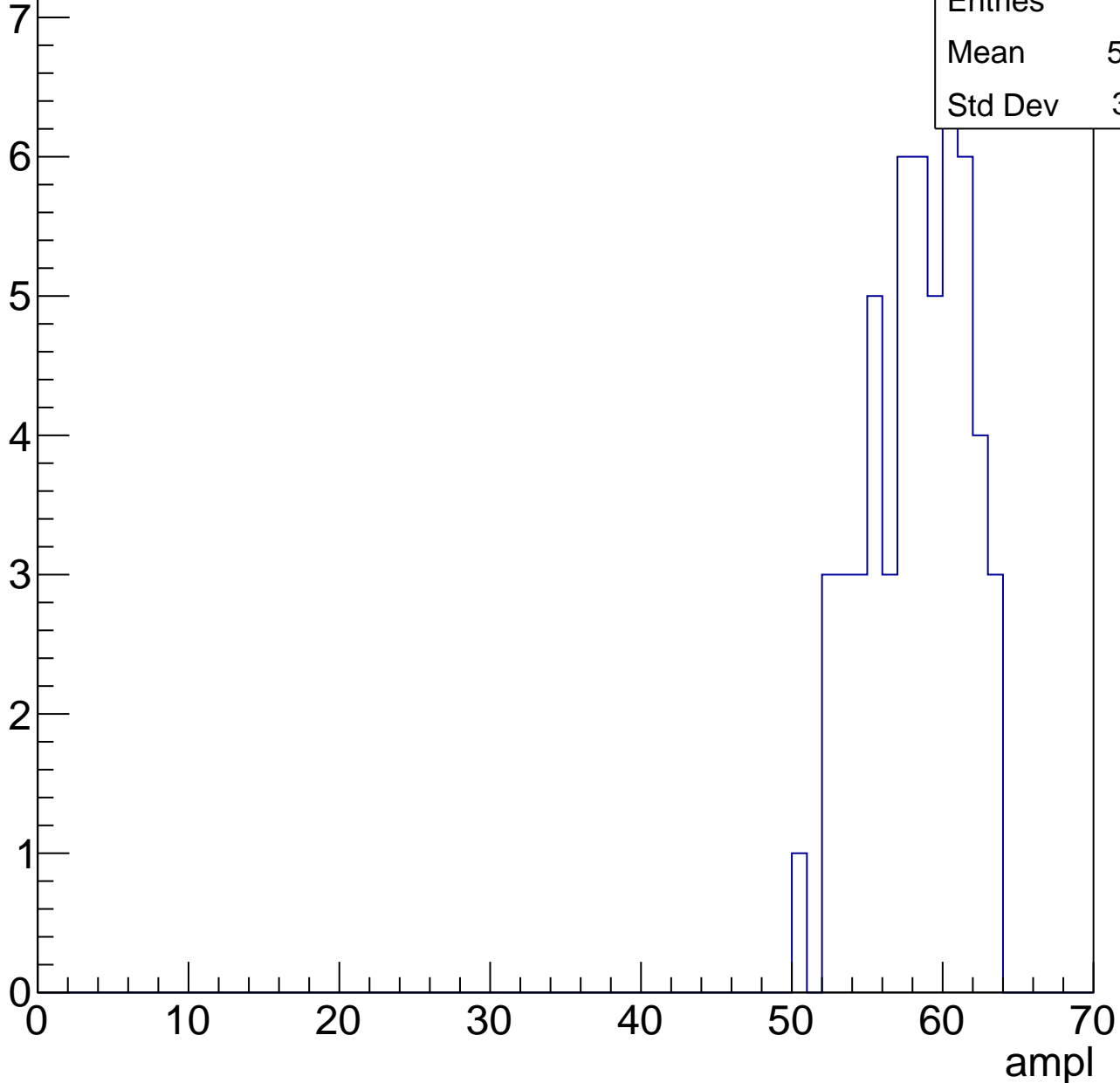


B1L103S, U21-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.78
Std Dev	3.251

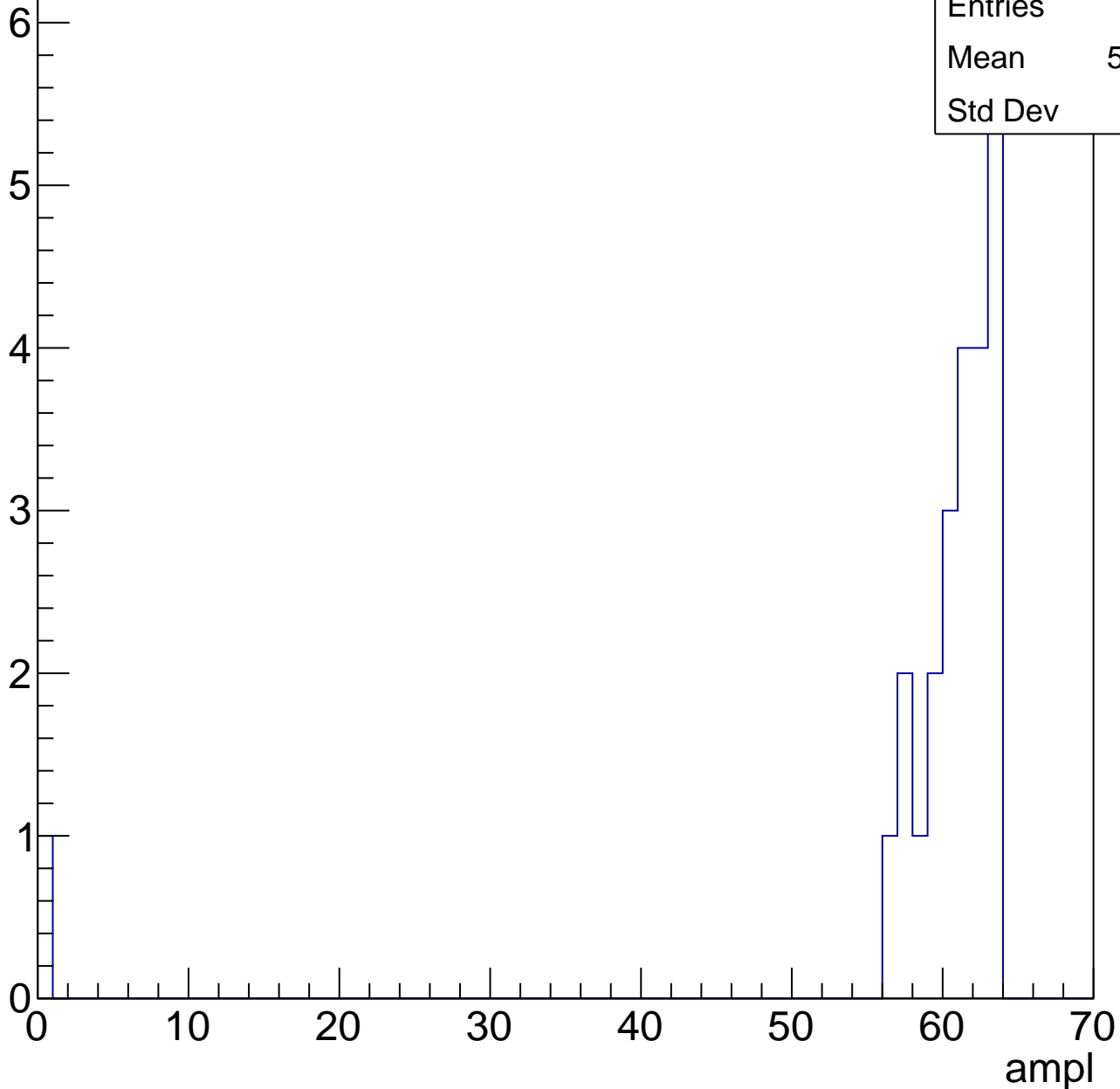


B1L103S, U21-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.17
Std Dev	12.3



B1L103S, U21-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U21-ch105, adc0

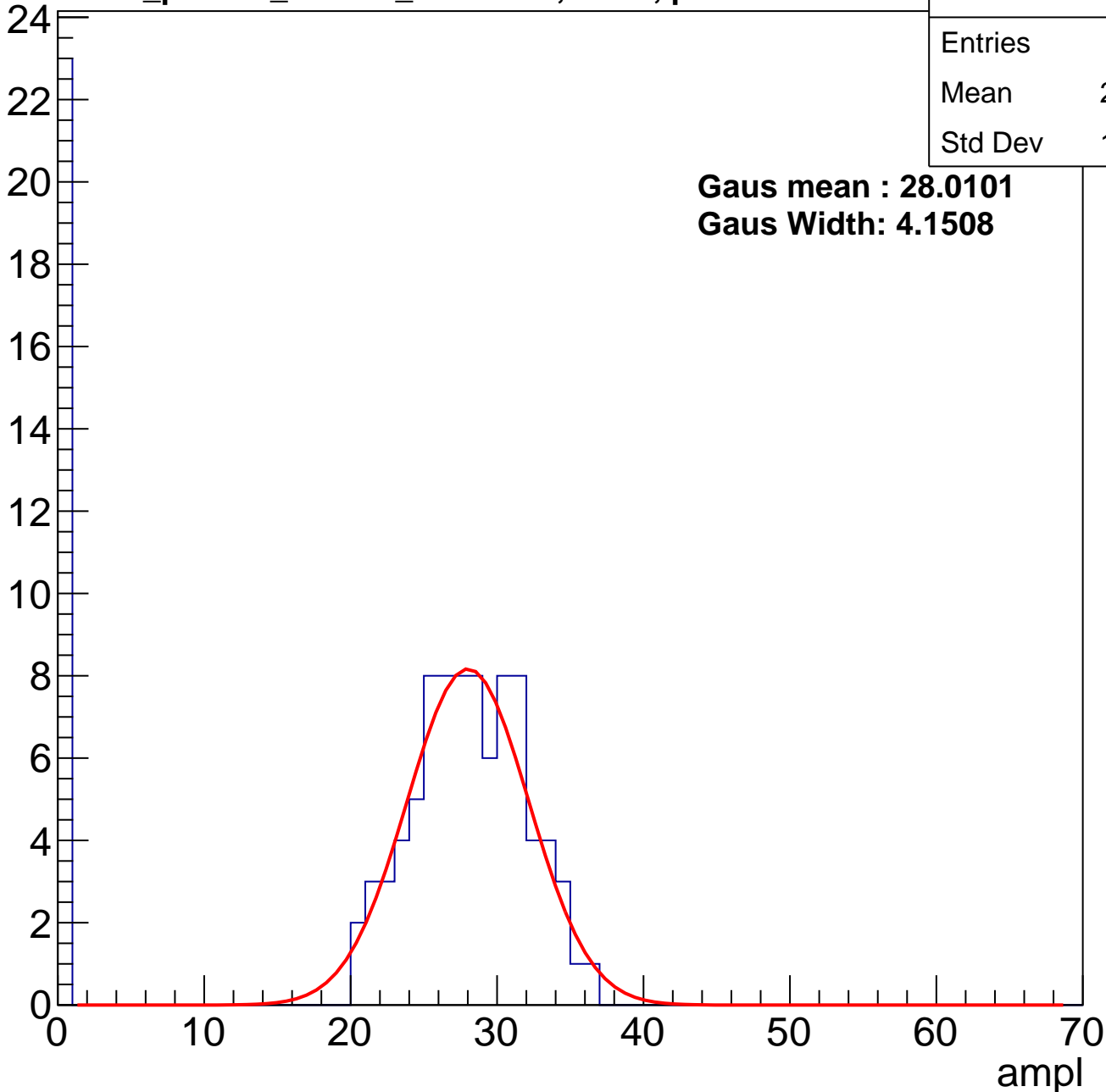
calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	21.72
Std Dev	11.83

Gaus mean : 28.0101

Gaus Width: 4.1508

Entry



B1L103S, U21-ch105, adc1

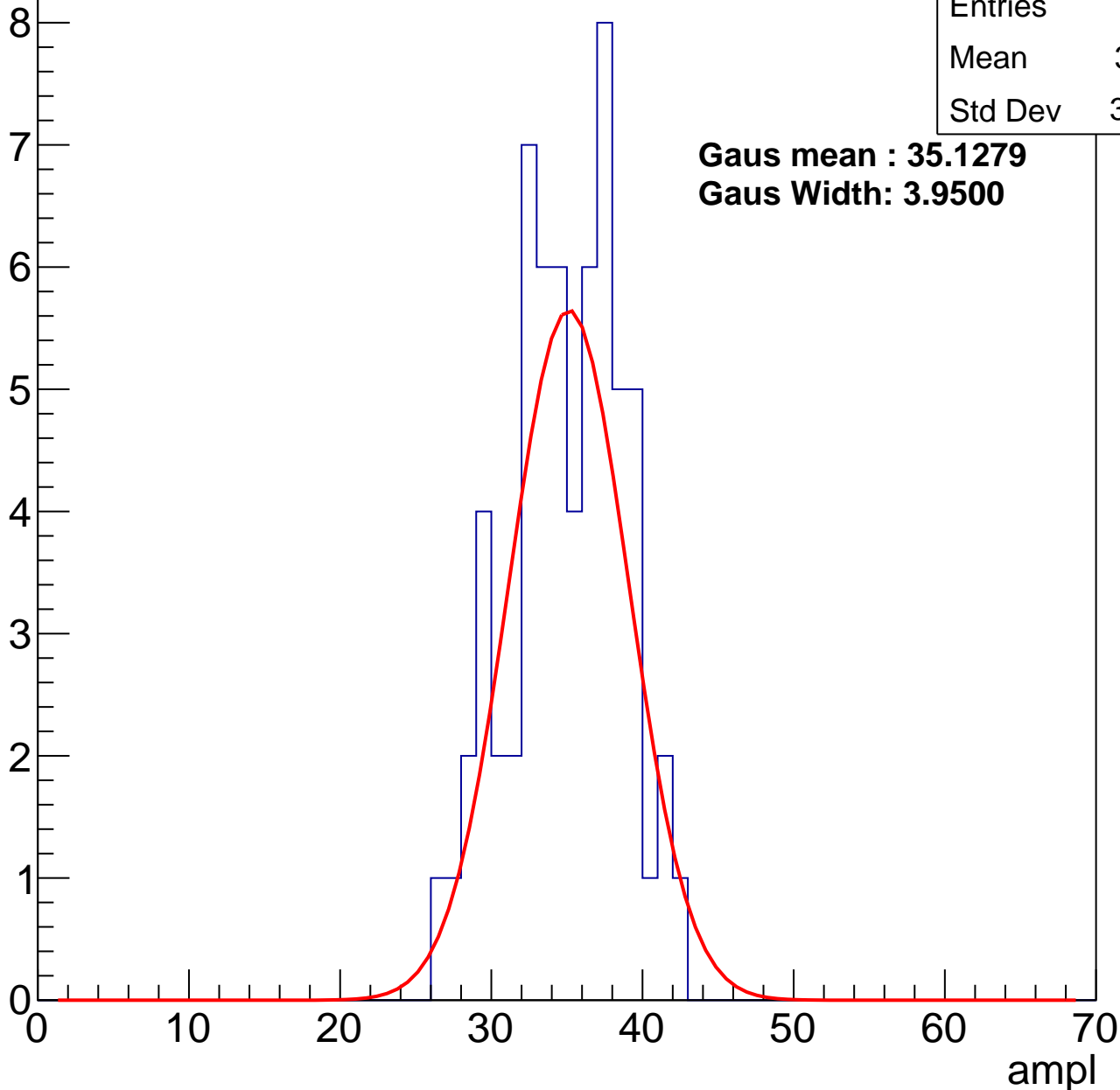
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.51
Std Dev	3.686

Gaus mean : 35.1279

Gaus Width: 3.9500



B1L103S, U21-ch105, adc2

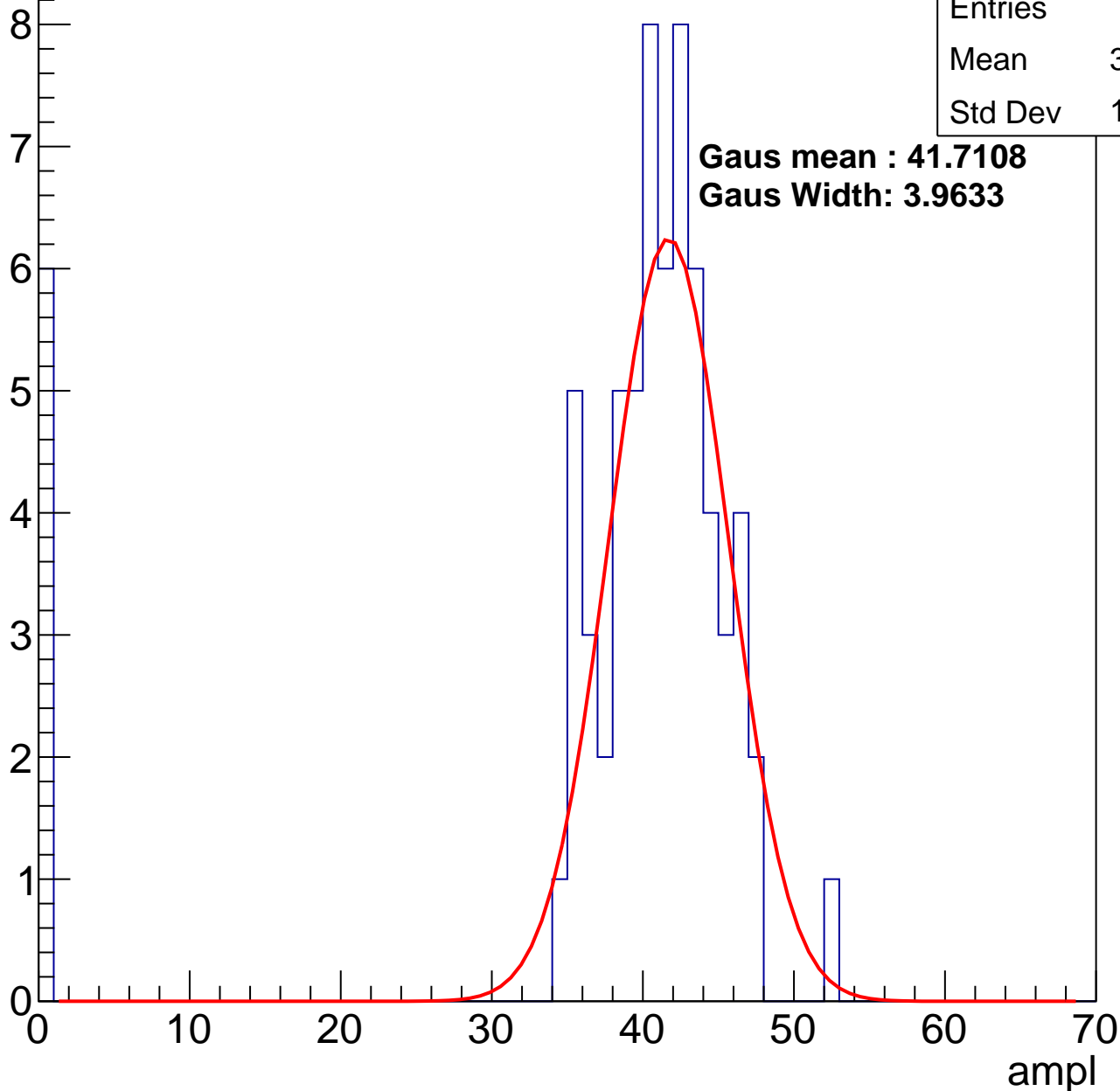
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	37.35
Std Dev	12.03

Gaus mean : 41.7108

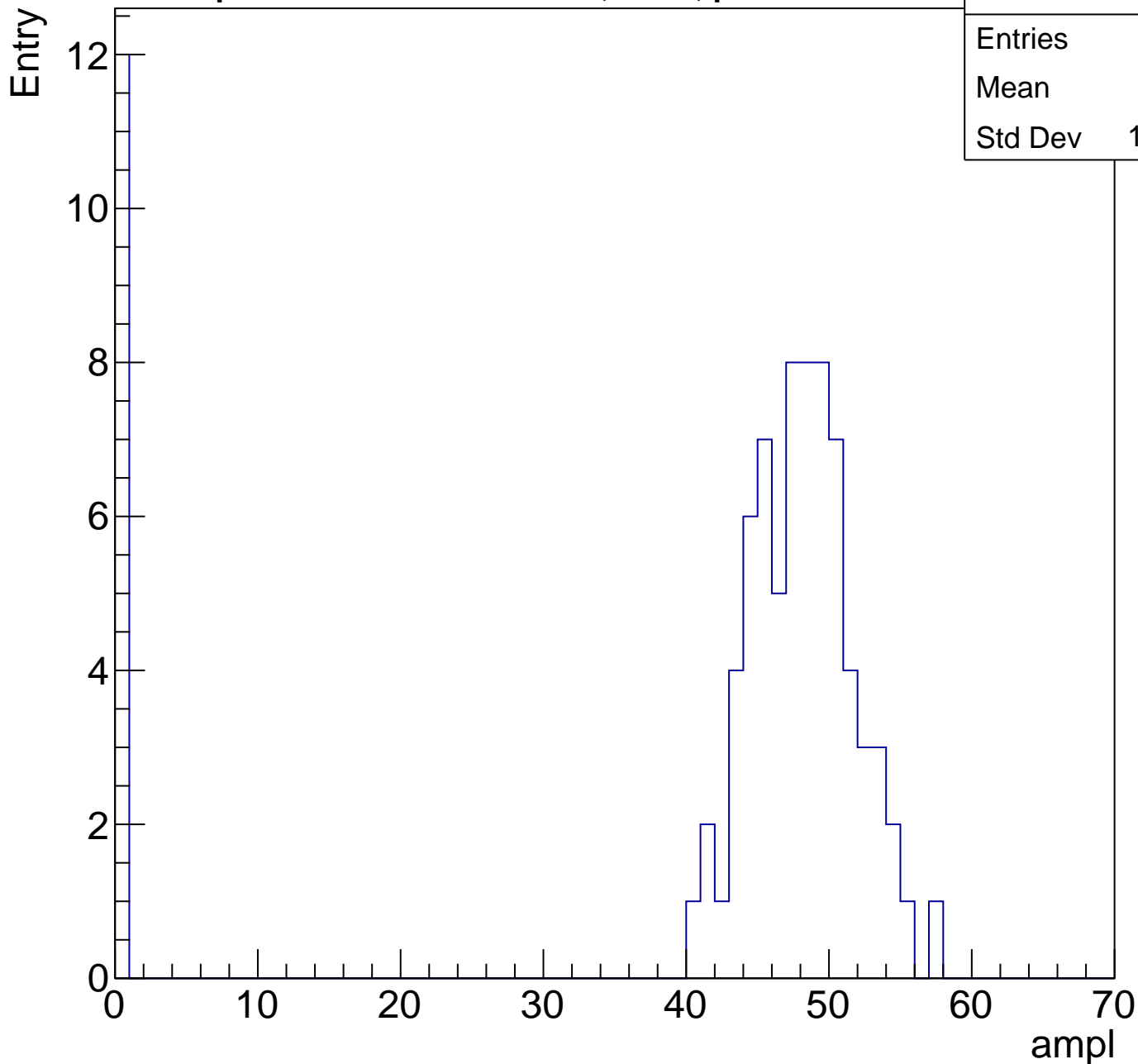
Gaus Width: 3.9633



B1L103S, U21-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	40.8
Std Dev	17.08

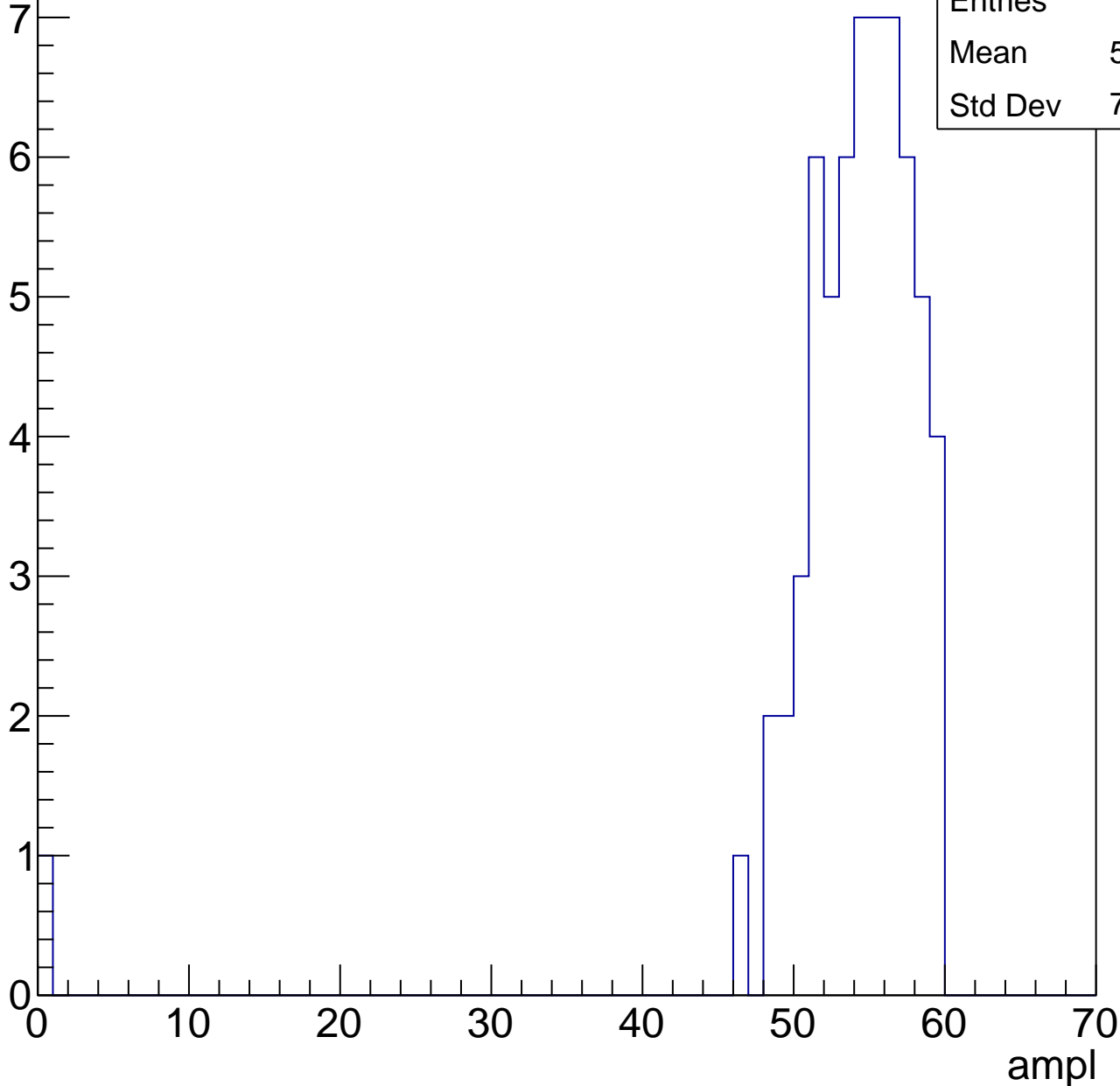


B1L103S, U21-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

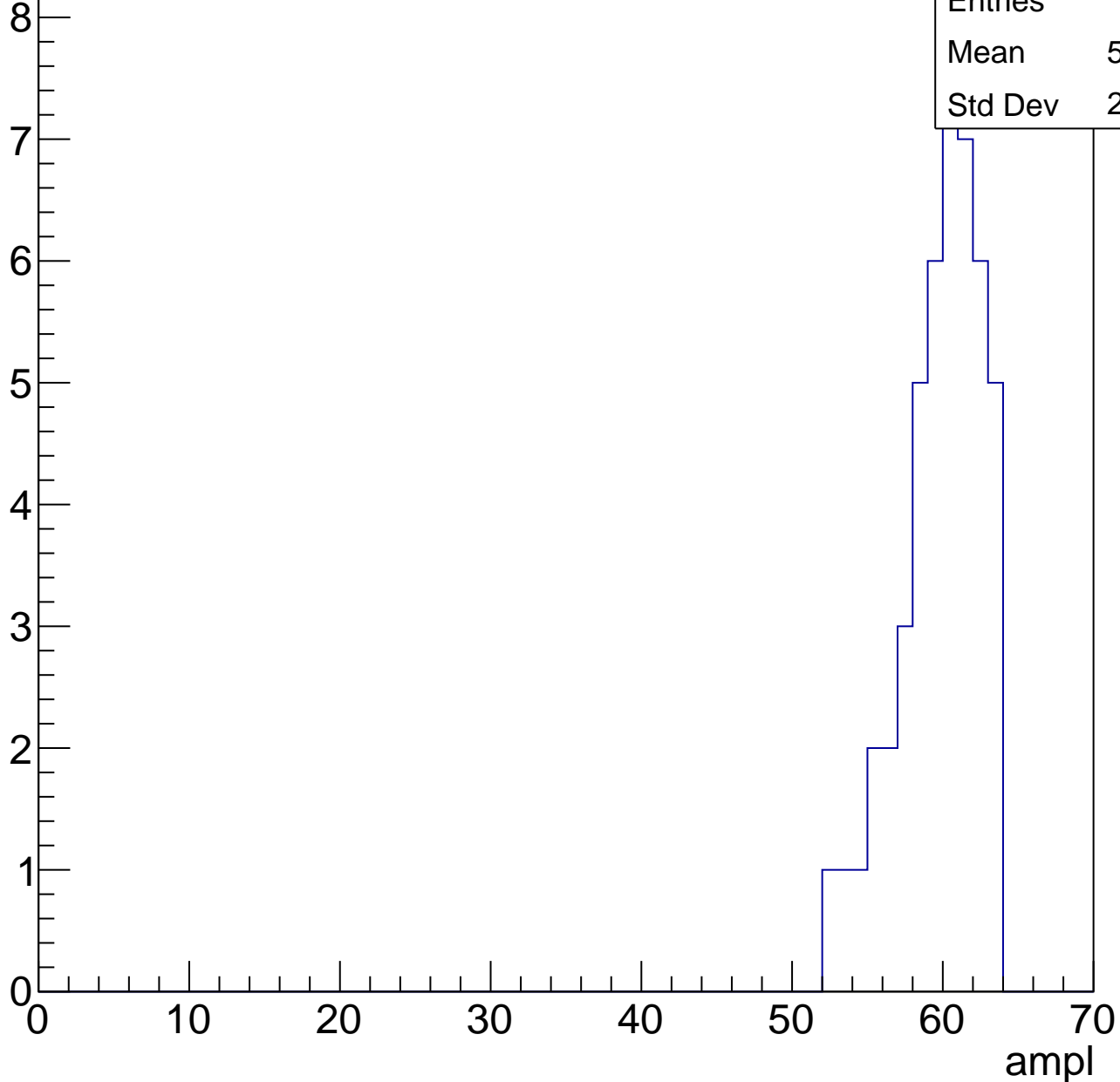
Entries	62
Mean	53.18
Std Dev	7.467



B1L103S, U21-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

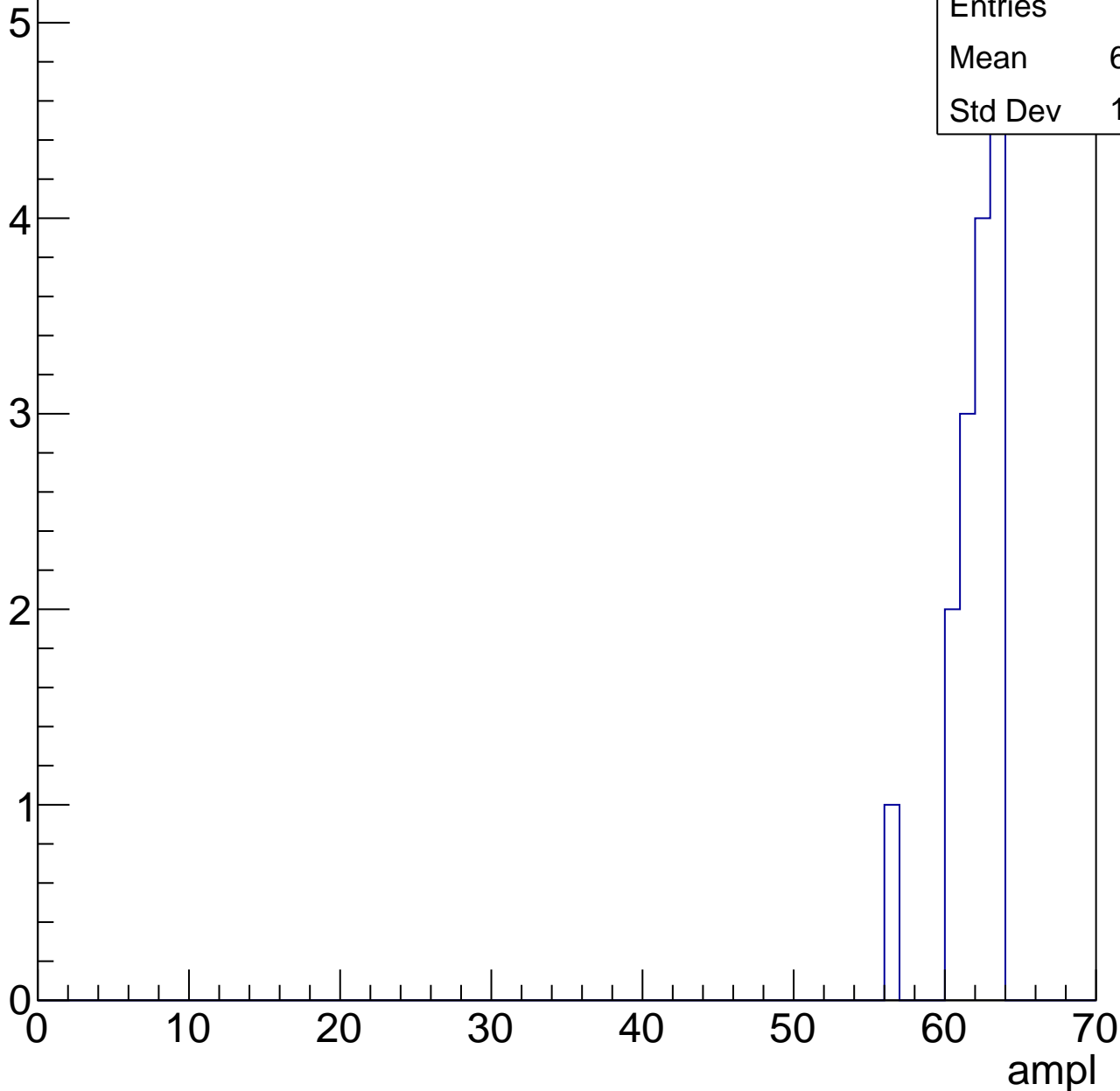


B1L103S, U21-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.47
Std Dev	1.784

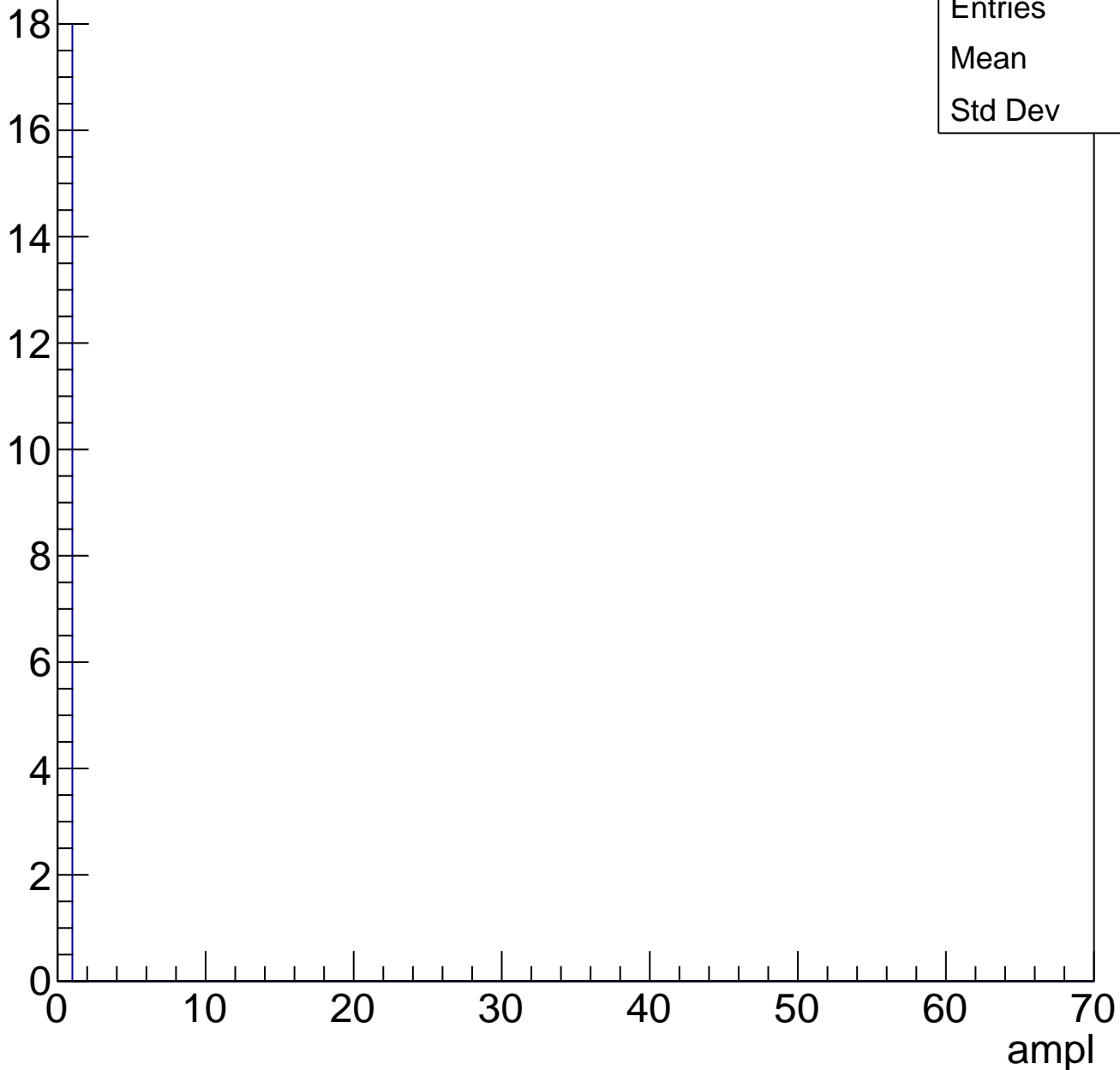


B1L103S, U21-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0

Entry



B1L103S, U21-ch106, adc0

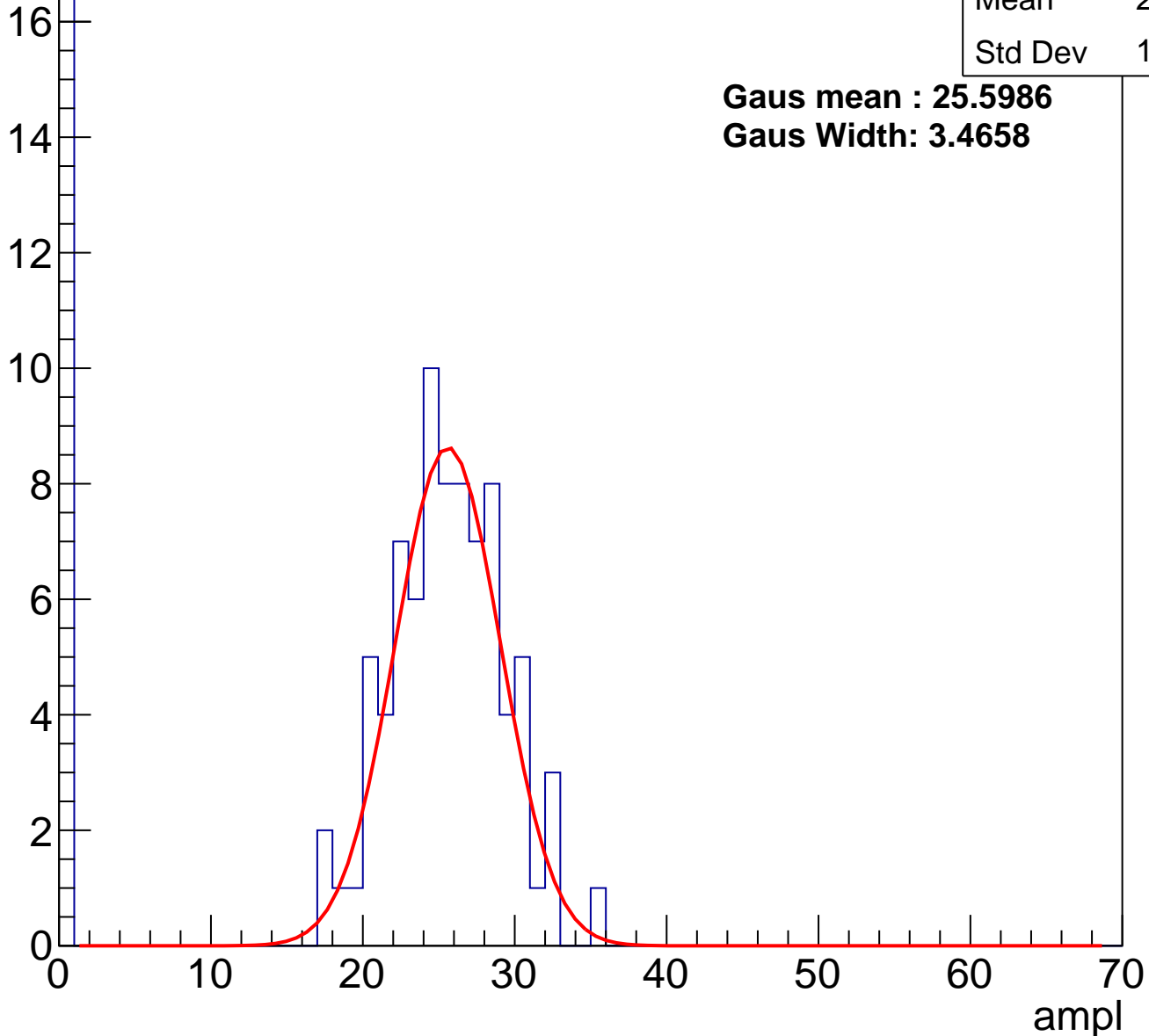
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	20.78
Std Dev	10.08

Gaus mean : 25.5986

Gaus Width: 3.4658

Entry



B1L103S, U21-ch106, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	32.32
Std Dev	7.055

Gaus mean : 34.1723

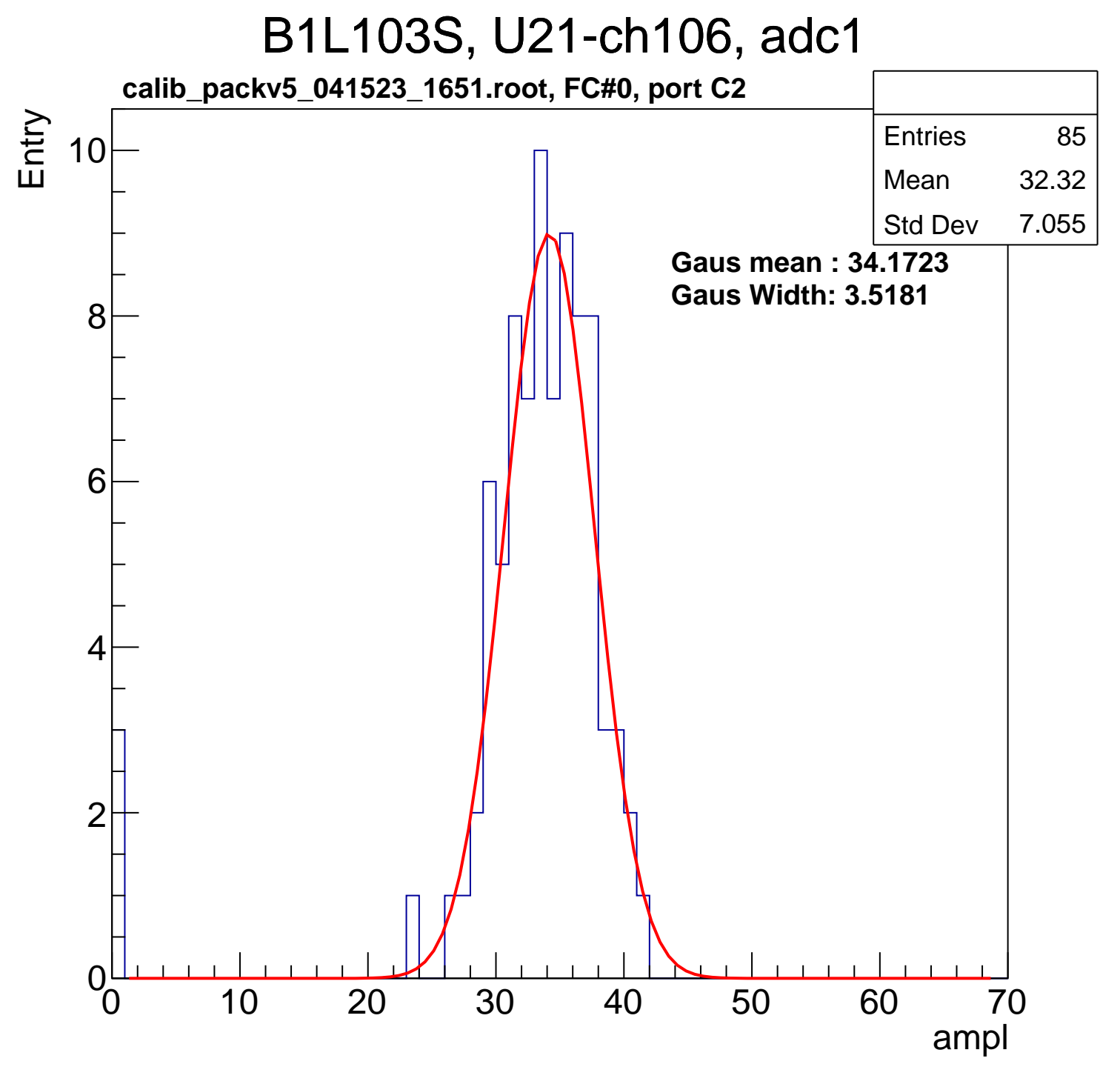
Gaus Width: 3.5181

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch106, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	31.98
Std Dev	16.05

Gaus mean : 40.2709

Gaus Width: 4.8194

Entry

10

8

6

4

2

0

0

10

20

30

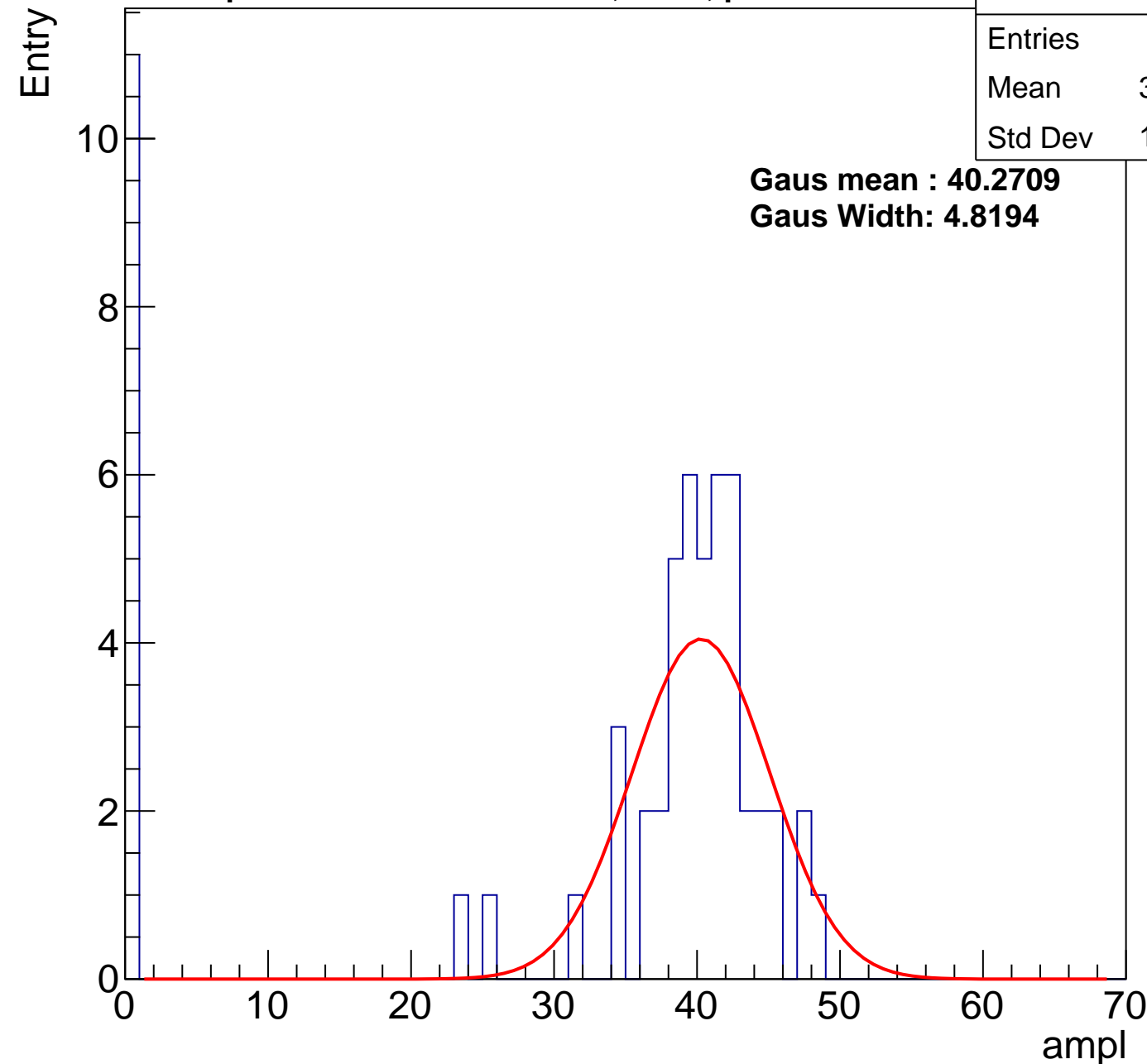
40

50

60

70

ampl

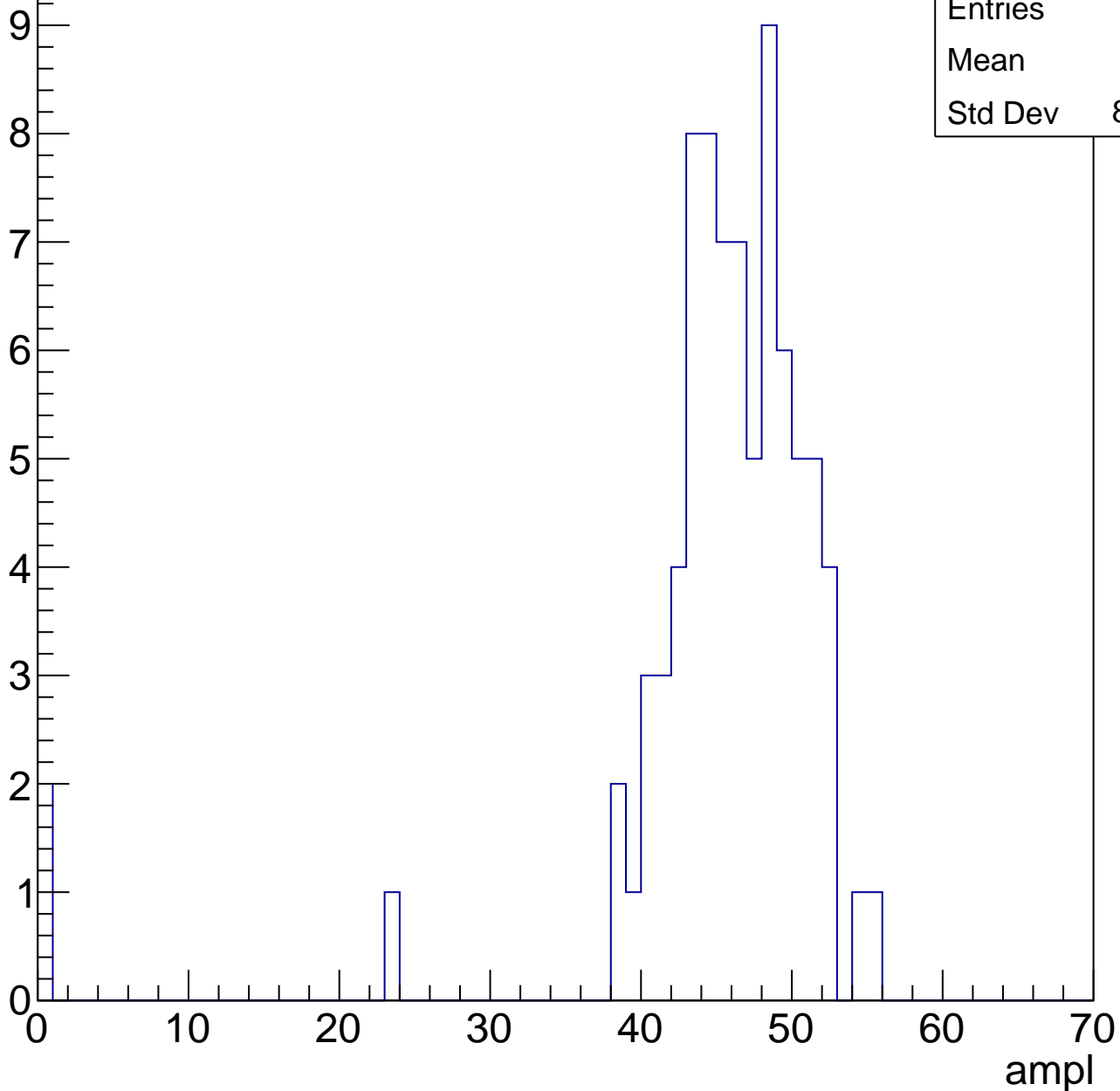


B1L103S, U21-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	44.7
Std Dev	8.371

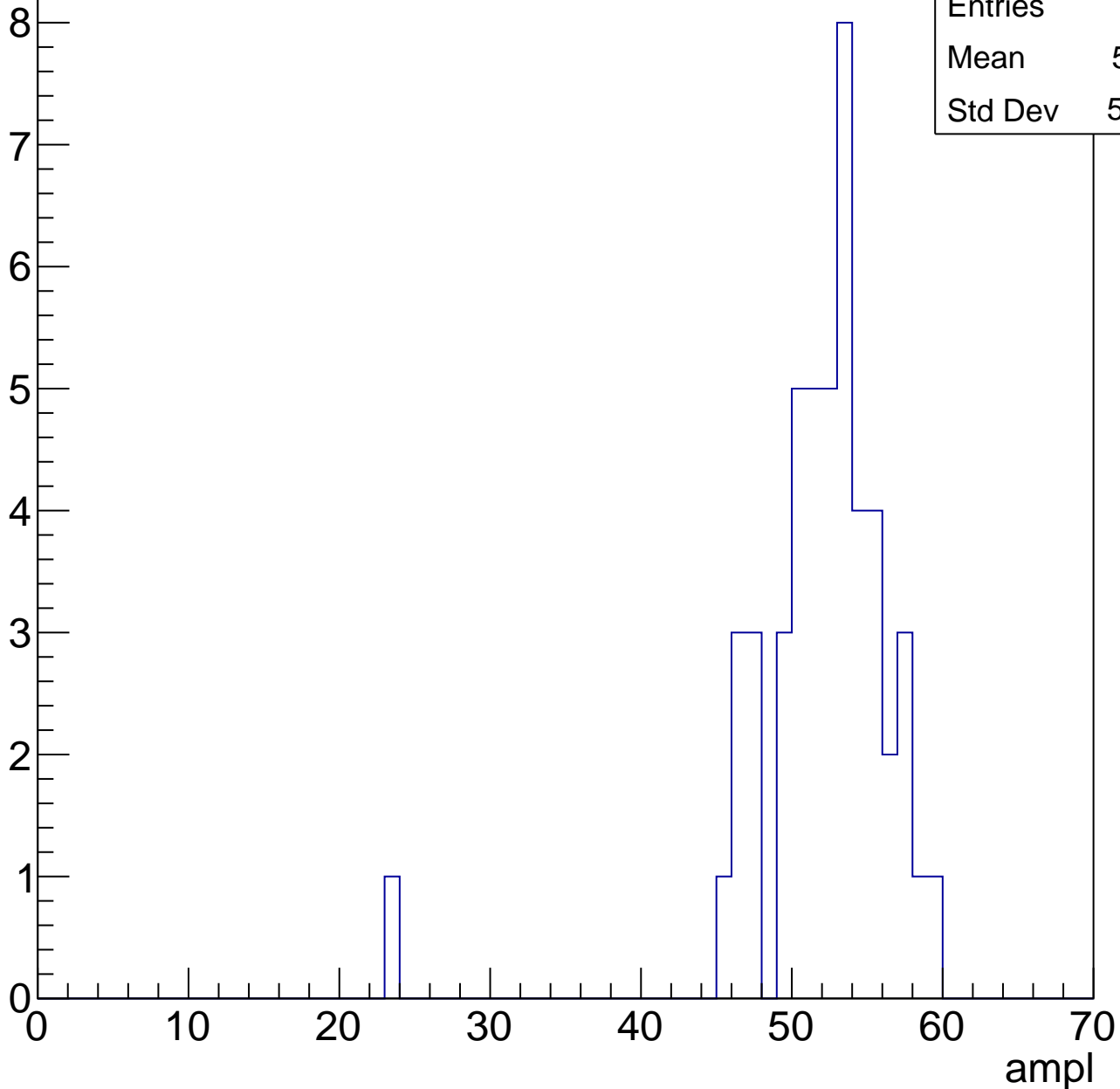


B1L103S, U21-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	51.41
Std Dev	5.268

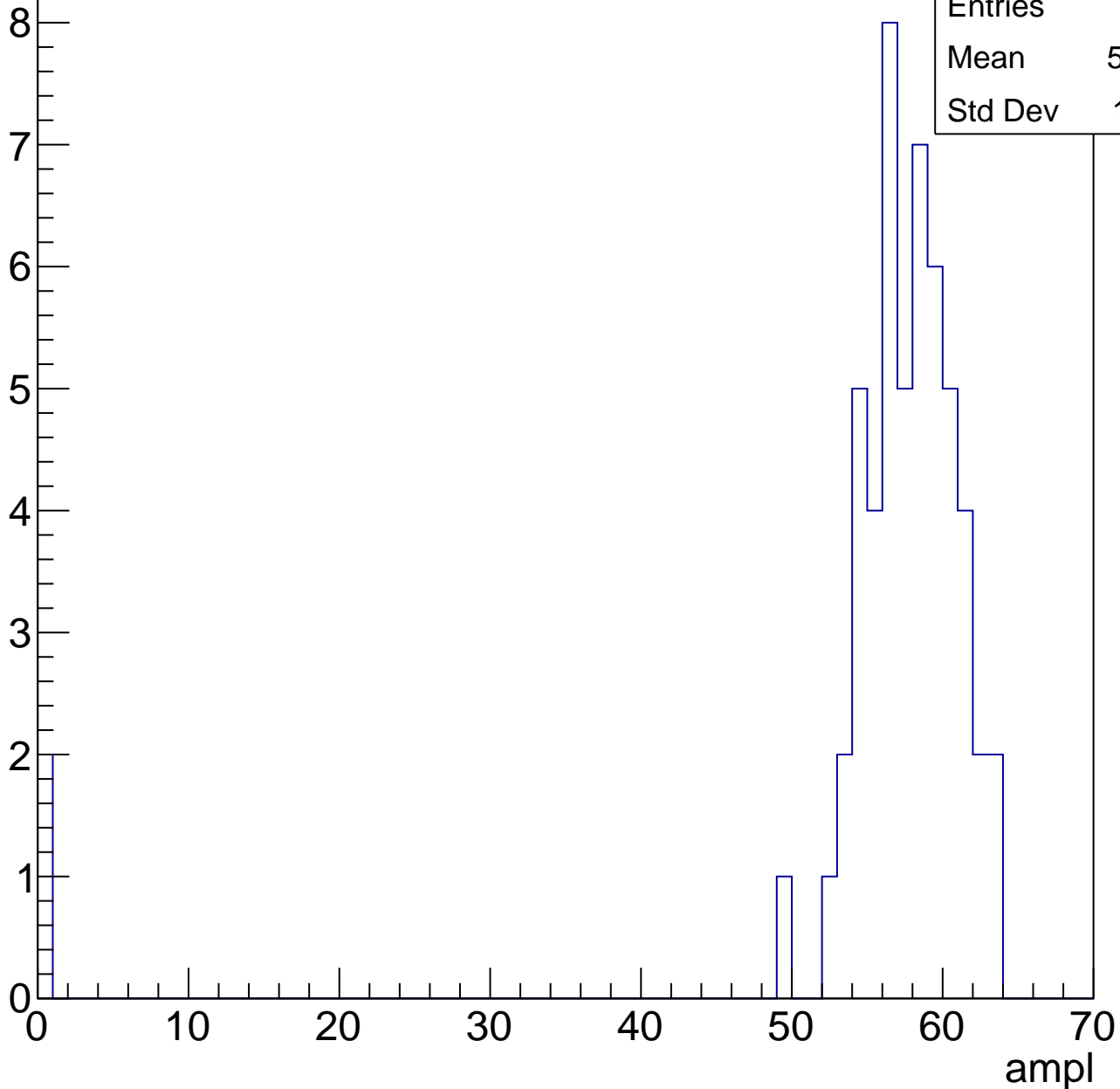


B1L103S, U21-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	55.26
Std Dev	11.21

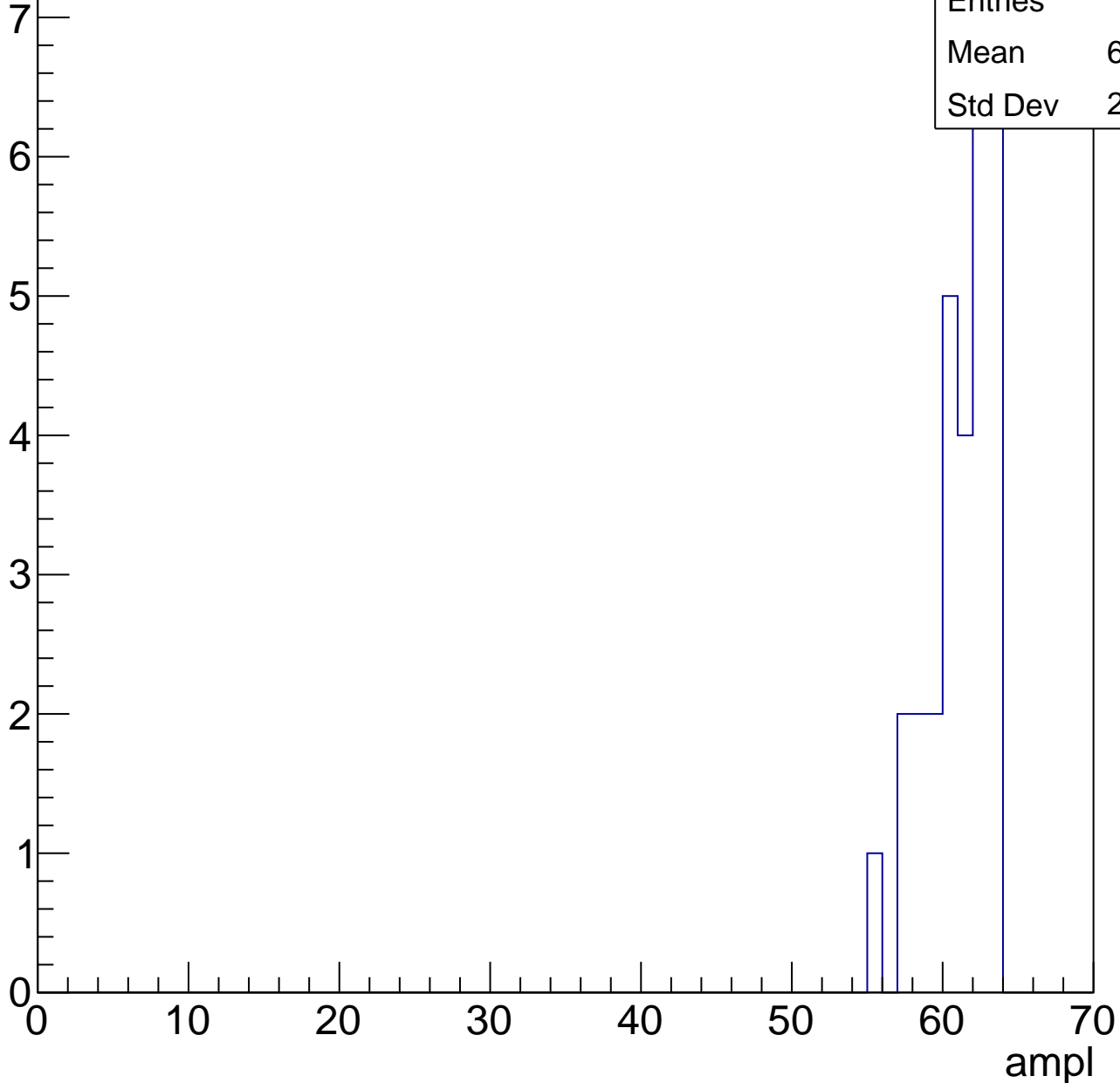


B1L103S, U21-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	60.73
Std Dev	2.097

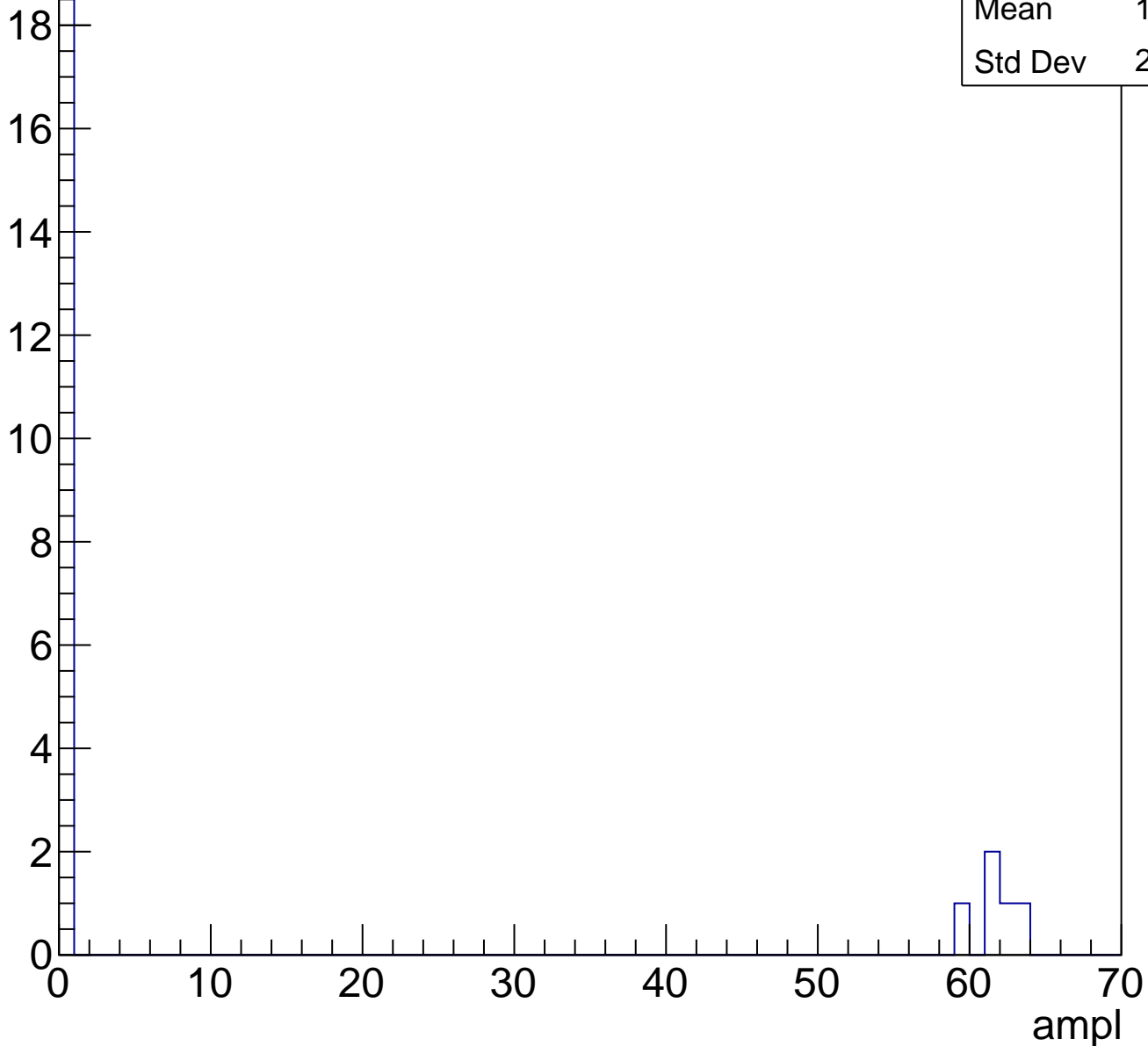


B1L103S, U21-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	12.75
Std Dev	24.86

Entry



B1L103S, U21-ch107, adc0

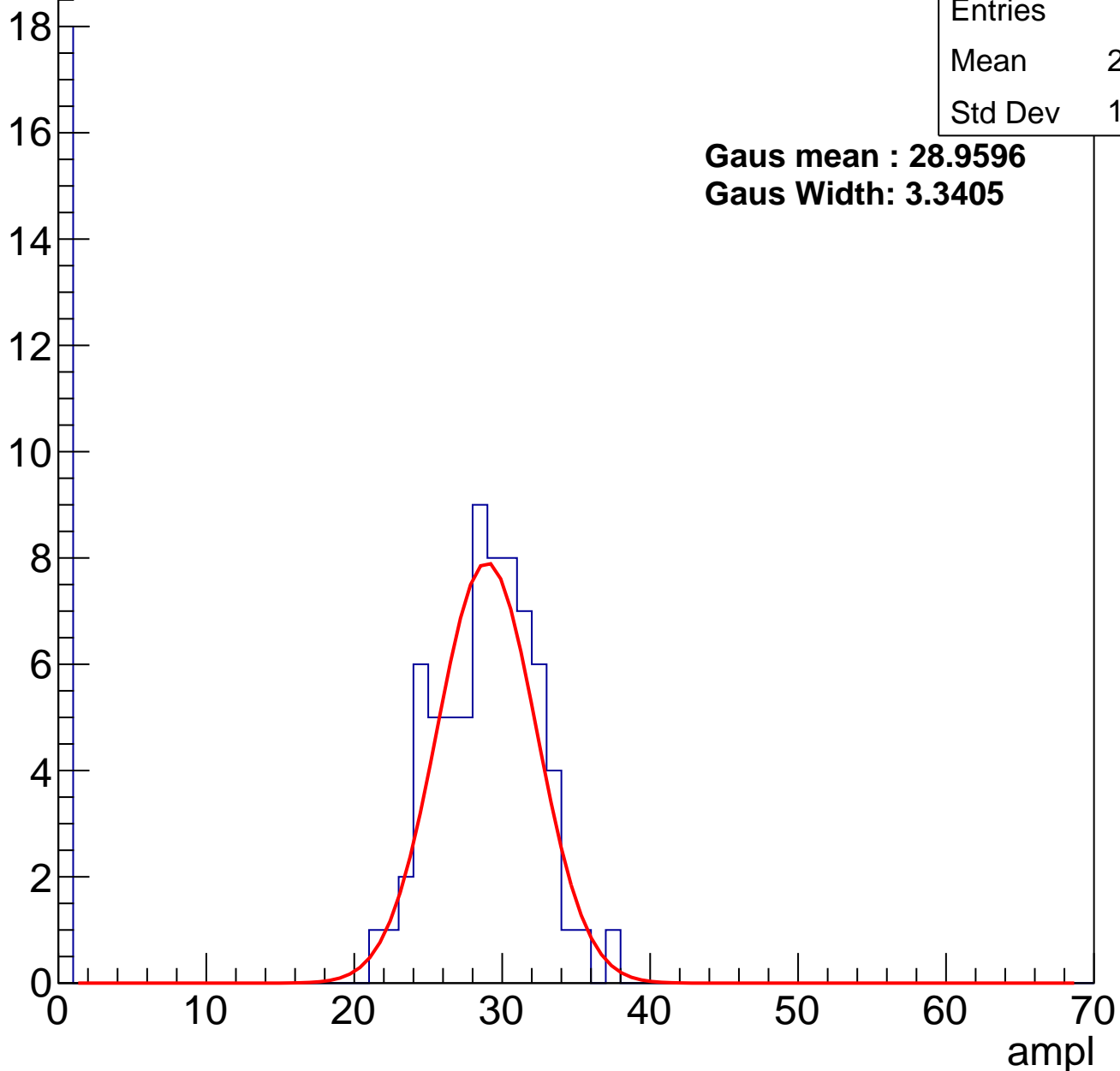
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	22.66
Std Dev	11.86

Gaus mean : 28.9596

Gaus Width: 3.3405

Entry



B1L103S, U21-ch107, adc1

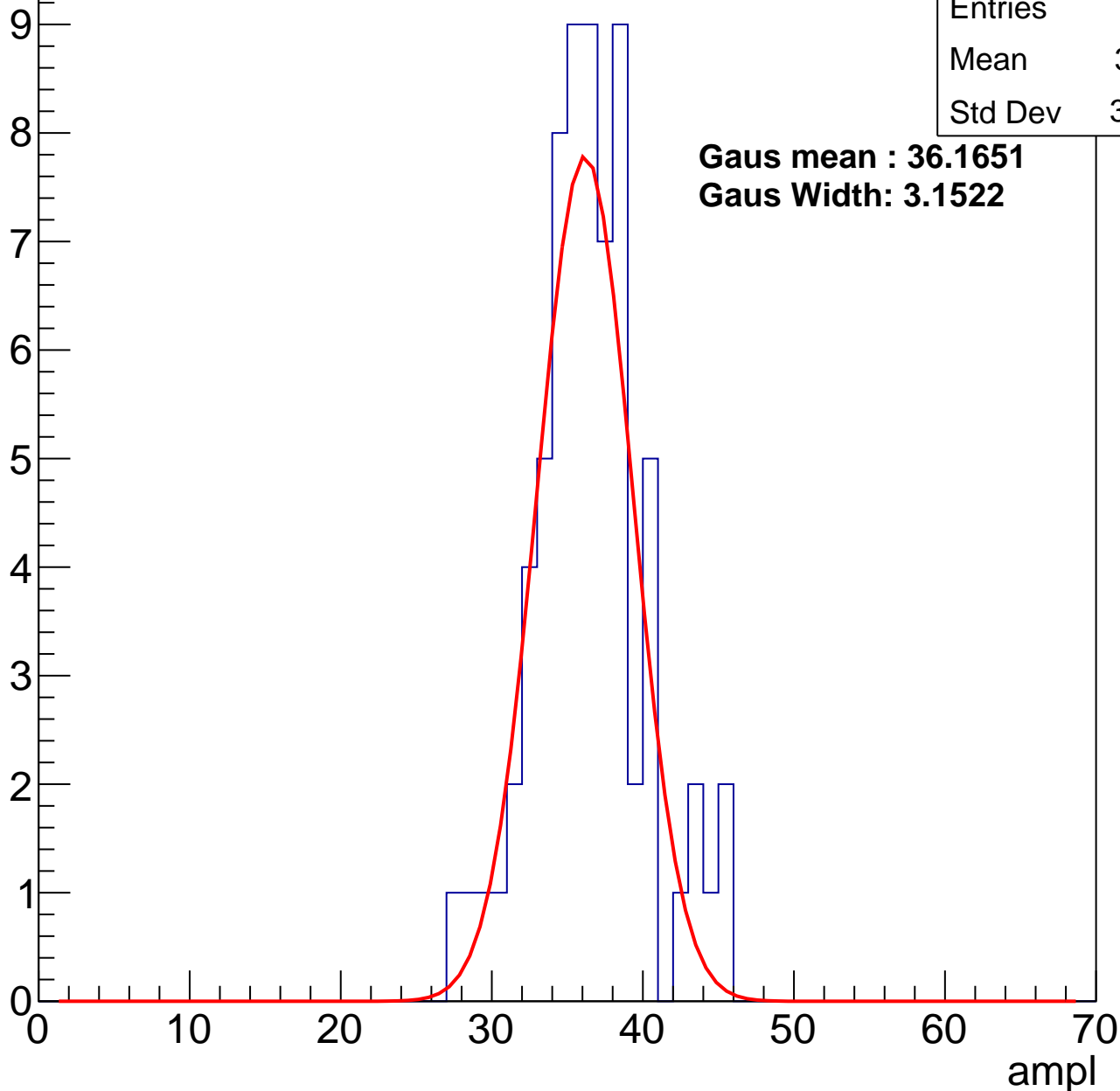
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.01
Std Dev	3.647

Gaus mean : 36.1651

Gaus Width: 3.1522



B1L103S, U21-ch107, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	38.92
Std Dev	13.28

Gaus mean : 43.6686

Gaus Width: 3.6285

Entry

10

8

6

4

2

0

0

10

20

30

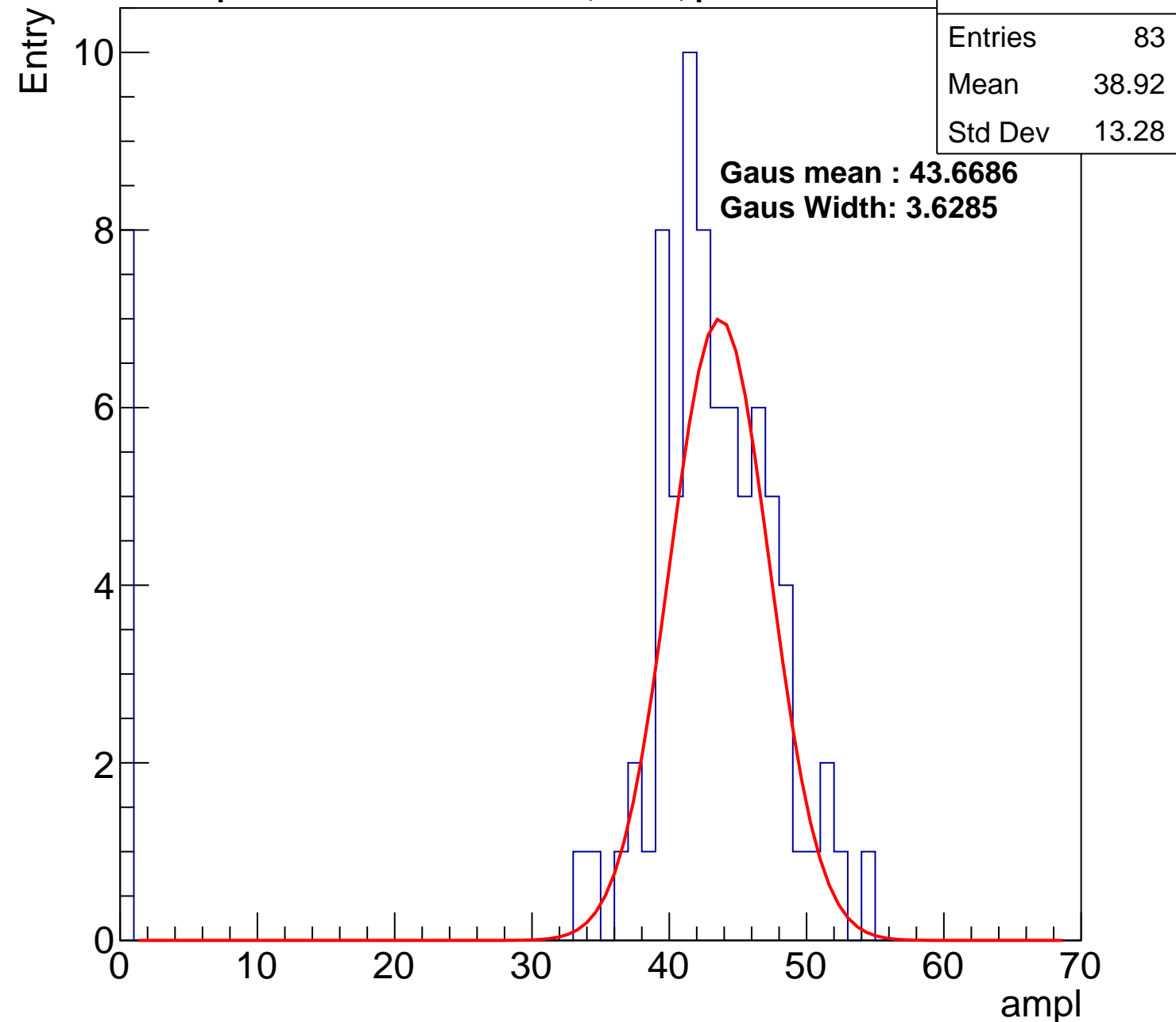
40

50

60

70

ampl

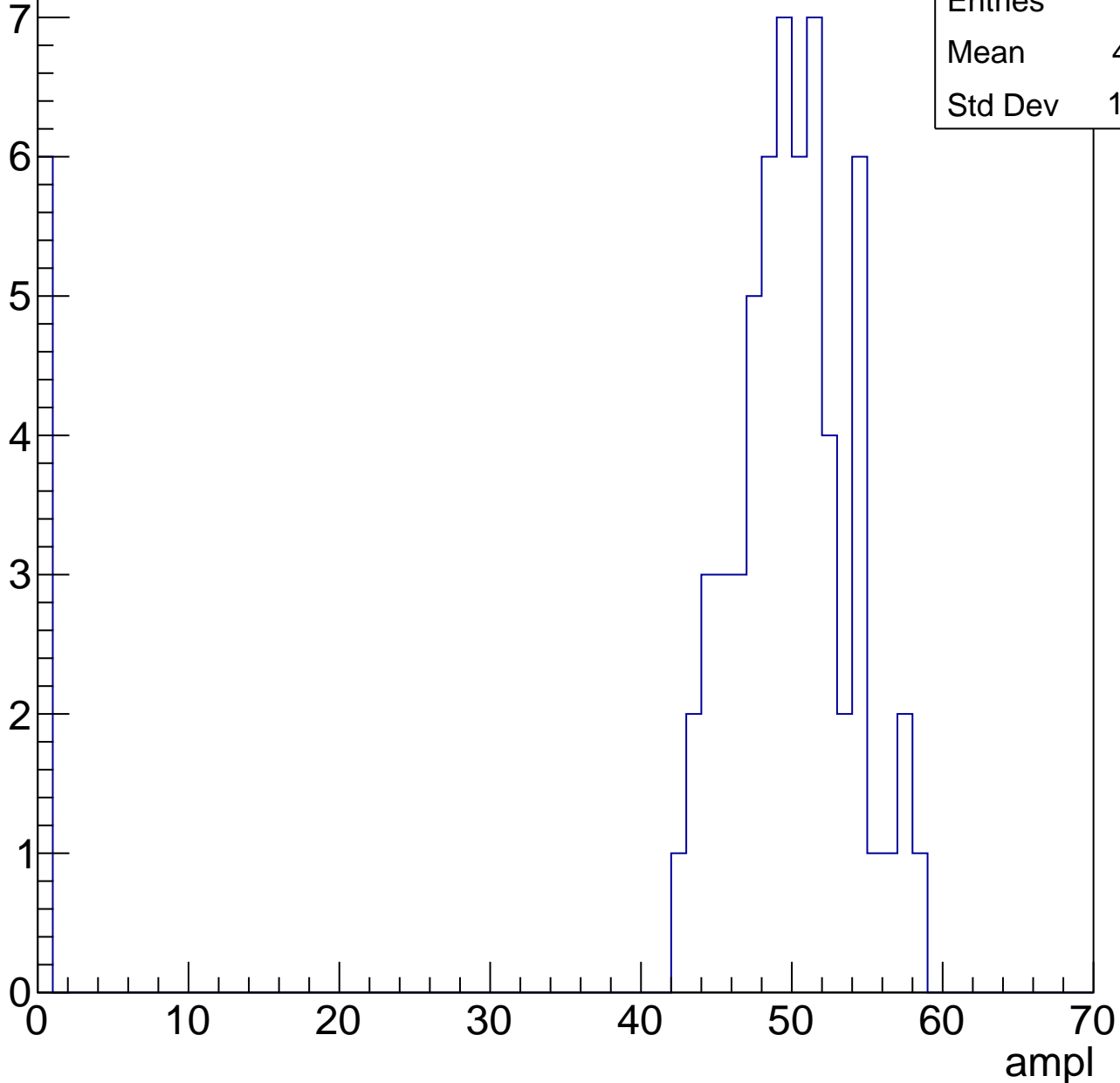


B1L103S, U21-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	45.11
Std Dev	14.69

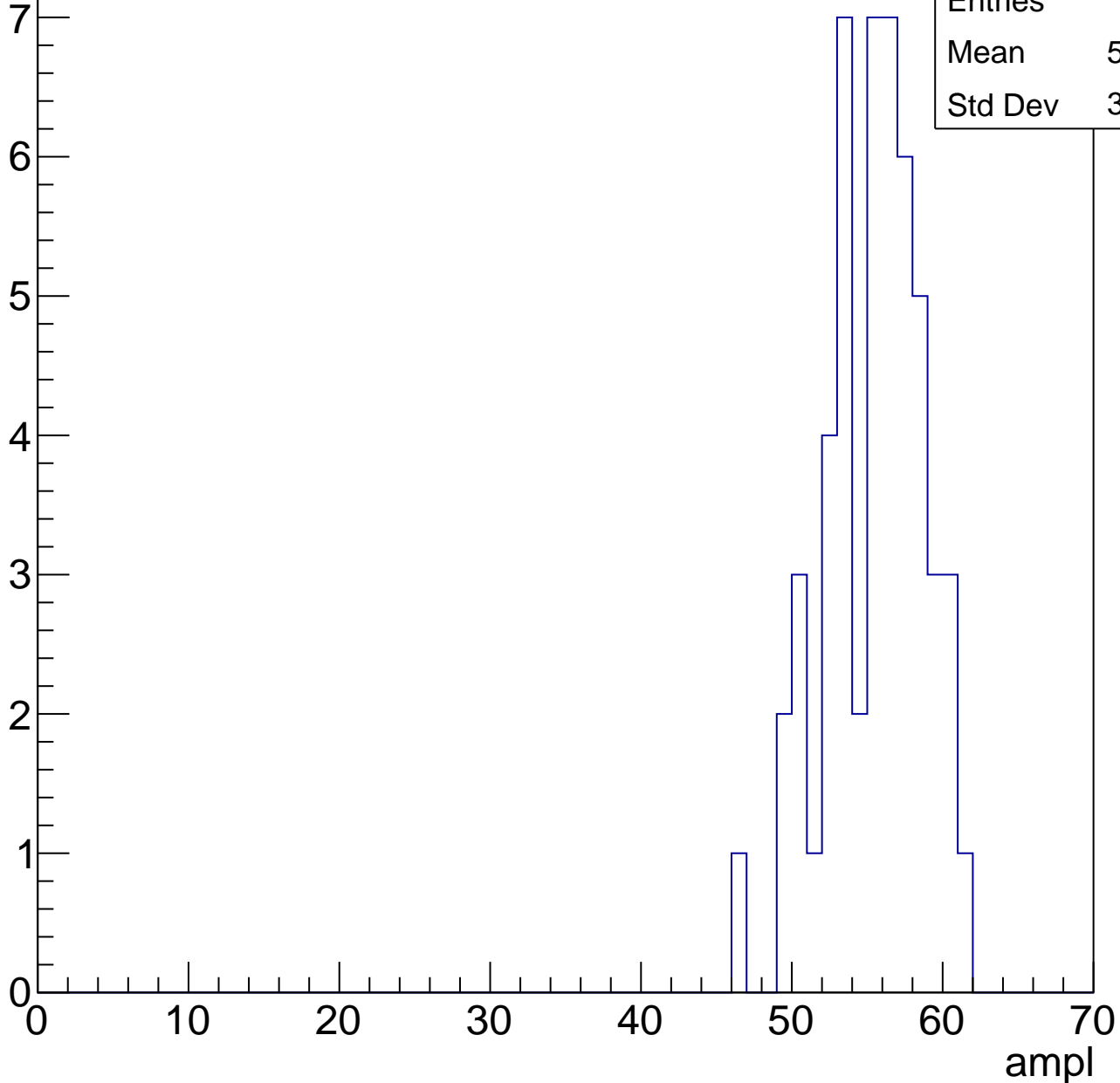


B1L103S, U21-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	54.98
Std Dev	3.237

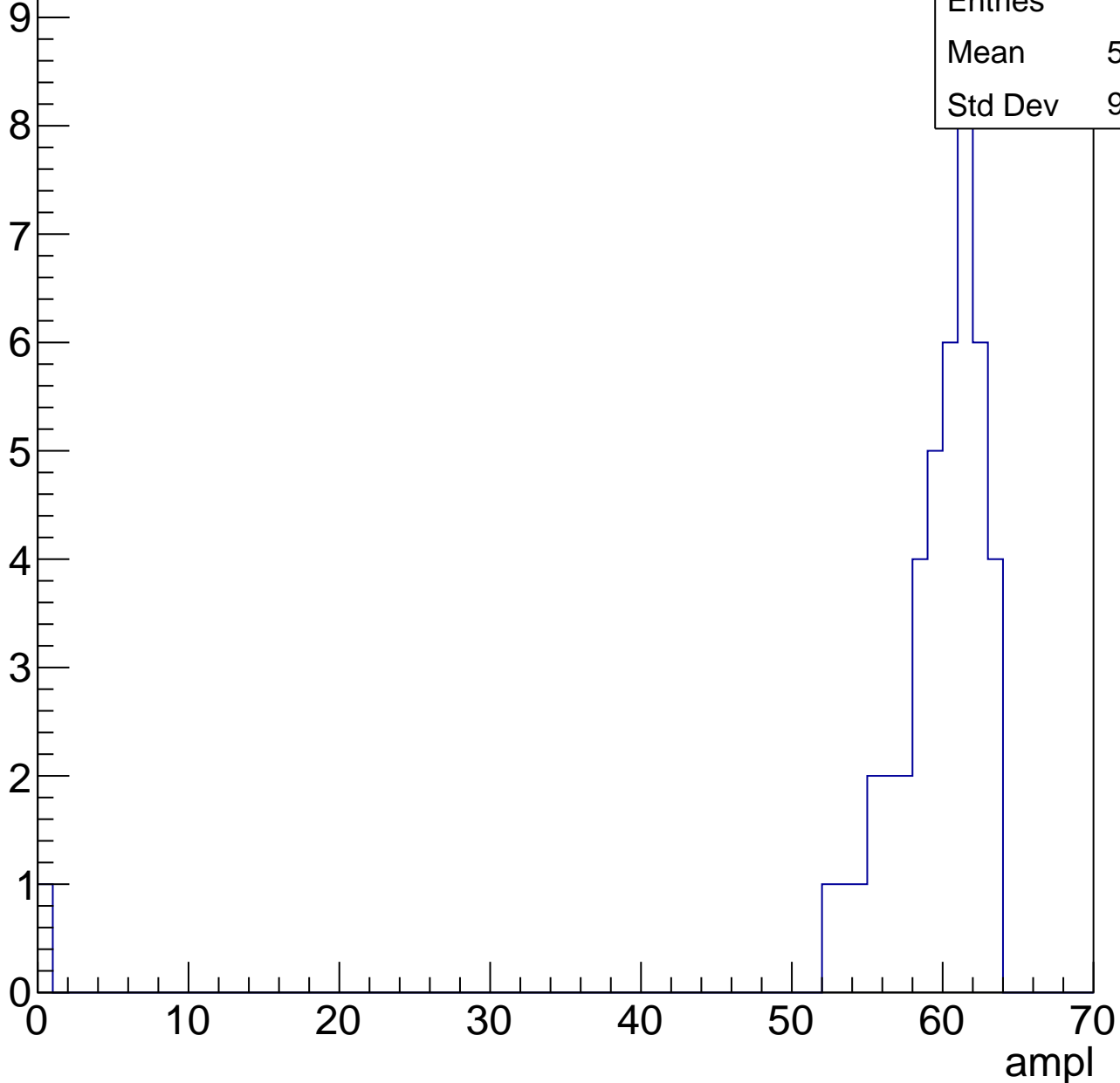


B1L103S, U21-ch107, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	58.07
Std Dev	9.262

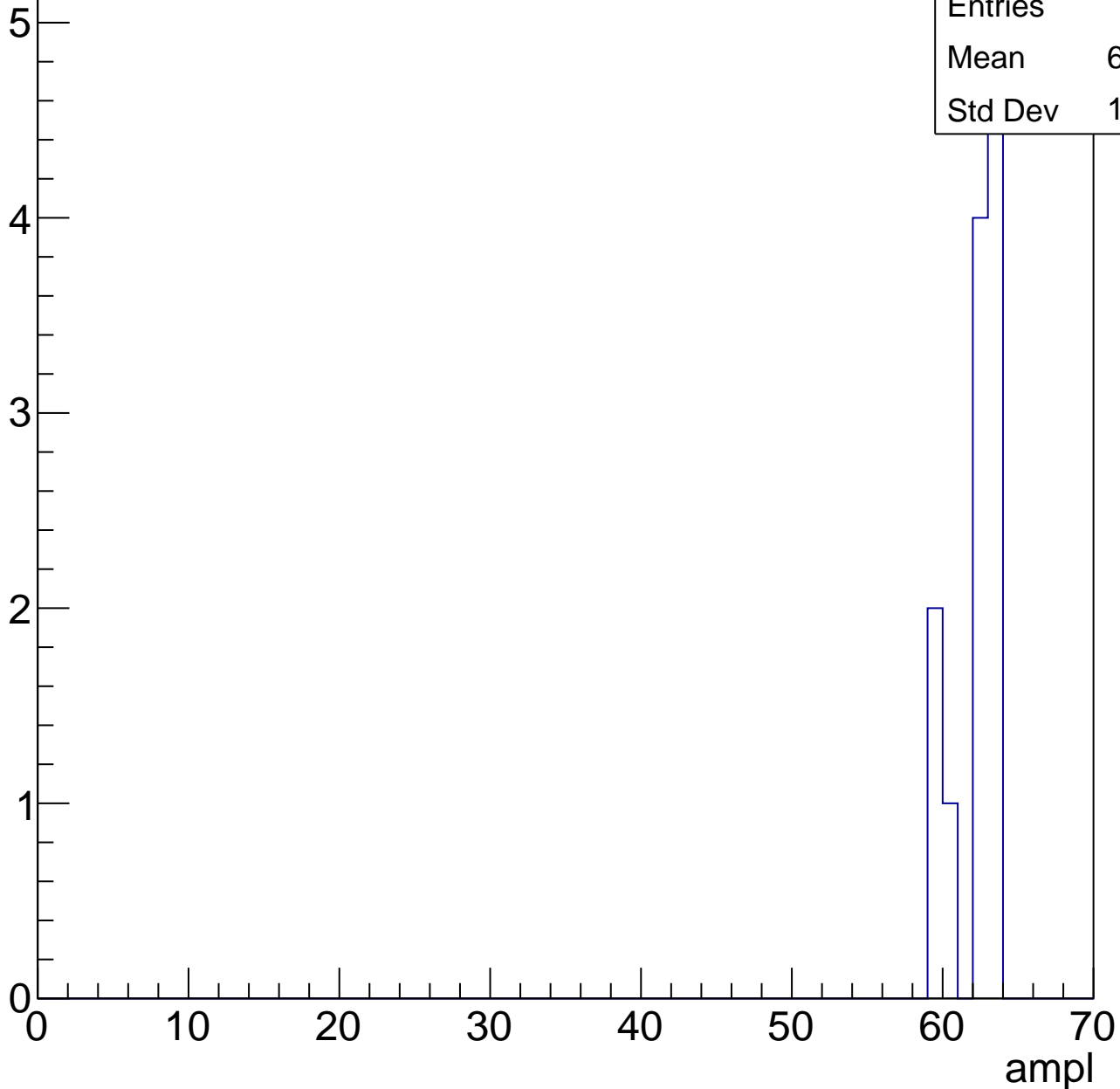


B1L103S, U21-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.75
Std Dev	1.479

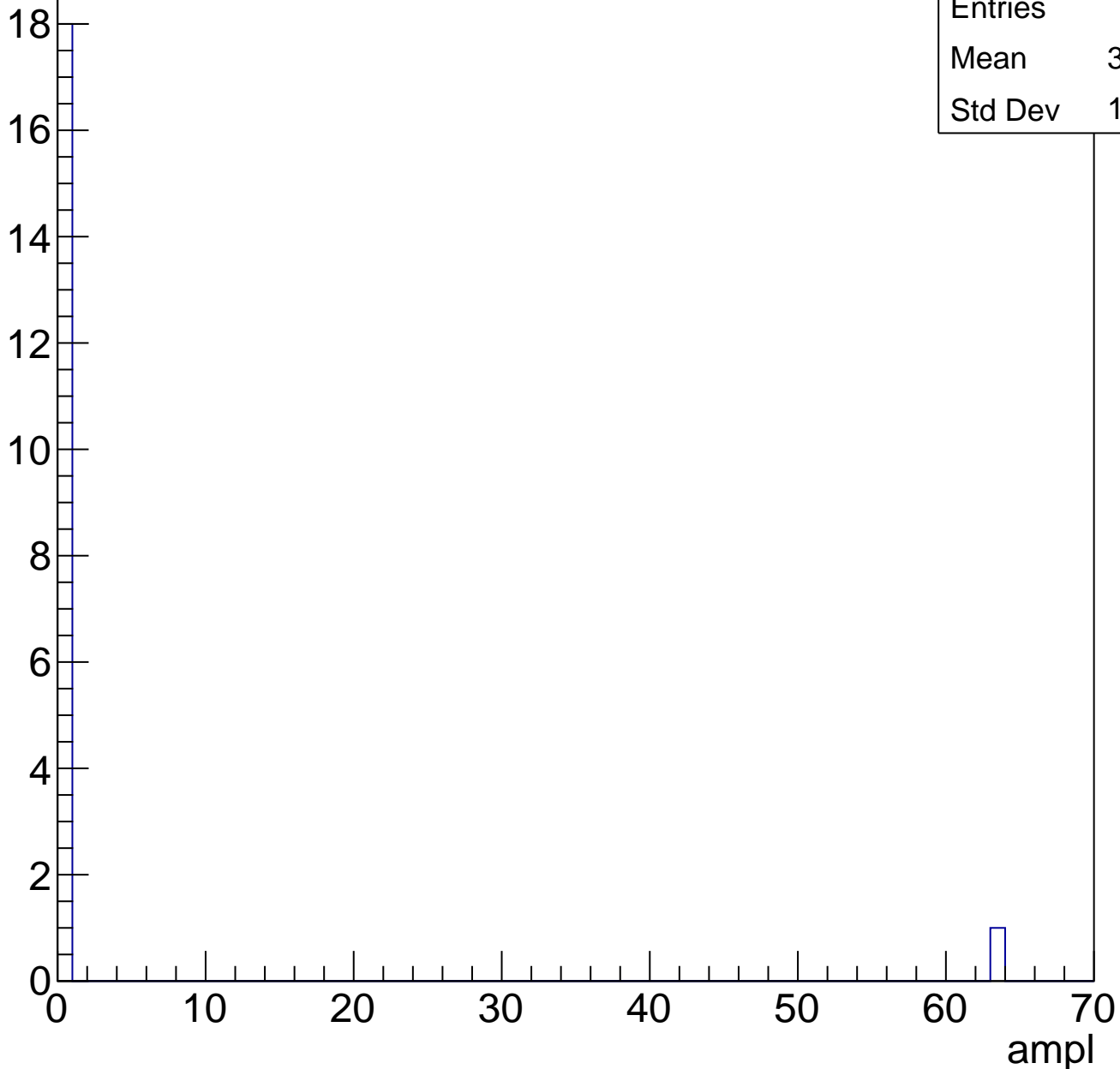


B1L103S, U21-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U21-ch108, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	20.88
Std Dev	10.75

Gaus mean : 26.6556

Gaus Width: 3.8226

Entry

14

12

10

8

6

4

2

0

0

10

20

30

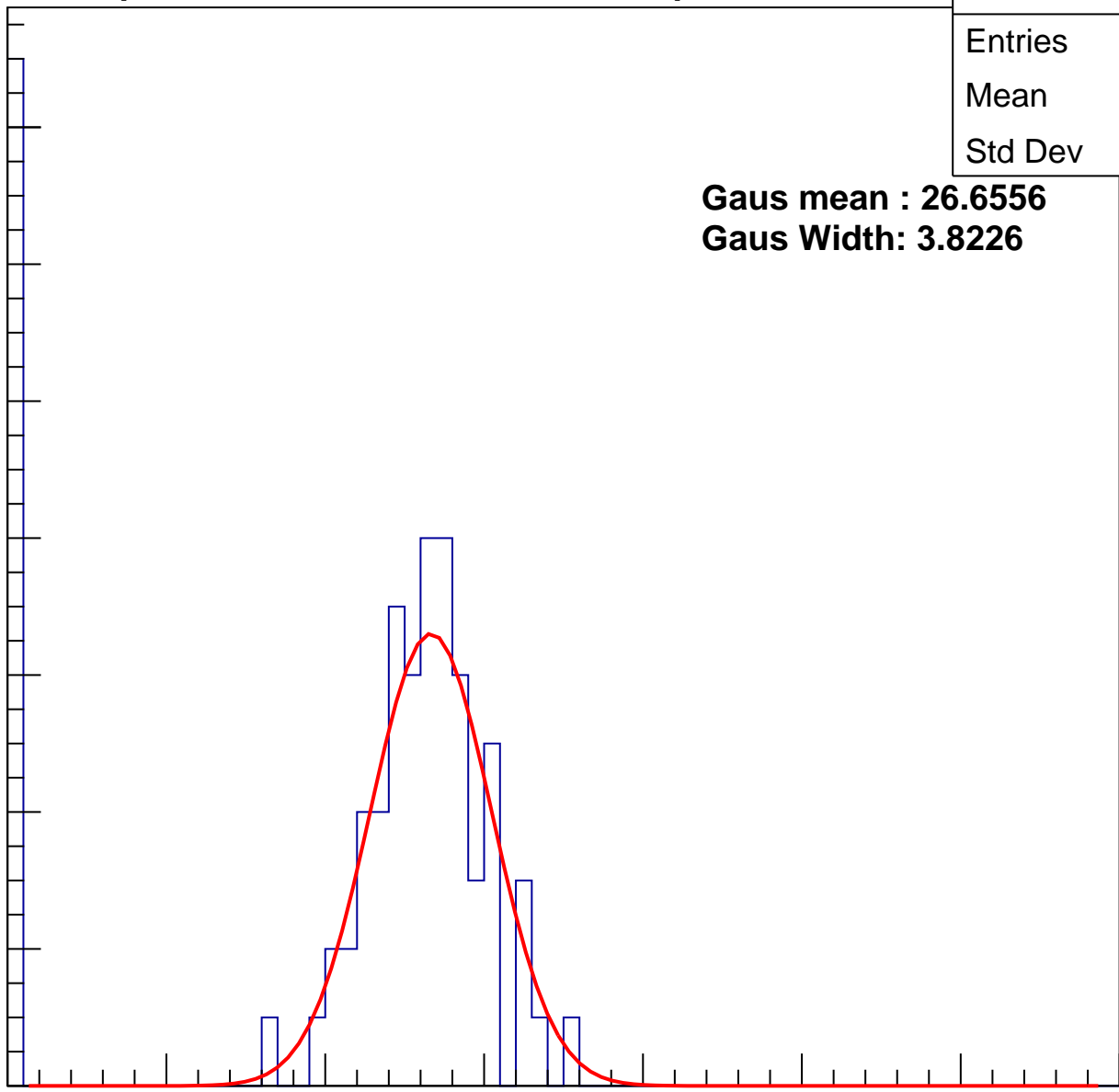
40

50

60

70

ampl



B1L103S, U21-ch108, adc1

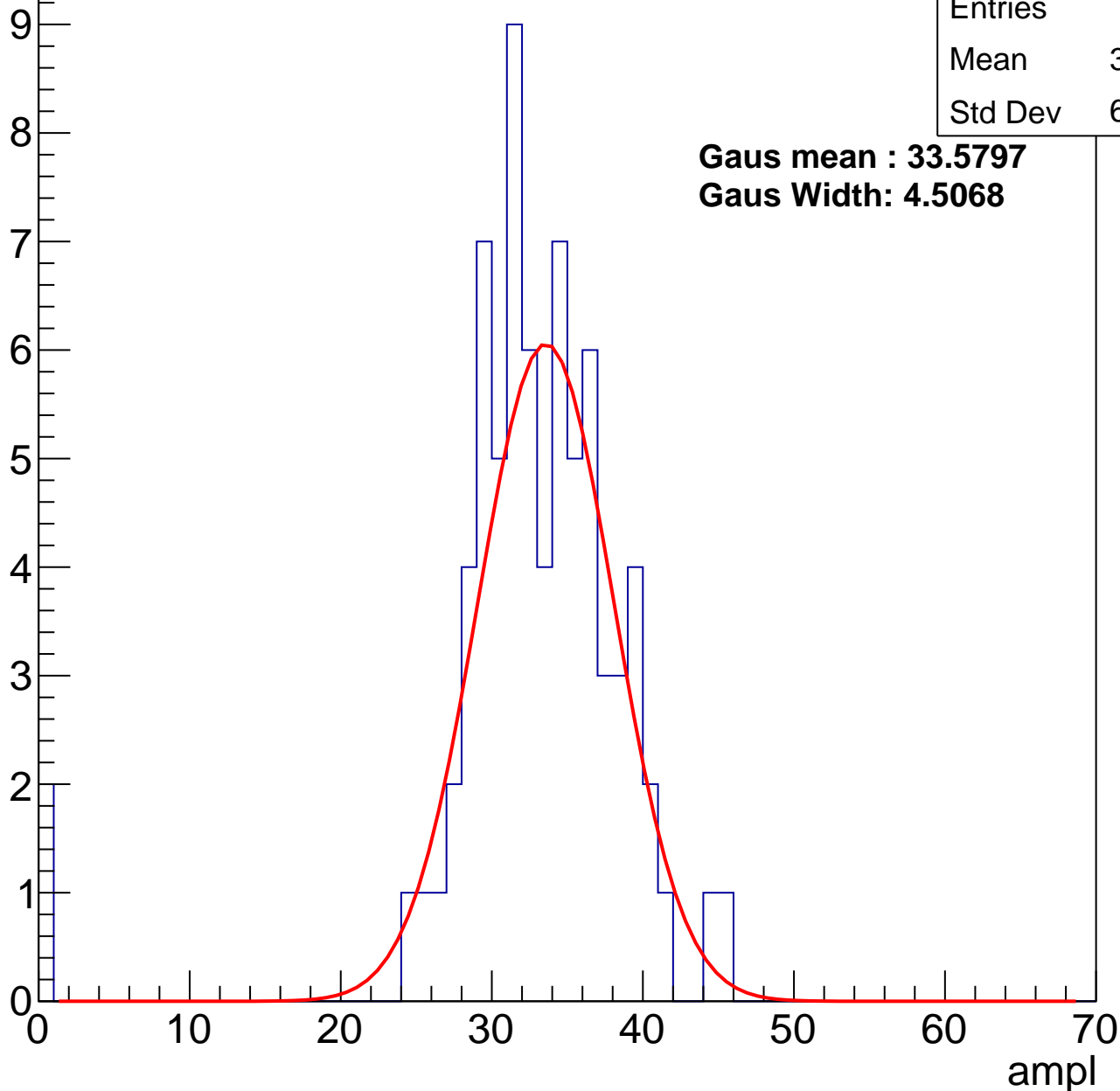
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	32.23
Std Dev	6.805

Gaus mean : 33.5797

Gaus Width: 4.5068



B1L103S, U21-ch108, adc2

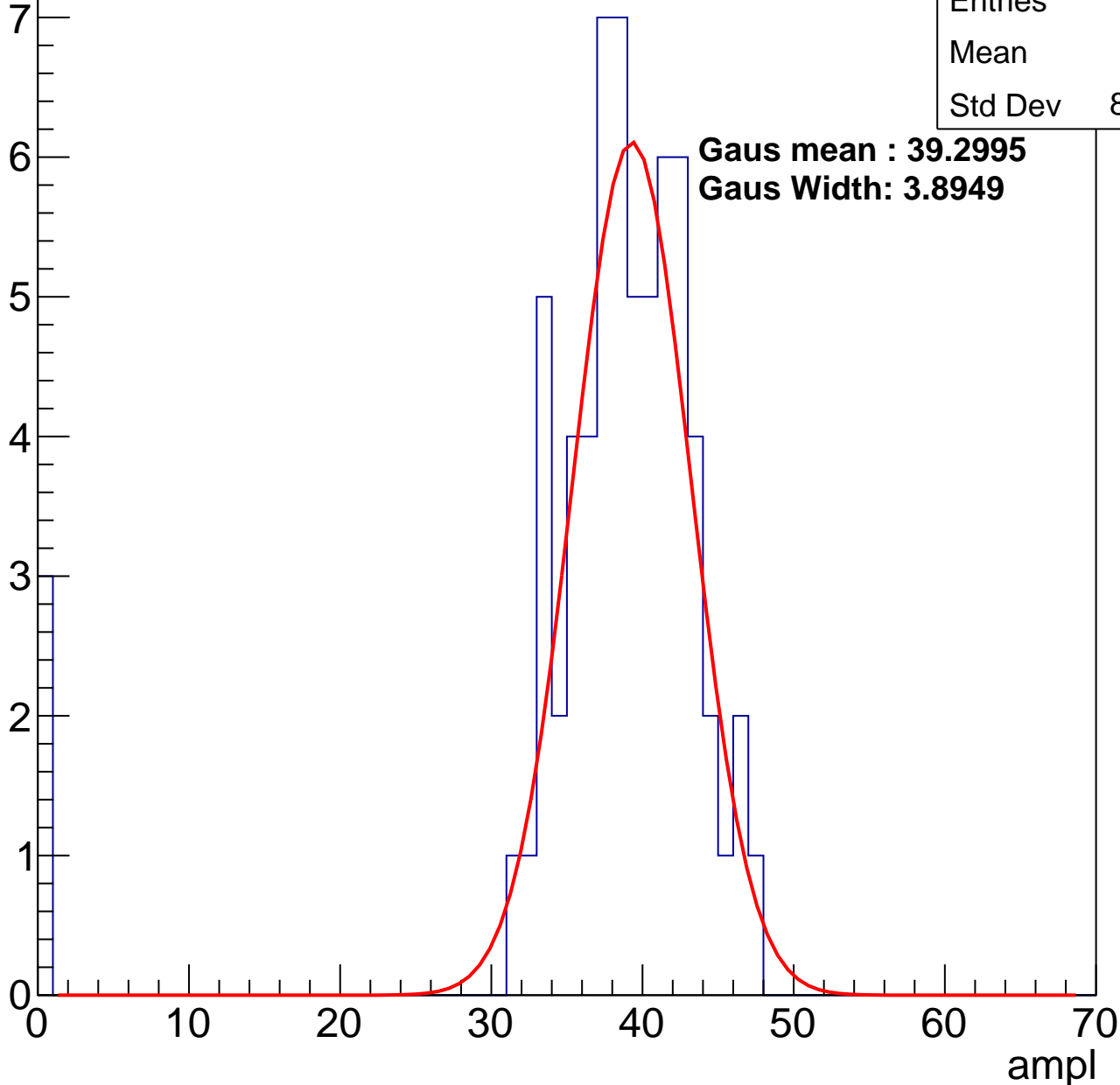
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	37
Std Dev	8.852

Gaus mean : 39.2995

Gaus Width: 3.8949

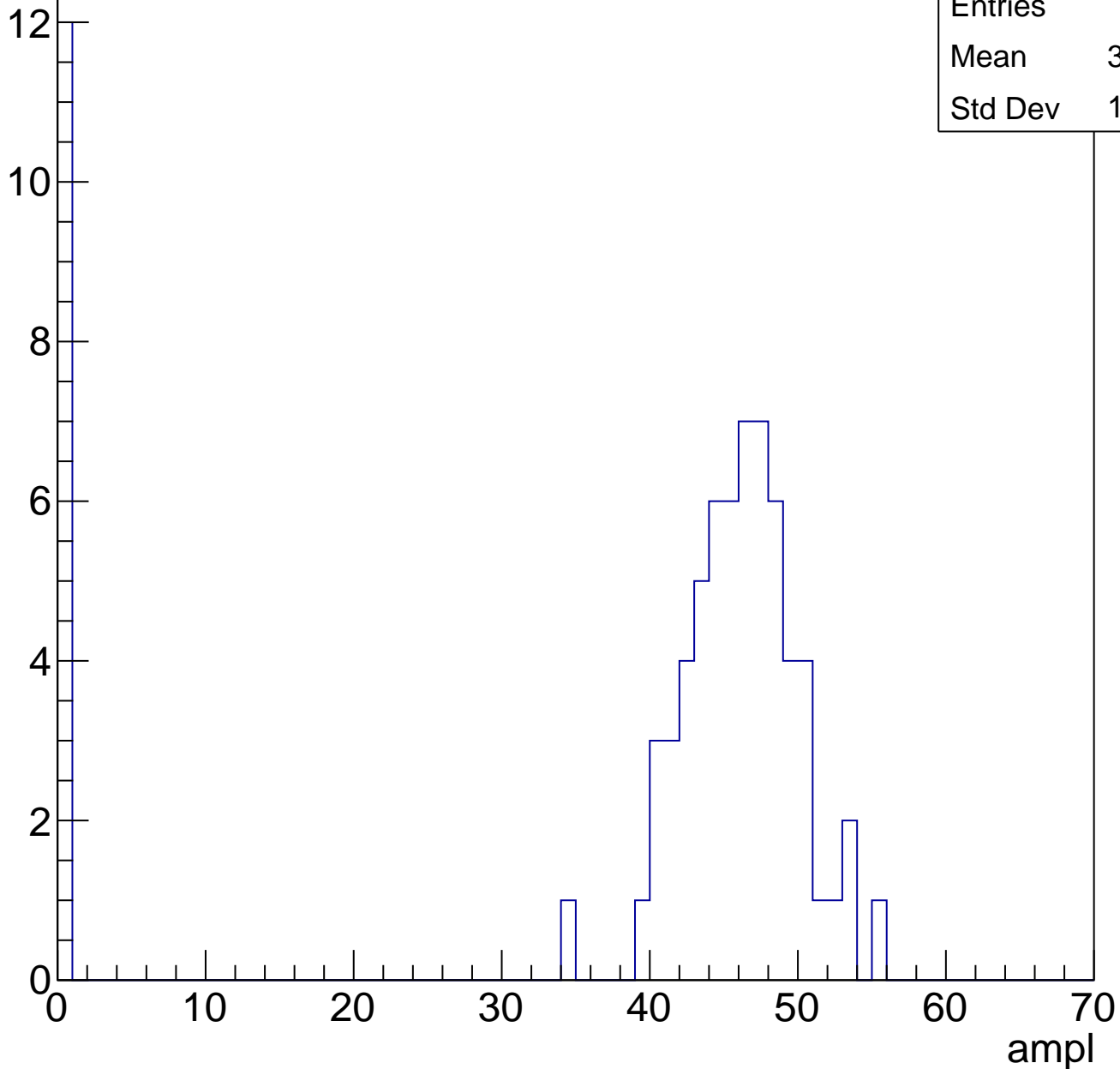


B1L103S, U21-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	38.27
Std Dev	17.19

Entry

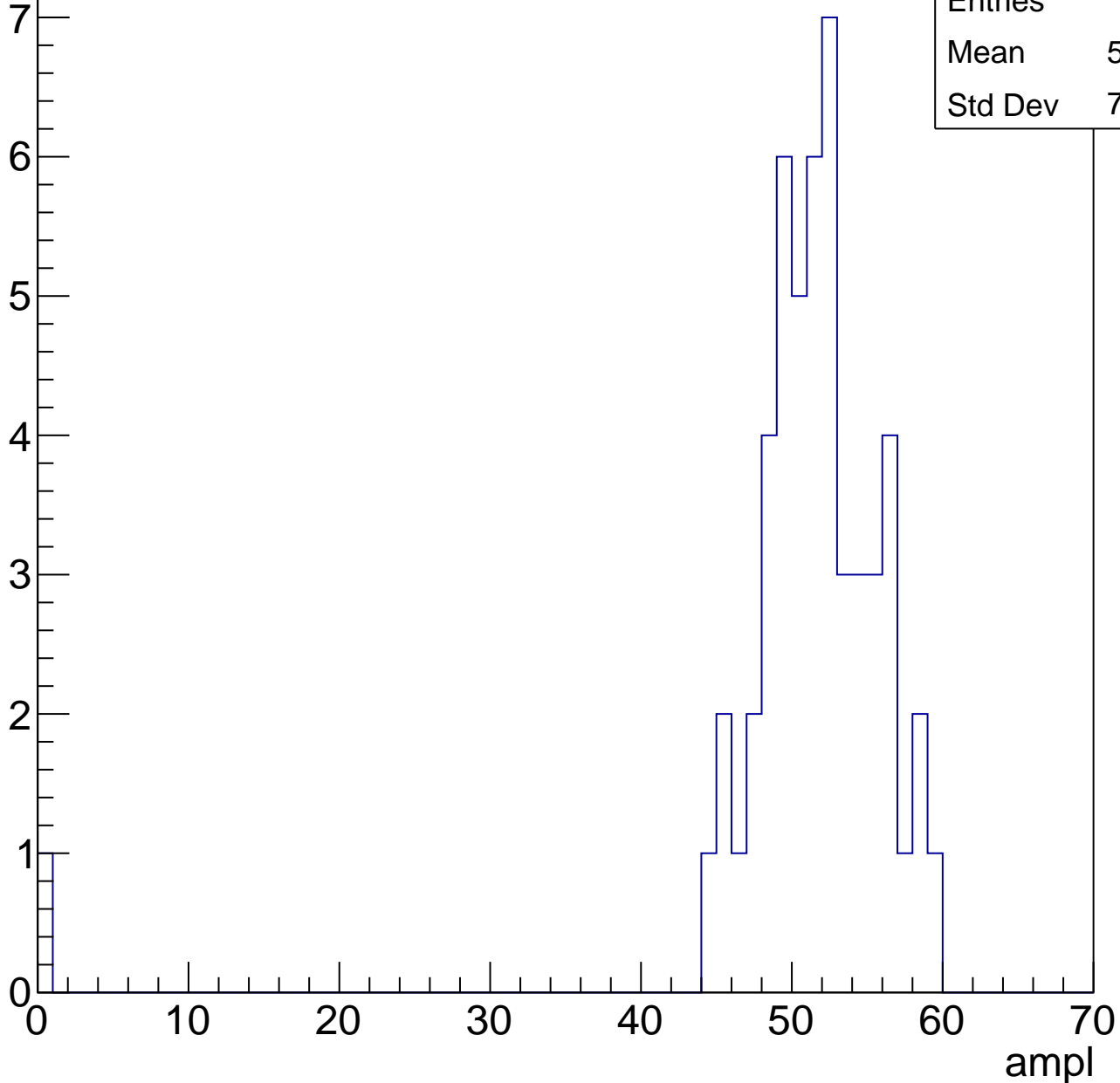


B1L103S, U21-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	50.42
Std Dev	7.868

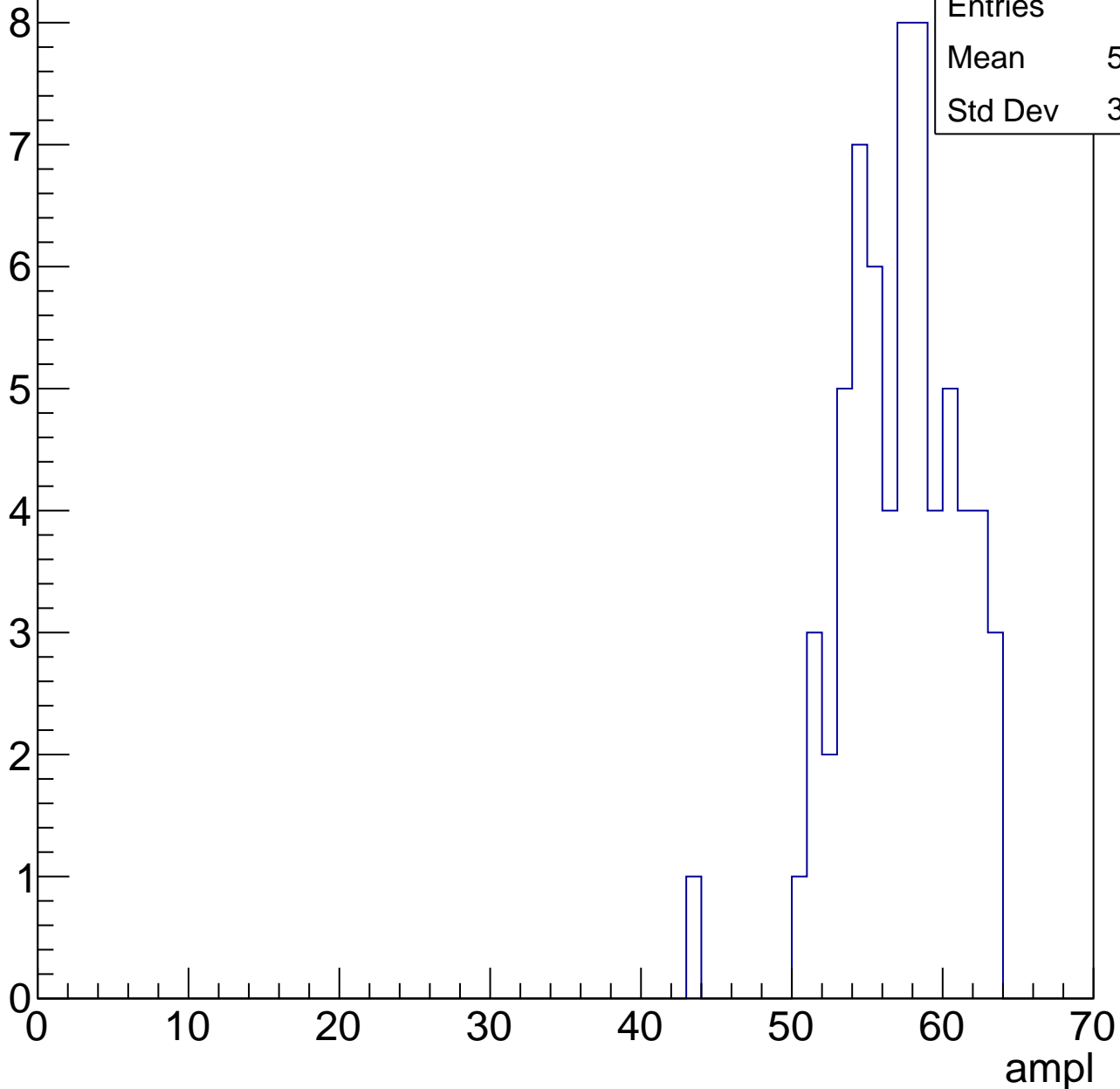


B1L103S, U21-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	56.68
Std Dev	3.738

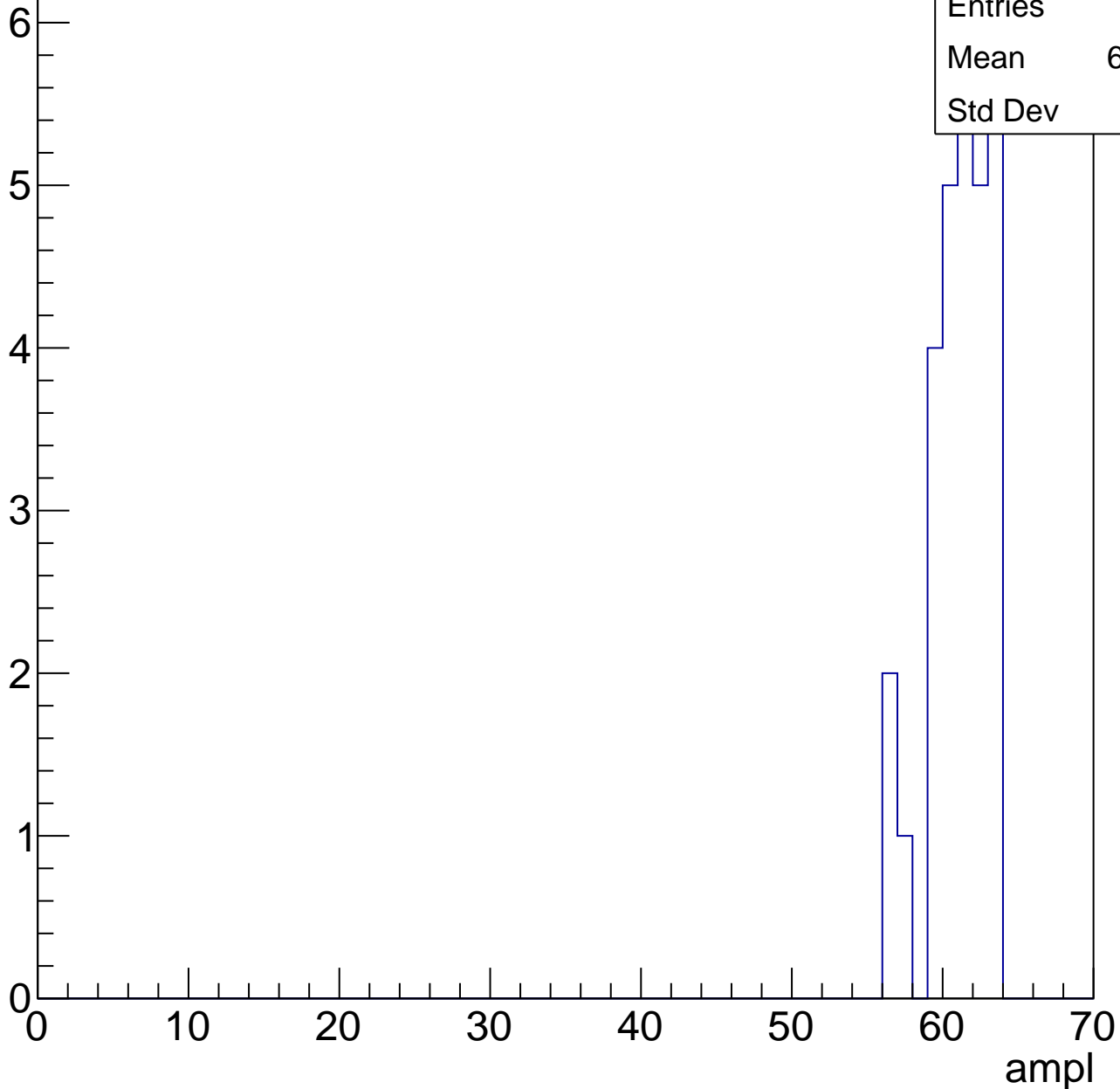


B1L103S, U21-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	60.66
Std Dev	1.97

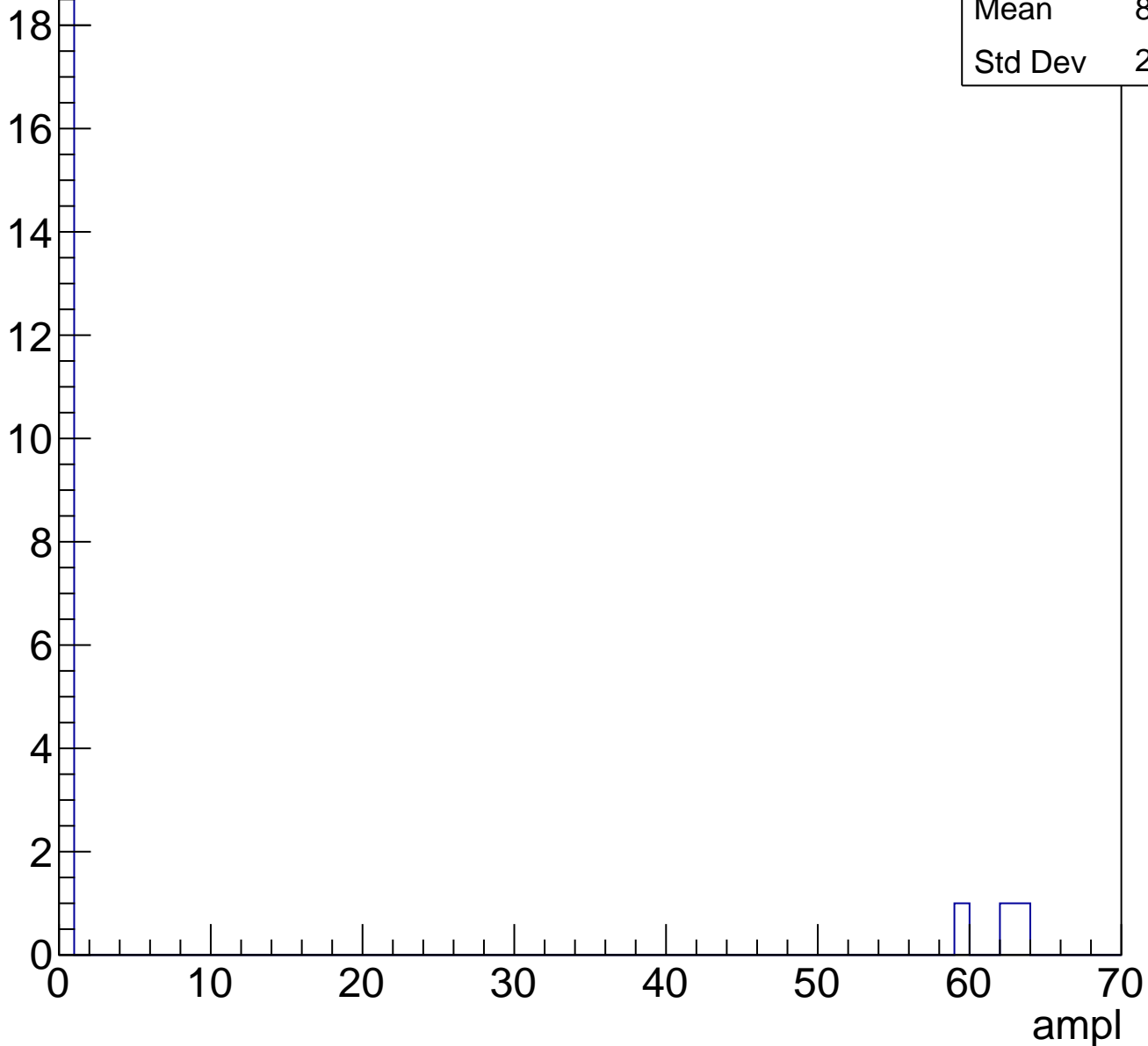


B1L103S, U21-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.364
Std Dev	21.06

Entry



B1L103S, U21-ch109, adc0

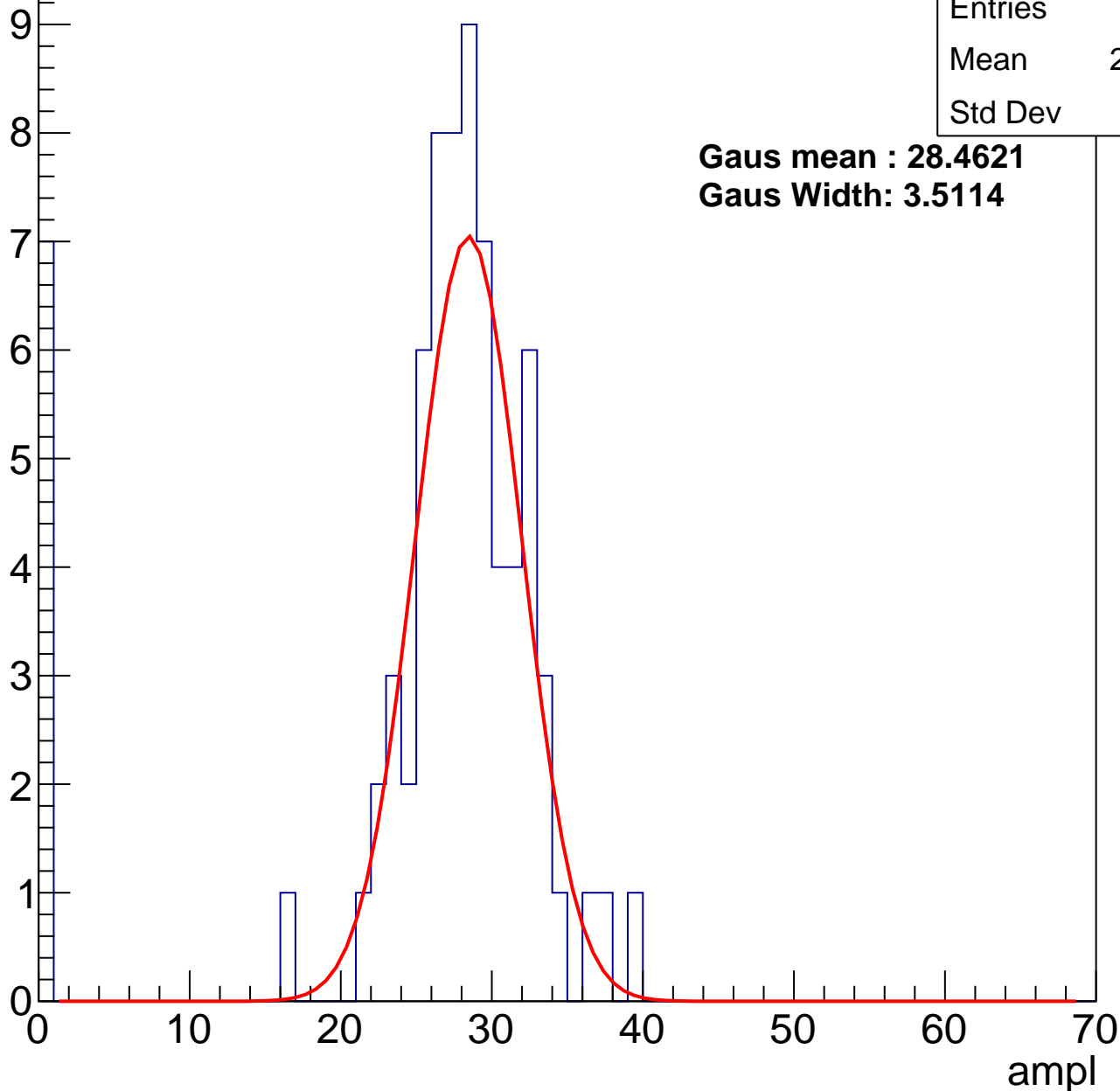
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	25.44
Std Dev	8.94

Gaus mean : 28.4621

Gaus Width: 3.5114



B1L103S, U21-ch109, adc1

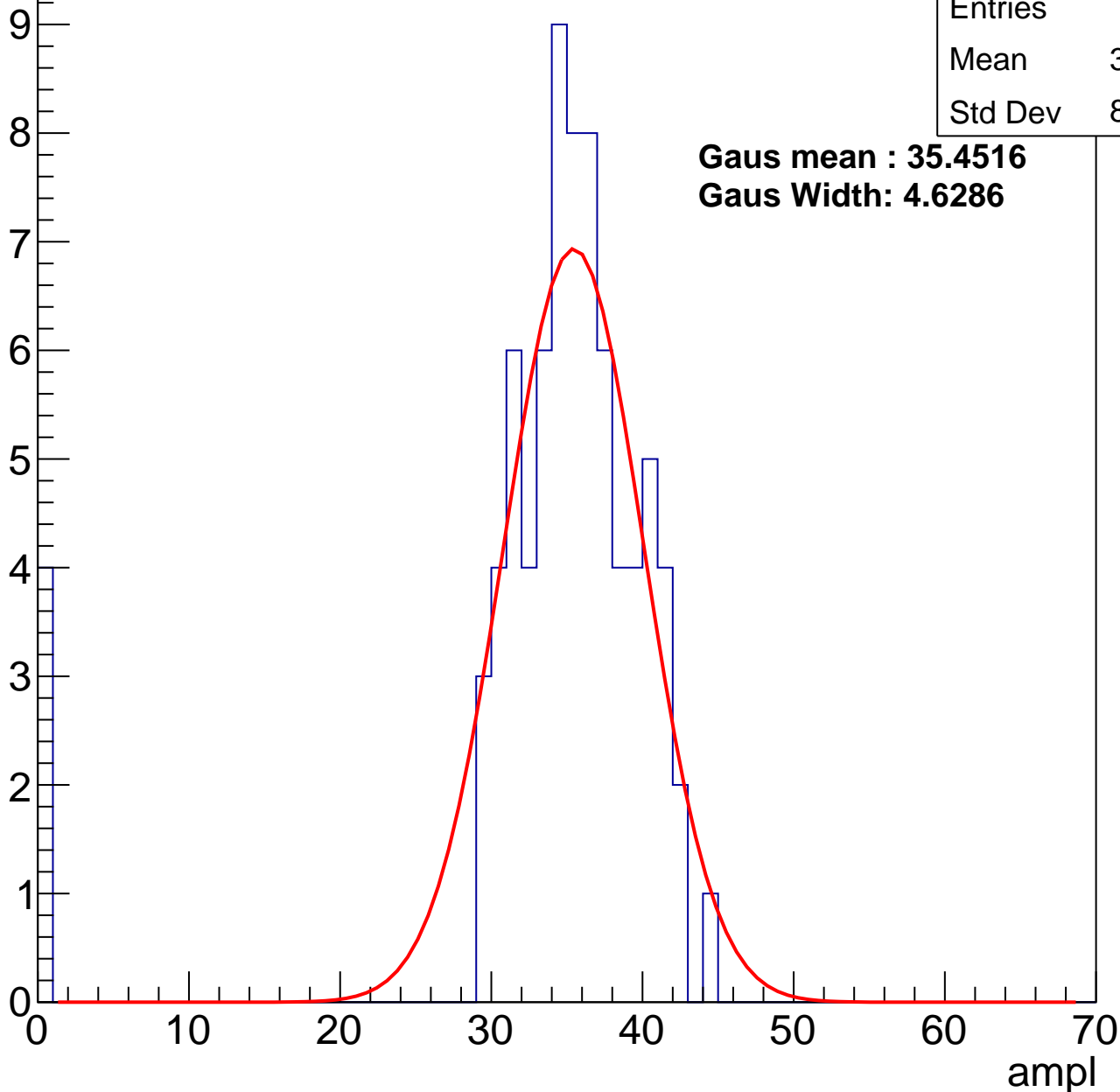
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	33.53
Std Dev	8.536

Gaus mean : 35.4516

Gaus Width: 4.6286



B1L103S, U21-ch109, adc2

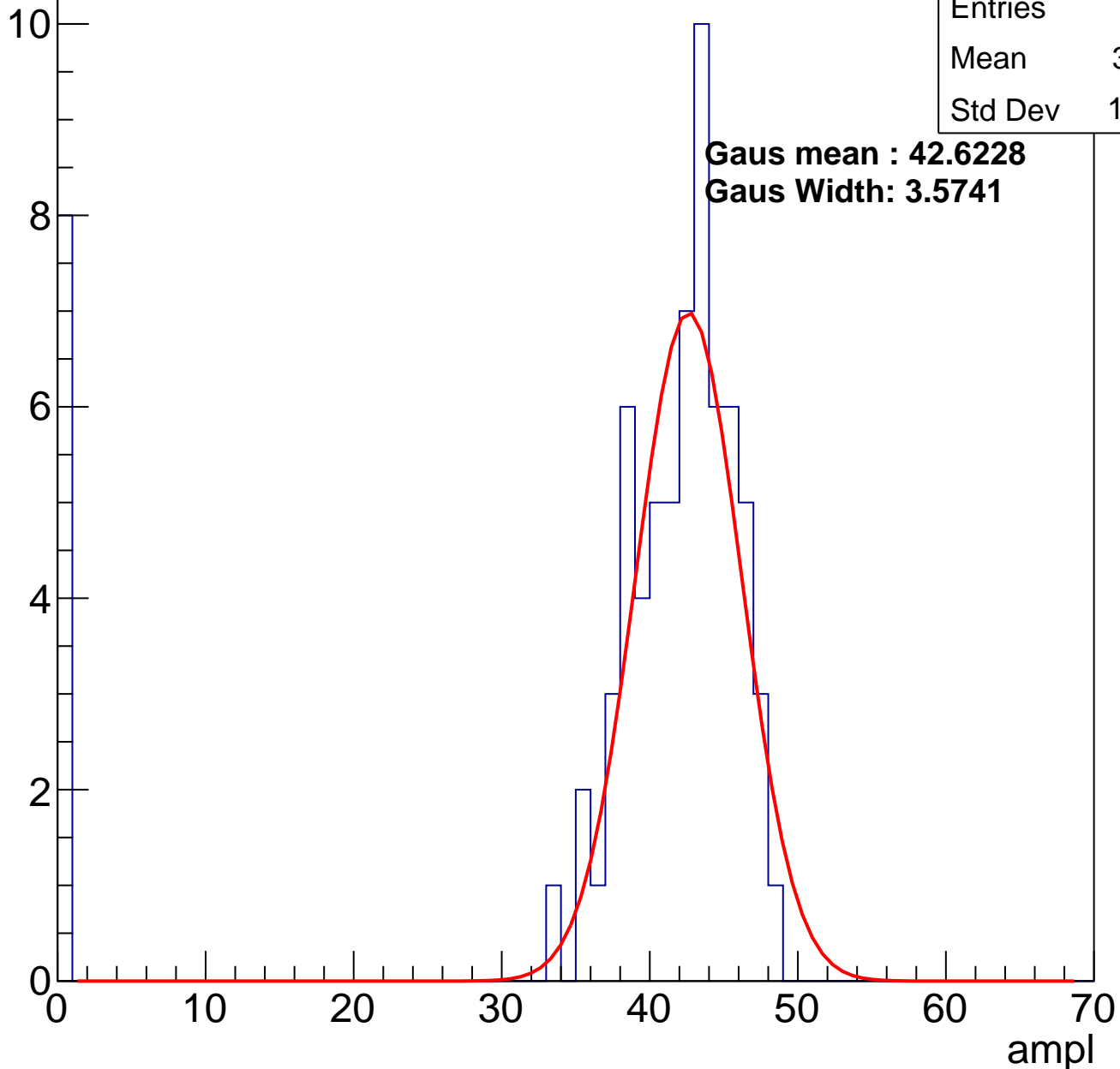
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	37.21
Std Dev	13.43

Gaus mean : 42.6228

Gaus Width: 3.5741

Entry

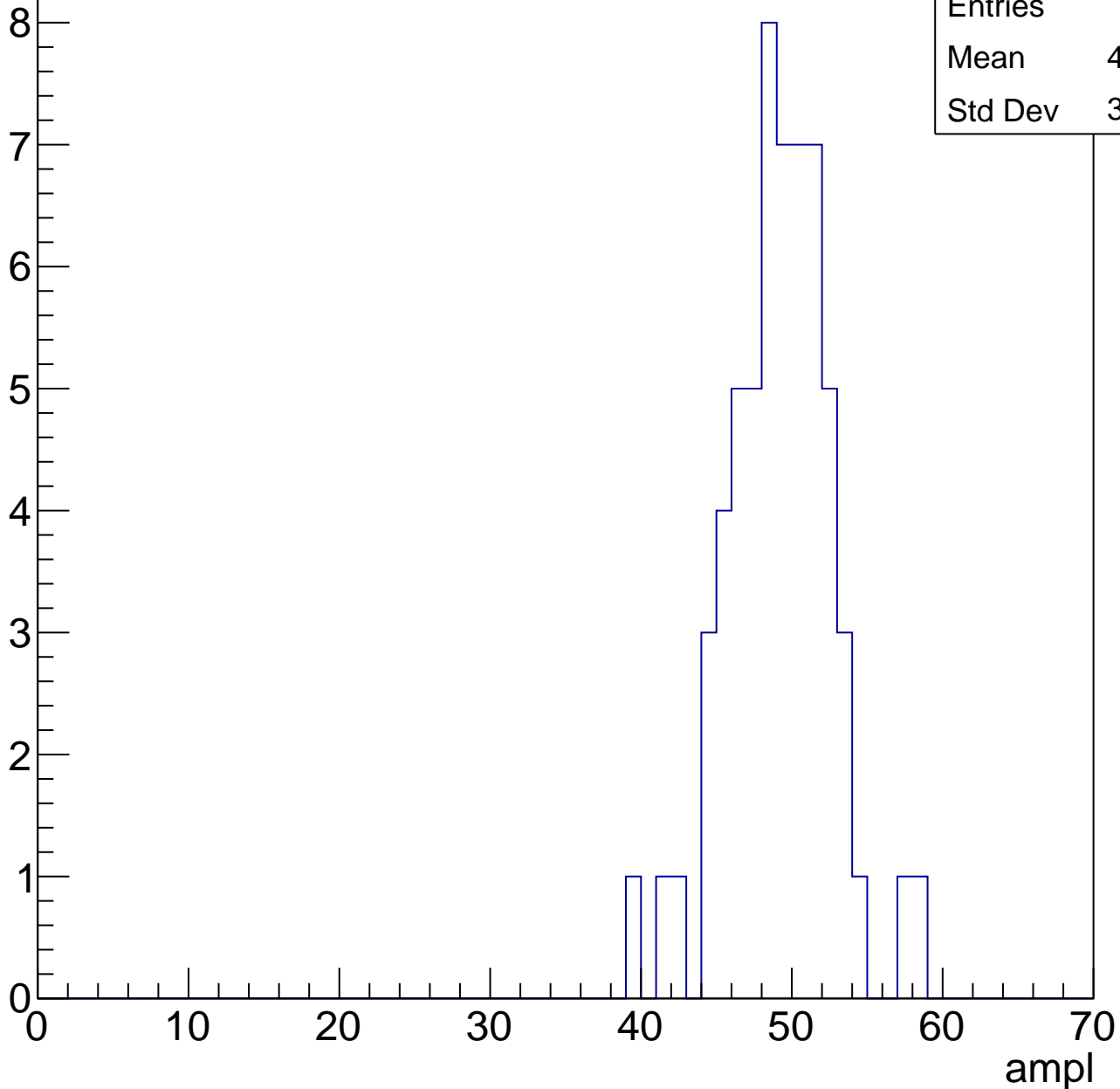


B1L103S, U21-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	48.68
Std Dev	3.457

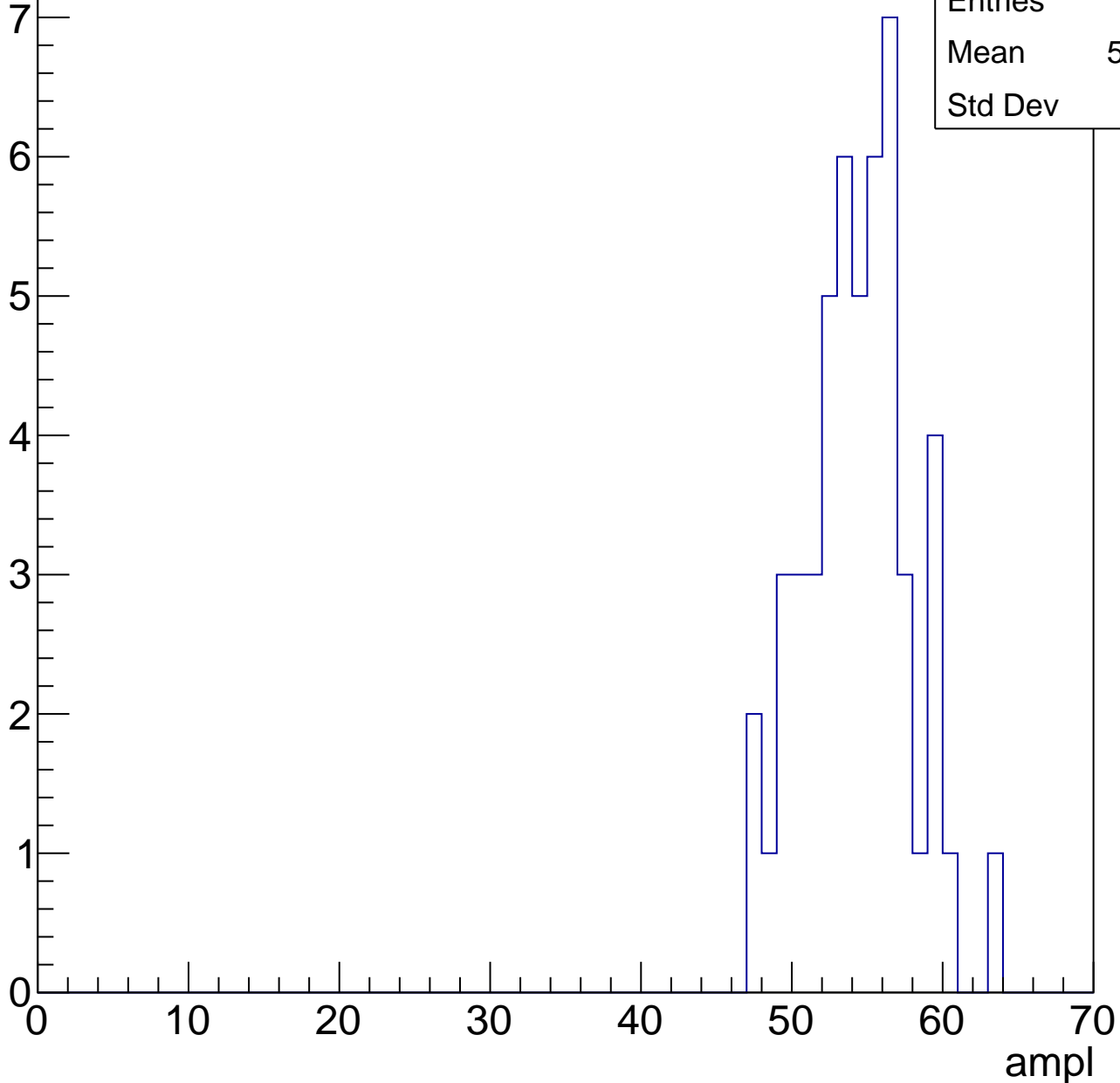


B1L103S, U21-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

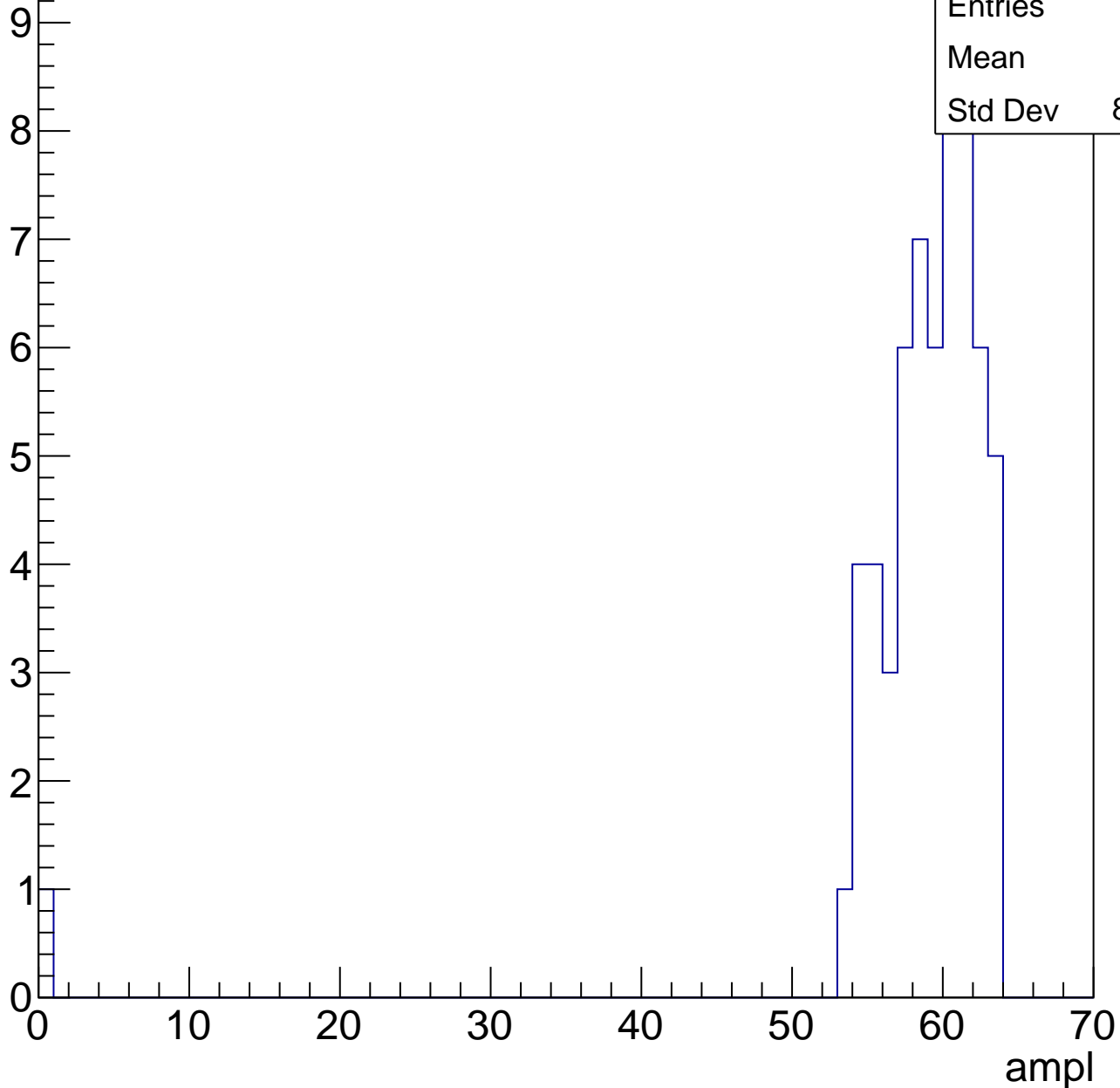
Entries	51
Mean	53.92
Std Dev	3.44



B1L103S, U21-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

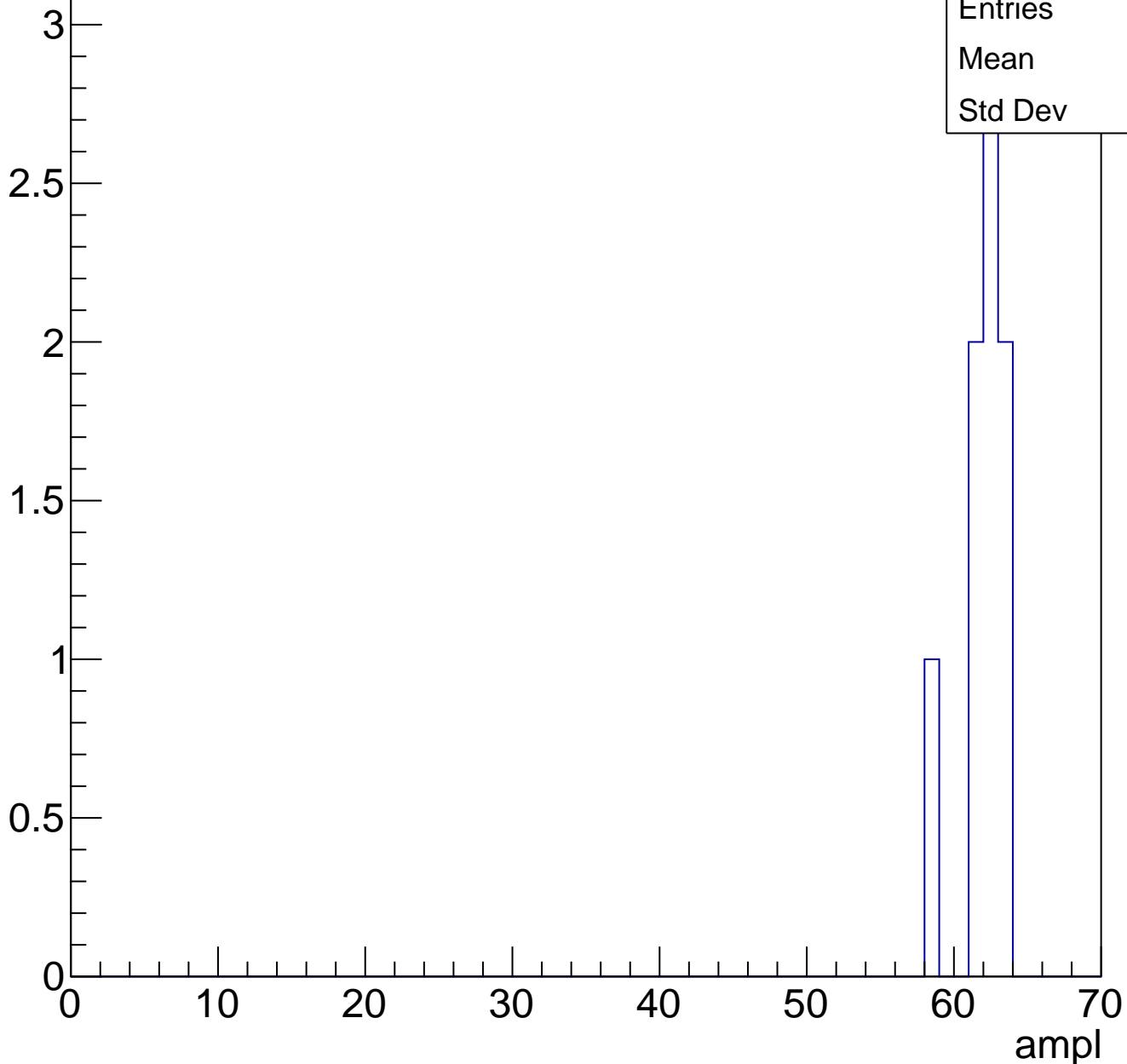
Entry



B1L103S, U21-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

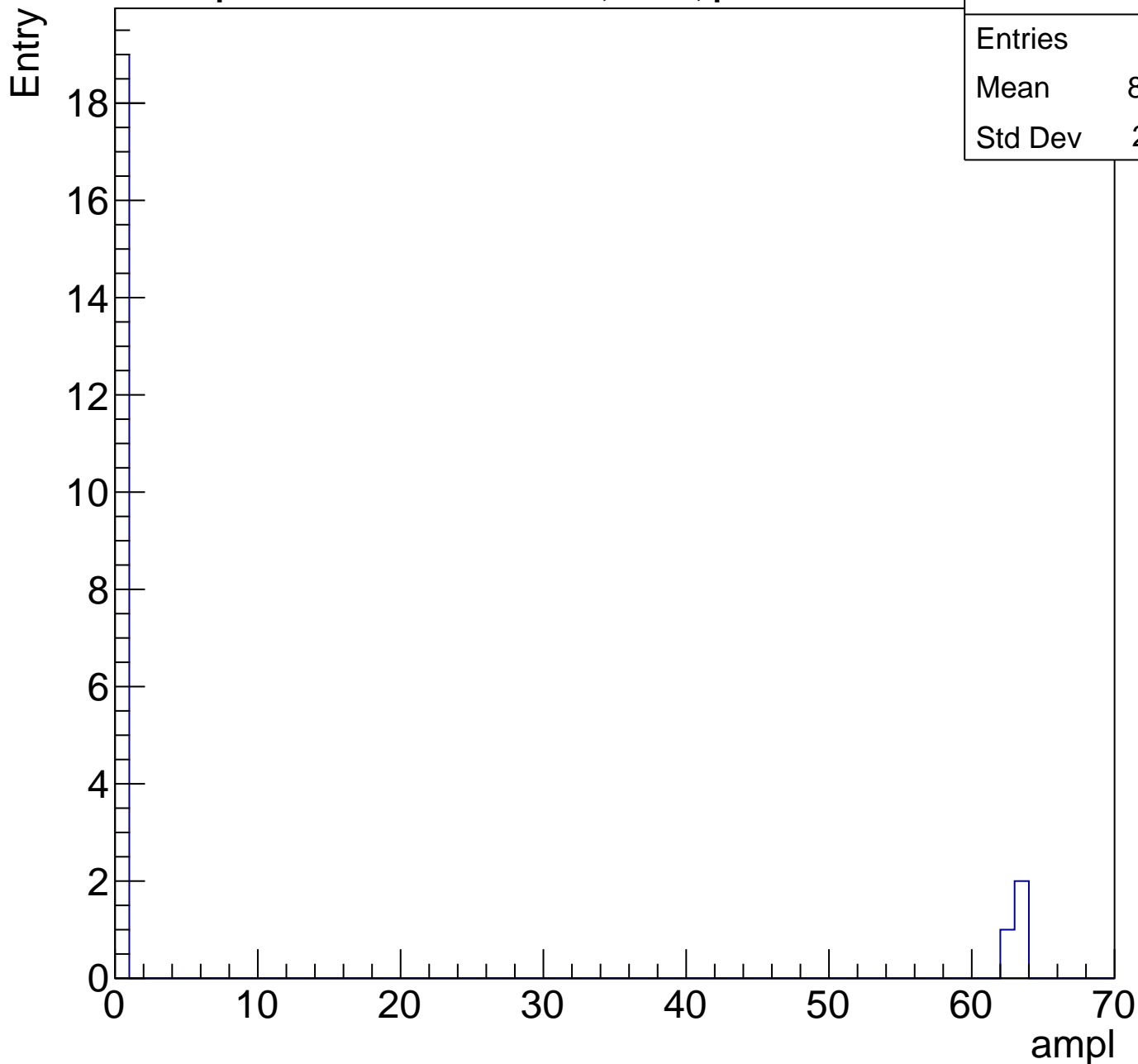
Entry



B1L103S, U21-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51



B1L103S, U21-ch110, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	110
Mean	20.57
Std Dev	11.72

Gaus mean : 27.2892

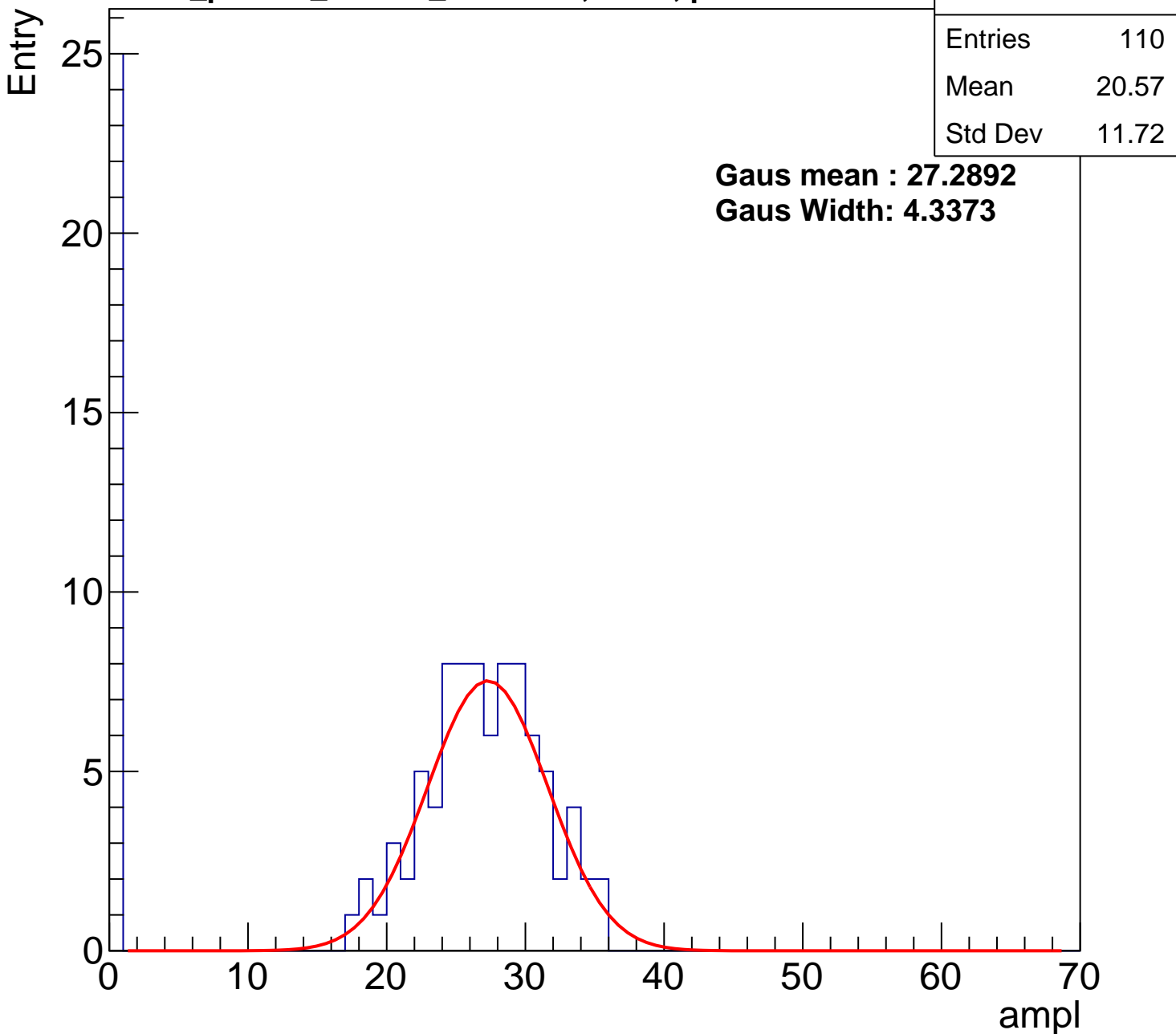
Gaus Width: 4.3373

Entry

25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch110, adc1

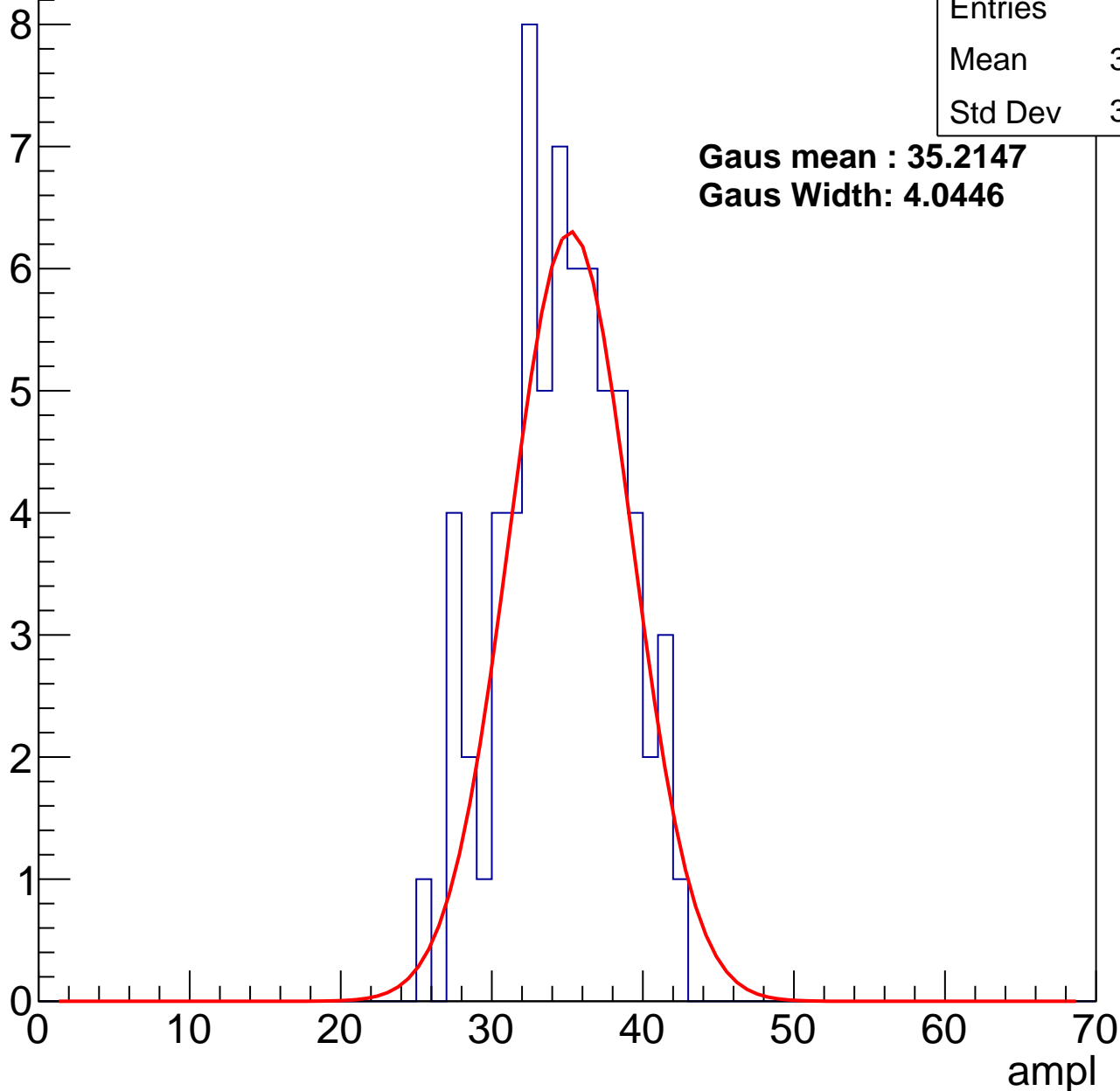
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.16
Std Dev	3.924

Gaus mean : 35.2147

Gaus Width: 4.0446



B1L103S, U21-ch110, adc2

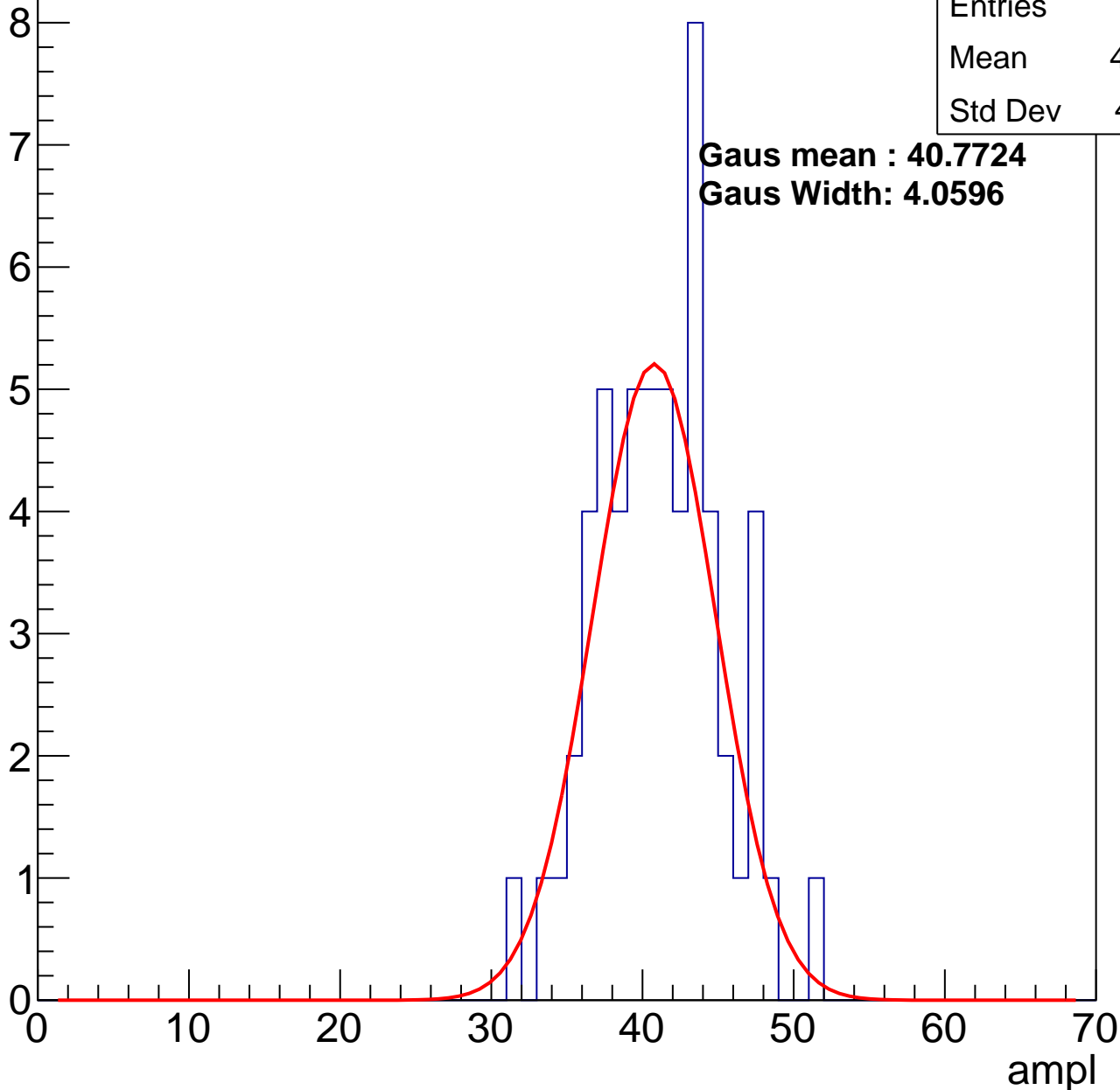
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	40.69
Std Dev	4.031

Gaus mean : 40.7724

Gaus Width: 4.0596

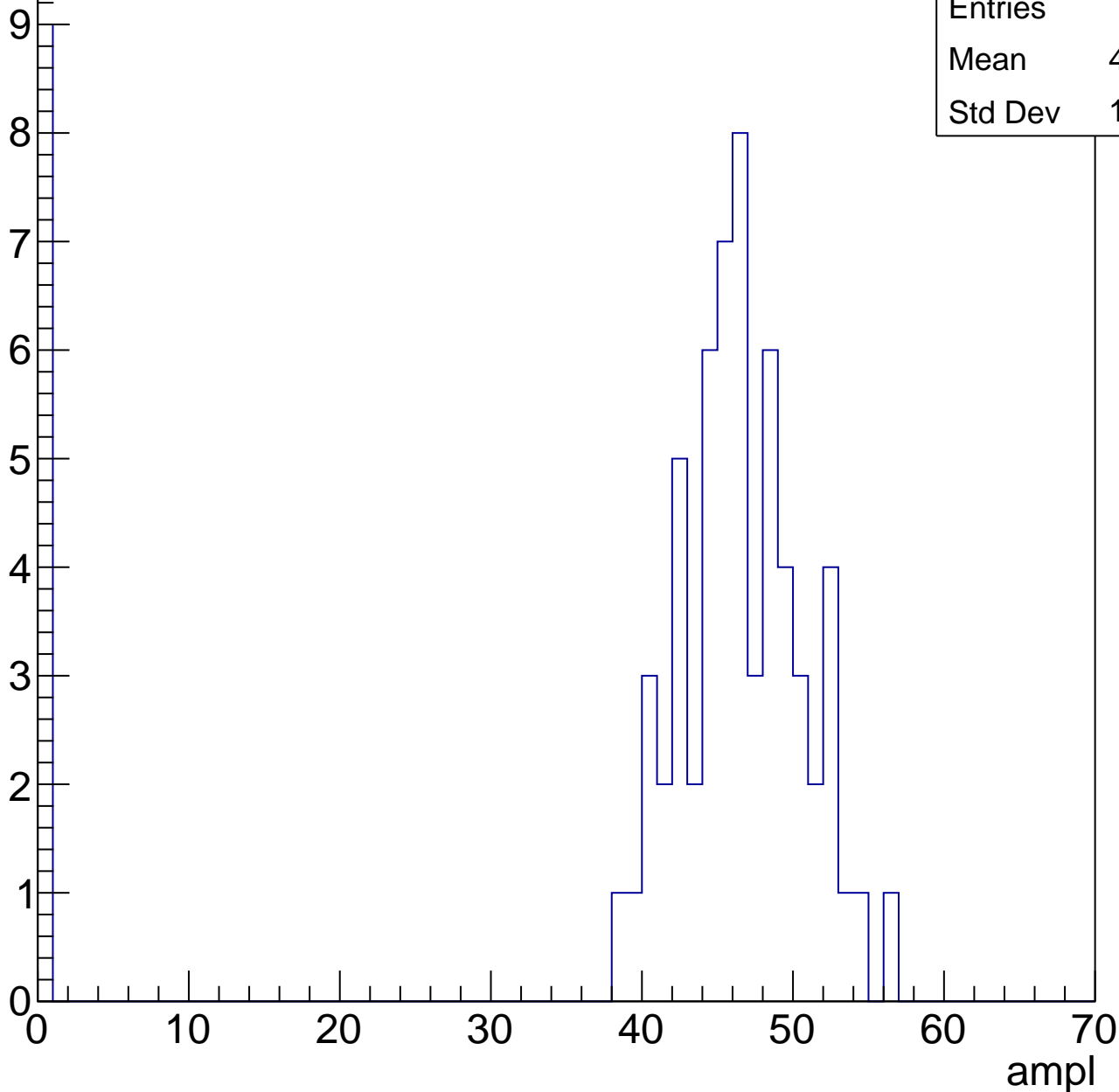


B1L103S, U21-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.14
Std Dev	15.97

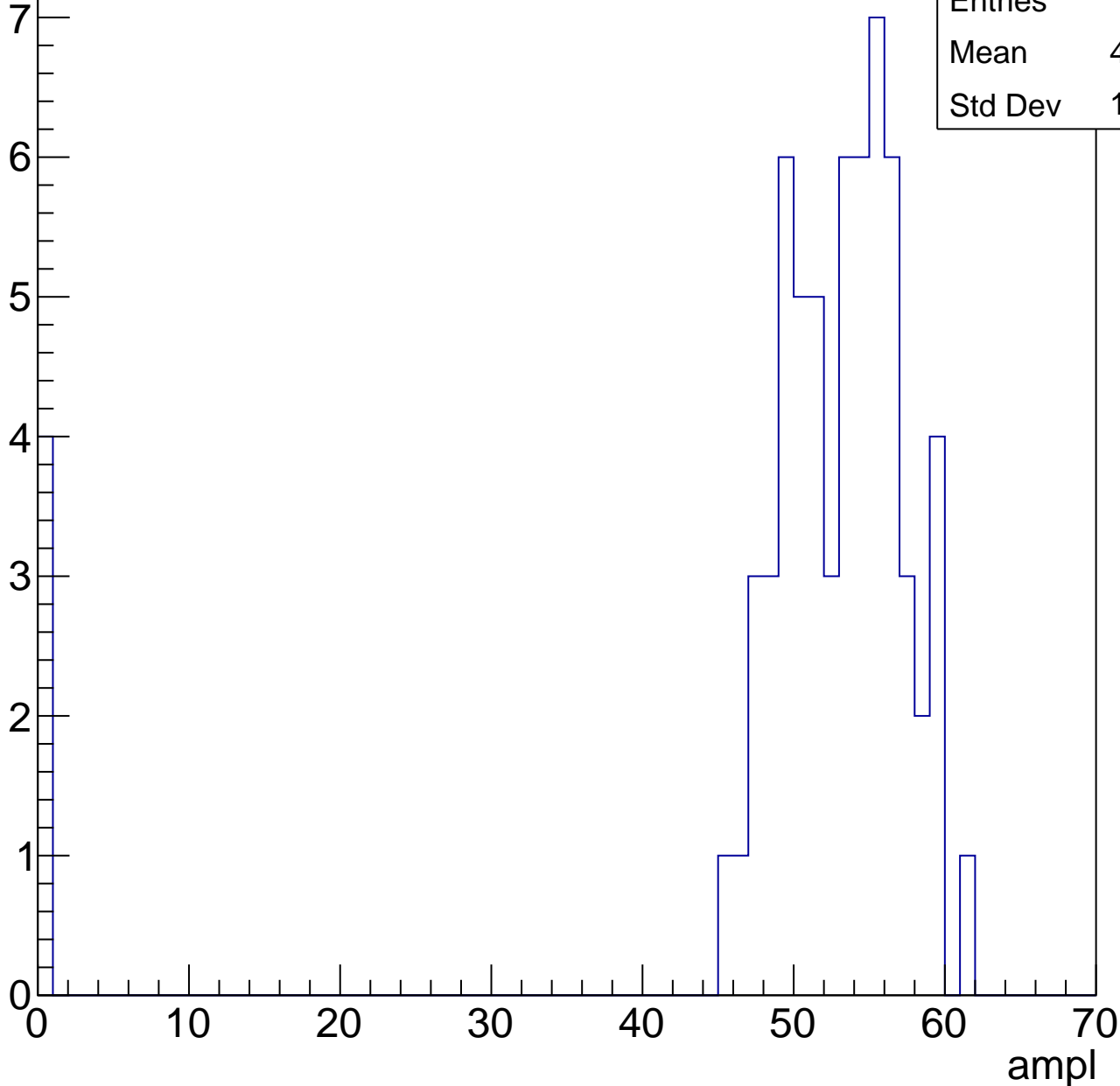


B1L103S, U21-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	49.67
Std Dev	13.12

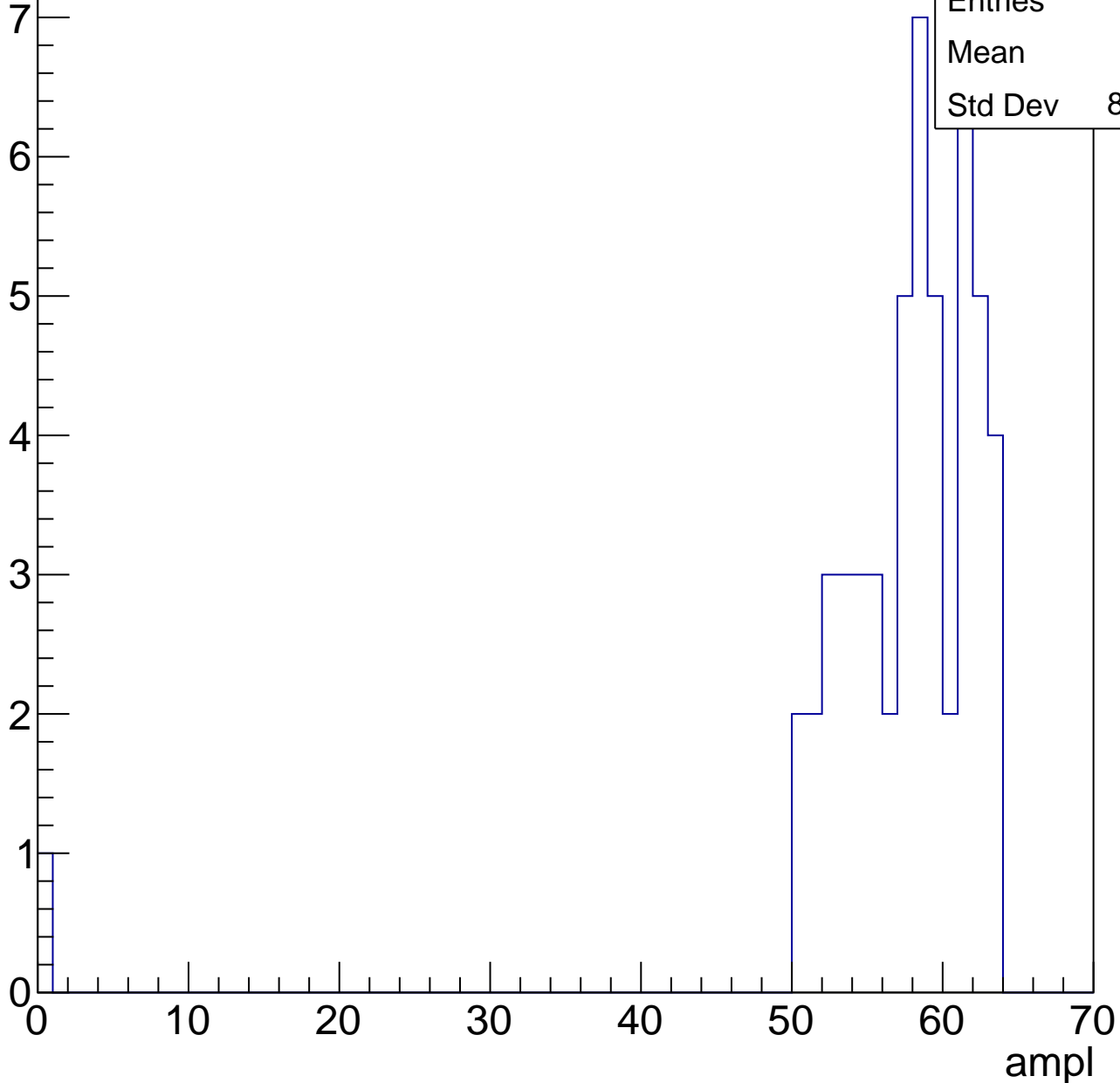


B1L103S, U21-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.5
Std Dev	8.602

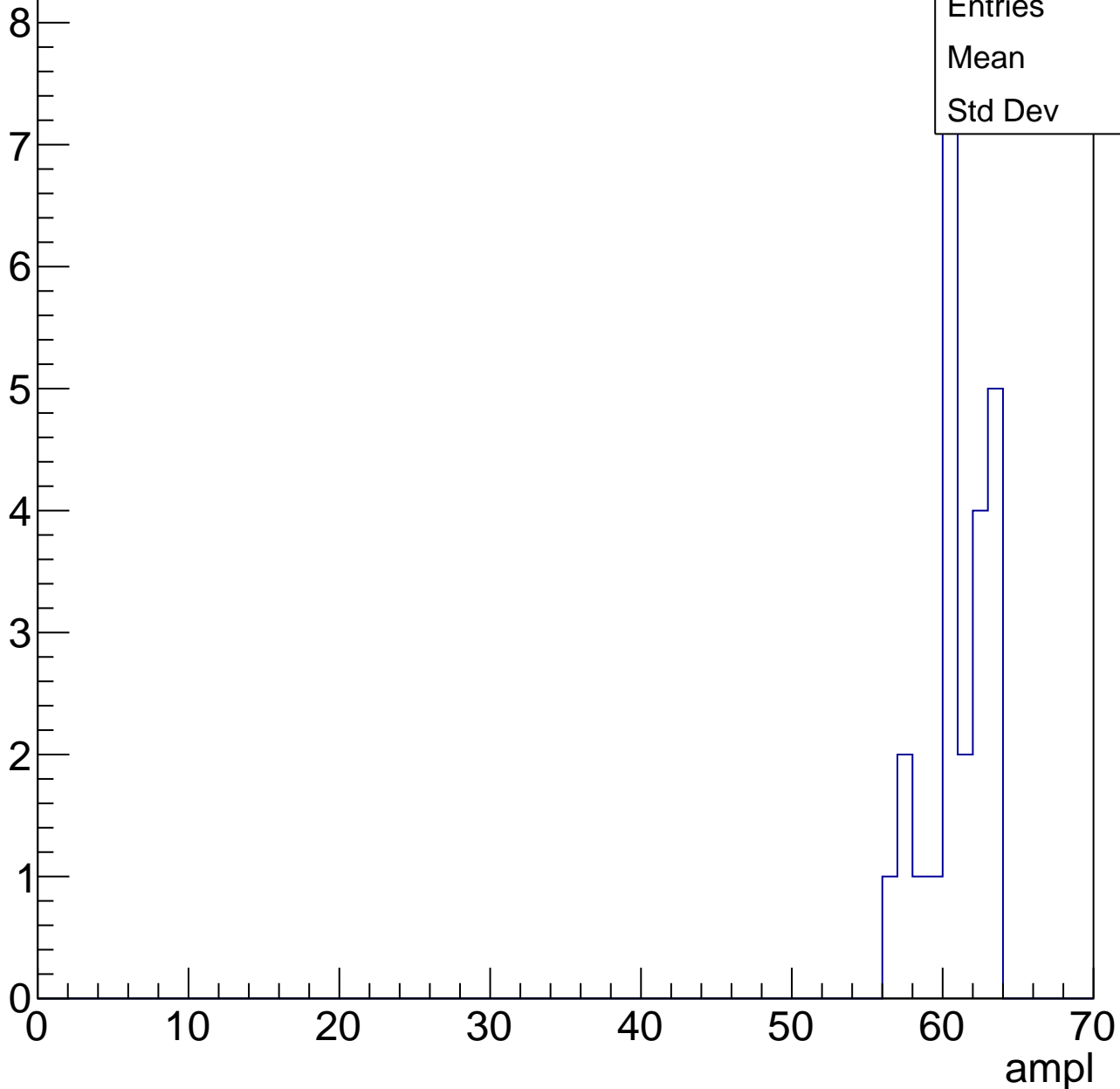


B1L103S, U21-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

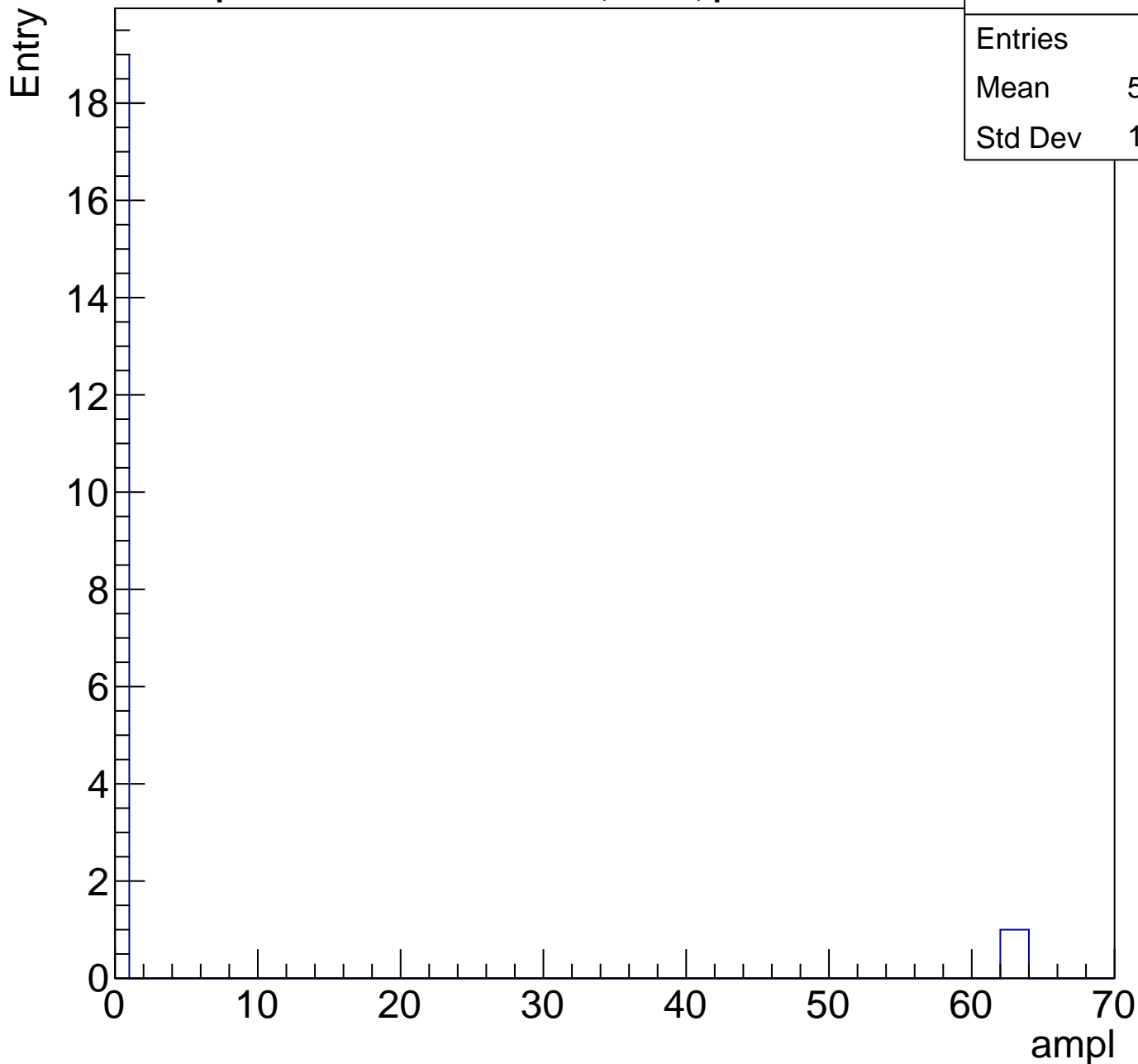
Entries	24
Mean	60.5
Std Dev	2



B1L103S, U21-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



B1L103S, U21-ch111, adc0

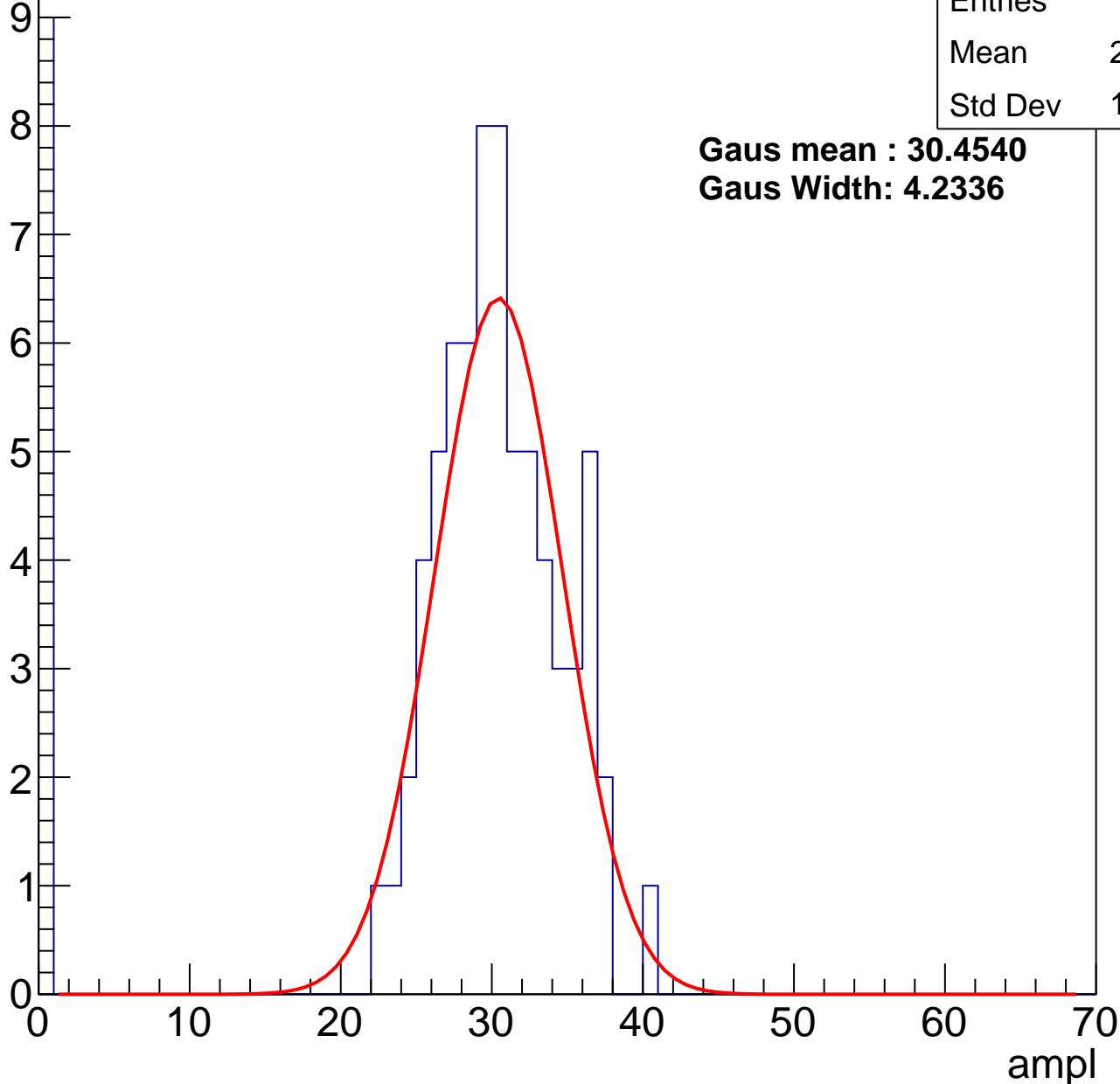
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	26.58
Std Dev	10.25

Gaus mean : 30.4540

Gaus Width: 4.2336



B1L103S, U21-ch111, adc1

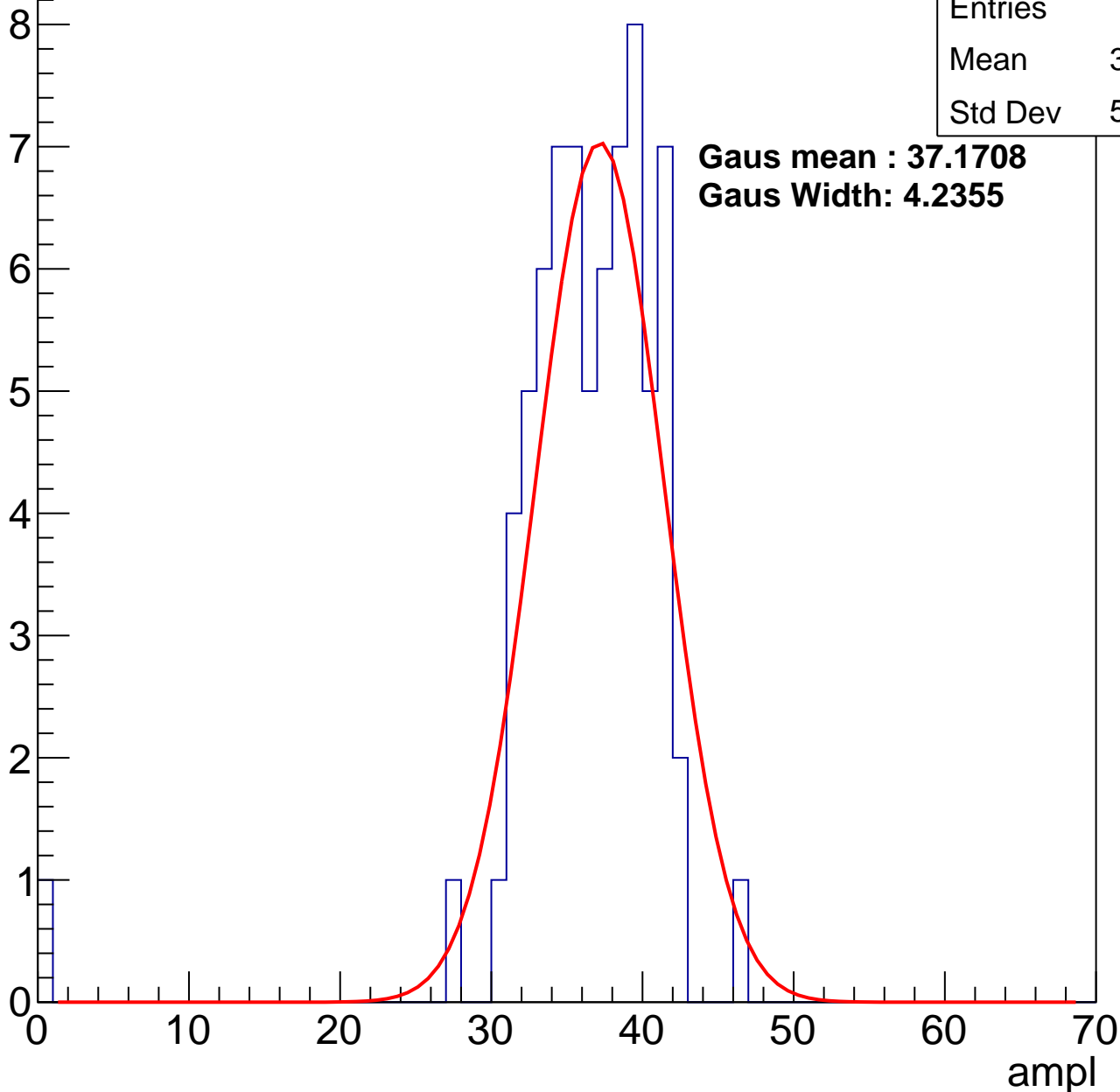
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.88
Std Dev	5.507

Gaus mean : 37.1708

Gaus Width: 4.2355



B1L103S, U21-ch111, adc2

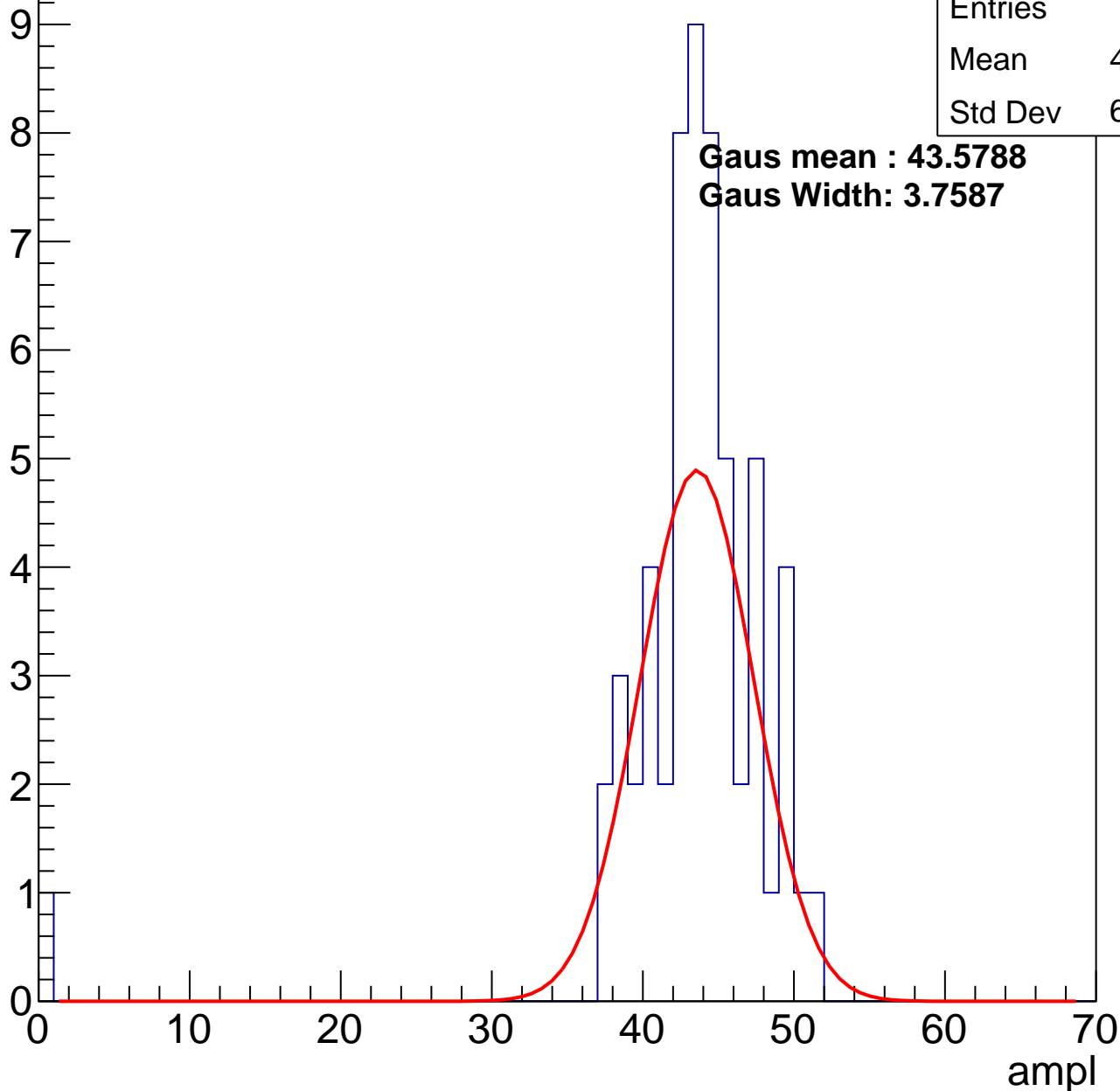
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	42.76
Std Dev	6.545

Gaus mean : 43.5788

Gaus Width: 3.7587



B1L103S, U21-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	44.32
Std Dev	16.64

Entry

10

8

6

4

2

0

0

10

20

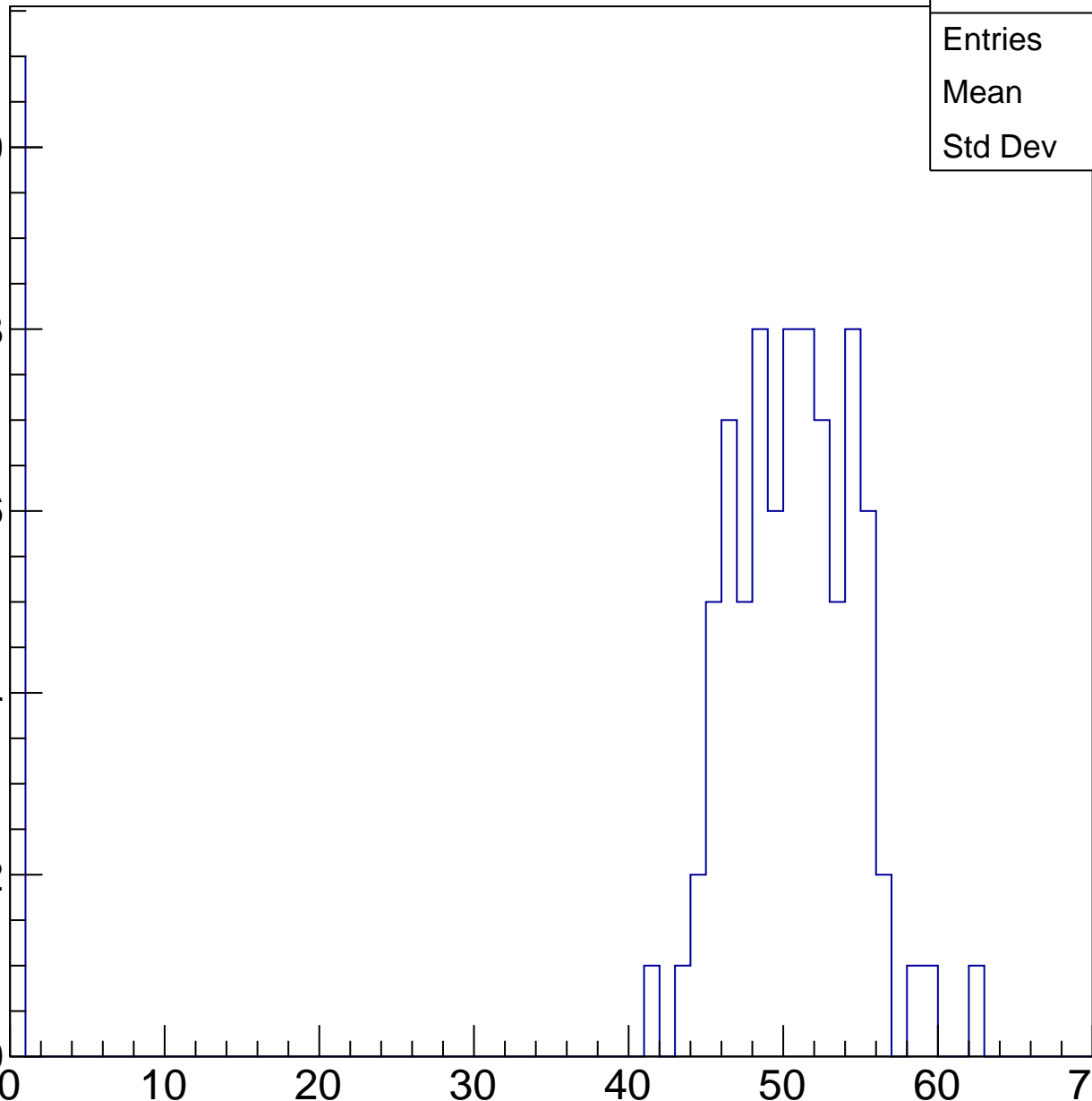
30

40

50

60

ampl

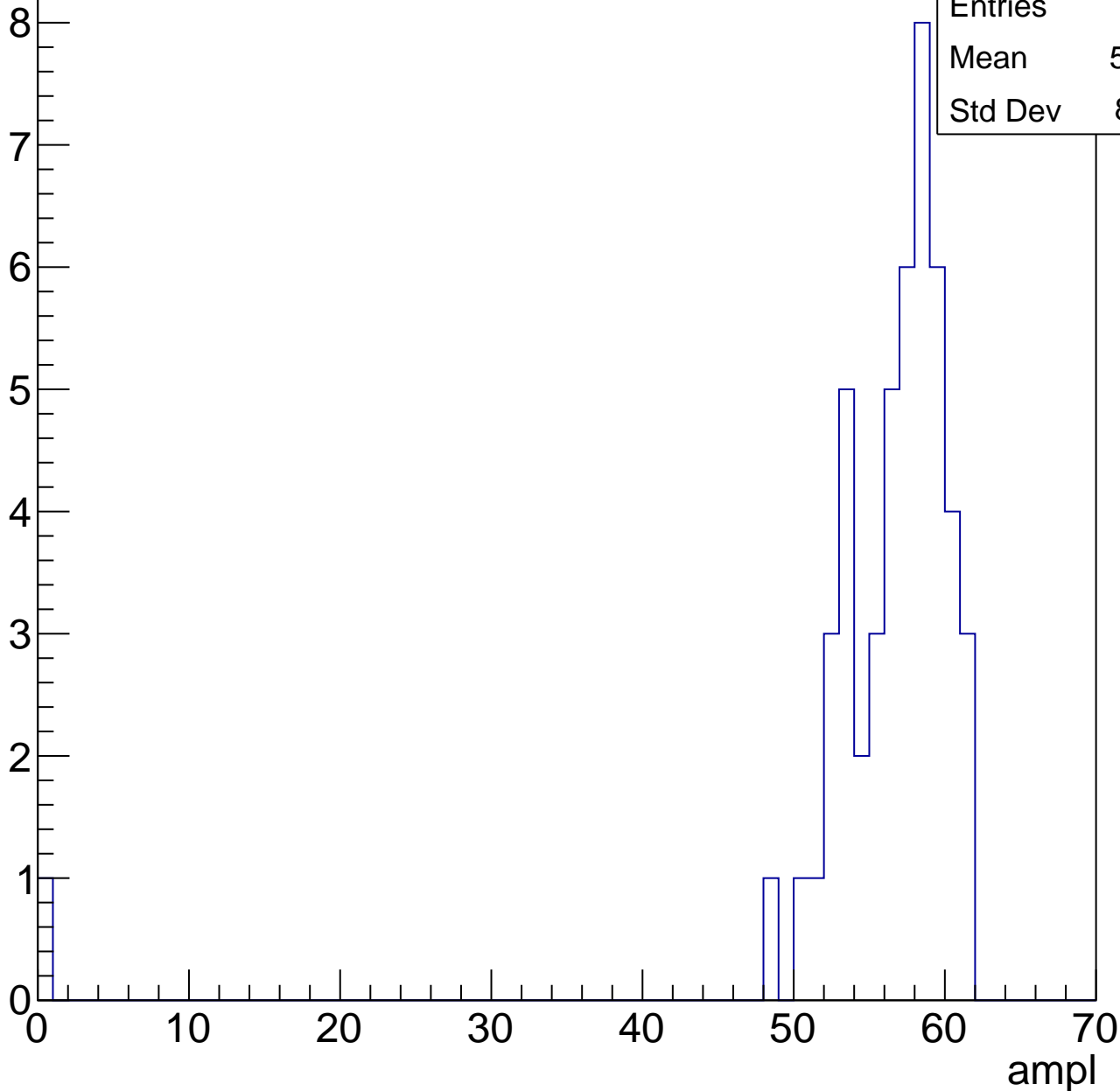


B1L103S, U21-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.22
Std Dev	8.531

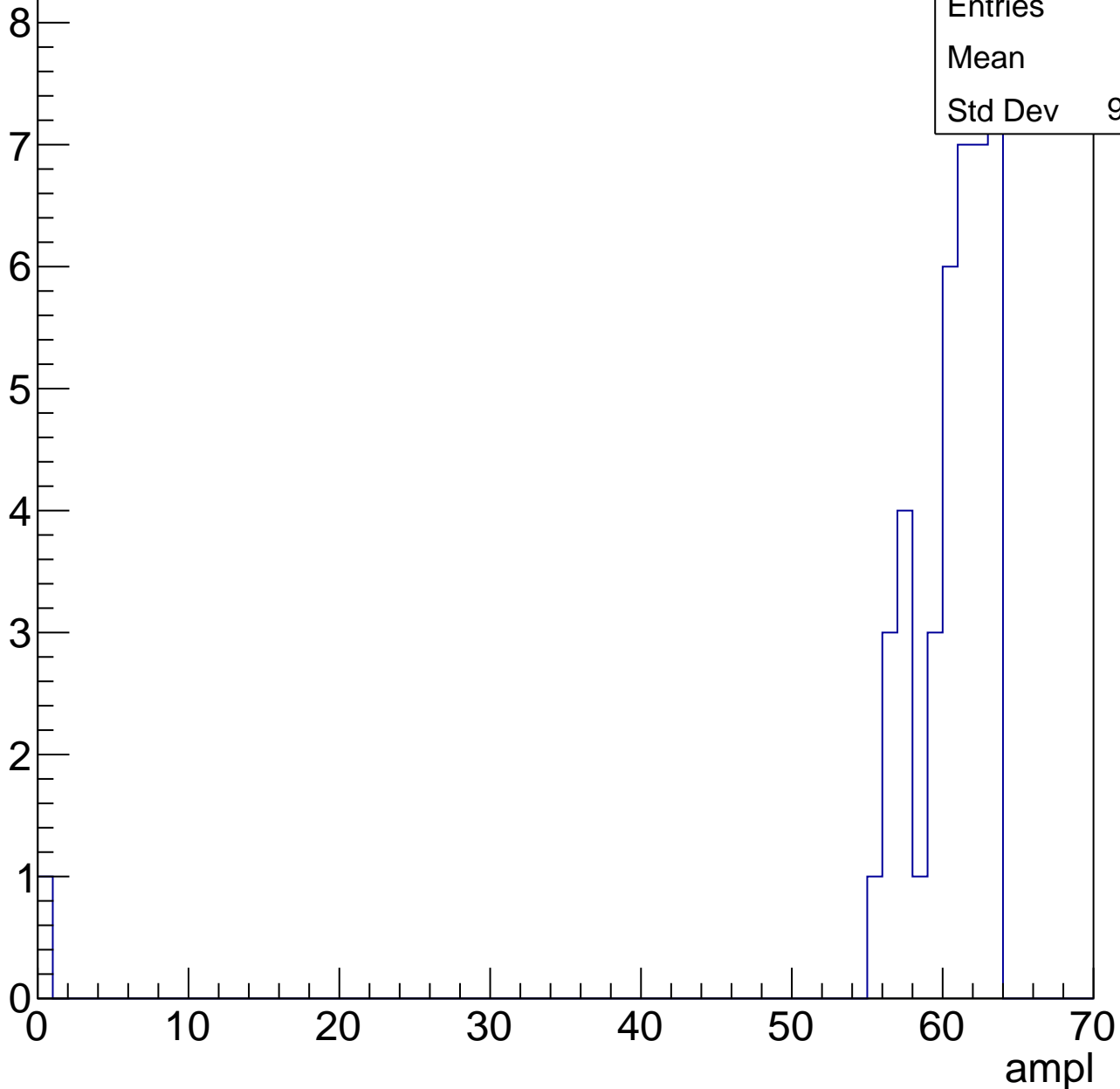


B1L103S, U21-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	58.8
Std Dev	9.582



B1L103S, U21-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

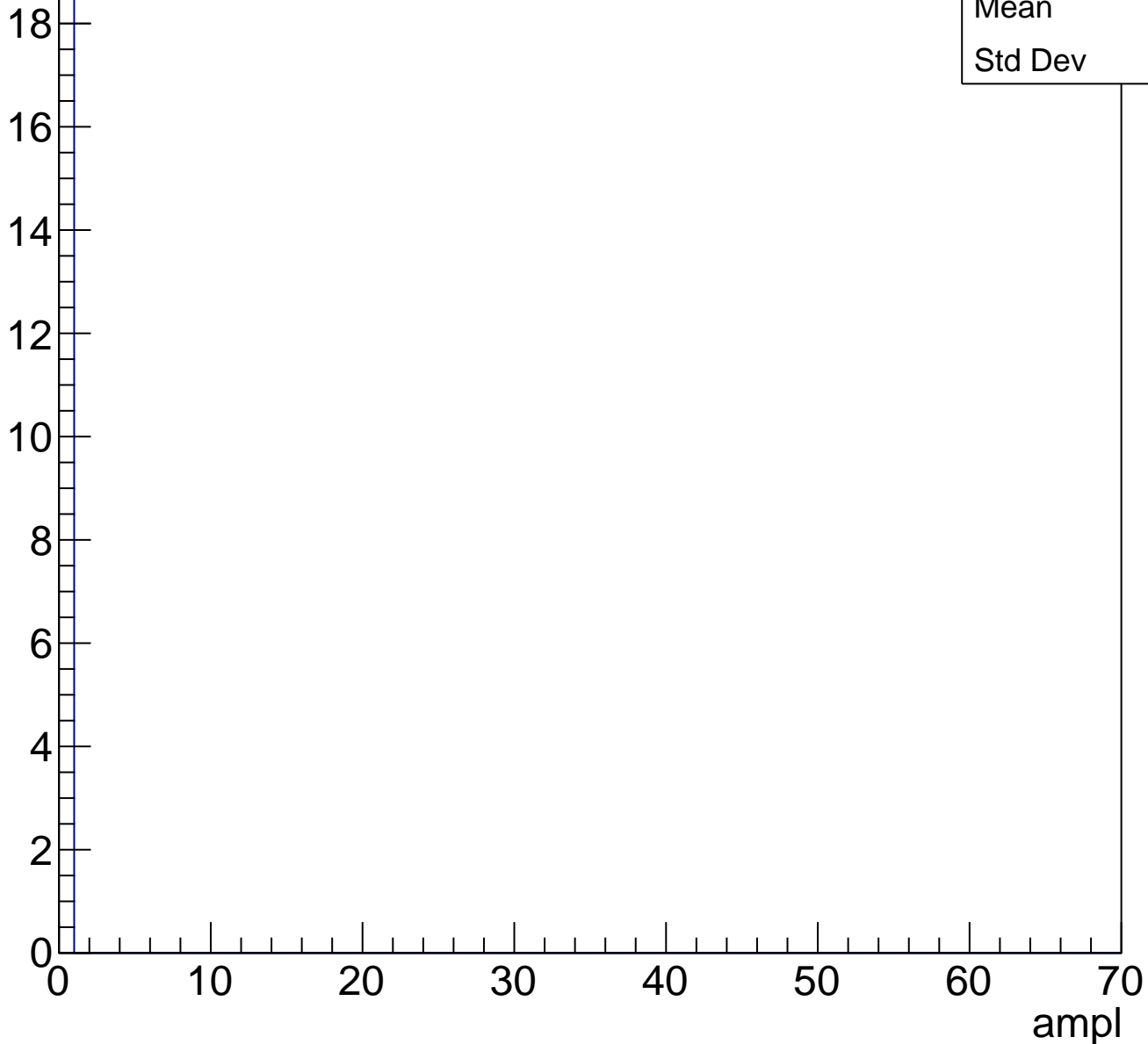
ampl

B1L103S, U21-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



B1L103S, U21-ch112, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	22.54
Std Dev	12.24

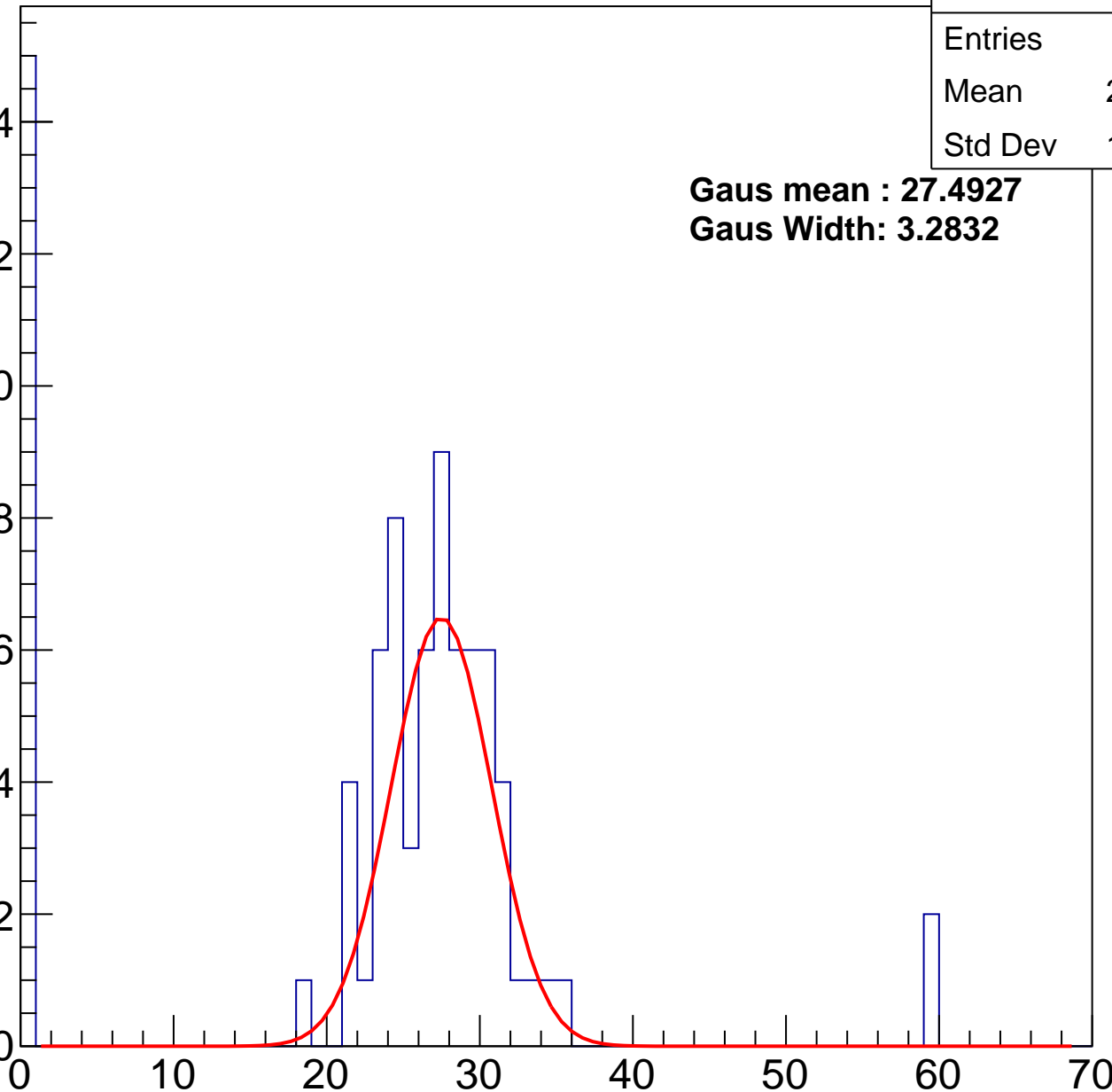
Gaus mean : 27.4927

Gaus Width: 3.2832

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U21-ch112, adc1

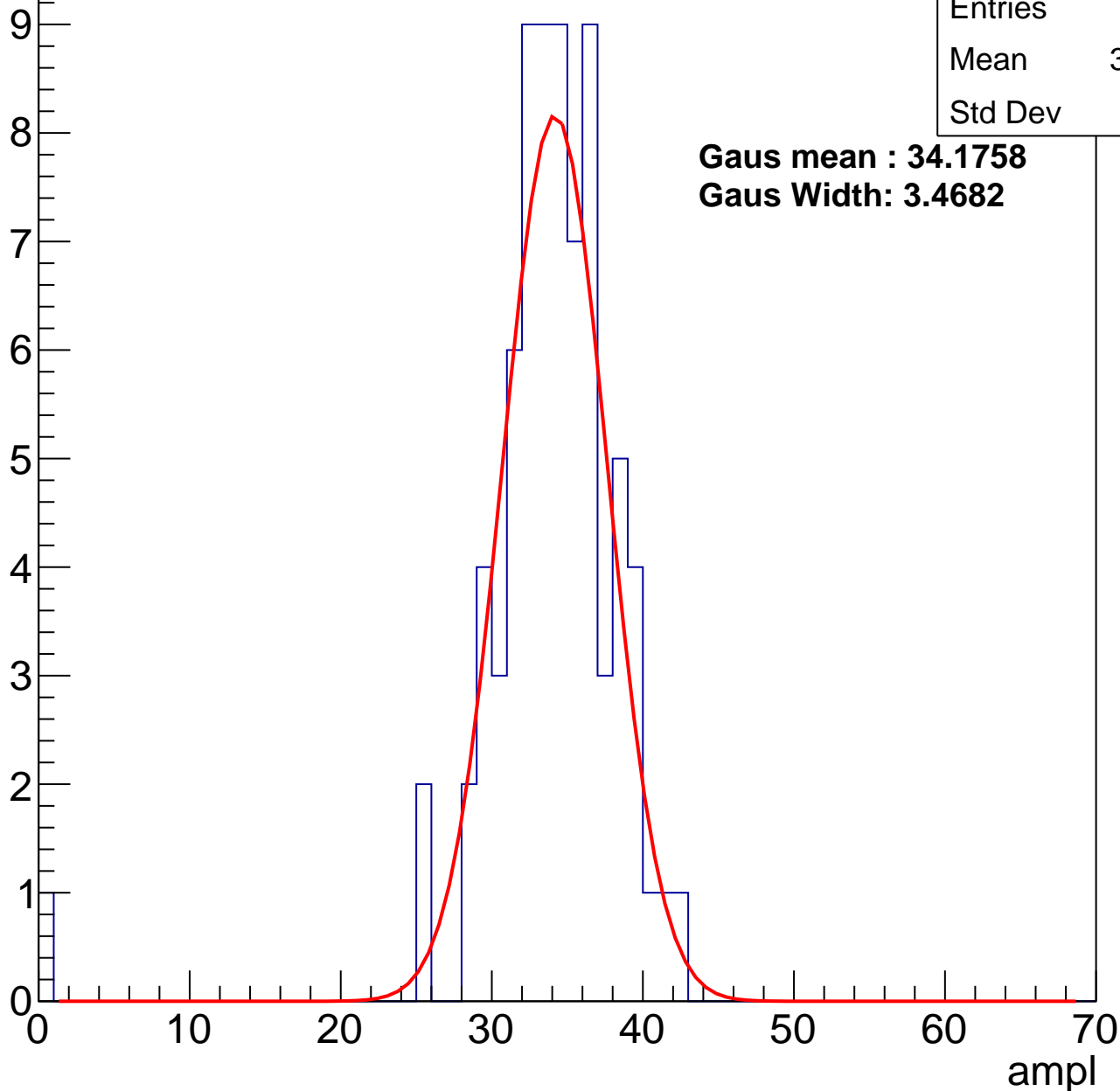
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	33.39
Std Dev	5.14

Gaus mean : 34.1758

Gaus Width: 3.4682



B1L103S, U21-ch112, adc2

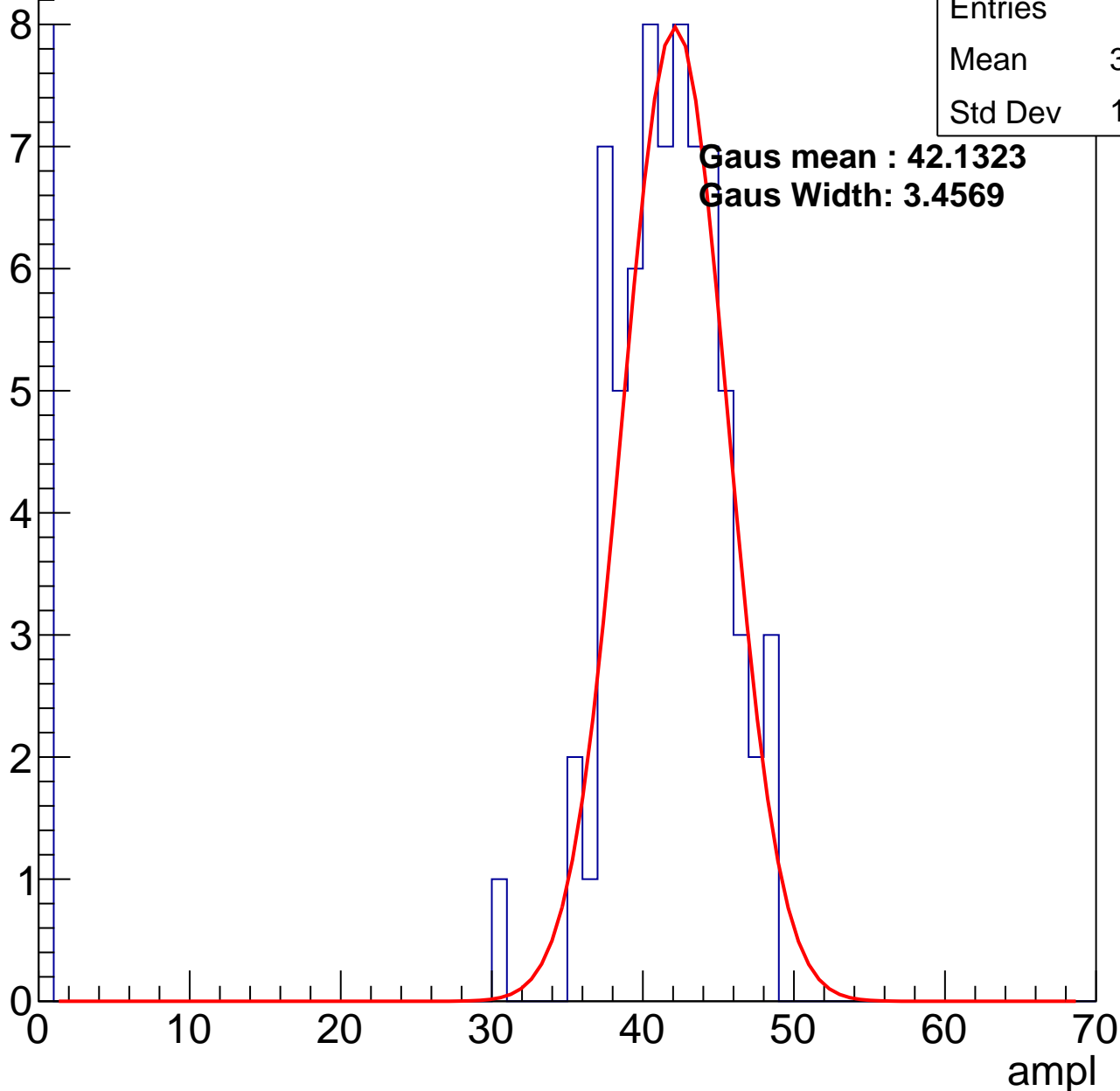
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	37.15
Std Dev	12.82

Gaus mean : 42.1323

Gaus Width: 3.4569

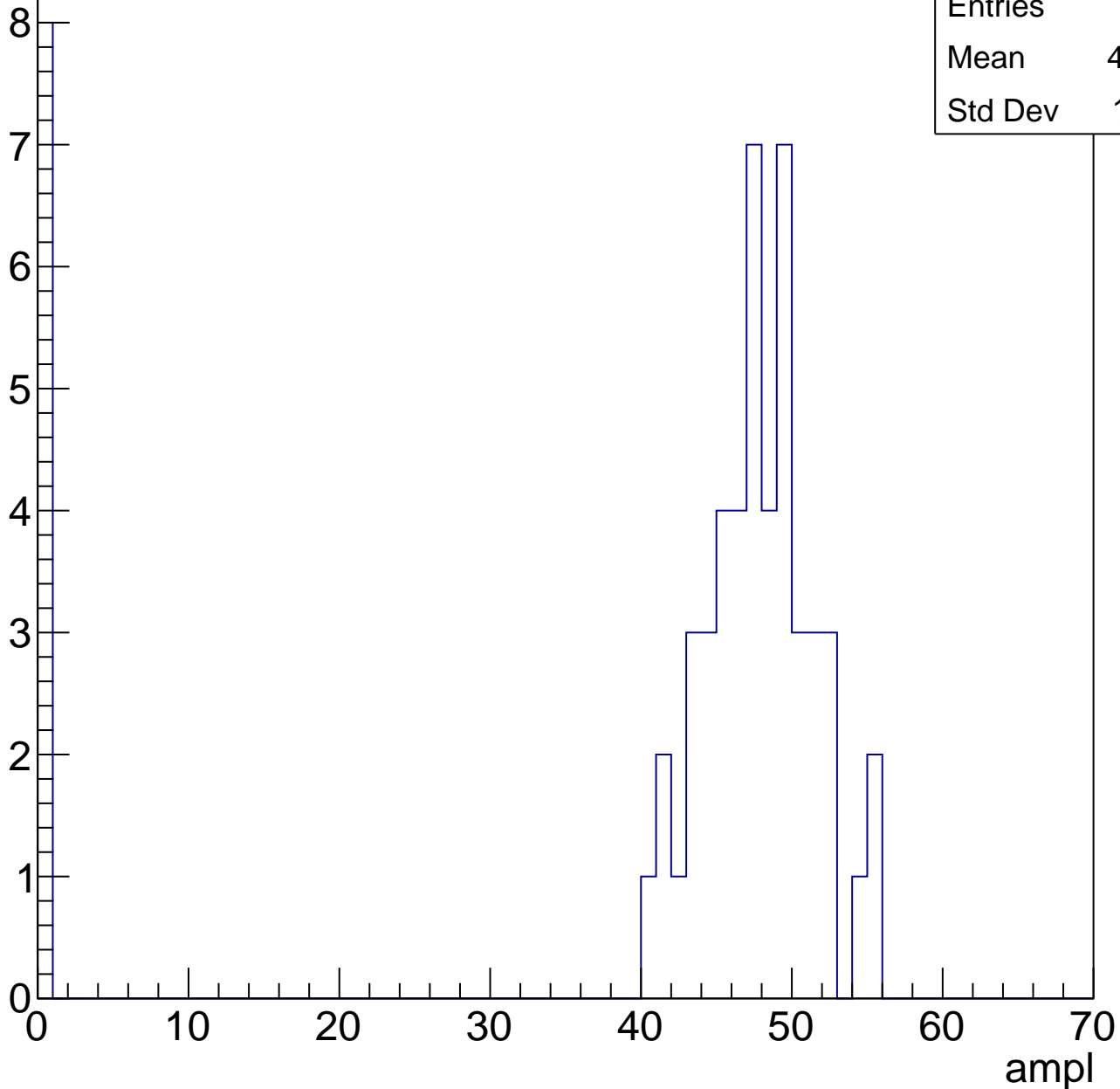


B1L103S, U21-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	40.64
Std Dev	16.91

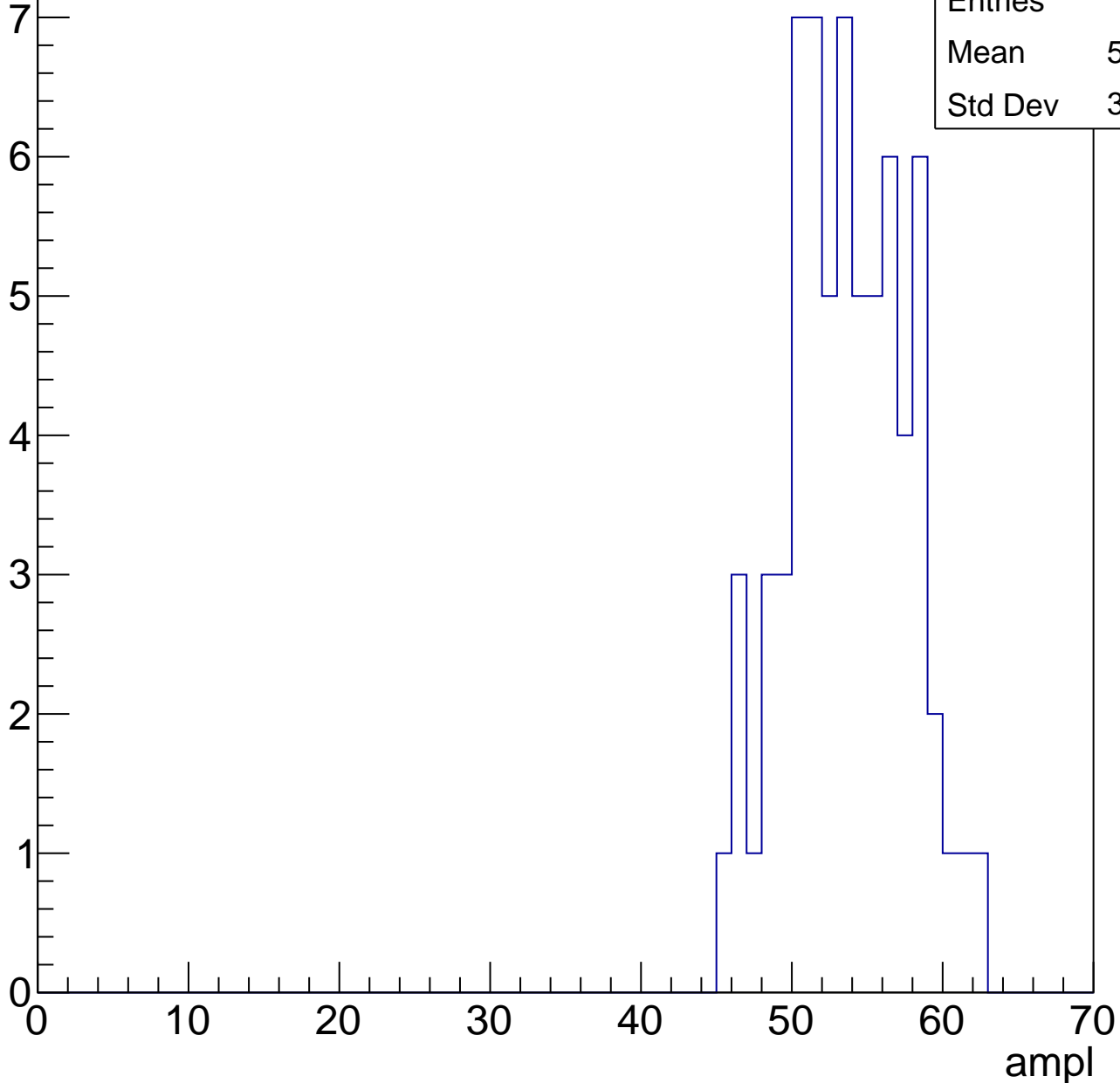


B1L103S, U21-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	53.19
Std Dev	3.878

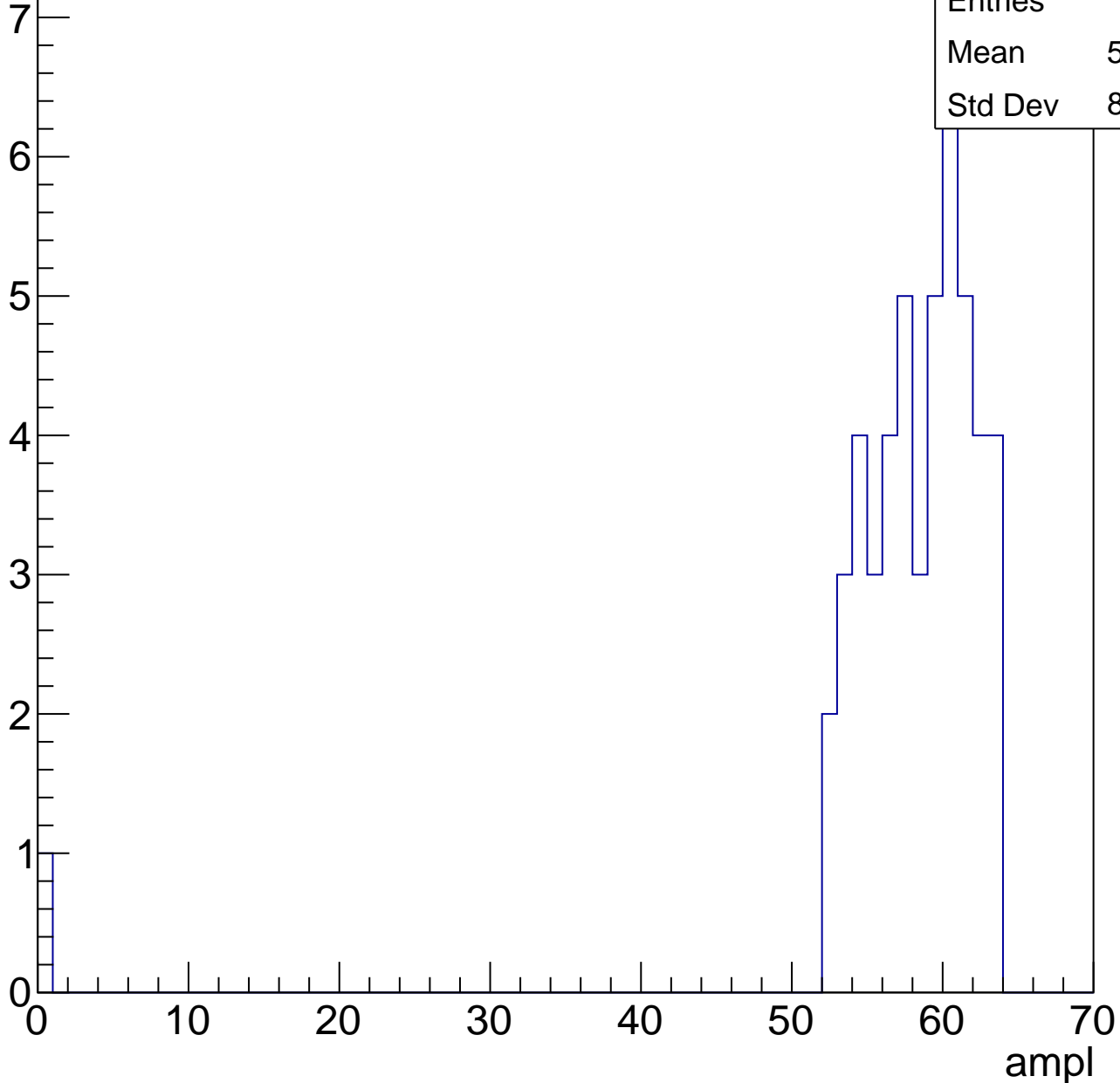


B1L103S, U21-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	56.94
Std Dev	8.728

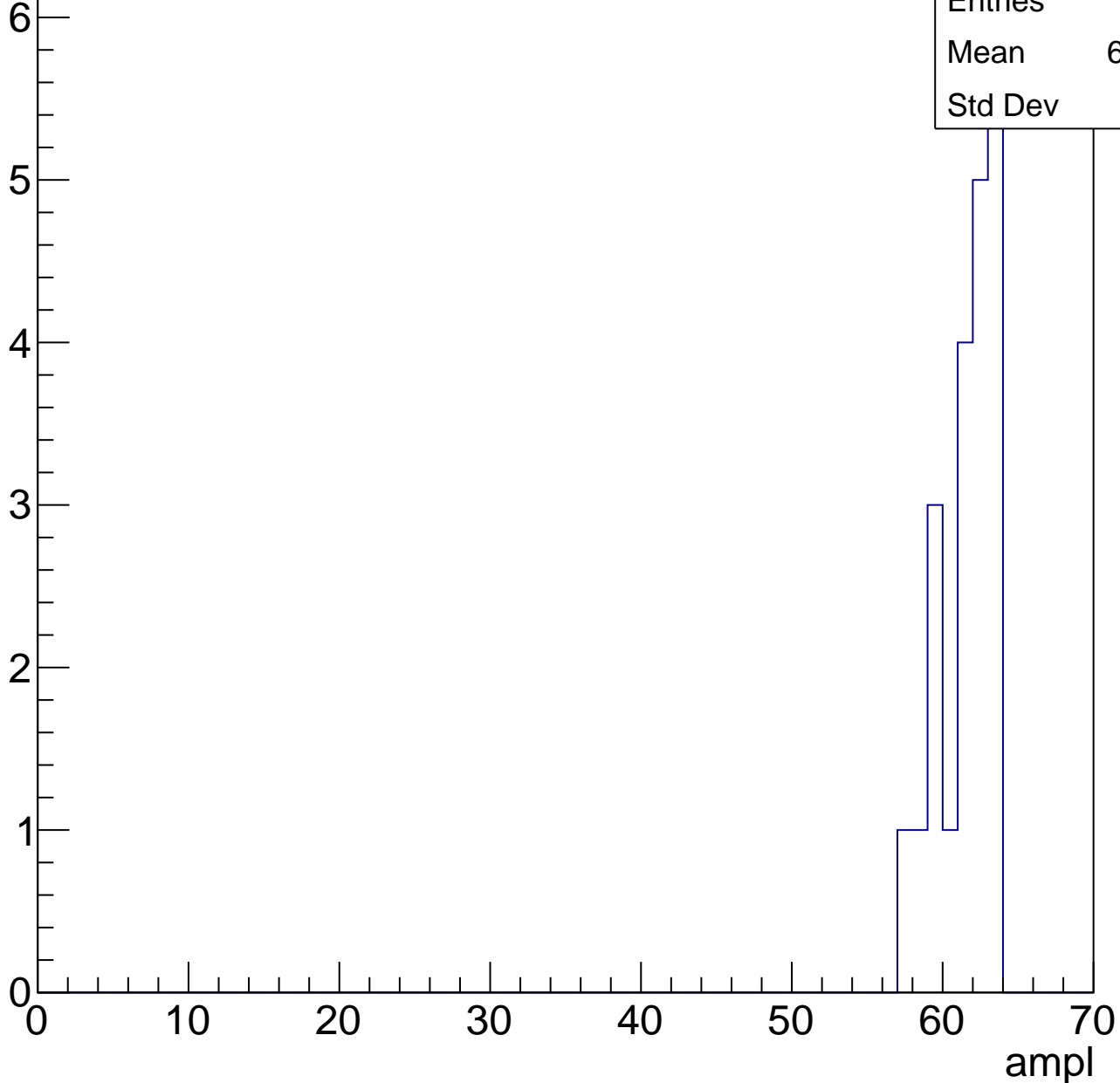


B1L103S, U21-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	61.14
Std Dev	1.78



B1L103S, U21-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U21-ch113, adc0

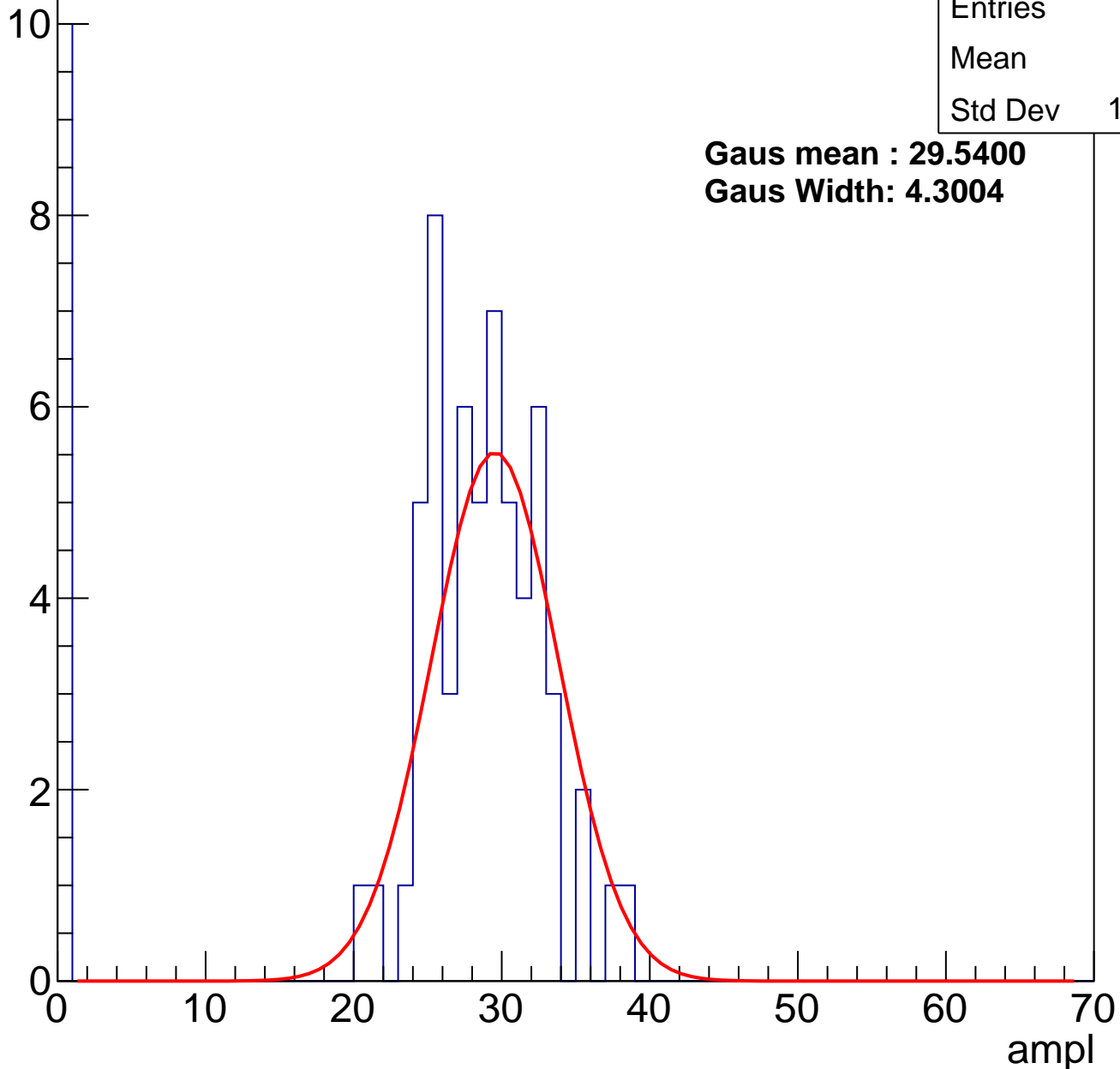
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	24.3
Std Dev	10.58

Gaus mean : 29.5400

Gaus Width: 4.3004

Entry



B1L103S, U21-ch113, adc1

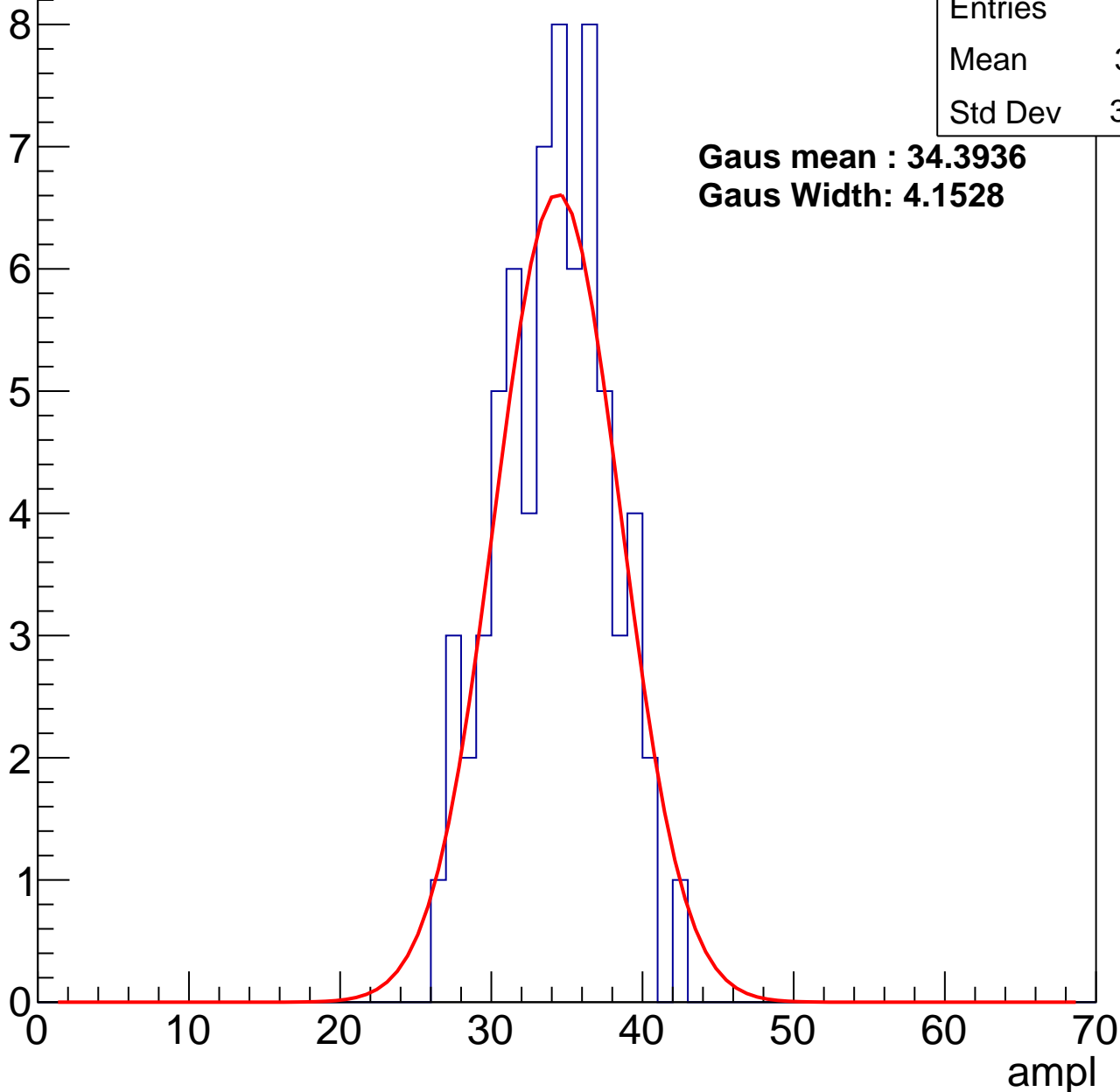
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	33.71
Std Dev	3.589

Gaus mean : 34.3936

Gaus Width: 4.1528



B1L103S, U21-ch113, adc2

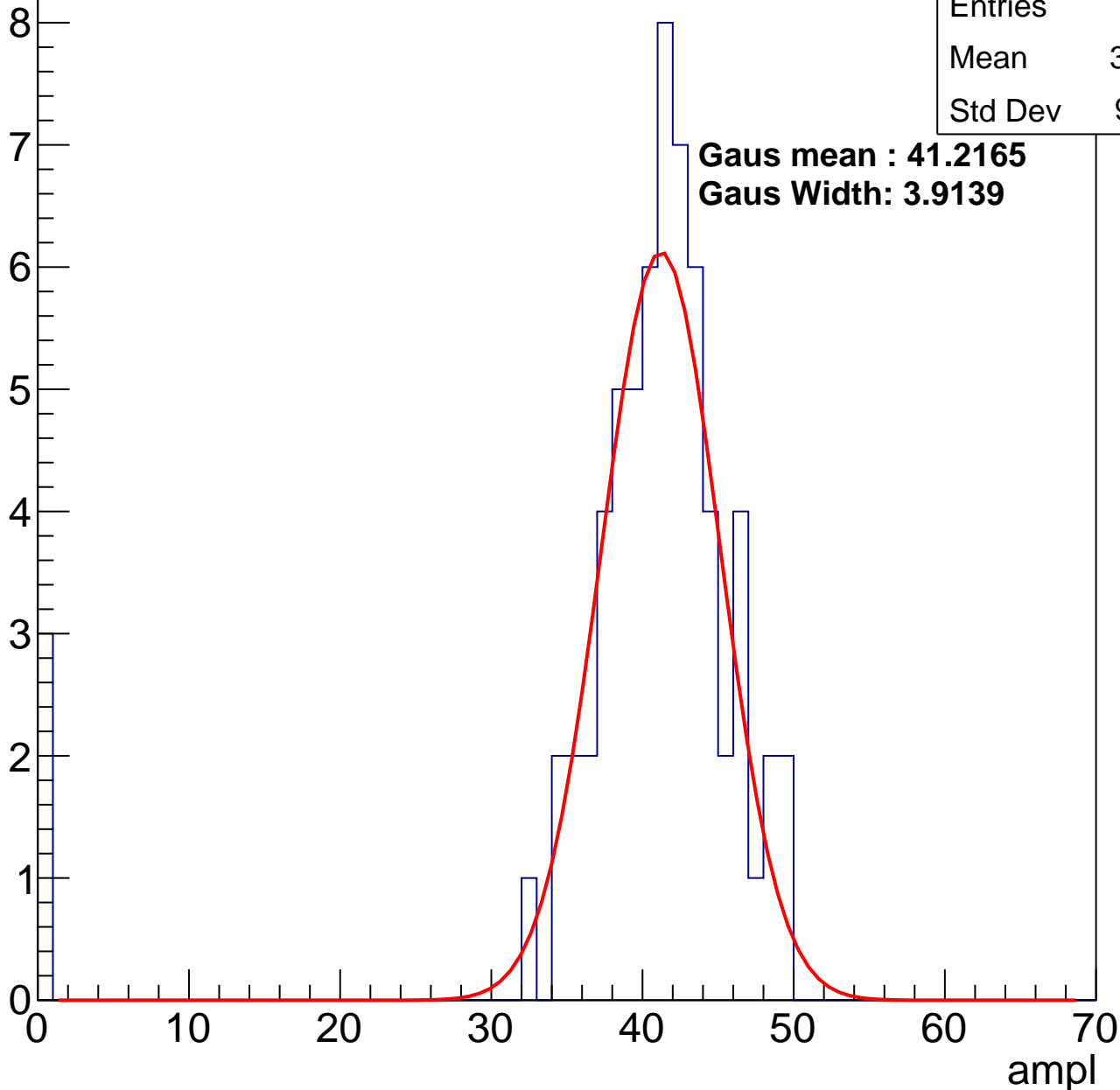
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	39.18
Std Dev	9.311

Gaus mean : 41.2165

Gaus Width: 3.9139

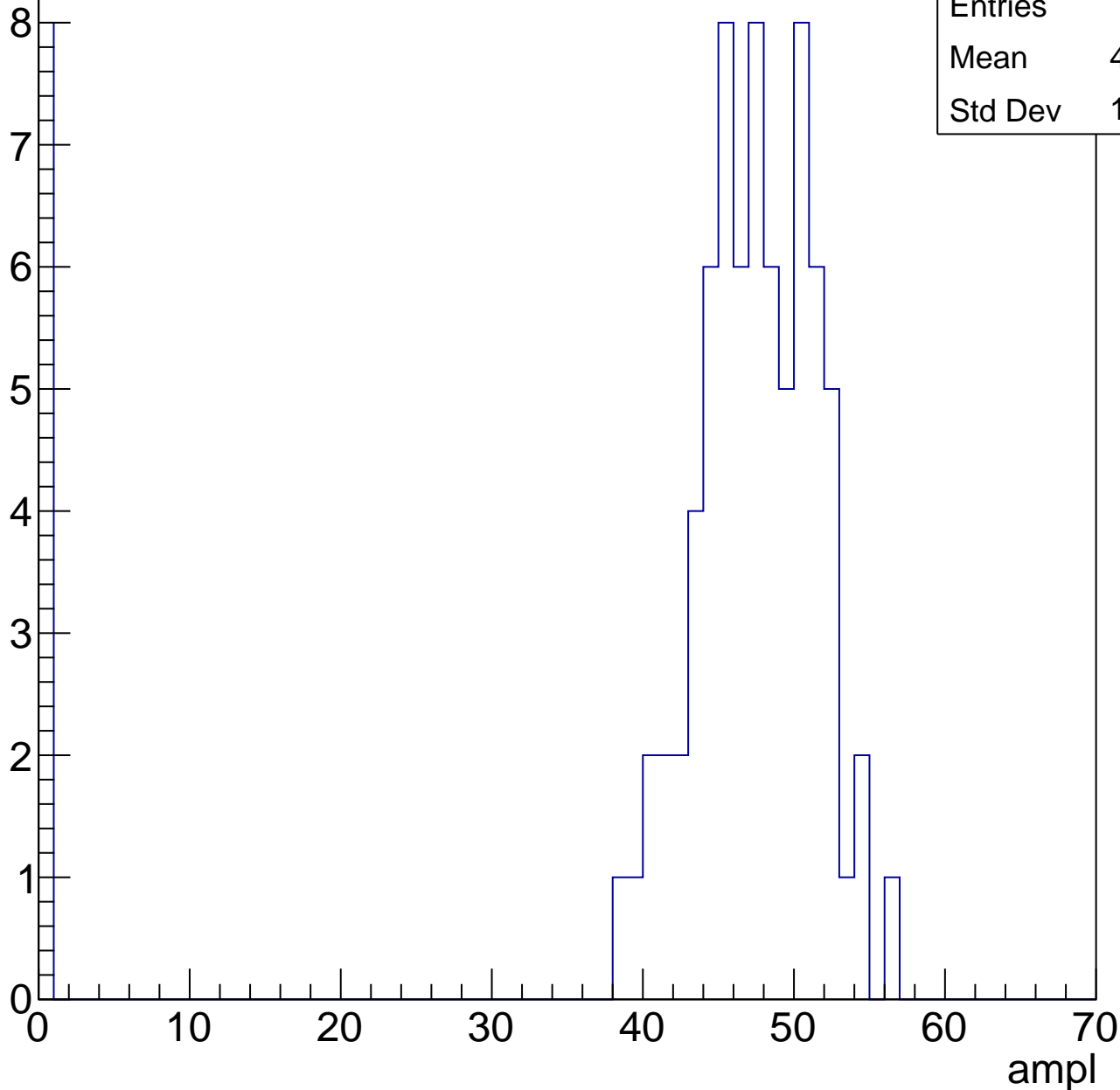


B1L103S, U21-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	42.52
Std Dev	14.43

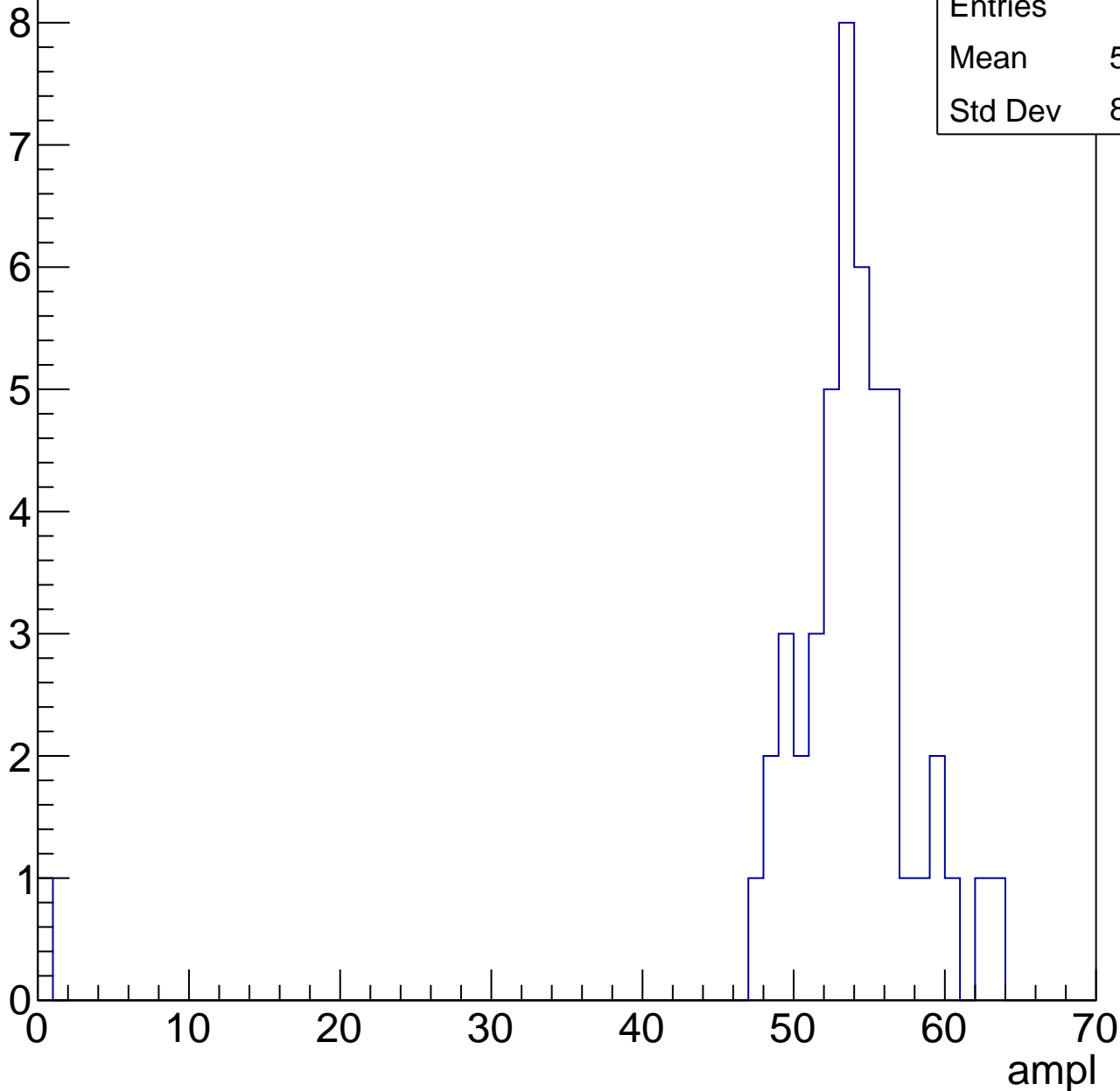


B1L103S, U21-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	52.58
Std Dev	8.396

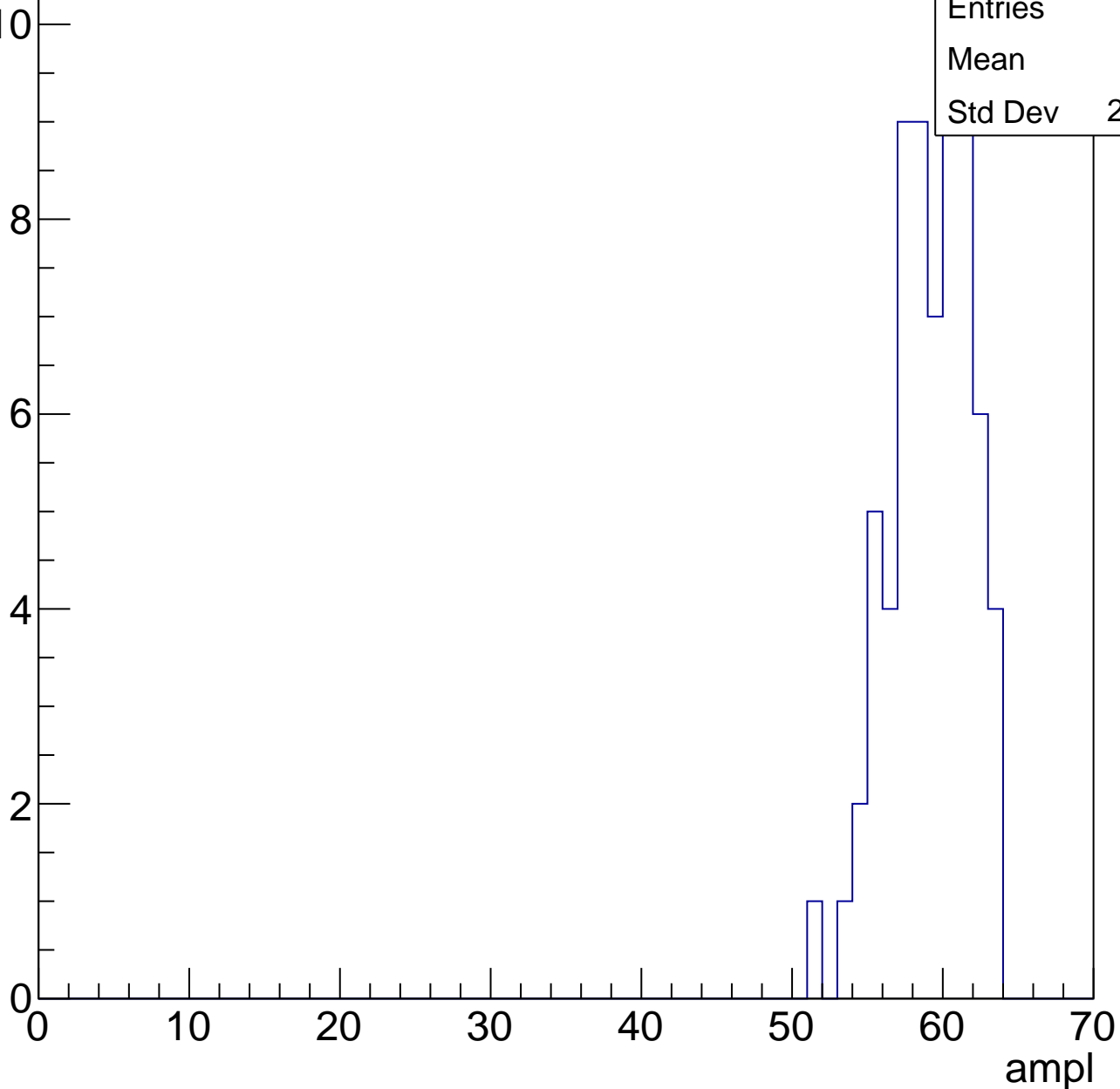


B1L103S, U21-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	58.7
Std Dev	2.654

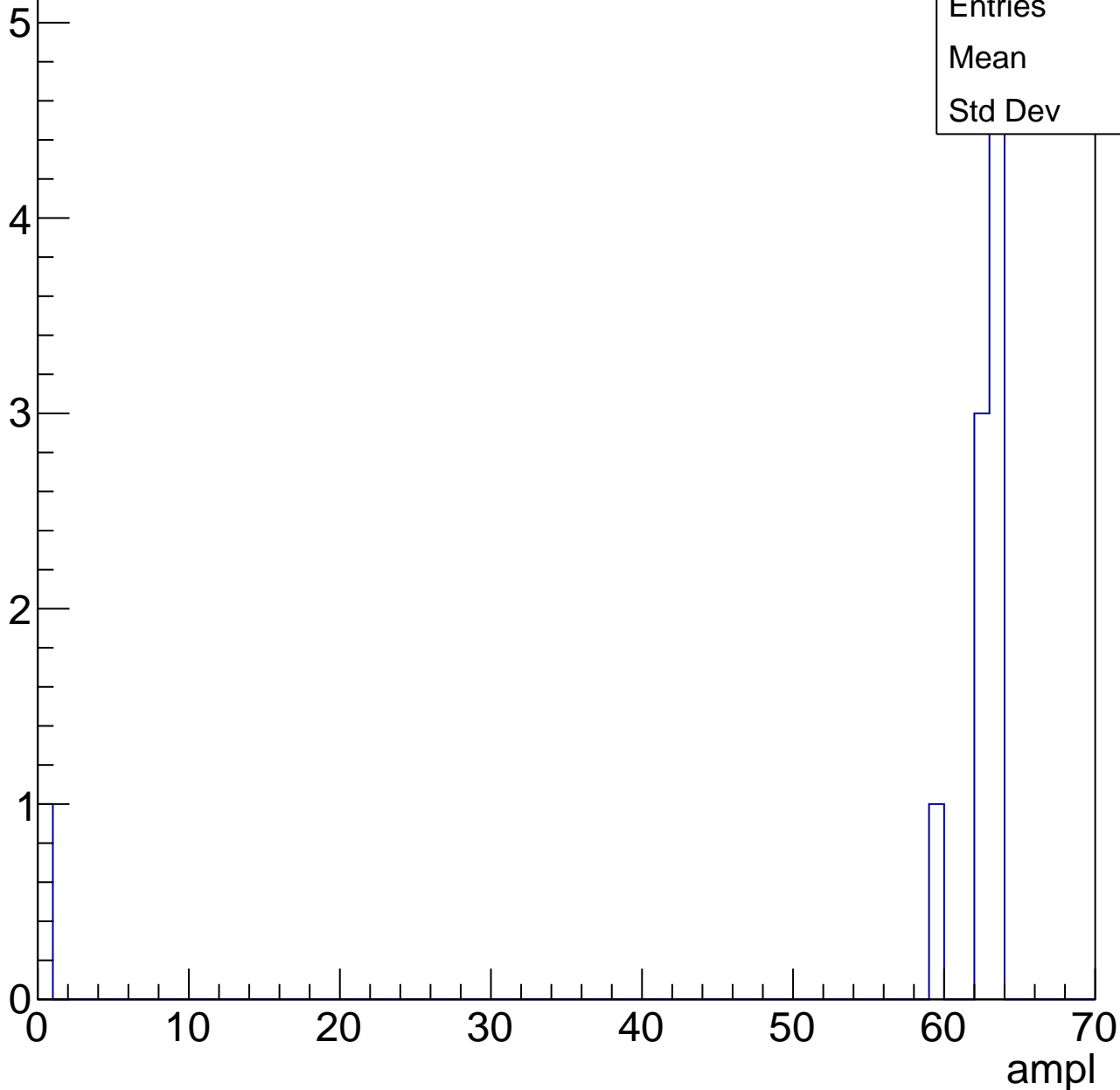


B1L103S, U21-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	56
Std Dev	18.7

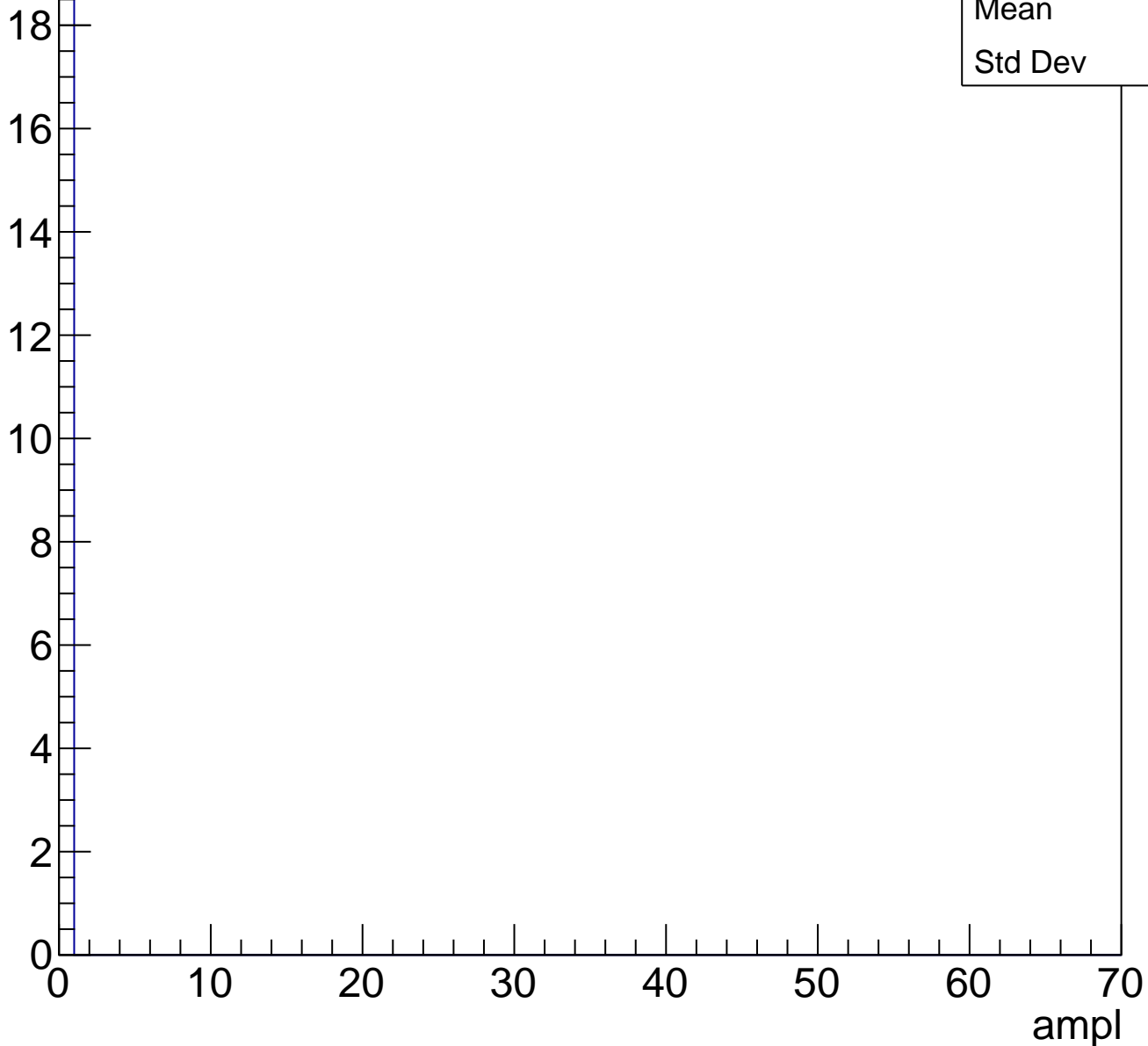


B1L103S, U21-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



B1L103S, U21-ch114, adc0

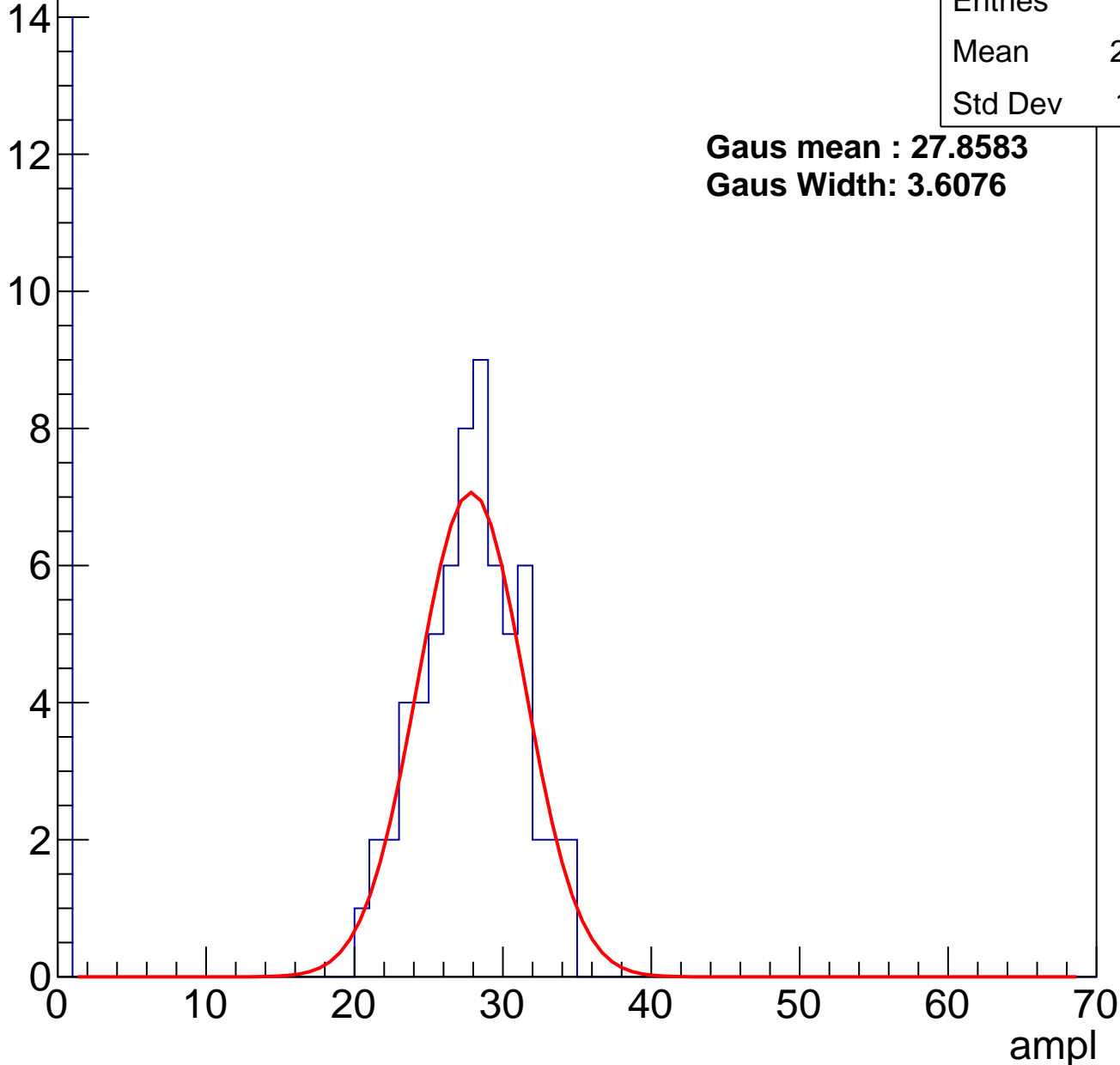
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	22.45
Std Dev	10.91

Gaus mean : 27.8583

Gaus Width: 3.6076

Entry



B1L103S, U21-ch114, adc1

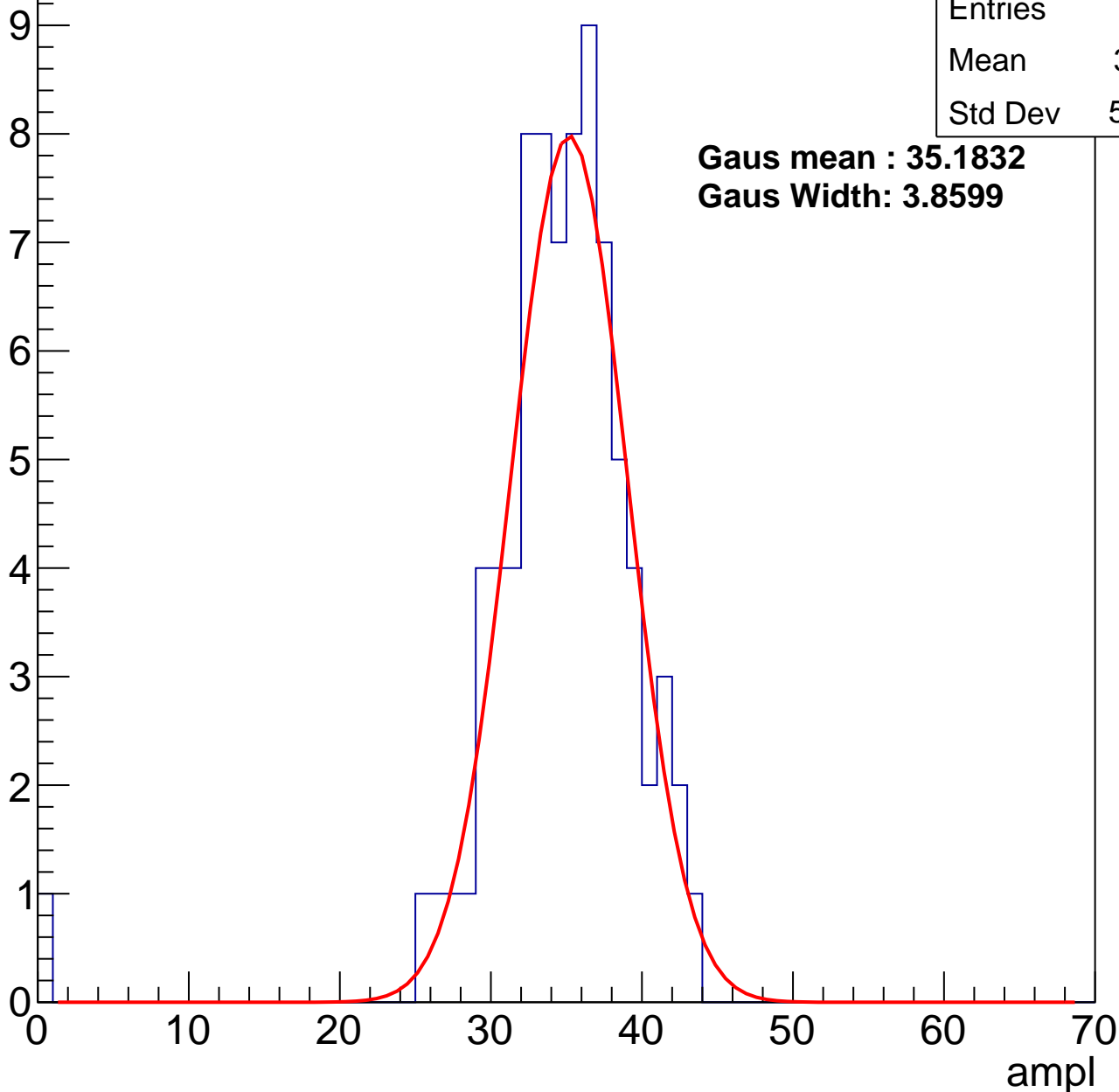
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	34.11
Std Dev	5.375

Gaus mean : 35.1832

Gaus Width: 3.8599



B1L103S, U21-ch114, adc2

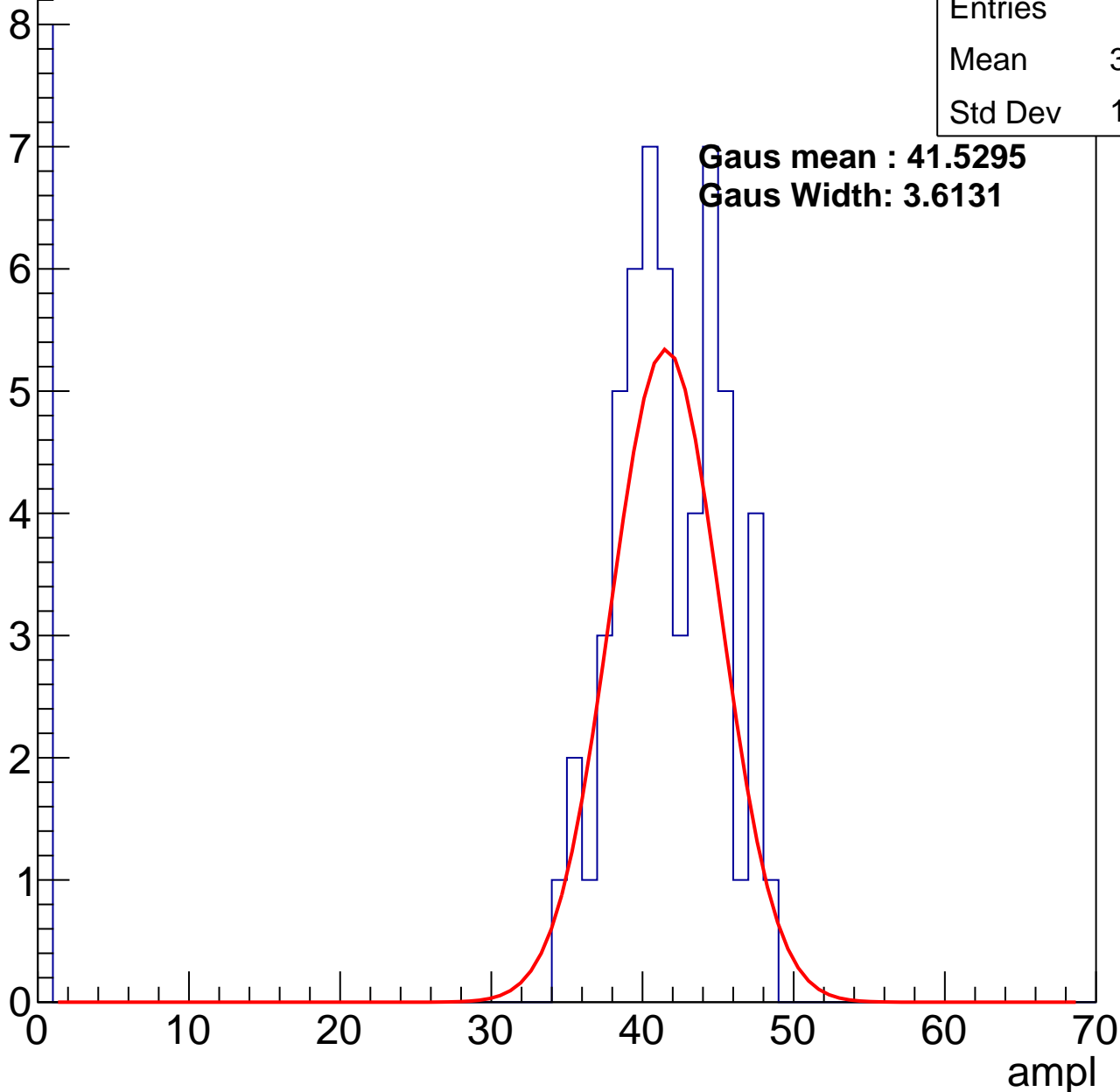
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.16
Std Dev	14.03

Gaus mean : 41.5295

Gaus Width: 3.6131

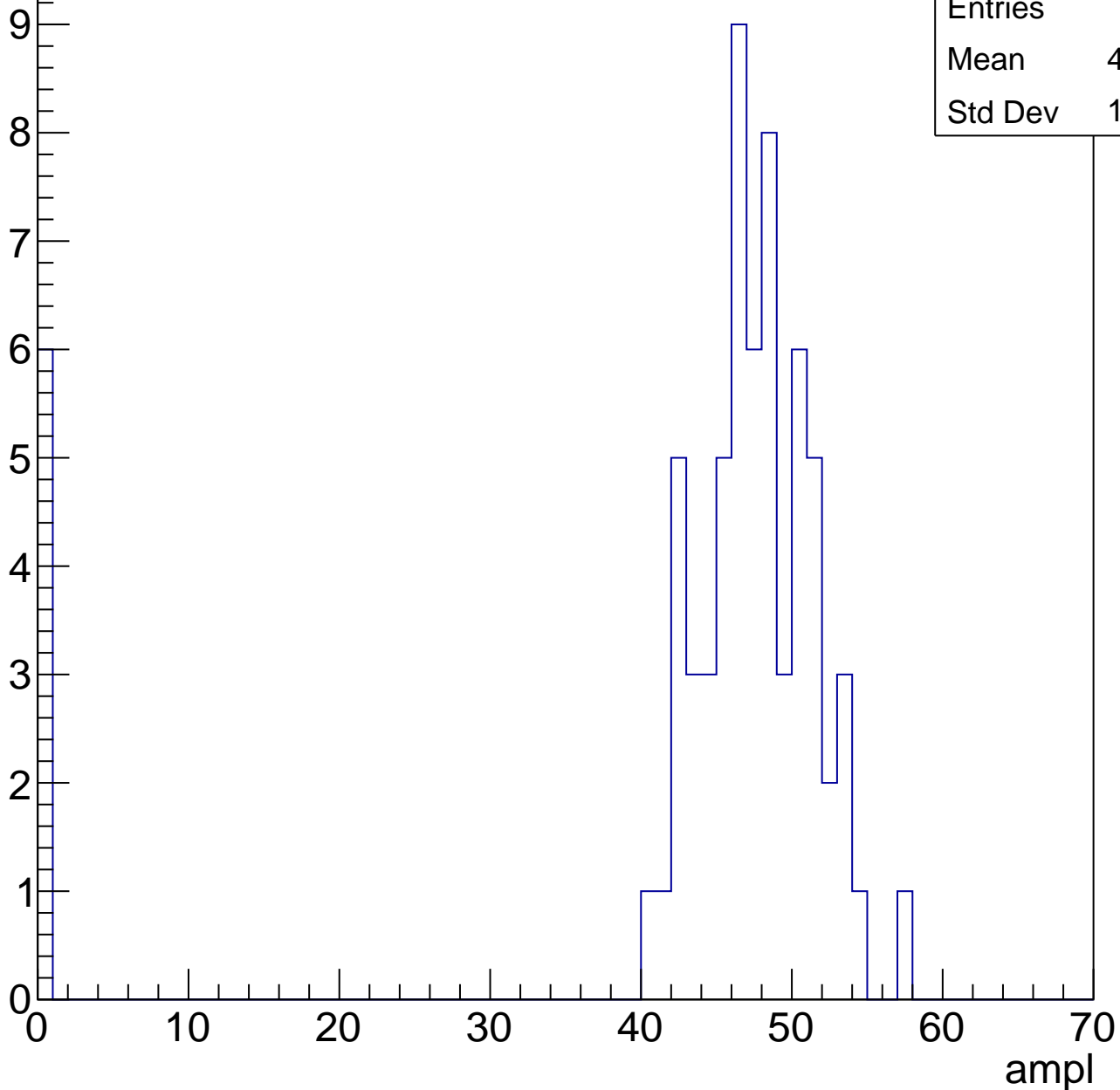


B1L103S, U21-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	43.13
Std Dev	13.83

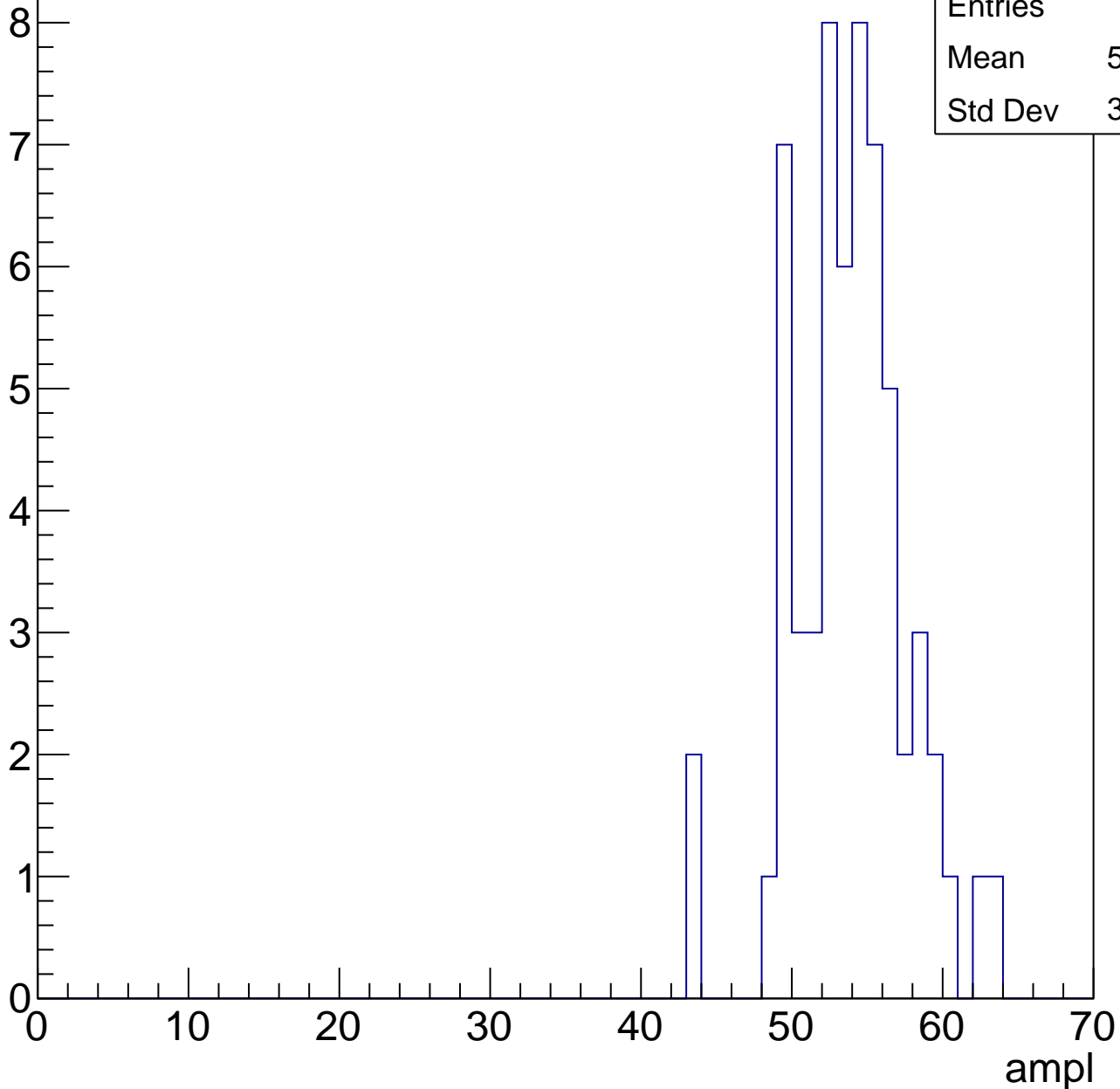


B1L103S, U21-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	53.37
Std Dev	3.799

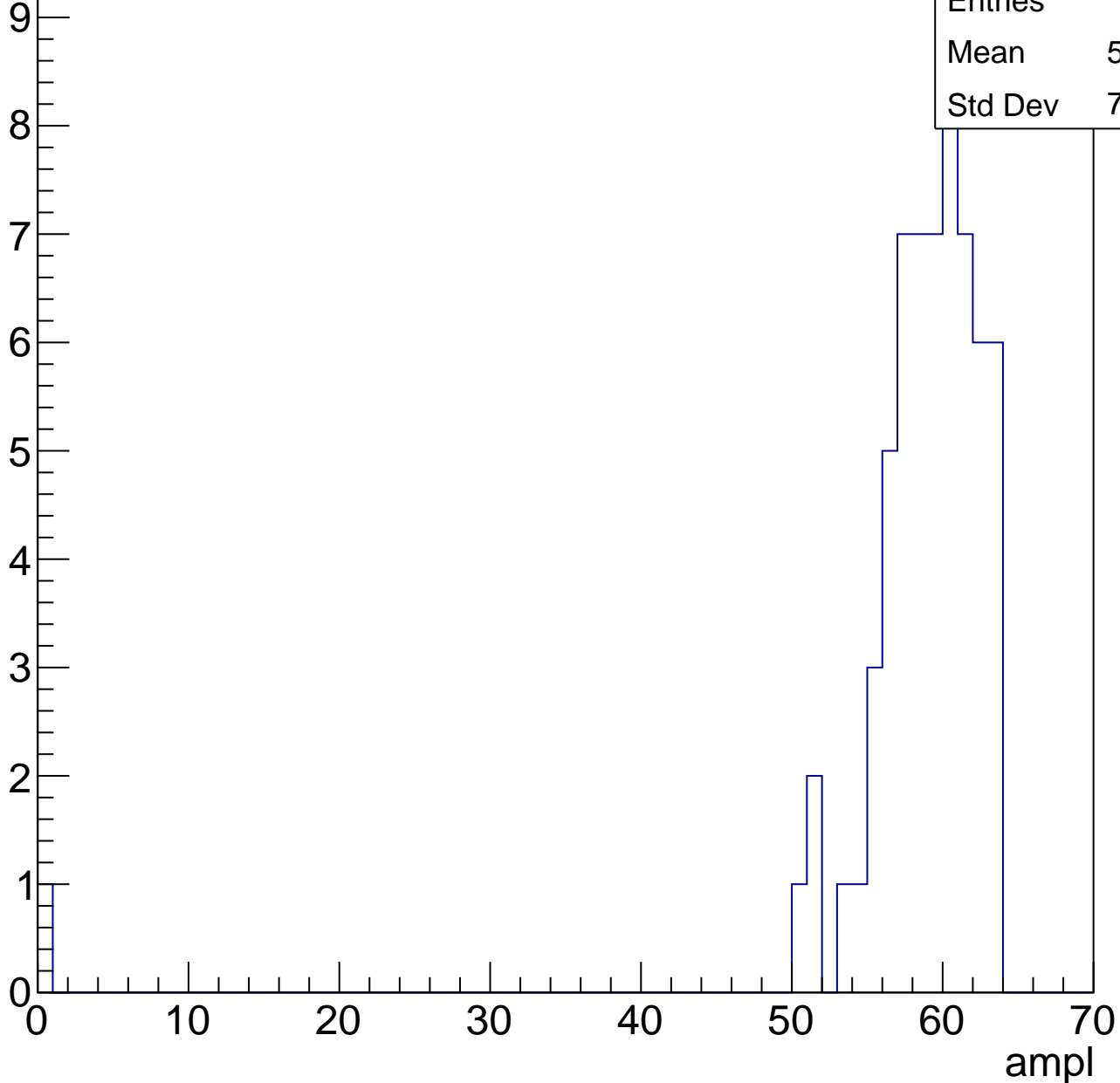


B1L103S, U21-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

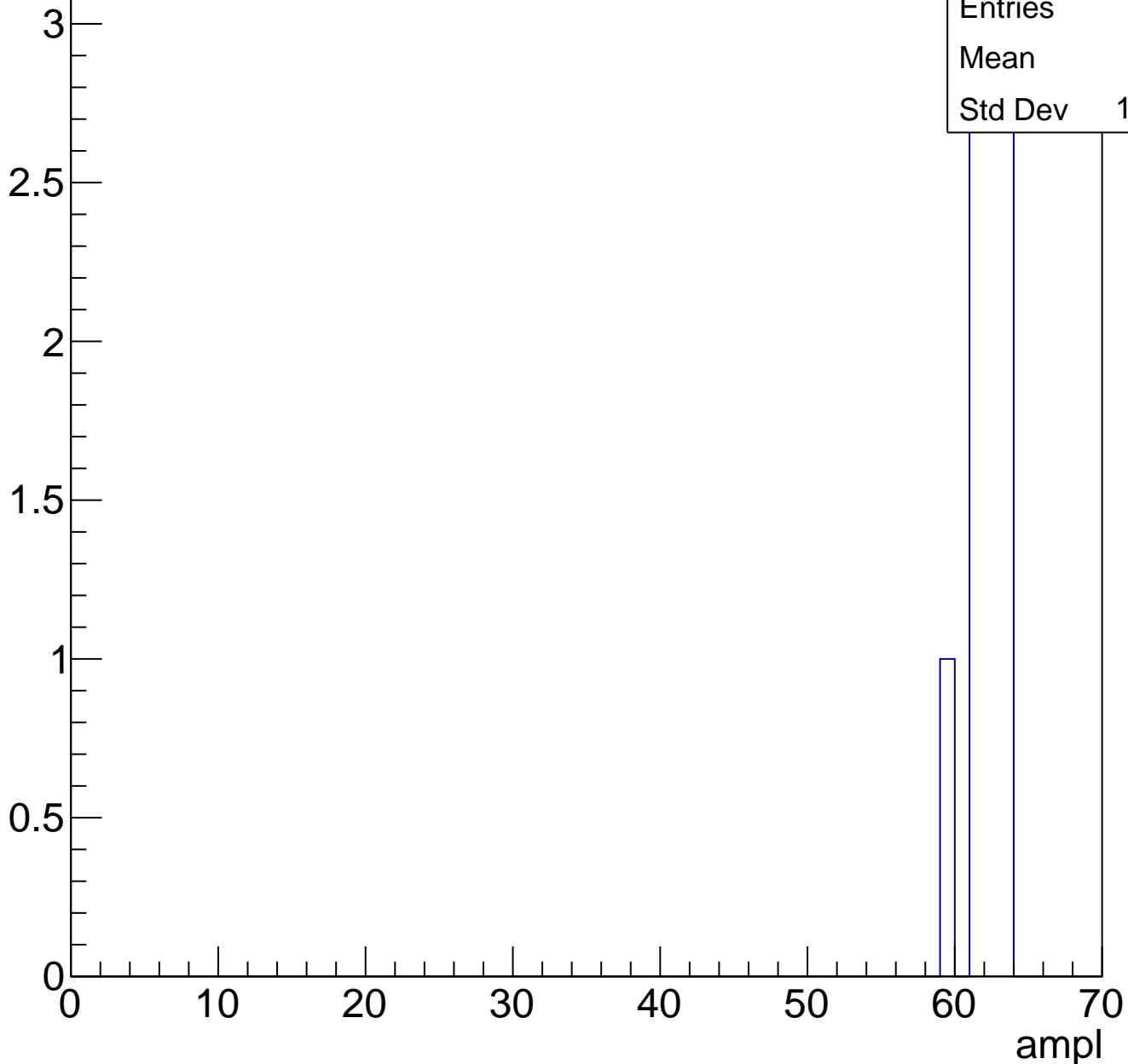
Entries	63
Mean	57.76
Std Dev	7.938



B1L103S, U21-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	10
Mean	61.7
Std Dev	1.187

B1L103S, U21-ch114, adc7

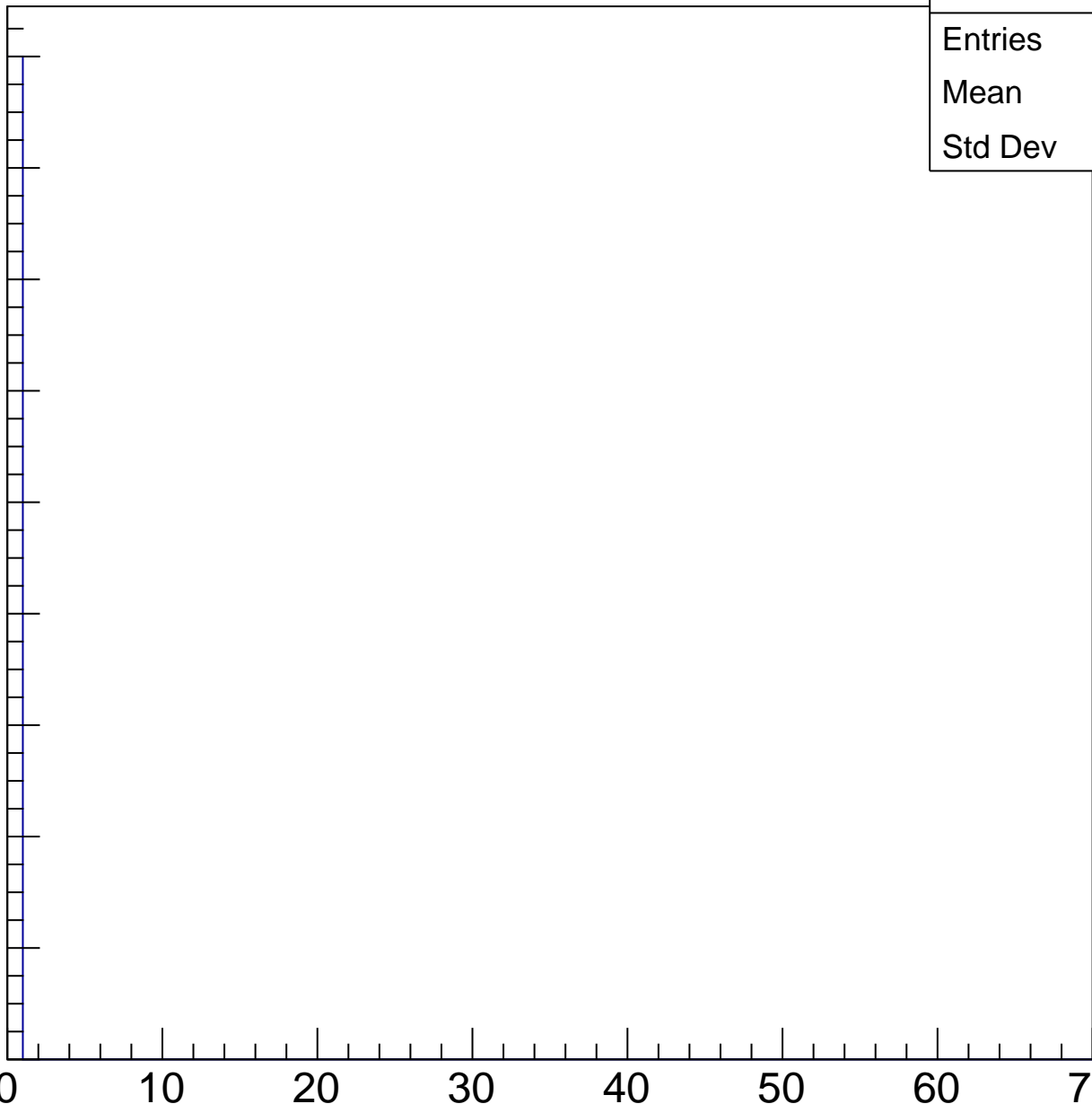
calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl



B1L103S, U21-ch115, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	20.71
Std Dev	11.57

Gaus mean : 27.7788

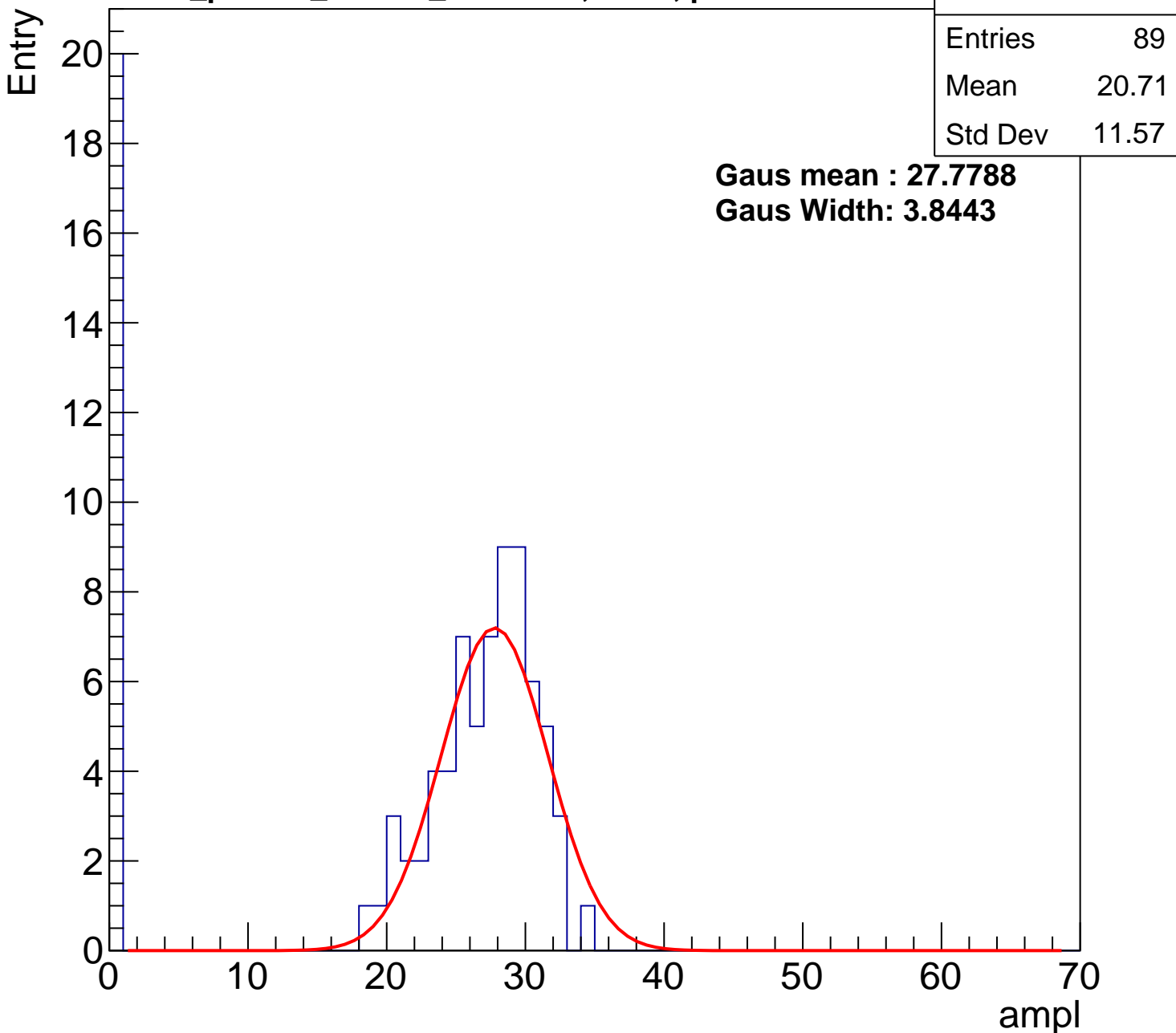
Gaus Width: 3.8443

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch115, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	34.46
Std Dev	5.396

Gaus mean : 35.3503

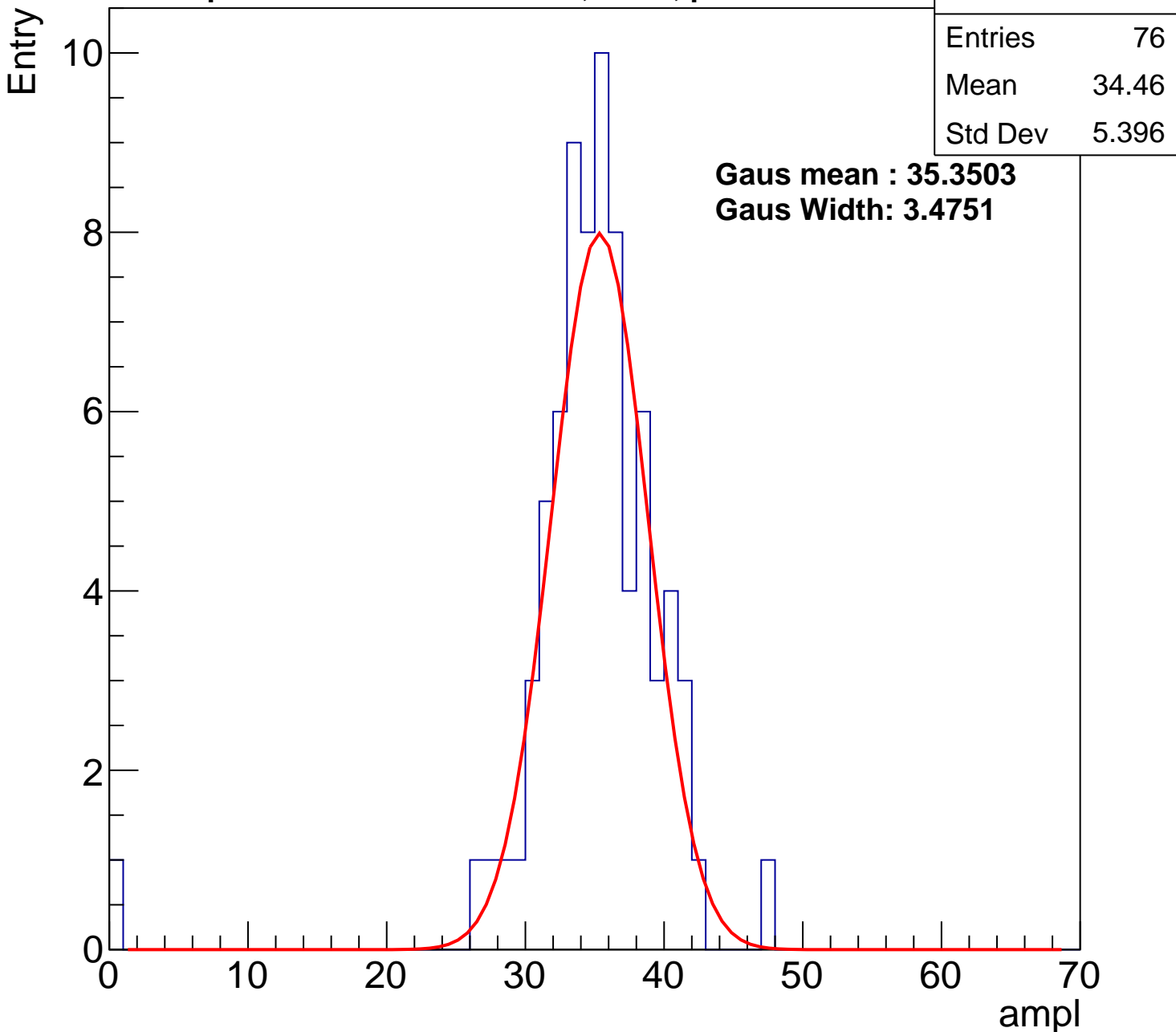
Gaus Width: 3.4751

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch115, adc2

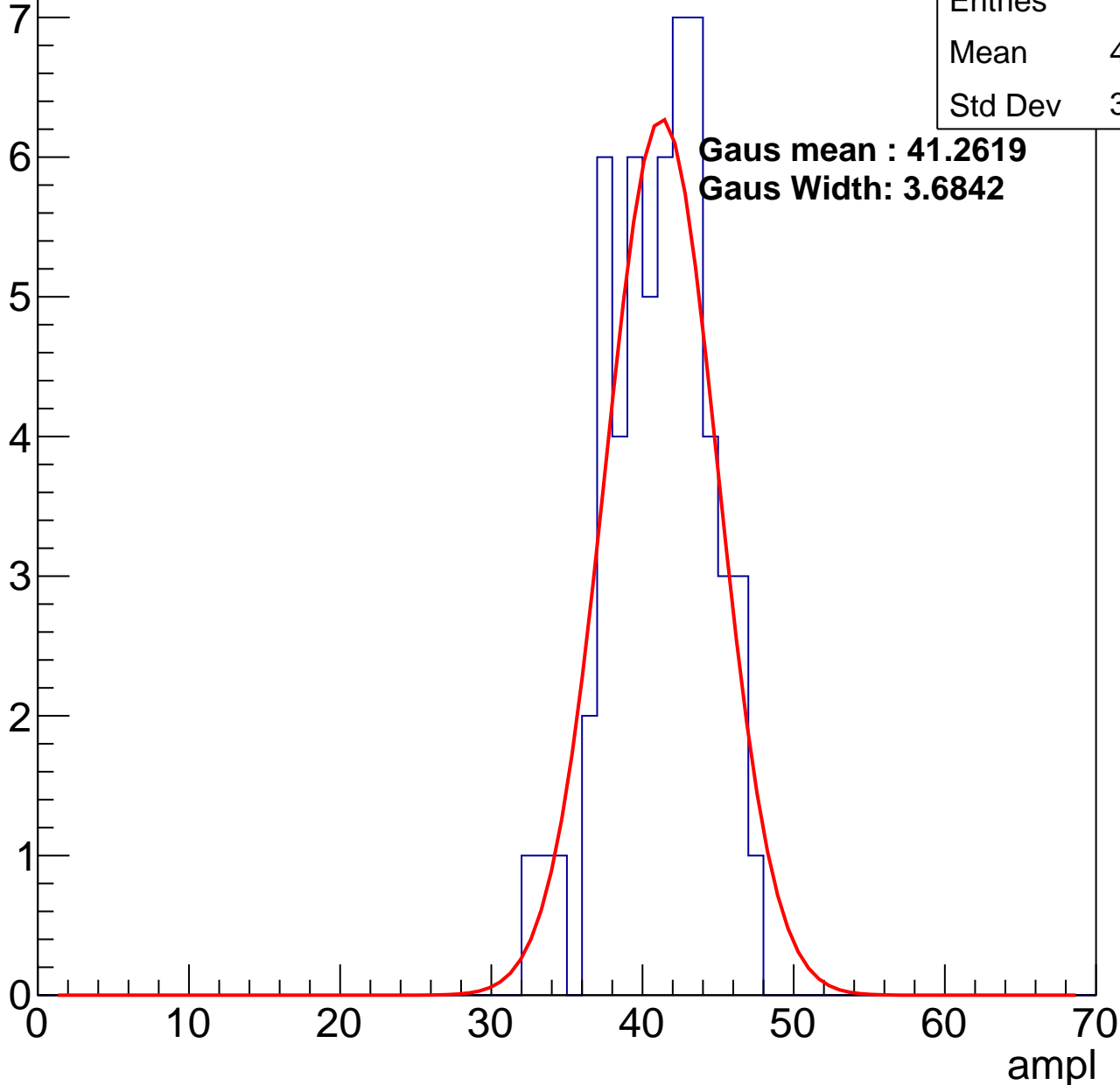
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	40.63
Std Dev	3.317

Gaus mean : 41.2619

Gaus Width: 3.6842



B1L103S, U21-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	40.69
Std Dev	16.93

Entry

10

8

6

4

2

0

0

10

20

30

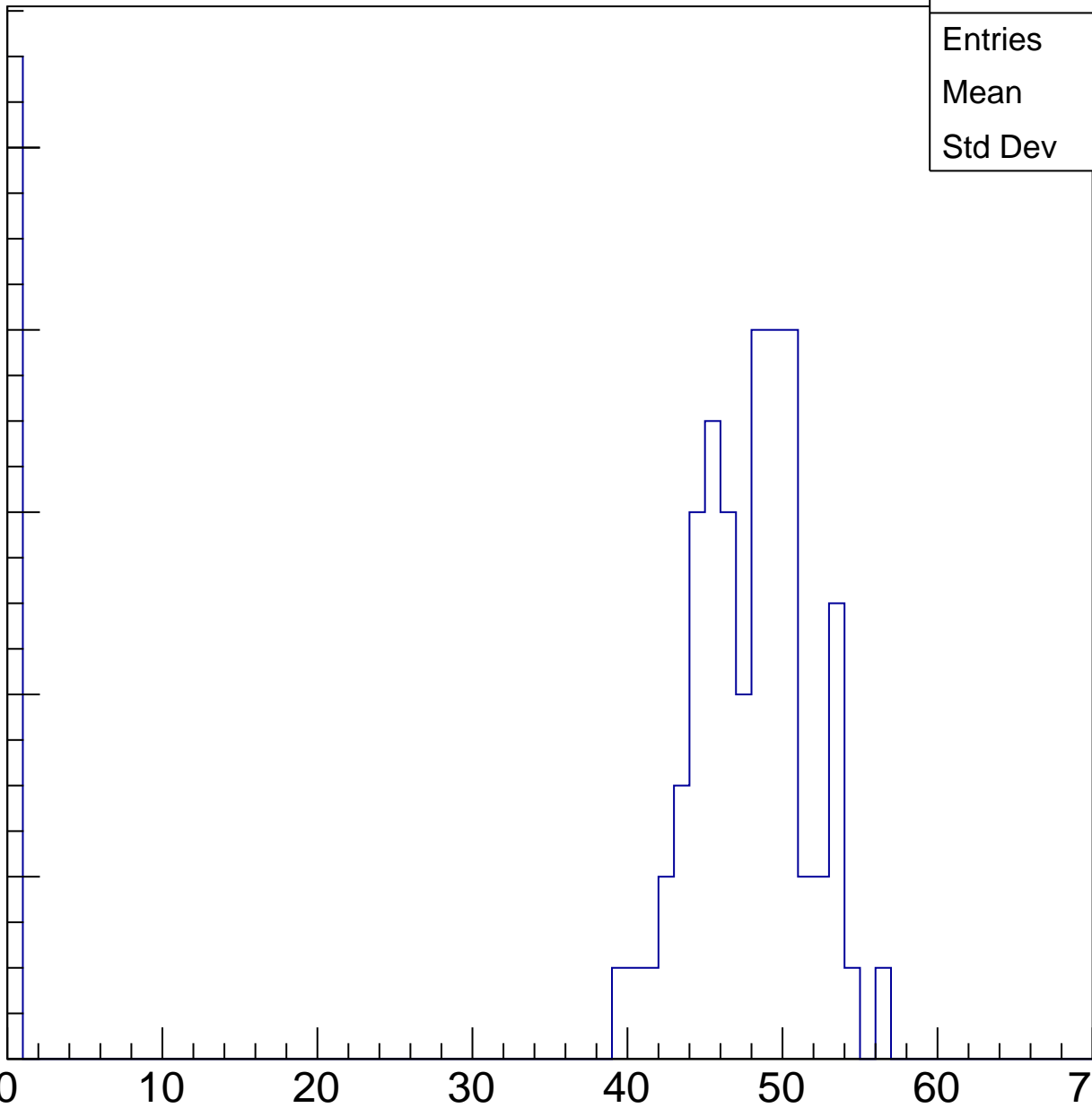
40

50

60

70

ampl

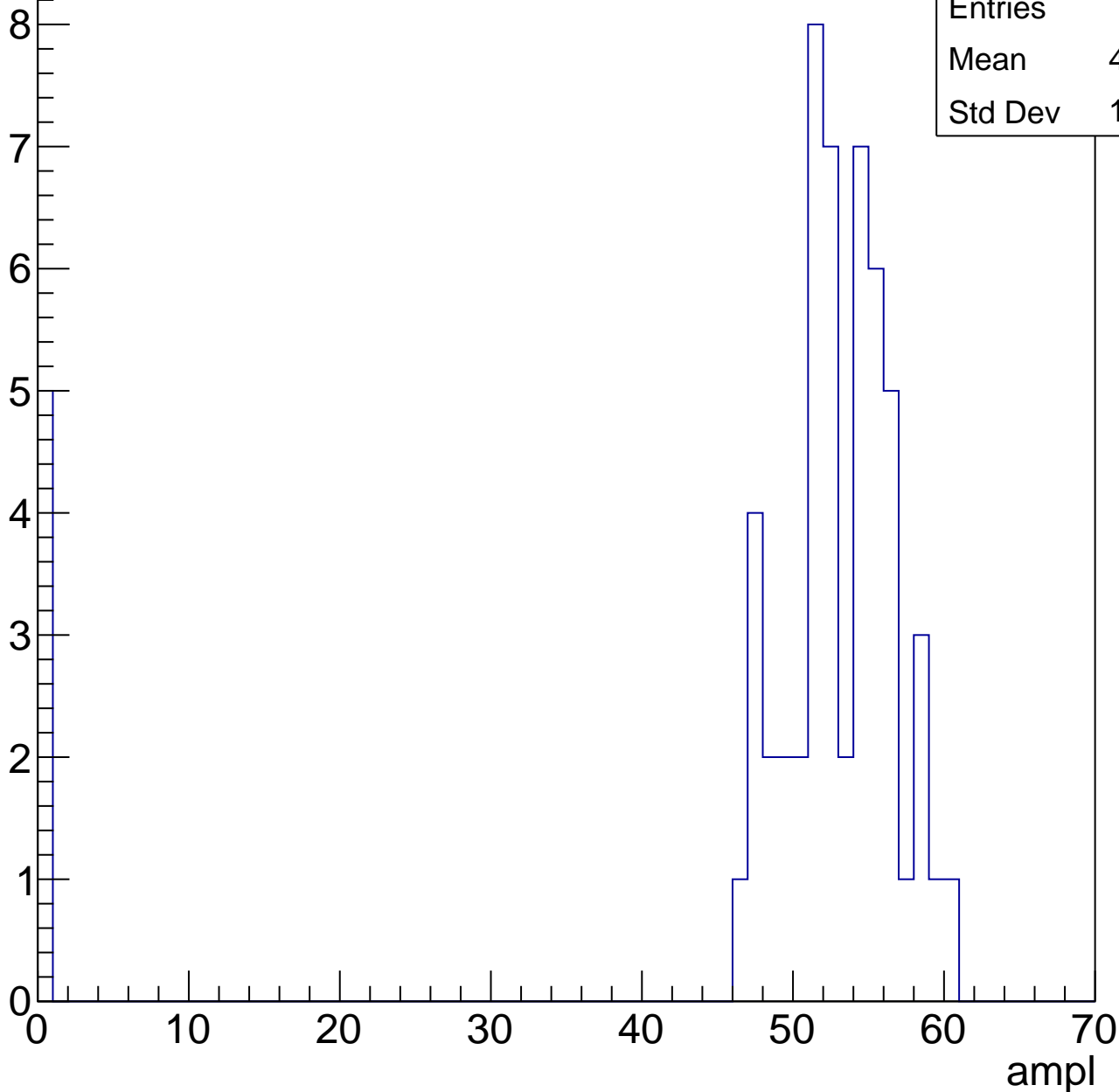


B1L103S, U21-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

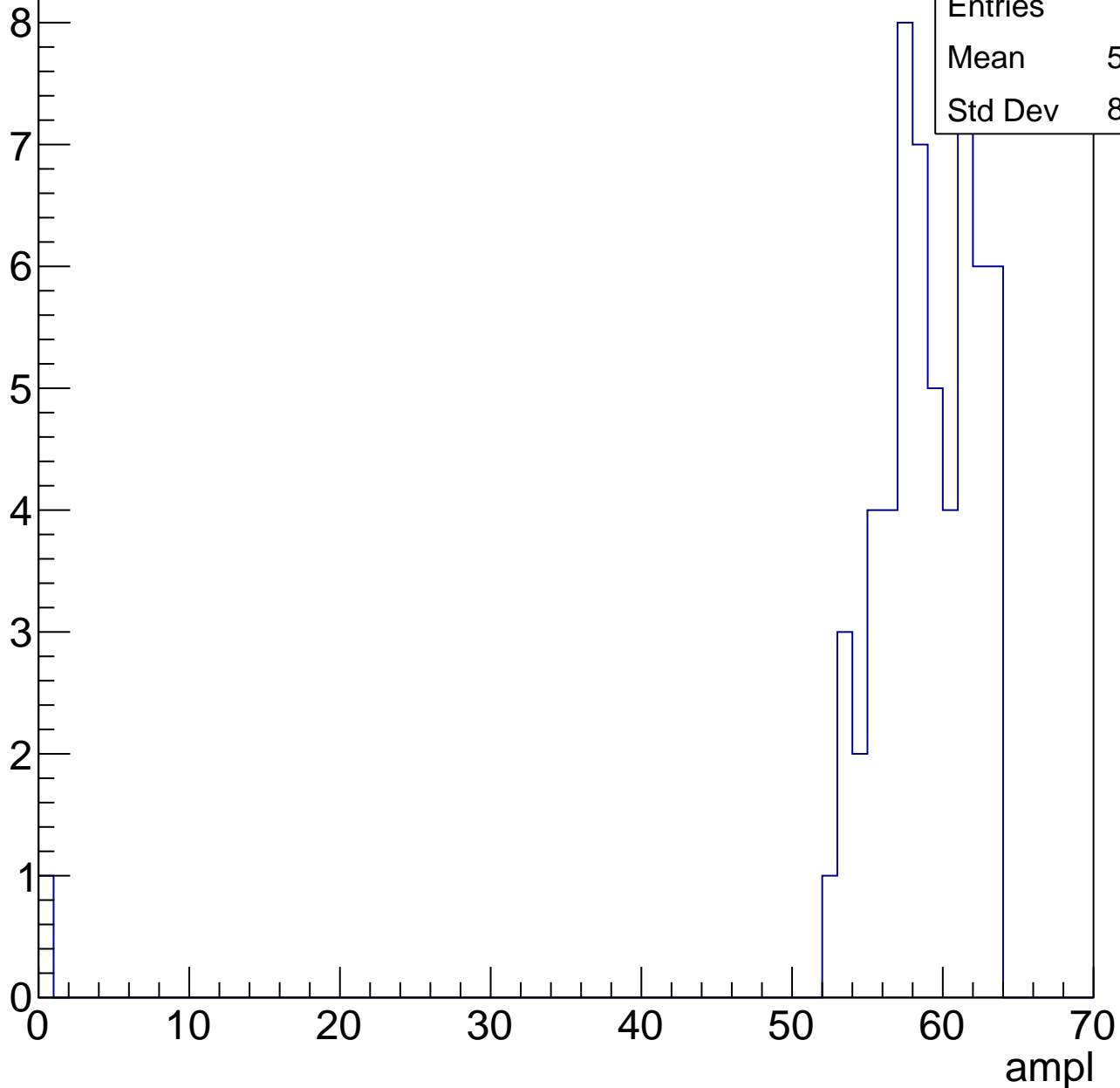
Entries	57
Mean	48.14
Std Dev	15.27



B1L103S, U21-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

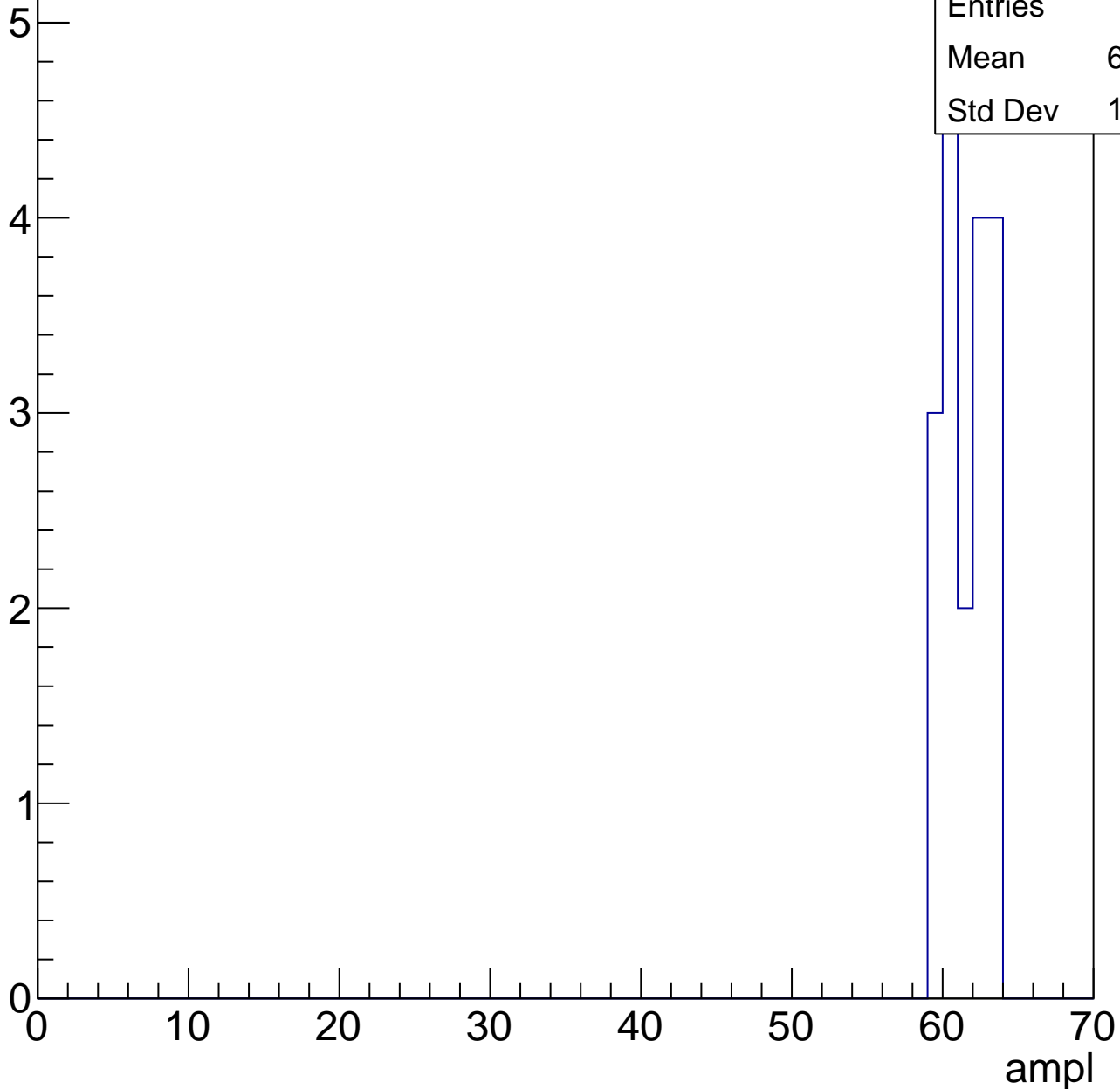


B1L103S, U21-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.06
Std Dev	1.433

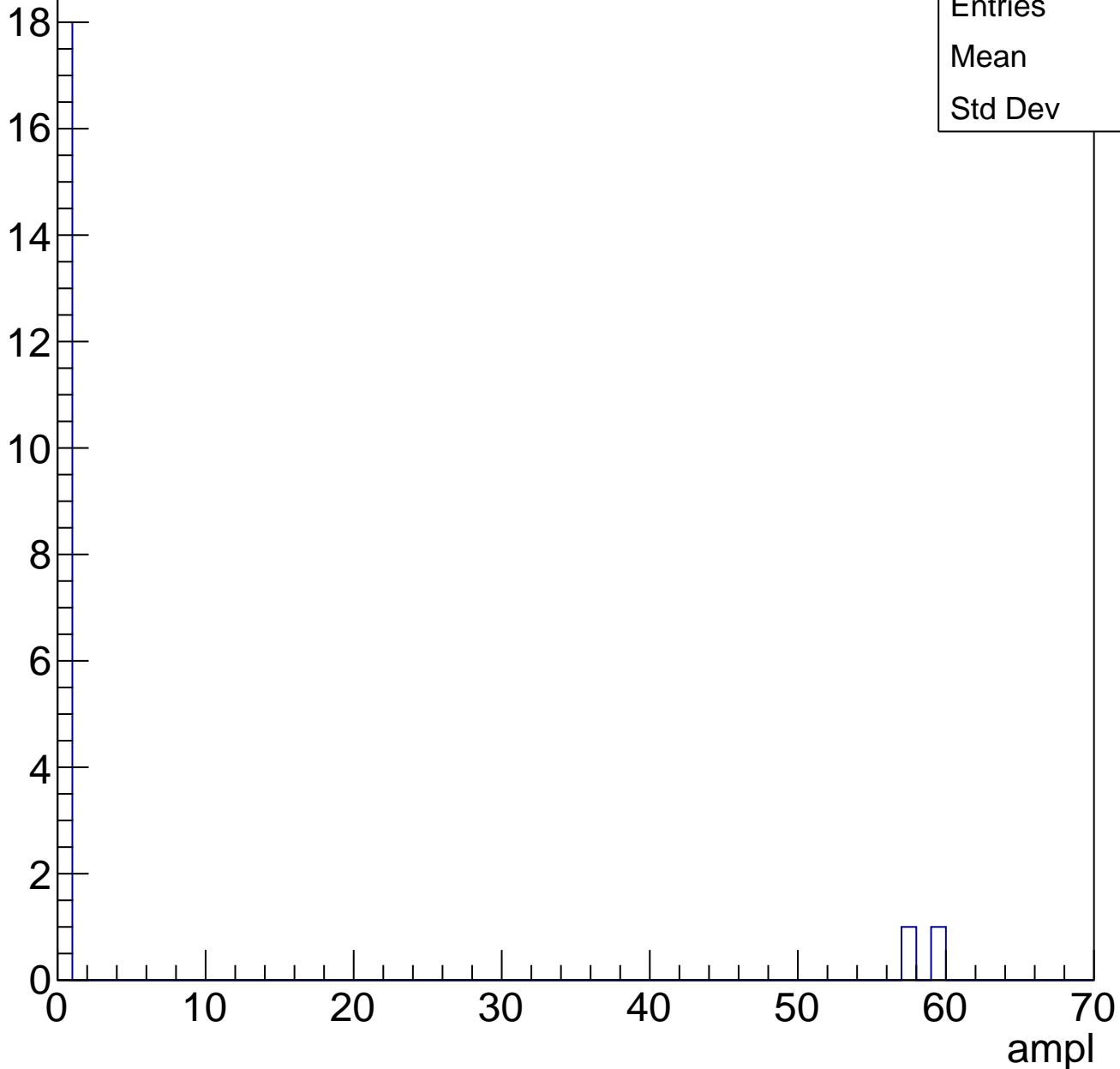


B1L103S, U21-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	5.8
Std Dev	17.4

Entry



B1L103S, U21-ch116, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	21.94
Std Dev	9.365

Gaus mean : 25.6303

Gaus Width: 3.9247

Entry

10

8

6

4

2

0

0

10

20

30

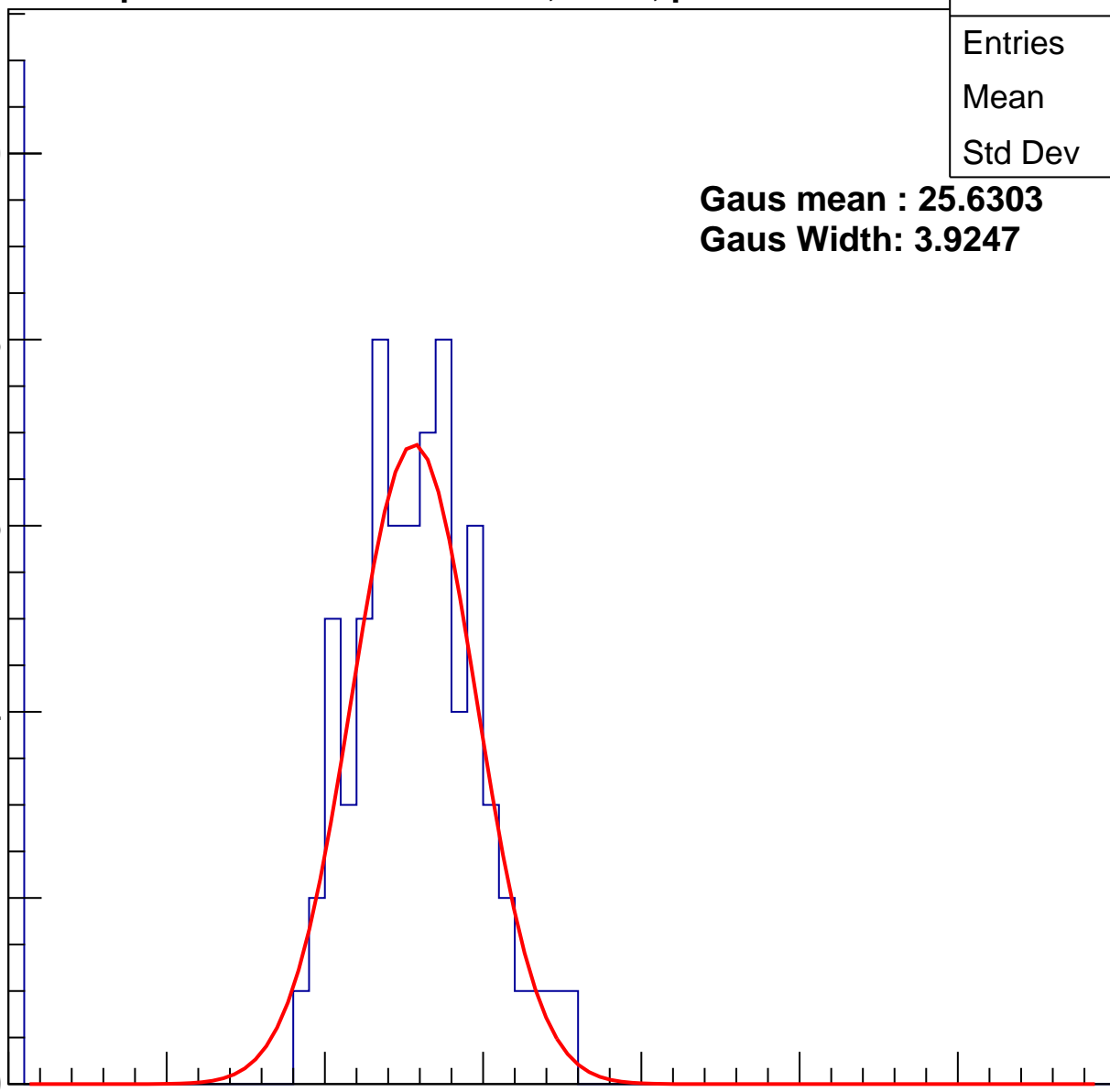
40

50

60

70

ampl



B1L103S, U21-ch116, adc1

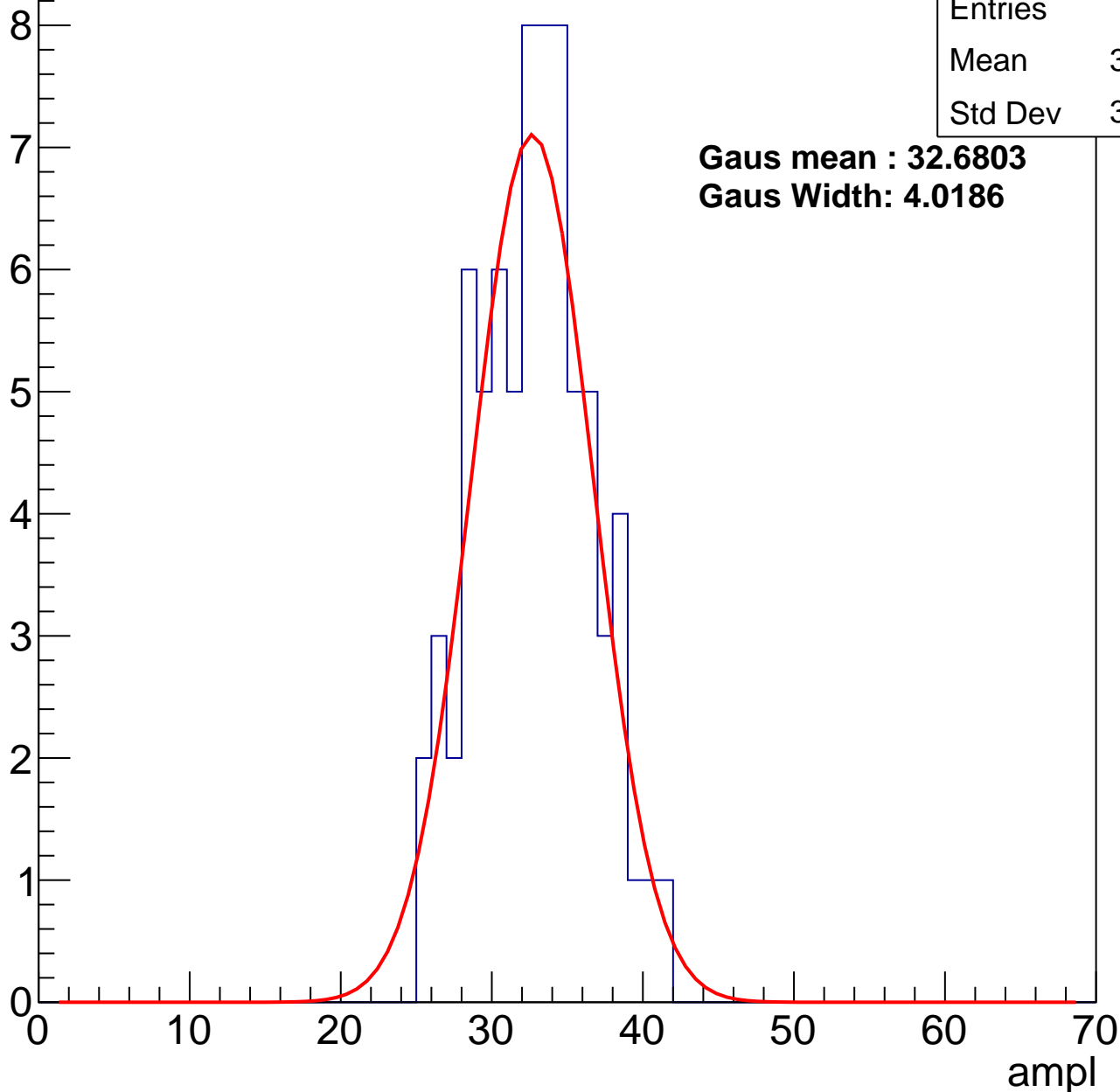
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.33
Std Dev	3.694

Gaus mean : 32.6803

Gaus Width: 4.0186



B1L103S, U21-ch116, adc2

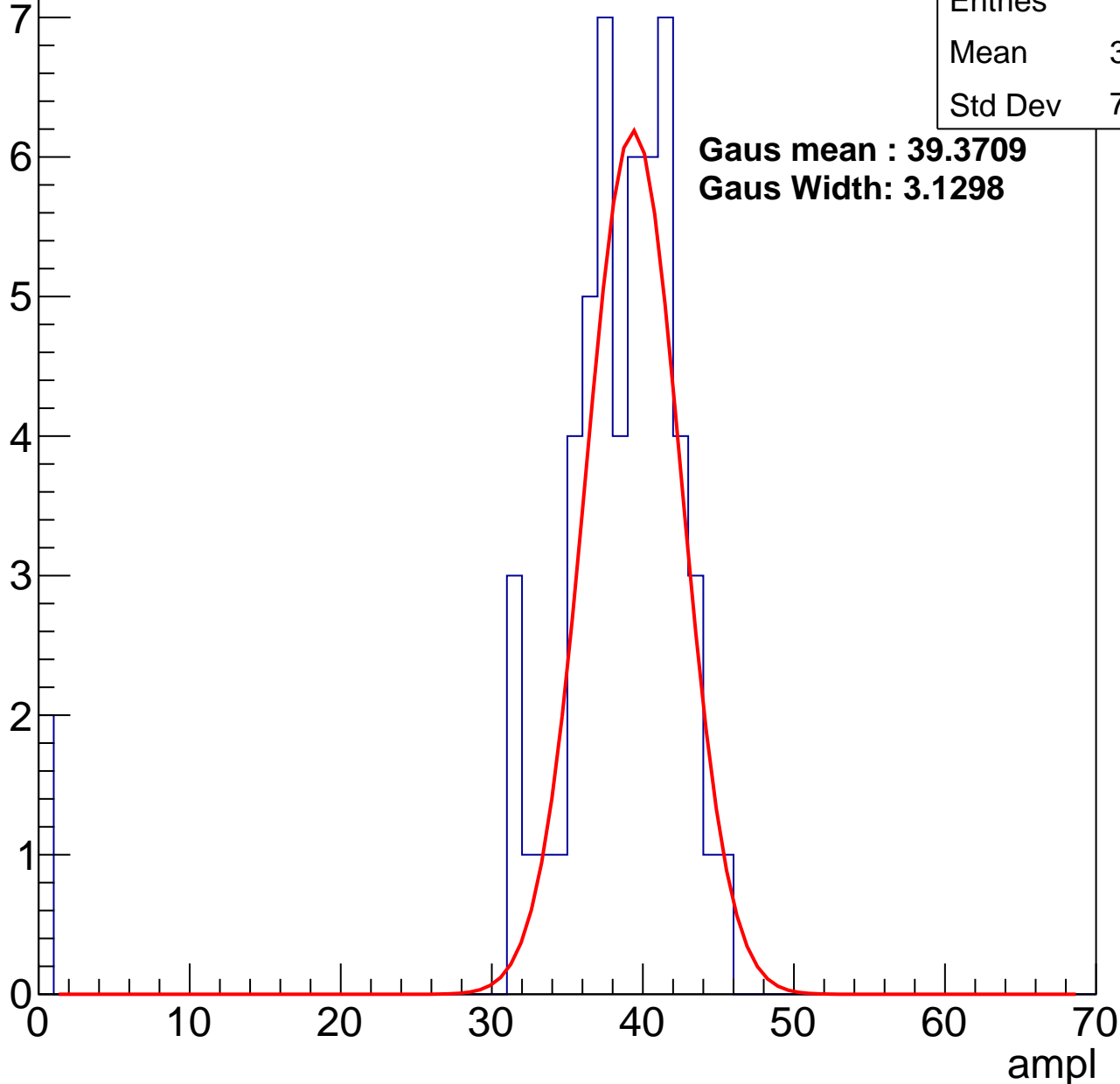
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	36.96
Std Dev	7.828

Gaus mean : 39.3709

Gaus Width: 3.1298



B1L103S, U21-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

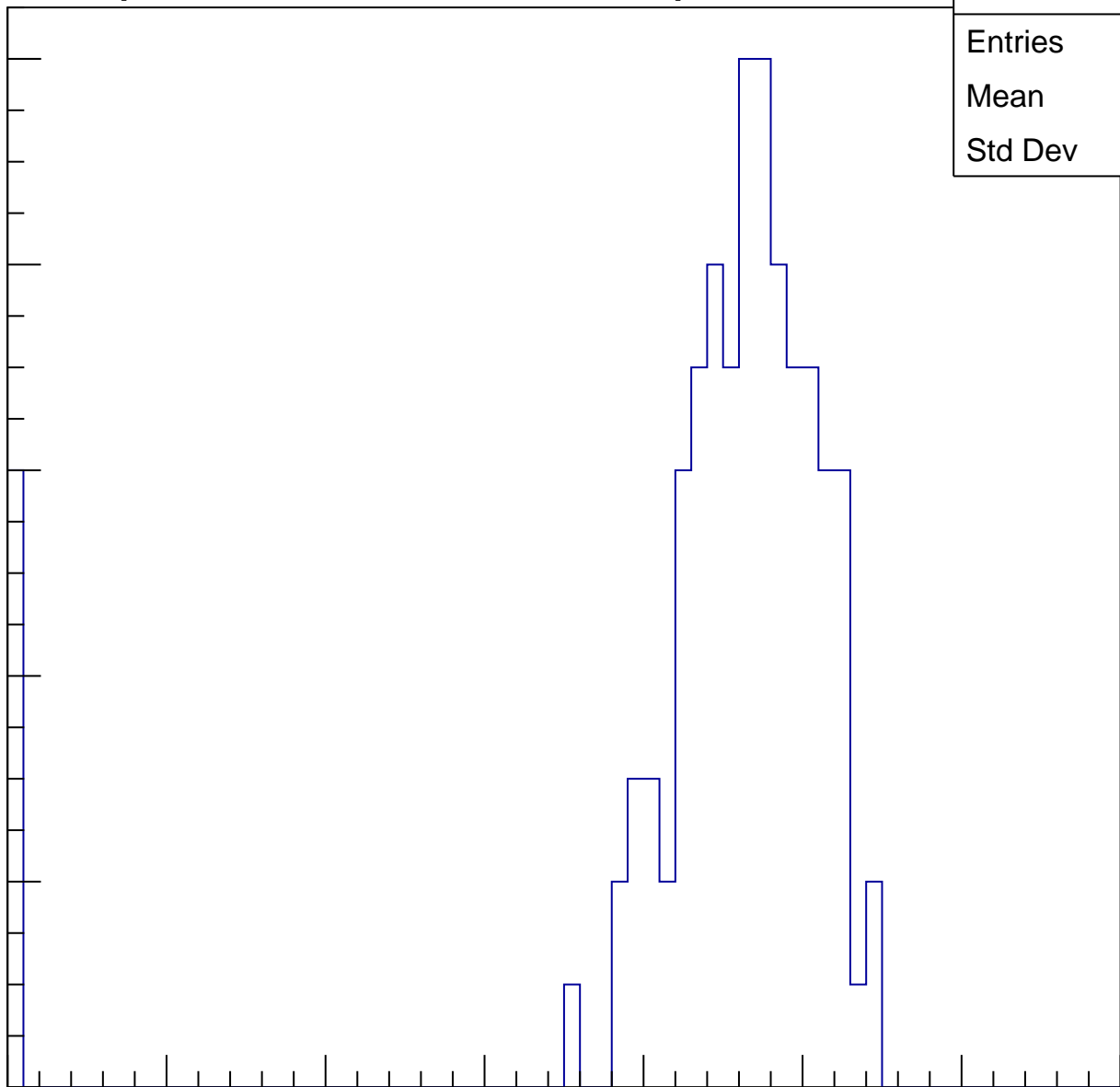
Entries	102
Mean	43.49
Std Dev	11.53

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

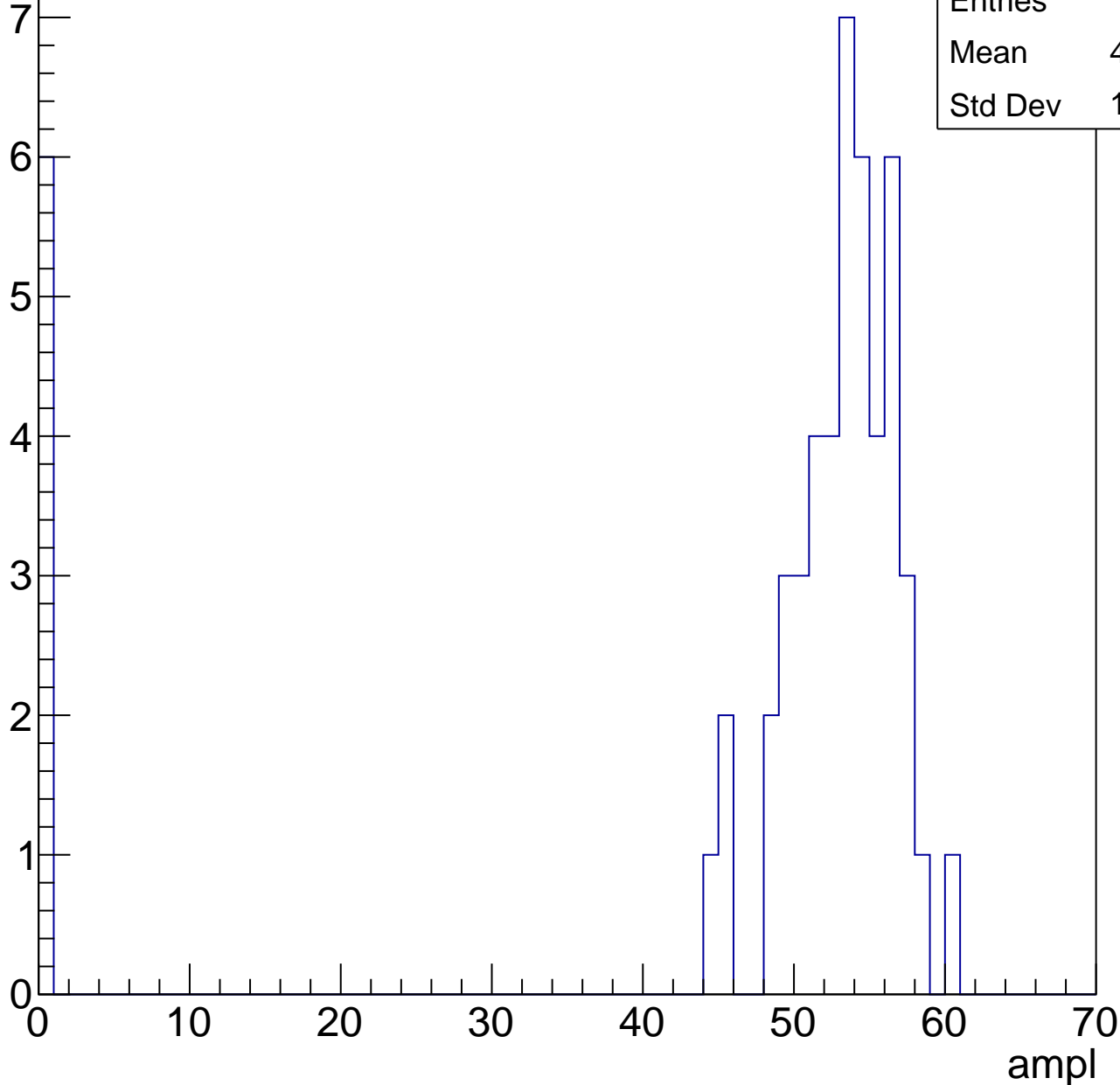


B1L103S, U21-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	46.77
Std Dev	17.02

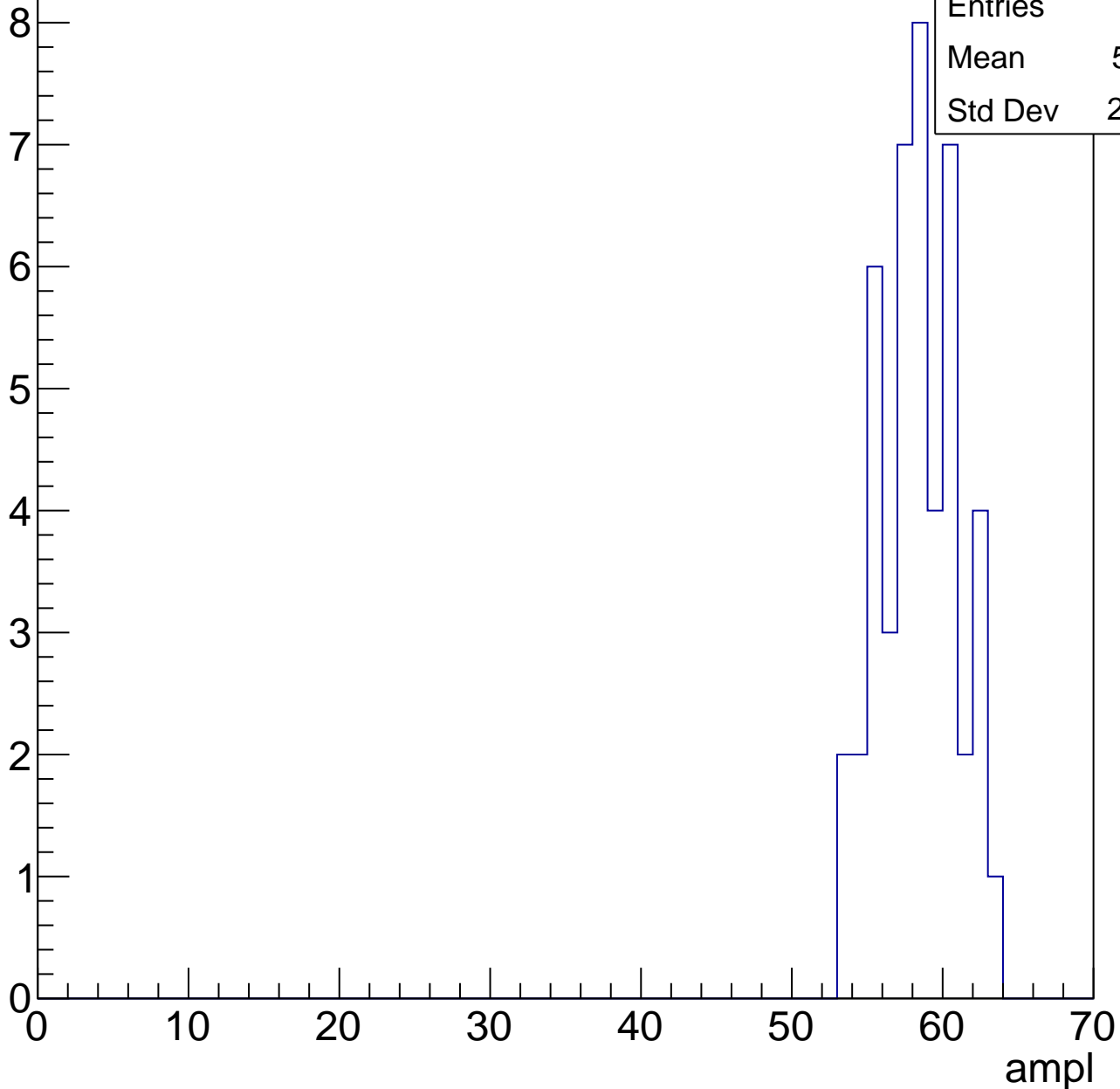


B1L103S, U21-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	57.91
Std Dev	2.527

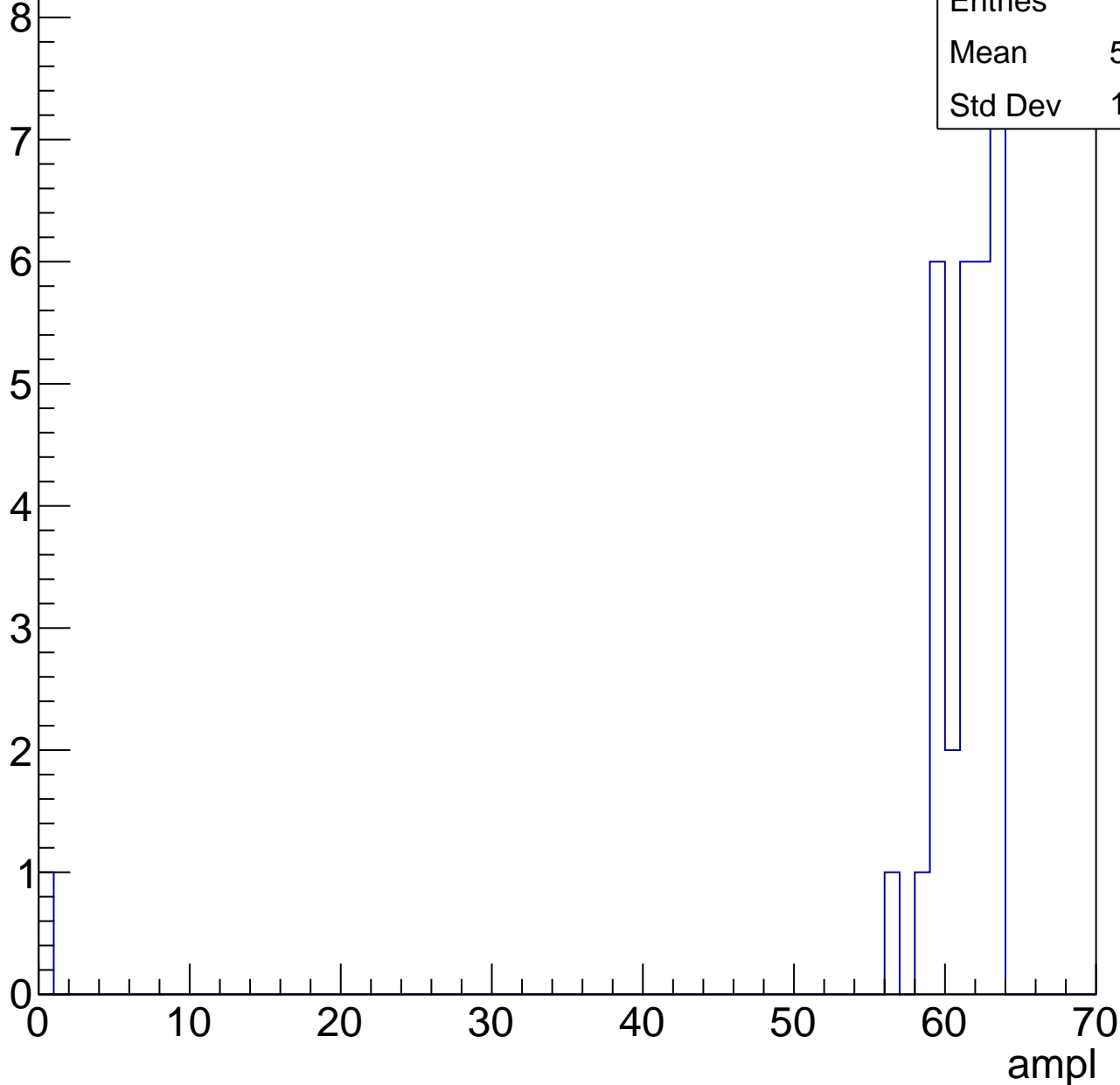


B1L103S, U21-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	59.03
Std Dev	10.92

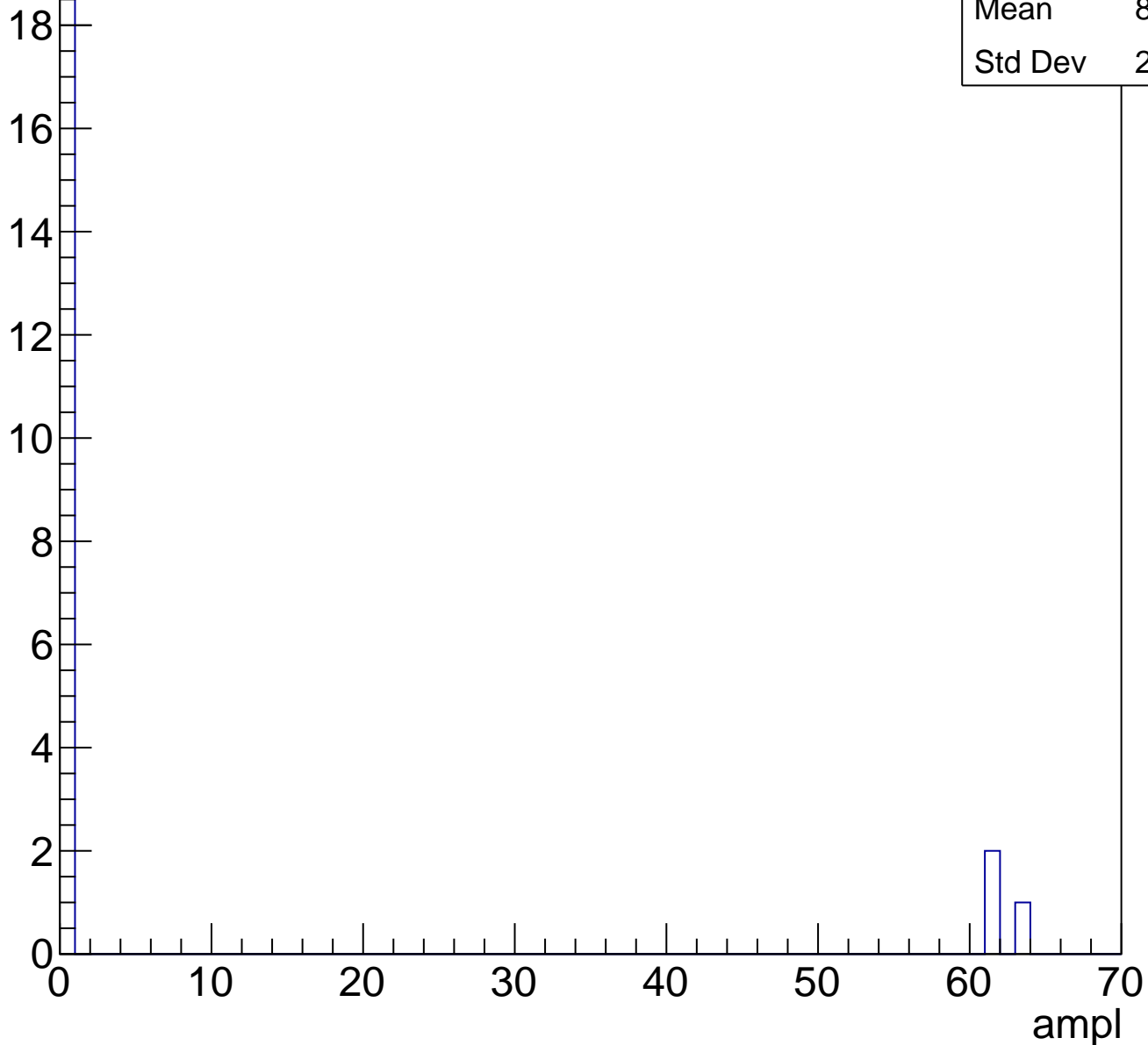


B1L103S, U21-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.409
Std Dev	21.17

Entry



B1L103S, U21-ch117, adc0

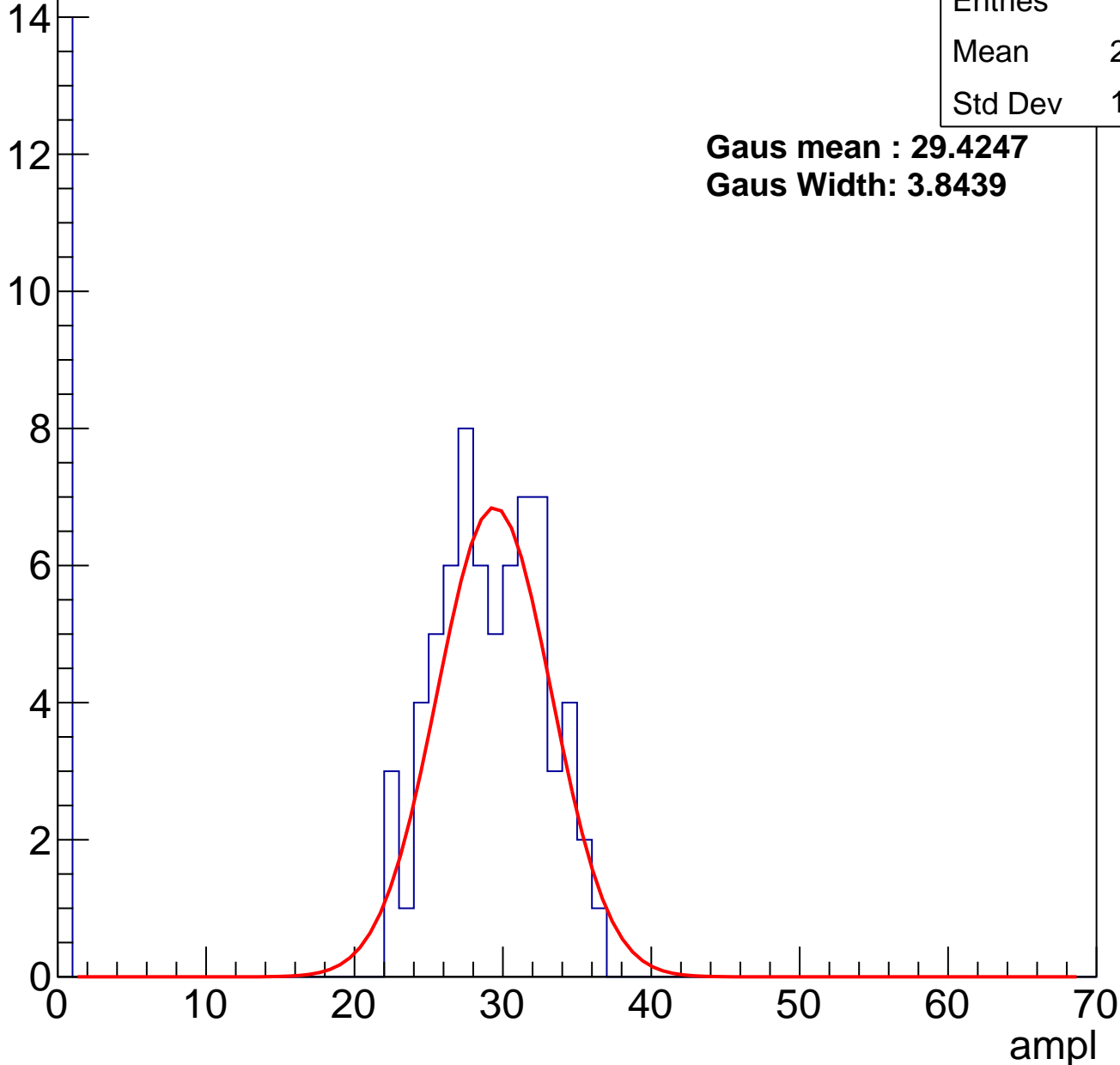
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.87
Std Dev	11.28

Gaus mean : 29.4247

Gaus Width: 3.8439

Entry



B1L103S, U21-ch117, adc1

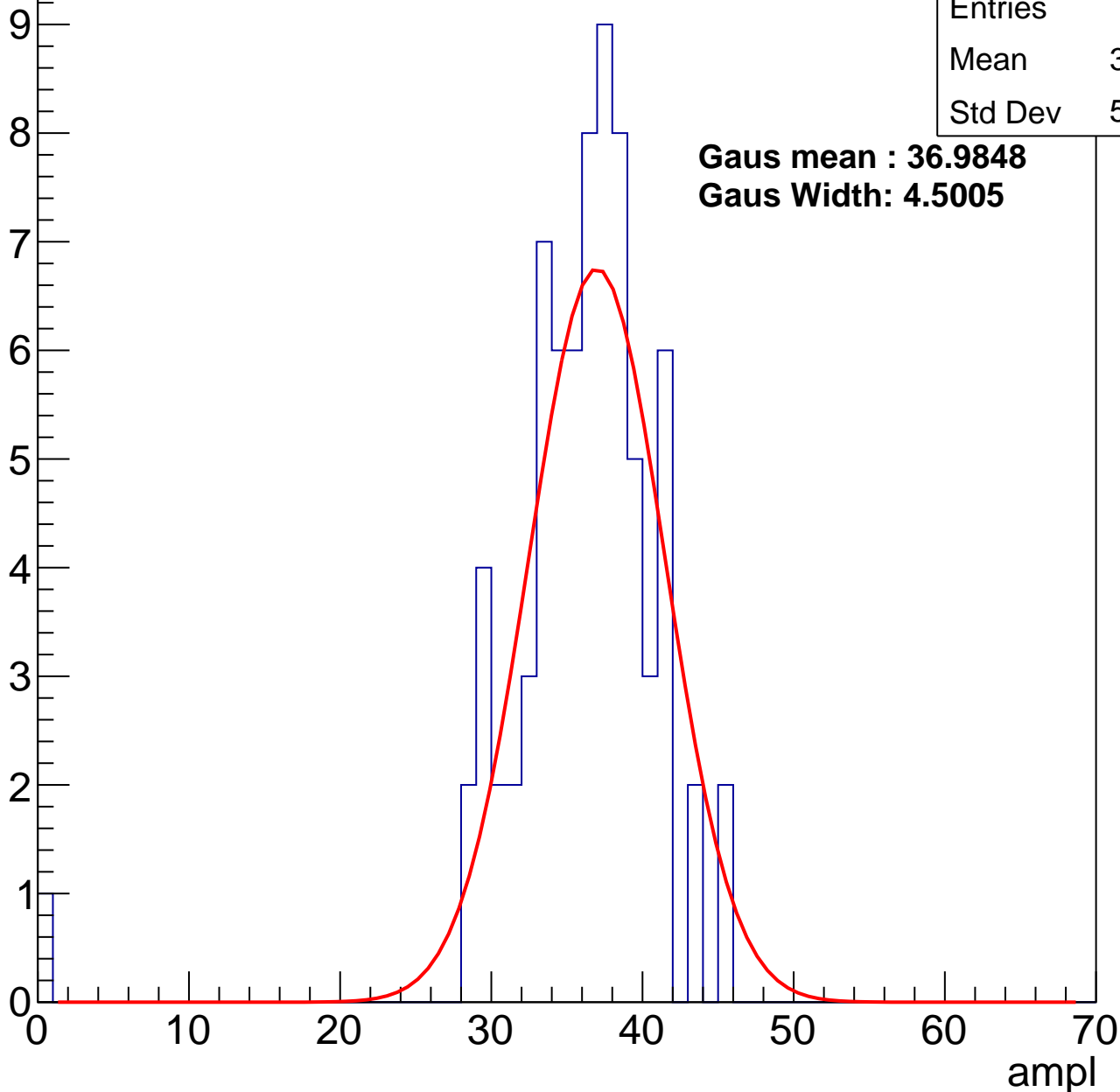
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	35.49
Std Dev	5.628

Gaus mean : 36.9848

Gaus Width: 4.5005



B1L103S, U21-ch117, adc2

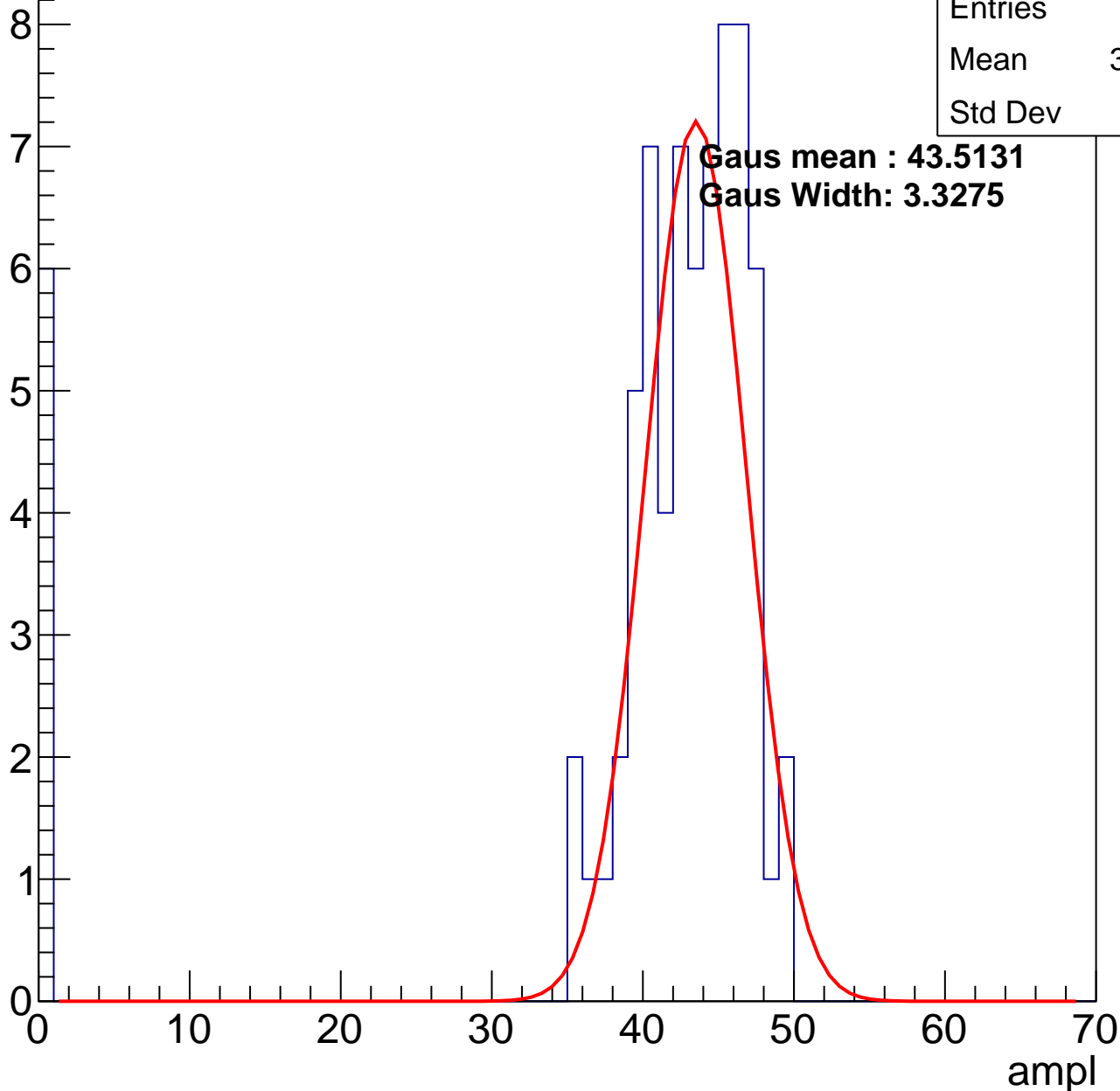
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	39.37
Std Dev	12.2

Gaus mean : 43.5131

Gaus Width: 3.3275



B1L103S, U21-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

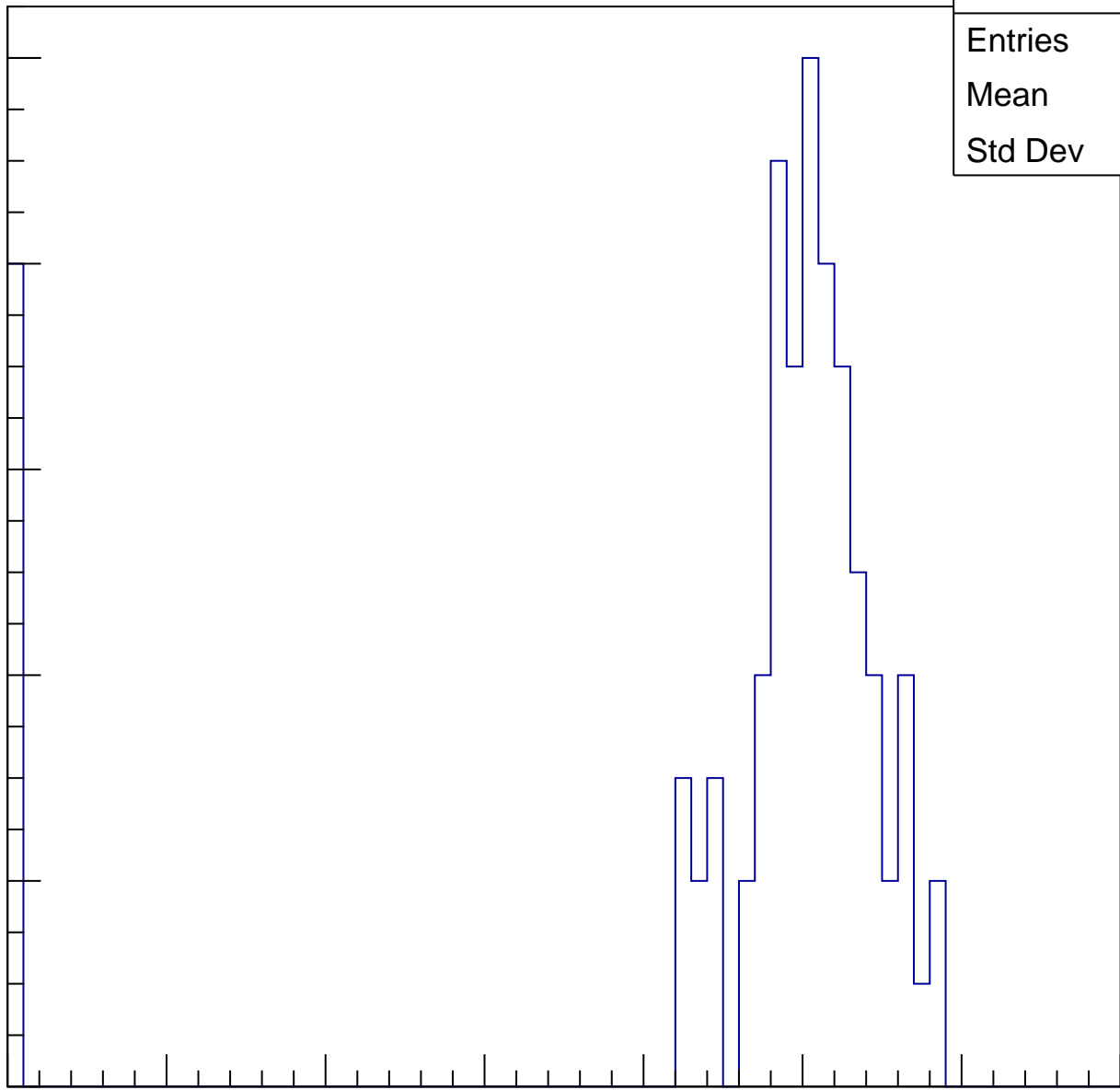
Entries	81
Mean	45.17
Std Dev	15.37

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

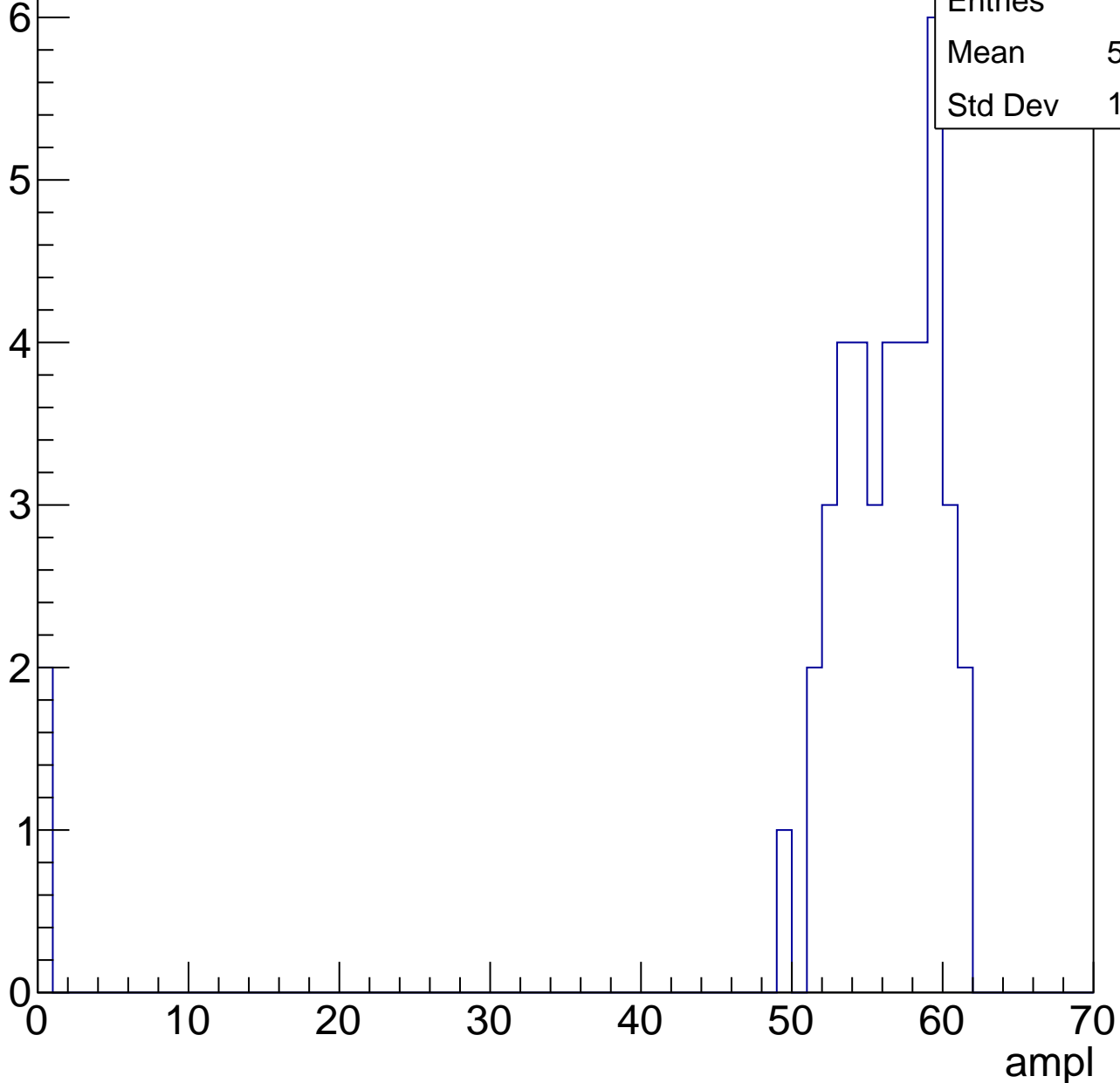


B1L103S, U21-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	53.33
Std Dev	12.29

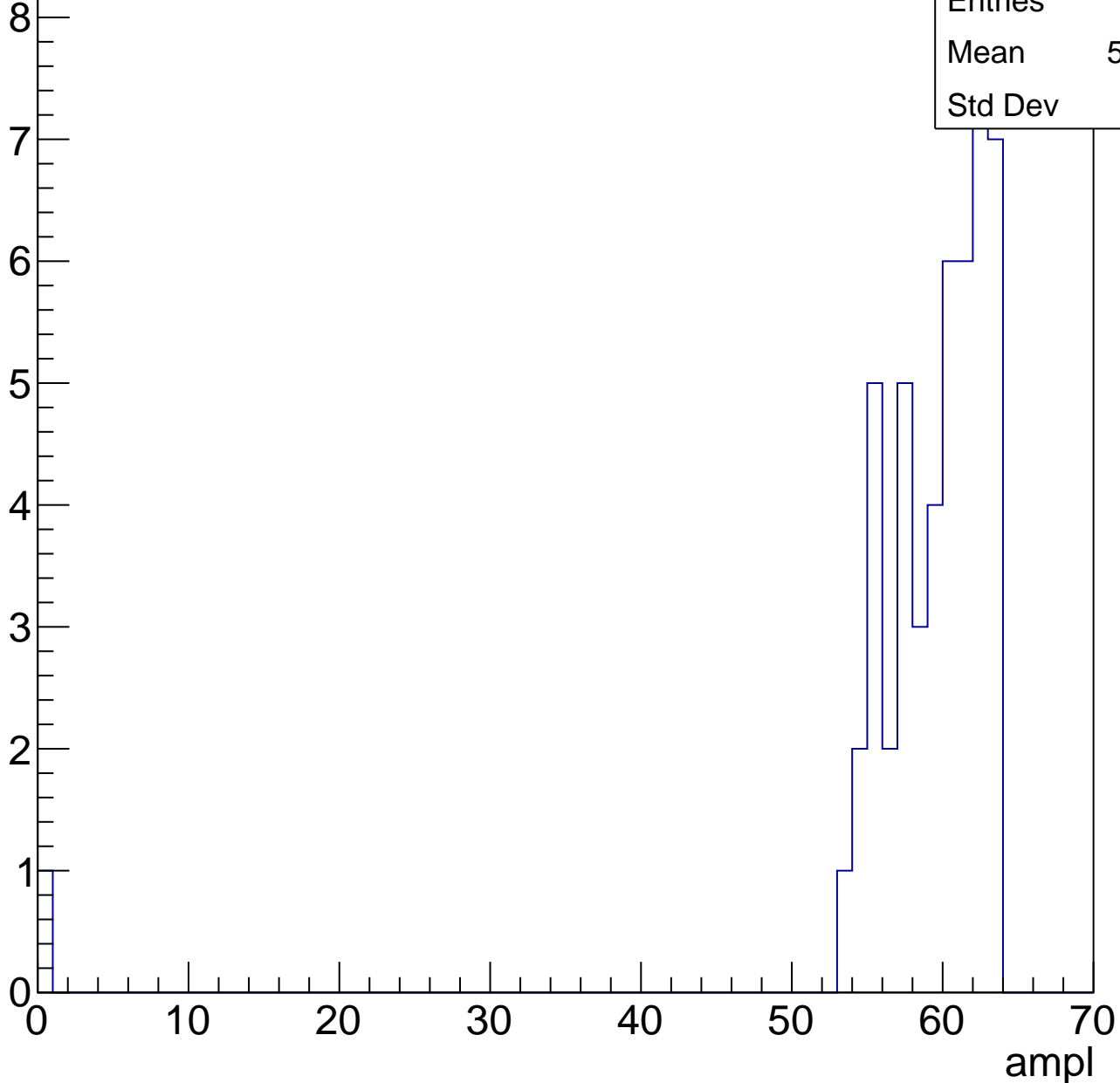


B1L103S, U21-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.12
Std Dev	8.79



B1L103S, U21-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

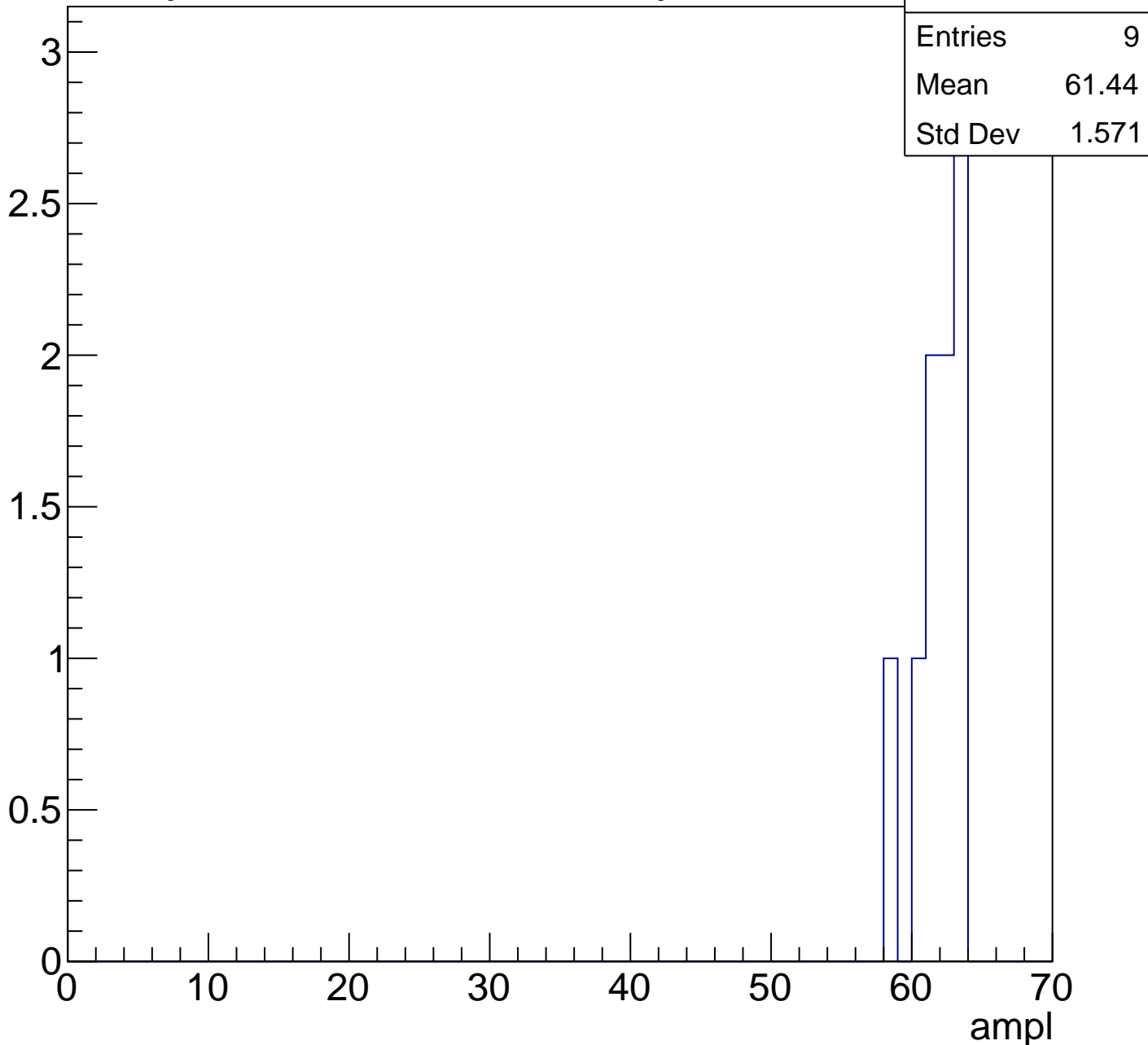
9

Mean

61.44

Std Dev

1.571

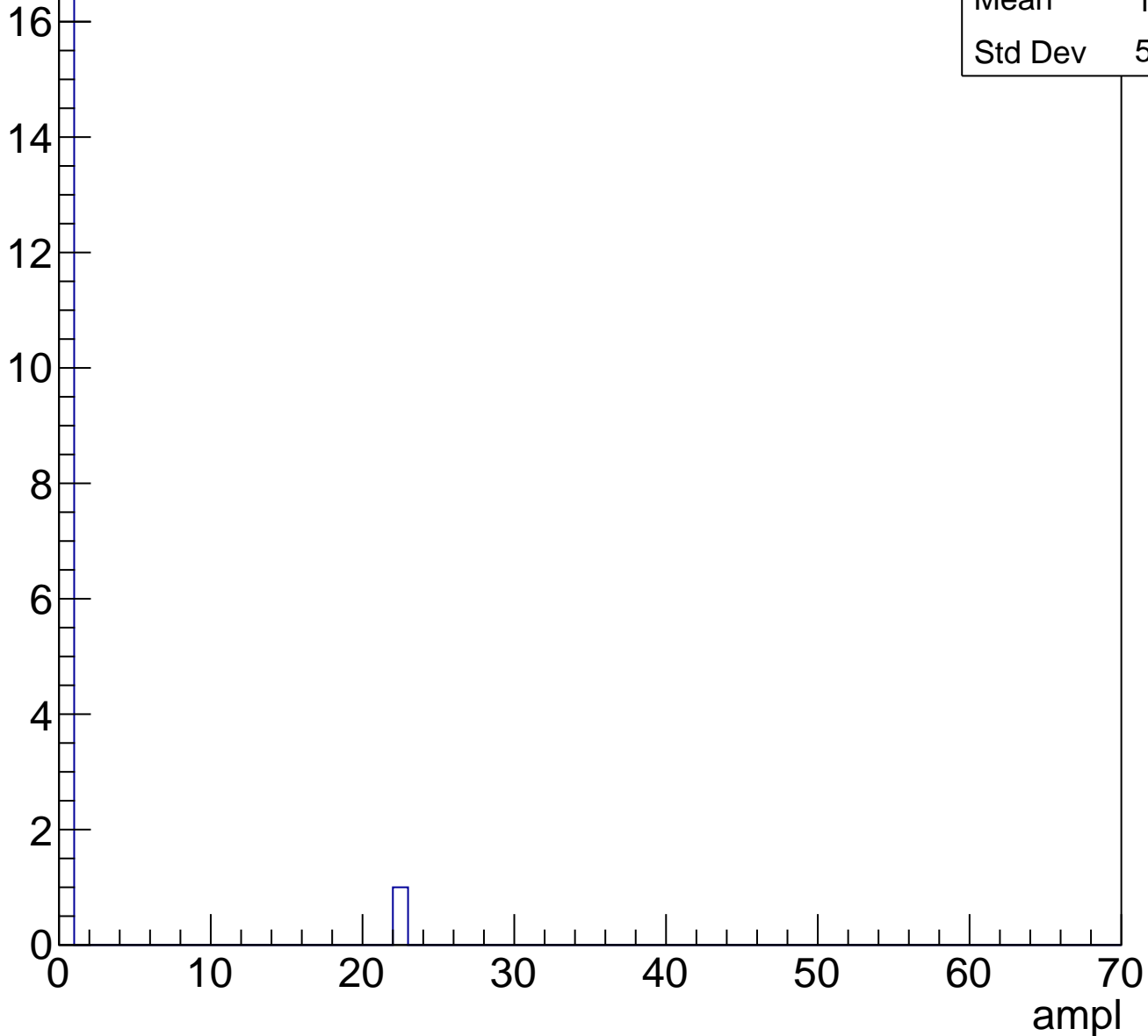


B1L103S, U21-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.222
Std Dev	5.039

Entry



B1L103S, U21-ch118, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	110
Mean	18.05
Std Dev	12.09

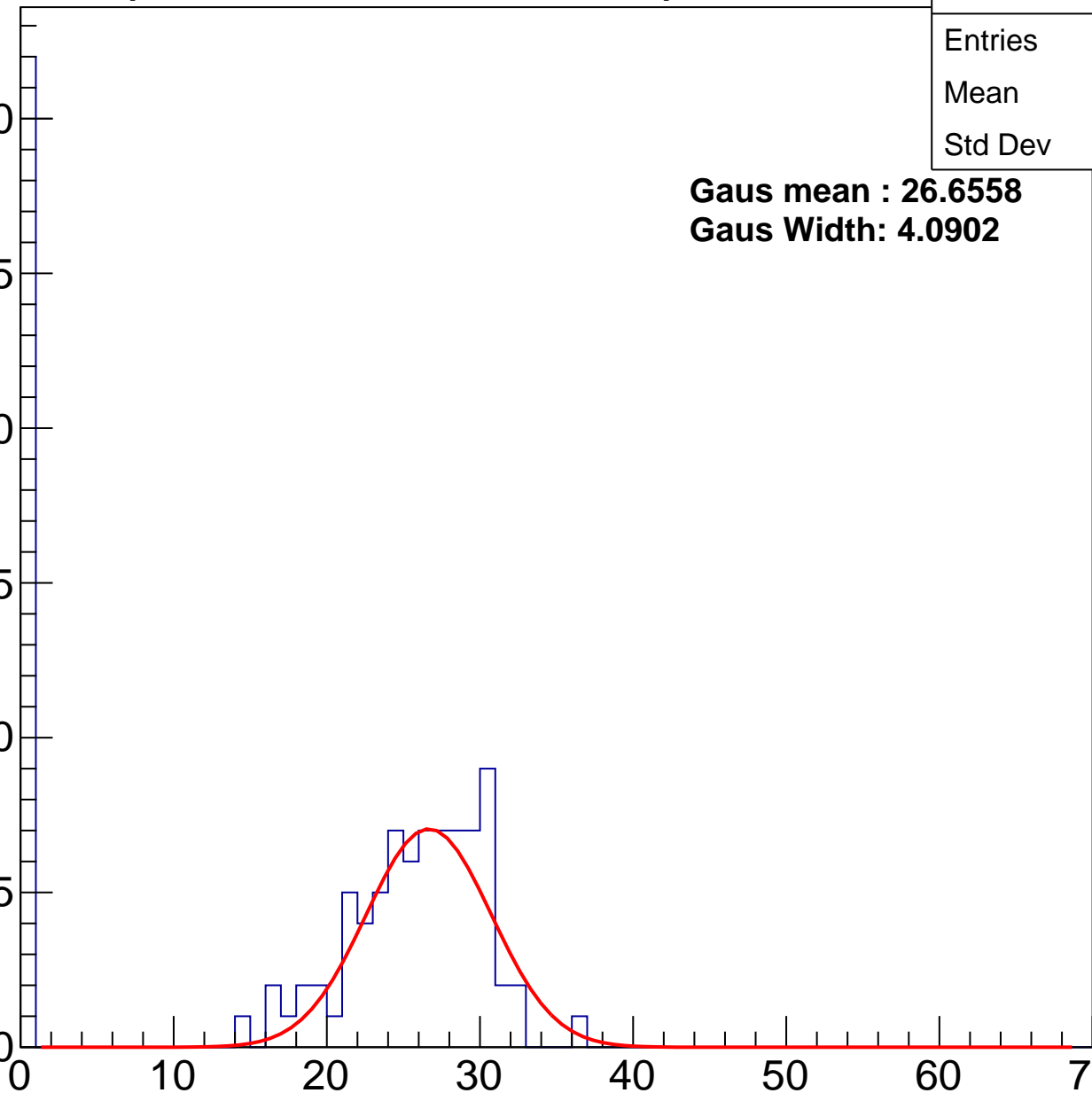
Gaus mean : 26.6558

Gaus Width: 4.0902

Entry

30
25
20
15
10
5
0

ampl



B1L103S, U21-ch118, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	30.54
Std Dev	6.763

Gaus mean : 34.8048

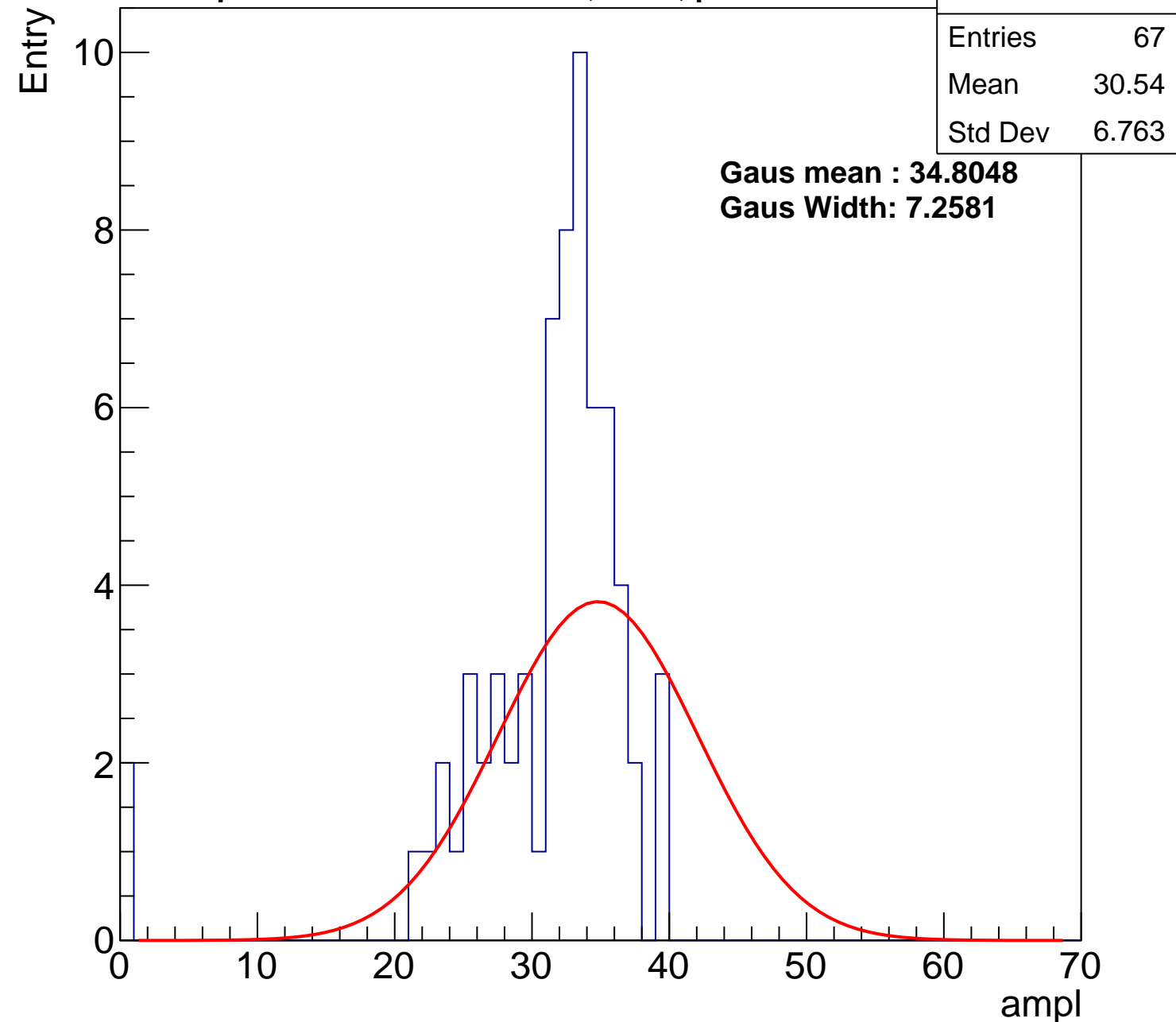
Gaus Width: 7.2581

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U21-ch118, adc2

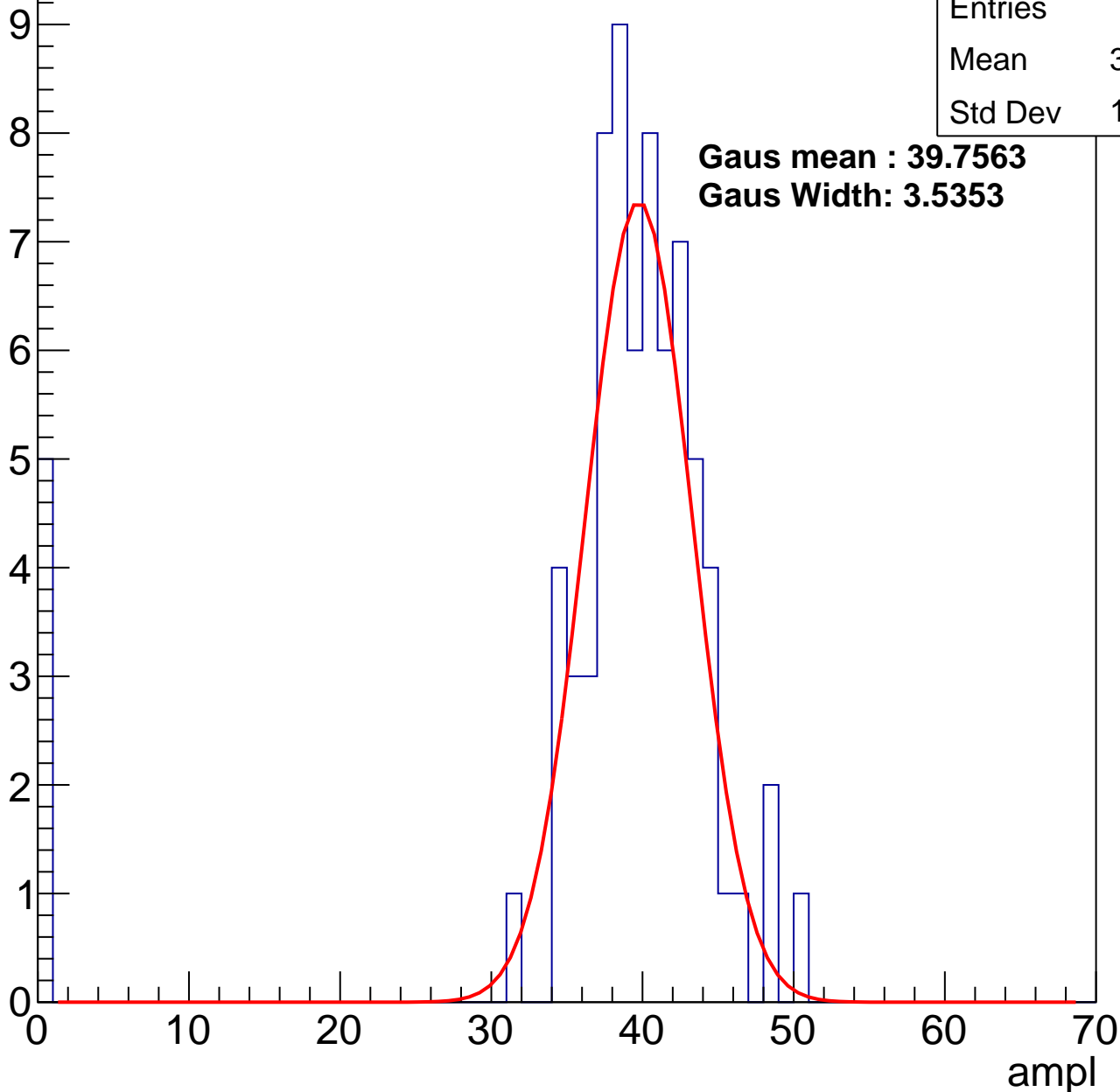
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	37.03
Std Dev	10.55

Gaus mean : 39.7563

Gaus Width: 3.5353



B1L103S, U21-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

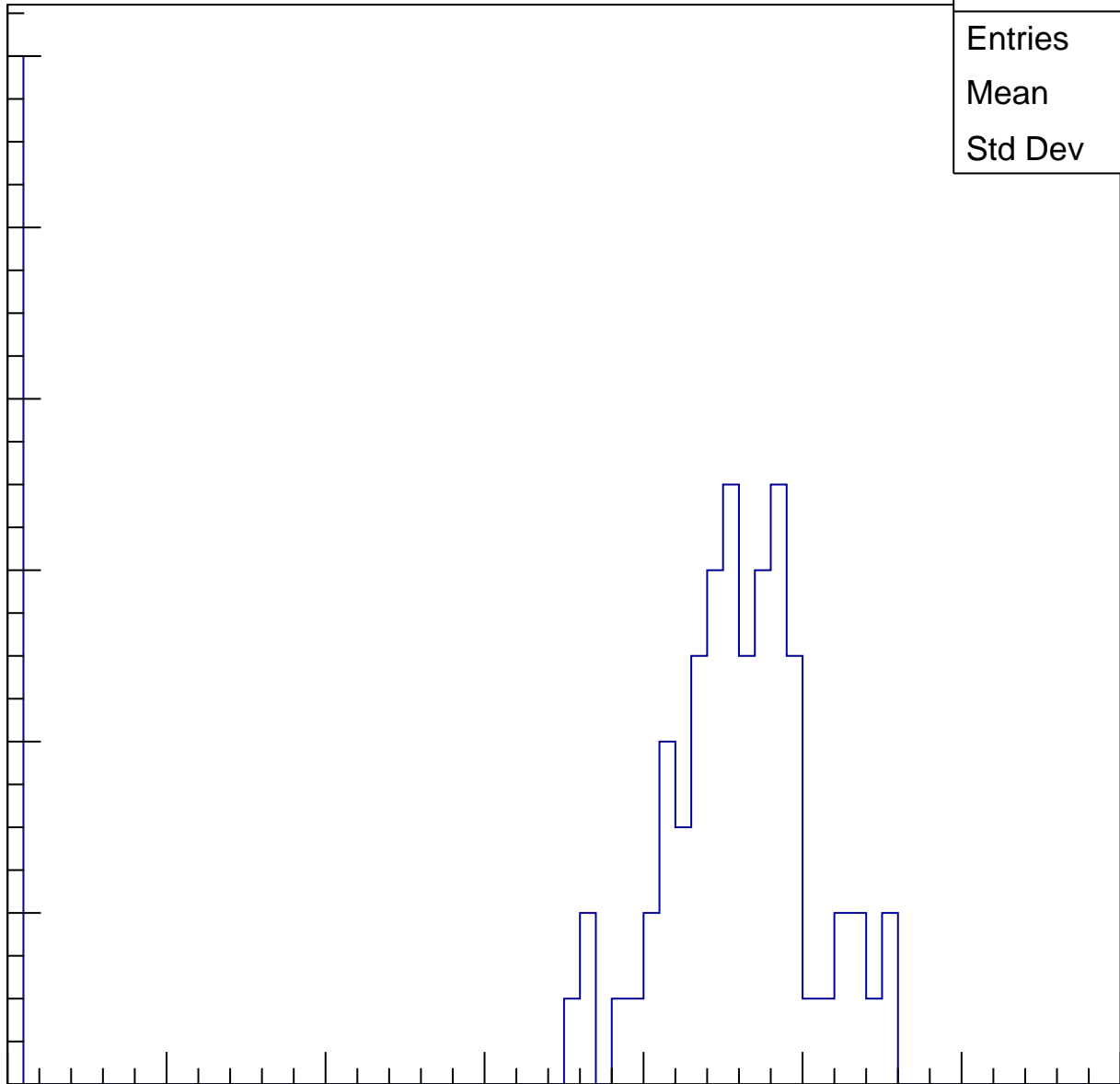
Entries	76
Mean	38.37
Std Dev	17.1

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

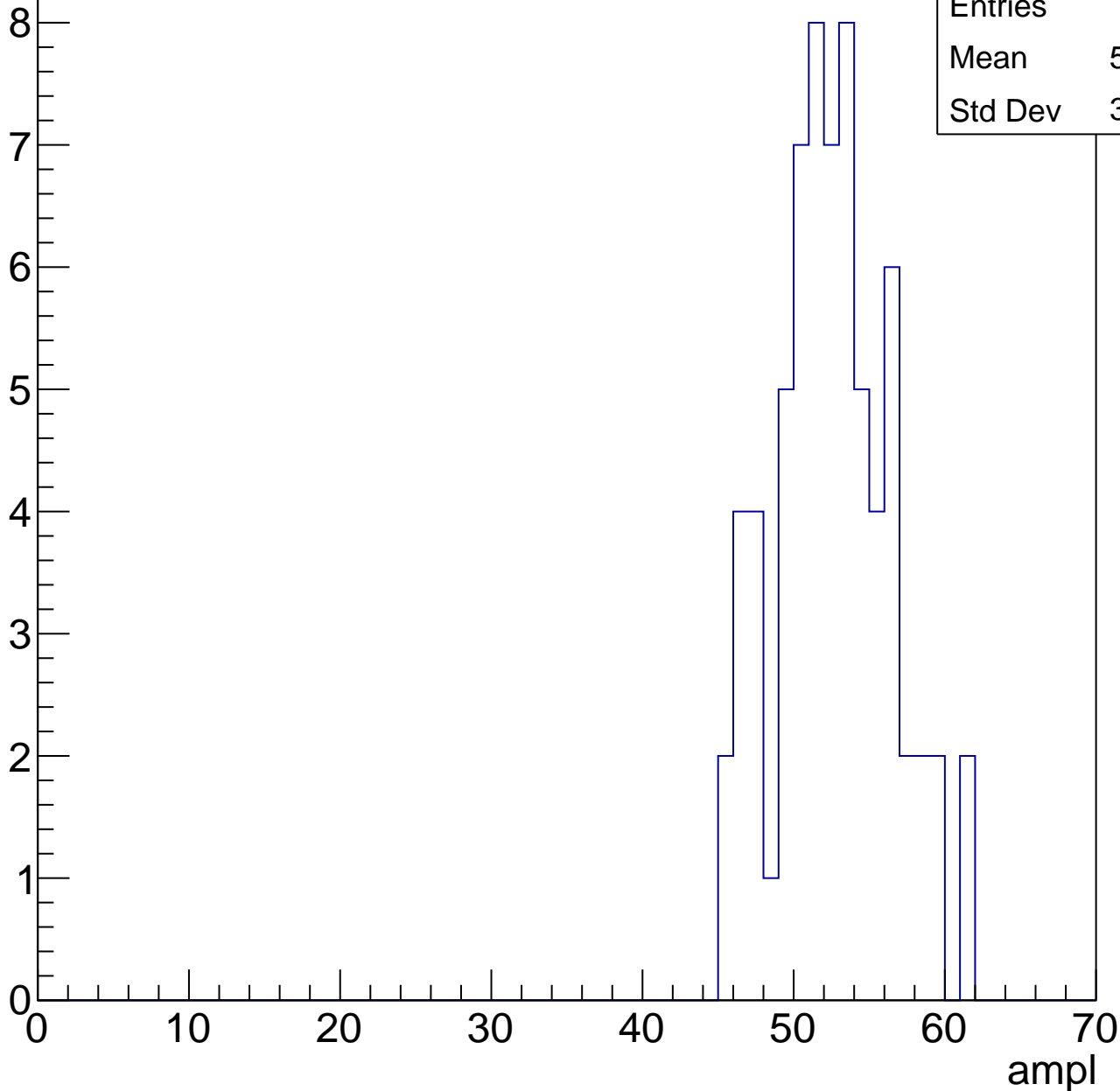


B1L103S, U21-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	52.13
Std Dev	3.768

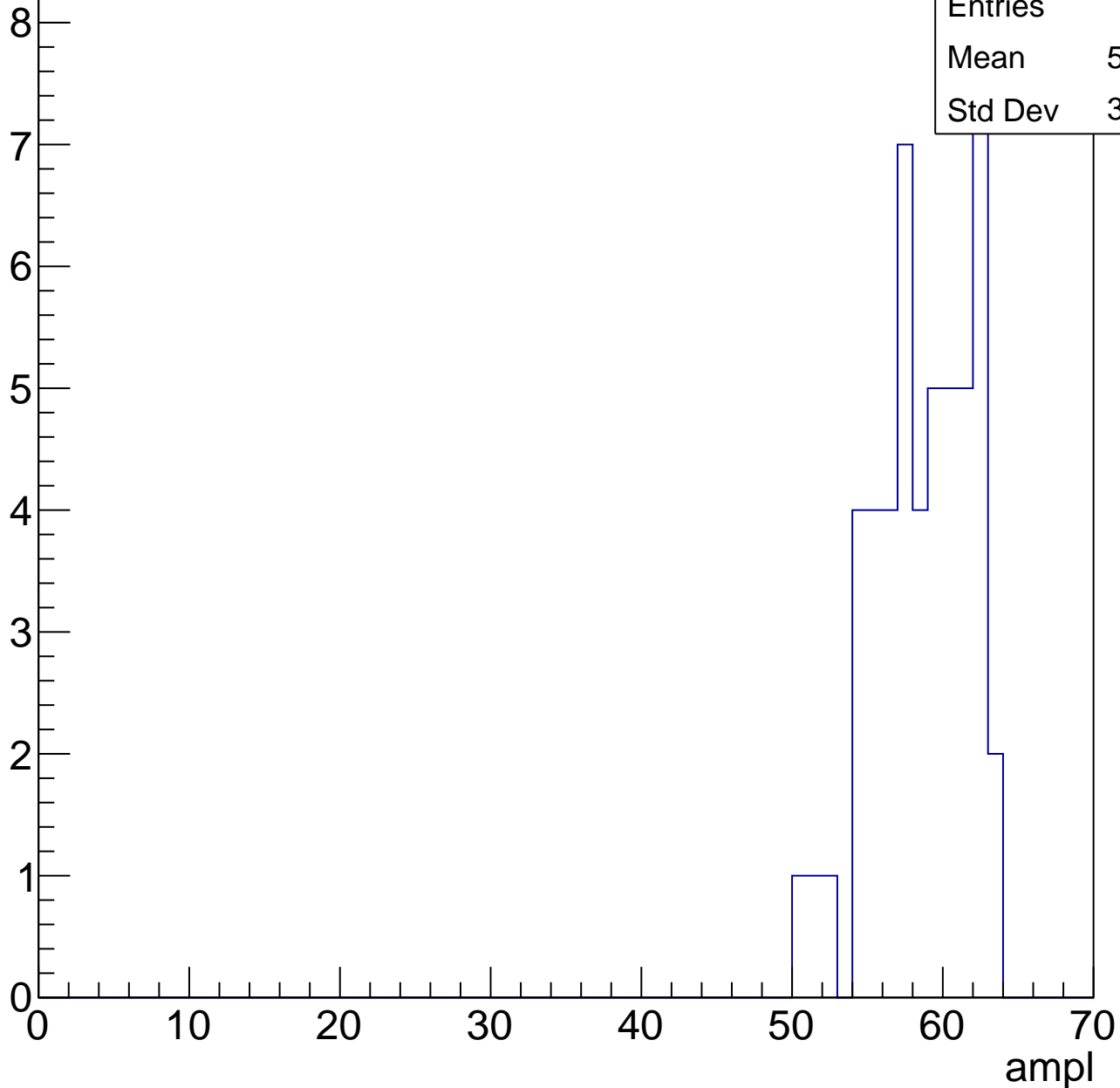


B1L103S, U21-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.16
Std Dev	3.189

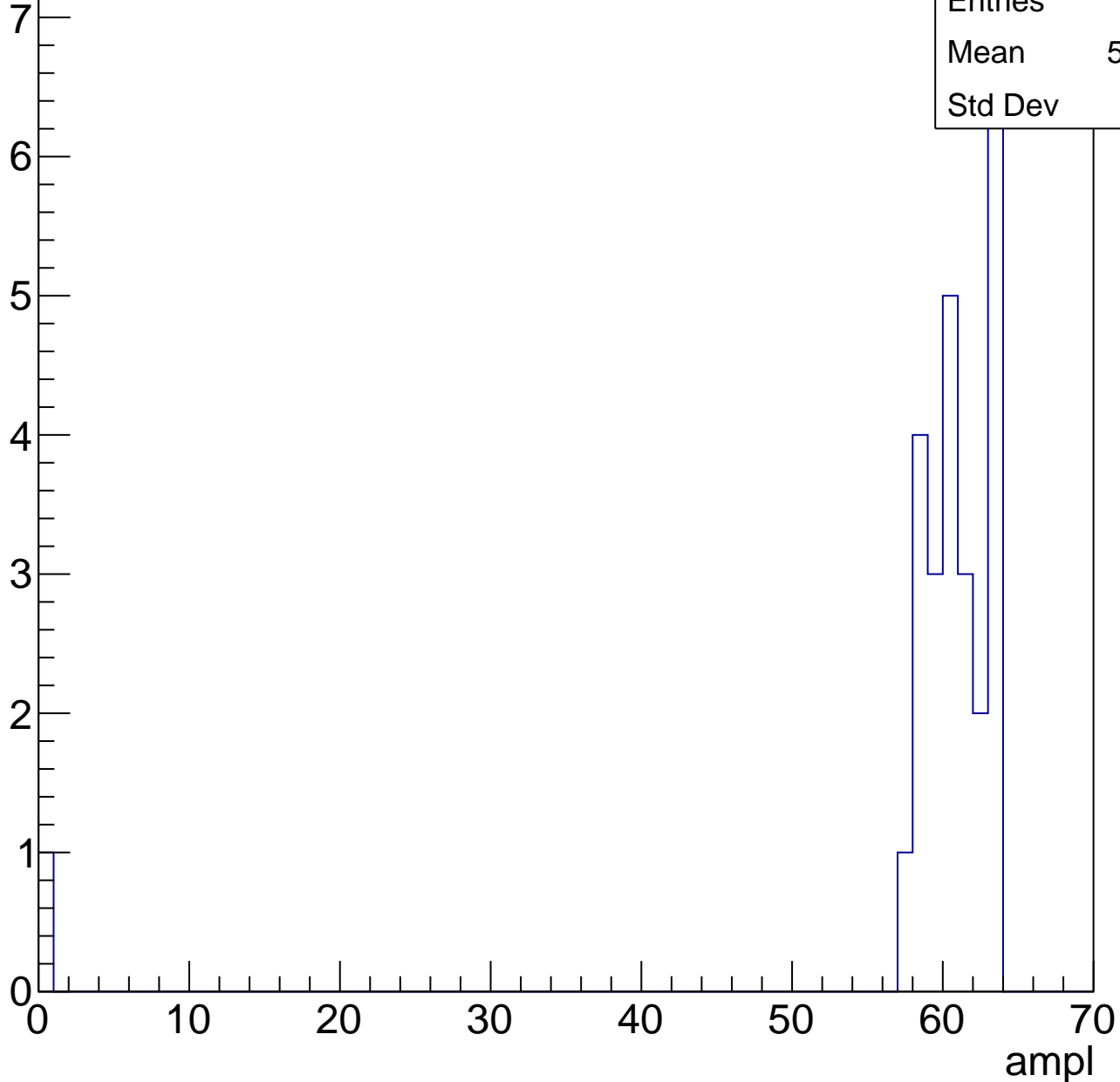


B1L103S, U21-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.23
Std Dev	11.8



B1L103S, U21-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U21-ch119, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	19.16
Std Dev	13.94

Gaus mean : 29.5781

Gaus Width: 2.6106

Entry

30

25

20

15

10

5

0

0

10

20

30

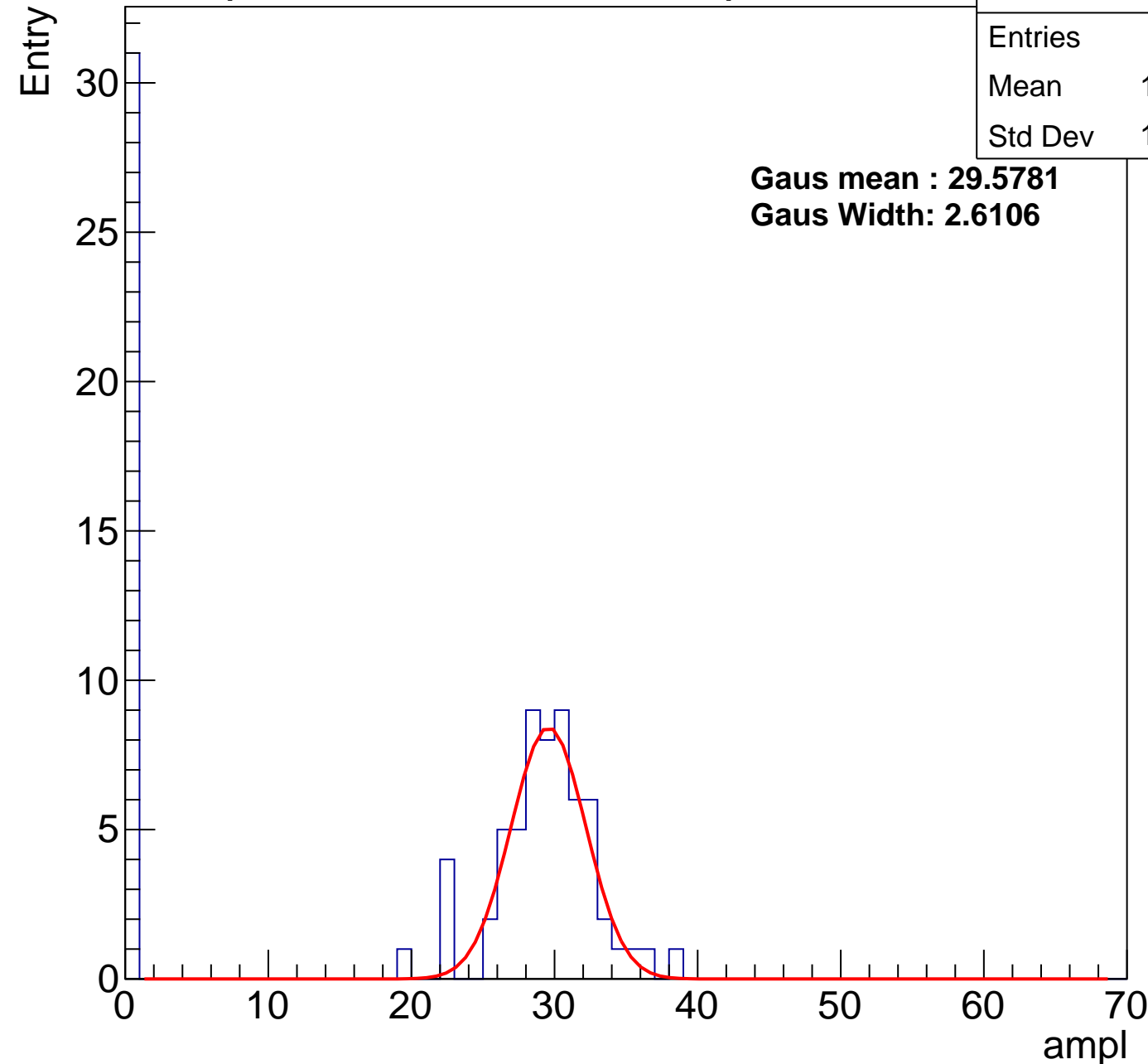
40

50

60

70

ampl



B1L103S, U21-ch119, adc1

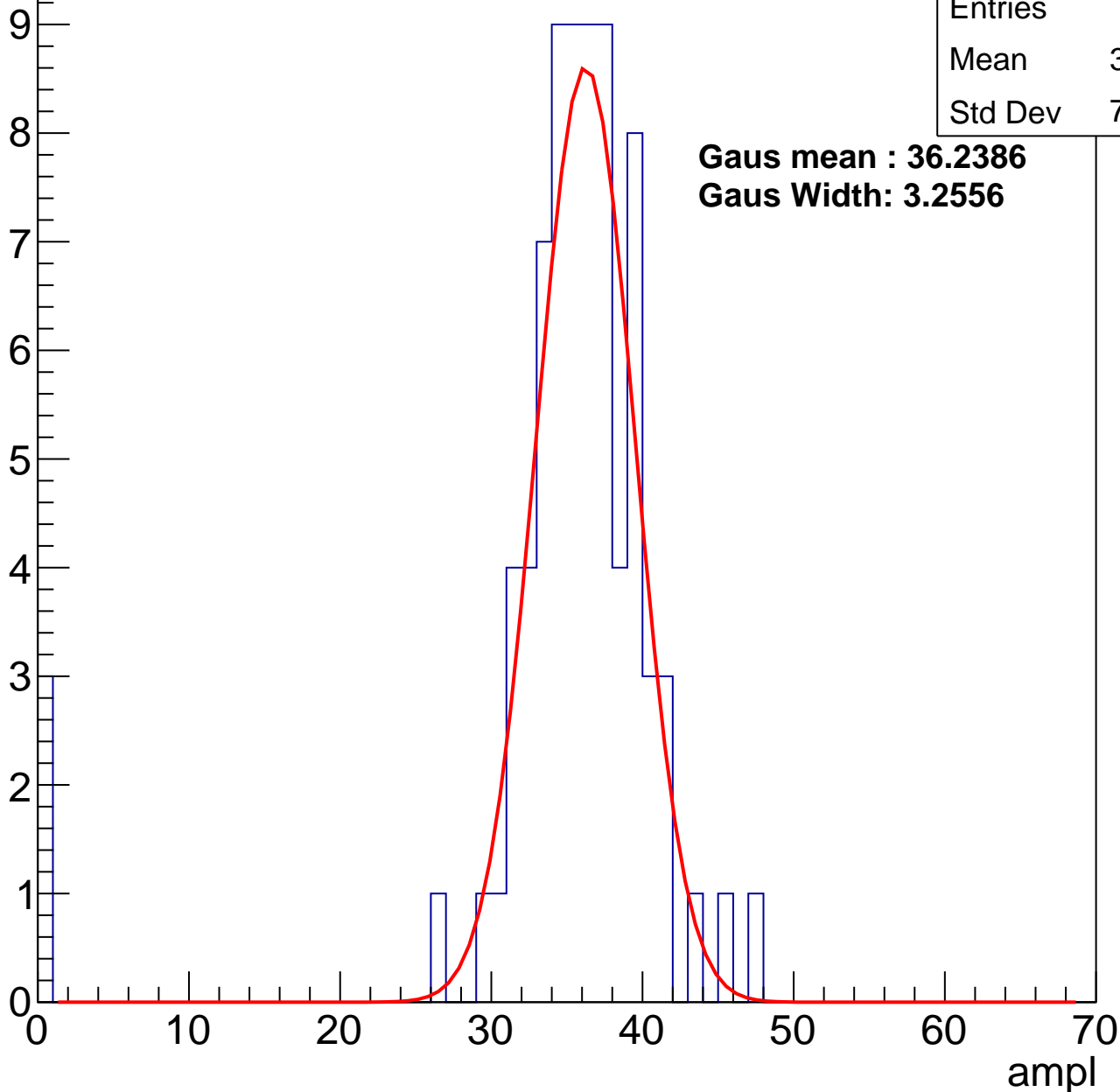
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.46
Std Dev	7.706

Gaus mean : 36.2386

Gaus Width: 3.2556



B1L103S, U21-ch119, adc2

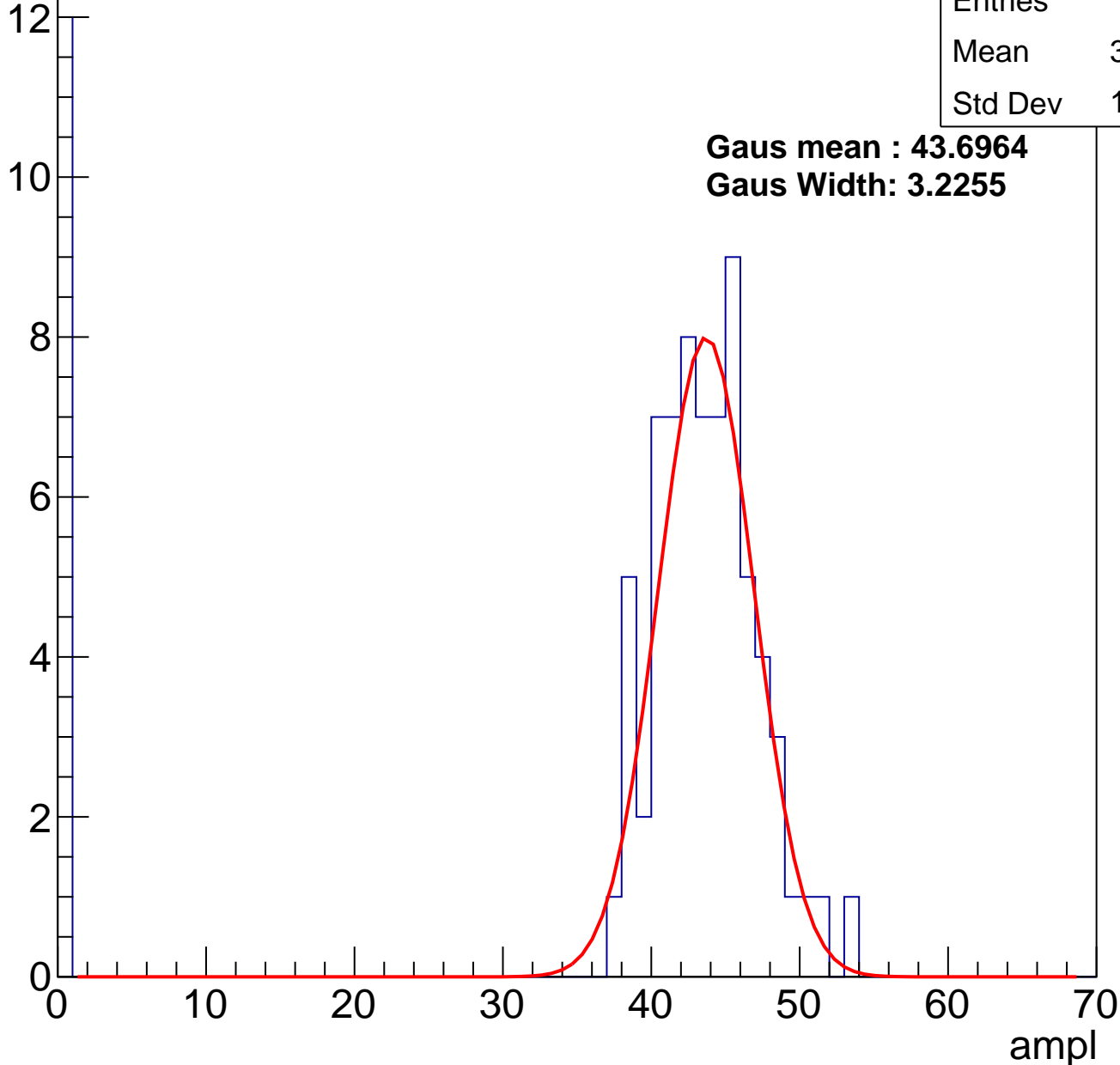
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	36.88
Std Dev	15.68

Gaus mean : 43.6964

Gaus Width: 3.2255

Entry

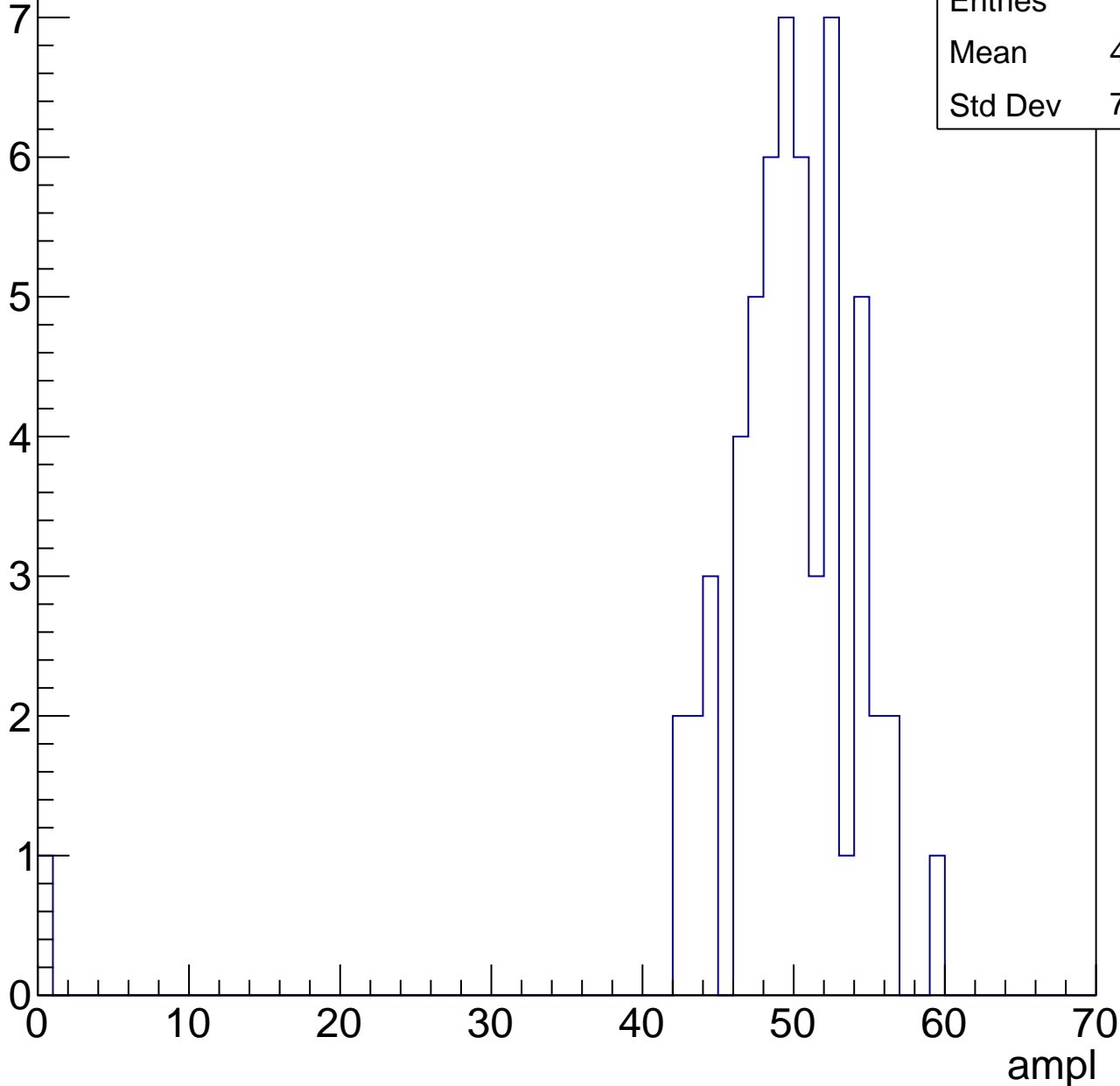


B1L103S, U21-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	48.65
Std Dev	7.484

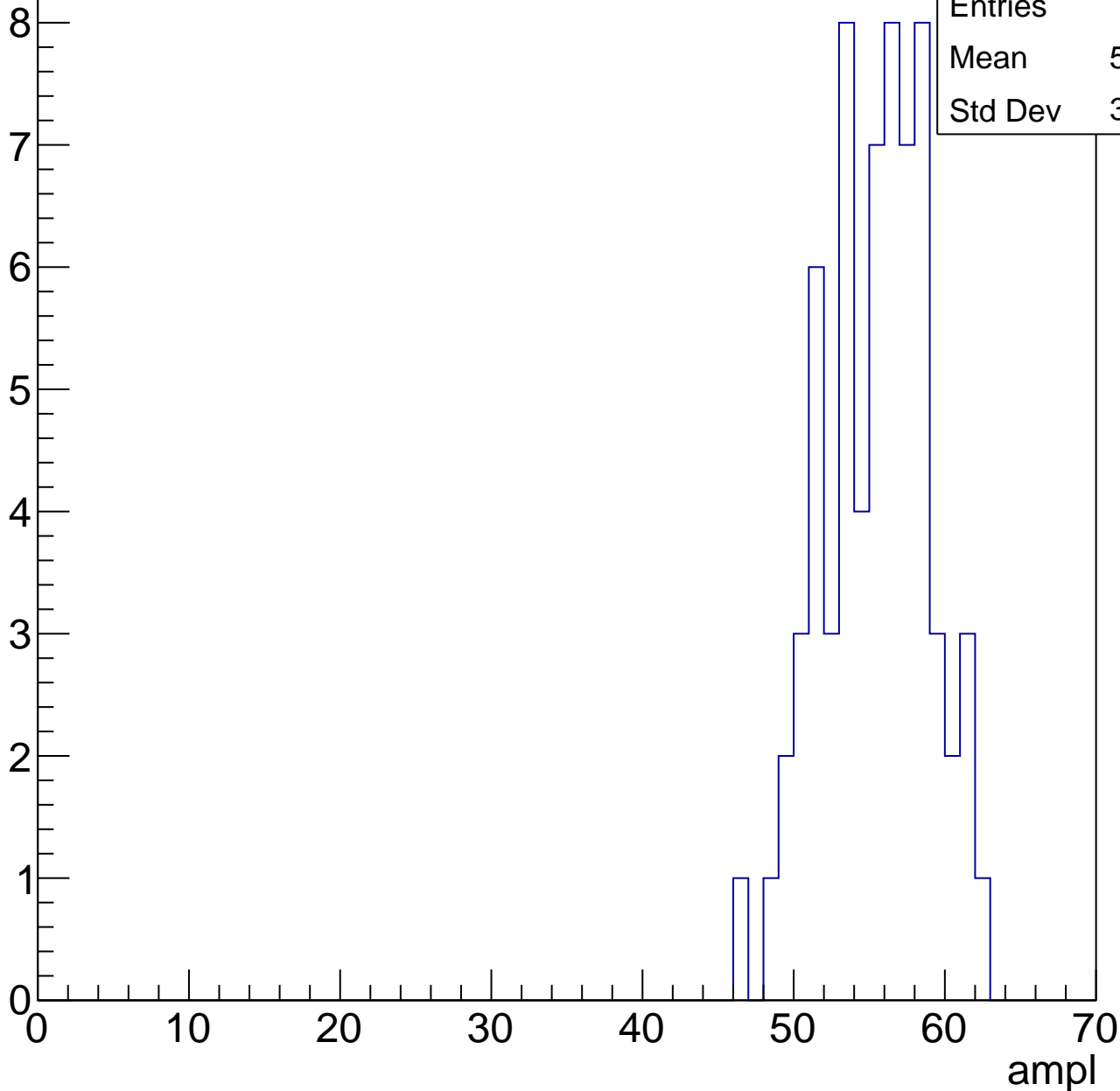


B1L103S, U21-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	54.96
Std Dev	3.453



B1L103S, U21-ch119, adc5

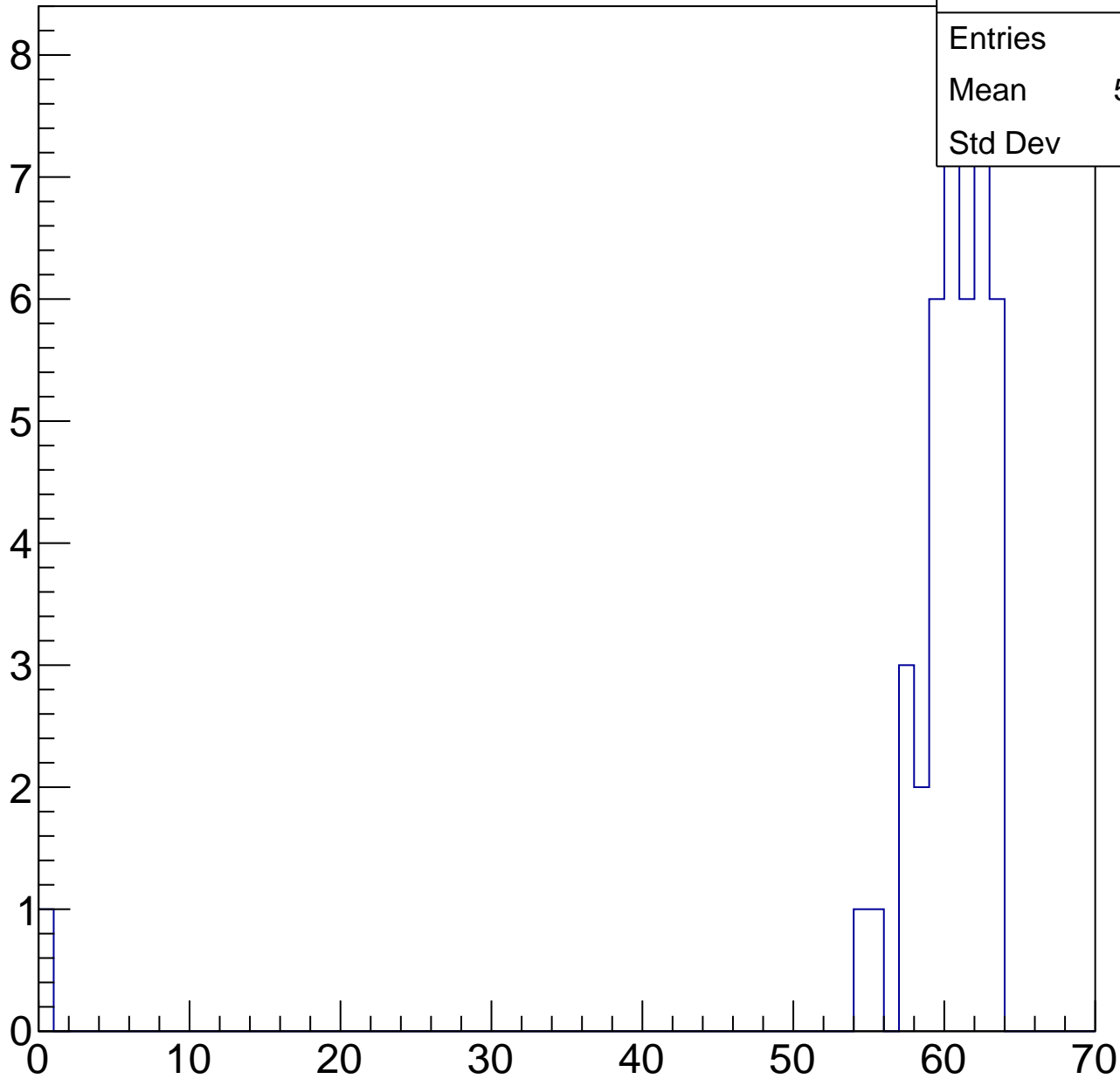
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.81
Std Dev	9.43

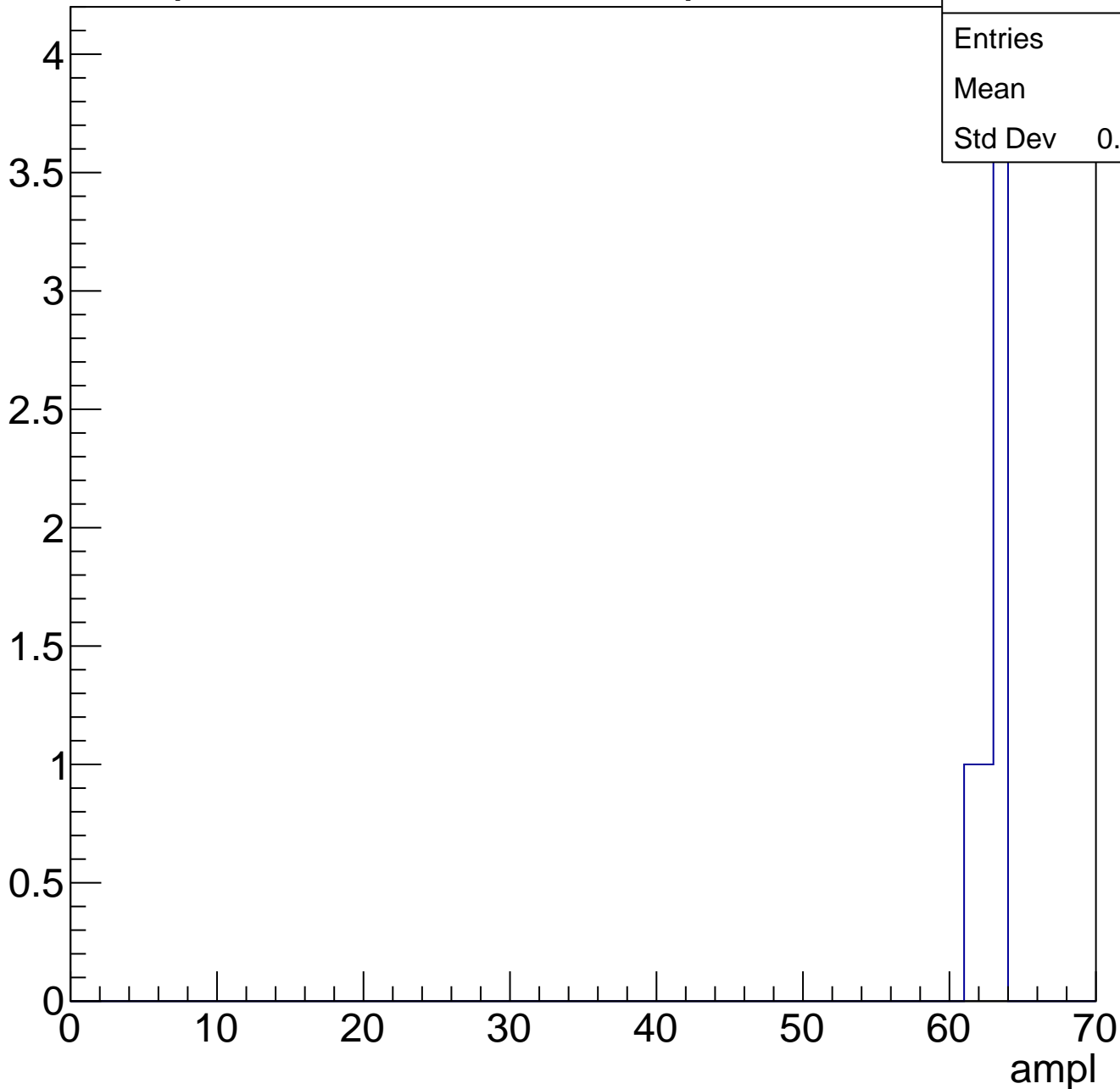
ampl



B1L103S, U21-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U21-ch120, adc0

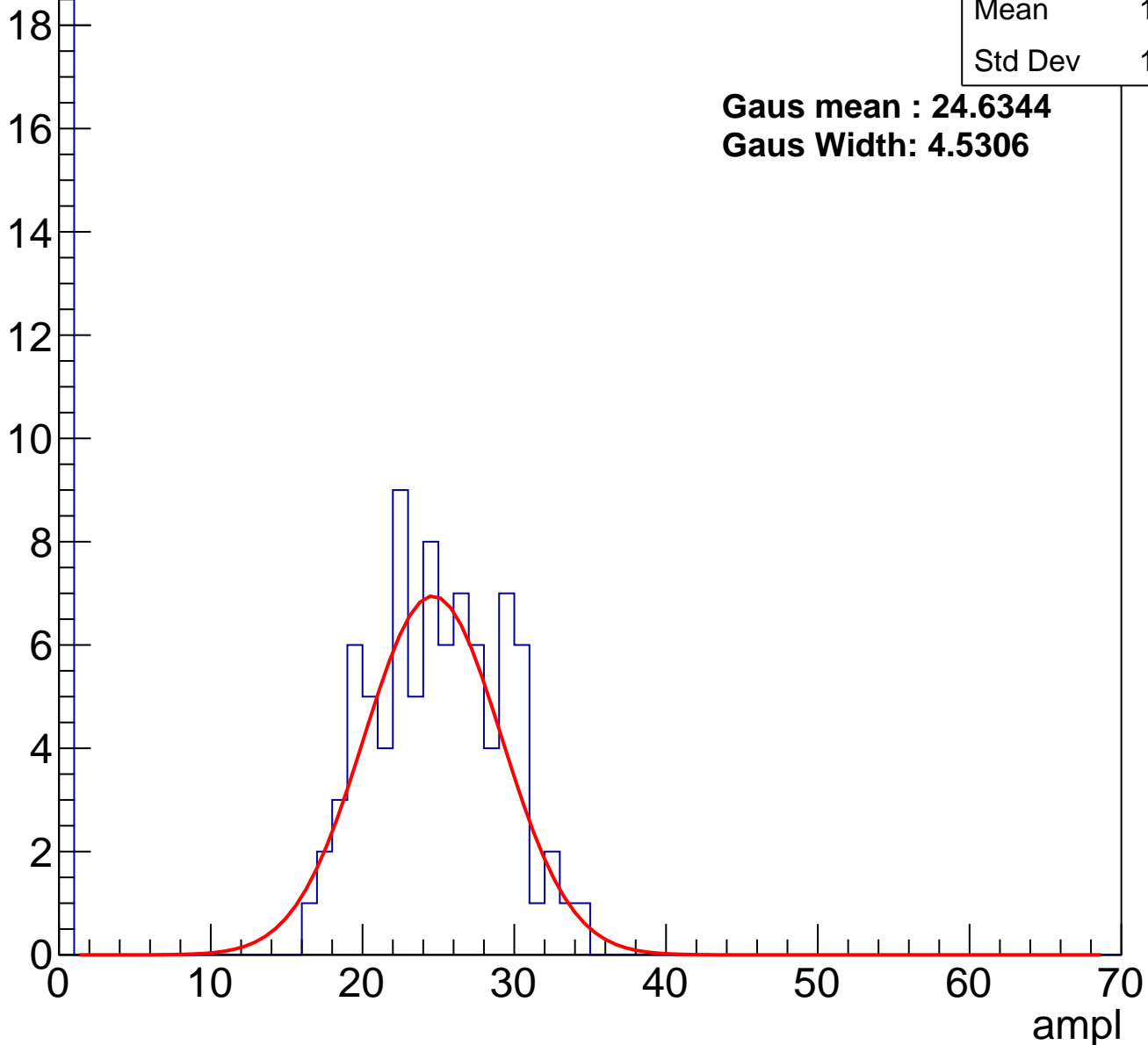
calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	19.98
Std Dev	10.22

Gaus mean : 24.6344

Gaus Width: 4.5306

Entry



B1L103S, U21-ch120, adc1

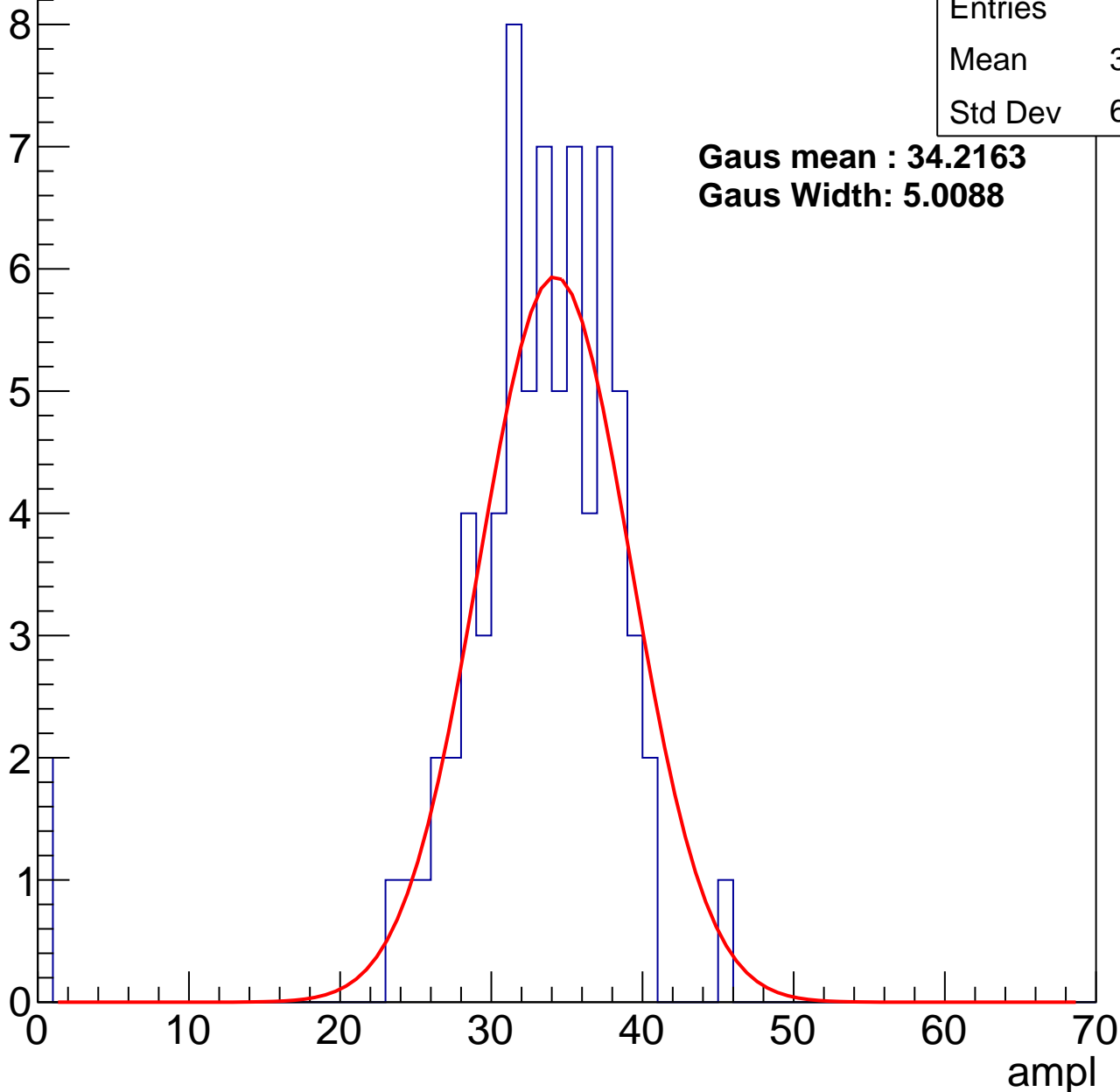
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	32.24
Std Dev	6.806

Gaus mean : 34.2163

Gaus Width: 5.0088



B1L103S, U21-ch120, adc2

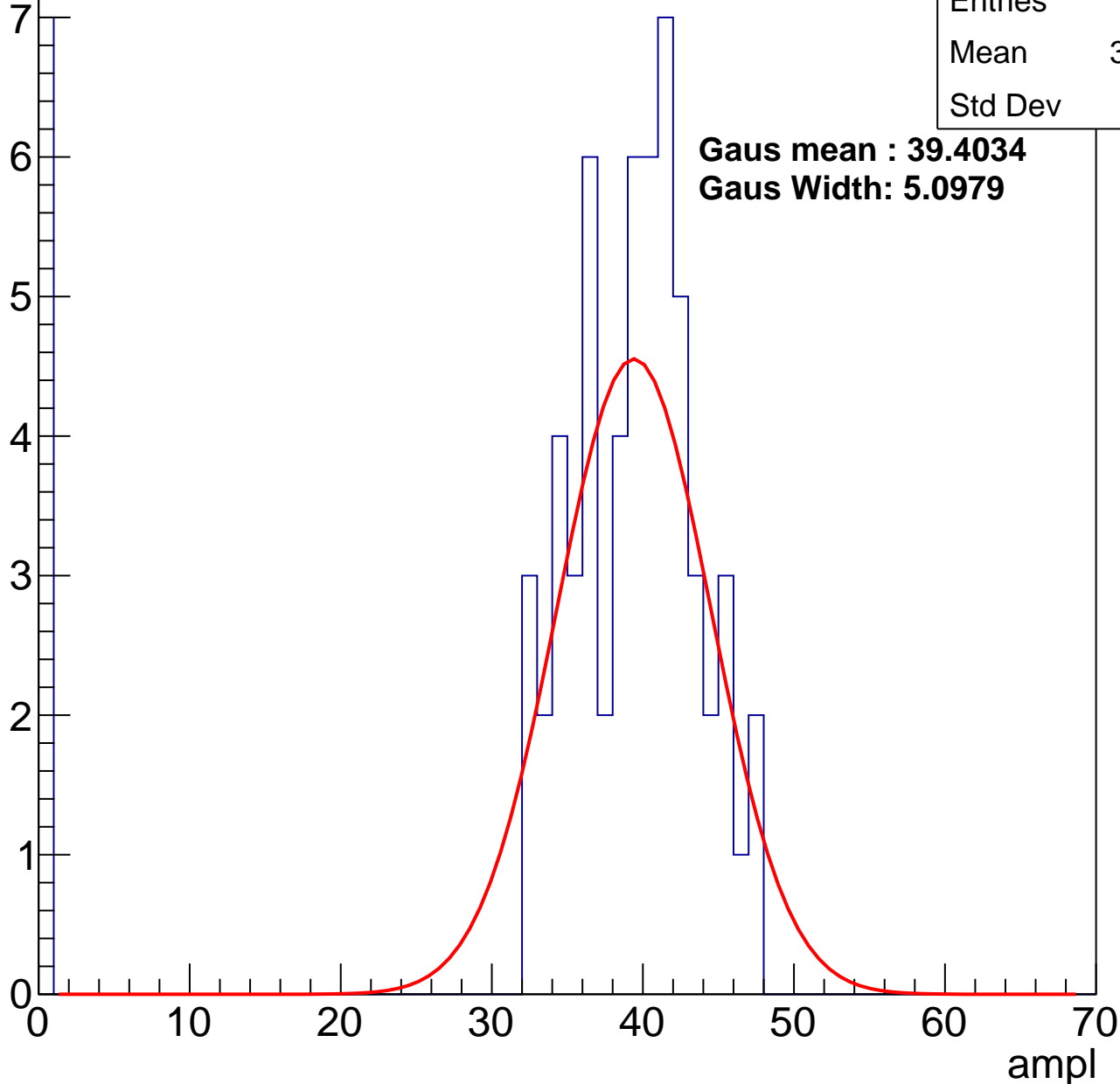
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.97
Std Dev	12.6

Gaus mean : 39.4034

Gaus Width: 5.0979

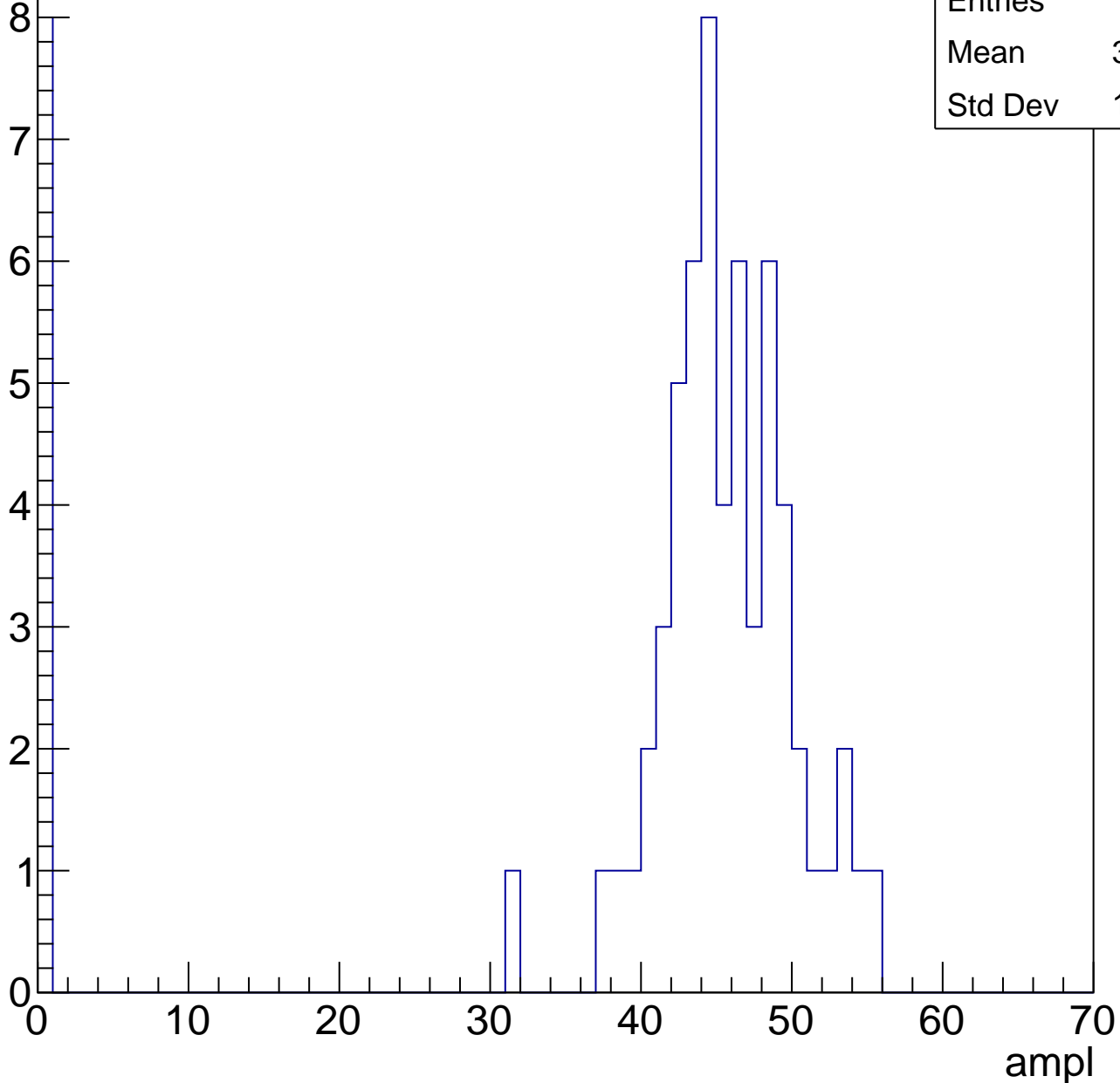


B1L103S, U21-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	39.81
Std Dev	15.21

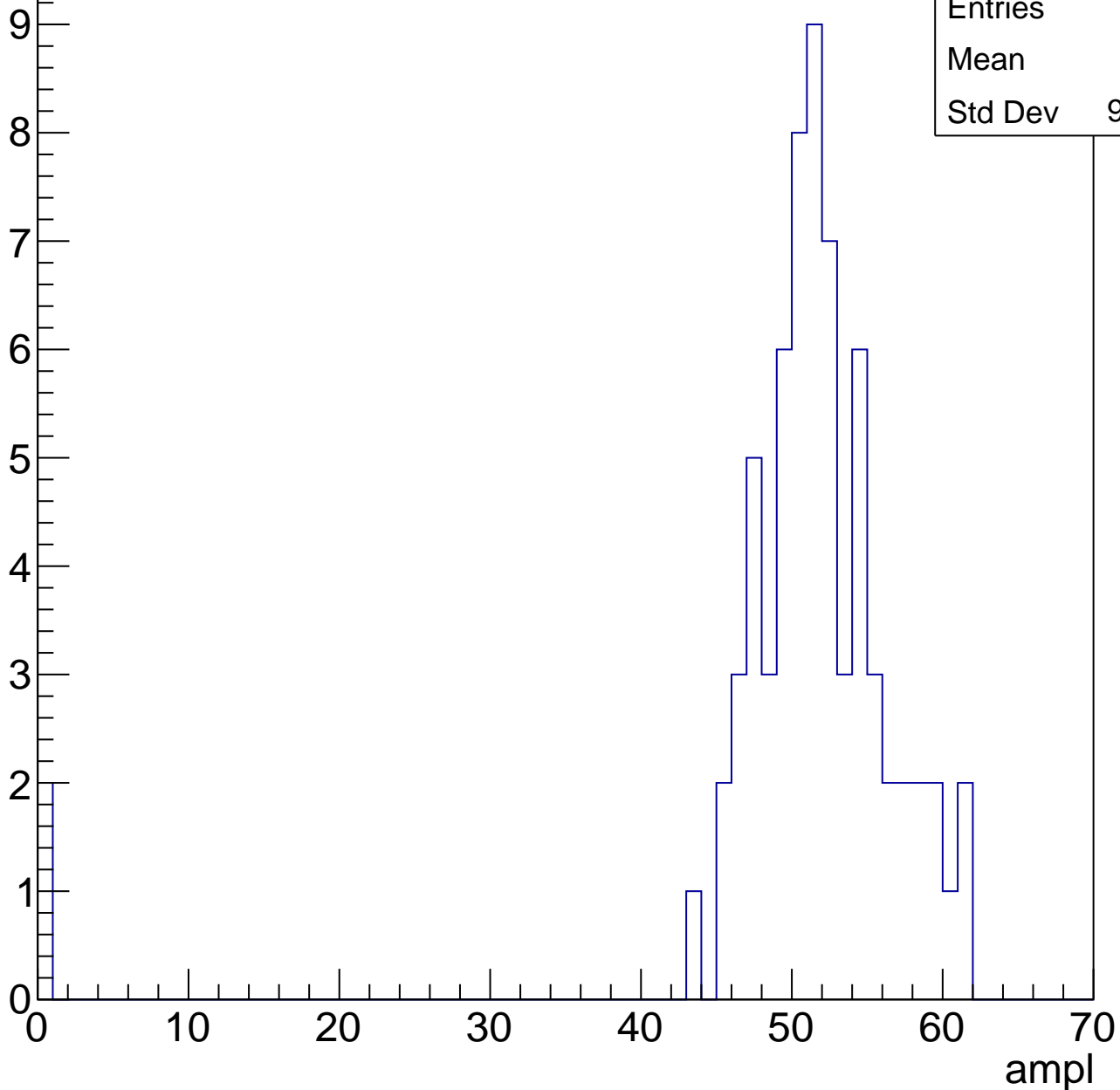


B1L103S, U21-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	50.1
Std Dev	9.515

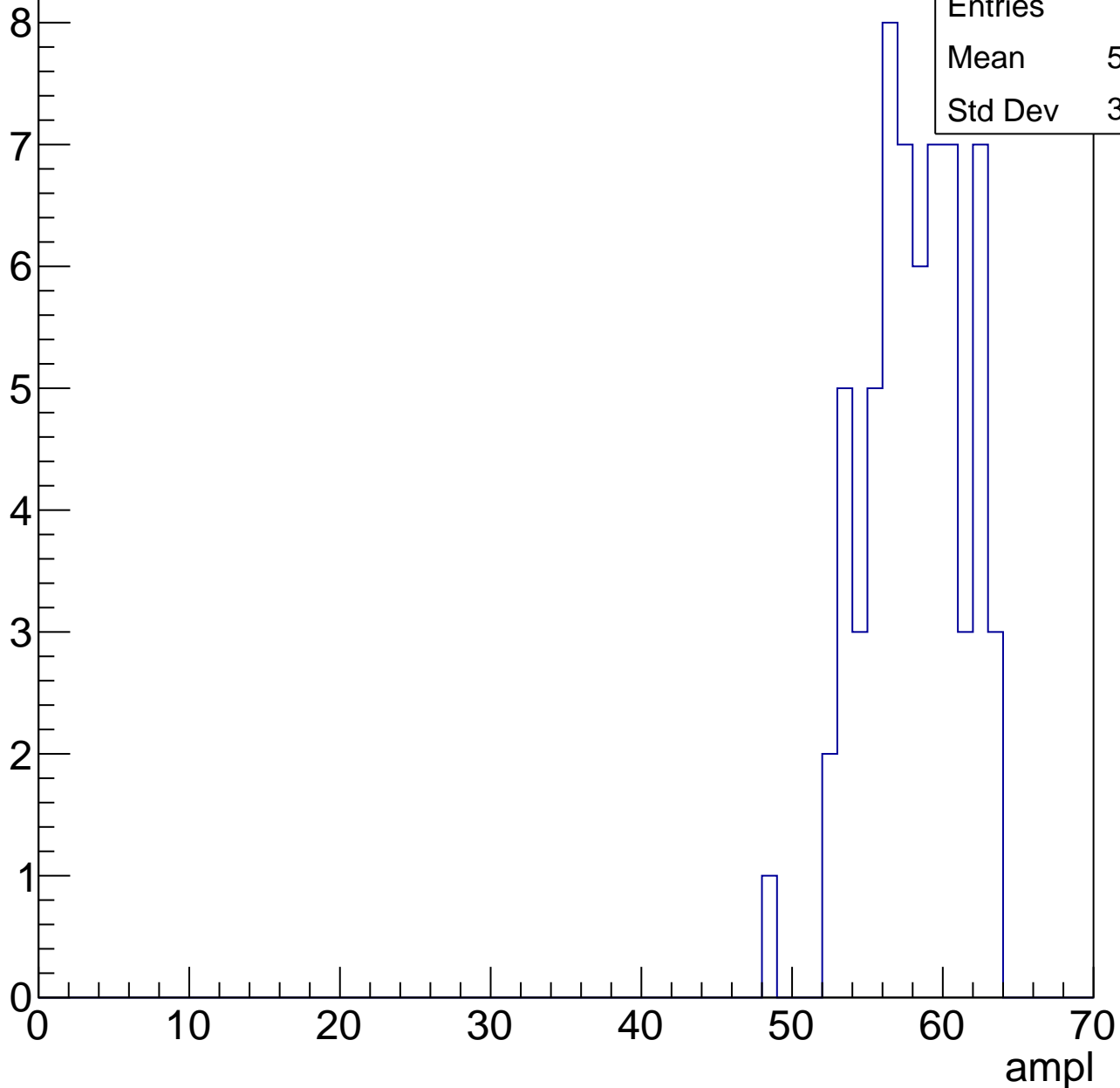


B1L103S, U21-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	57.62
Std Dev	3.233

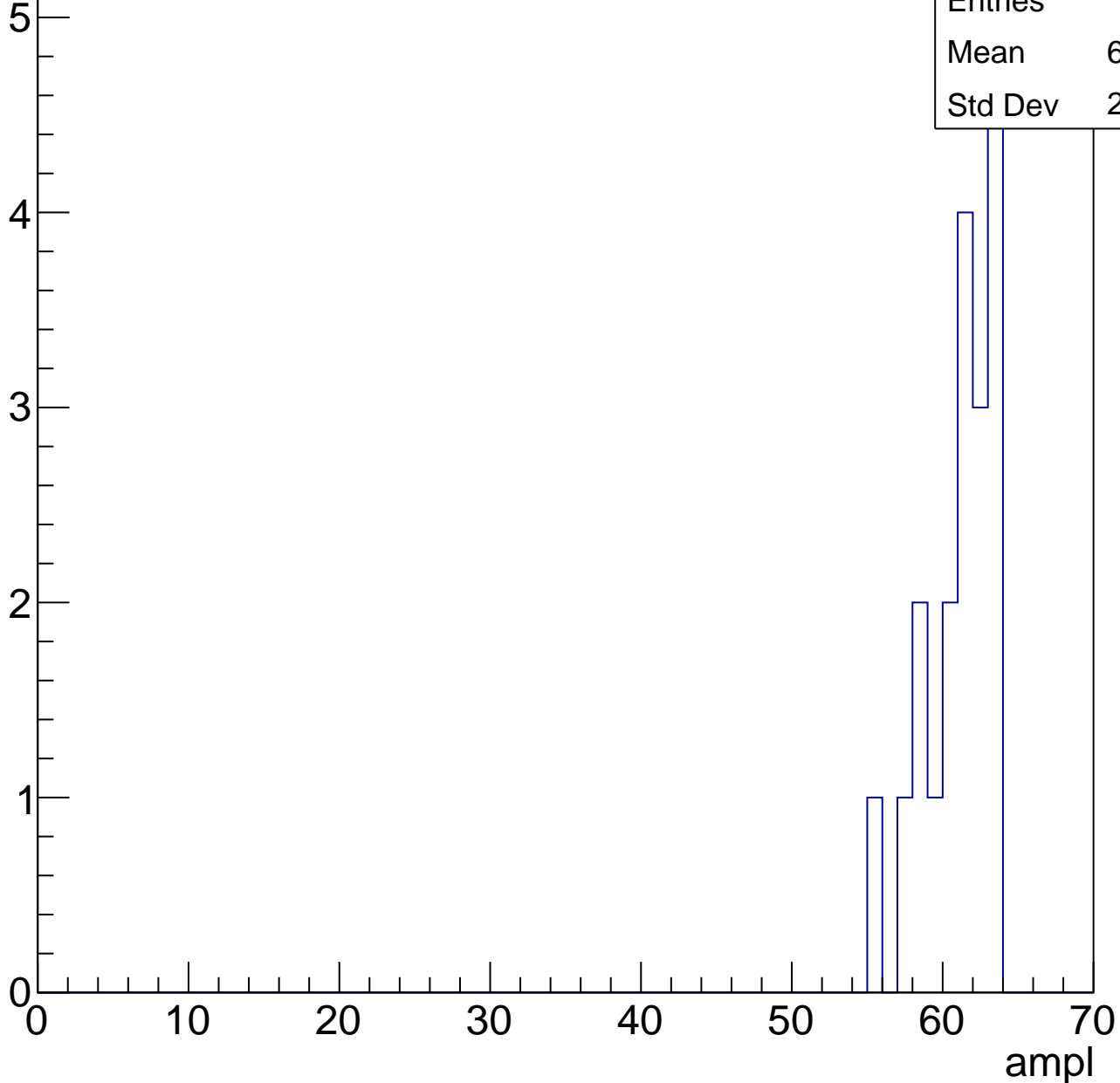


B1L103S, U21-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	60.63
Std Dev	2.253



B1L103S, U21-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

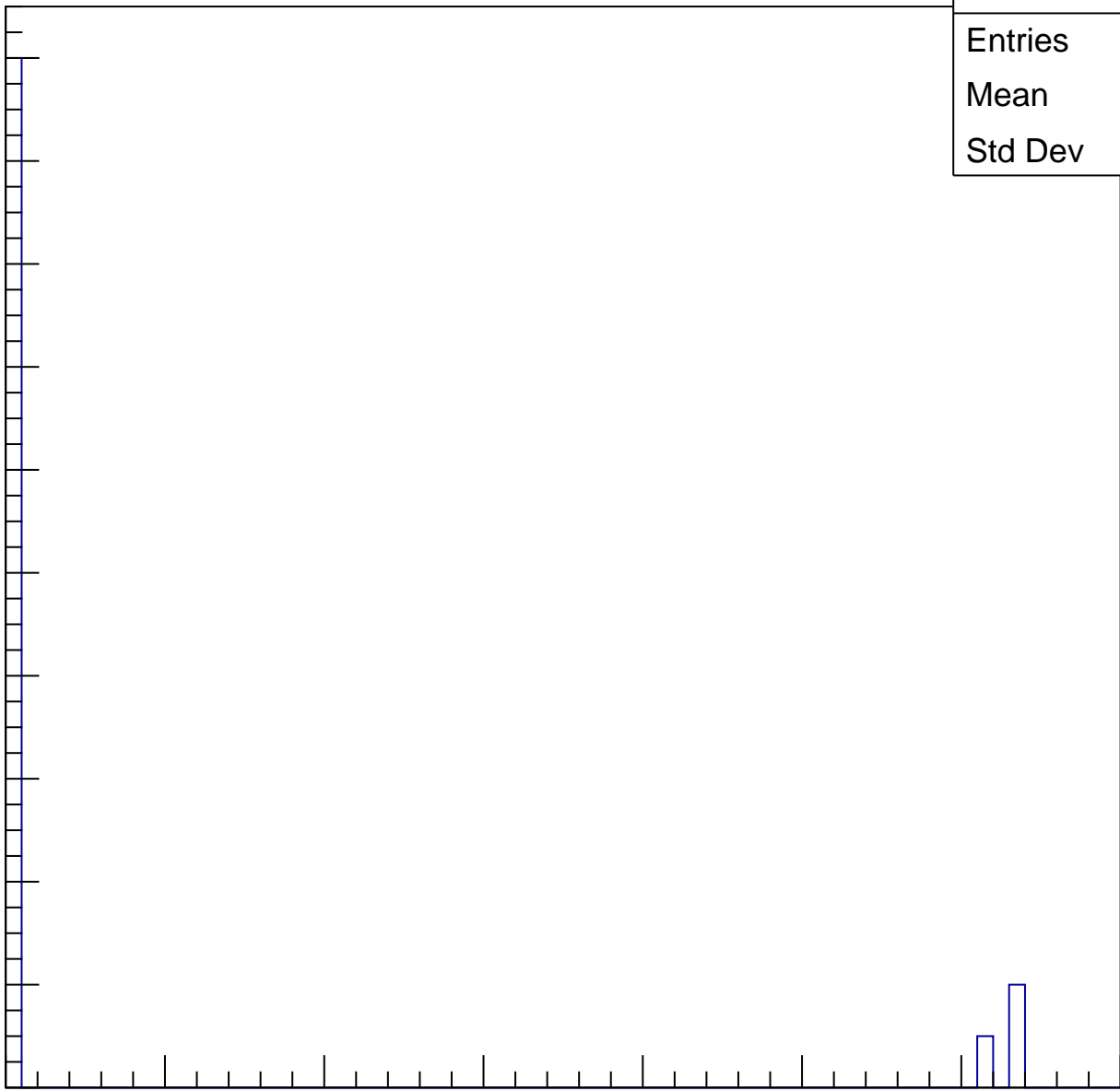
Entries	23
Mean	8.13
Std Dev	21

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U21-ch121, adc0

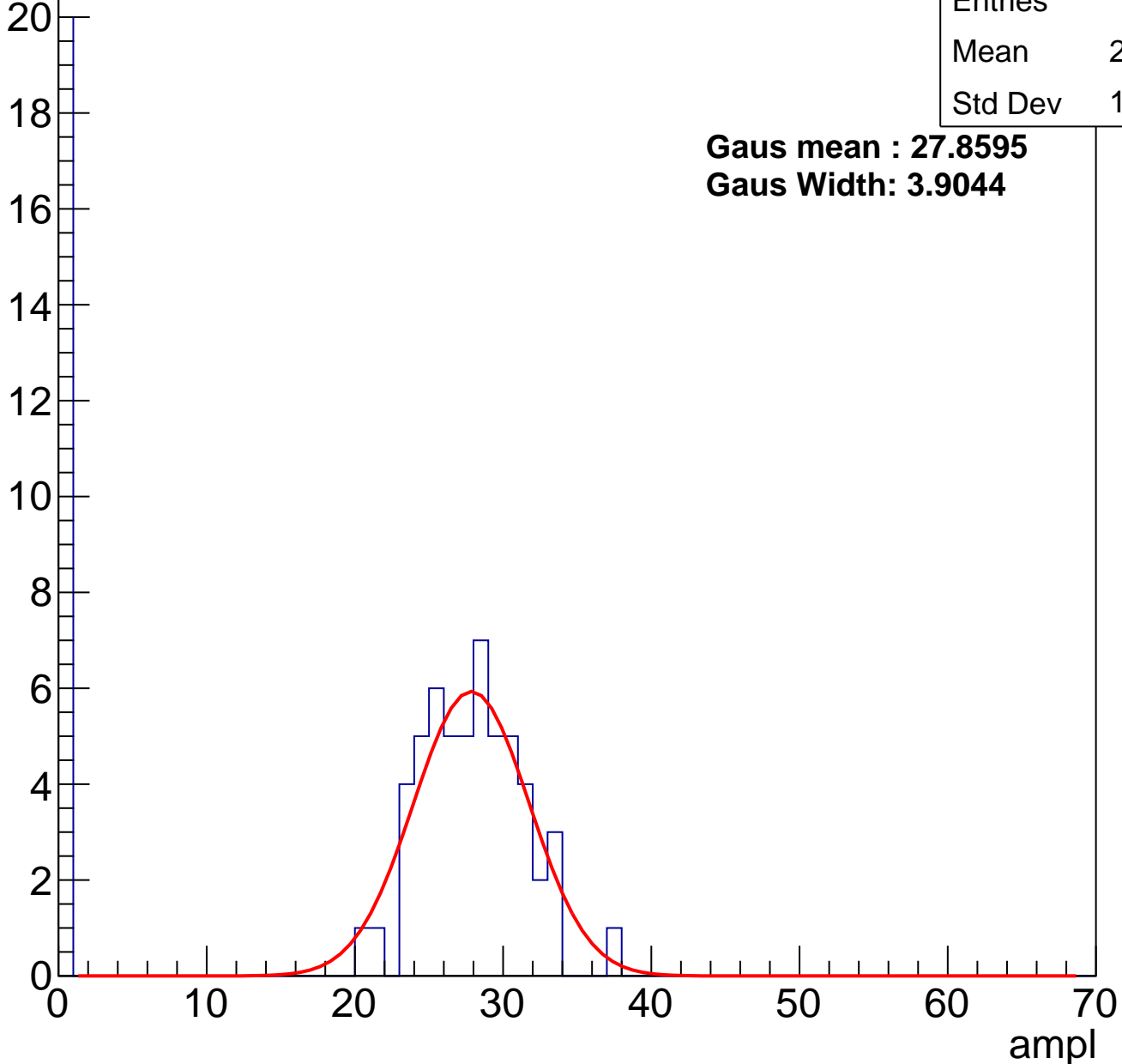
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	20.04
Std Dev	12.53

Gaus mean : 27.8595

Gaus Width: 3.9044

Entry



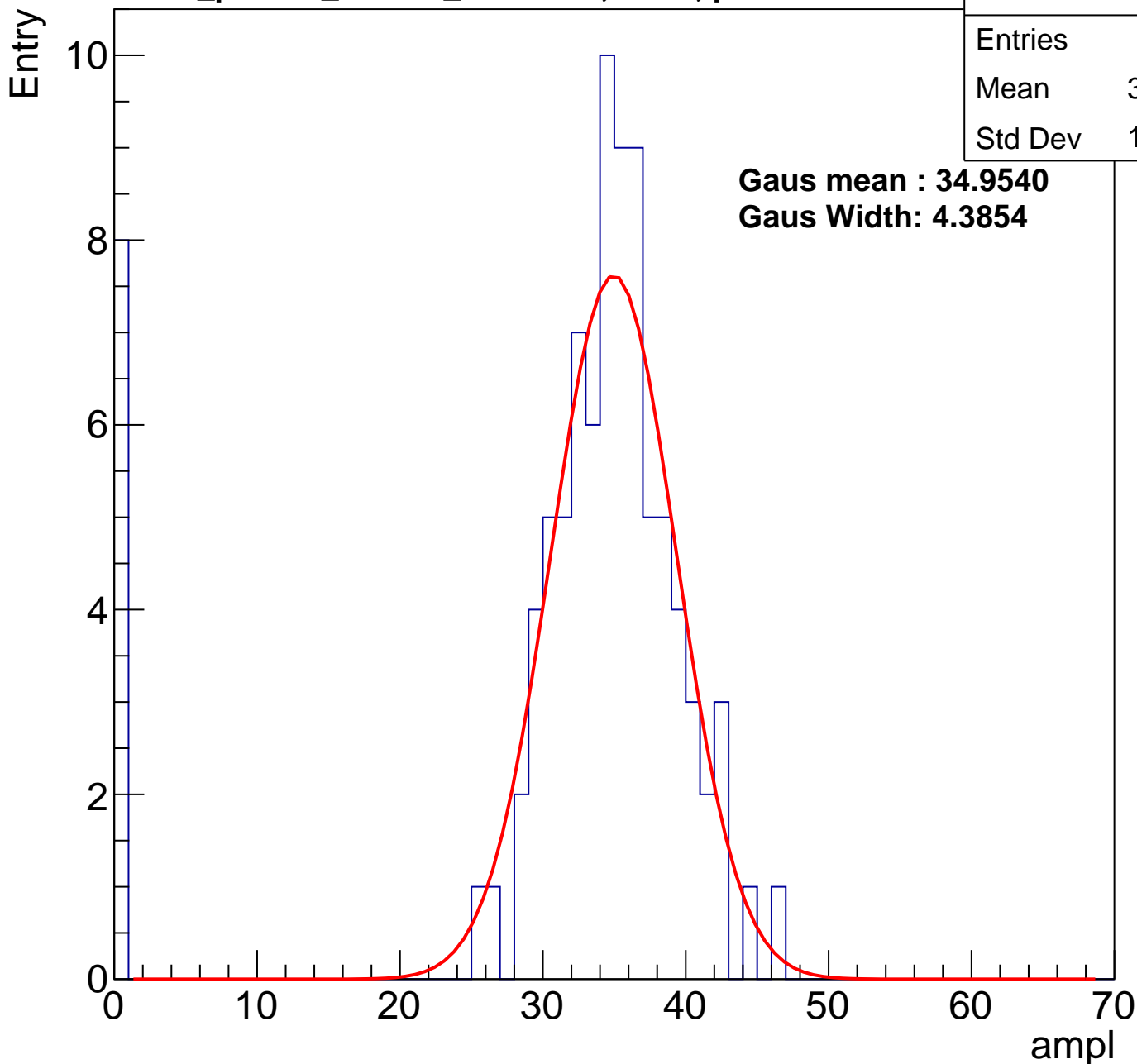
B1L103S, U21-ch121, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	31.63
Std Dev	10.55

Gaus mean : 34.9540

Gaus Width: 4.3854



B1L103S, U21-ch121, adc2

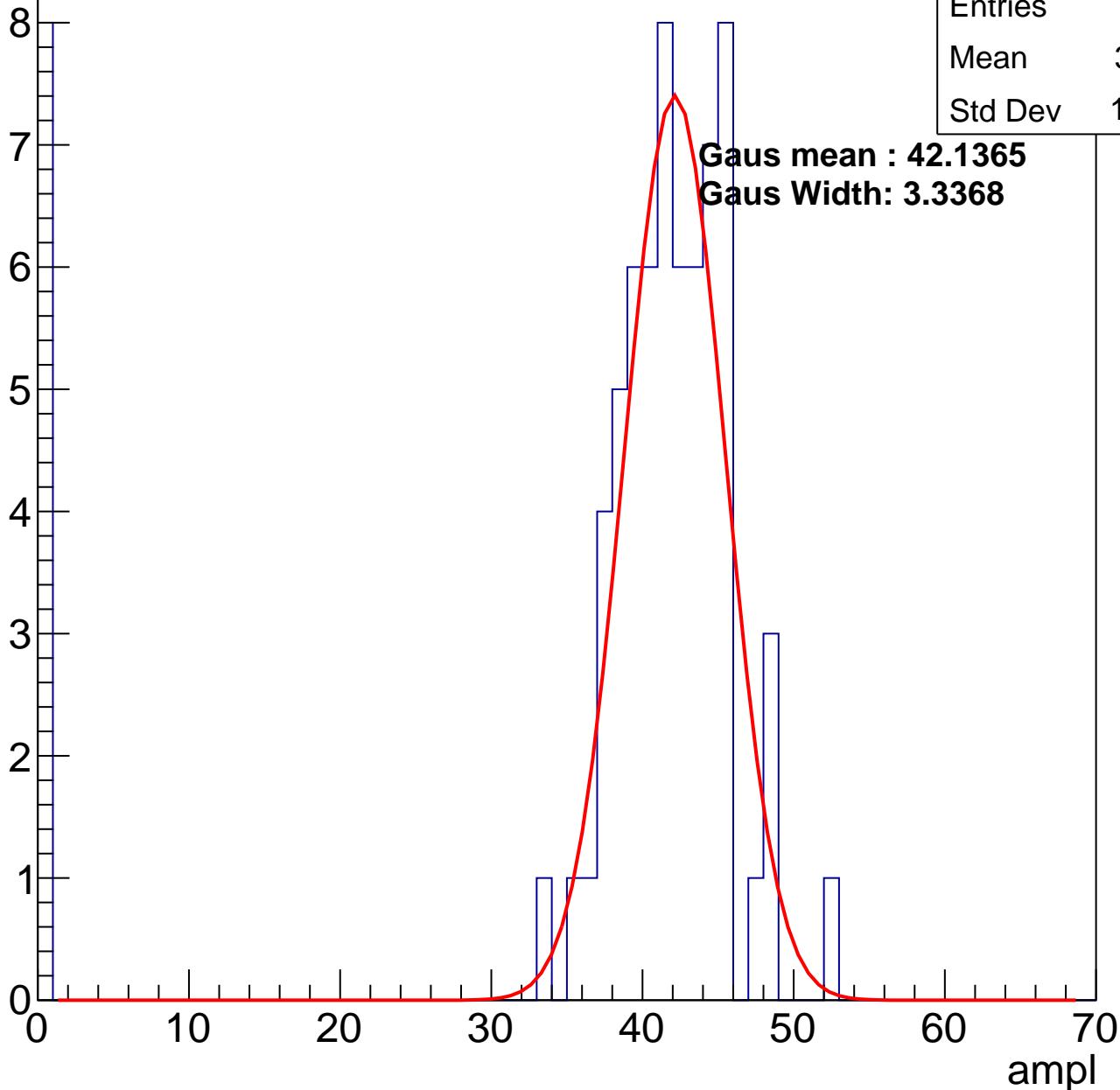
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	37.01
Std Dev	13.49

Gaus mean : 42.1365

Gaus Width: 3.3368

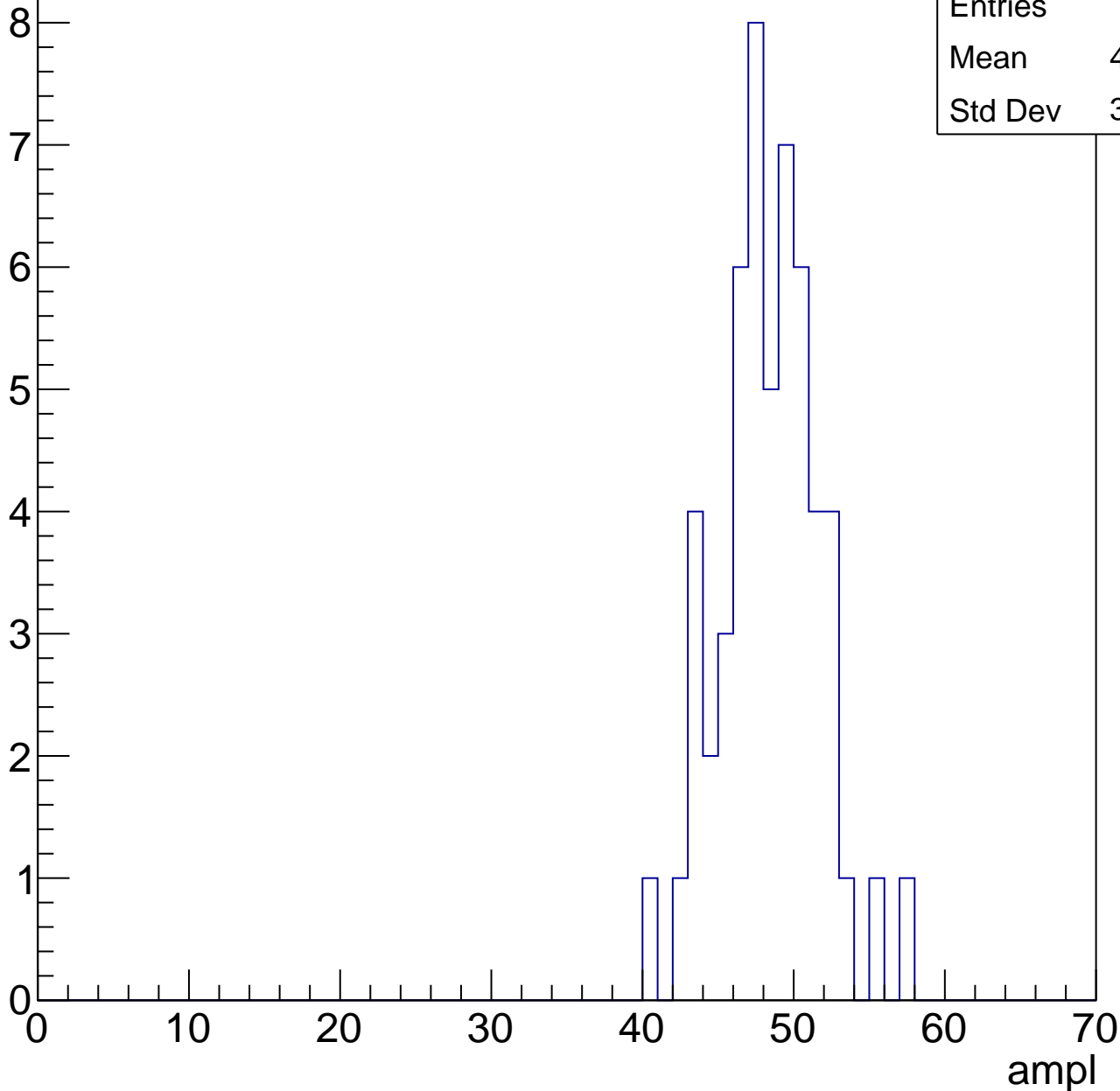


B1L103S, U21-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	47.94
Std Dev	3.268

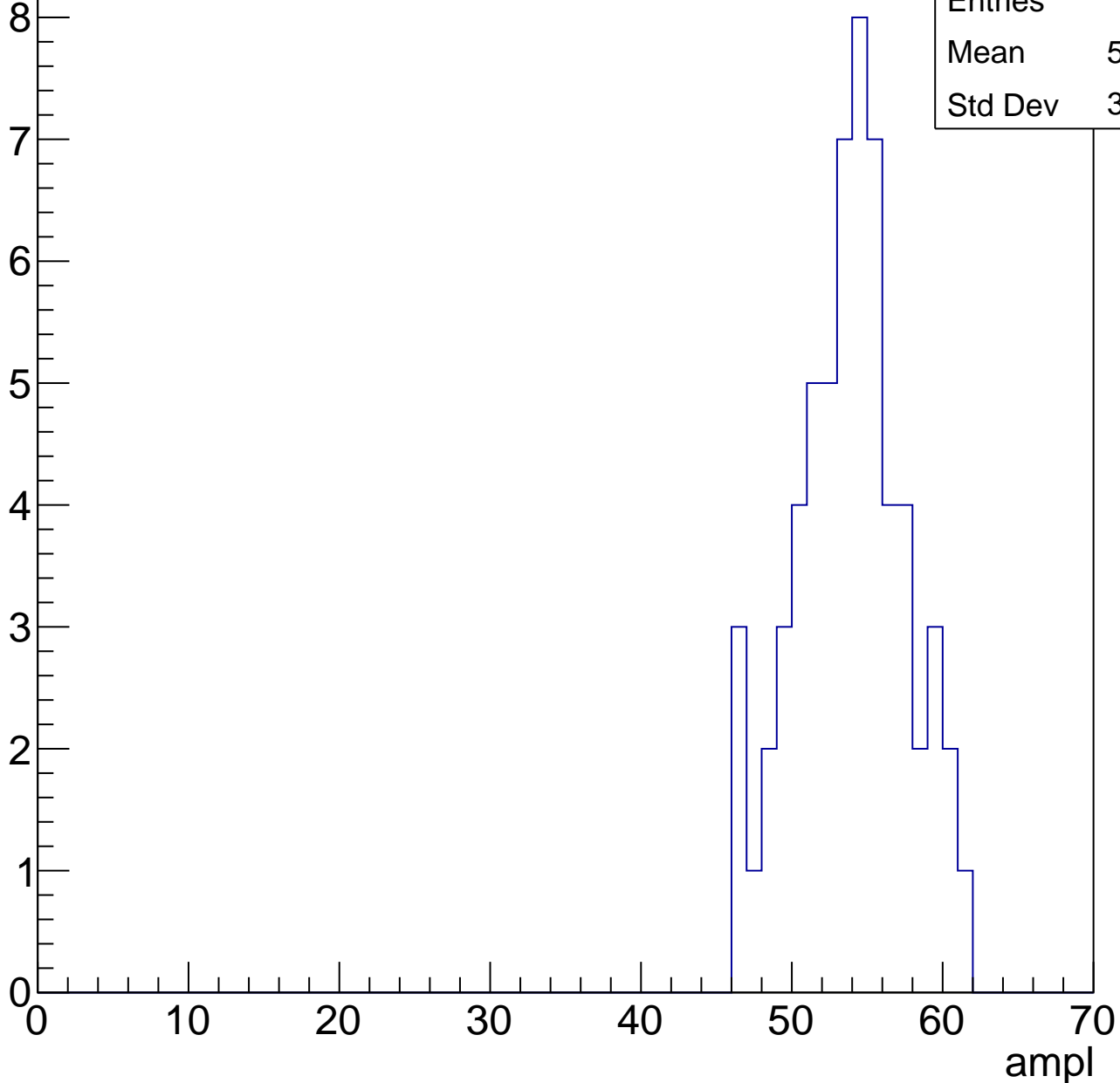


B1L103S, U21-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	53.39
Std Dev	3.595

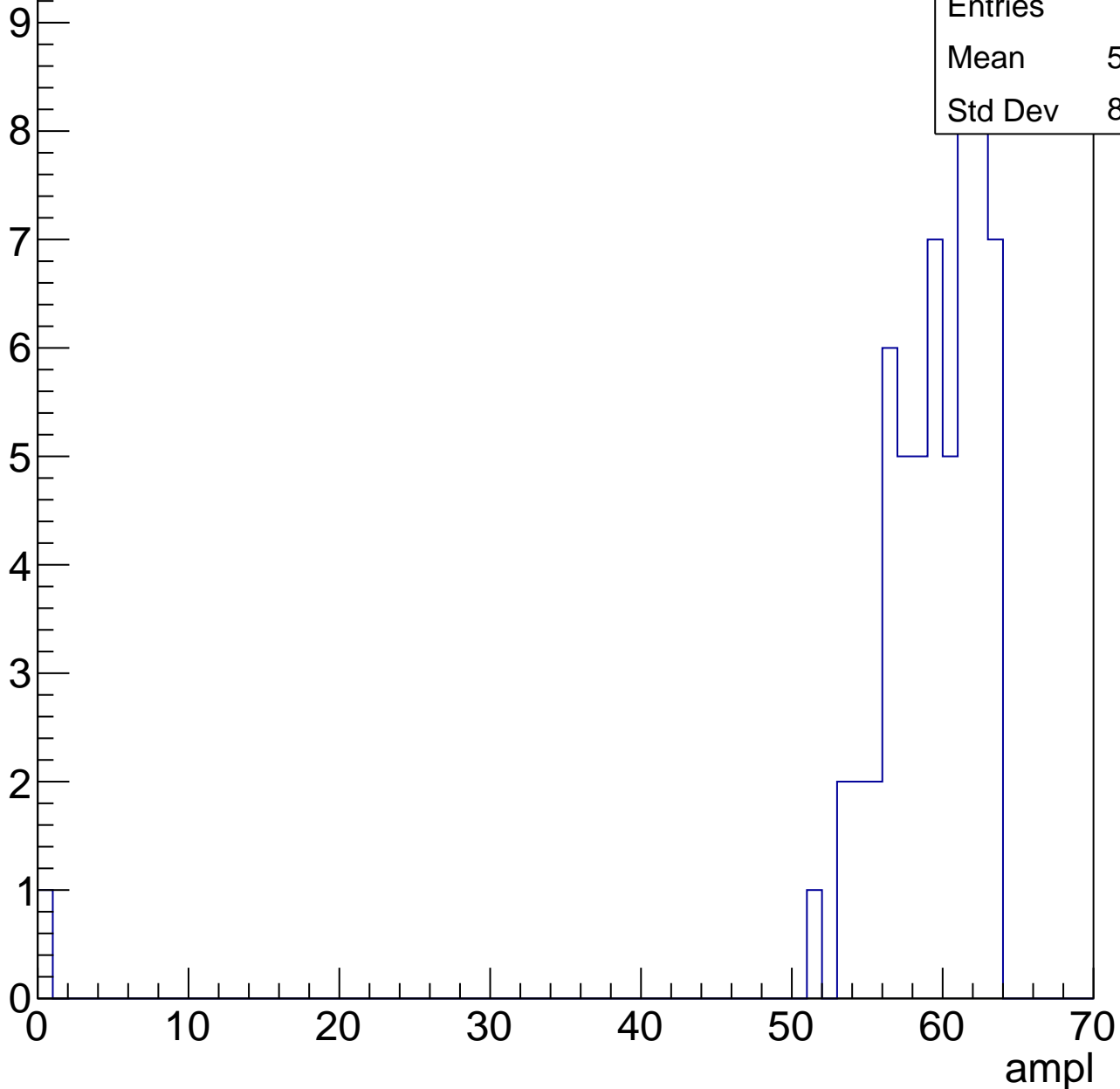


B1L103S, U21-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	58.08
Std Dev	8.116



B1L103S, U21-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	60.64
Std Dev	1.967

B1L103S, U21-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch122, adc0

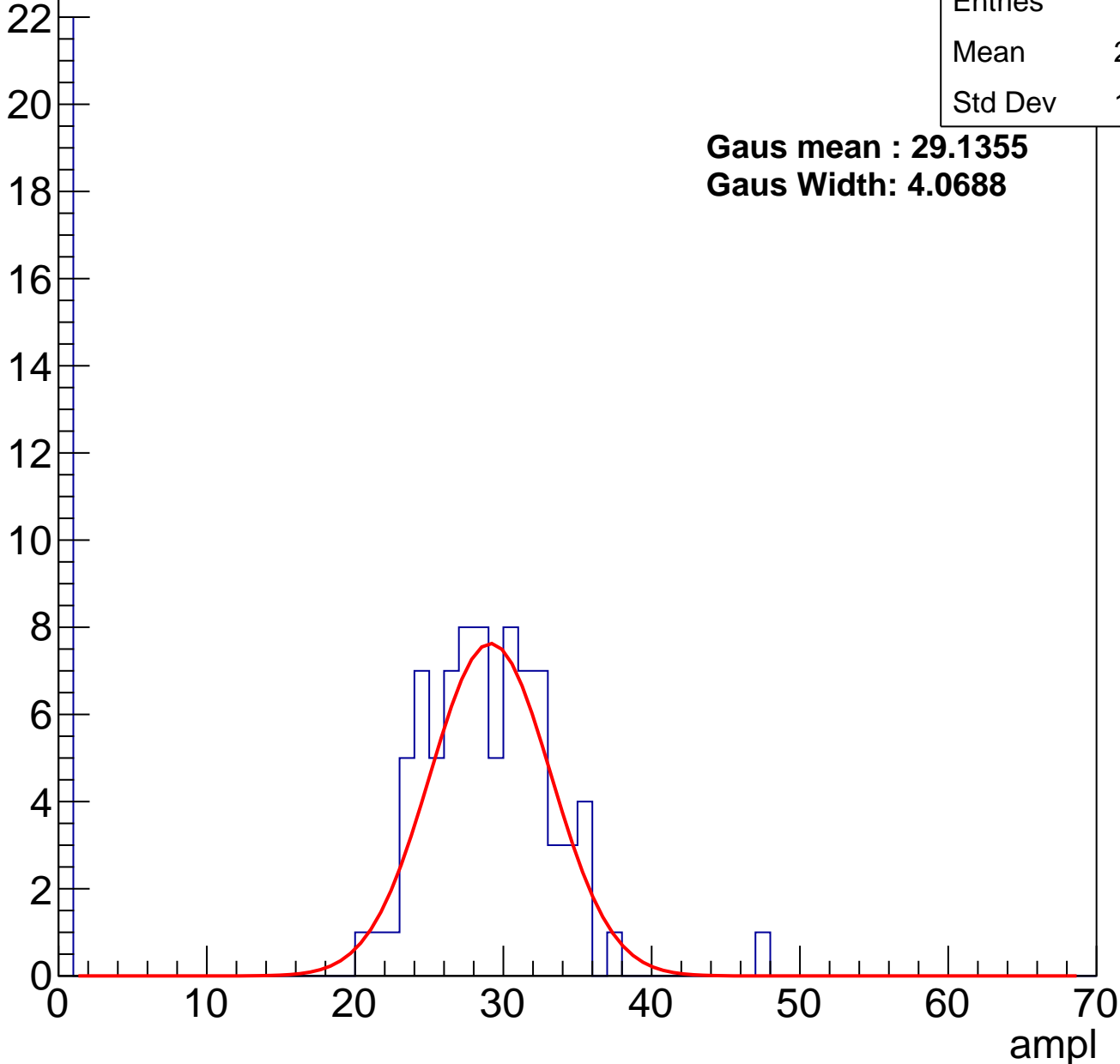
calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	22.54
Std Dev	12.27

Gaus mean : 29.1355

Gaus Width: 4.0688

Entry



B1L103S, U21-ch122, adc1

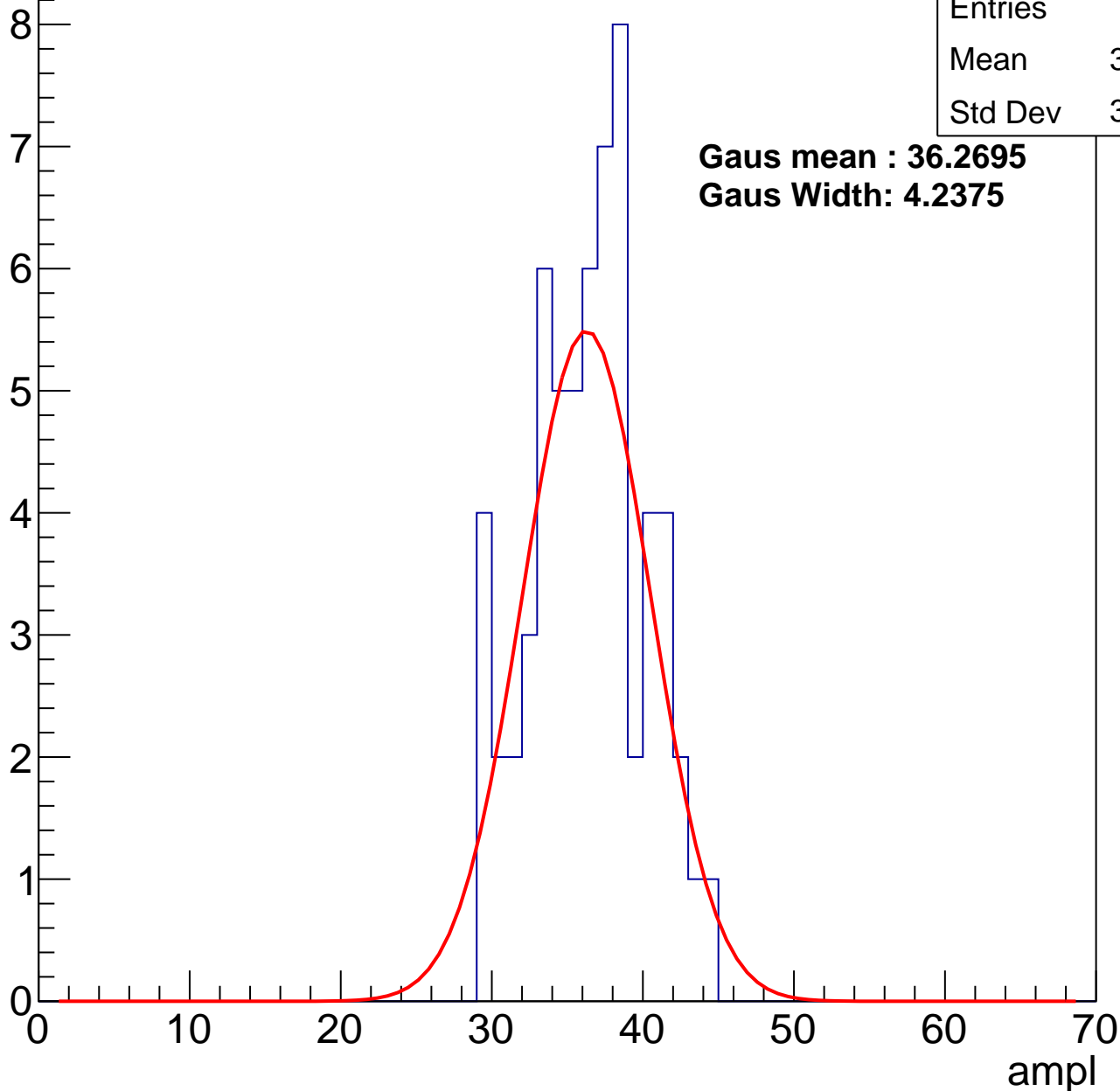
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	35.95
Std Dev	3.705

Gaus mean : 36.2695

Gaus Width: 4.2375



B1L103S, U21-ch122, adc2

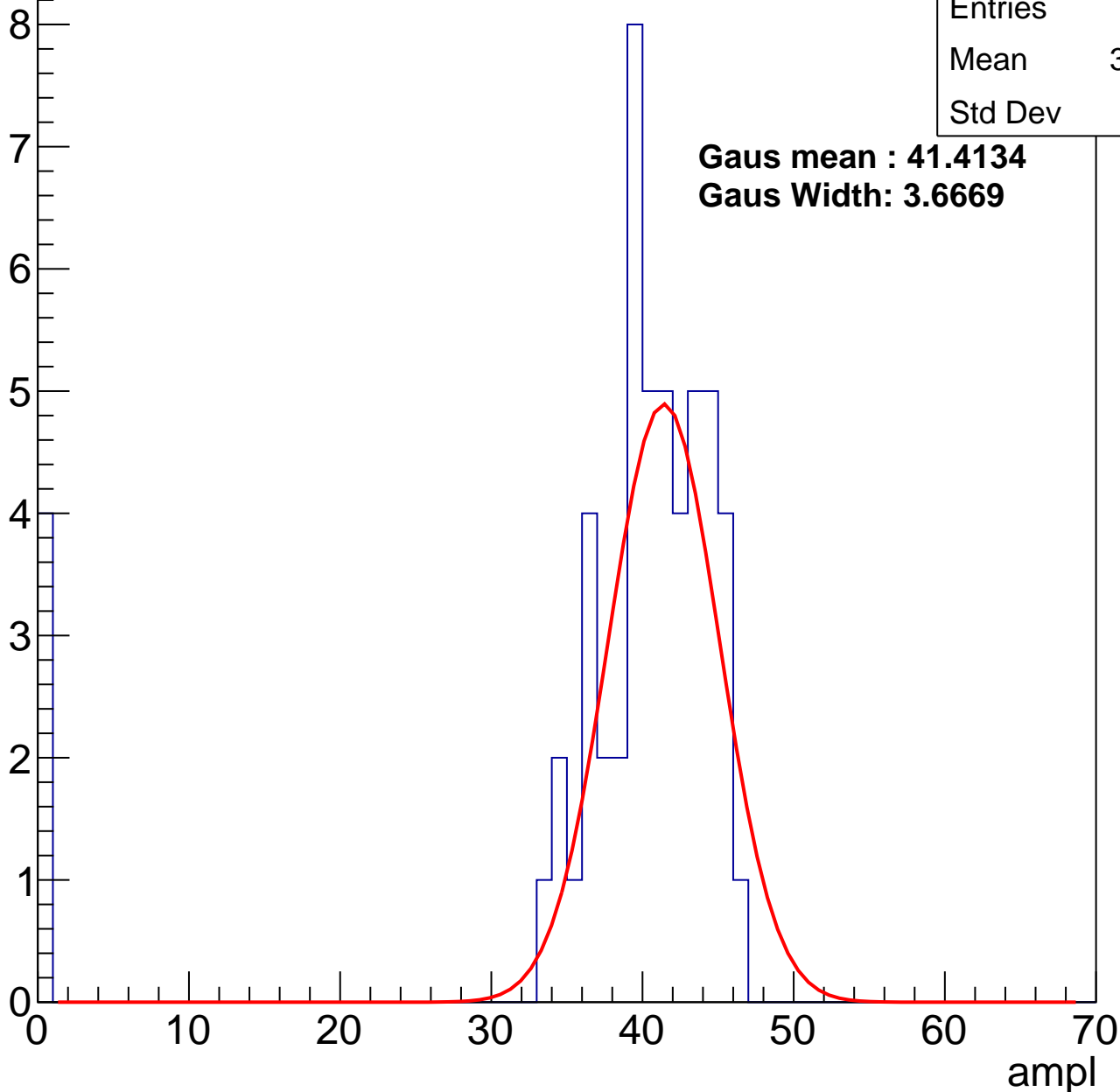
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	37.28
Std Dev	11.1

Gaus mean : 41.4134

Gaus Width: 3.6669

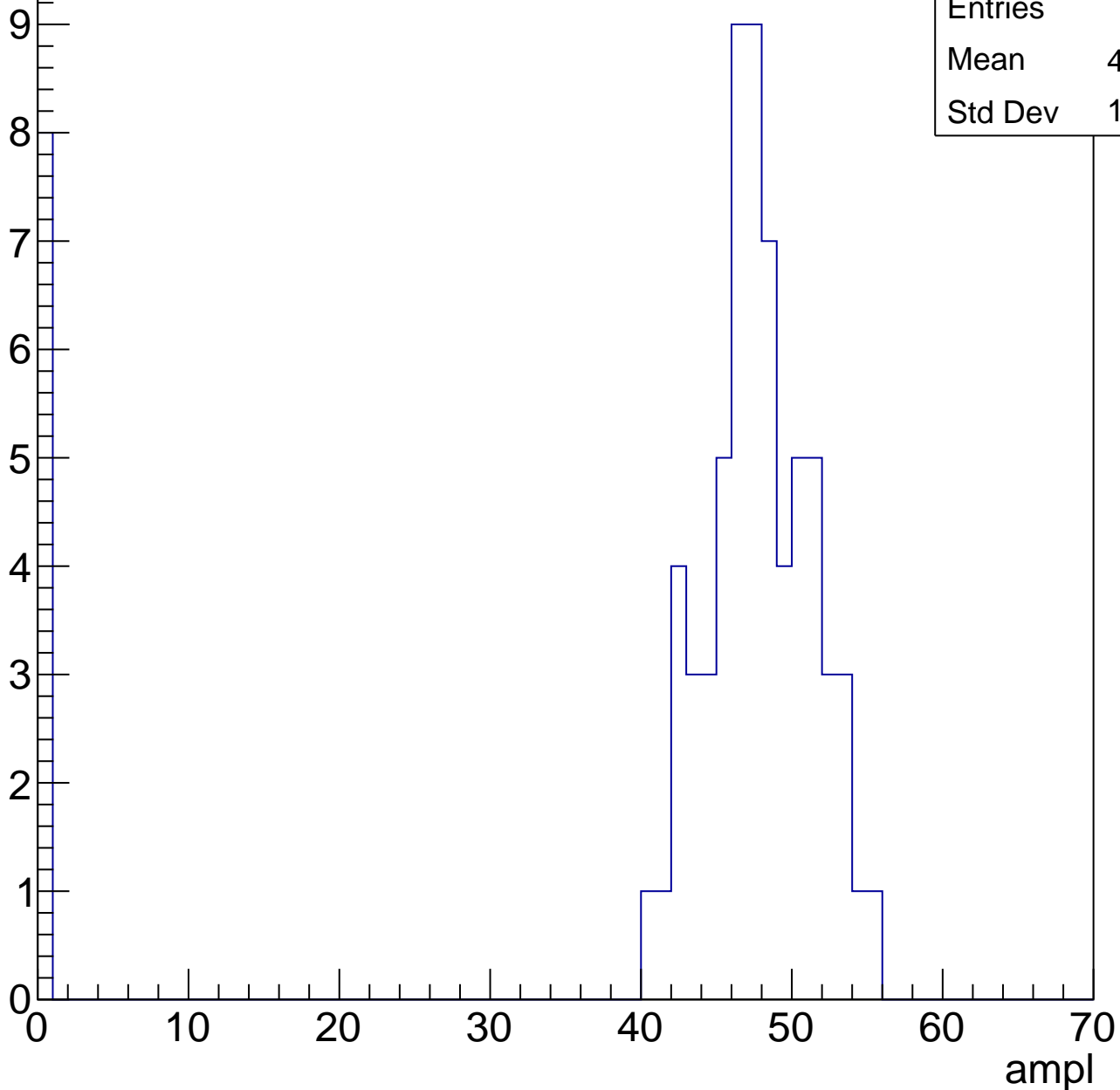


B1L103S, U21-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	42.12
Std Dev	15.23

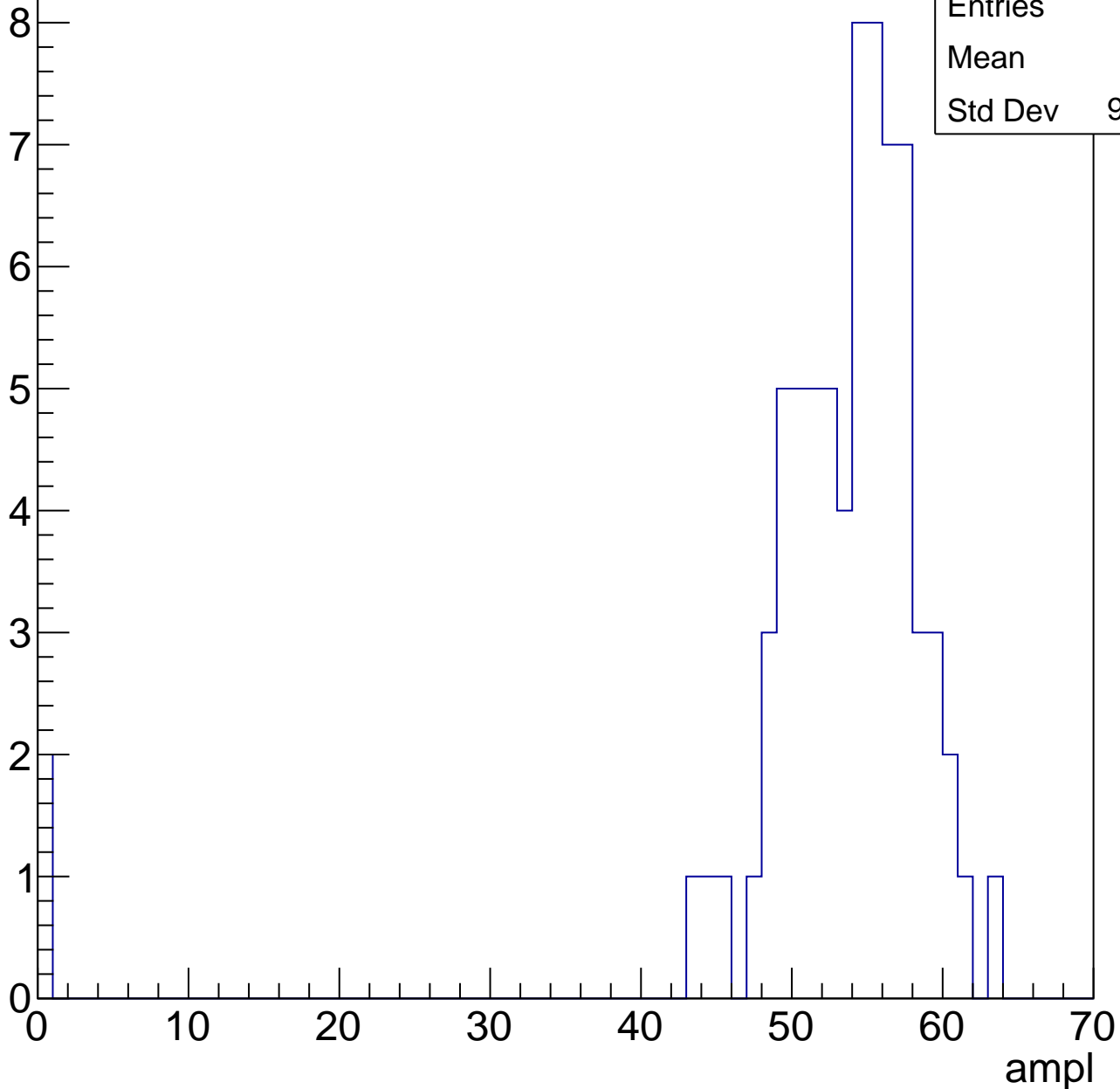


B1L103S, U21-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	52.1
Std Dev	9.598

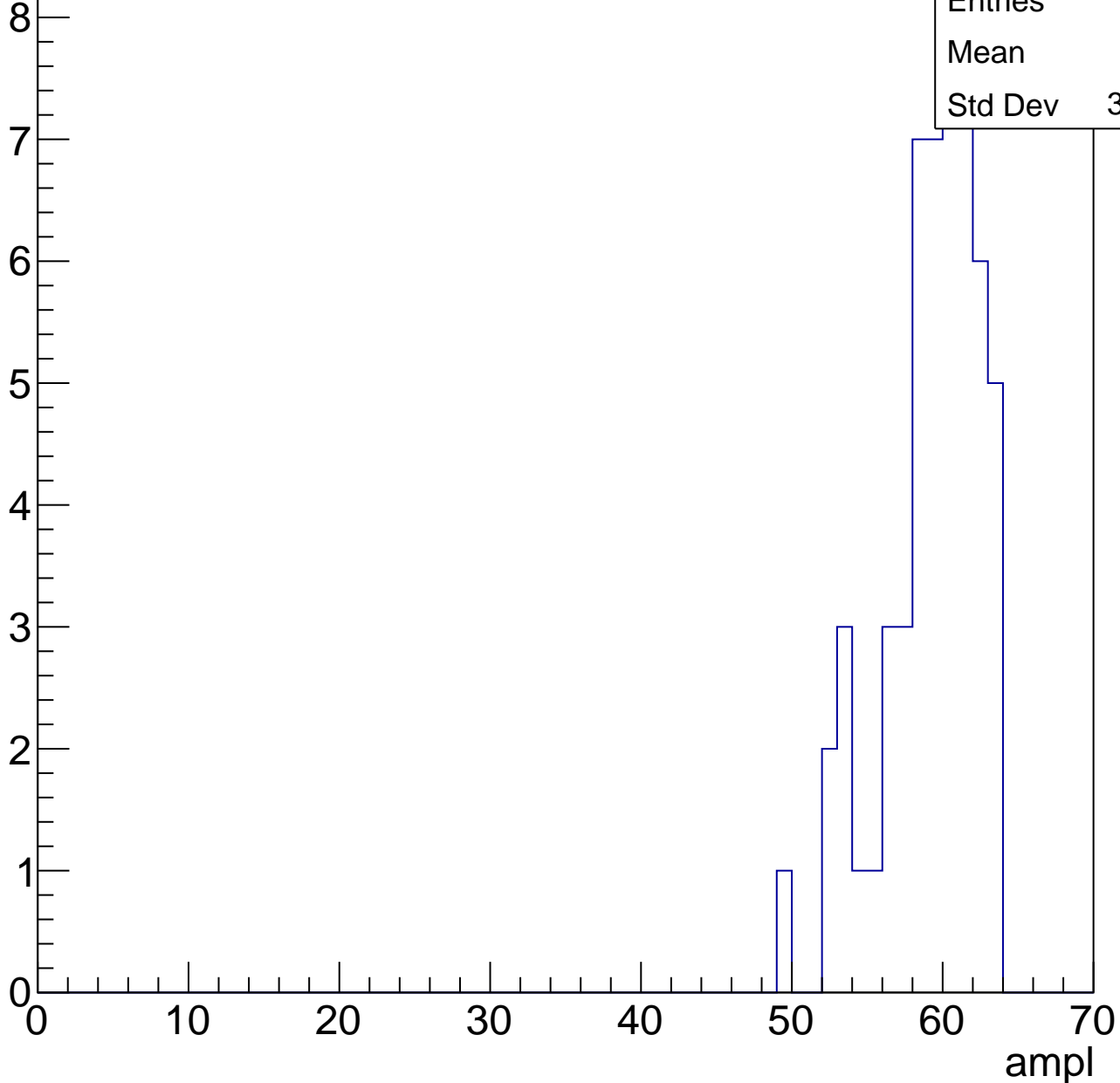


B1L103S, U21-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

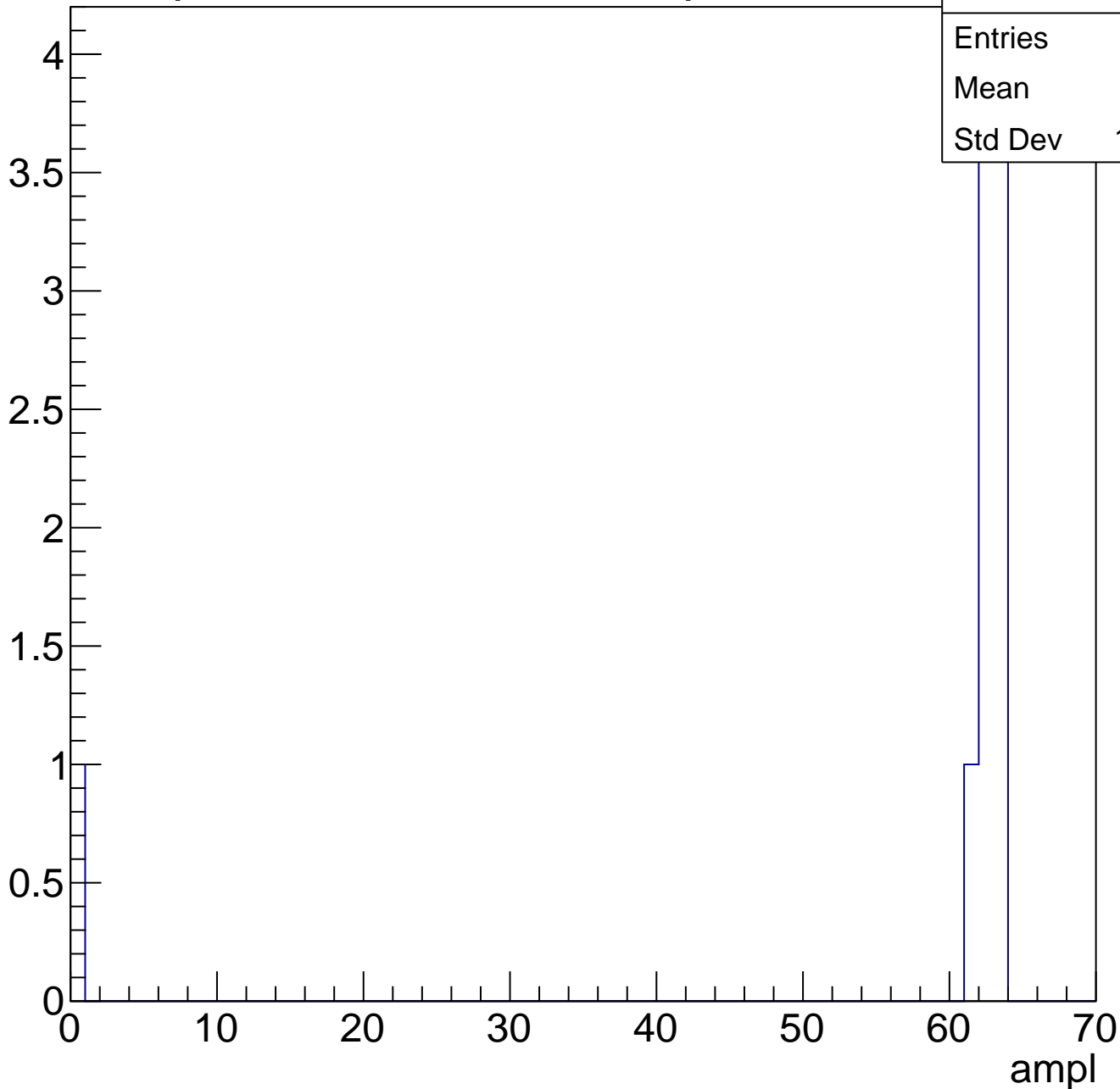
Entries	55
Mean	58.8
Std Dev	3.205



B1L103S, U21-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U21-ch123, adc0

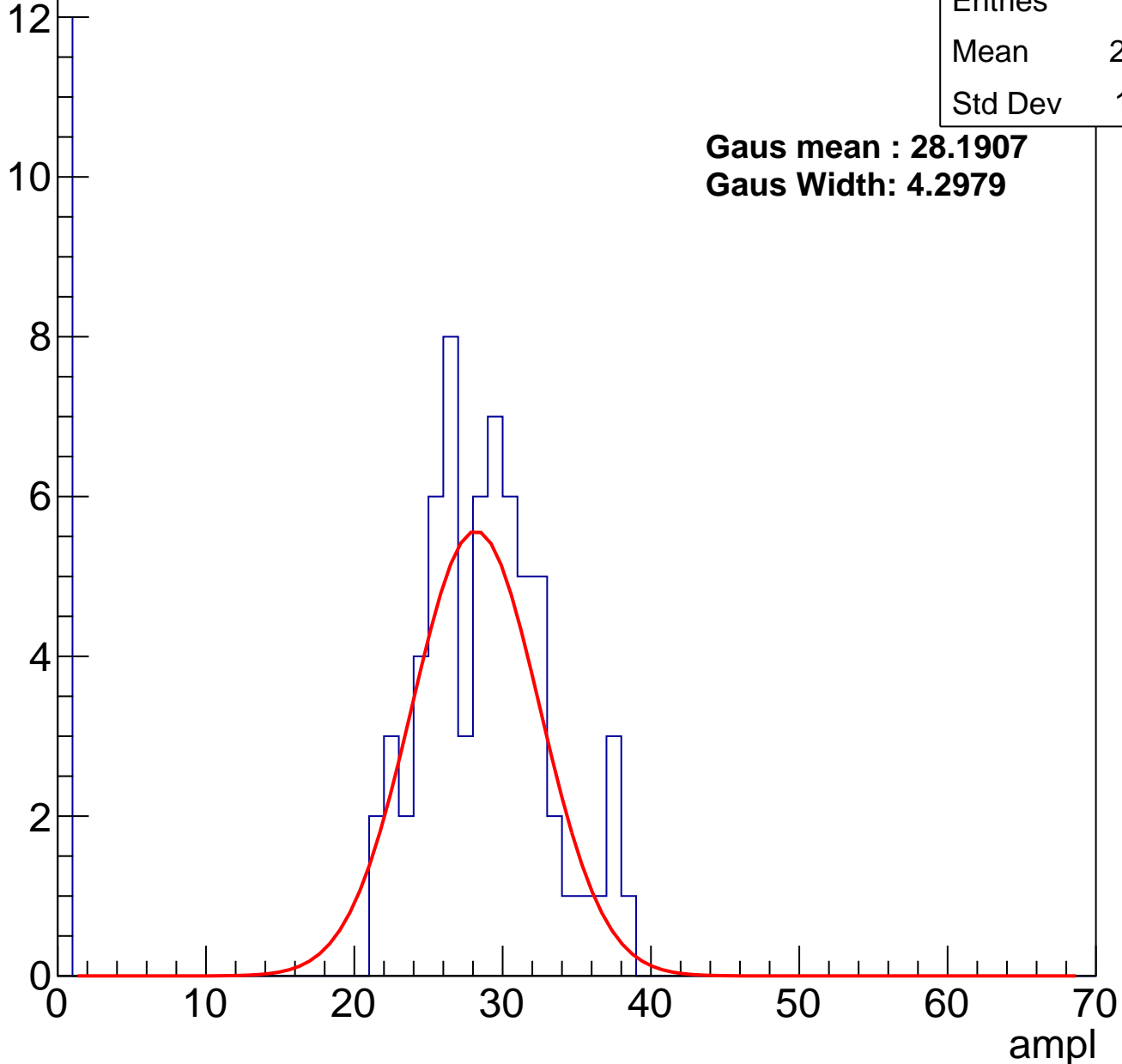
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	24.04
Std Dev	10.91

Gaus mean : 28.1907

Gaus Width: 4.2979

Entry



B1L103S, U21-ch123, adc1

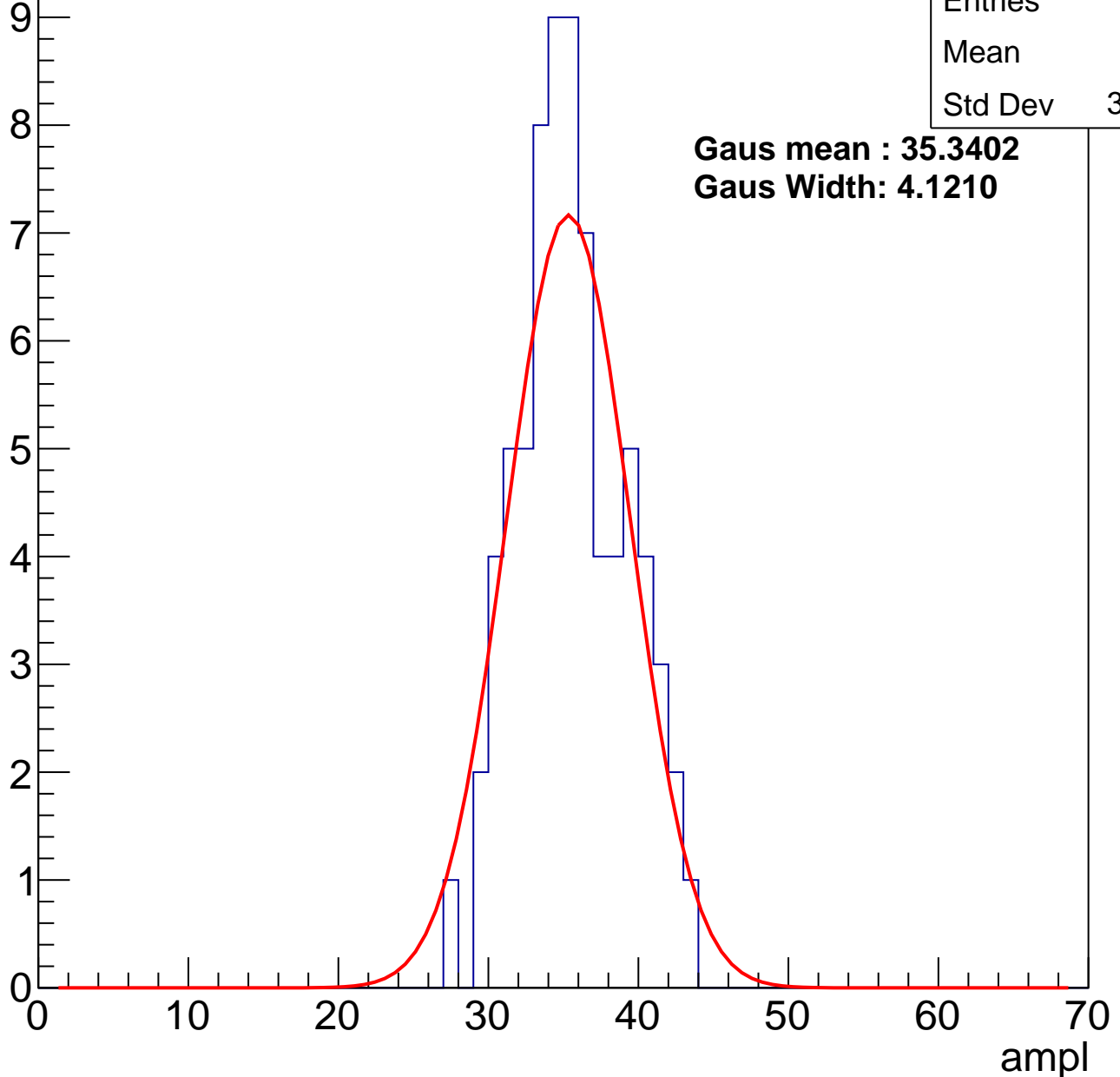
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.1
Std Dev	3.531

Gaus mean : 35.3402

Gaus Width: 4.1210



B1L103S, U21-ch123, adc2

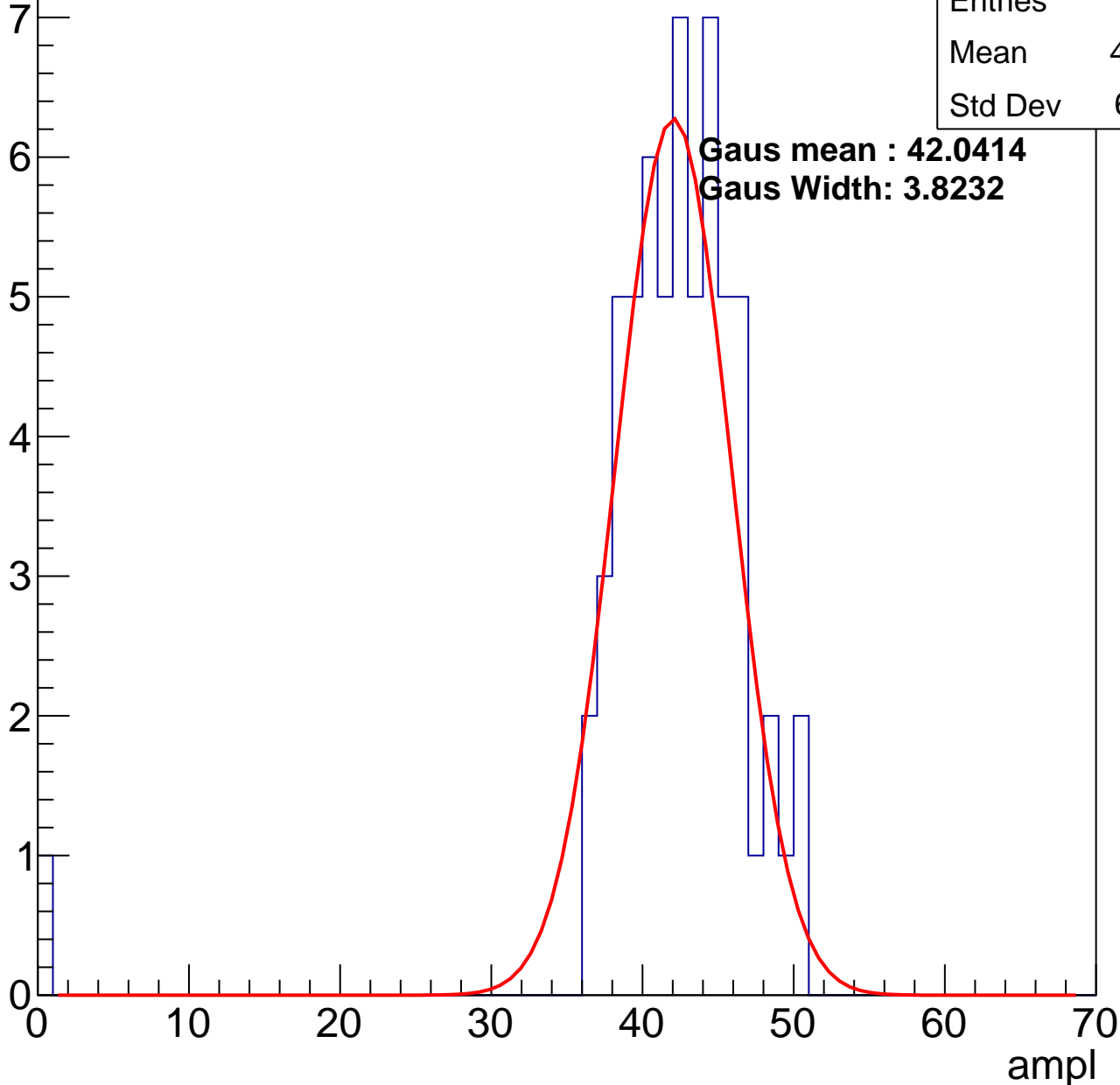
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	41.56
Std Dev	6.331

Gaus mean : 42.0414

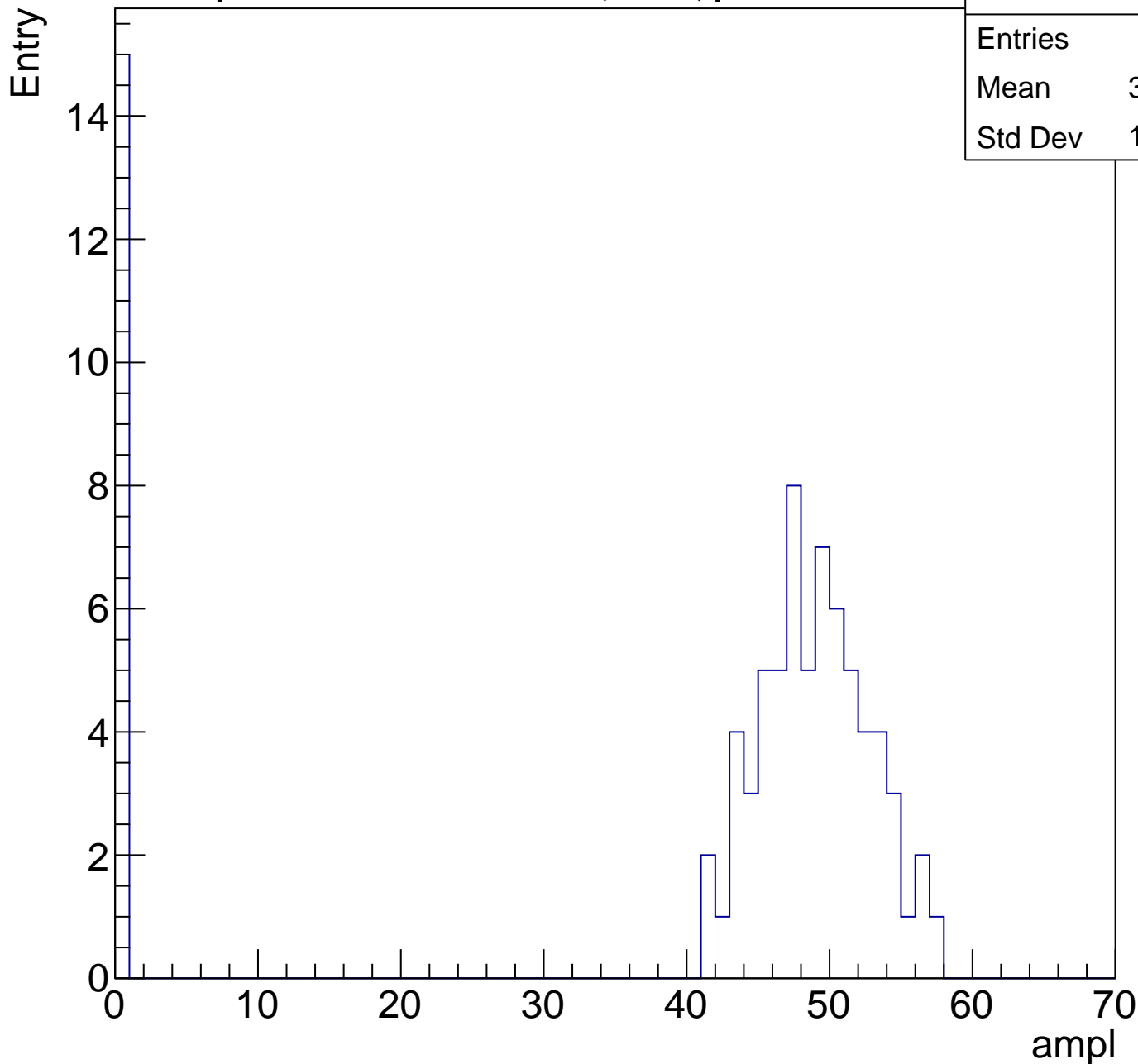
Gaus Width: 3.8232



B1L103S, U21-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	39.54
Std Dev	19.16

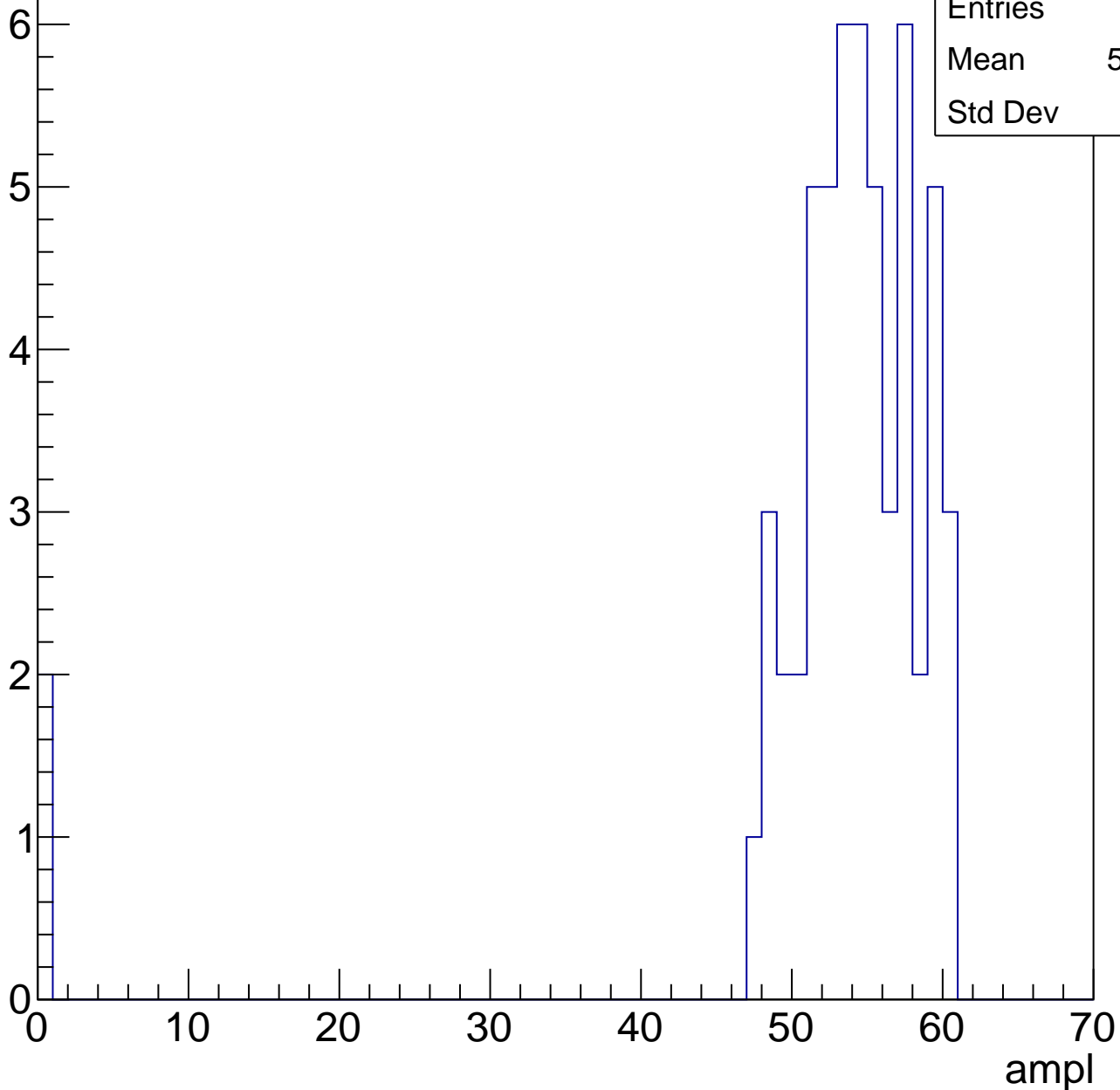


B1L103S, U21-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.18
Std Dev	10.6

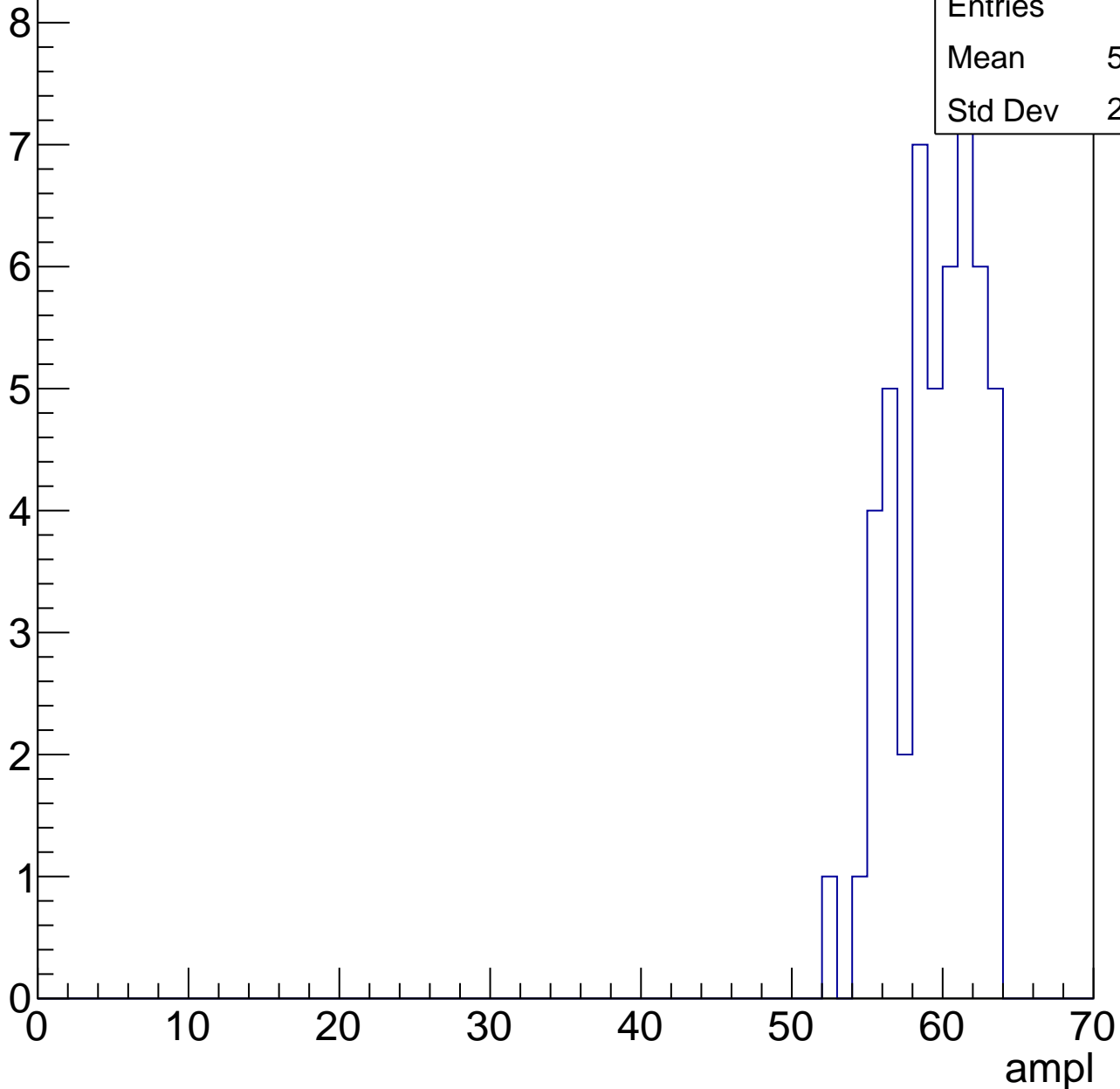


B1L103S, U21-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

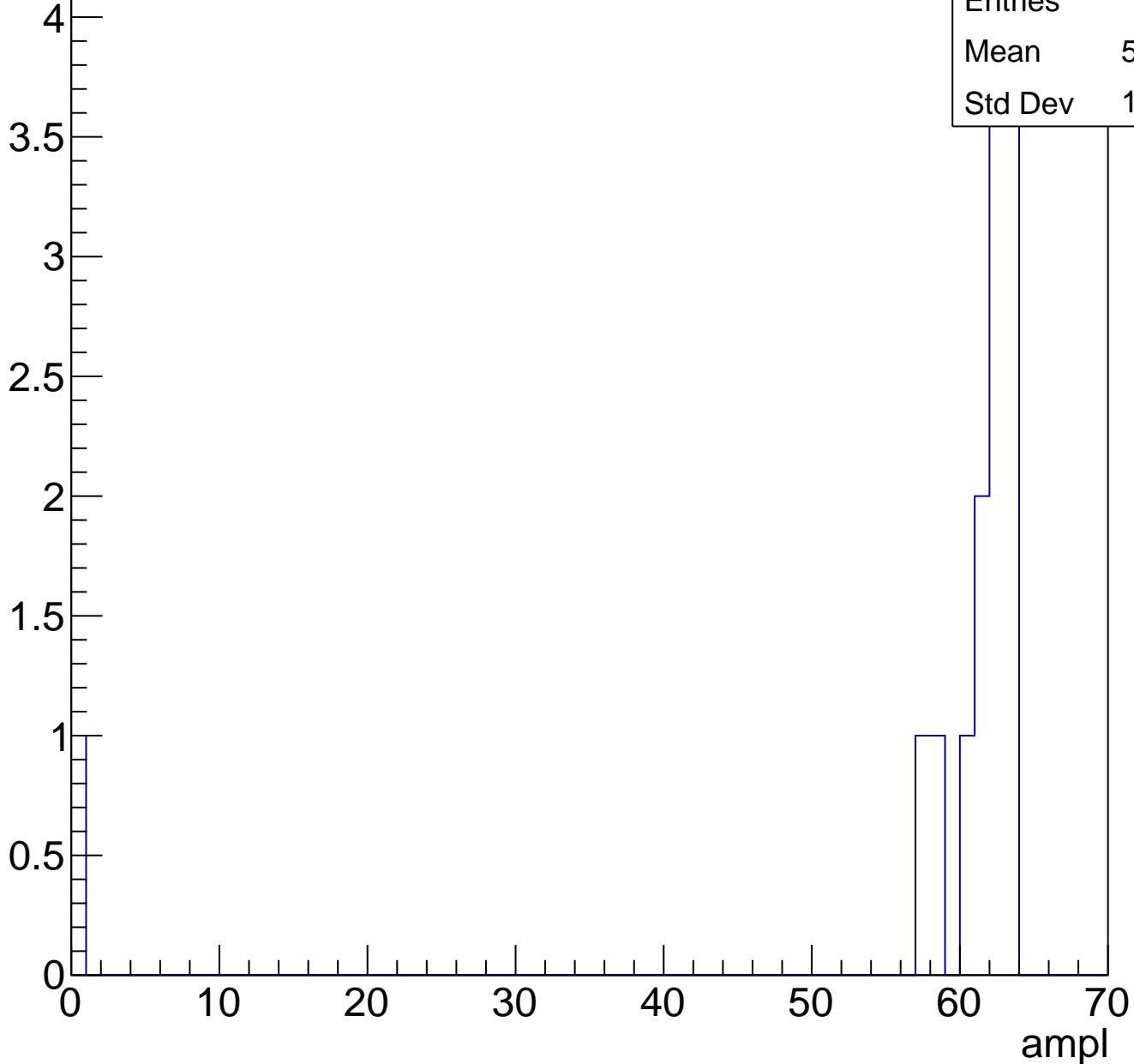
Entries	50
Mean	59.12
Std Dev	2.718



B1L103S, U21-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U21-ch124, adc0

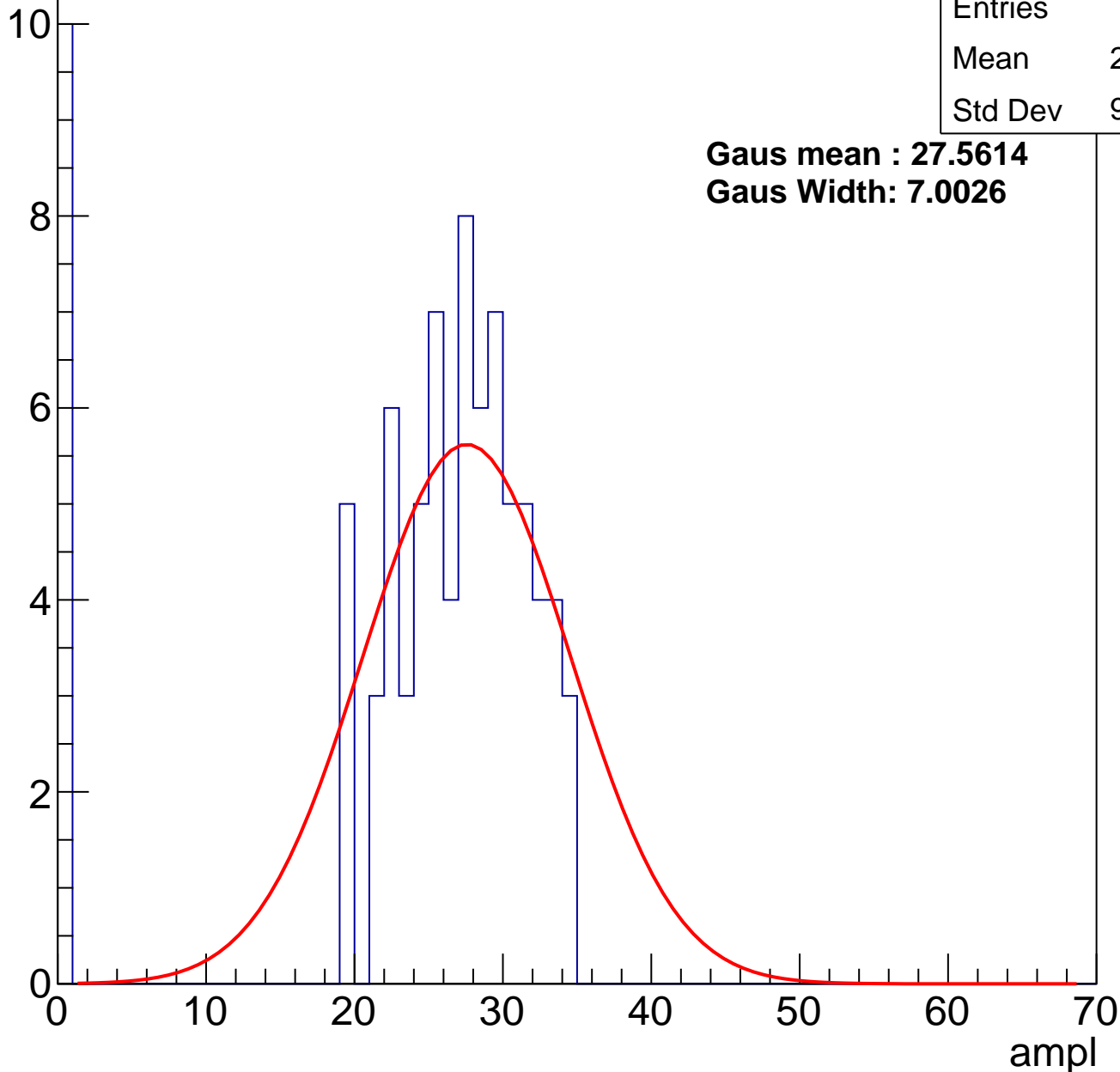
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	23.67
Std Dev	9.456

Gaus mean : 27.5614

Gaus Width: 7.0026

Entry



B1L103S, U21-ch124, adc1

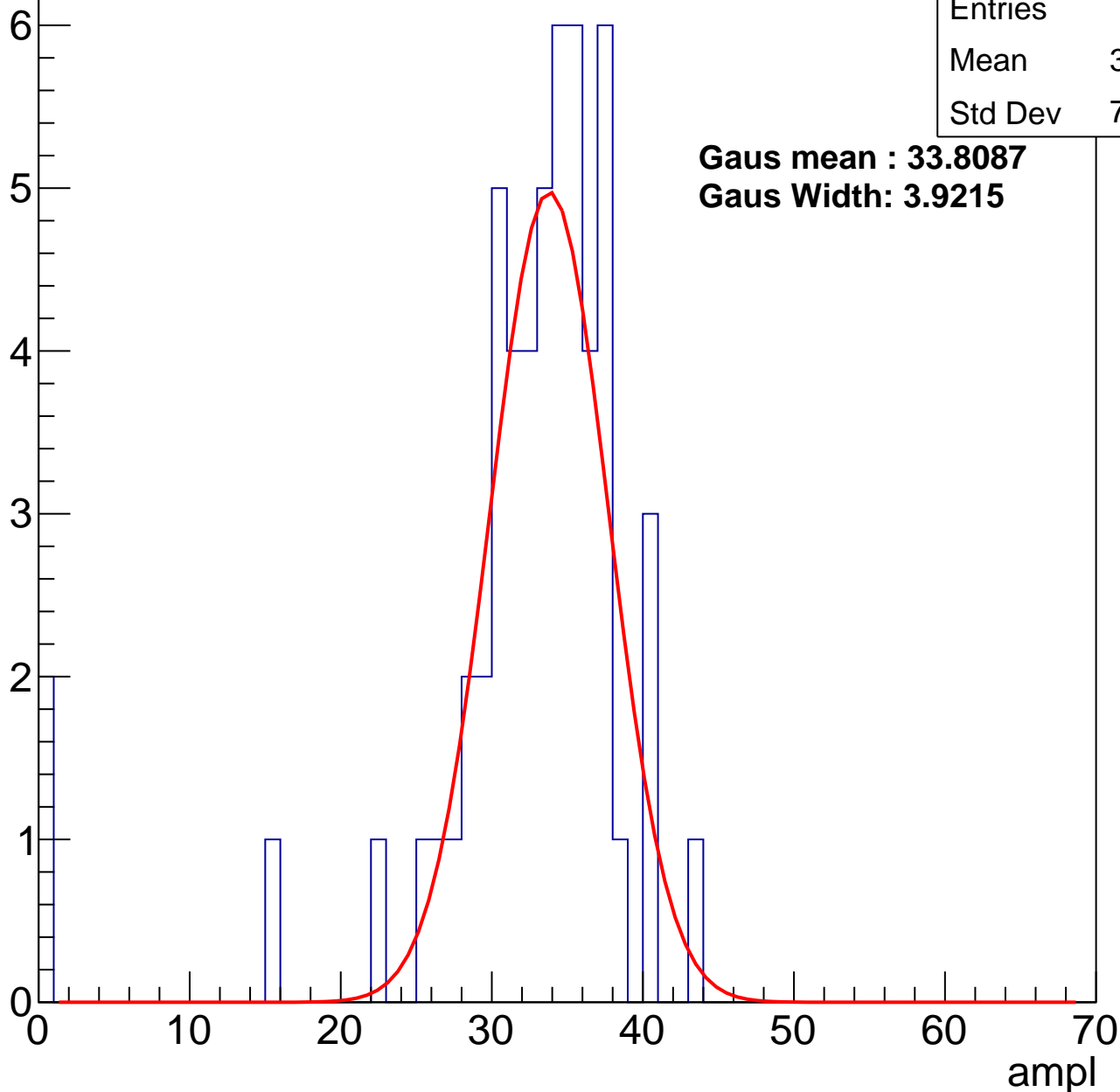
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	31.73
Std Dev	7.633

Gaus mean : 33.8087

Gaus Width: 3.9215



B1L103S, U21-ch124, adc2

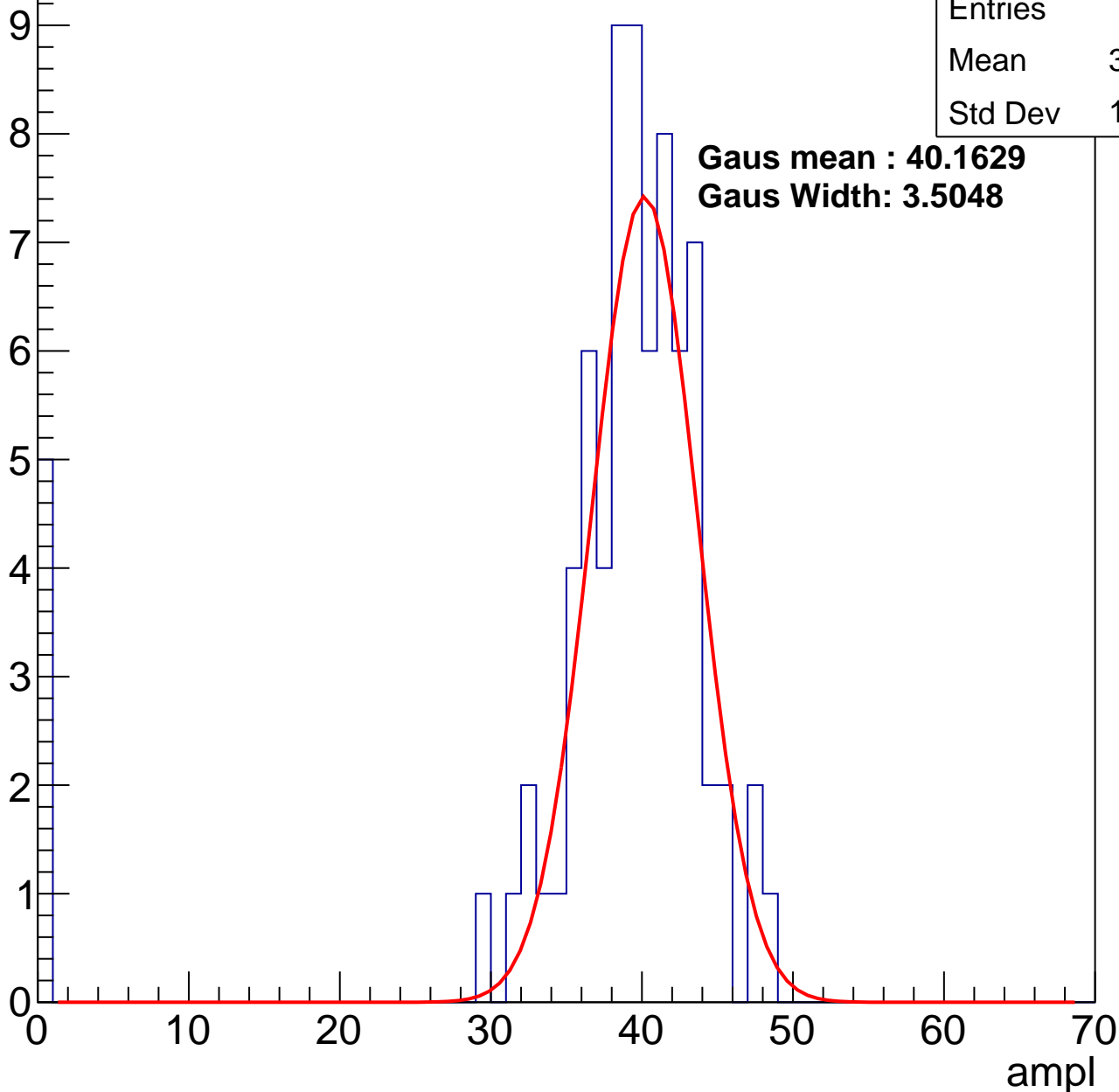
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	36.74
Std Dev	10.33

Gaus mean : 40.1629

Gaus Width: 3.5048

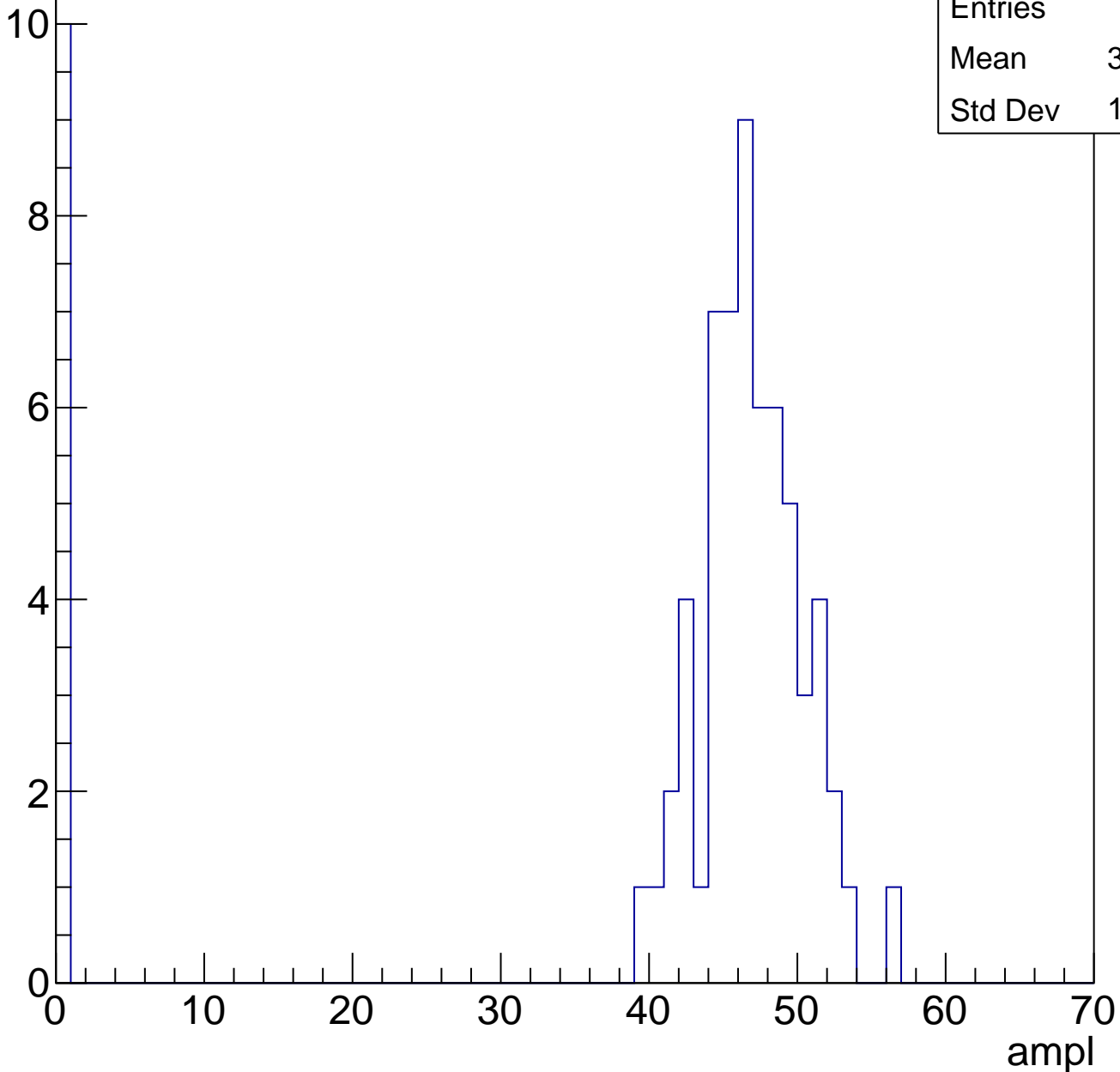


B1L103S, U21-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	39.87
Std Dev	16.57

Entry

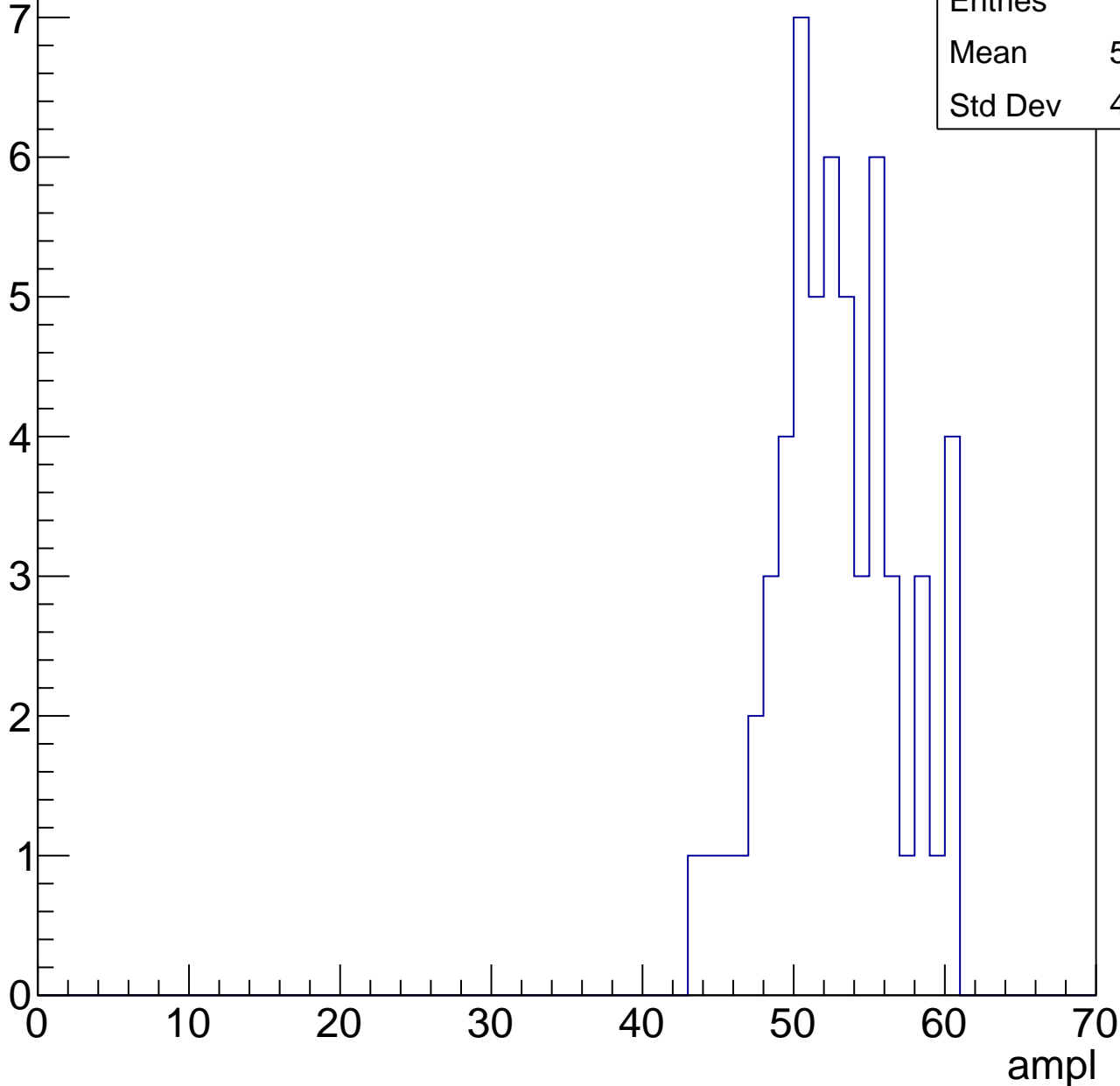


B1L103S, U21-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	52.35
Std Dev	4.076

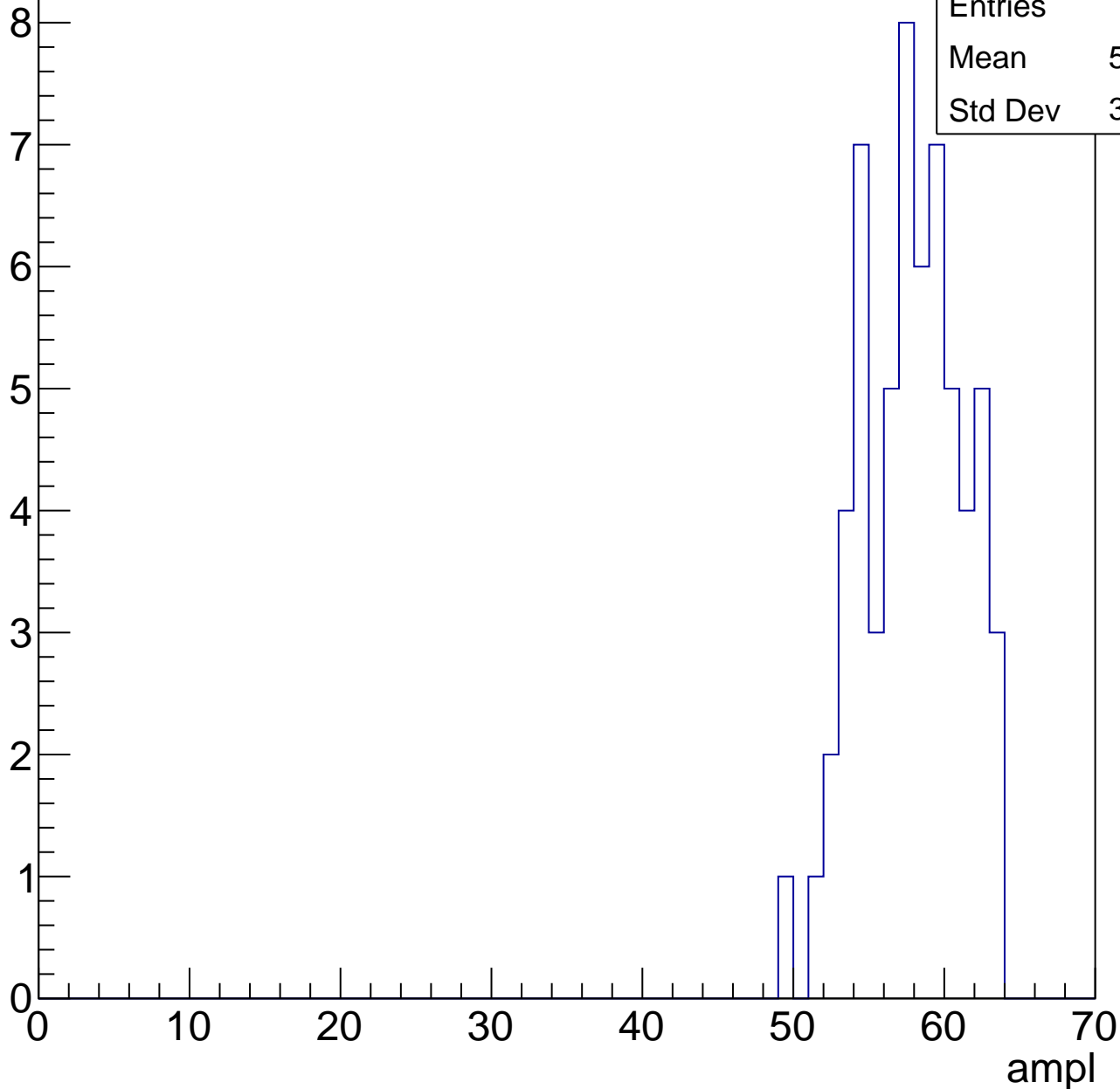


B1L103S, U21-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

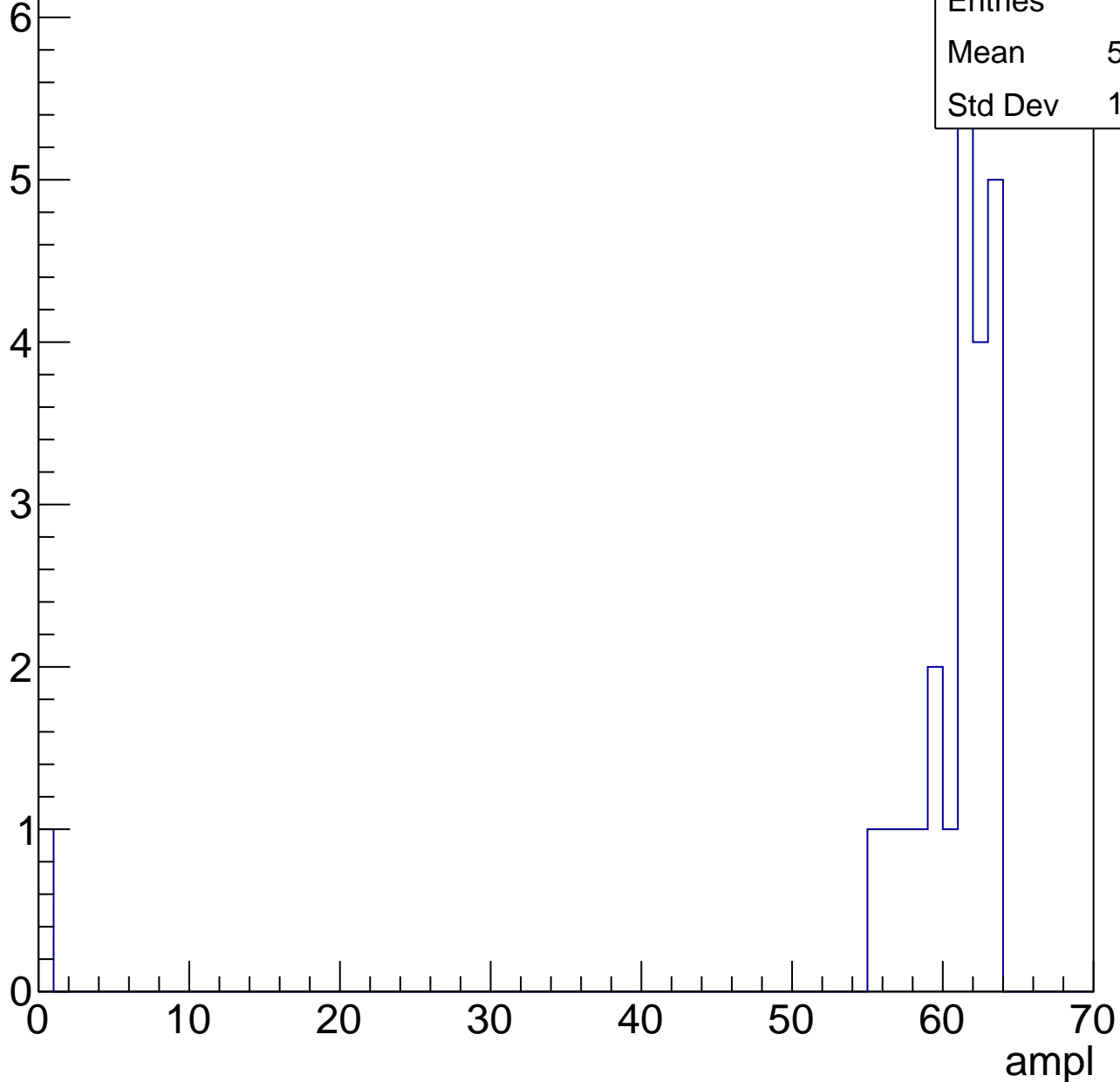
Entries	61
Mean	57.36
Std Dev	3.294



B1L103S, U21-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U21-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51

Entry



B1L103S, U21-ch125, adc0

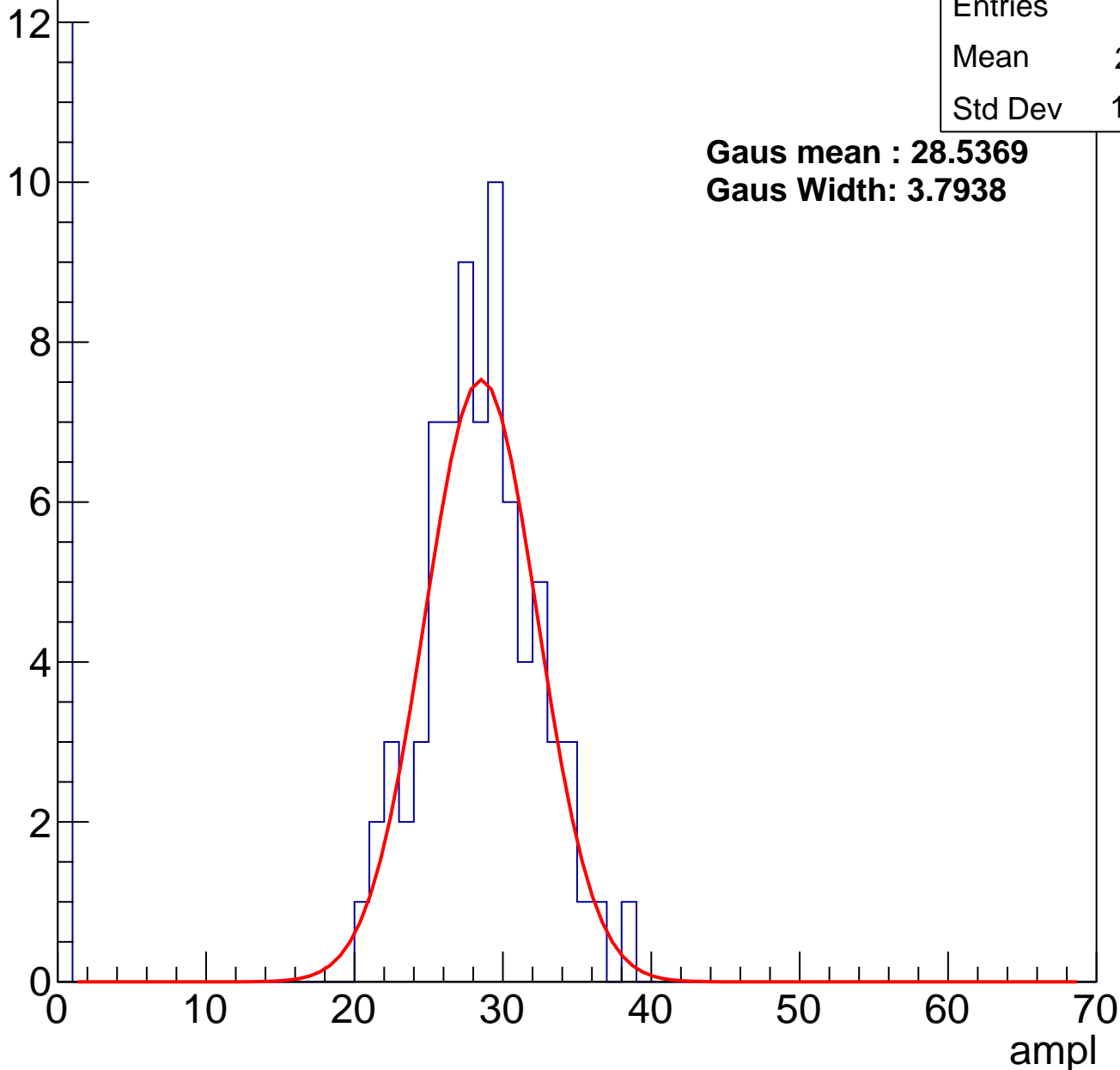
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	24.21
Std Dev	10.27

Gaus mean : 28.5369

Gaus Width: 3.7938

Entry



B1L103S, U21-ch125, adc1

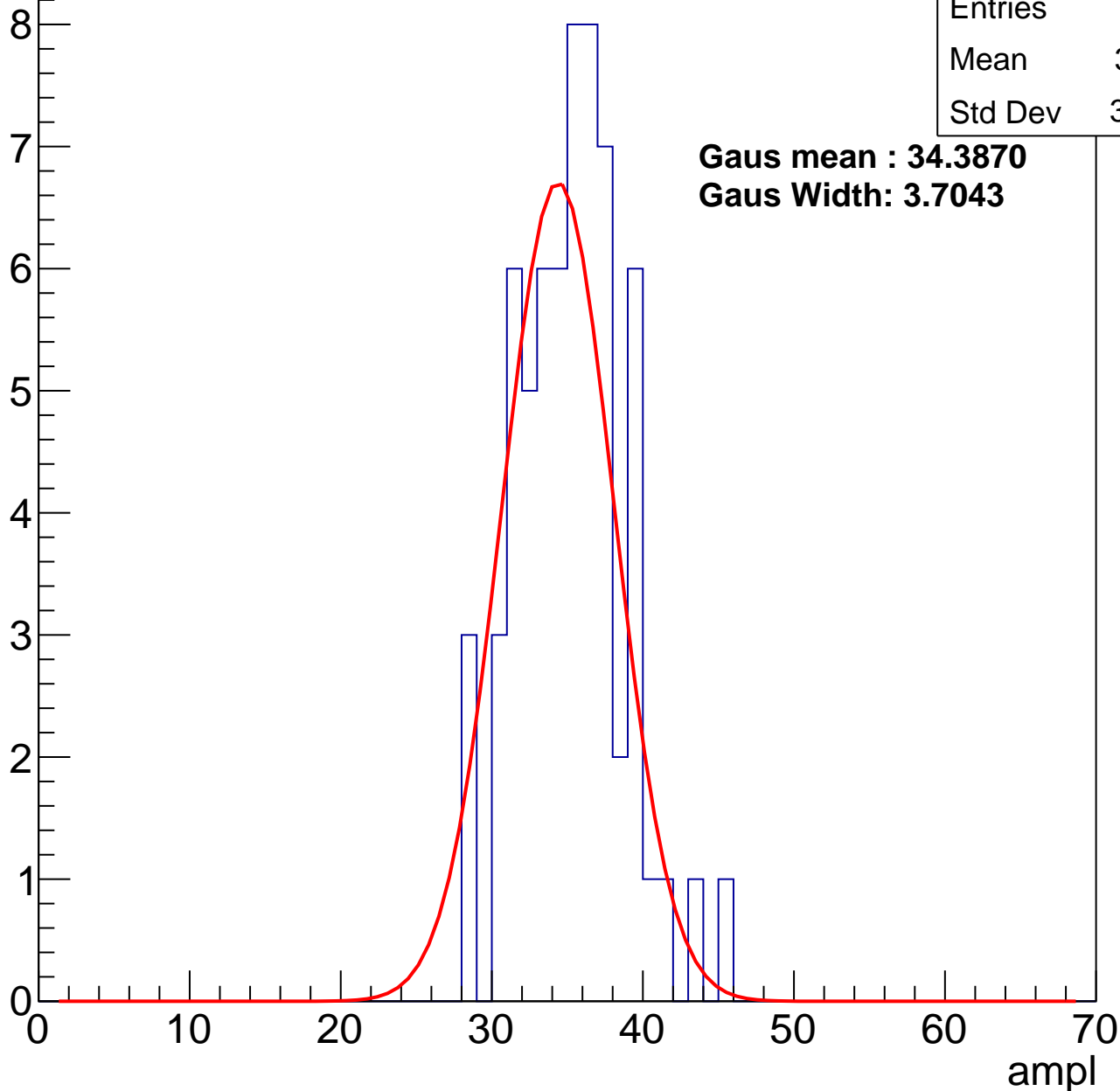
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.81
Std Dev	3.459

Gaus mean : 34.3870

Gaus Width: 3.7043



B1L103S, U21-ch125, adc2

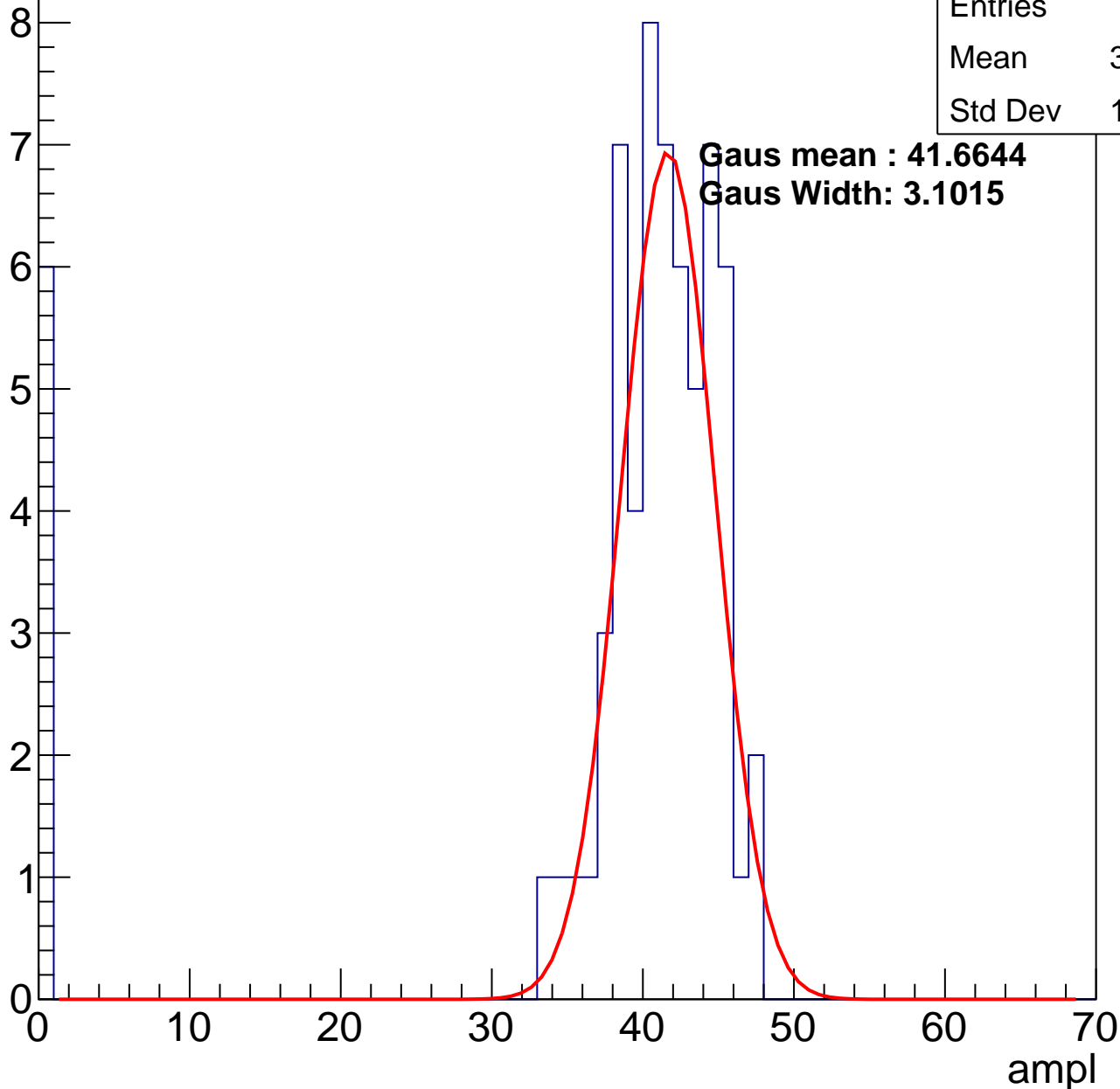
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	37.32
Std Dev	12.17

Gaus mean : 41.6644

Gaus Width: 3.1015

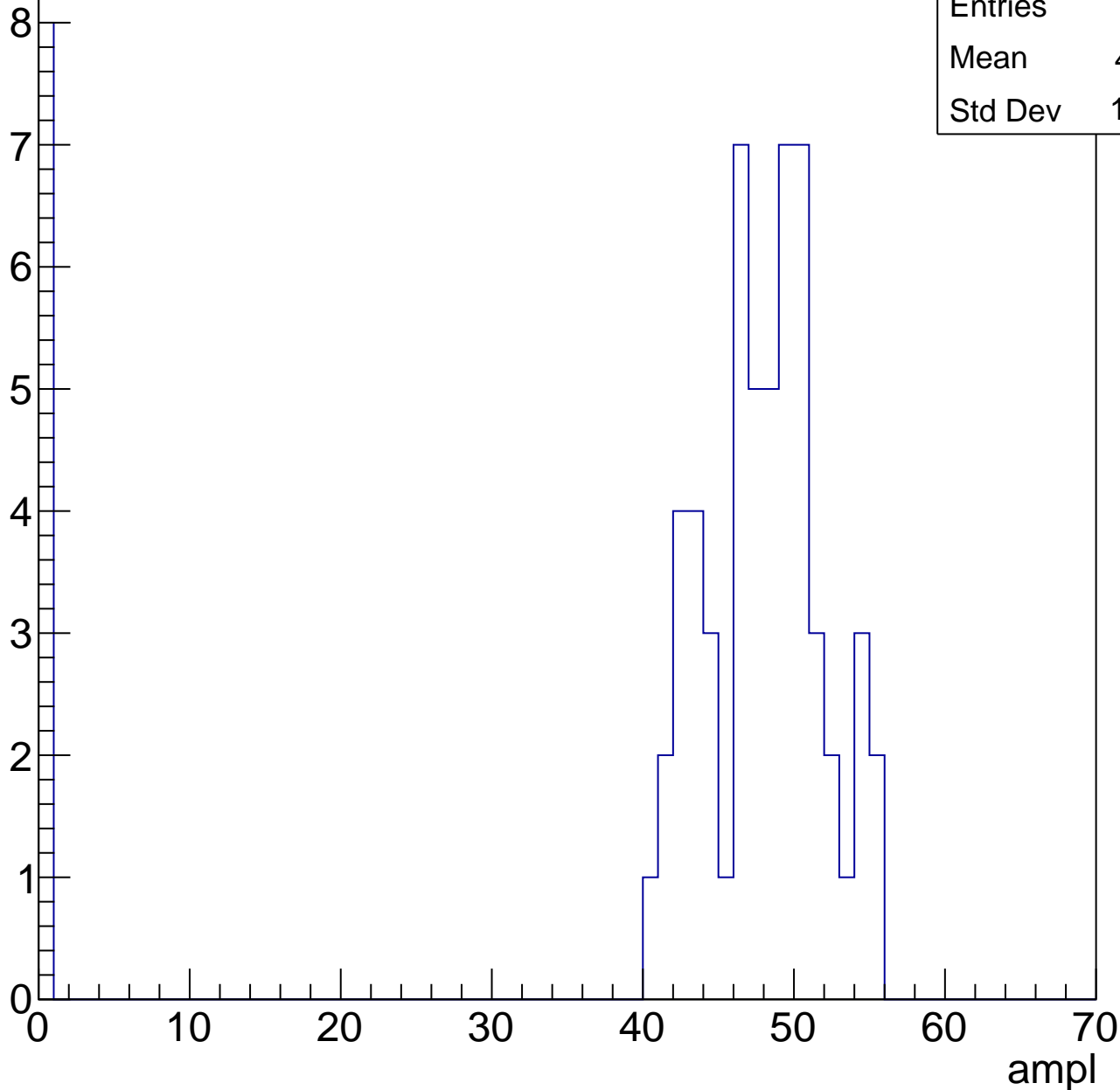


B1L103S, U21-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	41.71
Std Dev	16.02

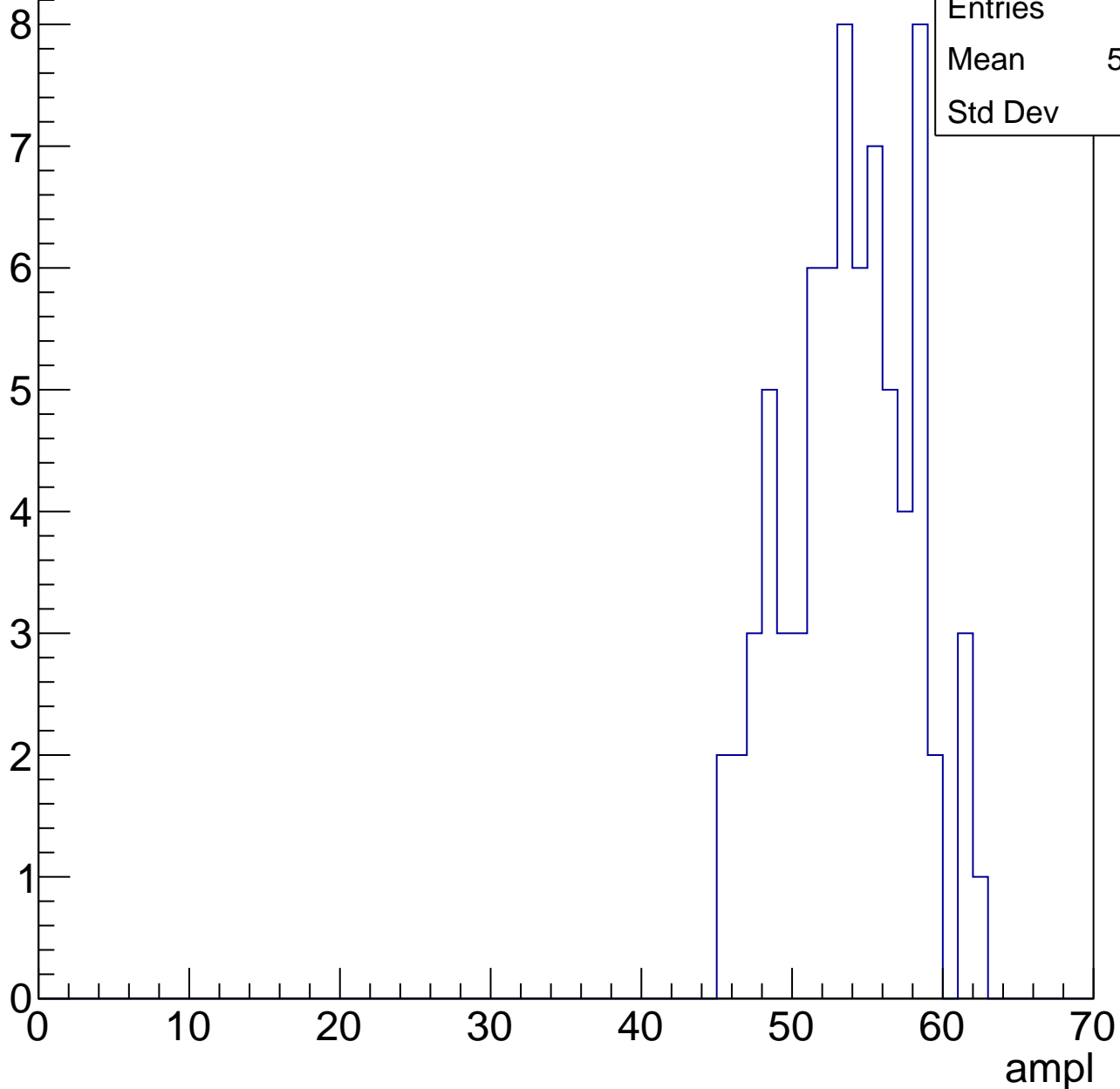


B1L103S, U21-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	53.32
Std Dev	4.1



B1L103S, U21-ch125, adc5

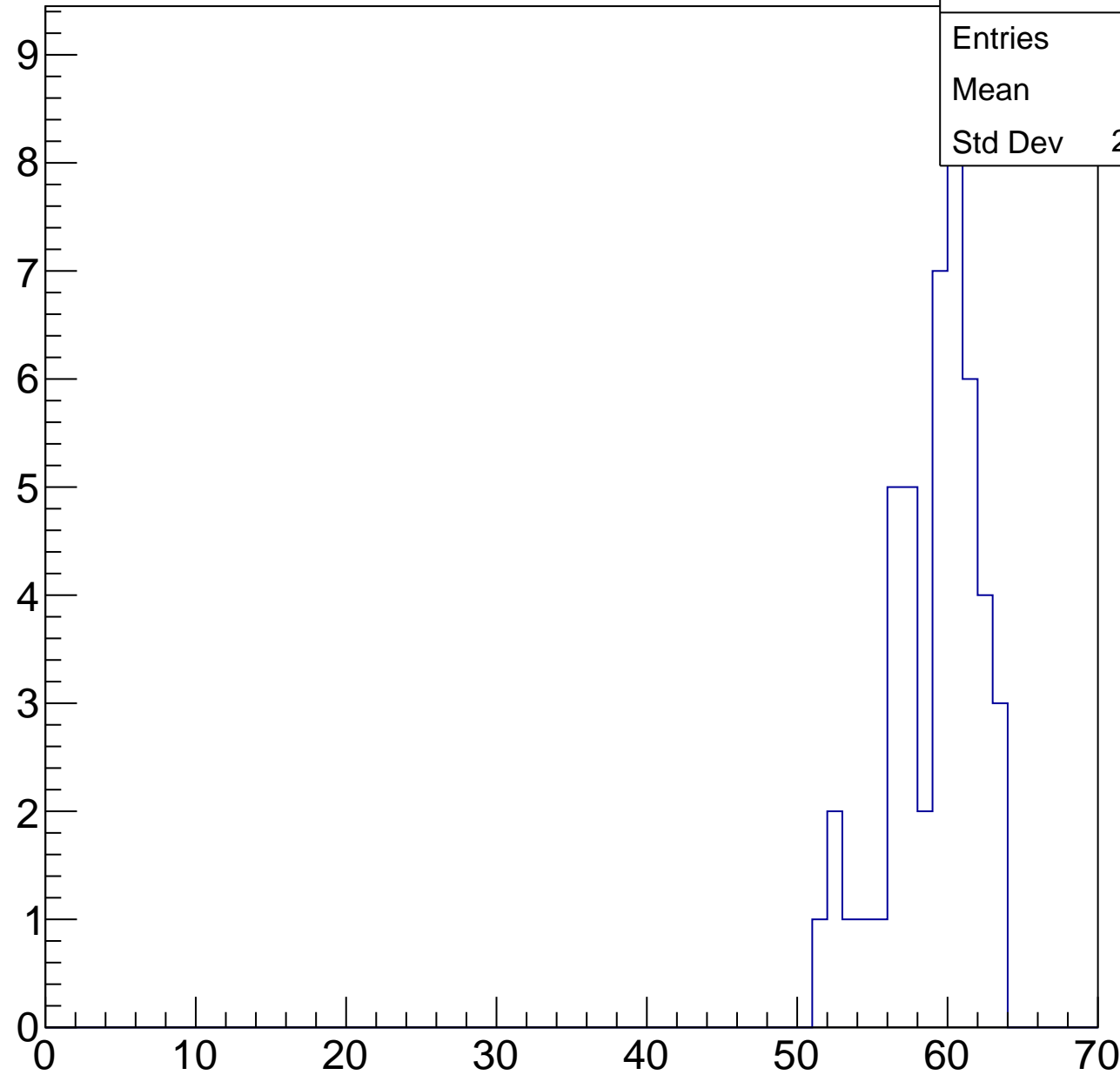
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.6
Std Dev	2.973

ampl

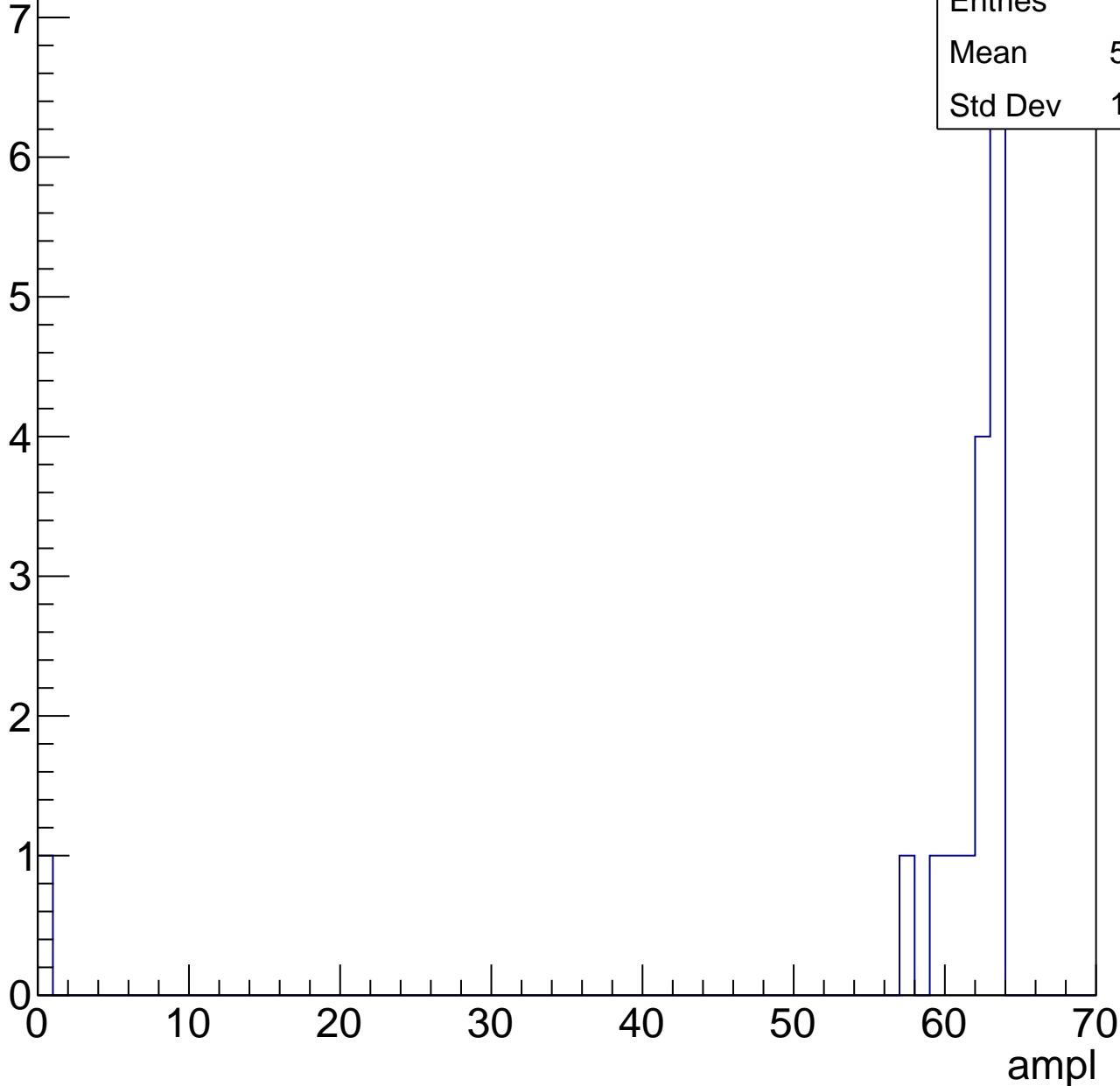


B1L103S, U21-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

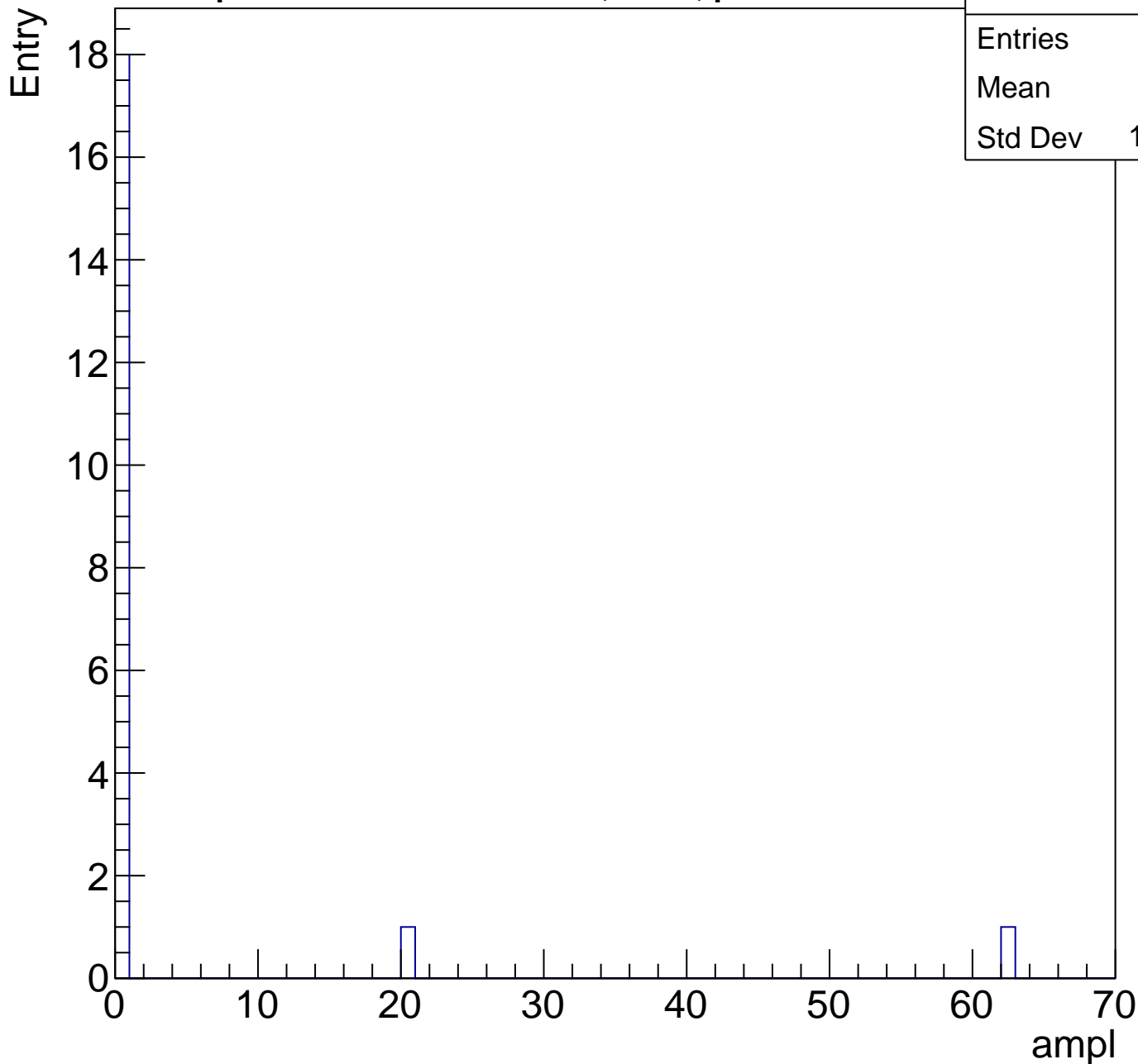
Entries	16
Mean	57.88
Std Dev	15.04



B1L103S, U21-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.1
Std Dev	13.98



B1L103S, U21-ch126, adc0

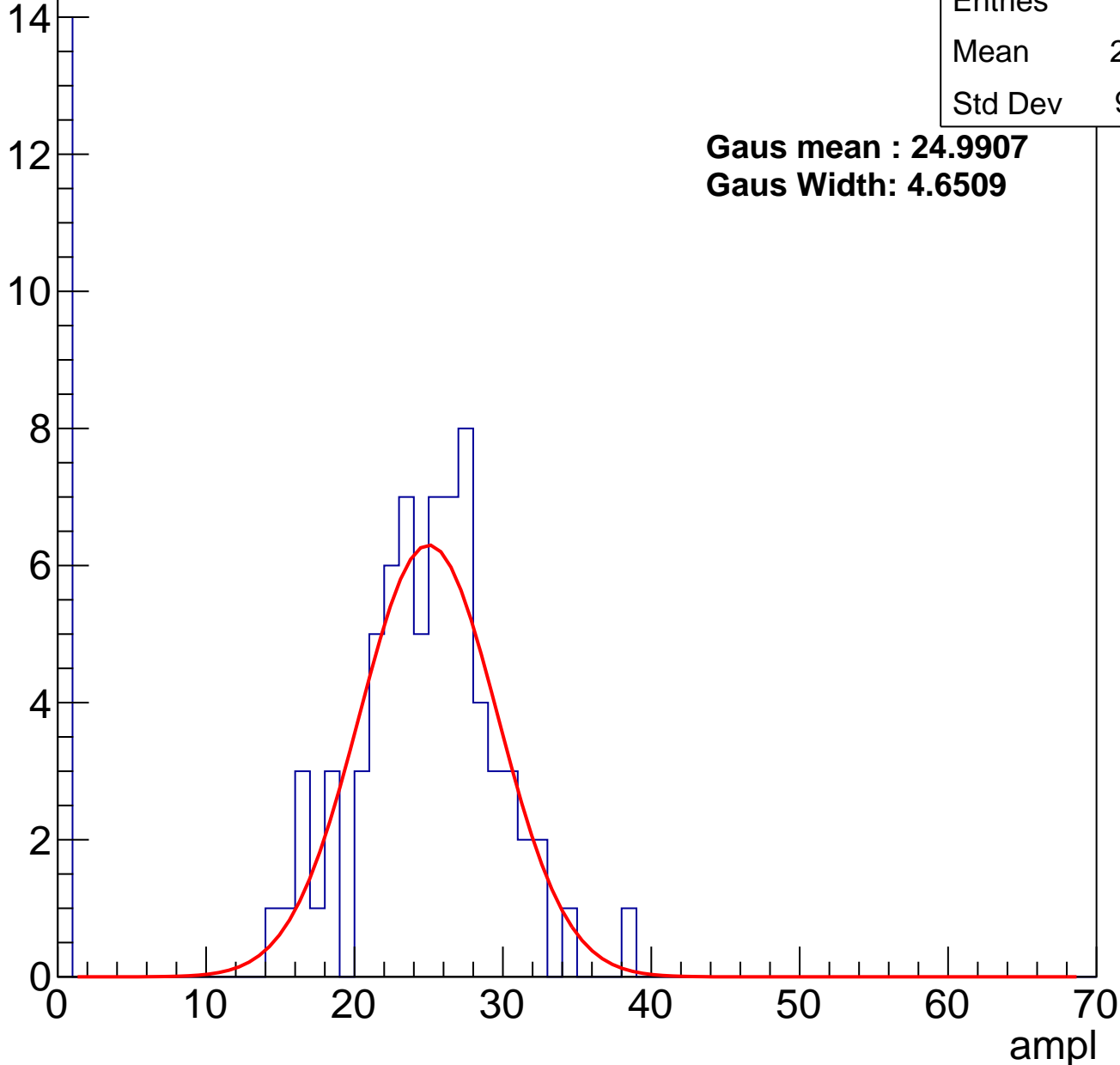
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	20.53
Std Dev	9.911

Gaus mean : 24.9907

Gaus Width: 4.6509

Entry



B1L103S, U21-ch126, adc1

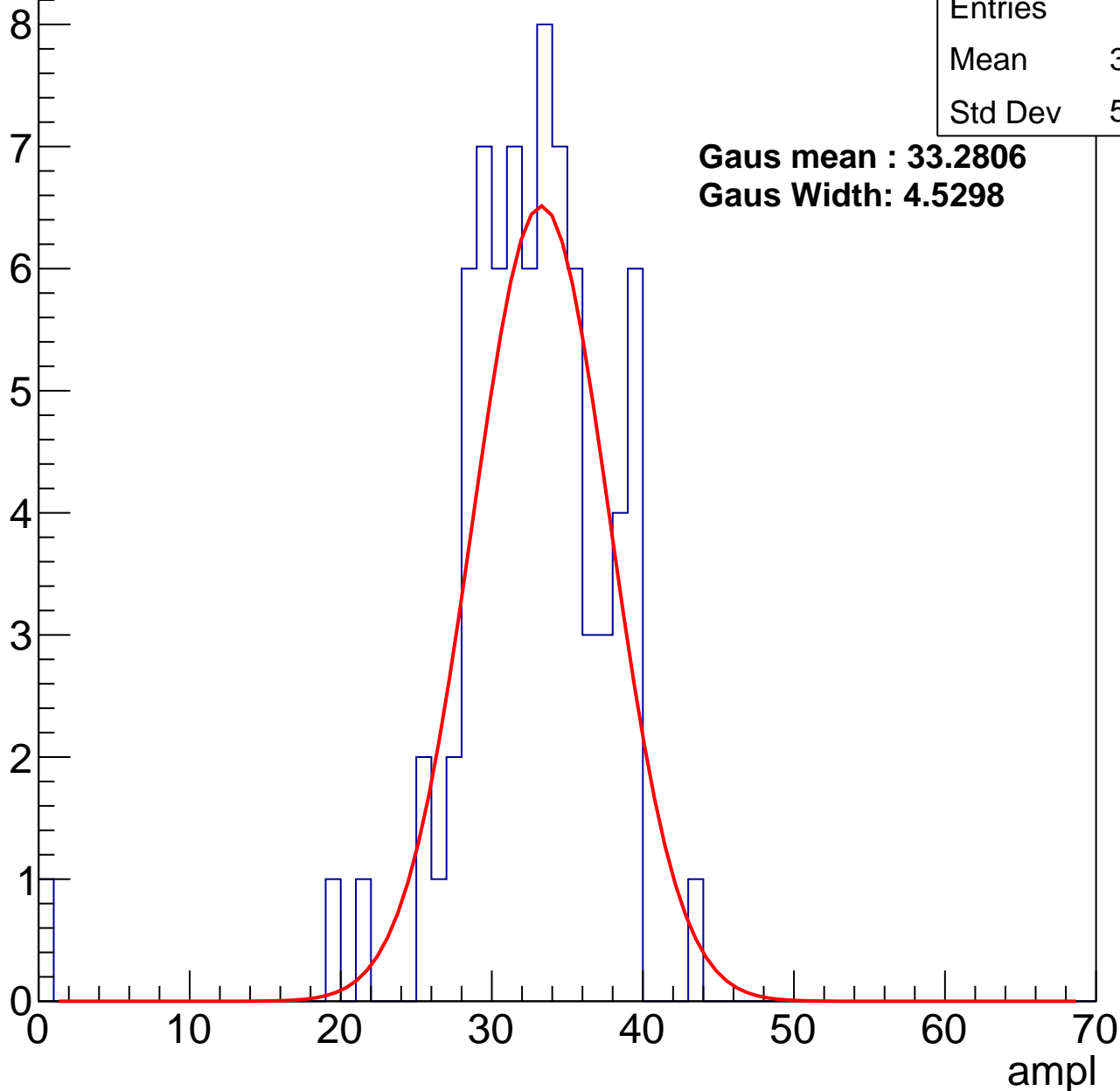
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	31.92
Std Dev	5.615

Gaus mean : 33.2806

Gaus Width: 4.5298



B1L103S, U21-ch126, adc2

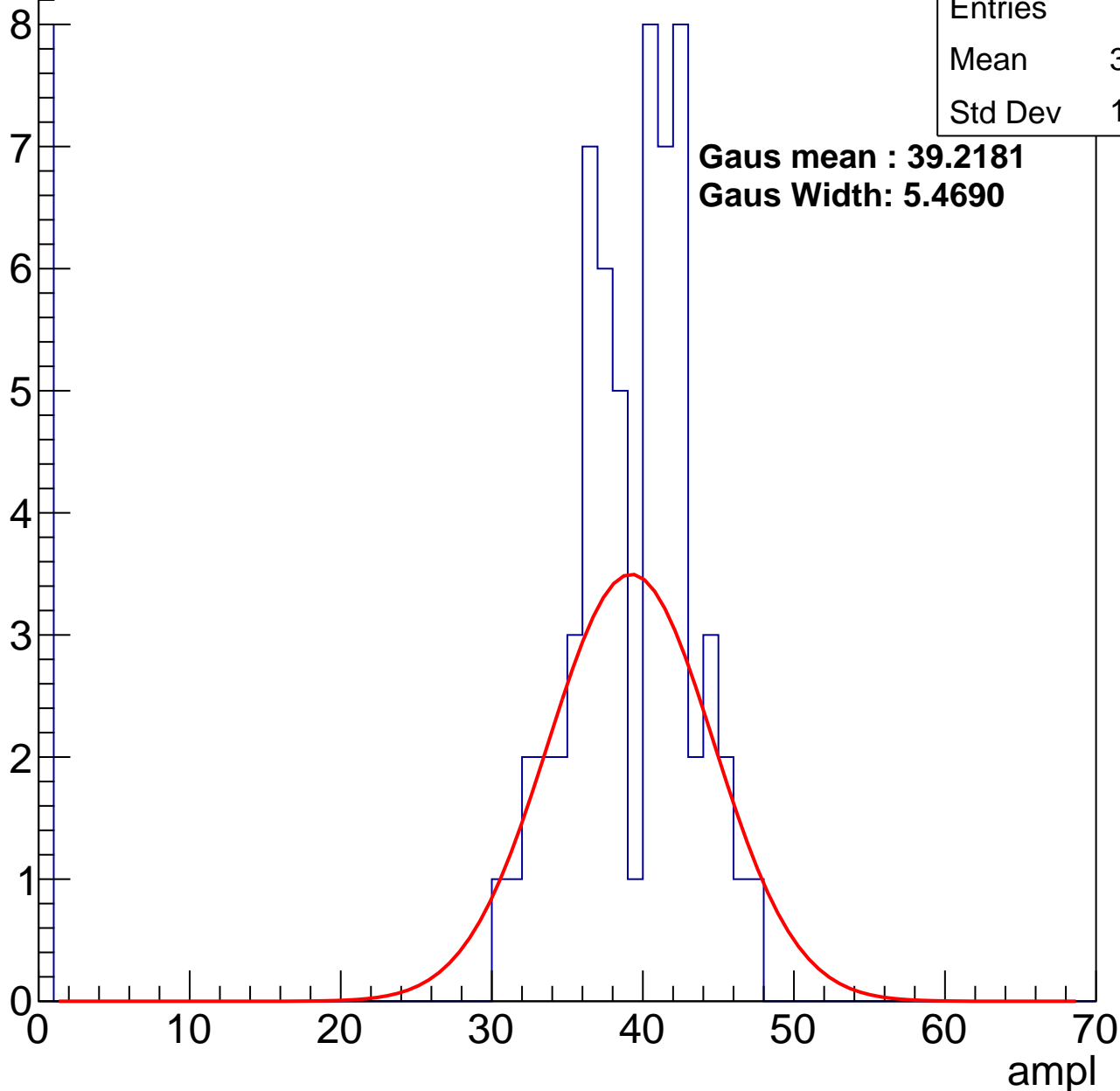
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.44
Std Dev	12.88

Gaus mean : 39.2181

Gaus Width: 5.4690

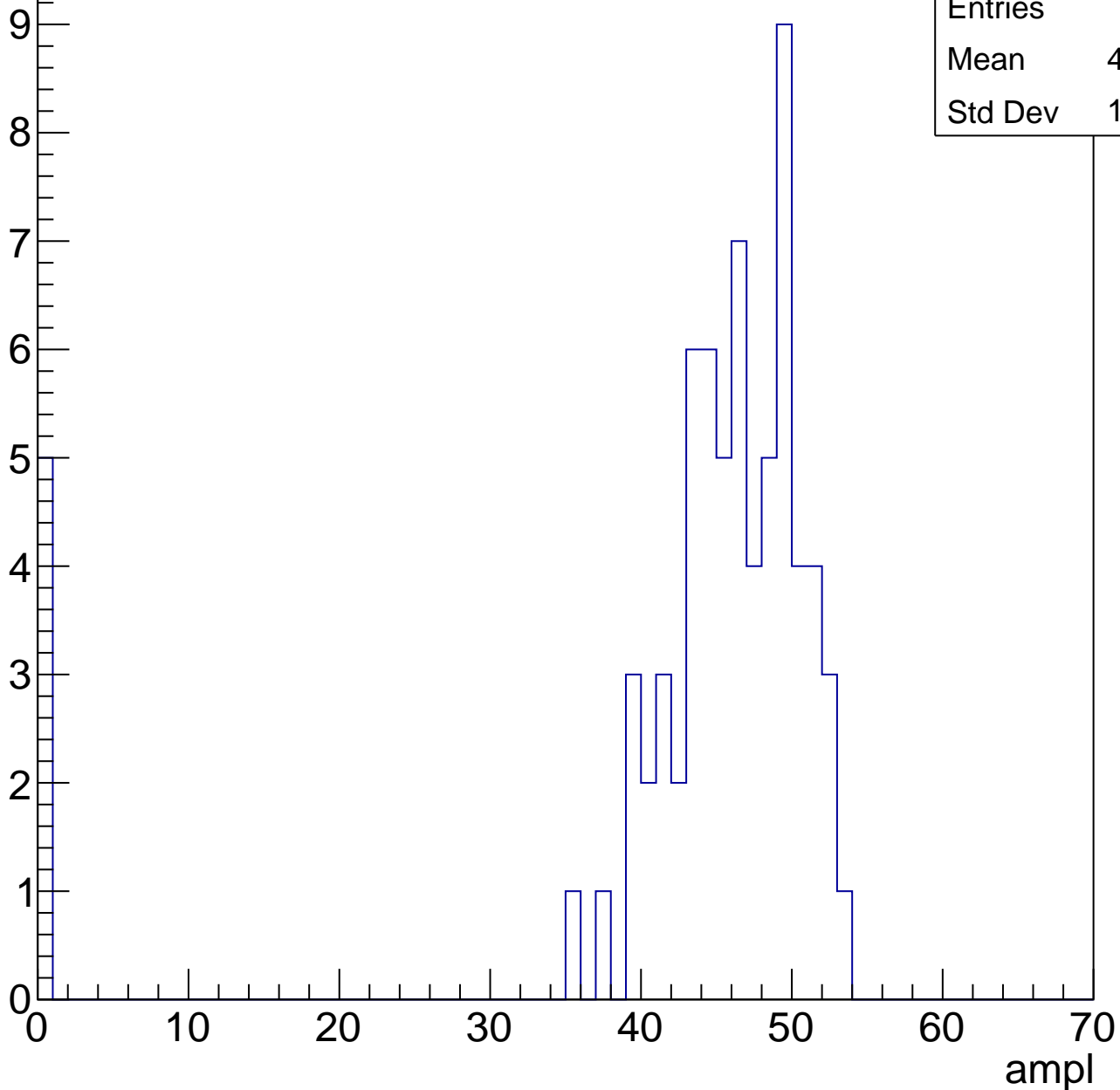


B1L103S, U21-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	42.63
Std Dev	12.34

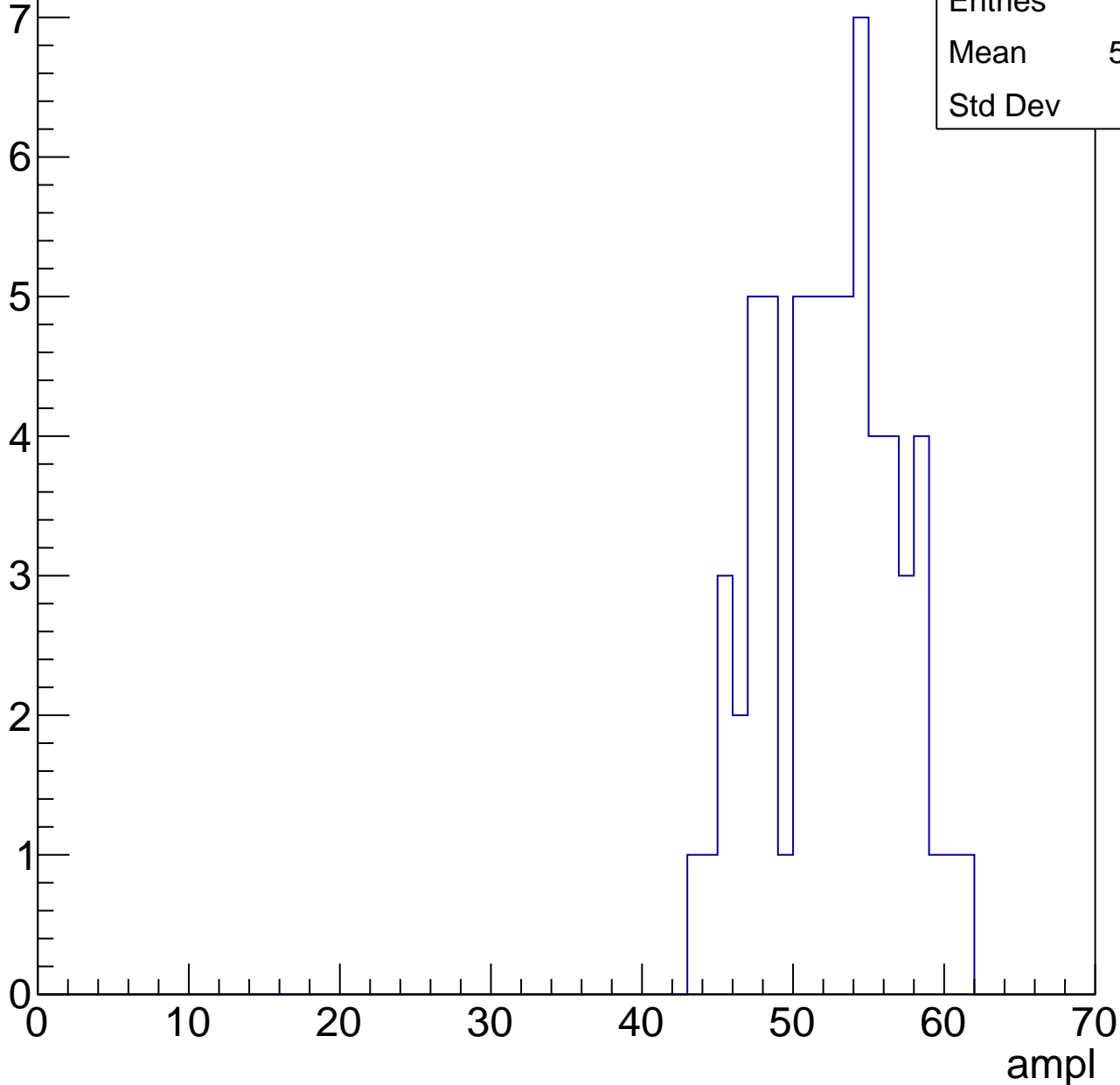


B1L103S, U21-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	51.95
Std Dev	4.27

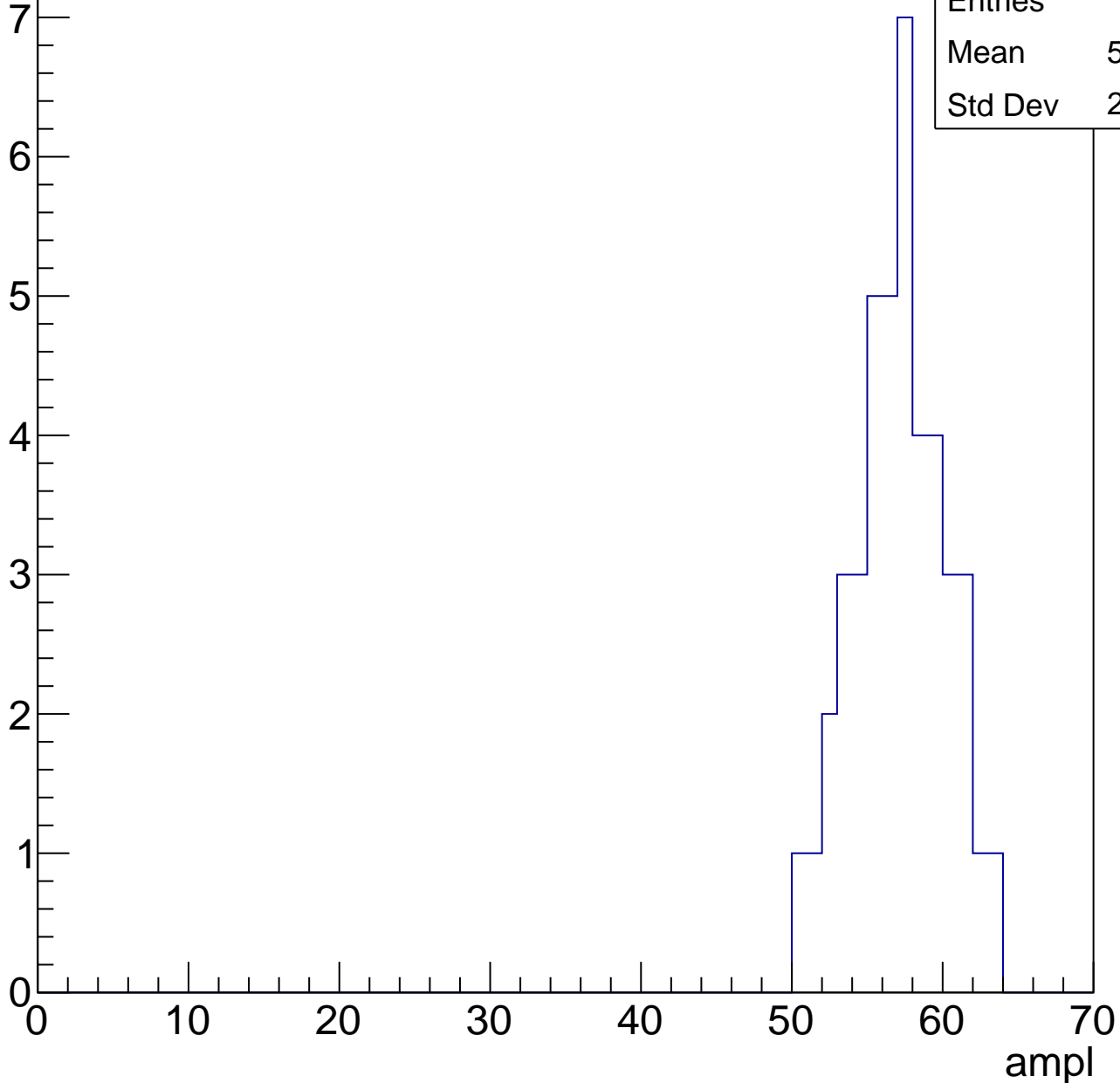


B1L103S, U21-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	56.65
Std Dev	2.995

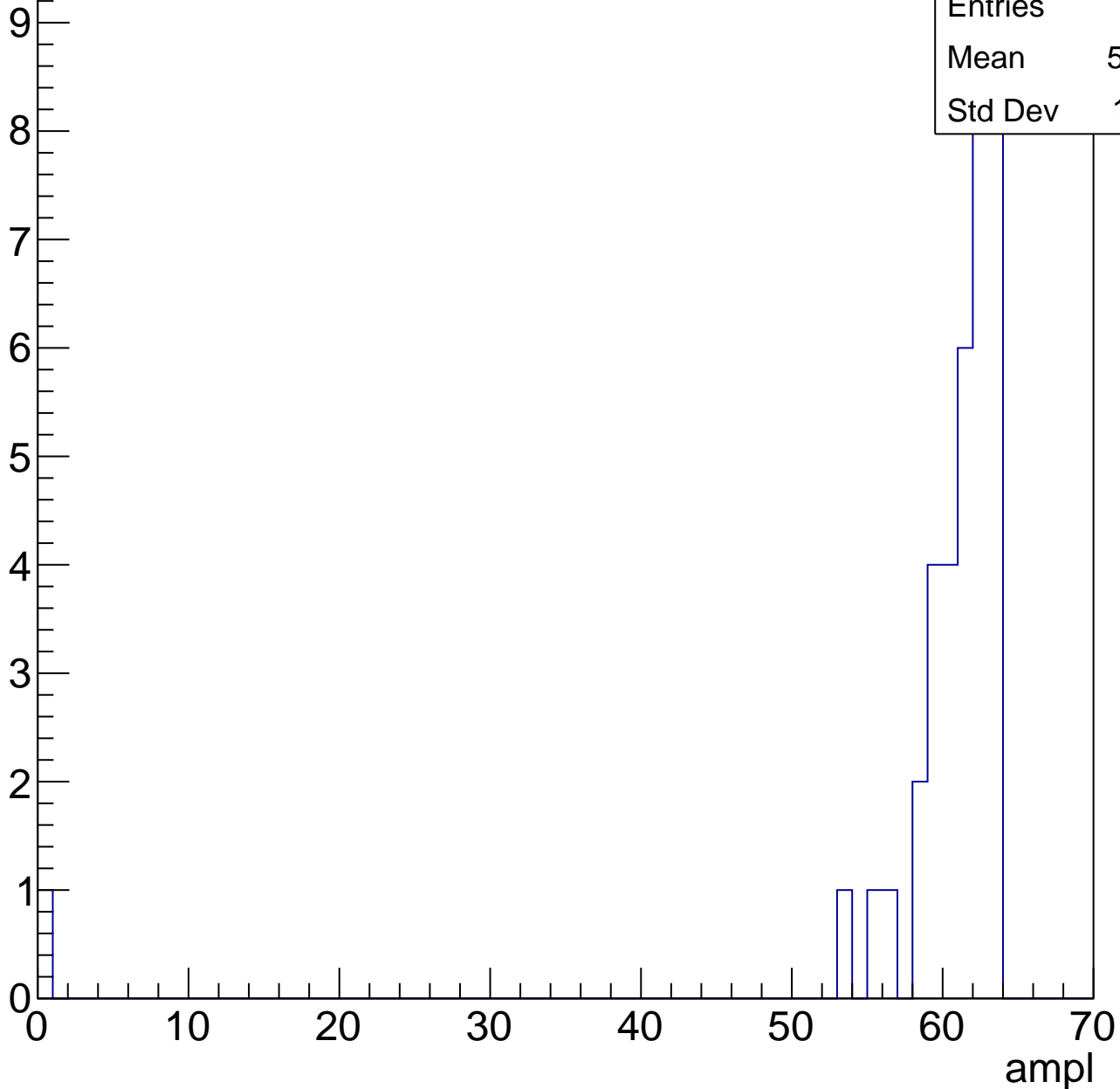


B1L103S, U21-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	59.03
Std Dev	10.11

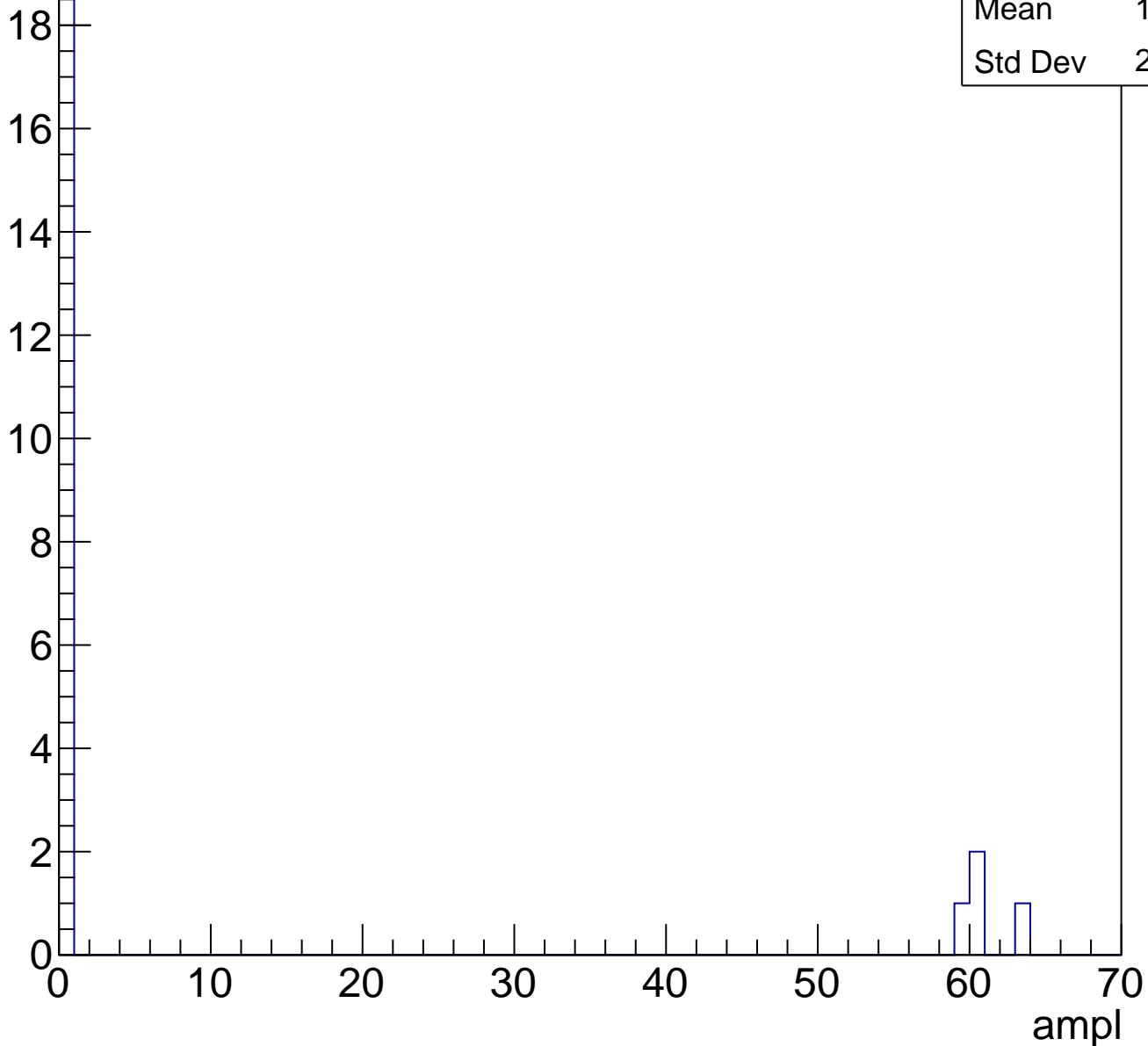


B1L103S, U21-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.52
Std Dev	22.94

Entry



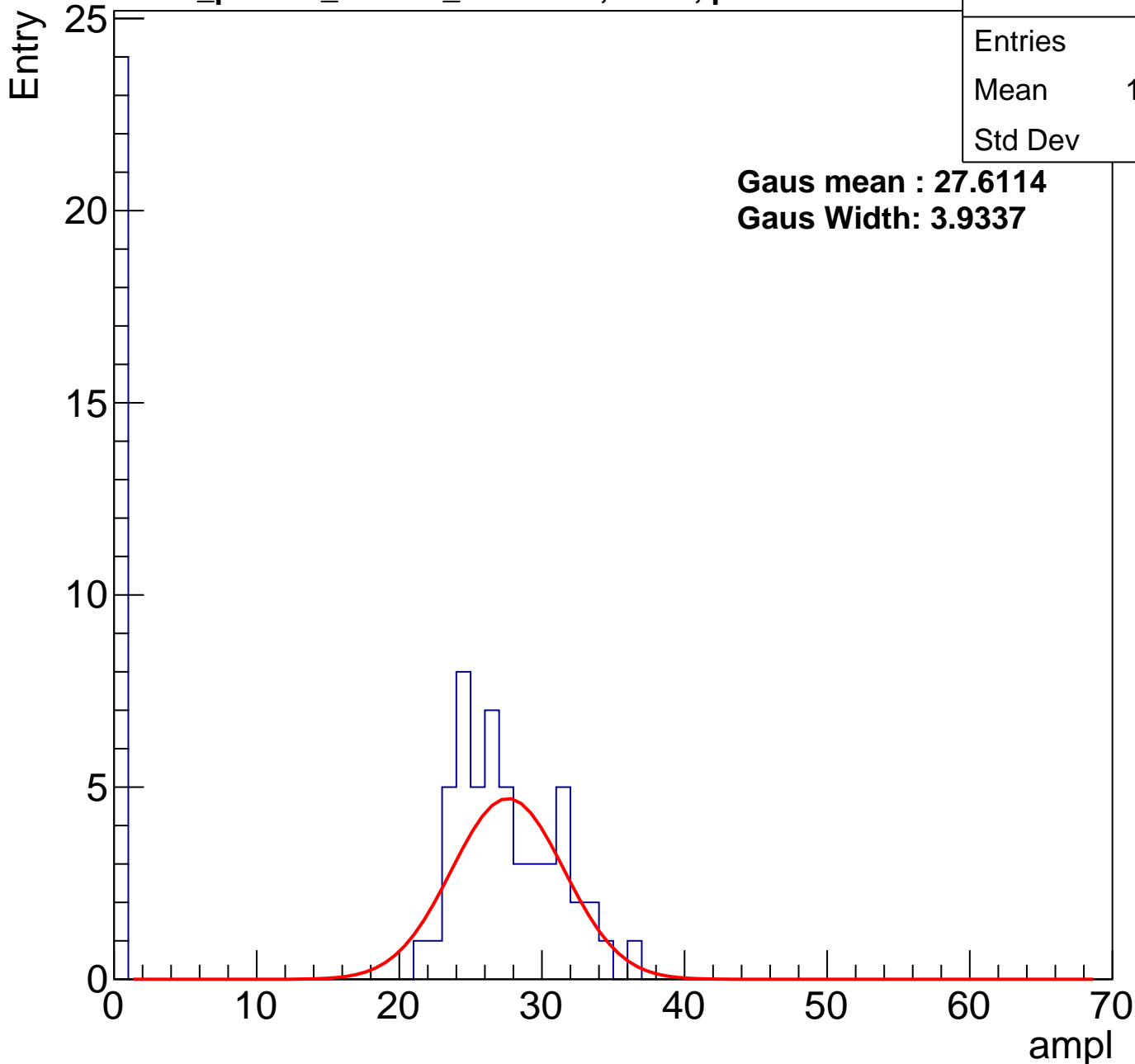
B1L103S, U21-ch127, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	18.53
Std Dev	12.9

Gaus mean : 27.6114

Gaus Width: 3.9337



B1L103S, U21-ch127, adc1

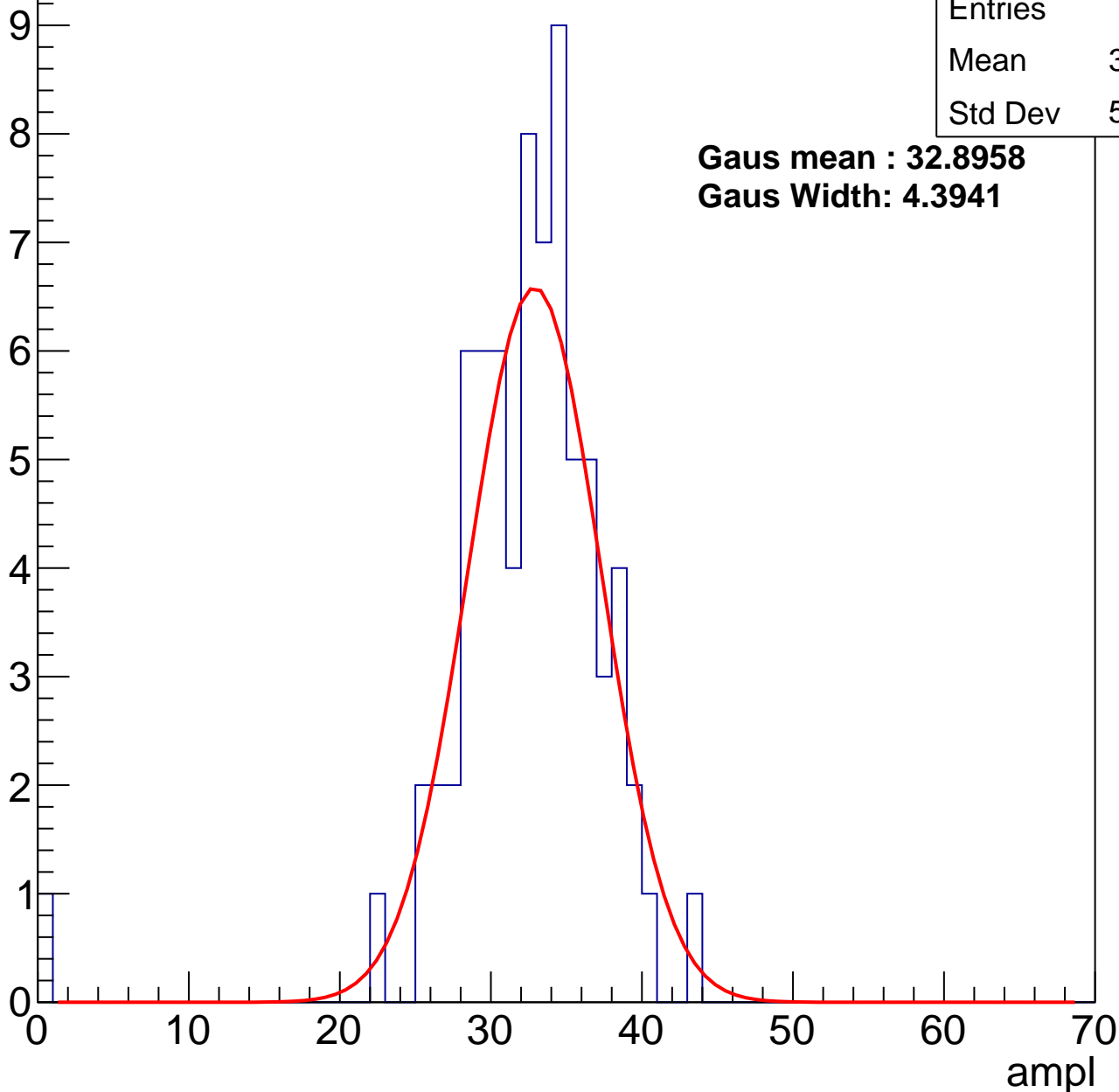
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31.95
Std Dev	5.406

Gaus mean : 32.8958

Gaus Width: 4.3941



B1L103S, U21-ch127, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	35.17
Std Dev	13.78

Gaus mean : 40.5599

Gaus Width: 3.8520

Entry

10

8

6

4

2

0

0

10

20

30

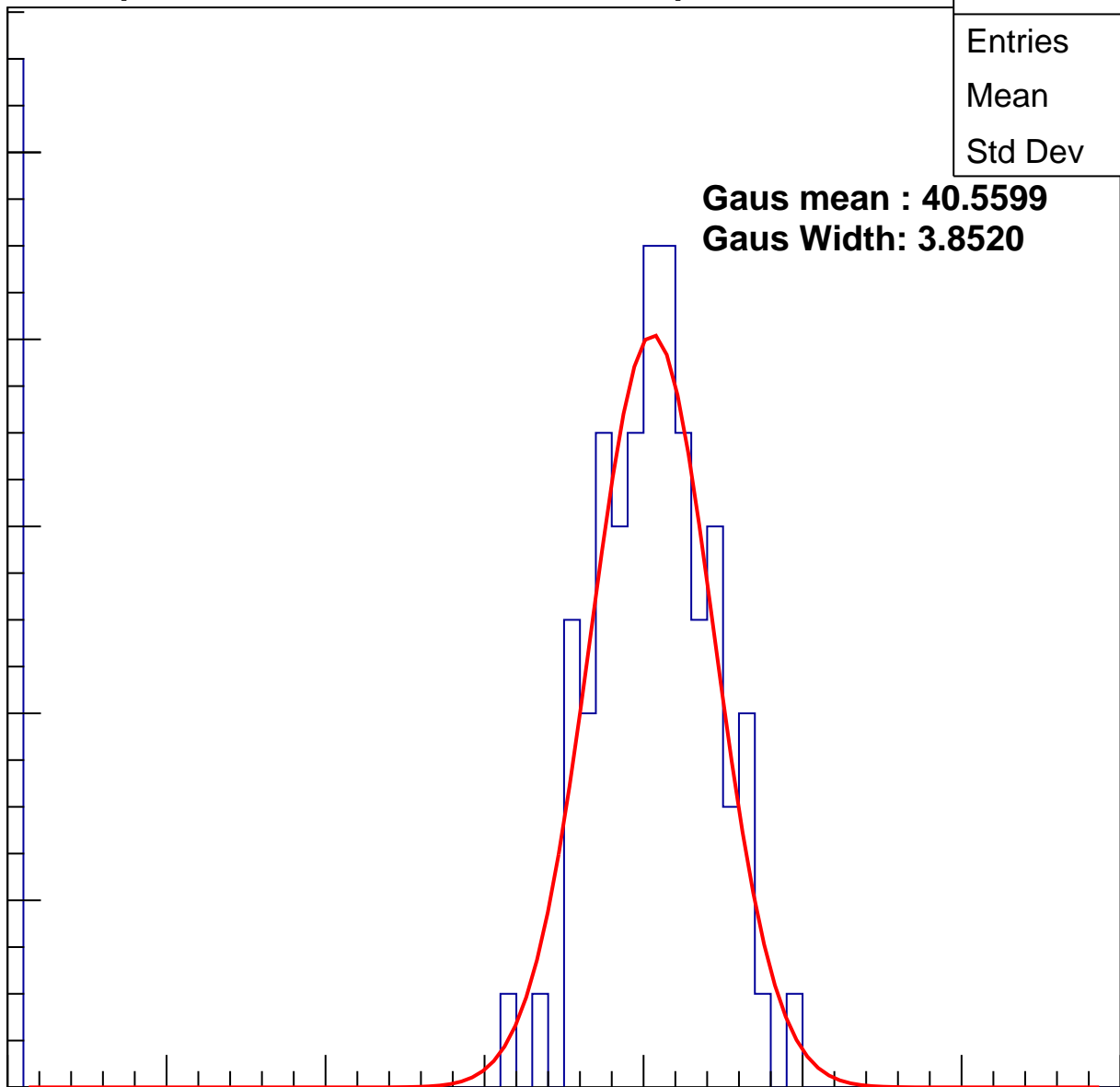
40

50

60

70

ampl

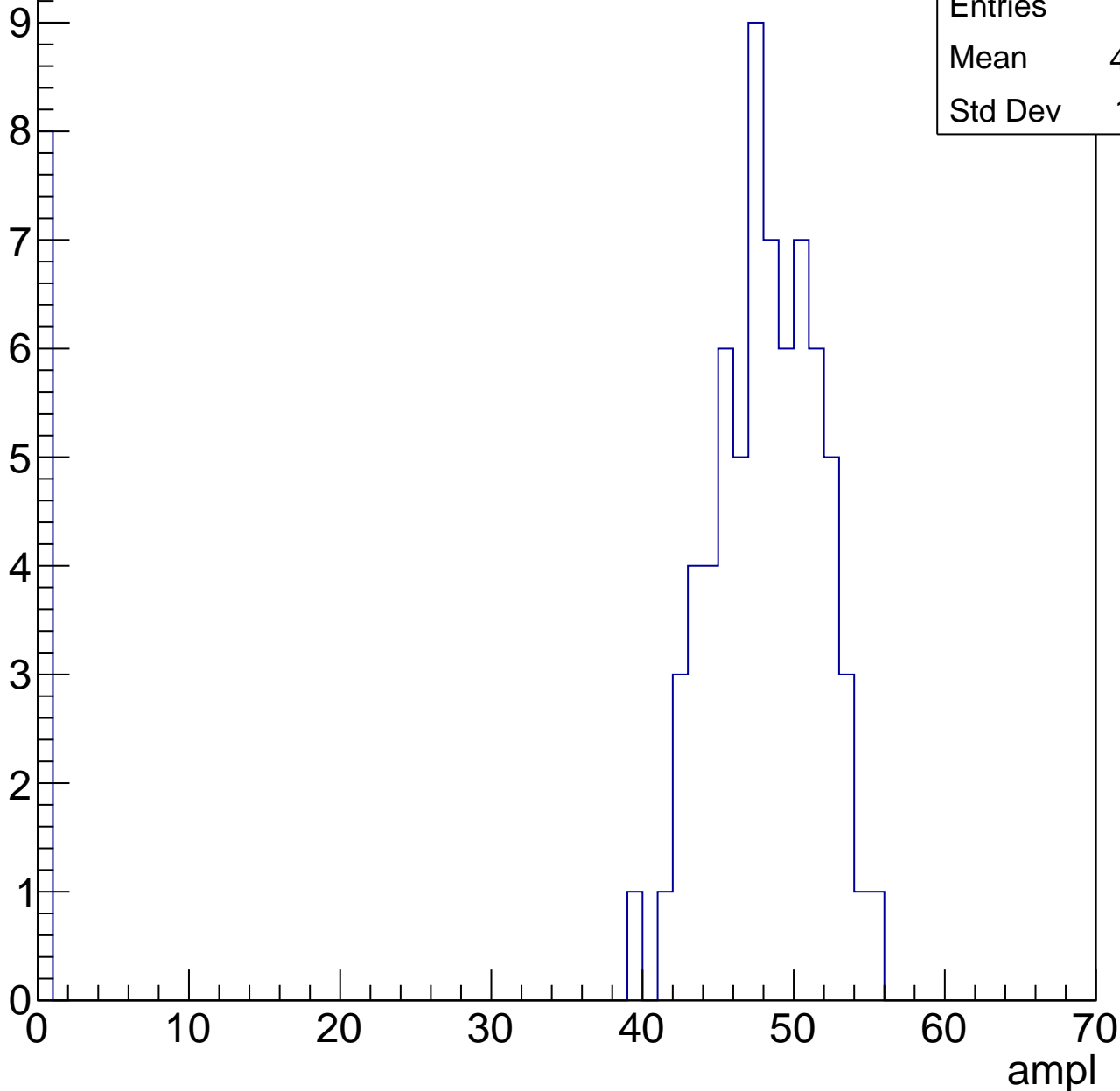


B1L103S, U21-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	42.74
Std Dev	14.91

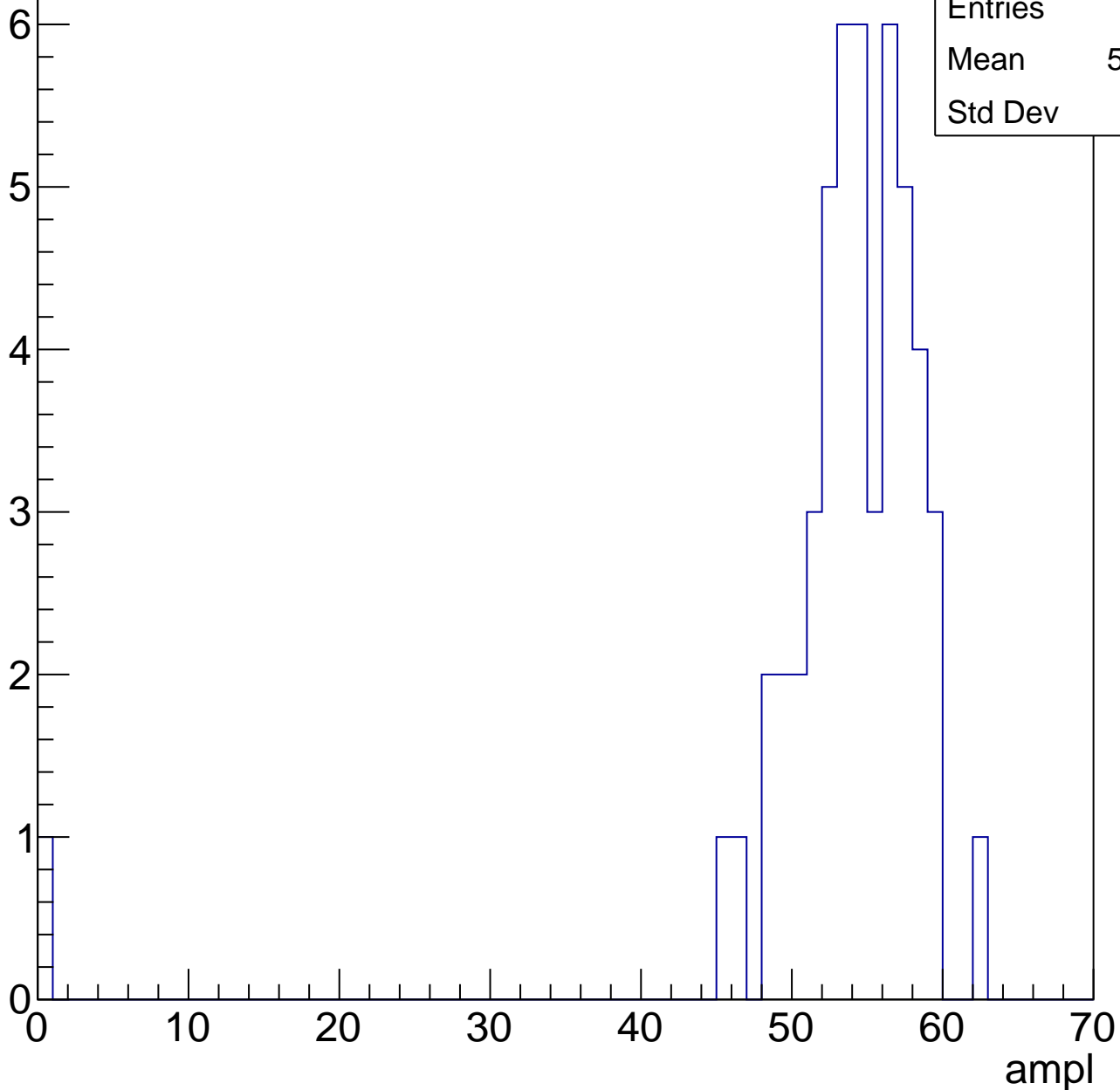


B1L103S, U21-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	52.88
Std Dev	8.26

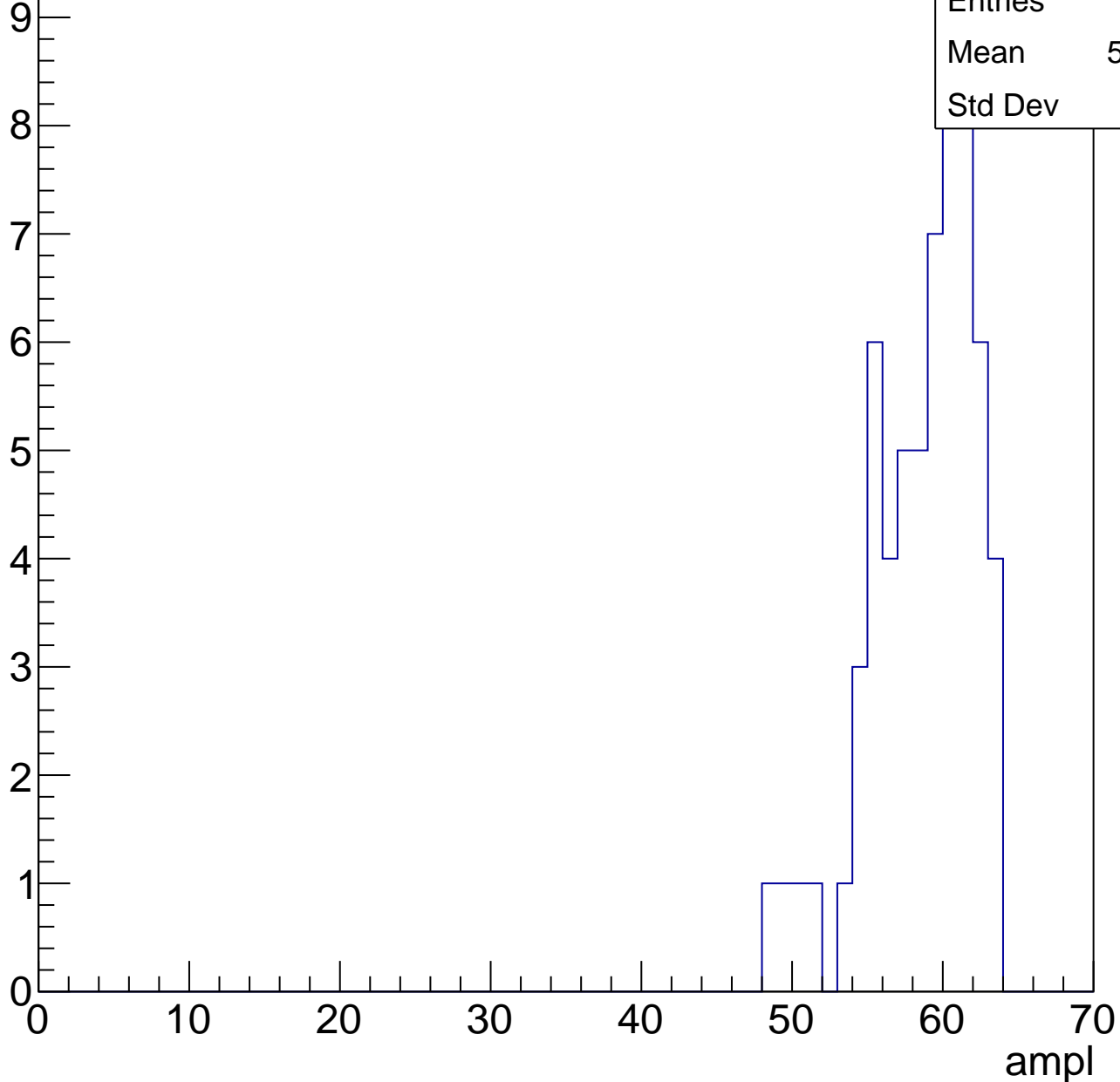


B1L103S, U21-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

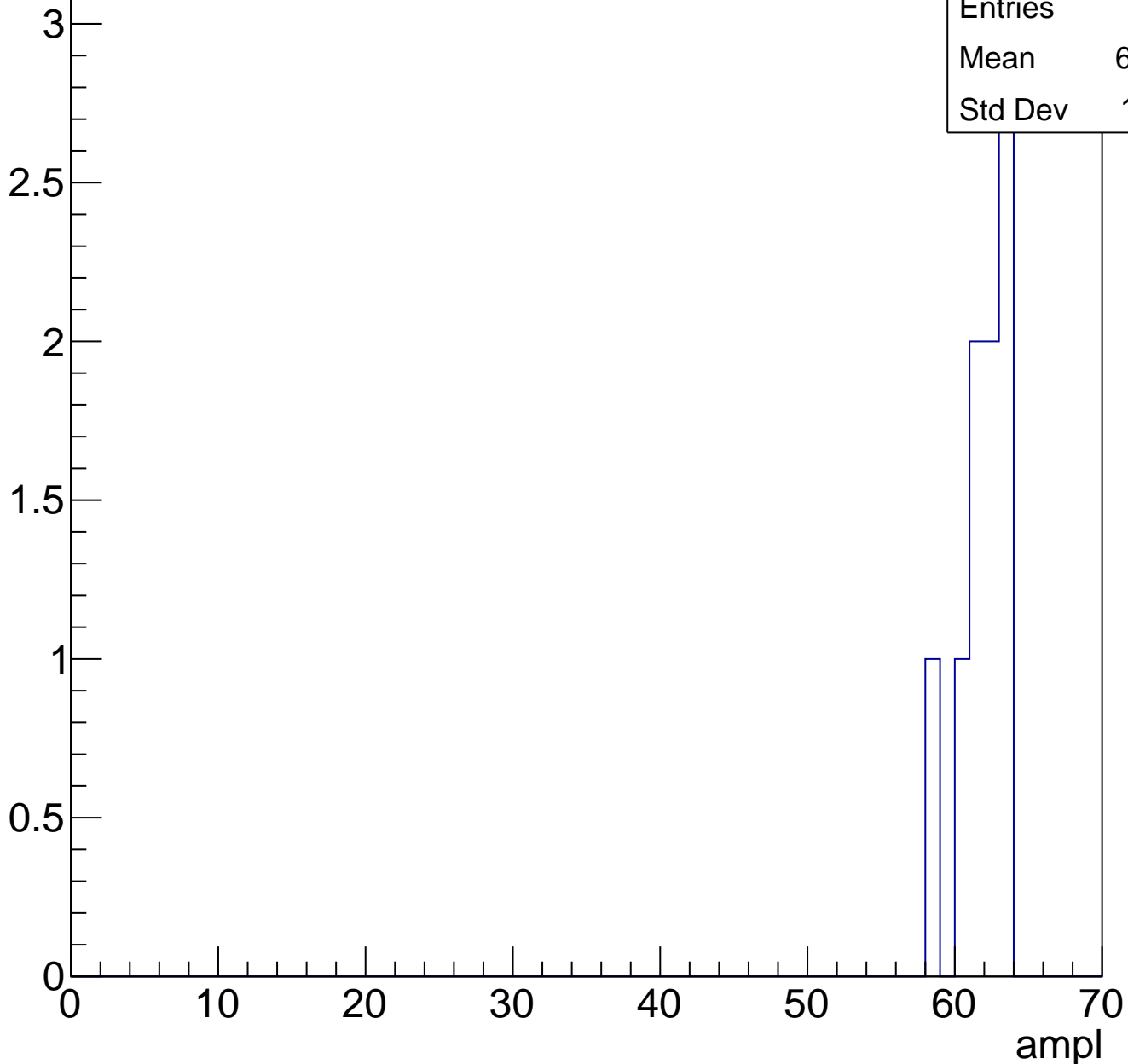
Entries	62
Mean	58.18
Std Dev	3.48



B1L103S, U21-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

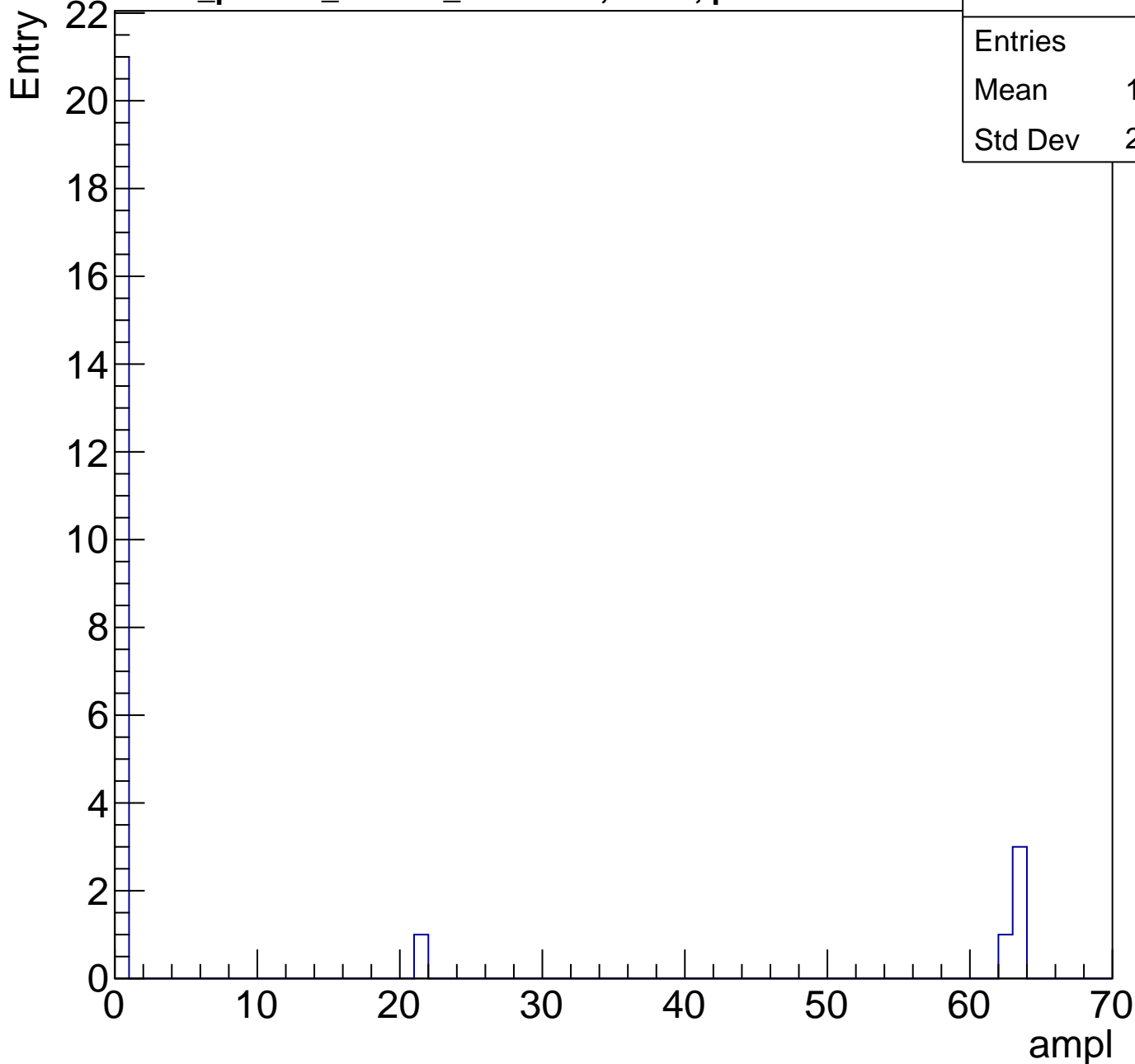
Entry



B1L103S, U21-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	10.46
Std Dev	22.66



B1L103S, U21-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	10.46
Std Dev	22.66

Entry

