



# B1L000S, U1-ch0, adc0

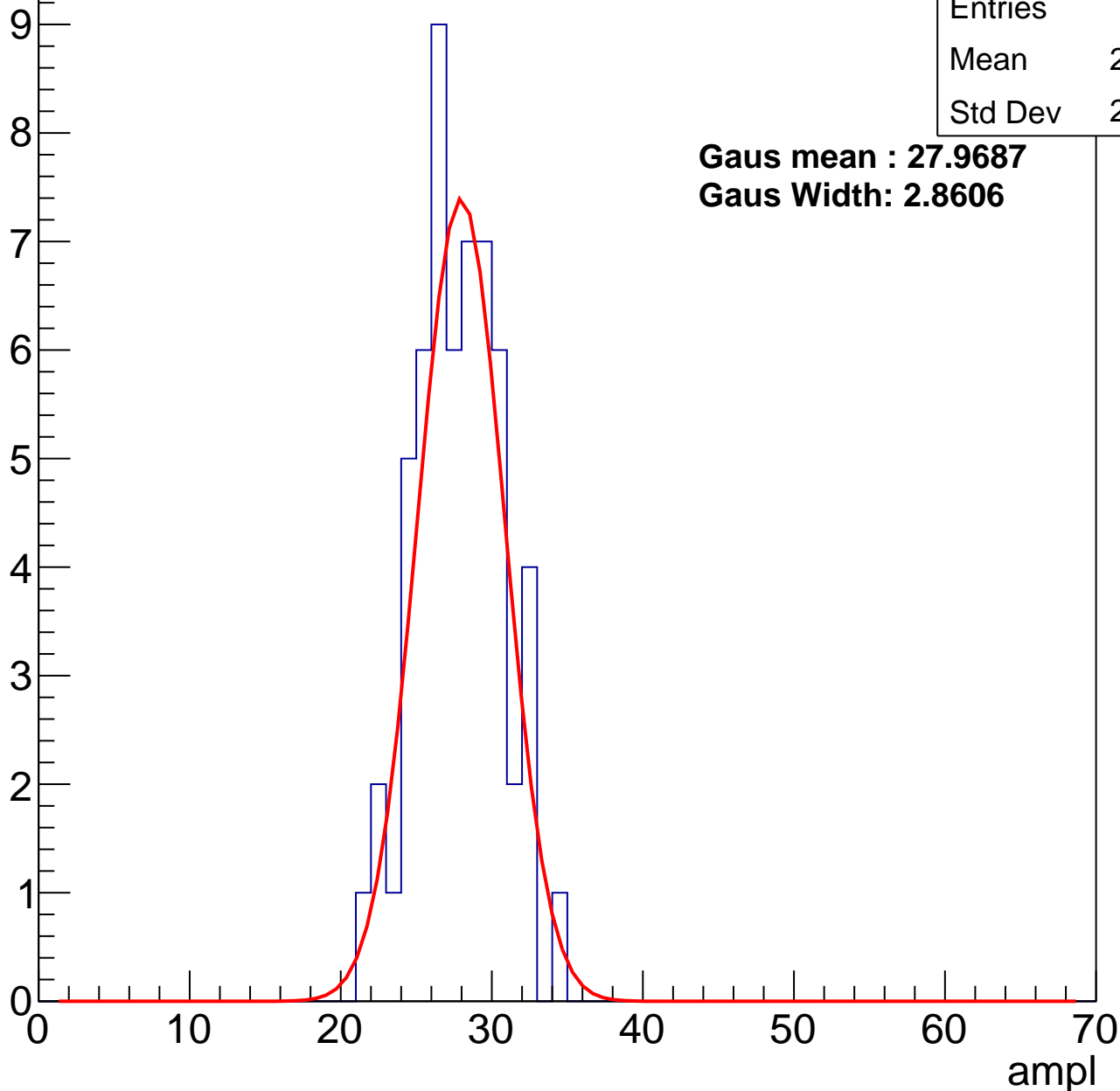
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	27.32
Std Dev	2.792

**Gaus mean : 27.9687**

**Gaus Width: 2.8606**



# B1L000S, U1-ch0, adc1

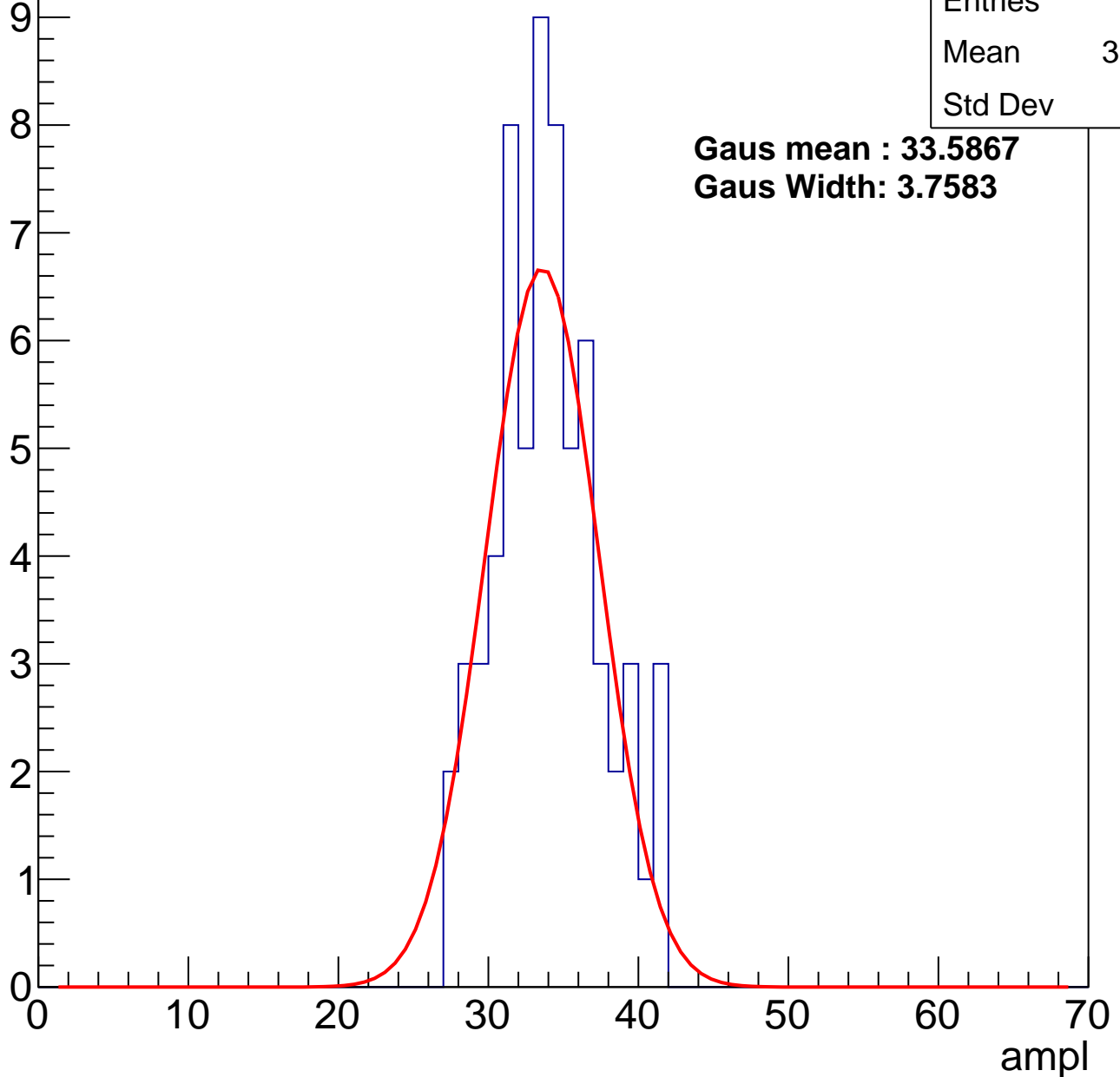
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	33.54
Std Dev	3.46

**Gaus mean : 33.5867**

**Gaus Width: 3.7583**



# B1L000S, U1-ch0, adc2

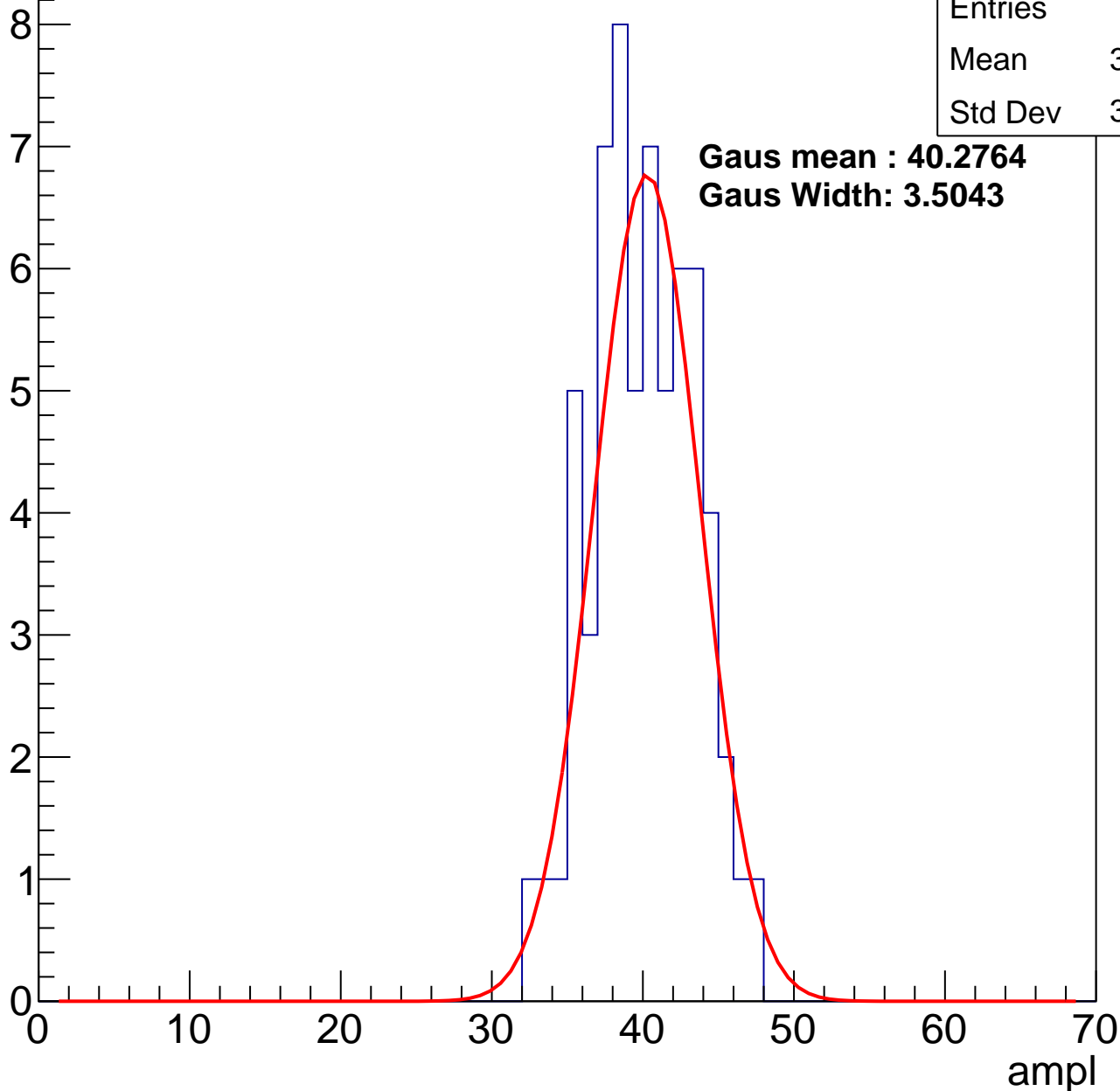
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	39.59
Std Dev	3.317

**Gaus mean : 40.2764**

**Gaus Width: 3.5043**

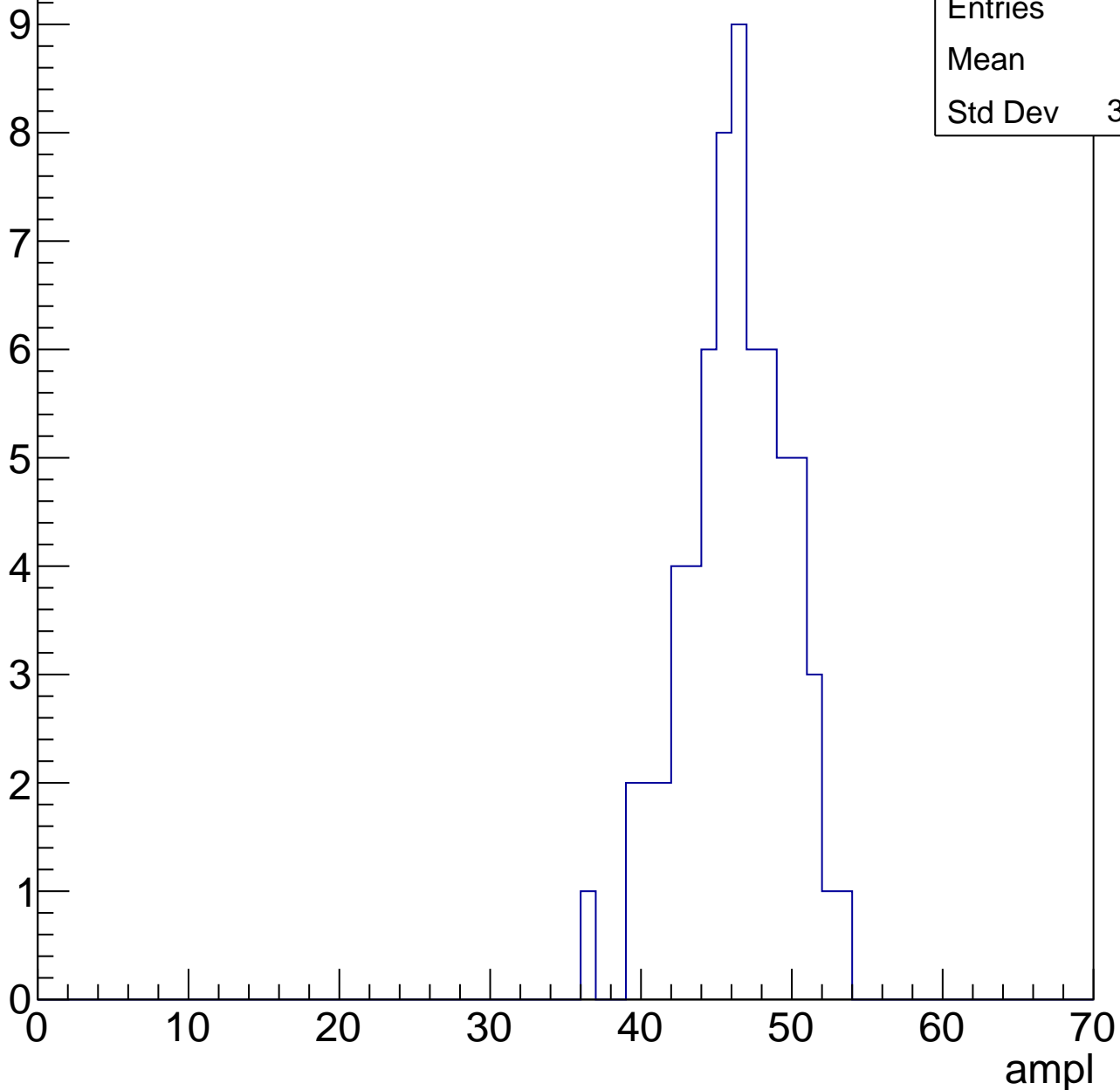


# B1L000S, U1-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

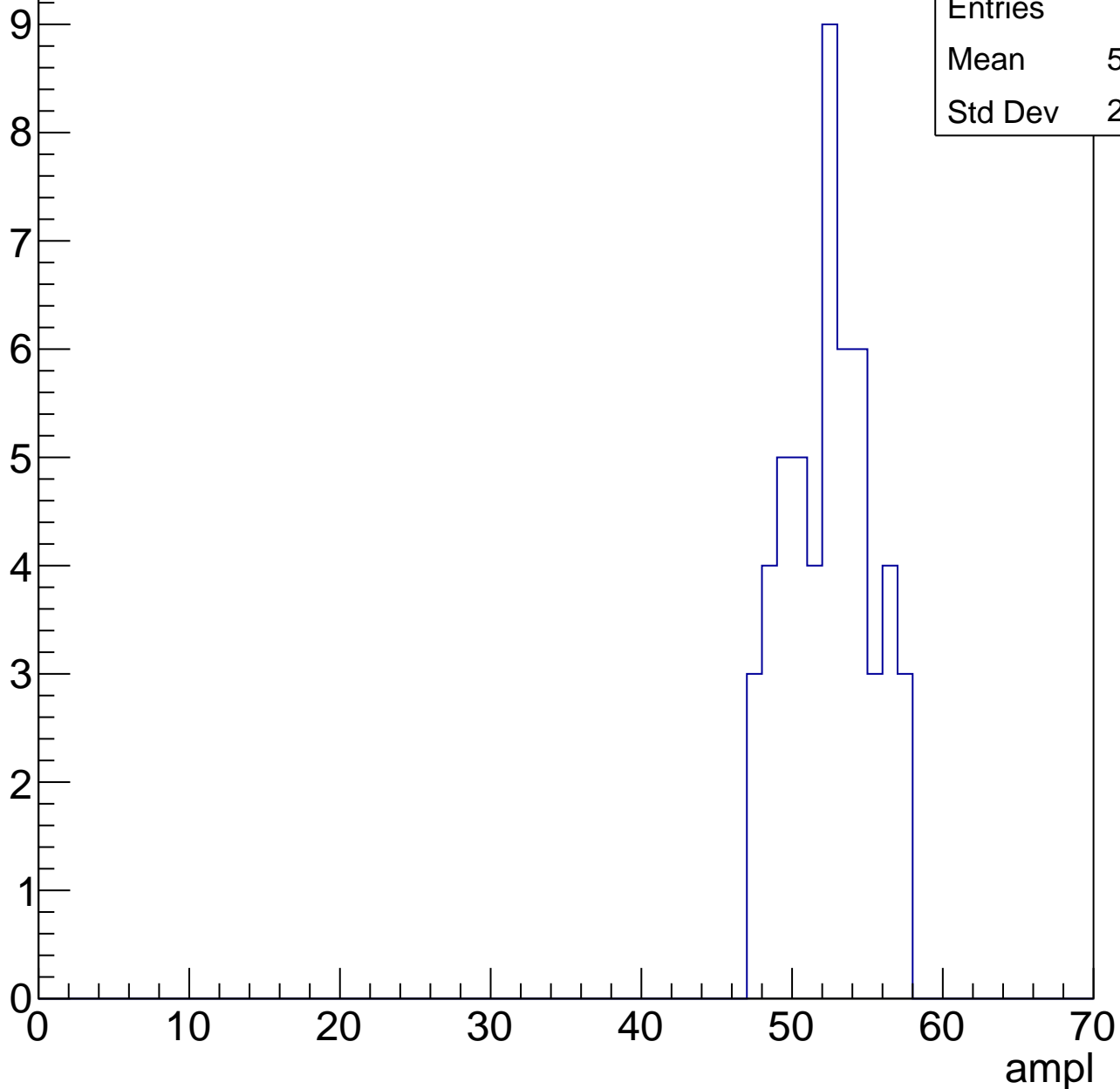
Entries	65
Mean	45.8
Std Dev	3.429



# B1L000S, U1-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



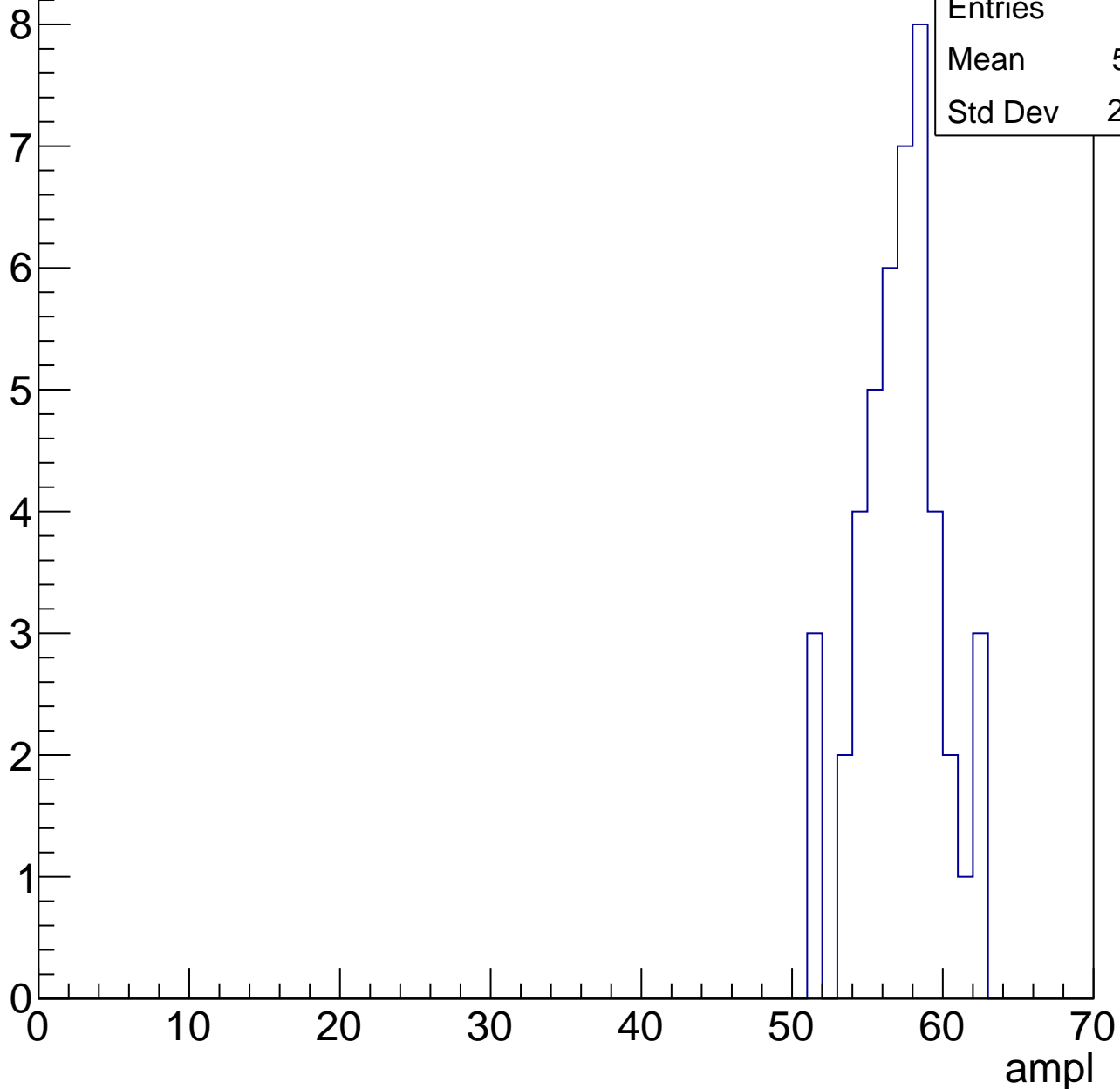
Entries	52
Mean	51.96
Std Dev	2.787

# B1L000S, U1-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	56.71
Std Dev	2.713



# B1L000S, U1-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	44
Mean	59.2
Std Dev	9.307

Entry

10

8

6

4

2

0

0

10

20

30

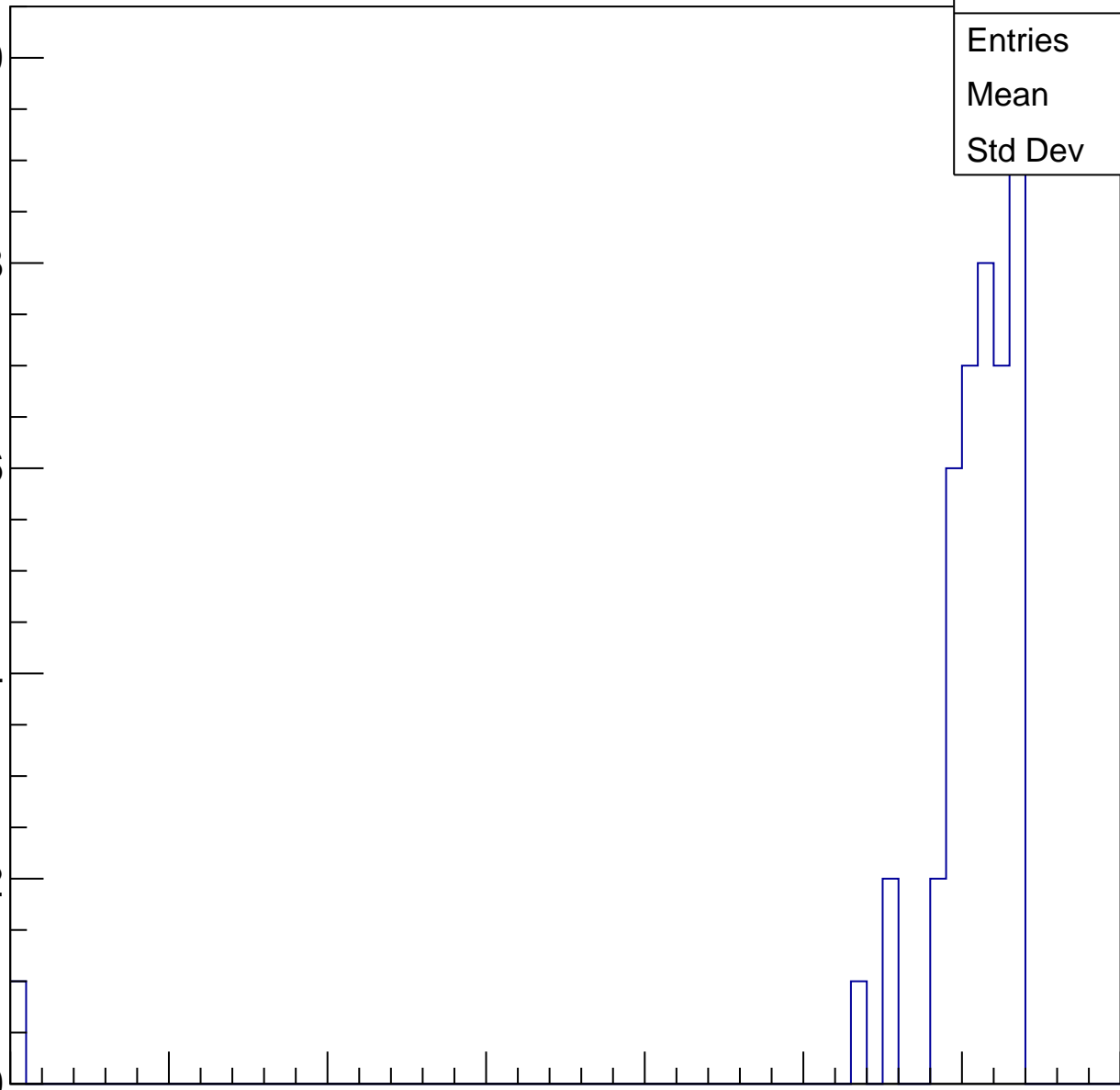
40

50

60

ampl

70

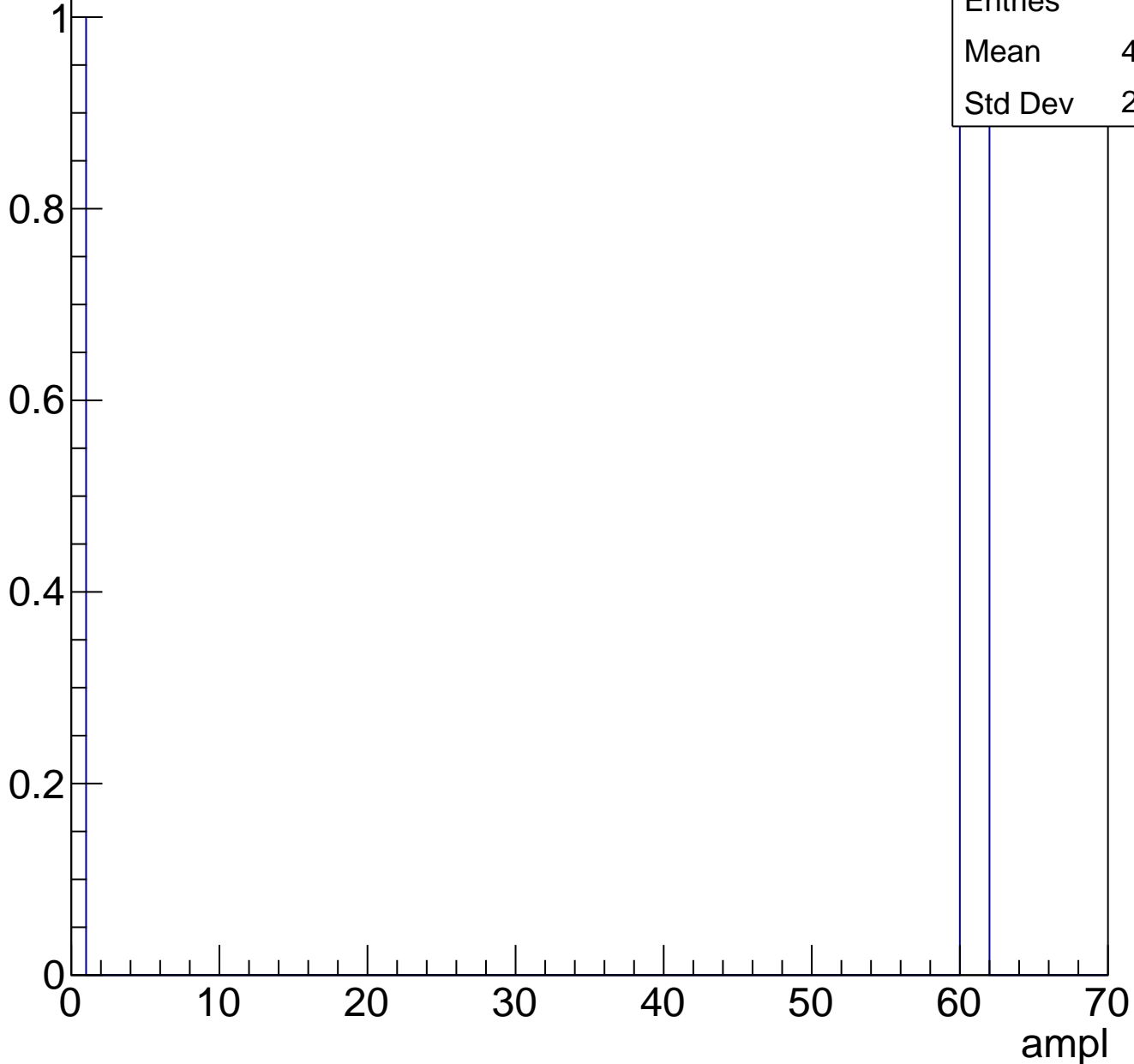




# B1L000S, U1-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch1, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	26.49
Std Dev	5.447

**Gaus mean : 28.0294**

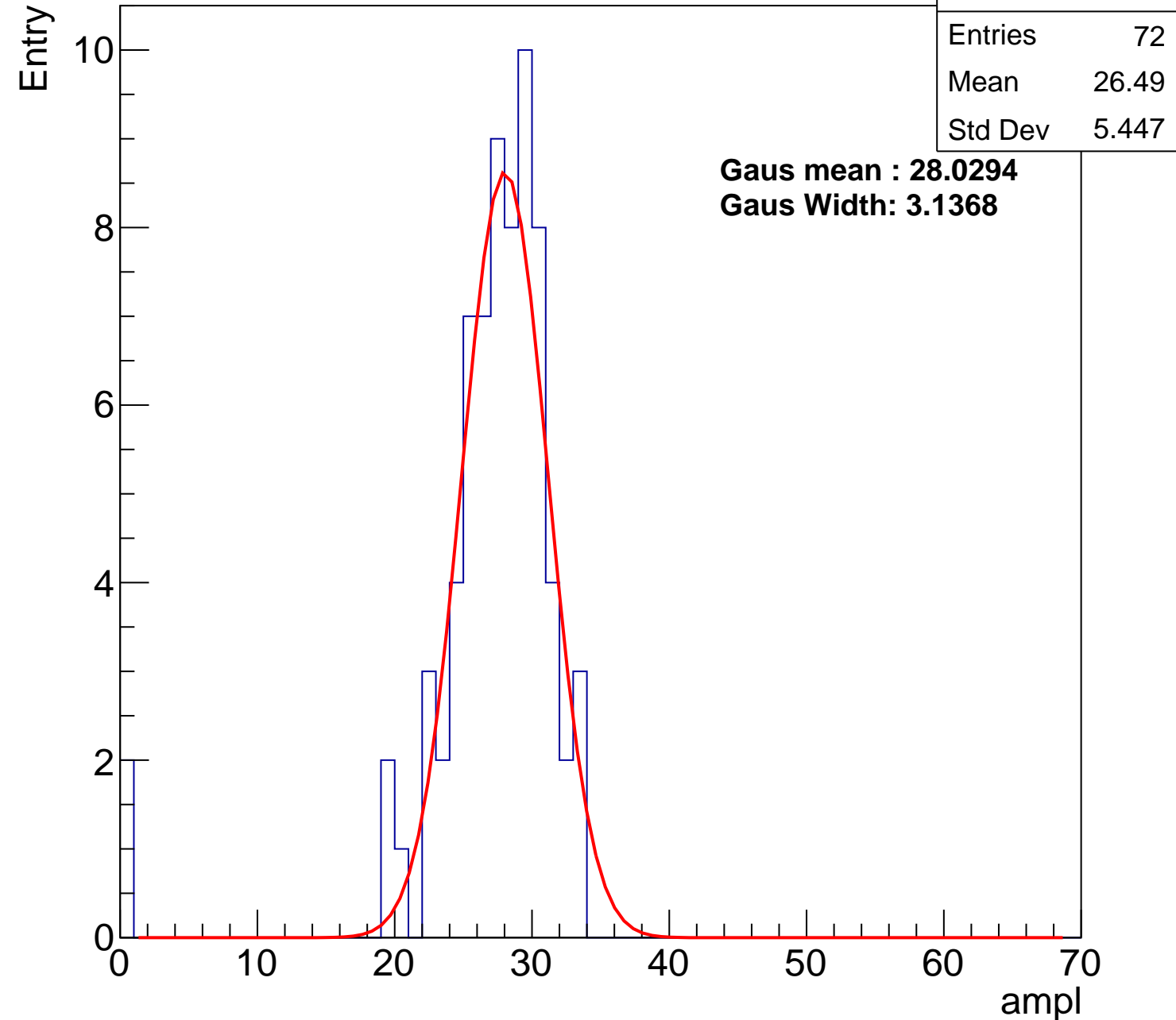
**Gaus Width: 3.1368**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch1, adc1

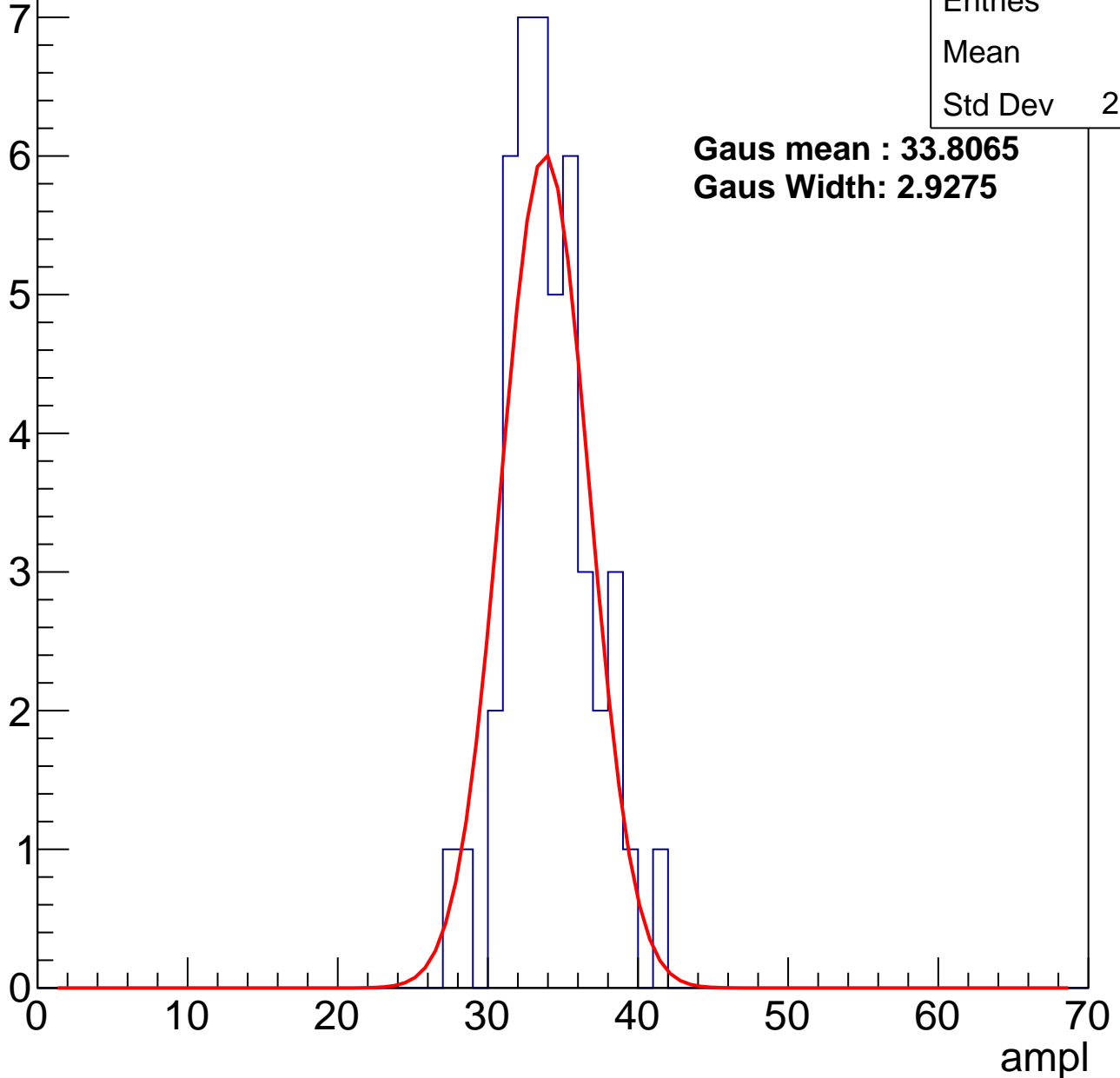
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	33.6
Std Dev	2.816

**Gaus mean : 33.8065**

**Gaus Width: 2.9275**



# B1L000S, U1-ch1, adc2

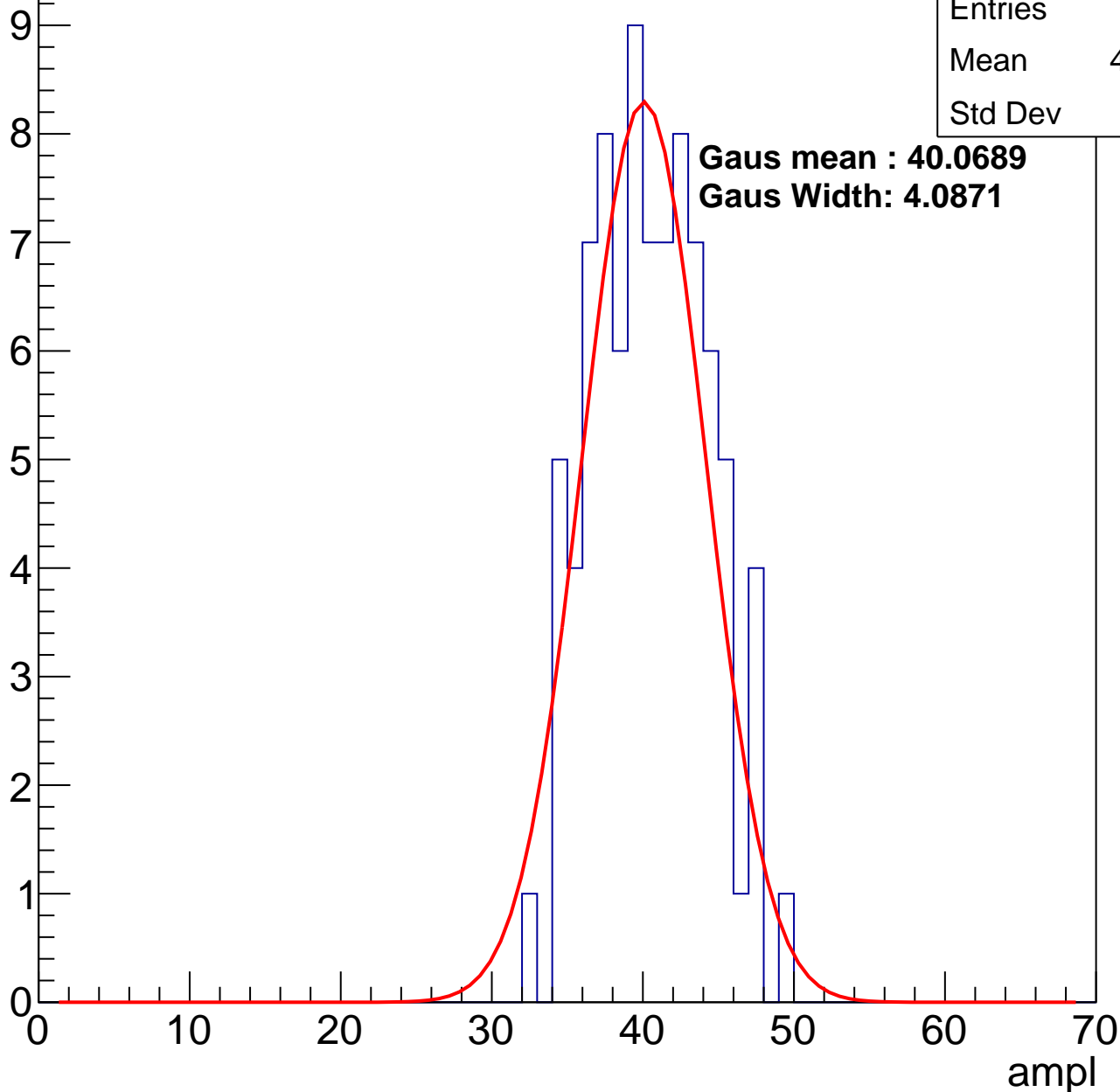
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	86
Mean	40.06
Std Dev	3.73

**Gaus mean : 40.0689**

**Gaus Width: 4.0871**

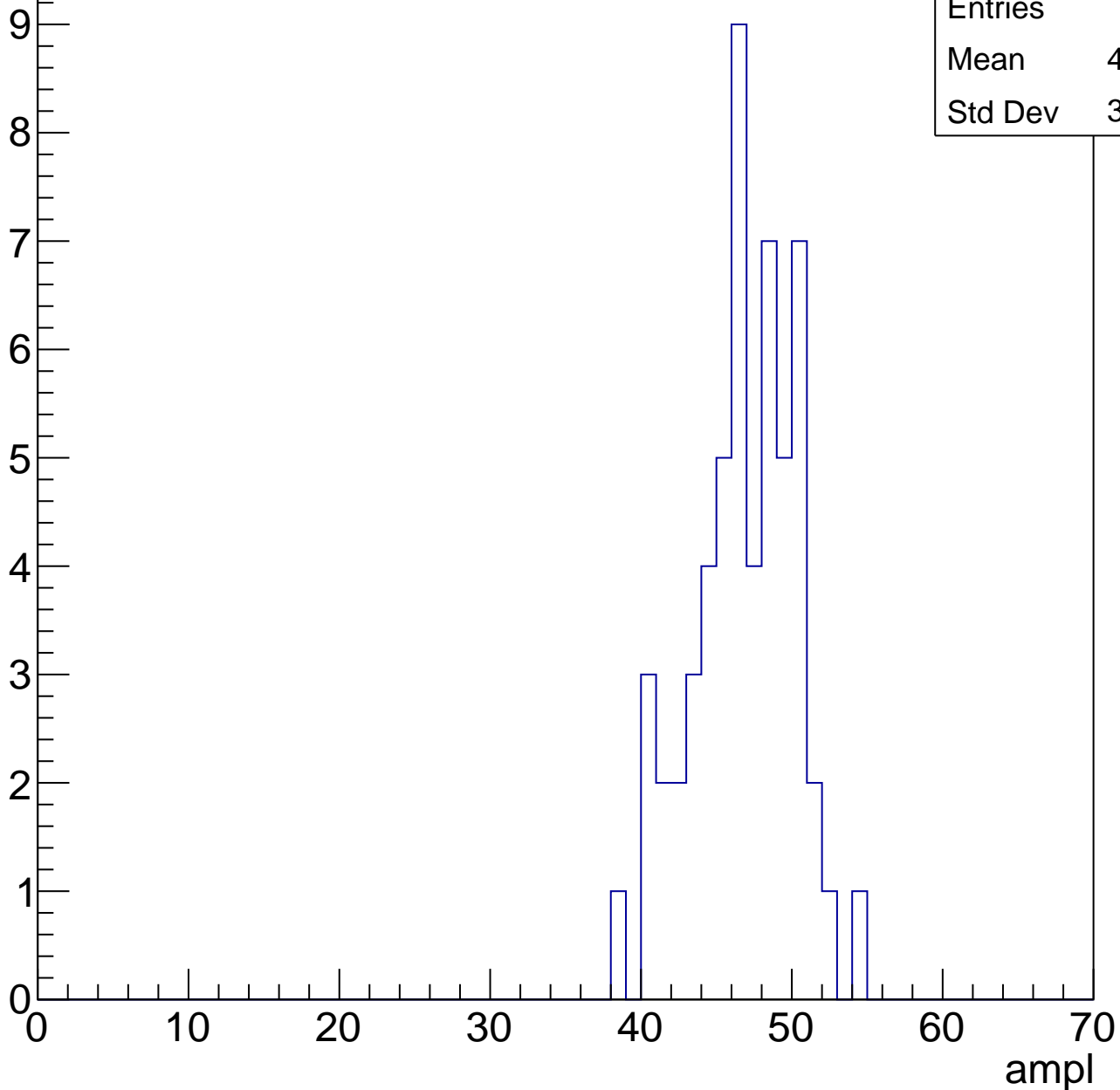


# B1L000S, U1-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

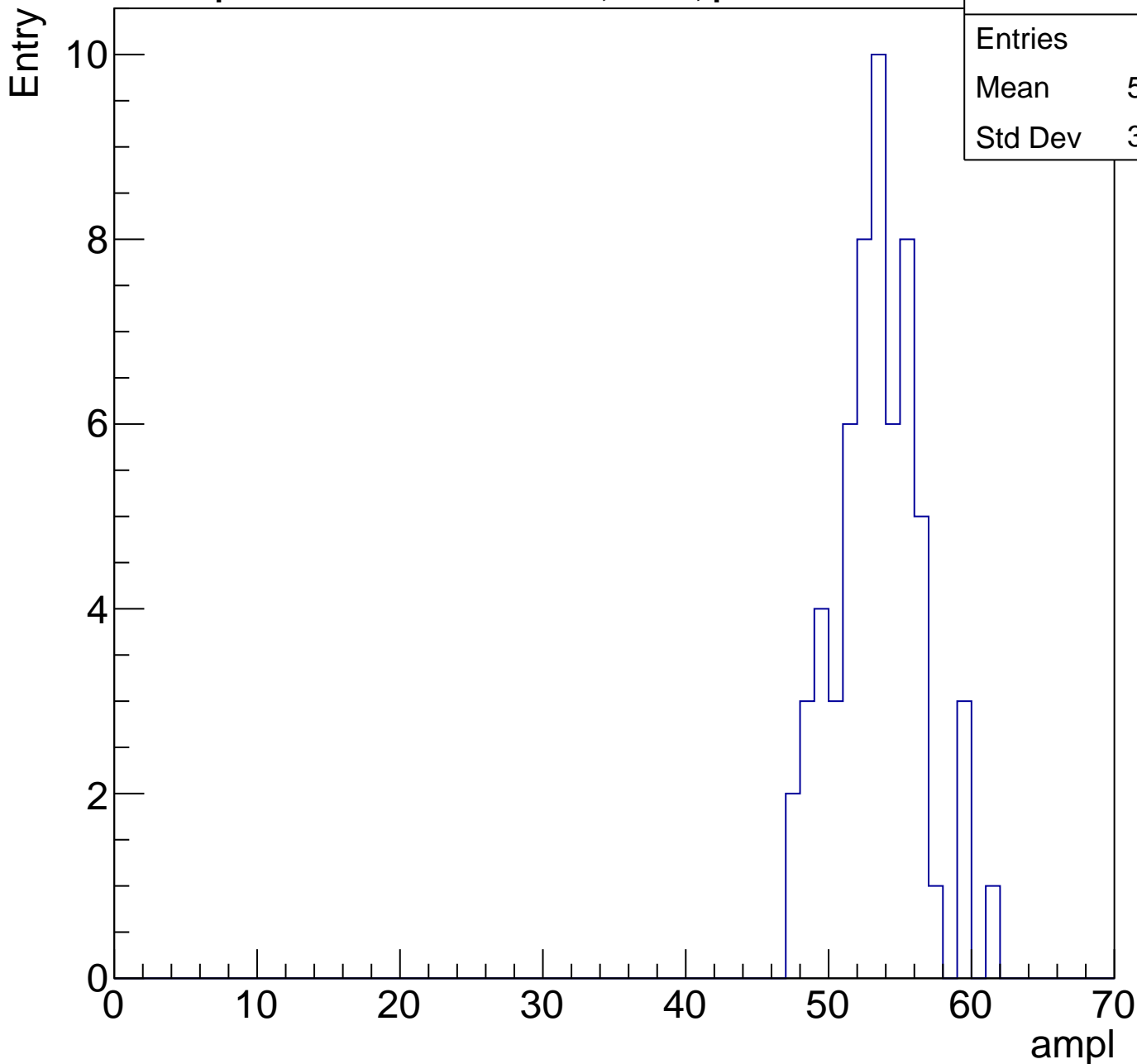
Entries	56
Mean	46.34
Std Dev	3.366



# B1L000S, U1-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	52.92
Std Dev	3.002

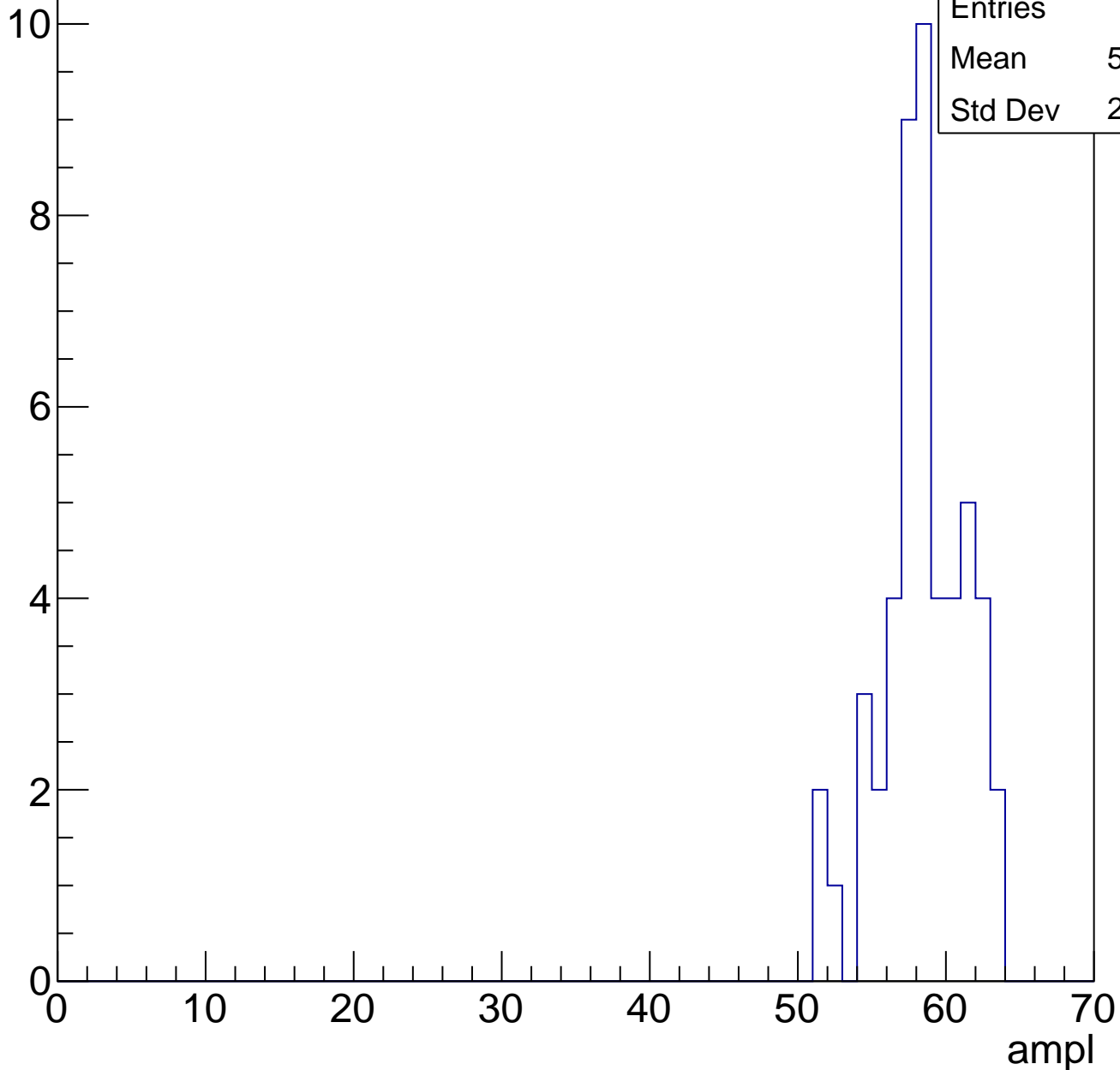


# B1L000S, U1-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	50
Mean	57.96
Std Dev	2.842

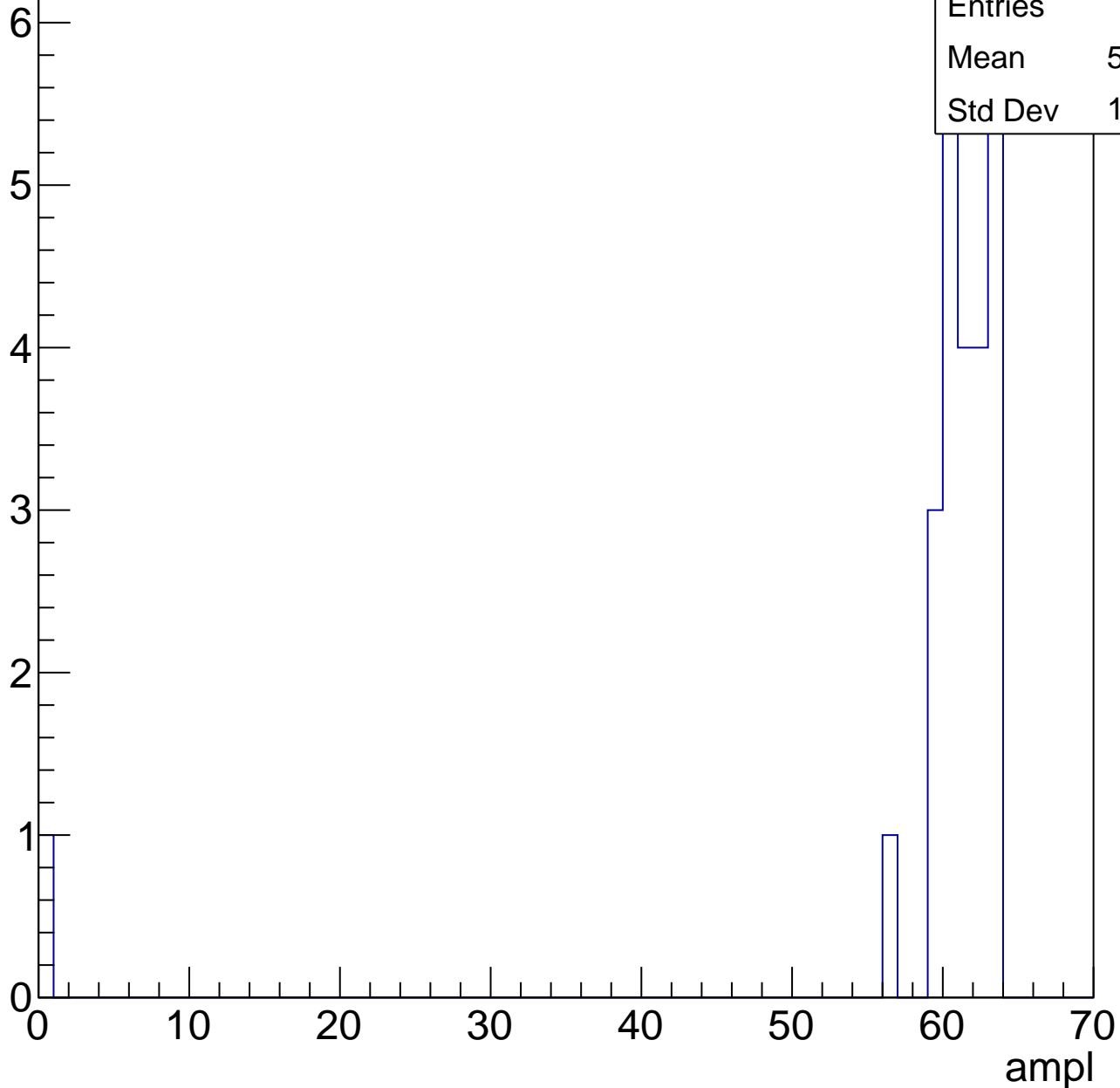
Entry



# B1L000S, U1-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

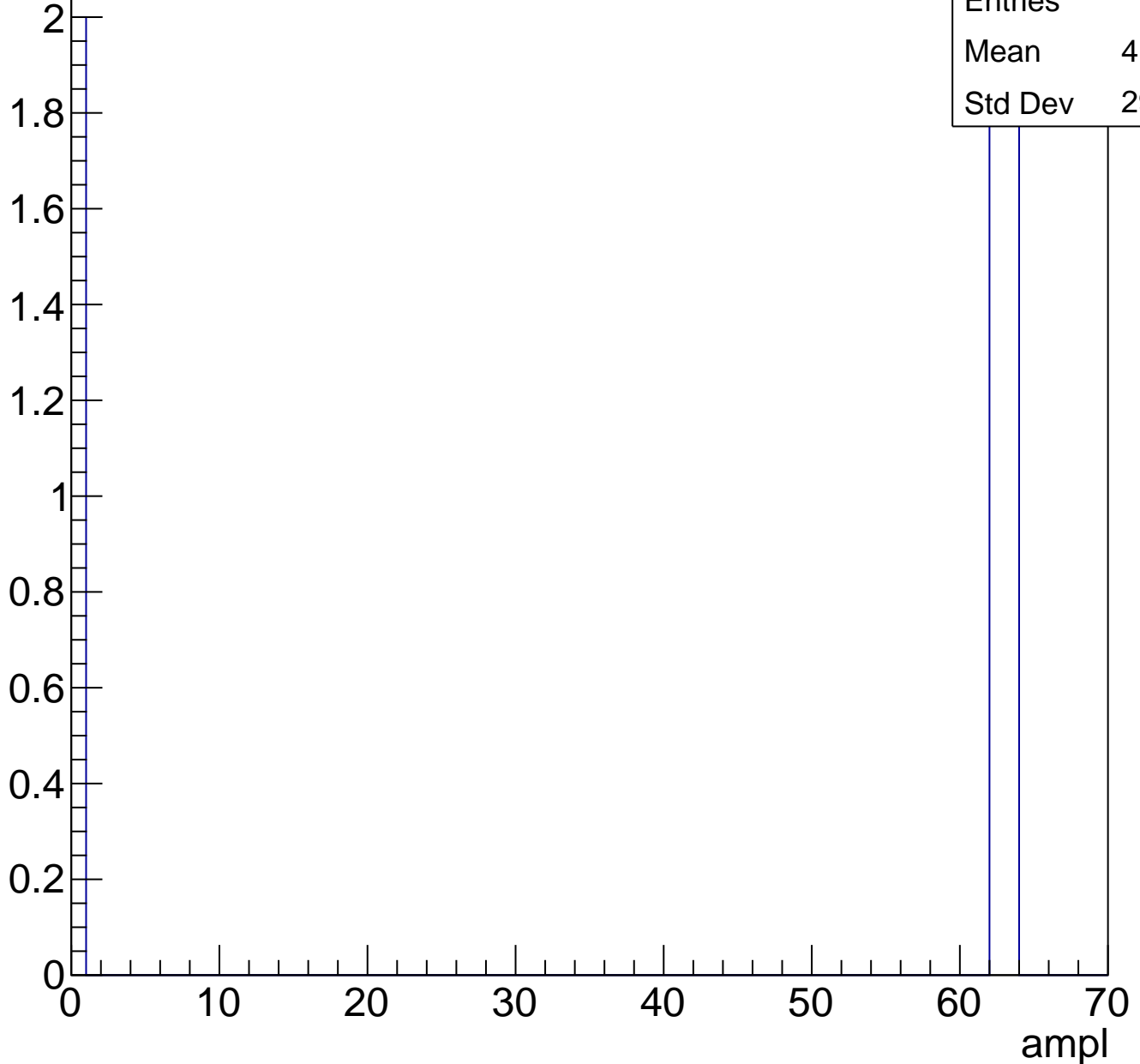




# B1L000S, U1-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch2, adc0

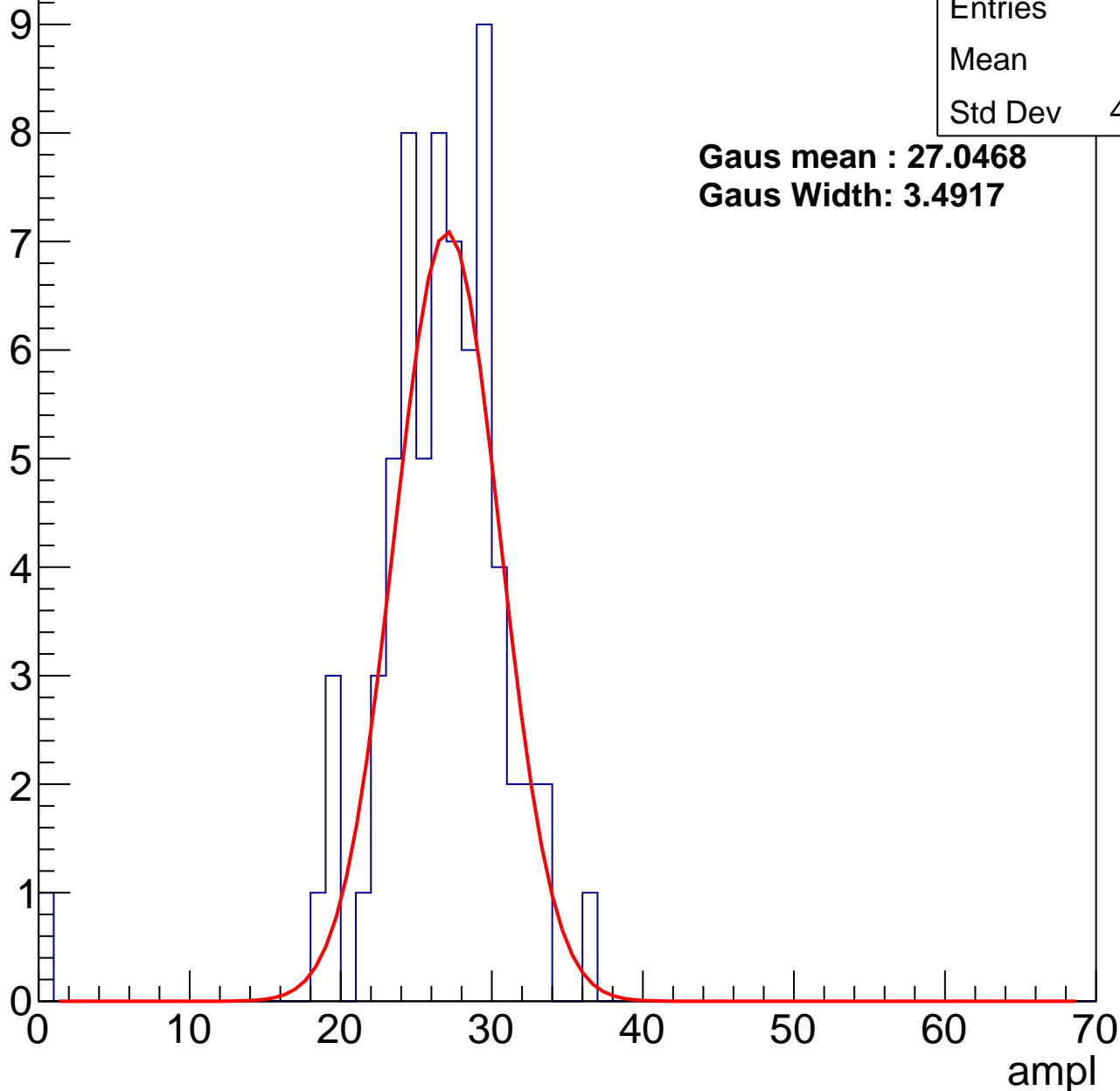
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	26
Std Dev	4.756

**Gaus mean : 27.0468**

**Gaus Width: 3.4917**



# B1L000S, U1-ch2, adc1

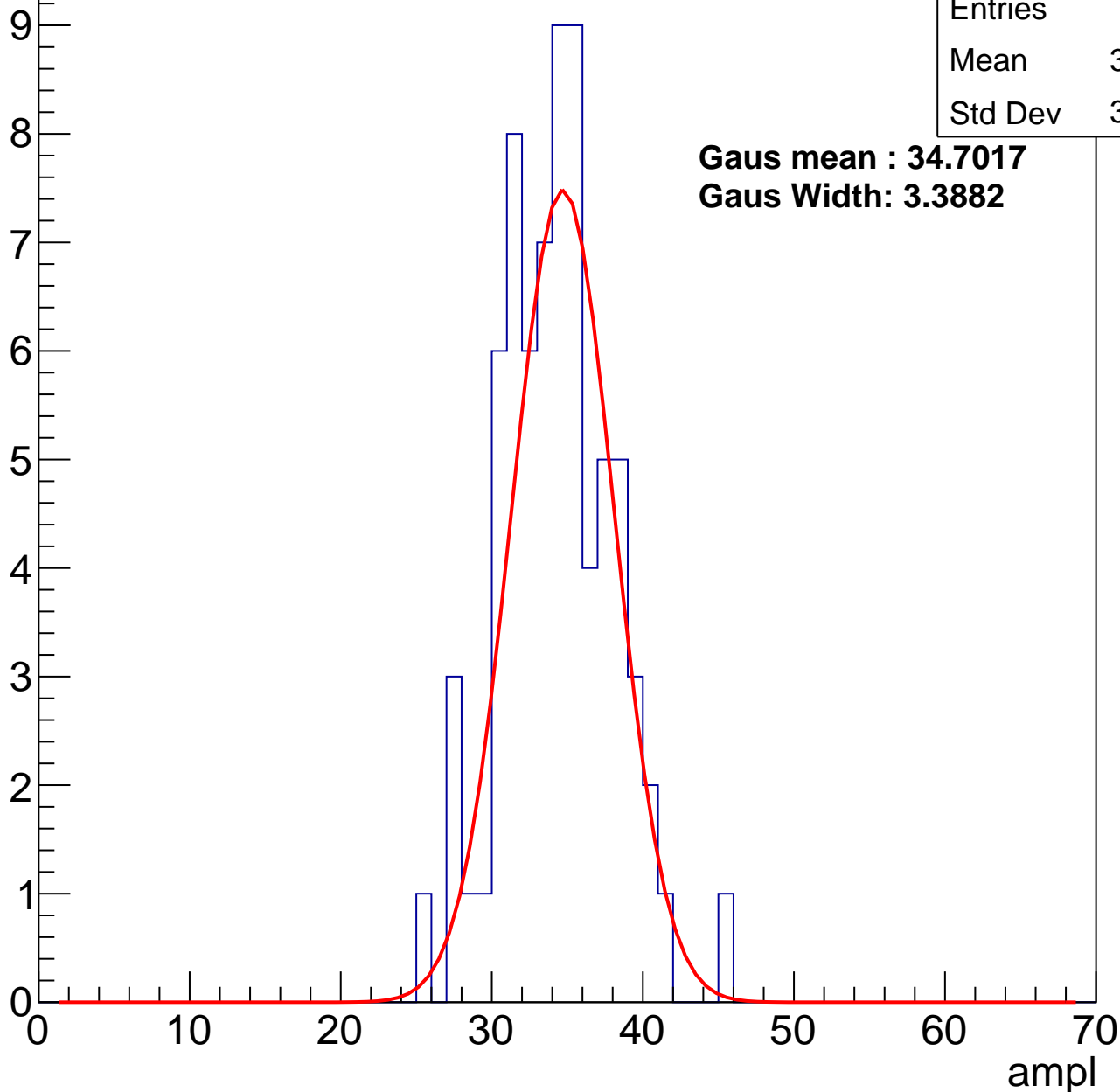
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	33.85
Std Dev	3.639

**Gaus mean : 34.7017**

**Gaus Width: 3.3882**



# B1L000S, U1-ch2, adc2

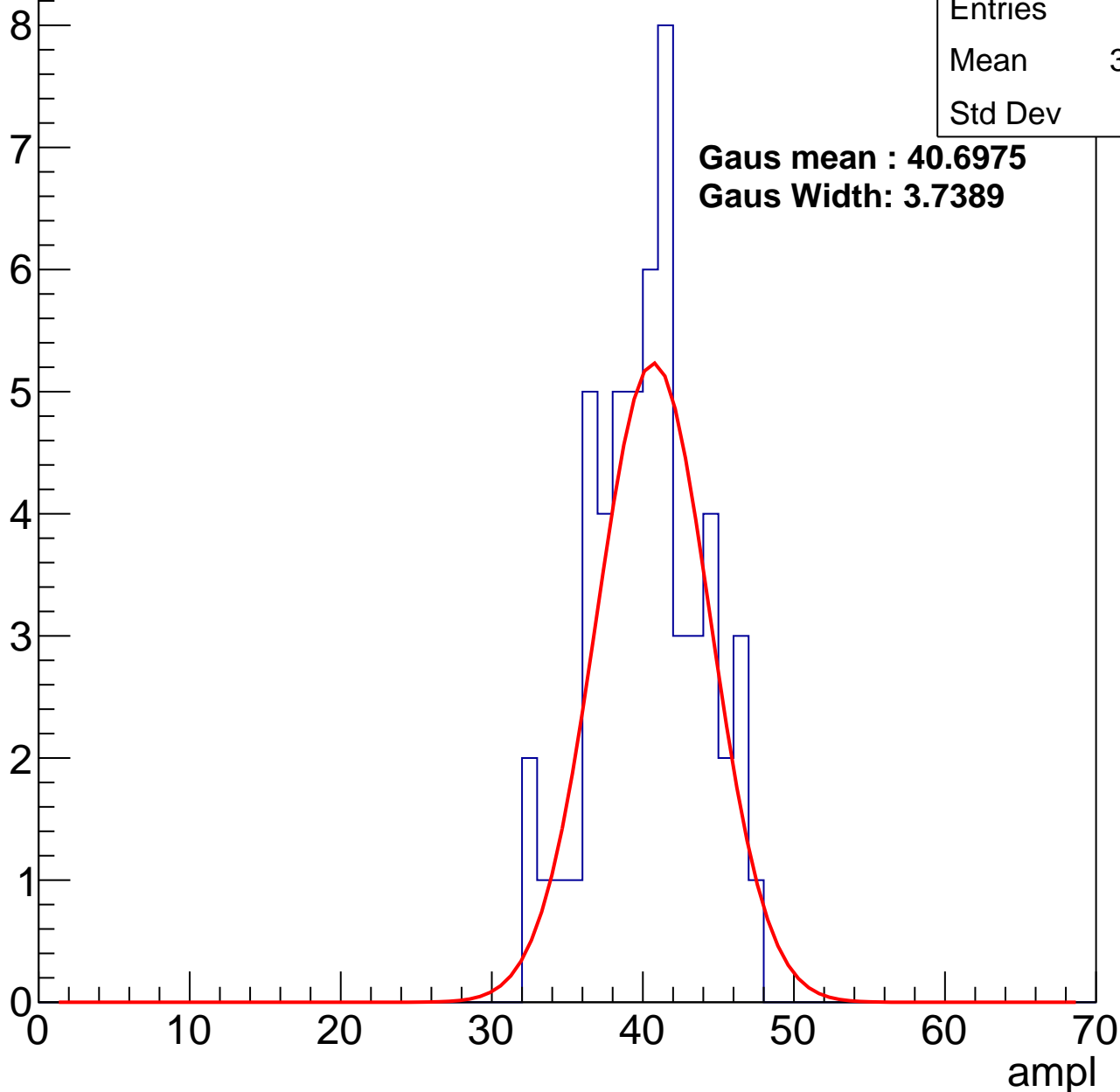
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	39.87
Std Dev	3.59

**Gaus mean : 40.6975**

**Gaus Width: 3.7389**

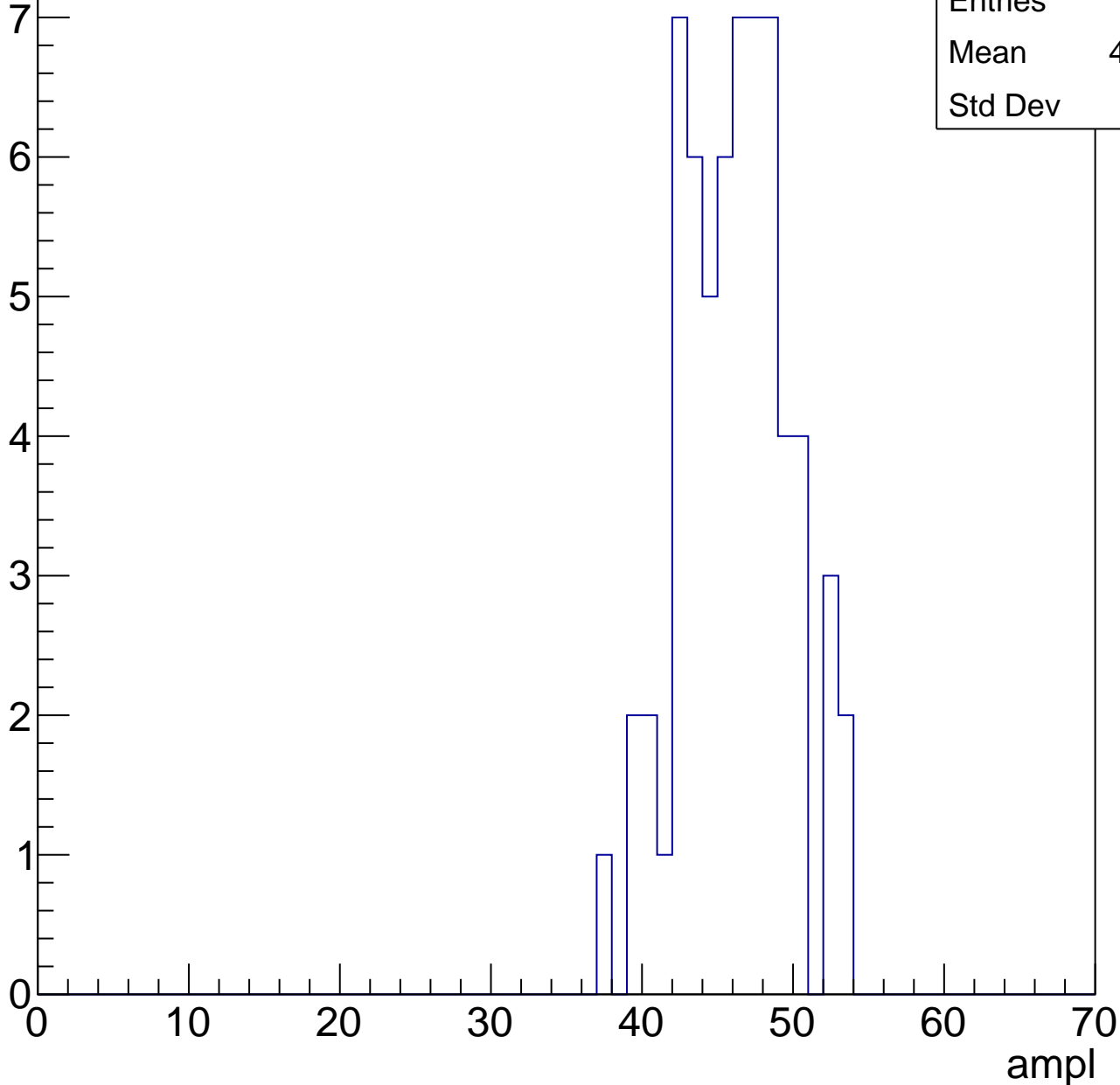


# B1L000S, U1-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	45.67
Std Dev	3.54

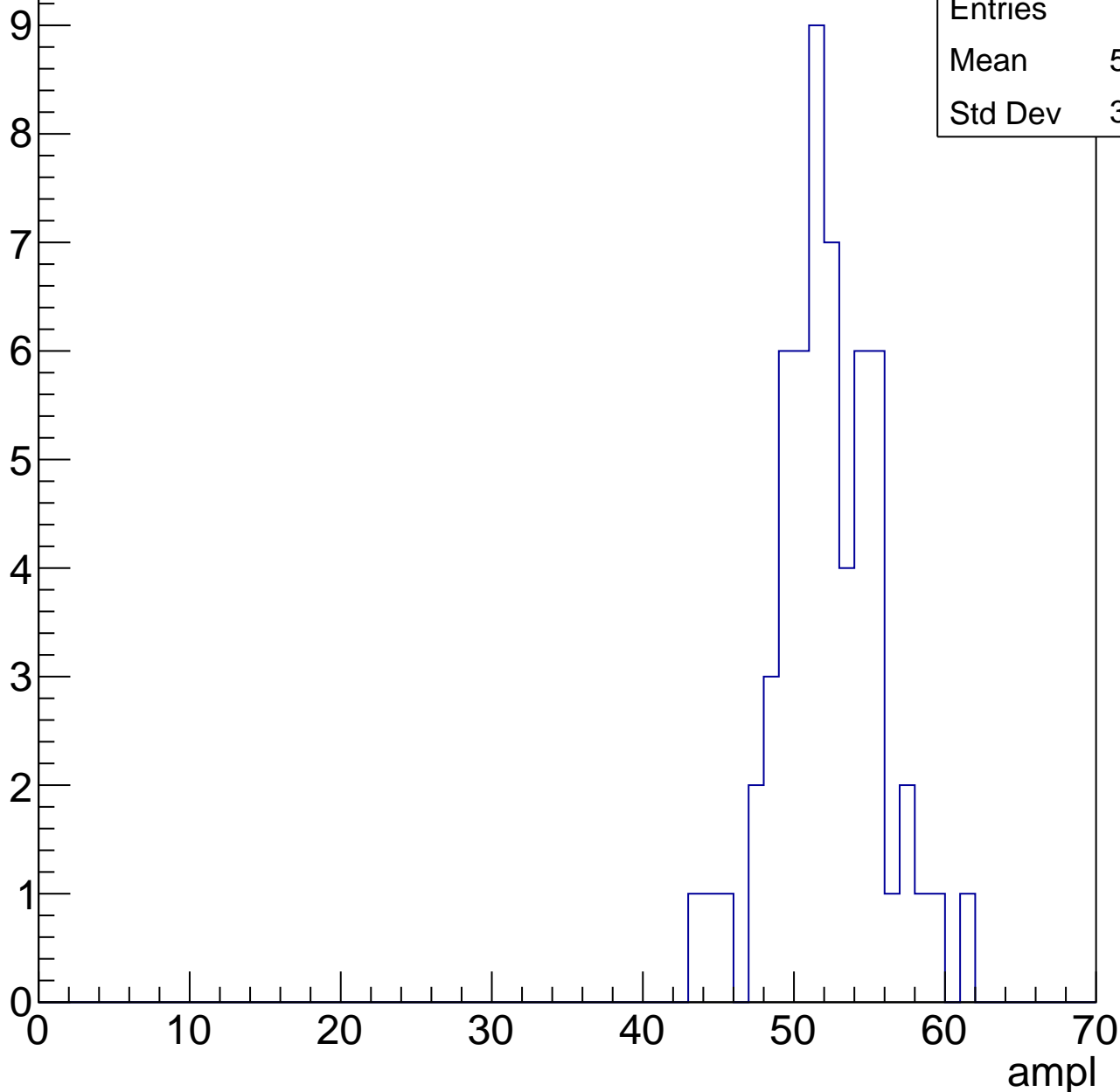


# B1L000S, U1-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	51.74
Std Dev	3.447

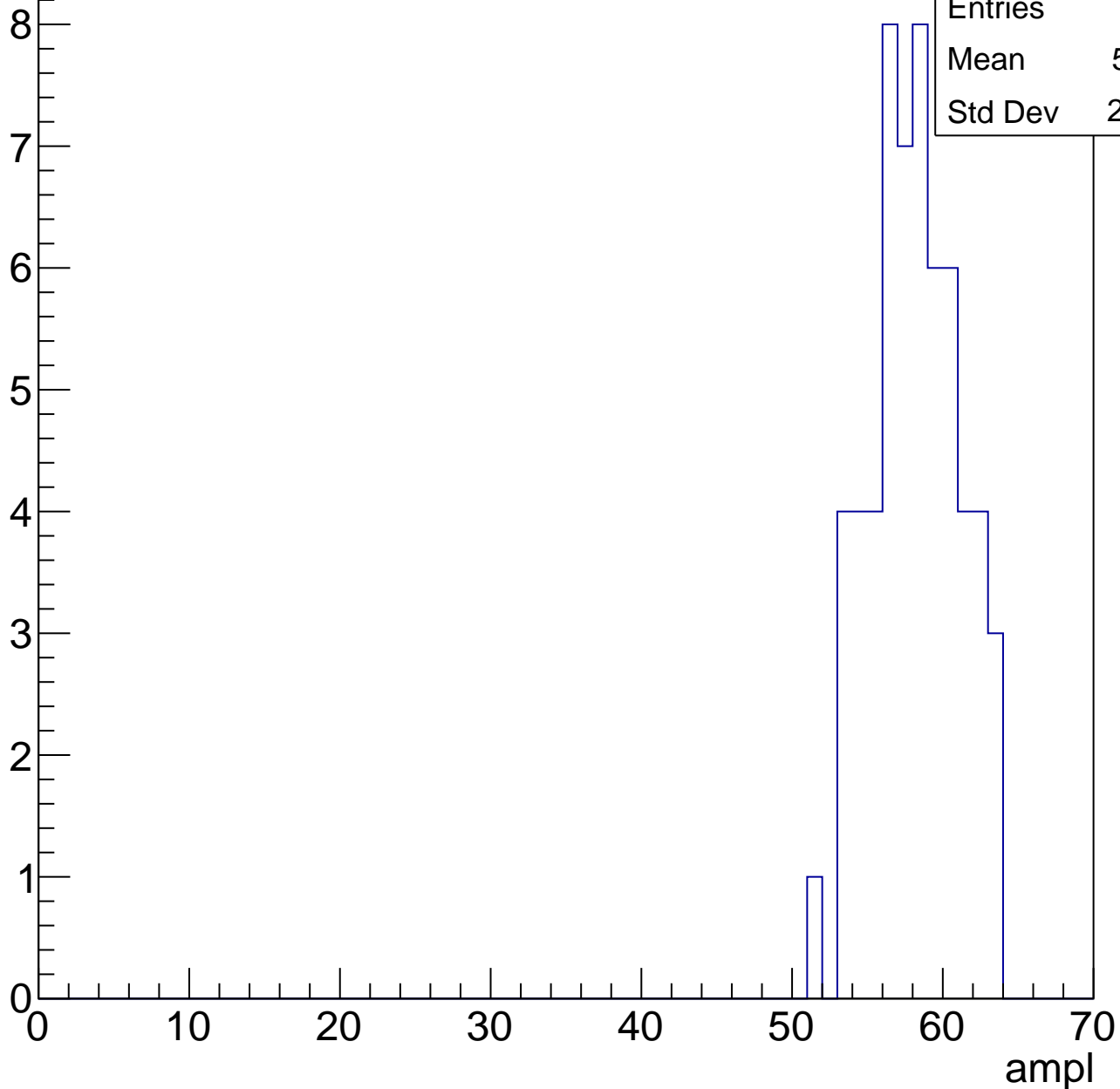


# B1L000S, U1-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.71
Std Dev	2.876

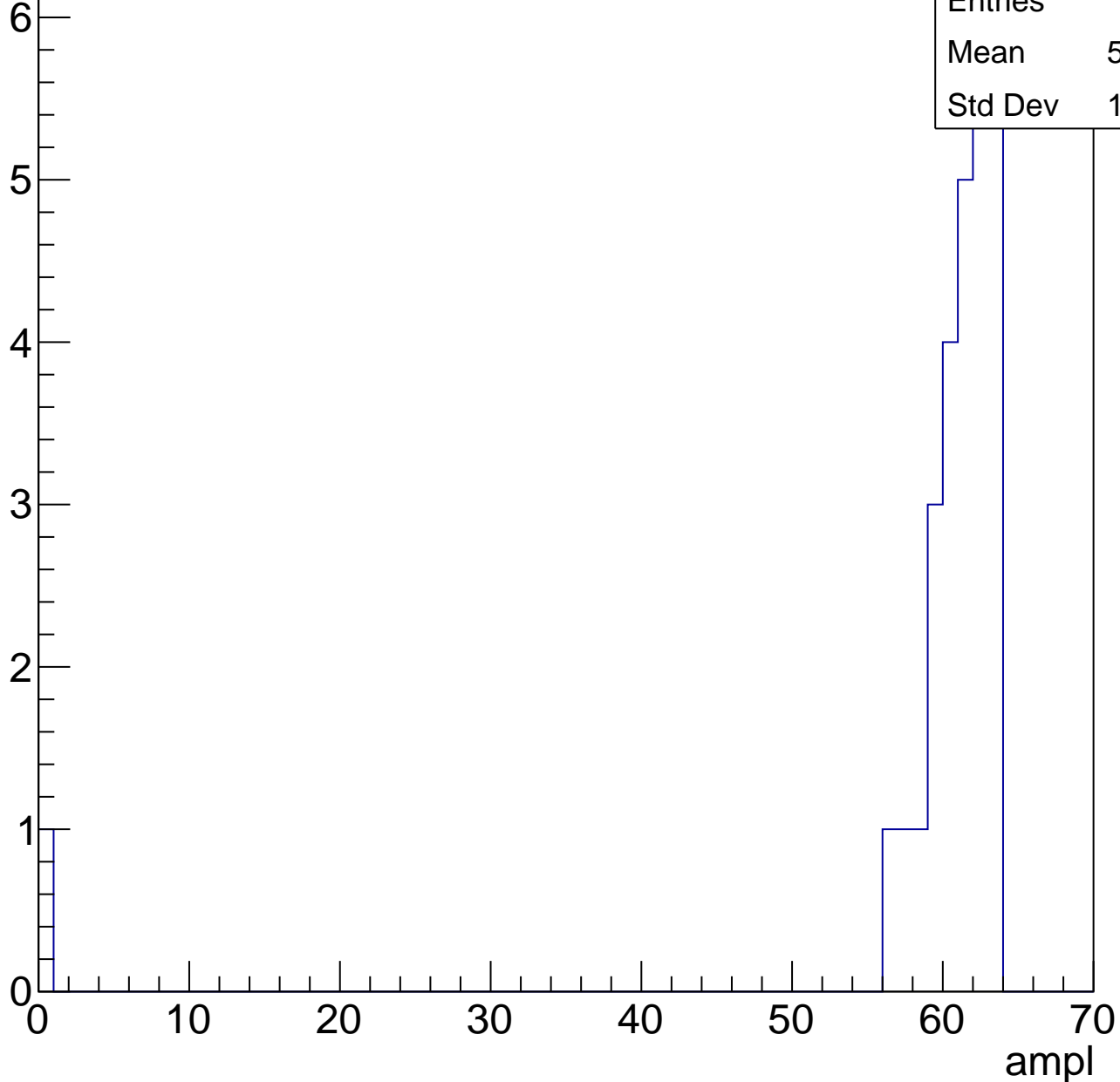


# B1L000S, U1-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.68
Std Dev	11.44

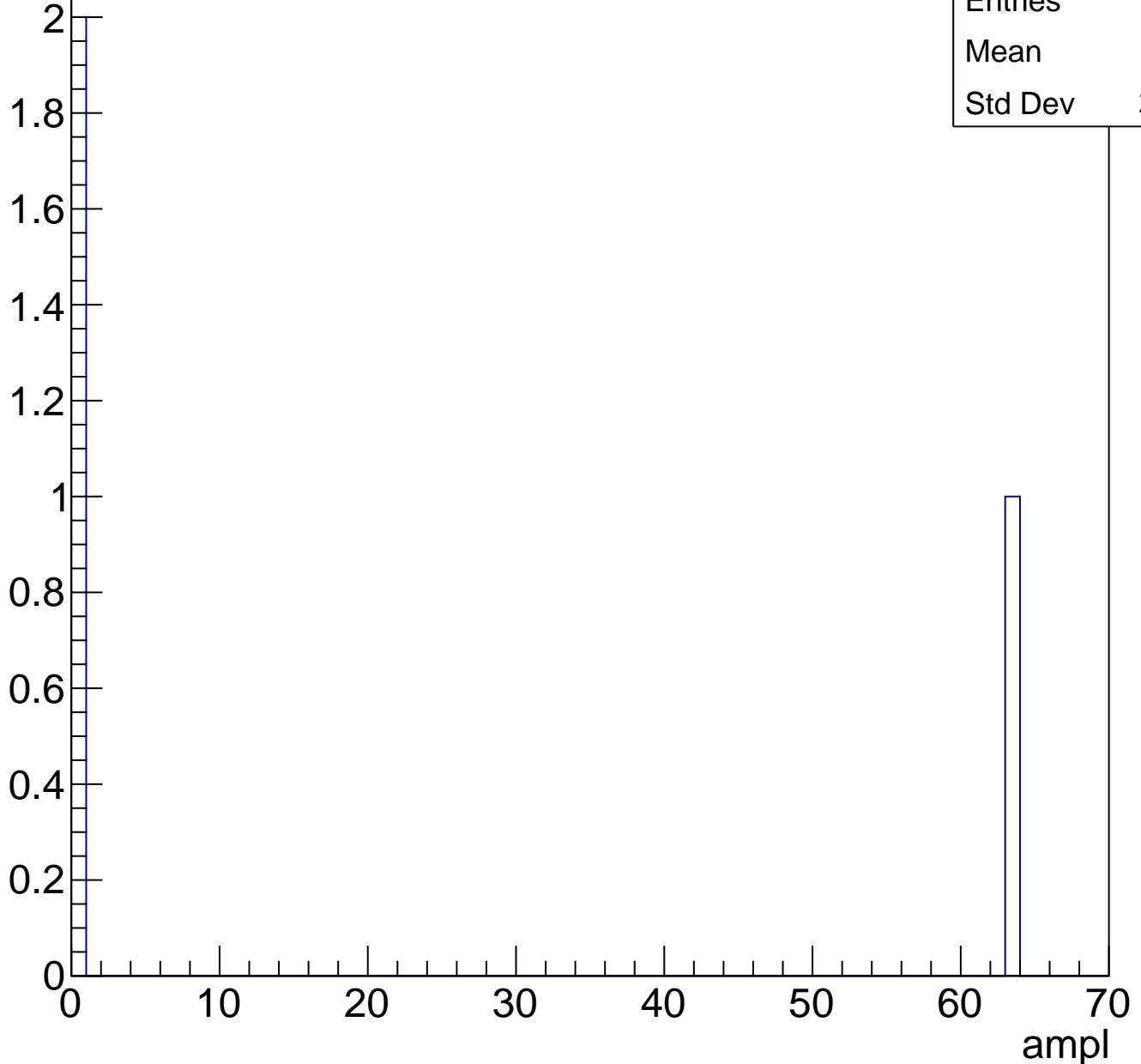




# B1L000S, U1-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U1-ch3, adc0

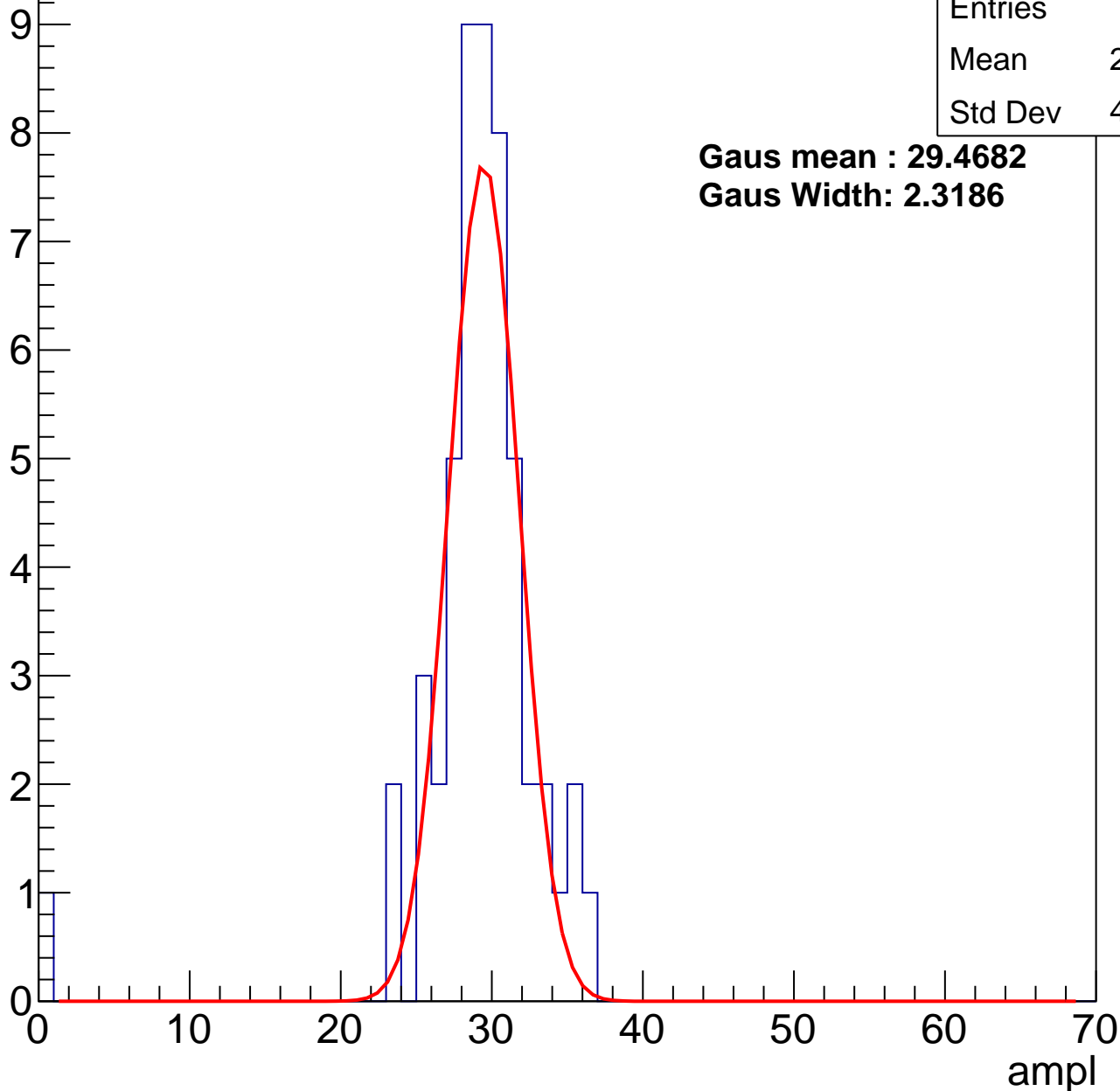
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	28.58
Std Dev	4.849

**Gaus mean : 29.4682**

**Gaus Width: 2.3186**



# B1L000S, U1-ch3, adc1

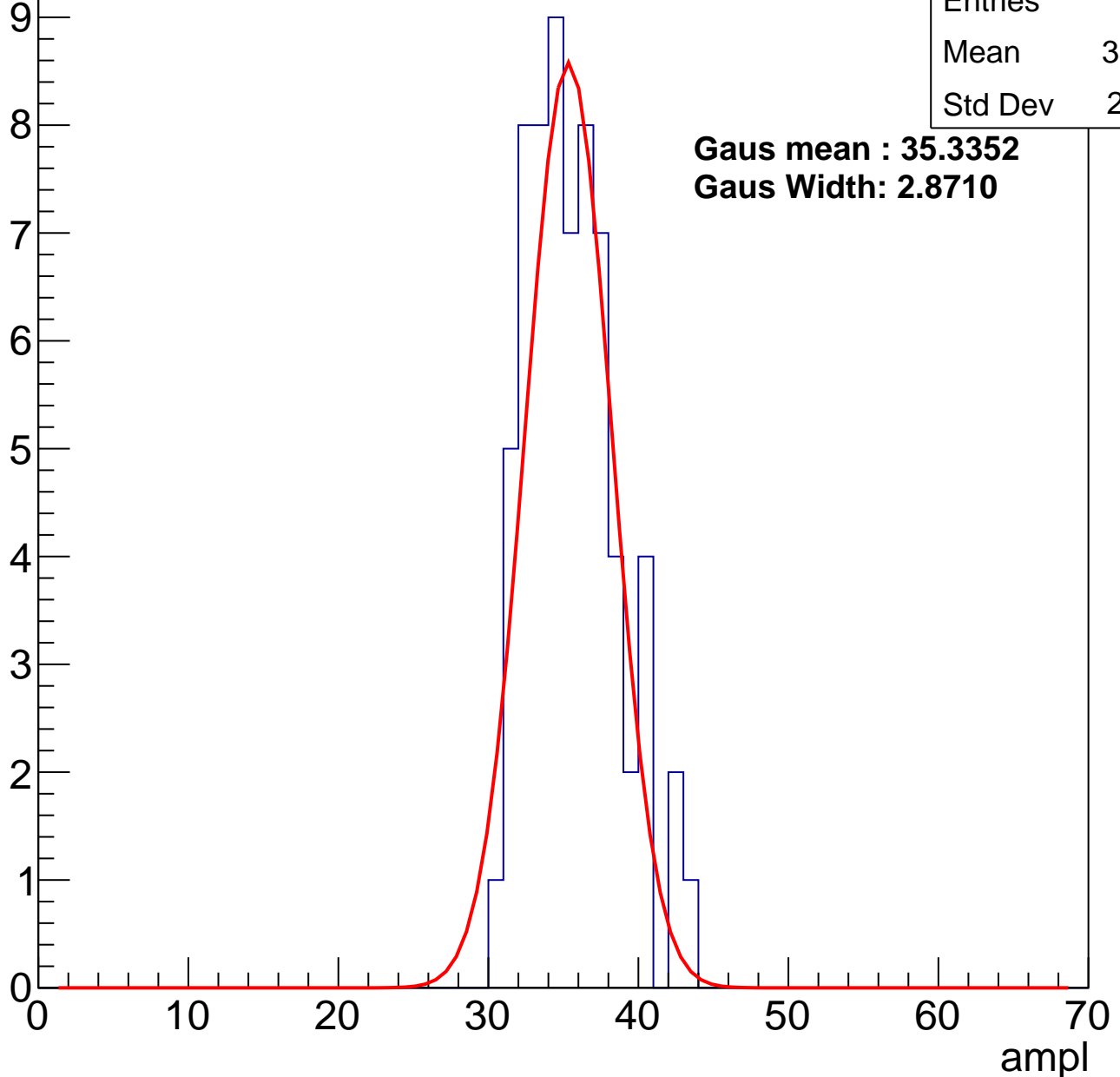
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	35.15
Std Dev	2.971

**Gaus mean : 35.3352**

**Gaus Width: 2.8710**



# B1L000S, U1-ch3, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	41.96
Std Dev	3.168

**Gaus mean : 42.2724**

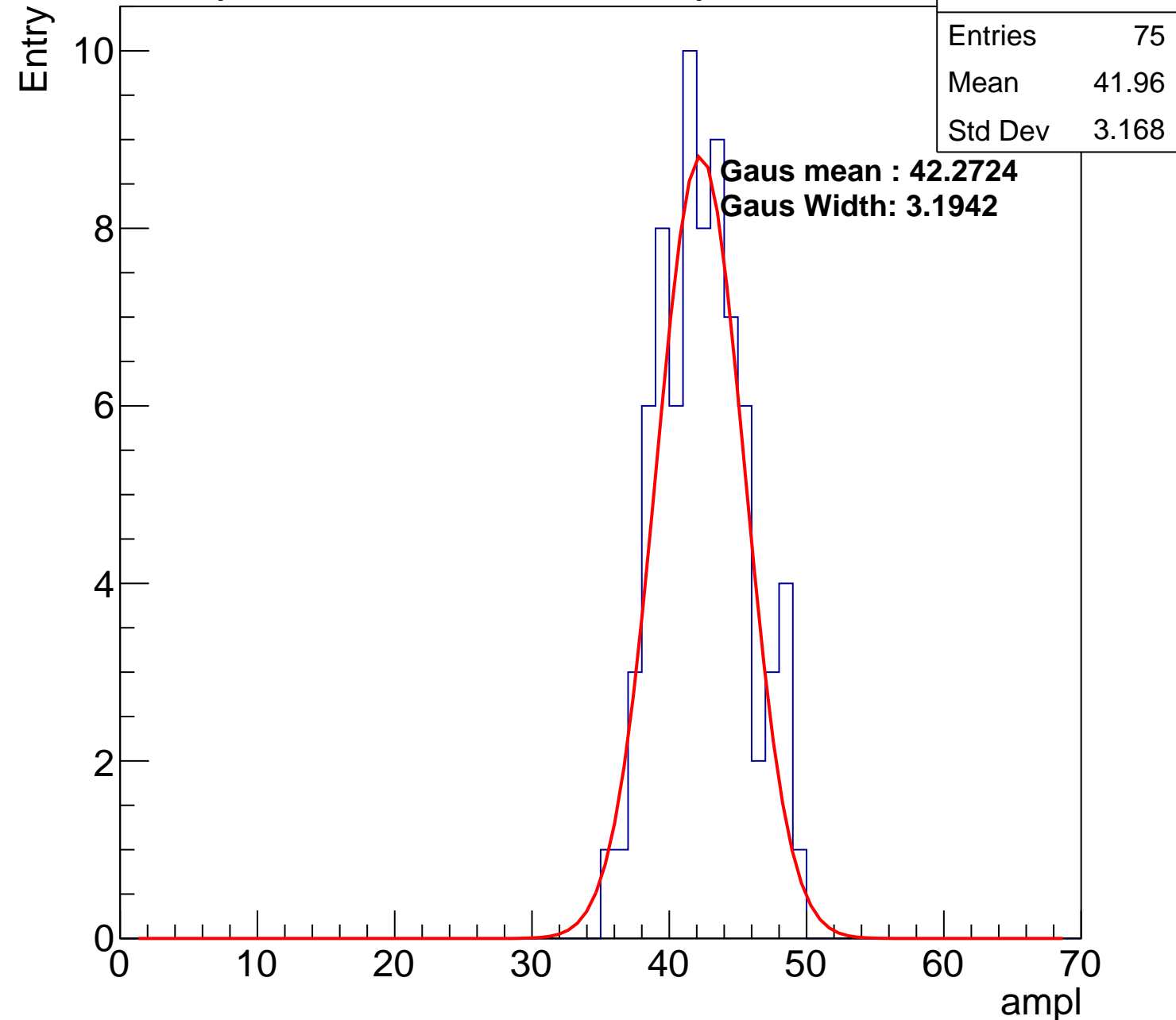
**Gaus Width: 3.1942**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

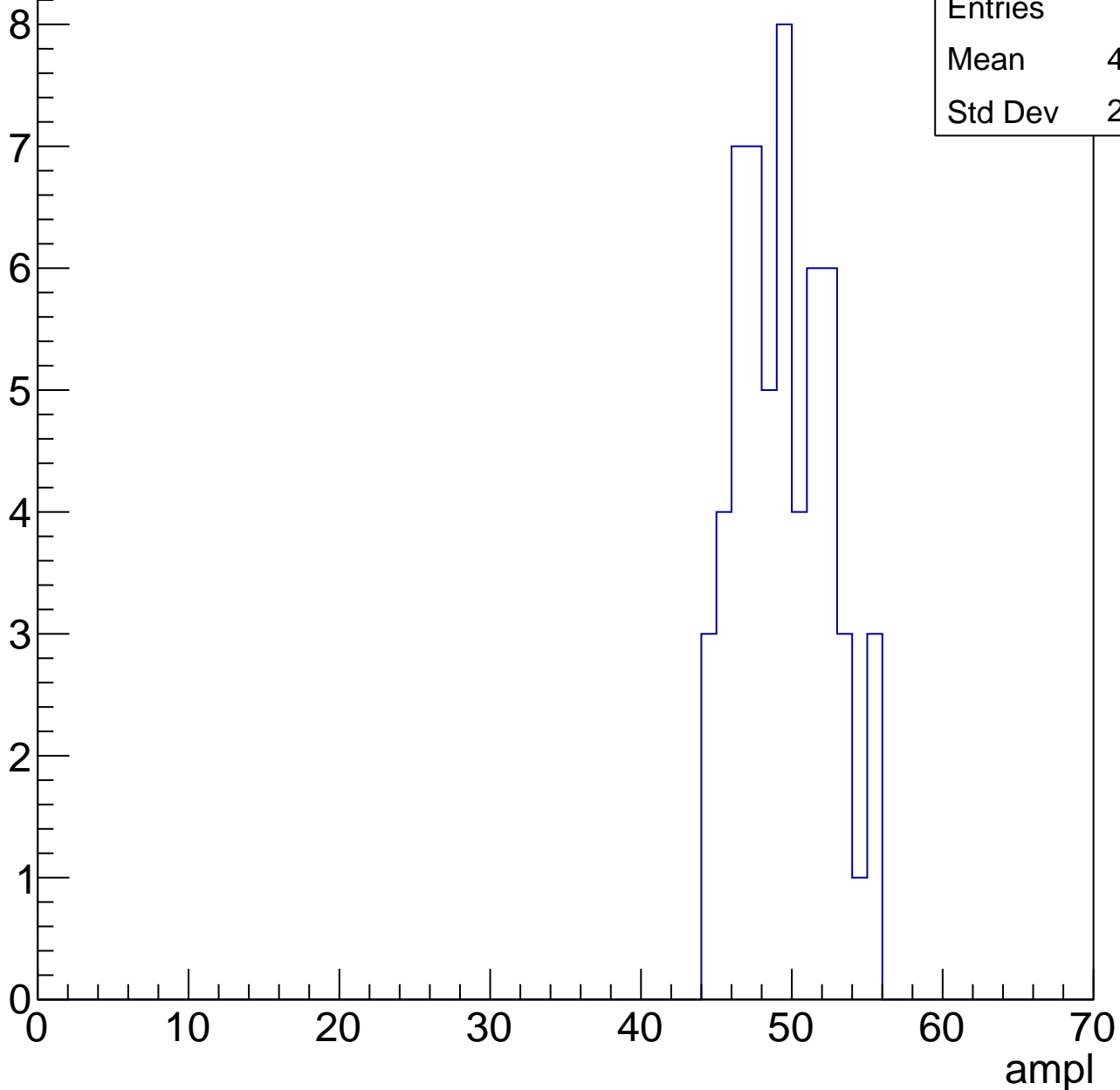


# B1L000S, U1-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	48.96
Std Dev	2.956

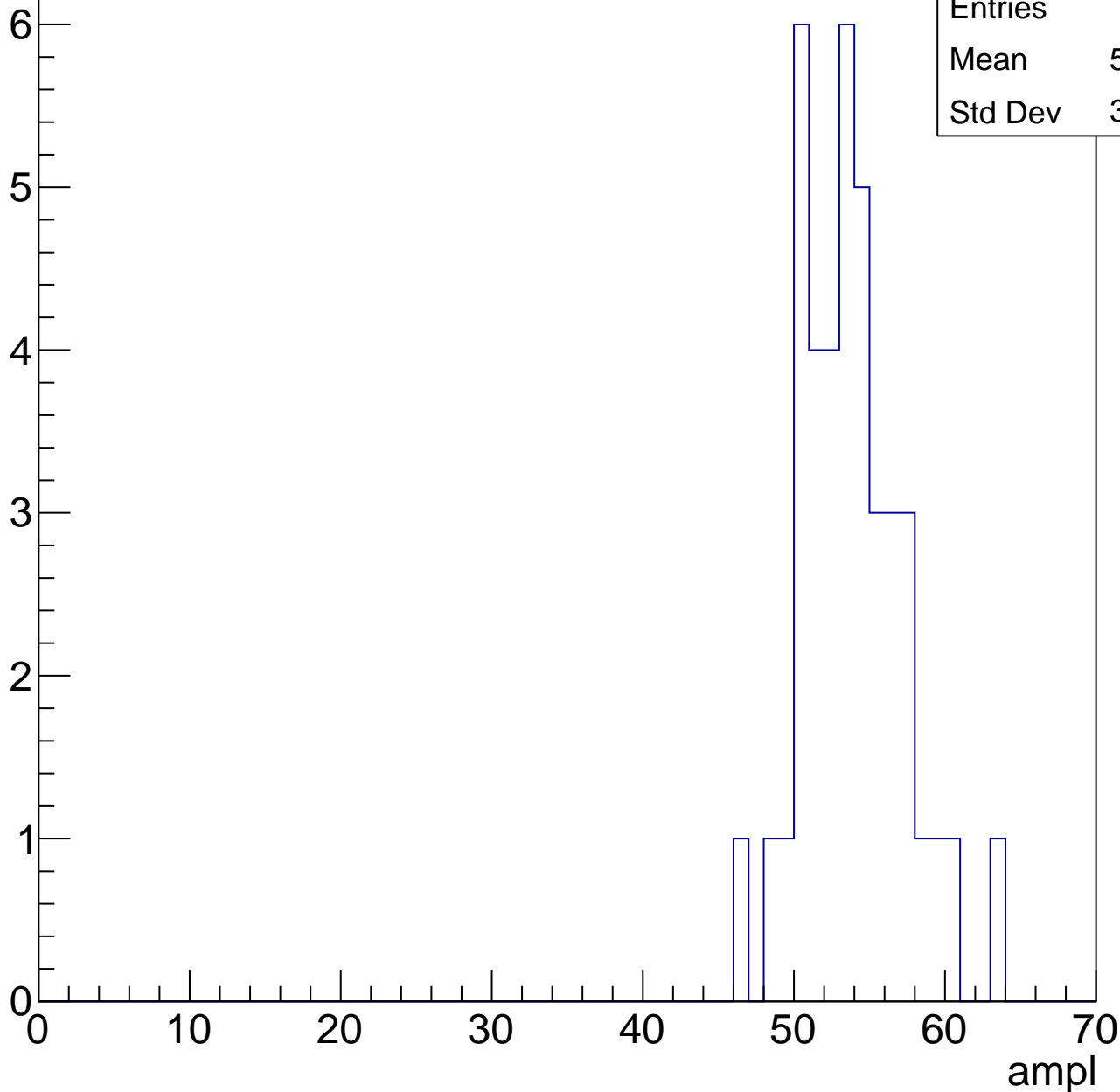


# B1L000S, U1-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

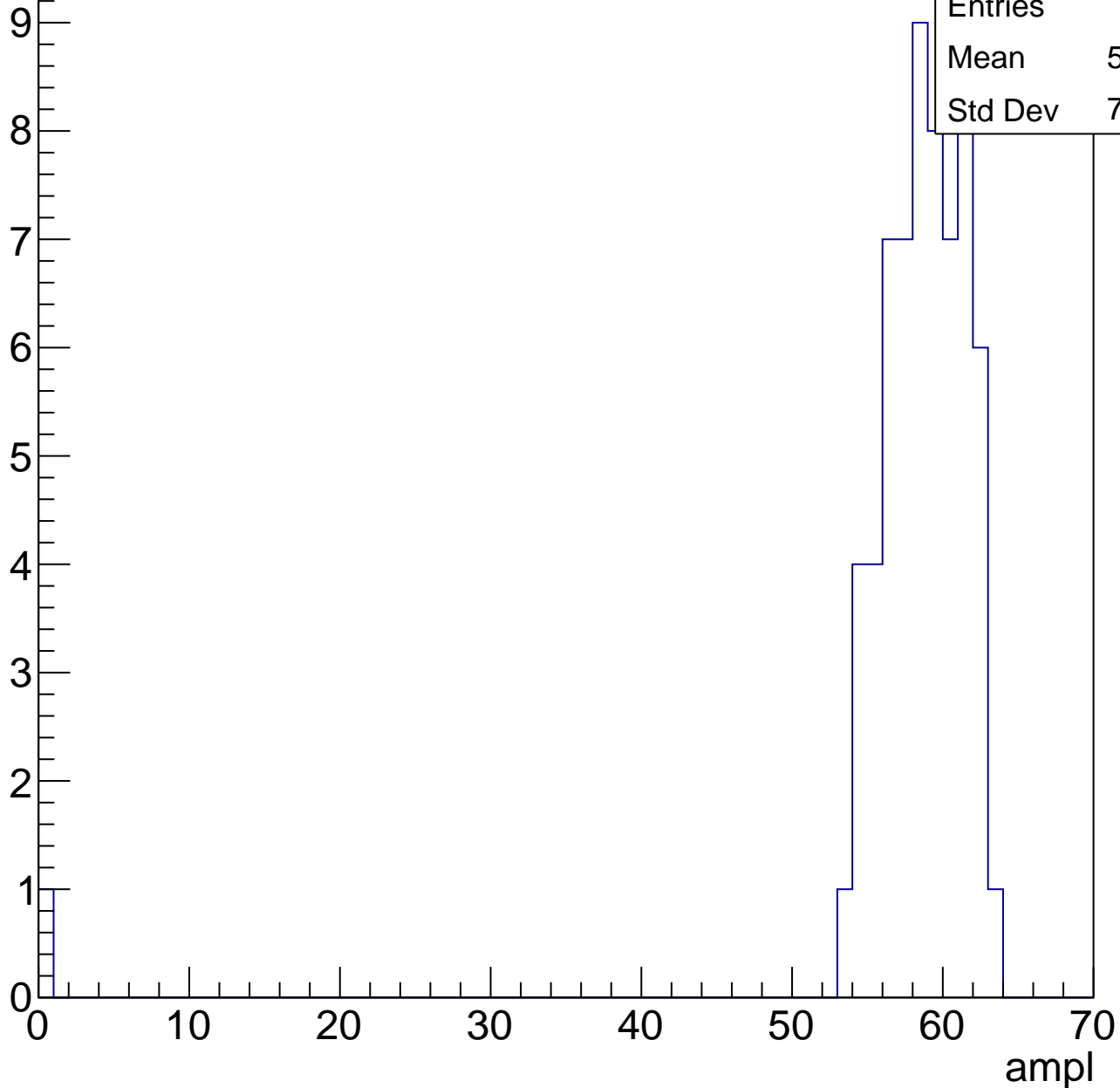
Entries	41
Mean	53.34
Std Dev	3.354



# B1L000S, U1-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

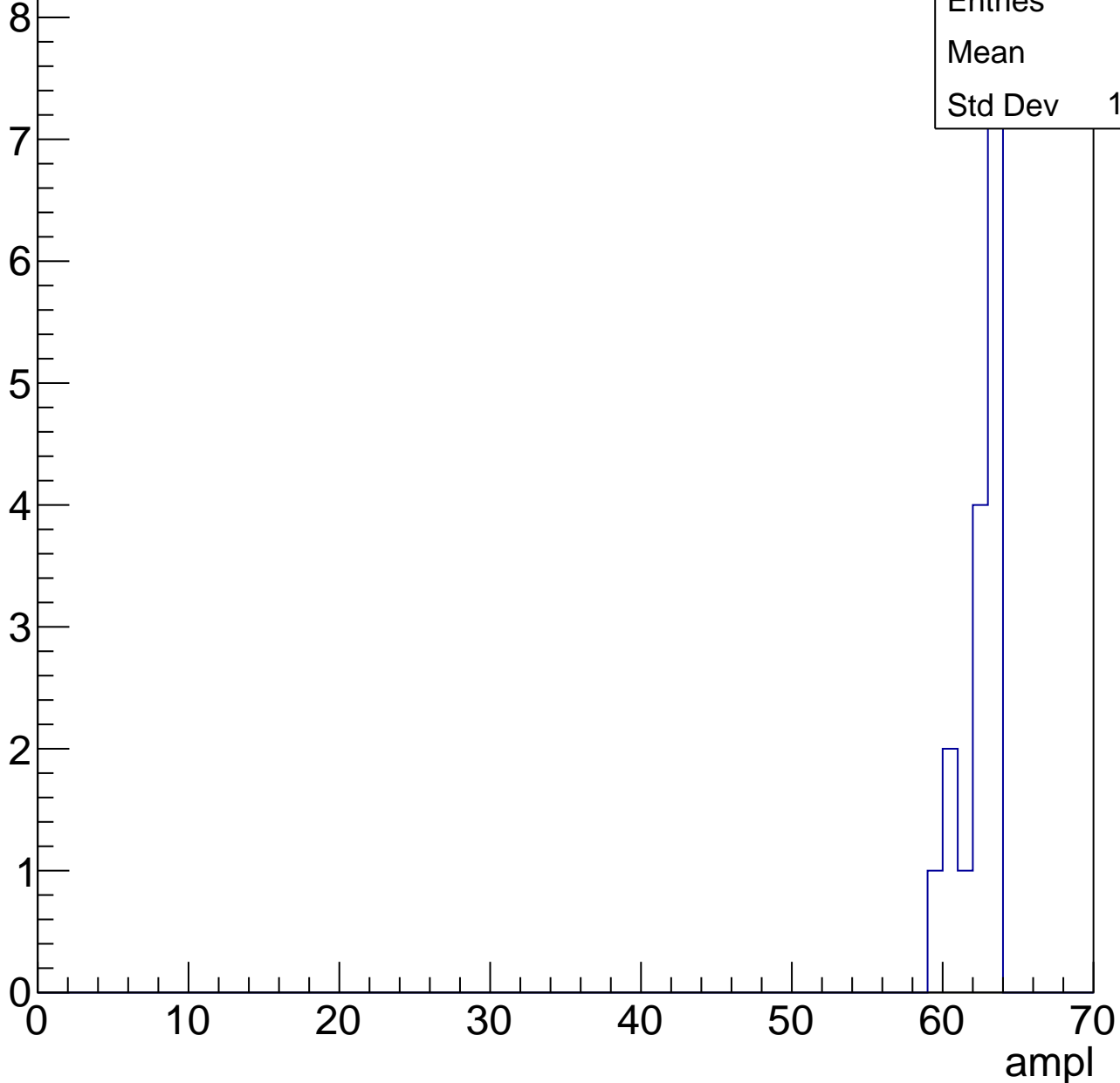


# B1L000S, U1-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	62
Std Dev	1.275





# B1L000S, U1-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U1-ch4, adc0

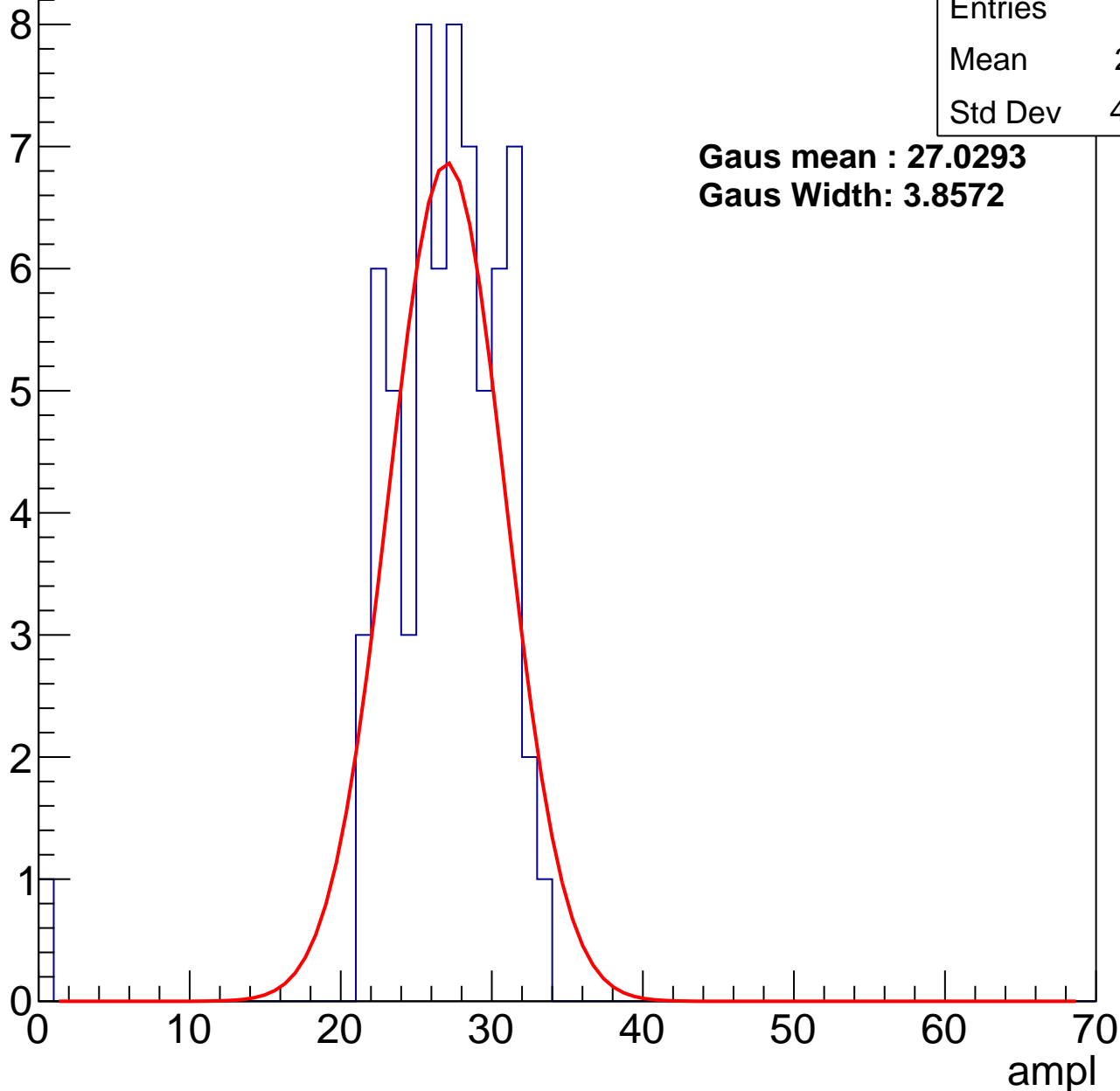
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	26.31
Std Dev	4.502

**Gaus mean : 27.0293**

**Gaus Width: 3.8572**



# B1L000S, U1-ch4, adc1

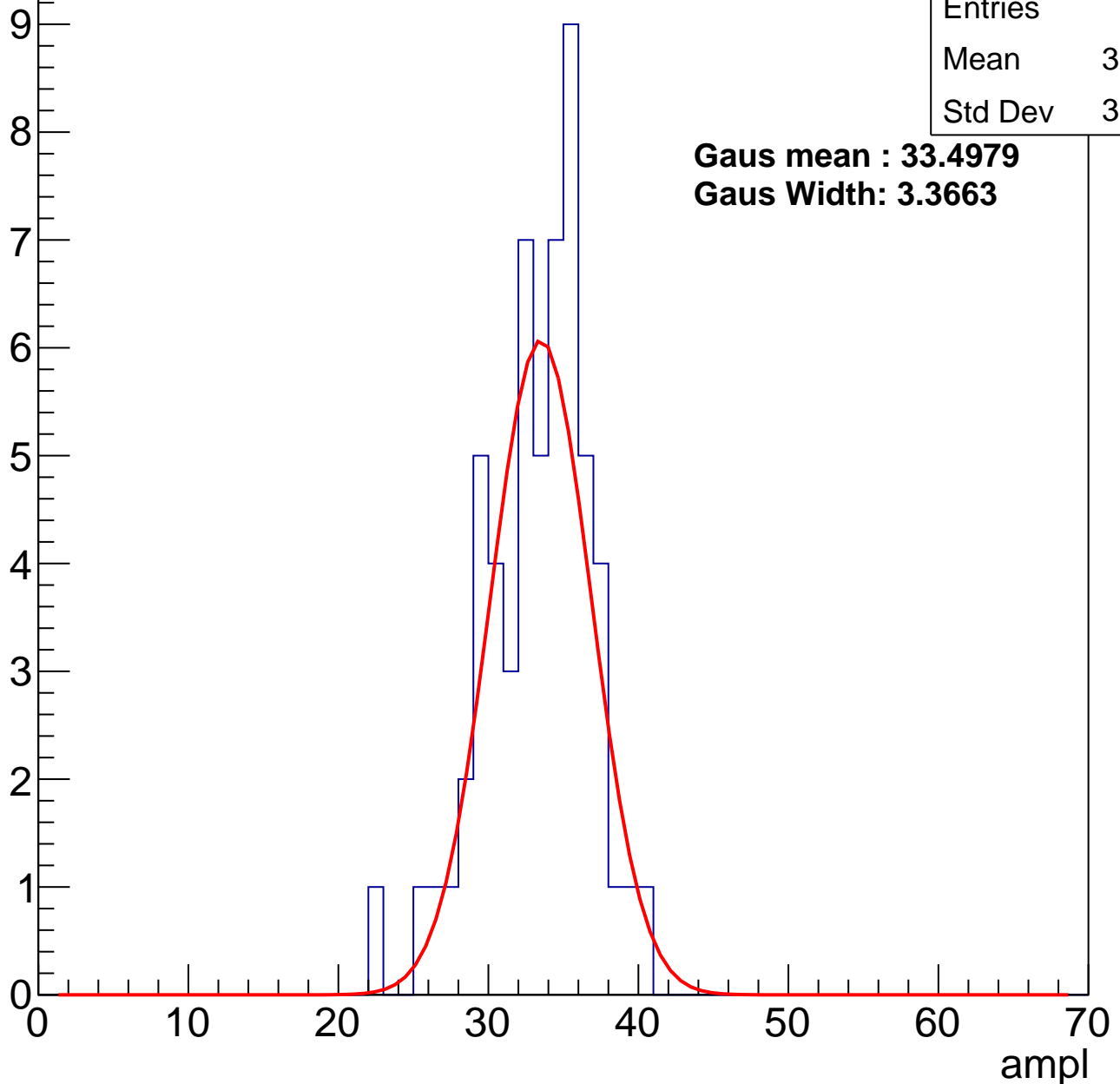
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	32.78
Std Dev	3.509

**Gaus mean : 33.4979**

**Gaus Width: 3.3663**



# B1L000S, U1-ch4, adc2

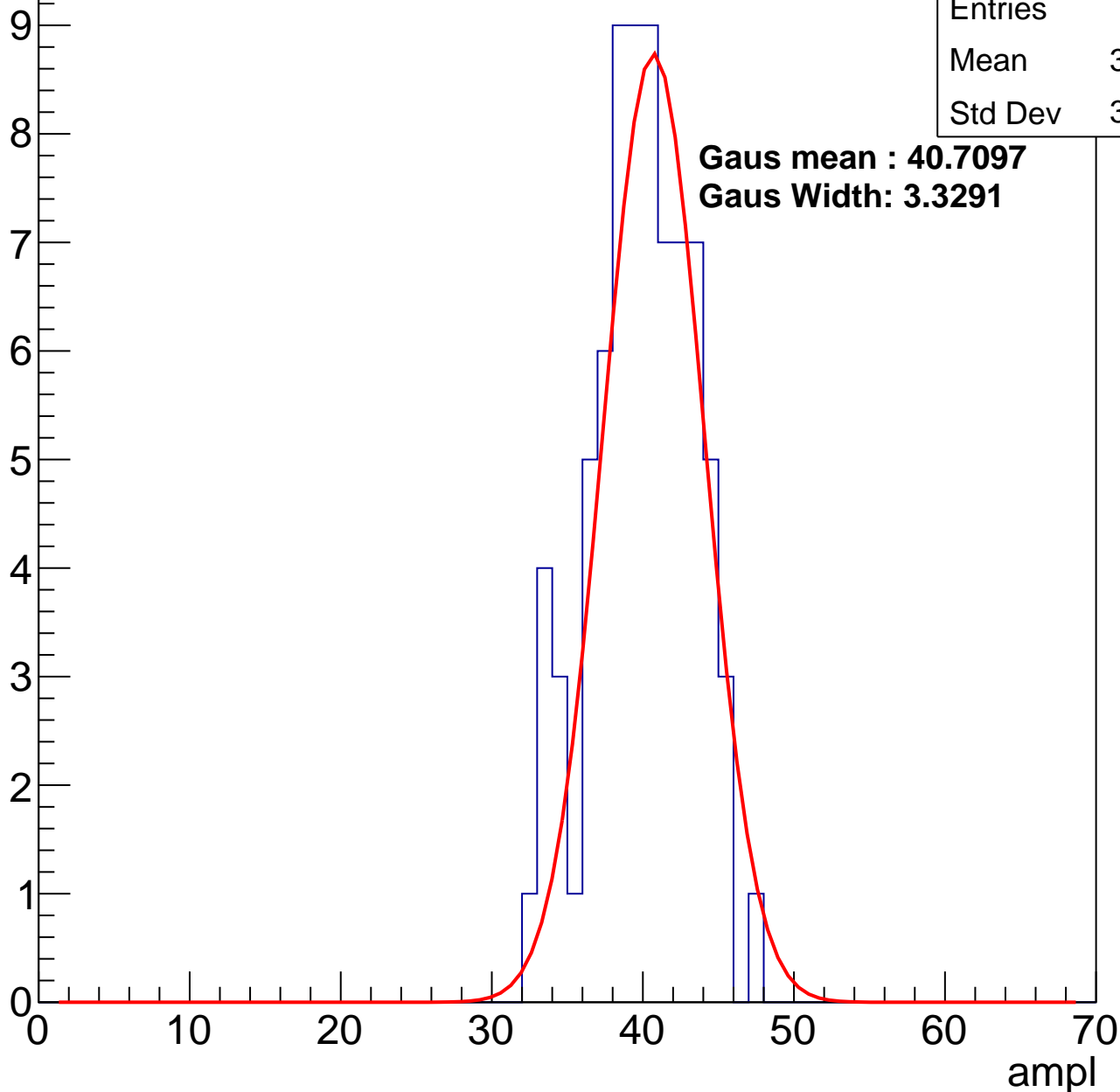
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	39.48
Std Dev	3.329

**Gaus mean : 40.7097**

**Gaus Width: 3.3291**

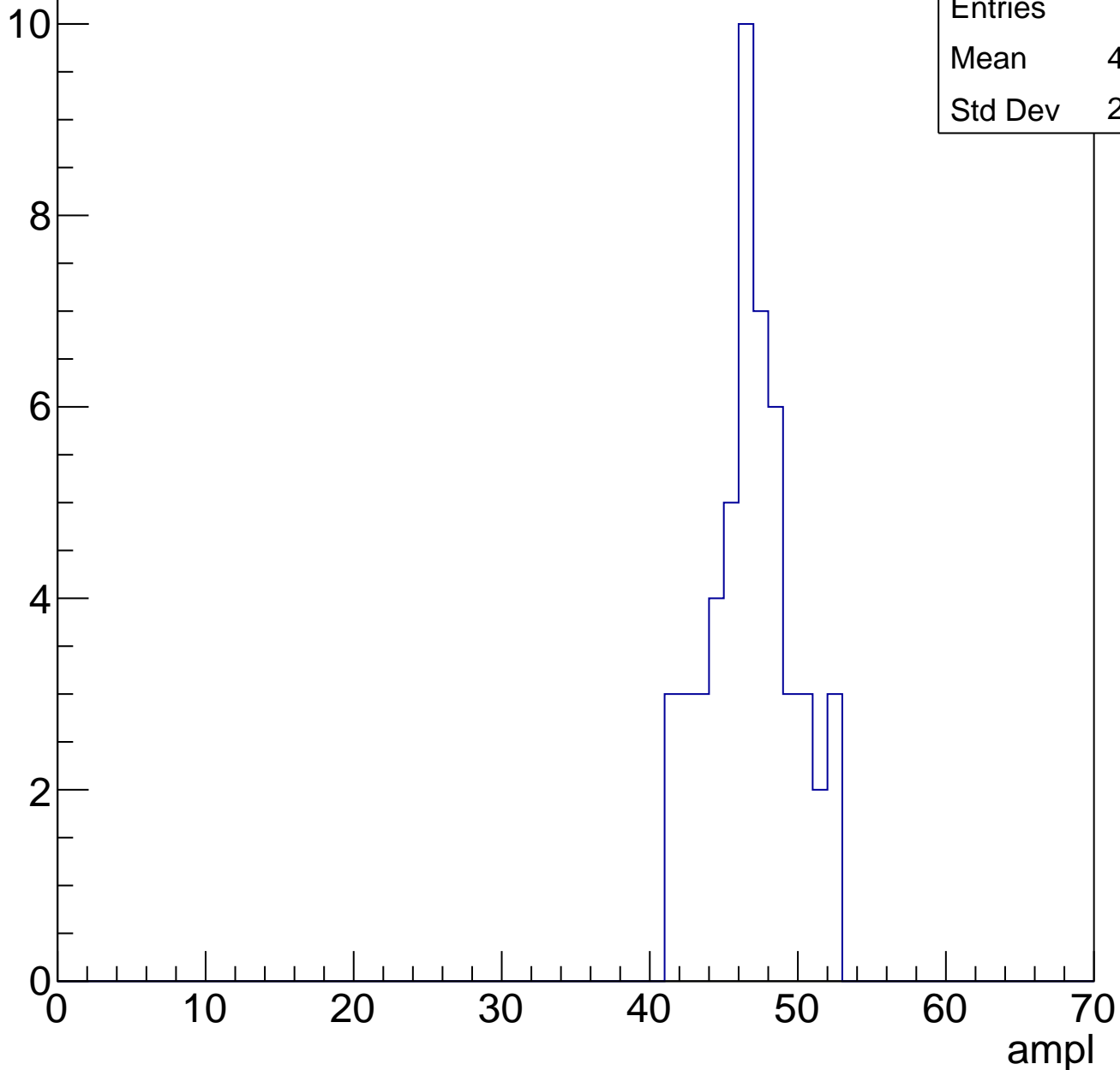


# B1L000S, U1-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	52
Mean	46.37
Std Dev	2.869

Entry

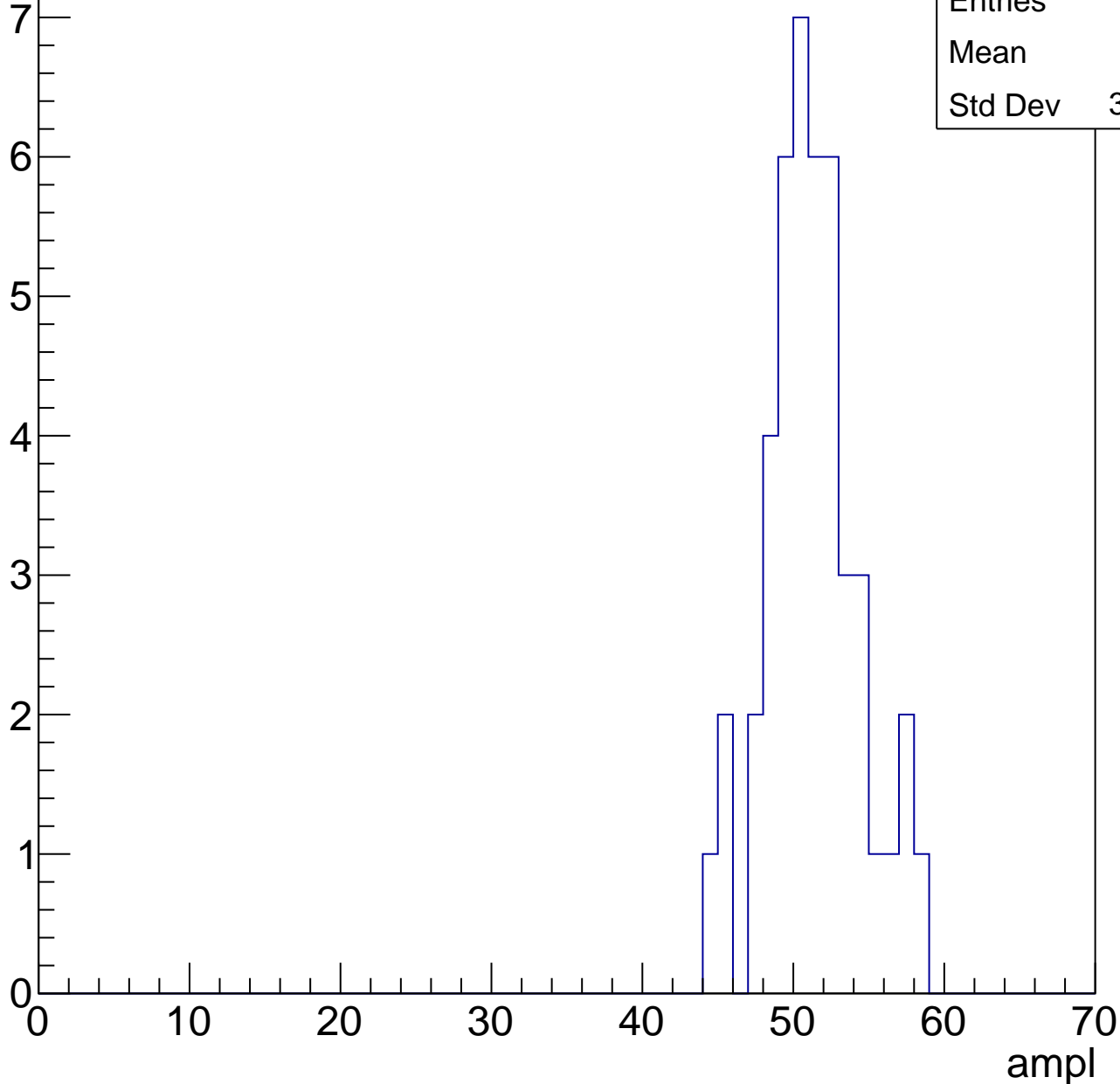


# B1L000S, U1-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

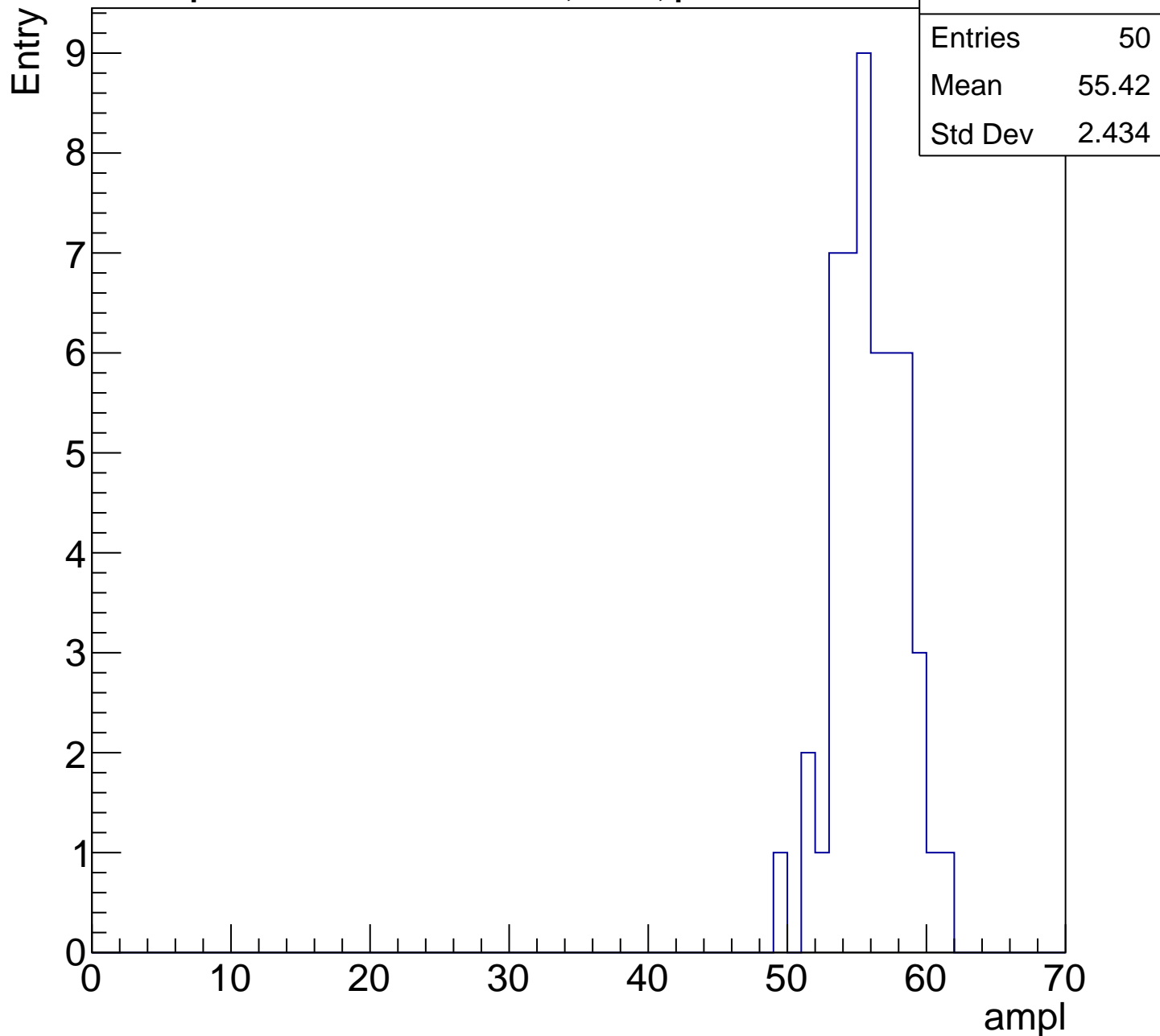
Entry

Entries	45
Mean	50.8
Std Dev	3.074



# B1L000S, U1-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

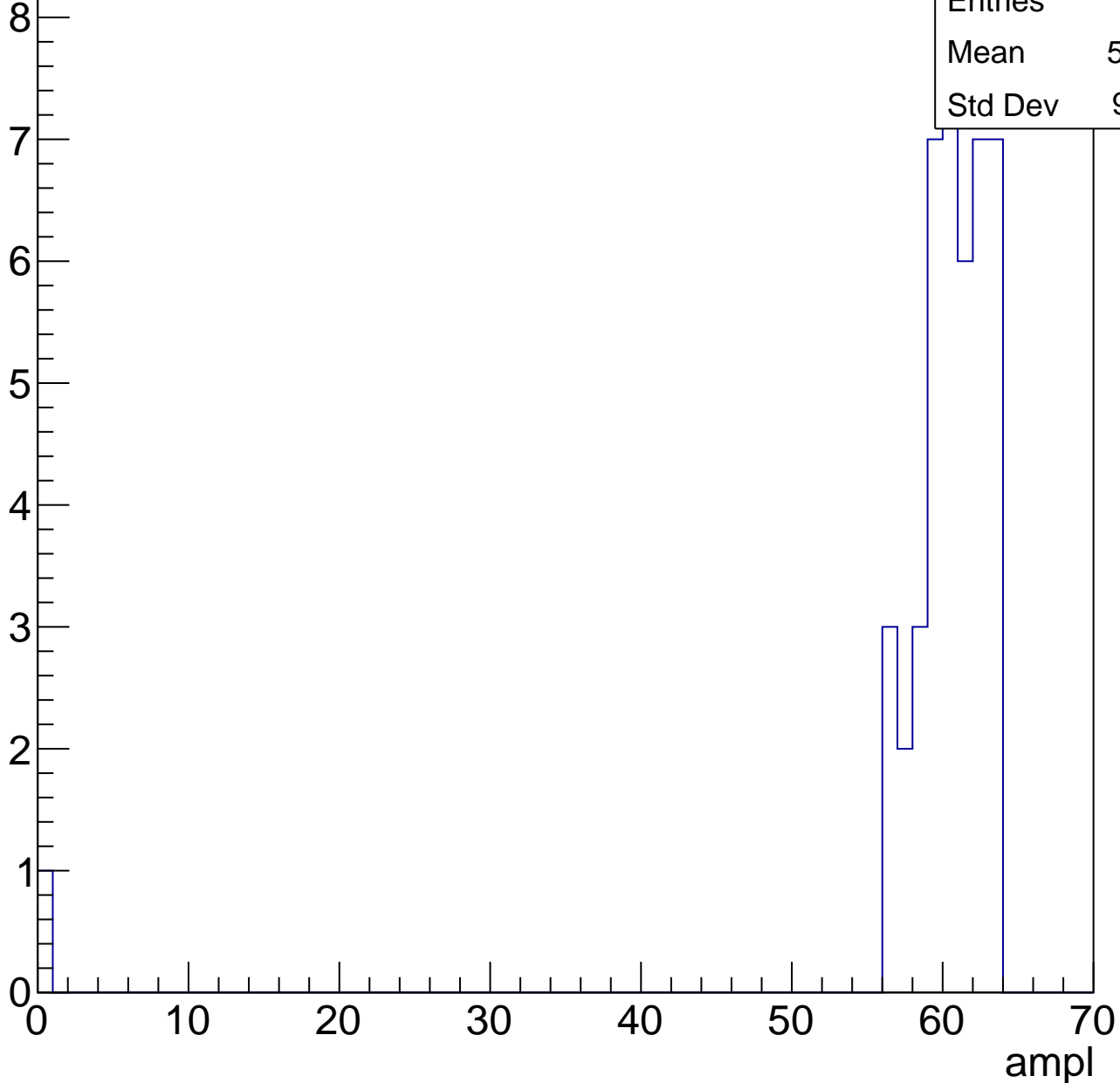


# B1L000S, U1-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	58.86
Std Dev	9.201

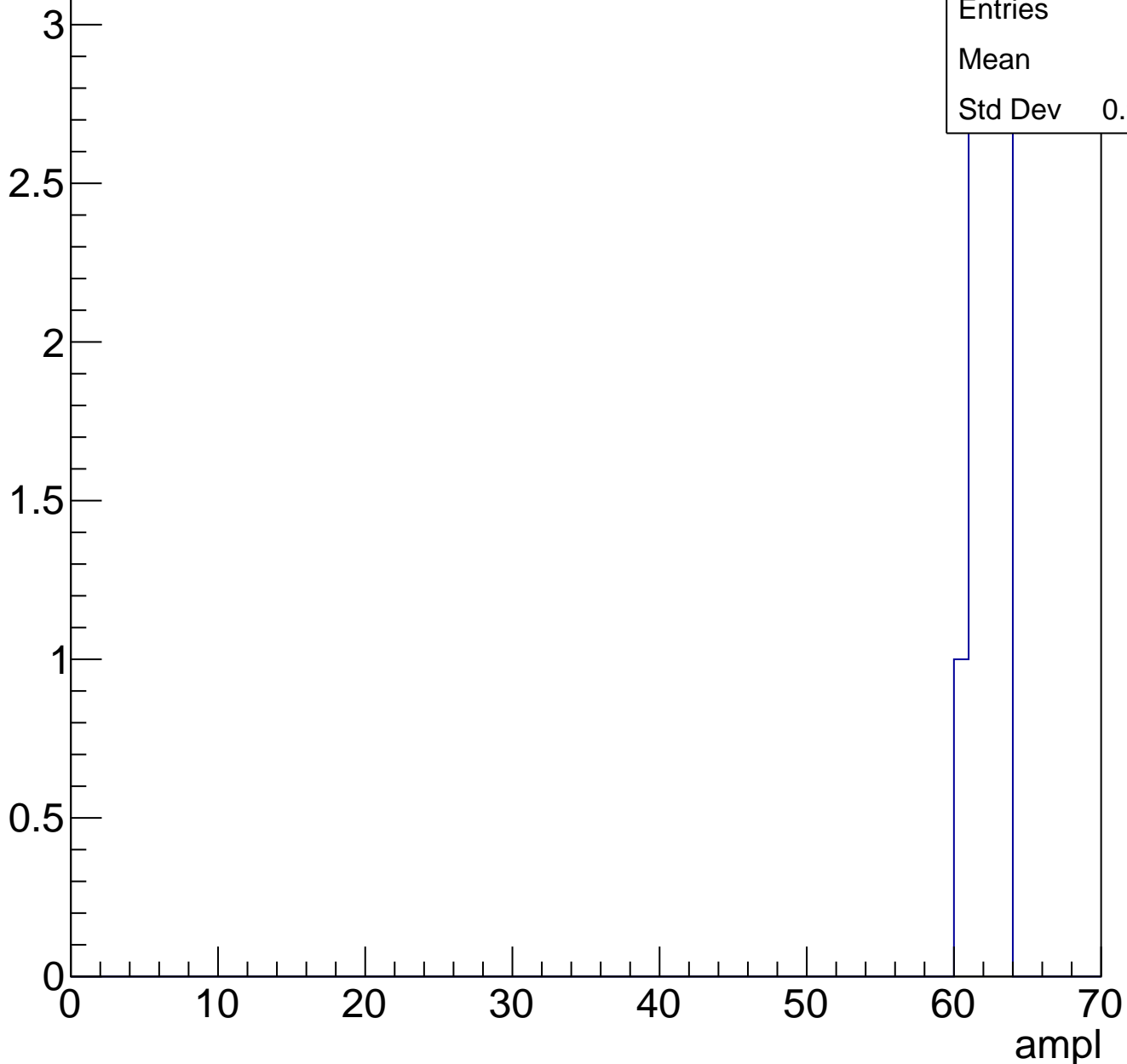




# B1L000S, U1-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch5, adc0

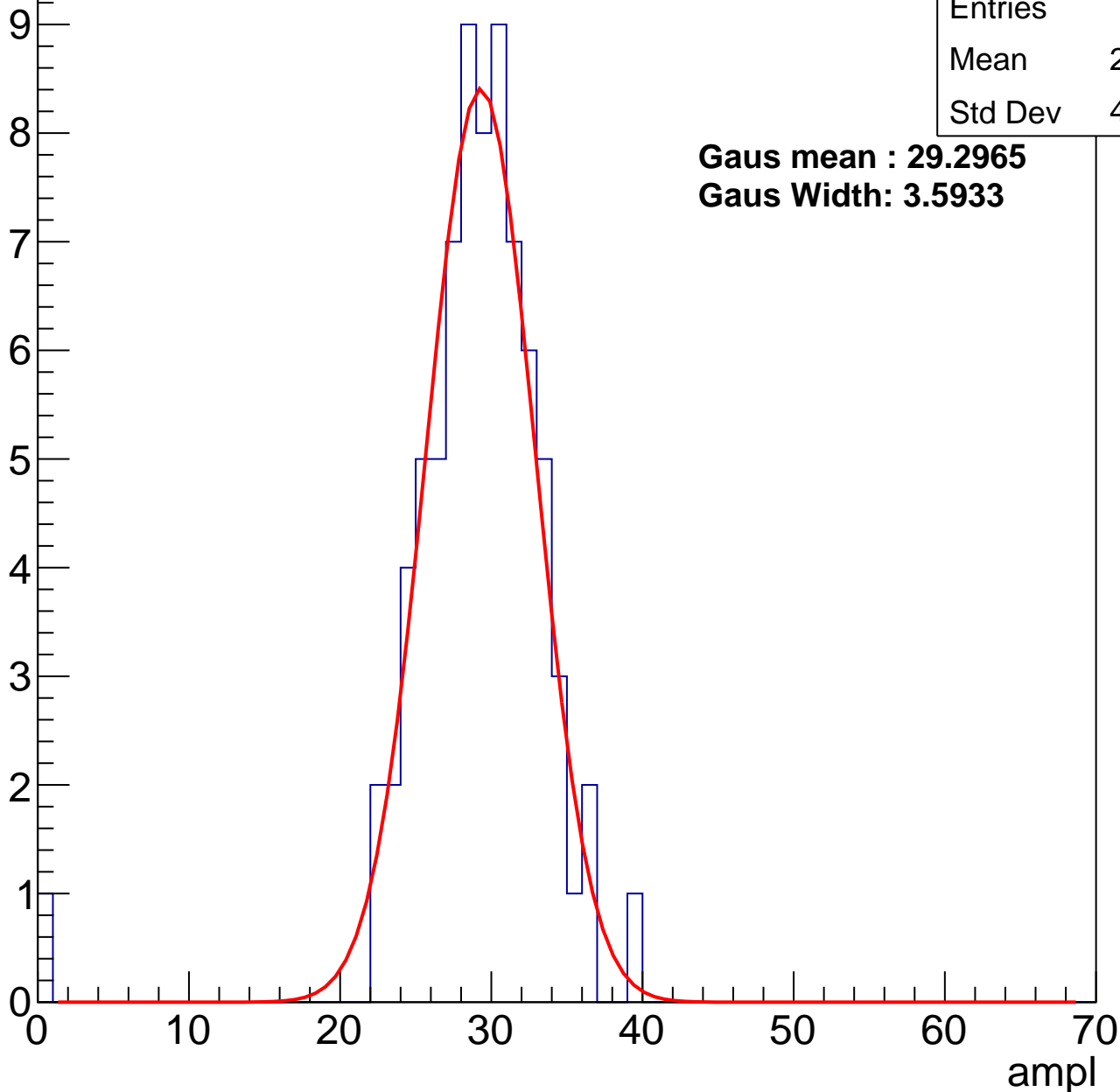
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	28.65
Std Dev	4.758

**Gaus mean : 29.2965**

**Gaus Width: 3.5933**



# B1L000S, U1-ch5, adc1

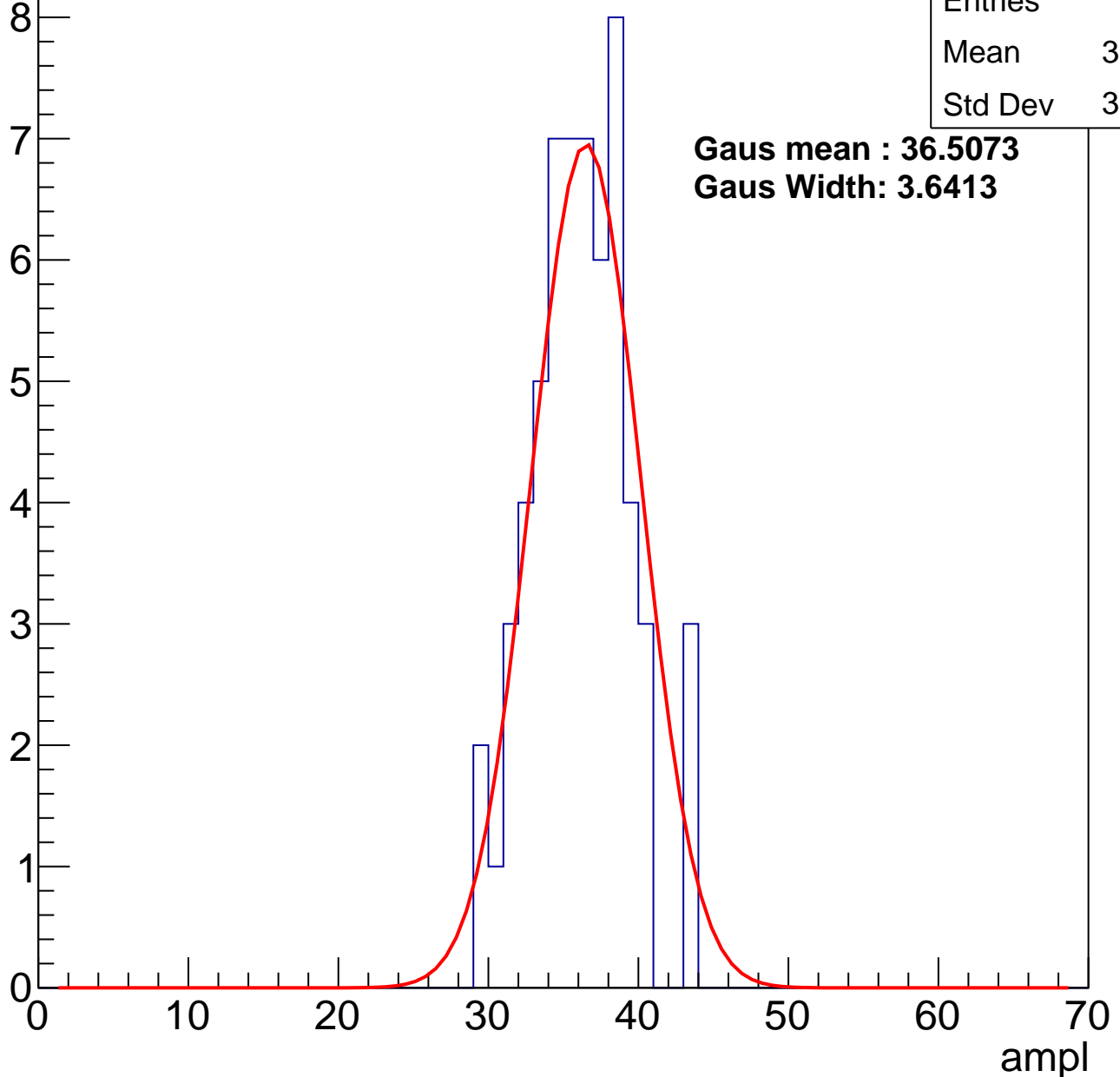
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	35.67
Std Dev	3.202

**Gaus mean : 36.5073**

**Gaus Width: 3.6413**



# B1L000S, U1-ch5, adc2

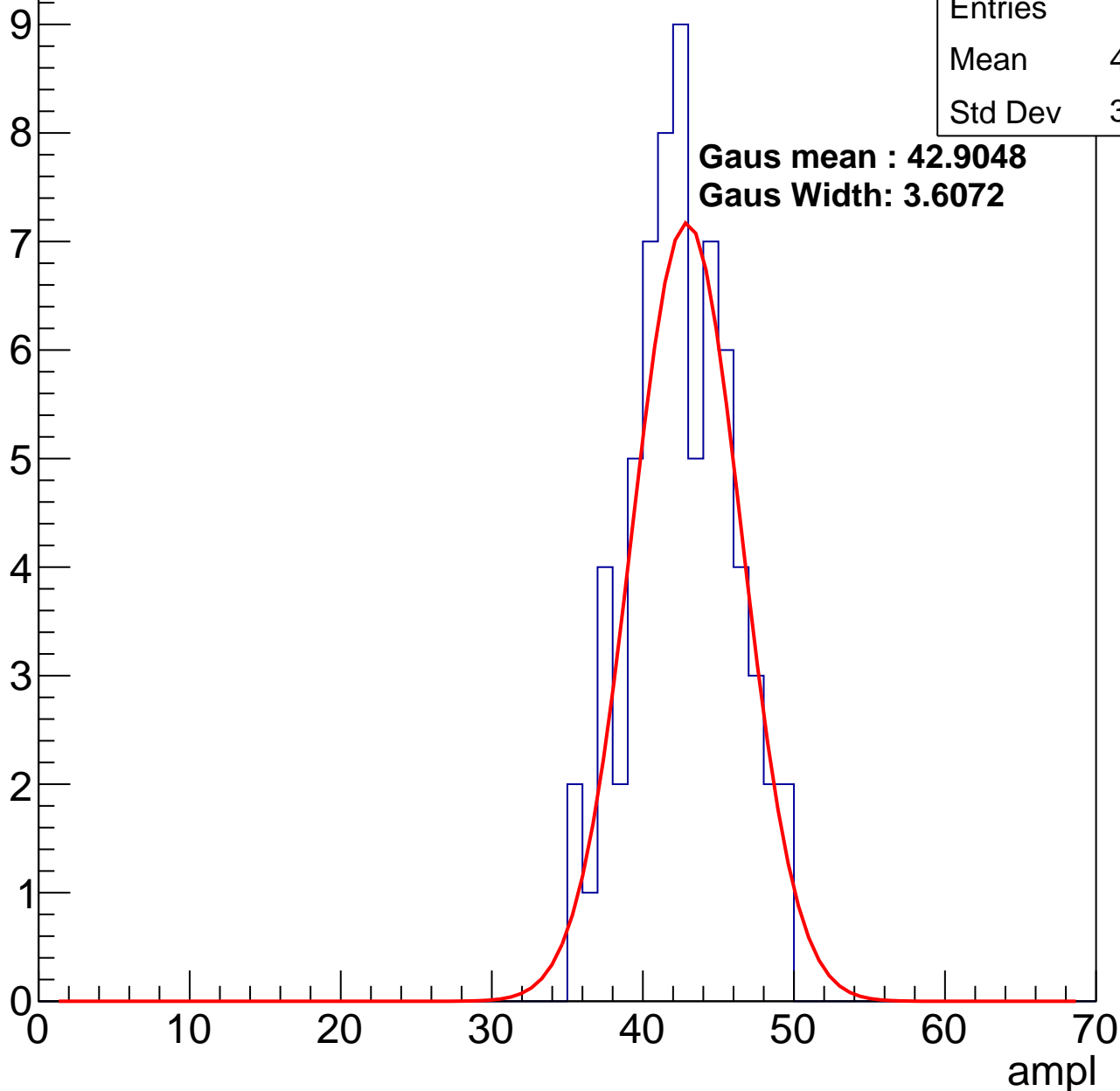
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.13
Std Dev	3.327

**Gaus mean : 42.9048**

**Gaus Width: 3.6072**

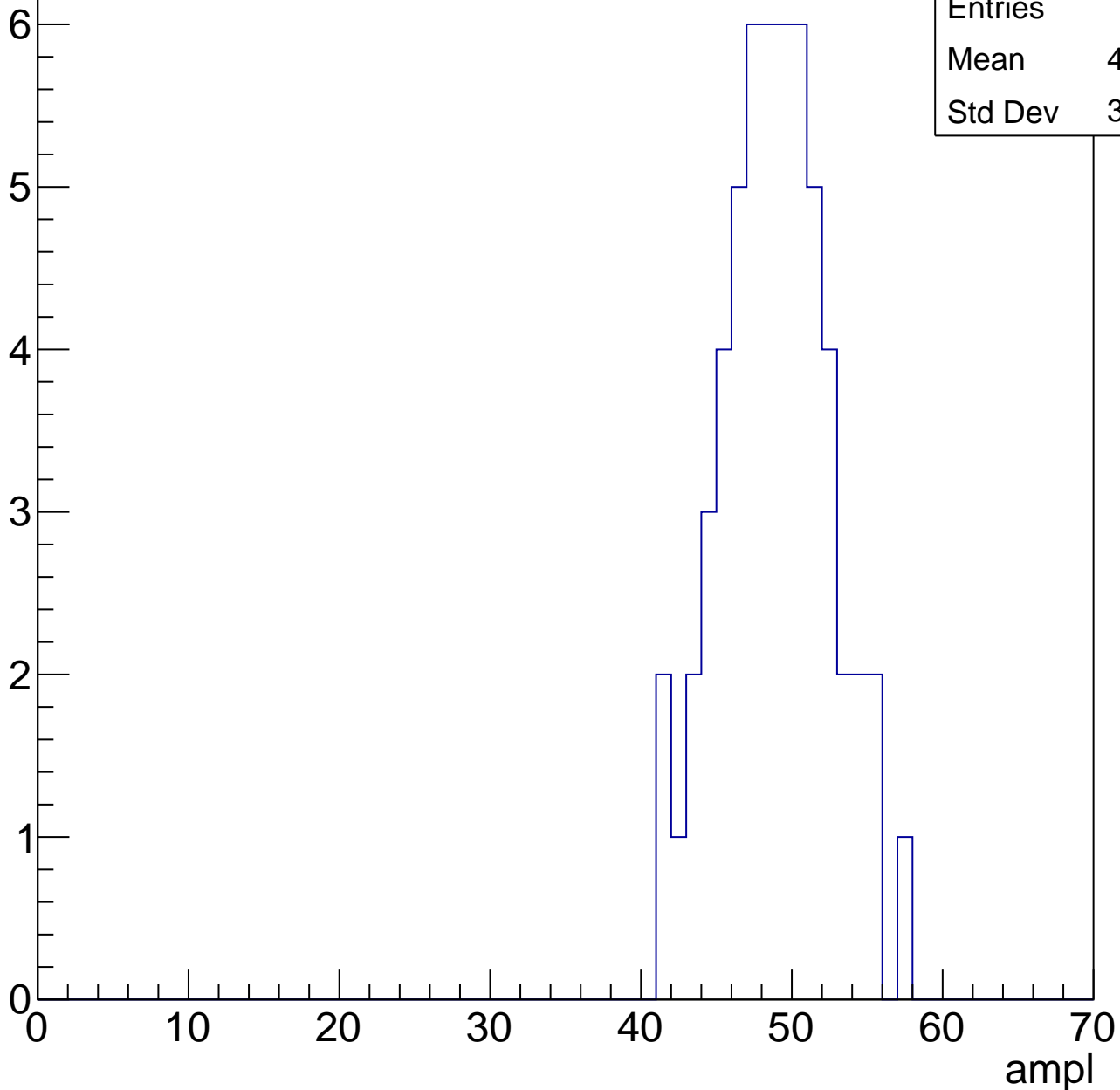


# B1L000S, U1-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

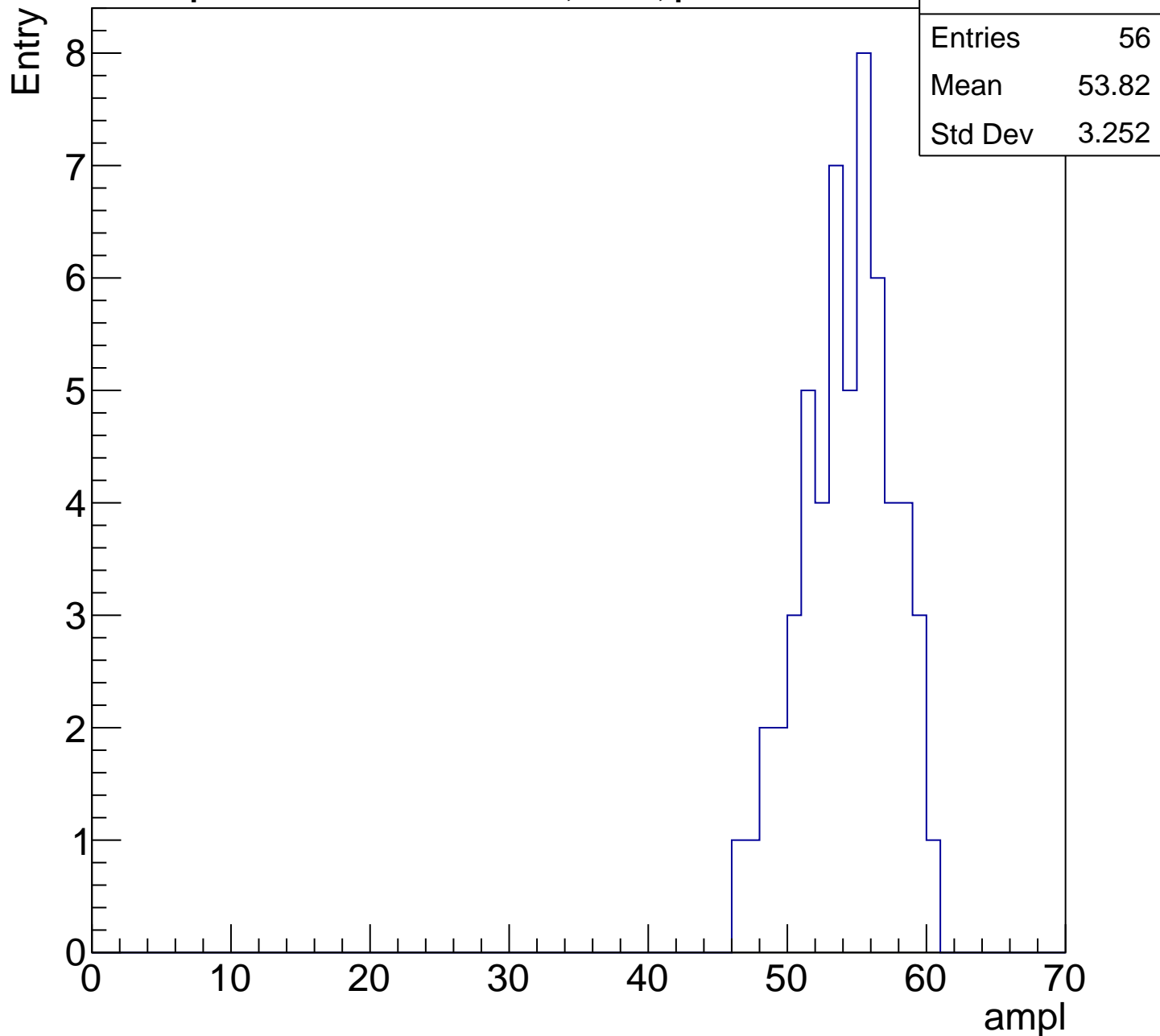
Entry

Entries	57
Mean	48.42
Std Dev	3.564



# B1L000S, U1-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

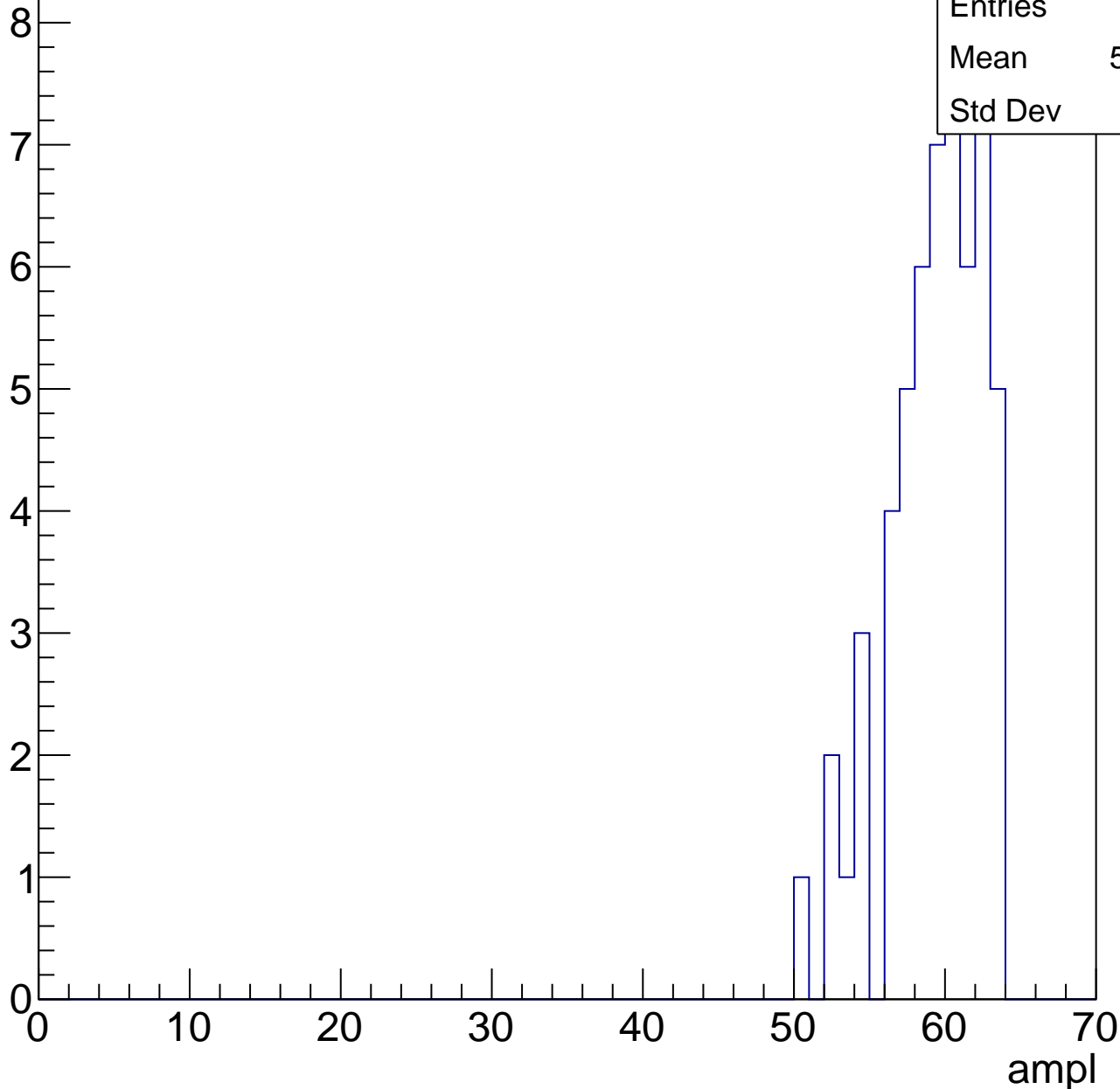


# B1L000S, U1-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

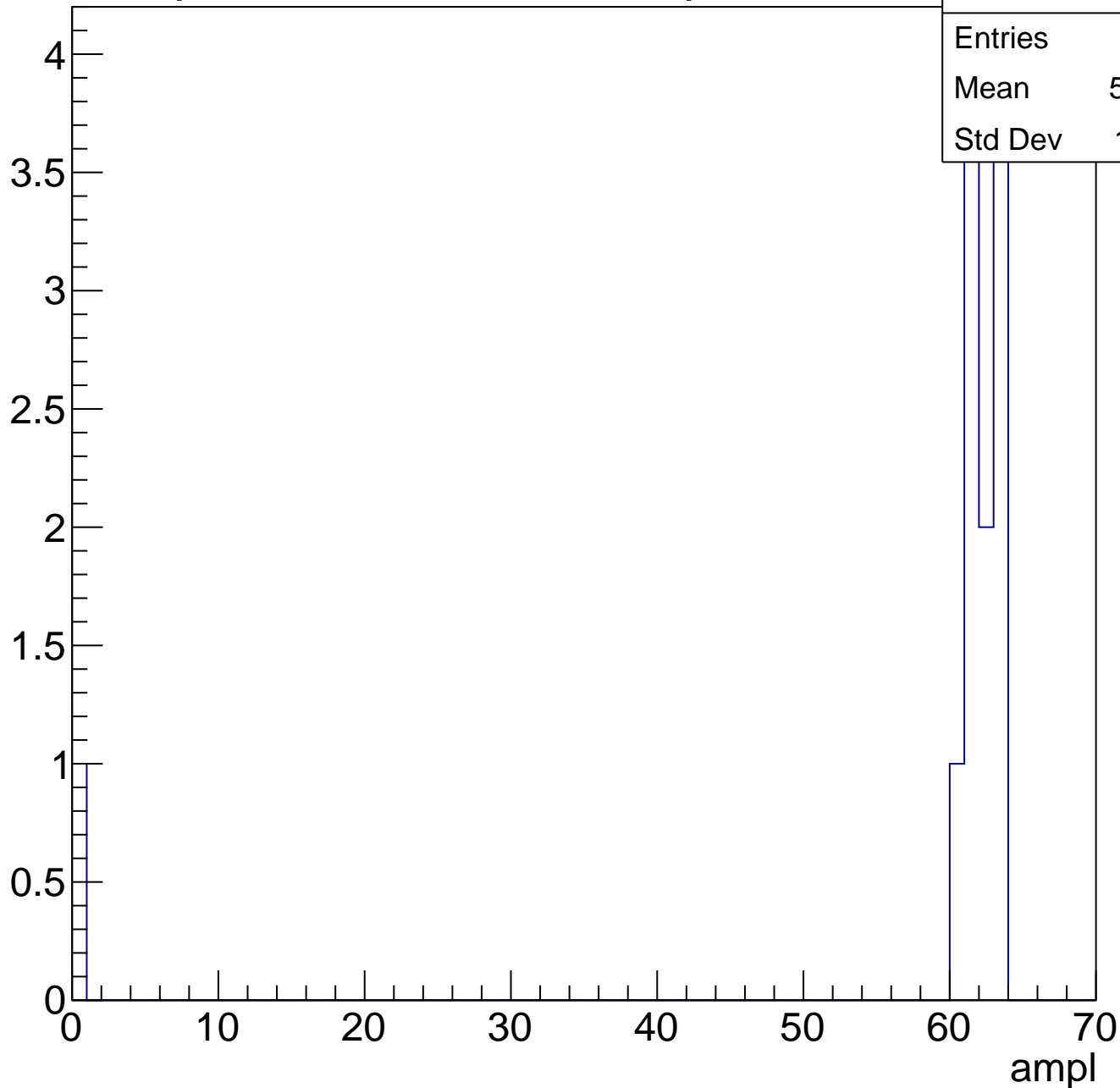
Entries	56
Mean	58.86
Std Dev	3.09



# B1L000S, U1-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U1-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch6, adc0

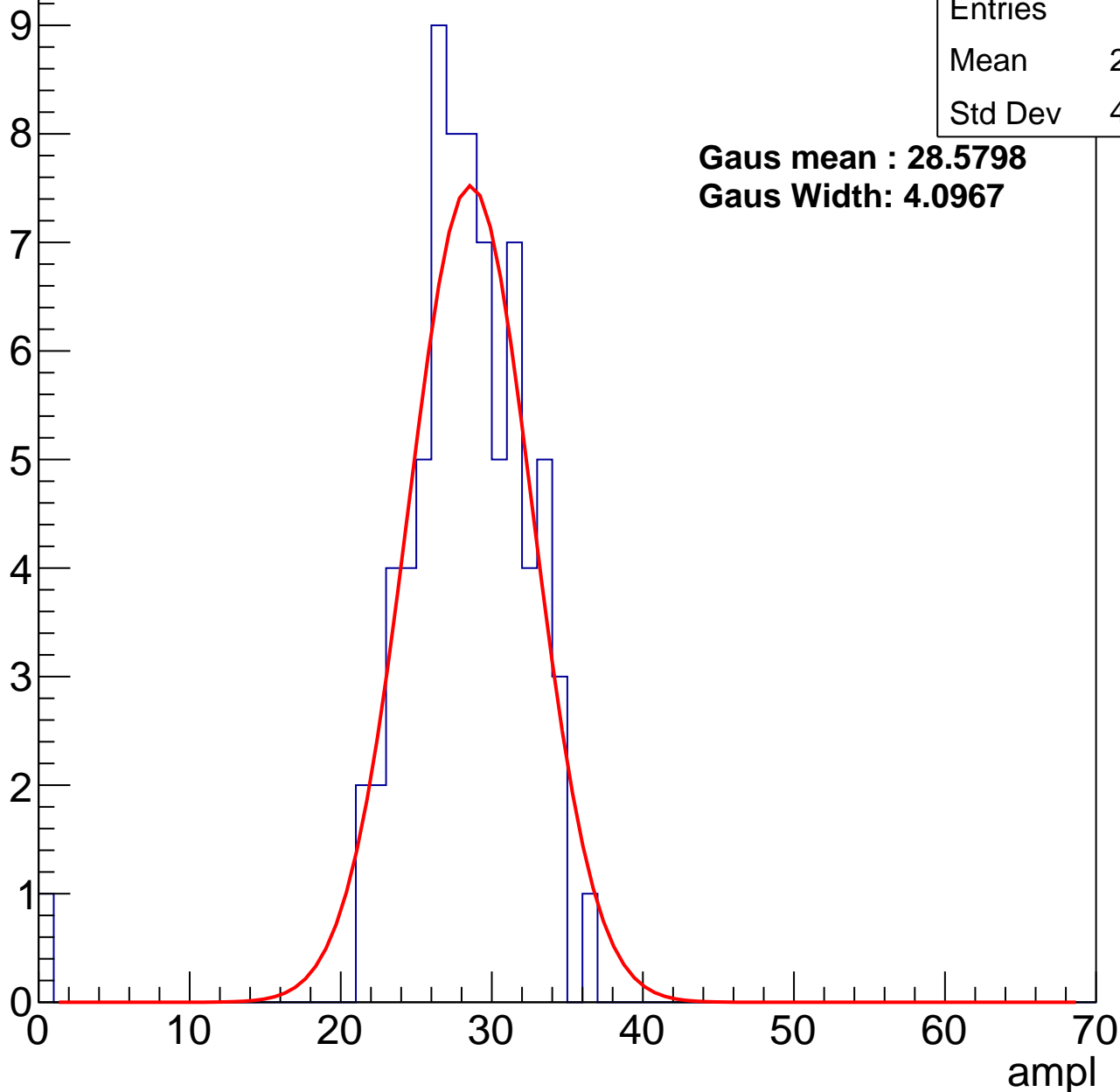
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	27.65
Std Dev	4.689

**Gaus mean : 28.5798**

**Gaus Width: 4.0967**



# B1L000S, U1-ch6, adc1

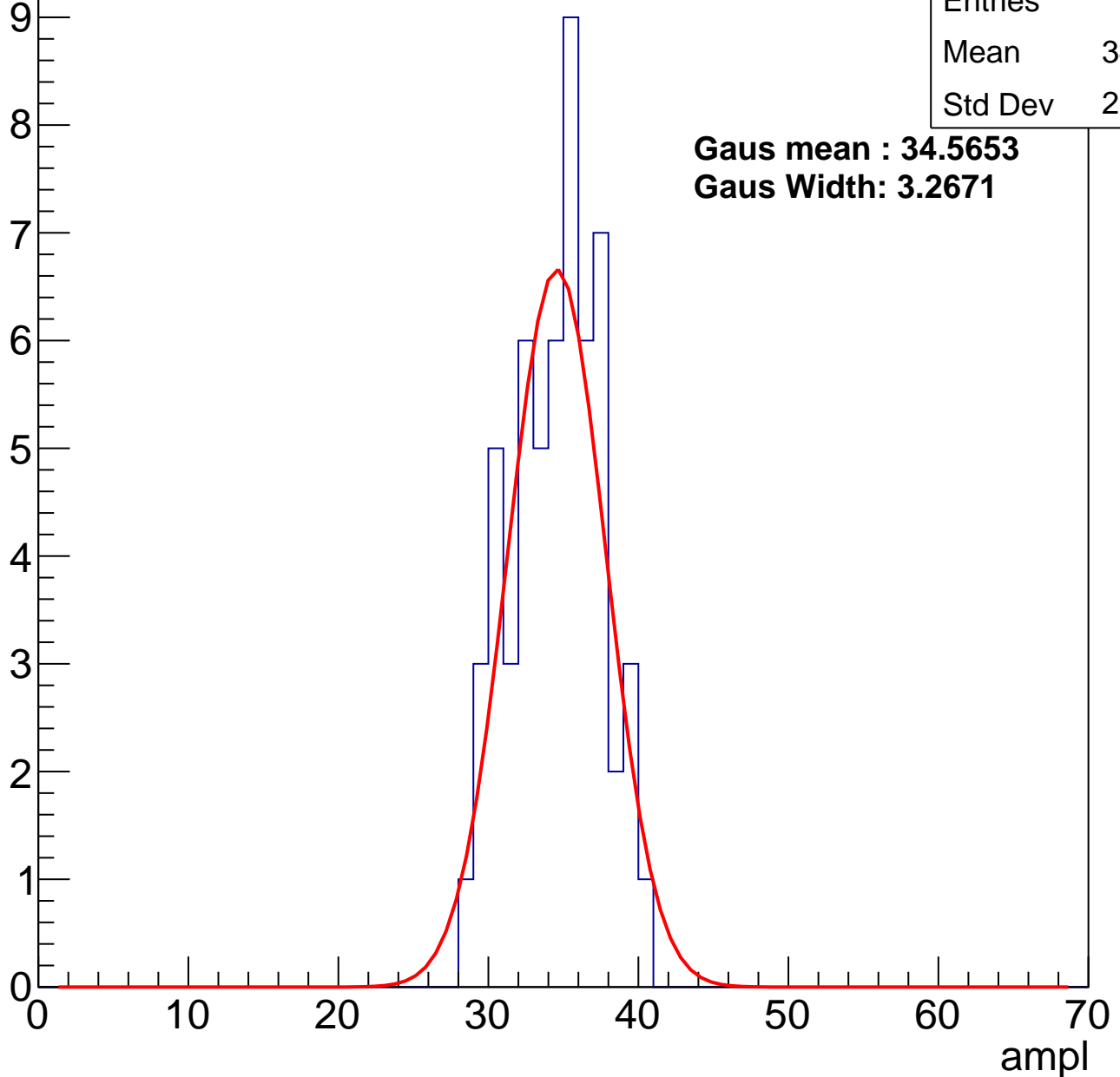
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	34.07
Std Dev	2.919

**Gaus mean : 34.5653**

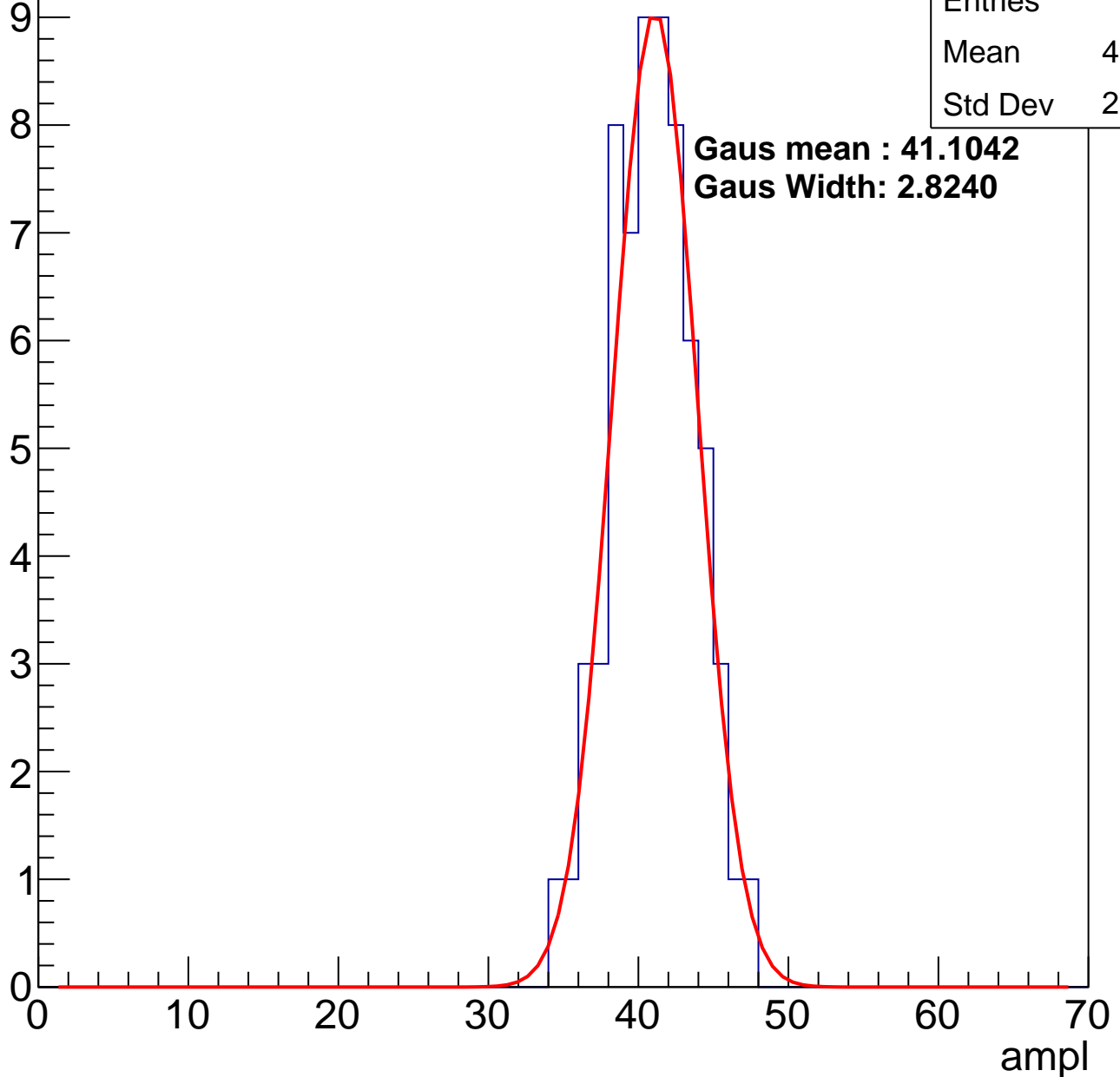
**Gaus Width: 3.2671**



# B1L000S, U1-ch6, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

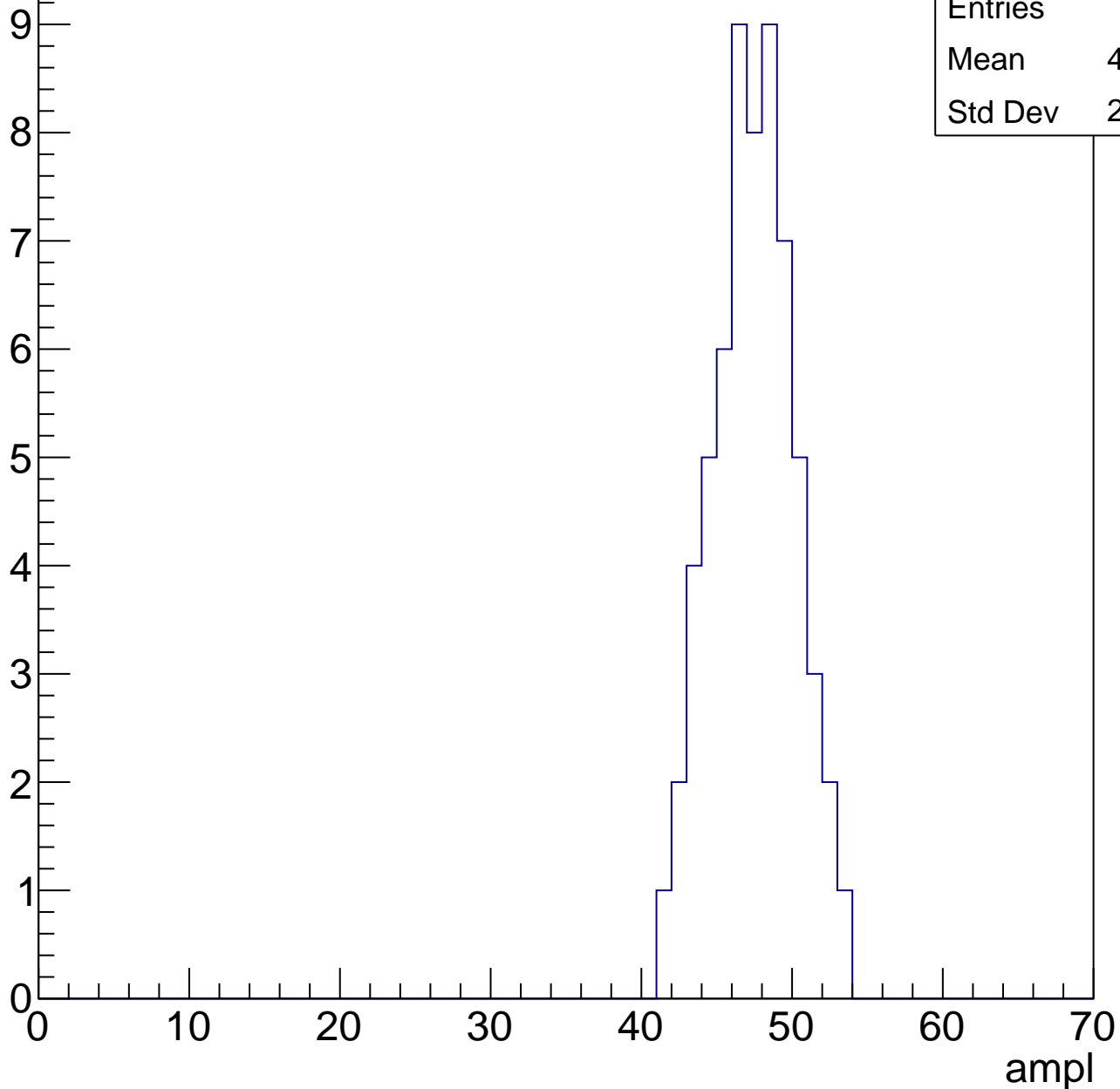
Entry



# B1L000S, U1-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

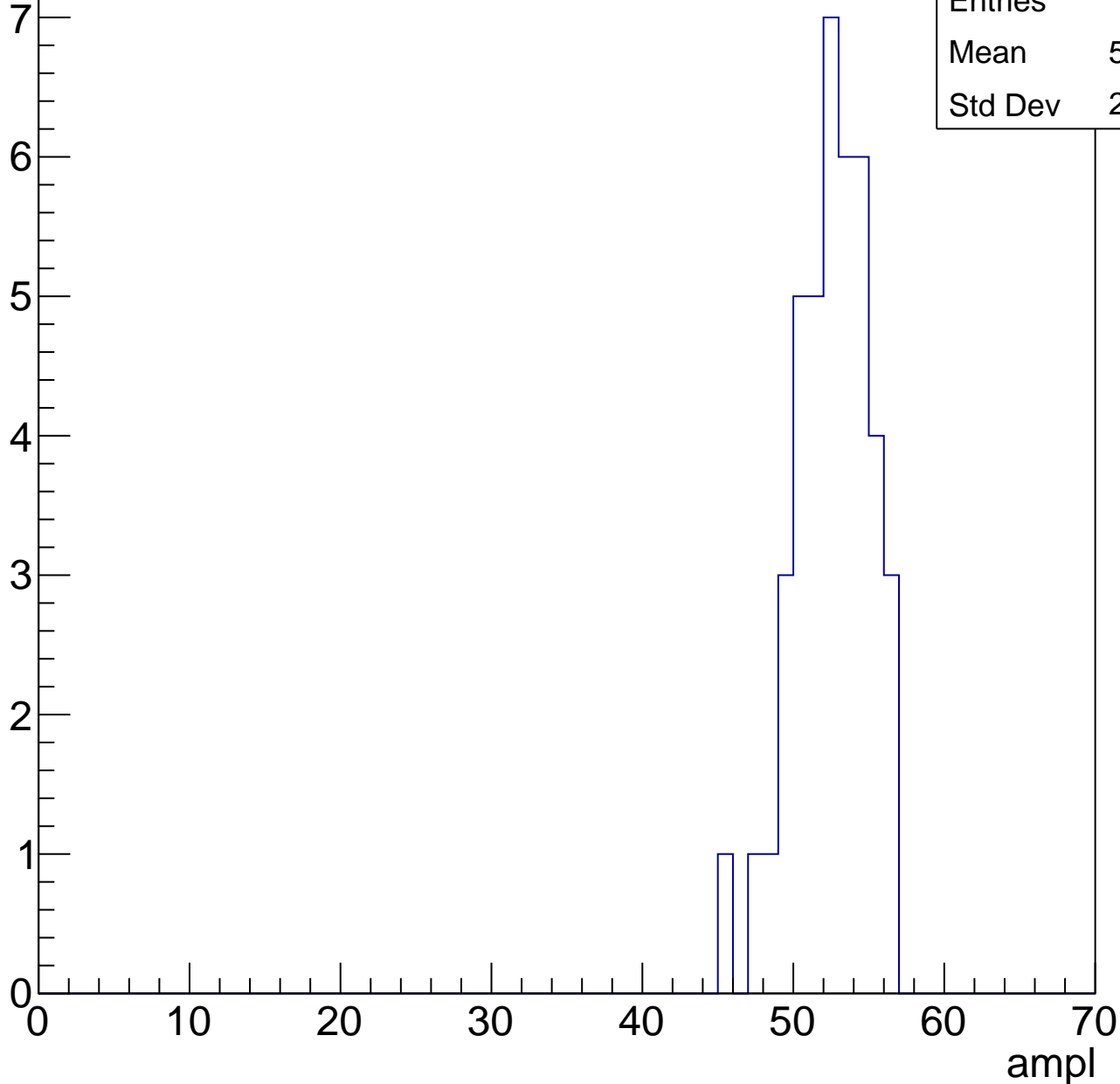


# B1L000S, U1-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

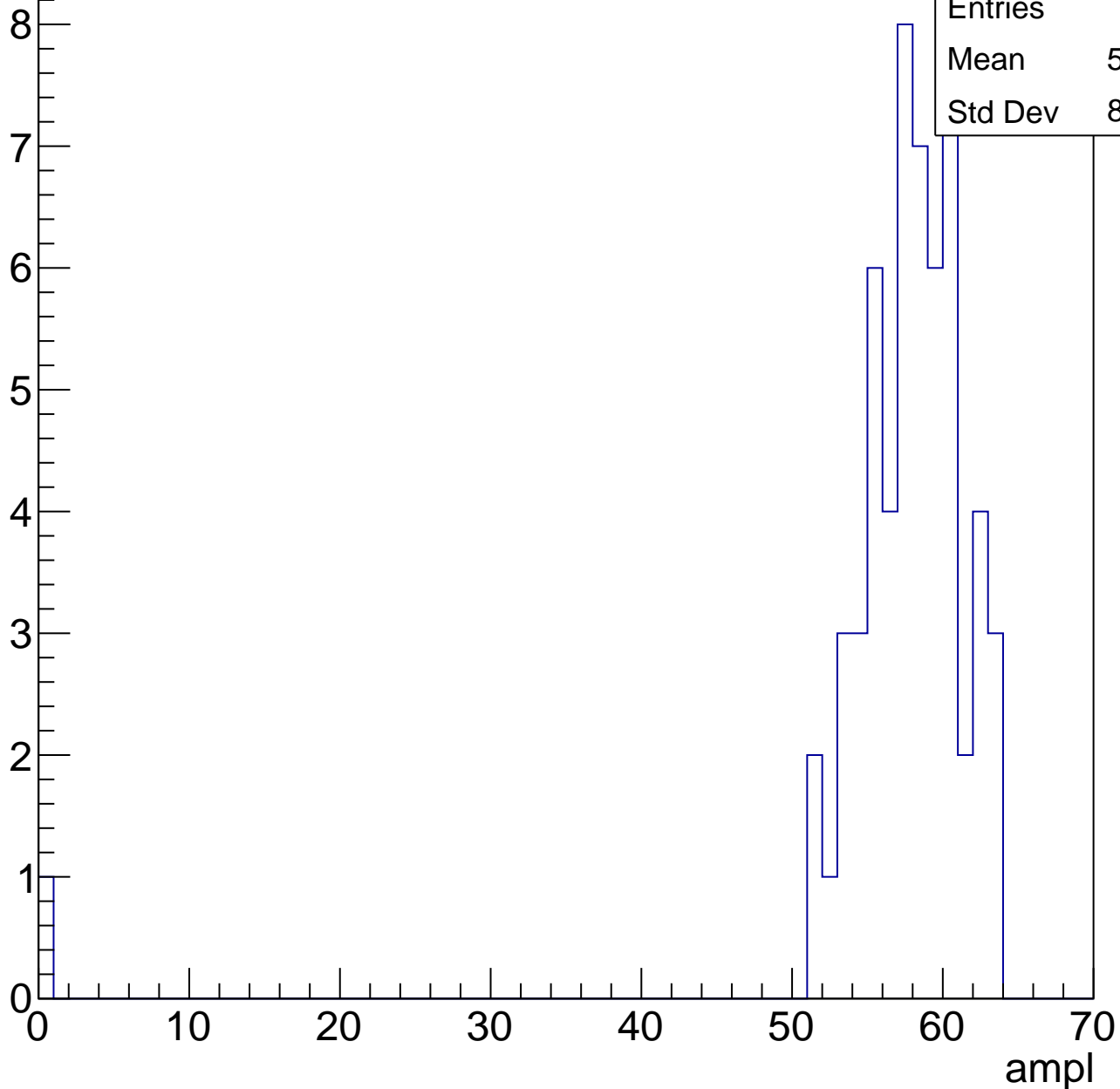
Entries	42
Mean	52.05
Std Dev	2.468



# B1L000S, U1-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

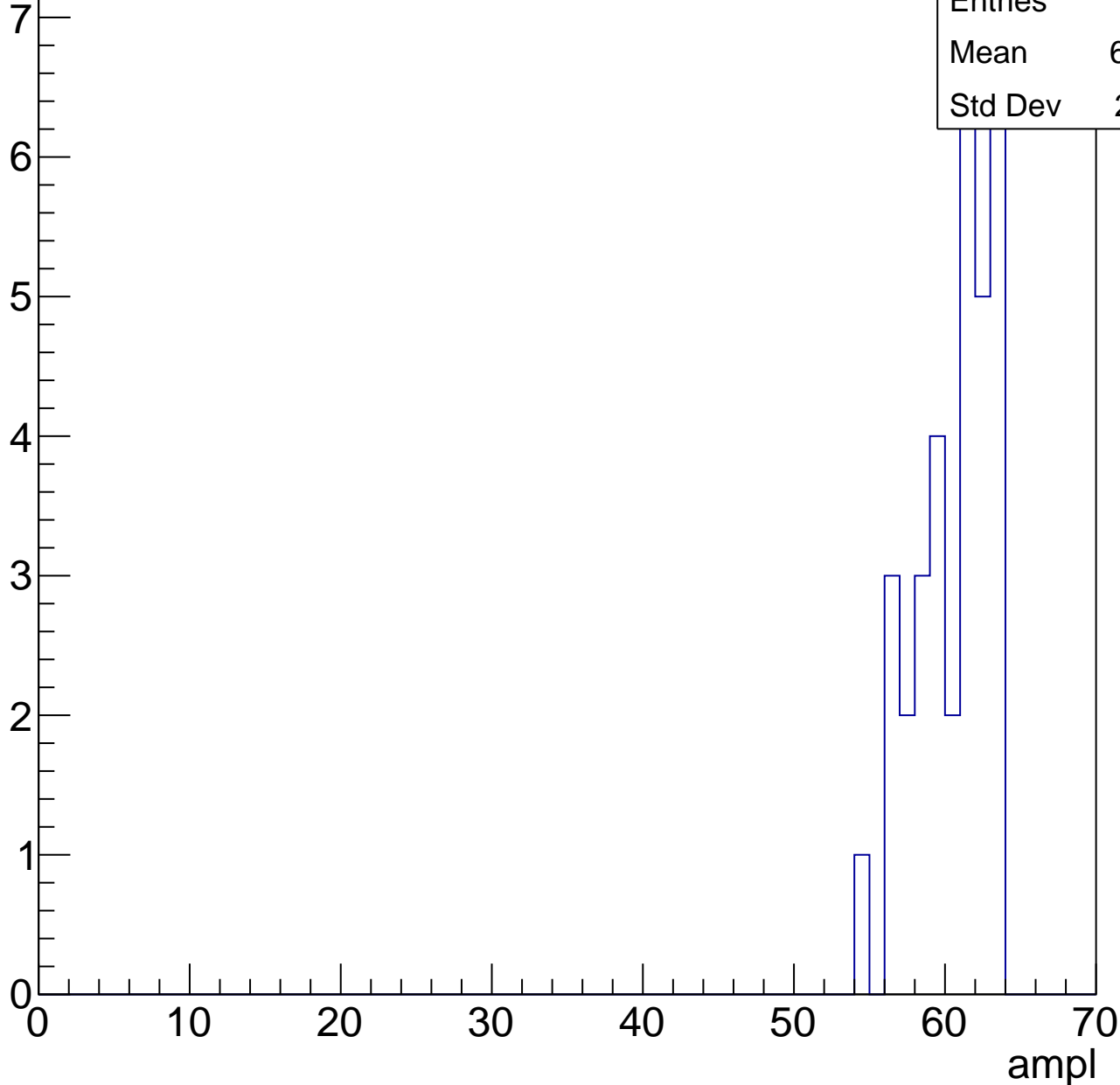


# B1L000S, U1-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	60.12
Std Dev	2.471

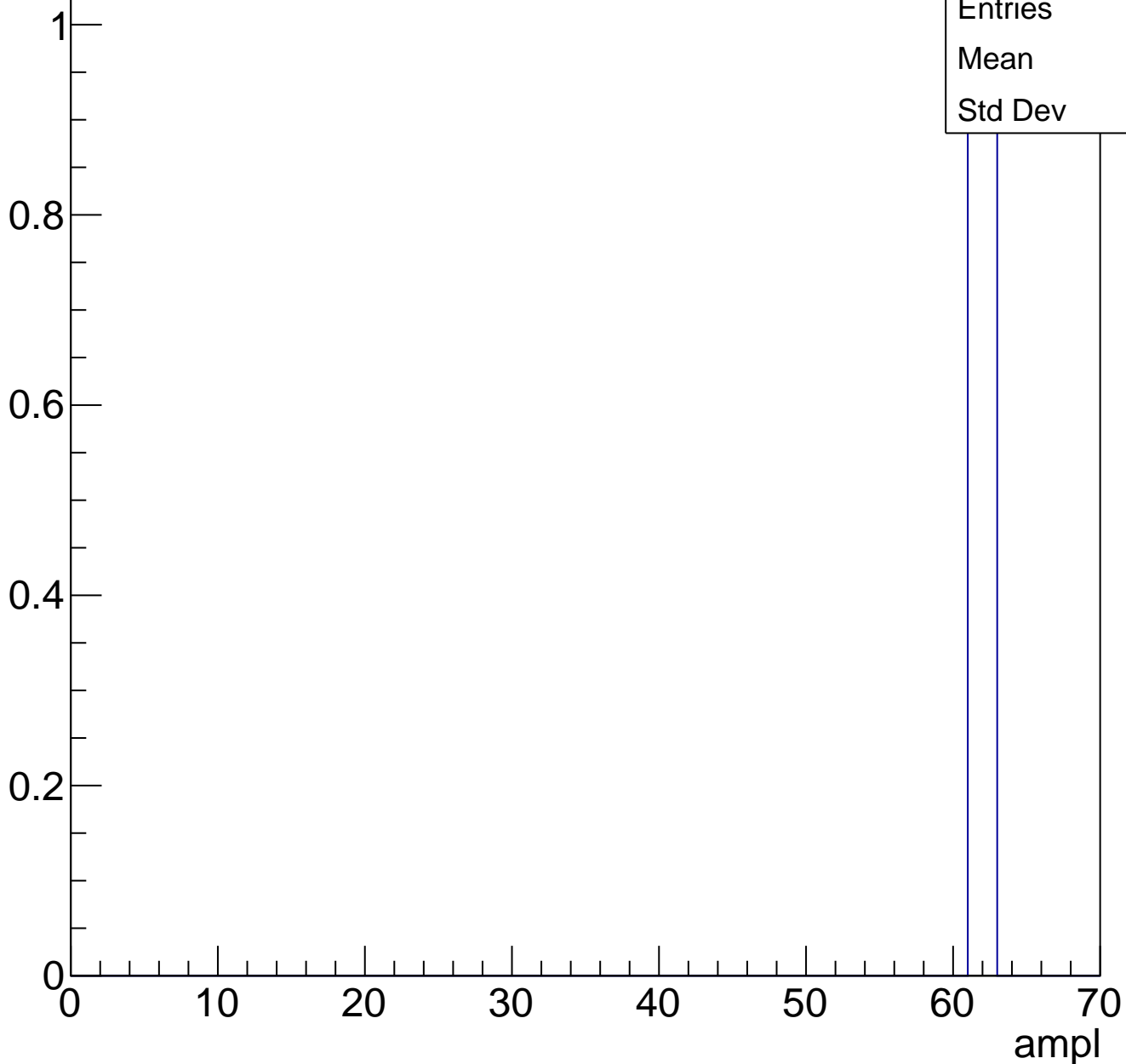




# B1L000S, U1-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch7, adc0

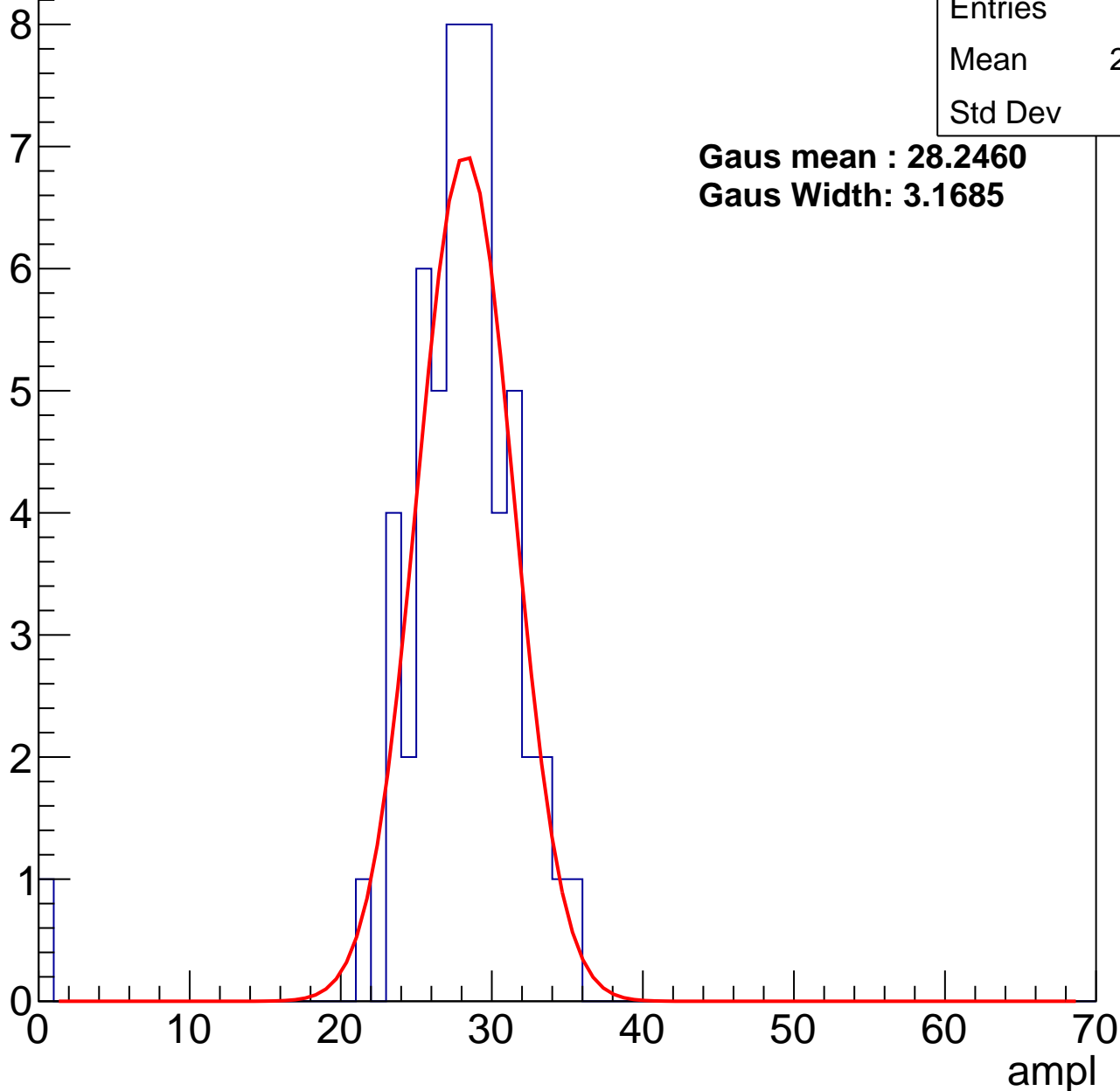
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	27.36
Std Dev	4.66

**Gaus mean : 28.2460**

**Gaus Width: 3.1685**



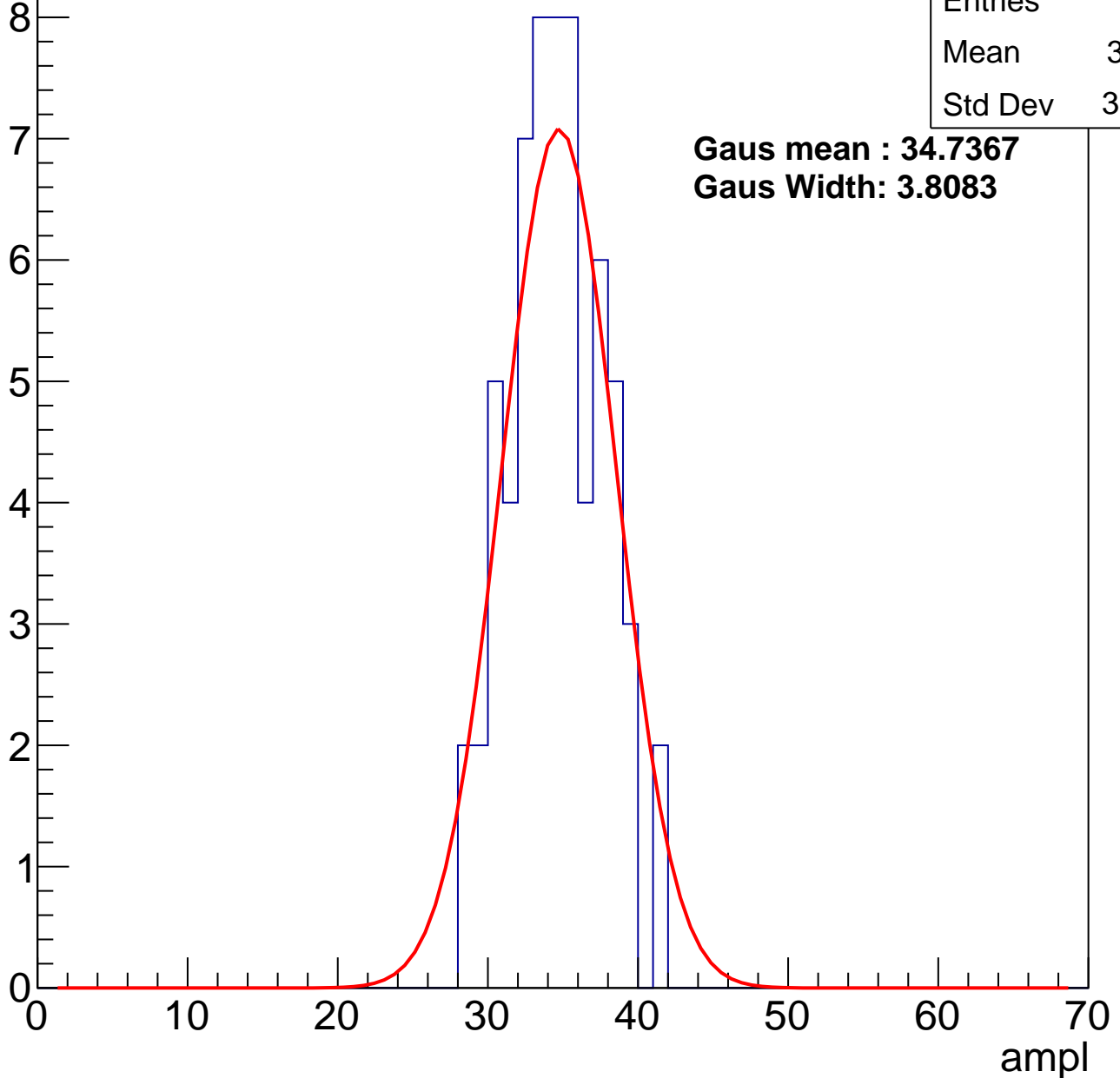
# B1L000S, U1-ch7, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.11
Std Dev	3.073

**Gaus mean : 34.7367**  
**Gaus Width: 3.8083**



# B1L000S, U1-ch7, adc2

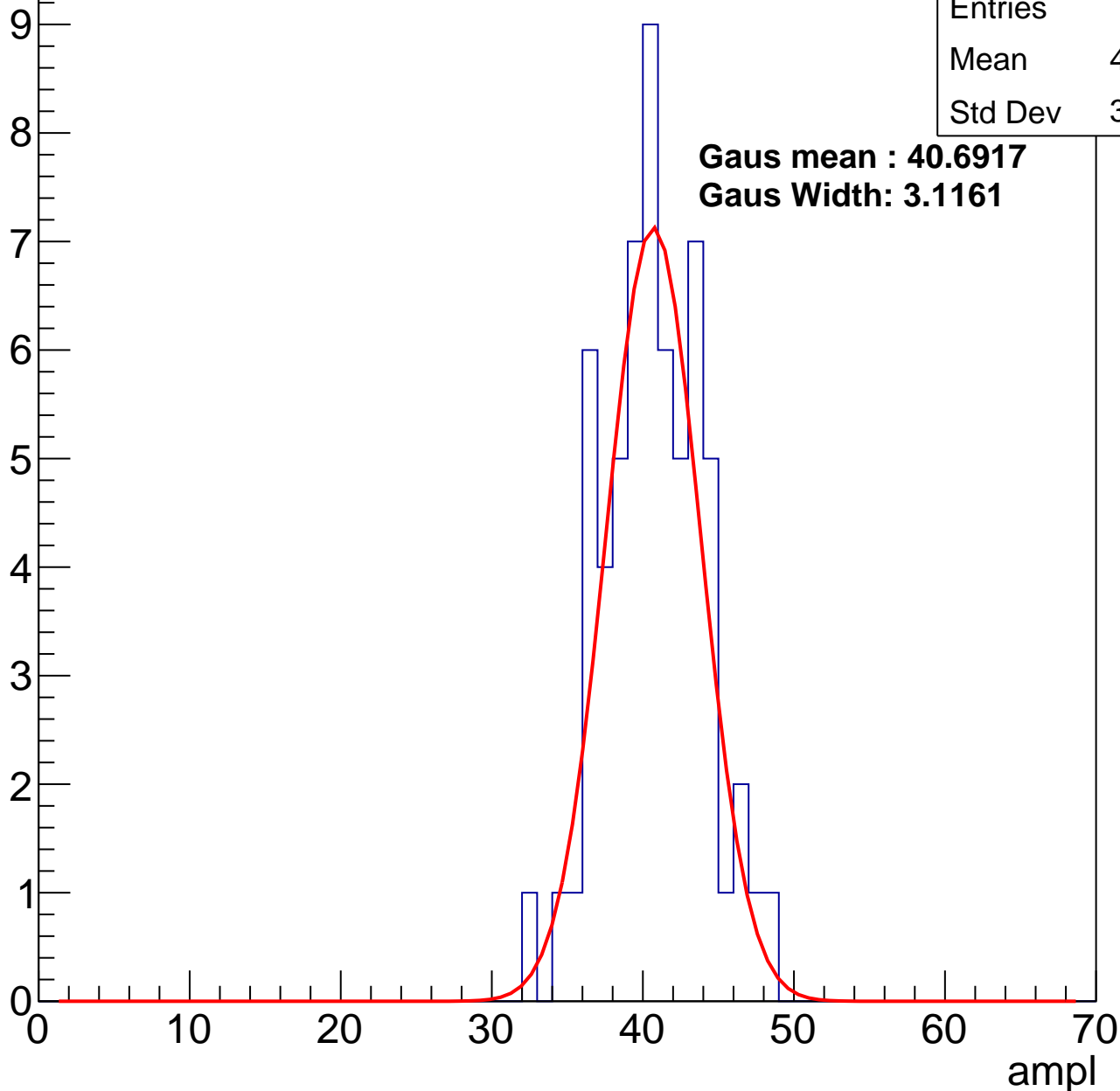
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.27
Std Dev	3.259

**Gaus mean : 40.6917**

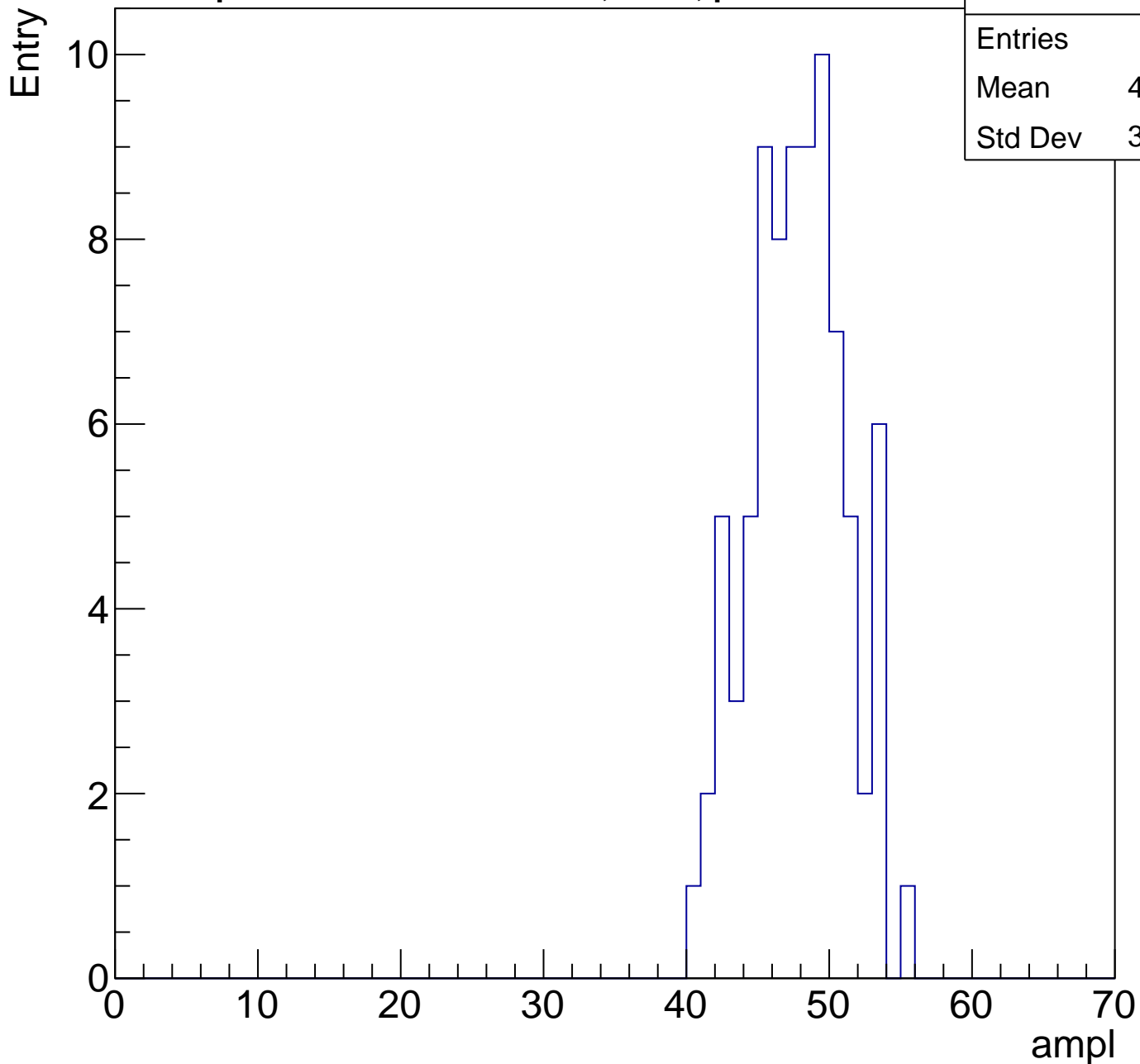
**Gaus Width: 3.1161**



# B1L000S, U1-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	82
Mean	47.33
Std Dev	3.306

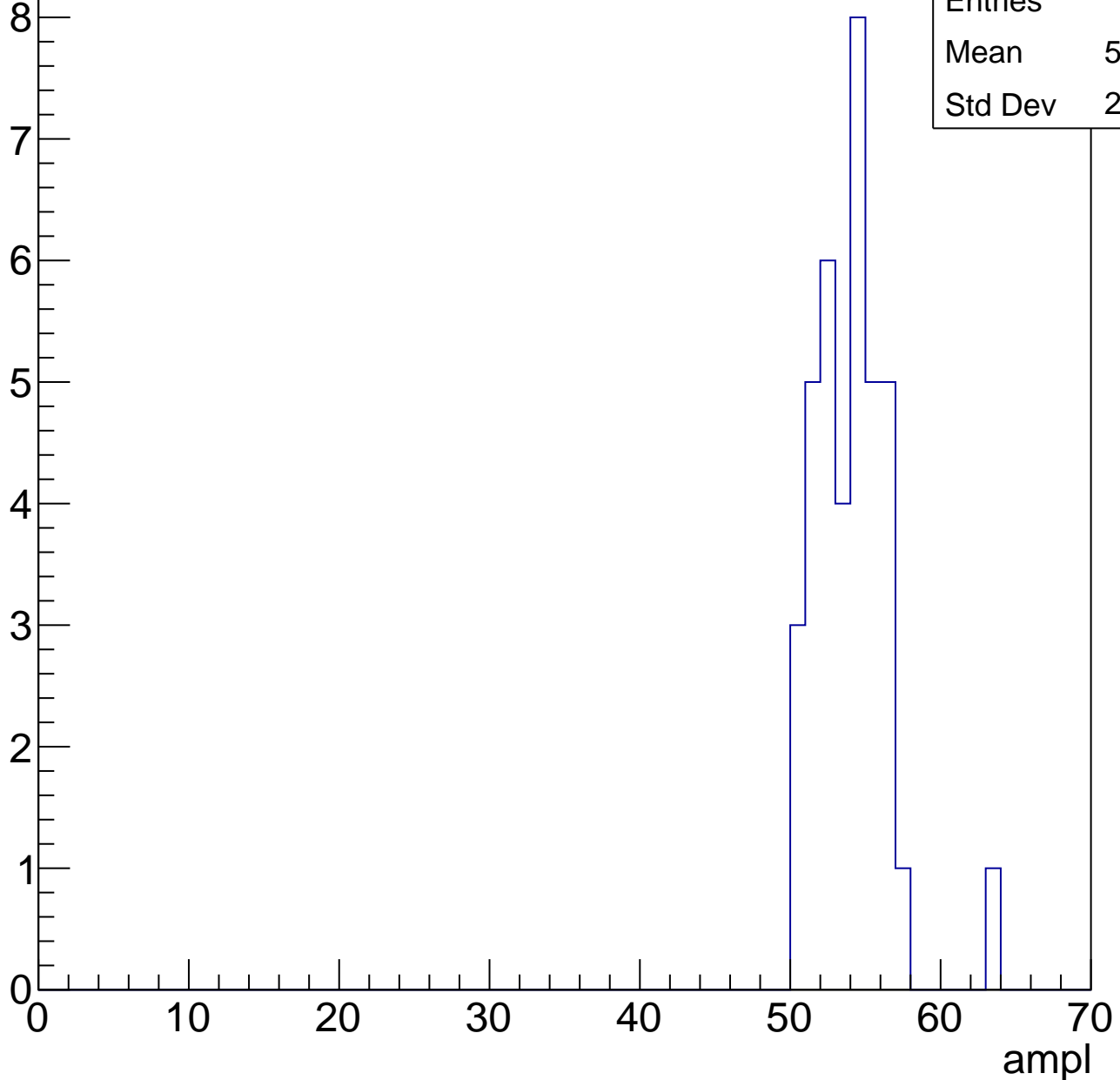


# B1L000S, U1-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

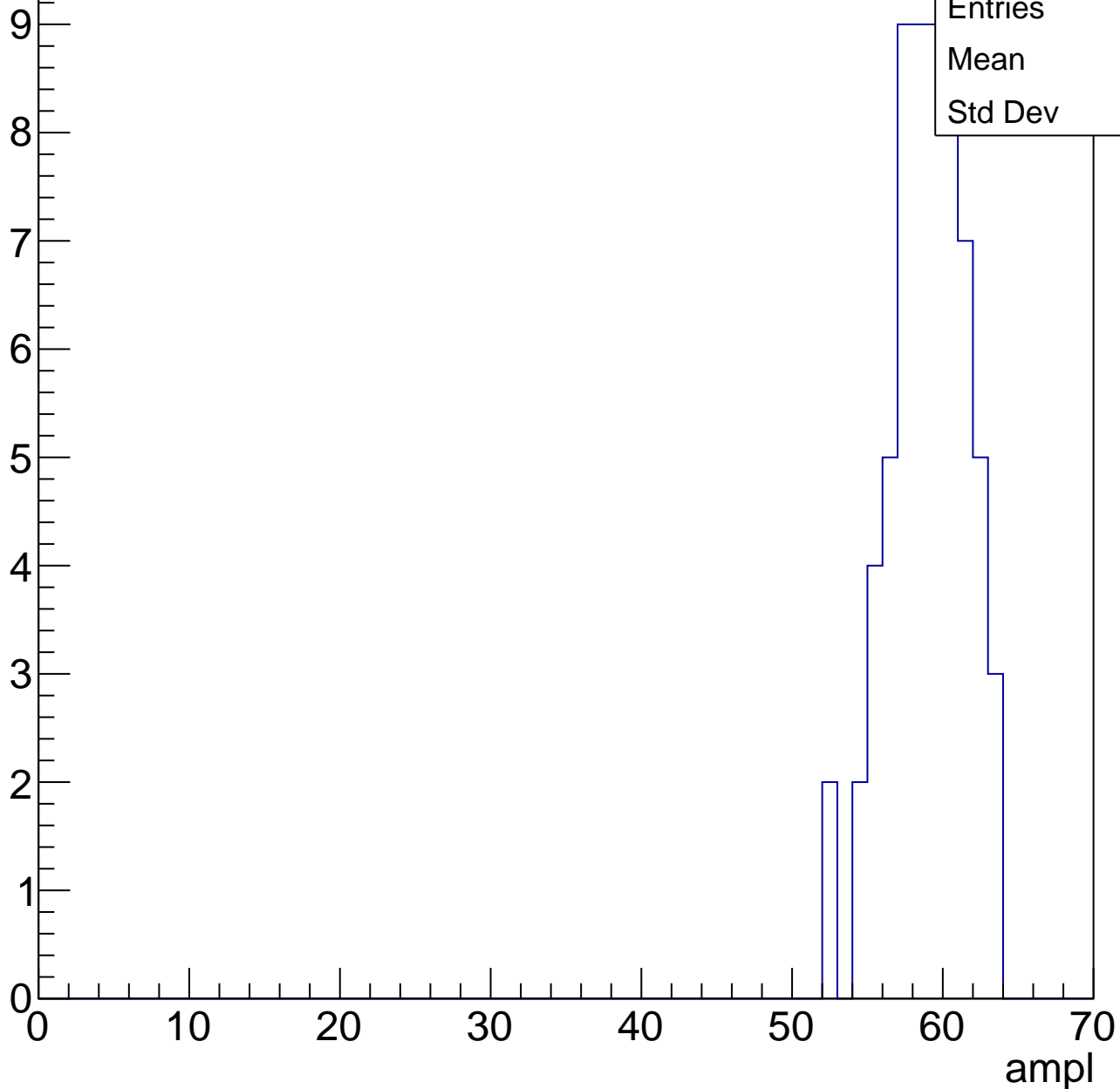
Entries	38
Mean	53.58
Std Dev	2.456



# B1L000S, U1-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



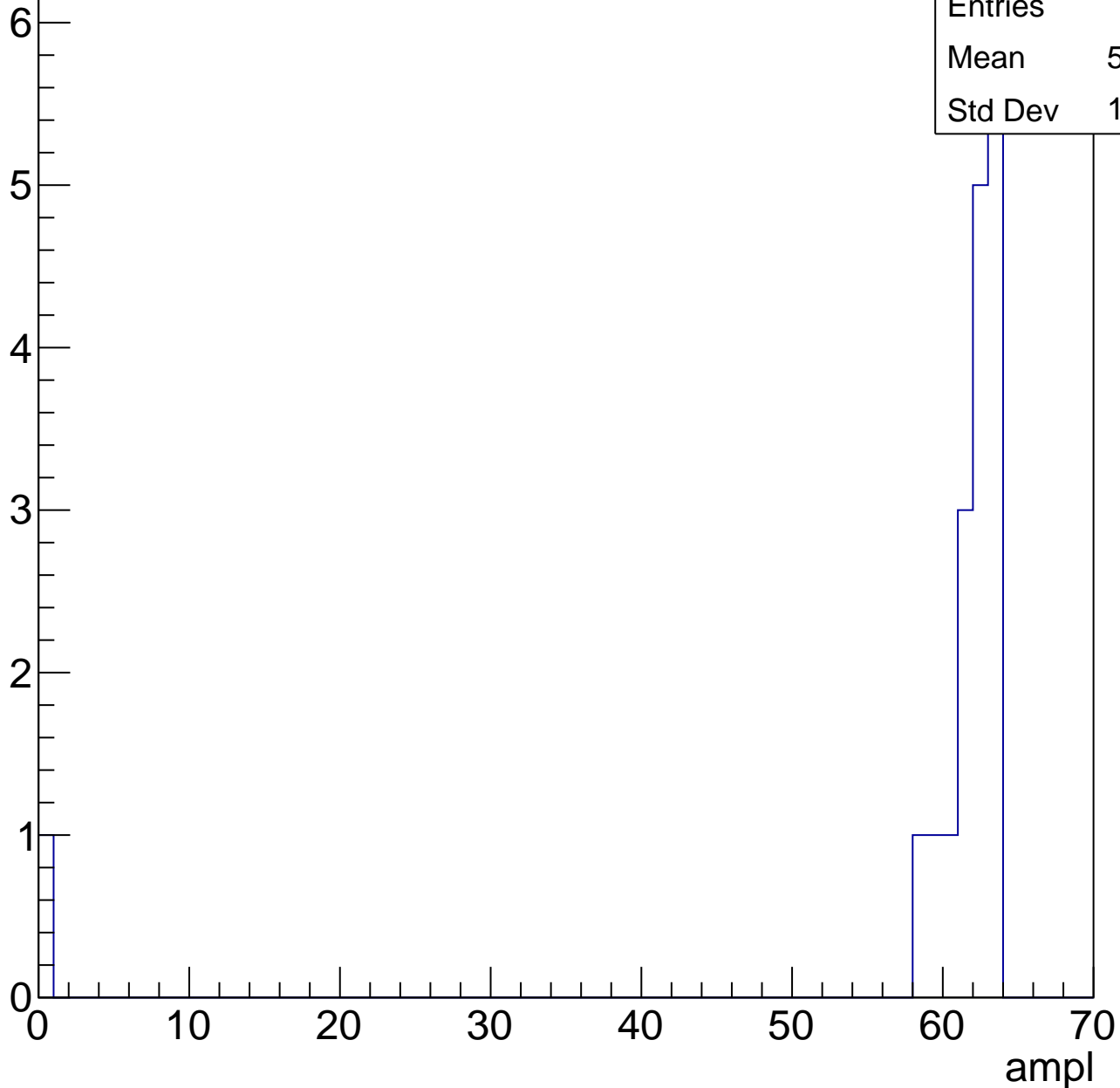
Entries	64
Mean	58.5
Std Dev	2.55

# B1L000S, U1-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.22
Std Dev	14.19





# B1L000S, U1-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U1-ch8, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	28.74
Std Dev	3.564

**Gaus mean : 29.3159**

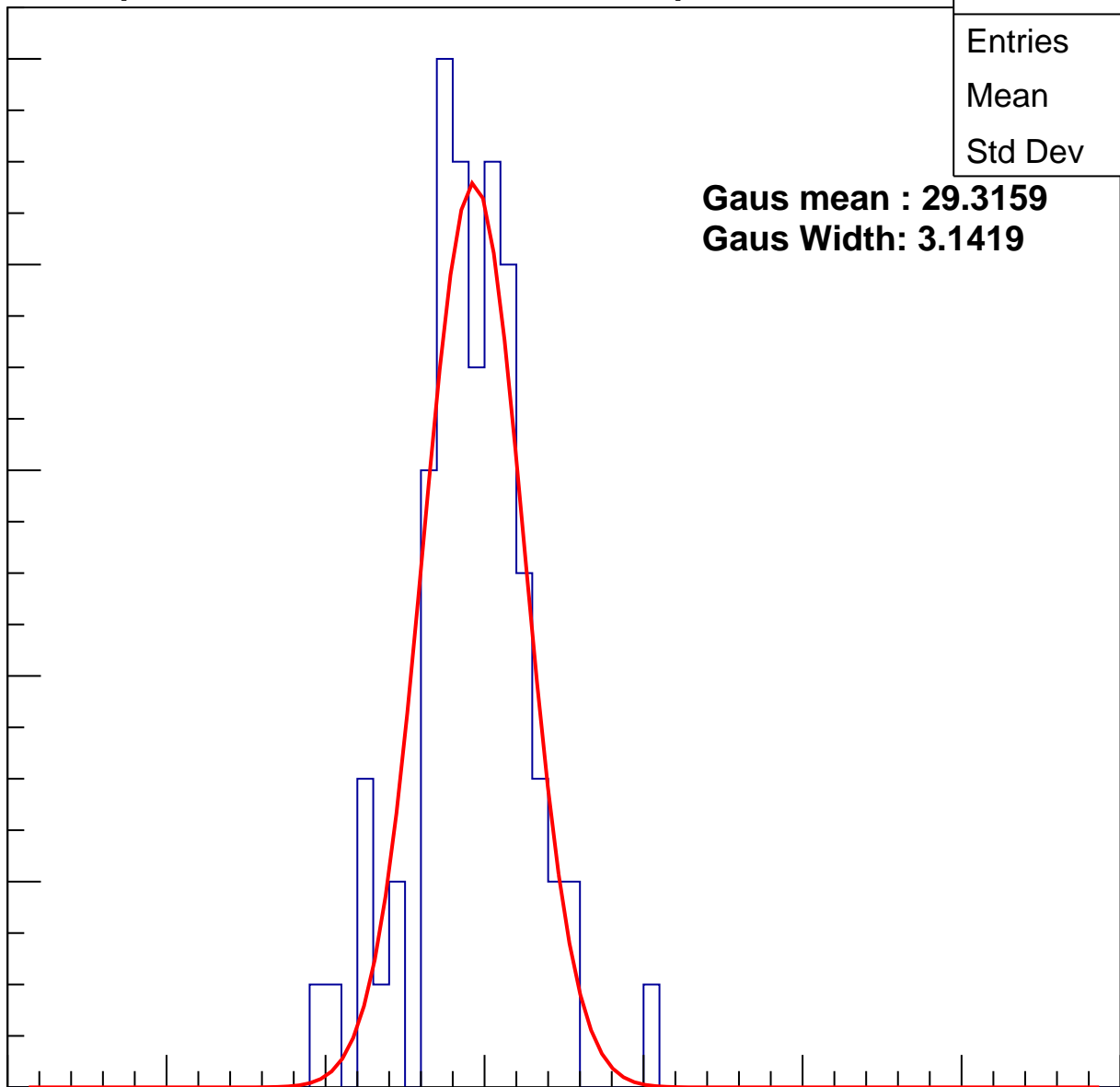
**Gaus Width: 3.1419**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch8, adc1

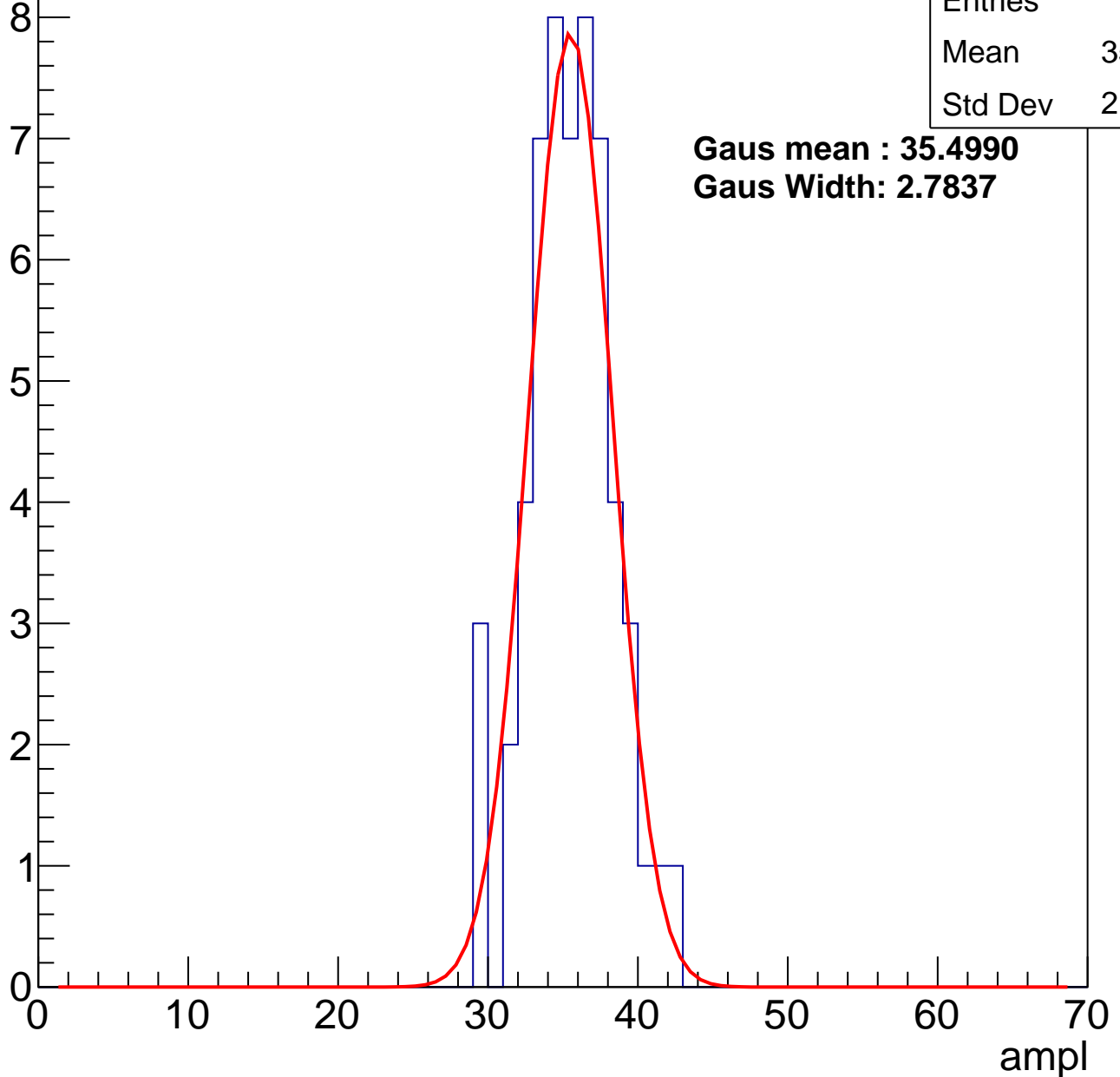
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.07
Std Dev	2.809

**Gaus mean : 35.4990**

**Gaus Width: 2.7837**



# B1L000S, U1-ch8, adc2

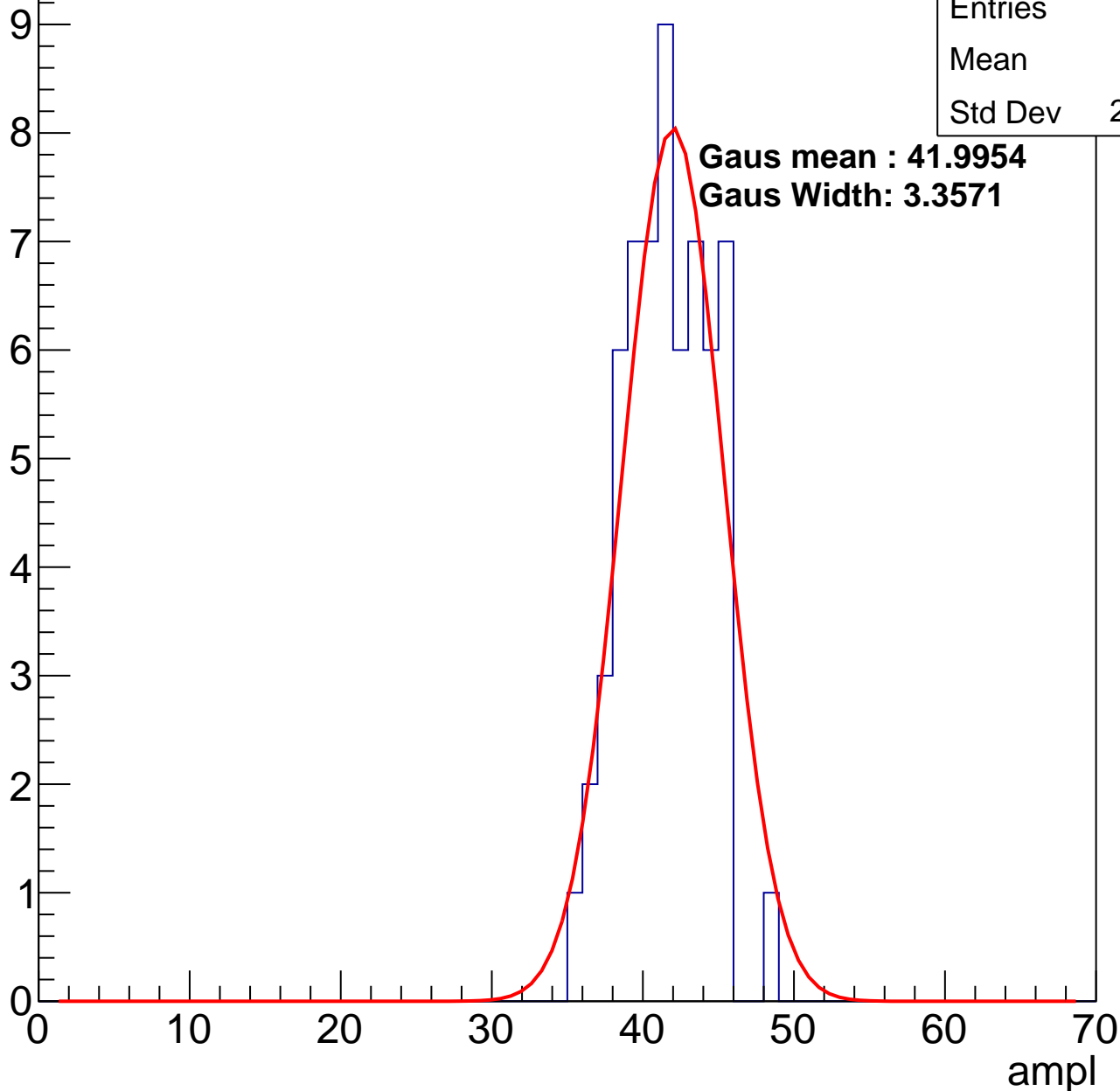
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.1
Std Dev	2.757

**Gaus mean : 41.9954**

**Gaus Width: 3.3571**

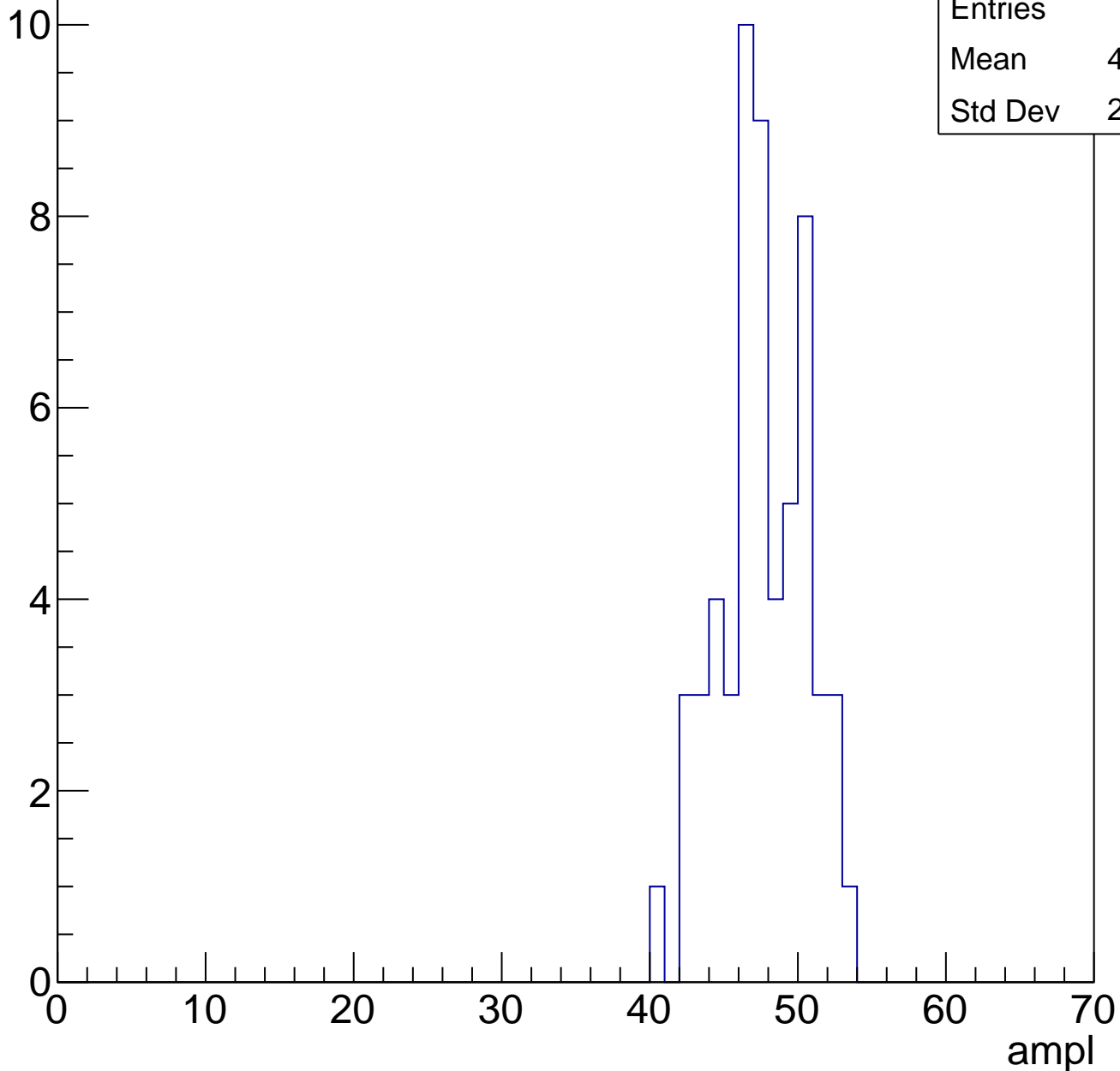


# B1L000S, U1-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	57
Mean	47.16
Std Dev	2.913

Entry

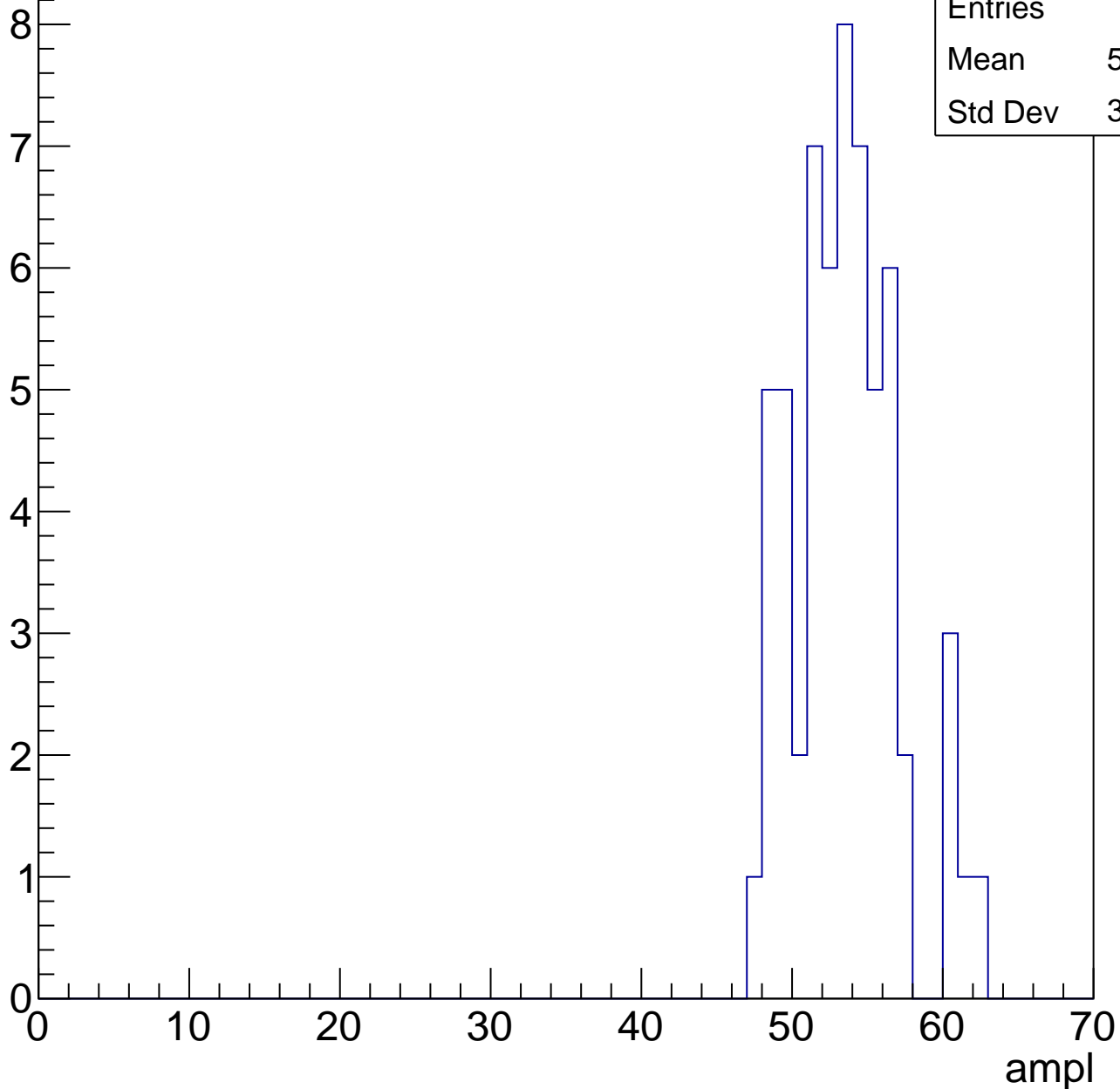


# B1L000S, U1-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

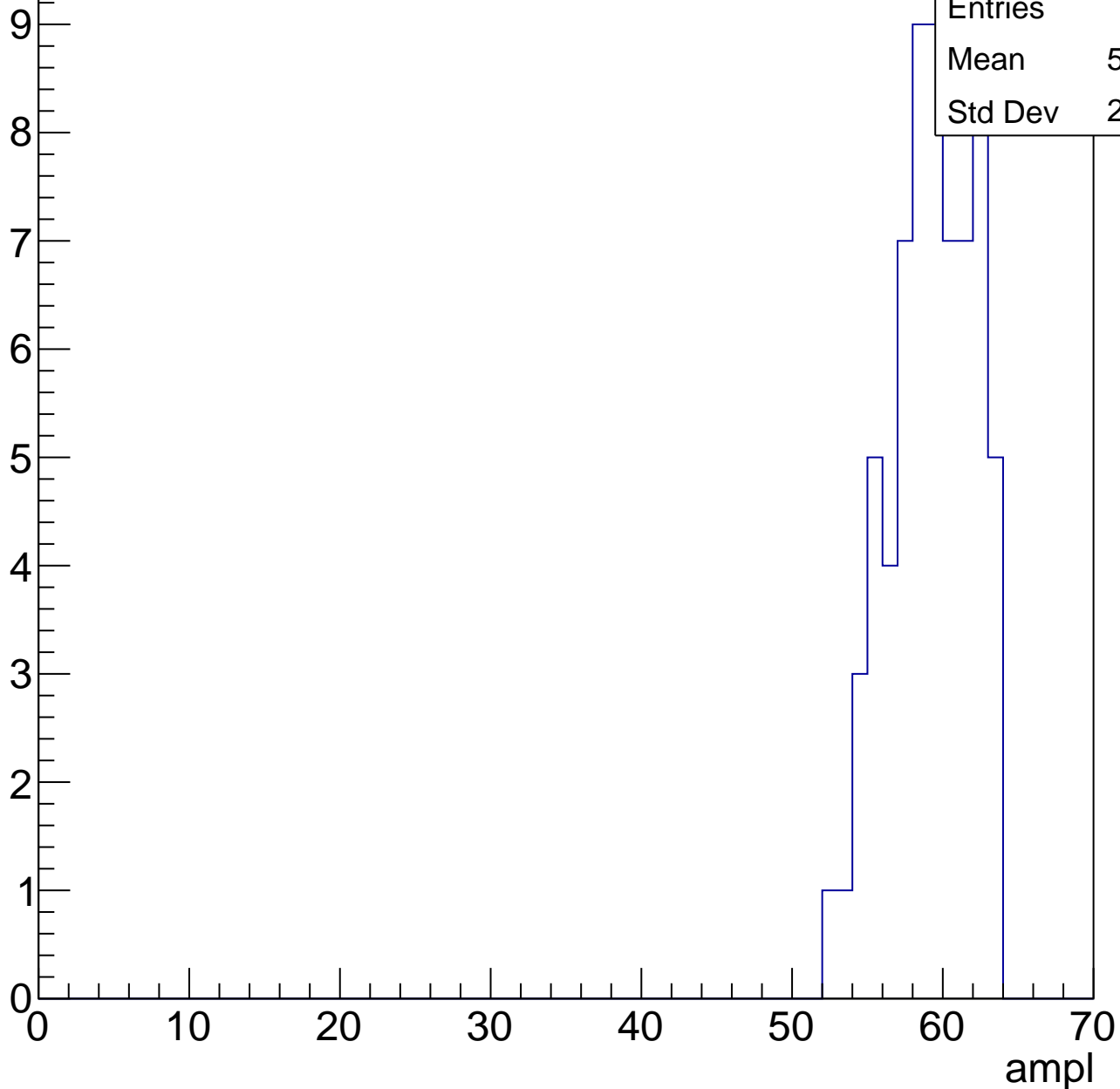
Entries	59
Mean	53.07
Std Dev	3.439



# B1L000S, U1-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



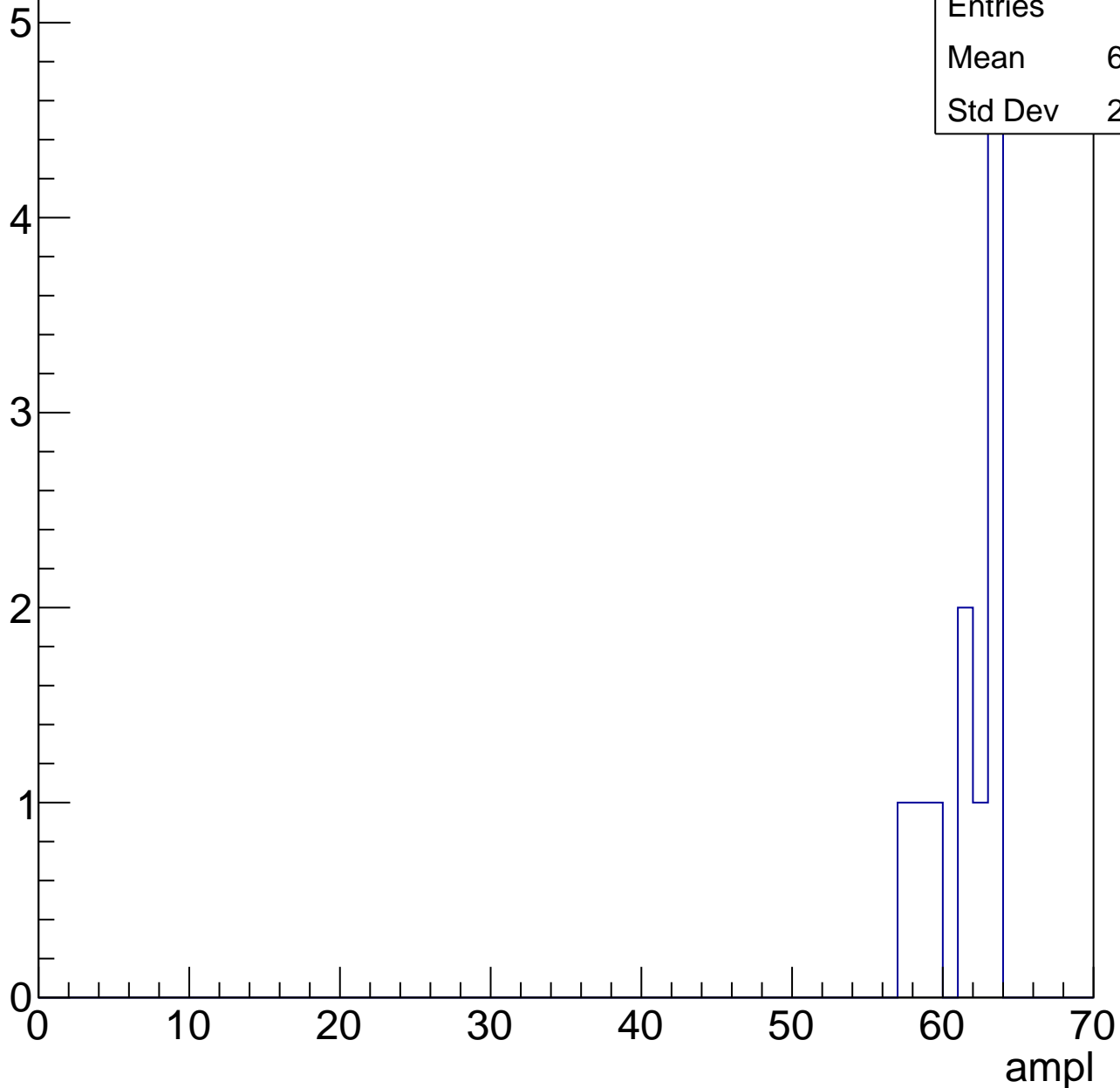
Entries	66
Mean	58.73
Std Dev	2.739

# B1L000S, U1-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	61.18
Std Dev	2.124

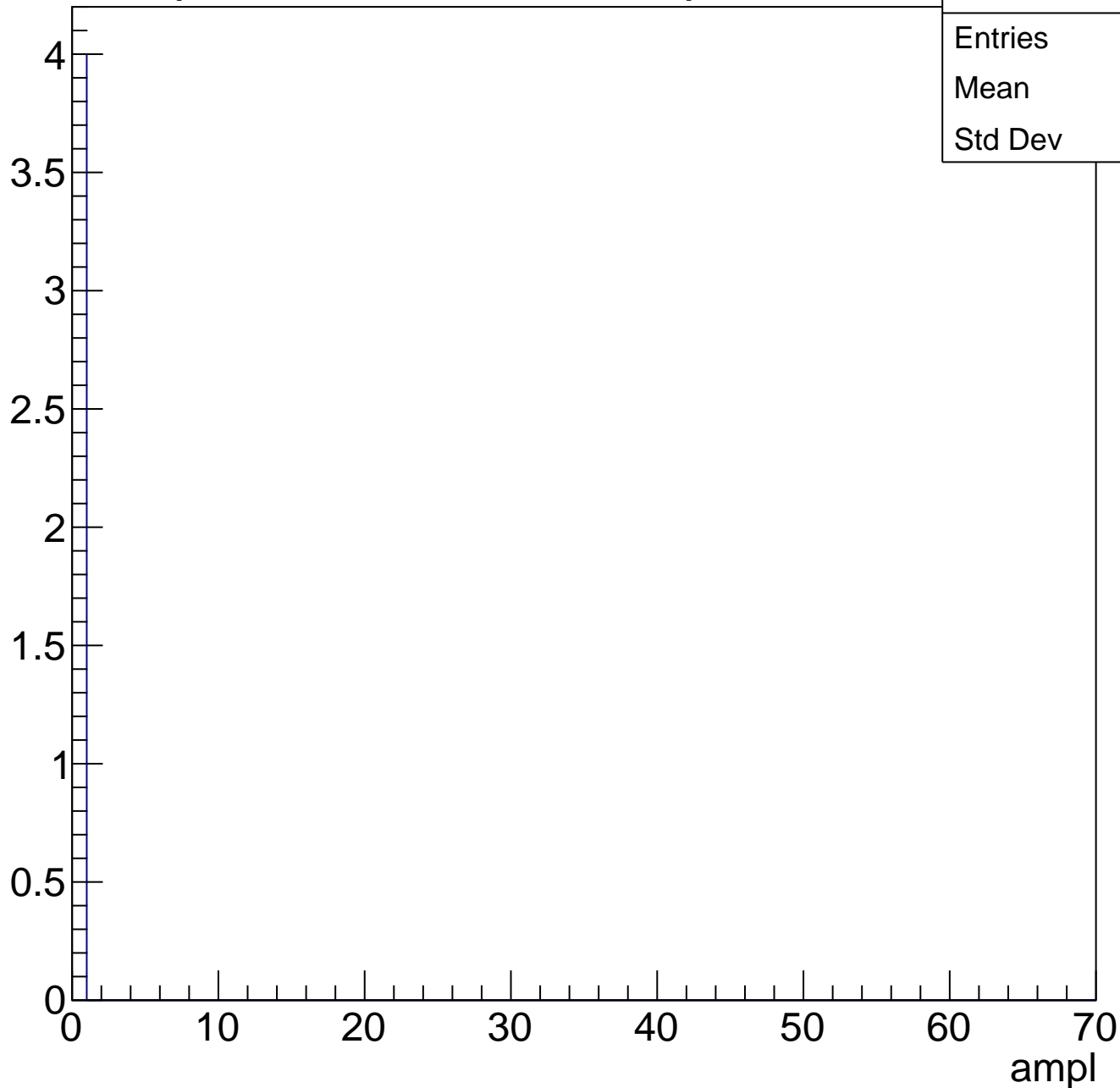




# B1L000S, U1-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch9, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	27.62
Std Dev	3.565

**Gaus mean : 27.7271**

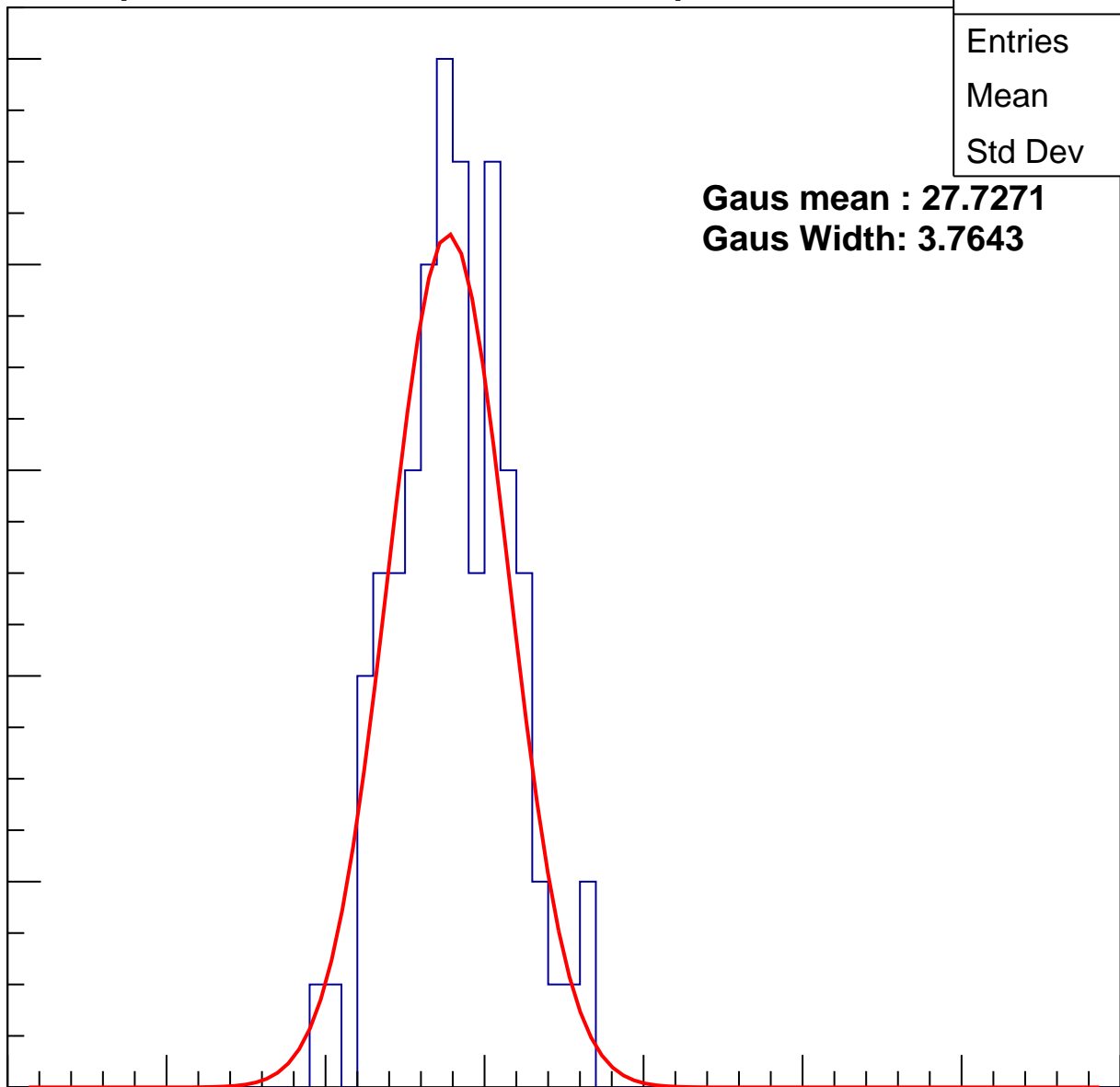
**Gaus Width: 3.7643**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch9, adc1

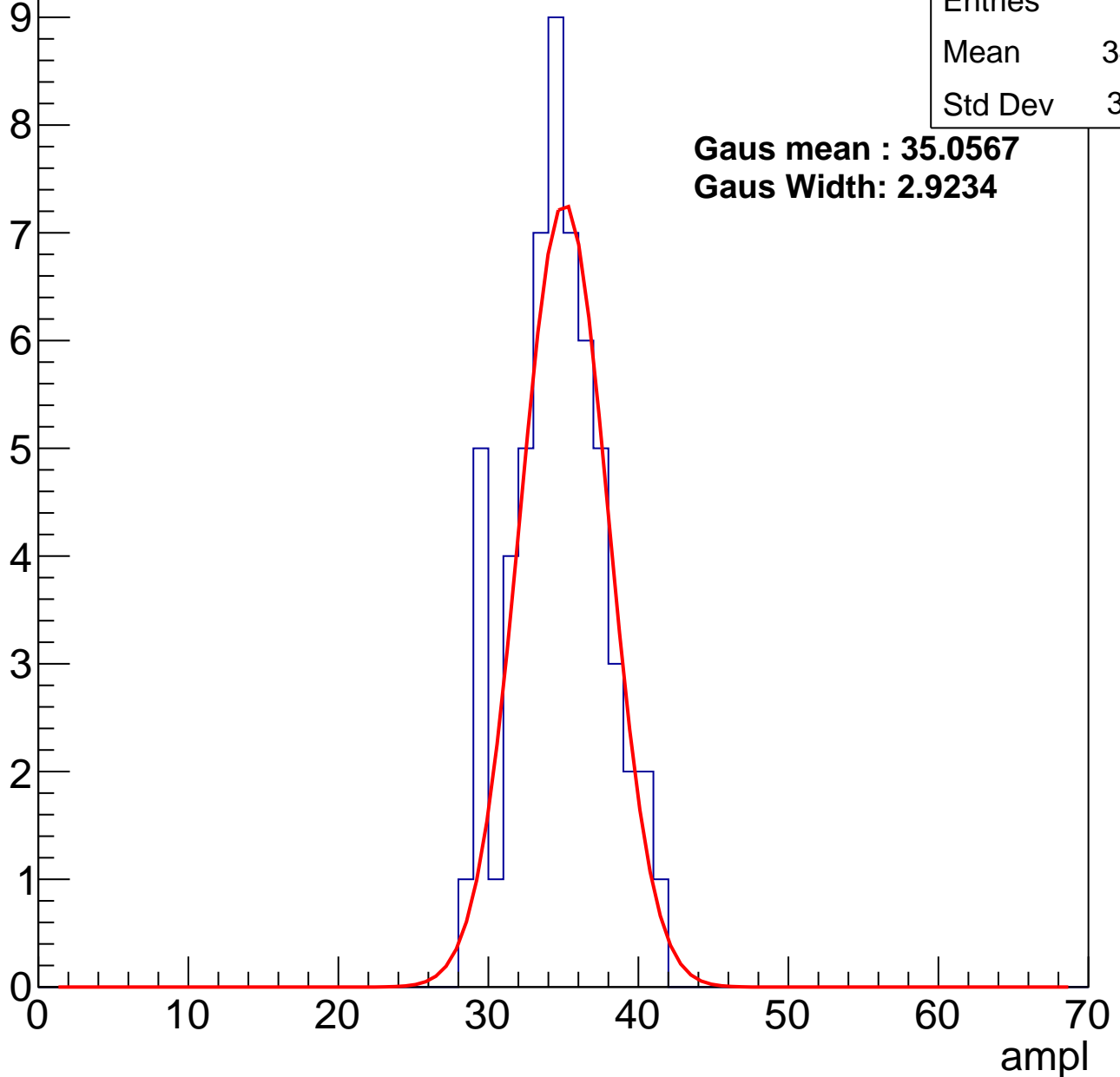
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.19
Std Dev	3.031

**Gaus mean : 35.0567**

**Gaus Width: 2.9234**



# B1L000S, U1-ch9, adc2

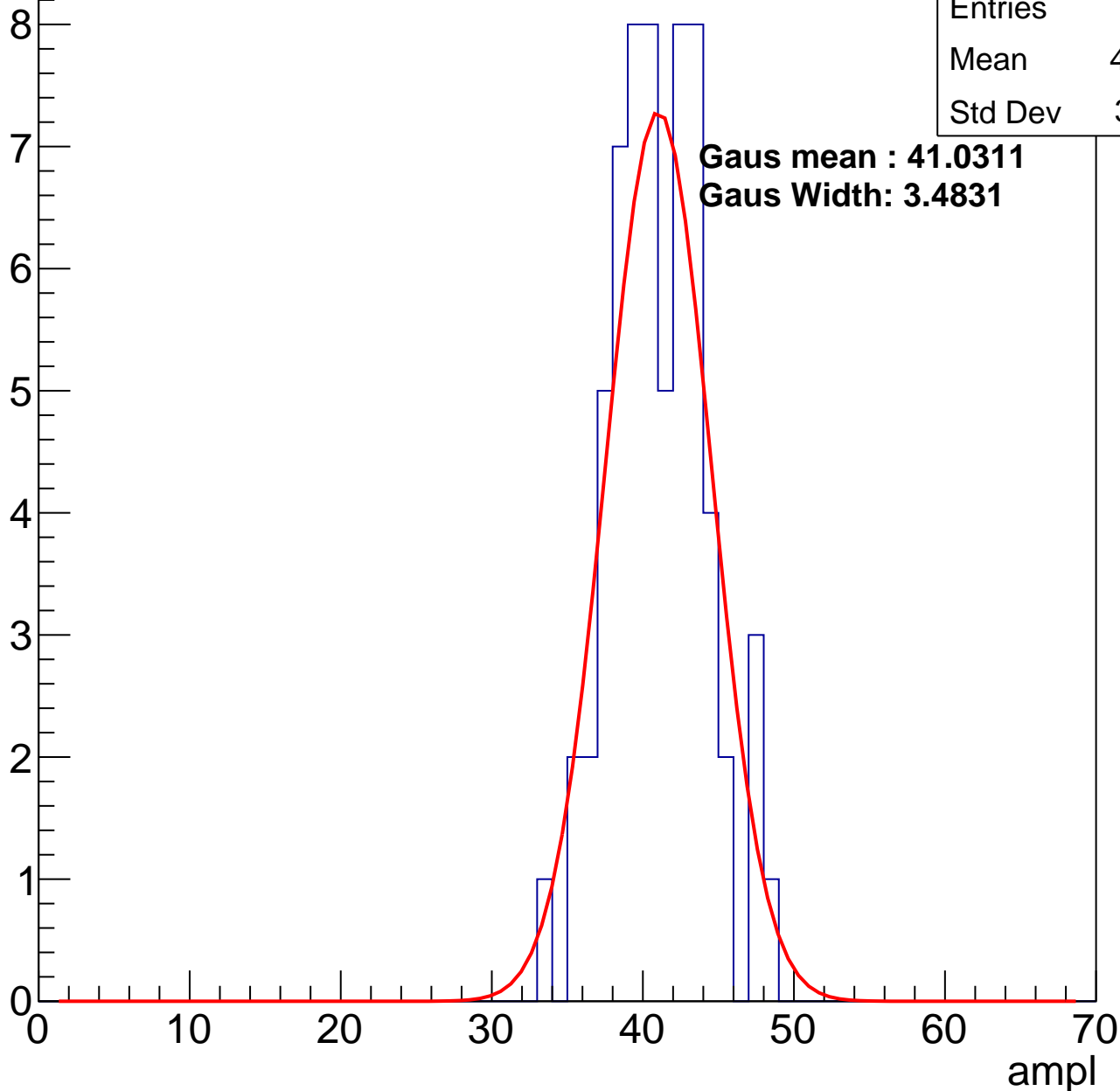
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	40.59
Std Dev	3.121

**Gaus mean : 41.0311**

**Gaus Width: 3.4831**

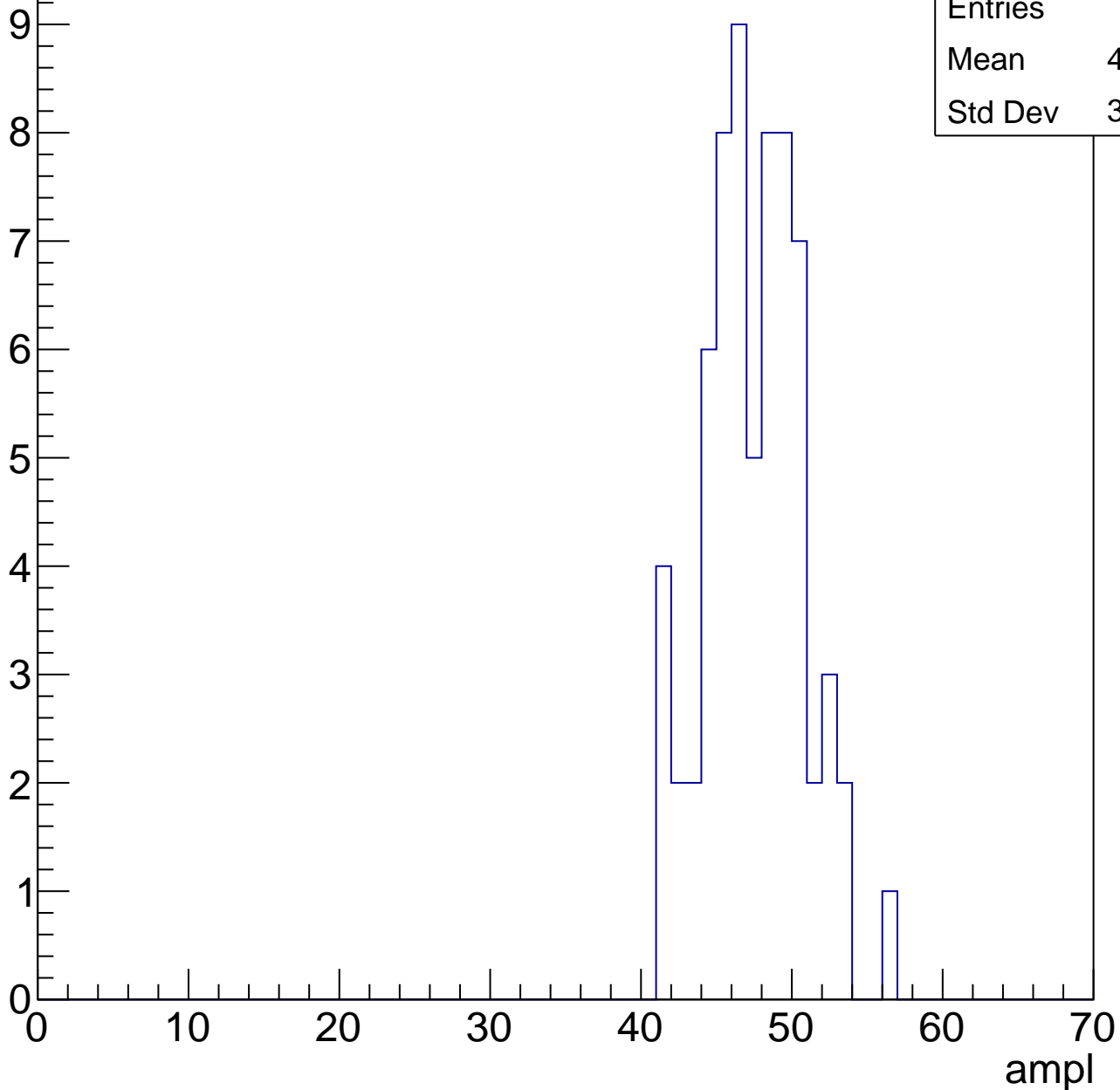


# B1L000S, U1-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

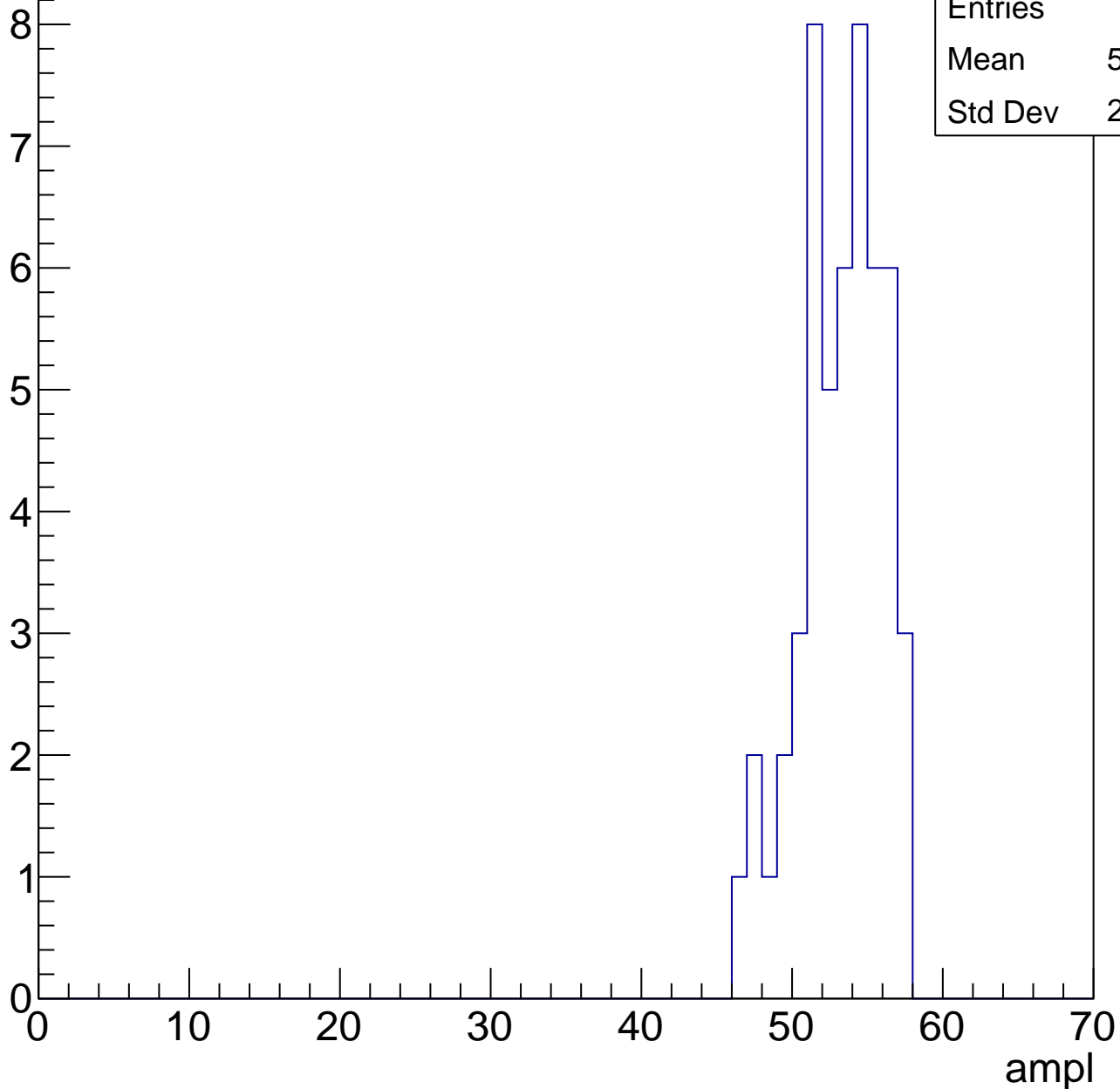
Entries	67
Mean	47.06
Std Dev	3.195



# B1L000S, U1-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

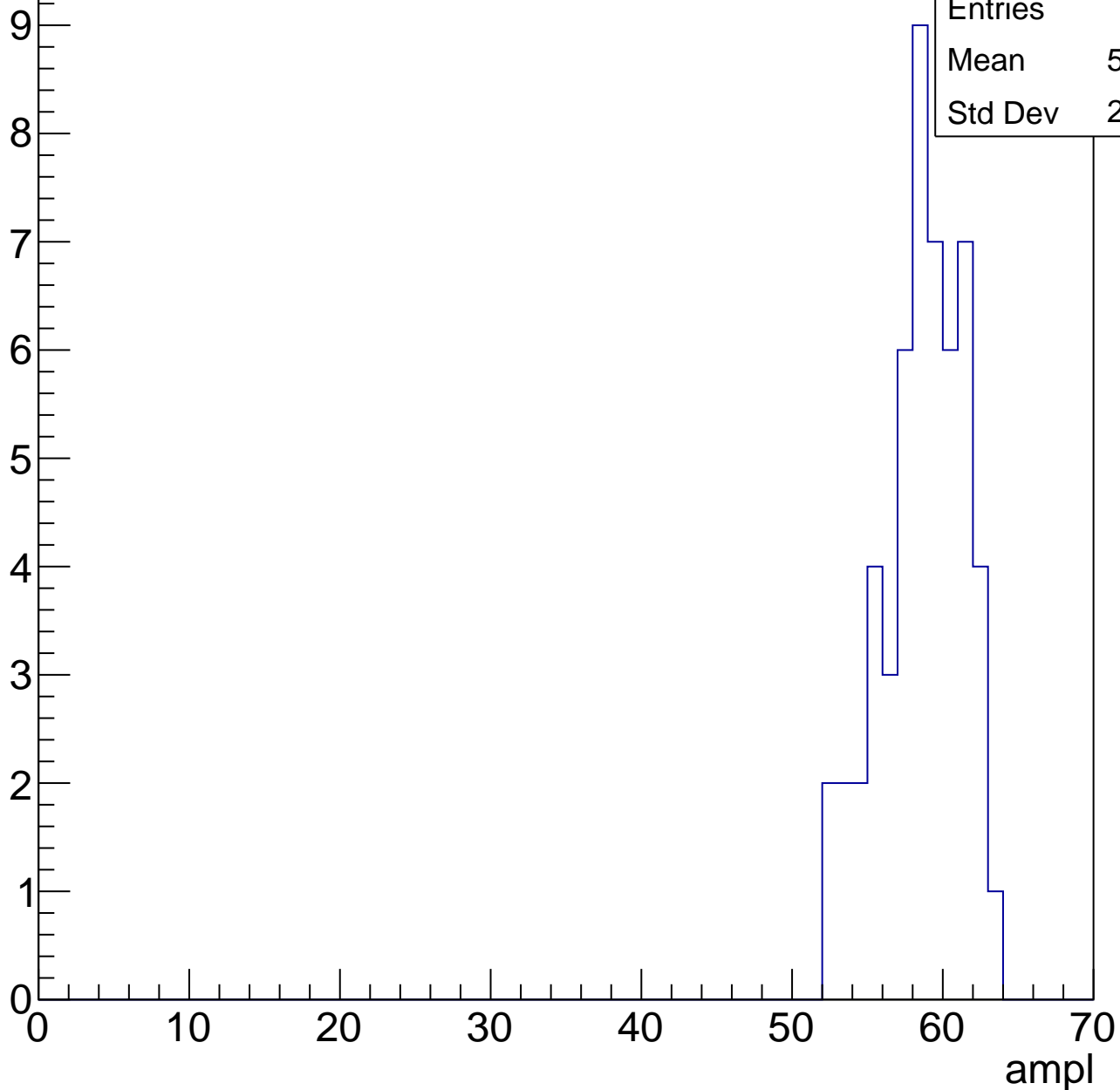
Entry



# B1L000S, U1-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

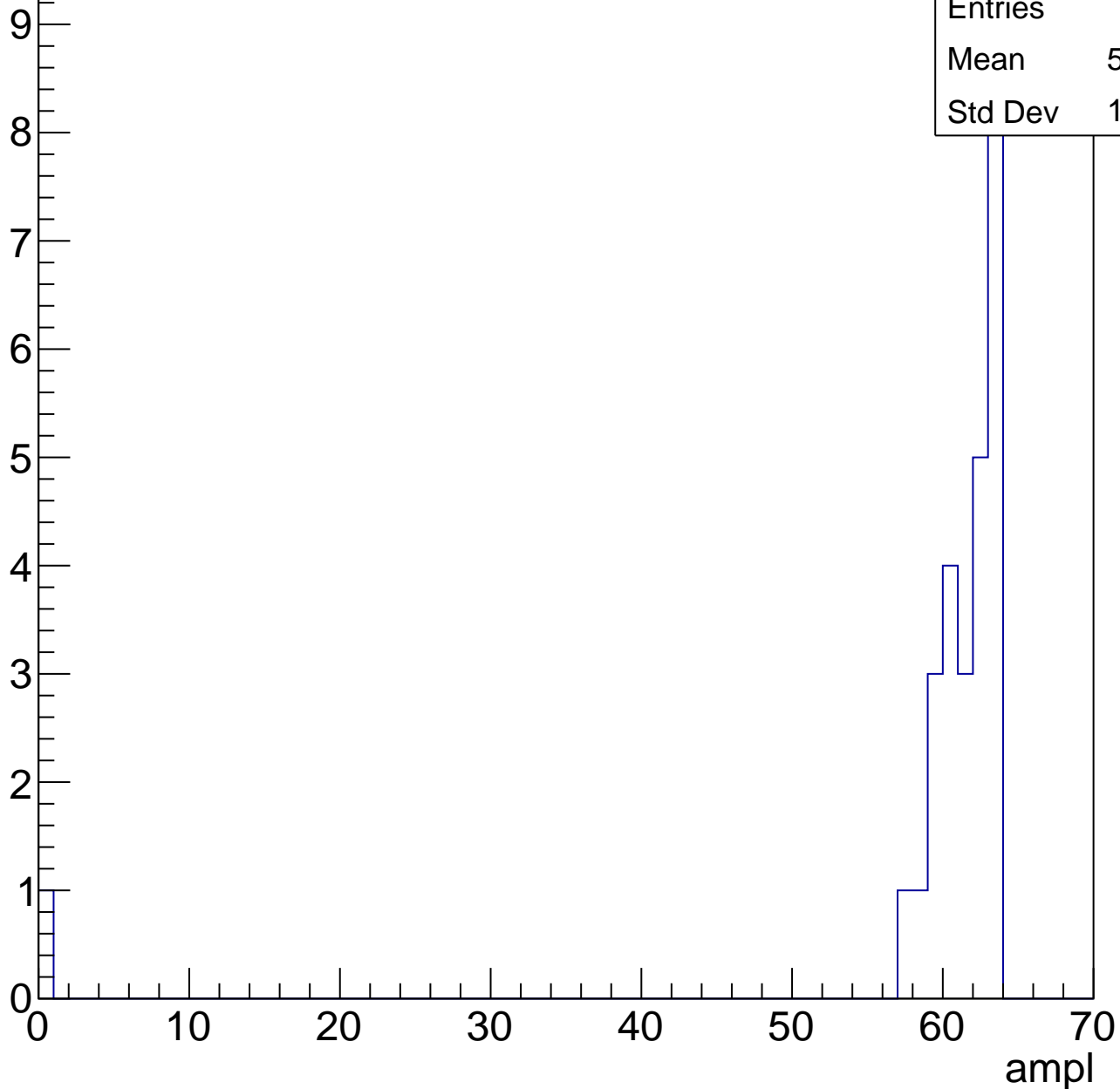


Entries	53
Mean	58.13
Std Dev	2.713

# B1L000S, U1-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

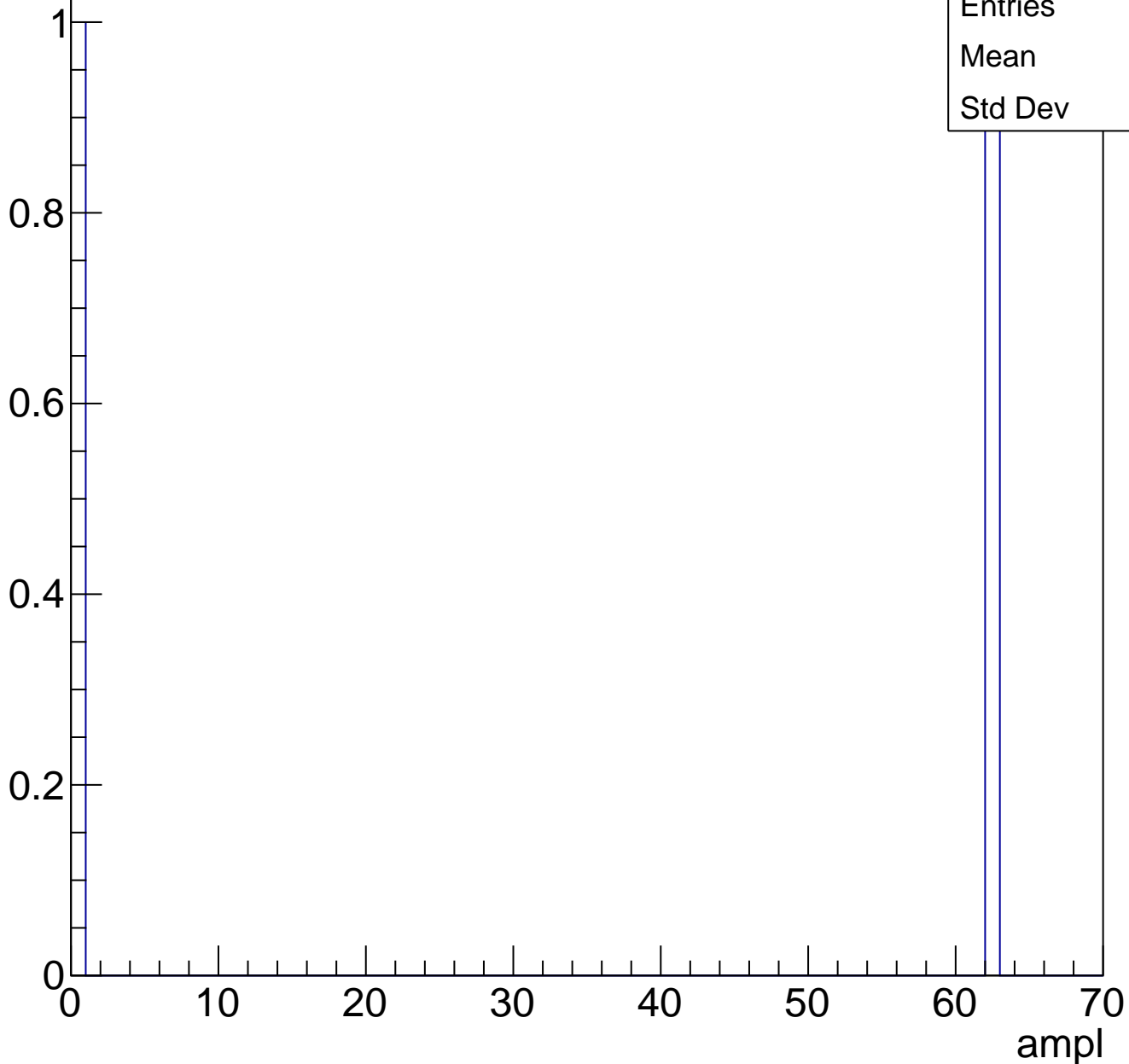




# B1L000S, U1-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch10, adc0

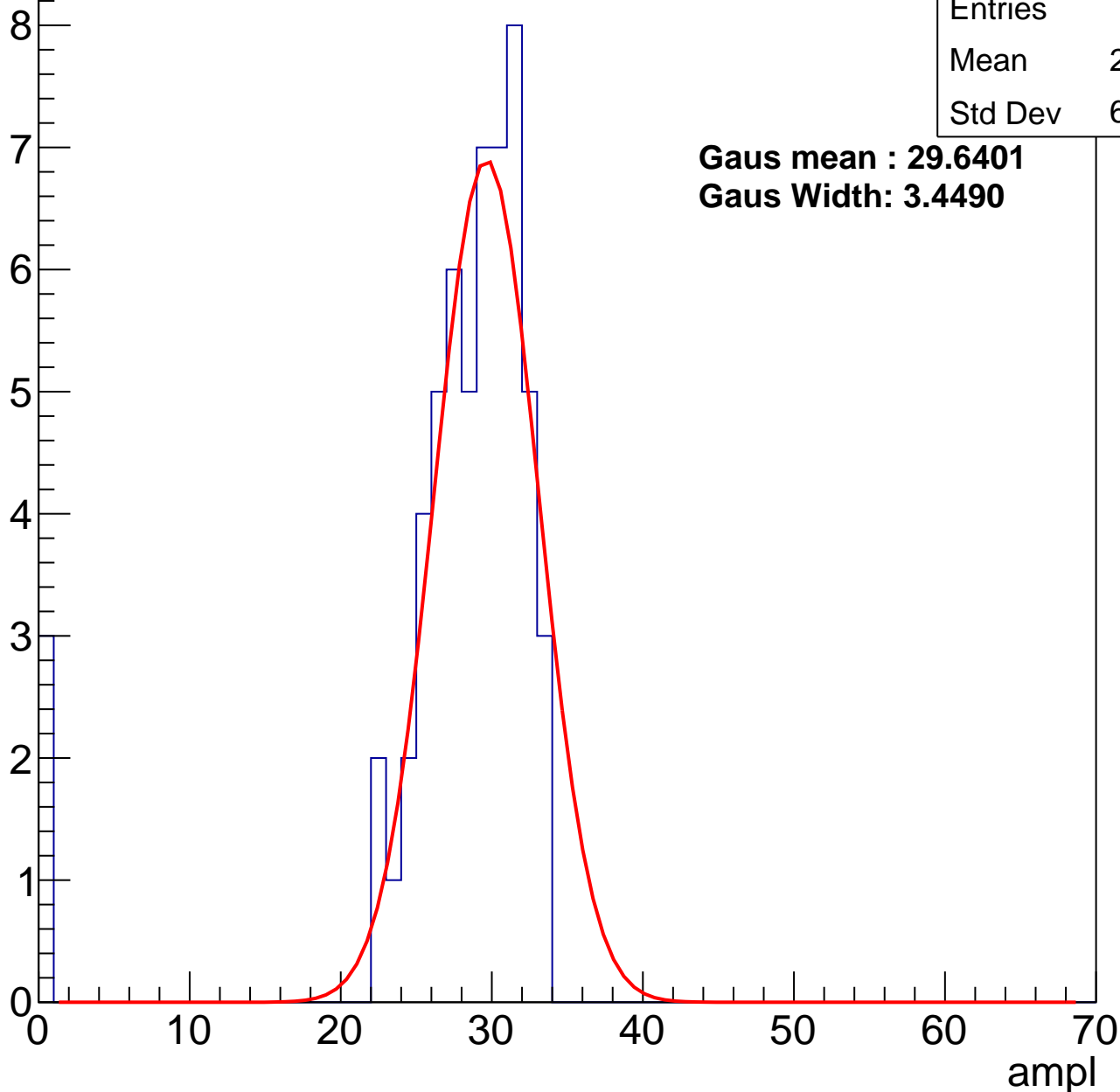
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	27.02
Std Dev	6.884

**Gaus mean : 29.6401**

**Gaus Width: 3.4490**



# B1L000S, U1-ch10, adc1

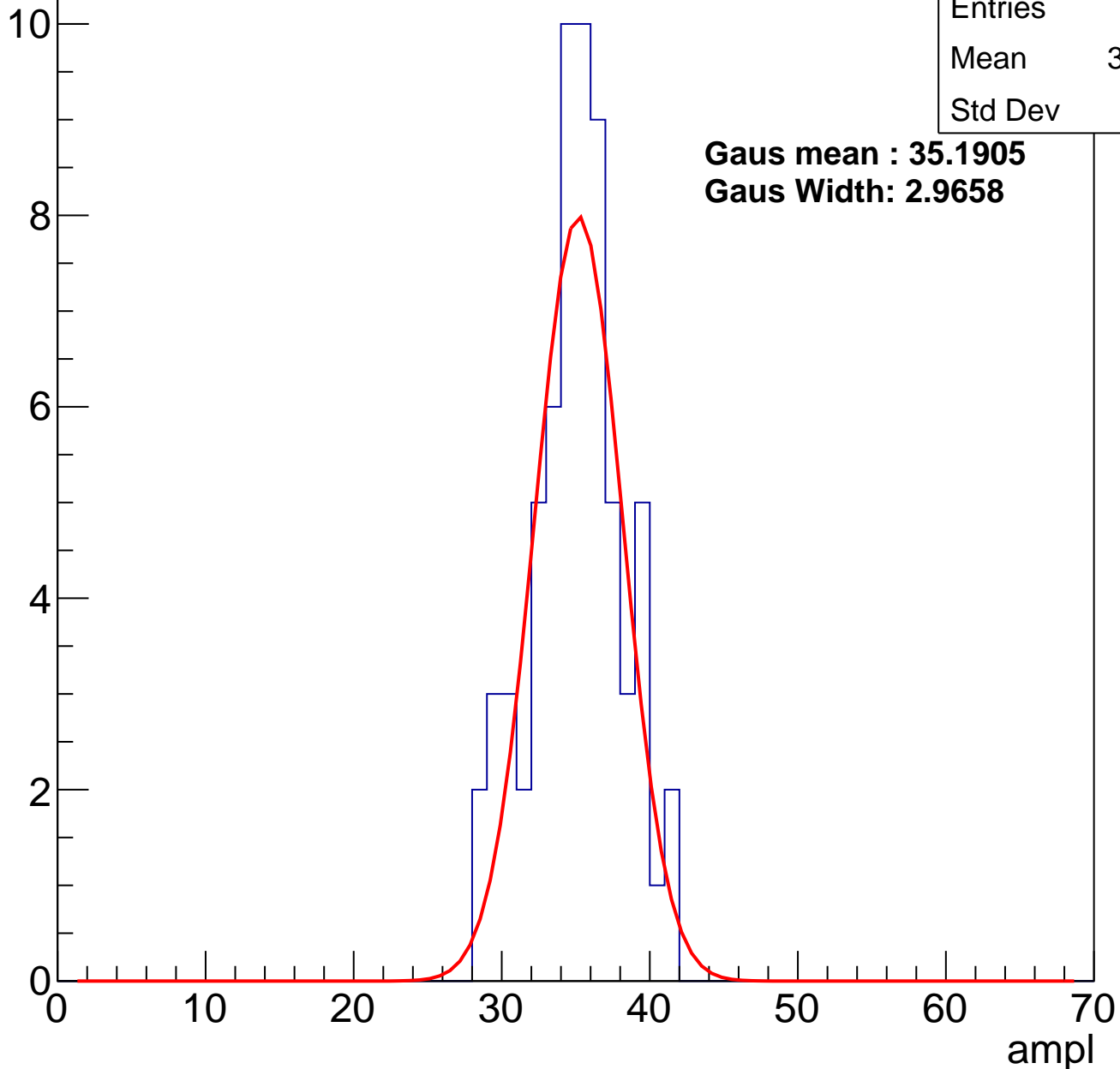
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	34.59
Std Dev	3.05

**Gaus mean : 35.1905**

**Gaus Width: 2.9658**

Entry



# B1L000S, U1-ch10, adc2

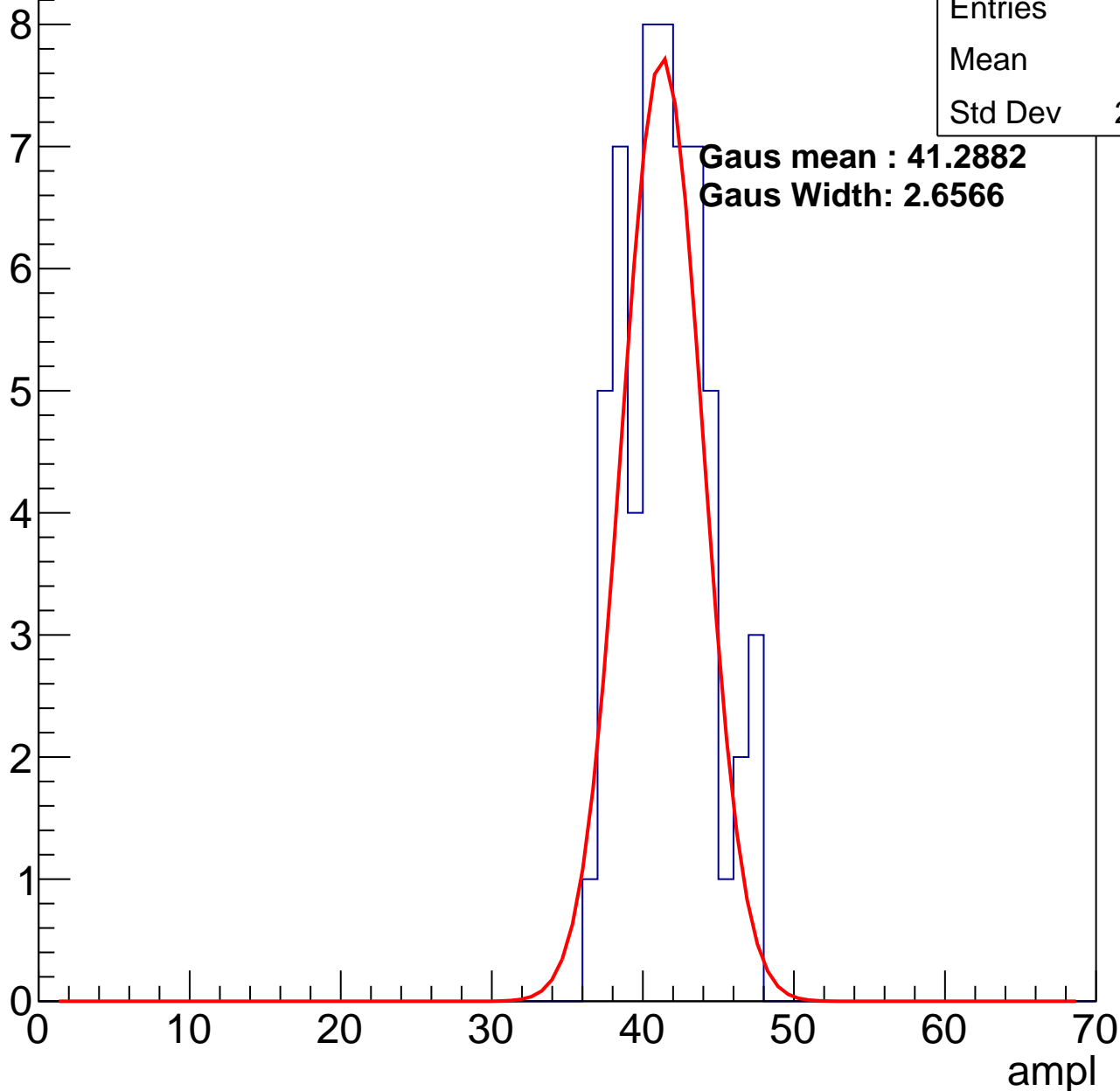
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.1
Std Dev	2.771

**Gaus mean : 41.2882**

**Gaus Width: 2.6566**

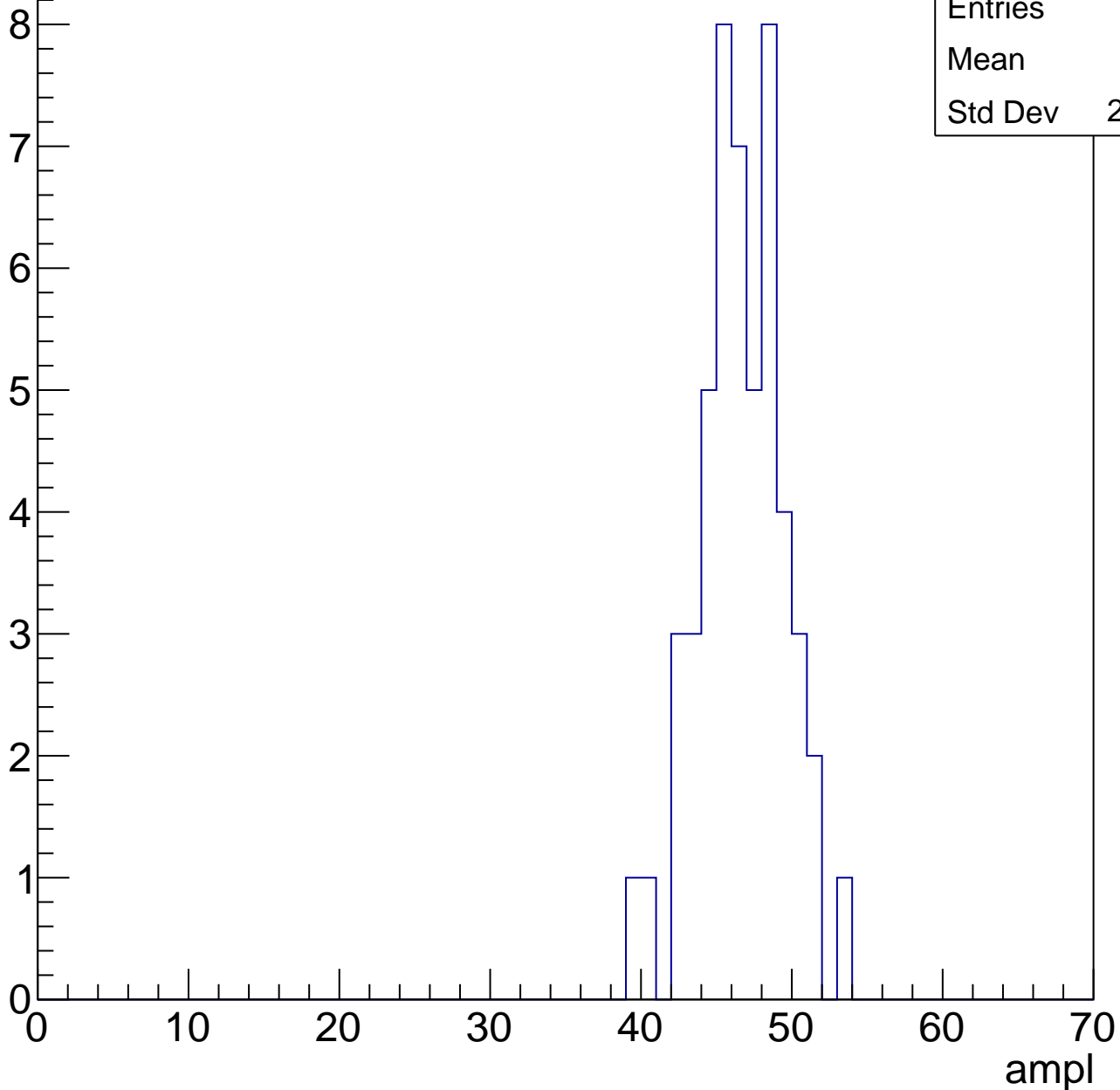


# B1L000S, U1-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	46.2
Std Dev	2.822

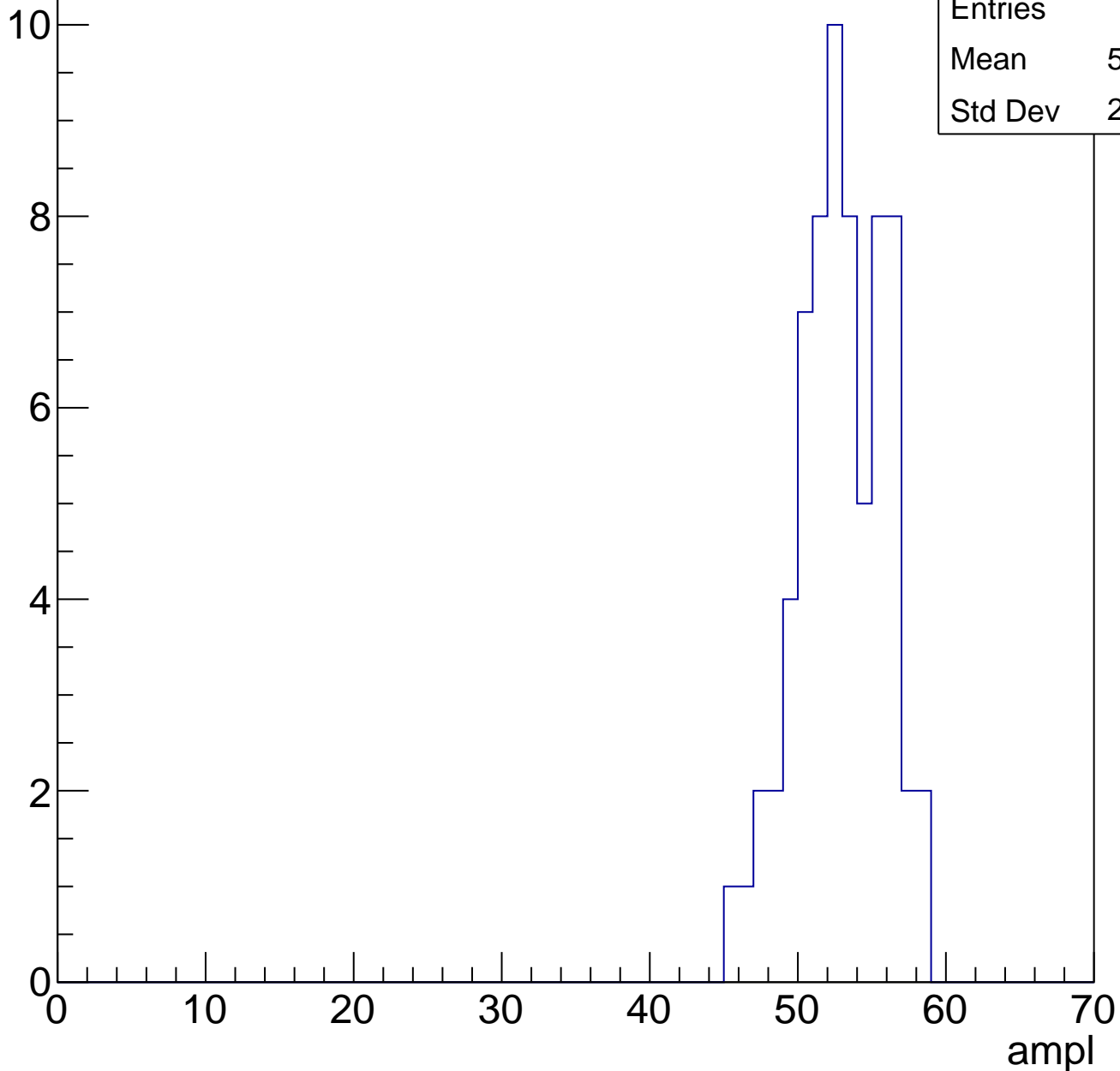


# B1L000S, U1-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	52.46
Std Dev	2.908

Entry



# B1L000S, U1-ch10, adc5

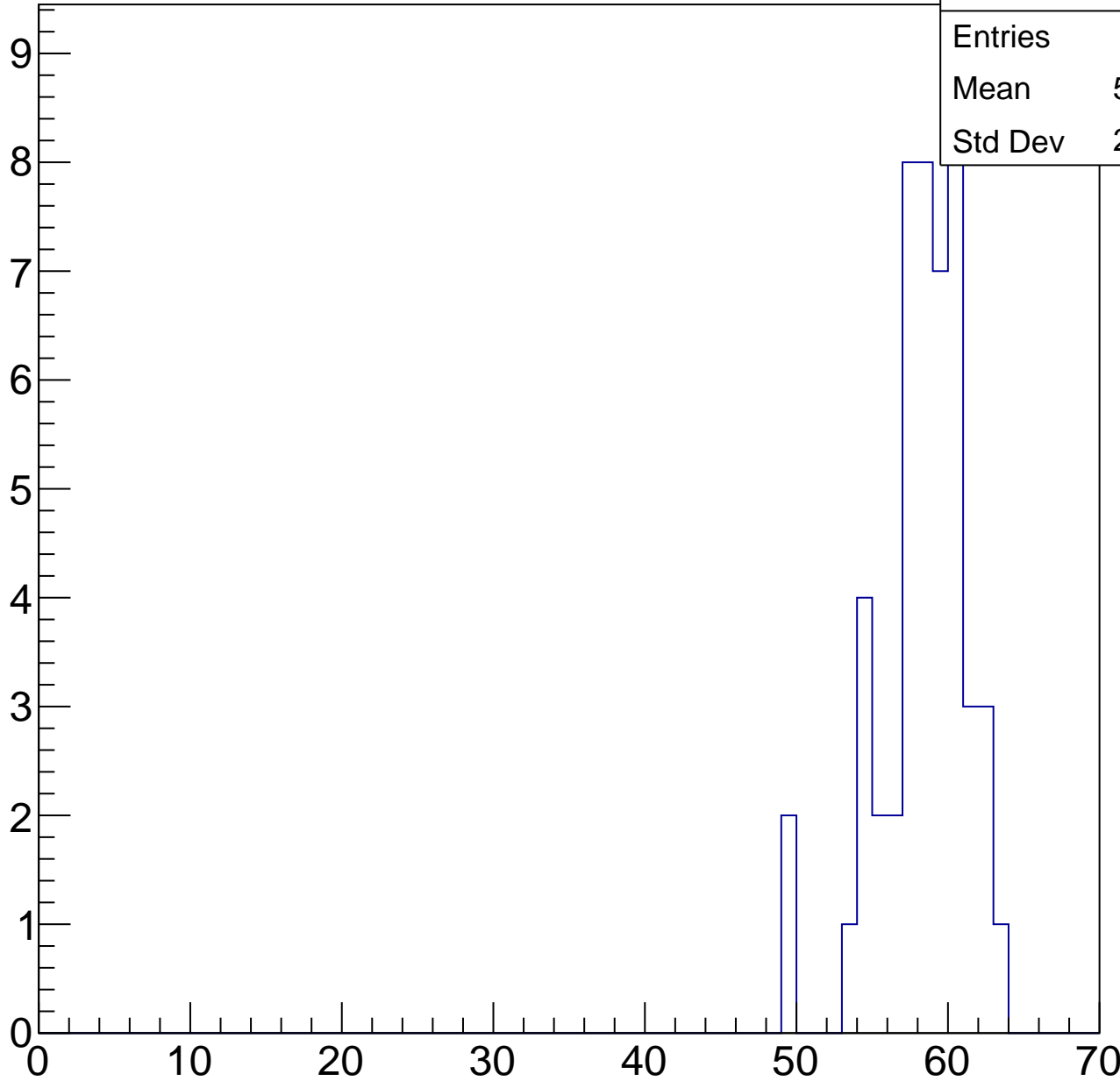
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	57.88
Std Dev	2.923

ampl

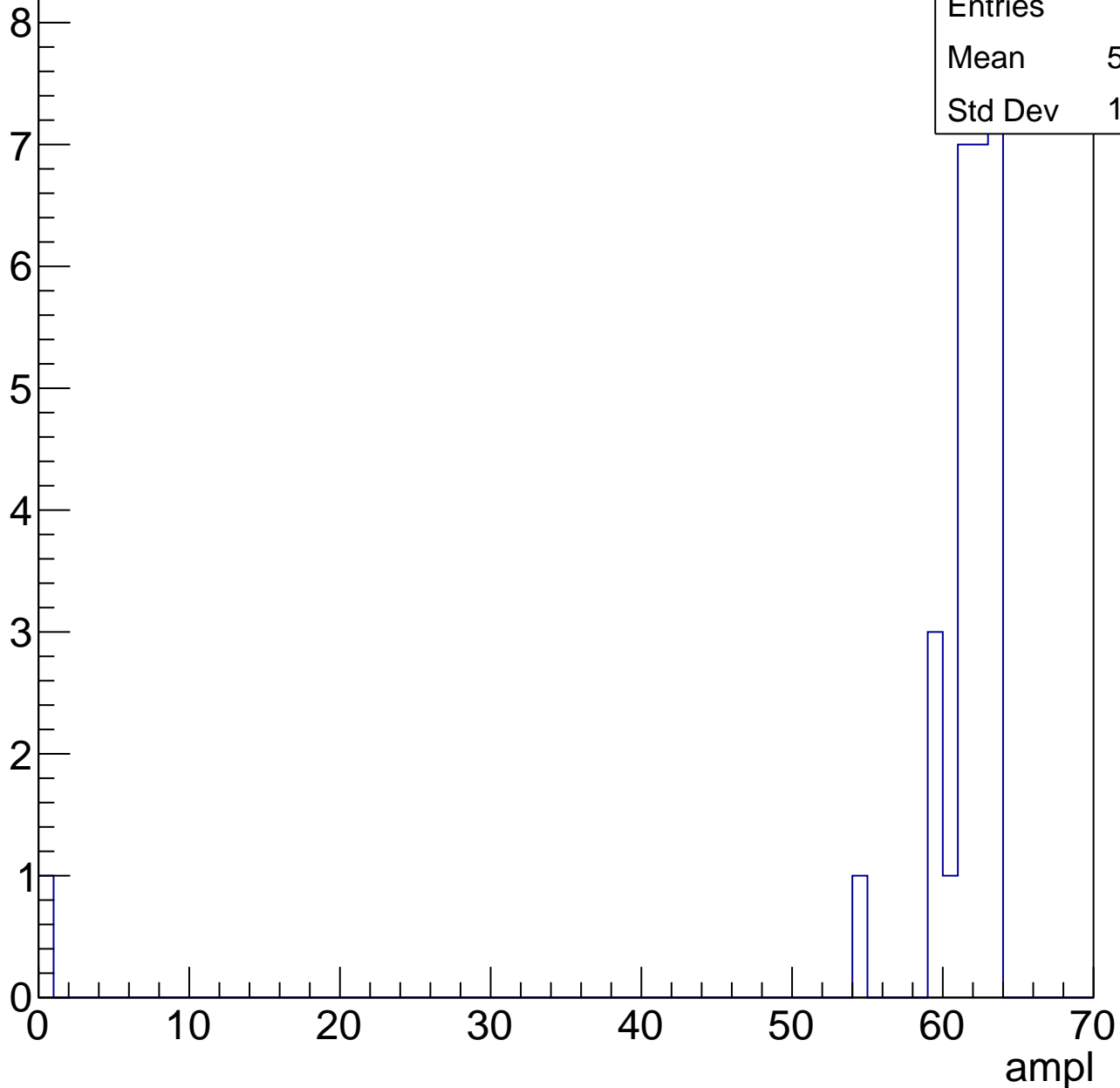


# B1L000S, U1-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	59.14
Std Dev	11.53





# B1L000S, U1-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch11, adc0

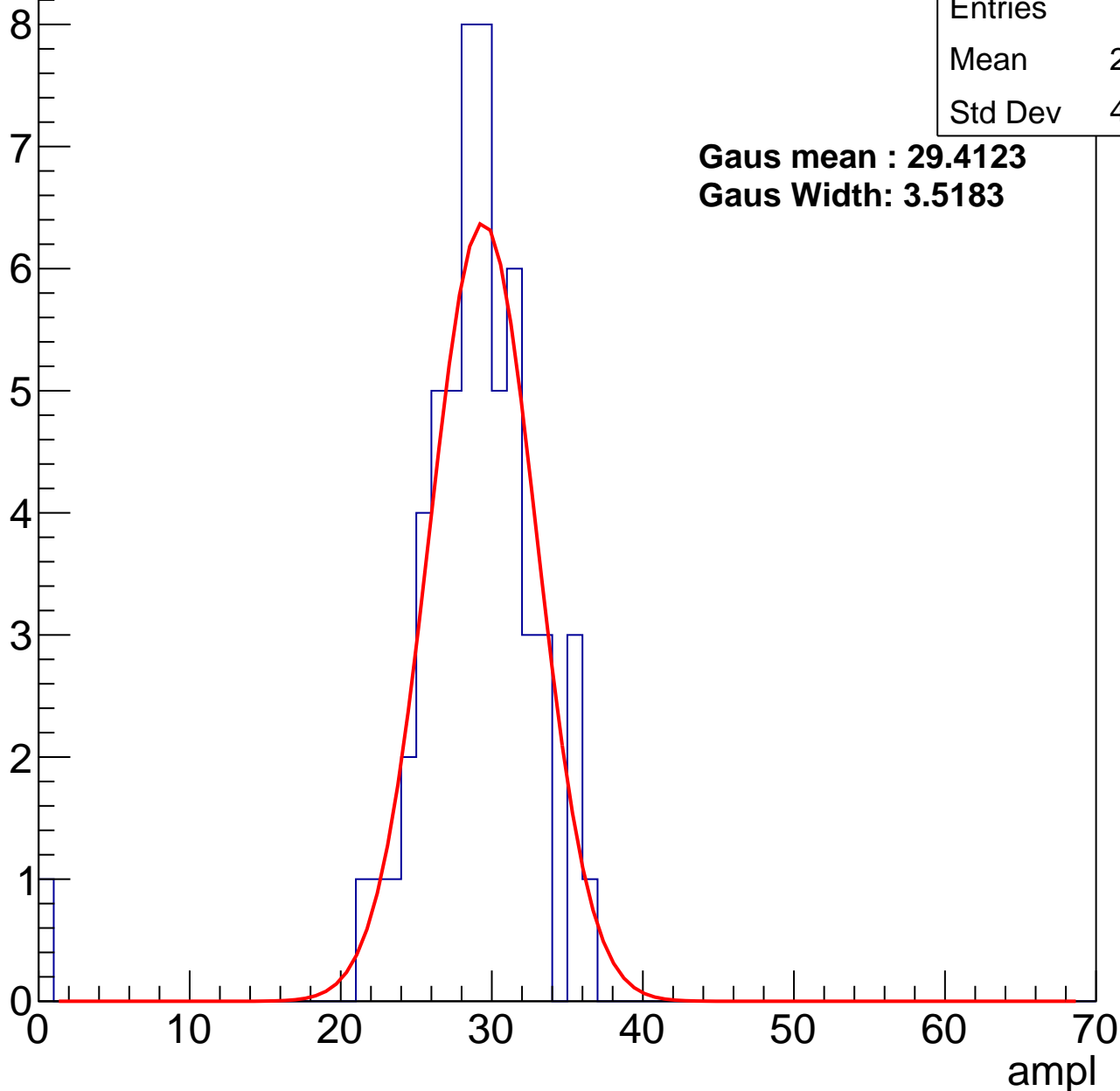
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28.19
Std Dev	4.957

**Gaus mean : 29.4123**

**Gaus Width: 3.5183**



# B1L000S, U1-ch11, adc1

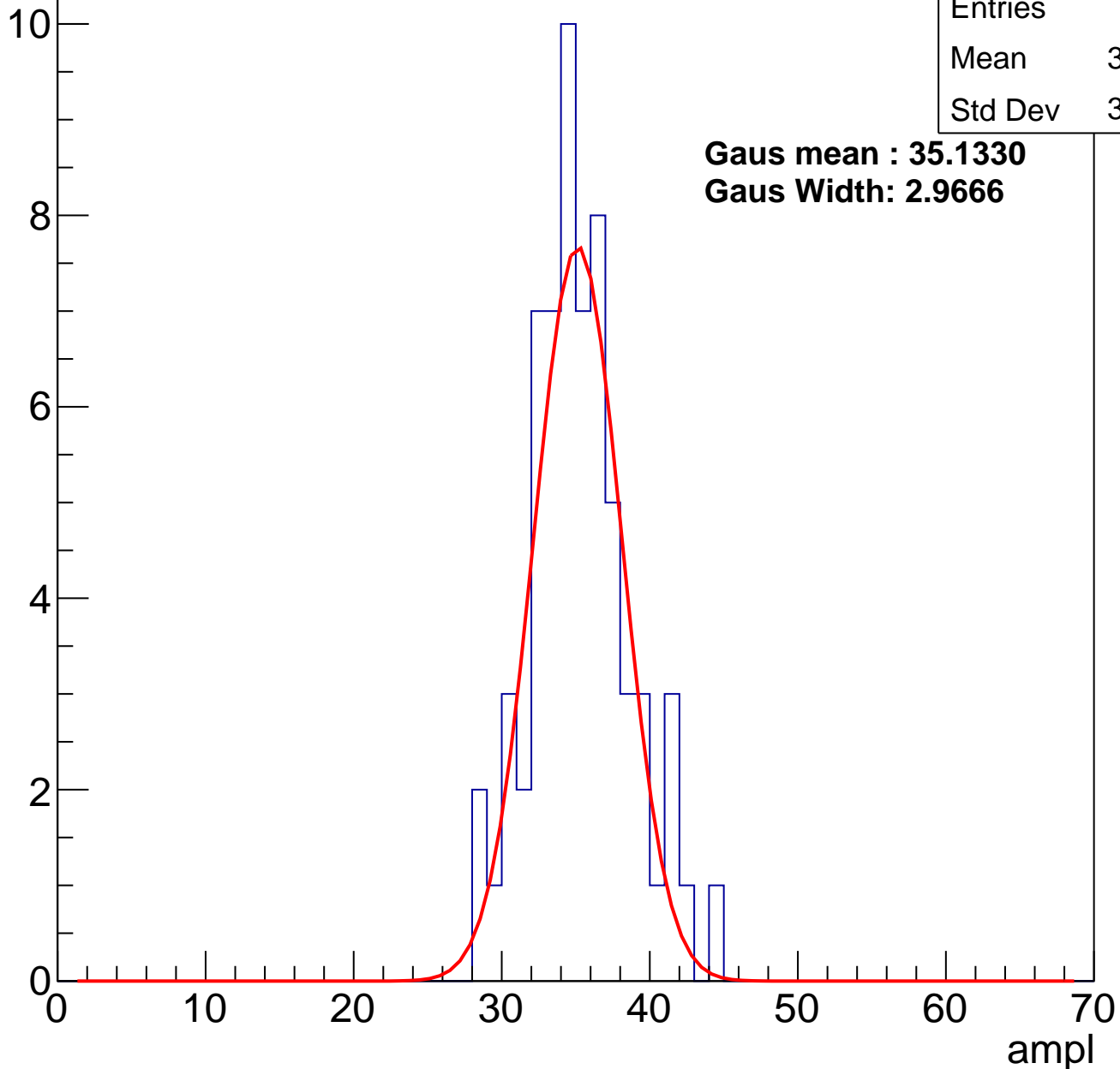
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	34.84
Std Dev	3.322

**Gaus mean : 35.1330**

**Gaus Width: 2.9666**

Entry



# B1L000S, U1-ch11, adc2

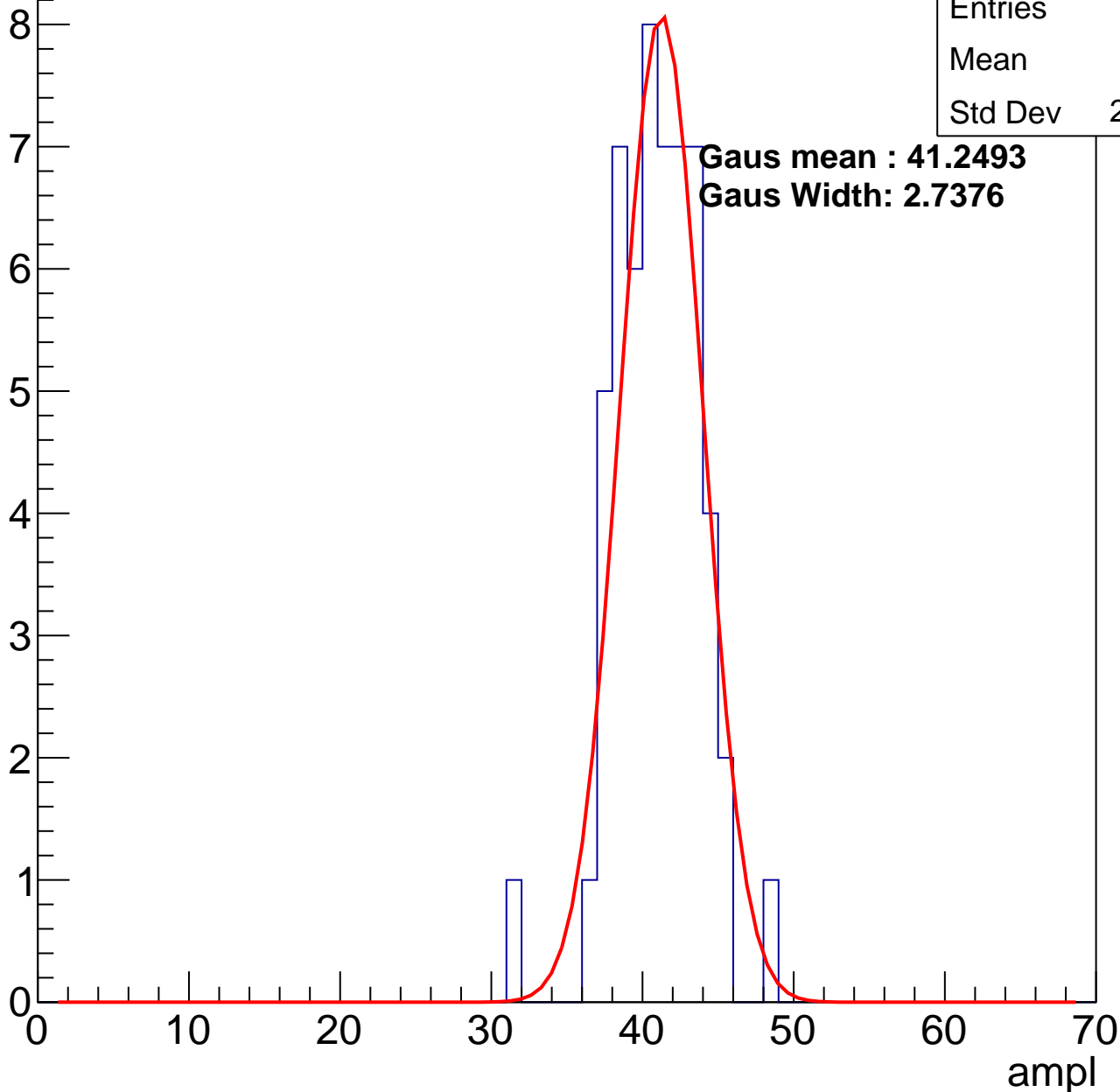
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	40.5
Std Dev	2.797

**Gaus mean : 41.2493**

**Gaus Width: 2.7376**

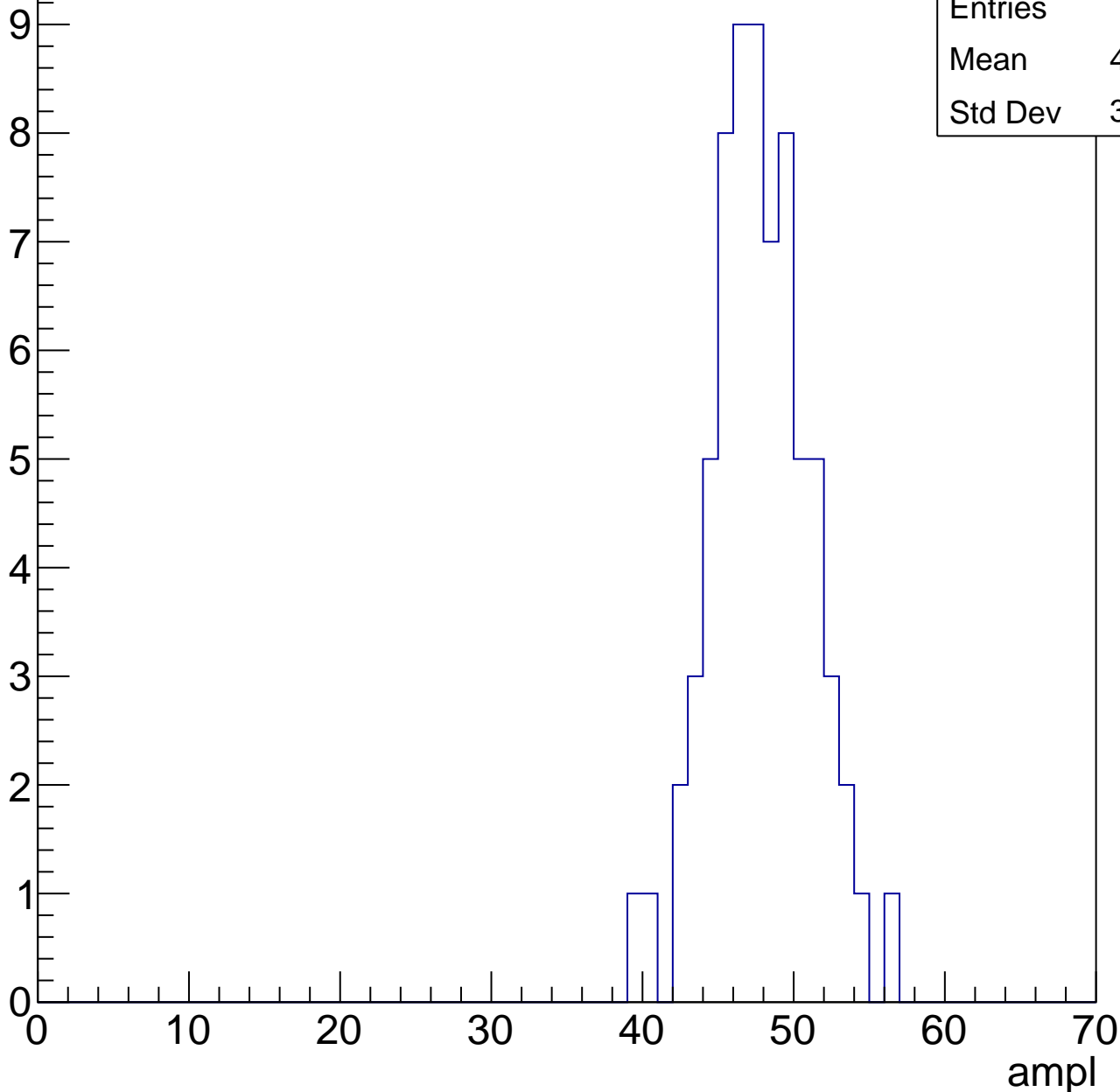


# B1L000S, U1-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	47.34
Std Dev	3.229

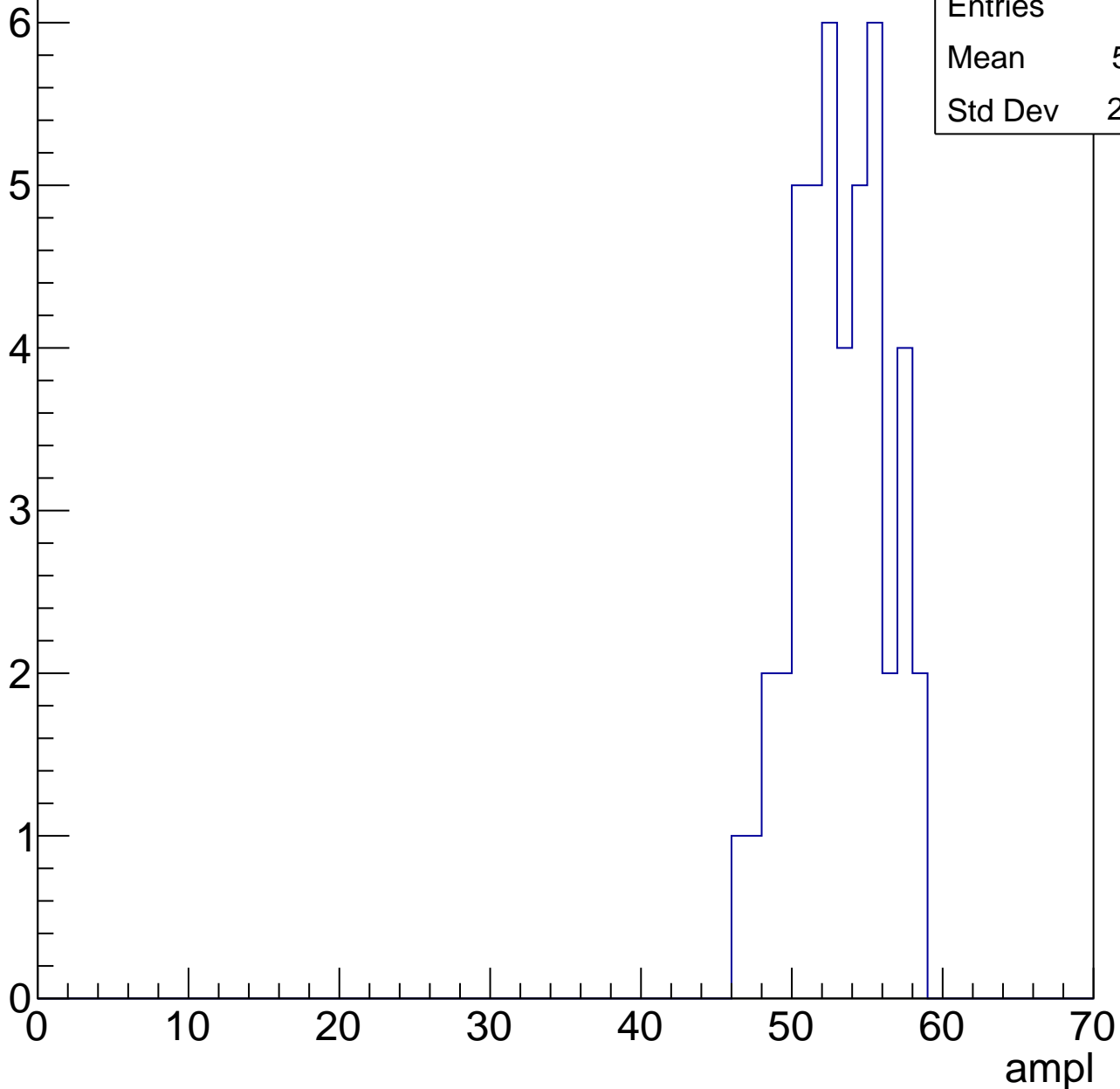


# B1L000S, U1-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

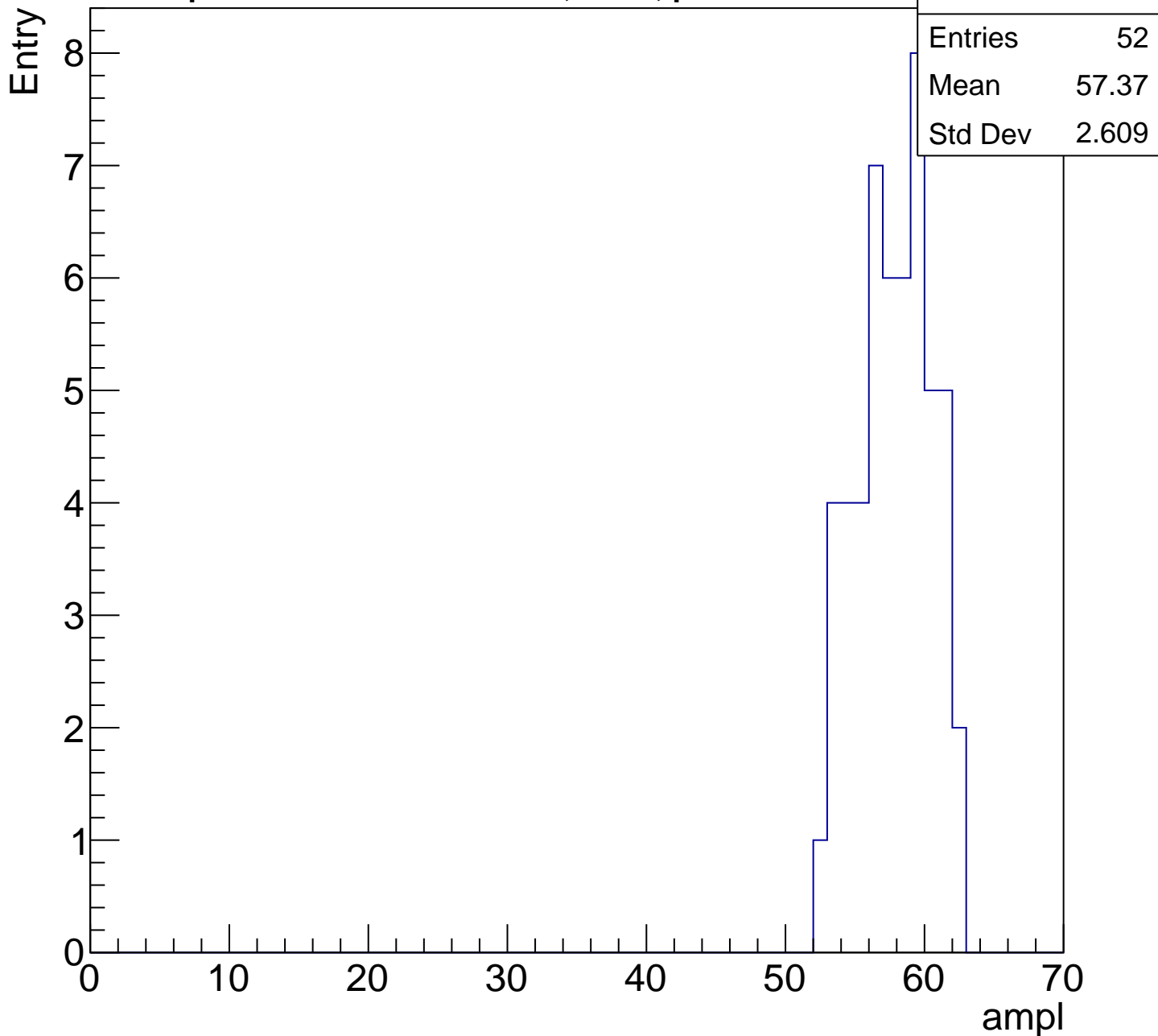
Entry

Entries	45
Mean	52.71
Std Dev	2.964



# B1L000S, U1-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

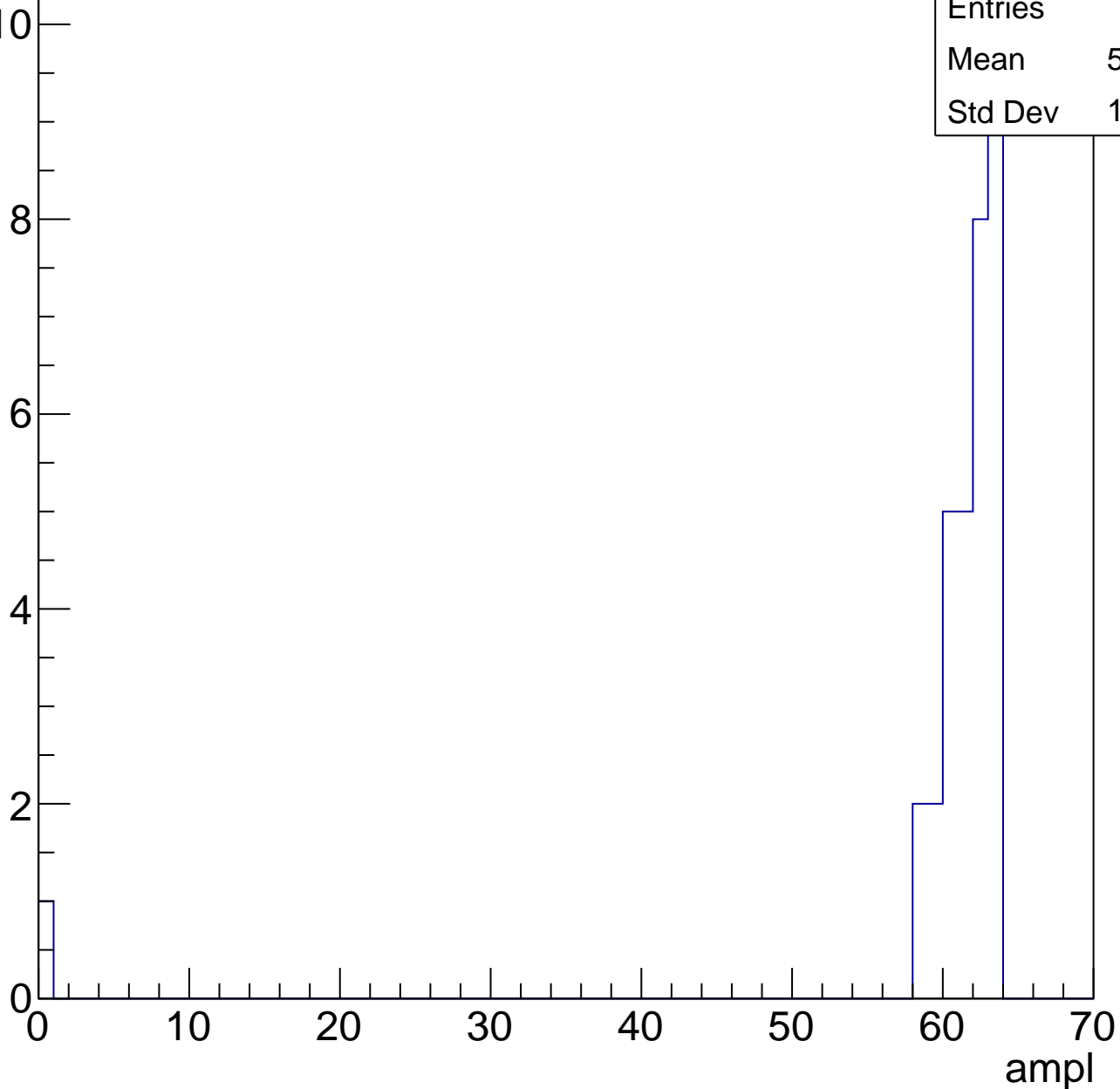


# B1L000S, U1-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	33
Mean	59.55
Std Dev	10.63





# B1L000S, U1-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U1-ch12, adc0

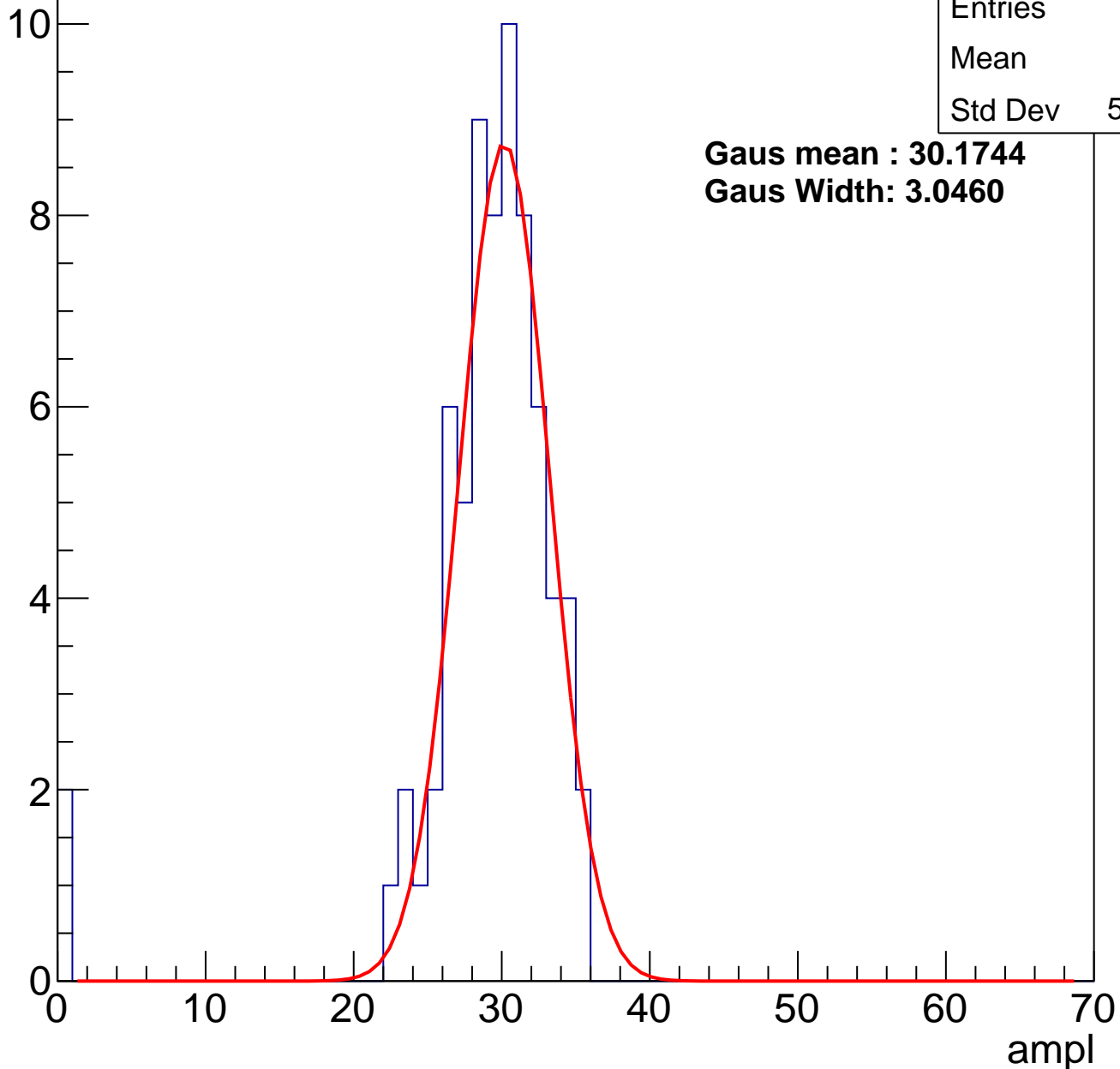
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	28.5
Std Dev	5.679

**Gaus mean : 30.1744**

**Gaus Width: 3.0460**

Entry



# B1L000S, U1-ch12, adc1

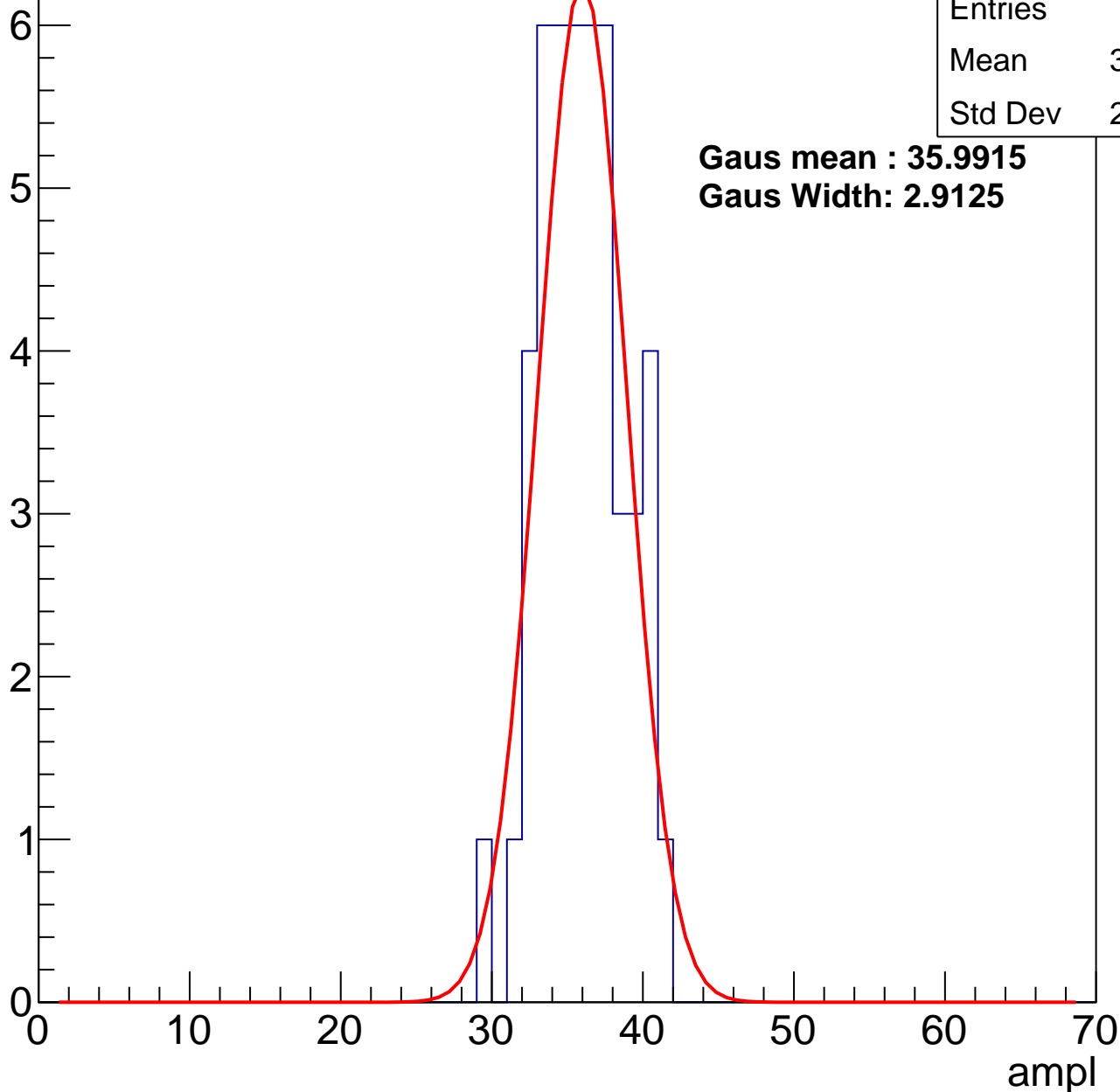
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	35.53
Std Dev	2.712

**Gaus mean : 35.9915**

**Gaus Width: 2.9125**



# B1L000S, U1-ch12, adc2

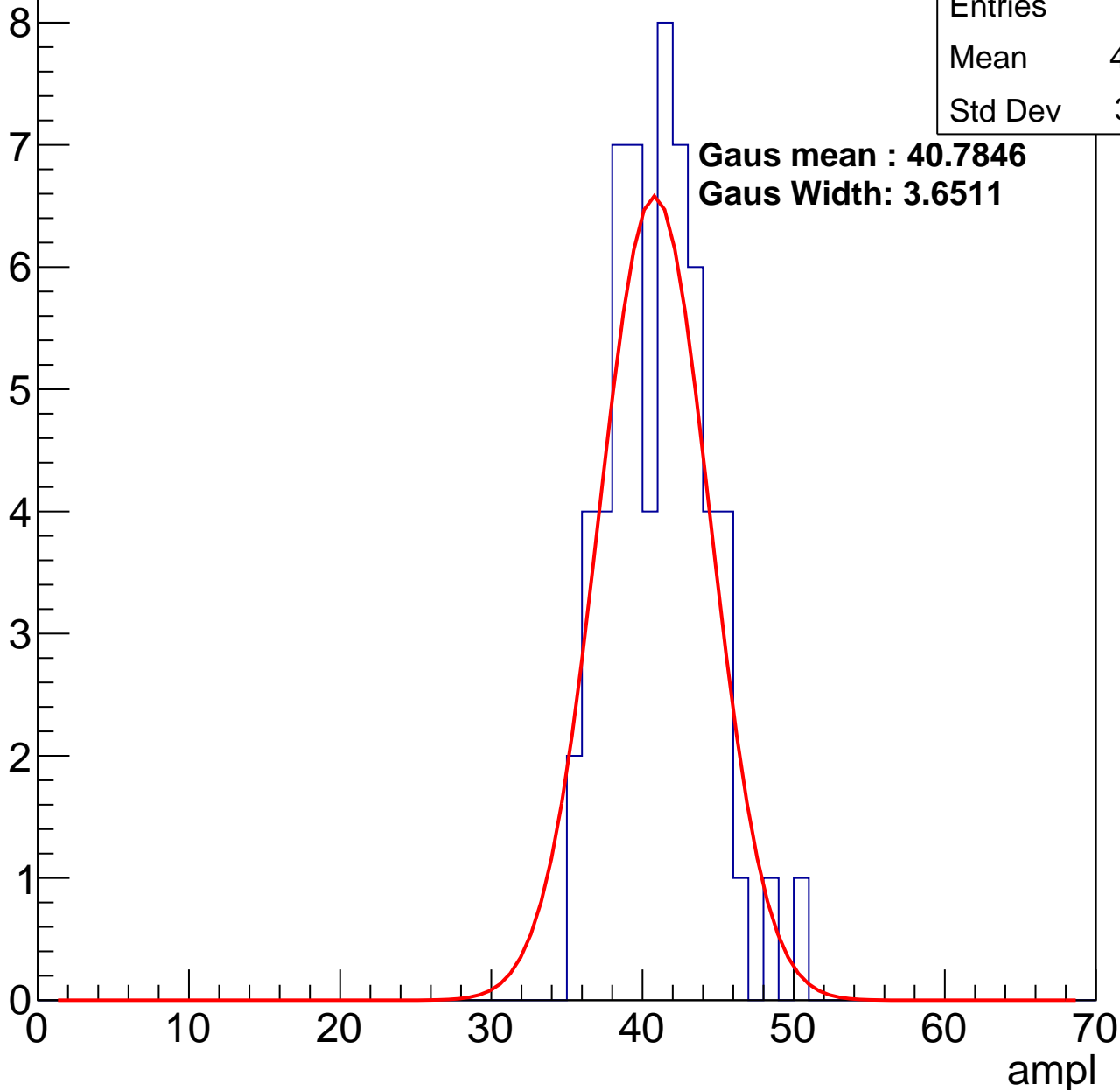
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	40.68
Std Dev	3.191

**Gaus mean : 40.7846**

**Gaus Width: 3.6511**

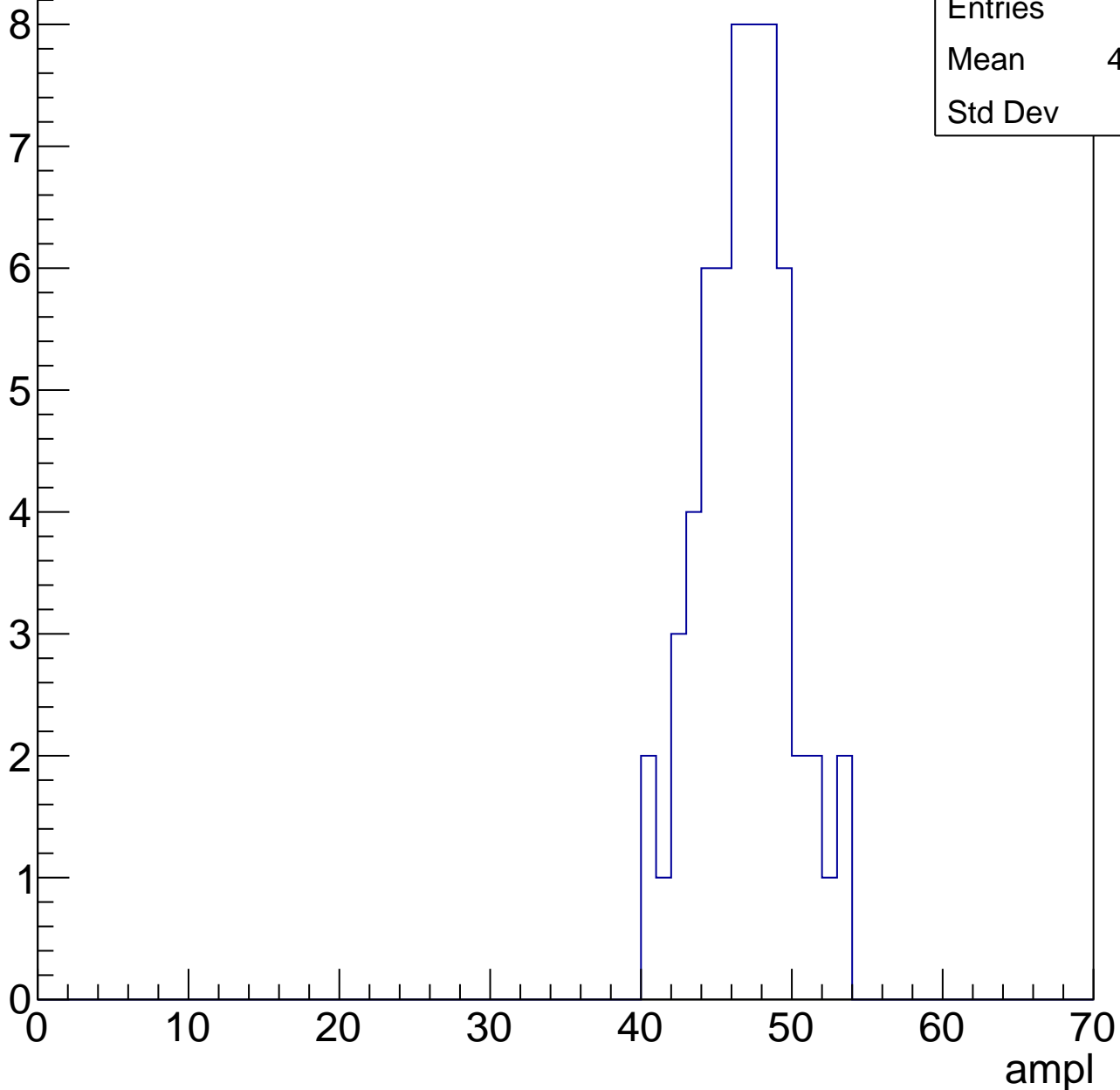


# B1L000S, U1-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	46.36
Std Dev	2.95

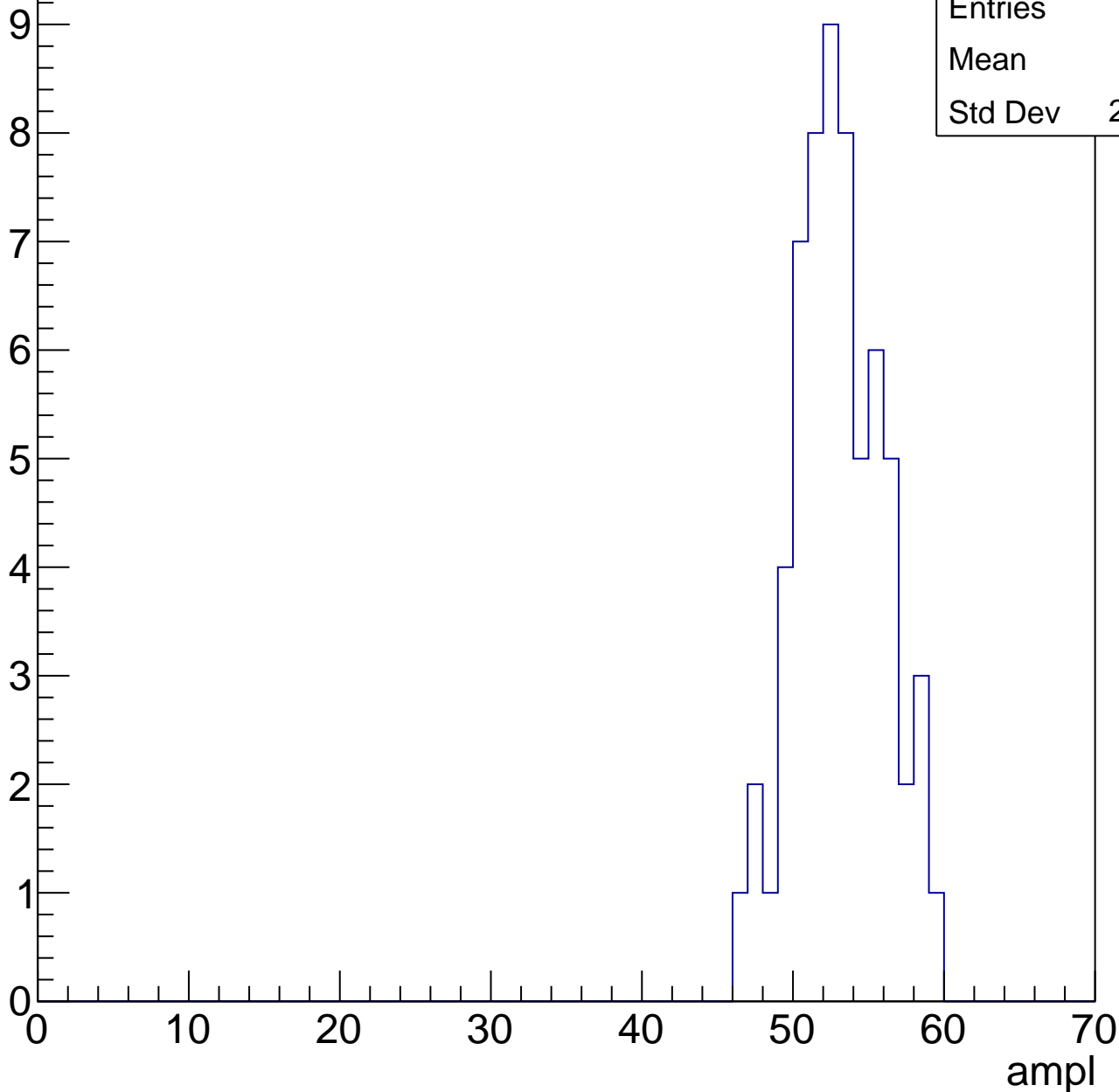


# B1L000S, U1-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	52.6
Std Dev	2.899



# B1L000S, U1-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries	53
Mean	58.53
Std Dev	2.646

ampl

0

10

20

30

40

50

60

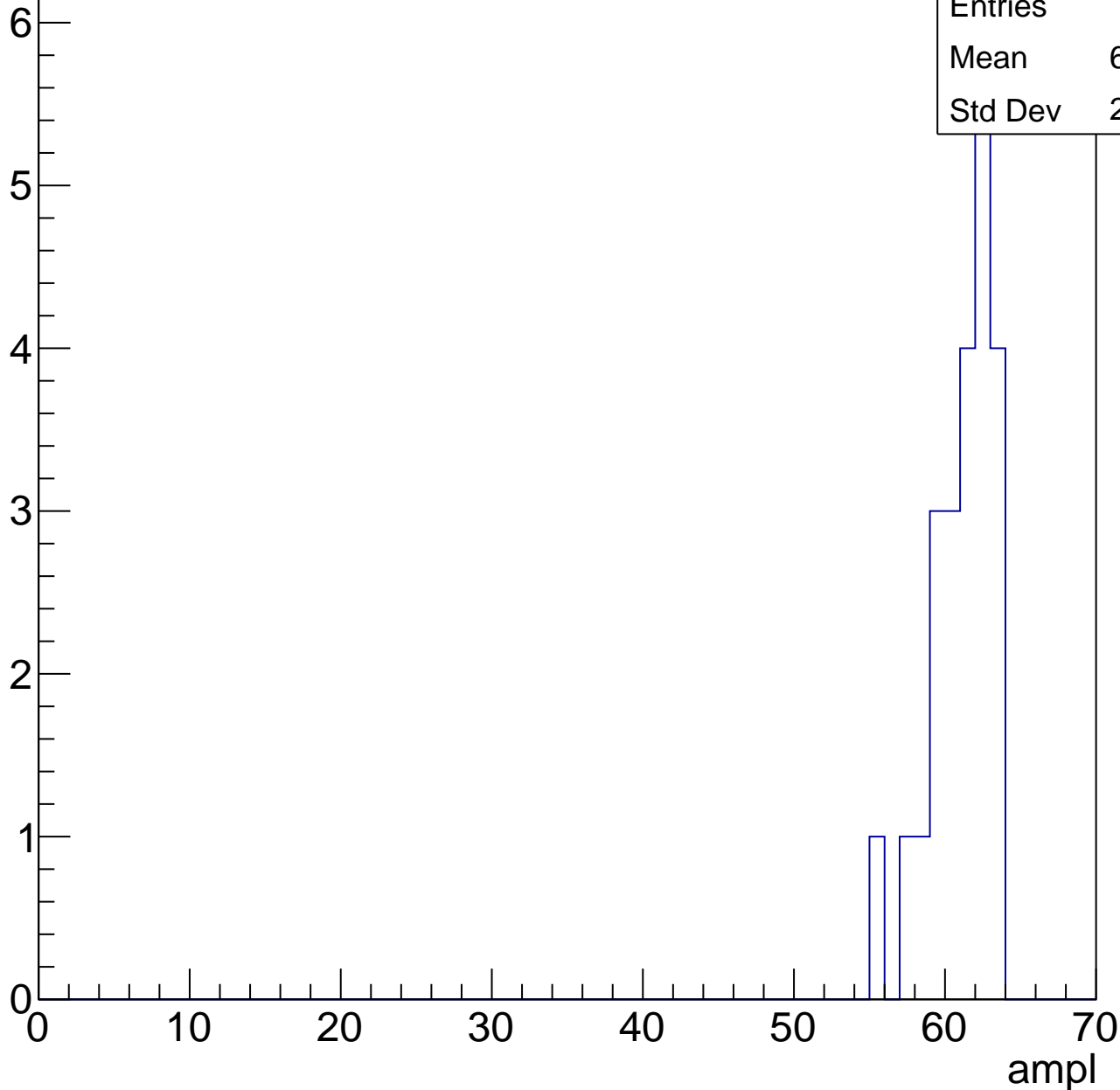
70

# B1L000S, U1-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	60.65
Std Dev	2.035





# B1L000S, U1-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch13, adc0

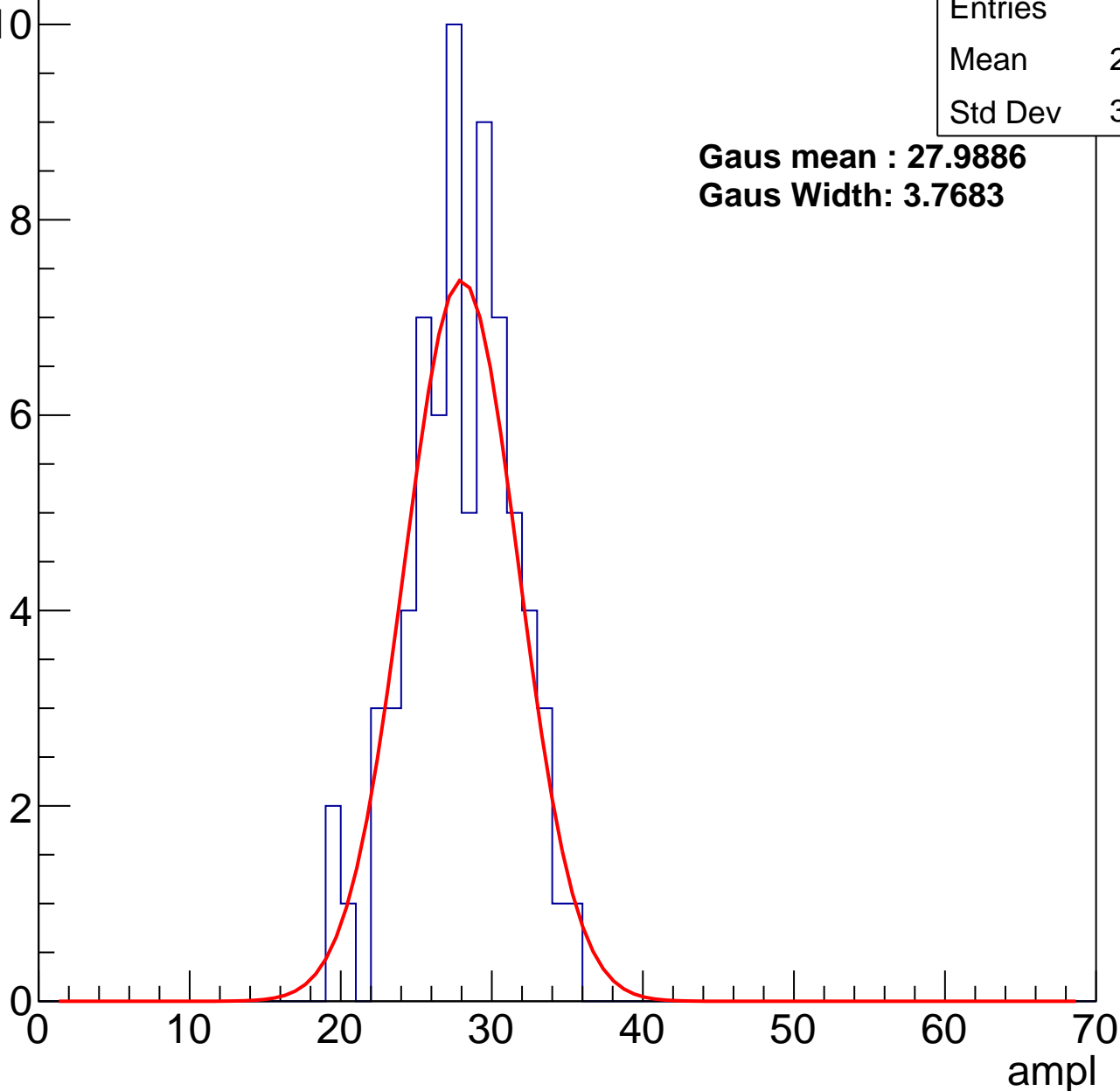
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	27.49
Std Dev	3.476

**Gaus mean : 27.9886**

**Gaus Width: 3.7683**



# B1L000S, U1-ch13, adc1

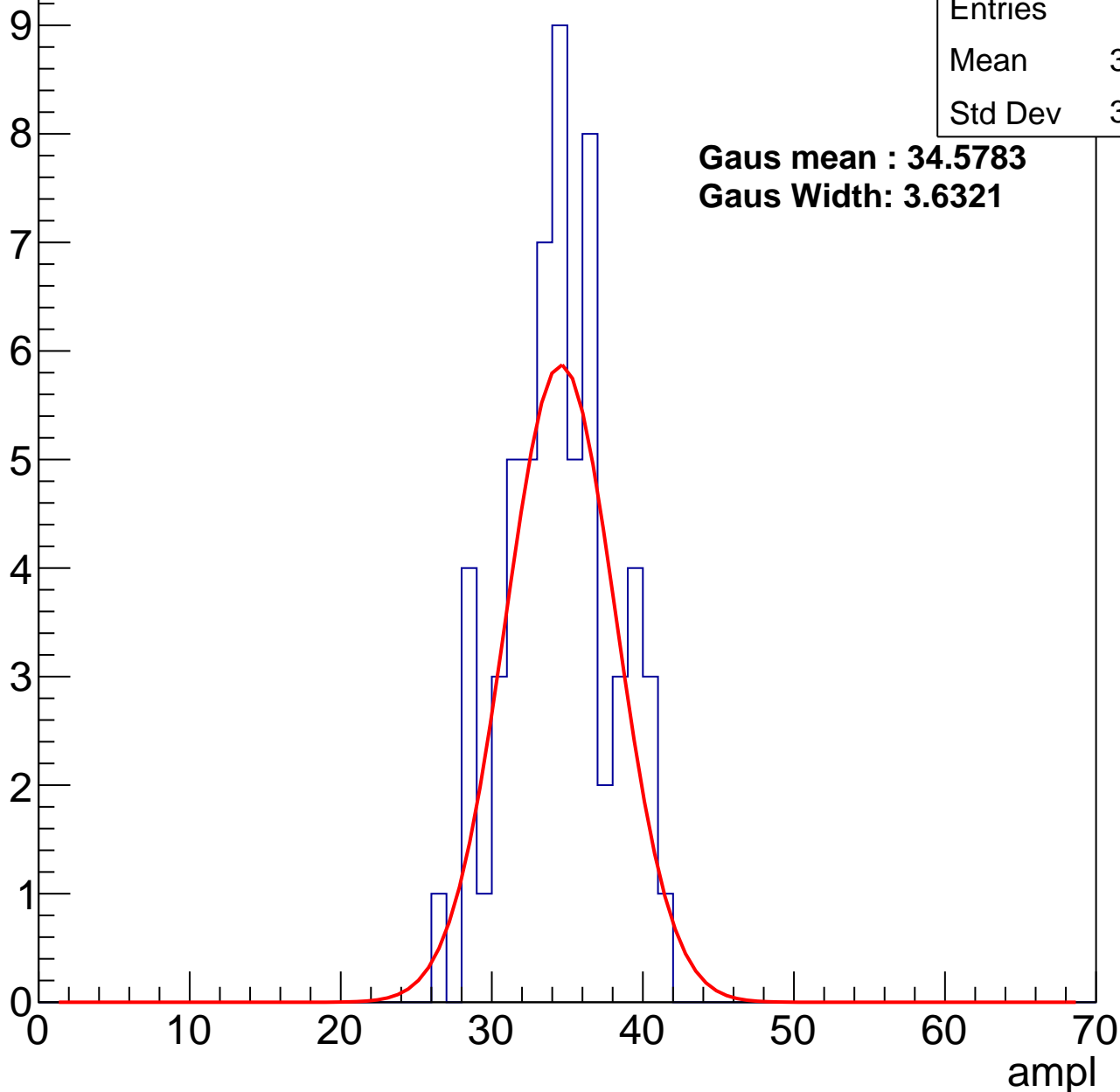
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	34.05
Std Dev	3.418

**Gaus mean : 34.5783**

**Gaus Width: 3.6321**



# B1L000S, U1-ch13, adc2

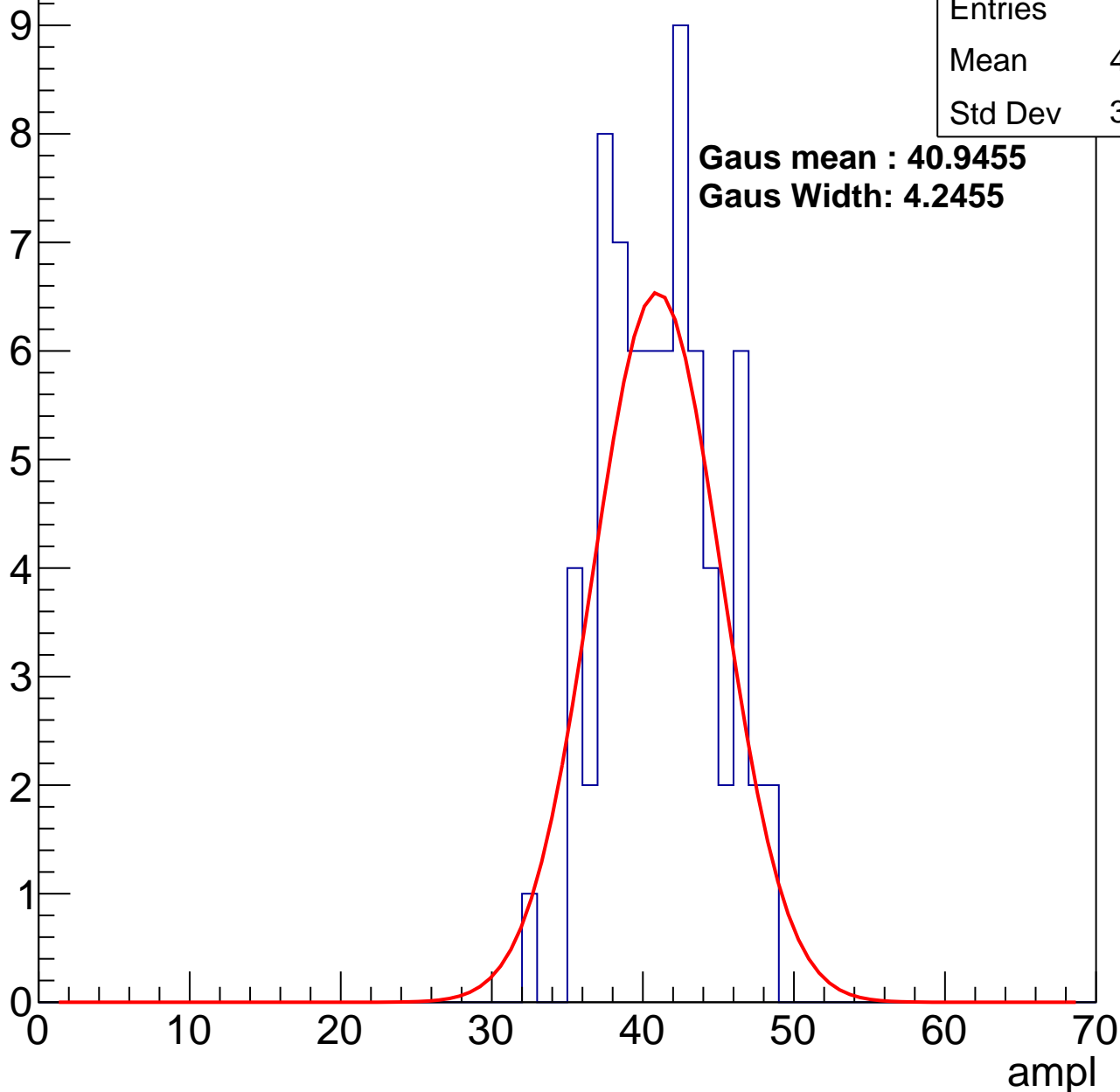
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	40.76
Std Dev	3.586

**Gaus mean : 40.9455**

**Gaus Width: 4.2455**

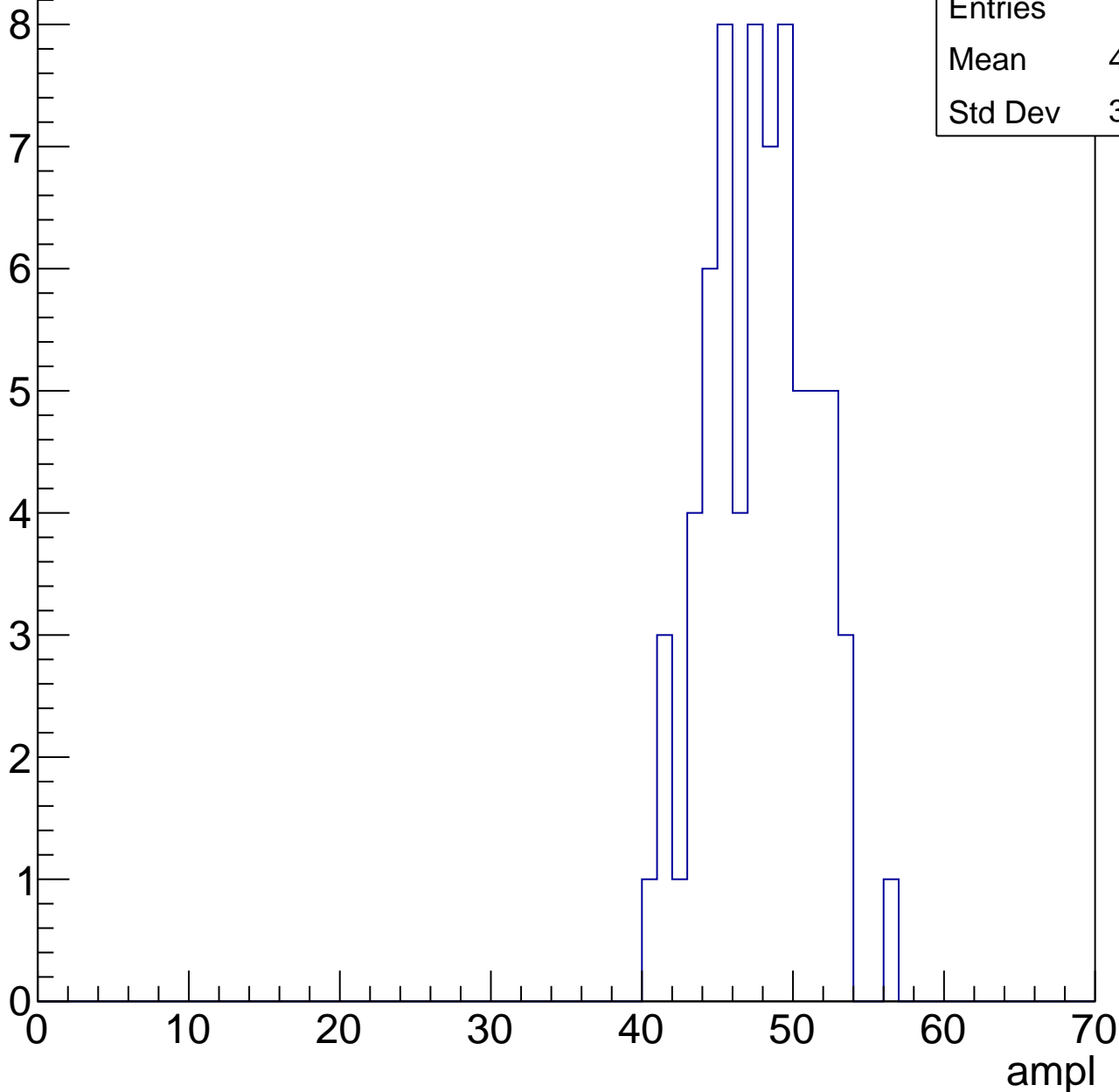


# B1L000S, U1-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	47.38
Std Dev	3.414



# B1L000S, U1-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

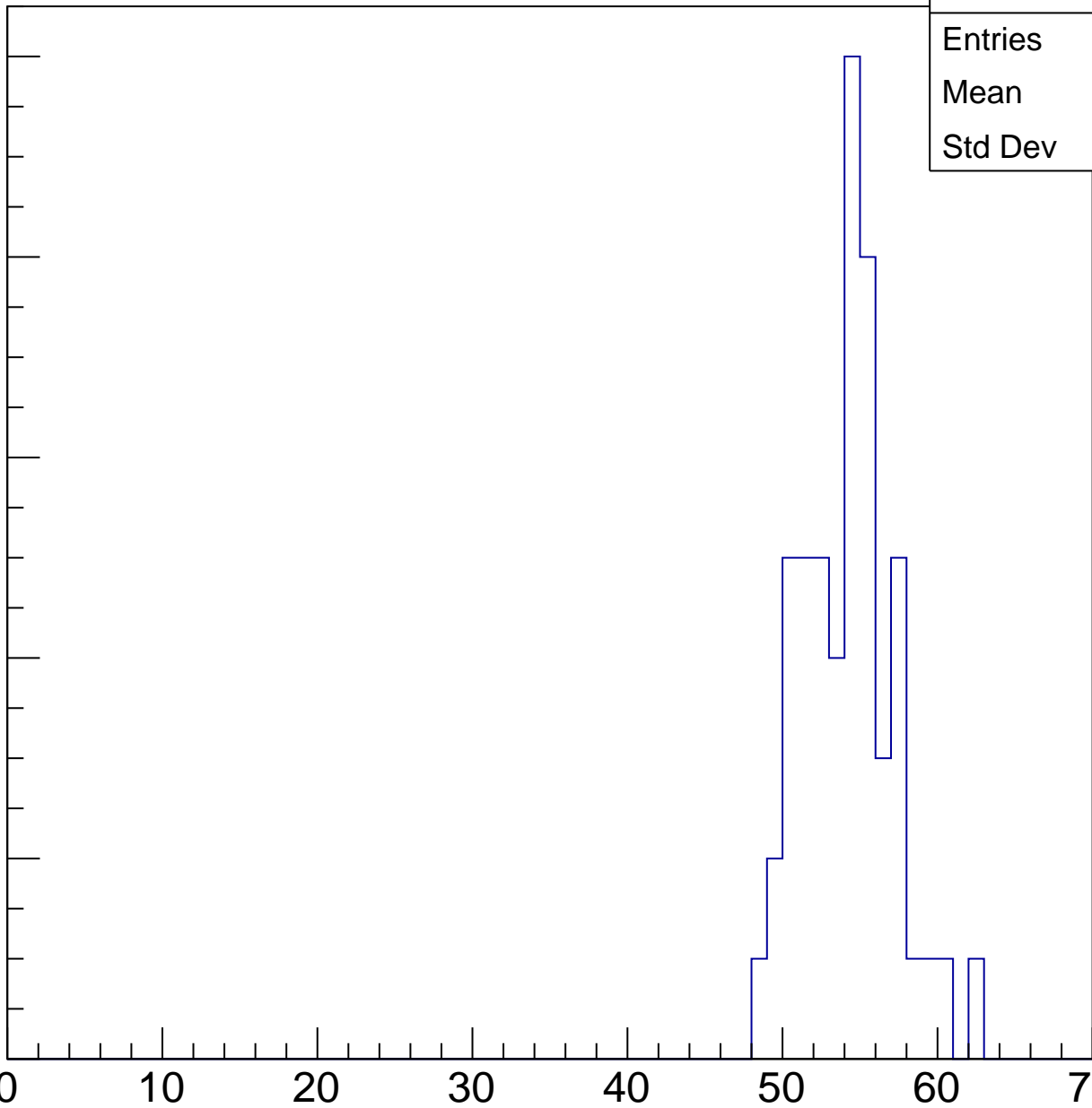
Entries	52
Mean	53.75
Std Dev	2.901

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

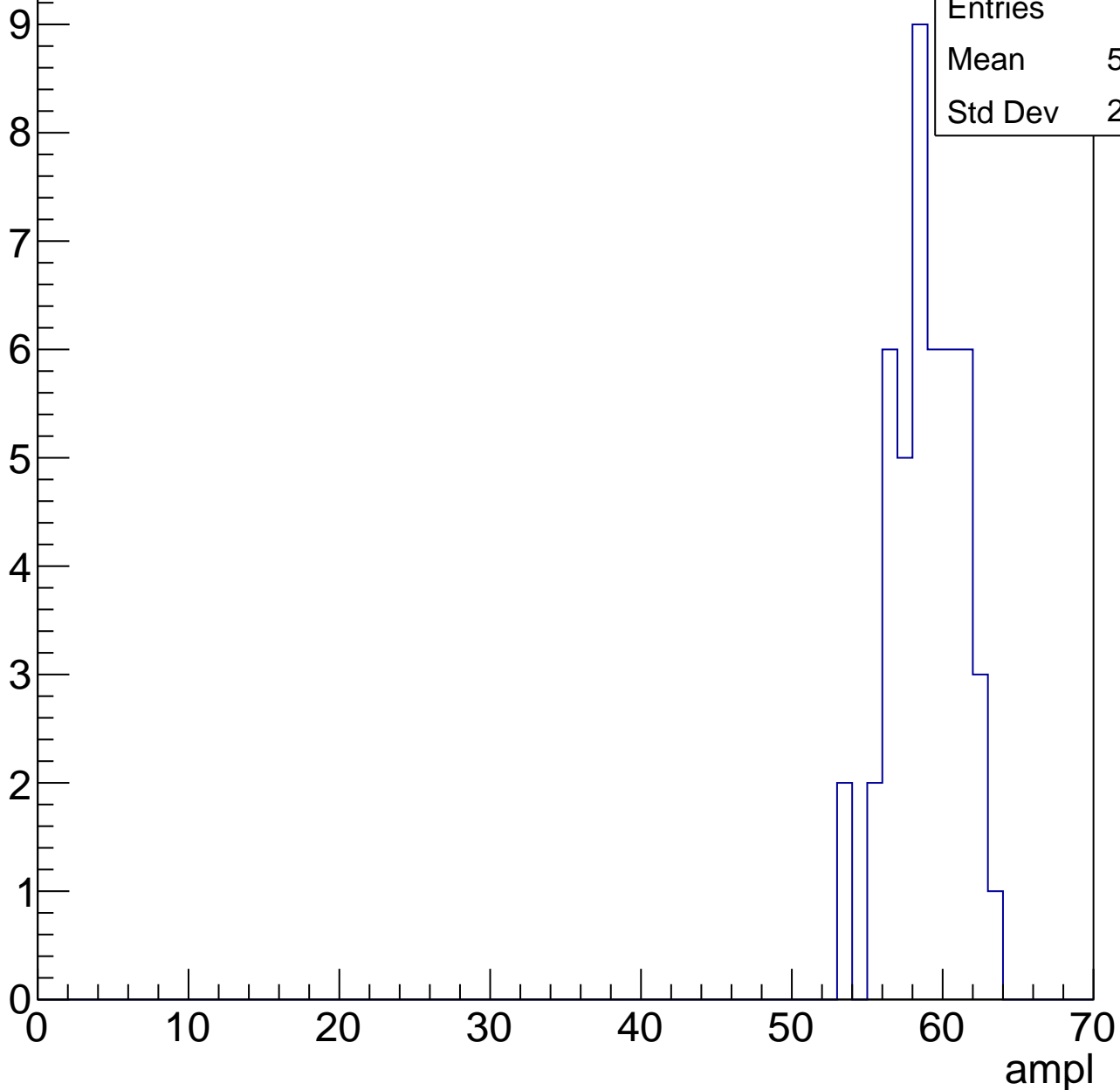


# B1L000S, U1-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	58.43
Std Dev	2.309

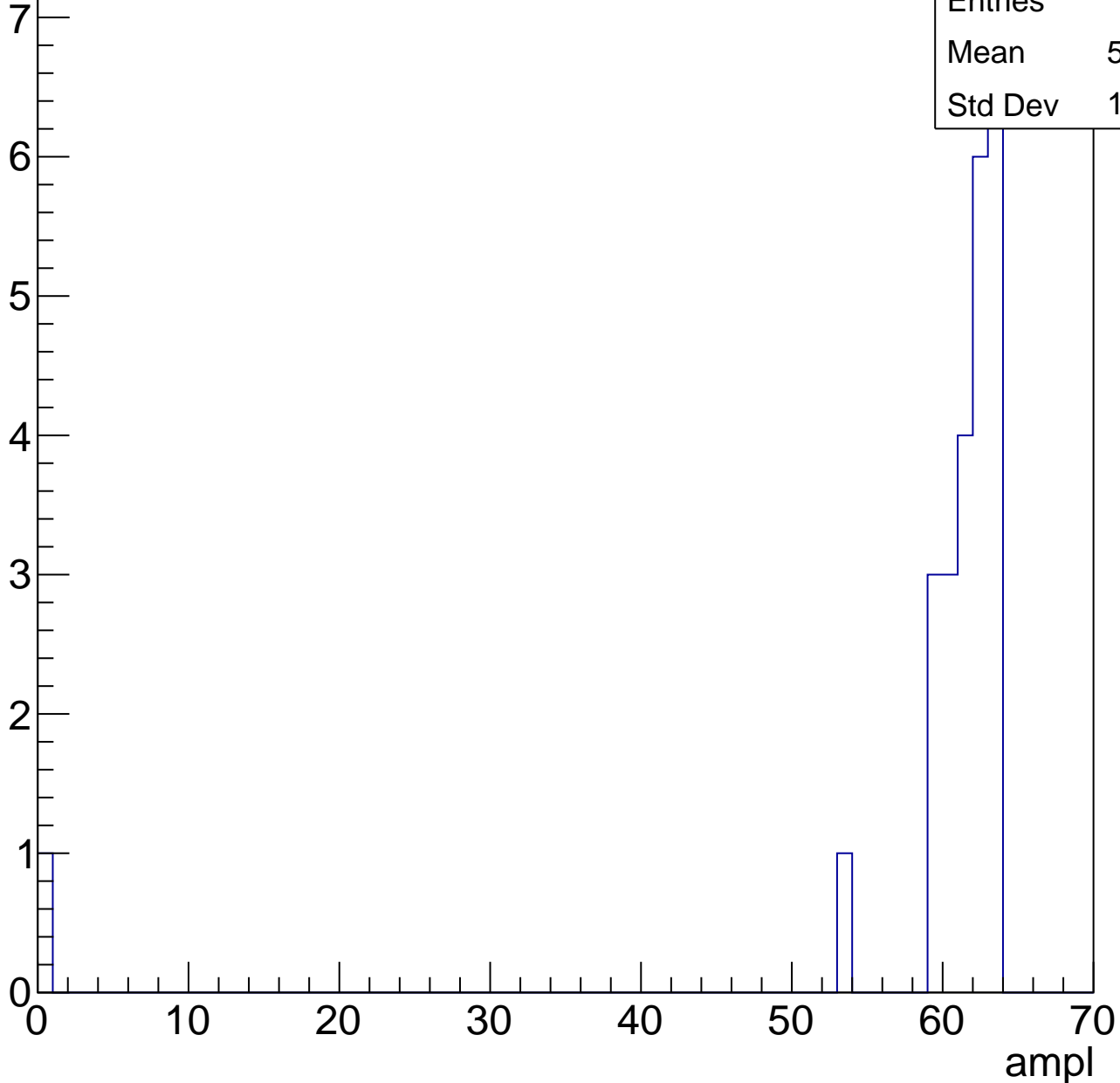


# B1L000S, U1-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.68
Std Dev	12.16

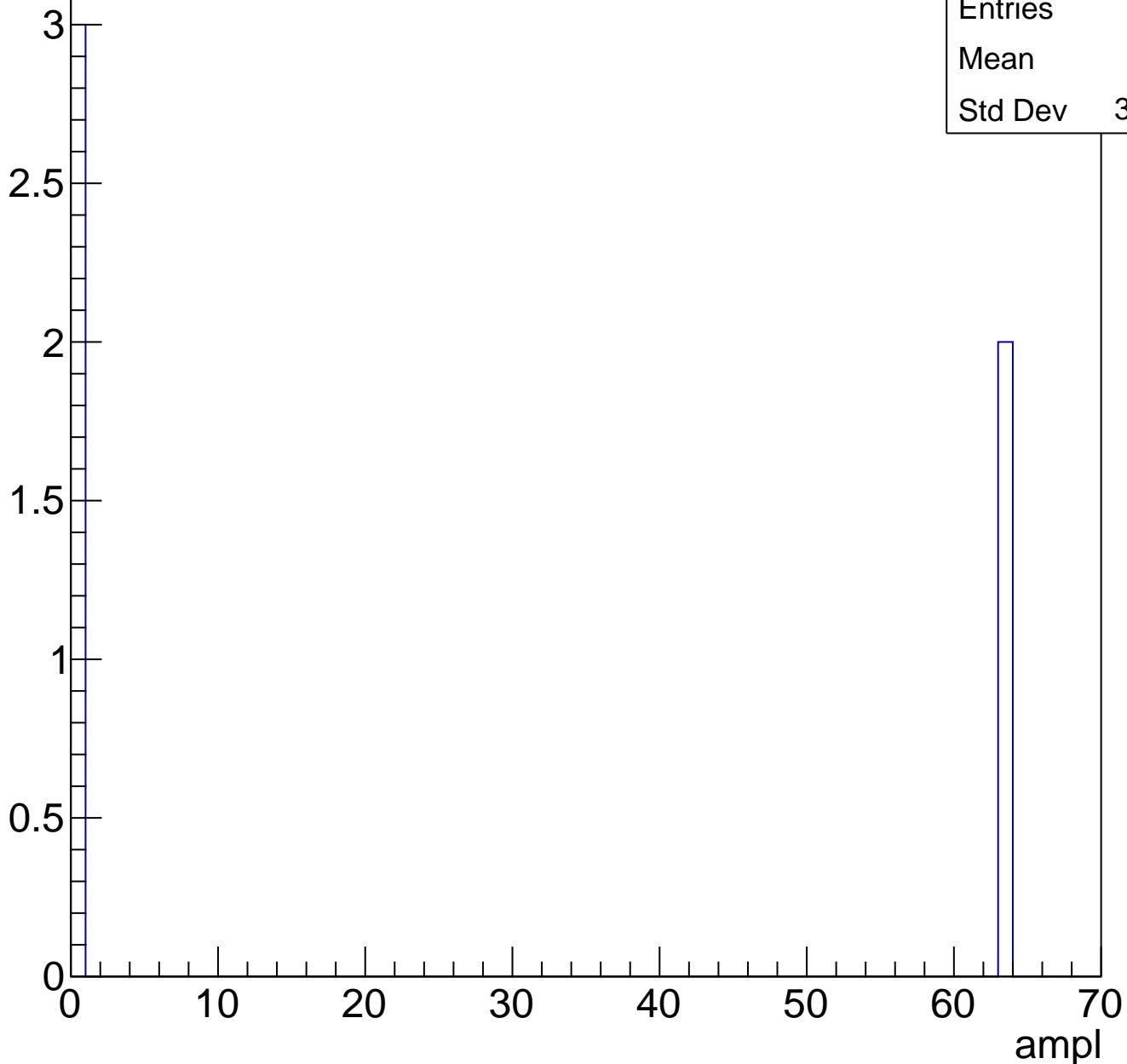




# B1L000S, U1-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



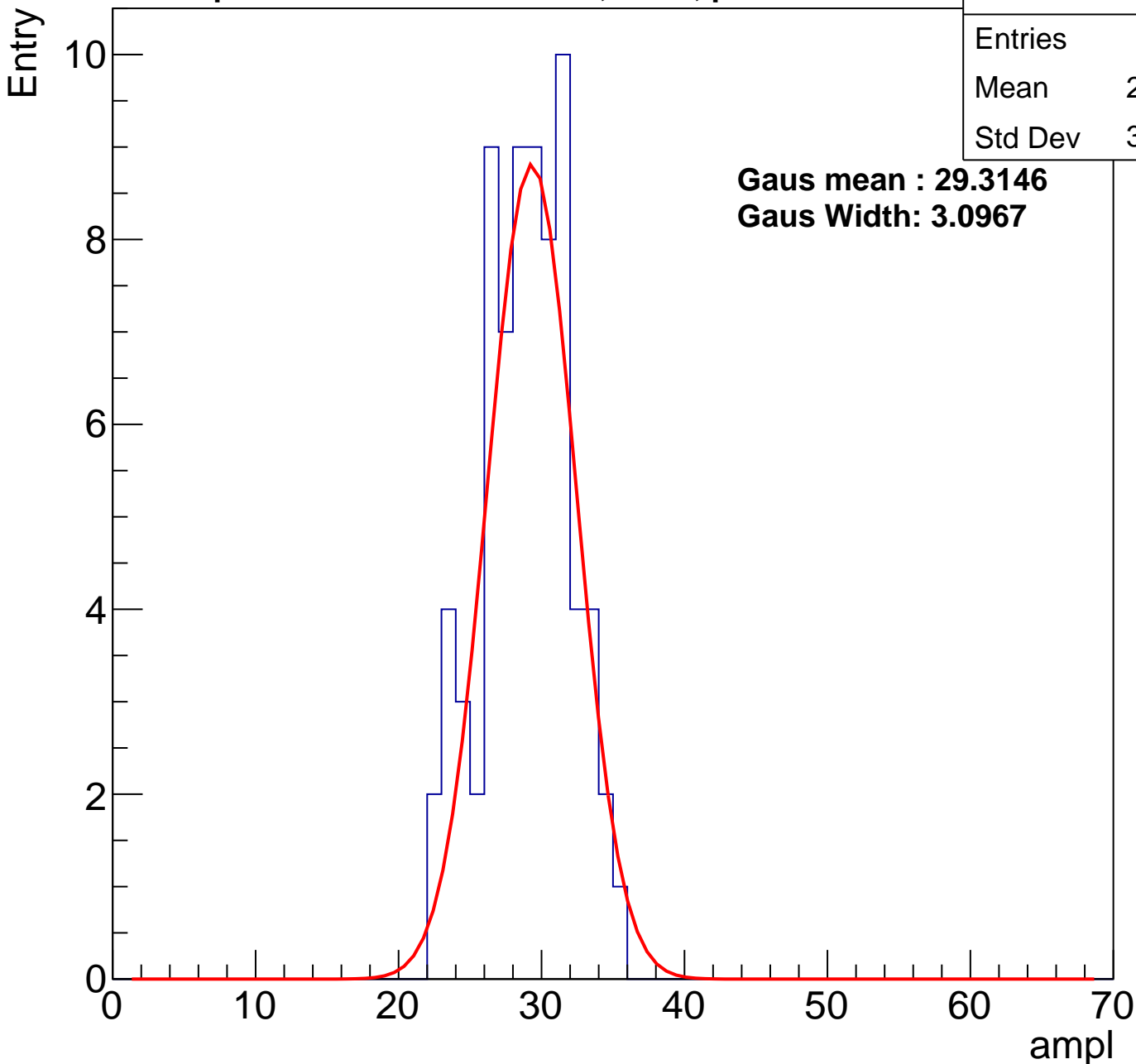
# B1L000S, U1-ch14, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	28.47
Std Dev	3.032

**Gaus mean : 29.3146**

**Gaus Width: 3.0967**



# B1L000S, U1-ch14, adc1

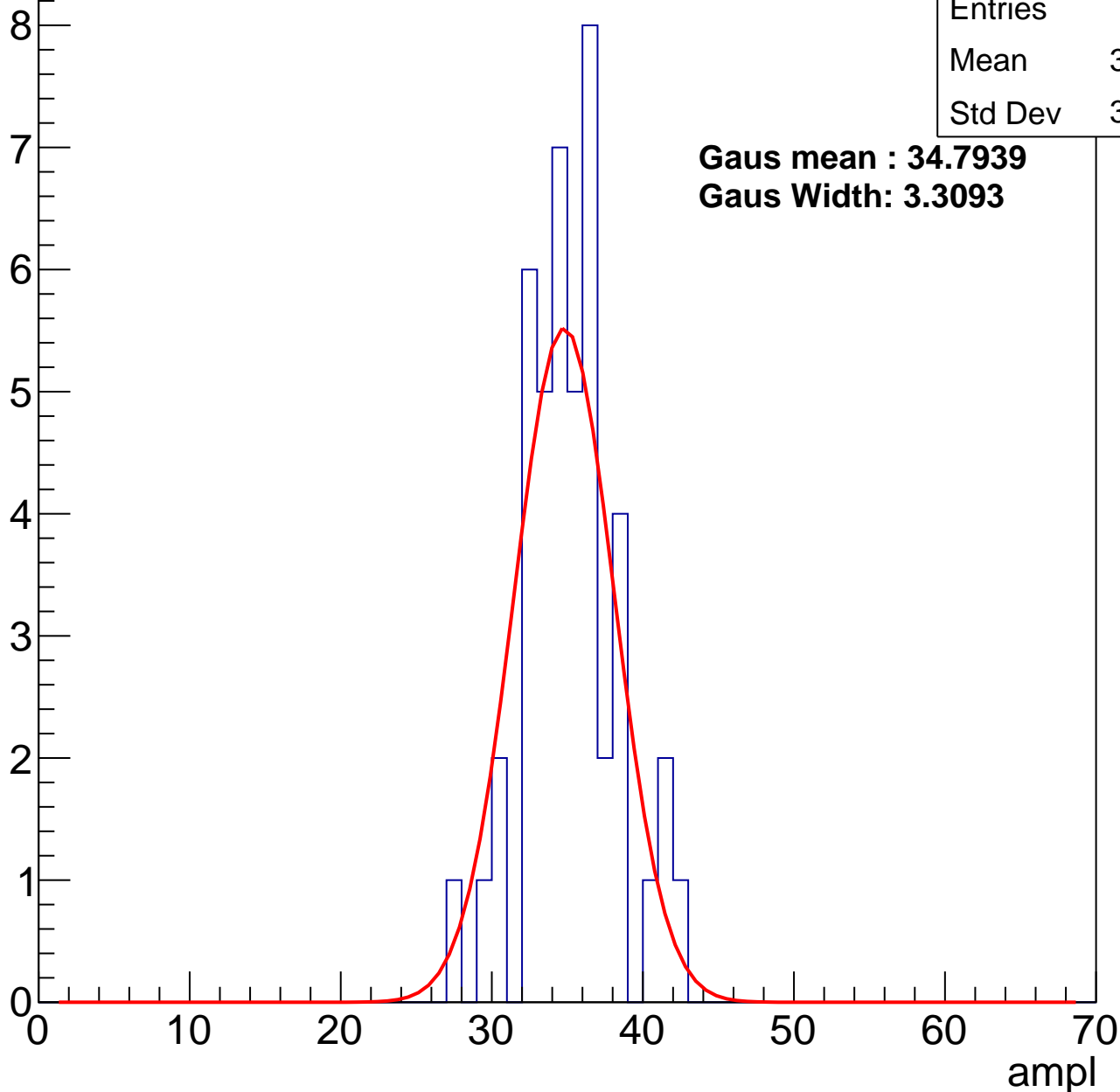
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	34.76
Std Dev	3.078

**Gaus mean : 34.7939**

**Gaus Width: 3.3093**



# B1L000S, U1-ch14, adc2

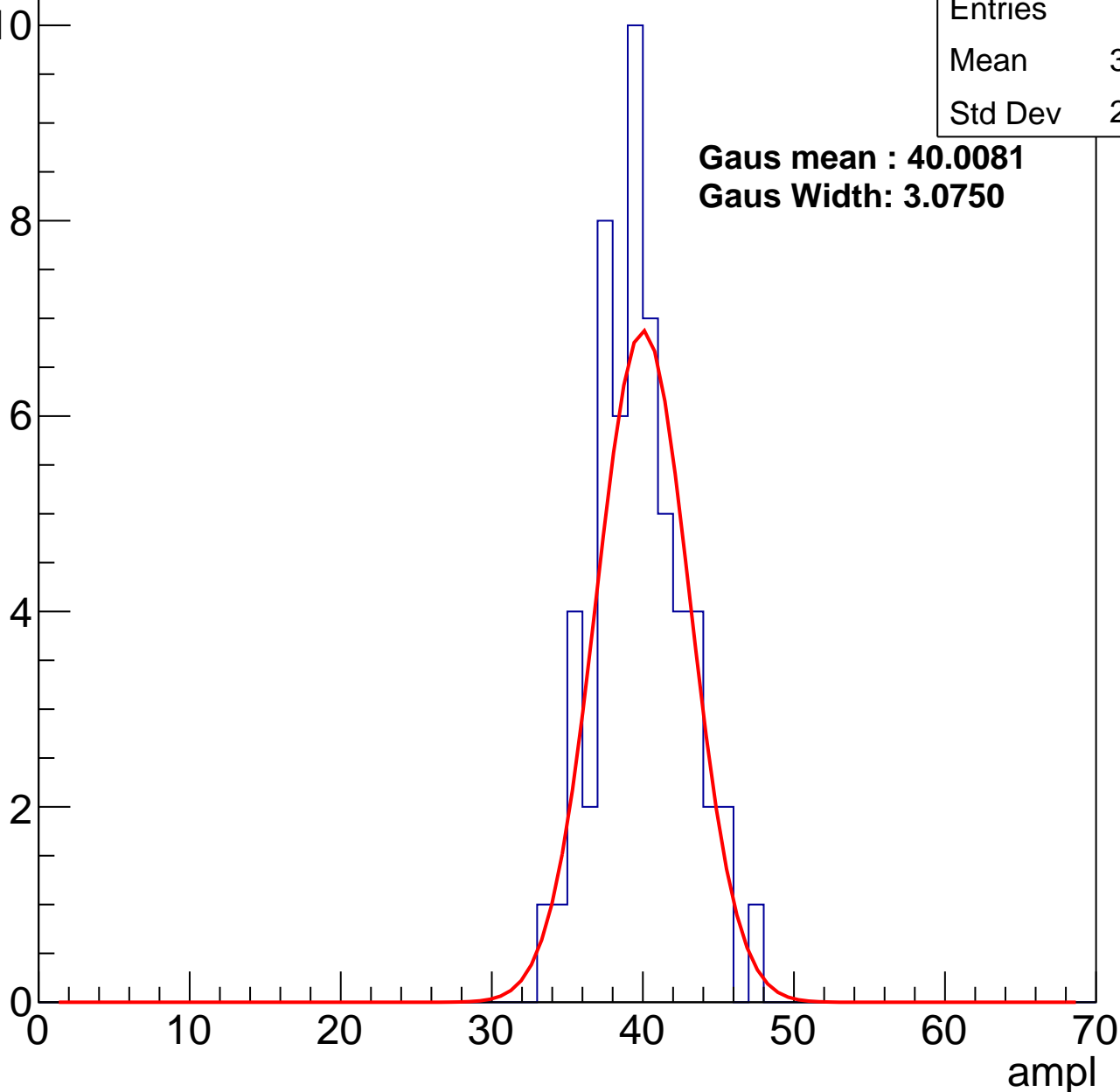
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	39.35
Std Dev	2.923

**Gaus mean : 40.0081**

**Gaus Width: 3.0750**



# B1L000S, U1-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	46.41
Std Dev	3.304

Entry

10

8

6

4

2

0

0

10

20

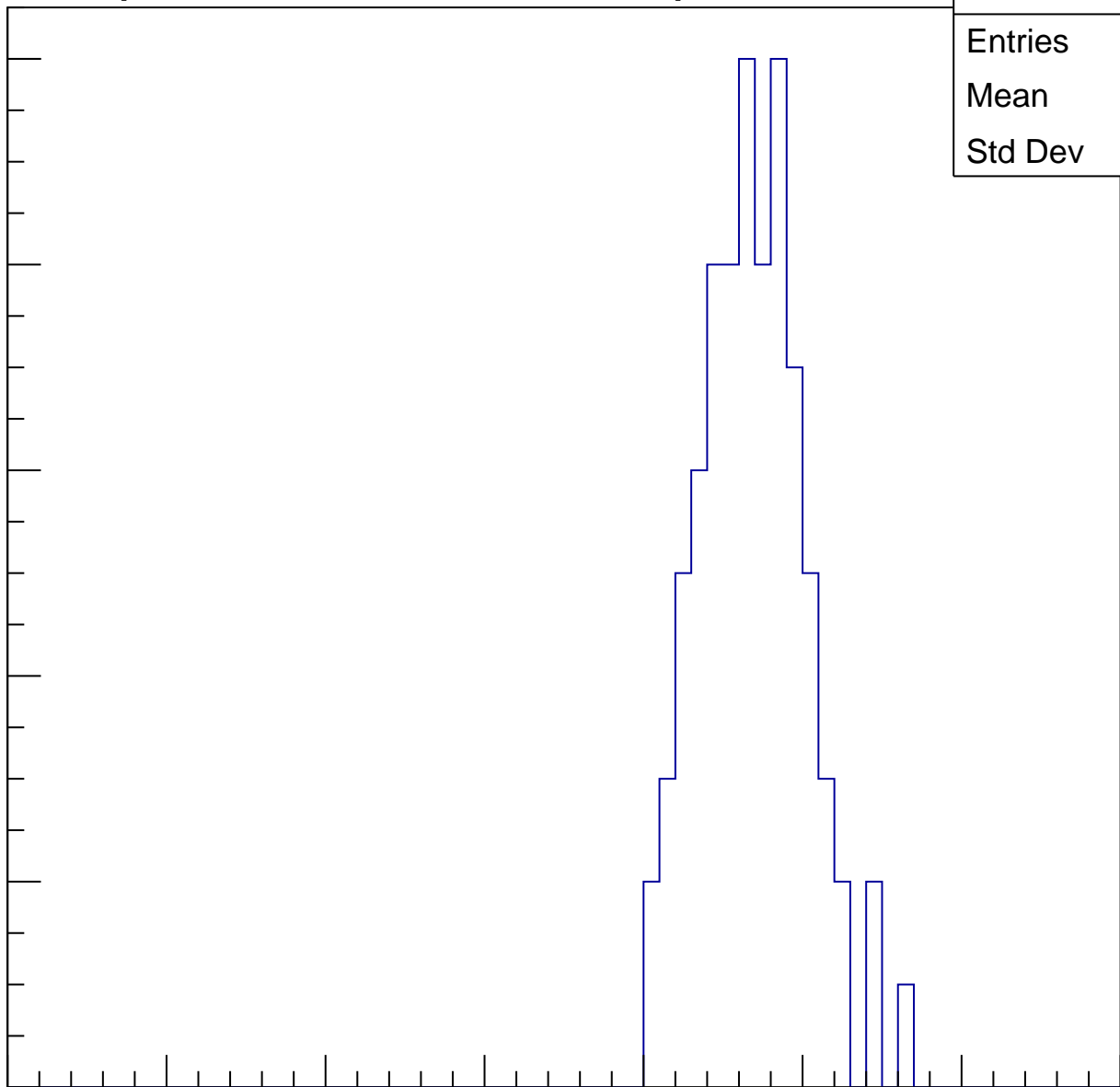
30

40

50

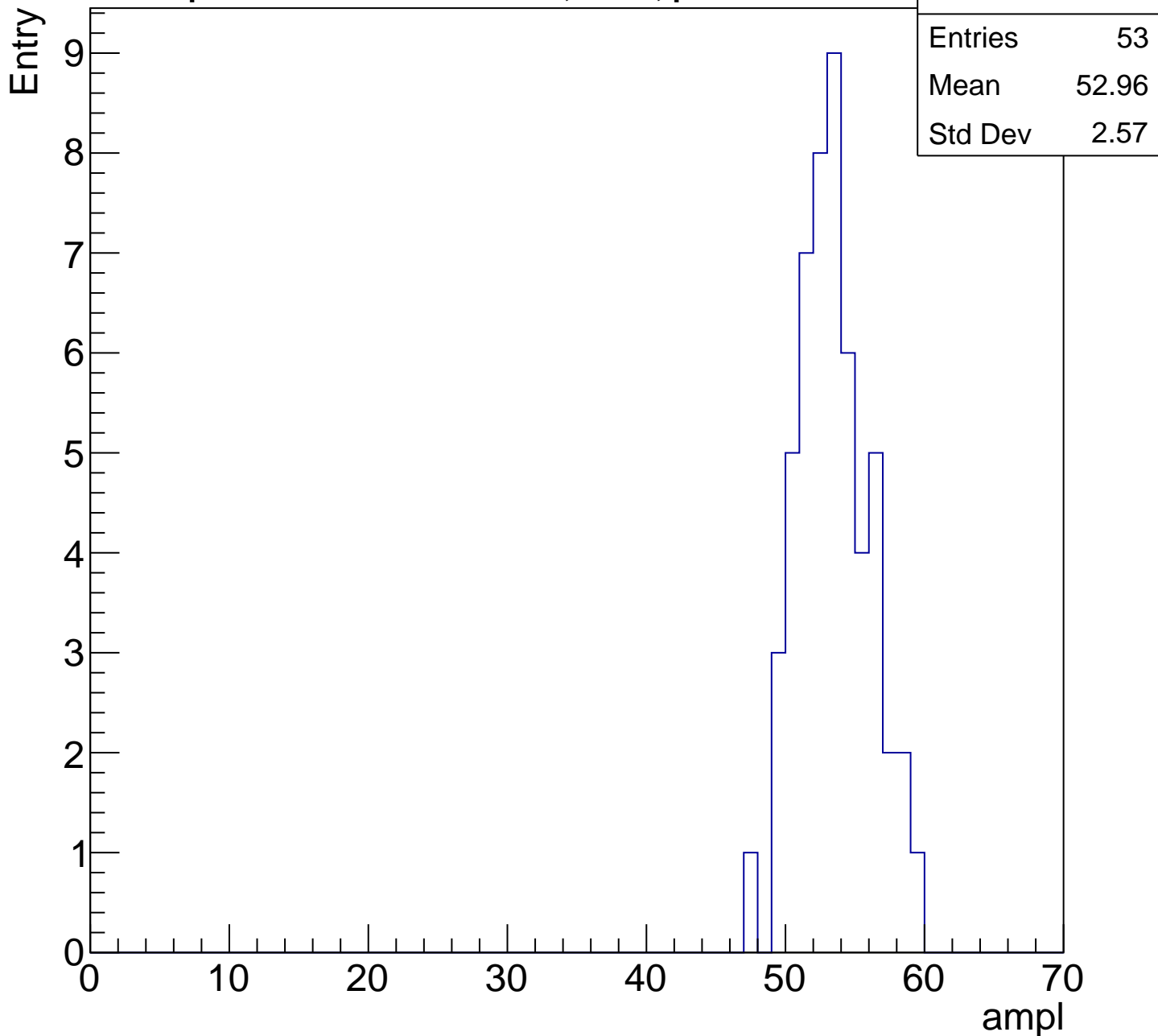
60

ampl



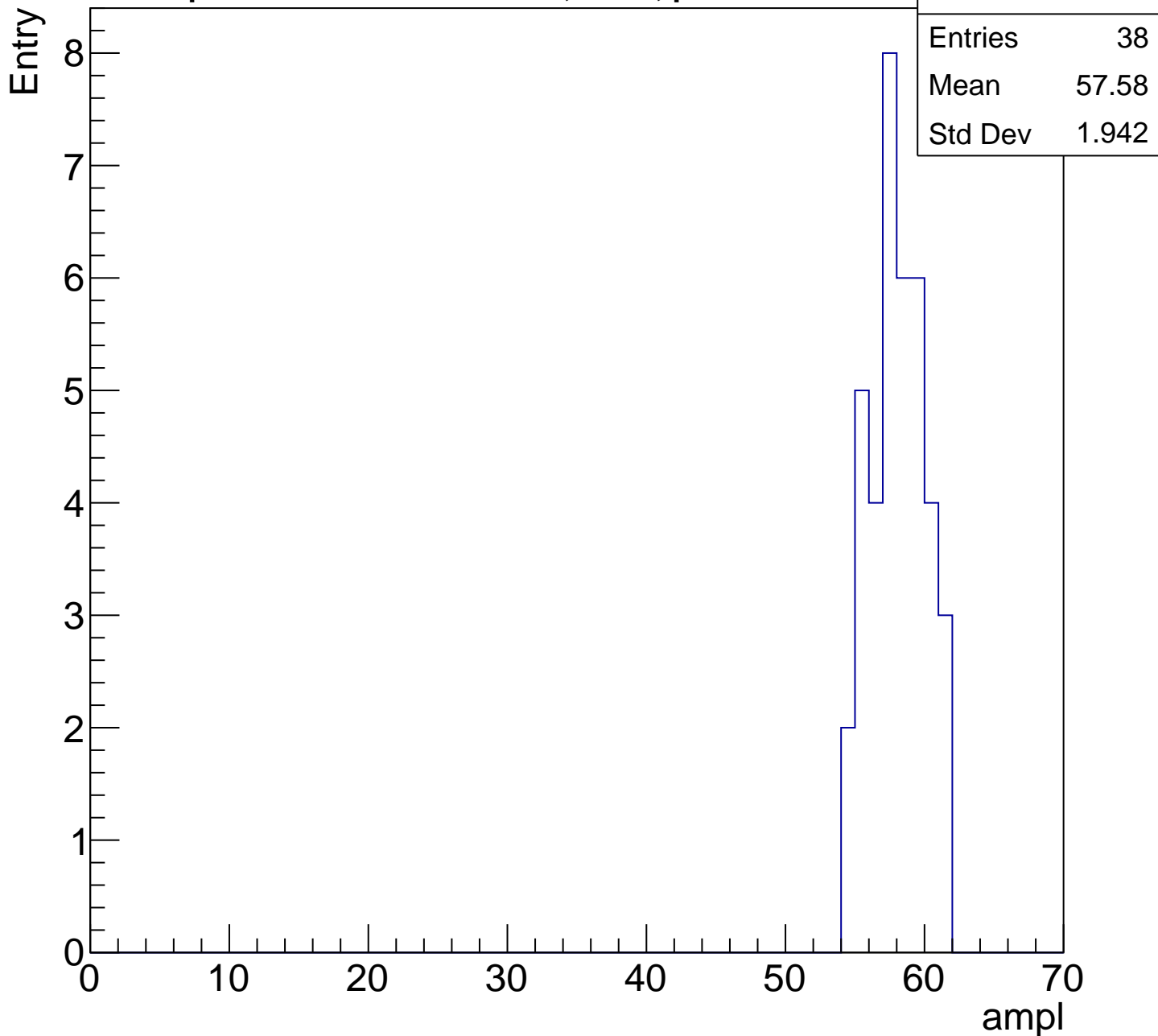
# B1L000S, U1-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

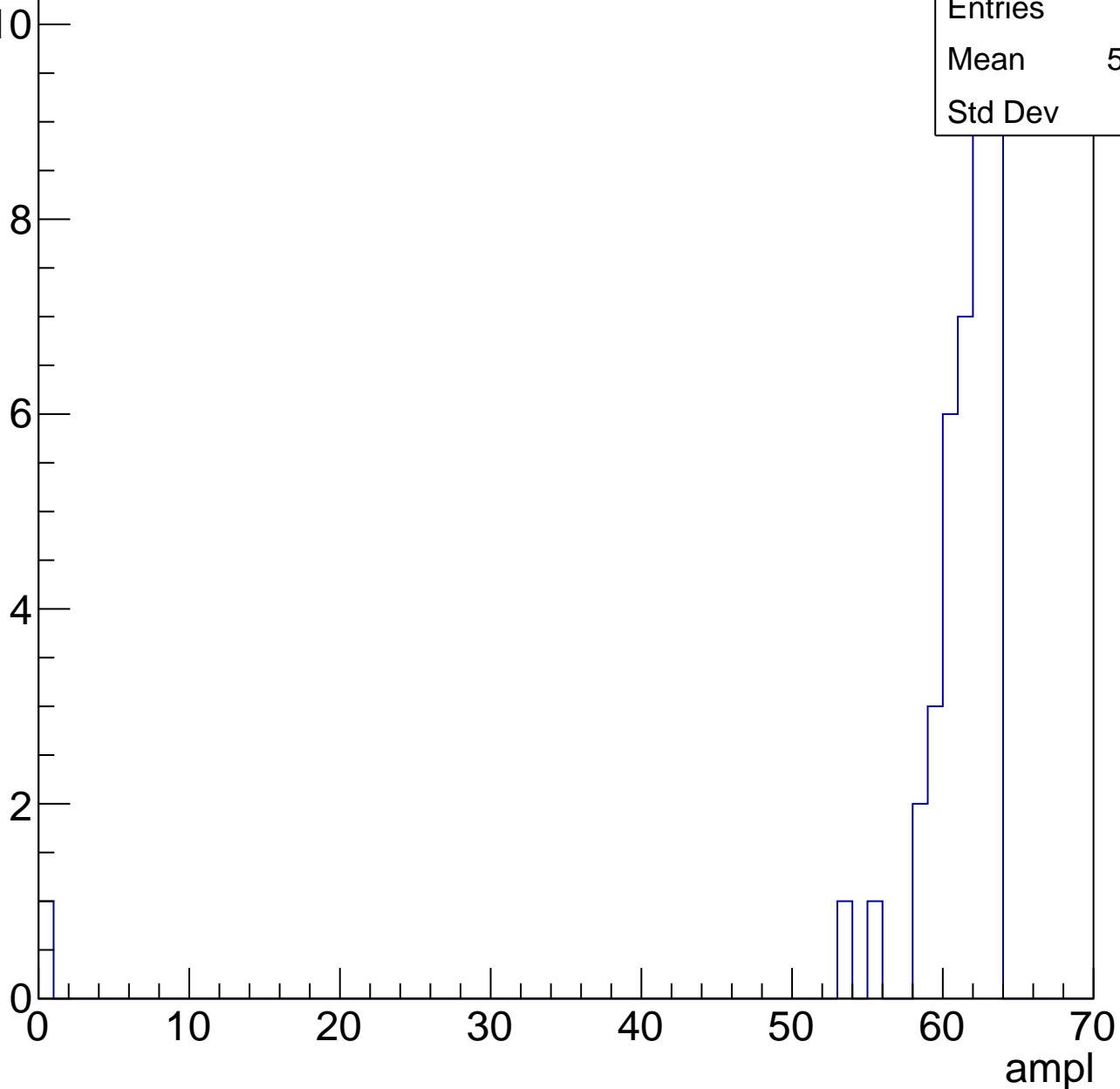


# B1L000S, U1-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	41
Mean	59.46
Std Dev	9.64





# B1L000S, U1-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch15, adc0

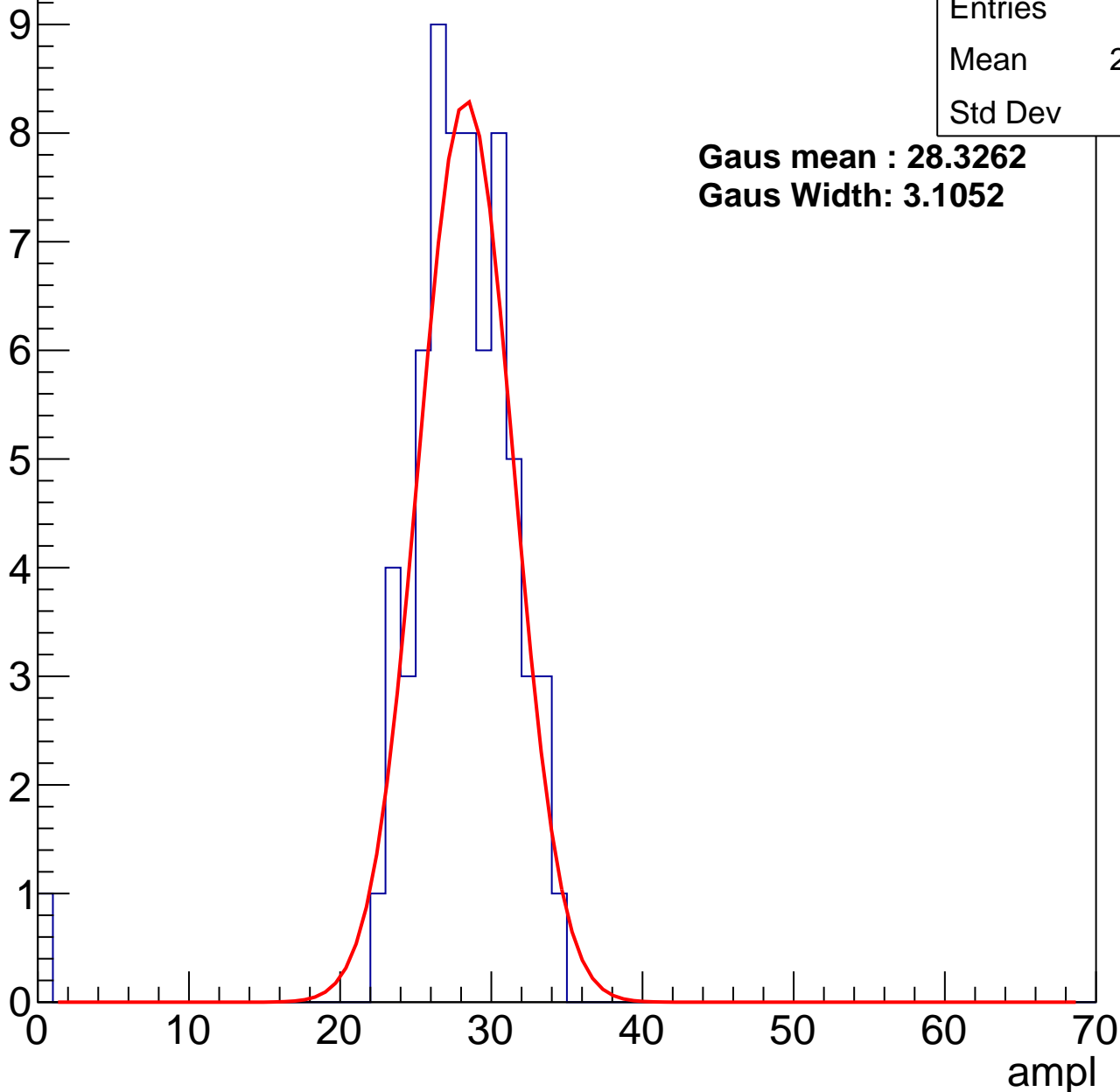
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	27.39
Std Dev	4.41

**Gaus mean : 28.3262**

**Gaus Width: 3.1052**



# B1L000S, U1-ch15, adc1

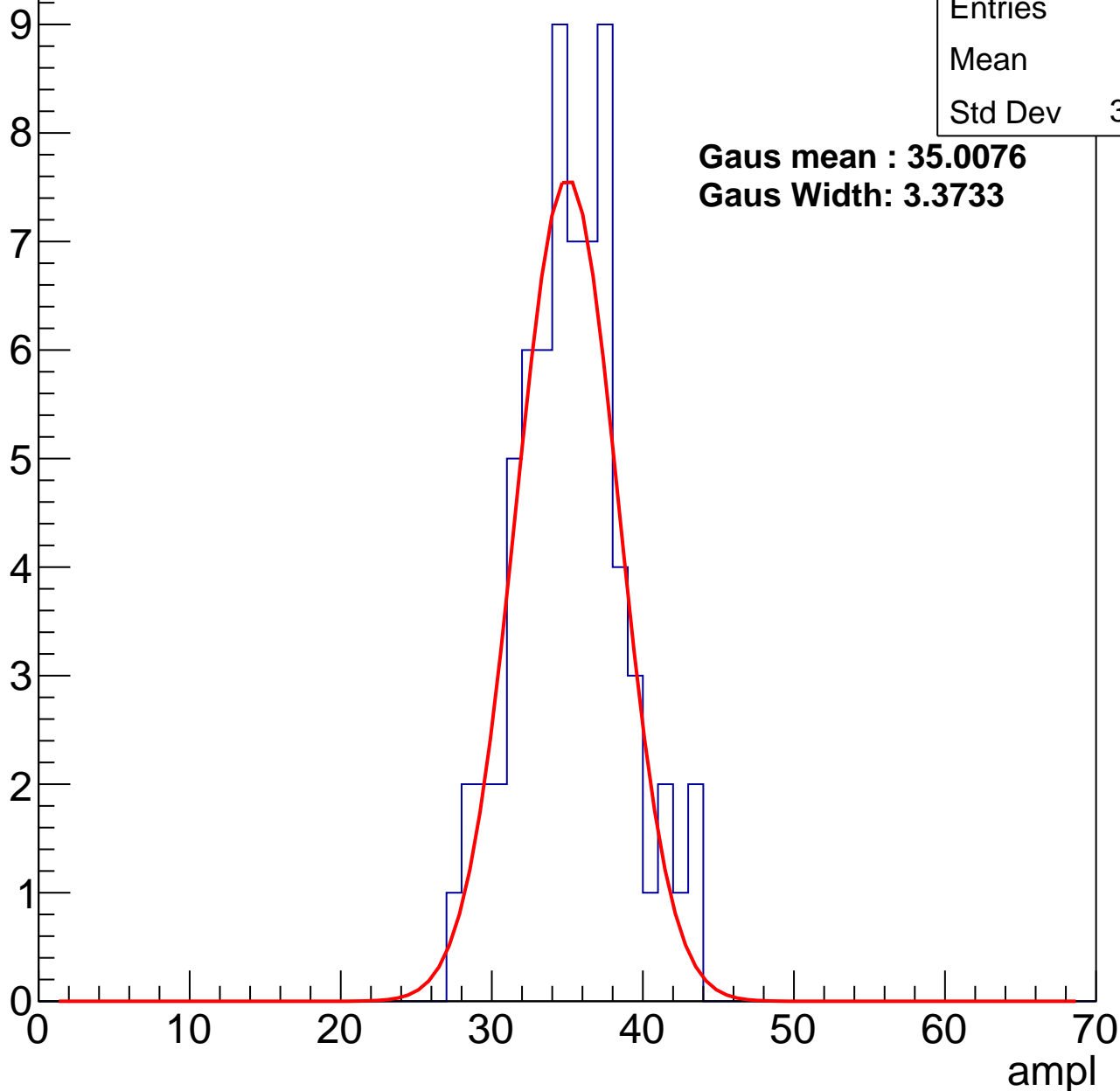
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.8
Std Dev	3.508

**Gaus mean : 35.0076**

**Gaus Width: 3.3733**



# B1L000S, U1-ch15, adc2

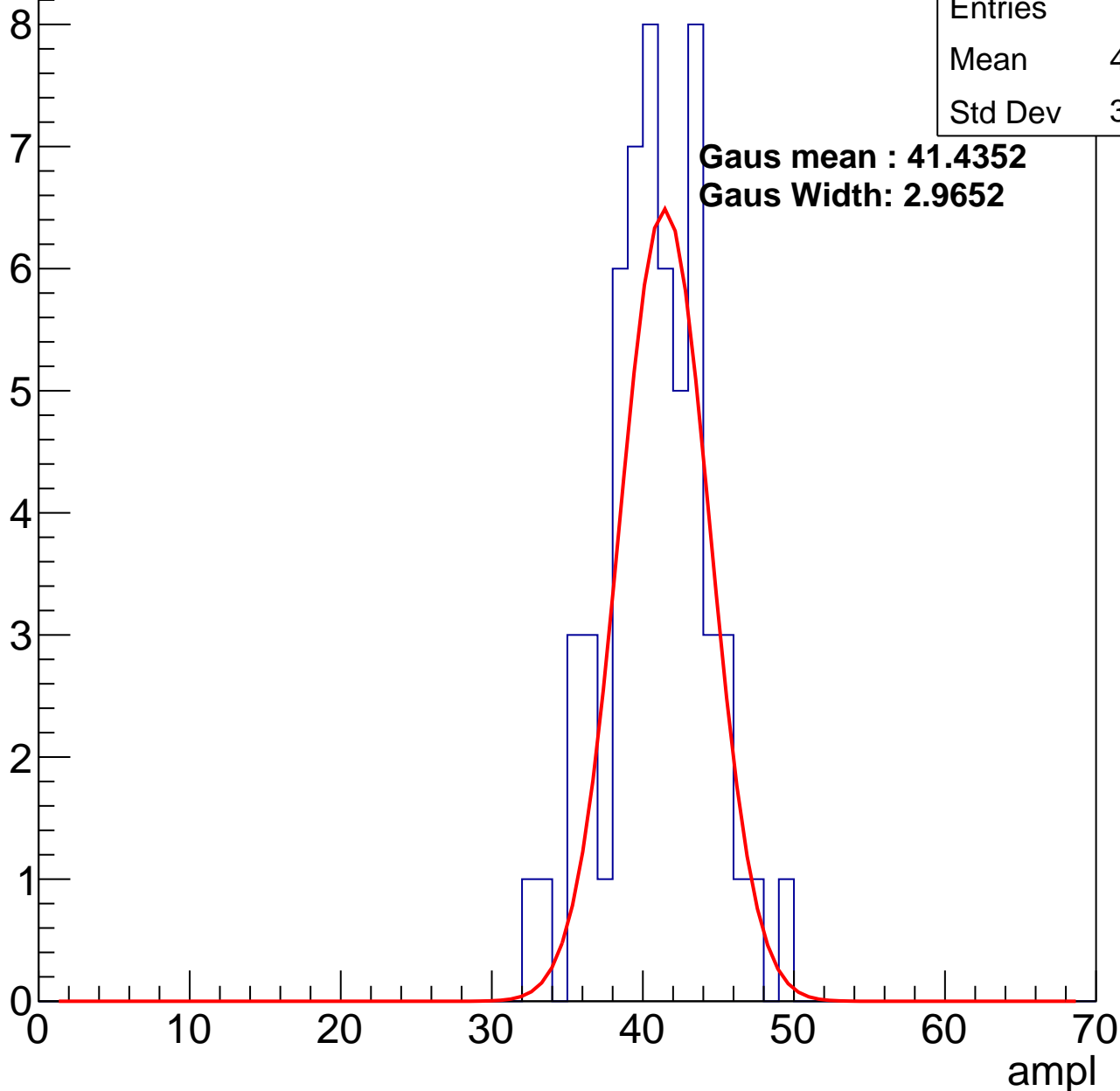
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	40.43
Std Dev	3.353

**Gaus mean : 41.4352**

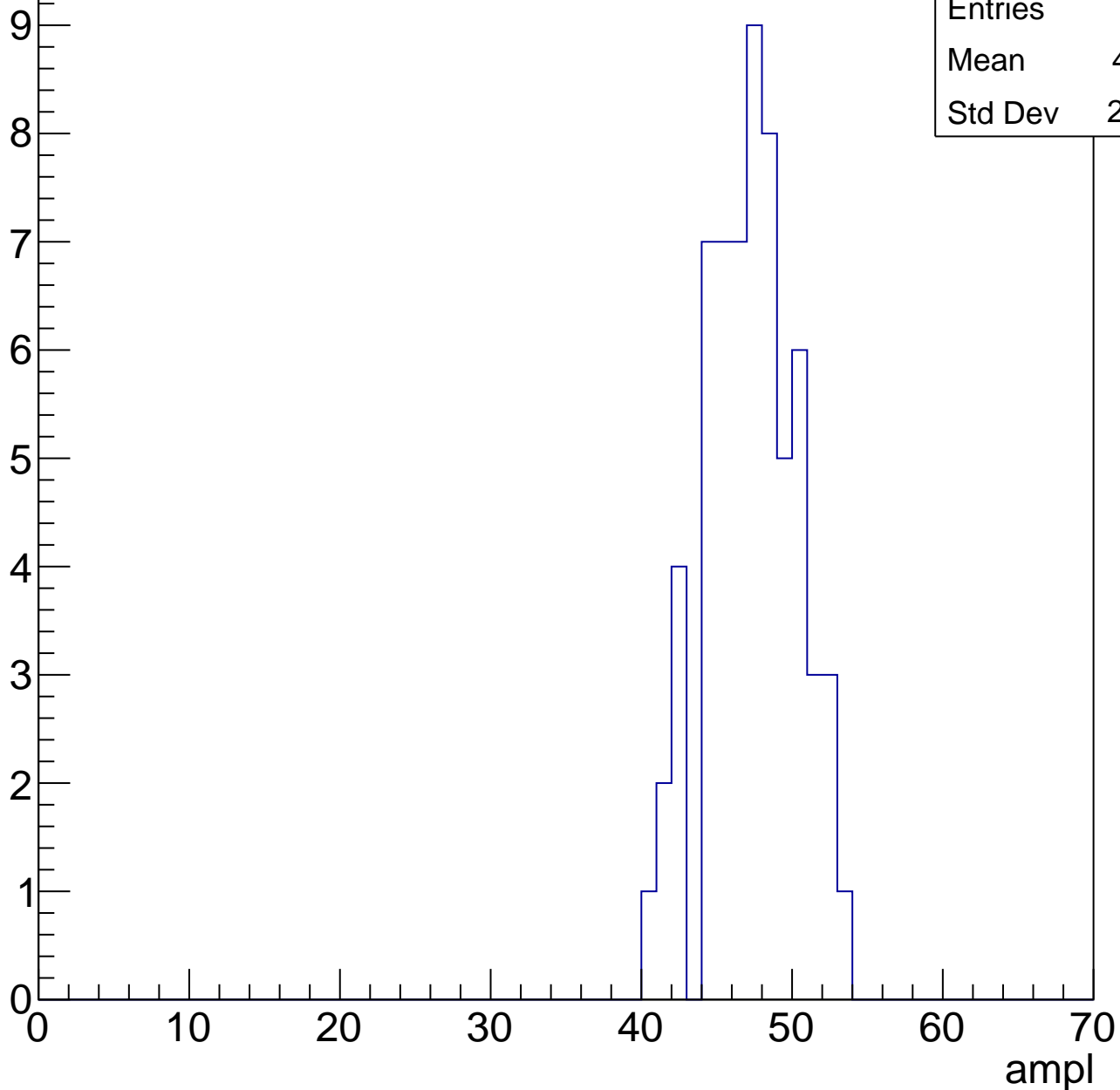
**Gaus Width: 2.9652**



# B1L000S, U1-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



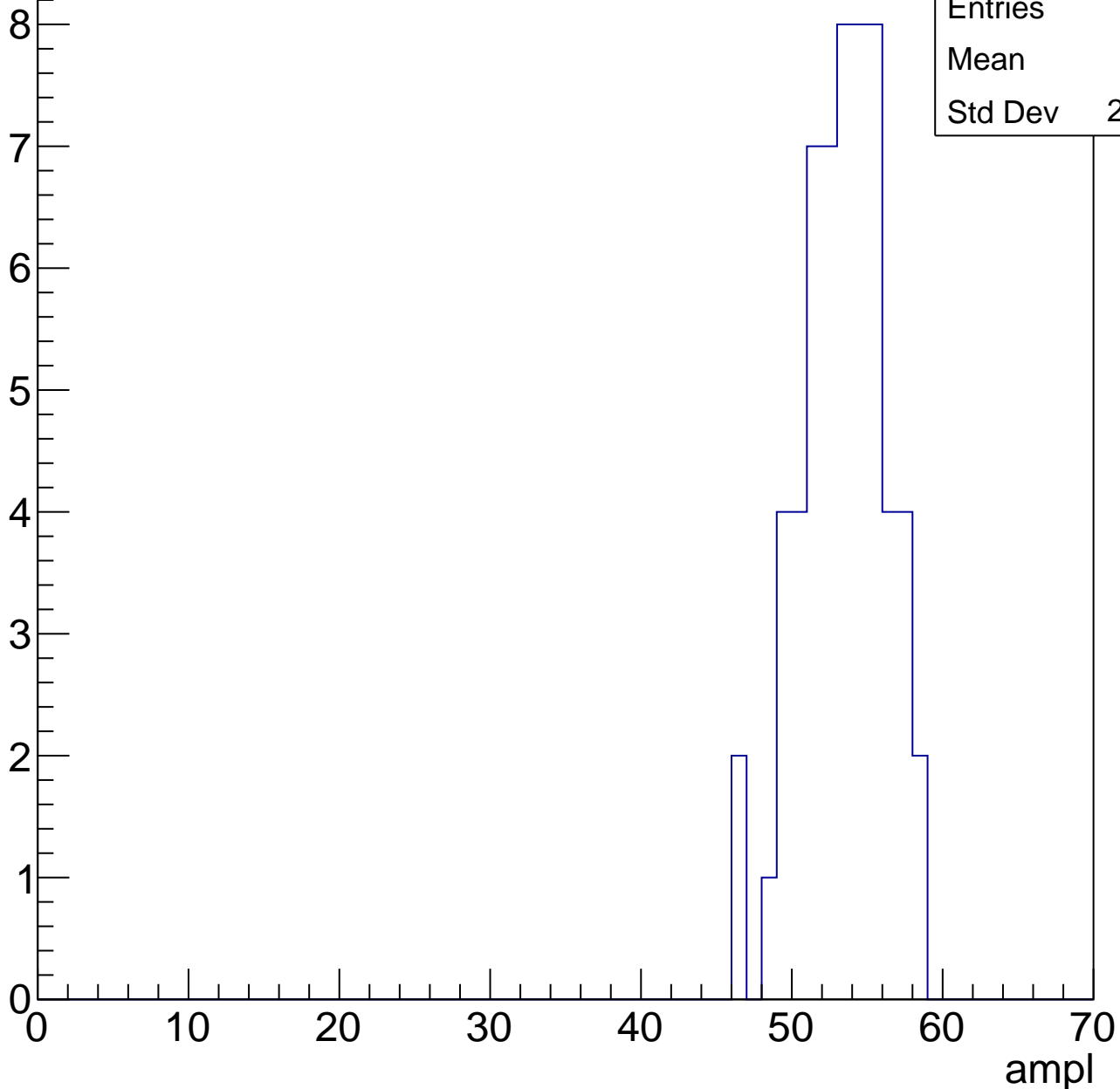
Entries	63
Mean	46.81
Std Dev	2.975

# B1L000S, U1-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

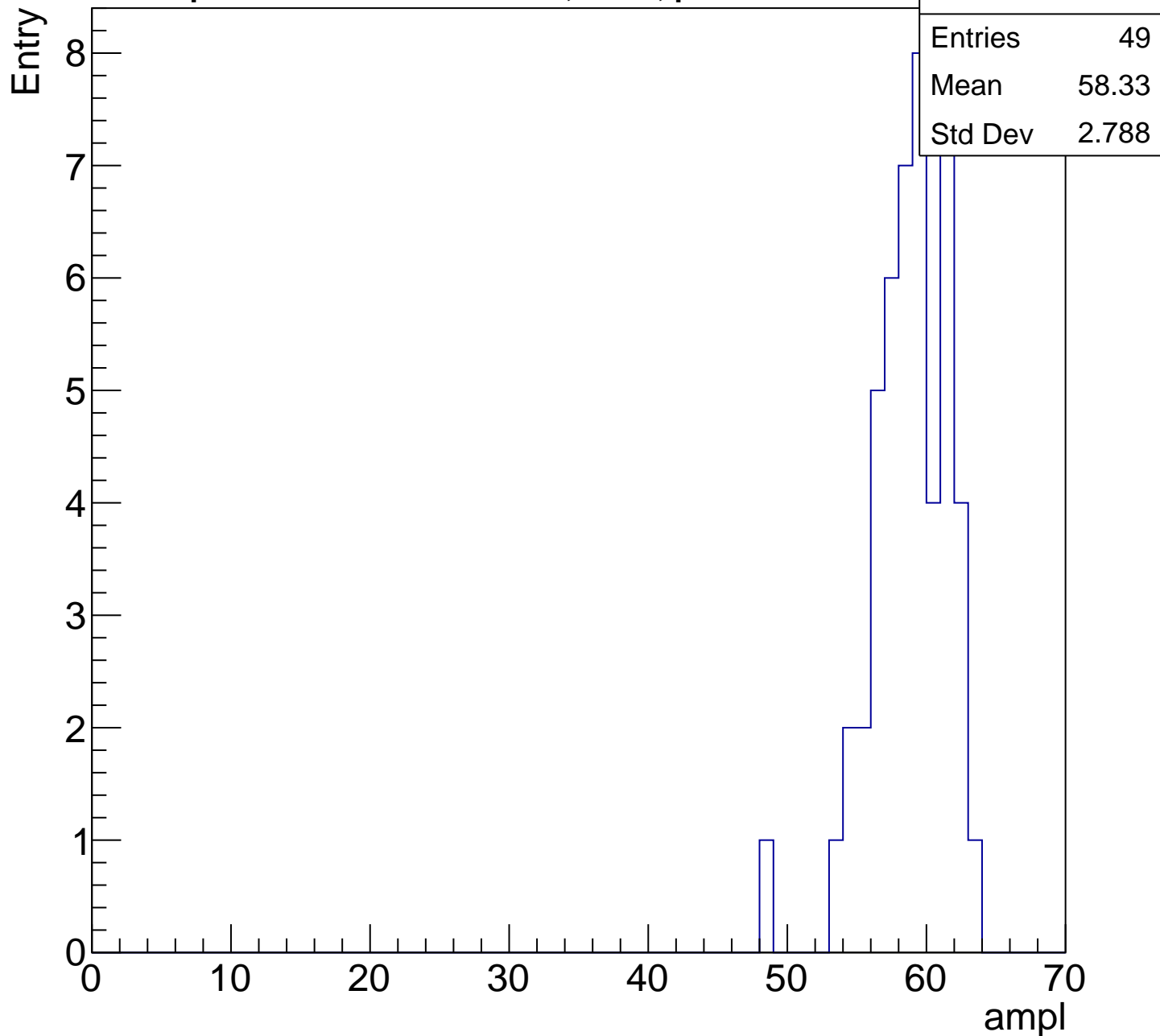
Entry

Entries	59
Mean	52.9
Std Dev	2.754



# B1L000S, U1-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

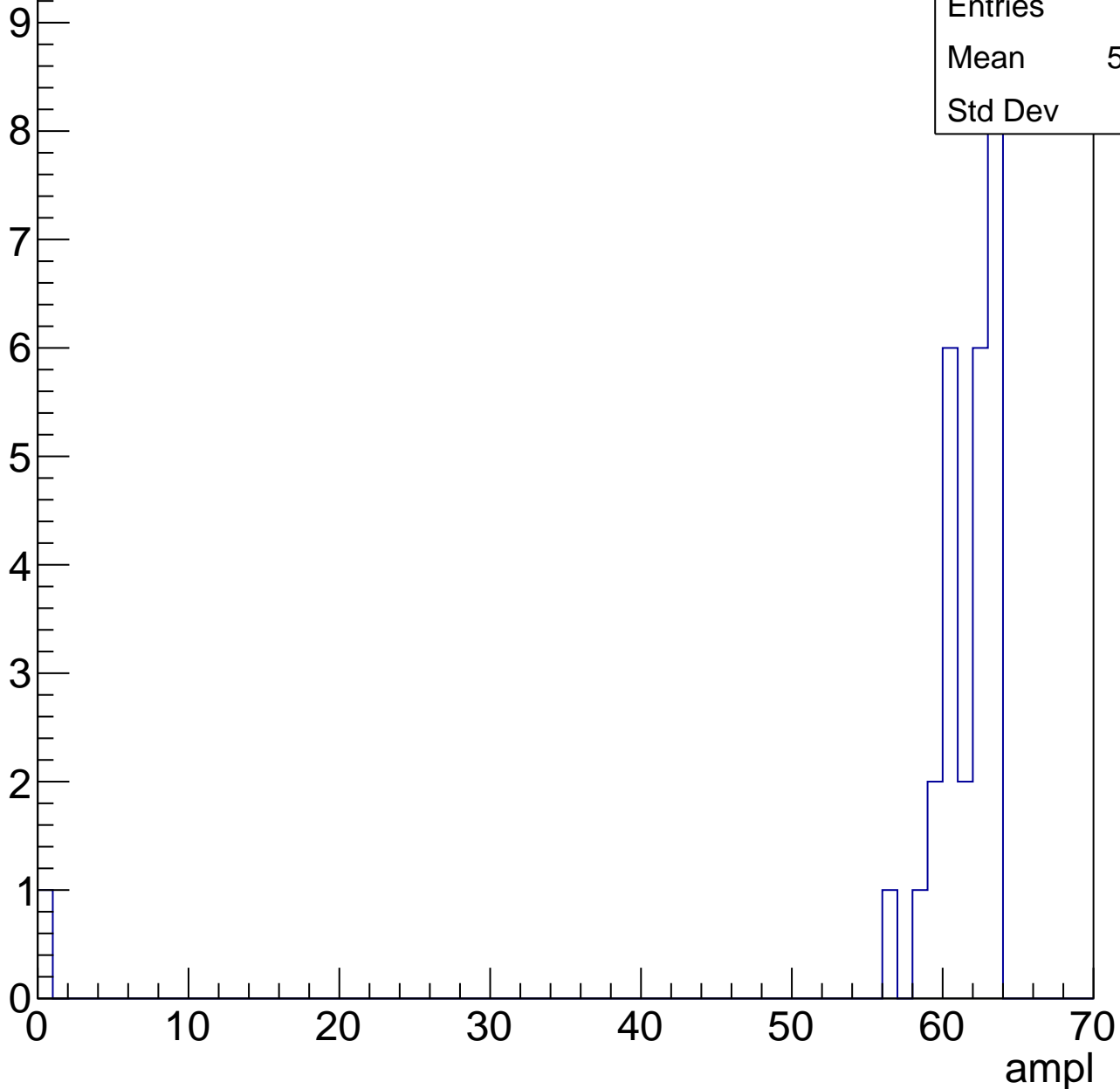


# B1L000S, U1-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	59.04
Std Dev	11.5

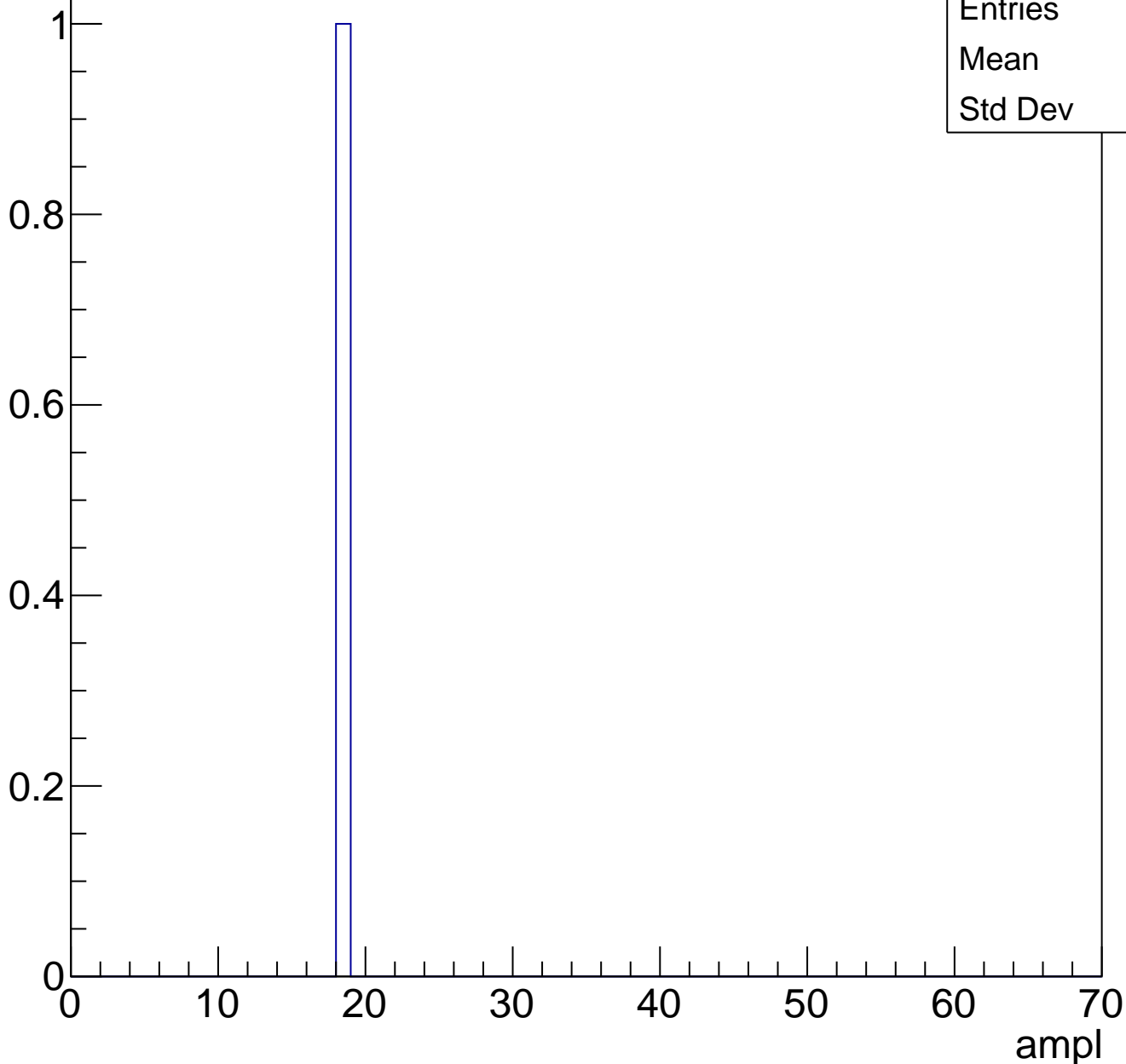




# B1L000S, U1-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	18
Std Dev	0

# B1L000S, U1-ch16, adc0

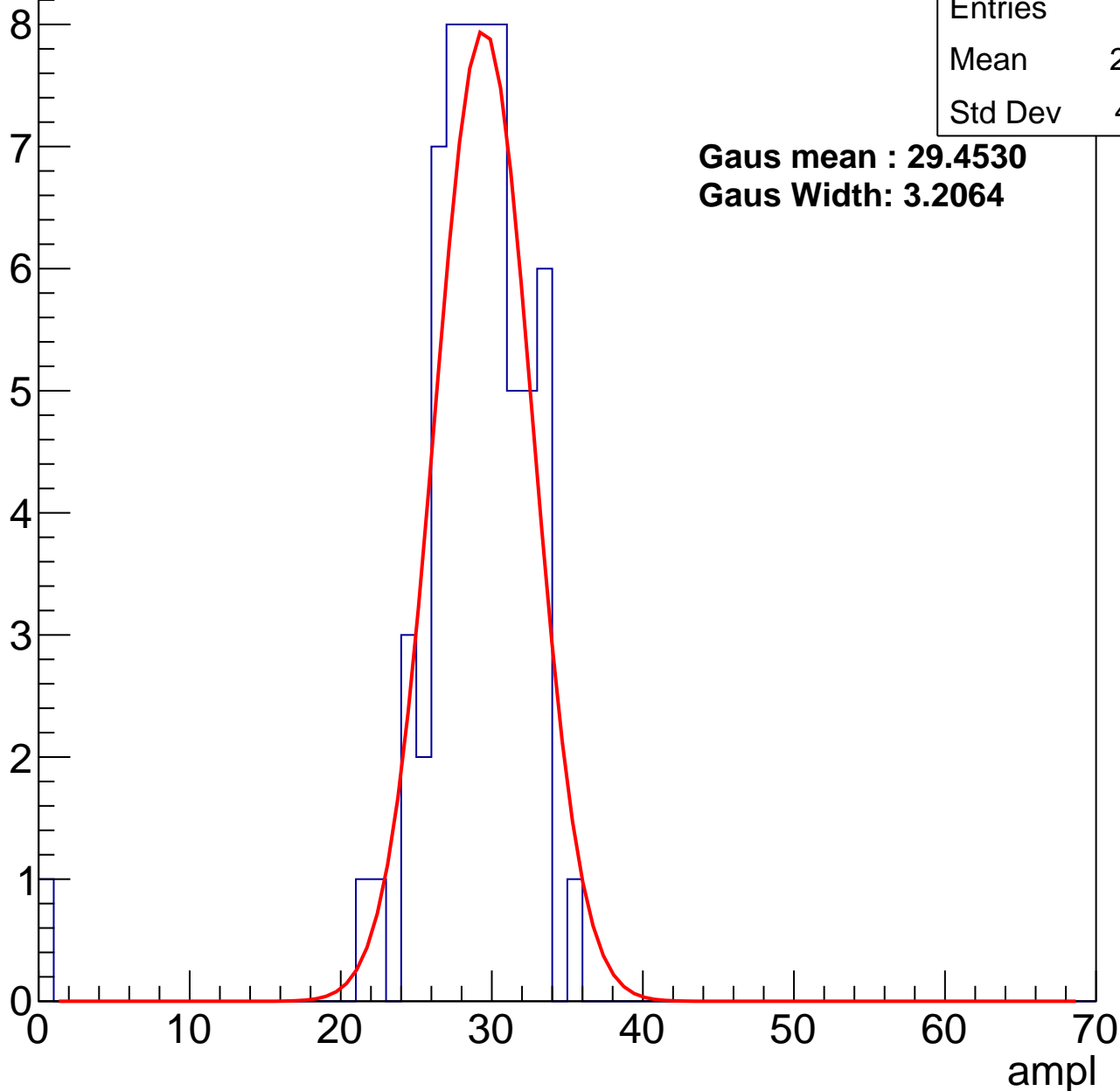
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.23
Std Dev	4.561

**Gaus mean : 29.4530**

**Gaus Width: 3.2064**



# B1L000S, U1-ch16, adc1

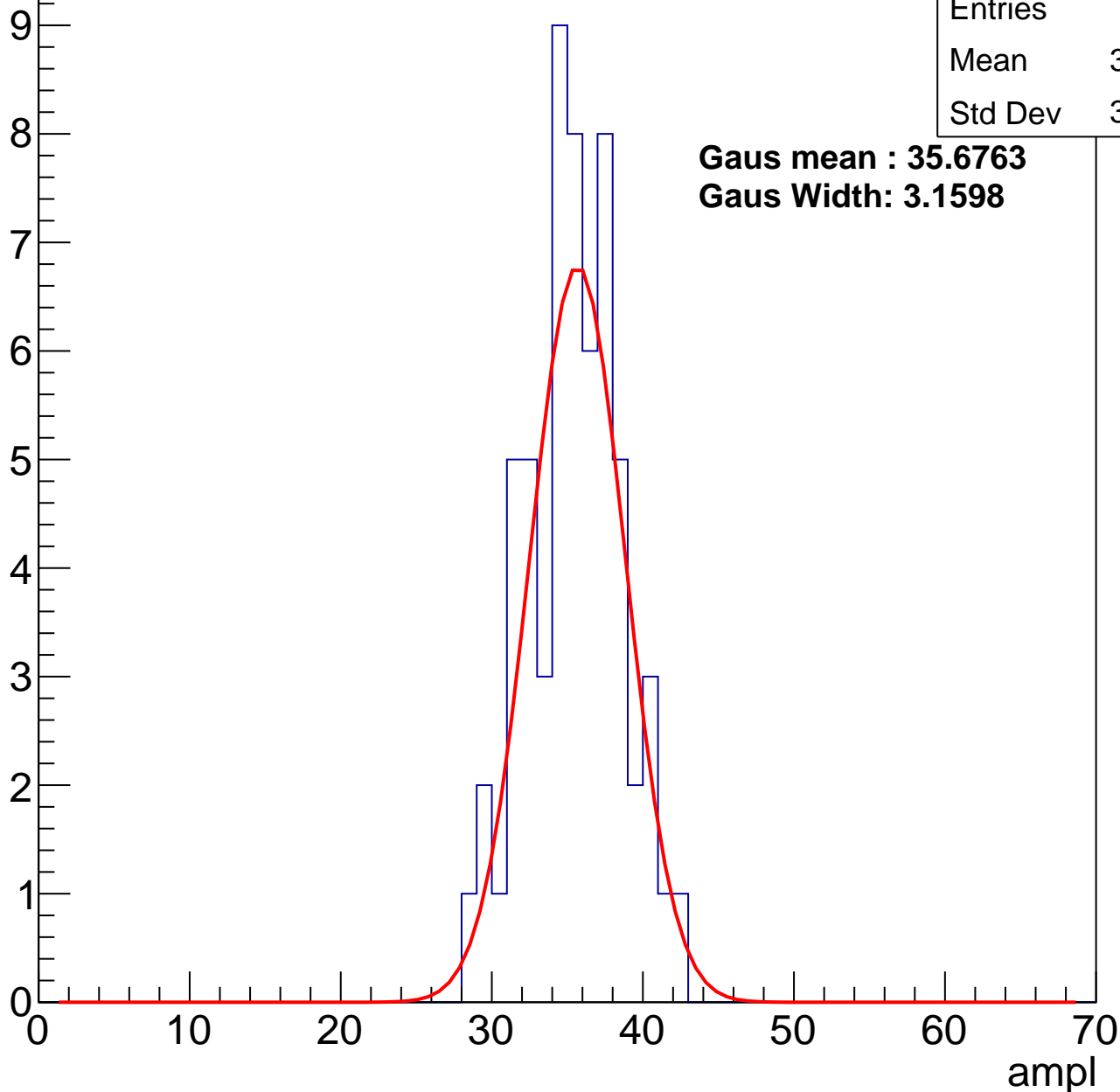
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	34.98
Std Dev	3.074

**Gaus mean : 35.6763**

**Gaus Width: 3.1598**



# B1L000S, U1-ch16, adc2

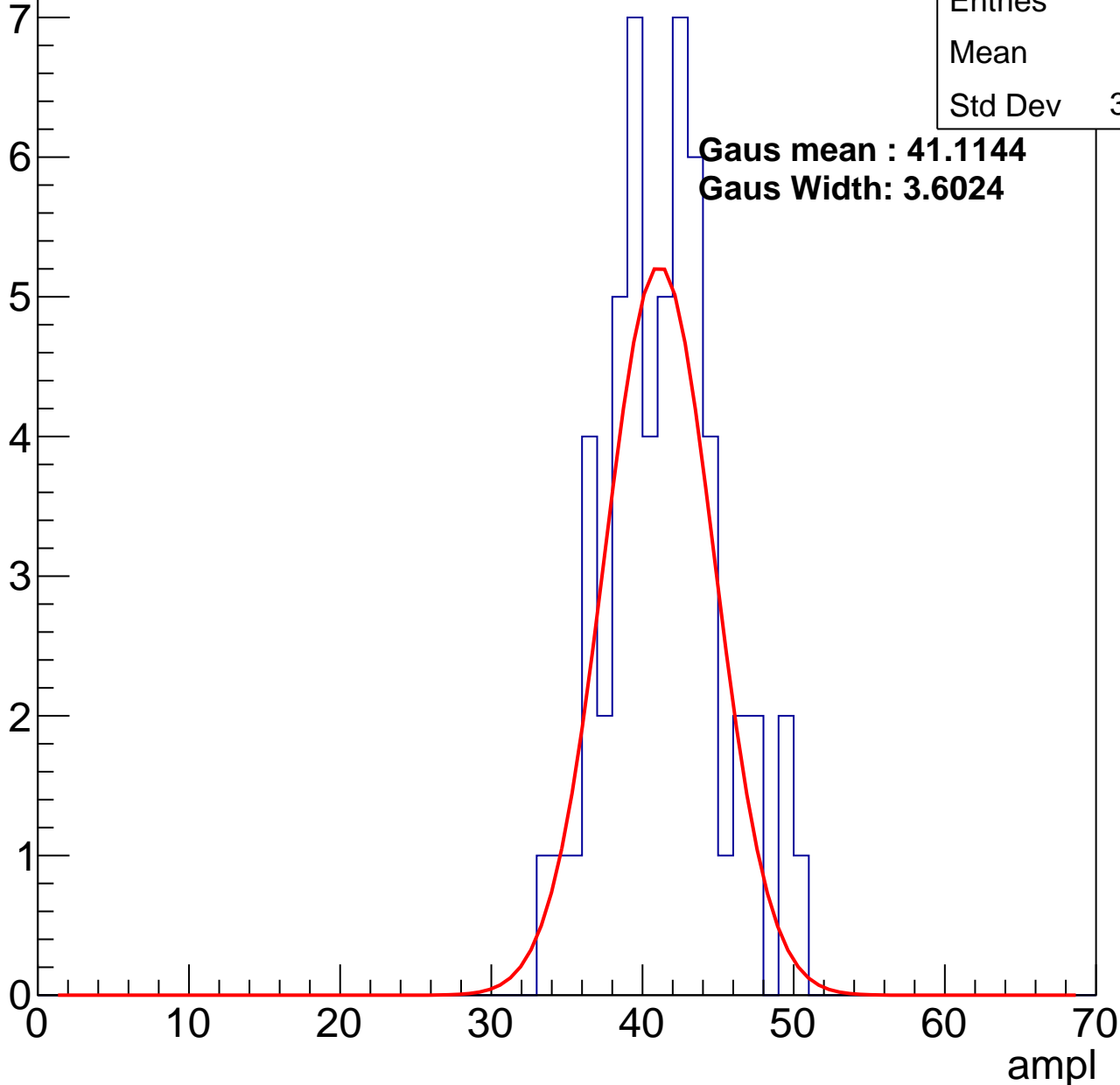
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41
Std Dev	3.747

**Gaus mean : 41.1144**

**Gaus Width: 3.6024**

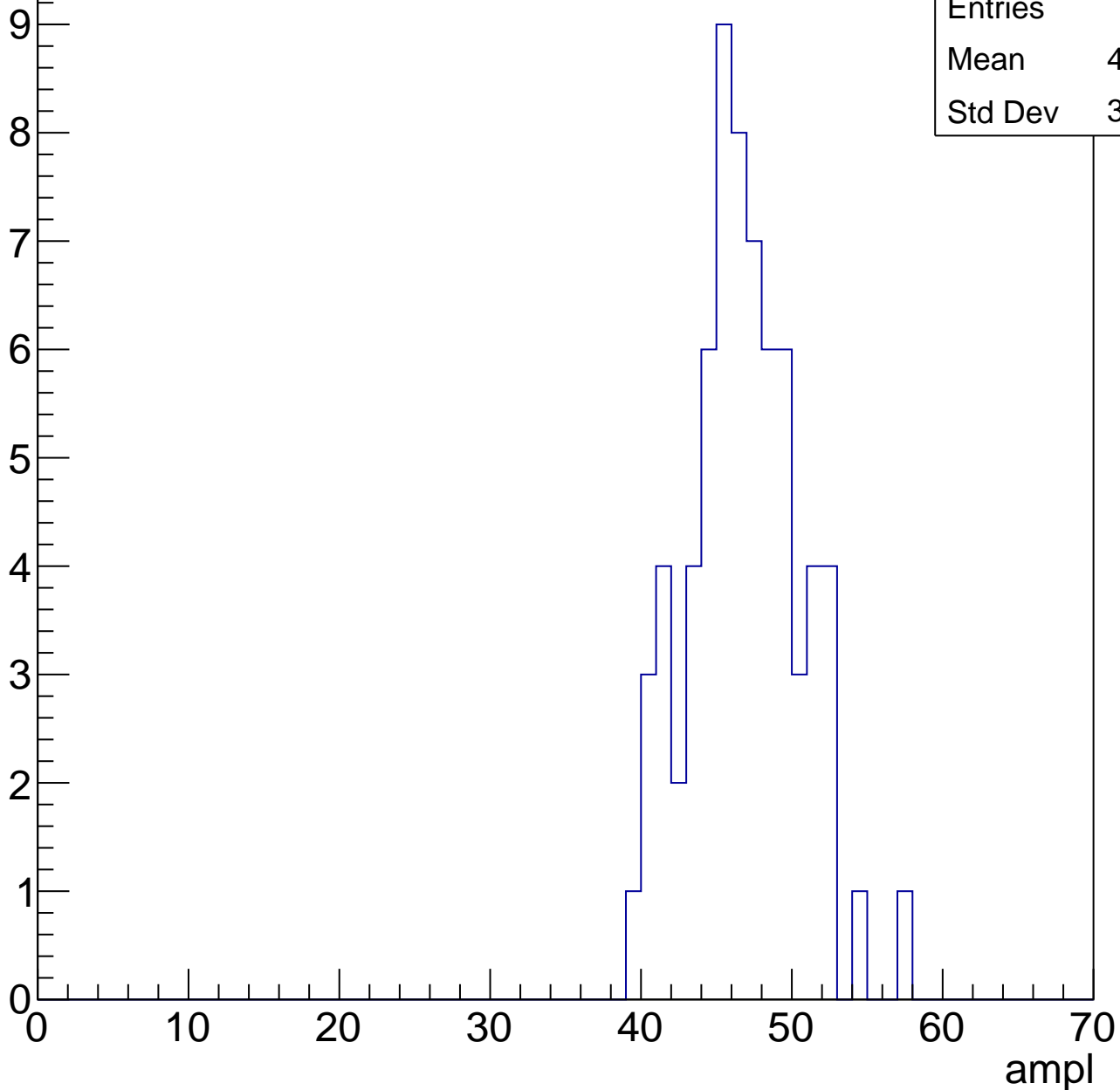


# B1L000S, U1-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

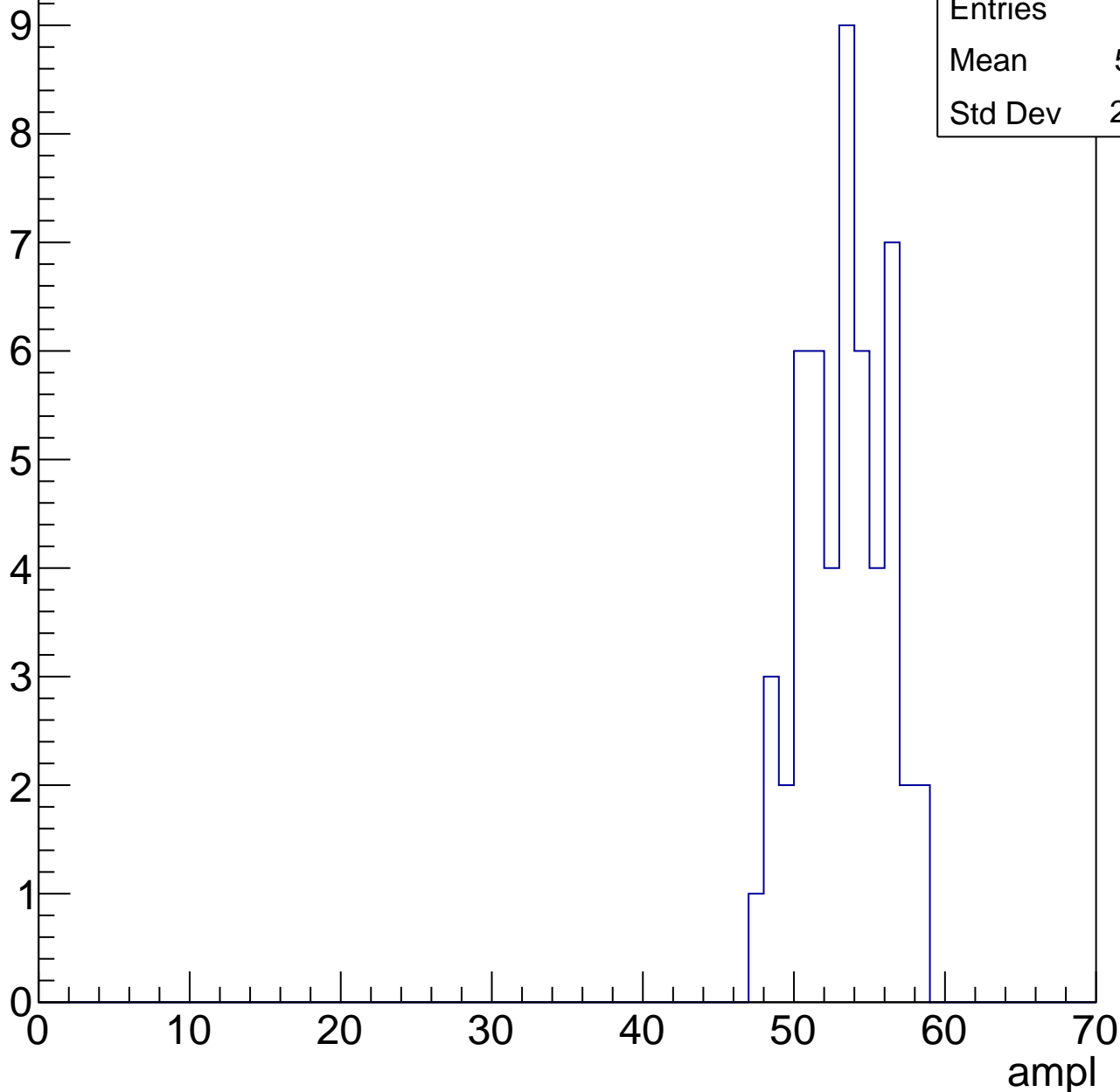
Entries	69
Mean	46.38
Std Dev	3.632



# B1L000S, U1-ch16, adc4

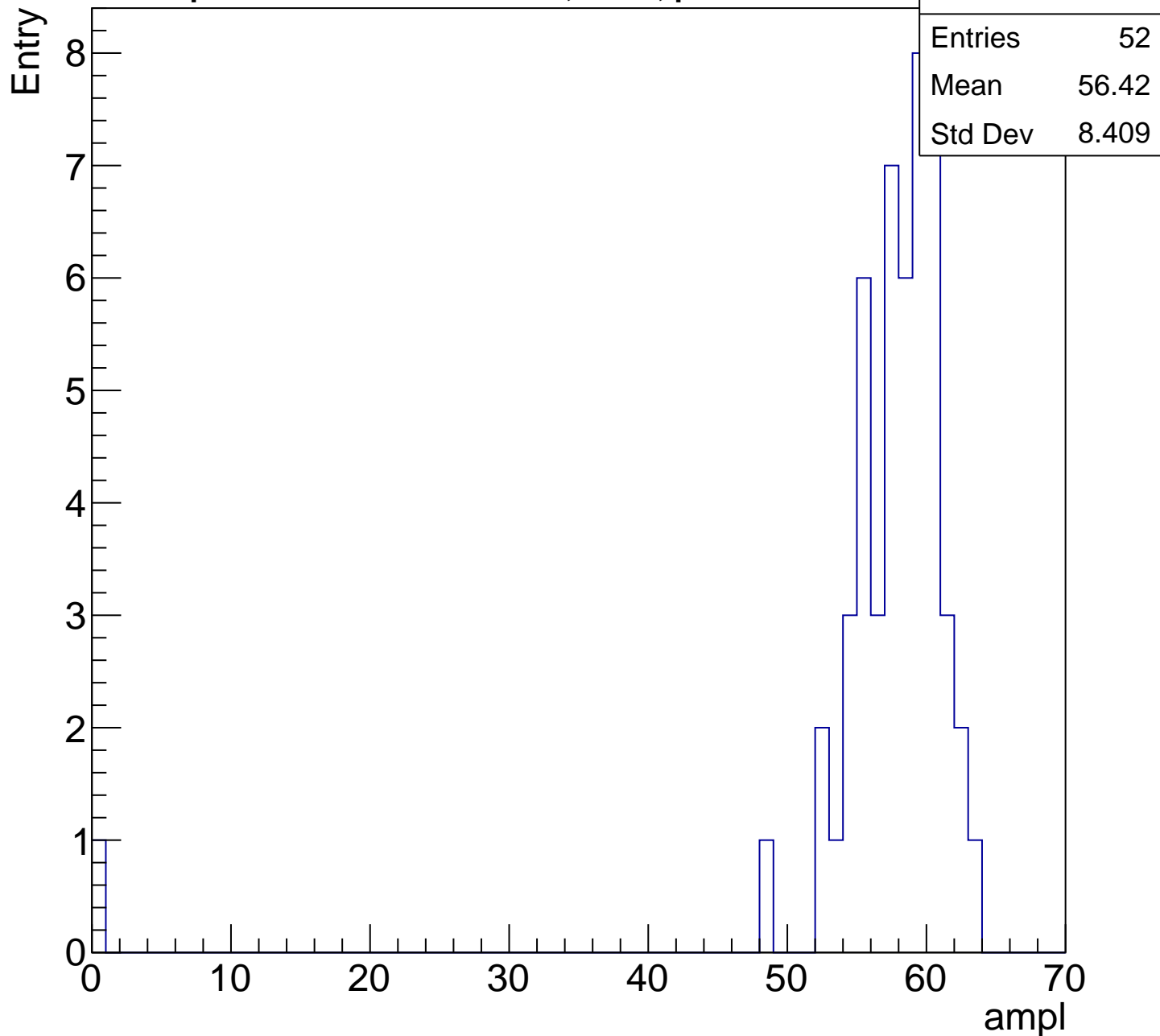
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch16, adc6

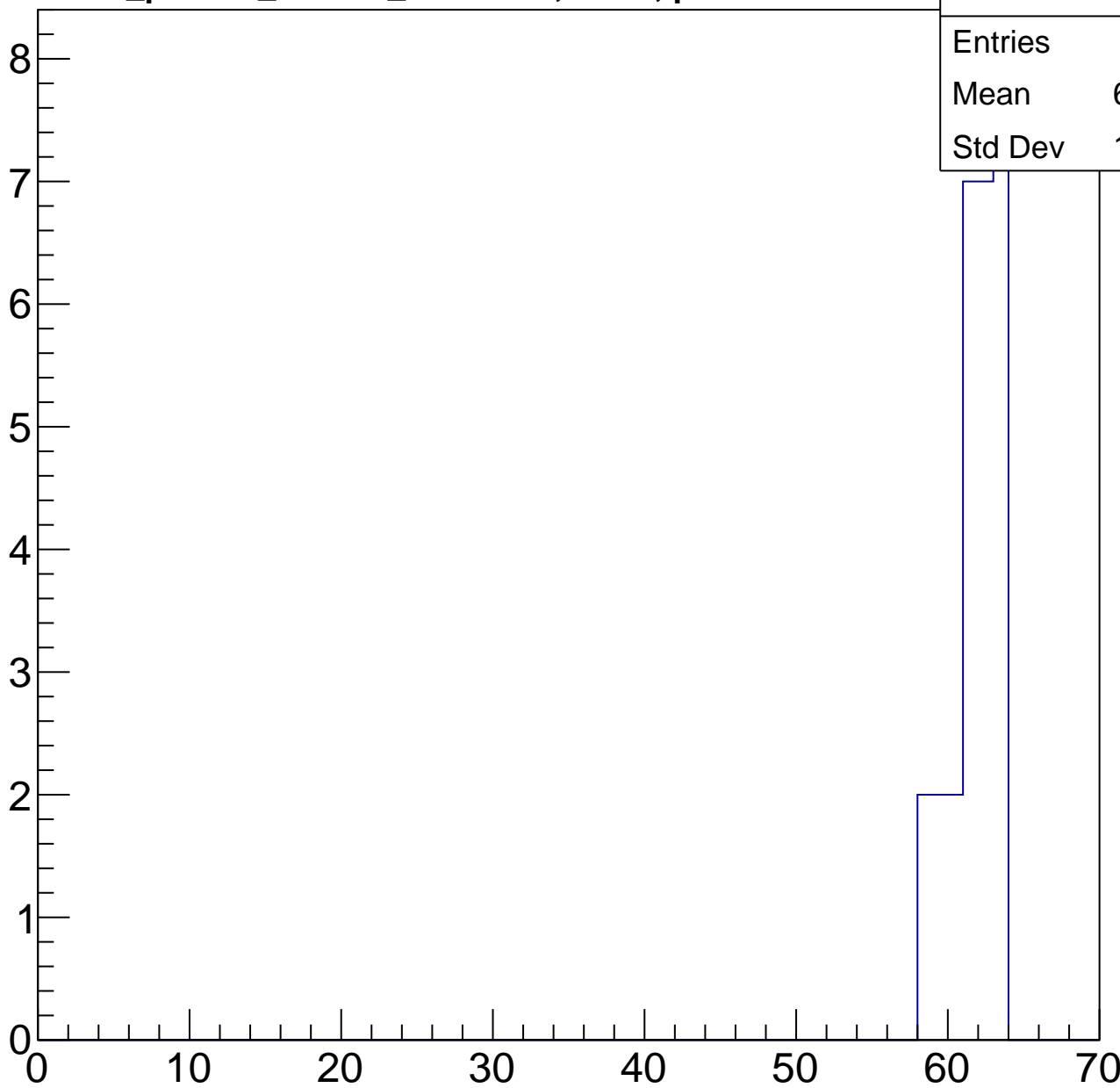
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	28
Mean	61.39
Std Dev	1.496

ampl





# B1L000S, U1-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch17, adc0

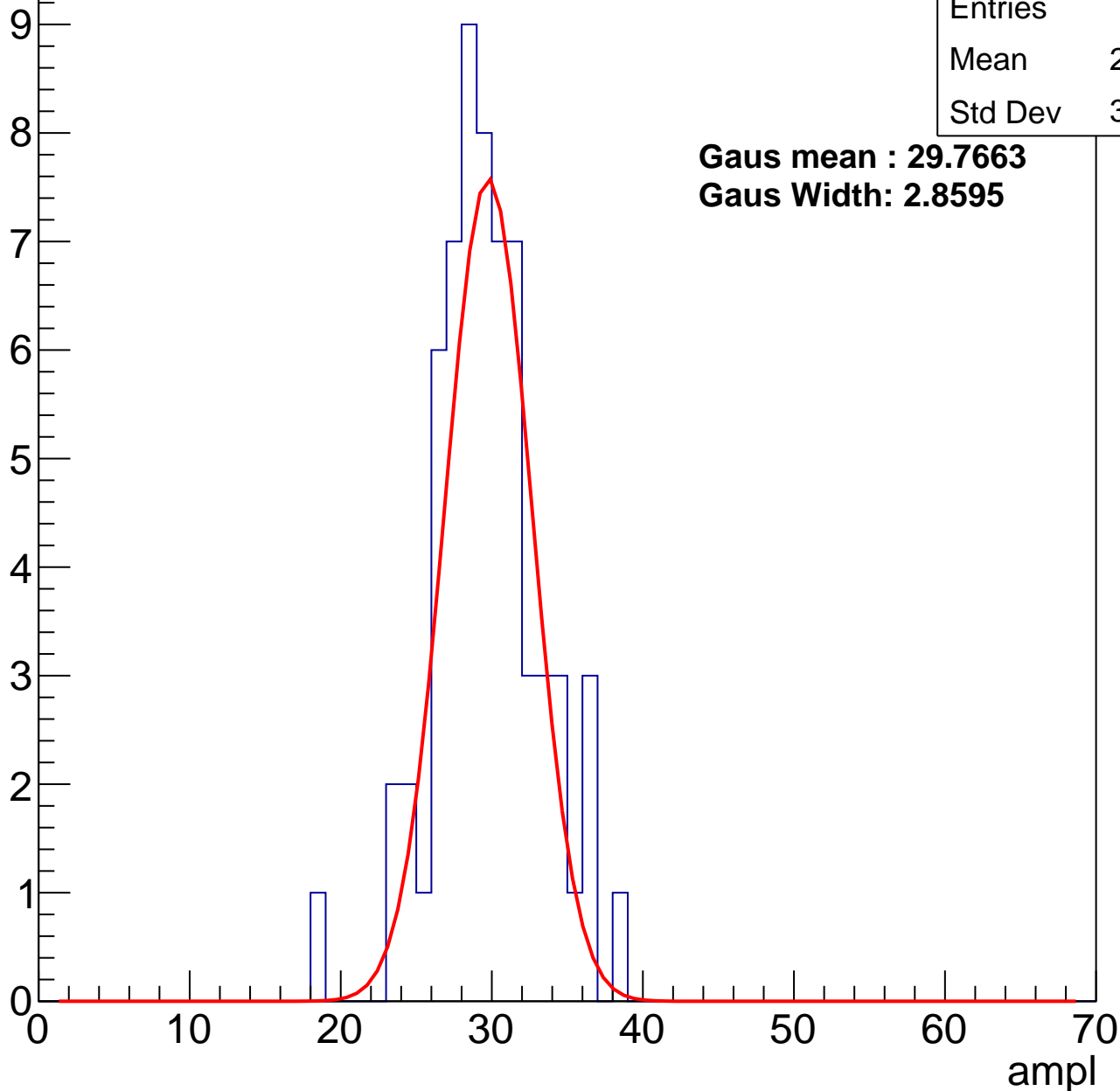
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.23
Std Dev	3.526

**Gaus mean : 29.7663**

**Gaus Width: 2.8595**



# B1L000S, U1-ch17, adc1

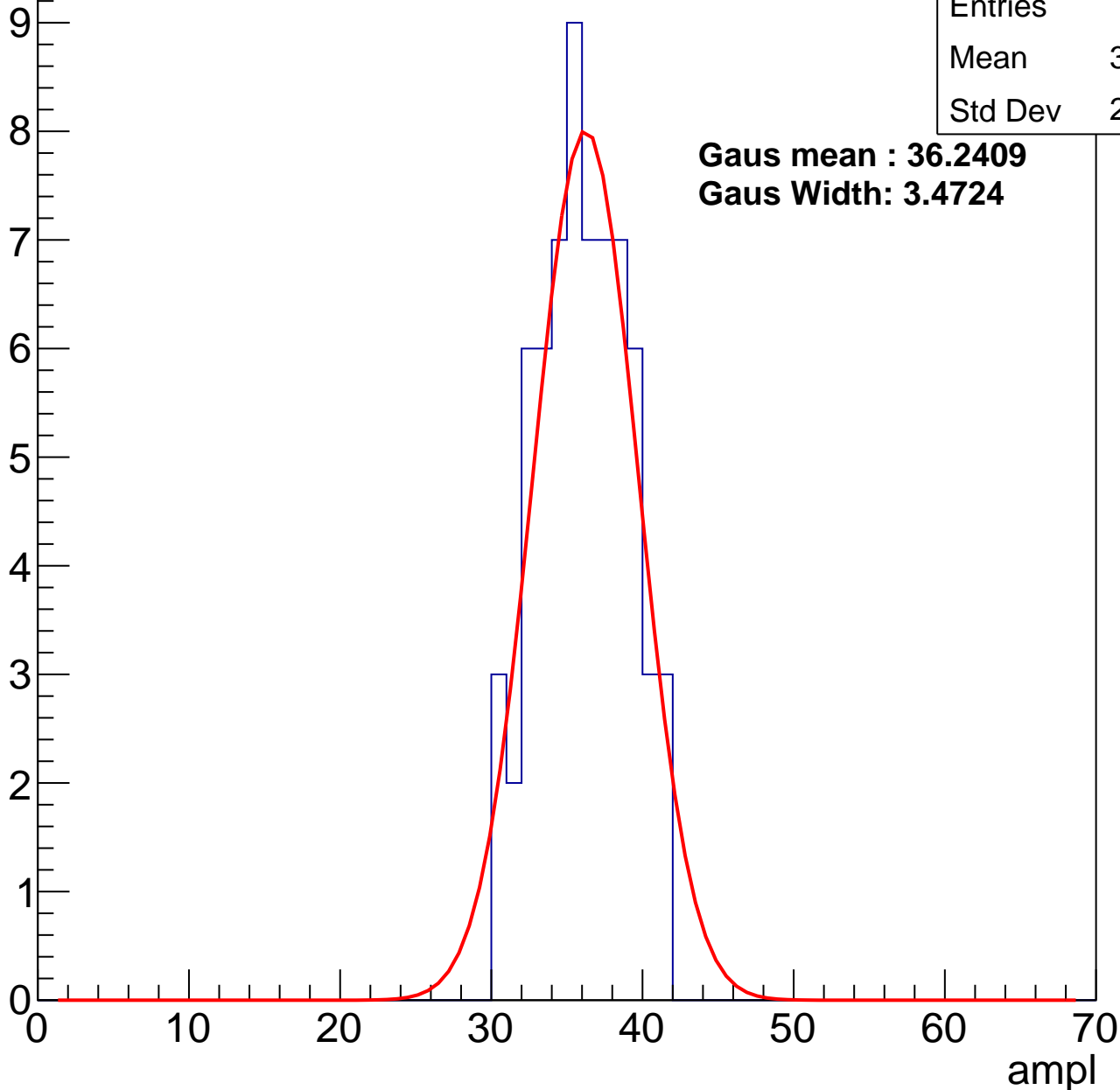
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	35.59
Std Dev	2.876

**Gaus mean : 36.2409**

**Gaus Width: 3.4724**



# B1L000S, U1-ch17, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	42.24
Std Dev	2.904

**Gaus mean : 42.7648**

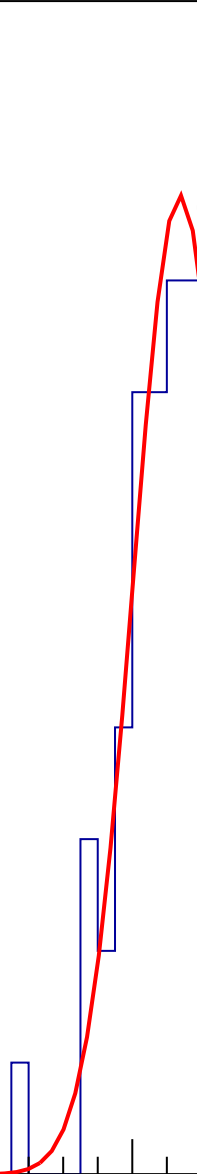
**Gaus Width: 2.7164**

Entry

10  
8  
6  
4  
2  
0

ampl

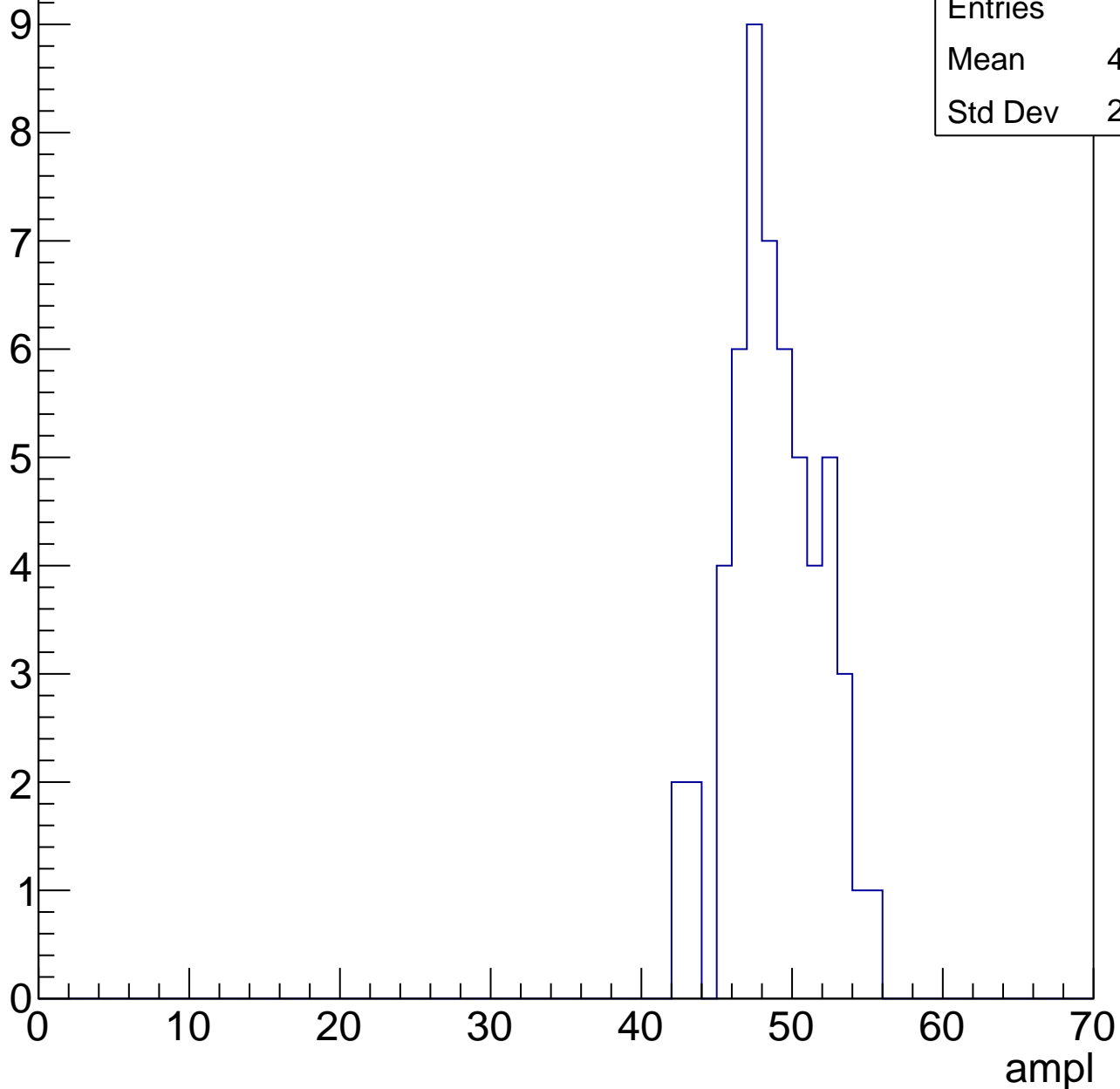
0 10 20 30 40 50 60 70



# B1L000S, U1-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



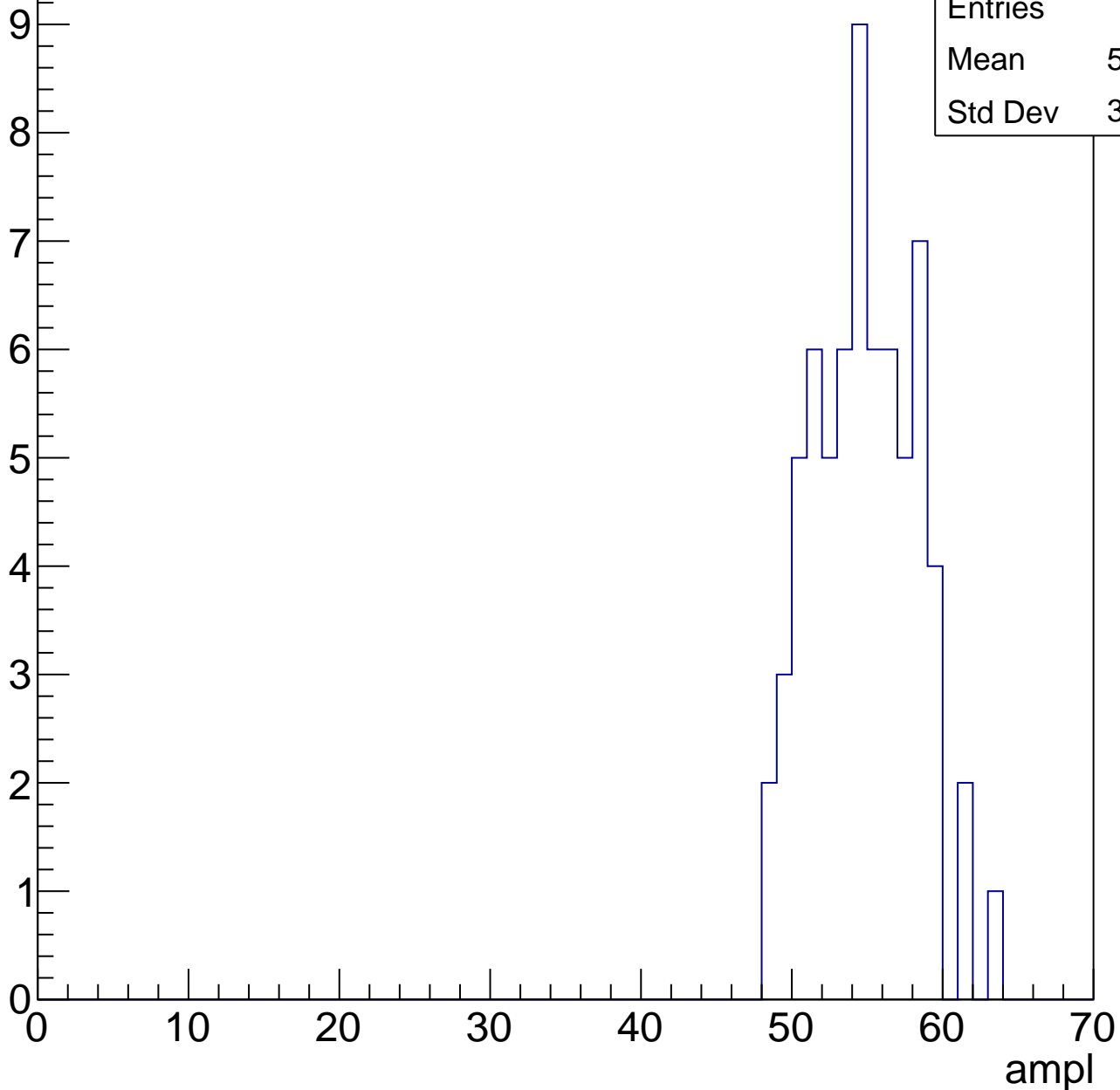
Entries	55
Mean	48.38
Std Dev	2.969

# B1L000S, U1-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	54.34
Std Dev	3.379

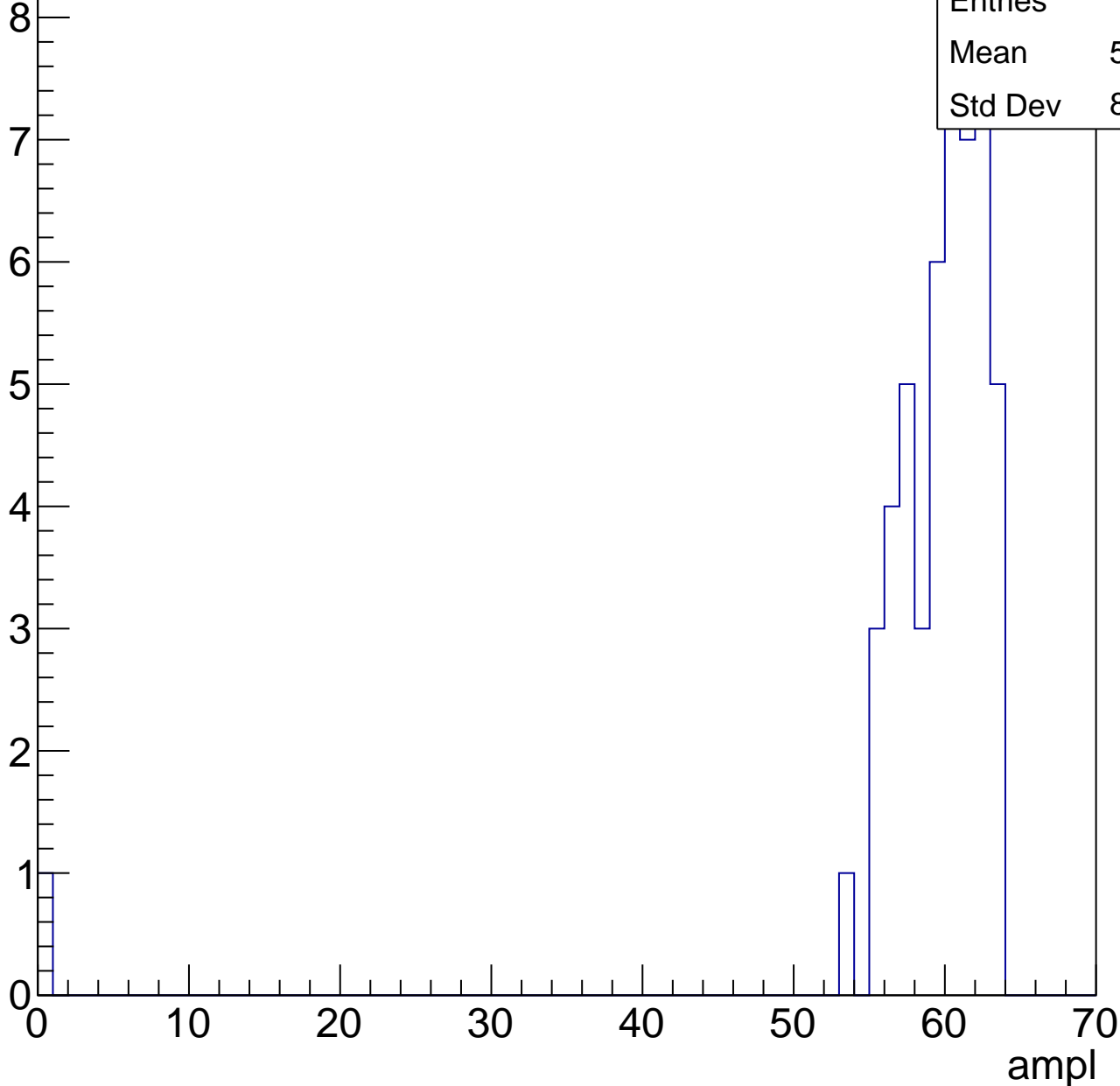


# B1L000S, U1-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

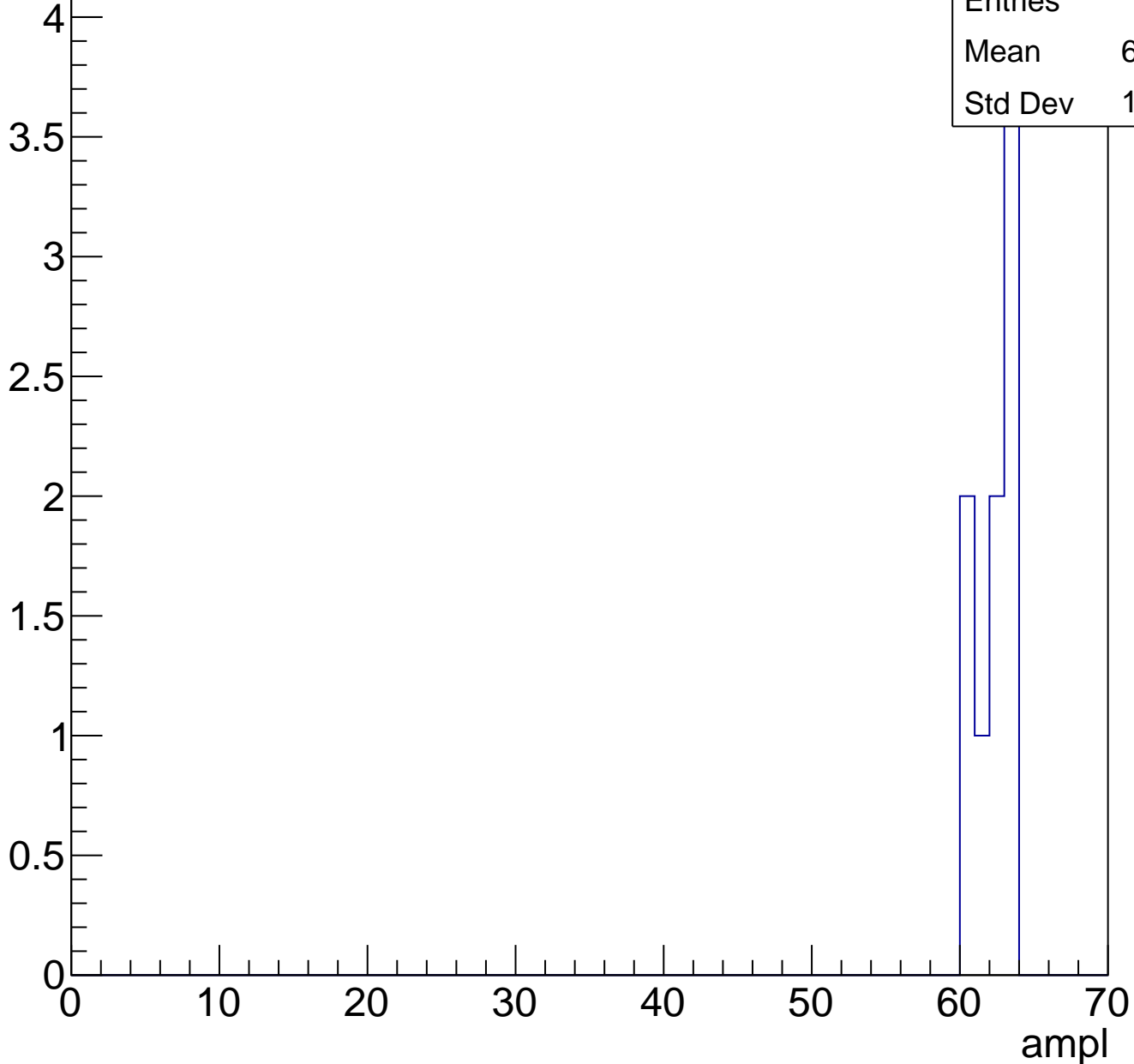
Entries	51
Mean	58.29
Std Dev	8.617



# B1L000S, U1-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U1-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U1-ch18, adc0

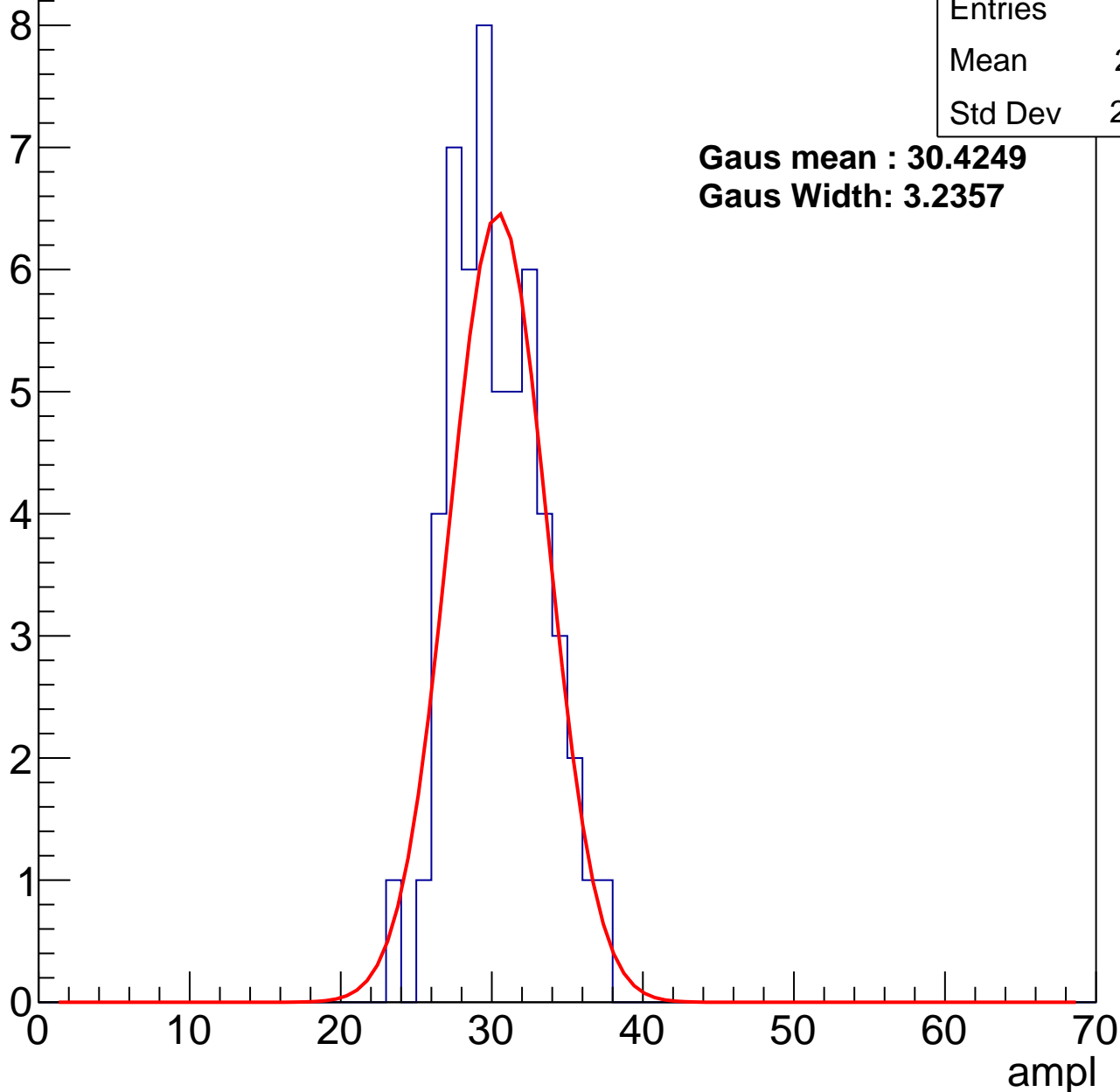
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	29.91
Std Dev	2.983

**Gaus mean : 30.4249**

**Gaus Width: 3.2357**



# B1L000S, U1-ch18, adc1

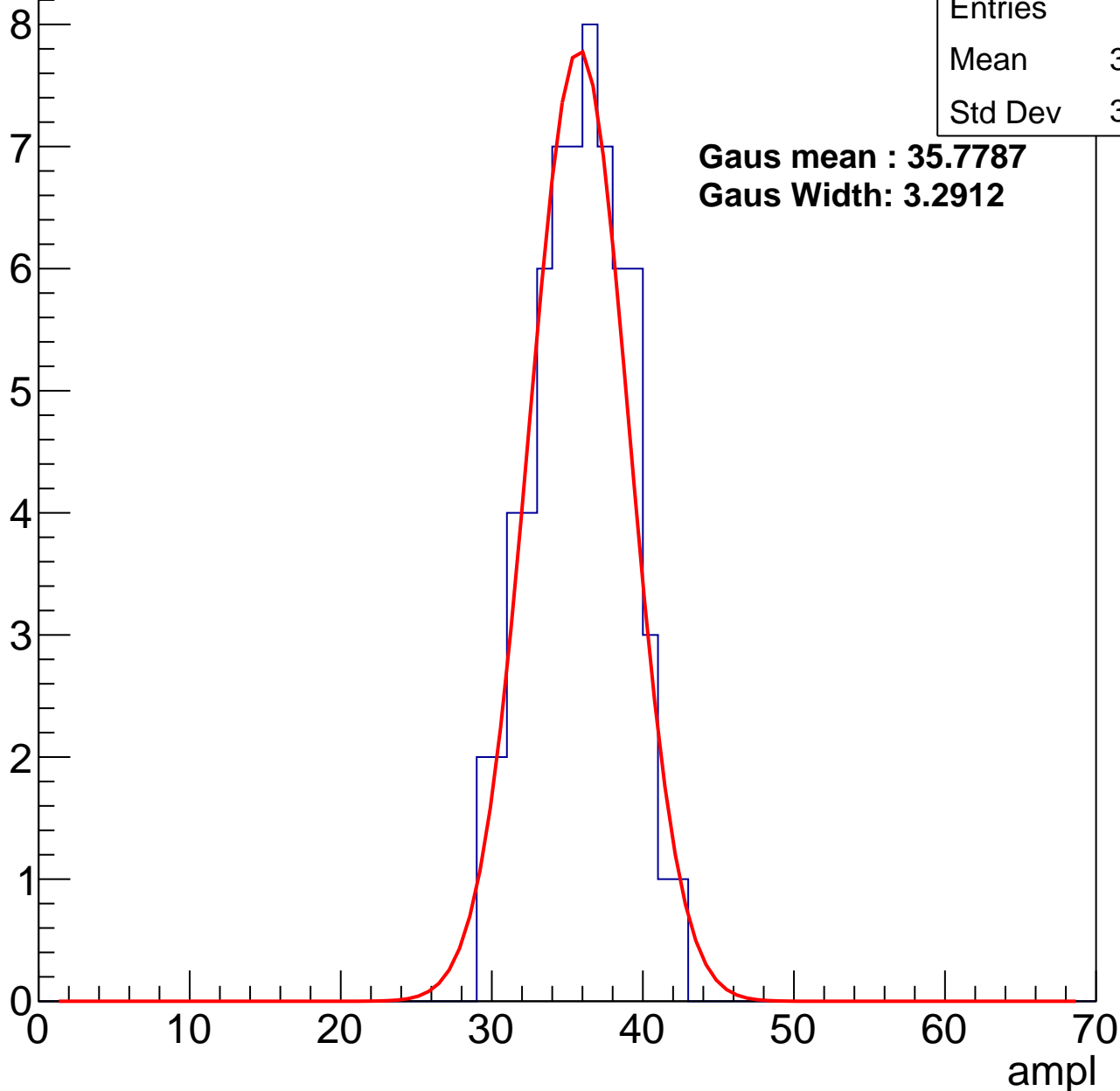
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.36
Std Dev	3.038

**Gaus mean : 35.7787**

**Gaus Width: 3.2912**

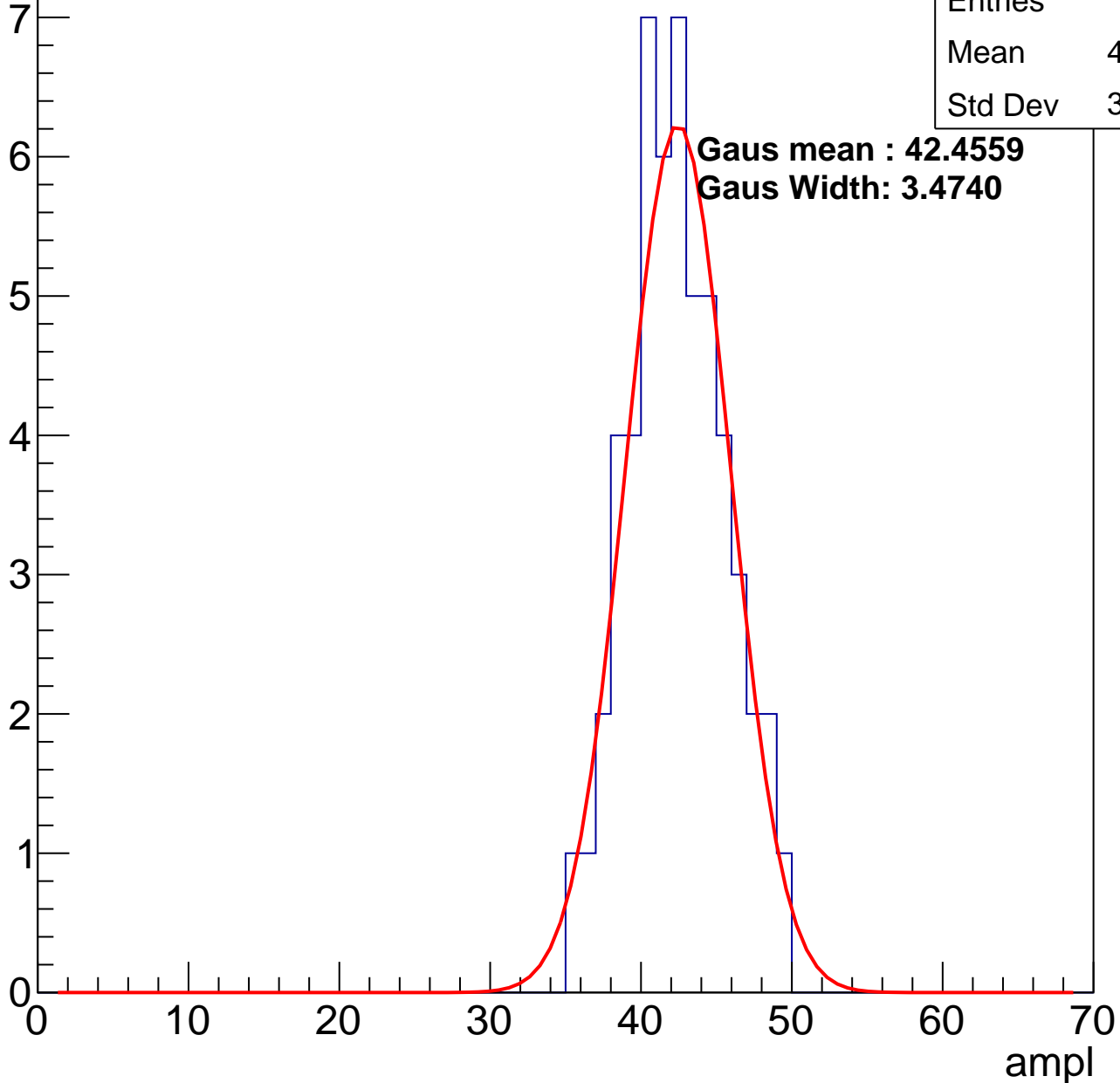


# B1L000S, U1-ch18, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.94
Std Dev	3.188

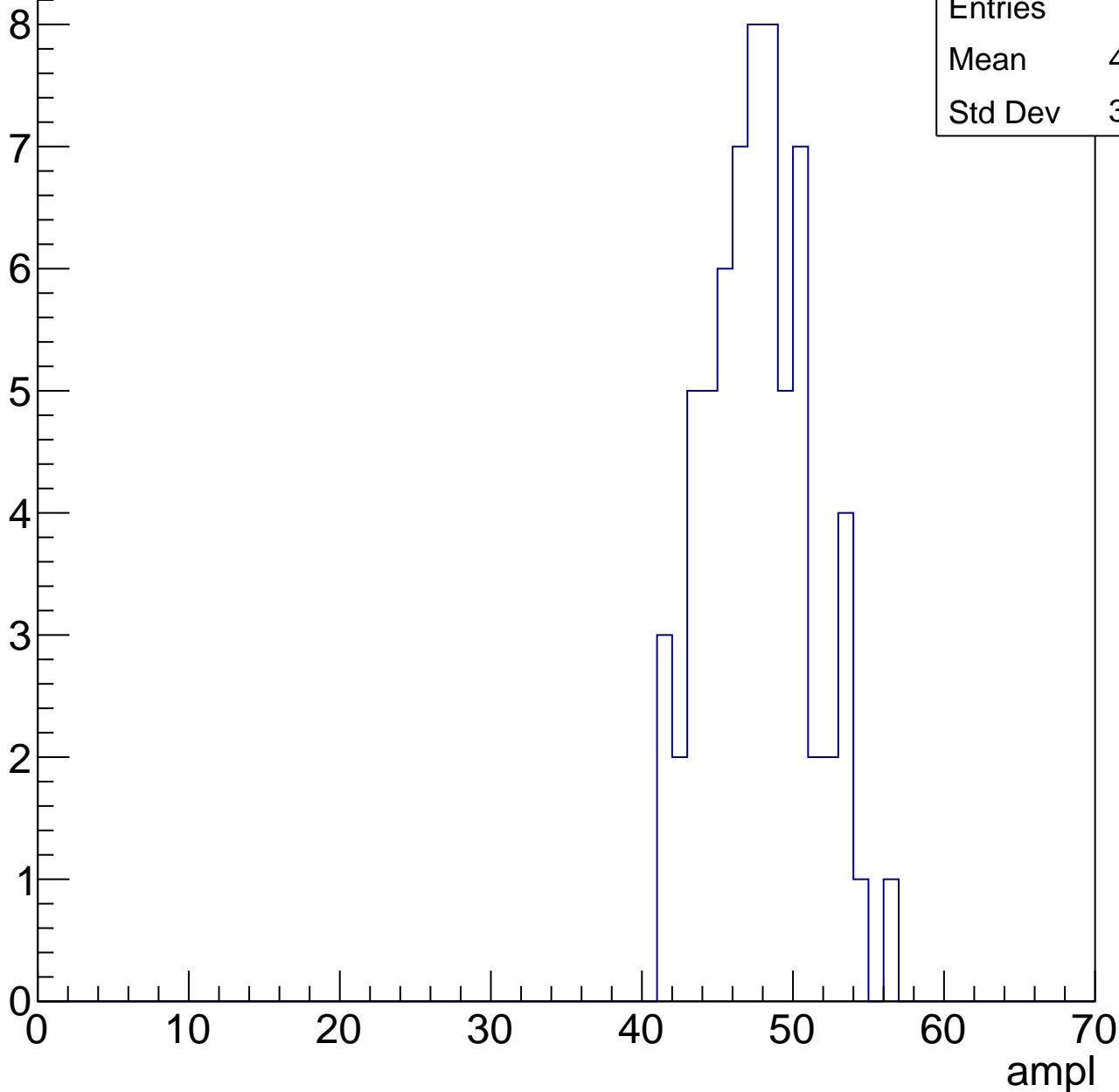


# B1L000S, U1-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

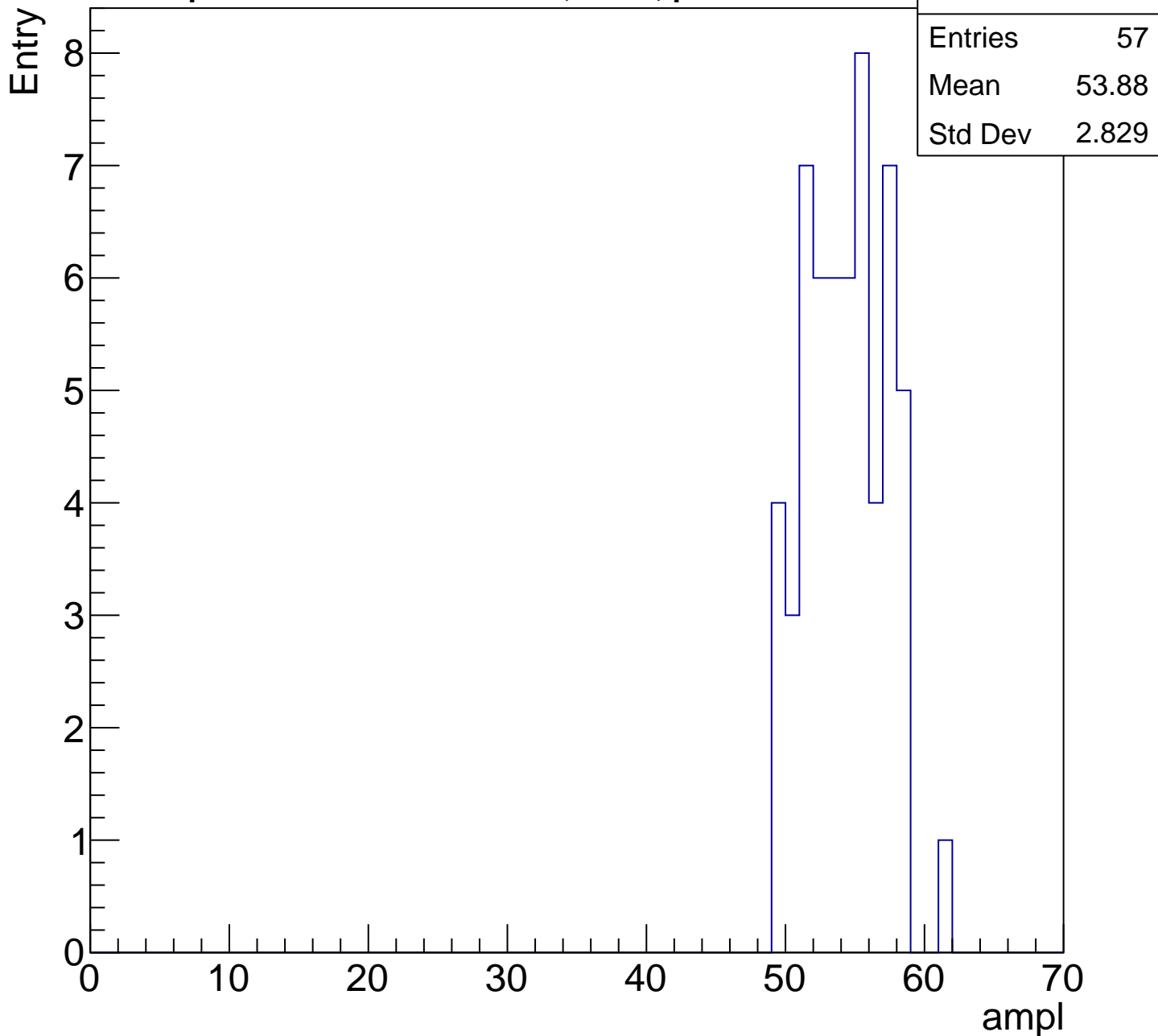
Entry

Entries	66
Mean	47.23
Std Dev	3.388



# B1L000S, U1-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

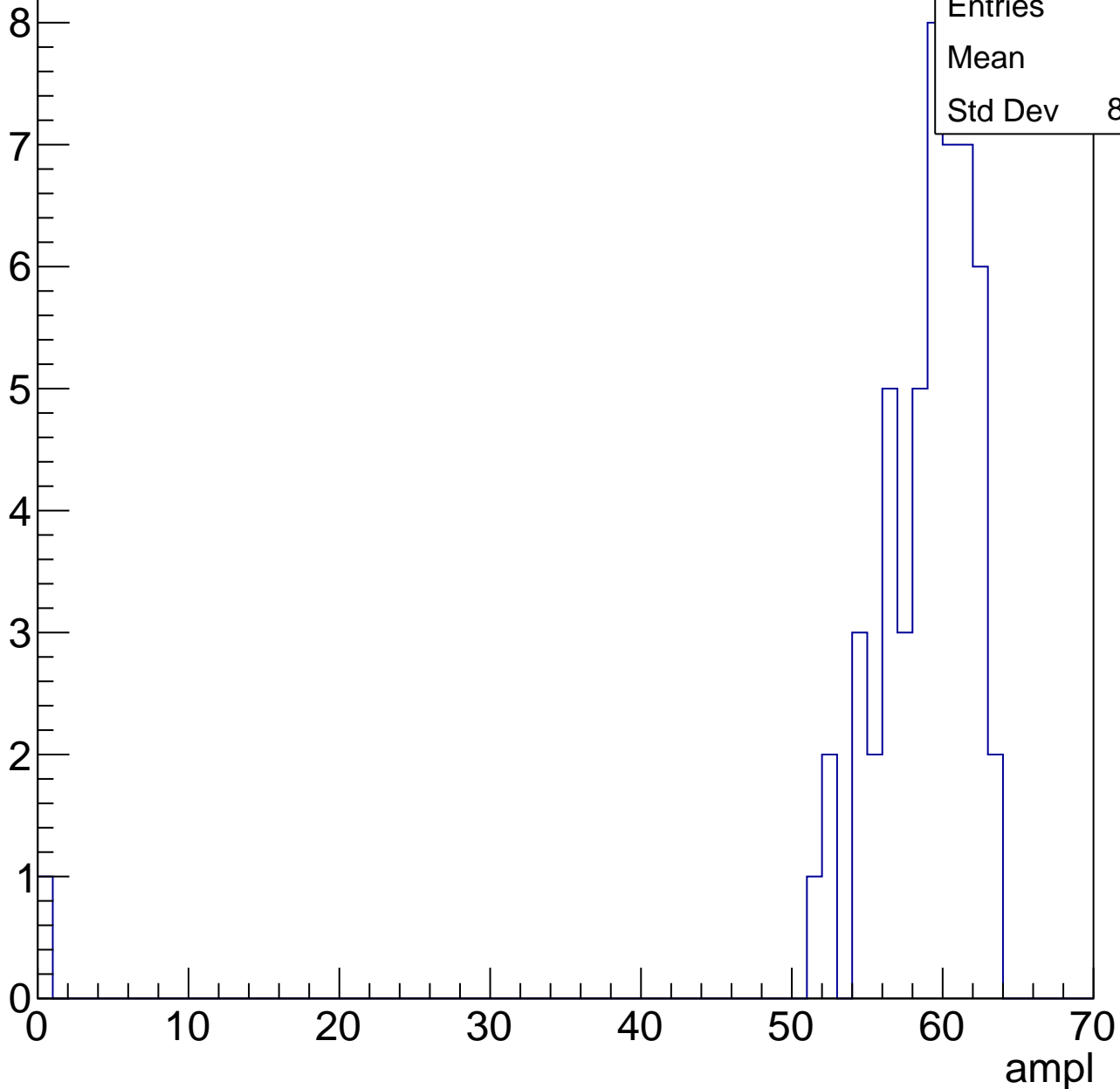


# B1L000S, U1-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	57.4
Std Dev	8.549

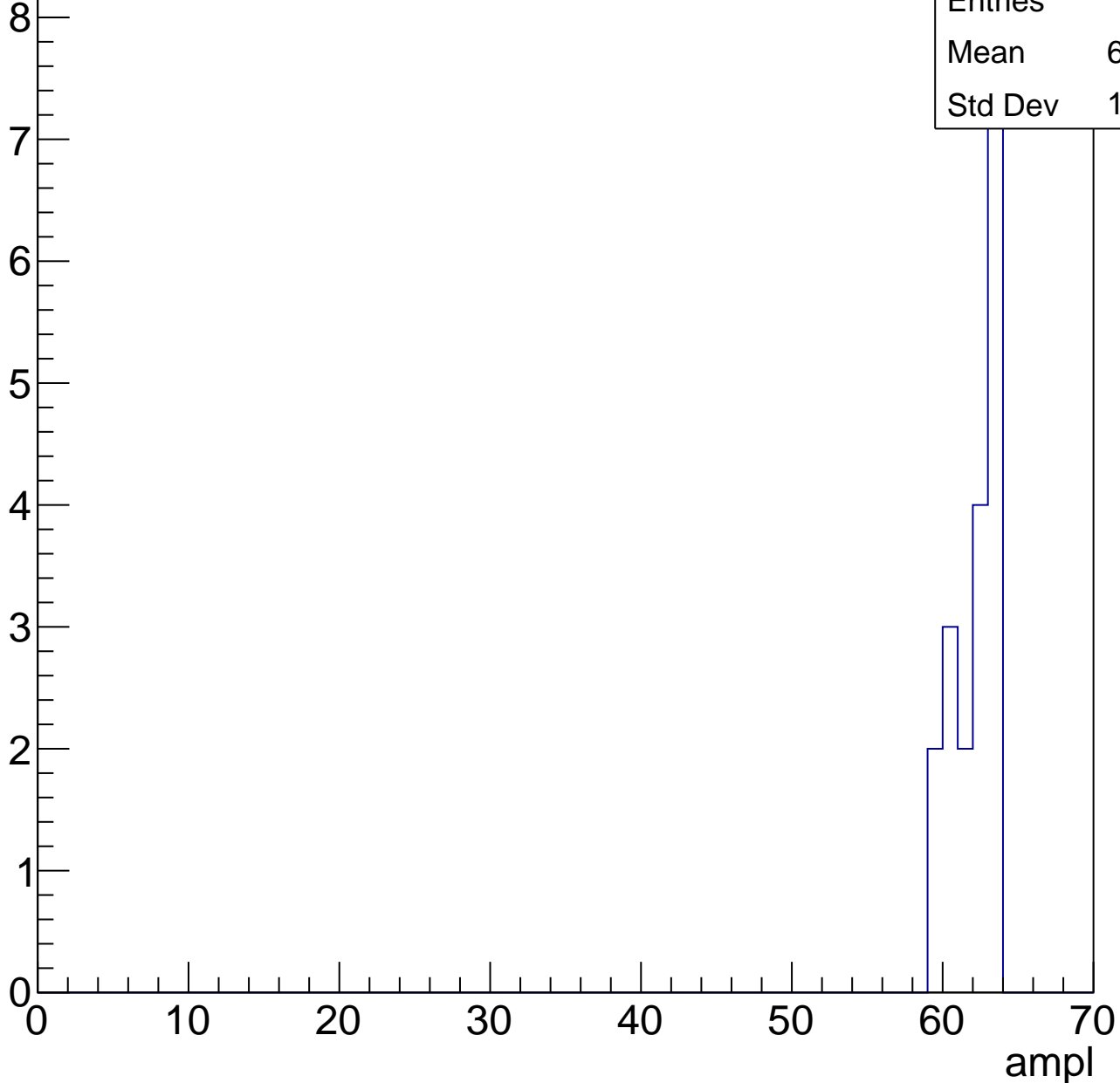


# B1L000S, U1-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	61.68
Std Dev	1.416





# B1L000S, U1-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U1-ch19, adc0

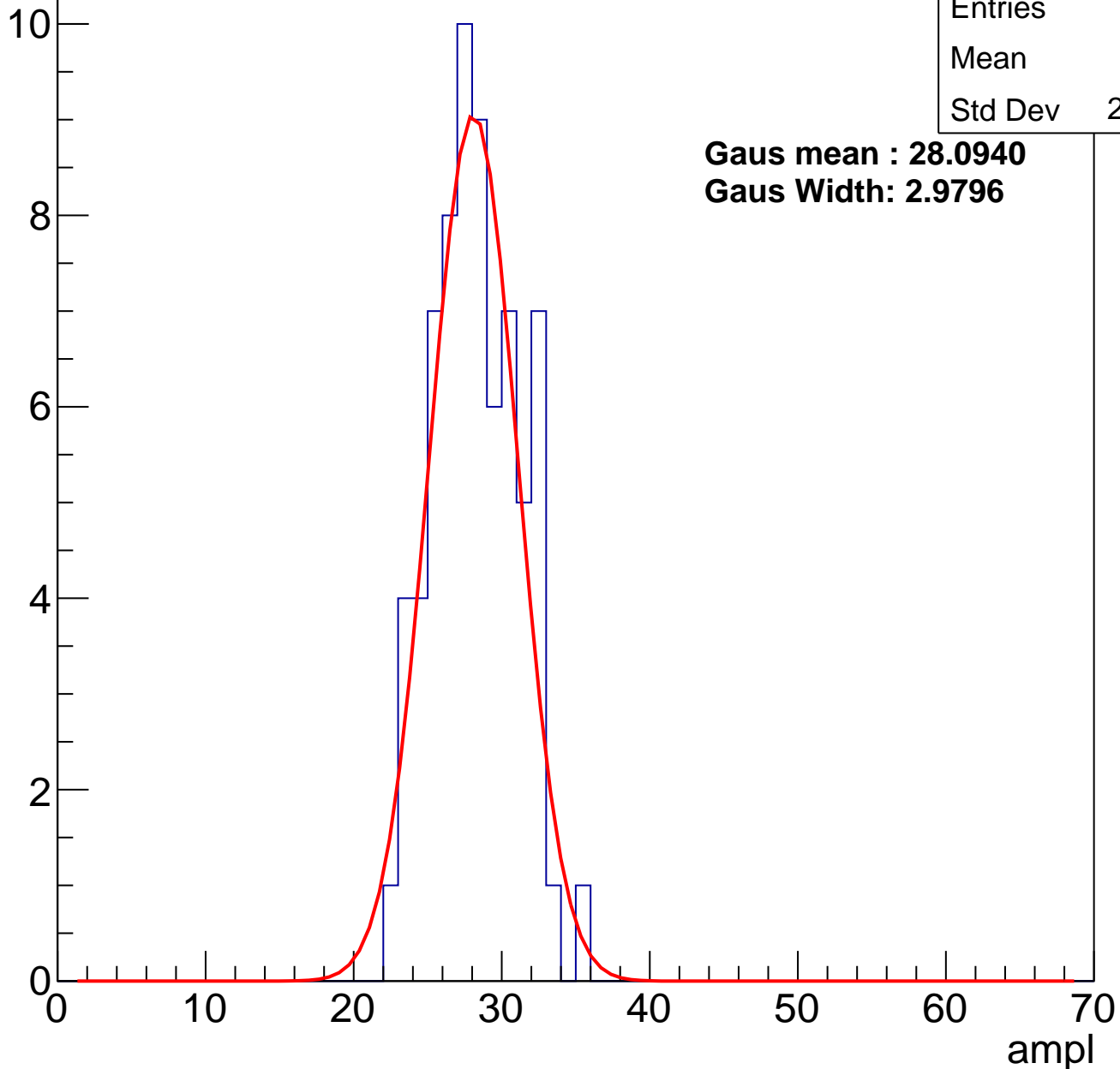
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	27.8
Std Dev	2.847

**Gaus mean : 28.0940**

**Gaus Width: 2.9796**

Entry



# B1L000S, U1-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	34.09
Std Dev	3.458

**Gaus mean : 34.6788**

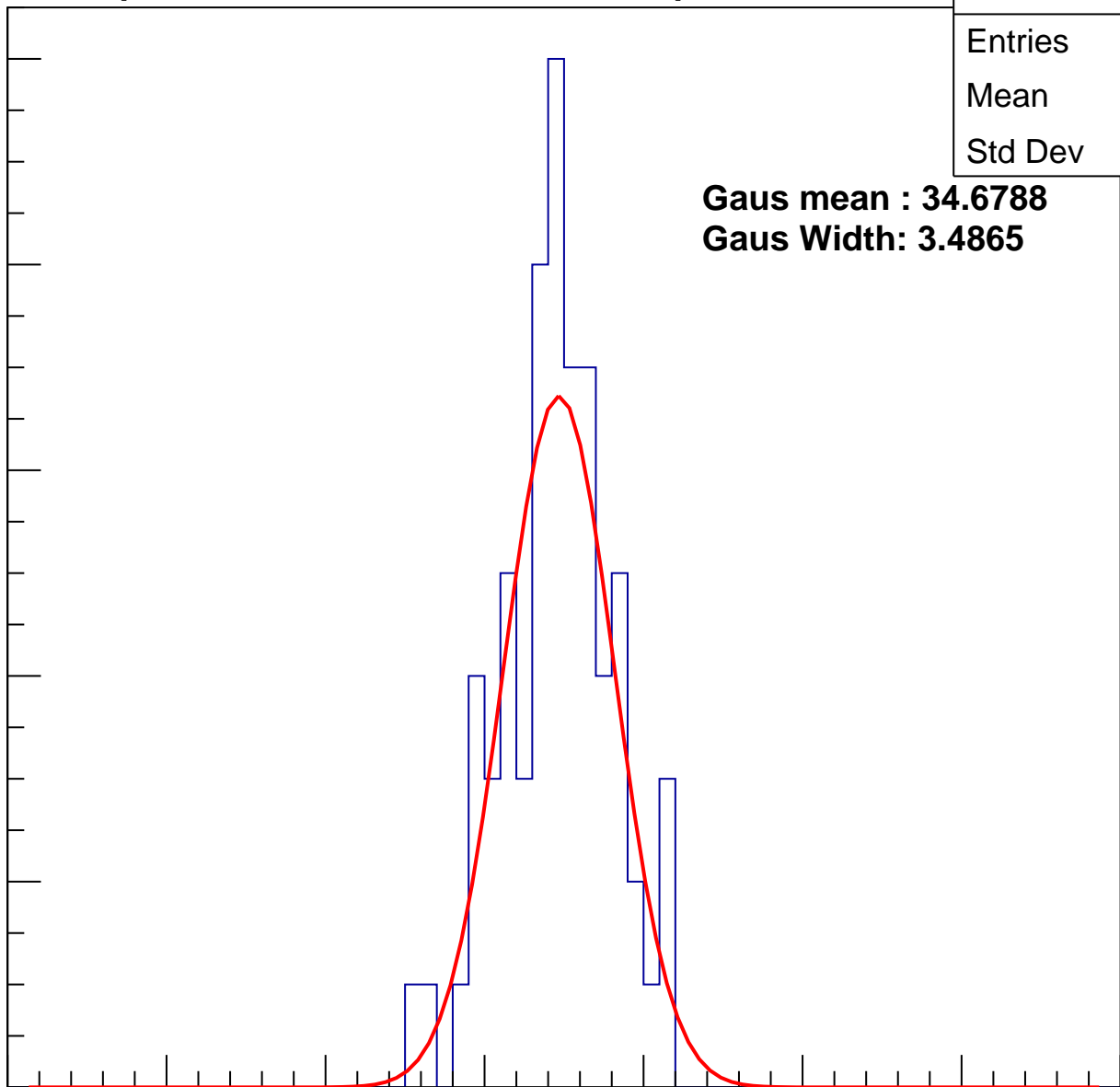
**Gaus Width: 3.4865**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch19, adc2

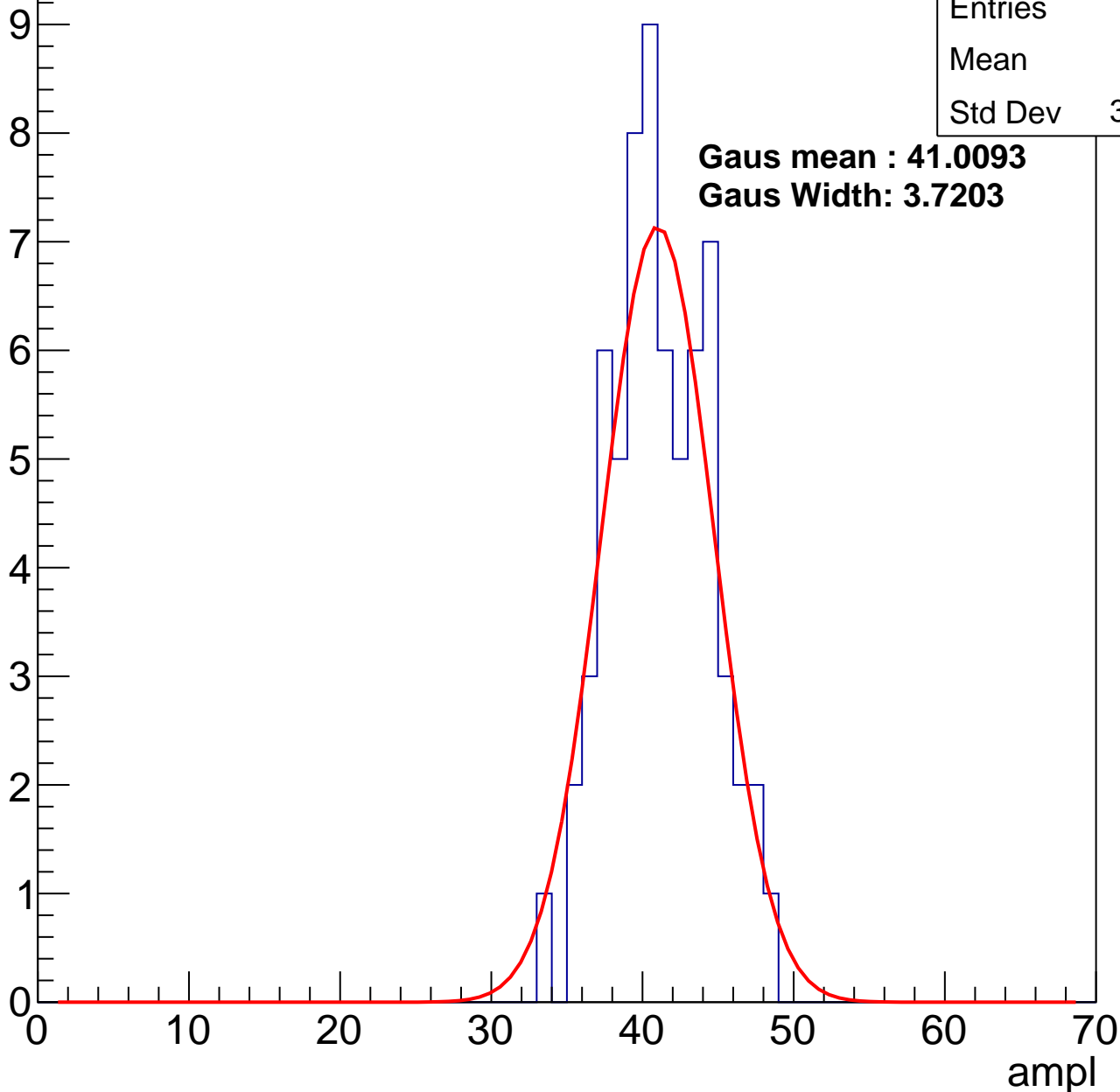
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	40.7
Std Dev	3.252

**Gaus mean : 41.0093**

**Gaus Width: 3.7203**

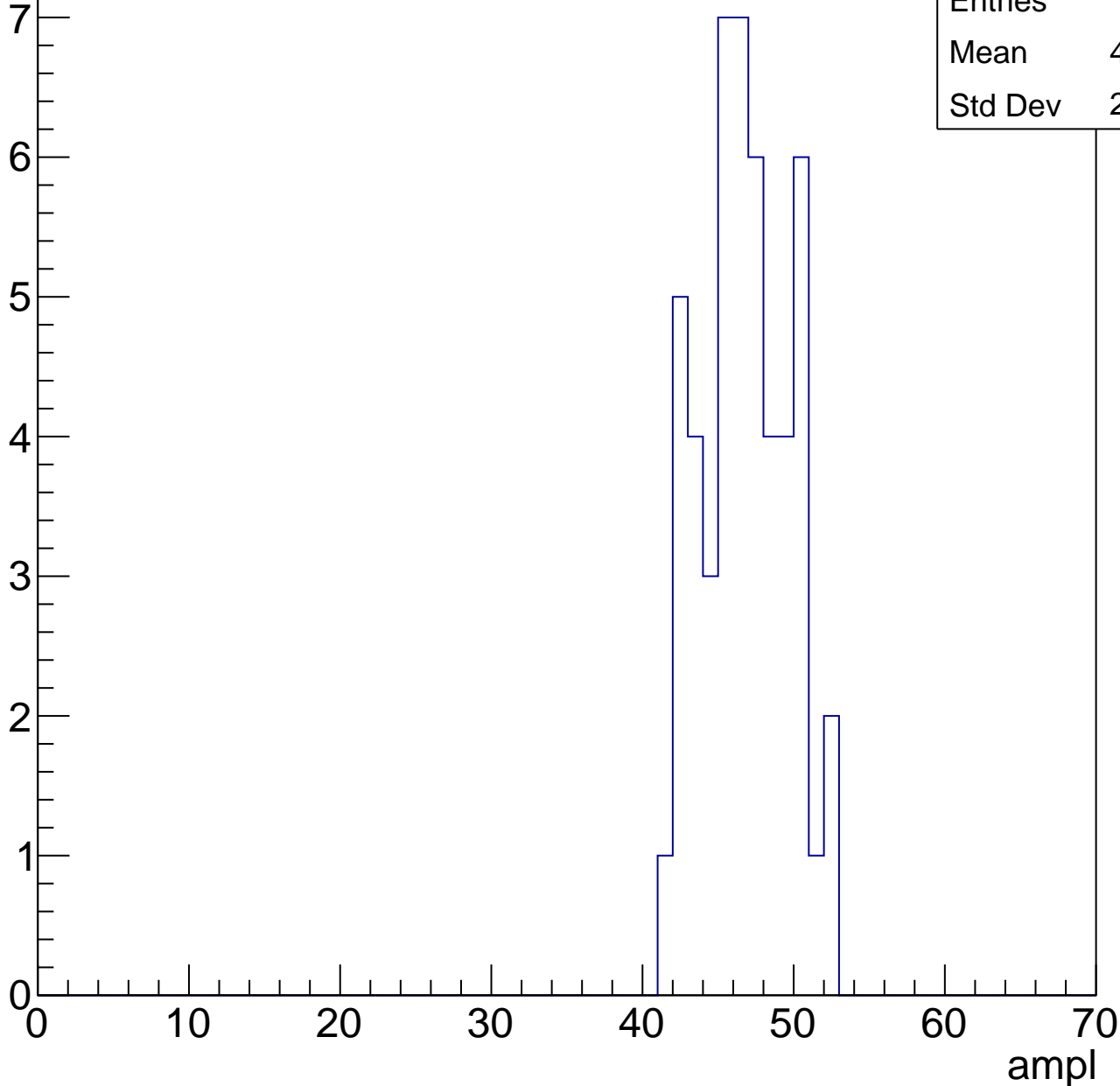


# B1L000S, U1-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

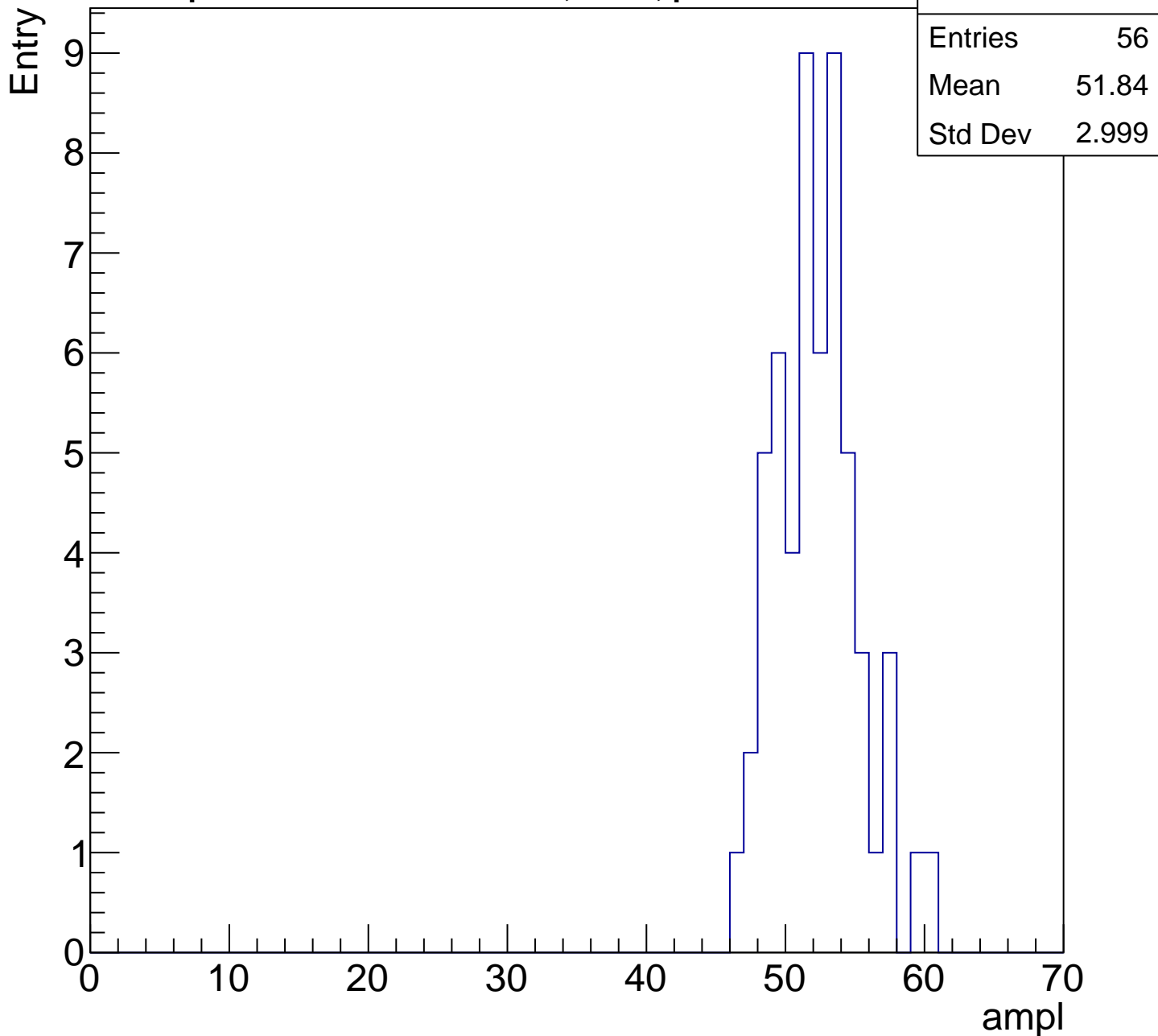
Entry

Entries	50
Mean	46.34
Std Dev	2.847



# B1L000S, U1-ch19, adc4

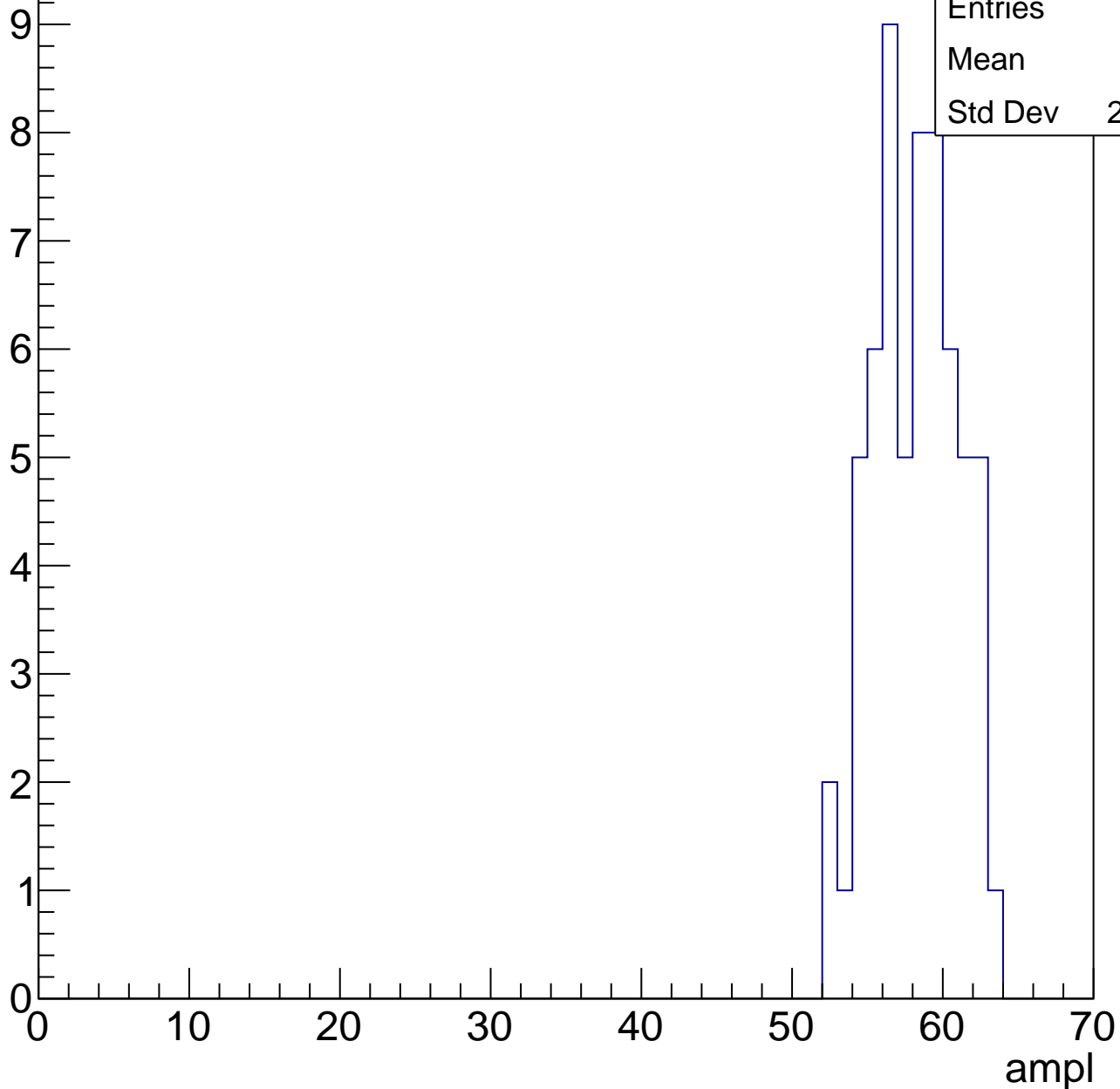
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

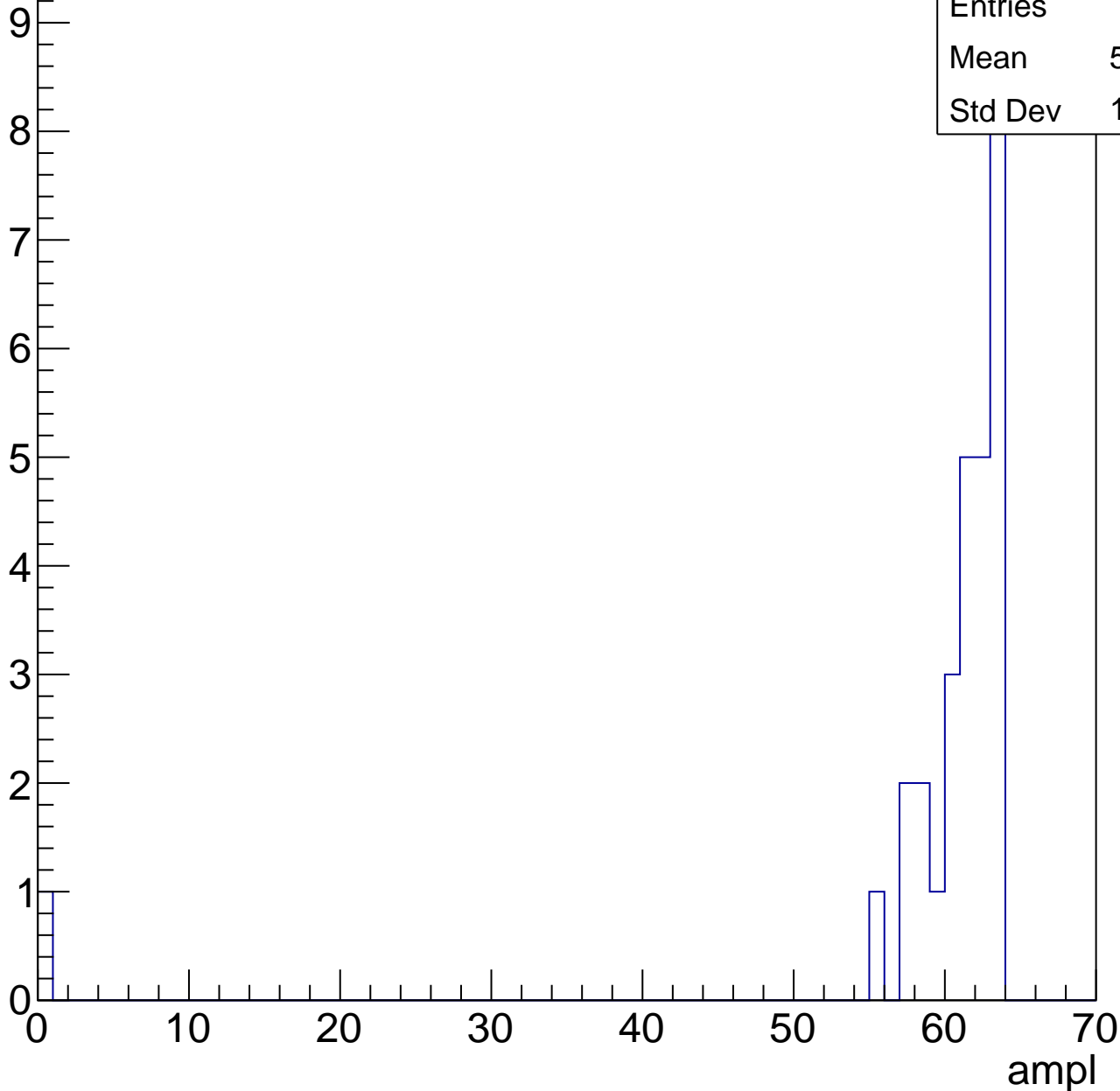


# B1L000S, U1-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	58.83
Std Dev	11.32

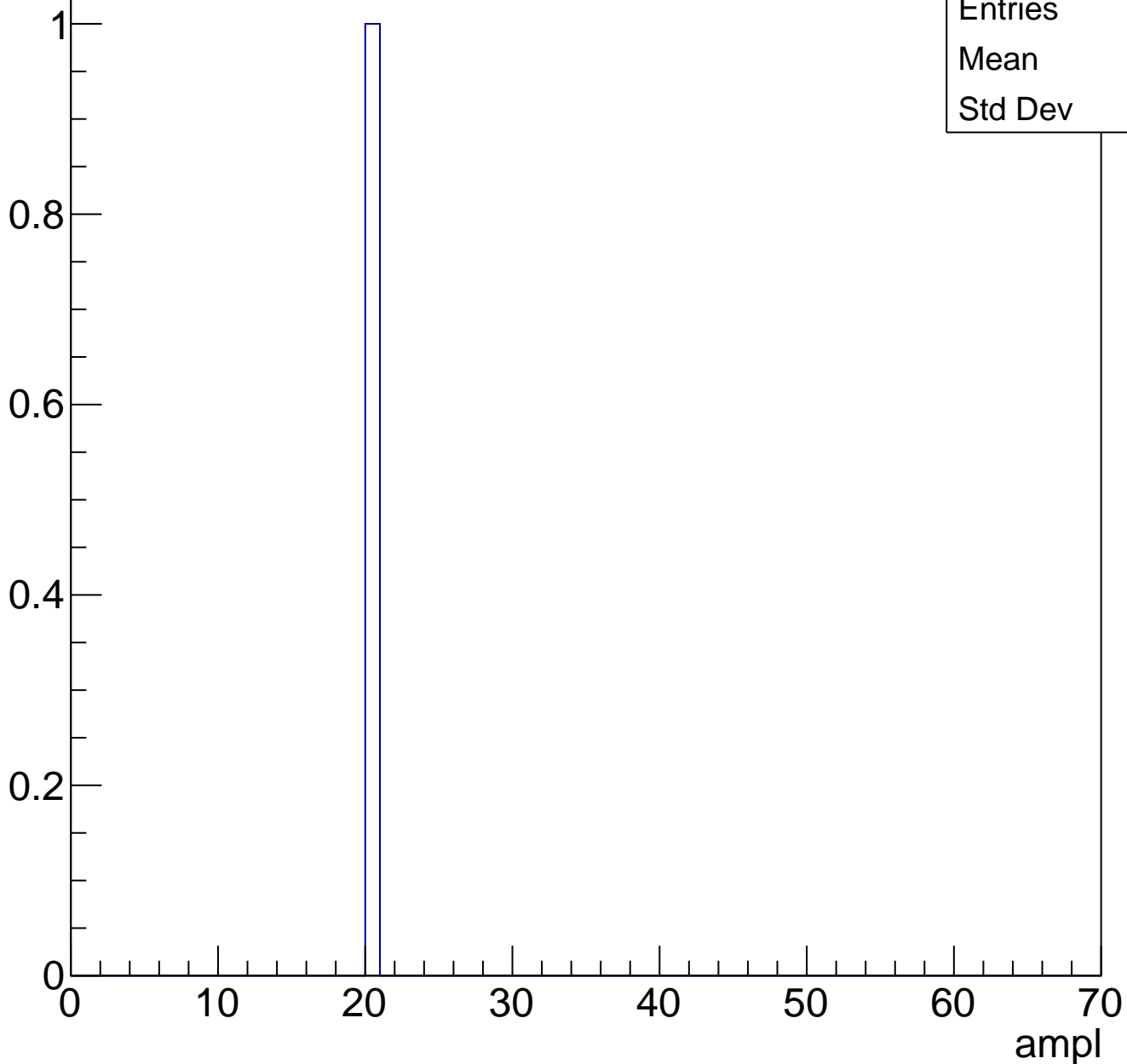




# B1L000S, U1-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U1-ch20, adc0

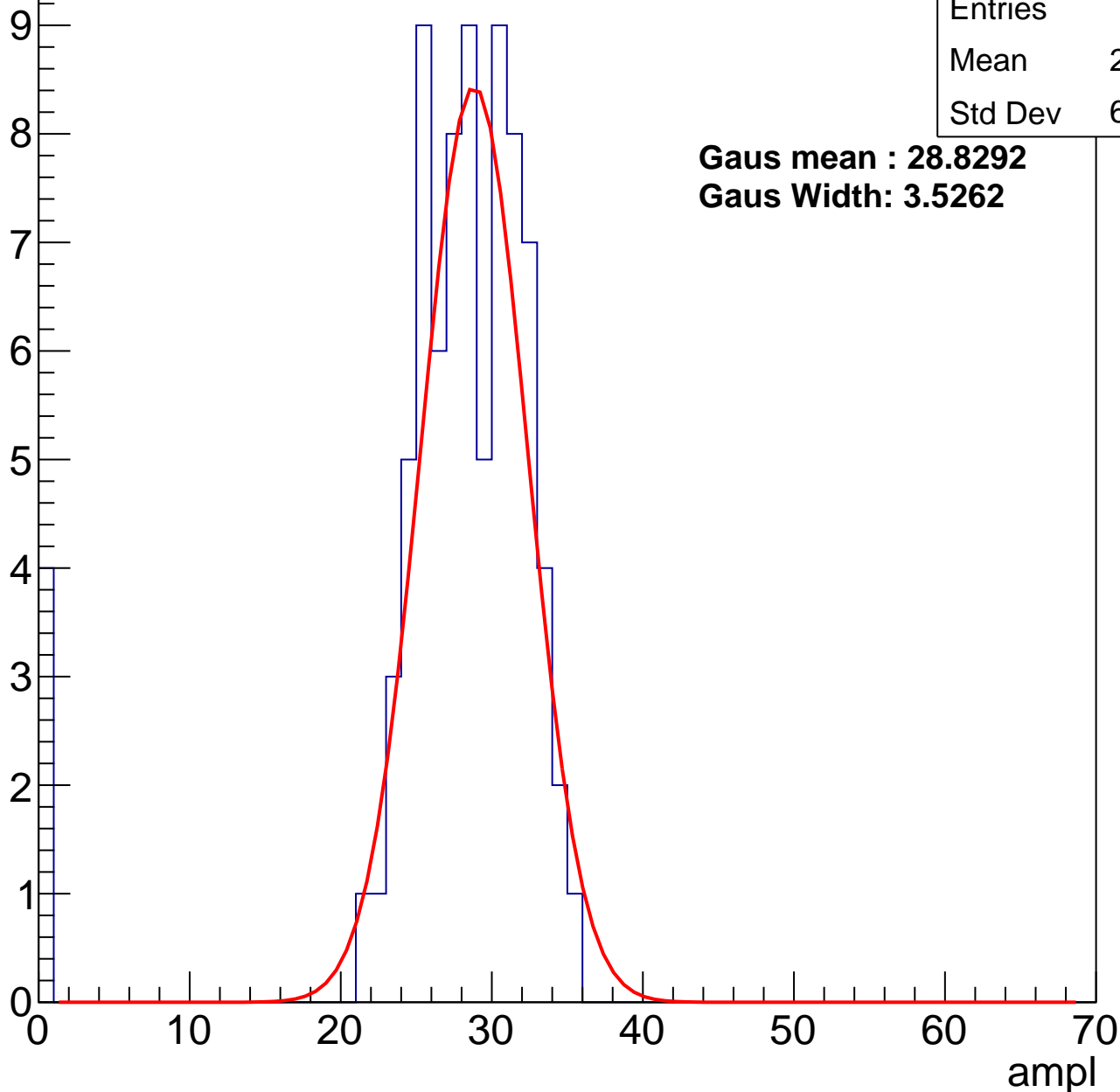
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	26.87
Std Dev	6.834

**Gaus mean : 28.8292**

**Gaus Width: 3.5262**



# B1L000S, U1-ch20, adc1

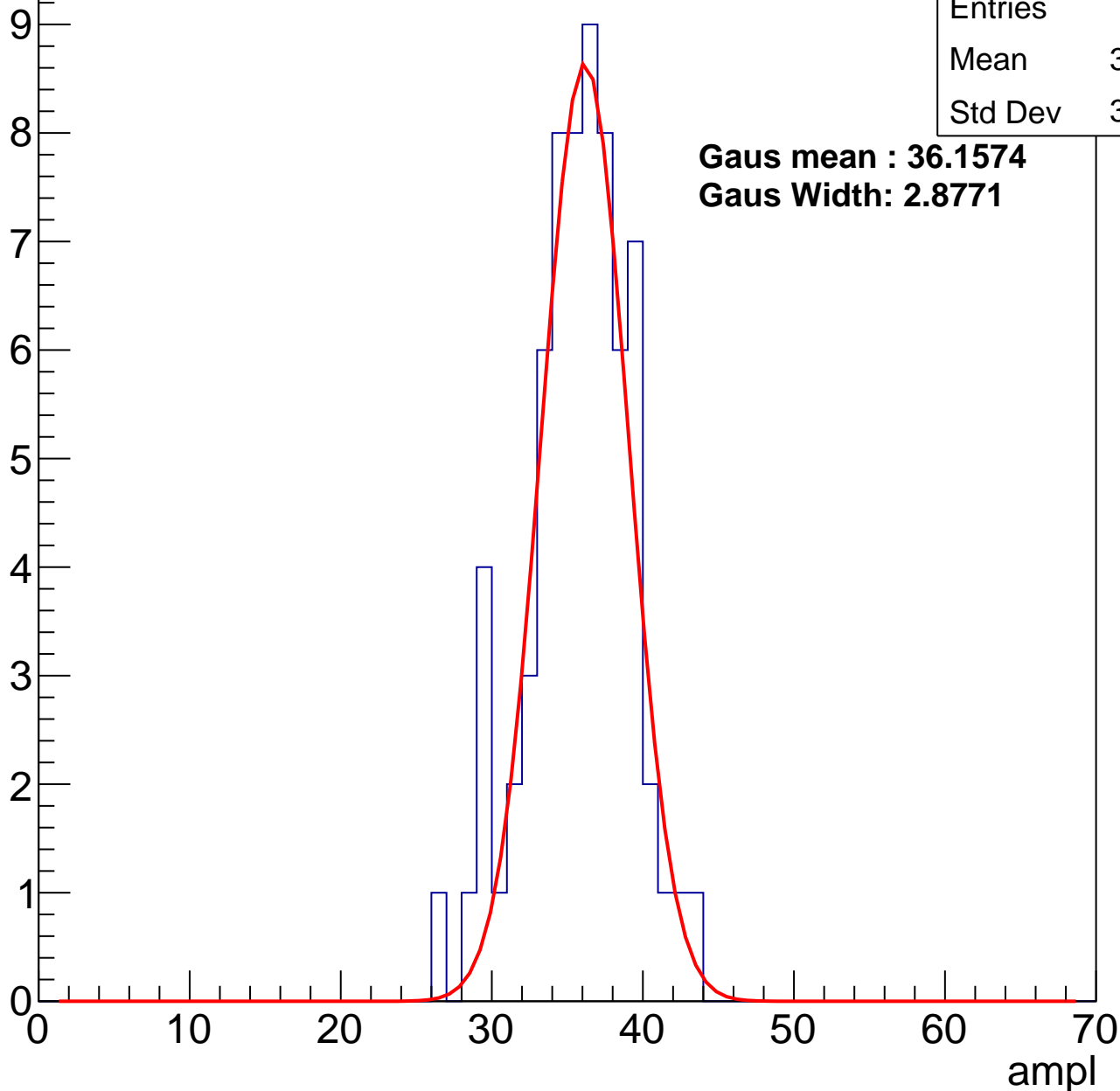
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.29
Std Dev	3.384

**Gaus mean : 36.1574**

**Gaus Width: 2.8771**



# B1L000S, U1-ch20, adc2

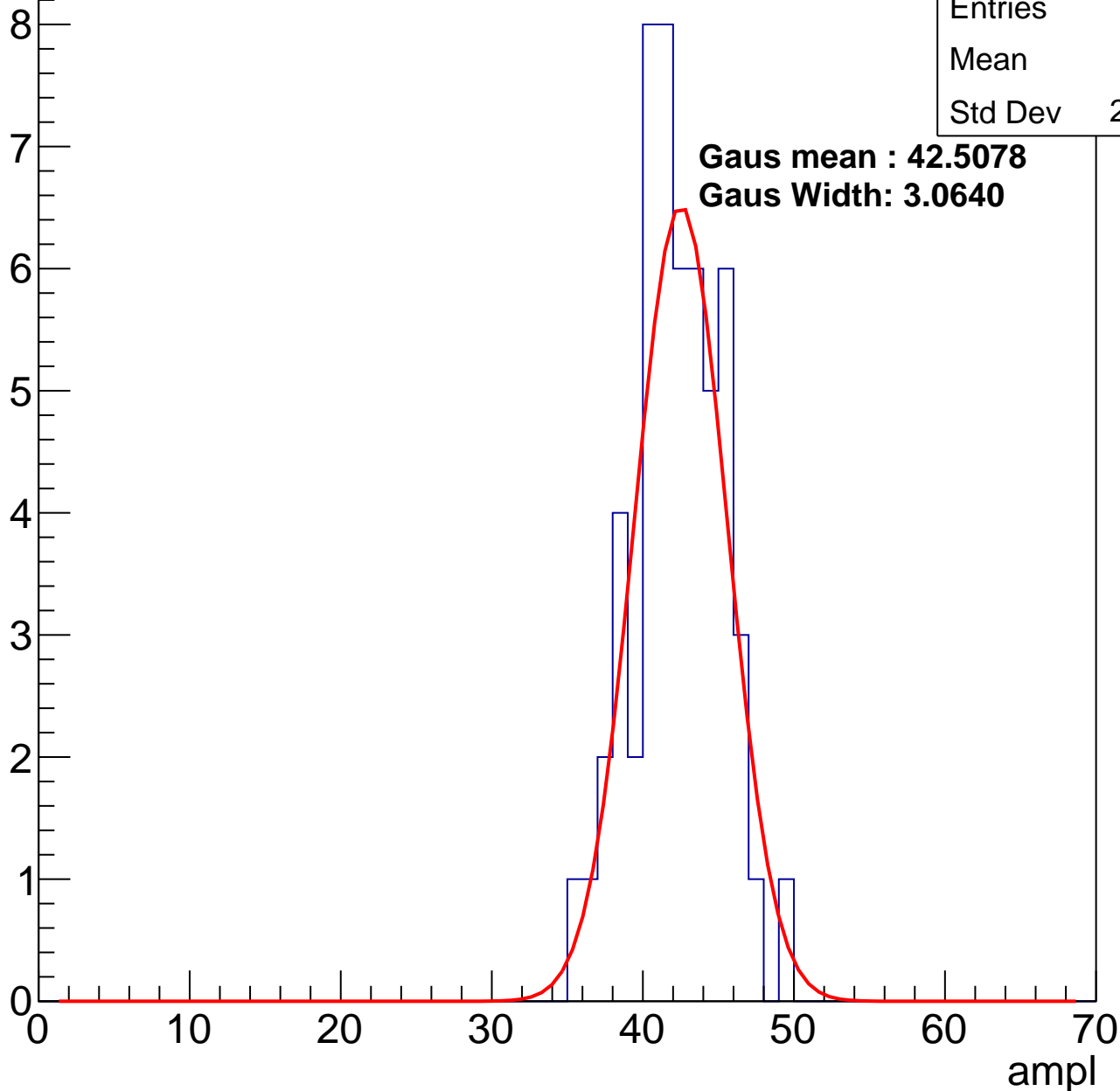
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.8
Std Dev	2.908

**Gaus mean : 42.5078**

**Gaus Width: 3.0640**



# B1L000S, U1-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

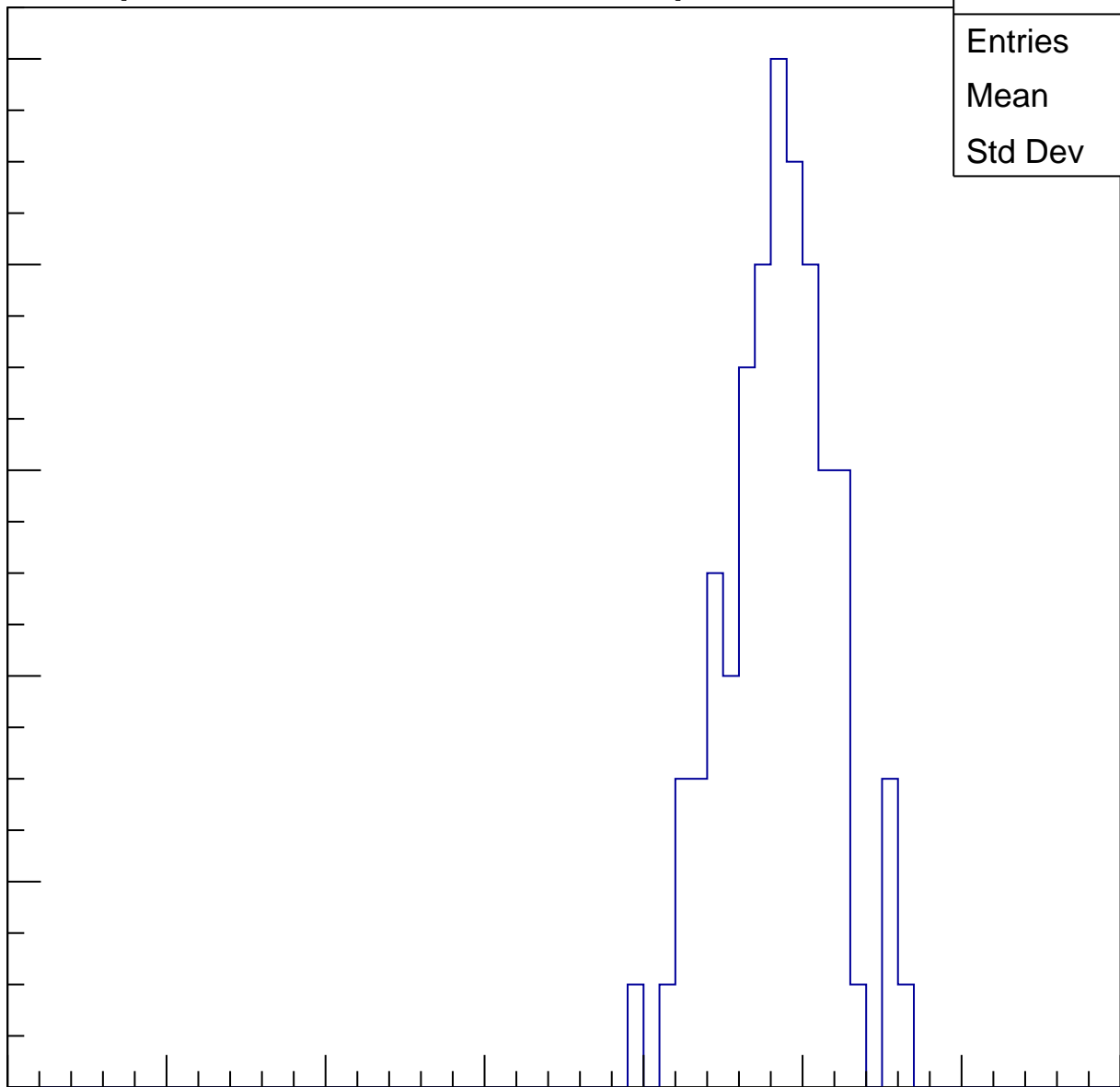
Entries	76
Mean	47.97
Std Dev	3.426

Entry

10  
8  
6  
4  
2  
0

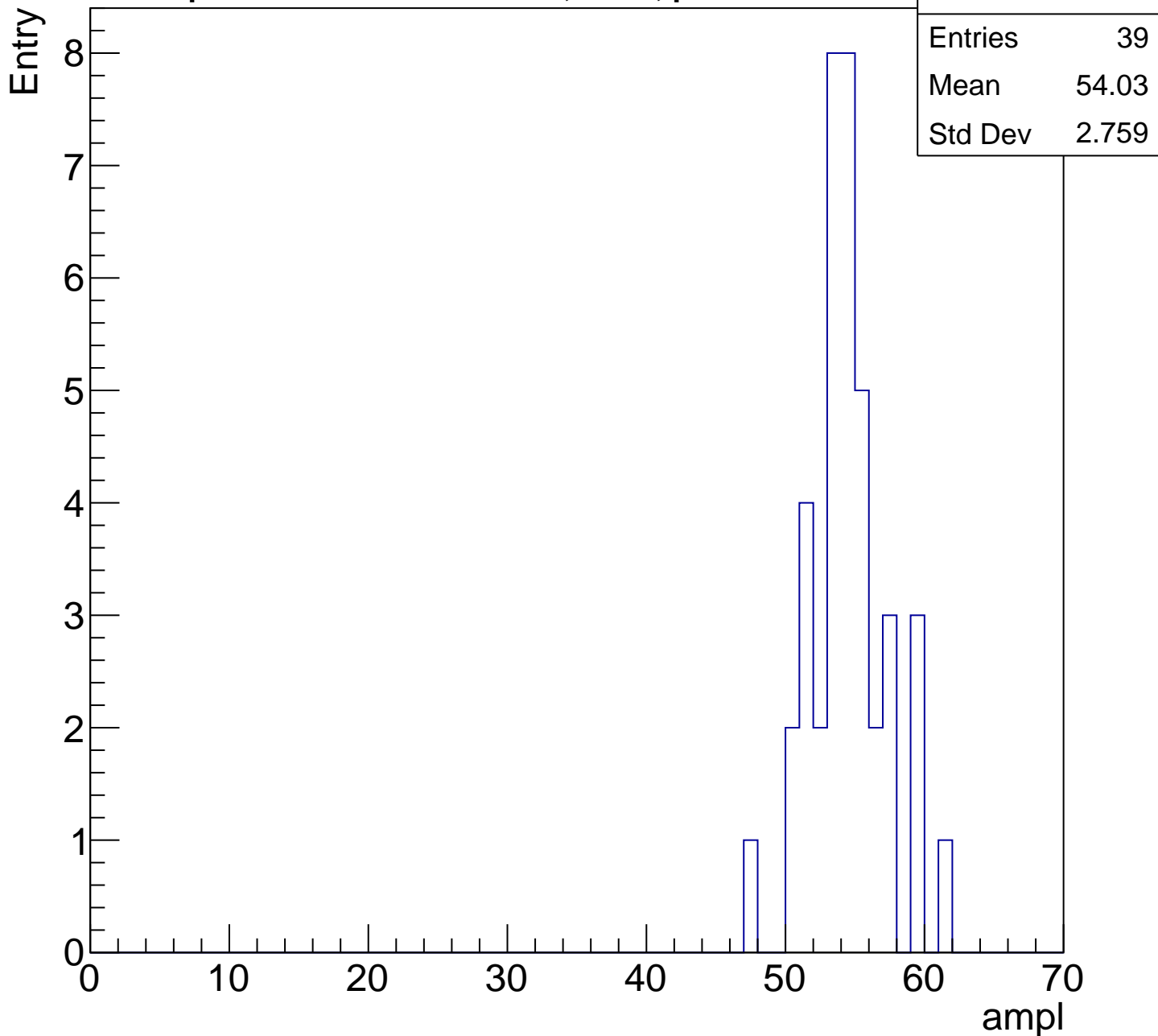
0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

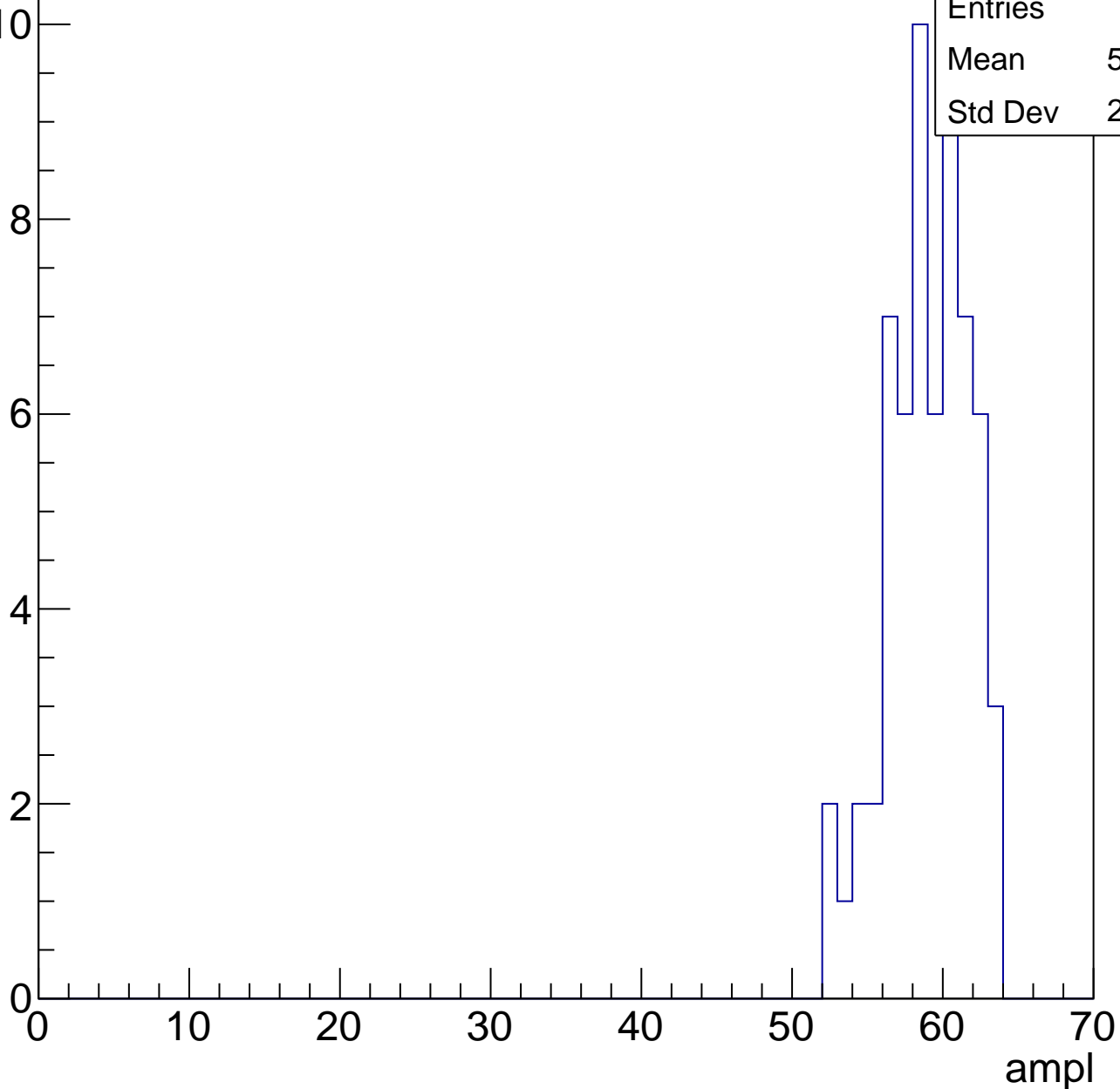


# B1L000S, U1-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	58.54
Std Dev	2.683

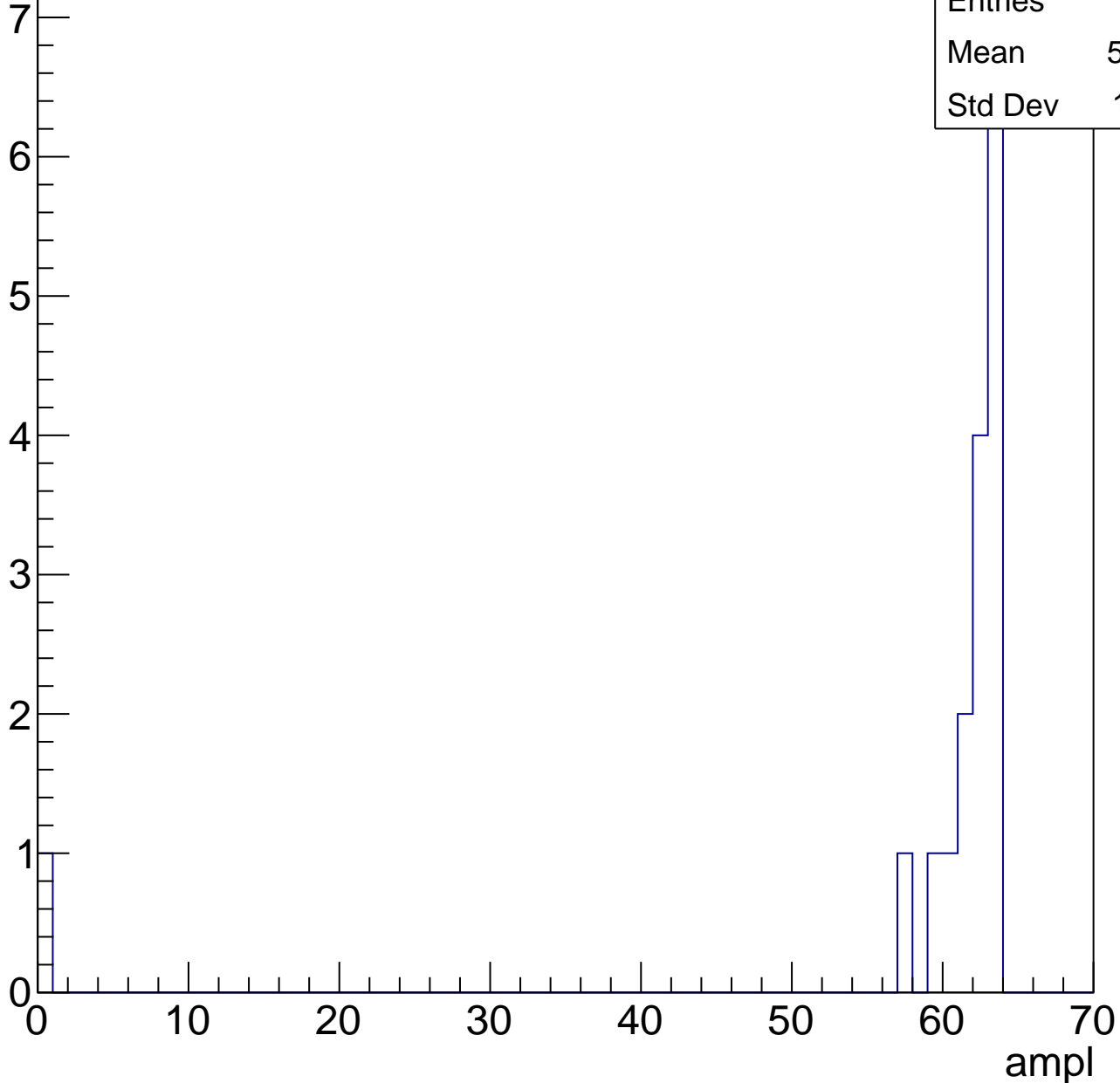


# B1L000S, U1-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.06
Std Dev	14.61





# B1L000S, U1-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch21, adc0

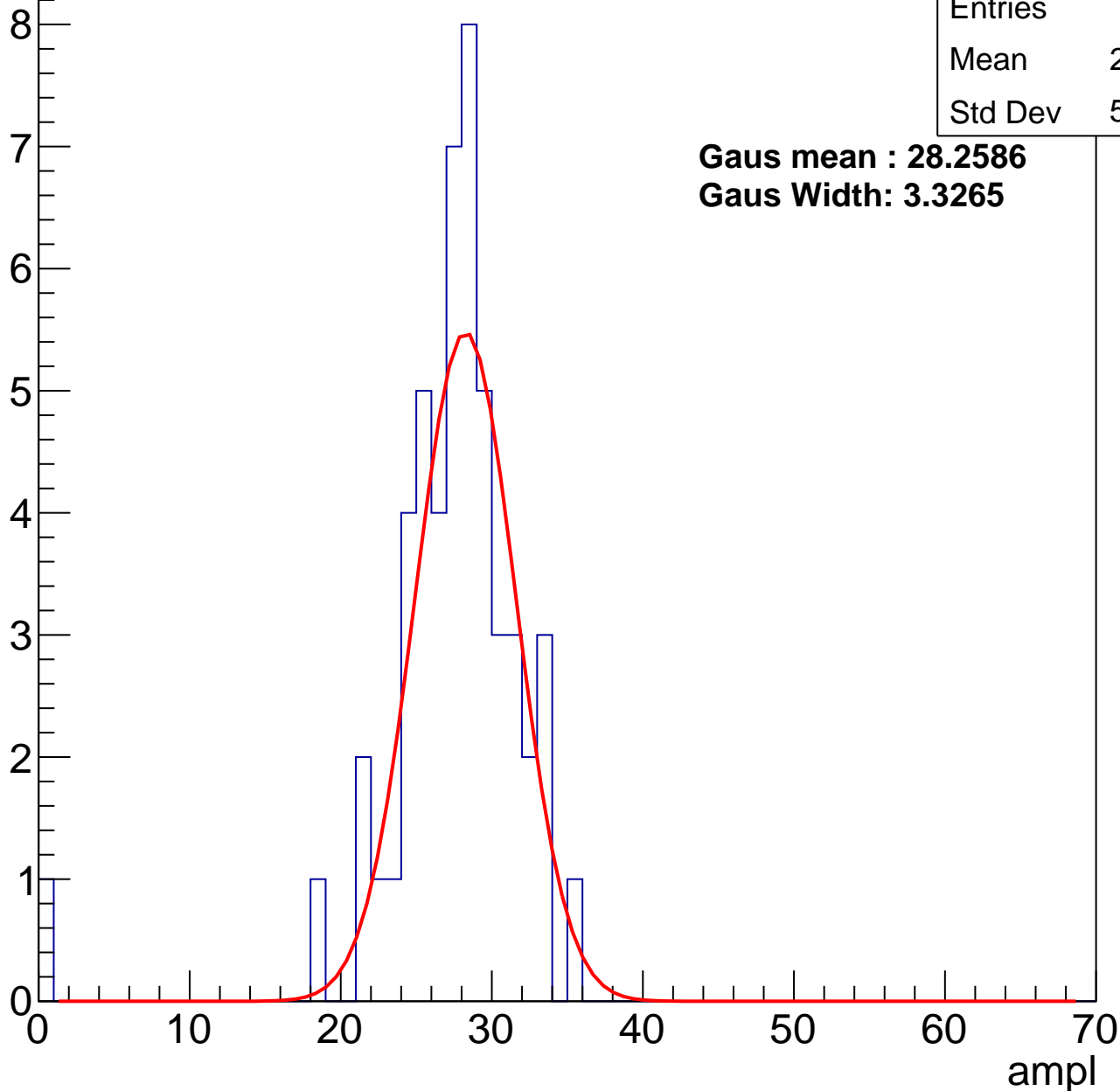
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	26.84
Std Dev	5.062

**Gaus mean : 28.2586**

**Gaus Width: 3.3265**



# B1L000S, U1-ch21, adc1

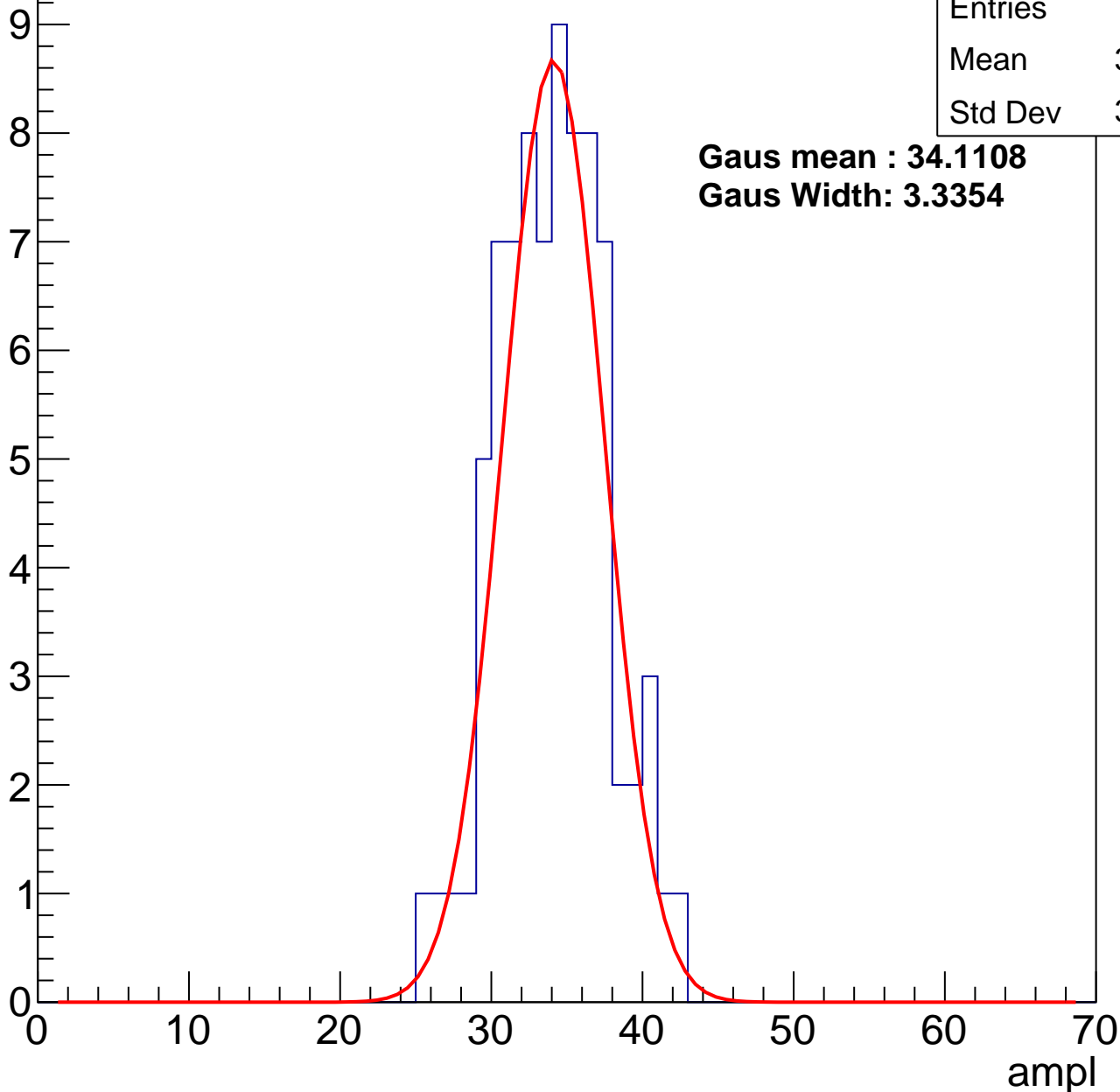
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	33.61
Std Dev	3.491

**Gaus mean : 34.1108**

**Gaus Width: 3.3354**



# B1L000S, U1-ch21, adc2

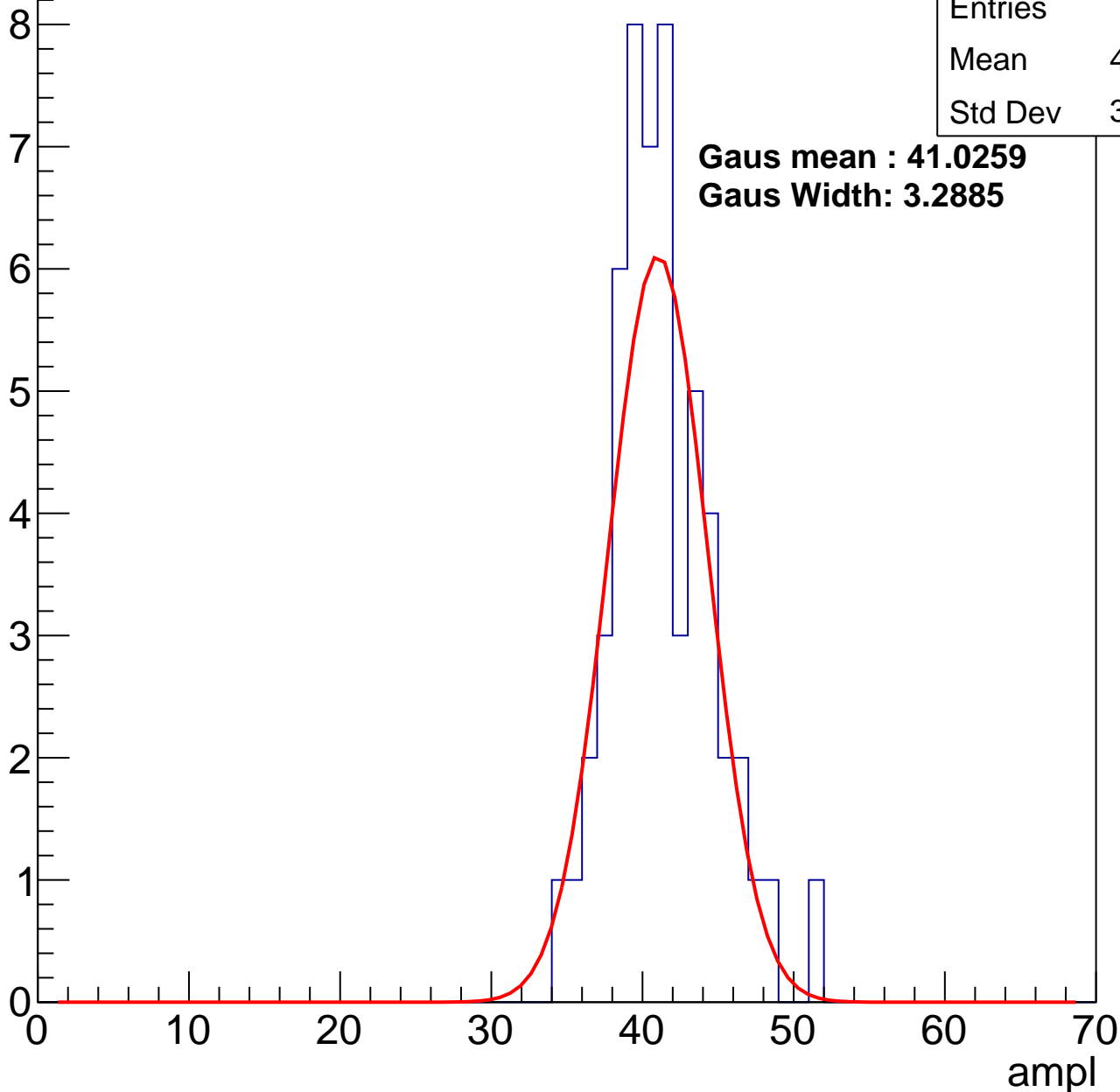
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	40.82
Std Dev	3.303

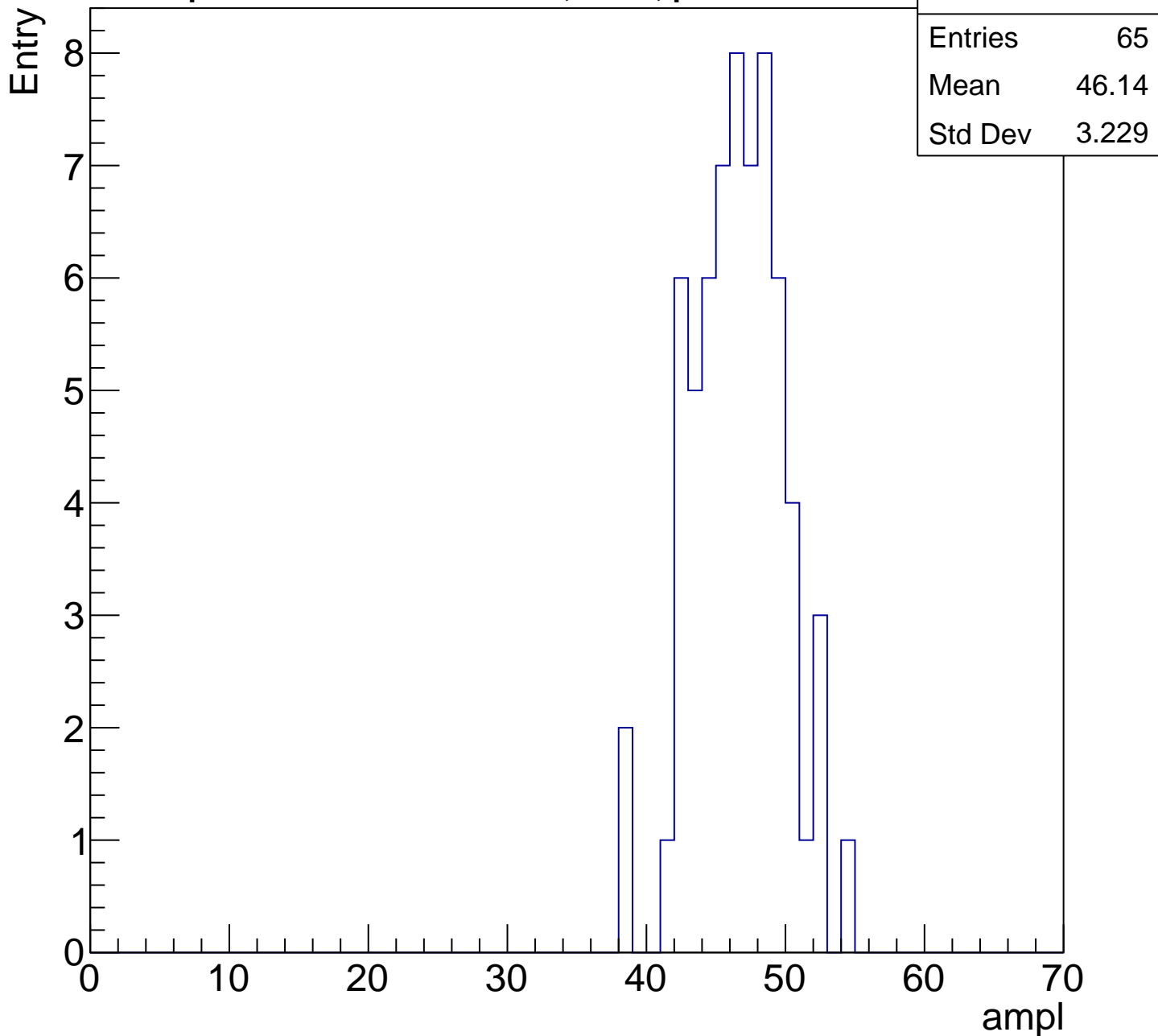
**Gaus mean : 41.0259**

**Gaus Width: 3.2885**



# B1L000S, U1-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

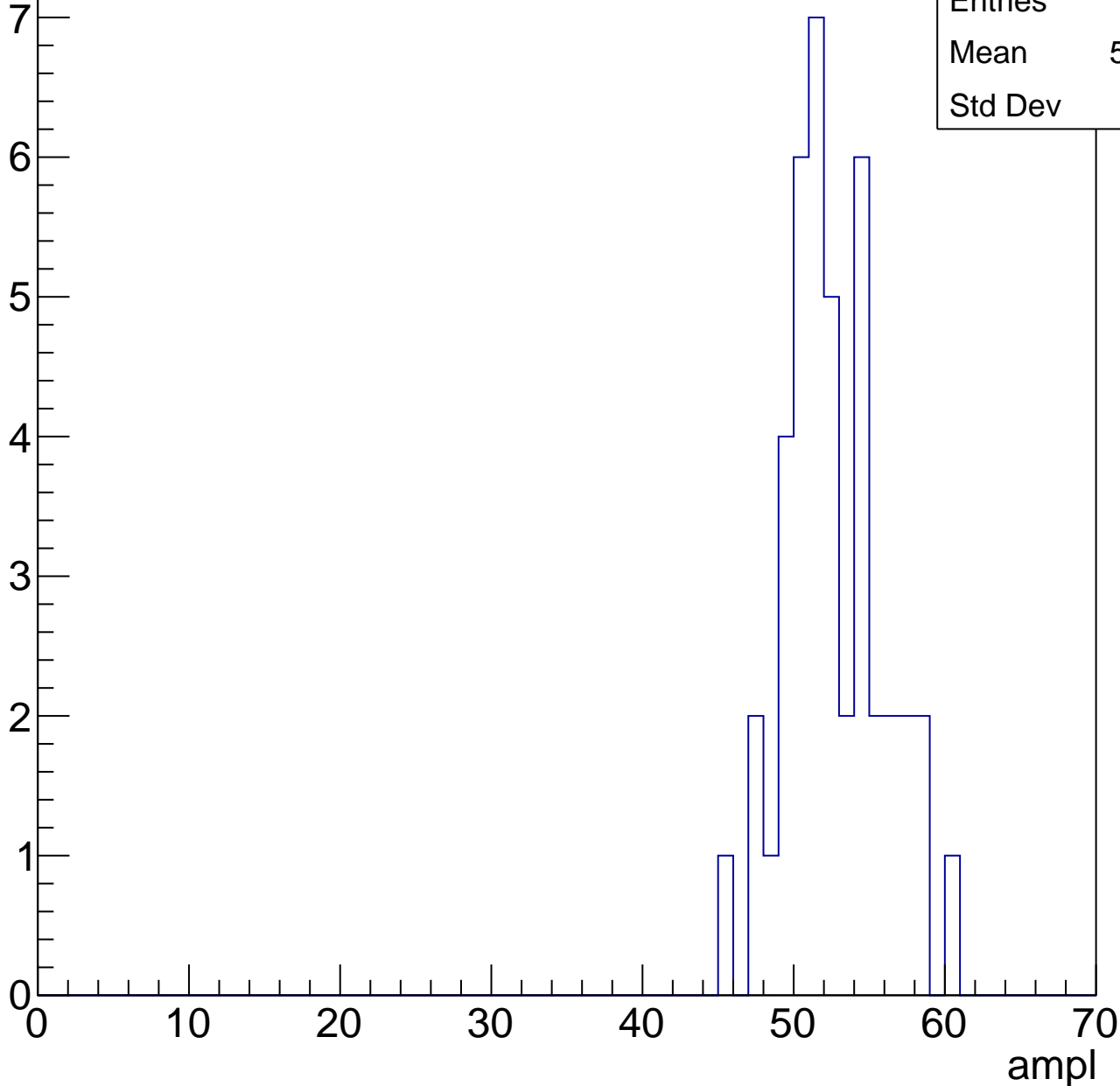


# B1L000S, U1-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

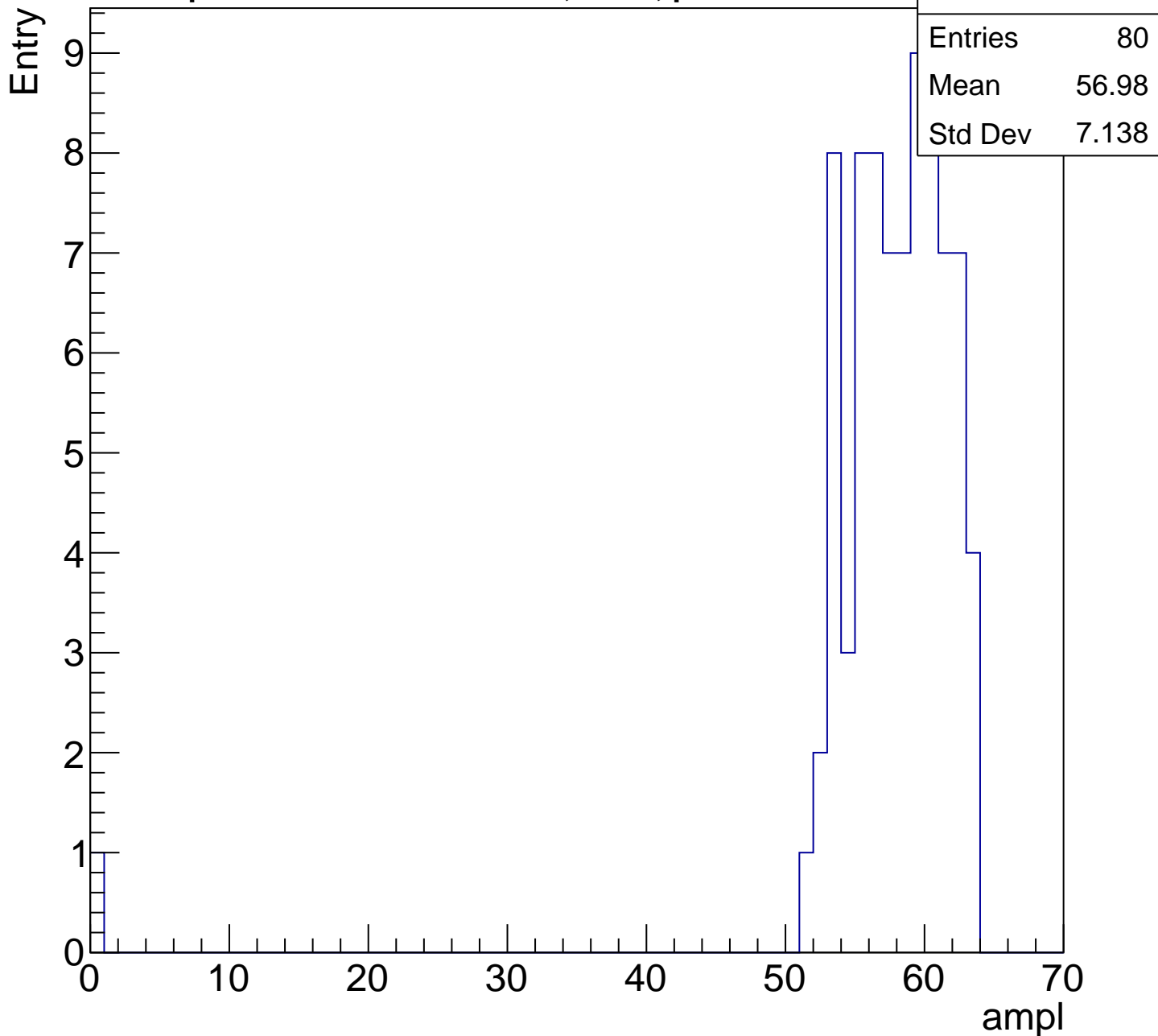
Entry

Entries	43
Mean	52.14
Std Dev	3.21



# B1L000S, U1-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

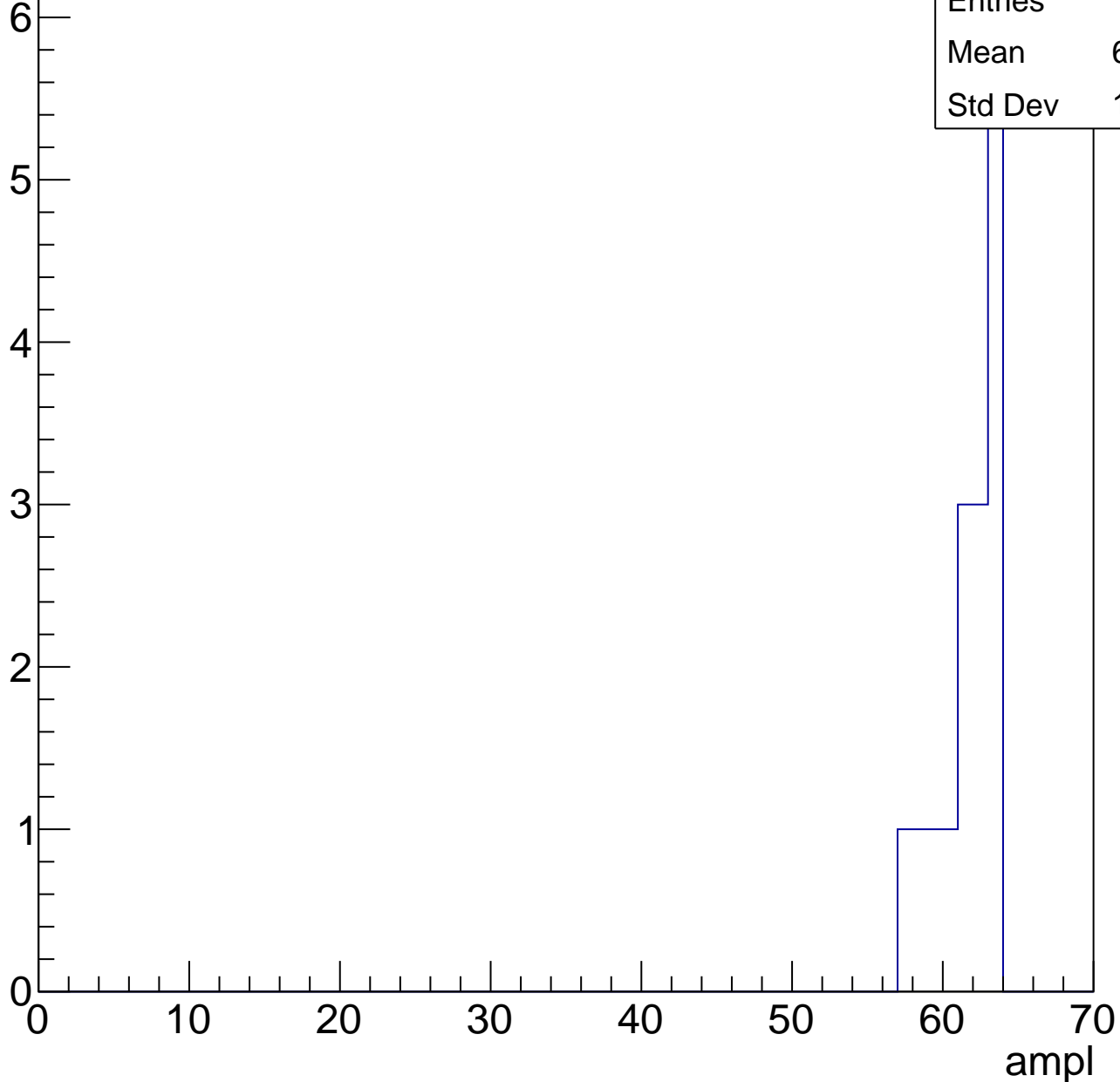


# B1L000S, U1-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.31
Std Dev	1.861





# B1L000S, U1-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L000S, U1-ch22, adc0

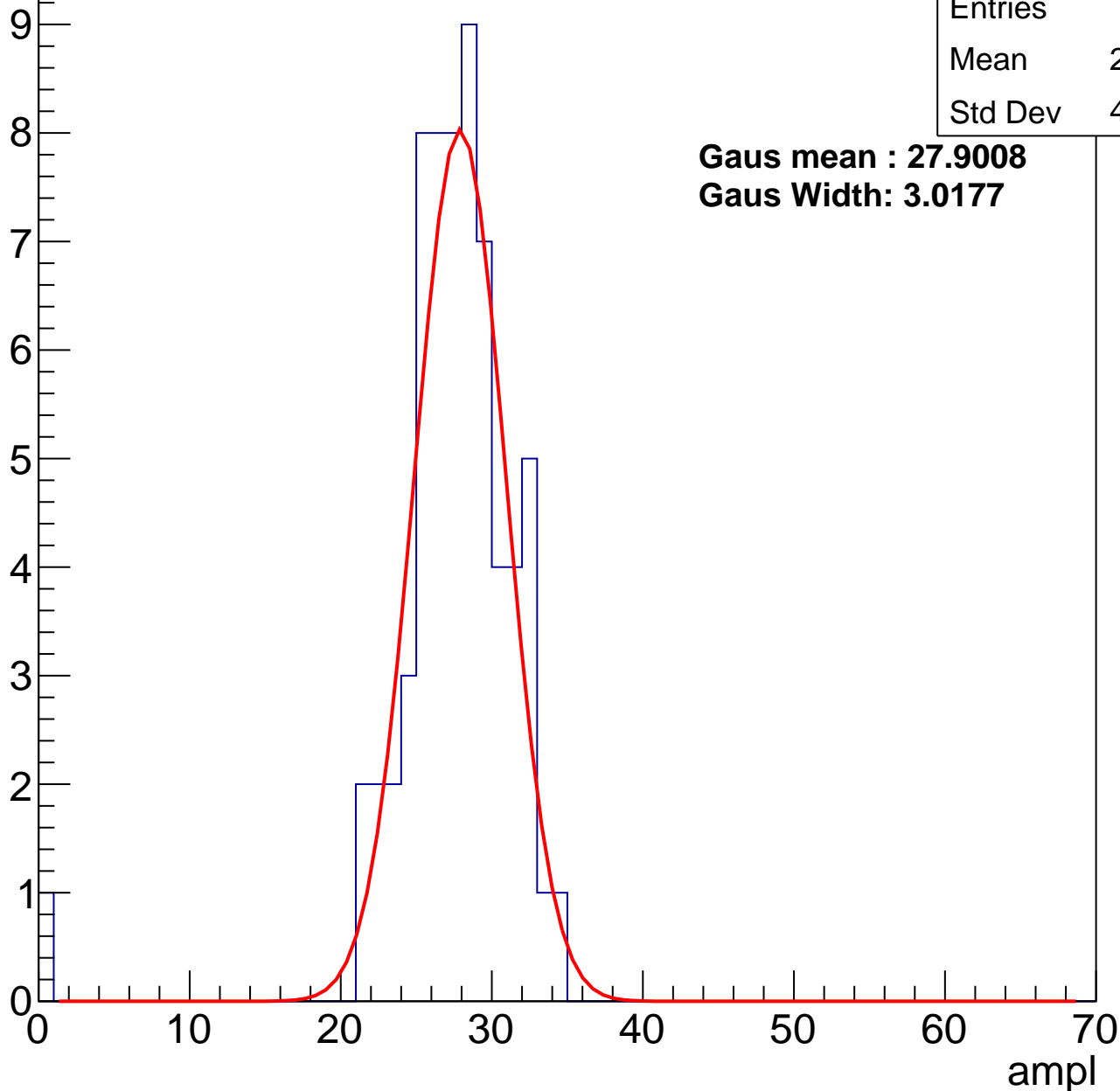
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	26.98
Std Dev	4.467

**Gaus mean : 27.9008**

**Gaus Width: 3.0177**



# B1L000S, U1-ch22, adc1

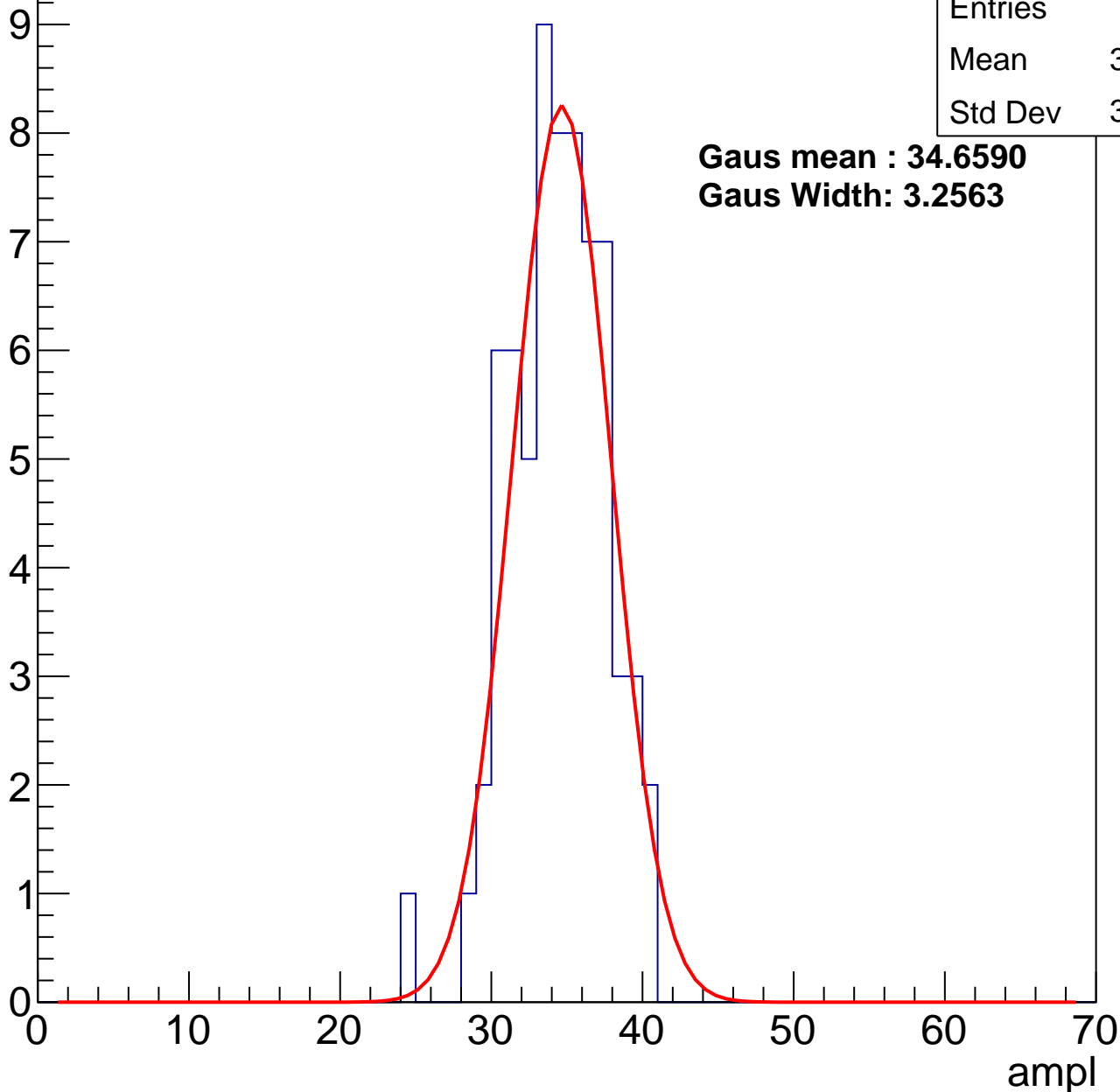
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	33.93
Std Dev	3.112

**Gaus mean : 34.6590**

**Gaus Width: 3.2563**



# B1L000S, U1-ch22, adc2

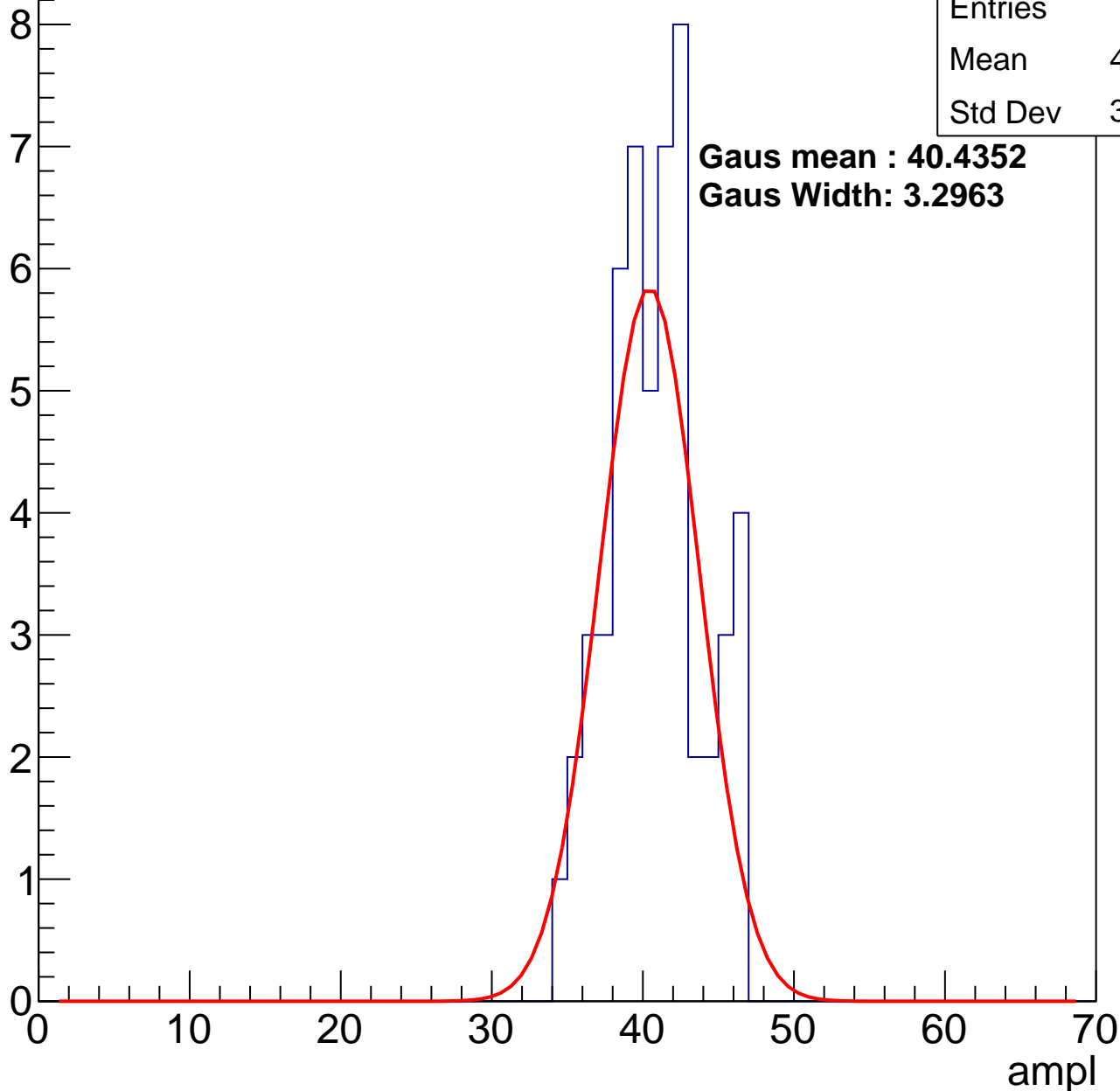
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	40.38
Std Dev	3.048

**Gaus mean : 40.4352**

**Gaus Width: 3.2963**

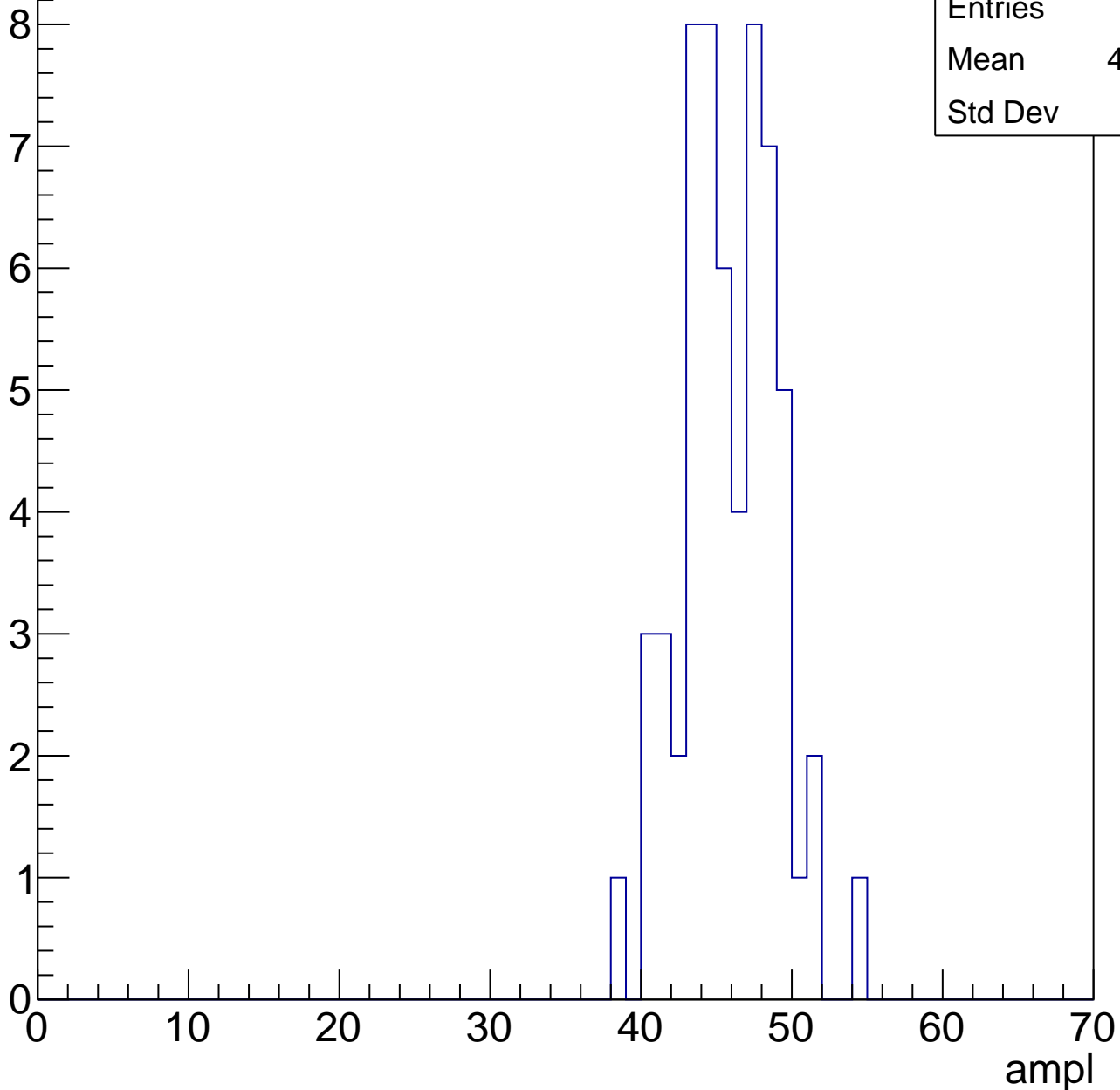


# B1L000S, U1-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	45.39
Std Dev	3.13

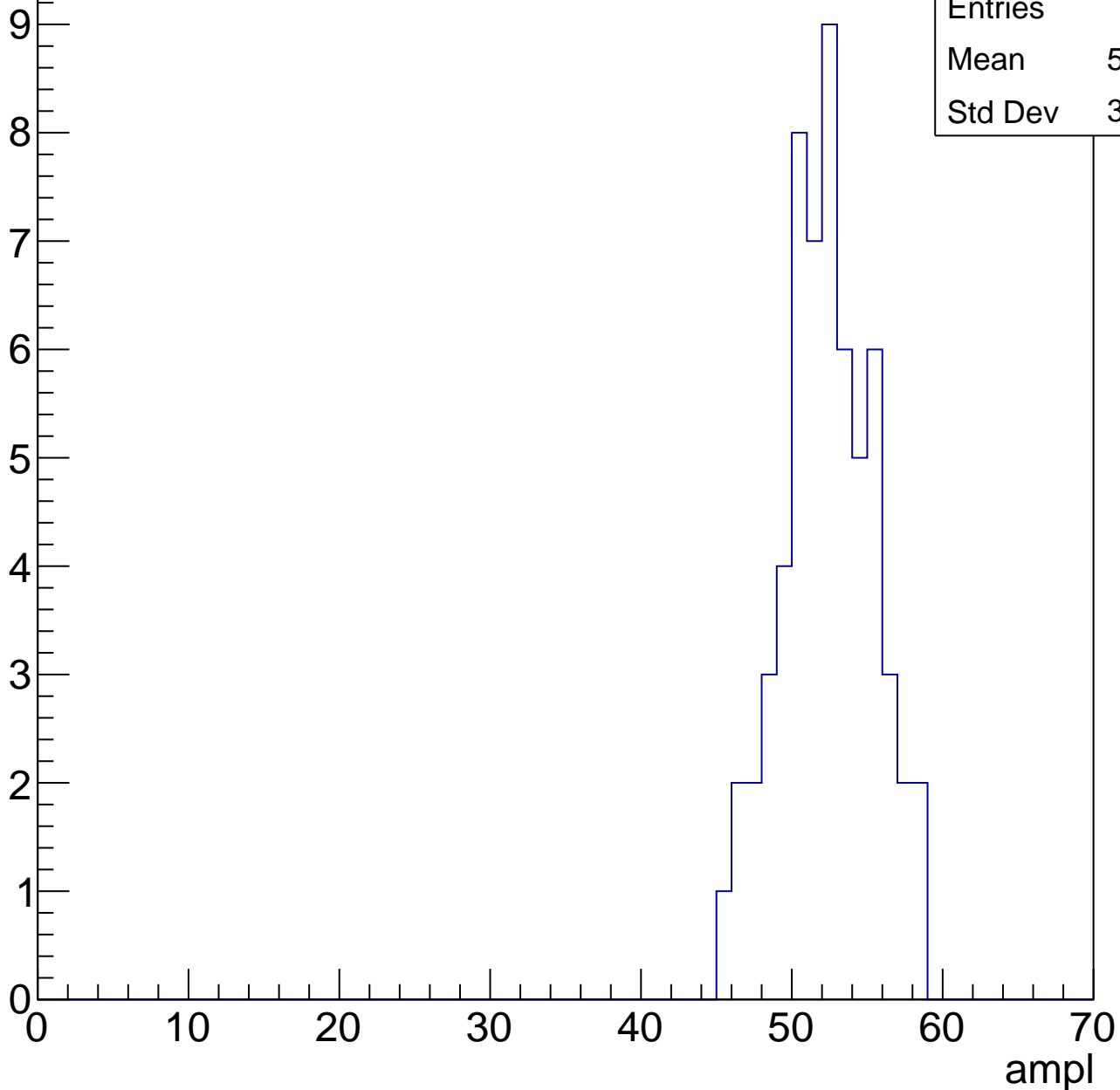


# B1L000S, U1-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	51.87
Std Dev	3.008

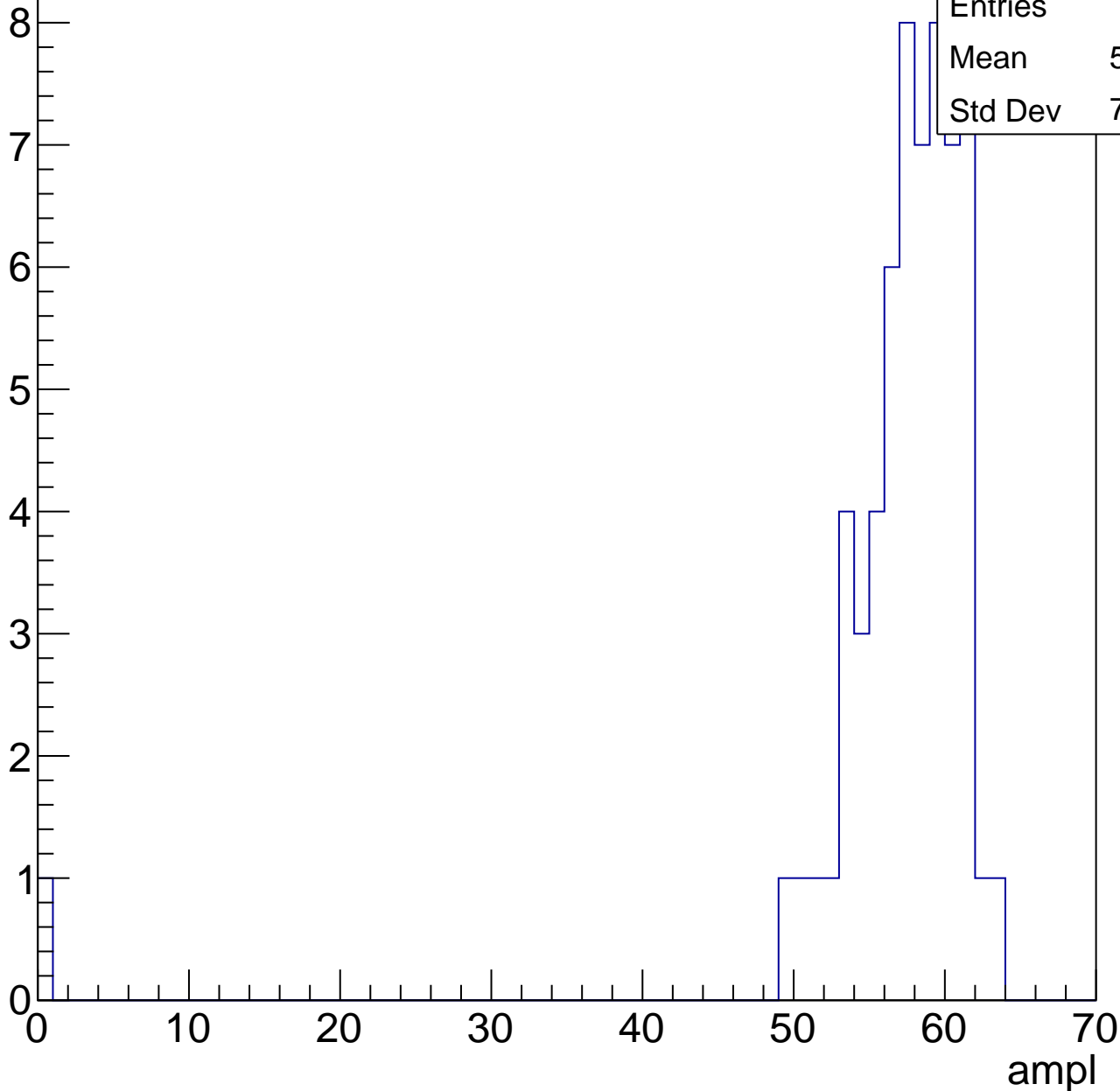


# B1L000S, U1-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	56.44
Std Dev	7.836

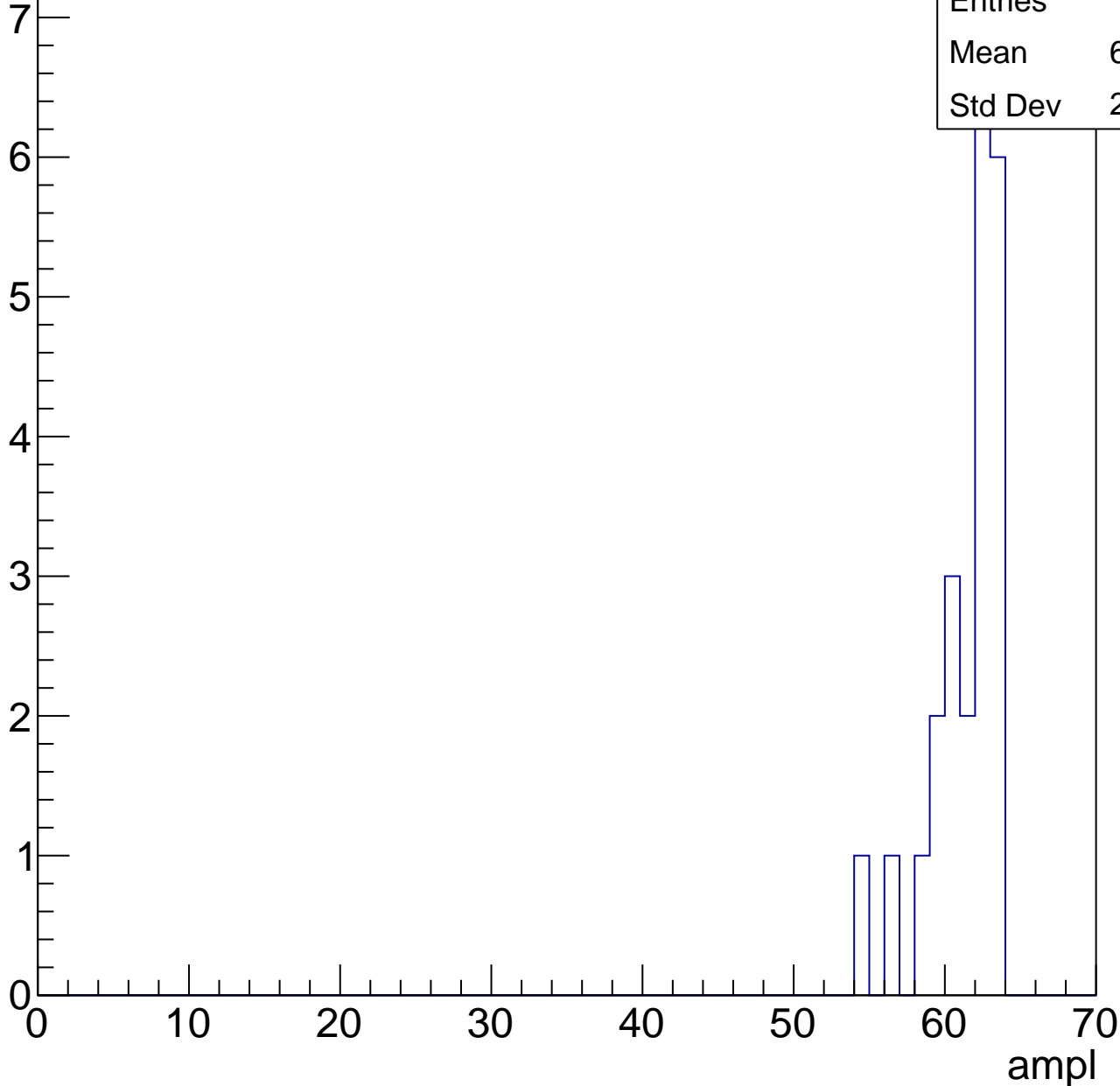


# B1L000S, U1-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	60.87
Std Dev	2.328

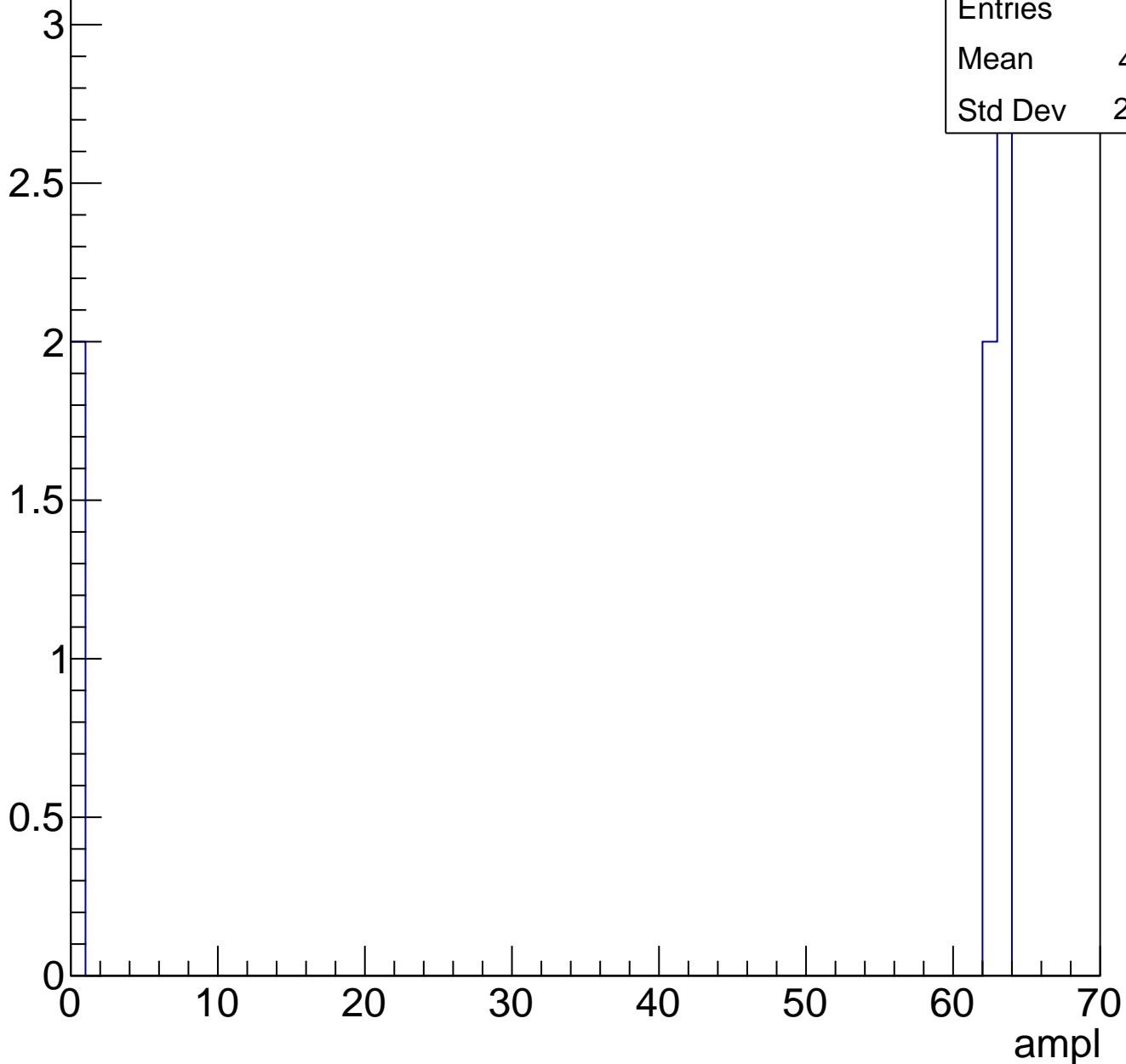




# B1L000S, U1-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch23, adc0

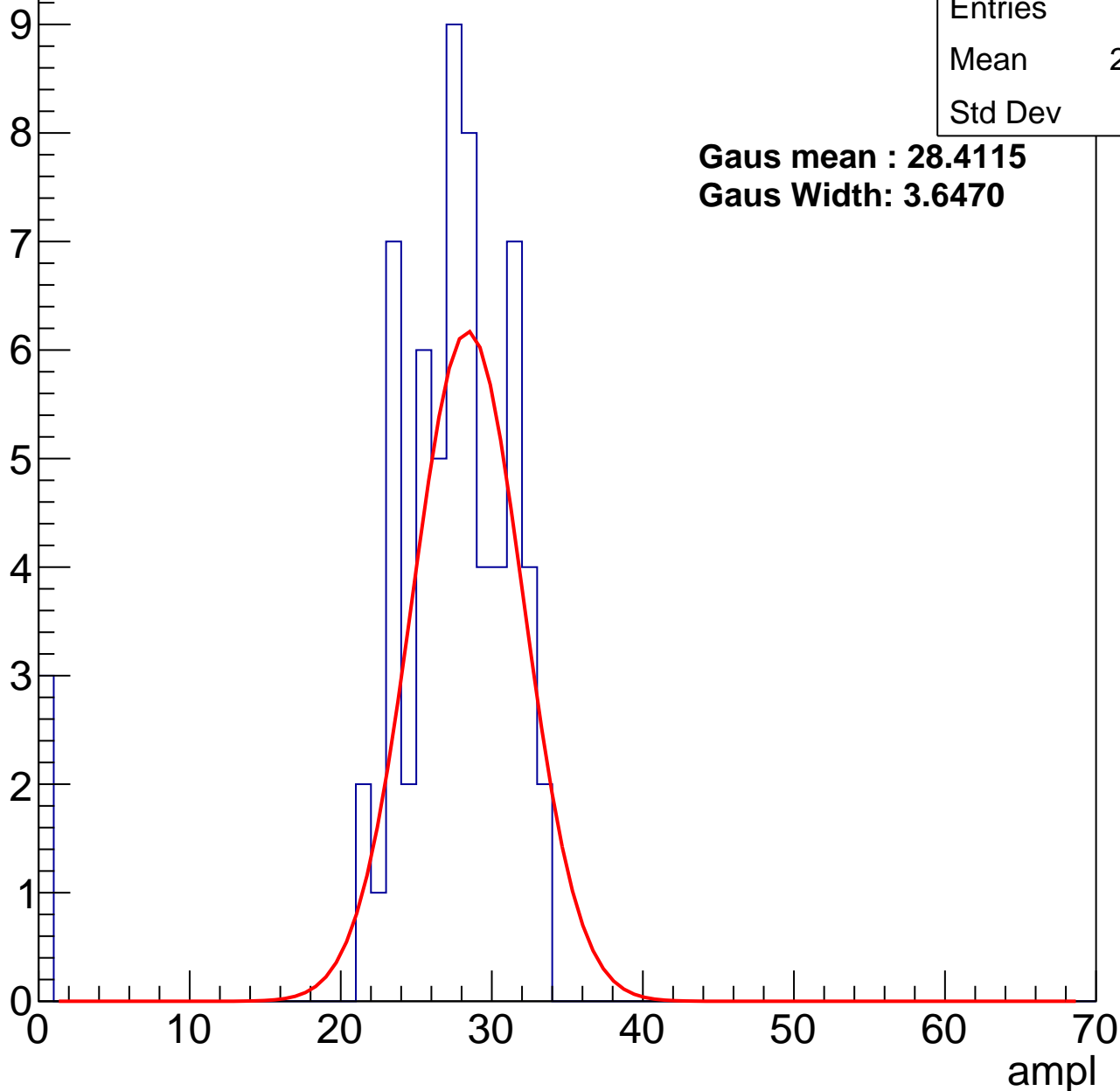
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	26.05
Std Dev	6.53

**Gaus mean : 28.4115**

**Gaus Width: 3.6470**



# B1L000S, U1-ch23, adc1

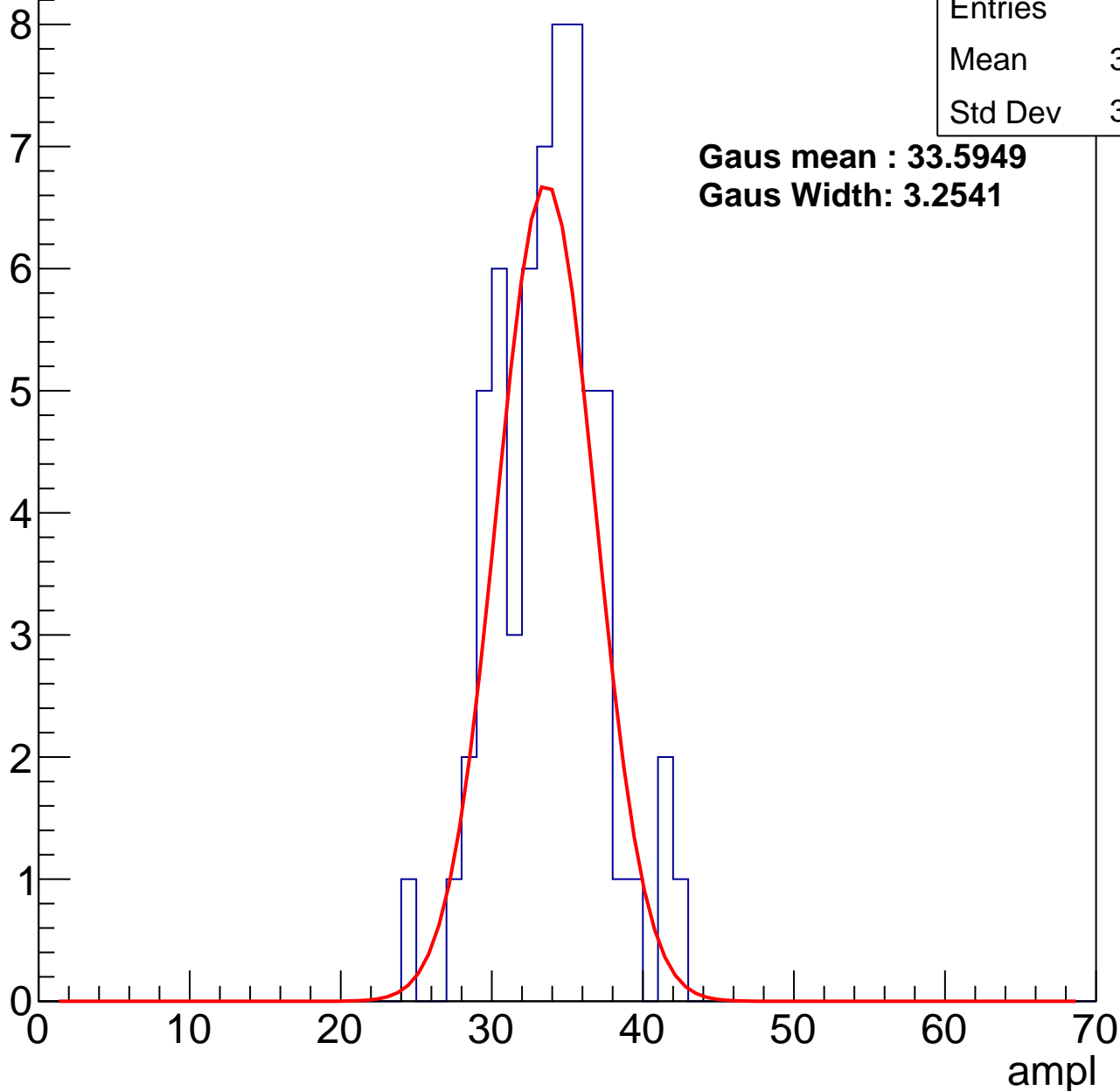
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	33.32
Std Dev	3.472

**Gaus mean : 33.5949**

**Gaus Width: 3.2541**



# B1L000S, U1-ch23, adc2

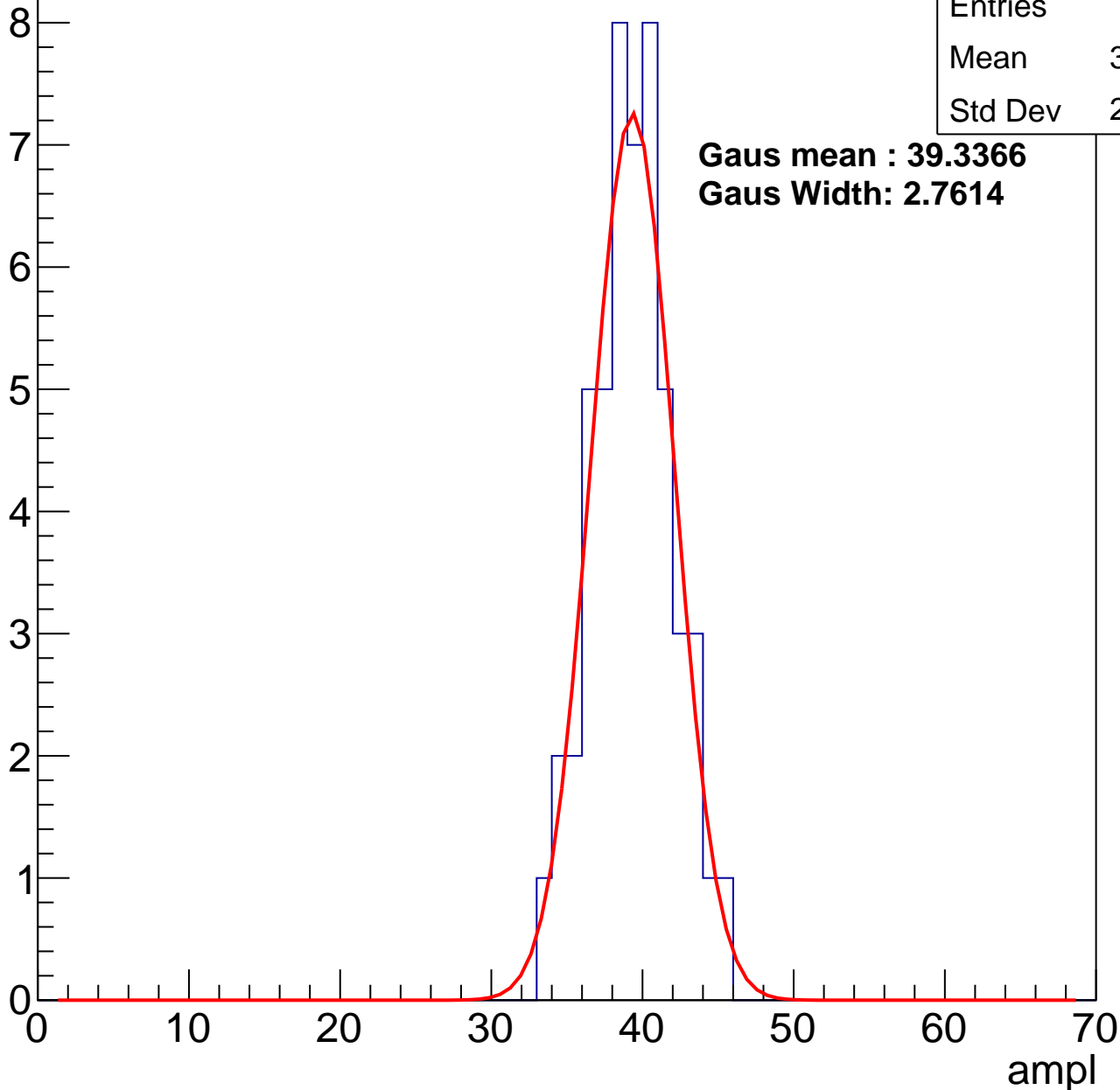
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	38.86
Std Dev	2.635

**Gaus mean : 39.3366**

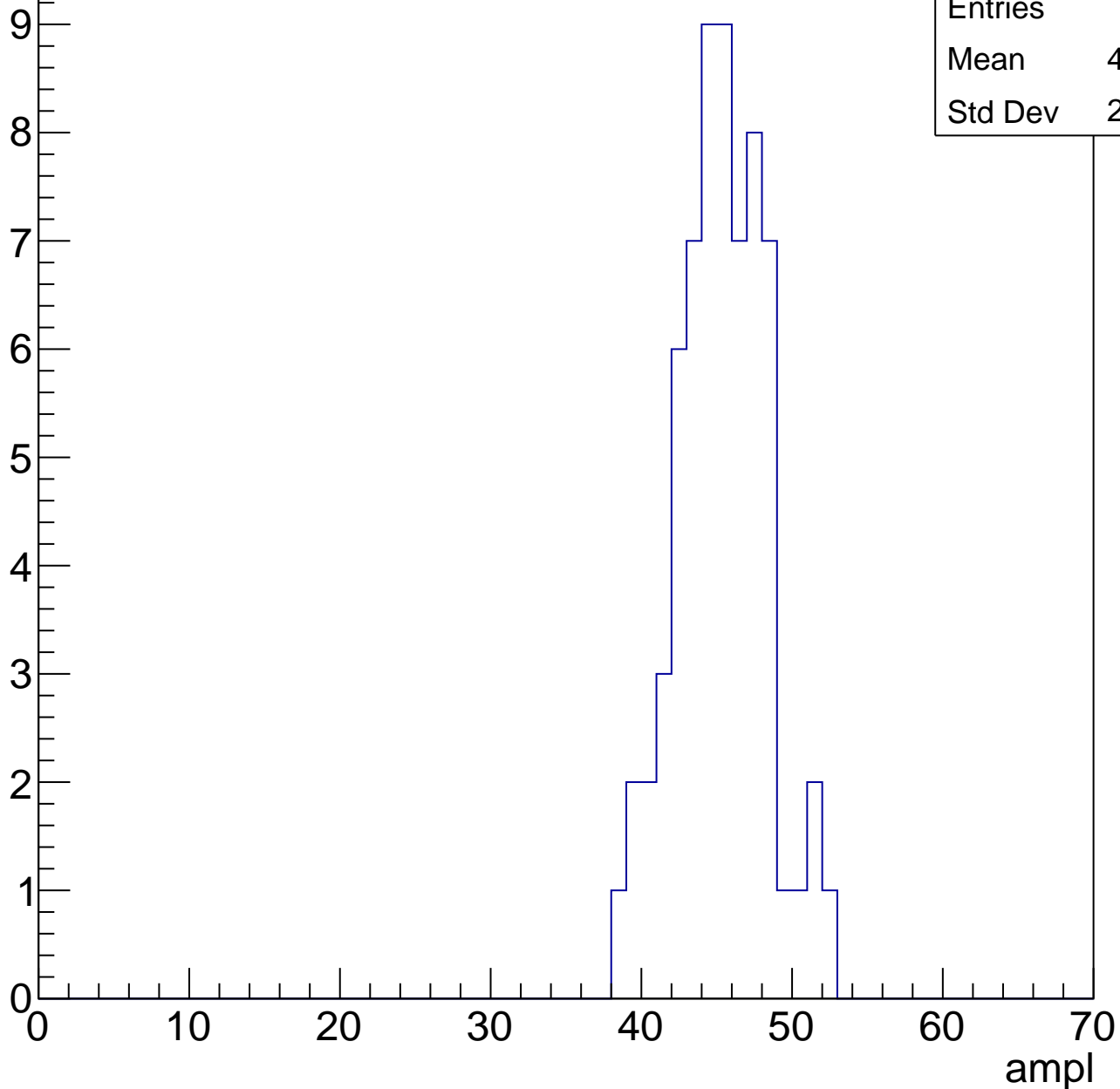
**Gaus Width: 2.7614**



# B1L000S, U1-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	66
Mean	44.85
Std Dev	2.945

# B1L000S, U1-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	51.69
Std Dev	2.891

Entry

10

8

6

4

2

0

0

10

20

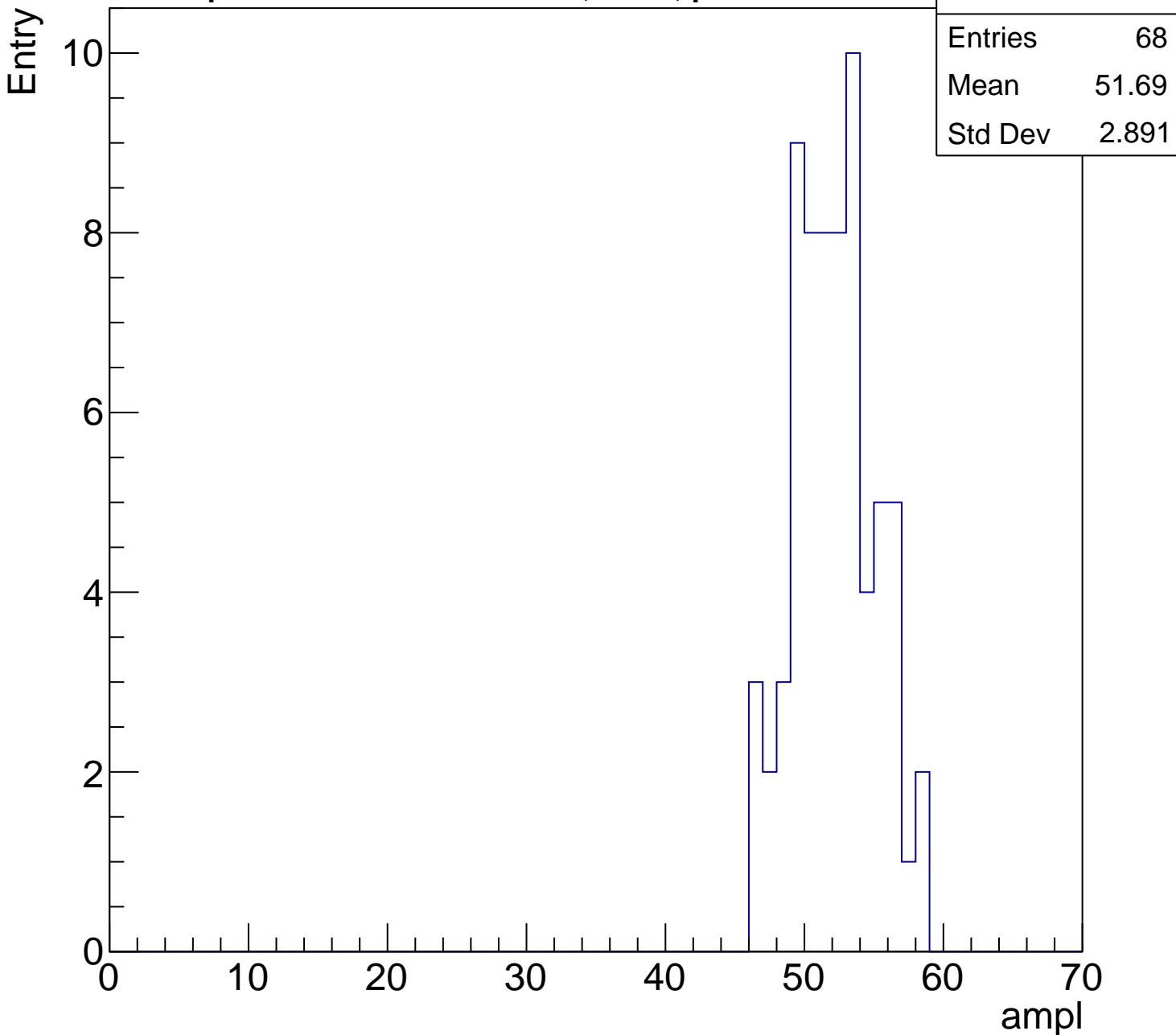
30

40

50

60

ampl

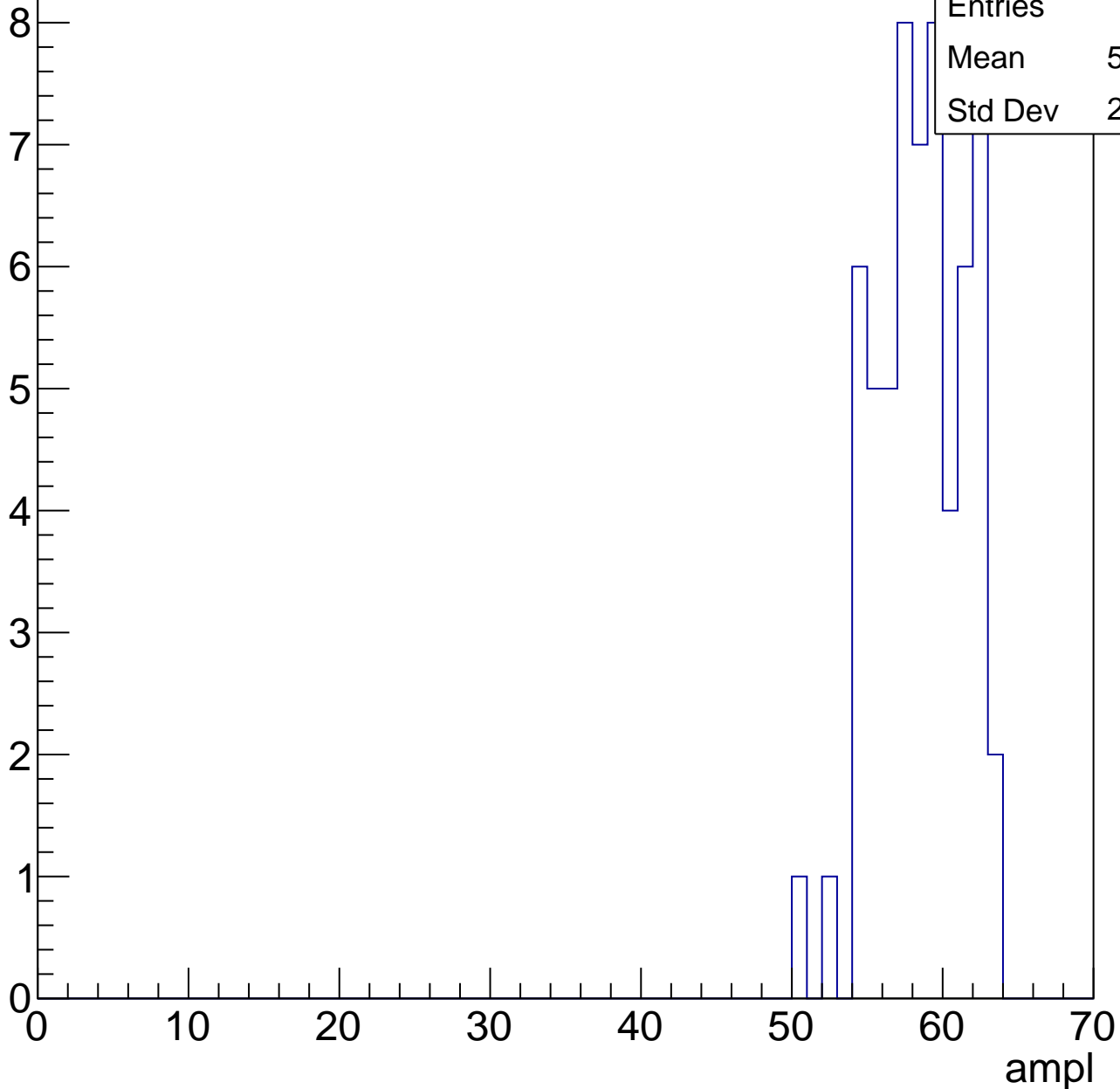


# B1L000S, U1-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	58.08
Std Dev	2.933

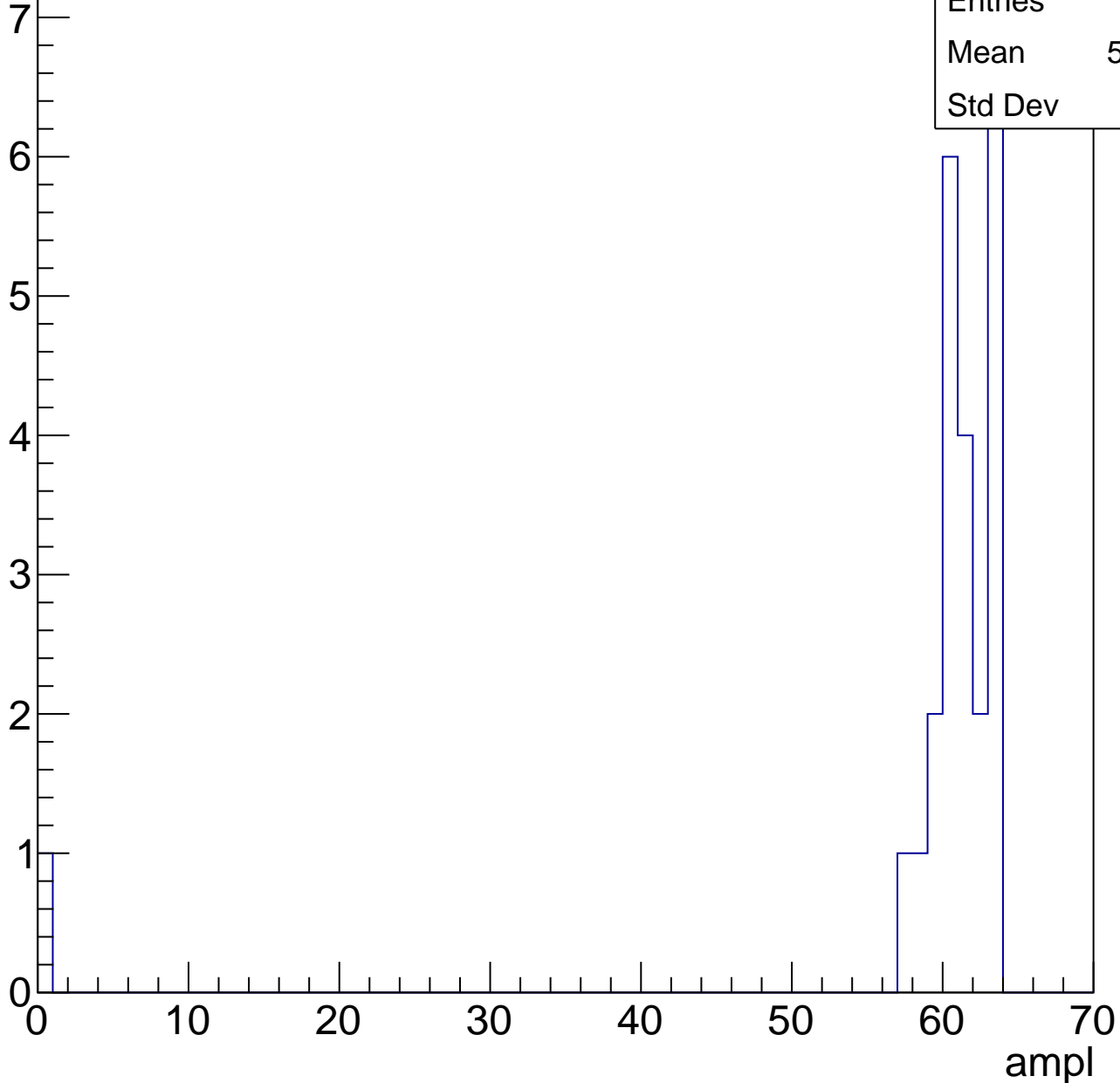


# B1L000S, U1-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.42
Std Dev	12.3

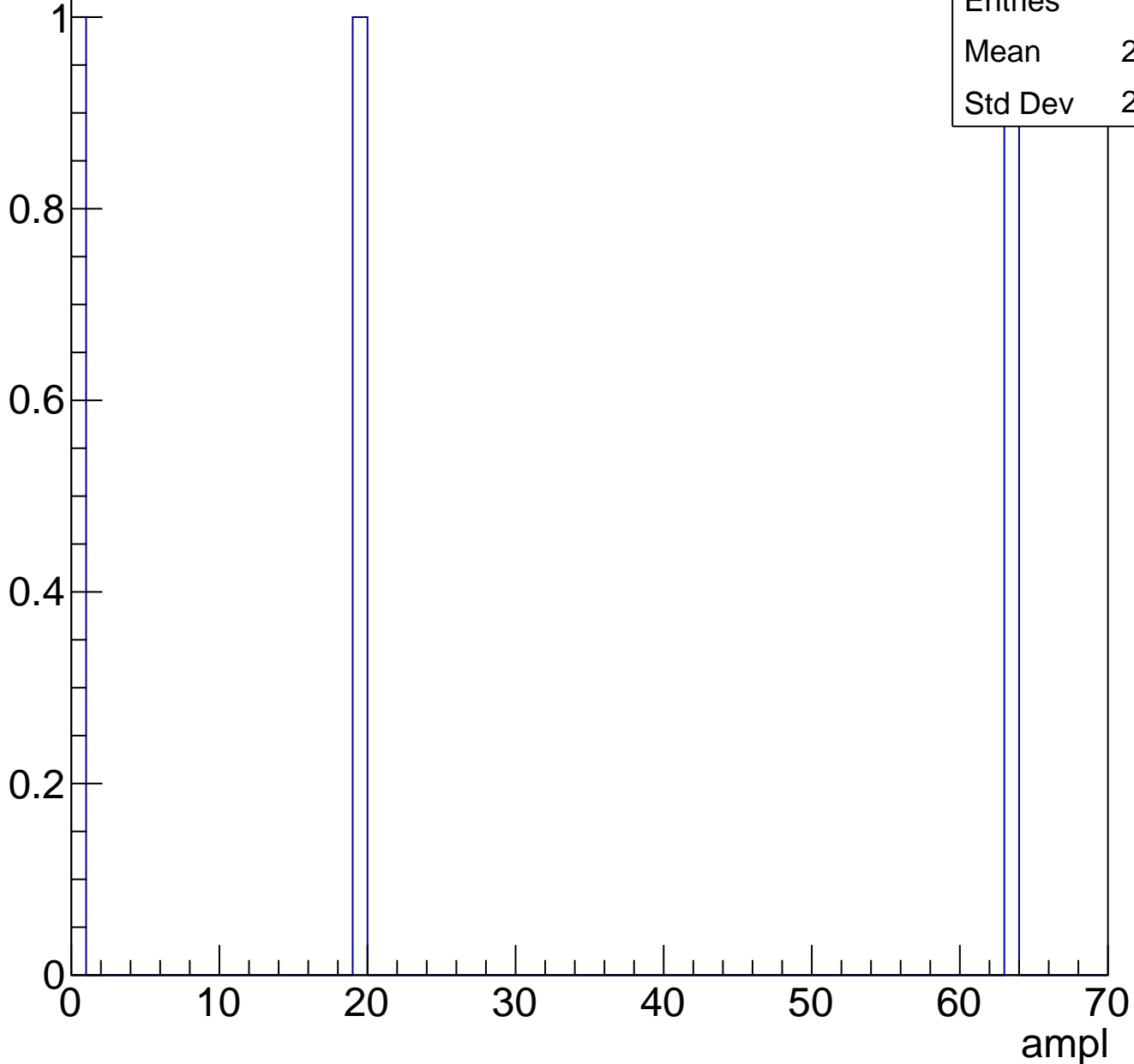




# B1L000S, U1-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	27.33
Std Dev	26.39

# B1L000S, U1-ch24, adc0

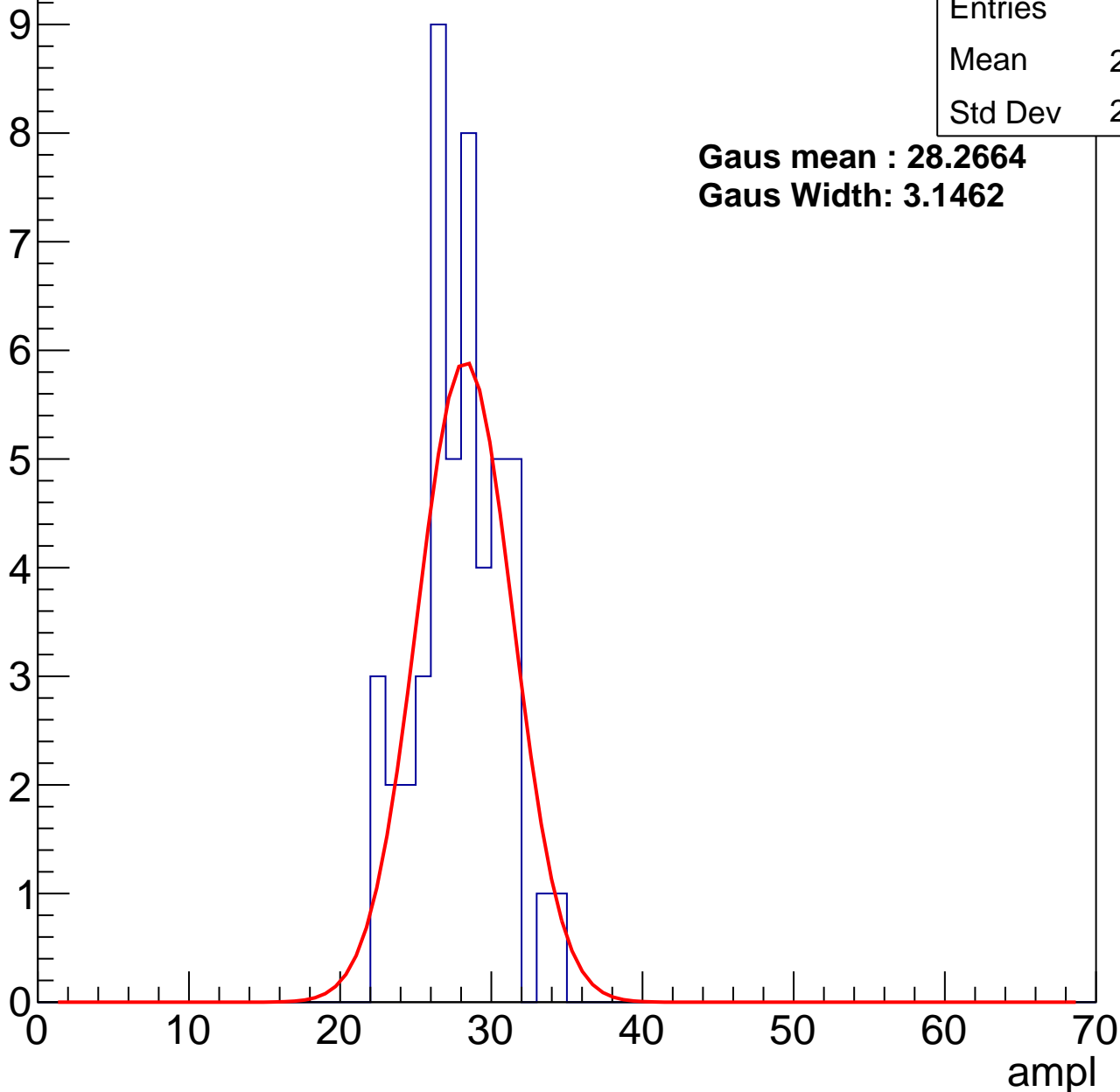
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	27.42
Std Dev	2.783

**Gaus mean : 28.2664**

**Gaus Width: 3.1462**



# B1L000S, U1-ch24, adc1

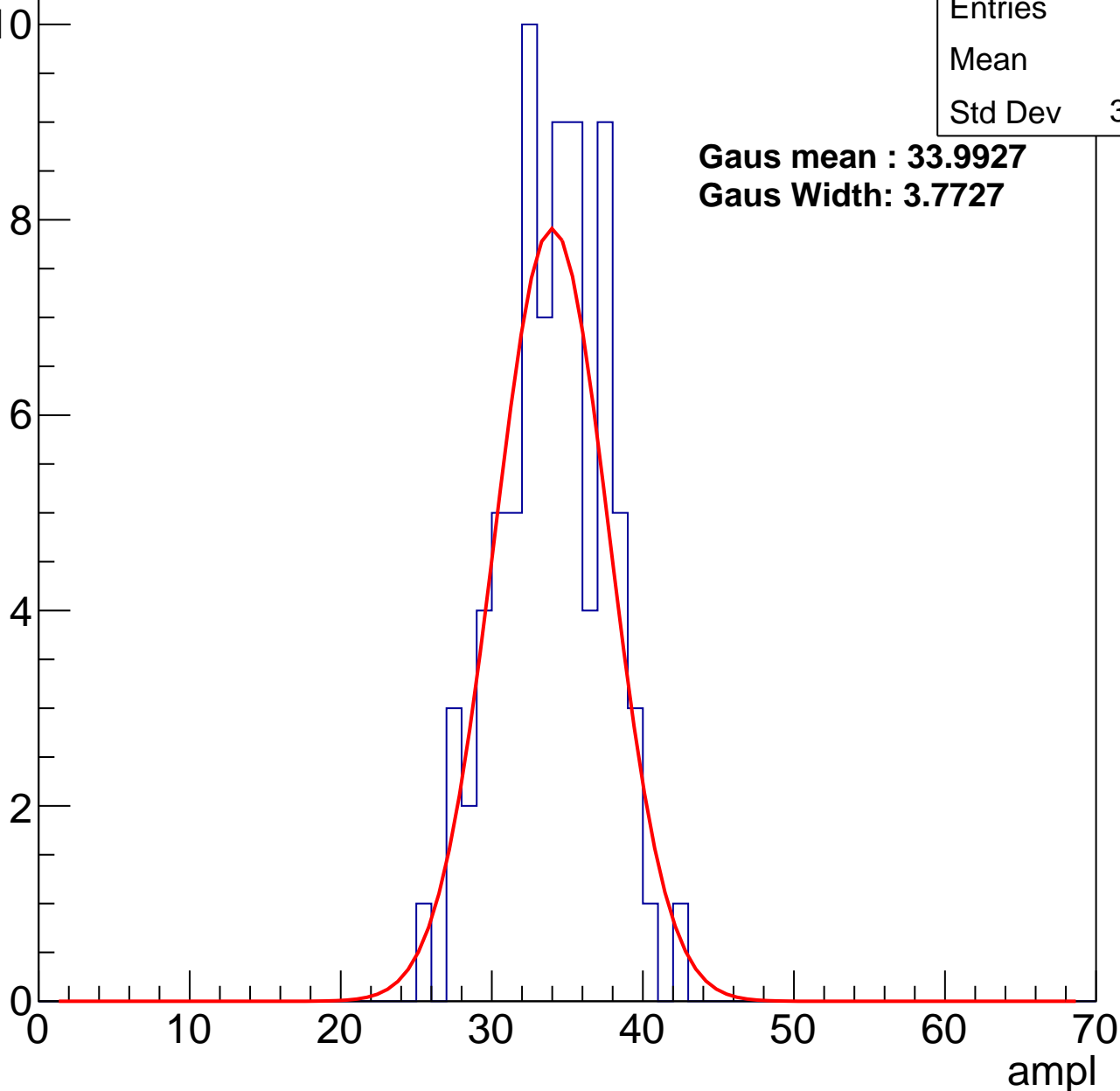
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	33.6
Std Dev	3.436

**Gaus mean : 33.9927**

**Gaus Width: 3.7727**



# B1L000S, U1-ch24, adc2

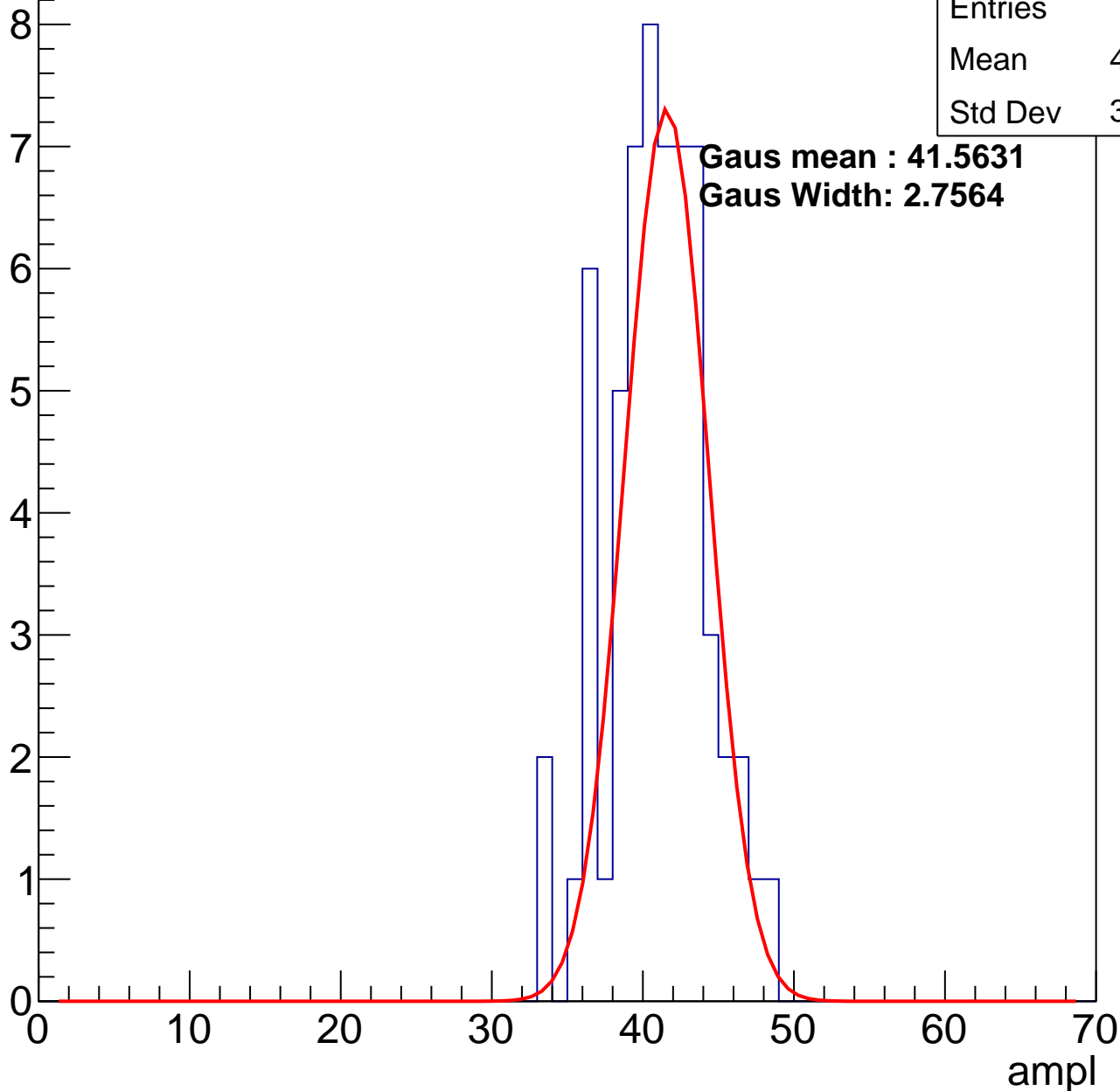
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	40.47
Std Dev	3.222

**Gaus mean : 41.5631**

**Gaus Width: 2.7564**

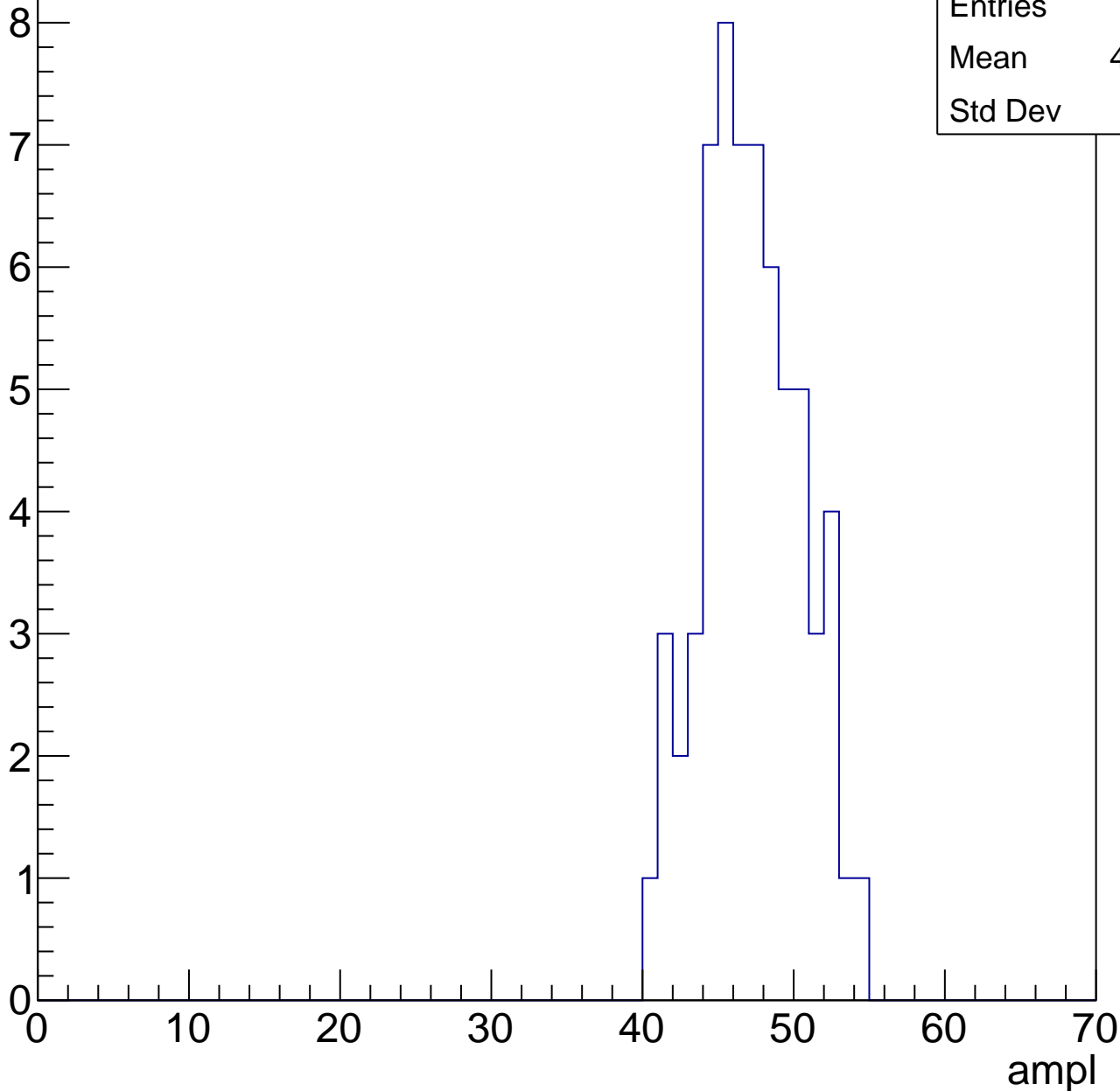


# B1L000S, U1-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	46.76
Std Dev	3.23

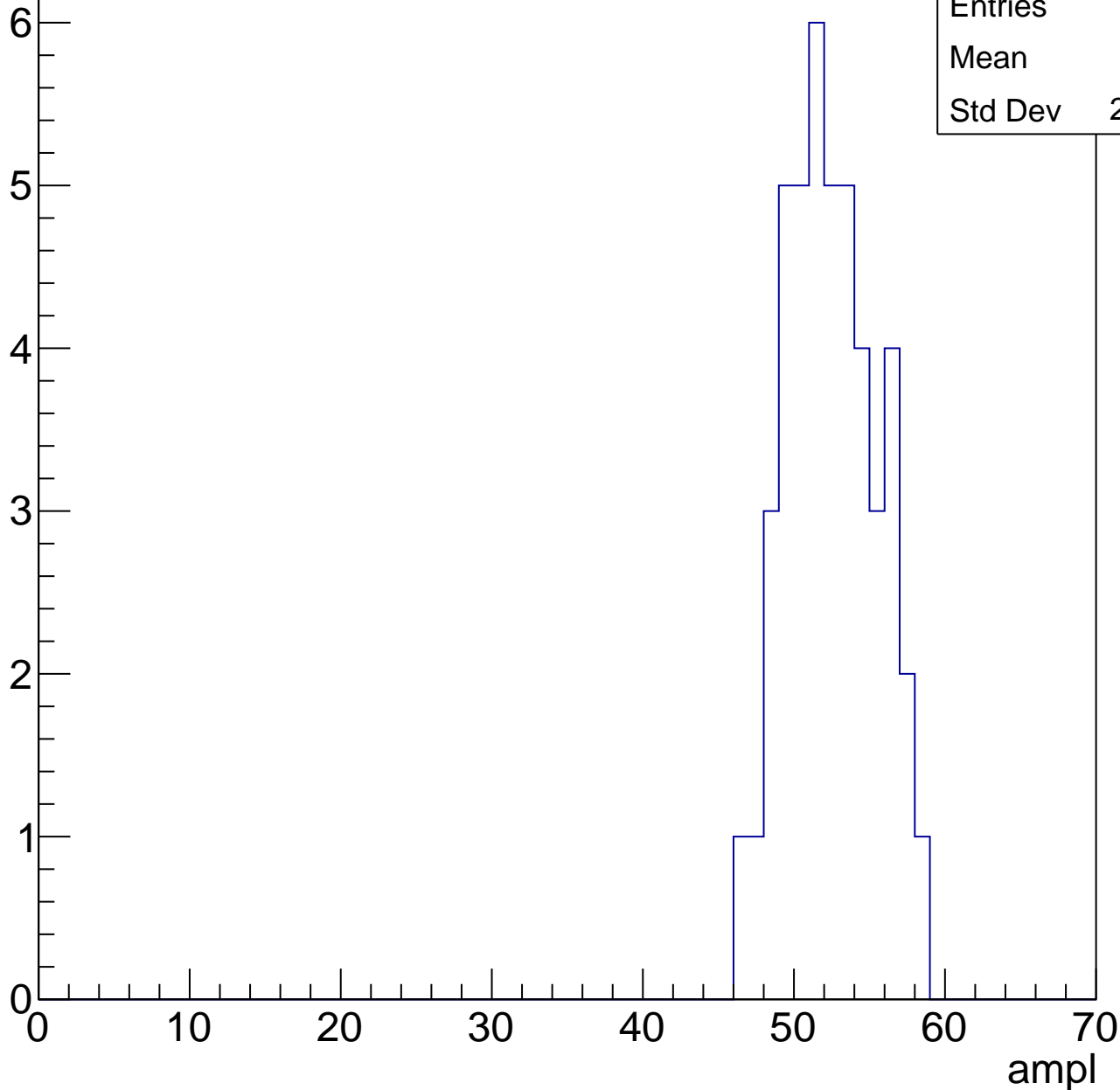


# B1L000S, U1-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

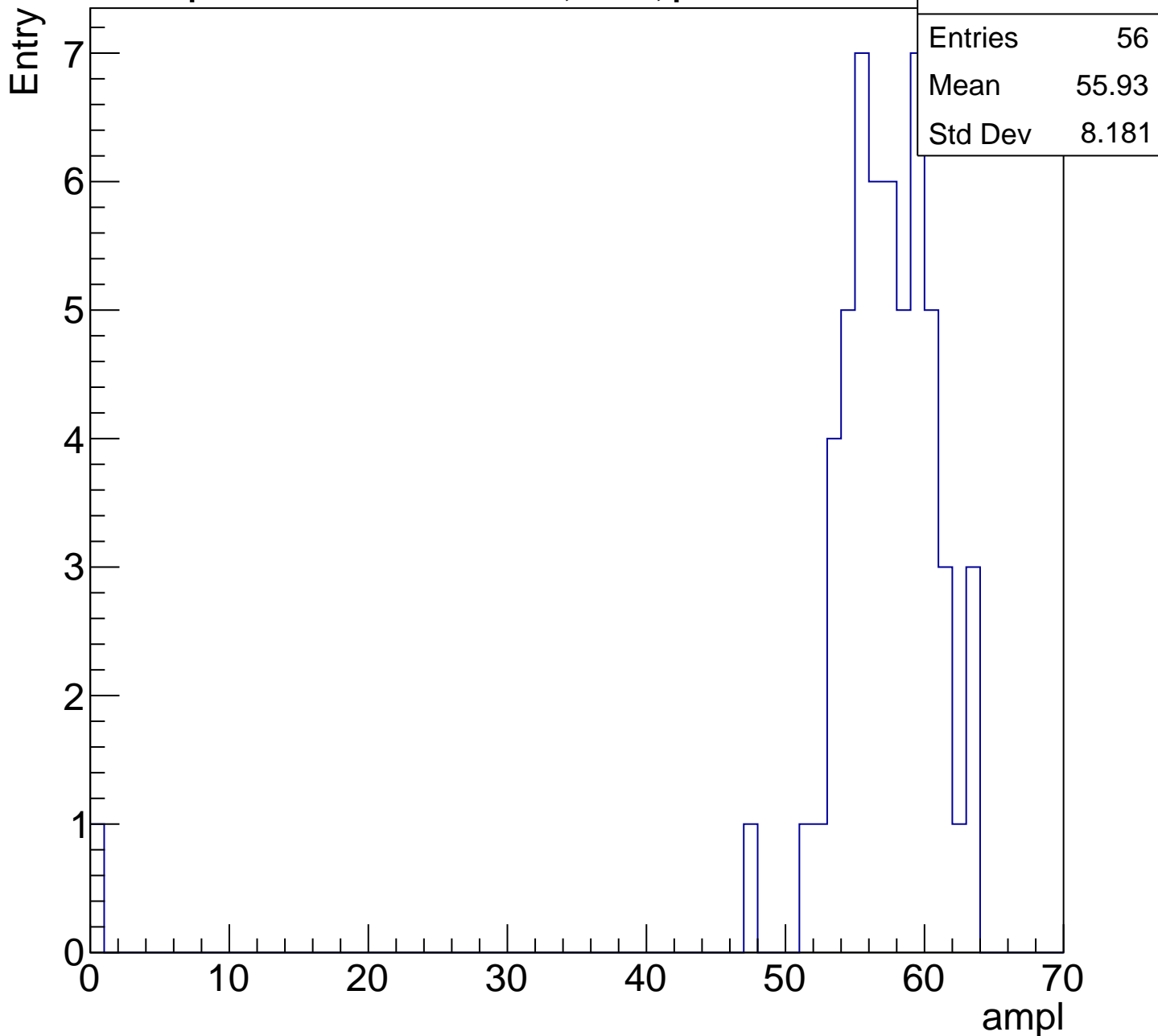
Entry

Entries	45
Mean	52
Std Dev	2.898



# B1L000S, U1-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

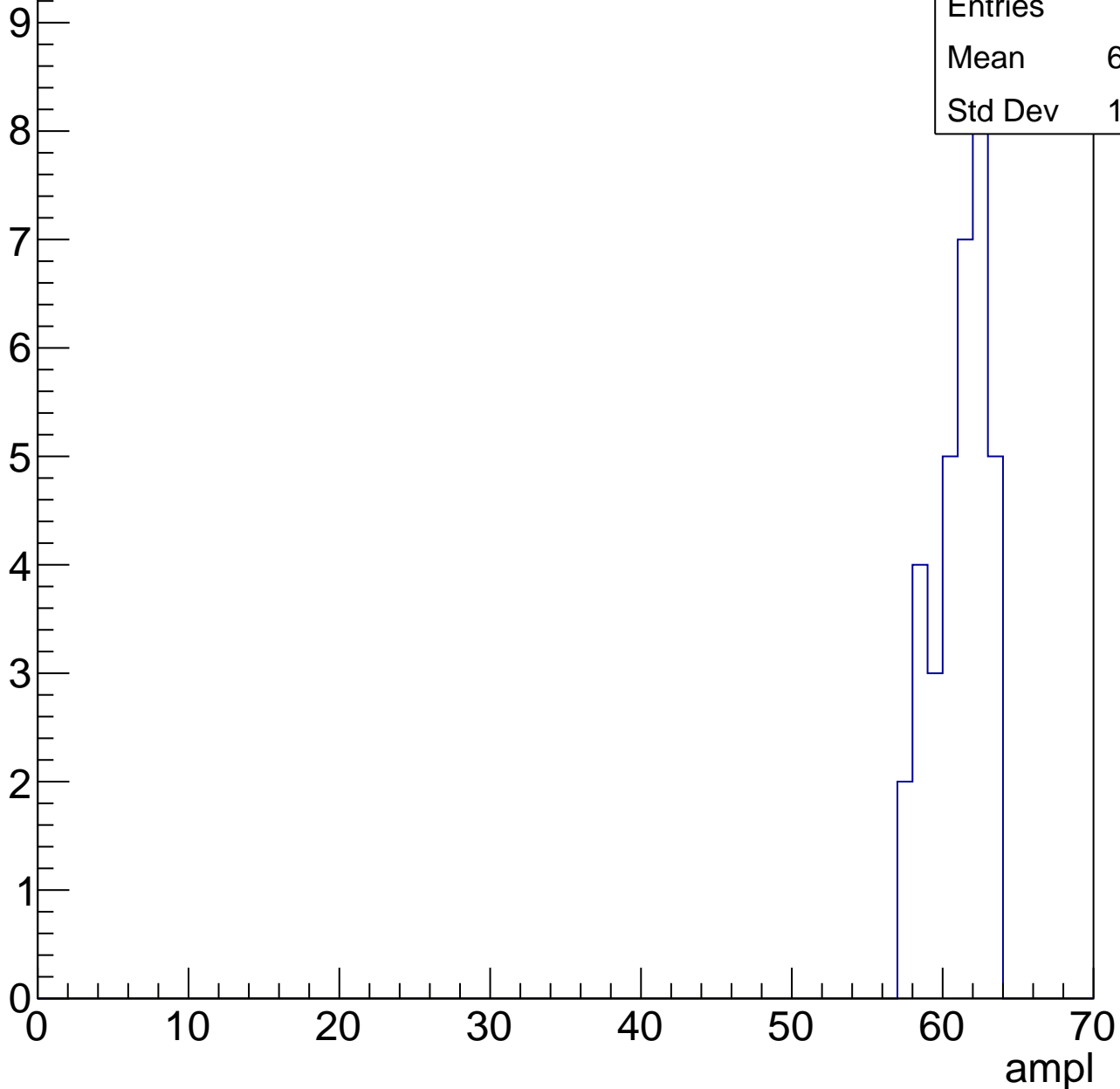


# B1L000S, U1-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	35
Mean	60.66
Std Dev	1.772

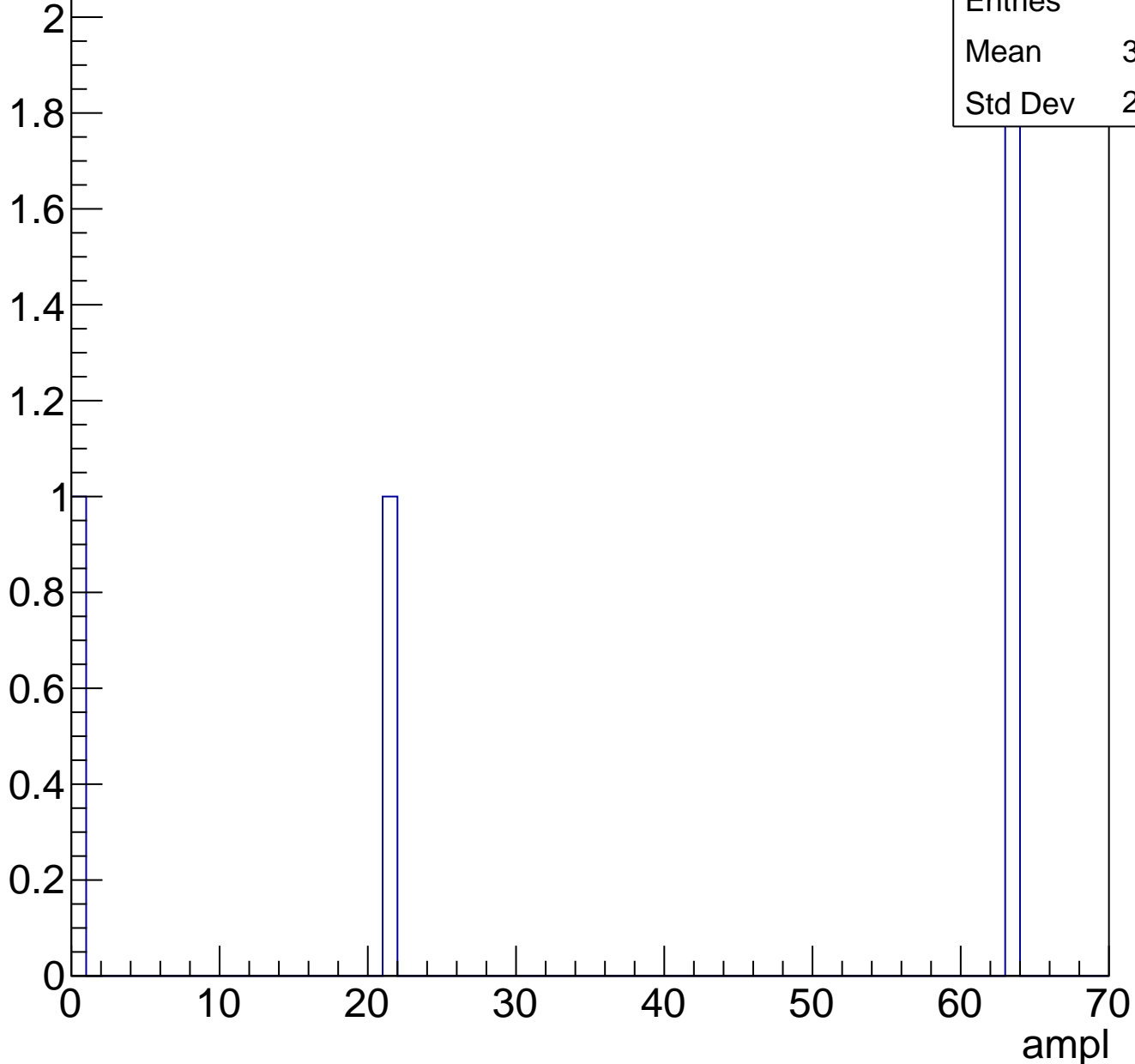




# B1L000S, U1-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	36.75
Std Dev	27.28

# B1L000S, U1-ch25, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	27.82
Std Dev	3.652

**Gaus mean : 28.9710**

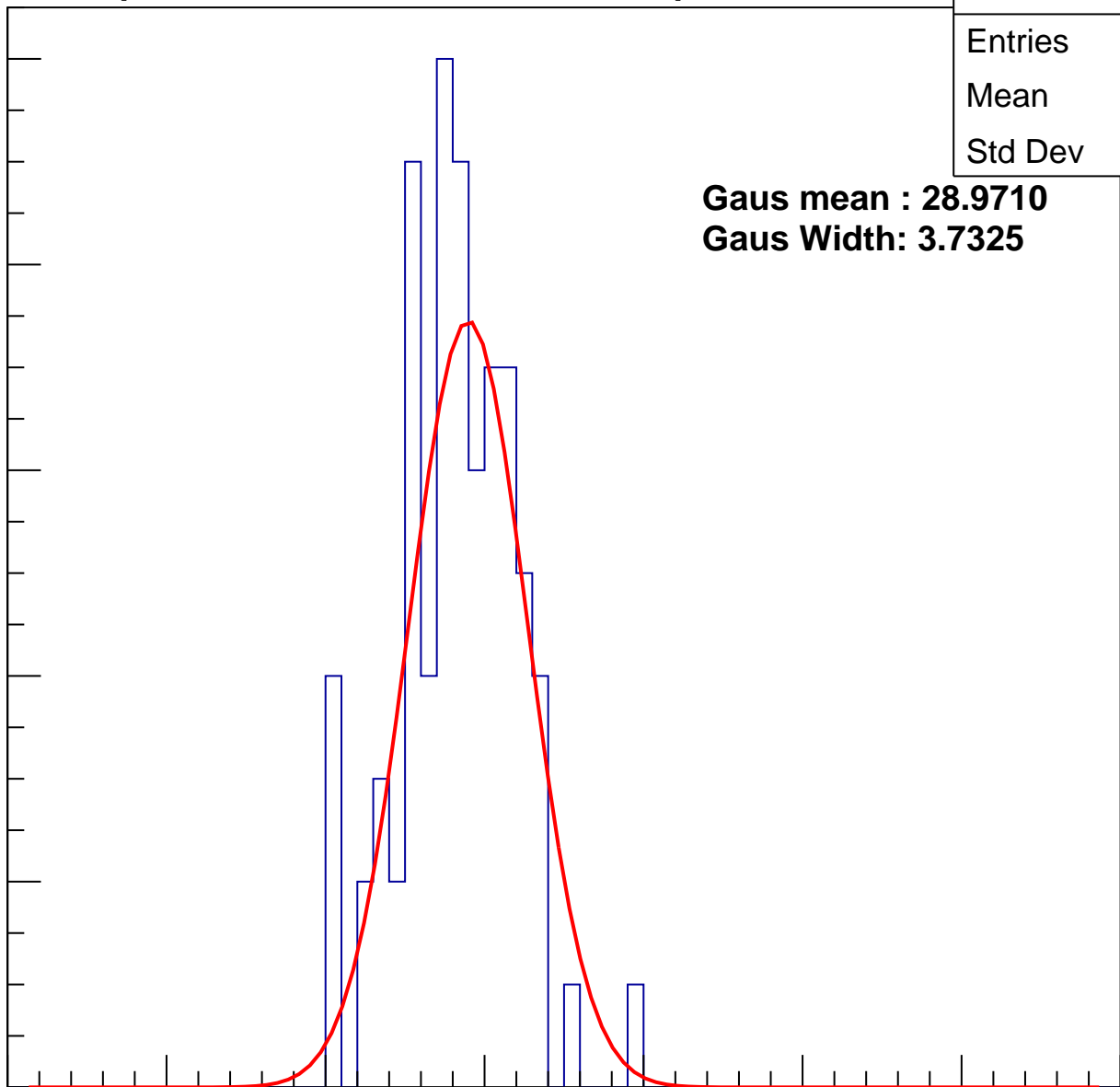
**Gaus Width: 3.7325**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

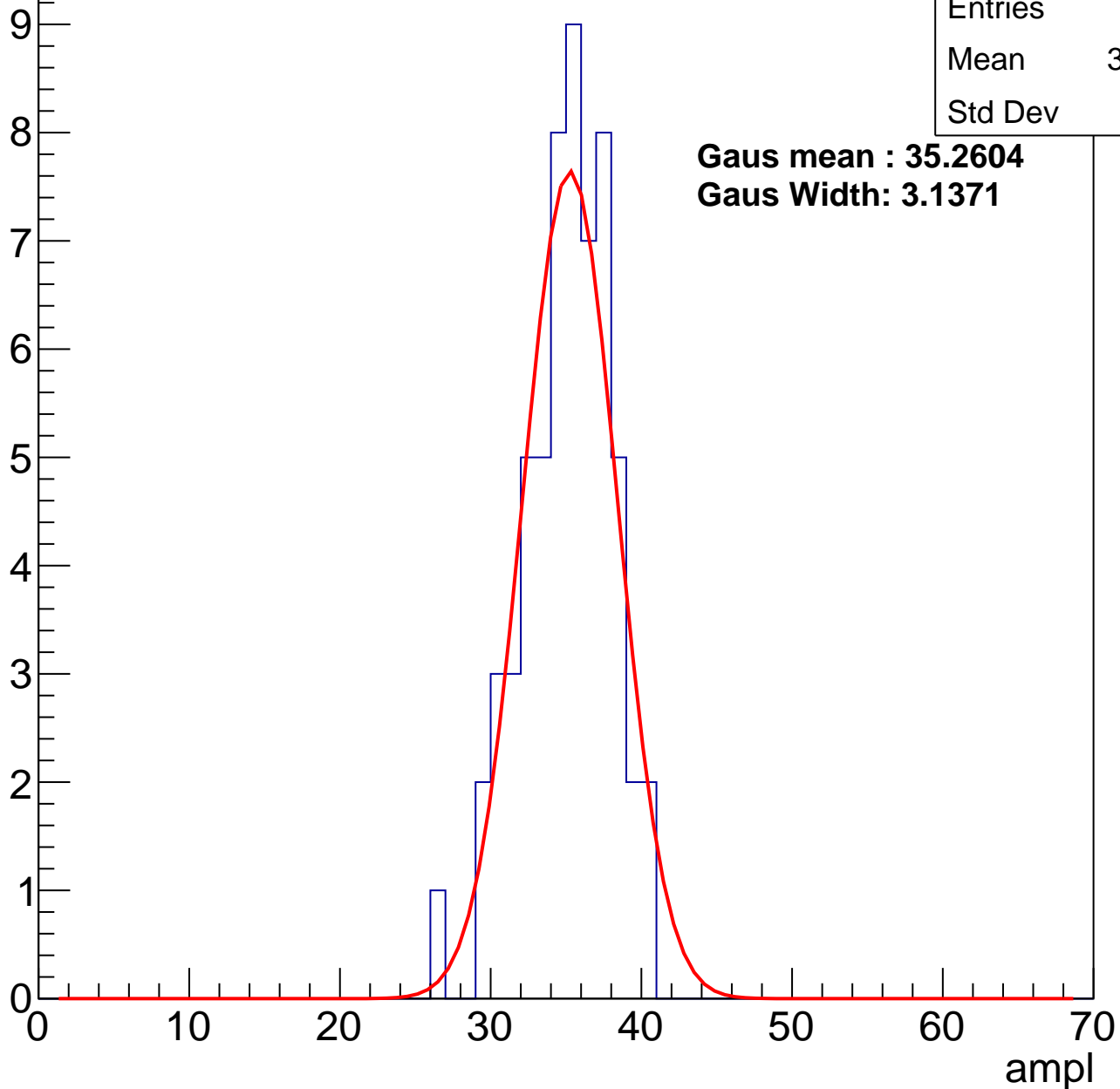
ampl



# B1L000S, U1-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch25, adc2

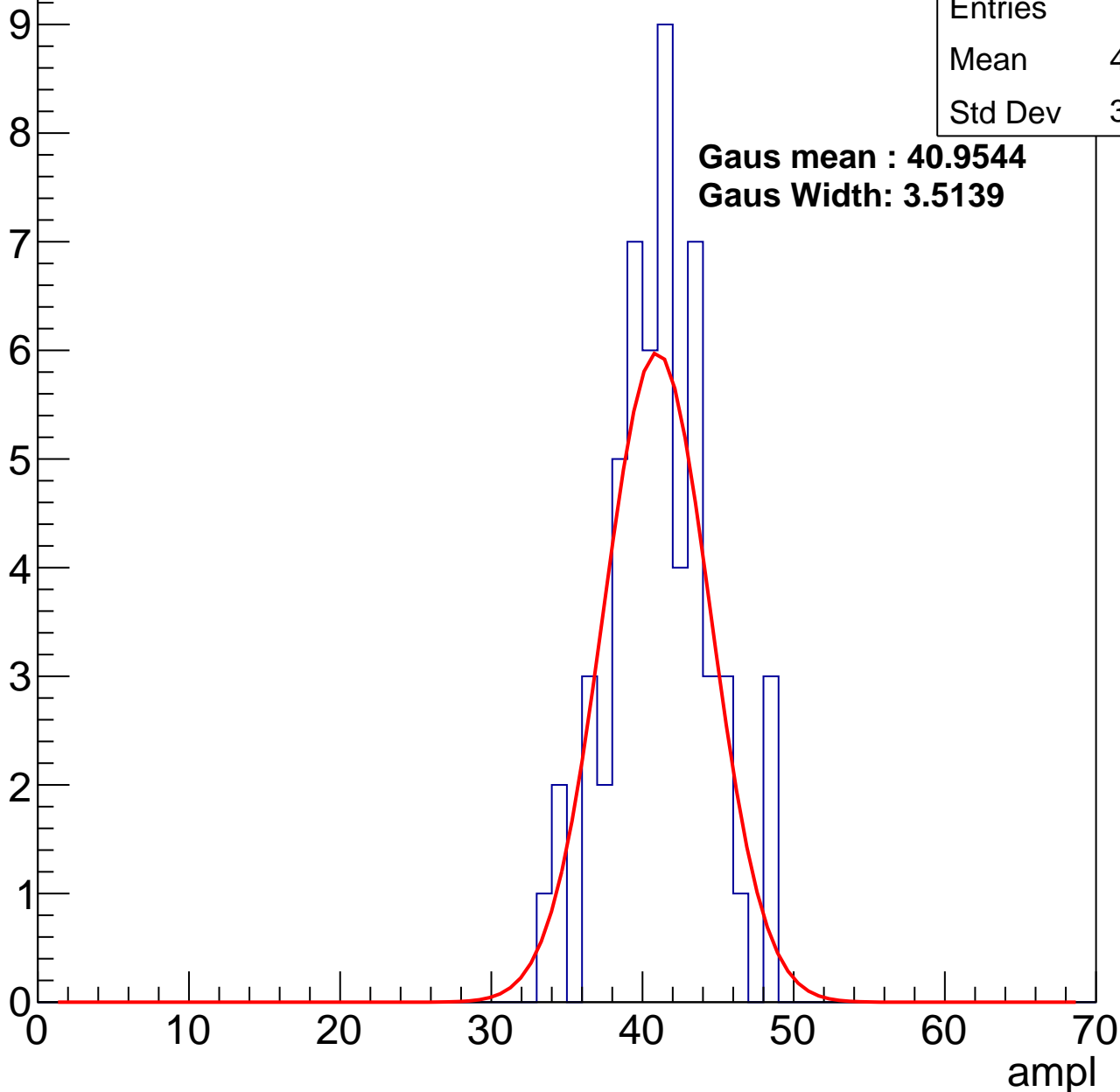
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	40.73
Std Dev	3.335

**Gaus mean : 40.9544**

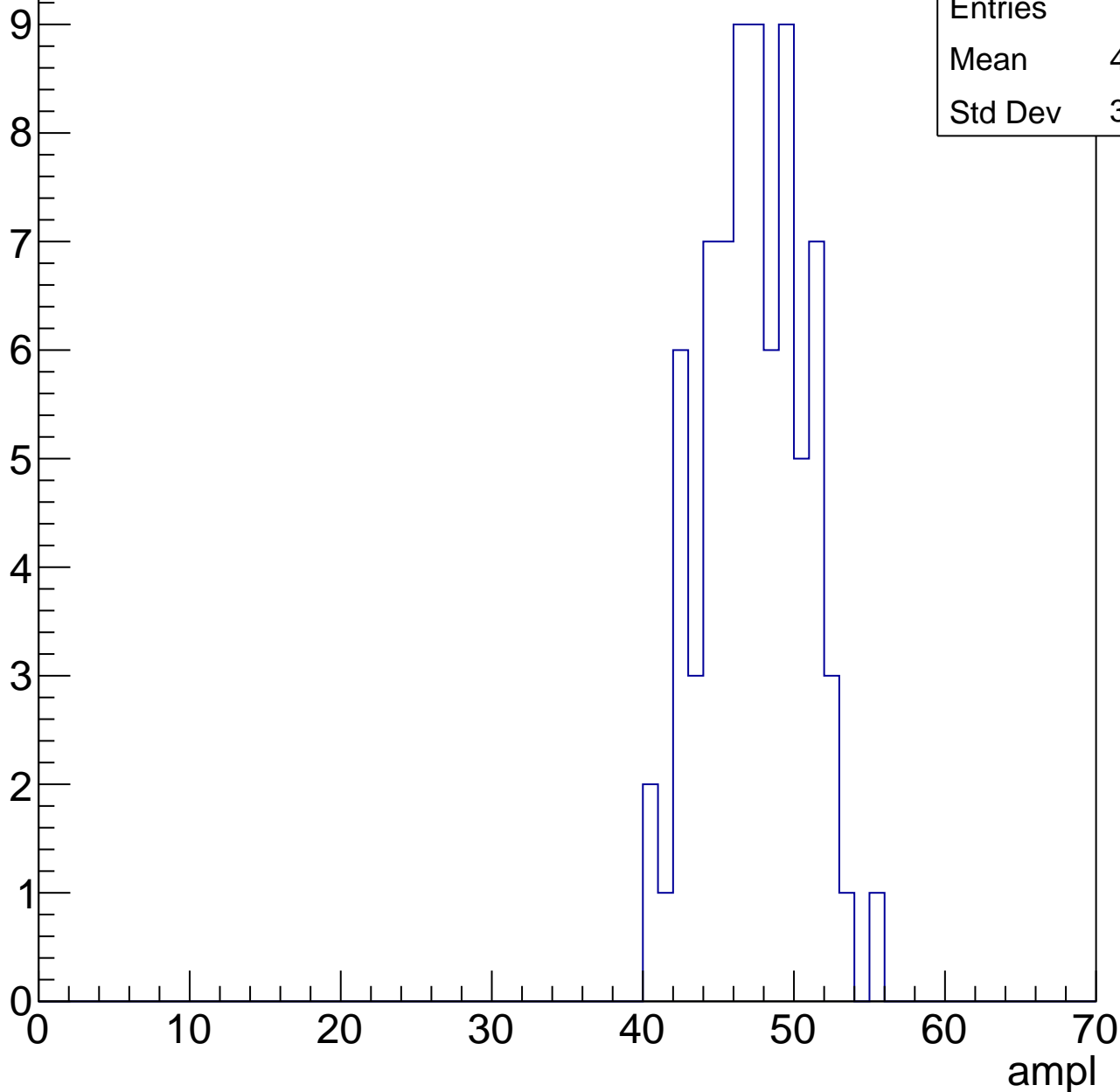
**Gaus Width: 3.5139**



# B1L000S, U1-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

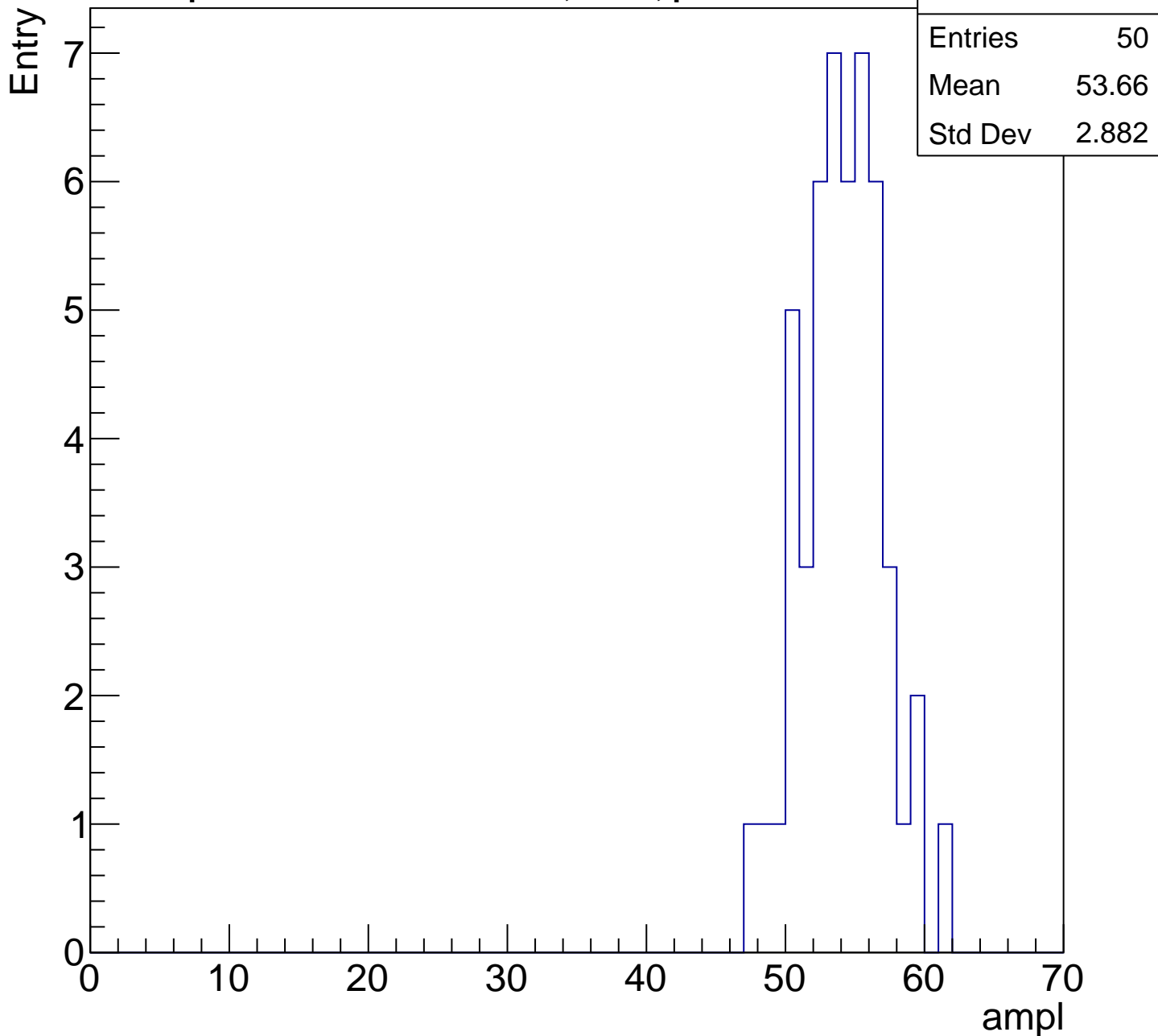
Entry



Entries	76
Mean	46.87
Std Dev	3.254

# B1L000S, U1-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

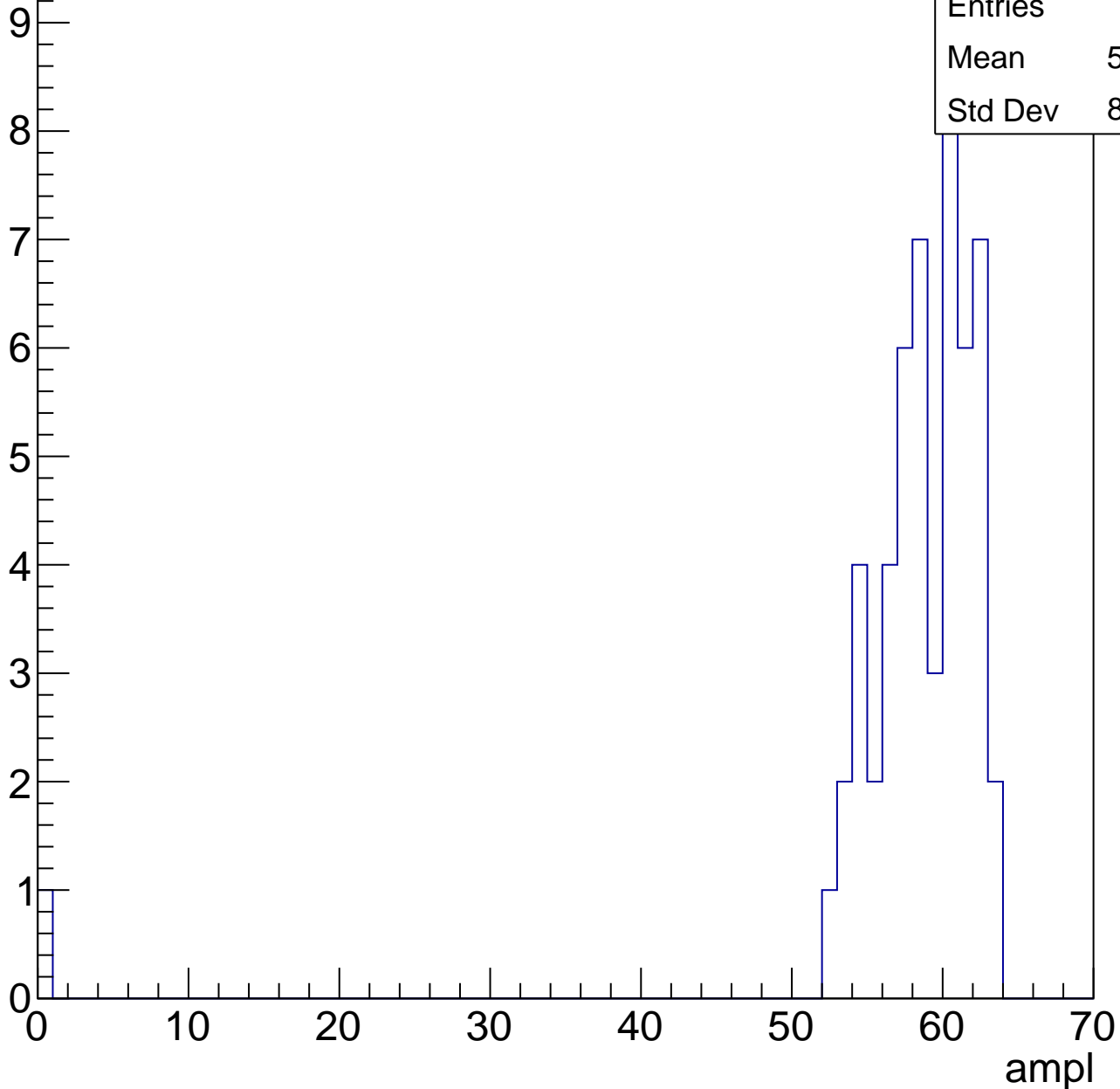


# B1L000S, U1-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	57.39
Std Dev	8.376

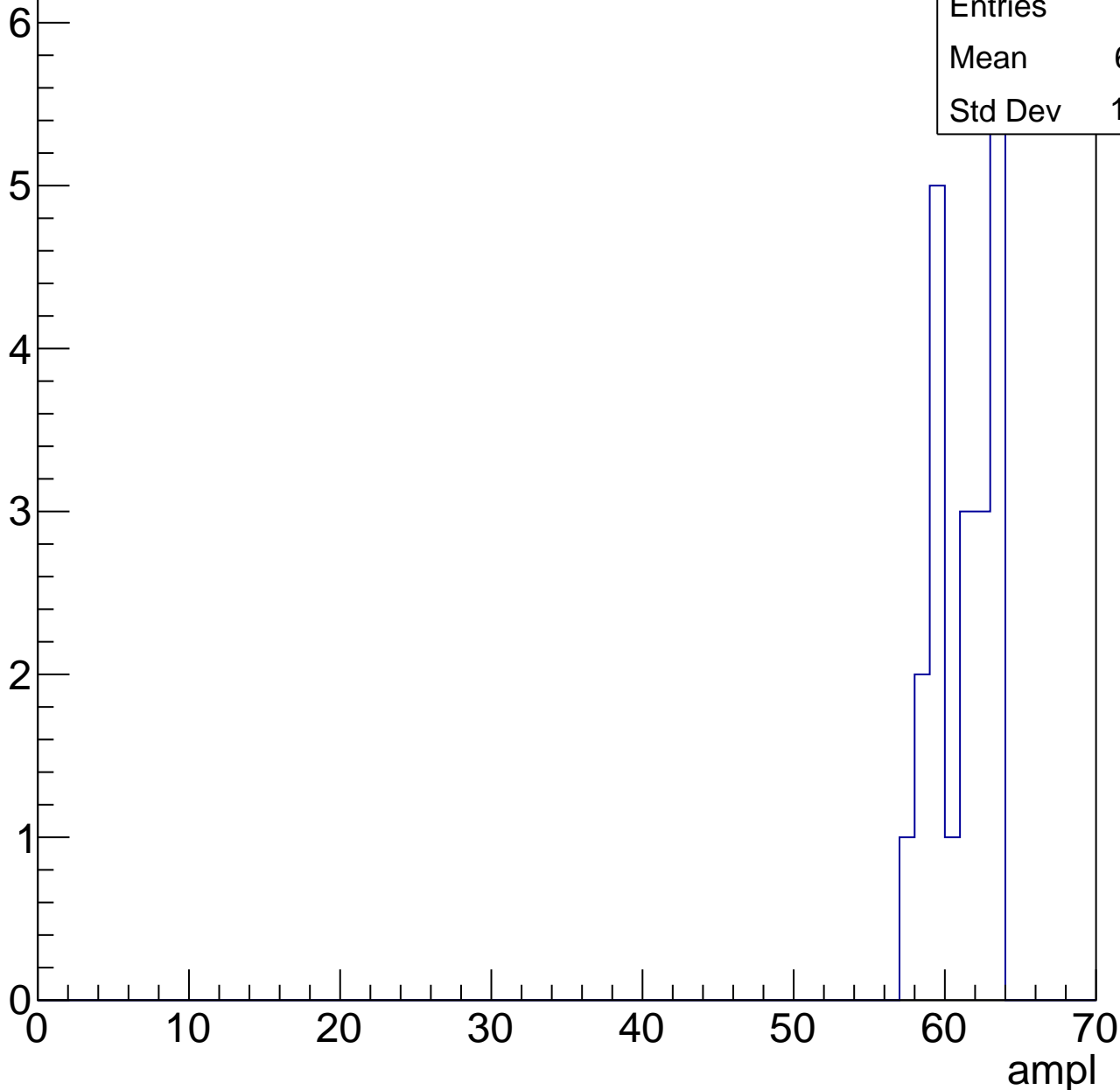


# B1L000S, U1-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	60.71
Std Dev	1.955





# B1L000S, U1-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch26, adc0

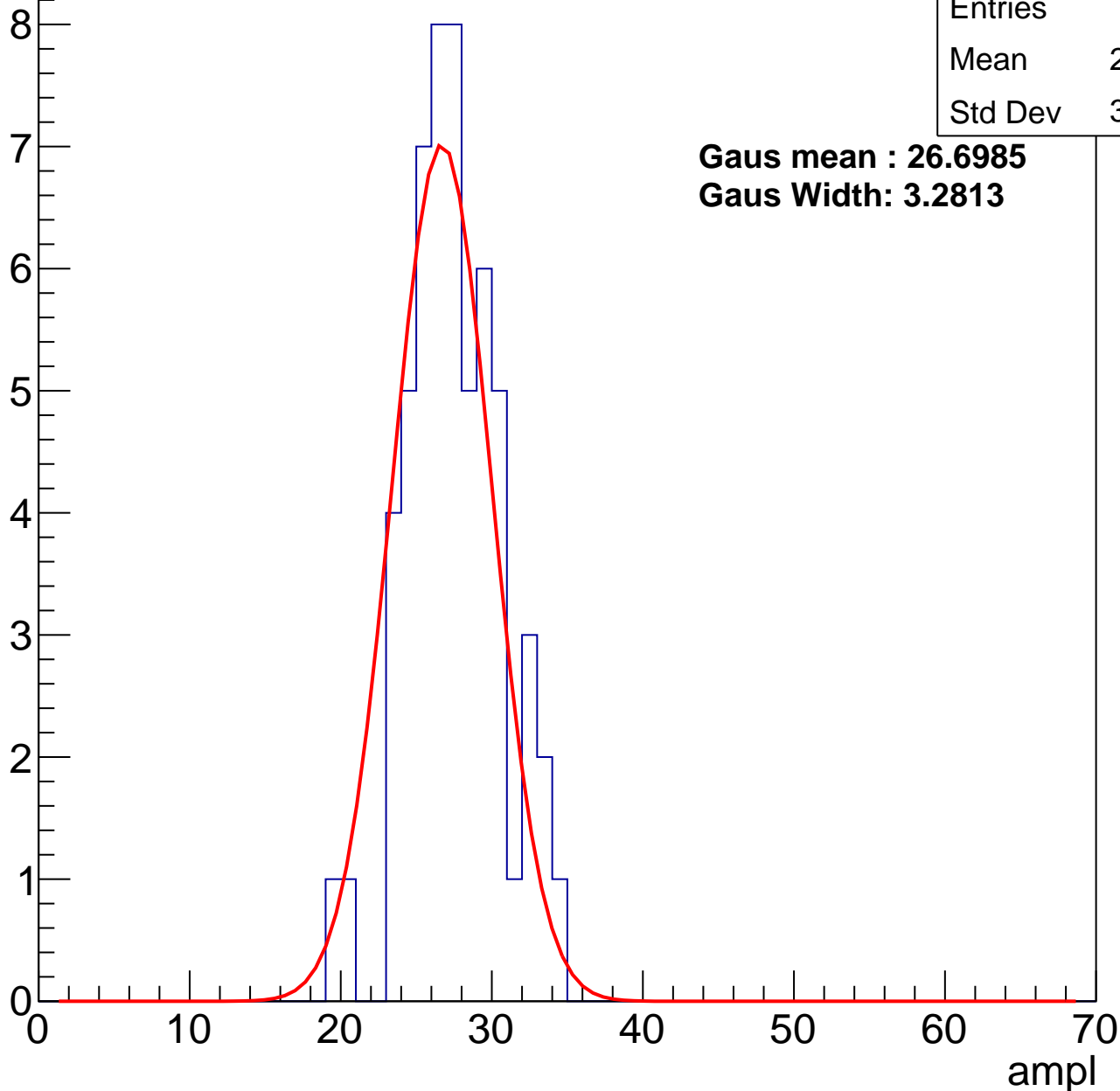
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	27.04
Std Dev	3.089

**Gaus mean : 26.6985**

**Gaus Width: 3.2813**



# B1L000S, U1-ch26, adc1

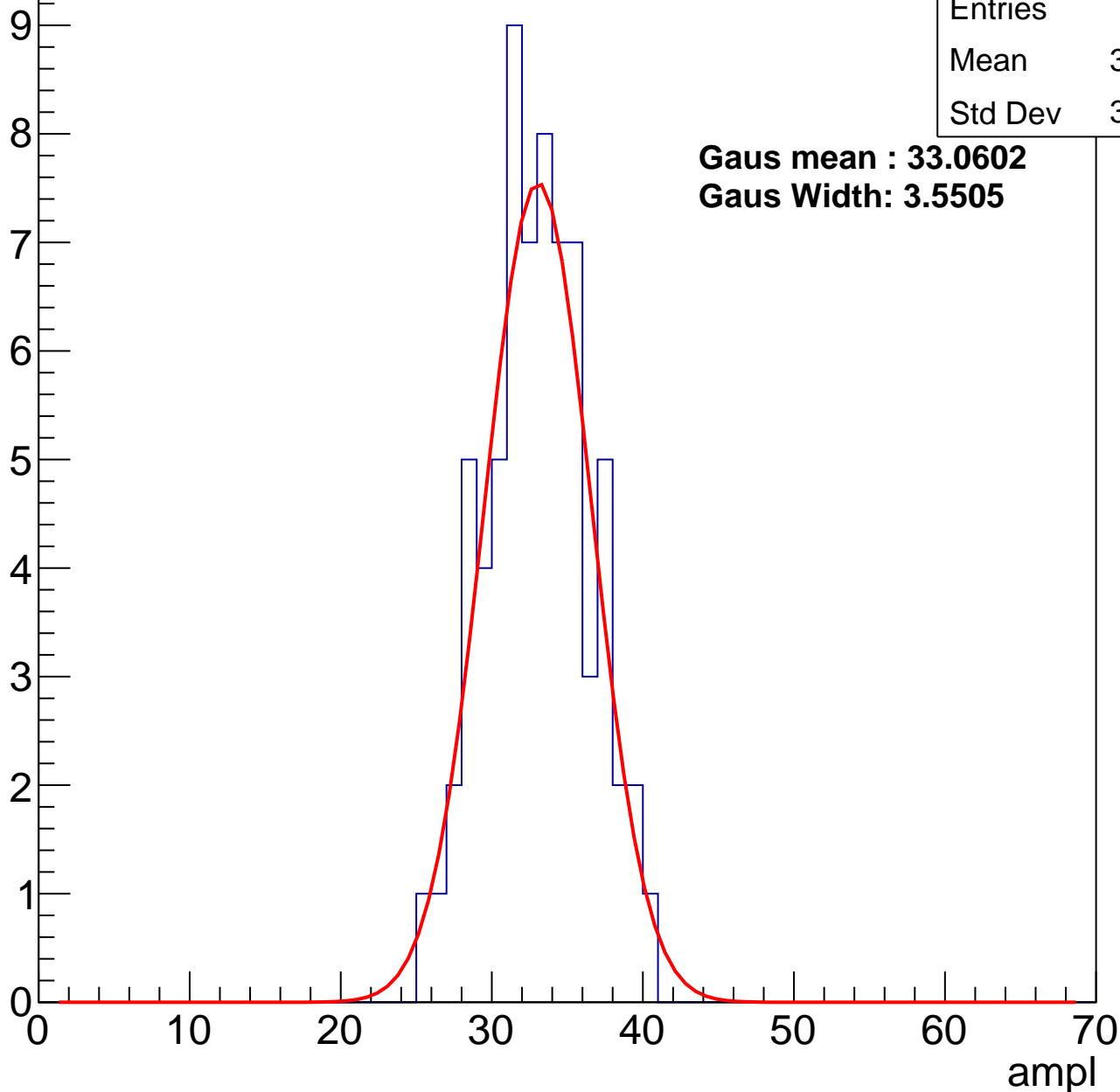
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	32.58
Std Dev	3.325

**Gaus mean : 33.0602**

**Gaus Width: 3.5505**



# B1L000S, U1-ch26, adc2

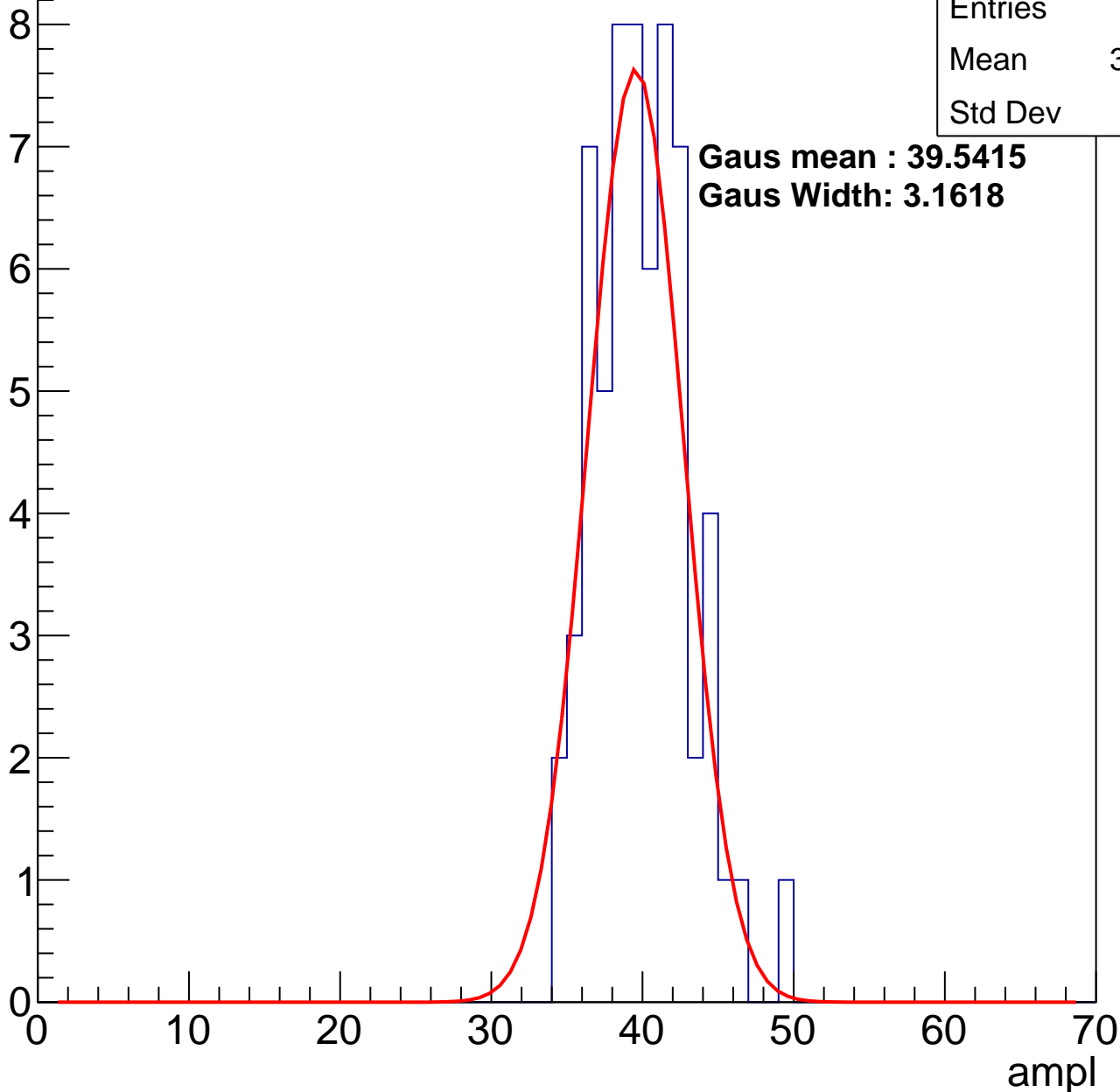
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	39.52
Std Dev	3.06

**Gaus mean : 39.5415**

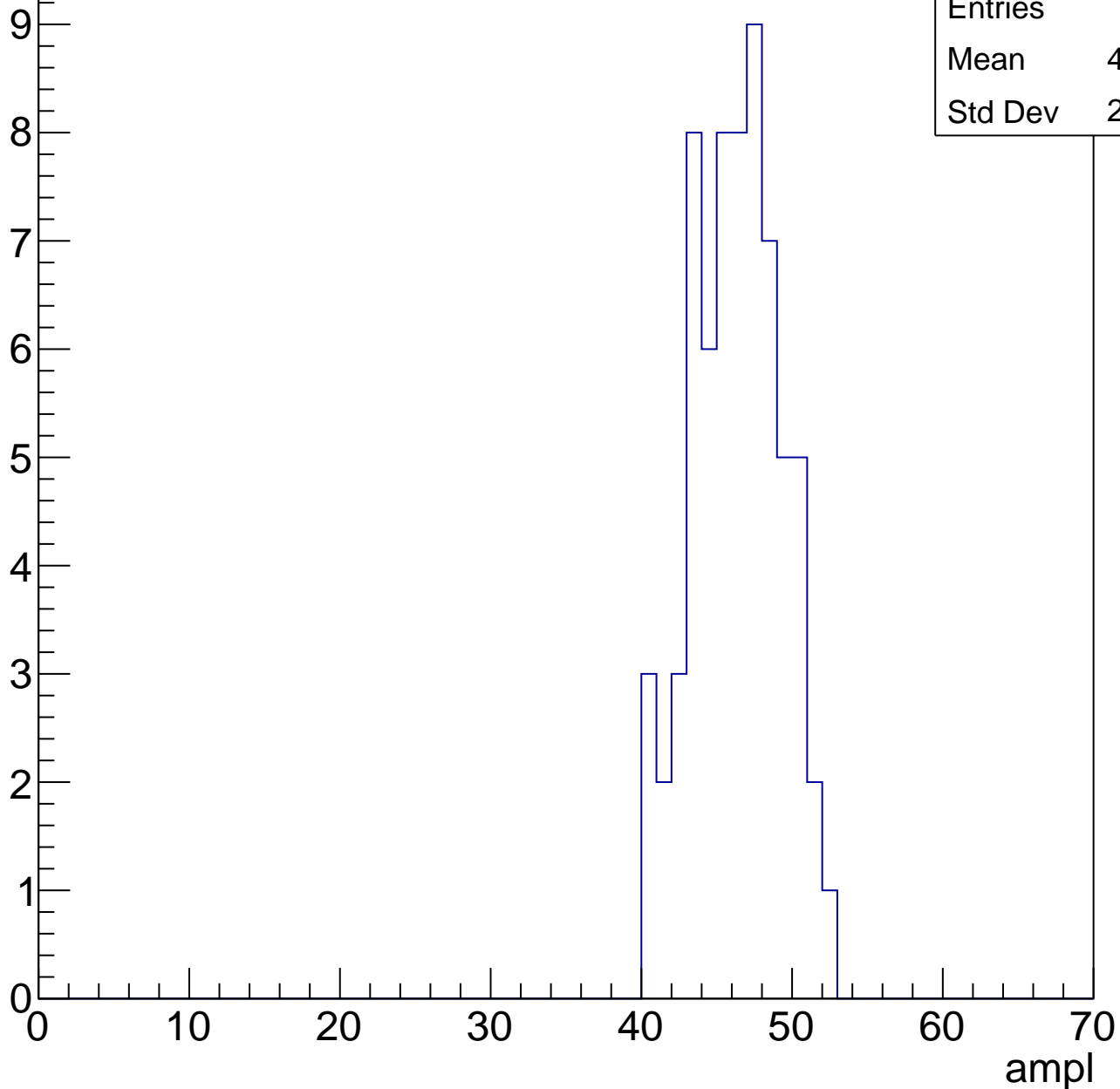
**Gaus Width: 3.1618**



# B1L000S, U1-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



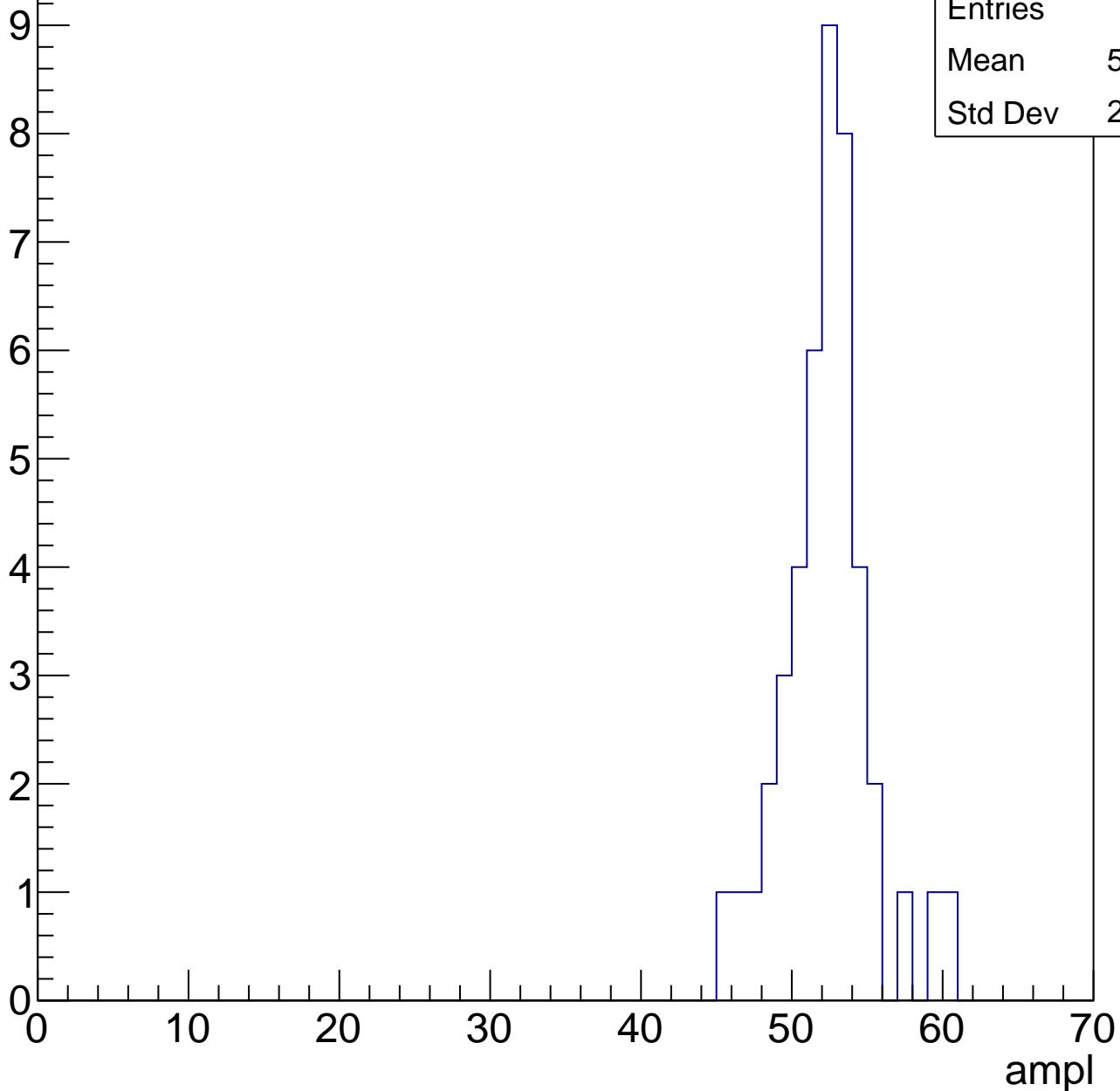
Entries	67
Mean	45.85
Std Dev	2.882

# B1L000S, U1-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	51.84
Std Dev	2.899

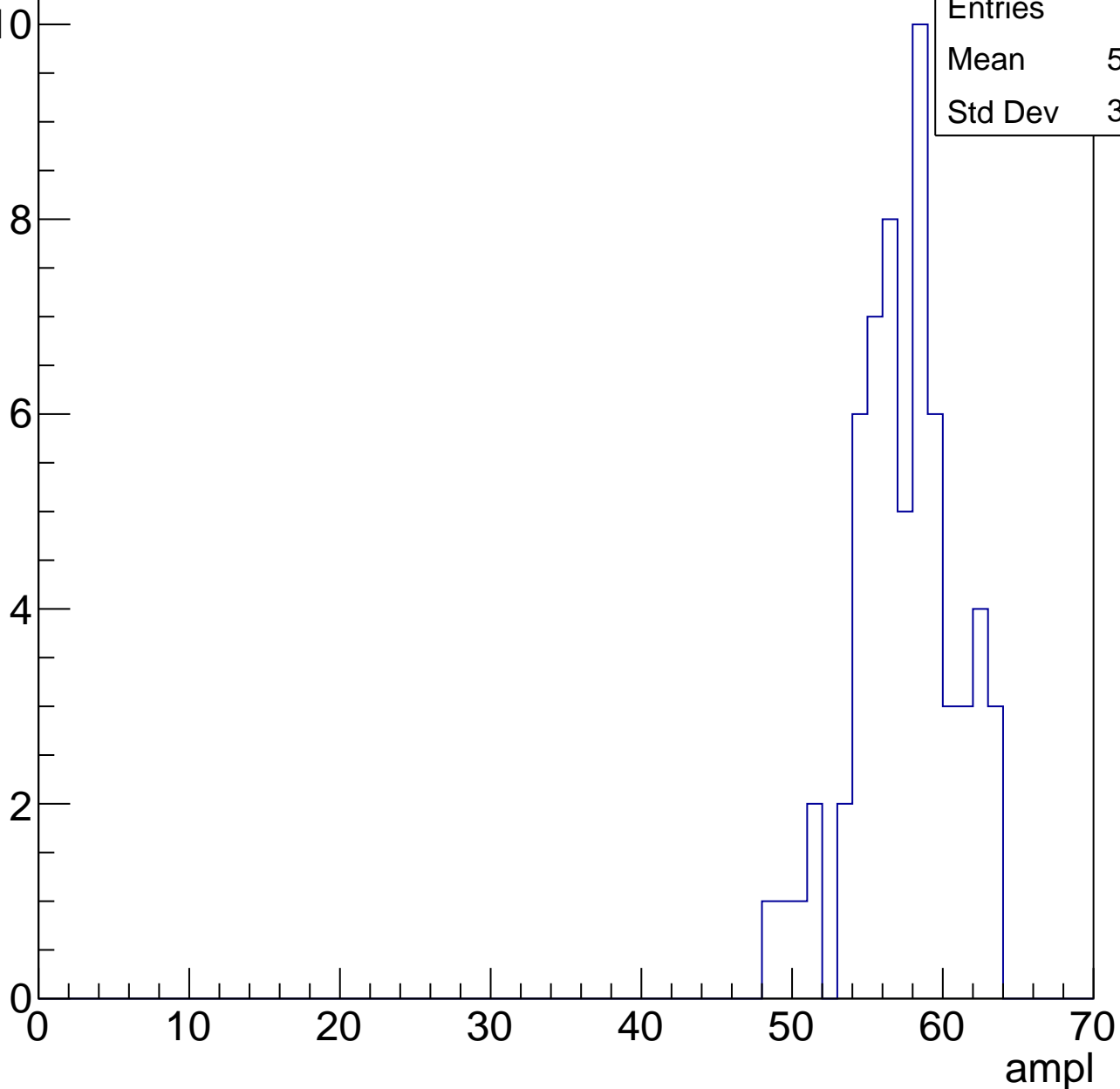


# B1L000S, U1-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	56.95
Std Dev	3.372

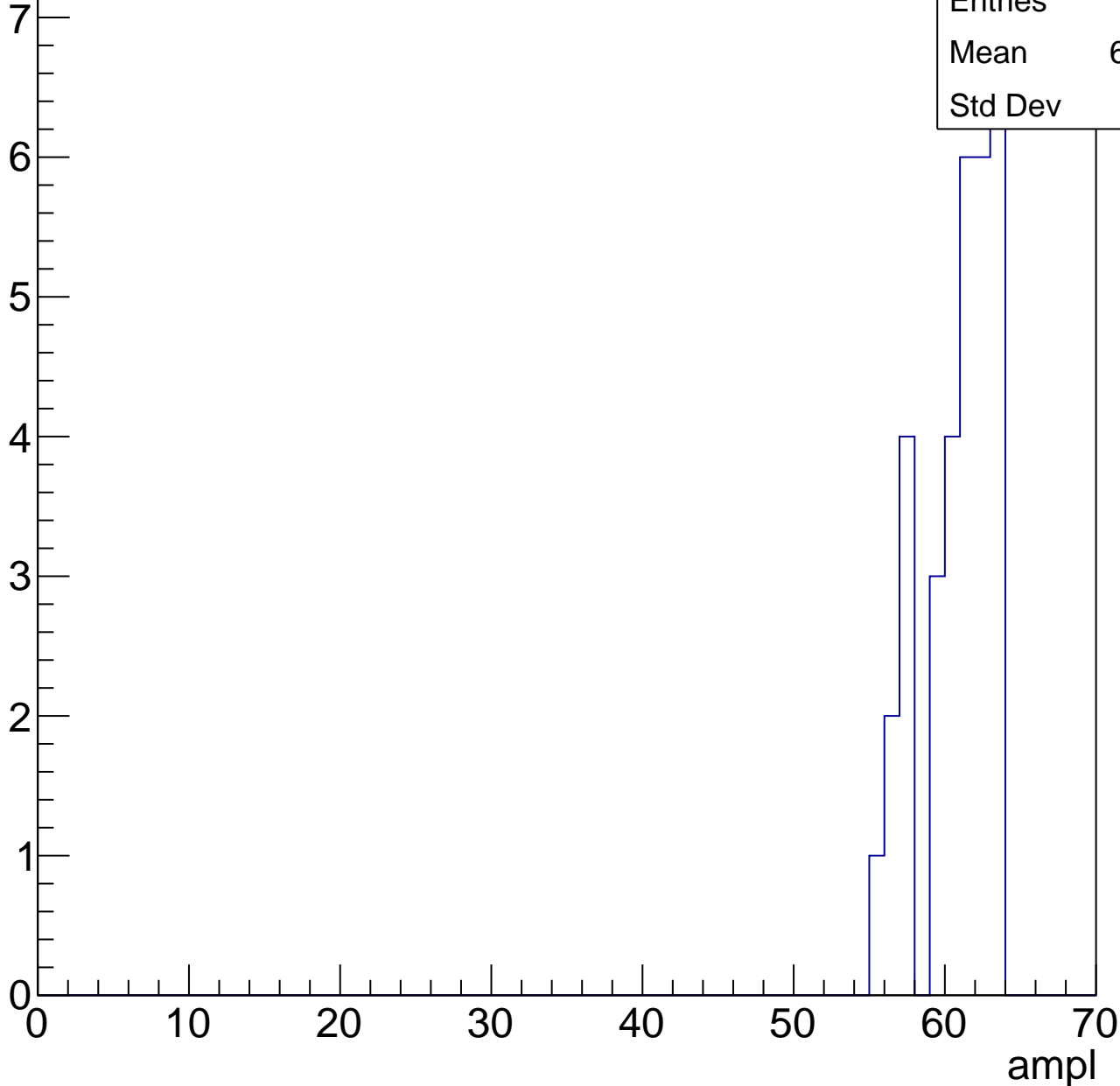


# B1L000S, U1-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	33
Mean	60.33
Std Dev	2.37

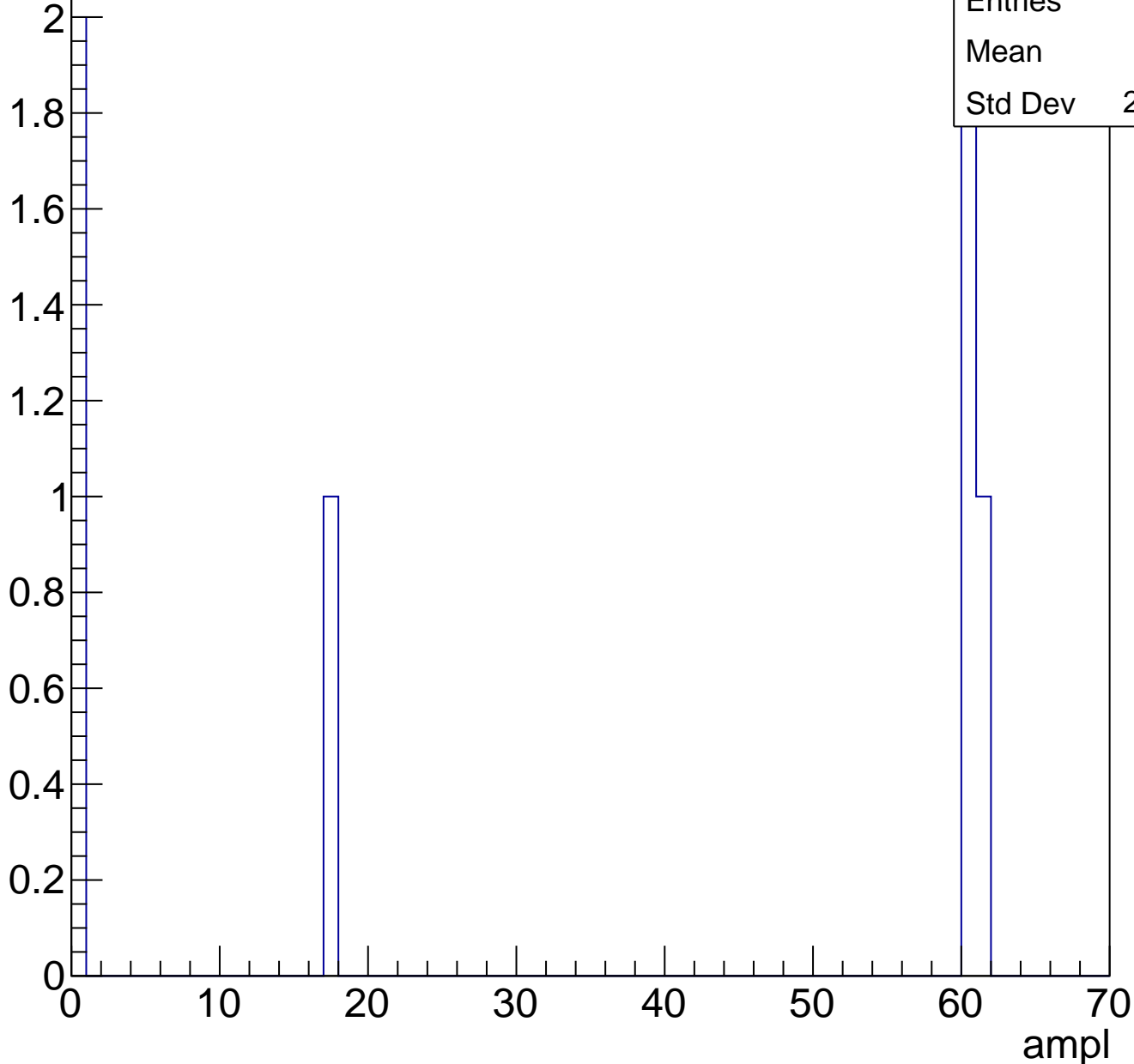




# B1L000S, U1-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	6
Mean	33
Std Dev	27.92

# B1L000S, U1-ch27, adc0

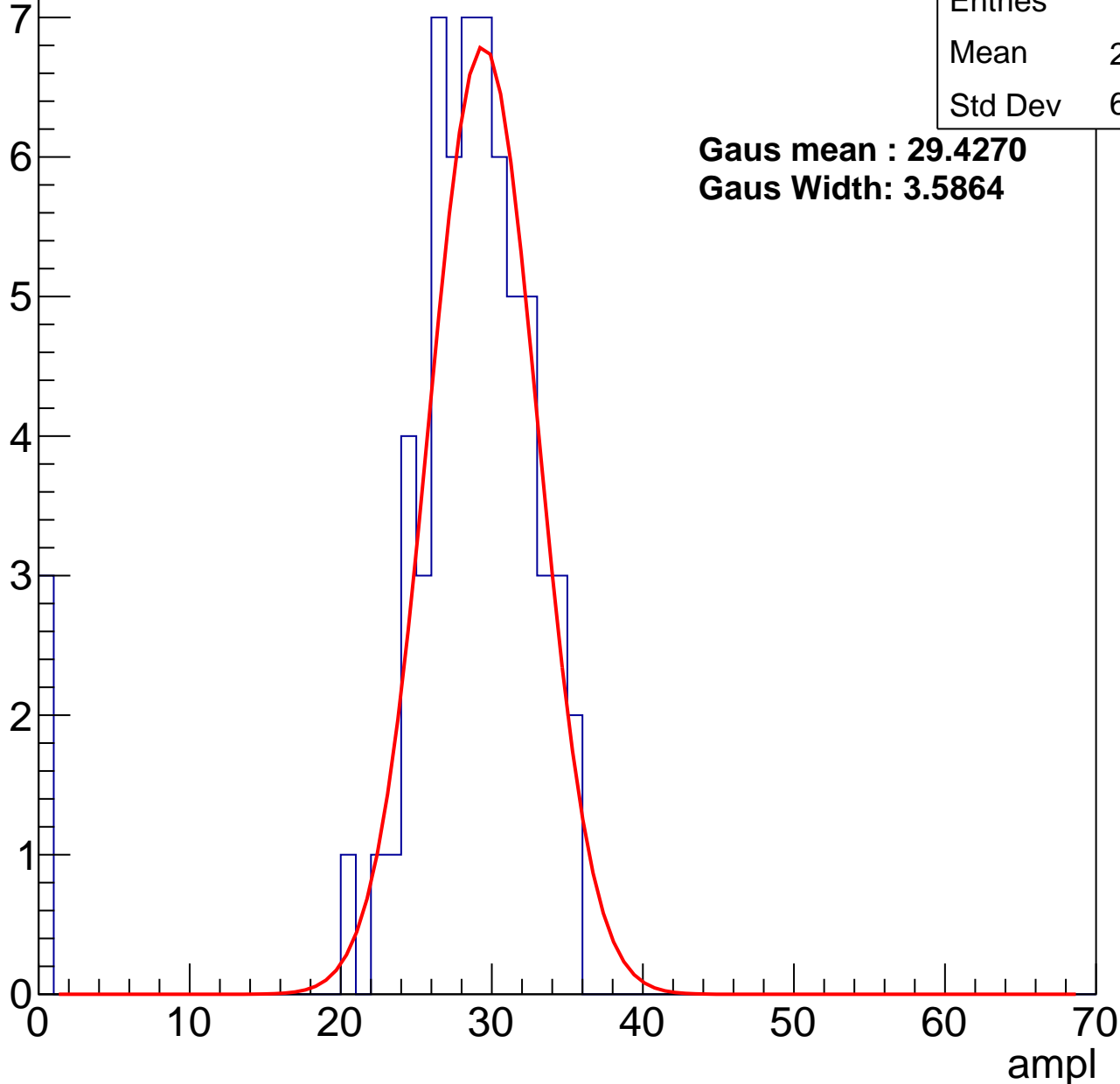
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	27.27
Std Dev	6.852

**Gaus mean : 29.4270**

**Gaus Width: 3.5864**



# B1L000S, U1-ch27, adc1

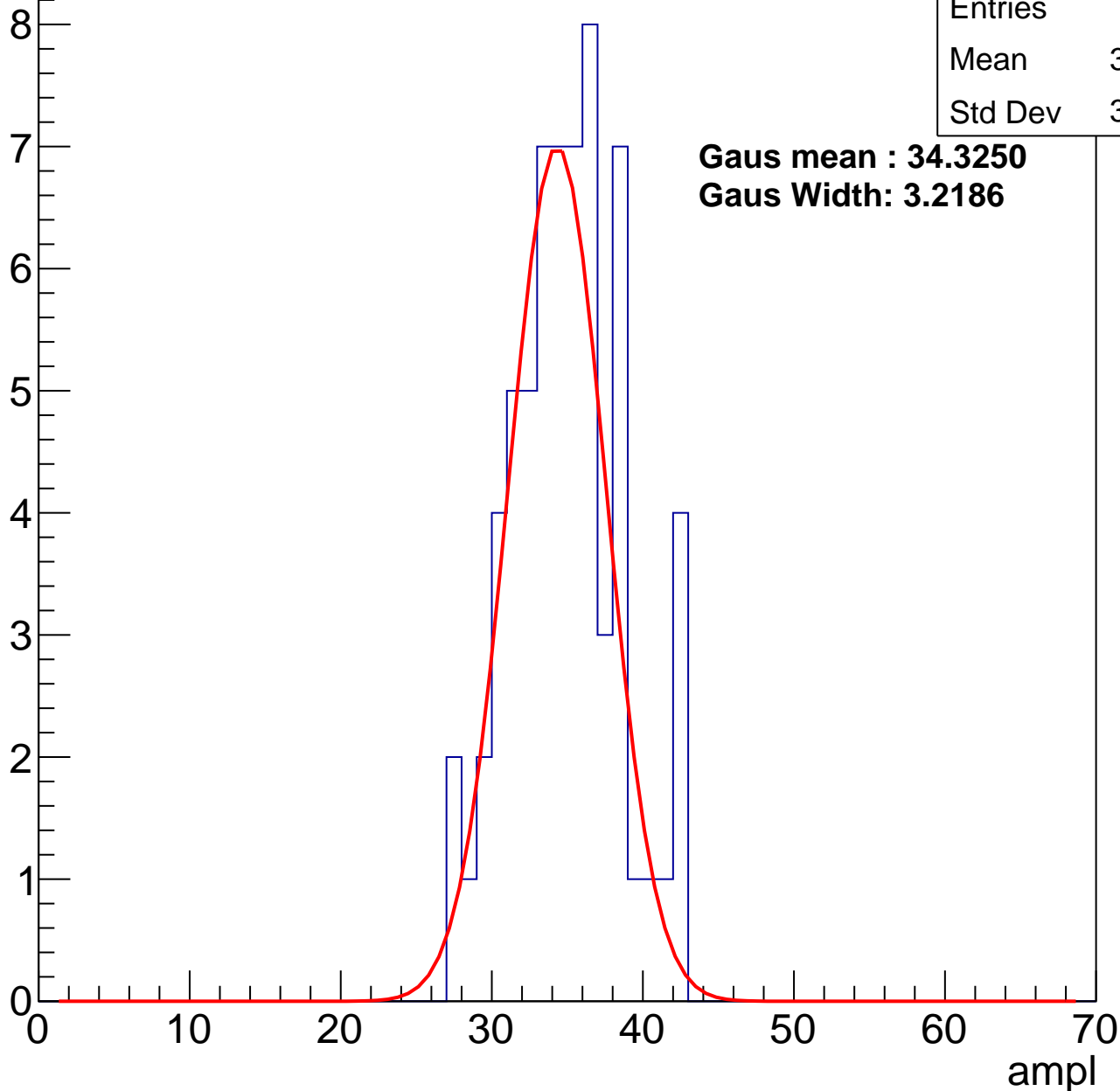
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	34.49
Std Dev	3.612

**Gaus mean : 34.3250**

**Gaus Width: 3.2186**



# B1L000S, U1-ch27, adc2

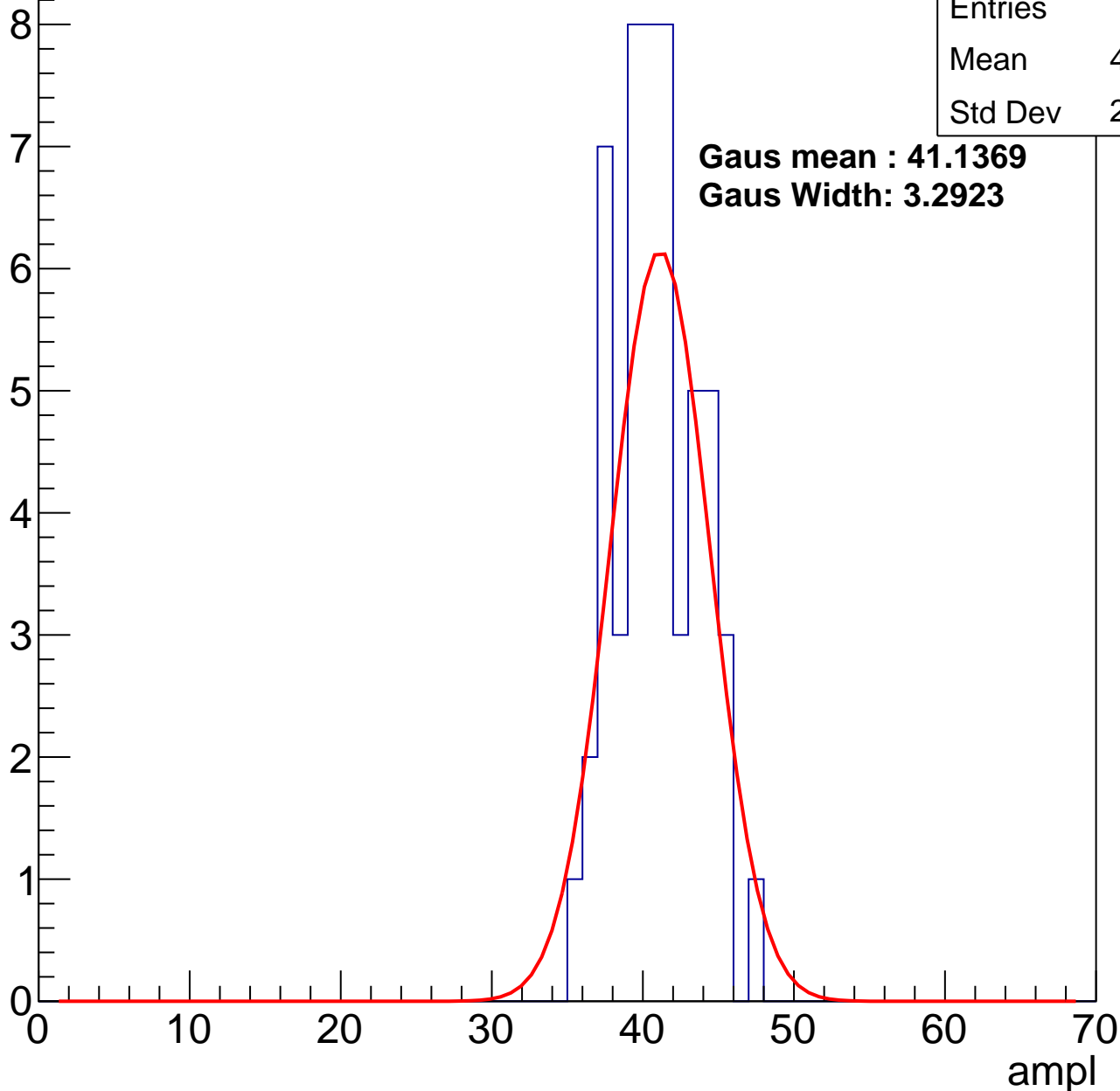
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	40.43
Std Dev	2.719

**Gaus mean : 41.1369**

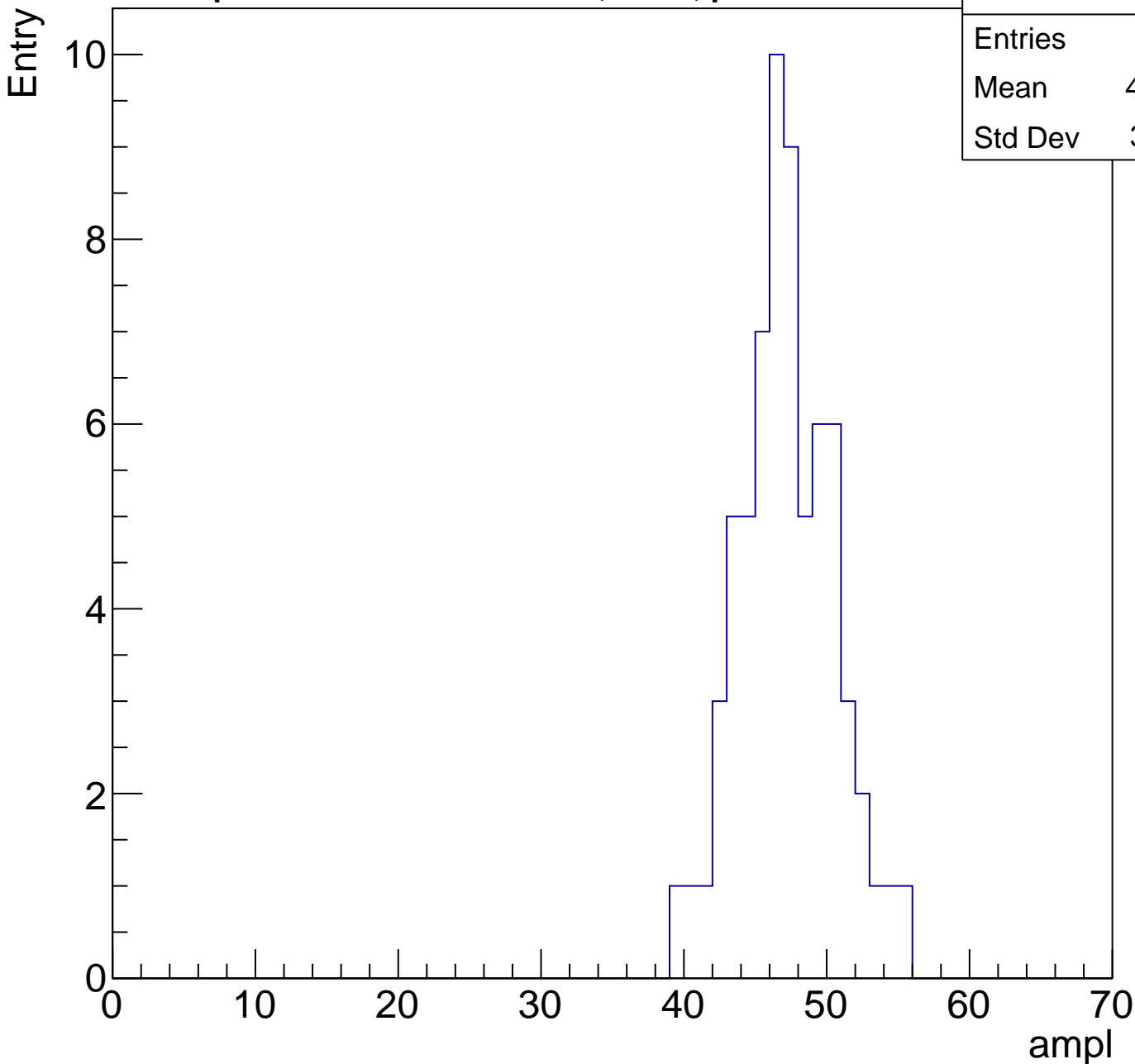
**Gaus Width: 3.2923**



# B1L000S, U1-ch27, adc3

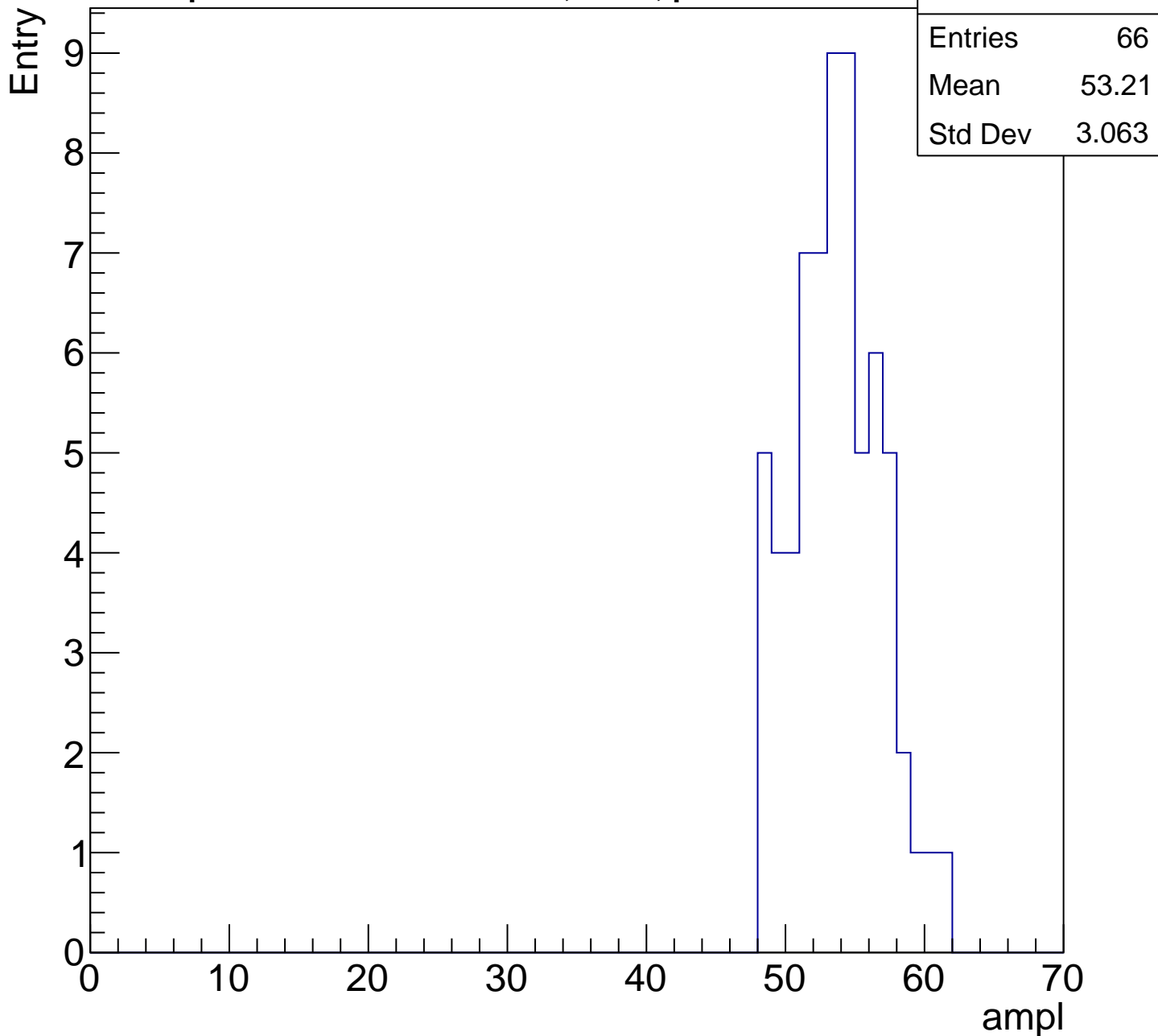
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	46.75
Std Dev	3.261



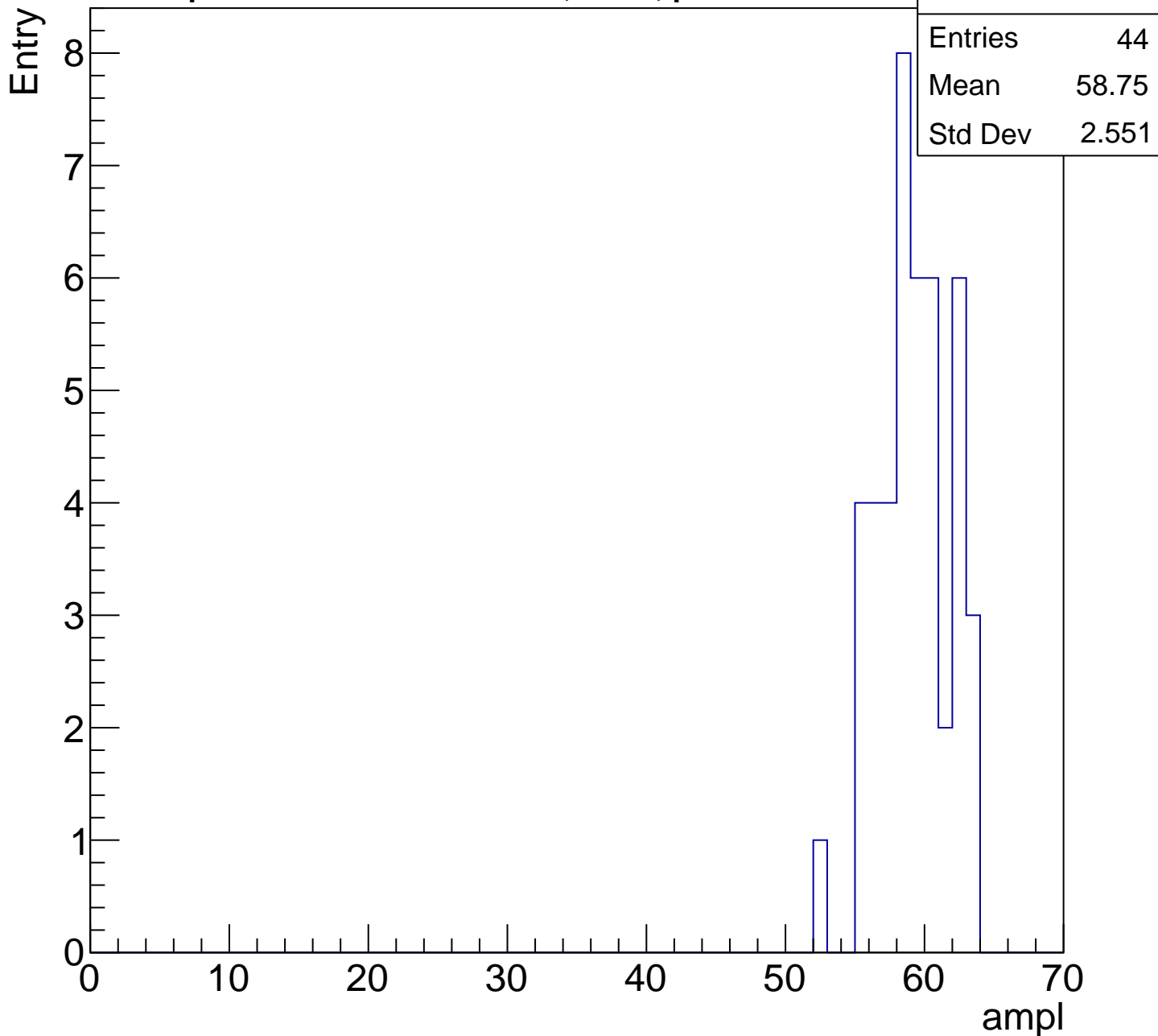
# B1L000S, U1-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

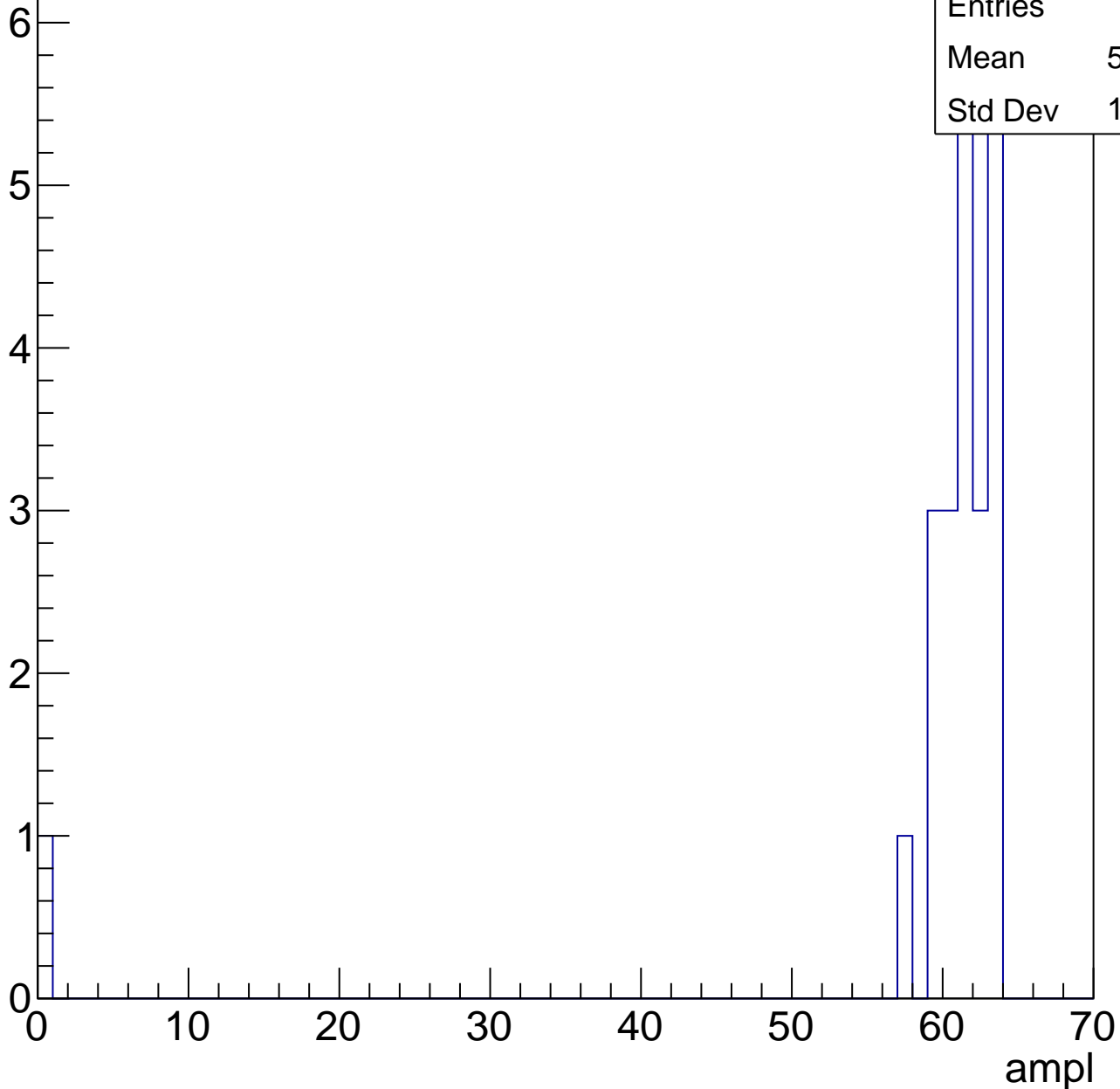


# B1L000S, U1-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	58.43
Std Dev	12.56

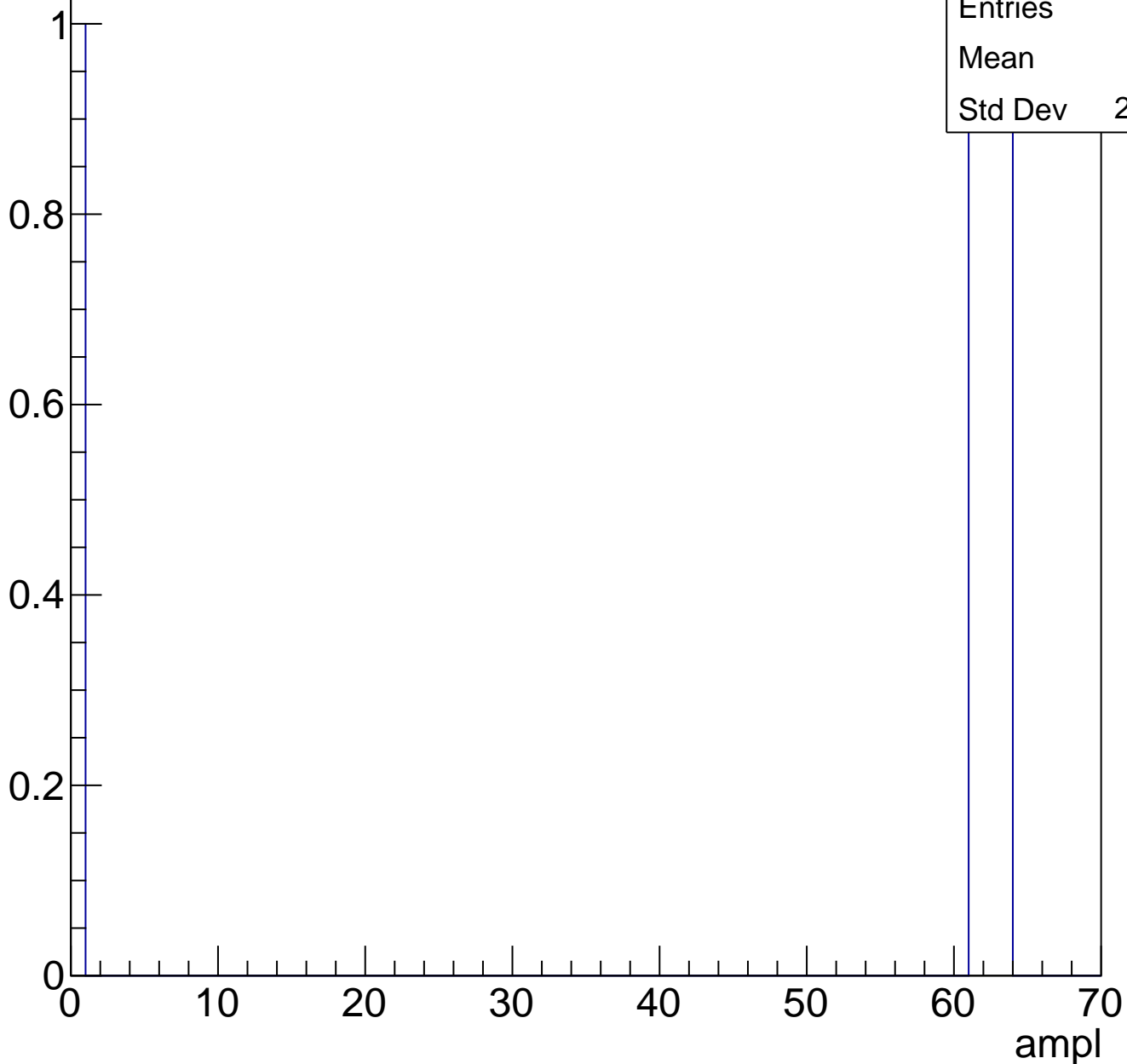




# B1L000S, U1-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch28, adc0

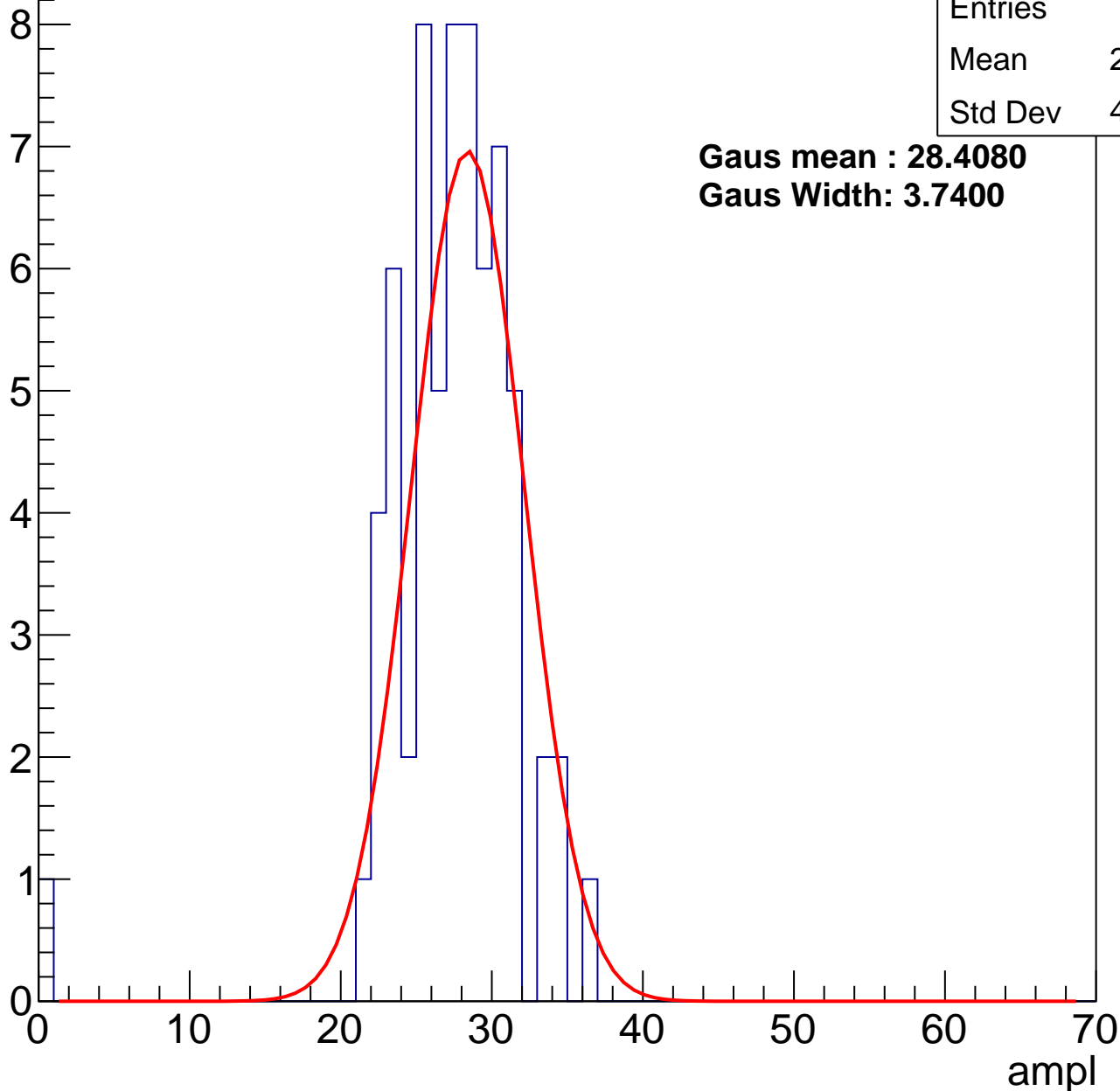
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	26.88
Std Dev	4.673

**Gaus mean : 28.4080**

**Gaus Width: 3.7400**



# B1L000S, U1-ch28, adc1

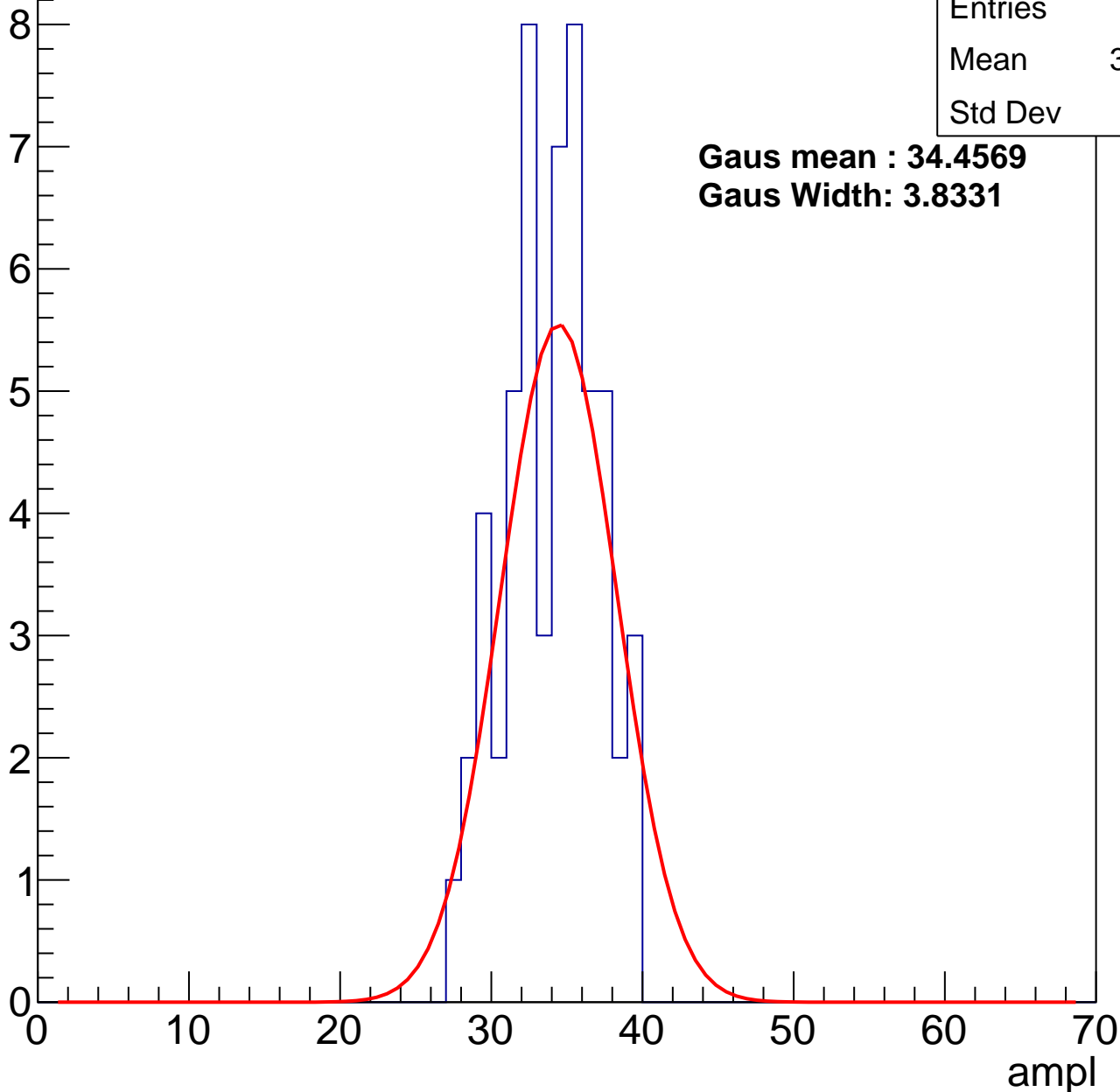
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	33.55
Std Dev	3.02

**Gaus mean : 34.4569**

**Gaus Width: 3.8331**



# B1L000S, U1-ch28, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	39.24
Std Dev	3.565

**Gaus mean : 40.4239**

**Gaus Width: 3.2848**

10

8

6

4

2

0

0

10

20

30

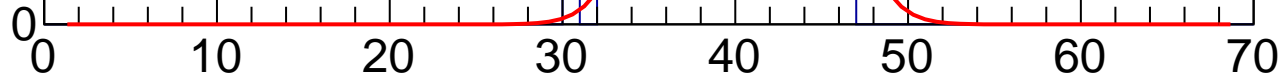
40

50

60

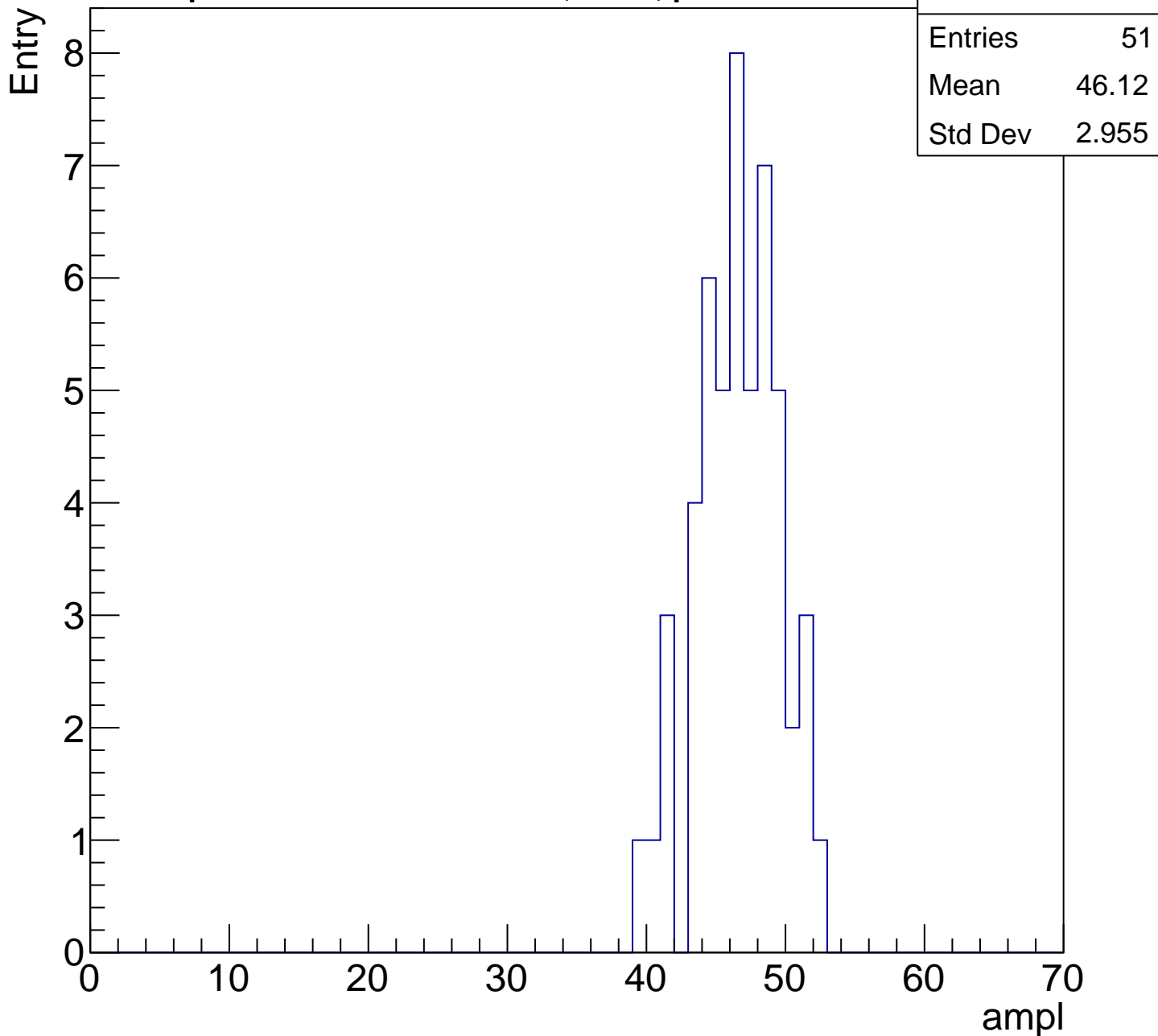
70

ampl



# B1L000S, U1-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

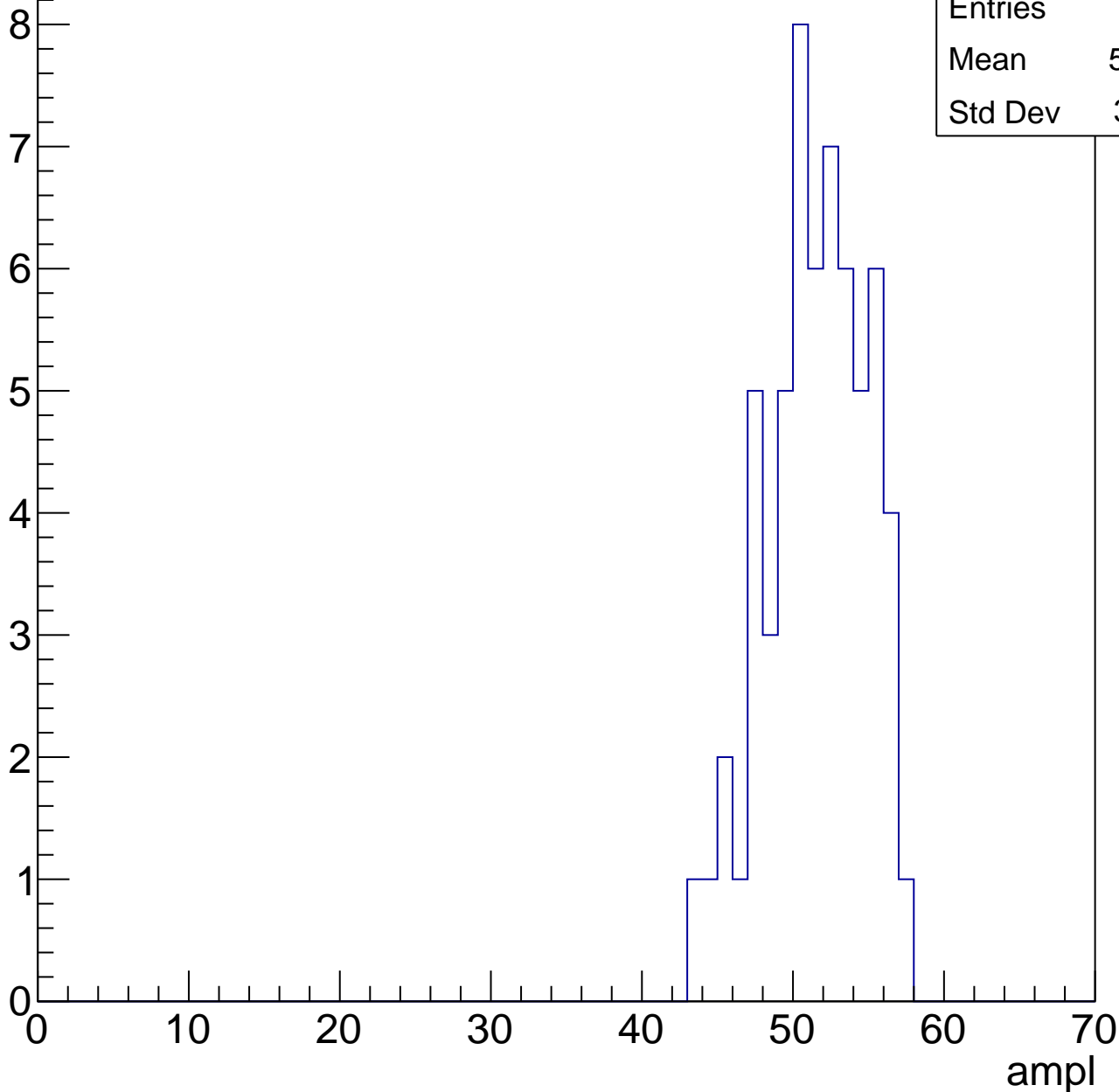


# B1L000S, U1-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

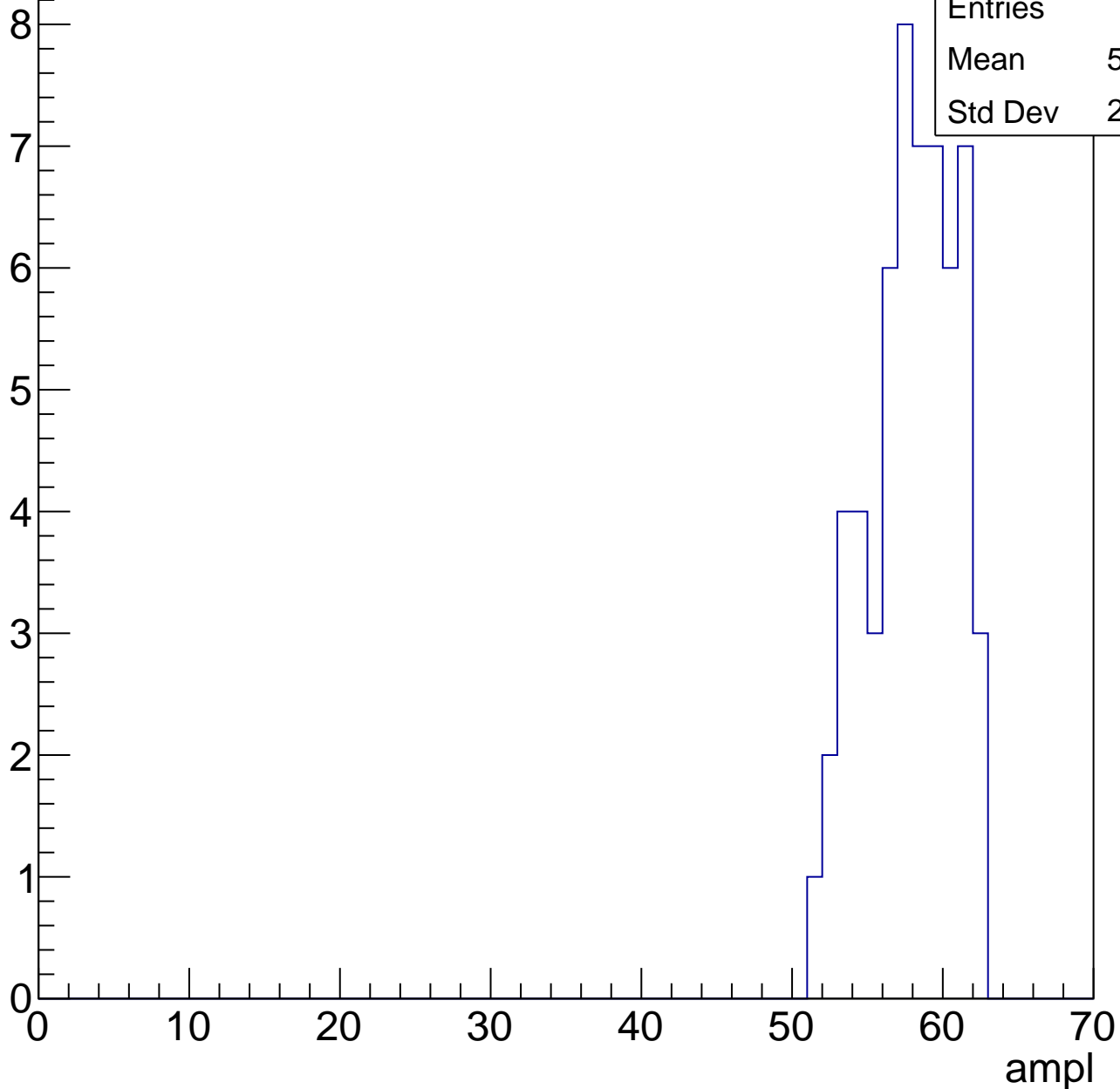
Entries	61
Mean	51.08
Std Dev	3.271



# B1L000S, U1-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

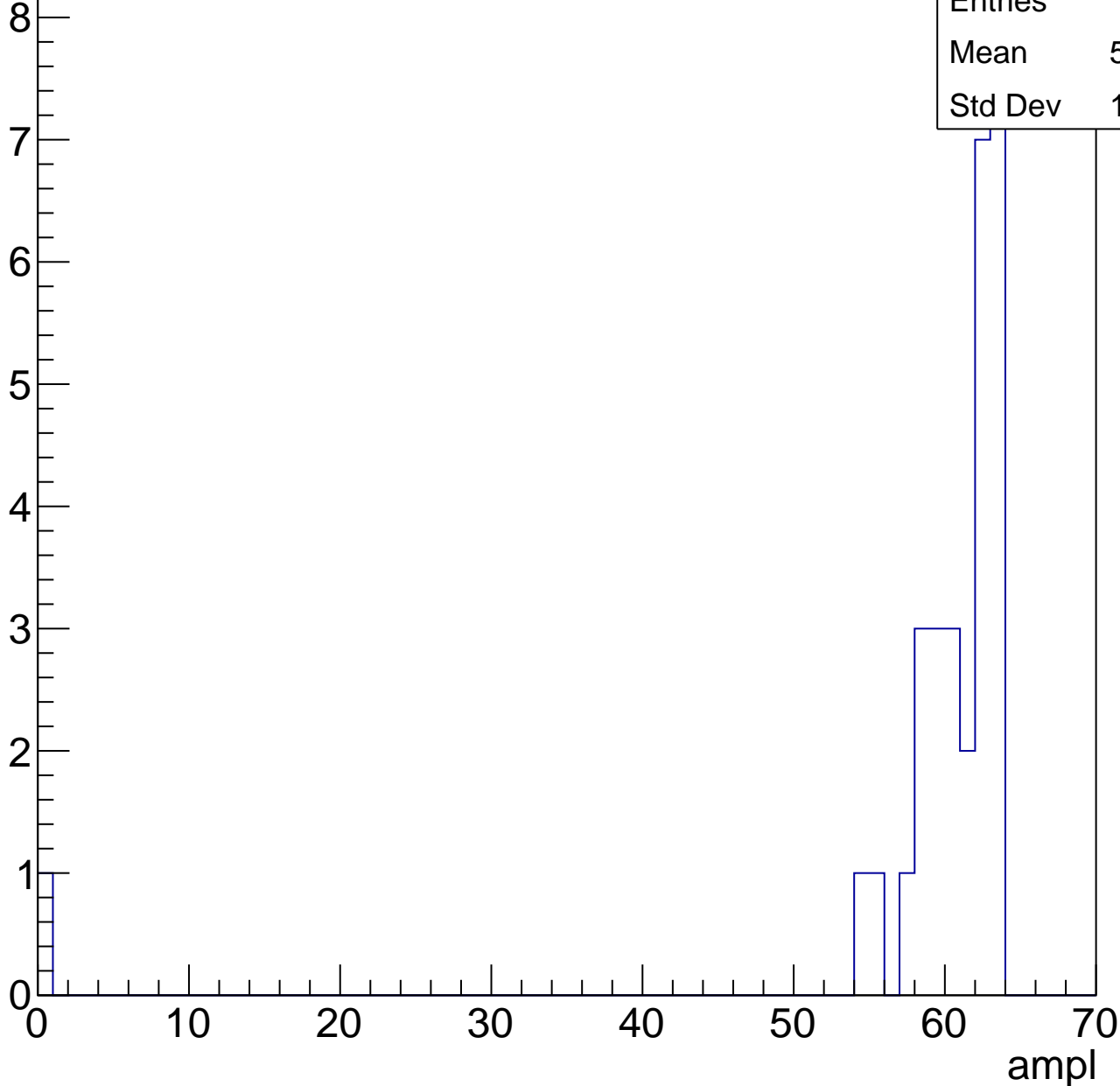


# B1L000S, U1-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	58.57
Std Dev	11.14

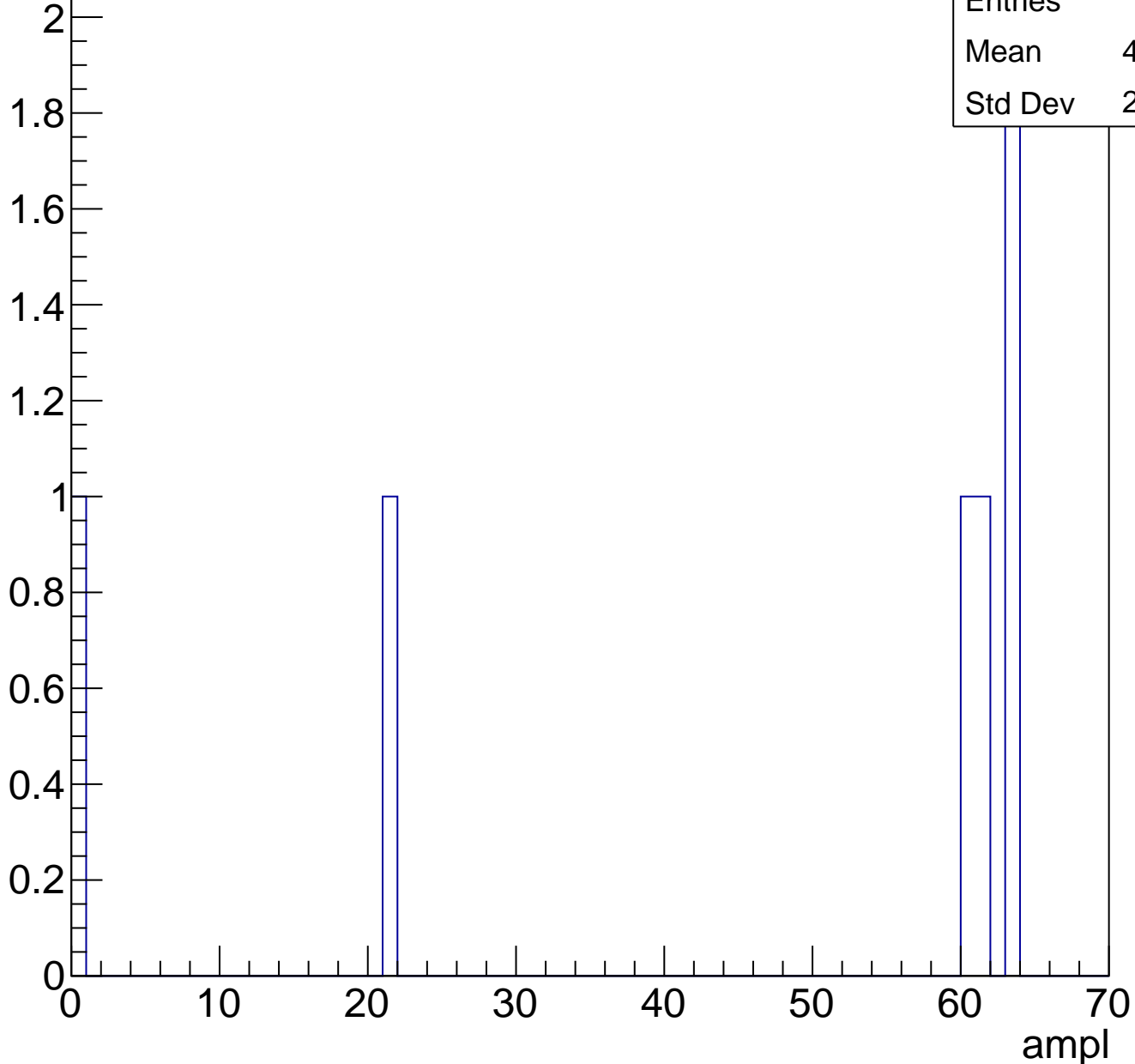




# B1L000S, U1-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	6
Mean	44.67
Std Dev	24.93

# B1L000S, U1-ch29, adc0

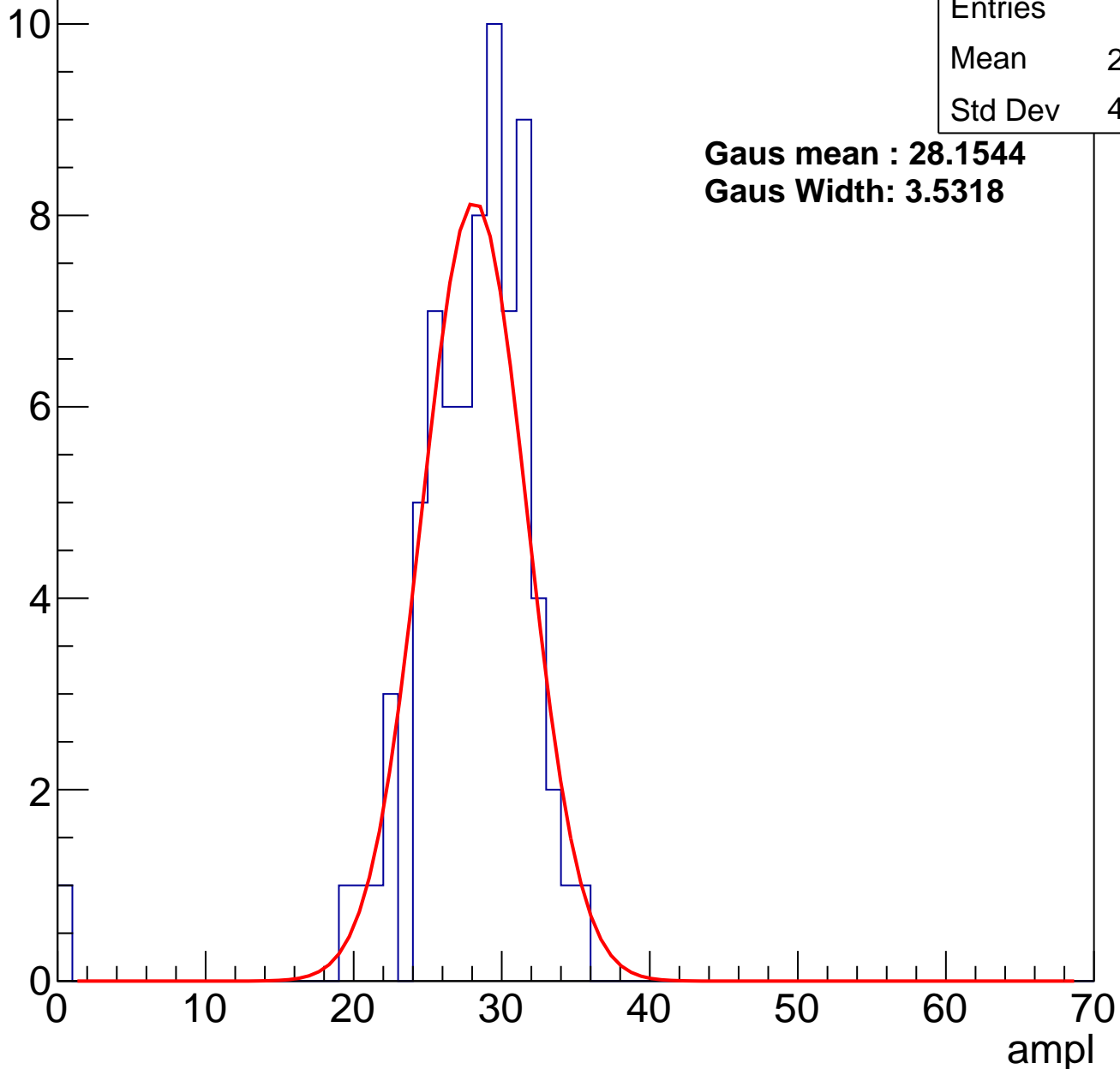
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	27.47
Std Dev	4.617

**Gaus mean : 28.1544**

**Gaus Width: 3.5318**

Entry



# B1L000S, U1-ch29, adc1

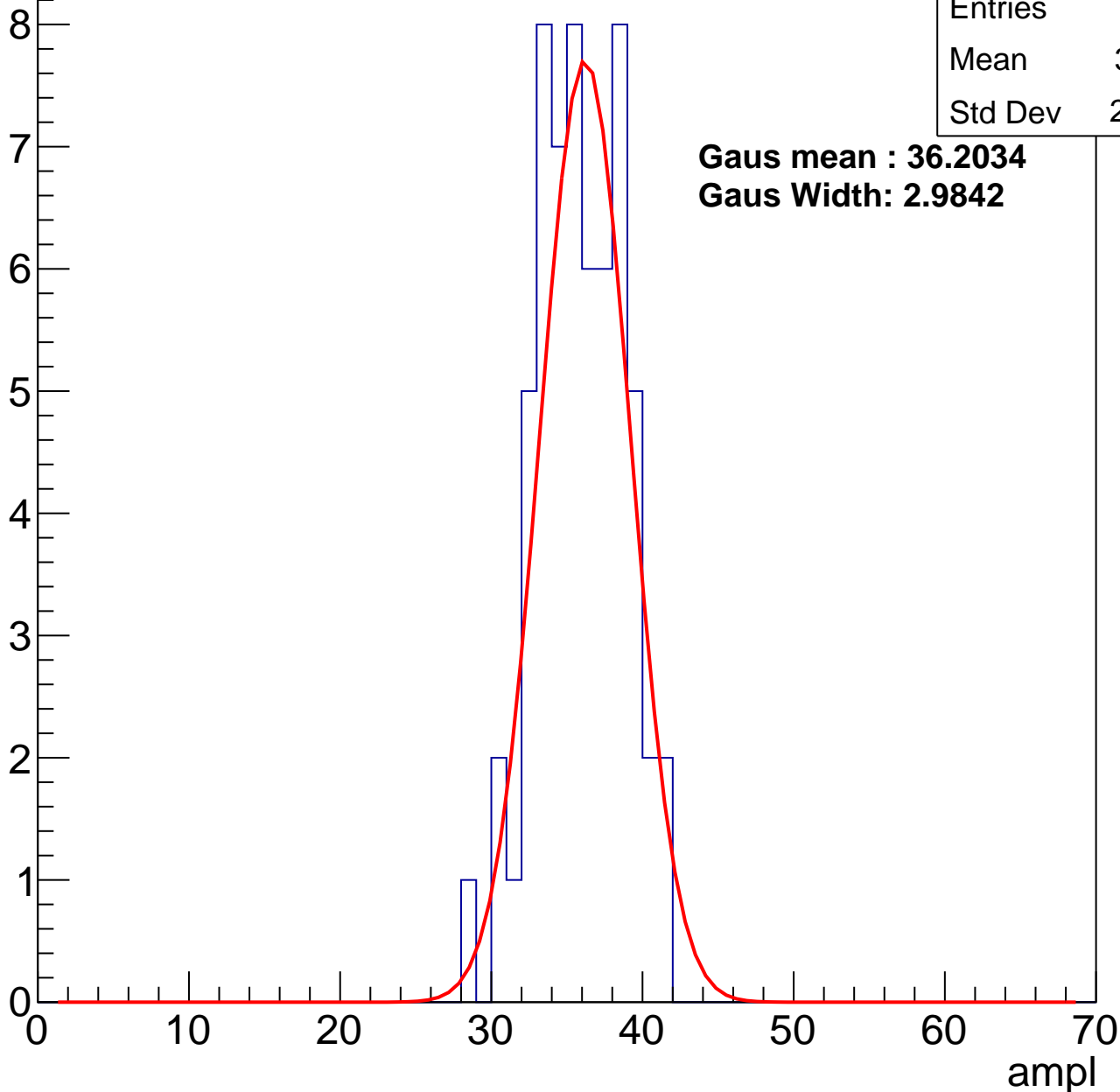
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.41
Std Dev	2.842

**Gaus mean : 36.2034**

**Gaus Width: 2.9842**



# B1L000S, U1-ch29, adc2

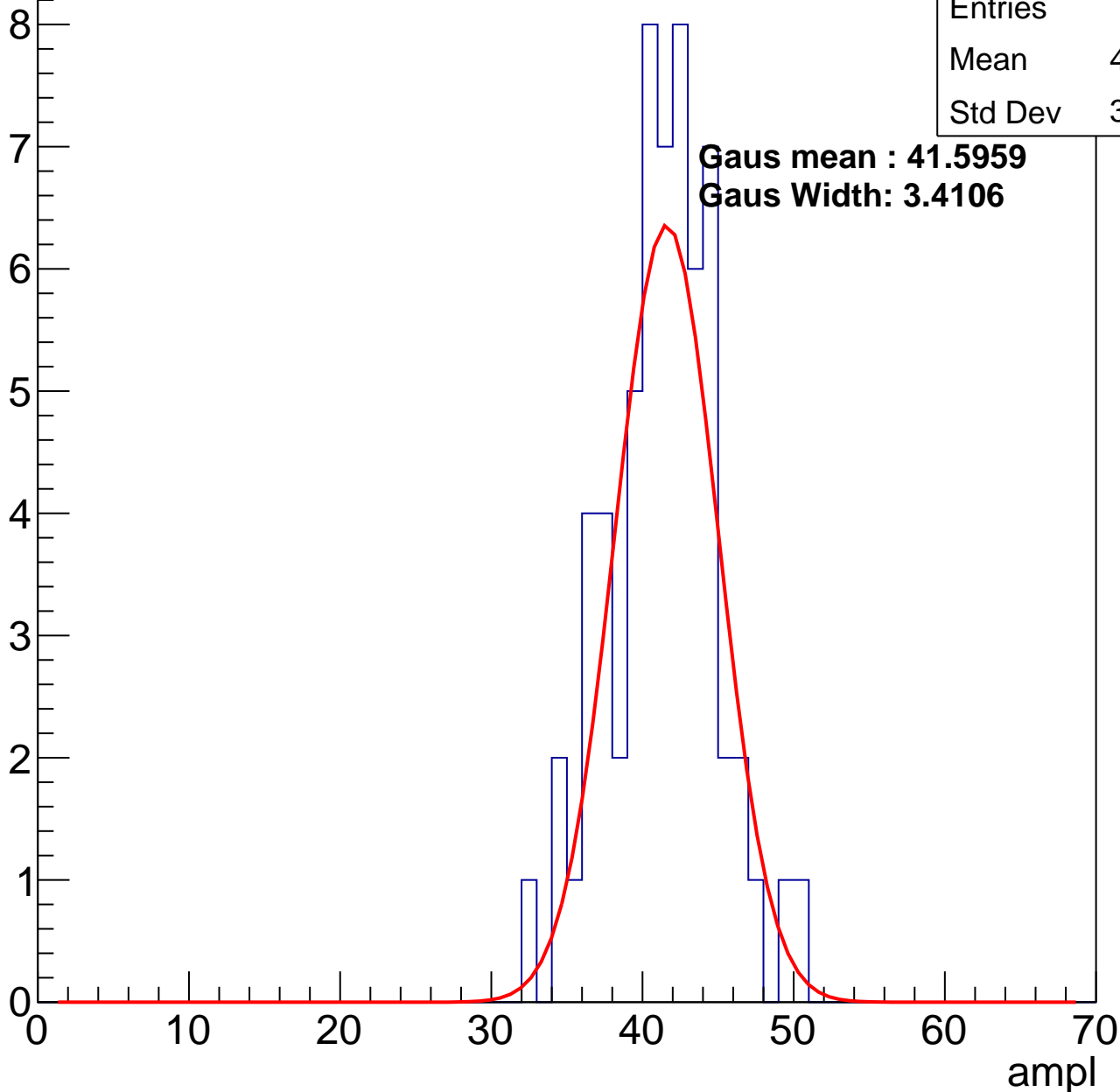
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.89
Std Dev	3.552

**Gaus mean : 41.5959**

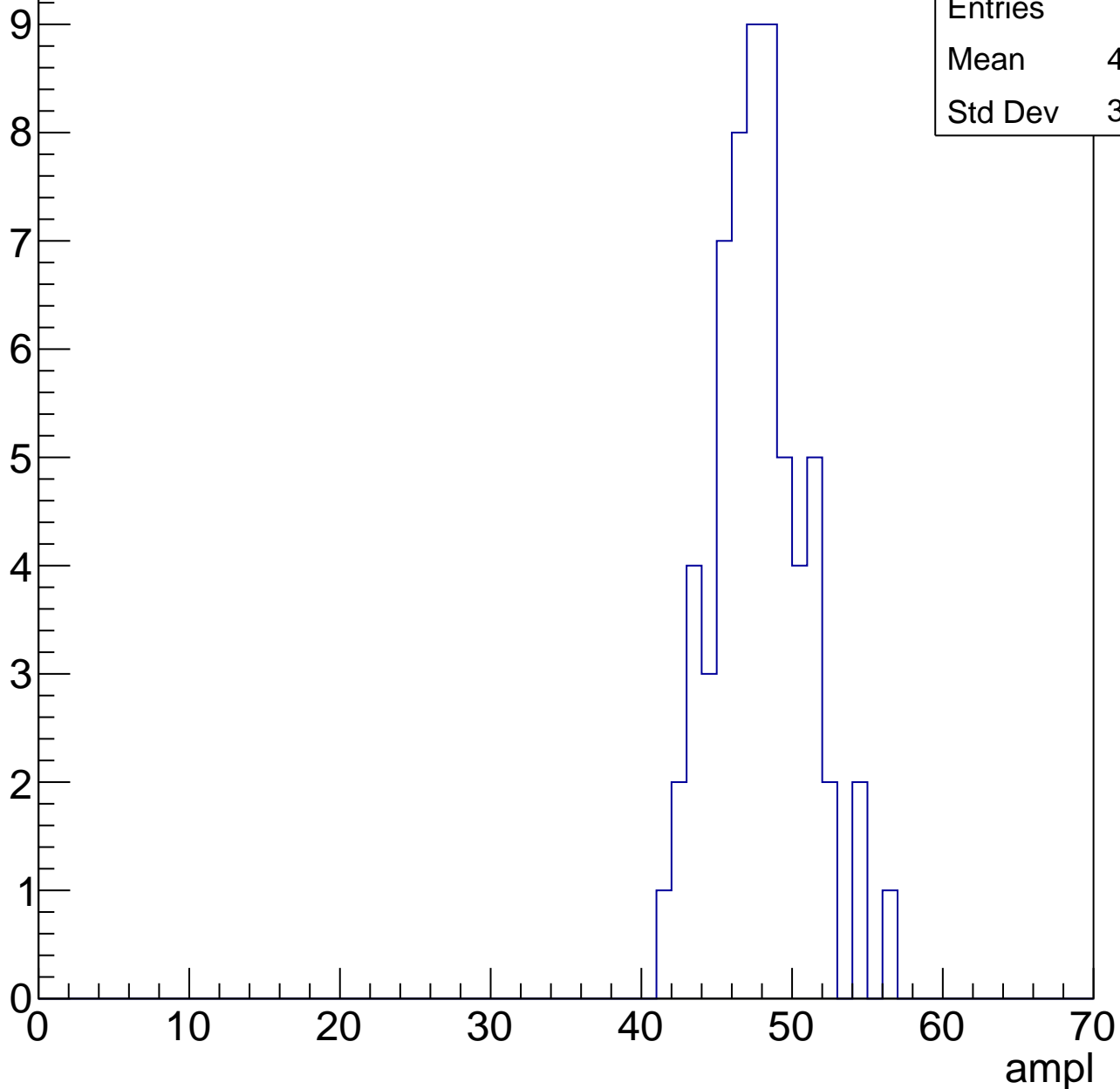
**Gaus Width: 3.4106**



# B1L000S, U1-ch29, adc3

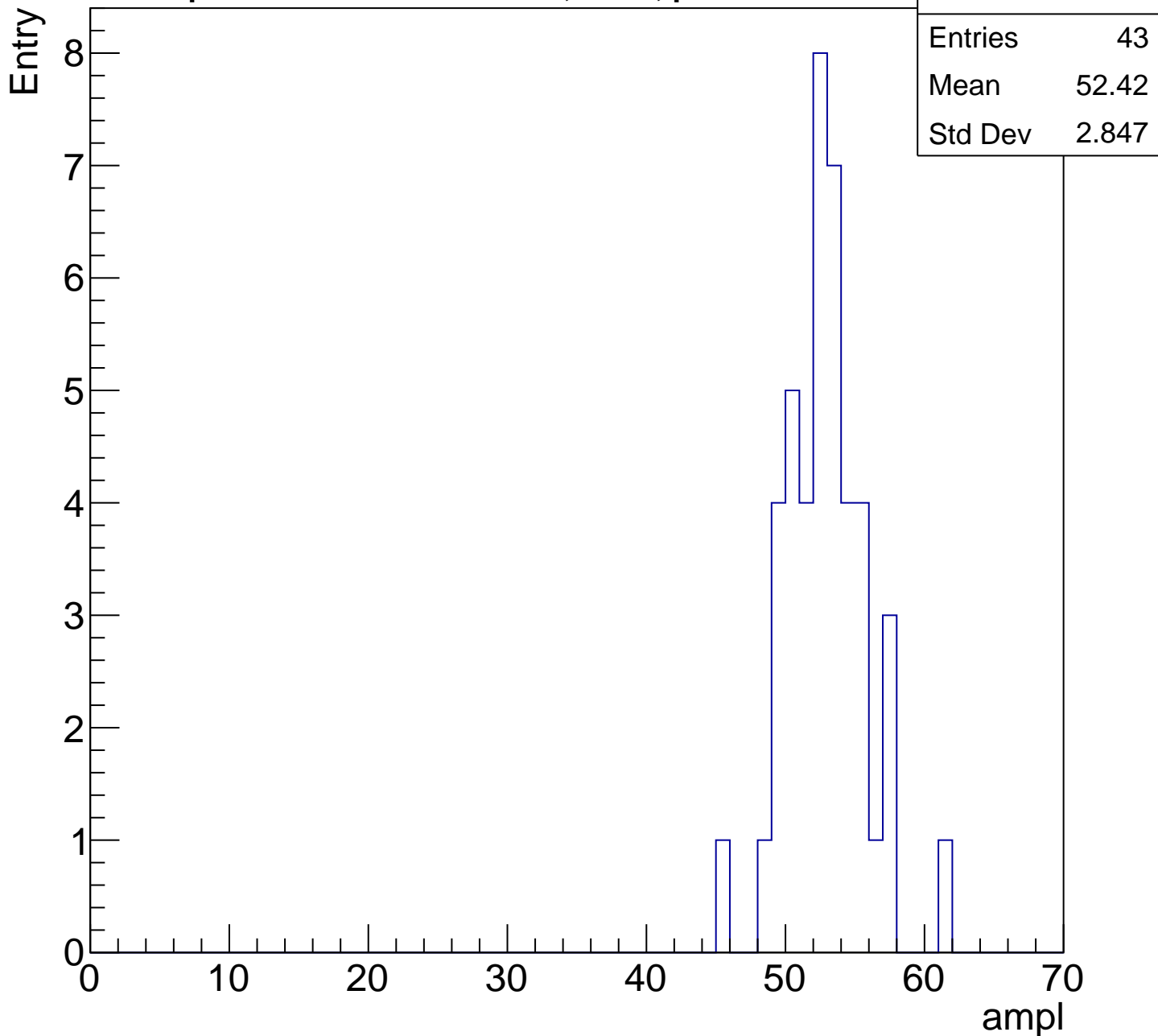
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch29, adc4

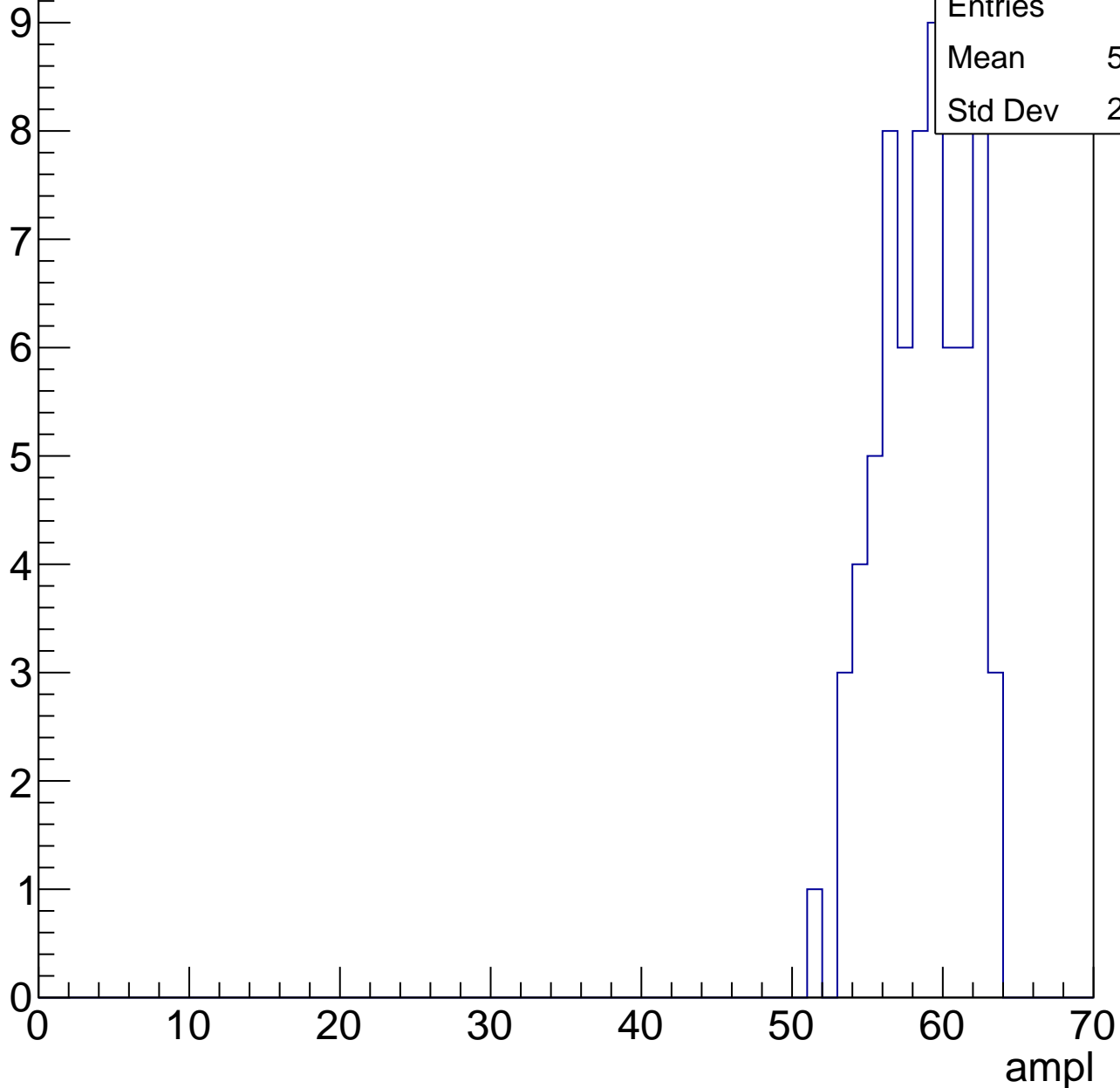
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

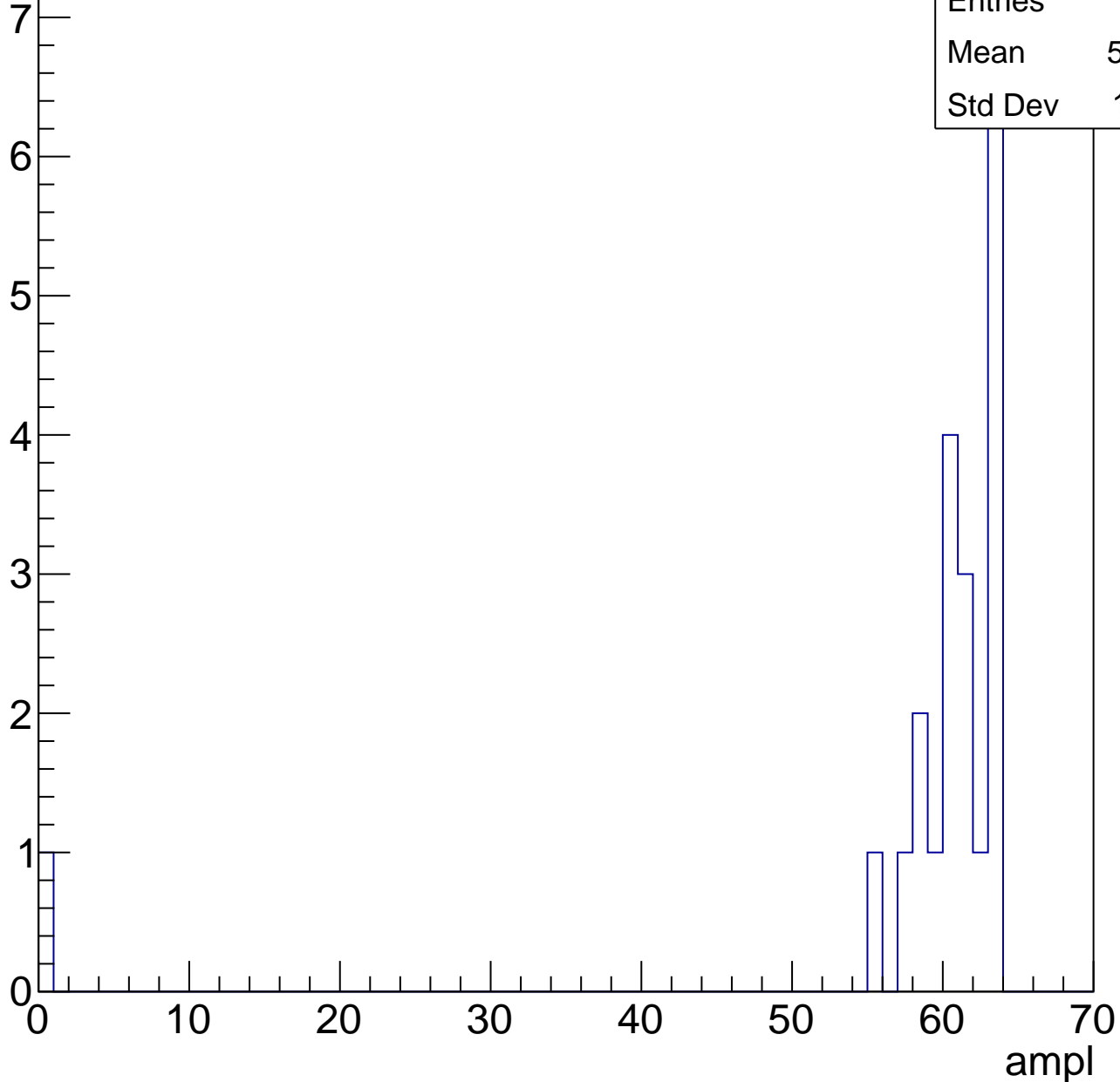


# B1L000S, U1-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	57.76
Std Dev	13.11





# B1L000S, U1-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch30, adc0

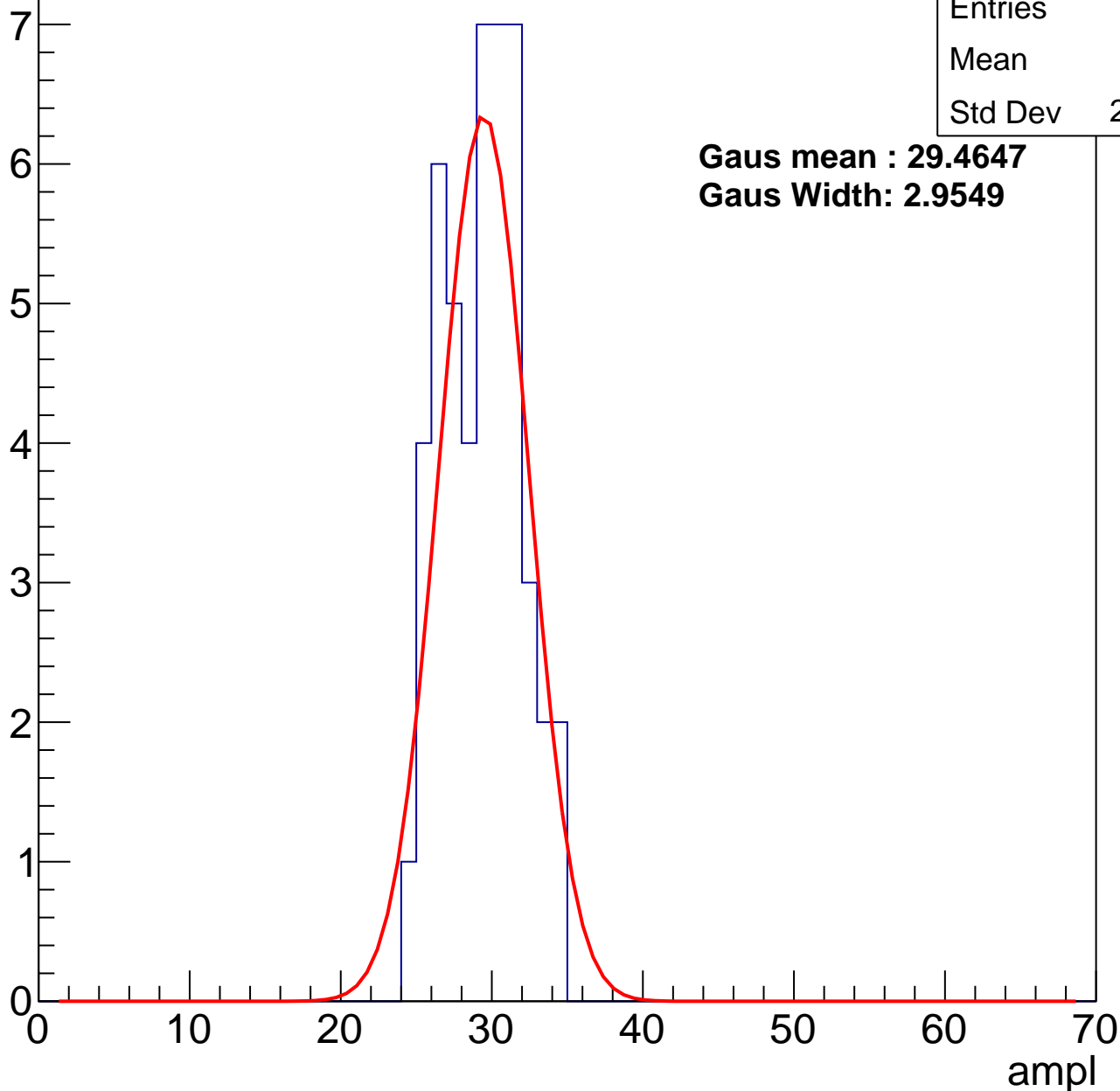
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	28.9
Std Dev	2.543

**Gaus mean : 29.4647**

**Gaus Width: 2.9549**



# B1L000S, U1-ch30, adc1

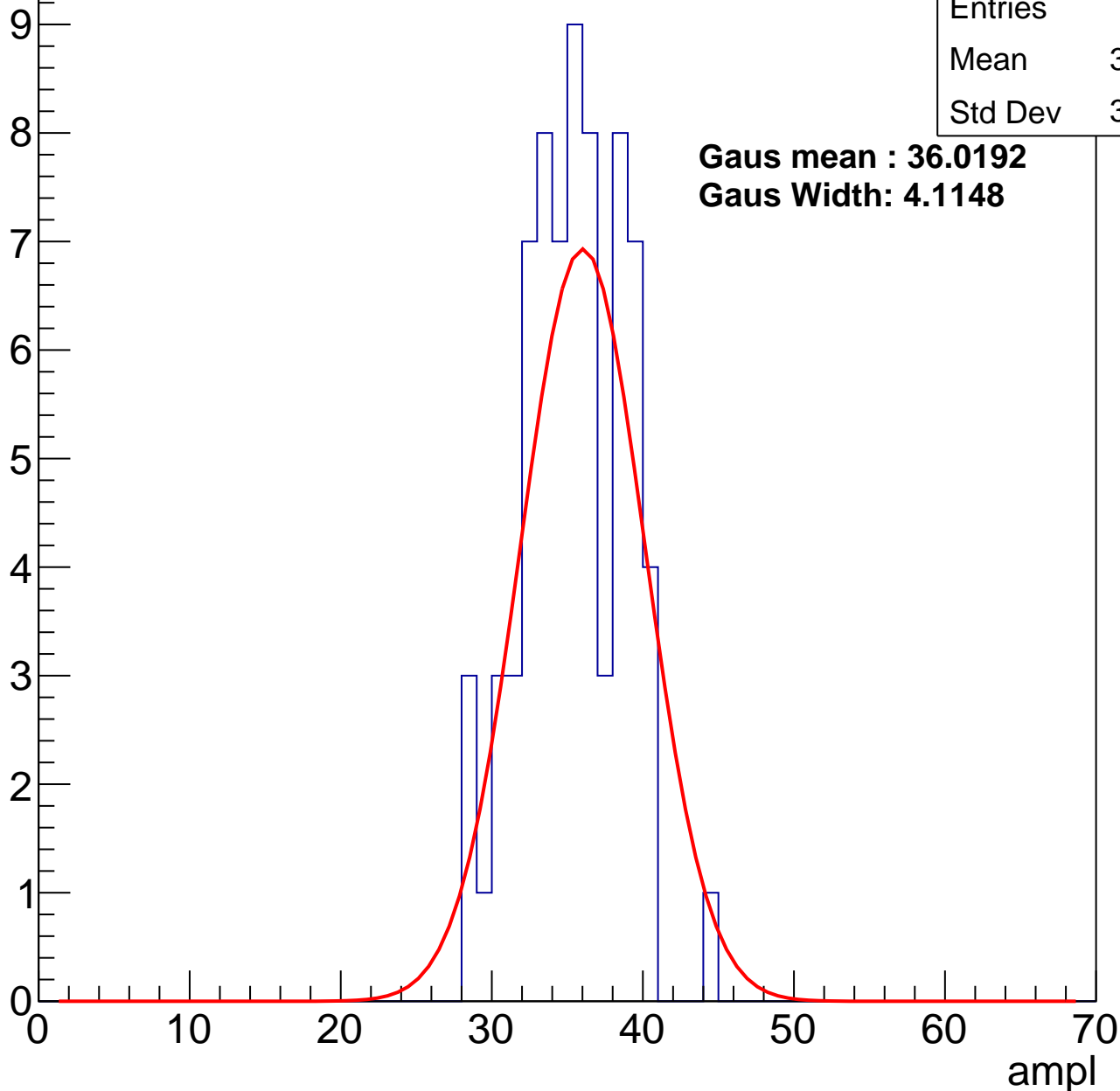
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	34.96
Std Dev	3.318

**Gaus mean : 36.0192**

**Gaus Width: 4.1148**



# B1L000S, U1-ch30, adc2

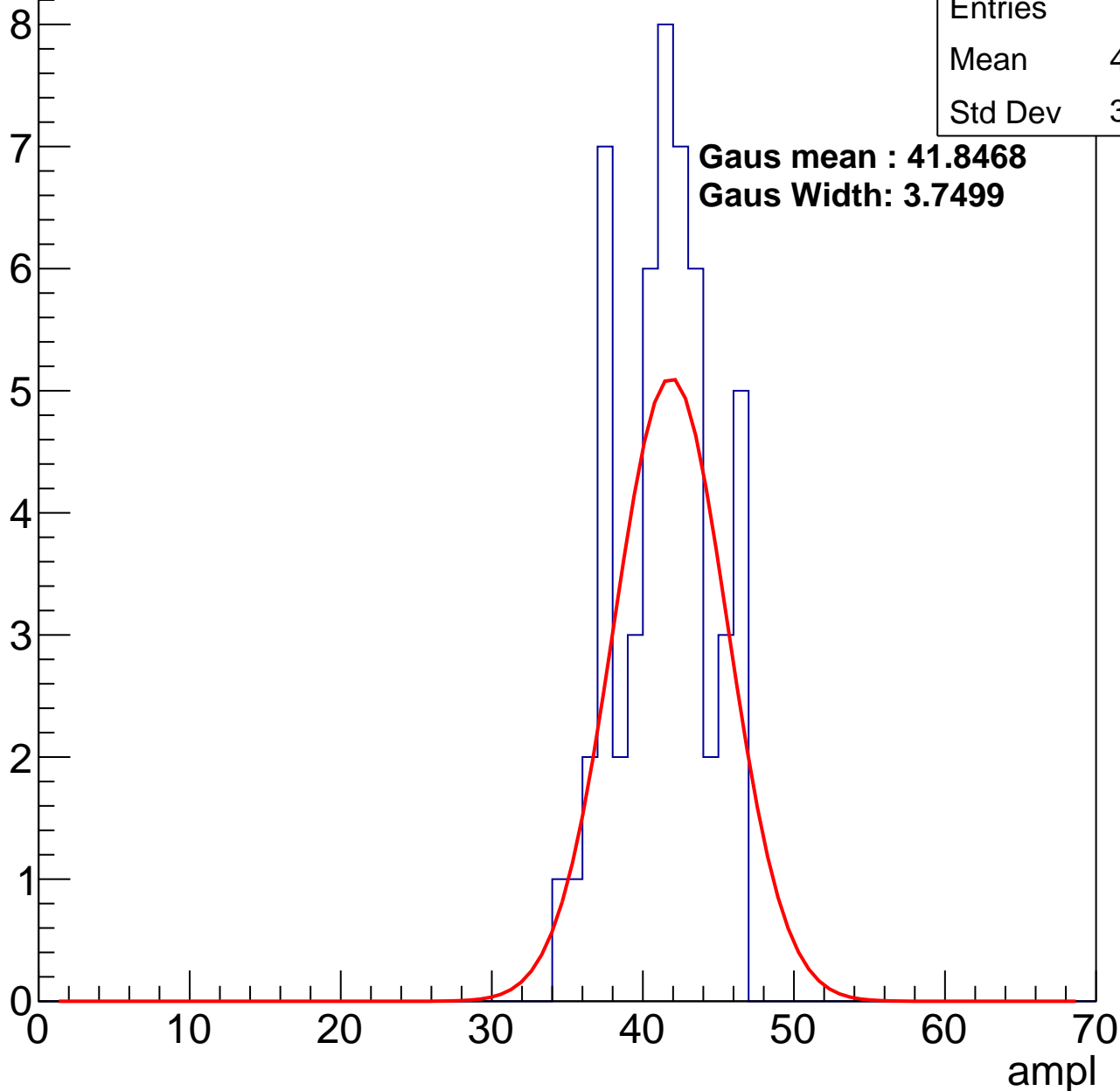
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	40.87
Std Dev	3.084

**Gaus mean : 41.8468**

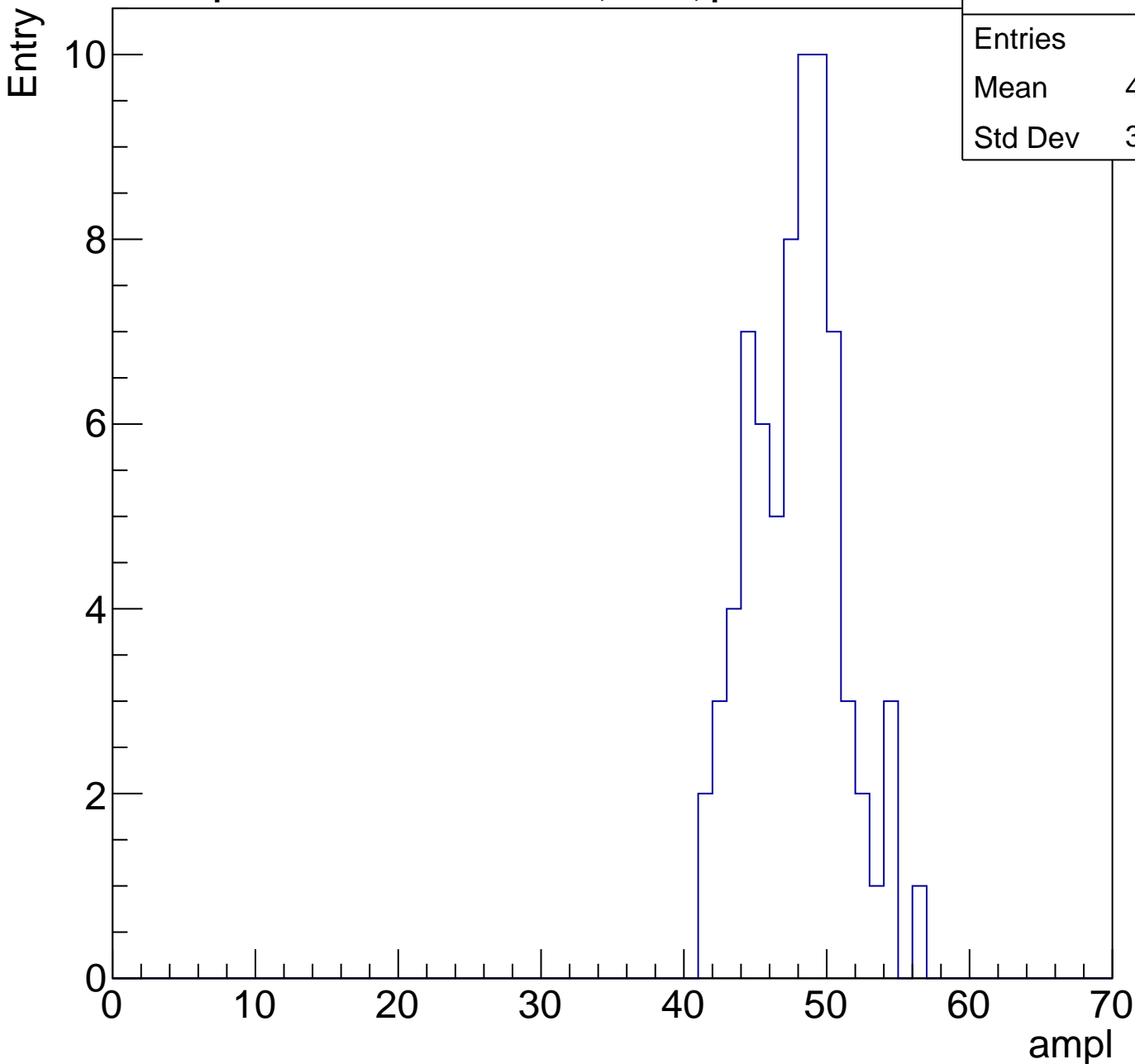
**Gaus Width: 3.7499**



# B1L000S, U1-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	47.39
Std Dev	3.264

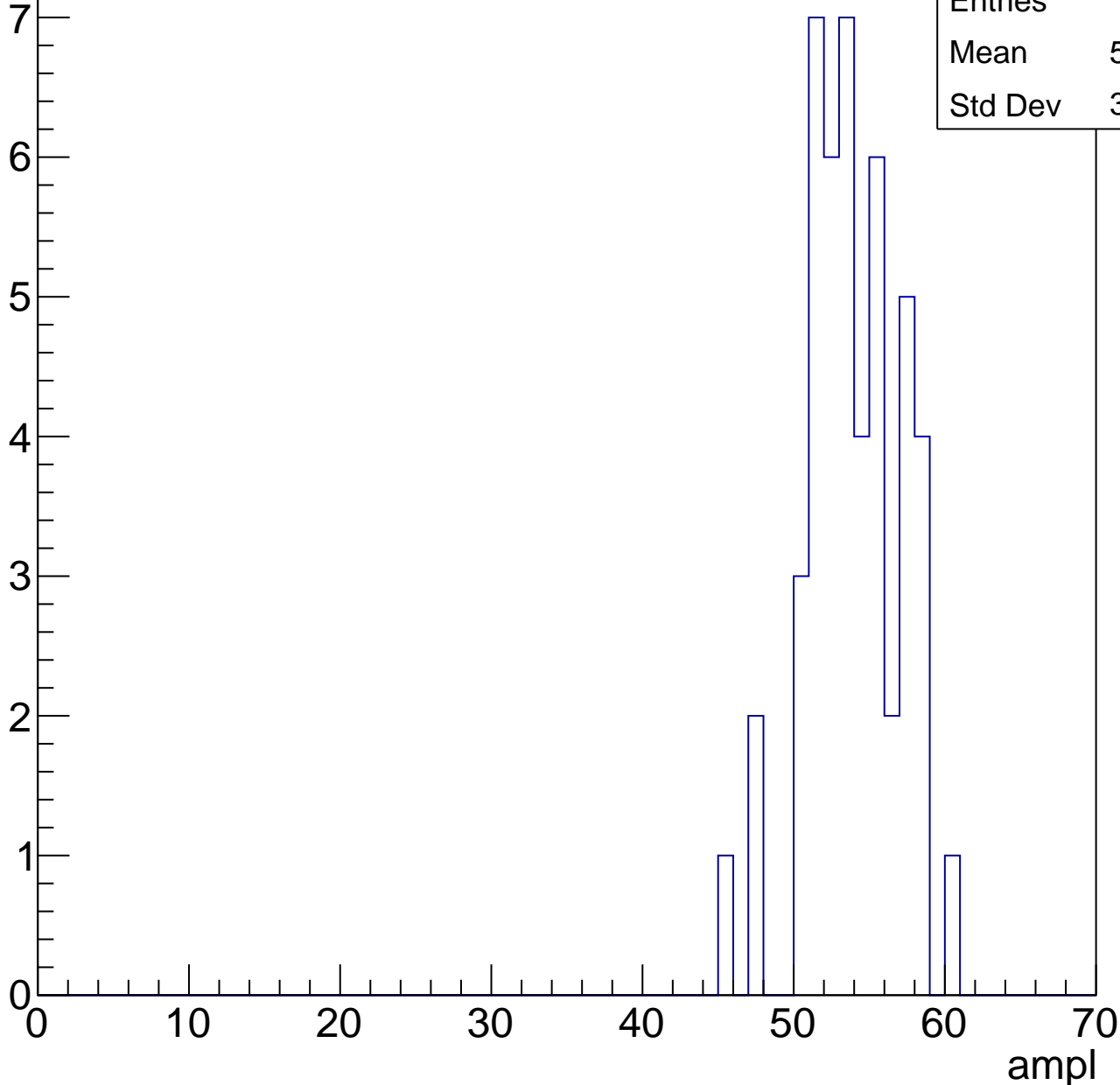


# B1L000S, U1-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	53.42
Std Dev	3.108

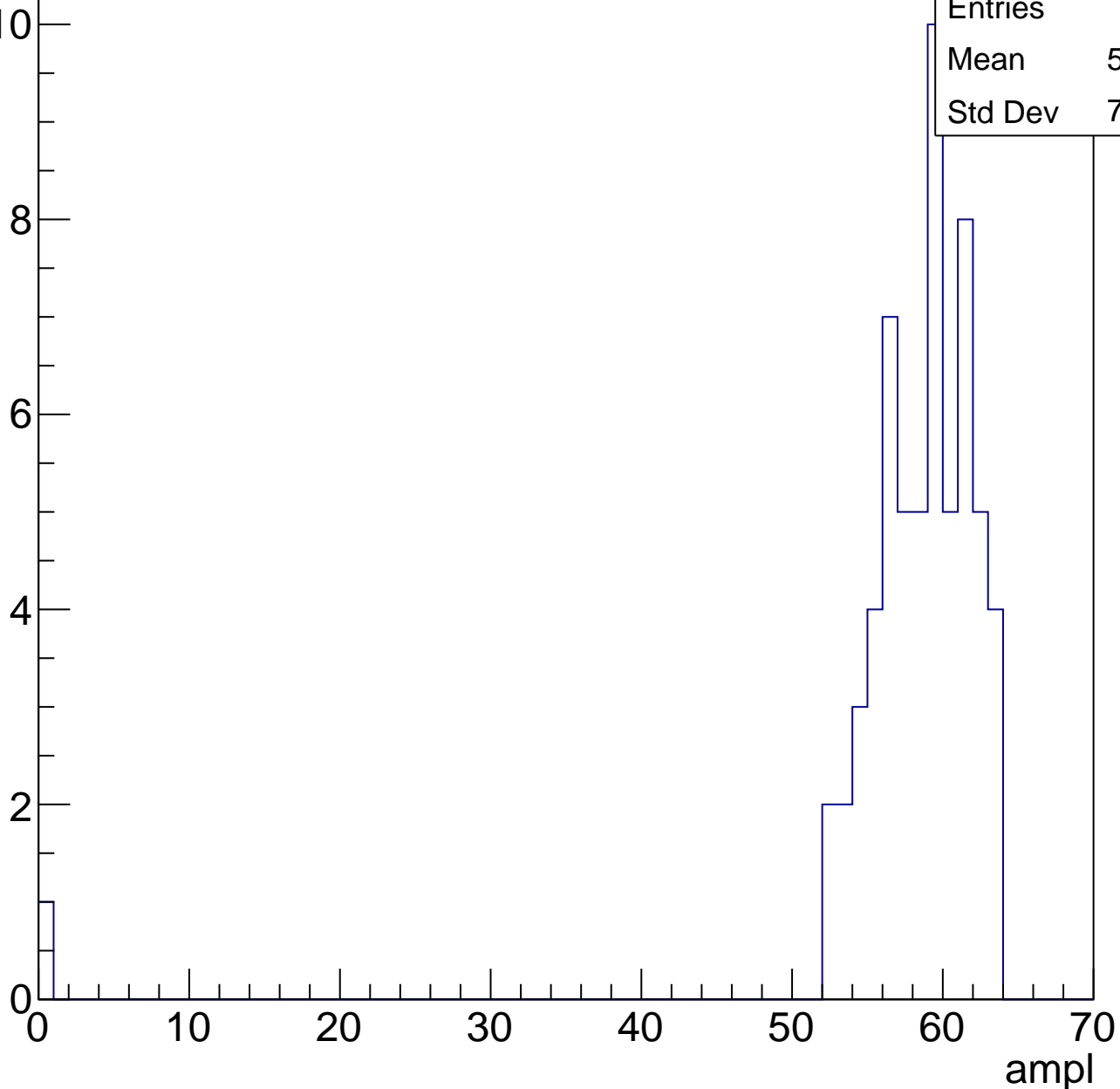


# B1L000S, U1-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	57.36
Std Dev	7.955

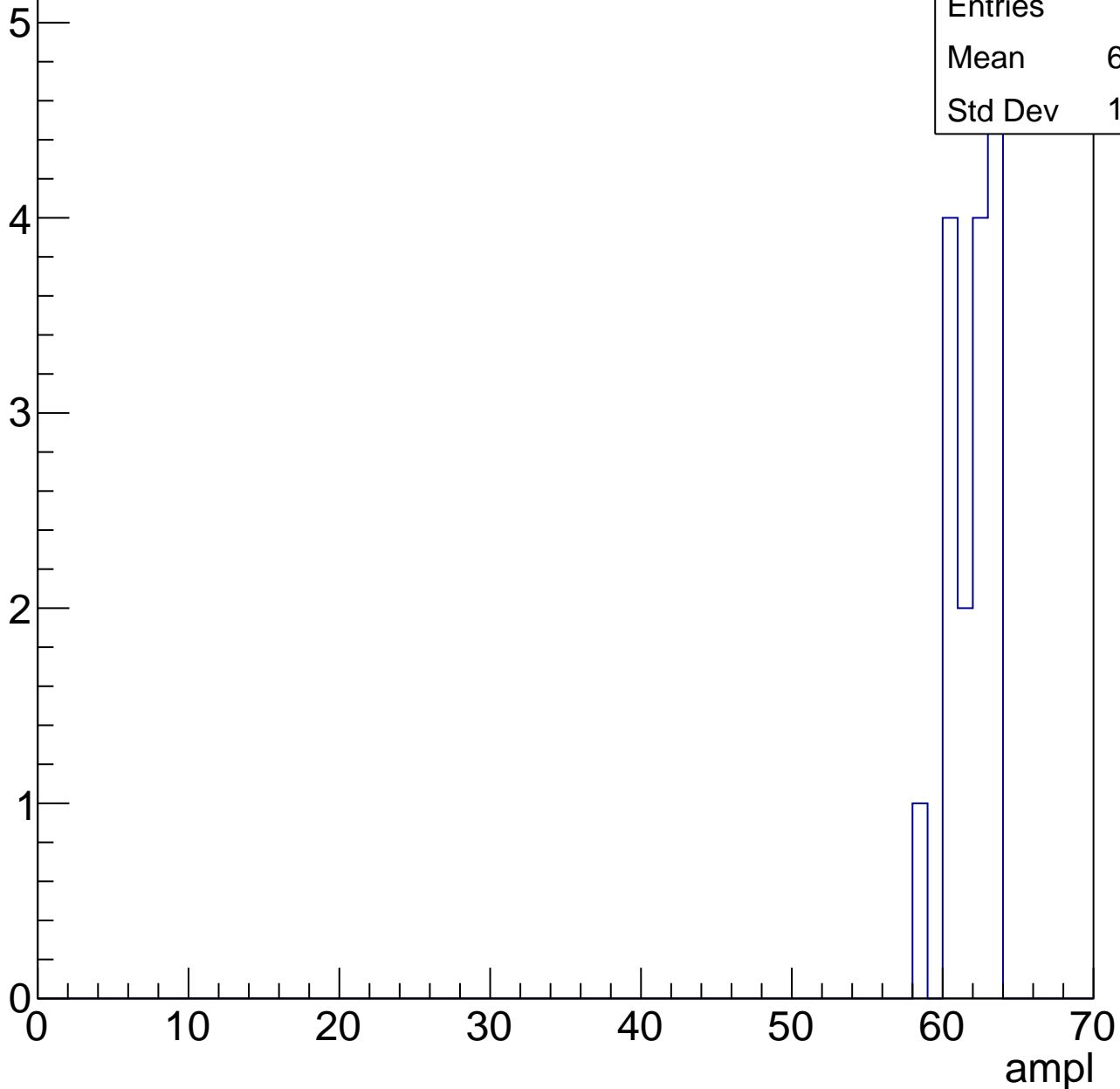


# B1L000S, U1-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.44
Std Dev	1.456





# B1L000S, U1-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	41.67
Std Dev	29.47

# B1L000S, U1-ch31, adc0

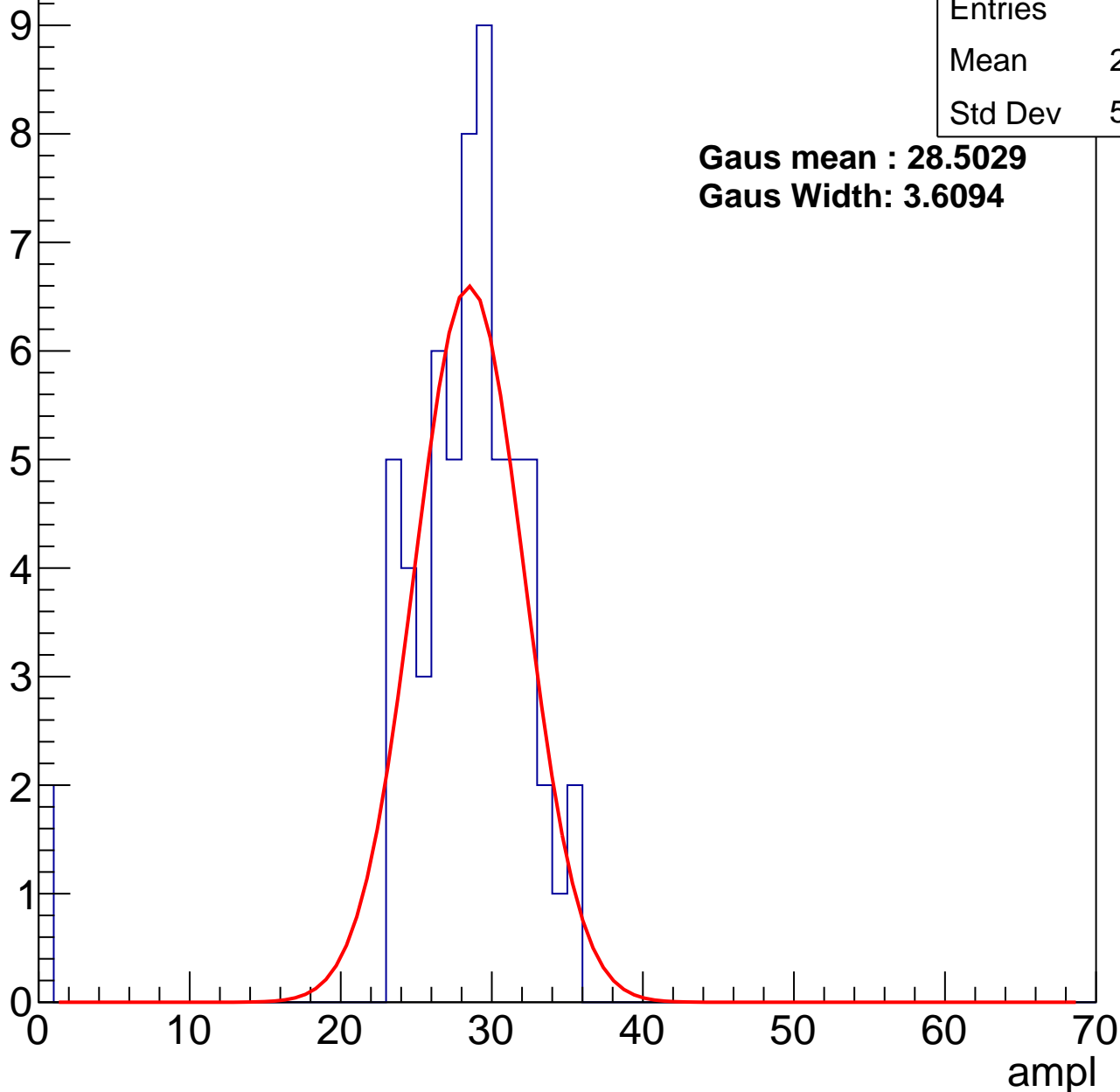
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	27.37
Std Dev	5.856

**Gaus mean : 28.5029**

**Gaus Width: 3.6094**



# B1L000S, U1-ch31, adc1

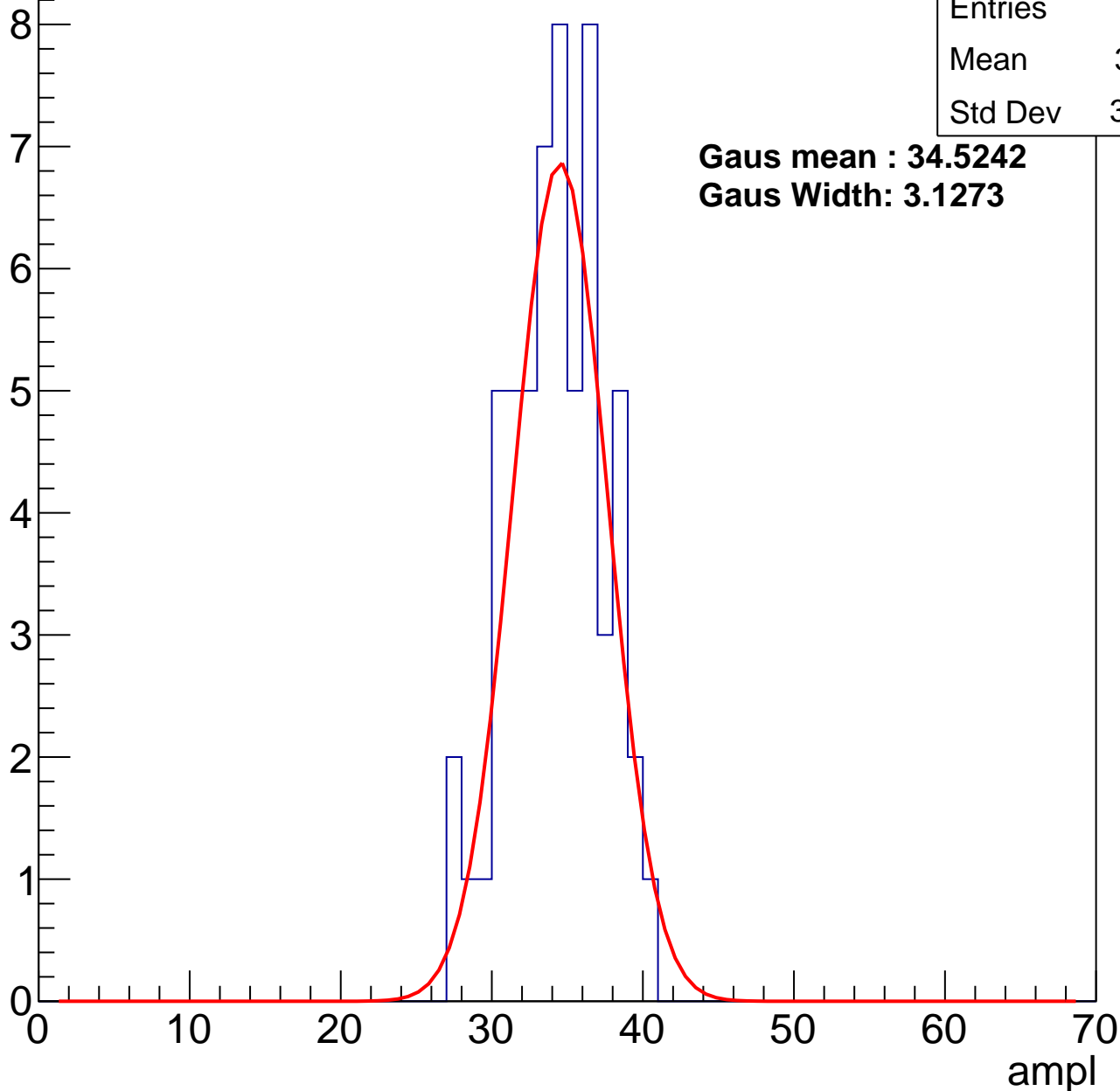
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	33.81
Std Dev	3.048

**Gaus mean : 34.5242**

**Gaus Width: 3.1273**



# B1L000S, U1-ch31, adc2

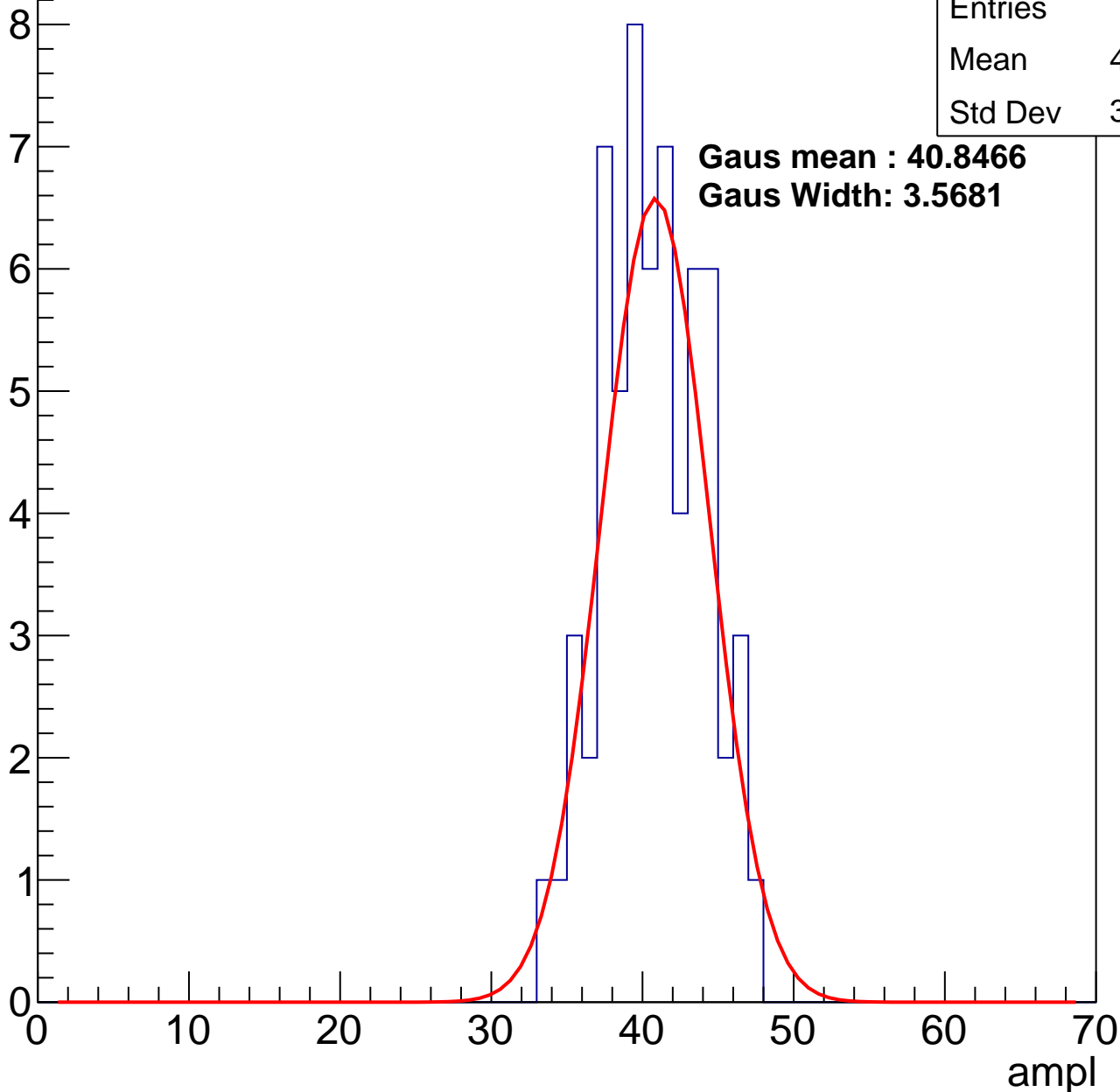
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.27
Std Dev	3.259

**Gaus mean : 40.8466**

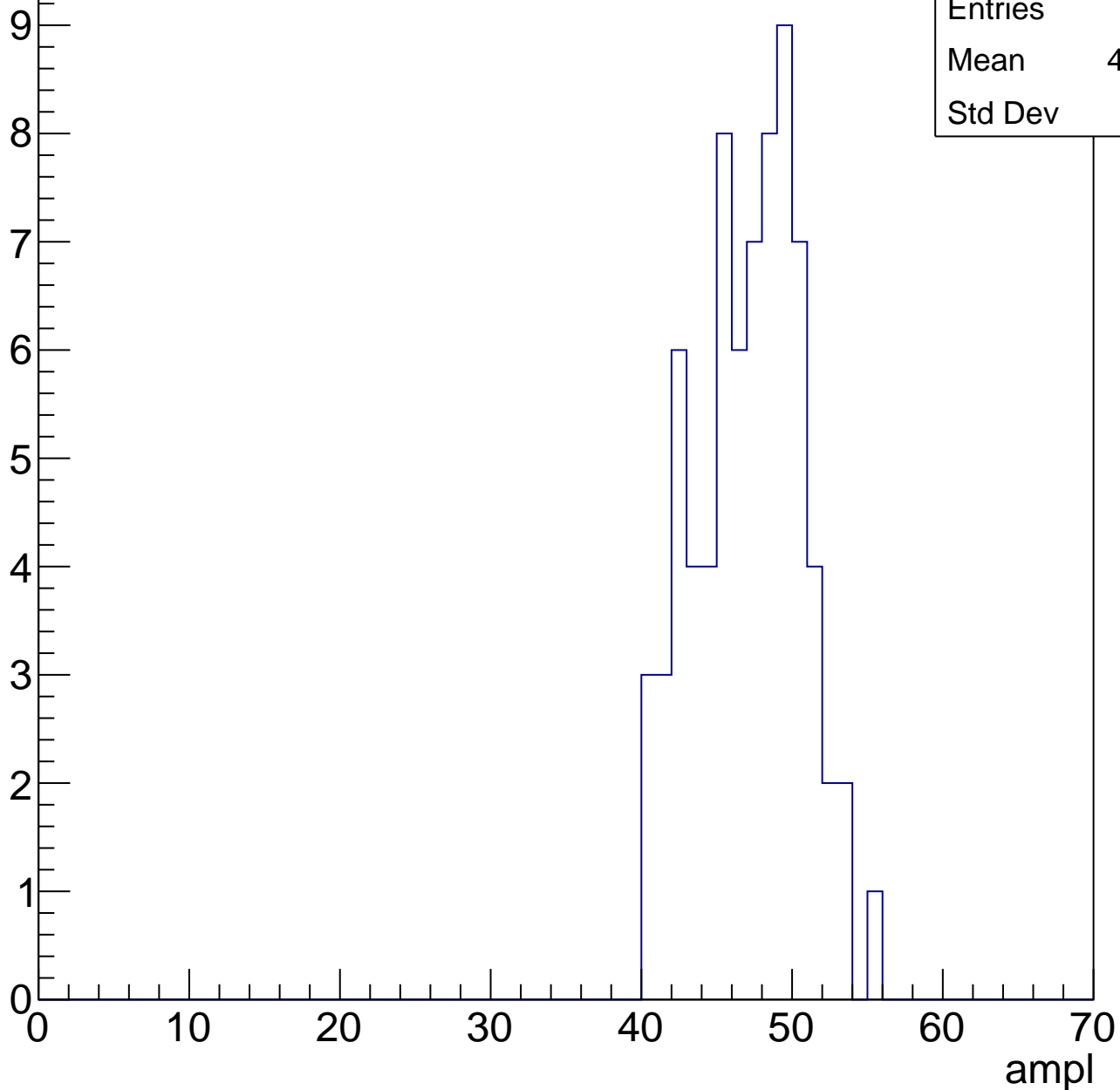
**Gaus Width: 3.5681**



# B1L000S, U1-ch31, adc3

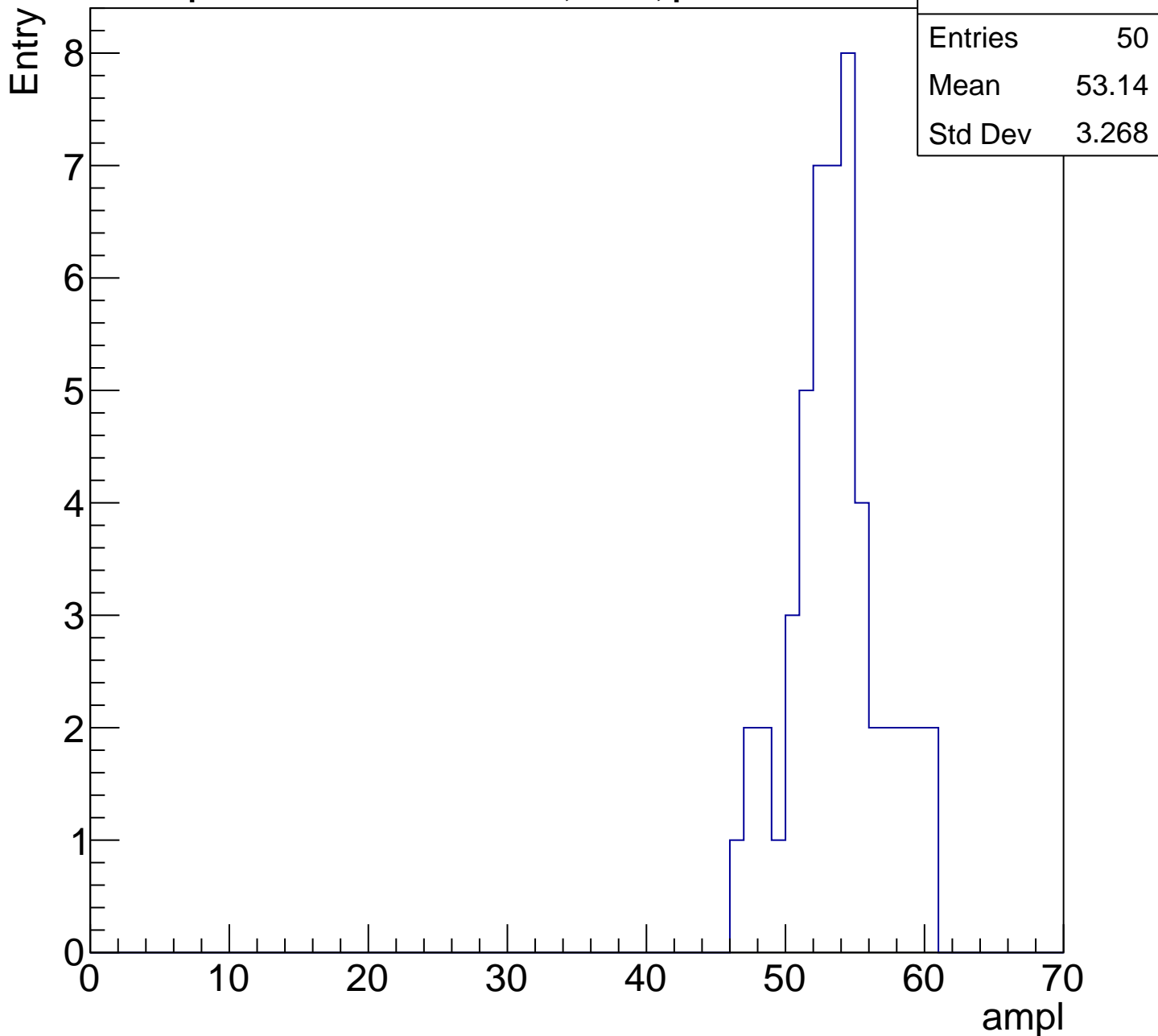
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



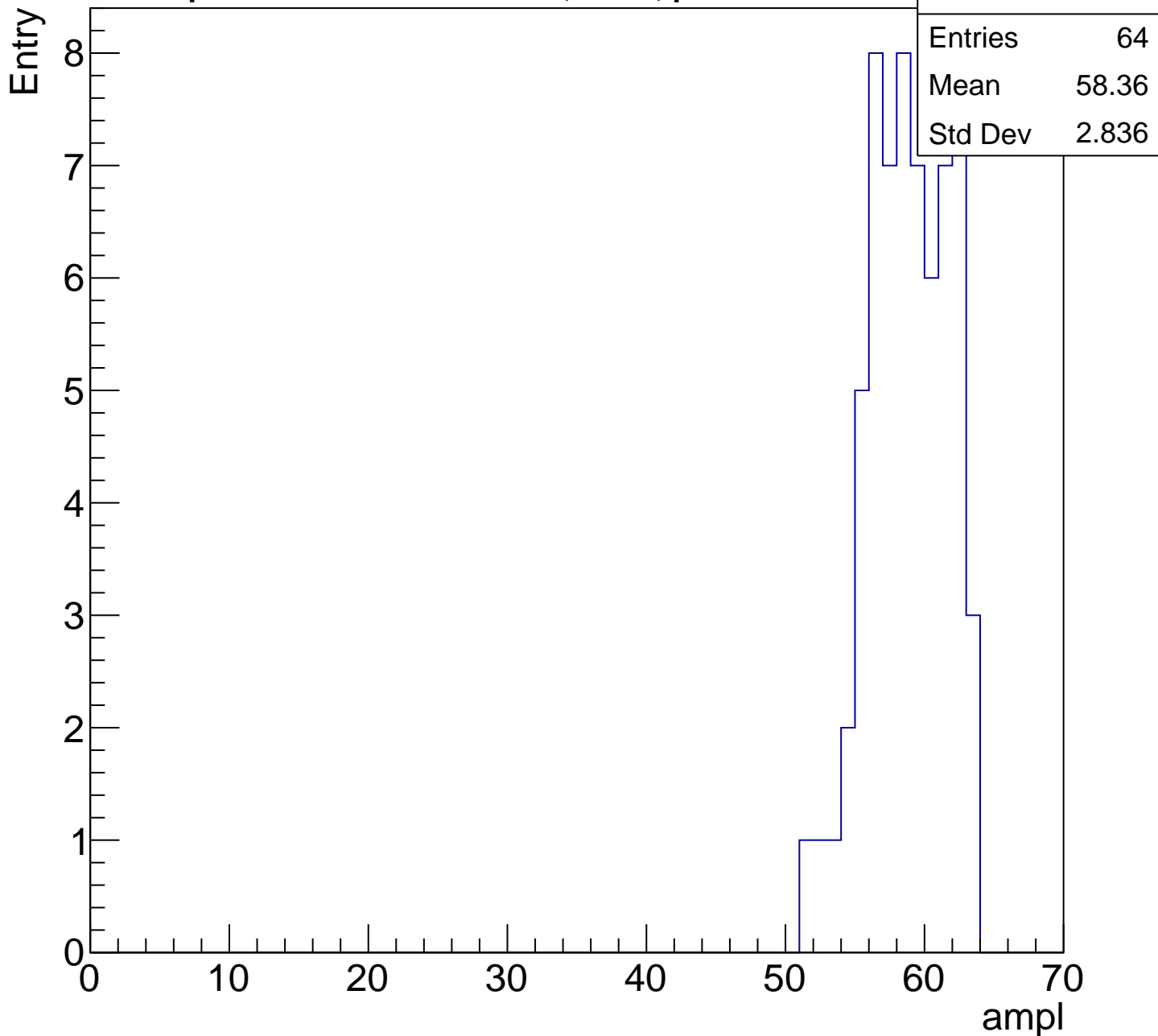
# B1L000S, U1-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

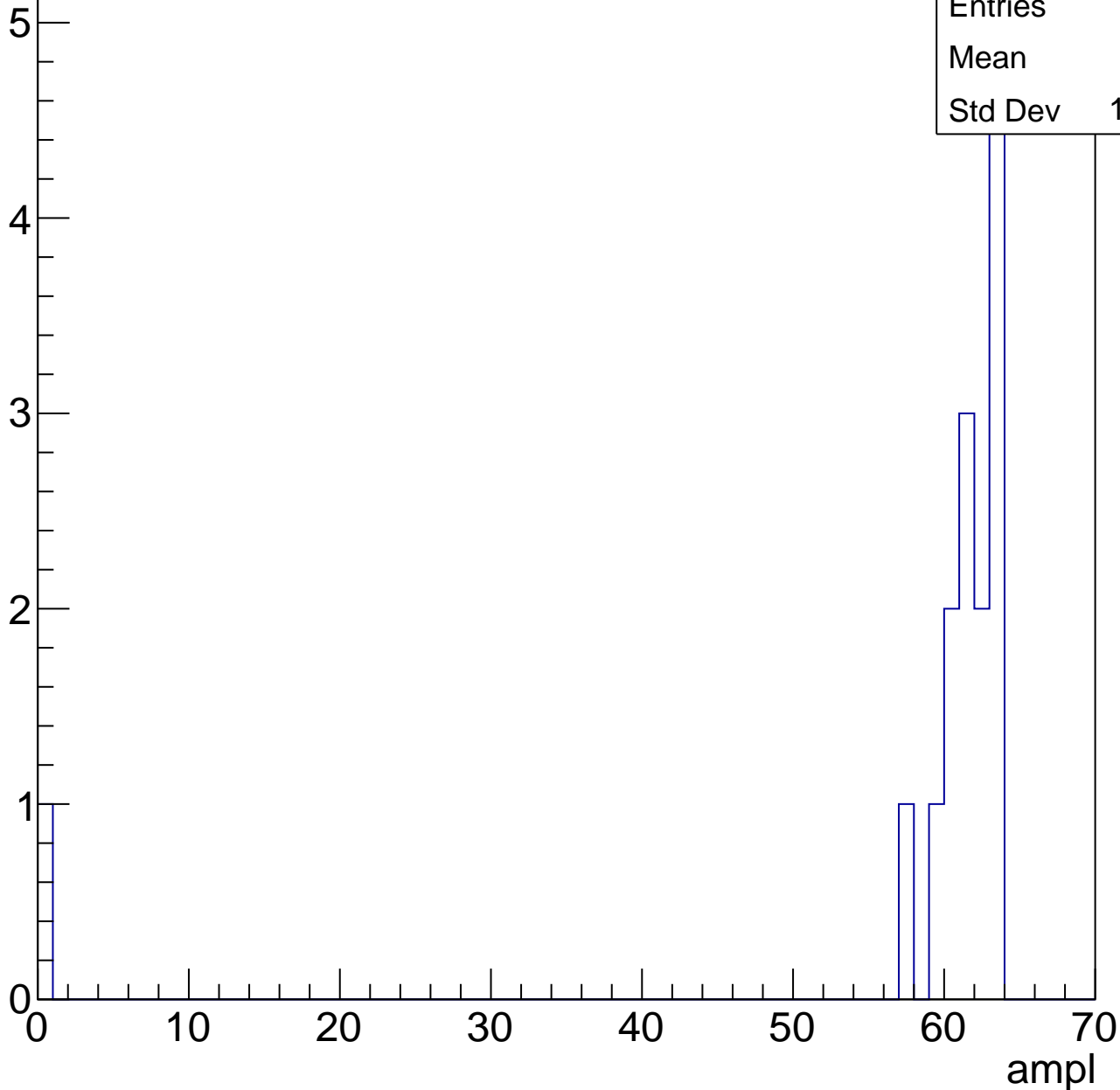


# B1L000S, U1-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.2
Std Dev	15.38

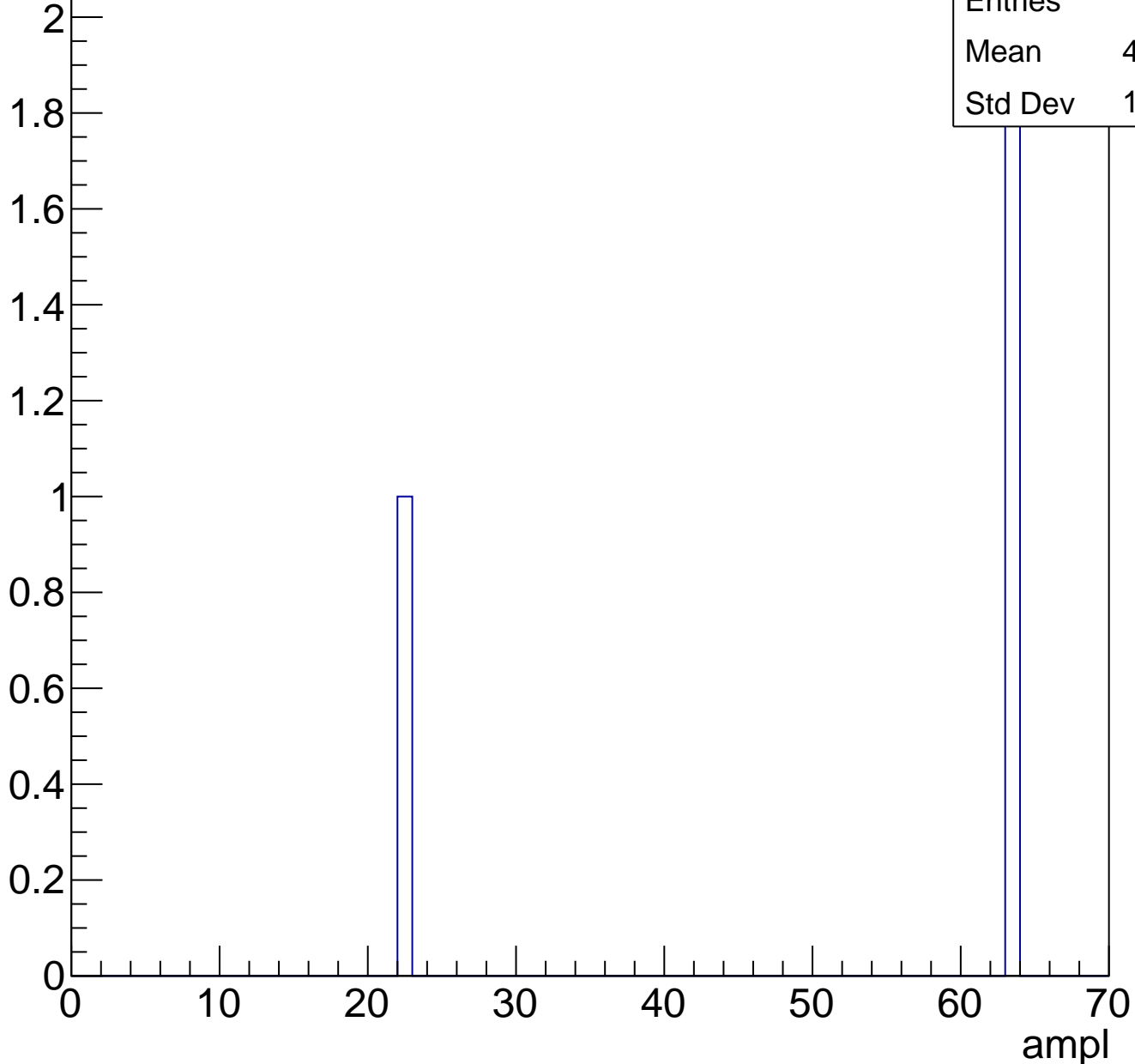




# B1L000S, U1-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	49.33
Std Dev	19.33

# B1L000S, U1-ch32, adc0

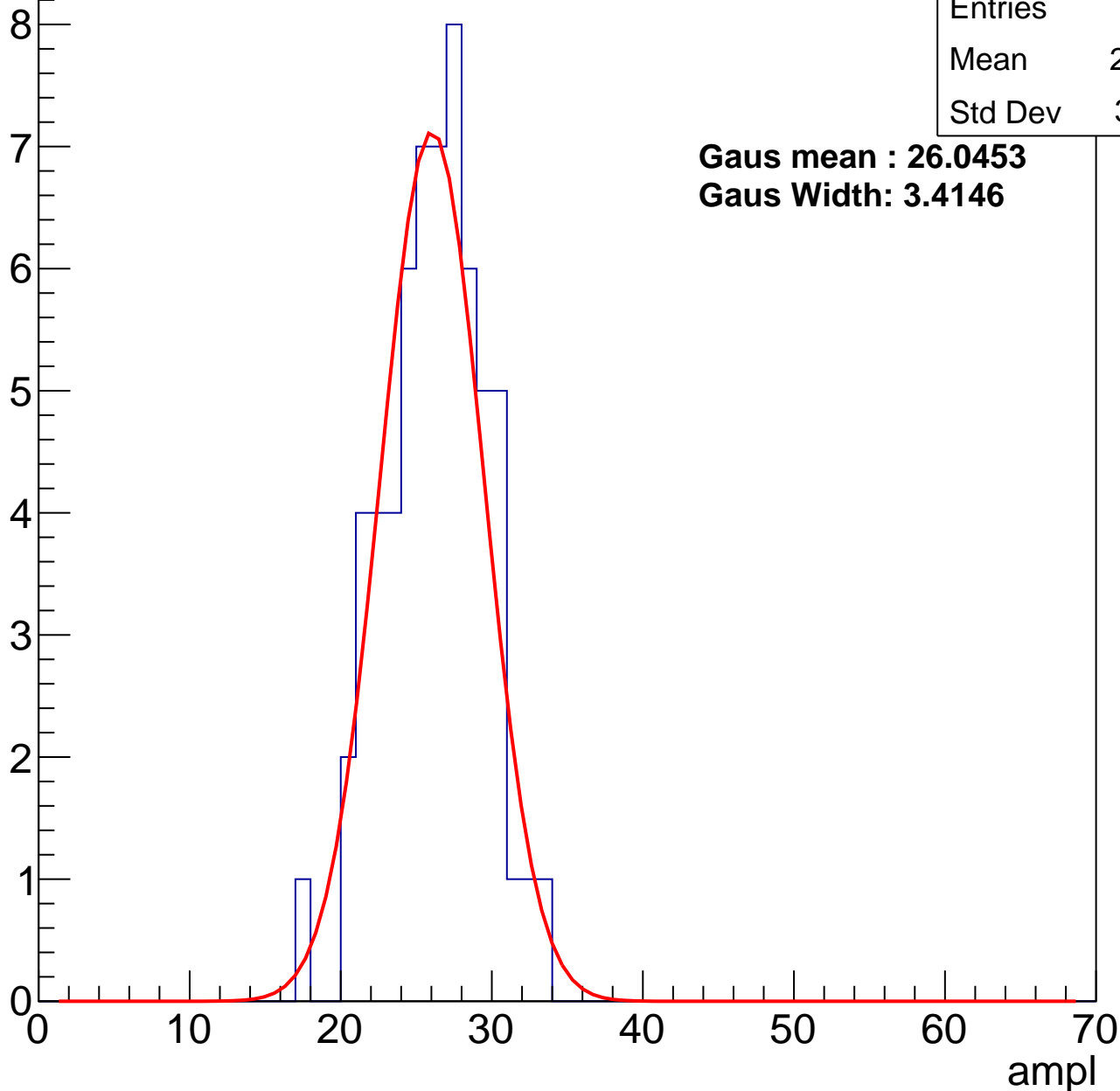
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	25.76
Std Dev	3.231

**Gaus mean : 26.0453**

**Gaus Width: 3.4146**



# B1L000S, U1-ch32, adc1

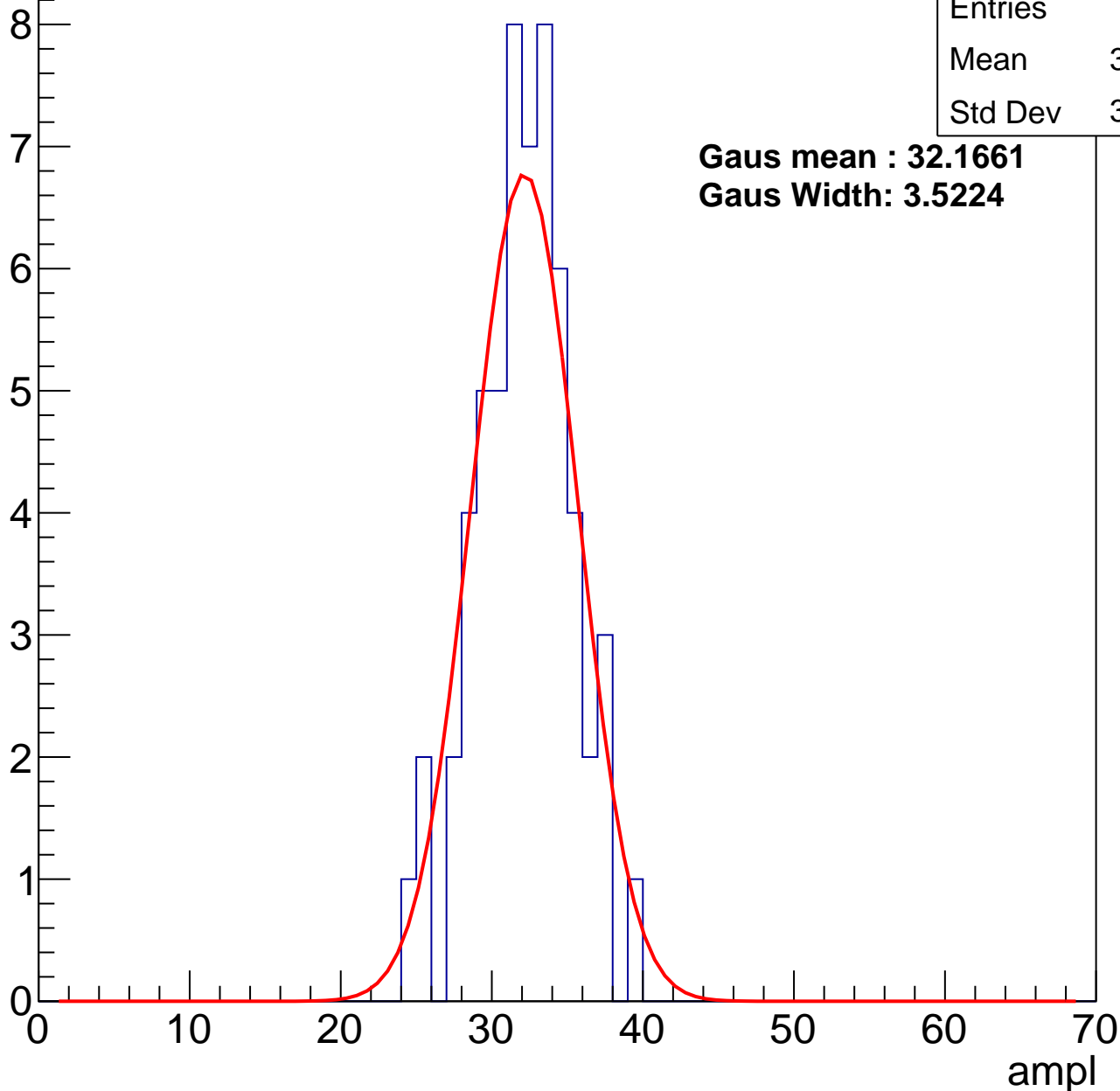
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	31.67
Std Dev	3.126

**Gaus mean : 32.1661**

**Gaus Width: 3.5224**



# B1L000S, U1-ch32, adc2

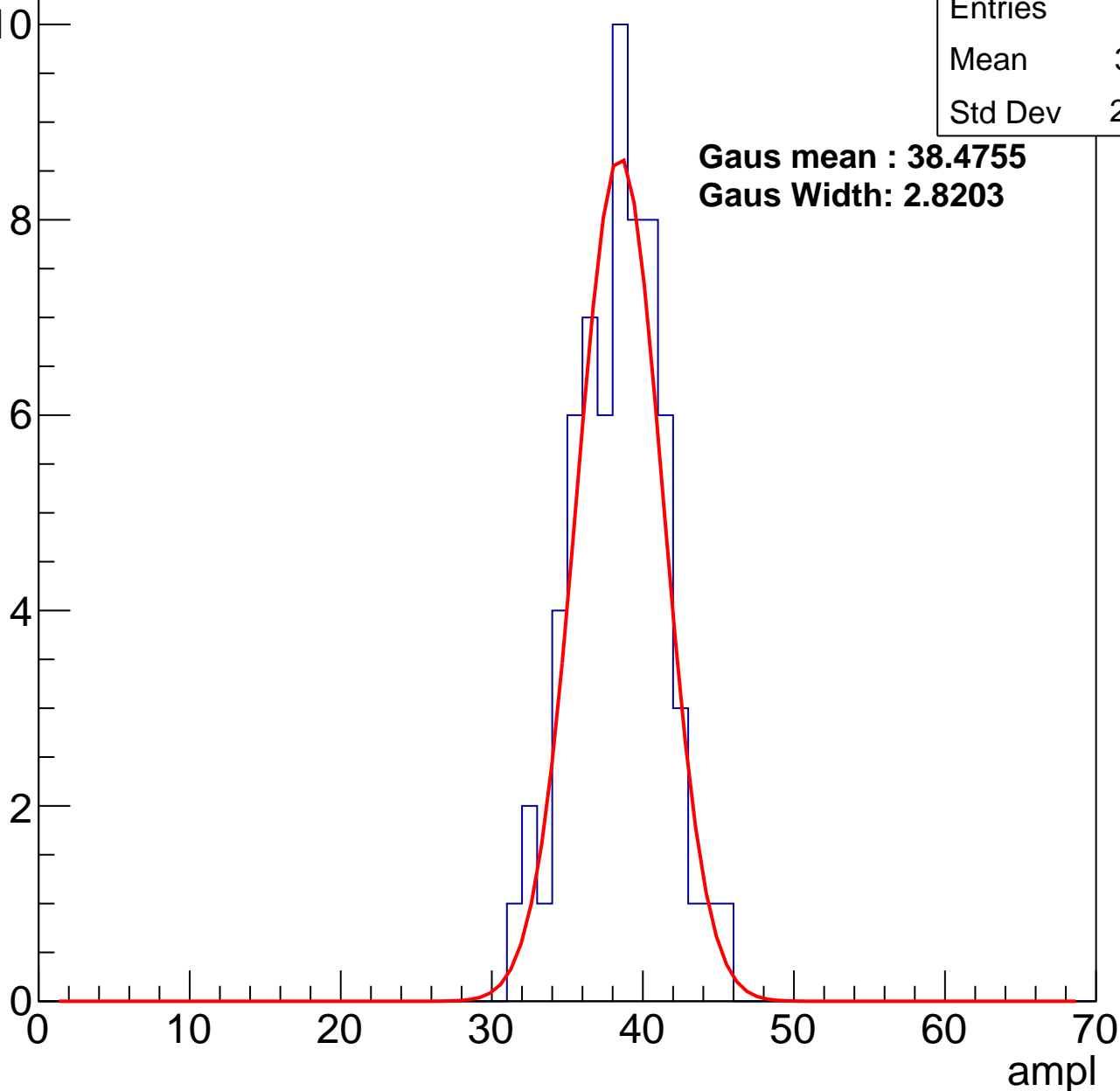
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	37.91
Std Dev	2.907

**Gaus mean : 38.4755**

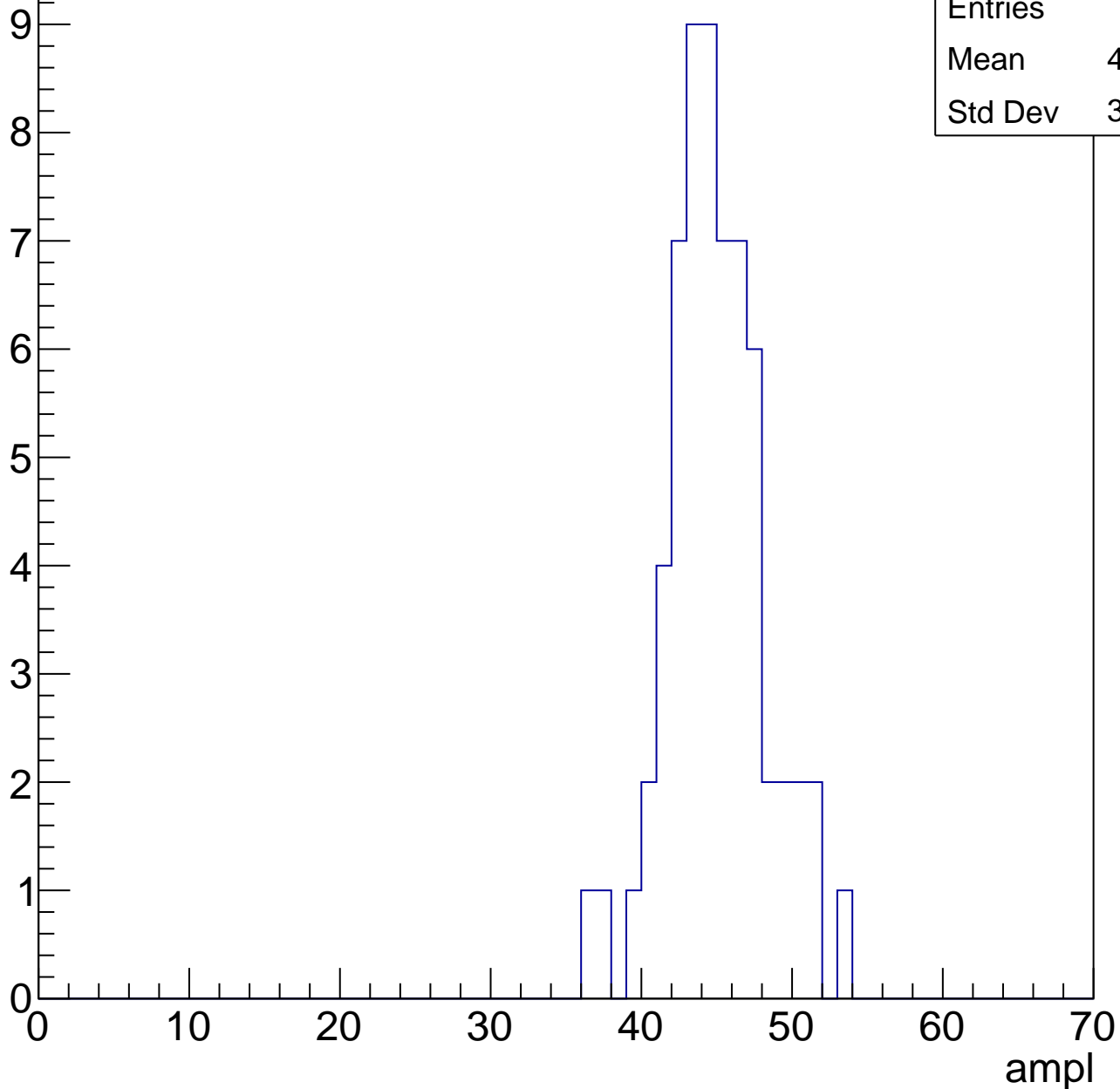
**Gaus Width: 2.8203**



# B1L000S, U1-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	63
Mean	44.46
Std Dev	3.216

# B1L000S, U1-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 49

Mean 50.04

Std Dev 2.466

0

10

20

30

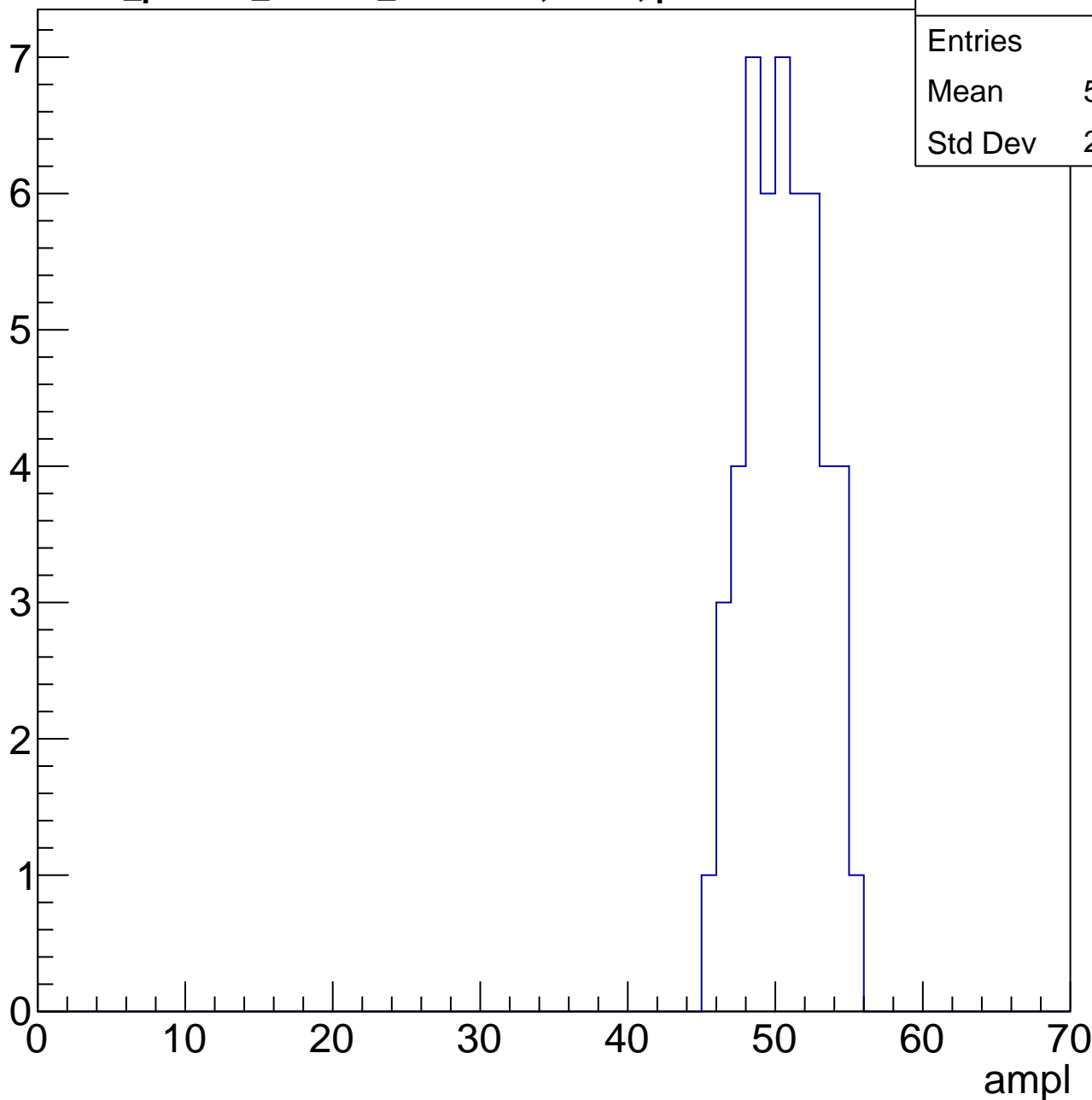
40

50

60

70

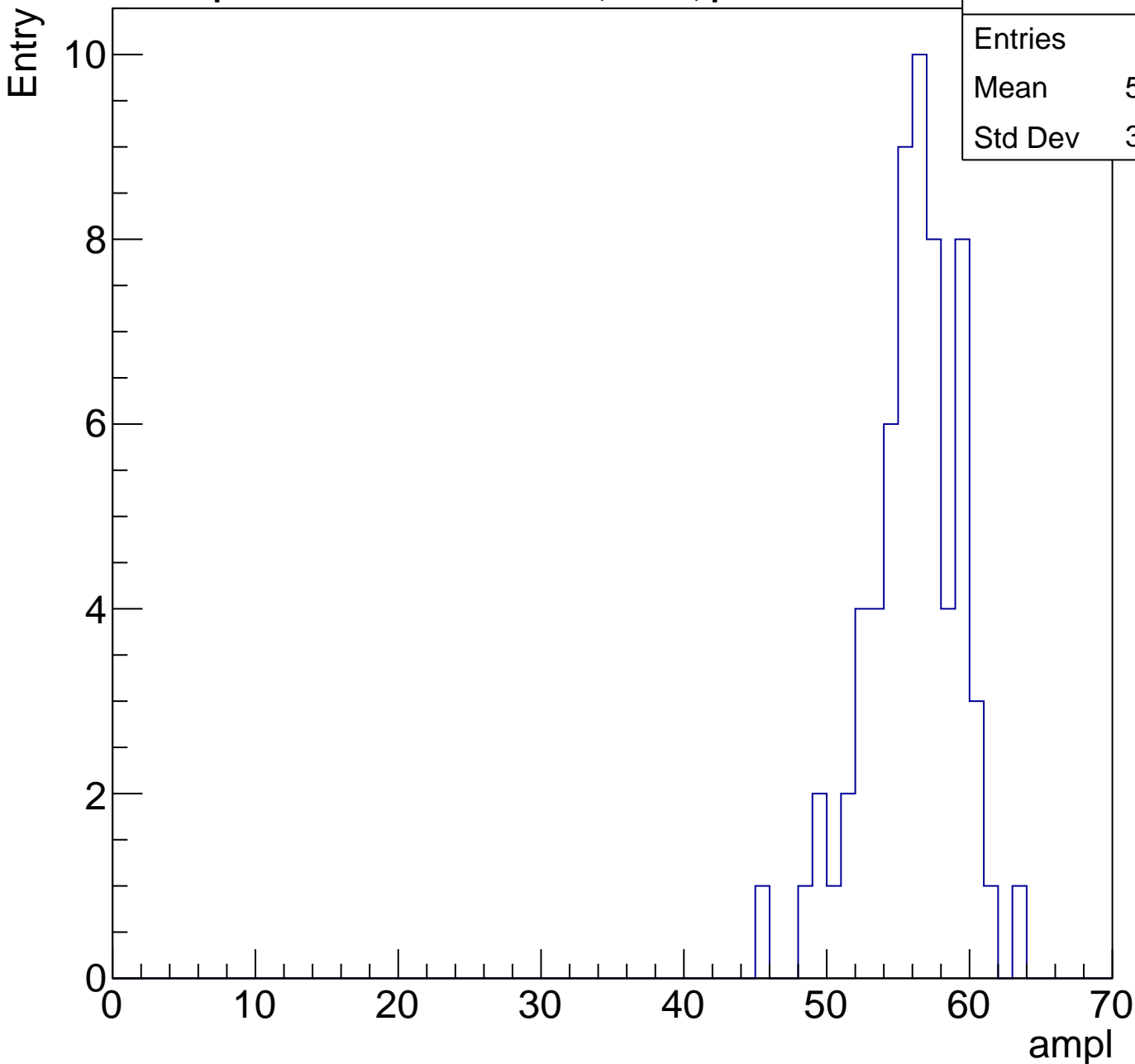
ampl



# B1L000S, U1-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	55.48
Std Dev	3.287

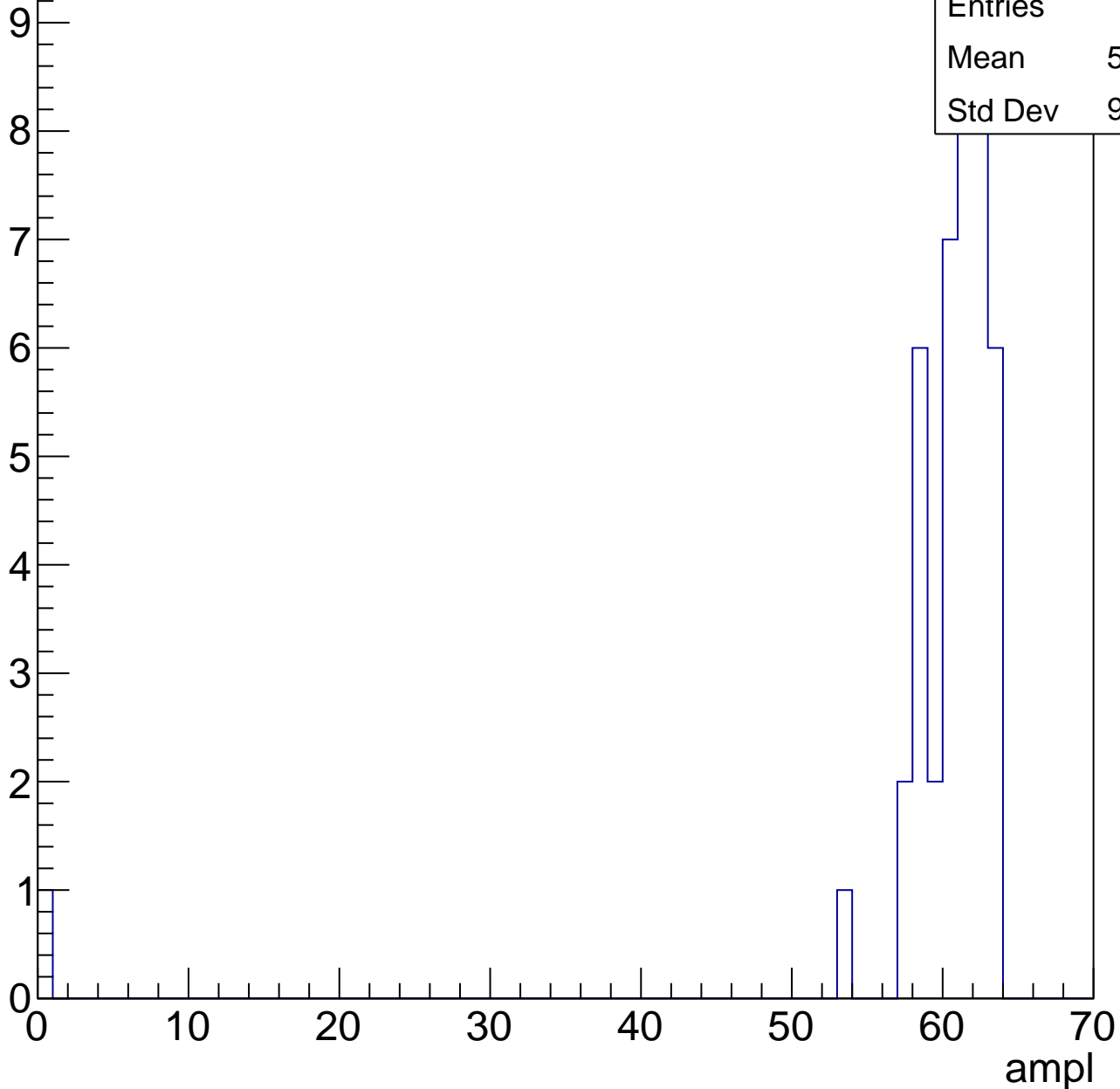


# B1L000S, U1-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	58.98
Std Dev	9.445

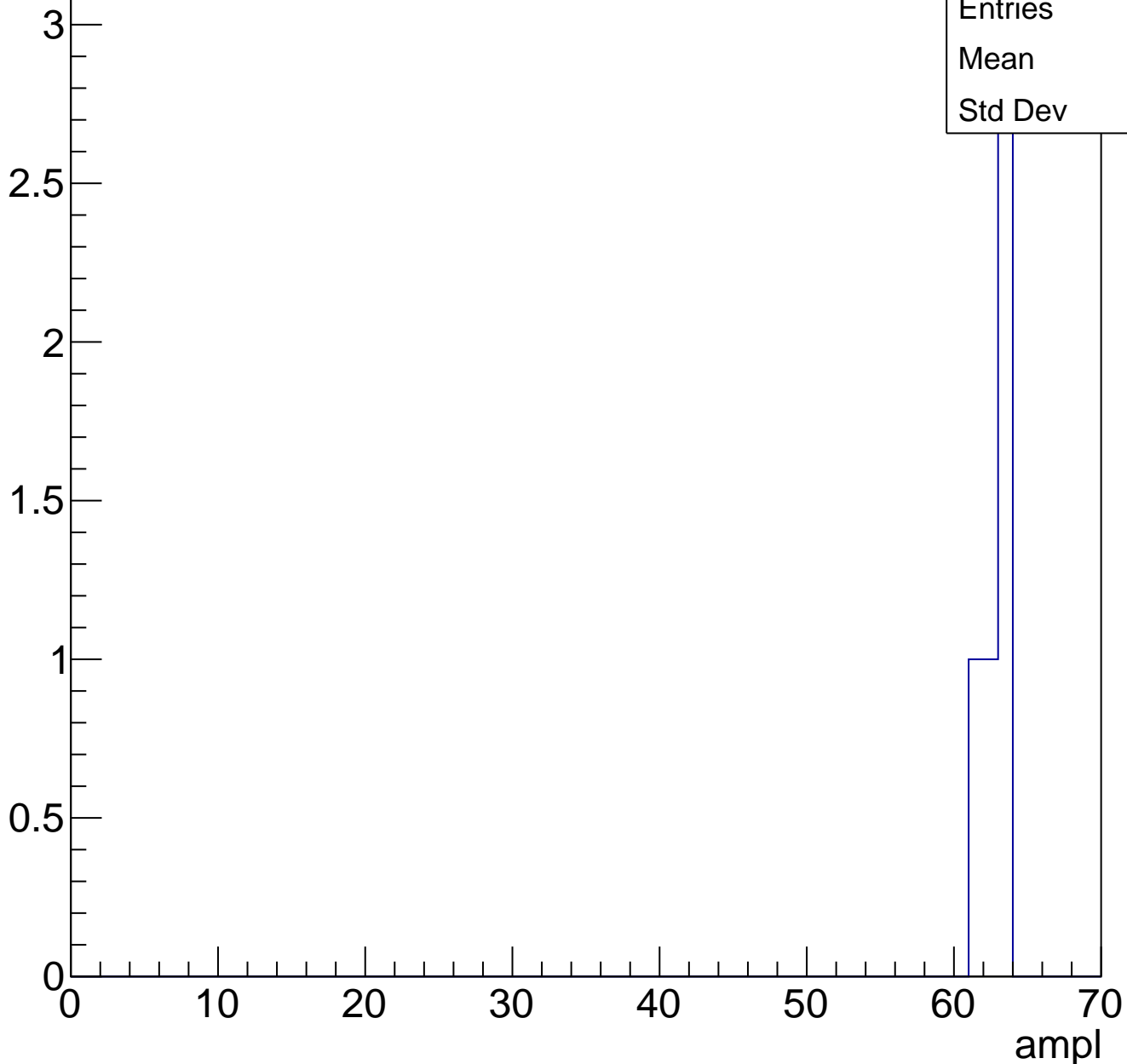




# B1L000S, U1-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch33, adc0

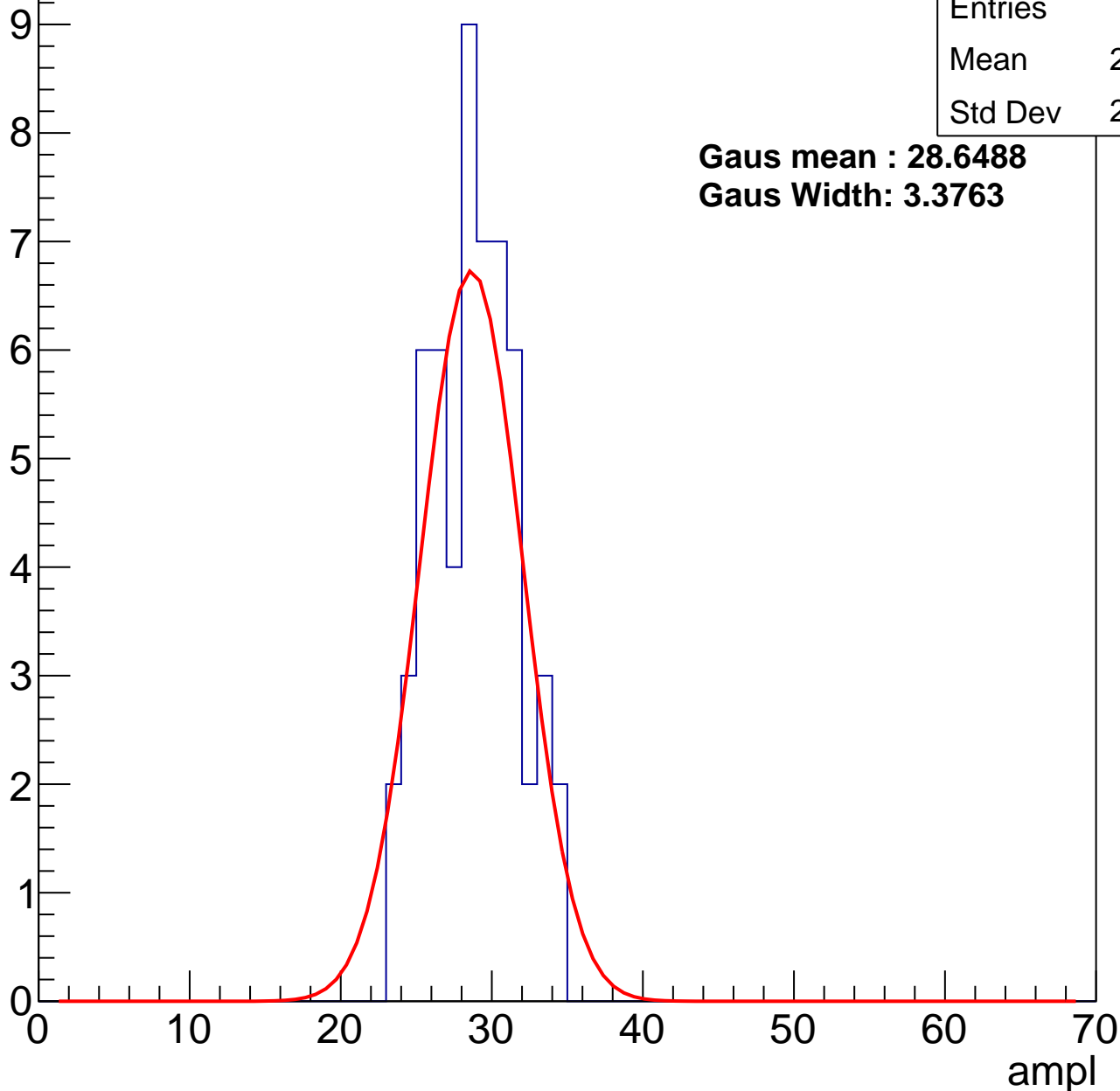
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28.32
Std Dev	2.786

**Gaus mean : 28.6488**

**Gaus Width: 3.3763**



# B1L000S, U1-ch33, adc1

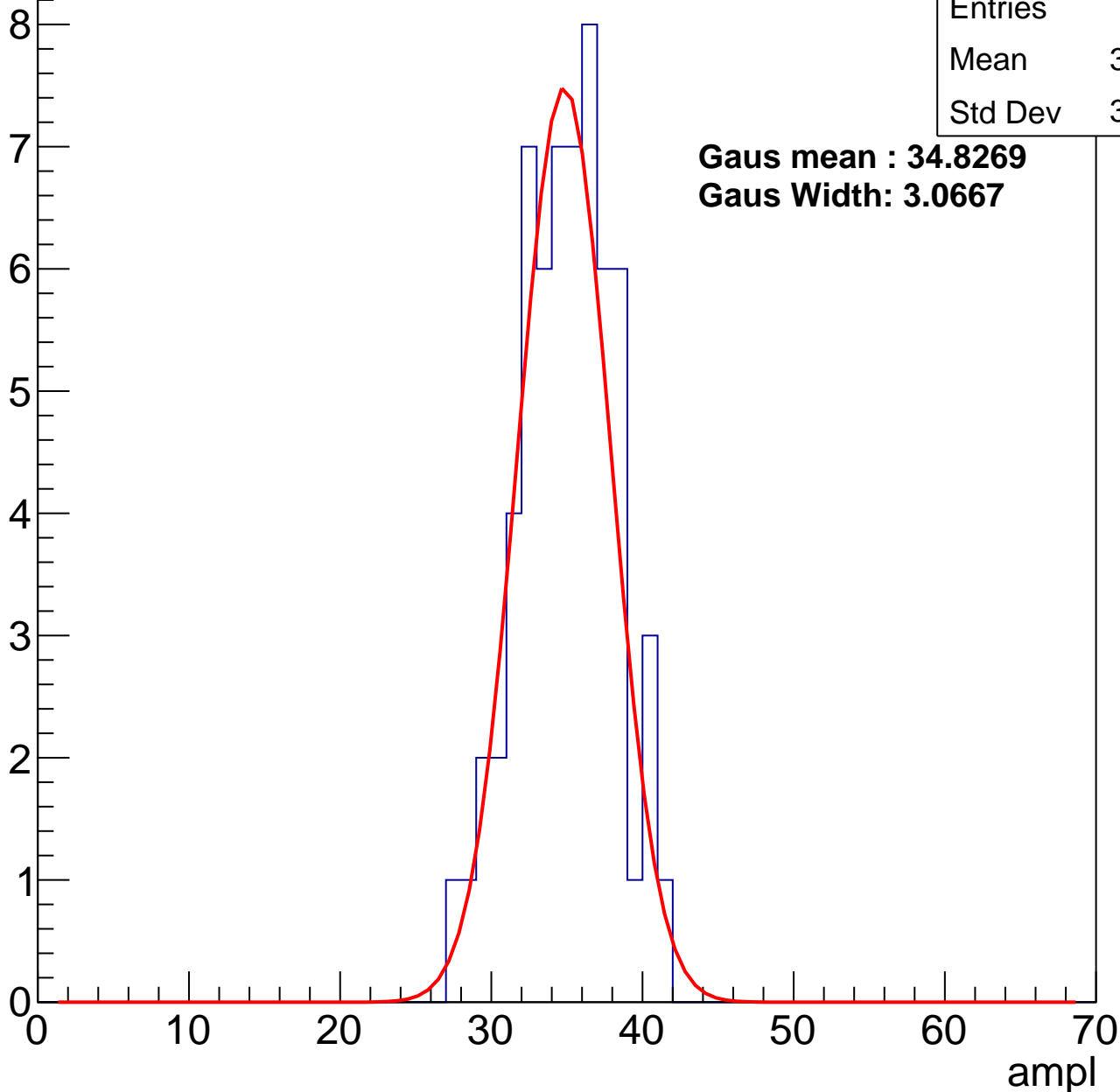
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	34.52
Std Dev	3.089

**Gaus mean : 34.8269**

**Gaus Width: 3.0667**



# B1L000S, U1-ch33, adc2

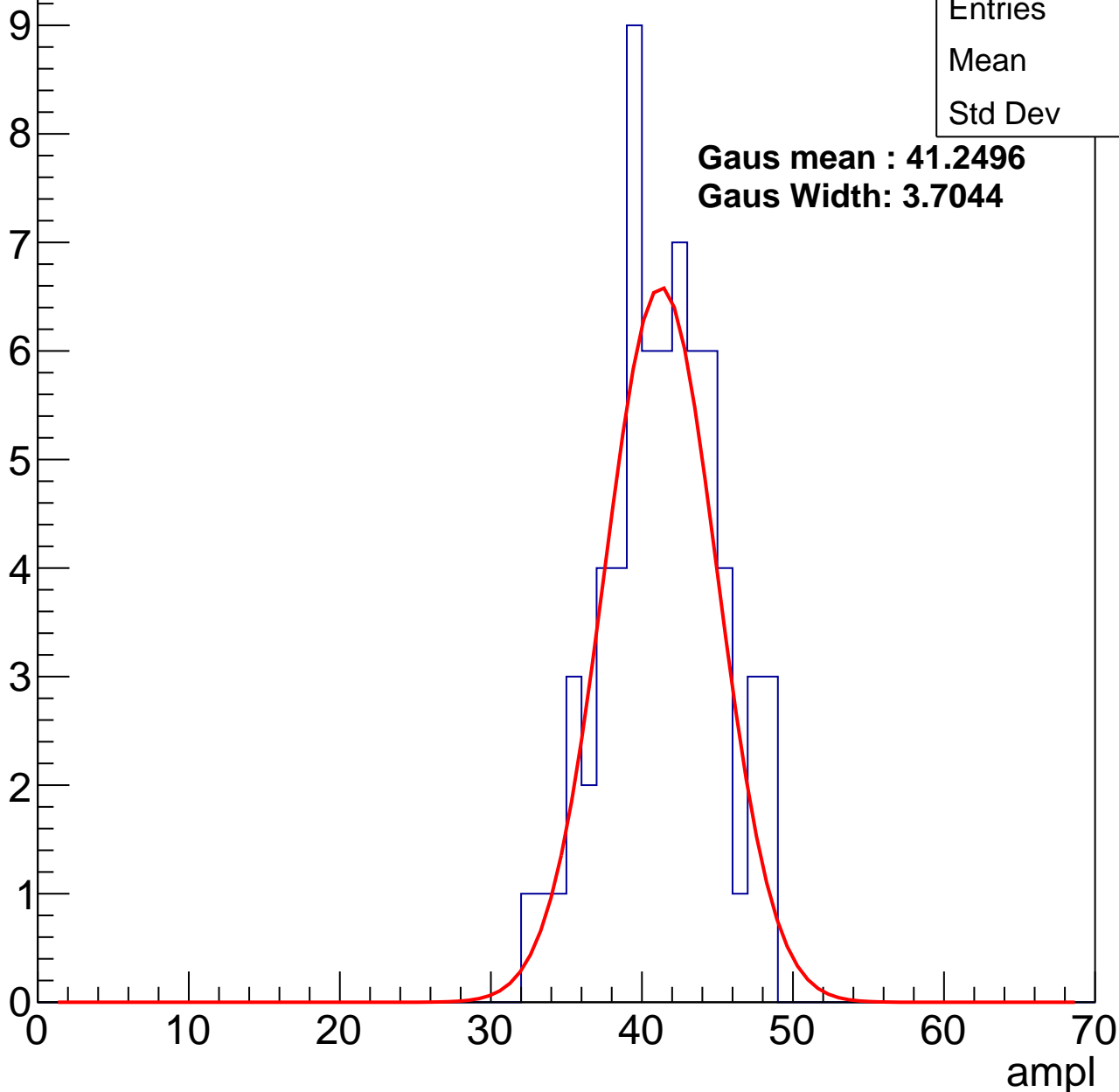
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	40.9
Std Dev	3.71

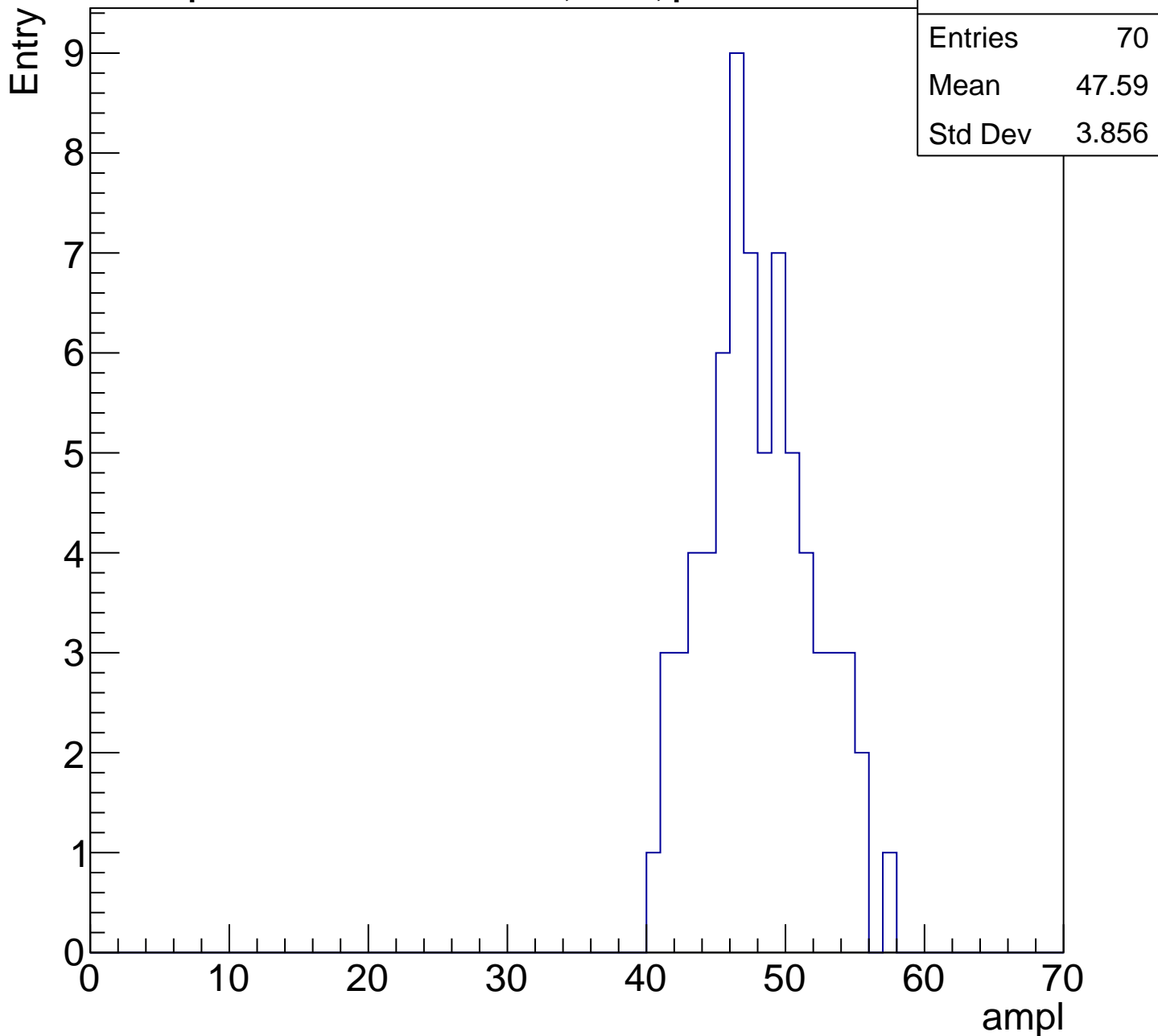
**Gaus mean : 41.2496**

**Gaus Width: 3.7044**



# B1L000S, U1-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

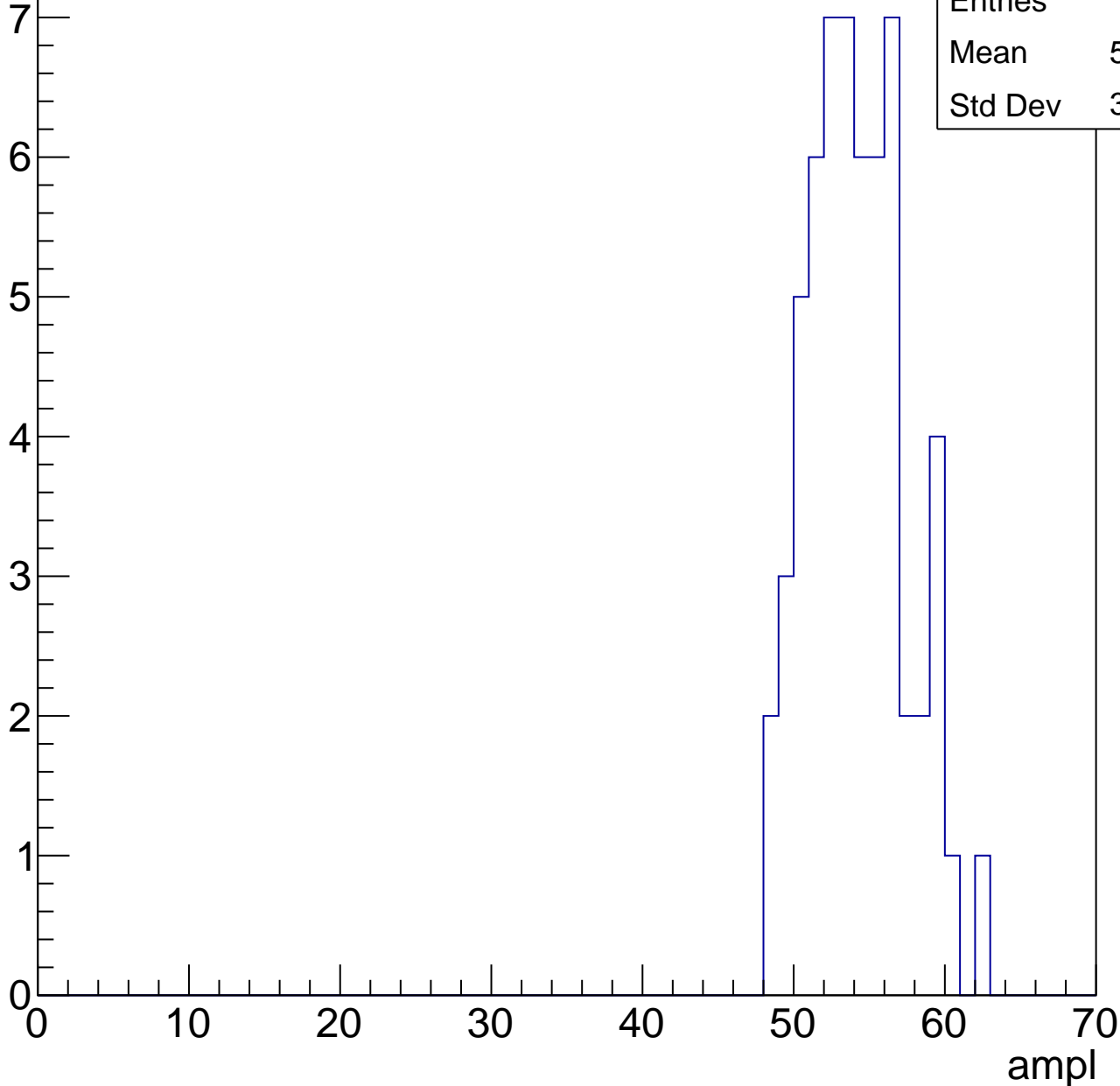


# B1L000S, U1-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	53.69
Std Dev	3.174

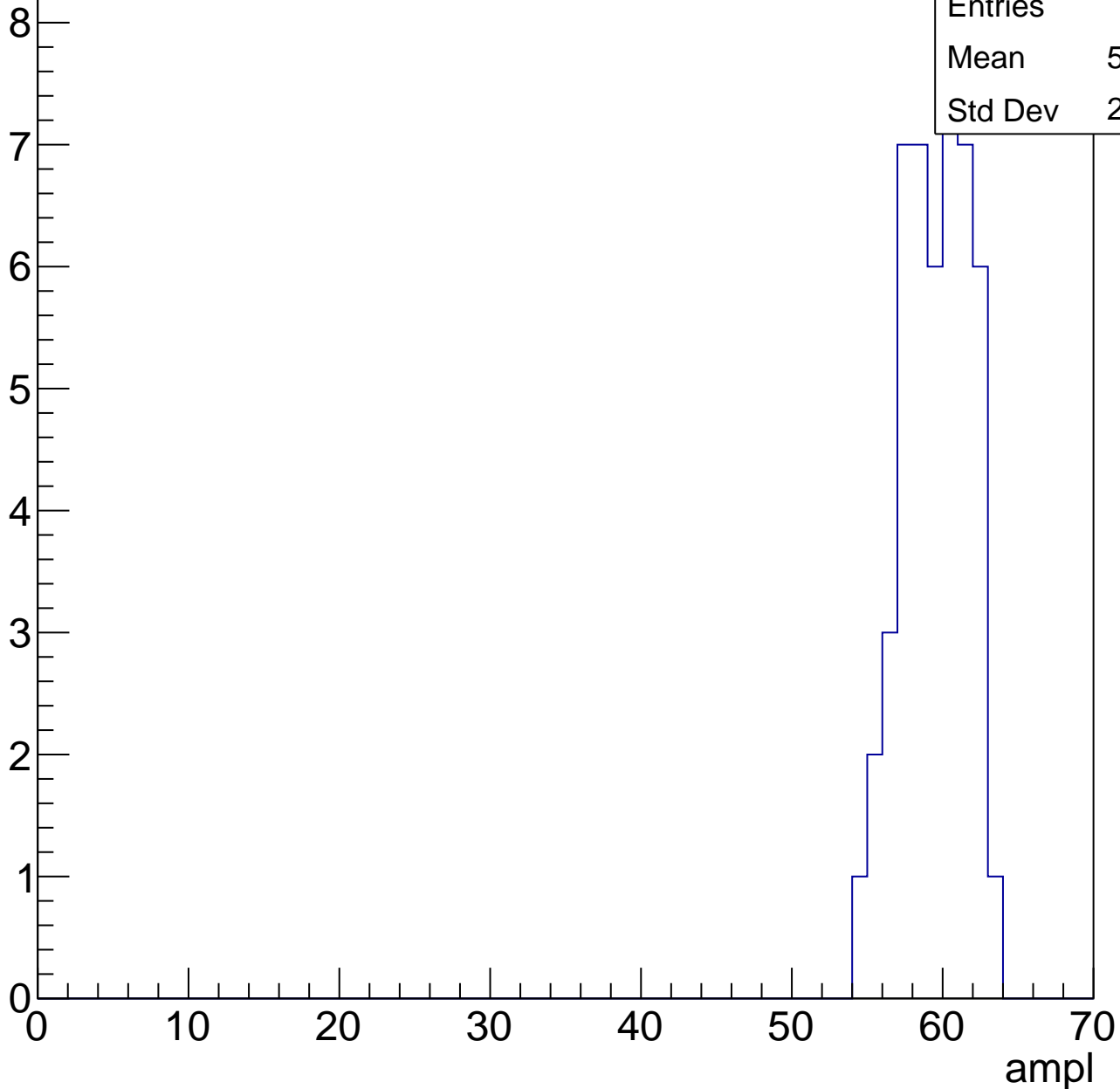


# B1L000S, U1-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

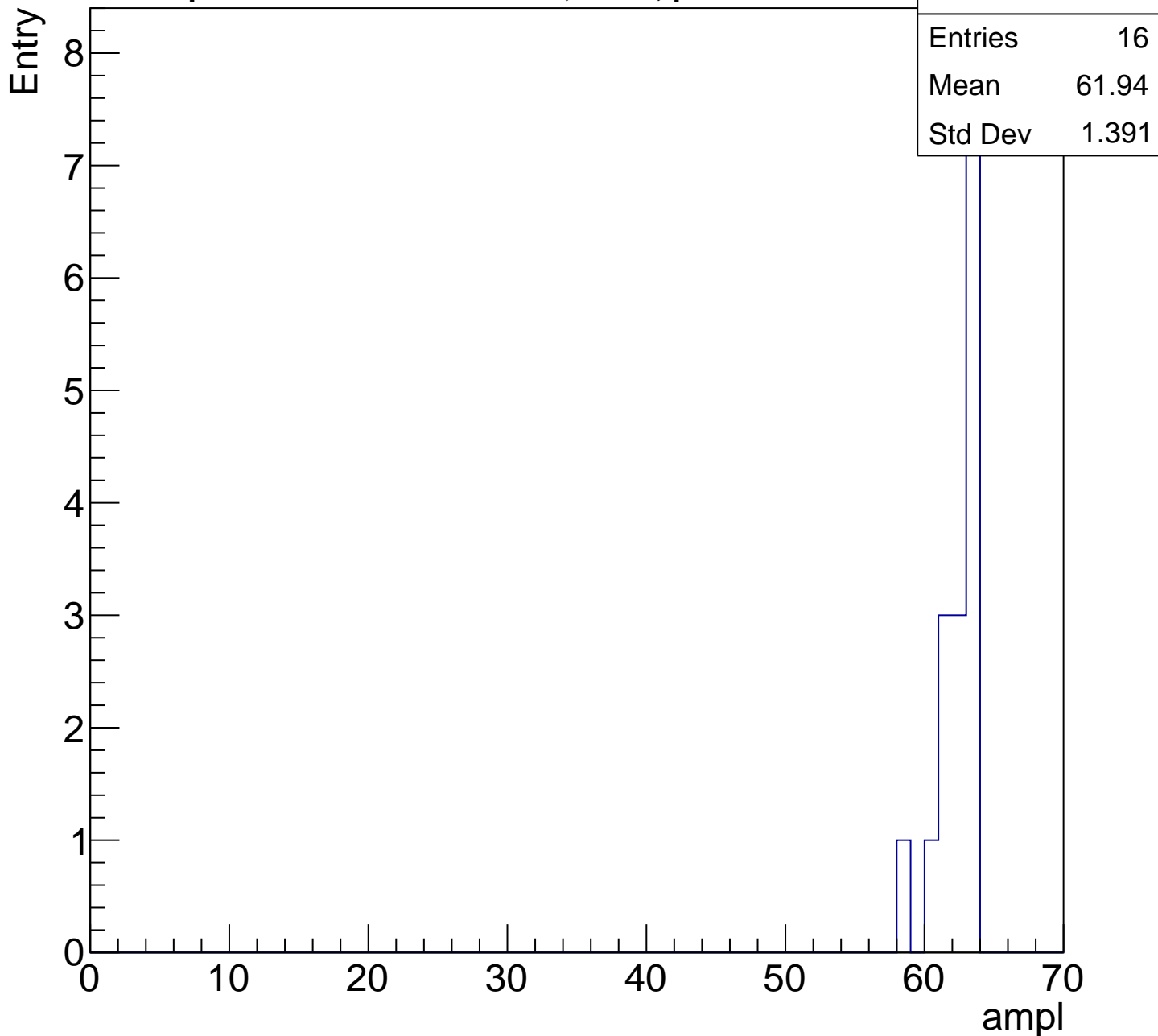
Entry

Entries	48
Mean	59.02
Std Dev	2.165



# B1L000S, U1-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3





# B1L000S, U1-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U1-ch34, adc0

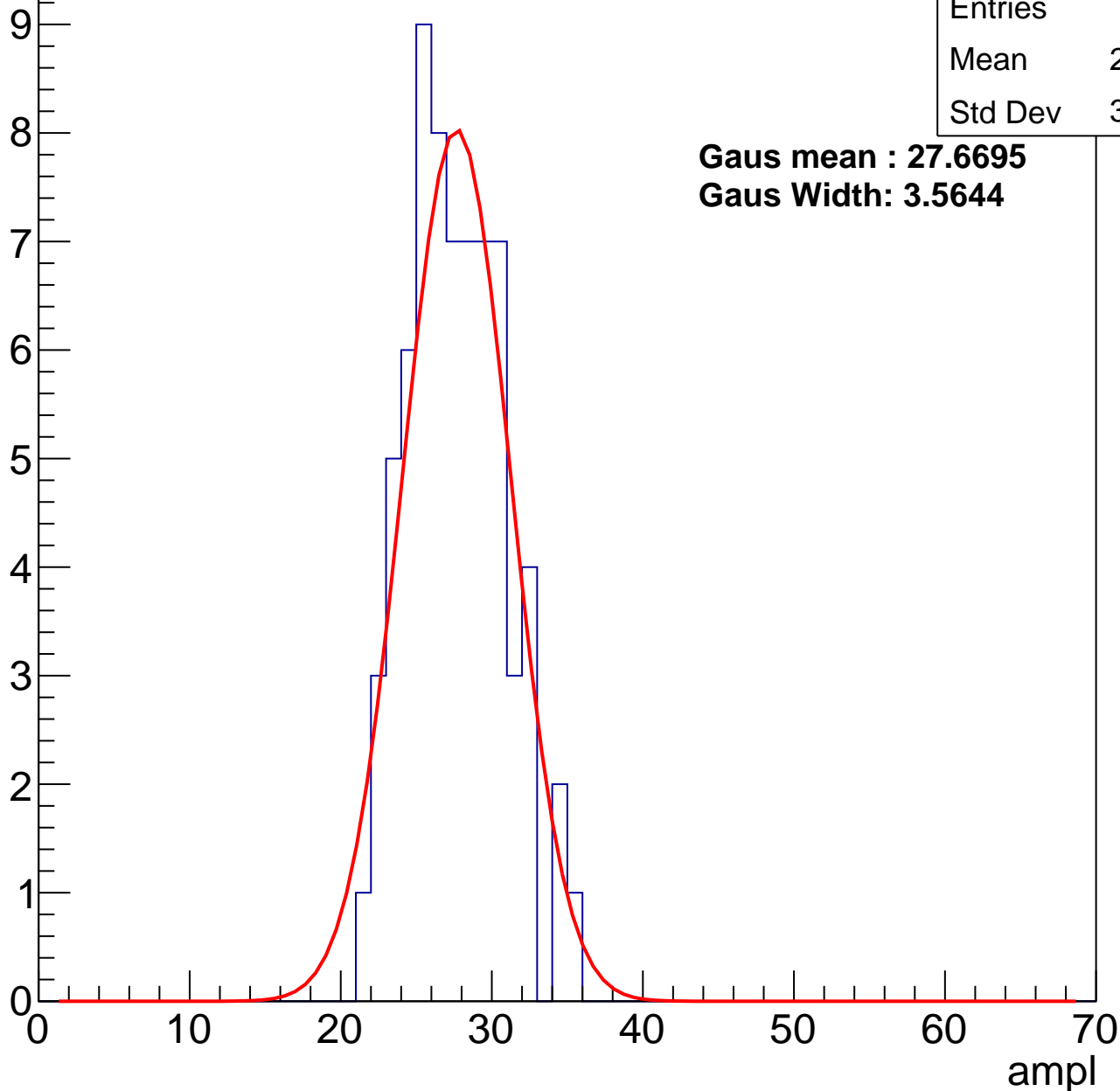
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	27.16
Std Dev	3.152

**Gaus mean : 27.6695**

**Gaus Width: 3.5644**



# B1L000S, U1-ch34, adc1

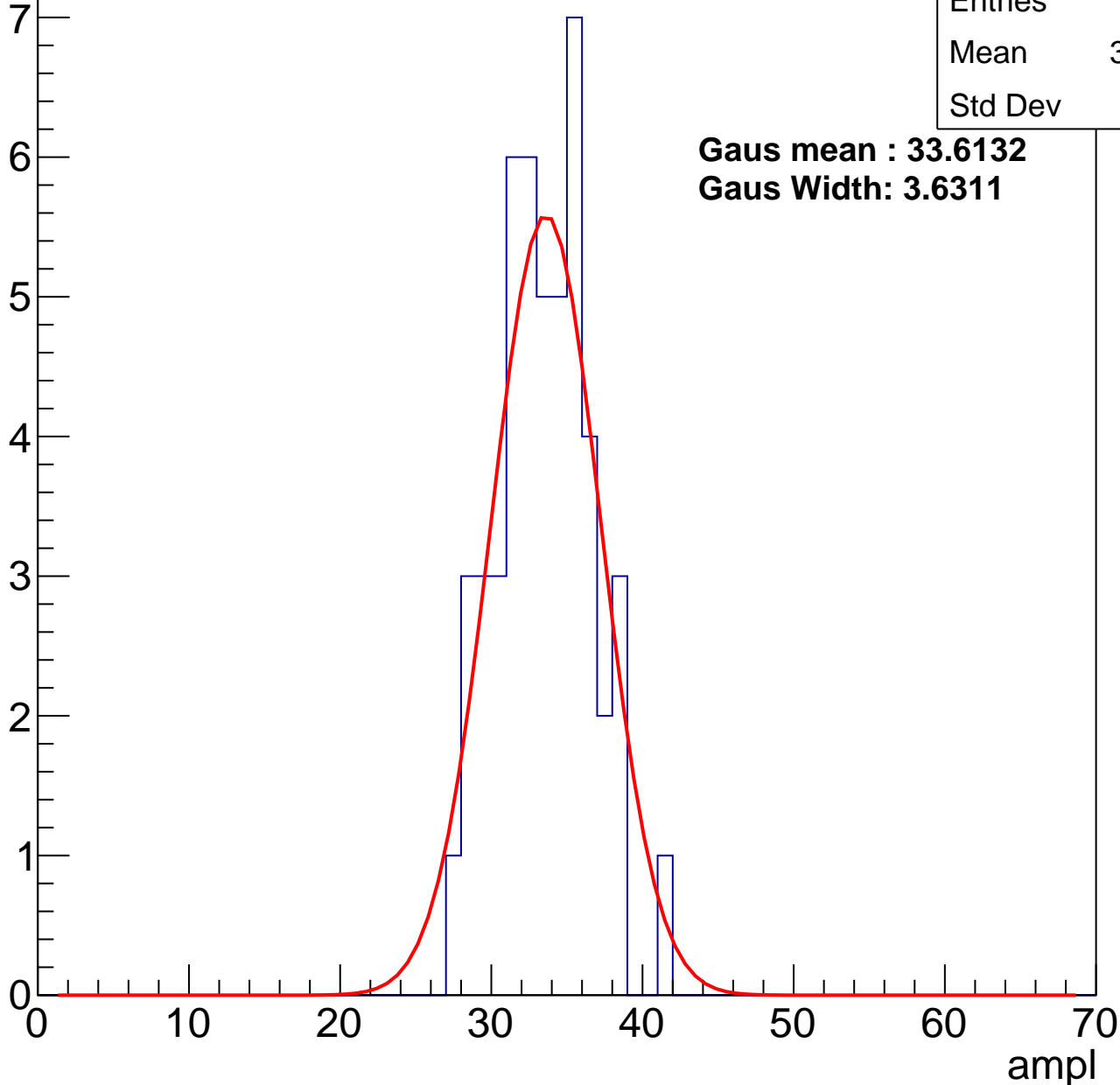
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	33.04
Std Dev	3.05

**Gaus mean : 33.6132**

**Gaus Width: 3.6311**



# B1L000S, U1-ch34, adc2

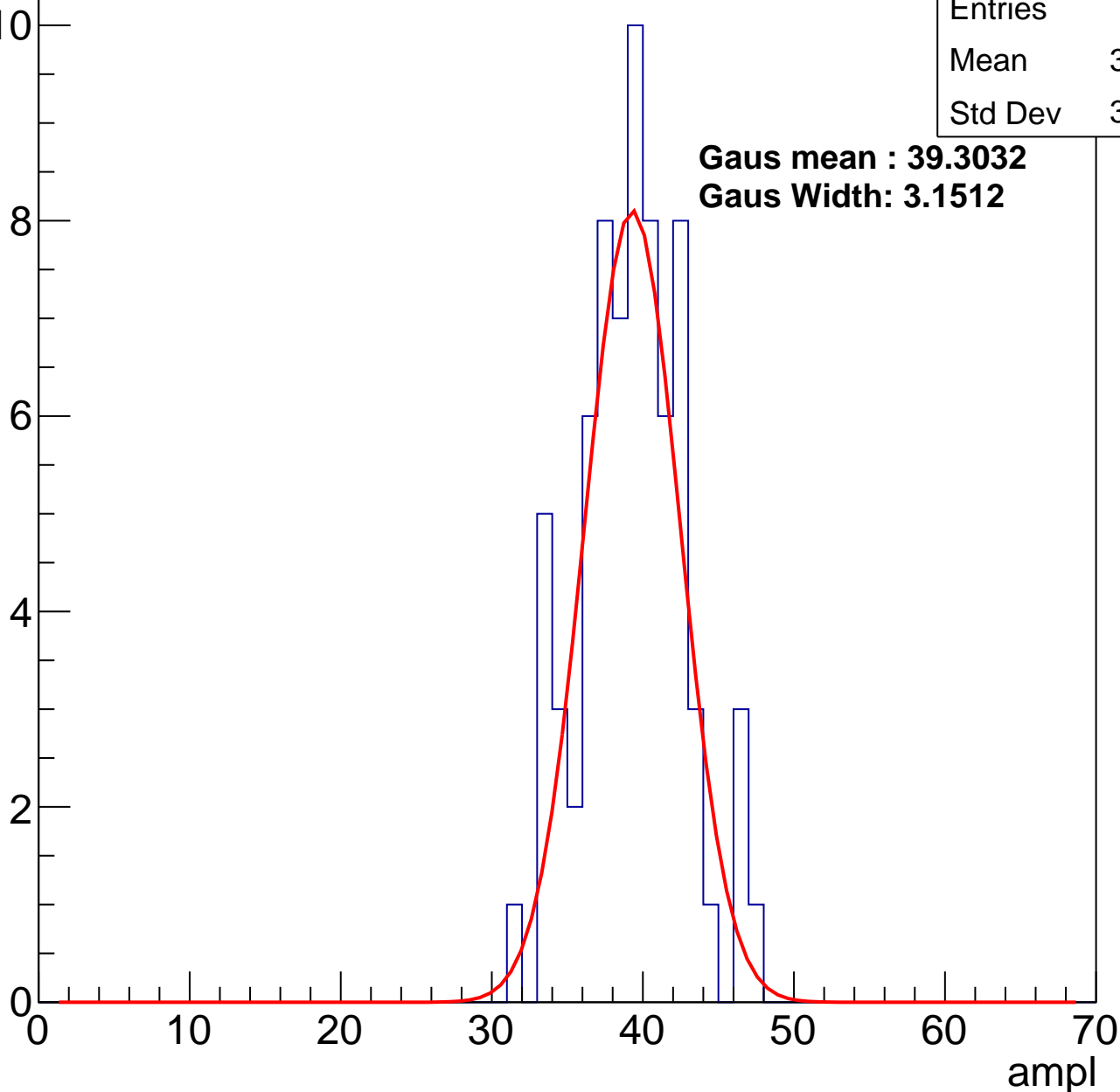
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	38.83
Std Dev	3.395

**Gaus mean : 39.3032**

**Gaus Width: 3.1512**

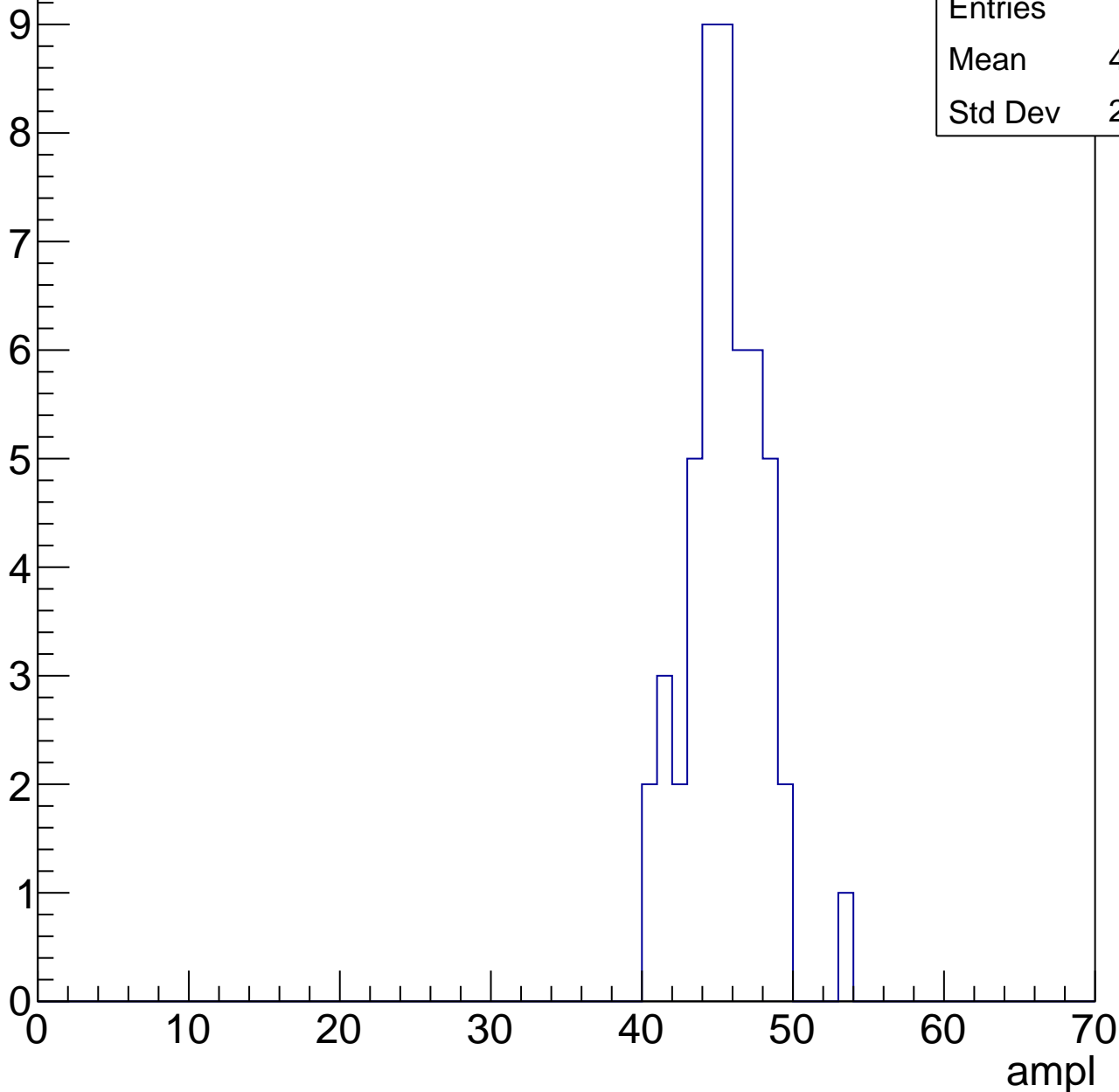


# B1L000S, U1-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	45.04
Std Dev	2.514

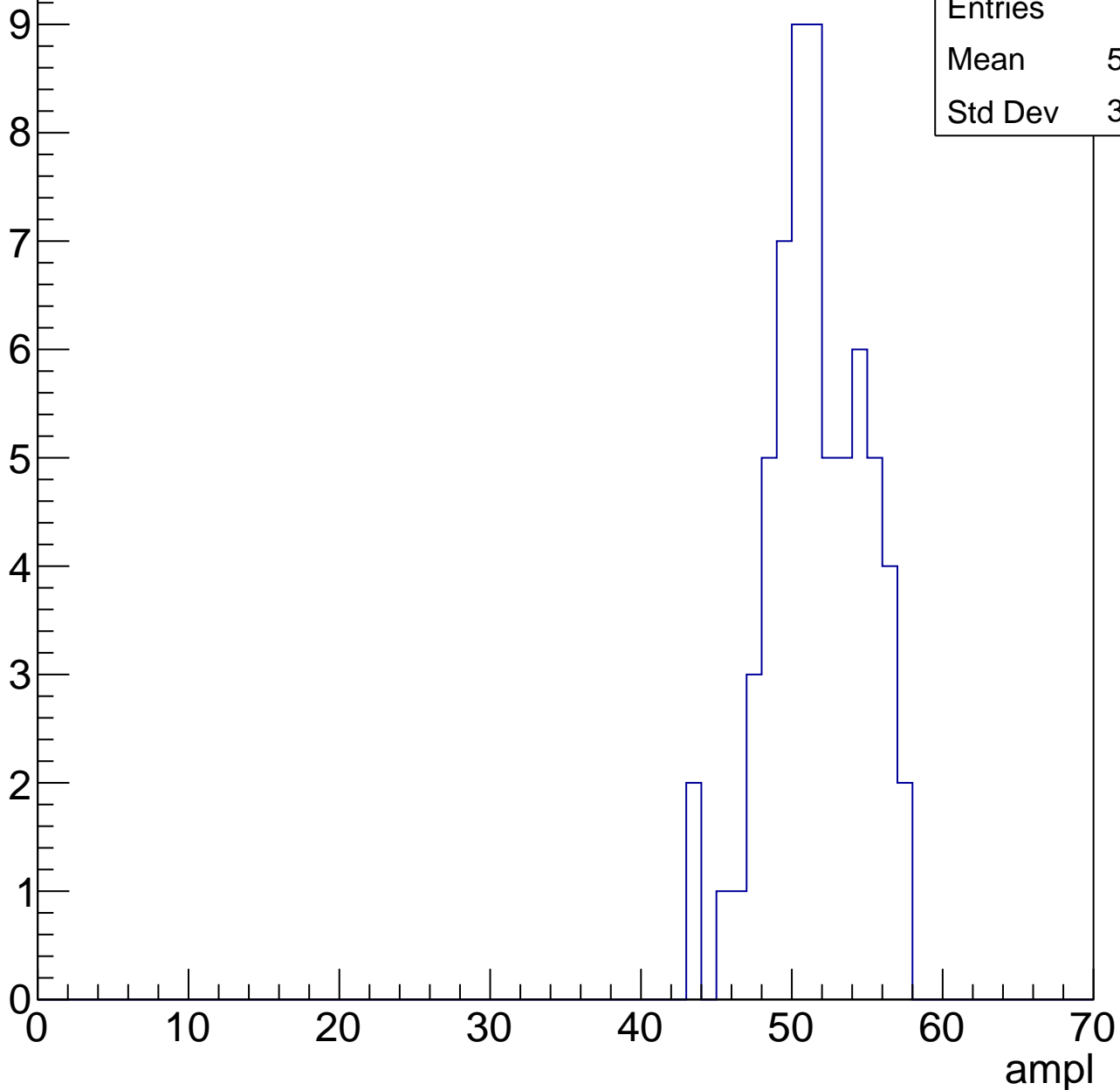


# B1L000S, U1-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	51.12
Std Dev	3.184



# B1L000S, U1-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 67

Mean 56.82

Std Dev 3.11

8

6

4

2

0

0

10

20

30

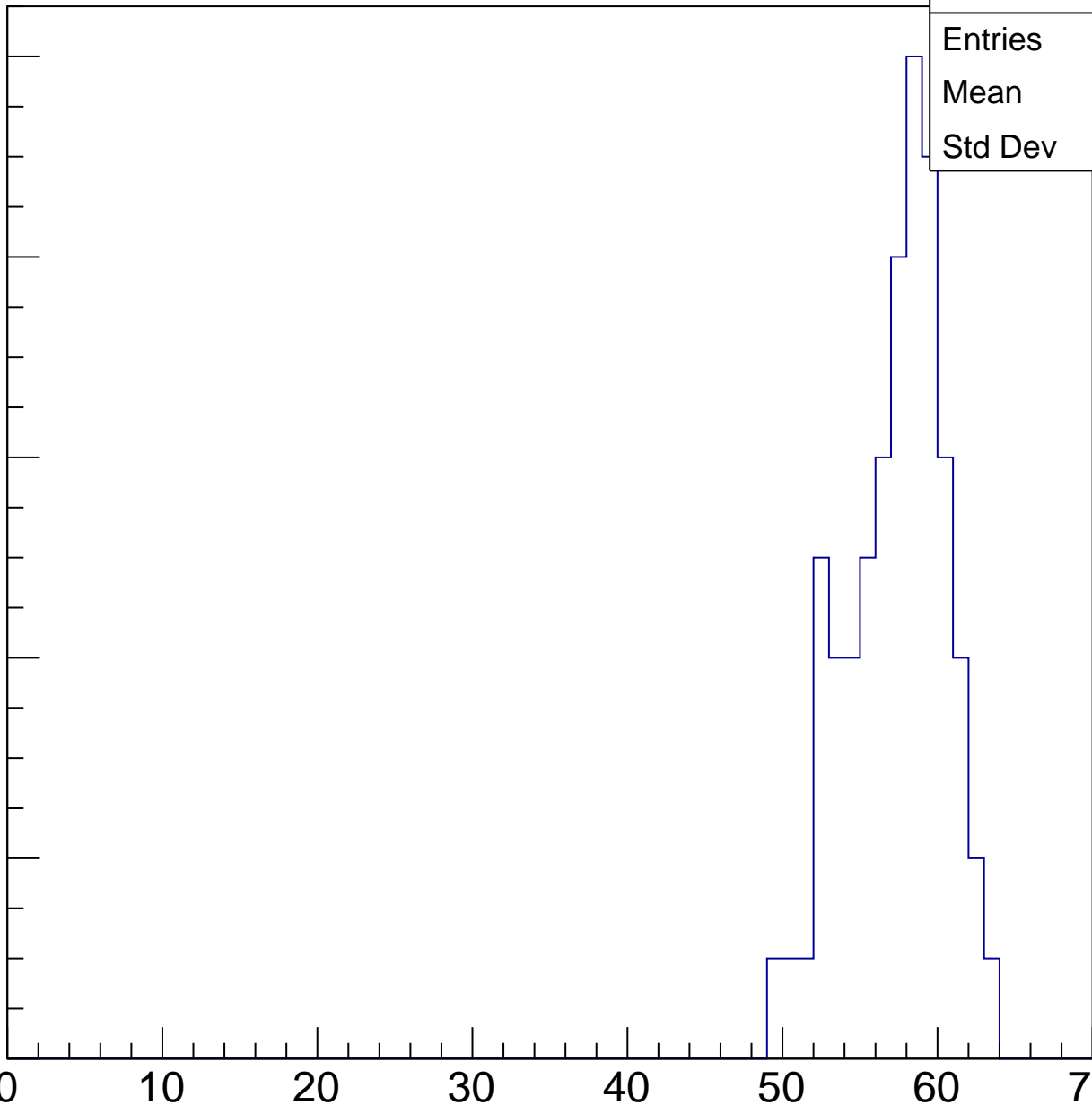
40

50

60

70

ampl

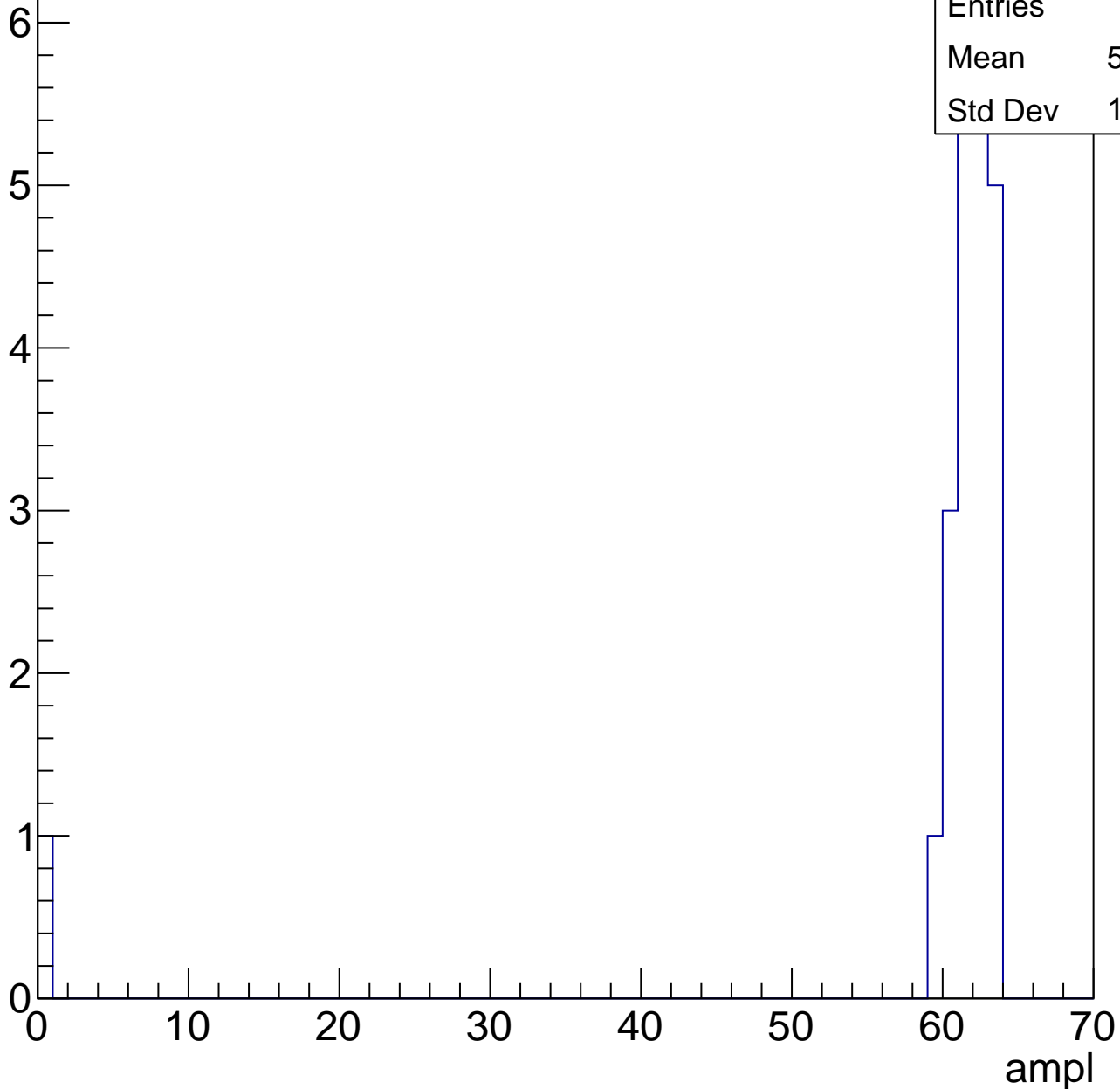


# B1L000S, U1-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.73
Std Dev	12.86

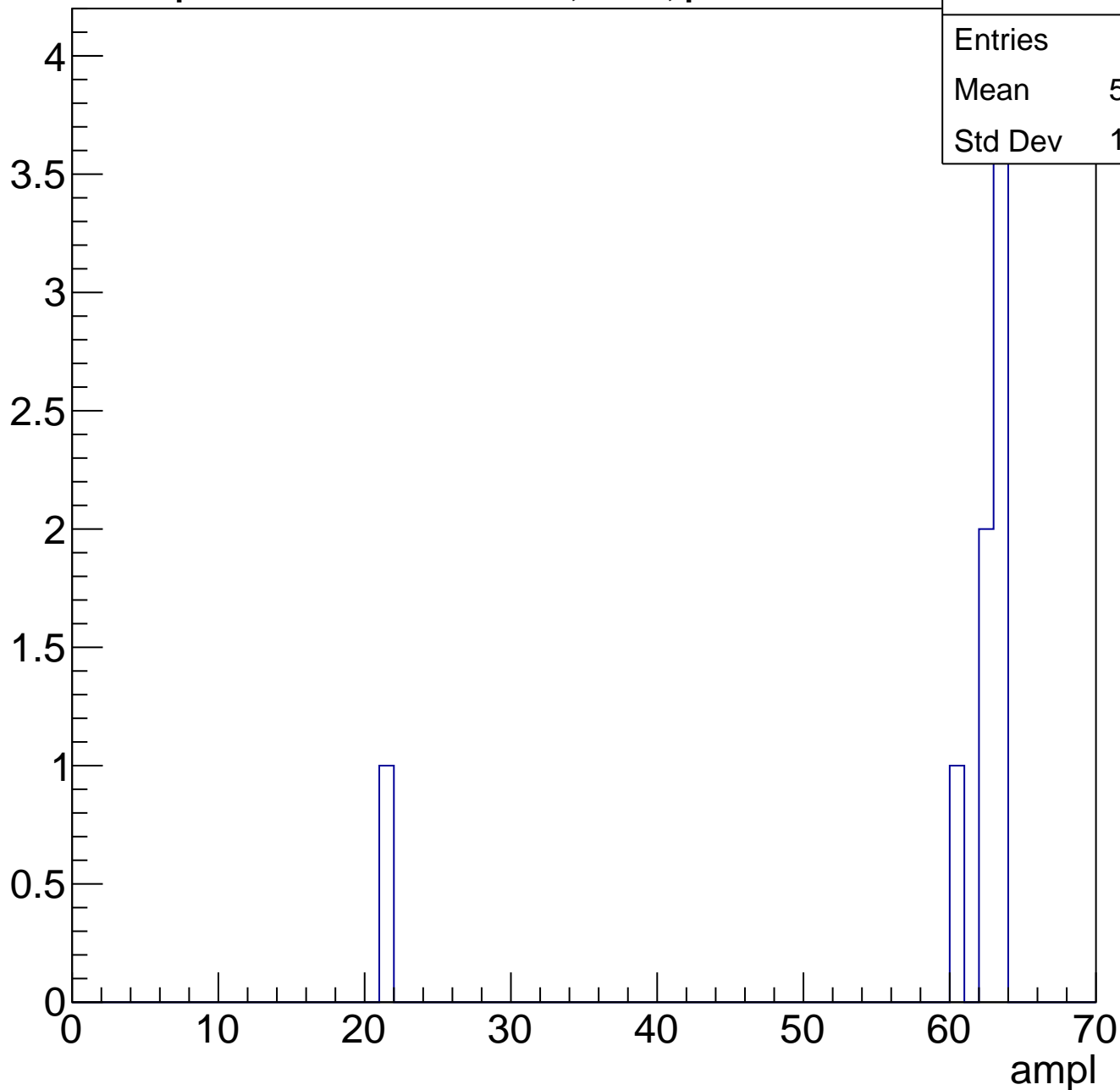




# B1L000S, U1-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch35, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	28.55
Std Dev	5.79

**Gaus mean : 30.1531**

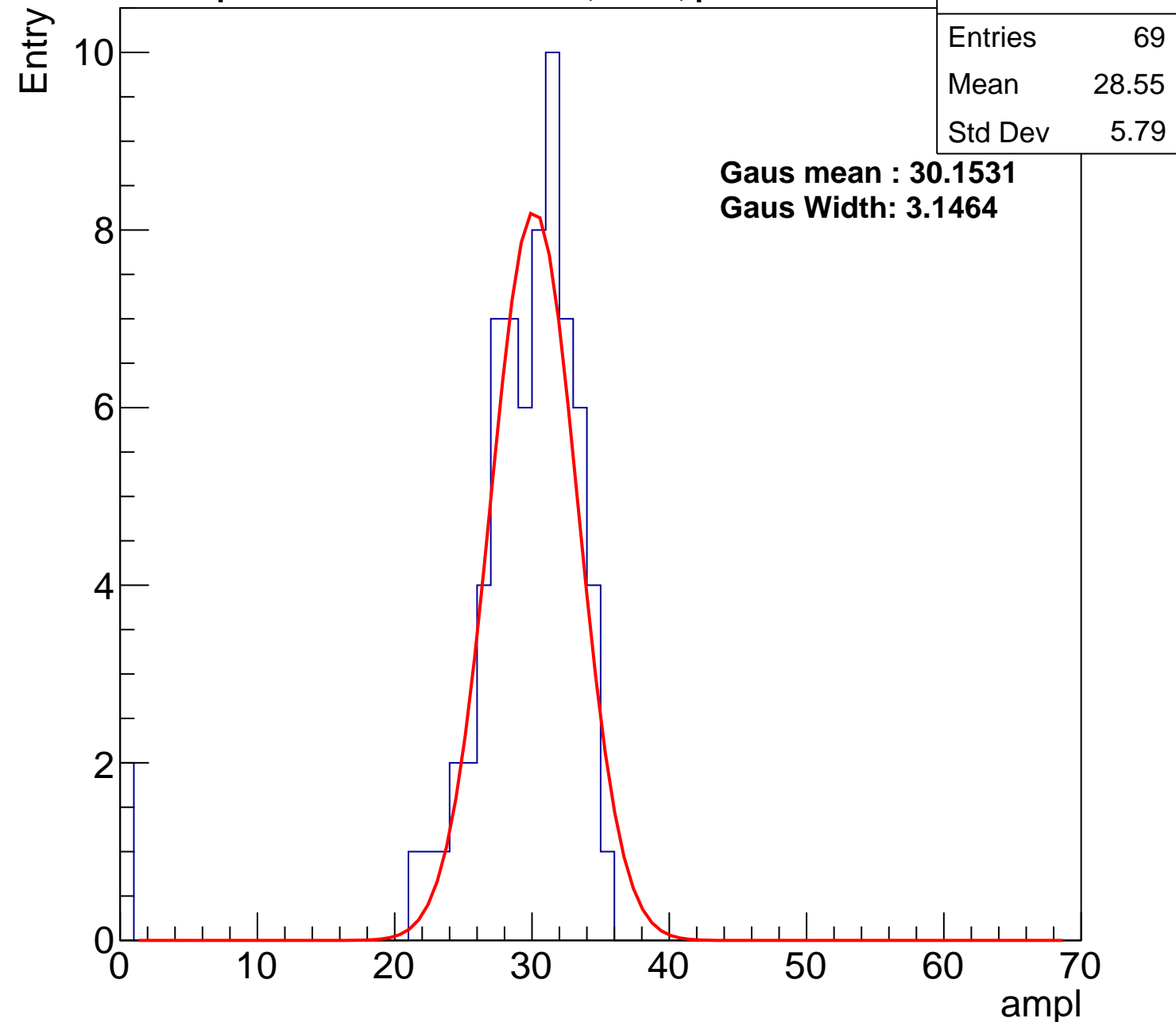
**Gaus Width: 3.1464**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch35, adc1

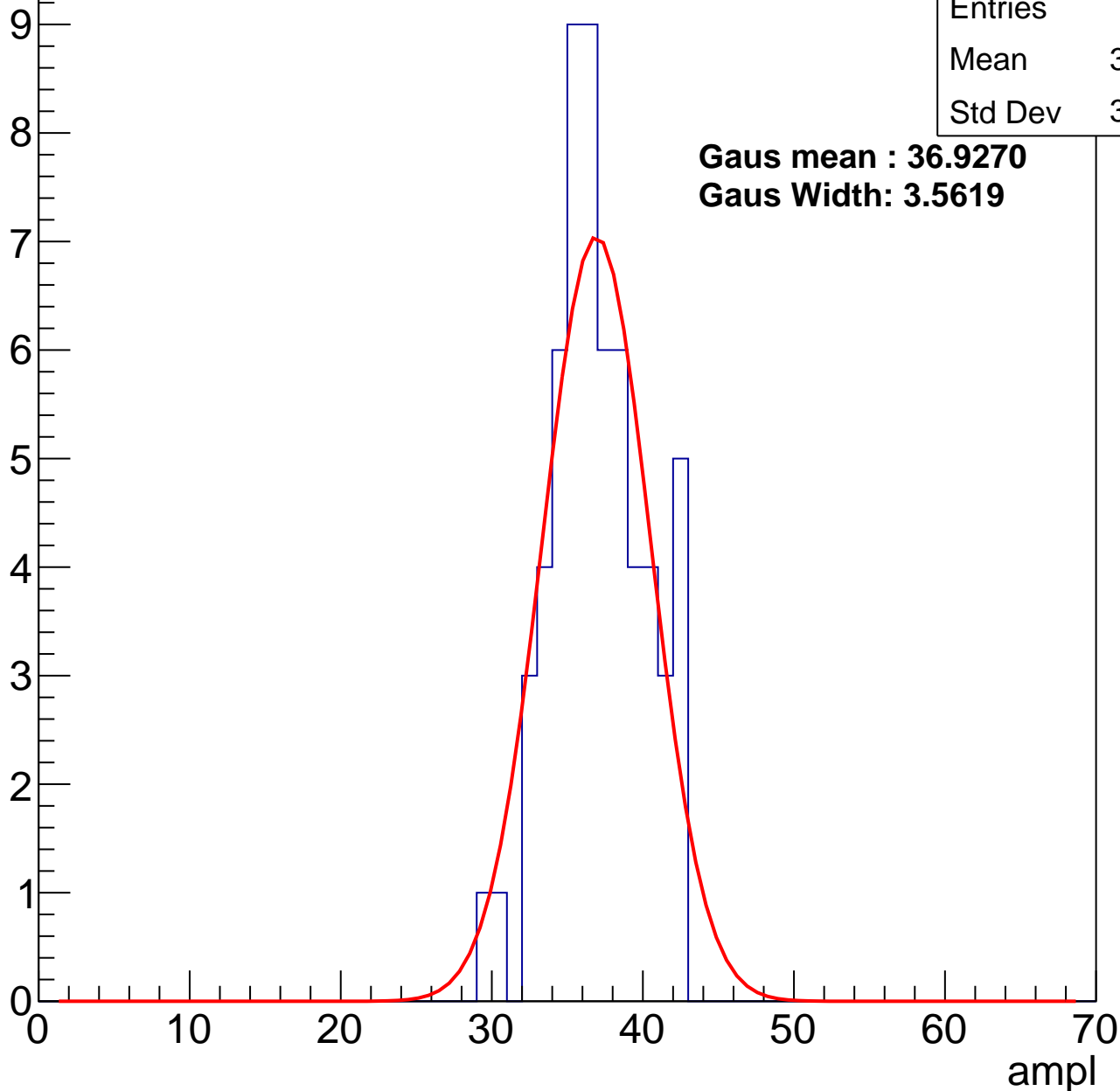
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	36.54
Std Dev	3.055

**Gaus mean : 36.9270**

**Gaus Width: 3.5619**



# B1L000S, U1-ch35, adc2

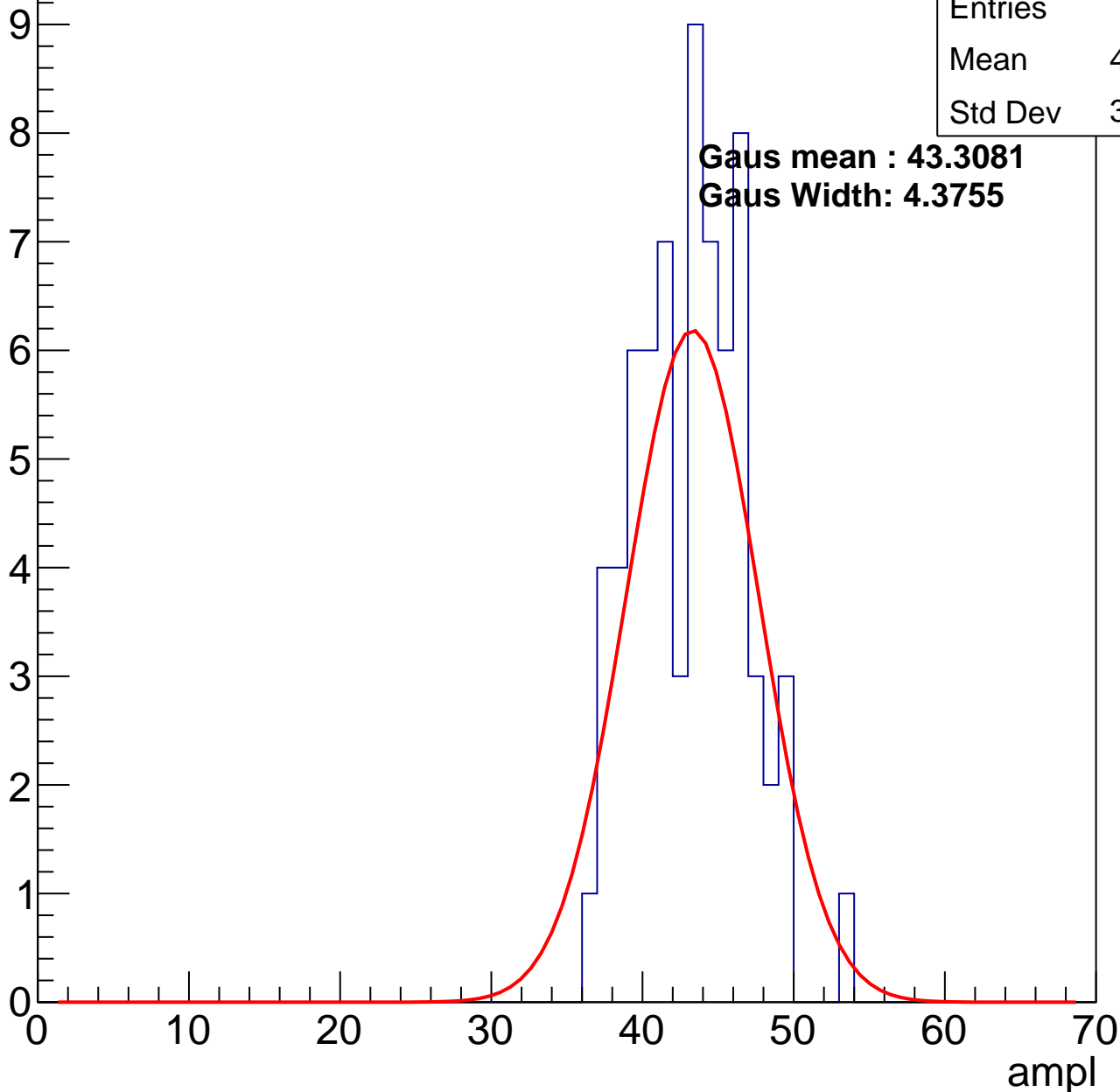
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	42.76
Std Dev	3.543

**Gaus mean : 43.3081**

**Gaus Width: 4.3755**

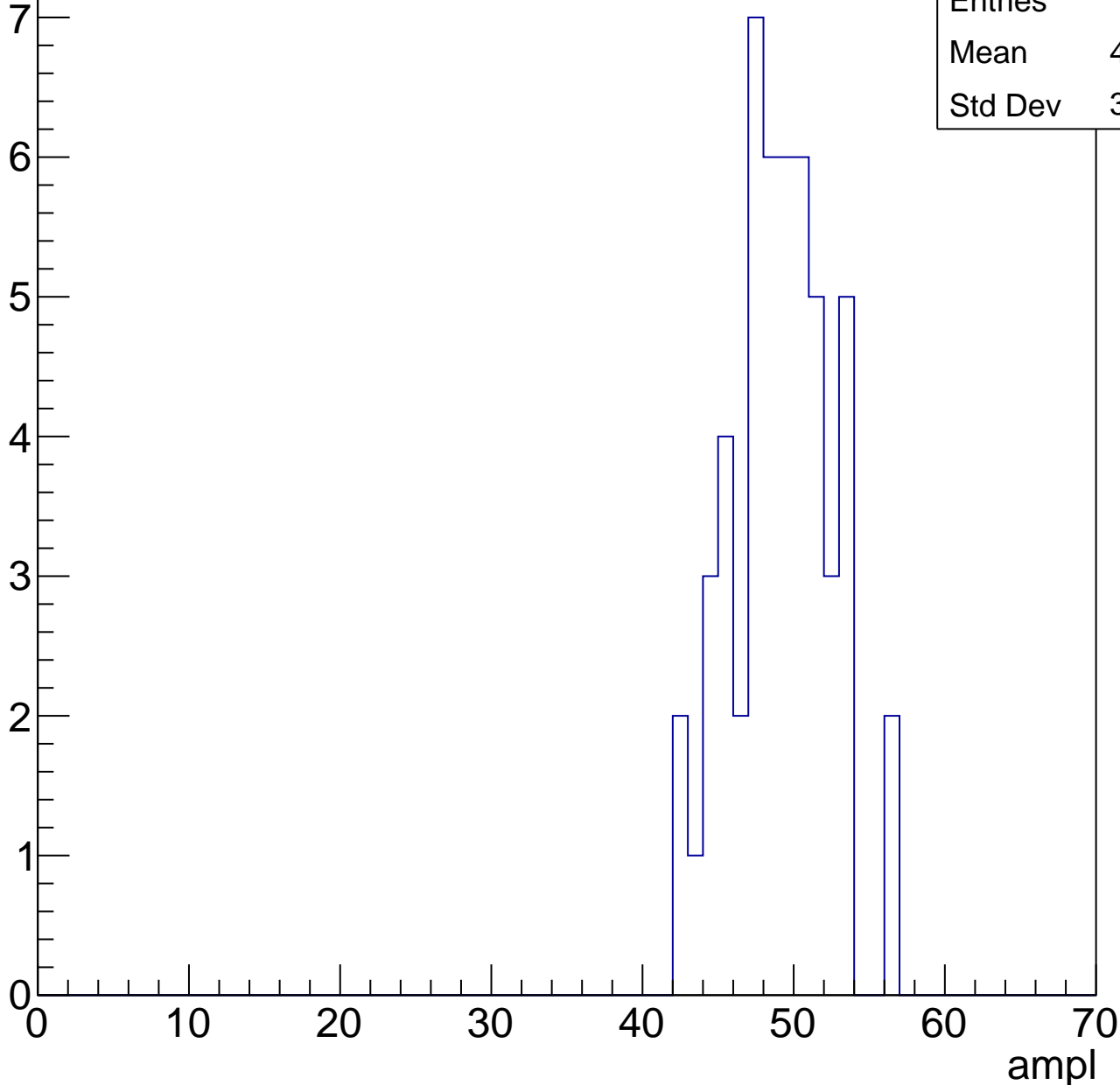


# B1L000S, U1-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	48.65
Std Dev	3.252



# B1L000S, U1-ch35, adc4

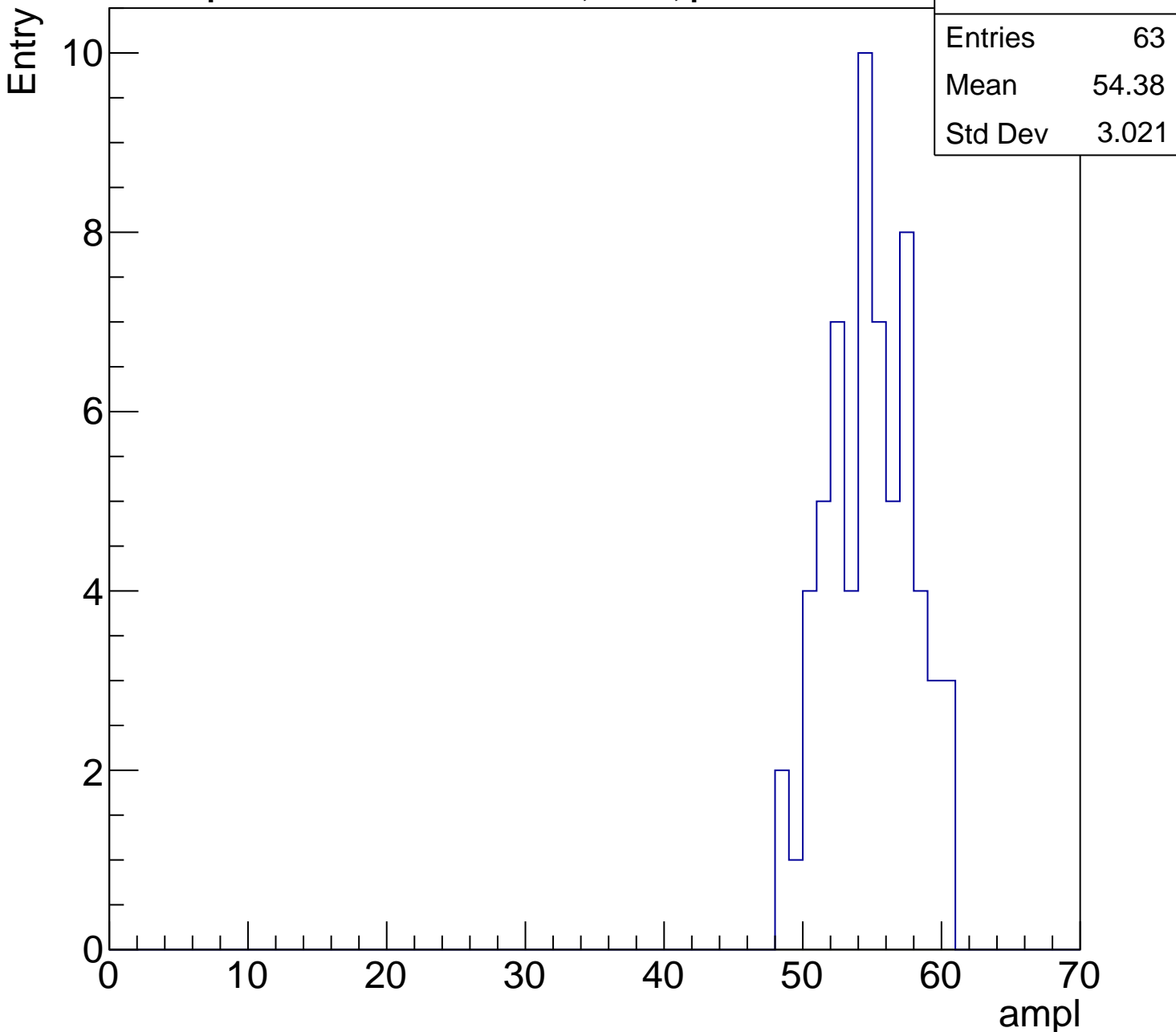
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	54.38
Std Dev	3.021

Entry

10  
8  
6  
4  
2  
0

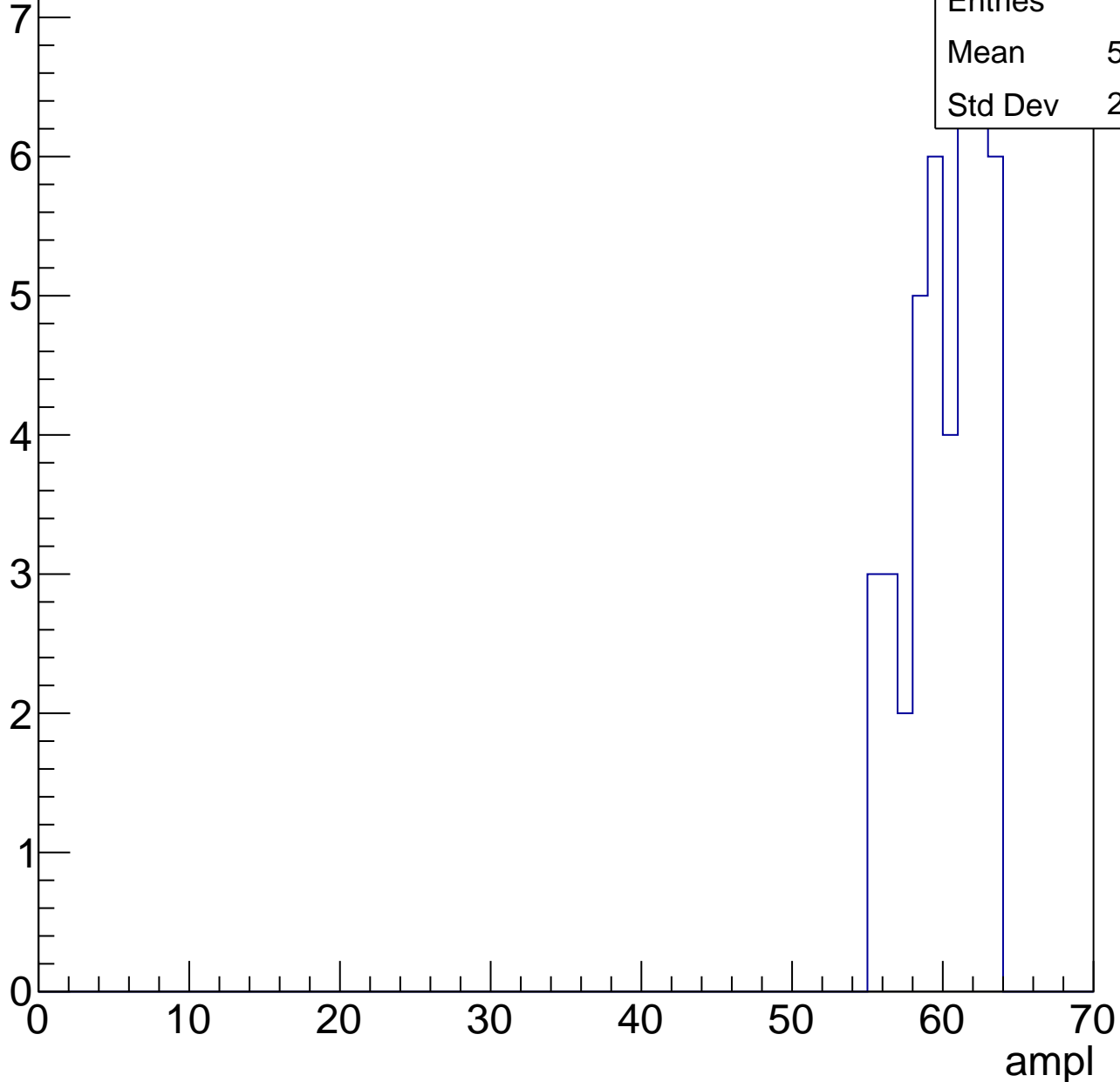
0 10 20 30 40 50 60 70  
ampl



# B1L000S, U1-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

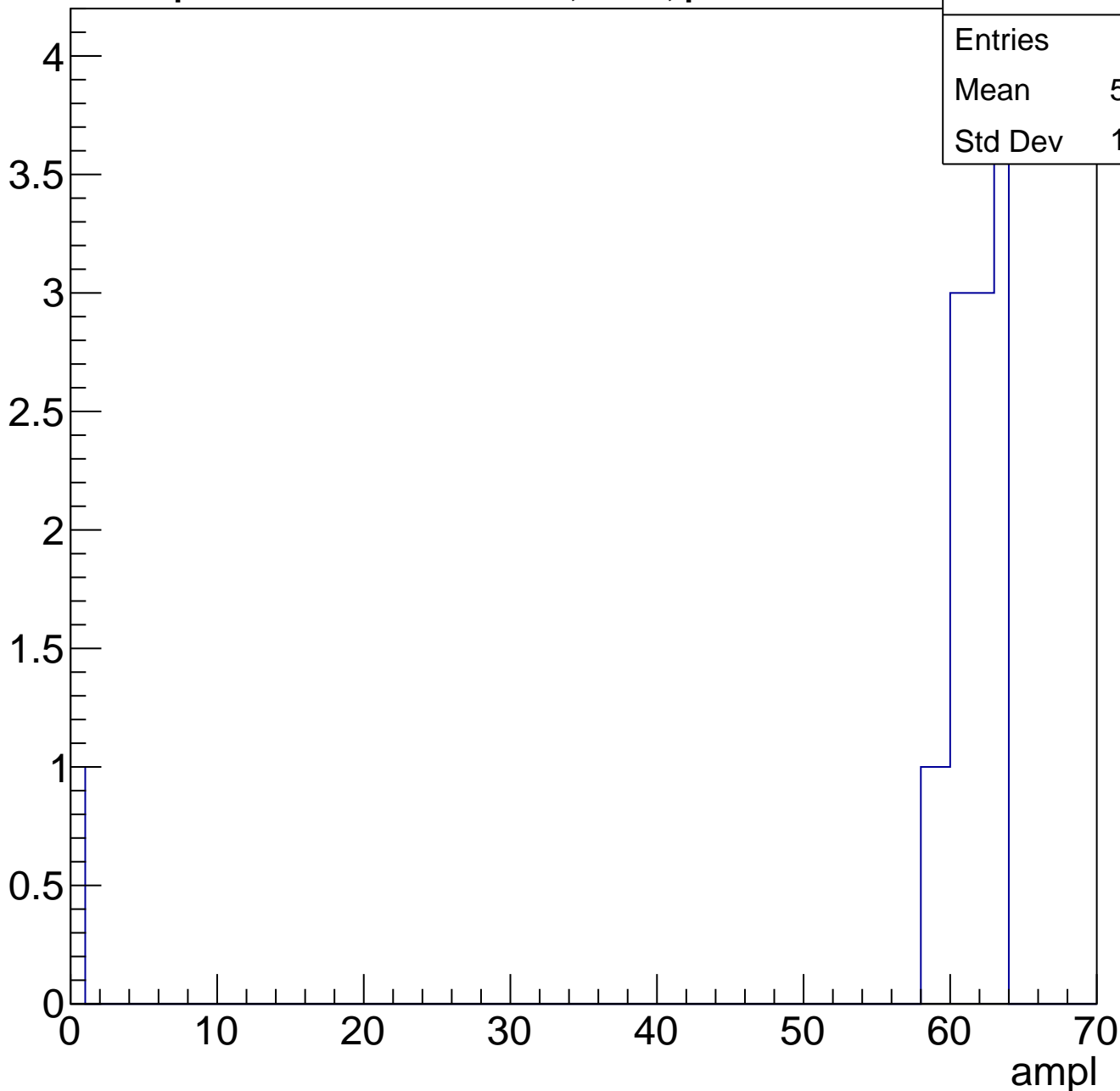


Entries	43
Mean	59.77
Std Dev	2.429

# B1L000S, U1-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U1-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch36, adc0

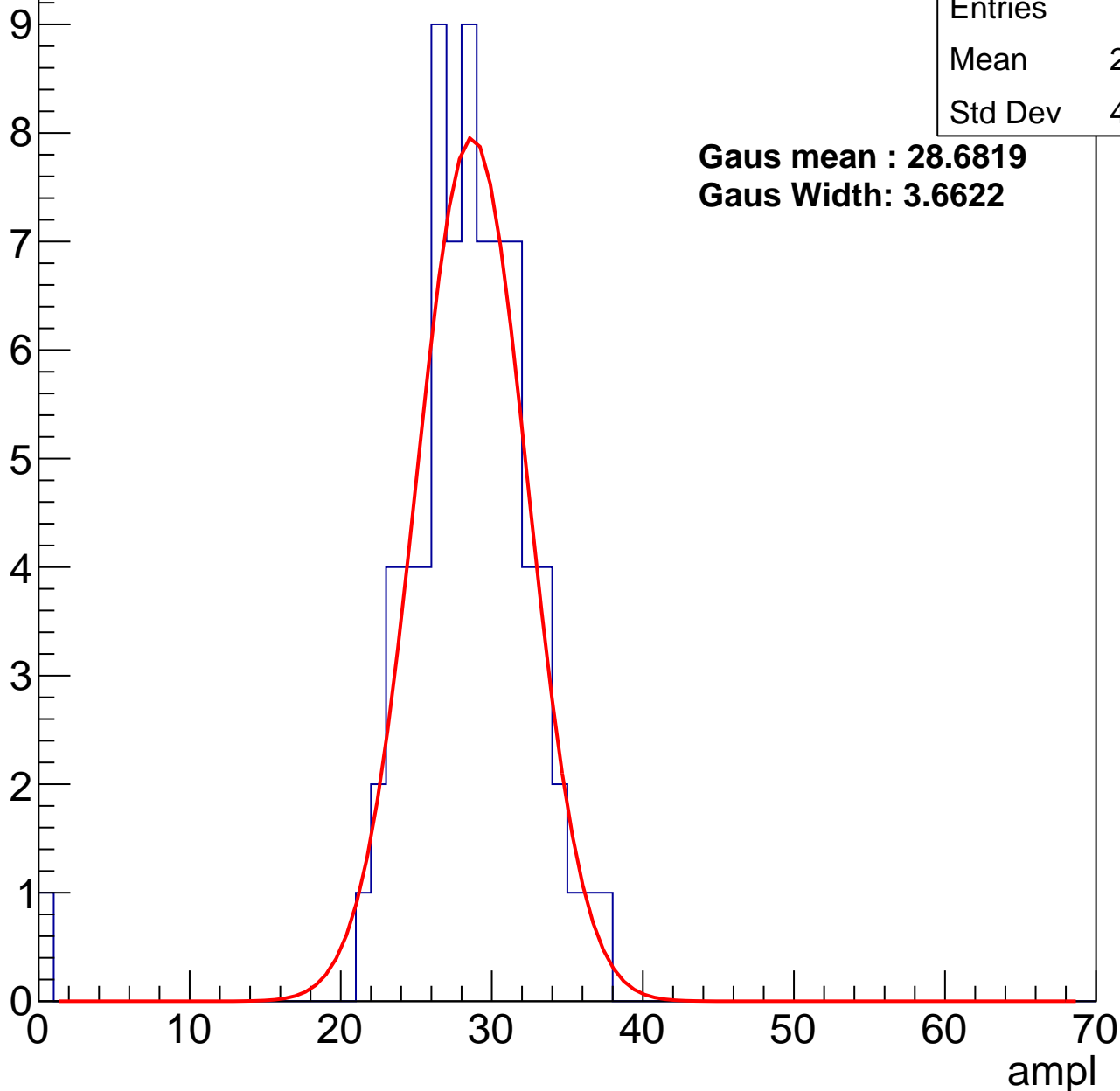
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	27.92
Std Dev	4.727

**Gaus mean : 28.6819**

**Gaus Width: 3.6622**



# B1L000S, U1-ch36, adc1

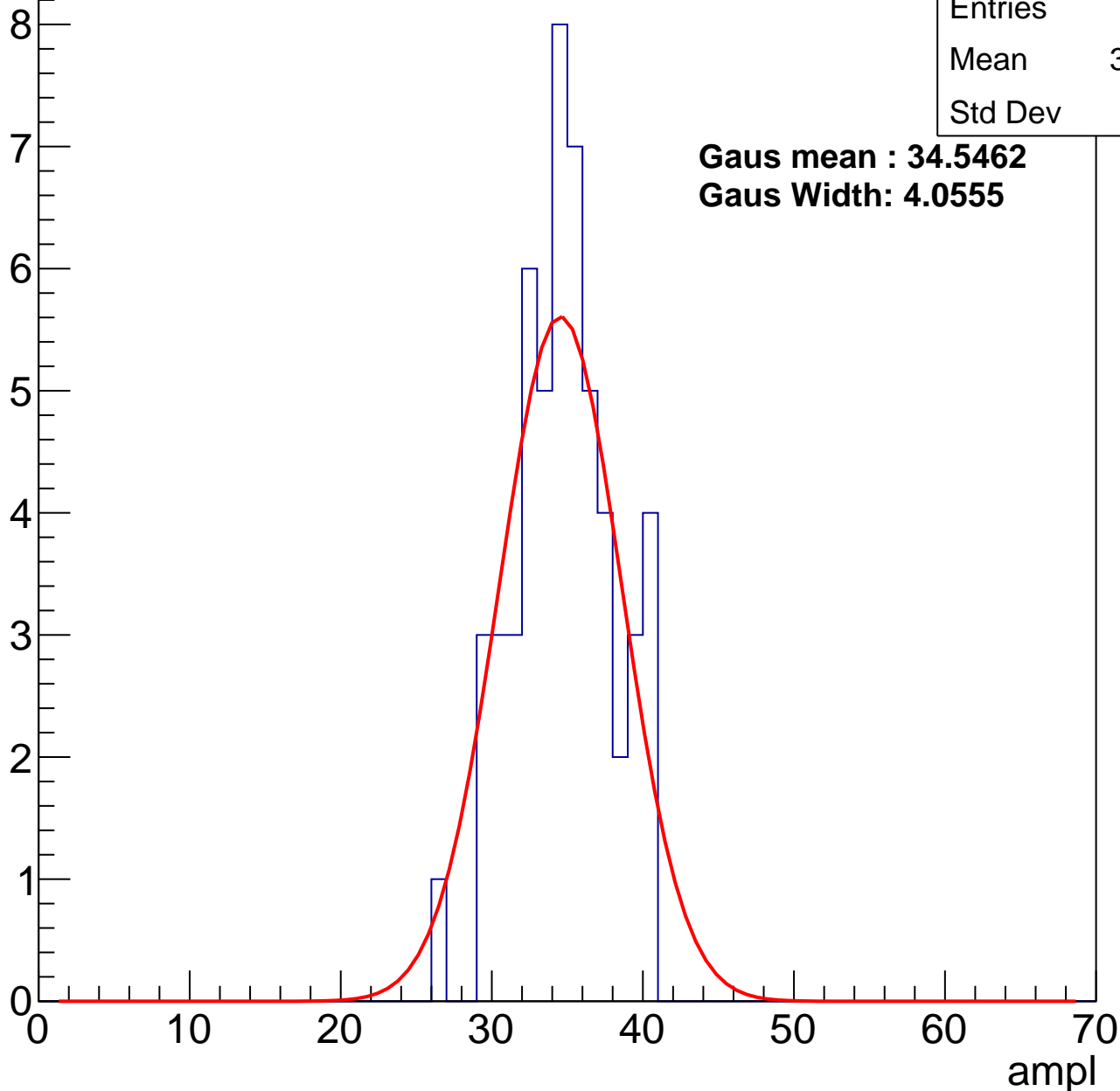
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	34.28
Std Dev	3.2

**Gaus mean : 34.5462**

**Gaus Width: 4.0555**



# B1L000S, U1-ch36, adc2

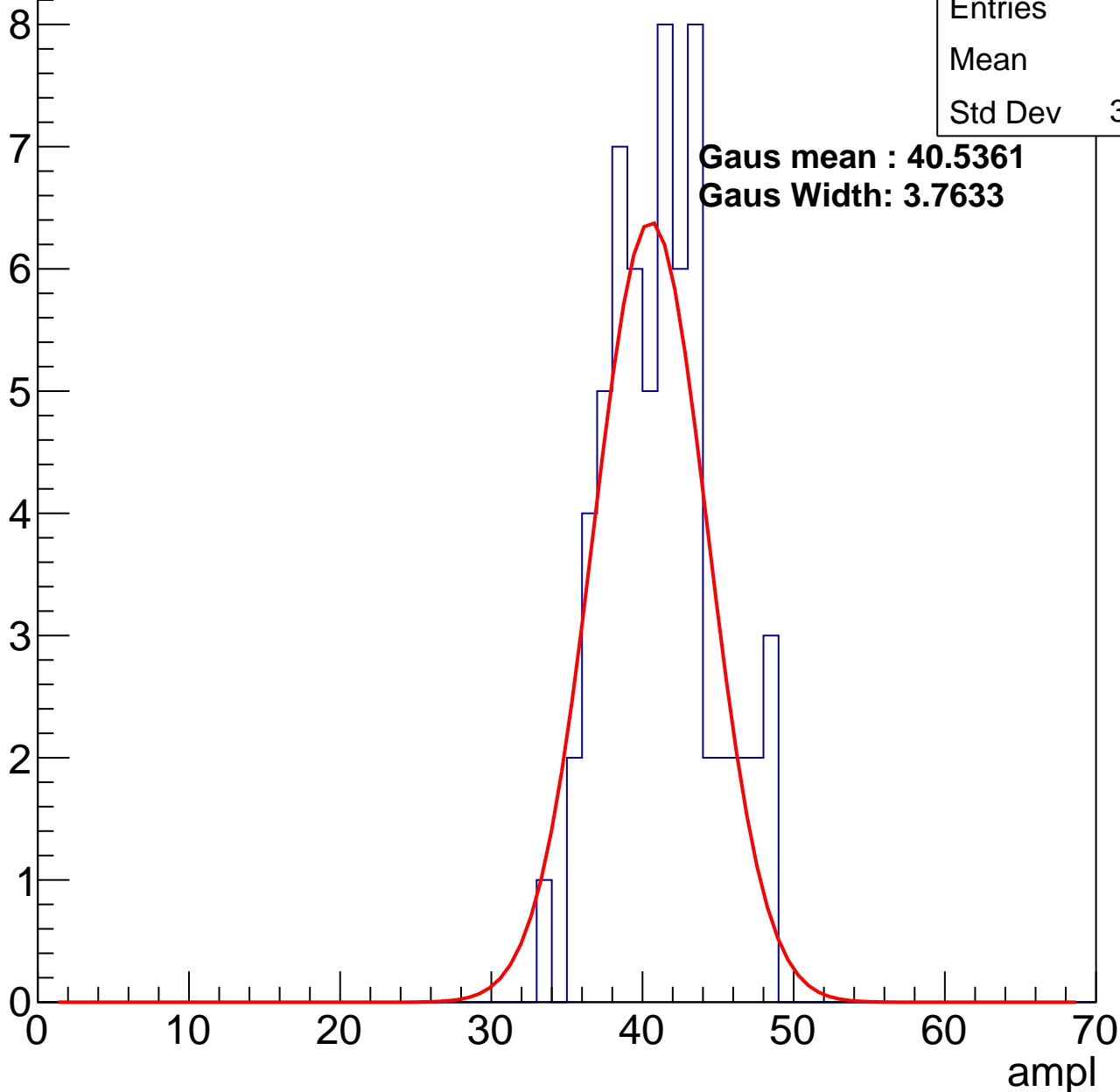
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	40.7
Std Dev	3.472

**Gaus mean : 40.5361**

**Gaus Width: 3.7633**

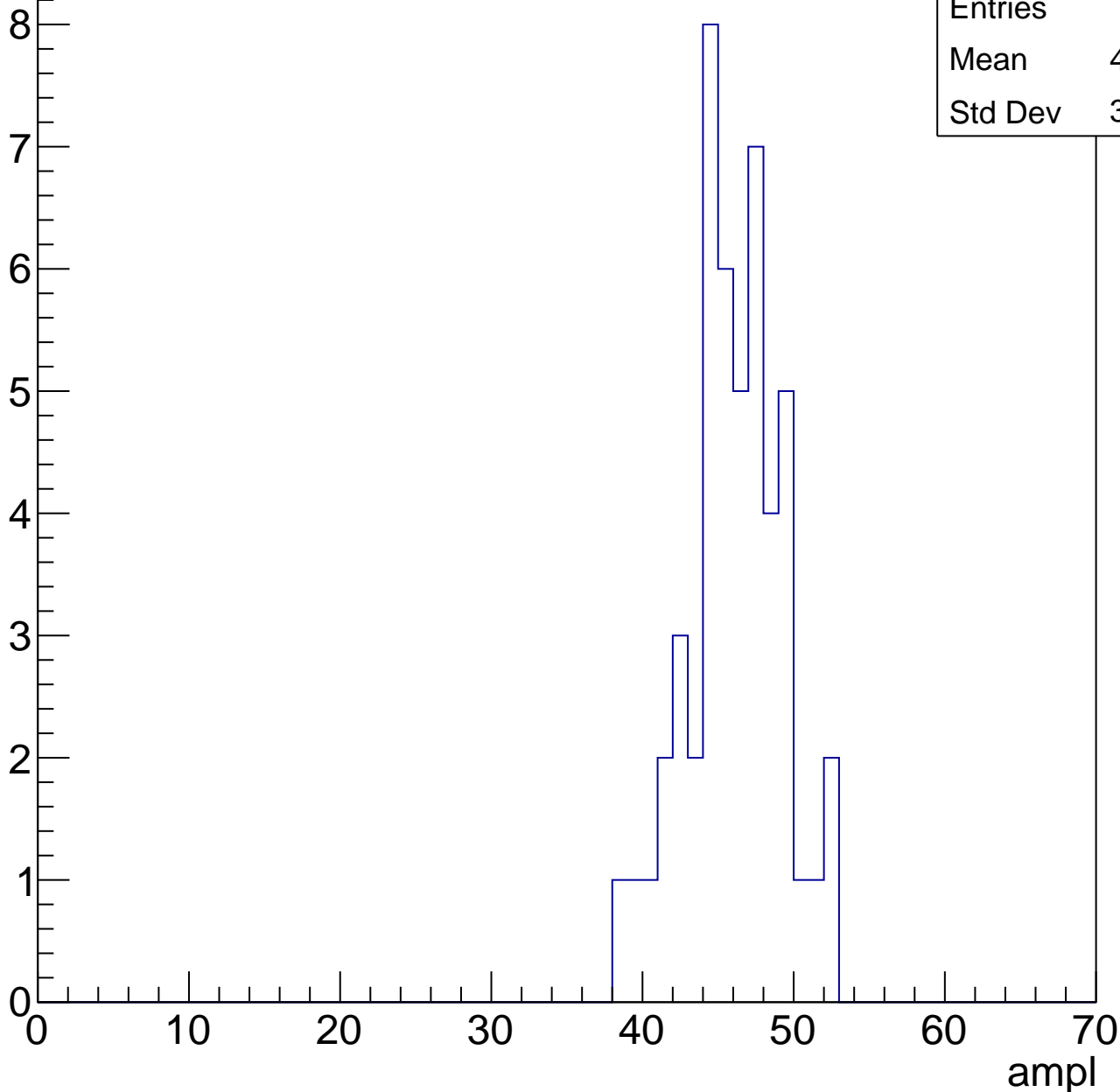


# B1L000S, U1-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	45.59
Std Dev	3.116

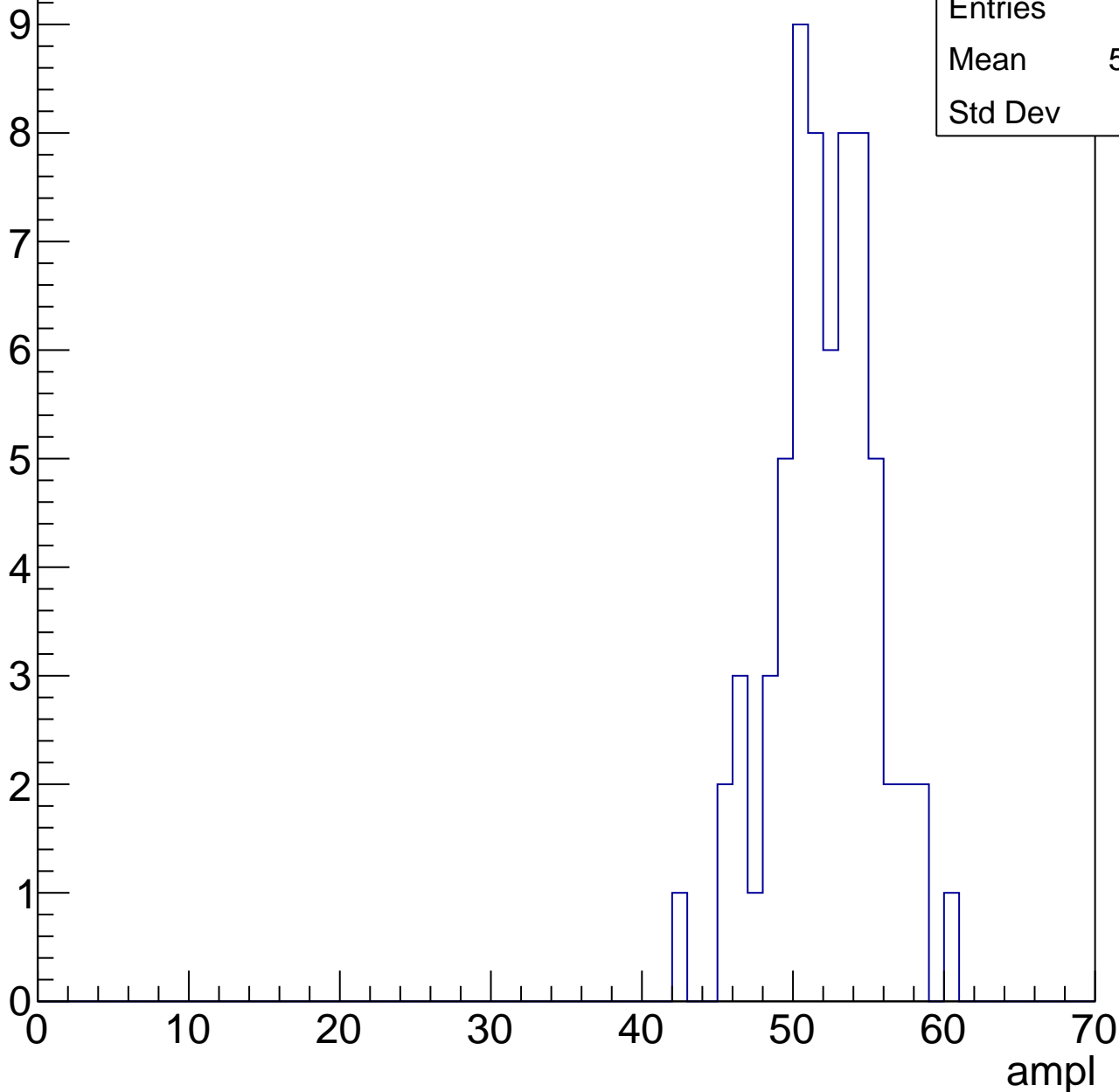


# B1L000S, U1-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

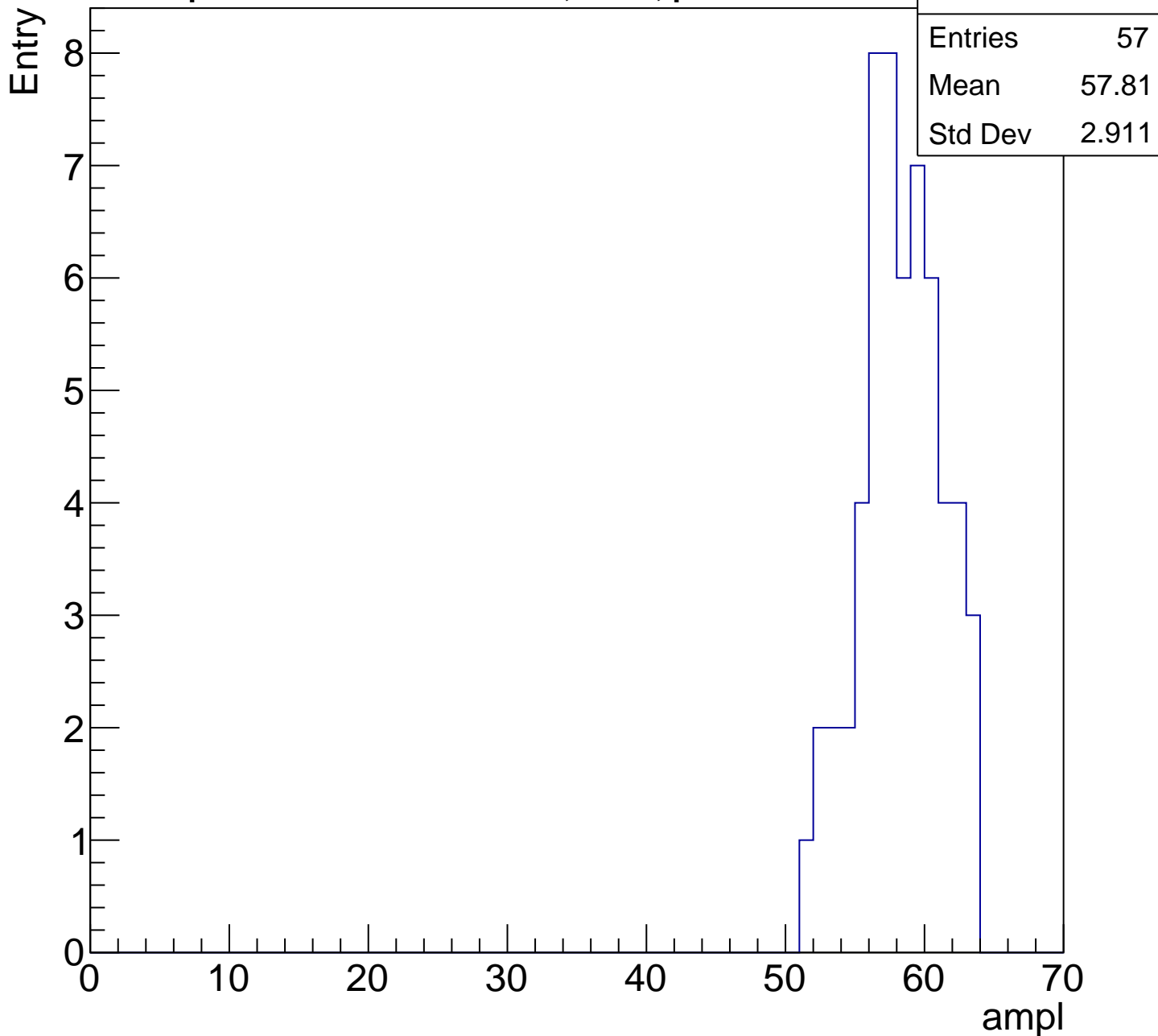
Entry

Entries	66
Mean	51.65
Std Dev	3.4



# B1L000S, U1-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

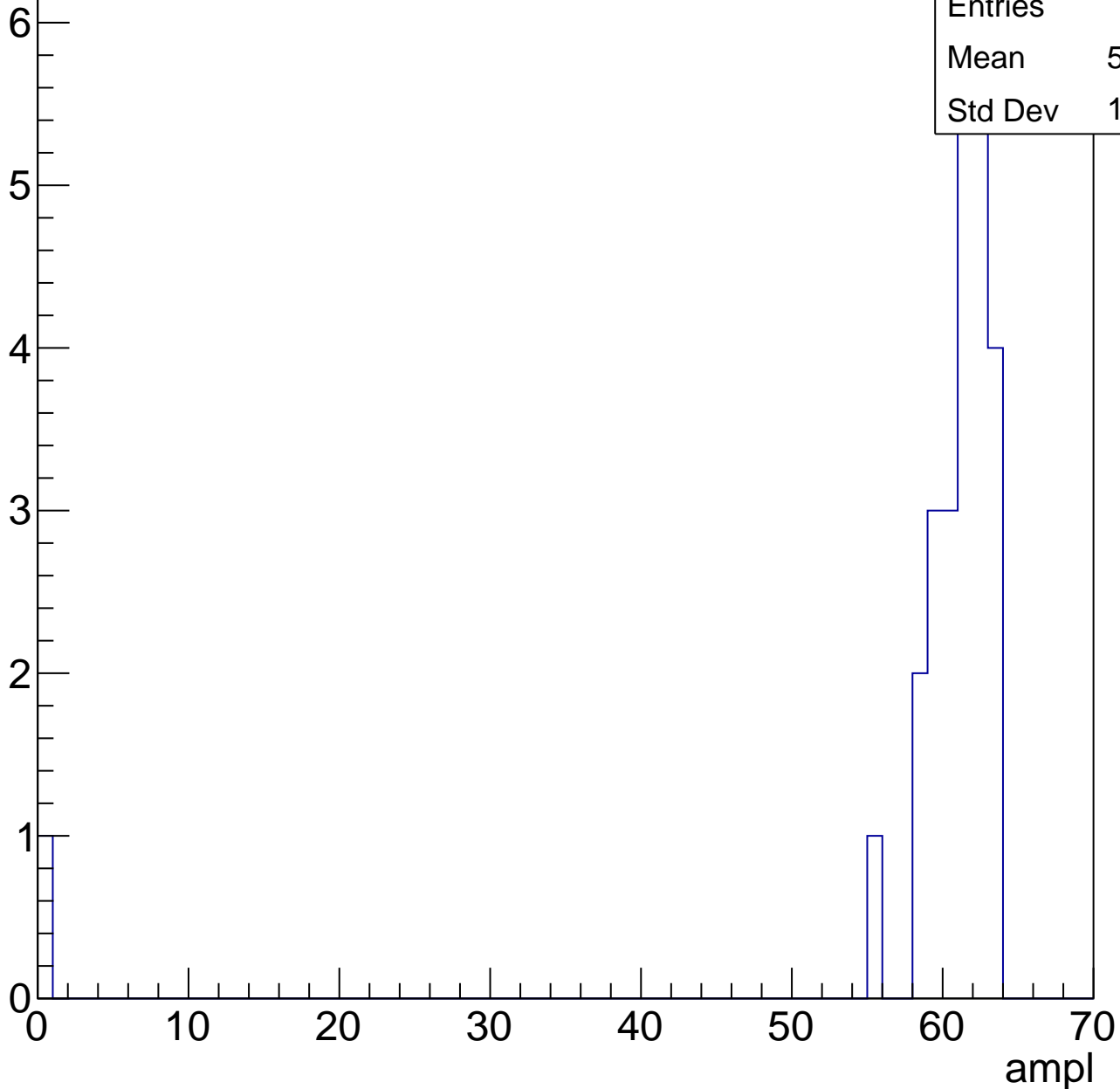


# B1L000S, U1-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.38
Std Dev	11.82





# B1L000S, U1-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch37, adc0

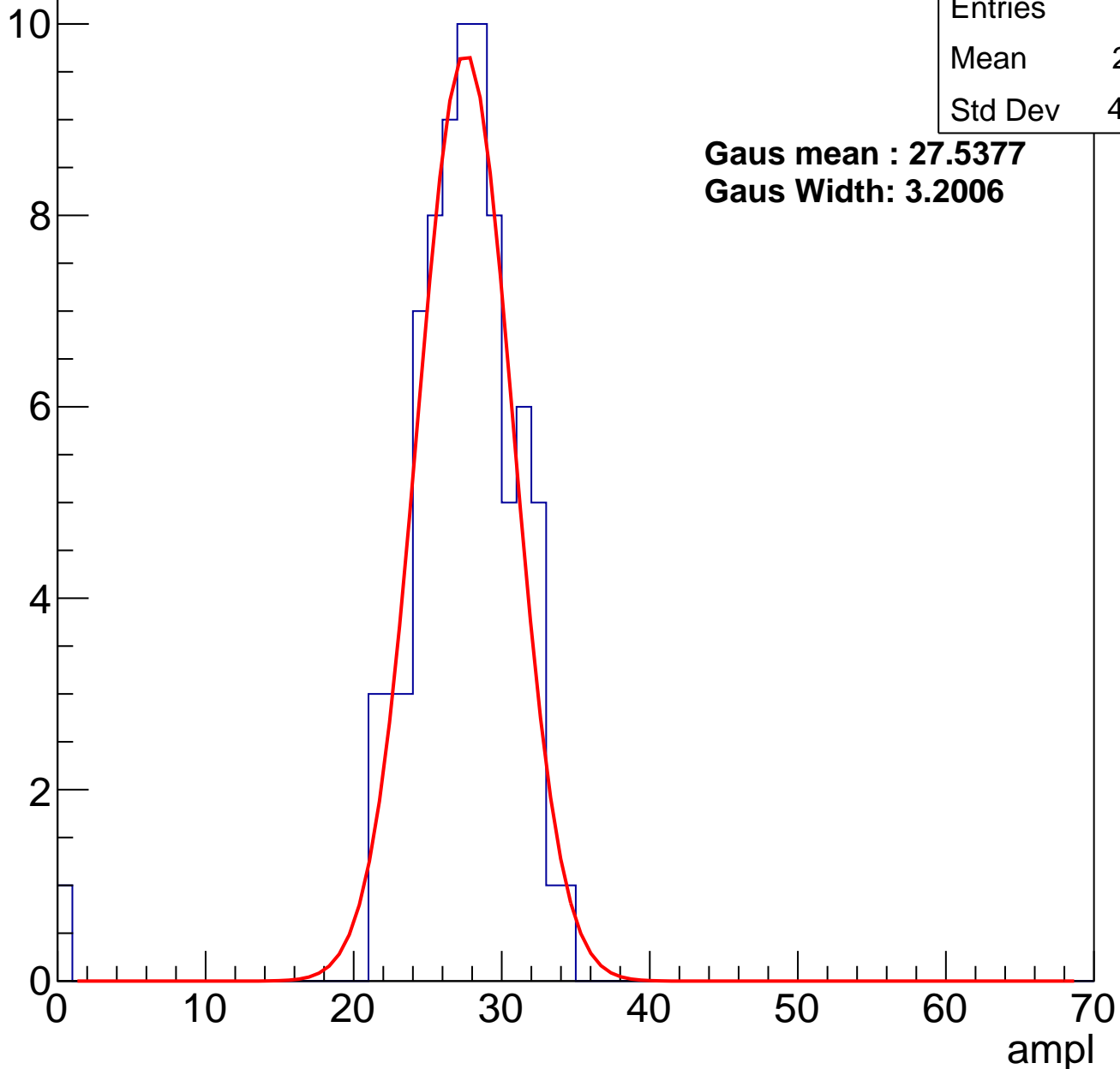
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	26.81
Std Dev	4.263

**Gaus mean : 27.5377**

**Gaus Width: 3.2006**

Entry



# B1L000S, U1-ch37, adc1

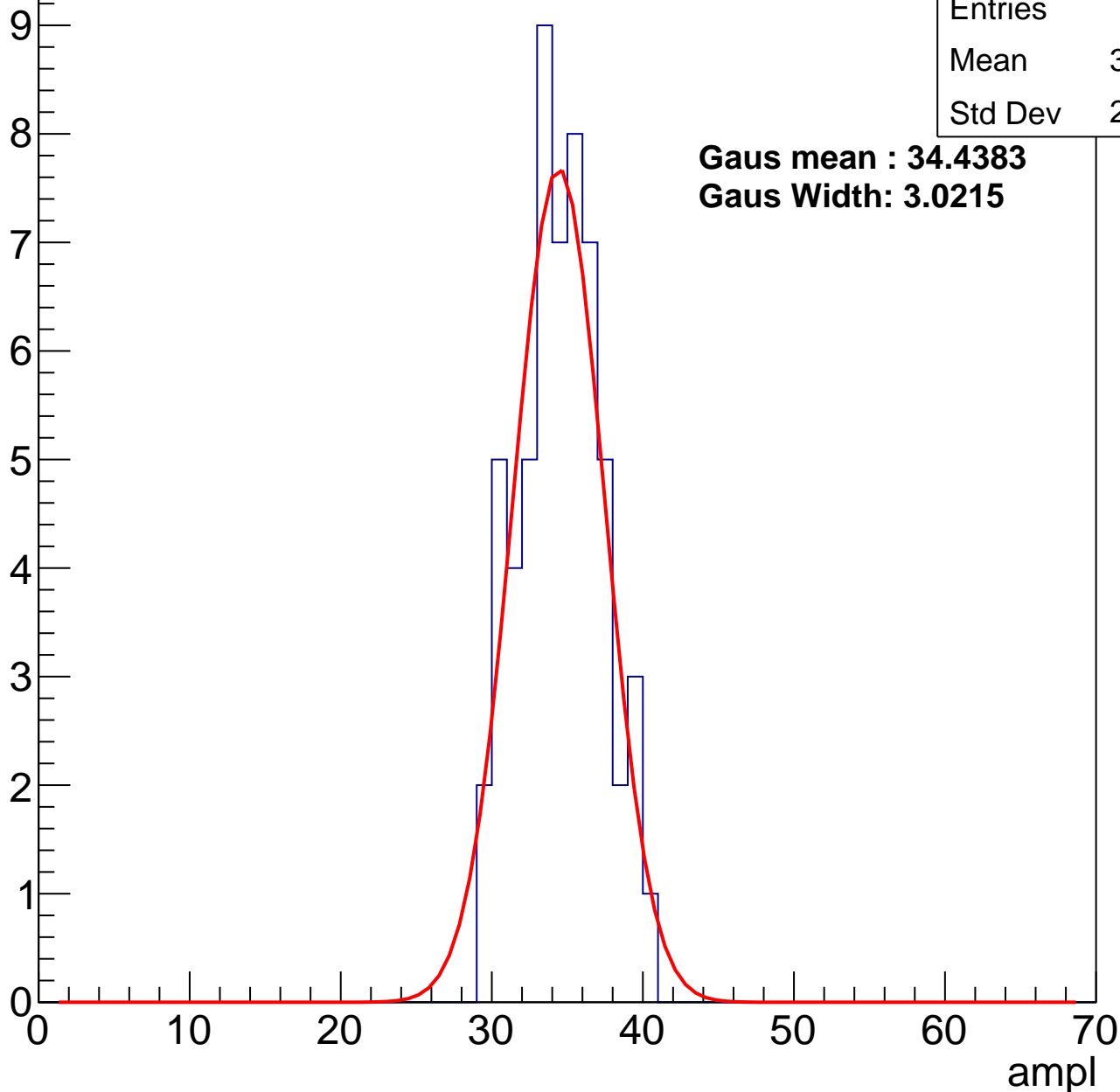
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.09
Std Dev	2.686

**Gaus mean : 34.4383**

**Gaus Width: 3.0215**



# B1L000S, U1-ch37, adc2

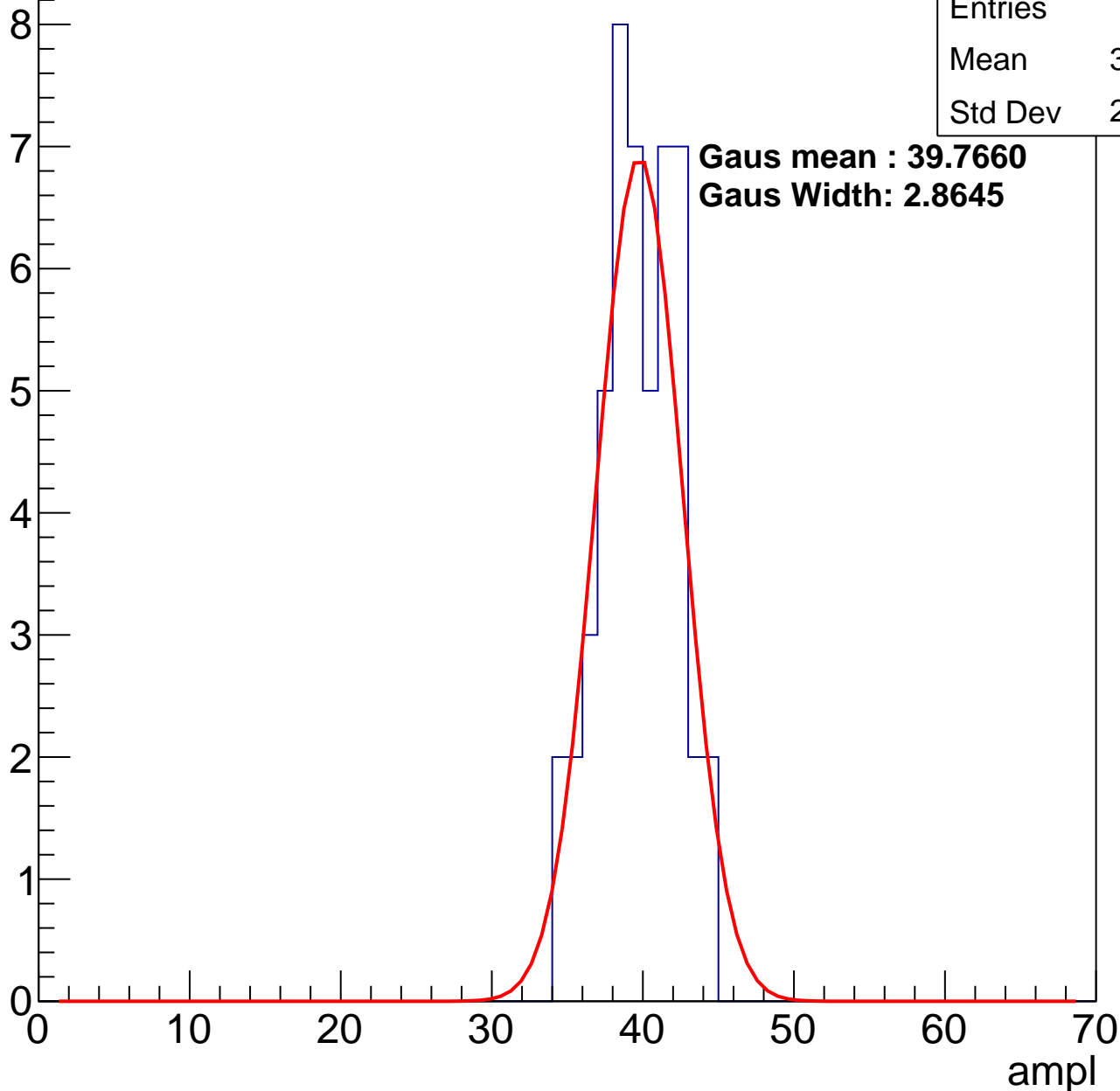
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	39.26
Std Dev	2.496

**Gaus mean : 39.7660**

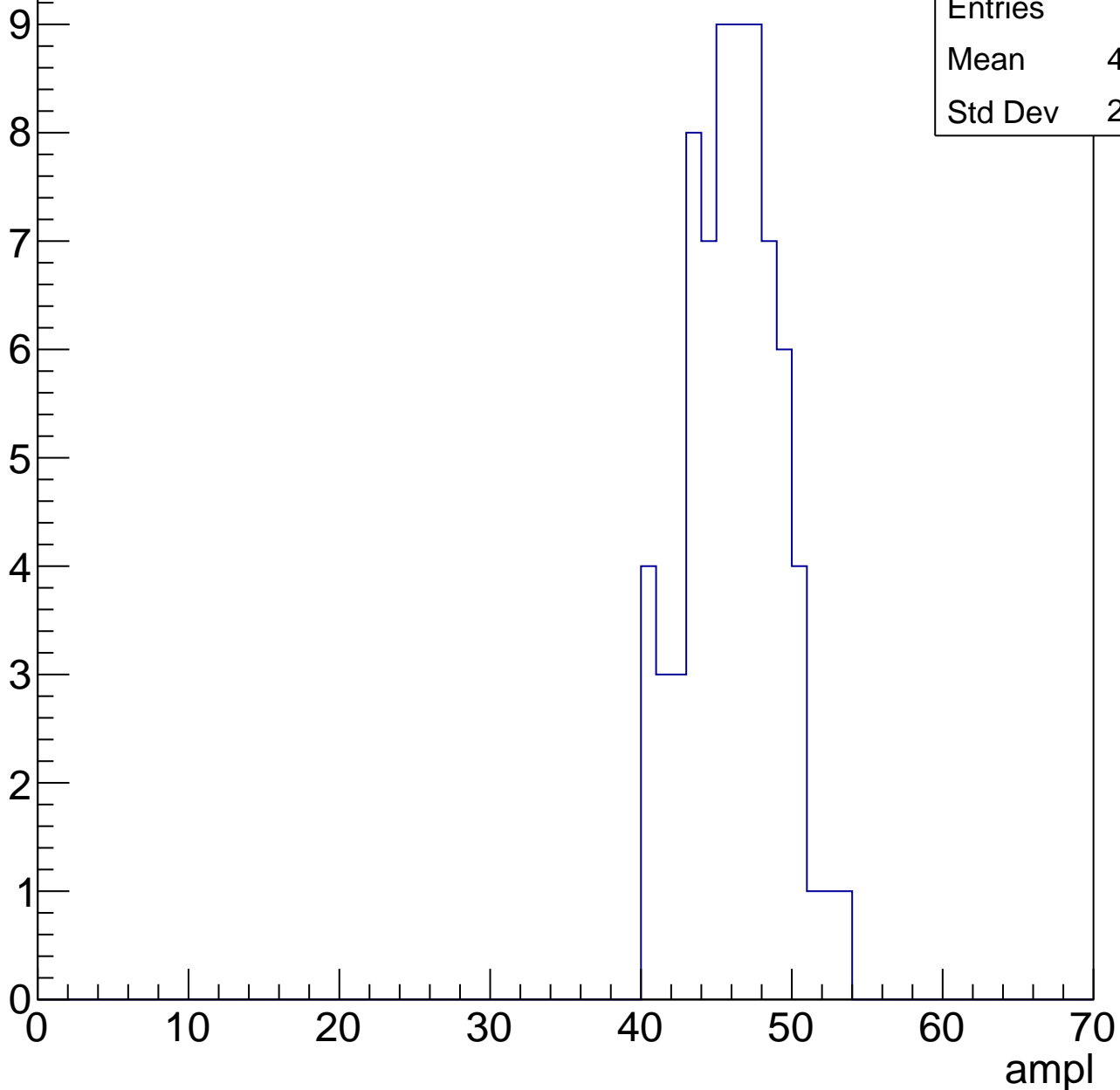
**Gaus Width: 2.8645**



# B1L000S, U1-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

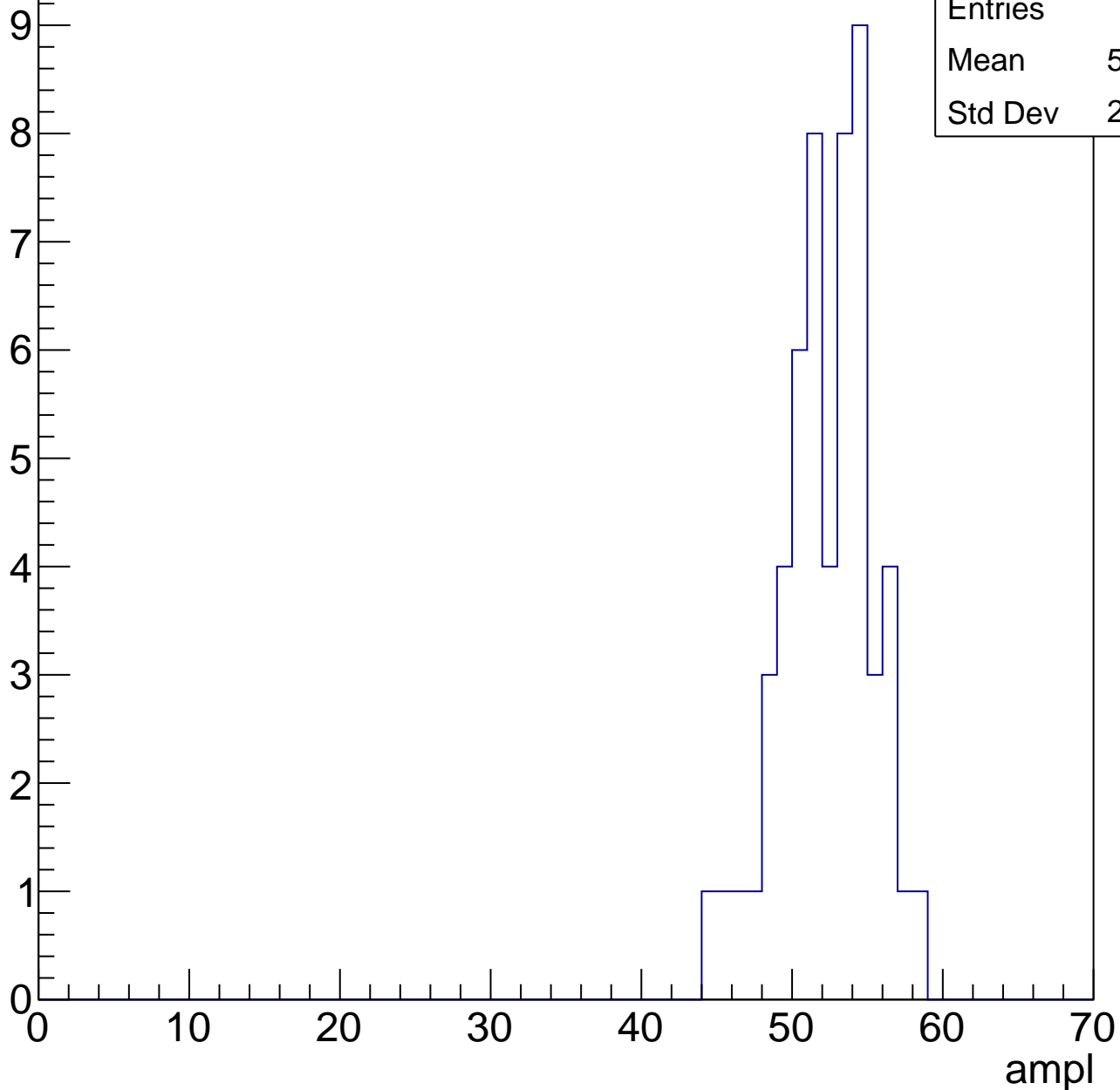


Entries	72
Mean	45.68
Std Dev	2.967

# B1L000S, U1-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



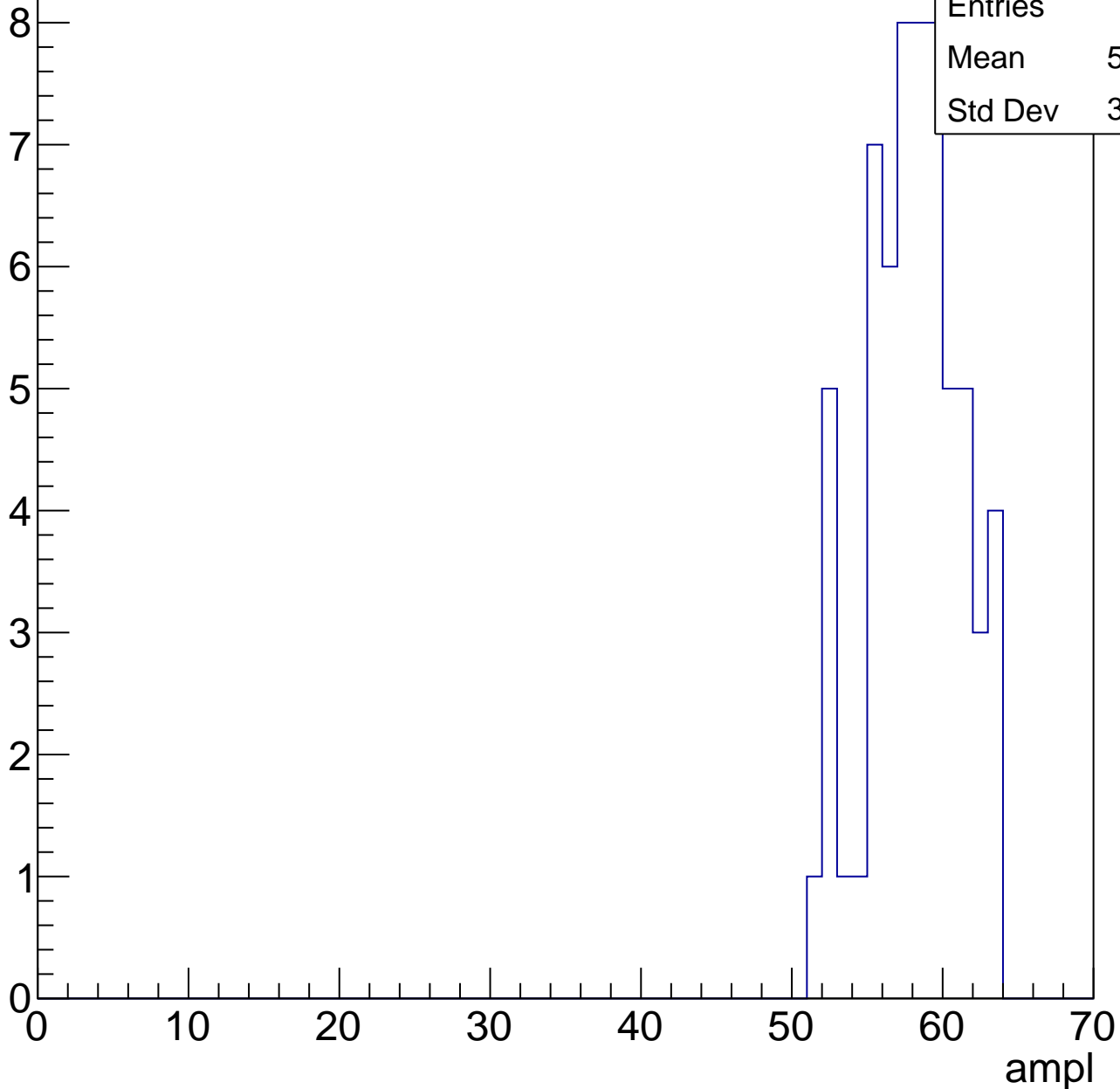
Entries	55
Mean	51.85
Std Dev	2.969

# B1L000S, U1-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	57.65
Std Dev	3.054

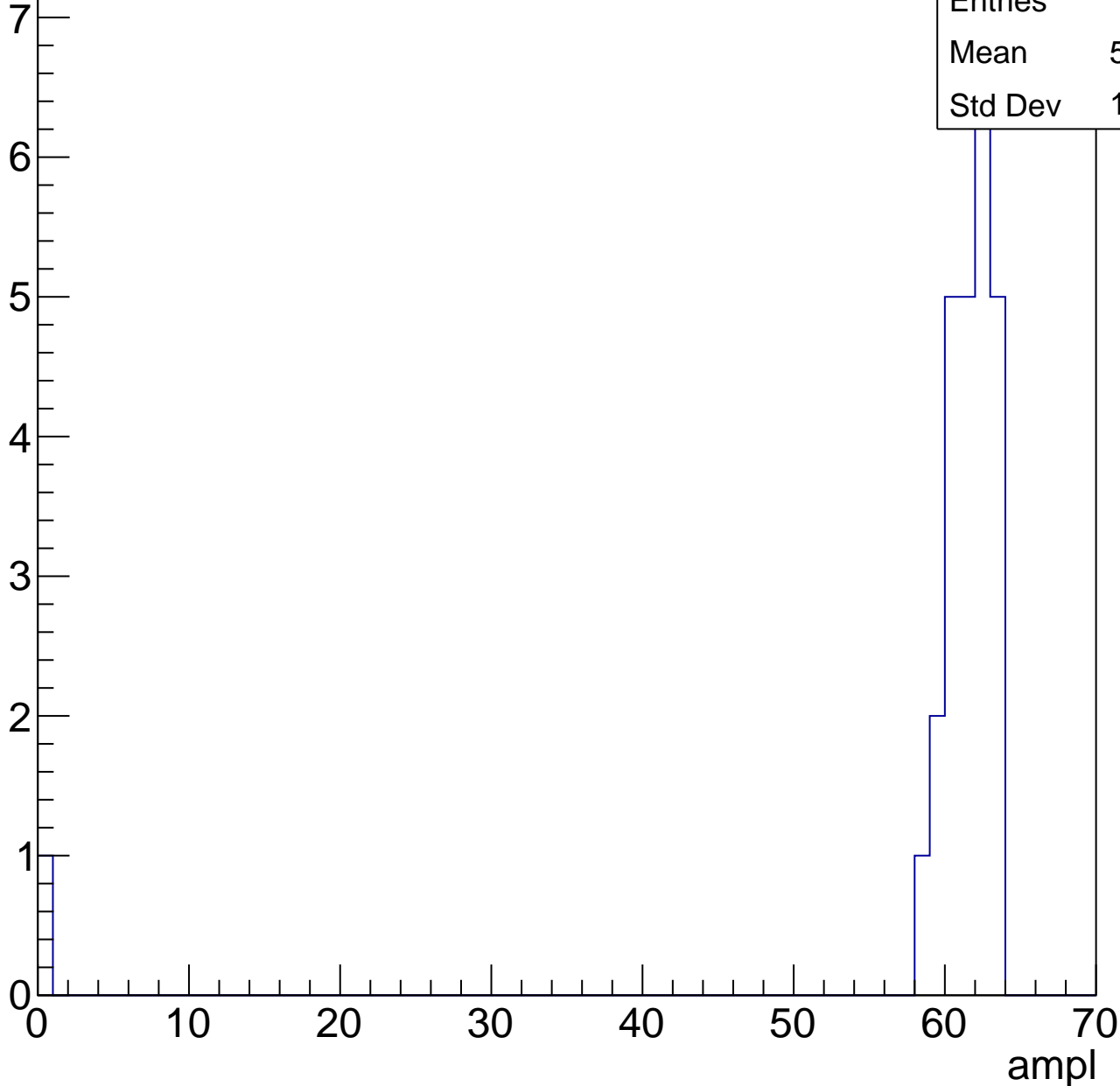


# B1L000S, U1-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.85
Std Dev	11.85

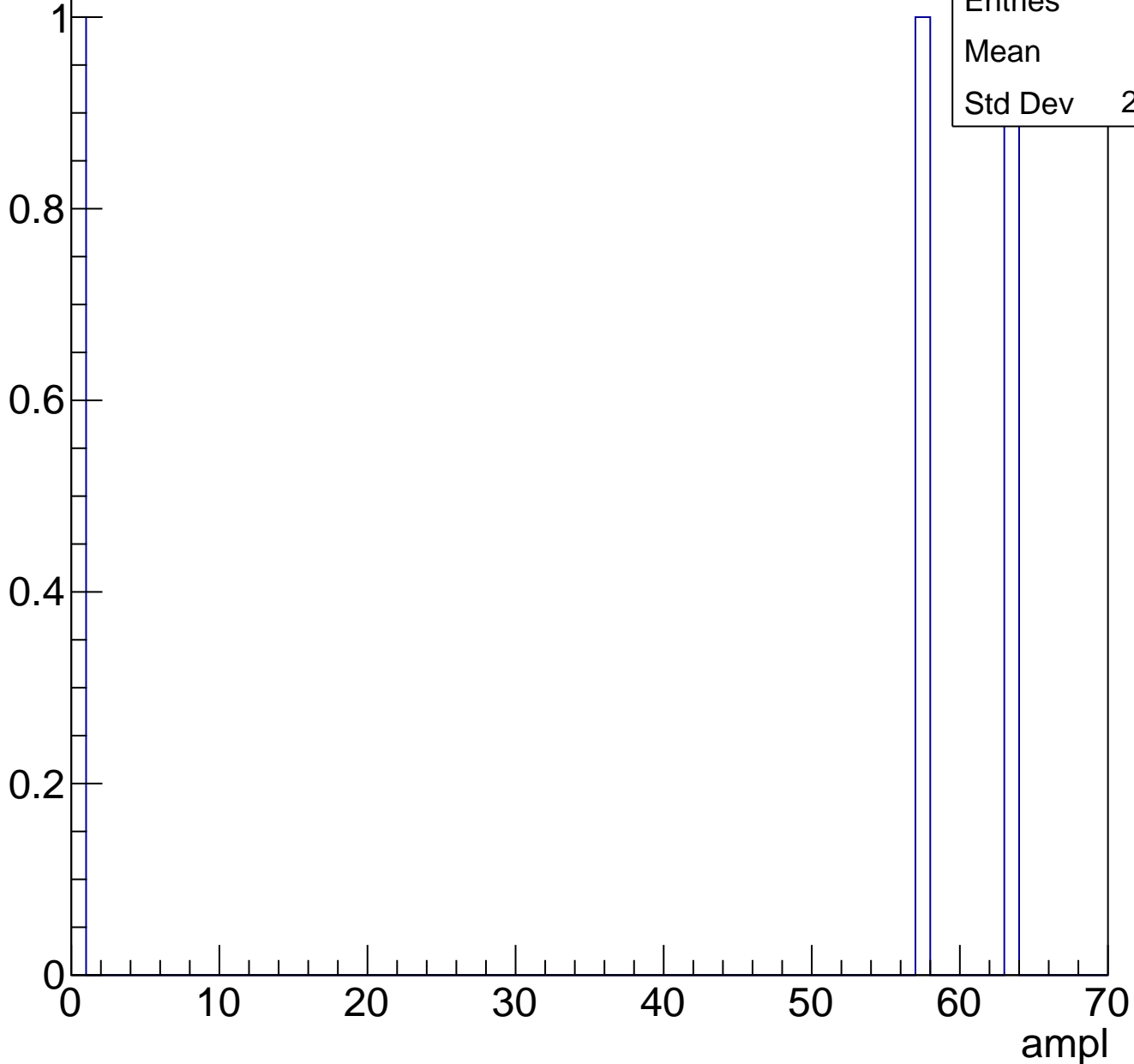




# B1L000S, U1-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch38, adc0

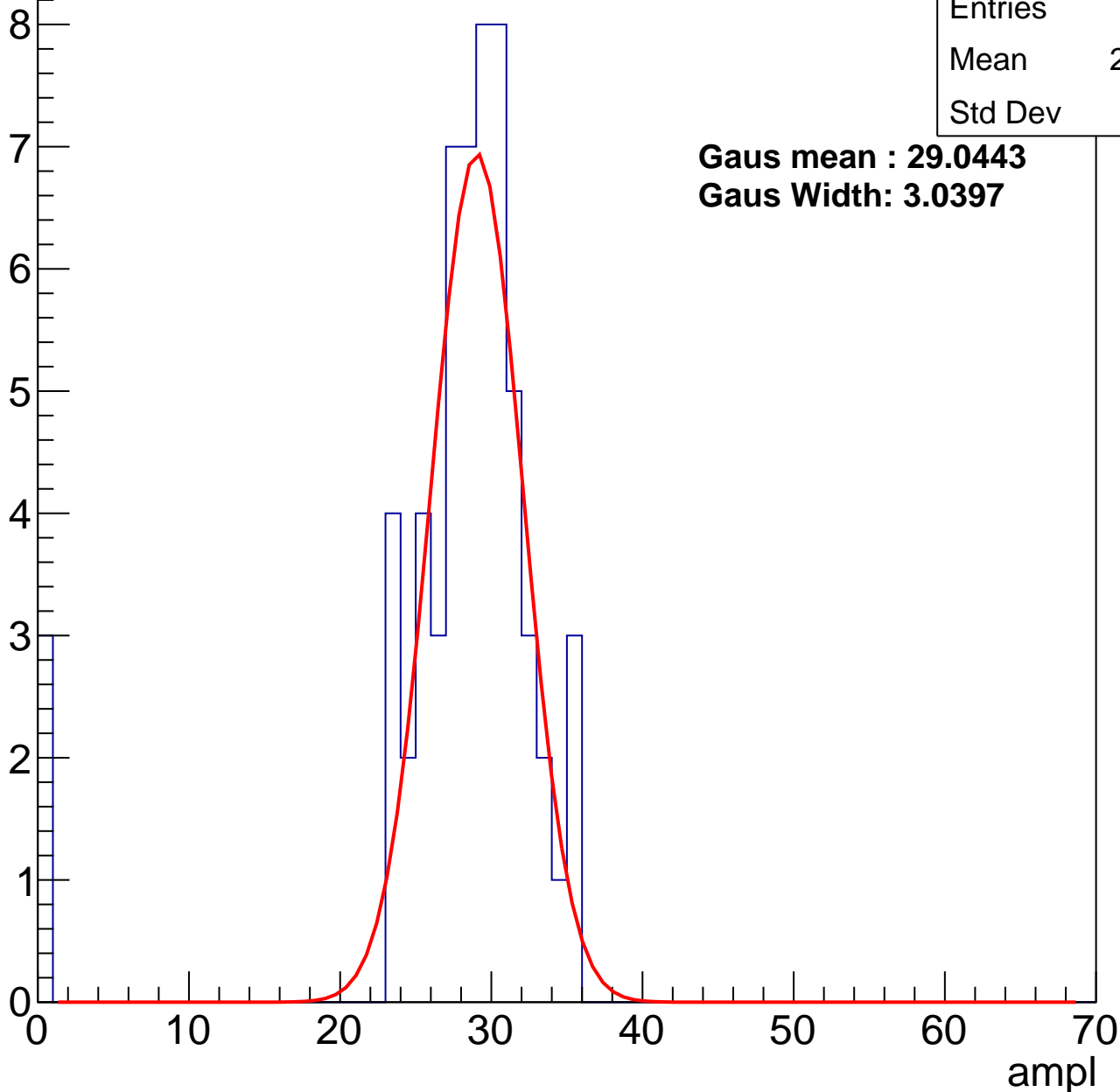
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.18
Std Dev	6.91

**Gaus mean : 29.0443**

**Gaus Width: 3.0397**



# B1L000S, U1-ch38, adc1

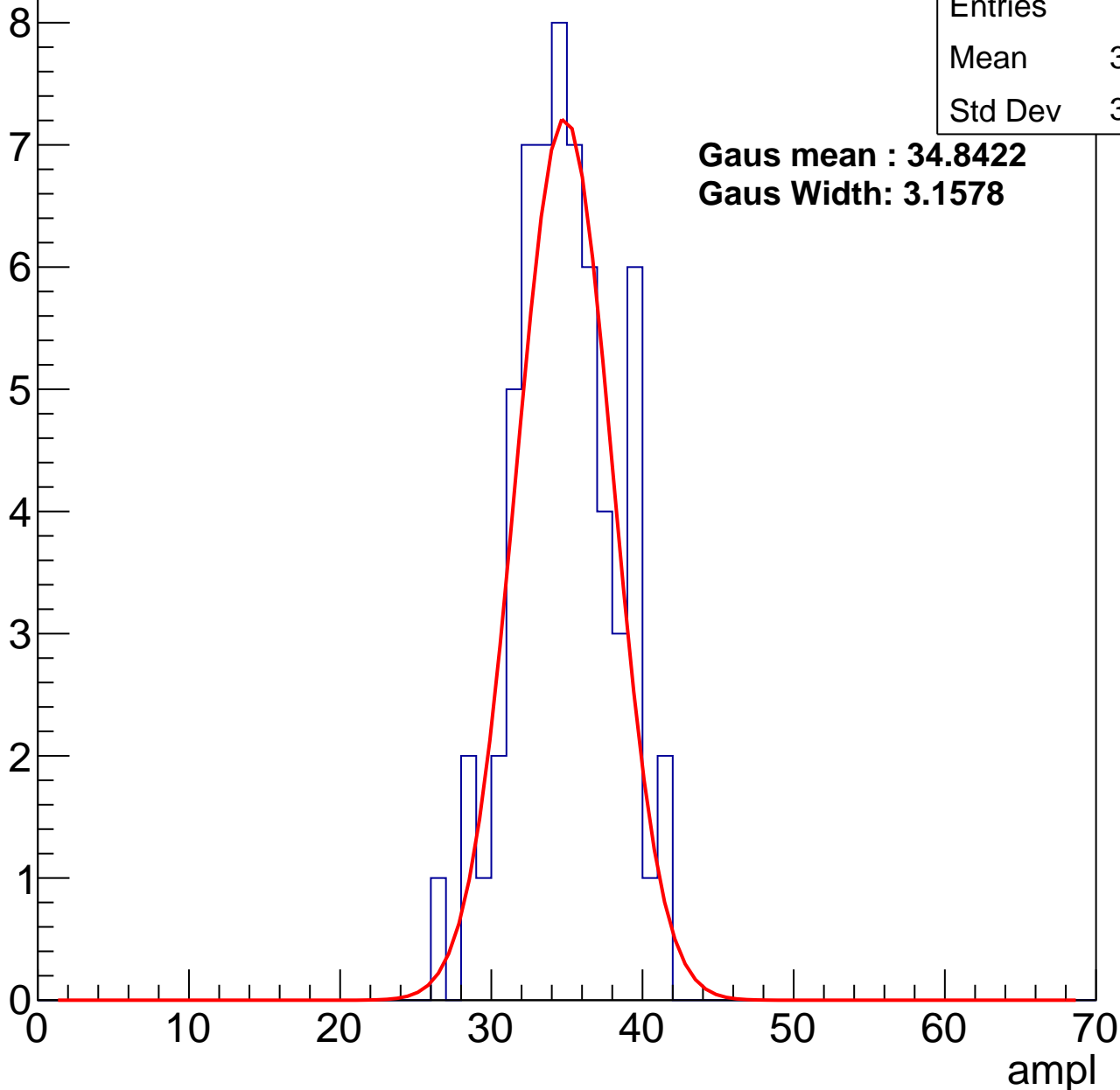
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	34.39
Std Dev	3.269

**Gaus mean : 34.8422**

**Gaus Width: 3.1578**



# B1L000S, U1-ch38, adc2

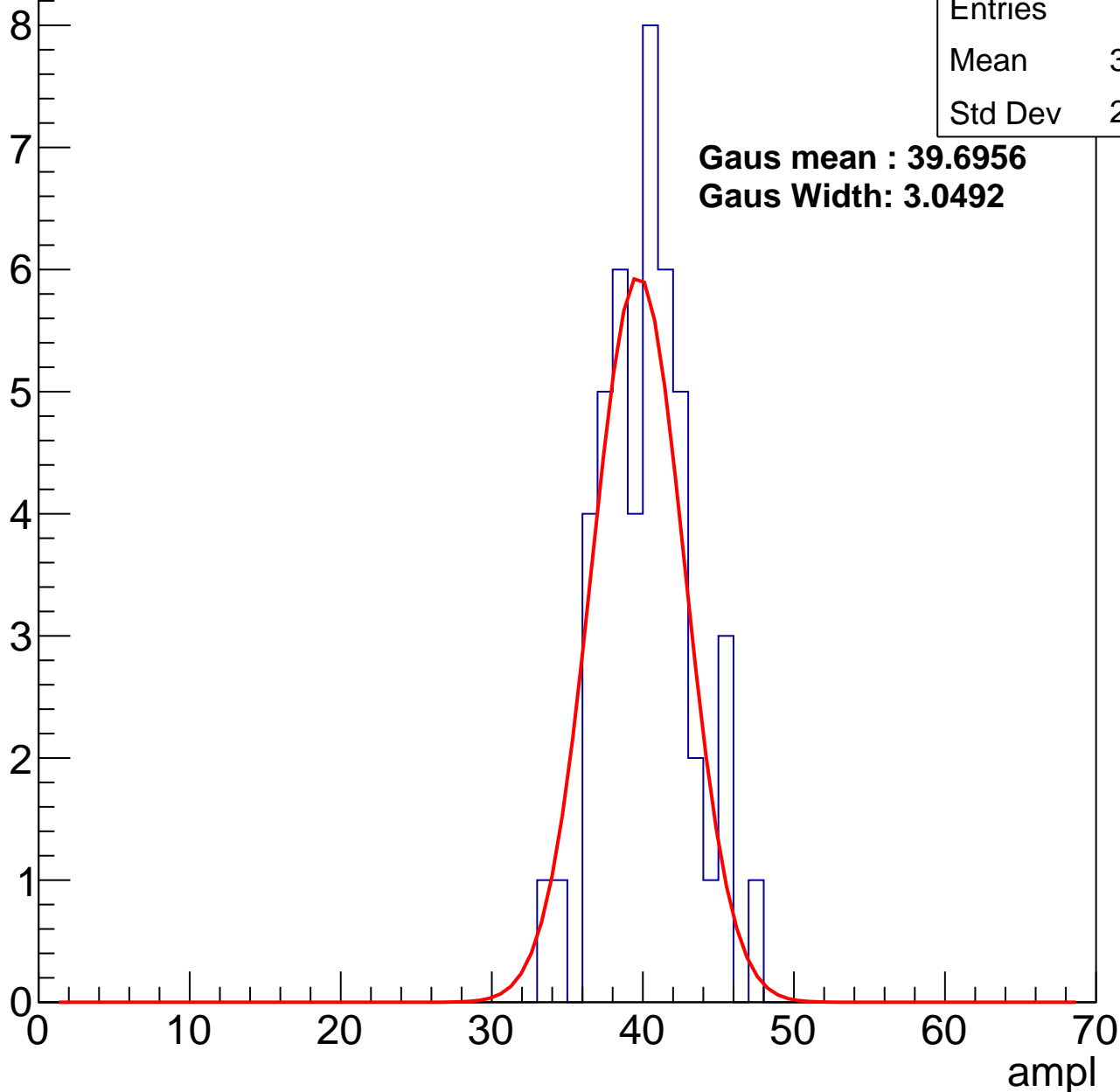
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	39.74
Std Dev	2.928

**Gaus mean : 39.6956**

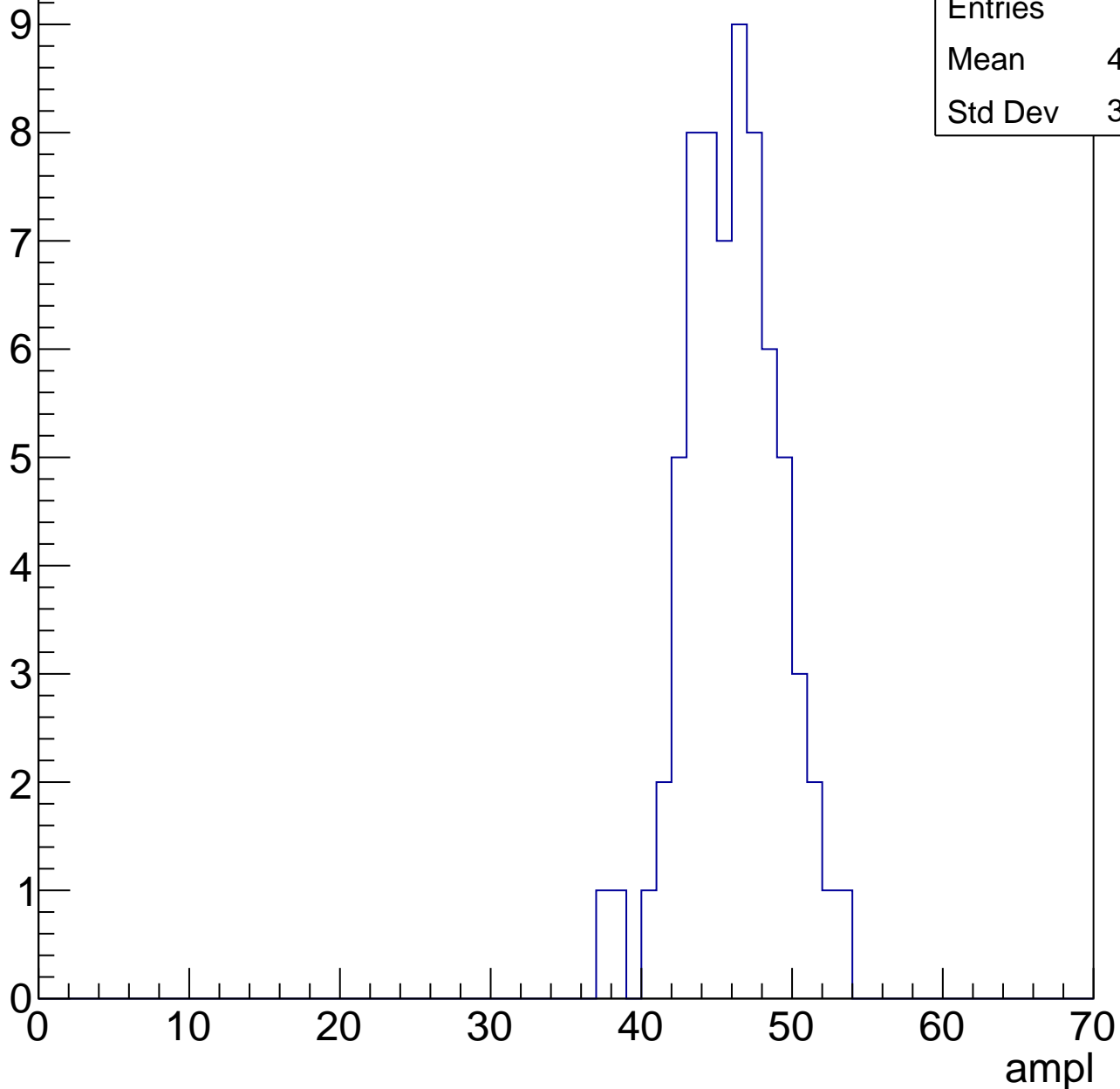
**Gaus Width: 3.0492**



# B1L000S, U1-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



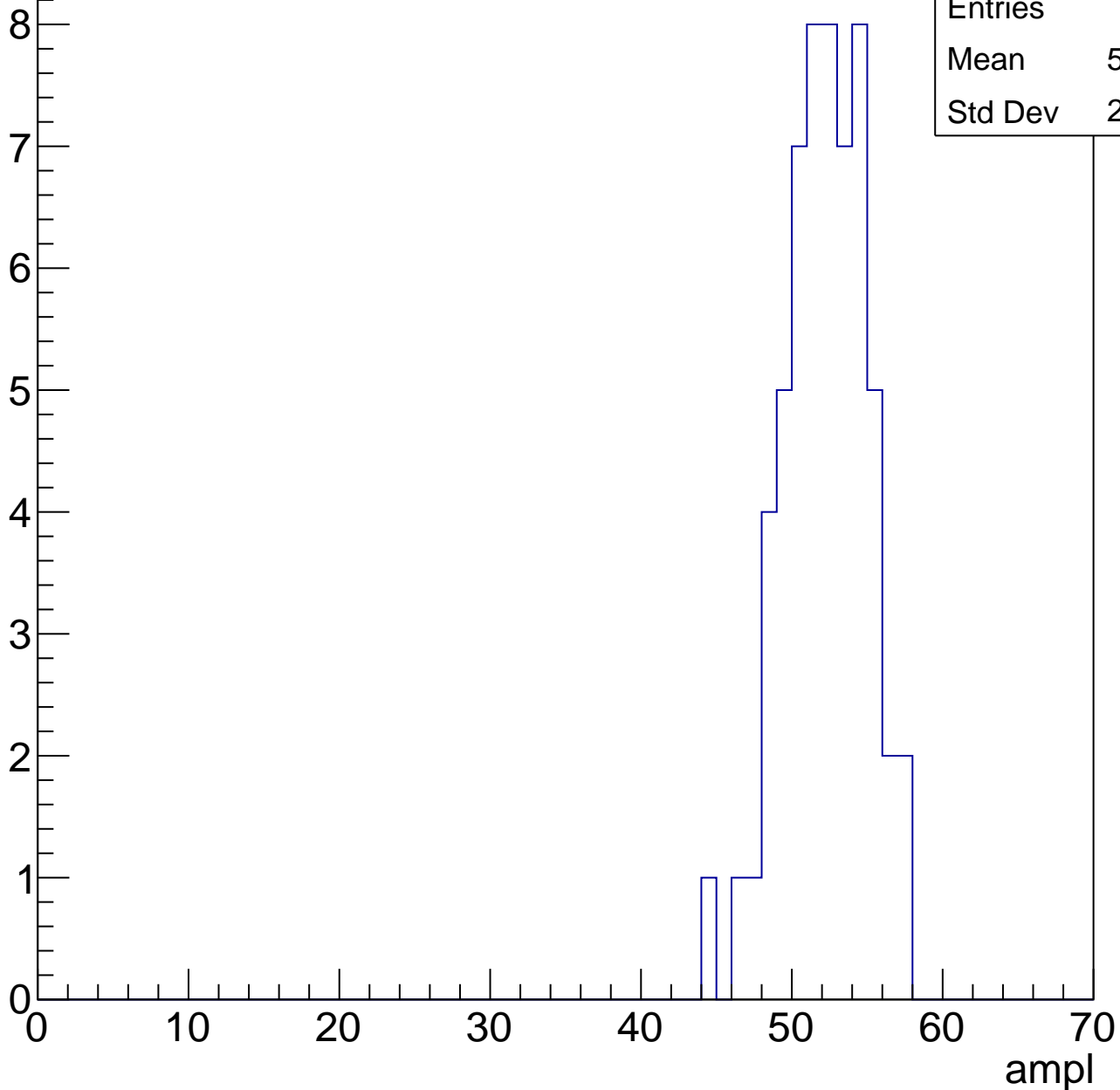
Entries	68
Mean	45.56
Std Dev	3.136

# B1L000S, U1-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

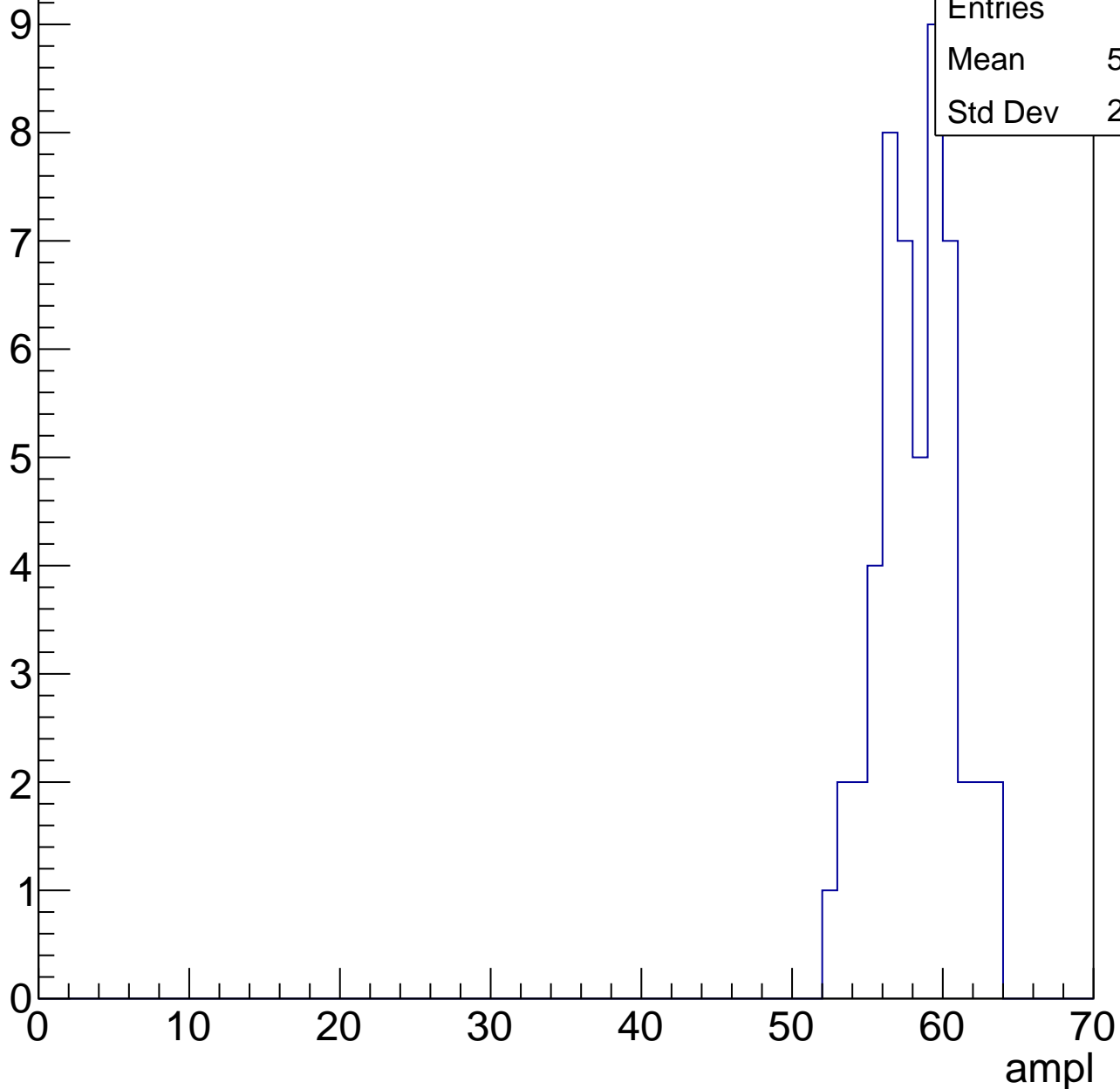
Entries	59
Mean	51.73
Std Dev	2.705



# B1L000S, U1-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

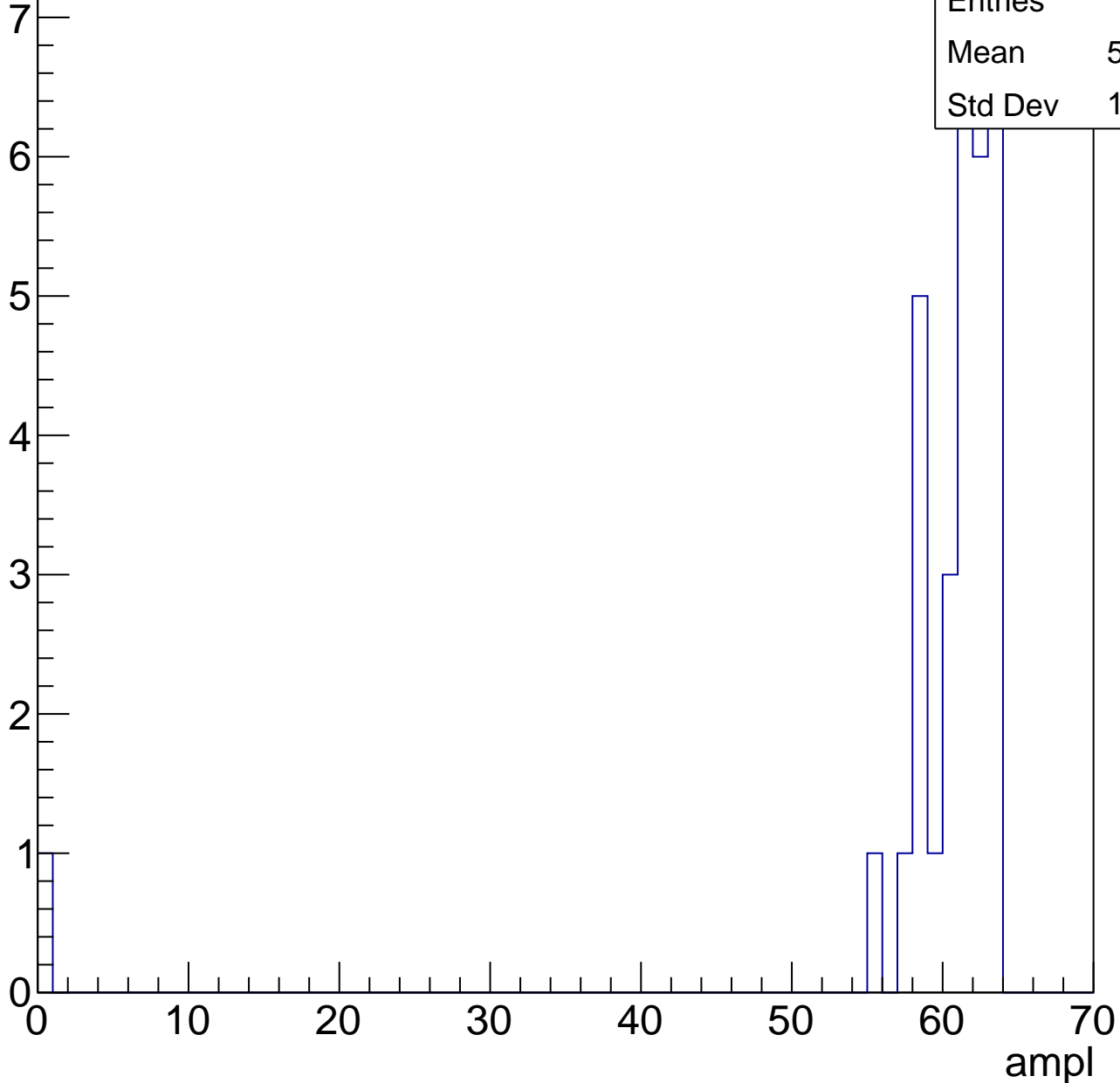


# B1L000S, U1-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	58.78
Std Dev	10.76

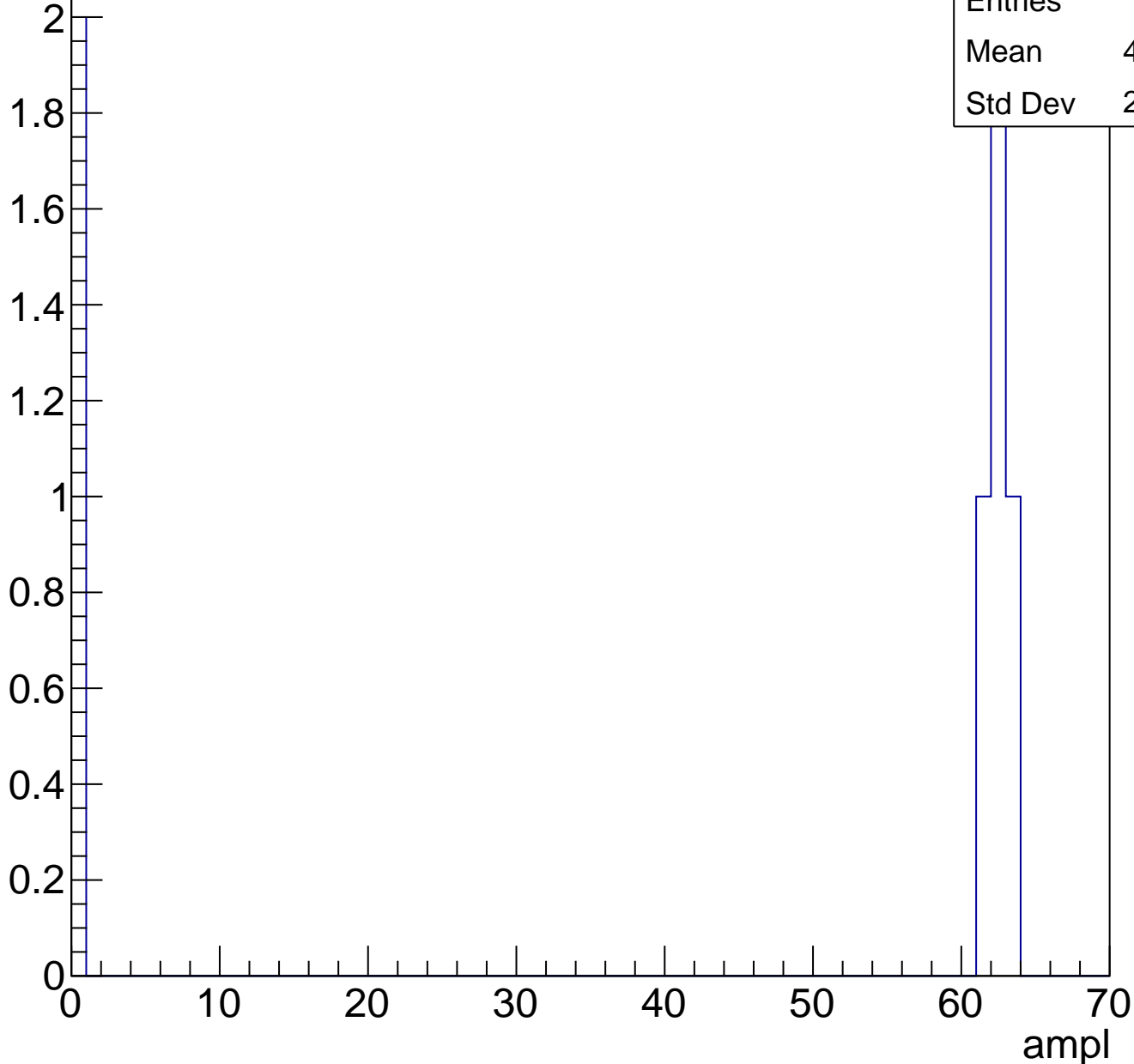




# B1L000S, U1-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch39, adc0

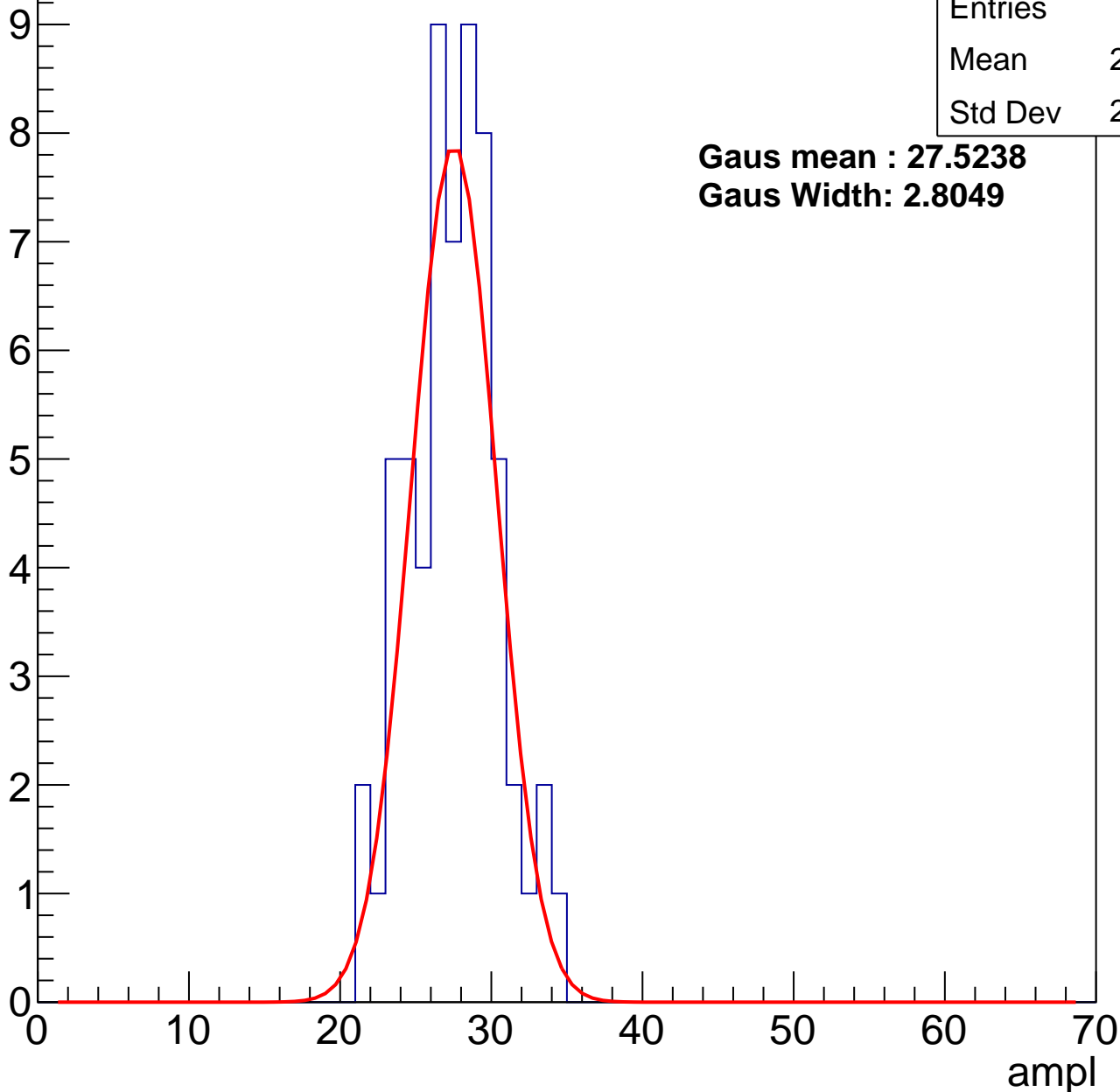
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	27.05
Std Dev	2.894

**Gaus mean : 27.5238**

**Gaus Width: 2.8049**



# B1L000S, U1-ch39, adc1

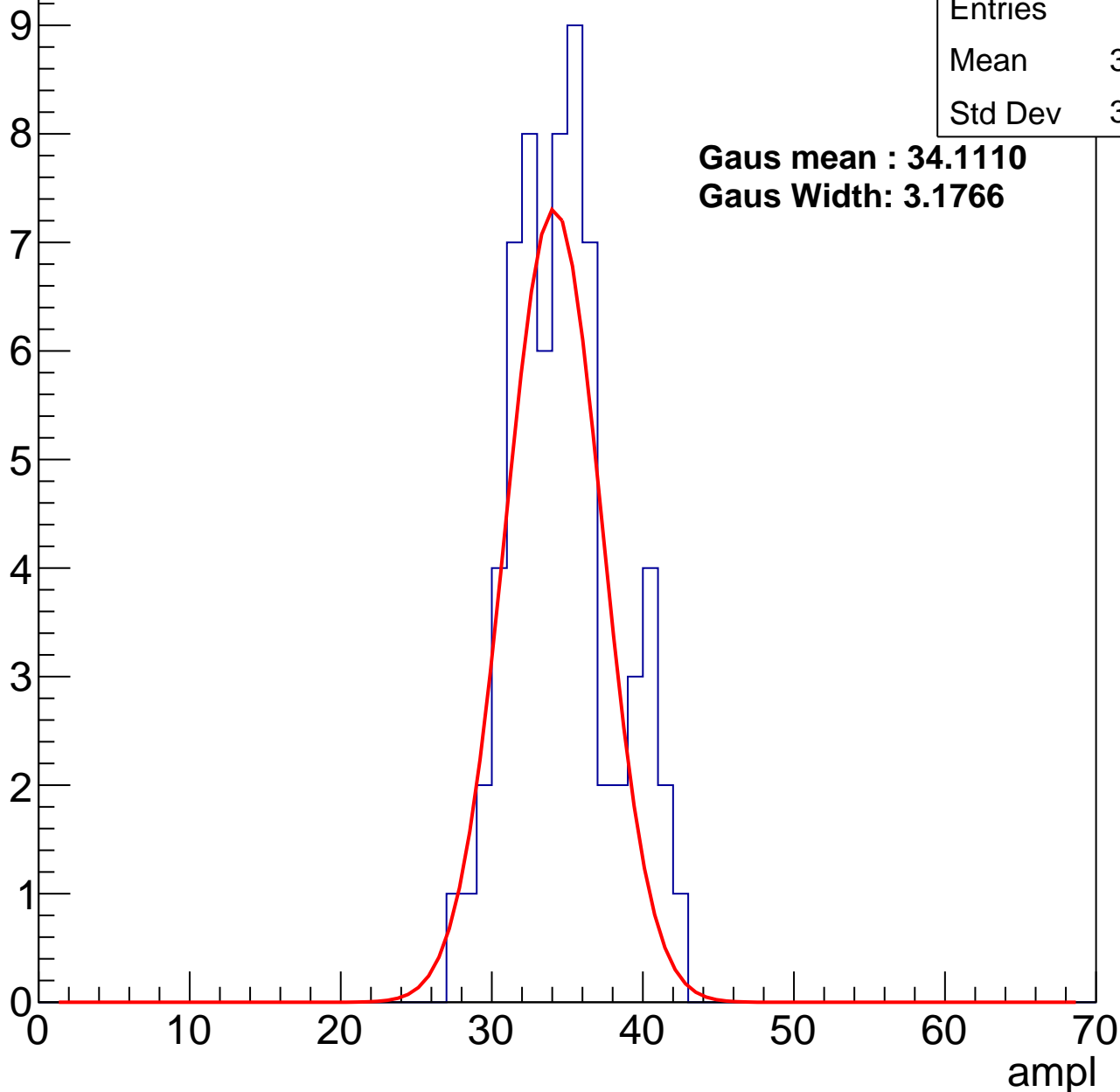
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.24
Std Dev	3.377

**Gaus mean : 34.1110**

**Gaus Width: 3.1766**



# B1L000S, U1-ch39, adc2

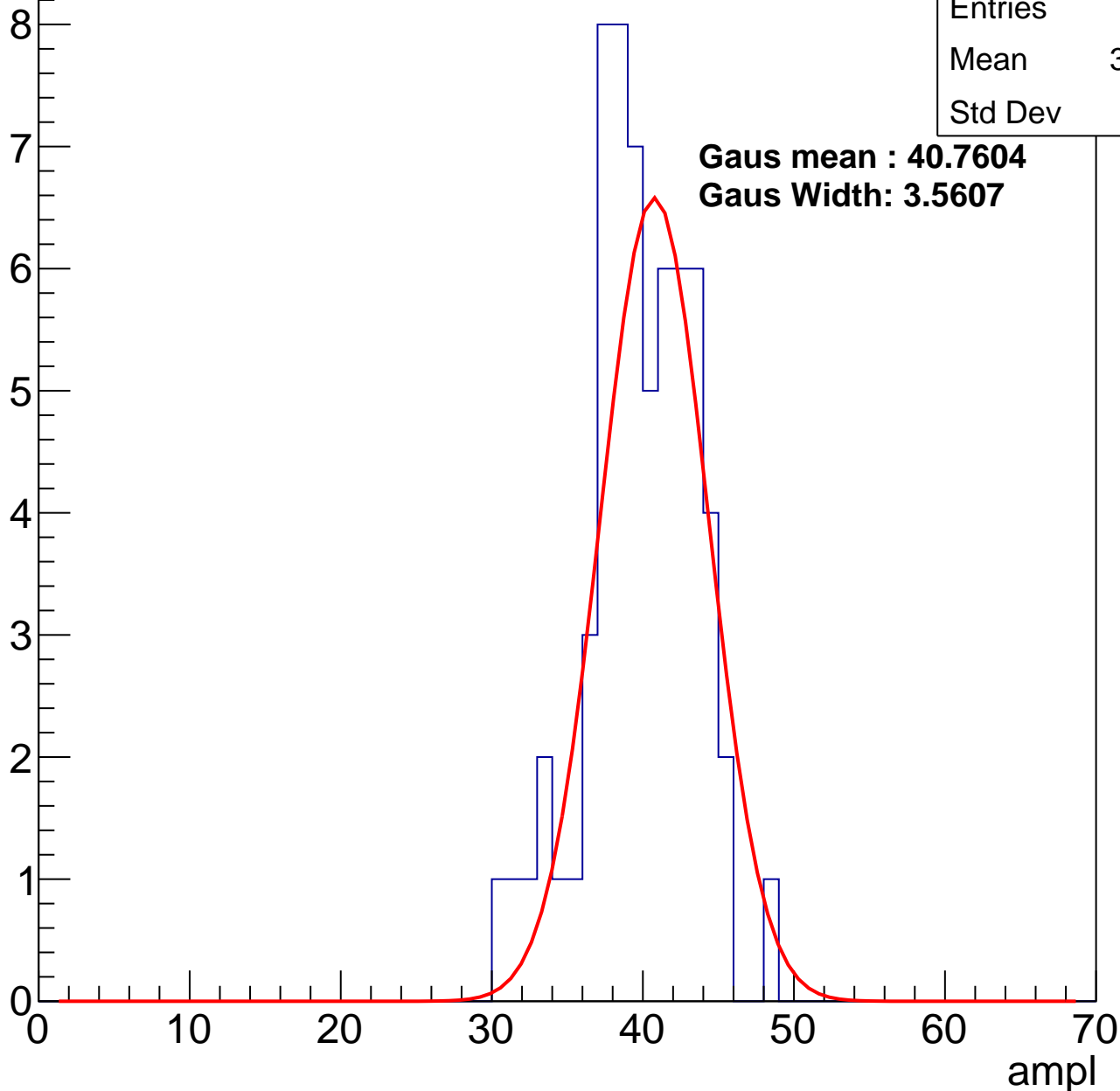
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	39.35
Std Dev	3.56

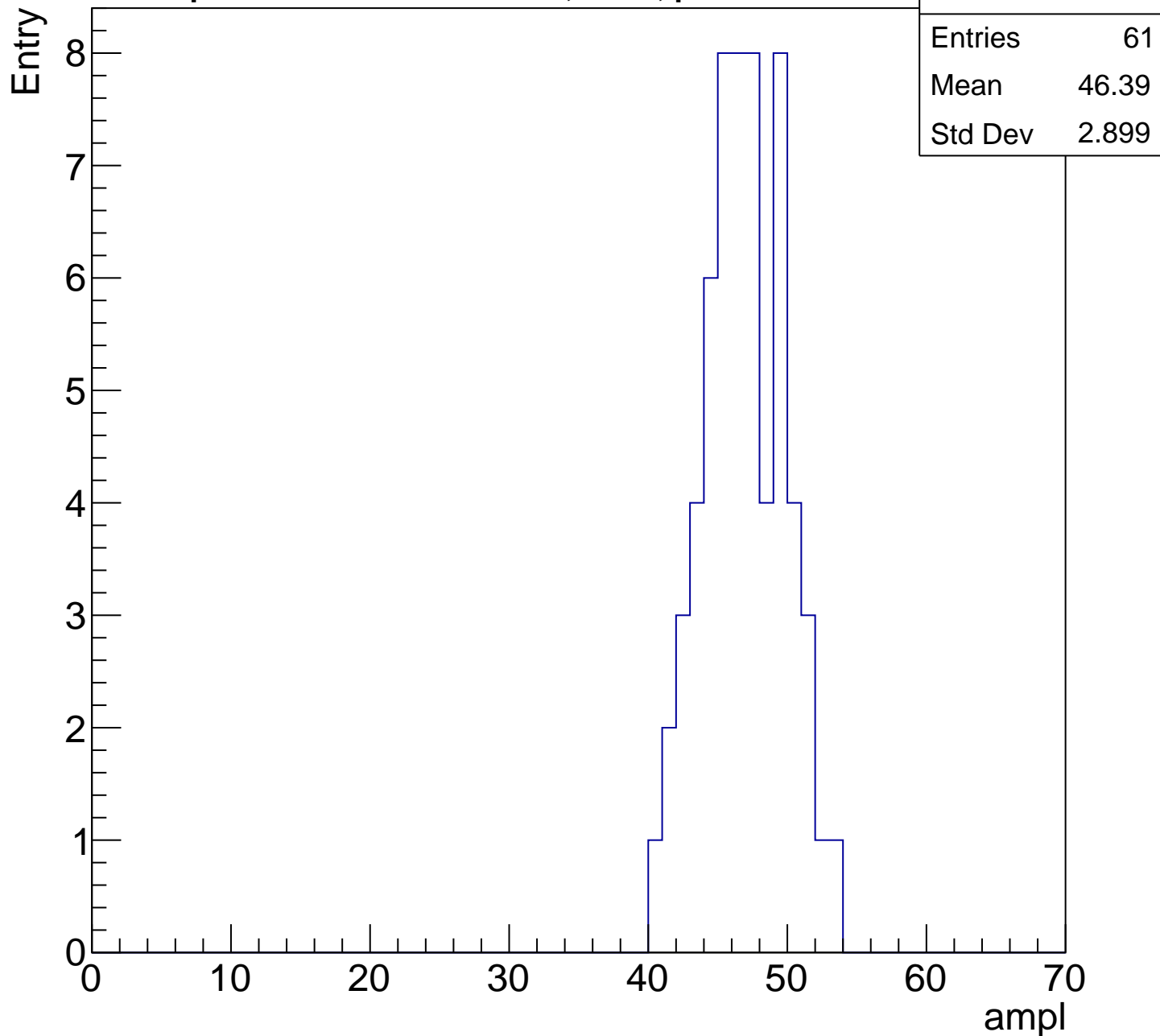
**Gaus mean : 40.7604**

**Gaus Width: 3.5607**



# B1L000S, U1-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

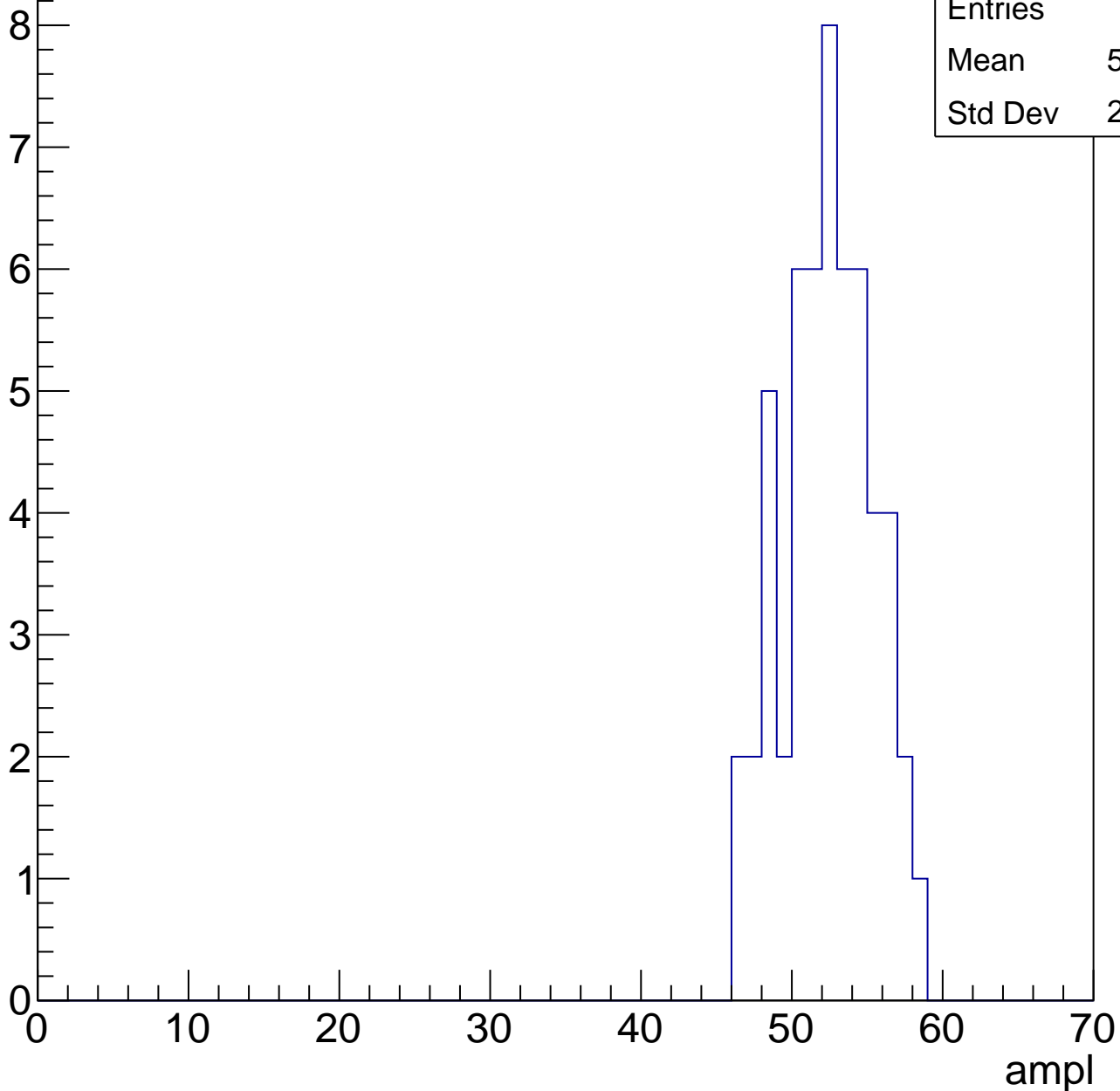


# B1L000S, U1-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

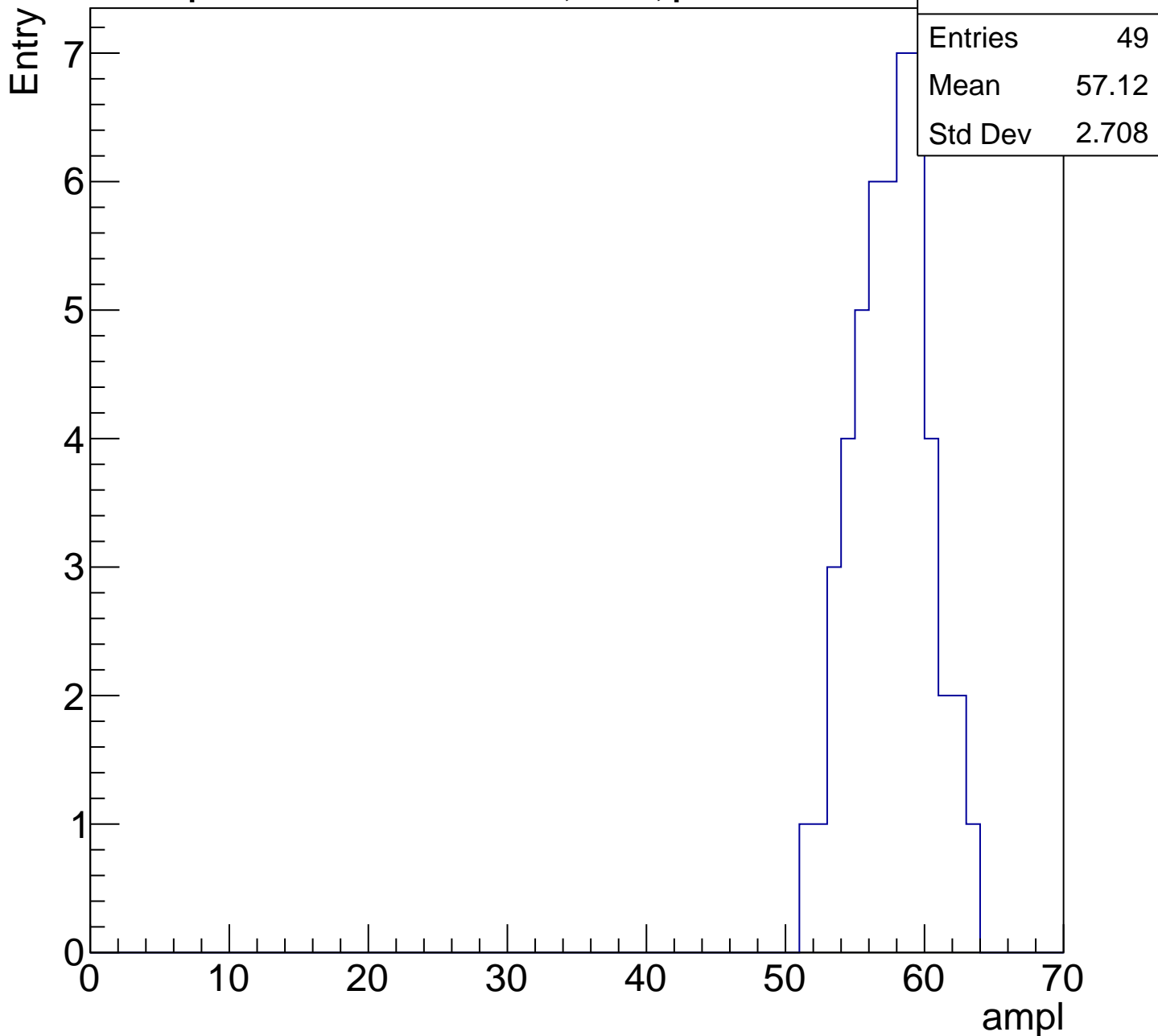
Entry

Entries	54
Mean	51.93
Std Dev	2.937



# B1L000S, U1-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

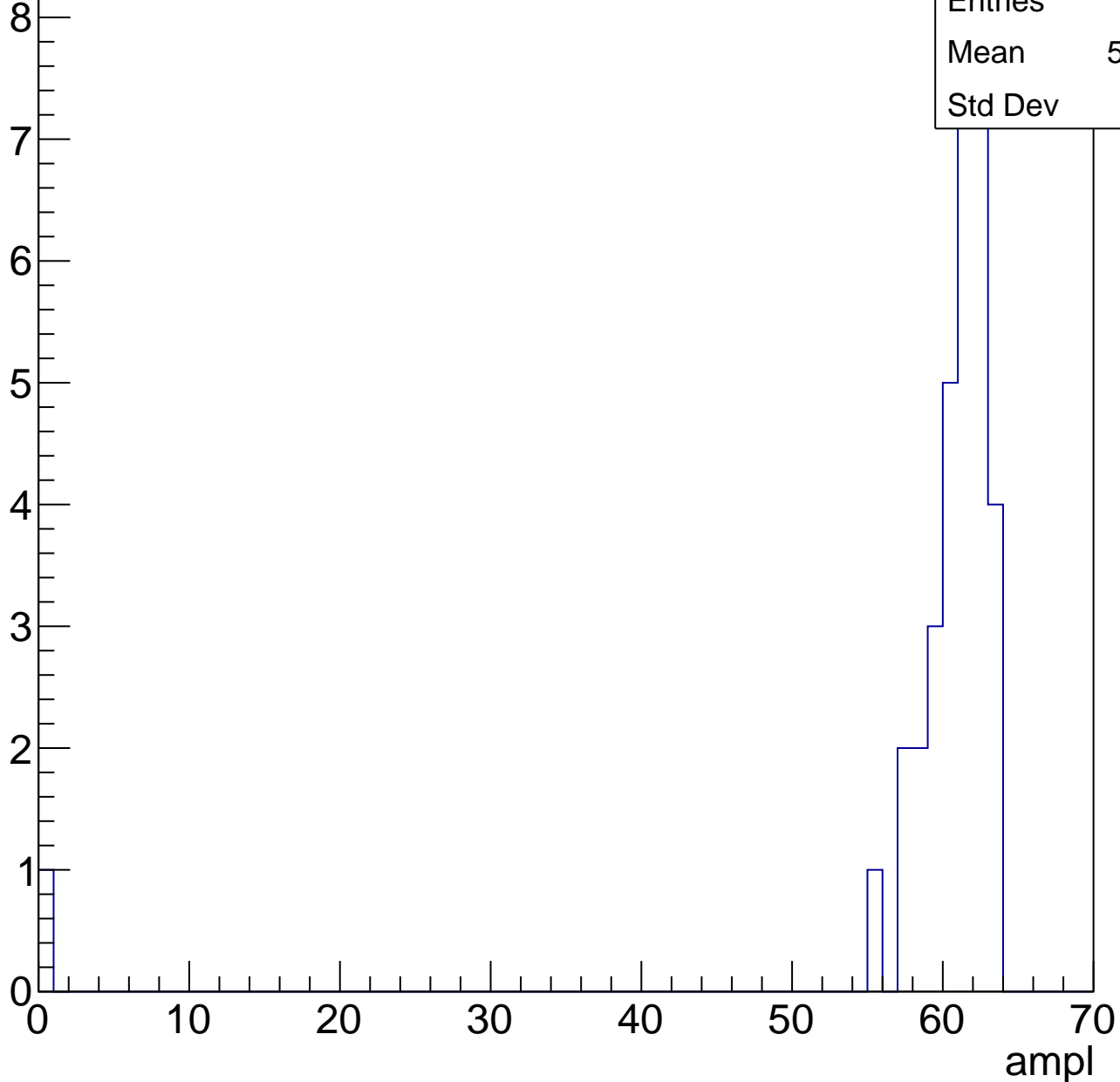


# B1L000S, U1-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	58.76
Std Dev	10.4



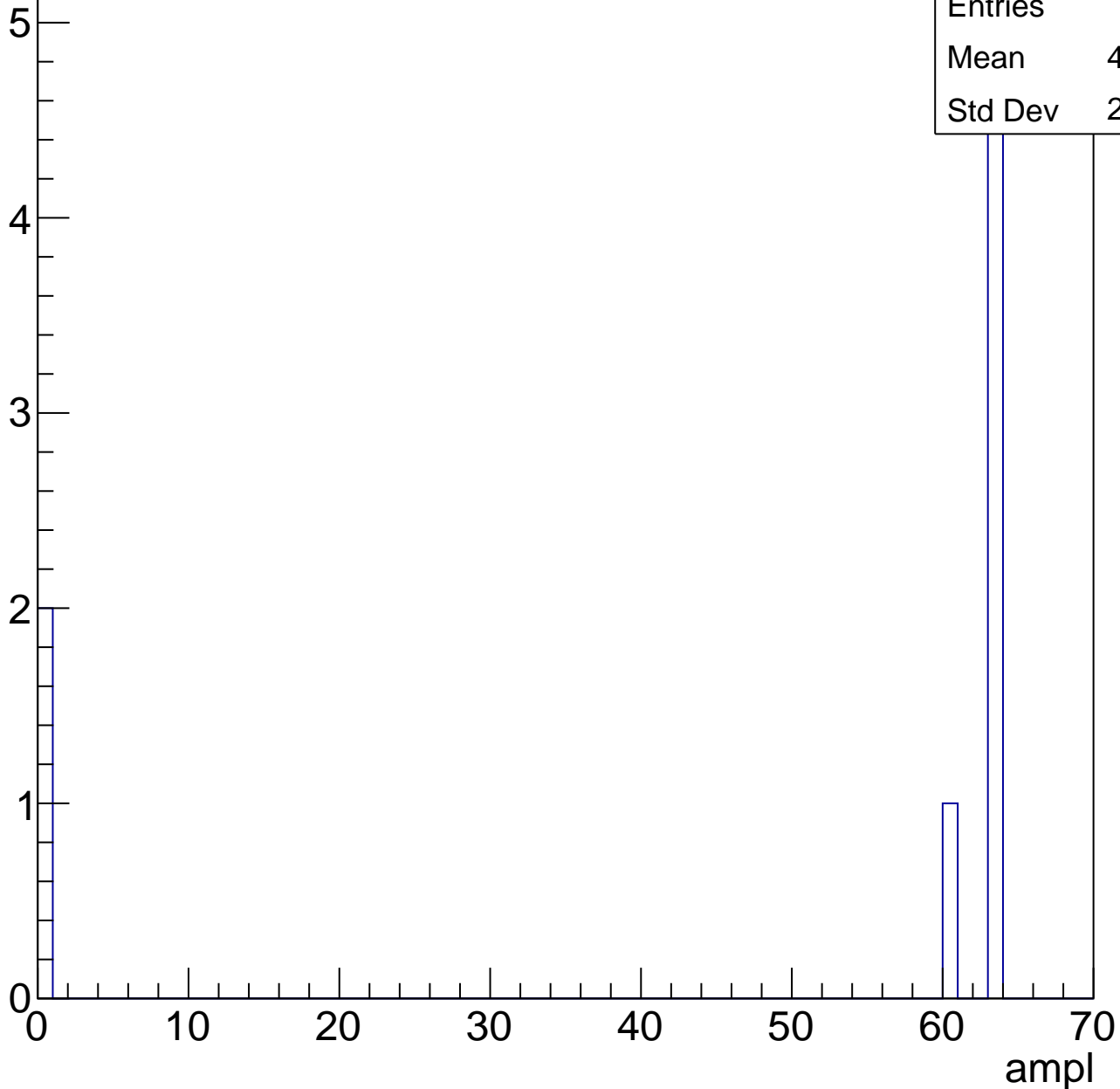


# B1L000S, U1-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	46.88
Std Dev	27.08



# B1L000S, U1-ch40, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

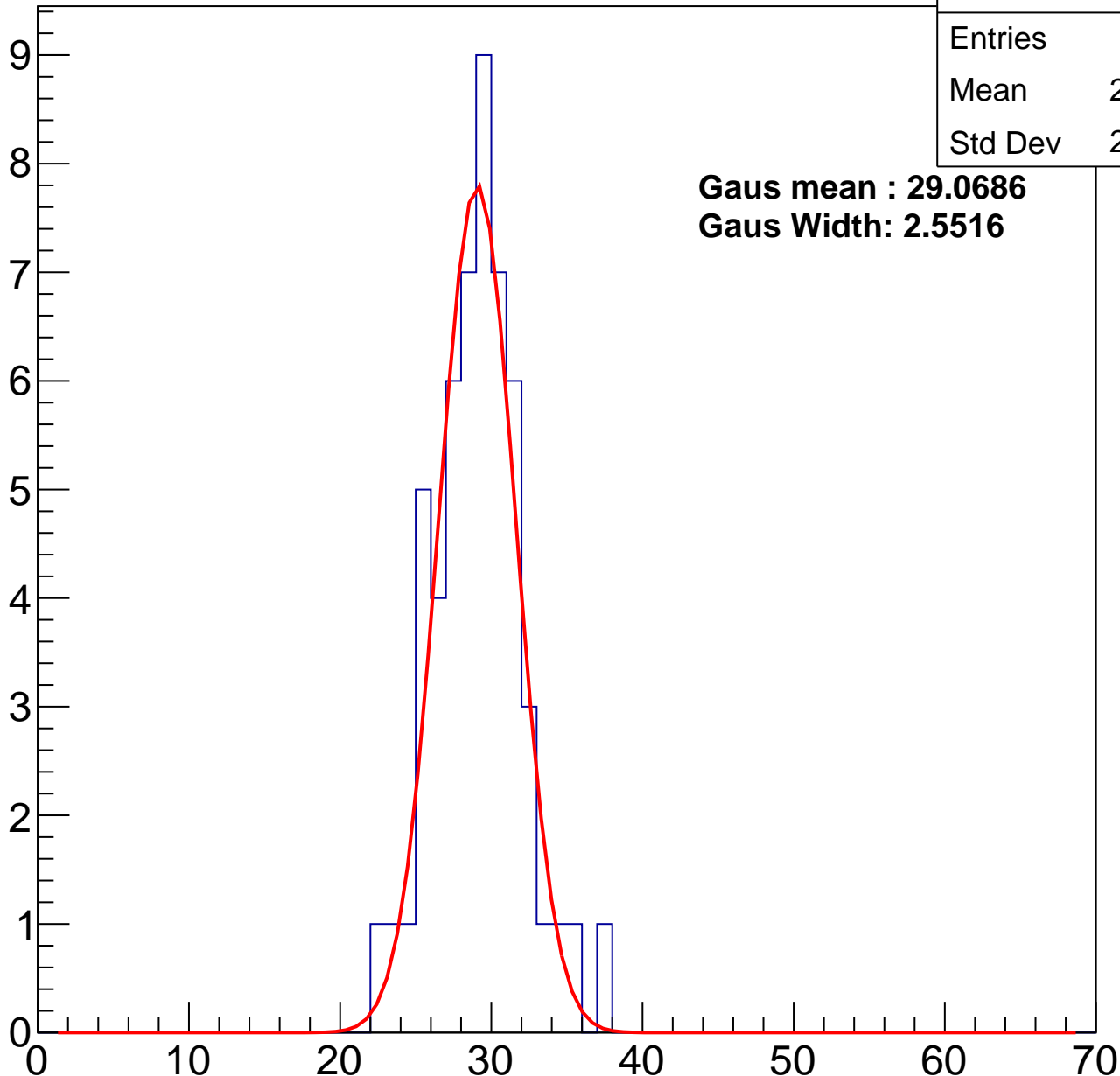
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	28.67
Std Dev	2.887

**Gaus mean : 29.0686**

**Gaus Width: 2.5516**

ampl



# B1L000S, U1-ch40, adc1

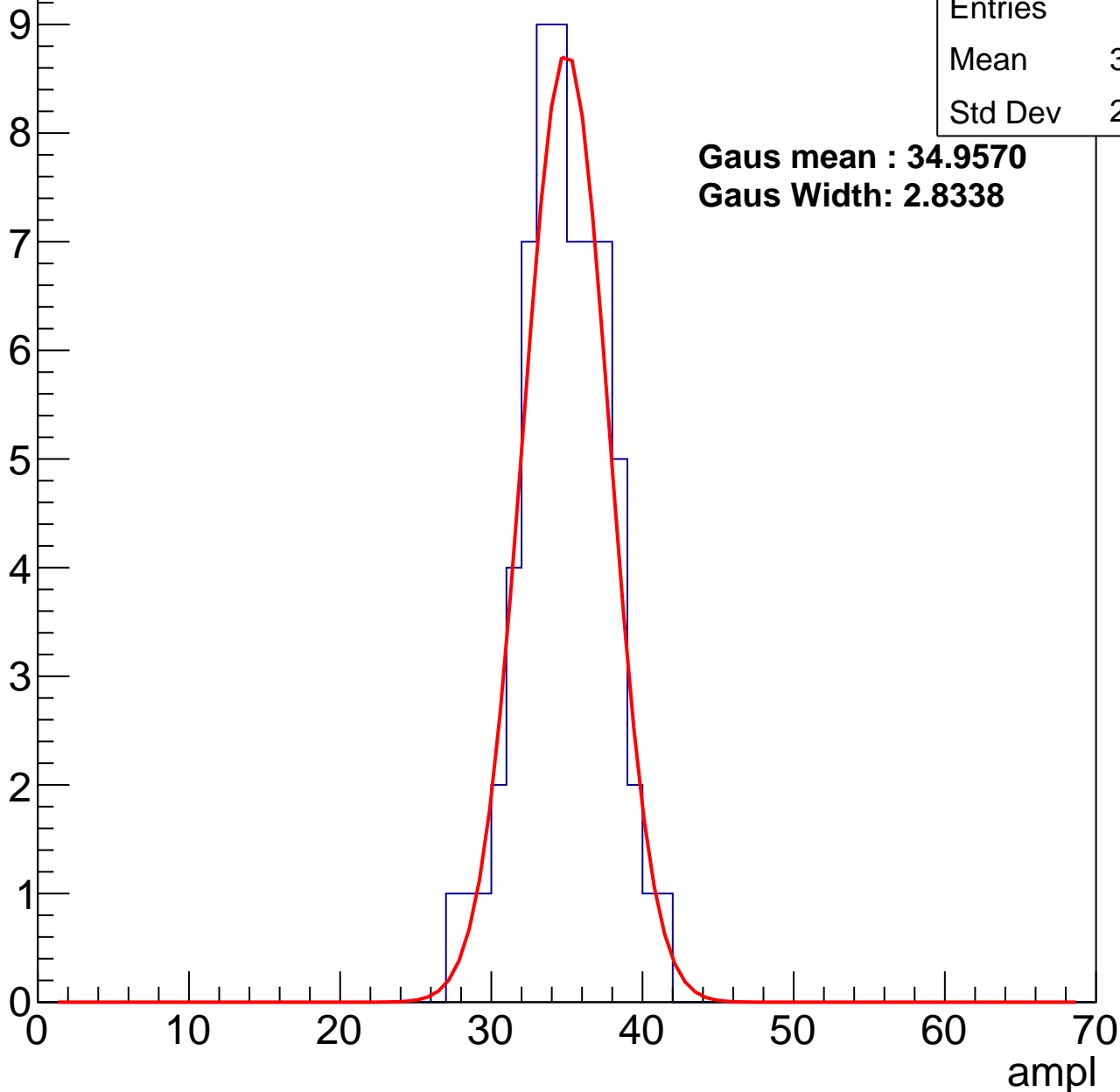
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.38
Std Dev	2.848

**Gaus mean : 34.9570**

**Gaus Width: 2.8338**



# B1L000S, U1-ch40, adc2

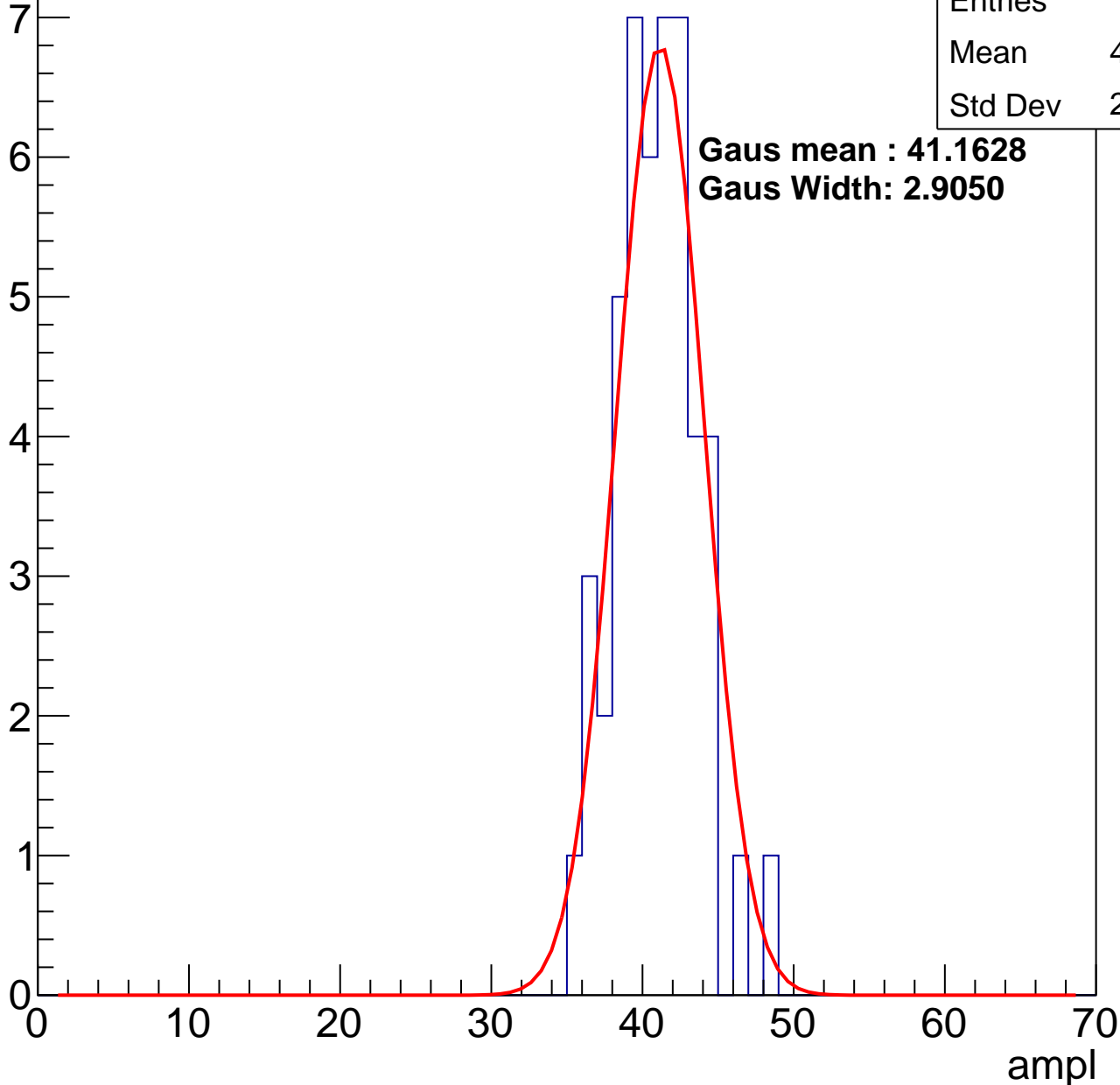
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	40.48
Std Dev	2.669

**Gaus mean : 41.1628**

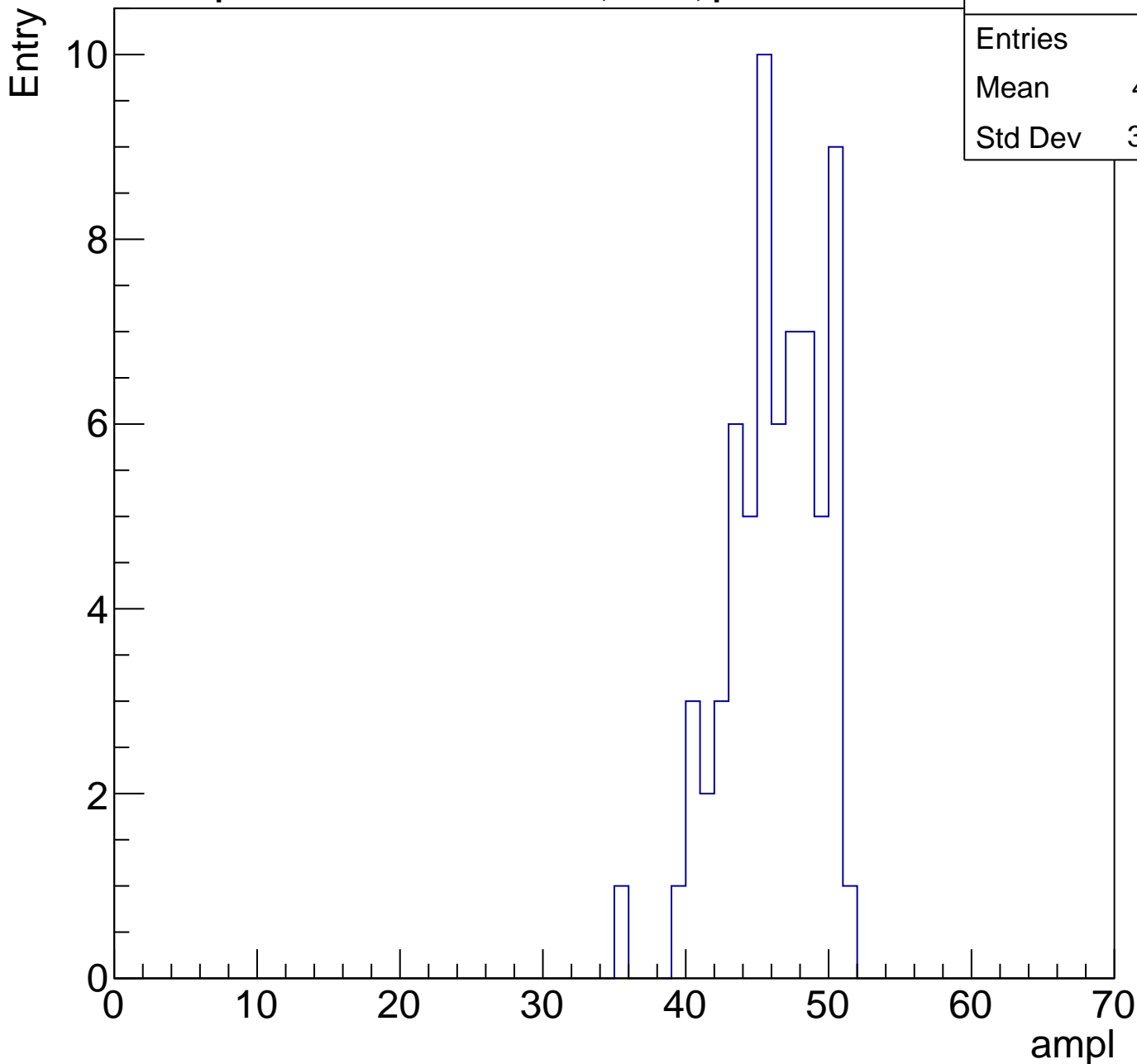
**Gaus Width: 2.9050**



# B1L000S, U1-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

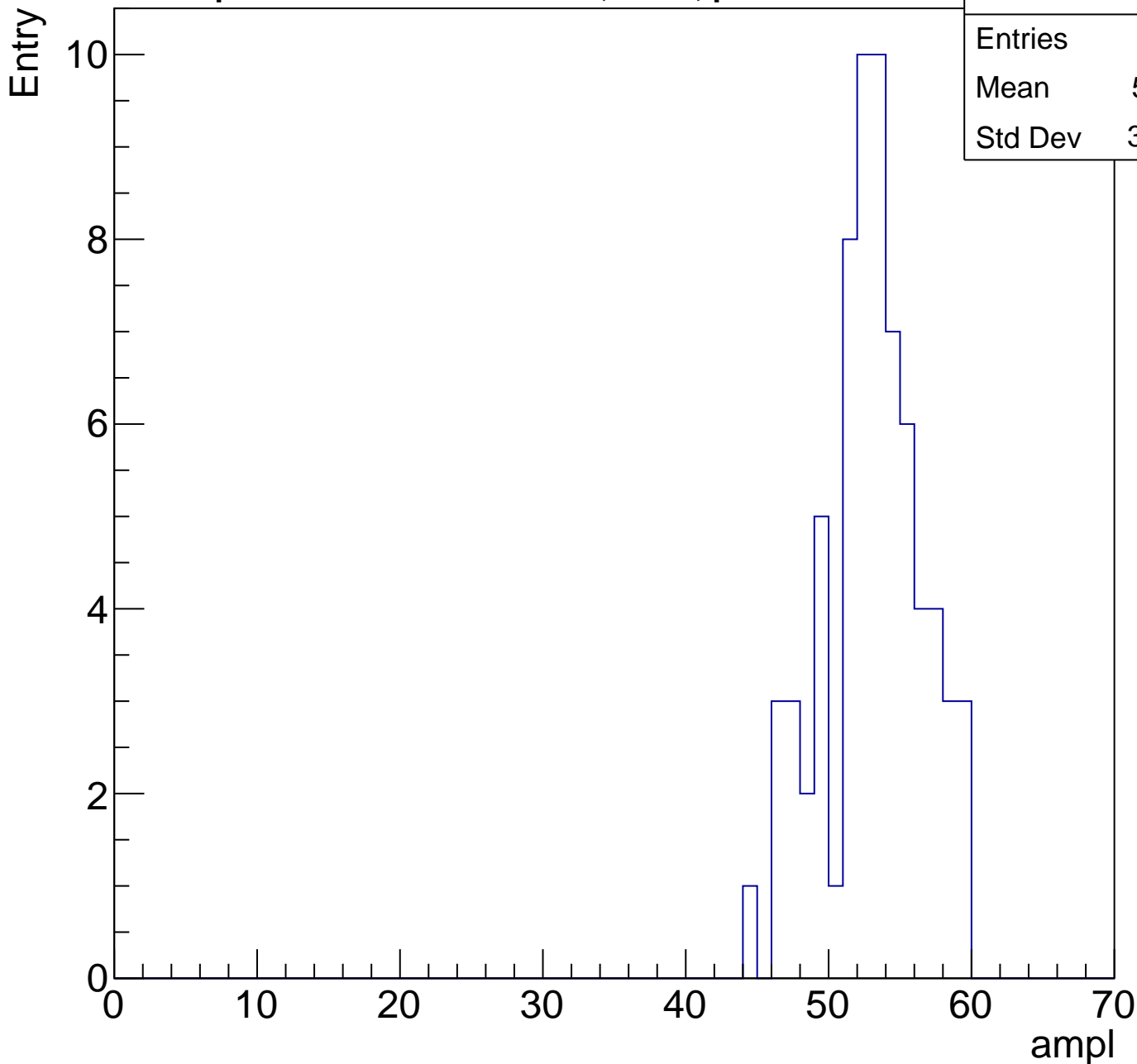
Entries	66
Mean	45.71
Std Dev	3.265



# B1L000S, U1-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	52.61
Std Dev	3.424

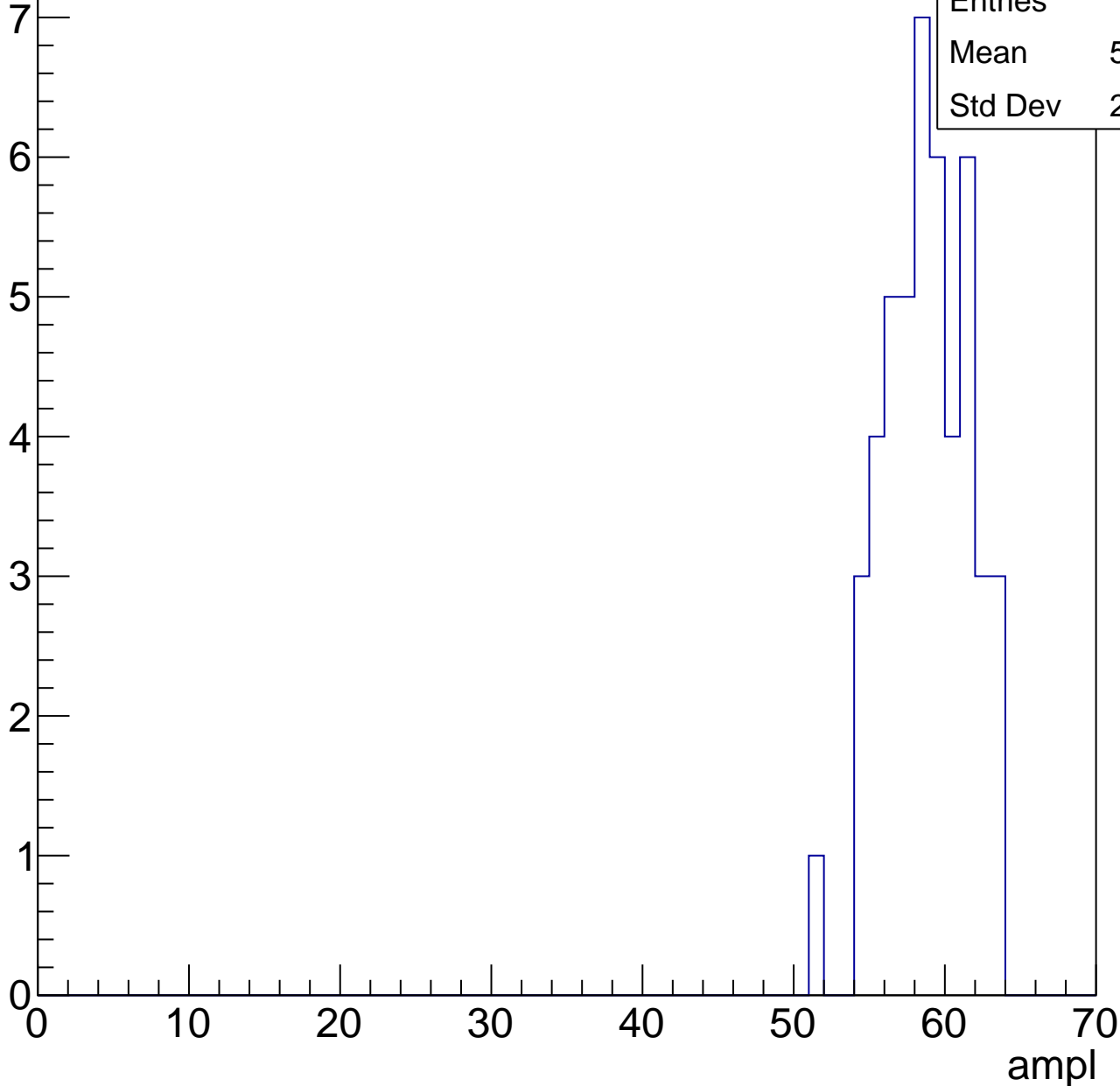


# B1L000S, U1-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	58.28
Std Dev	2.742

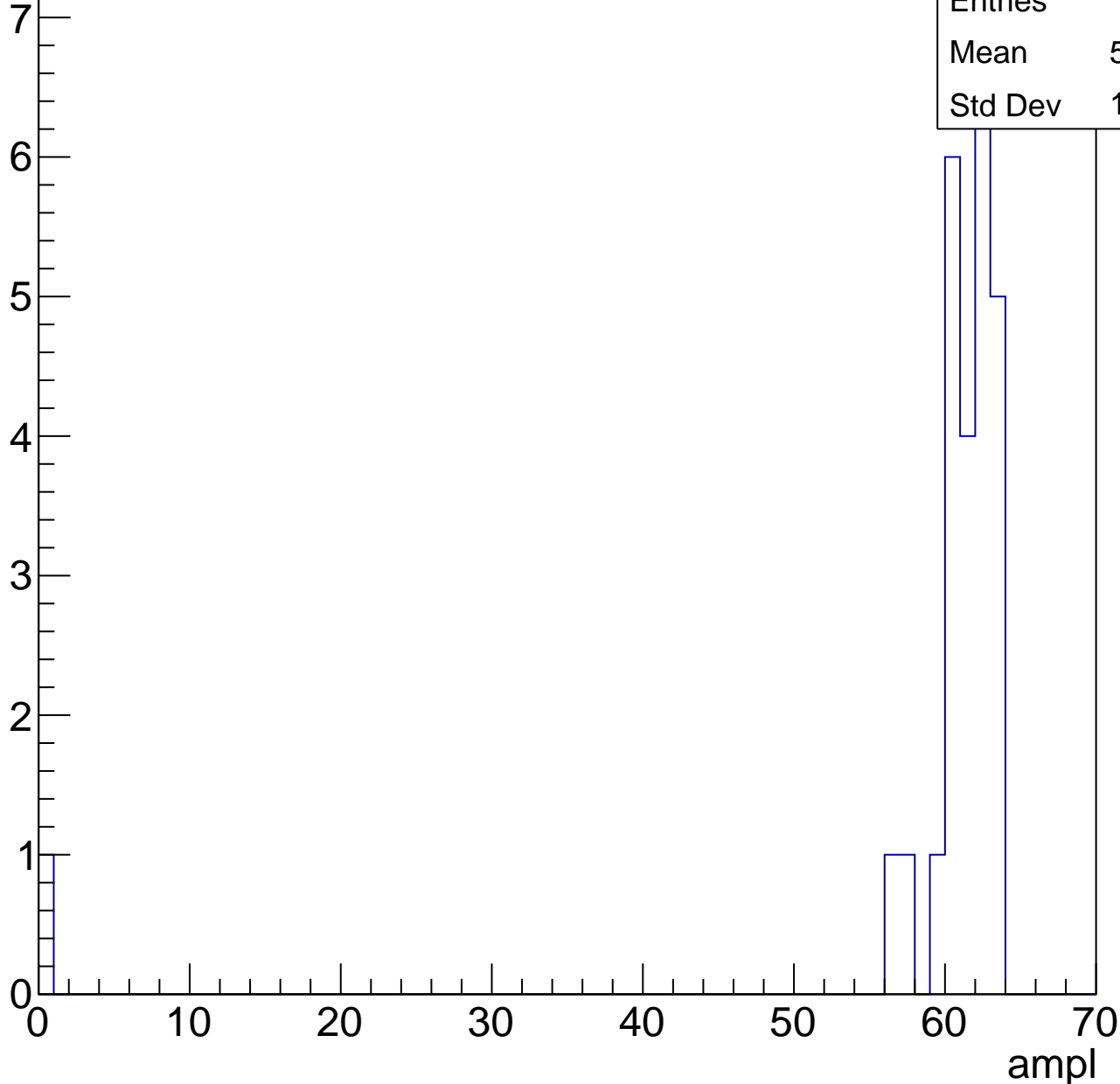


# B1L000S, U1-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.65
Std Dev	11.86





# B1L000S, U1-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch41, adc0

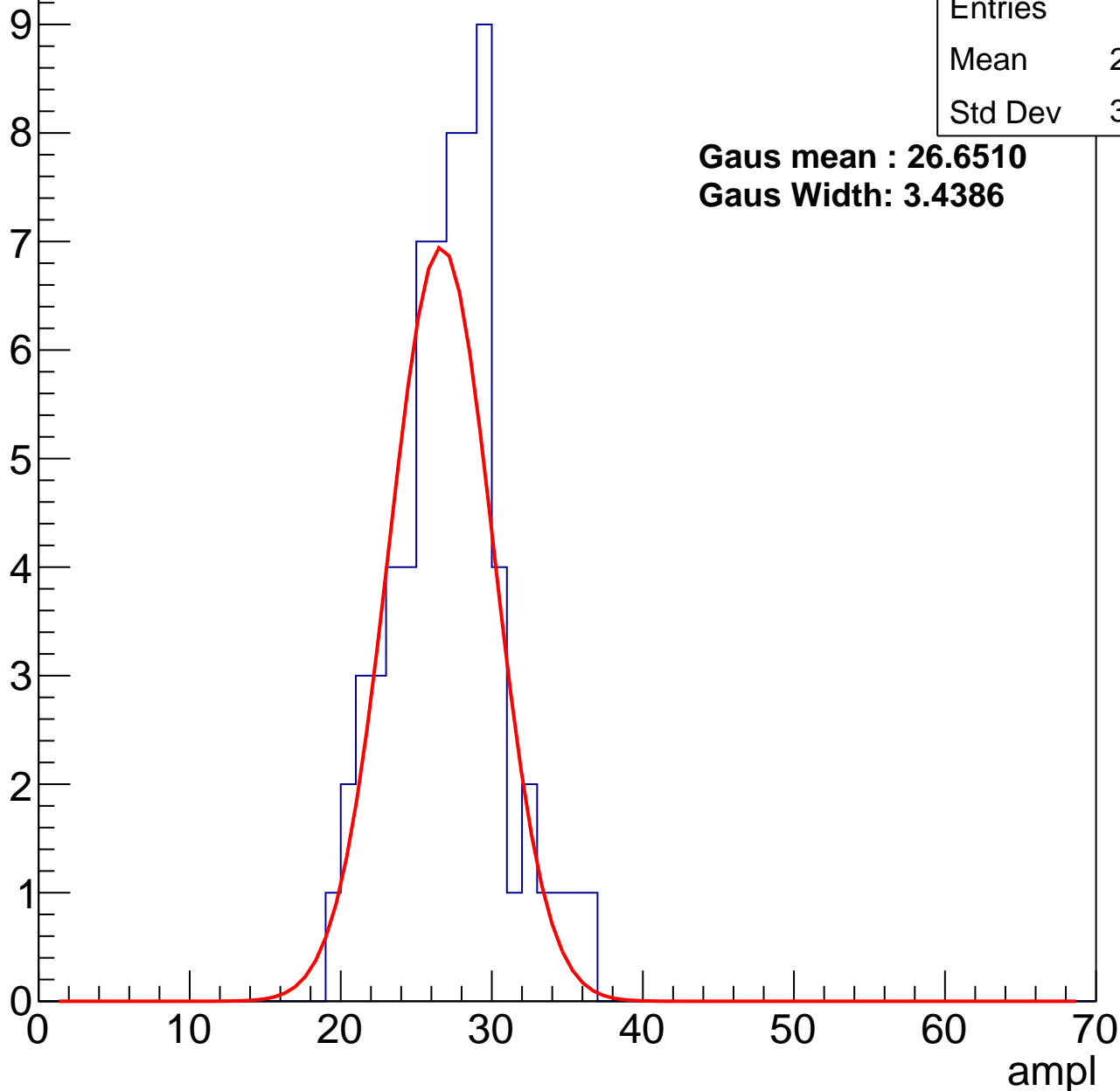
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	26.67
Std Dev	3.559

**Gaus mean : 26.6510**

**Gaus Width: 3.4386**



# B1L000S, U1-ch41, adc1

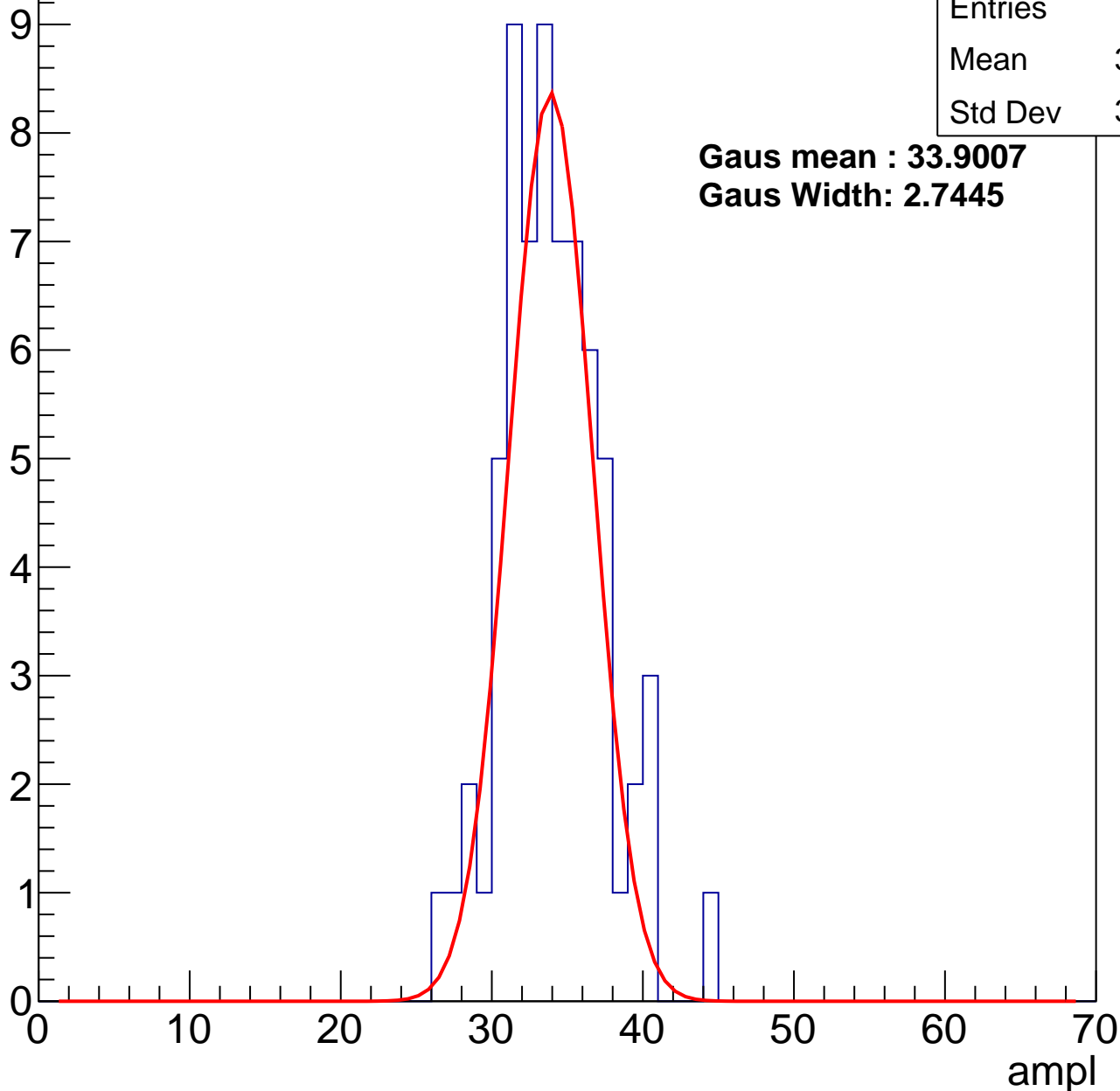
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	33.61
Std Dev	3.341

**Gaus mean : 33.9007**

**Gaus Width: 2.7445**



# B1L000S, U1-ch41, adc2

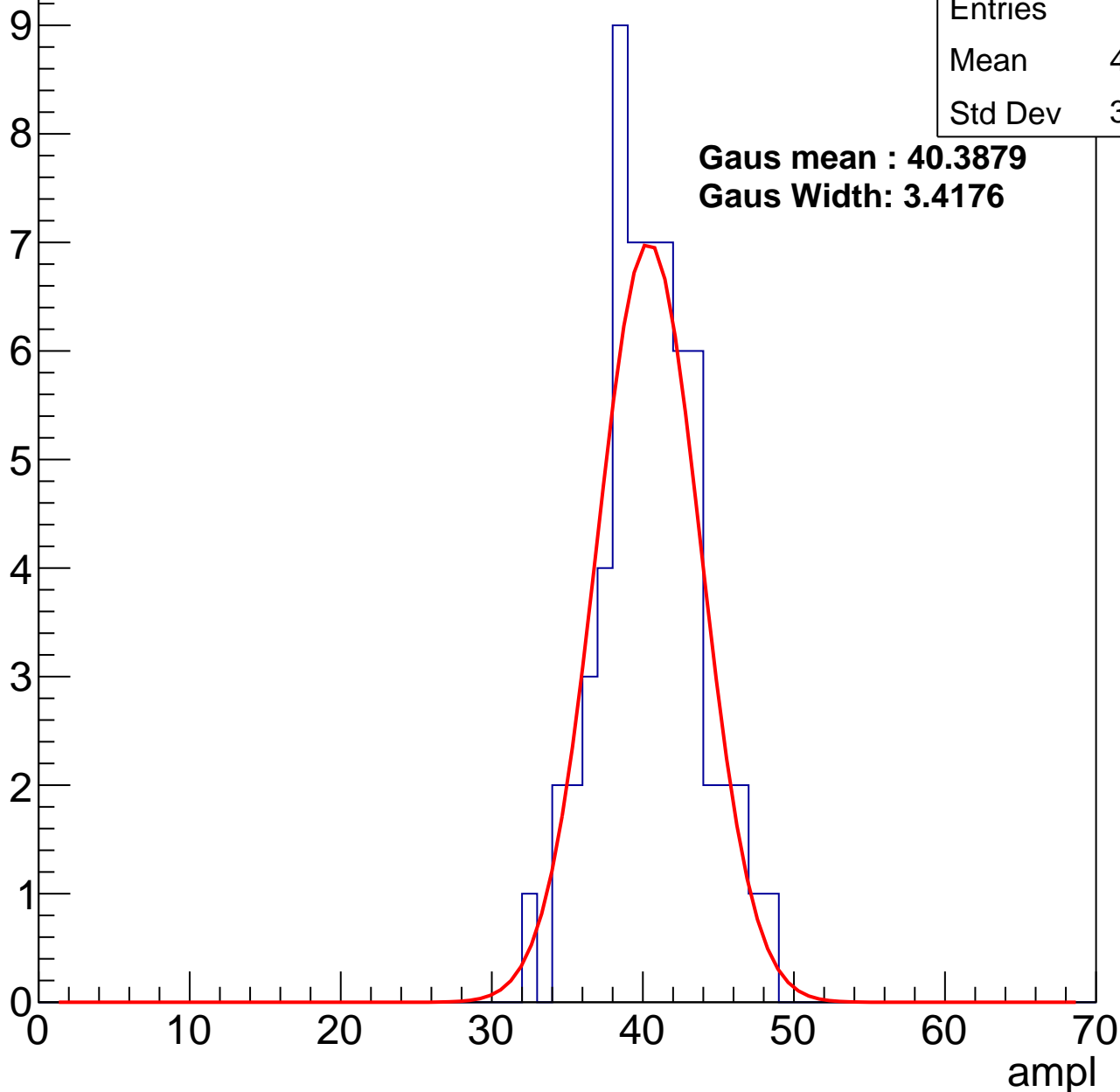
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.05
Std Dev	3.275

**Gaus mean : 40.3879**

**Gaus Width: 3.4176**

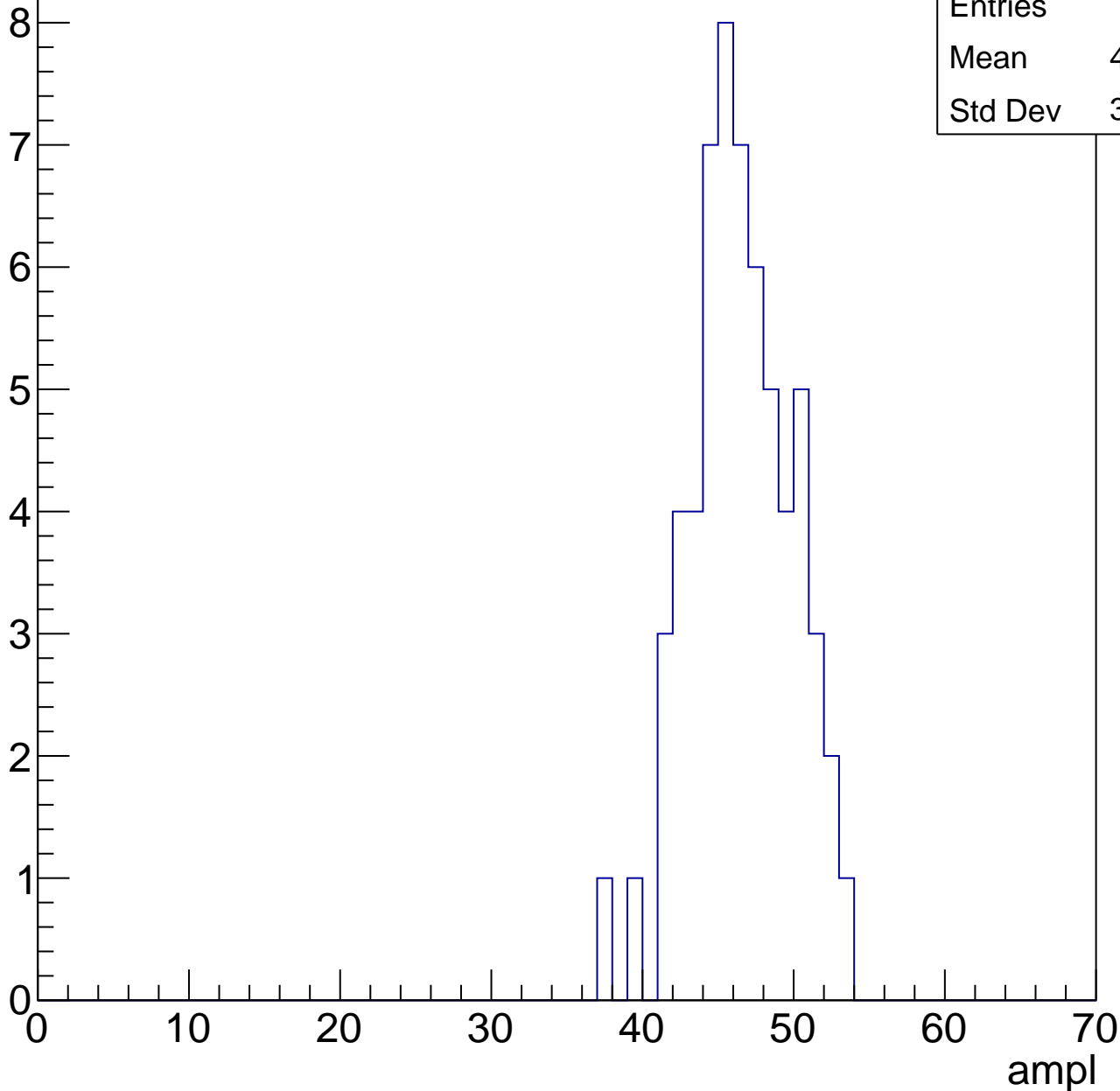


# B1L000S, U1-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

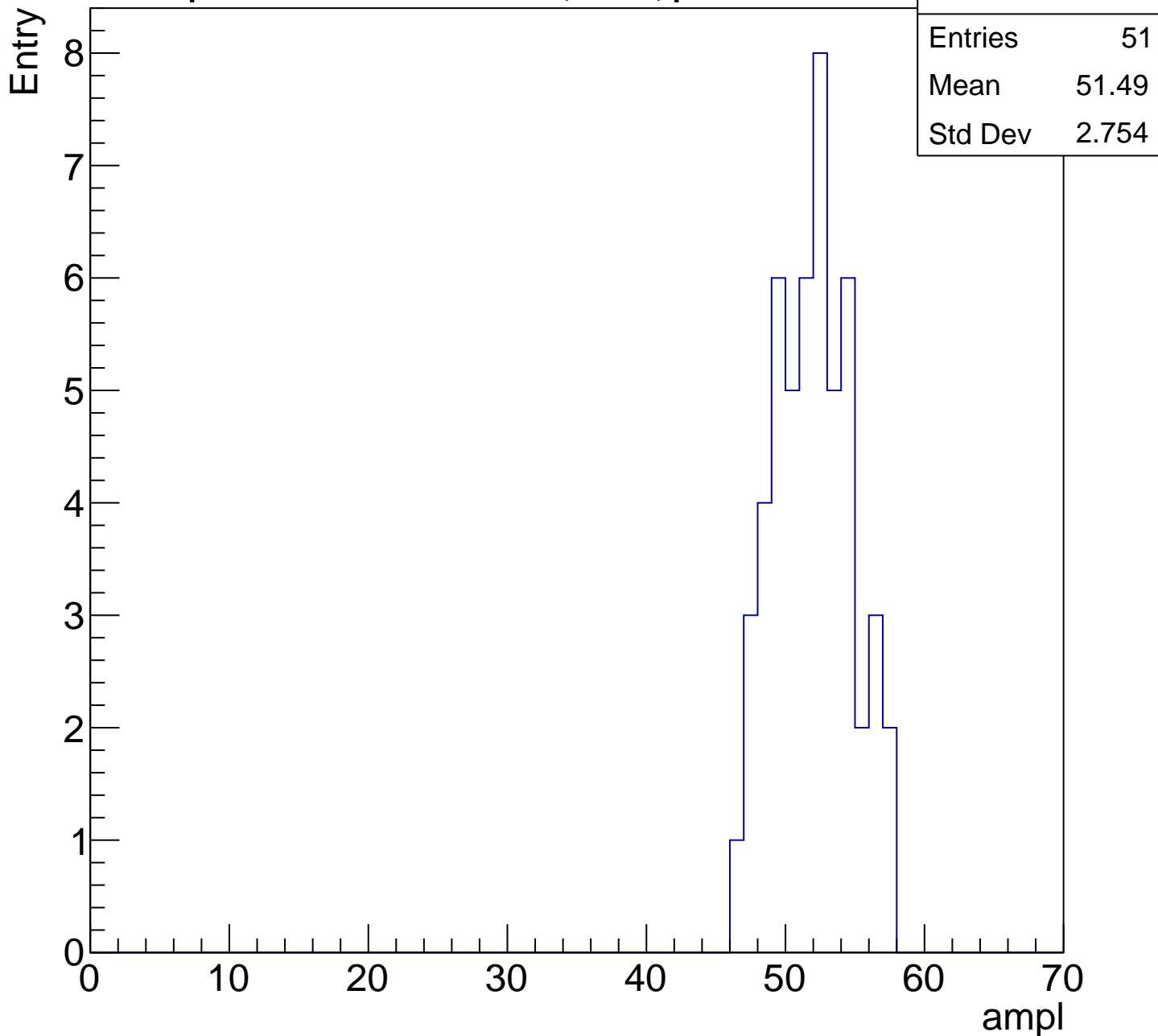
Entry

Entries	61
Mean	46.02
Std Dev	3.336



# B1L000S, U1-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

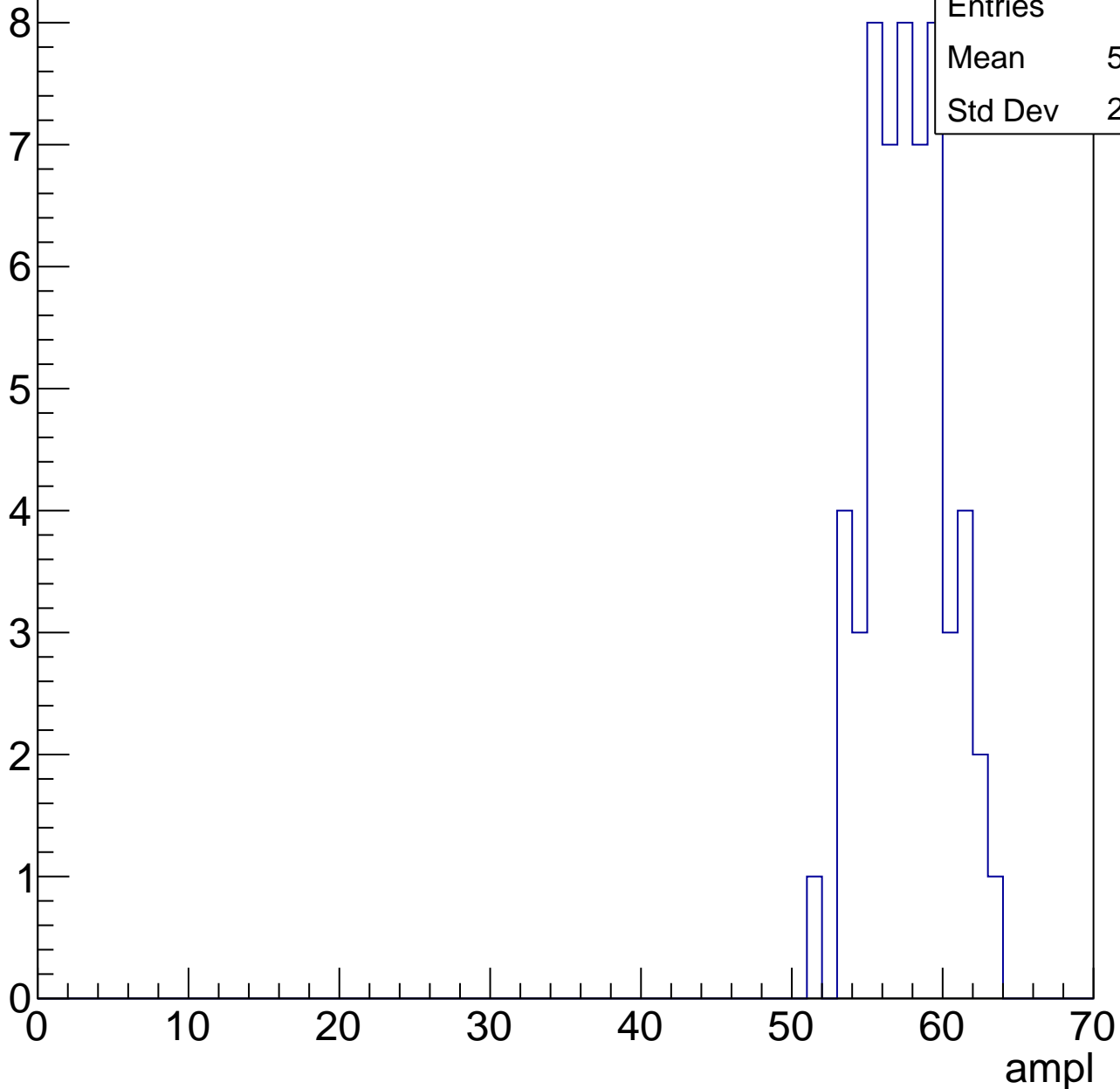


# B1L000S, U1-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	57.18
Std Dev	2.606

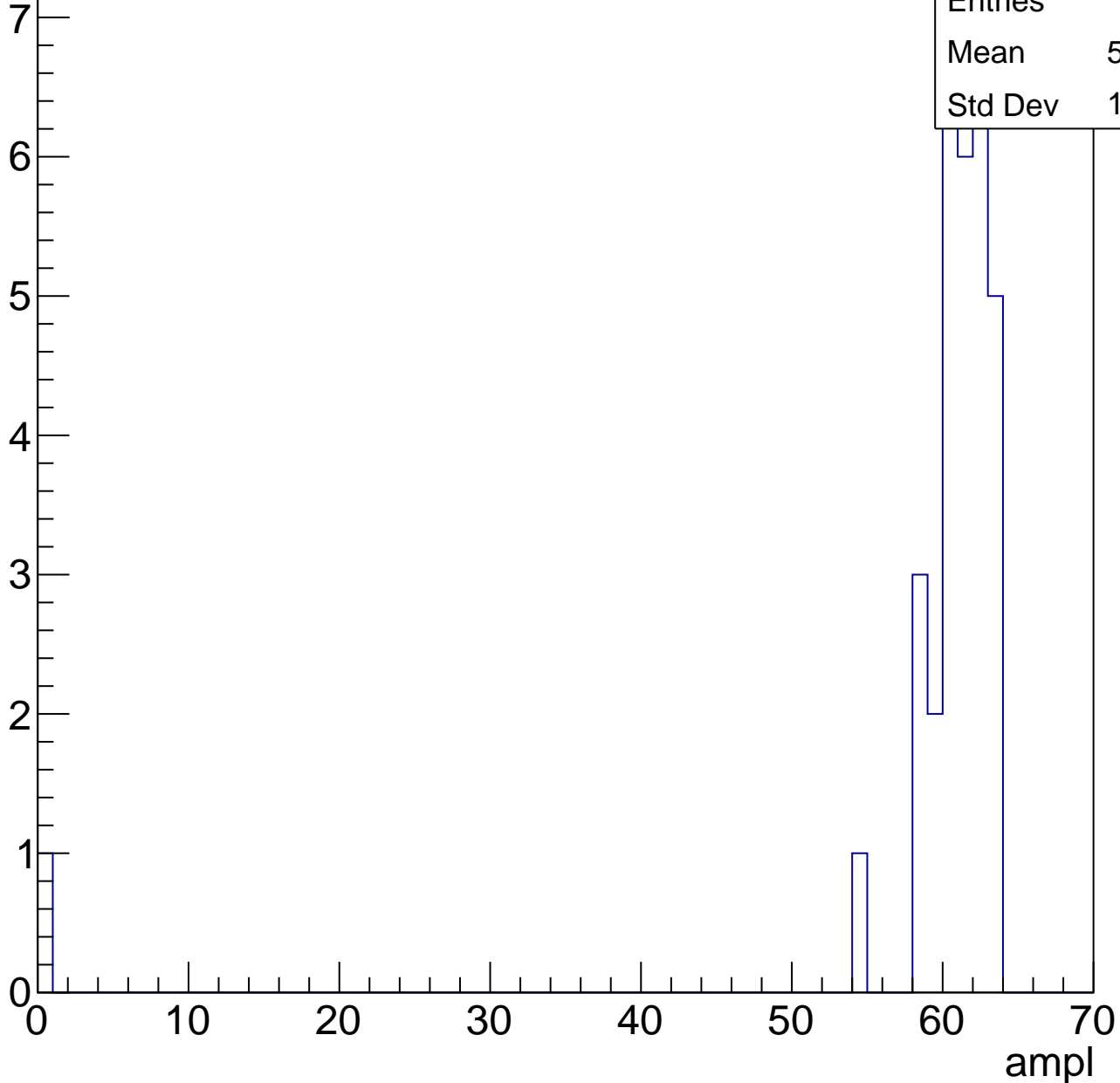


# B1L000S, U1-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	58.78
Std Dev	10.73

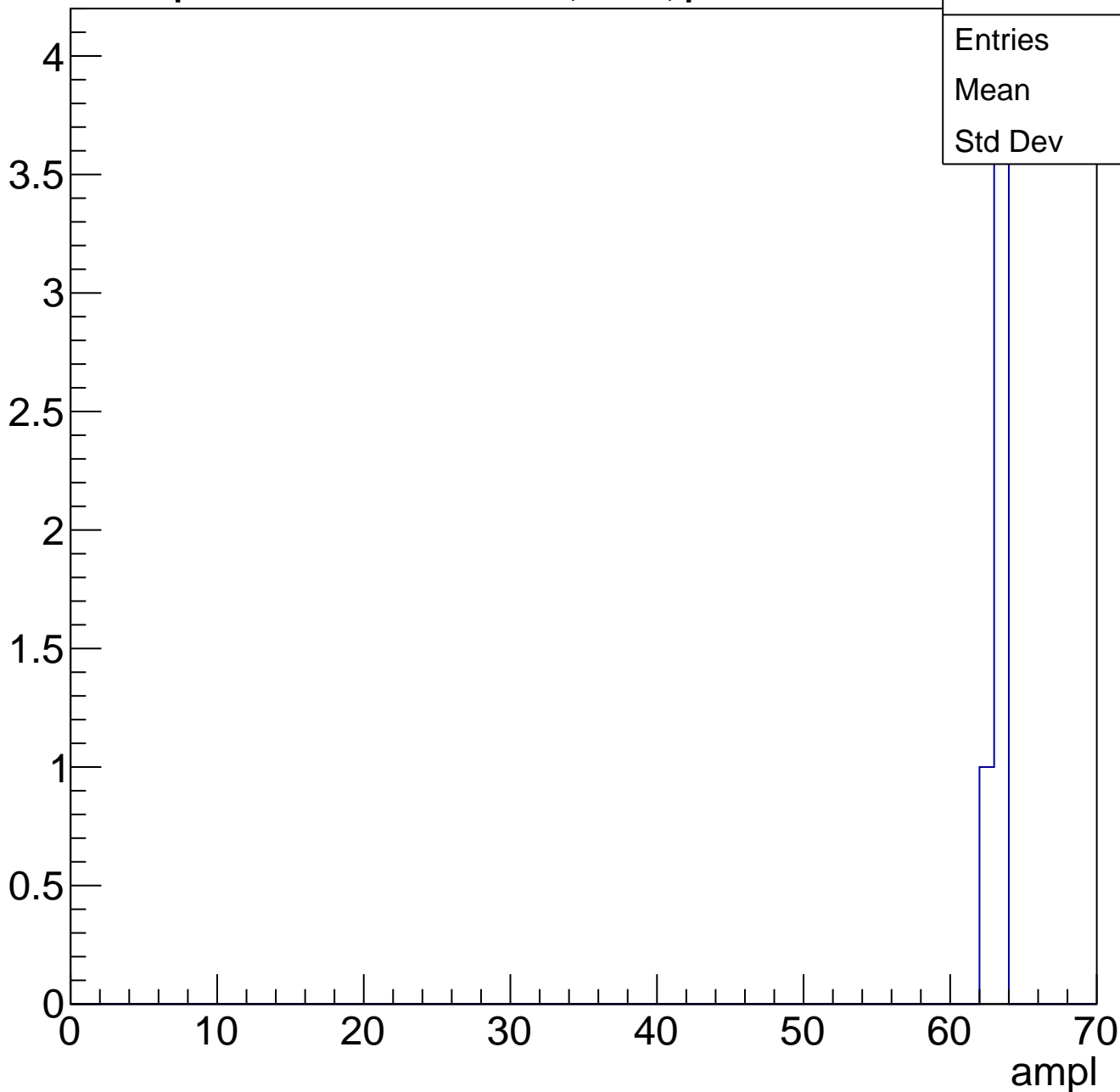




# B1L000S, U1-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	62.8
Std Dev	0.4

# B1L000S, U1-ch42, adc0

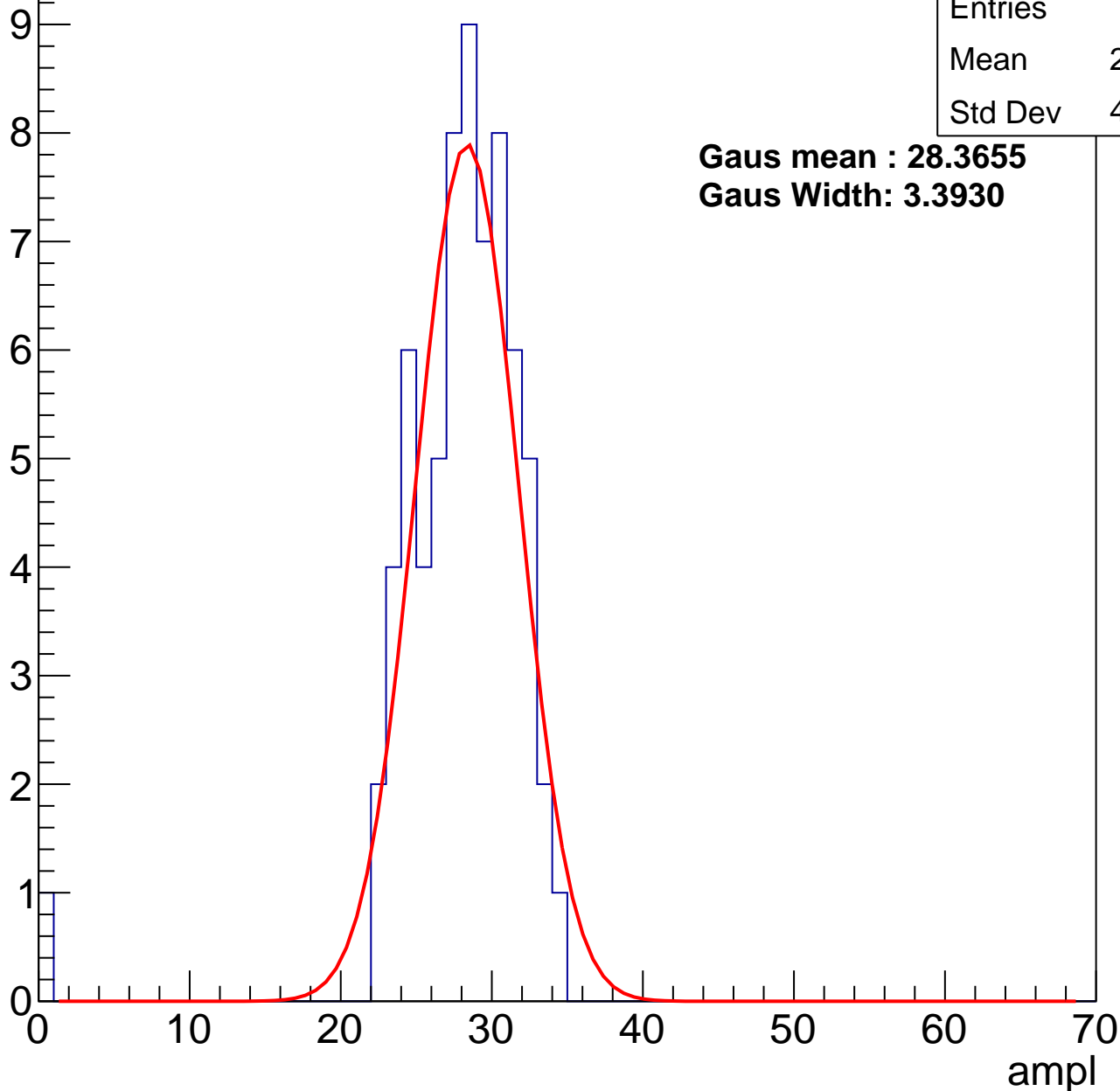
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	27.46
Std Dev	4.464

**Gaus mean : 28.3655**

**Gaus Width: 3.3930**



# B1L000S, U1-ch42, adc1

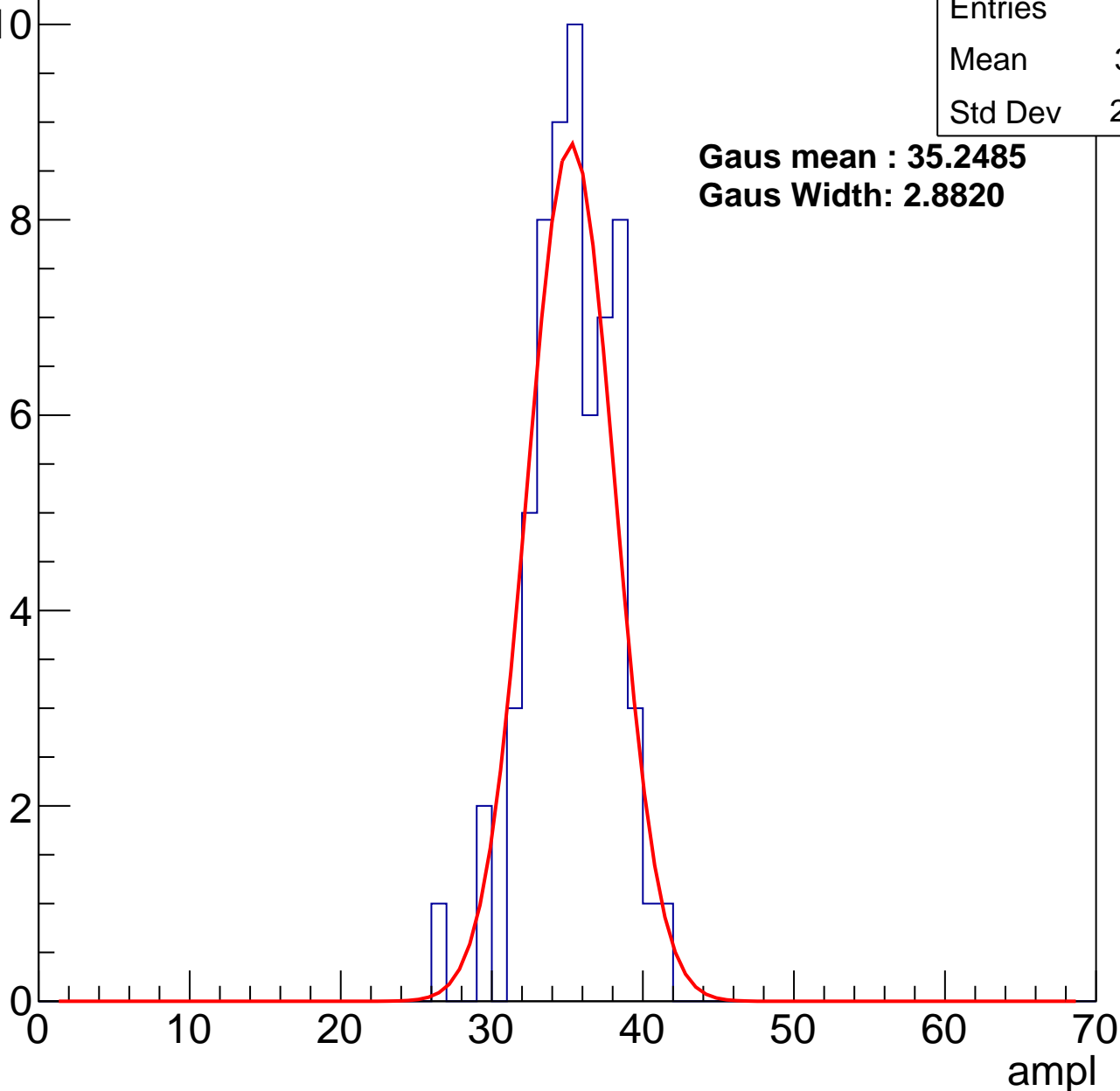
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.91
Std Dev	2.799

**Gaus mean : 35.2485**

**Gaus Width: 2.8820**

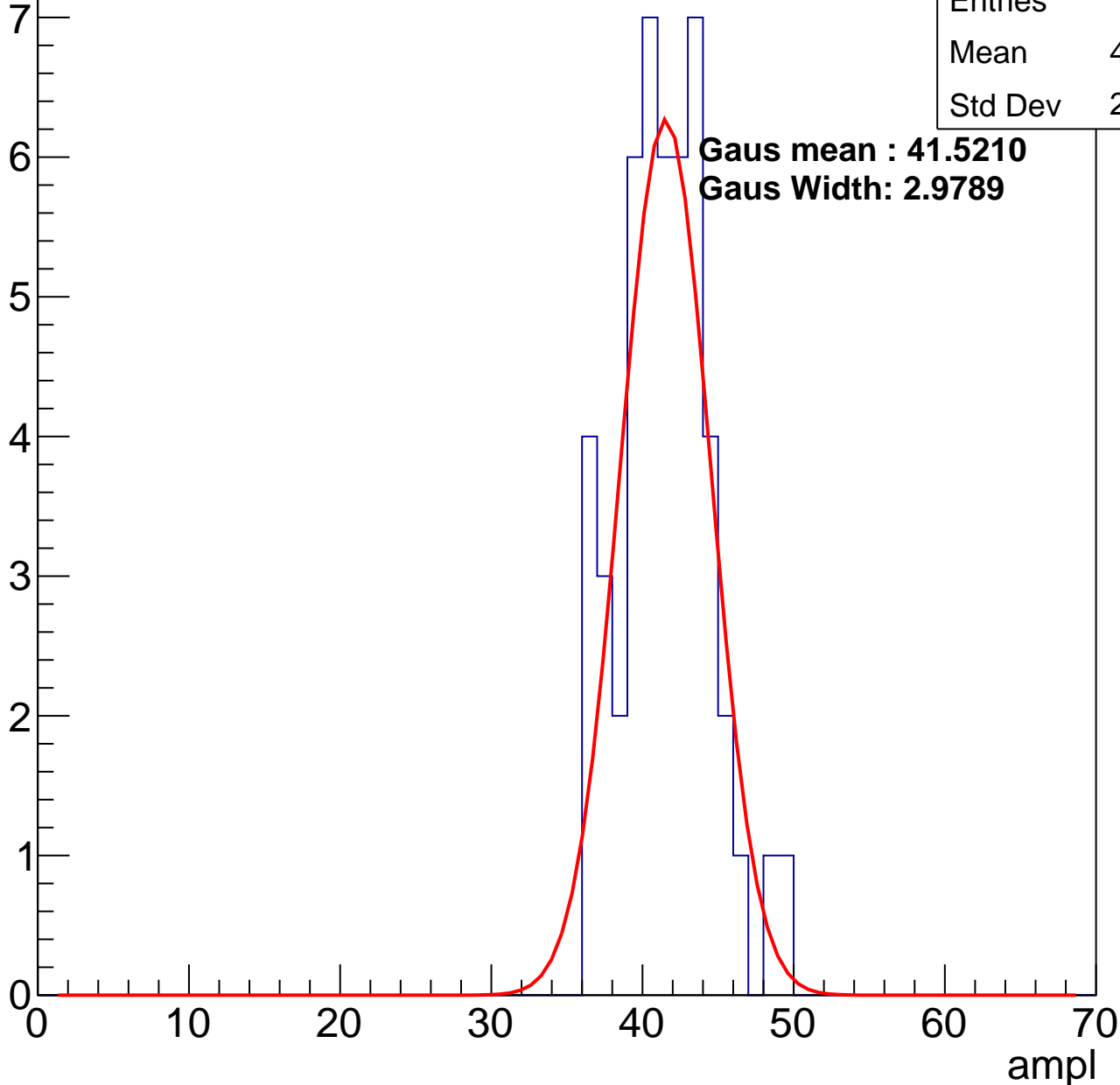


# B1L000S, U1-ch42, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	41.06
Std Dev	2.956



# B1L000S, U1-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

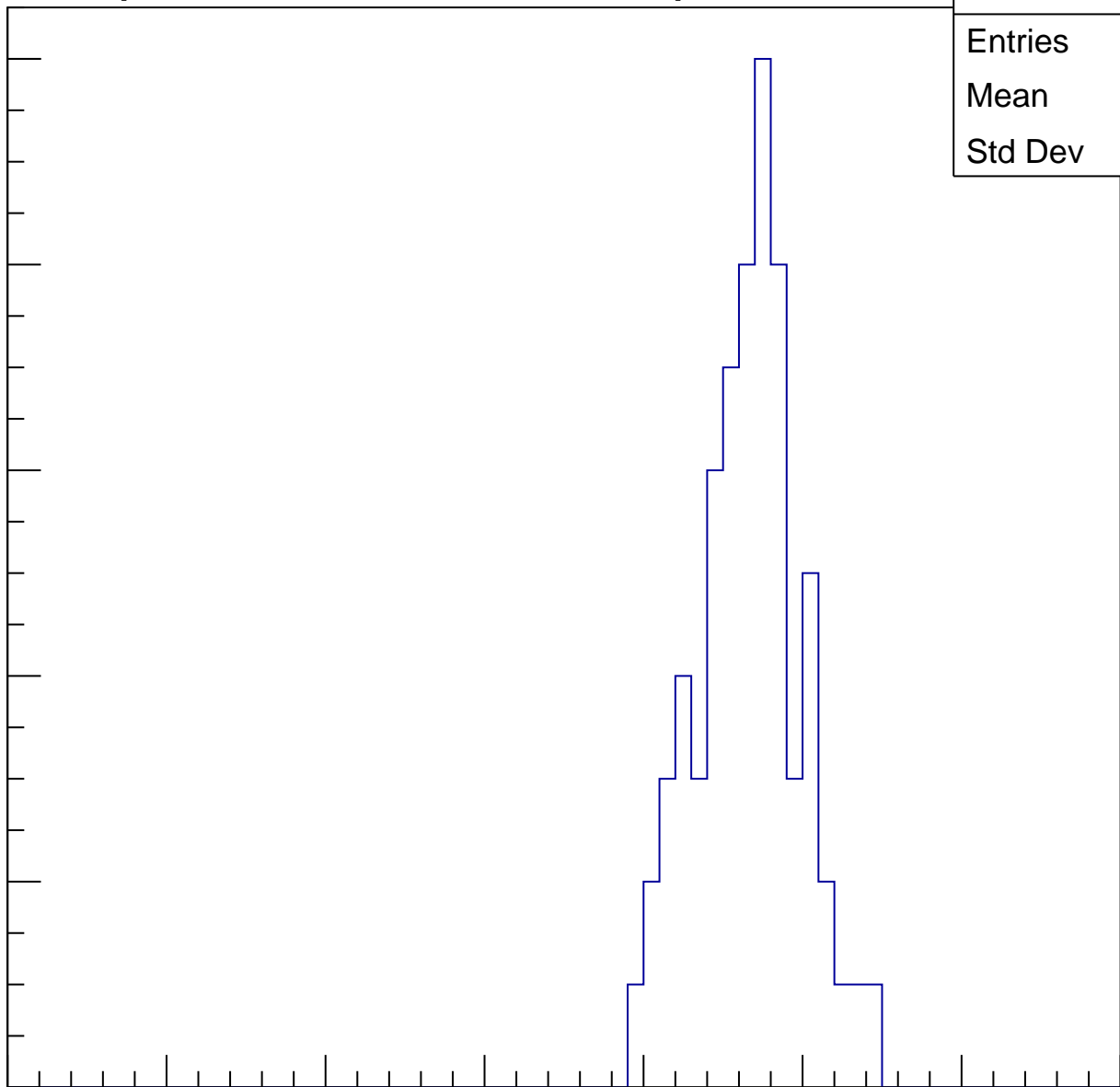
Entries	65
Mean	46.12
Std Dev	3.199

Entry

10  
8  
6  
4  
2  
0

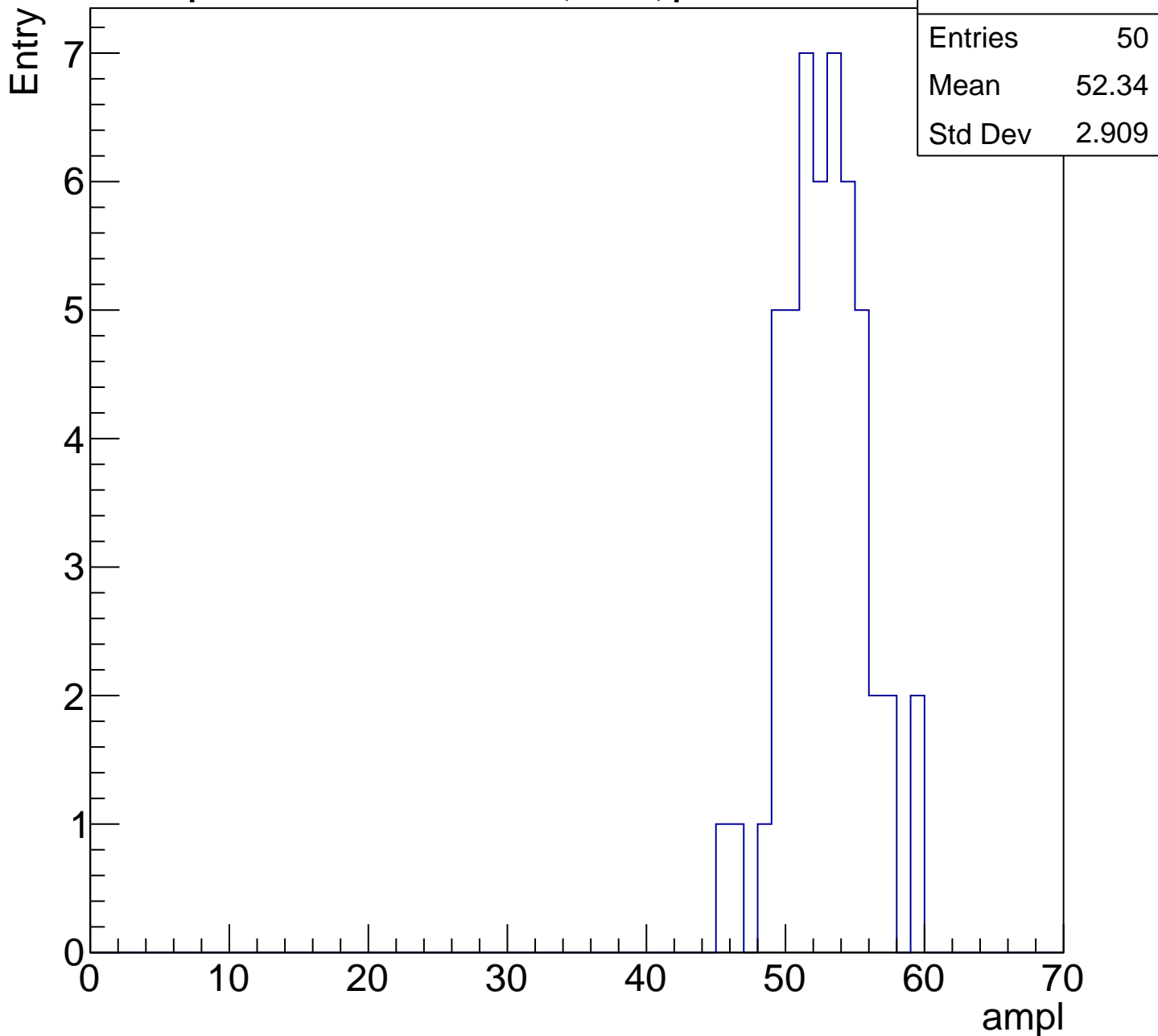
0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

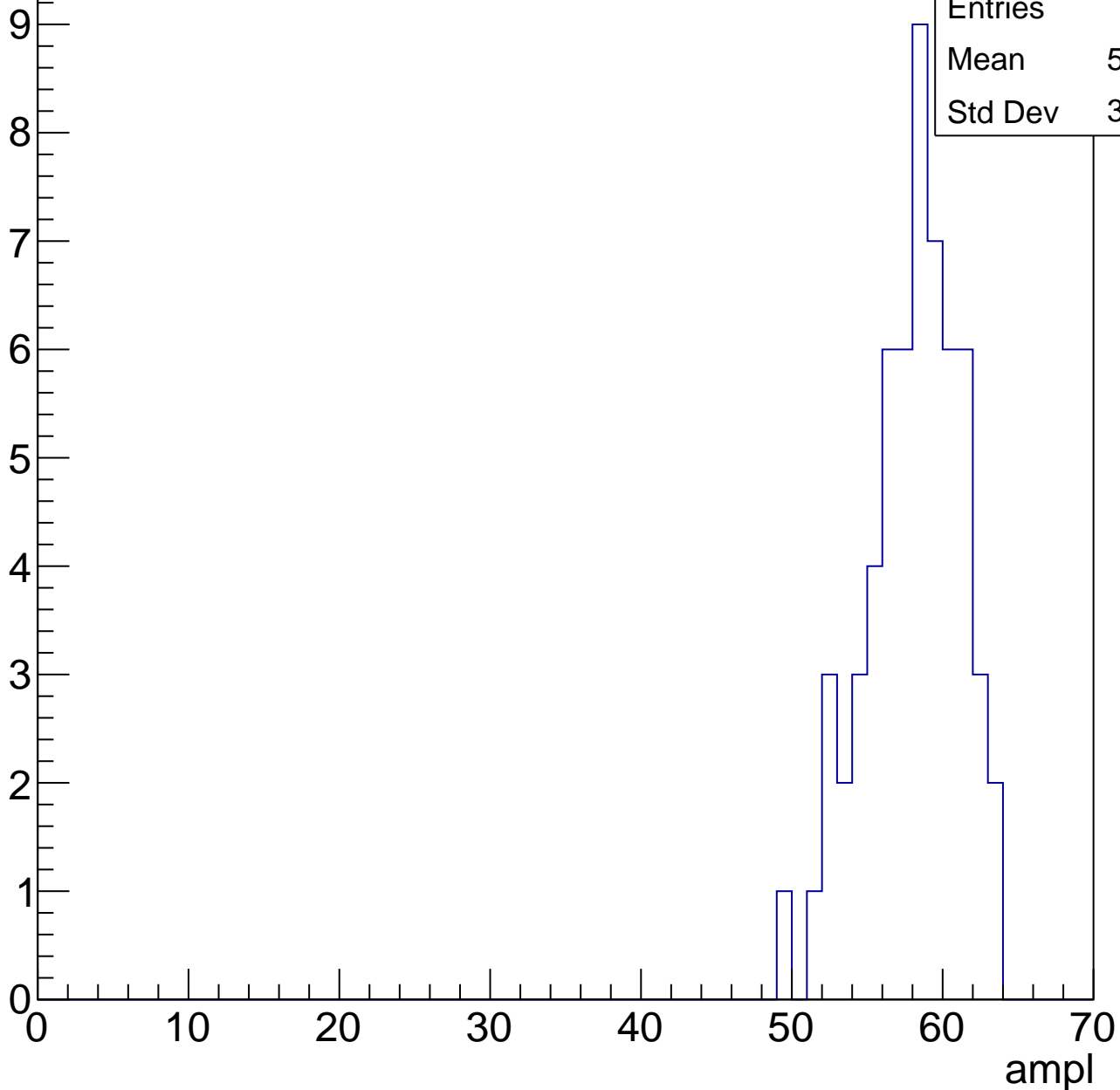


# B1L000S, U1-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.54
Std Dev	3.115

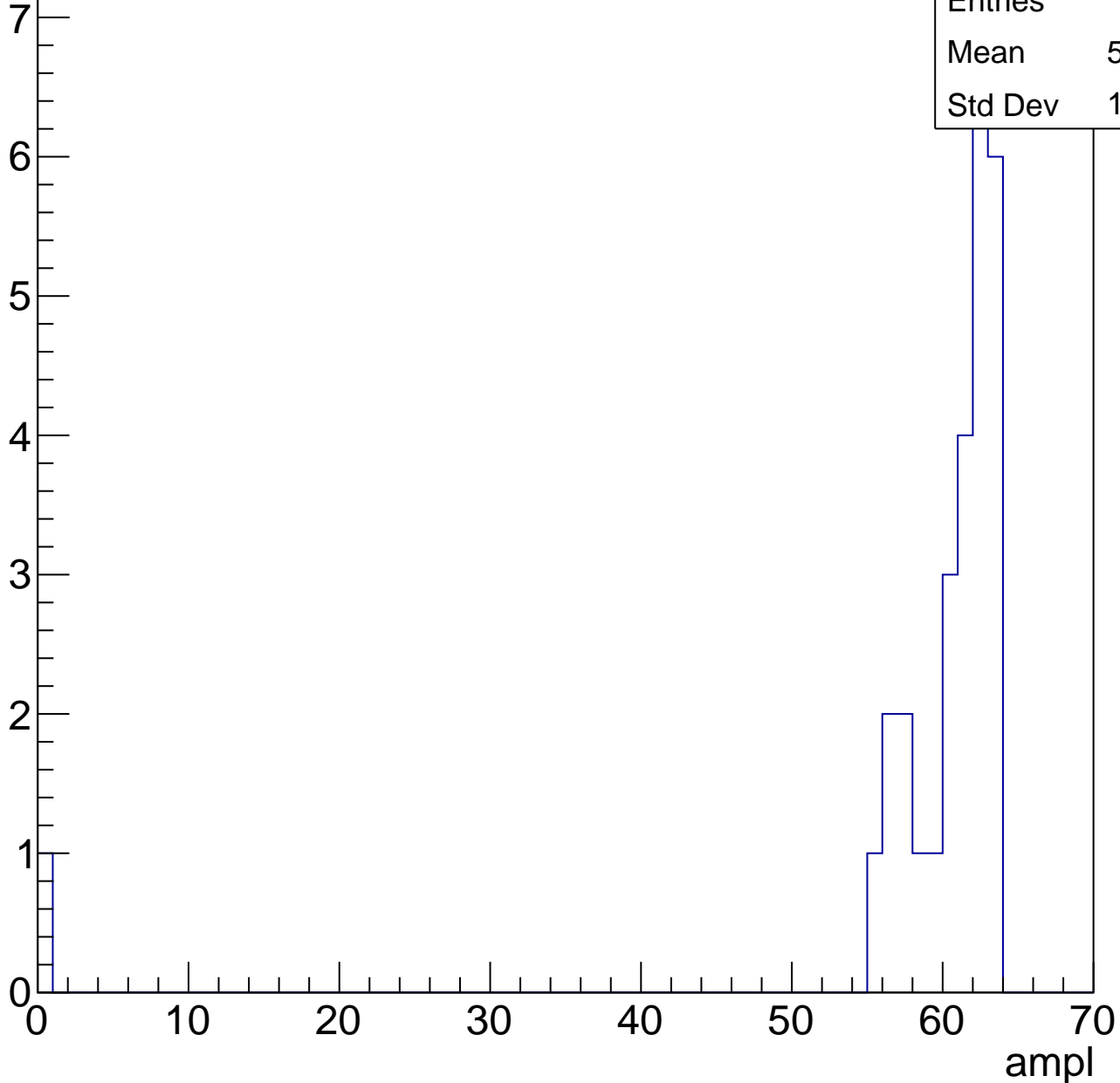


# B1L000S, U1-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.36
Std Dev	11.48

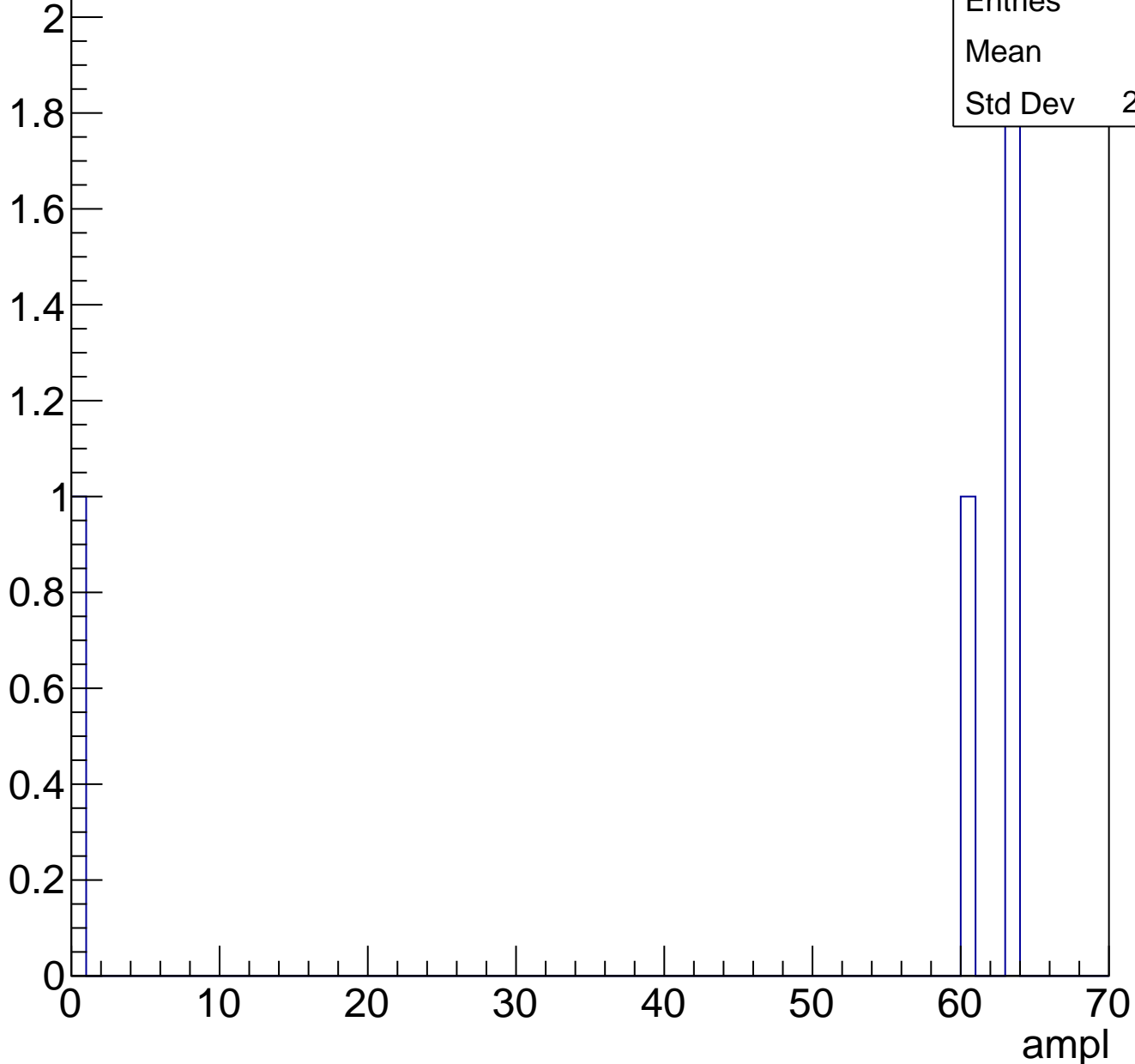




# B1L000S, U1-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch43, adc0

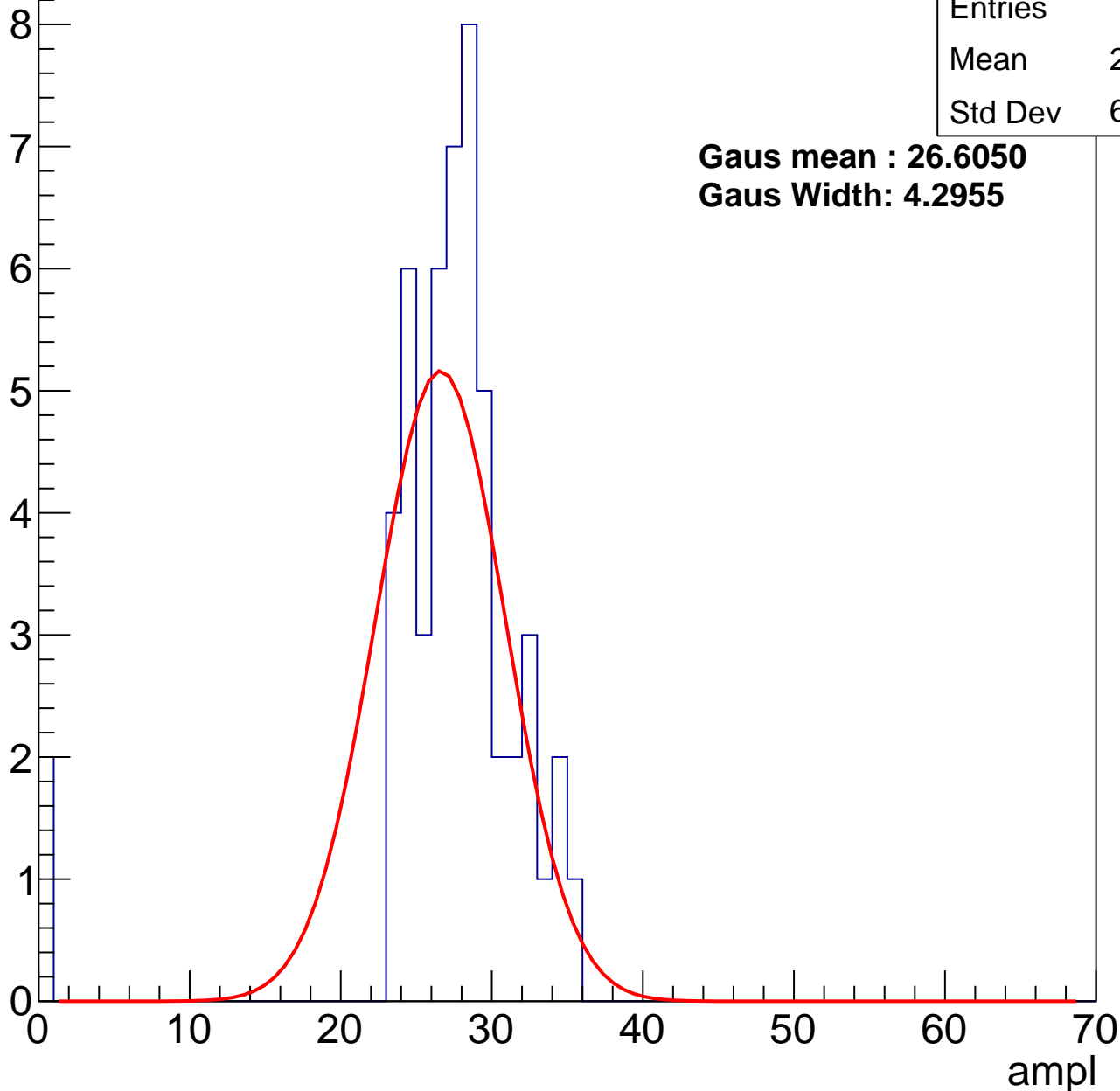
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	26.52
Std Dev	6.097

**Gaus mean : 26.6050**

**Gaus Width: 4.2955**



# B1L000S, U1-ch43, adc1

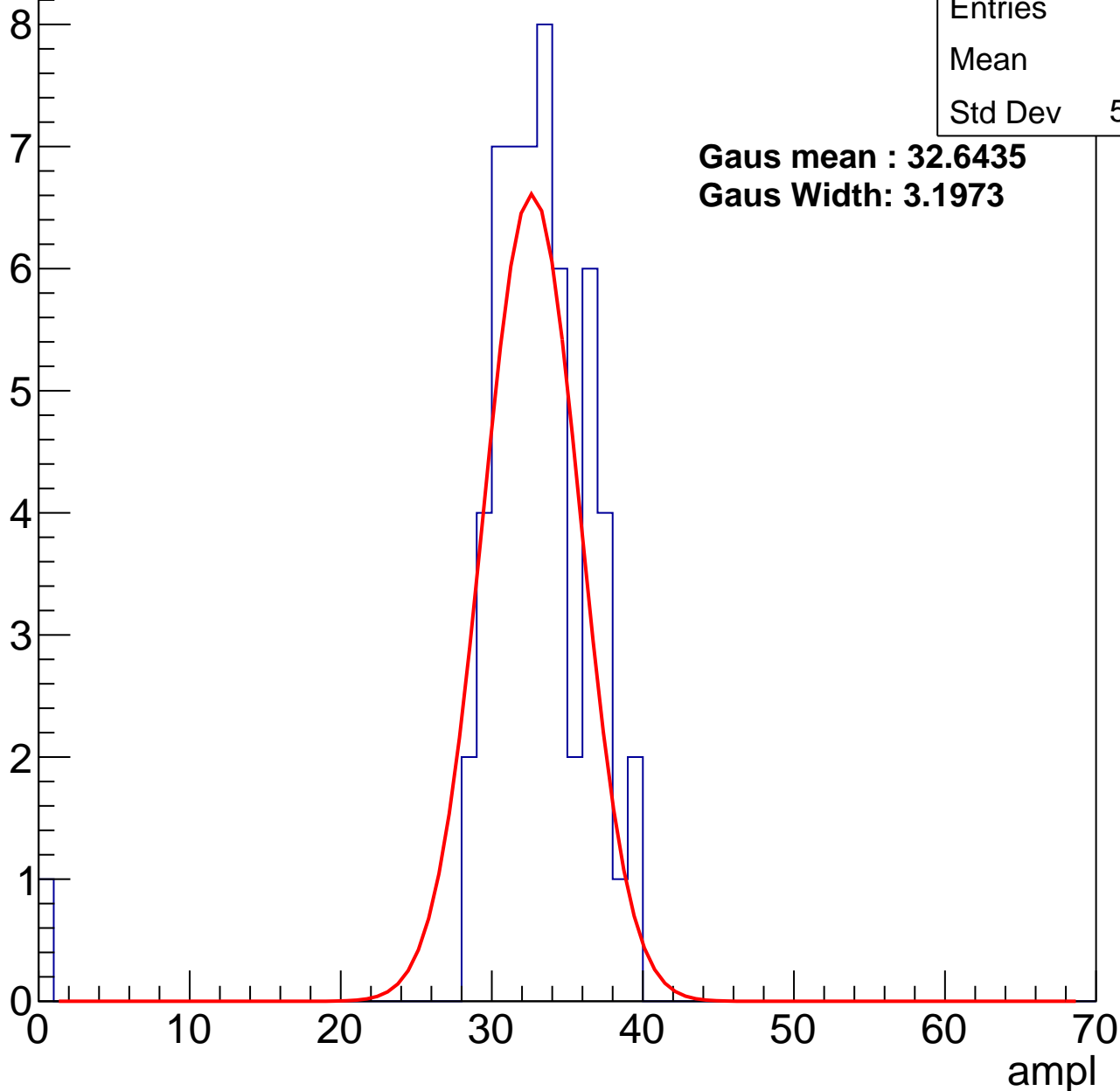
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	32.3
Std Dev	5.133

**Gaus mean : 32.6435**

**Gaus Width: 3.1973**



# B1L000S, U1-ch43, adc2

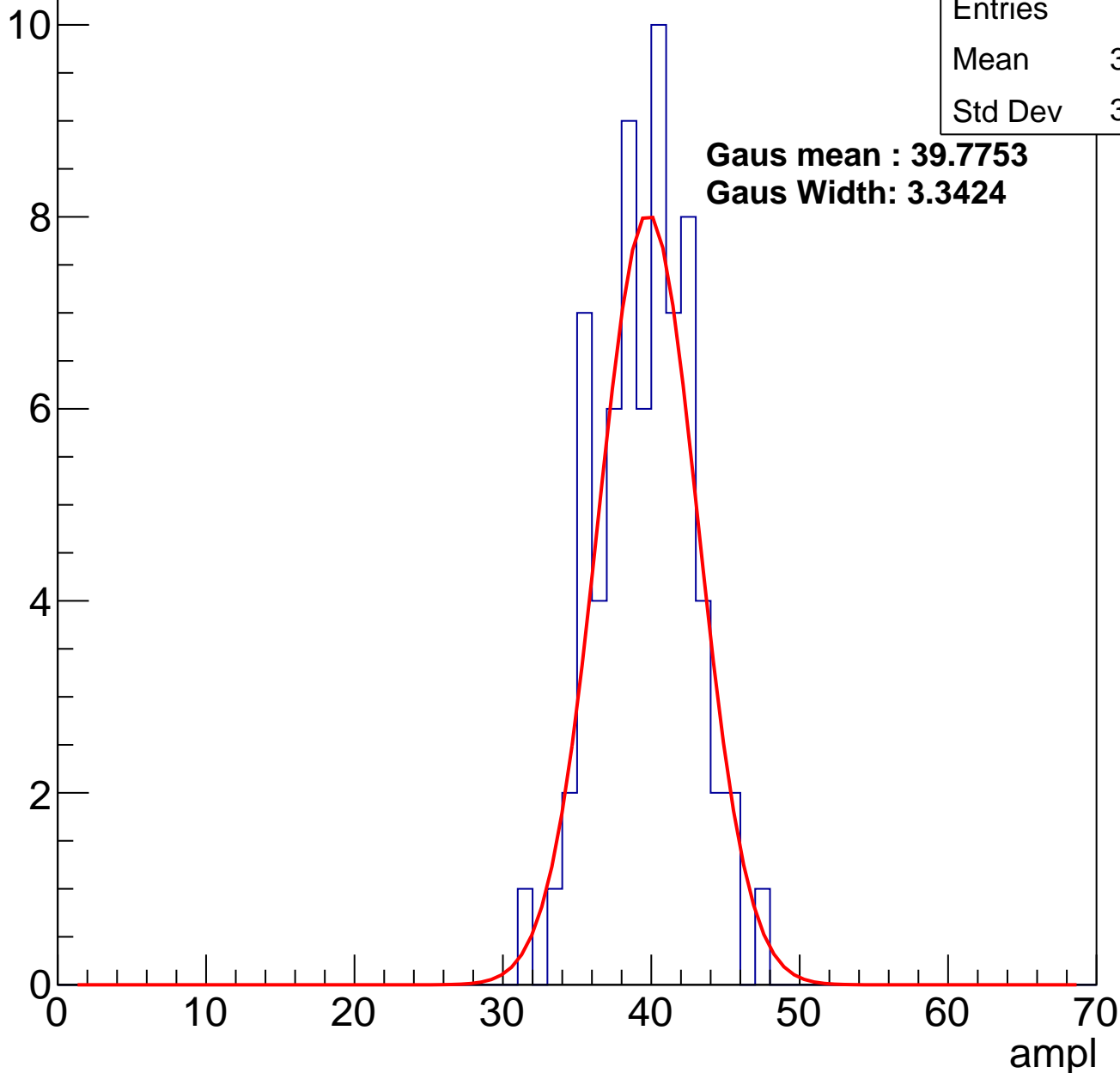
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	39.13
Std Dev	3.139

**Gaus mean : 39.7753**

**Gaus Width: 3.3424**

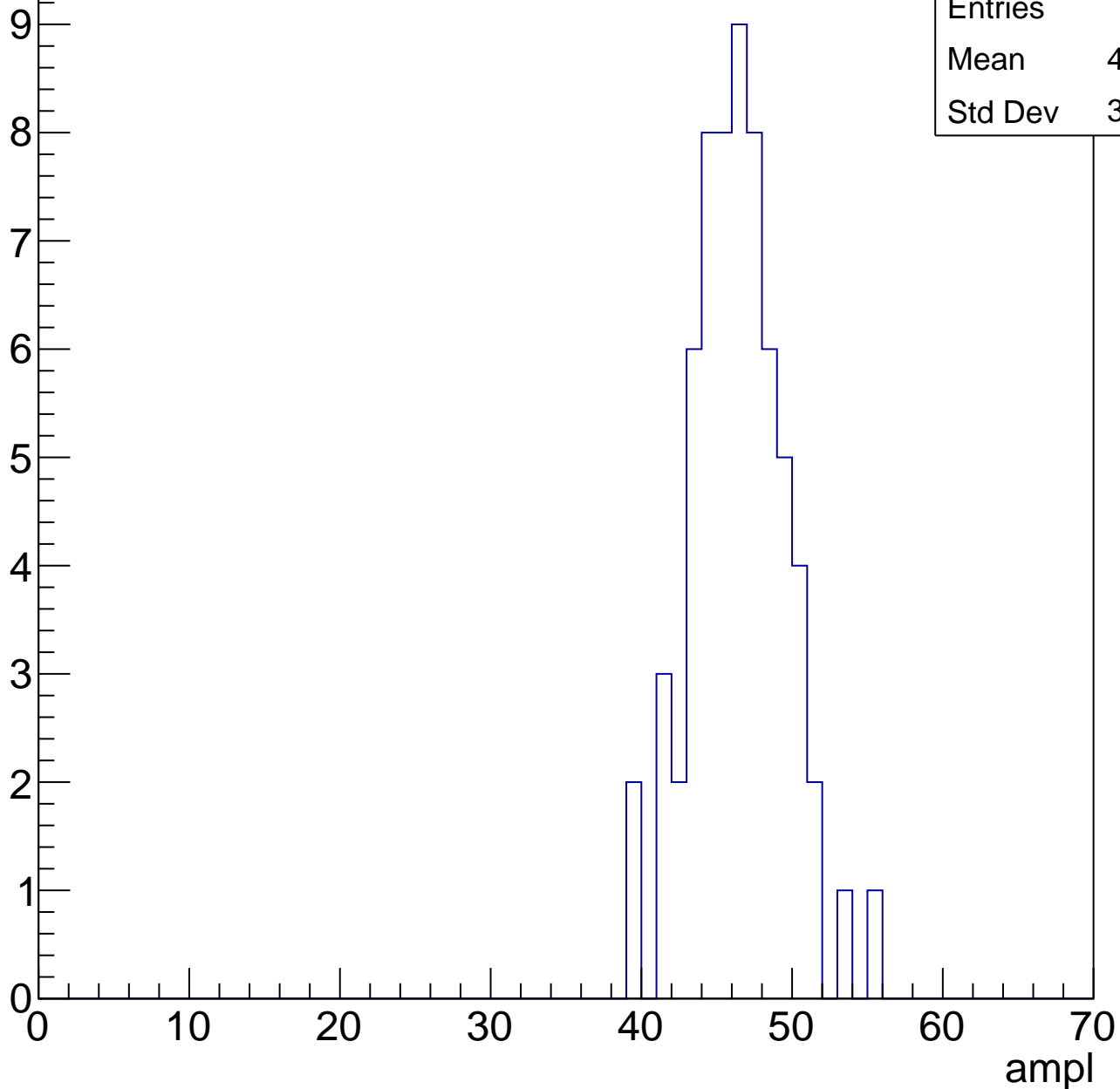
Entry



# B1L000S, U1-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



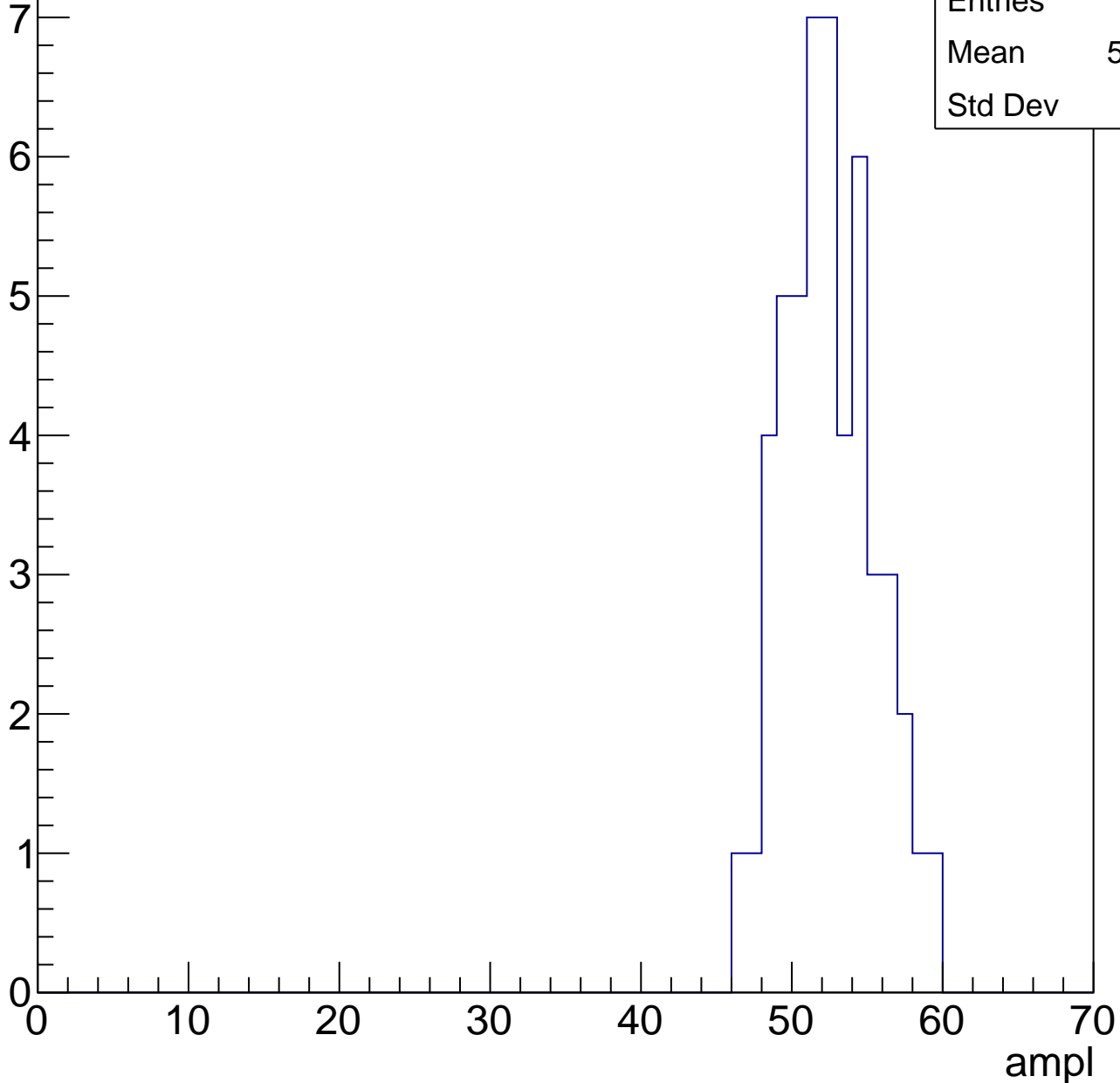
Entries	65
Mean	45.97
Std Dev	3.088

# B1L000S, U1-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	52.02
Std Dev	2.95



# B1L000S, U1-ch43, adc5

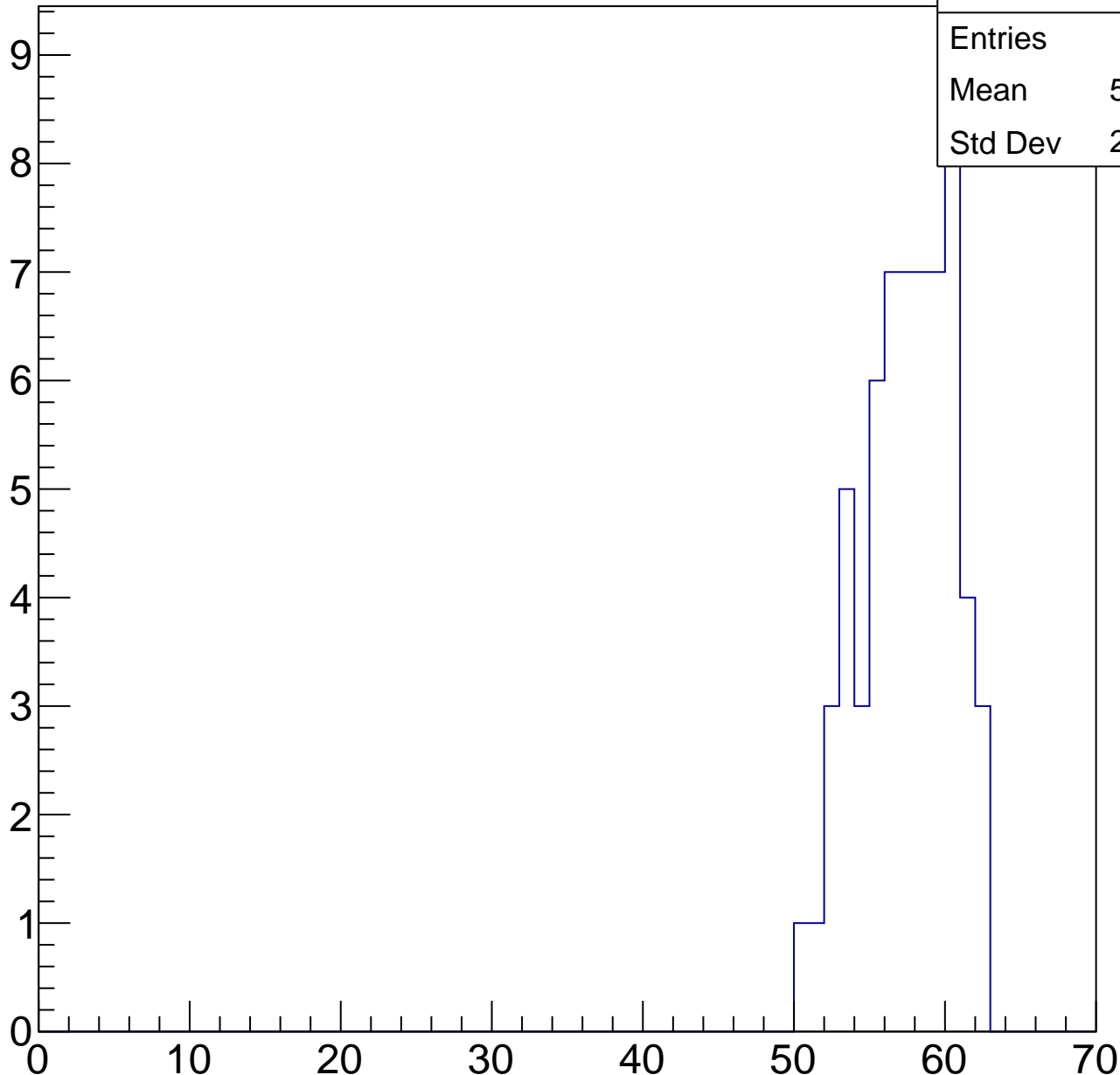
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	57.05
Std Dev	2.962

ampl

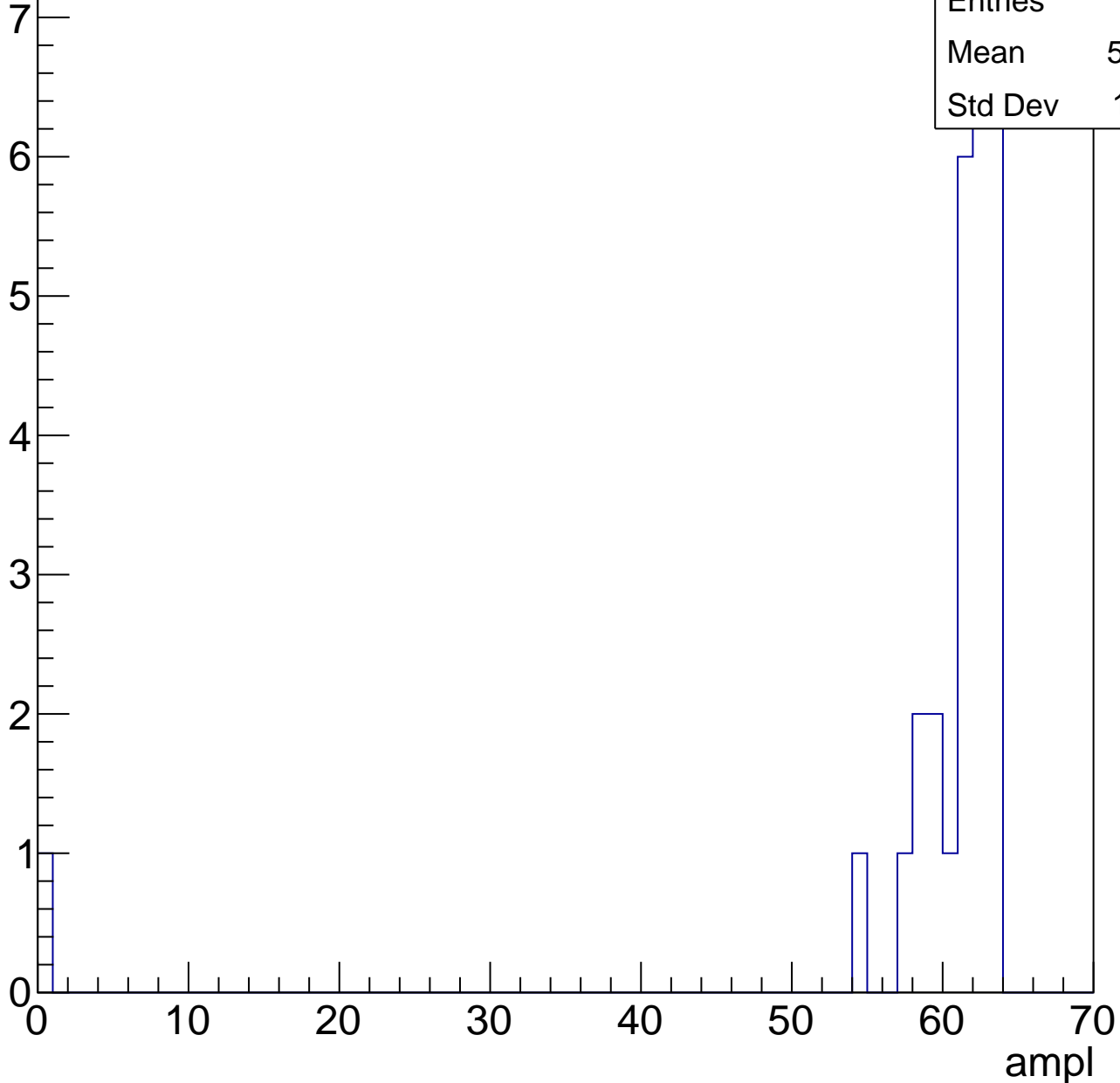


# B1L000S, U1-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.79
Std Dev	11.51

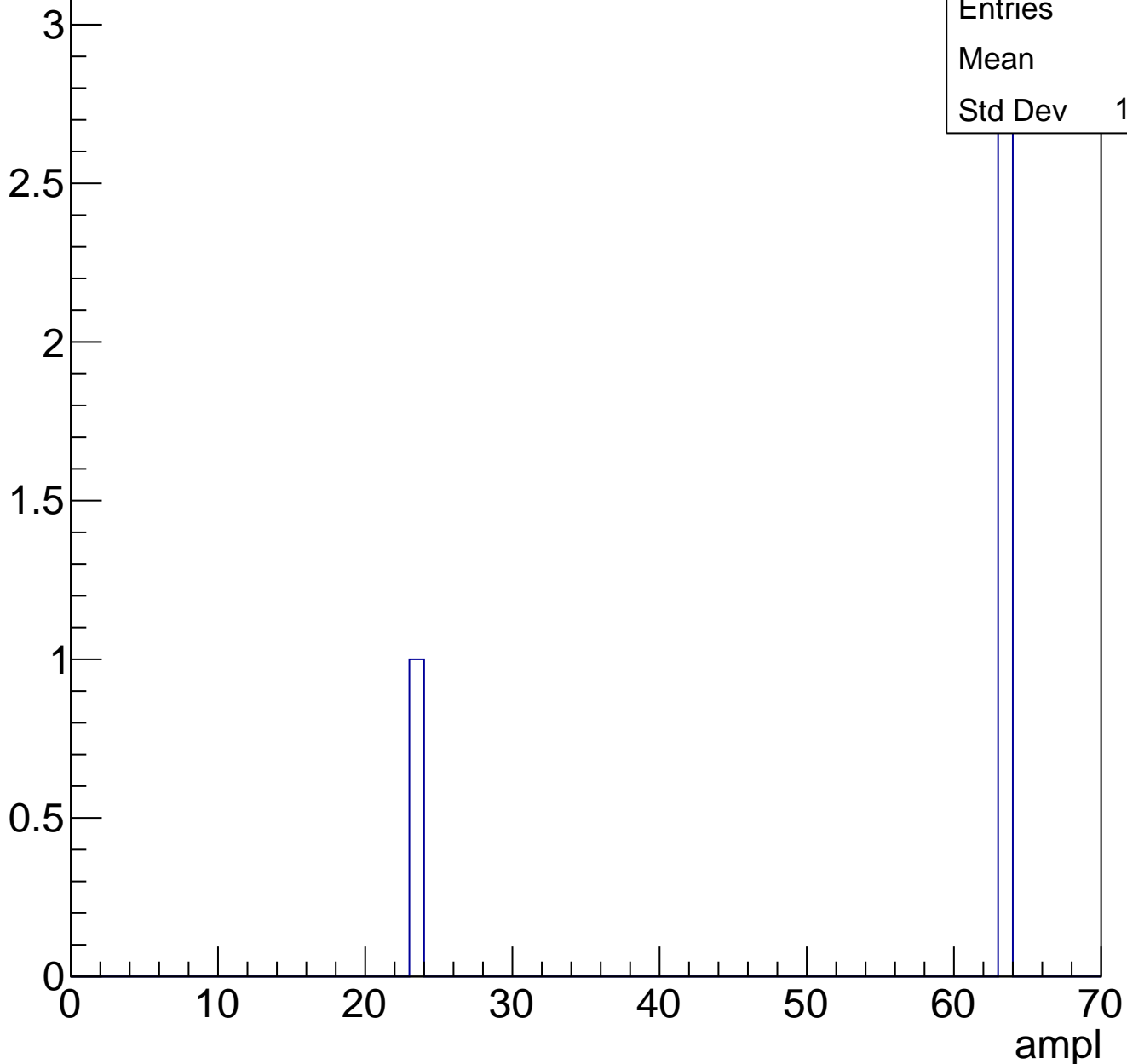




# B1L000S, U1-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch44, adc0

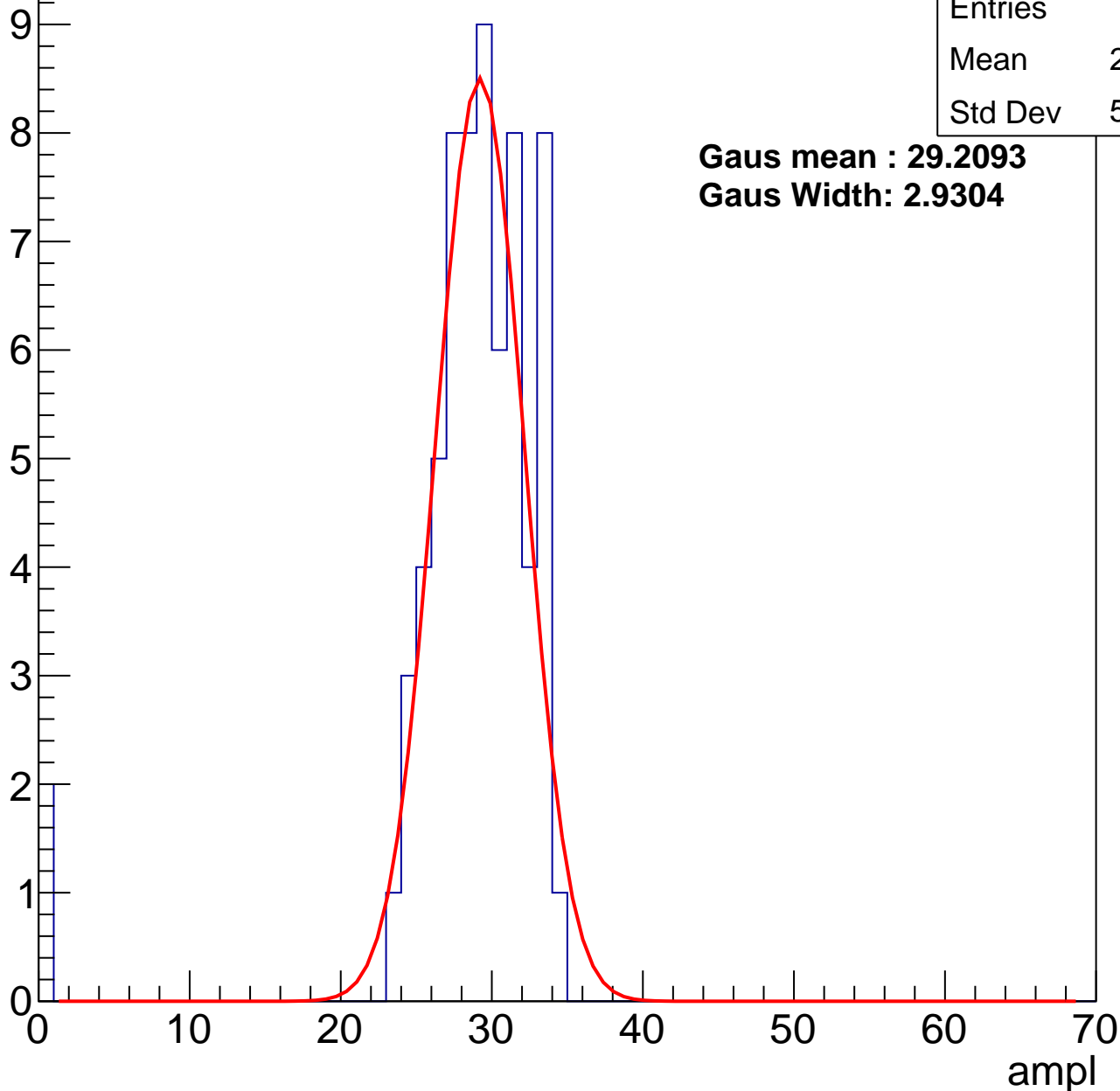
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.06
Std Dev	5.612

**Gaus mean : 29.2093**

**Gaus Width: 2.9304**



# B1L000S, U1-ch44, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	35.98
Std Dev	3.264

**Gaus mean : 36.7540**

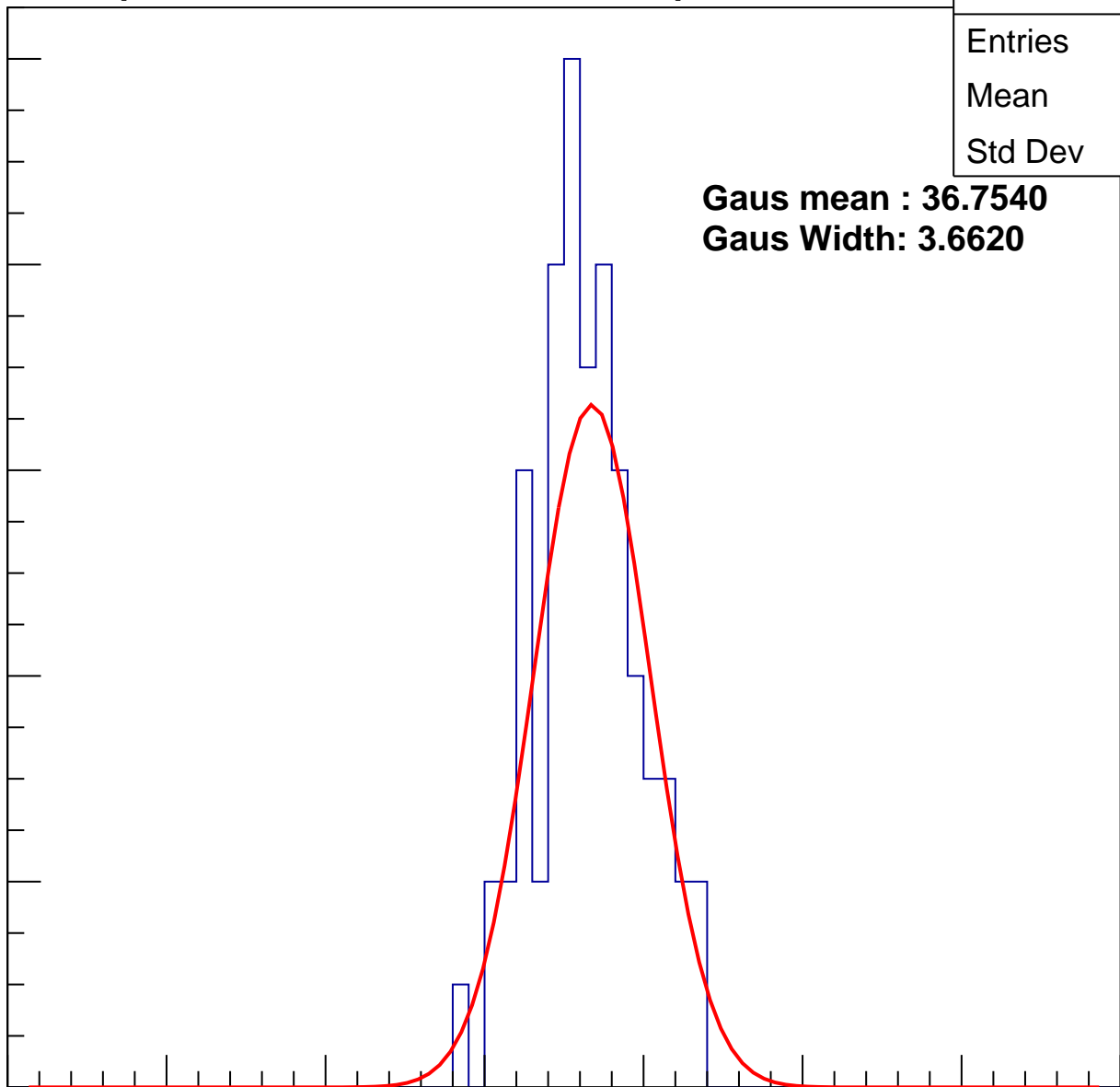
**Gaus Width: 3.6620**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch44, adc2

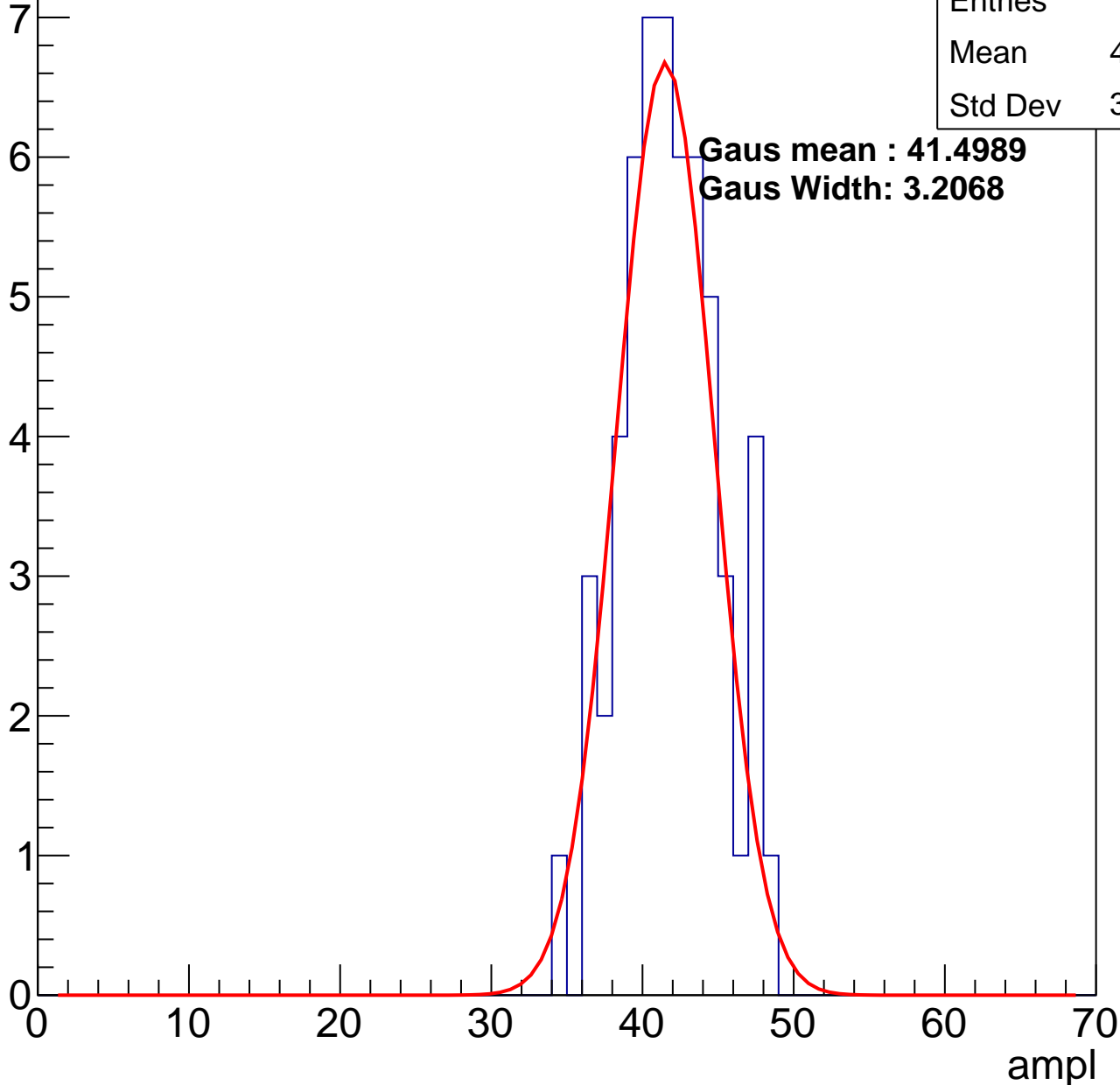
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.36
Std Dev	3.153

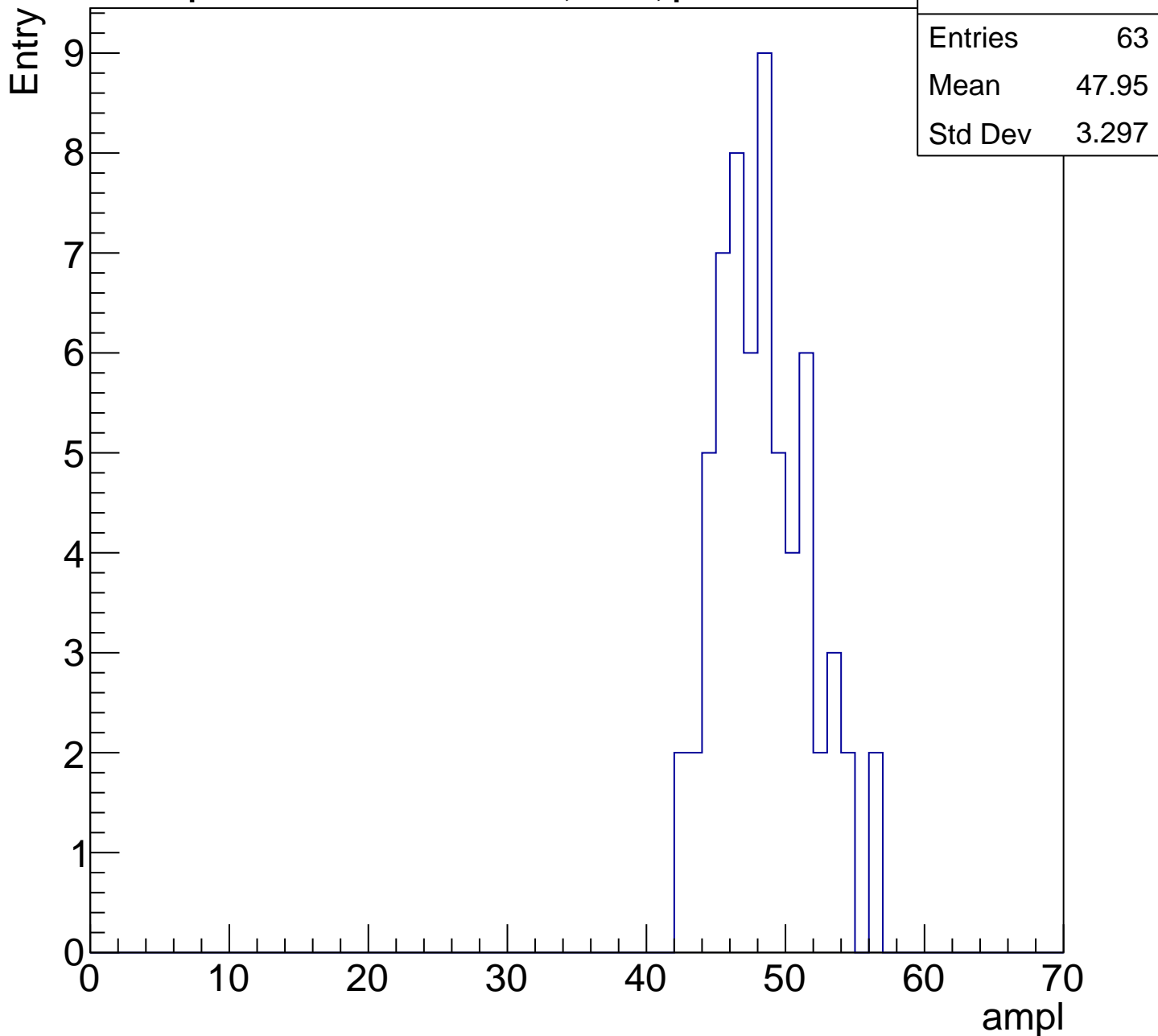
**Gaus mean : 41.4989**

**Gaus Width: 3.2068**



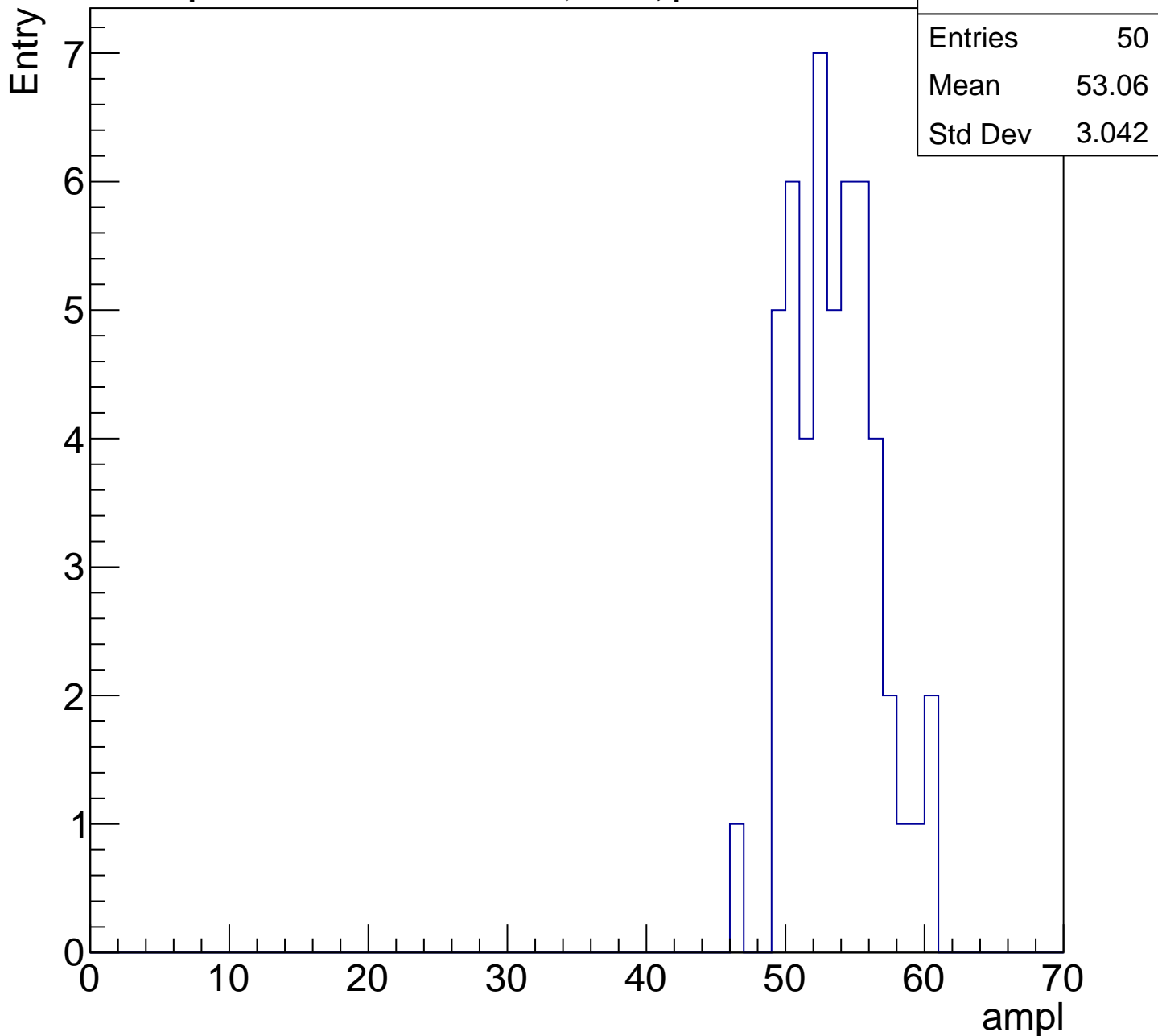
# B1L000S, U1-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3



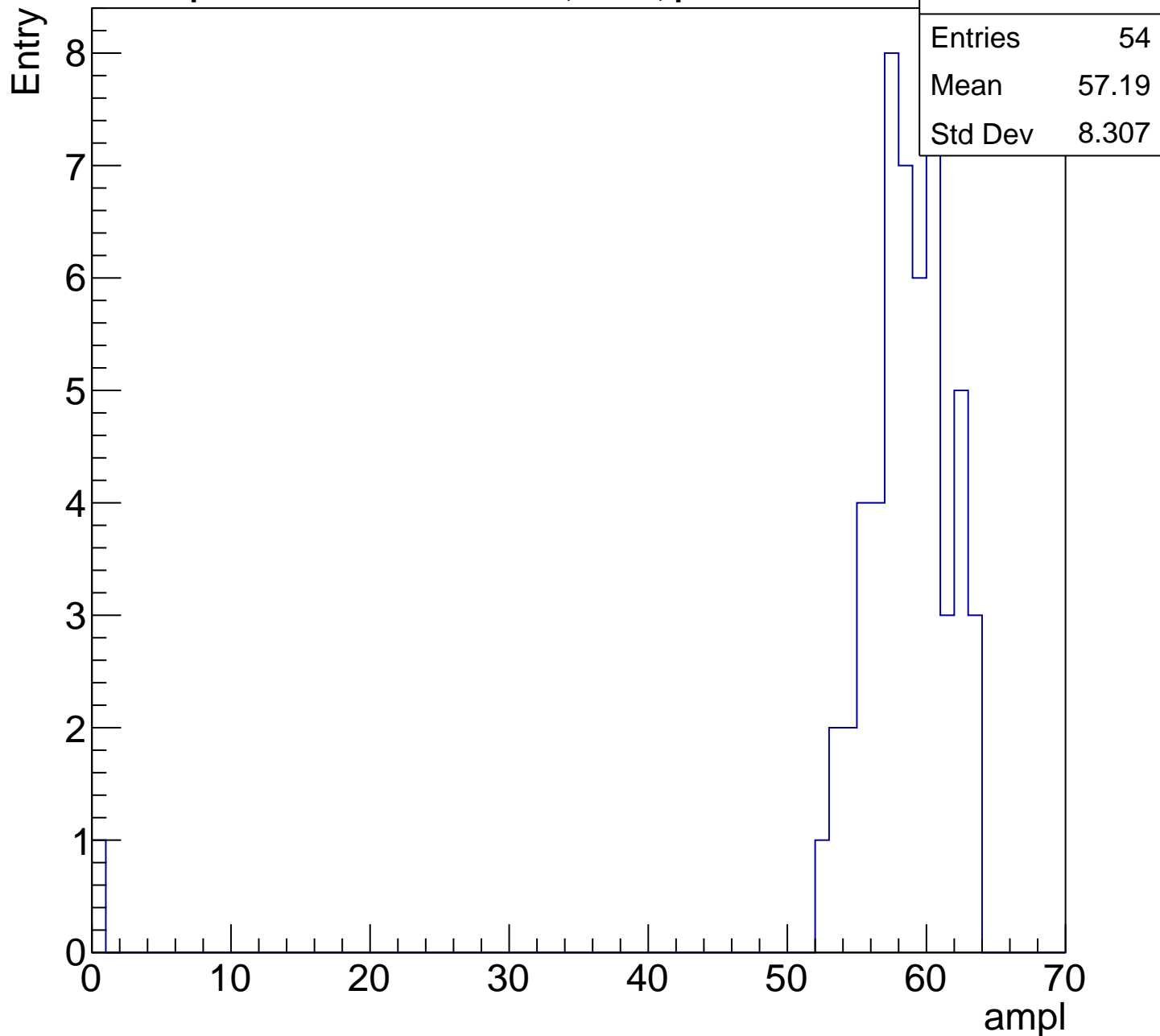
# B1L000S, U1-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch44, adc5

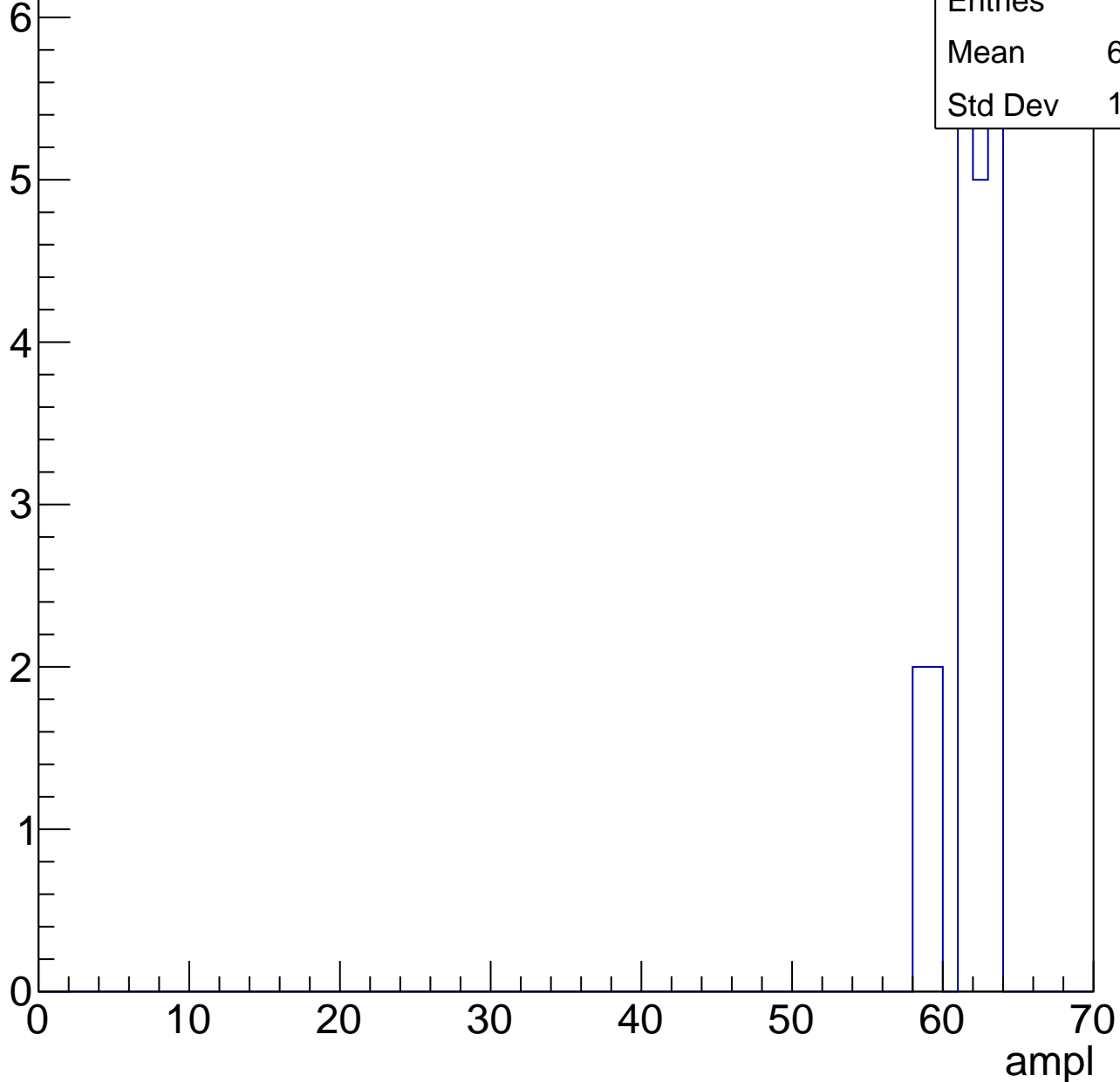
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

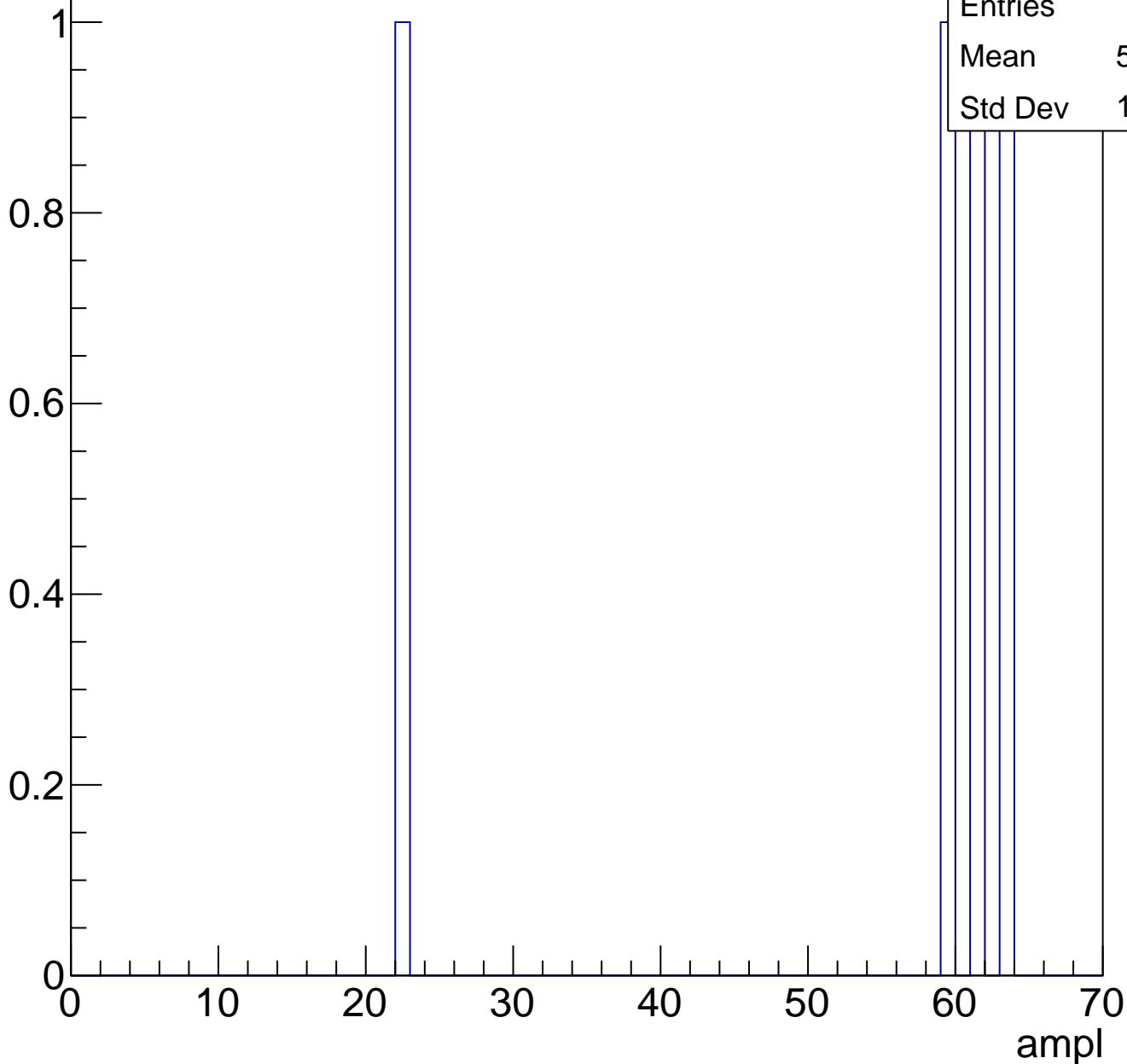




# B1L000S, U1-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch45, adc0

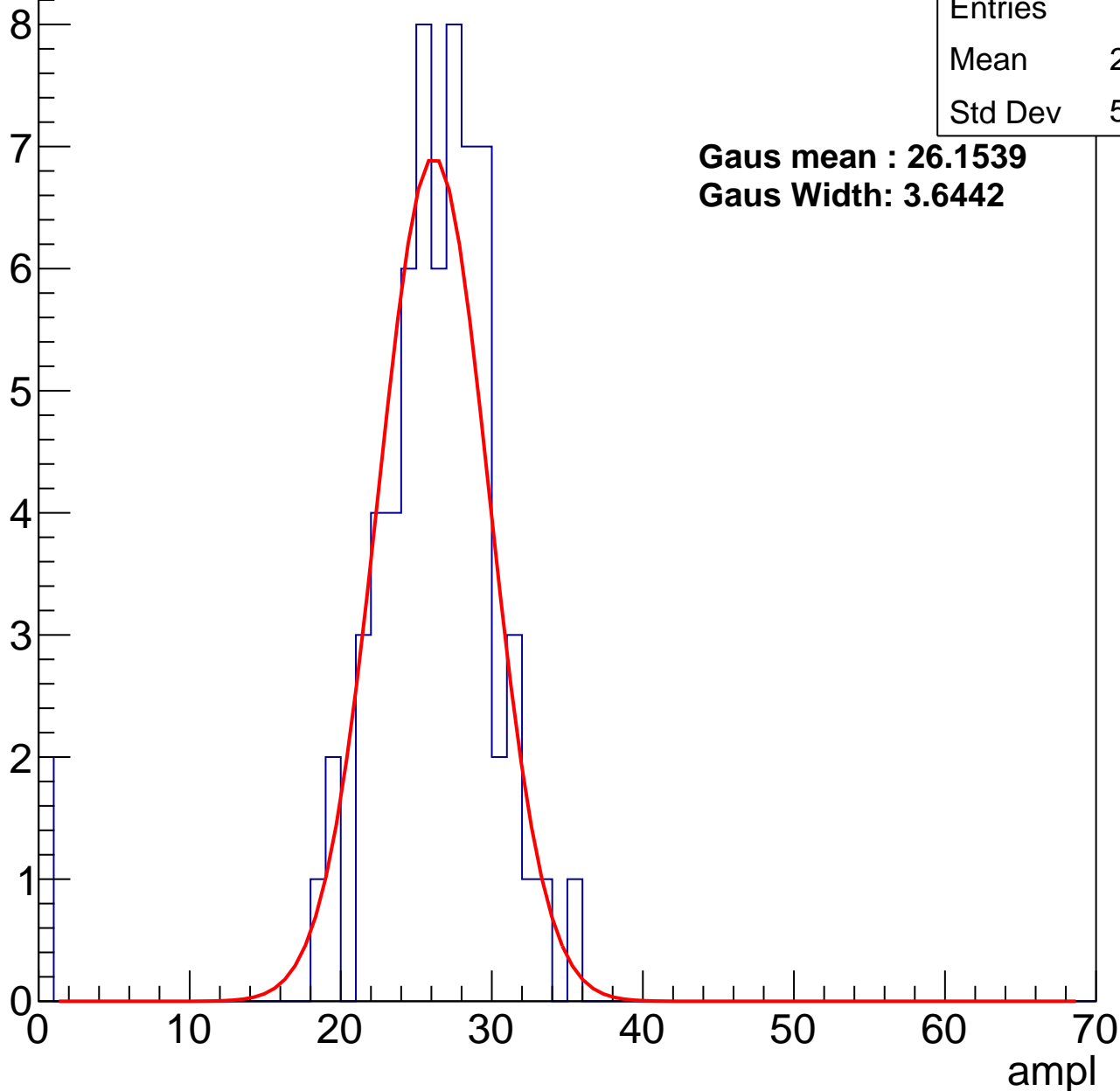
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	25.26
Std Dev	5.582

**Gaus mean : 26.1539**

**Gaus Width: 3.6442**



# B1L000S, U1-ch45, adc1

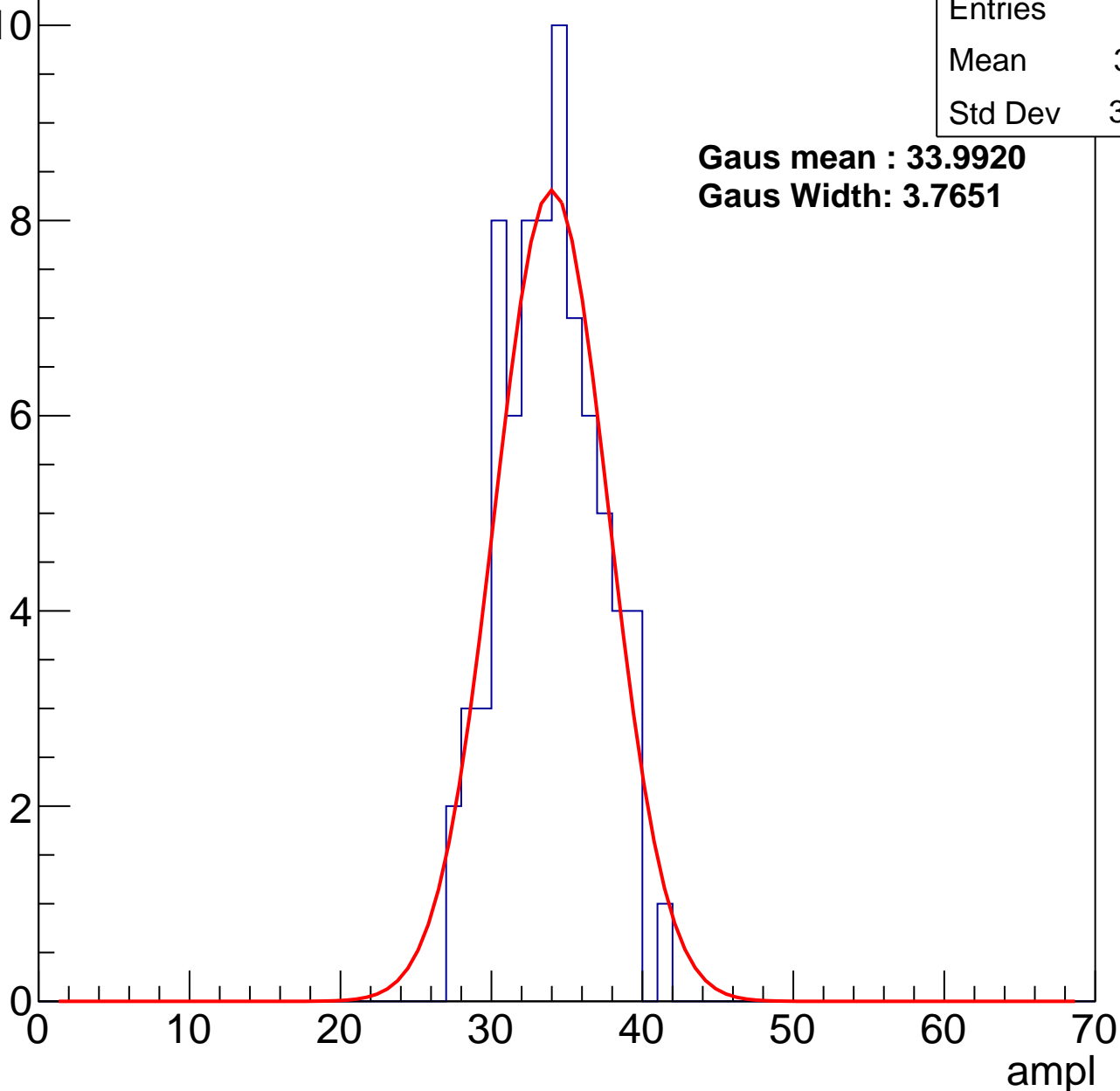
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	33.41
Std Dev	3.196

**Gaus mean : 33.9920**

**Gaus Width: 3.7651**



# B1L000S, U1-ch45, adc2

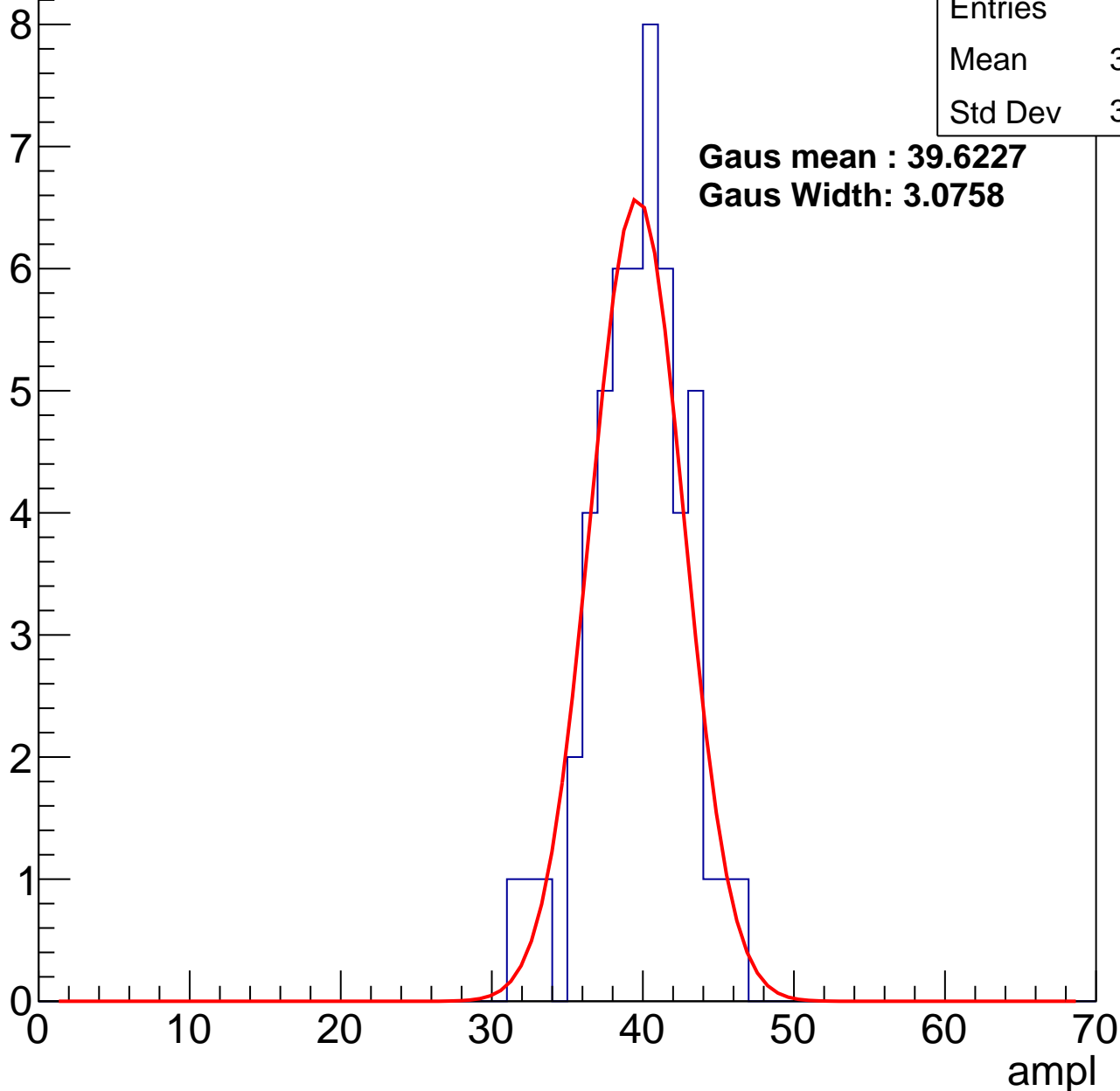
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	39.25
Std Dev	3.094

**Gaus mean : 39.6227**

**Gaus Width: 3.0758**

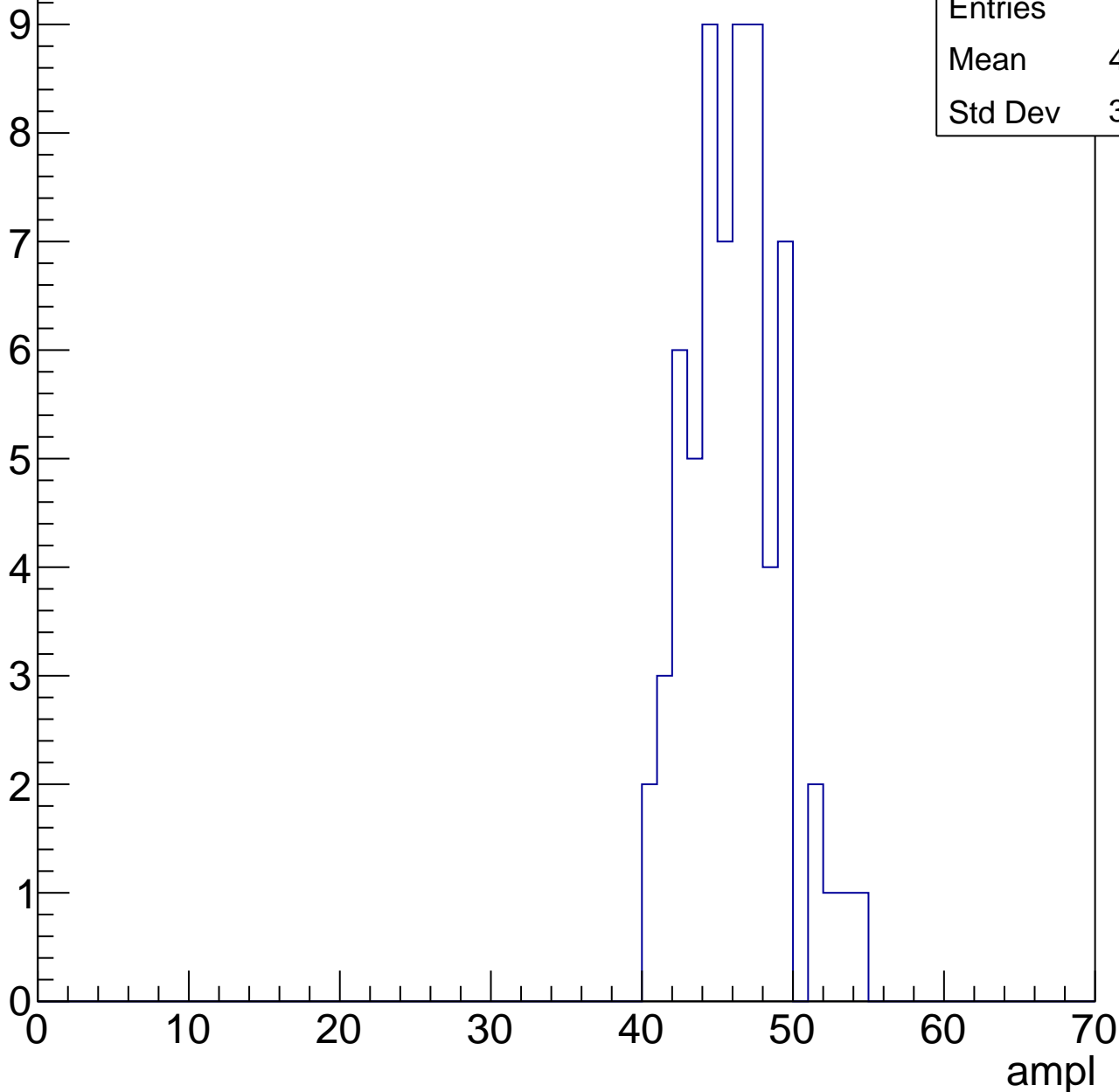


# B1L000S, U1-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	45.67
Std Dev	3.042

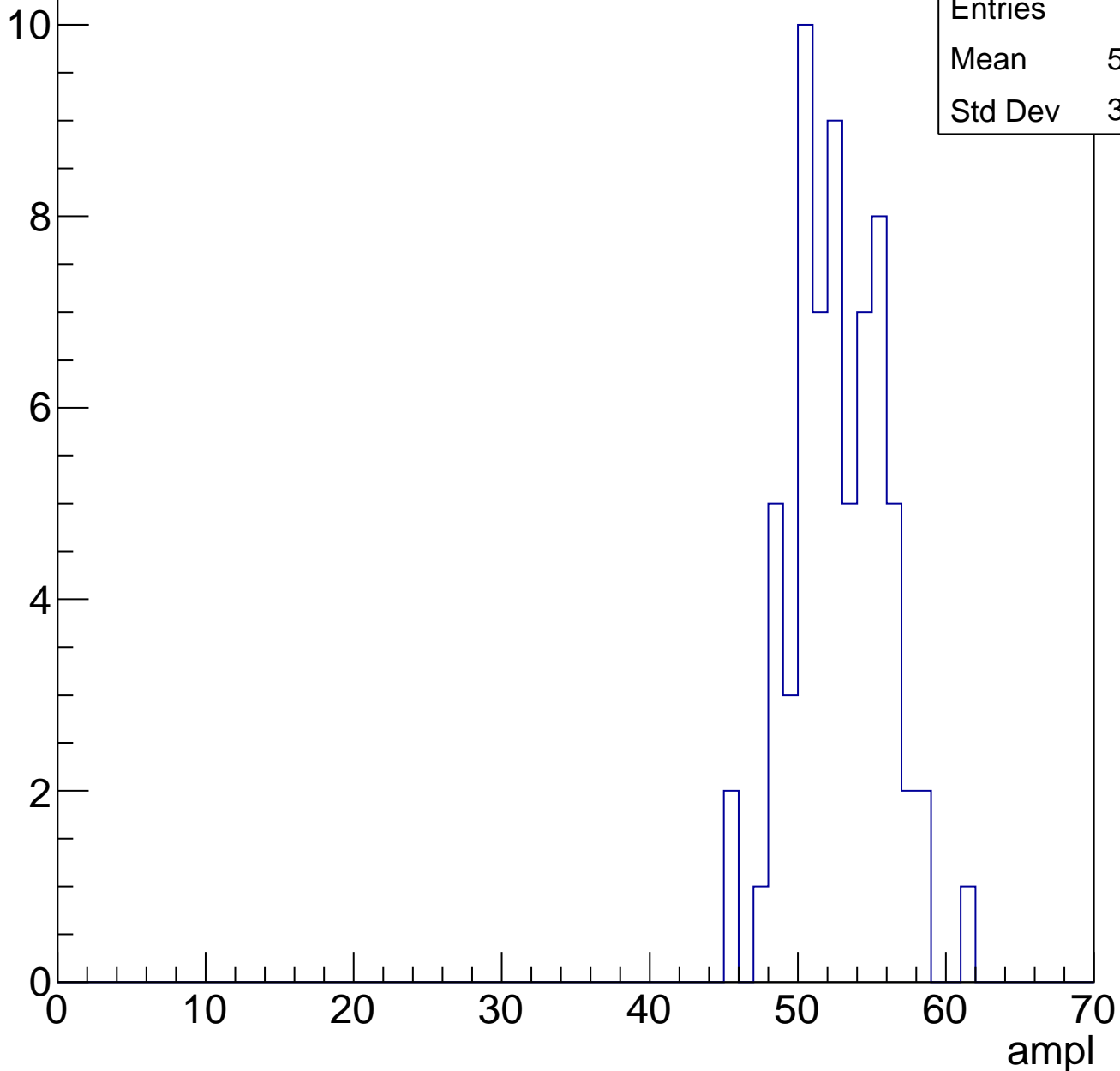


# B1L000S, U1-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

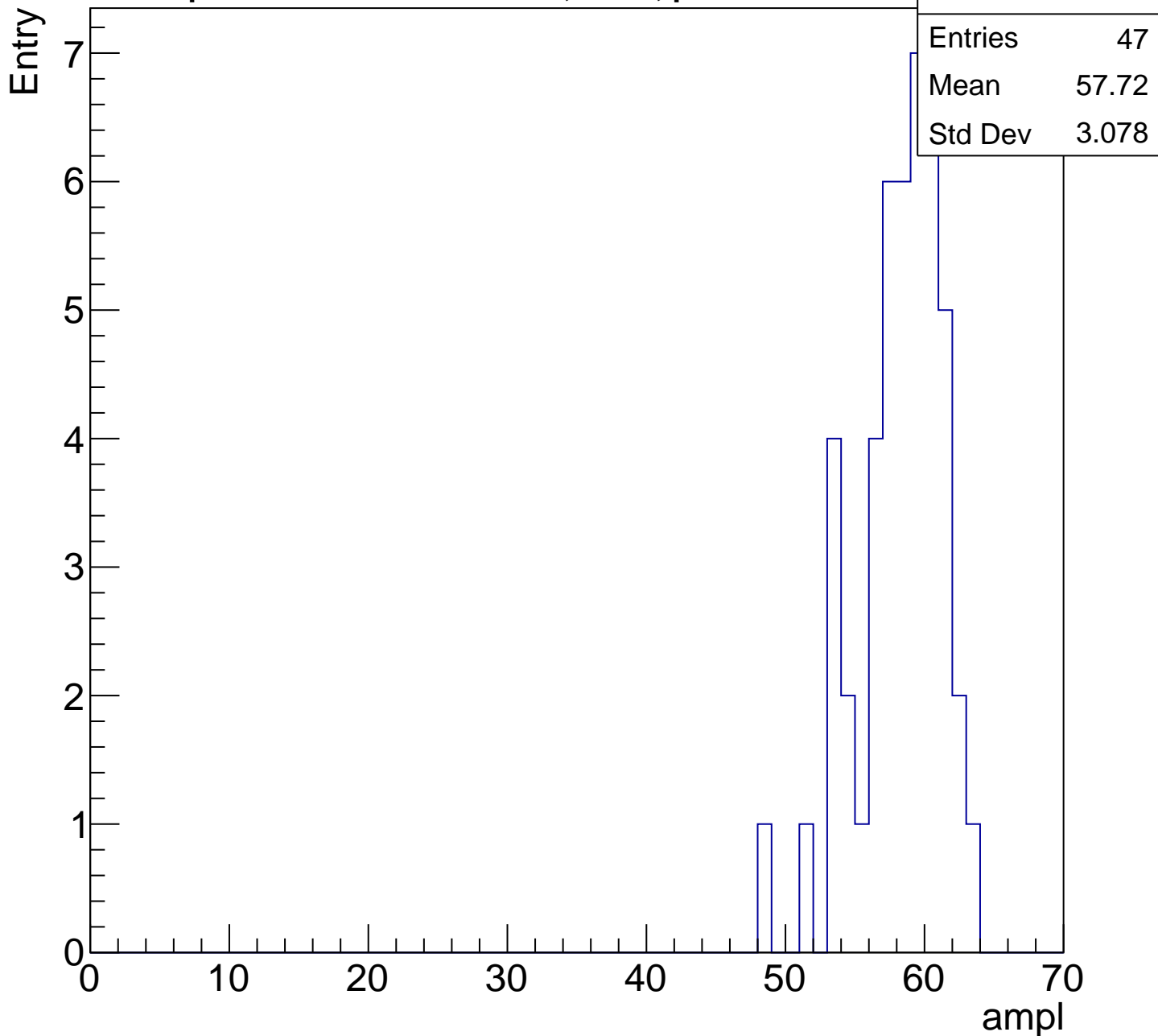
Entries	67
Mean	52.28
Std Dev	3.138

Entry



# B1L000S, U1-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

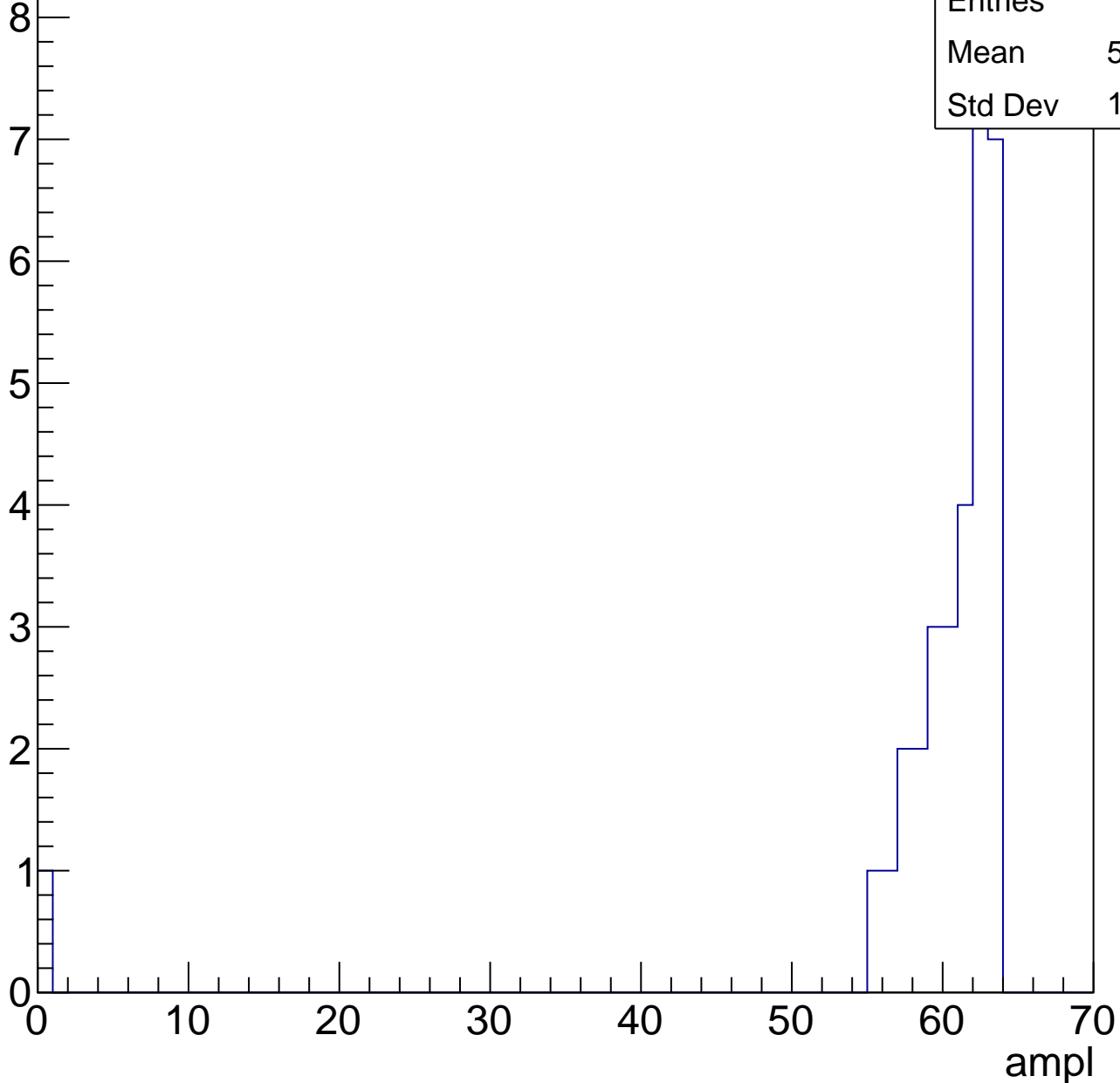


# B1L000S, U1-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	58.72
Std Dev	10.78





# B1L000S, U1-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch46, adc0

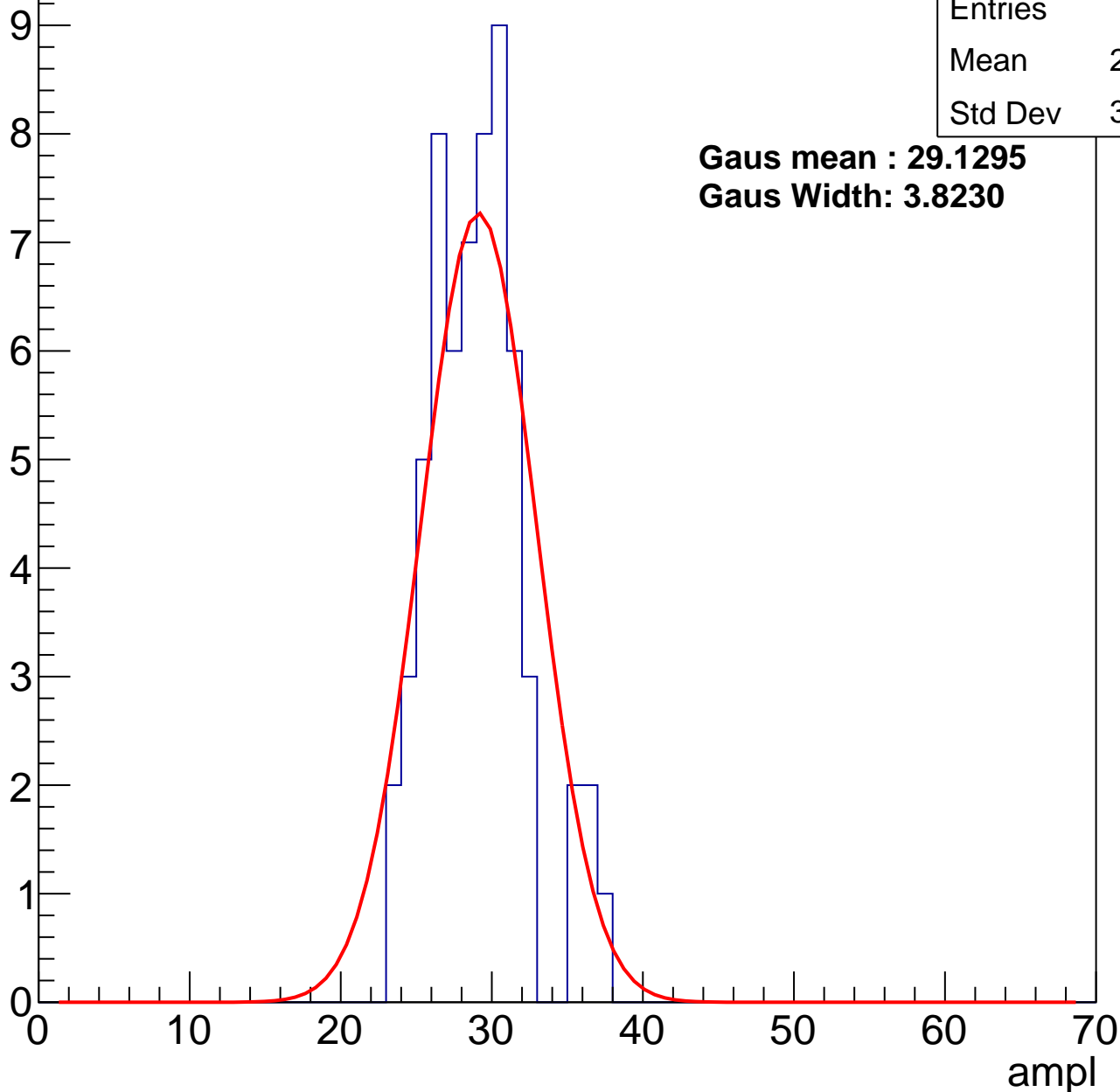
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.58
Std Dev	3.145

**Gaus mean : 29.1295**

**Gaus Width: 3.8230**



# B1L000S, U1-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	34.35
Std Dev	2.762

**Gaus mean : 34.9815**

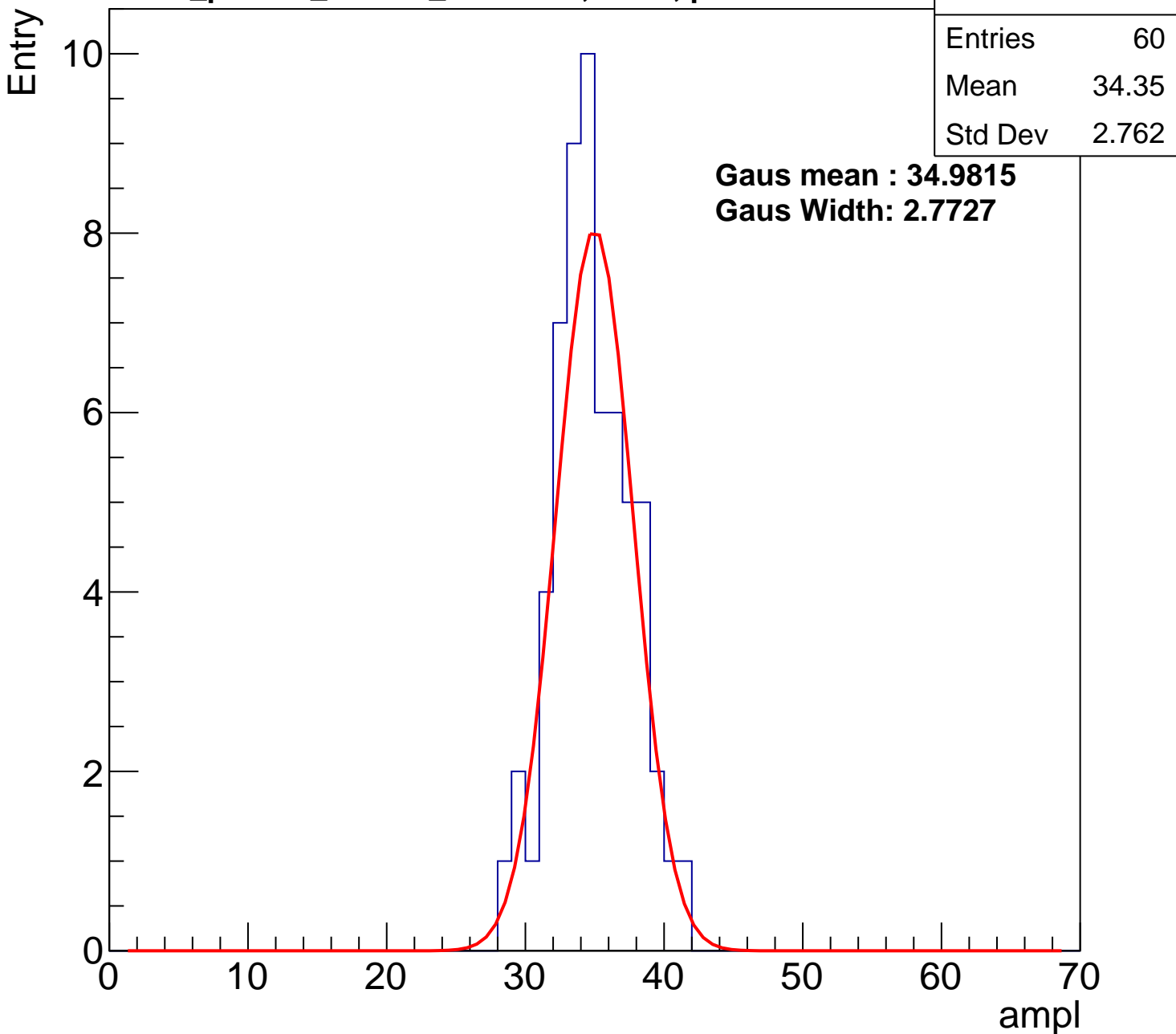
**Gaus Width: 2.7727**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch46, adc2

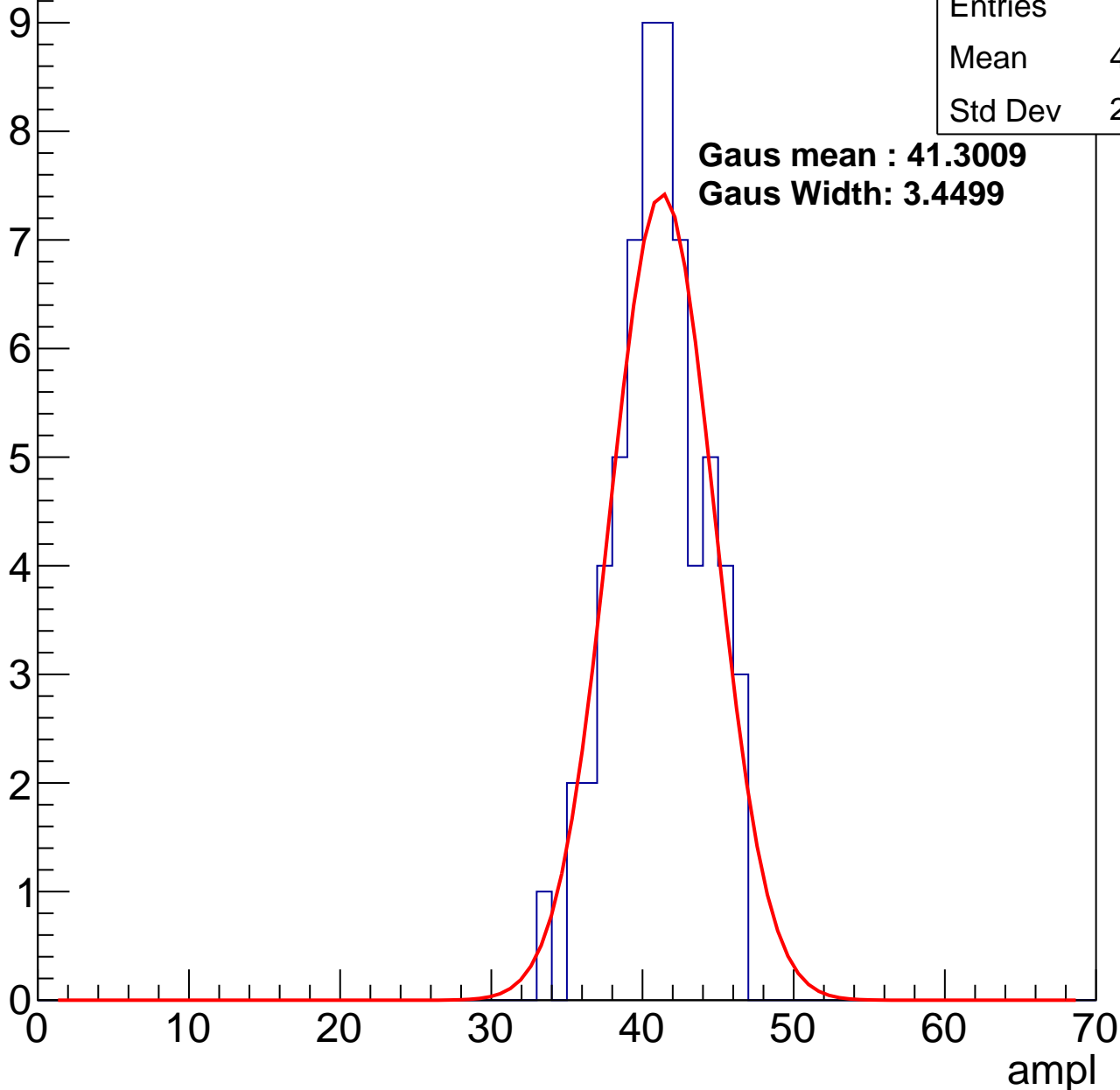
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.63
Std Dev	2.925

**Gaus mean : 41.3009**

**Gaus Width: 3.4499**

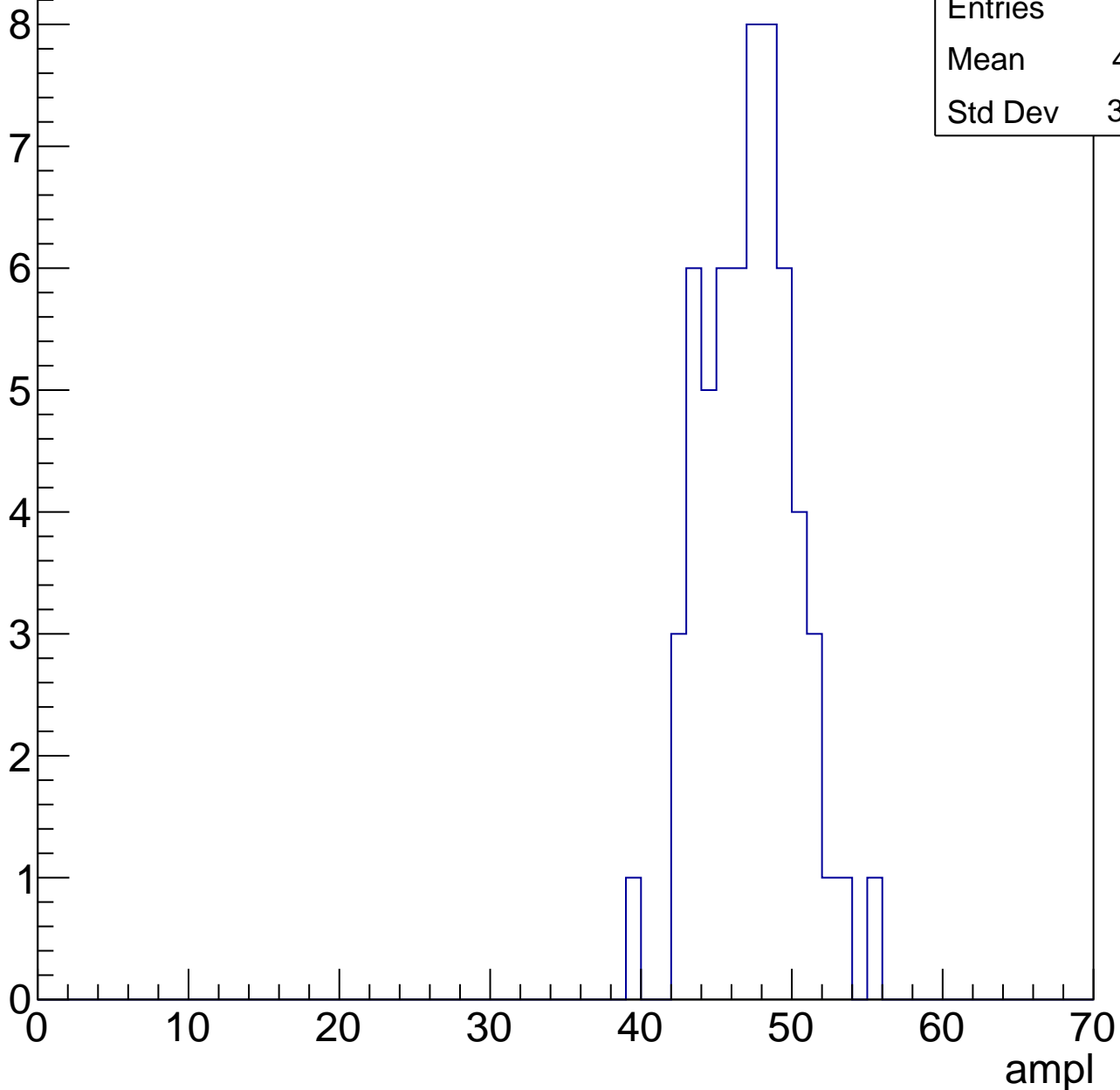


# B1L000S, U1-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	46.71
Std Dev	3.042

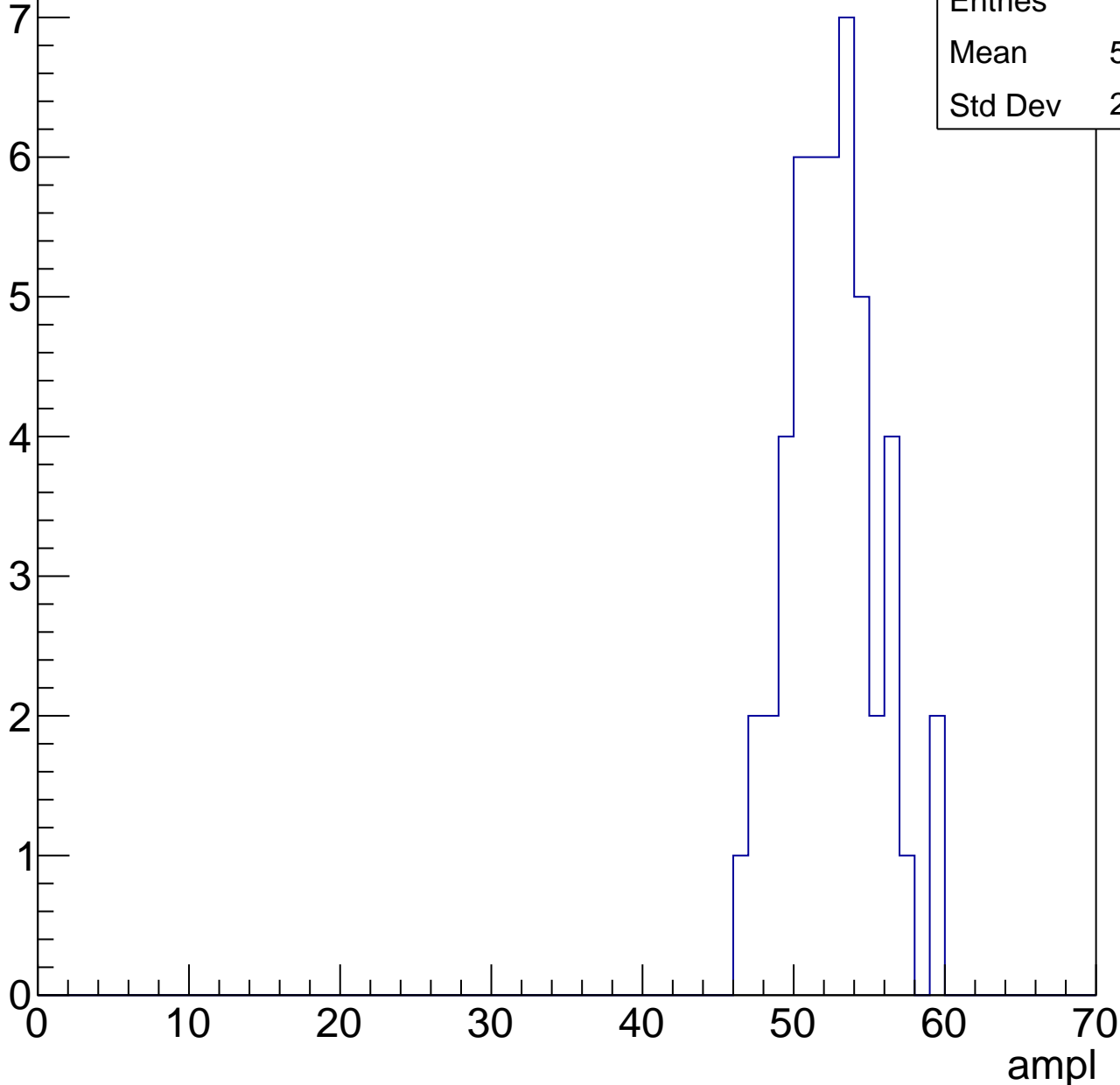


# B1L000S, U1-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	52.08
Std Dev	2.943

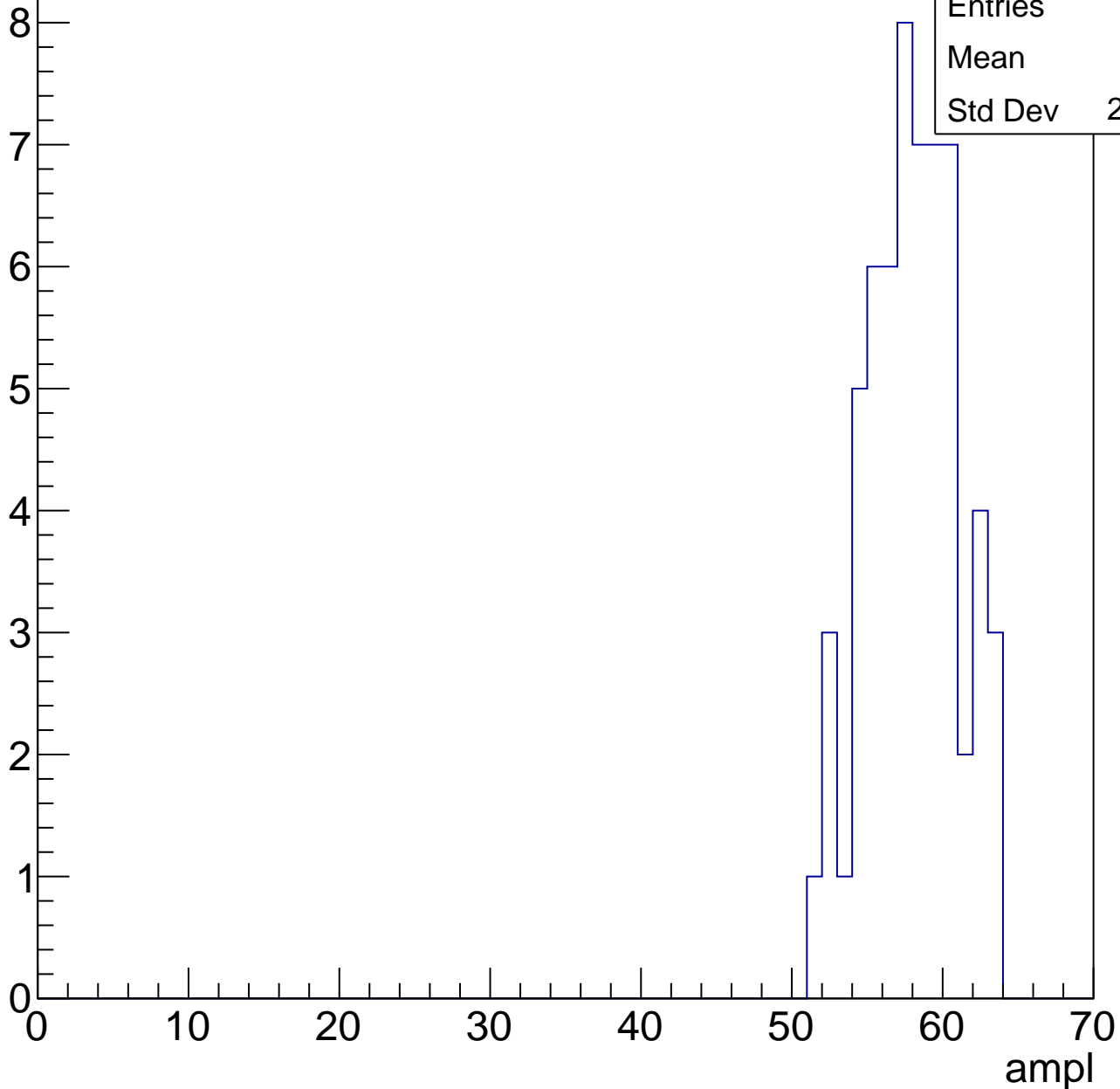


# B1L000S, U1-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	57.5
Std Dev	2.958

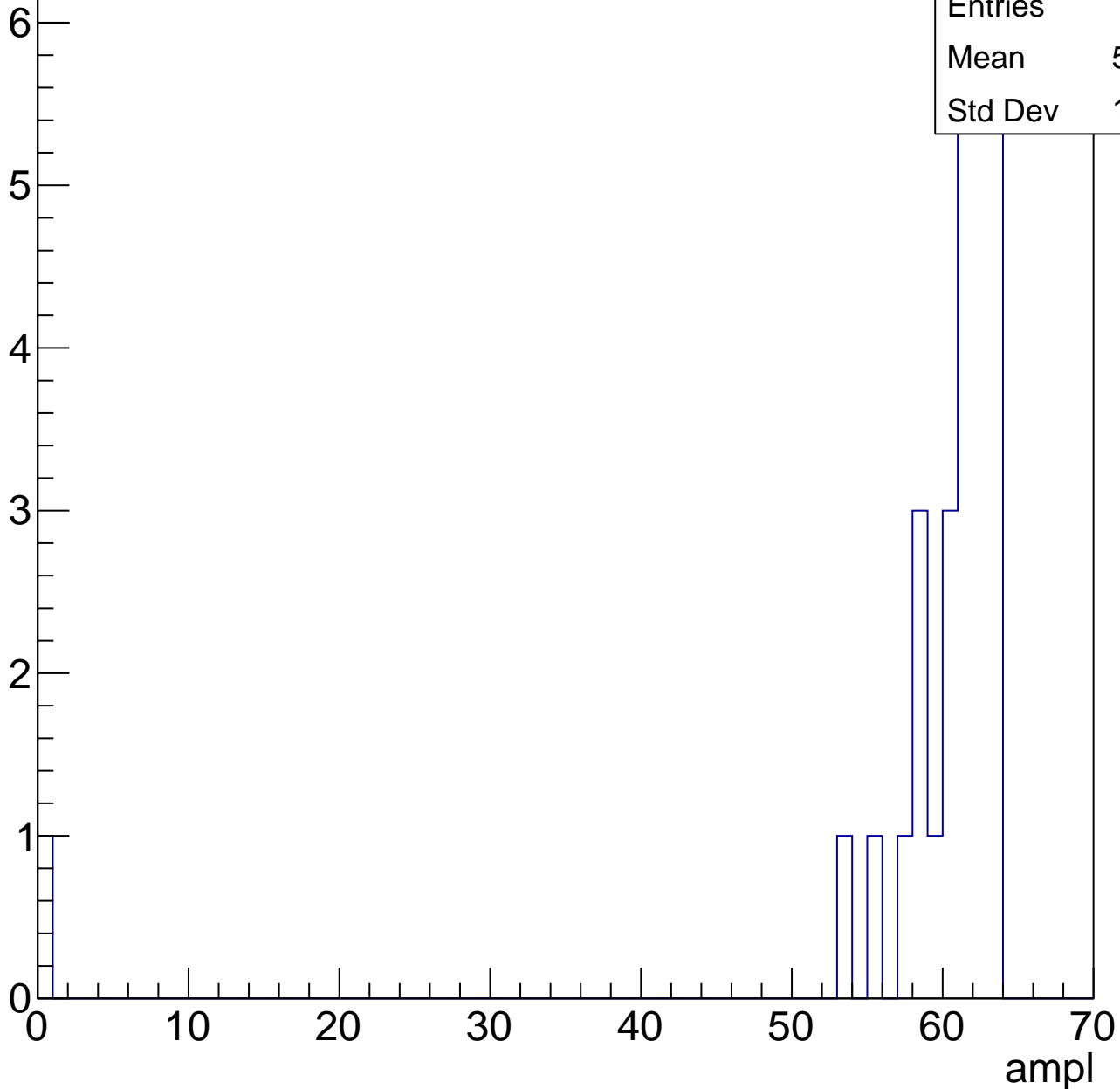


# B1L000S, U1-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	58.41
Std Dev	11.31

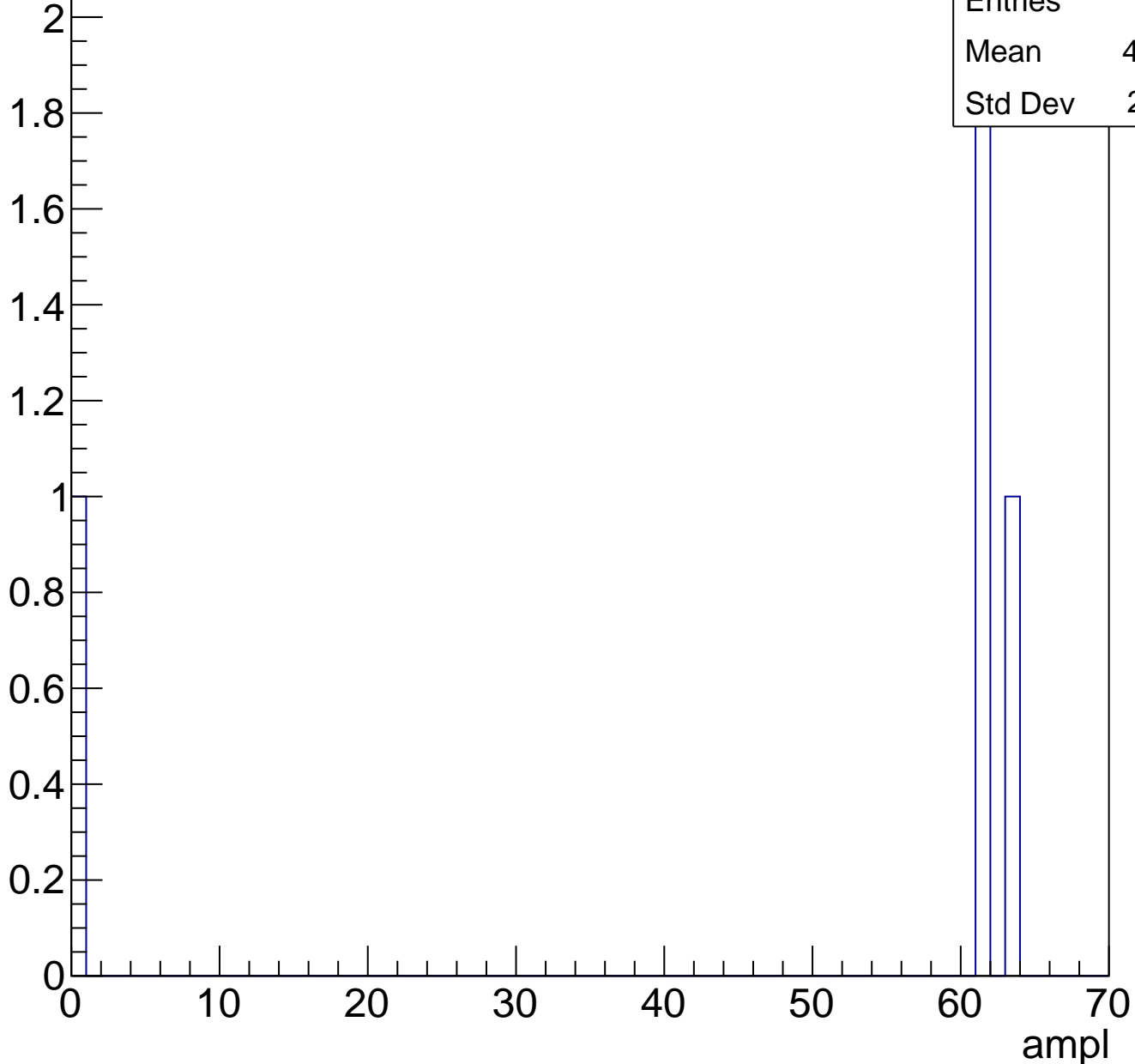




# B1L000S, U1-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	46.25
Std Dev	26.71

# B1L000S, U1-ch47, adc0

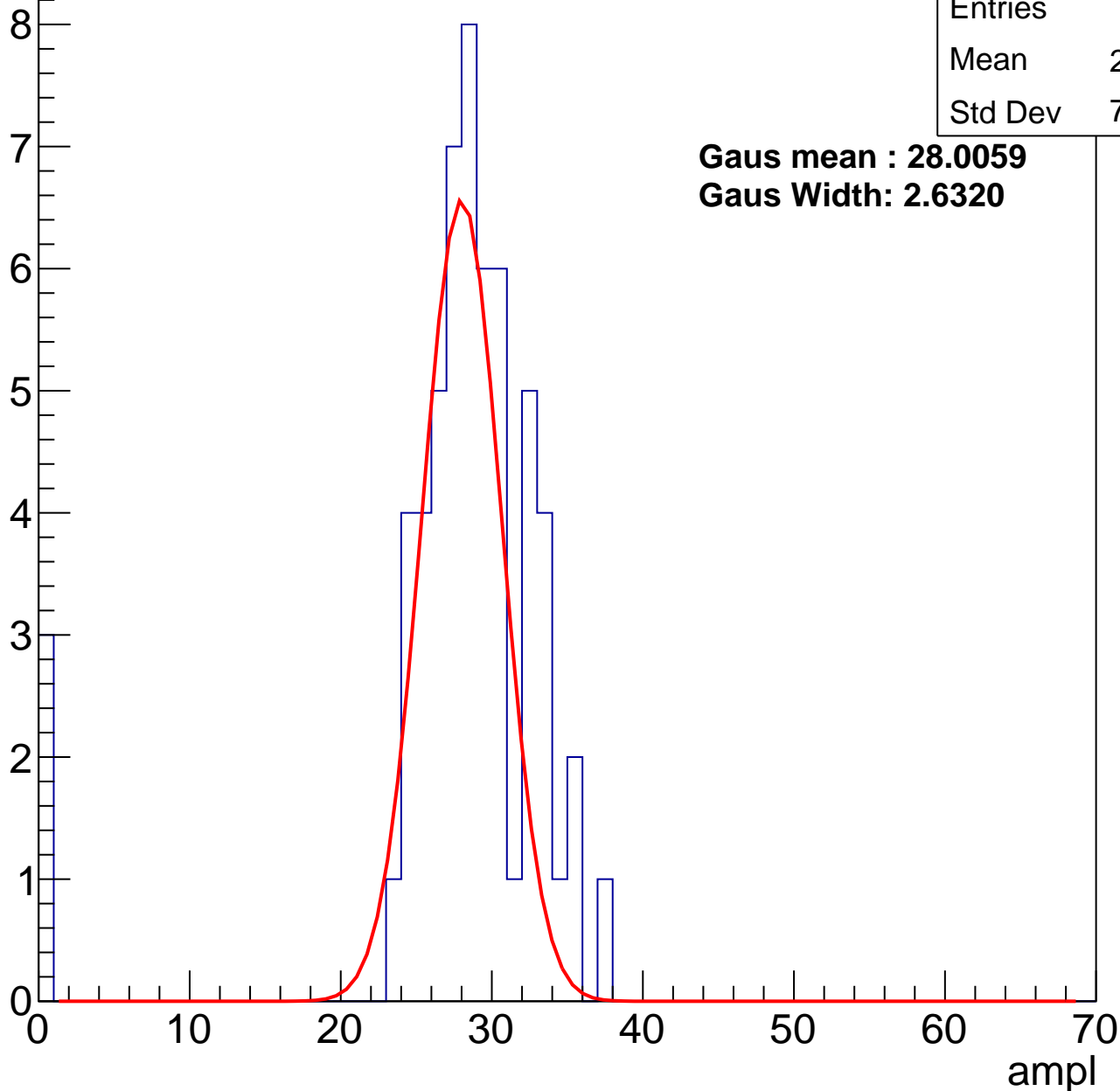
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	27.24
Std Dev	7.077

**Gaus mean : 28.0059**

**Gaus Width: 2.6320**



# B1L000S, U1-ch47, adc1

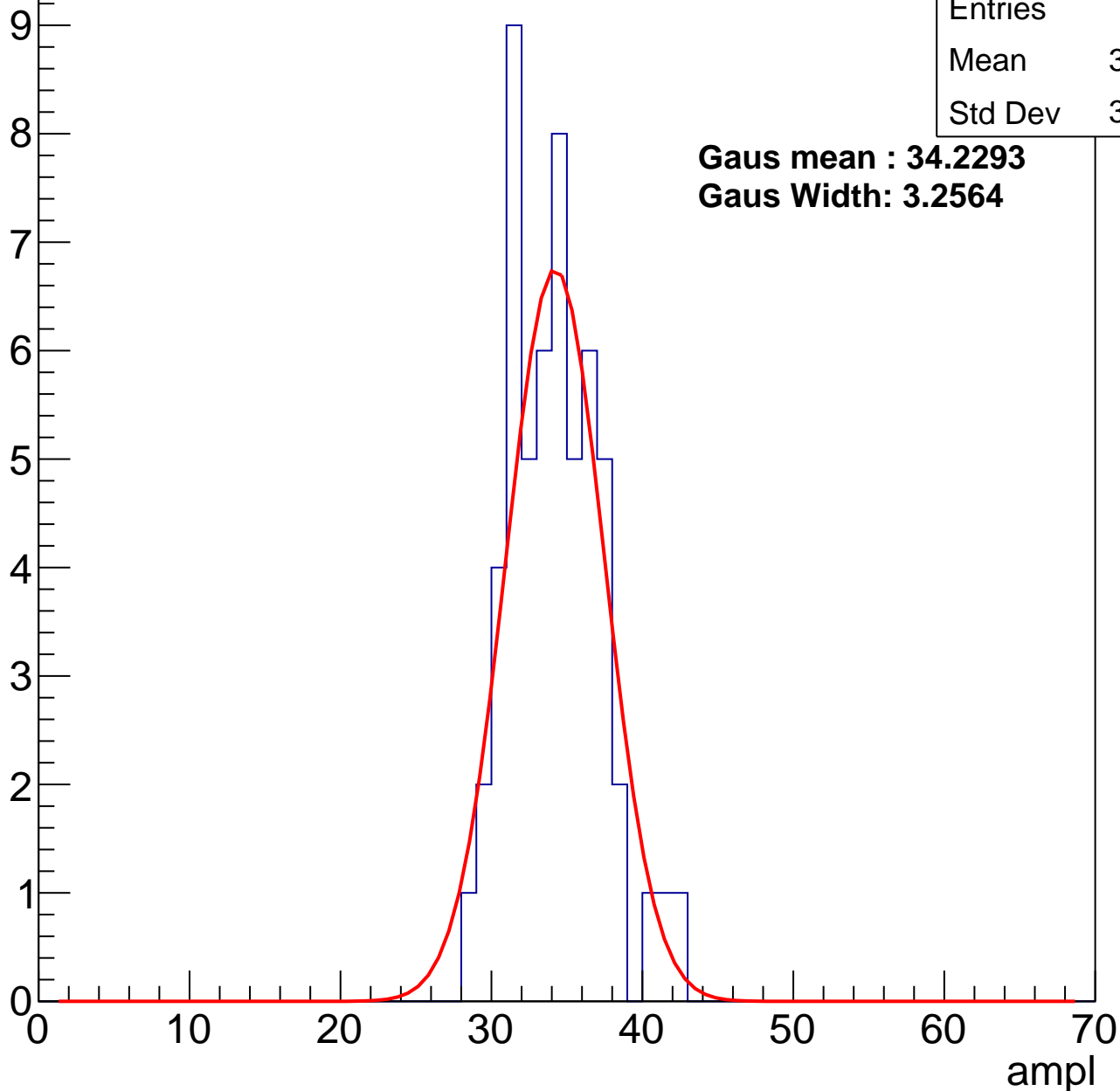
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	33.75
Std Dev	3.013

**Gaus mean : 34.2293**

**Gaus Width: 3.2564**



# B1L000S, U1-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	40.28
Std Dev	3.145

**Gaus mean : 40.4574**

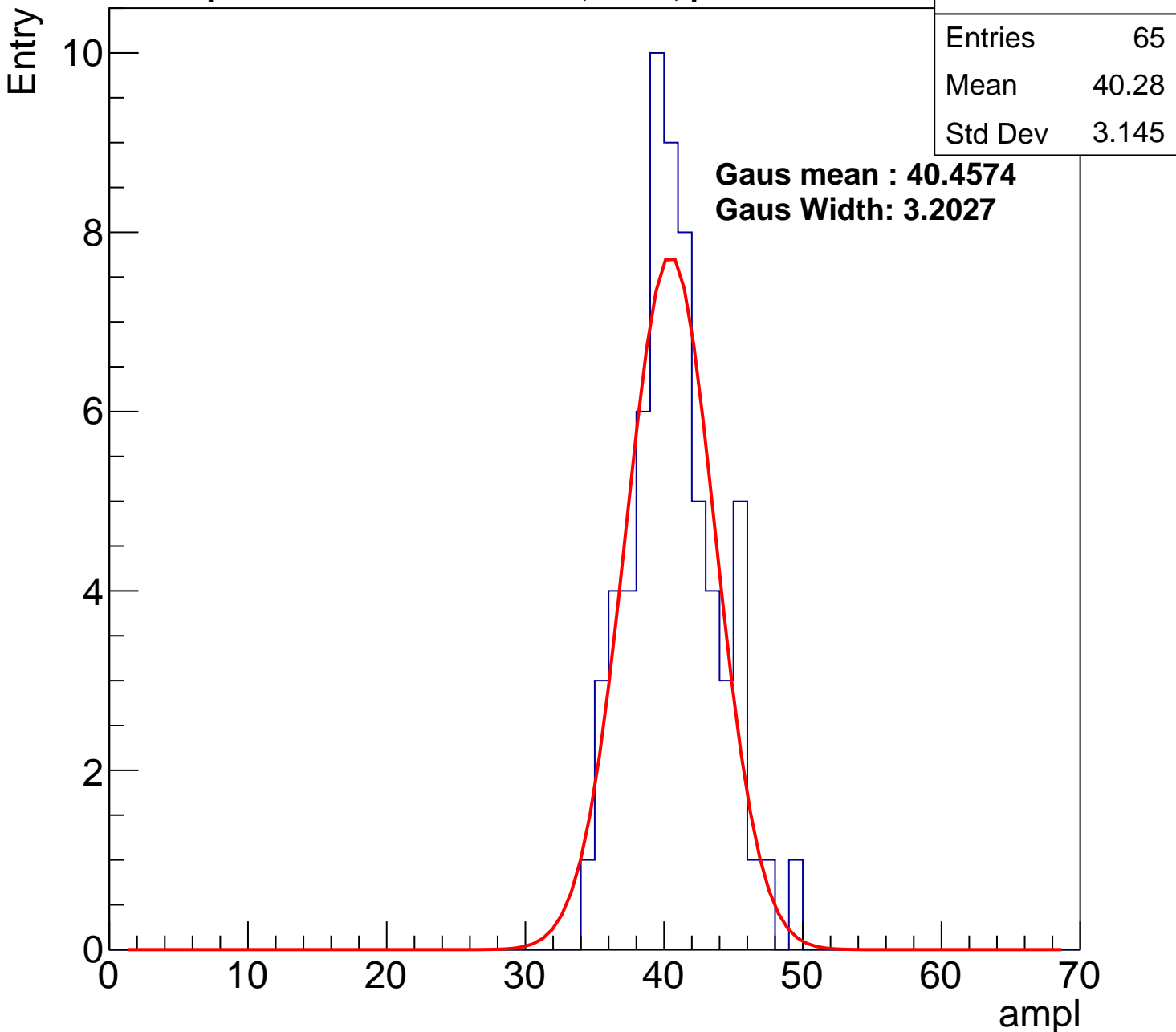
**Gaus Width: 3.2027**

Entry

10  
8  
6  
4  
2  
0

ampl

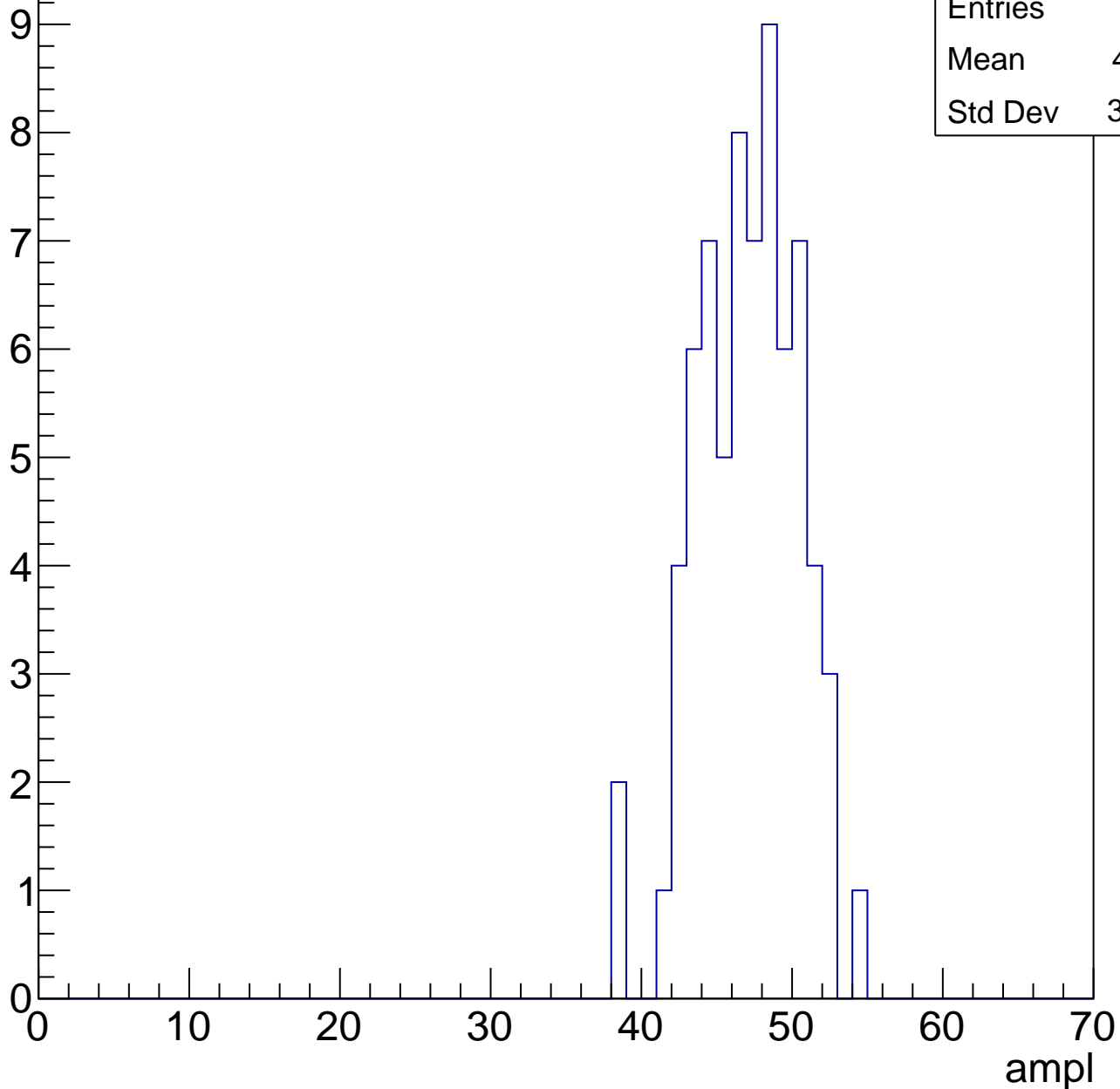
0 10 20 30 40 50 60 70



# B1L000S, U1-ch47, adc3

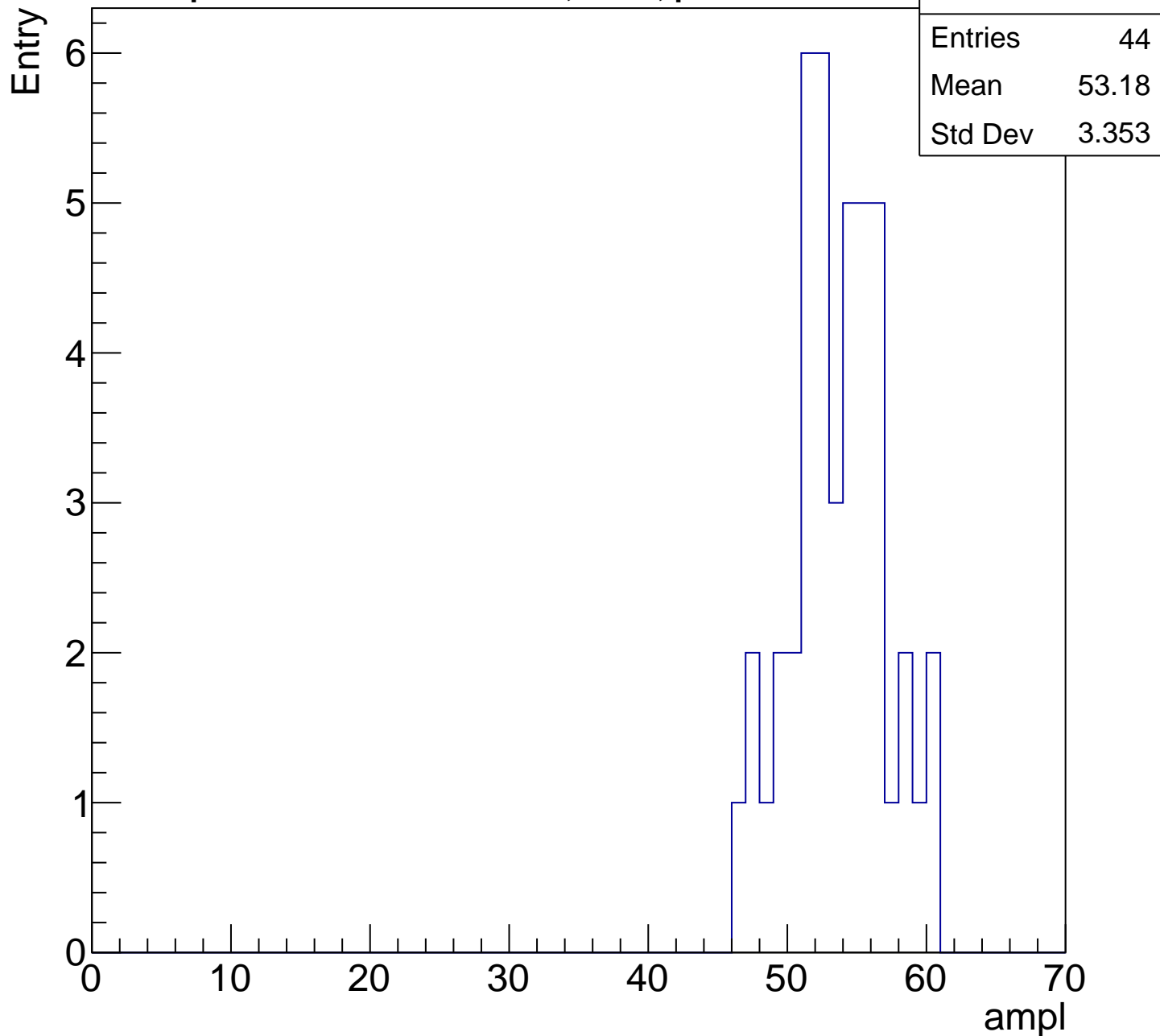
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



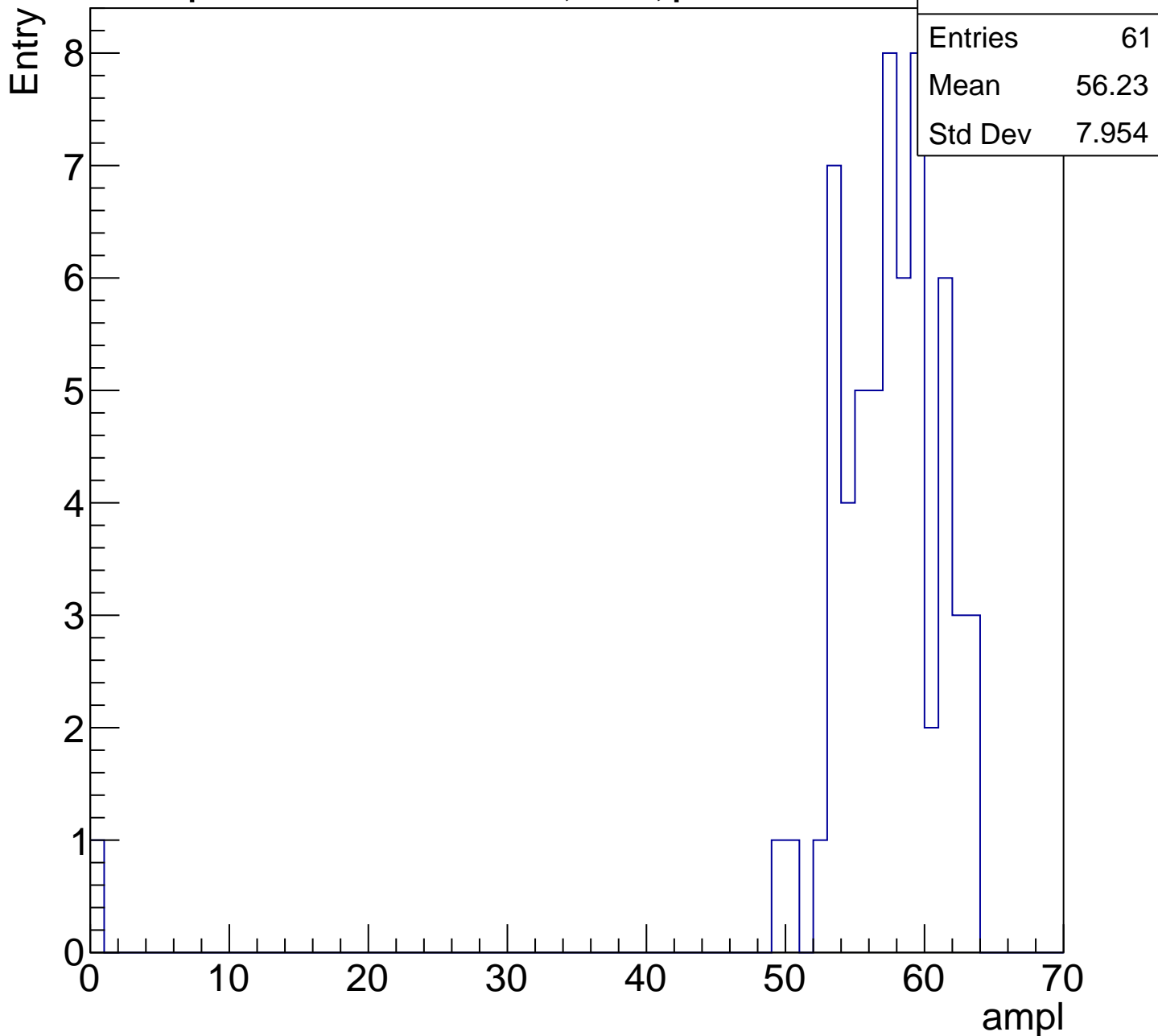
# B1L000S, U1-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

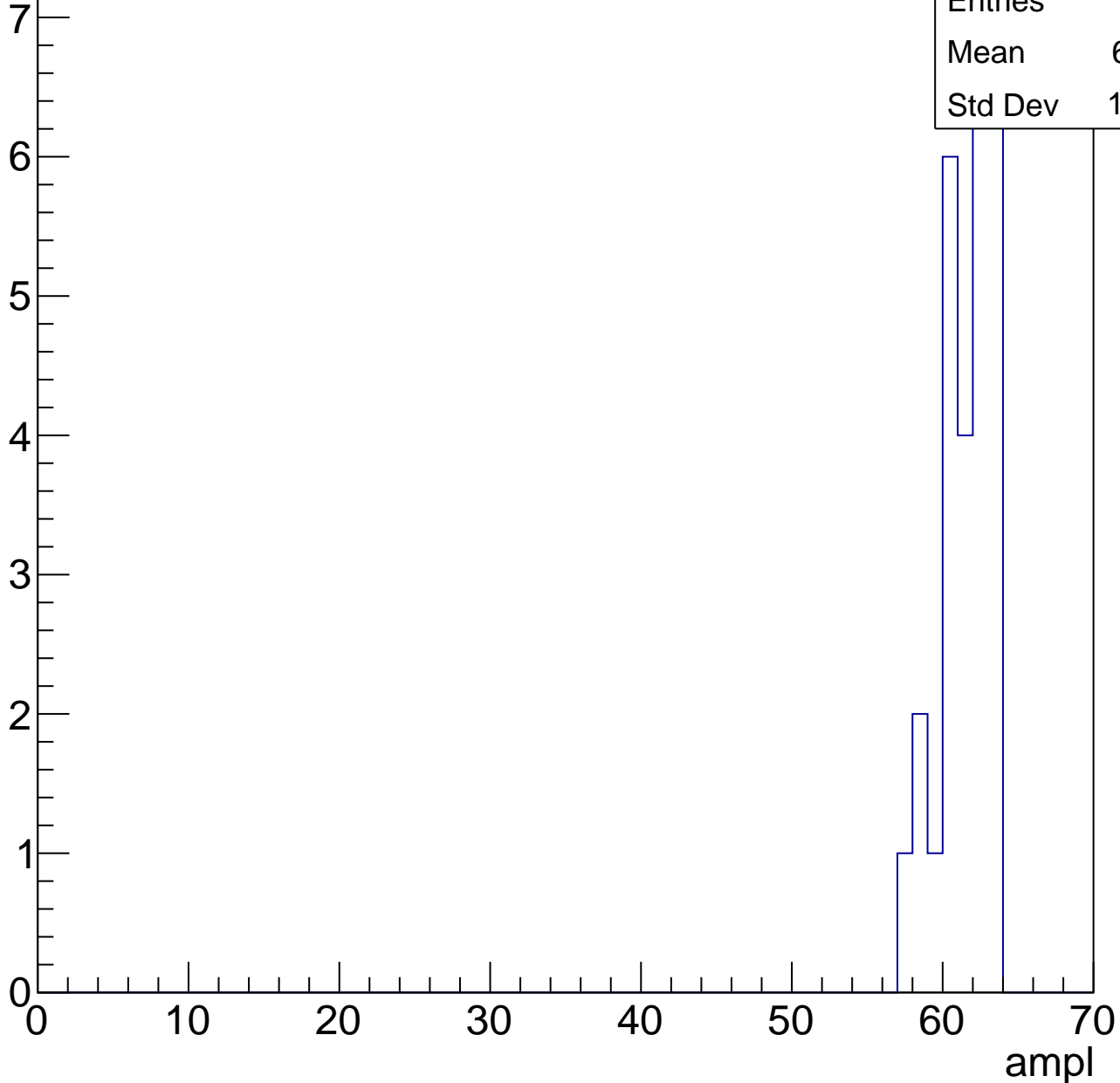


# B1L000S, U1-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	61.11
Std Dev	1.676





# B1L000S, U1-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch48, adc0

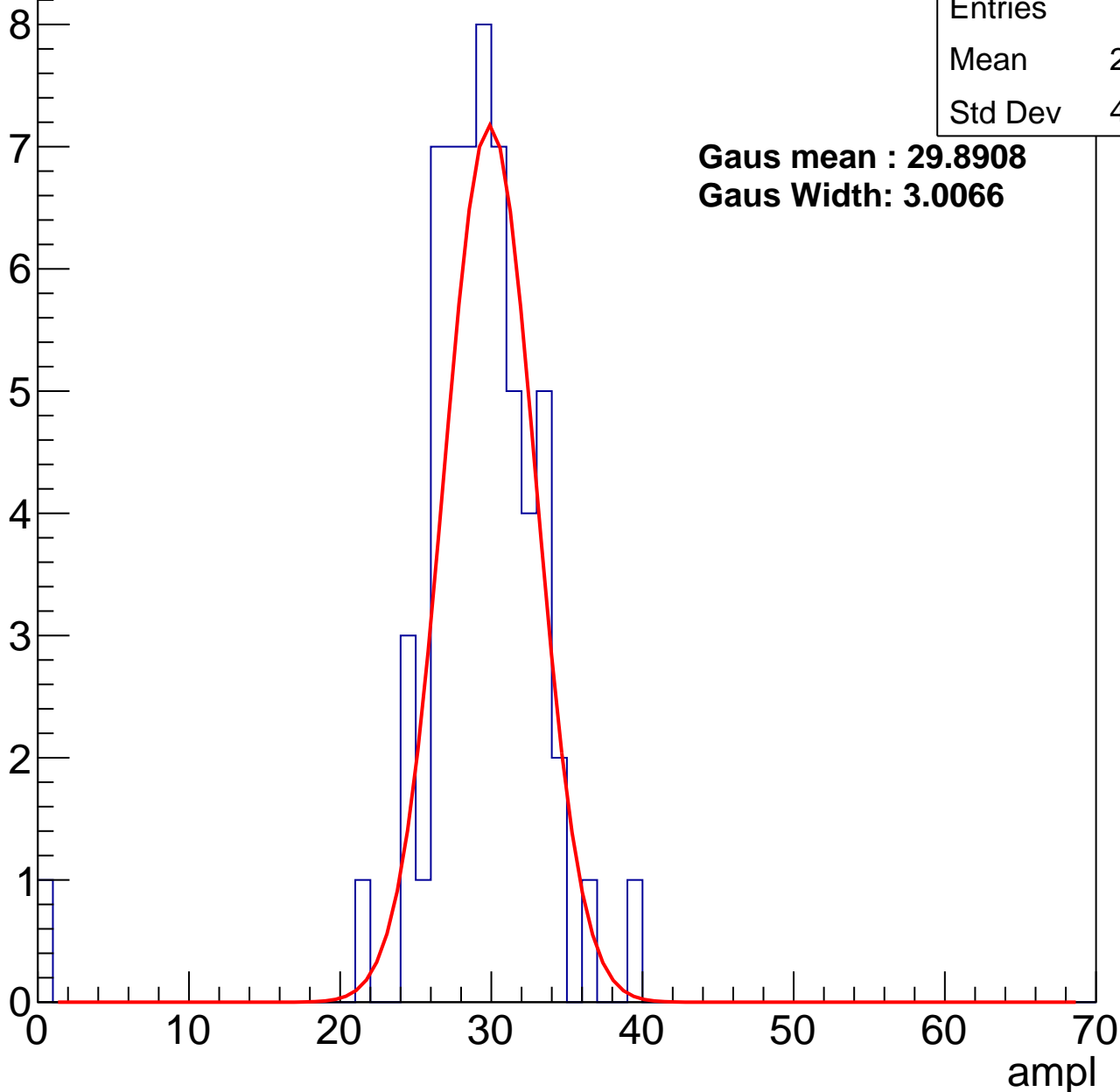
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	28.63
Std Dev	4.878

**Gaus mean : 29.8908**

**Gaus Width: 3.0066**



# B1L000S, U1-ch48, adc1

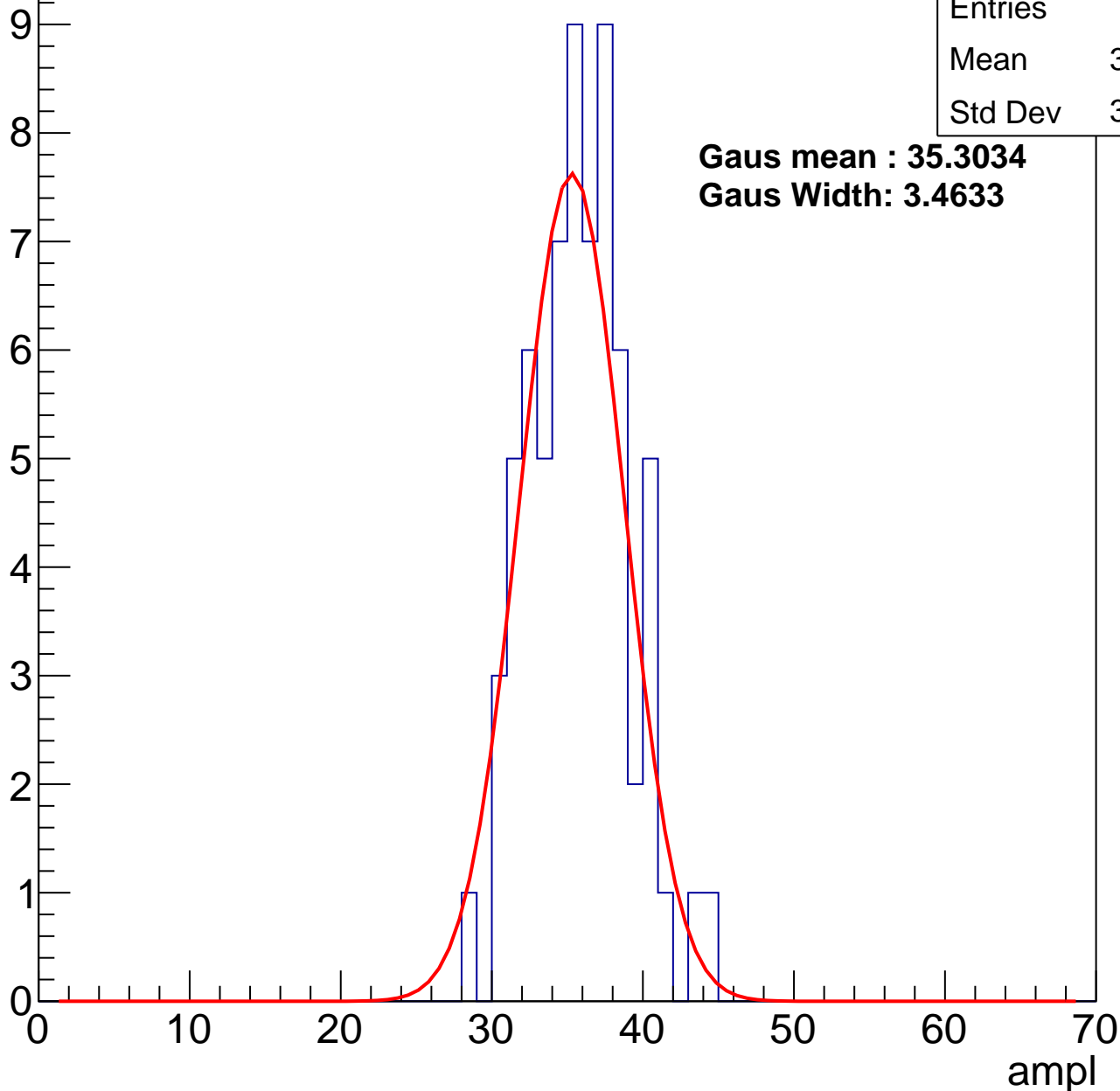
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.32
Std Dev	3.238

**Gaus mean : 35.3034**

**Gaus Width: 3.4633**



# B1L000S, U1-ch48, adc2

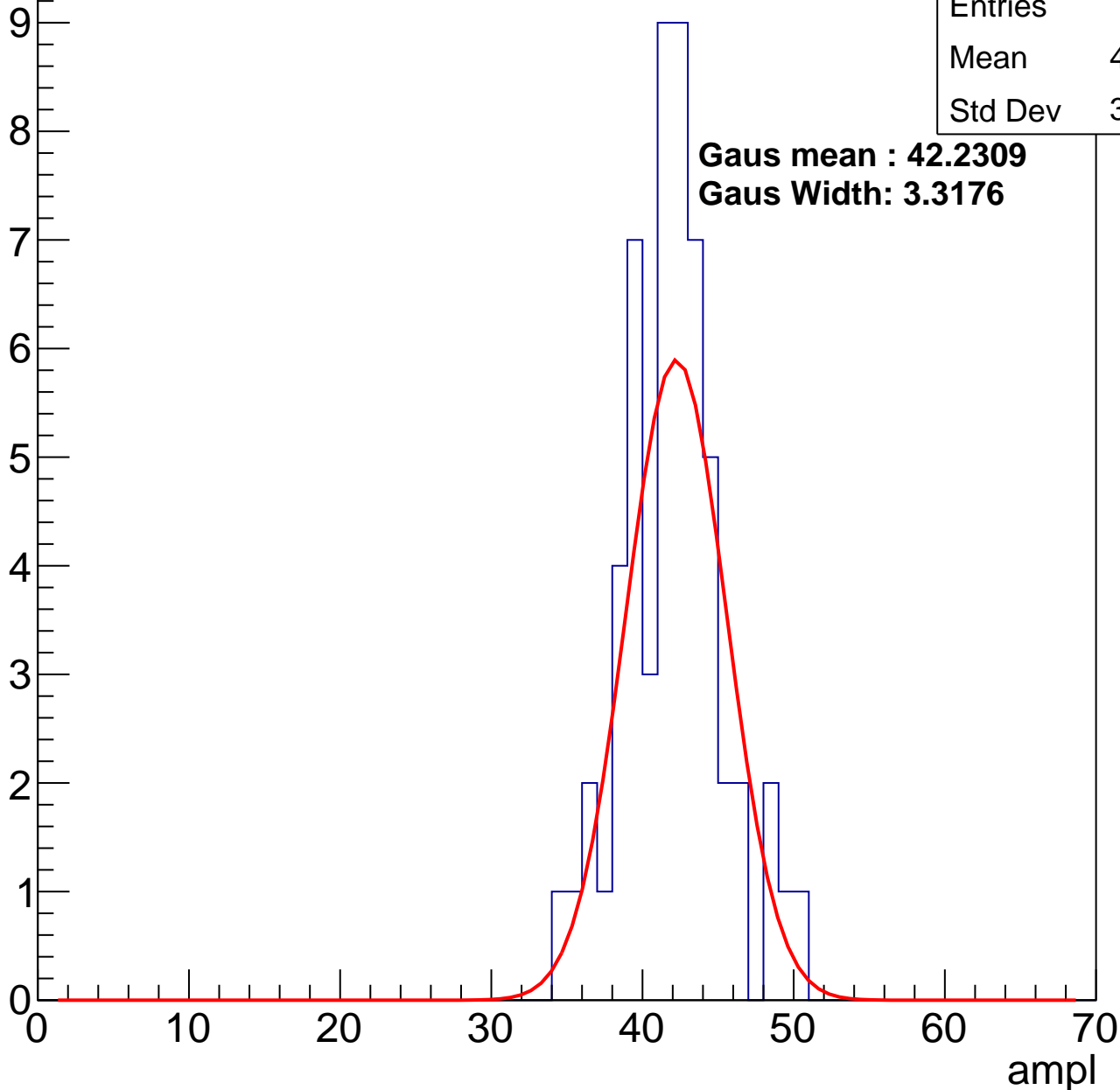
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.54
Std Dev	3.266

**Gaus mean : 42.2309**

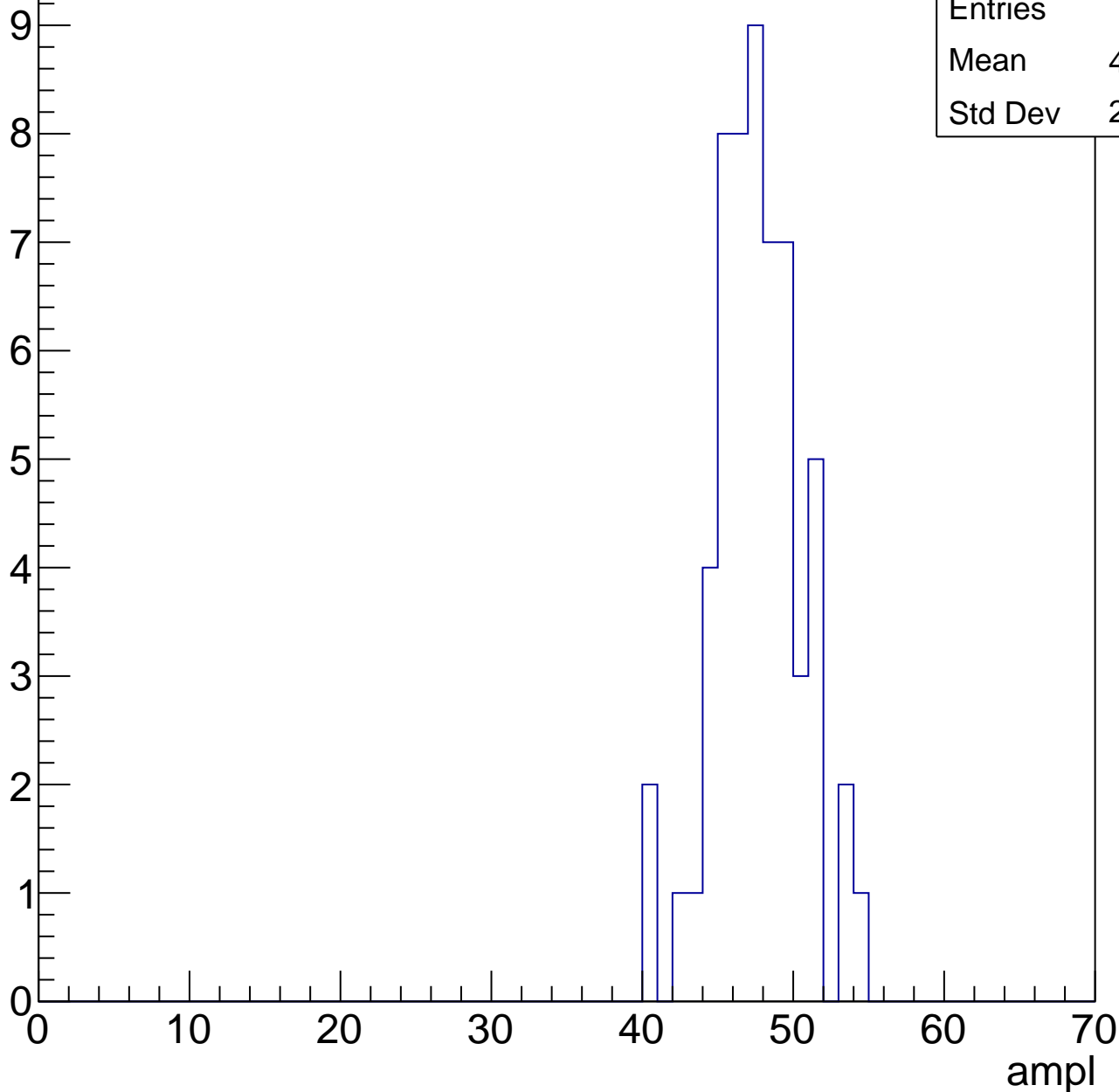
**Gaus Width: 3.3176**



# B1L000S, U1-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	58
Mean	47.17
Std Dev	2.866

# B1L000S, U1-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

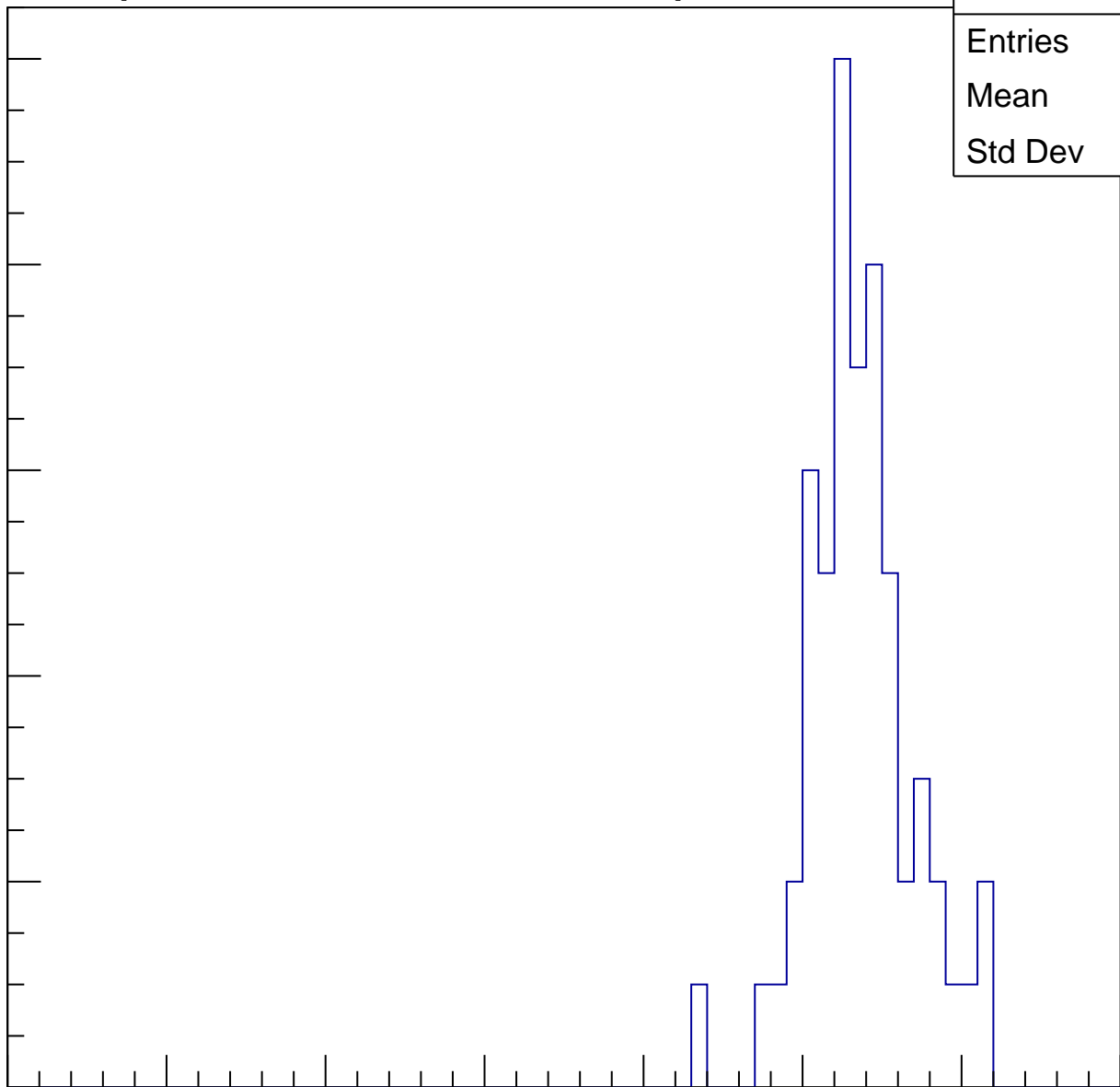
Entries	57
Mean	53.14
Std Dev	3.332

Entry

10  
8  
6  
4  
2  
0

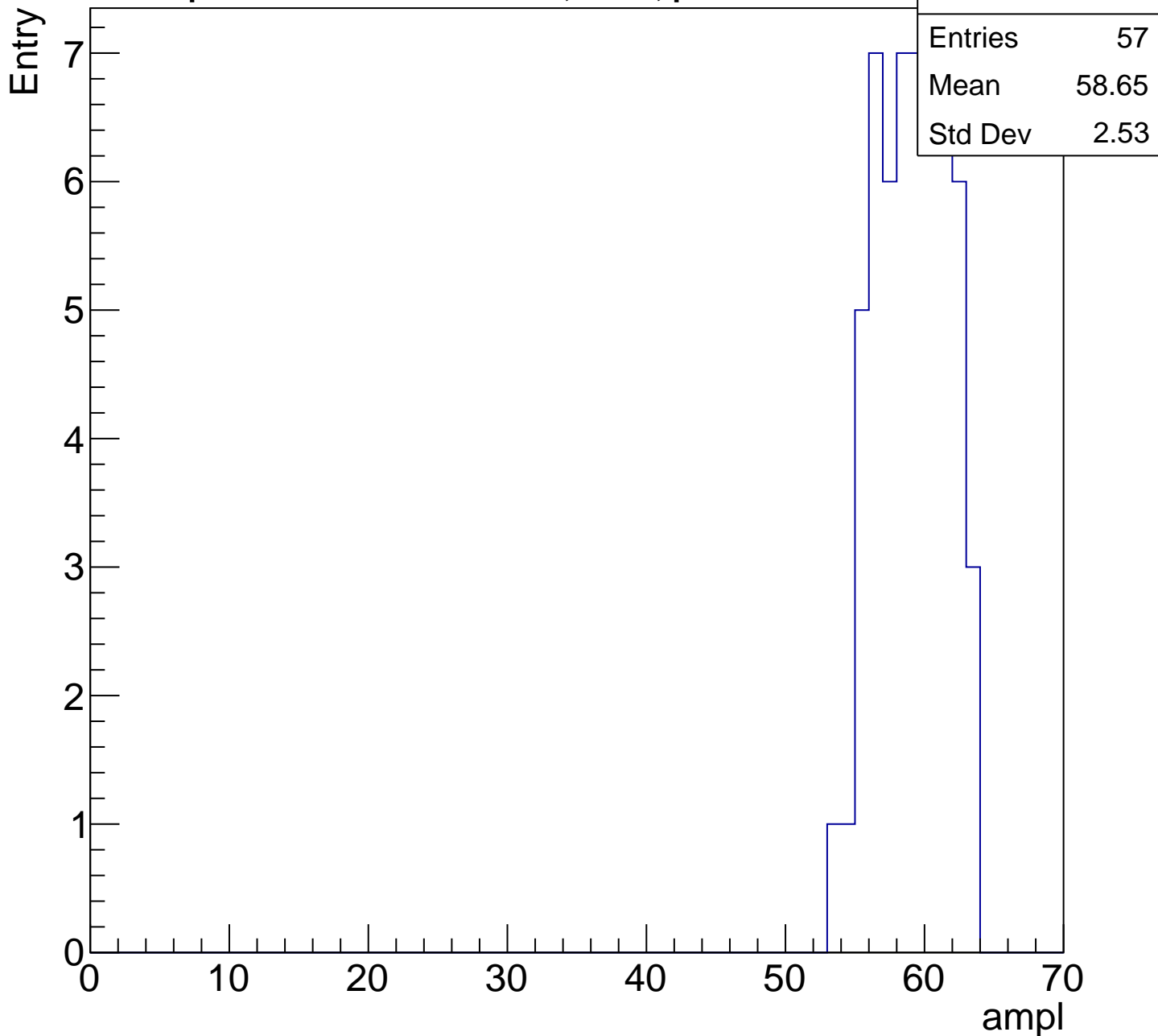
0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

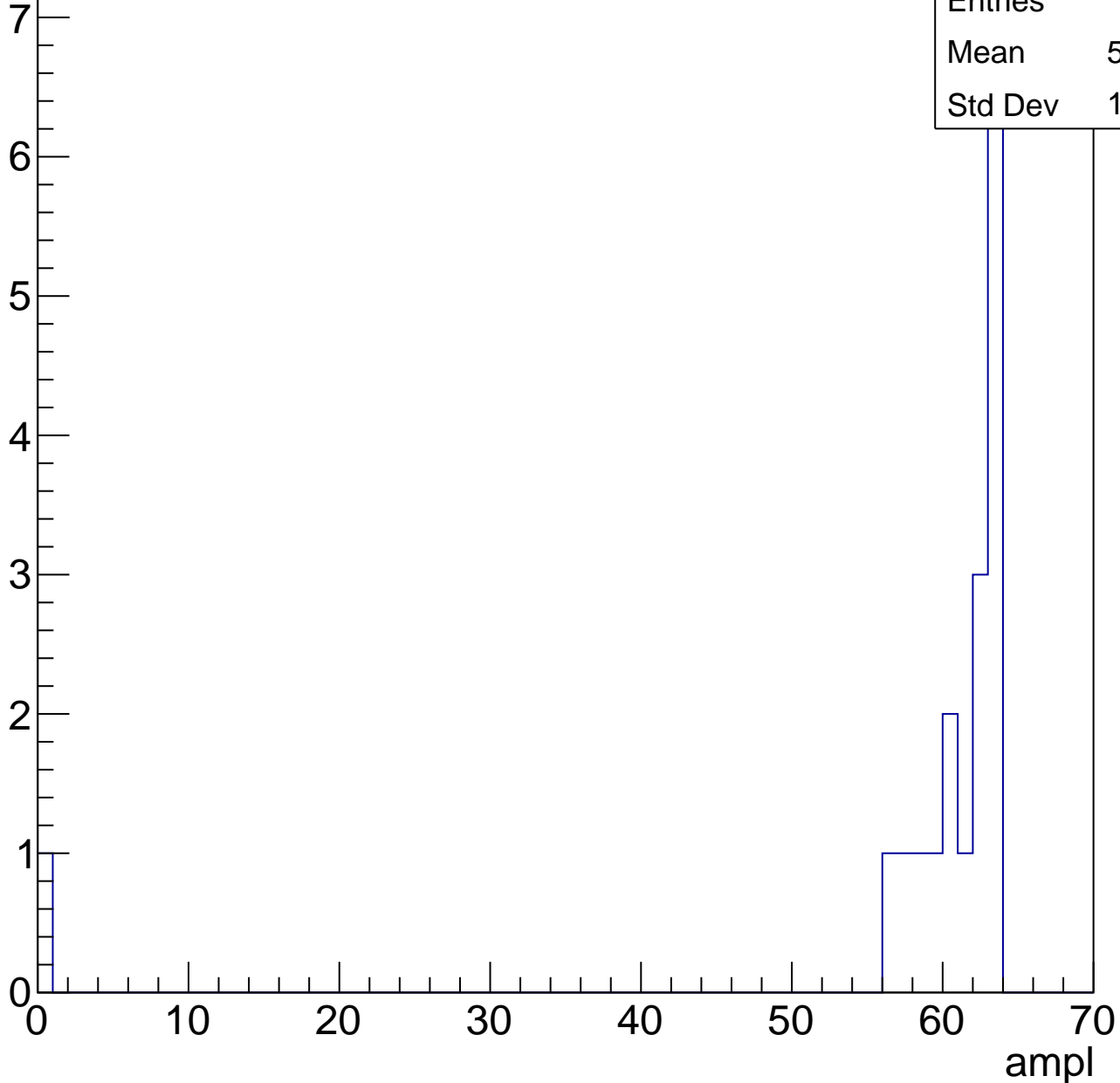


# B1L000S, U1-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	57.67
Std Dev	14.16

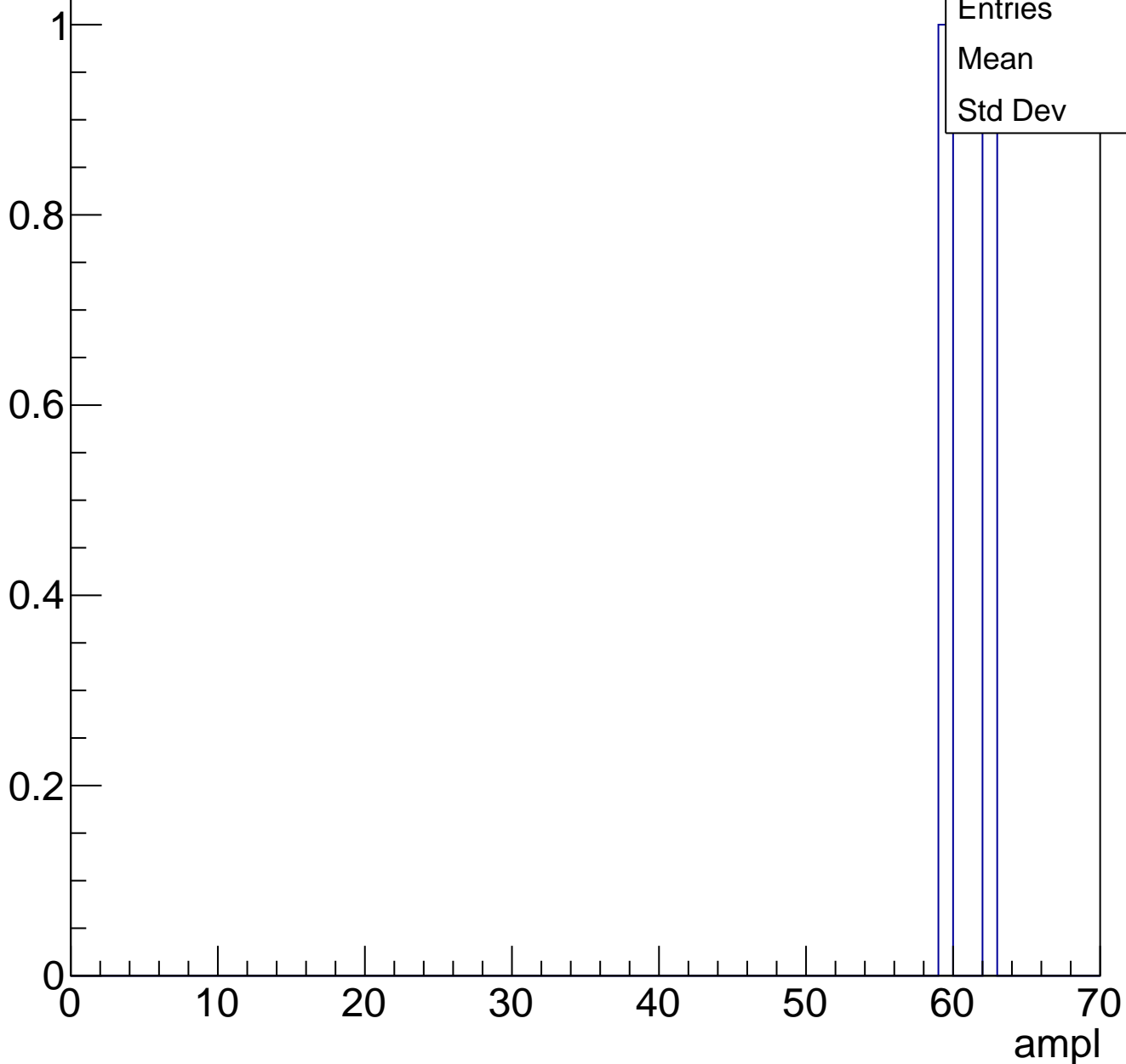




# B1L000S, U1-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch49, adc0

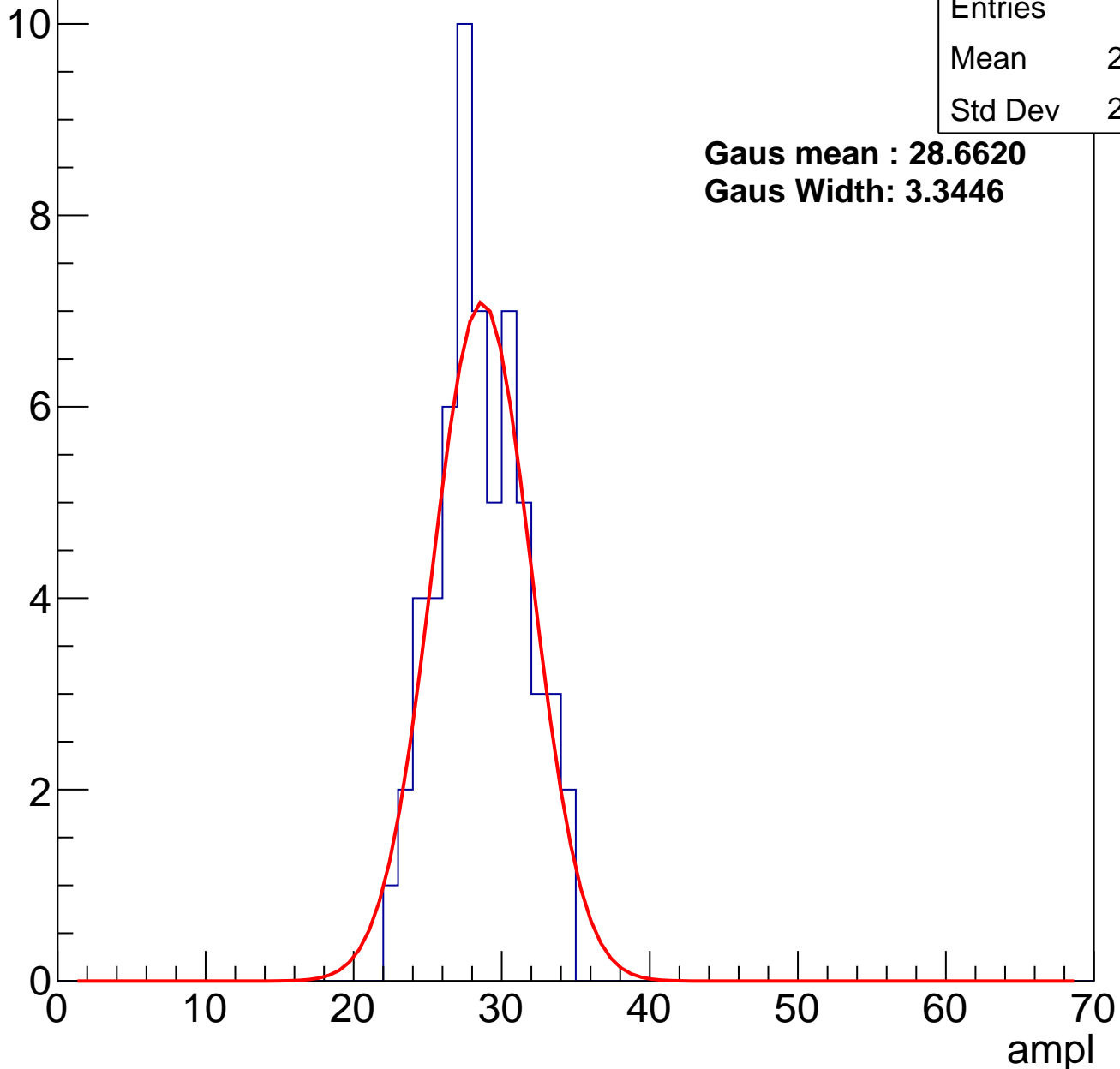
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	59
Mean	28.12
Std Dev	2.888

**Gaus mean : 28.6620**

**Gaus Width: 3.3446**

Entry



# B1L000S, U1-ch49, adc1

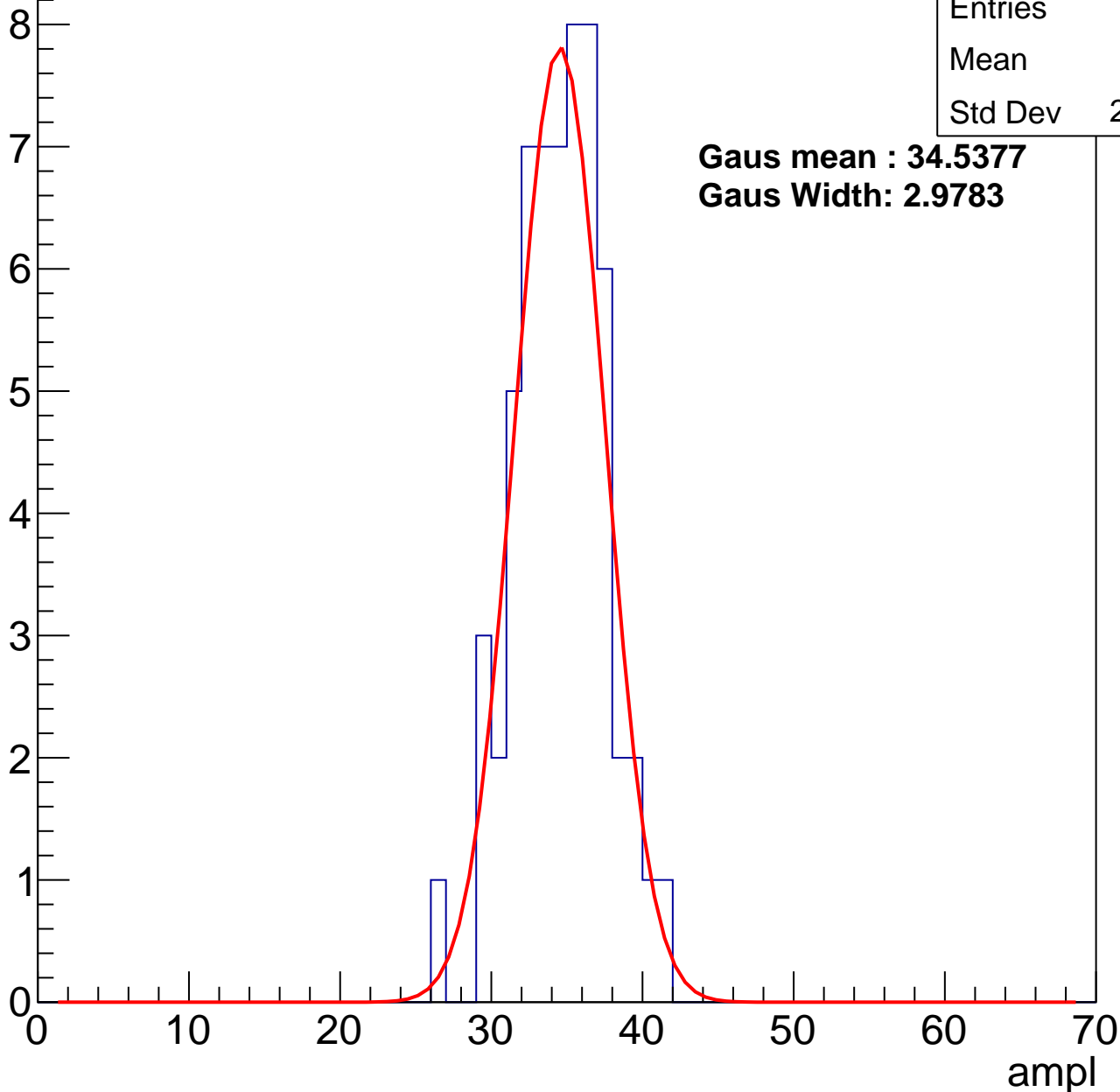
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	34.1
Std Dev	2.919

**Gaus mean : 34.5377**

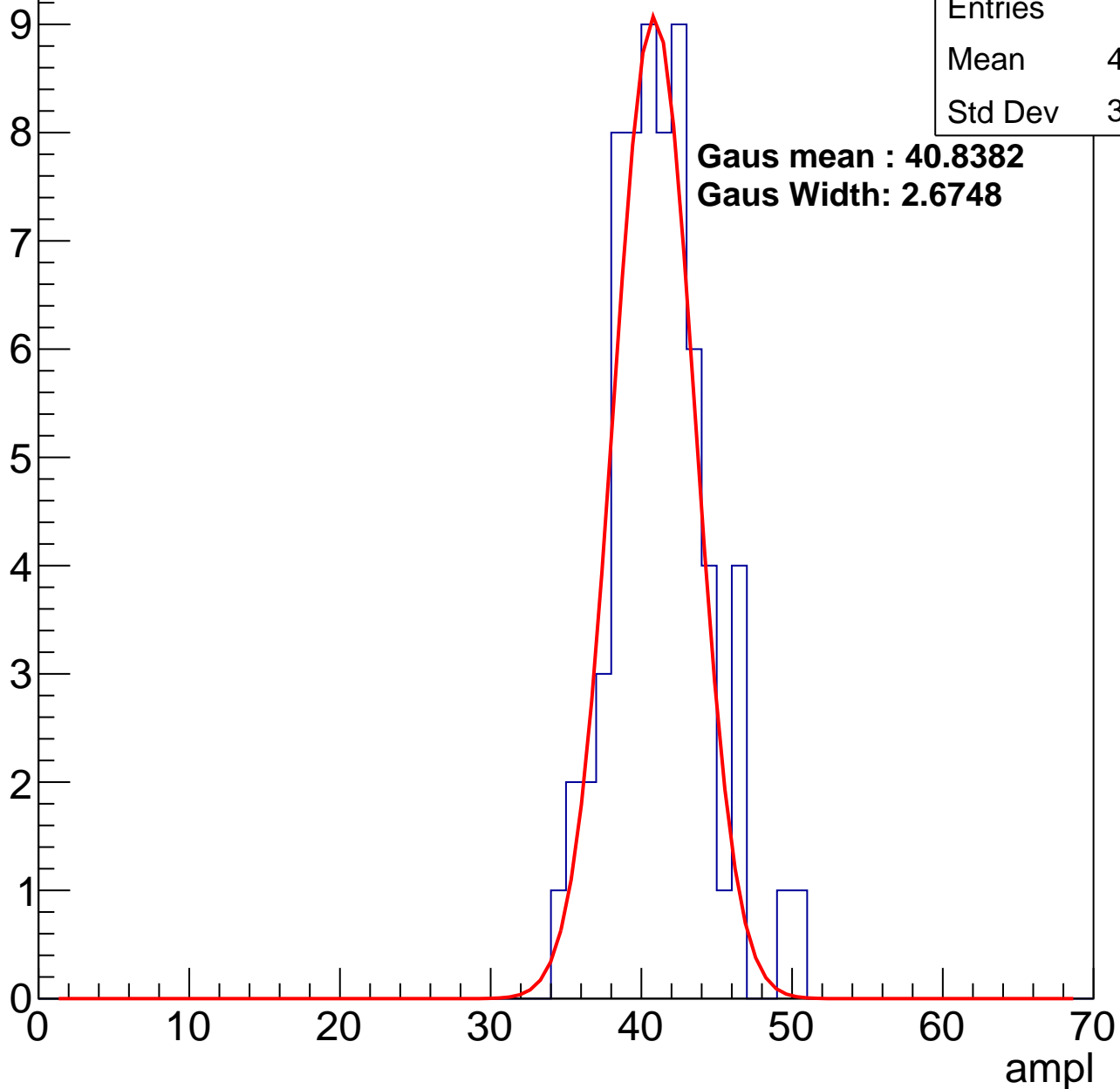
**Gaus Width: 2.9783**



# B1L000S, U1-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



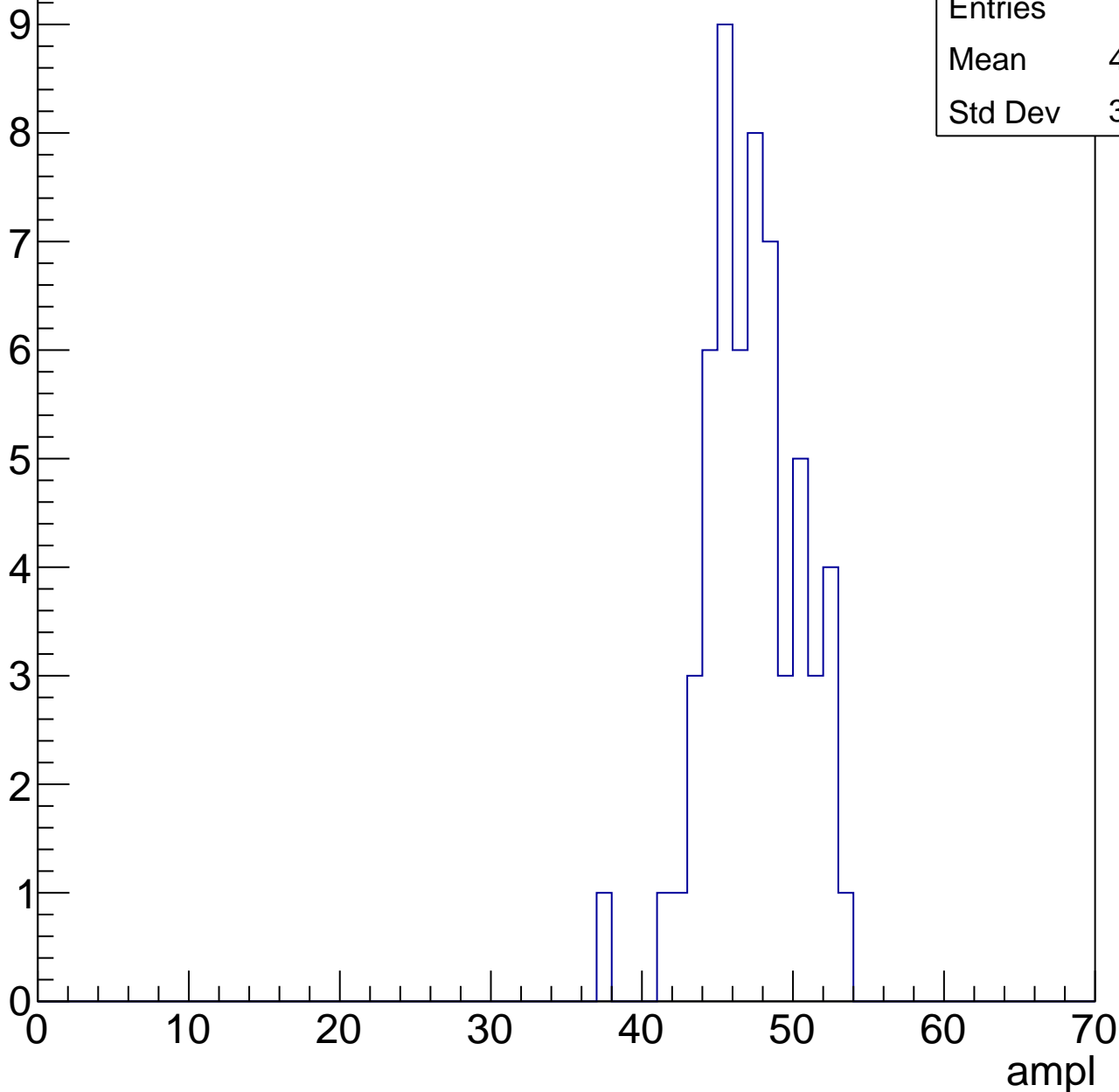
Entries	67
Mean	40.76
Std Dev	3.134

# B1L000S, U1-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	46.84
Std Dev	3.084

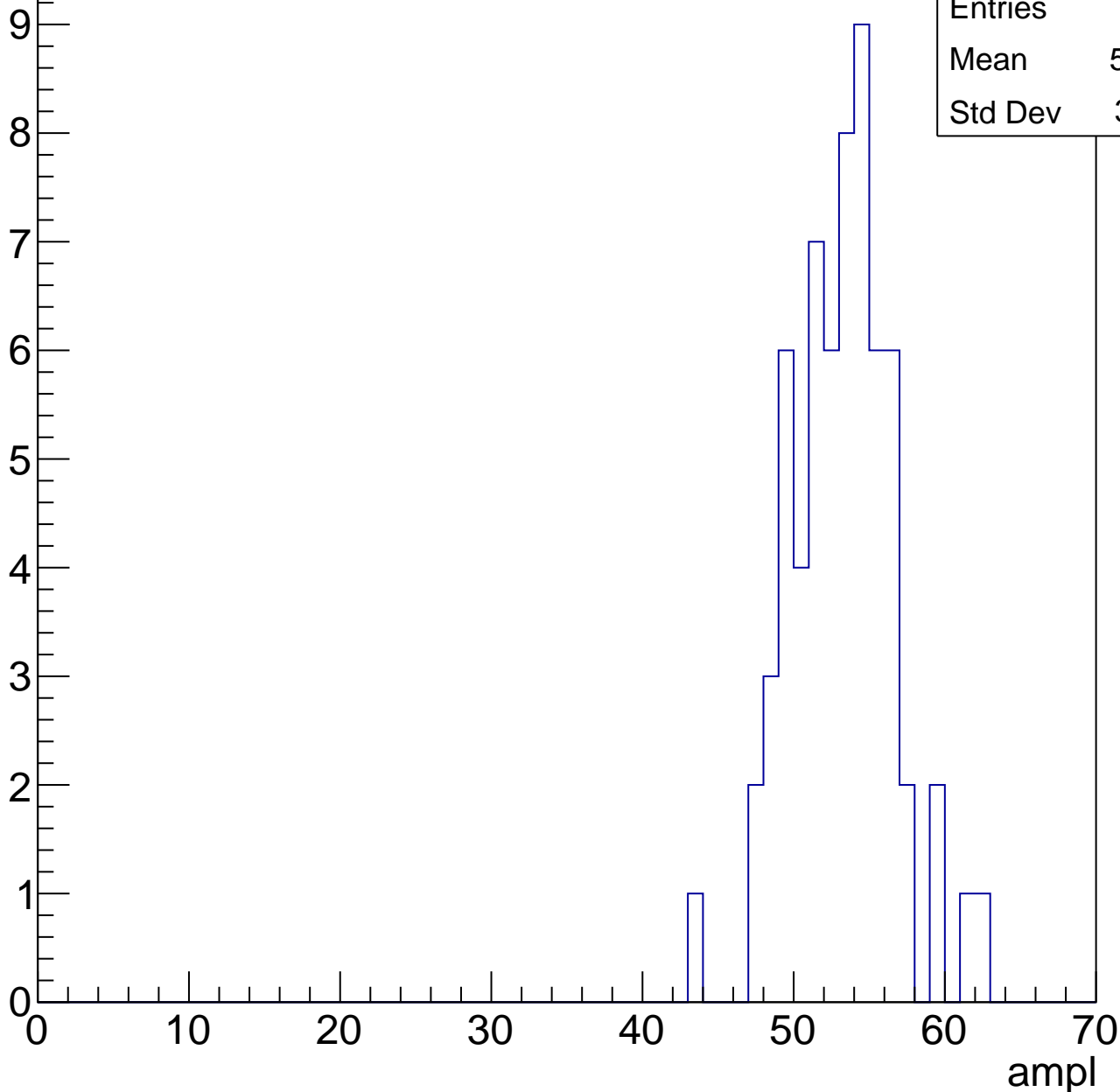


# B1L000S, U1-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

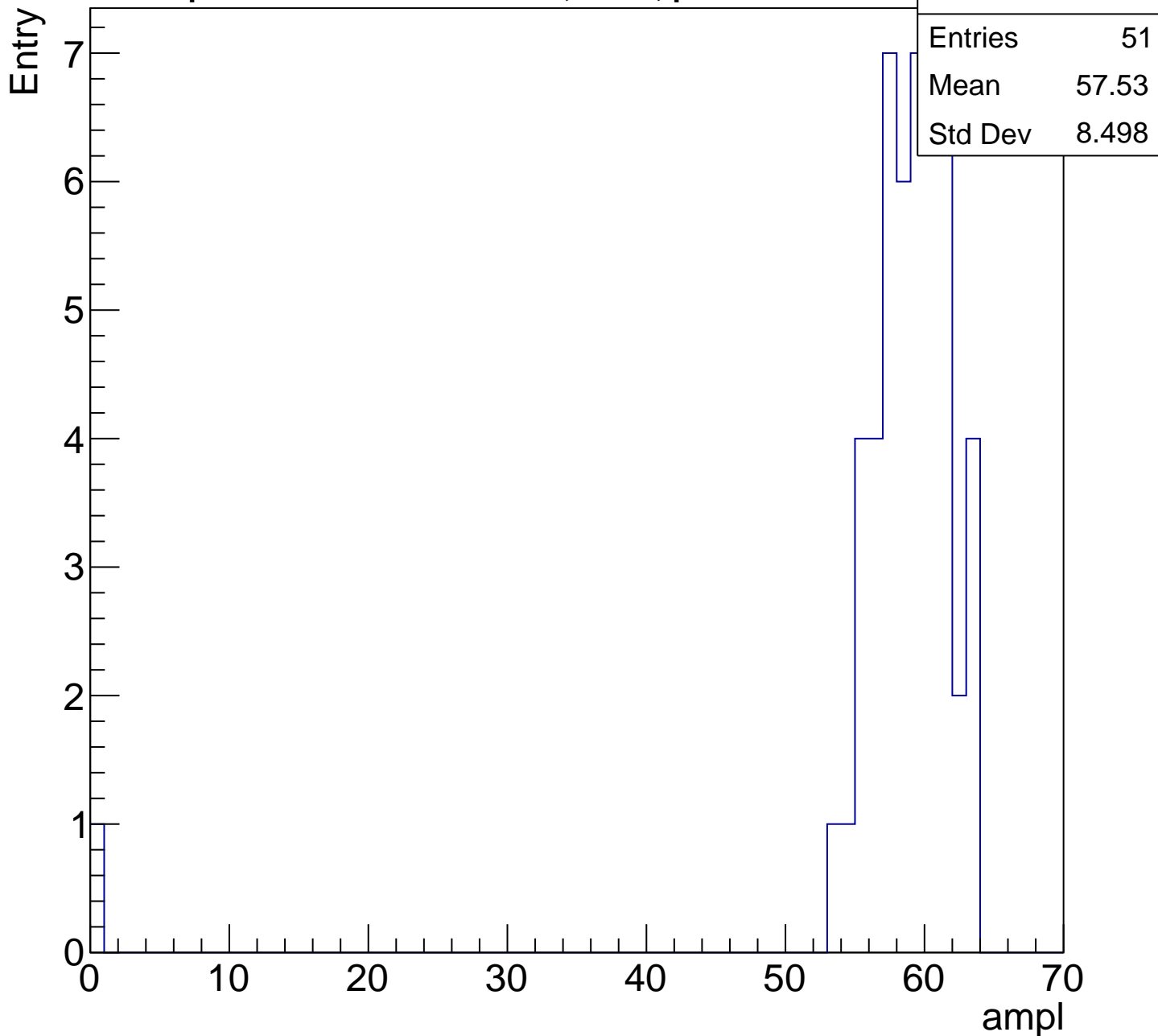
Entry

Entries	64
Mean	52.73
Std Dev	3.411



# B1L000S, U1-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

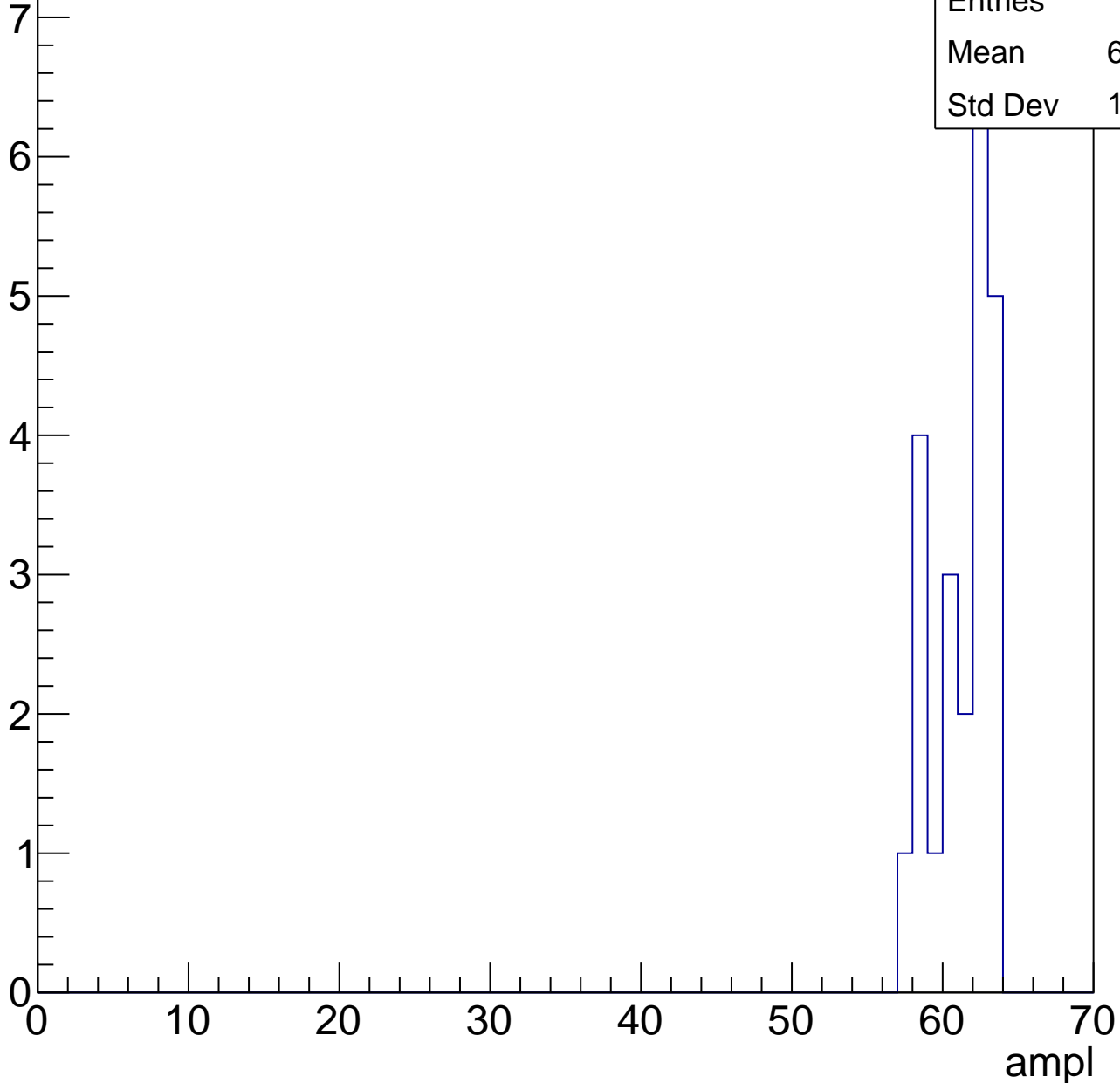


# B1L000S, U1-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	60.83
Std Dev	1.926





# B1L000S, U1-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U1-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	27.51
Std Dev	5.621

**Gaus mean : 28.8315**

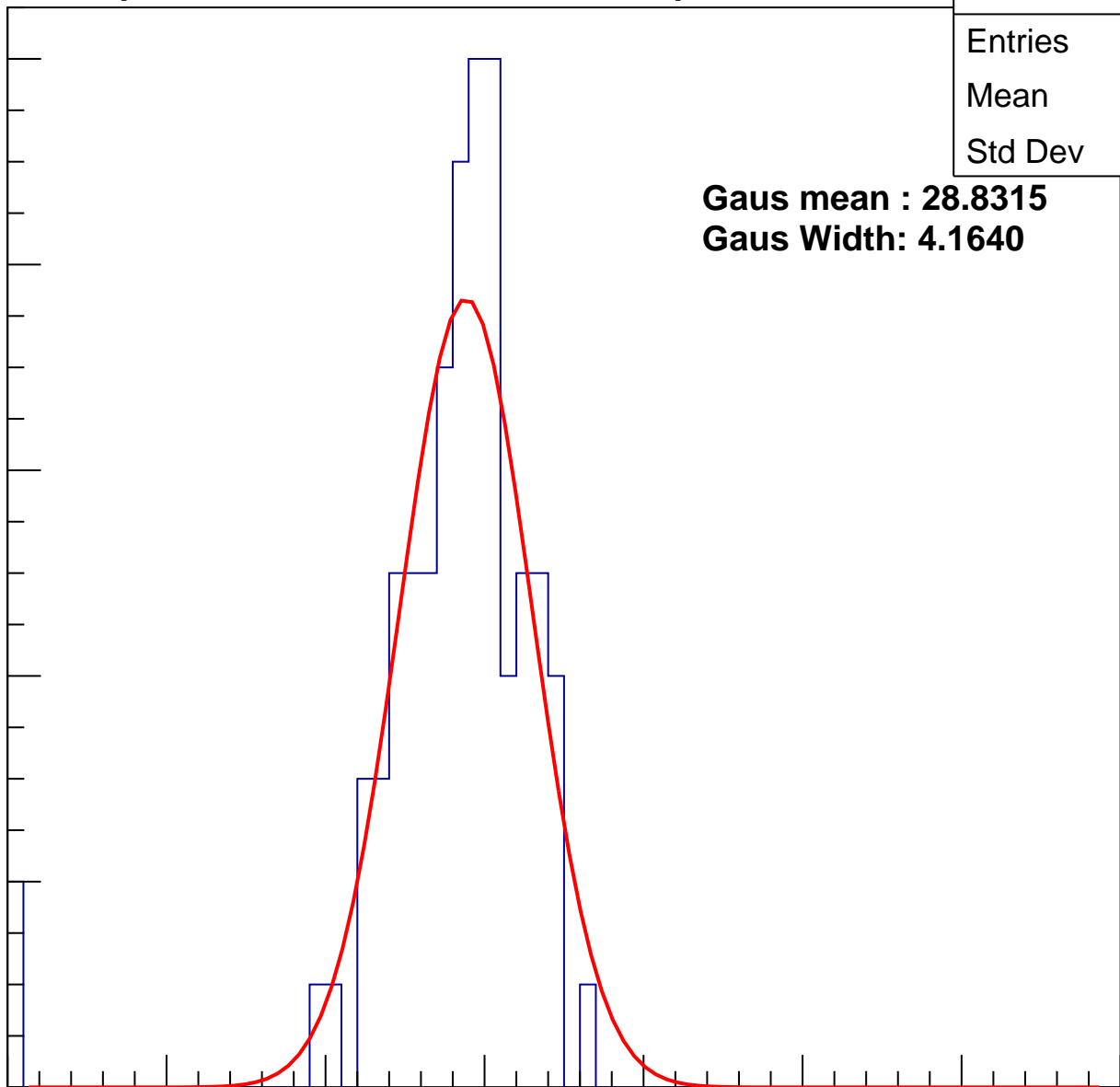
**Gaus Width: 4.1640**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch50, adc1

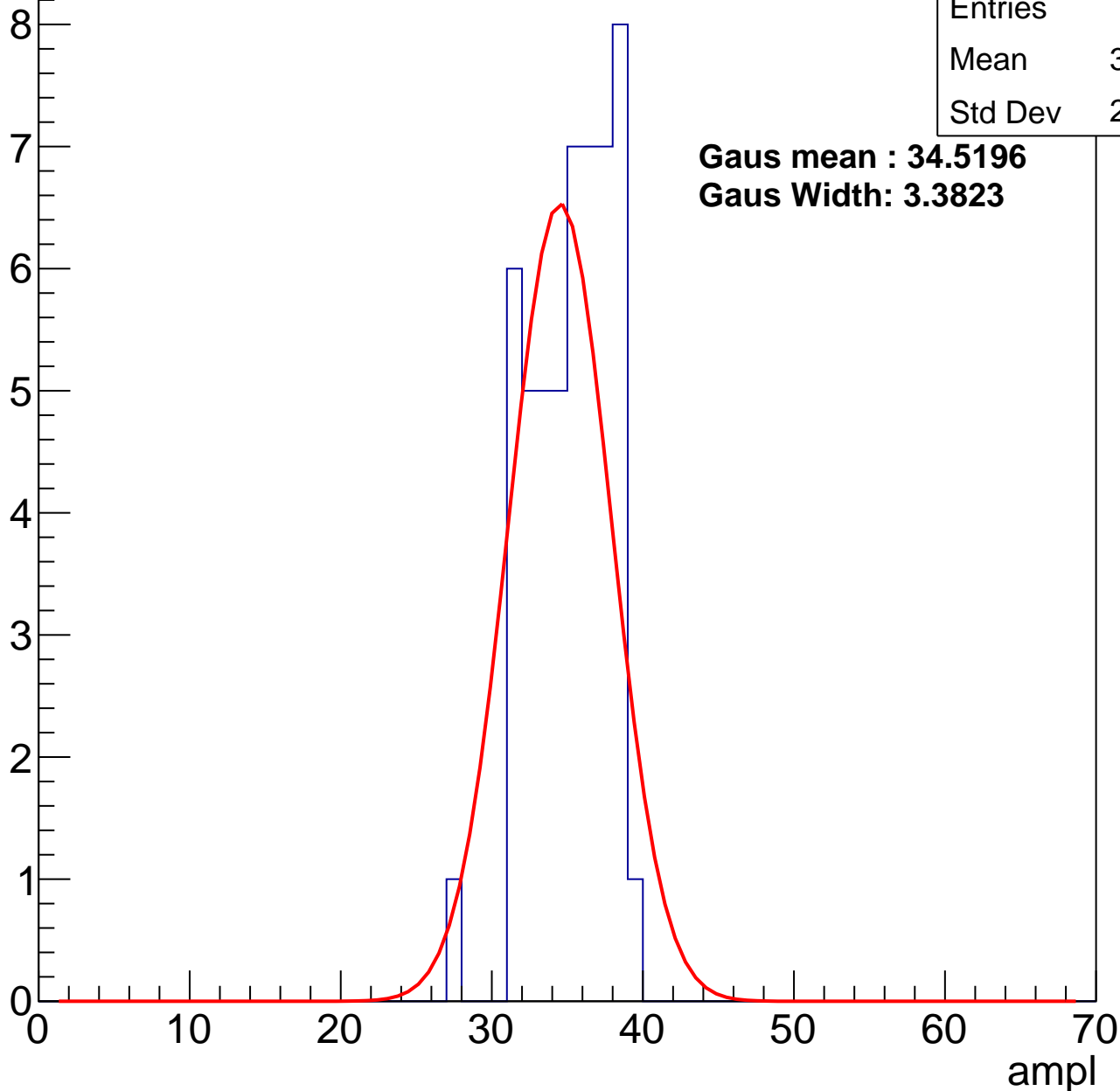
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	34.75
Std Dev	2.593

**Gaus mean : 34.5196**

**Gaus Width: 3.3823**



# B1L000S, U1-ch50, adc2

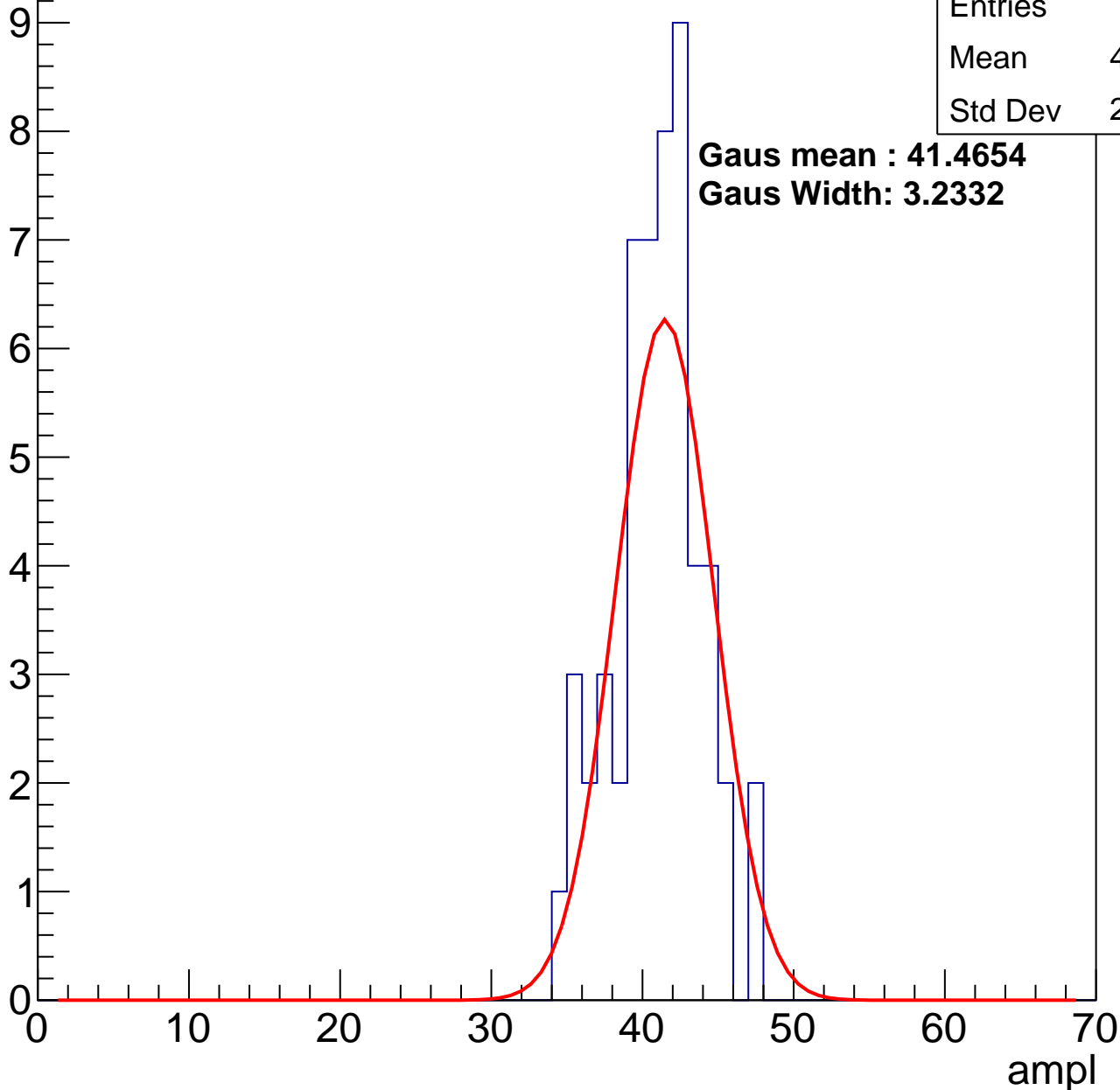
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	40.54
Std Dev	2.923

**Gaus mean : 41.4654**

**Gaus Width: 3.2332**

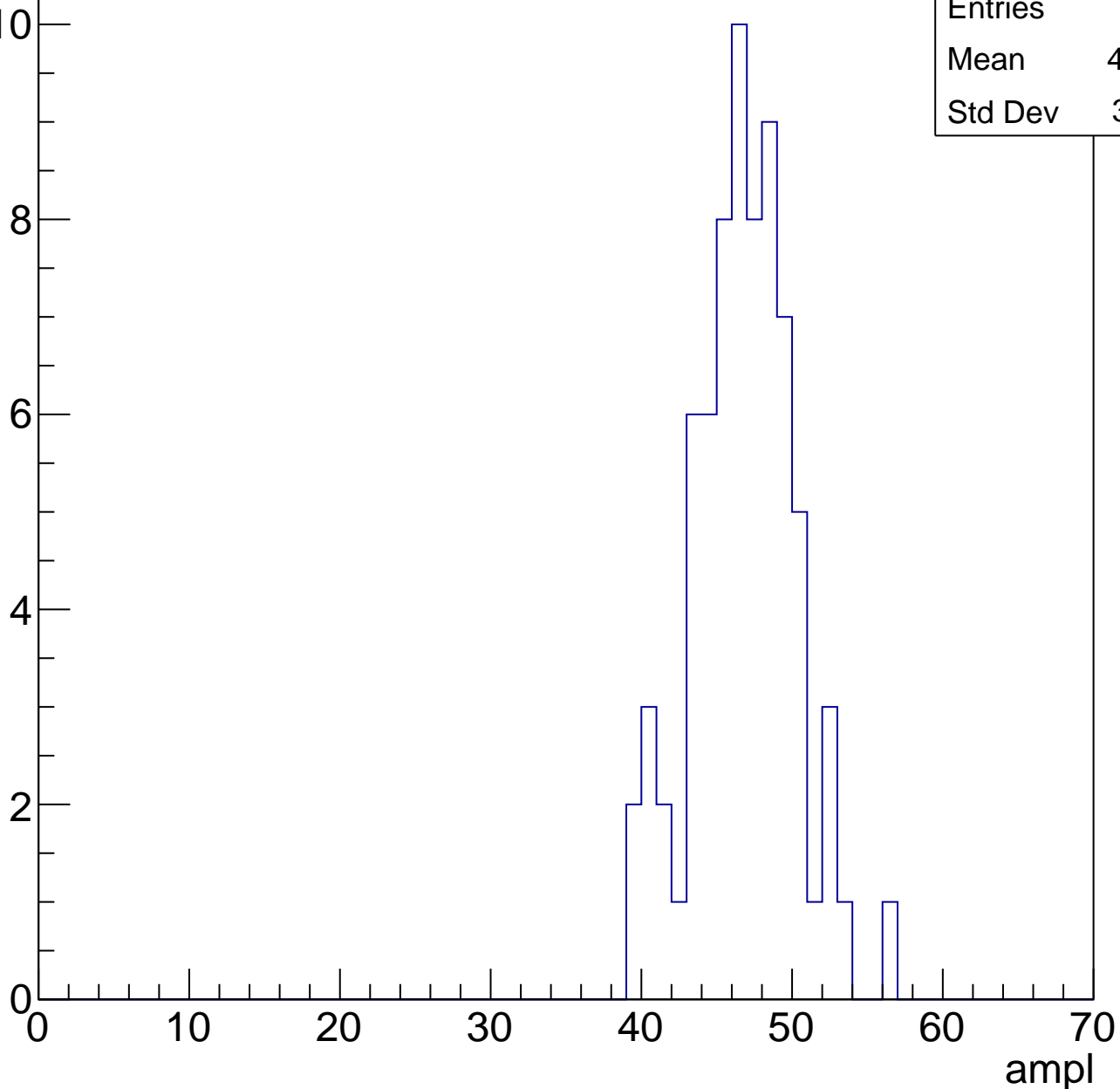


# B1L000S, U1-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

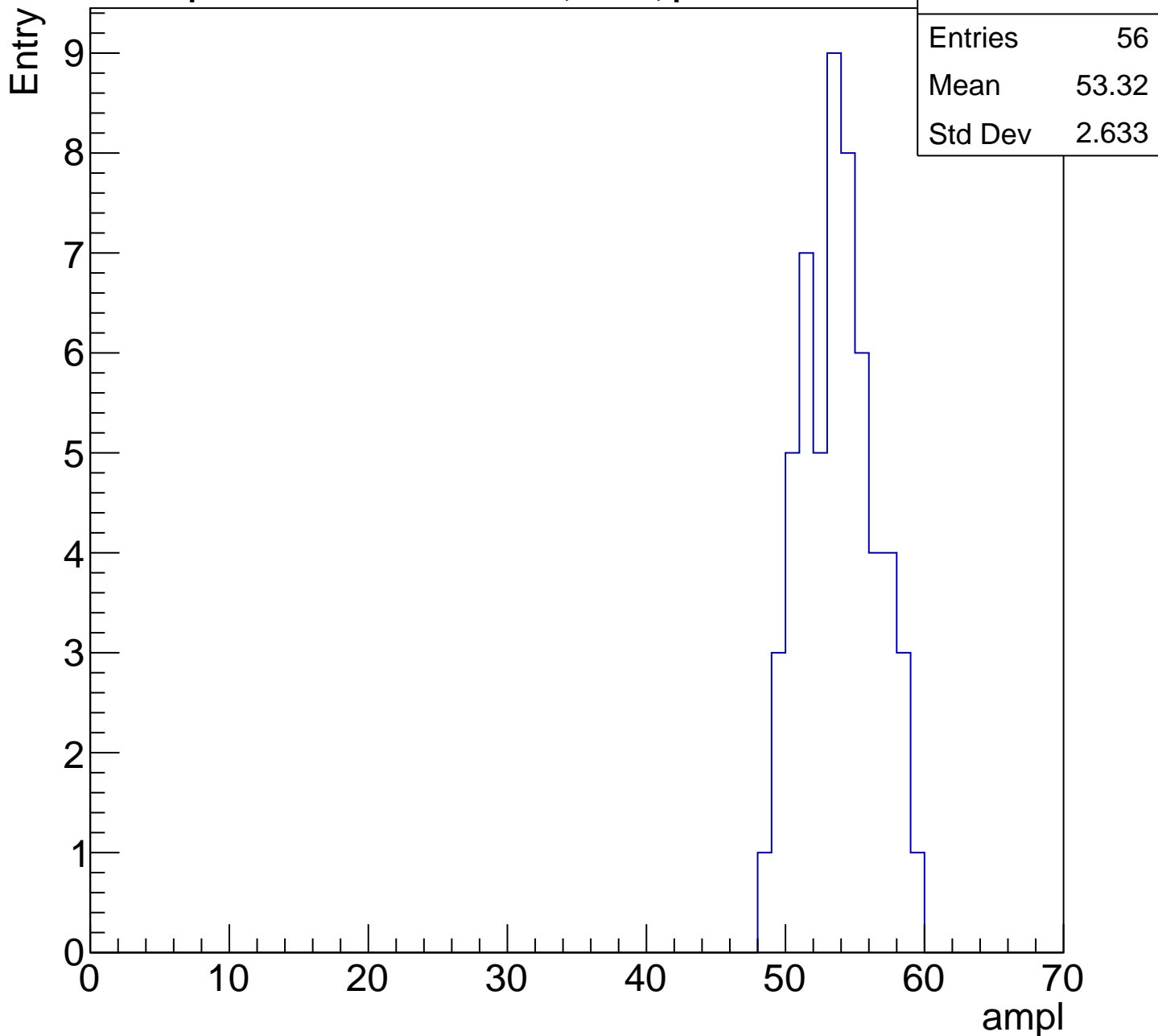
Entry

Entries	73
Mean	46.32
Std Dev	3.351



# B1L000S, U1-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch50, adc5

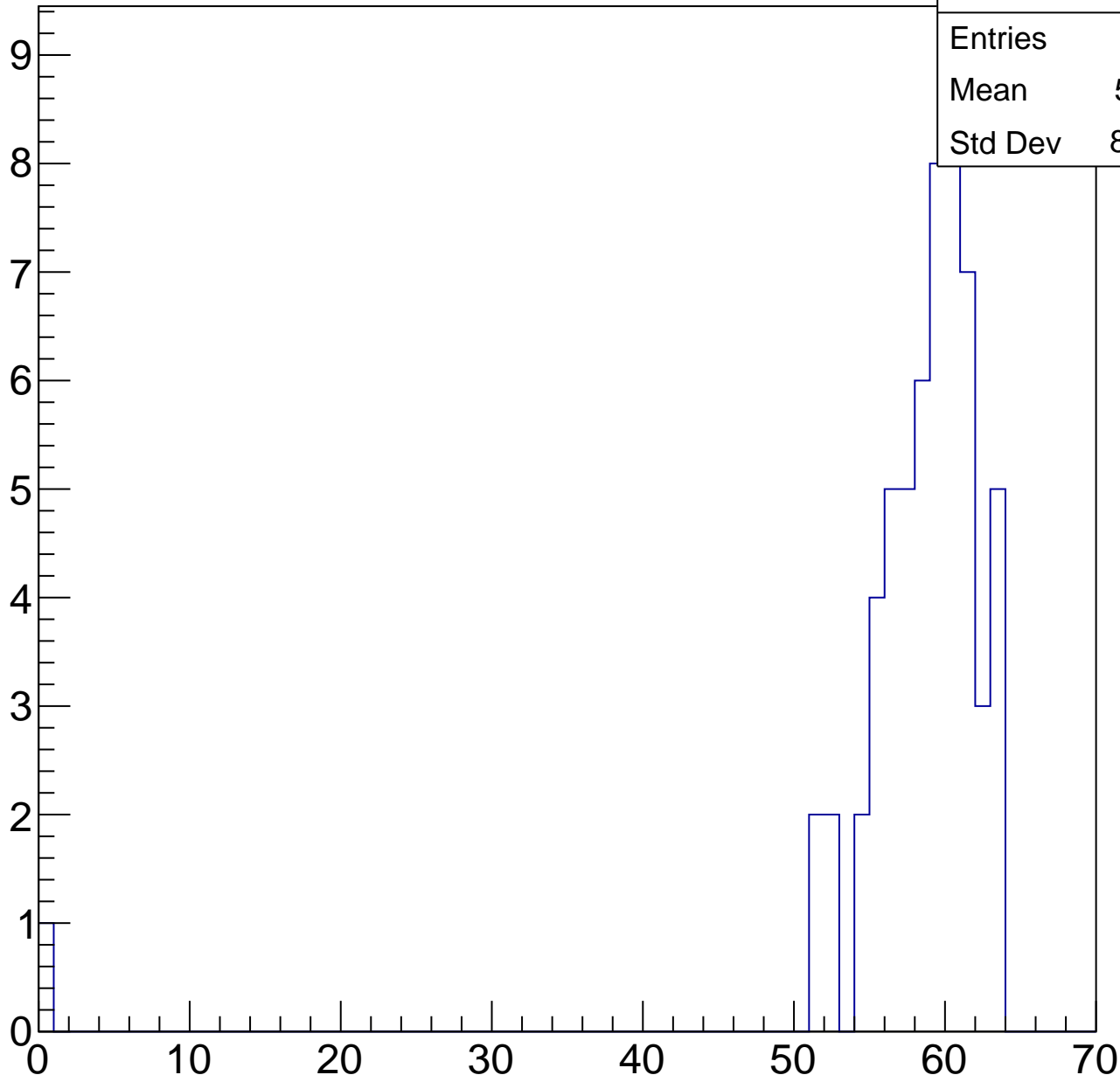
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	57.41
Std Dev	8.116

ampl

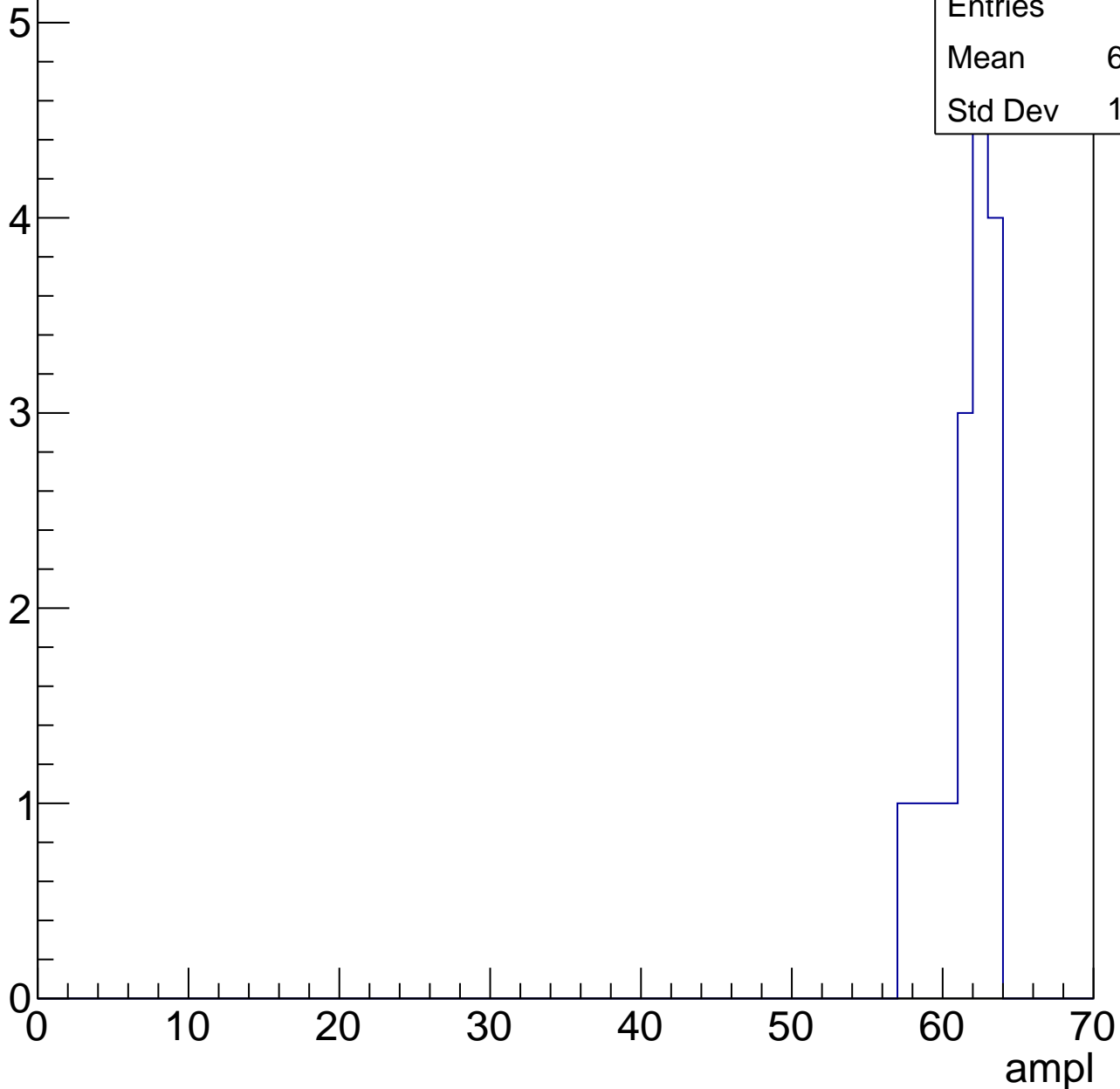


# B1L000S, U1-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.19
Std Dev	1.775

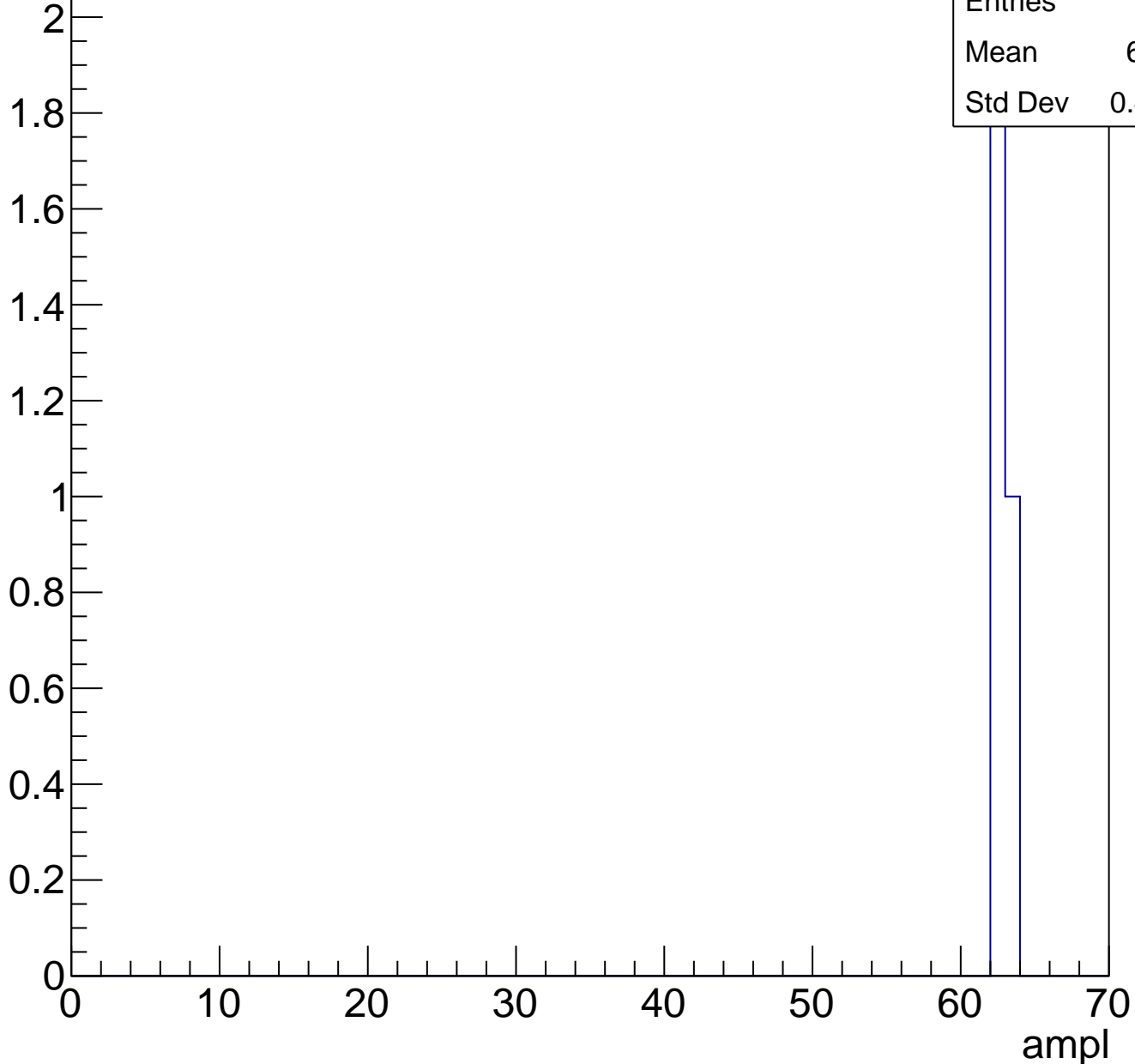




# B1L000S, U1-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch51, adc0

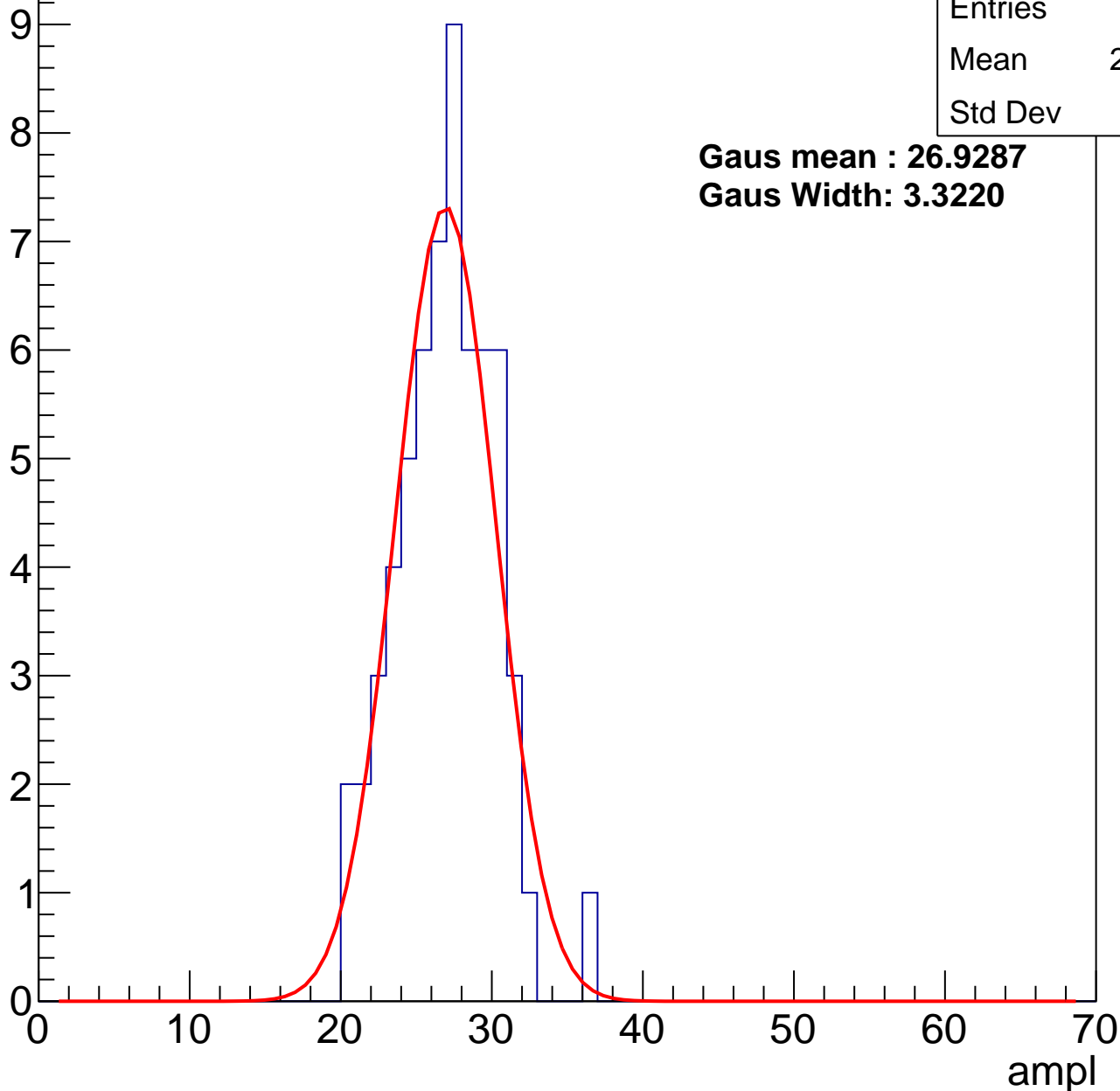
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	26.52
Std Dev	3.16

**Gaus mean : 26.9287**

**Gaus Width: 3.3220**



# B1L000S, U1-ch51, adc1

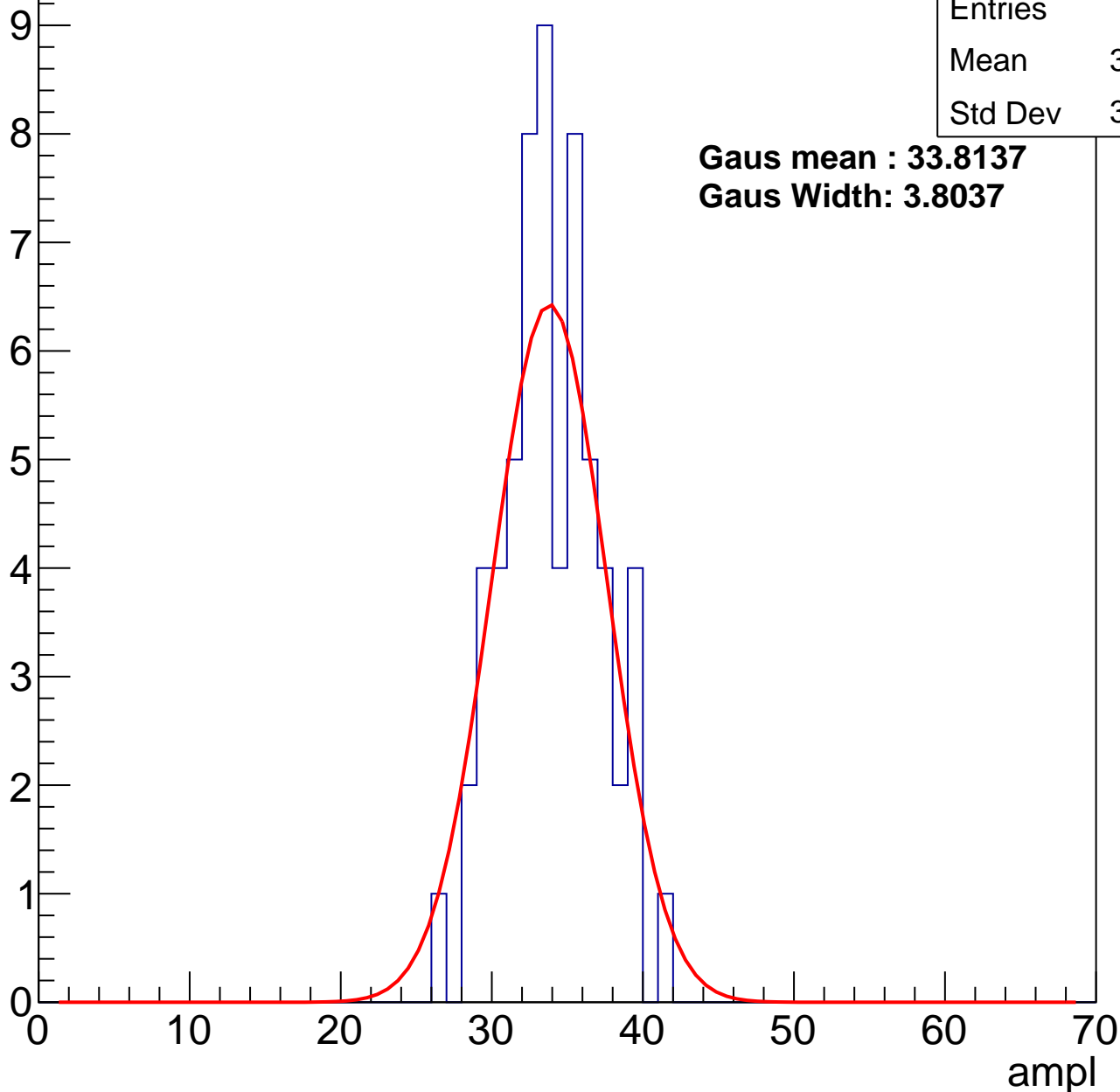
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	33.49
Std Dev	3.176

**Gaus mean : 33.8137**

**Gaus Width: 3.8037**



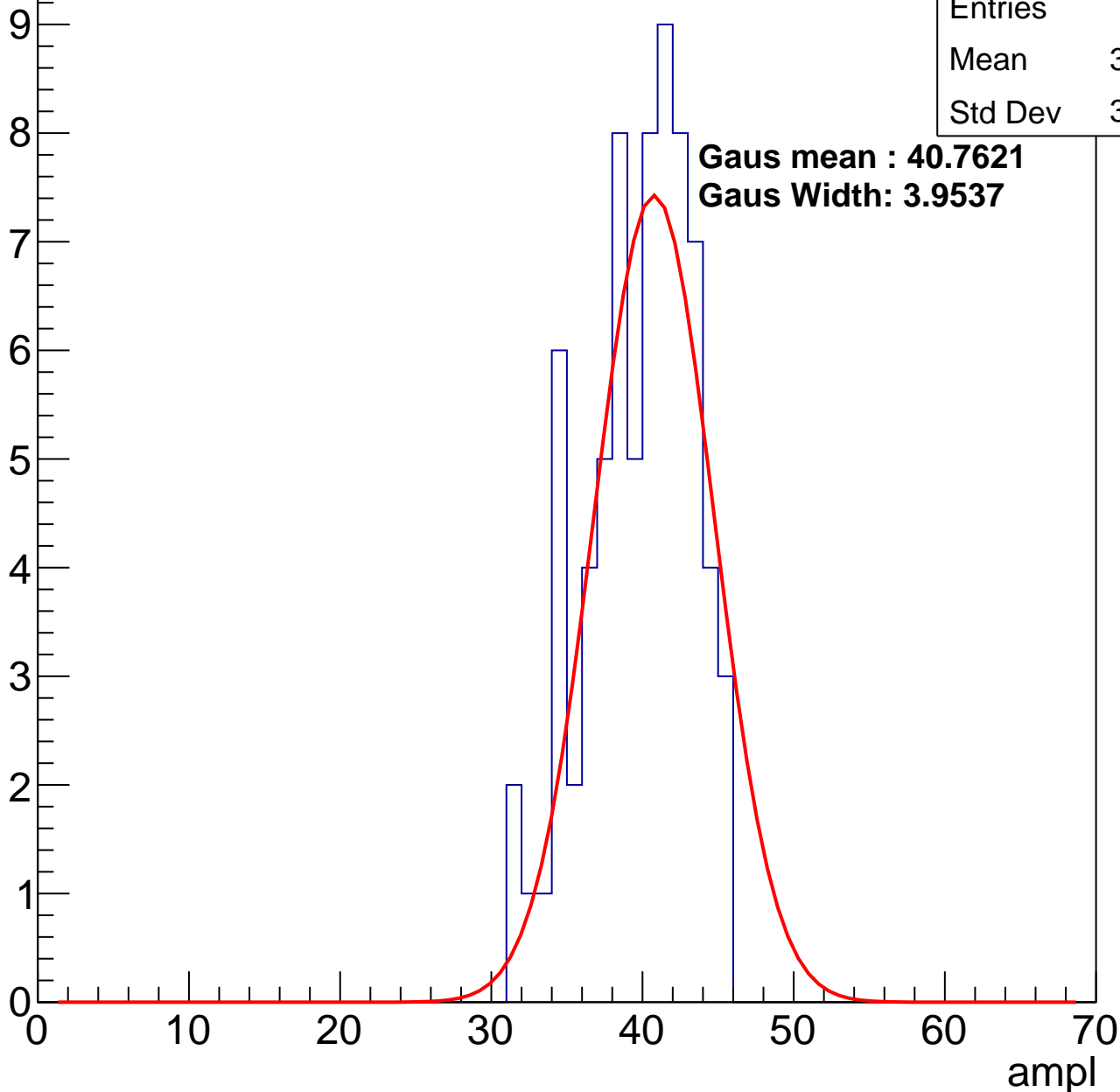
# B1L000S, U1-ch51, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	39.26
Std Dev	3.492

**Gaus mean : 40.7621**  
**Gaus Width: 3.9537**

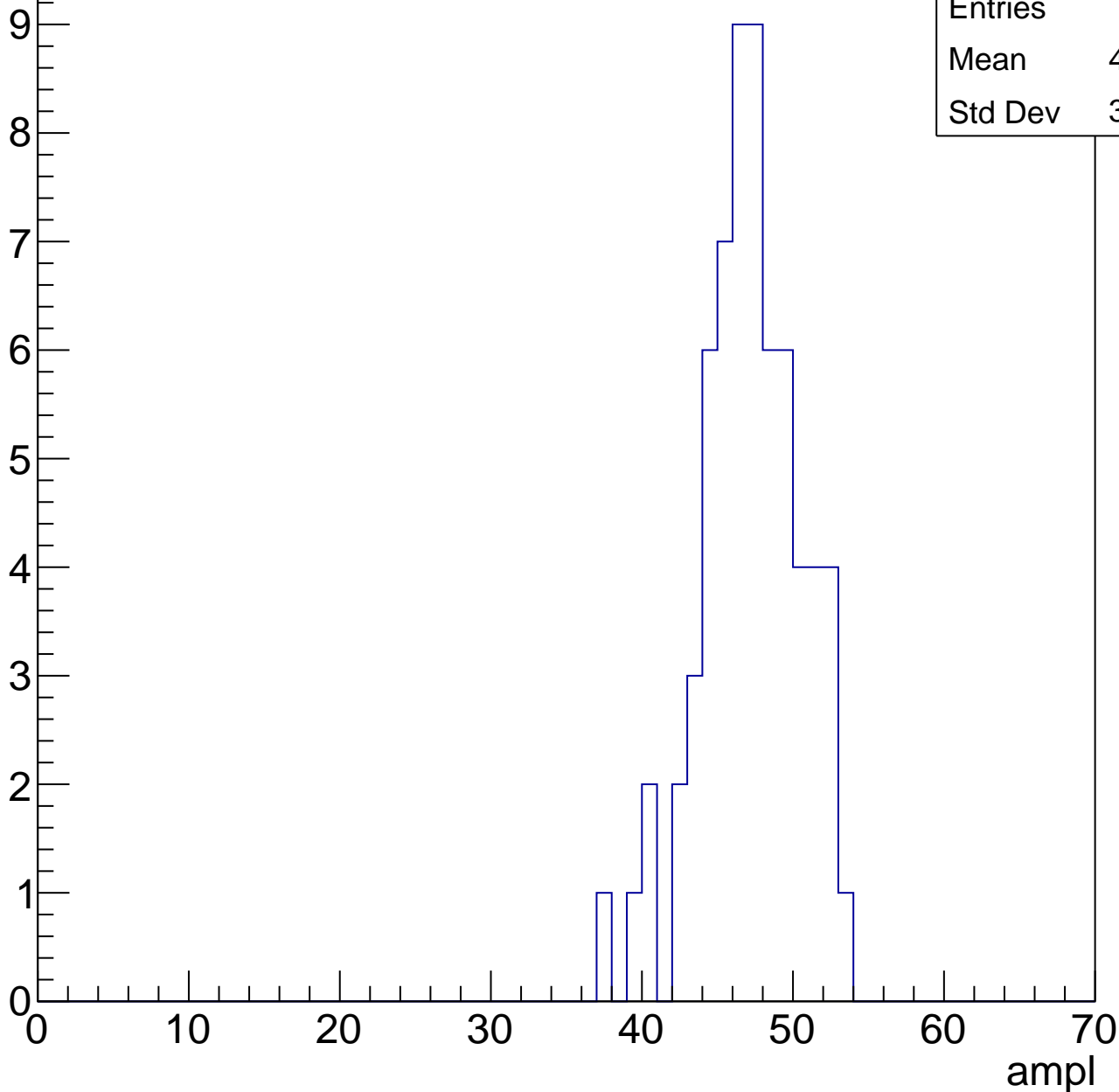


# B1L000S, U1-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	46.65
Std Dev	3.302

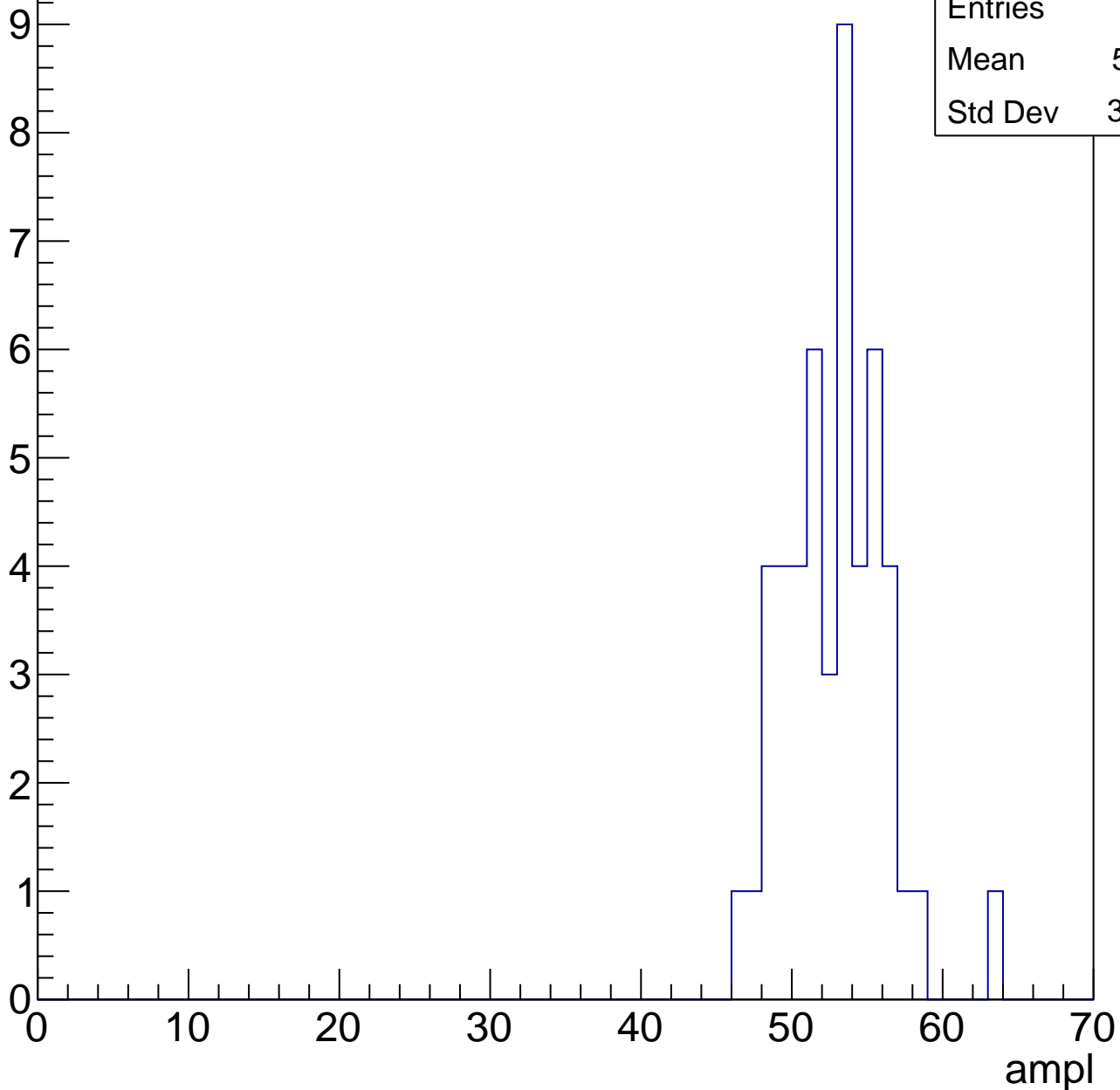


# B1L000S, U1-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

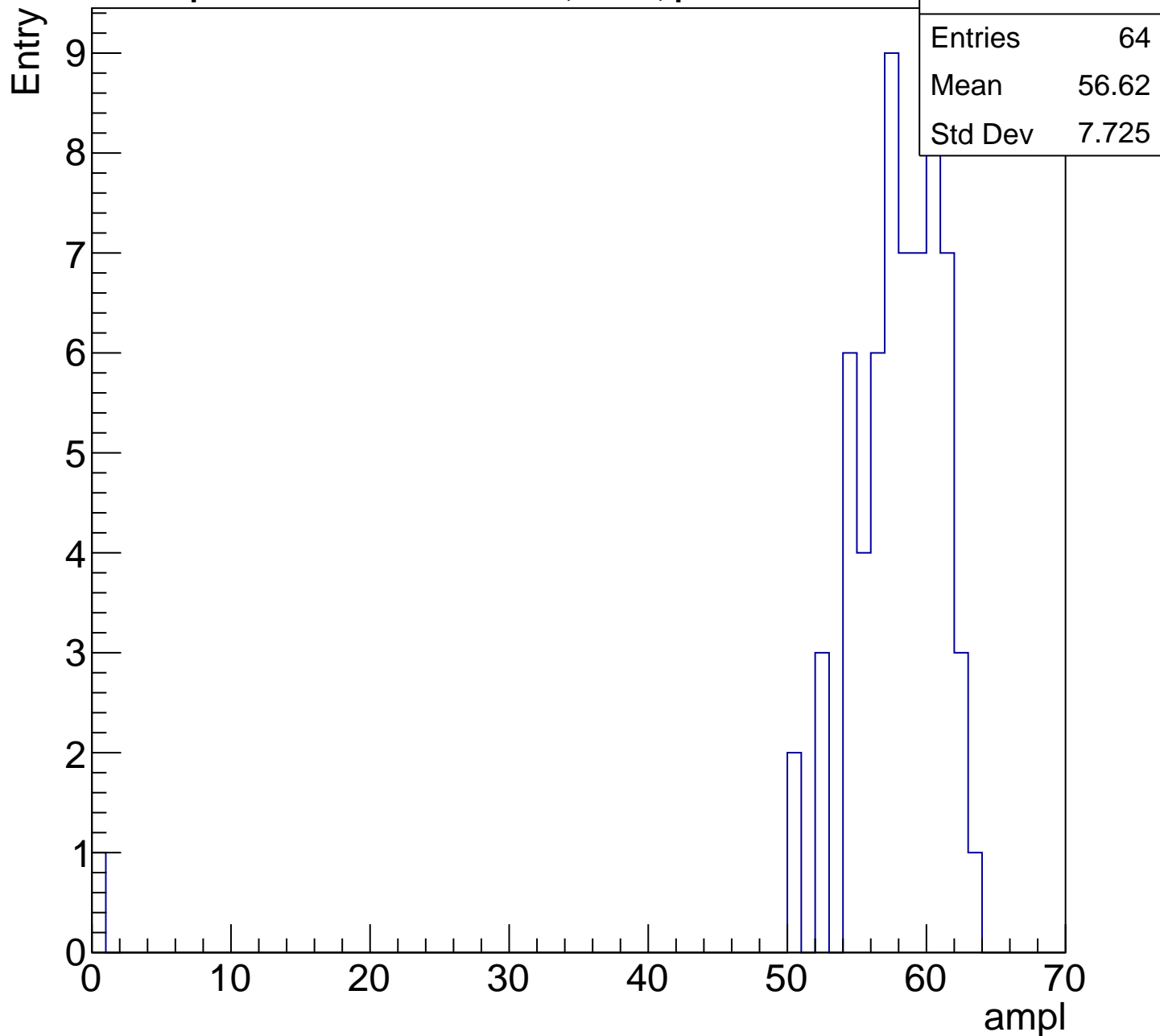
Entry

Entries	49
Mean	52.41
Std Dev	3.194



# B1L000S, U1-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

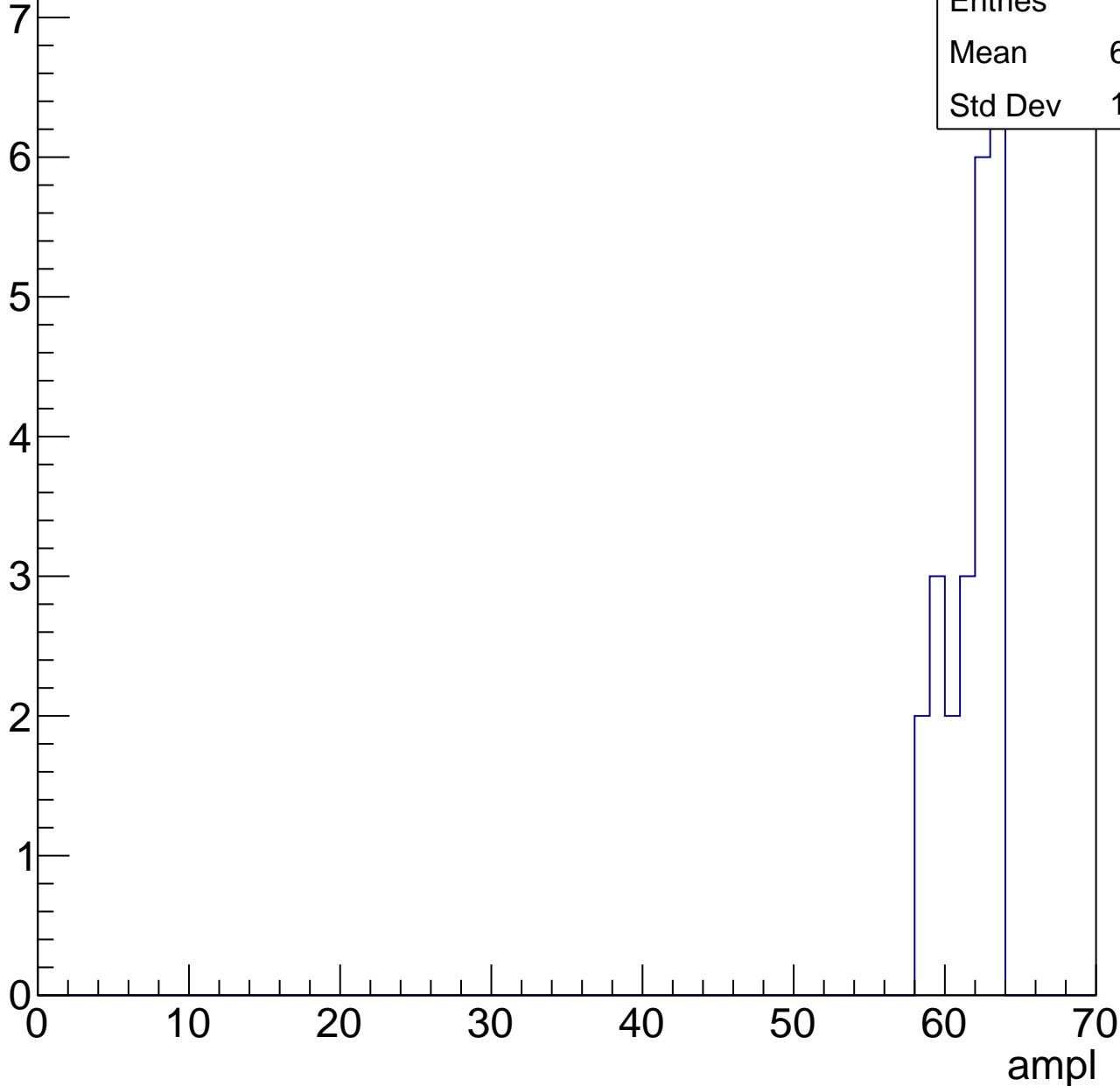


# B1L000S, U1-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	61.26
Std Dev	1.674





# B1L000S, U1-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch52, adc0

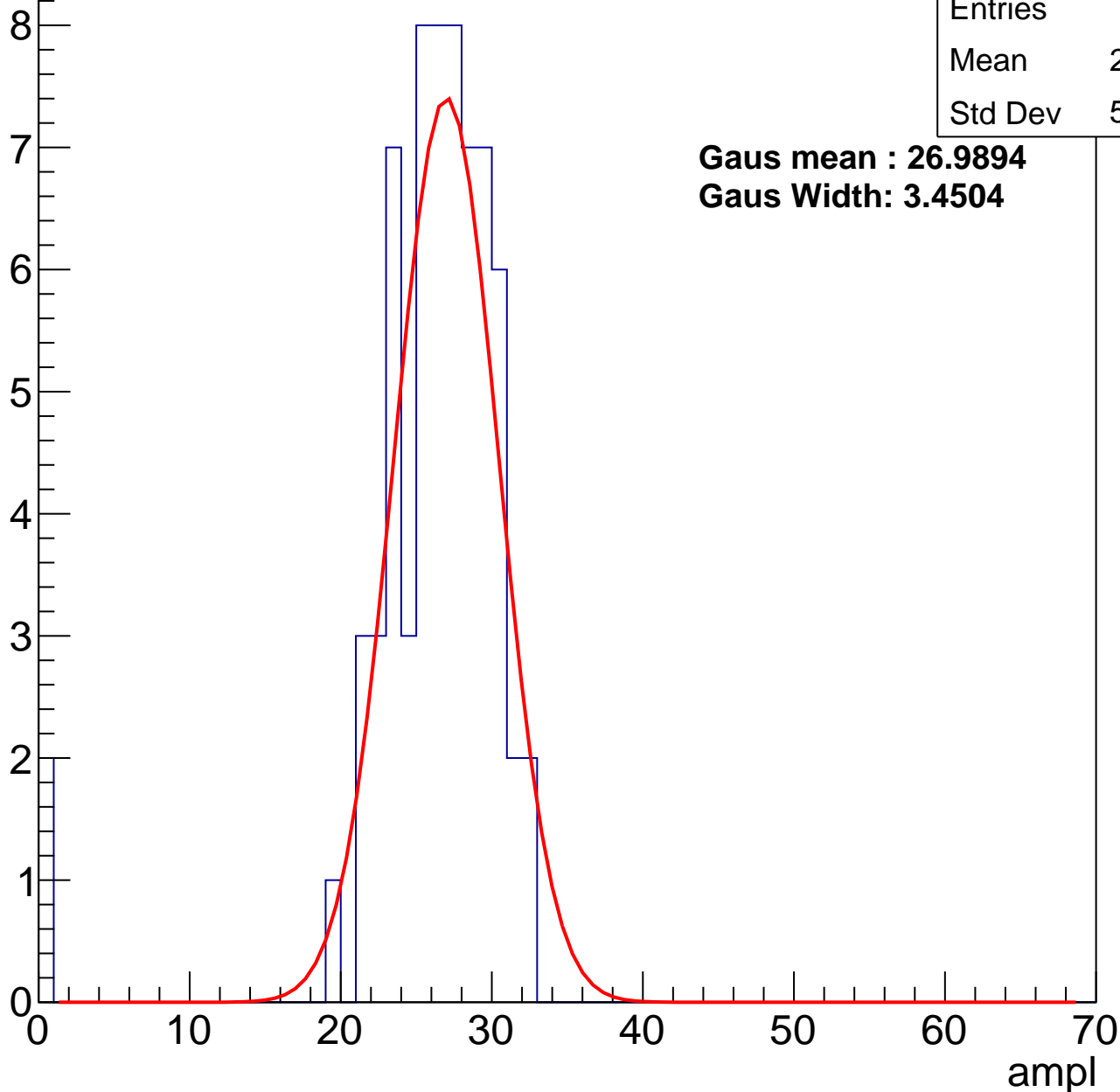
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	25.52
Std Dev	5.337

**Gaus mean : 26.9894**

**Gaus Width: 3.4504**



# B1L000S, U1-ch52, adc1

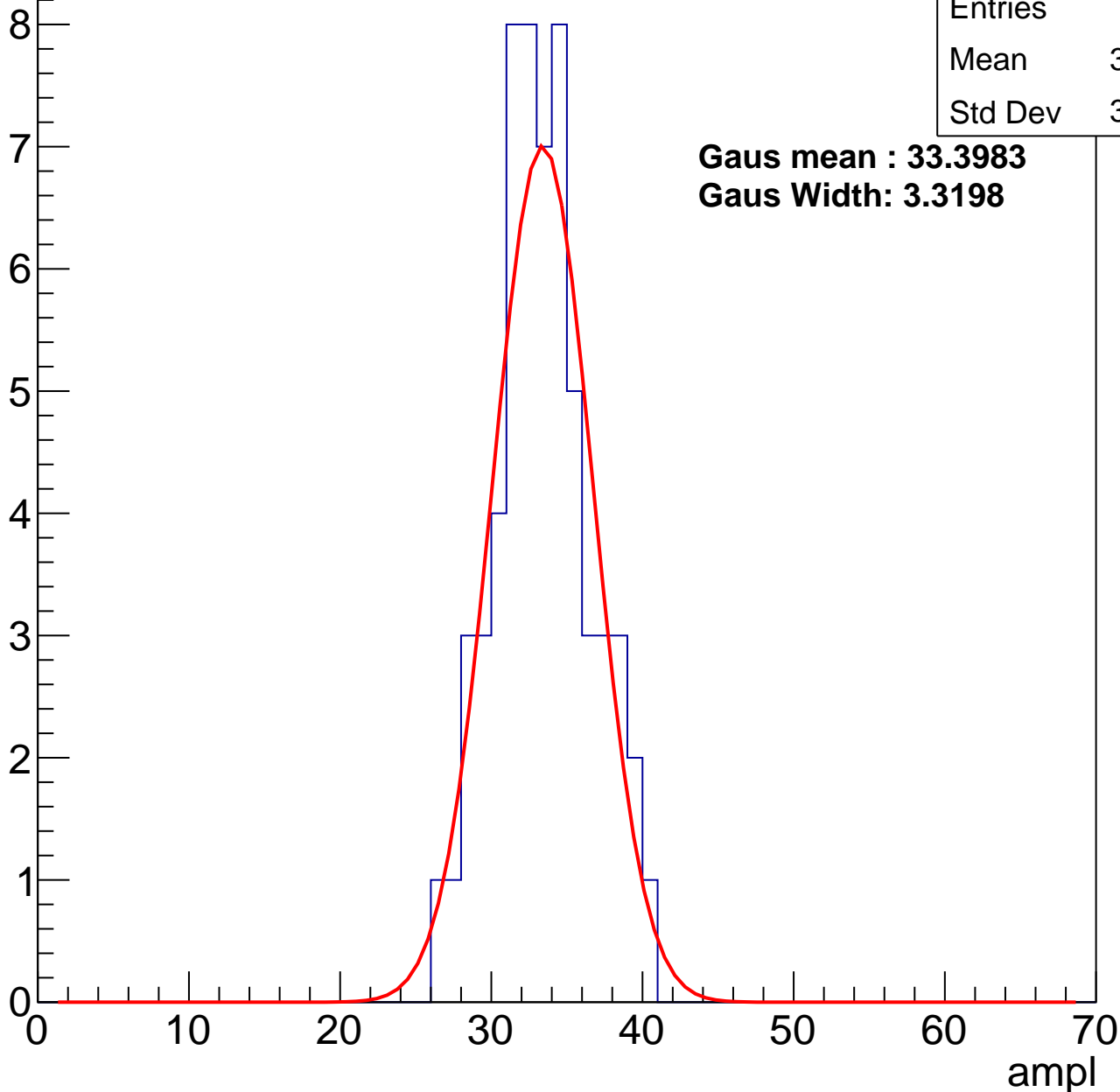
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	32.95
Std Dev	3.117

**Gaus mean : 33.3983**

**Gaus Width: 3.3198**



# B1L000S, U1-ch52, adc2

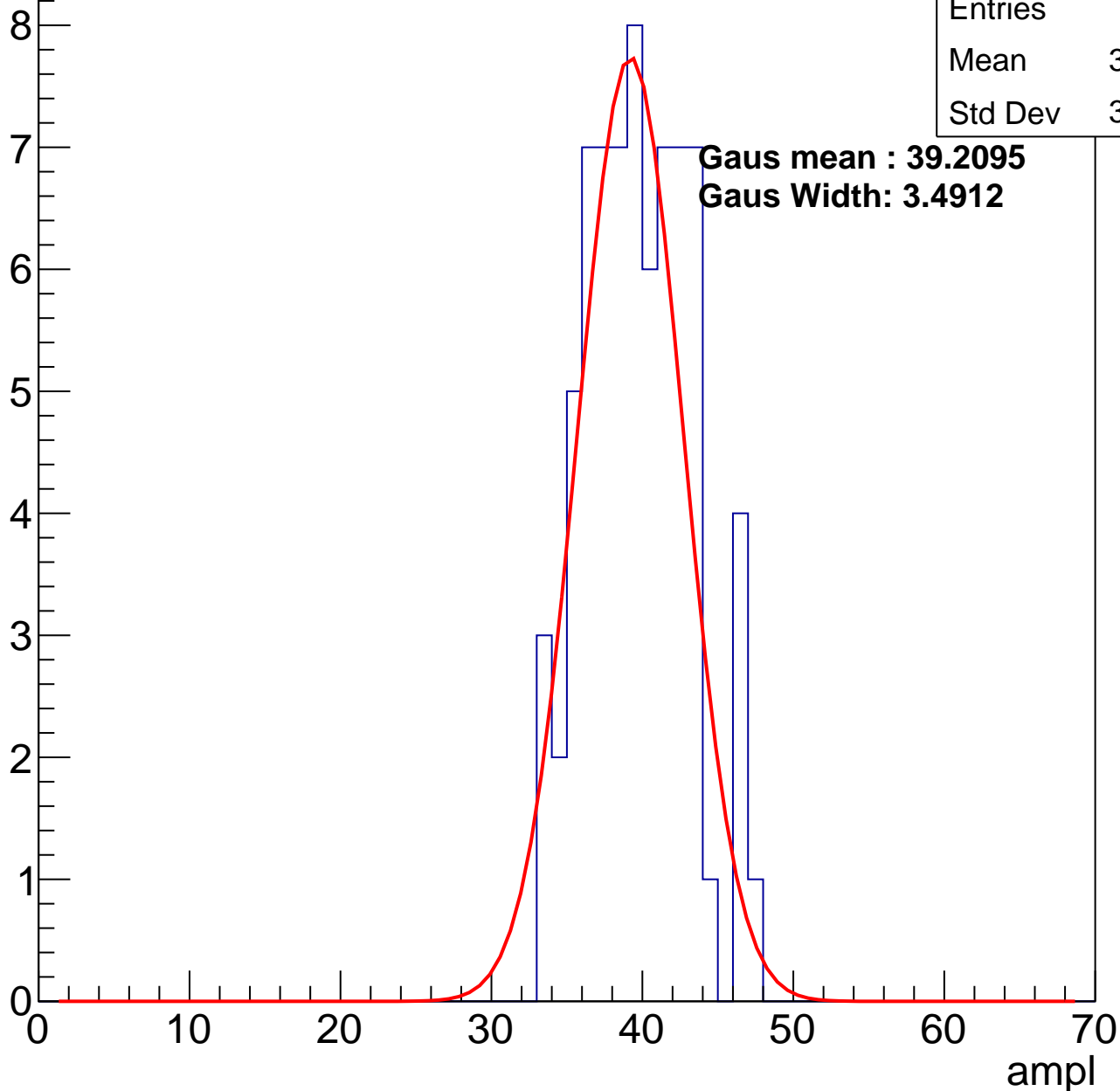
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	39.28
Std Dev	3.384

**Gaus mean : 39.2095**

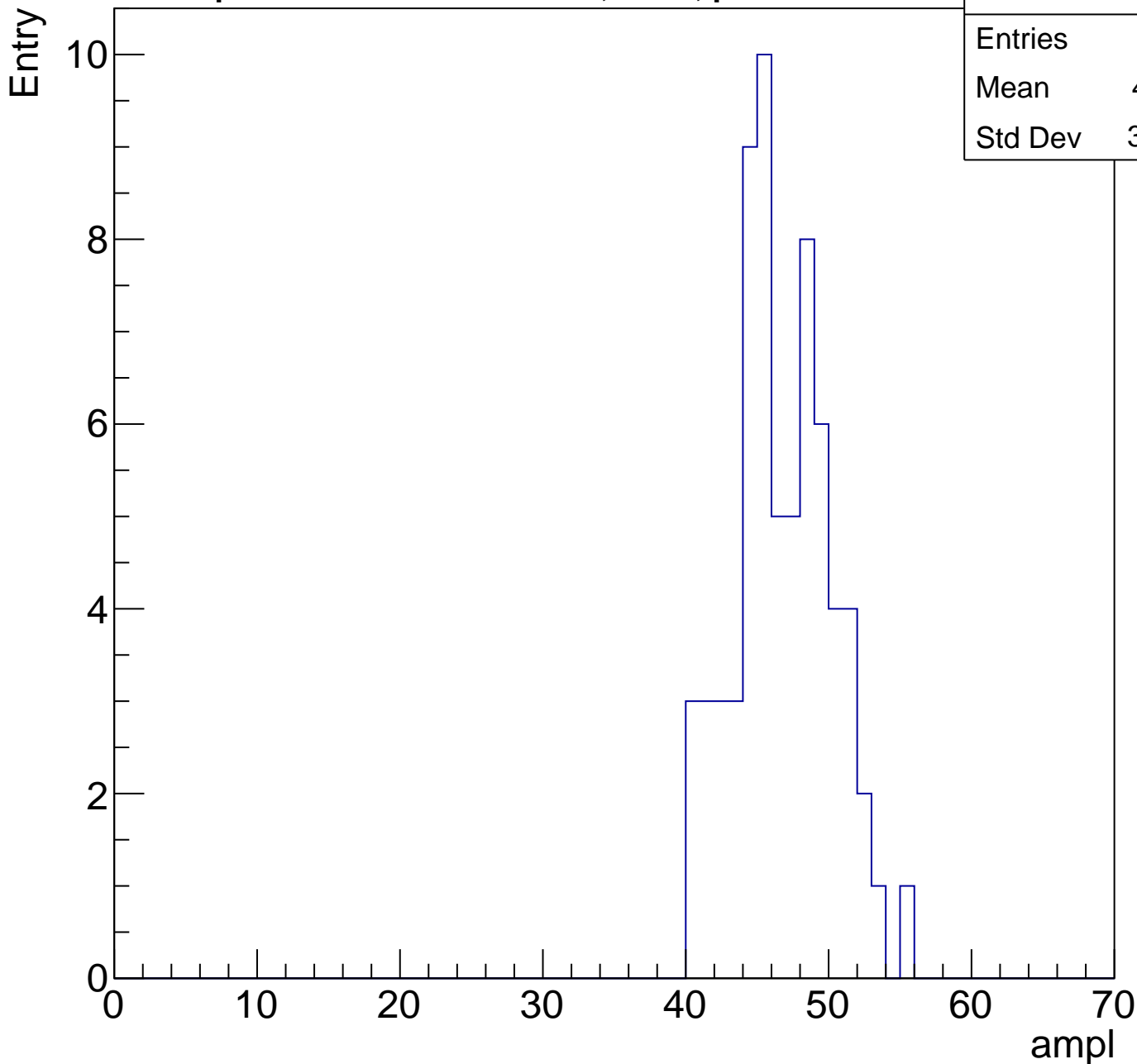
**Gaus Width: 3.4912**



# B1L000S, U1-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	46.31
Std Dev	3.356

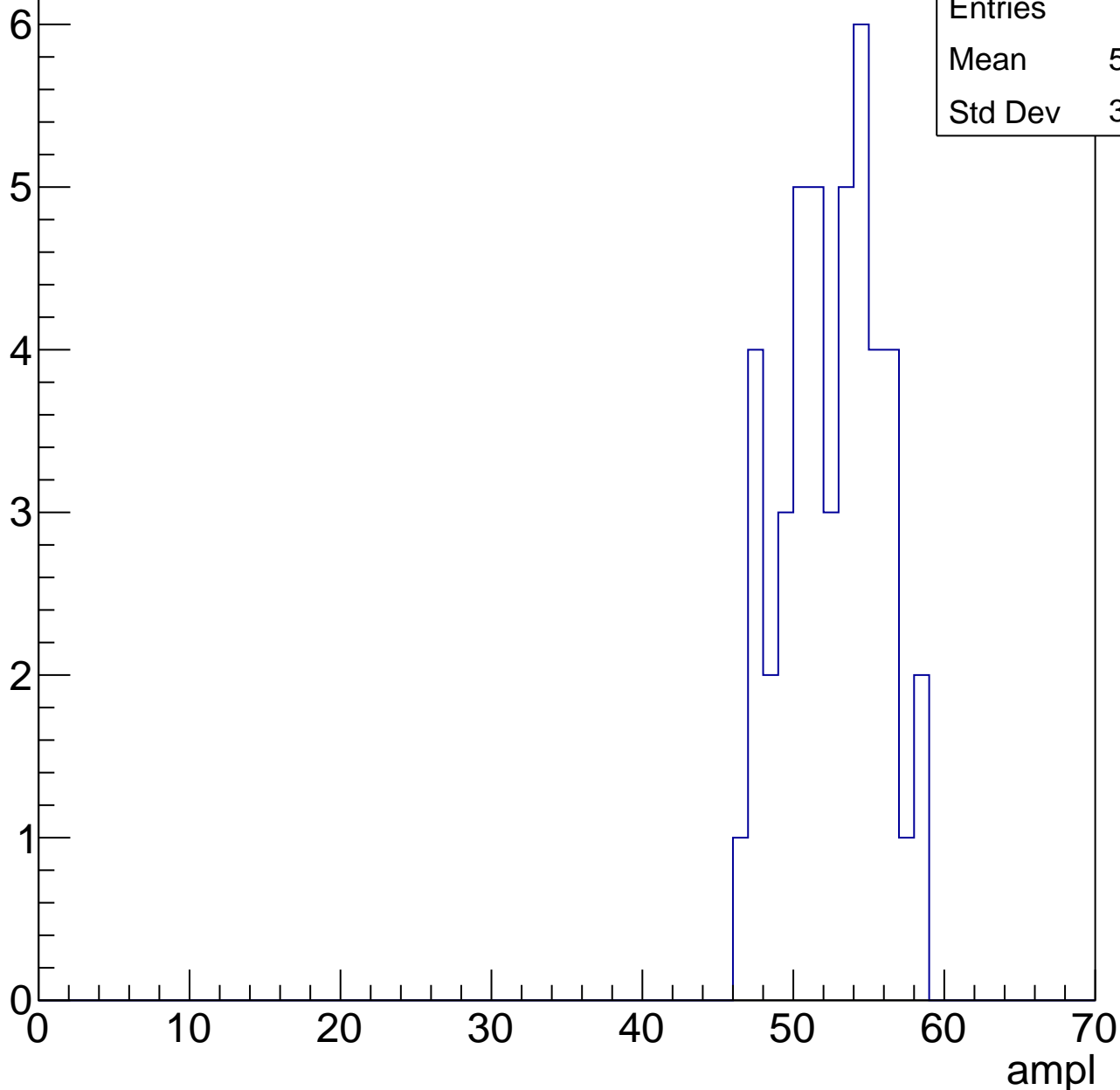


# B1L000S, U1-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

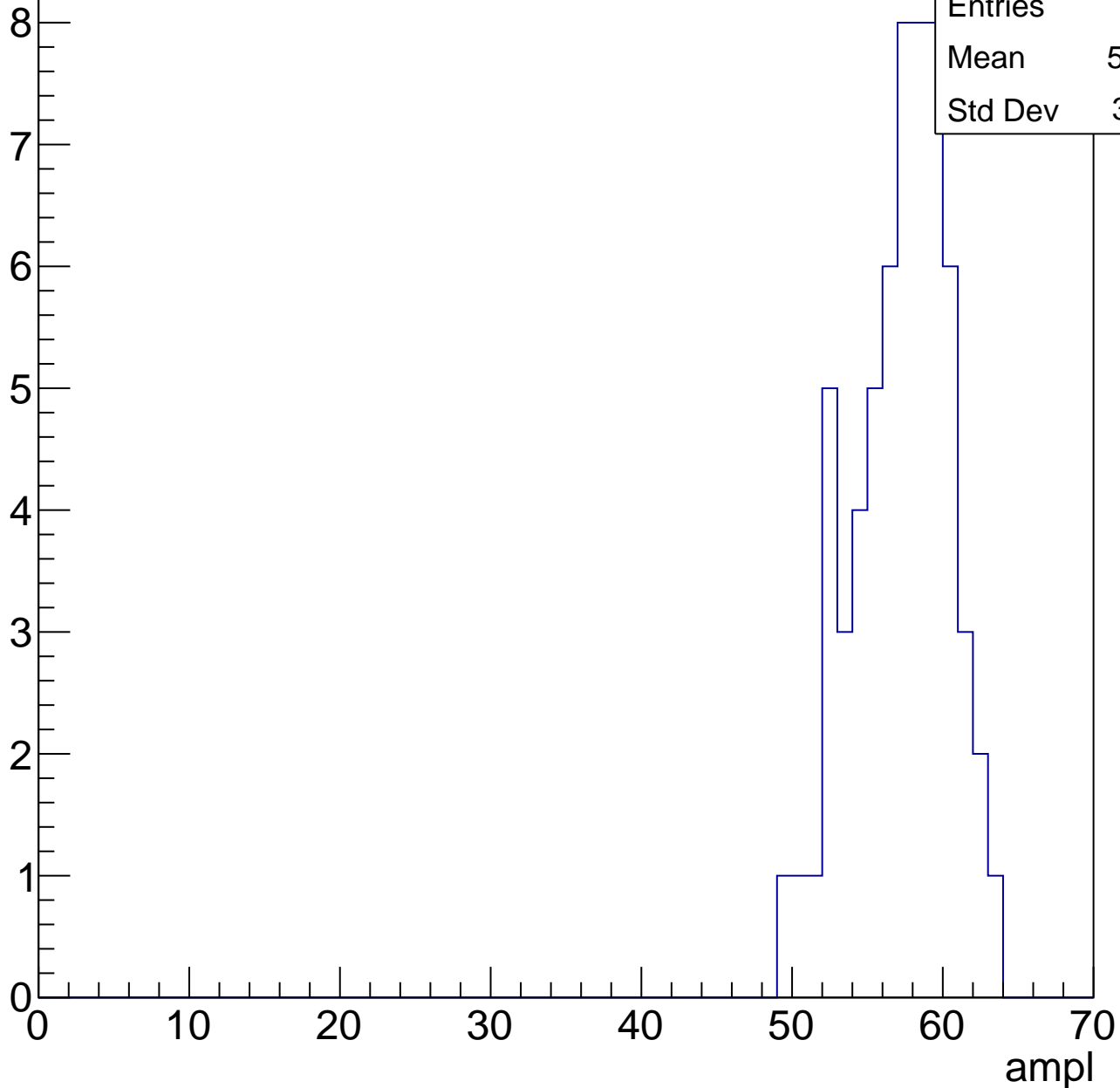
Entries	45
Mean	52.09
Std Dev	3.147



# B1L000S, U1-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

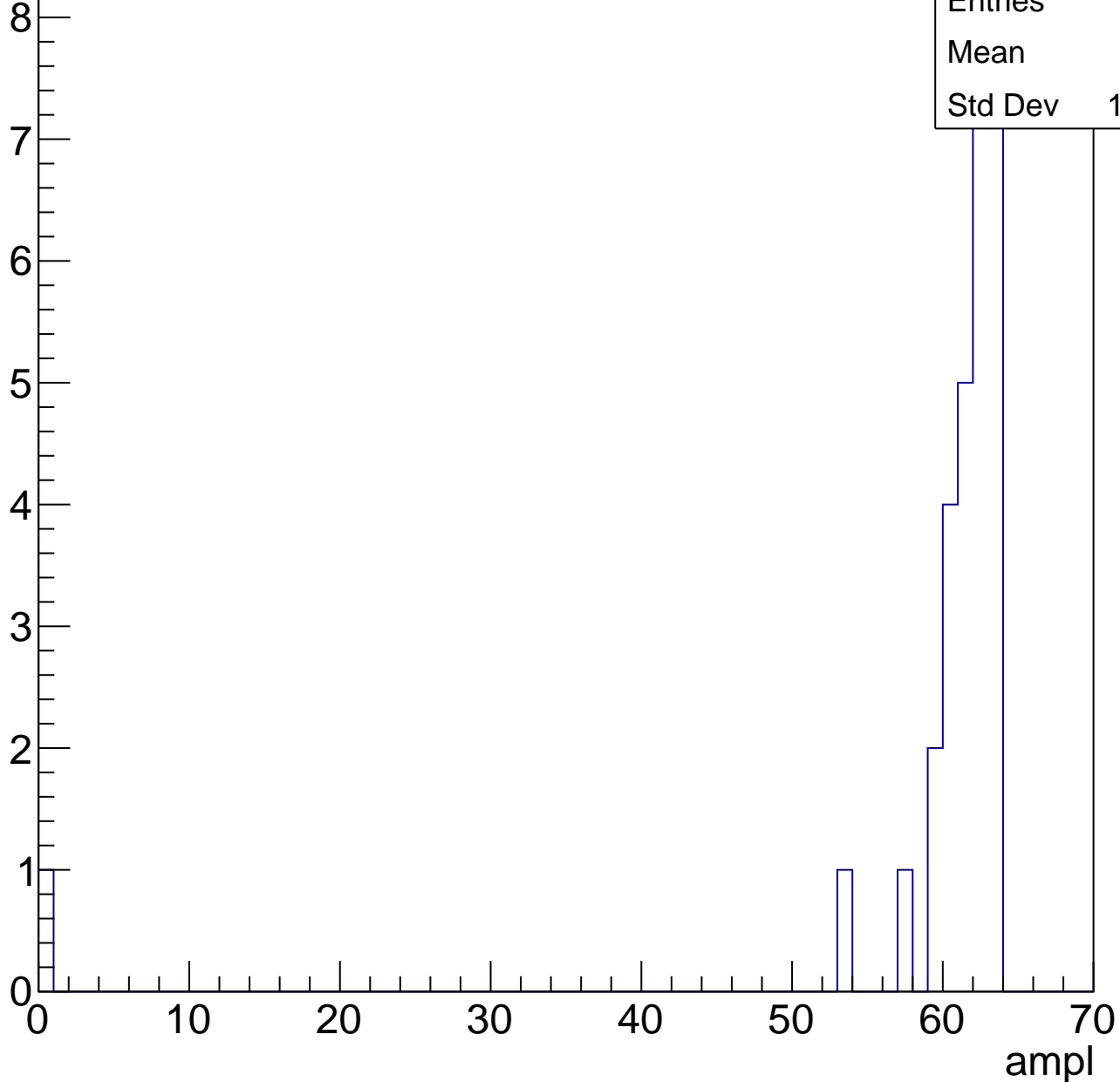


# B1L000S, U1-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	59.1
Std Dev	11.17

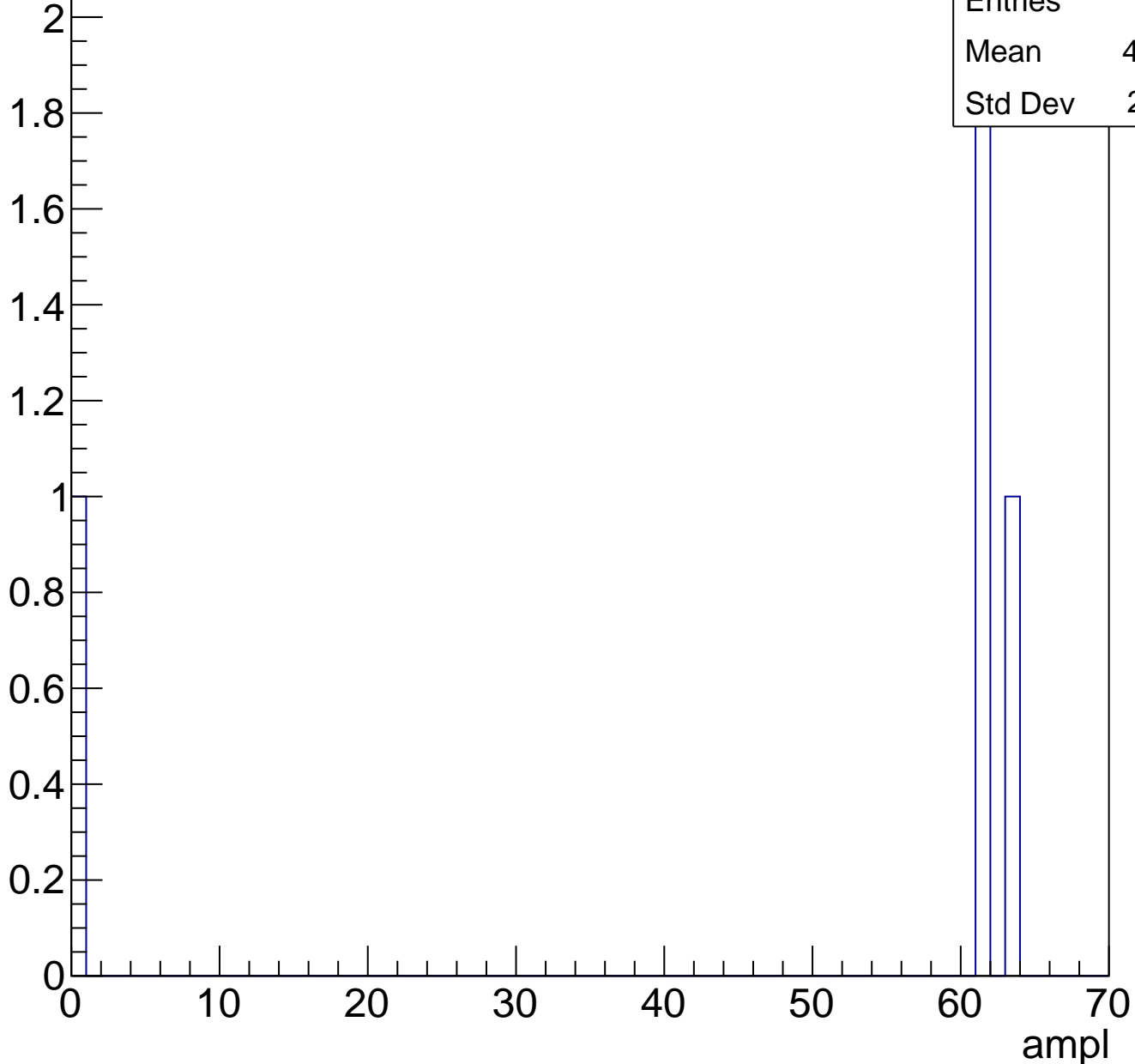




# B1L000S, U1-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	46.25
Std Dev	26.71

# B1L000S, U1-ch53, adc0

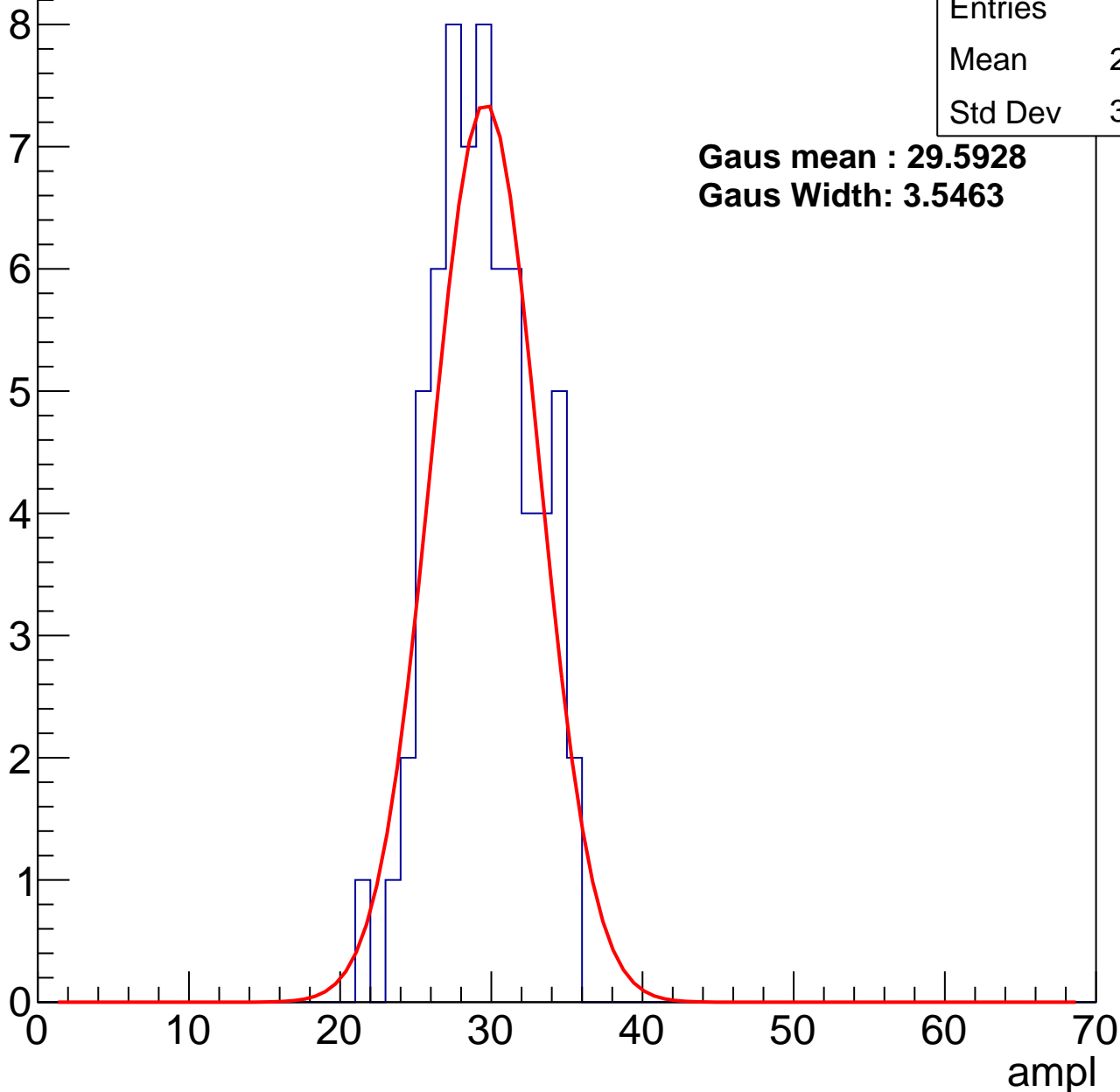
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.97
Std Dev	3.162

**Gaus mean : 29.5928**

**Gaus Width: 3.5463**



# B1L000S, U1-ch53, adc1

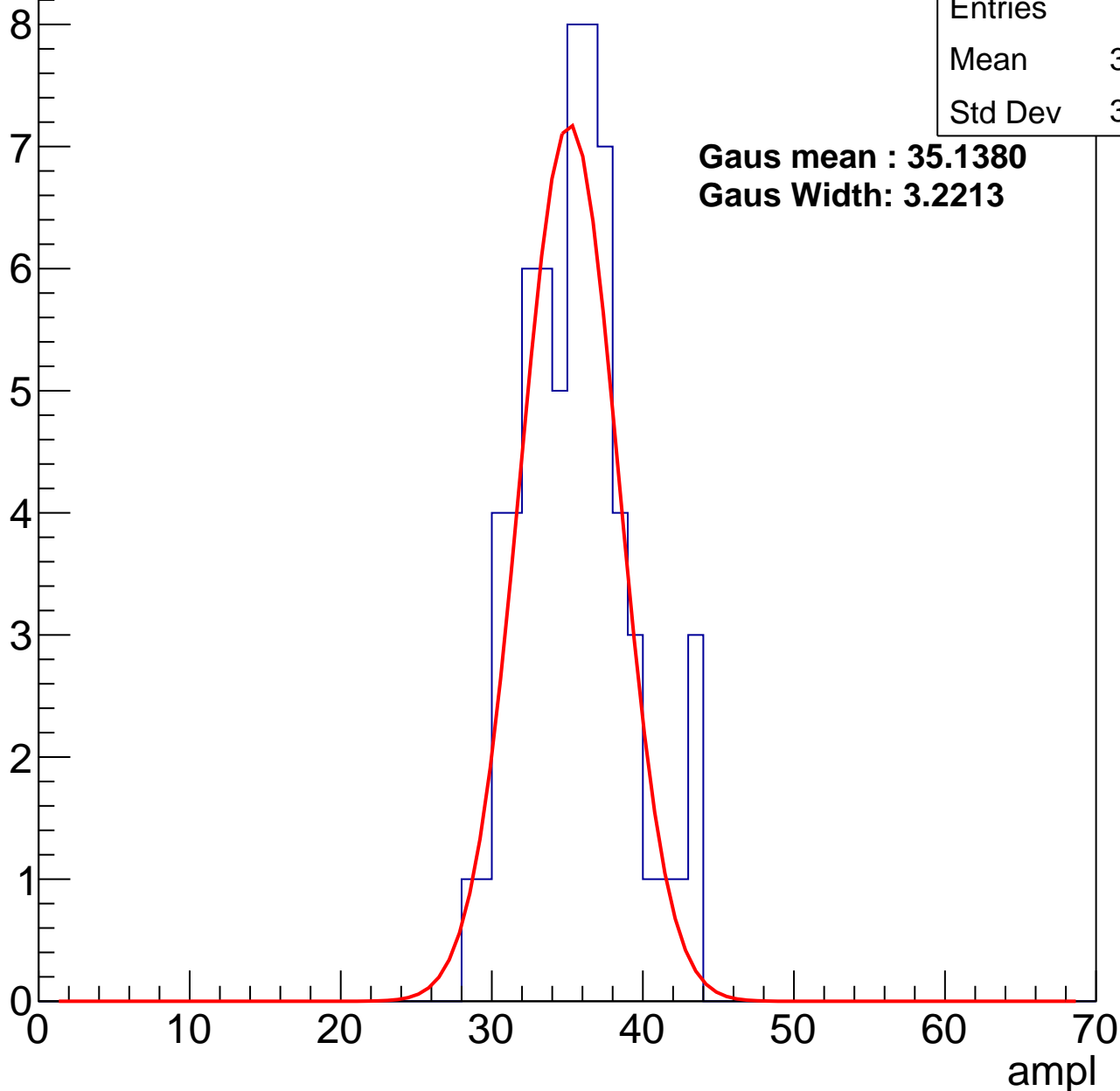
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.06
Std Dev	3.459

**Gaus mean : 35.1380**

**Gaus Width: 3.2213**



# B1L000S, U1-ch53, adc2

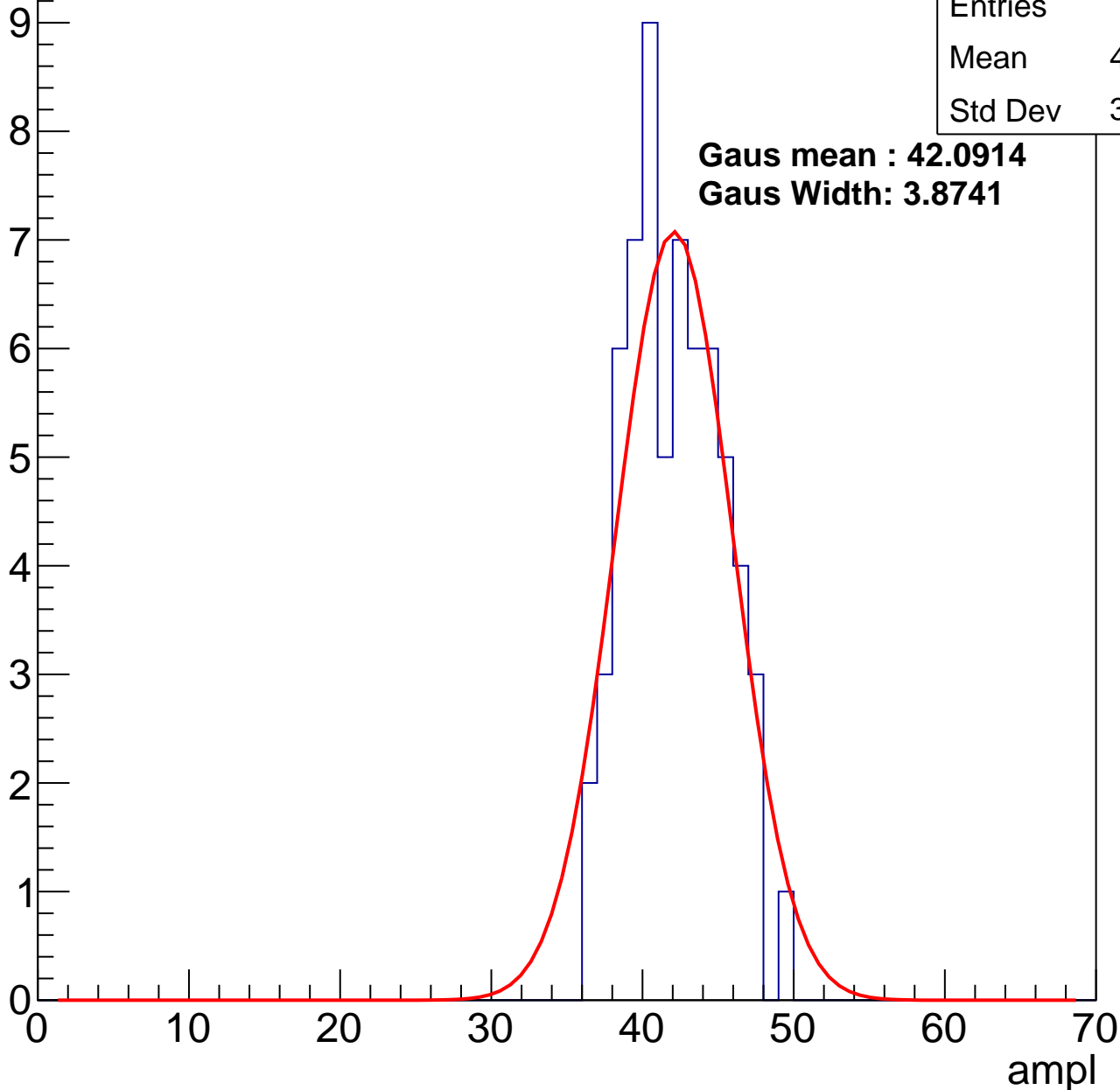
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.62
Std Dev	3.064

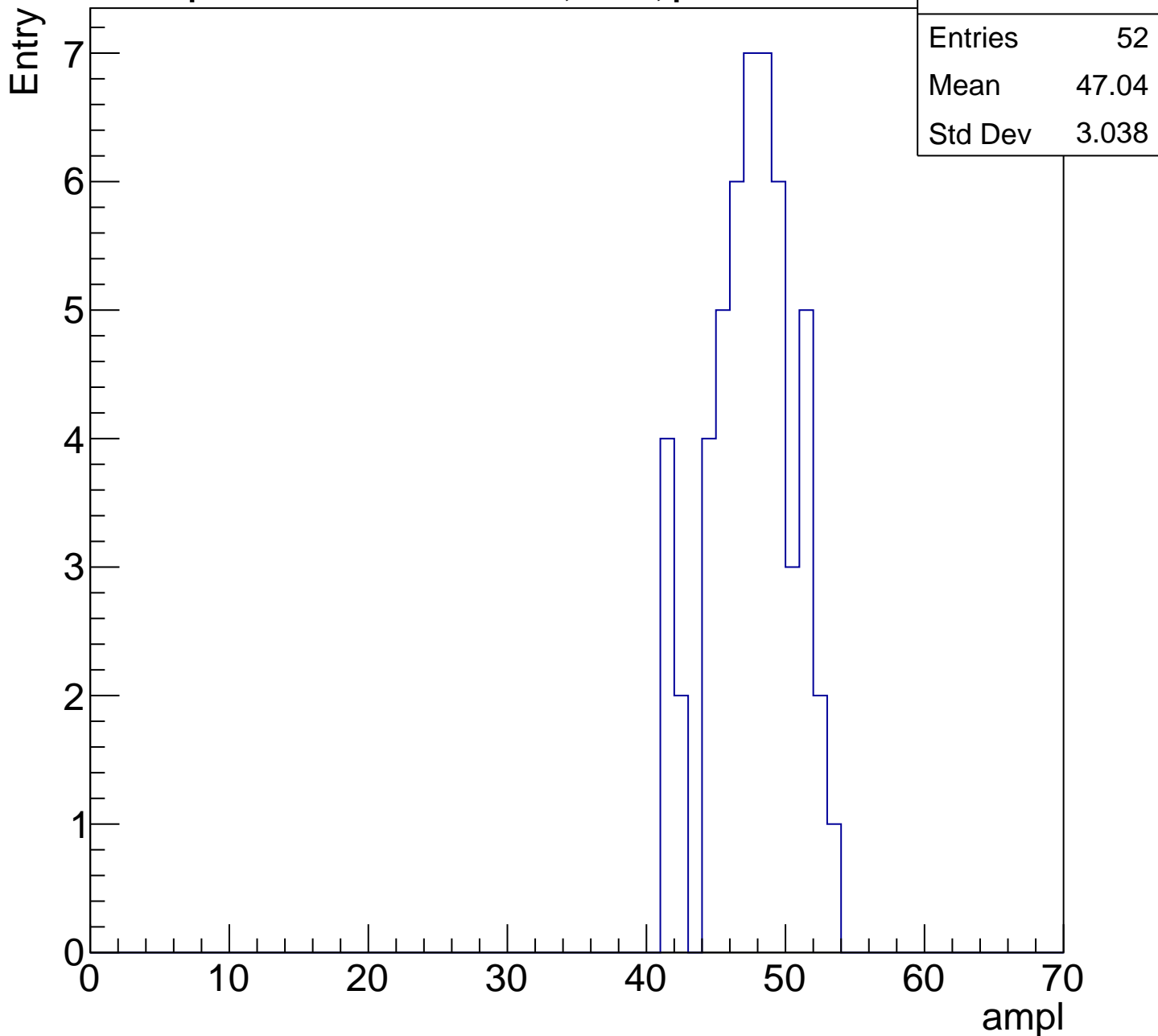
**Gaus mean : 42.0914**

**Gaus Width: 3.8741**



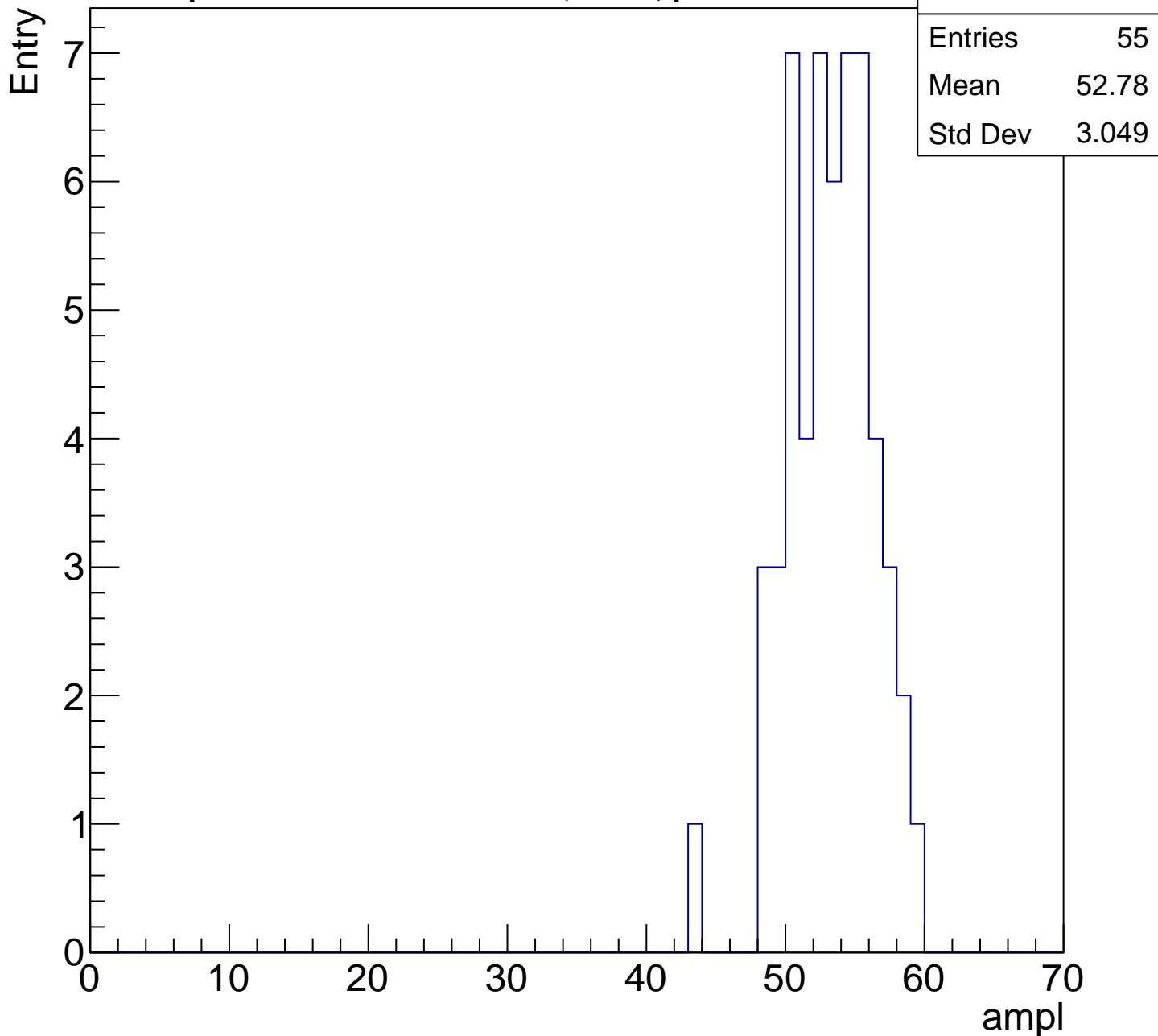
# B1L000S, U1-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

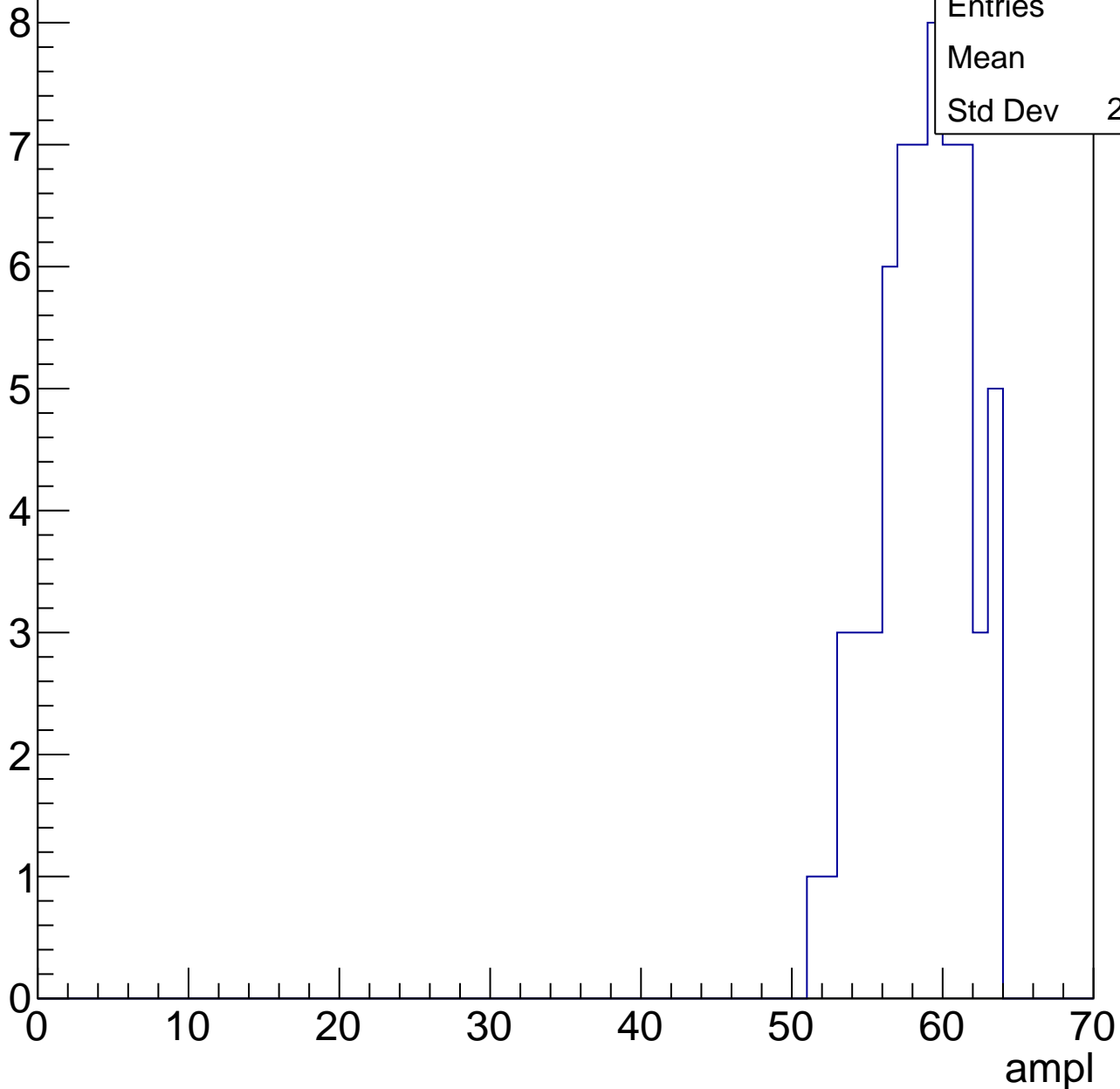


# B1L000S, U1-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

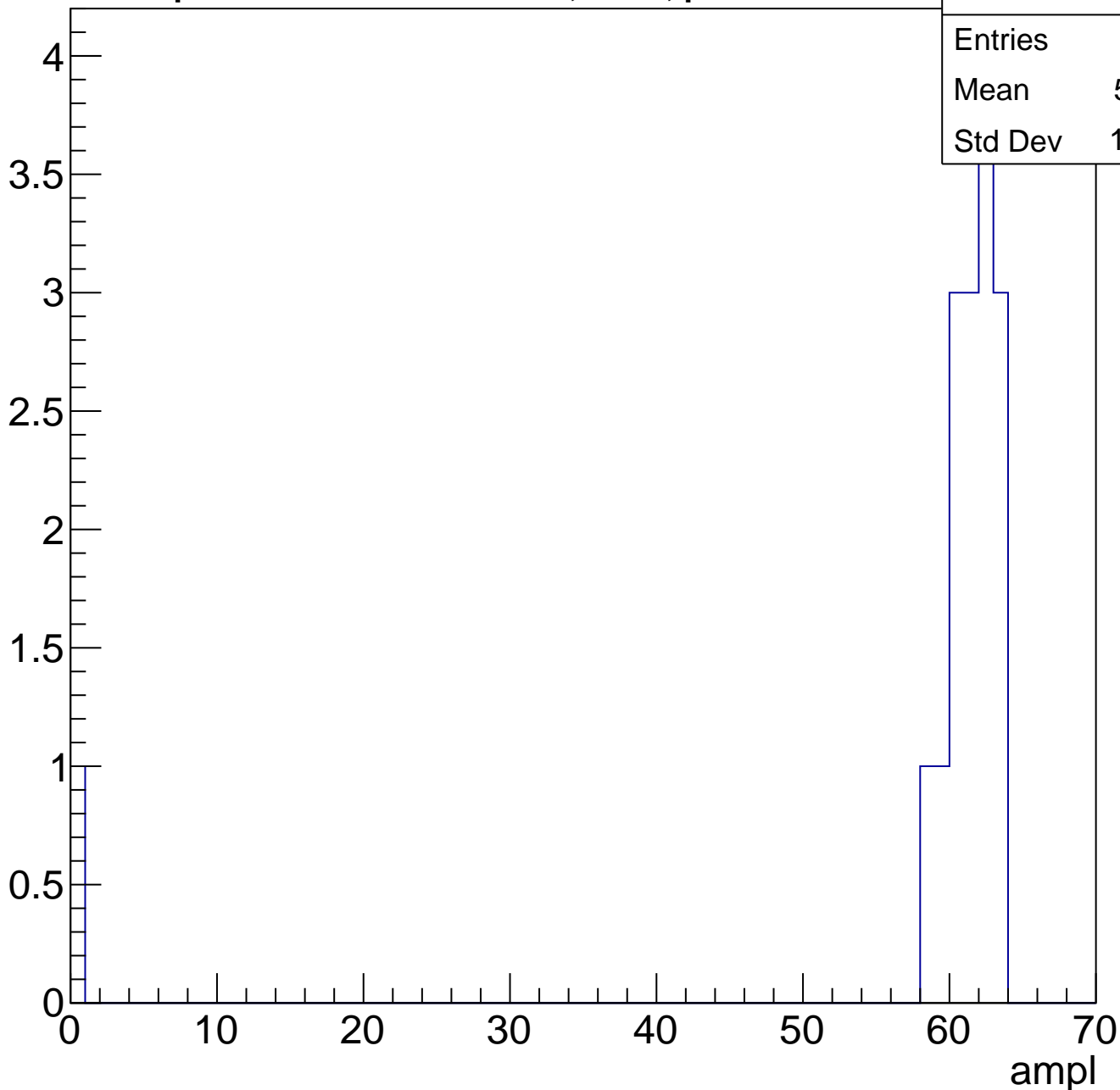
Entries	61
Mean	58.2
Std Dev	2.963



# B1L000S, U1-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

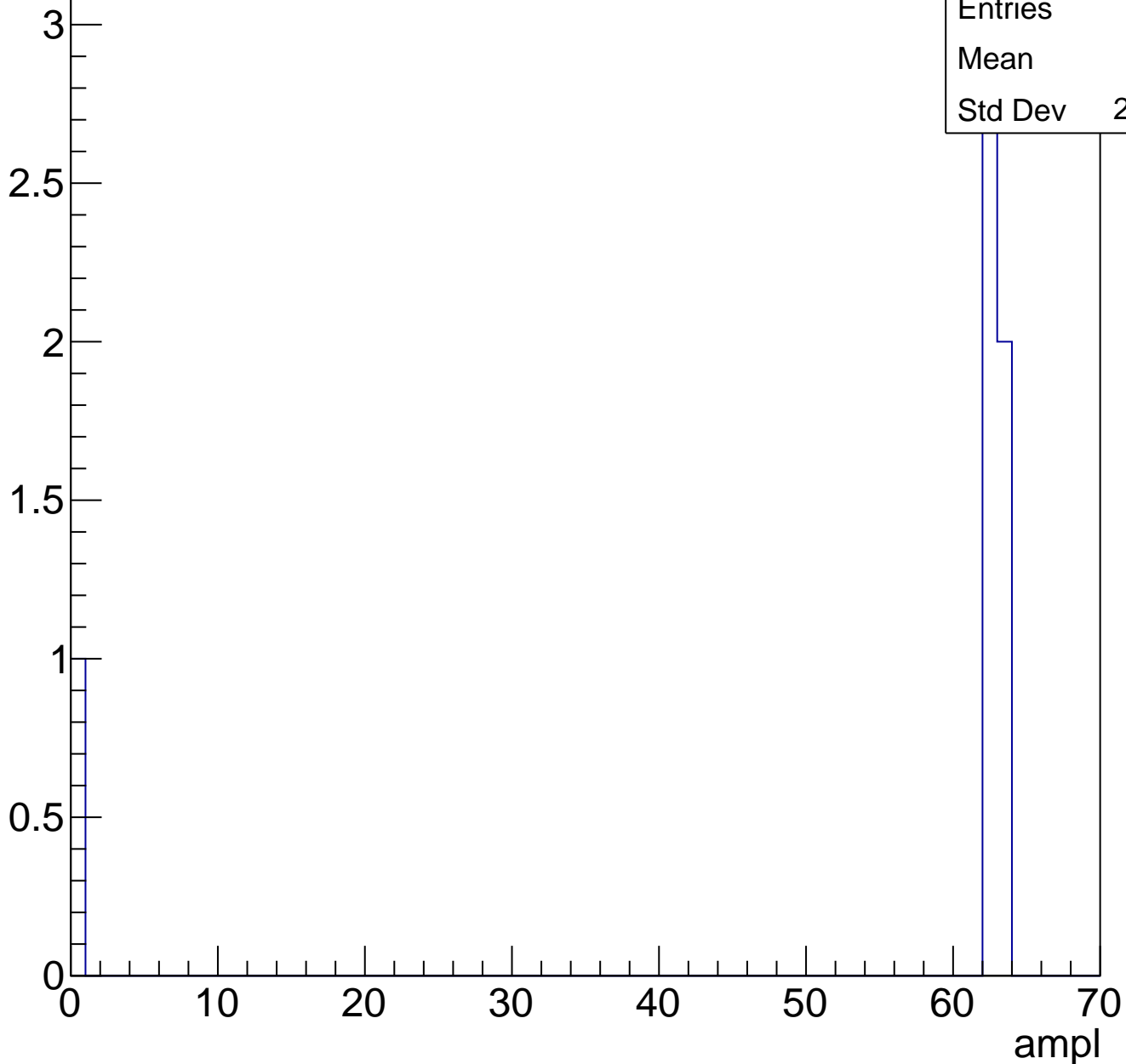




# B1L000S, U1-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch54, adc0

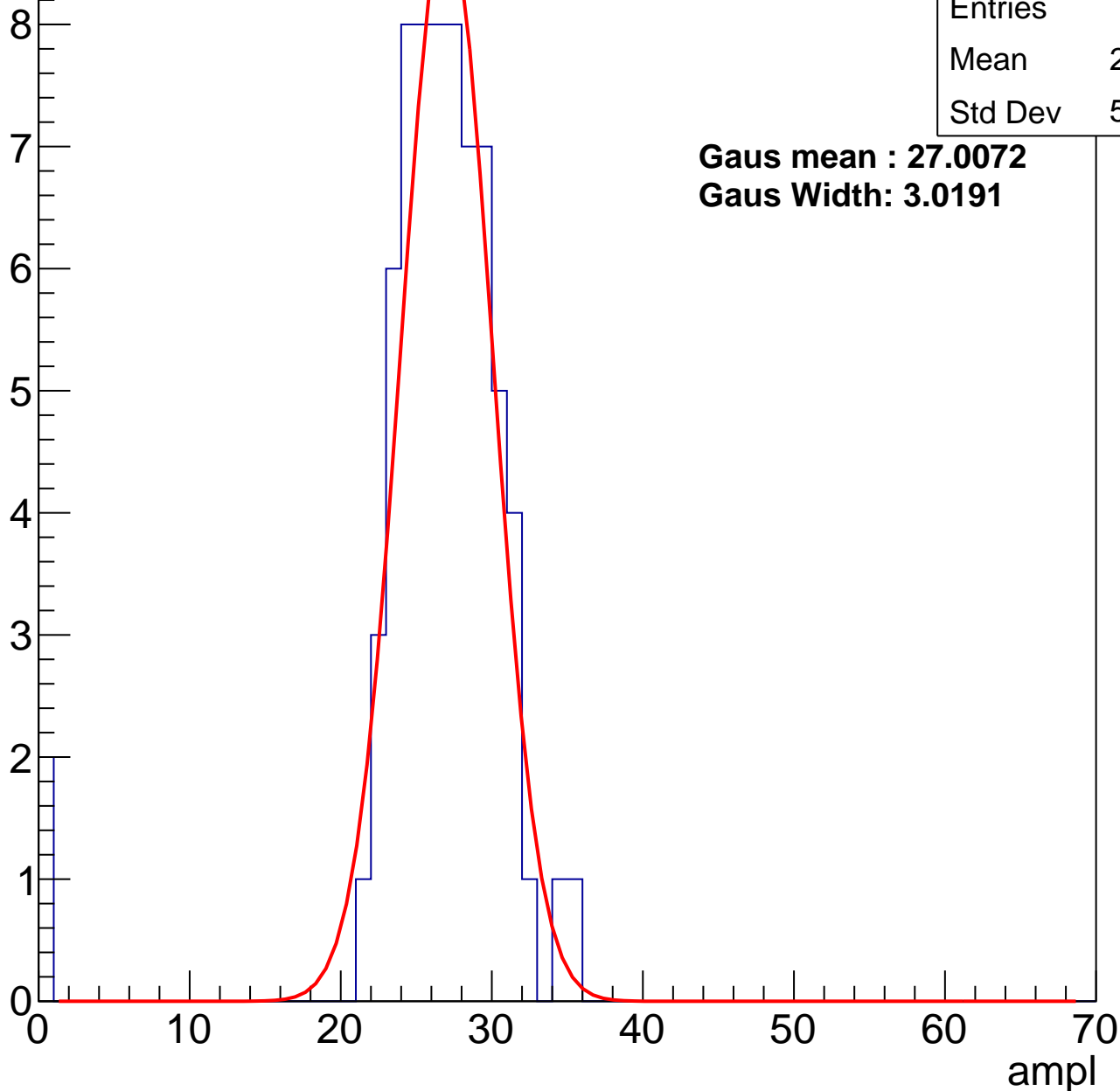
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	25.93
Std Dev	5.317

**Gaus mean : 27.0072**

**Gaus Width: 3.0191**



# B1L000S, U1-ch54, adc1

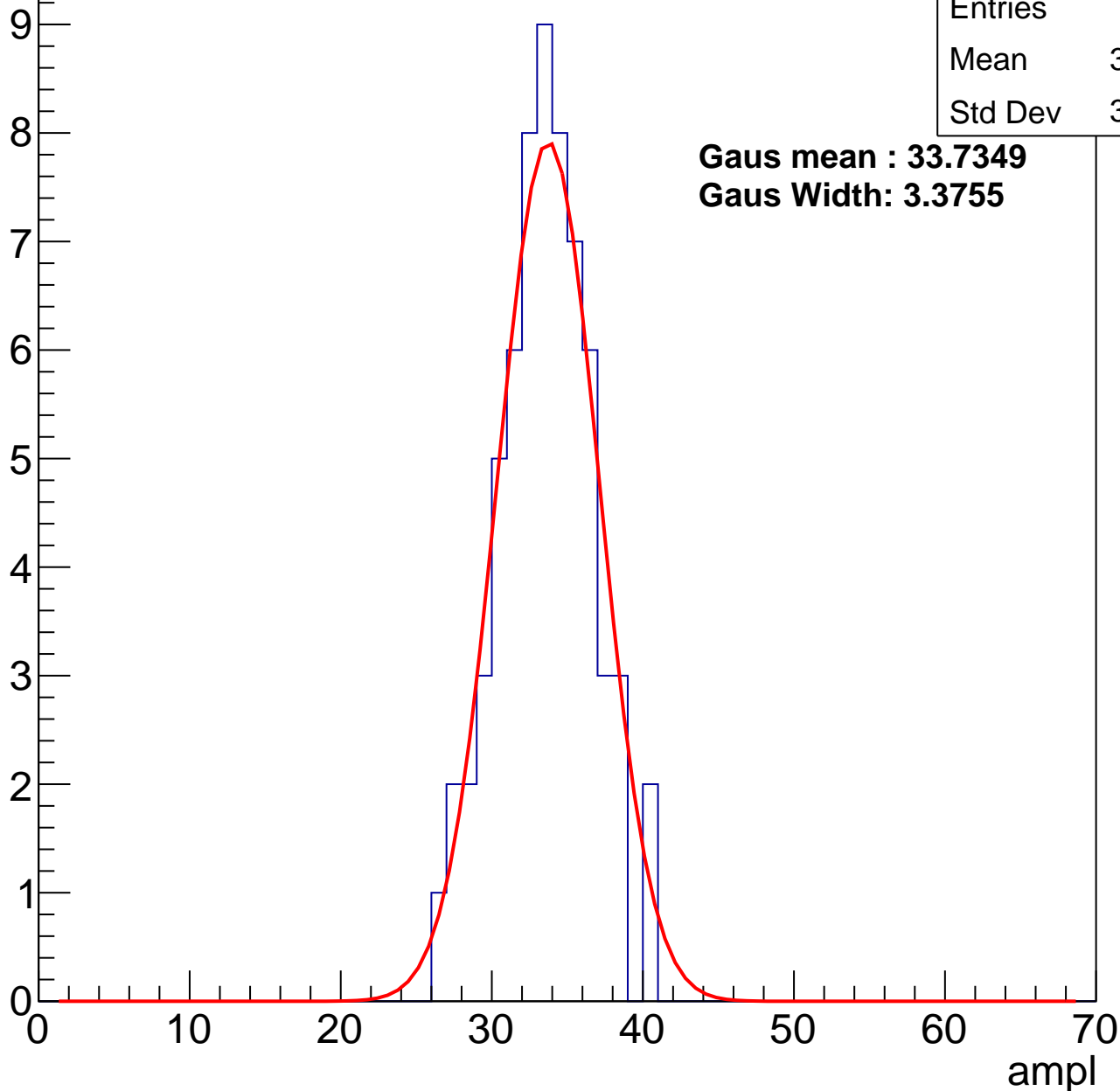
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	33.08
Std Dev	3.055

**Gaus mean : 33.7349**

**Gaus Width: 3.3755**



# B1L000S, U1-ch54, adc2

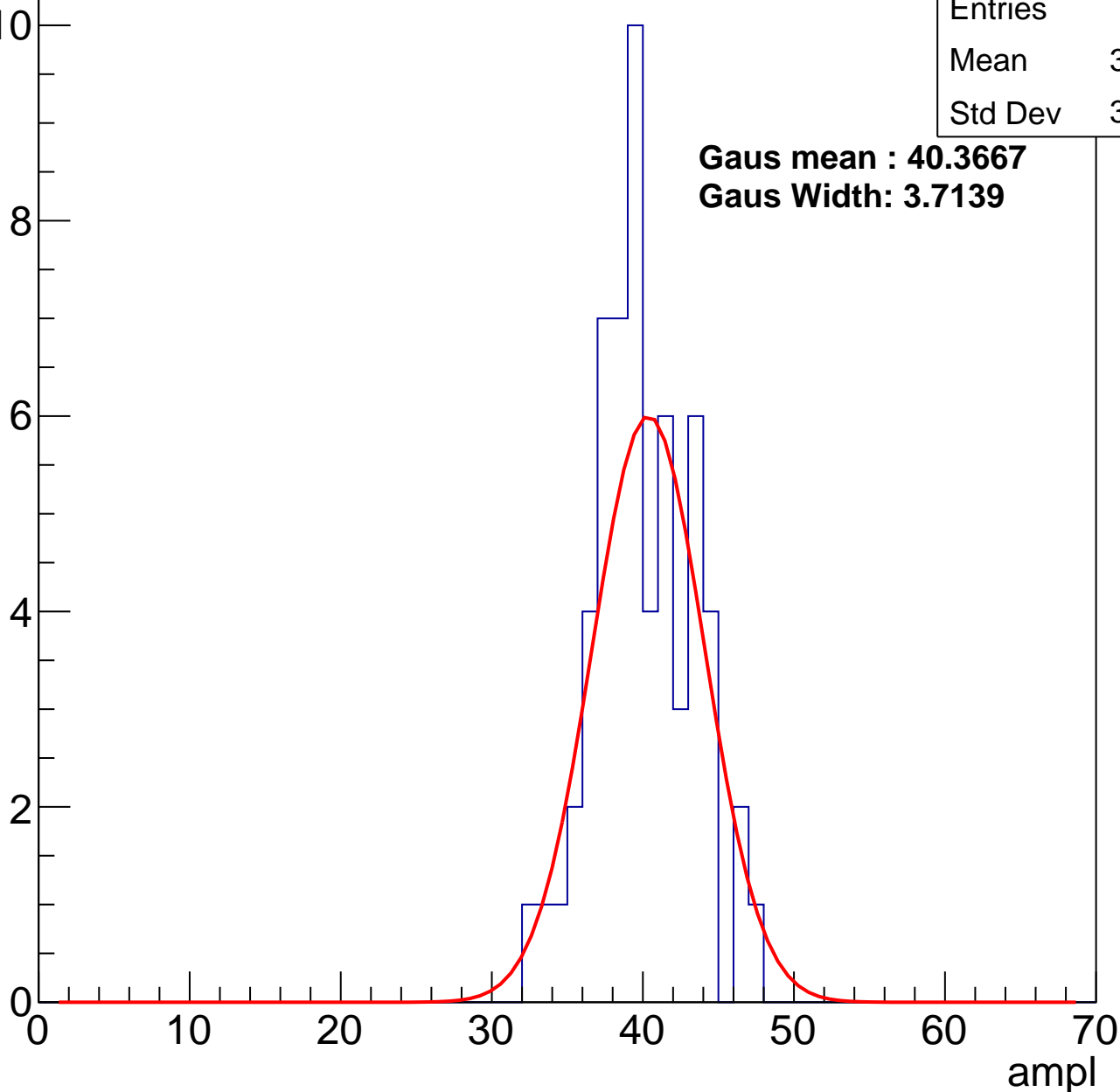
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	39.54
Std Dev	3.212

**Gaus mean : 40.3667**

**Gaus Width: 3.7139**



# B1L000S, U1-ch54, adc3

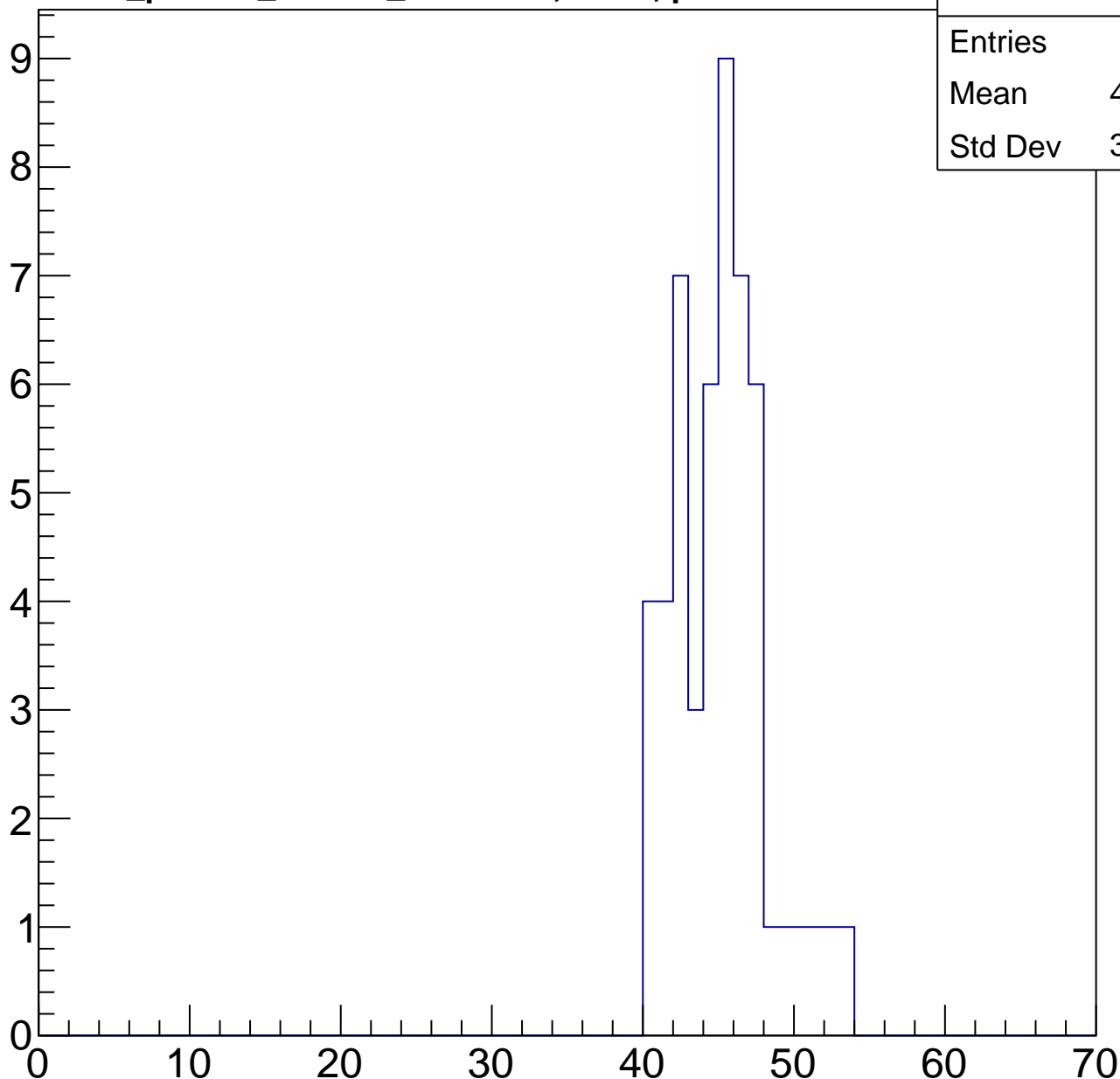
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	44.67
Std Dev	3.005

ampl

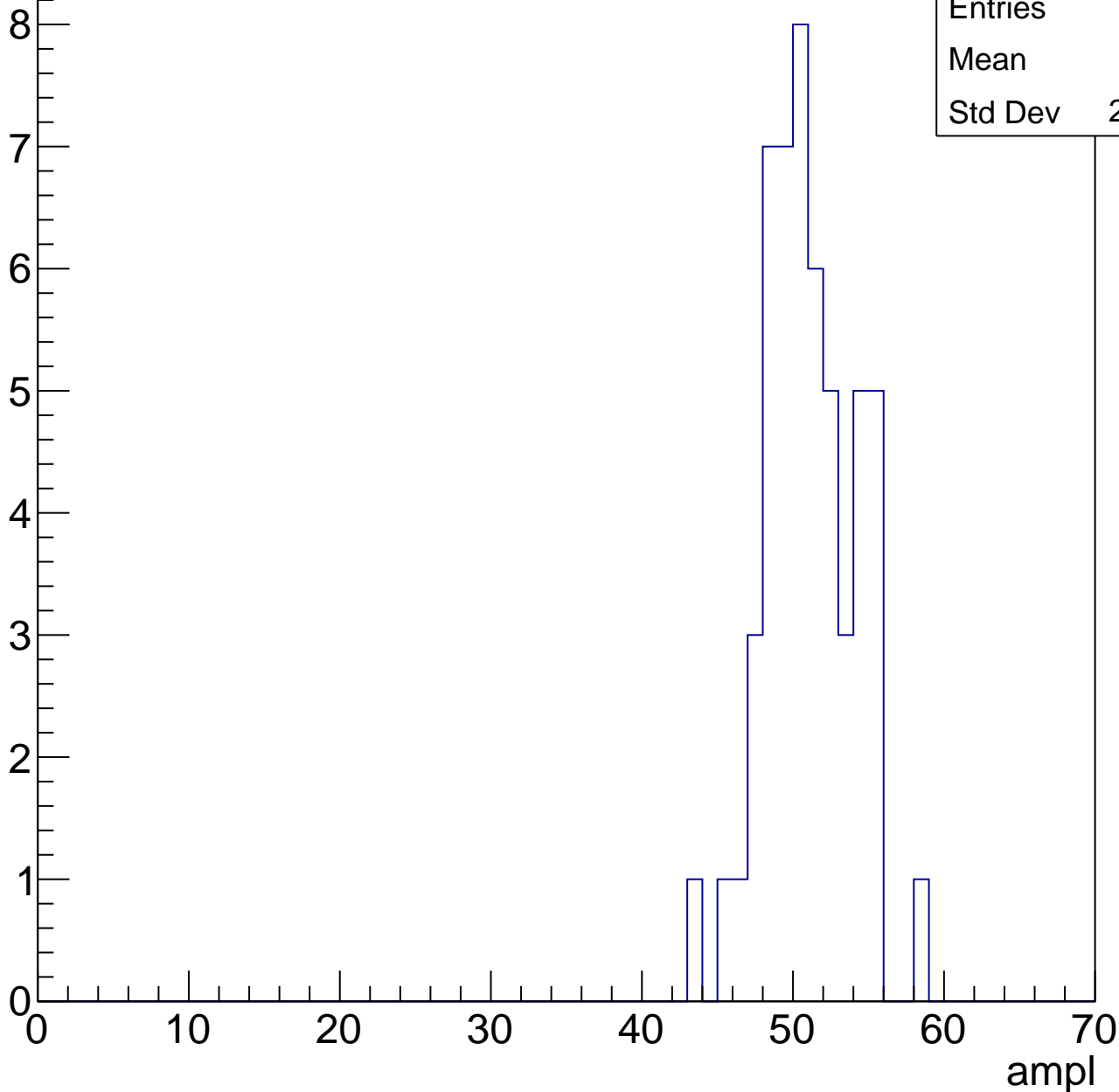


# B1L000S, U1-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	50.6
Std Dev	2.929



# B1L000S, U1-ch54, adc5

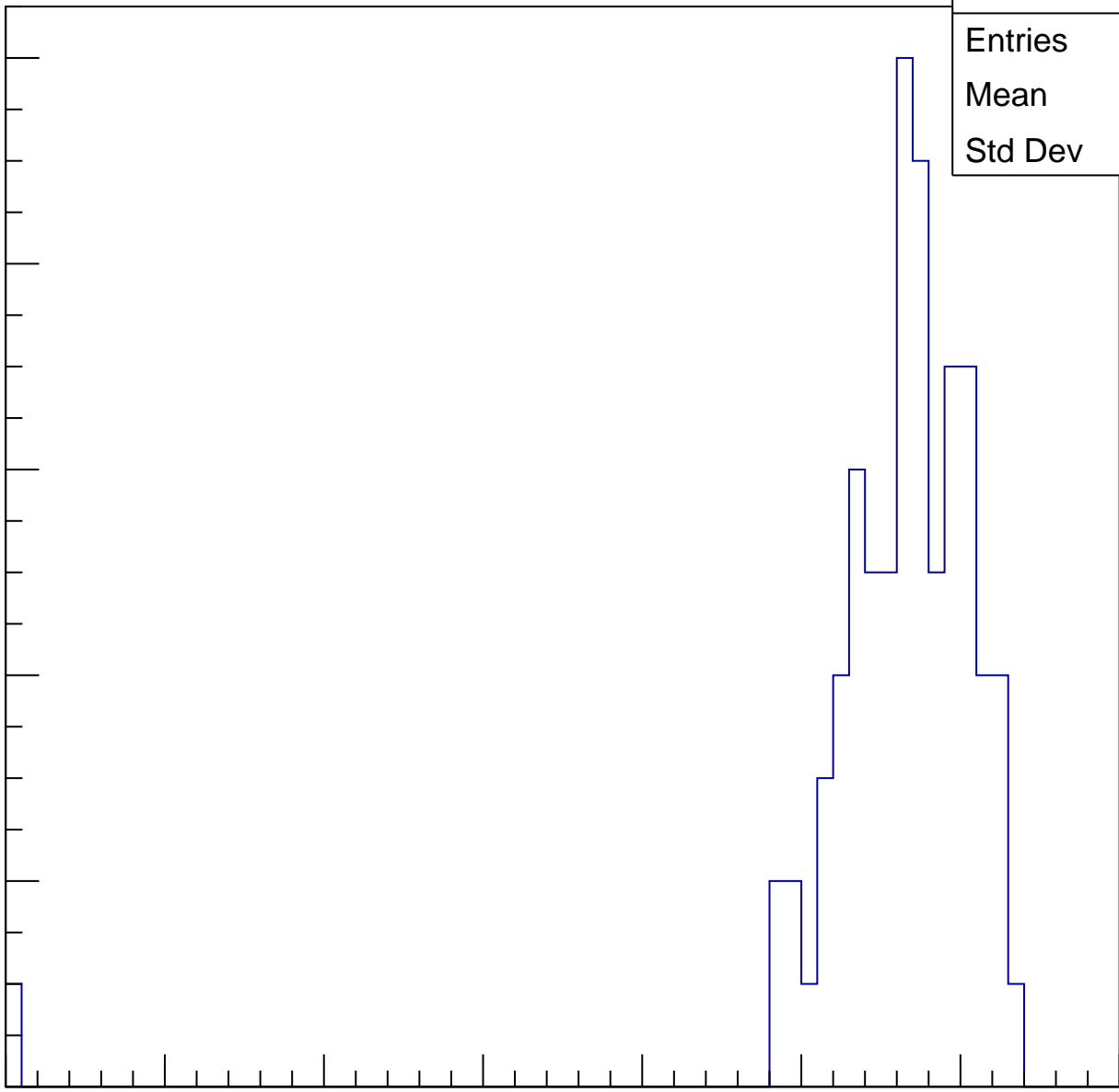
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	55.51
Std Dev	7.34

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70  
ampl

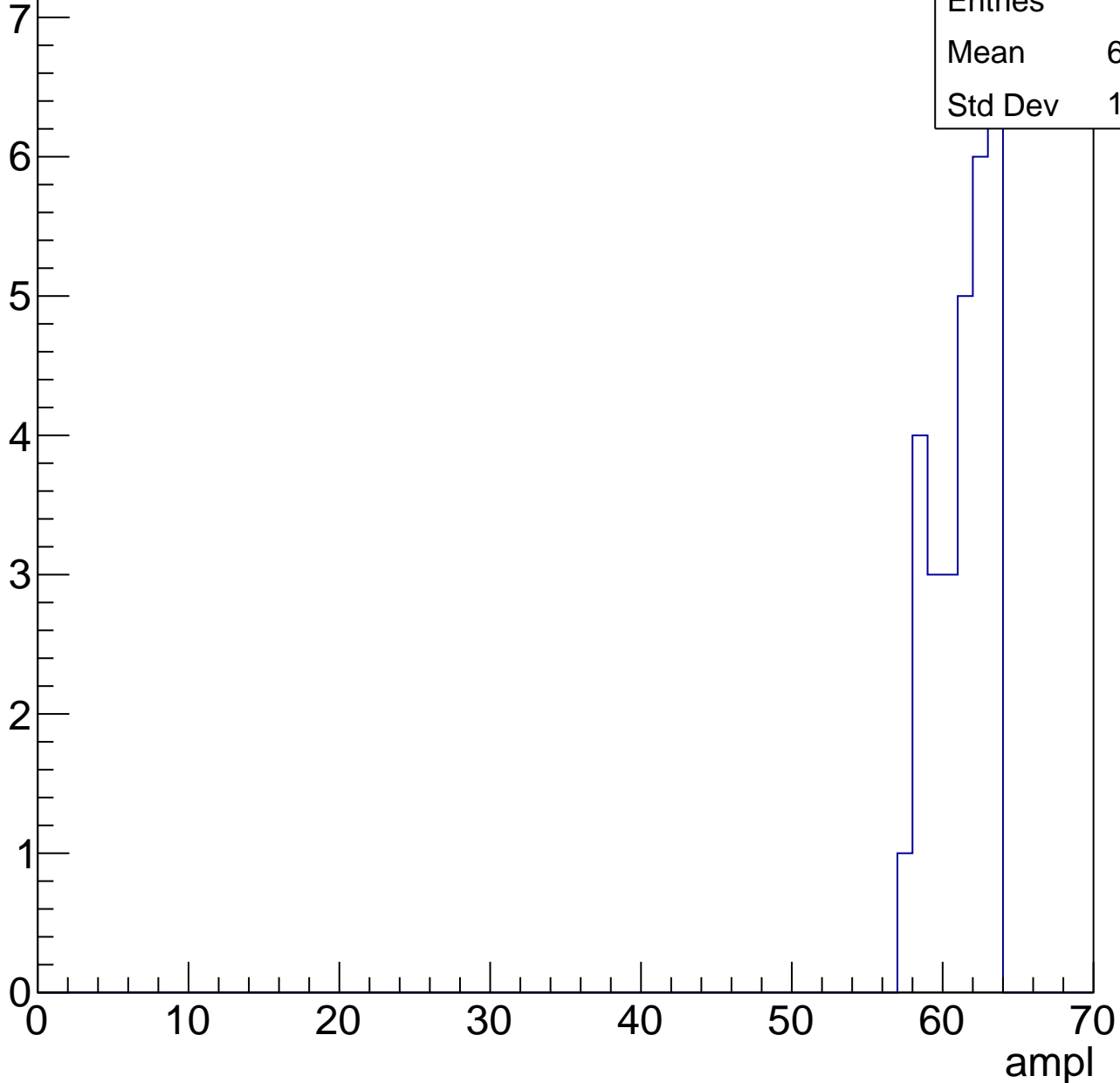


# B1L000S, U1-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	60.83
Std Dev	1.858

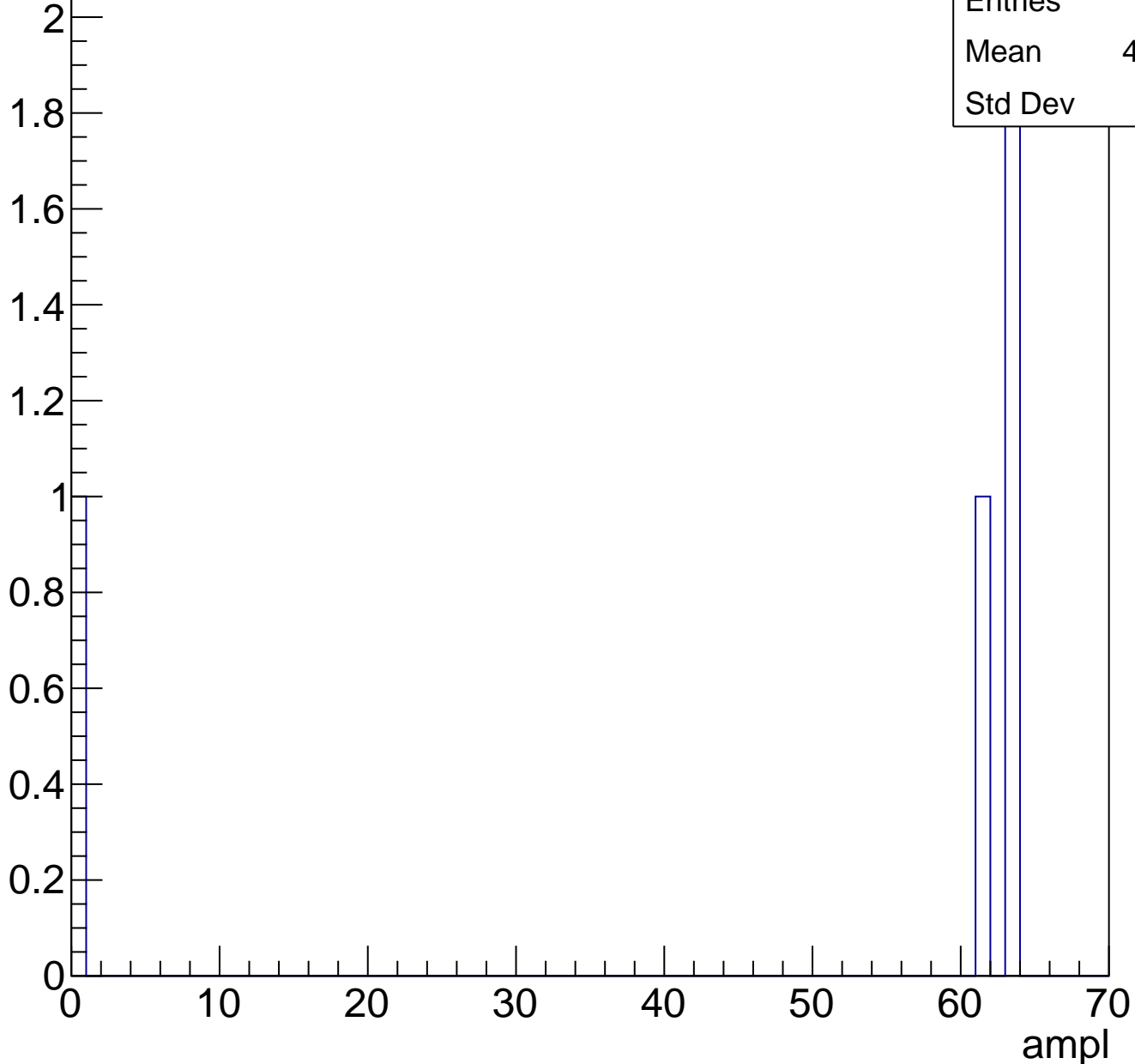




# B1L000S, U1-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	46.75
Std Dev	27

# B1L000S, U1-ch55, adc0

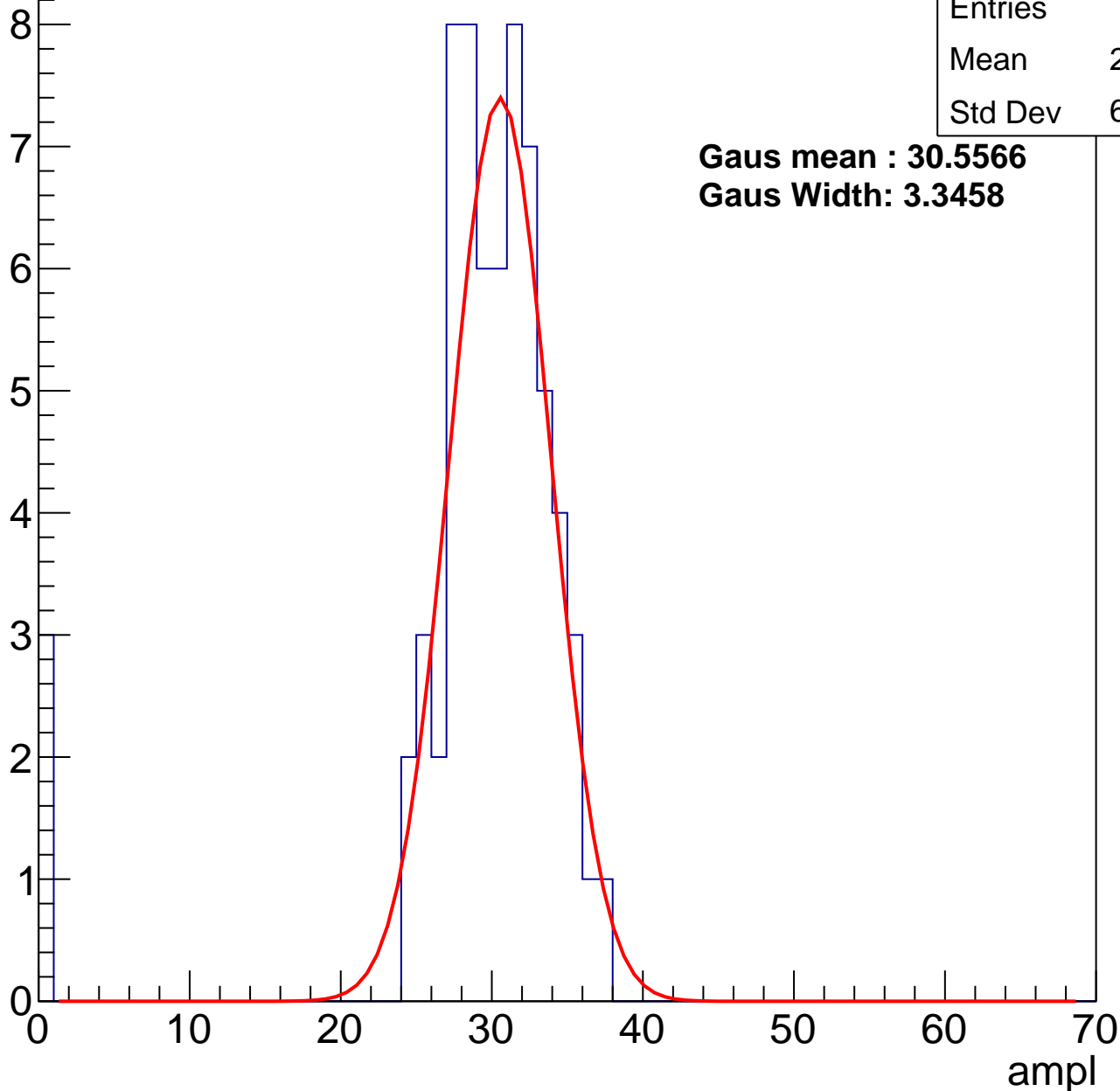
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.66
Std Dev	6.882

**Gaus mean : 30.5566**

**Gaus Width: 3.3458**



# B1L000S, U1-ch55, adc1

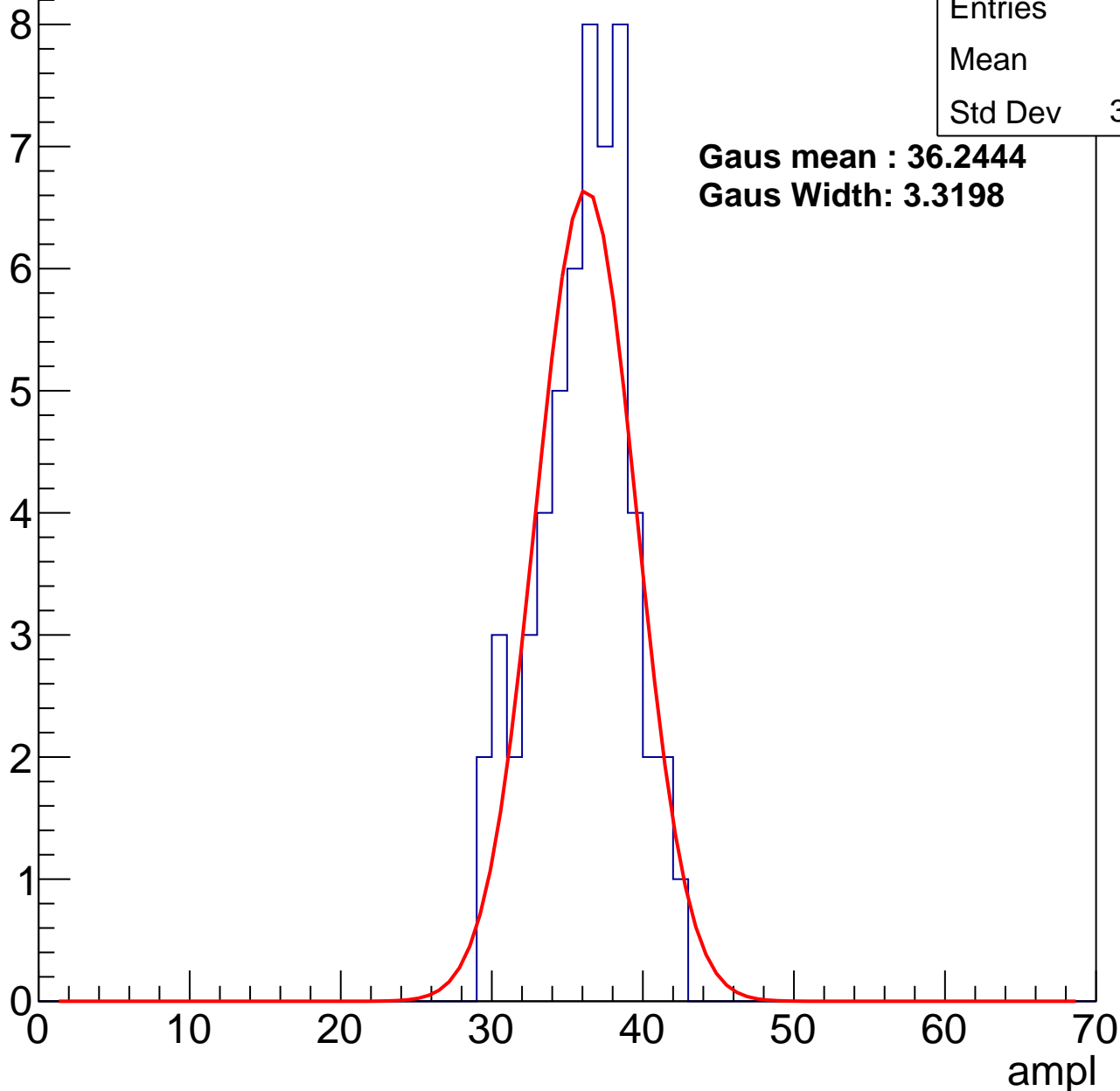
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.6
Std Dev	3.106

**Gaus mean : 36.2444**

**Gaus Width: 3.3198**



# B1L000S, U1-ch55, adc2

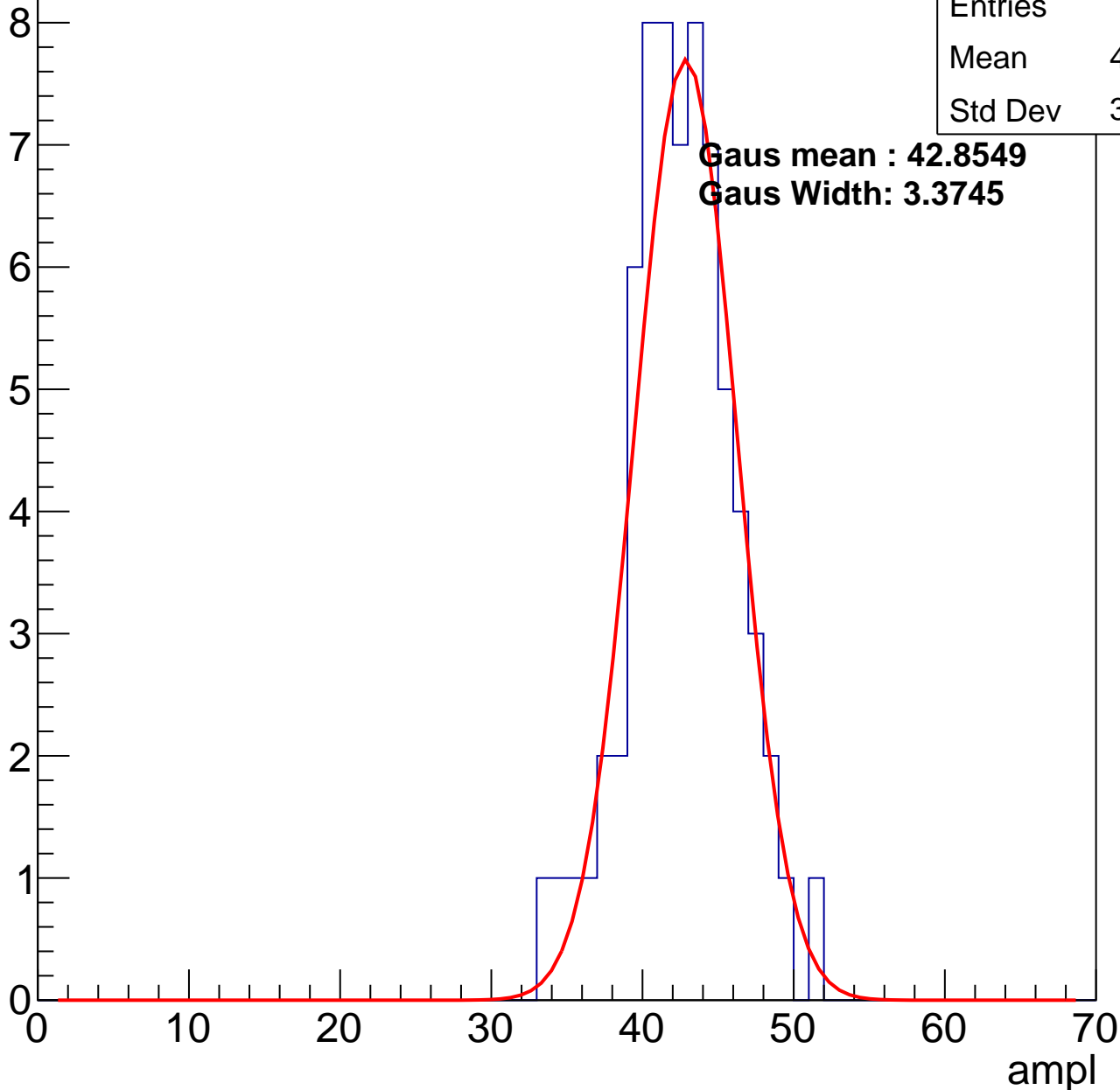
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	42.09
Std Dev	3.488

**Gaus mean : 42.8549**

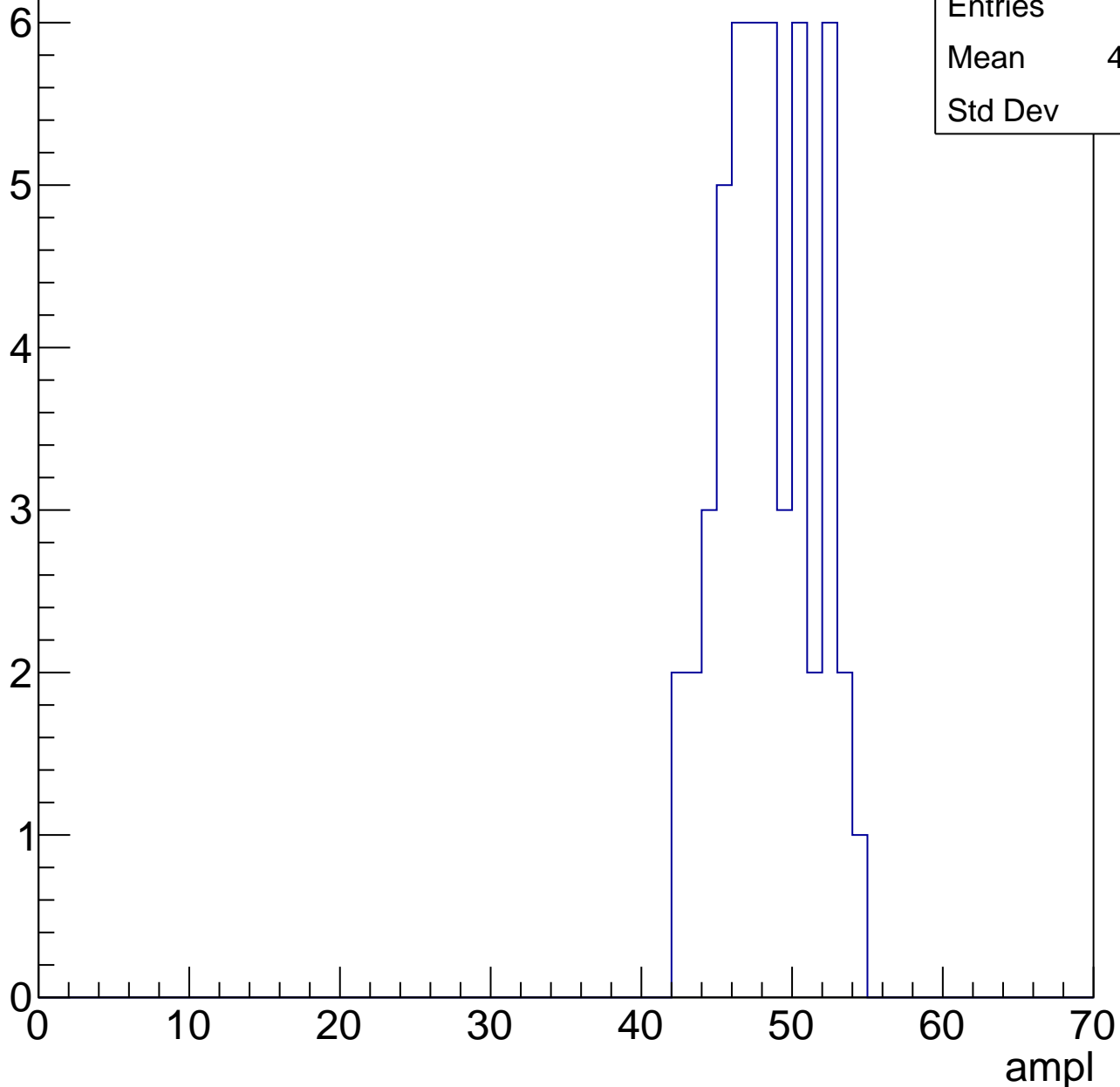
**Gaus Width: 3.3745**



# B1L000S, U1-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



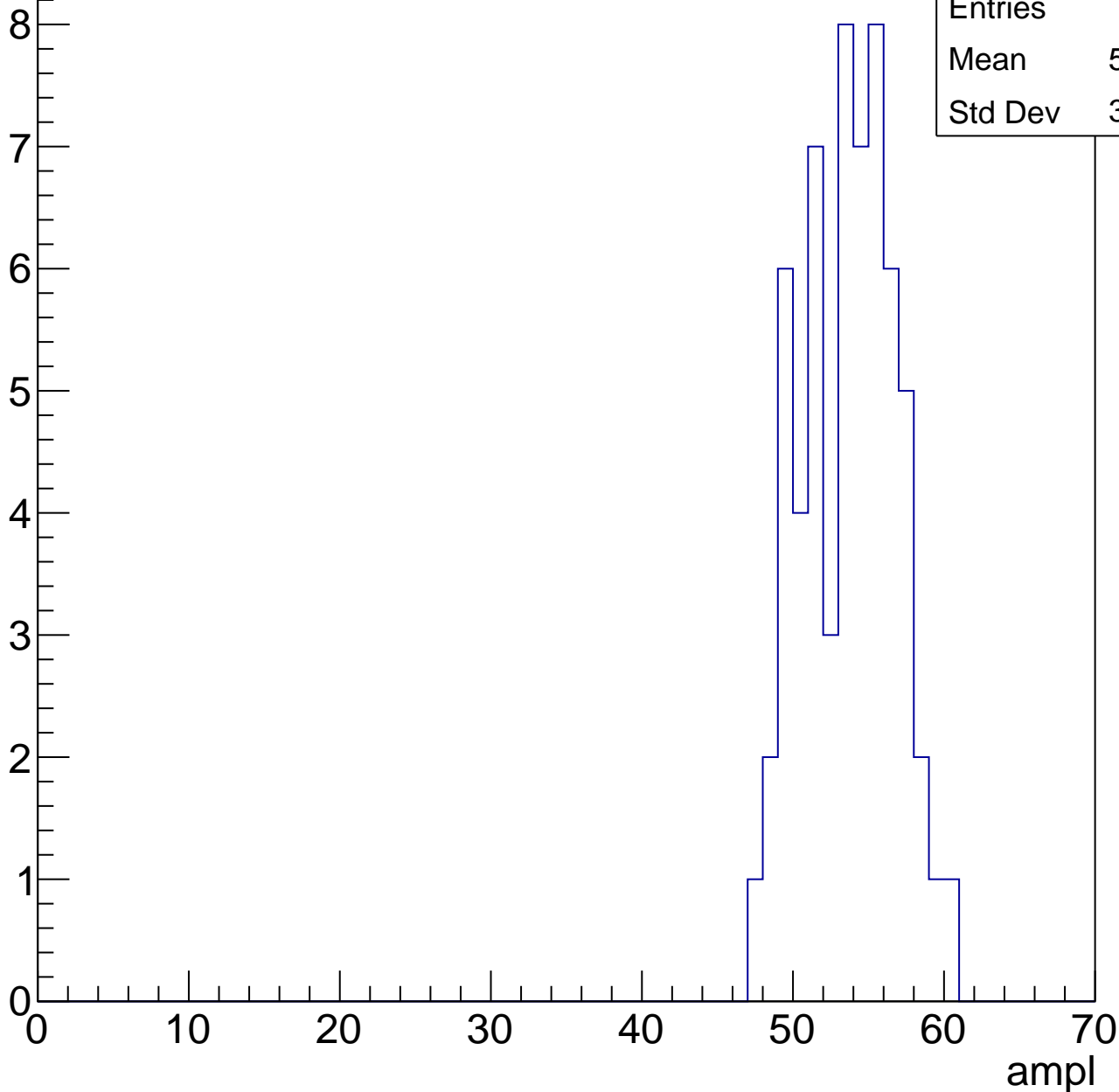
Entries	50
Mean	47.88
Std Dev	3.07

# B1L000S, U1-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

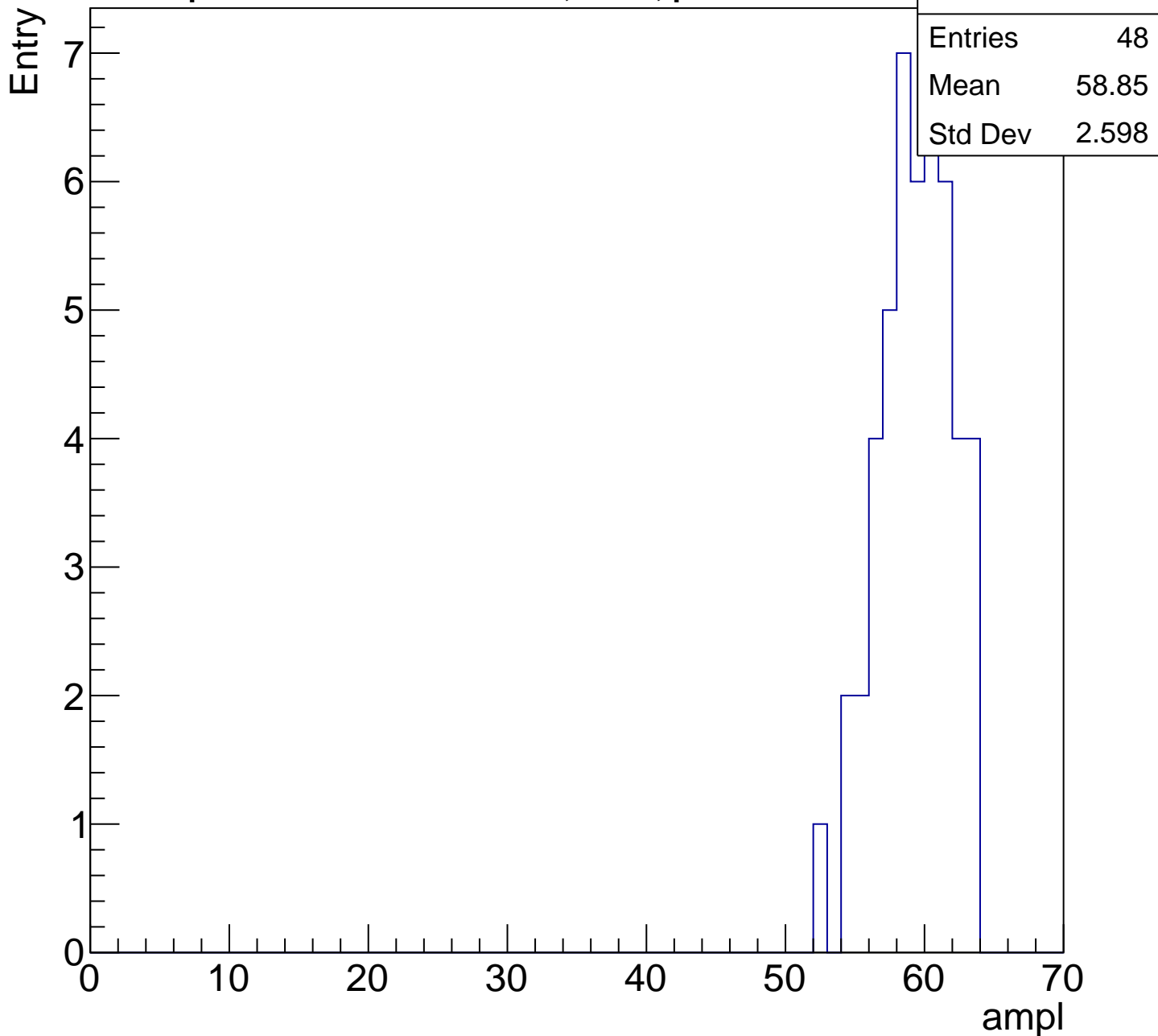
Entry

Entries	61
Mean	53.25
Std Dev	3.012



# B1L000S, U1-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

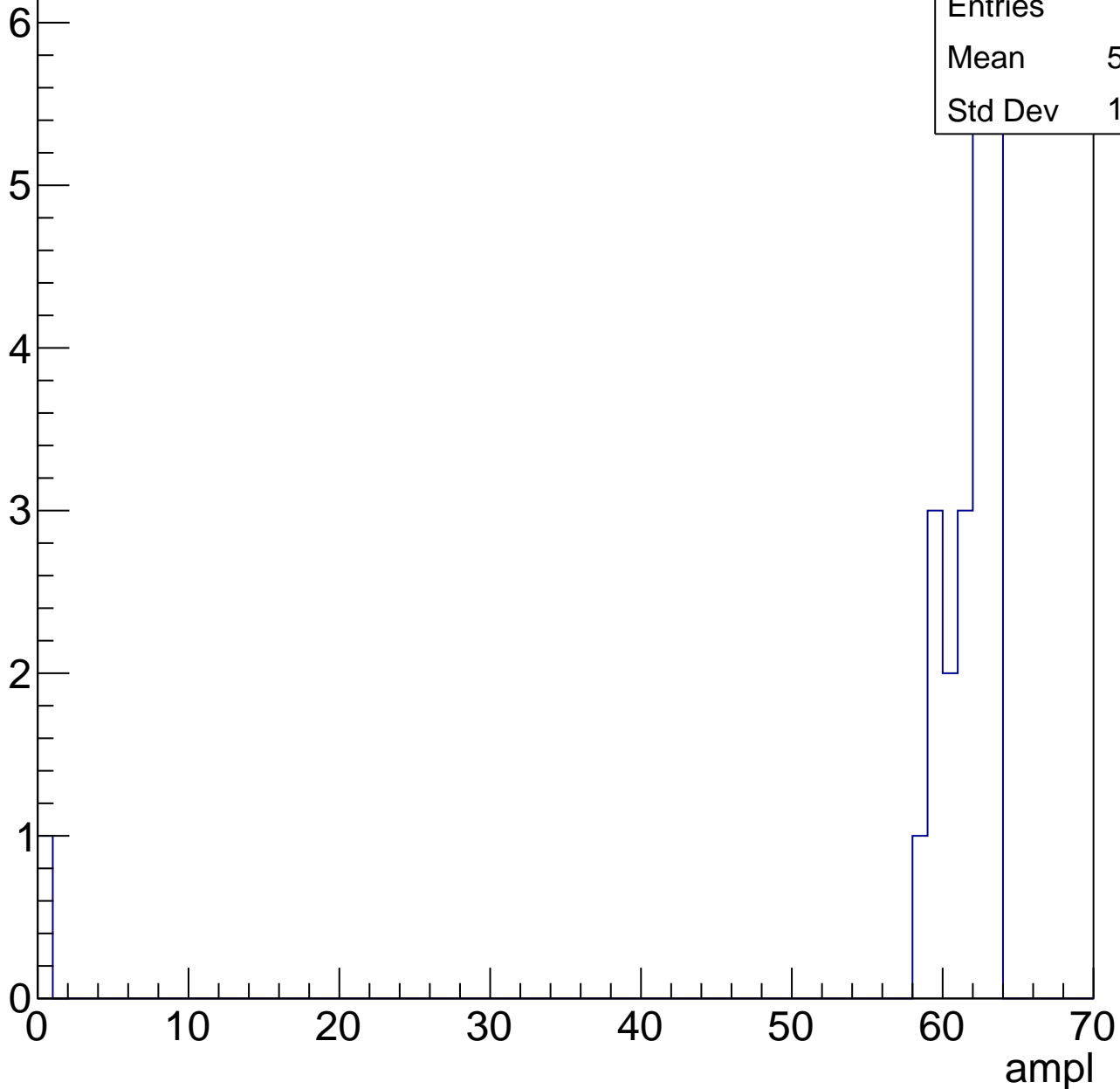


# B1L000S, U1-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.55
Std Dev	12.87

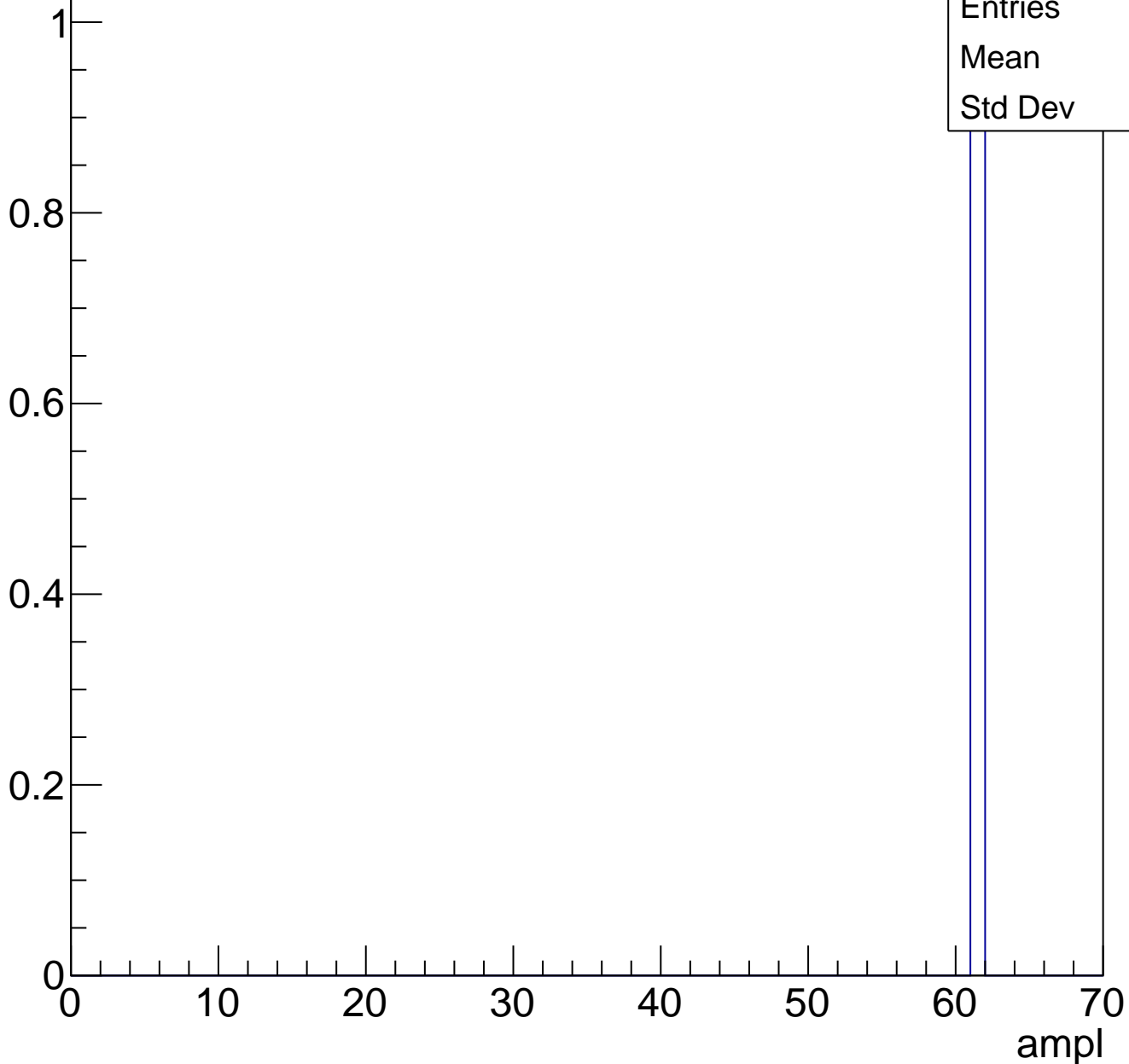




# B1L000S, U1-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch56, adc0

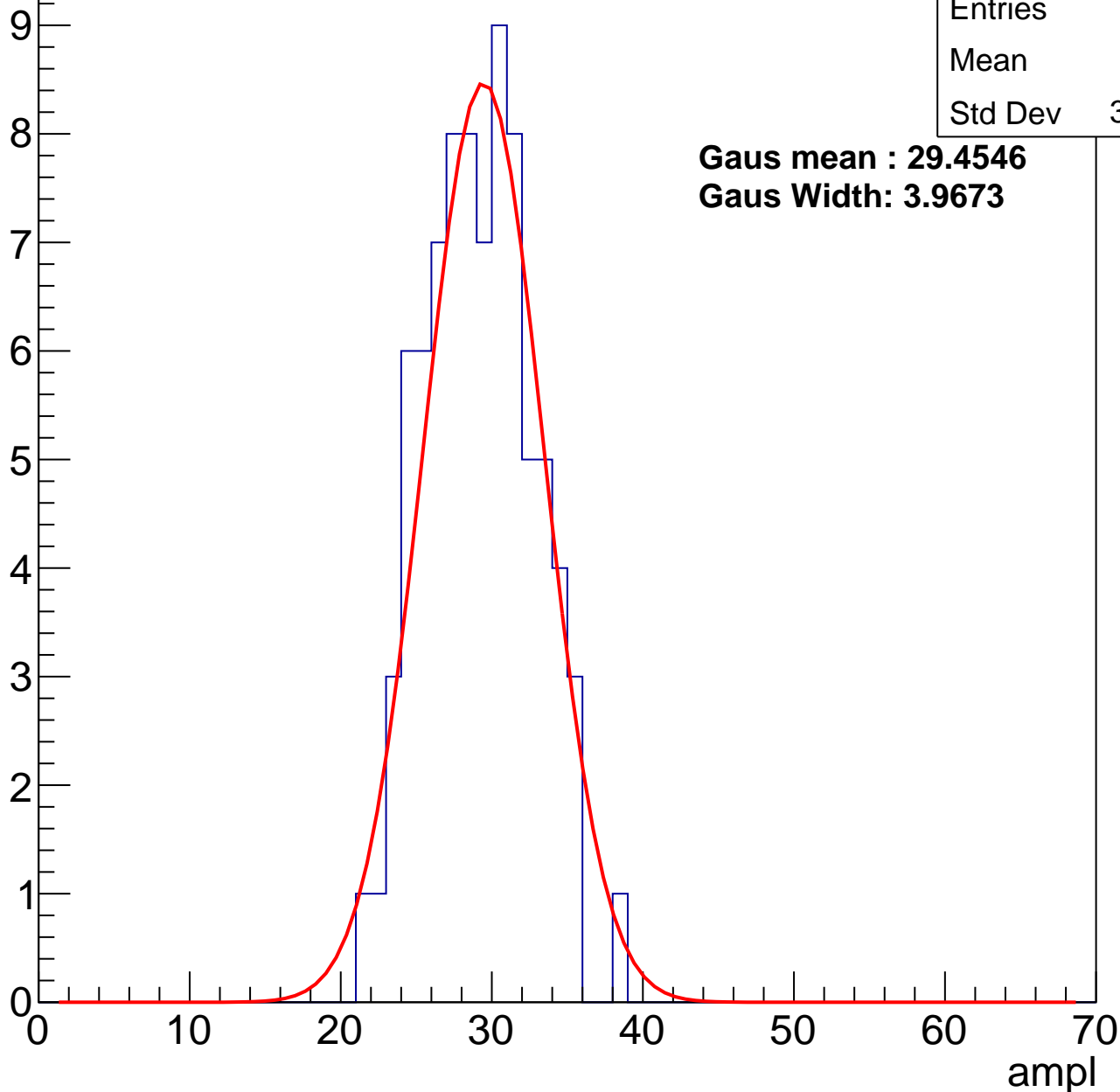
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	28.7
Std Dev	3.522

**Gaus mean : 29.4546**

**Gaus Width: 3.9673**



# B1L000S, U1-ch56, adc1

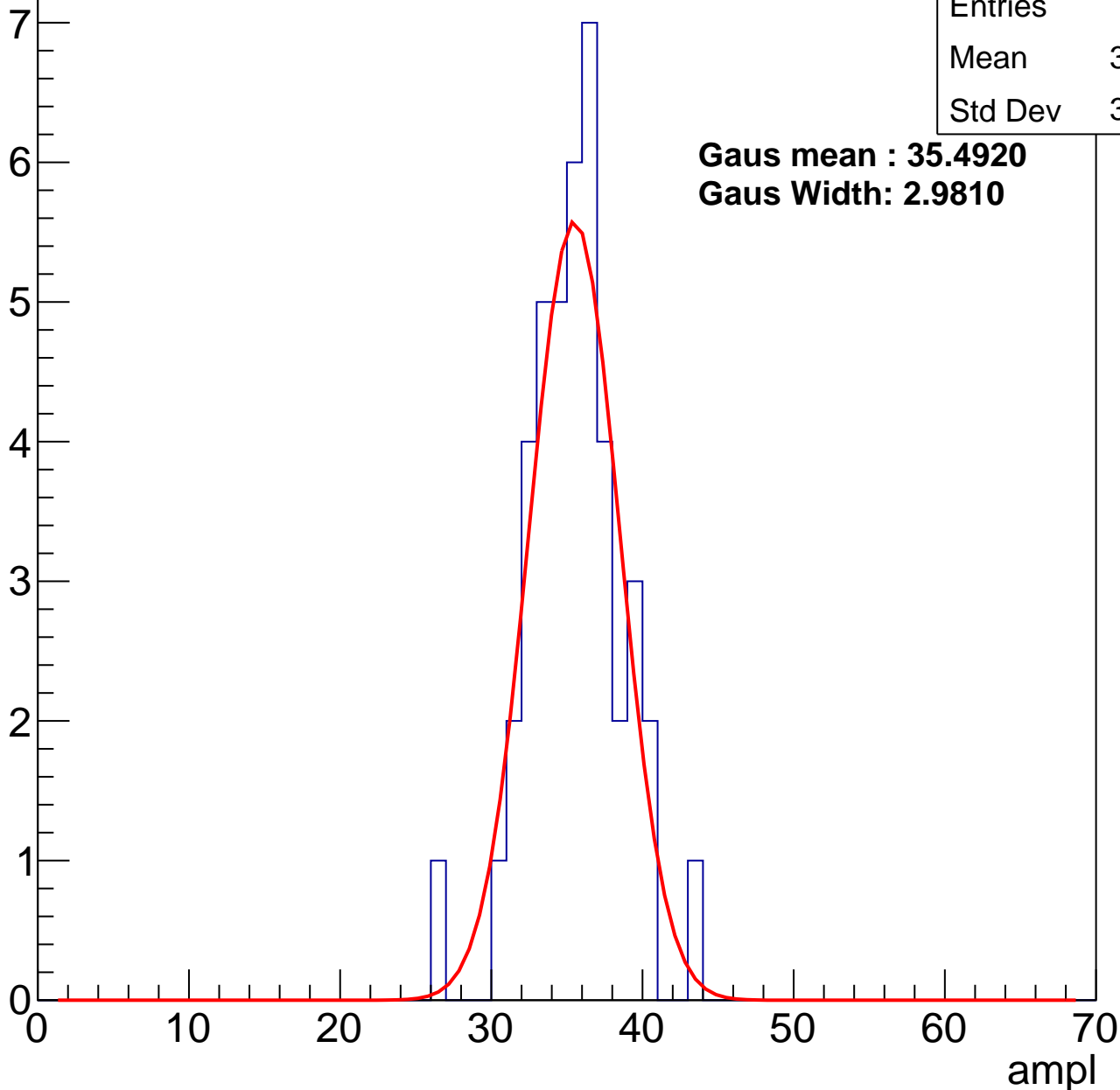
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	35.05
Std Dev	3.057

**Gaus mean : 35.4920**

**Gaus Width: 2.9810**



# B1L000S, U1-ch56, adc2

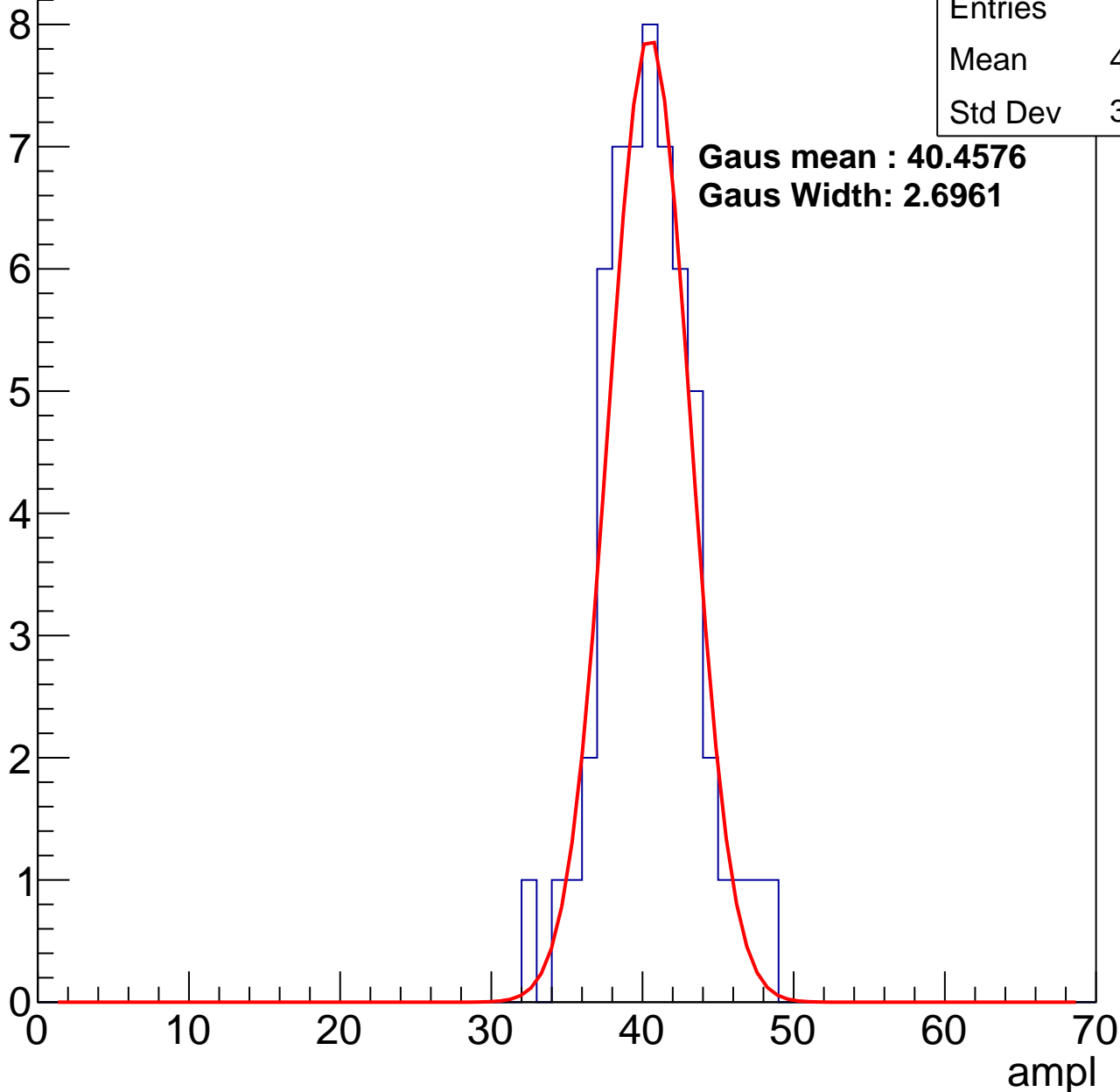
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	40.04
Std Dev	3.043

**Gaus mean : 40.4576**

**Gaus Width: 2.6961**

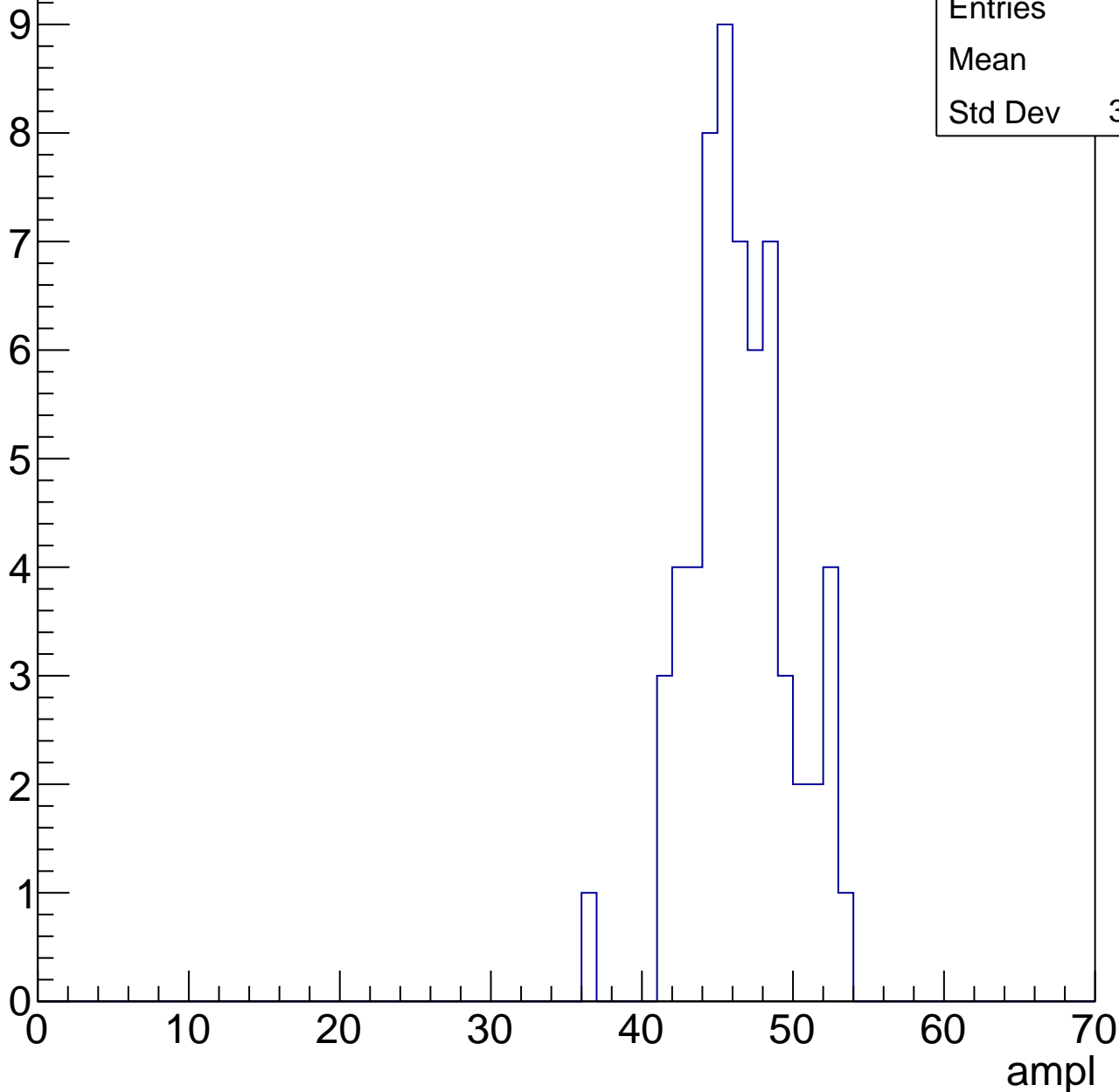


# B1L000S, U1-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

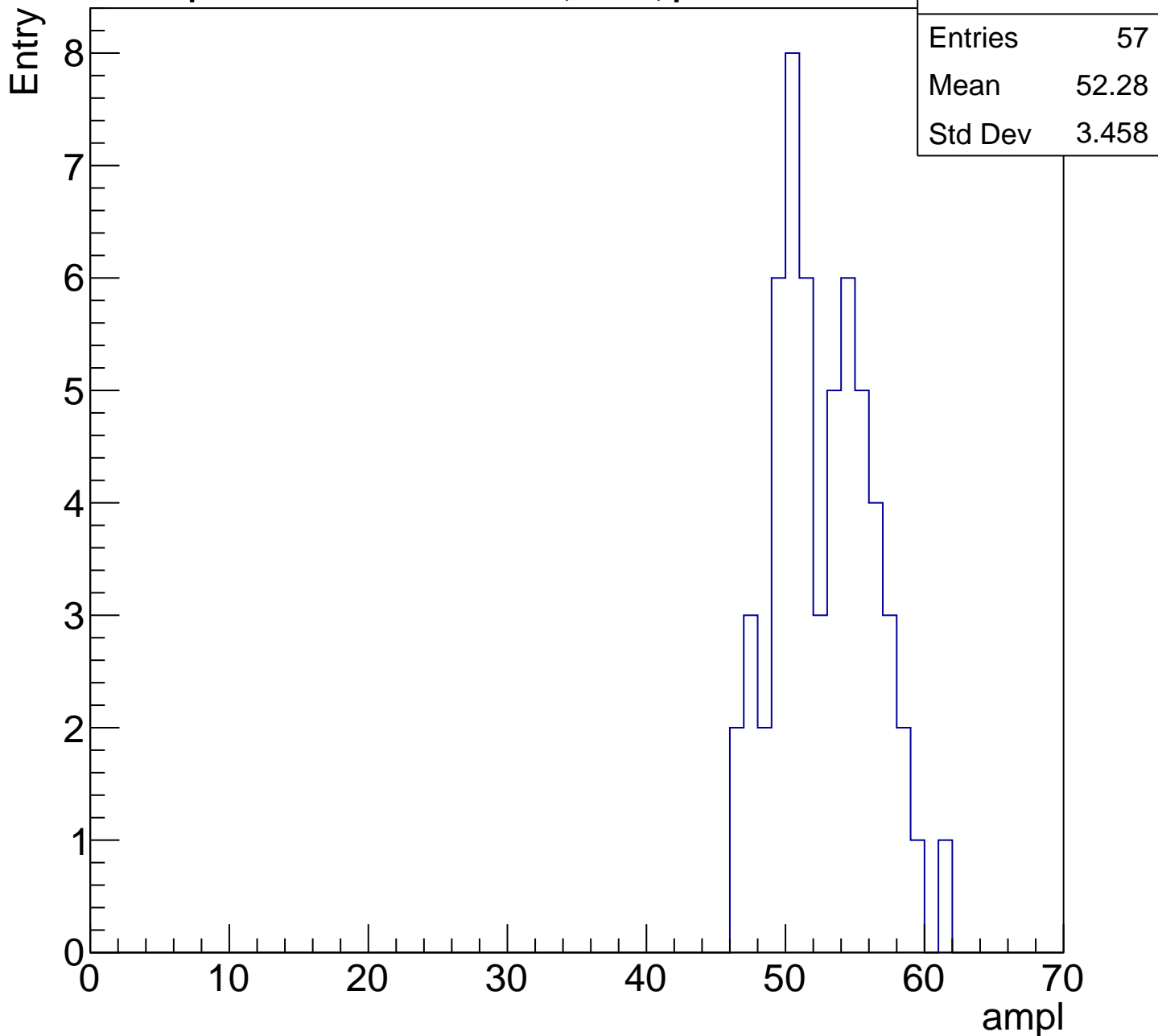
Entry

Entries	61
Mean	46
Std Dev	3.269



# B1L000S, U1-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

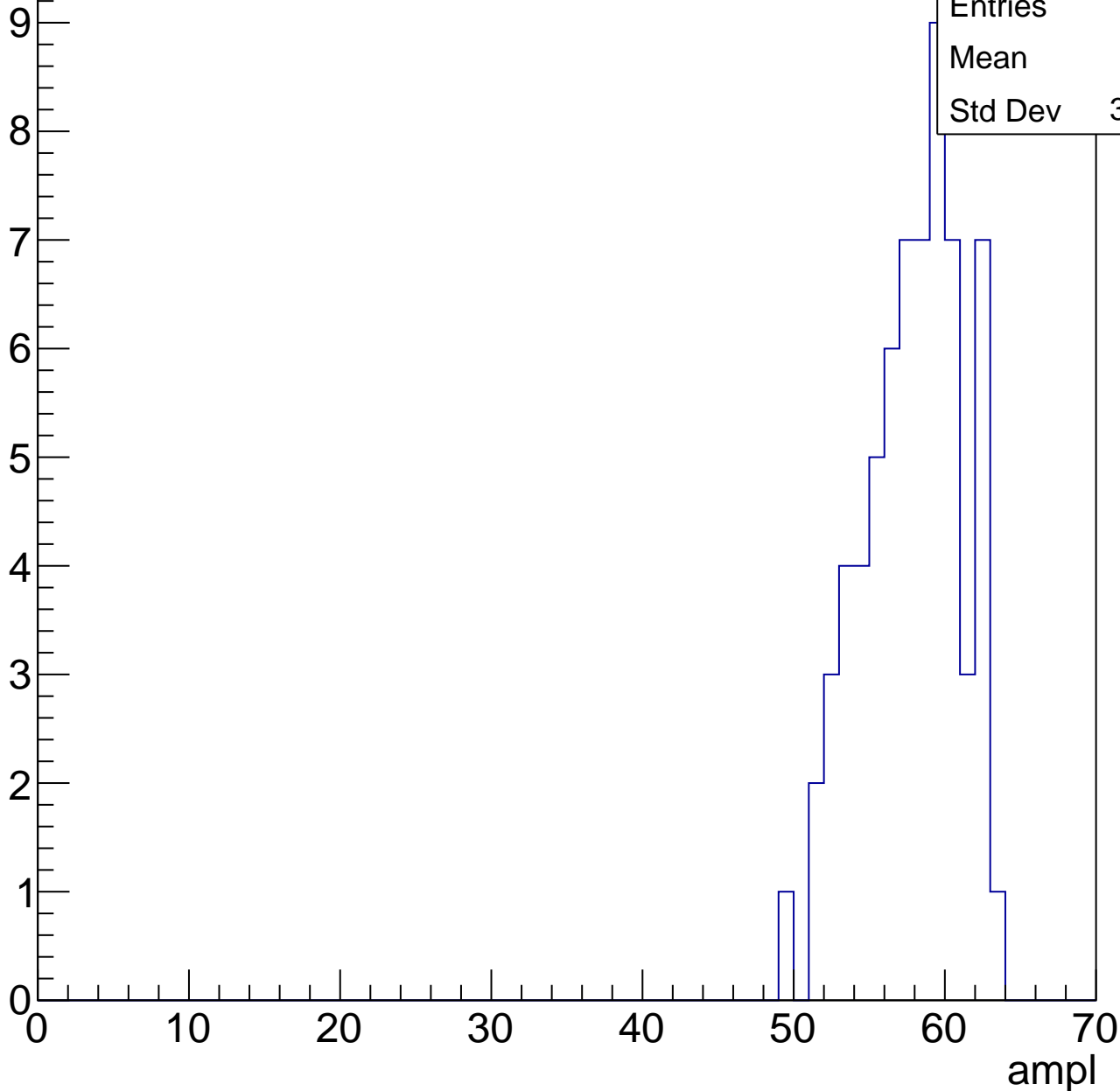


# B1L000S, U1-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	57.3
Std Dev	3.247

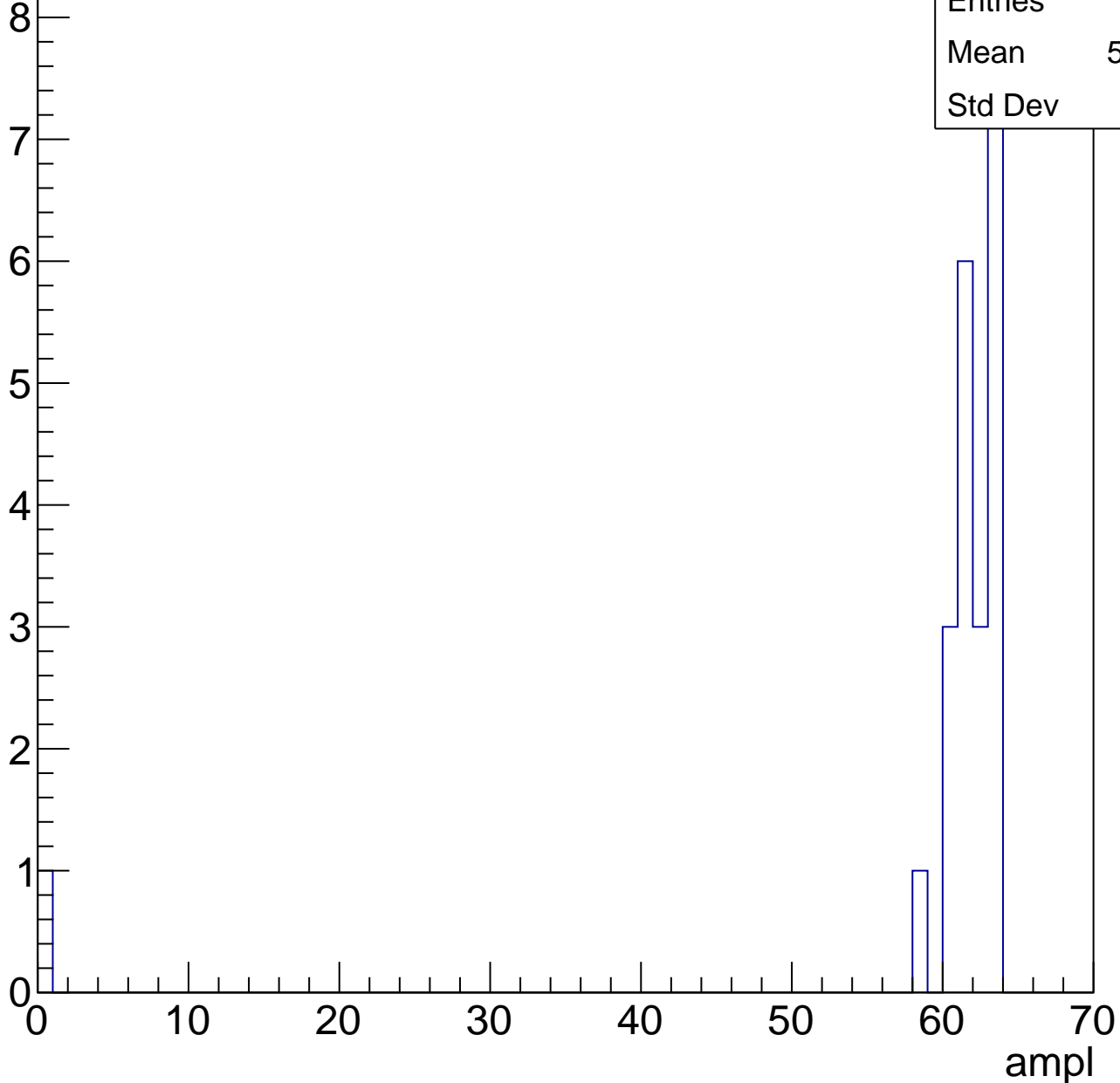


# B1L000S, U1-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.82
Std Dev	12.9





# B1L000S, U1-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch57, adc0

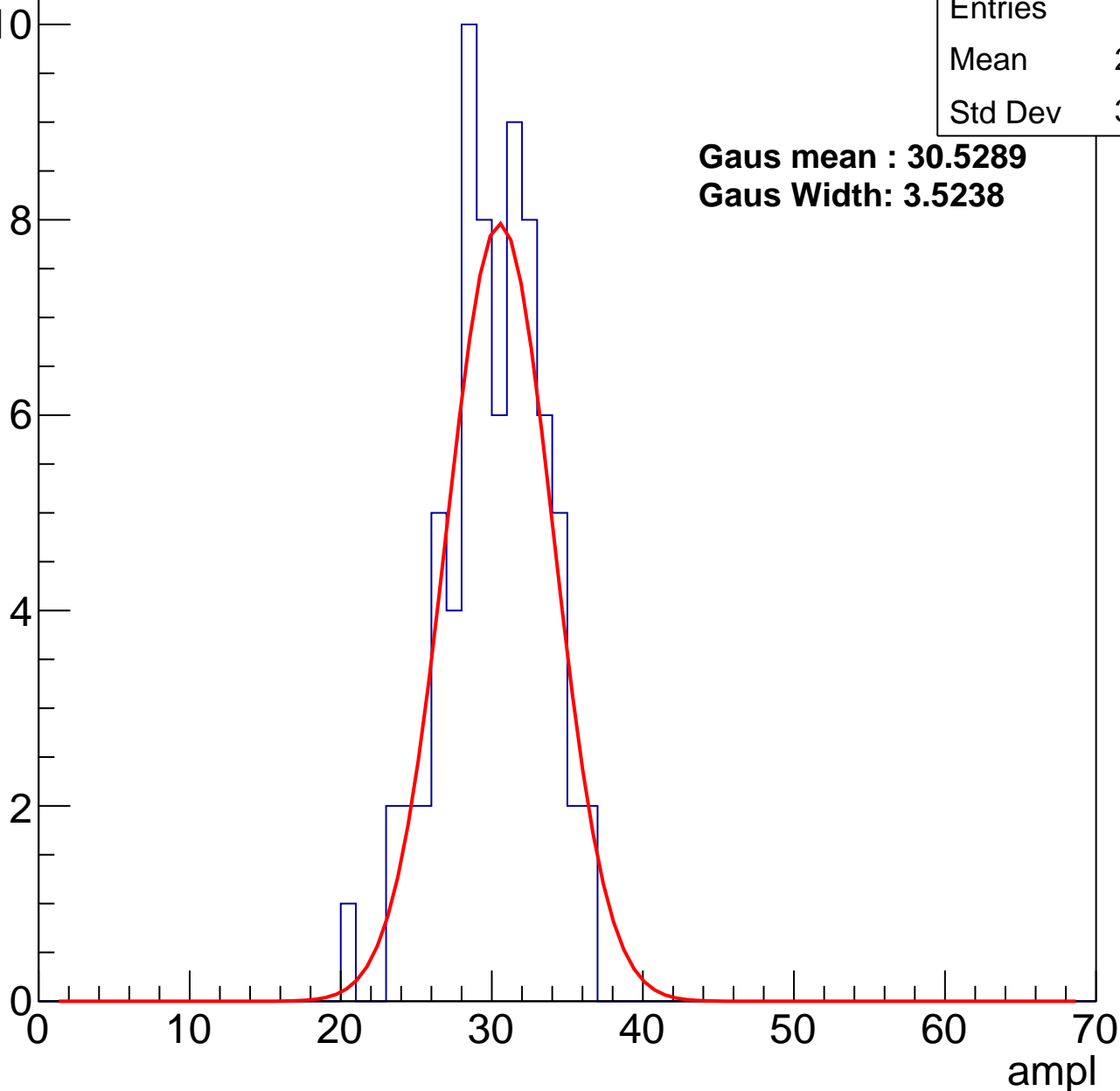
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	29.71
Std Dev	3.281

**Gaus mean : 30.5289**

**Gaus Width: 3.5238**



# B1L000S, U1-ch57, adc1

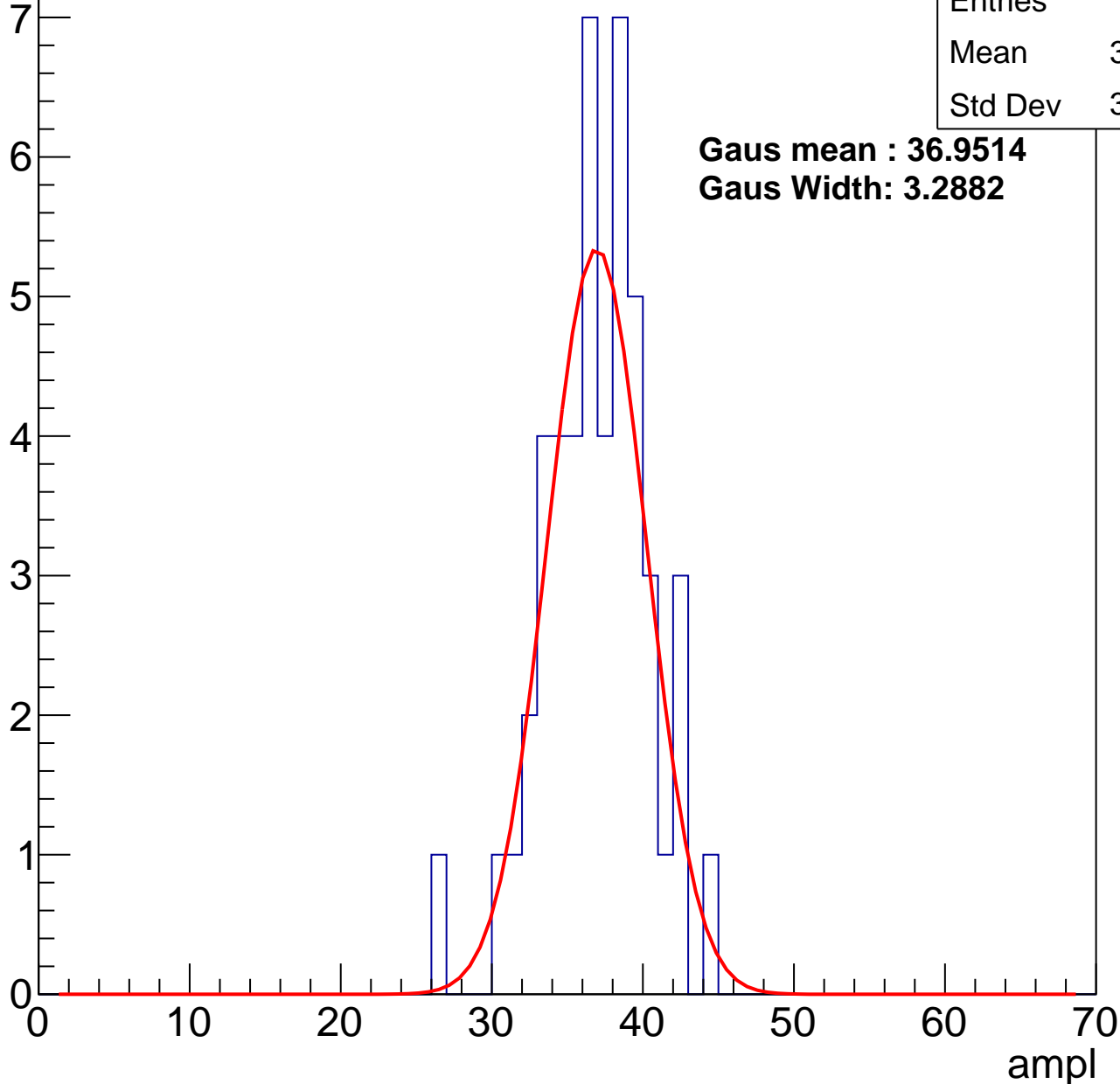
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	36.48
Std Dev	3.416

**Gaus mean : 36.9514**

**Gaus Width: 3.2882**



# B1L000S, U1-ch57, adc2

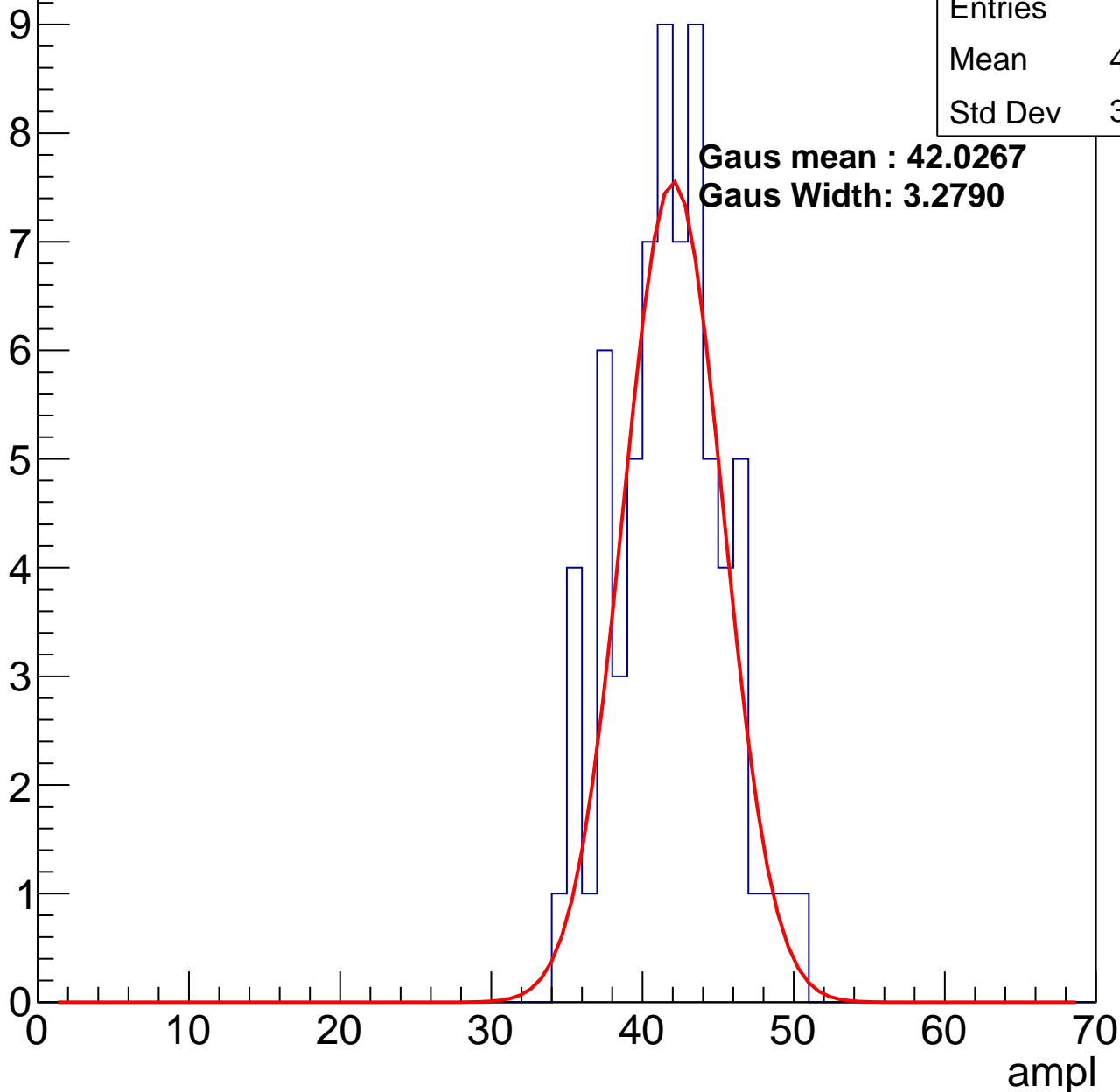
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	41.36
Std Dev	3.517

**Gaus mean : 42.0267**

**Gaus Width: 3.2790**

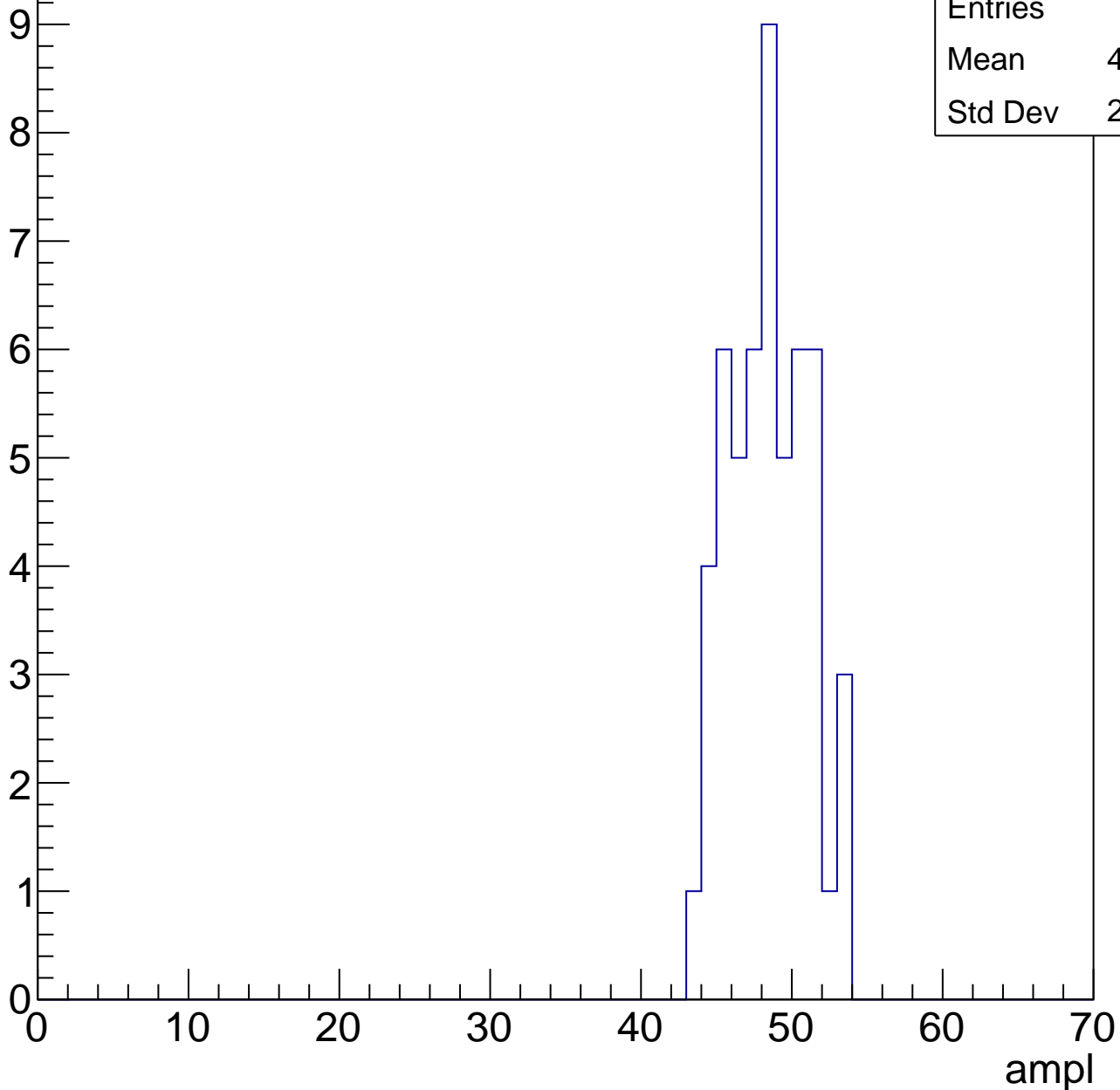


# B1L000S, U1-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

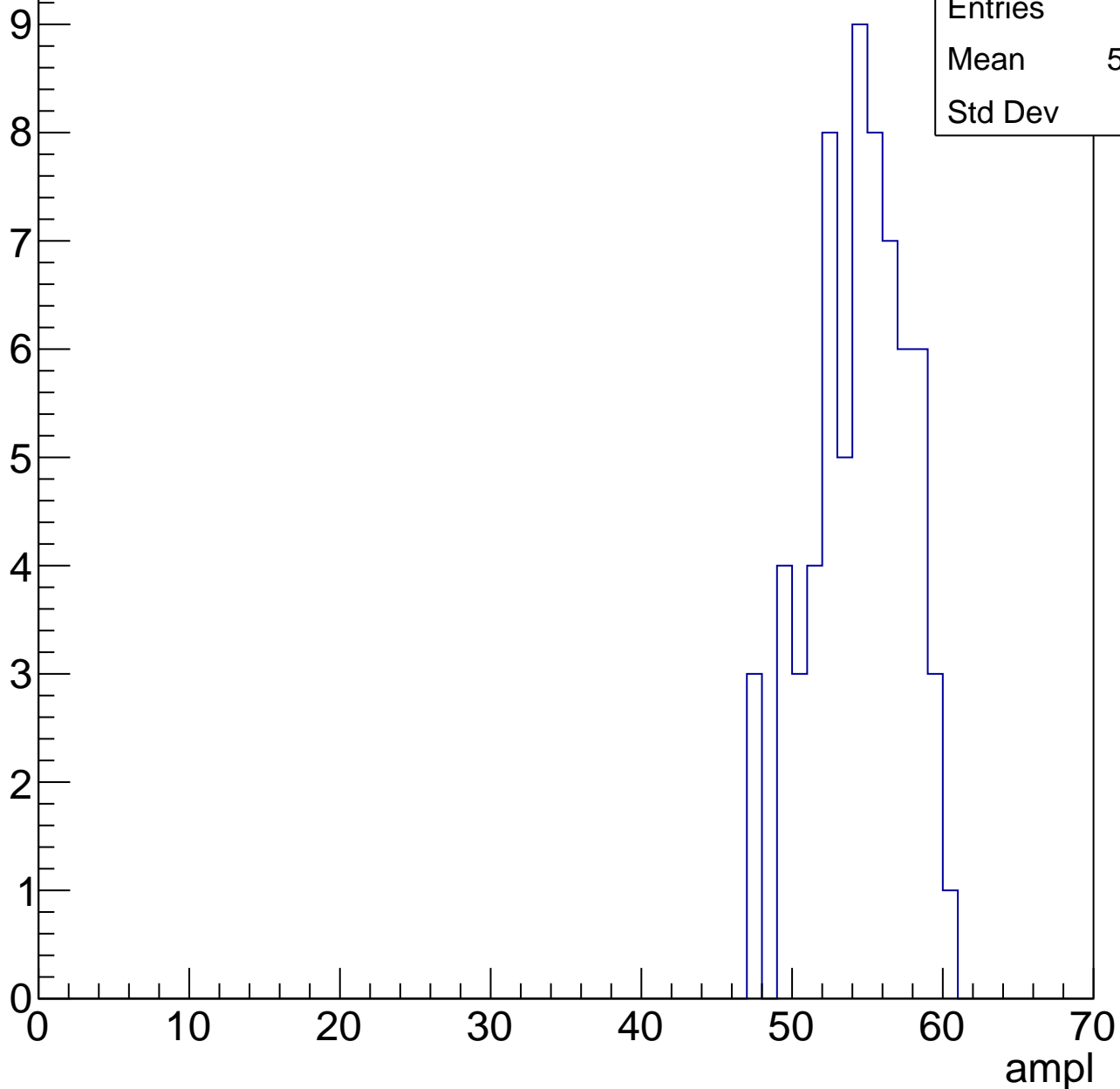
Entries	52
Mean	47.98
Std Dev	2.568



# B1L000S, U1-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

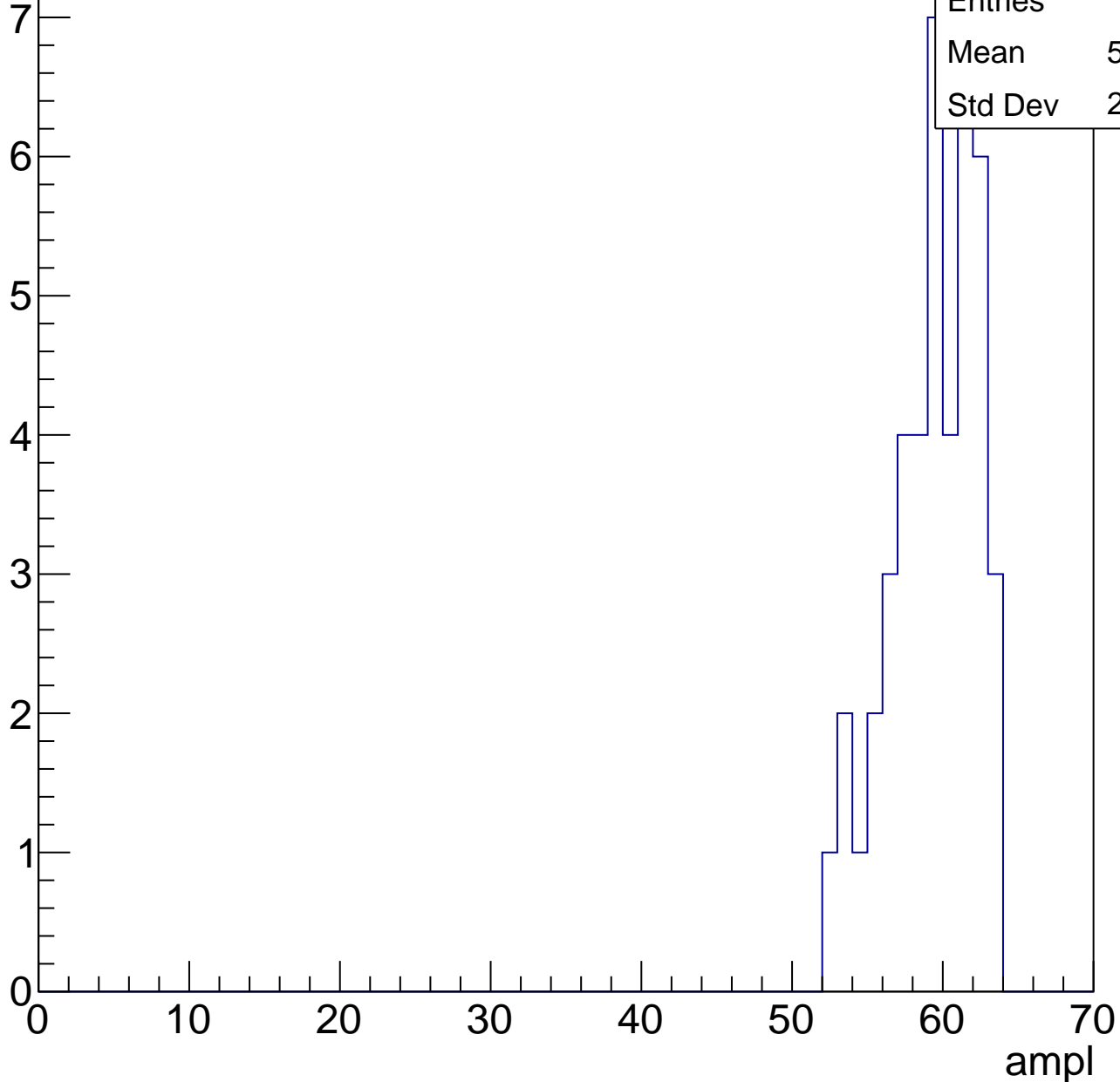
Entry



# B1L000S, U1-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

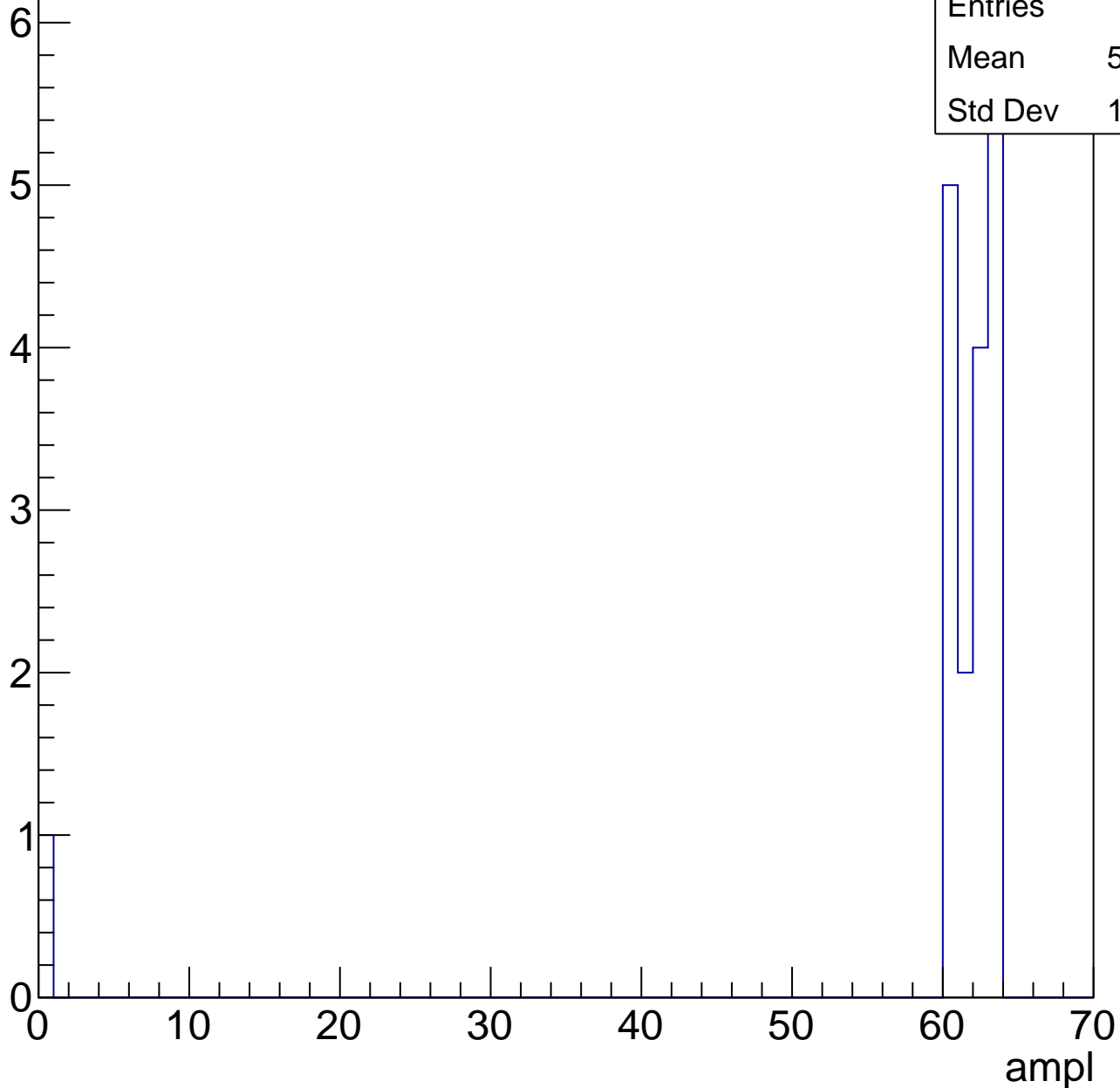


# B1L000S, U1-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.22
Std Dev	14.17

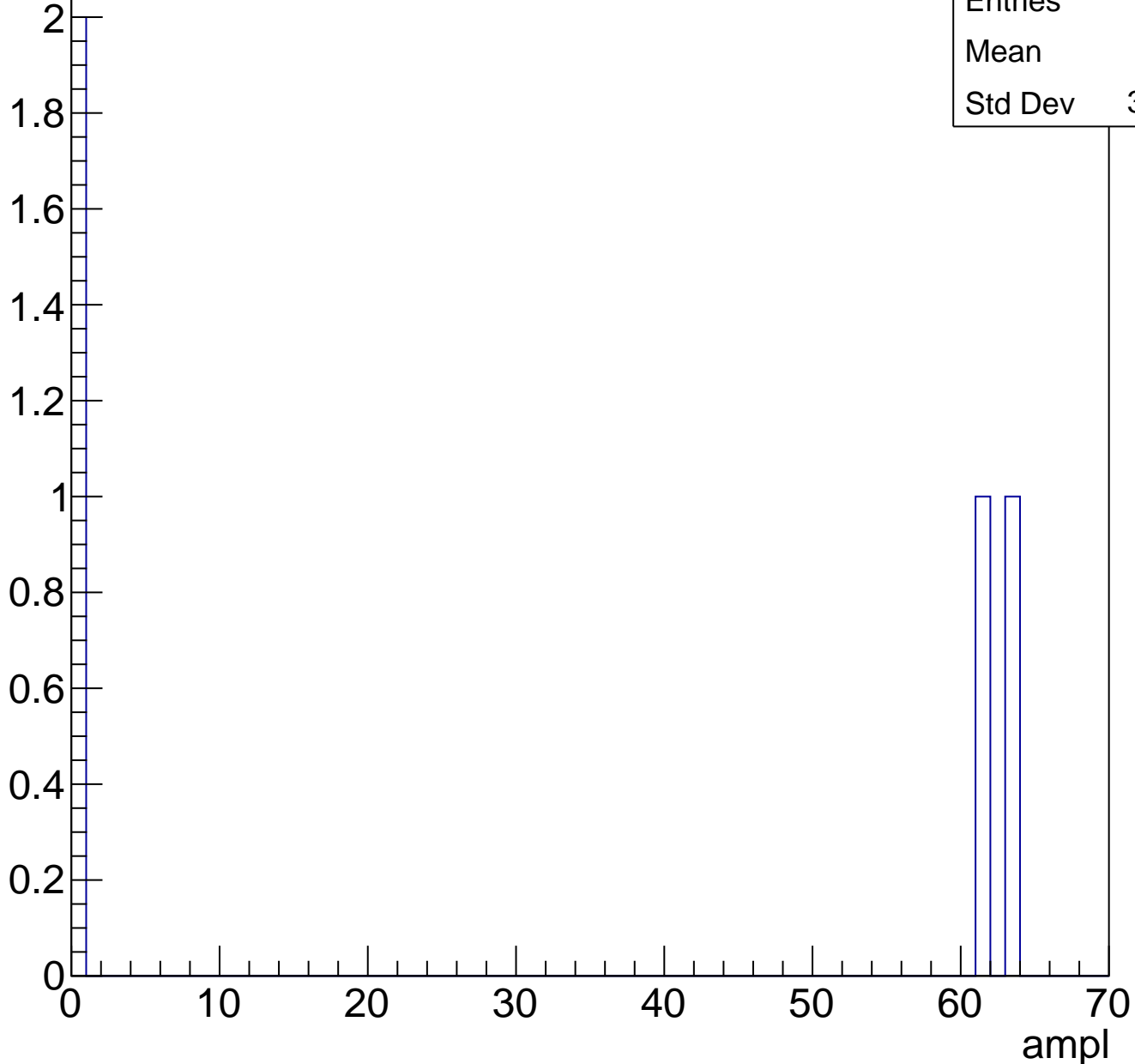




# B1L000S, U1-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	31
Std Dev	31.01

# B1L000S, U1-ch58, adc0

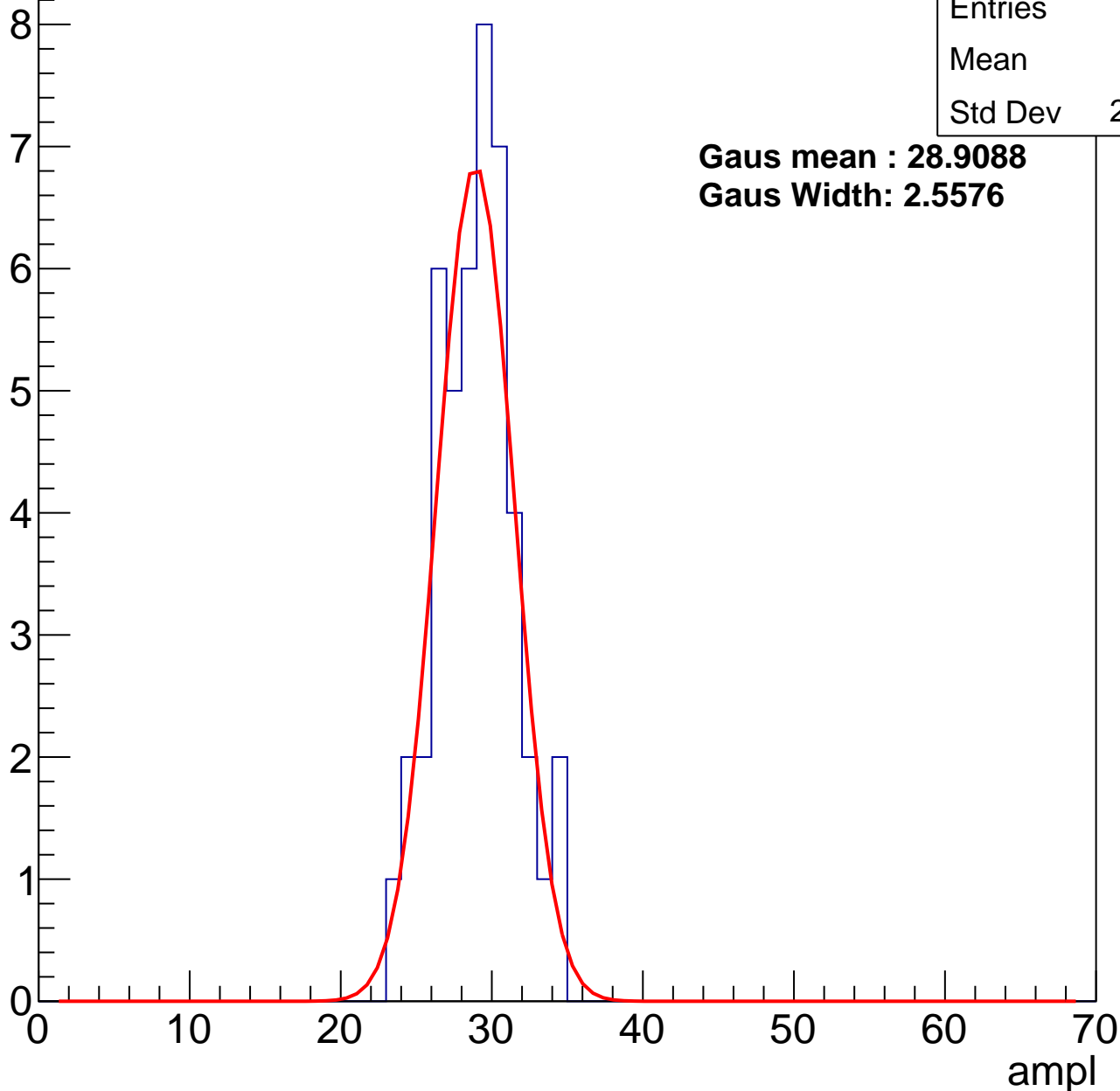
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	28.5
Std Dev	2.526

**Gaus mean : 28.9088**

**Gaus Width: 2.5576**



# B1L000S, U1-ch58, adc1

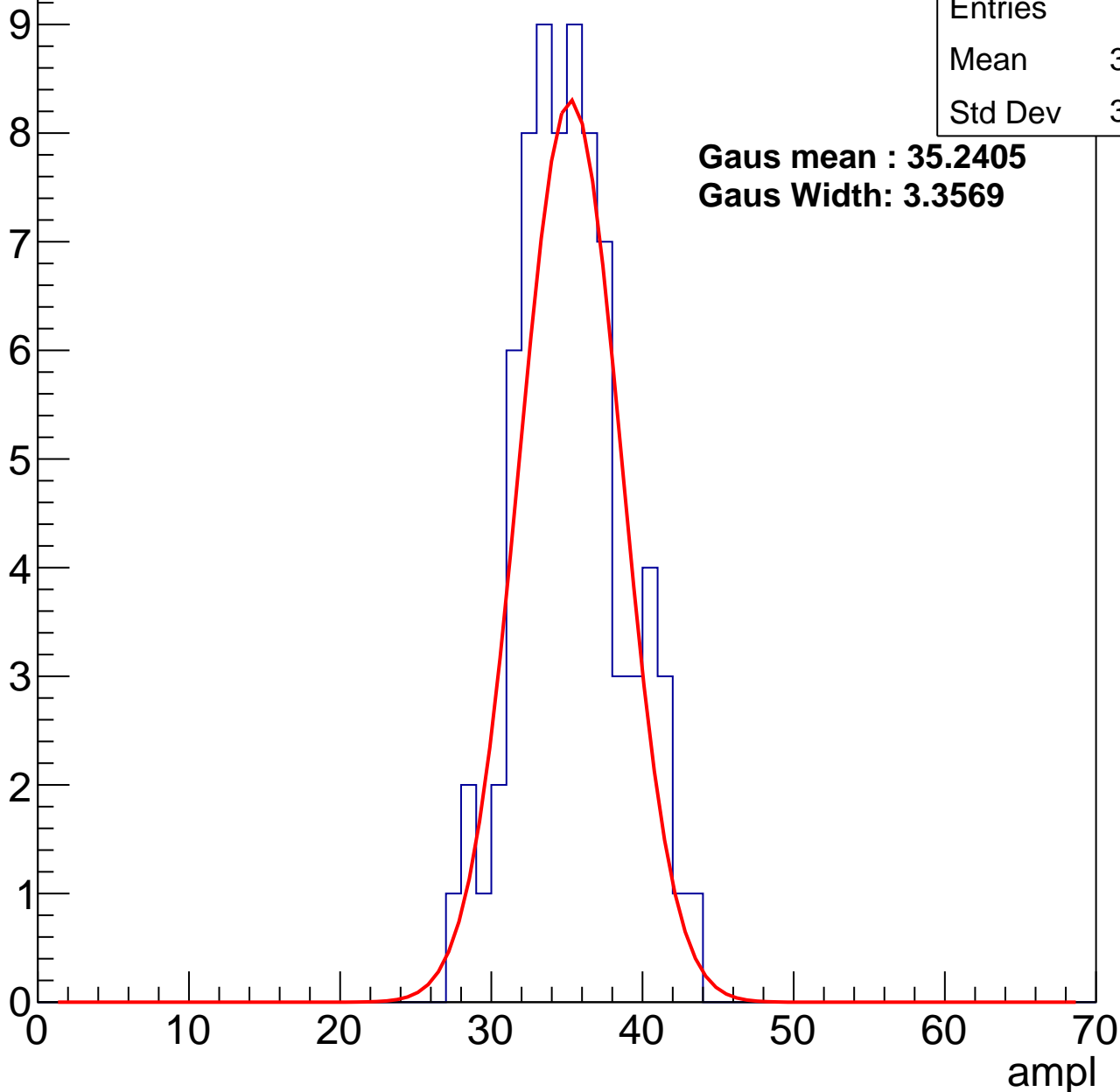
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	34.79
Std Dev	3.419

**Gaus mean : 35.2405**

**Gaus Width: 3.3569**



# B1L000S, U1-ch58, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 40.52

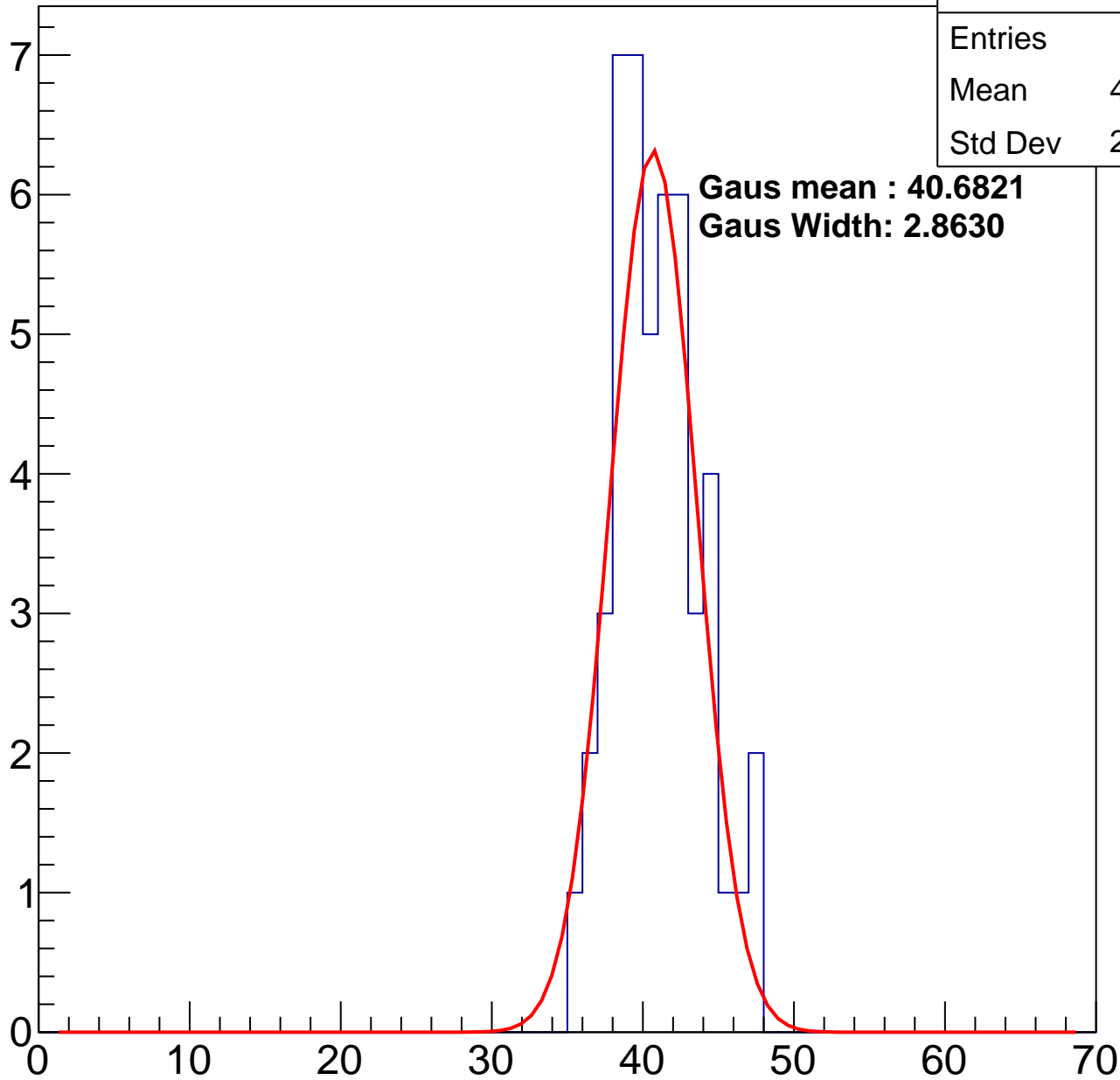
Std Dev 2.836

**Gaus mean : 40.6821**

**Gaus Width: 2.8630**

0 10 20 30 40 50 60 70

ampl

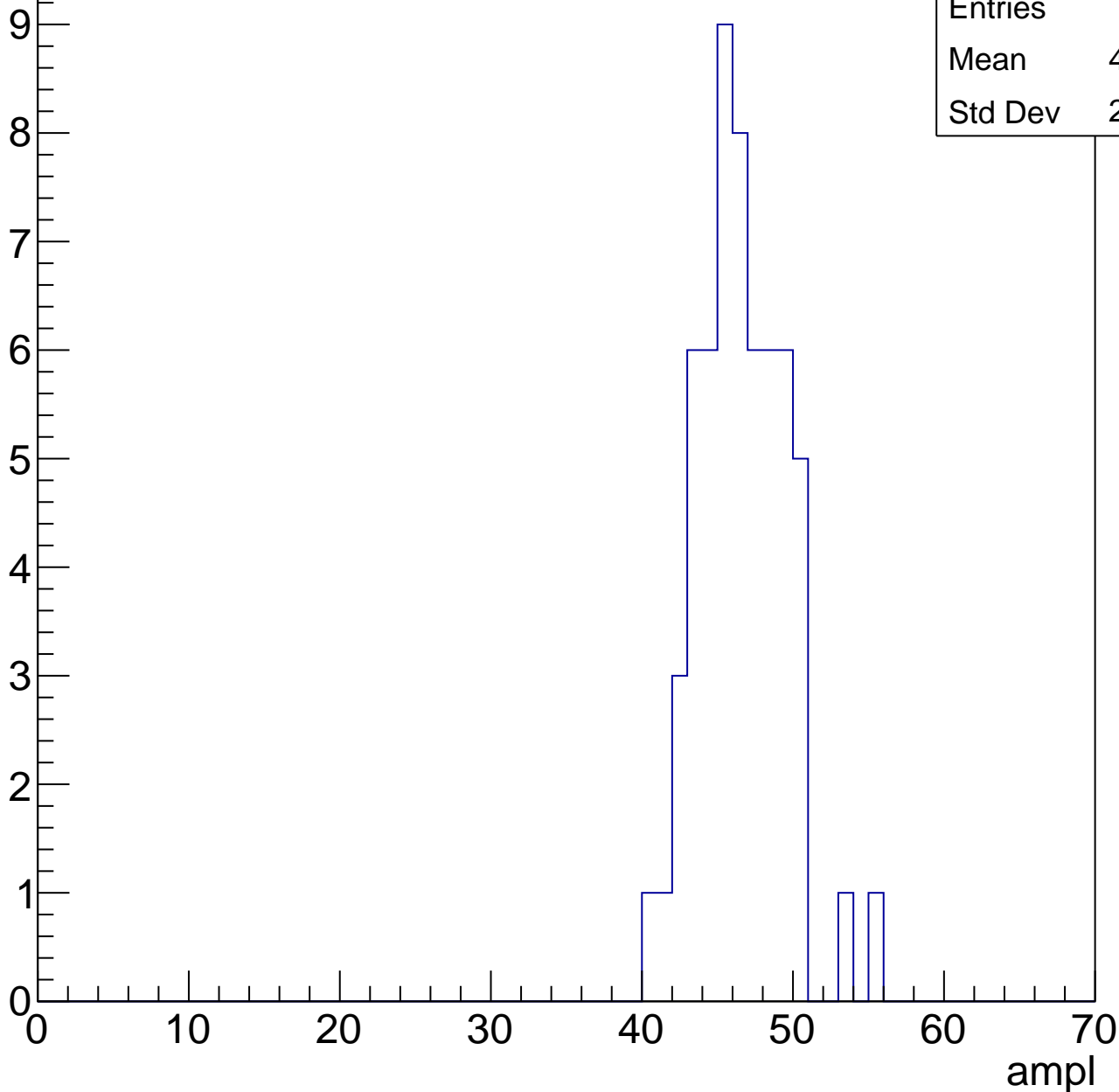


# B1L000S, U1-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	46.17
Std Dev	2.877

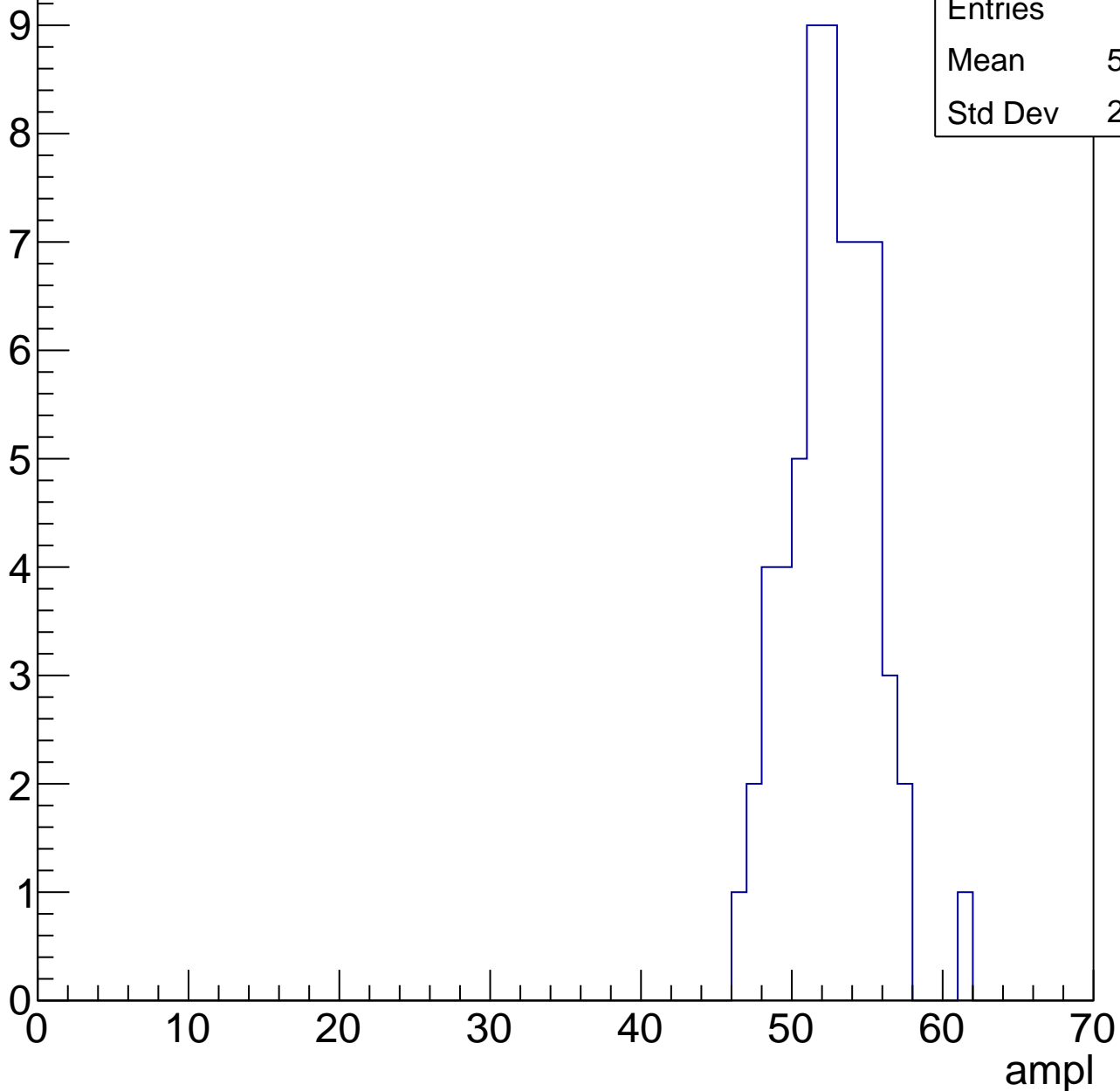


# B1L000S, U1-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

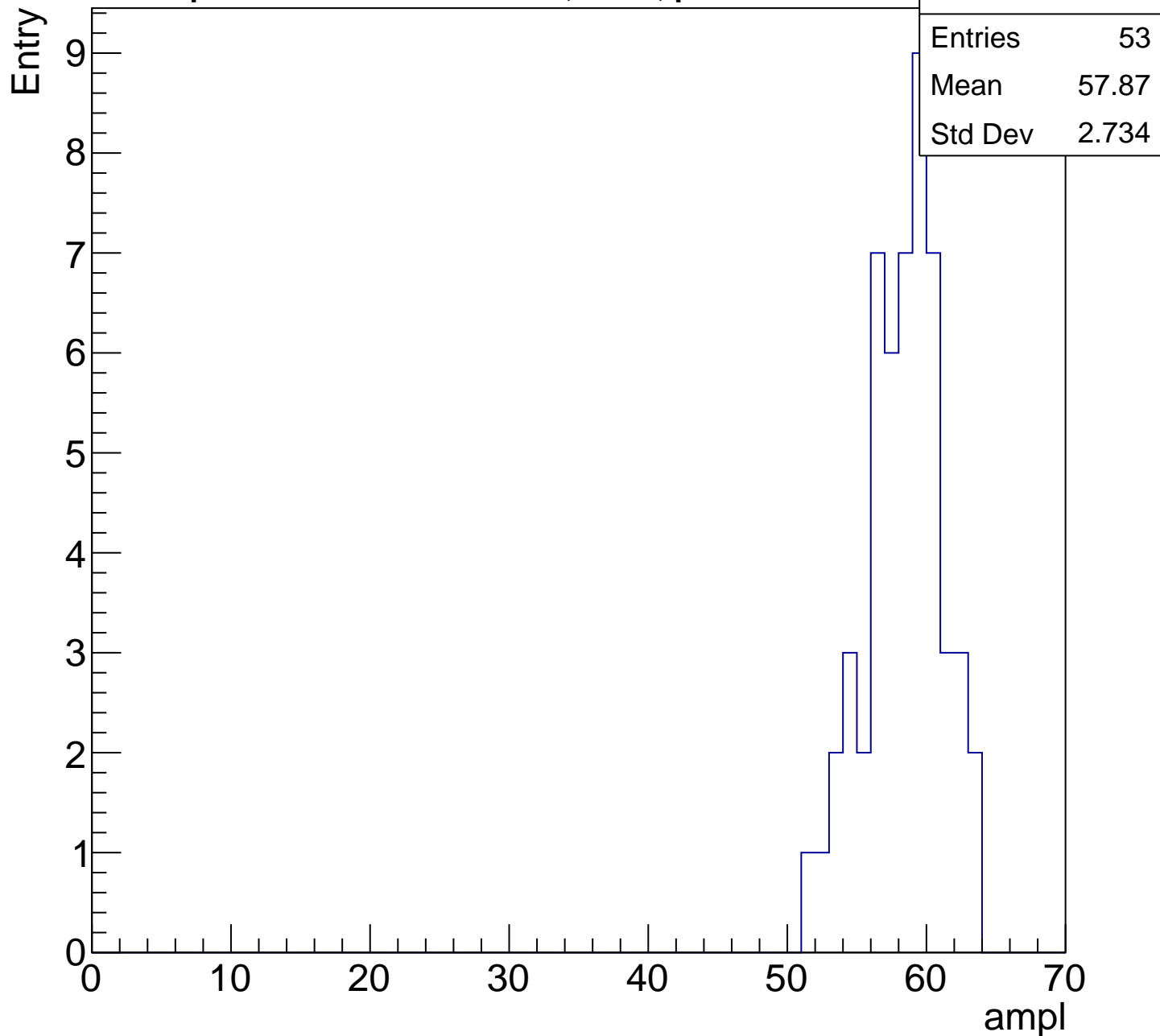
Entry

Entries	61
Mean	52.16
Std Dev	2.835



# B1L000S, U1-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

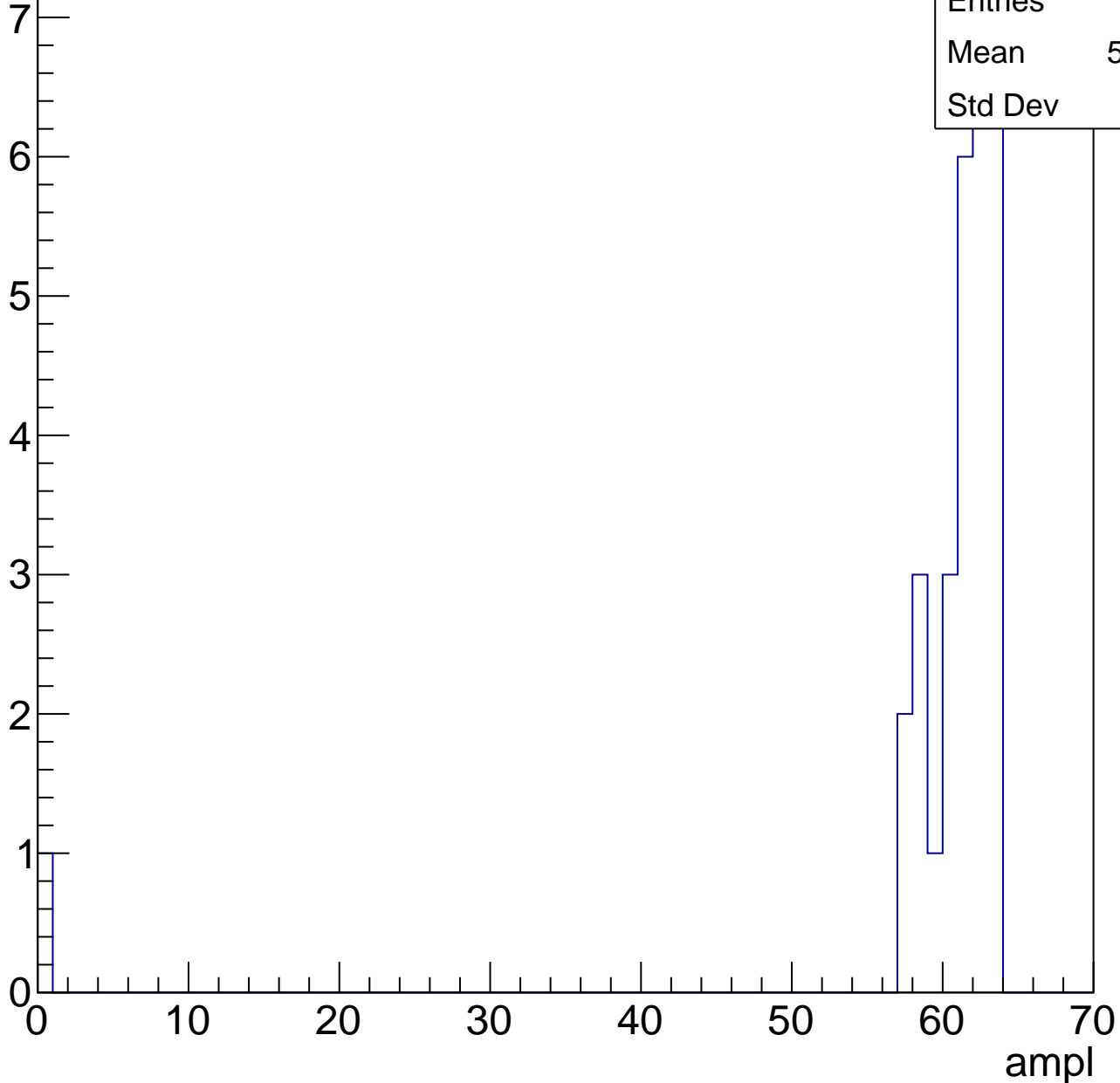


# B1L000S, U1-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	30
Mean	58.93
Std Dev	11.1





# B1L000S, U1-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch59, adc0

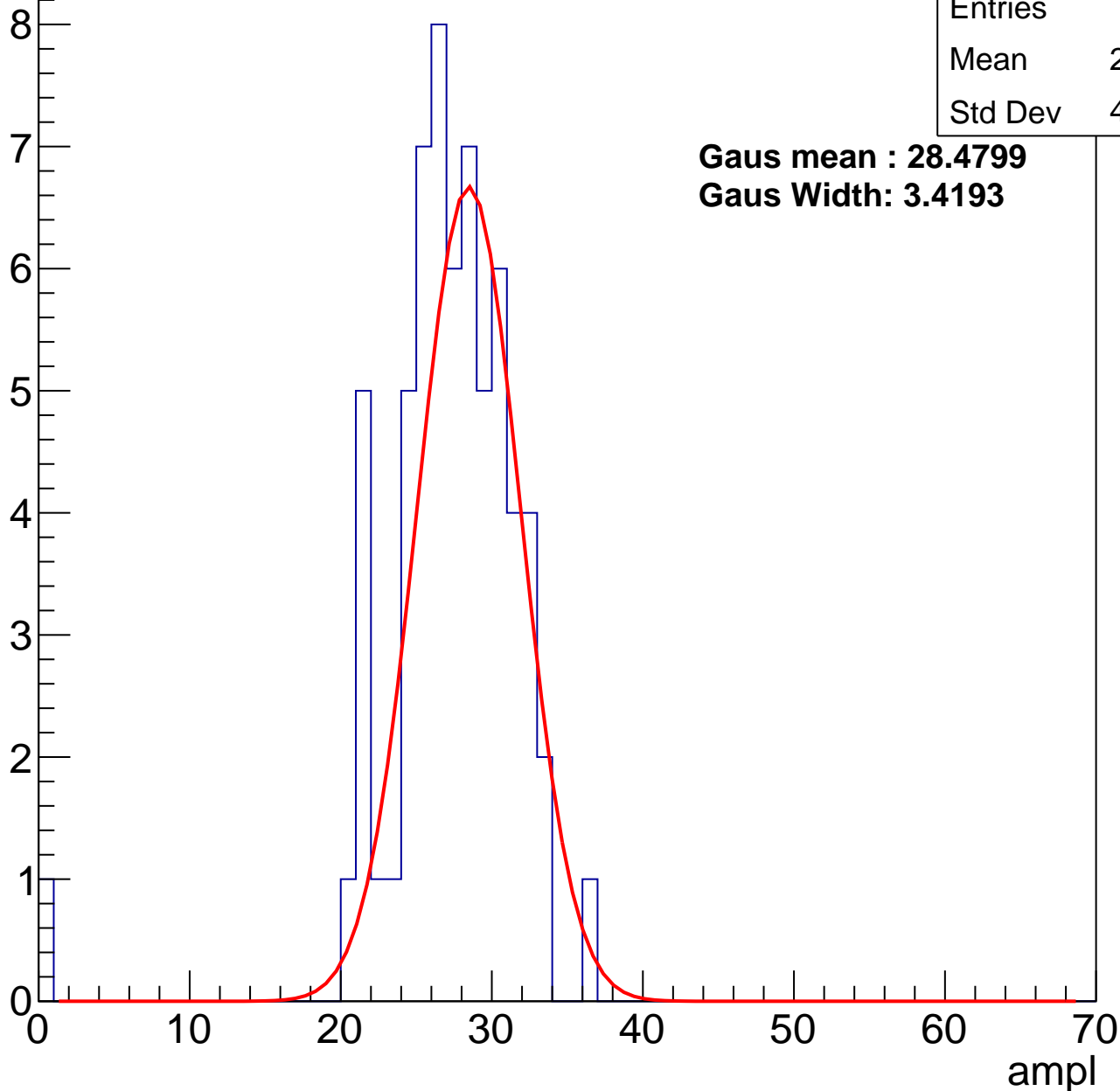
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	26.72
Std Dev	4.797

**Gaus mean : 28.4799**

**Gaus Width: 3.4193**



# B1L000S, U1-ch59, adc1

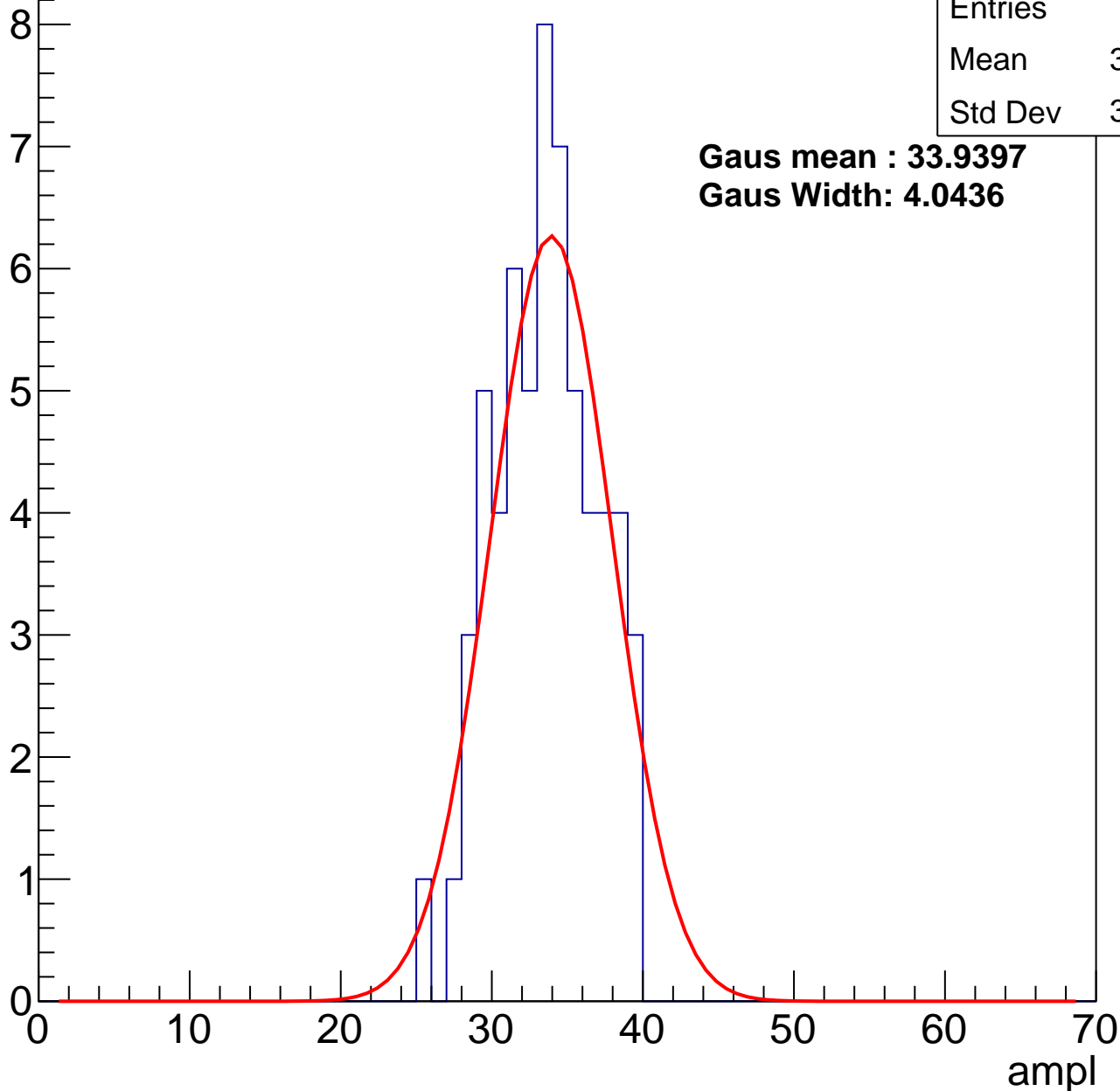
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	33.08
Std Dev	3.303

**Gaus mean : 33.9397**

**Gaus Width: 4.0436**



# B1L000S, U1-ch59, adc2

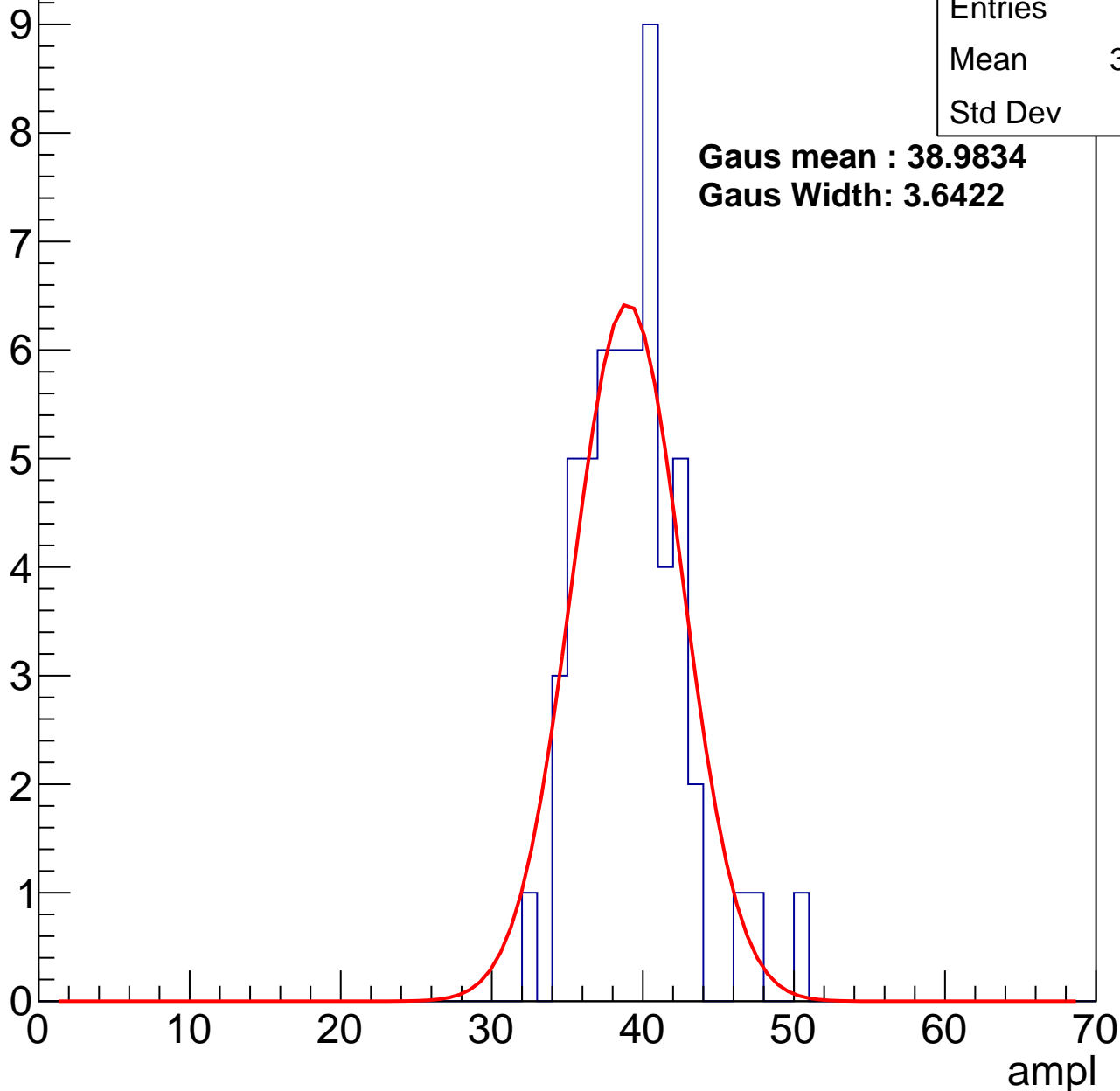
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	38.84
Std Dev	3.34

**Gaus mean : 38.9834**

**Gaus Width: 3.6422**

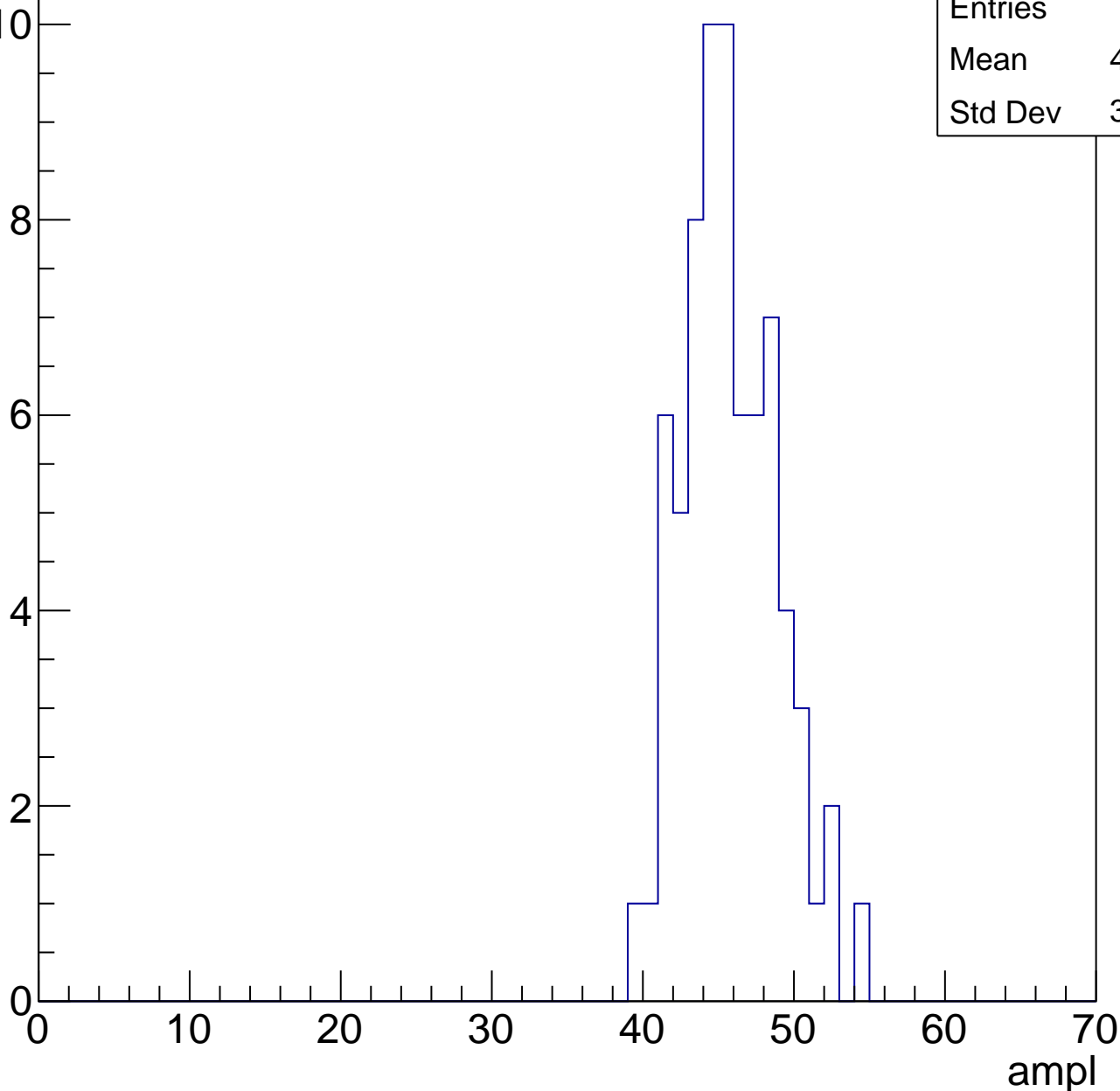


# B1L000S, U1-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	45.32
Std Dev	3.103

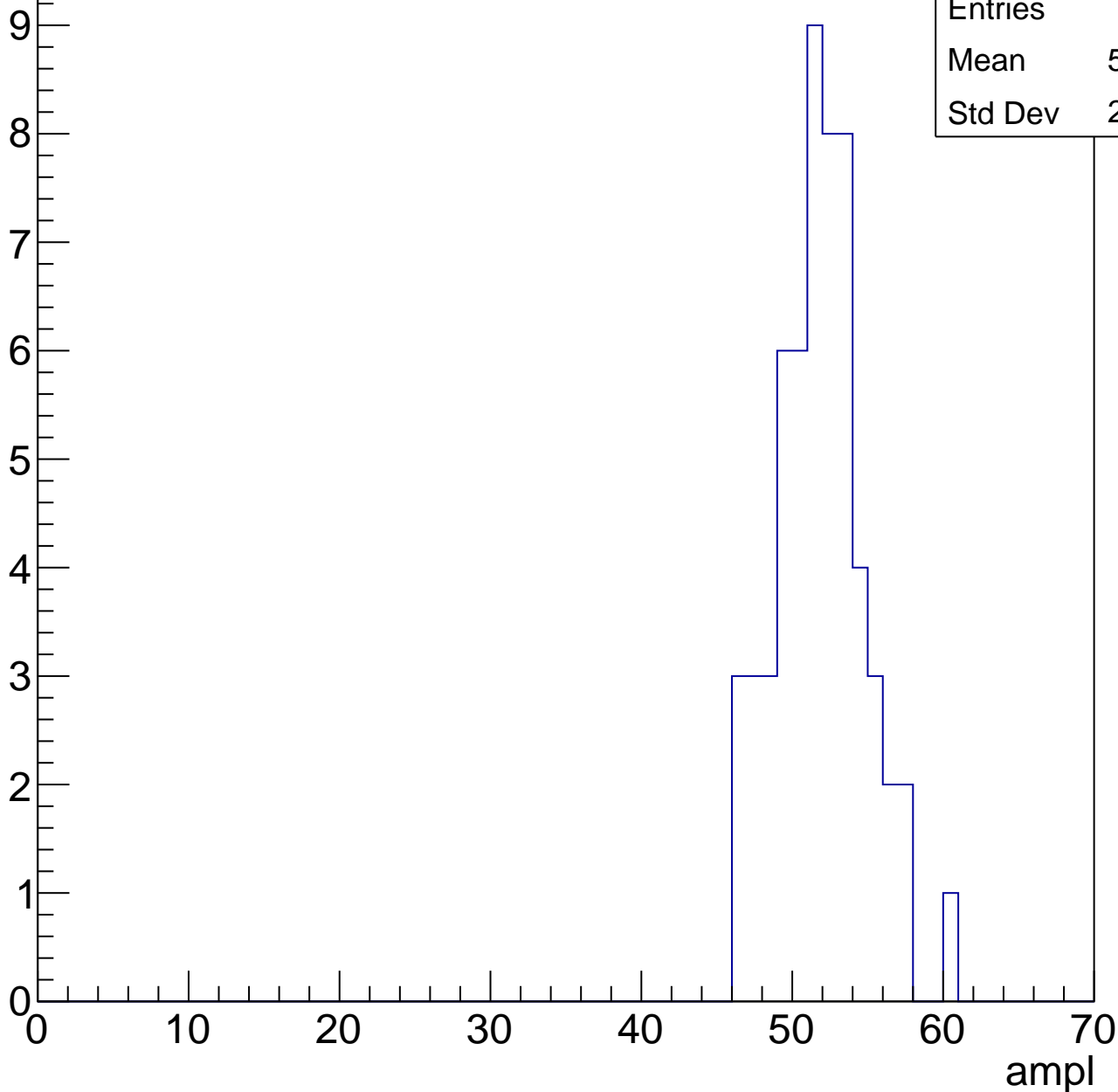


# B1L000S, U1-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	51.43
Std Dev	2.925



# B1L000S, U1-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 58

Mean 57.48

Std Dev 2.38

8

6

4

2

0

0

10

20

30

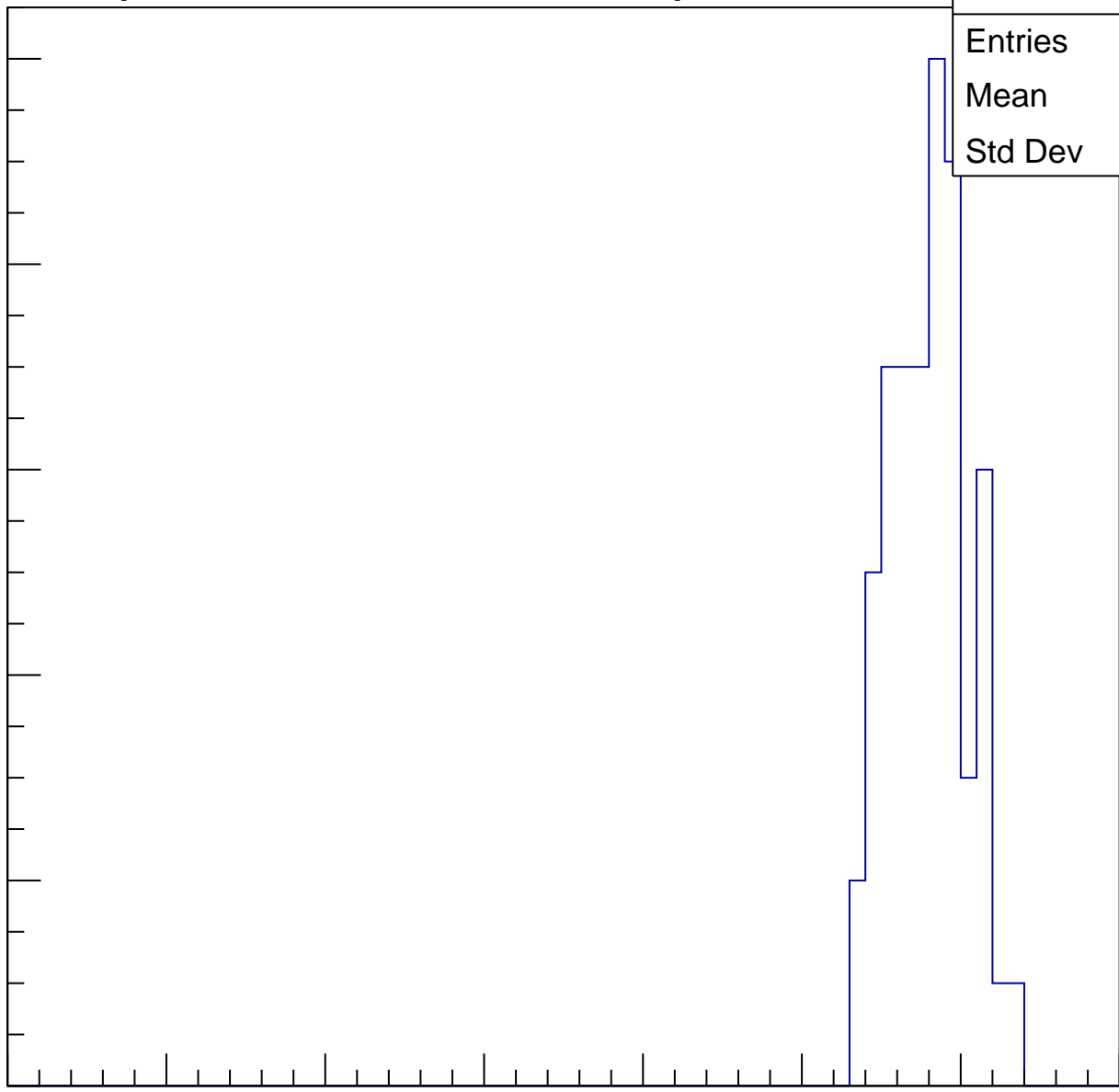
40

50

60

70

ampl

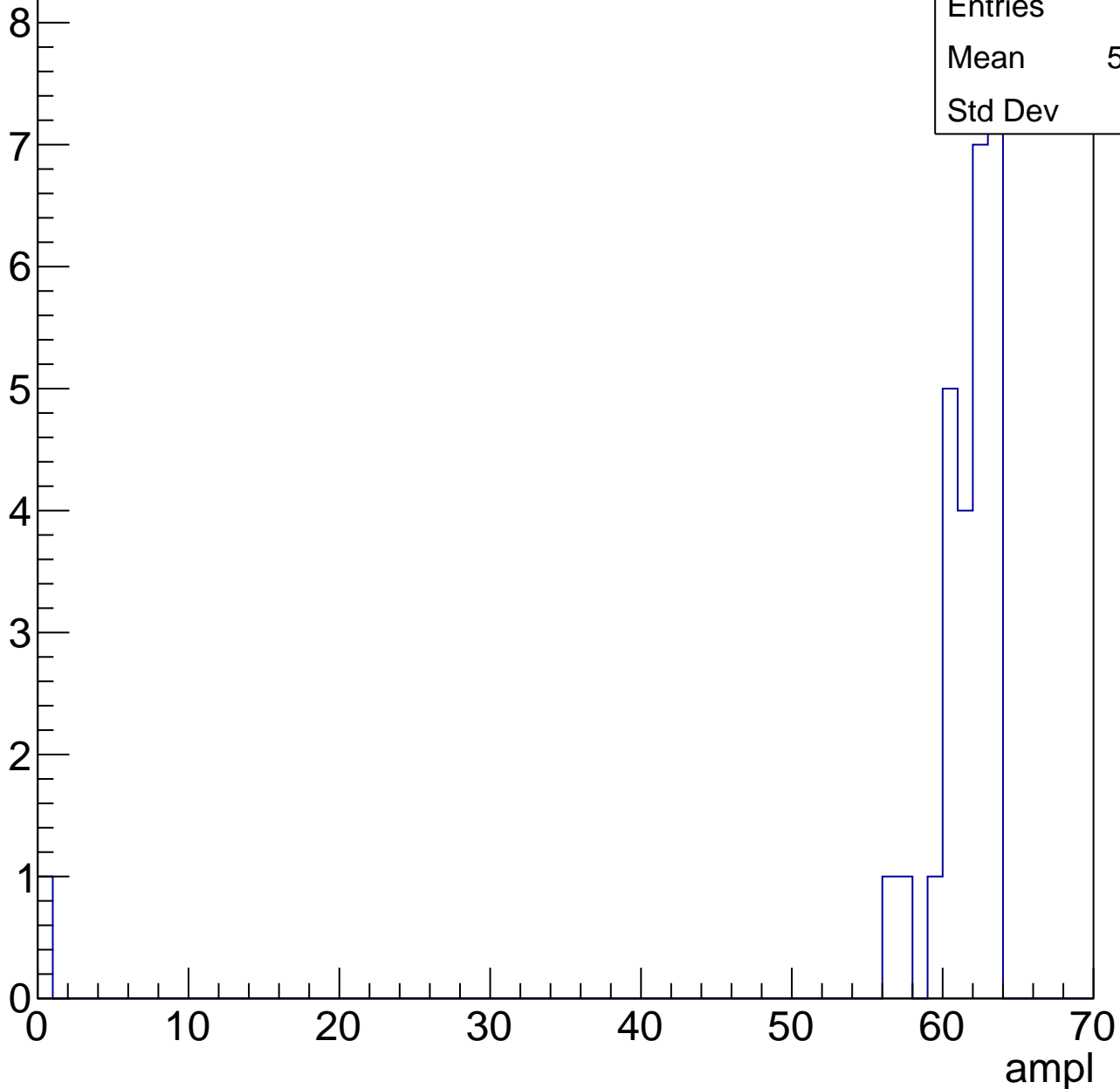


# B1L000S, U1-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	59.07
Std Dev	11.5

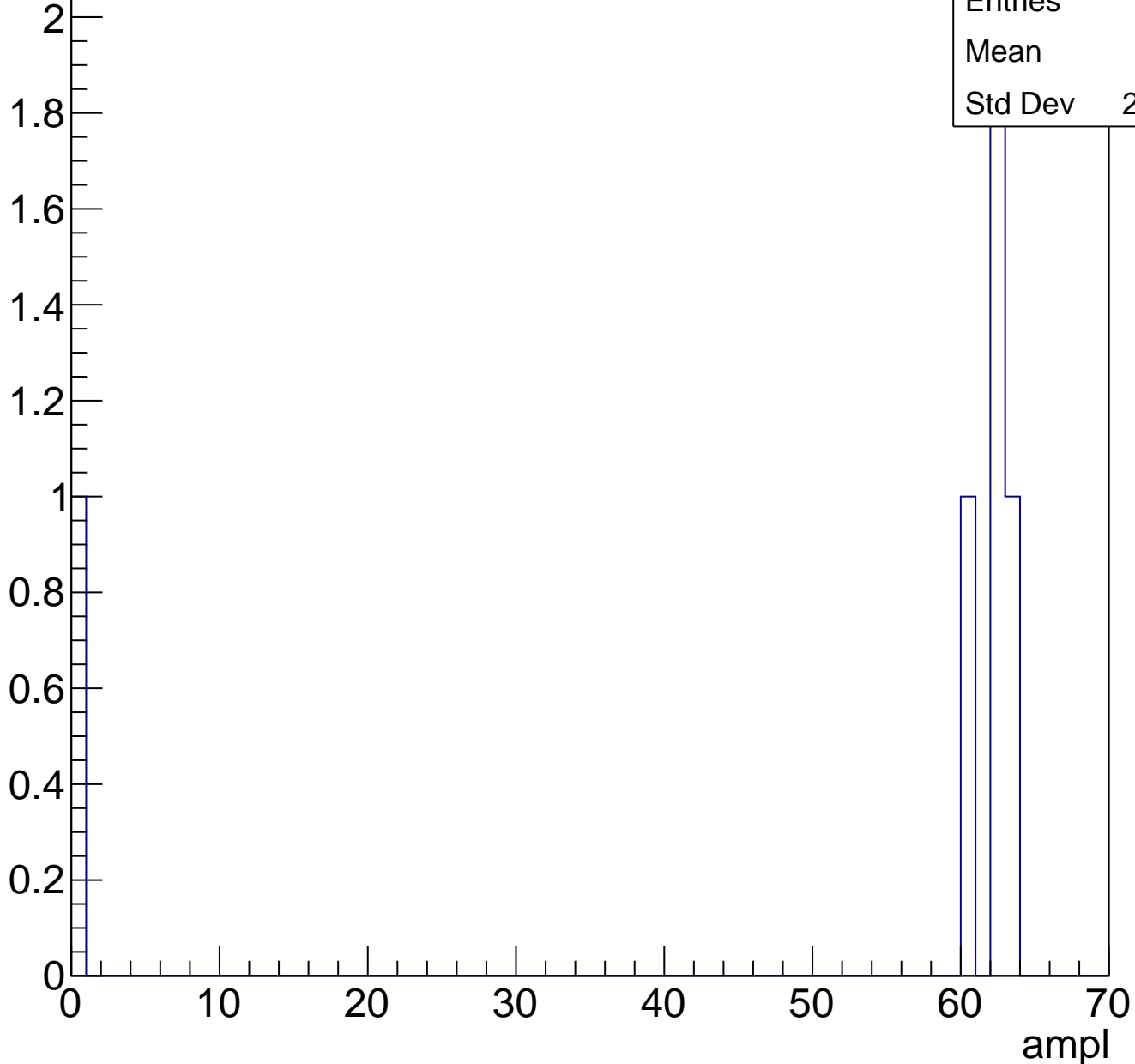




# B1L000S, U1-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	49.4
Std Dev	24.72

# B1L000S, U1-ch60, adc0

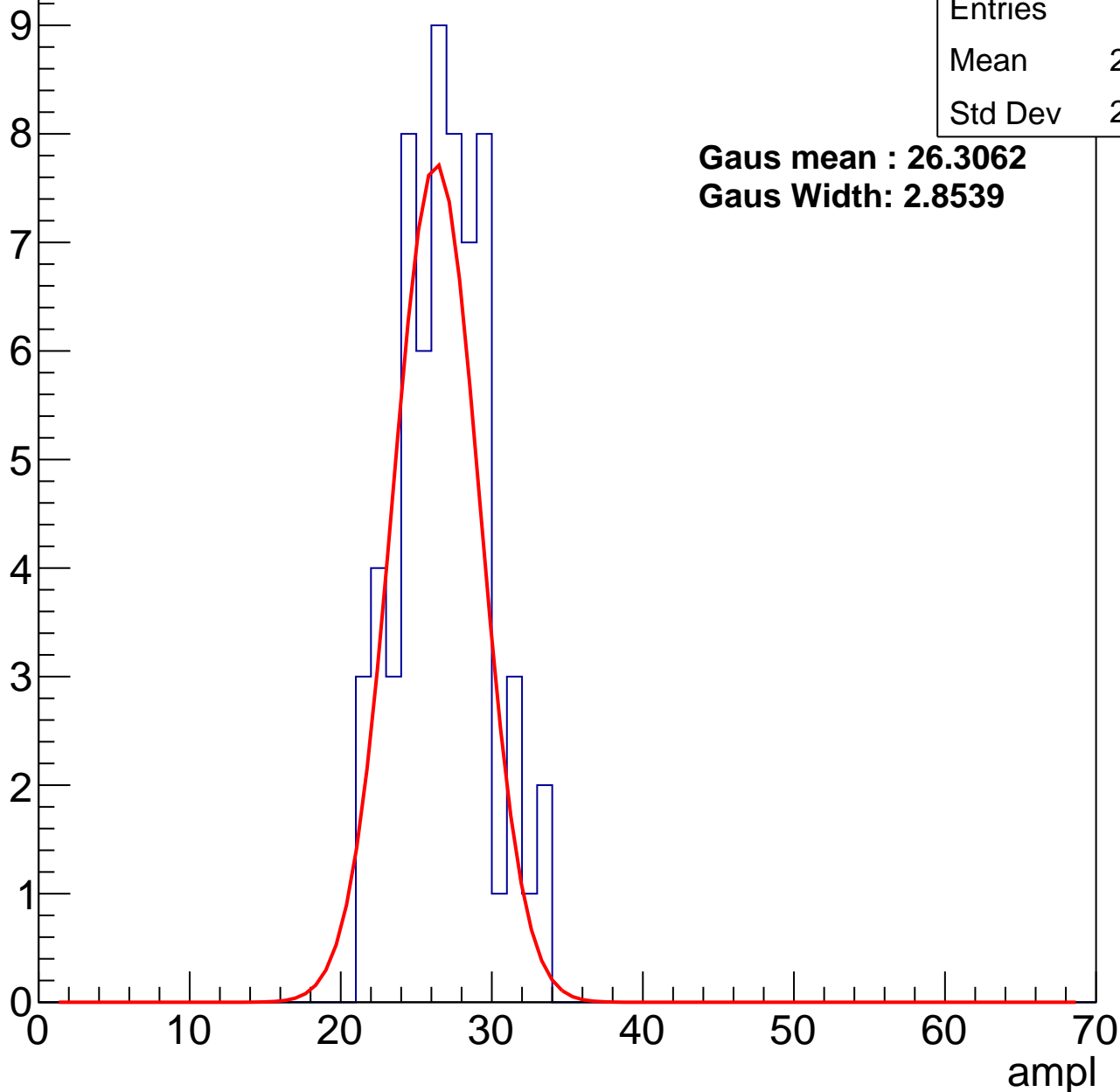
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	26.37
Std Dev	2.897

**Gaus mean : 26.3062**

**Gaus Width: 2.8539**



# B1L000S, U1-ch60, adc1

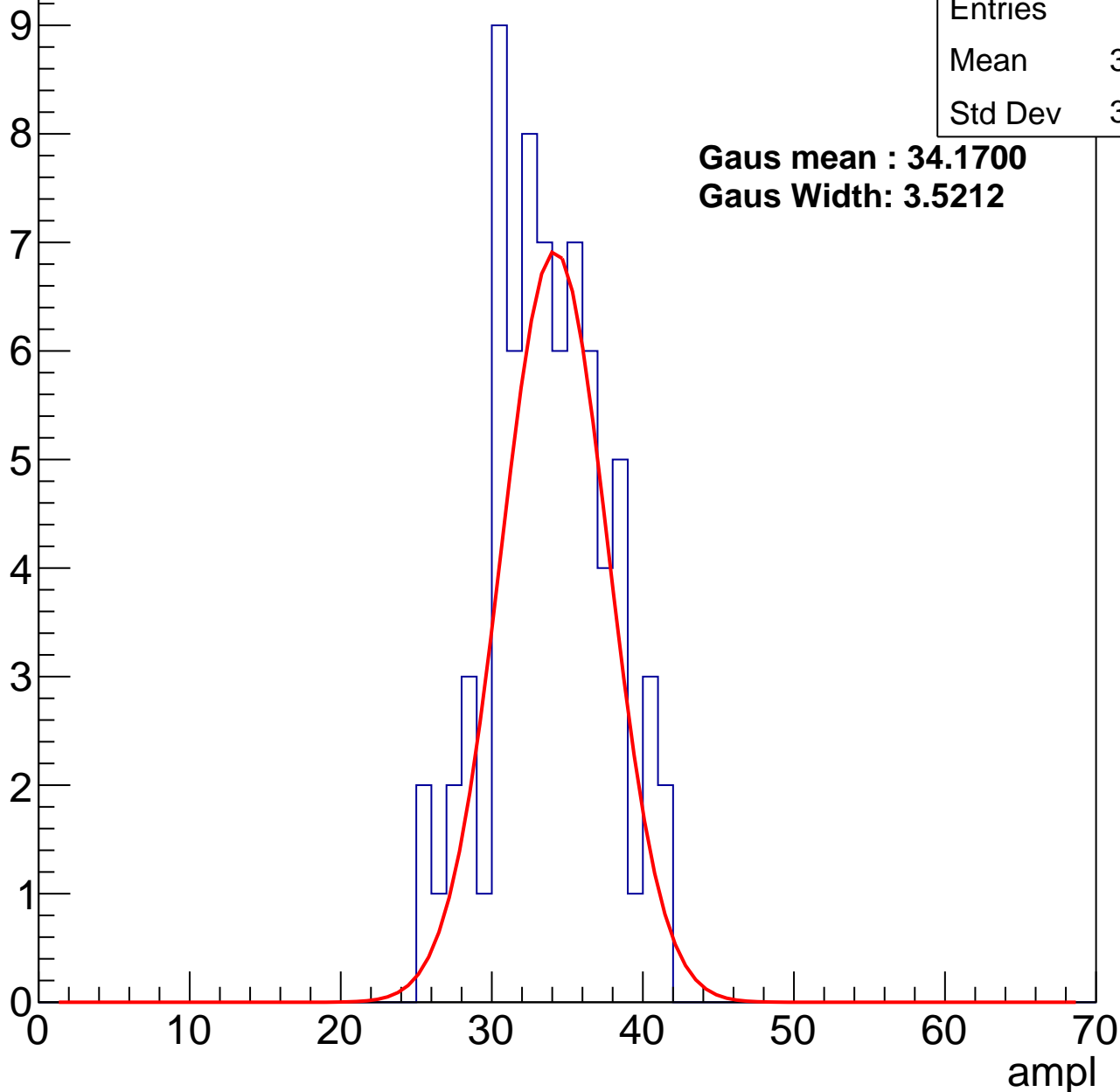
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	33.29
Std Dev	3.765

**Gaus mean : 34.1700**

**Gaus Width: 3.5212**



# B1L000S, U1-ch60, adc2

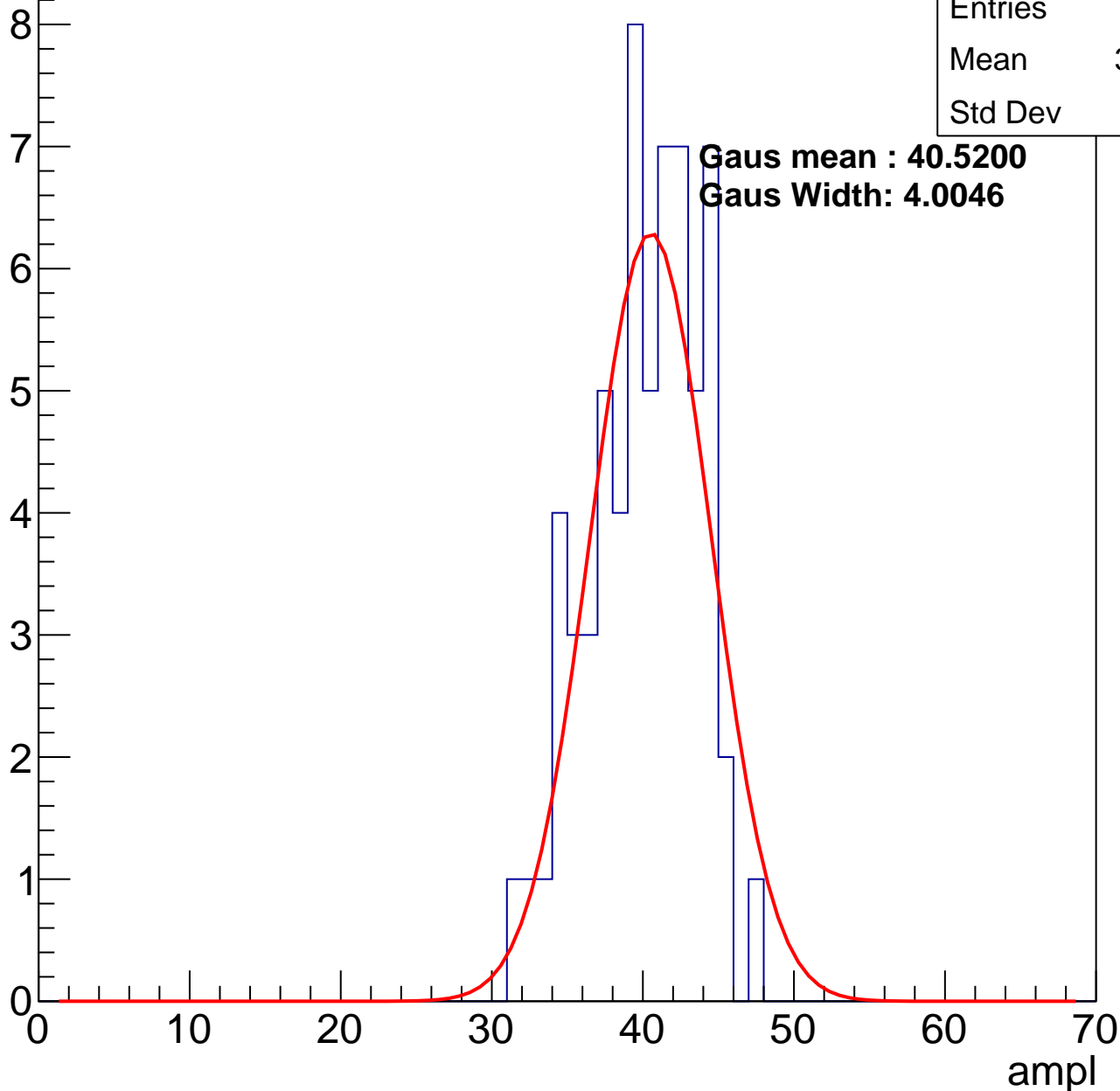
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	39.61
Std Dev	3.56

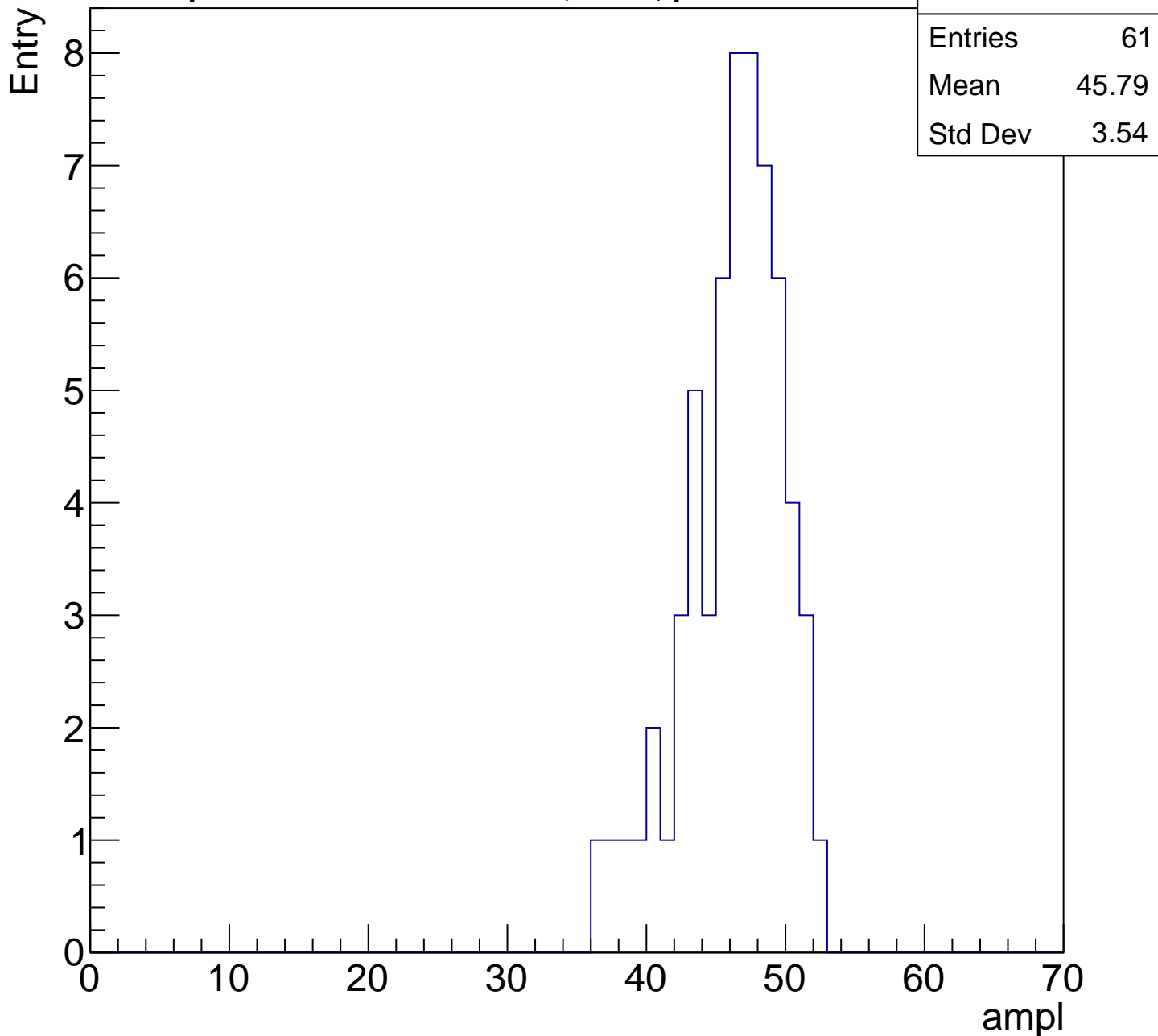
**Gaus mean : 40.5200**

**Gaus Width: 4.0046**



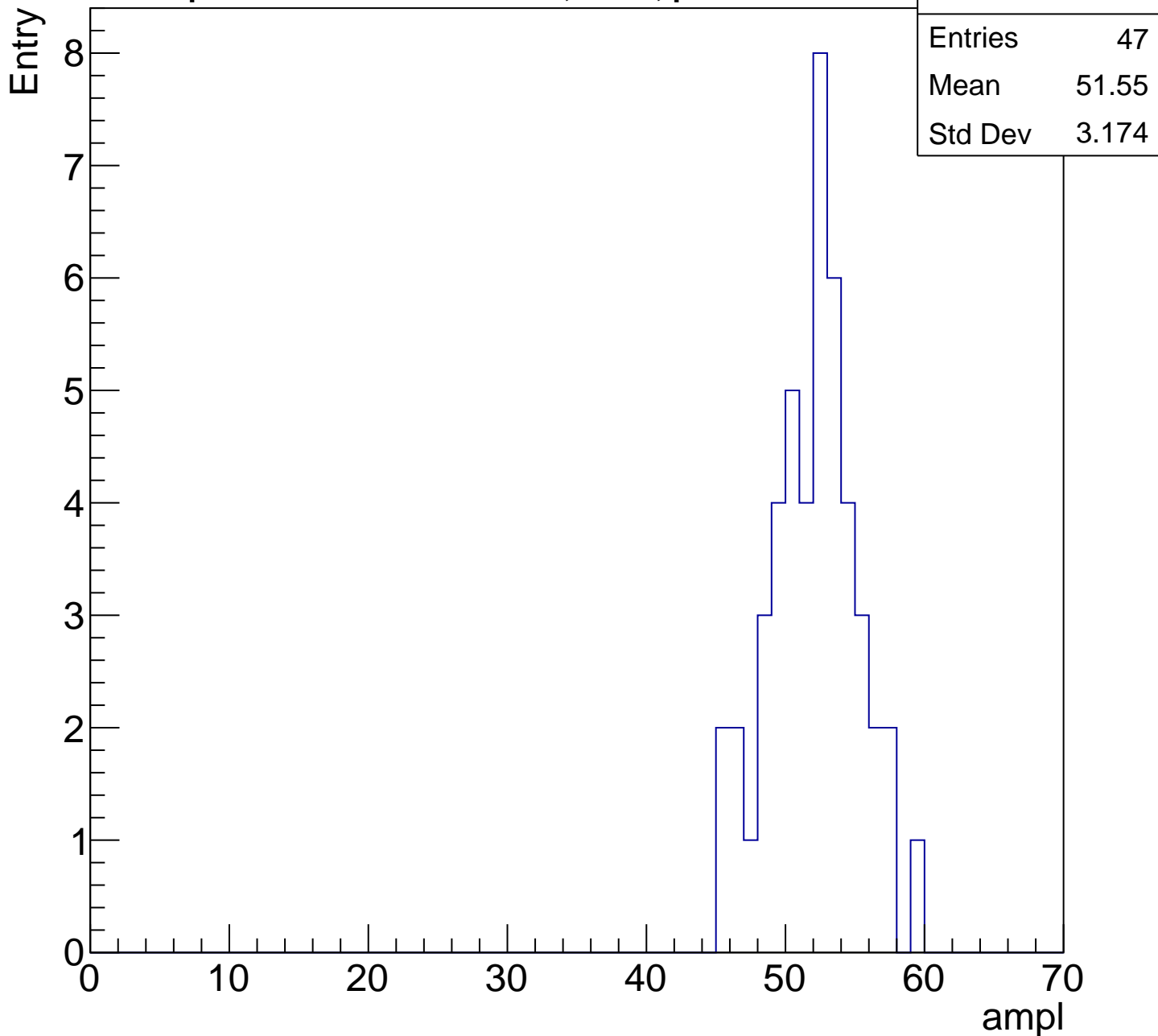
# B1L000S, U1-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

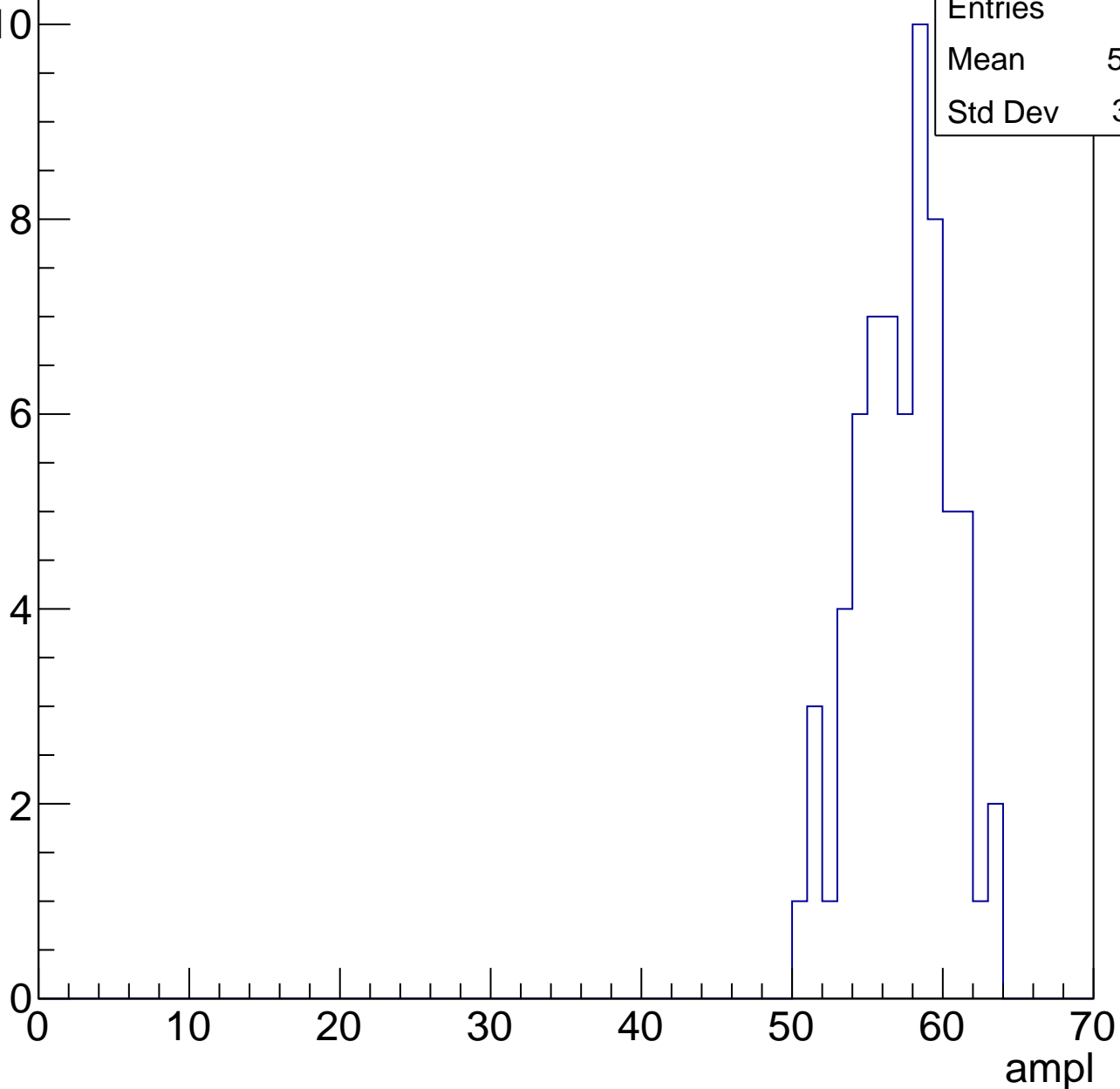


# B1L000S, U1-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	56.89
Std Dev	3.011

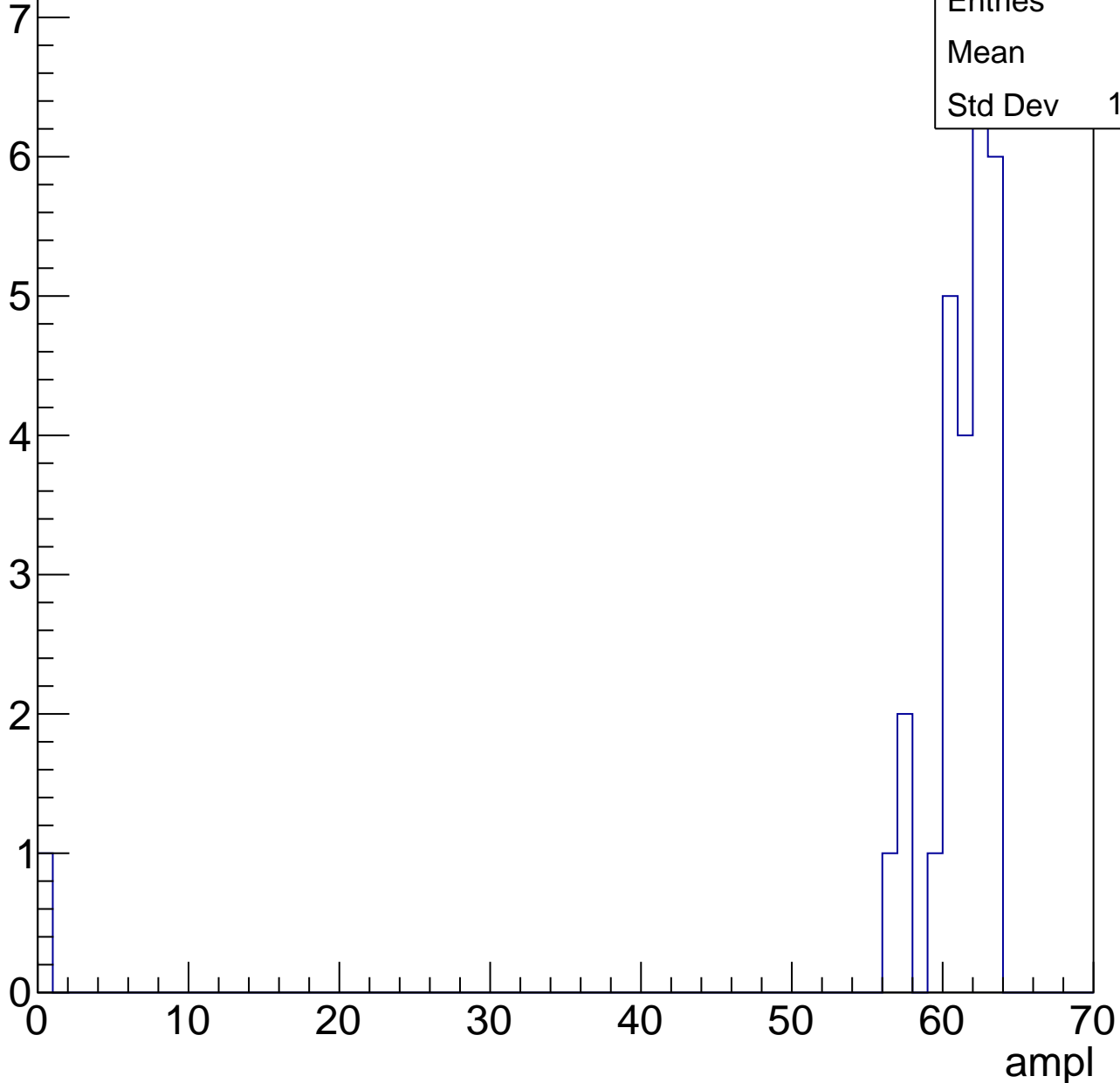


# B1L000S, U1-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	58.7
Std Dev	11.67

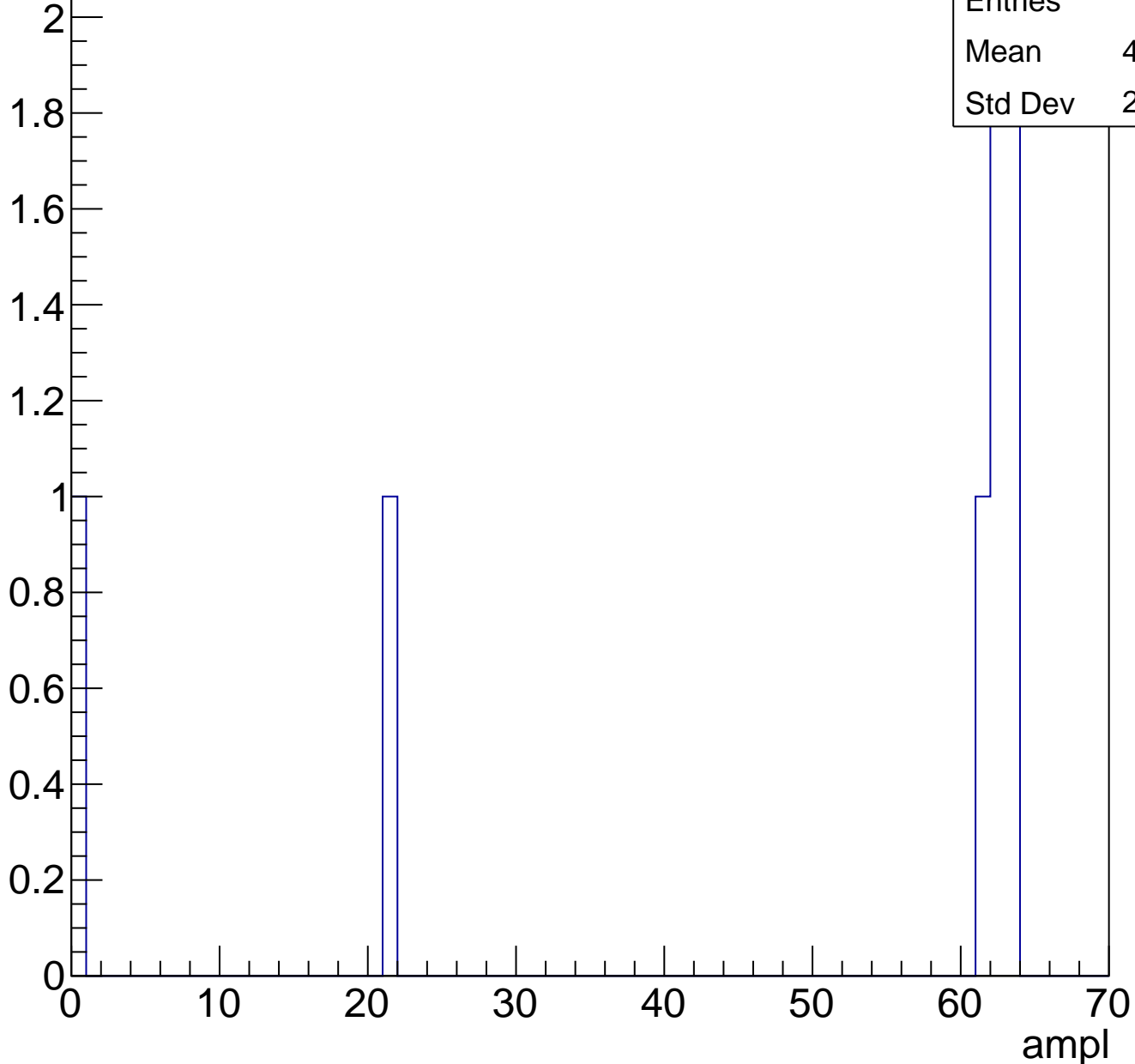




# B1L000S, U1-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	7
Mean	47.43
Std Dev	24.03

# B1L000S, U1-ch61, adc0

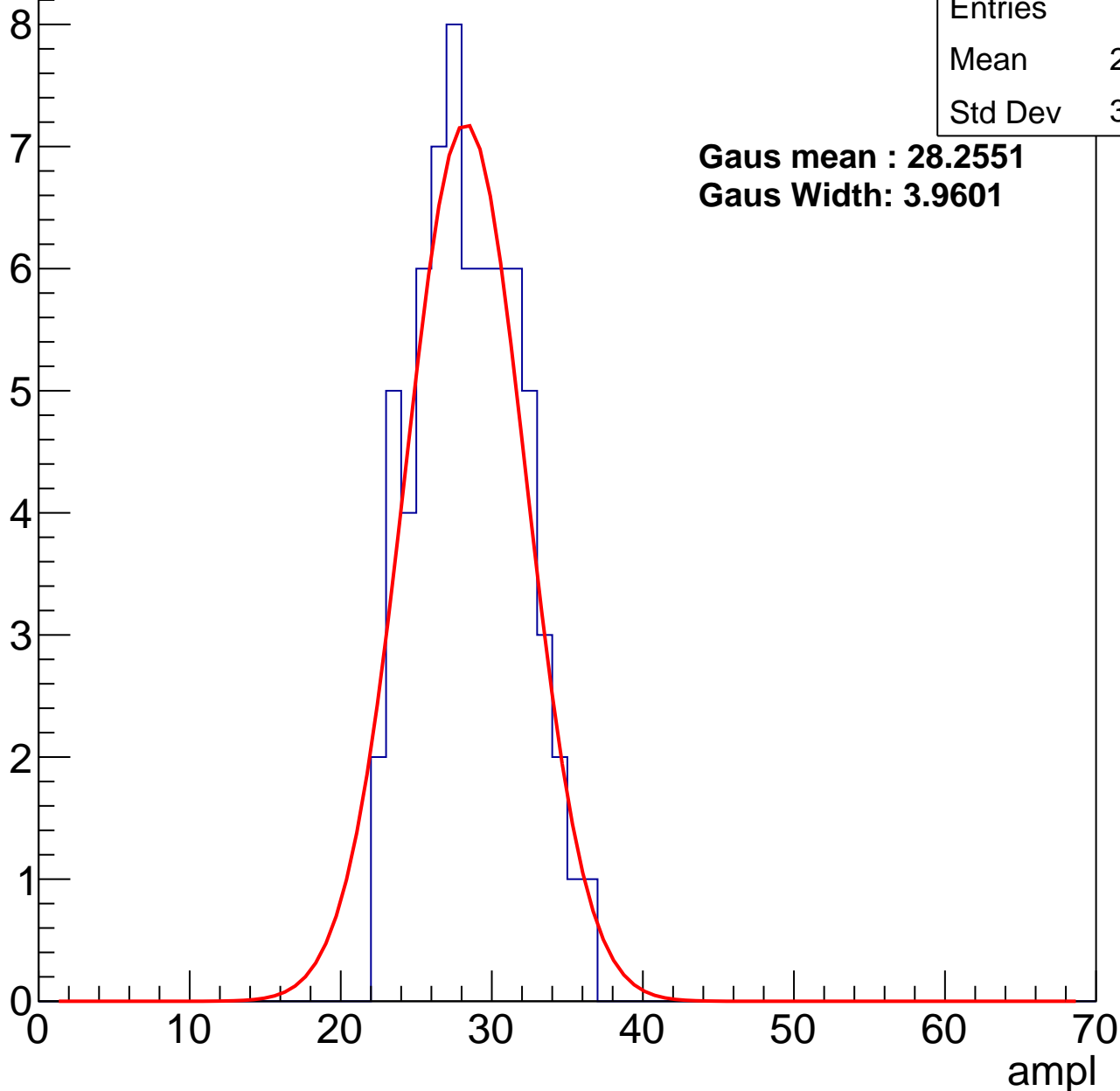
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.07
Std Dev	3.375

**Gaus mean : 28.2551**

**Gaus Width: 3.9601**



# B1L000S, U1-ch61, adc1

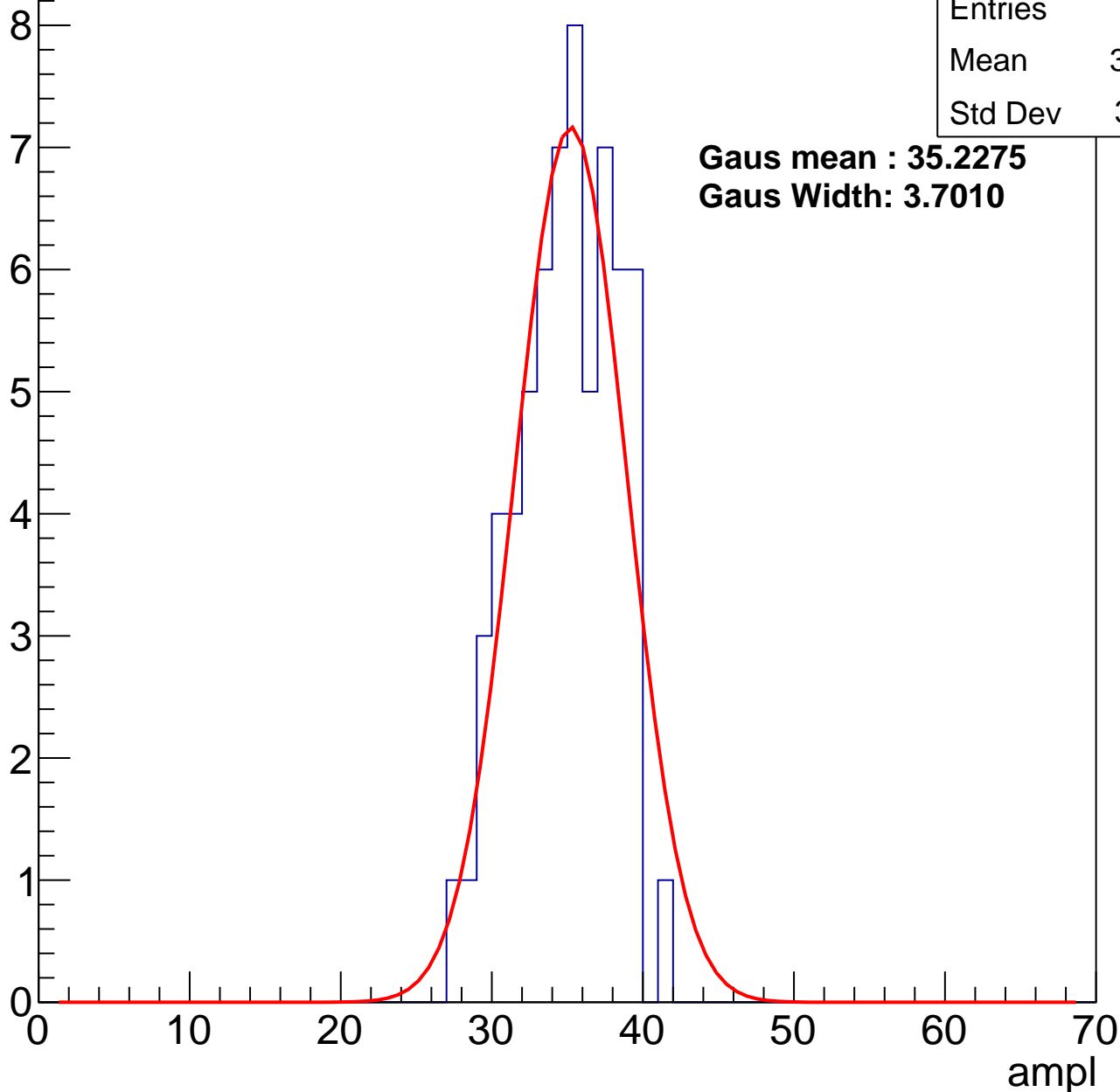
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.44
Std Dev	3.211

**Gaus mean : 35.2275**

**Gaus Width: 3.7010**



# B1L000S, U1-ch61, adc2

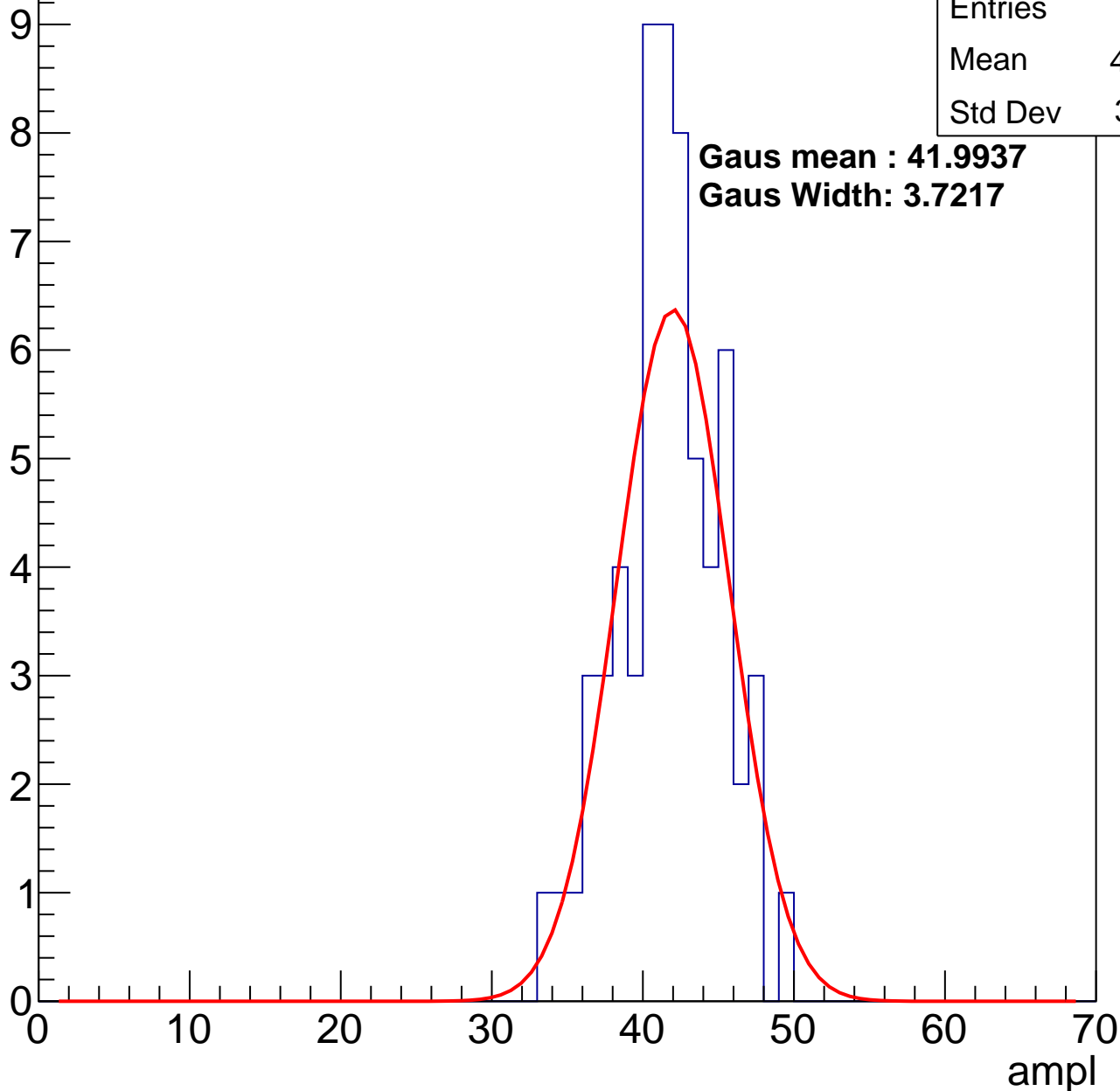
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.24
Std Dev	3.351

**Gaus mean : 41.9937**

**Gaus Width: 3.7217**

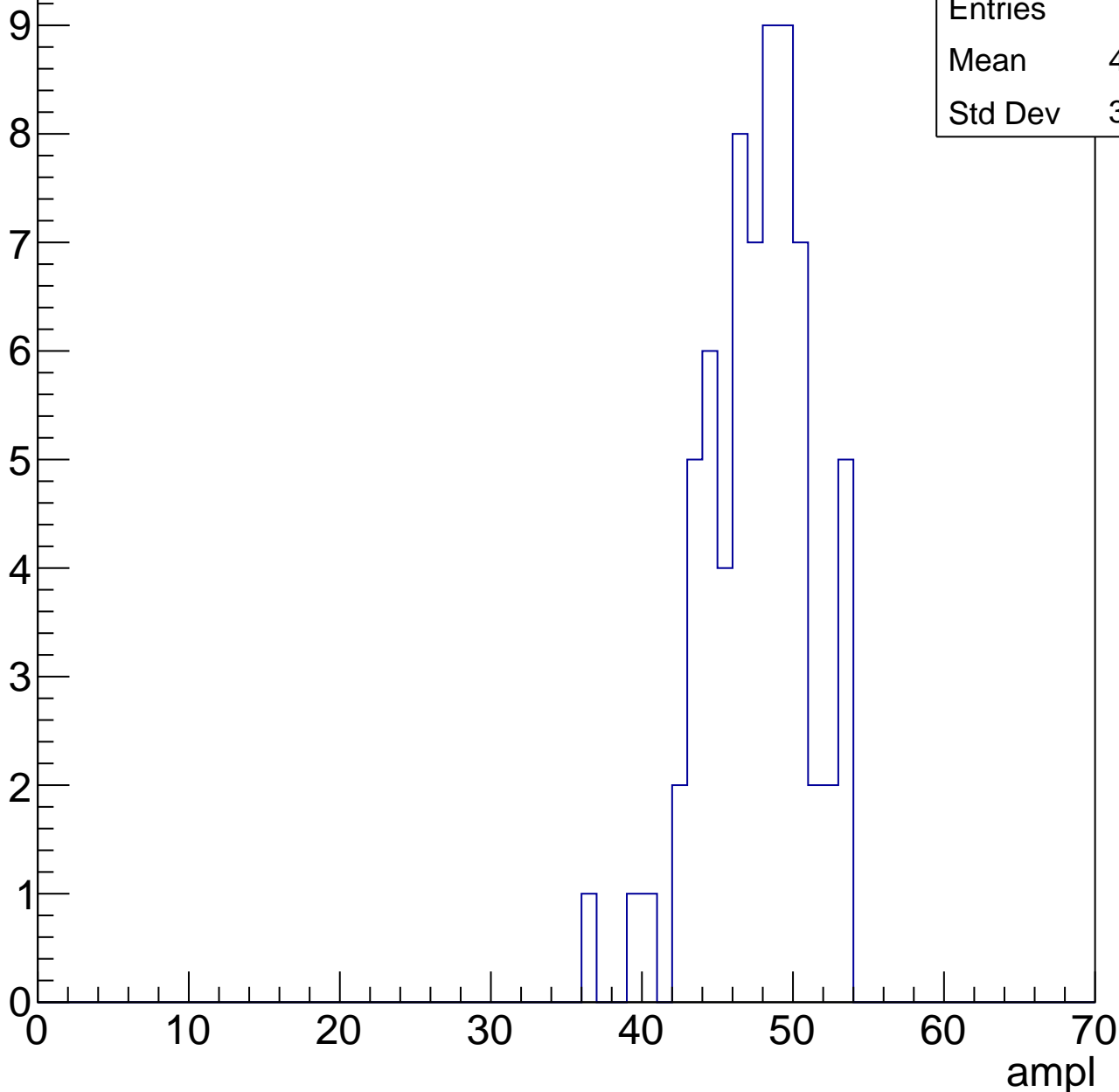


# B1L000S, U1-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	47.09
Std Dev	3.429

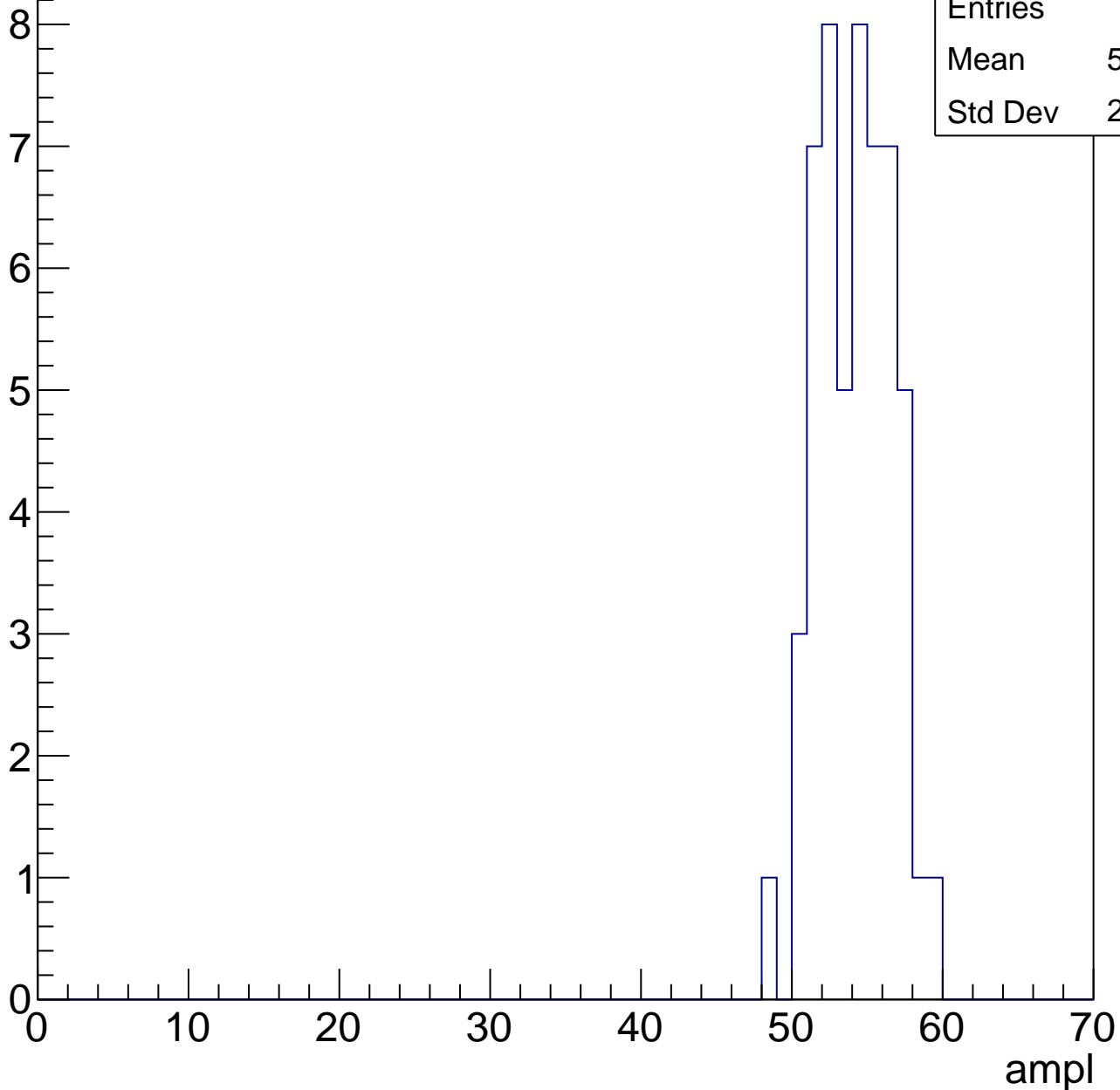


# B1L000S, U1-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

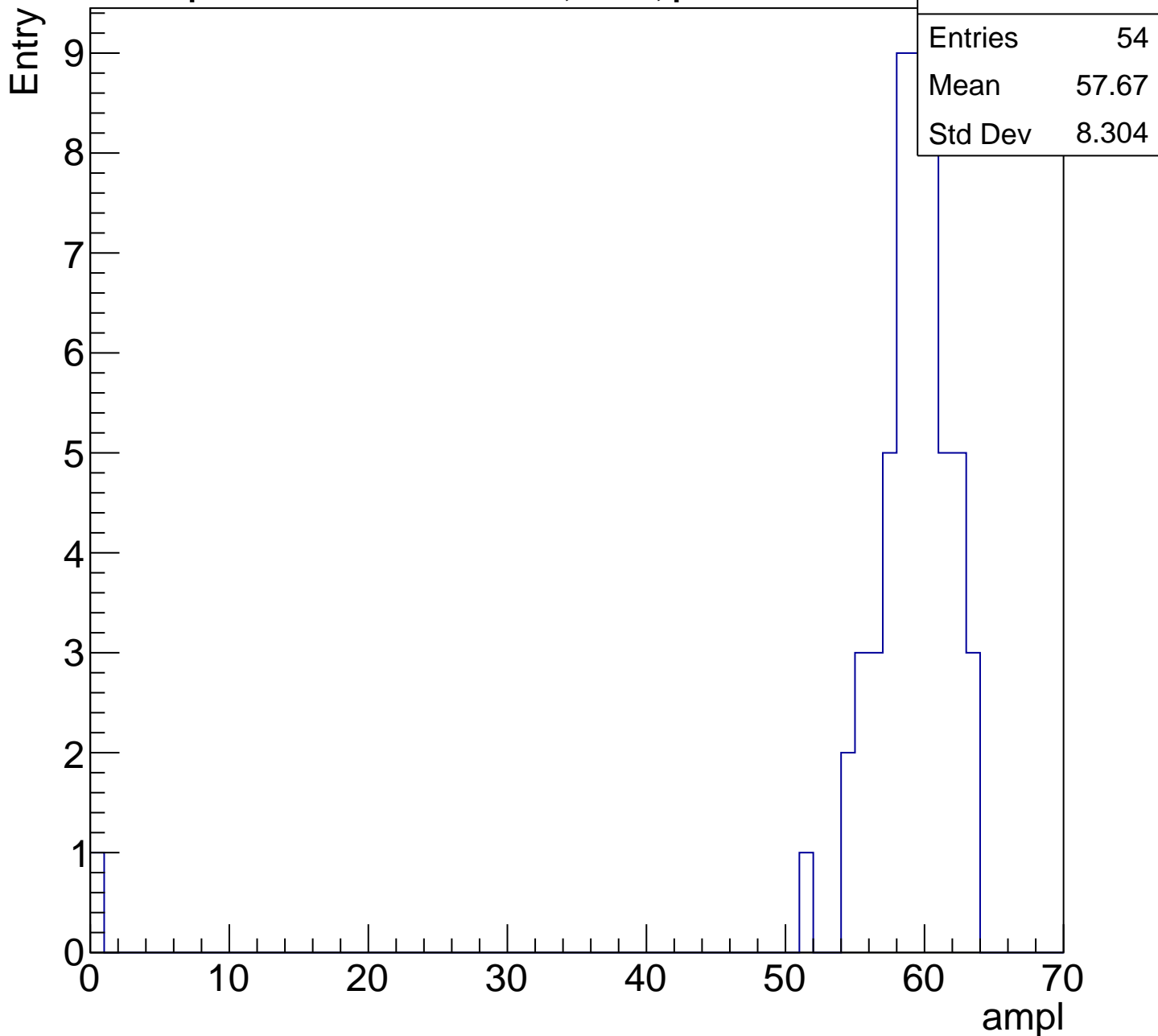
Entry

Entries	53
Mean	53.72
Std Dev	2.382



# B1L000S, U1-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

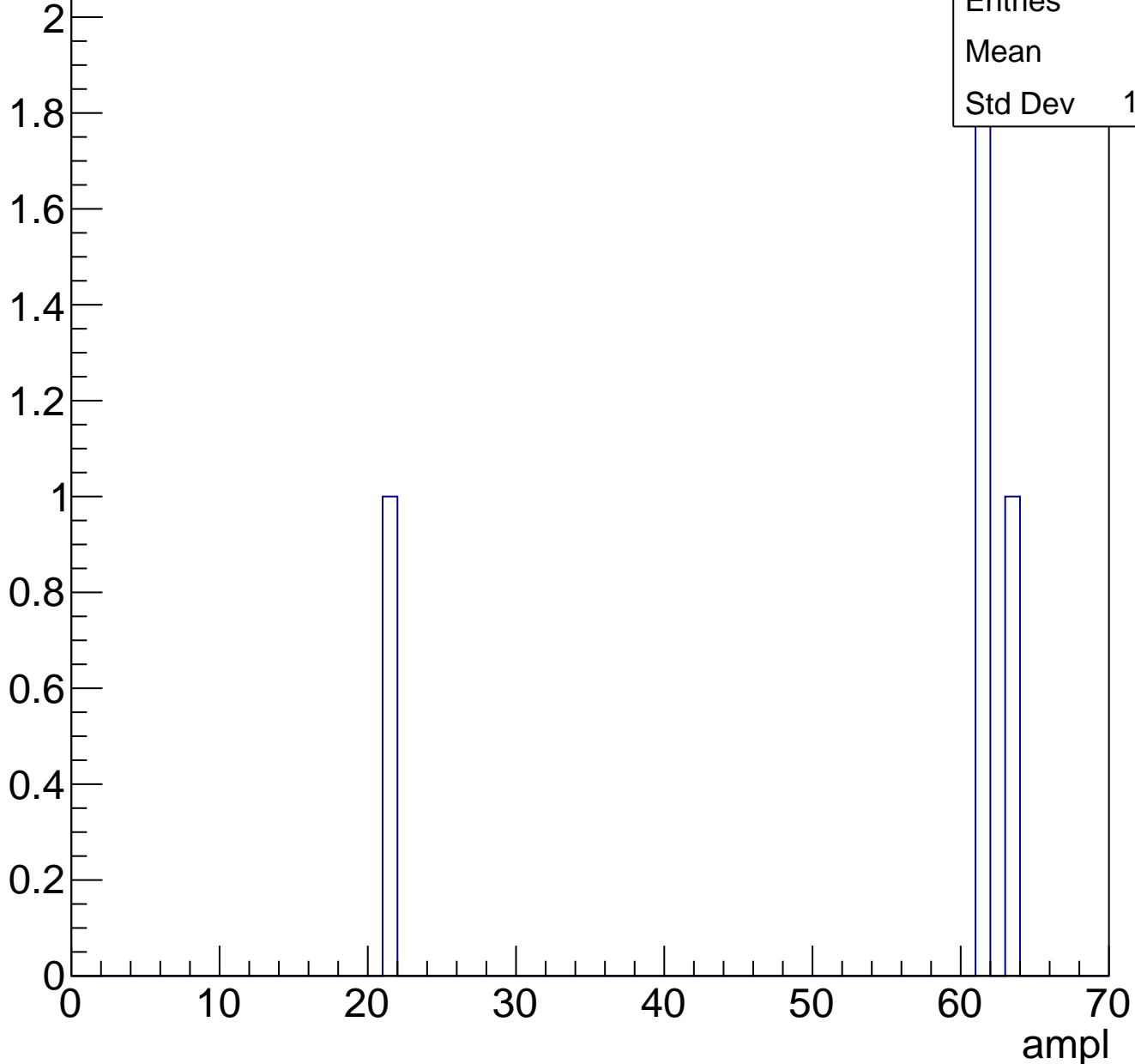
Entries	16
Mean	61.94
Std Dev	1.029



# B1L000S, U1-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	51.5
Std Dev	17.63

# B1L000S, U1-ch62, adc0

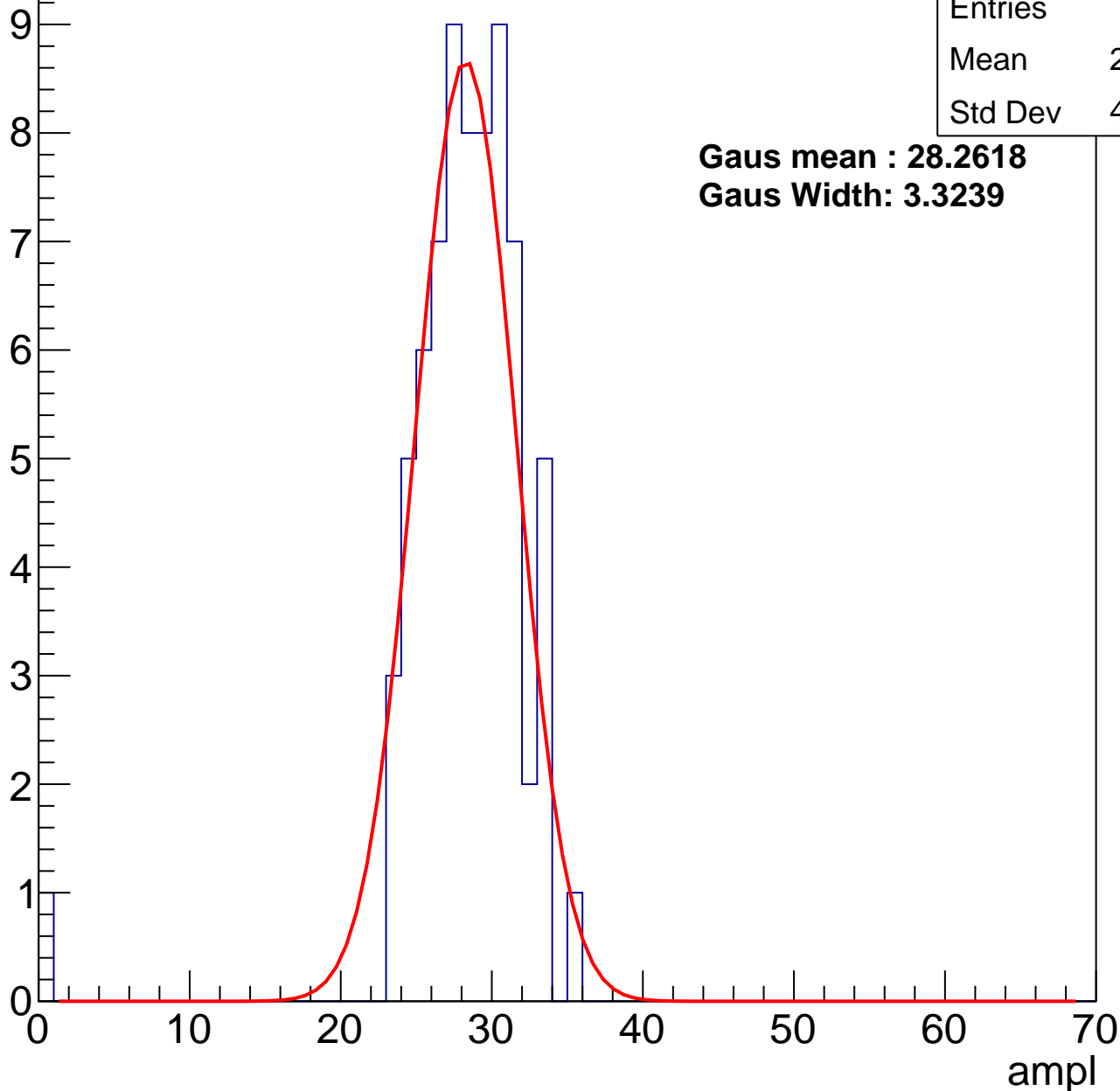
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	27.76
Std Dev	4.343

**Gaus mean : 28.2618**

**Gaus Width: 3.3239**



# B1L000S, U1-ch62, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.94
Std Dev	2.989

**Gaus mean : 35.2984**

**Gaus Width: 2.6756**

10

8

6

4

2

0

0

10

20

30

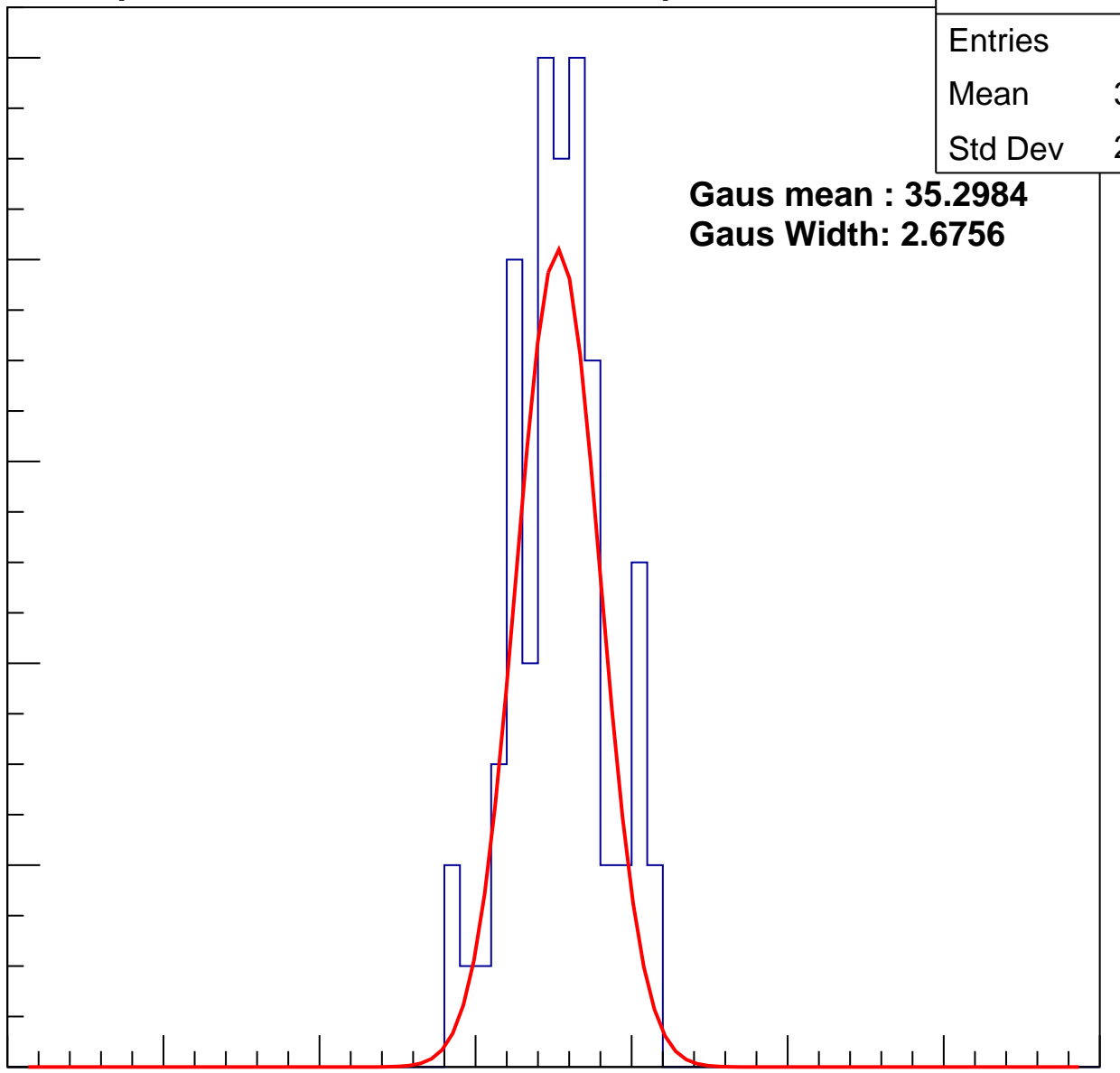
40

50

60

70

ampl



# B1L000S, U1-ch62, adc2

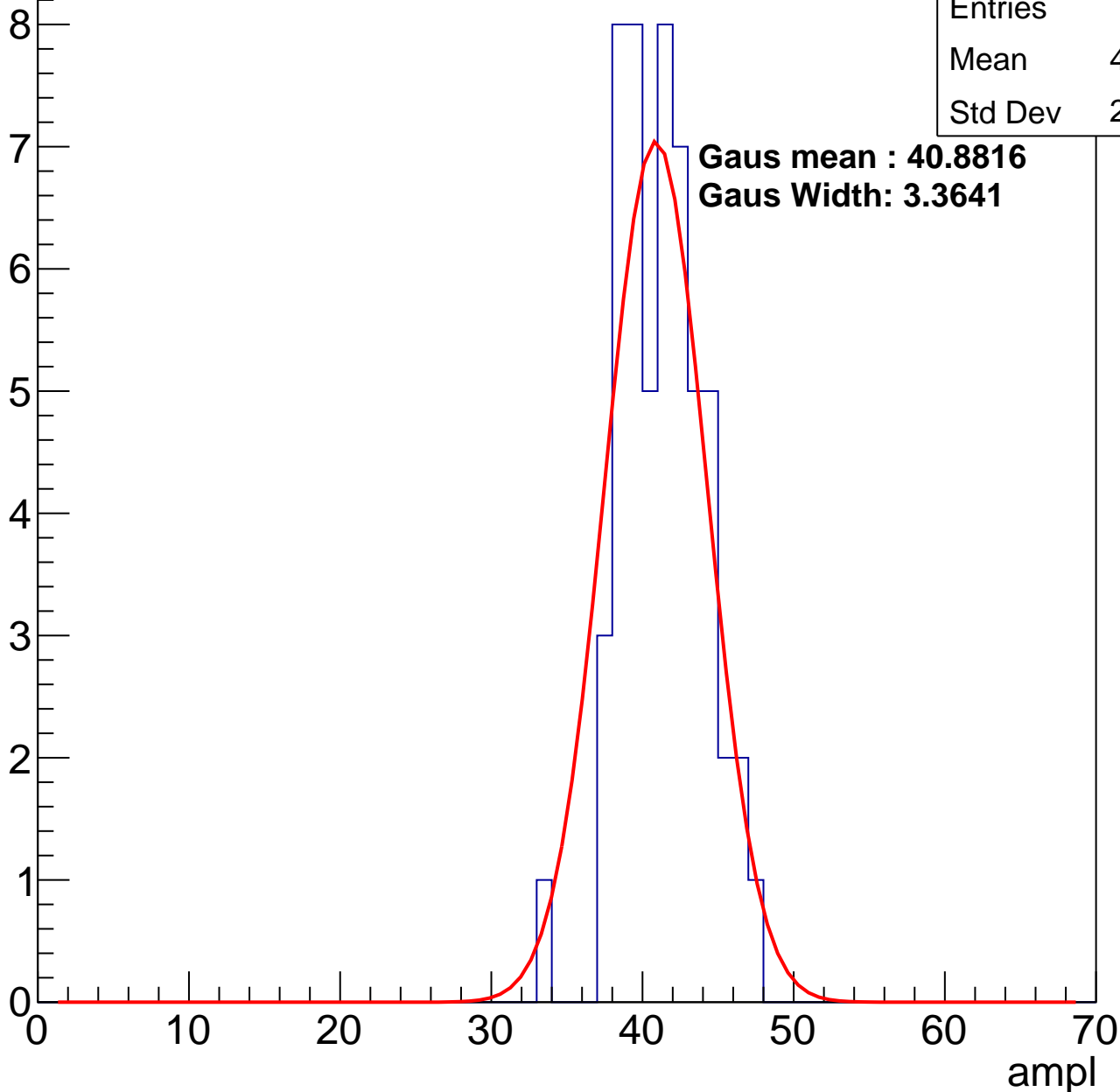
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	40.84
Std Dev	2.729

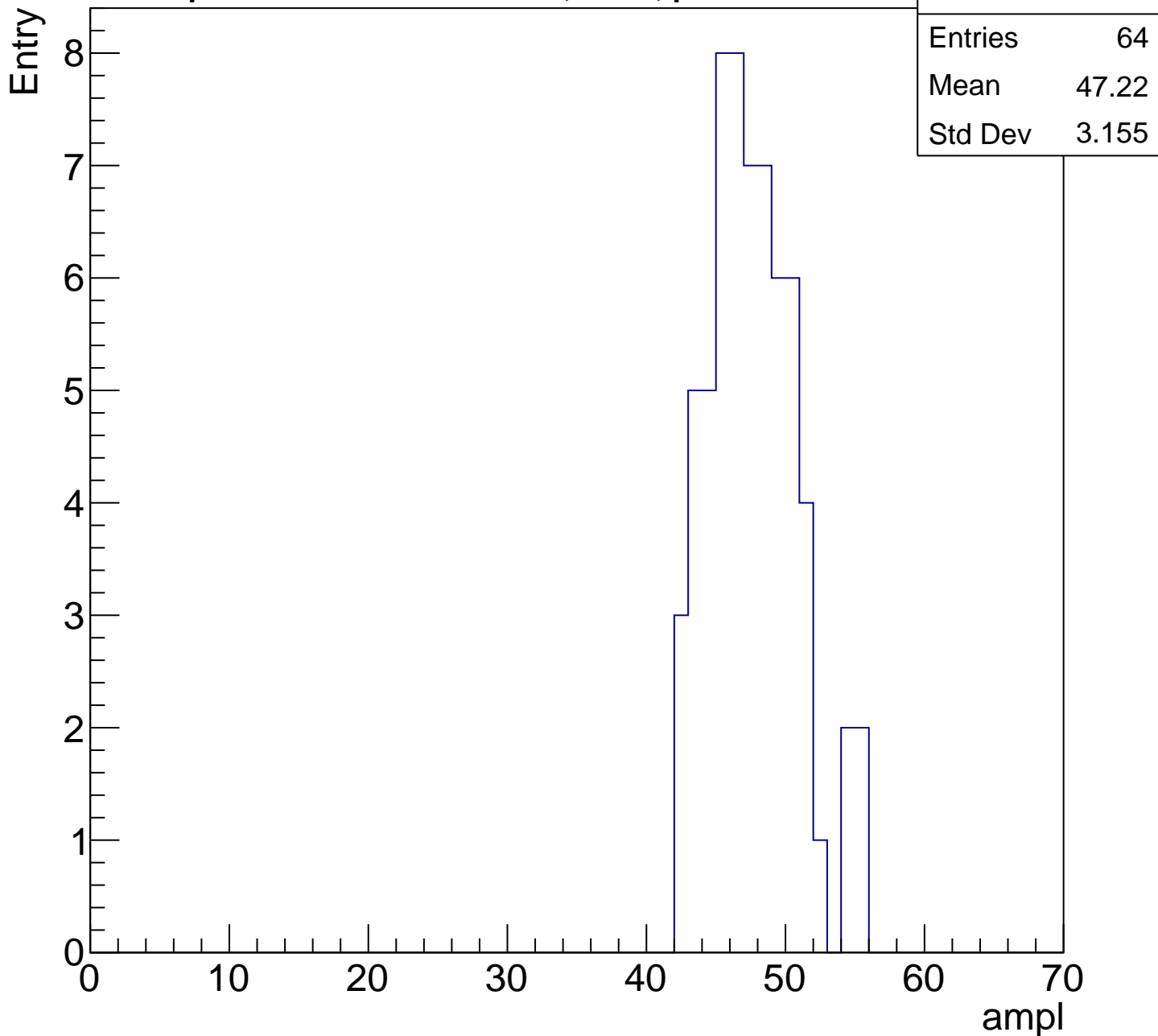
**Gaus mean : 40.8816**

**Gaus Width: 3.3641**



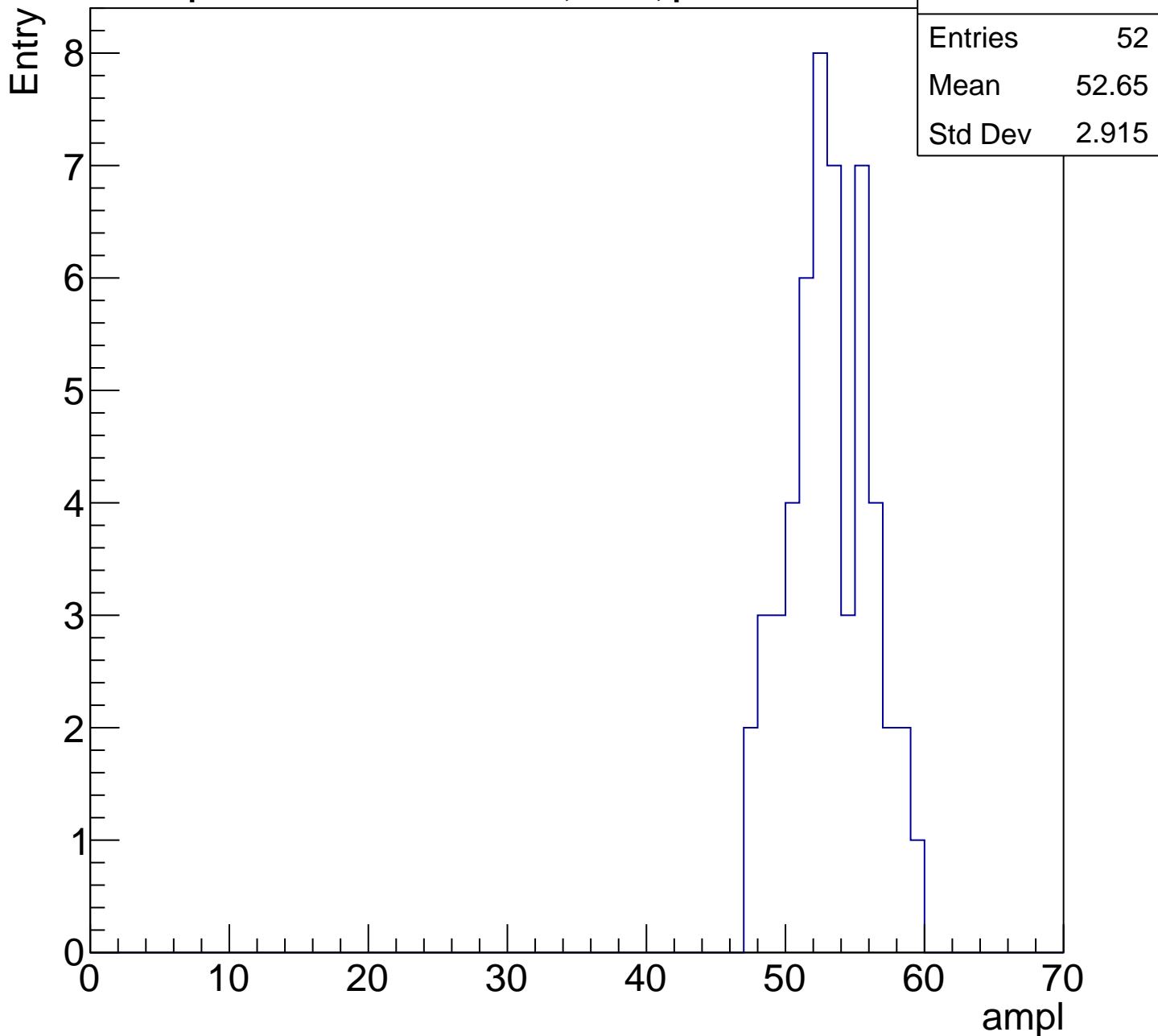
# B1L000S, U1-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

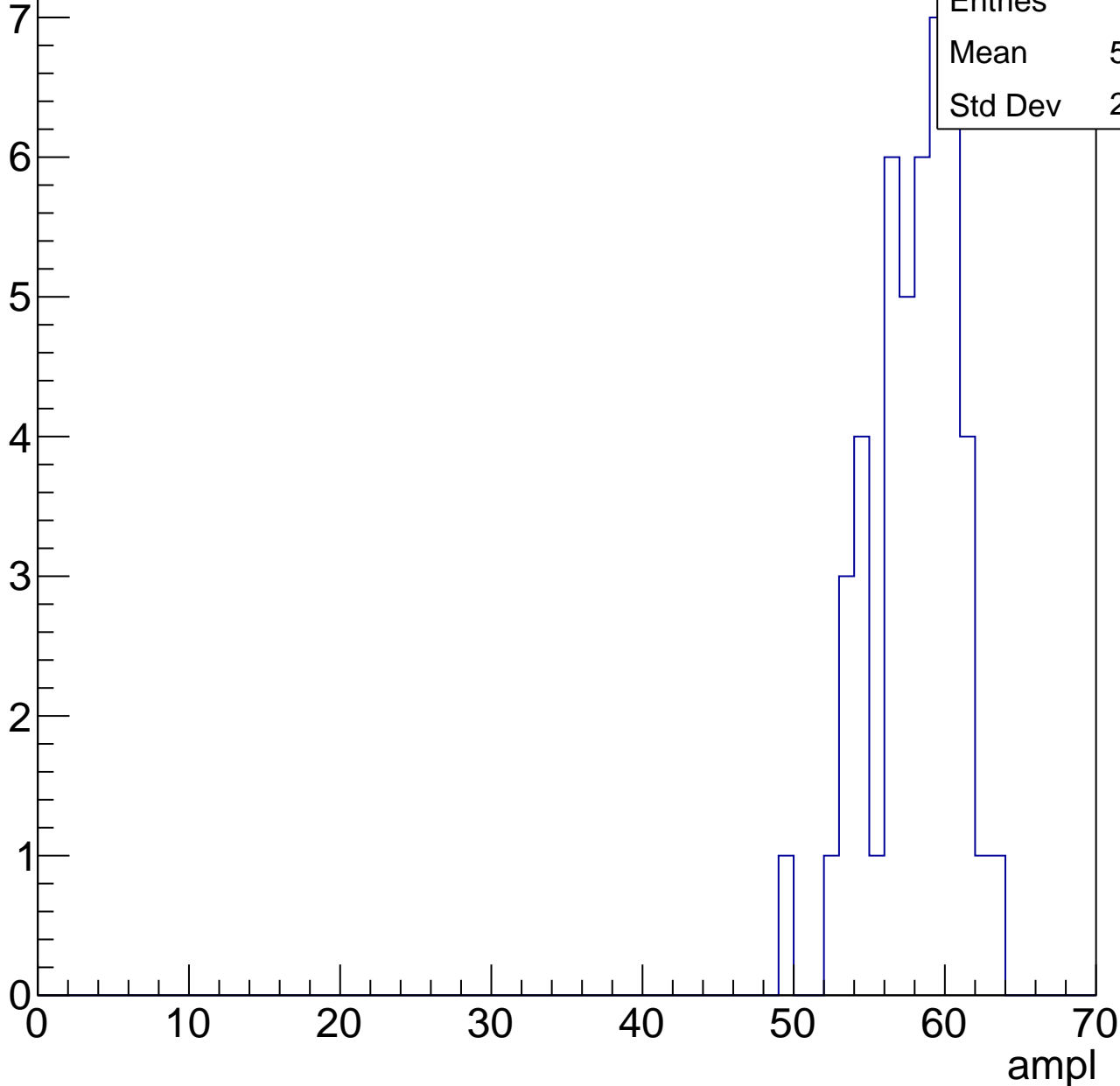


# B1L000S, U1-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	57.49
Std Dev	2.894

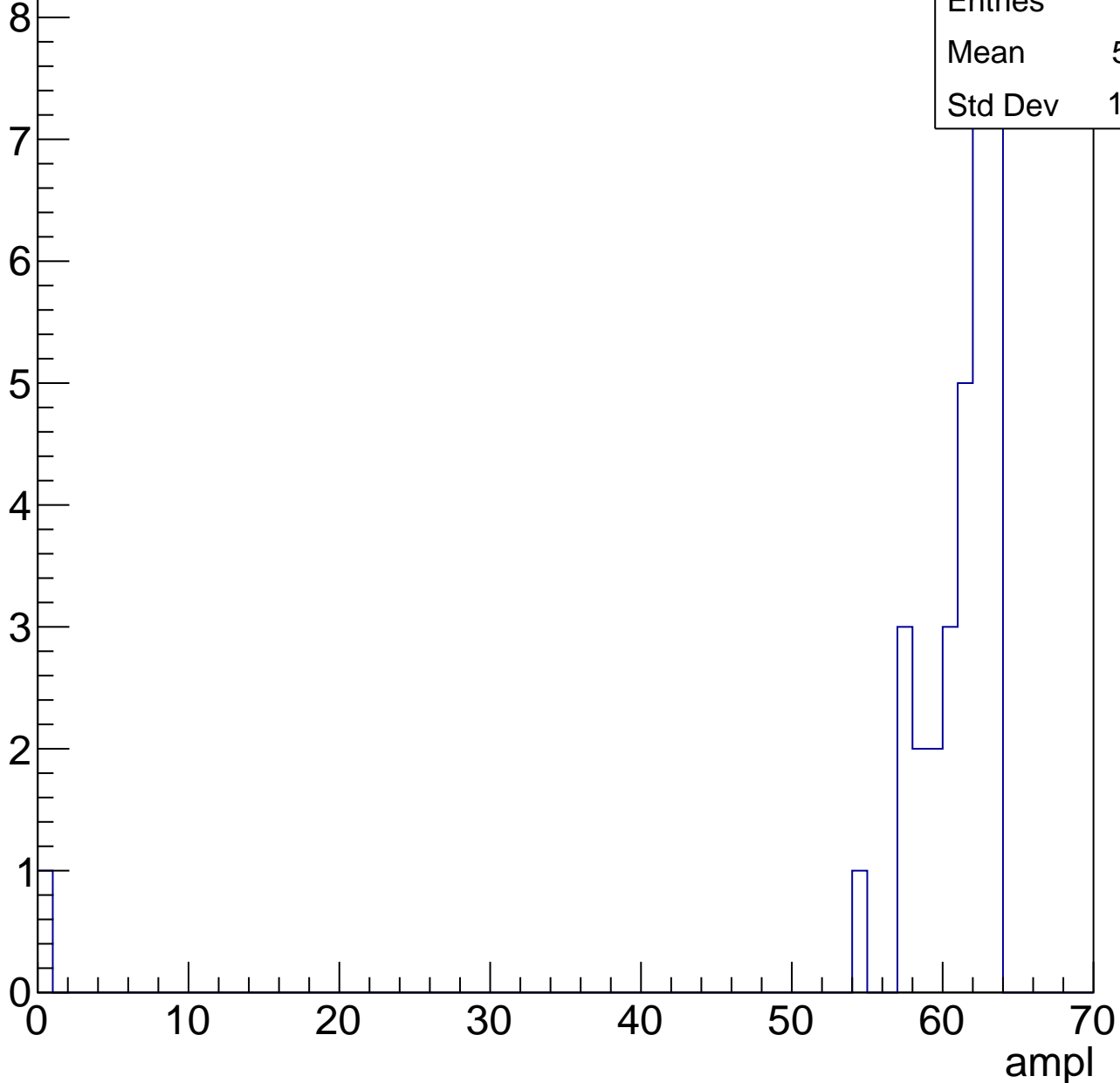


# B1L000S, U1-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	33
Mean	58.91
Std Dev	10.65

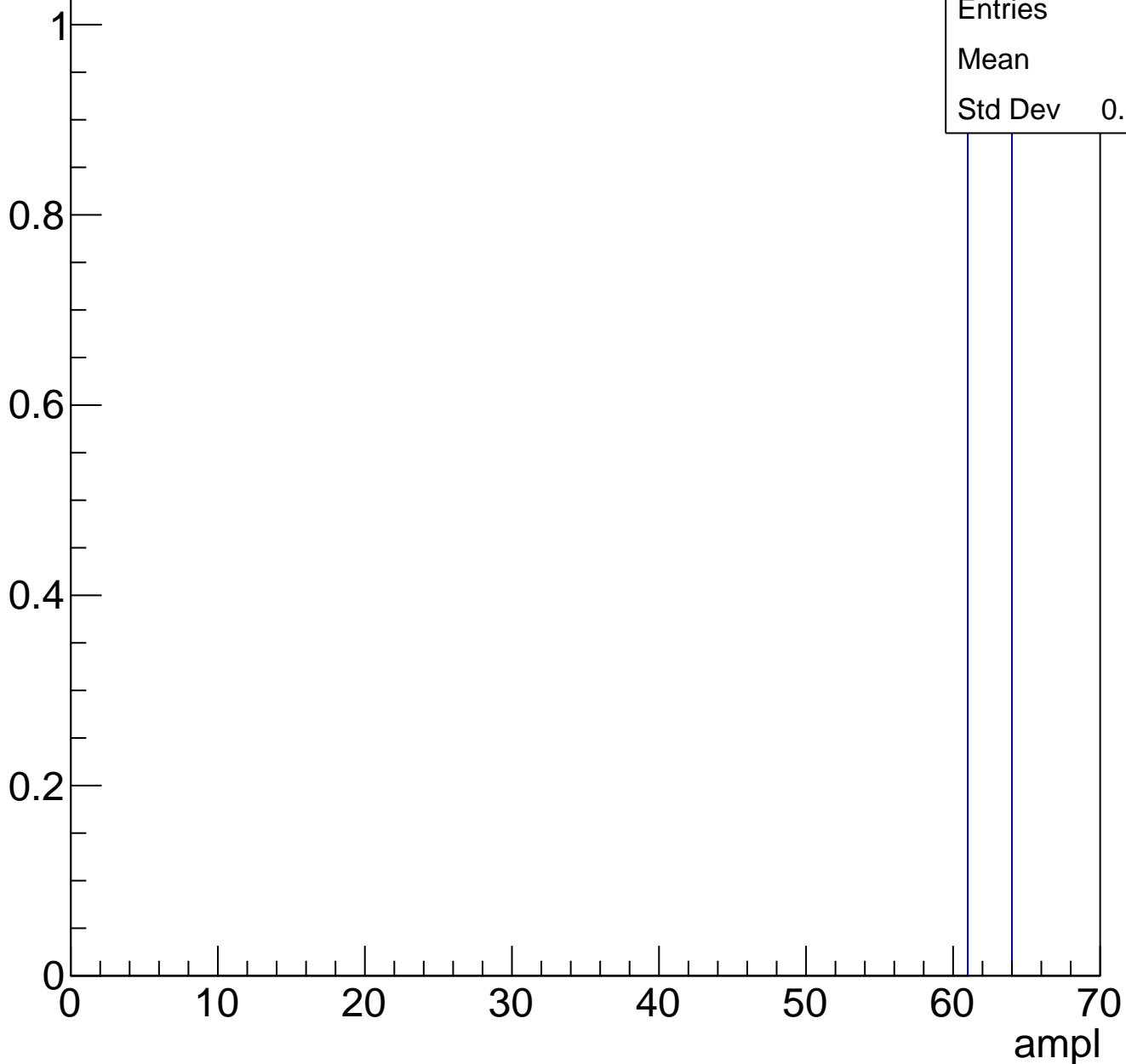




# B1L000S, U1-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch63, adc0

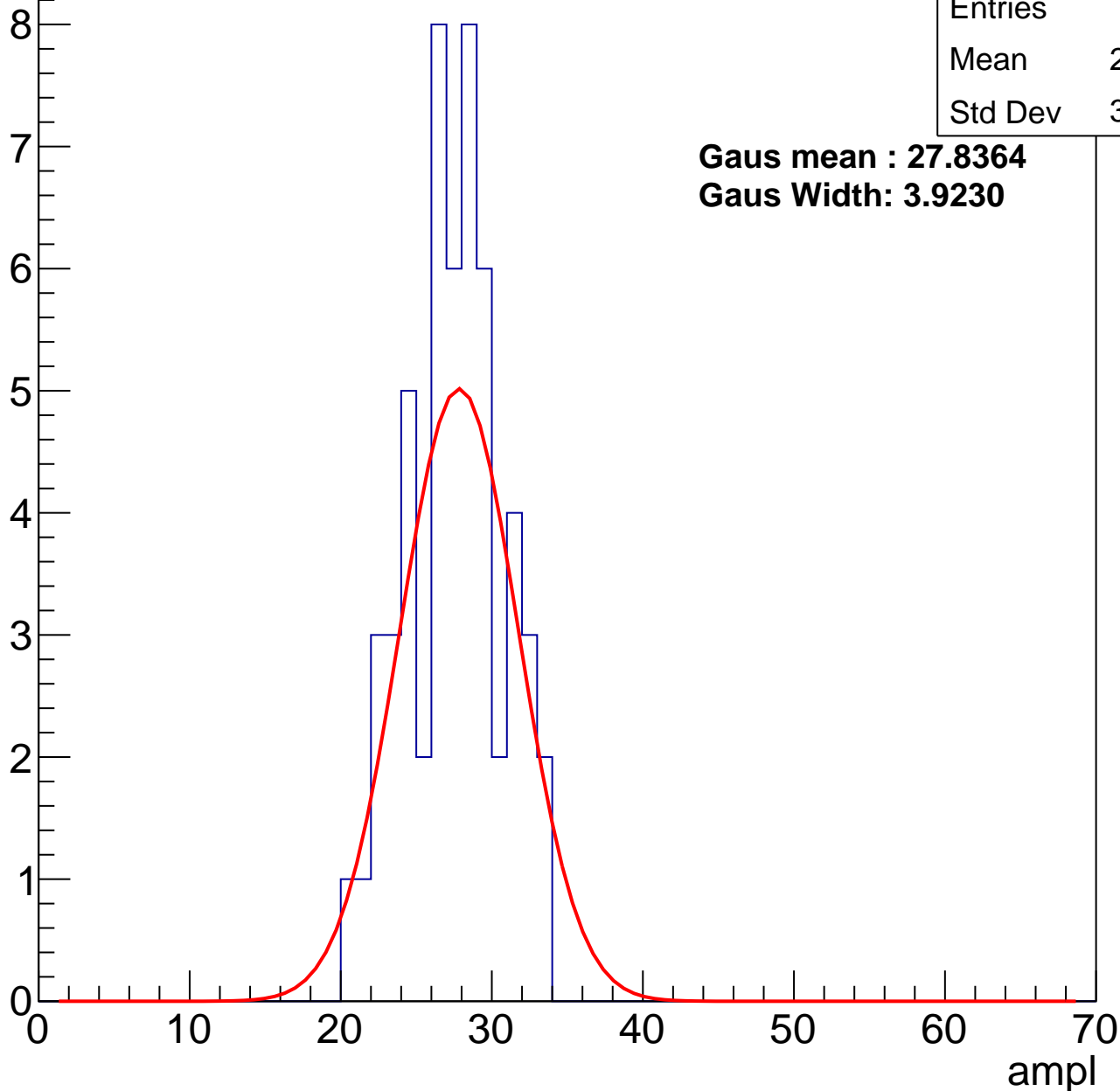
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	27.04
Std Dev	3.133

**Gaus mean : 27.8364**

**Gaus Width: 3.9230**



# B1L000S, U1-ch63, adc1

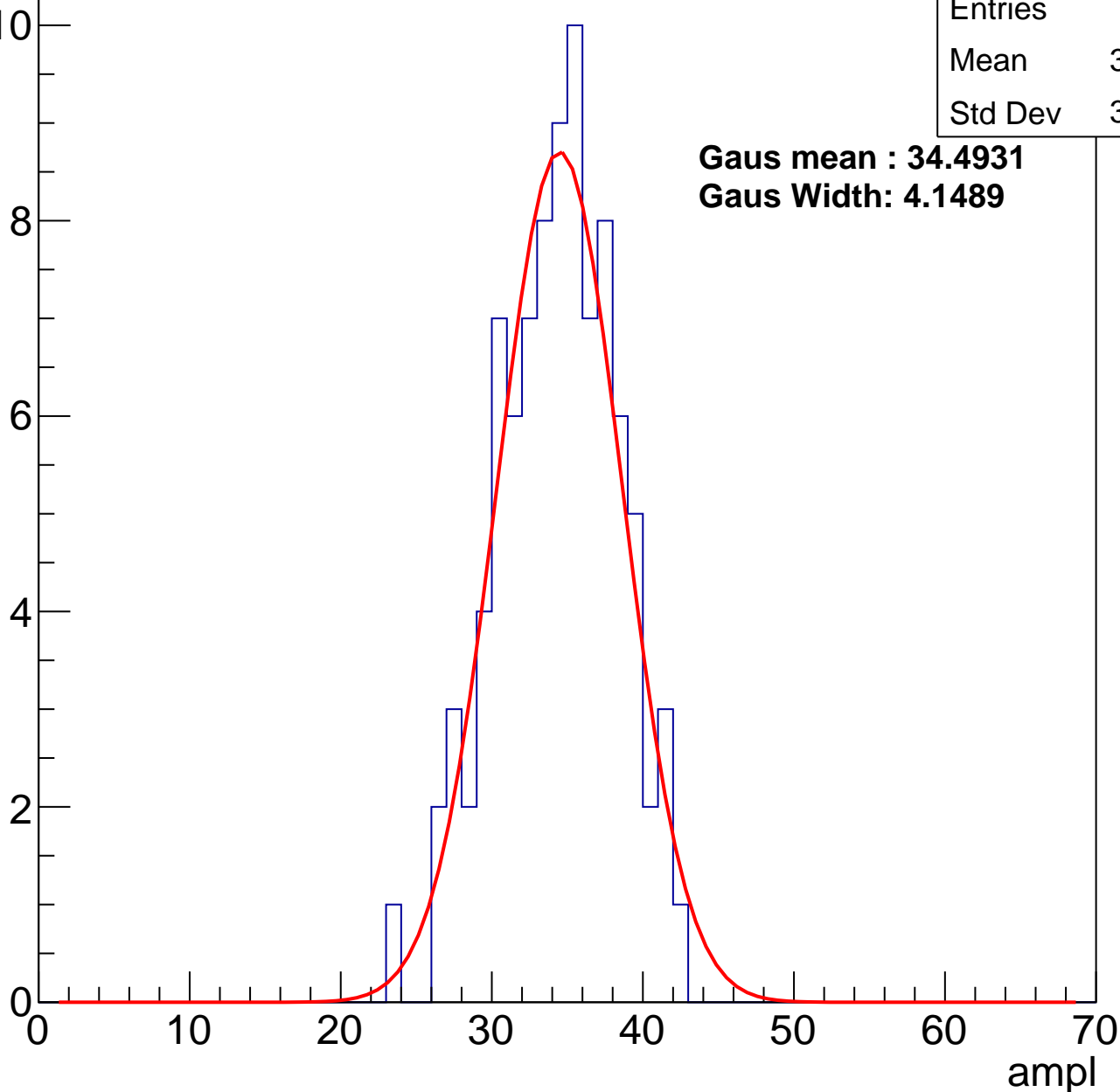
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	91
Mean	33.89
Std Dev	3.898

**Gaus mean : 34.4931**

**Gaus Width: 4.1489**



# B1L000S, U1-ch63, adc2

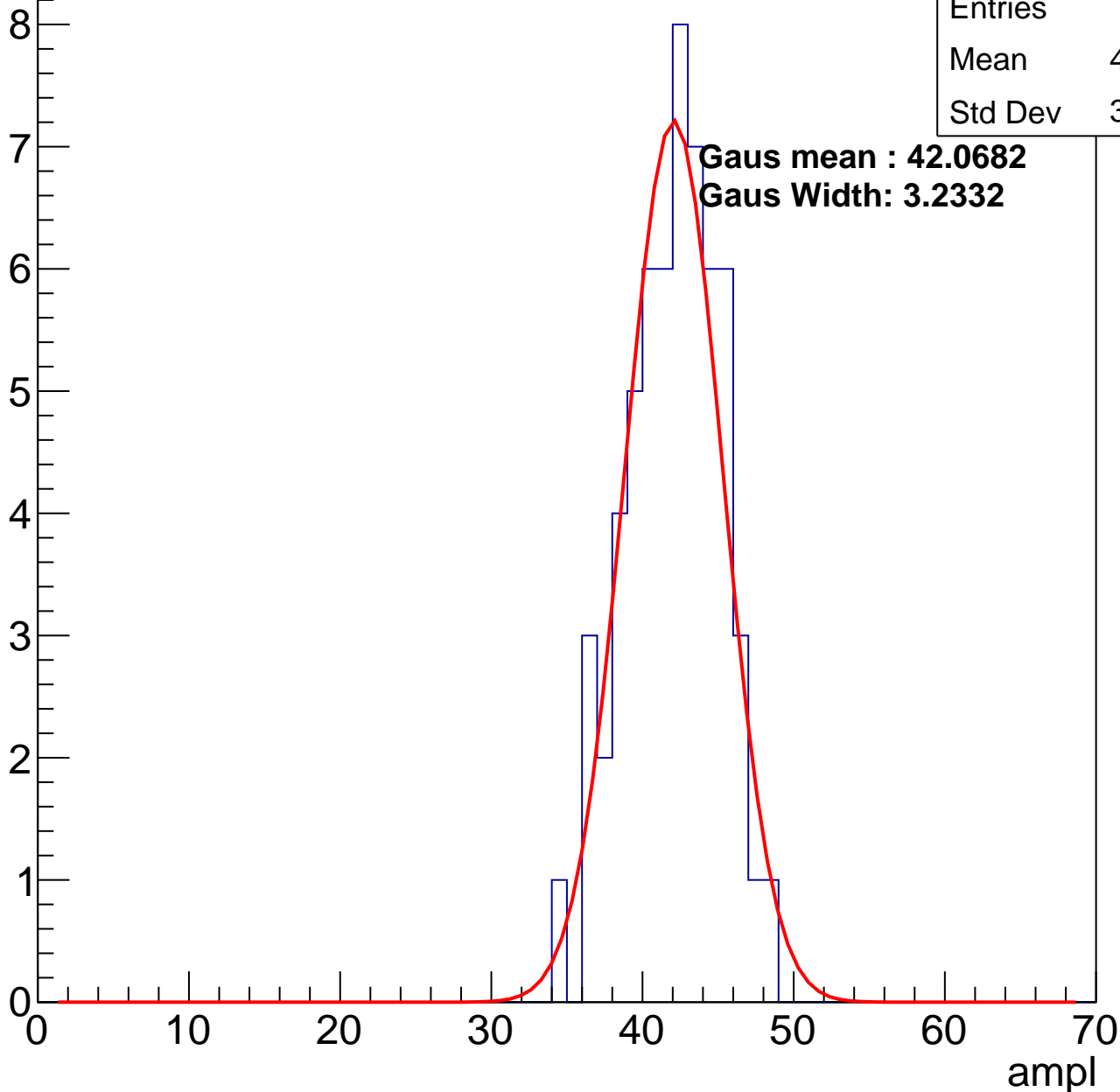
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.58
Std Dev	3.038

**Gaus mean : 42.0682**

**Gaus Width: 3.2332**

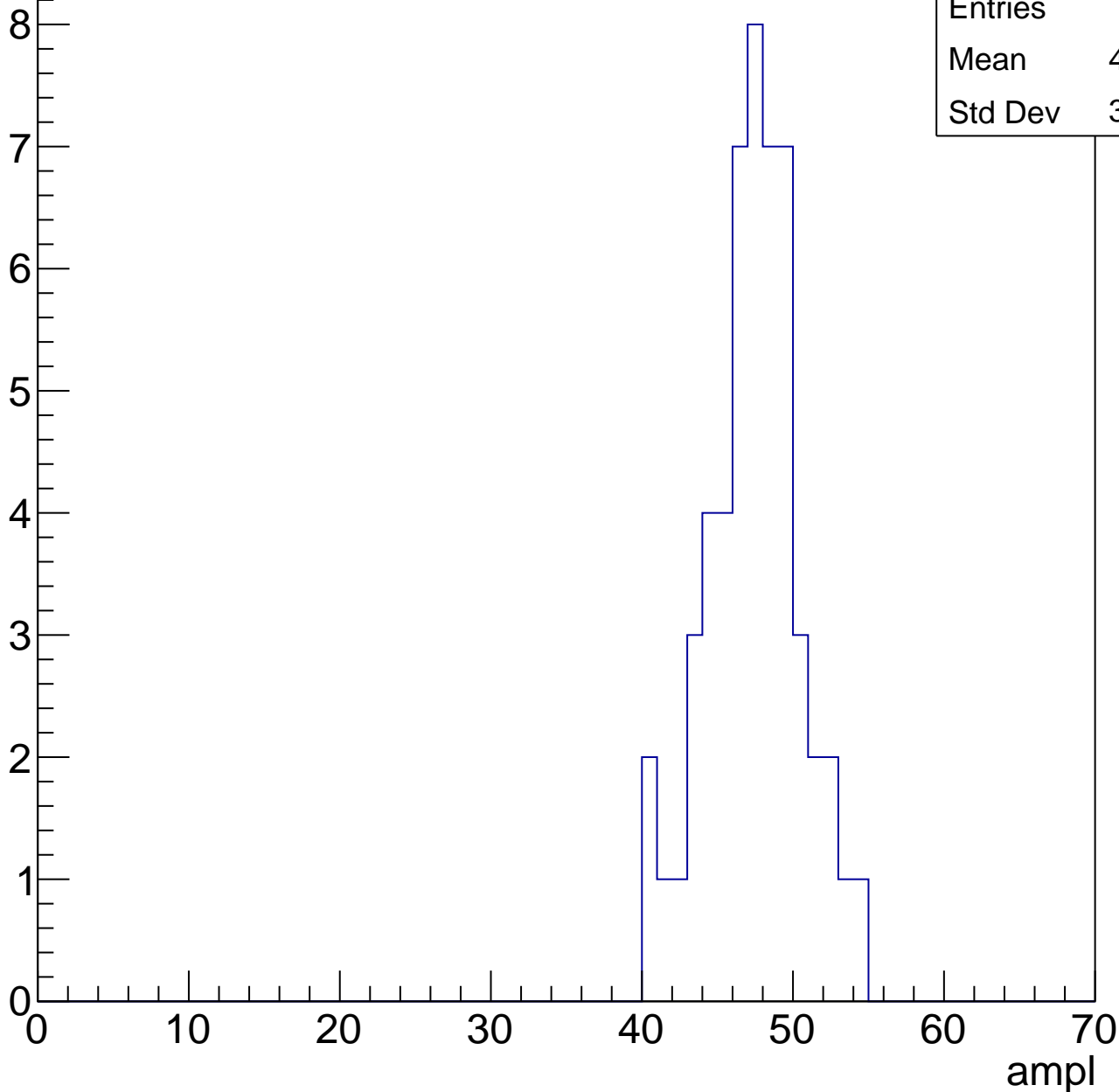


# B1L000S, U1-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

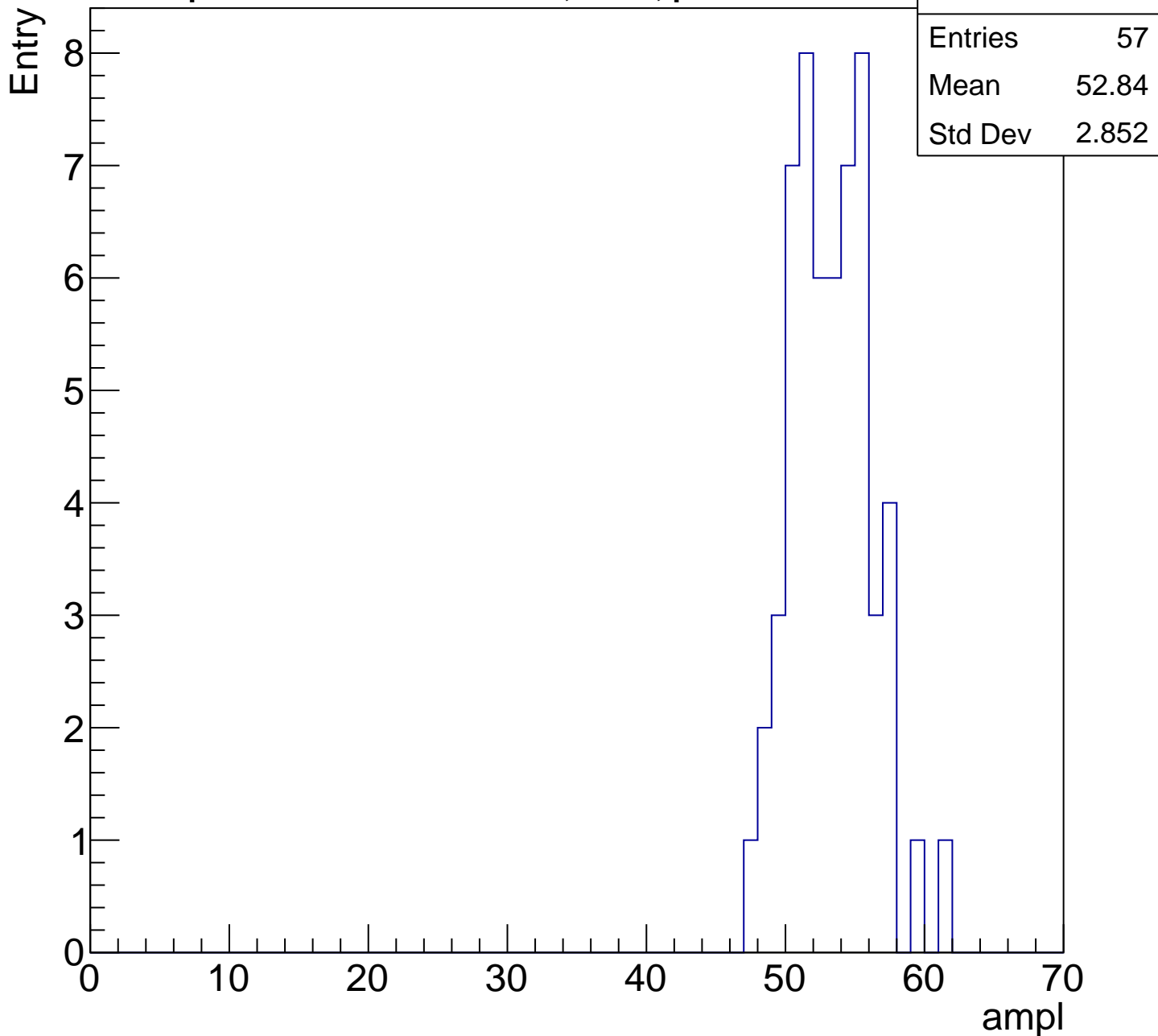
Entry

Entries	53
Mean	46.94
Std Dev	3.056



# B1L000S, U1-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

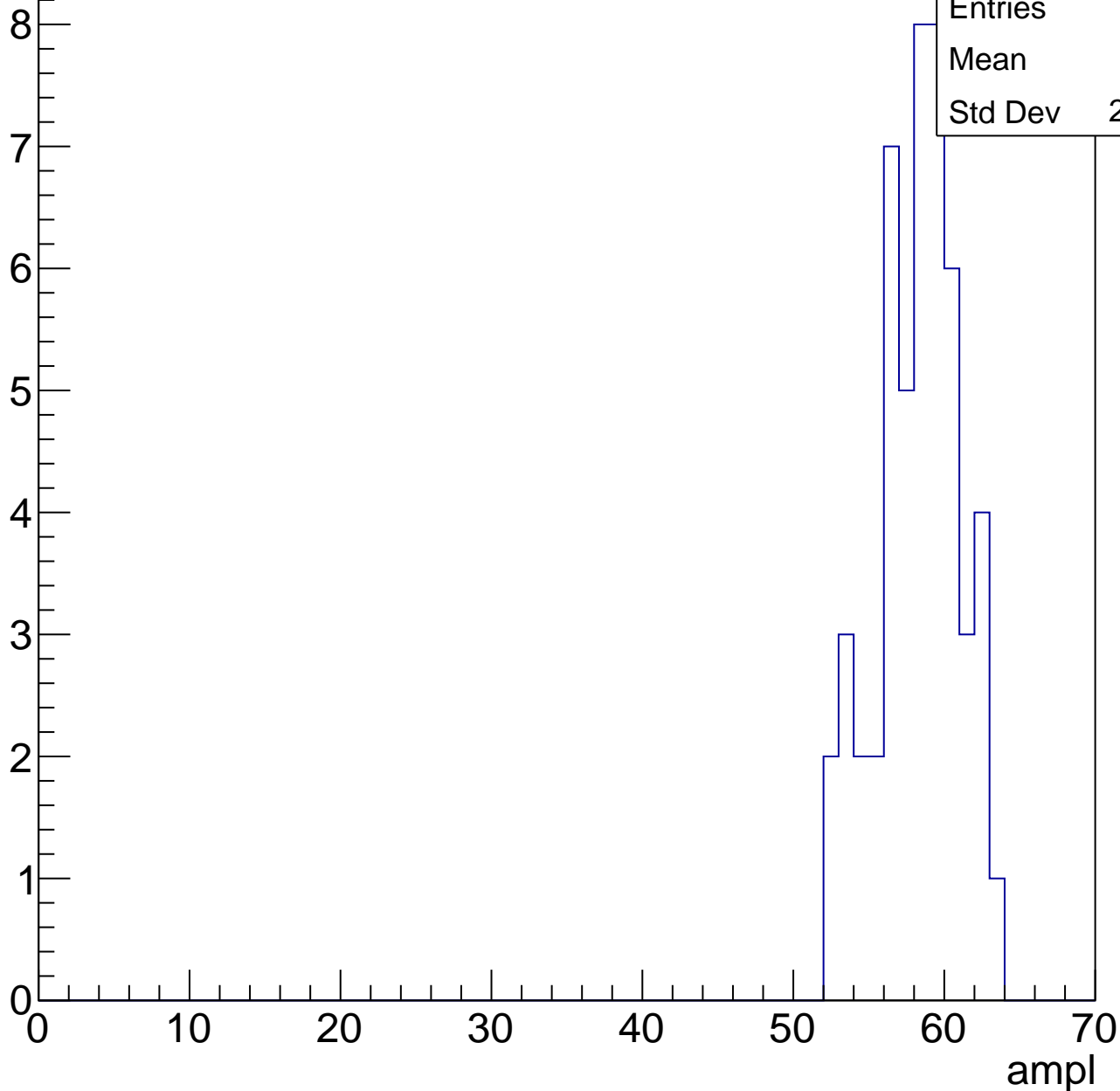


# B1L000S, U1-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	57.8
Std Dev	2.715

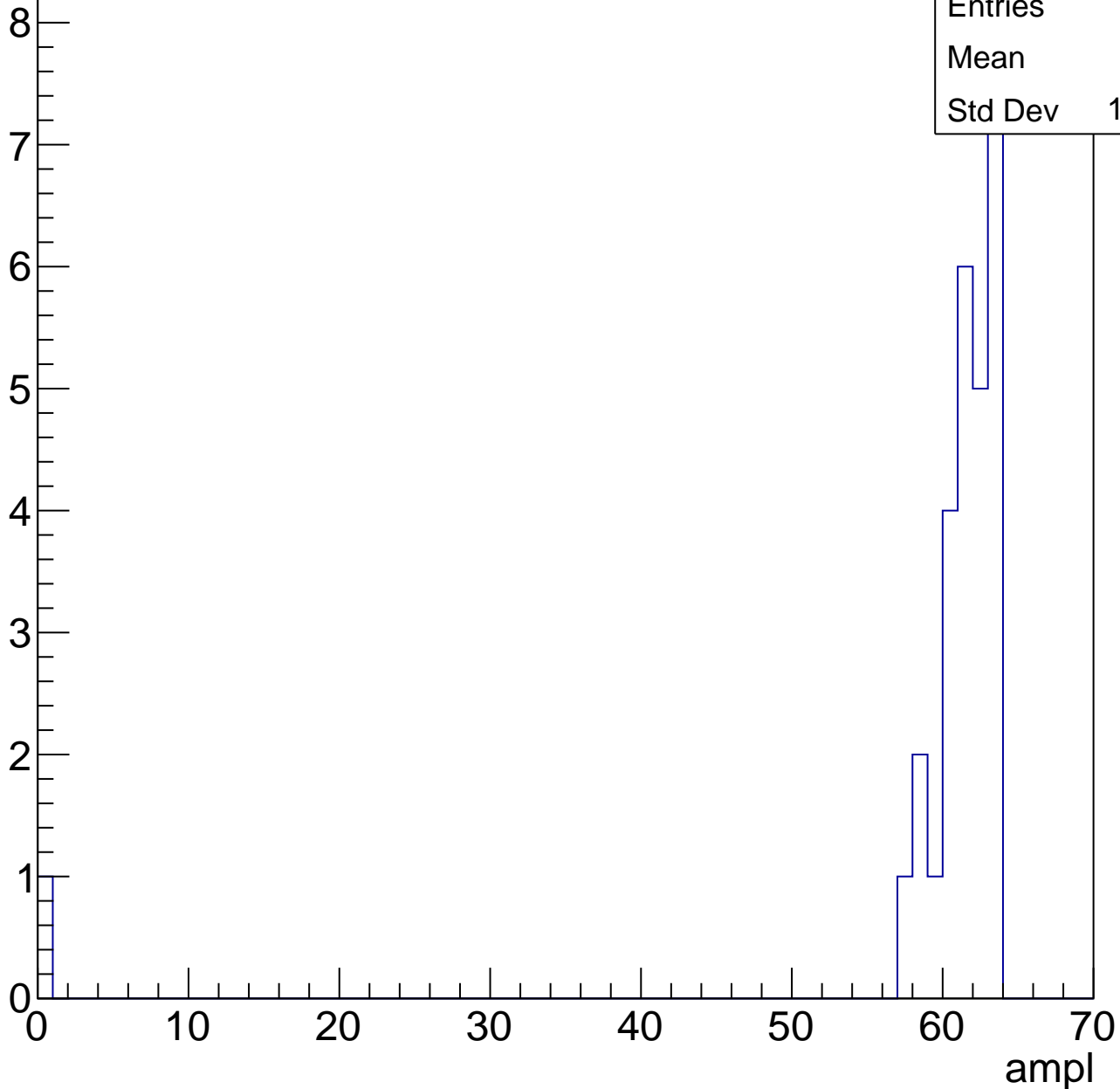


# B1L000S, U1-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	59
Std Dev	11.48

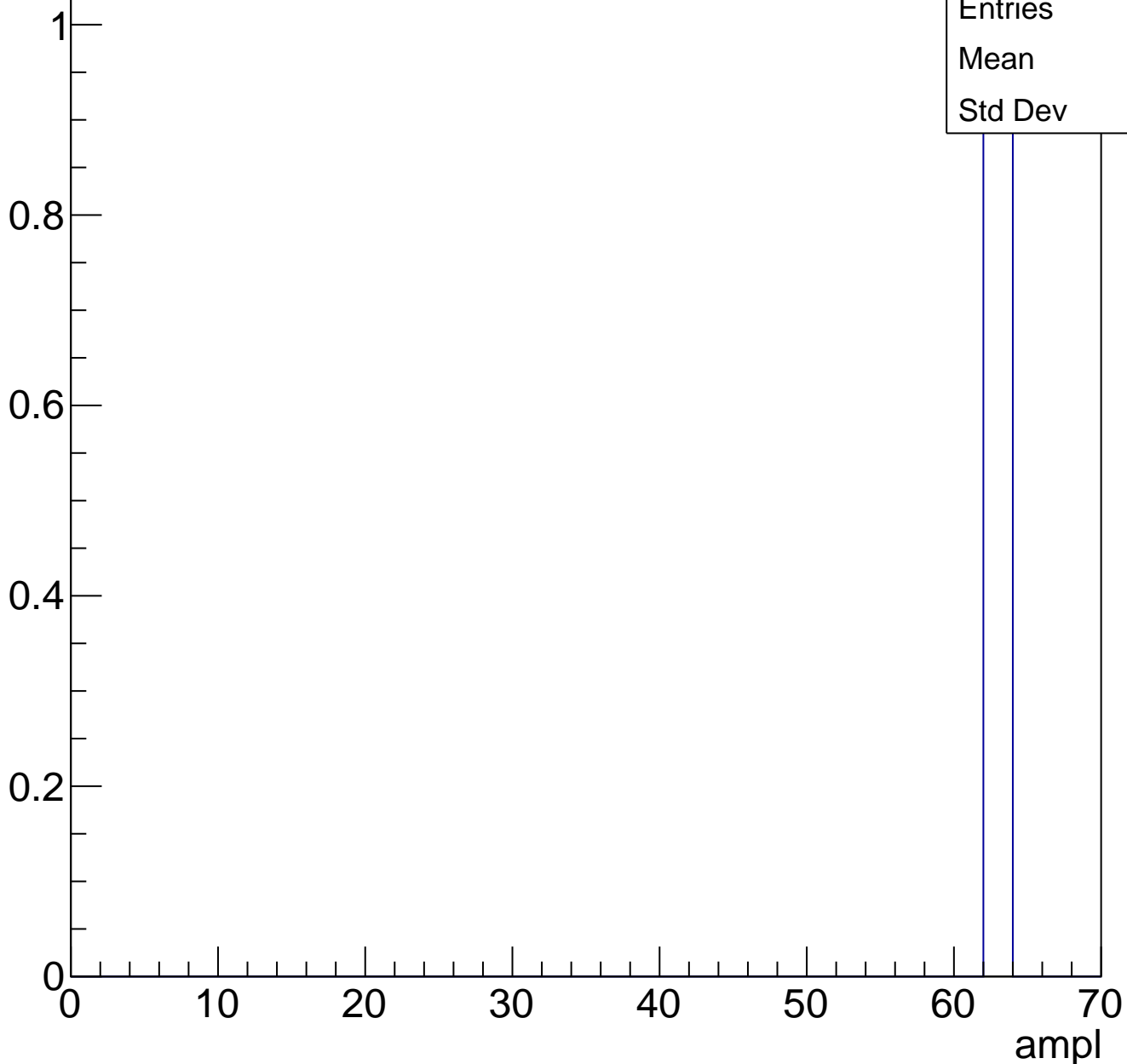




# B1L000S, U1-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch64, adc0

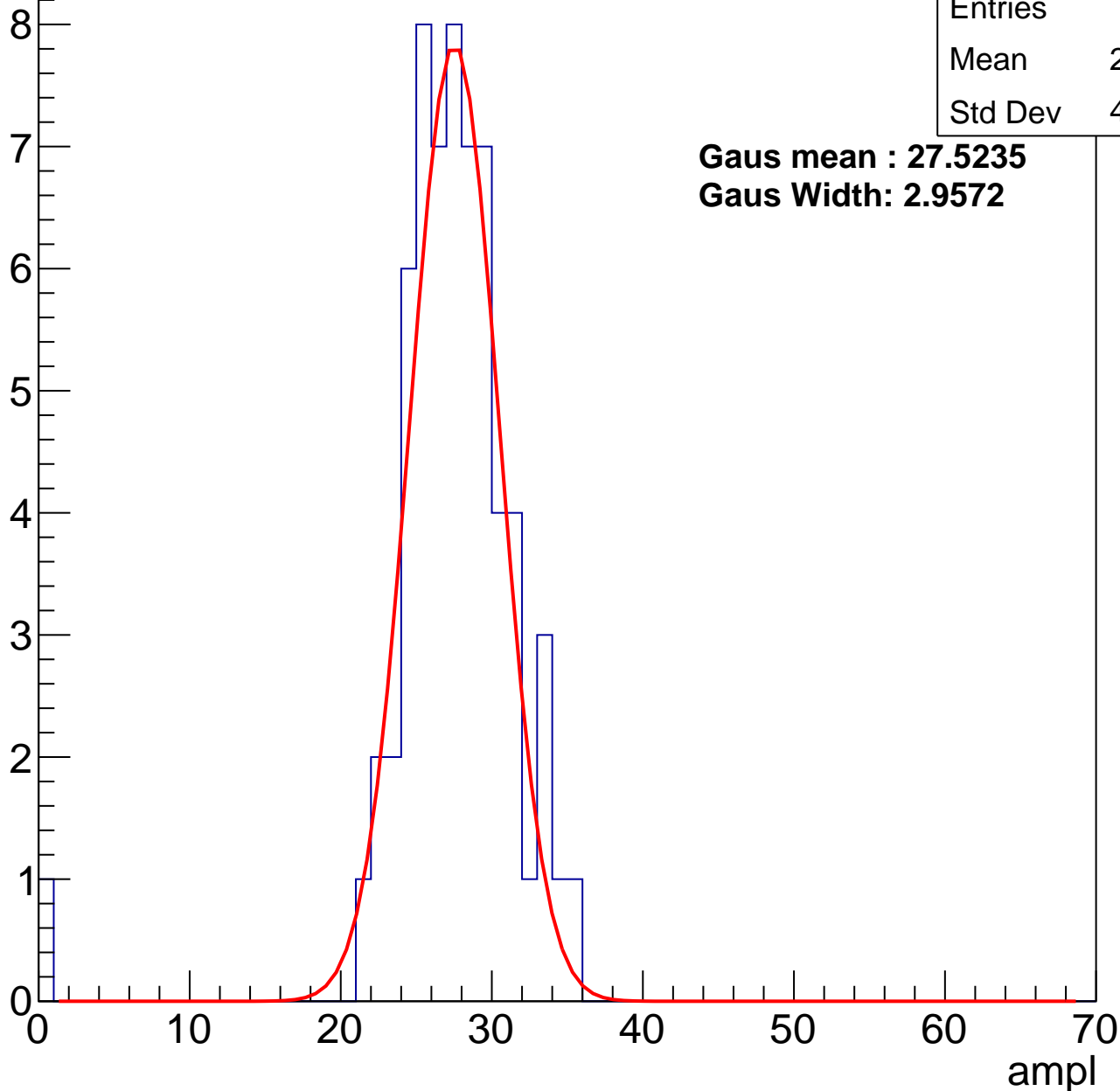
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	26.92
Std Dev	4.578

**Gaus mean : 27.5235**

**Gaus Width: 2.9572**



# B1L000S, U1-ch64, adc1

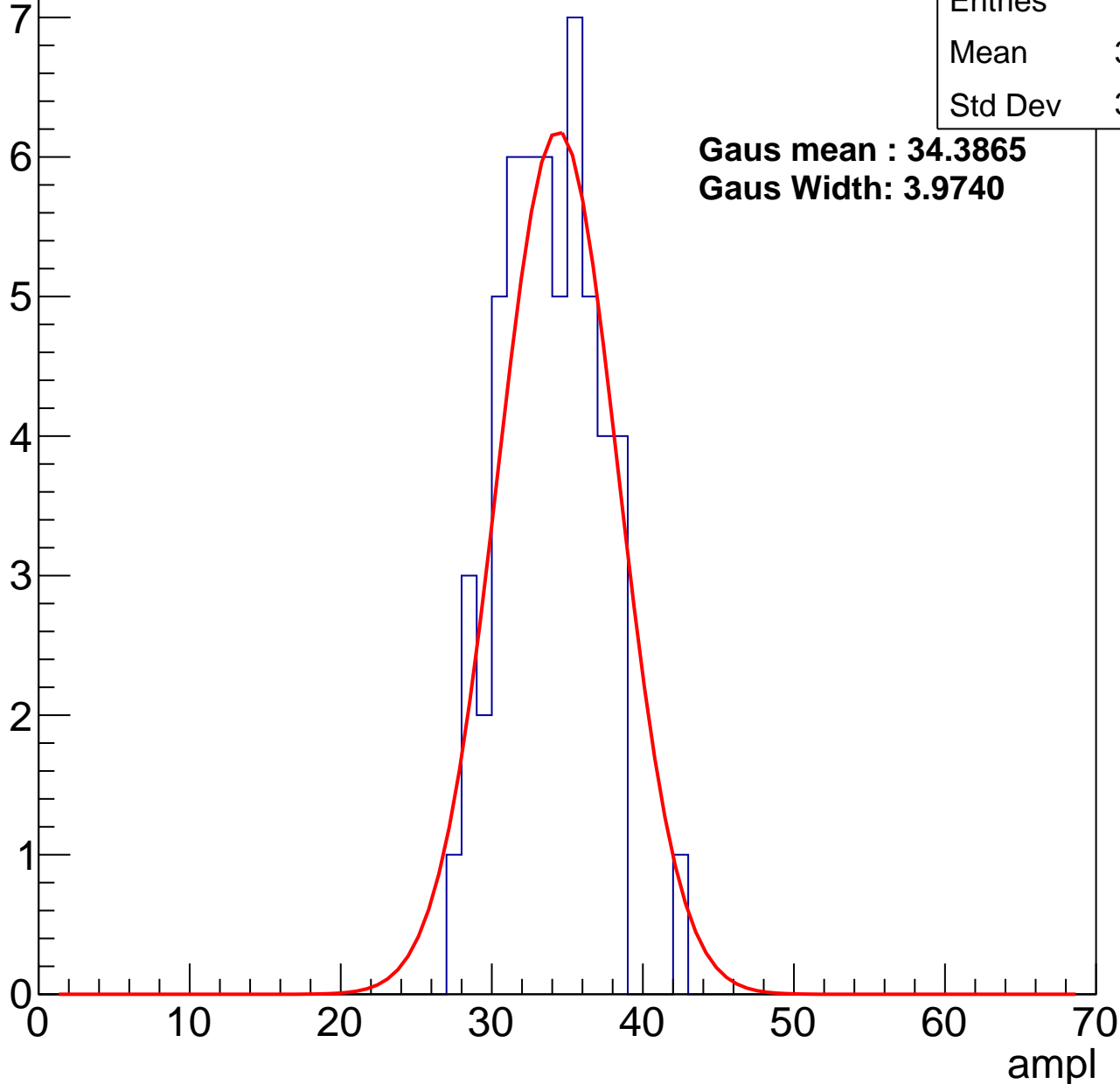
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	33.31
Std Dev	3.121

**Gaus mean : 34.3865**

**Gaus Width: 3.9740**



# B1L000S, U1-ch64, adc2

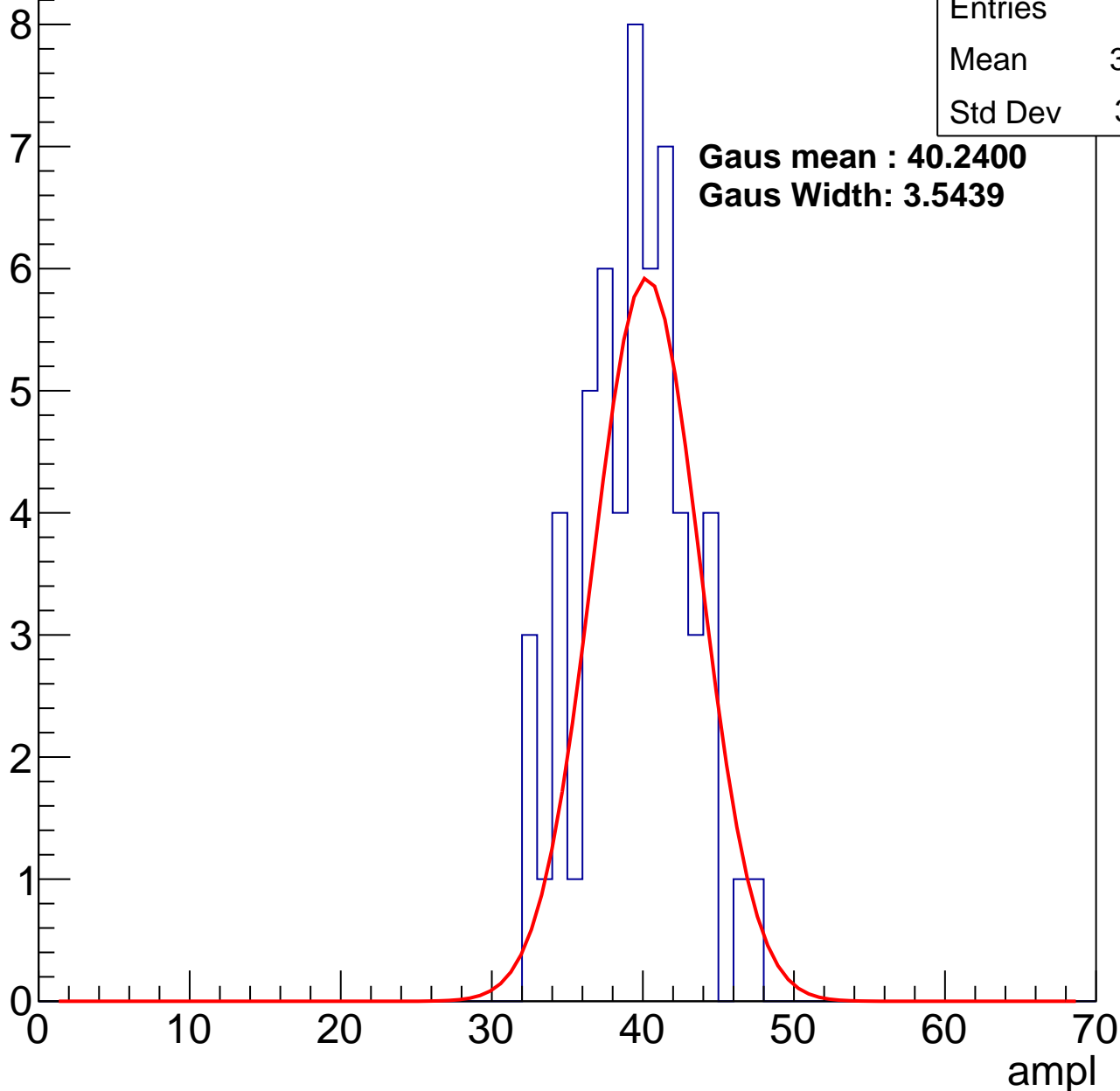
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	38.95
Std Dev	3.481

**Gaus mean : 40.2400**

**Gaus Width: 3.5439**

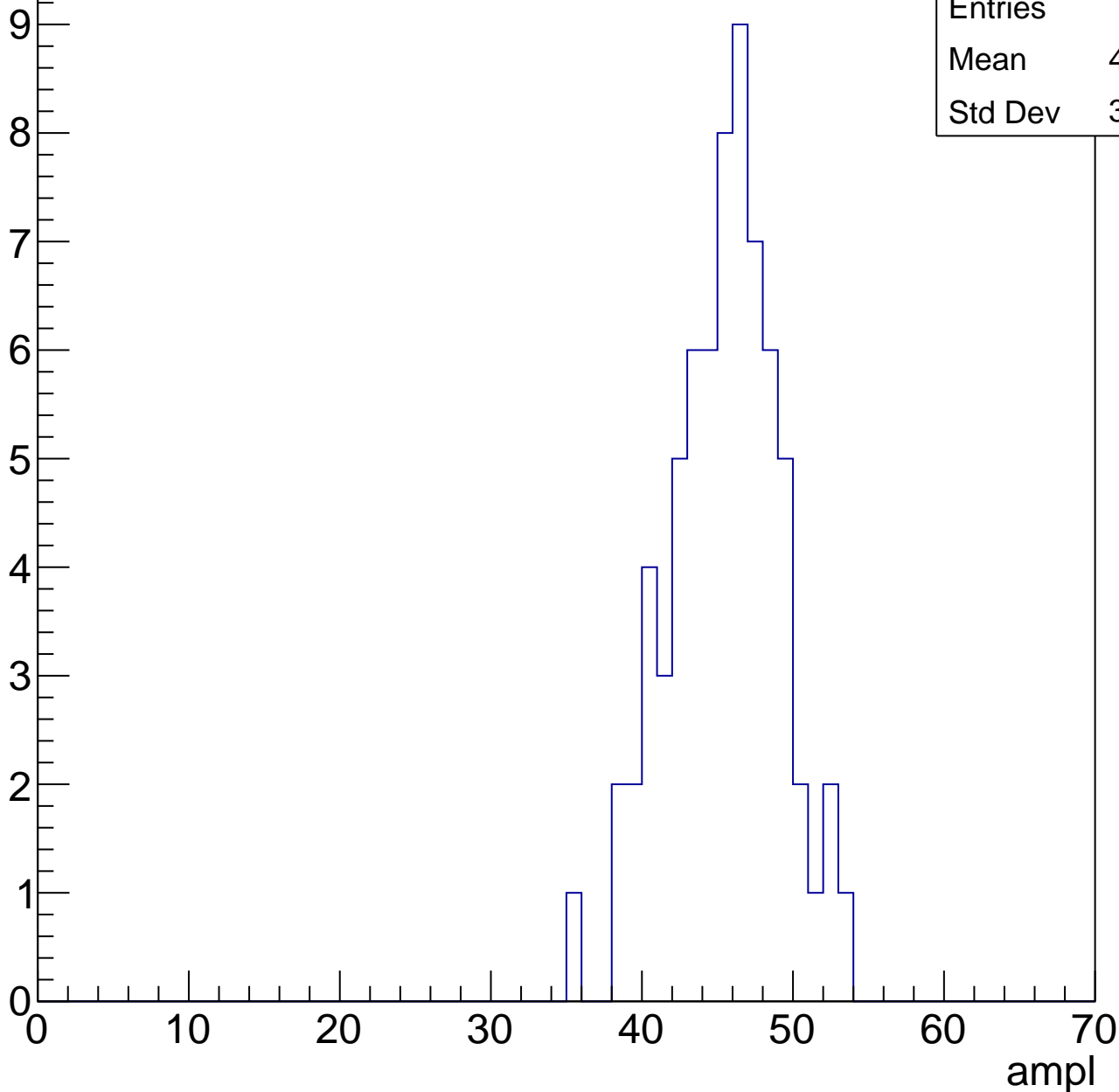


# B1L000S, U1-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	44.97
Std Dev	3.609

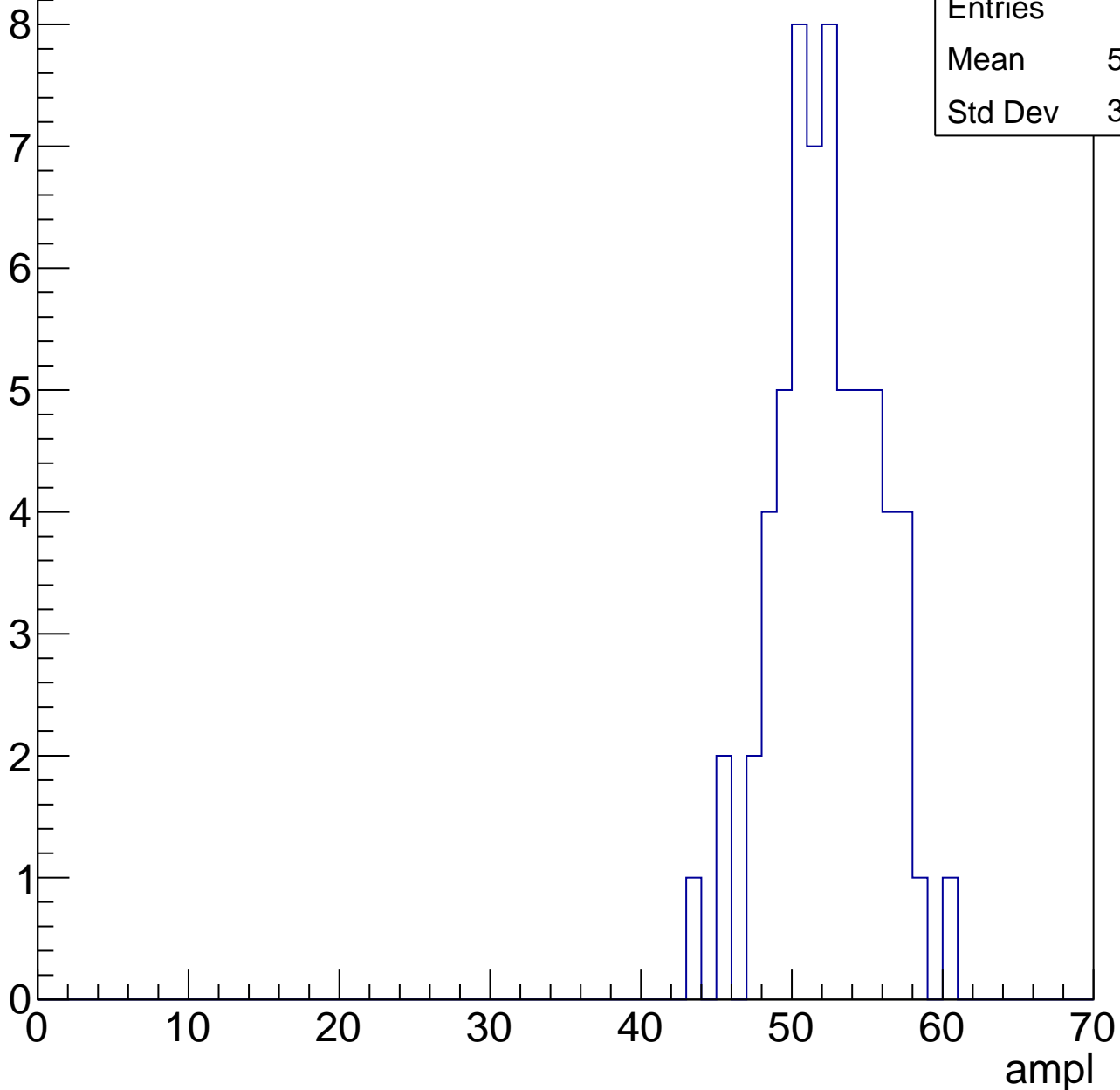


# B1L000S, U1-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

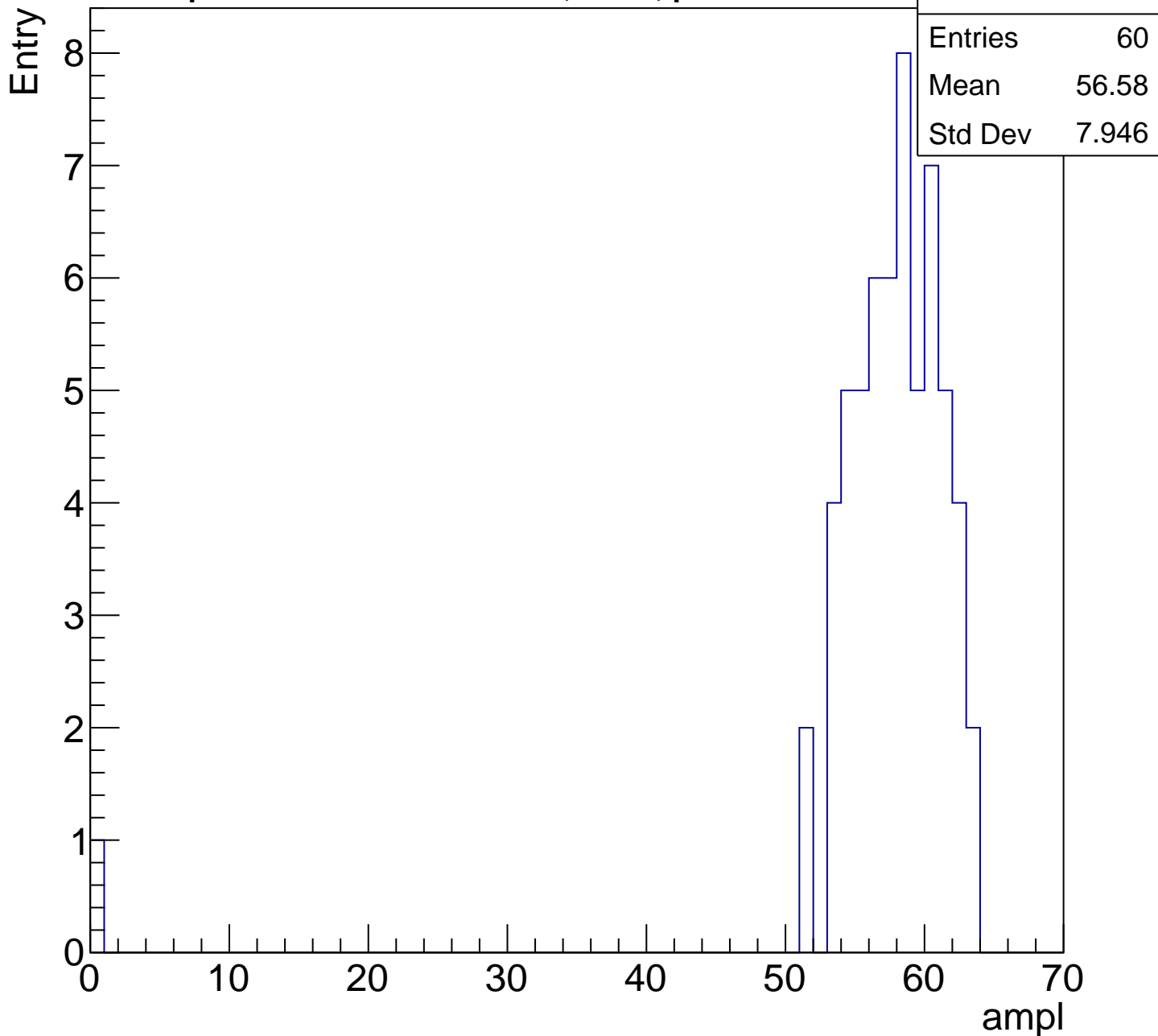
Entry

Entries	62
Mean	51.89
Std Dev	3.385



# B1L000S, U1-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

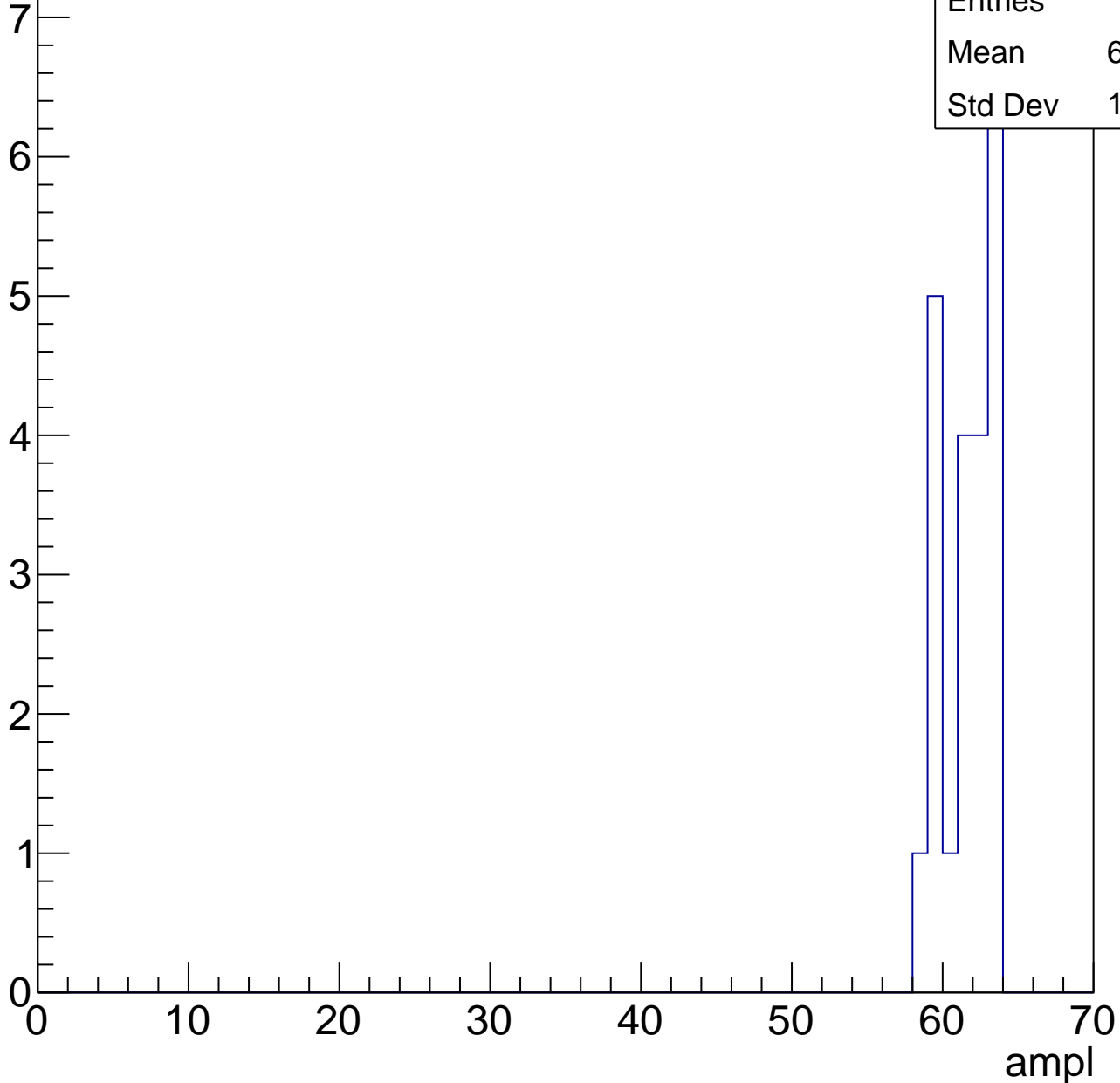


# B1L000S, U1-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	61.18
Std Dev	1.669

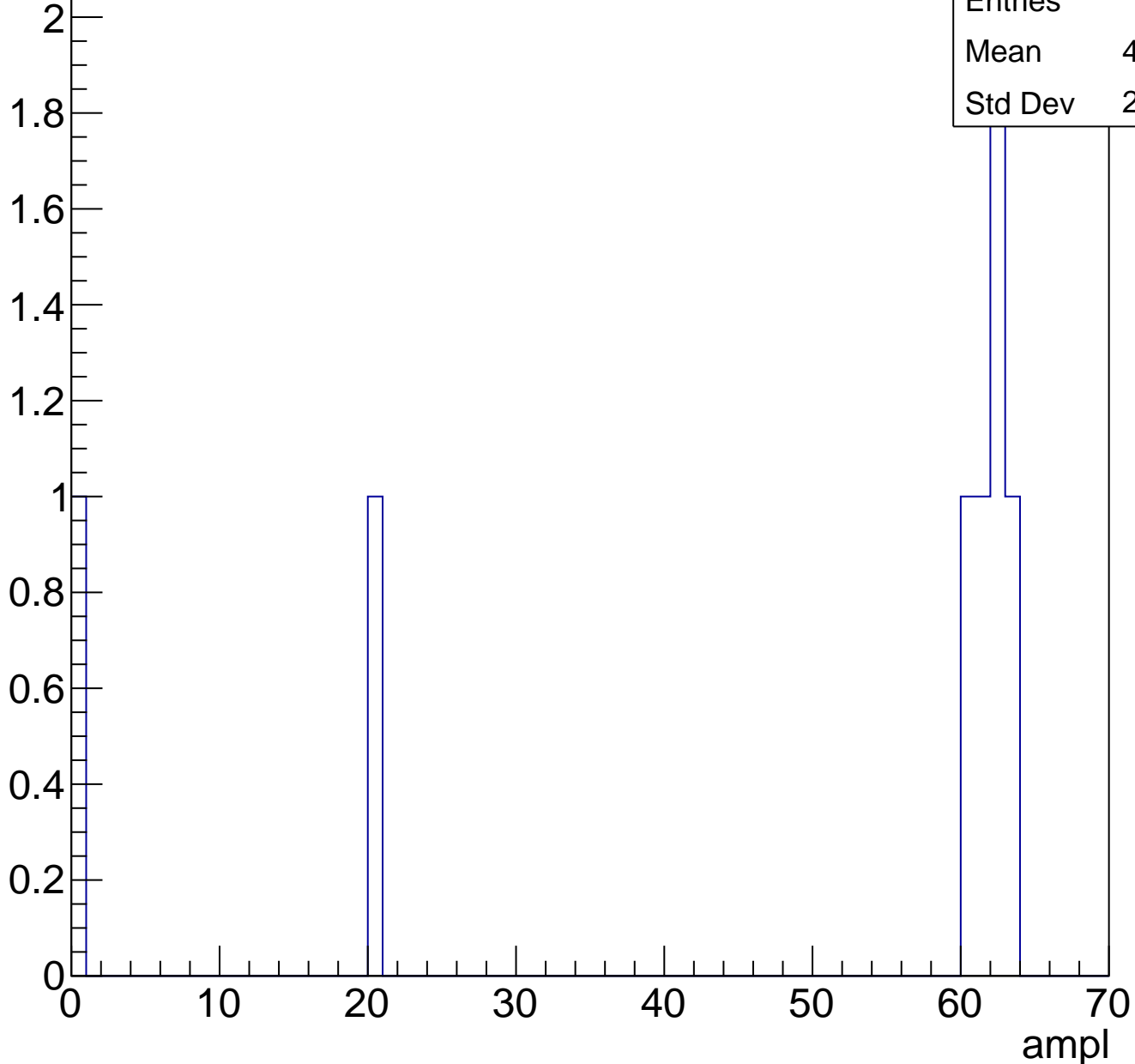




# B1L000S, U1-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	7
Mean	46.86
Std Dev	23.93

# B1L000S, U1-ch65, adc0

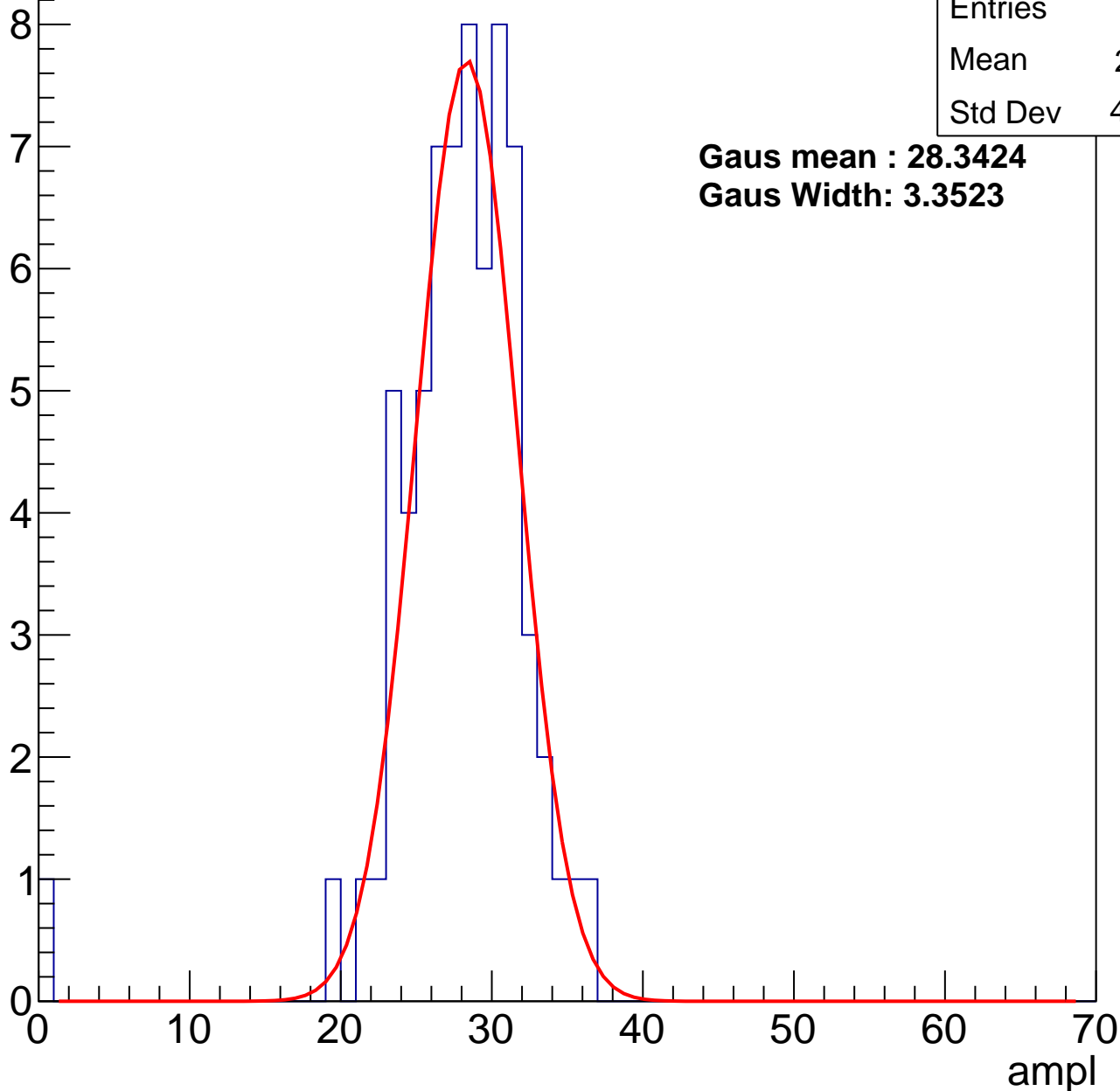
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	27.41
Std Dev	4.728

**Gaus mean : 28.3424**

**Gaus Width: 3.3523**



# B1L000S, U1-ch65, adc1

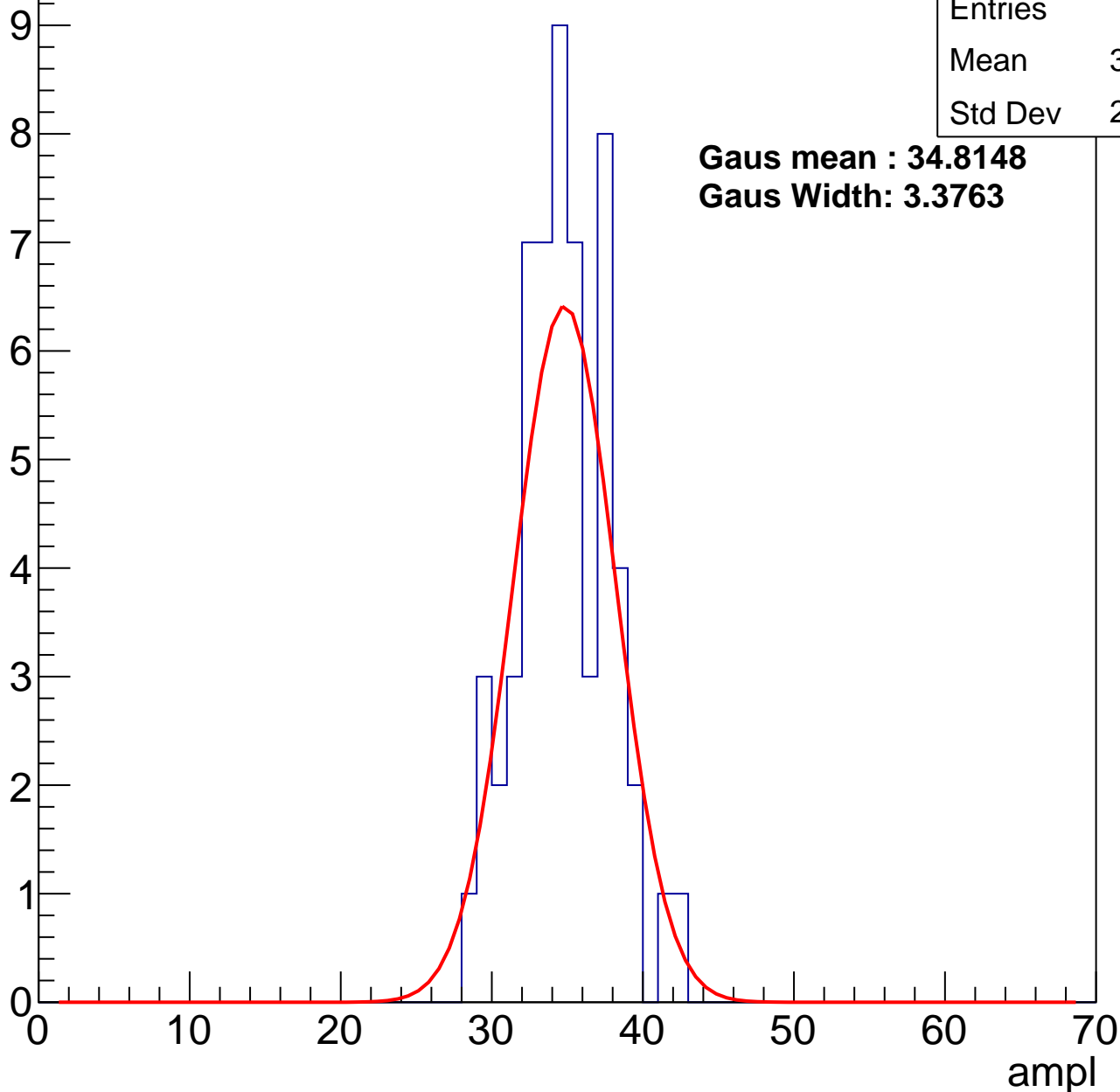
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.33
Std Dev	2.985

**Gaus mean : 34.8148**

**Gaus Width: 3.3763**



# B1L000S, U1-ch65, adc2

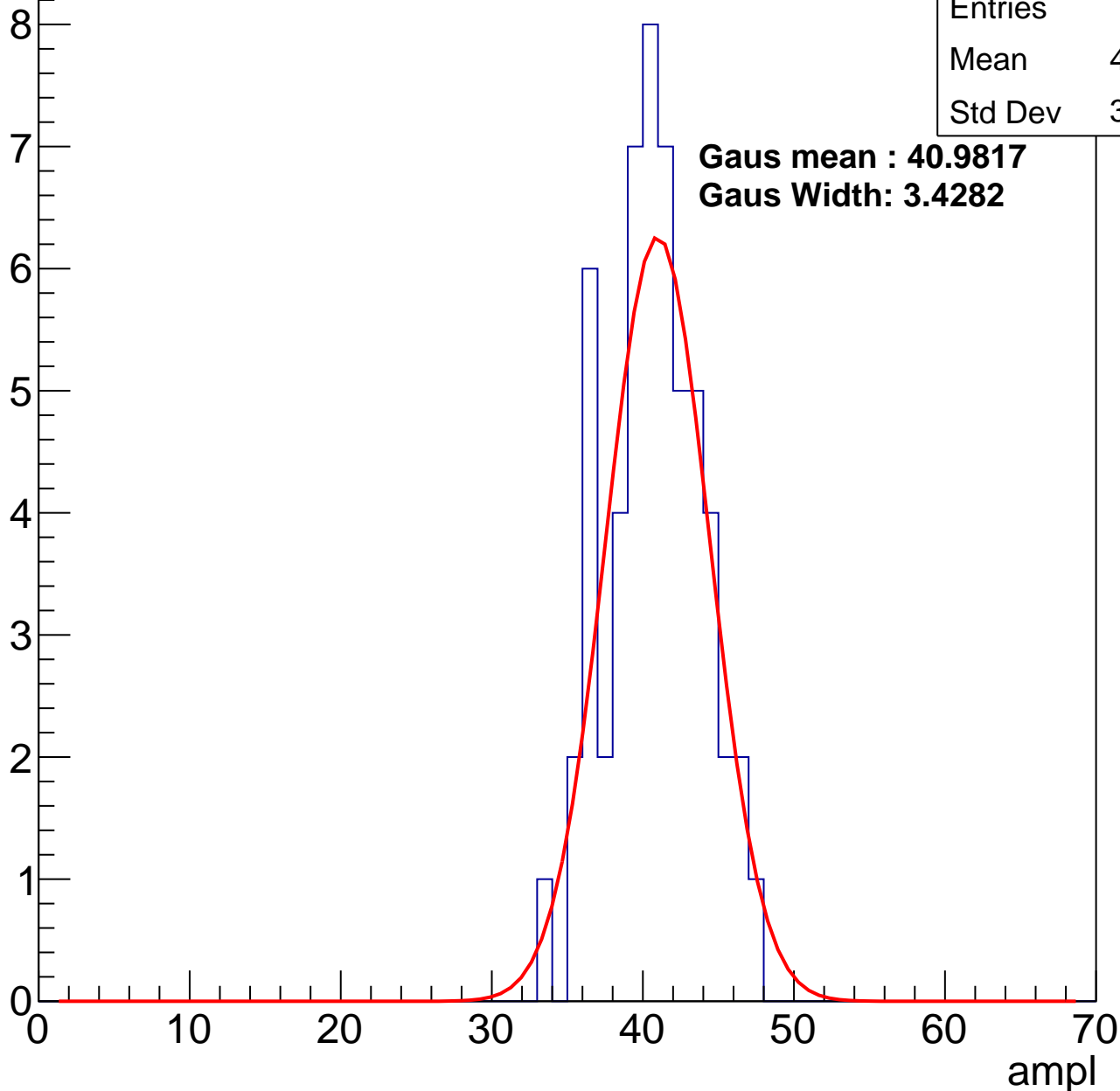
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	40.27
Std Dev	3.102

**Gaus mean : 40.9817**

**Gaus Width: 3.4282**



# B1L000S, U1-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

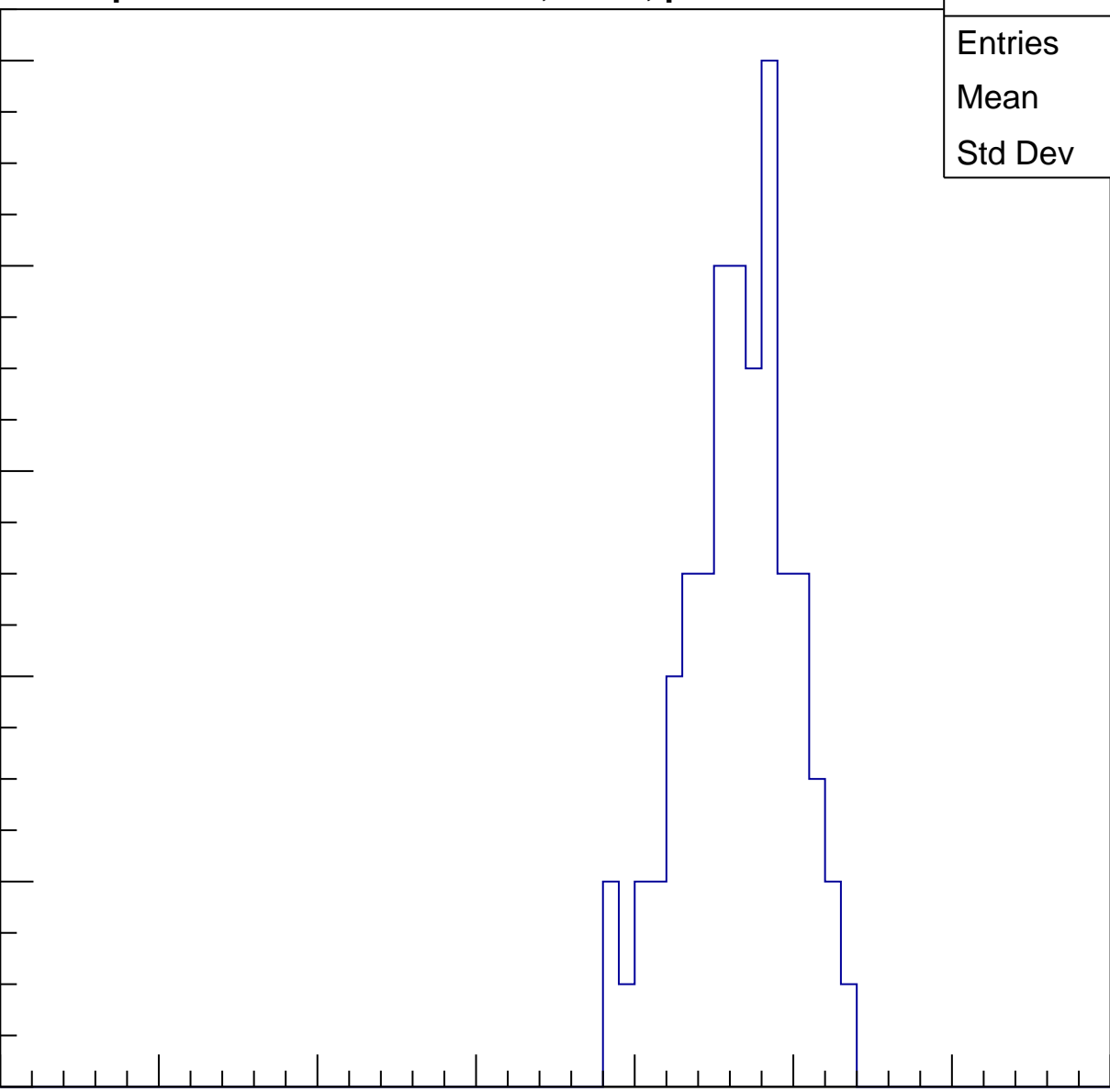
Entries	70
Mean	46.03
Std Dev	3.389

Entry

10  
8  
6  
4  
2  
0

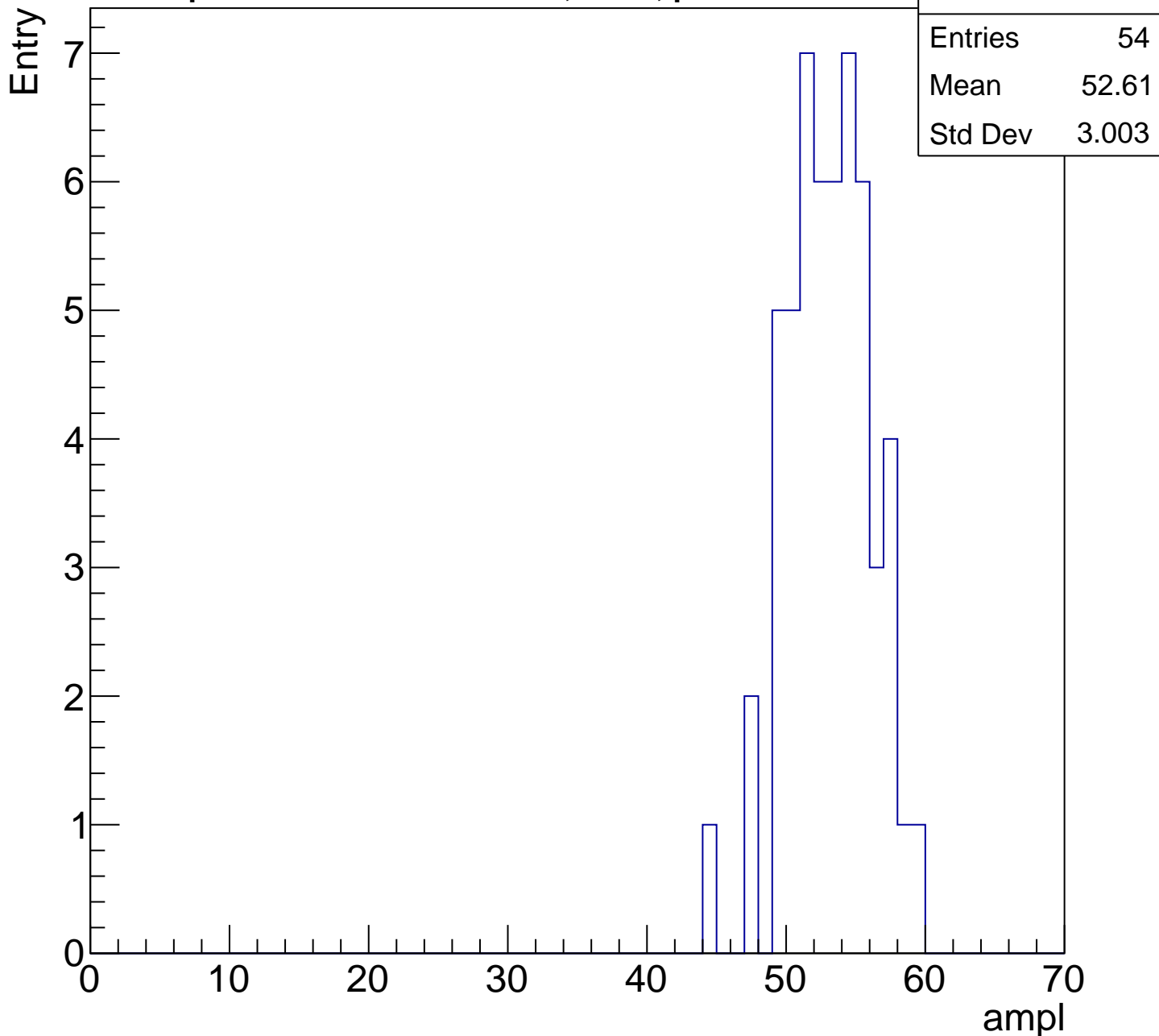
0 10 20 30 40 50 60 70

ampl



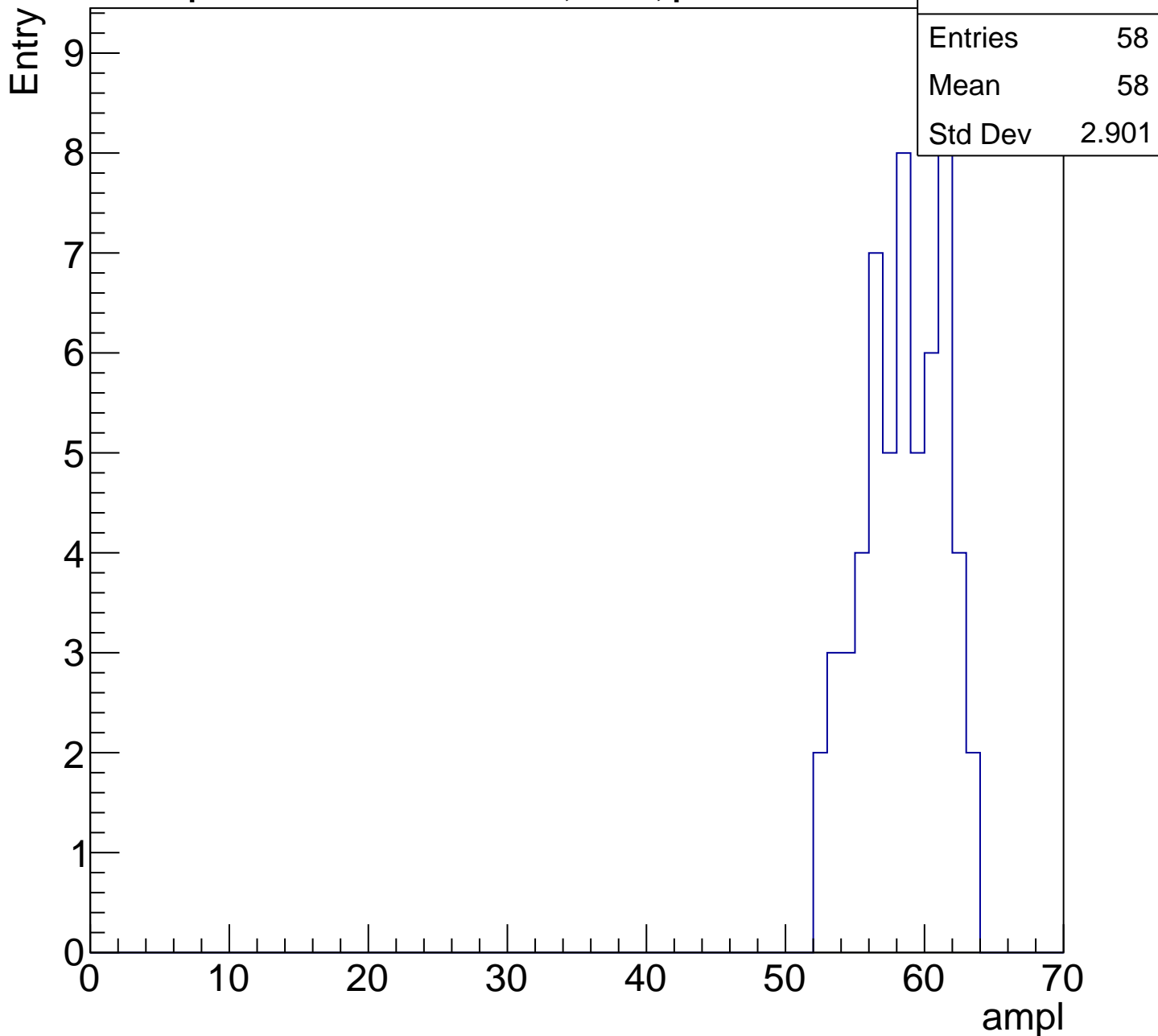
# B1L000S, U1-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

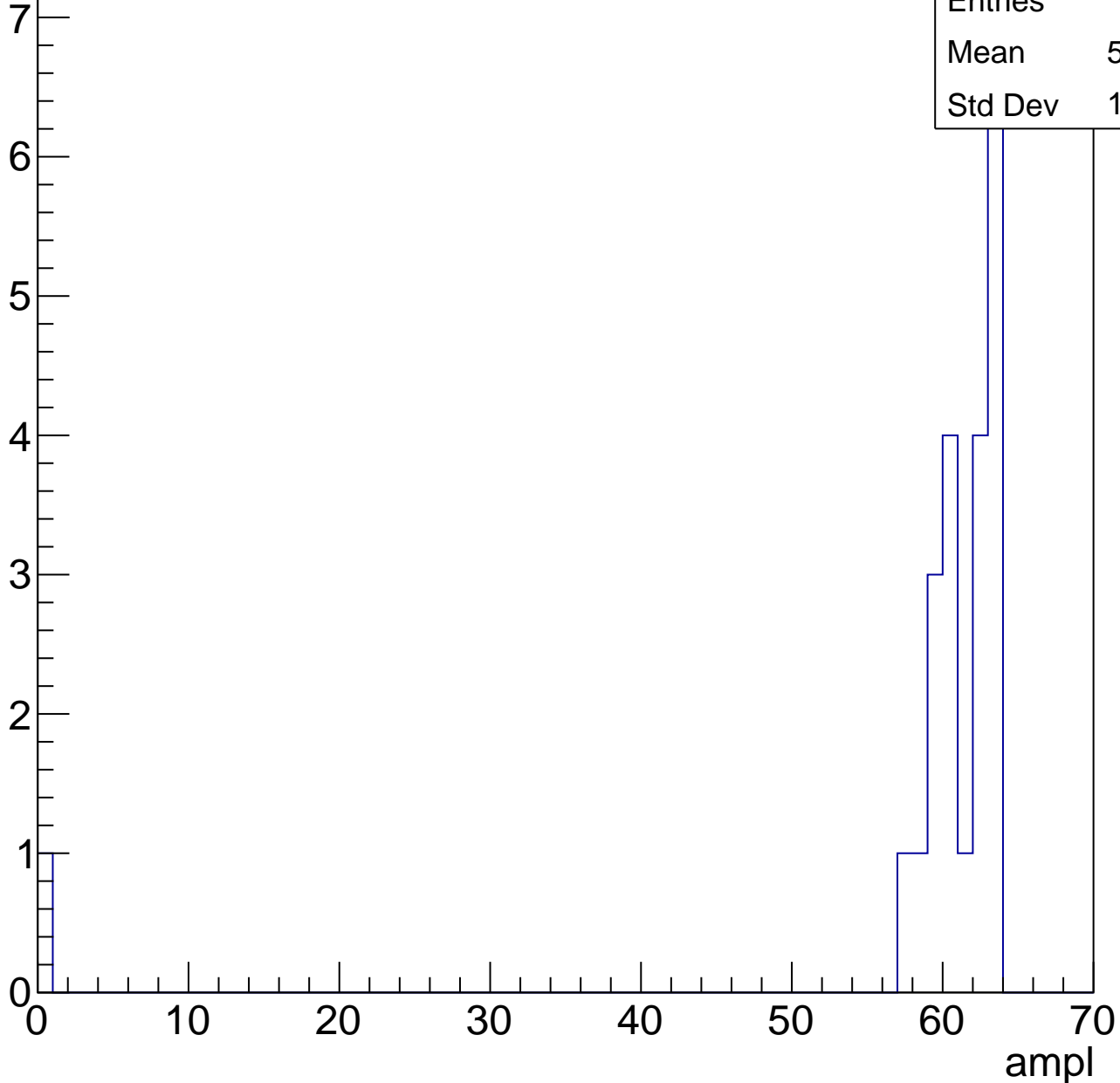


# B1L000S, U1-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.27
Std Dev	12.85

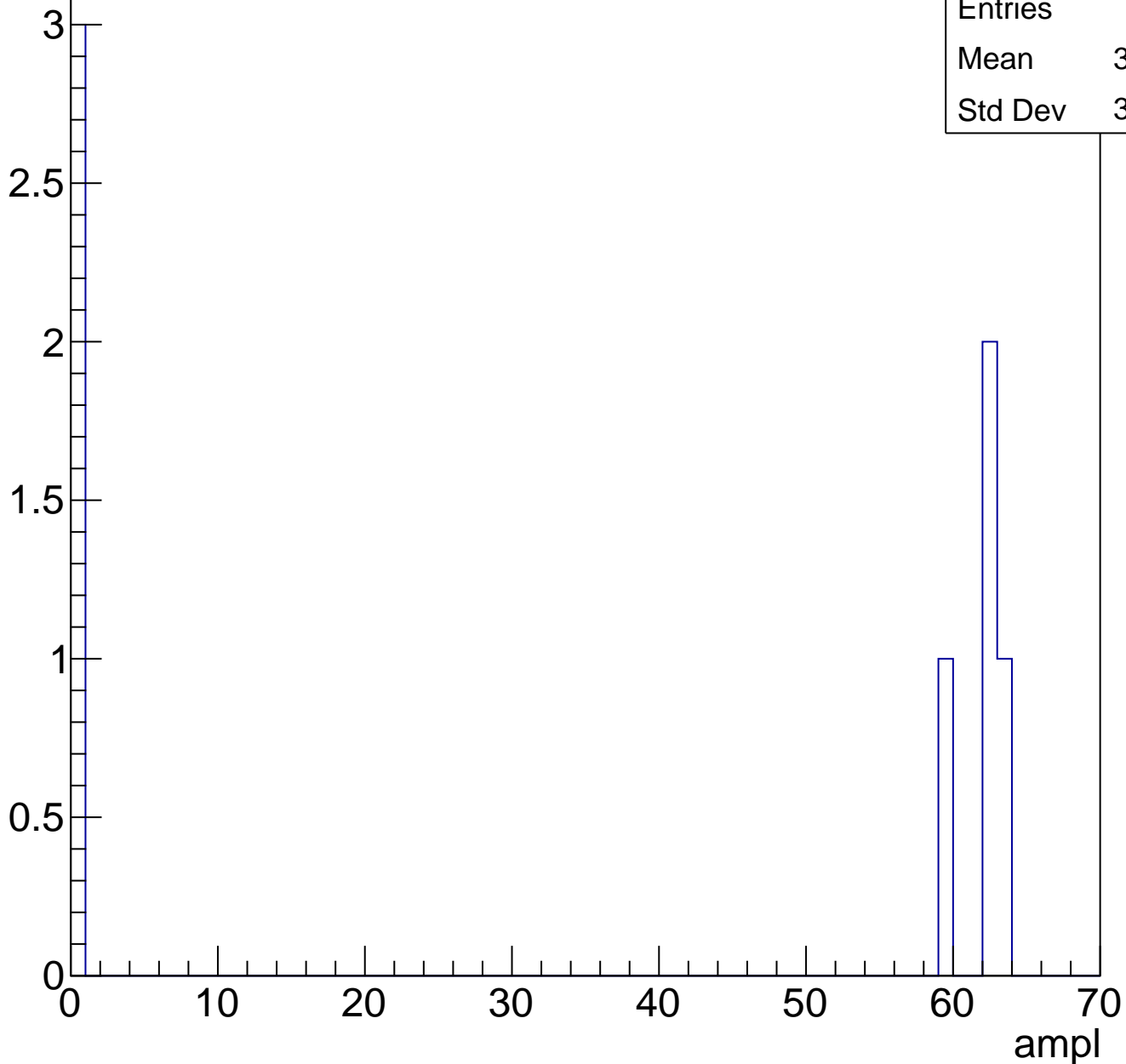




# B1L000S, U1-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch66, adc0

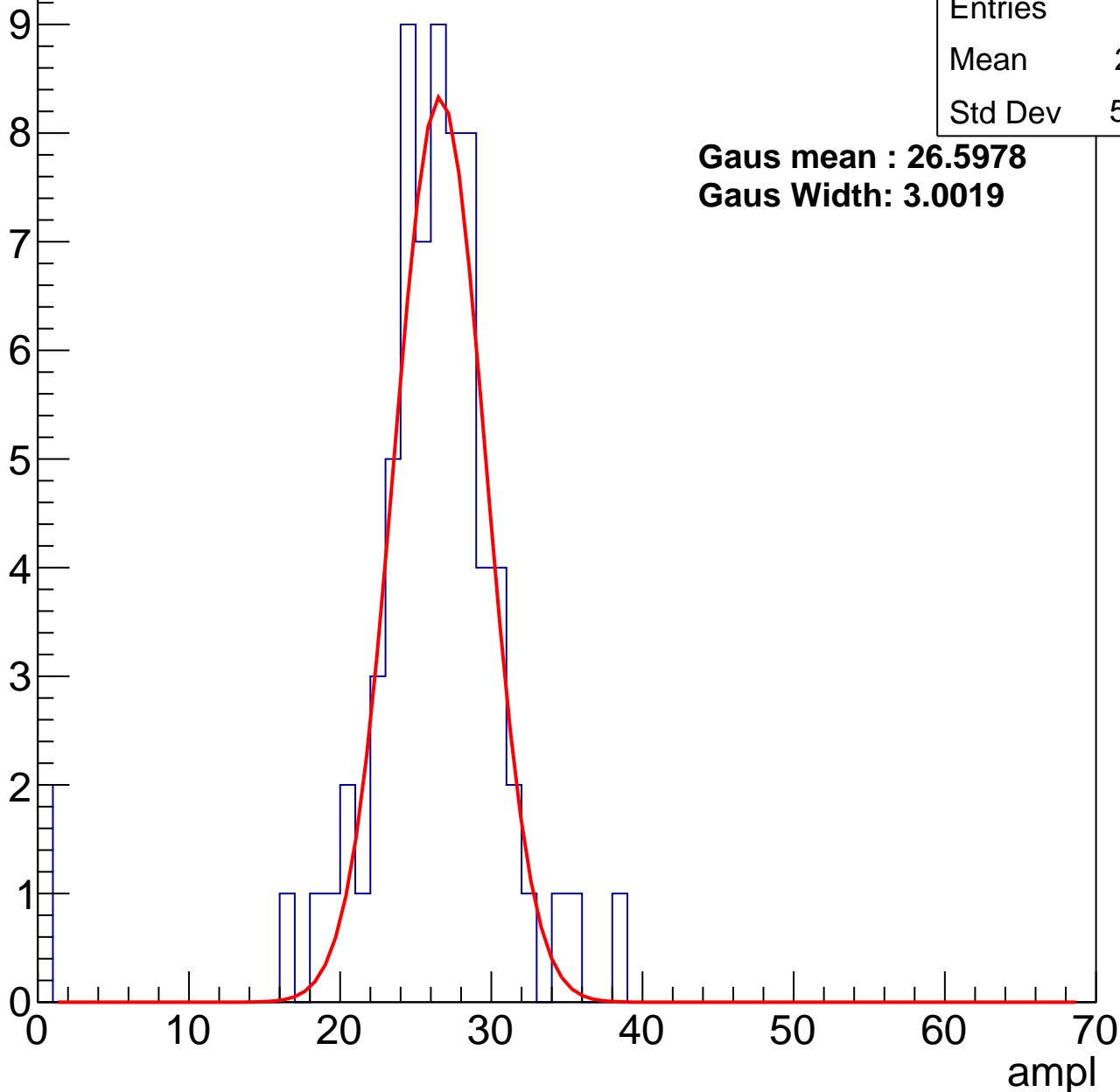
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	25.31
Std Dev	5.666

**Gaus mean : 26.5978**

**Gaus Width: 3.0019**



# B1L000S, U1-ch66, adc1

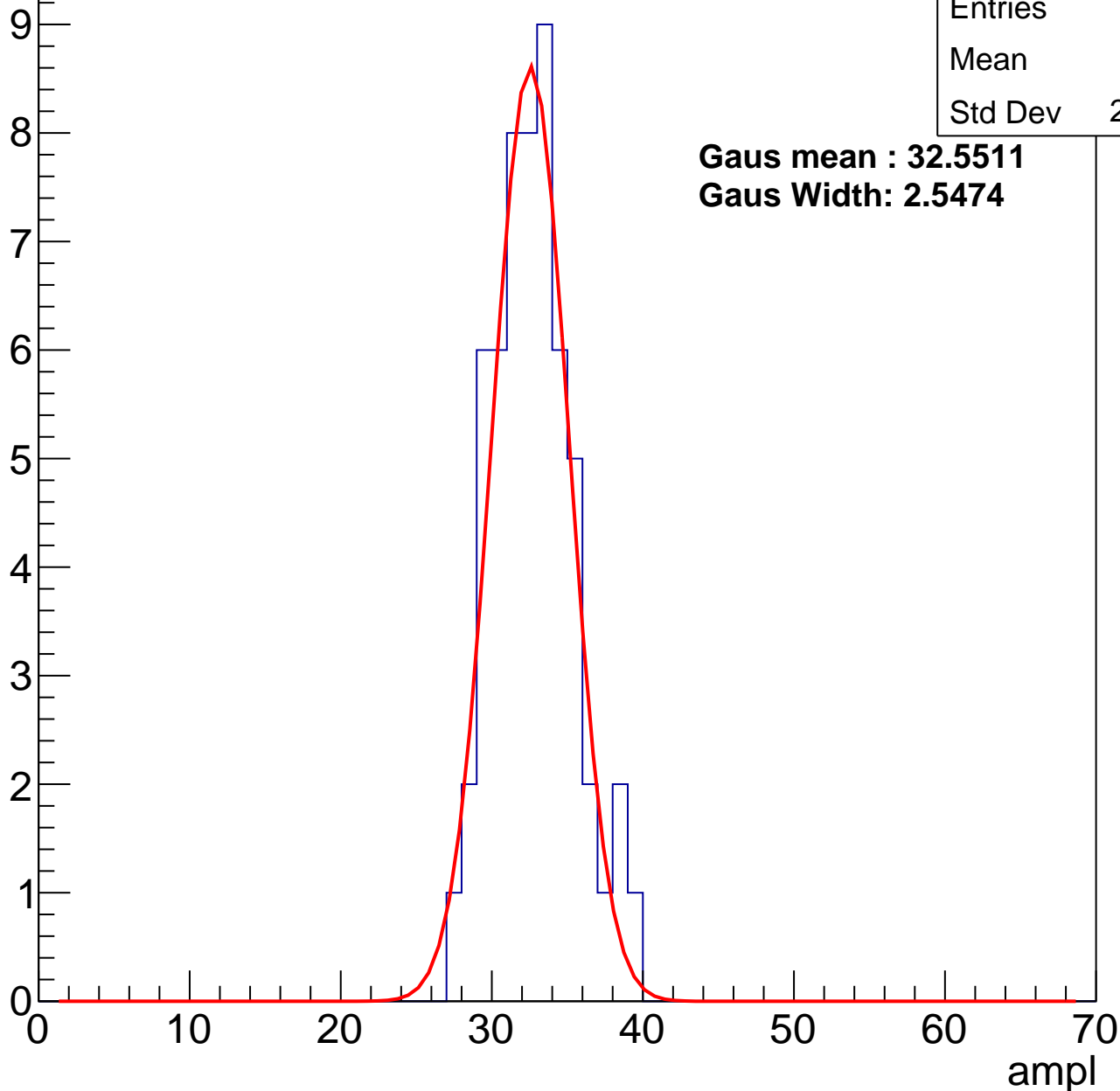
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	32.3
Std Dev	2.629

**Gaus mean : 32.5511**

**Gaus Width: 2.5474**



# B1L000S, U1-ch66, adc2

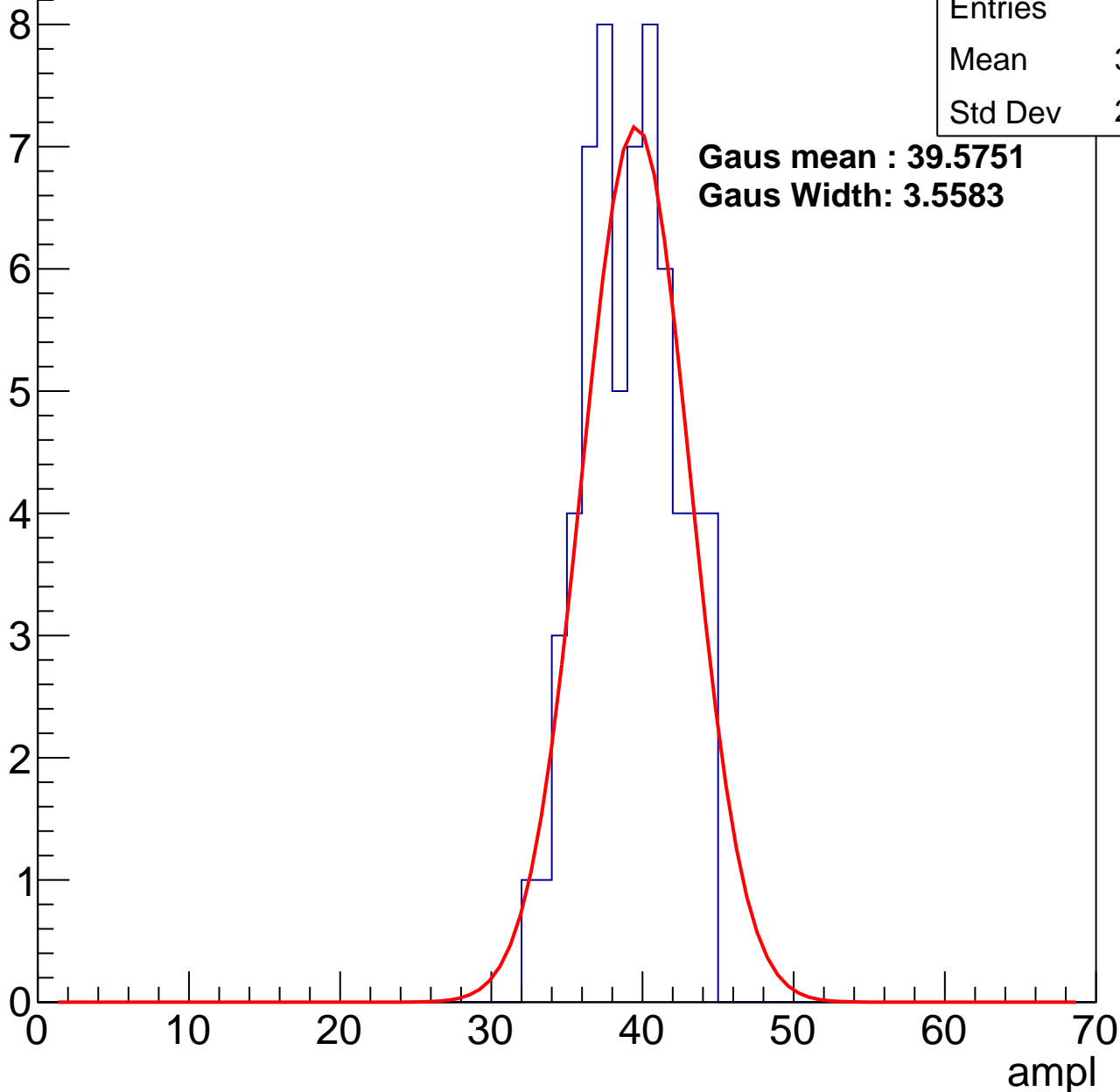
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	38.71
Std Dev	2.981

**Gaus mean : 39.5751**

**Gaus Width: 3.5583**

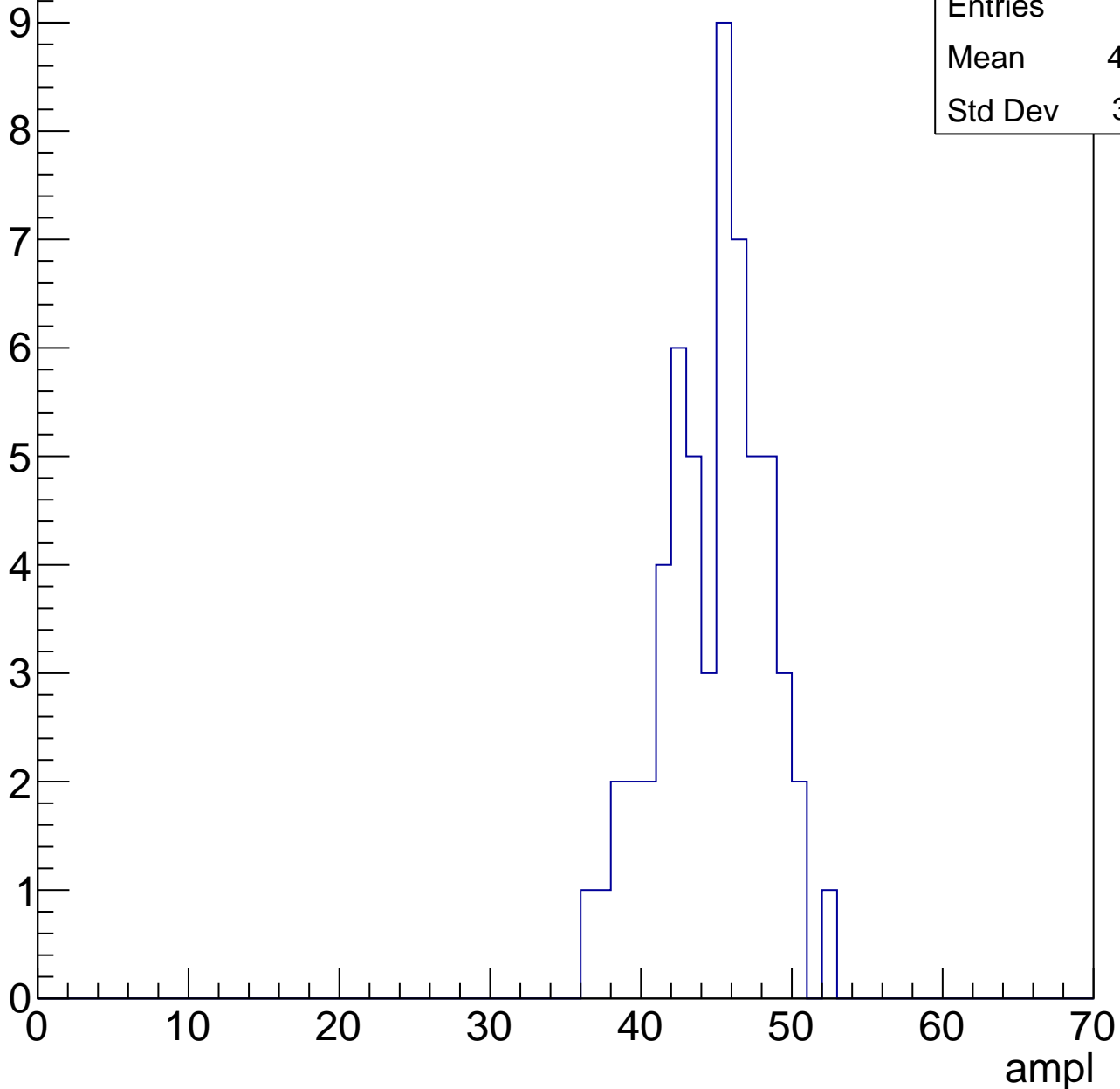


# B1L000S, U1-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	44.33
Std Dev	3.471

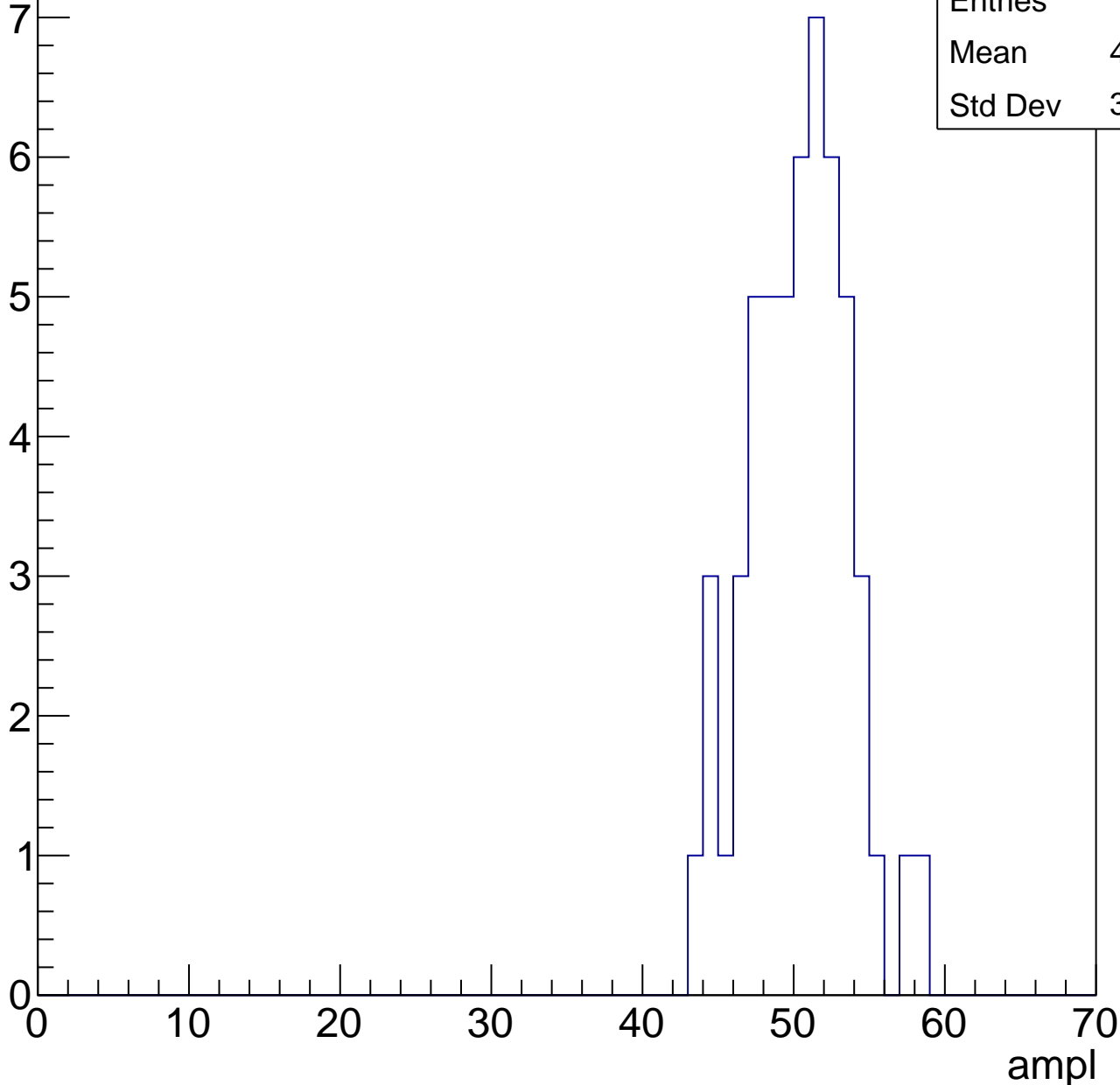


# B1L000S, U1-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	49.89
Std Dev	3.254

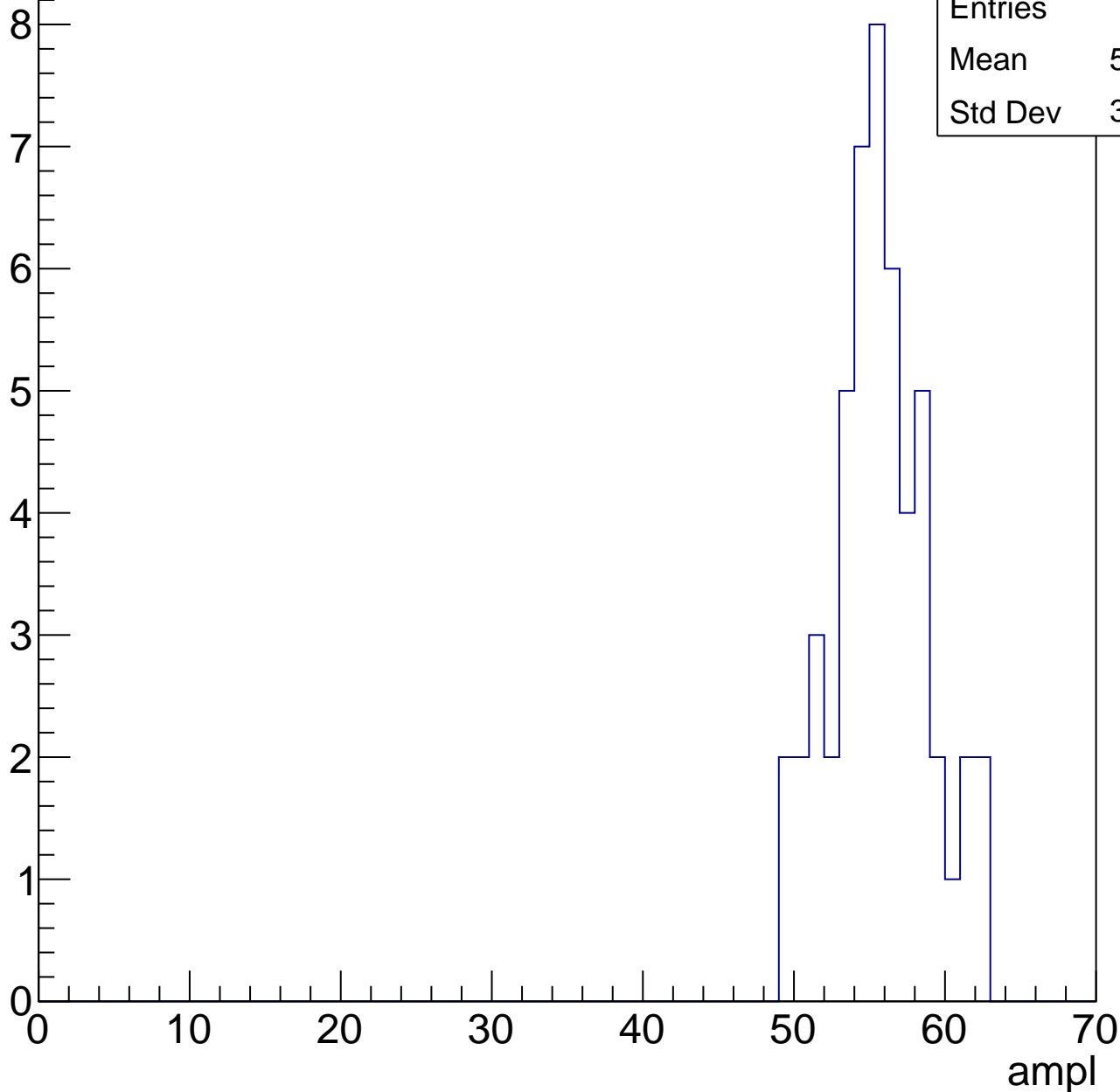


# B1L000S, U1-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	55.22
Std Dev	3.152

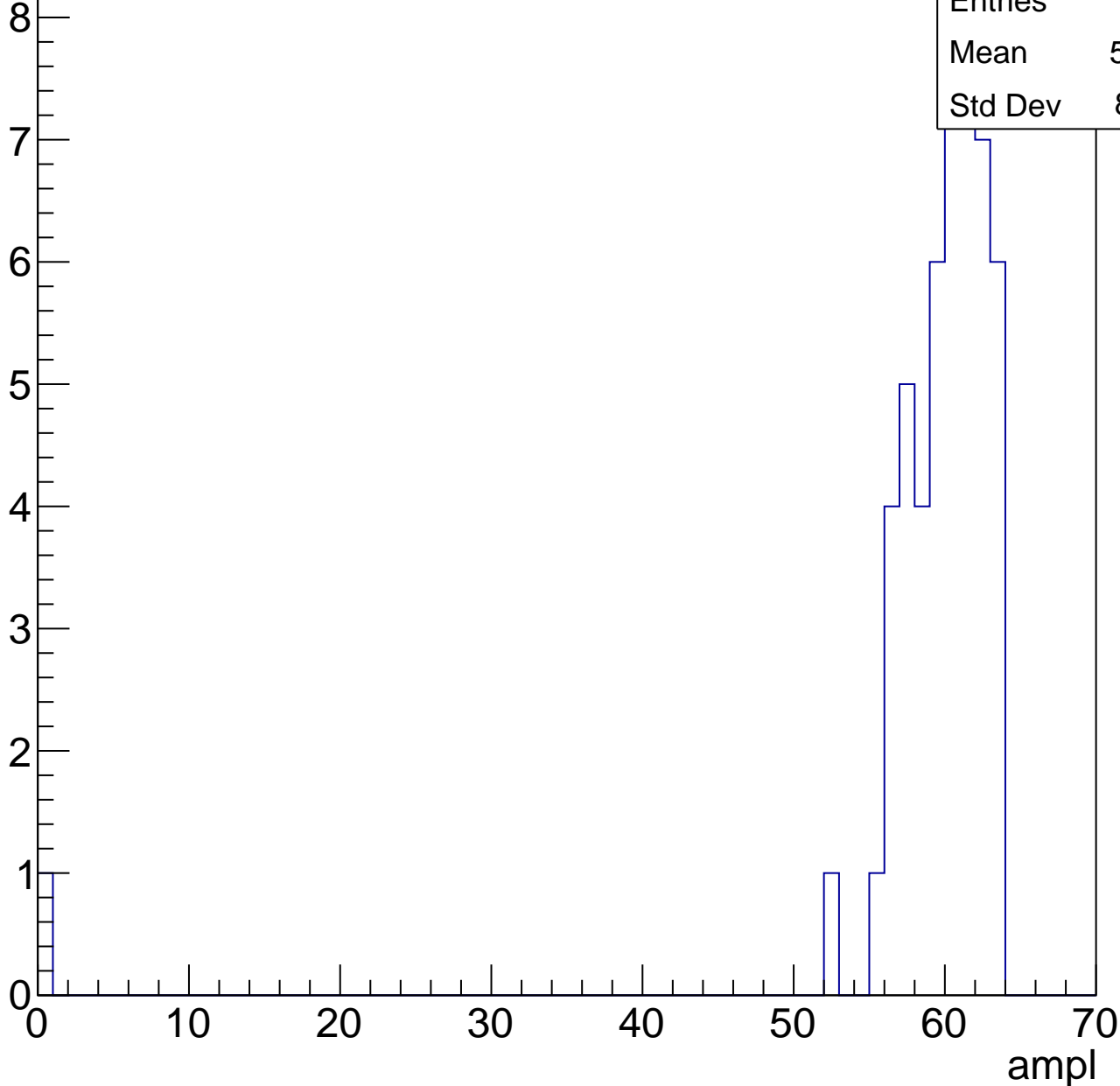


# B1L000S, U1-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	58.47
Std Dev	8.621

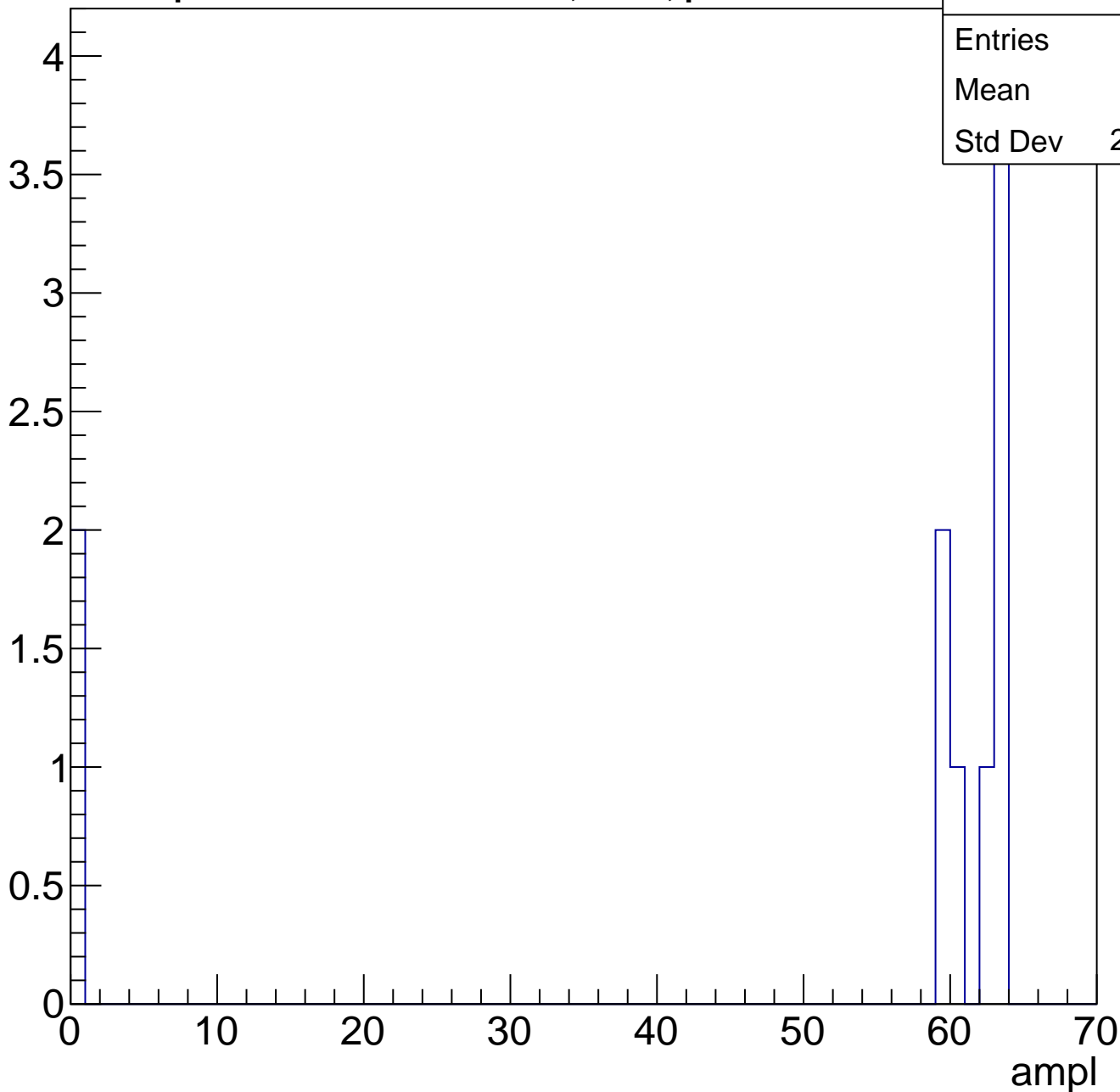




# B1L000S, U1-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch67, adc0

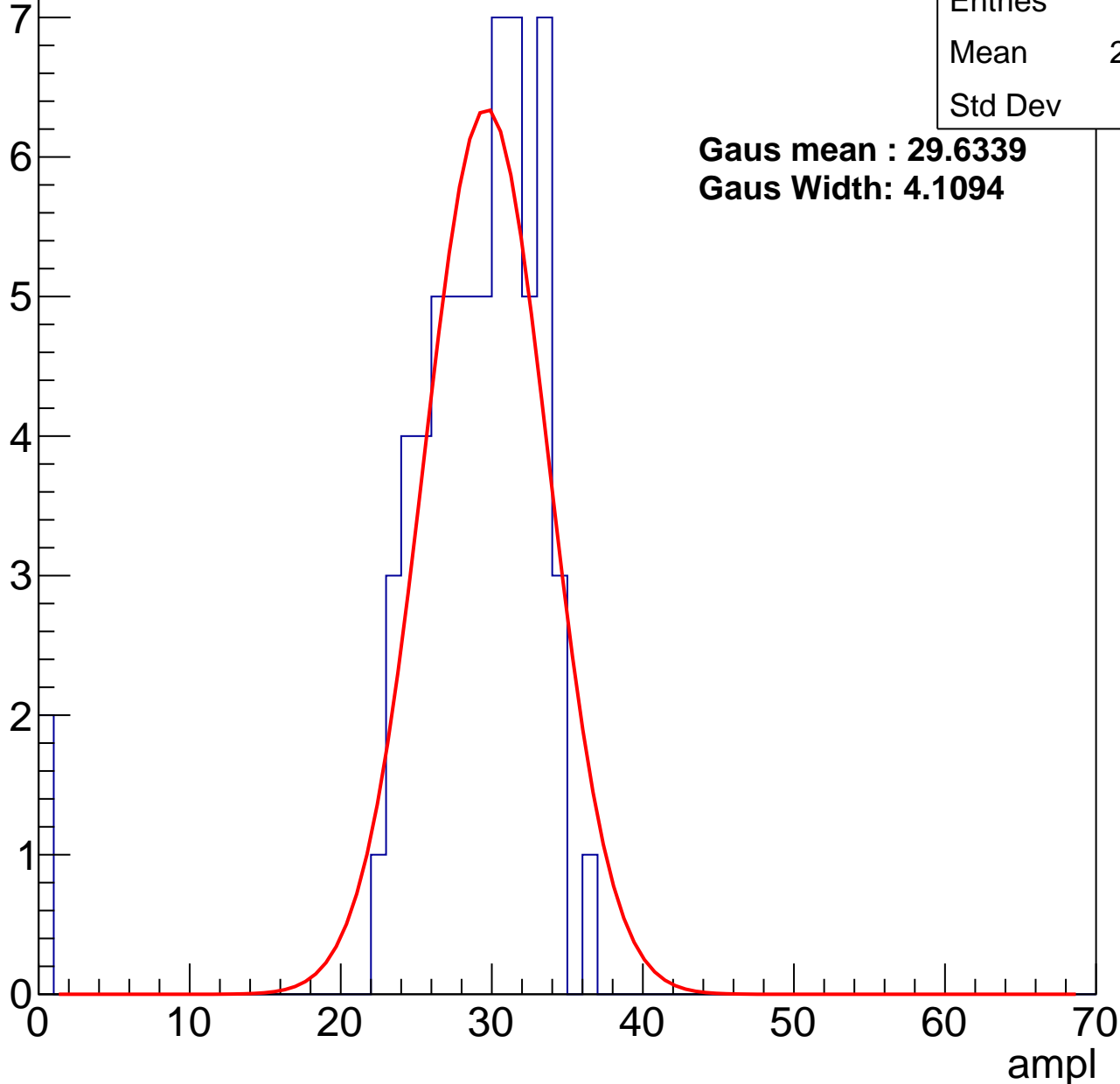
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.02
Std Dev	6.03

**Gaus mean : 29.6339**

**Gaus Width: 4.1094**



# B1L000S, U1-ch67, adc1

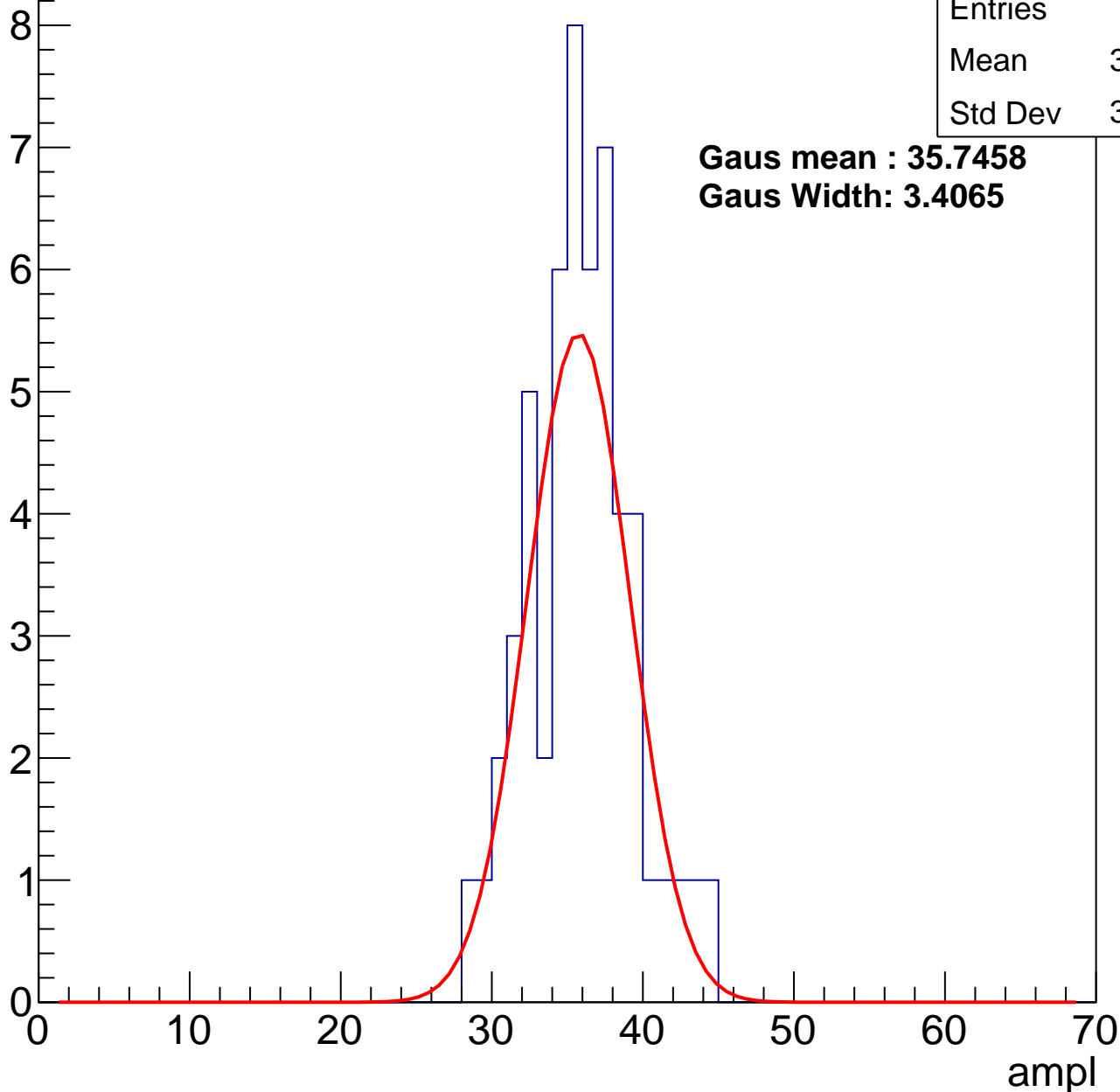
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	35.43
Std Dev	3.392

**Gaus mean : 35.7458**

**Gaus Width: 3.4065**



# B1L000S, U1-ch67, adc2

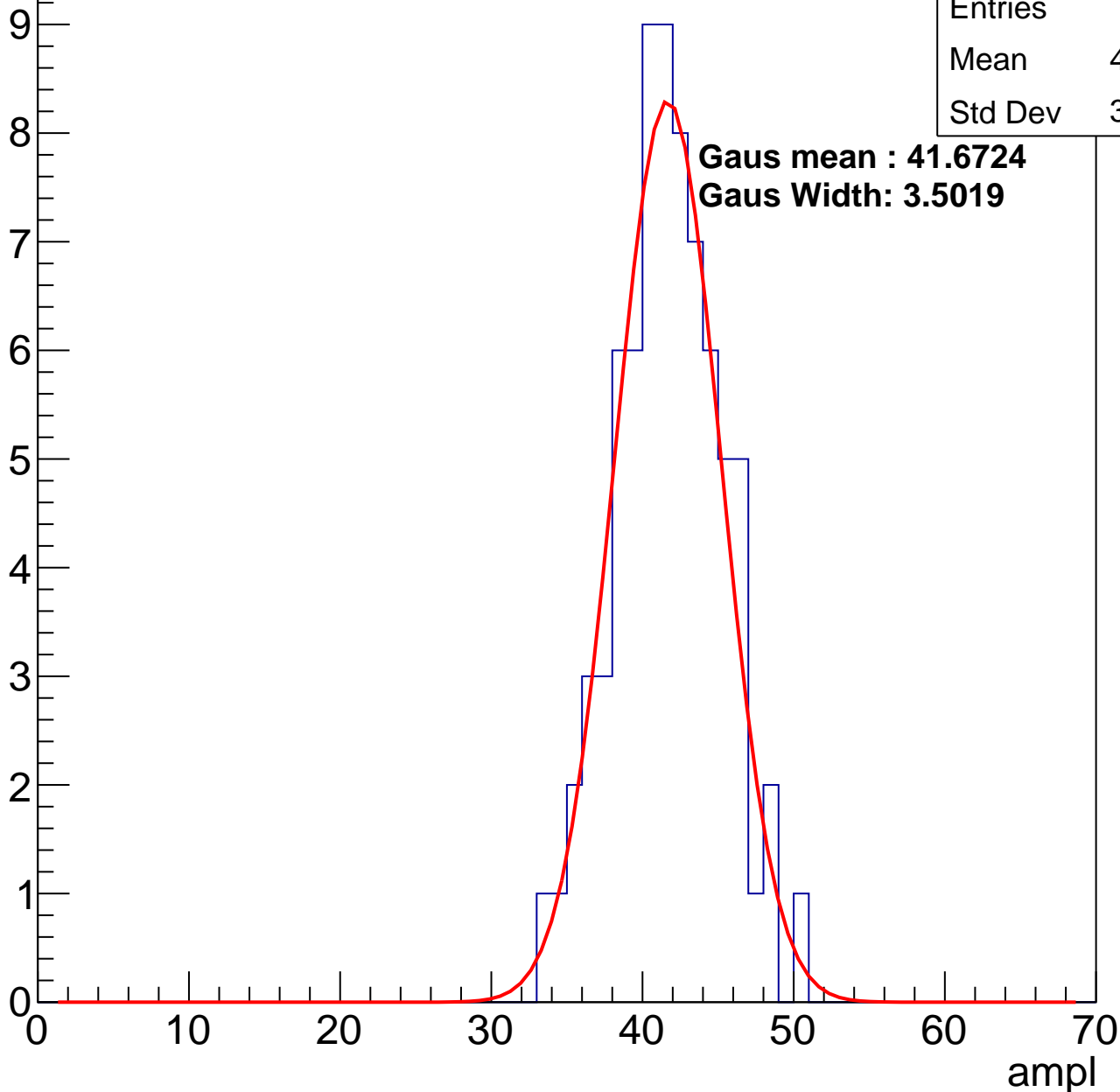
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	41.28
Std Dev	3.462

**Gaus mean : 41.6724**

**Gaus Width: 3.5019**

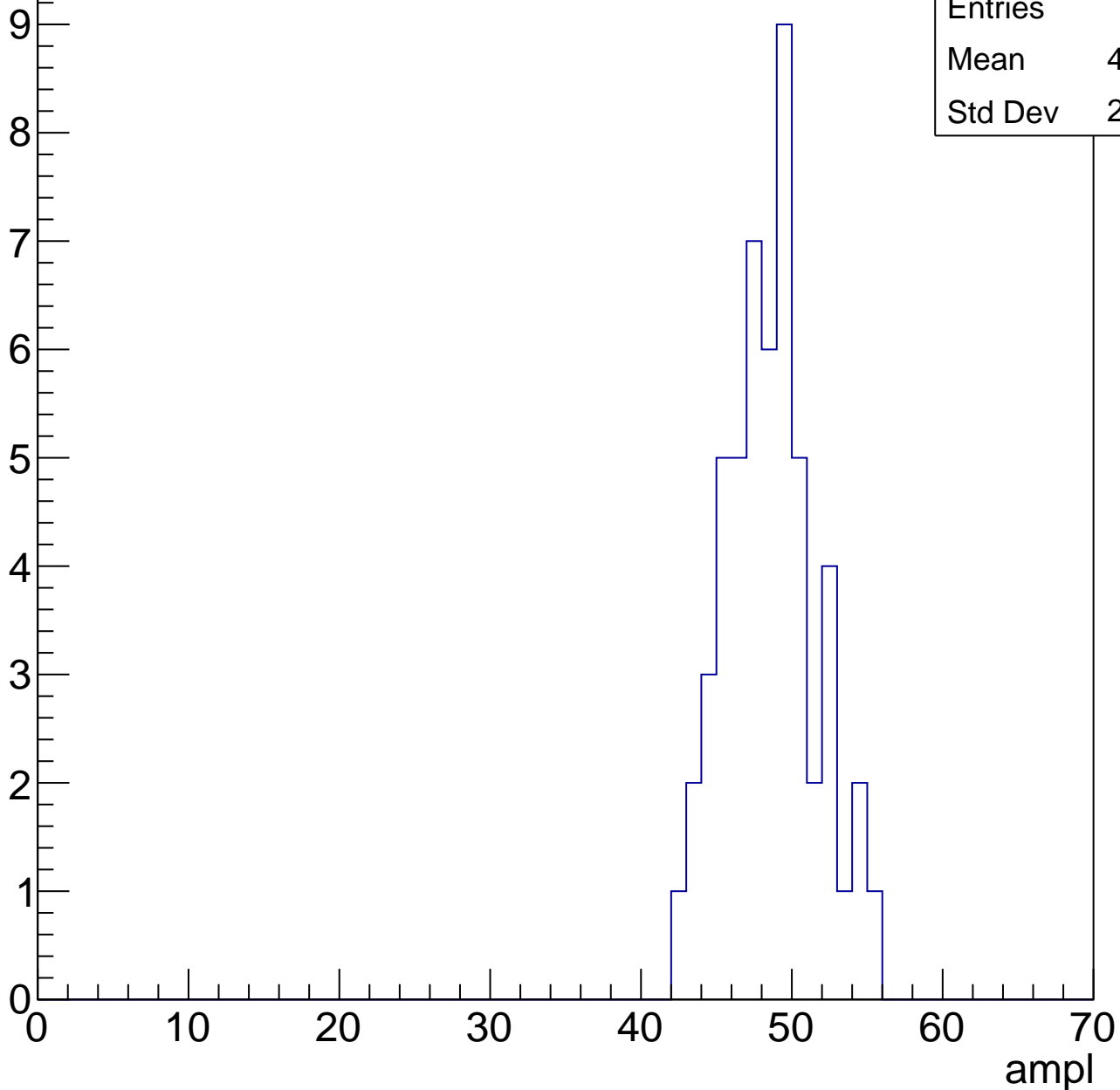


# B1L000S, U1-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

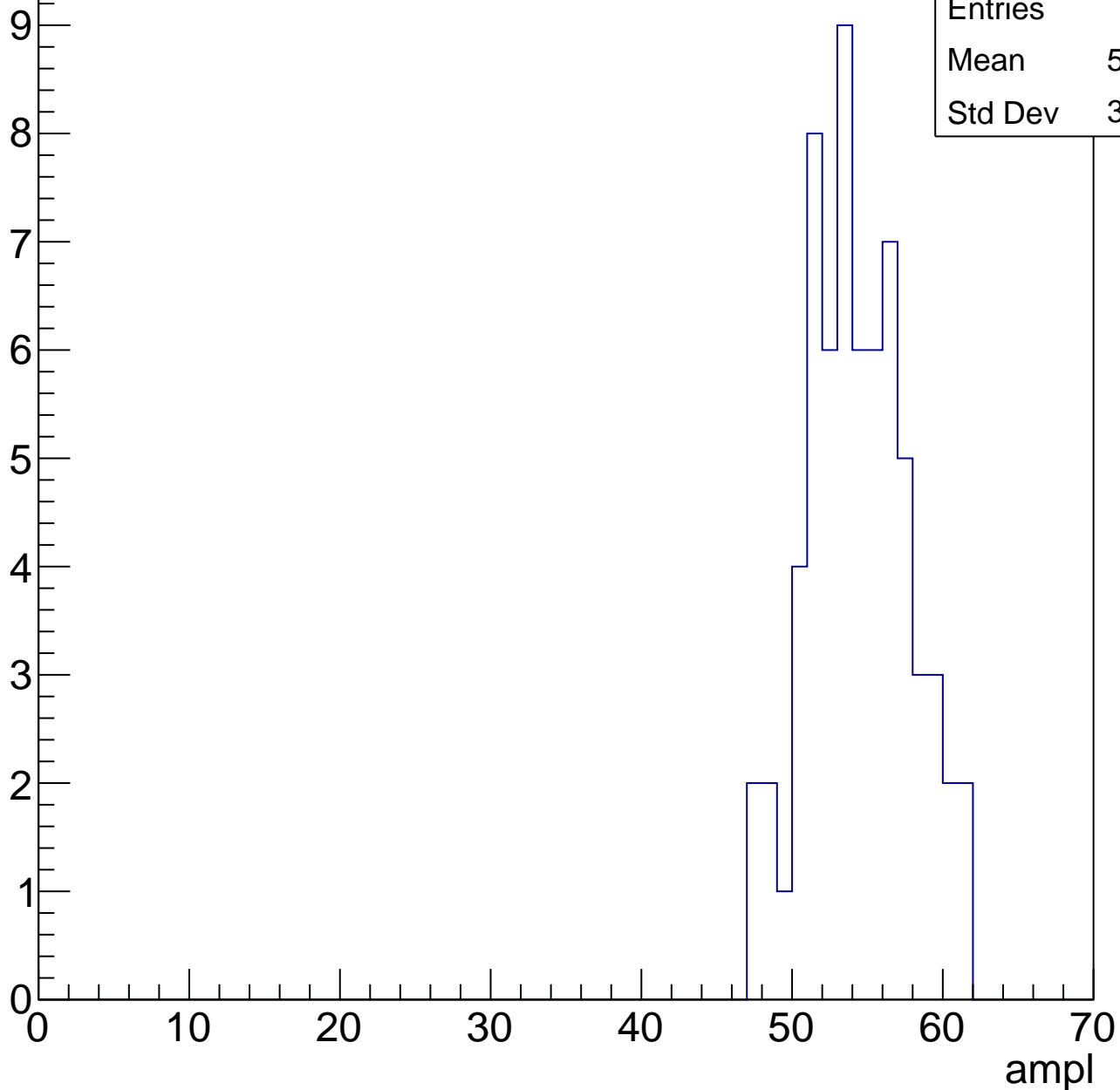
Entries	53
Mean	48.09
Std Dev	2.954



# B1L000S, U1-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

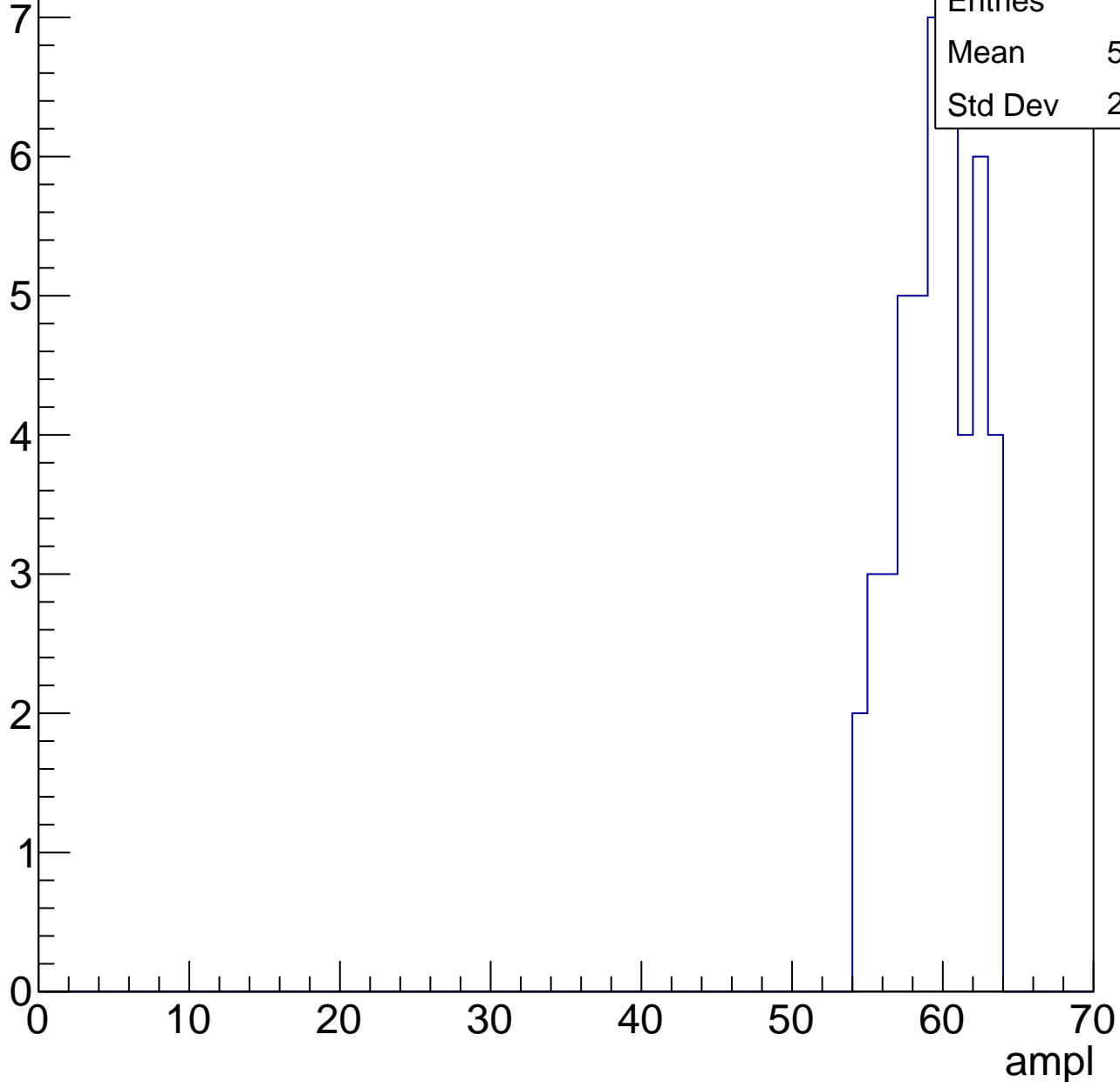
Entry



# B1L000S, U1-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



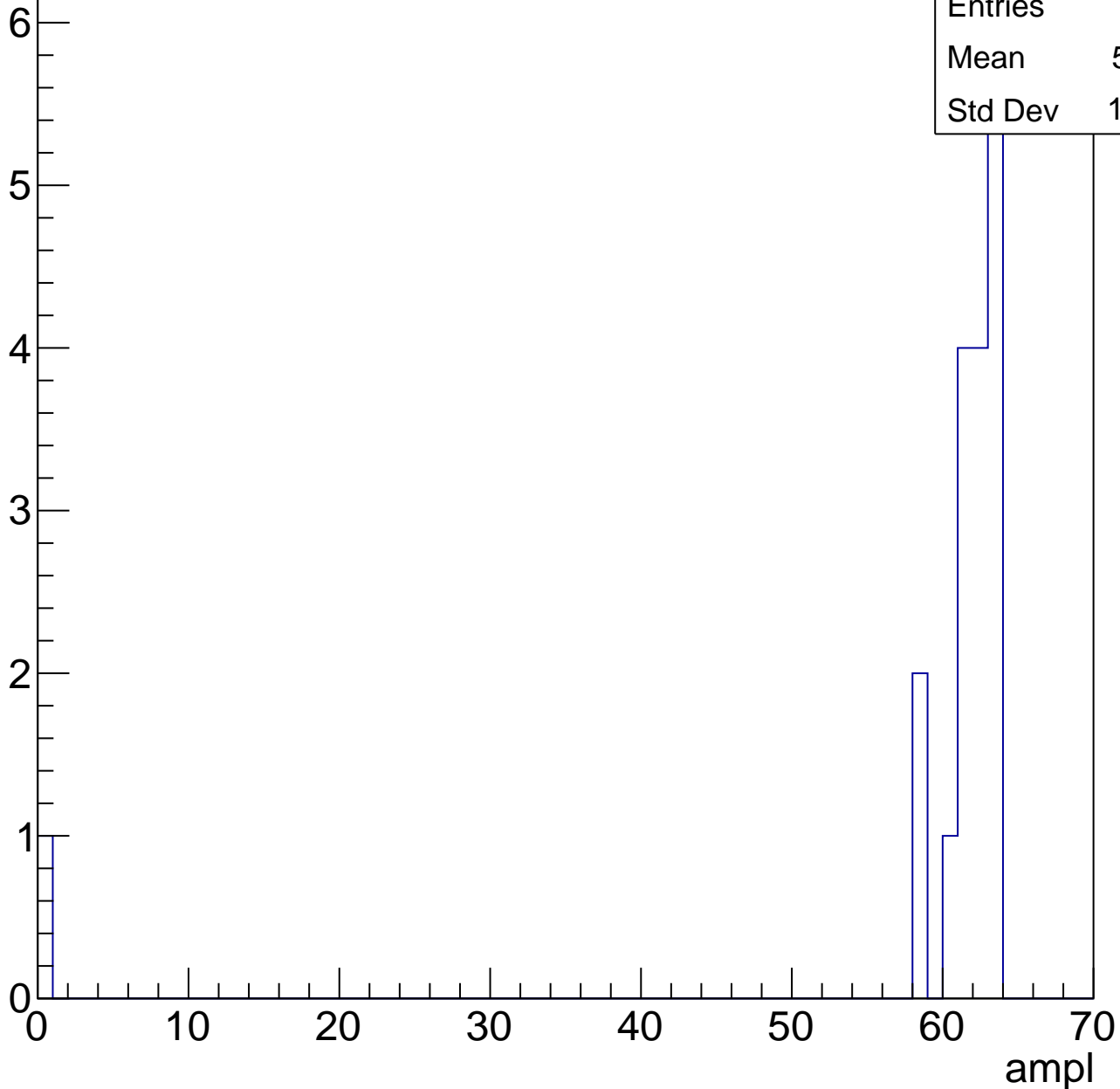
Entries	46
Mean	59.07
Std Dev	2.514

# B1L000S, U1-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.11
Std Dev	14.18





# B1L000S, U1-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U1-ch68, adc0

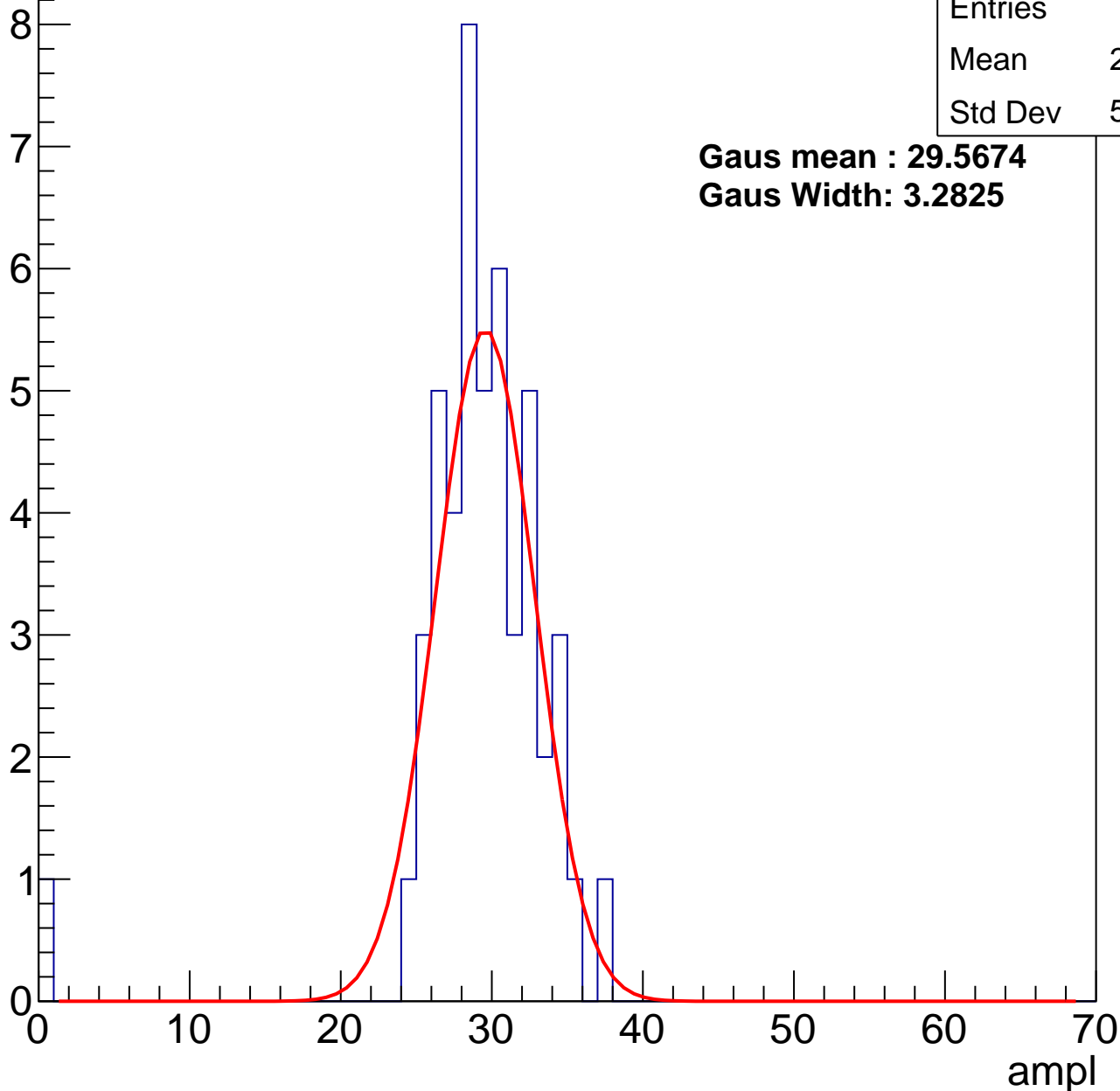
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	28.73
Std Dev	5.102

**Gaus mean : 29.5674**

**Gaus Width: 3.2825**



# B1L000S, U1-ch68, adc1

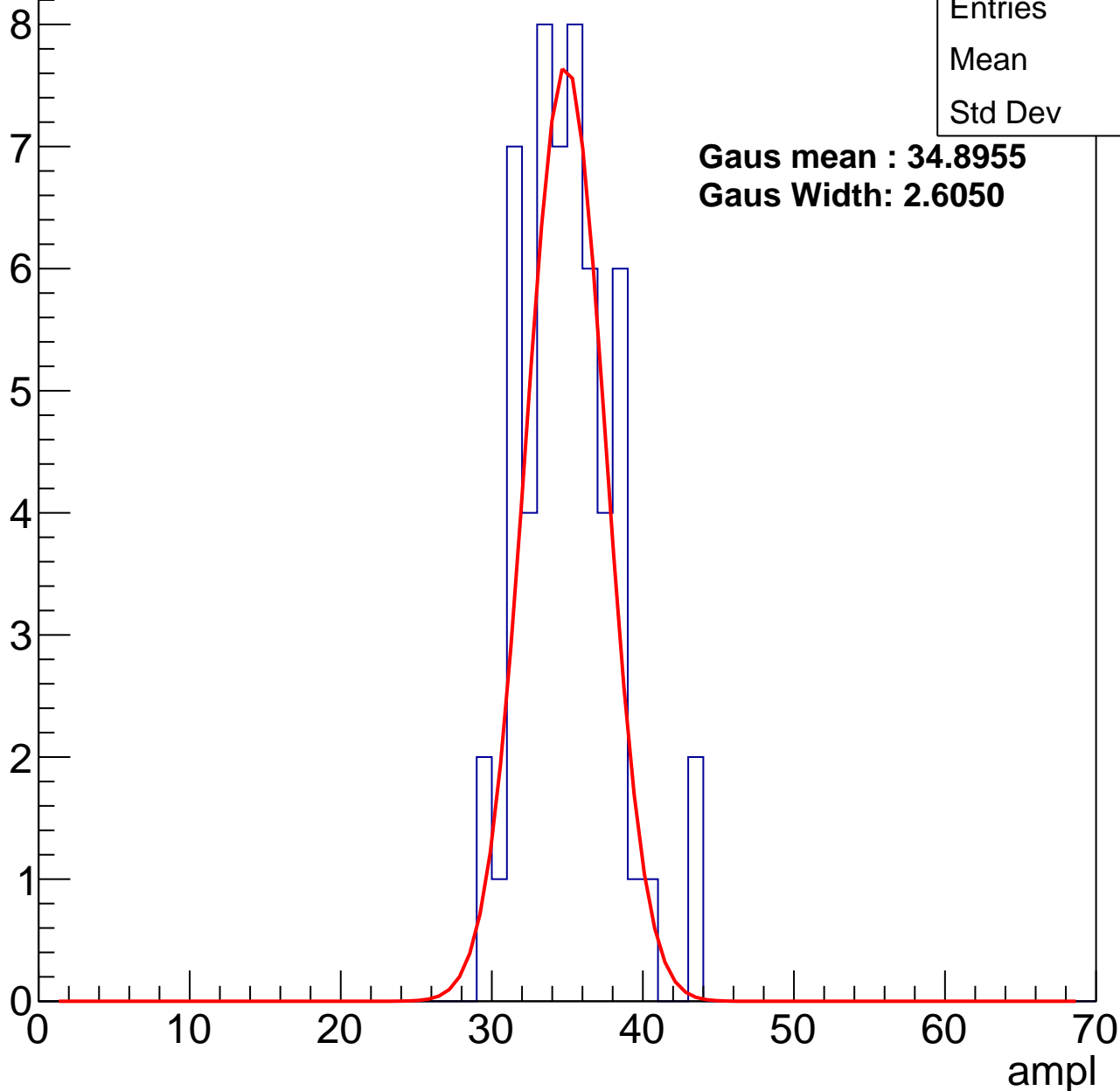
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	34.6
Std Dev	3.02

**Gaus mean : 34.8955**

**Gaus Width: 2.6050**



# B1L000S, U1-ch68, adc2

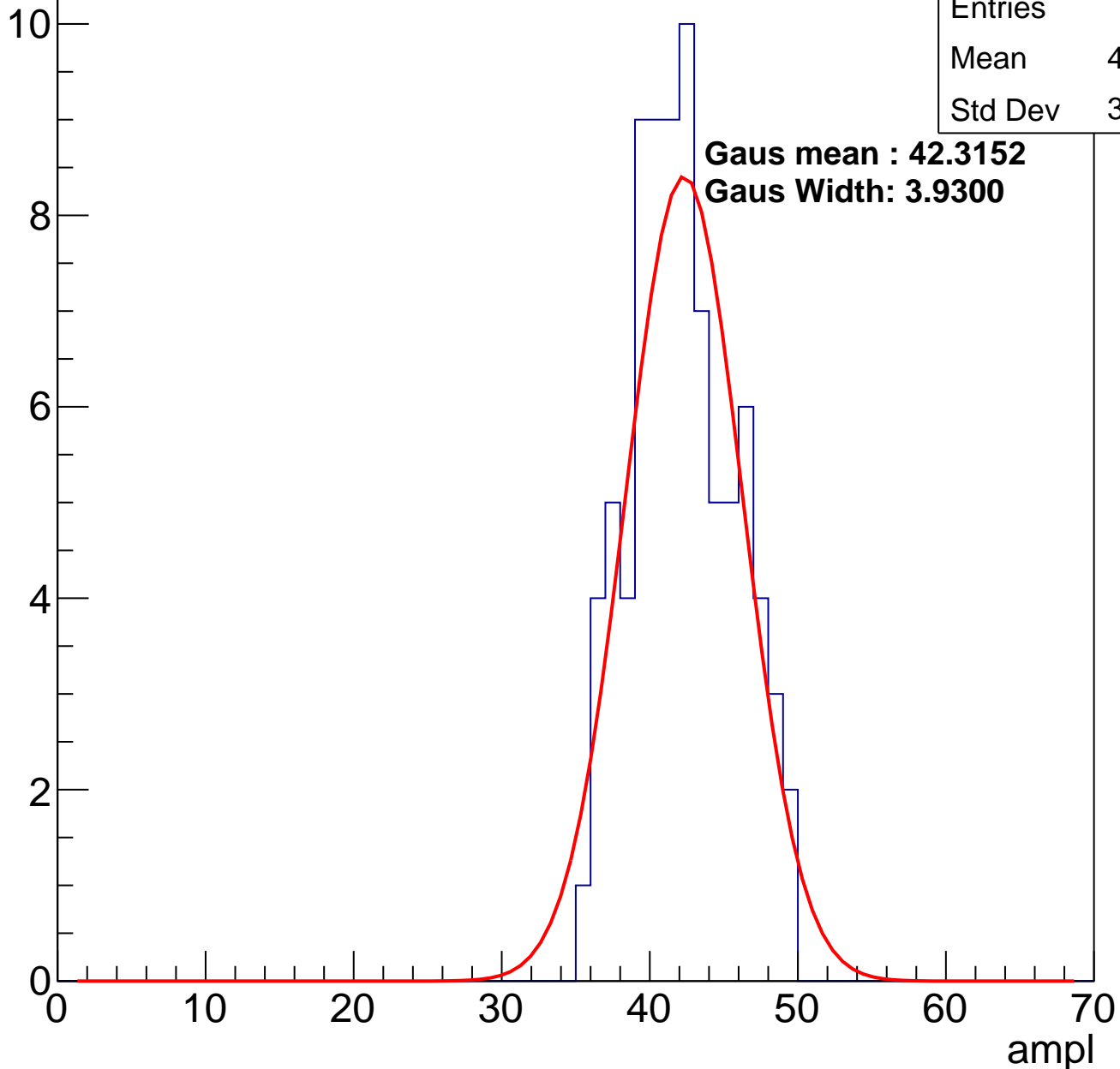
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	41.78
Std Dev	3.433

**Gaus mean : 42.3152**

**Gaus Width: 3.9300**

Entry

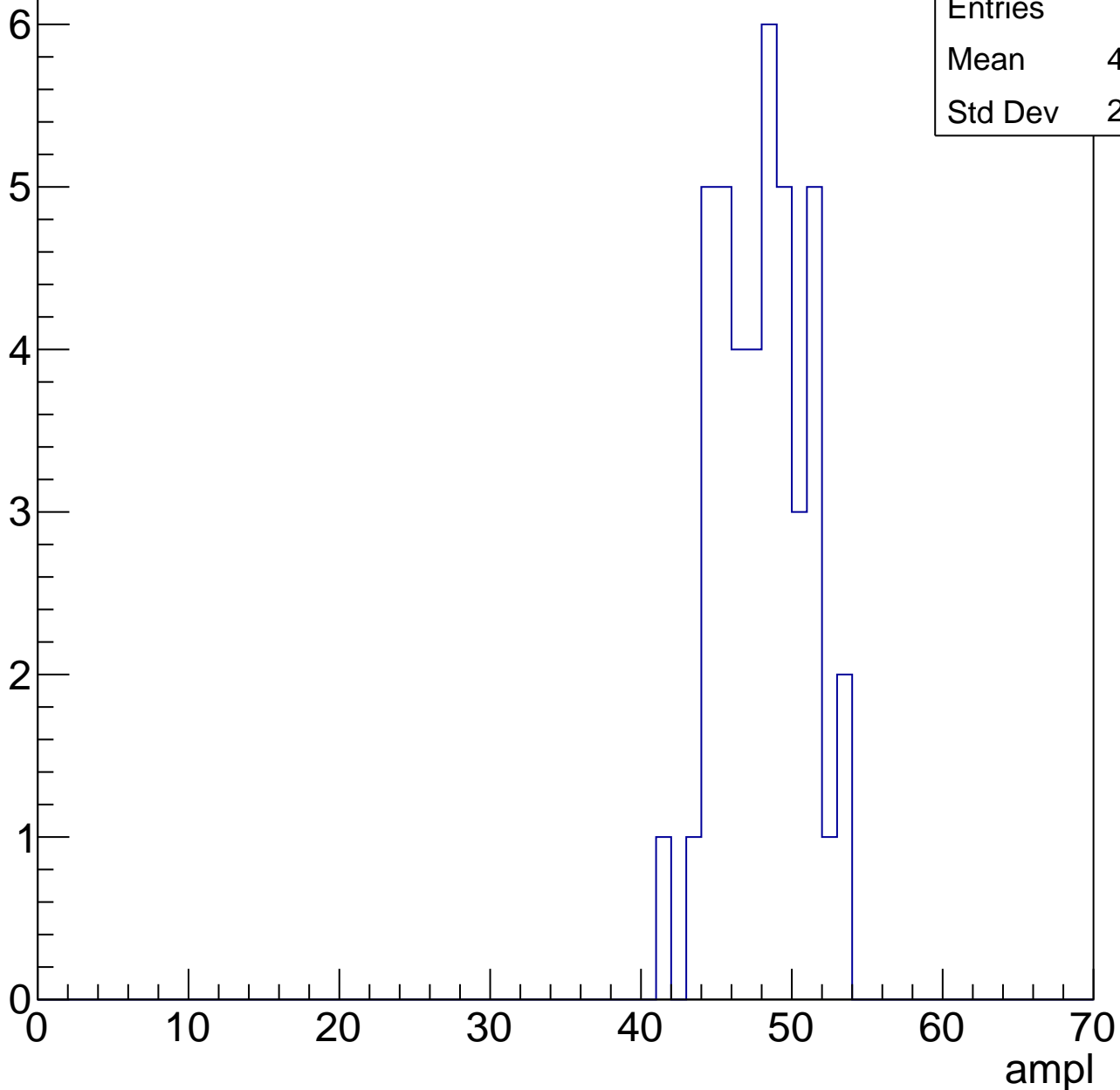


# B1L000S, U1-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	47.55
Std Dev	2.839

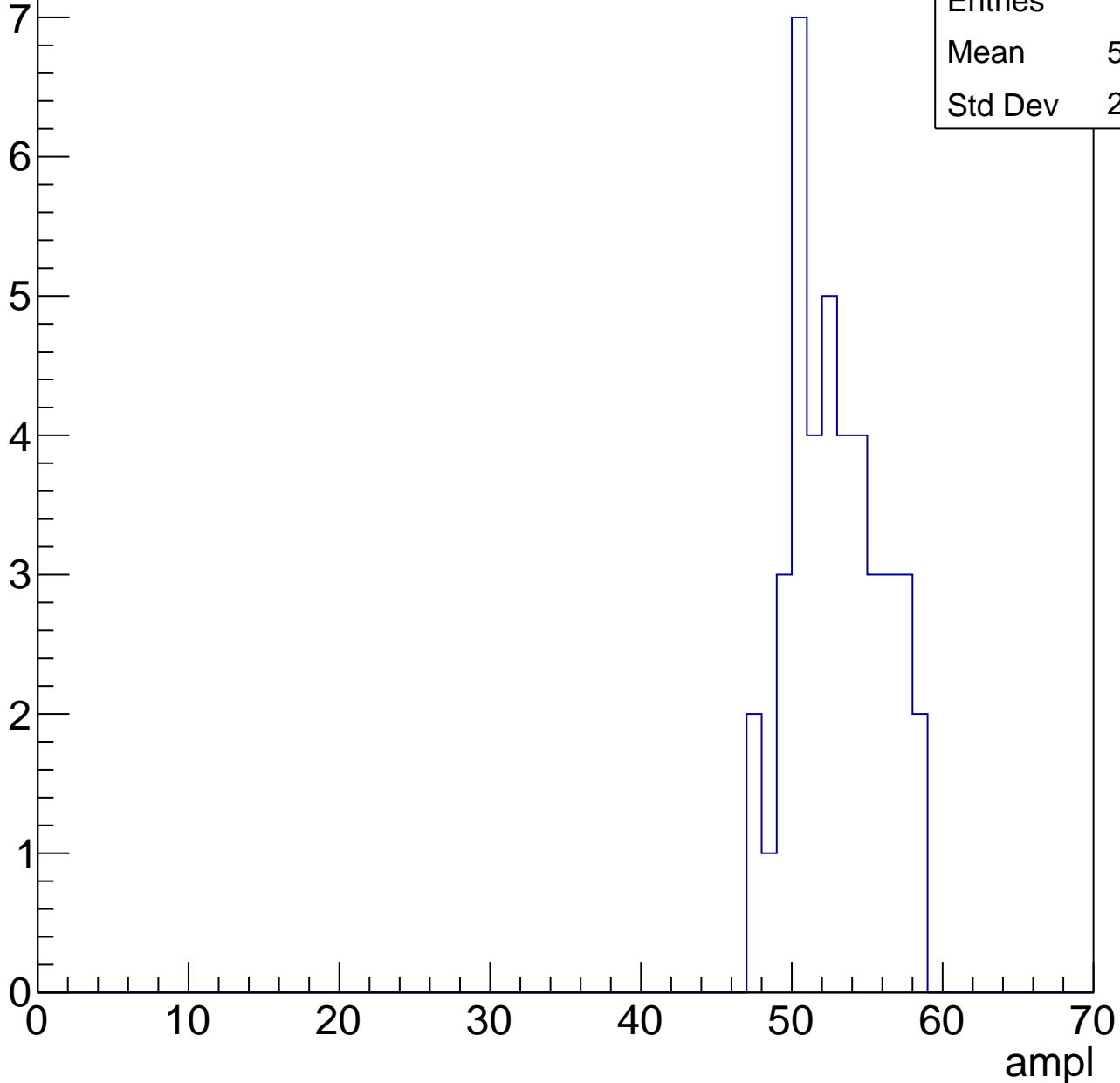


# B1L000S, U1-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

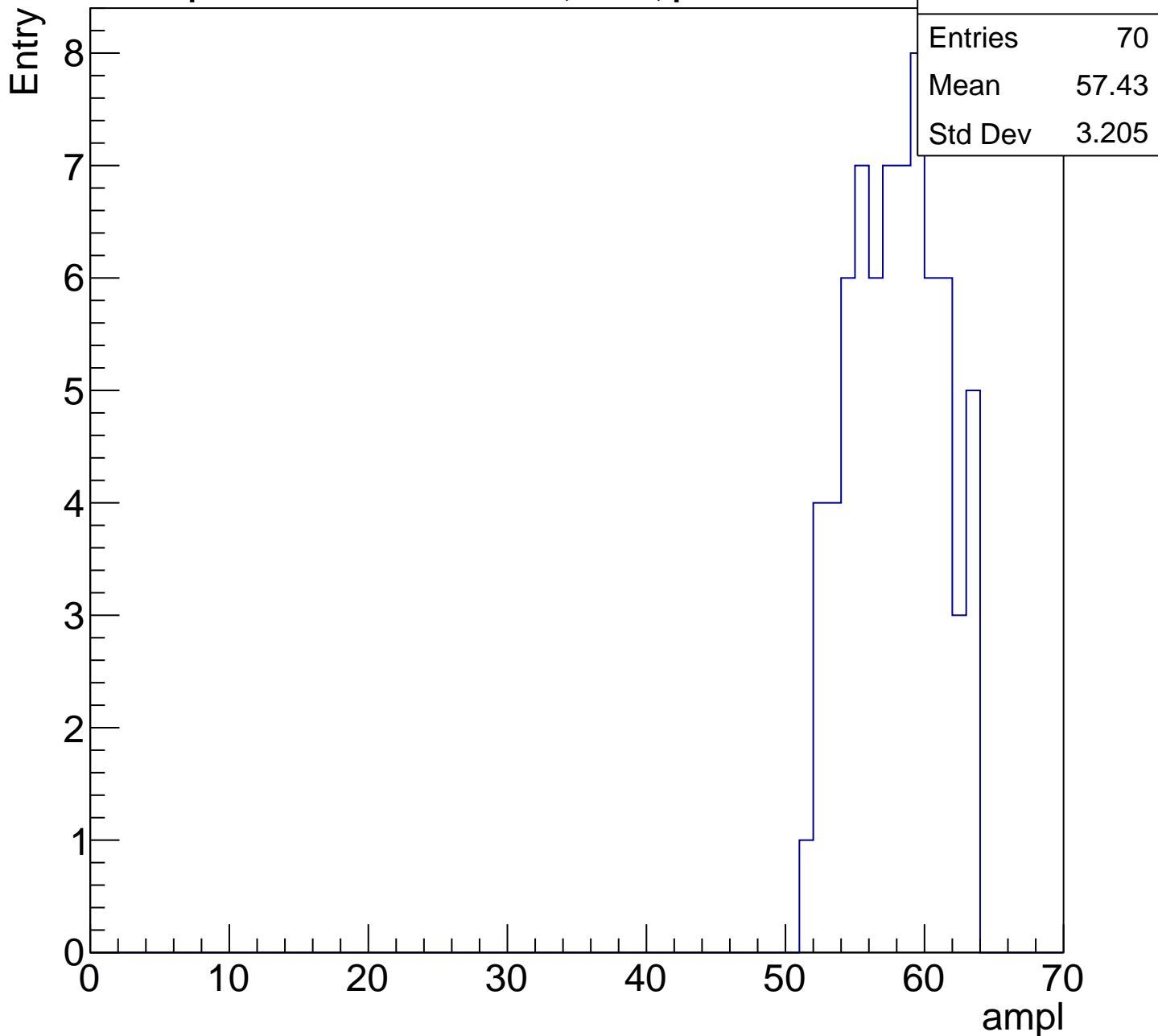
Entry

Entries	41
Mean	52.46
Std Dev	2.956



# B1L000S, U1-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

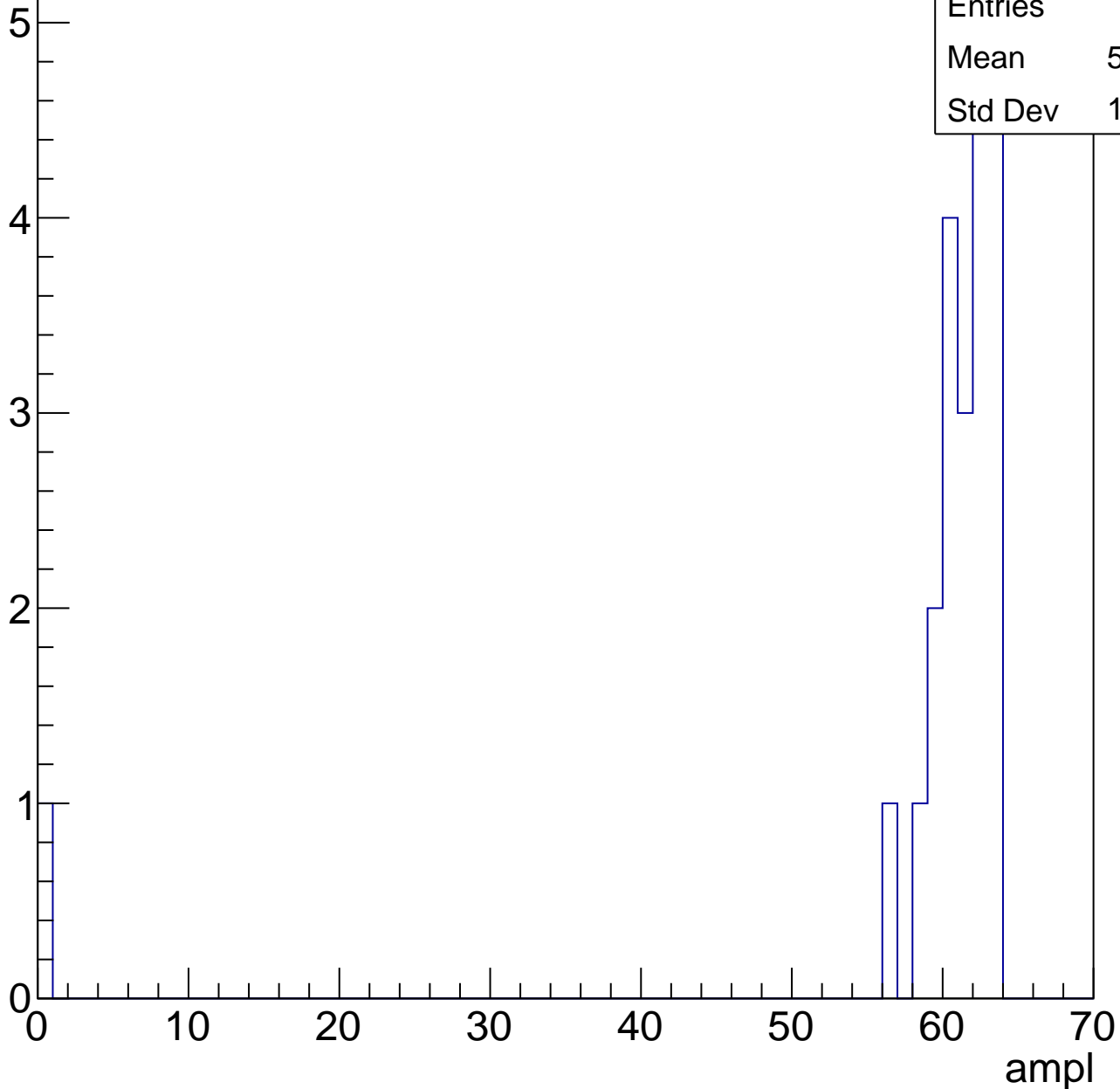


# B1L000S, U1-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	58.18
Std Dev	12.82

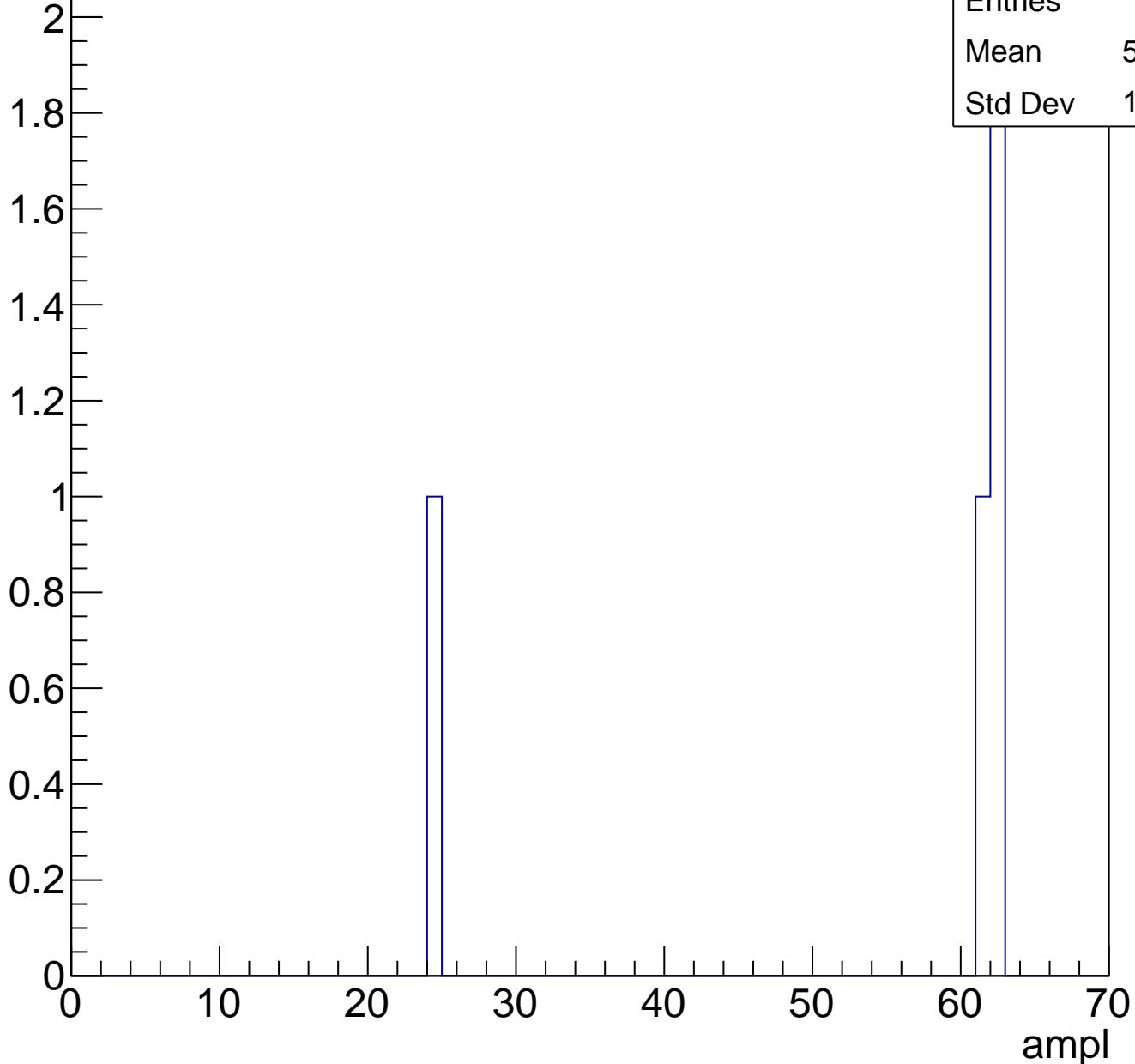




# B1L000S, U1-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	52.25
Std Dev	16.32

# B1L000S, U1-ch69, adc0

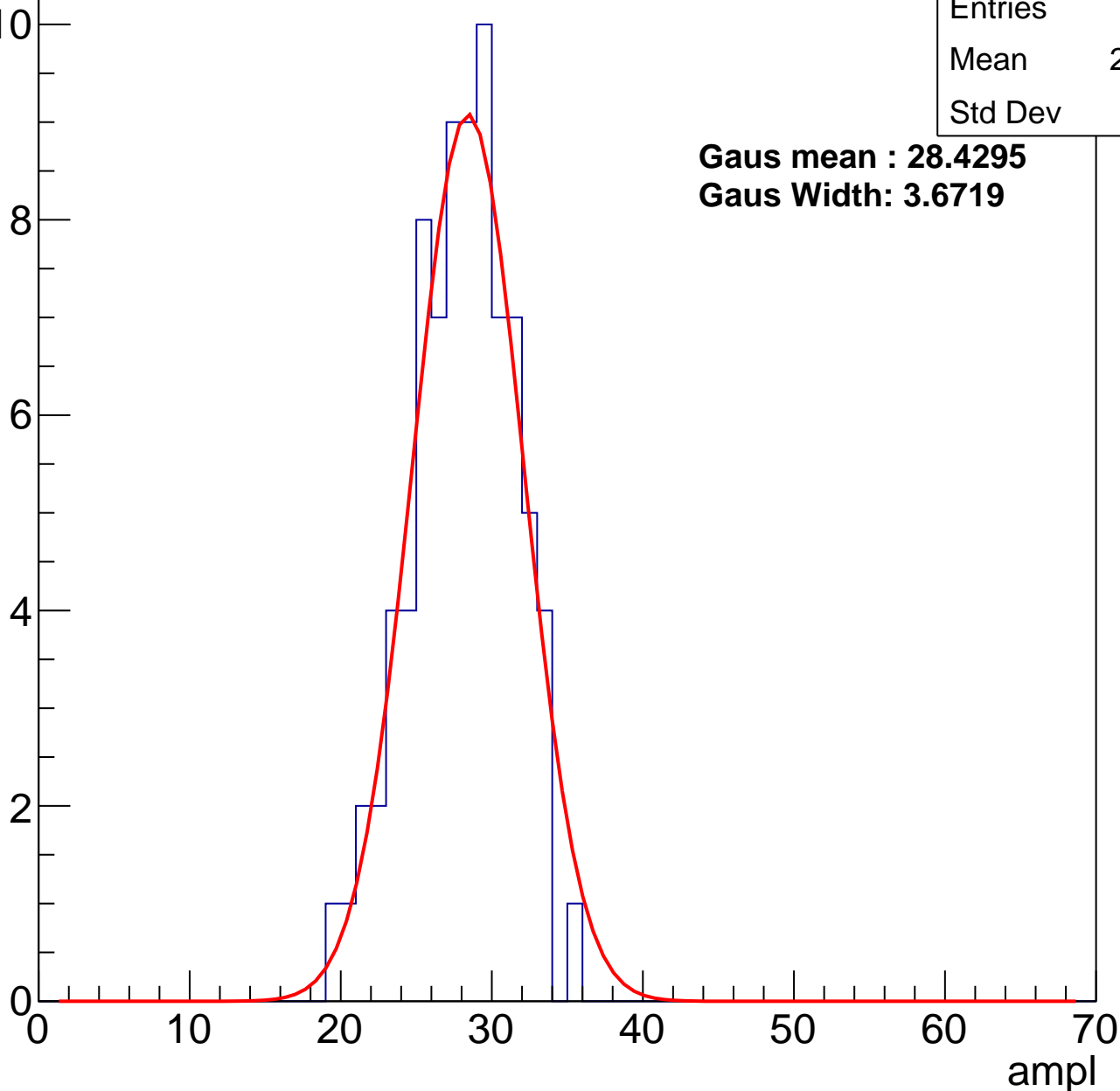
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	27.58
Std Dev	3.34

**Gaus mean : 28.4295**

**Gaus Width: 3.6719**



# B1L000S, U1-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	35.08
Std Dev	2.982

**Gaus mean : 35.2463**

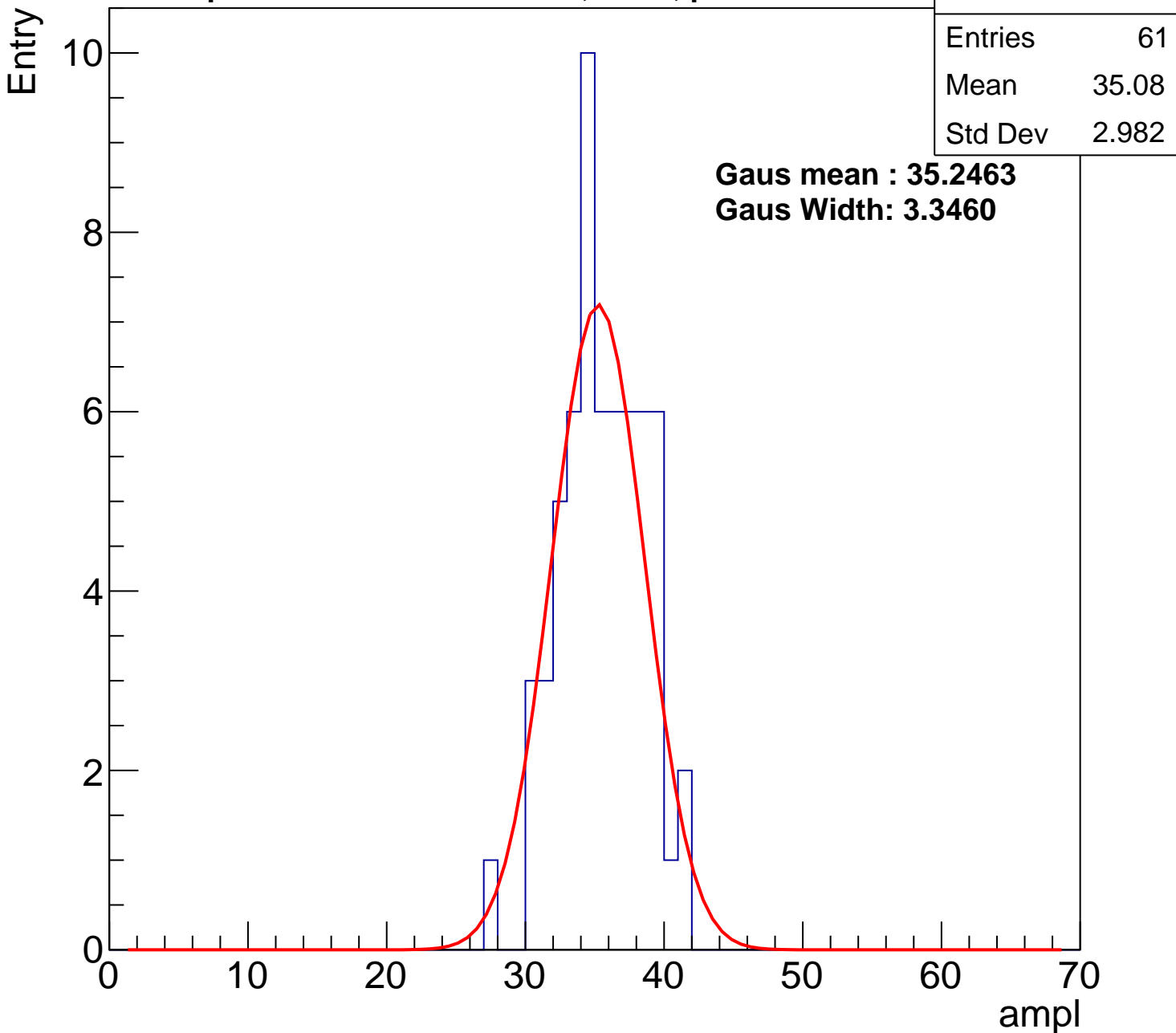
**Gaus Width: 3.3460**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch69, adc2

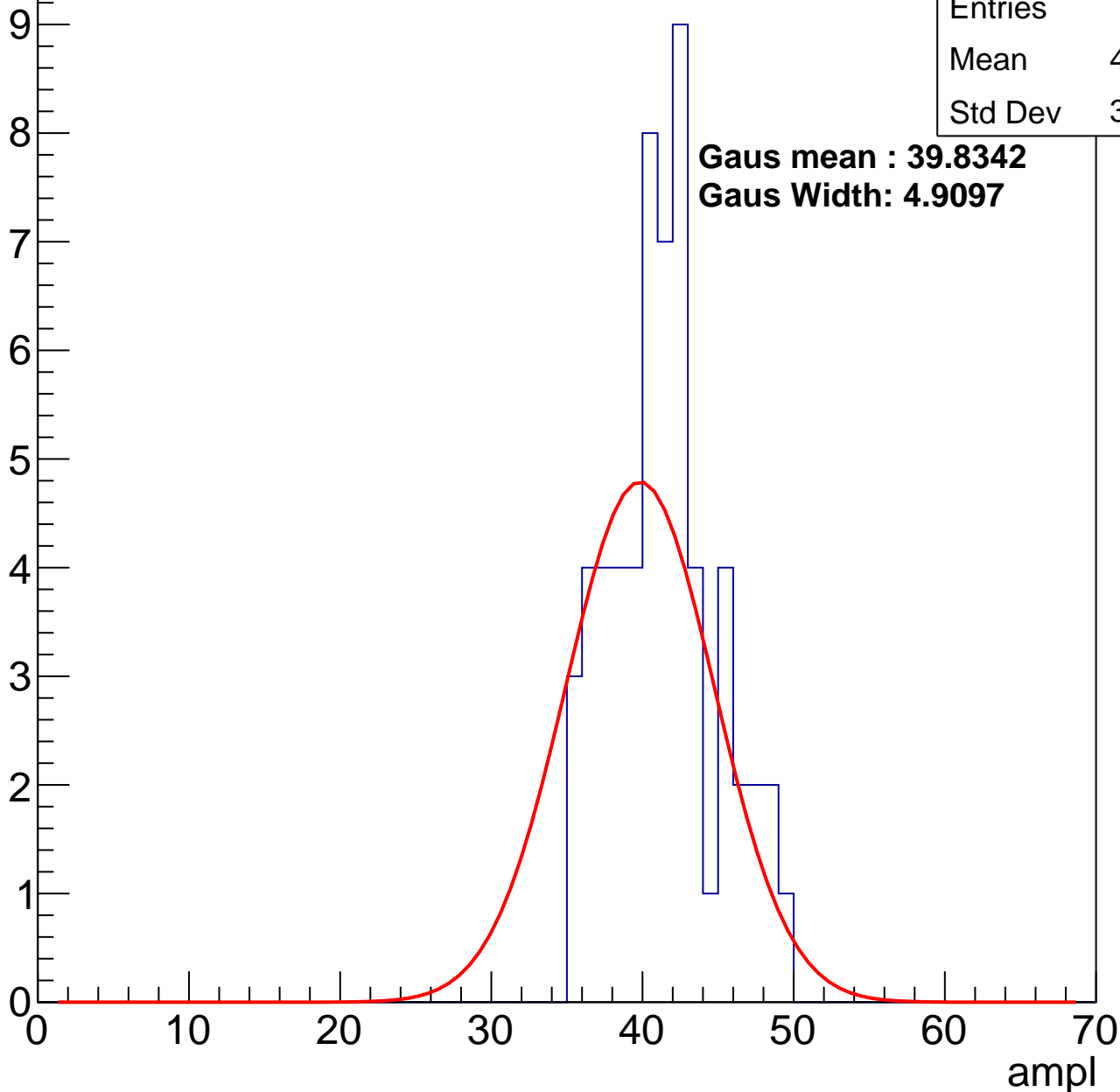
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	40.97
Std Dev	3.479

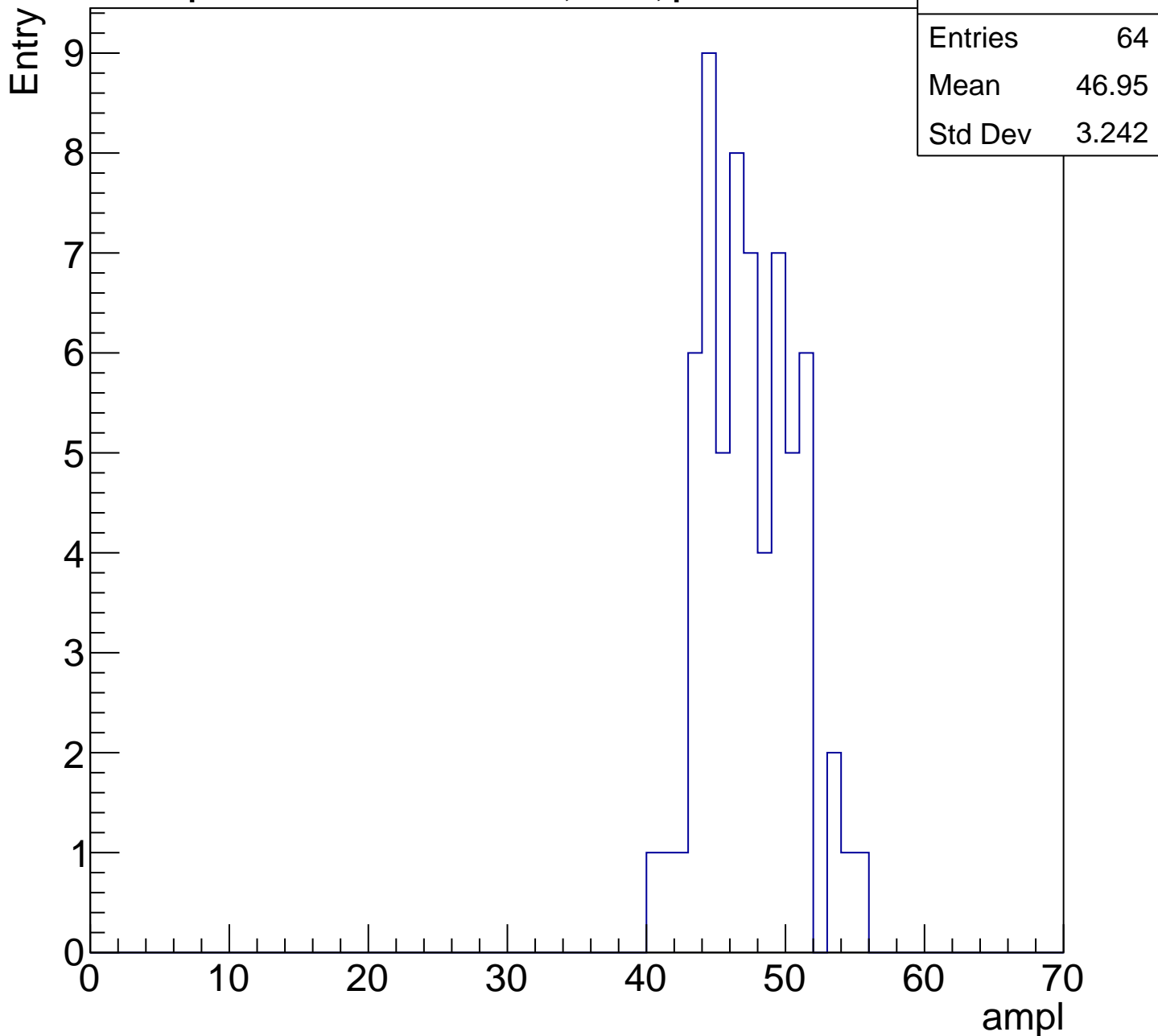
**Gaus mean : 39.8342**

**Gaus Width: 4.9097**



# B1L000S, U1-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

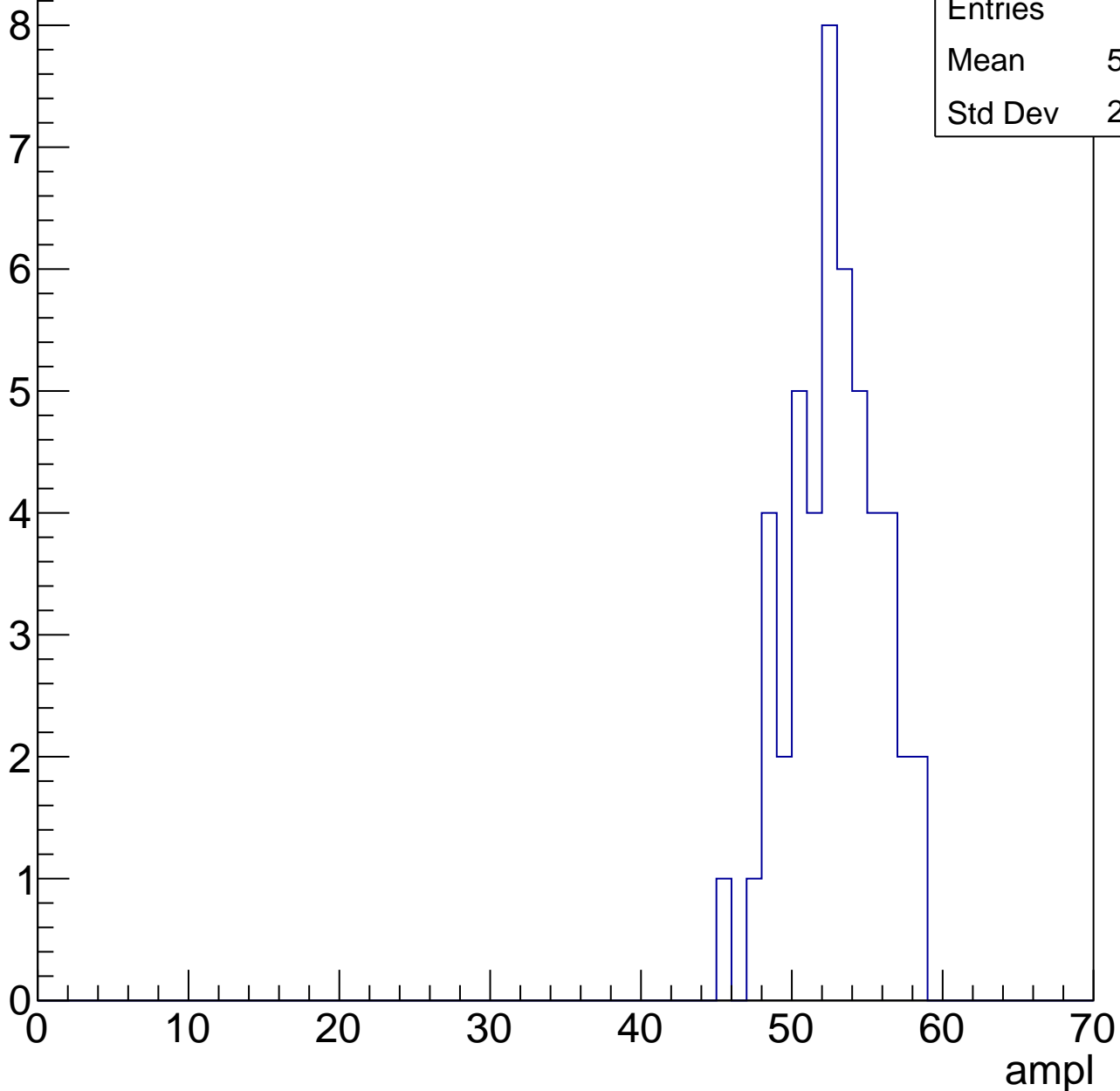


# B1L000S, U1-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

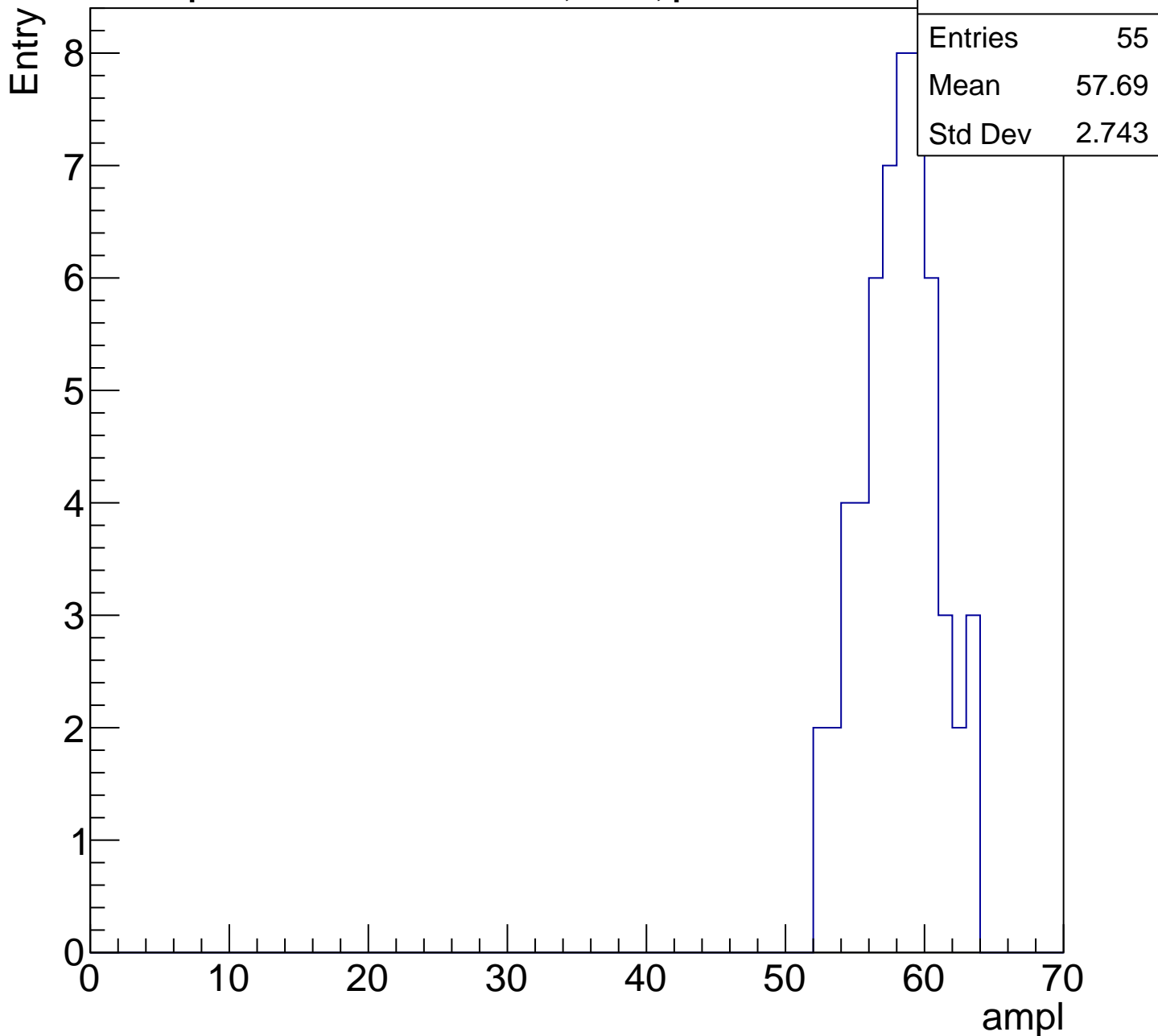
Entry

Entries	48
Mean	52.38
Std Dev	2.962



# B1L000S, U1-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

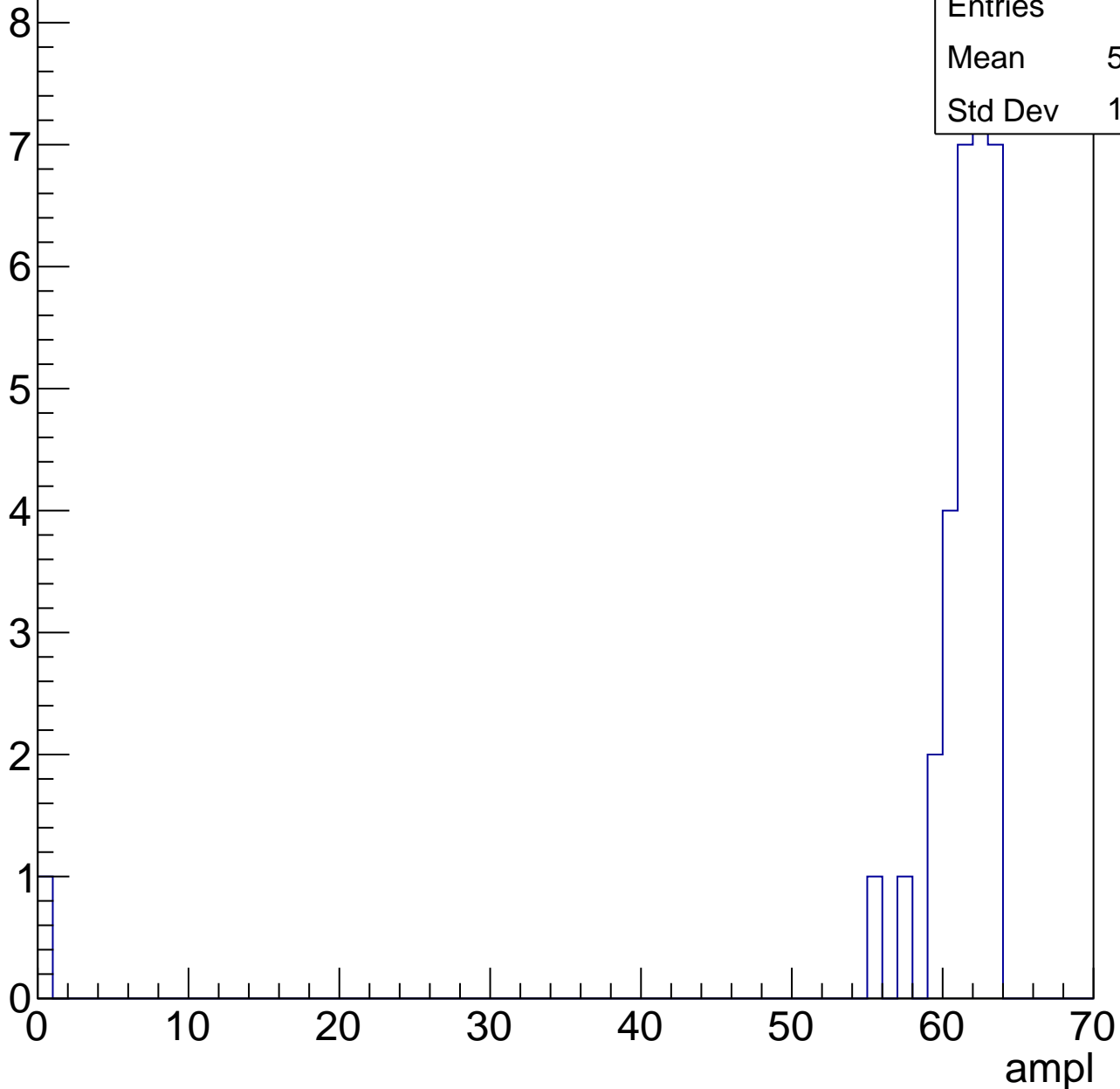


# B1L000S, U1-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	31
Mean	59.16
Std Dev	10.95





# B1L000S, U1-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U1-ch70, adc0

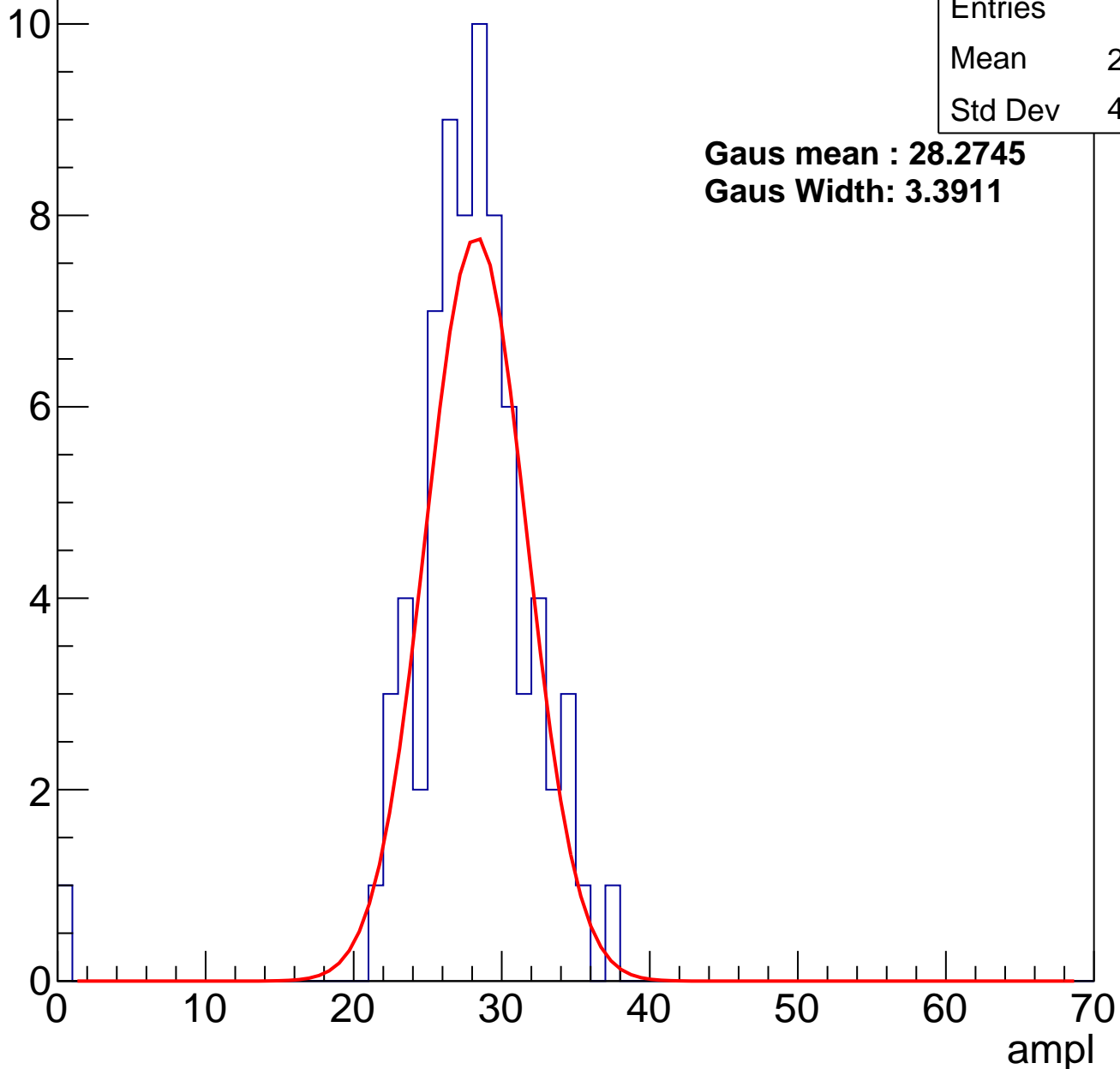
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	27.47
Std Dev	4.638

**Gaus mean : 28.2745**

**Gaus Width: 3.3911**

Entry



# B1L000S, U1-ch70, adc1

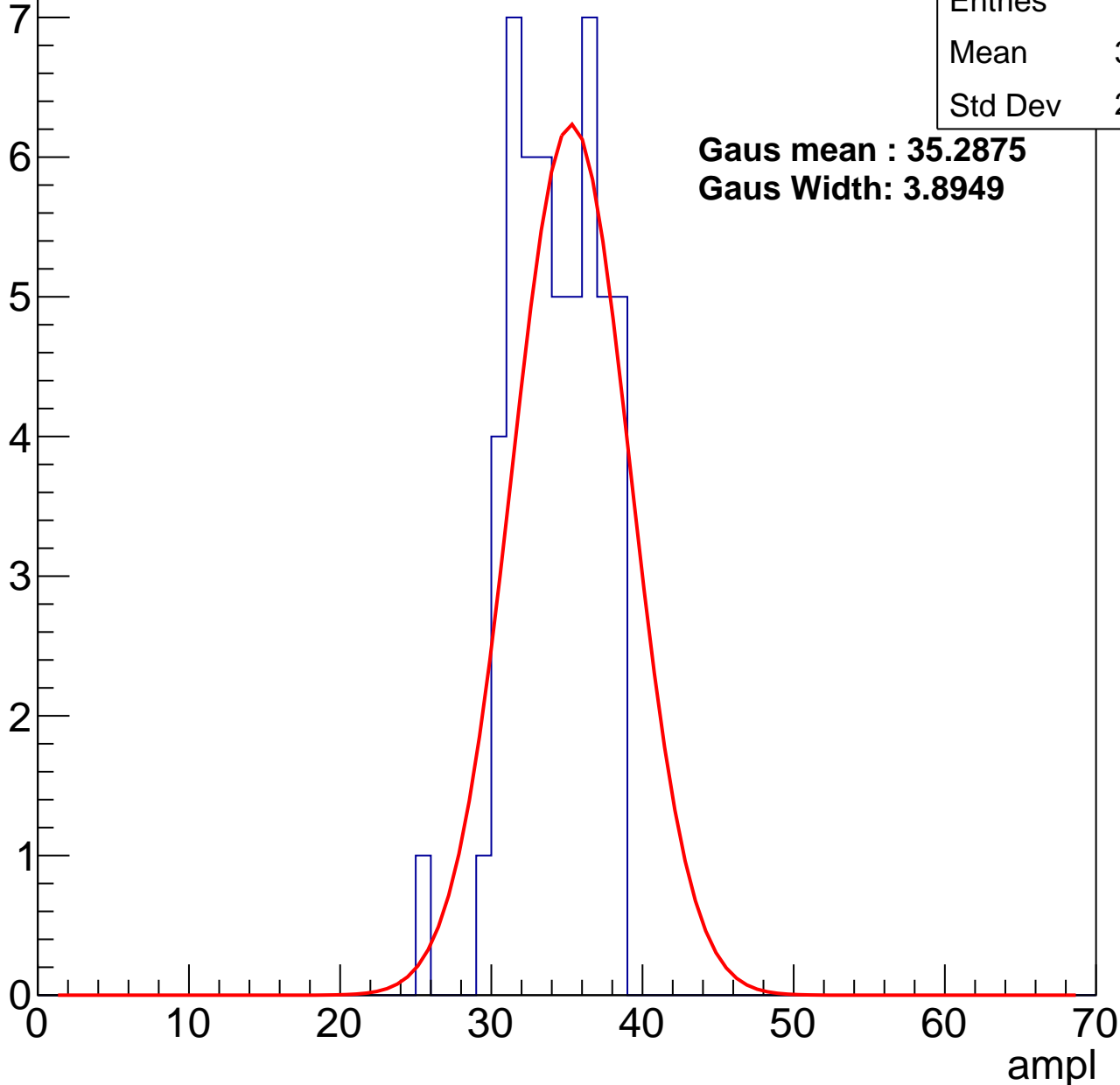
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	33.71
Std Dev	2.831

**Gaus mean : 35.2875**

**Gaus Width: 3.8949**



# B1L000S, U1-ch70, adc2

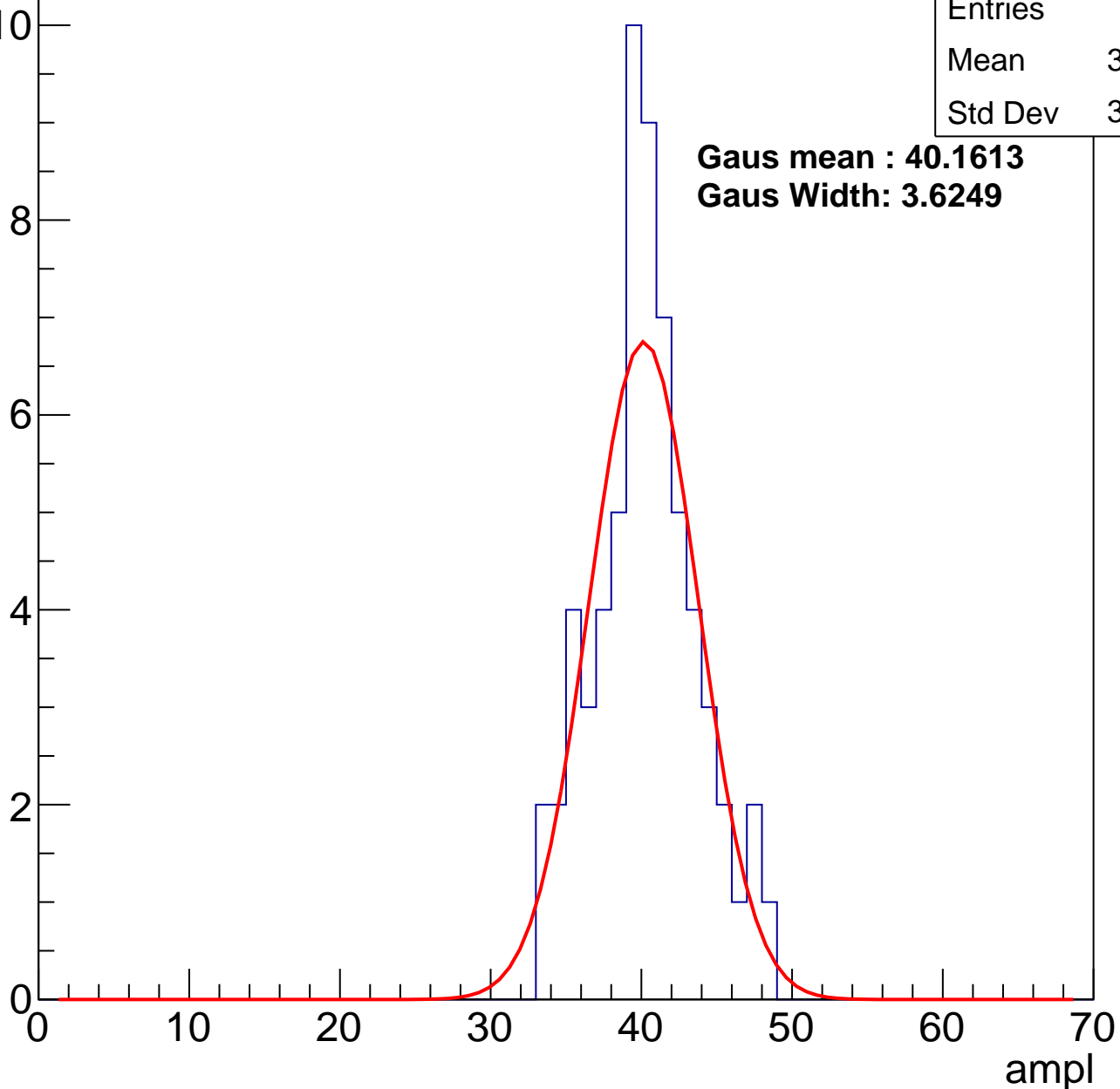
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	39.83
Std Dev	3.403

**Gaus mean : 40.1613**

**Gaus Width: 3.6249**

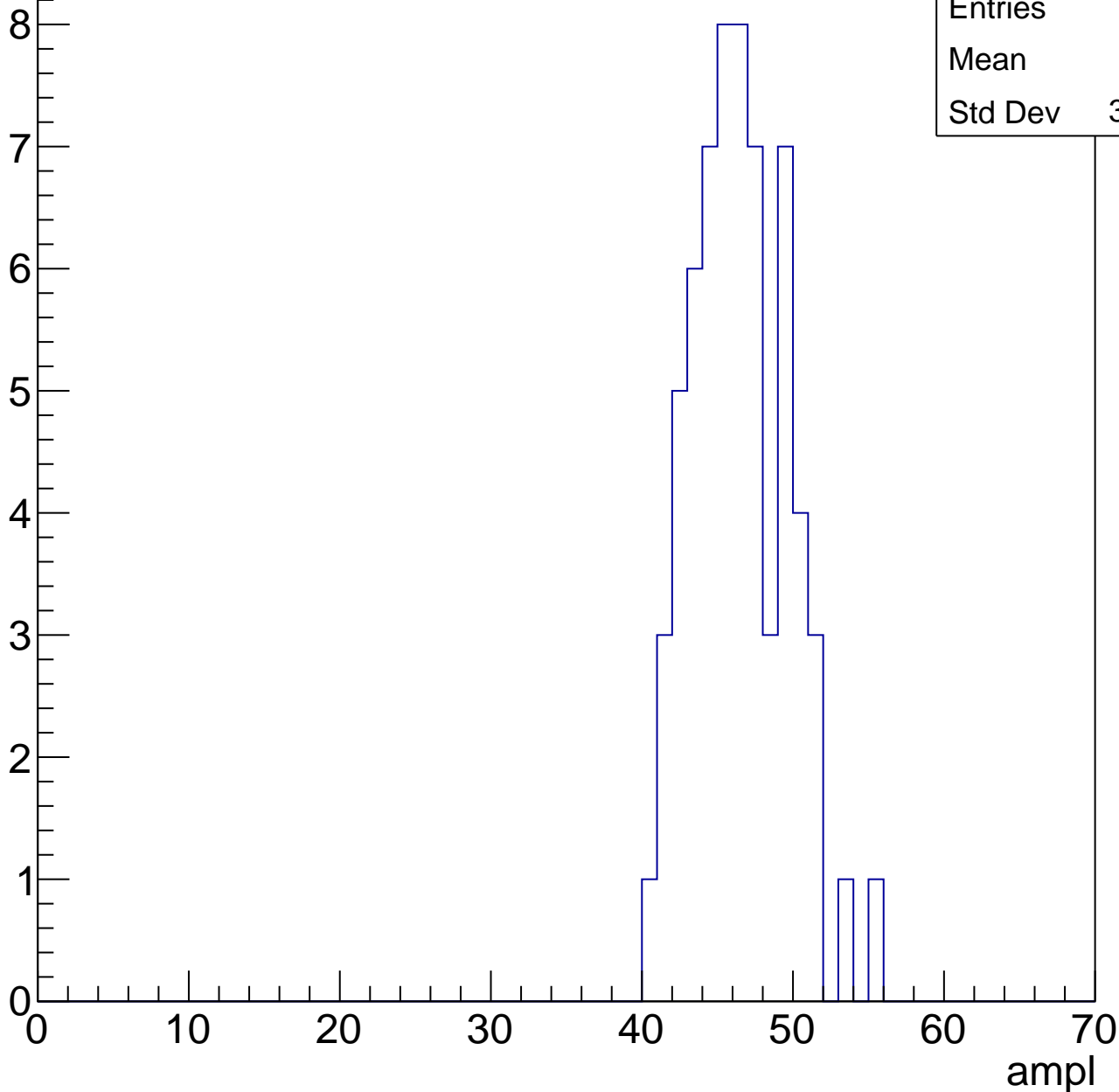


# B1L000S, U1-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	46
Std Dev	3.142

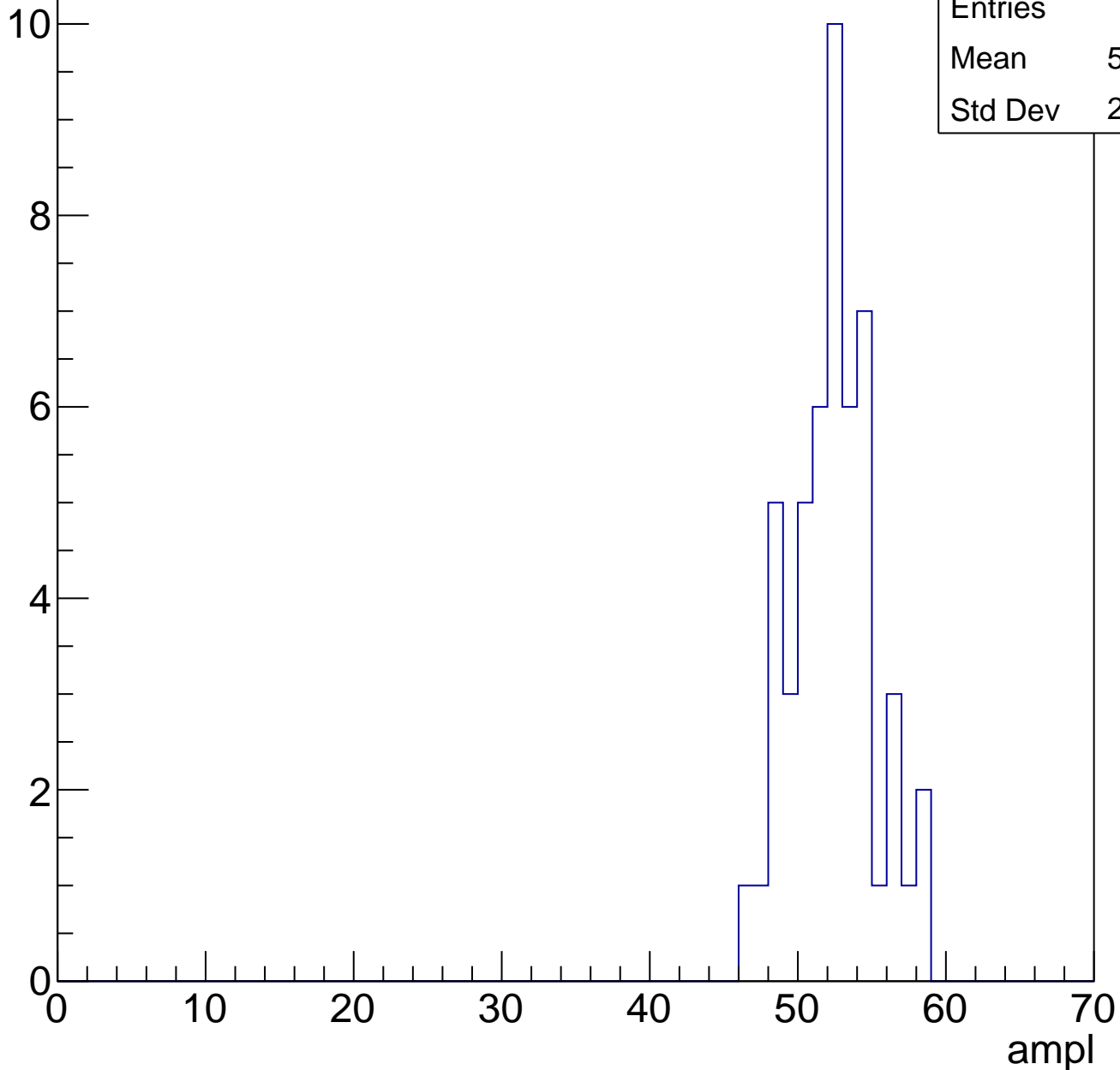


# B1L000S, U1-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	51
Mean	51.92
Std Dev	2.736

Entry

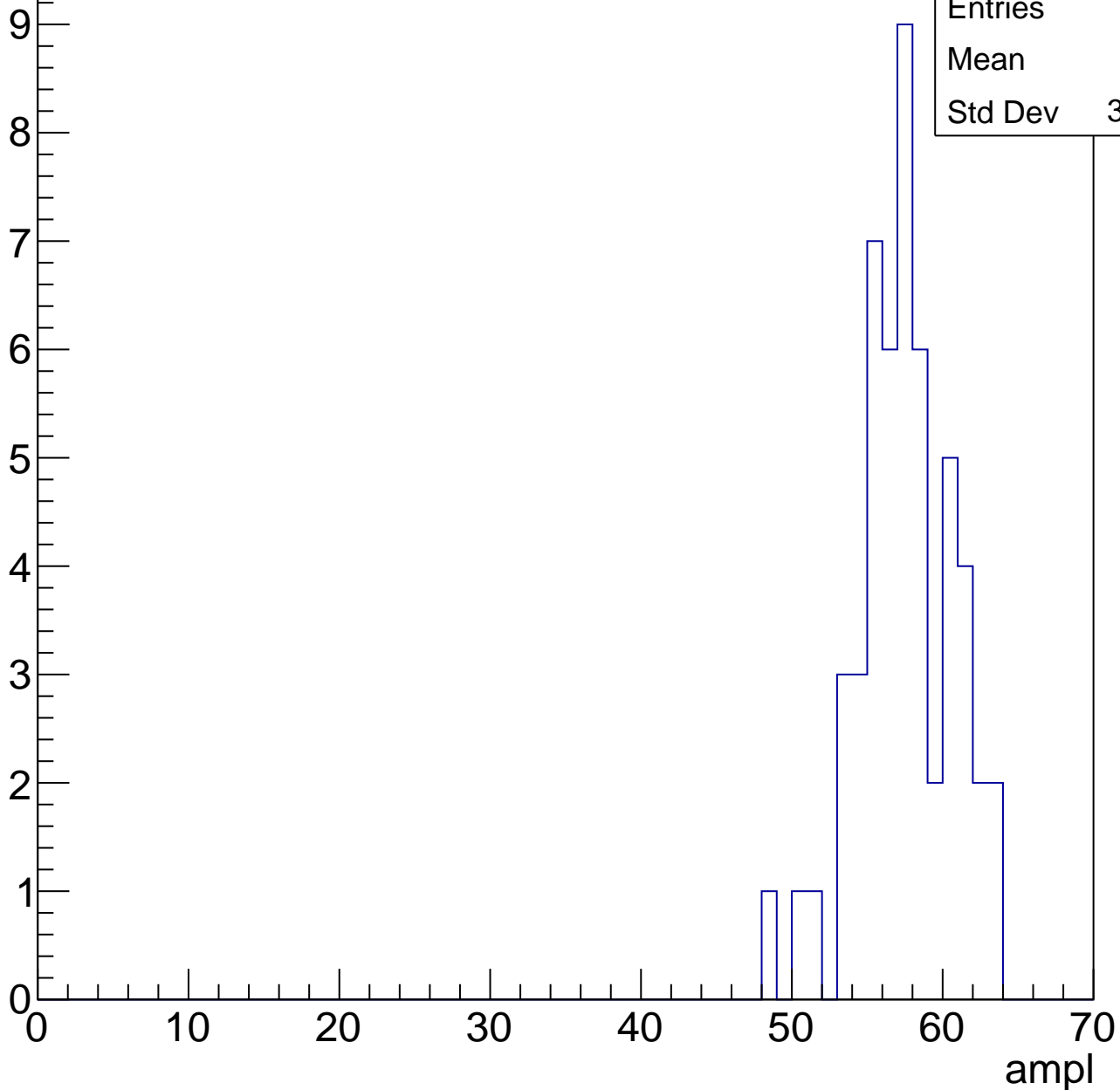


# B1L000S, U1-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

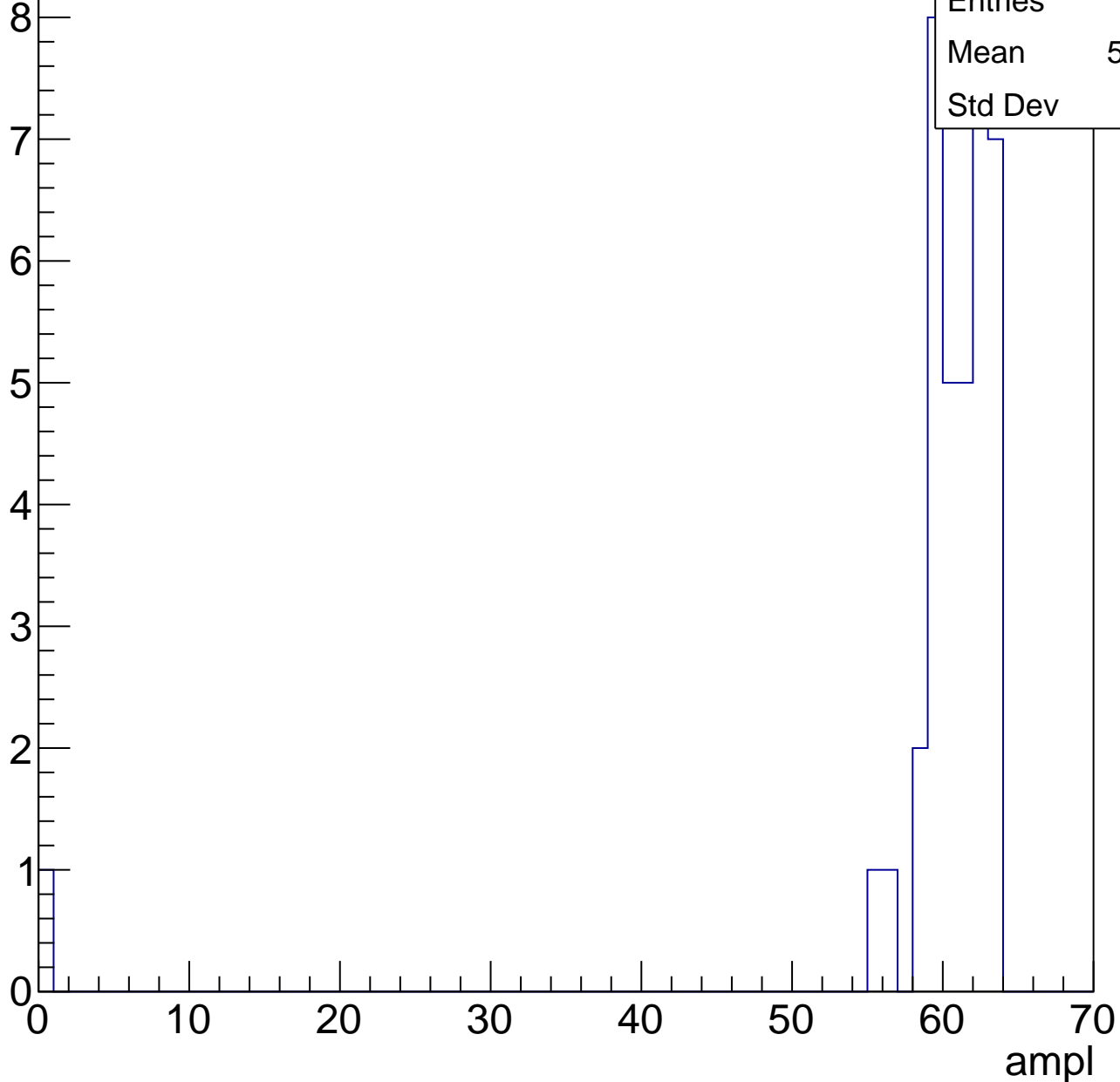
Entries	52
Mean	57
Std Dev	3.162



# B1L000S, U1-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

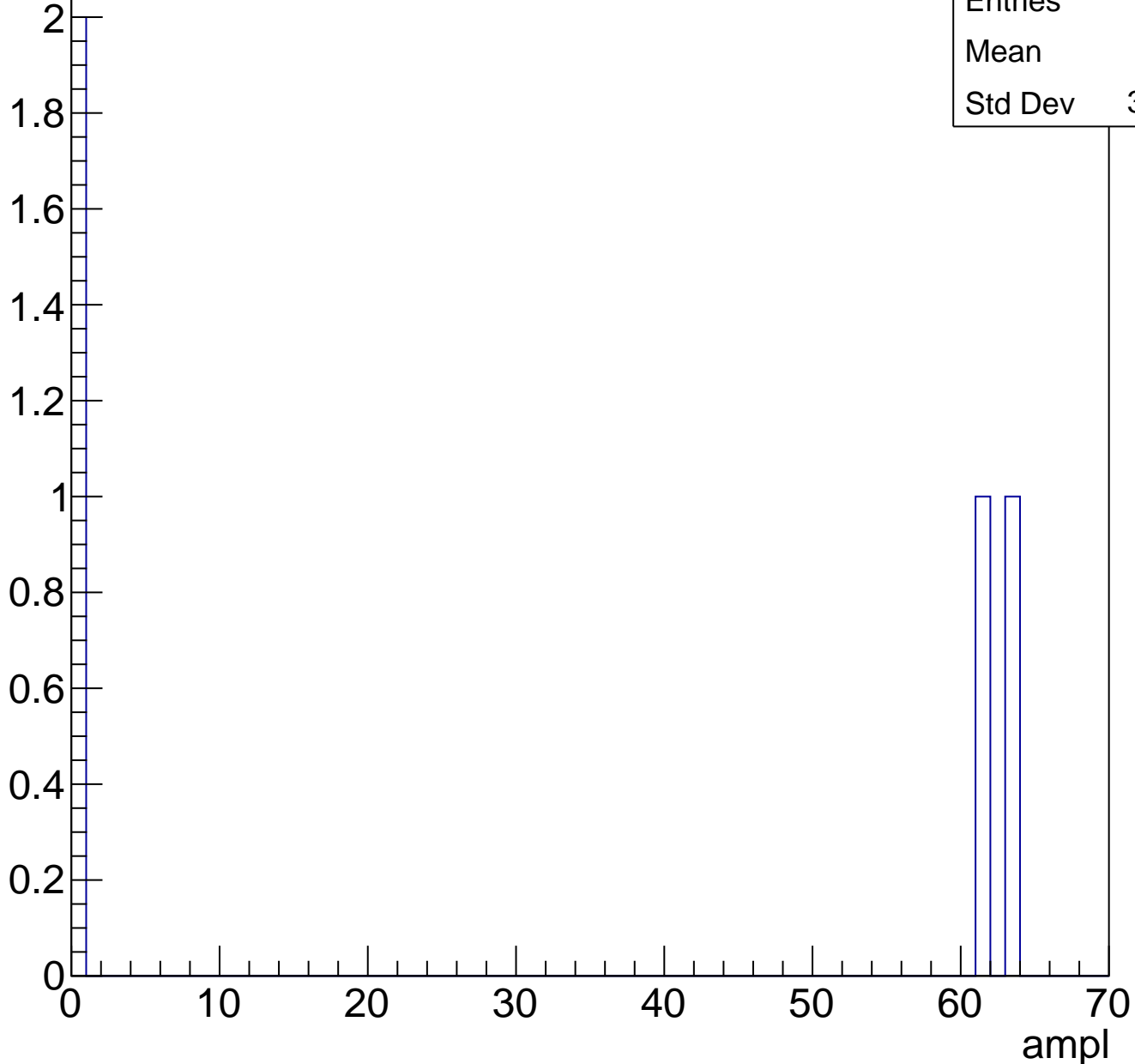




# B1L000S, U1-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	31
Std Dev	31.01

# B1L000S, U1-ch71, adc0

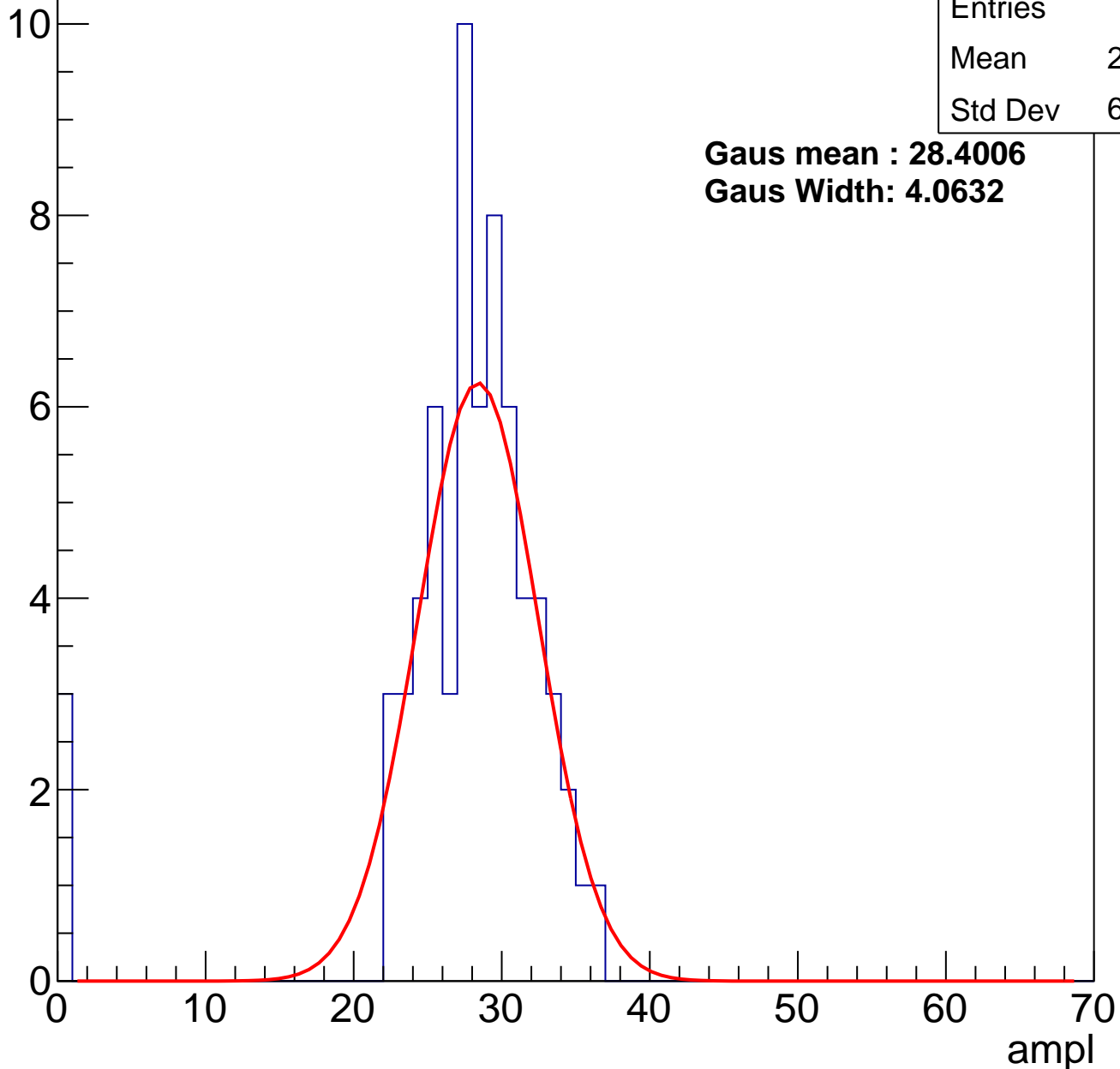
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	26.85
Std Dev	6.667

**Gaus mean : 28.4006**

**Gaus Width: 4.0632**

Entry



# B1L000S, U1-ch71, adc1

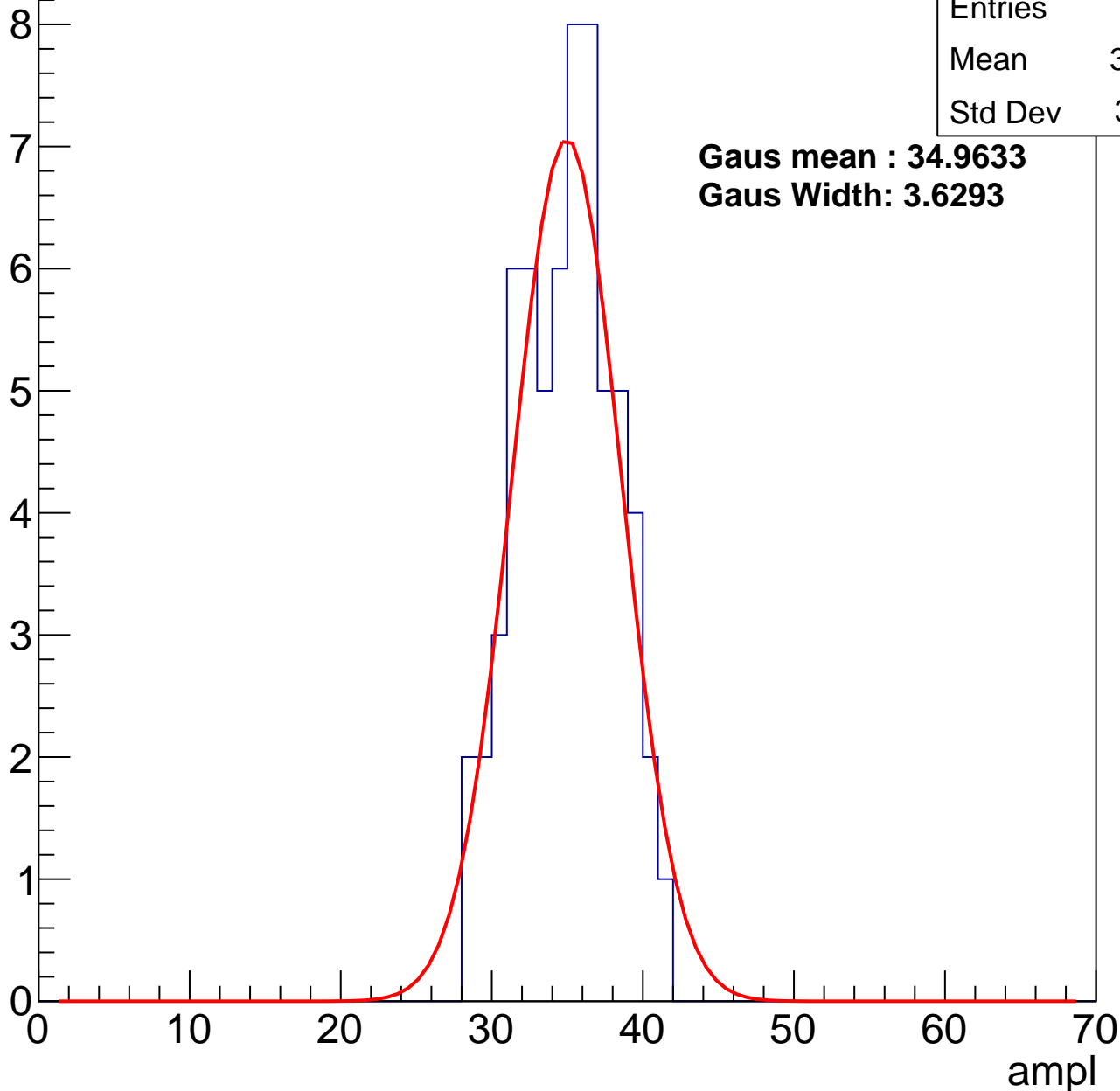
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	34.46
Std Dev	3.151

**Gaus mean : 34.9633**

**Gaus Width: 3.6293**



# B1L000S, U1-ch71, adc2

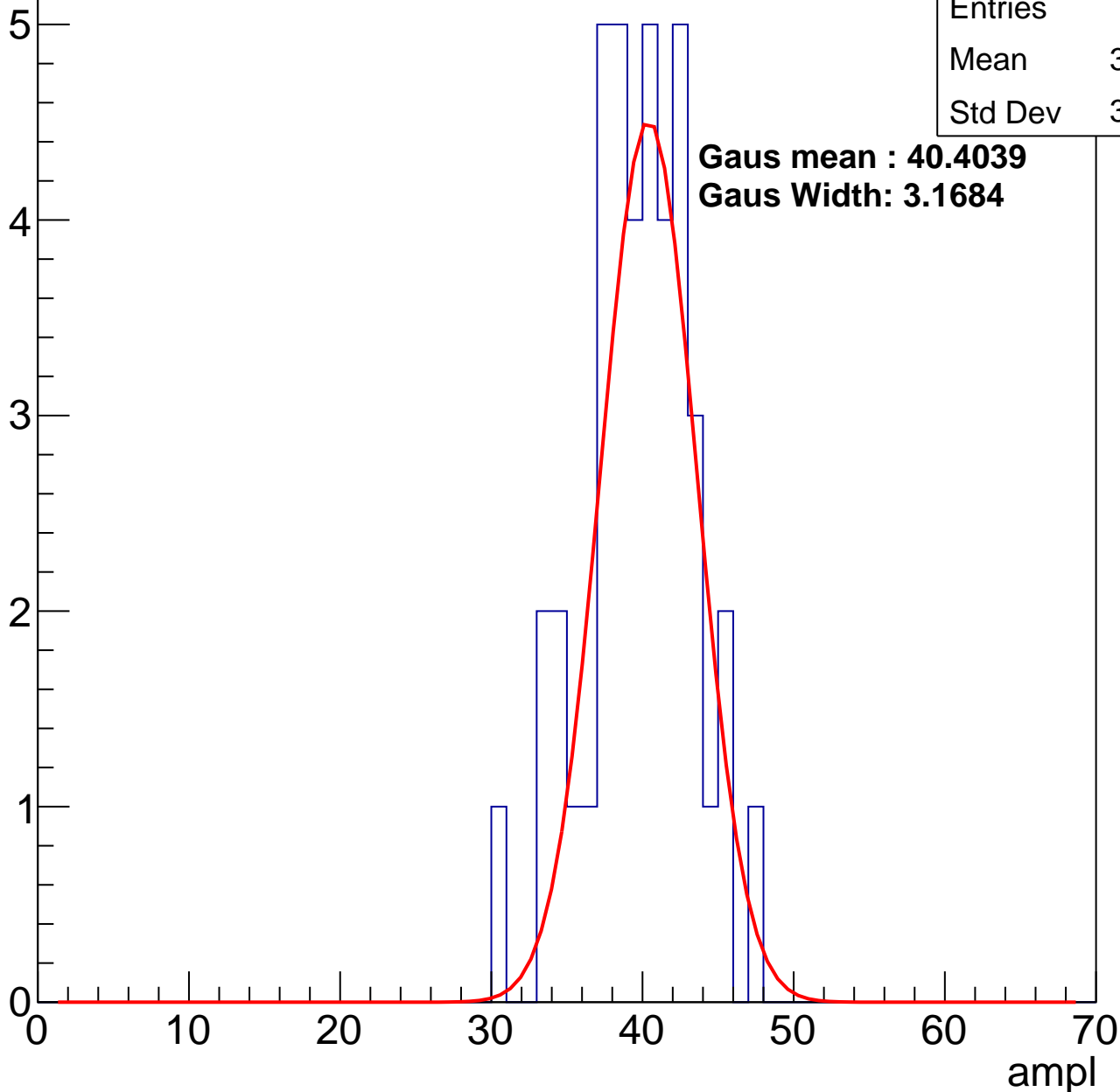
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	39.29
Std Dev	3.534

**Gaus mean : 40.4039**

**Gaus Width: 3.1684**



# B1L000S, U1-ch71, adc3

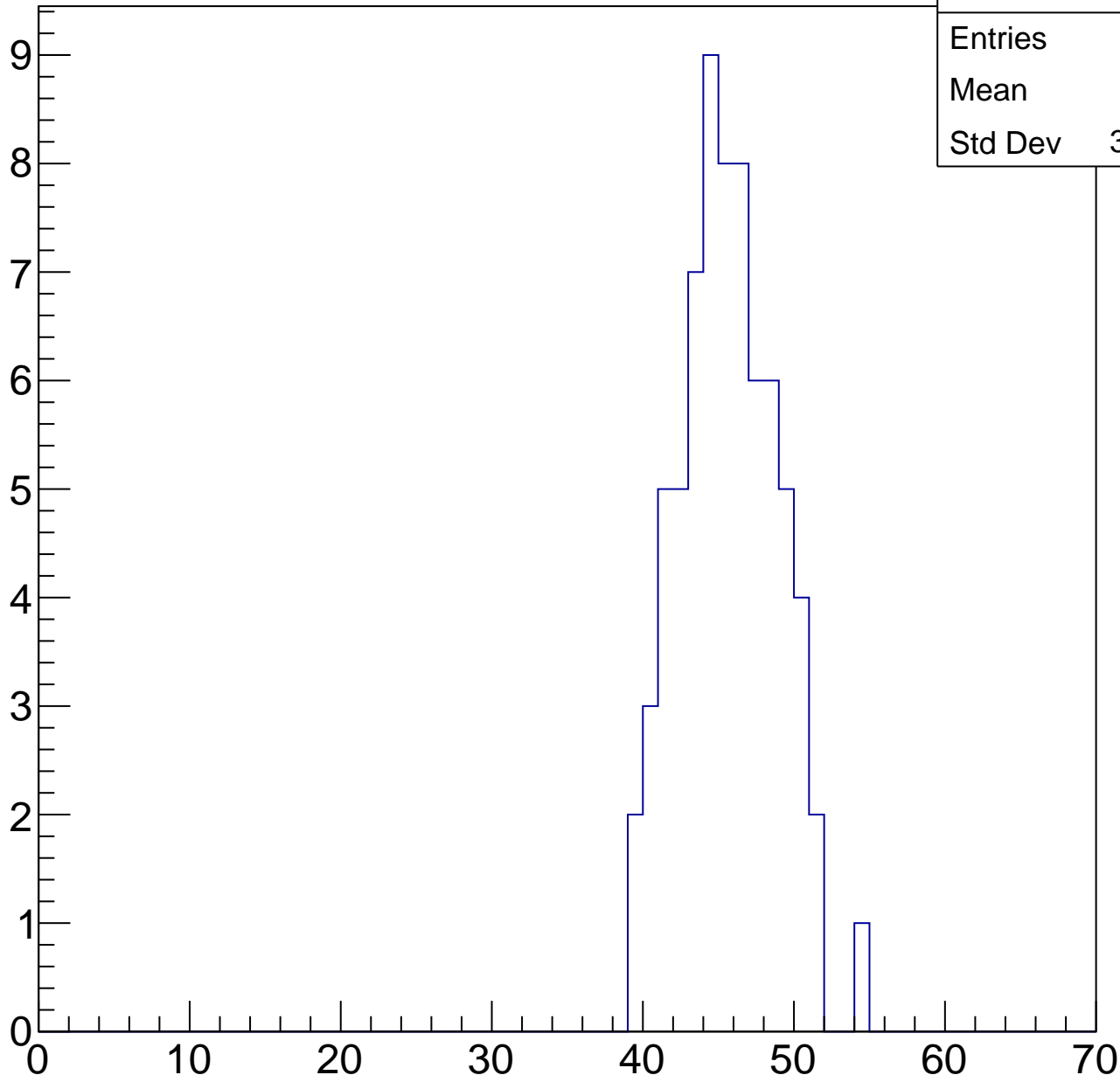
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	71
Mean	45.2
Std Dev	3.196

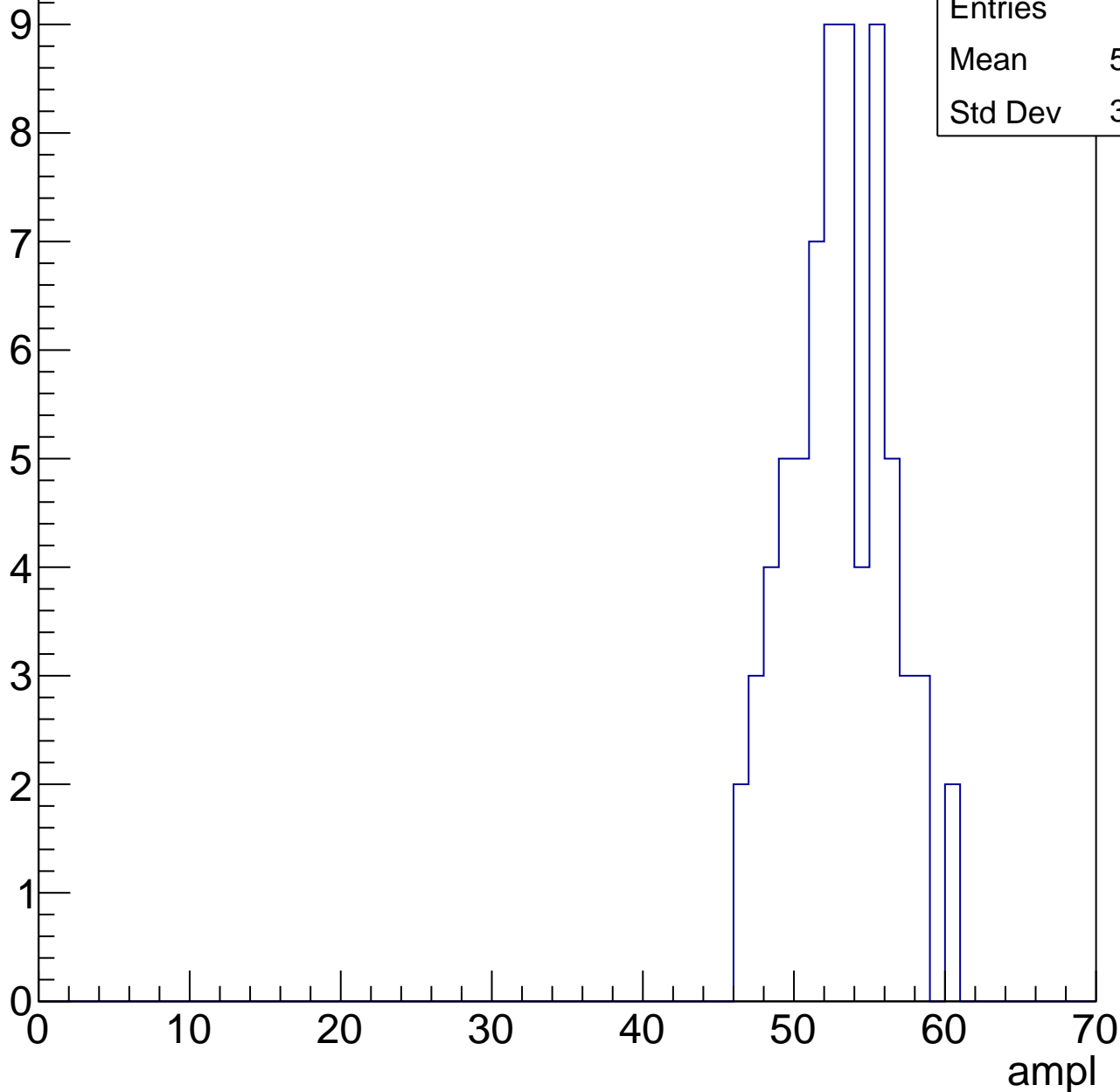
ampl



# B1L000S, U1-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

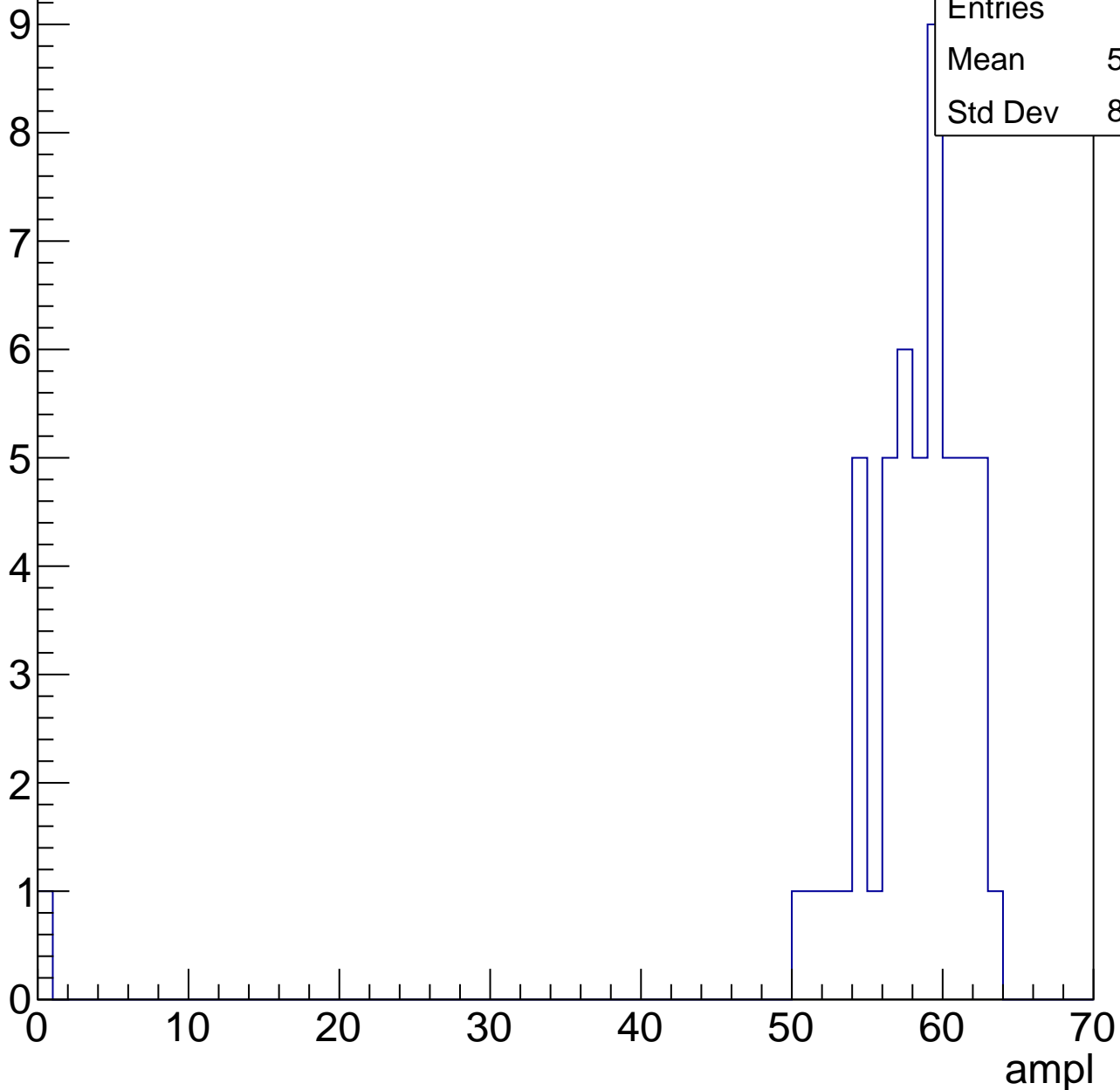
Entry



# B1L000S, U1-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

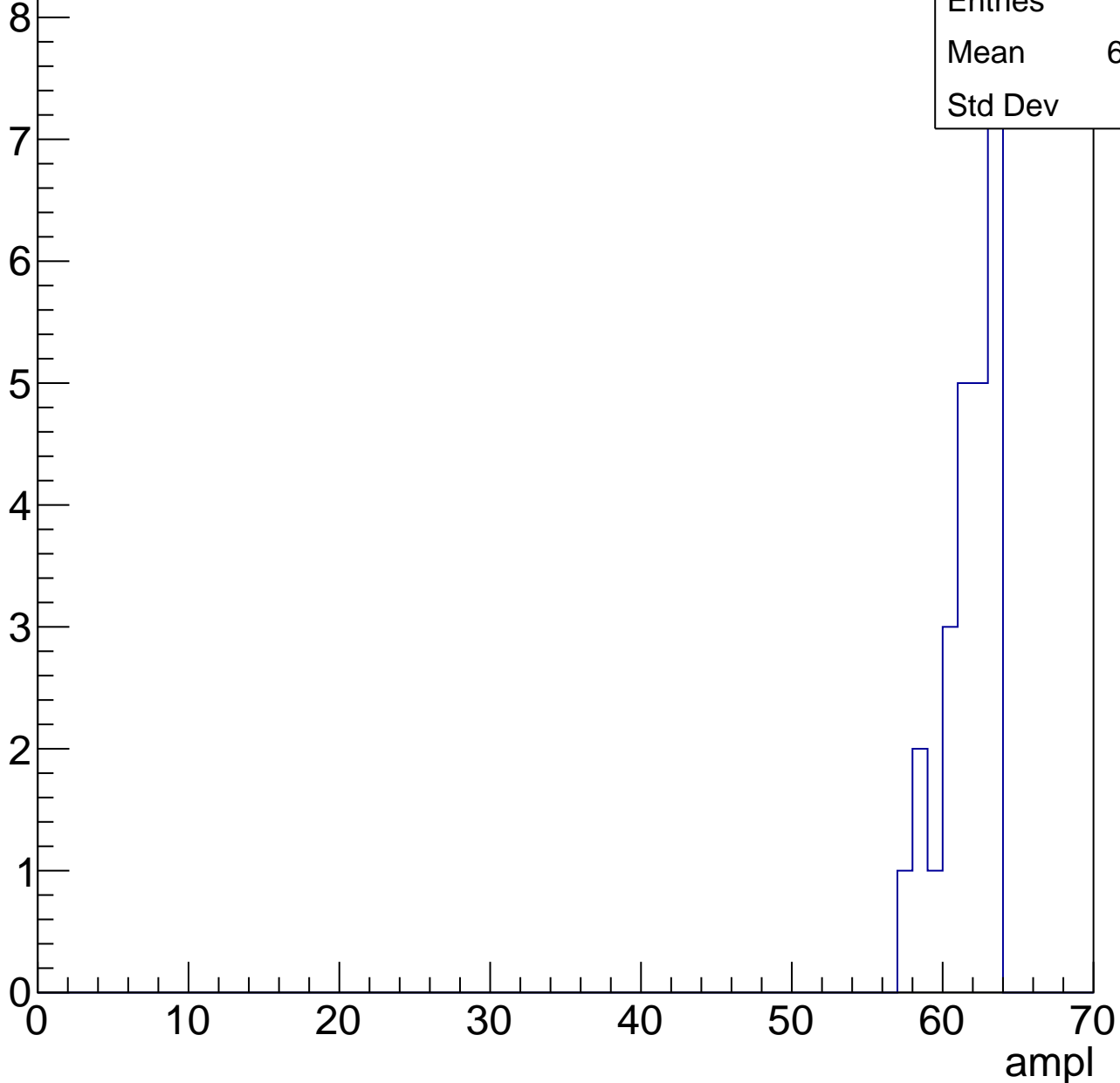


# B1L000S, U1-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	61.24
Std Dev	1.75





# B1L000S, U1-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

# B1L000S, U1-ch72, adc0

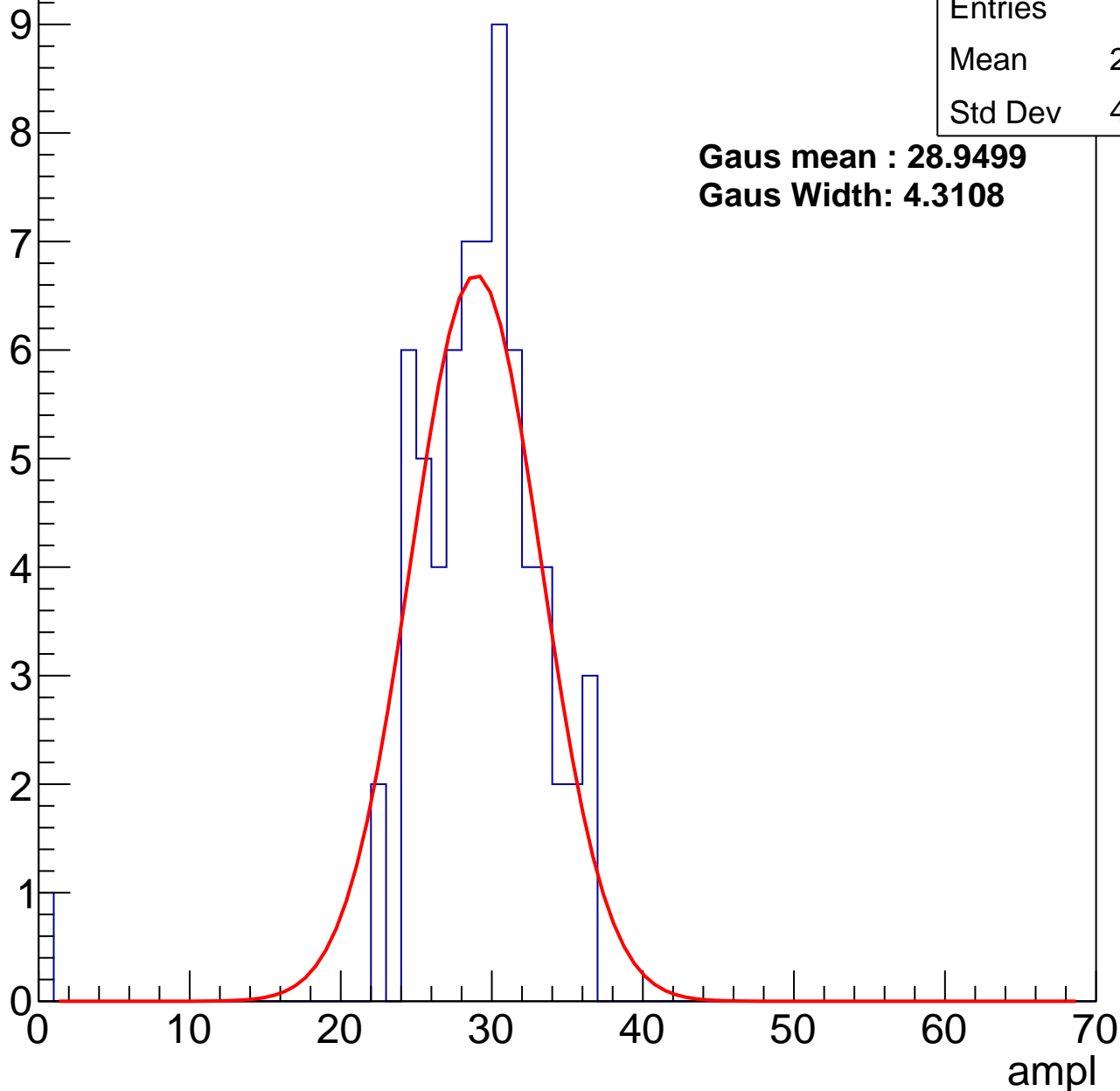
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.53
Std Dev	4.885

**Gaus mean : 28.9499**

**Gaus Width: 4.3108**



# B1L000S, U1-ch72, adc1

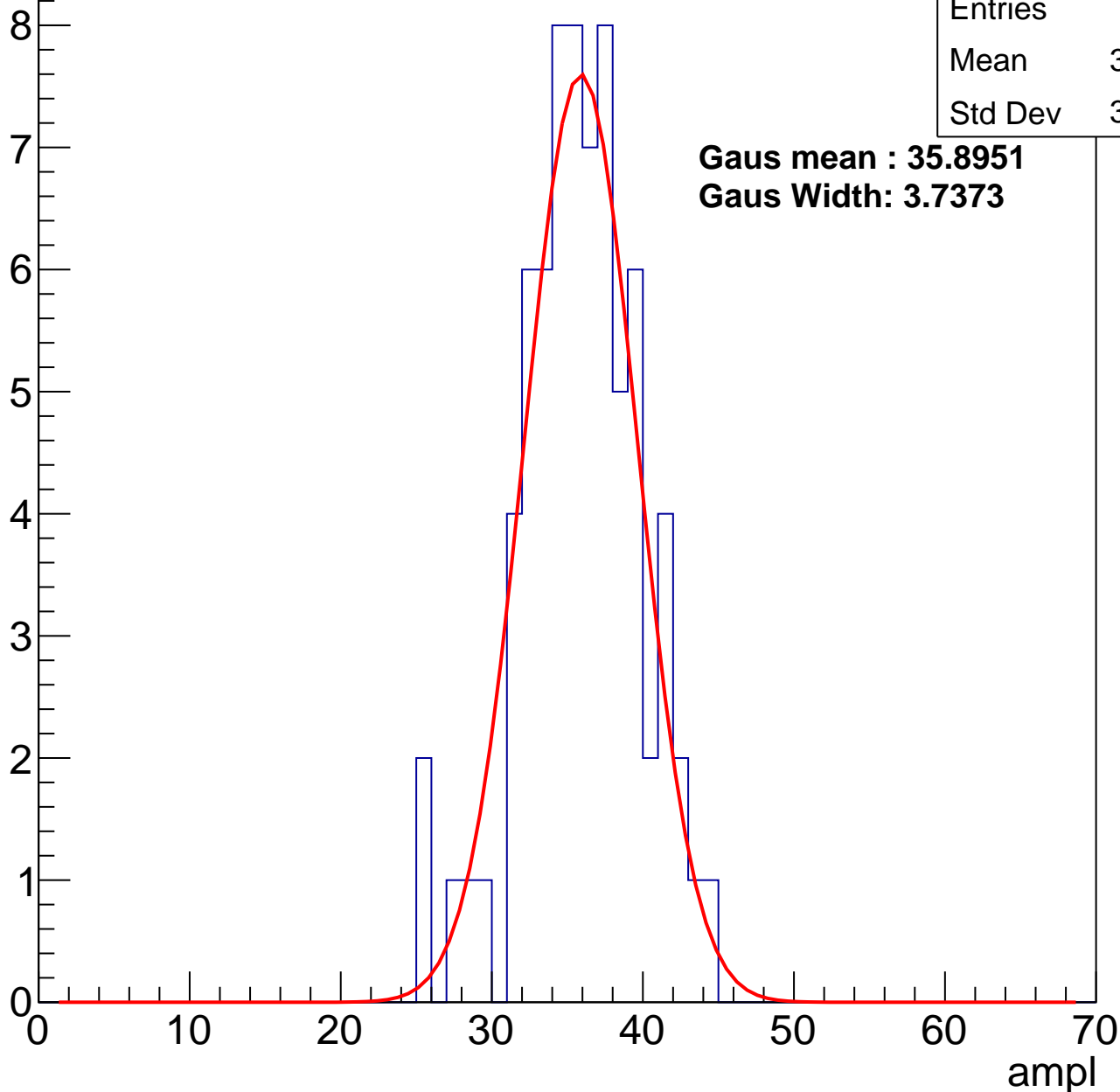
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	35.44
Std Dev	3.885

**Gaus mean : 35.8951**

**Gaus Width: 3.7373**



# B1L000S, U1-ch72, adc2

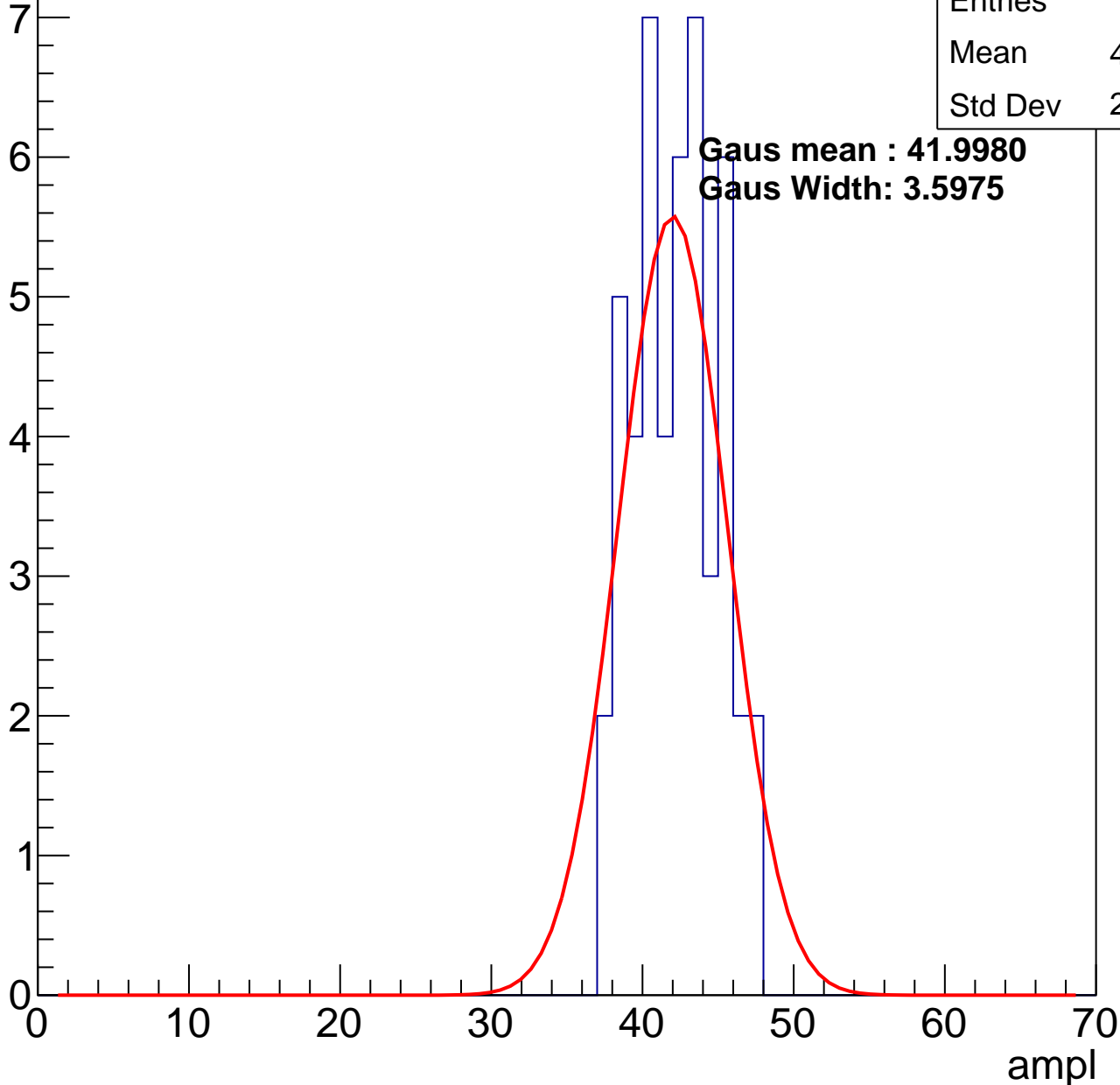
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	41.77
Std Dev	2.702

**Gaus mean : 41.9980**

**Gaus Width: 3.5975**



# B1L000S, U1-ch72, adc3

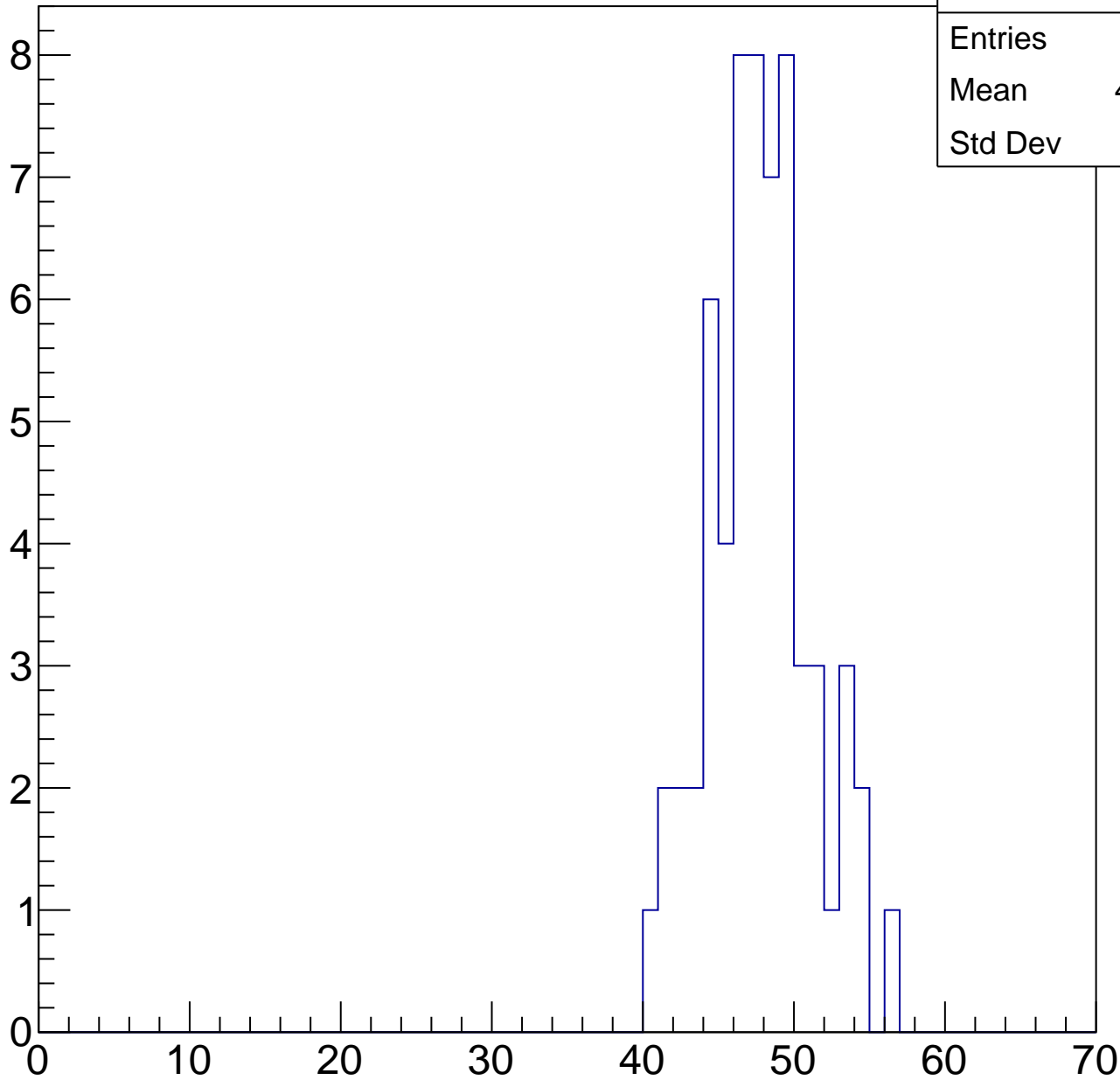
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	47.31
Std Dev	3.39

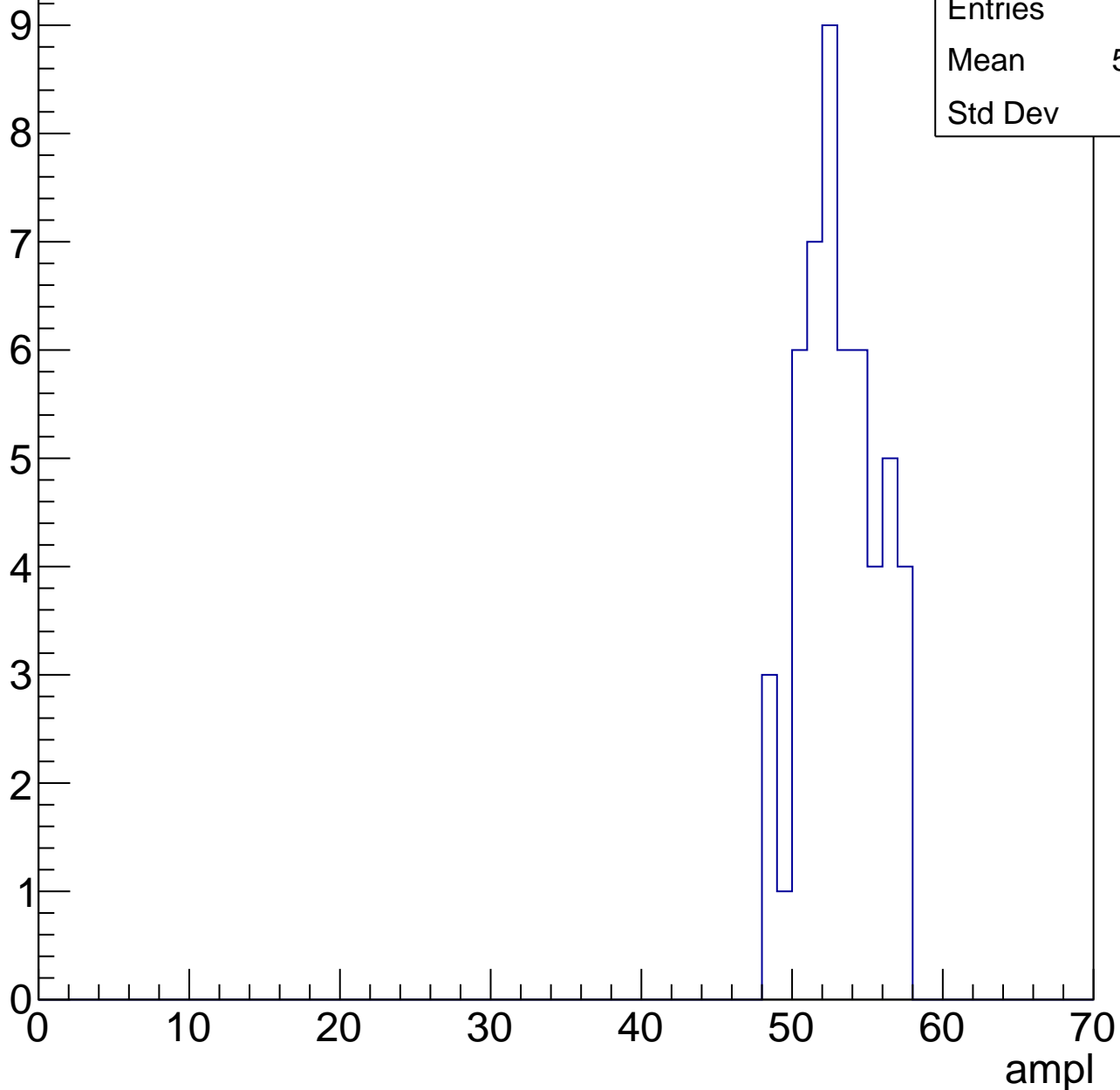
ampl



# B1L000S, U1-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

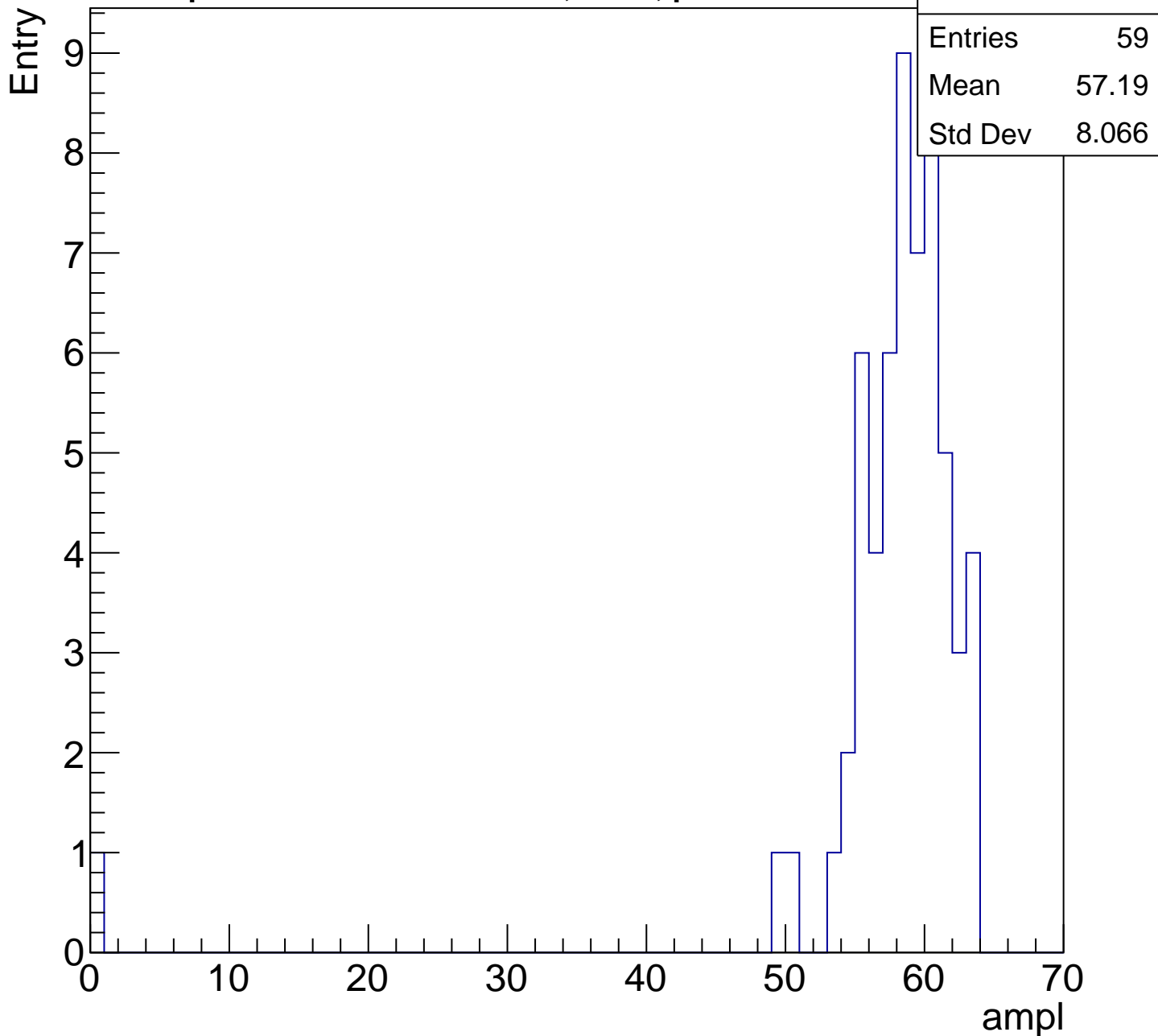
Entry



Entries	51
Mean	52.71
Std Dev	2.46

# B1L000S, U1-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

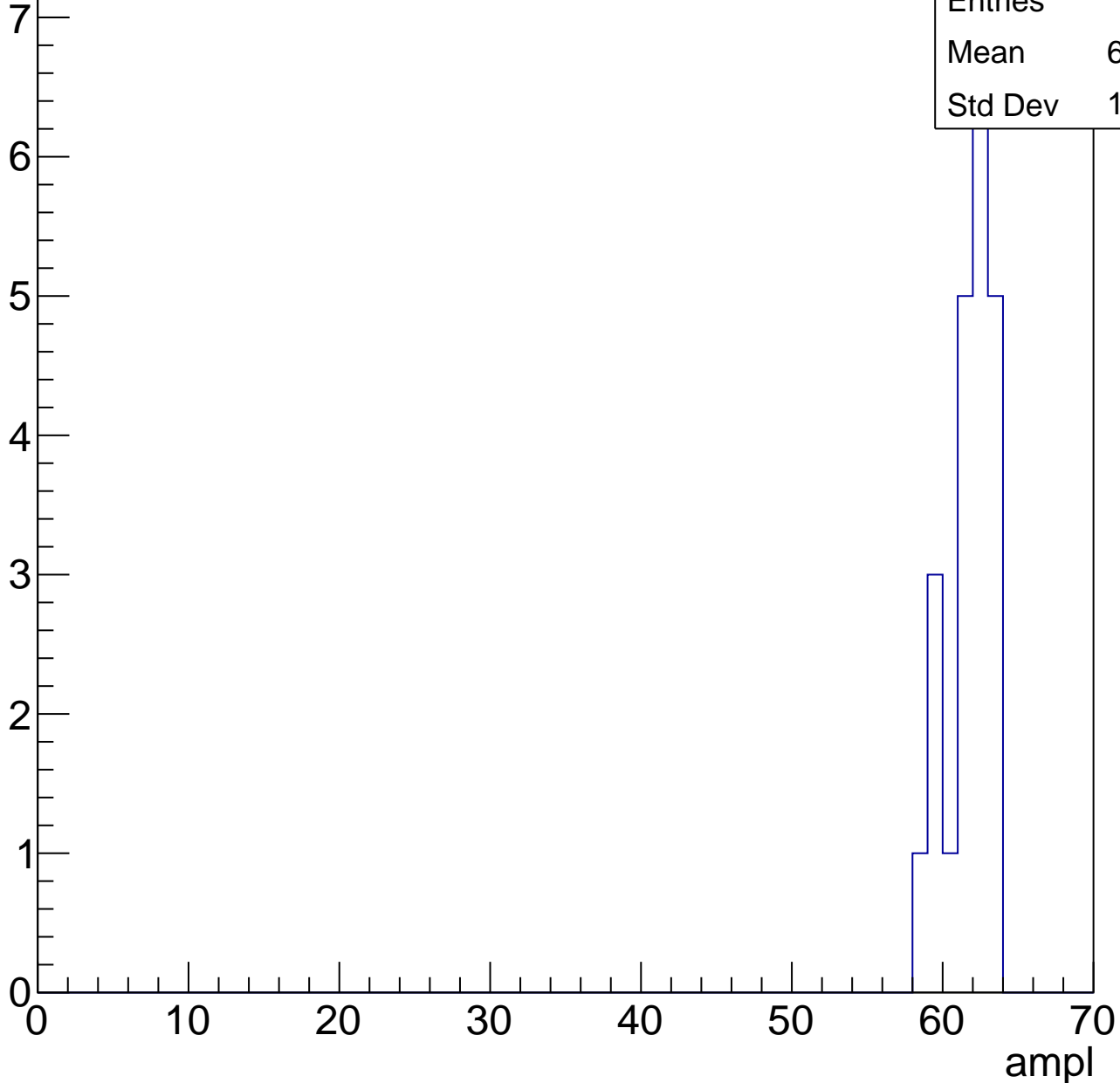


# B1L000S, U1-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	61.32
Std Dev	1.458





# B1L000S, U1-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U1-ch73, adc0

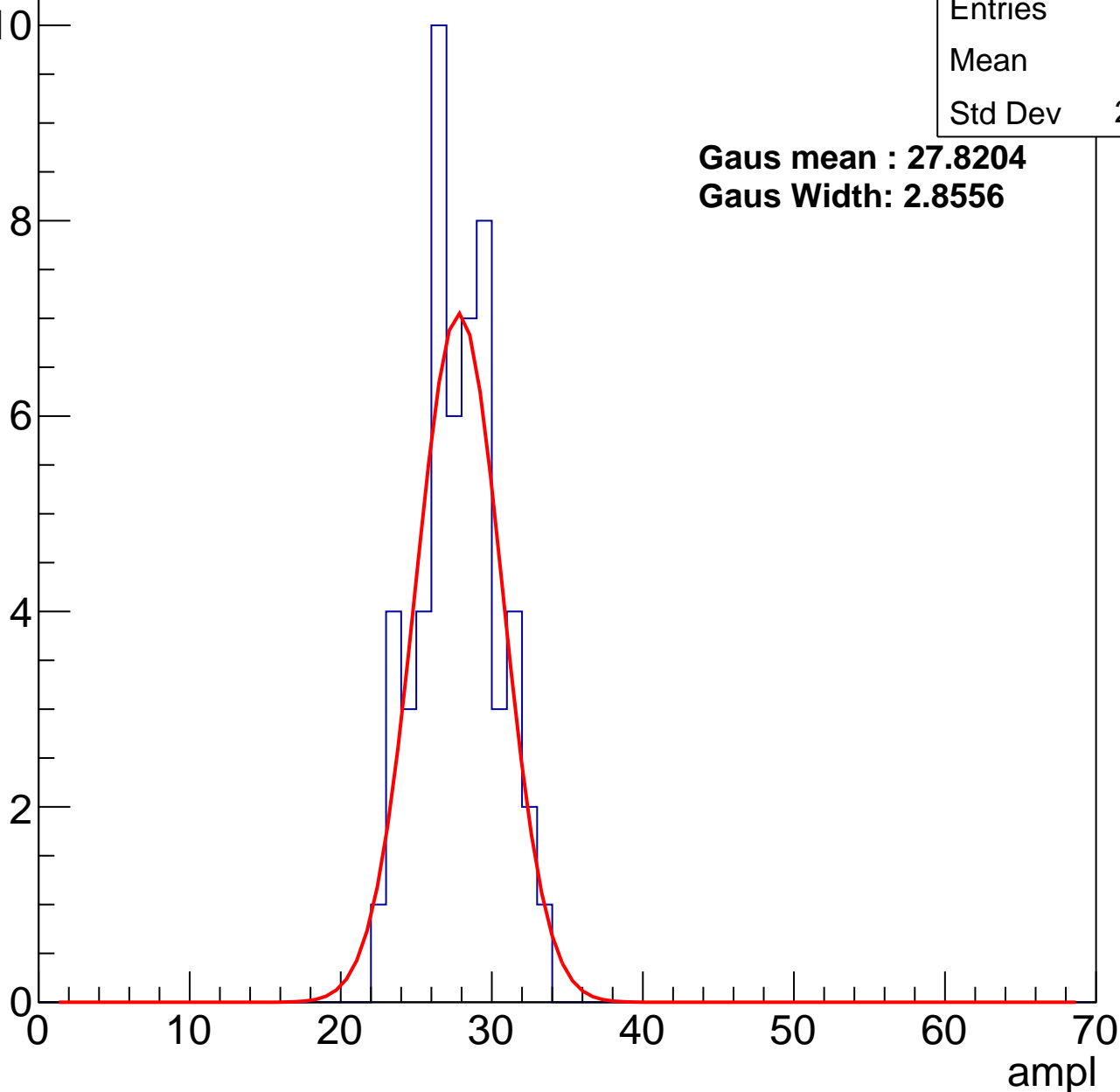
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	27.3
Std Dev	2.581

**Gaus mean : 27.8204**

**Gaus Width: 2.8556**



# B1L000S, U1-ch73, adc1

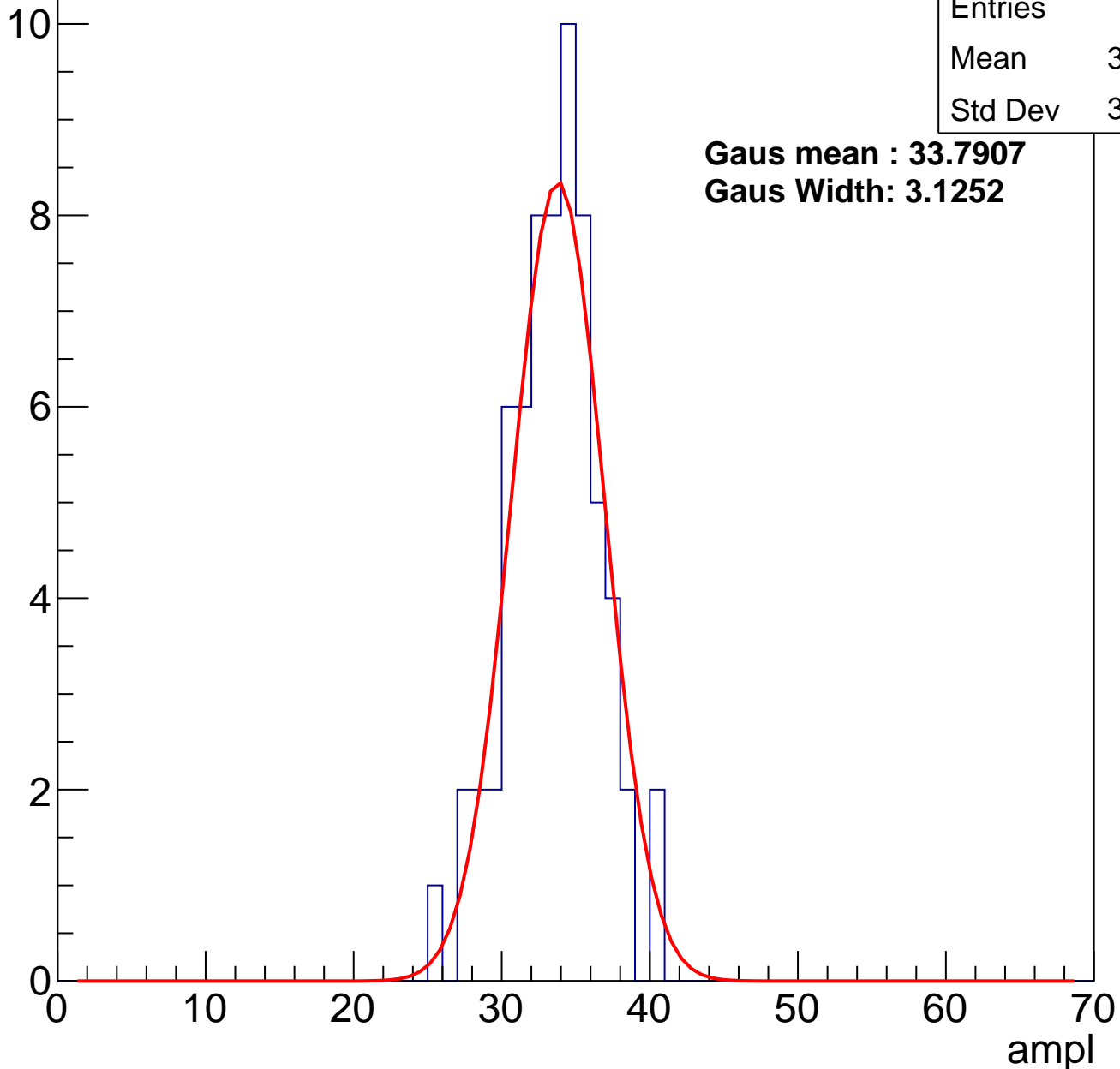
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	33.08
Std Dev	3.022

**Gaus mean : 33.7907**

**Gaus Width: 3.1252**

Entry



# B1L000S, U1-ch73, adc2

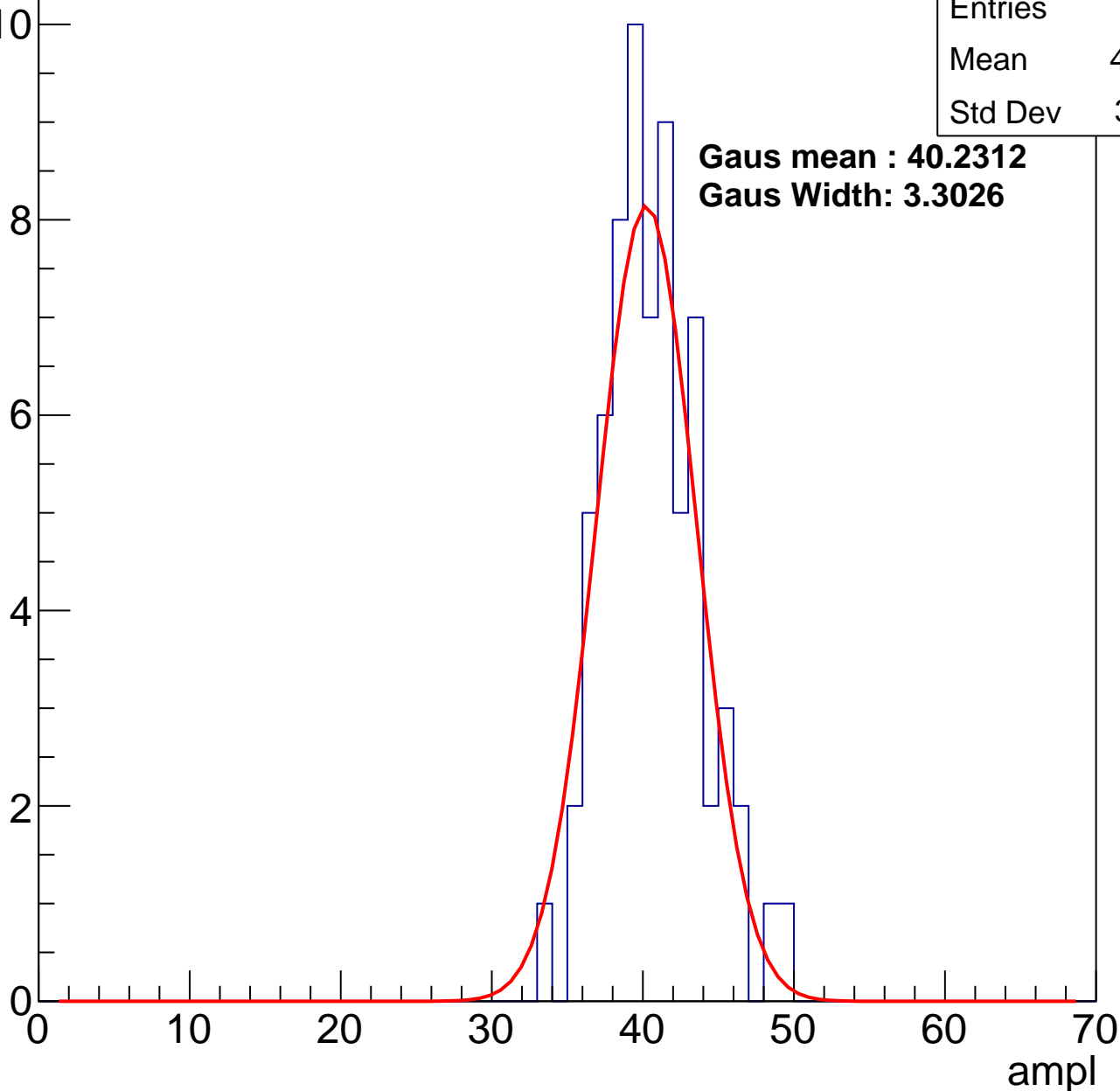
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	40.16
Std Dev	3.161

**Gaus mean : 40.2312**

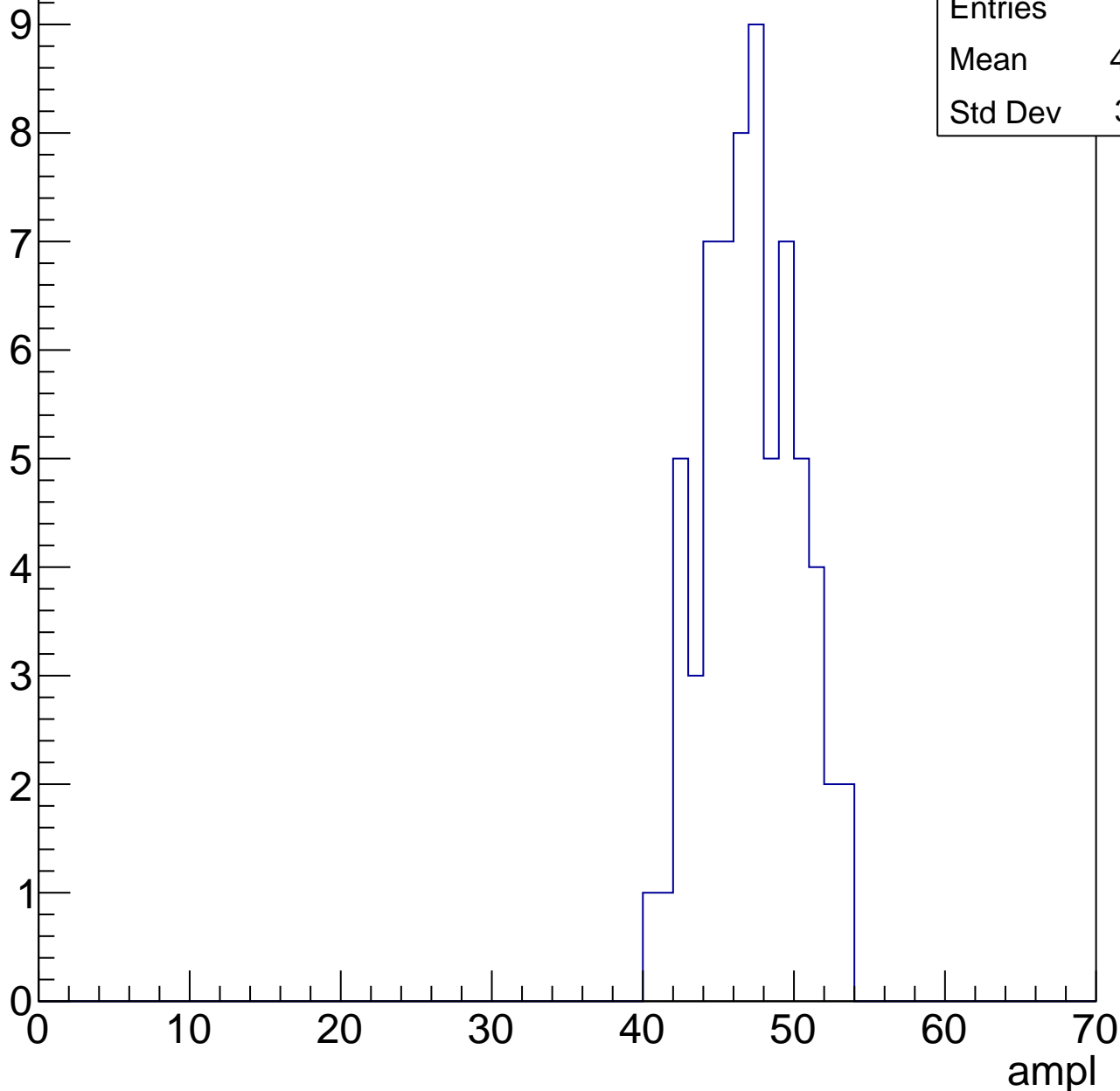
**Gaus Width: 3.3026**



# B1L000S, U1-ch73, adc3

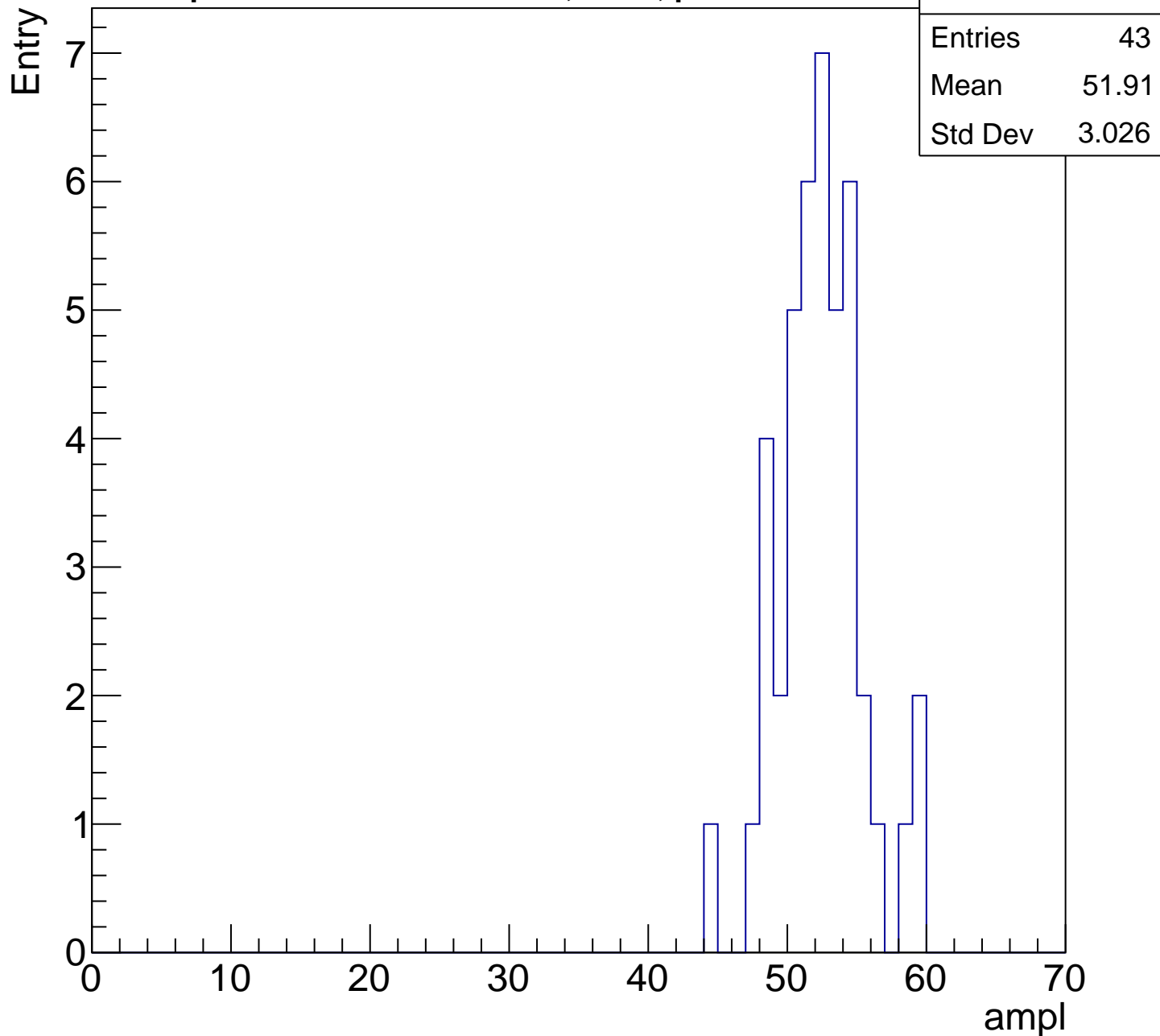
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

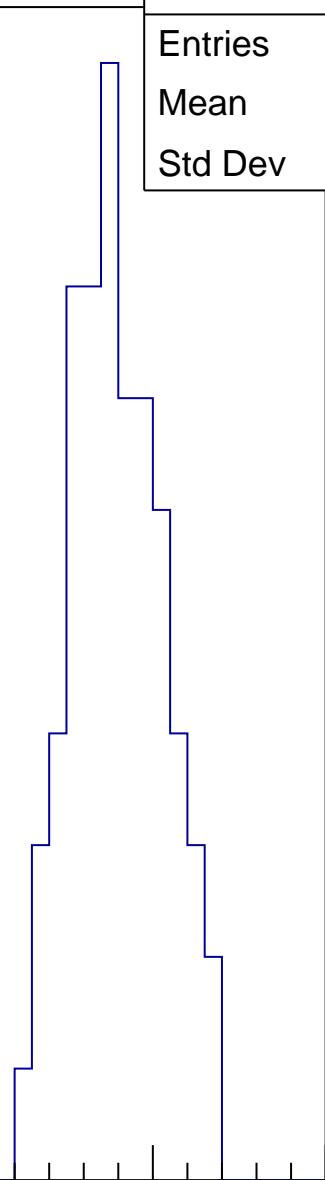
Entries	63
Mean	57.46
Std Dev	2.63

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

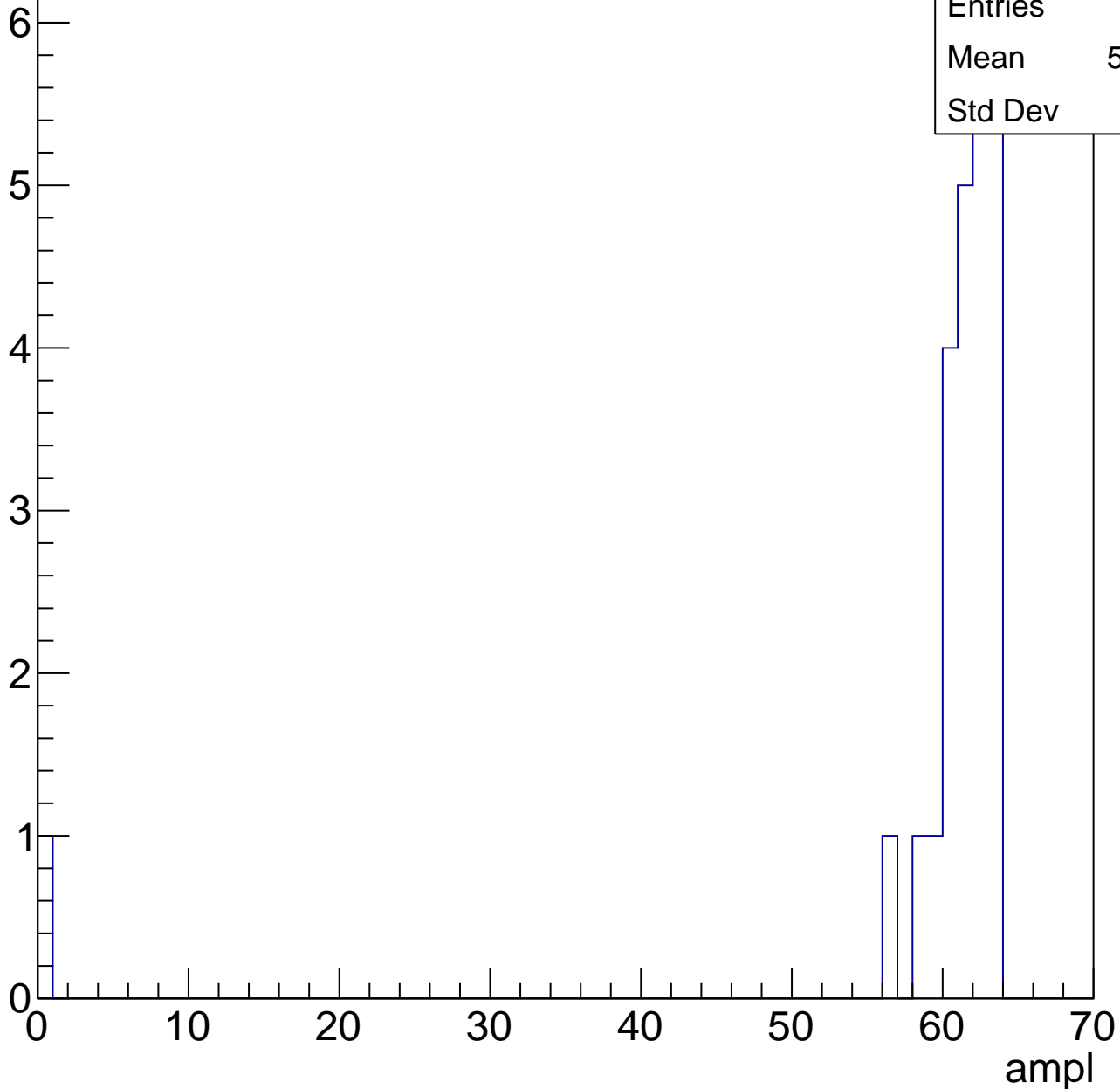


# B1L000S, U1-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.72
Std Dev	12.1

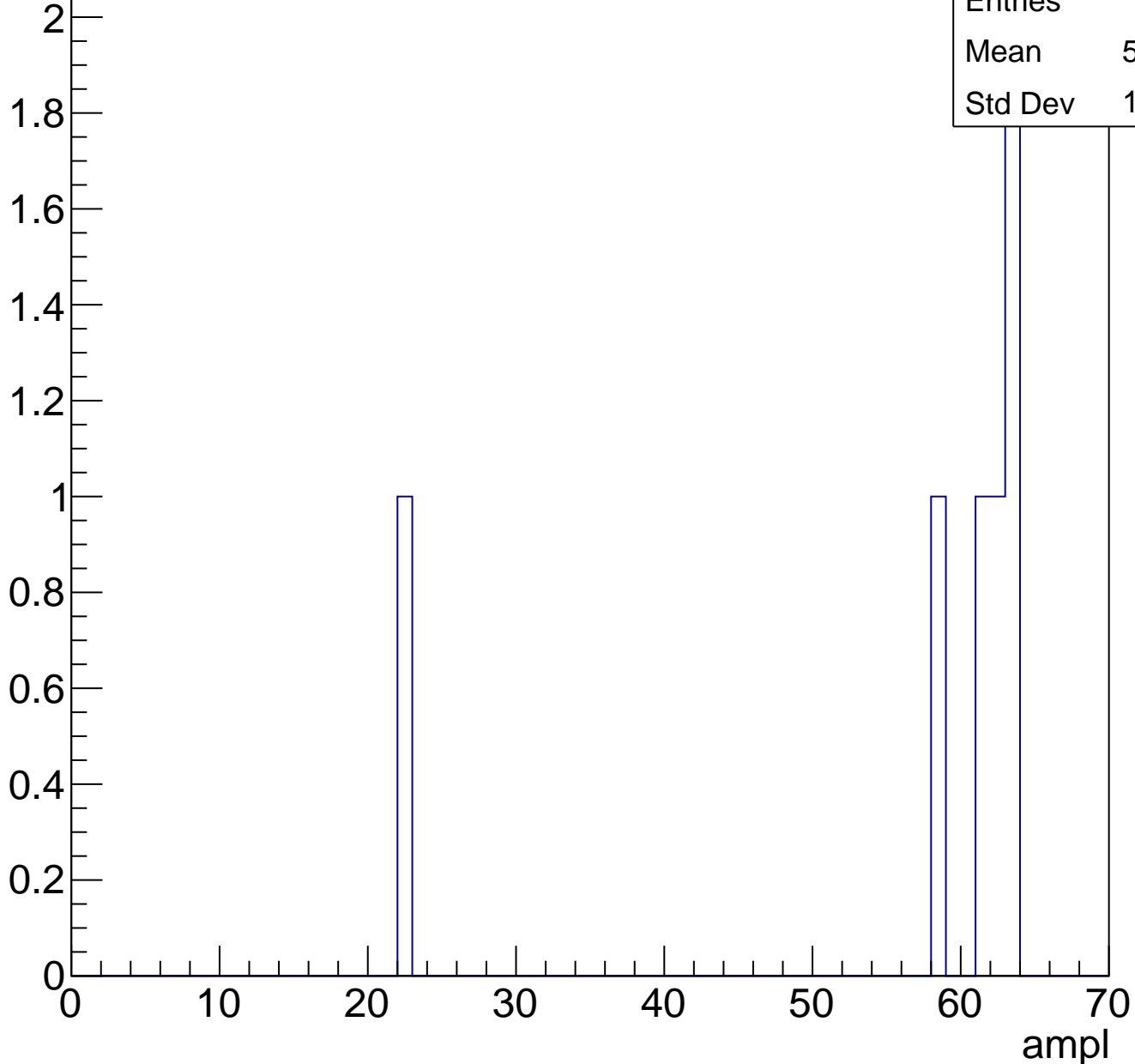




# B1L000S, U1-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch74, adc0

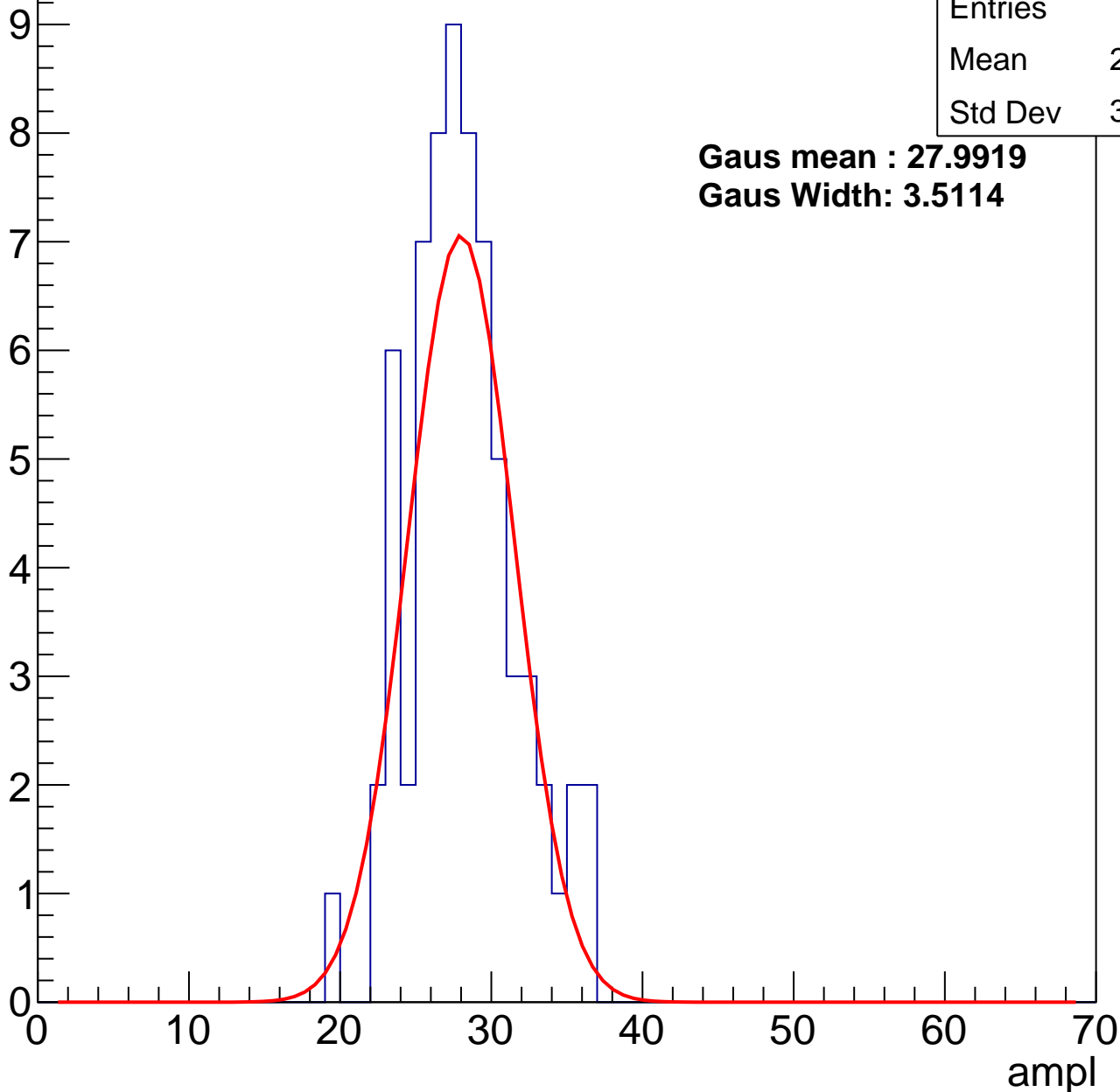
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	27.69
Std Dev	3.528

**Gaus mean : 27.9919**

**Gaus Width: 3.5114**



# B1L000S, U1-ch74, adc1

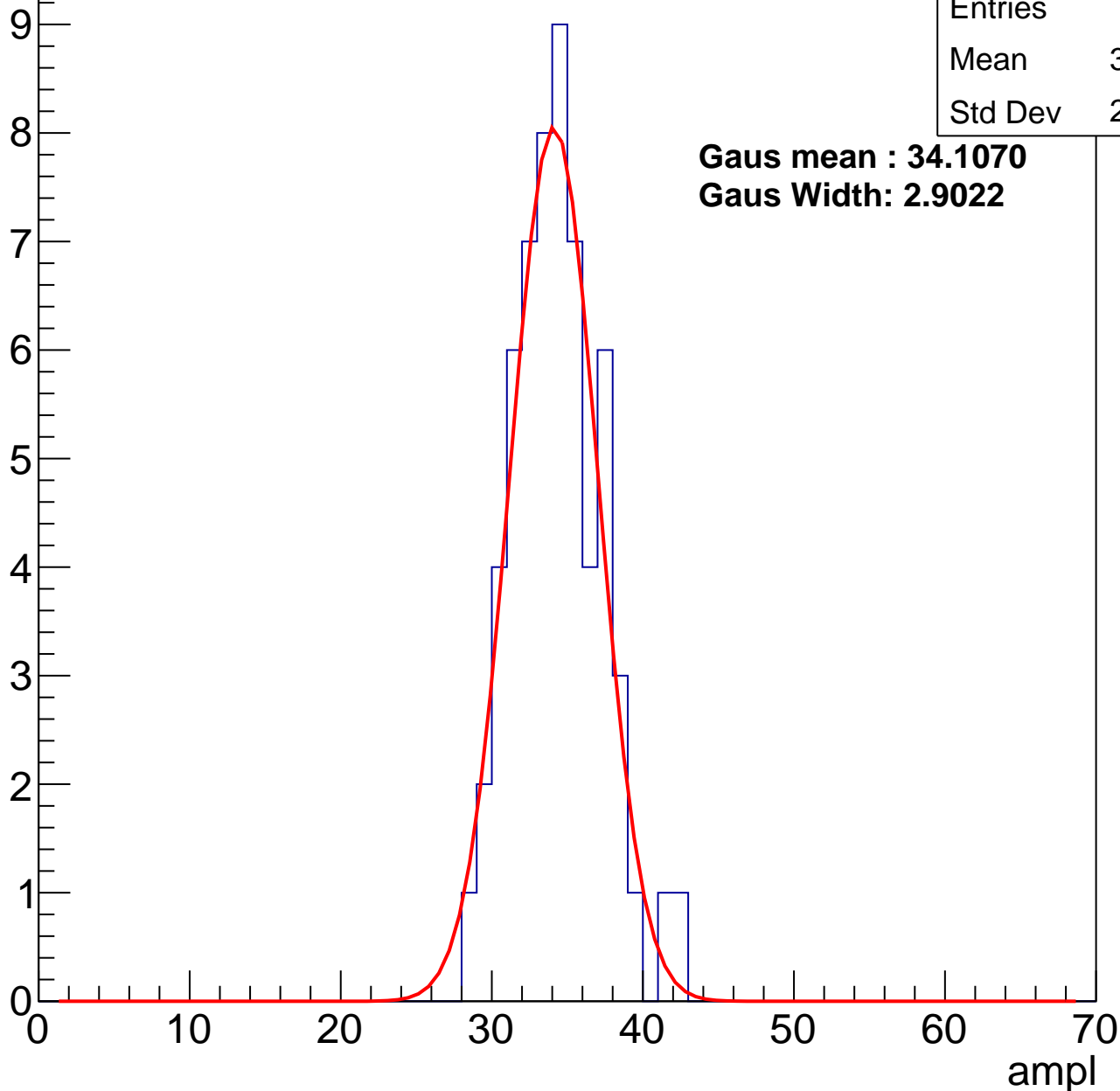
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	33.88
Std Dev	2.893

**Gaus mean : 34.1070**

**Gaus Width: 2.9022**



# B1L000S, U1-ch74, adc2

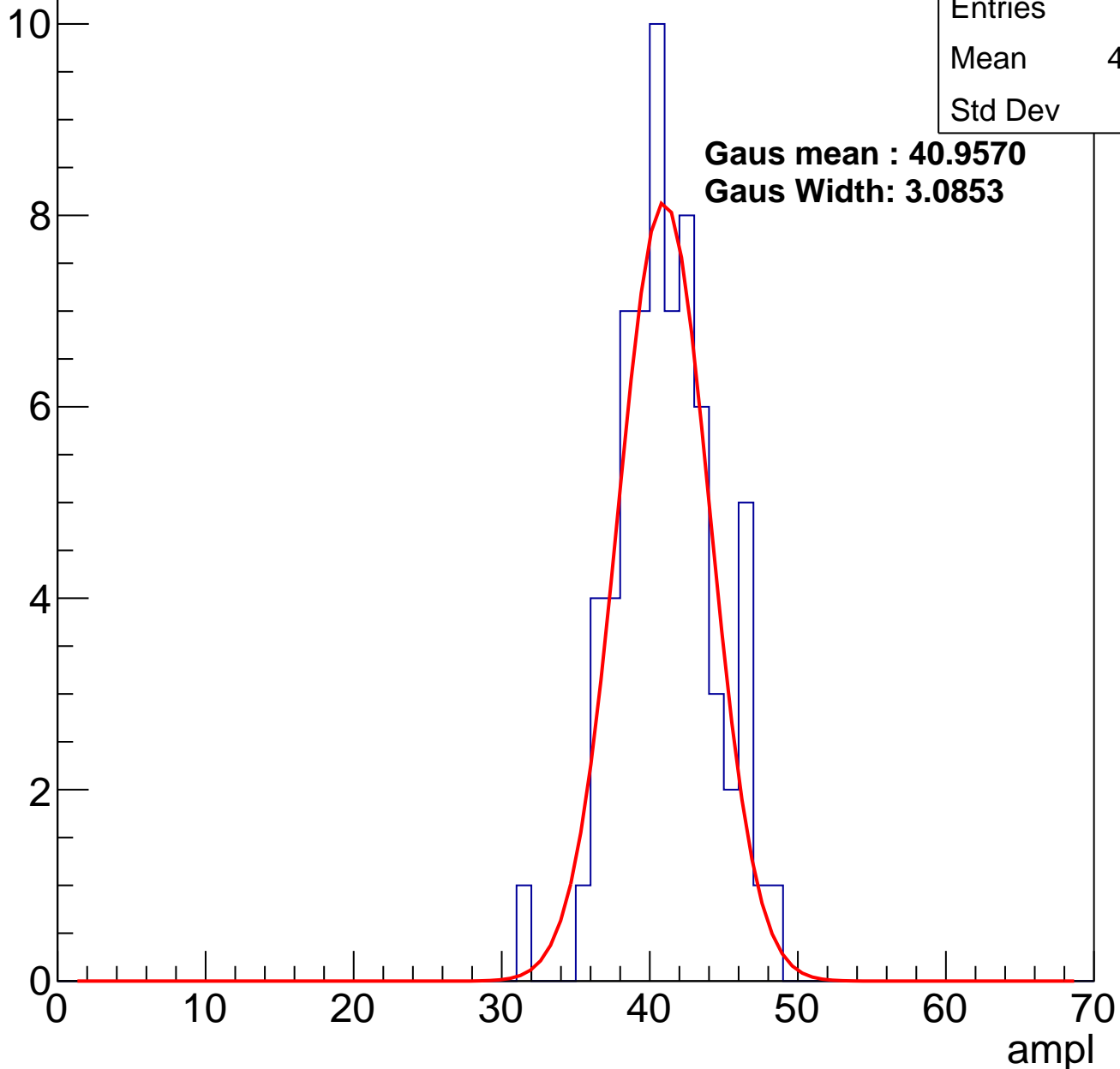
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	40.67
Std Dev	3.22

**Gaus mean : 40.9570**

**Gaus Width: 3.0853**

Entry

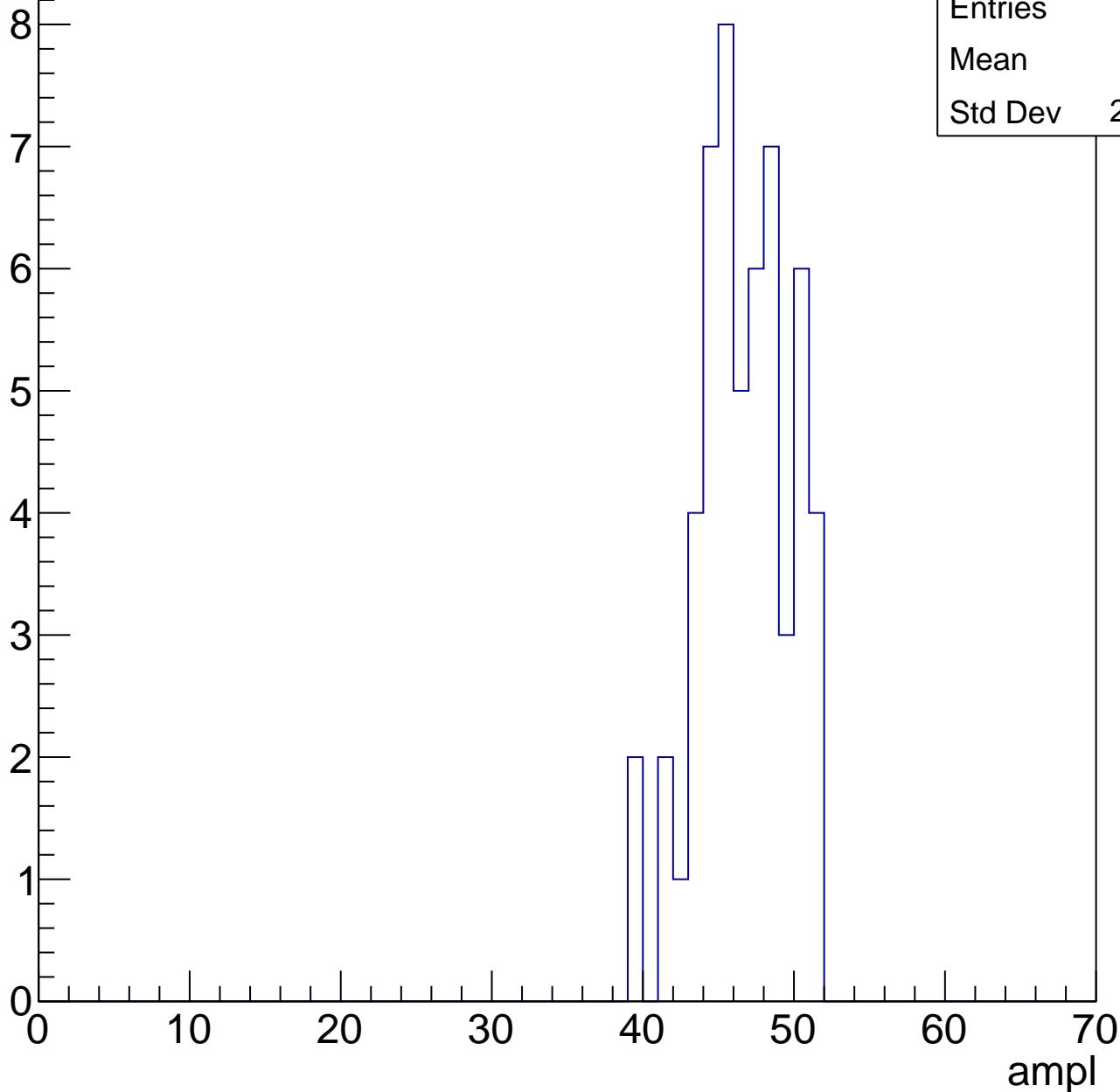


# B1L000S, U1-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	46.2
Std Dev	2.987

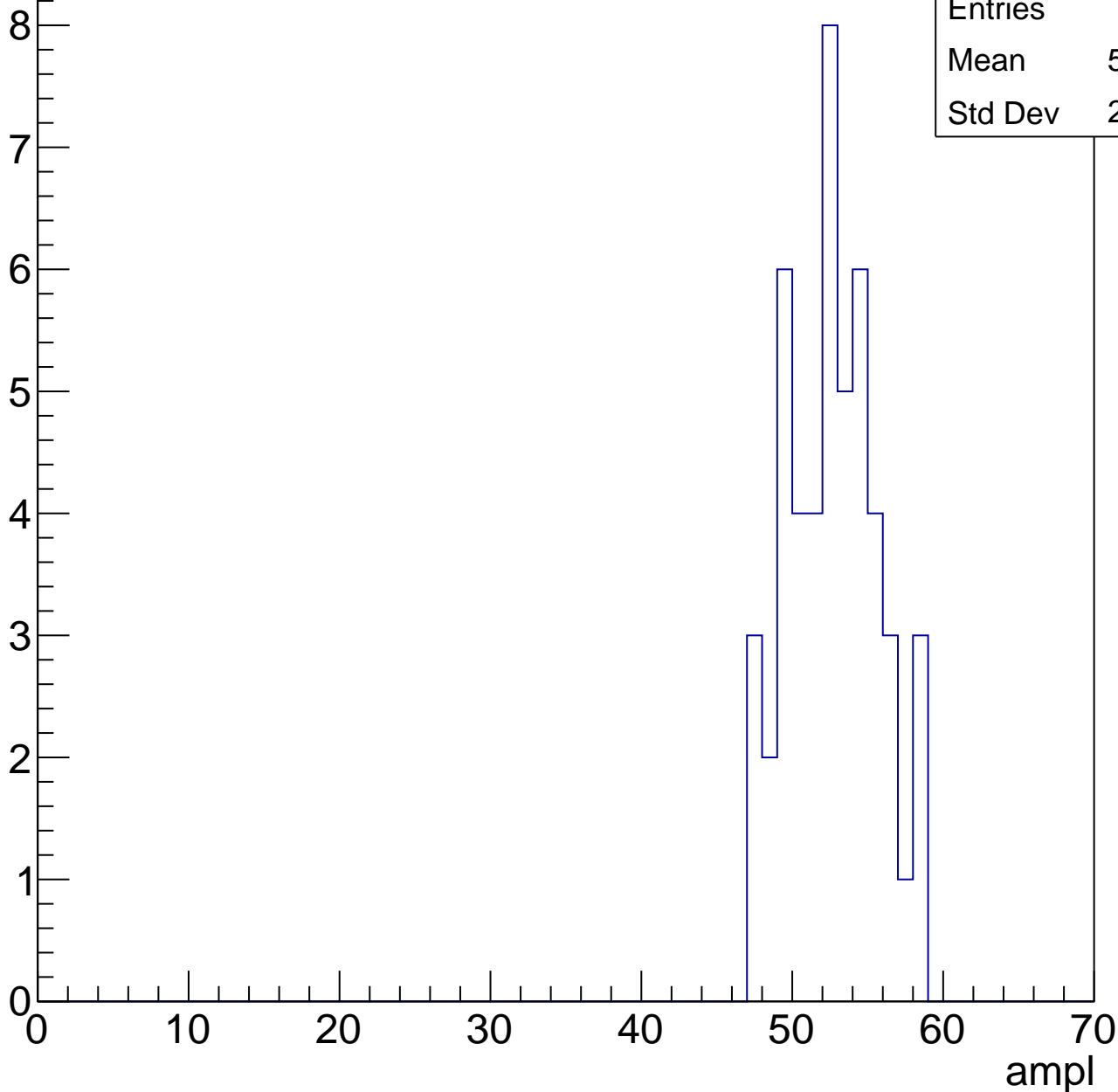


# B1L000S, U1-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

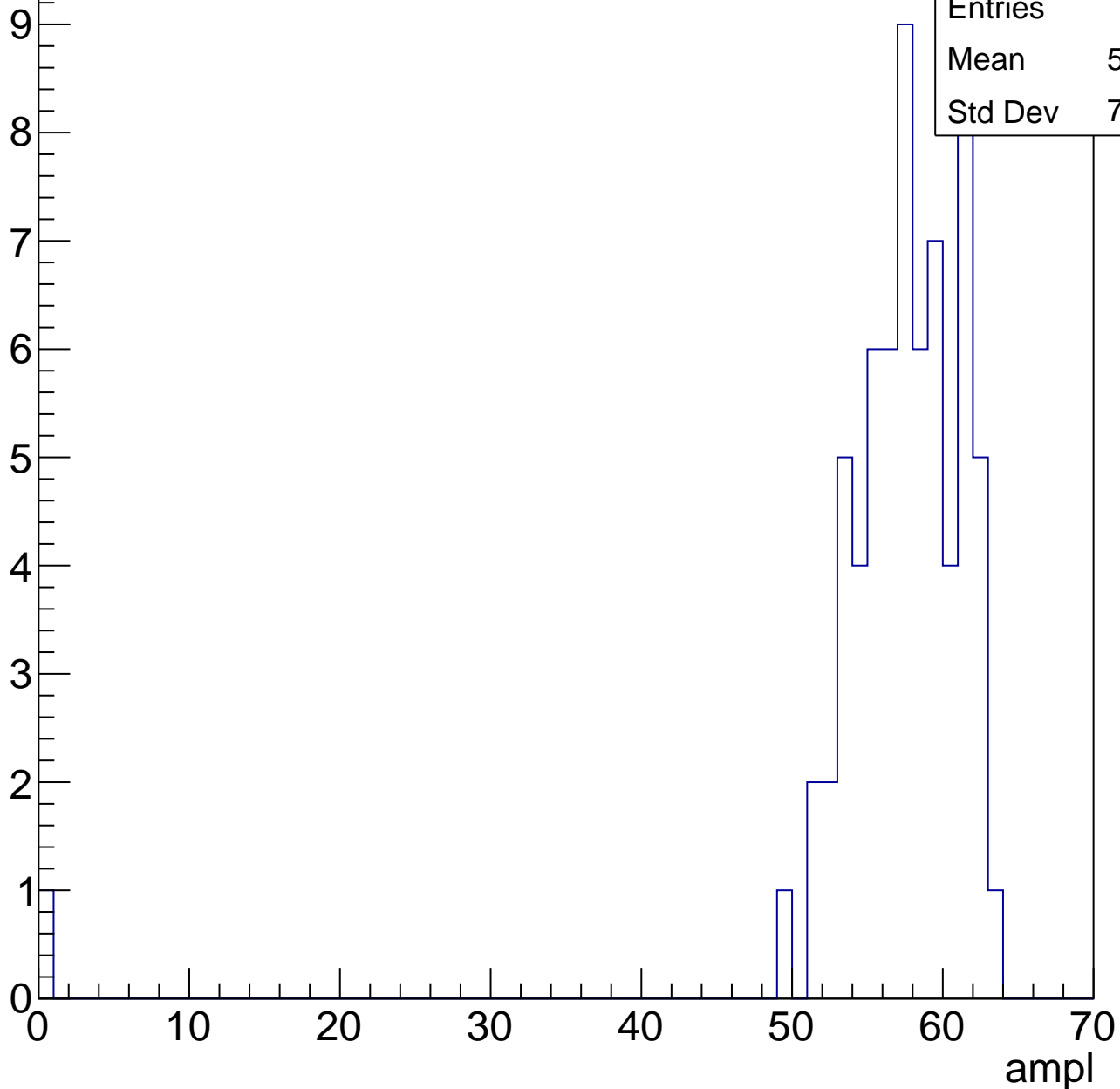
Entries	49
Mean	52.22
Std Dev	2.943



# B1L000S, U1-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

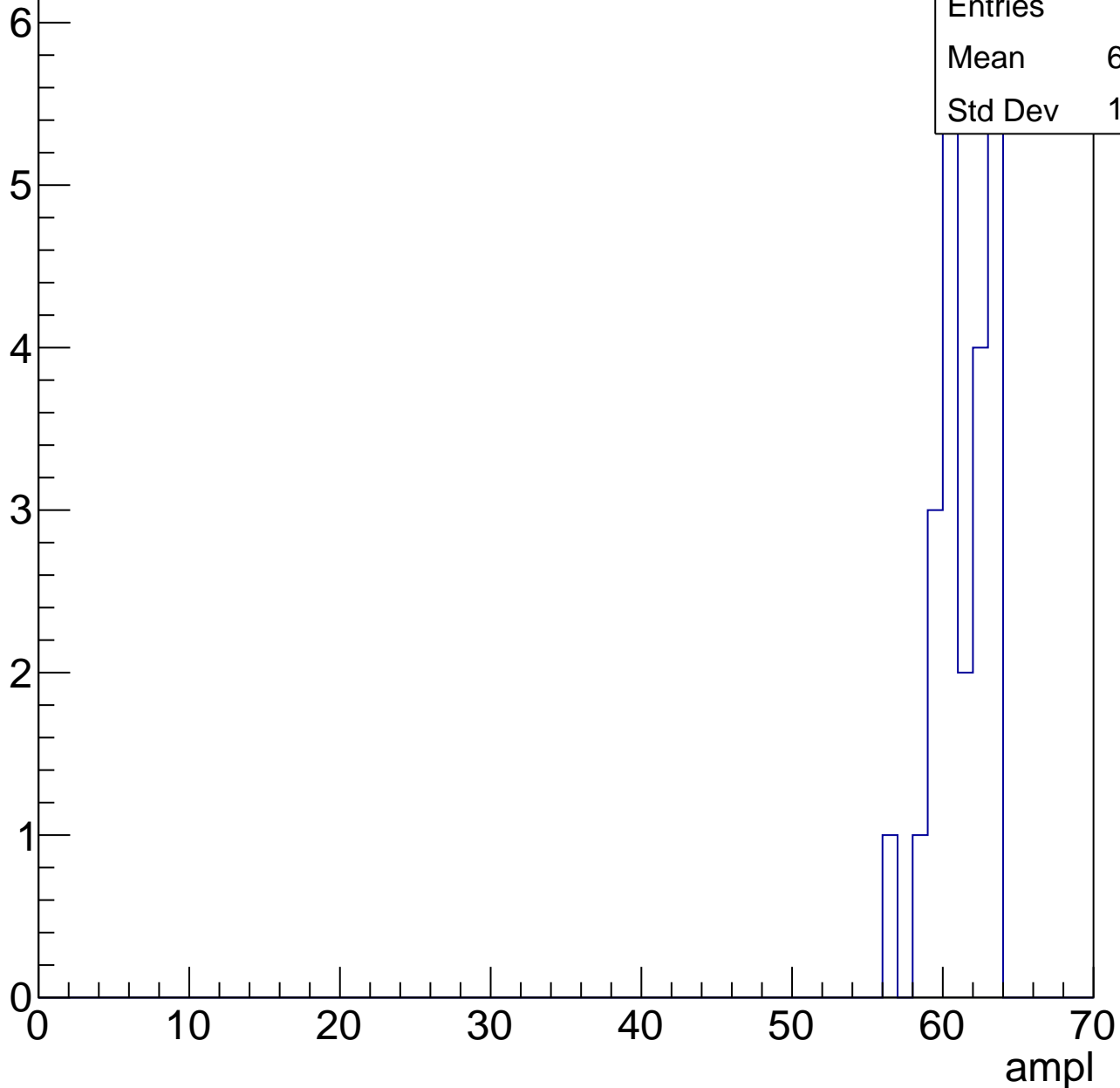
Entry



# B1L000S, U1-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

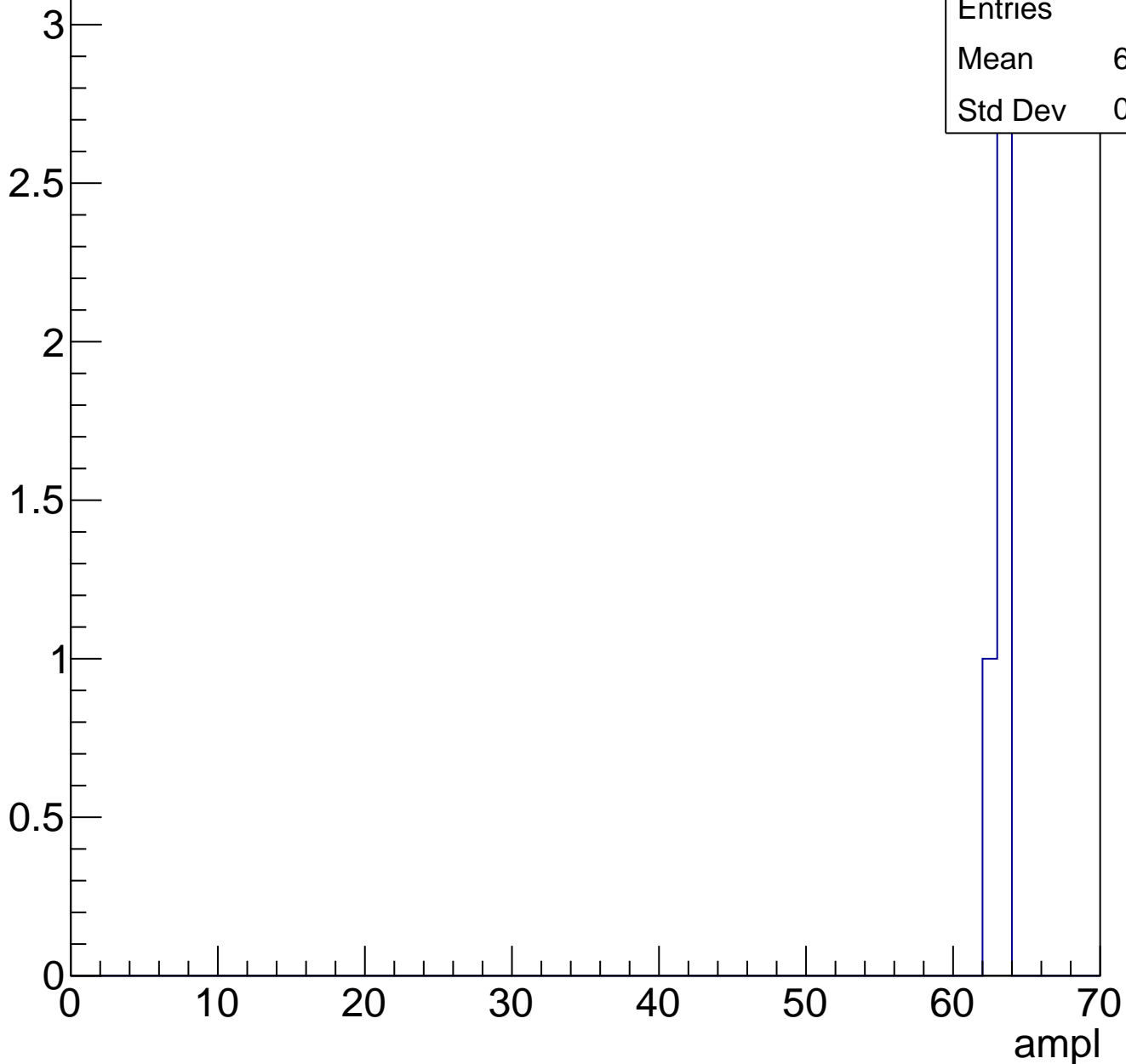




# B1L000S, U1-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch75, adc0

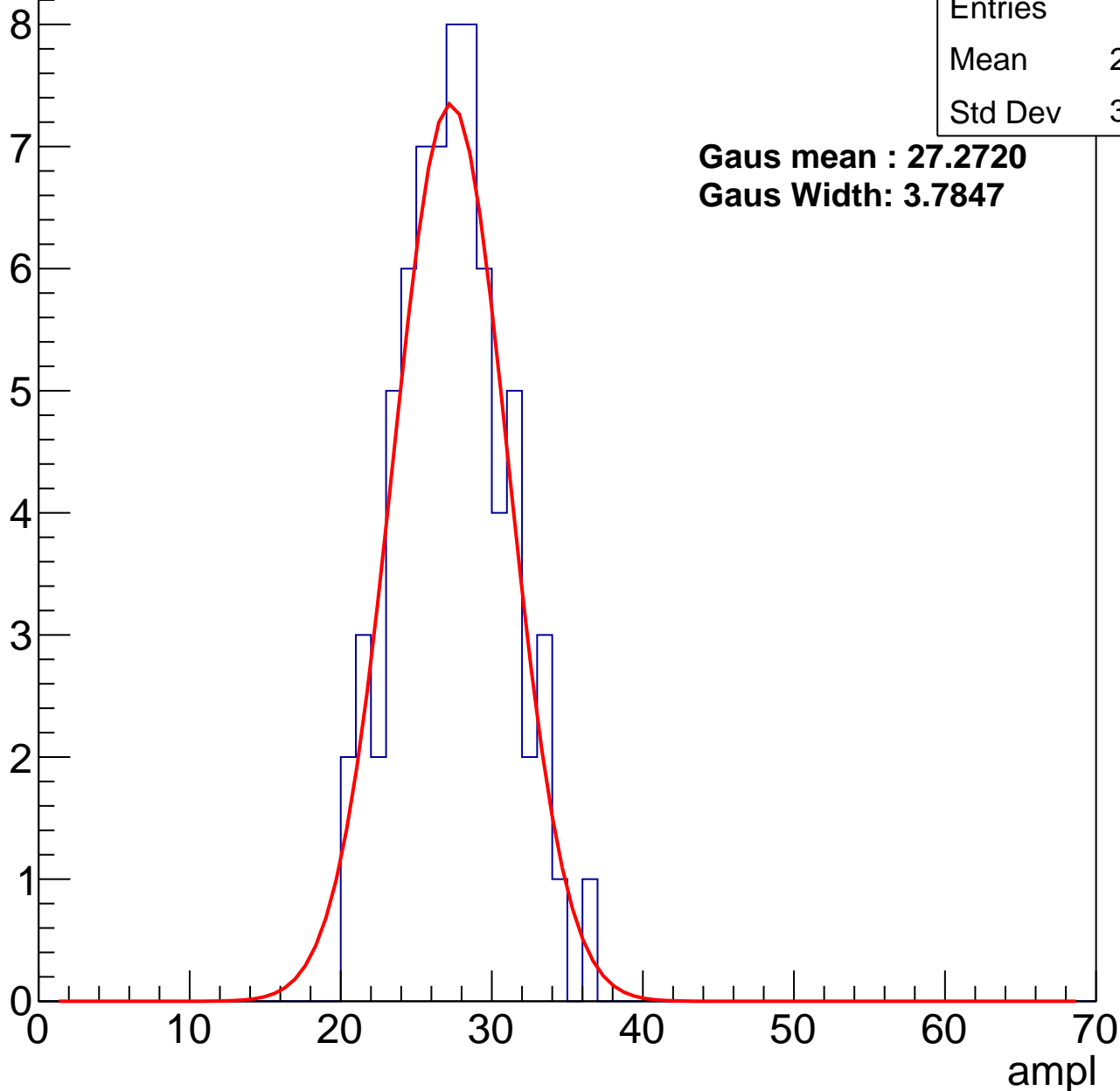
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	26.93
Std Dev	3.506

**Gaus mean : 27.2720**

**Gaus Width: 3.7847**



# B1L000S, U1-ch75, adc1

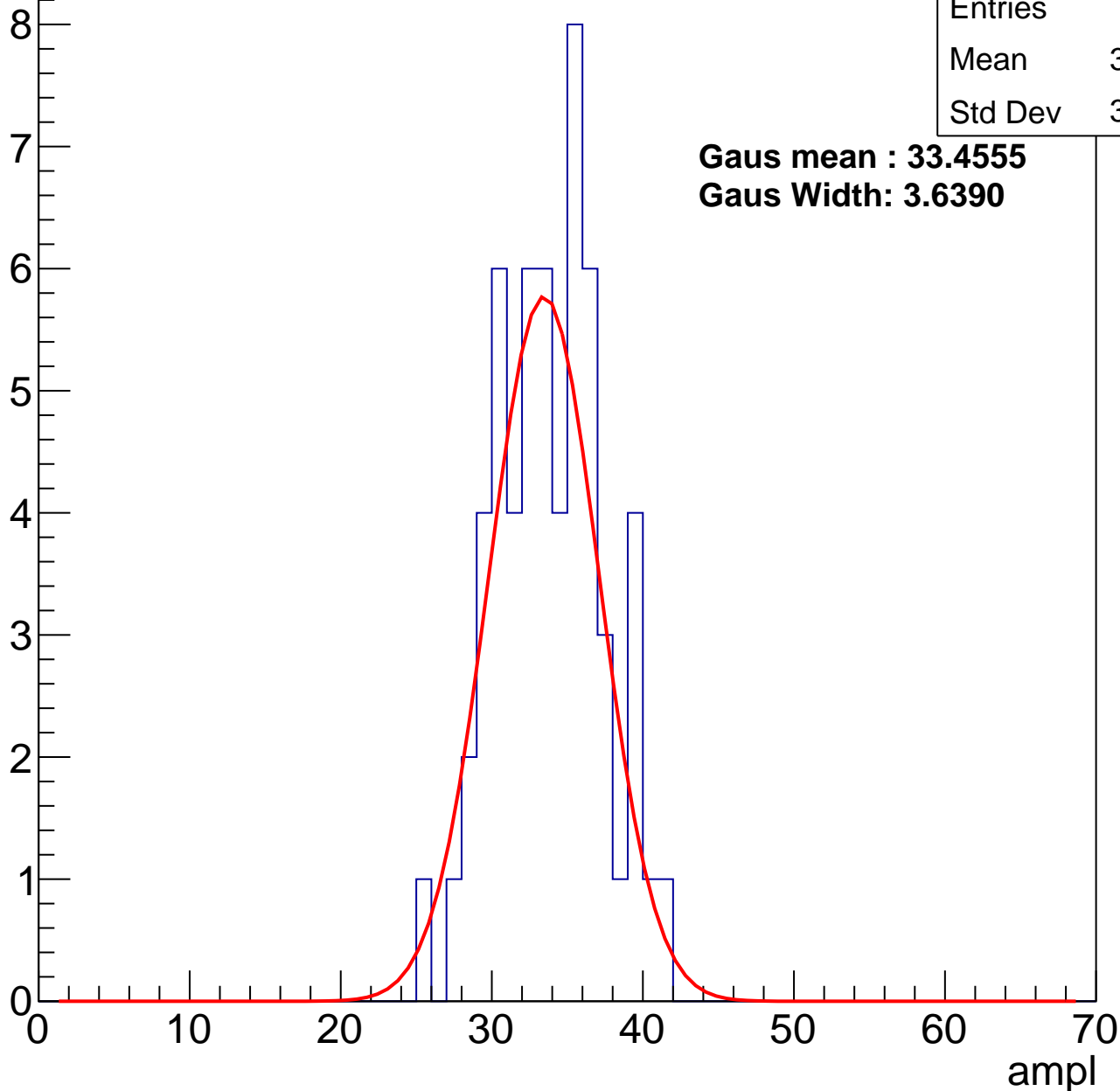
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	33.38
Std Dev	3.478

**Gaus mean : 33.4555**

**Gaus Width: 3.6390**



# B1L000S, U1-ch75, adc2

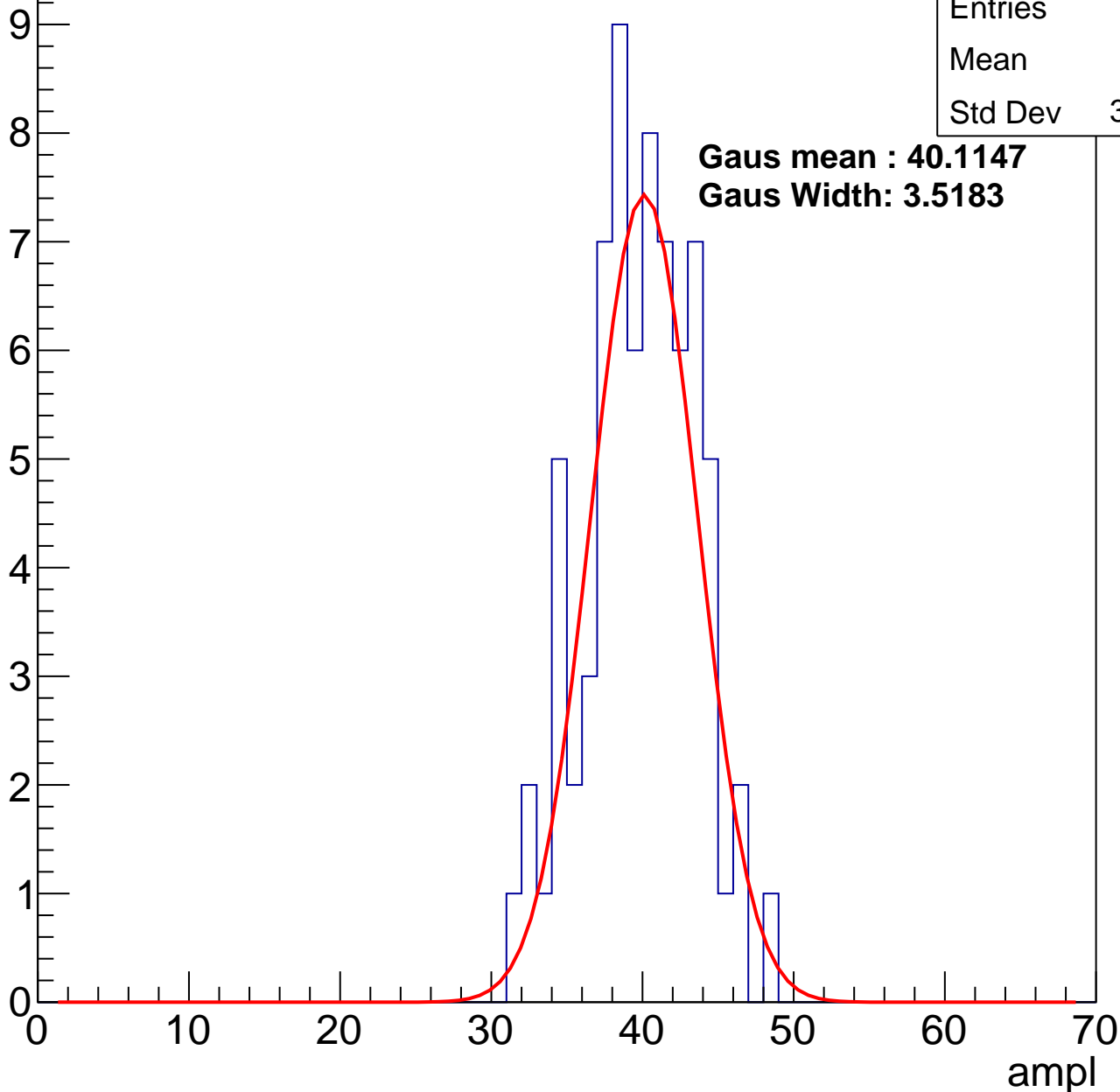
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	39.4
Std Dev	3.606

**Gaus mean : 40.1147**

**Gaus Width: 3.5183**

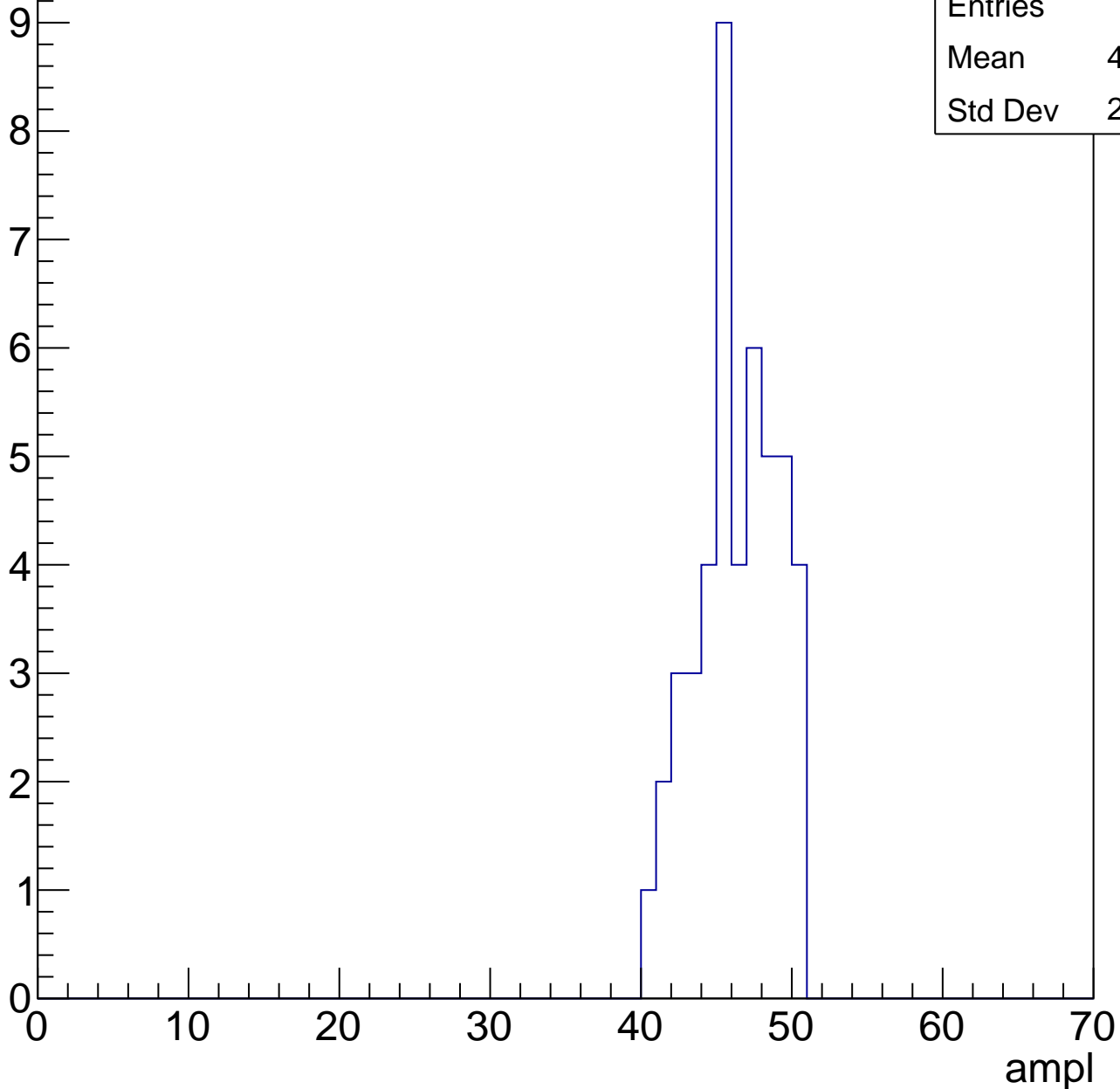


# B1L000S, U1-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	45.85
Std Dev	2.637

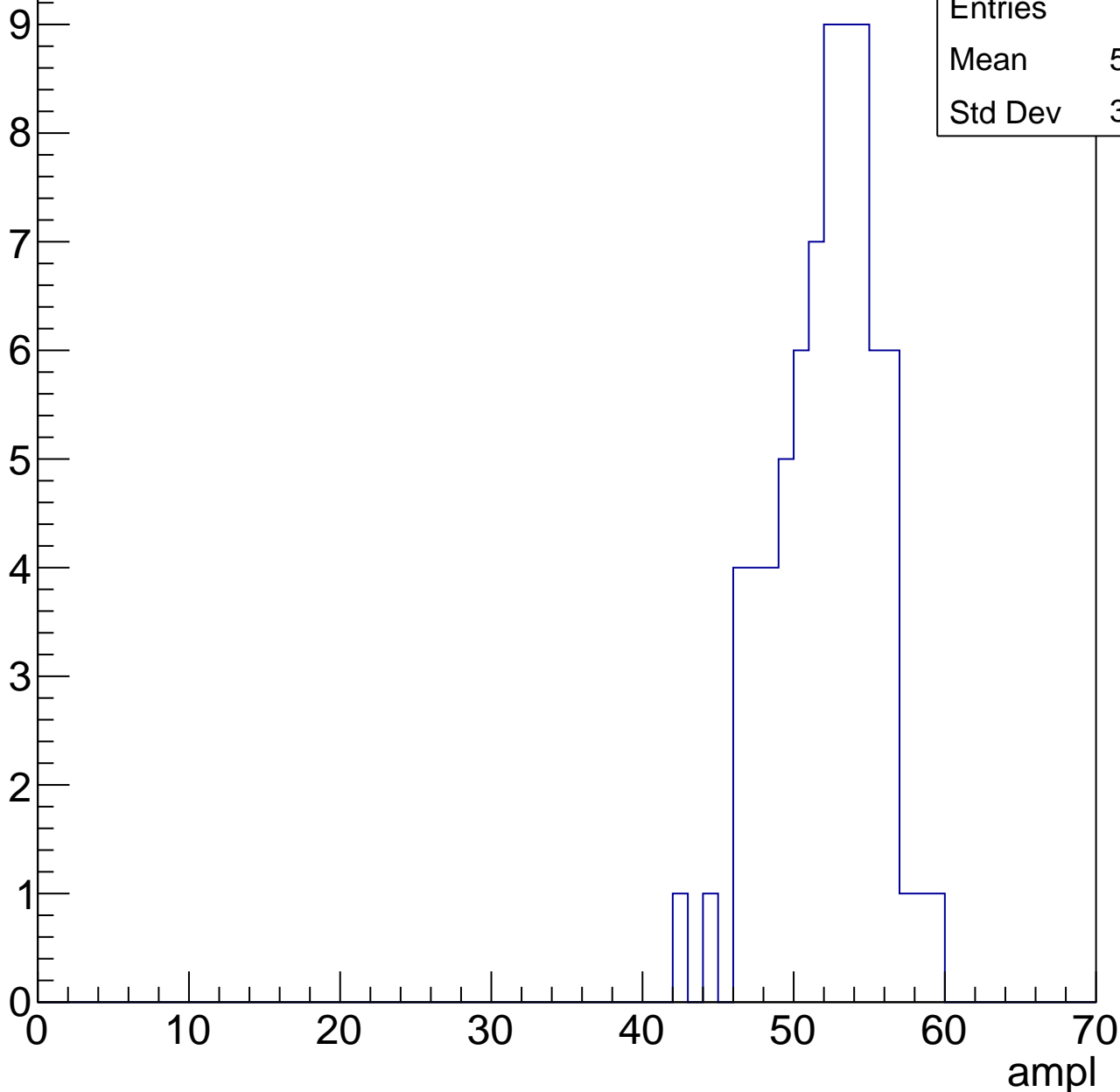


# B1L000S, U1-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

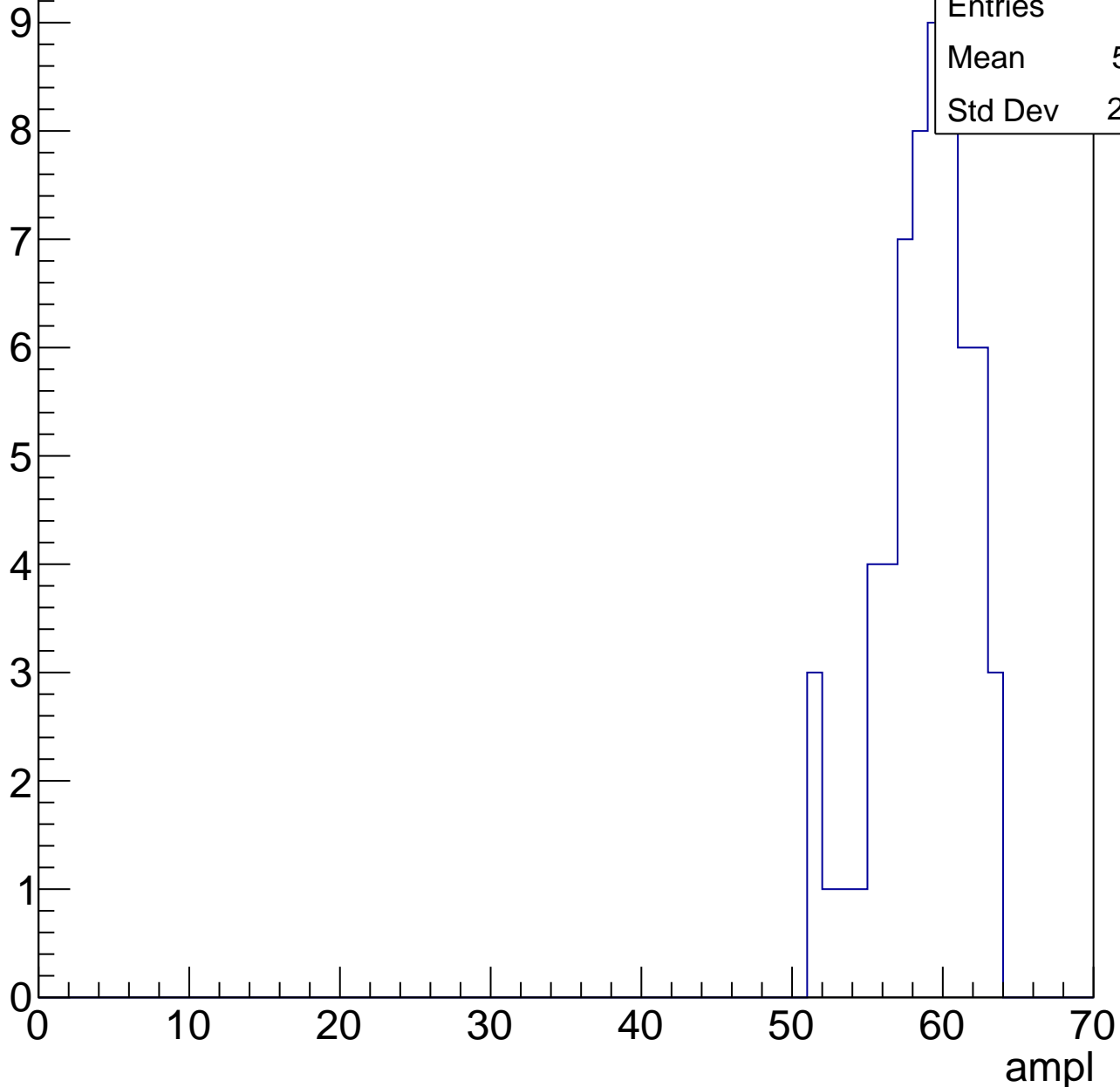
Entries	74
Mean	51.66
Std Dev	3.378



# B1L000S, U1-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



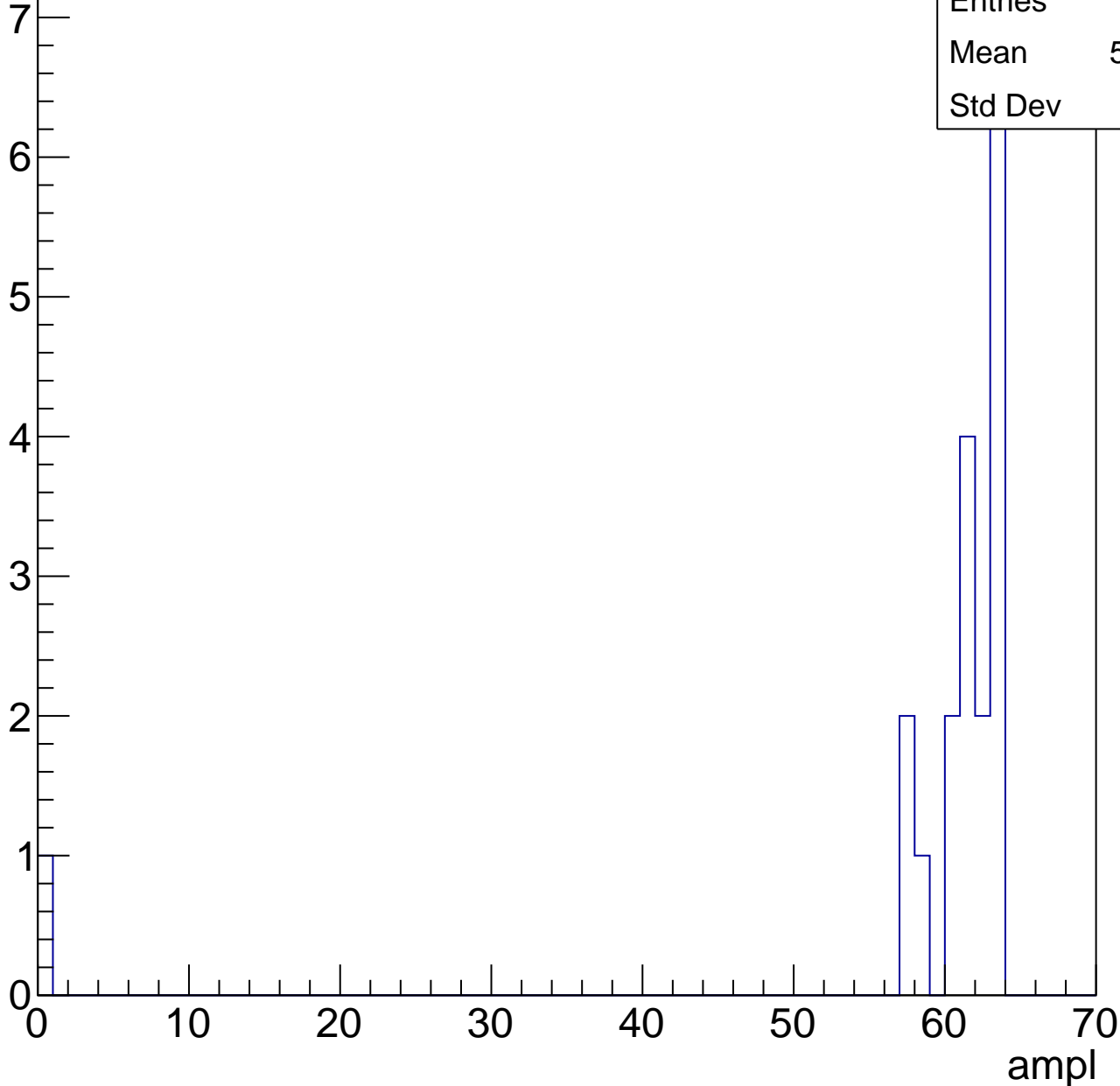
Entries	61
Mean	58.31
Std Dev	2.984

# B1L000S, U1-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	57.95
Std Dev	13.8

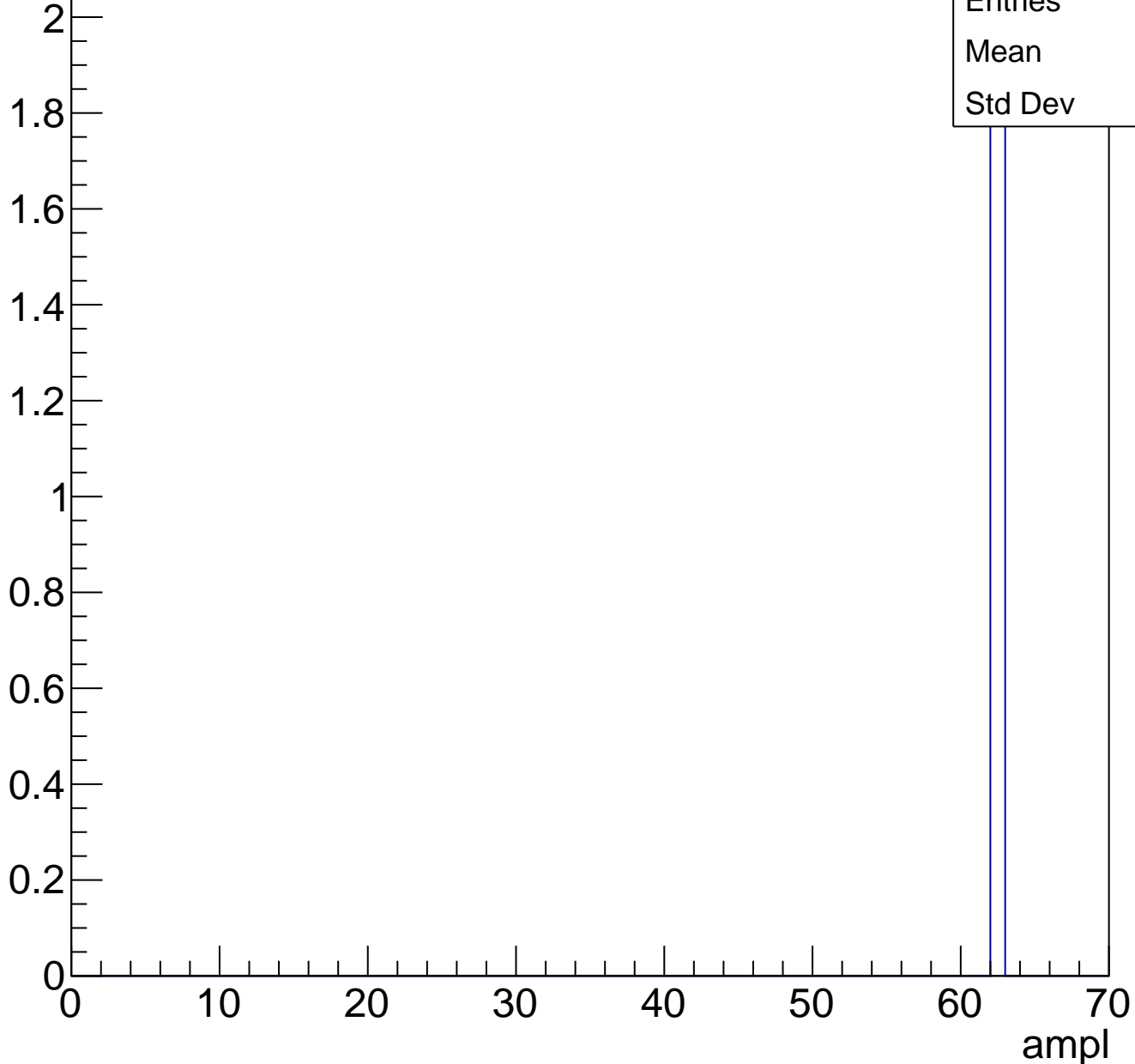




# B1L000S, U1-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch76, adc0

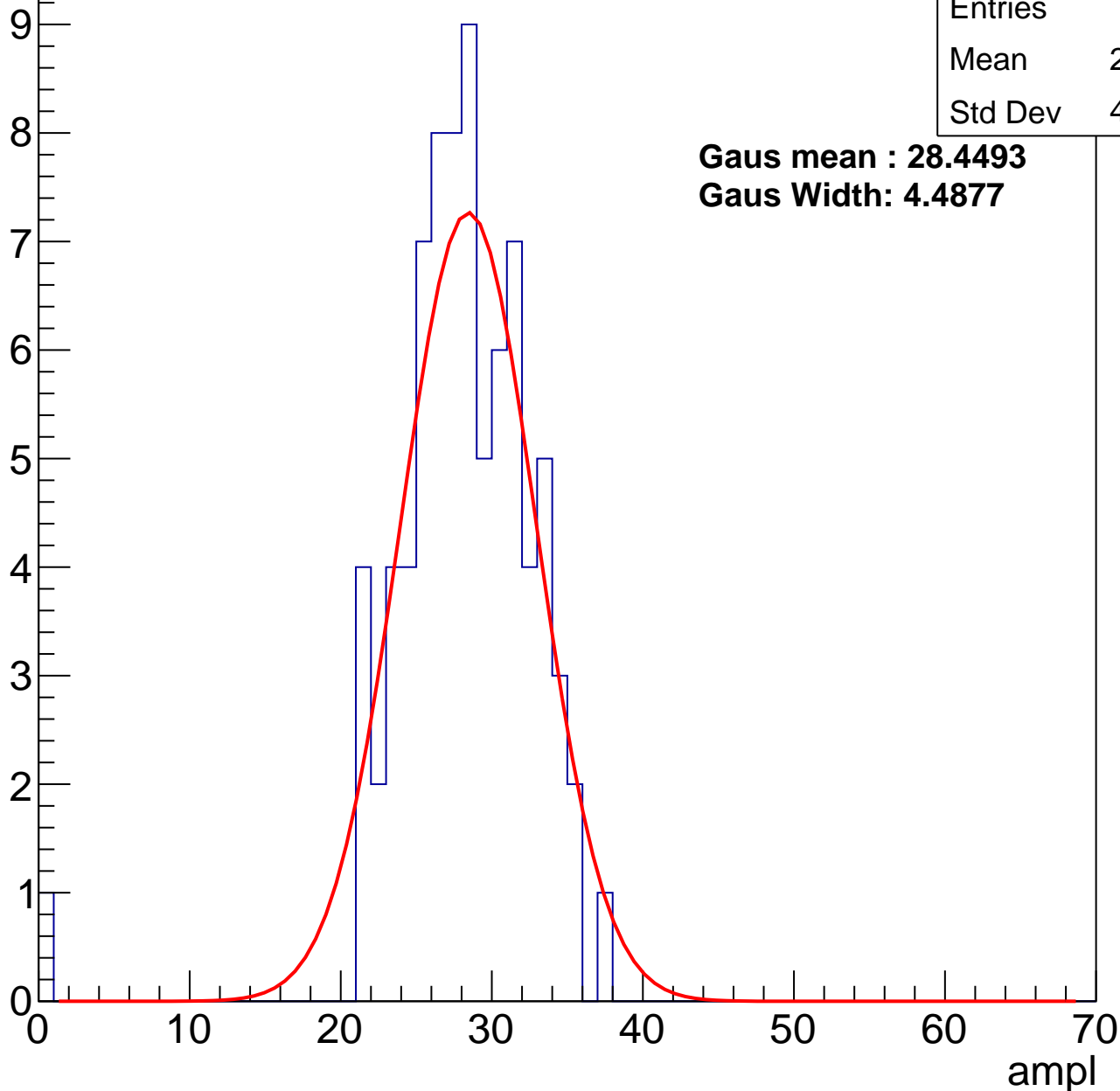
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	27.64
Std Dev	4.843

**Gaus mean : 28.4493**

**Gaus Width: 4.4877**



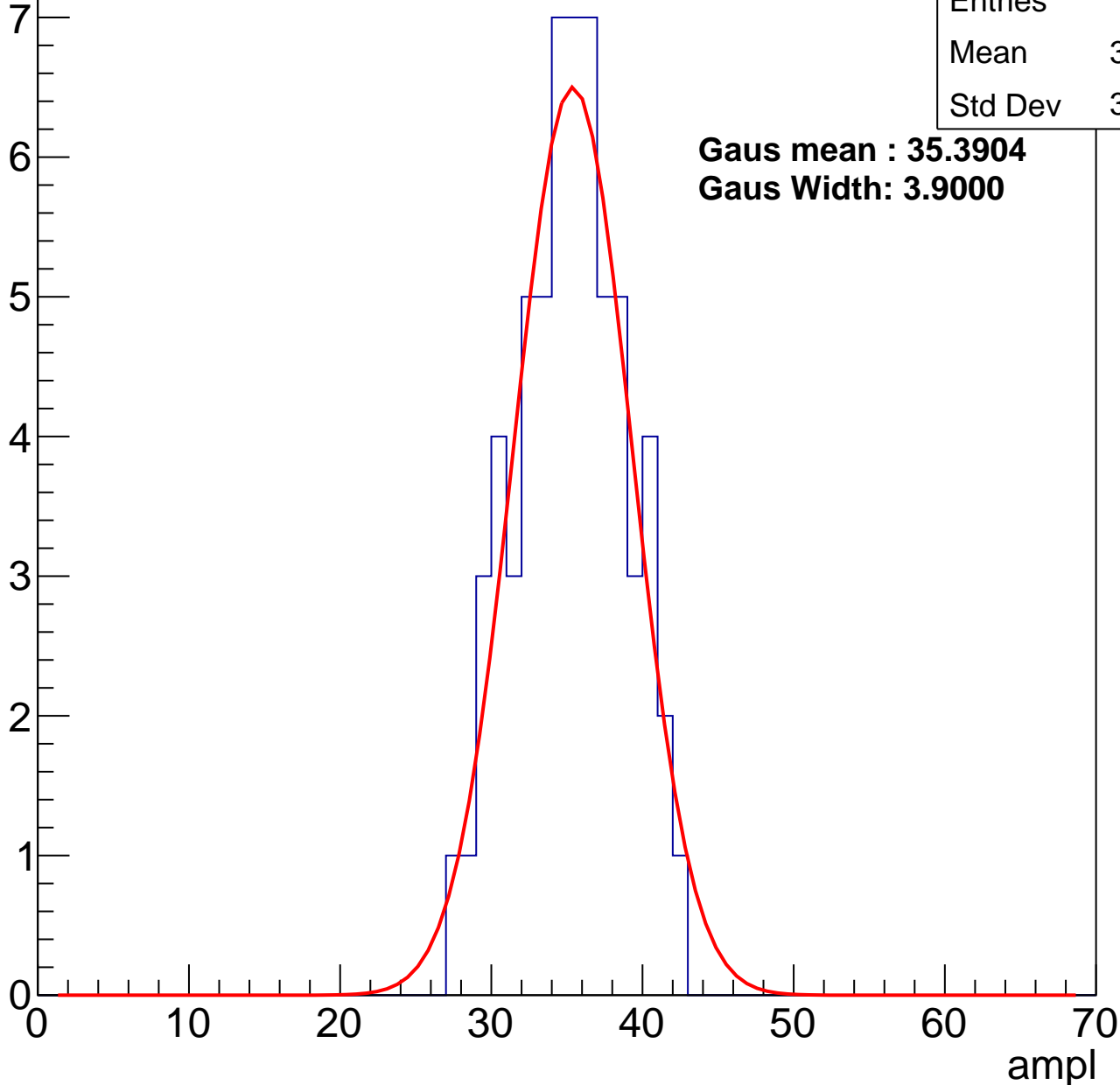
# B1L000S, U1-ch76, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	34.78
Std Dev	3.516

**Gaus mean : 35.3904**  
**Gaus Width: 3.9000**



# B1L000S, U1-ch76, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 40.12

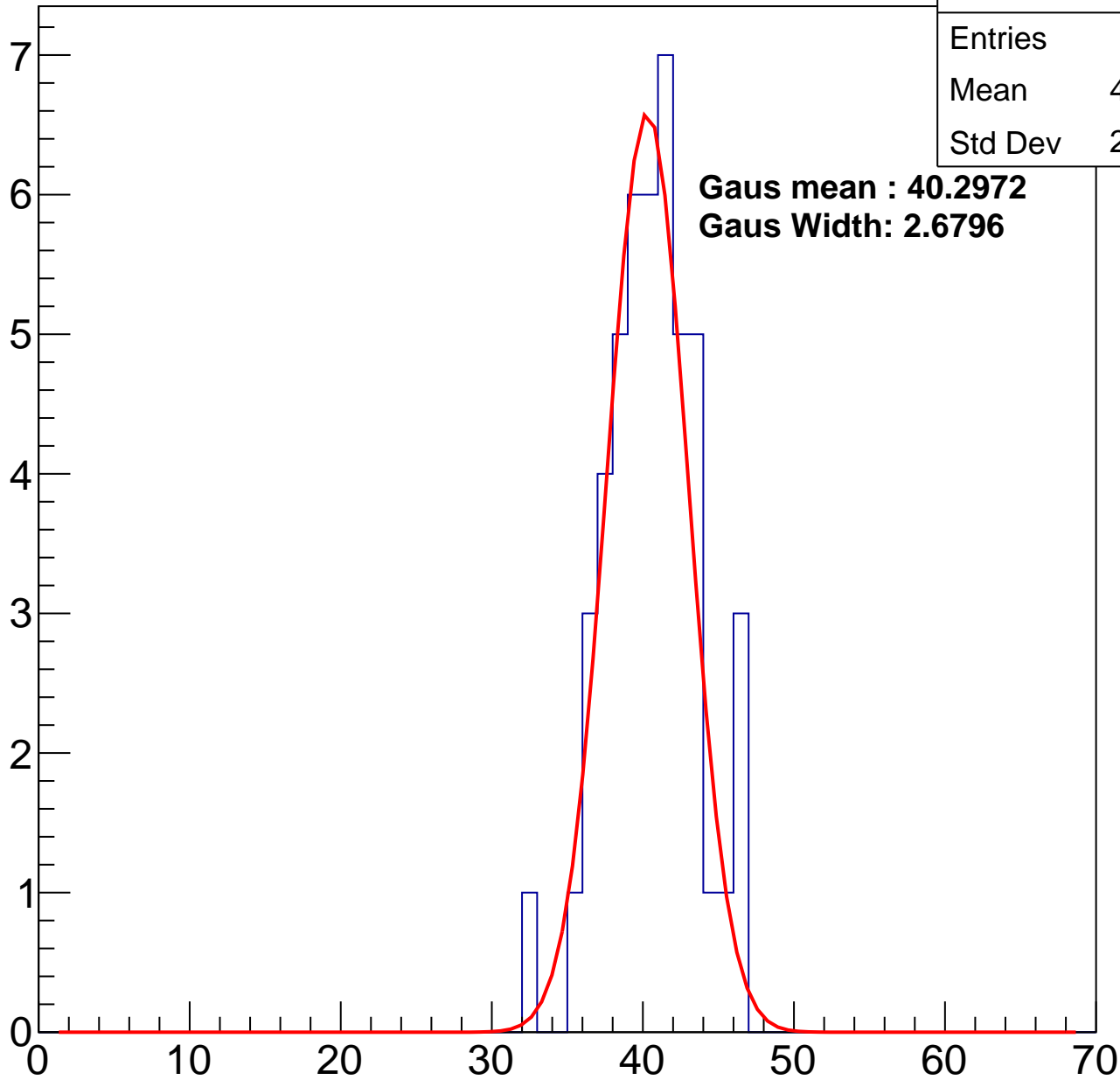
Std Dev 2.955

**Gaus mean : 40.2972**

**Gaus Width: 2.6796**

0 10 20 30 40 50 60 70

ampl

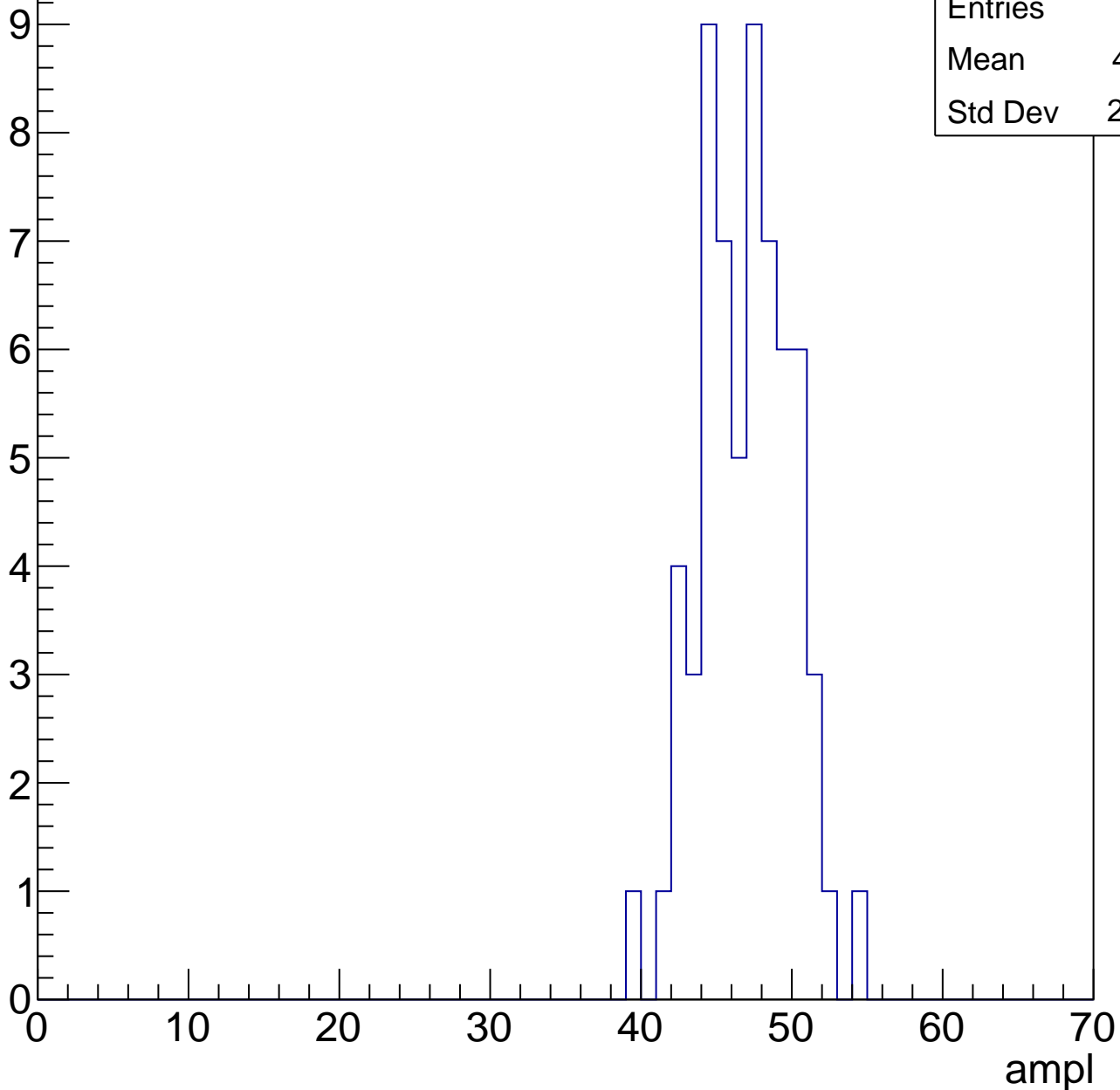


# B1L000S, U1-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

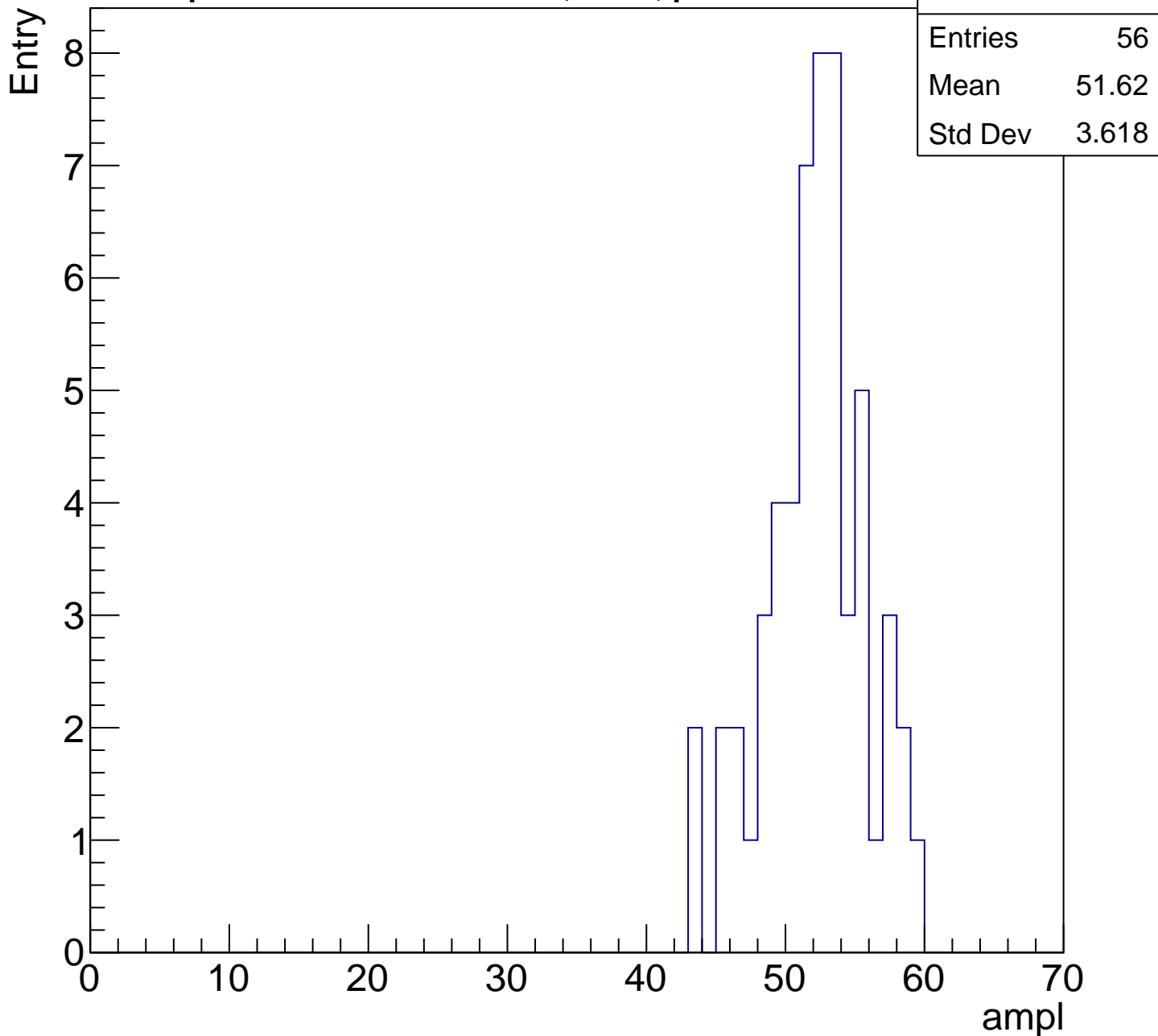
Entry

Entries	63
Mean	46.51
Std Dev	2.965



# B1L000S, U1-ch76, adc4

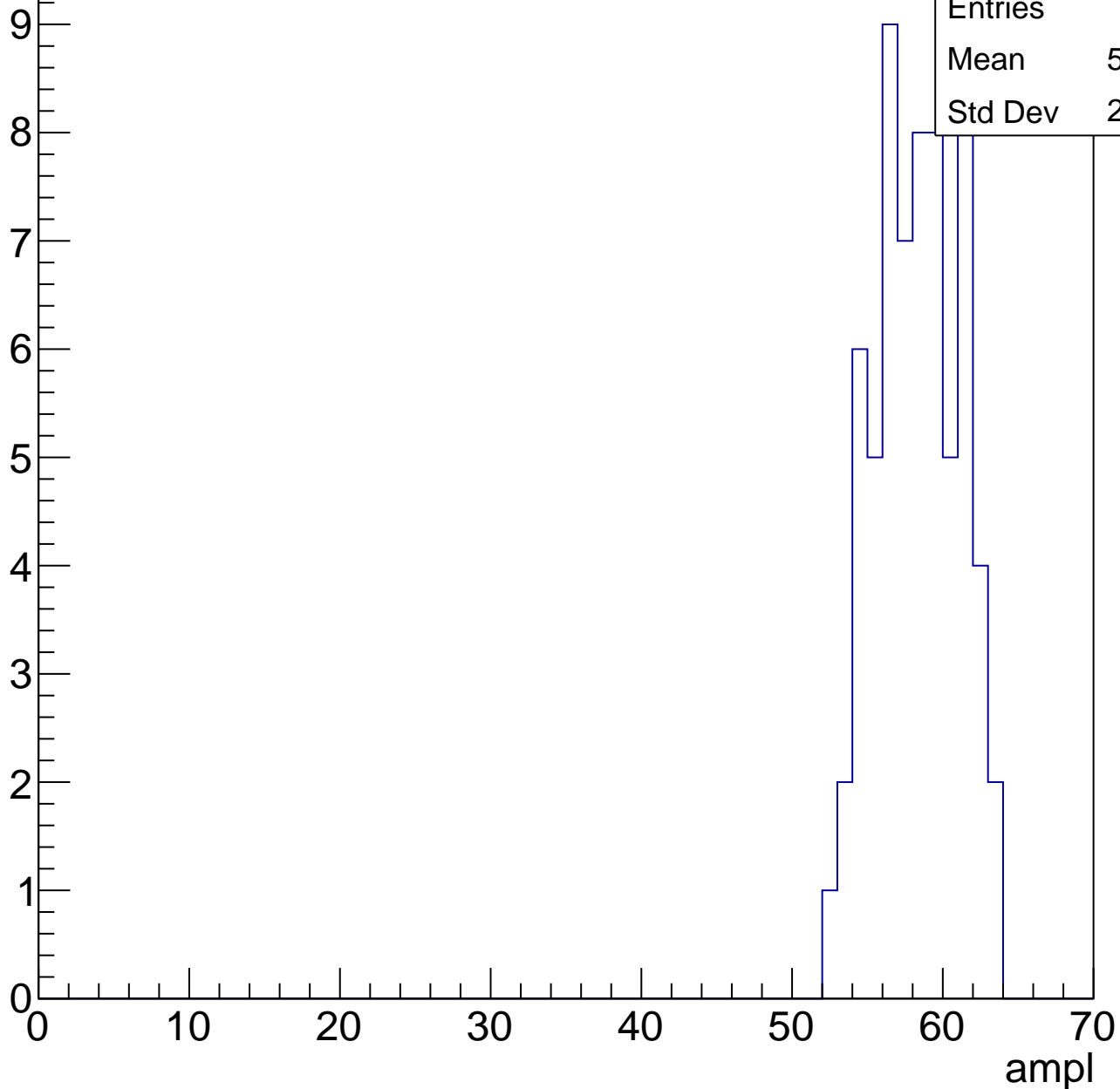
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

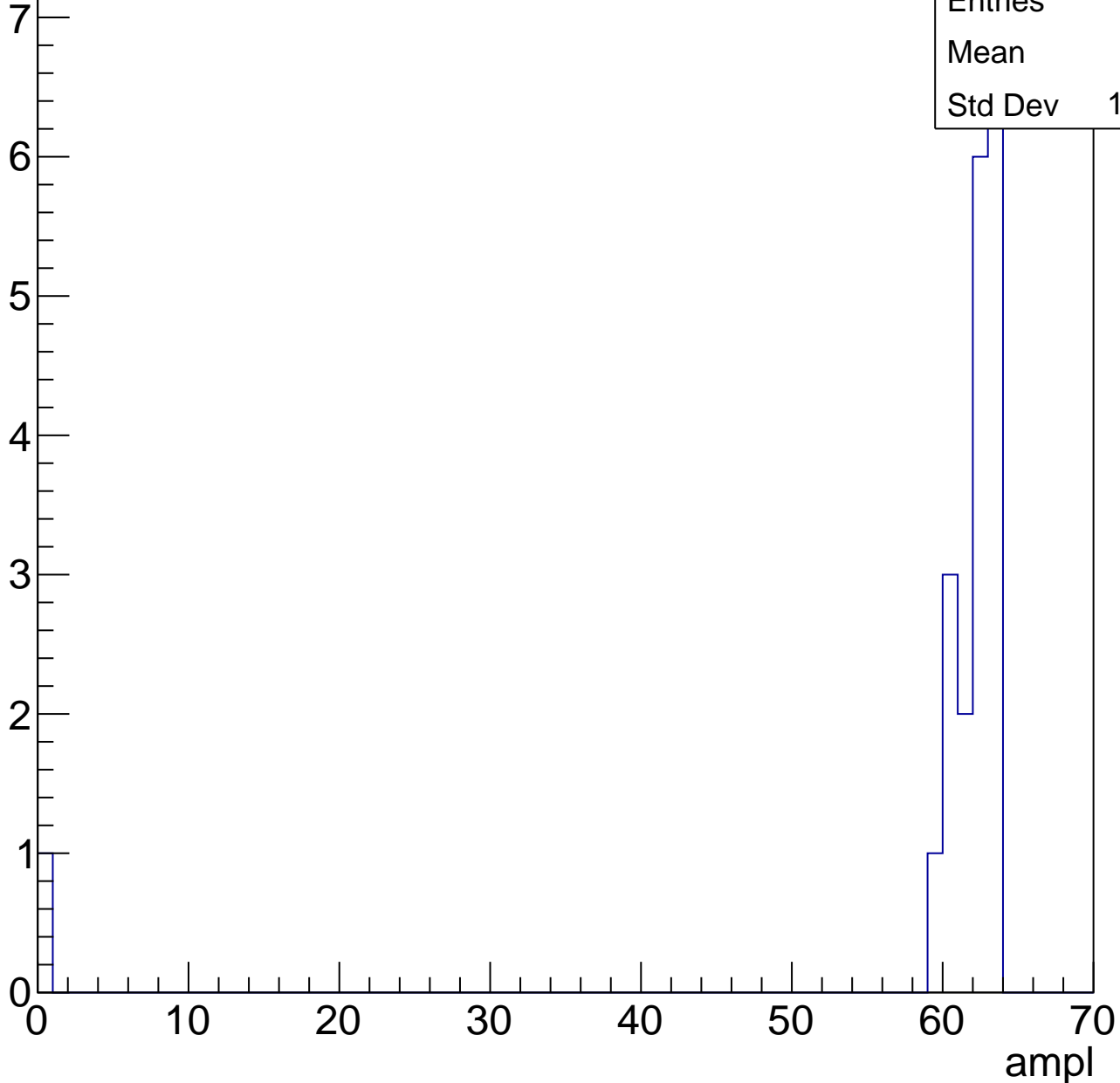


# B1L000S, U1-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.7
Std Dev	13.52





# B1L000S, U1-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

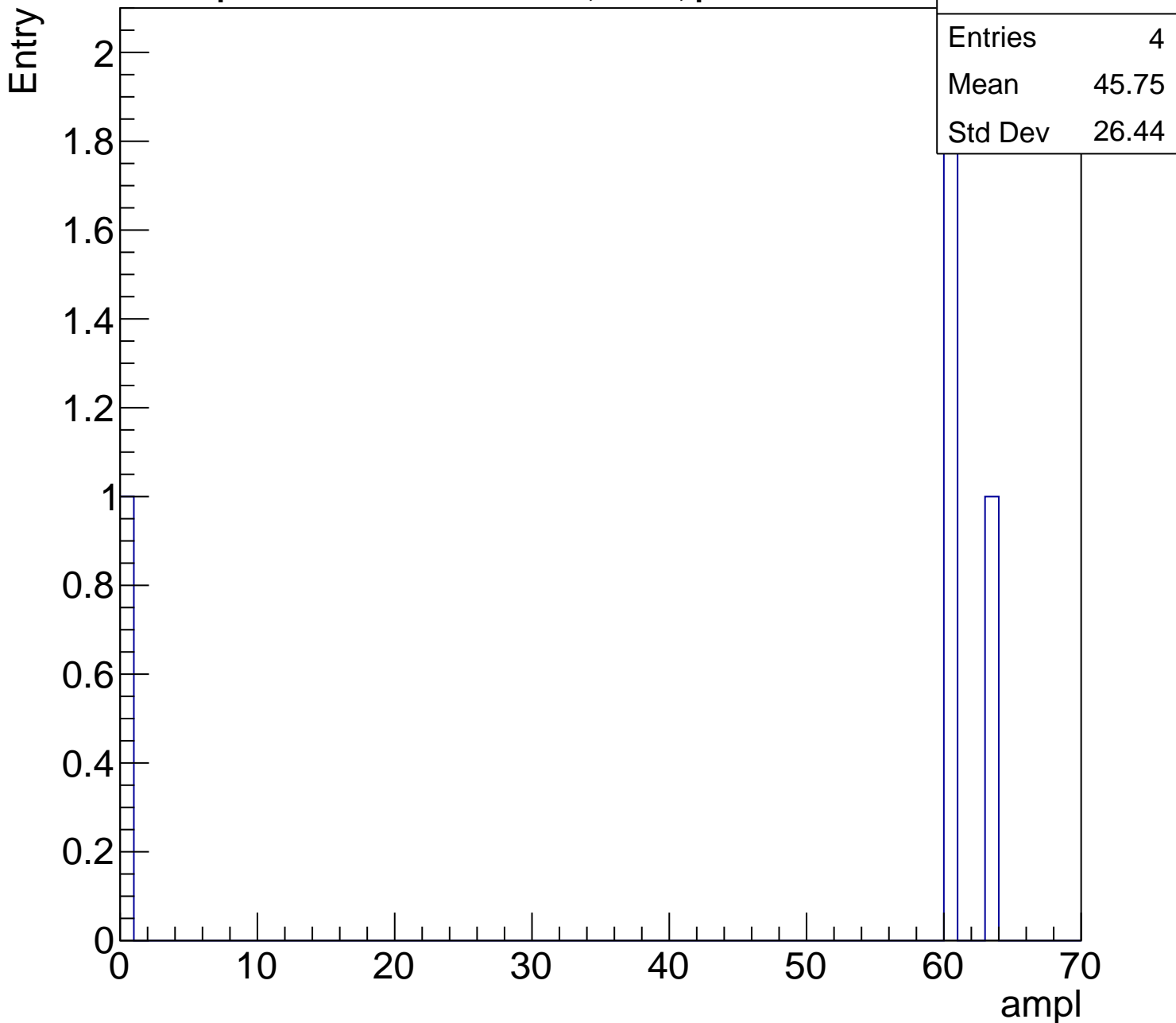
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	45.75
Std Dev	26.44

0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch77, adc0

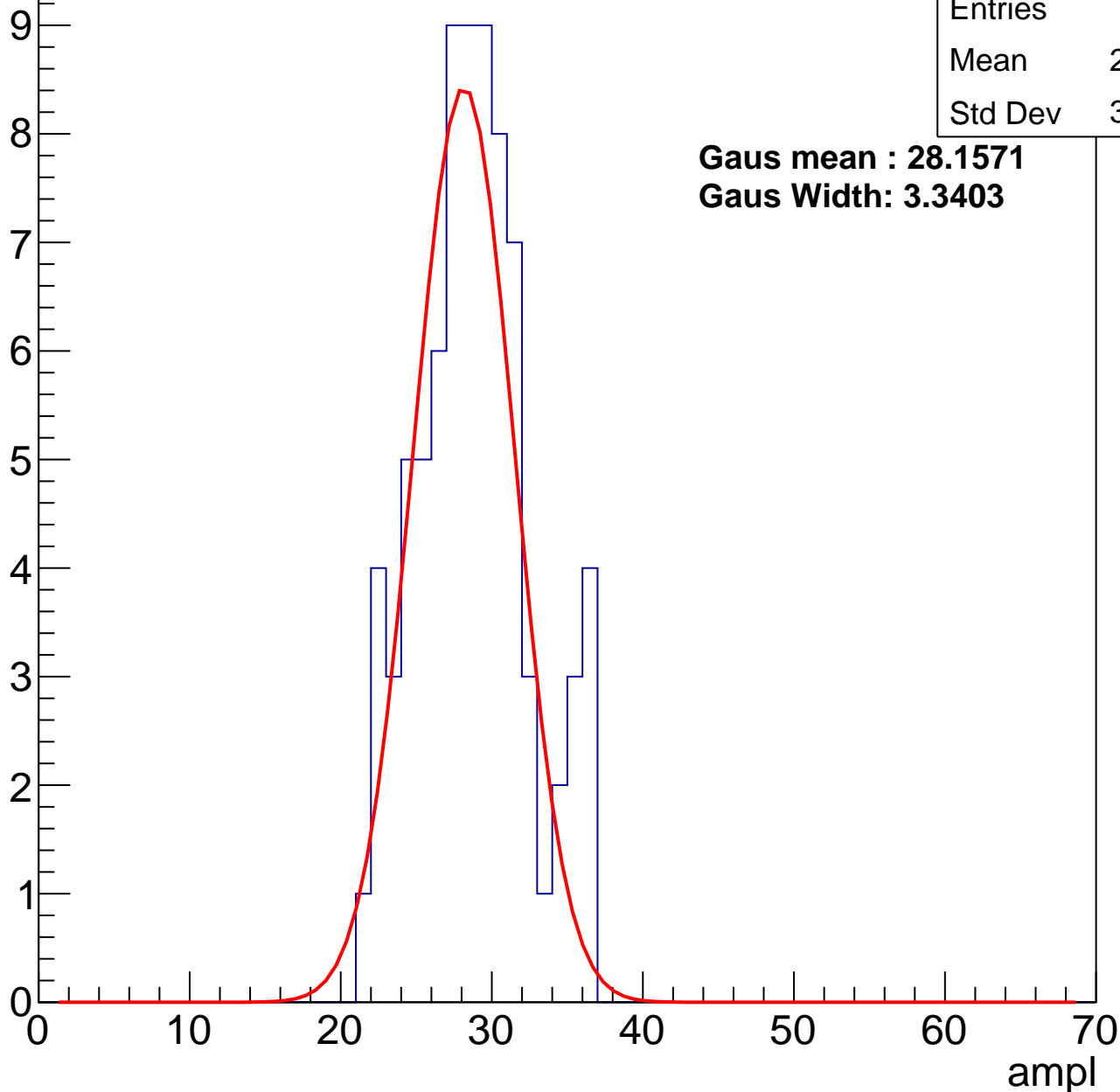
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	28.33
Std Dev	3.679

**Gaus mean : 28.1571**

**Gaus Width: 3.3403**



# B1L000S, U1-ch77, adc1

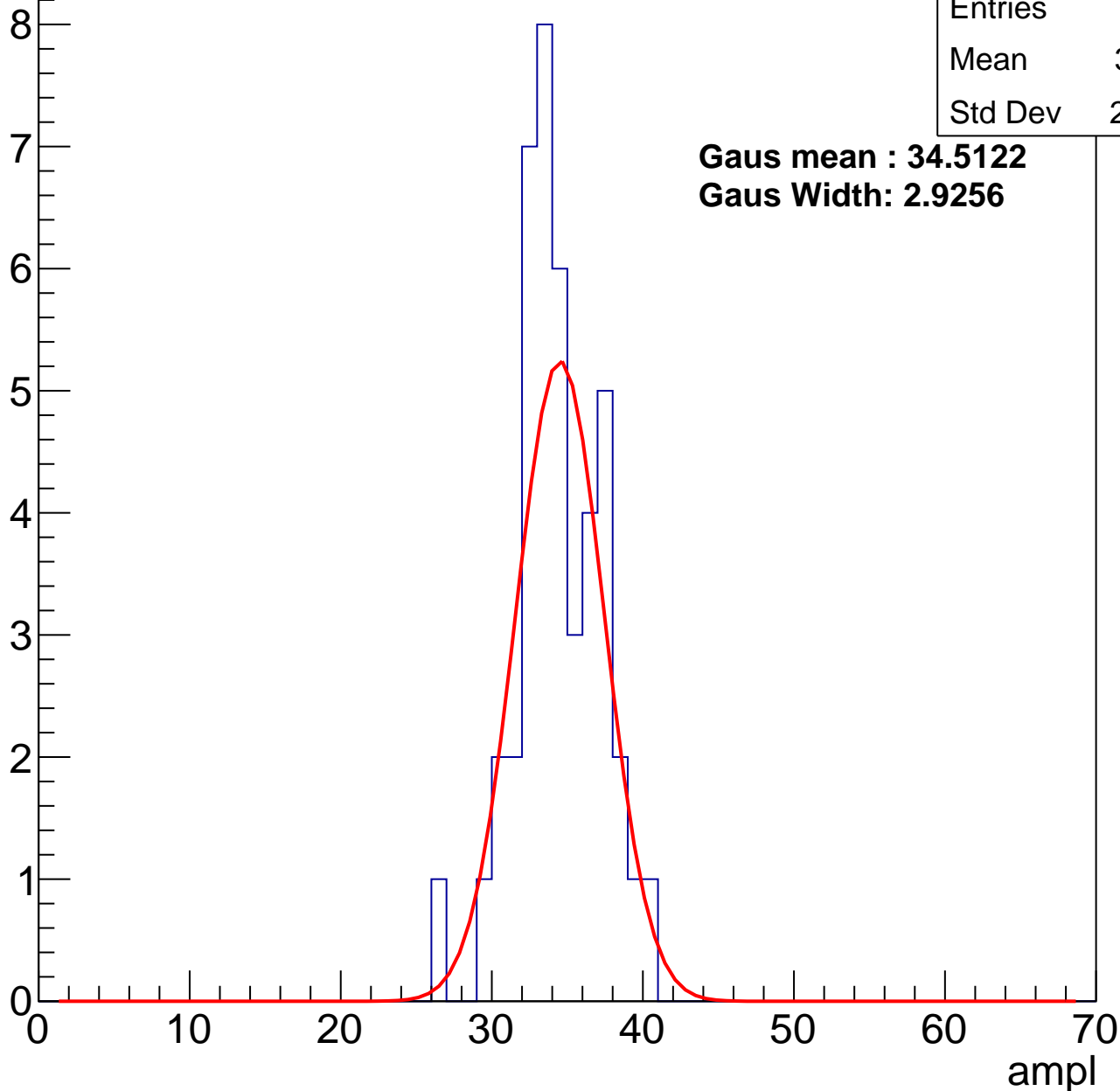
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	33.91
Std Dev	2.777

**Gaus mean : 34.5122**

**Gaus Width: 2.9256**



# B1L000S, U1-ch77, adc2

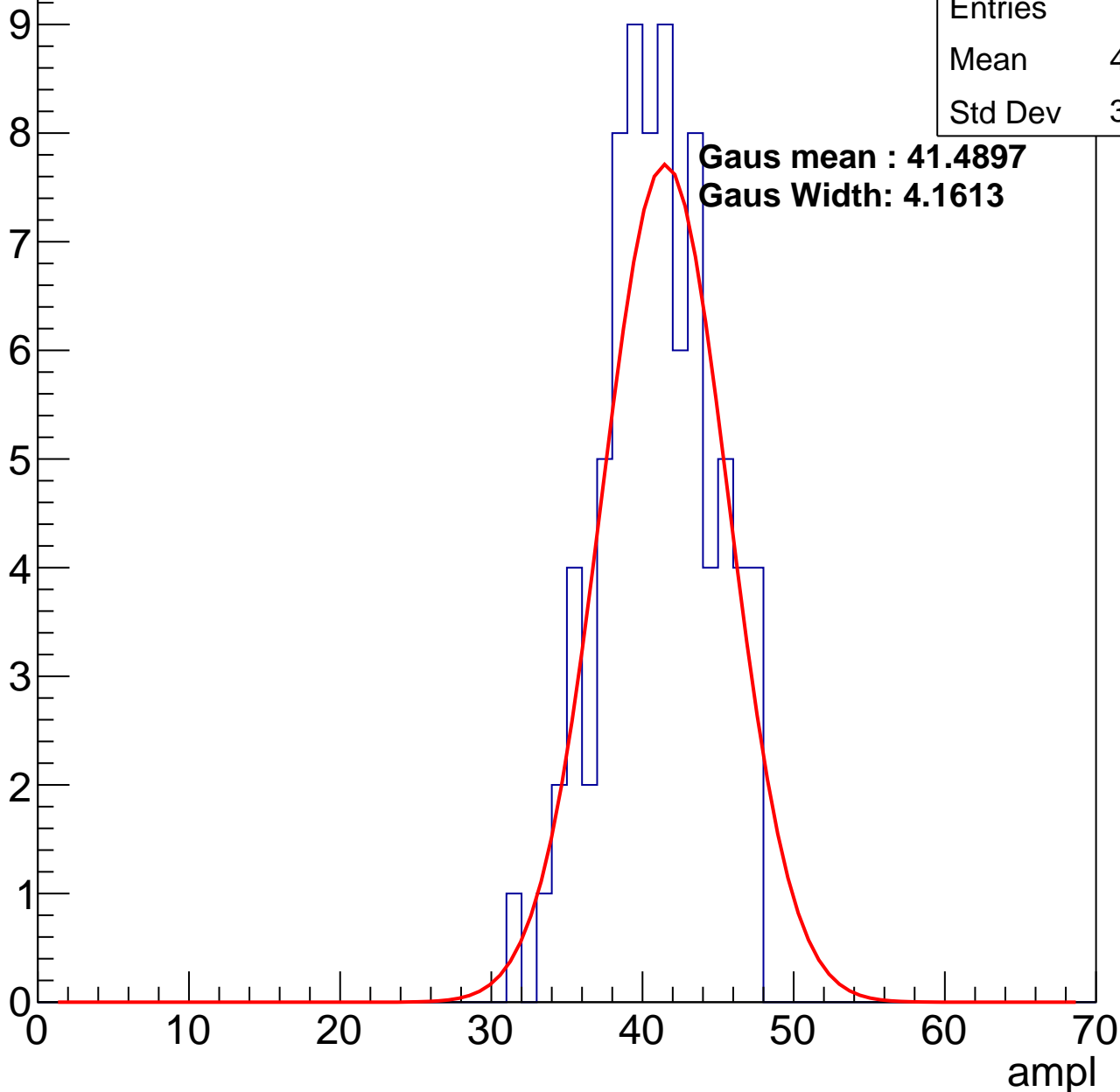
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	40.52
Std Dev	3.592

**Gaus mean : 41.4897**

**Gaus Width: 4.1613**

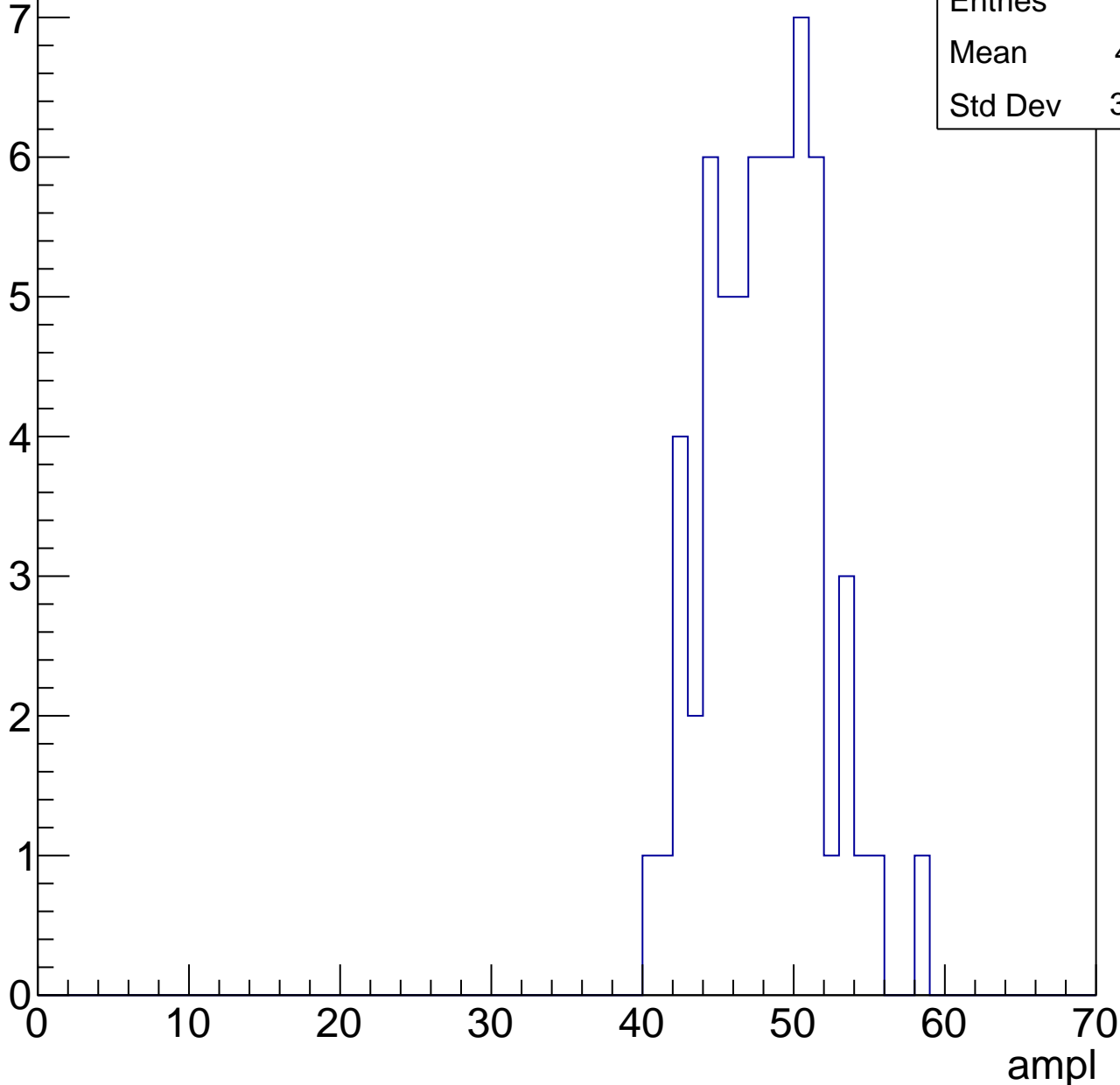


# B1L000S, U1-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	47.61
Std Dev	3.656

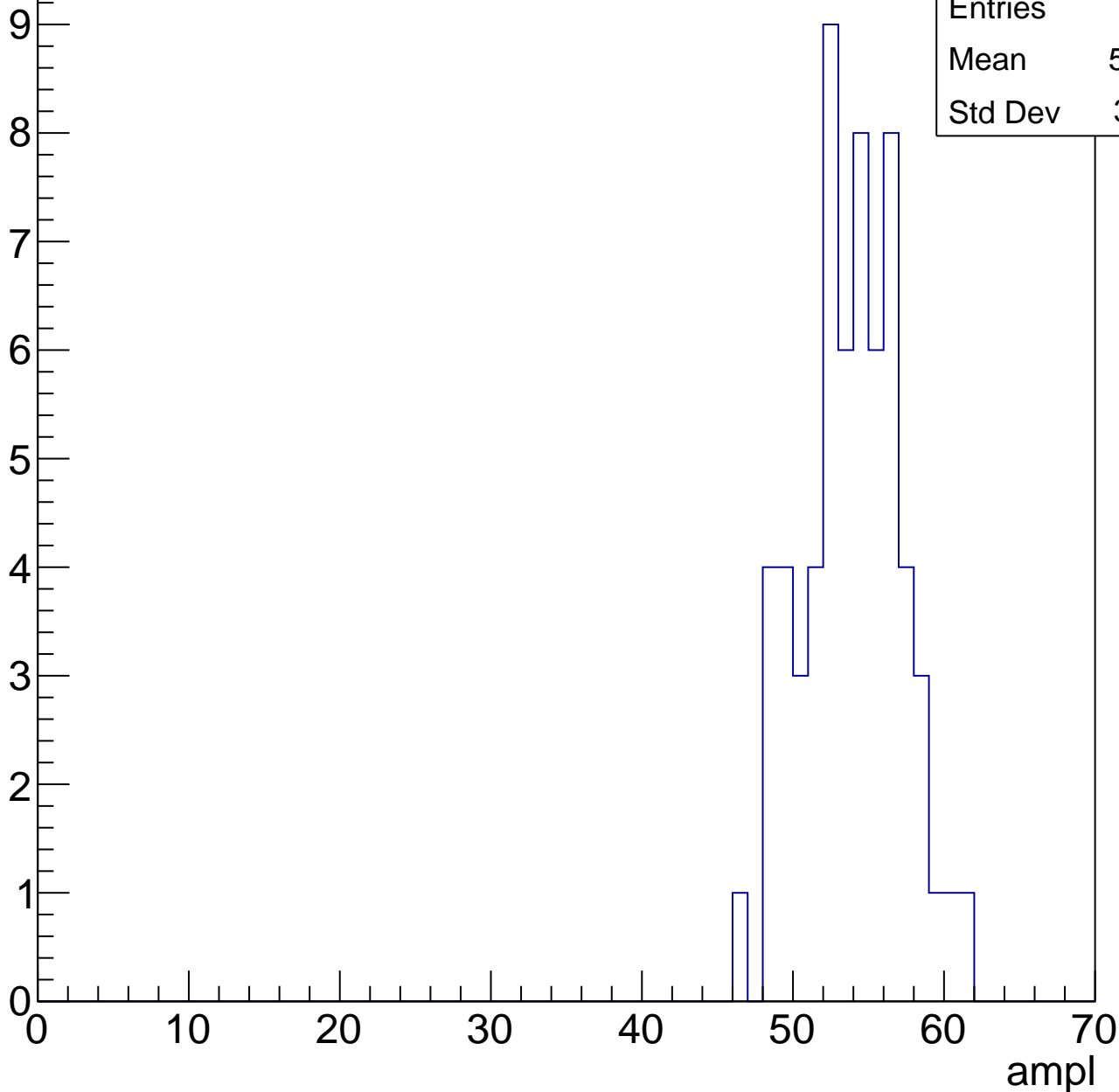


# B1L000S, U1-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

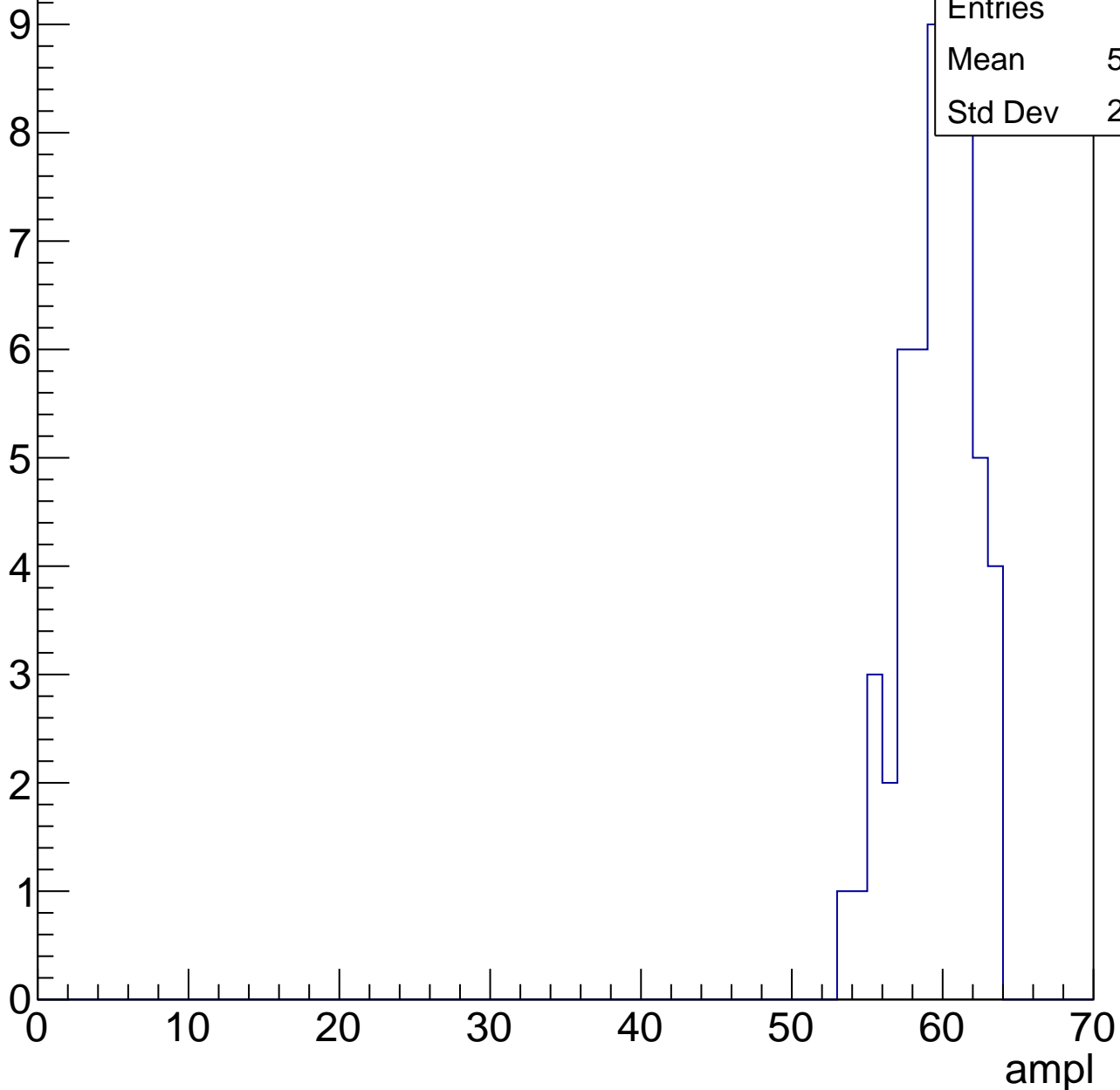
Entries	63
Mean	53.43
Std Dev	3.201



# B1L000S, U1-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

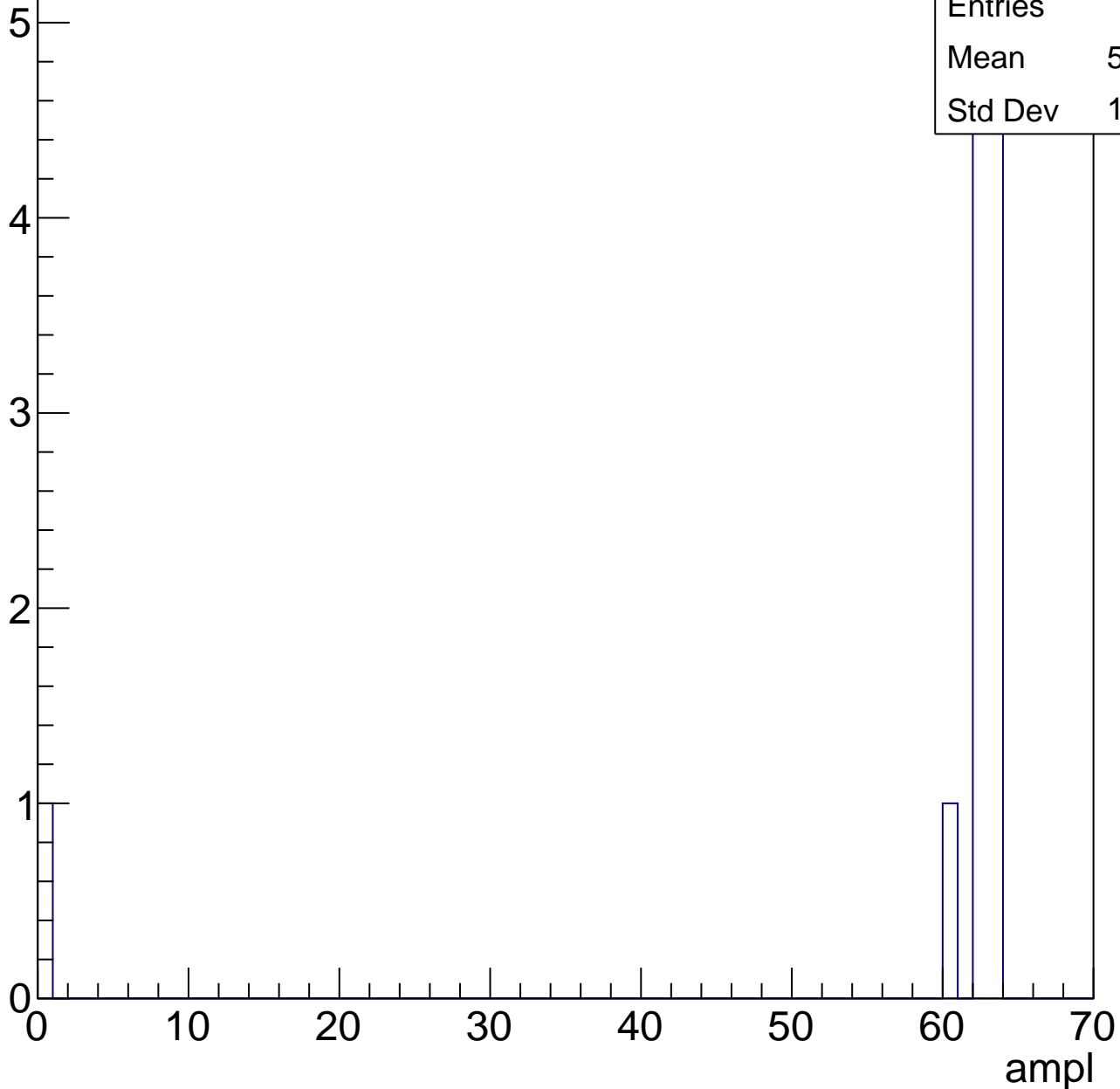


# B1L000S, U1-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	57.08
Std Dev	17.23





# B1L000S, U1-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch78, adc0

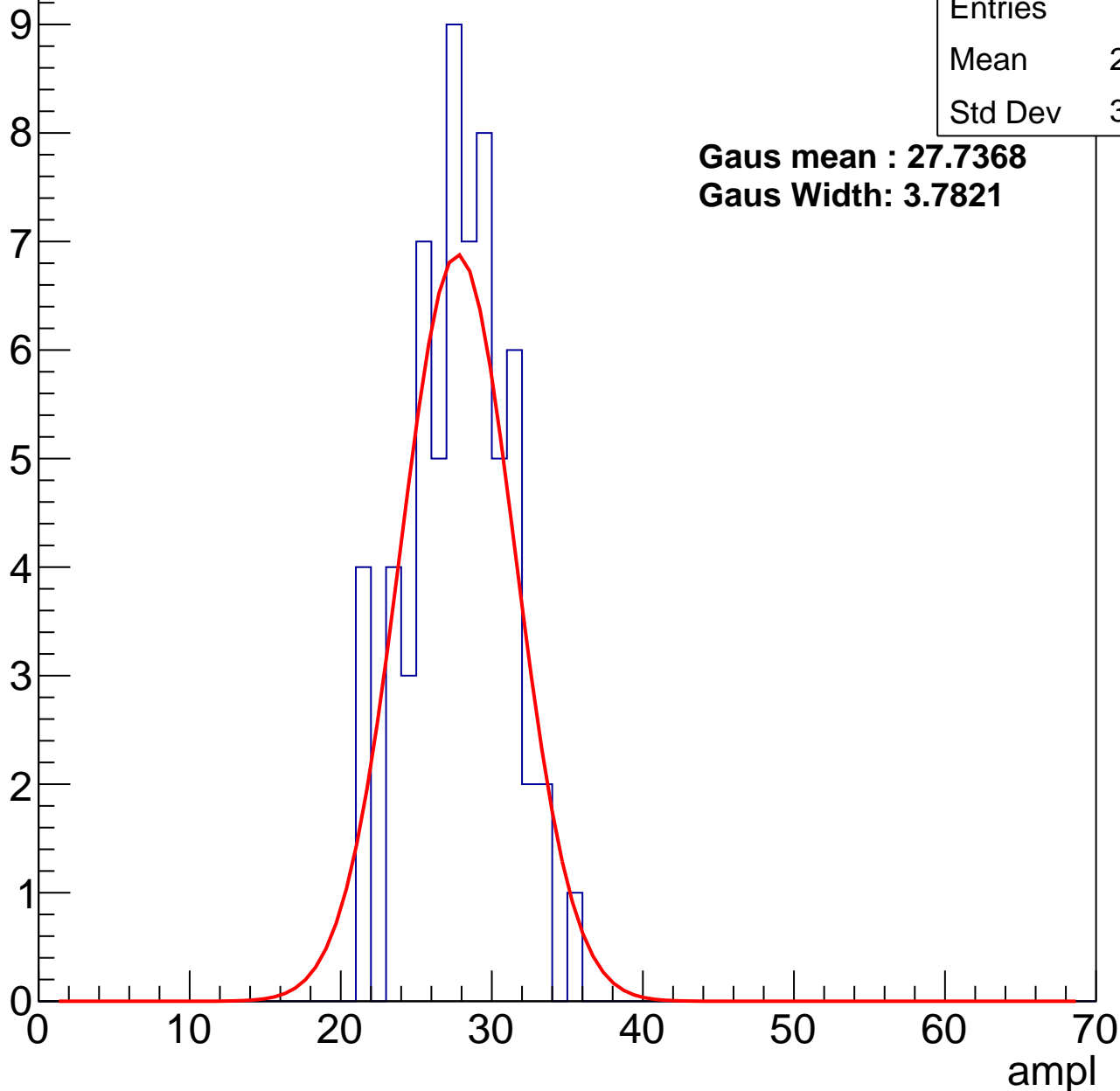
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	27.38
Std Dev	3.149

**Gaus mean : 27.7368**

**Gaus Width: 3.7821**



# B1L000S, U1-ch78, adc1

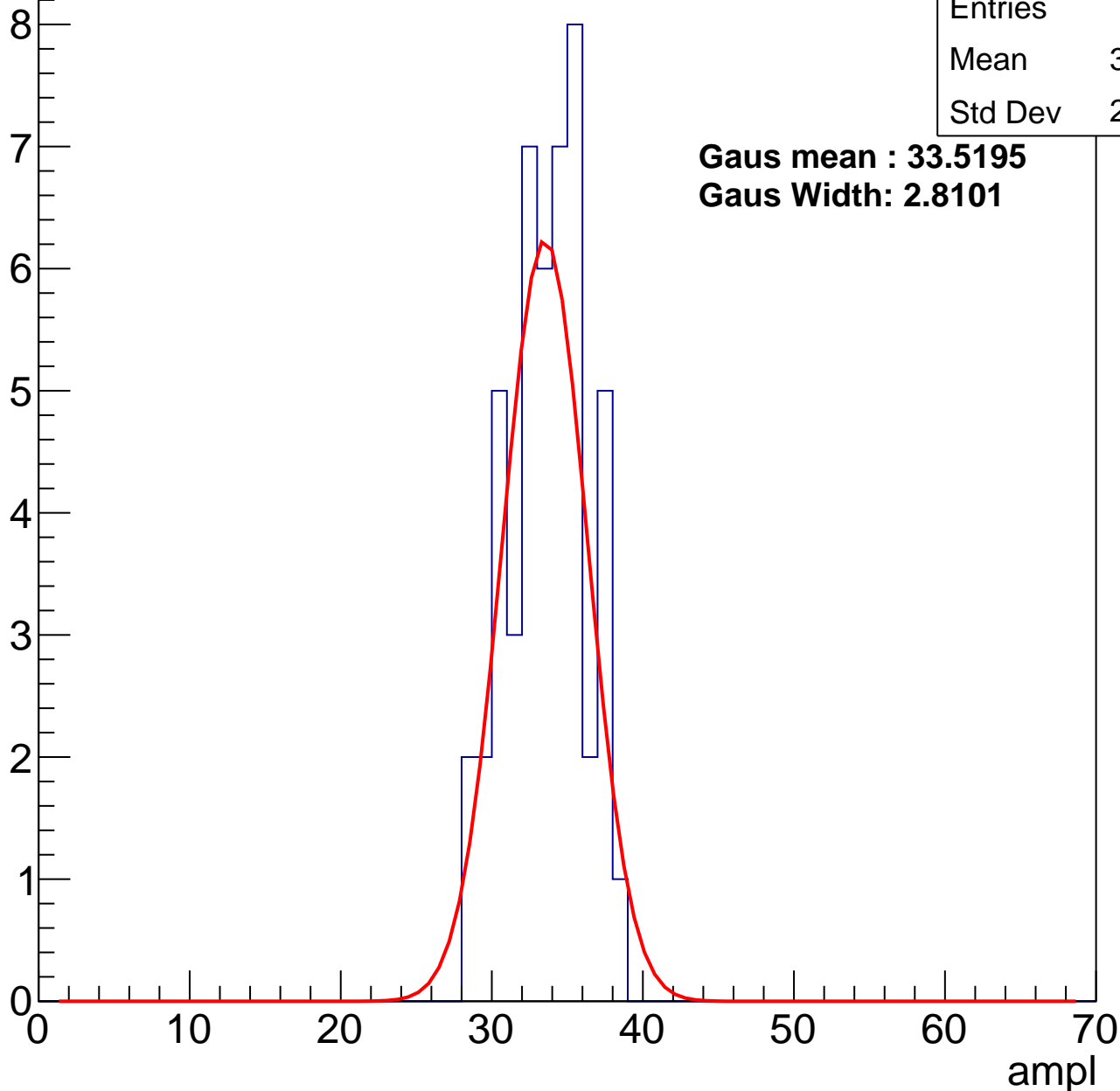
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	33.17
Std Dev	2.528

**Gaus mean : 33.5195**

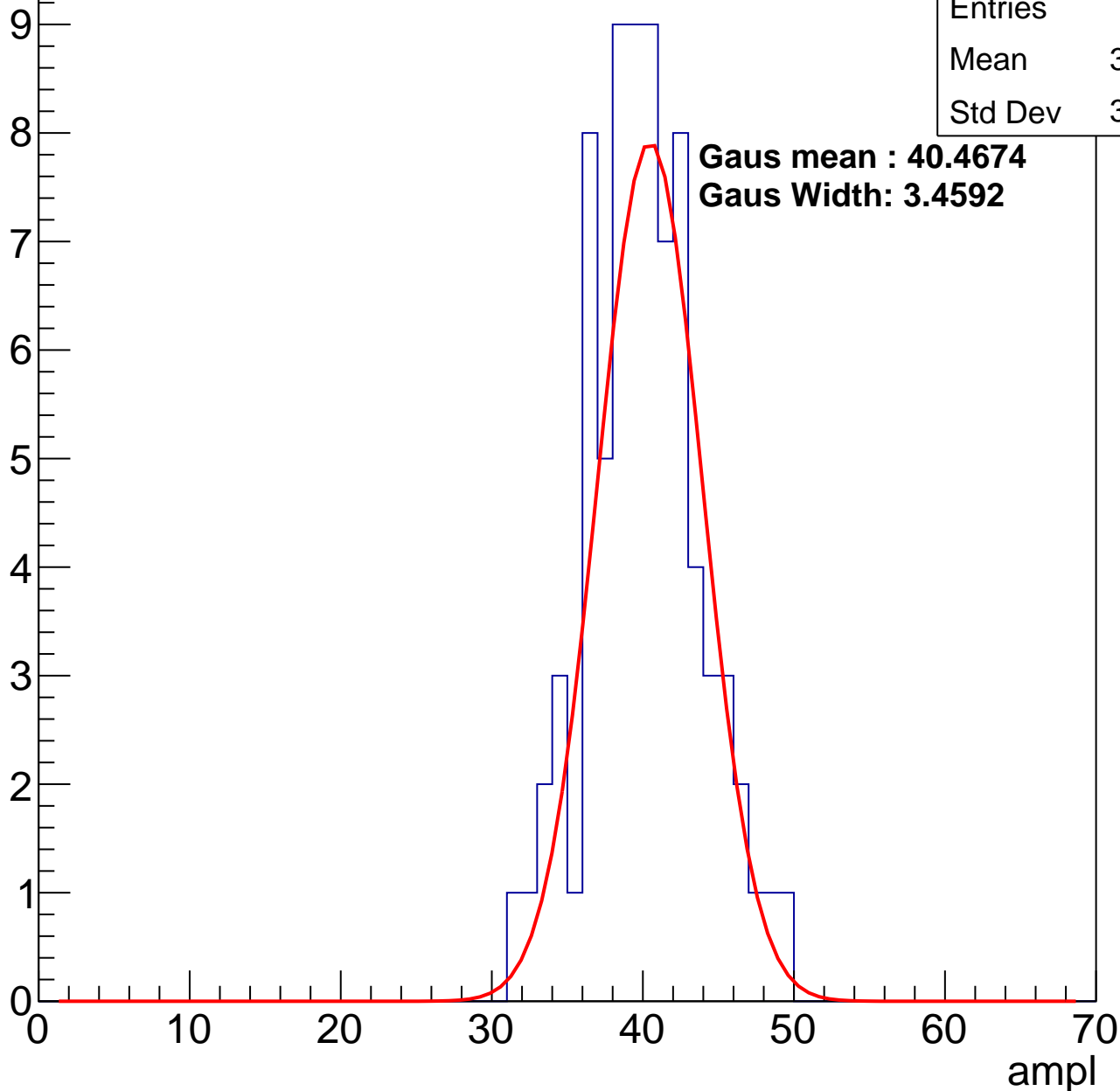
**Gaus Width: 2.8101**



# B1L000S, U1-ch78, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

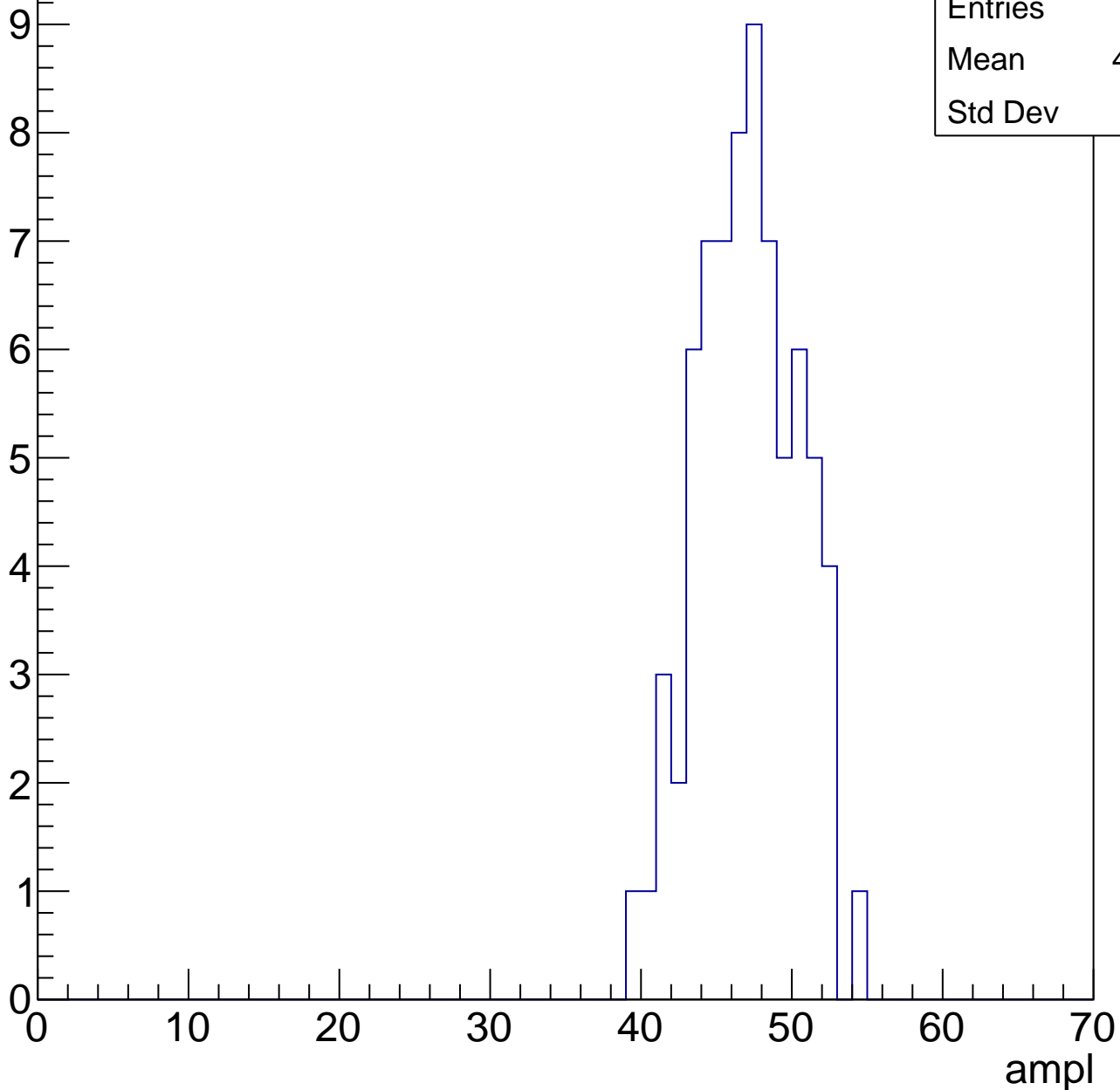
Entry



# B1L000S, U1-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



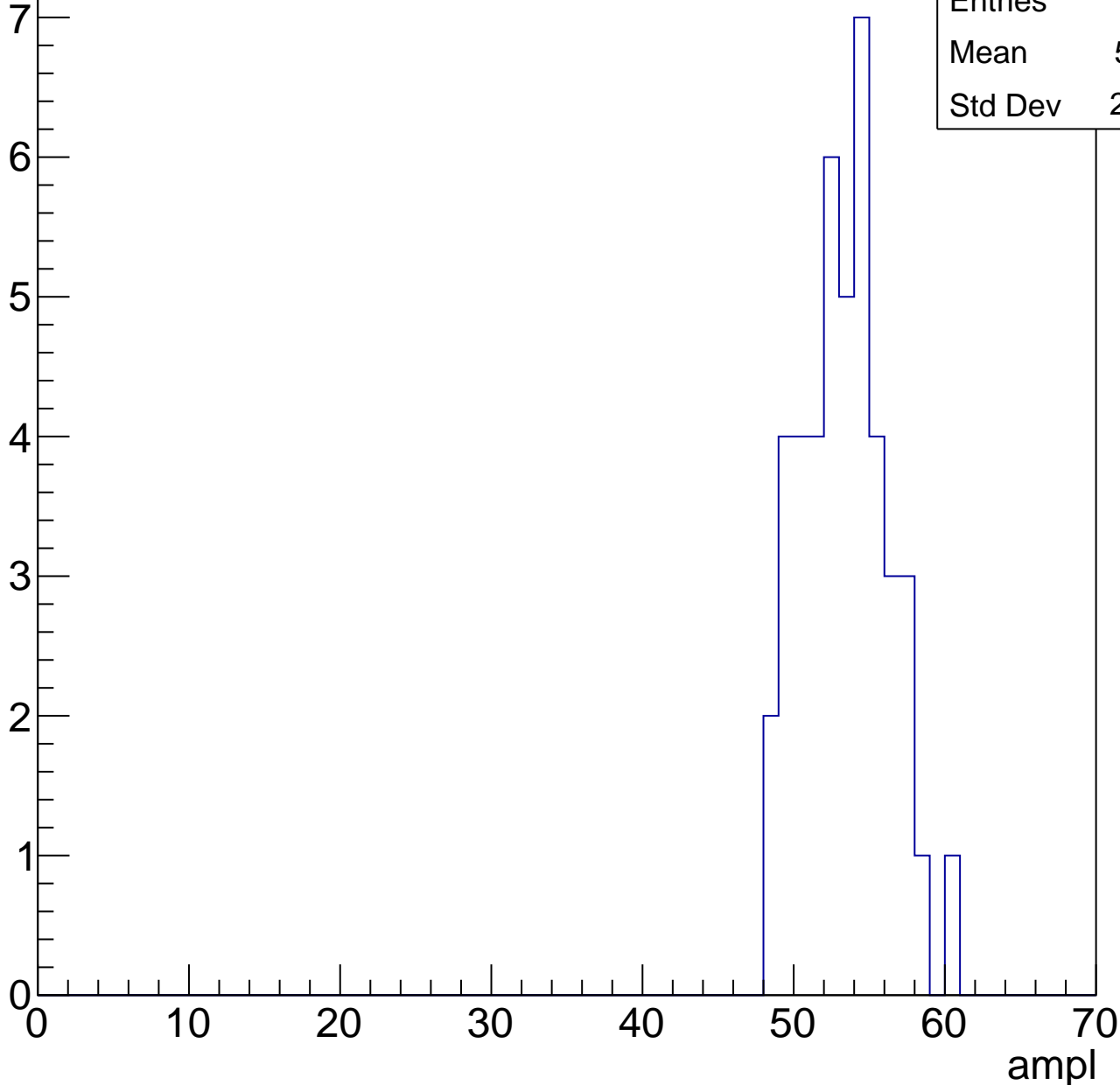
Entries	72
Mean	46.61
Std Dev	3.26

# B1L000S, U1-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	52.91
Std Dev	2.795

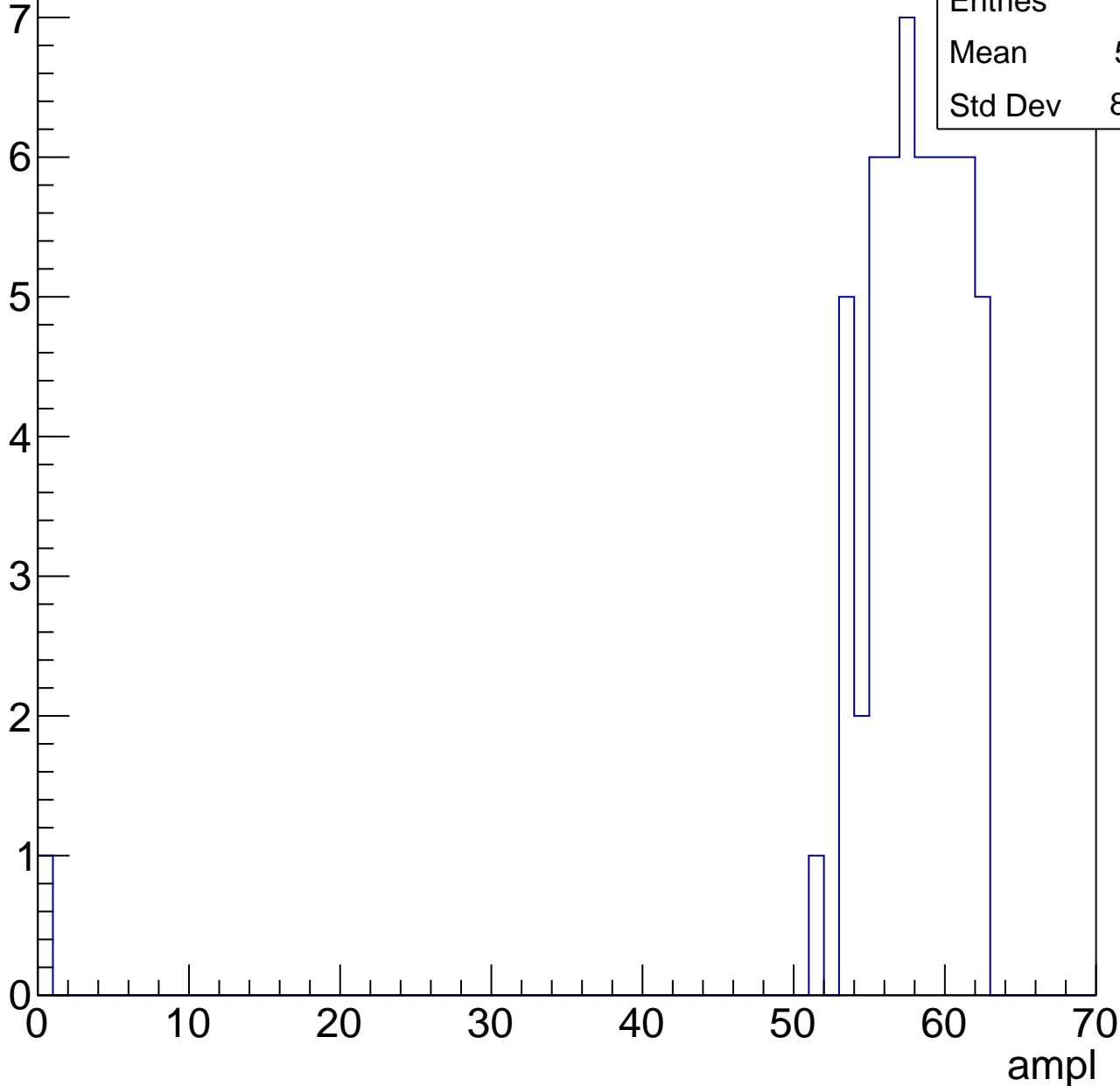


# B1L000S, U1-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	56.61
Std Dev	8.067

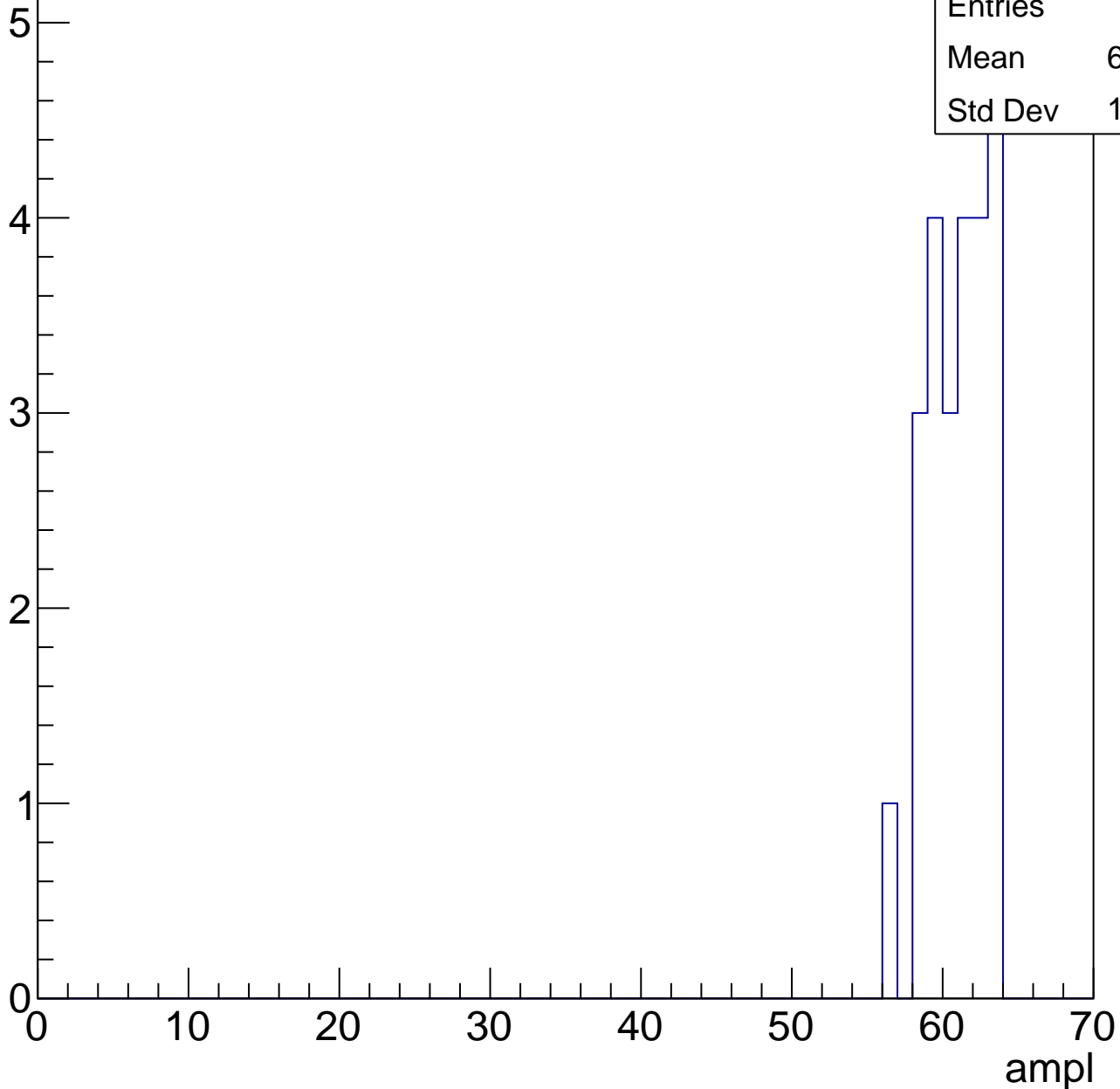


# B1L000S, U1-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	60.54
Std Dev	1.936



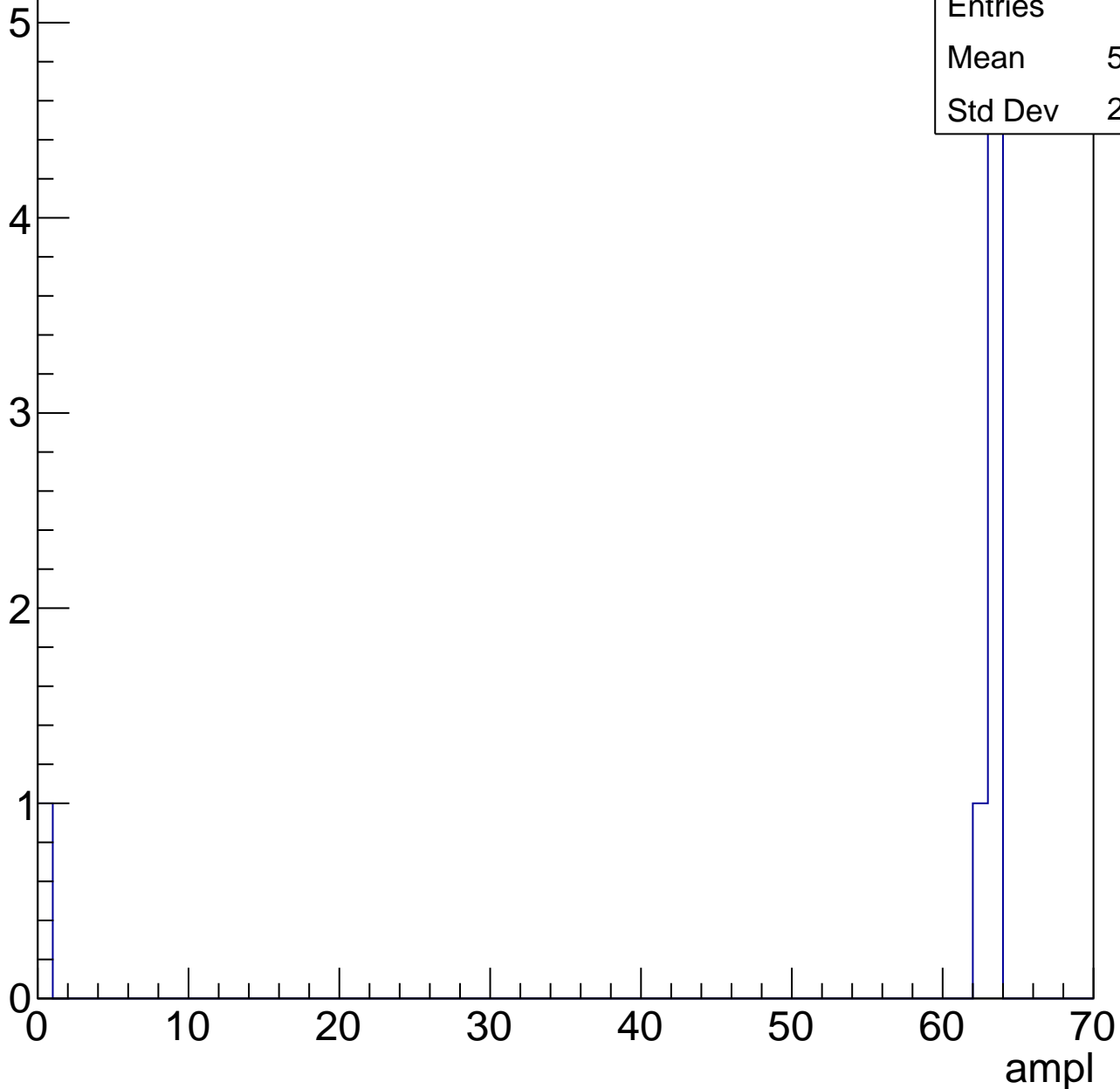


# B1L000S, U1-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	7
Mean	53.86
Std Dev	21.99



# B1L000S, U1-ch79, adc0

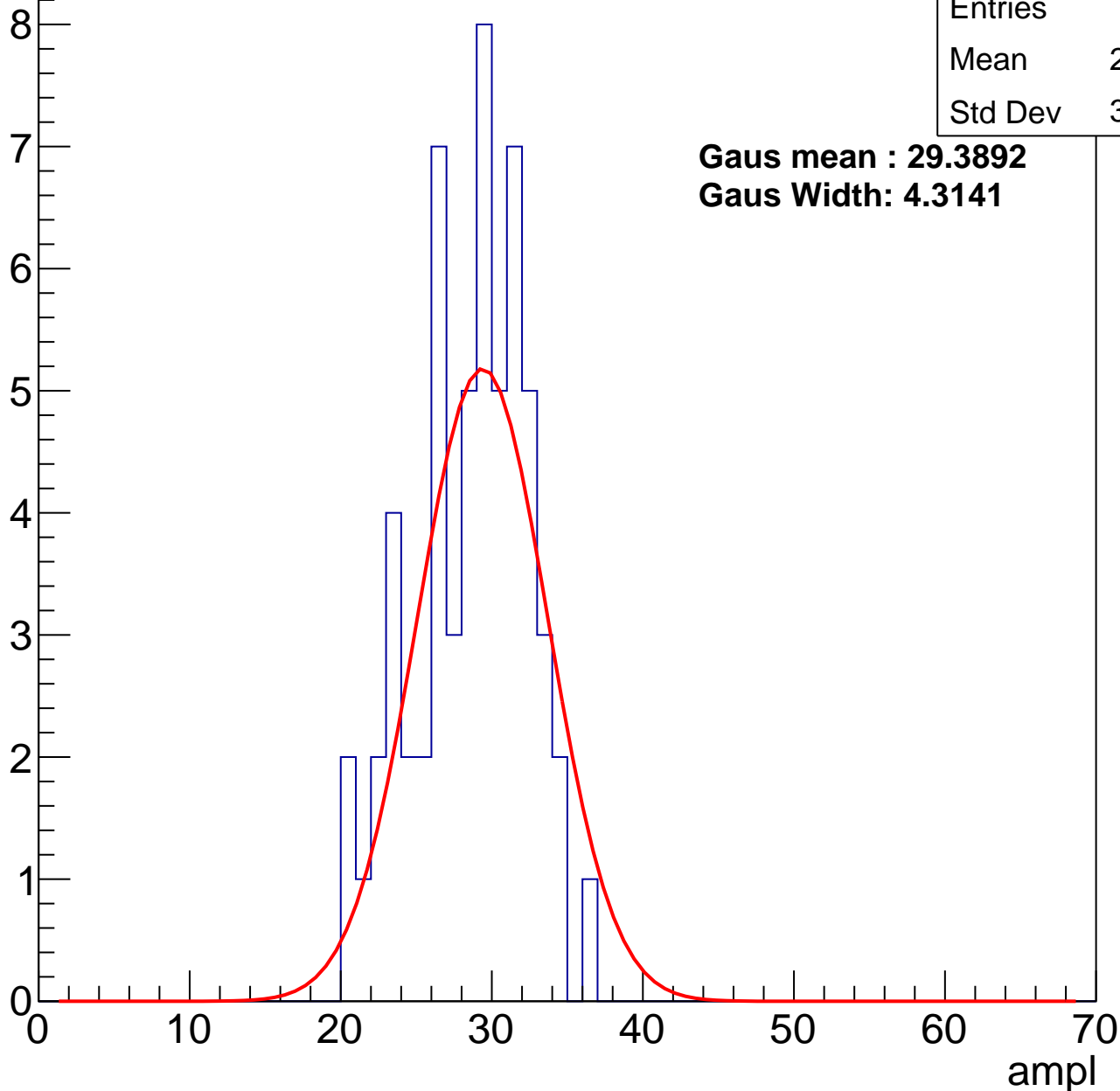
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	28.14
Std Dev	3.707

**Gaus mean : 29.3892**

**Gaus Width: 4.3141**



# B1L000S, U1-ch79, adc1

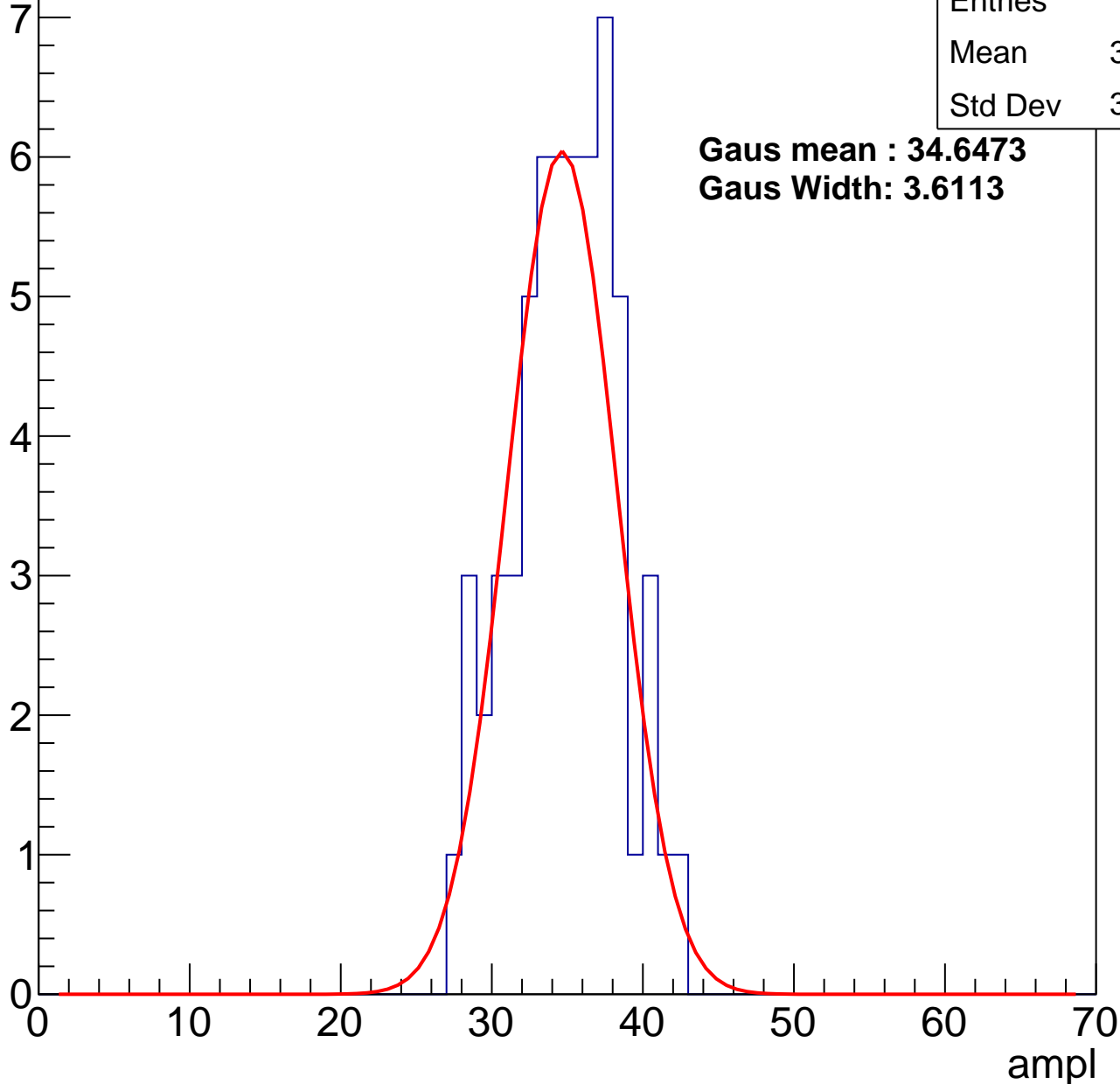
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	34.42
Std Dev	3.485

**Gaus mean : 34.6473**

**Gaus Width: 3.6113**



# B1L000S, U1-ch79, adc2

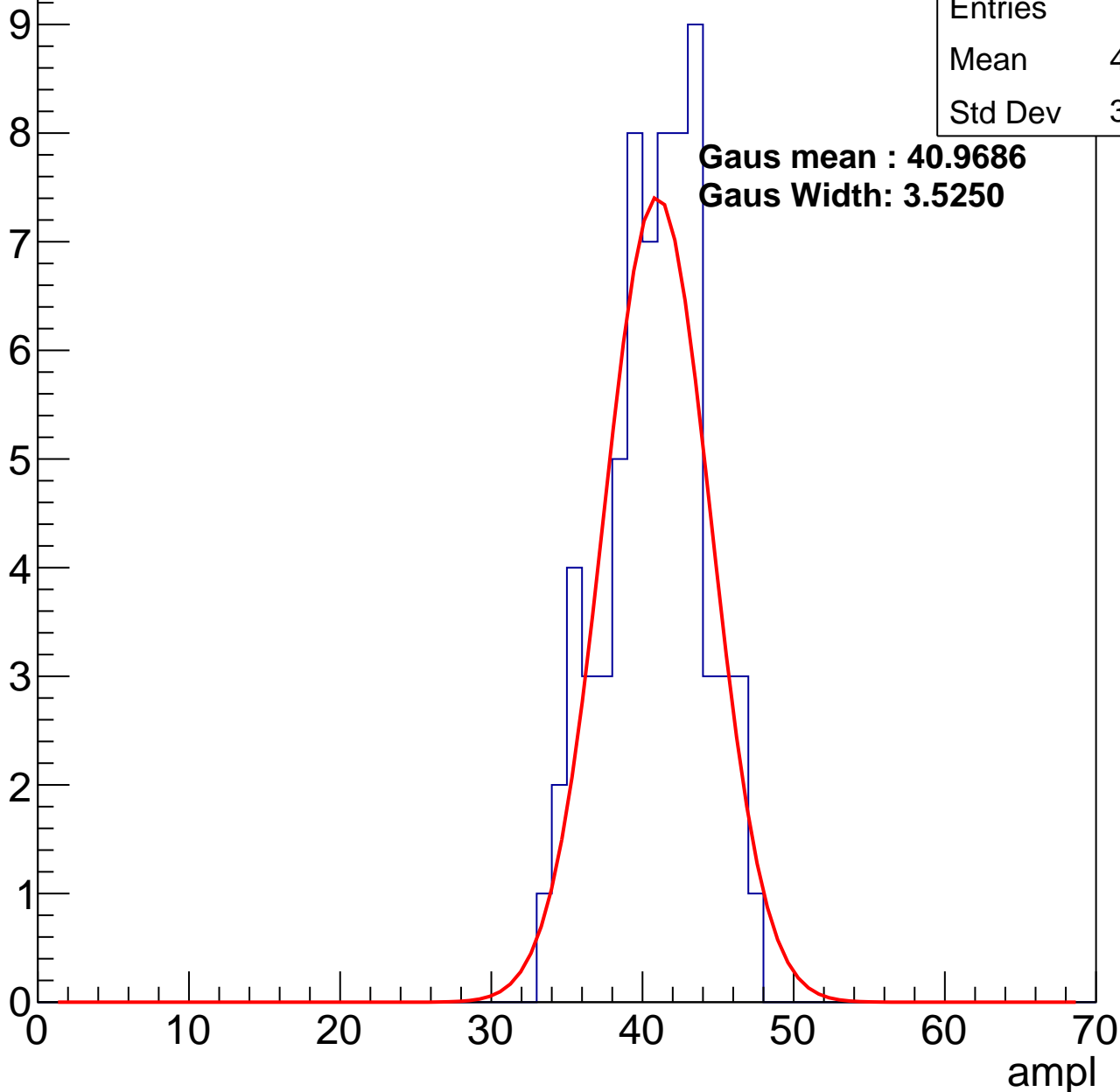
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	40.37
Std Dev	3.244

**Gaus mean : 40.9686**

**Gaus Width: 3.5250**

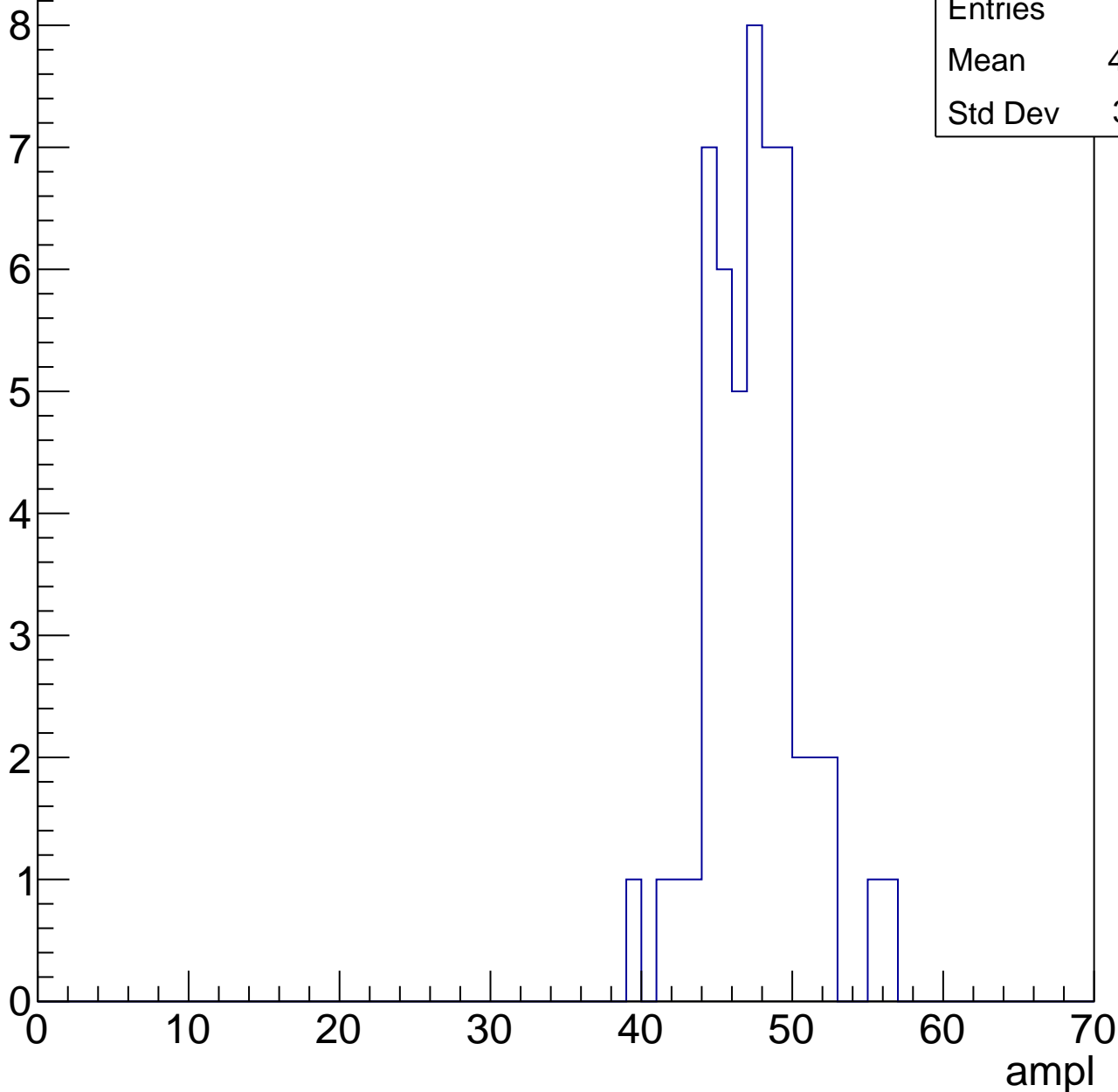


# B1L000S, U1-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	47.02
Std Dev	3.141

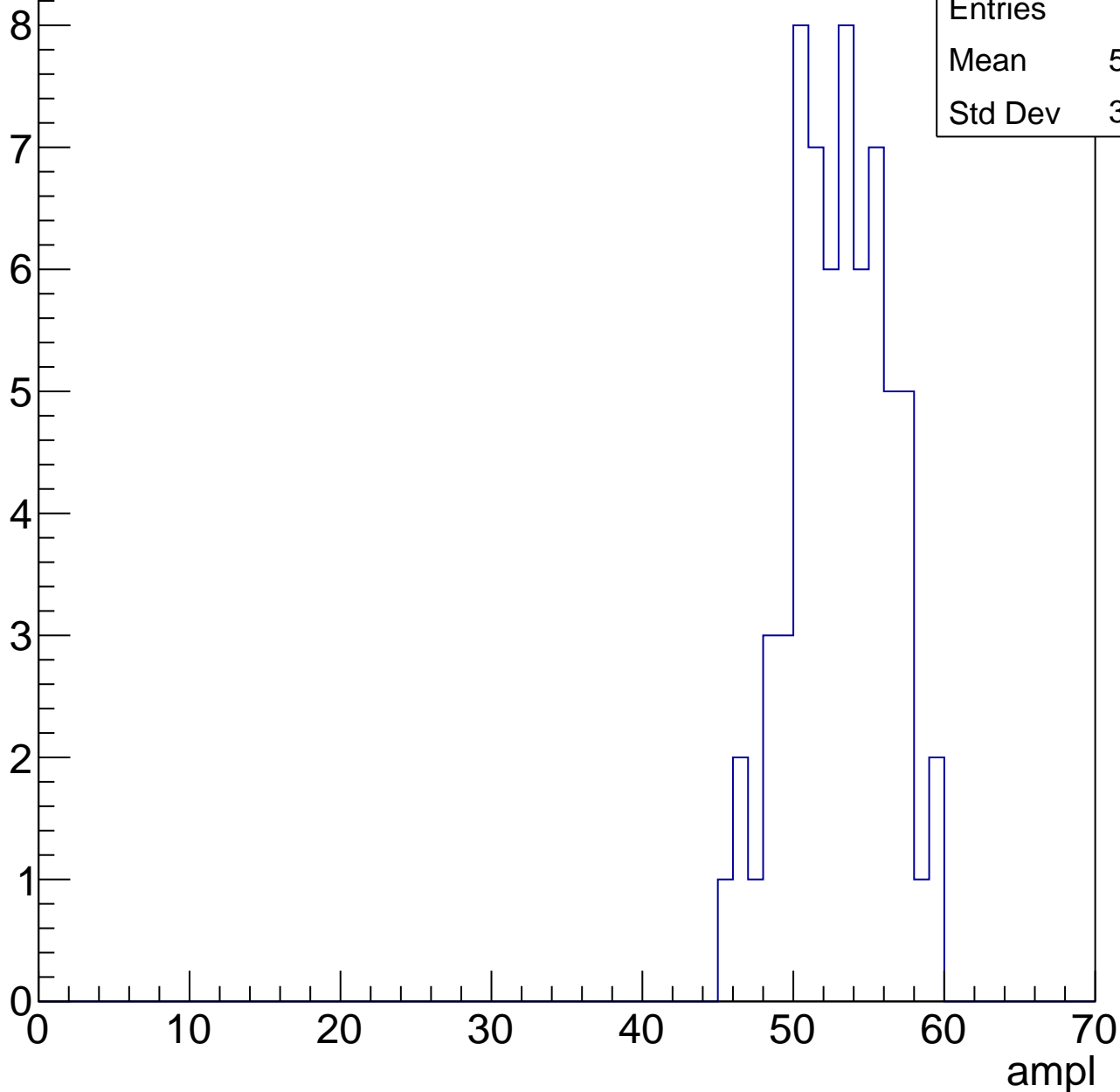


# B1L000S, U1-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

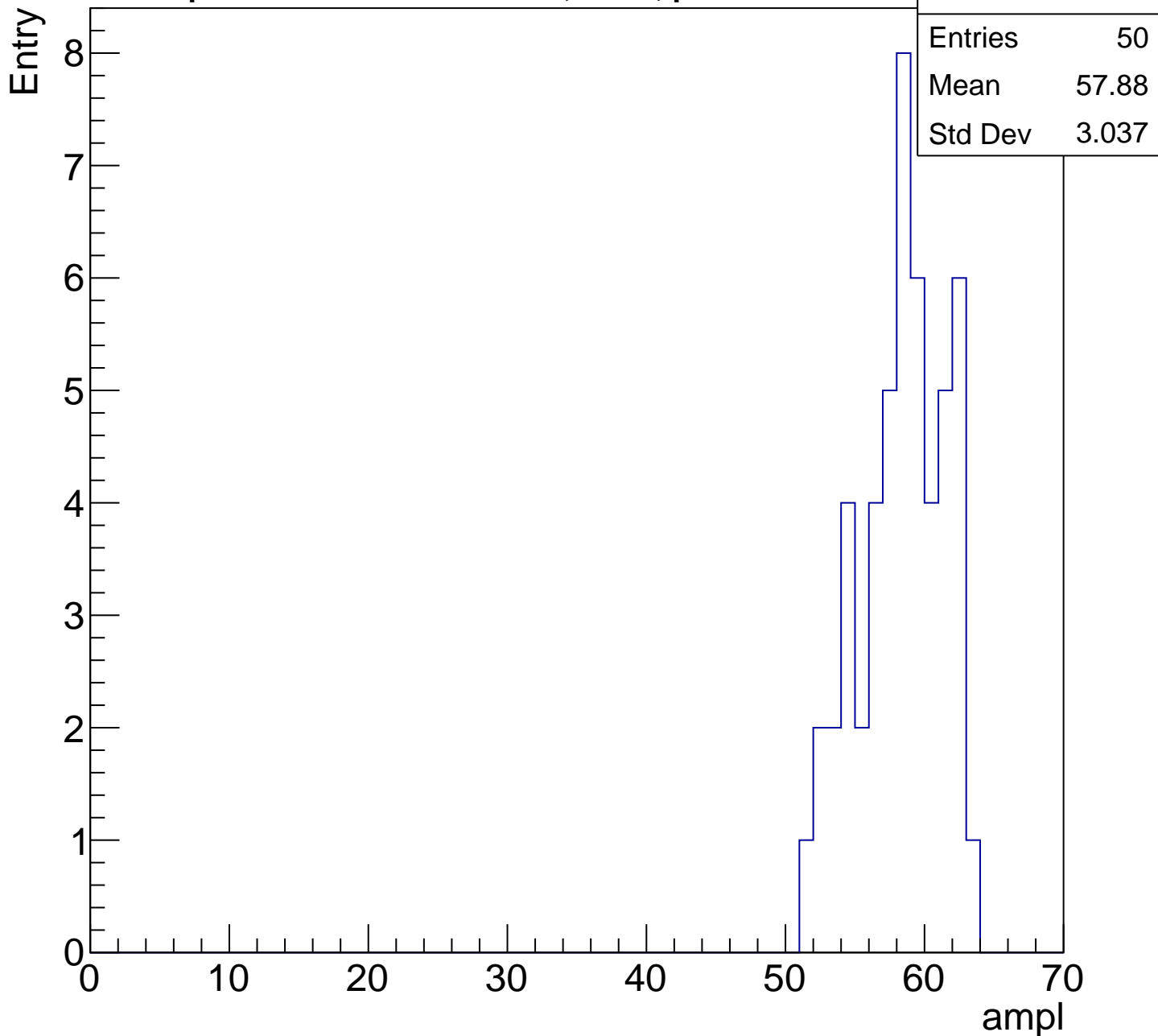
Entry

Entries	65
Mean	52.58
Std Dev	3.215



# B1L000S, U1-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

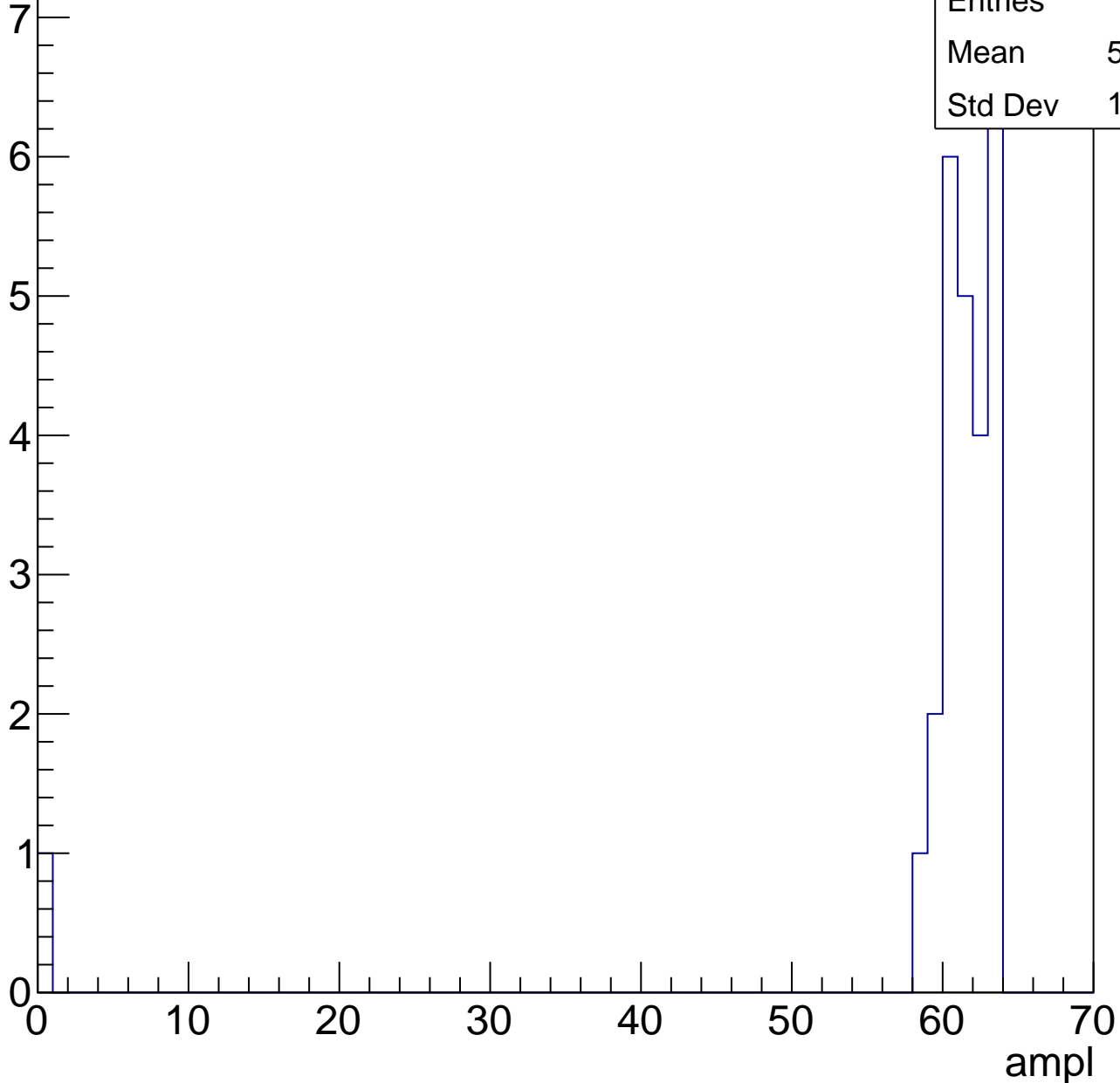


# B1L000S, U1-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.85
Std Dev	11.86

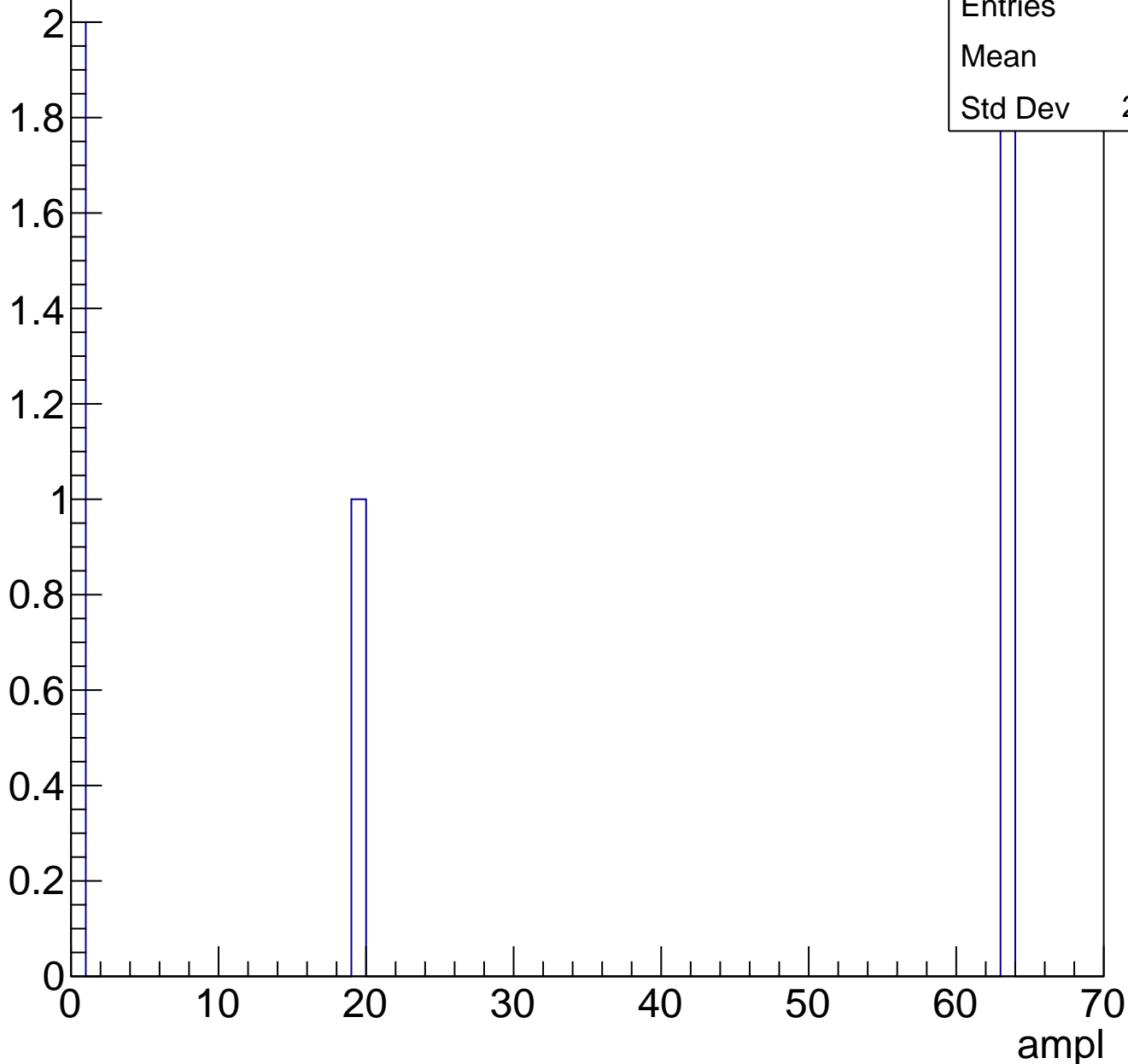




# B1L000S, U1-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	29
Std Dev	28.61

# B1L000S, U1-ch80, adc0

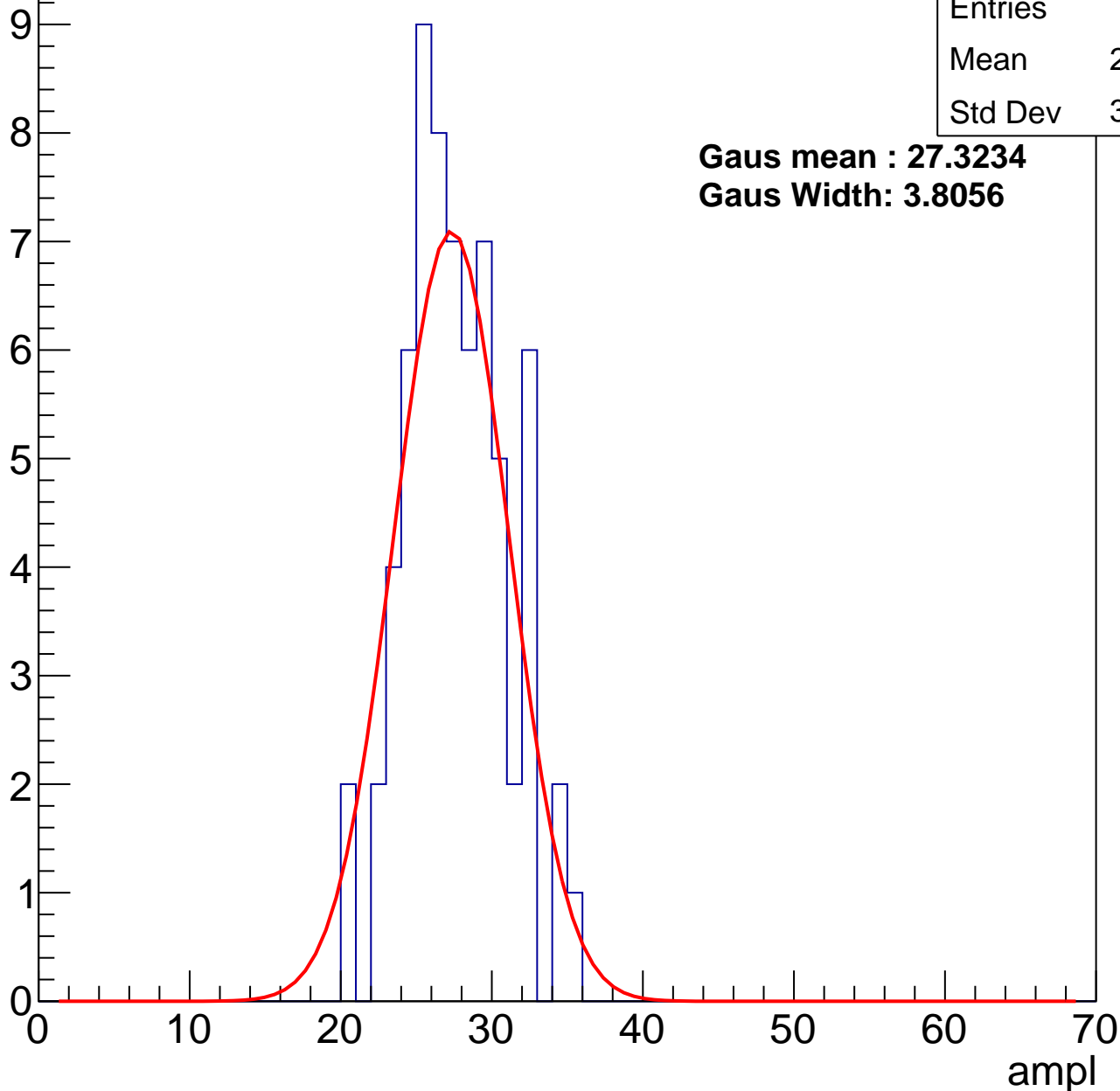
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	27.16
Std Dev	3.304

**Gaus mean : 27.3234**

**Gaus Width: 3.8056**



# B1L000S, U1-ch80, adc1

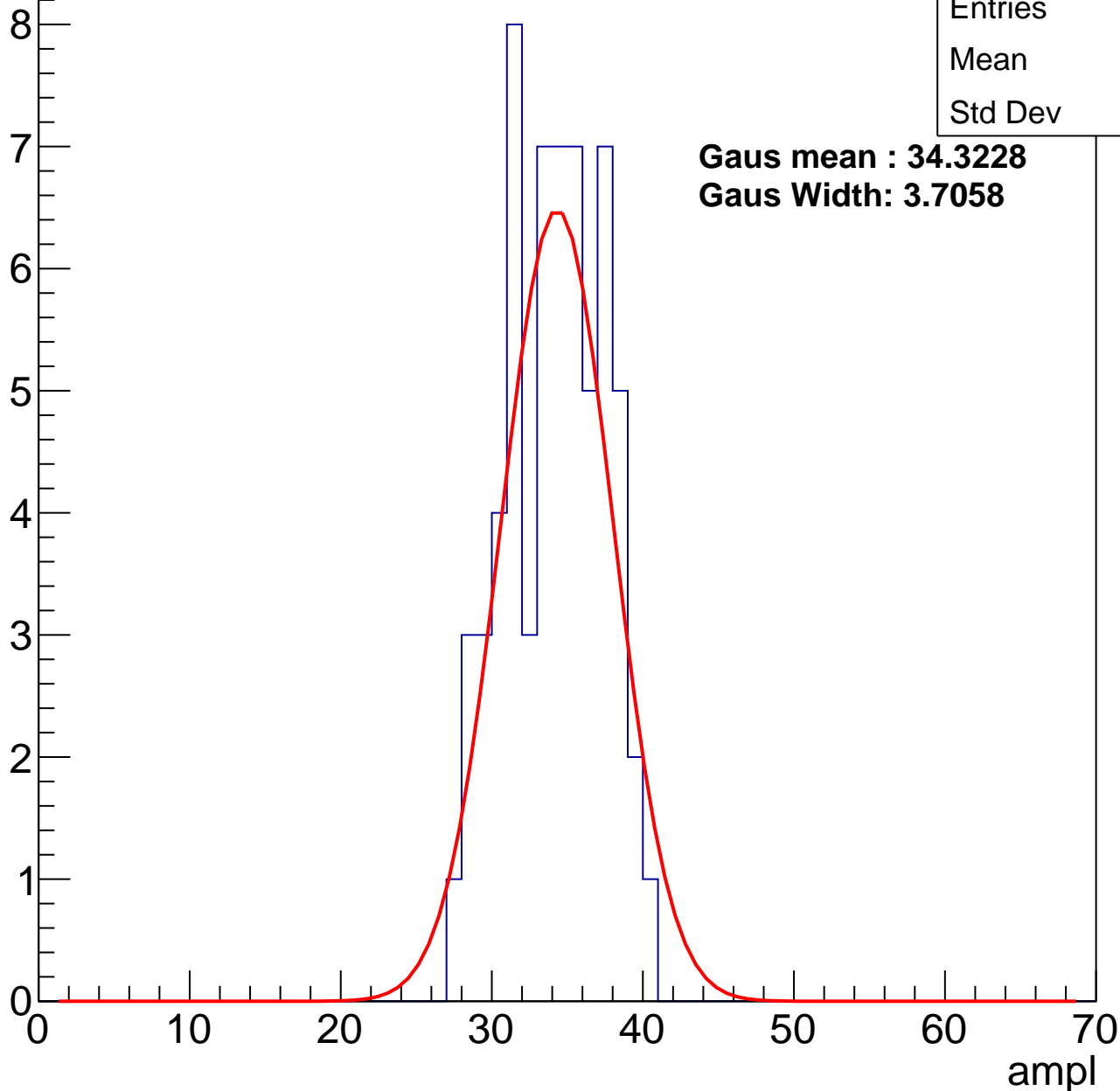
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	33.7
Std Dev	3.18

**Gaus mean : 34.3228**

**Gaus Width: 3.7058**



# B1L000S, U1-ch80, adc2

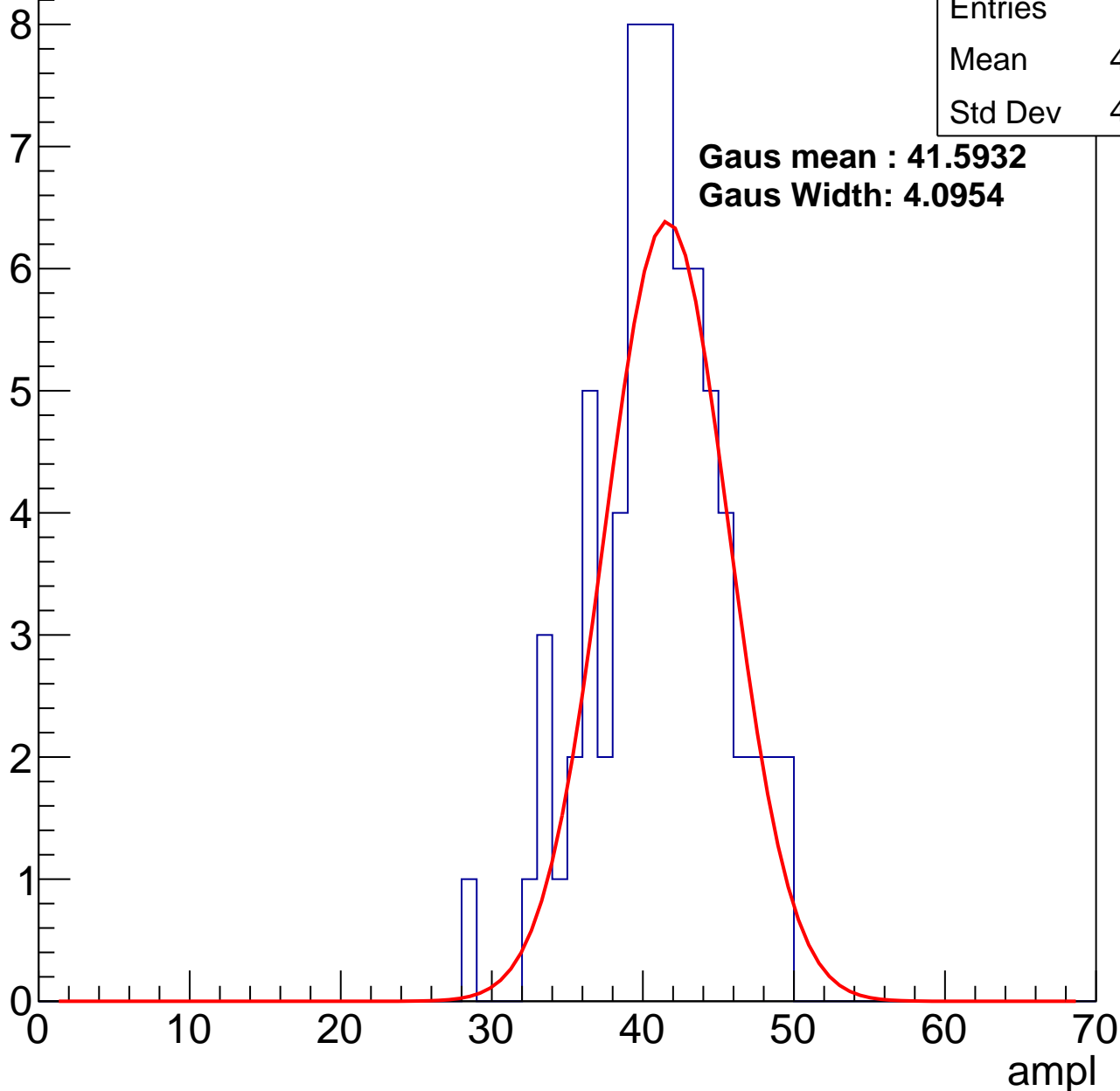
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	40.54
Std Dev	4.196

**Gaus mean : 41.5932**

**Gaus Width: 4.0954**



# B1L000S, U1-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 52

Mean 45.73

Std Dev 3.392

ampl

0

10

20

30

40

50

60

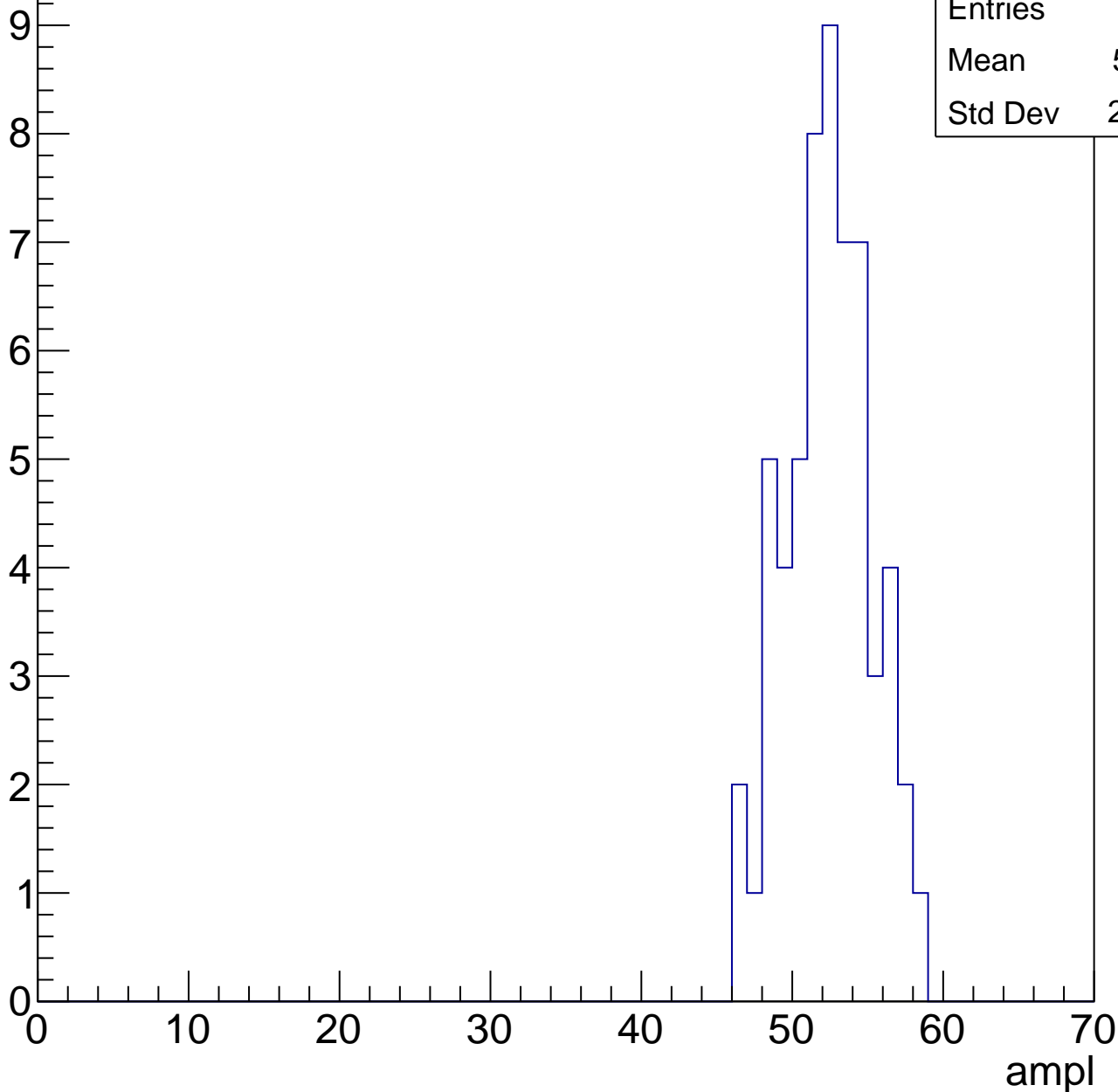
70

# B1L000S, U1-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

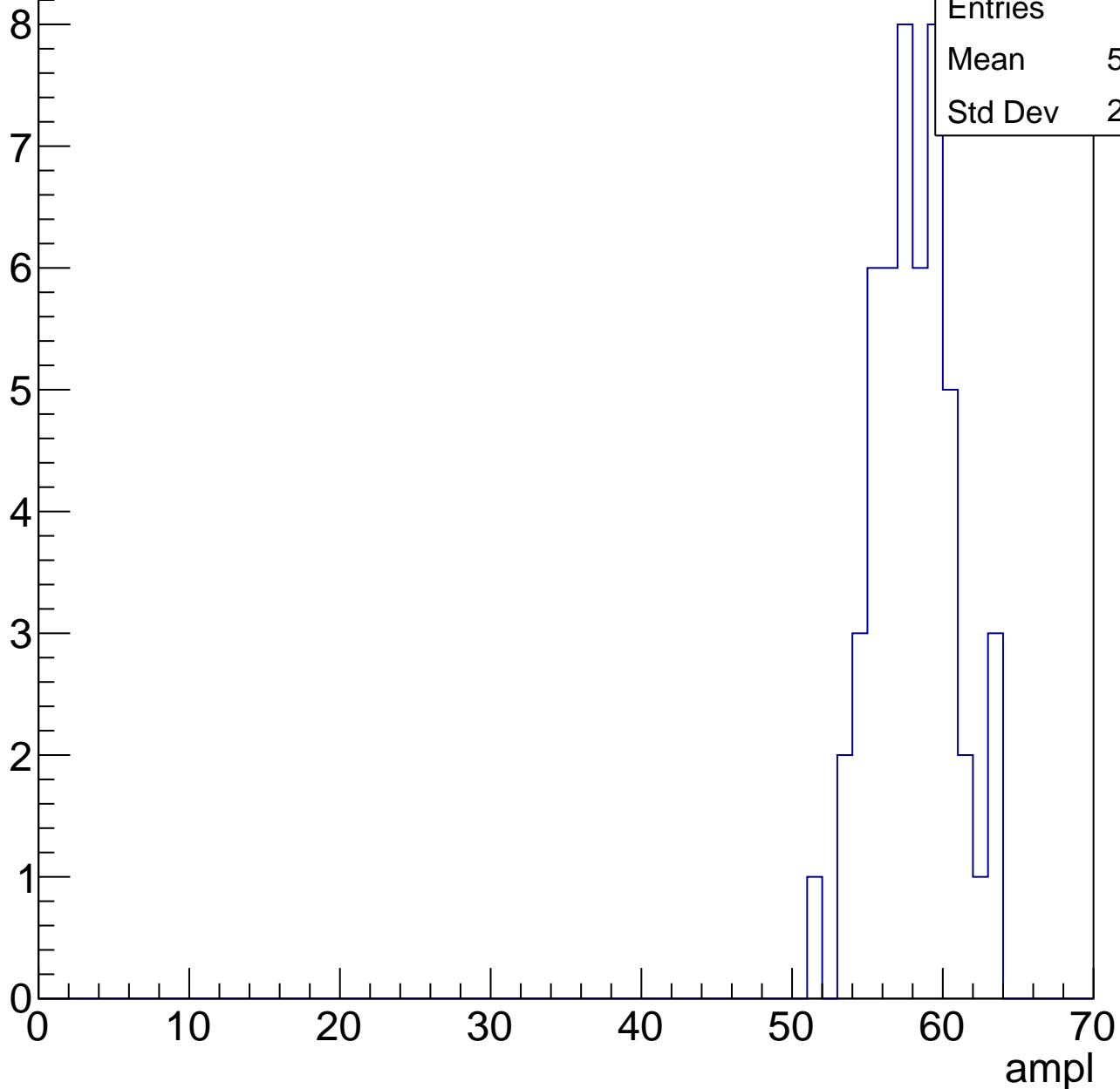
Entries	58
Mean	51.91
Std Dev	2.793



# B1L000S, U1-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

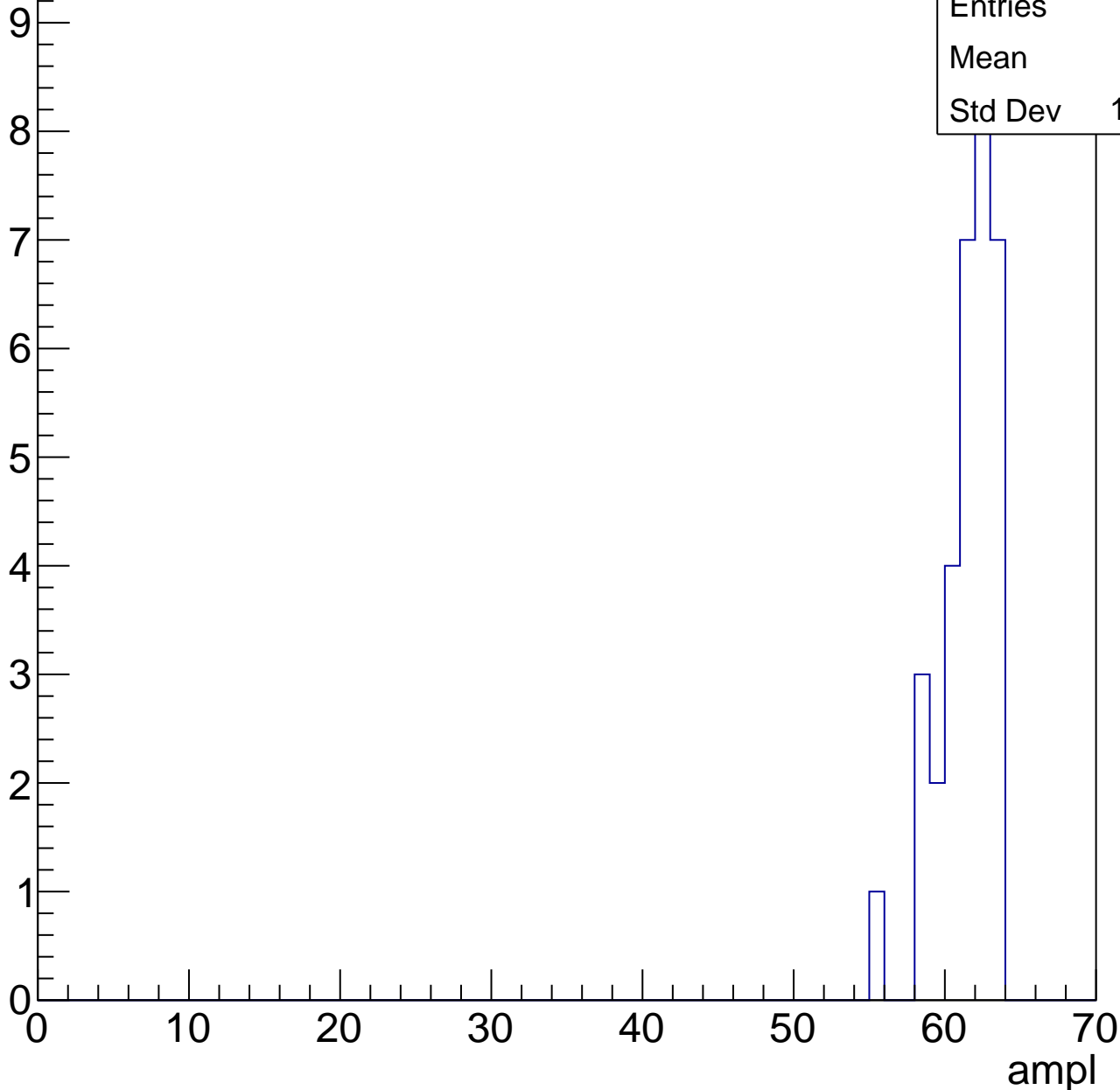


# B1L000S, U1-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	33
Mean	61
Std Dev	1.842

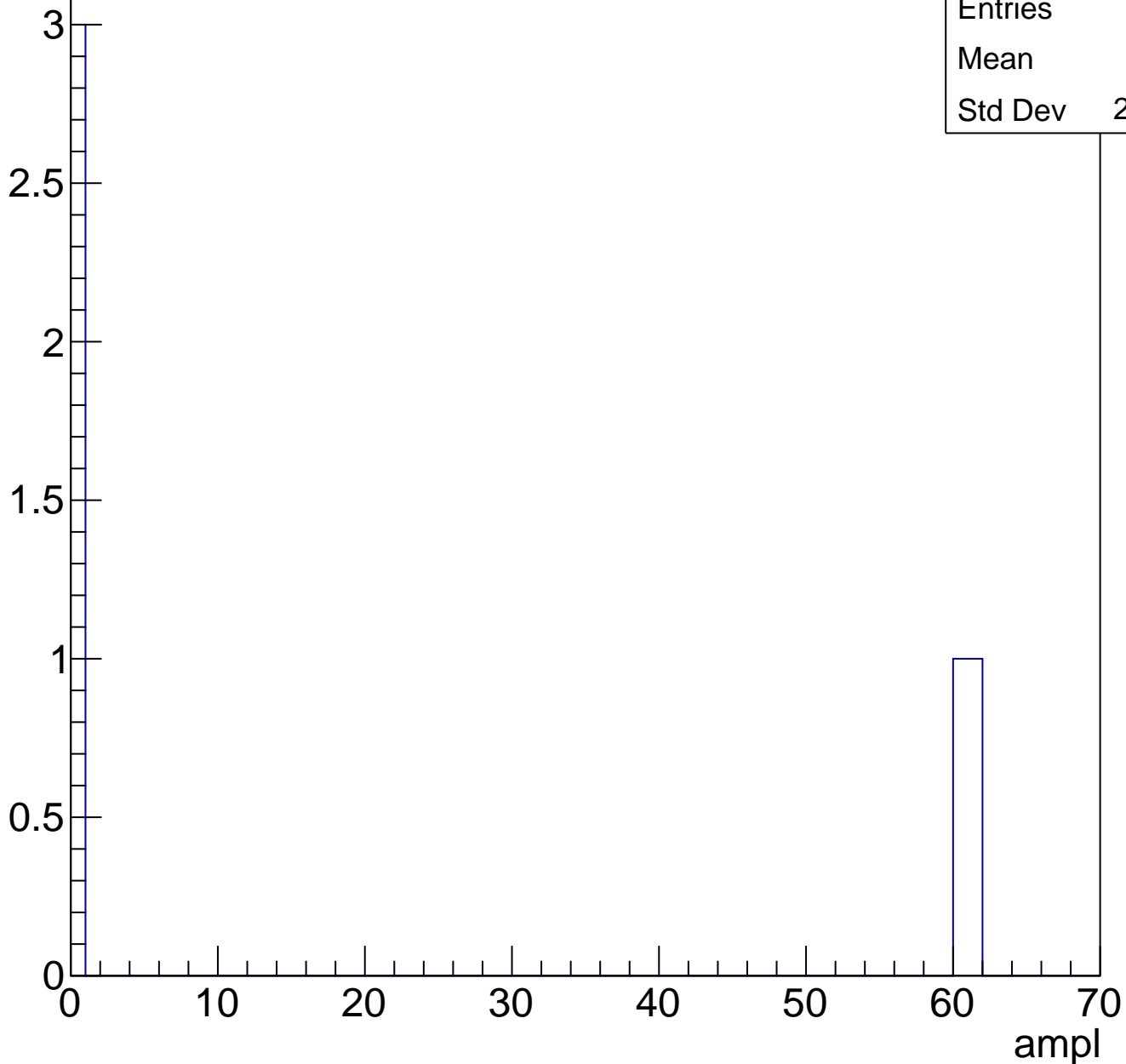




# B1L000S, U1-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch81, adc0

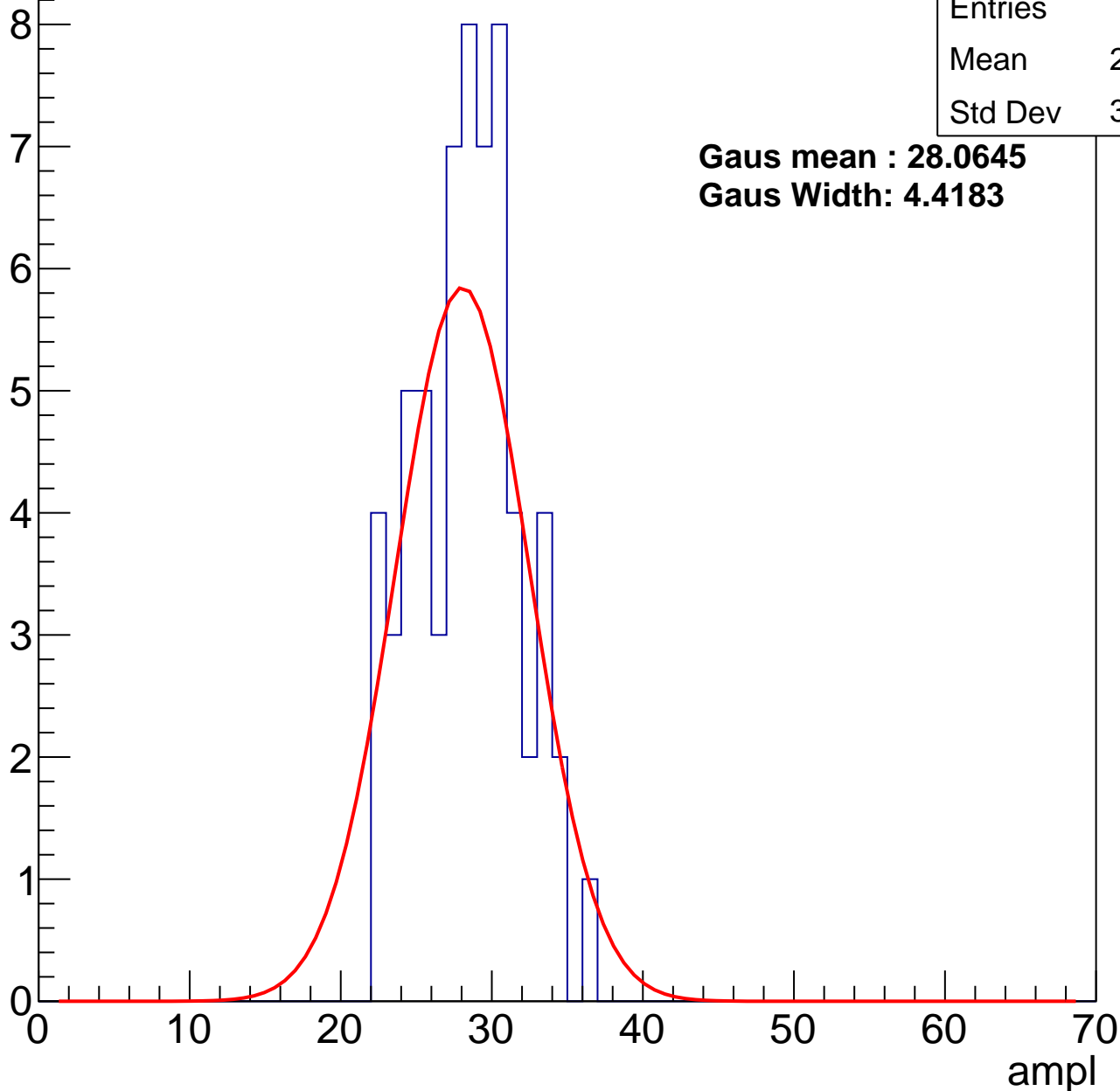
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	27.94
Std Dev	3.347

**Gaus mean : 28.0645**

**Gaus Width: 4.4183**



# B1L000S, U1-ch81, adc1

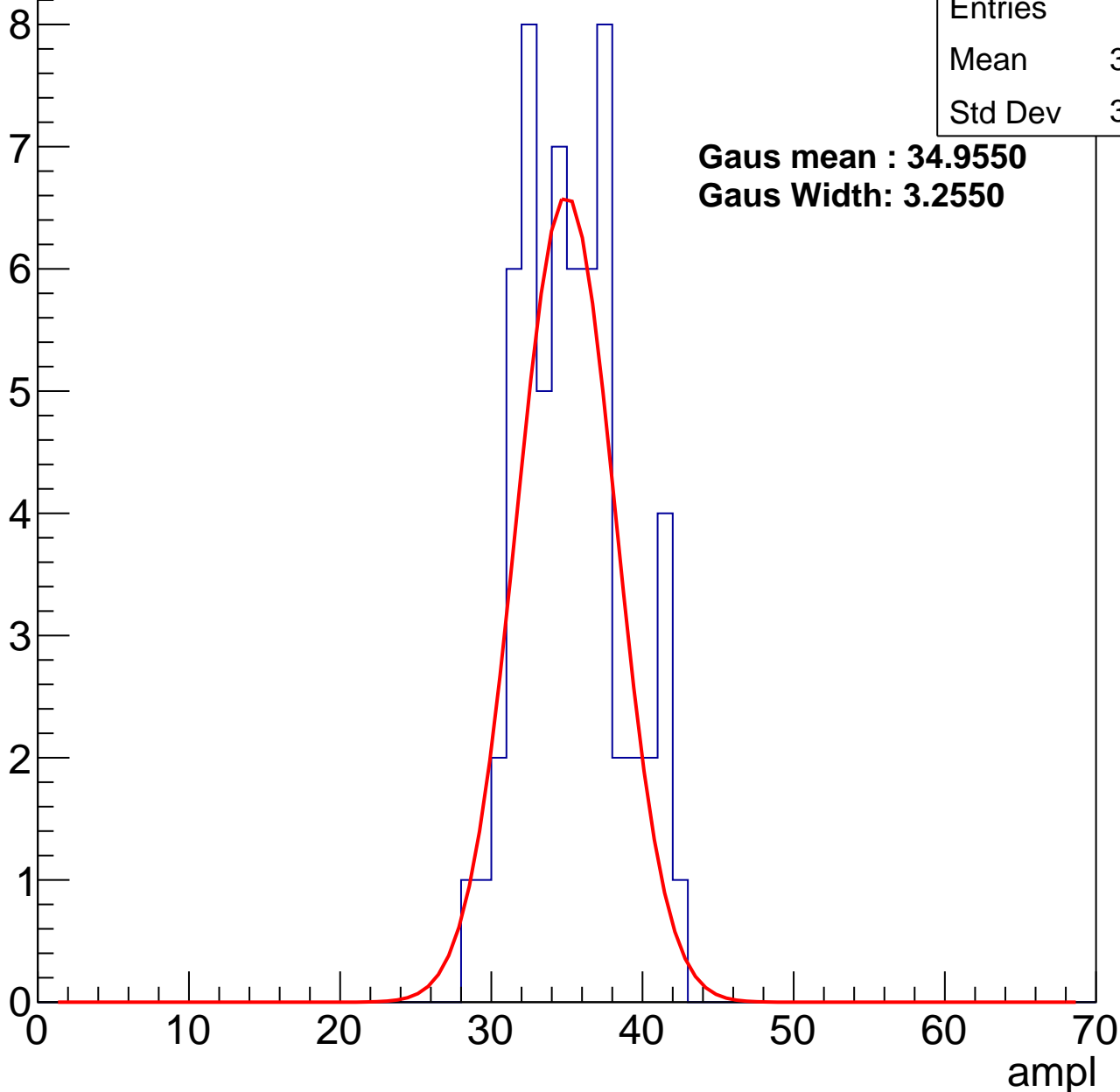
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	34.82
Std Dev	3.287

**Gaus mean : 34.9550**

**Gaus Width: 3.2550**



# B1L000S, U1-ch81, adc2

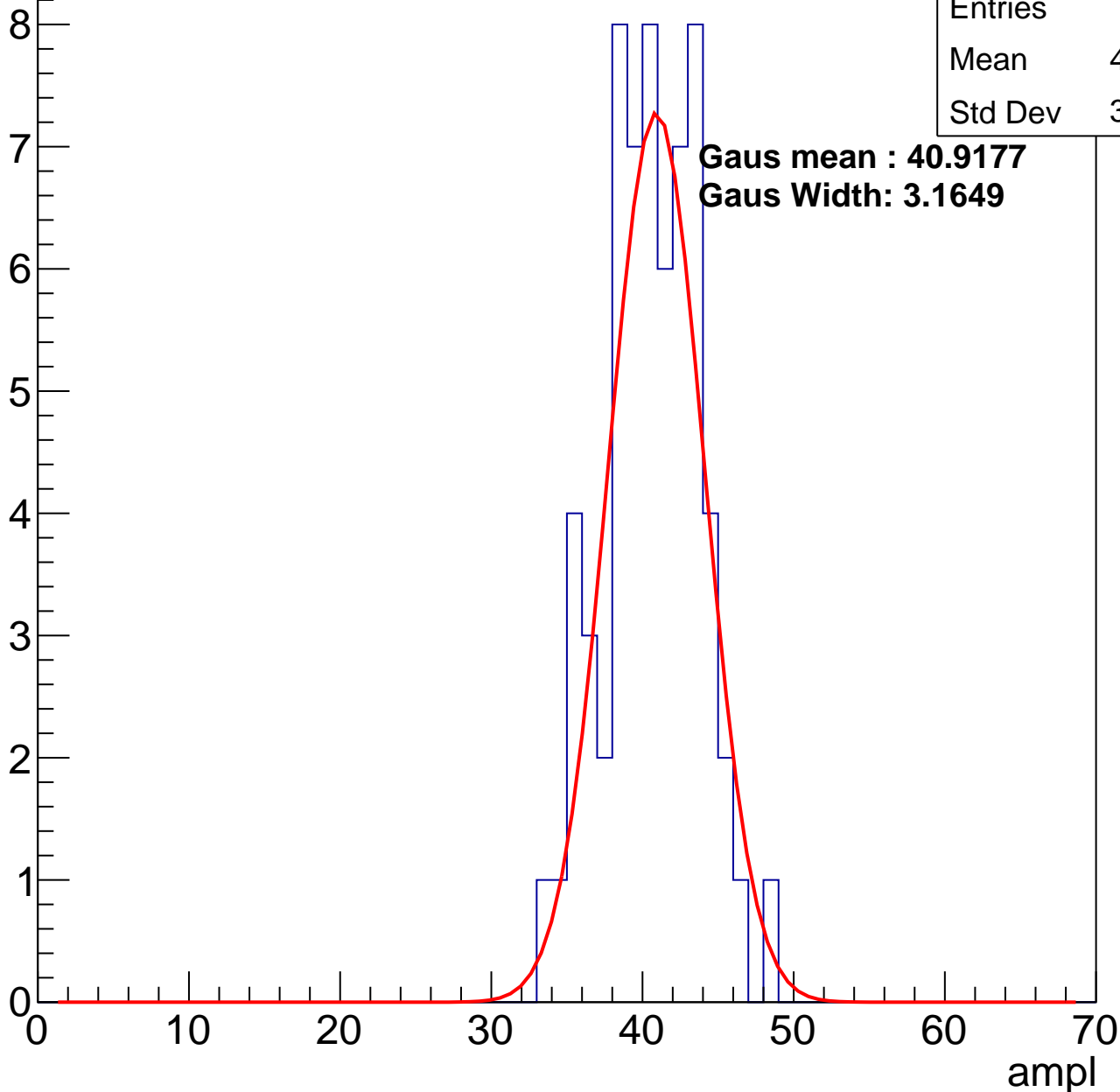
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	40.16
Std Dev	3.108

**Gaus mean : 40.9177**

**Gaus Width: 3.1649**

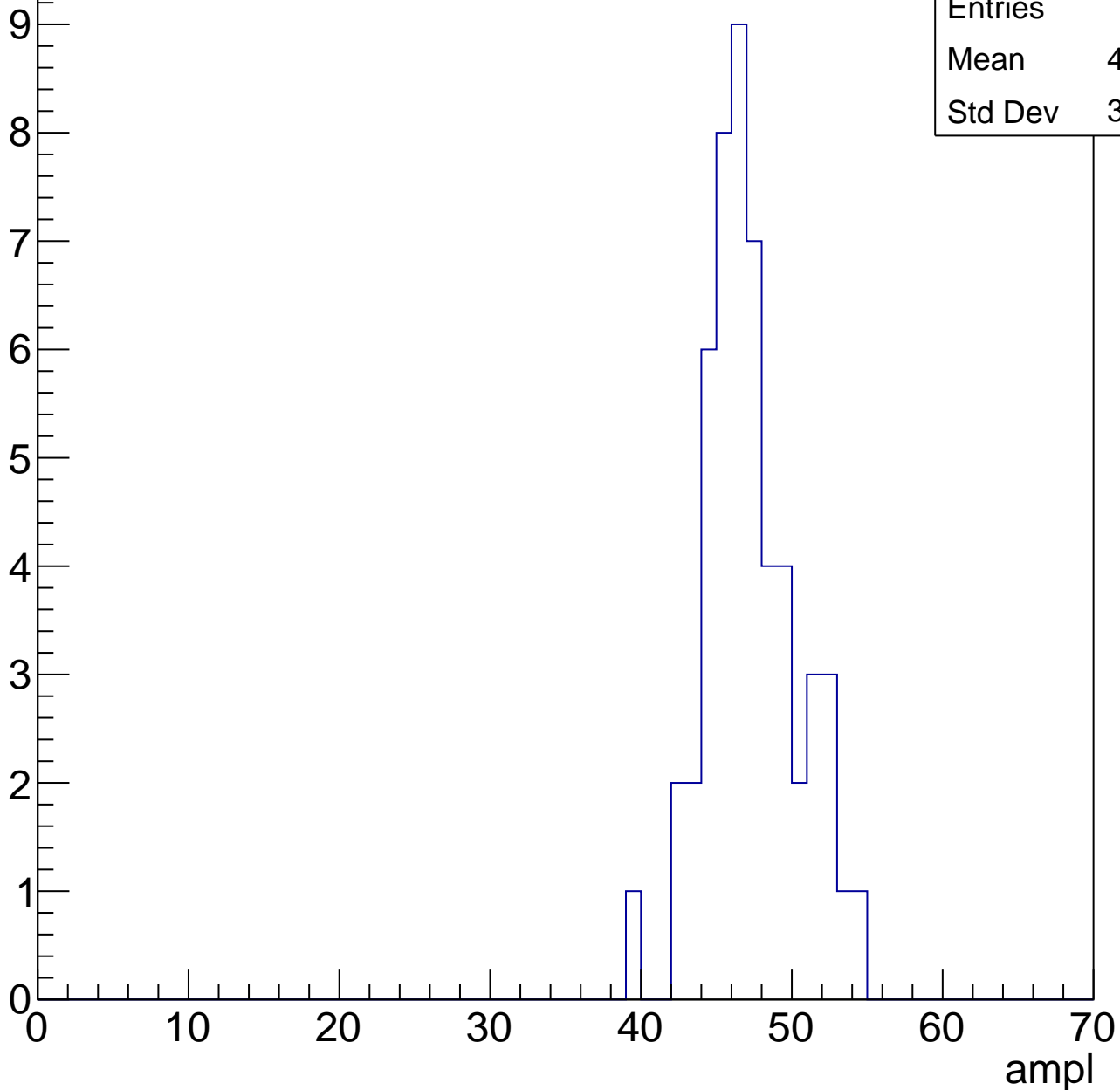


# B1L000S, U1-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	46.79
Std Dev	3.024

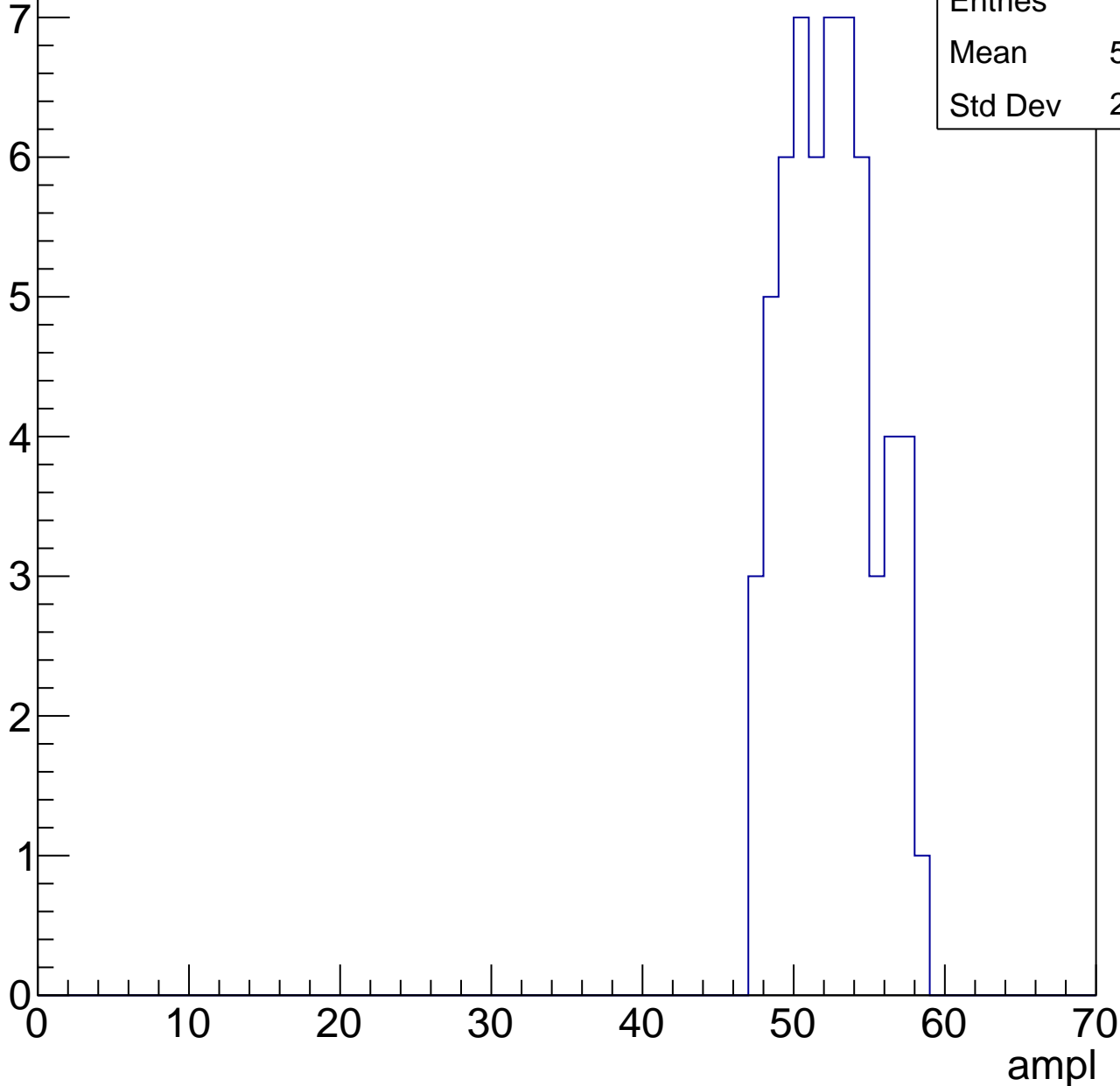


# B1L000S, U1-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

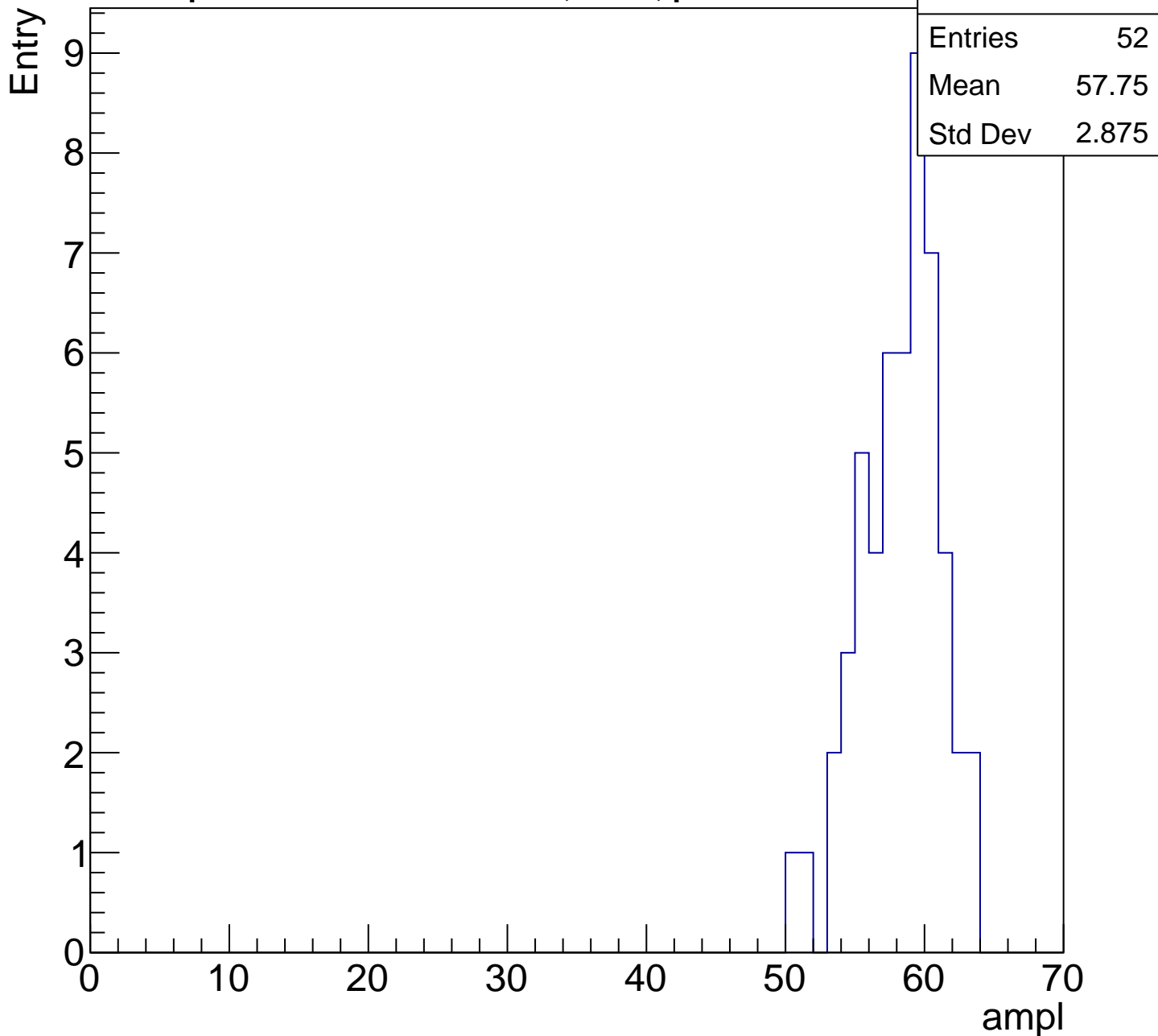
Entry

Entries	59
Mean	51.95
Std Dev	2.914



# B1L000S, U1-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

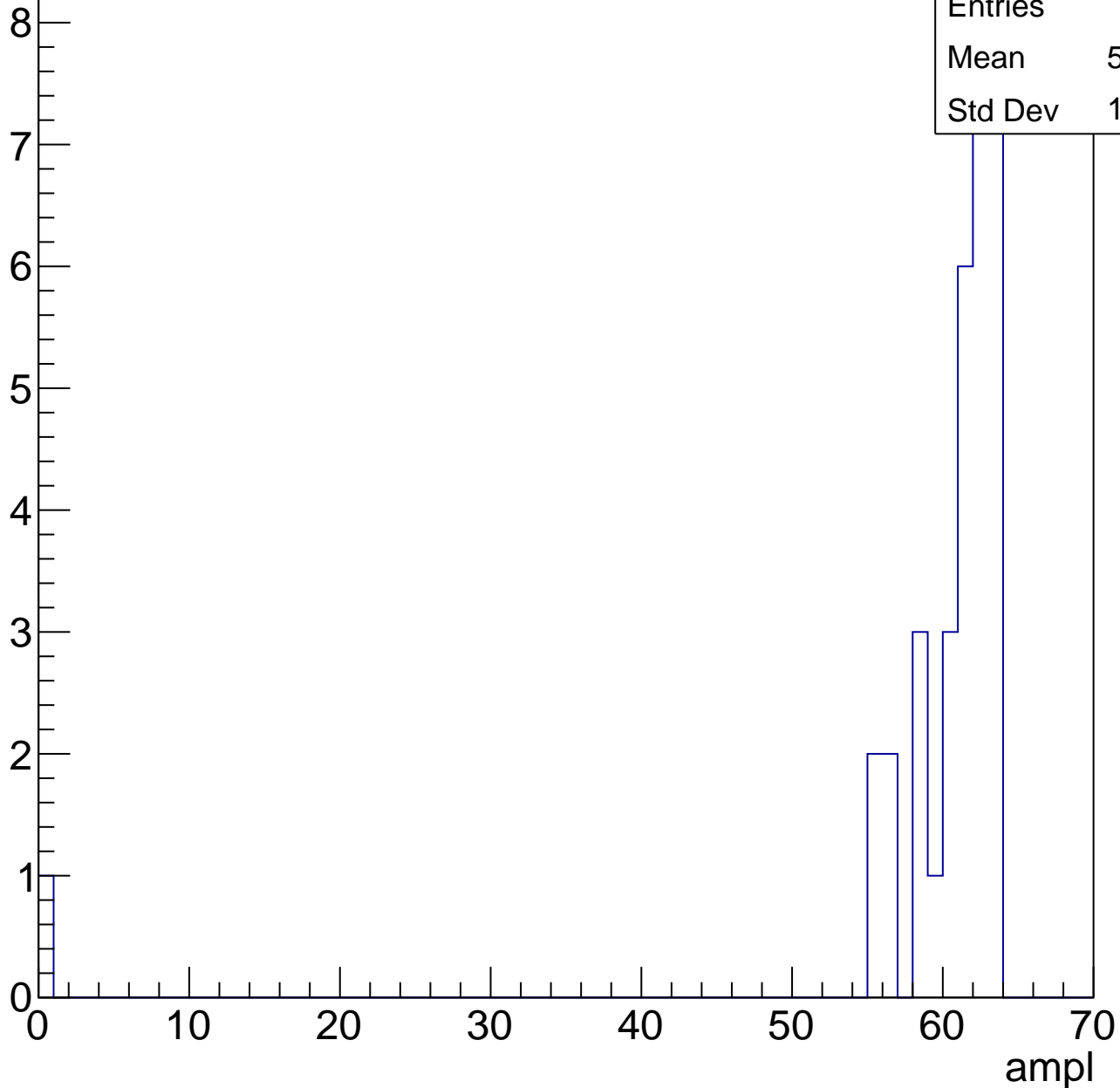


# B1L000S, U1-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	58.85
Std Dev	10.52





# B1L000S, U1-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U1-ch82, adc0

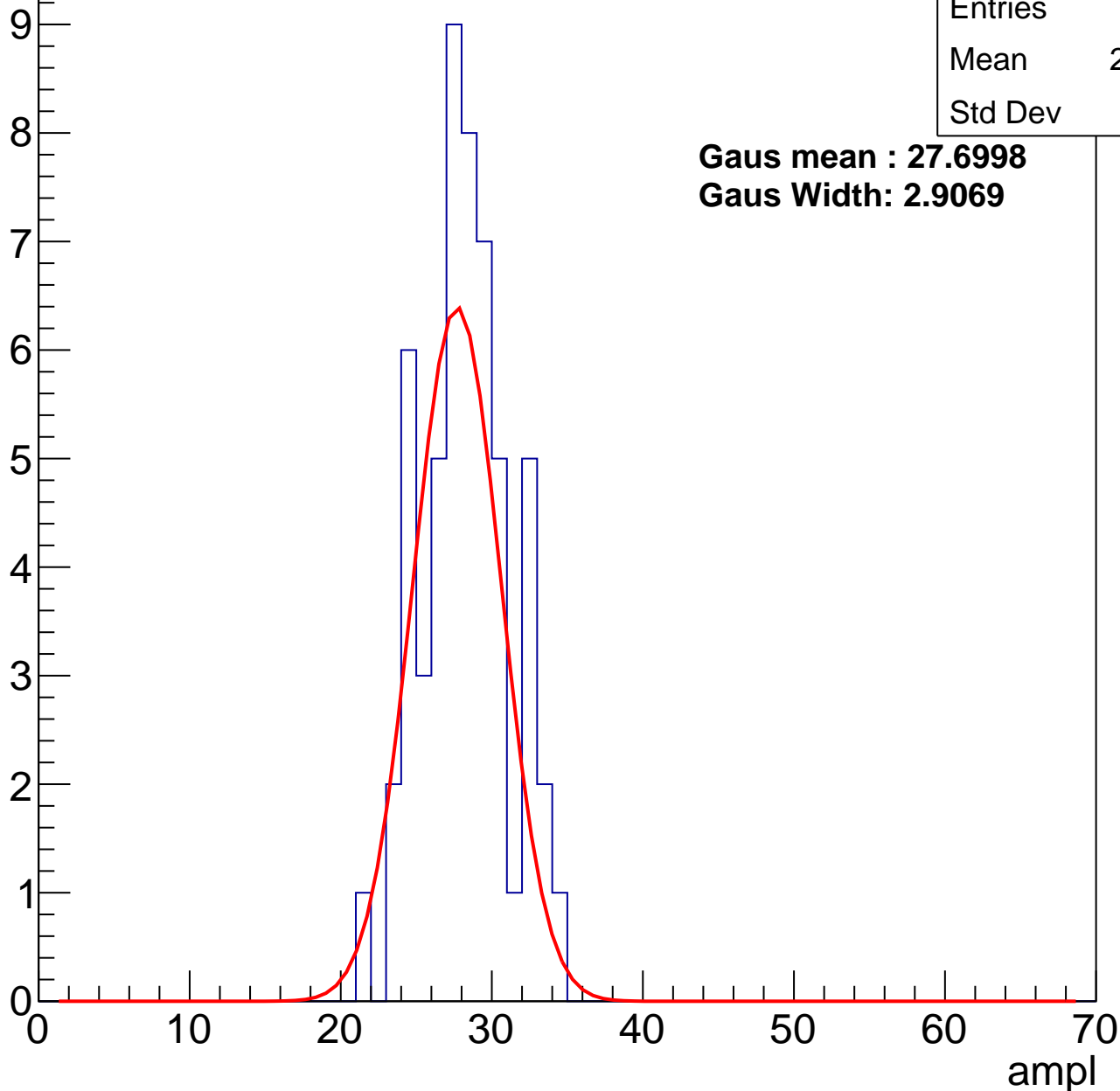
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	27.76
Std Dev	2.86

**Gaus mean : 27.6998**

**Gaus Width: 2.9069**



# B1L000S, U1-ch82, adc1

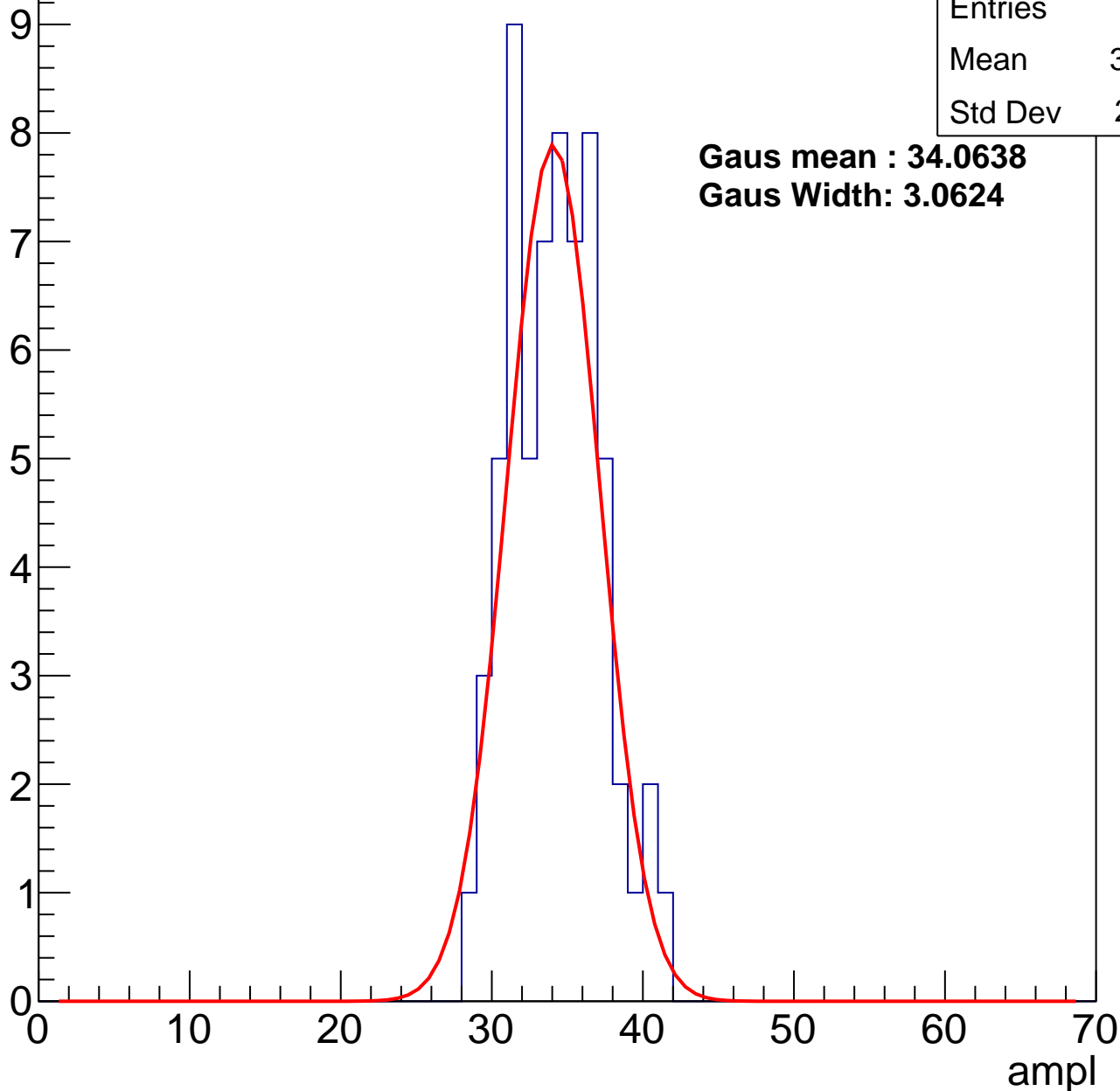
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	33.77
Std Dev	2.951

**Gaus mean : 34.0638**

**Gaus Width: 3.0624**



# B1L000S, U1-ch82, adc2

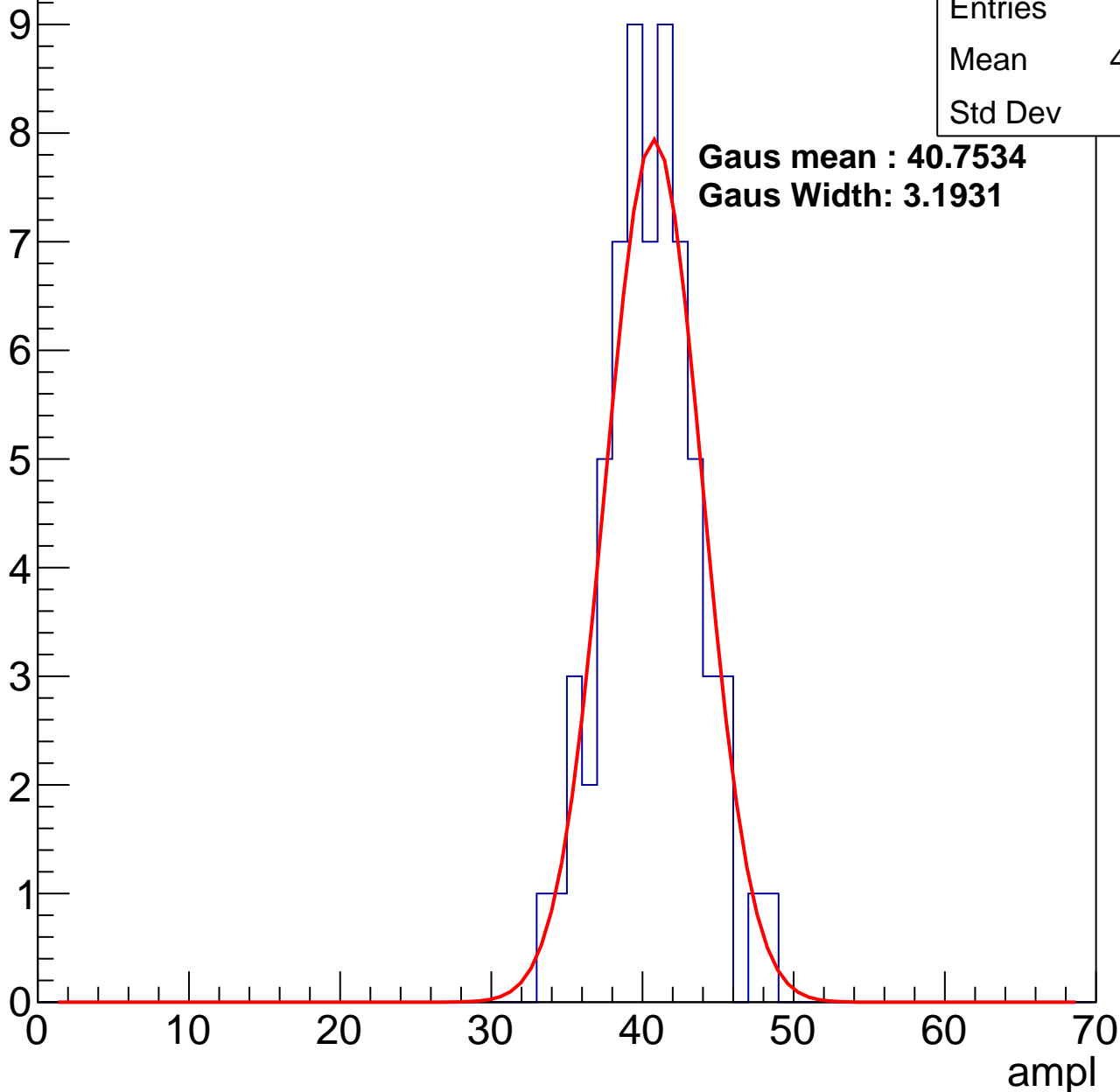
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	40.09
Std Dev	3.04

**Gaus mean : 40.7534**

**Gaus Width: 3.1931**

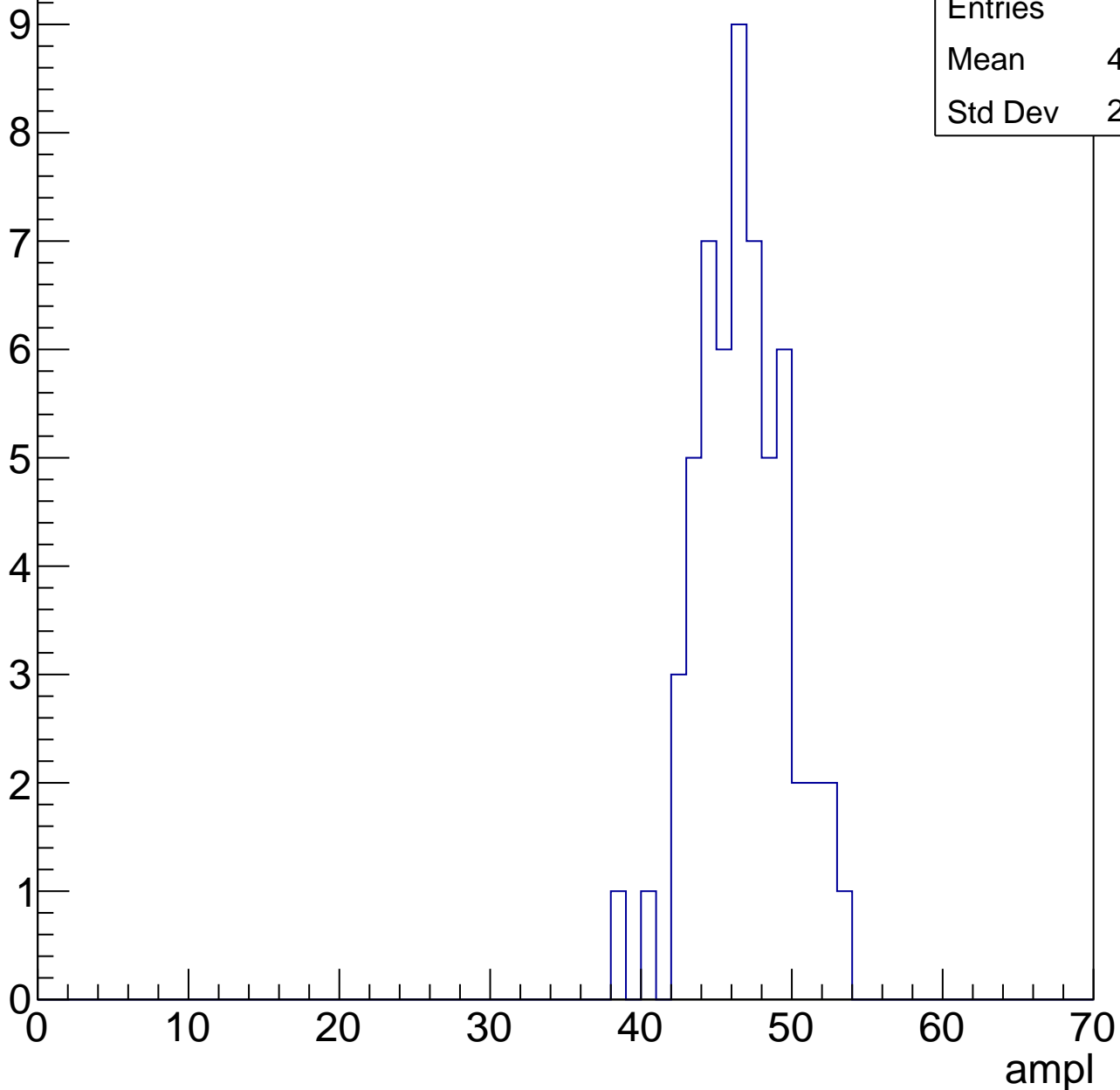


# B1L000S, U1-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	46.19
Std Dev	2.988

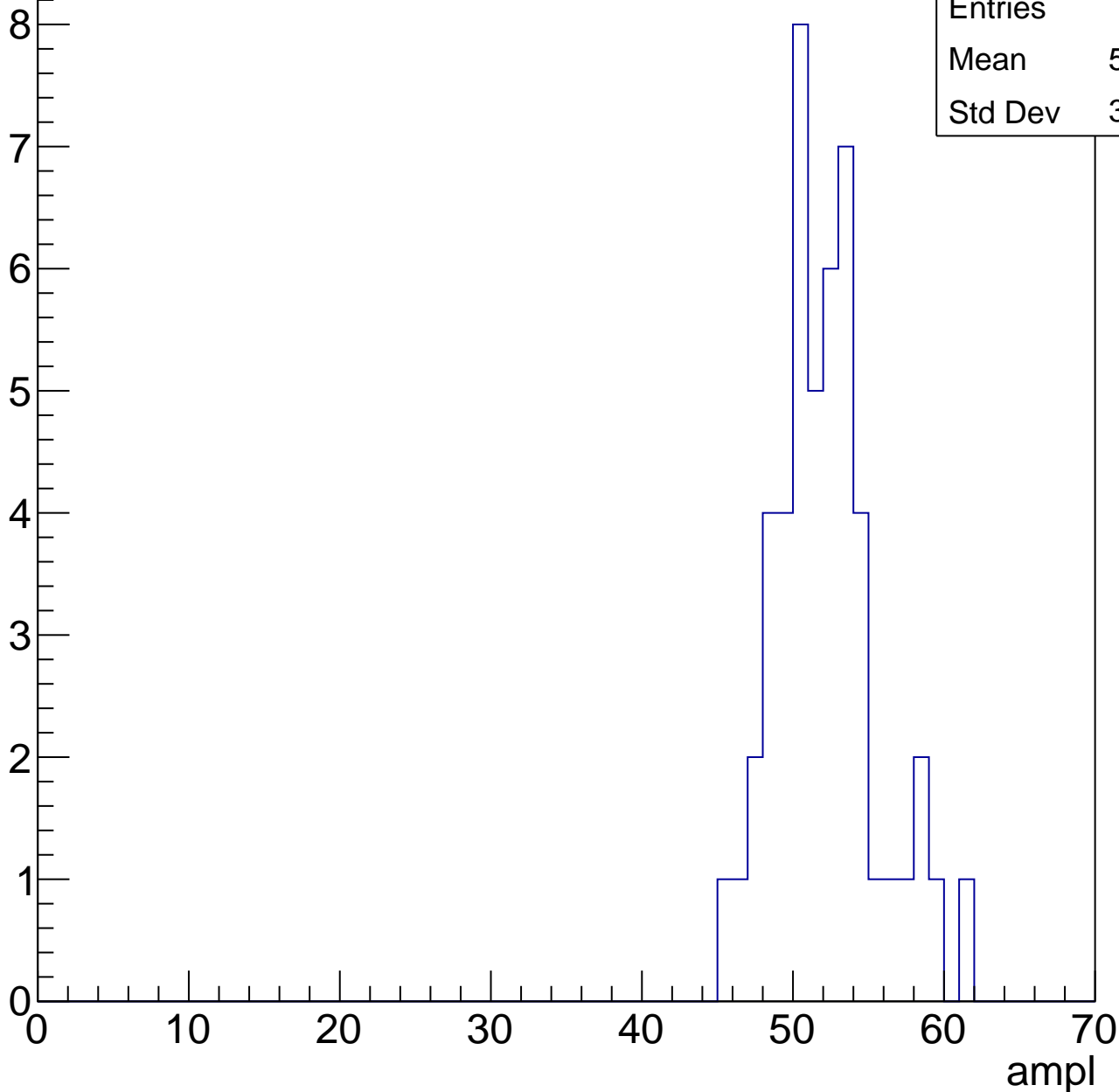


# B1L000S, U1-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	51.65
Std Dev	3.323

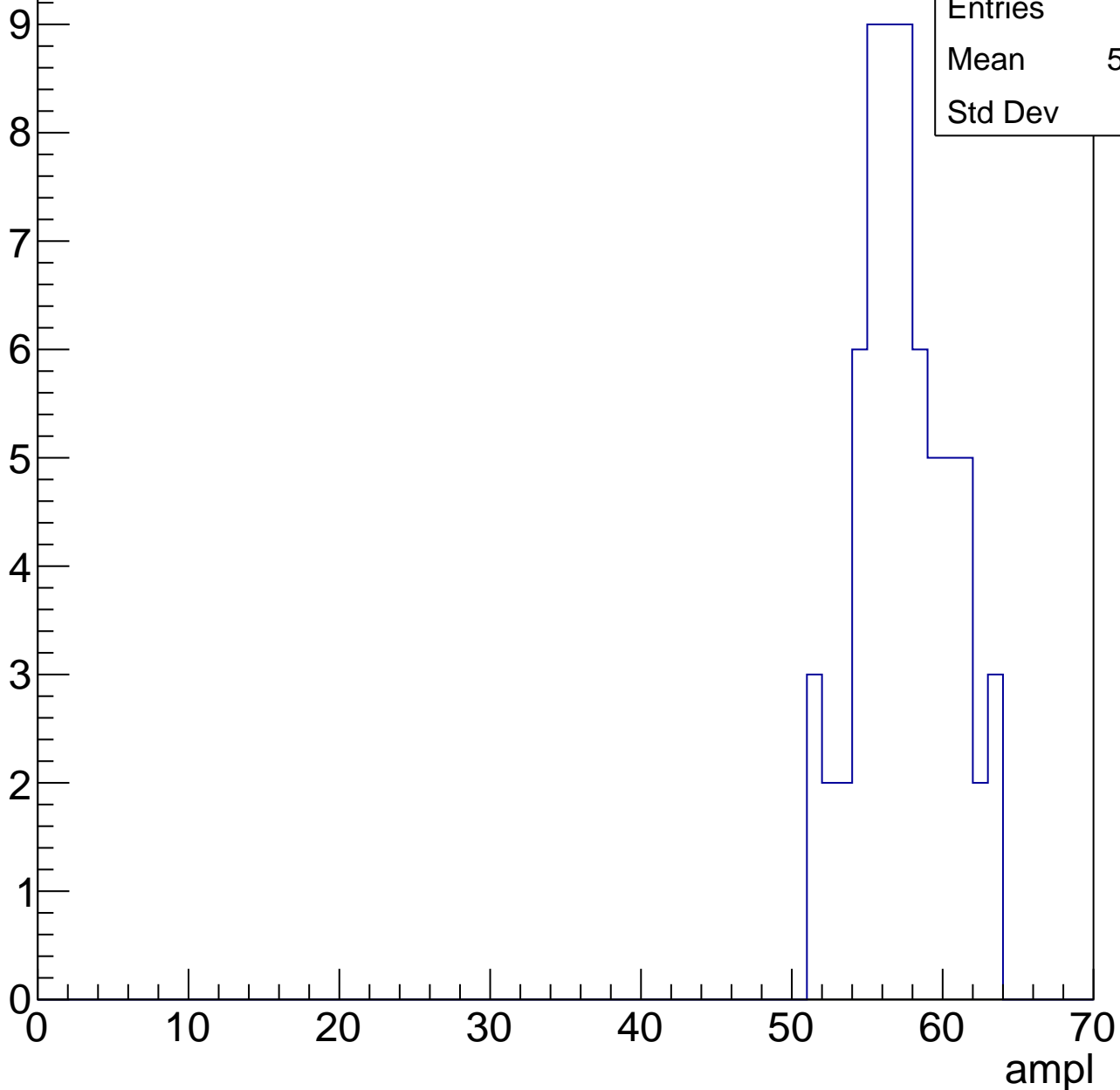


# B1L000S, U1-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	56.97
Std Dev	3.01

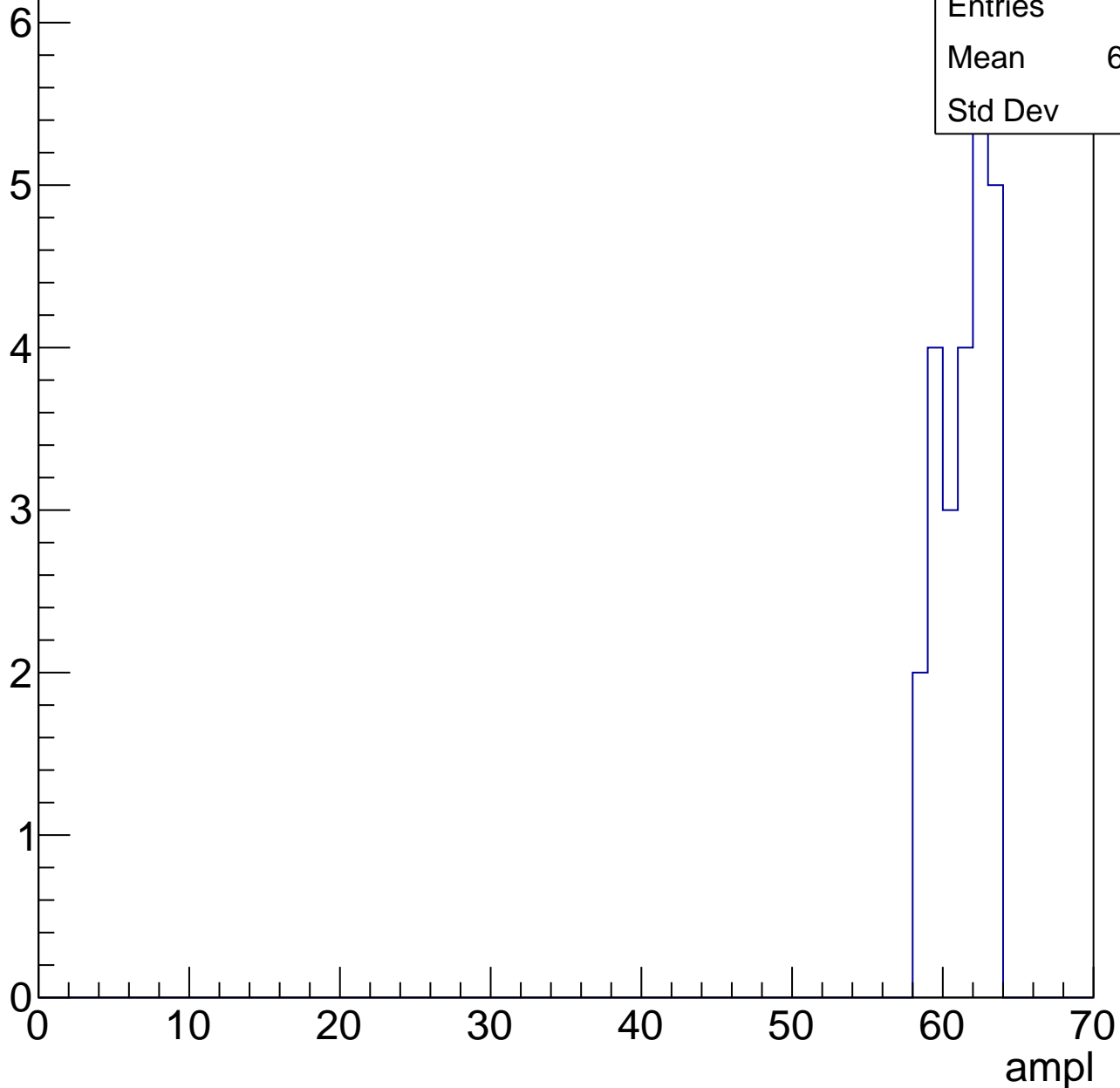


# B1L000S, U1-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	60.96
Std Dev	1.62





# B1L000S, U1-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

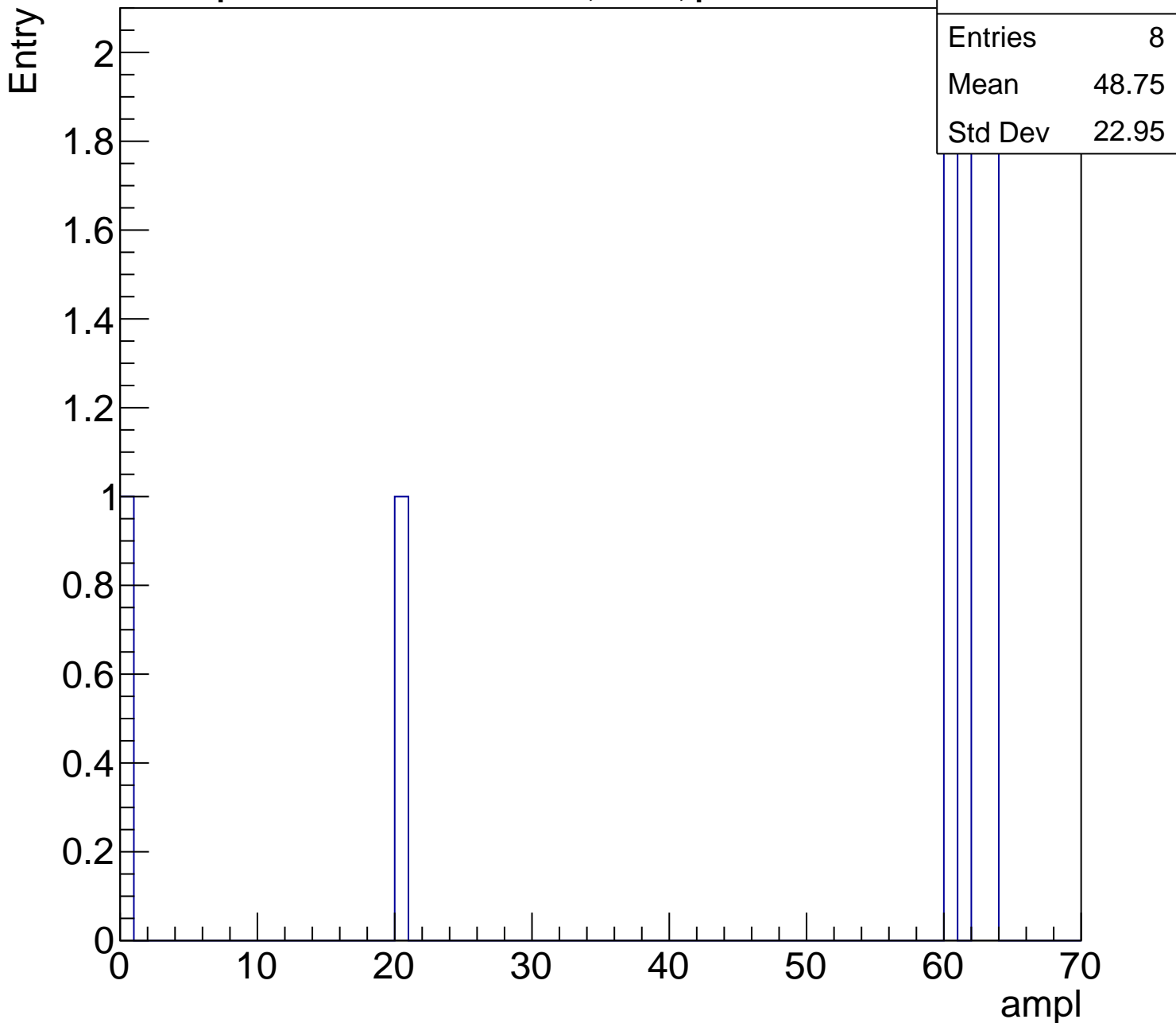
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	48.75
Std Dev	22.95

0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch83, adc0

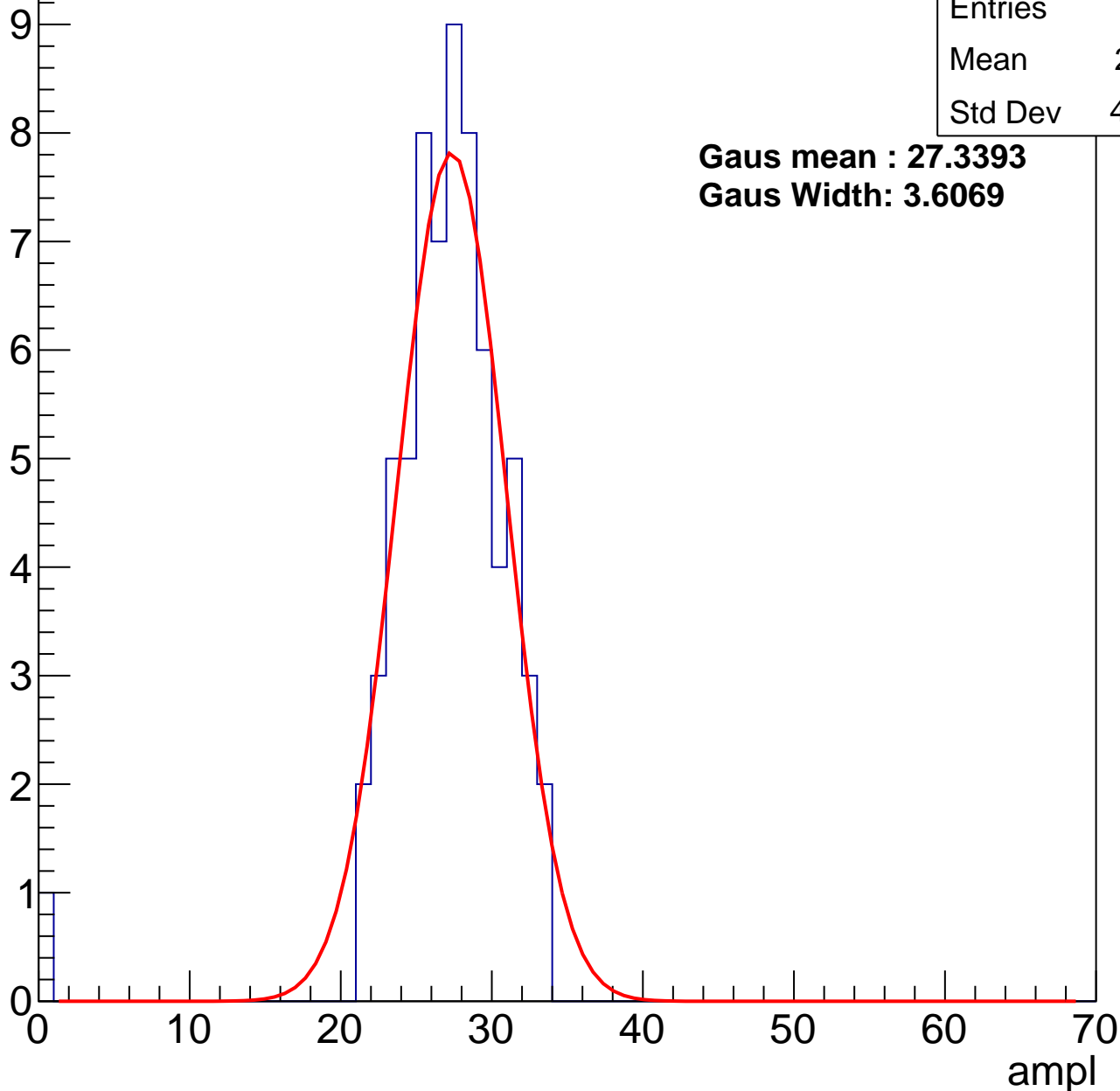
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	26.51
Std Dev	4.404

**Gaus mean : 27.3393**

**Gaus Width: 3.6069**



# B1L000S, U1-ch83, adc1

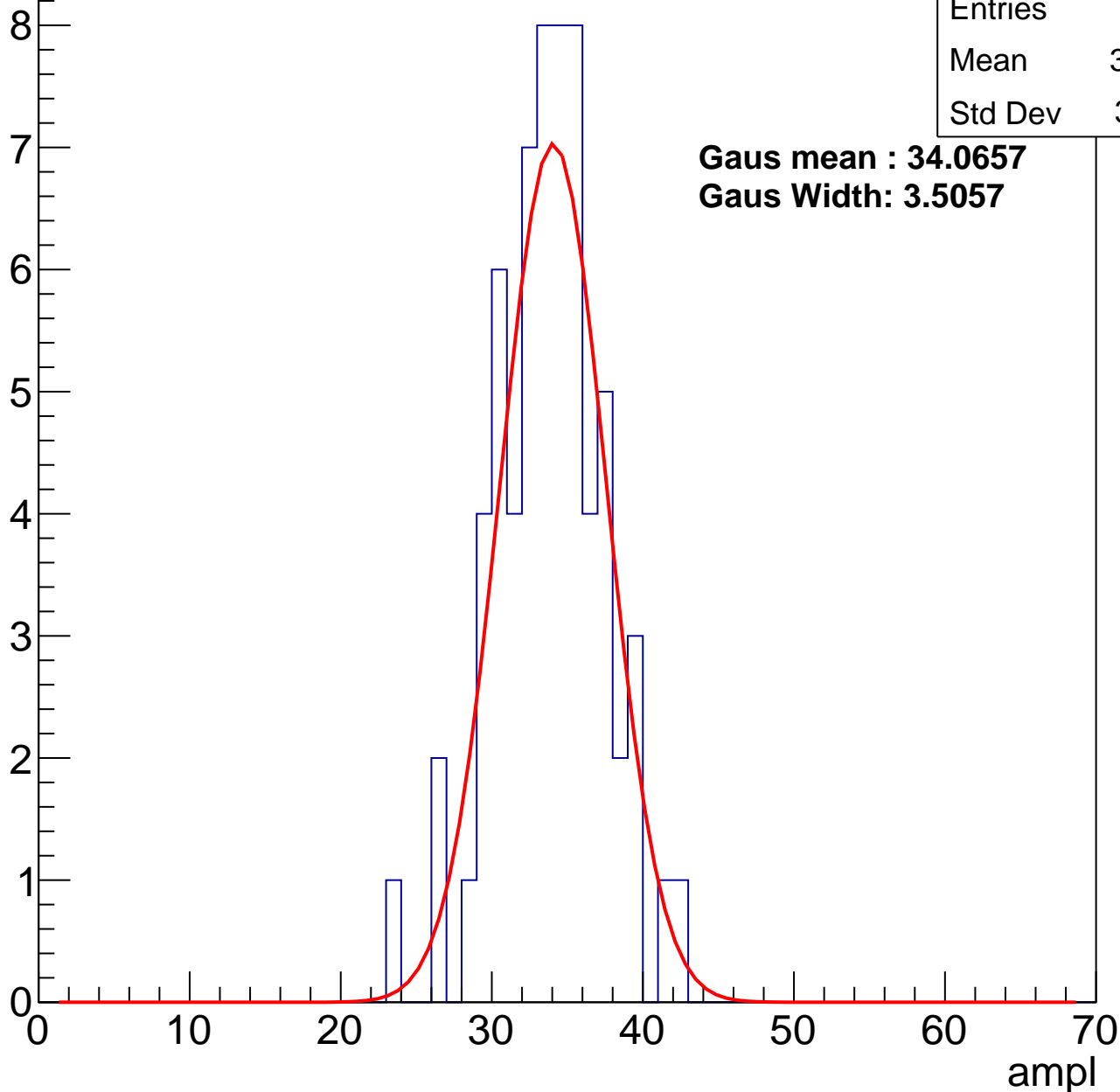
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	33.35
Std Dev	3.541

**Gaus mean : 34.0657**

**Gaus Width: 3.5057**



# B1L000S, U1-ch83, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	39.55
Std Dev	3.102

**Gaus mean : 39.9613**

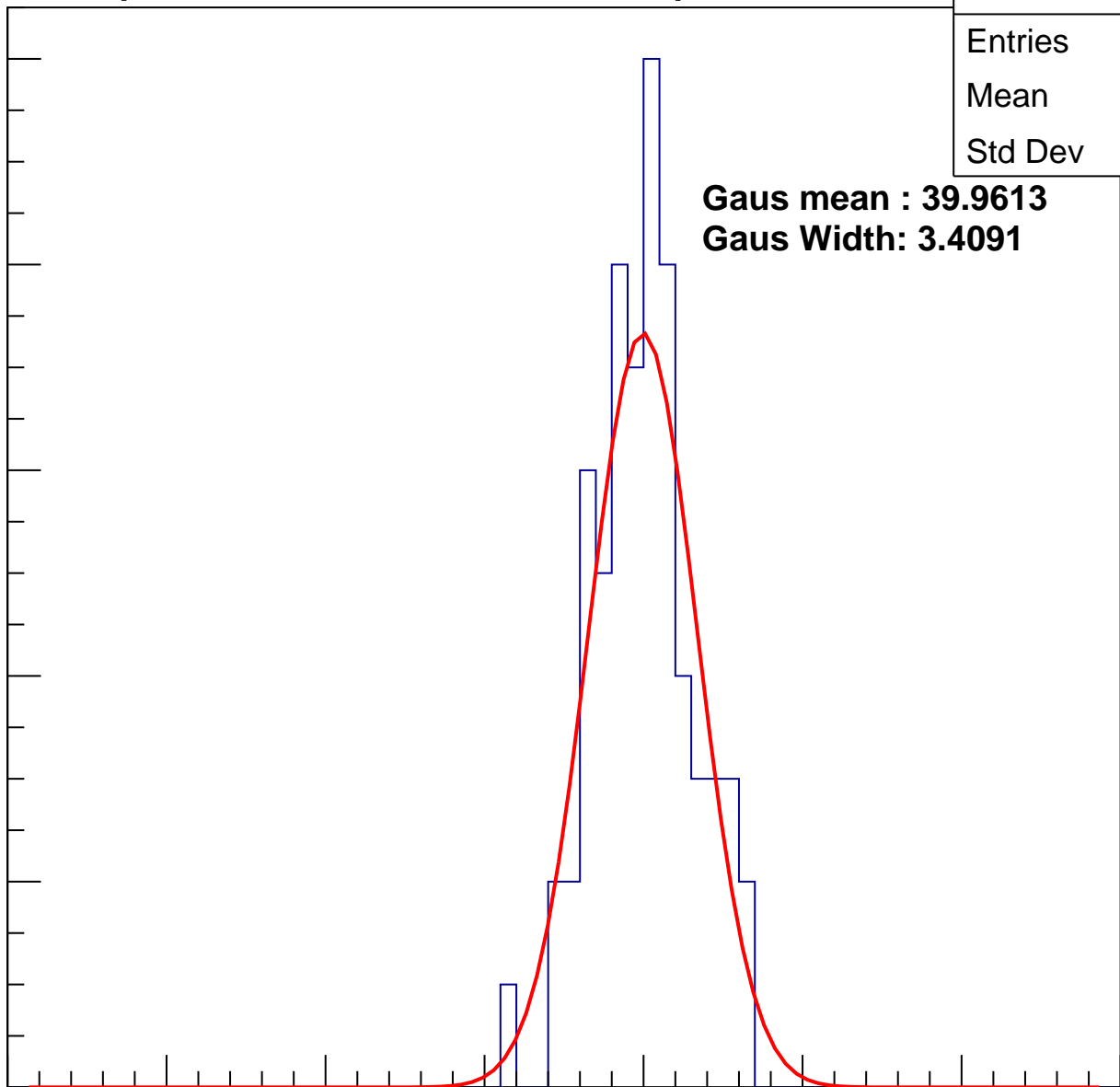
**Gaus Width: 3.4091**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

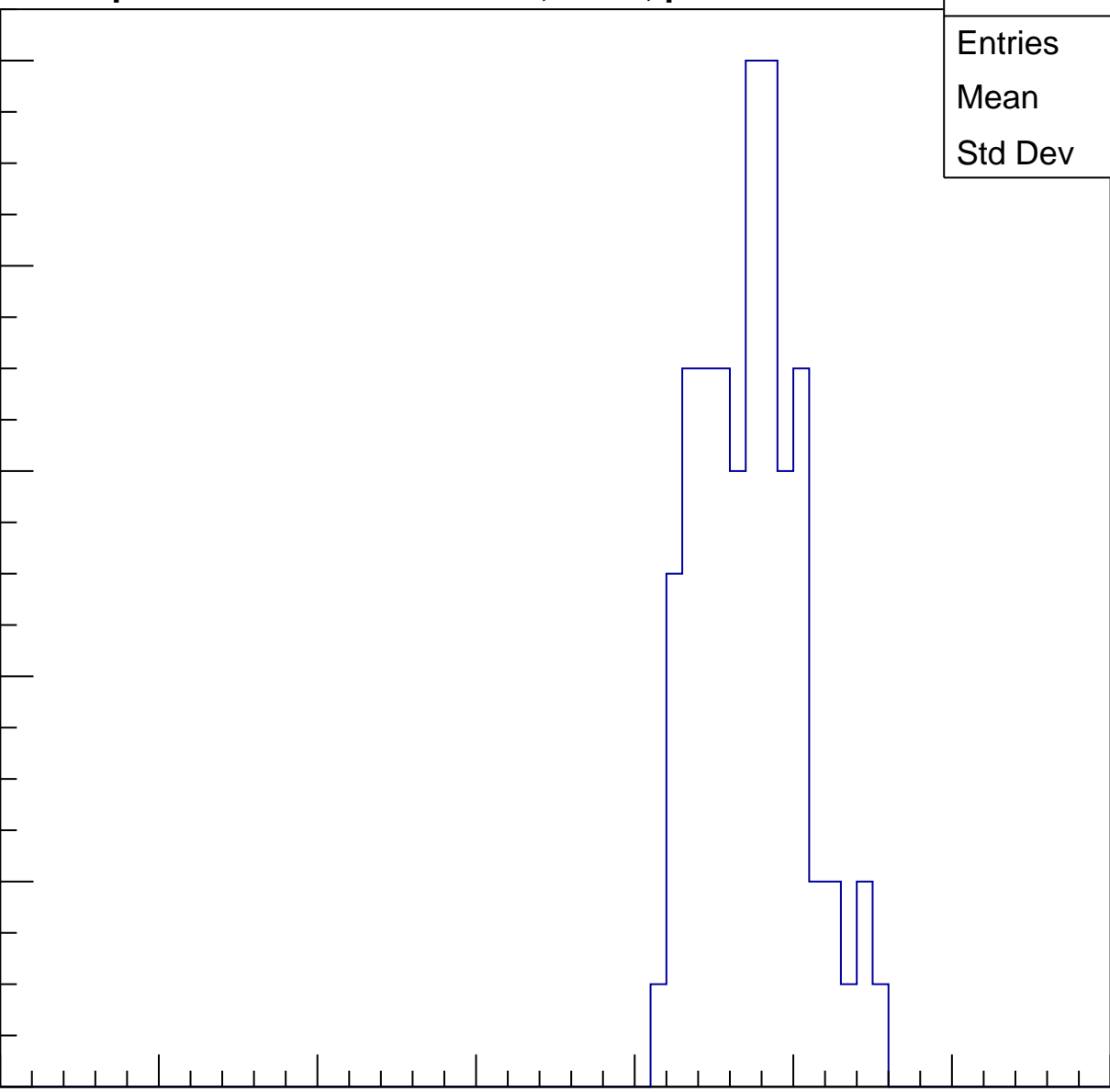
Entries	74
Mean	46.85
Std Dev	3.174

Entry

10  
8  
6  
4  
2  
0

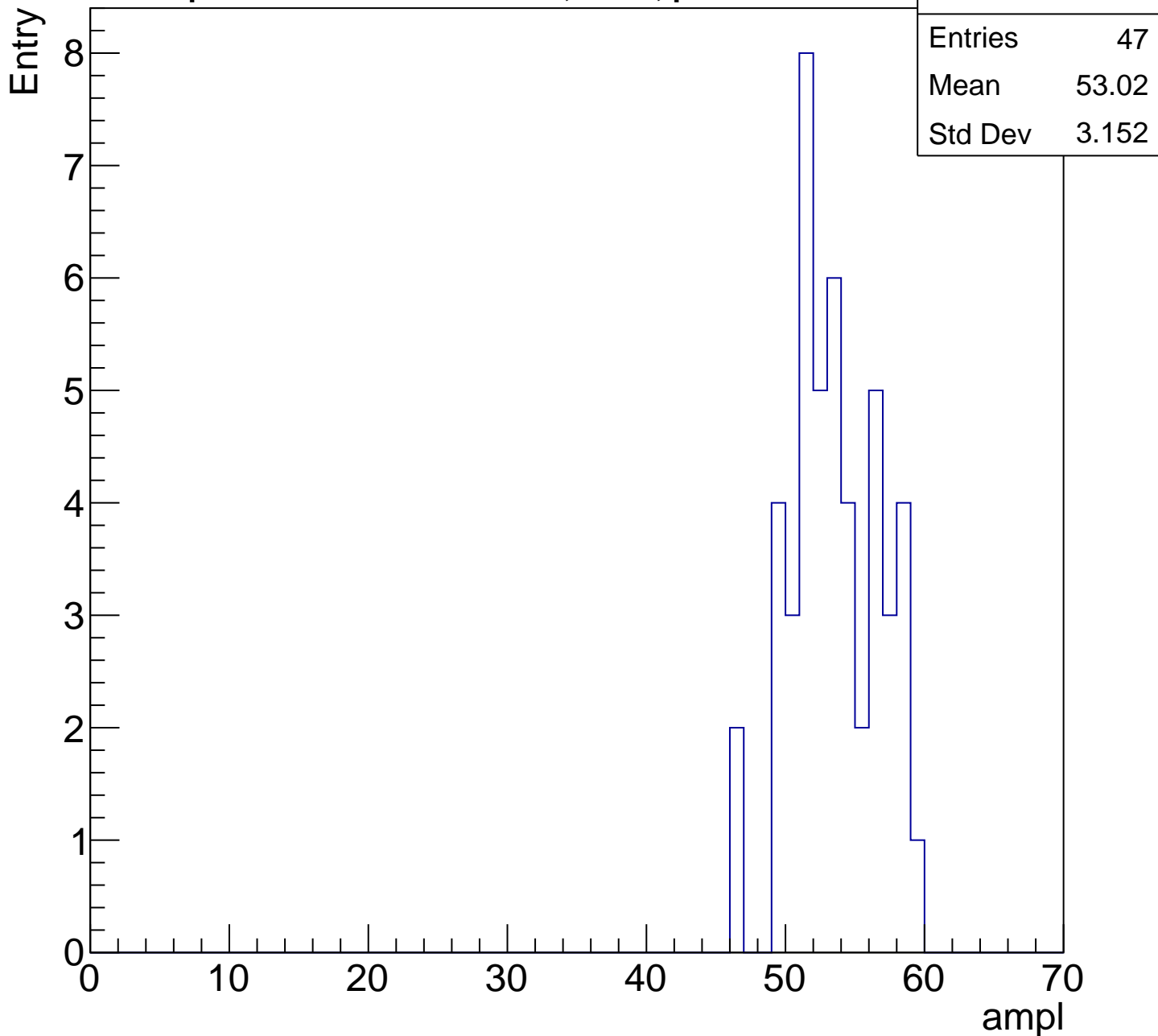
0 10 20 30 40 50 60 70

ampl



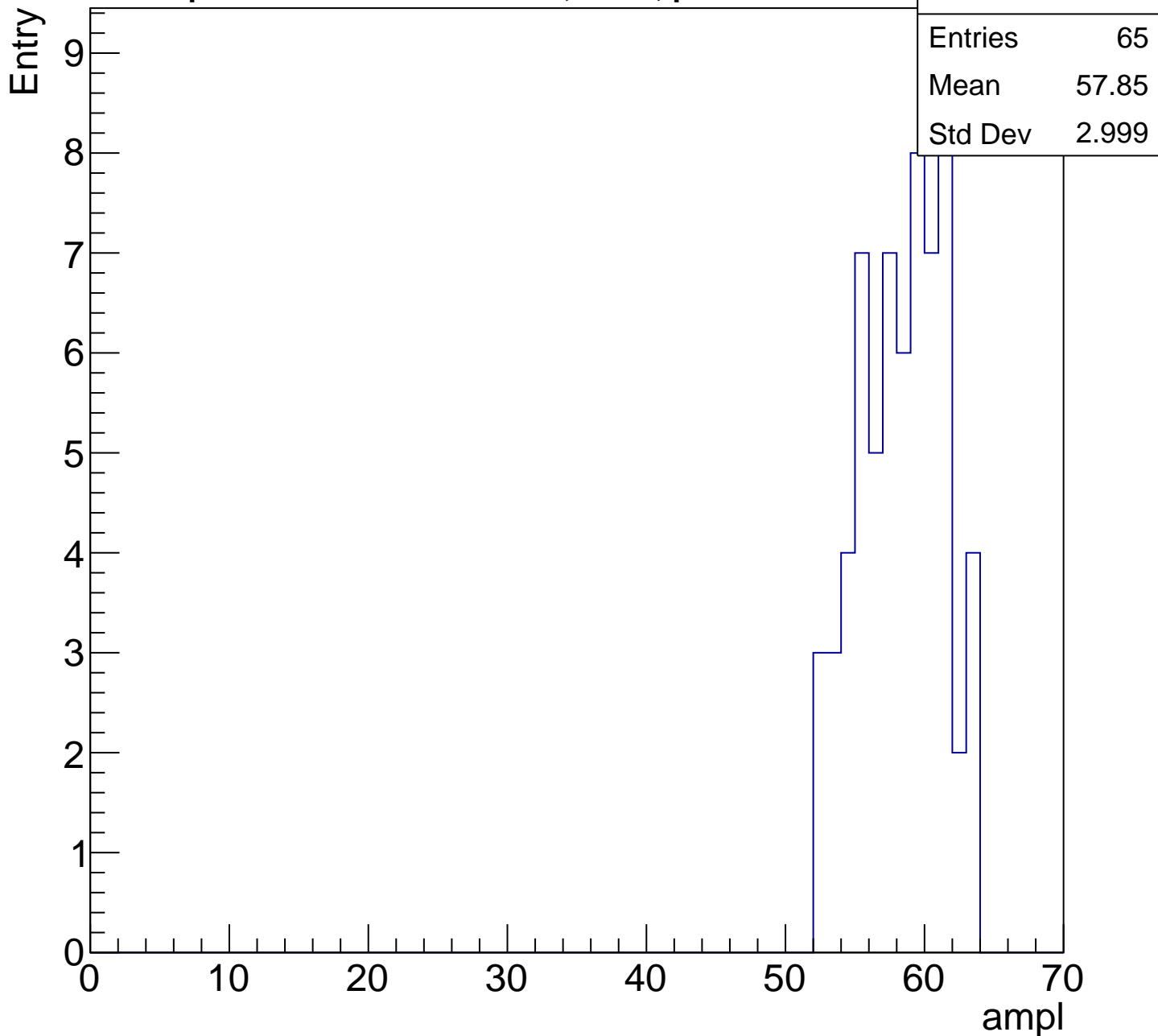
# B1L000S, U1-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

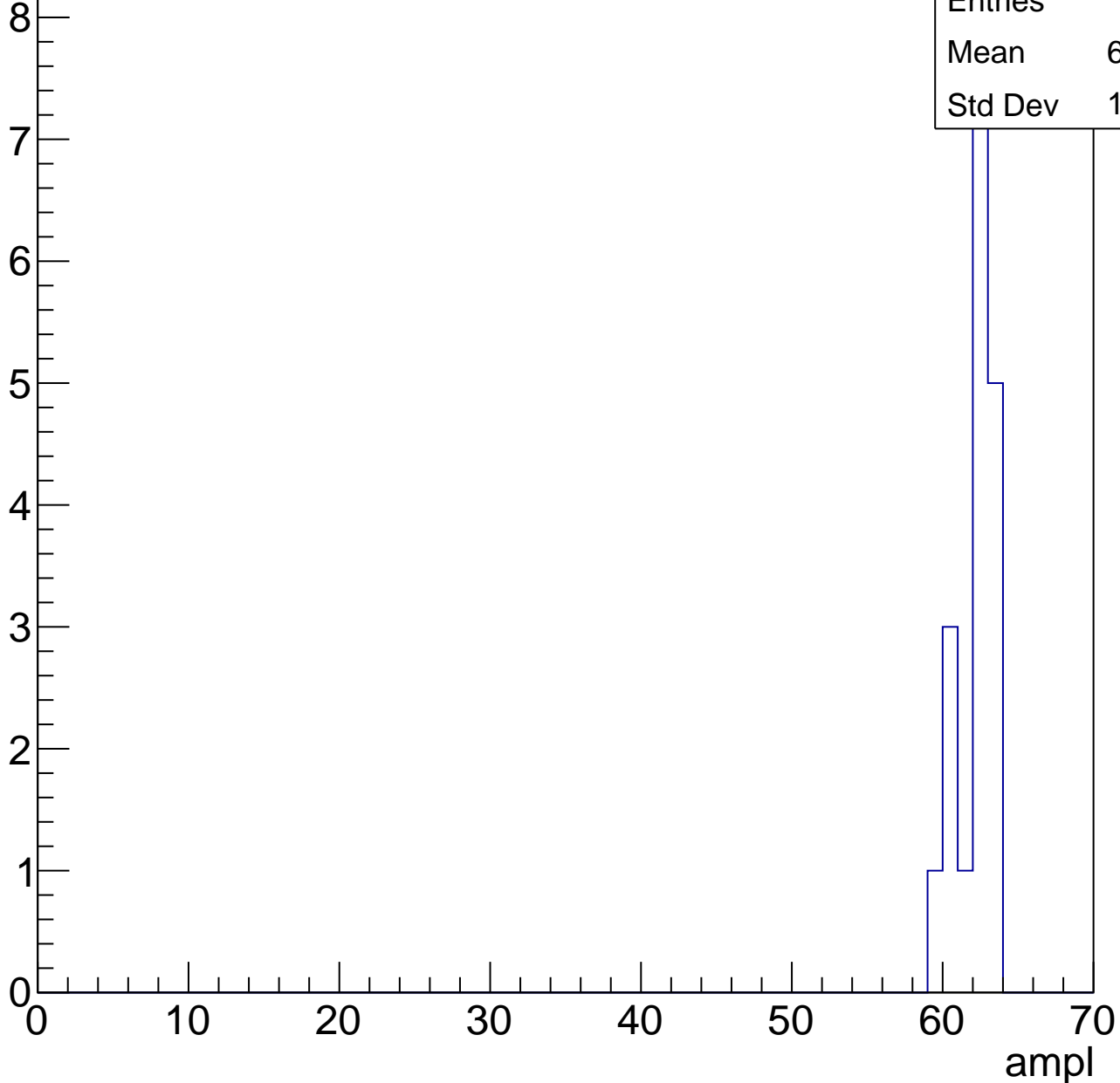


# B1L000S, U1-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.72
Std Dev	1.193

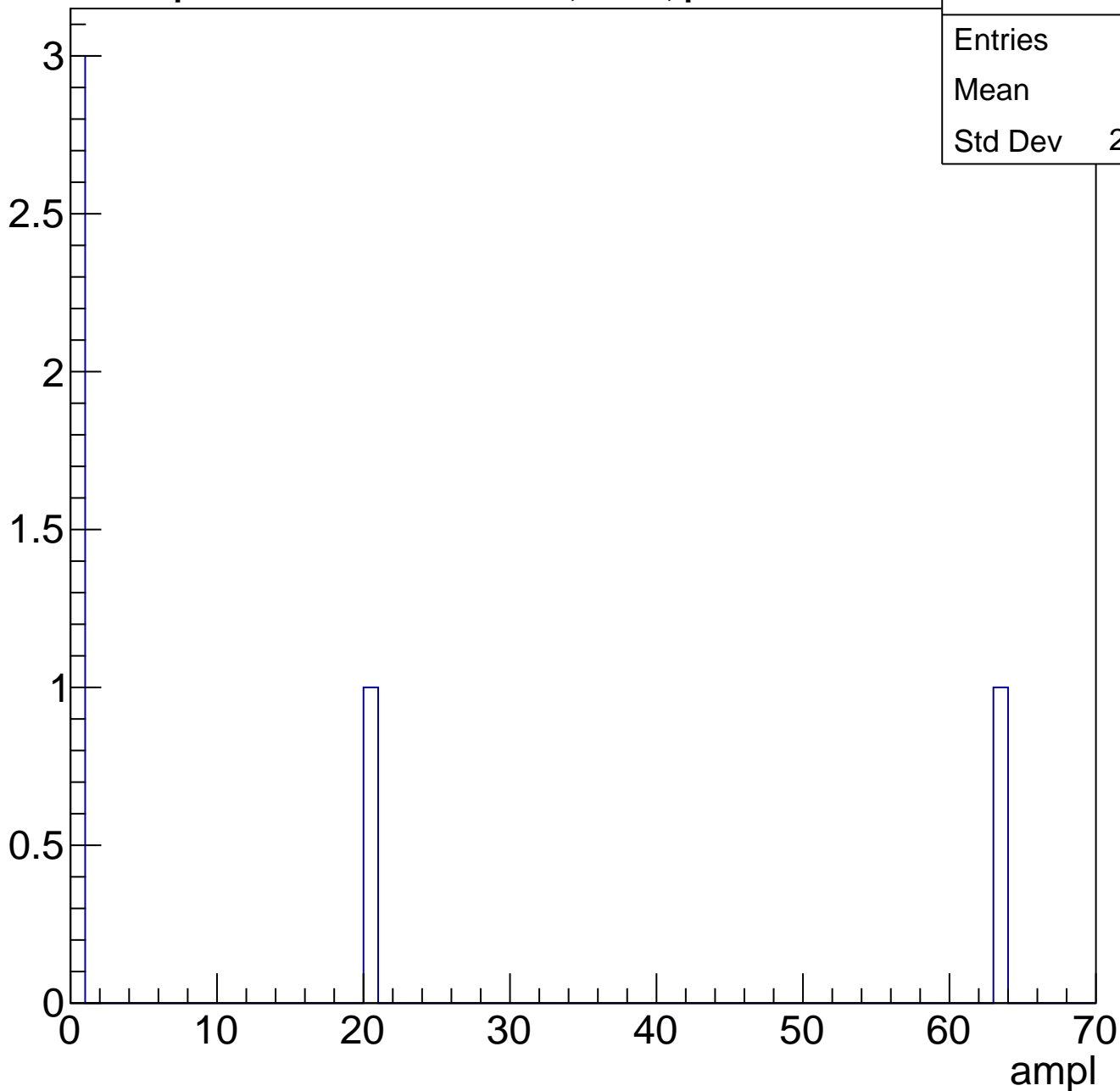




# B1L000S, U1-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	16.6
Std Dev	24.46

# B1L000S, U1-ch84, adc0

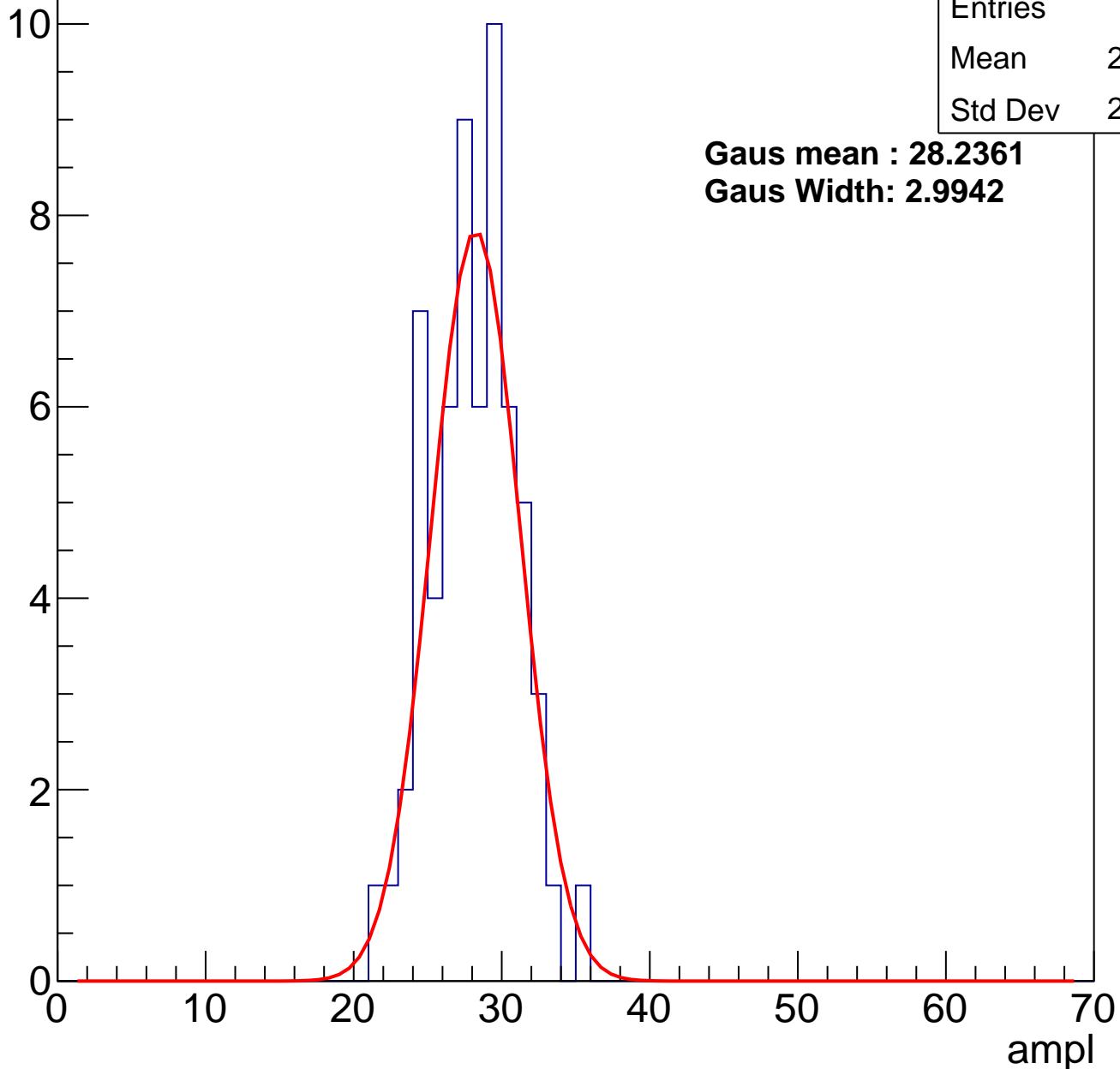
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	62
Mean	27.63
Std Dev	2.864

**Gaus mean : 28.2361**

**Gaus Width: 2.9942**

Entry



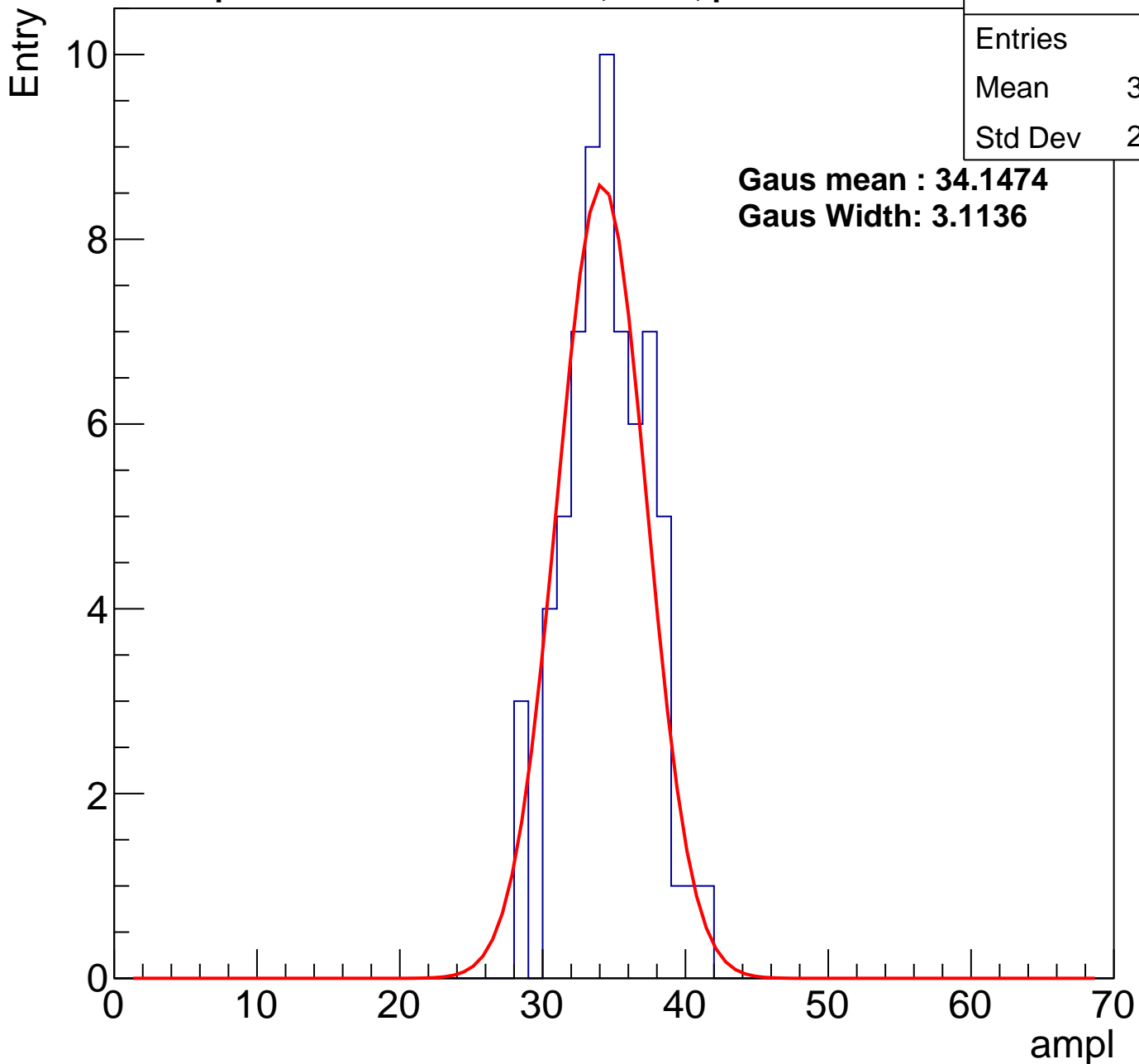
# B1L000S, U1-ch84, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	34.09
Std Dev	2.854

**Gaus mean : 34.1474**

**Gaus Width: 3.1136**



# B1L000S, U1-ch84, adc2

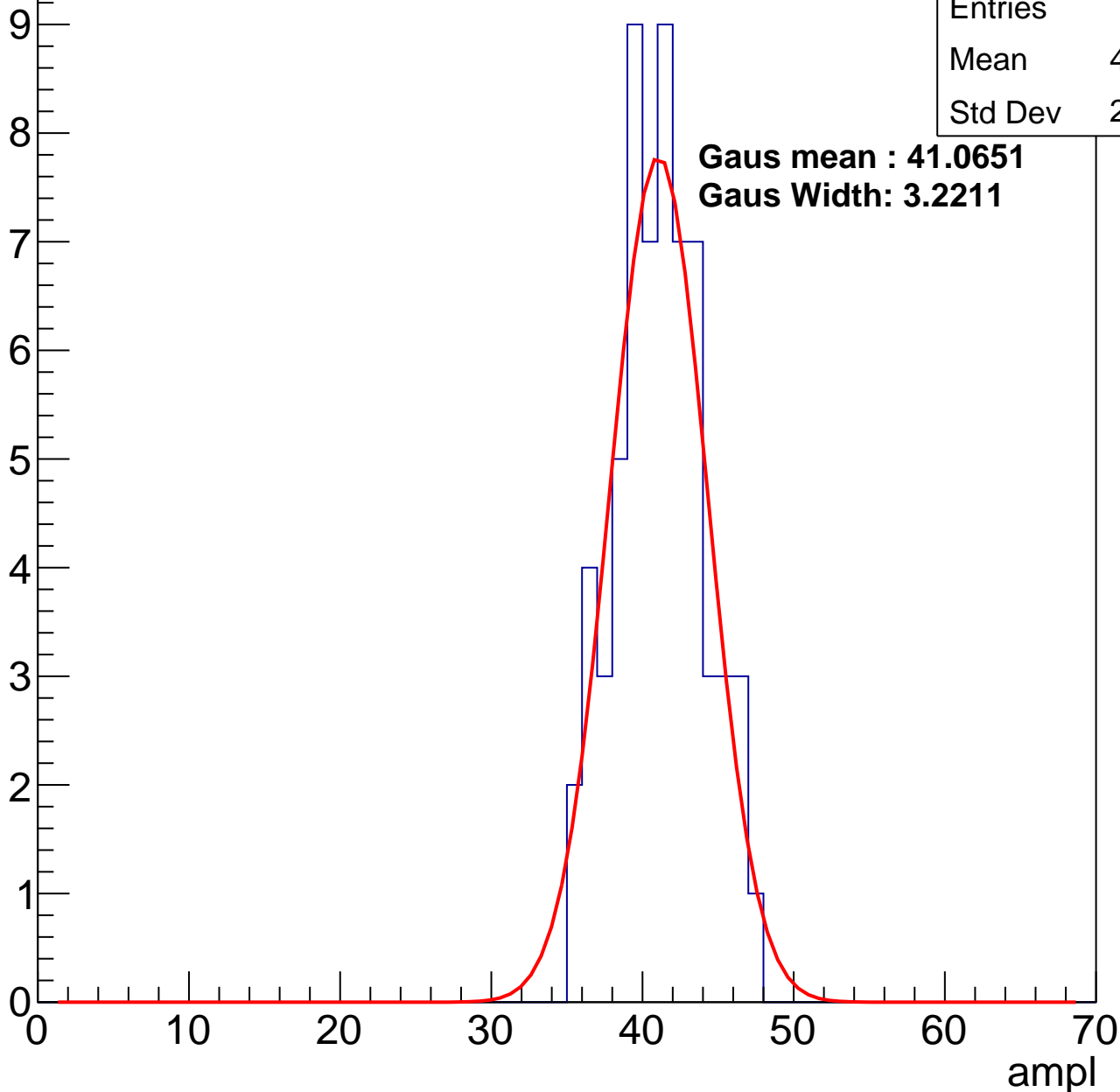
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	40.67
Std Dev	2.878

**Gaus mean : 41.0651**

**Gaus Width: 3.2211**

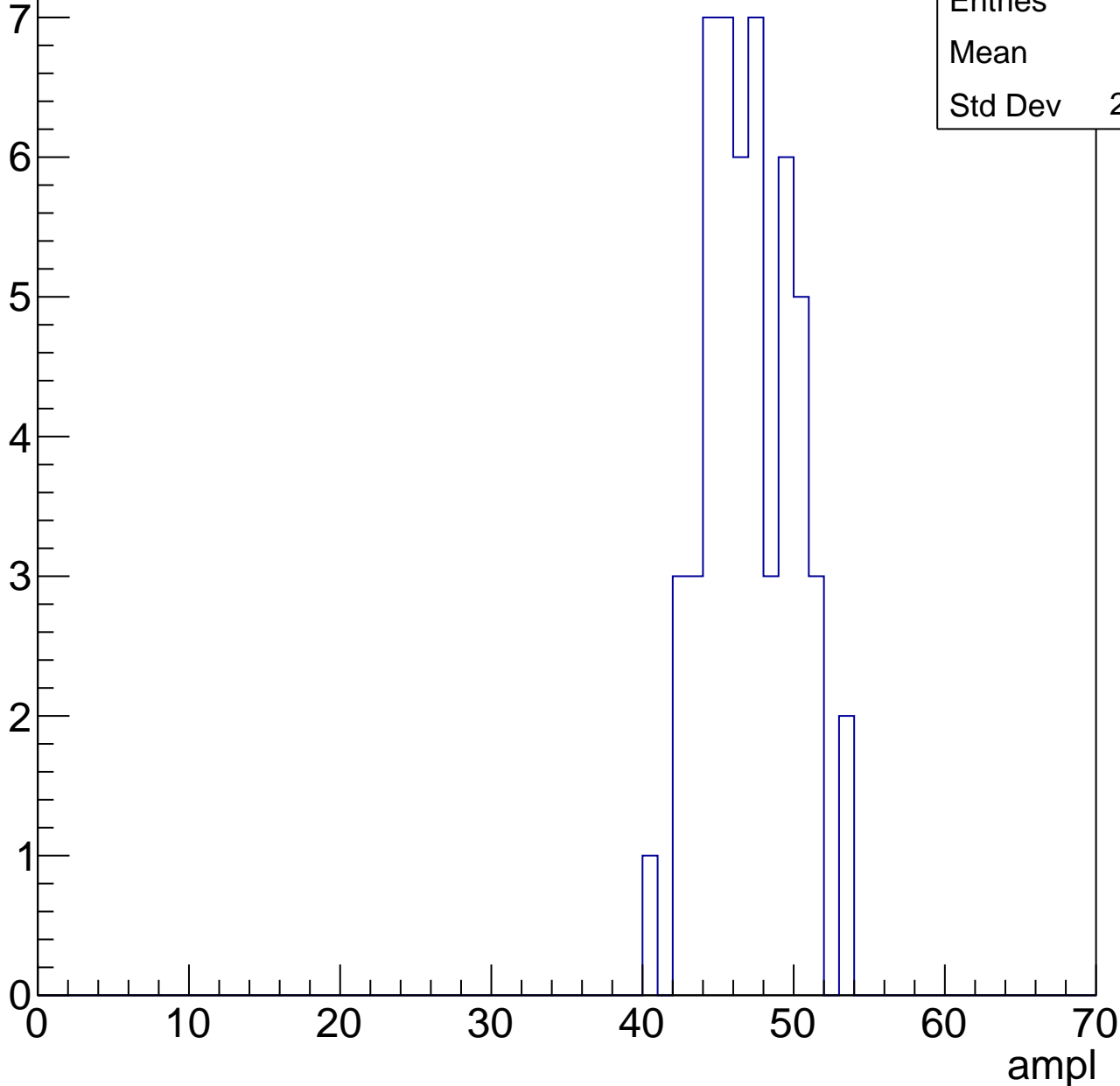


# B1L000S, U1-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	46.6
Std Dev	2.923

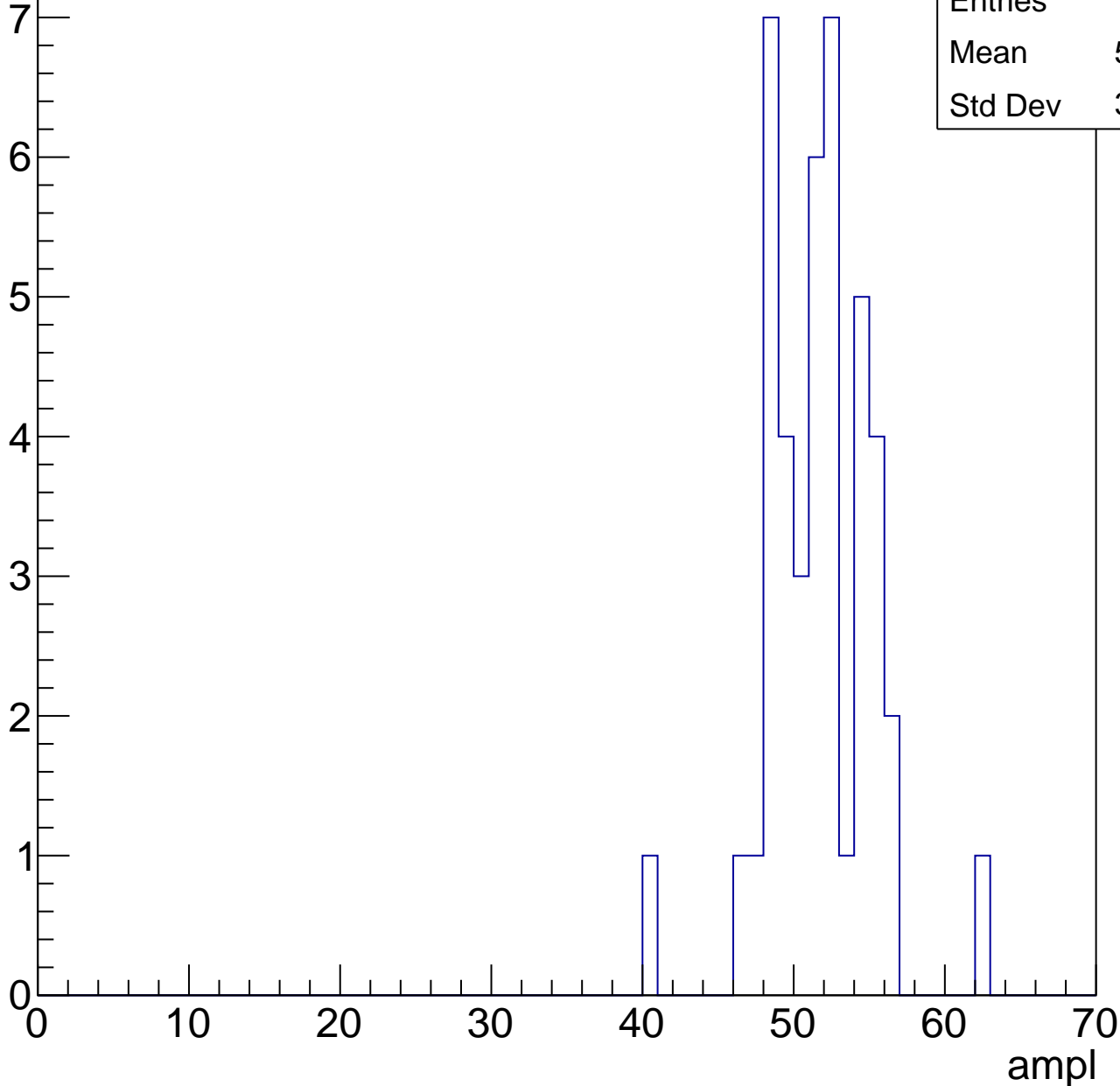


# B1L000S, U1-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

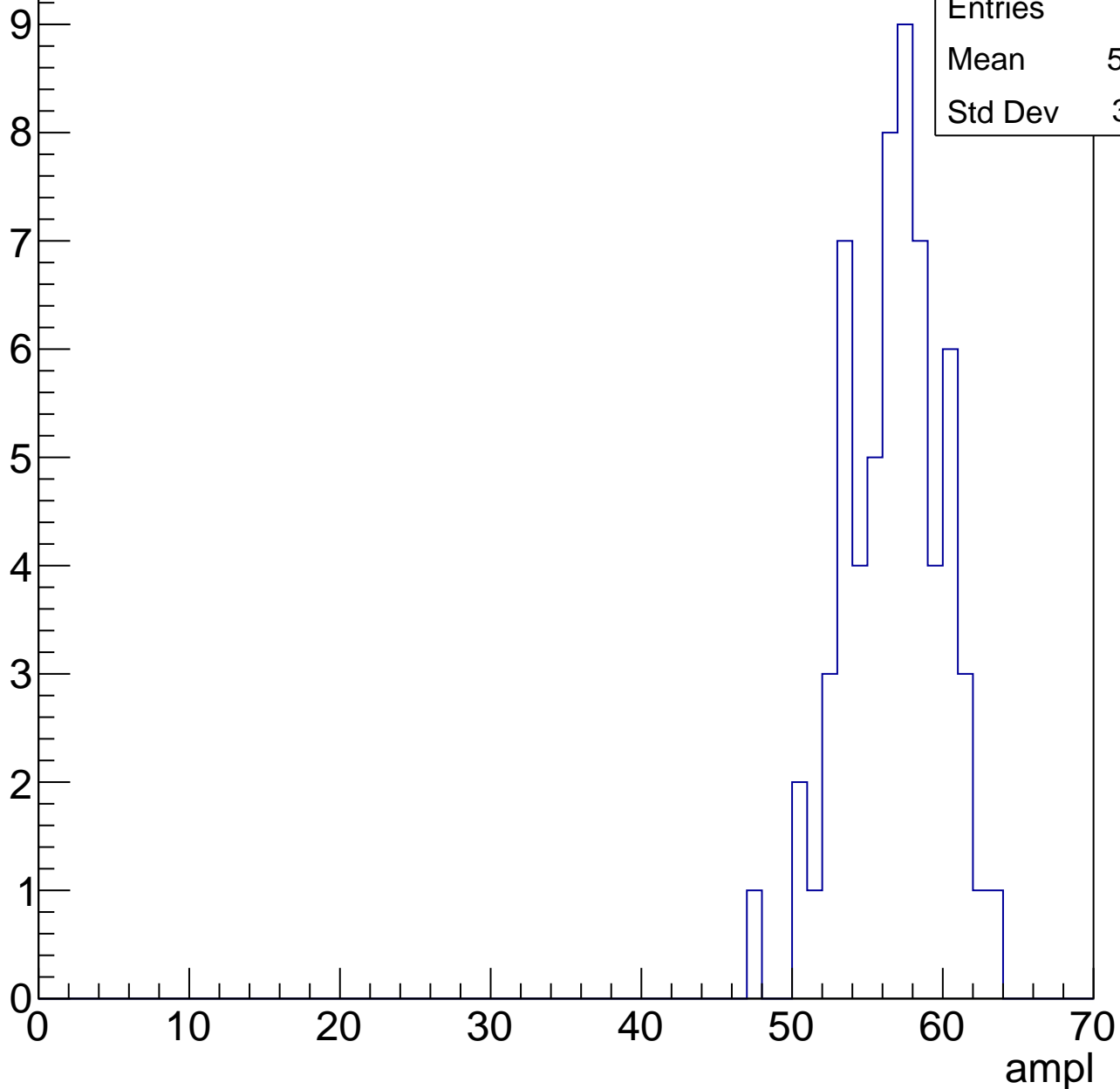
Entries	43
Mean	51.21
Std Dev	3.521



# B1L000S, U1-ch84, adc5

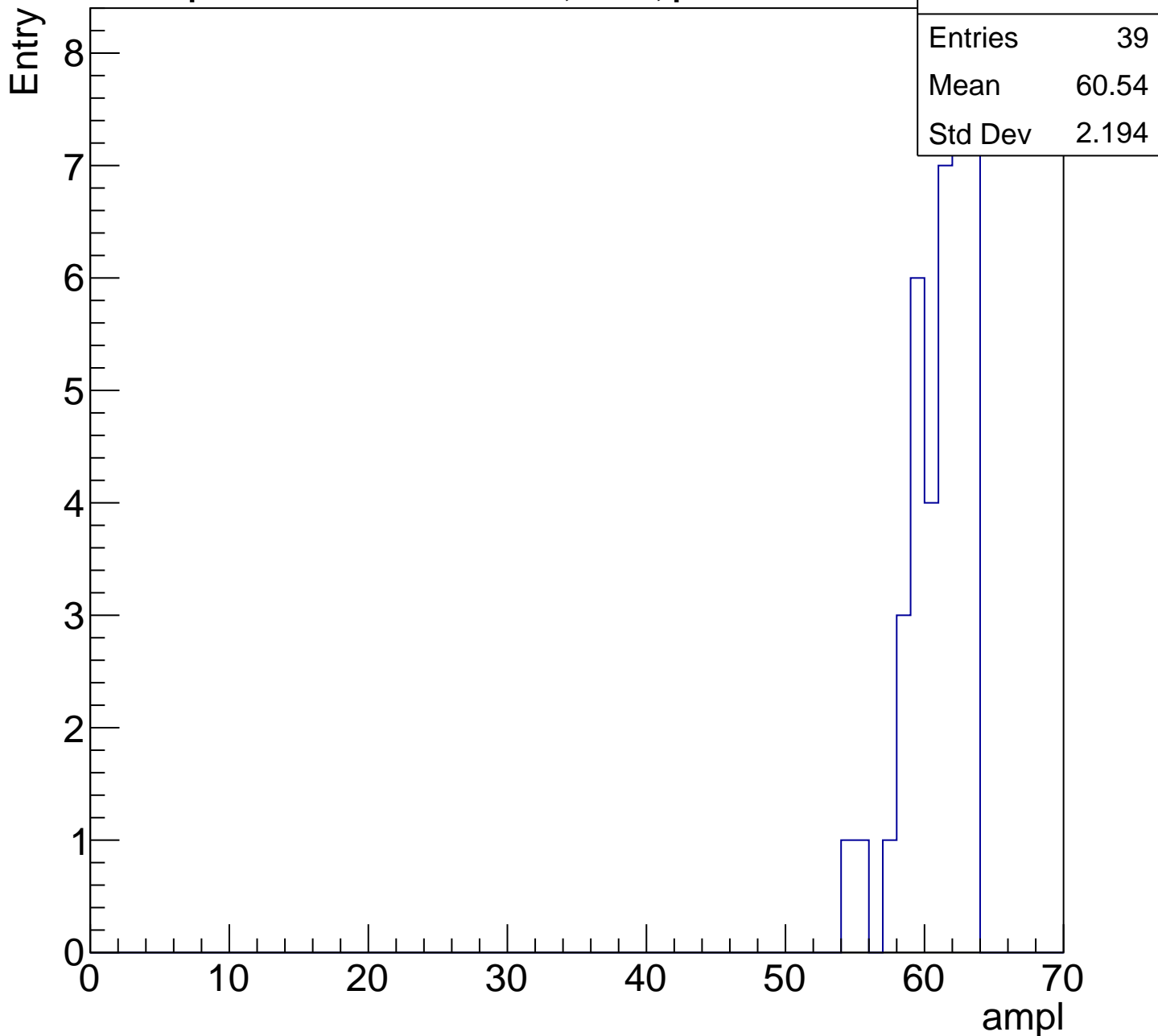
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

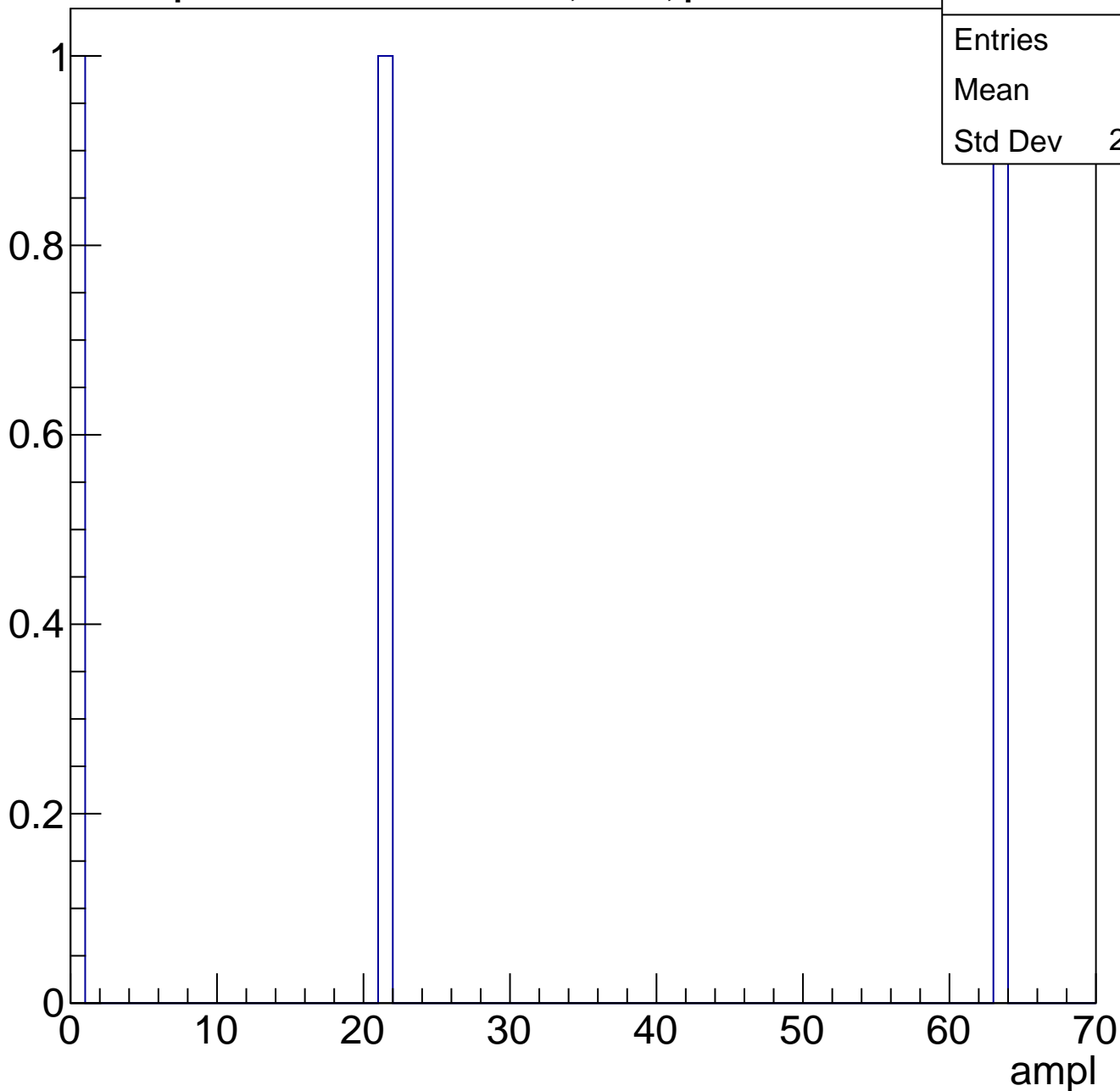




# B1L000S, U1-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch85, adc0

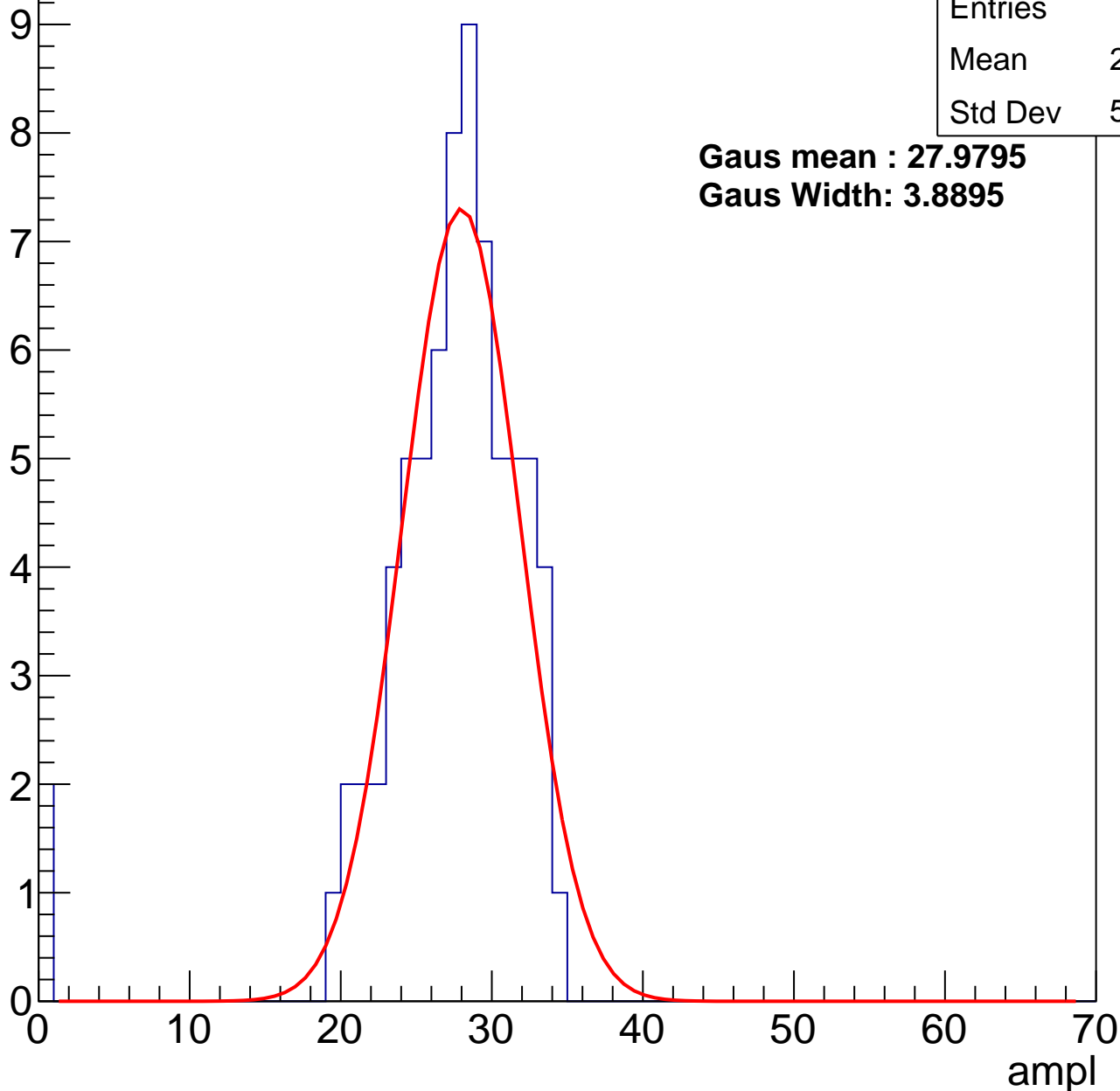
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	26.58
Std Dev	5.666

**Gaus mean : 27.9795**

**Gaus Width: 3.8895**



# B1L000S, U1-ch85, adc1

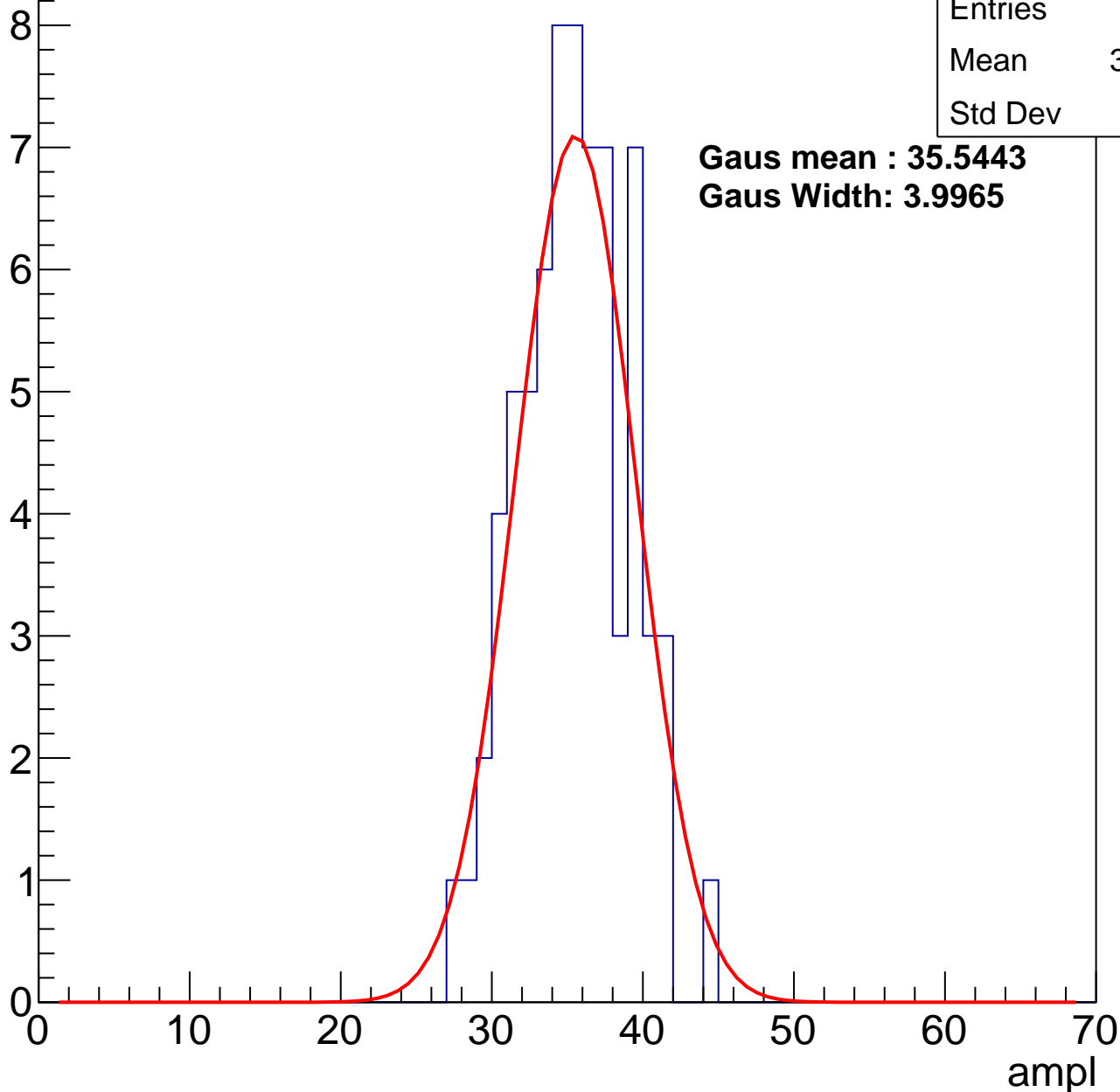
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	34.97
Std Dev	3.52

**Gaus mean : 35.5443**

**Gaus Width: 3.9965**



# B1L000S, U1-ch85, adc2

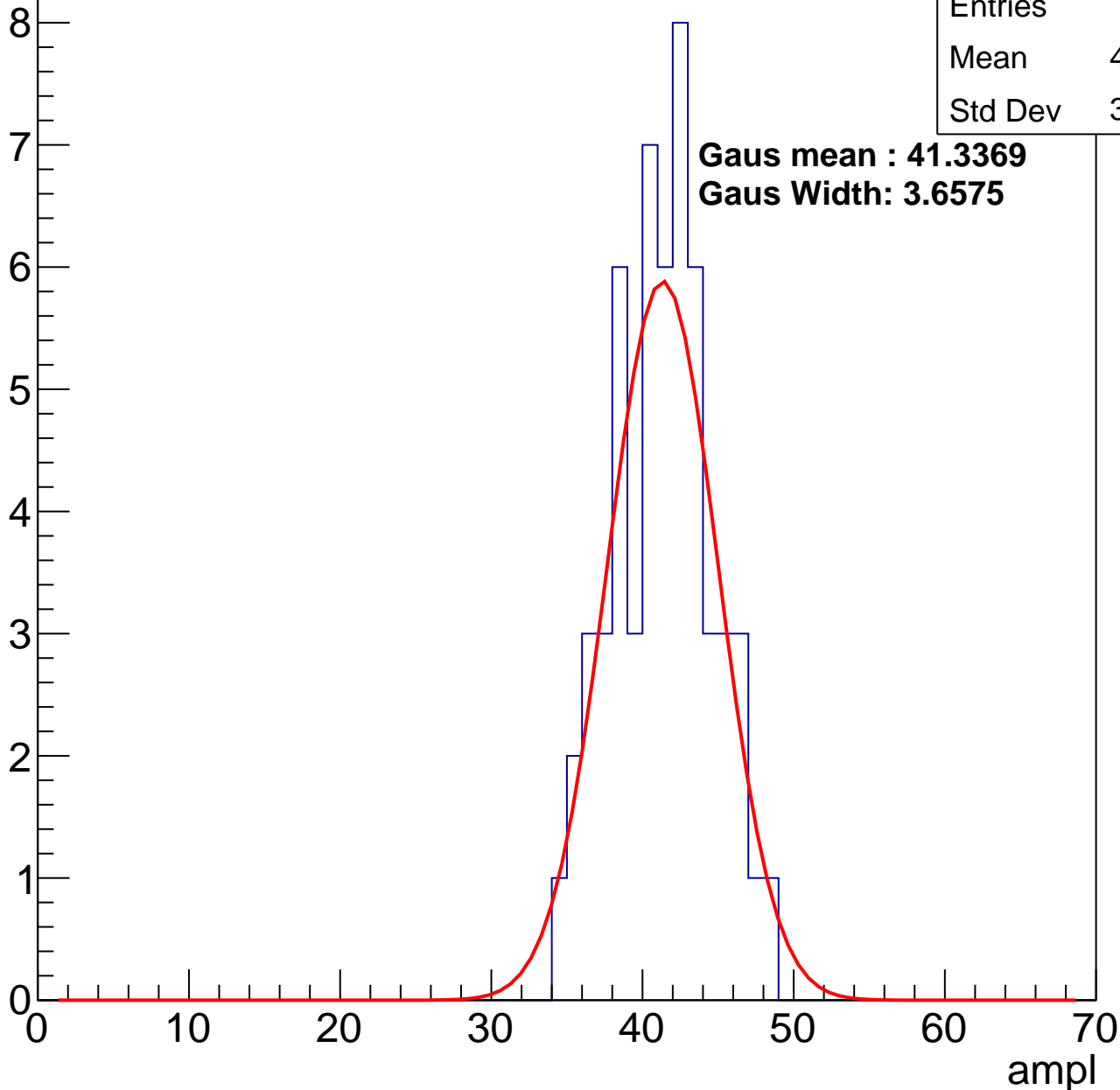
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	40.86
Std Dev	3.226

**Gaus mean : 41.3369**

**Gaus Width: 3.6575**

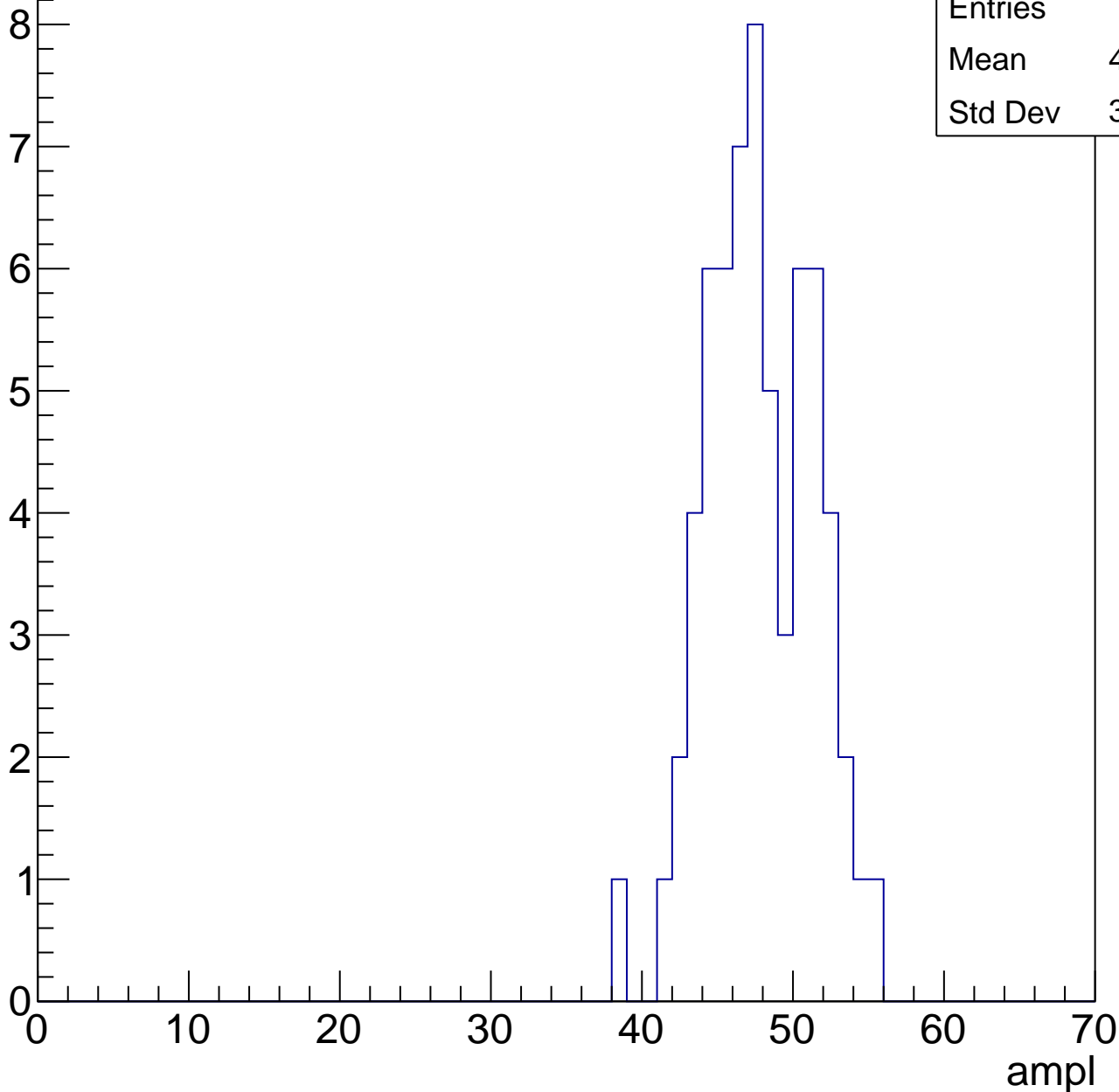


# B1L000S, U1-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	47.35
Std Dev	3.474

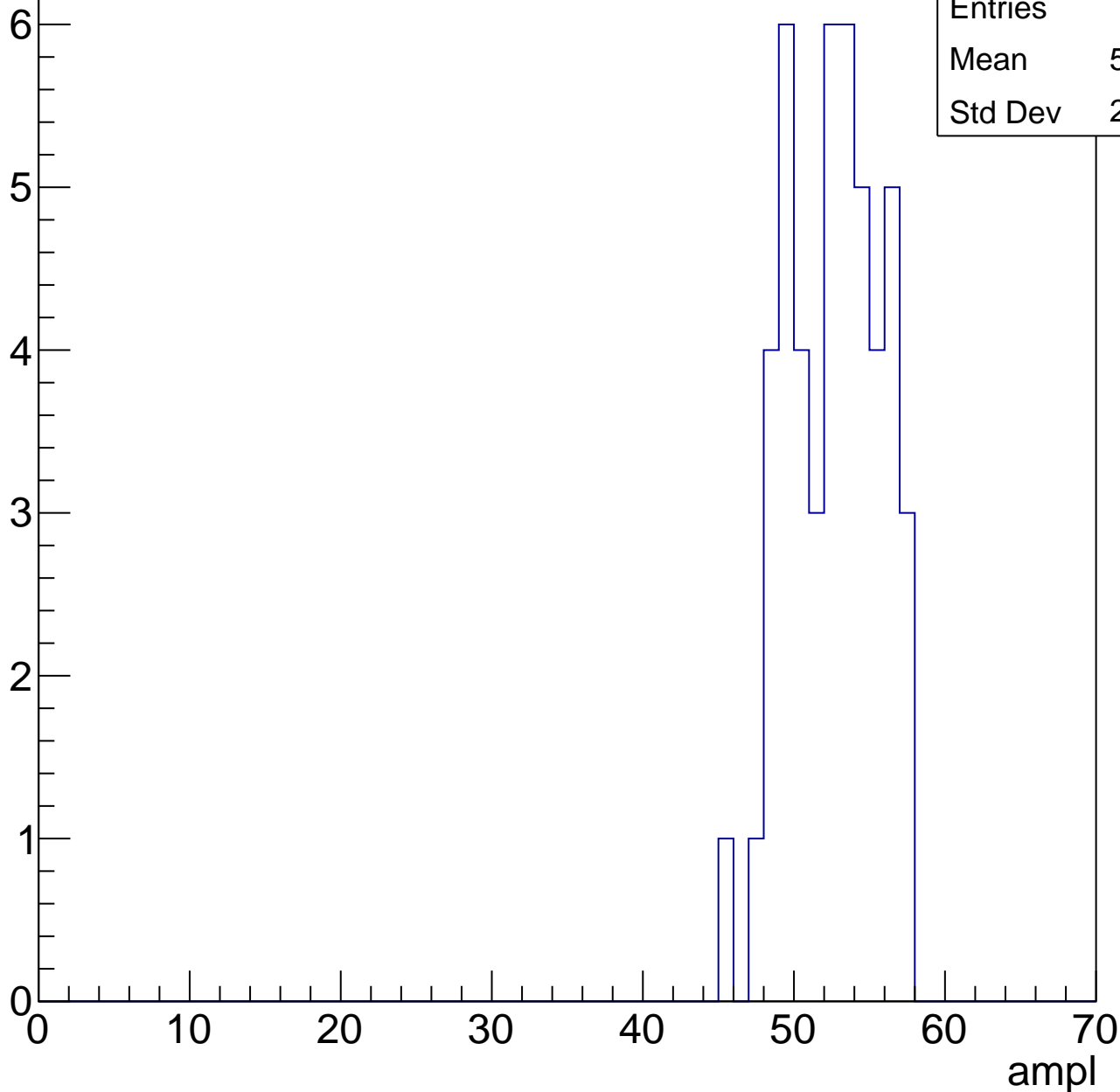


# B1L000S, U1-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

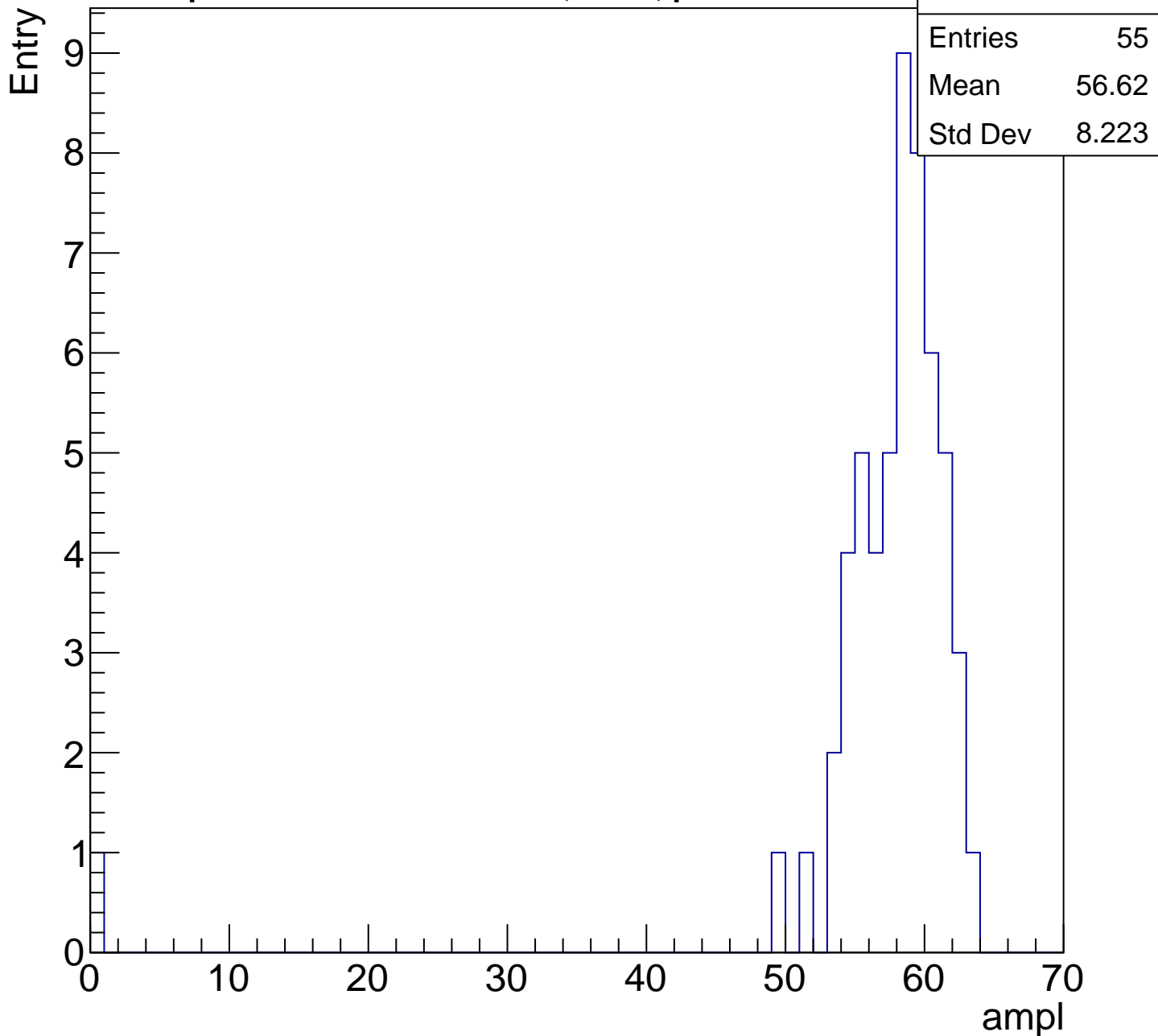
Entry

Entries	48
Mean	52.12
Std Dev	2.983



# B1L000S, U1-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

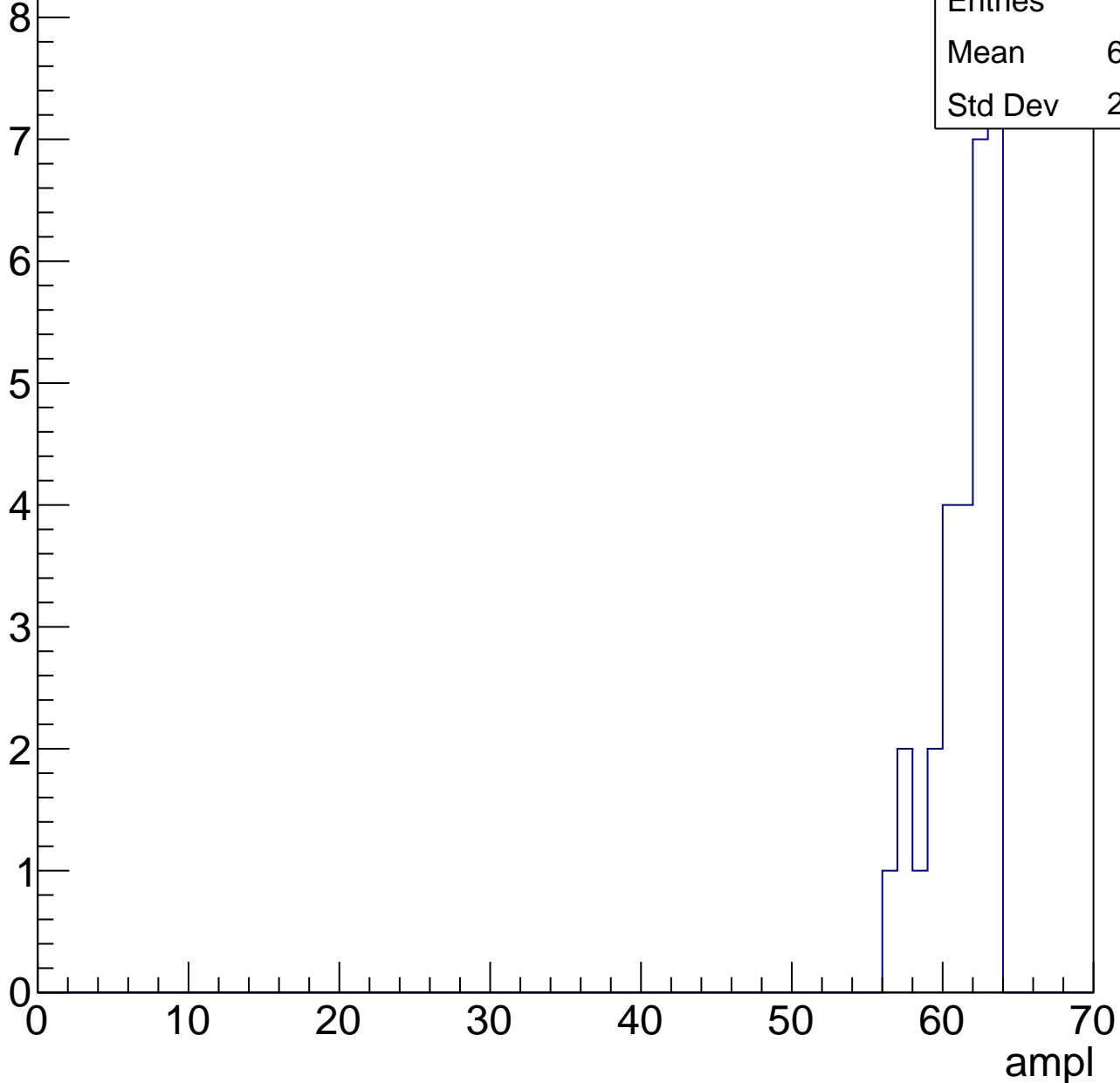


# B1L000S, U1-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	60.97
Std Dev	2.008

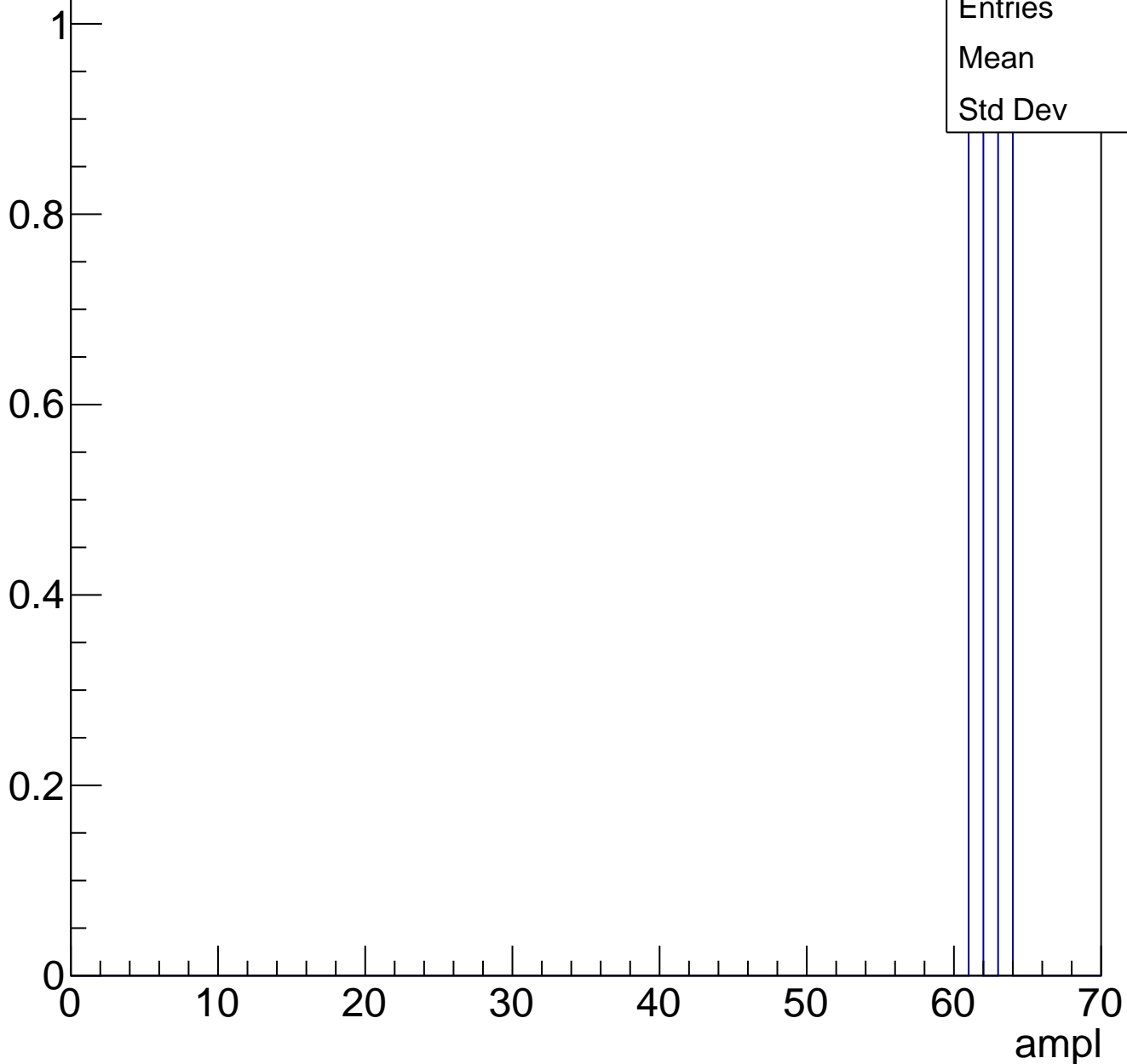




# B1L000S, U1-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch86, adc0

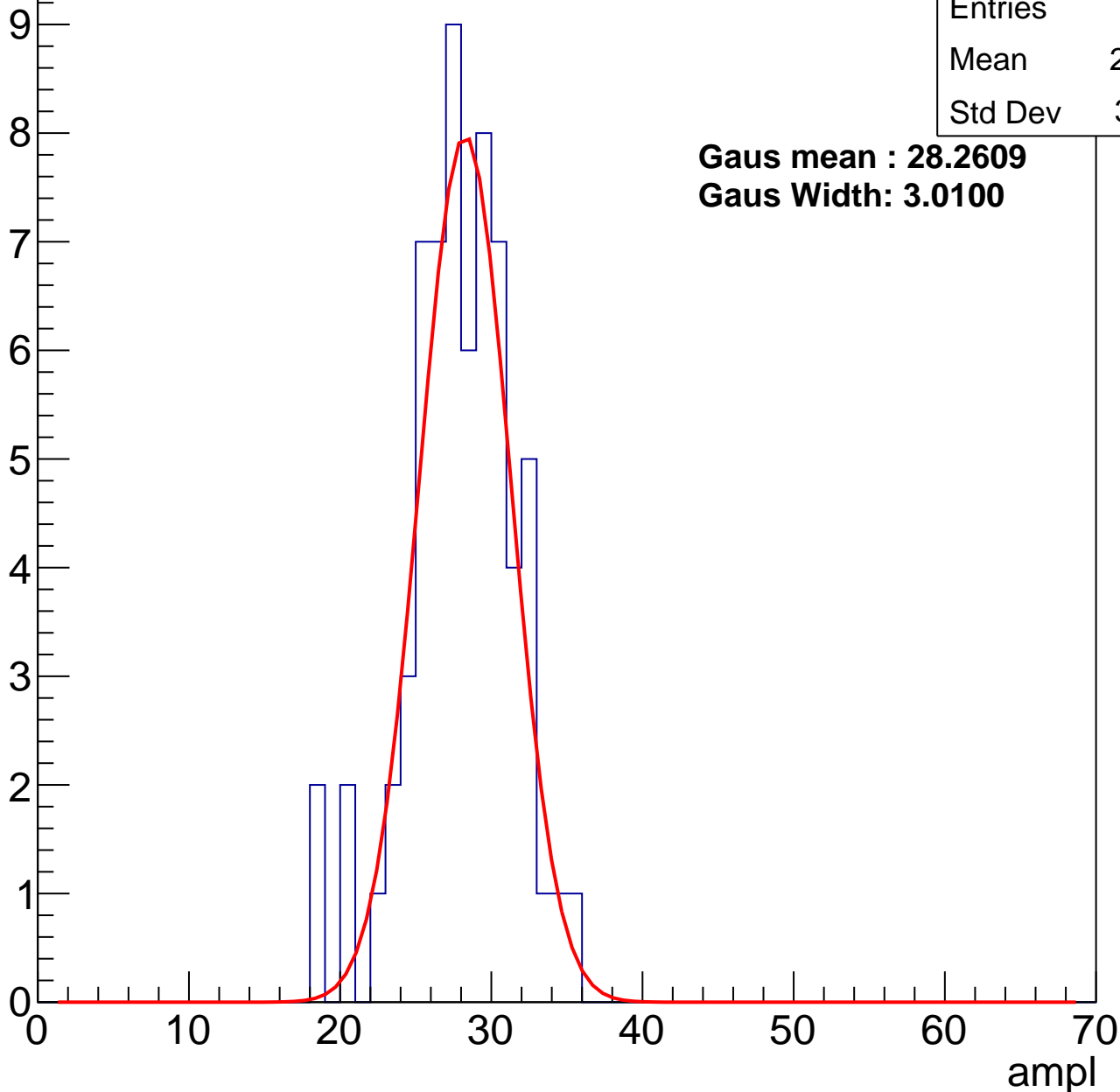
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	27.45
Std Dev	3.491

**Gaus mean : 28.2609**

**Gaus Width: 3.0100**



# B1L000S, U1-ch86, adc1

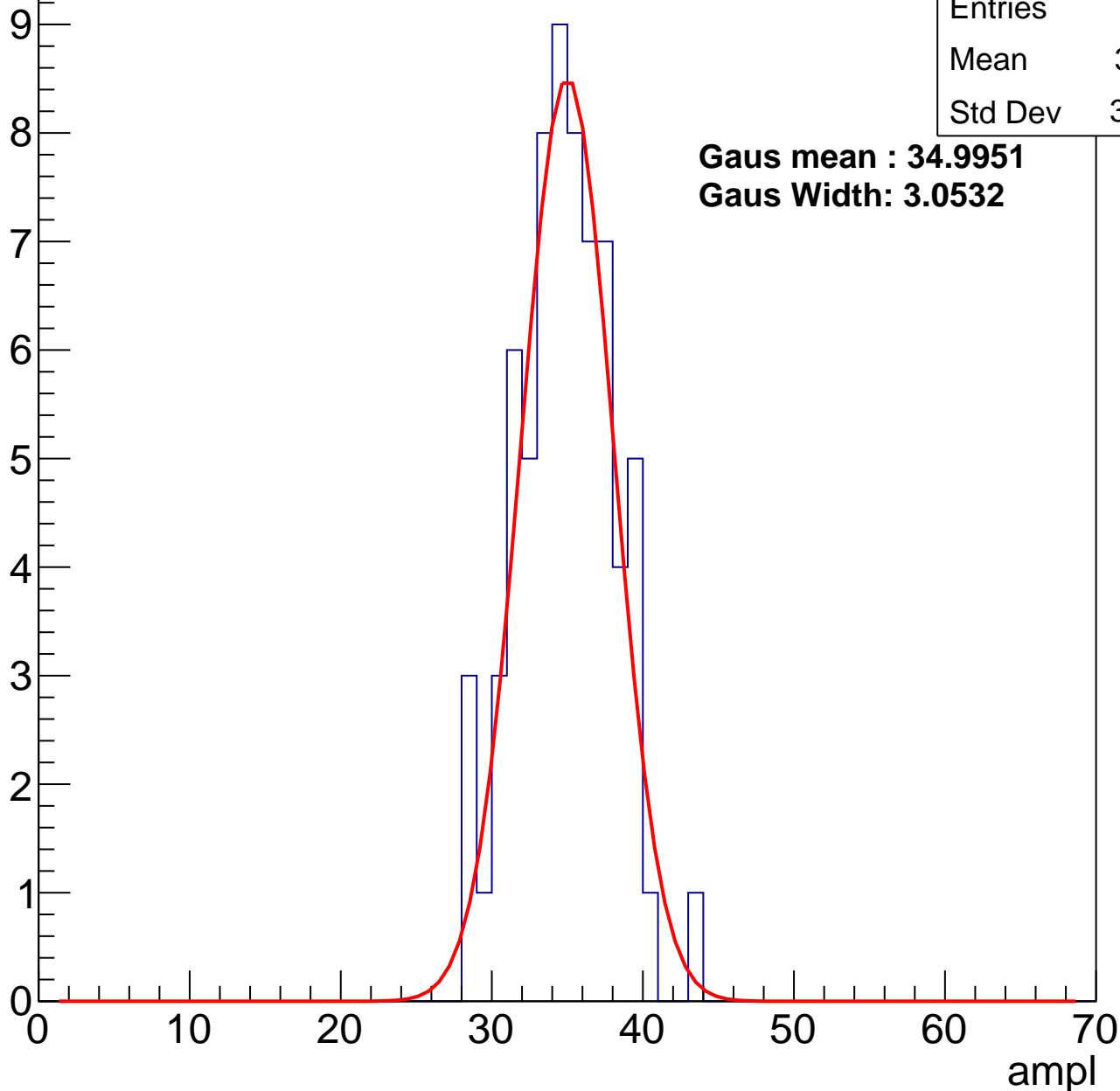
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	34.41
Std Dev	3.107

**Gaus mean : 34.9951**

**Gaus Width: 3.0532**



# B1L000S, U1-ch86, adc2

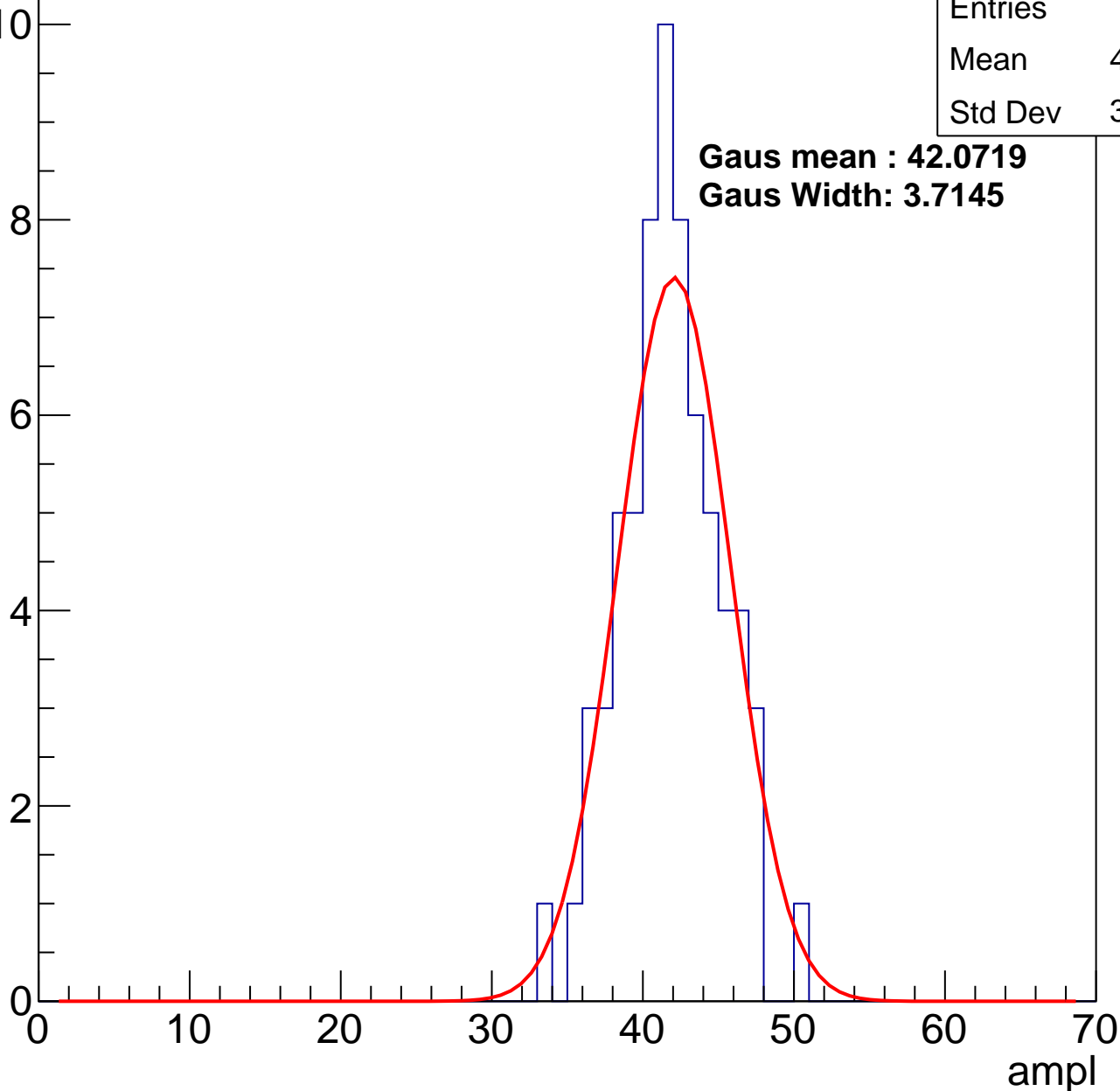
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	41.36
Std Dev	3.272

**Gaus mean : 42.0719**

**Gaus Width: 3.7145**

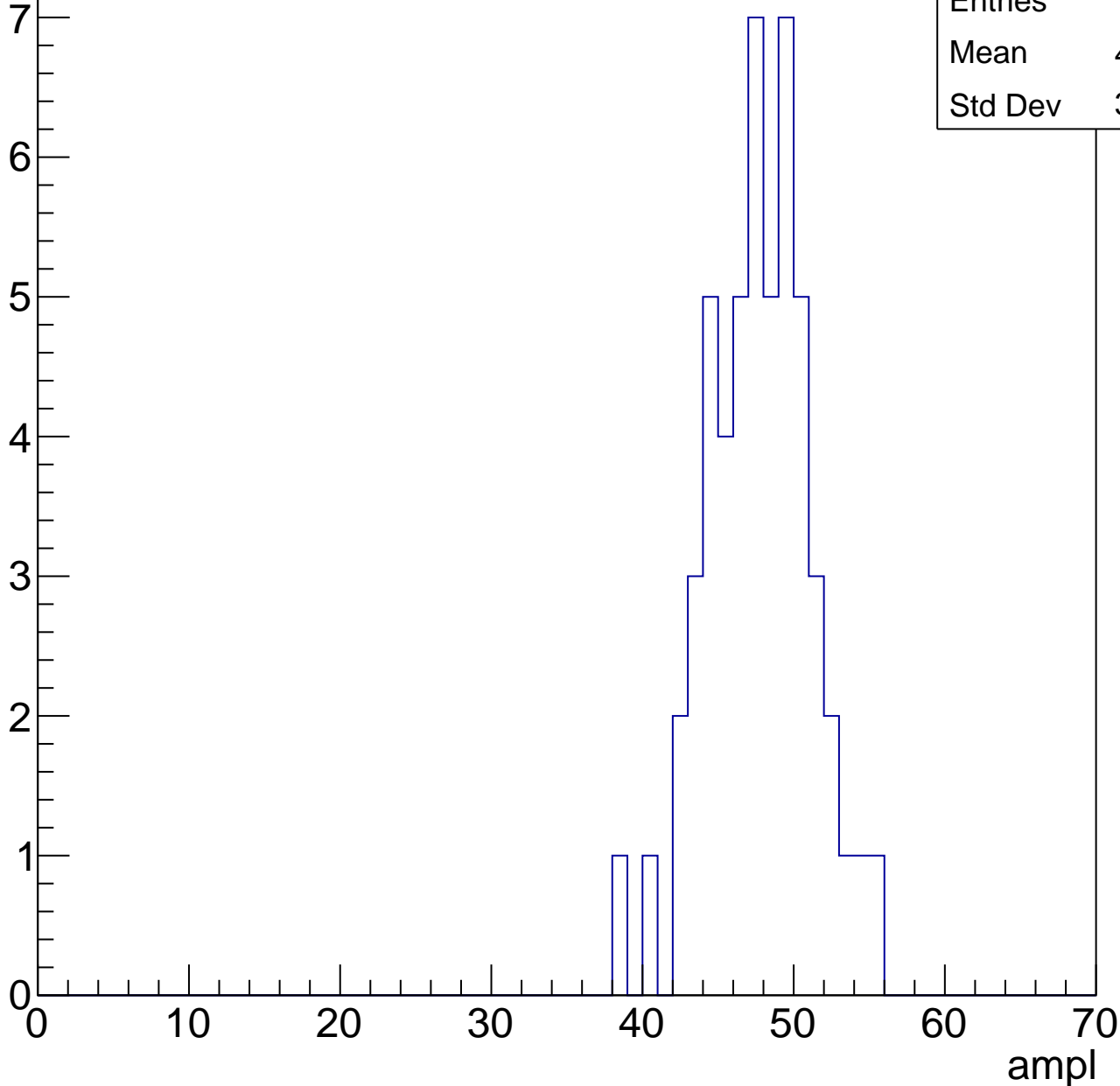


# B1L000S, U1-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	47.21
Std Dev	3.411

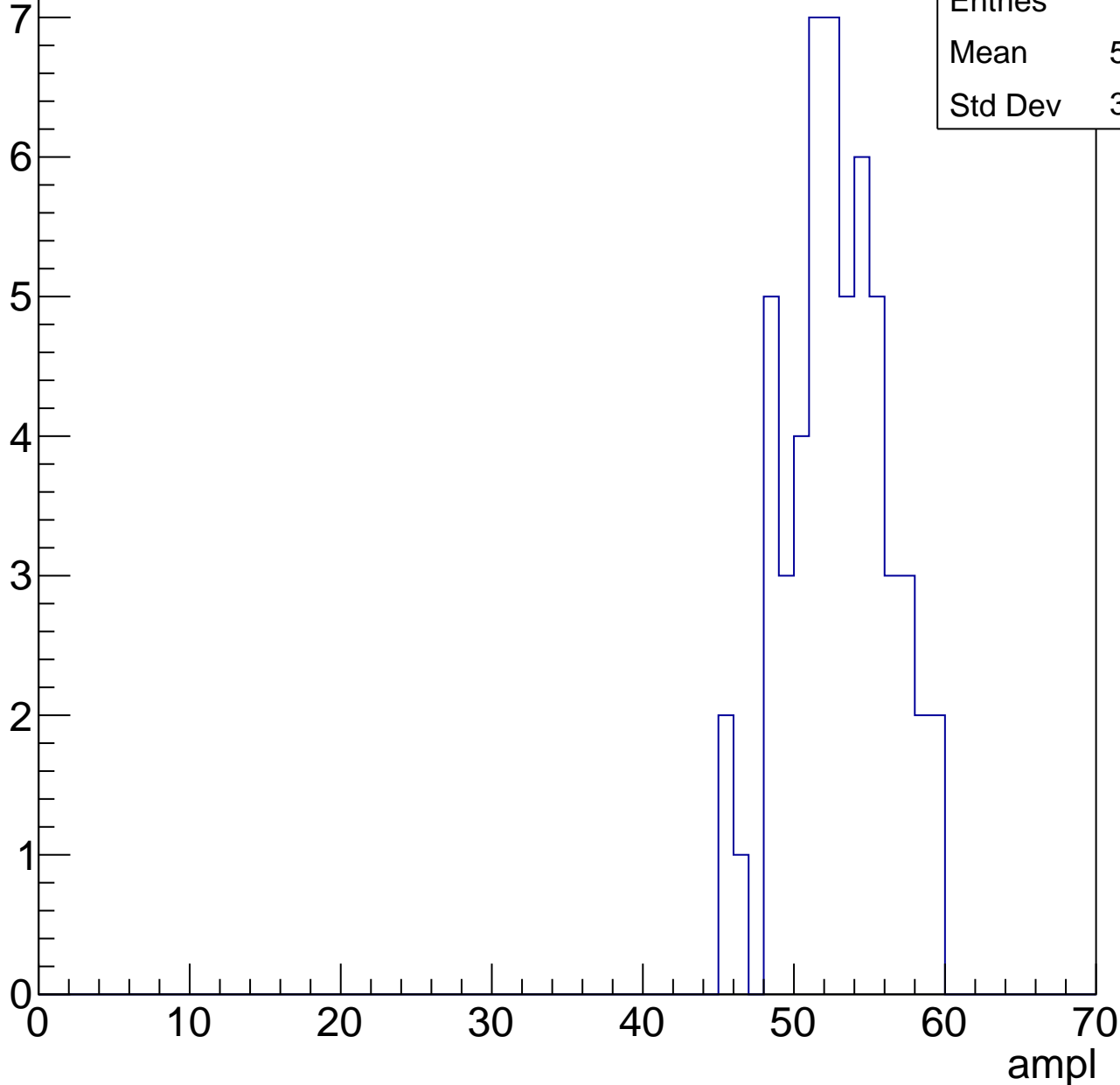


# B1L000S, U1-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	52.38
Std Dev	3.366

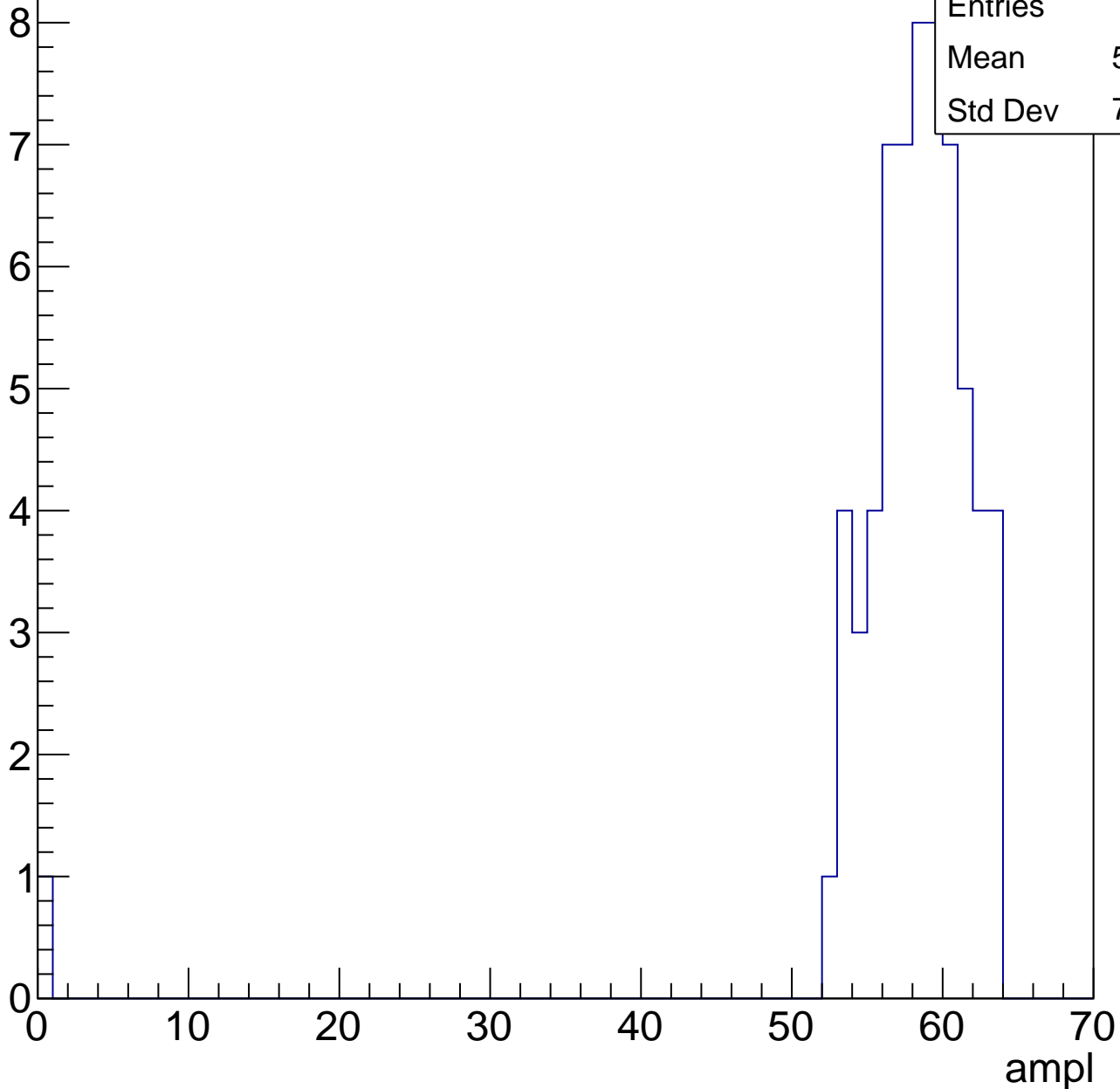


# B1L000S, U1-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	57.11
Std Dev	7.781

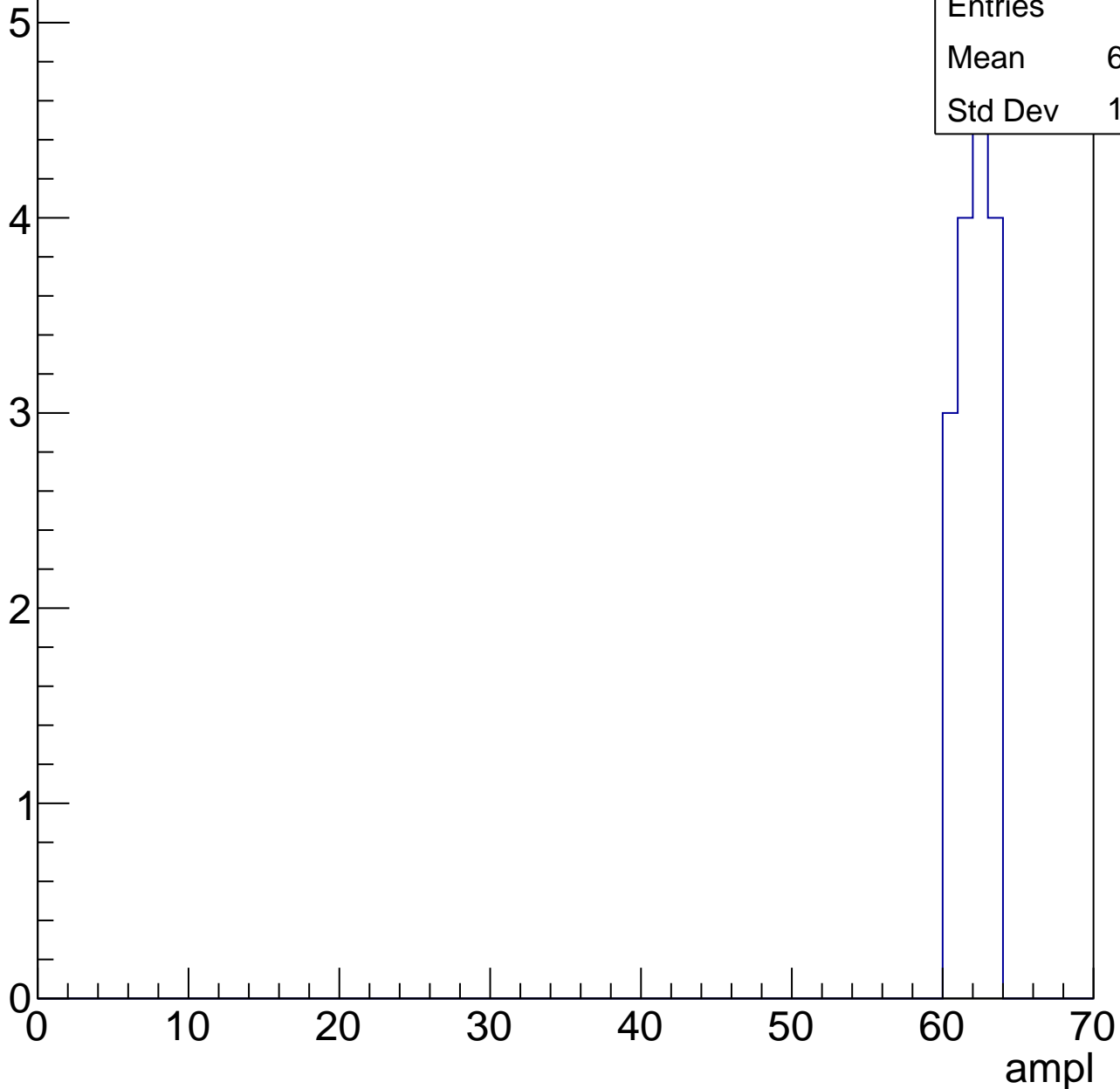


# B1L000S, U1-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.62
Std Dev	1.053

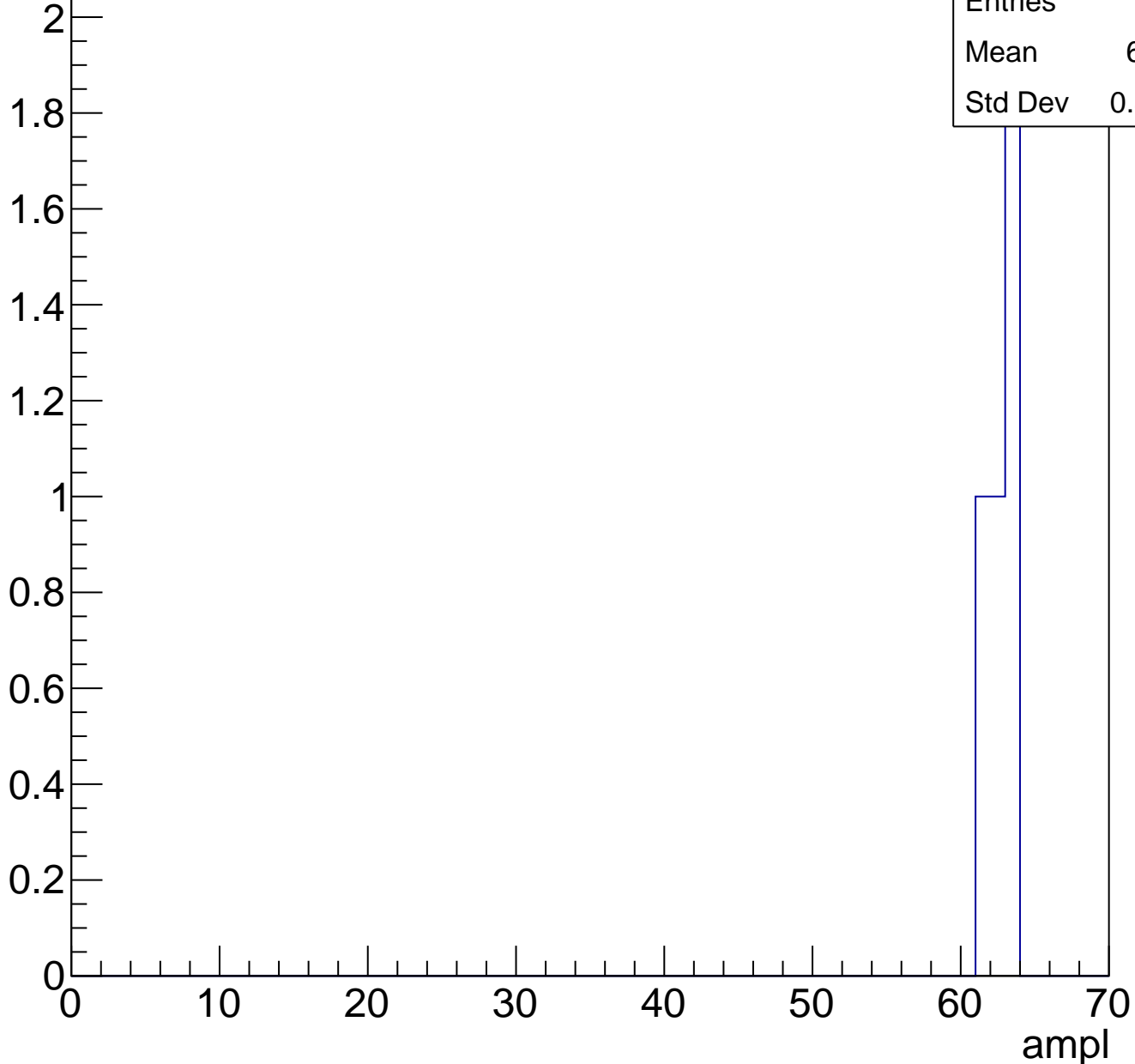




# B1L000S, U1-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch87, adc0

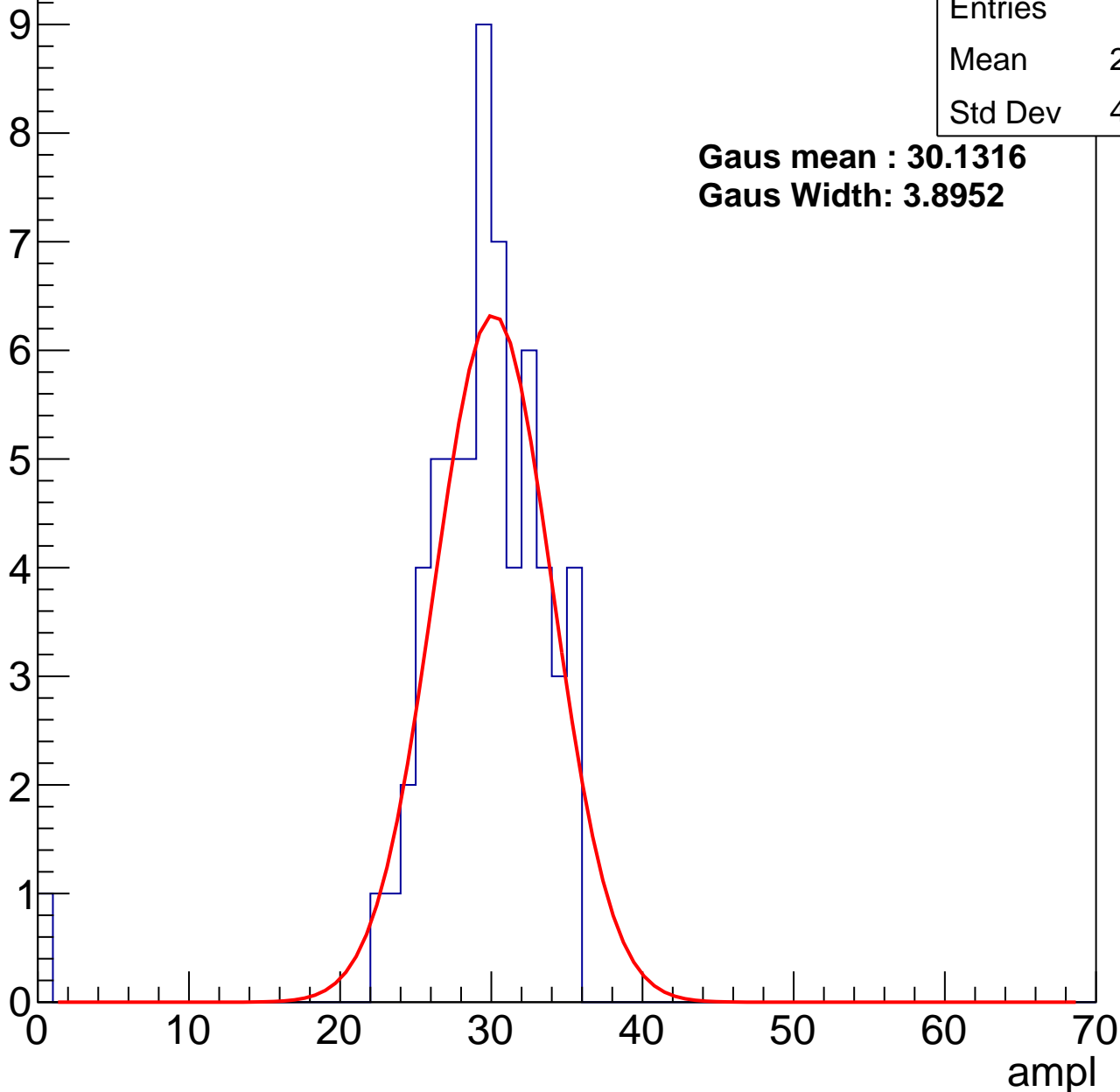
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.84
Std Dev	4.906

**Gaus mean : 30.1316**

**Gaus Width: 3.8952**



# B1L000S, U1-ch87, adc1

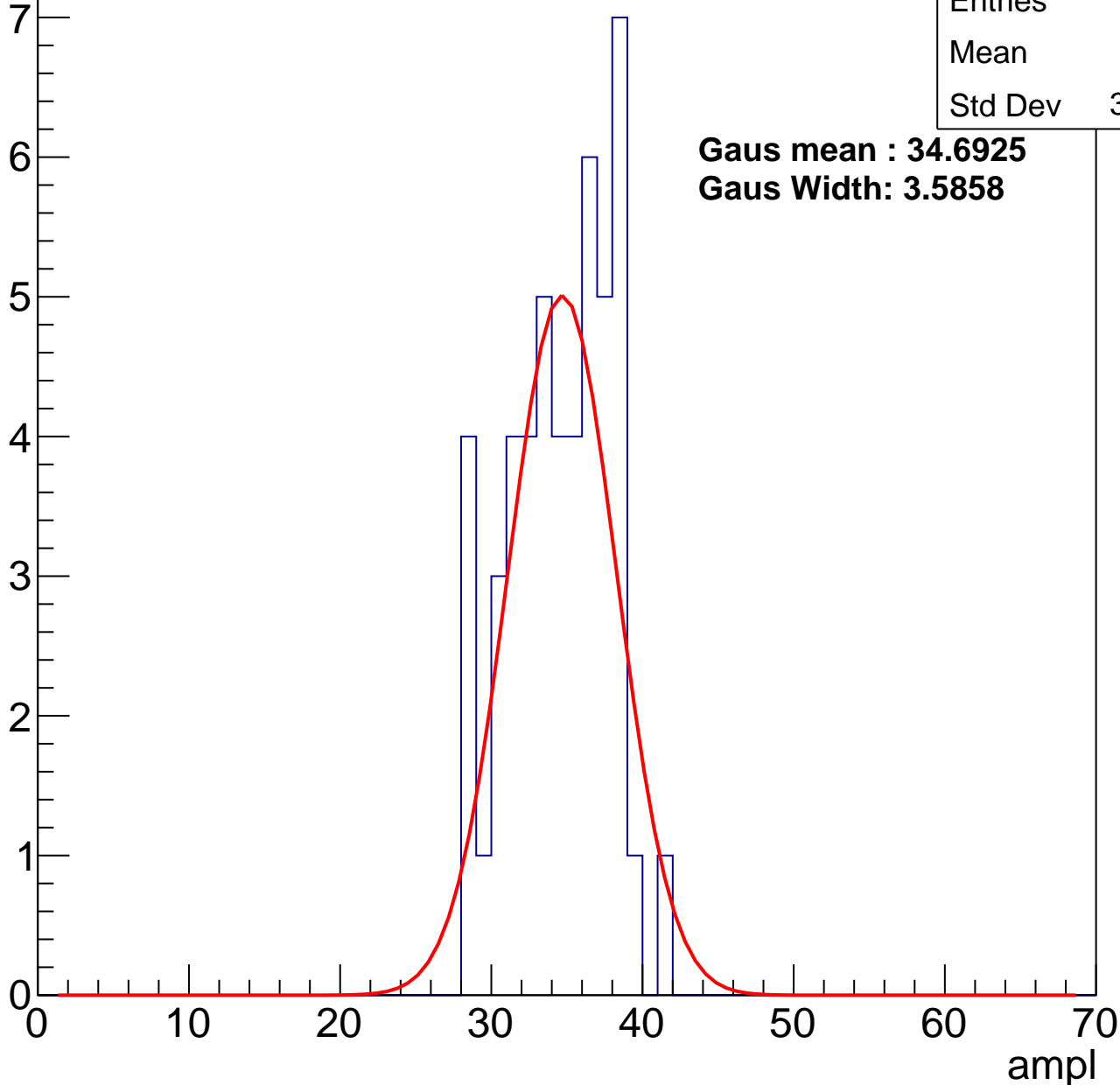
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	34.1
Std Dev	3.297

**Gaus mean : 34.6925**

**Gaus Width: 3.5858**



# B1L000S, U1-ch87, adc2

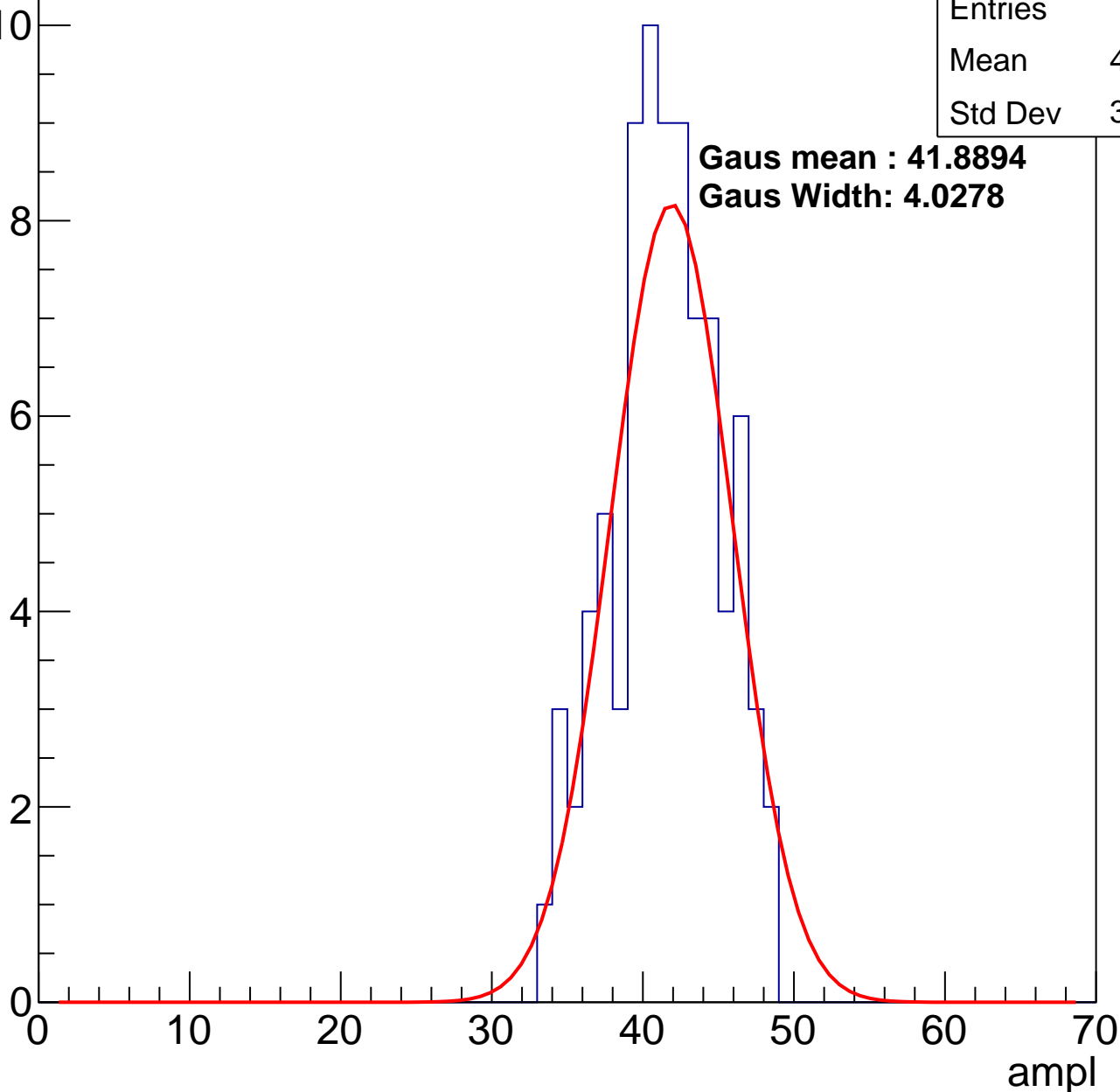
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	84
Mean	41.05
Std Dev	3.545

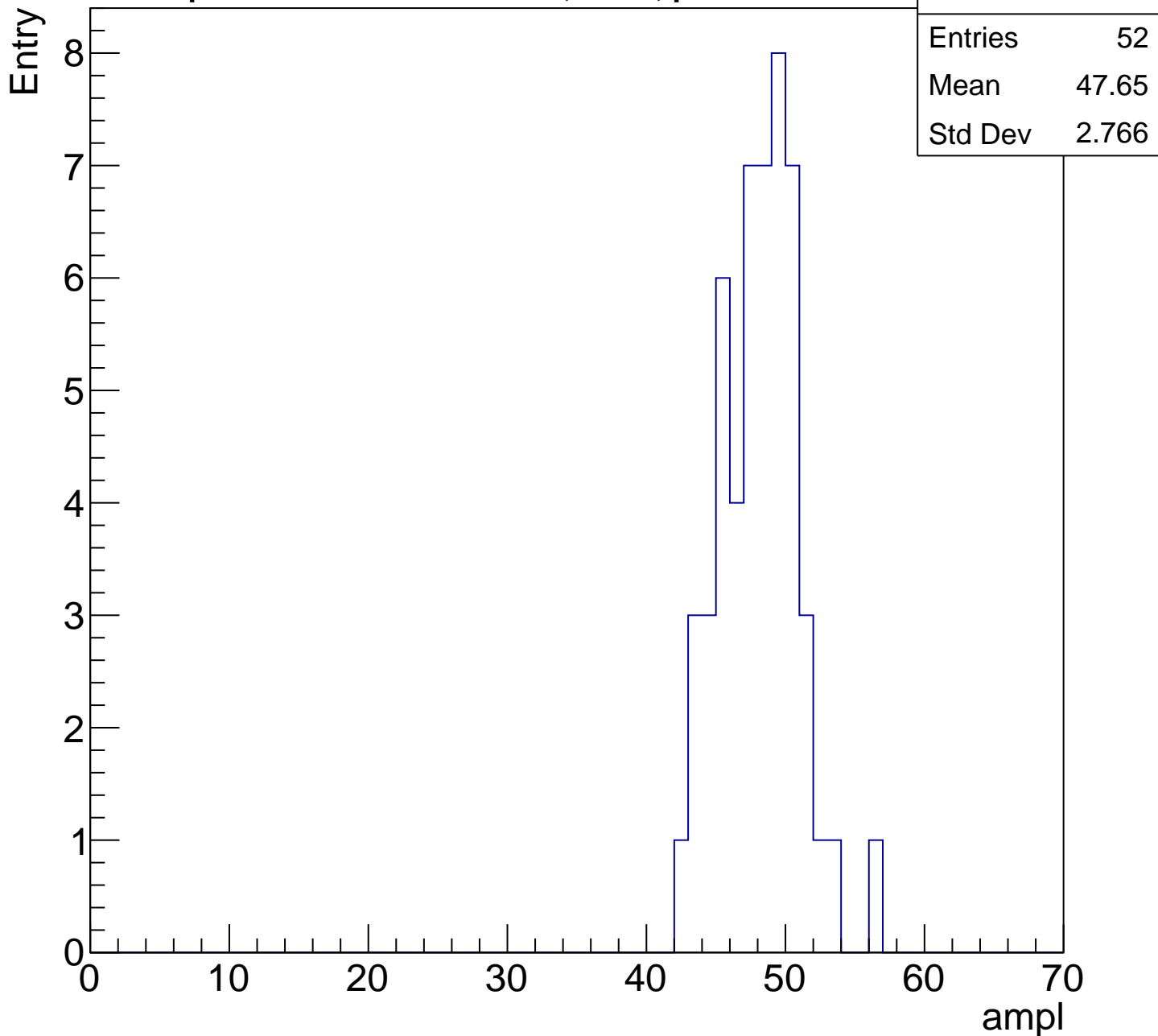
**Gaus mean : 41.8894**

**Gaus Width: 4.0278**



# B1L000S, U1-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

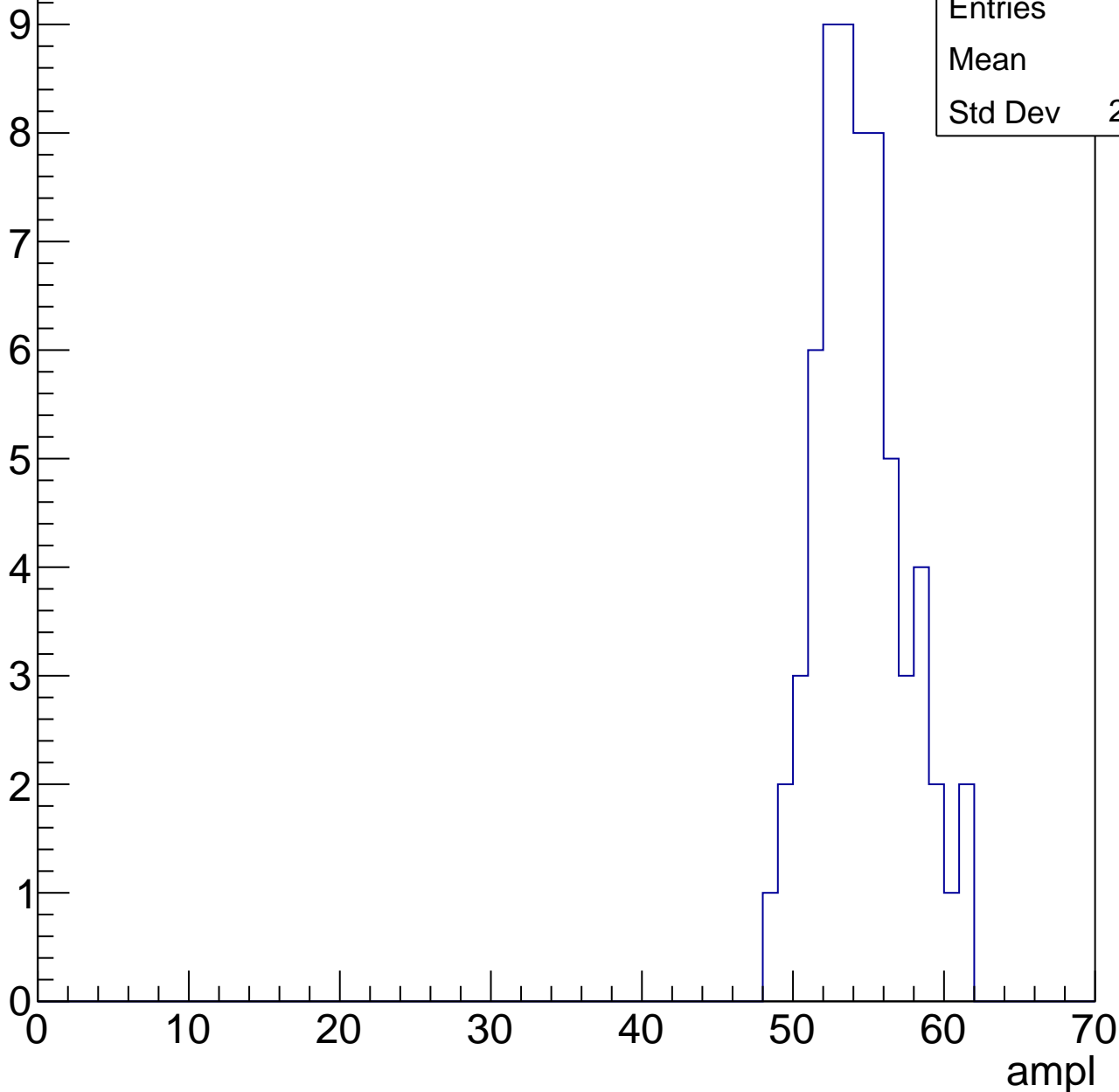


# B1L000S, U1-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	54
Std Dev	2.917



# B1L000S, U1-ch87, adc5

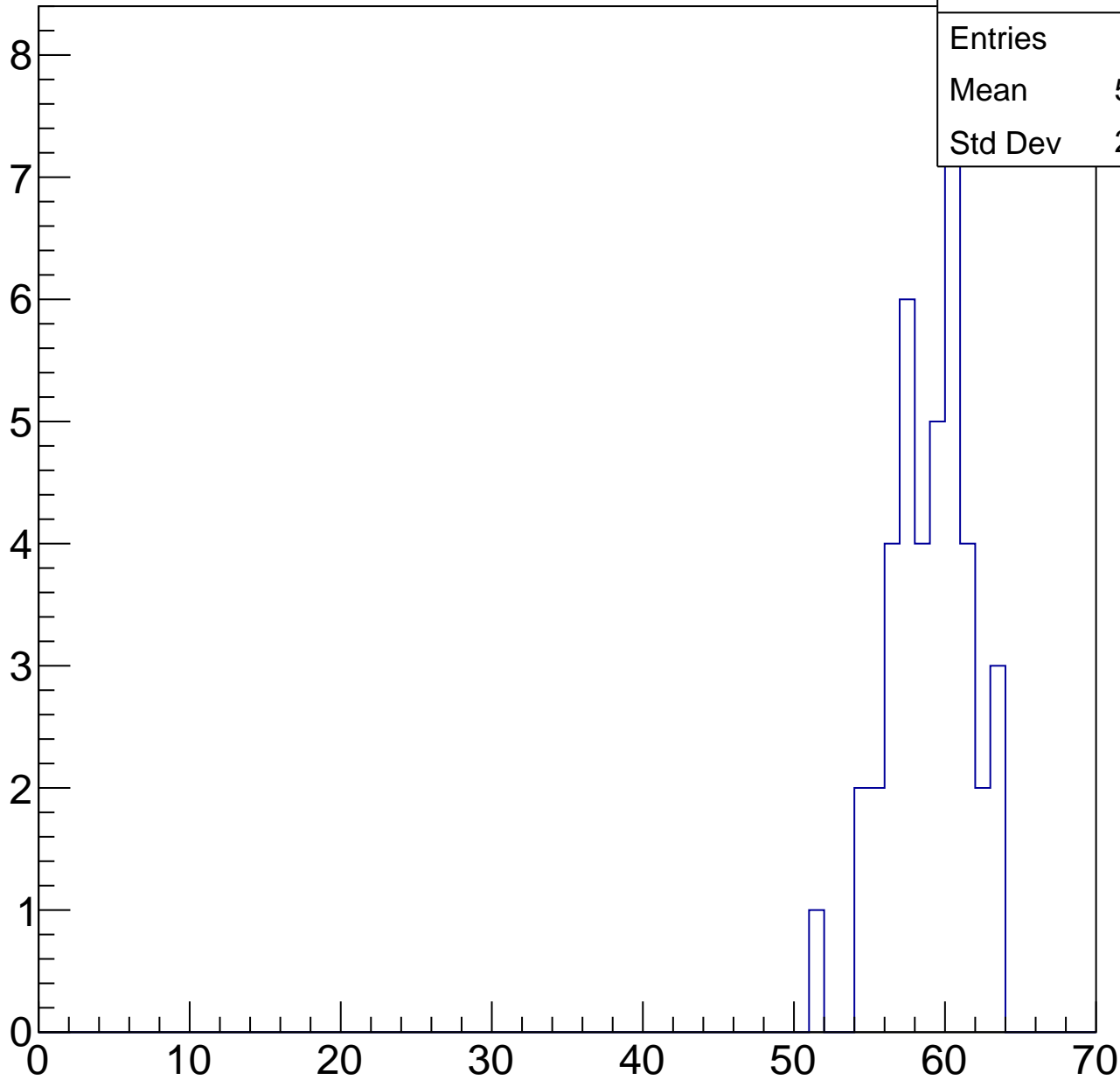
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	58.51
Std Dev	2.661

ampl

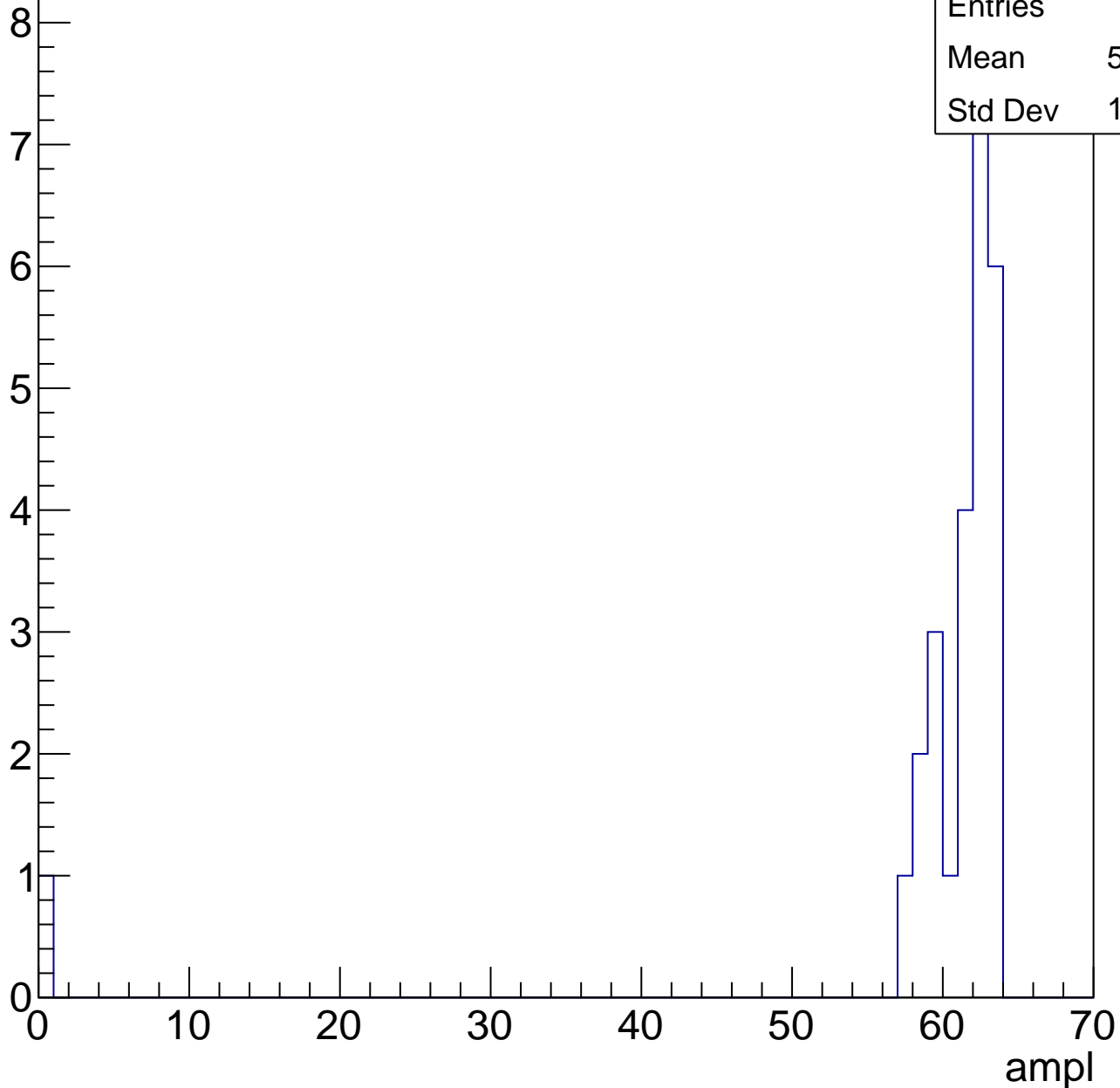


# B1L000S, U1-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.77
Std Dev	11.88





# B1L000S, U1-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch88, adc0

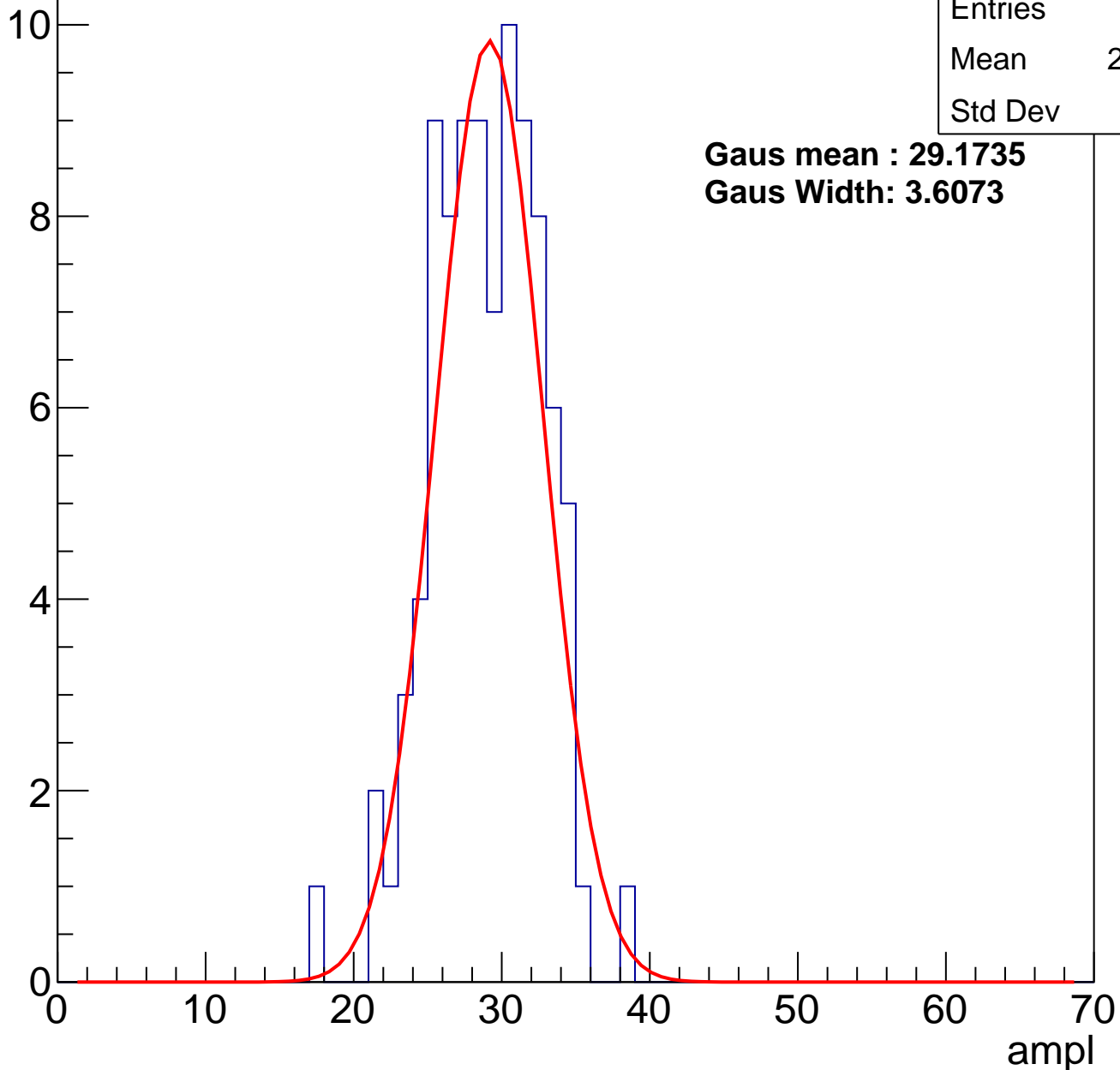
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	93
Mean	28.53
Std Dev	3.63

**Gaus mean : 29.1735**

**Gaus Width: 3.6073**

Entry



# B1L000S, U1-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	52
Mean	36.33
Std Dev	3.148

**Gaus mean : 36.1169**

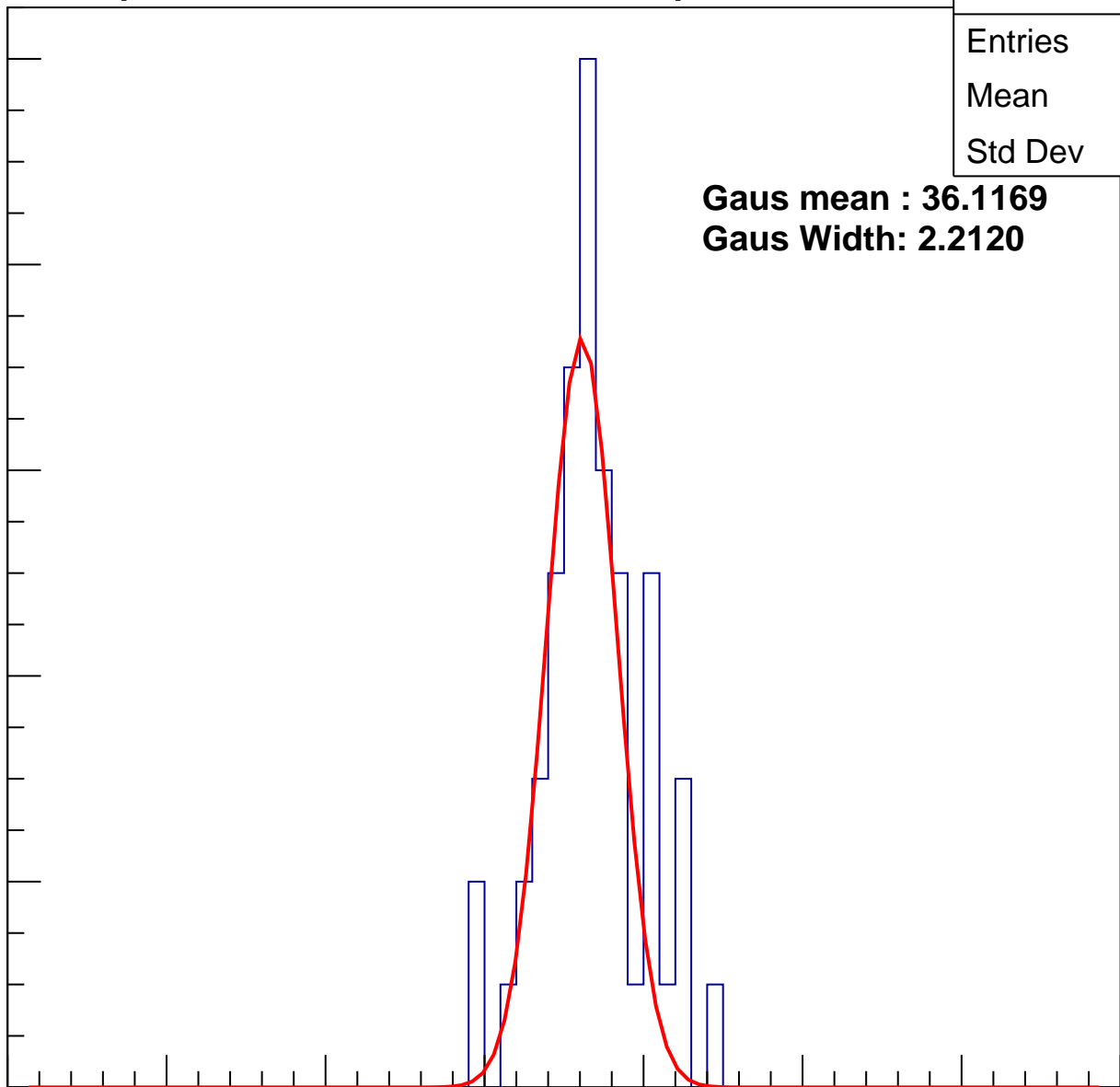
**Gaus Width: 2.2120**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch88, adc2

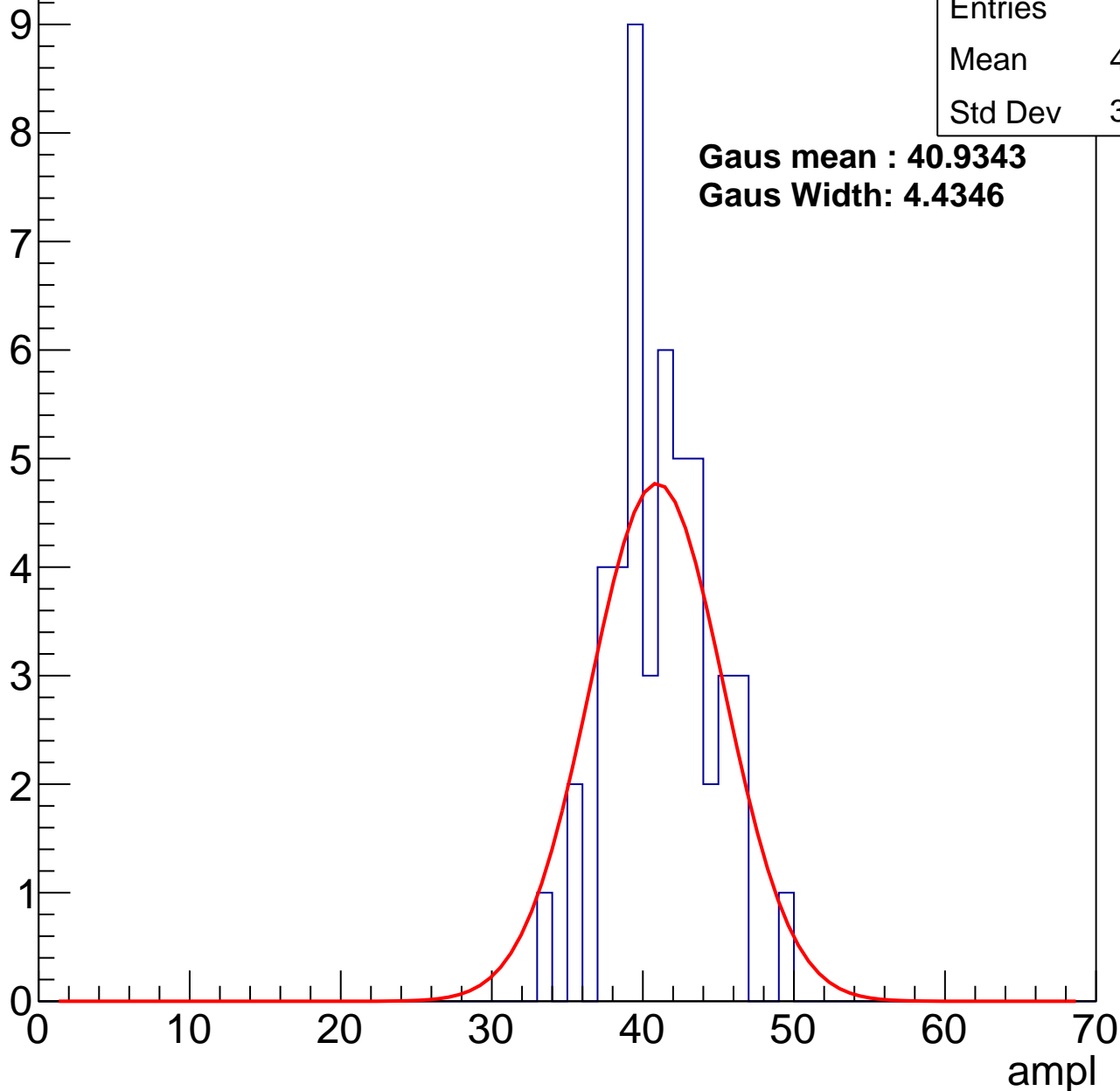
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	40.73
Std Dev	3.232

**Gaus mean : 40.9343**

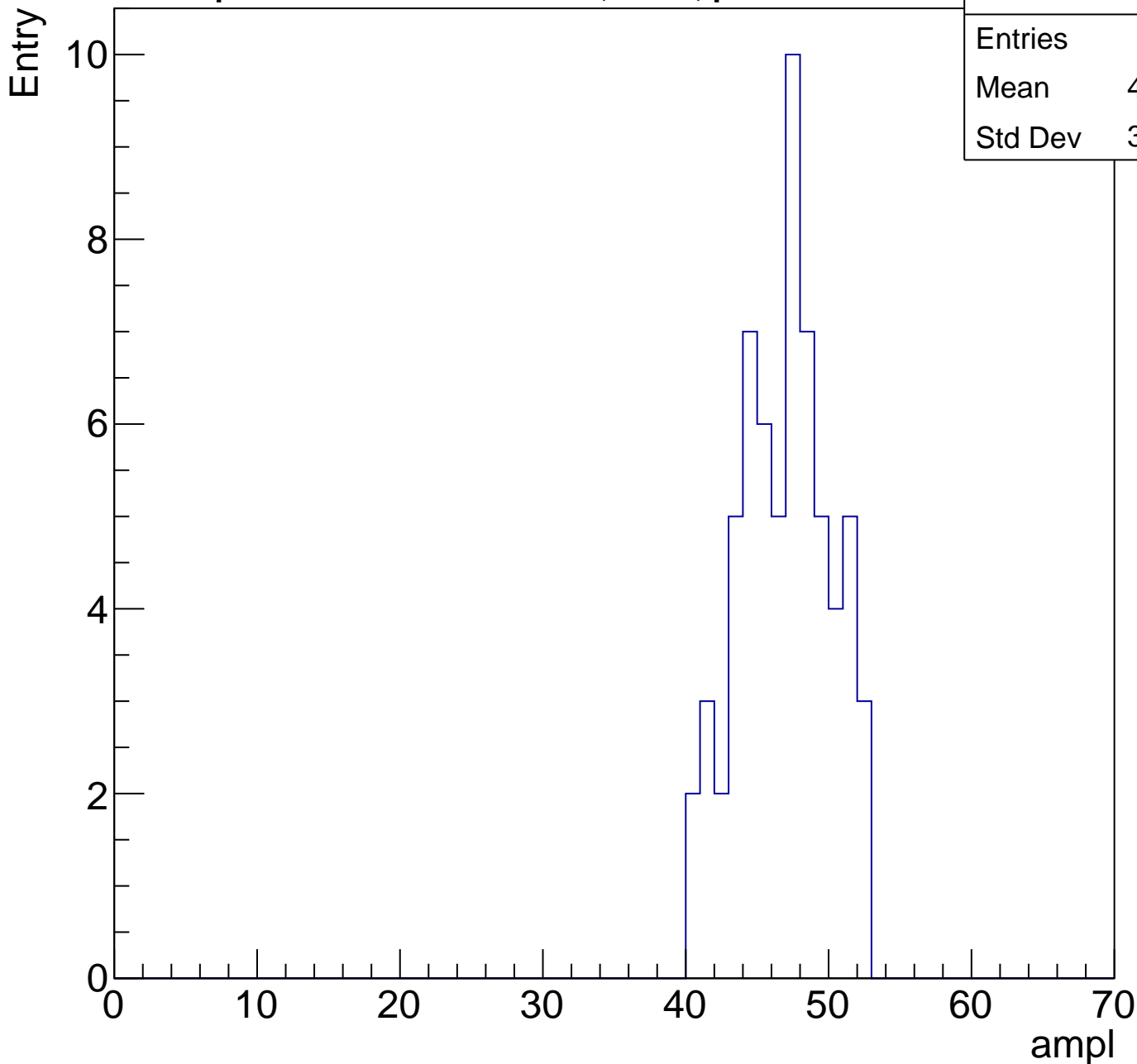
**Gaus Width: 4.4346**



# B1L000S, U1-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	46.44
Std Dev	3.127

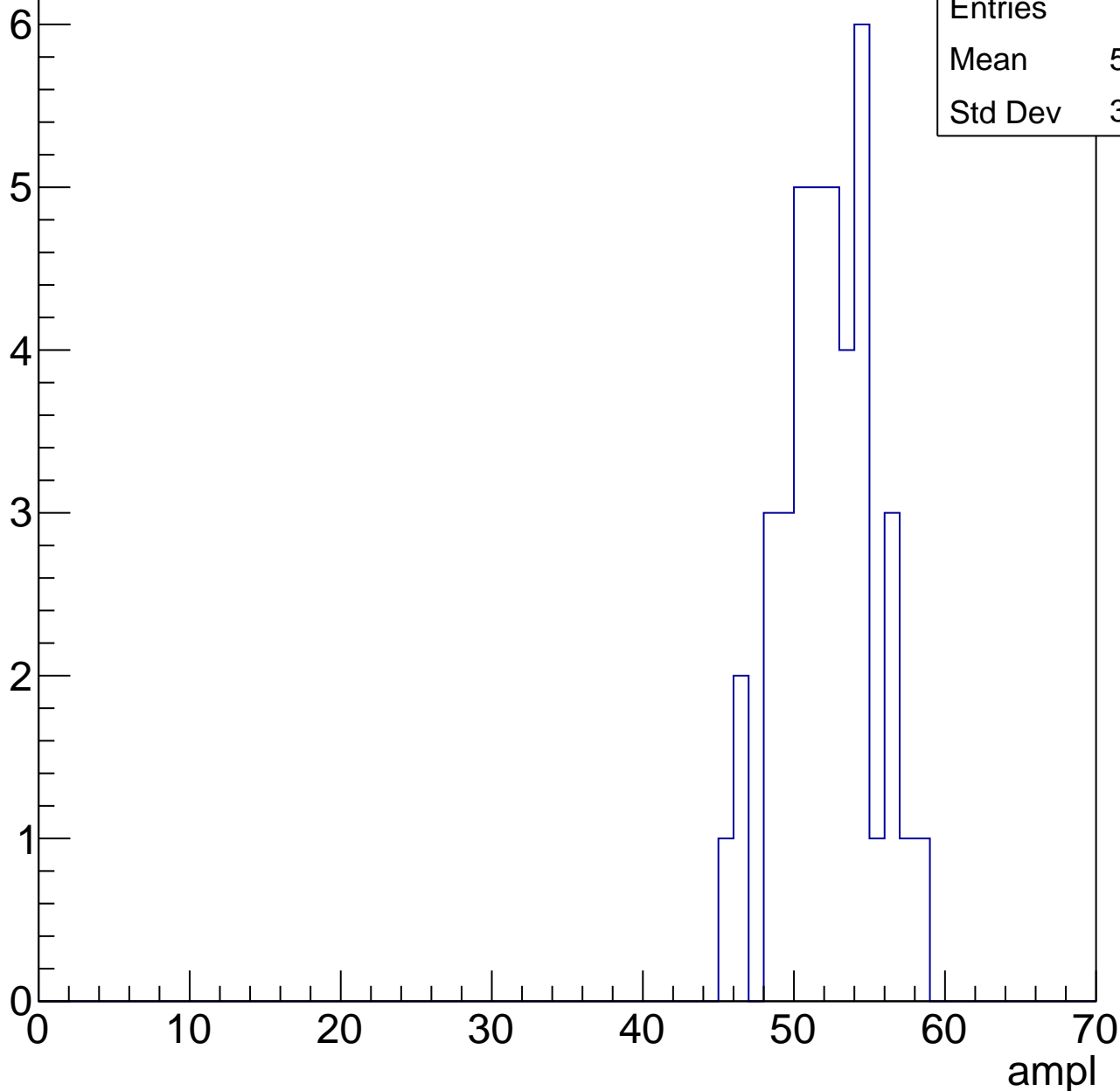


# B1L000S, U1-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

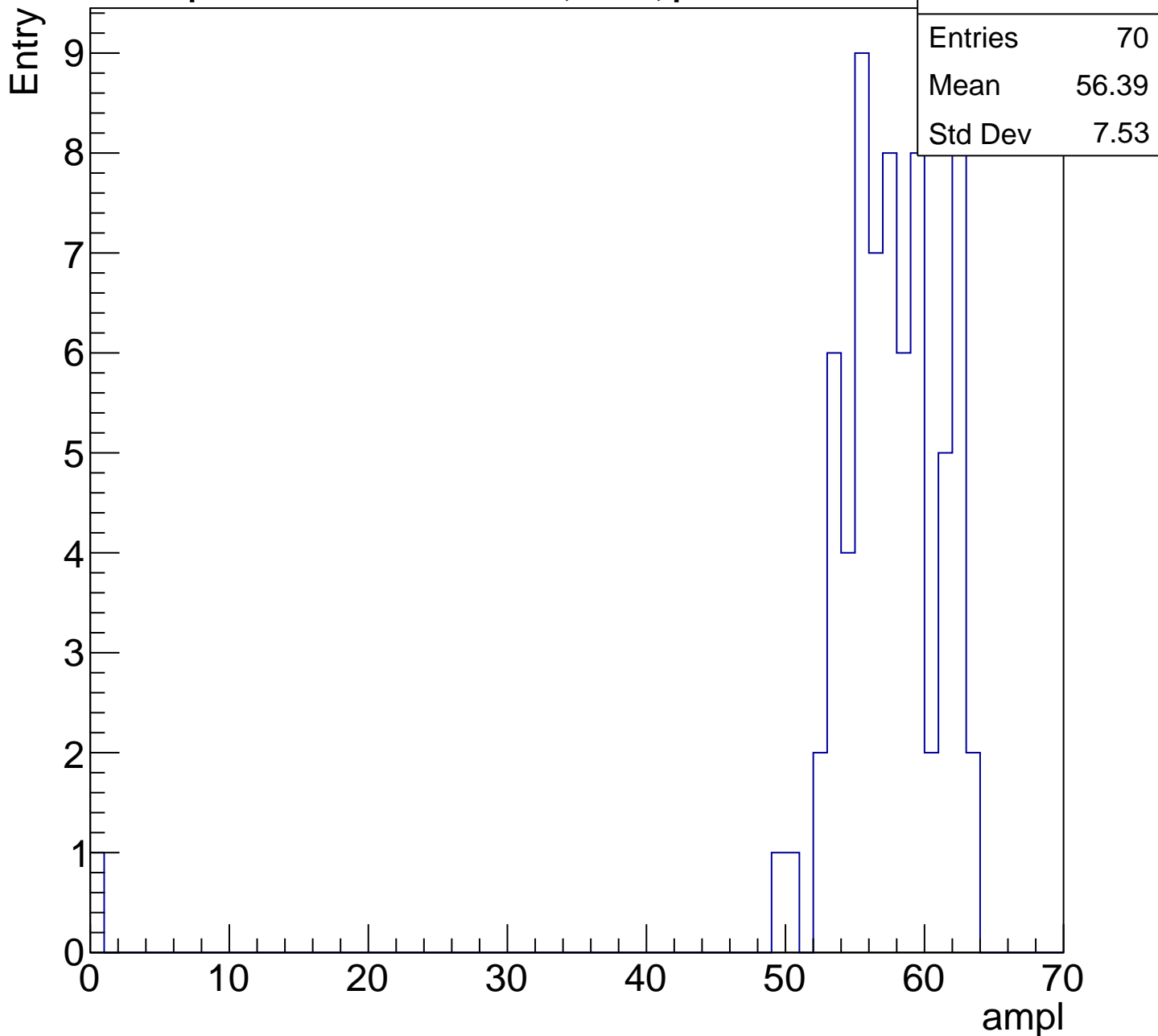
Entry

Entries	40
Mean	51.67
Std Dev	3.012



# B1L000S, U1-ch88, adc5

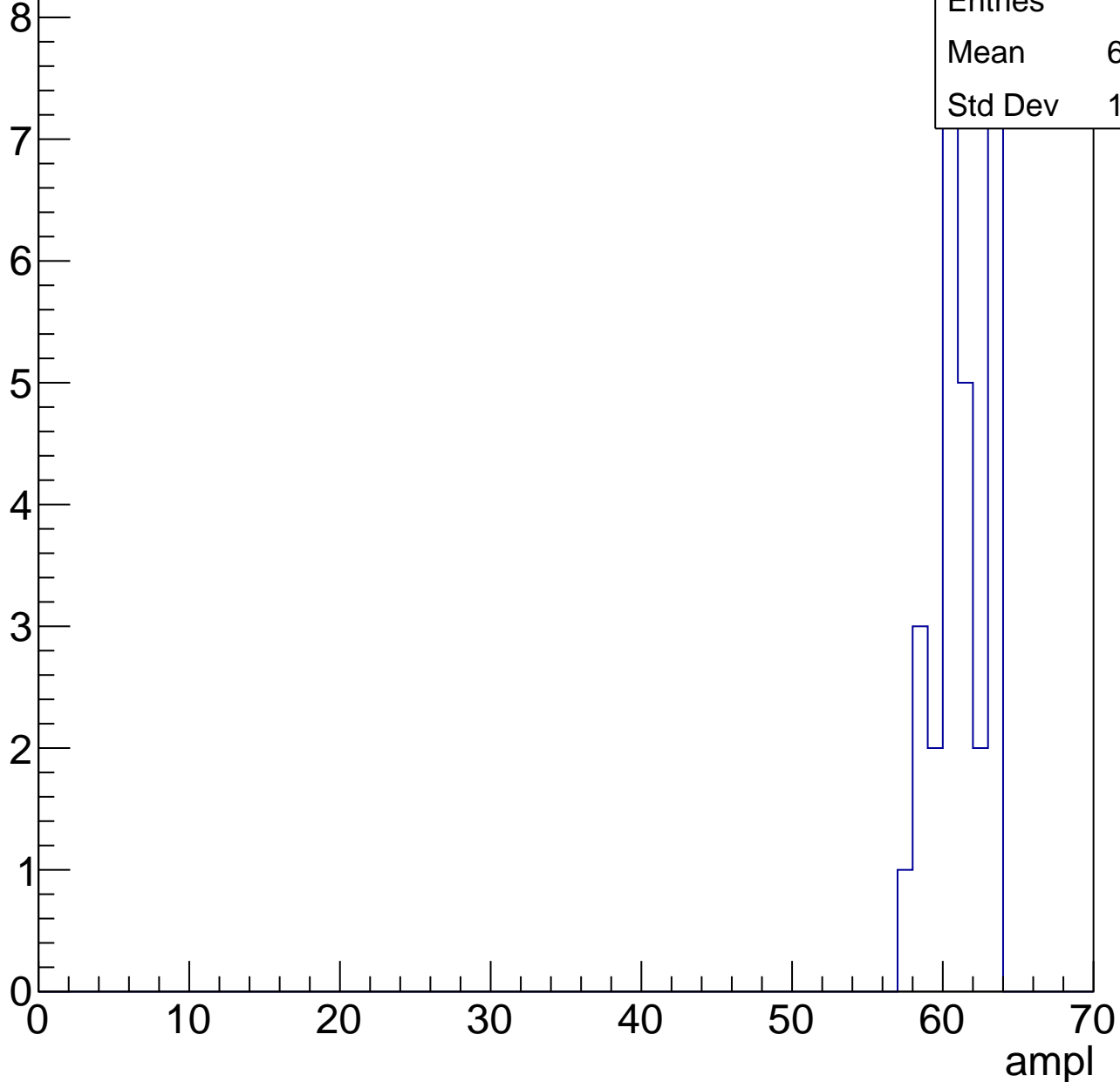
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U1-ch88, adc7

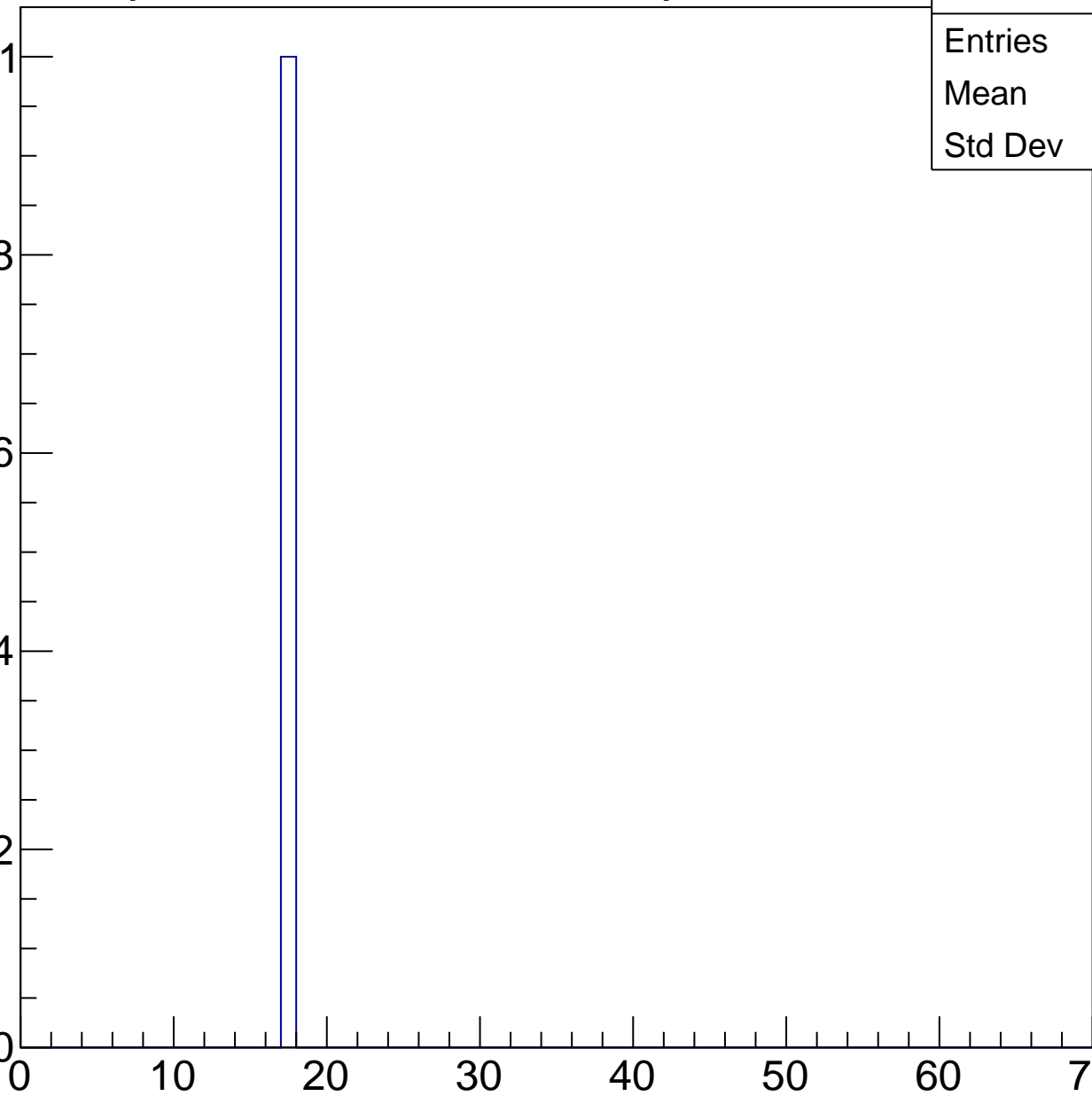
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	17
Std Dev	0

ampl



# B1L000S, U1-ch89, adc0

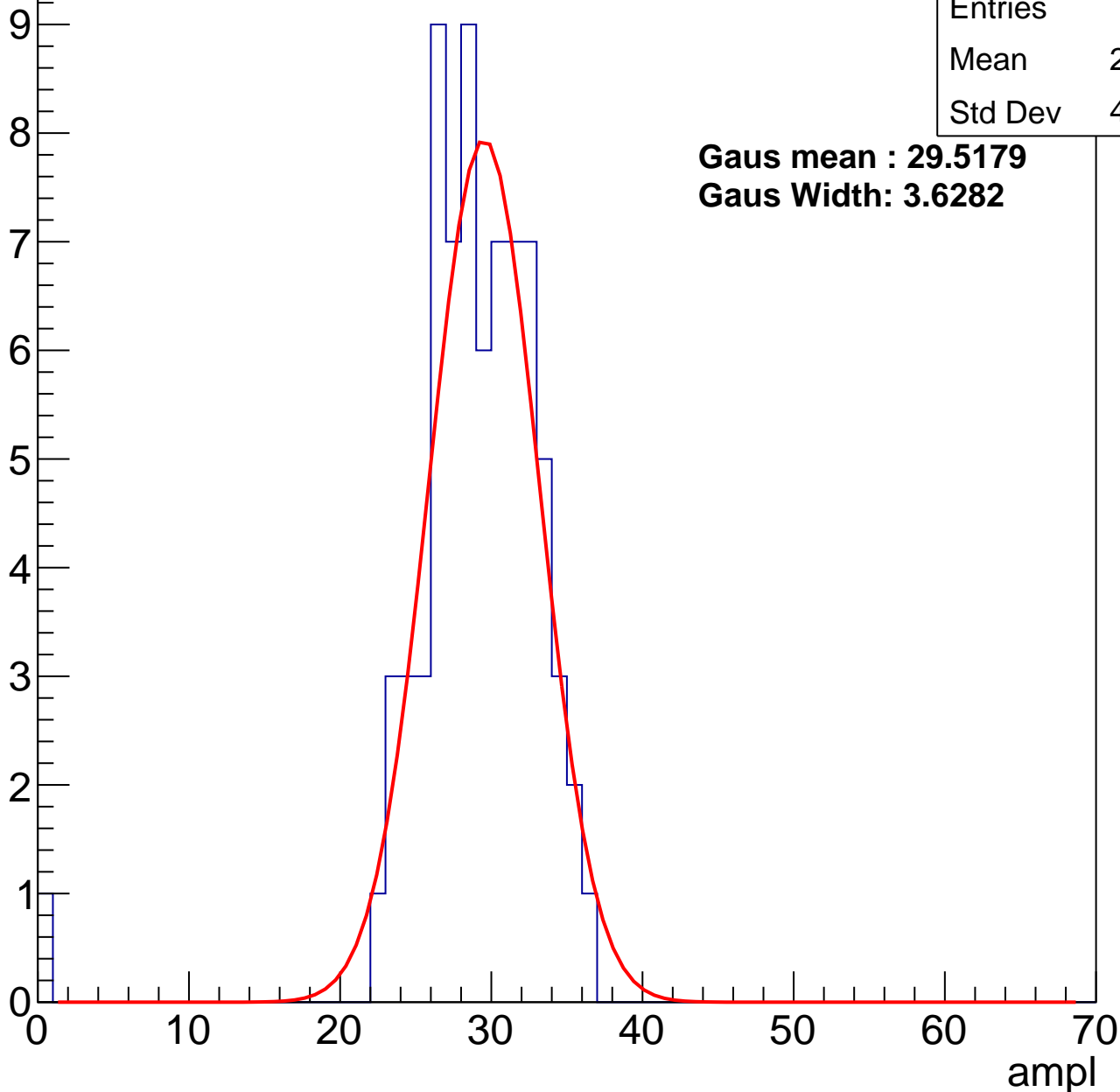
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	28.53
Std Dev	4.645

**Gaus mean : 29.5179**

**Gaus Width: 3.6282**



# B1L000S, U1-ch89, adc1

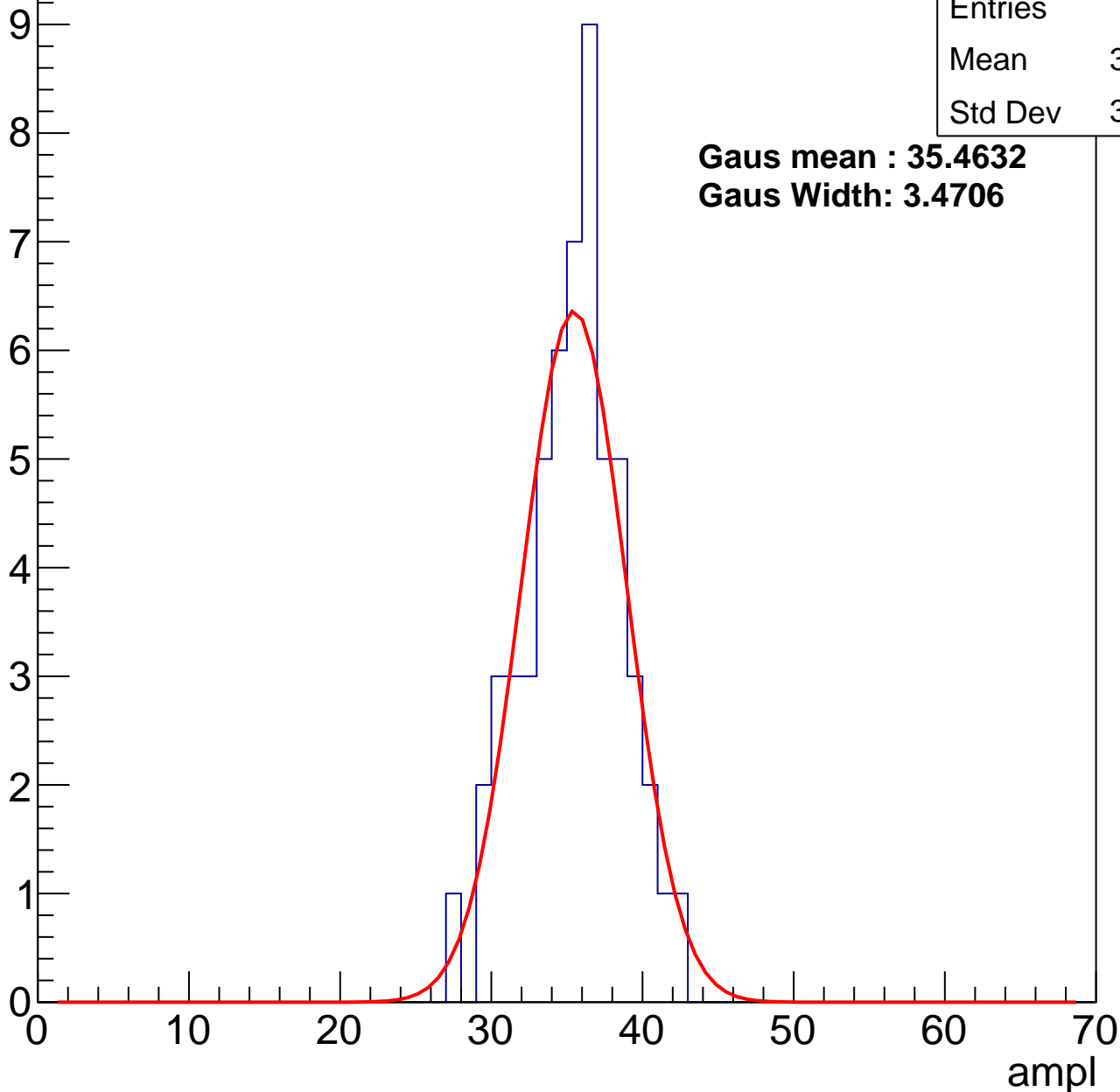
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	34.95
Std Dev	3.187

**Gaus mean : 35.4632**

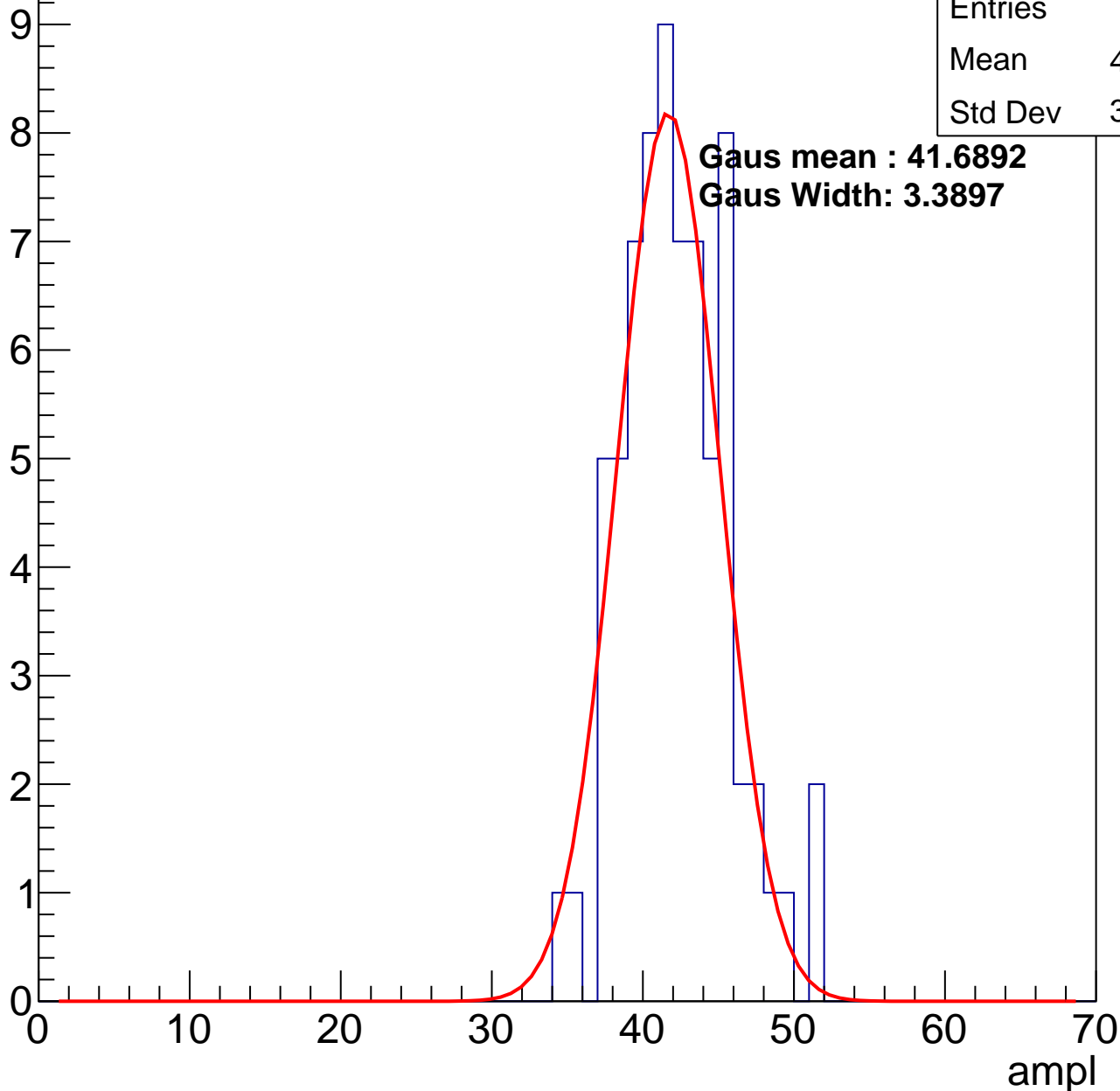
**Gaus Width: 3.4706**



# B1L000S, U1-ch89, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

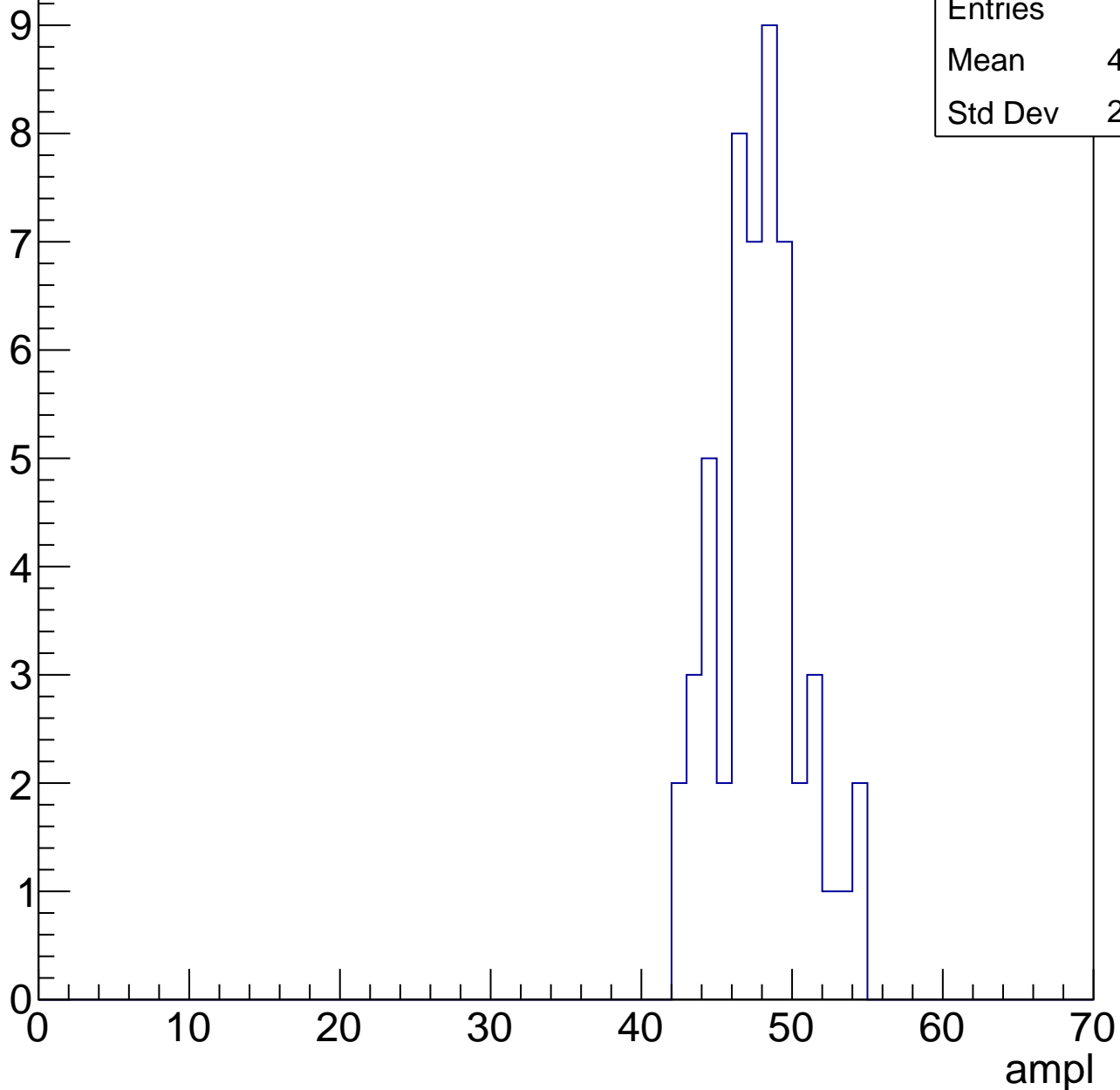
Entry



# B1L000S, U1-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

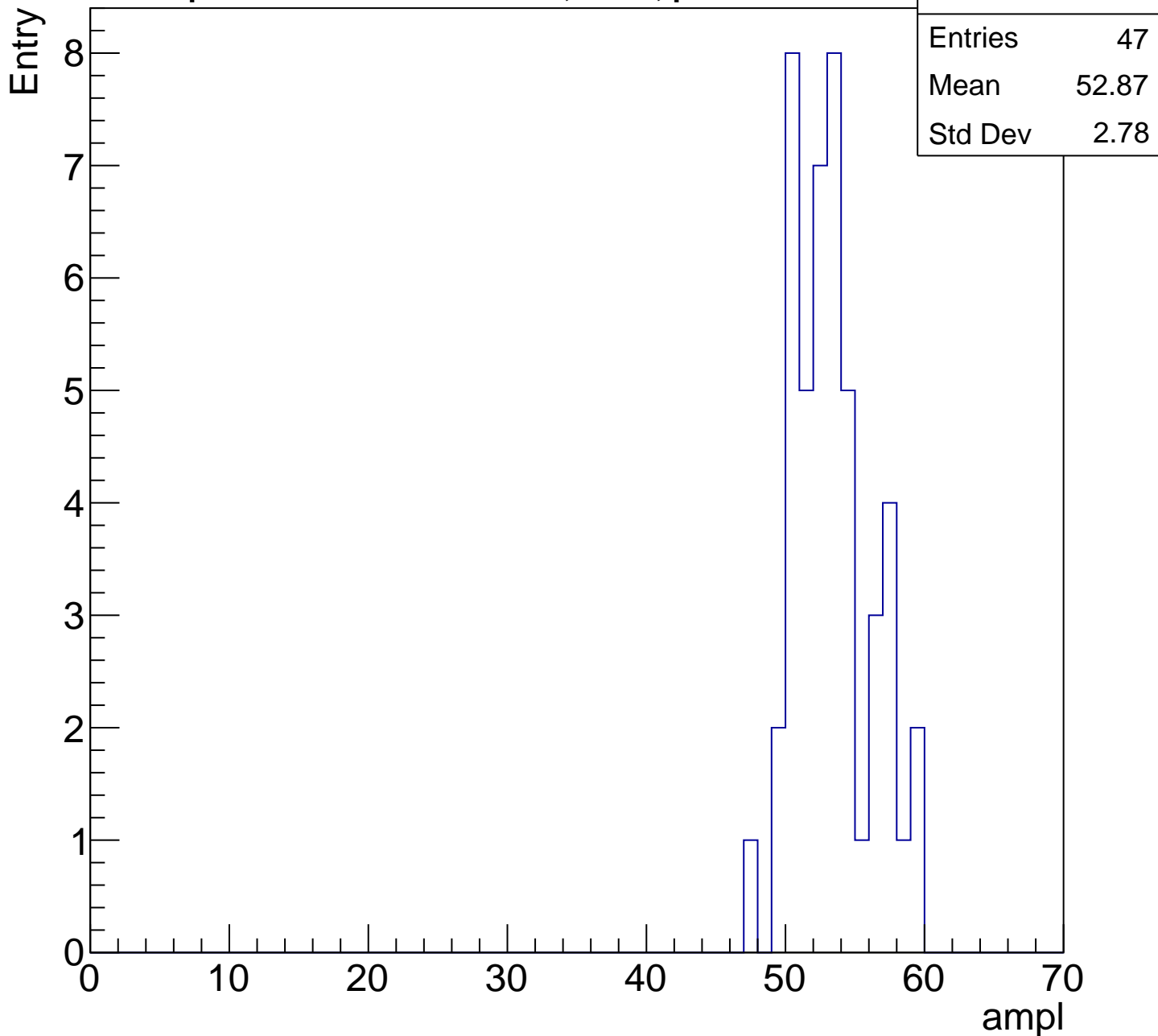
Entry



Entries	52
Mean	47.33
Std Dev	2.827

# B1L000S, U1-ch89, adc4

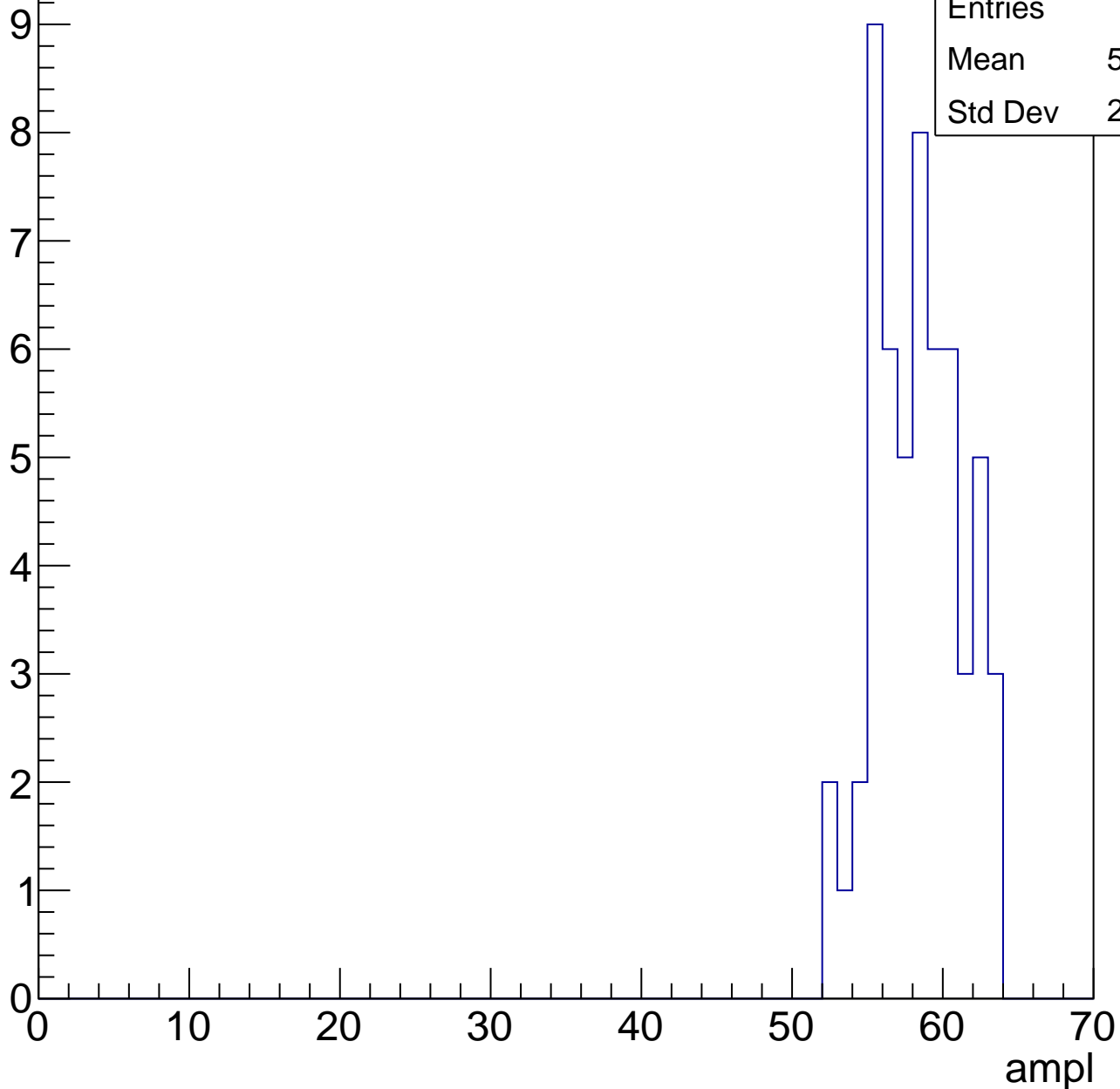
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



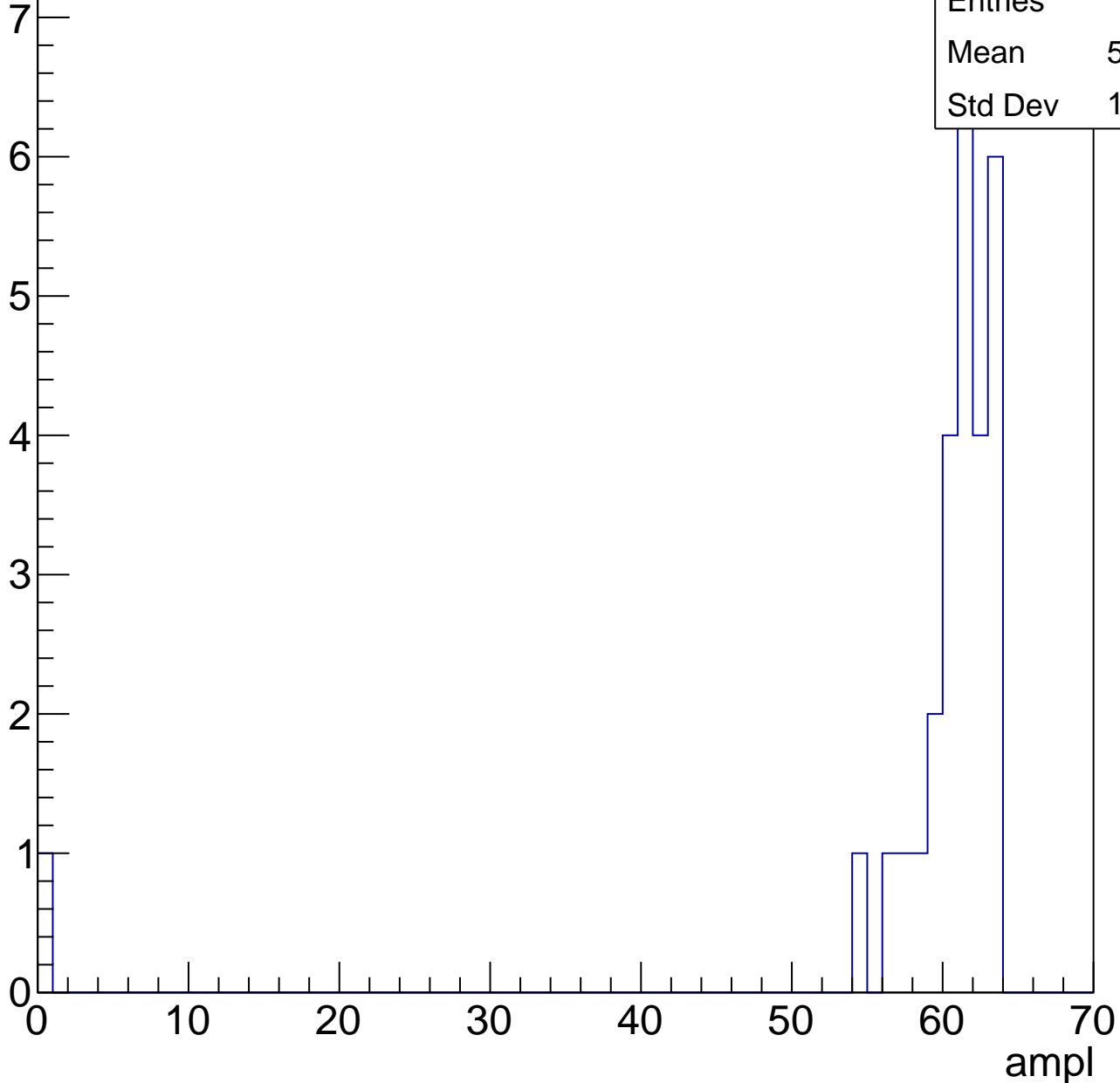
Entries	56
Mean	57.88
Std Dev	2.835

# B1L000S, U1-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.43
Std Dev	11.46





# B1L000S, U1-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch90, adc0

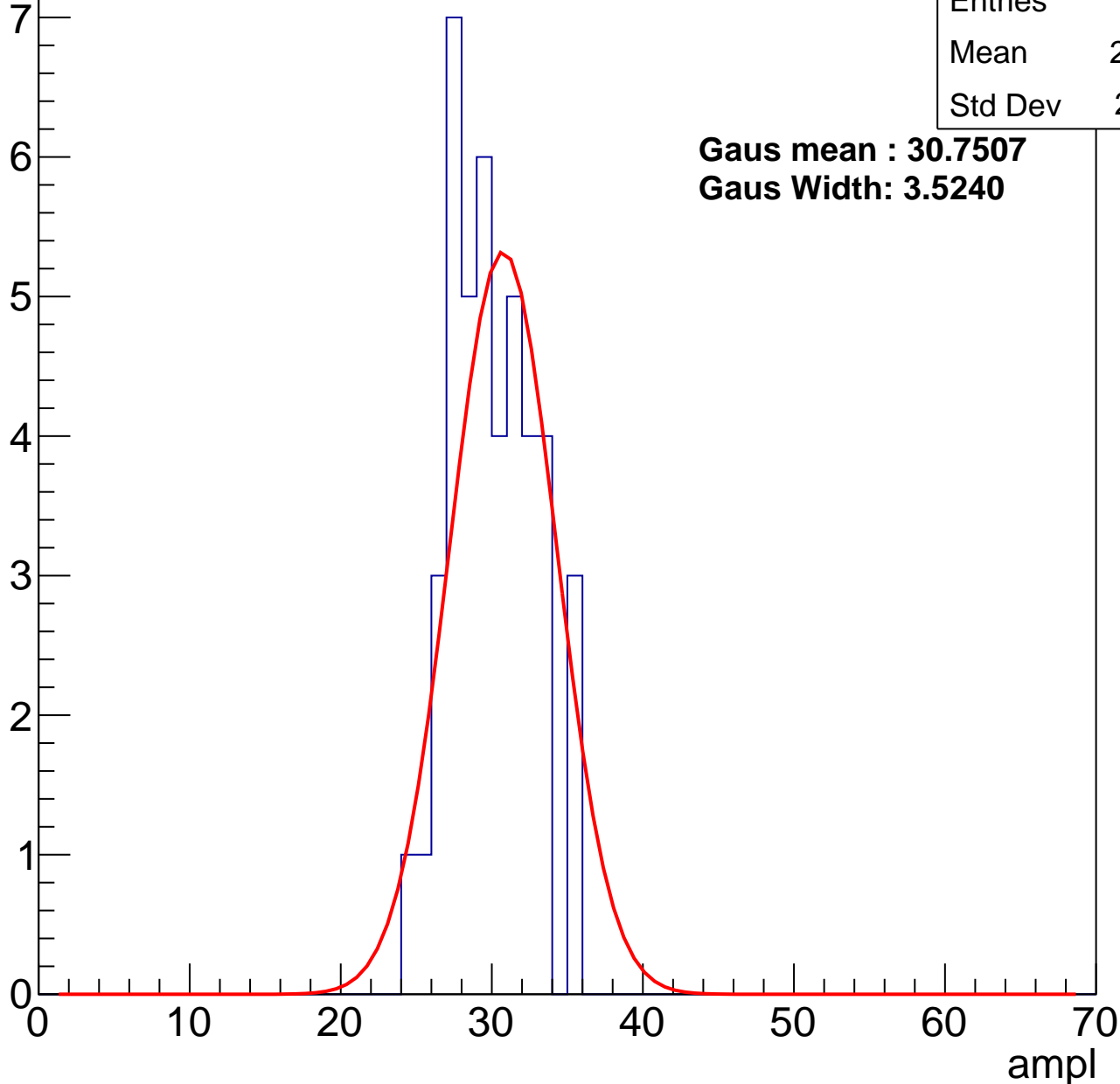
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	29.53
Std Dev	2.731

**Gaus mean : 30.7507**

**Gaus Width: 3.5240**



# B1L000S, U1-ch90, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	34.71
Std Dev	3.502

**Gaus mean : 34.5730**

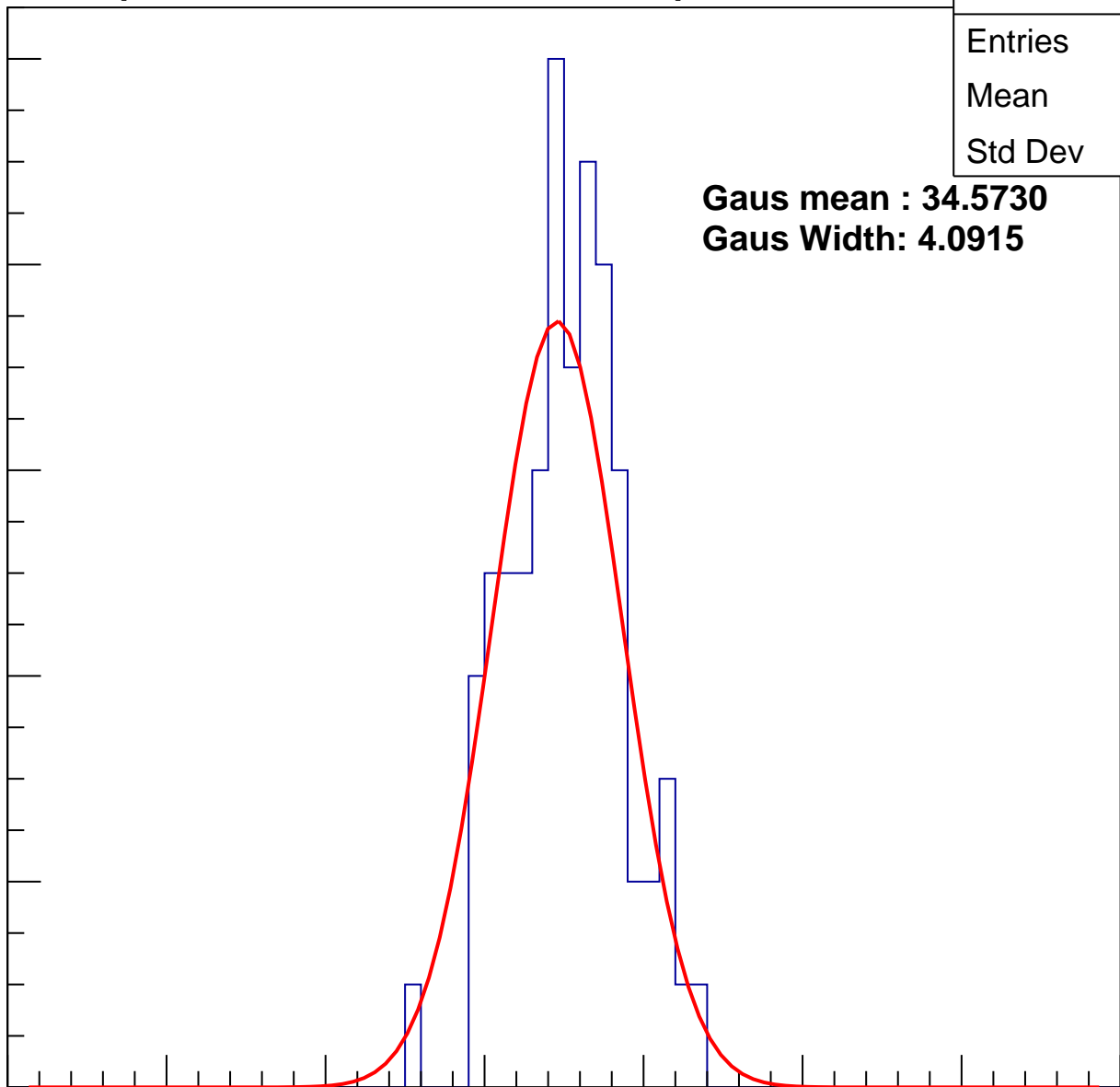
**Gaus Width: 4.0915**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch90, adc2

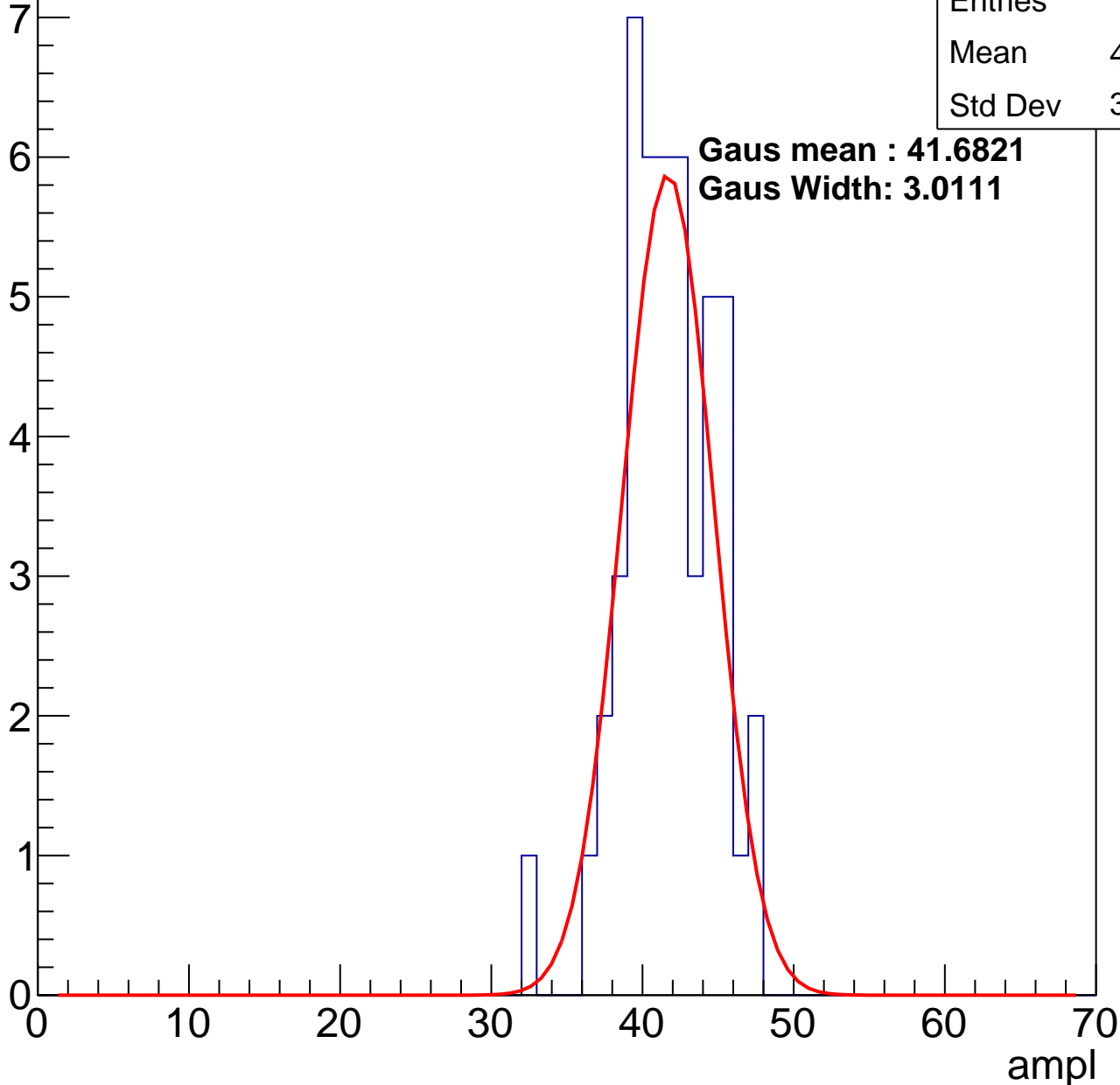
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	41.27
Std Dev	3.012

**Gaus mean : 41.6821**

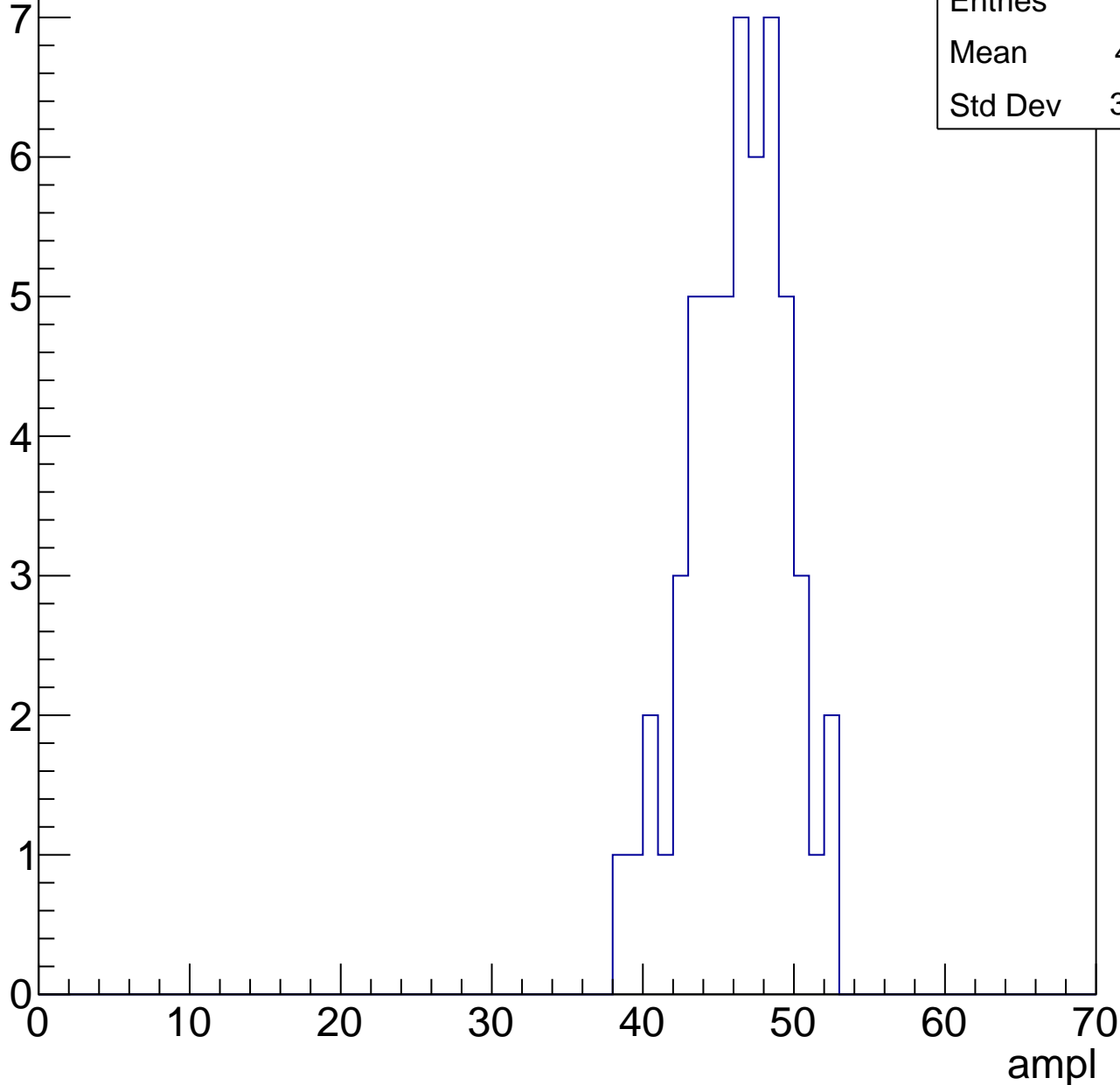
**Gaus Width: 3.0111**



# B1L000S, U1-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

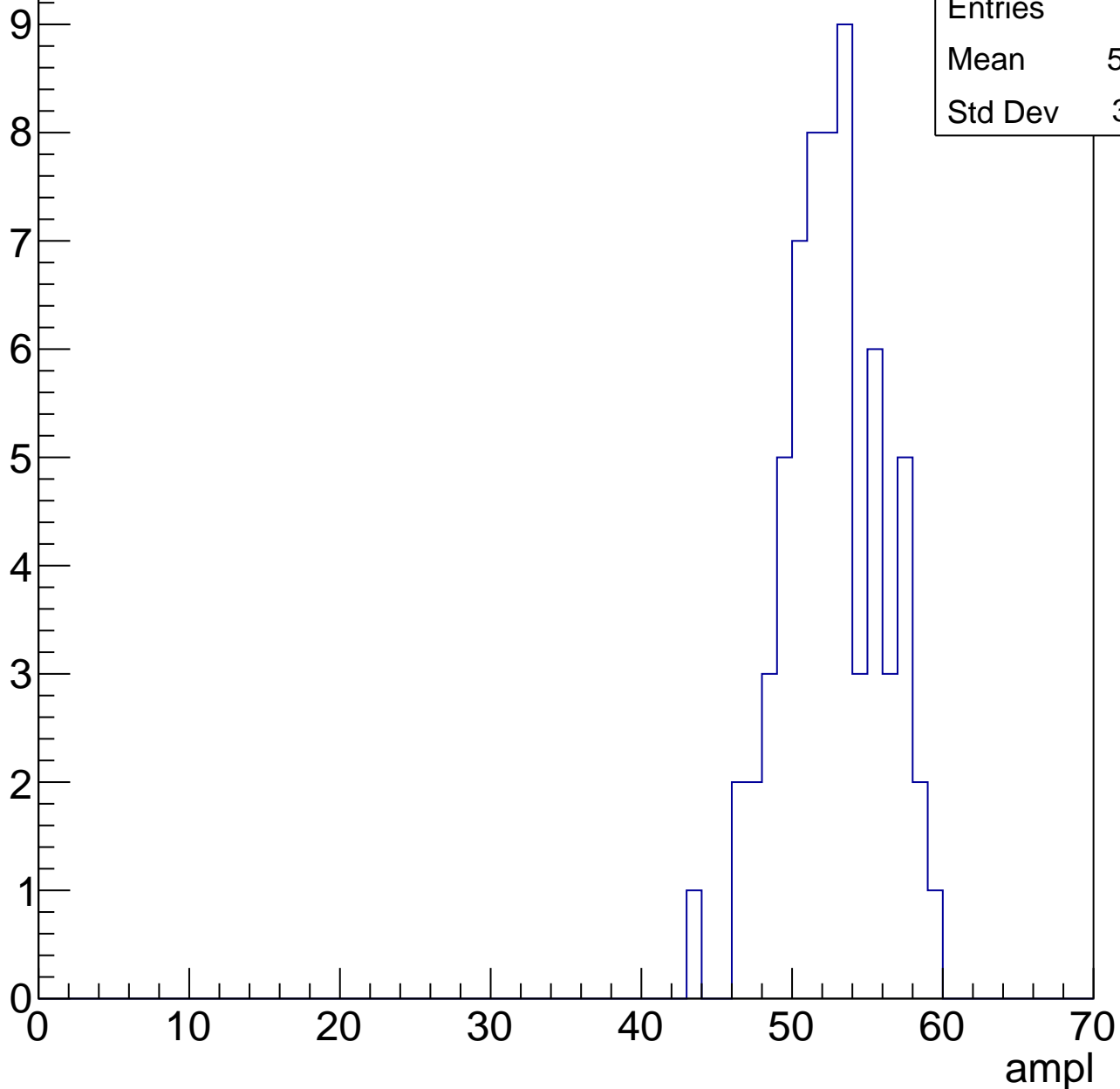


Entries	54
Mean	45.81
Std Dev	3.192

# B1L000S, U1-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

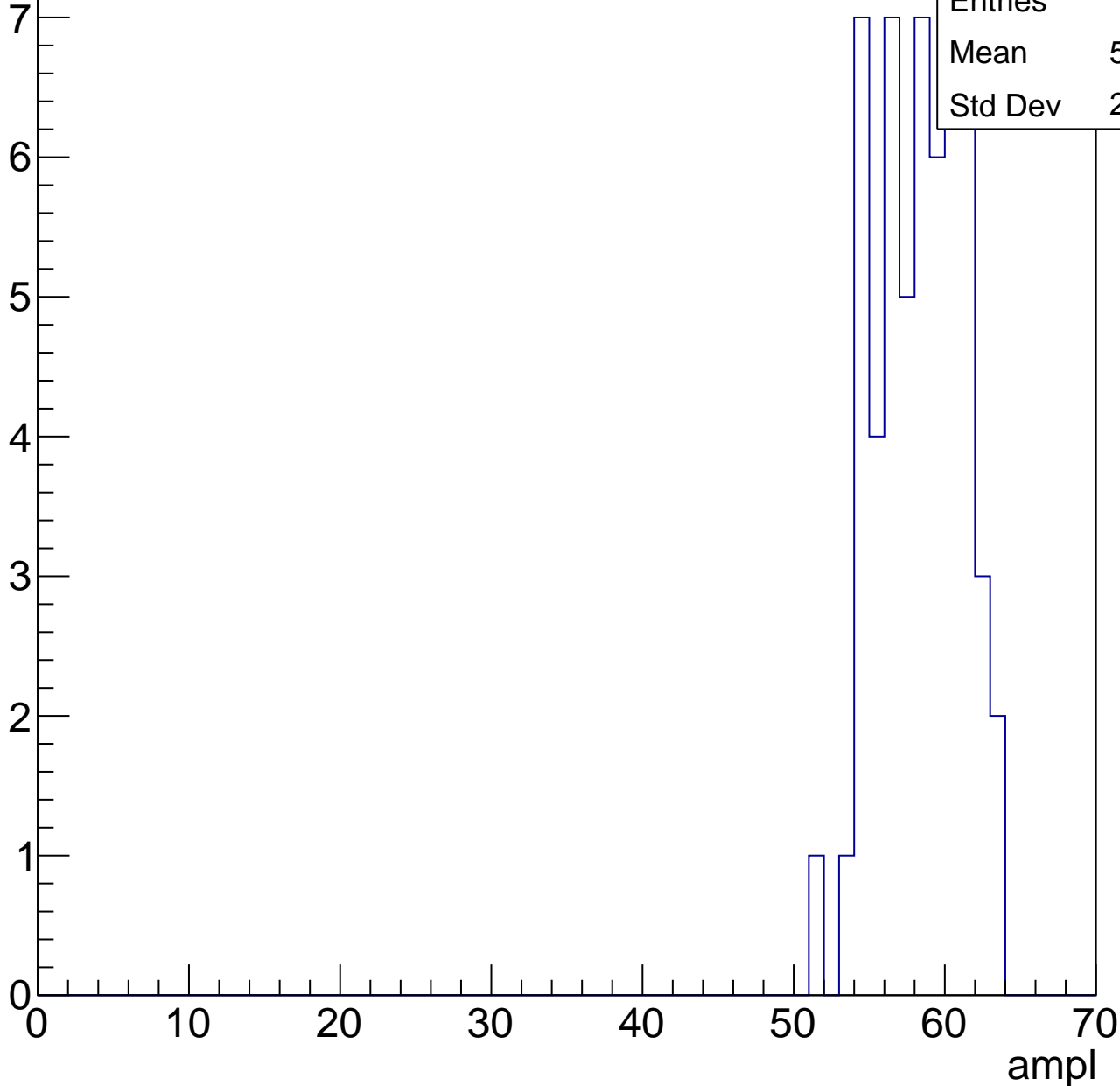


# B1L000S, U1-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	57.86
Std Dev	2.806

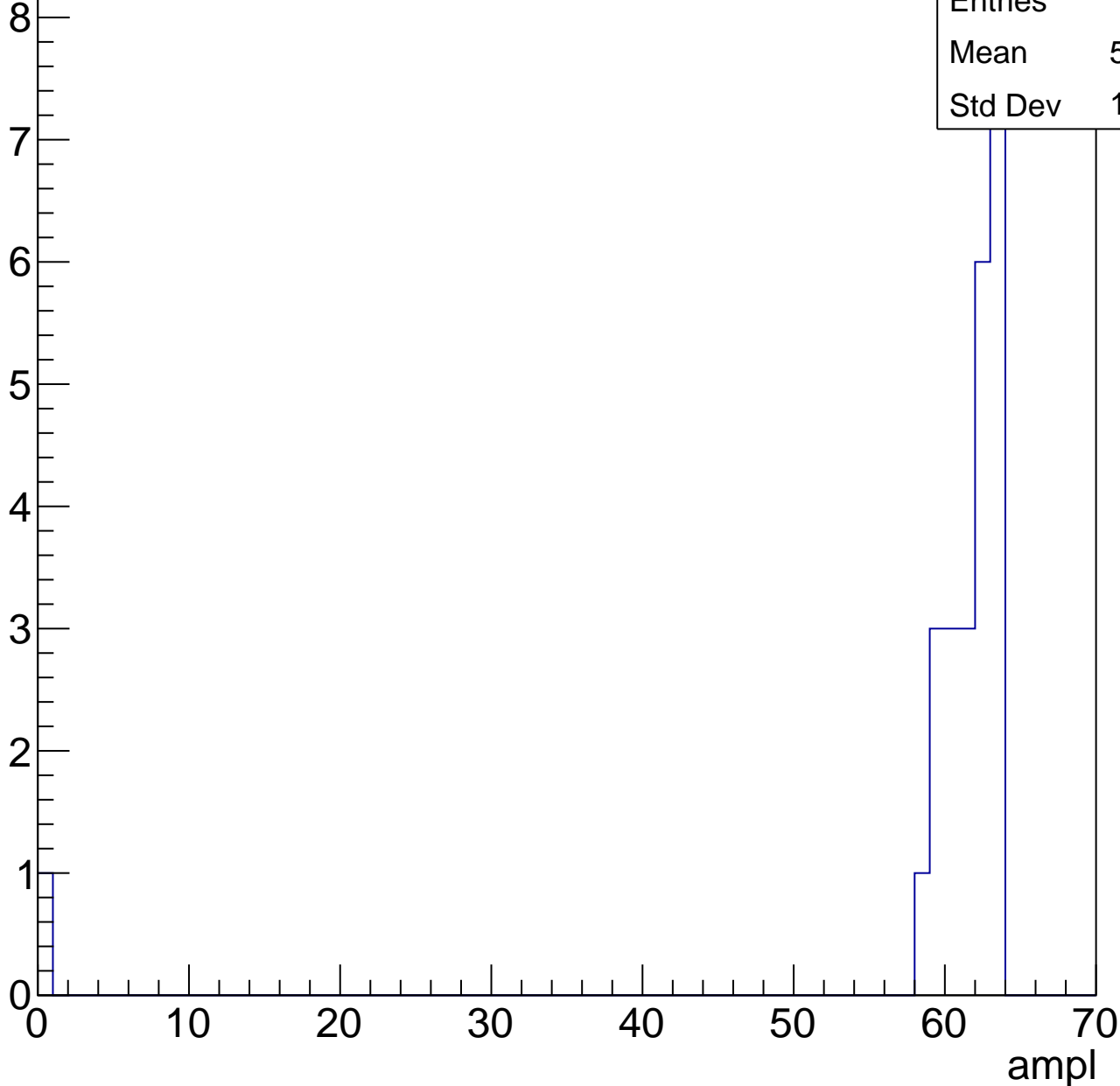


# B1L000S, U1-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.96
Std Dev	12.13

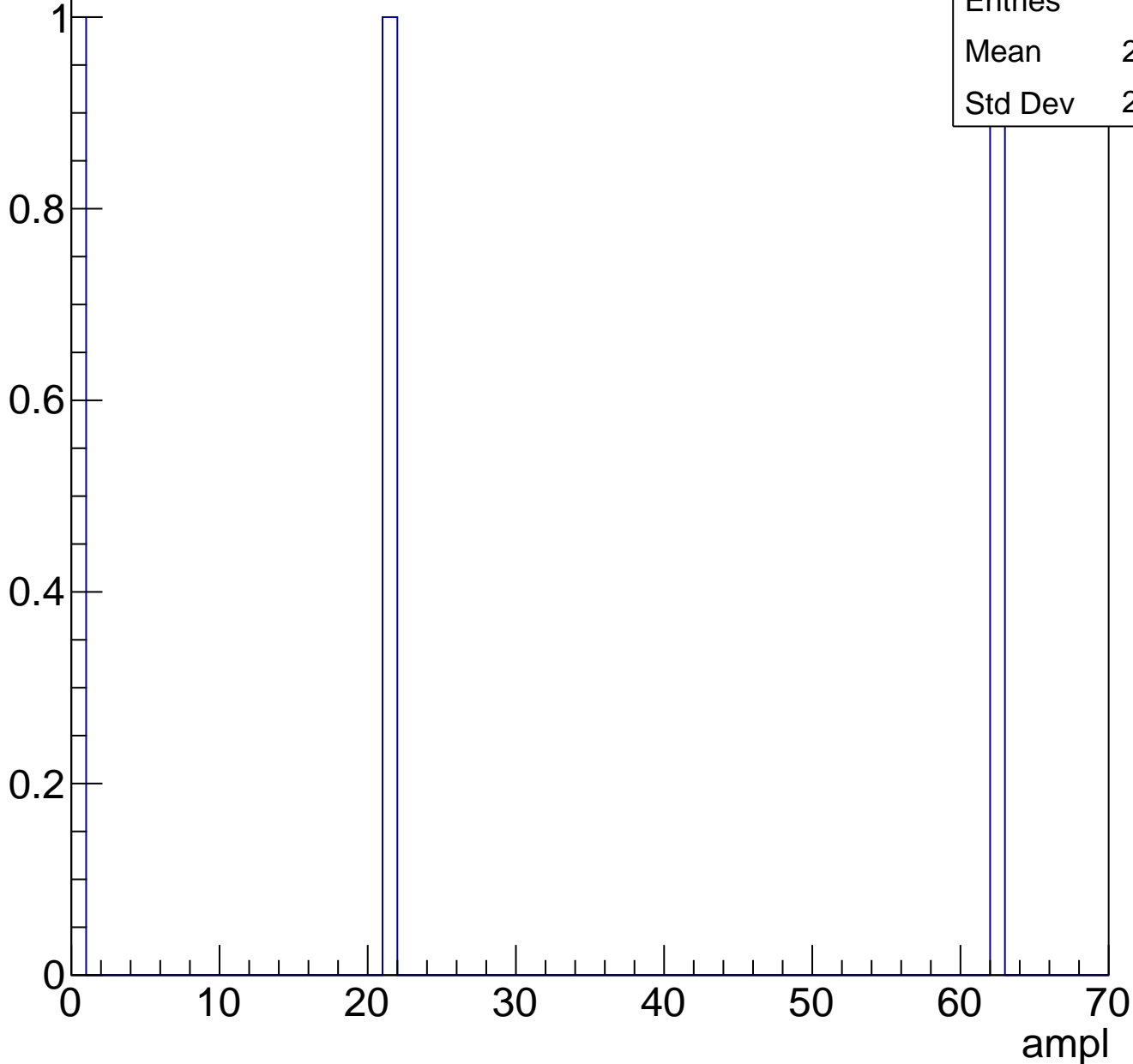




# B1L000S, U1-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	27.67
Std Dev	25.75

# B1L000S, U1-ch91, adc0

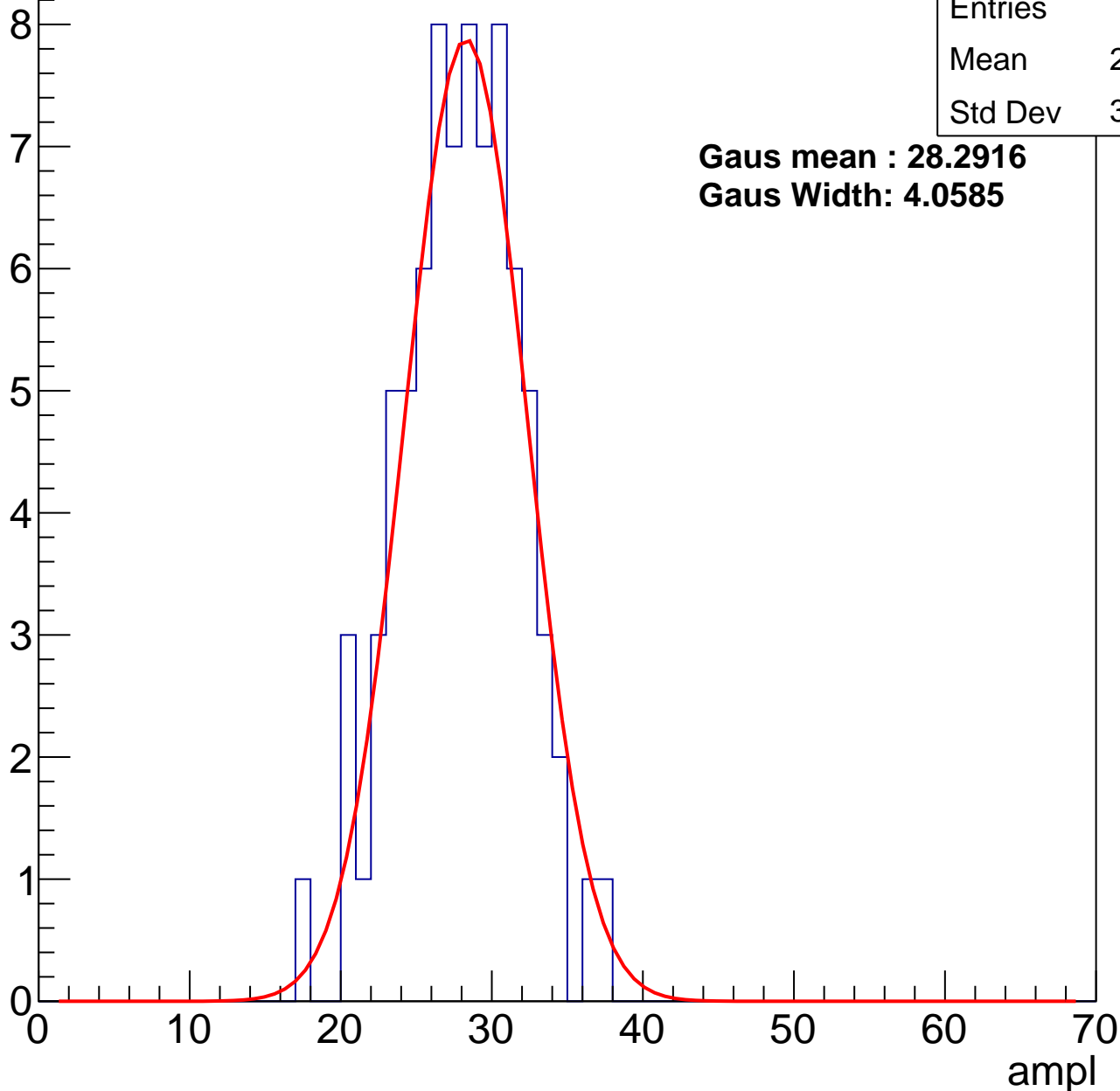
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	27.49
Std Dev	3.892

**Gaus mean : 28.2916**

**Gaus Width: 4.0585**



# B1L000S, U1-ch91, adc1

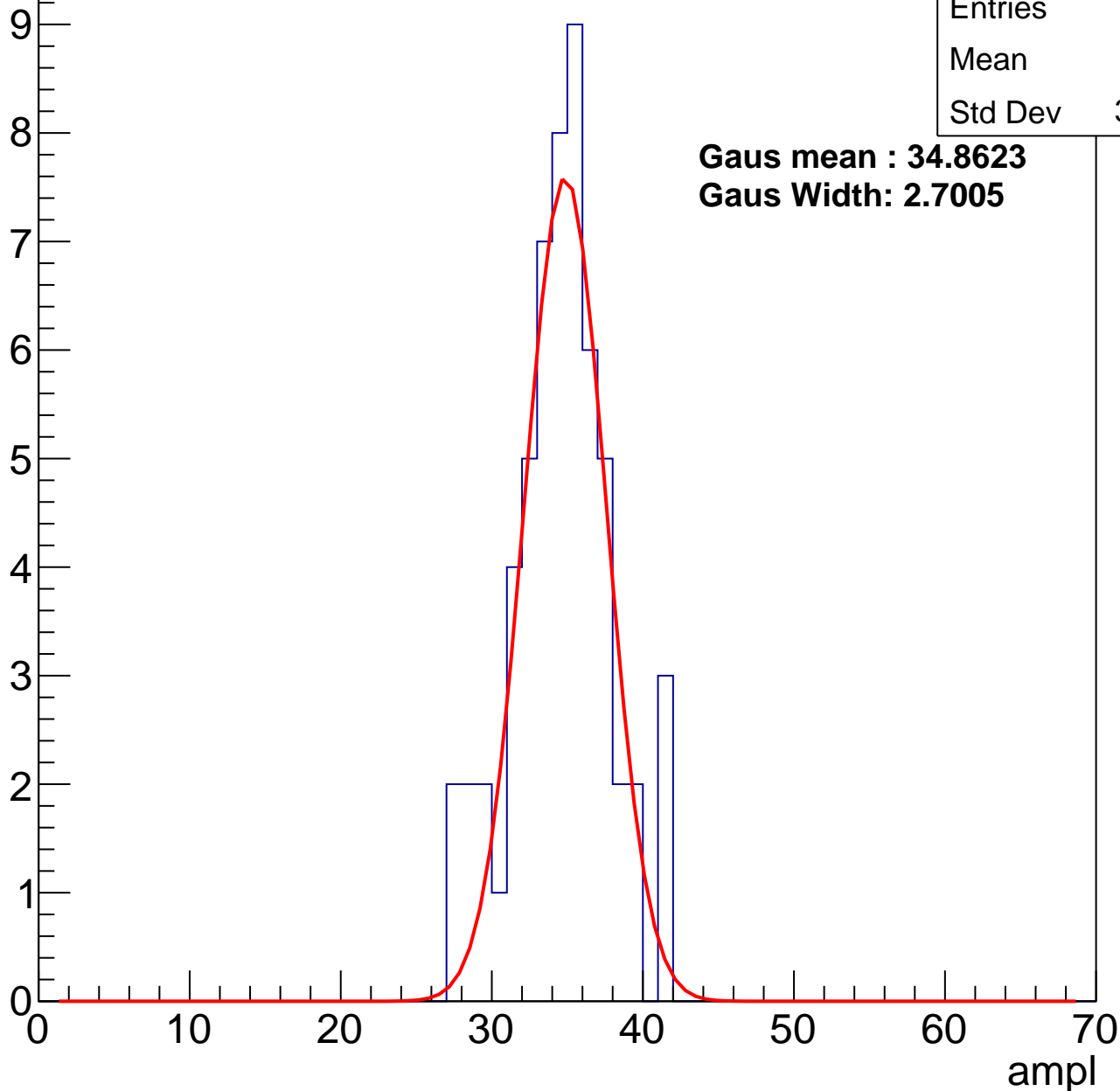
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.1
Std Dev	3.231

**Gaus mean : 34.8623**

**Gaus Width: 2.7005**



# B1L000S, U1-ch91, adc2

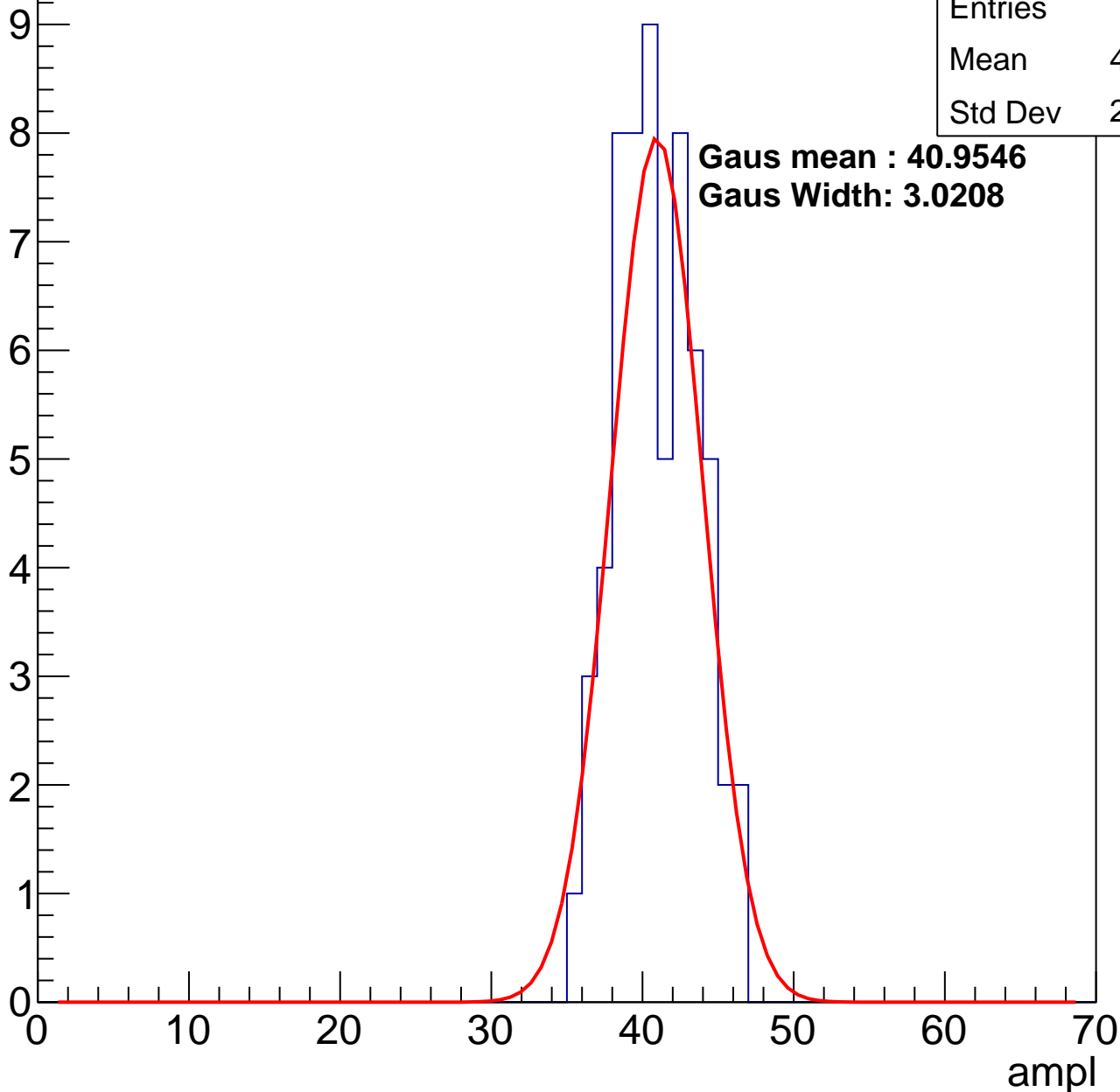
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	40.46
Std Dev	2.652

**Gaus mean : 40.9546**

**Gaus Width: 3.0208**

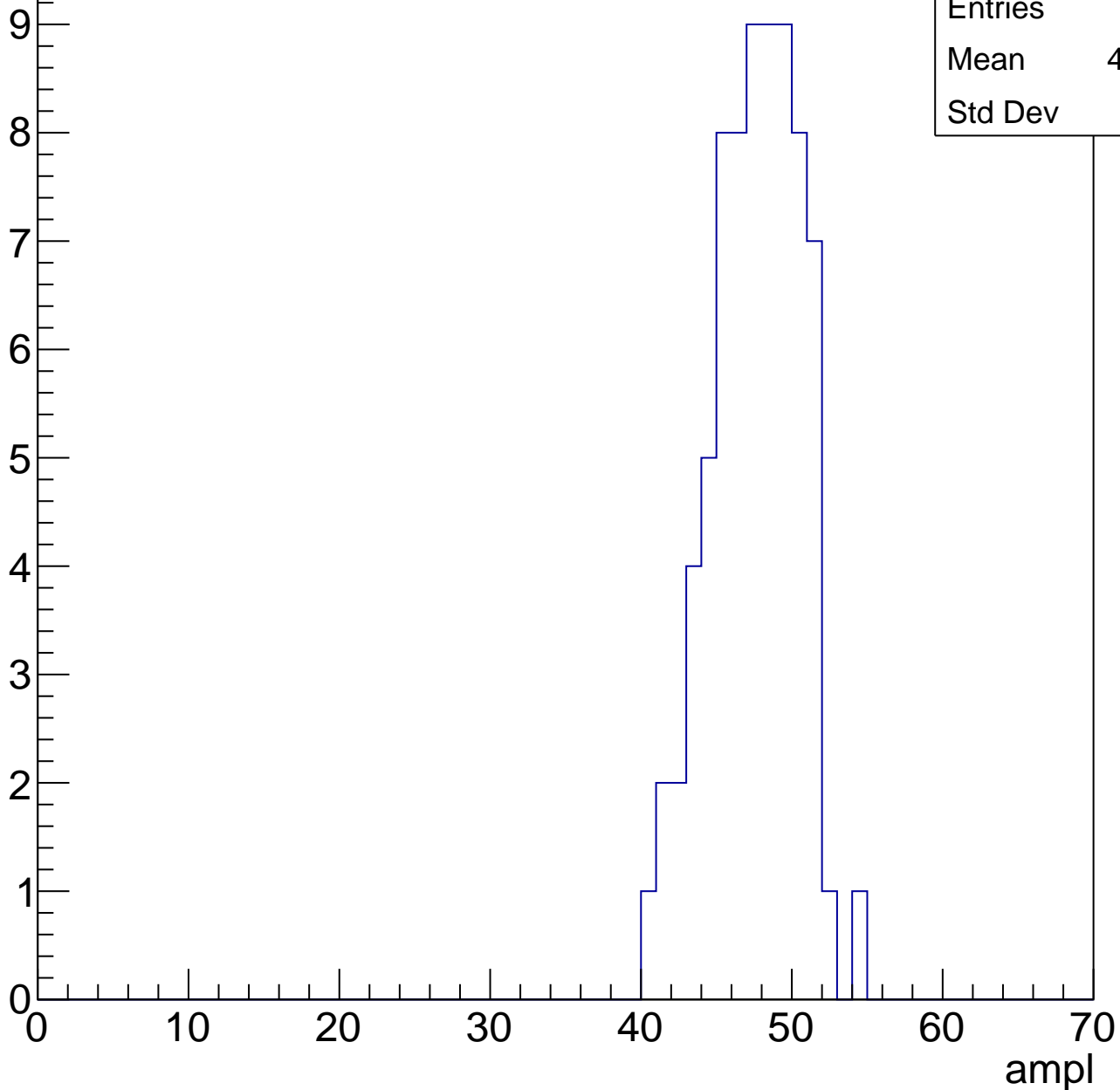


# B1L000S, U1-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	47.09
Std Dev	2.9

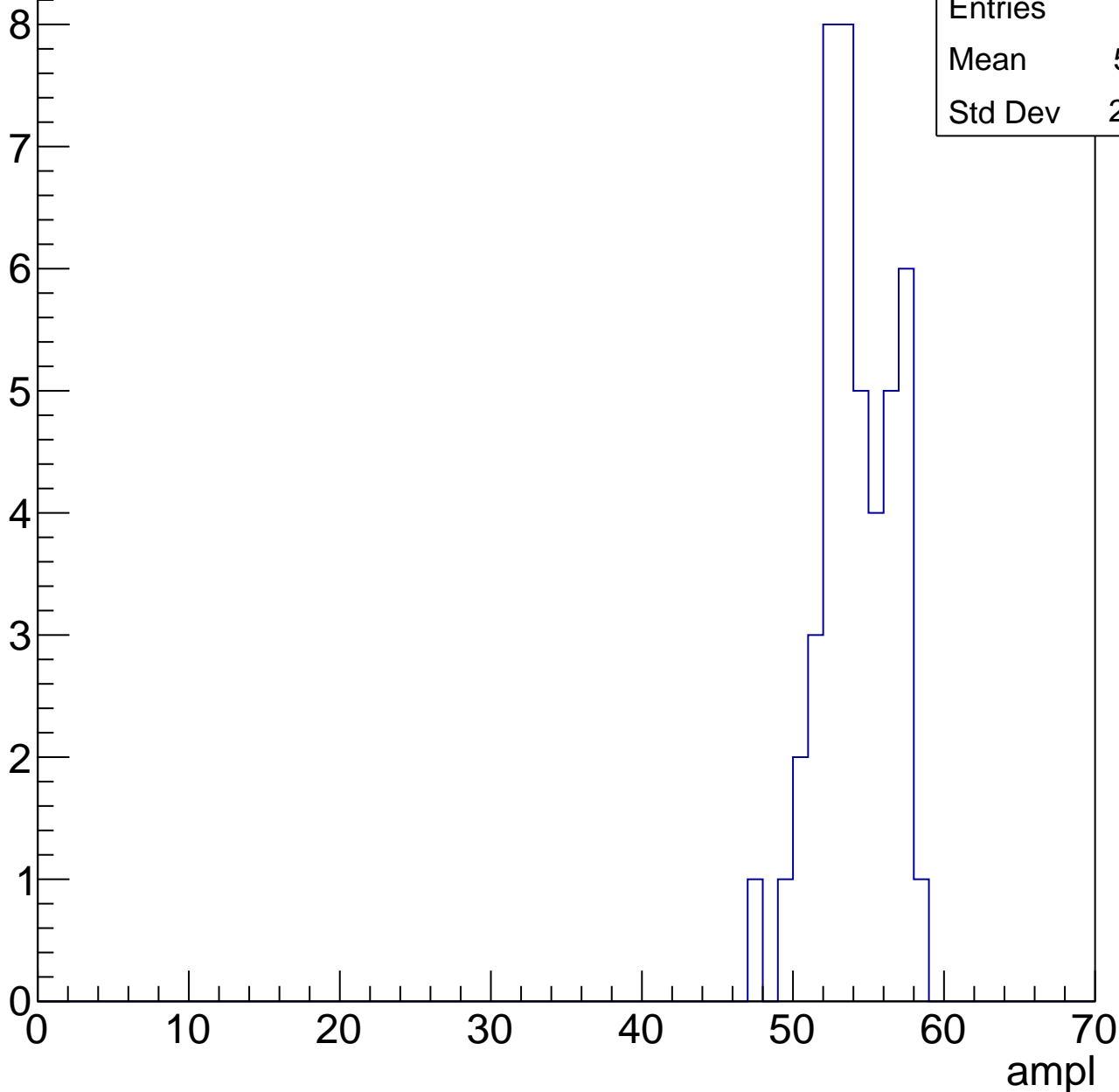


# B1L000S, U1-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	53.61
Std Dev	2.433

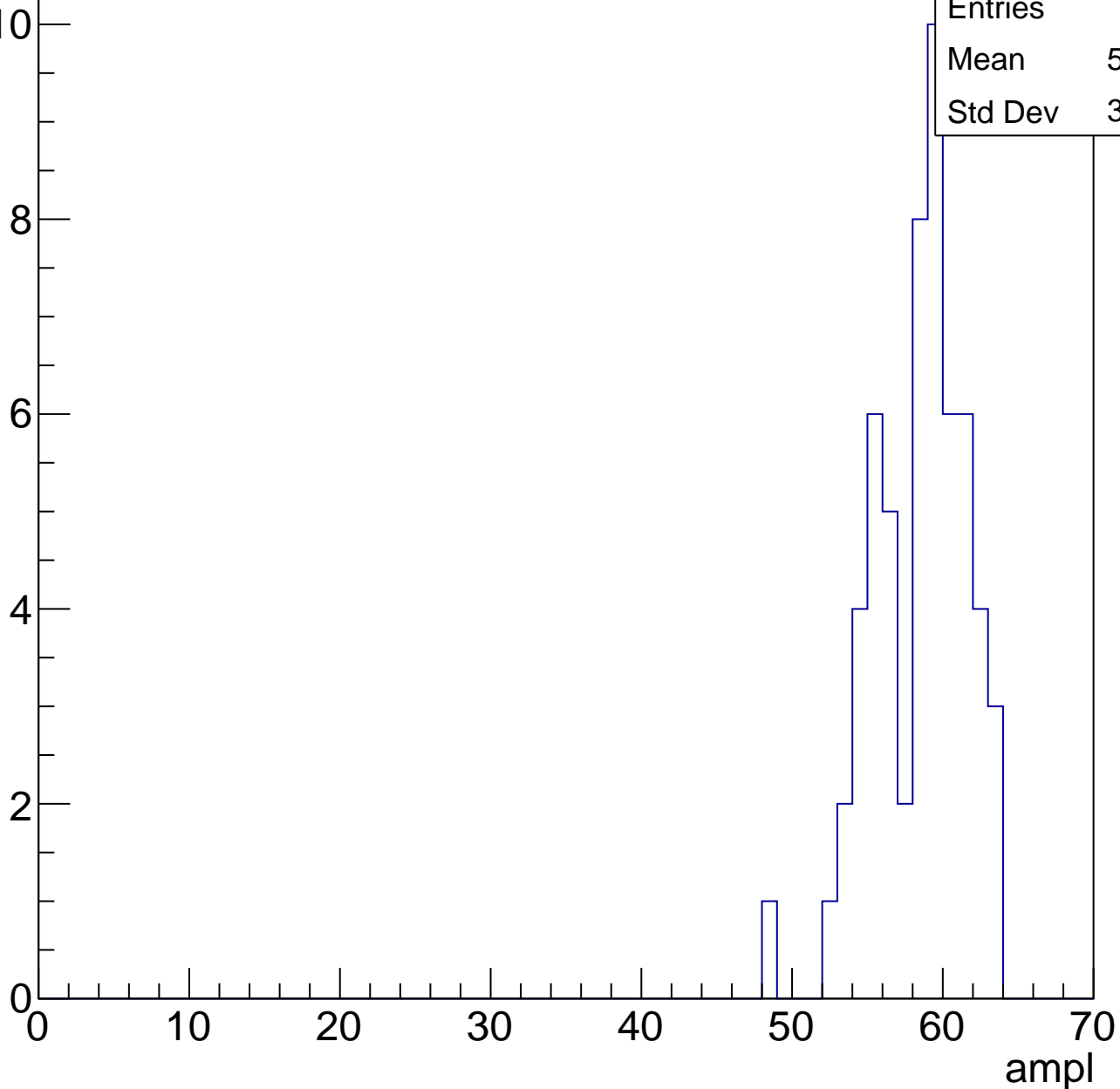


# B1L000S, U1-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	57.98
Std Dev	3.088

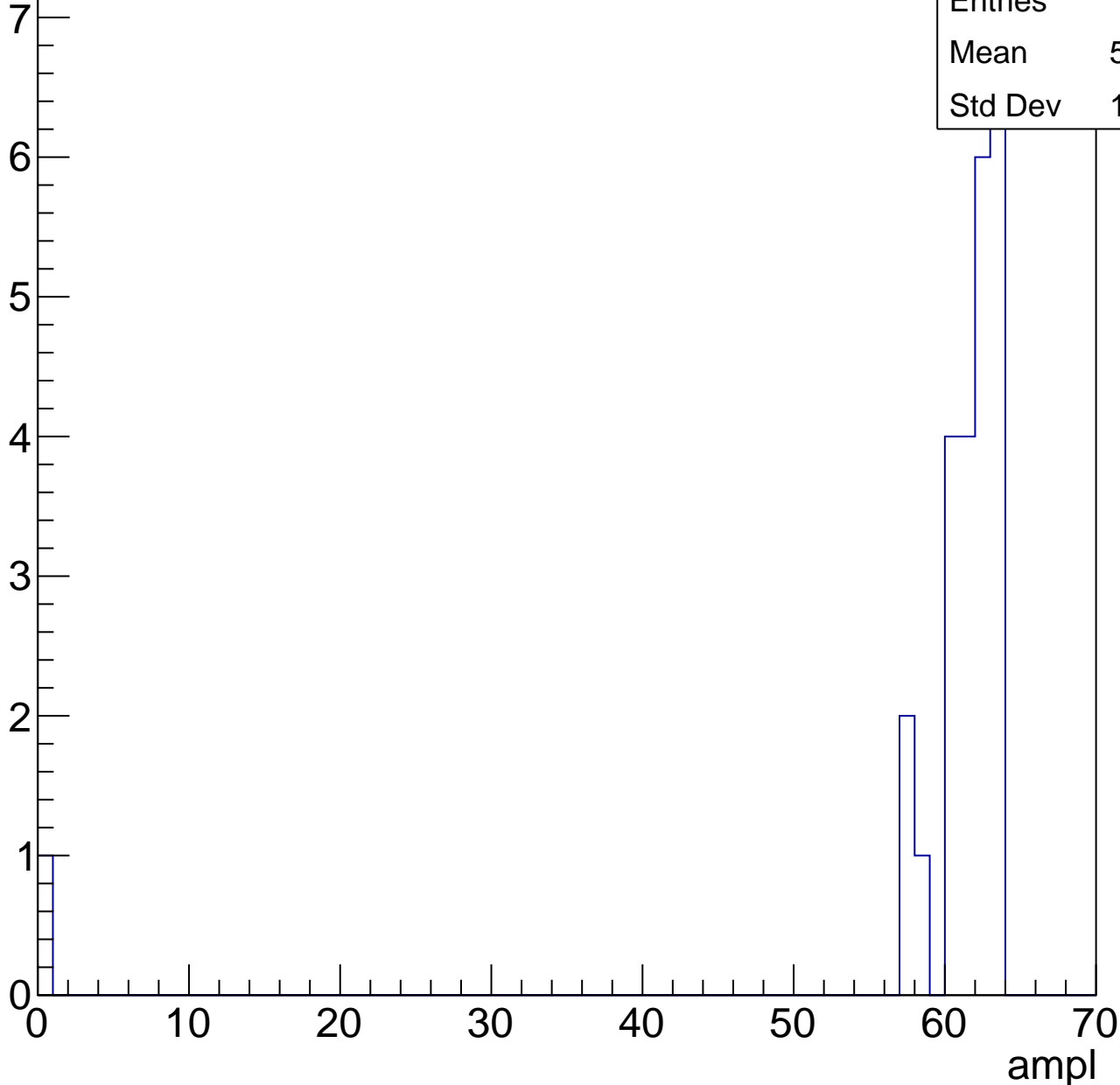


# B1L000S, U1-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.76
Std Dev	12.12





# B1L000S, U1-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U1-ch92, adc0

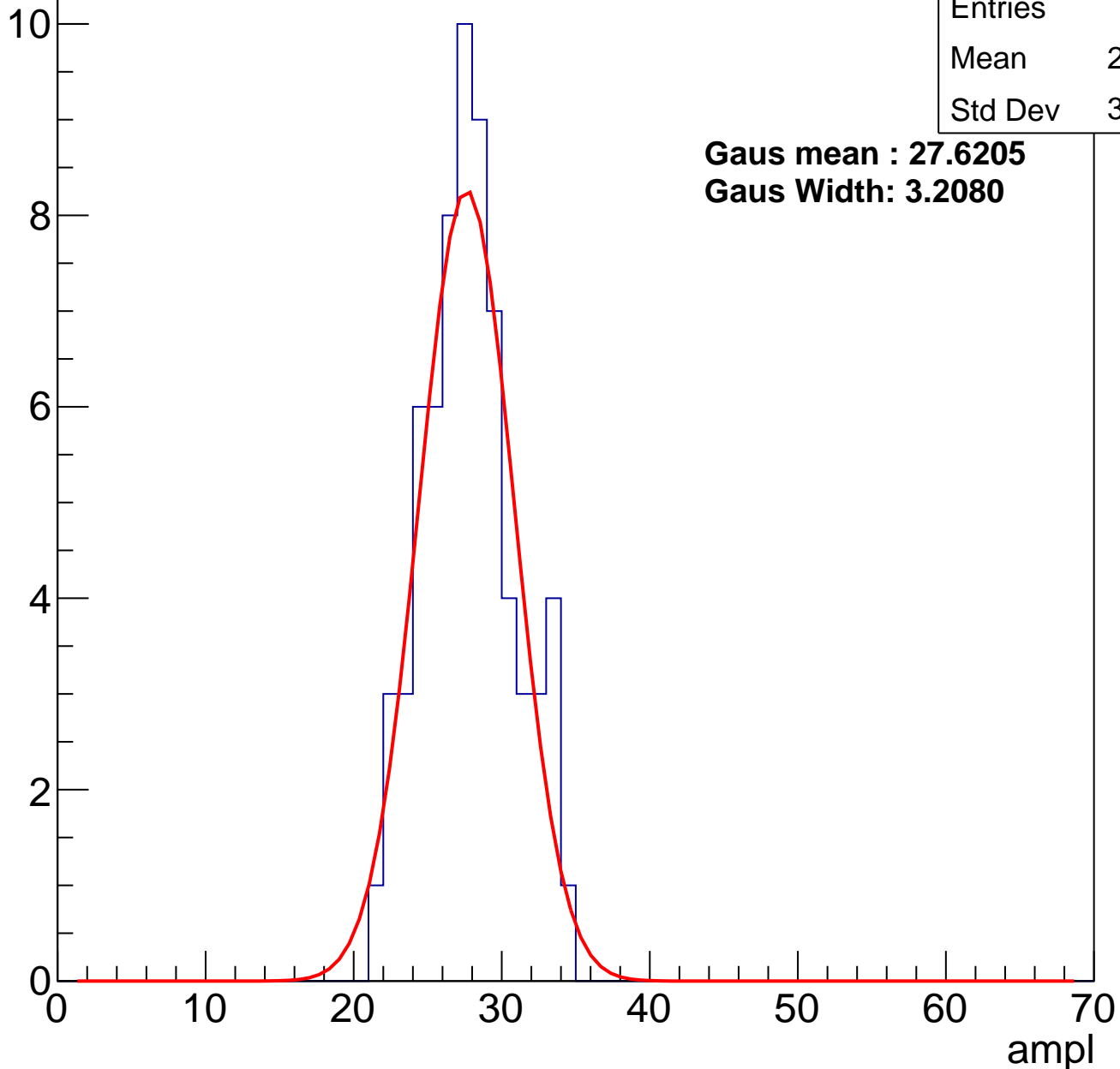
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	27.32
Std Dev	3.036

**Gaus mean : 27.6205**

**Gaus Width: 3.2080**

Entry



# B1L000S, U1-ch92, adc1

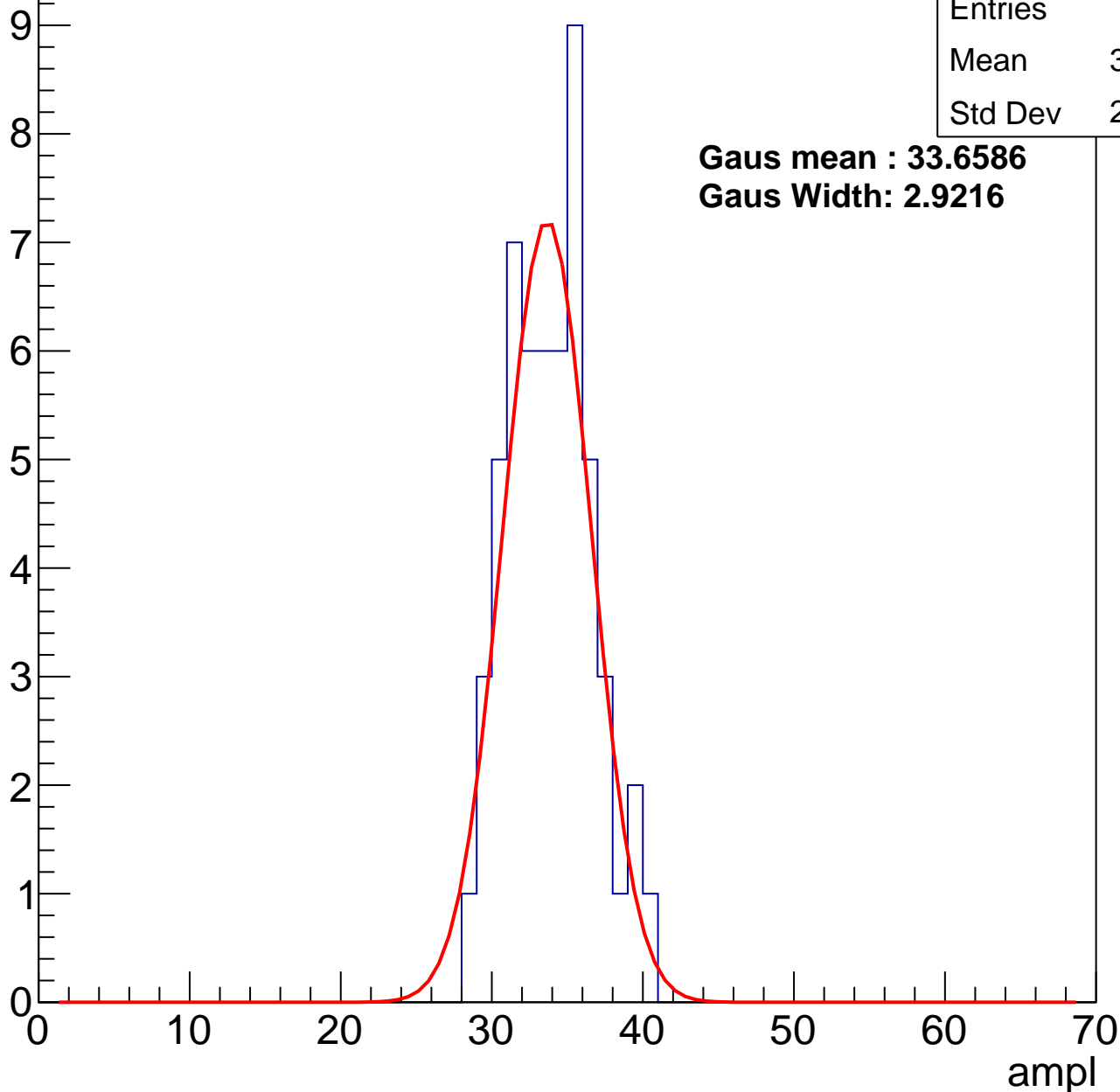
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	33.42
Std Dev	2.775

**Gaus mean : 33.6586**

**Gaus Width: 2.9216**



# B1L000S, U1-ch92, adc2

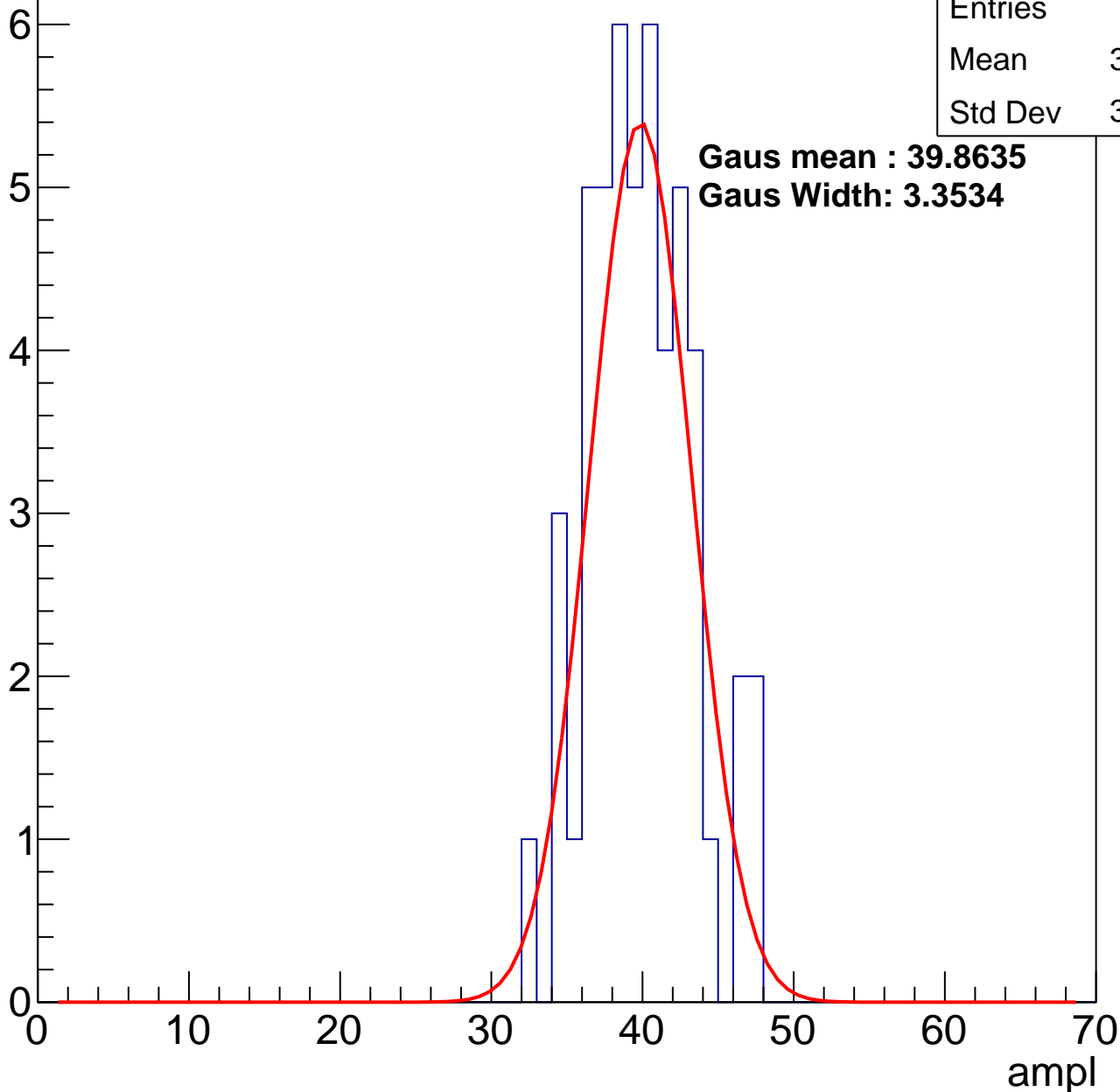
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	39.46
Std Dev	3.413

**Gaus mean : 39.8635**

**Gaus Width: 3.3534**



# B1L000S, U1-ch92, adc3

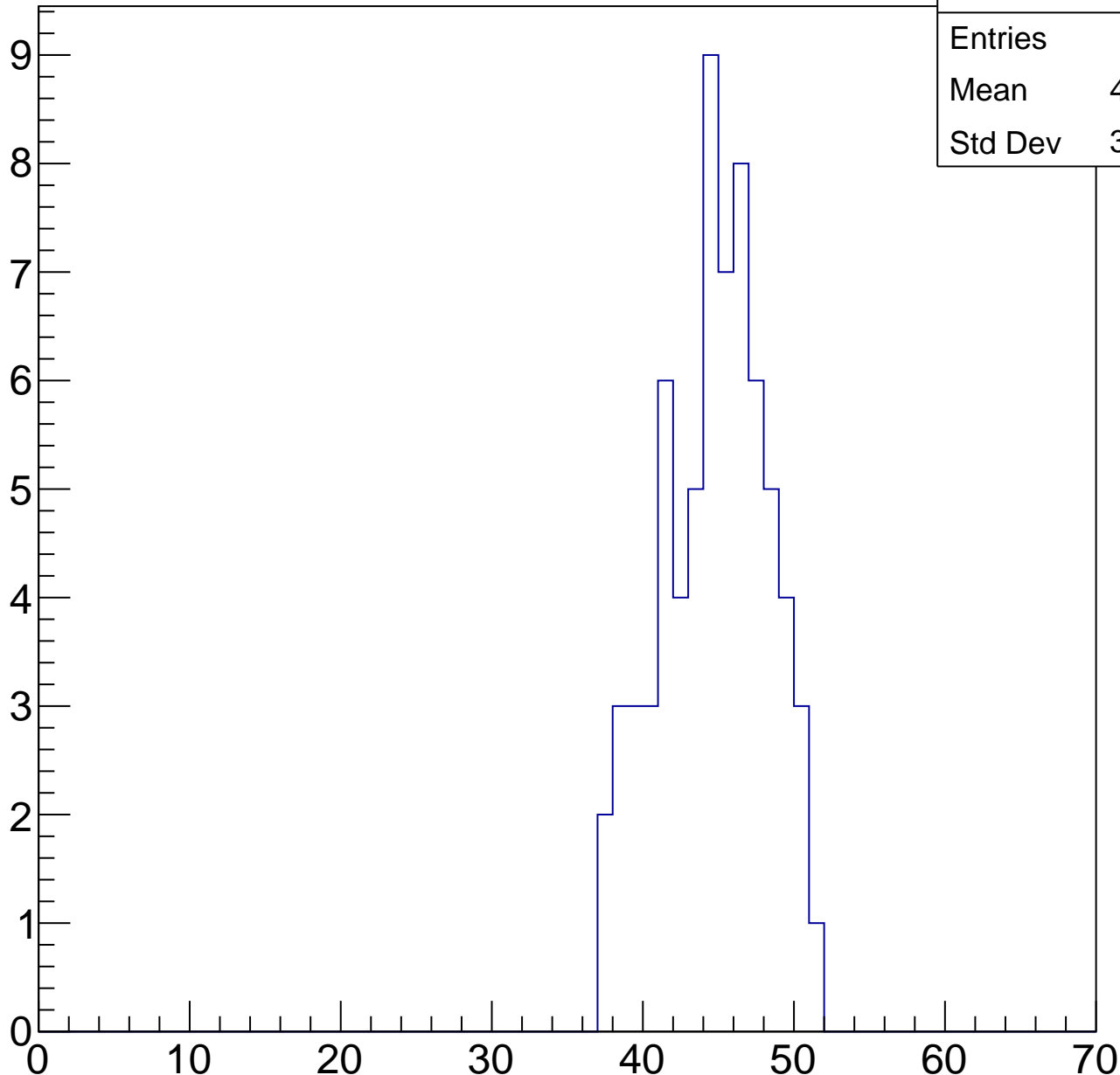
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	69
Mean	44.23
Std Dev	3.469

ampl

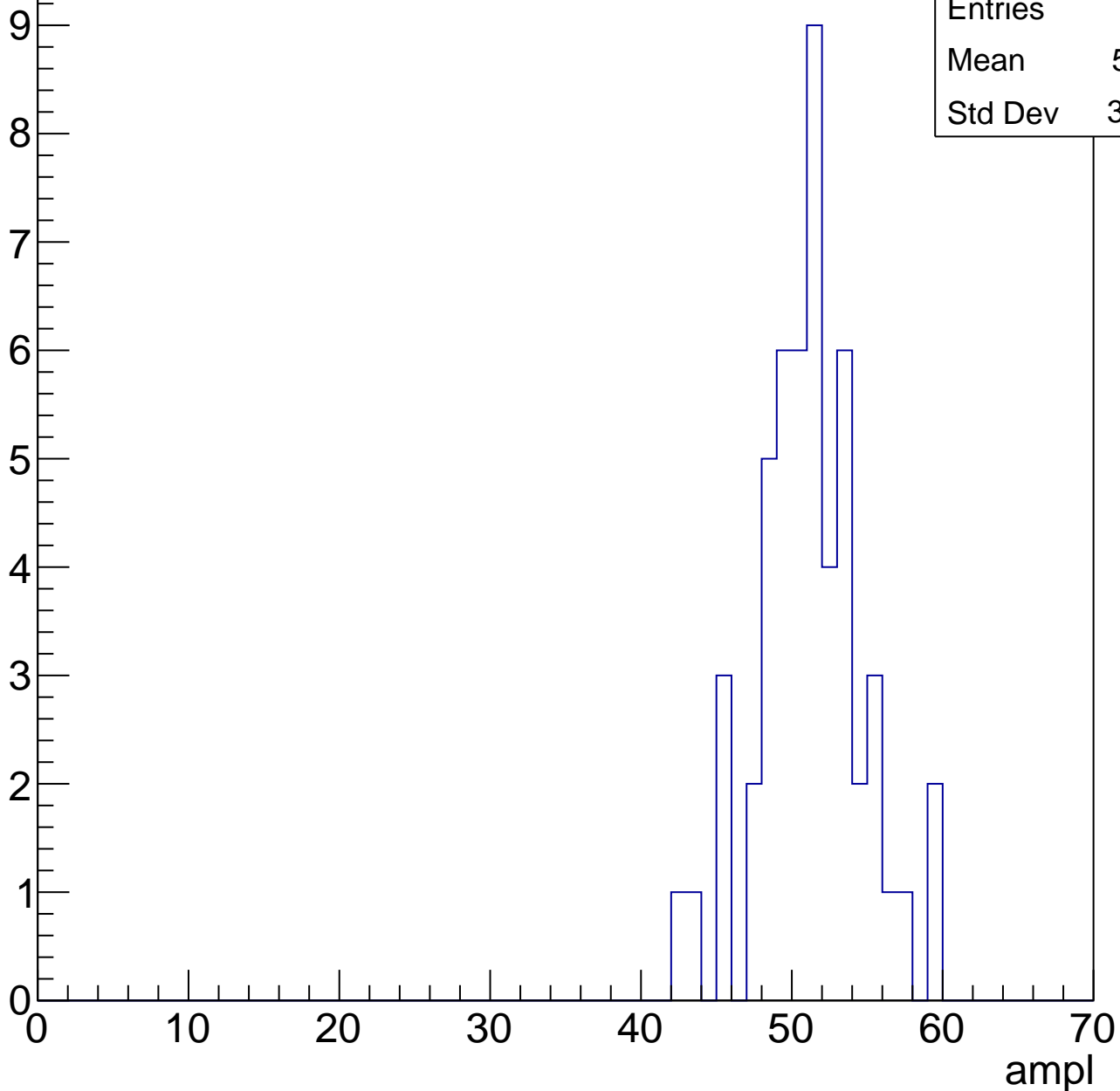


# B1L000S, U1-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

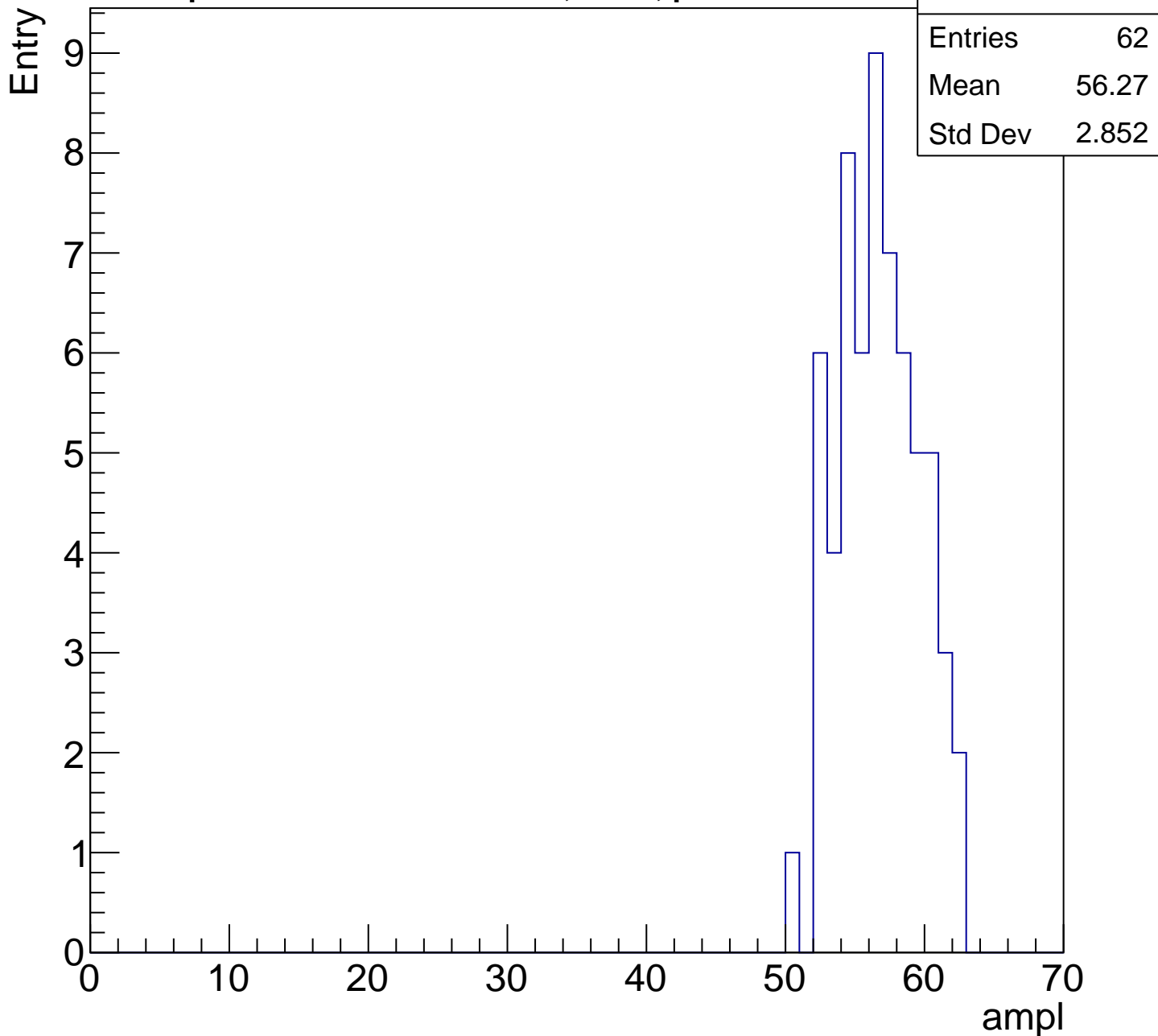
Entry

Entries	52
Mean	50.71
Std Dev	3.505



# B1L000S, U1-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

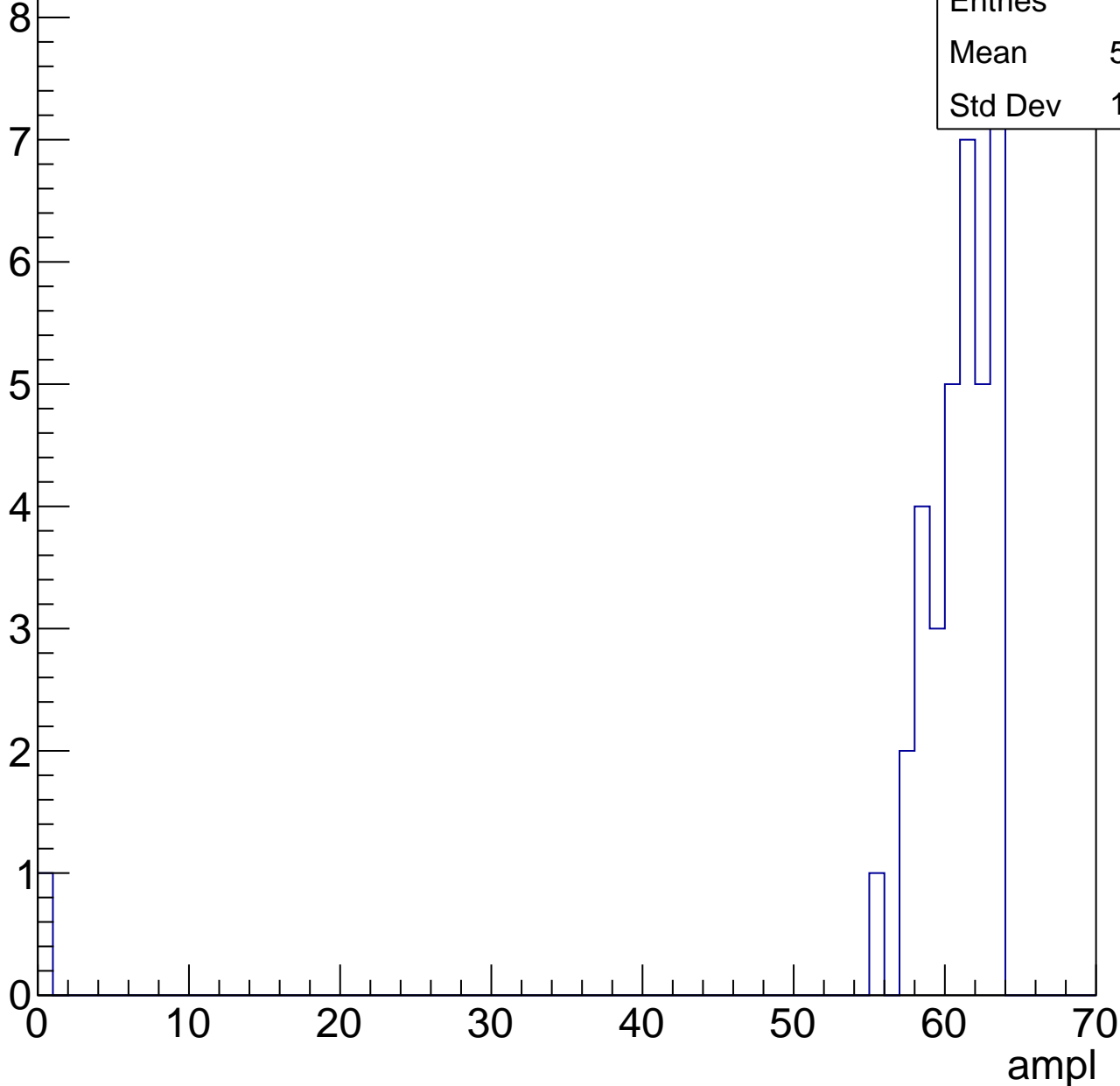


# B1L000S, U1-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	36
Mean	58.86
Std Dev	10.16

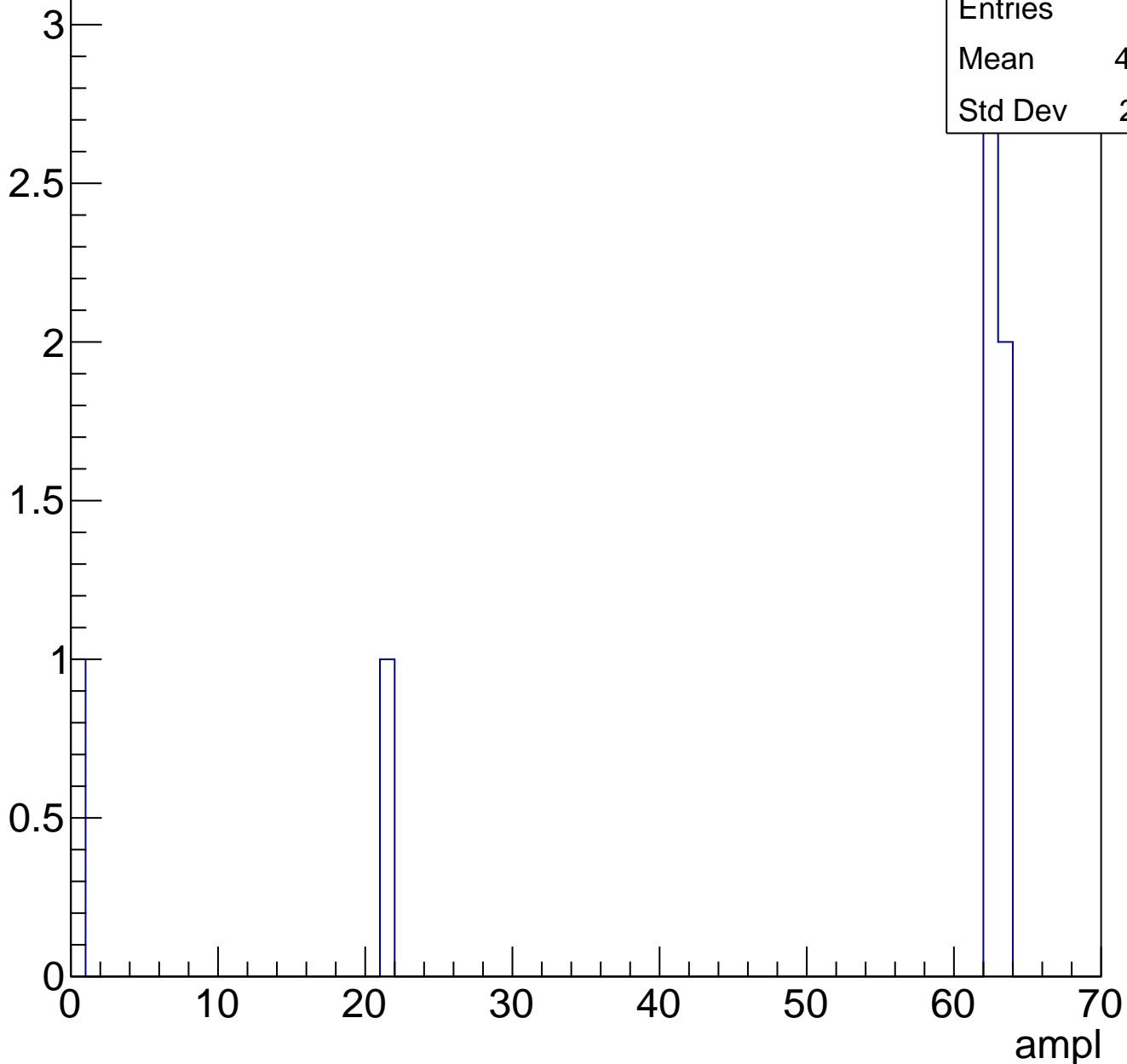




# B1L000S, U1-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch93, adc0

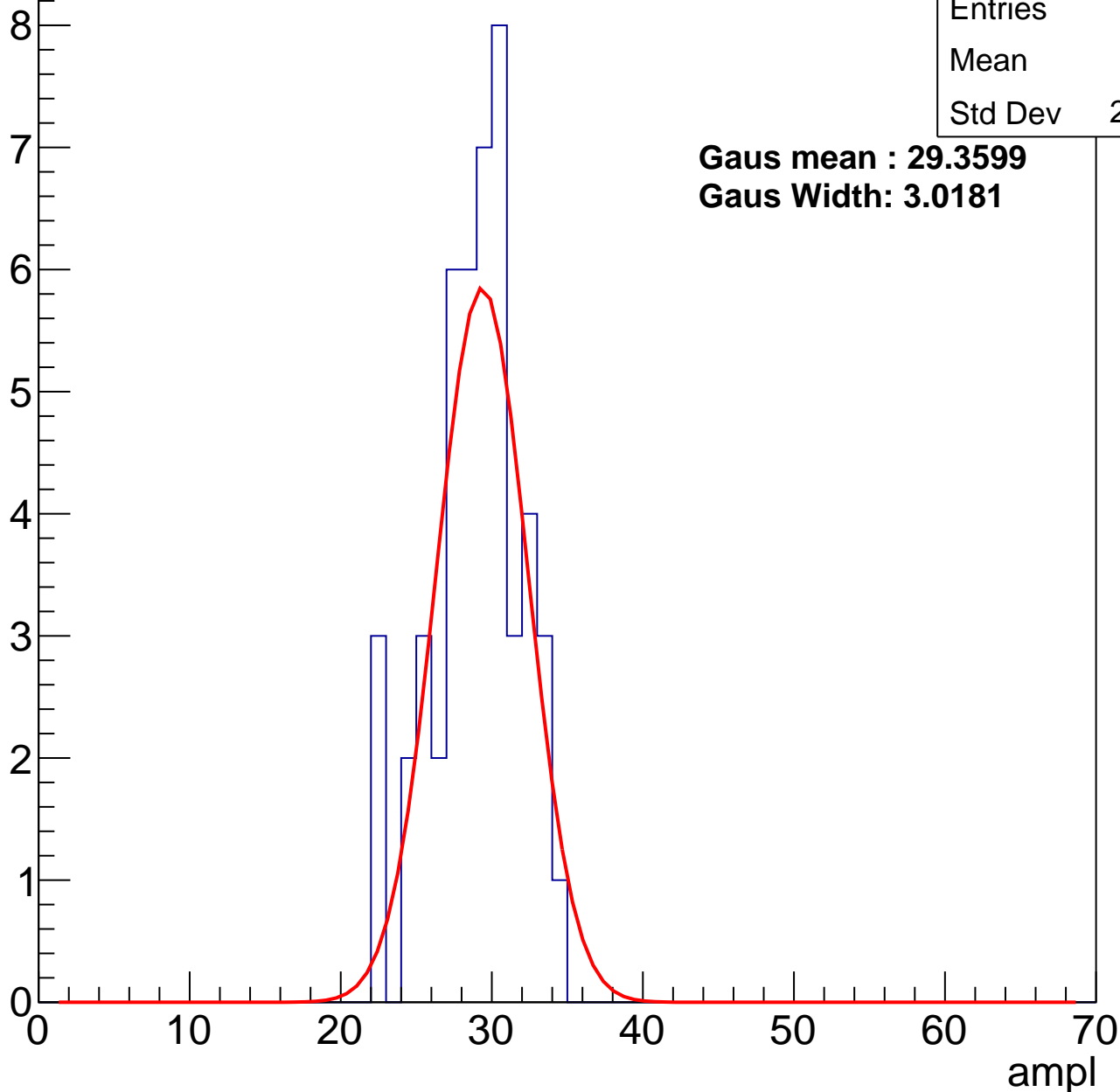
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	28.5
Std Dev	2.923

**Gaus mean : 29.3599**

**Gaus Width: 3.0181**



# B1L000S, U1-ch93, adc1

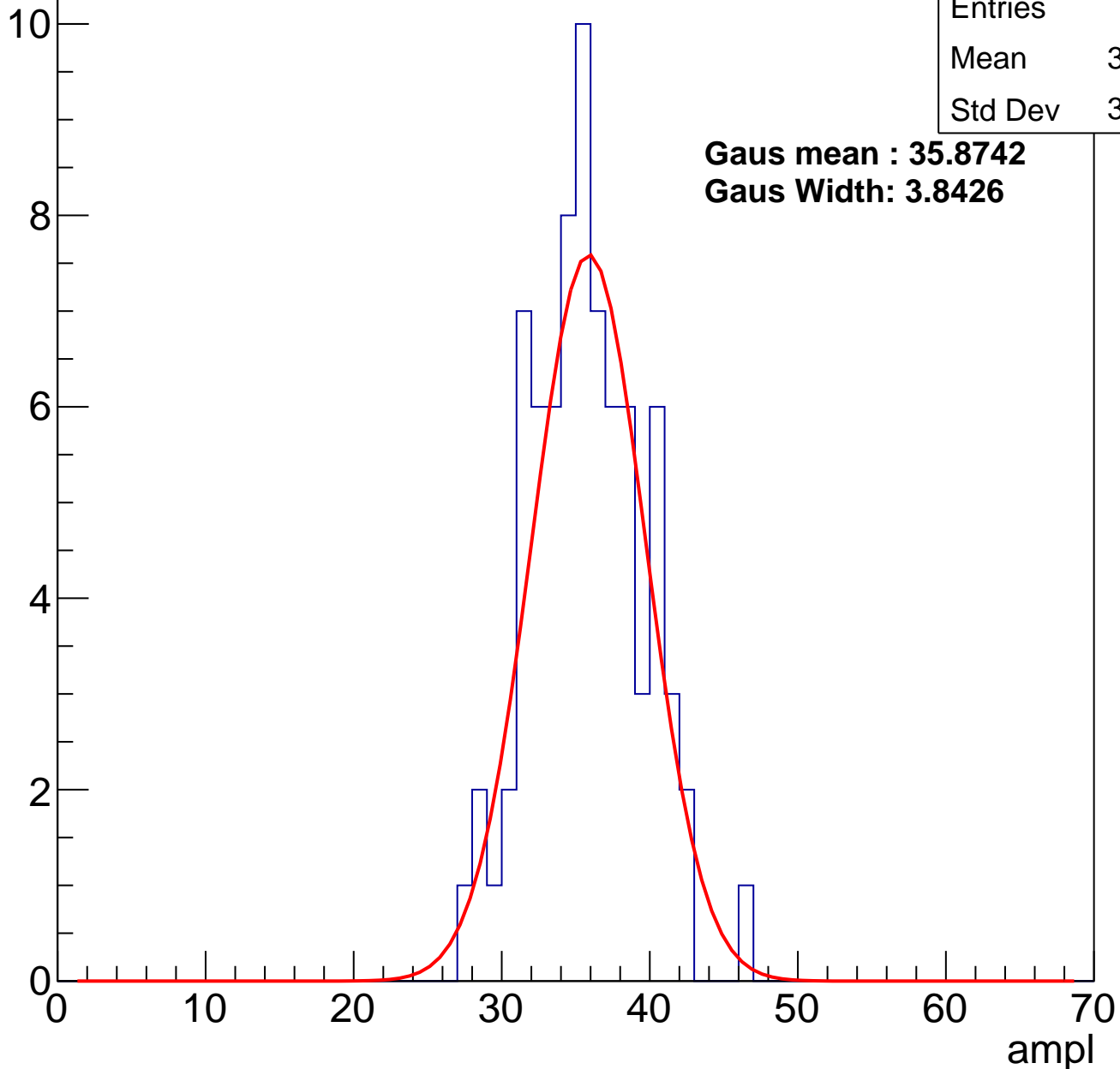
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	35.23
Std Dev	3.696

**Gaus mean : 35.8742**

**Gaus Width: 3.8426**

Entry



# B1L000S, U1-ch93, adc2

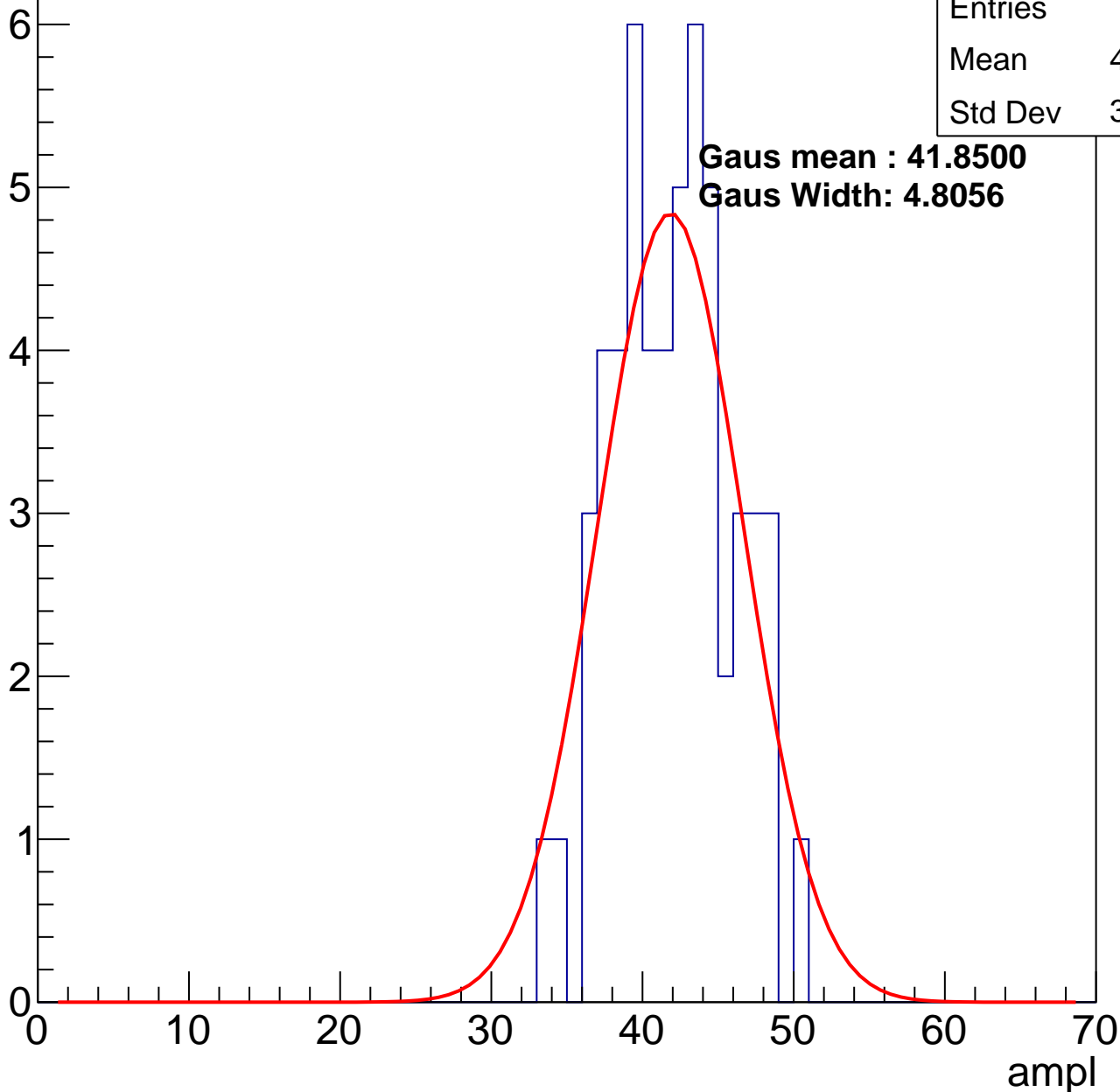
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	41.53
Std Dev	3.856

**Gaus mean : 41.8500**

**Gaus Width: 4.8056**

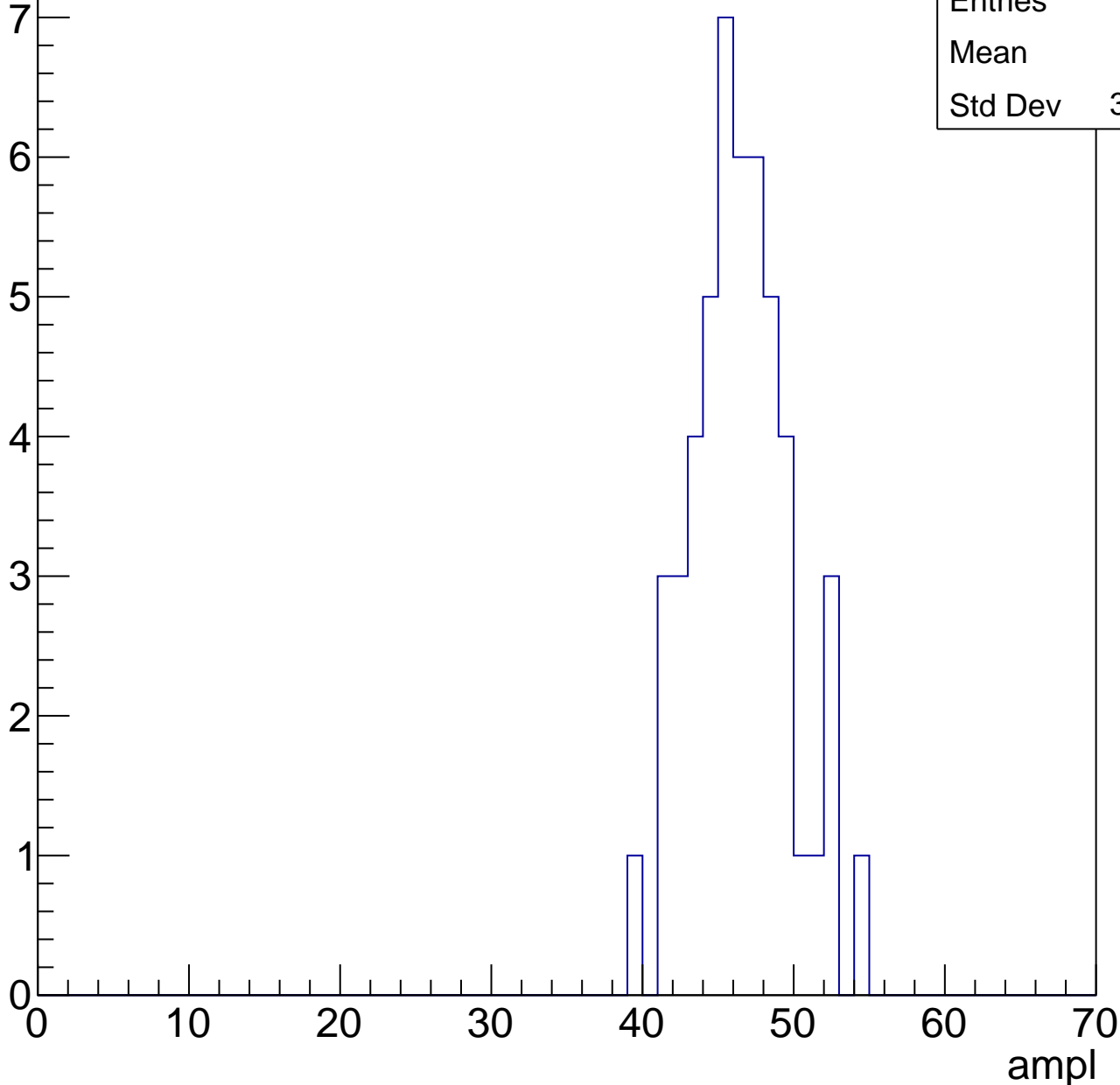


# B1L000S, U1-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	46
Std Dev	3.194

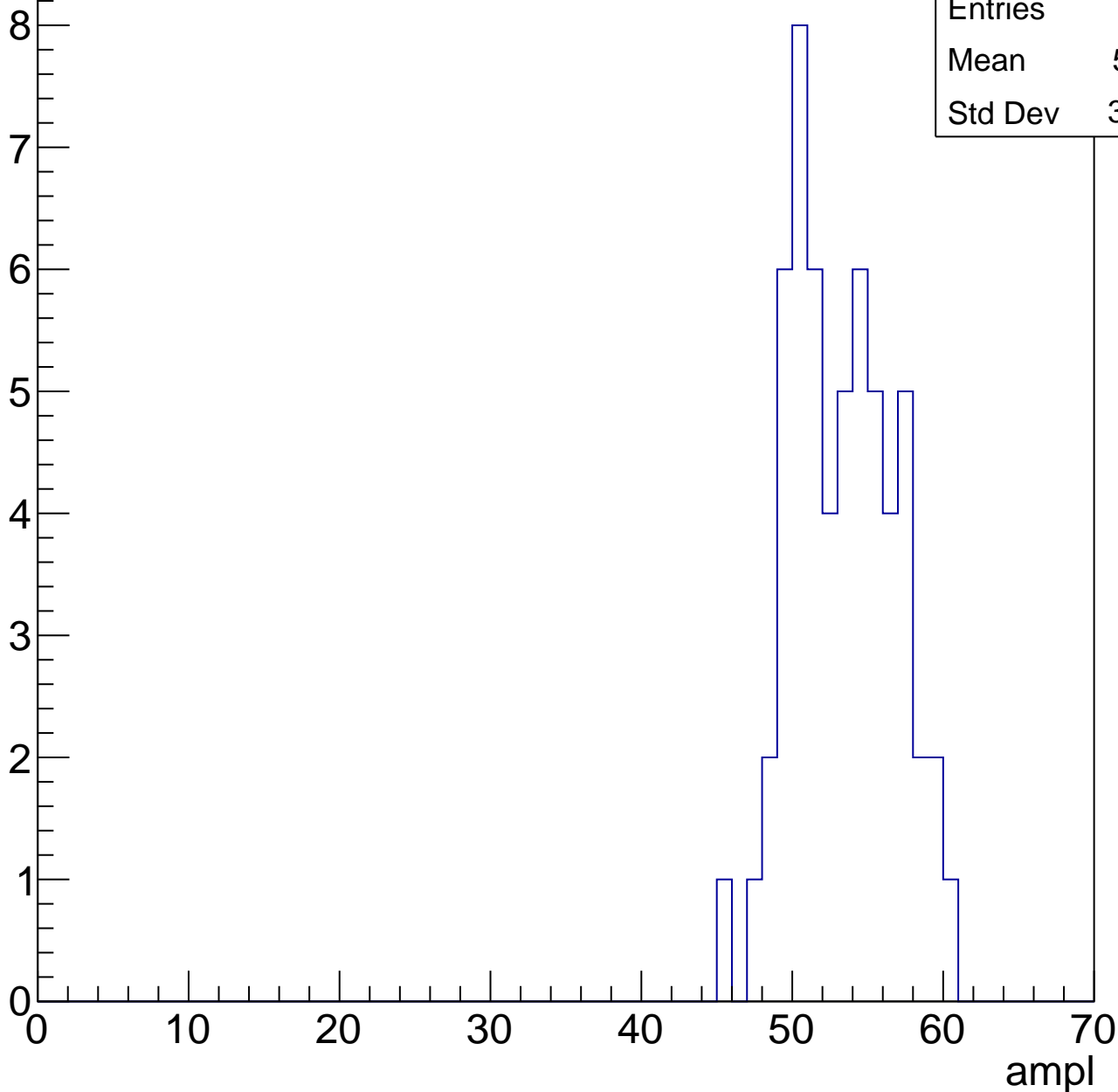


# B1L000S, U1-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

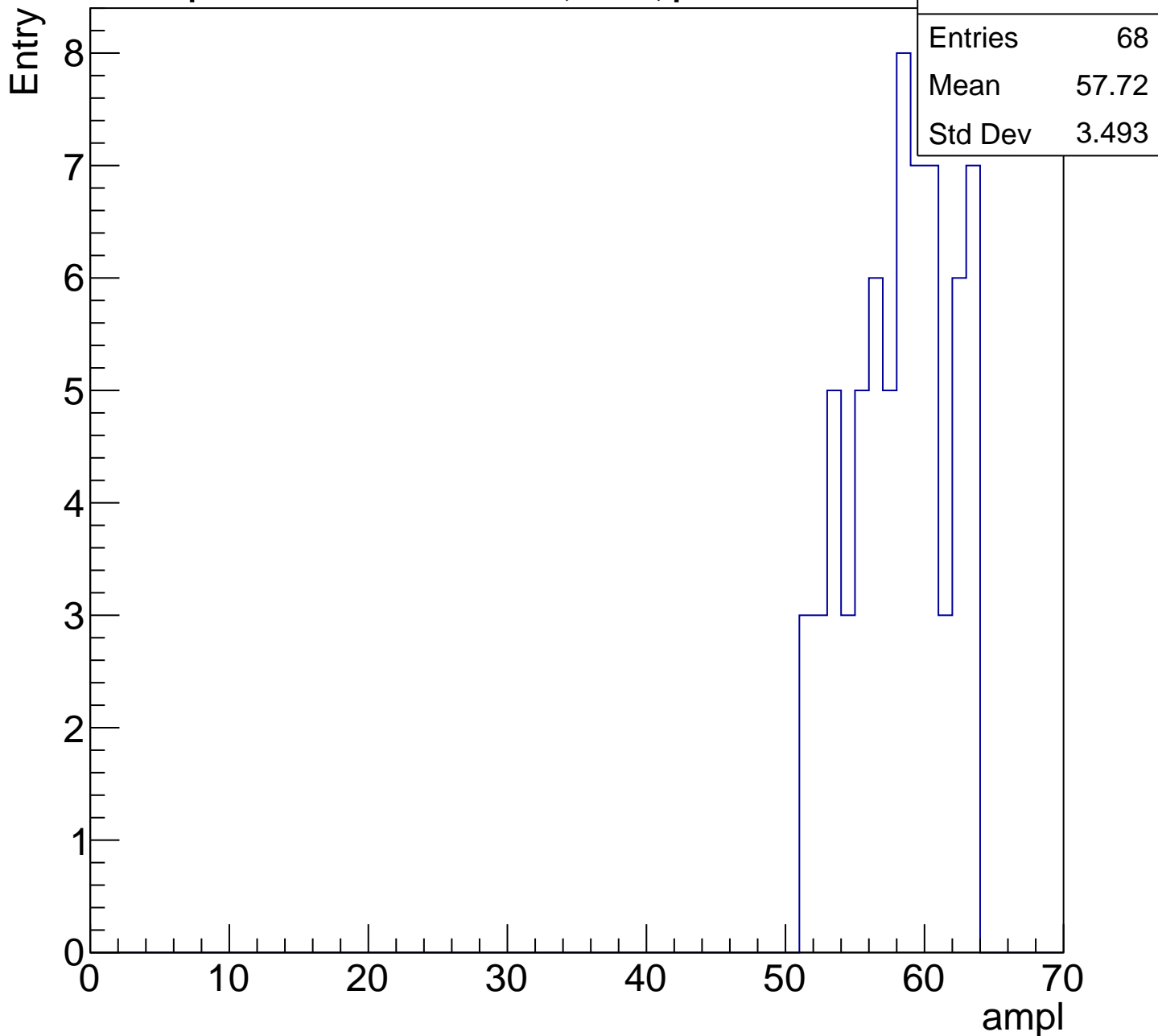
Entry

Entries	58
Mean	52.81
Std Dev	3.365



# B1L000S, U1-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

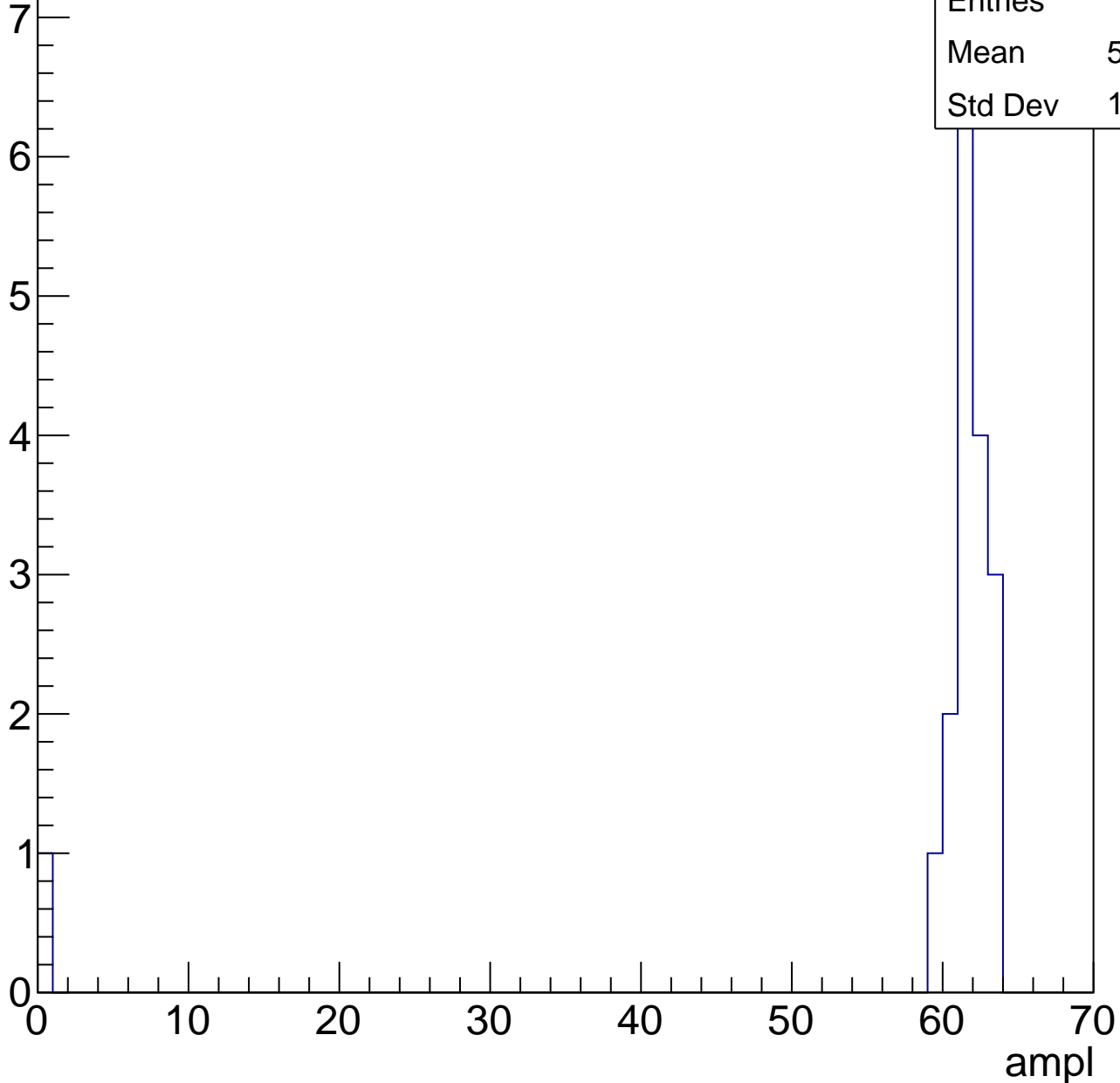


# B1L000S, U1-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	57.94
Std Dev	14.09





# B1L000S, U1-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch94, adc0

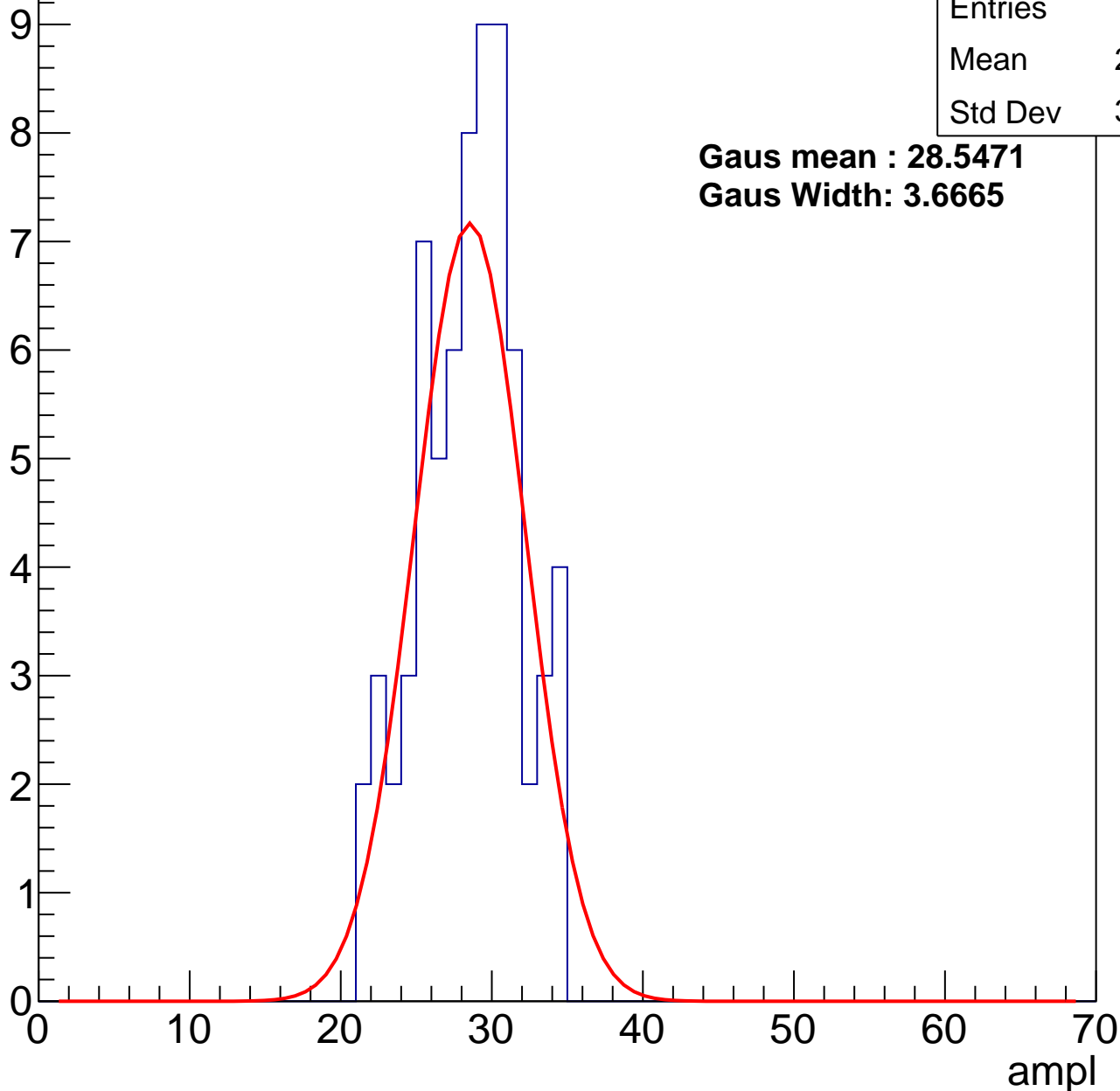
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.01
Std Dev	3.281

**Gaus mean : 28.5471**

**Gaus Width: 3.6665**



# B1L000S, U1-ch94, adc1

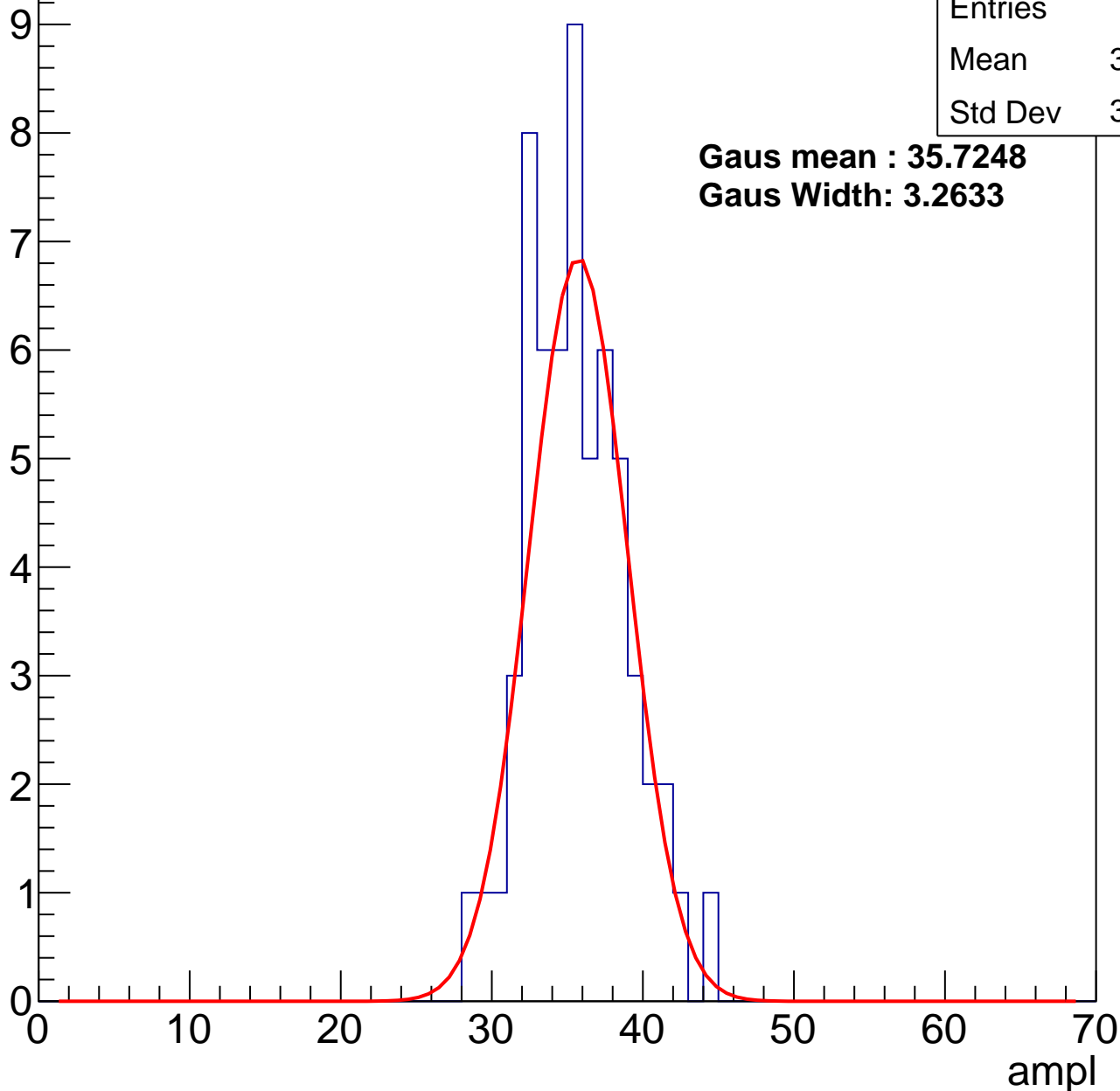
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	35.17
Std Dev	3.246

**Gaus mean : 35.7248**

**Gaus Width: 3.2633**



# B1L000S, U1-ch94, adc2

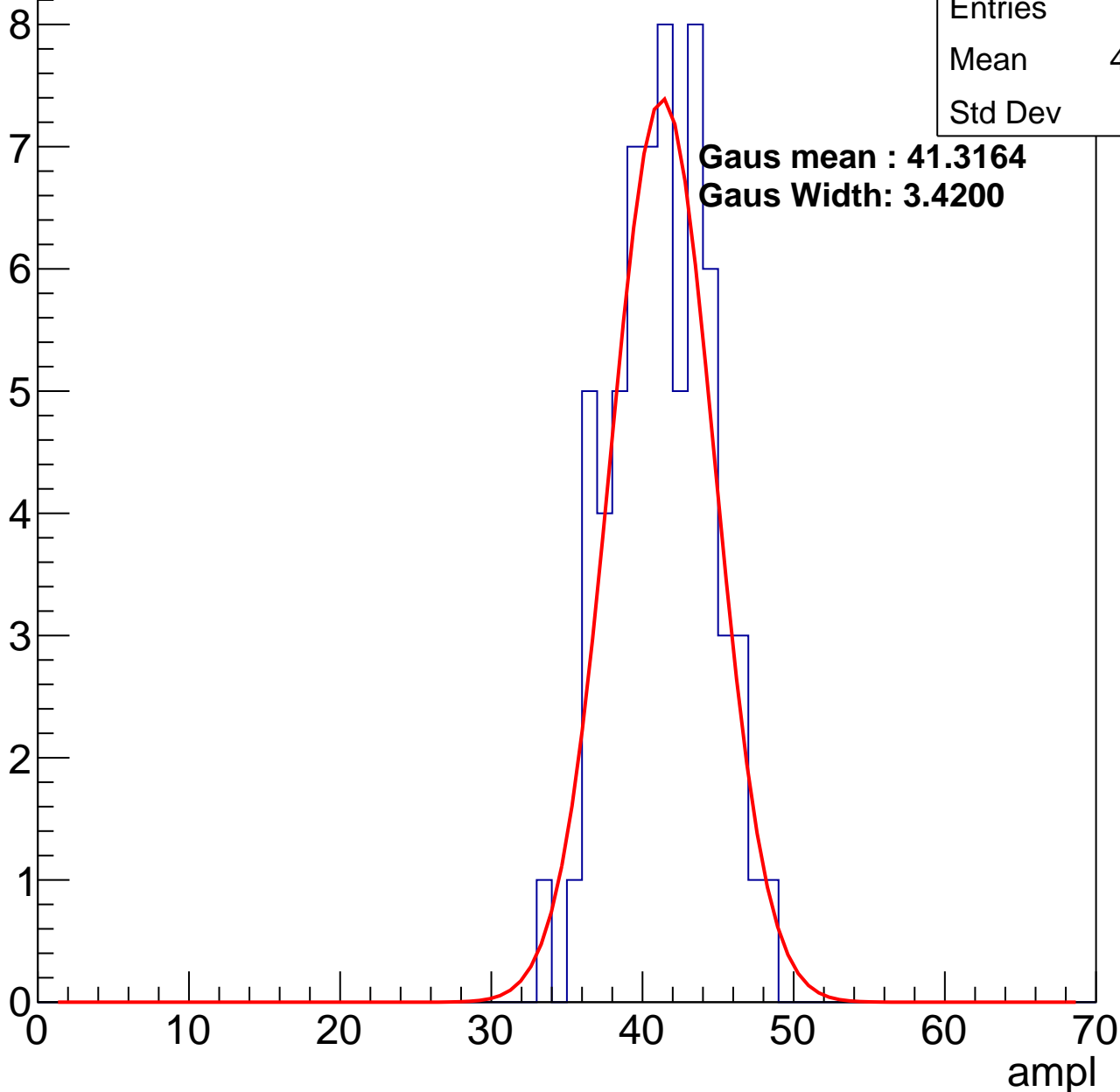
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	40.82
Std Dev	3.2

**Gaus mean : 41.3164**

**Gaus Width: 3.4200**

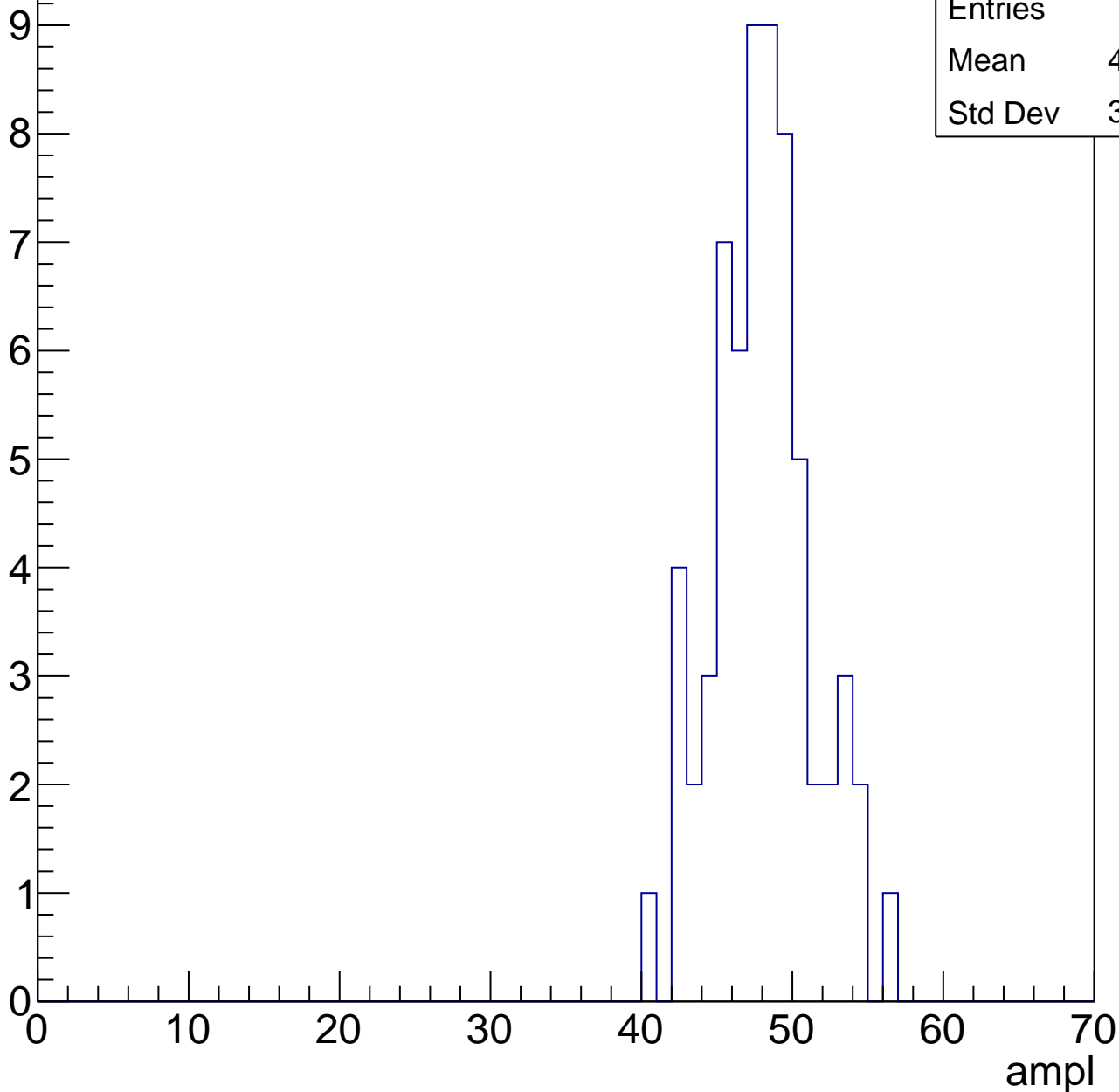


# B1L000S, U1-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	47.55
Std Dev	3.254

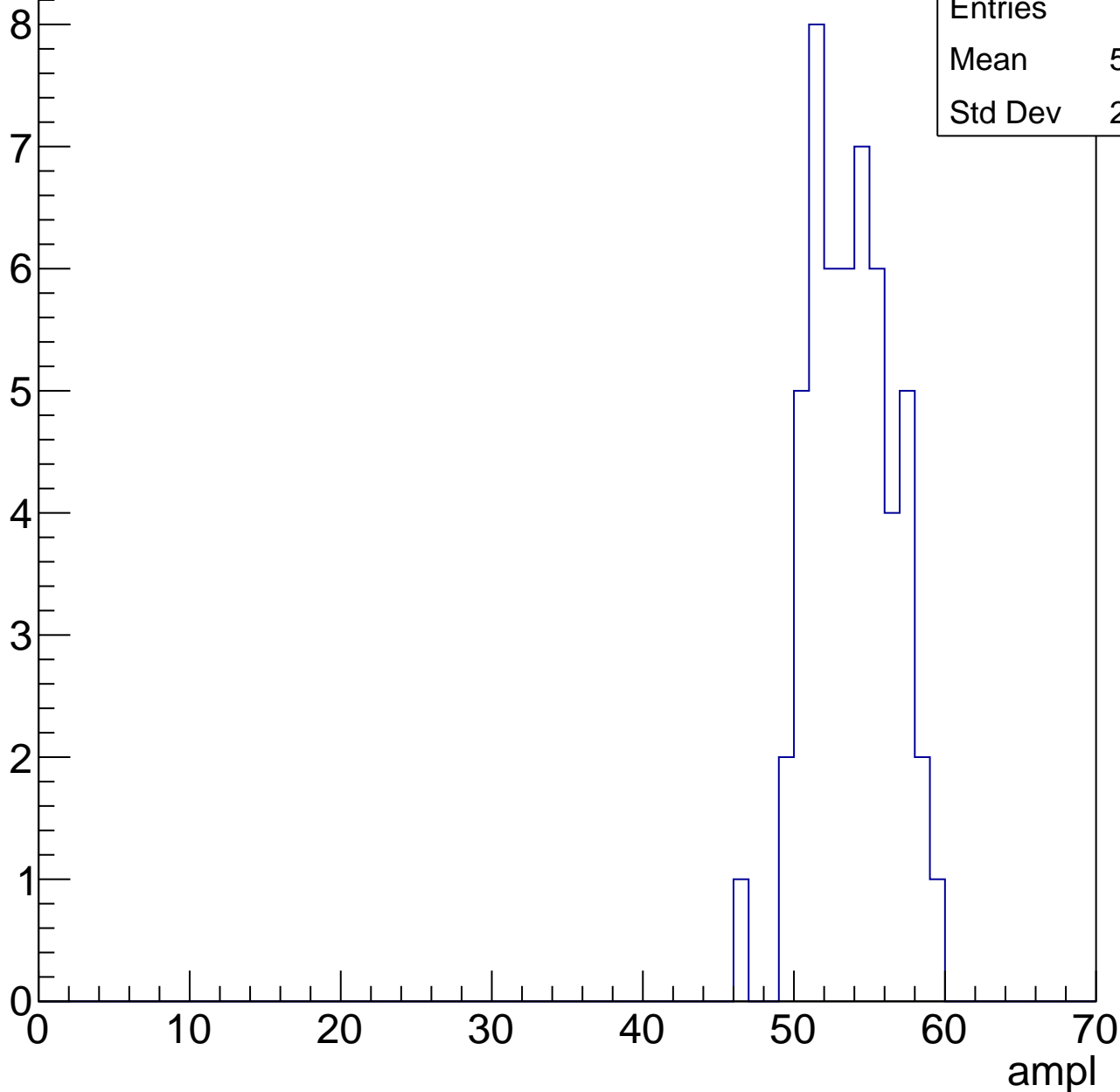


# B1L000S, U1-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

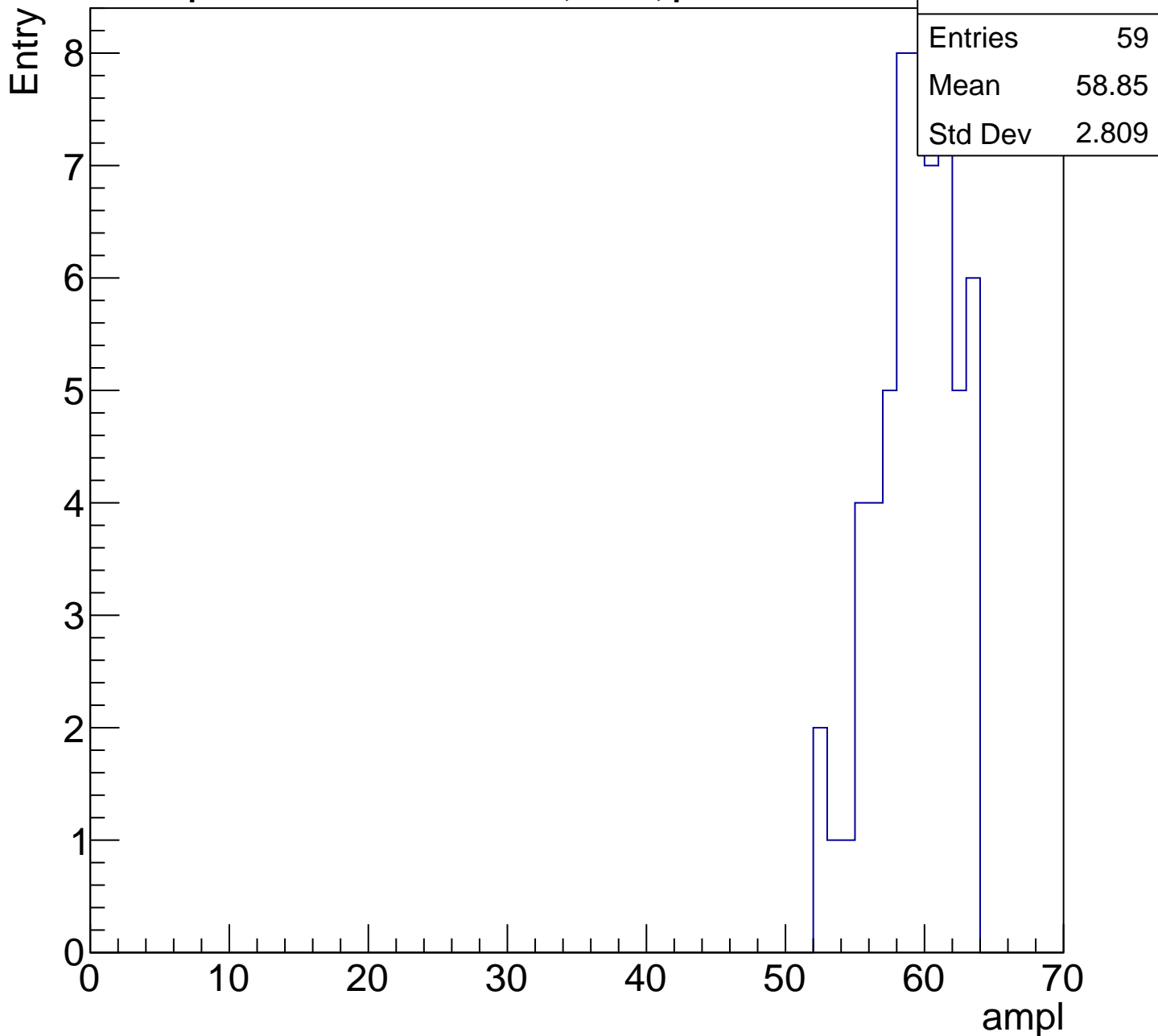
Entry

Entries	53
Mean	53.28
Std Dev	2.722



# B1L000S, U1-ch94, adc5

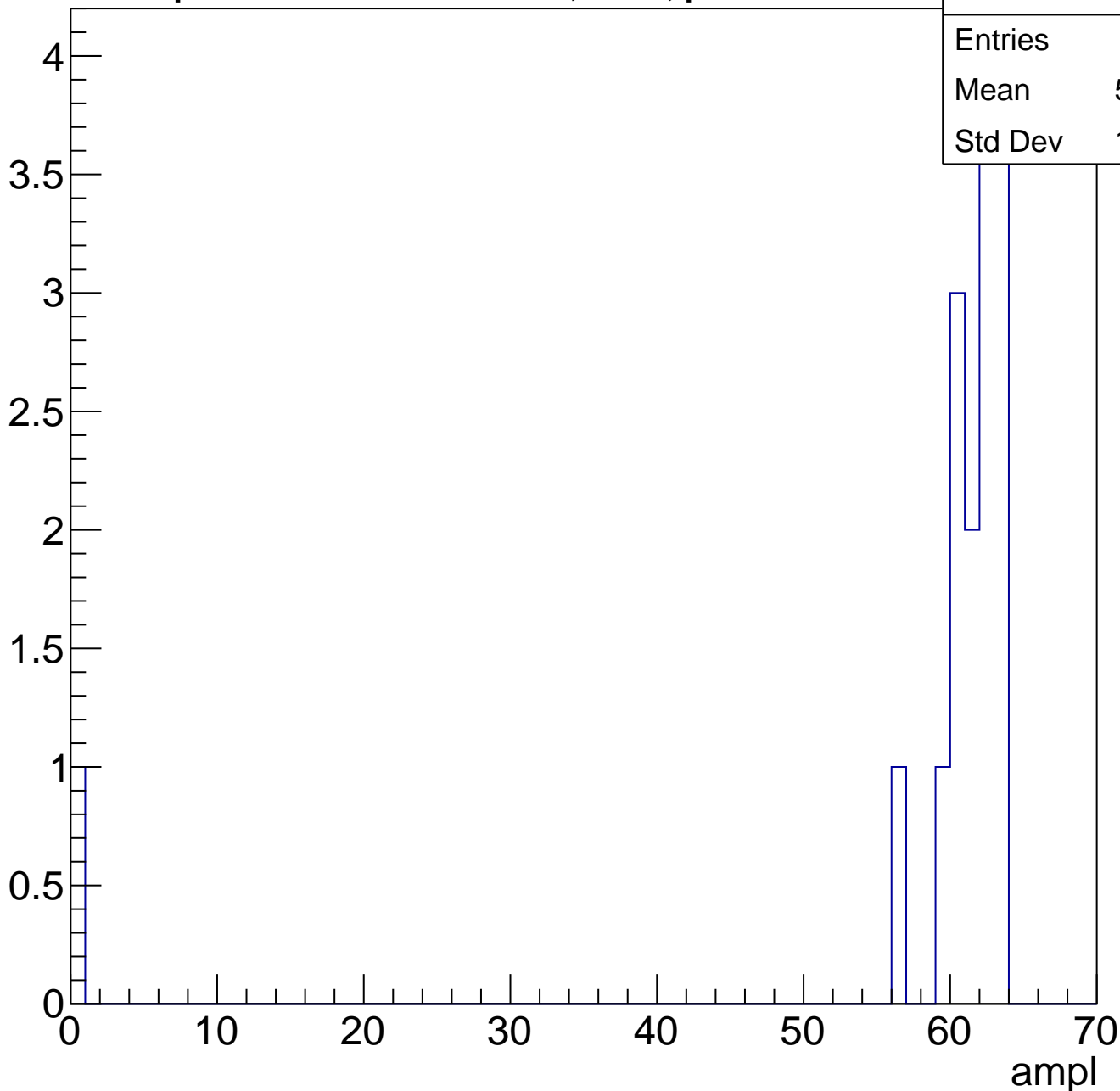
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U1-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch95, adc0

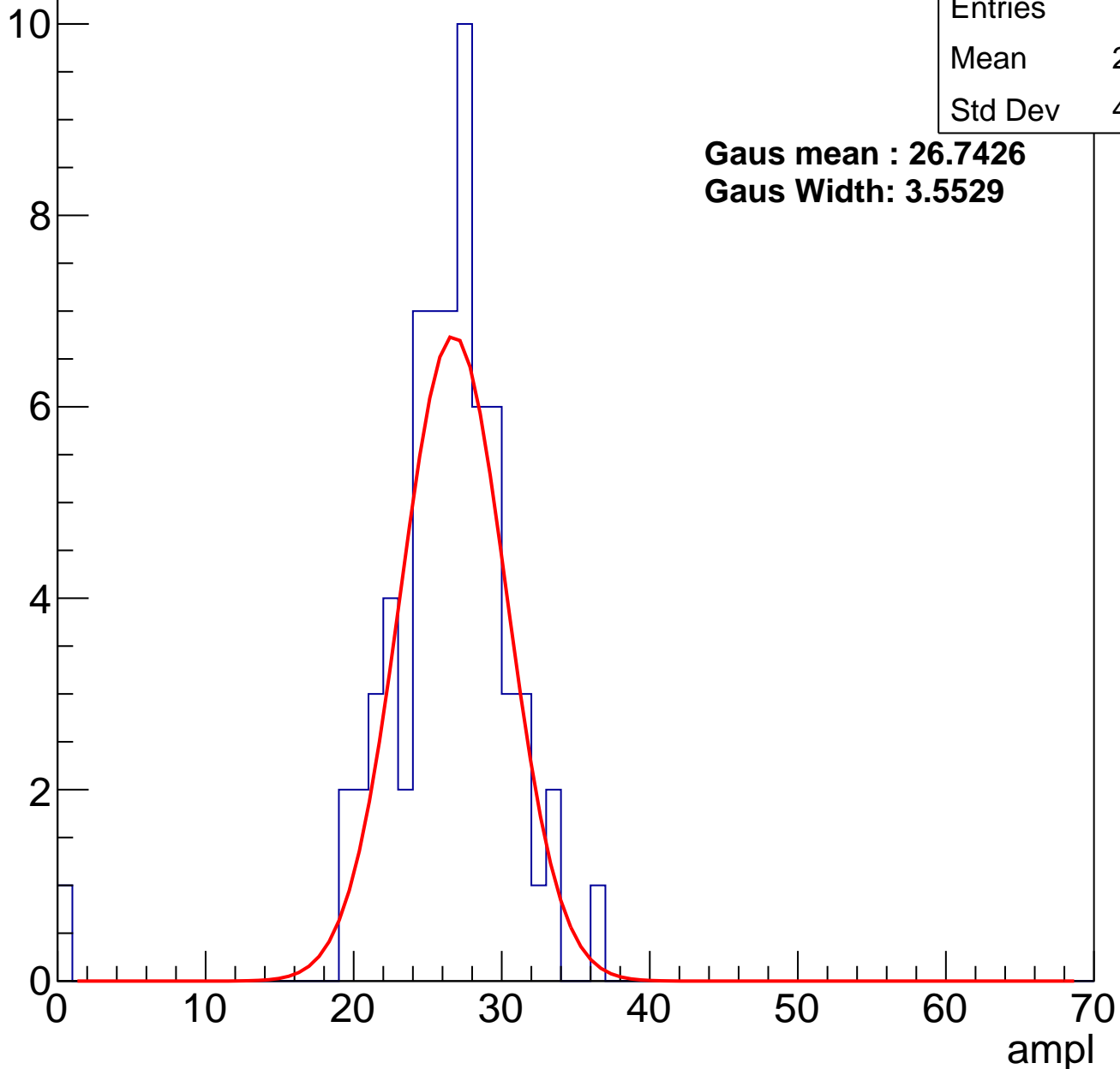
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	25.81
Std Dev	4.701

**Gaus mean : 26.7426**

**Gaus Width: 3.5529**

Entry



# B1L000S, U1-ch95, adc1

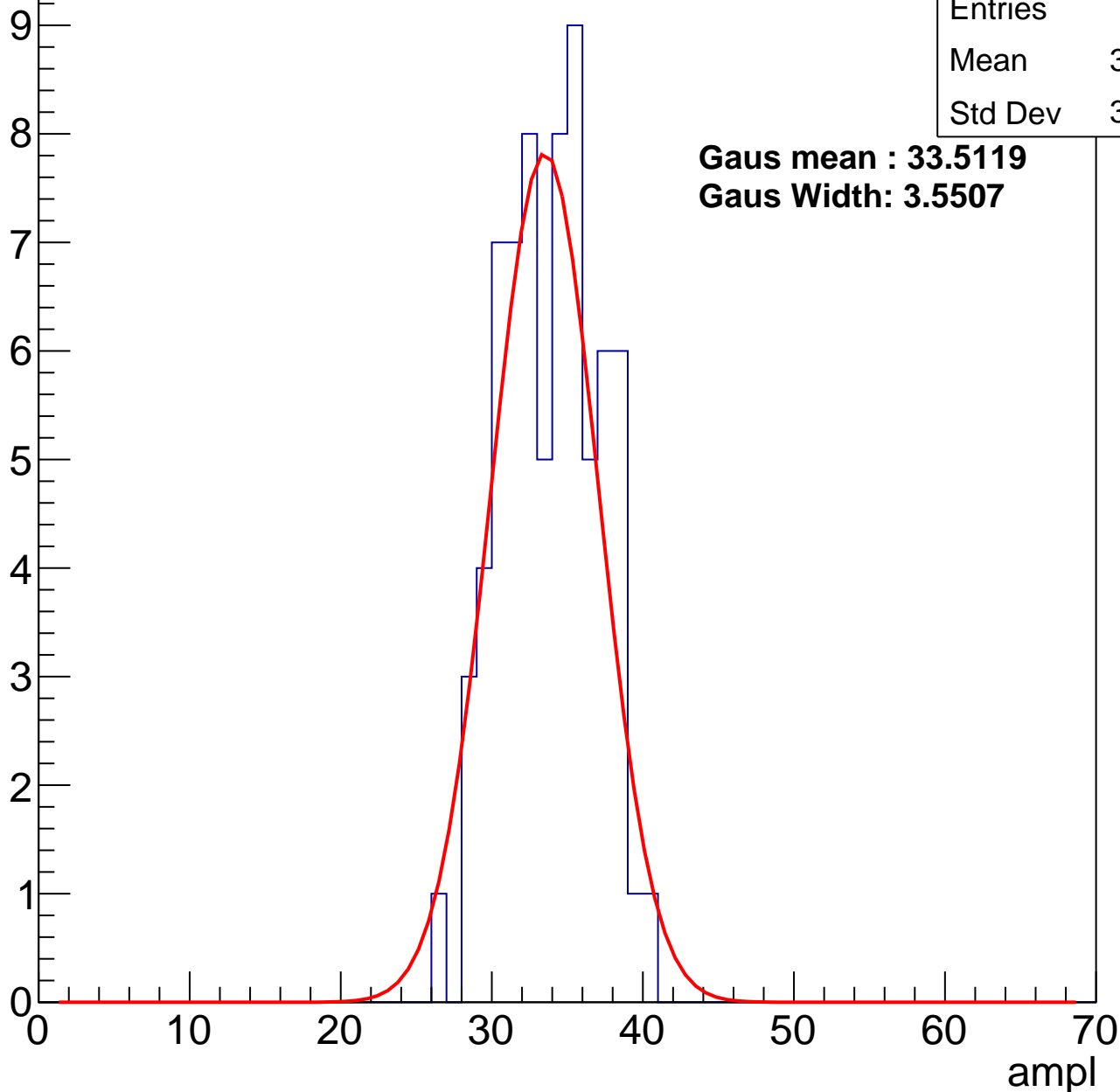
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	33.38
Std Dev	3.133

**Gaus mean : 33.5119**

**Gaus Width: 3.5507**



# B1L000S, U1-ch95, adc2

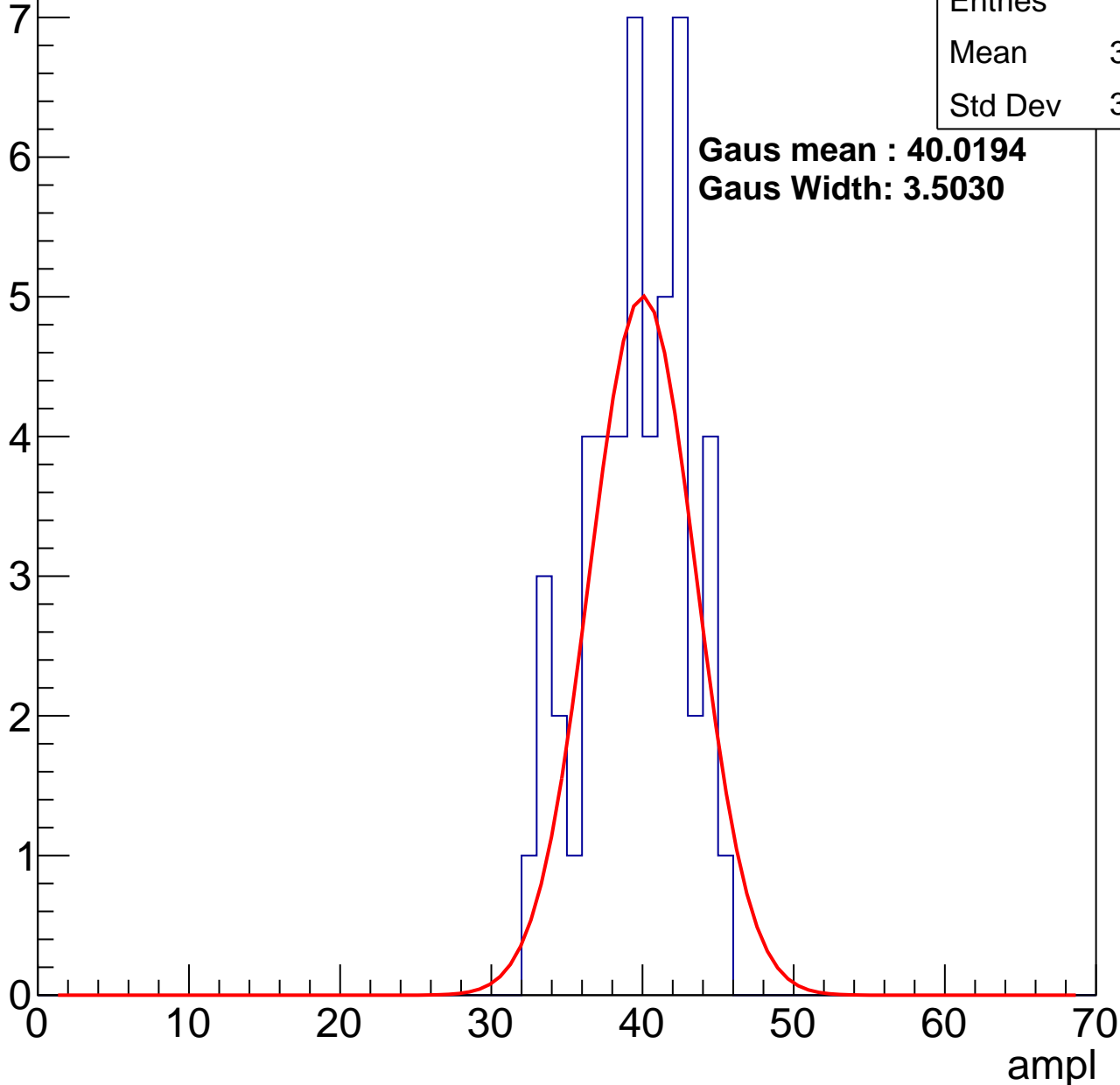
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	39.12
Std Dev	3.299

**Gaus mean : 40.0194**

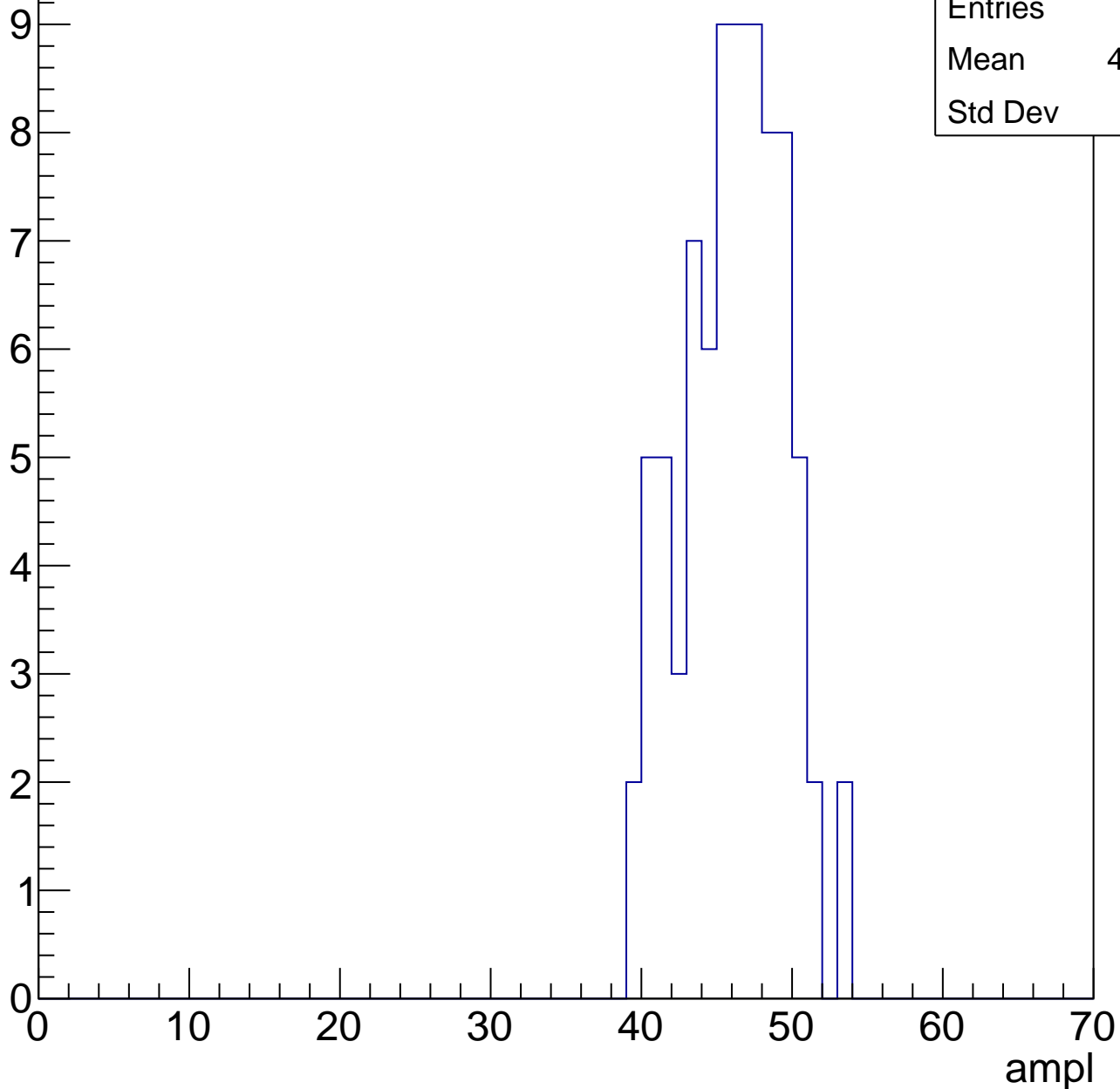
**Gaus Width: 3.5030**



# B1L000S, U1-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



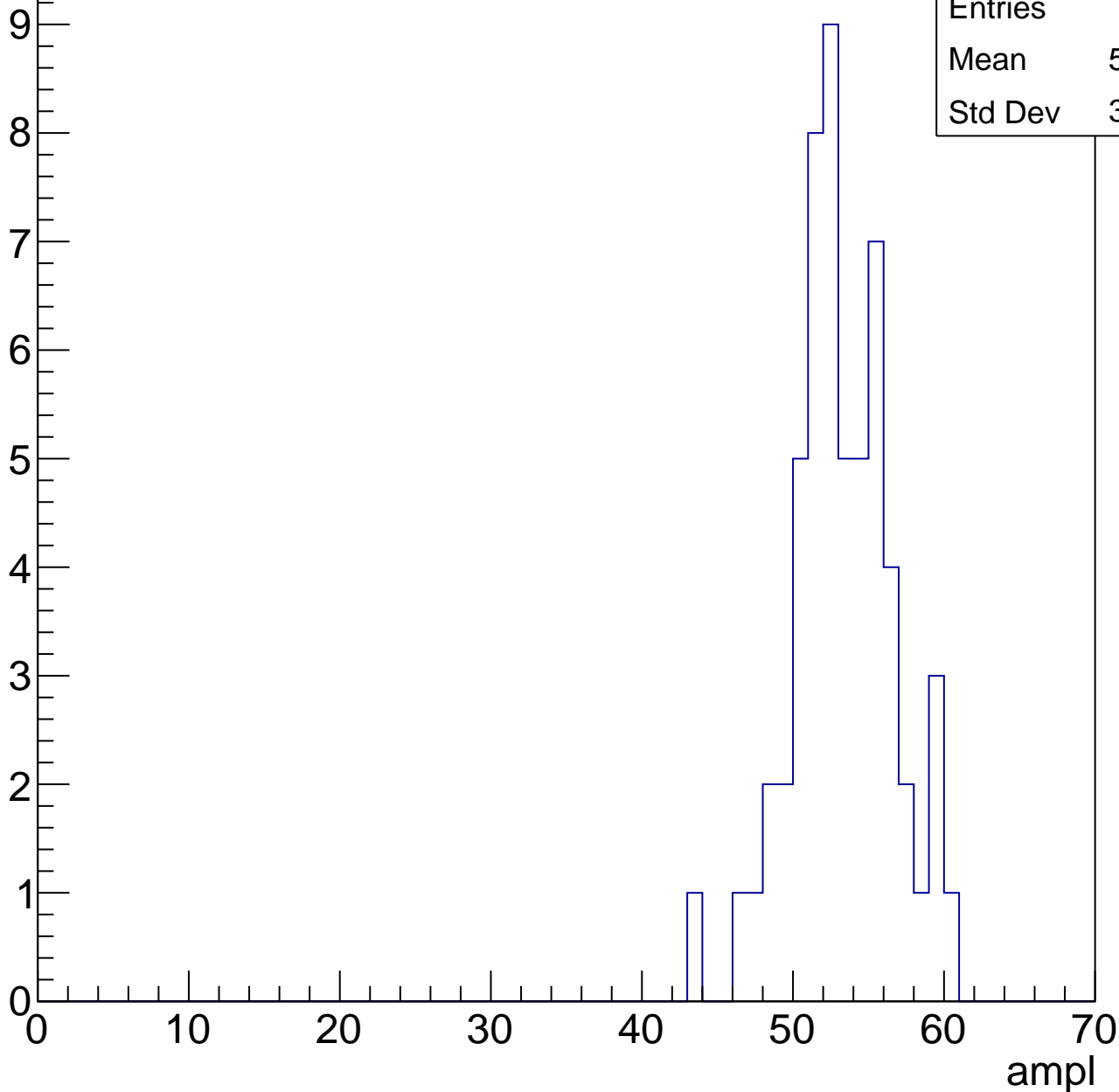
Entries	80
Mean	45.62
Std Dev	3.31

# B1L000S, U1-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	52.79
Std Dev	3.334



# B1L000S, U1-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 54

Mean 57.85

Std Dev 2.896

ampl

0

10

20

30

40

50

60

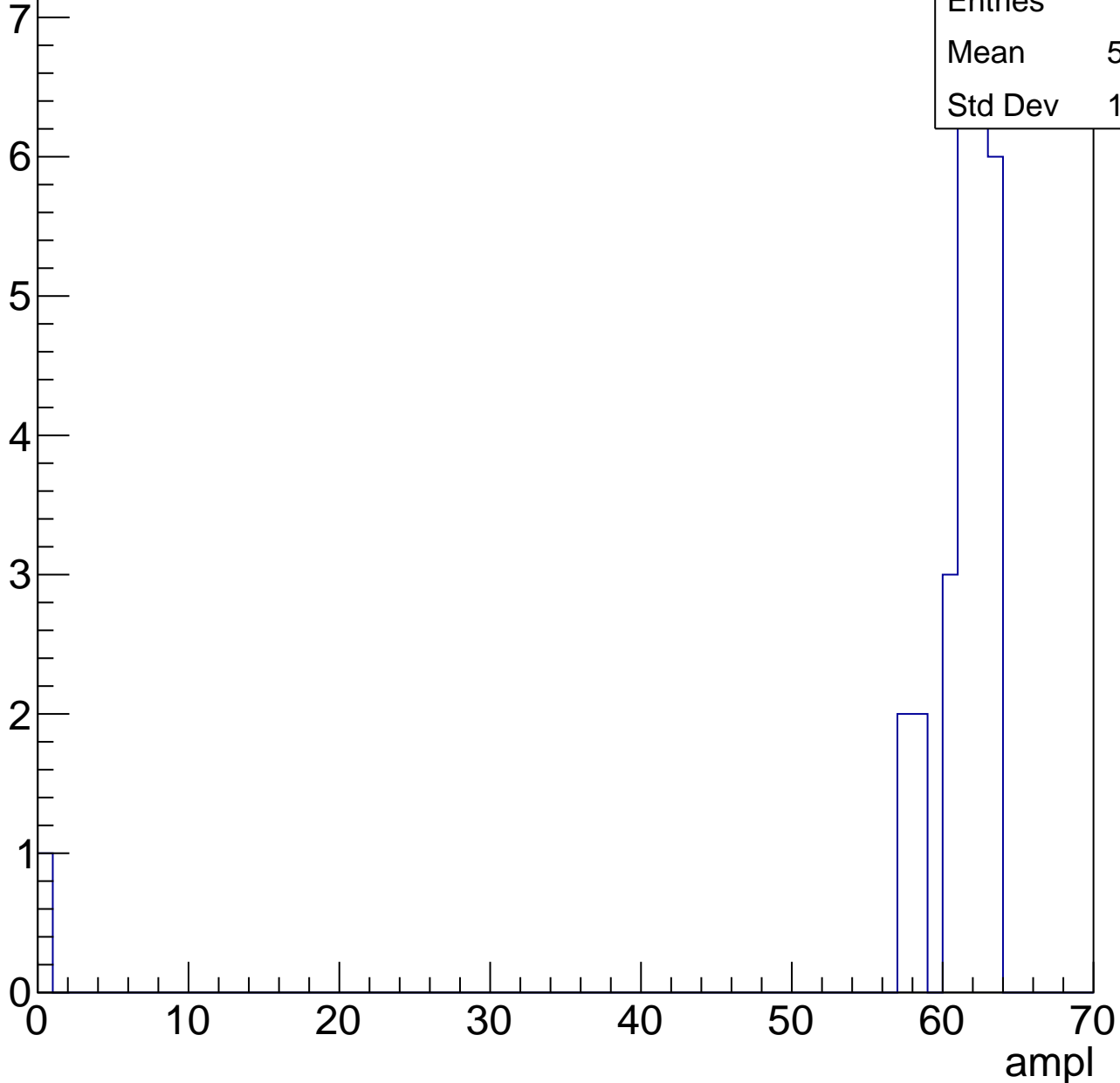
70

# B1L000S, U1-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	58.89
Std Dev	11.47

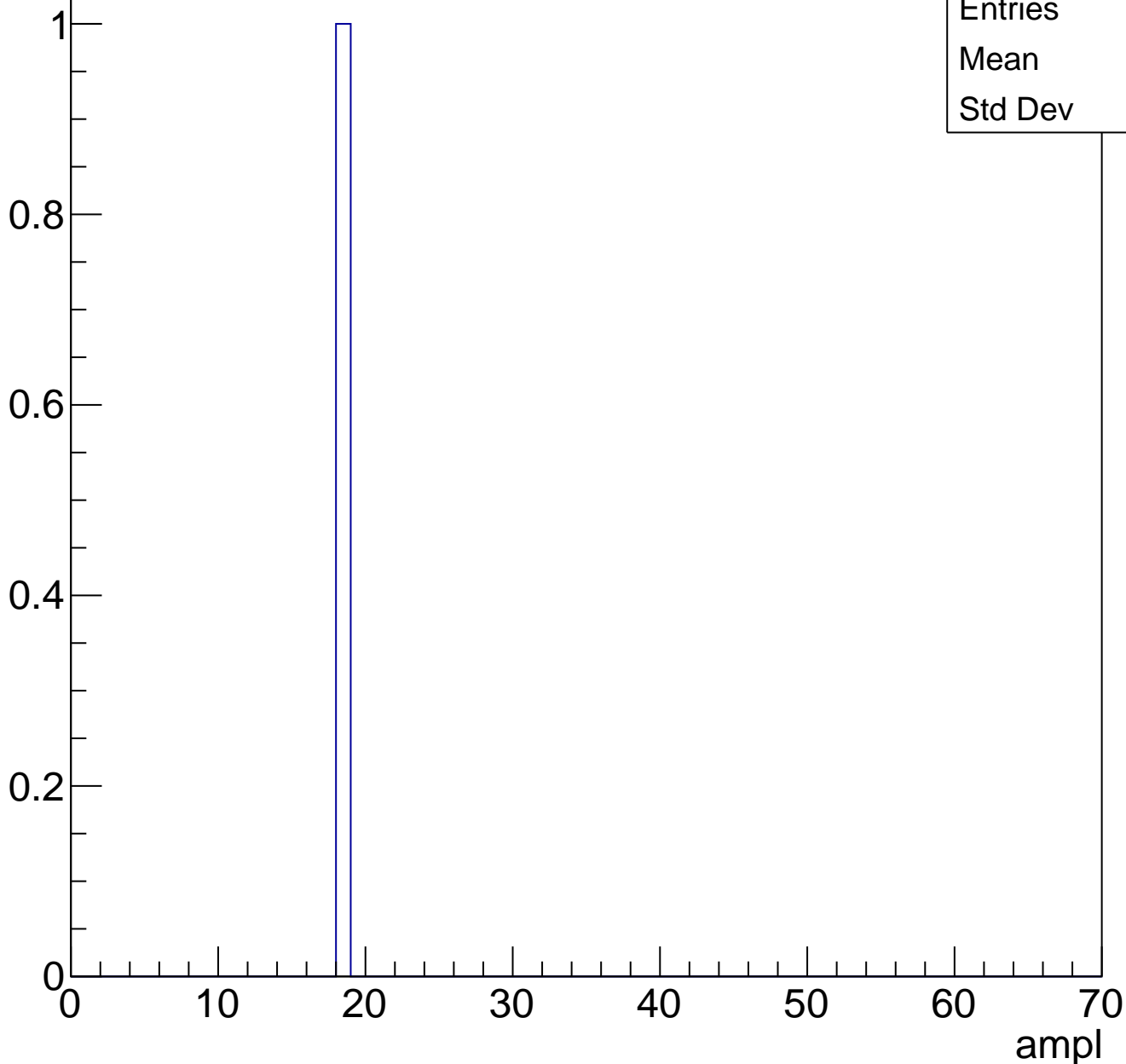




# B1L000S, U1-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch96, adc0

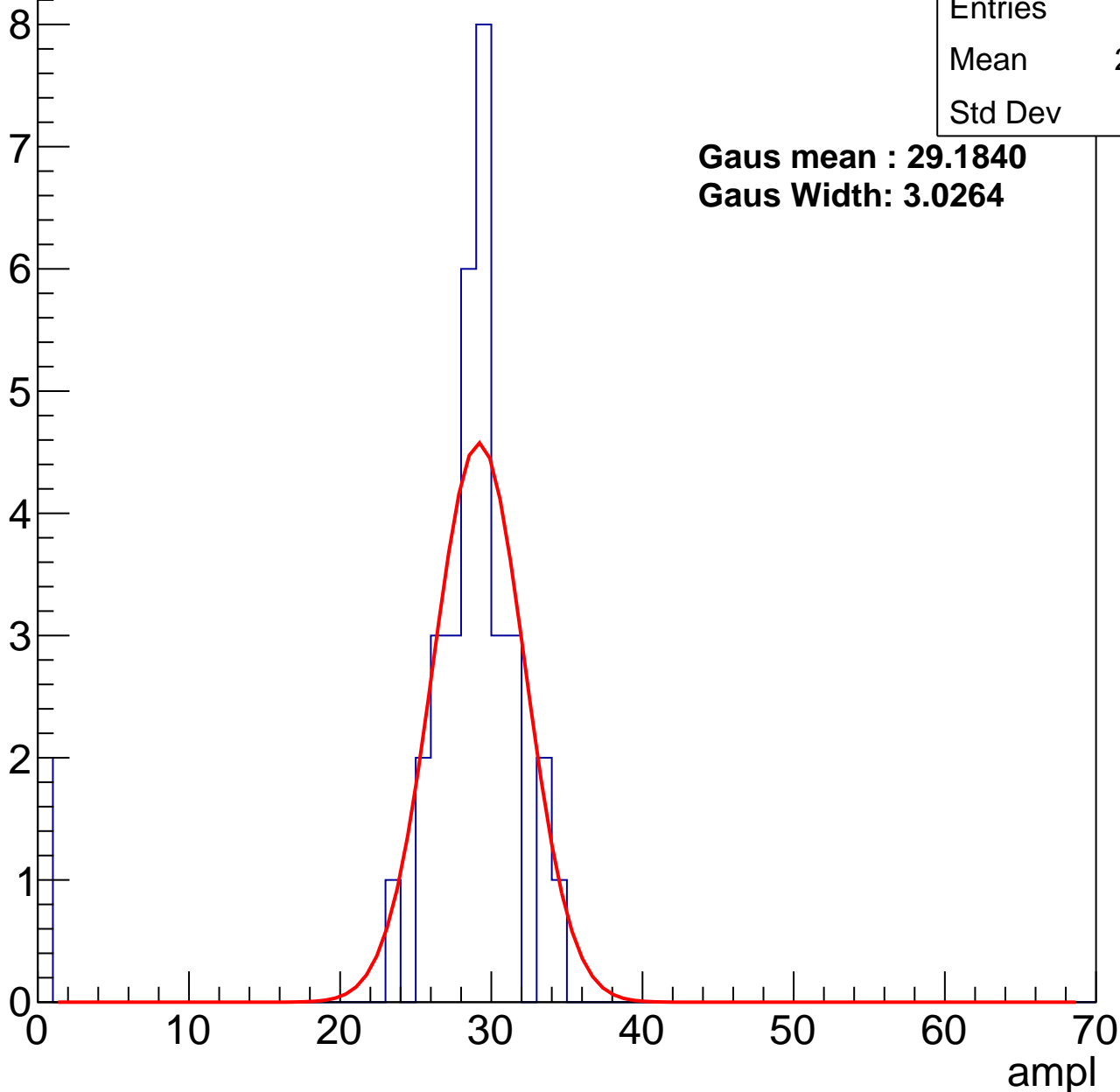
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	26.91
Std Dev	7.11

**Gaus mean : 29.1840**

**Gaus Width: 3.0264**



# B1L000S, U1-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	33.68
Std Dev	3.166

**Gaus mean : 34.3922**

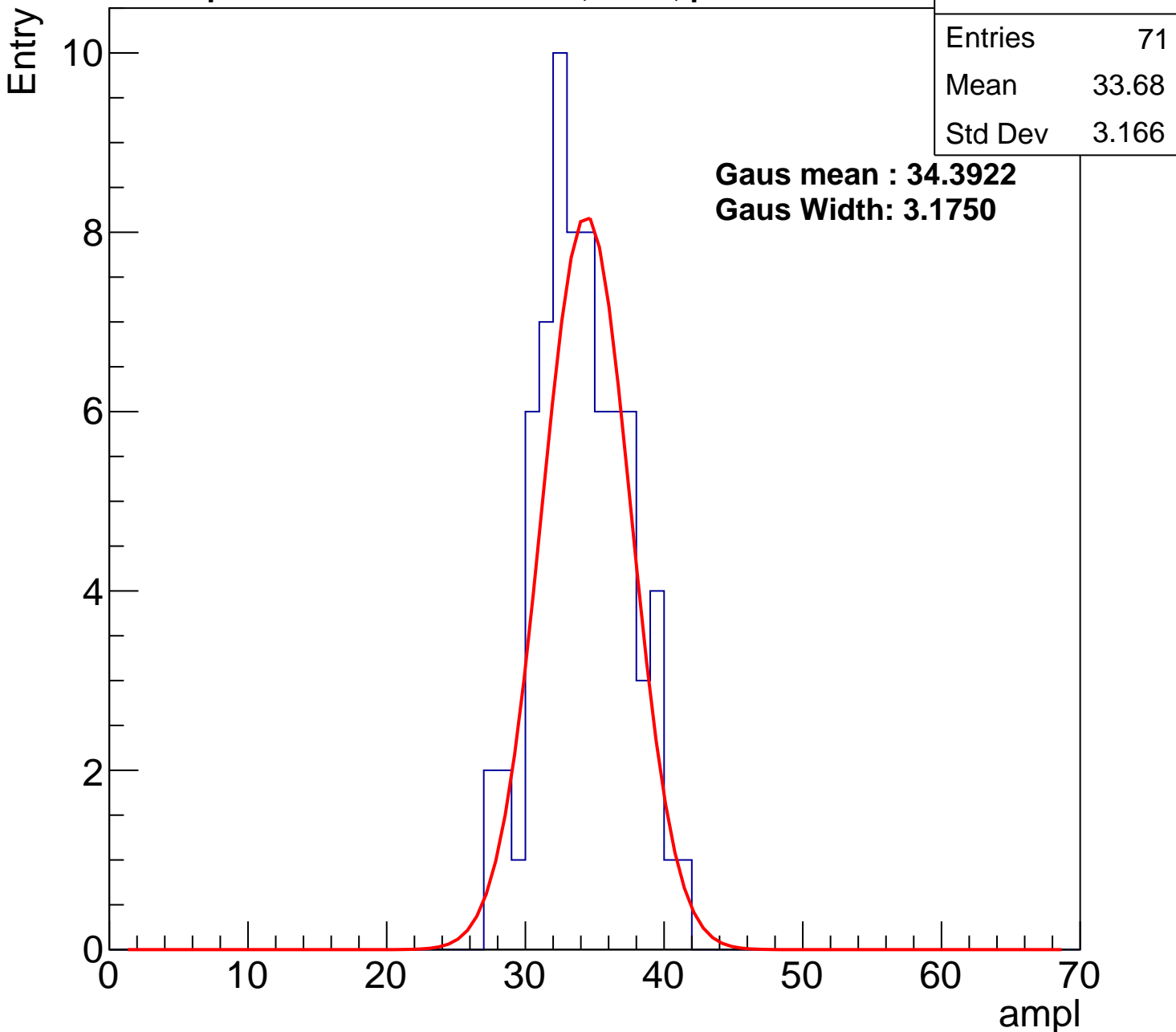
**Gaus Width: 3.1750**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch96, adc2

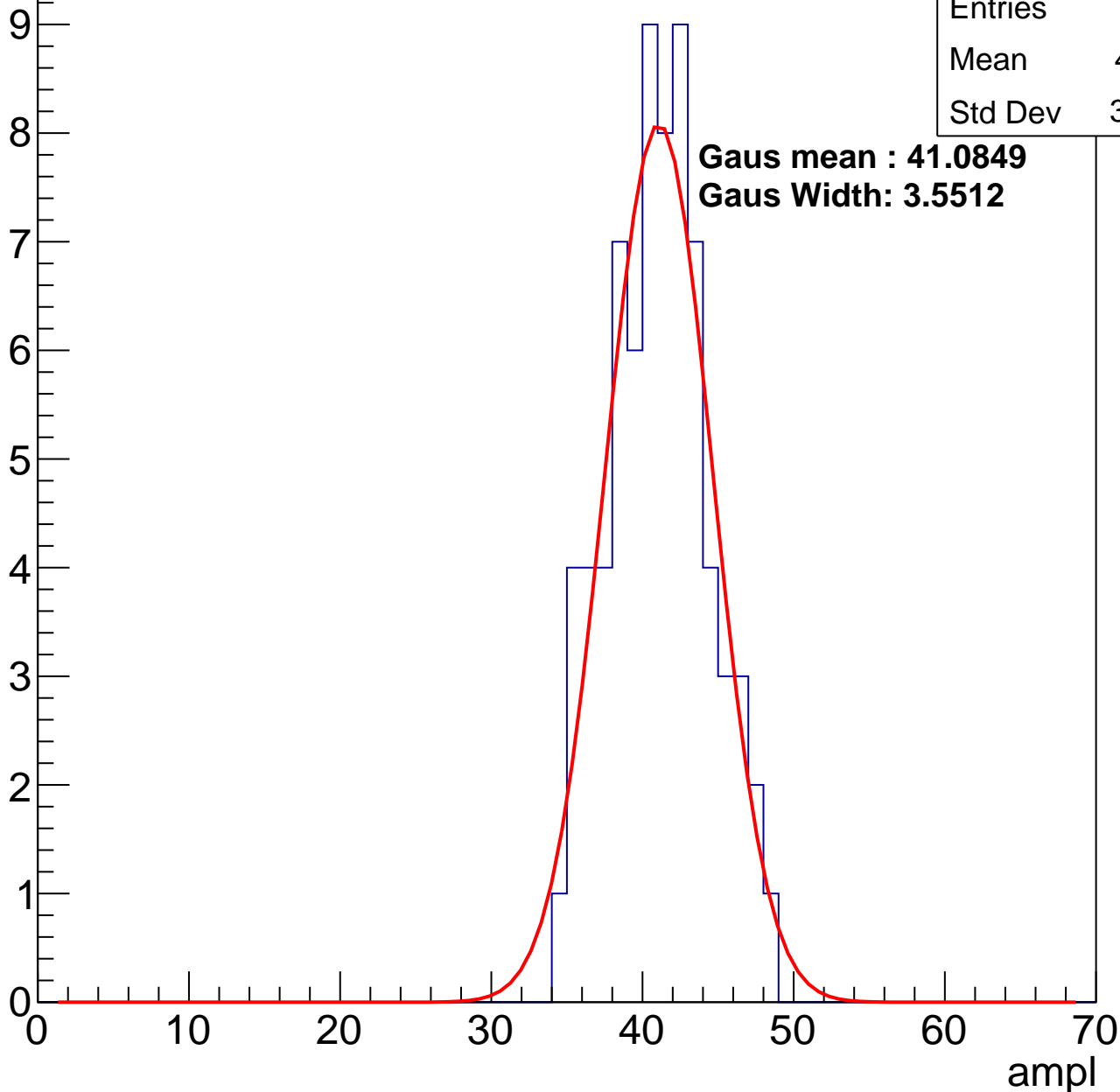
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	40.61
Std Dev	3.247

**Gaus mean : 41.0849**

**Gaus Width: 3.5512**



# B1L000S, U1-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

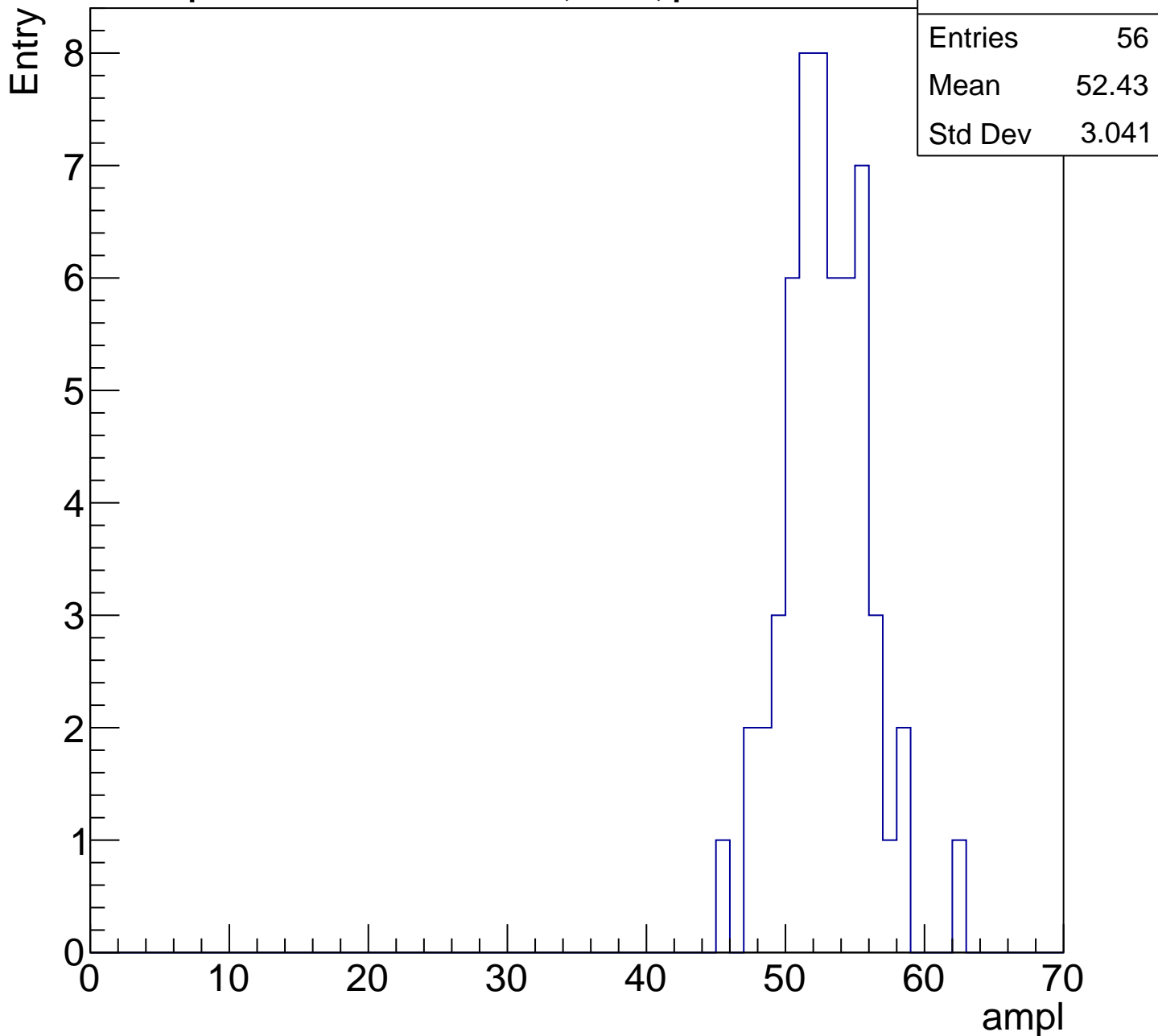
70

ampl

Entries	52
Mean	46.9
Std Dev	2.74

# B1L000S, U1-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

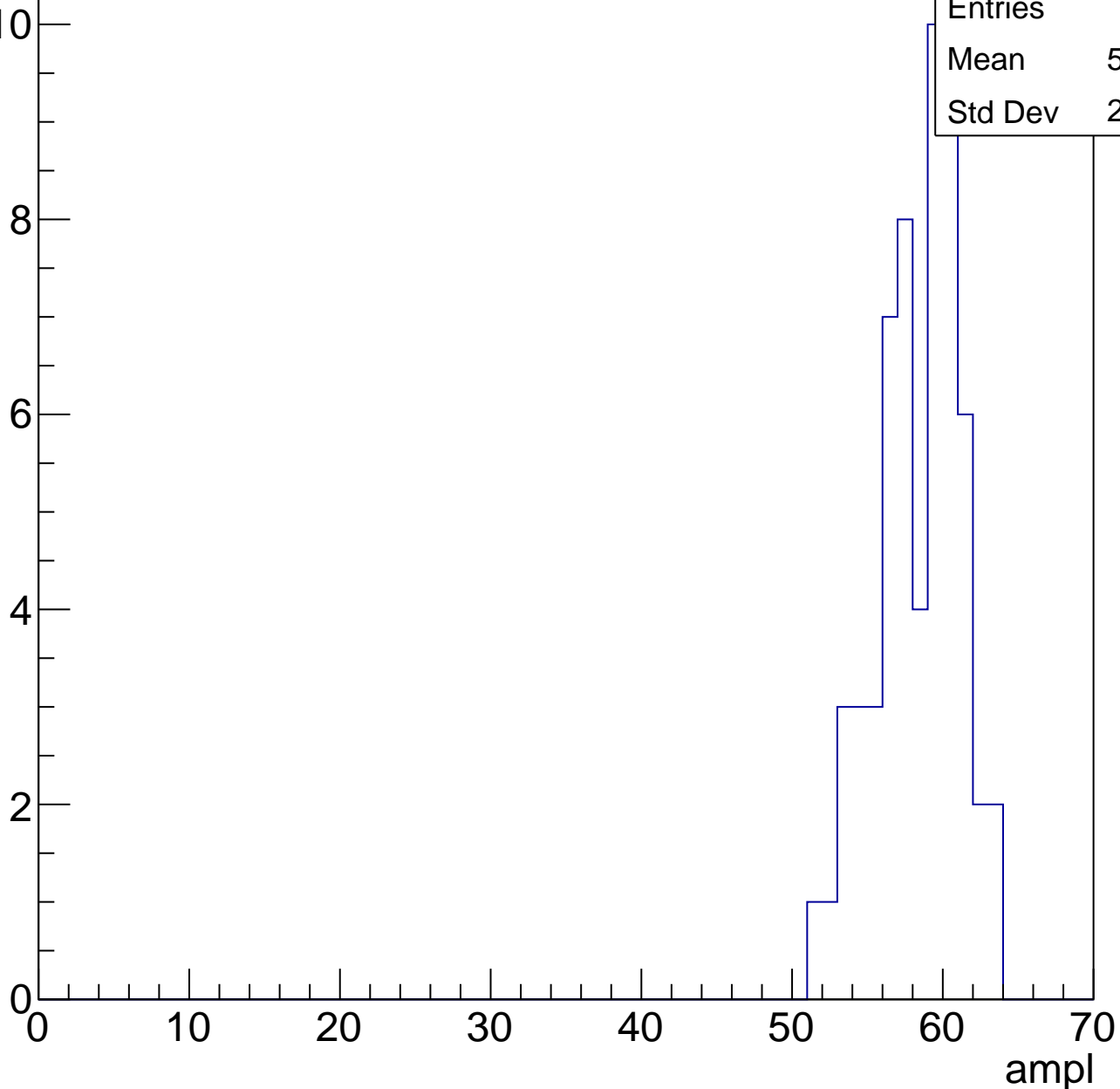


# B1L000S, U1-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	57.88
Std Dev	2.768

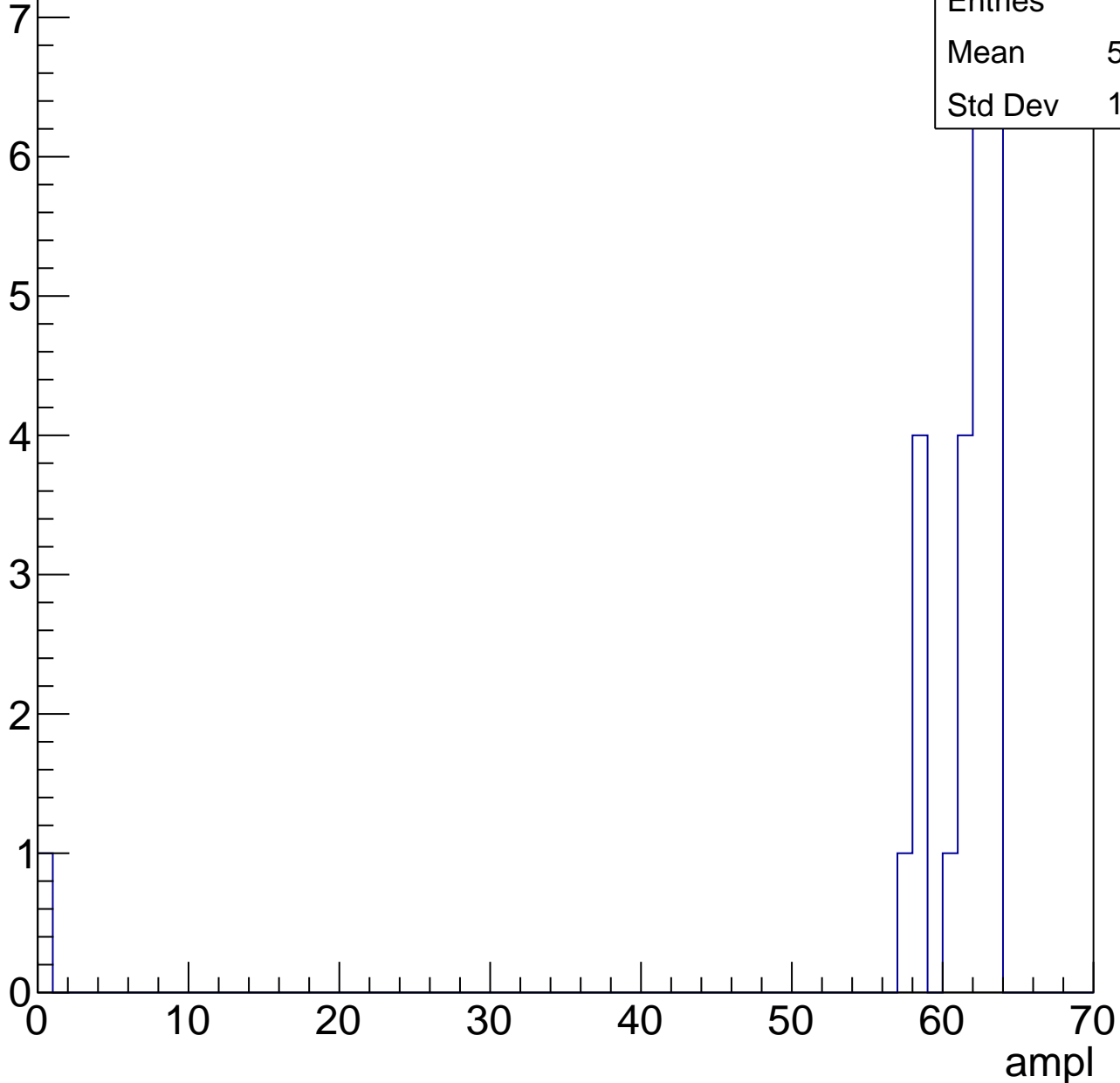


# B1L000S, U1-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	25
Mean	58.72
Std Dev	12.13





# B1L000S, U1-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

# B1L000S, U1-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	59
Mean	29.29
Std Dev	2.636

**Gaus mean : 30.0150**

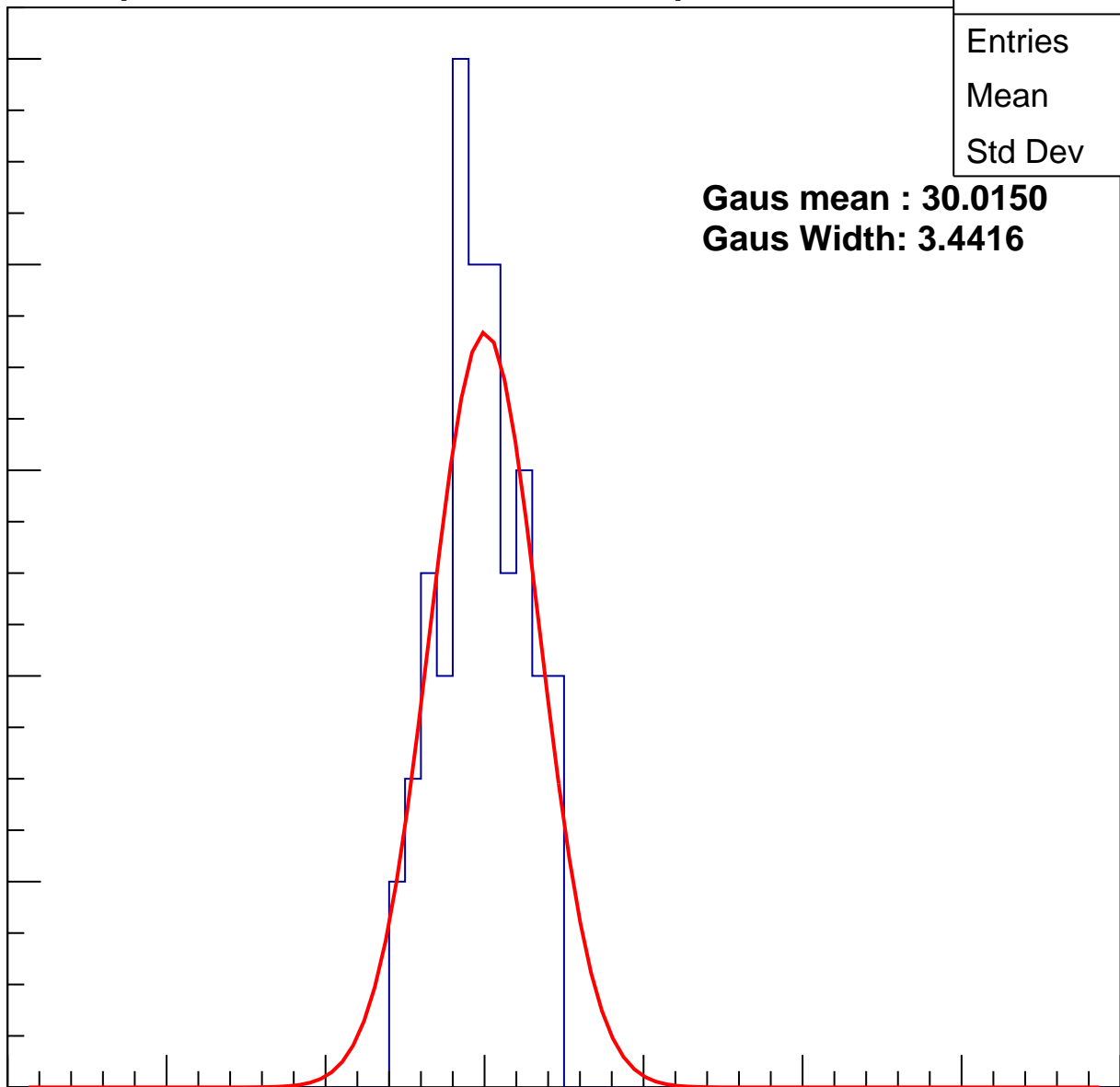
**Gaus Width: 3.4416**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch97, adc1

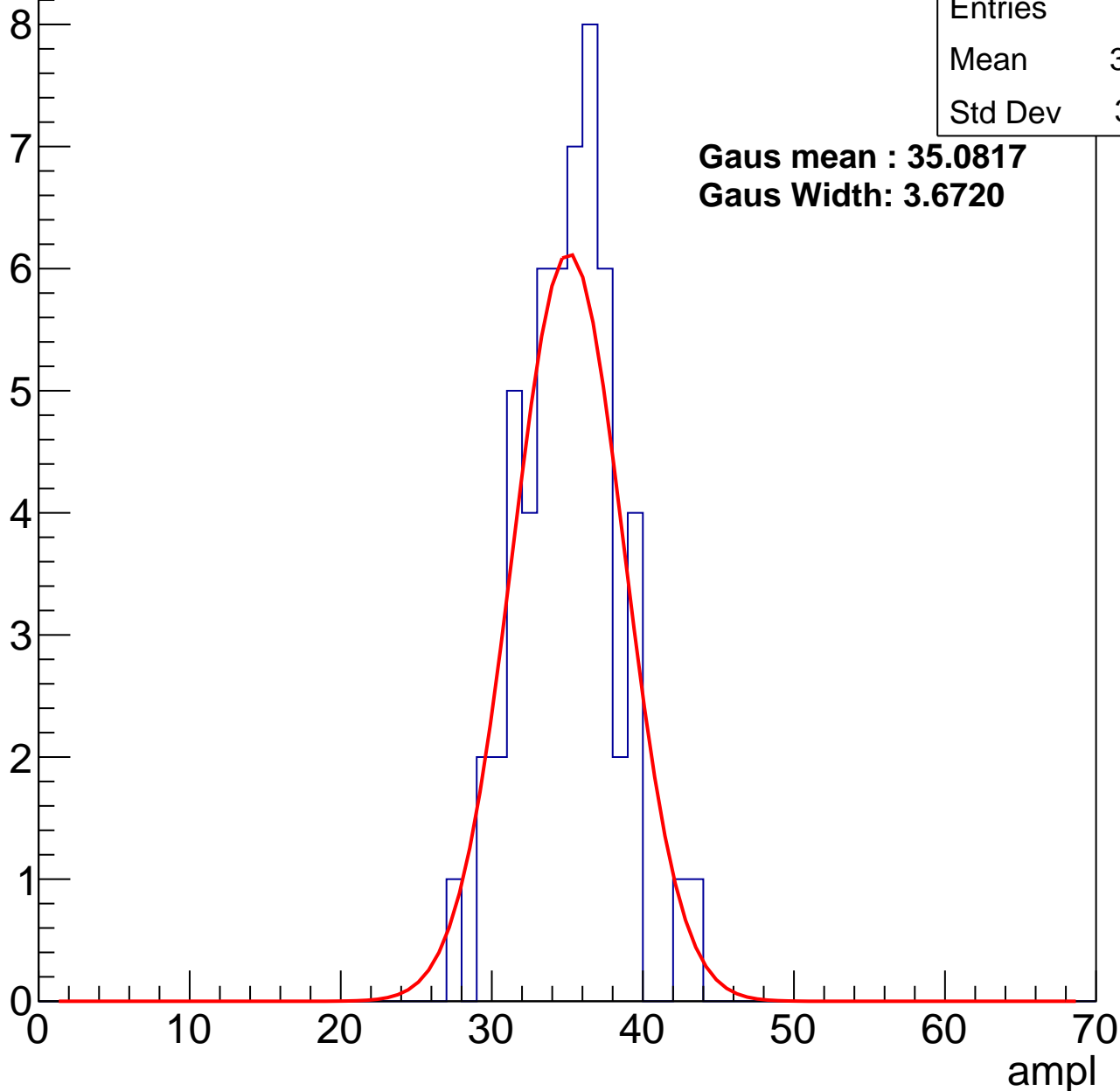
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34.58
Std Dev	3.161

**Gaus mean : 35.0817**

**Gaus Width: 3.6720**



# B1L000S, U1-ch97, adc2

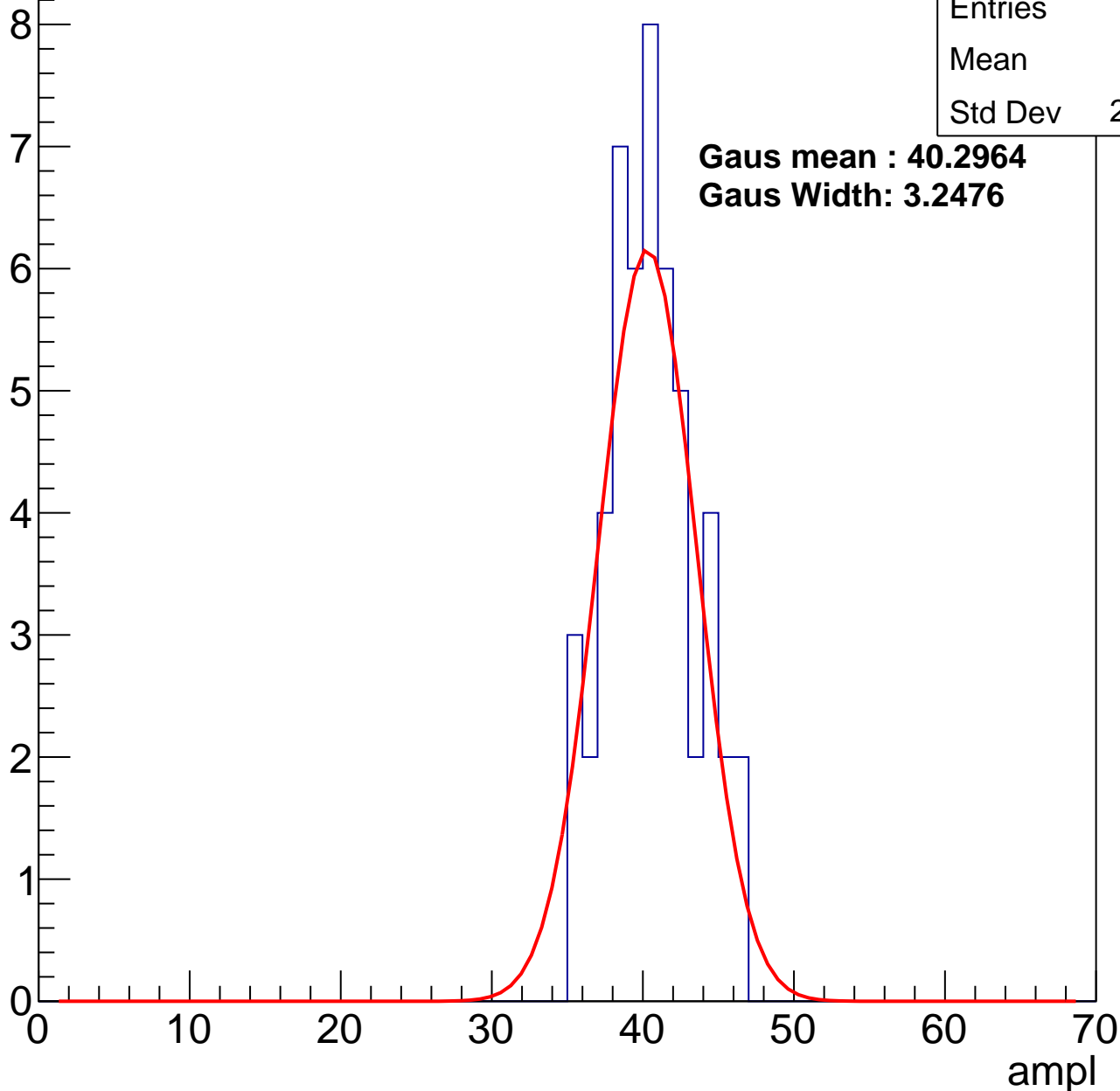
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	40.1
Std Dev	2.823

**Gaus mean : 40.2964**

**Gaus Width: 3.2476**

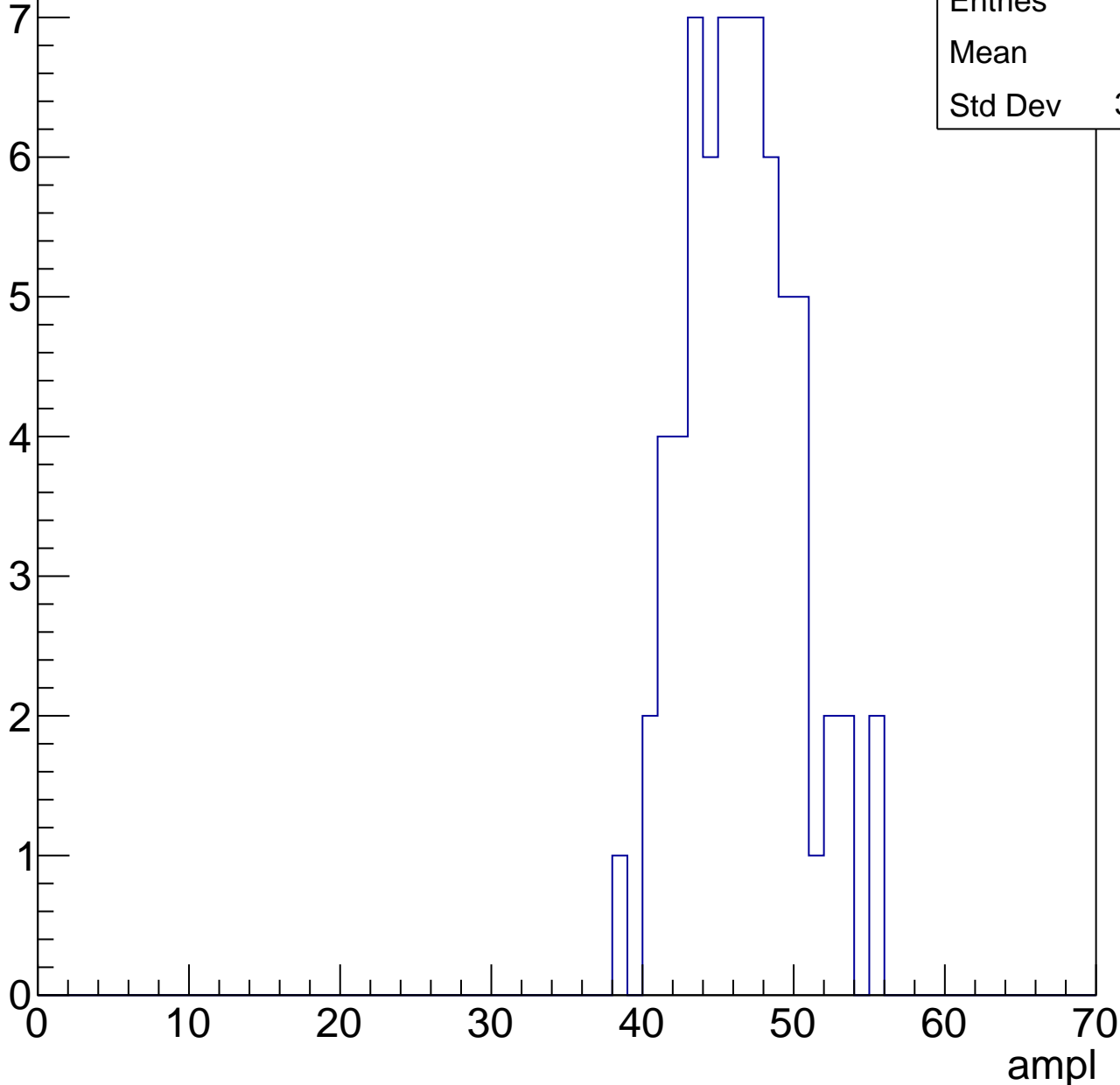


# B1L000S, U1-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	46.1
Std Dev	3.651

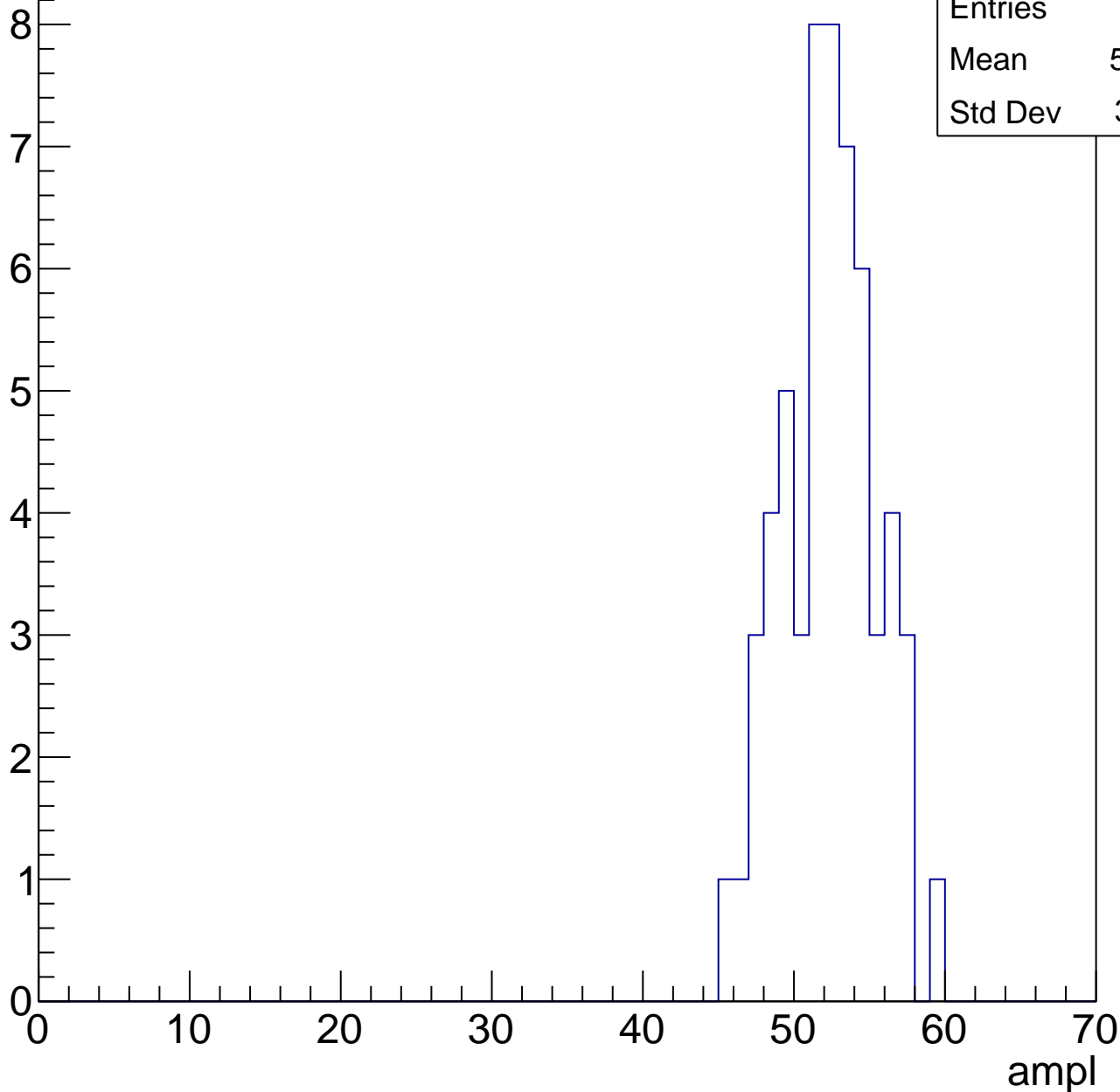


# B1L000S, U1-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	51.88
Std Dev	3.061

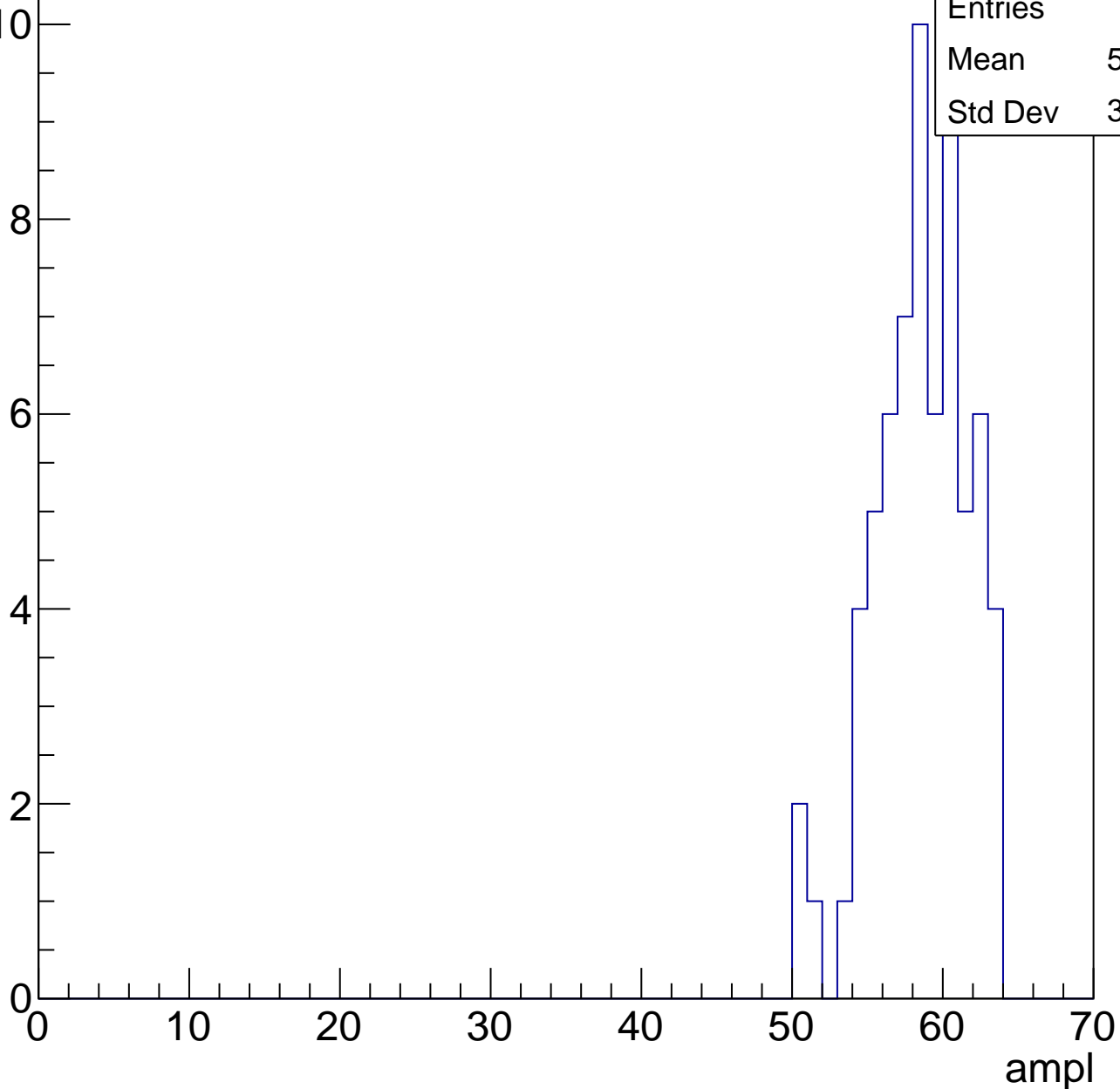


# B1L000S, U1-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	58.08
Std Dev	3.076

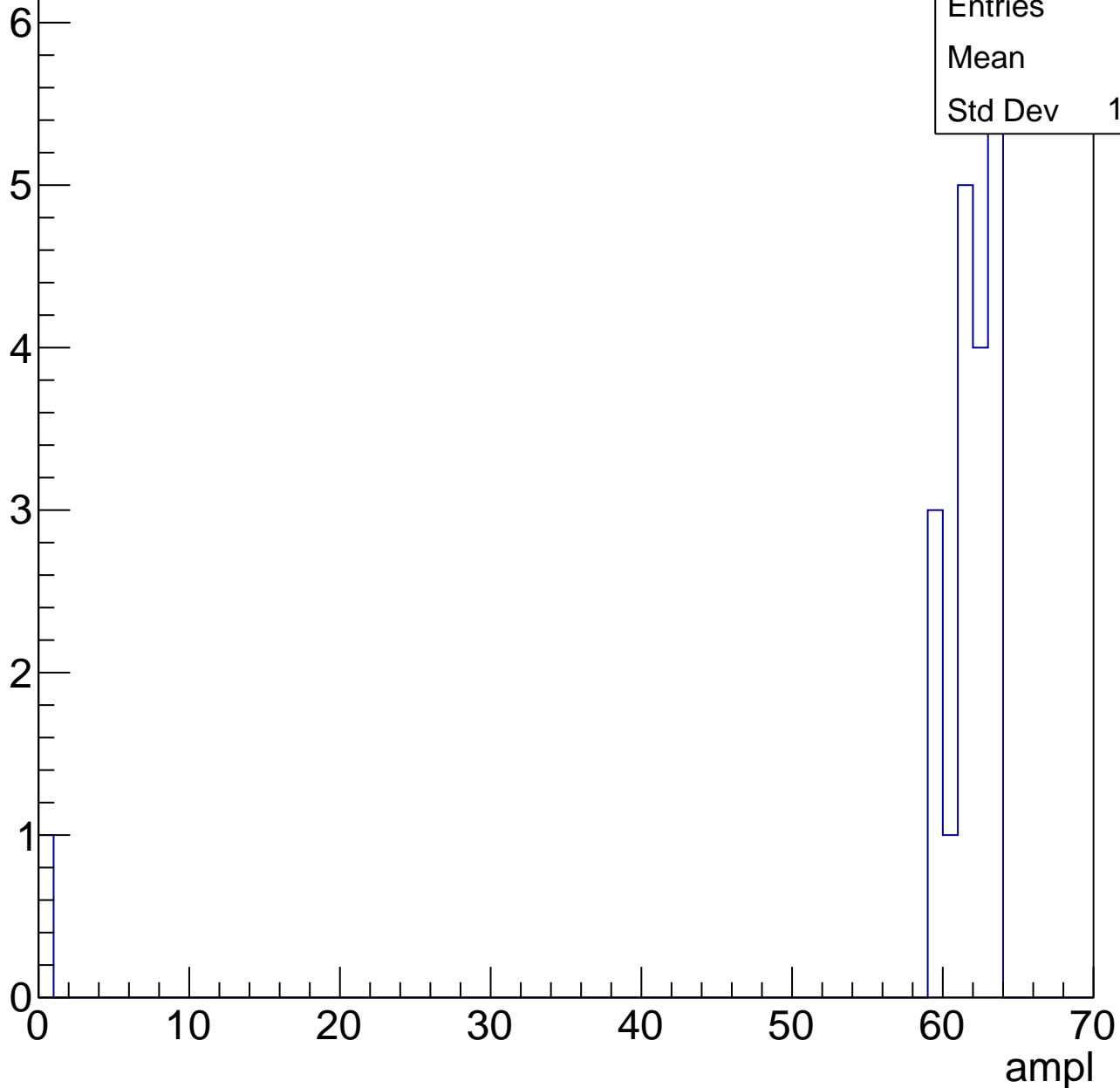


# B1L000S, U1-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.4
Std Dev	13.47





# B1L000S, U1-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U1-ch98, adc0

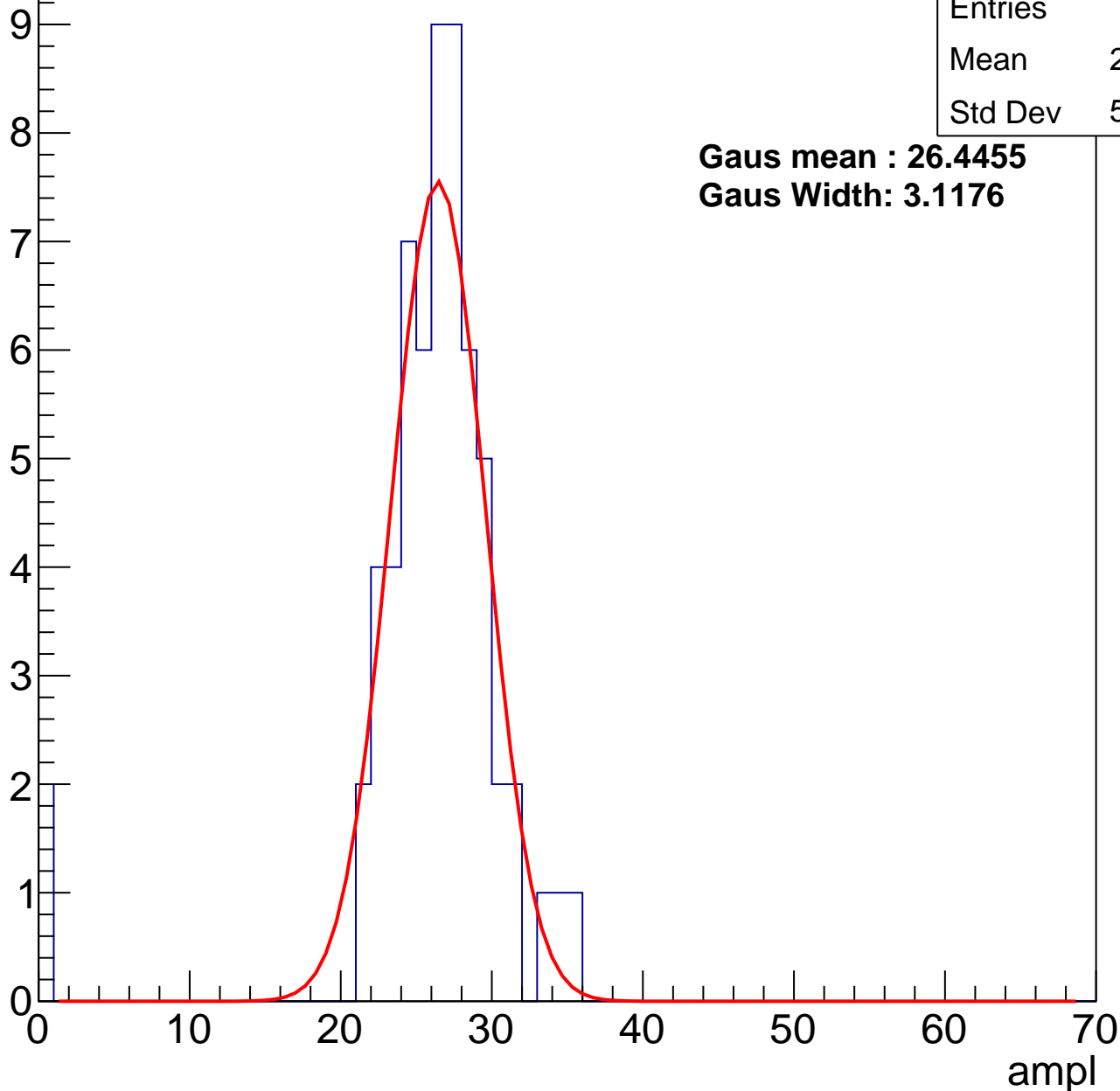
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	25.48
Std Dev	5.542

**Gaus mean : 26.4455**

**Gaus Width: 3.1176**



# B1L000S, U1-ch98, adc1

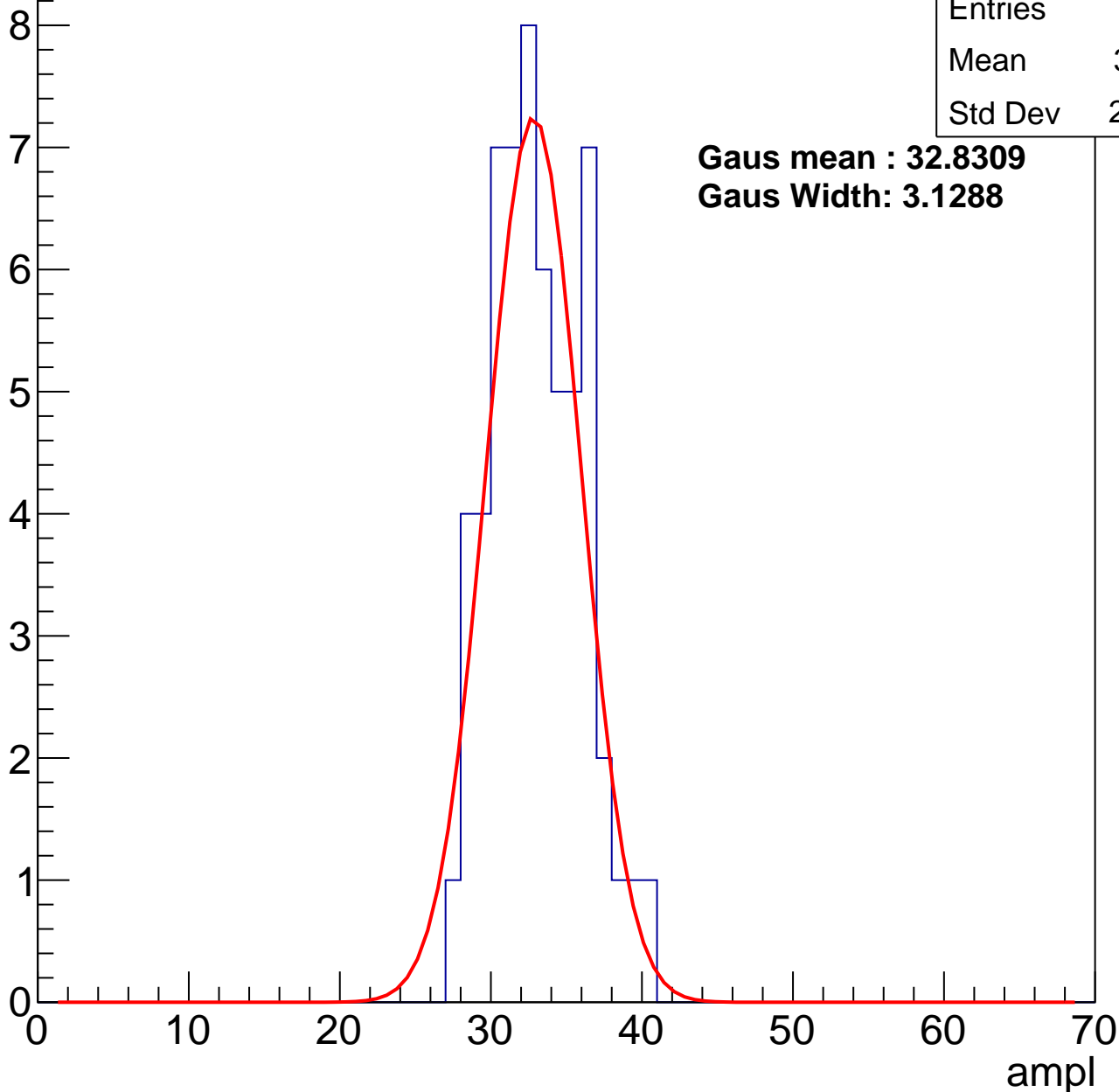
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	32.61
Std Dev	2.969

**Gaus mean : 32.8309**

**Gaus Width: 3.1288**



# B1L000S, U1-ch98, adc2

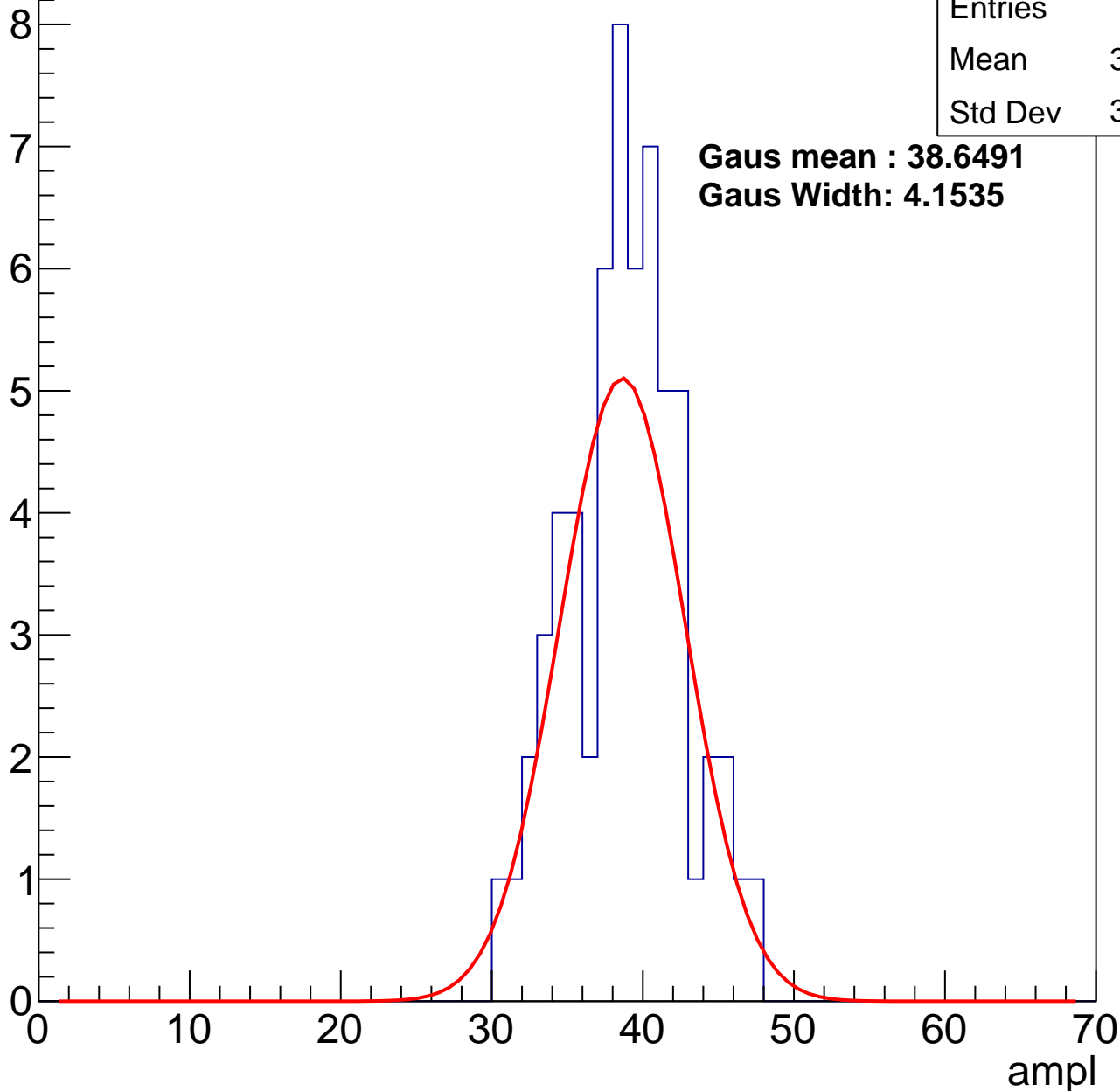
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	38.38
Std Dev	3.764

**Gaus mean : 38.6491**

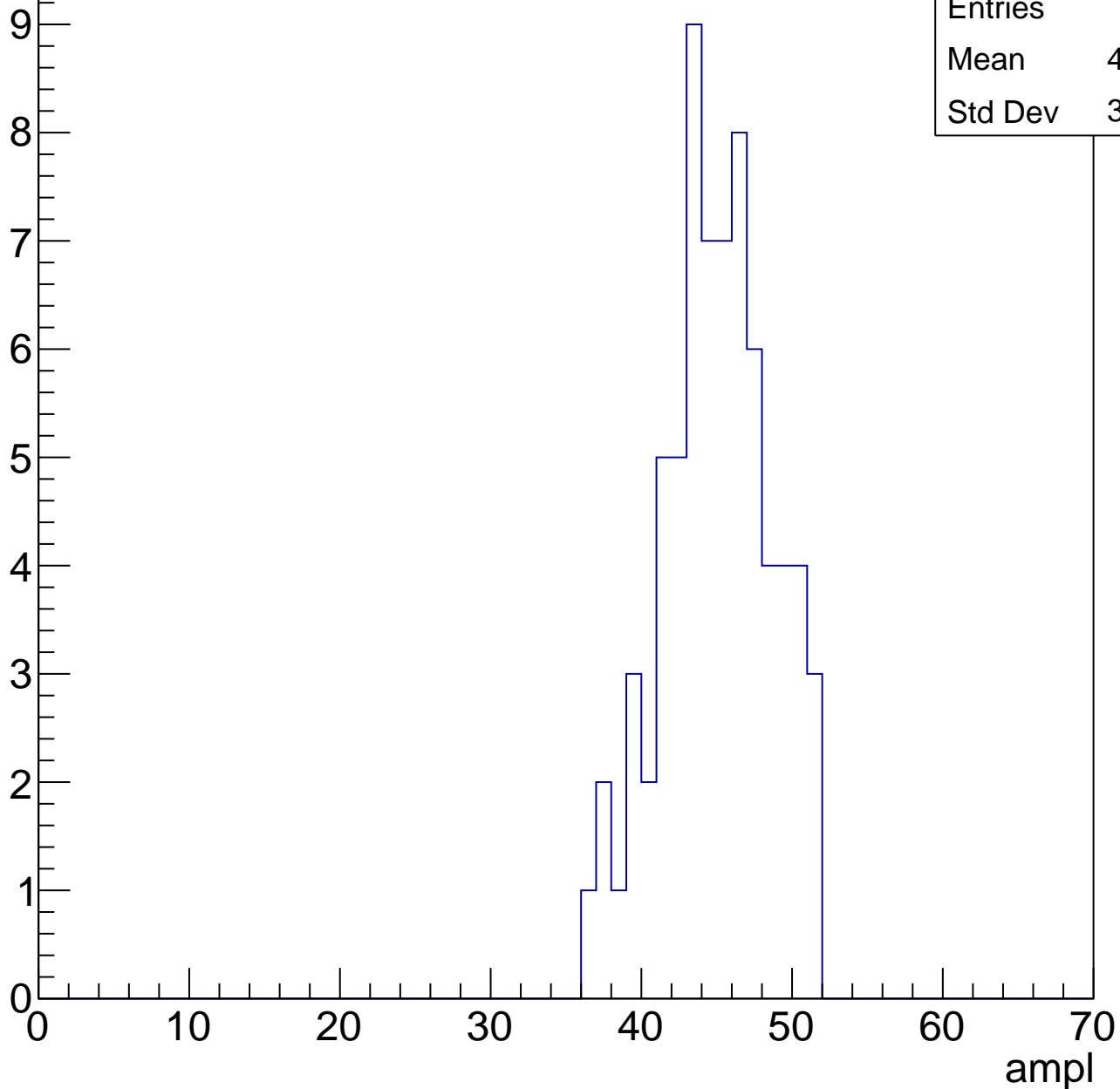
**Gaus Width: 4.1535**



# B1L000S, U1-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



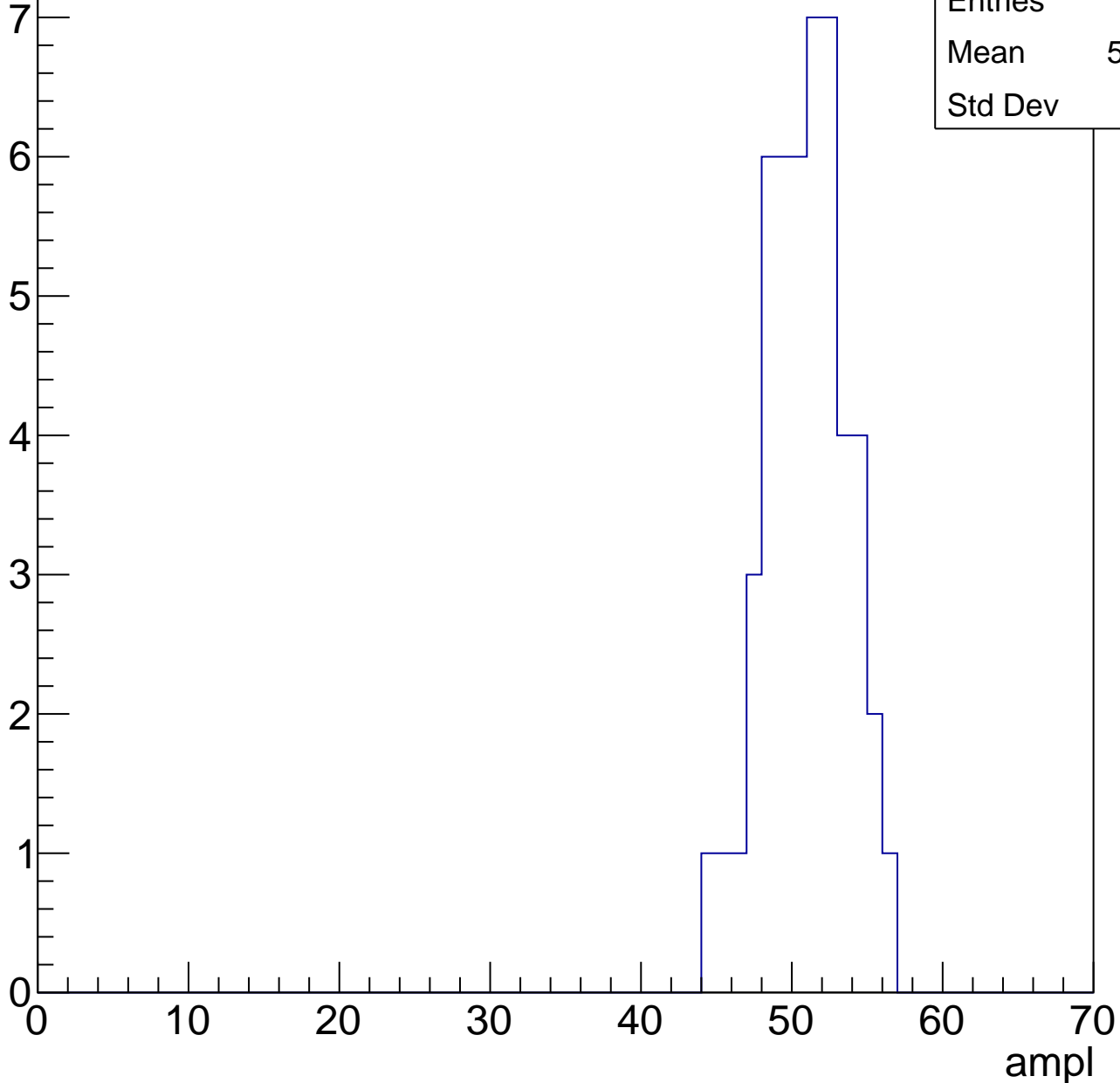
Entries	71
Mean	44.52
Std Dev	3.576

# B1L000S, U1-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

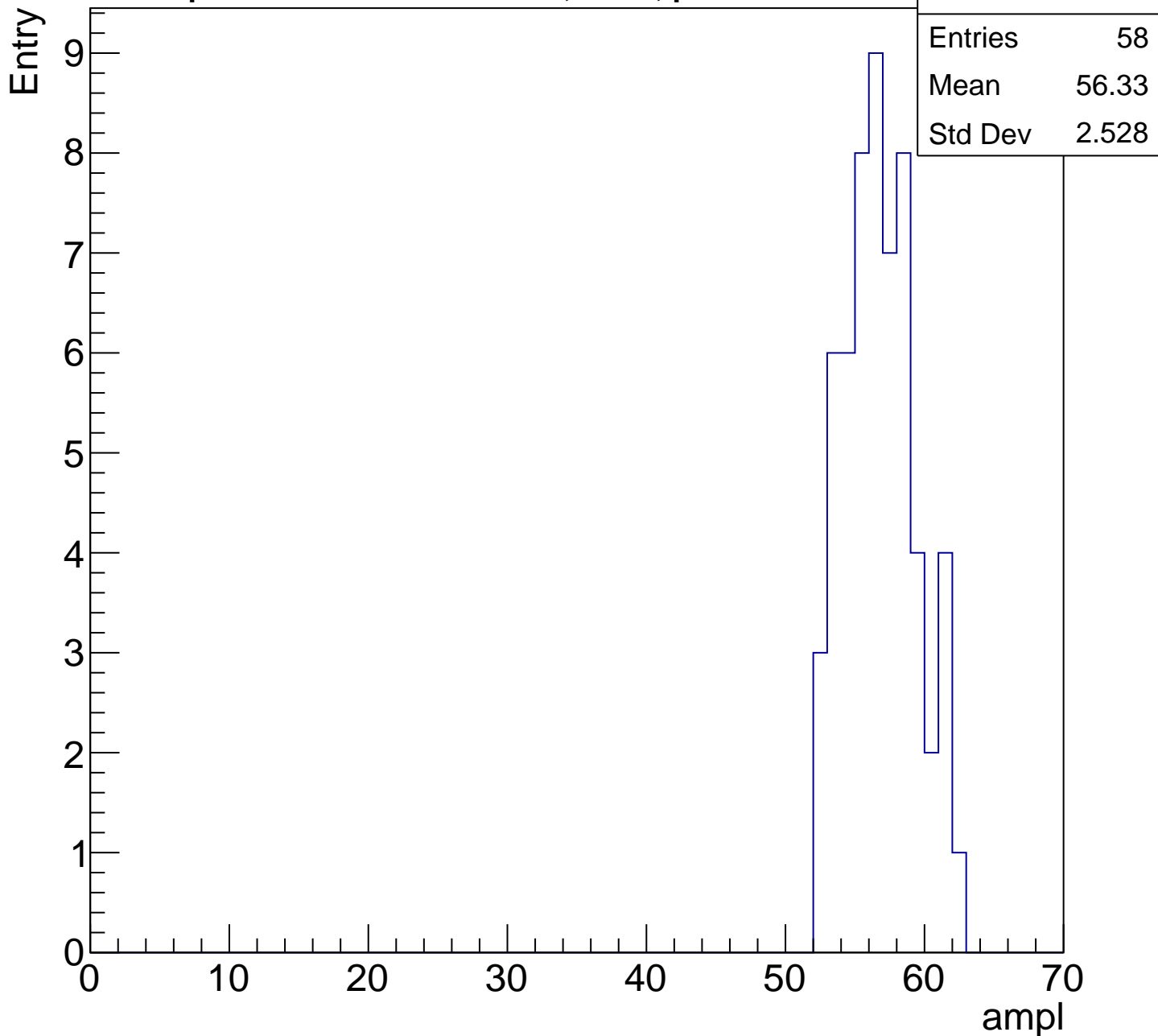
Entry

Entries	49
Mean	50.47
Std Dev	2.65



# B1L000S, U1-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

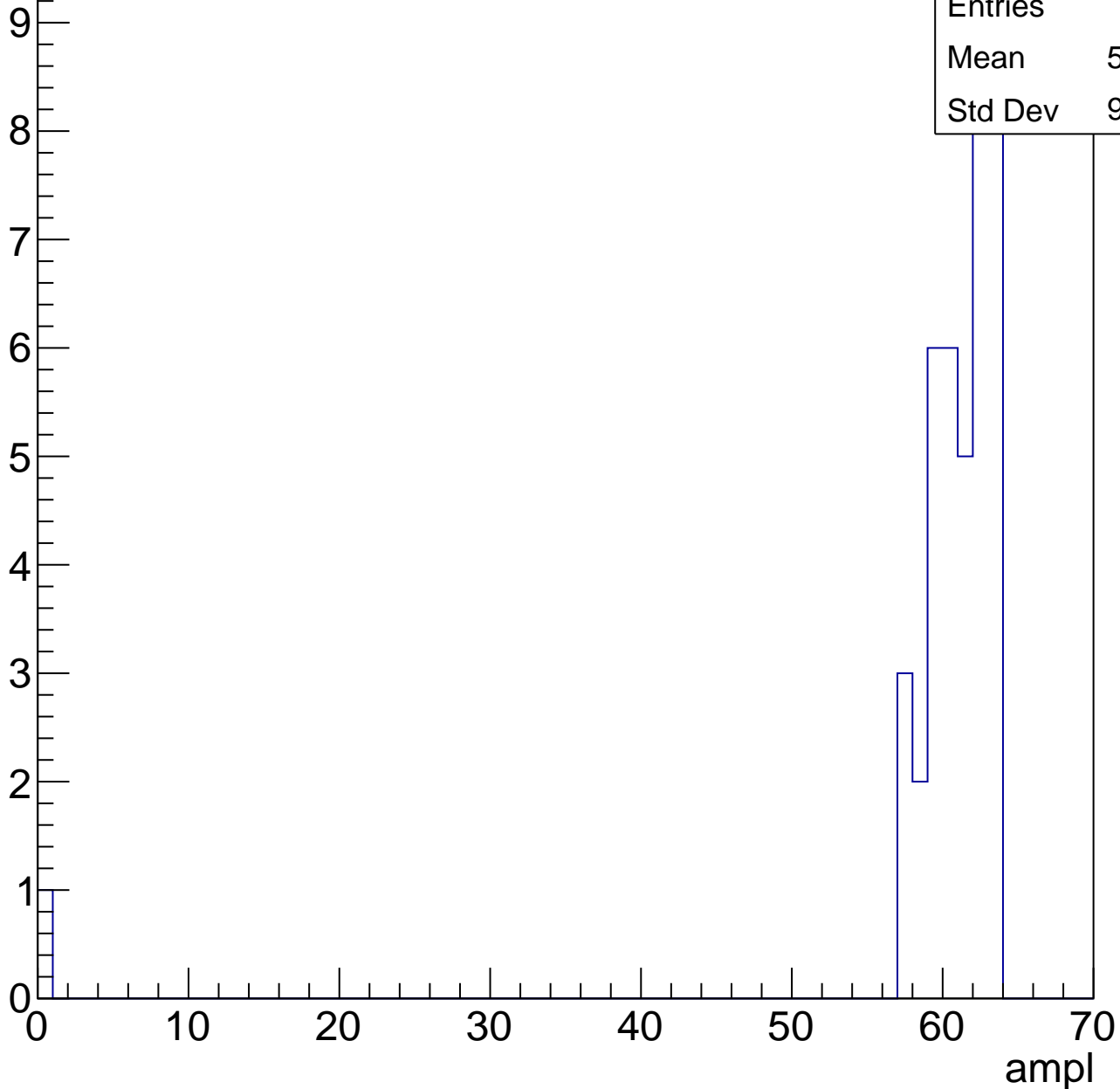


# B1L000S, U1-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	59.23
Std Dev	9.663





# B1L000S, U1-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

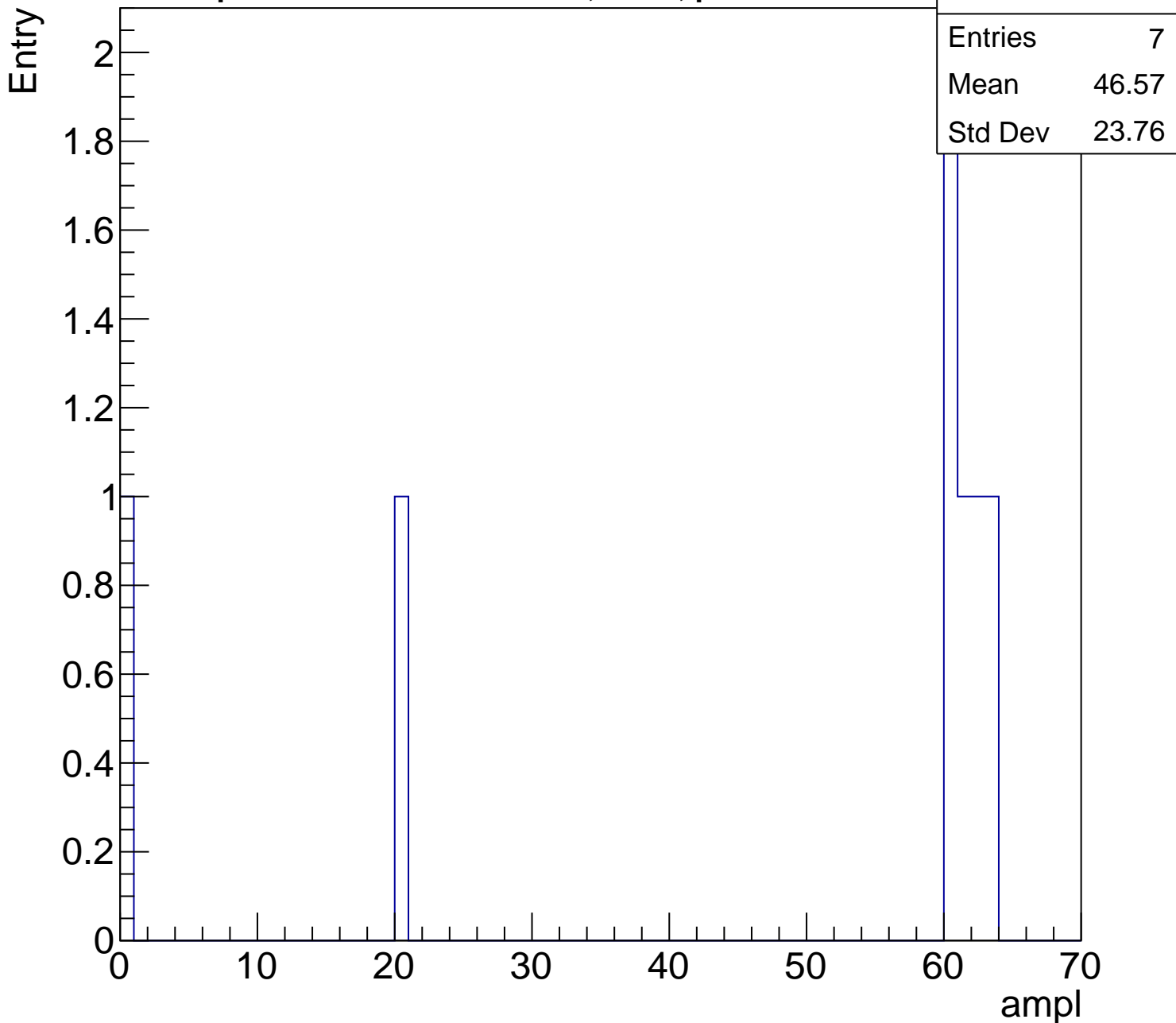
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	46.57
Std Dev	23.76

0 10 20 30 40 50 60 70

ampl



# B1L000S, U1-ch99, adc0

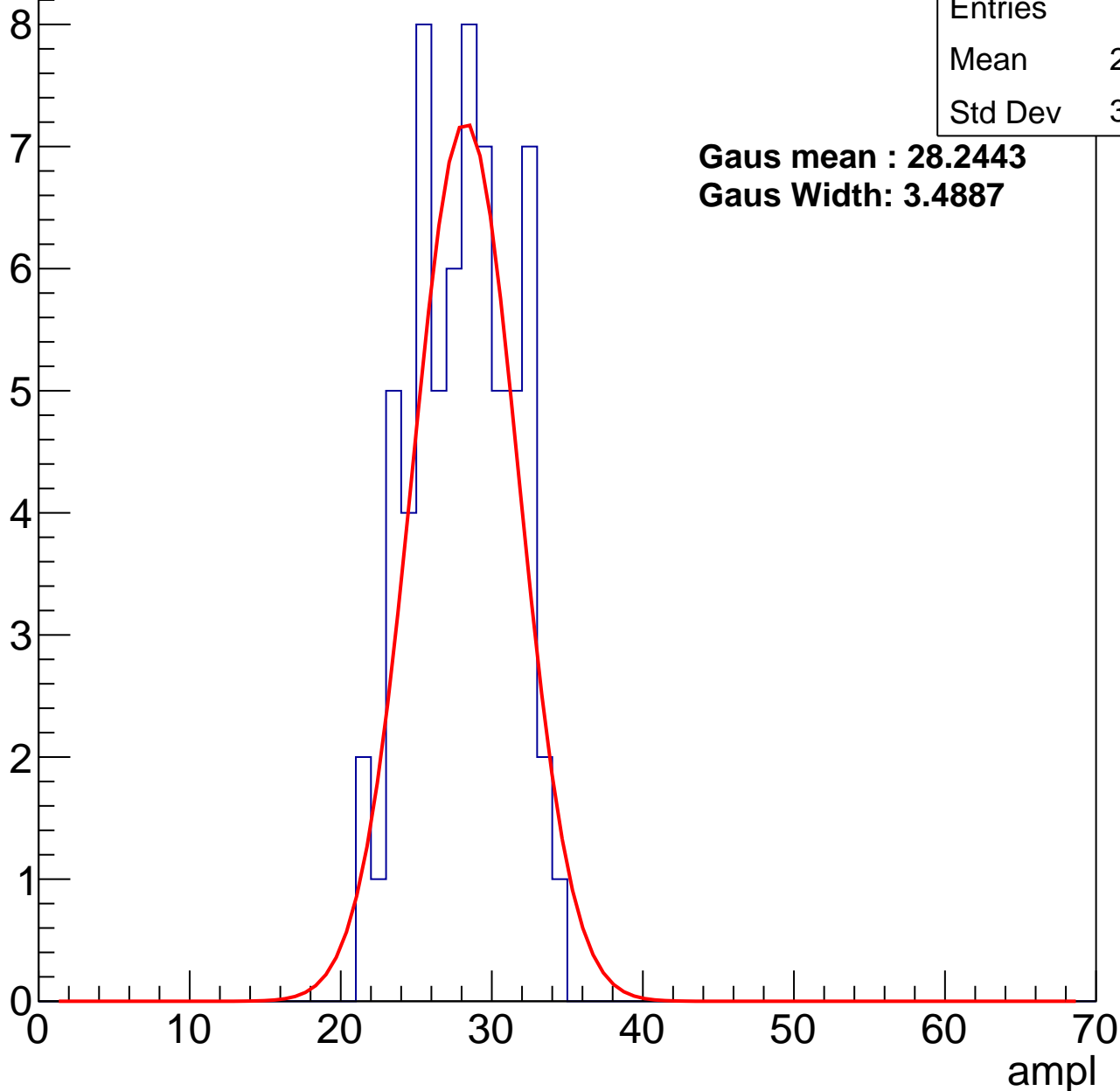
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	27.62
Std Dev	3.213

**Gaus mean : 28.2443**

**Gaus Width: 3.4887**



# B1L000S, U1-ch99, adc1

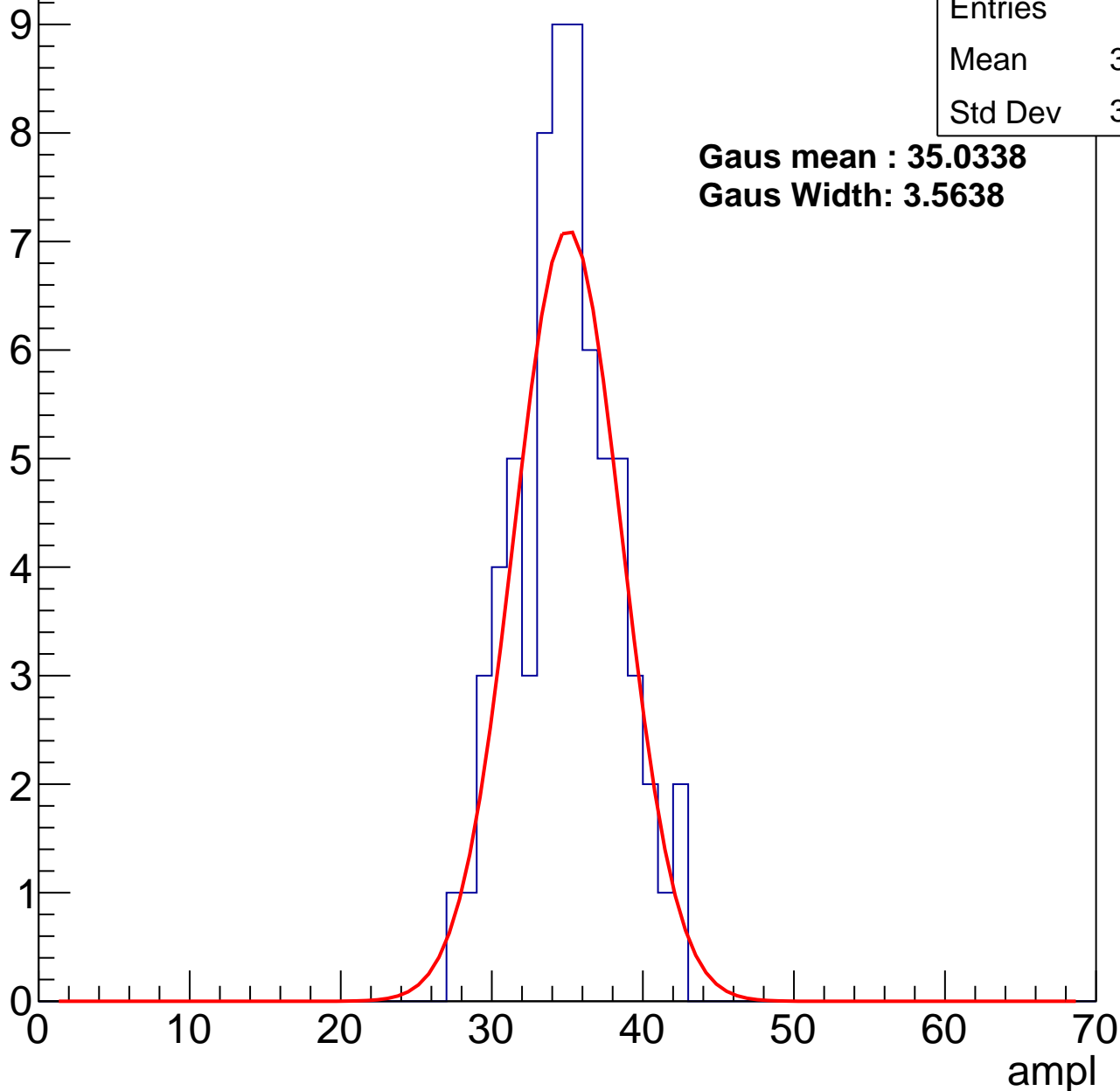
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.49
Std Dev	3.356

**Gaus mean : 35.0338**

**Gaus Width: 3.5638**



# B1L000S, U1-ch99, adc2

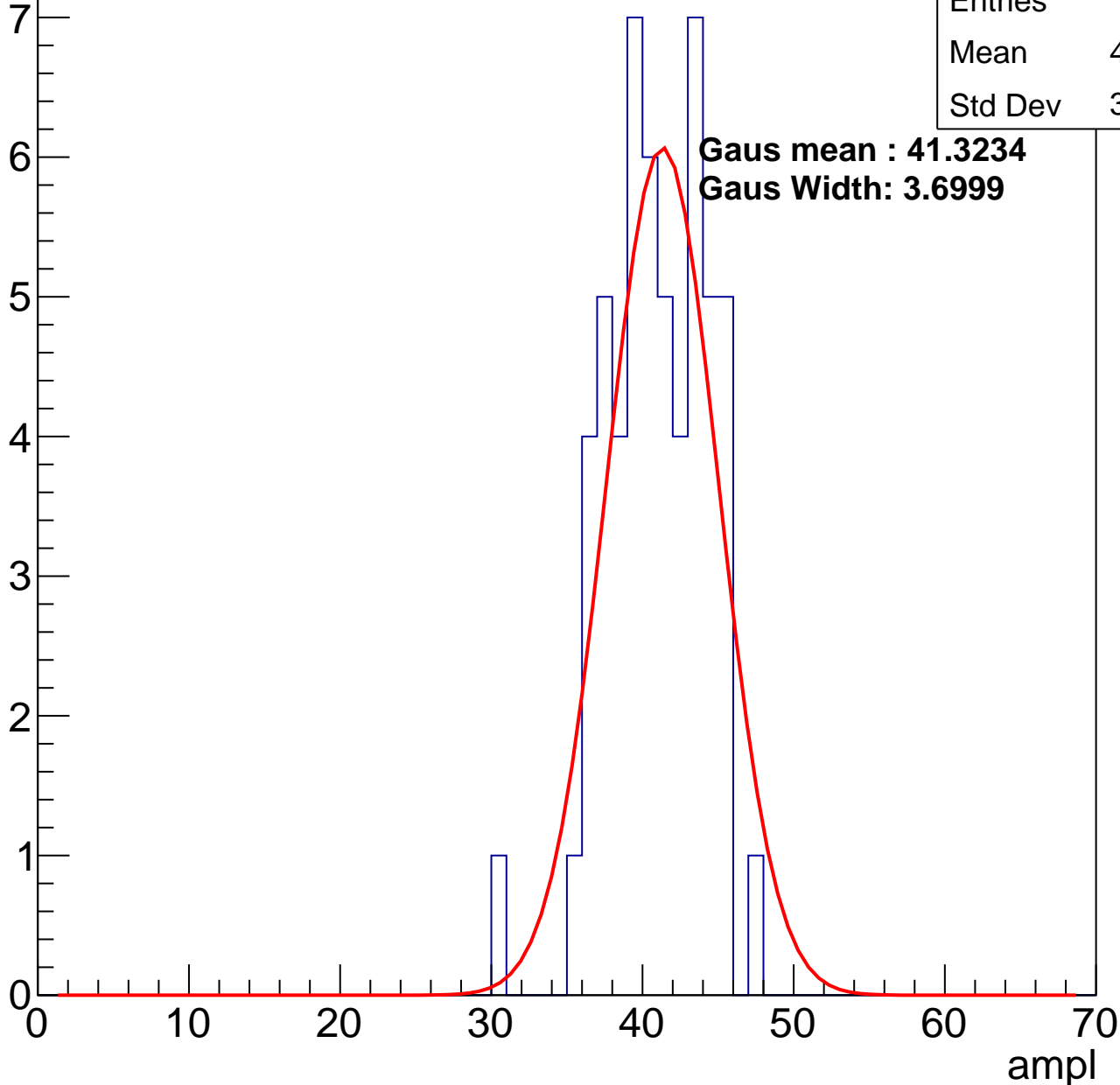
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	40.45
Std Dev	3.258

**Gaus mean : 41.3234**

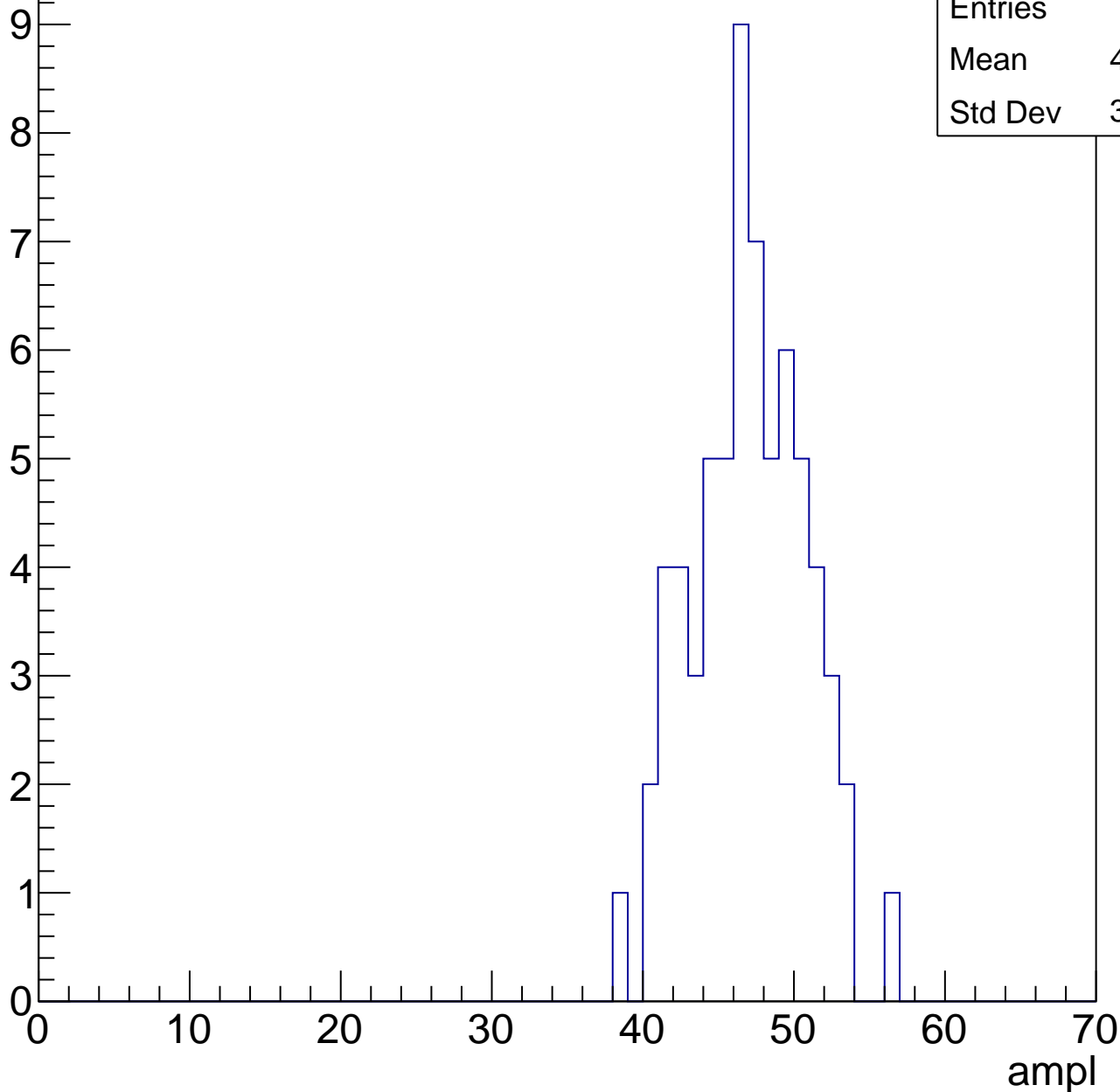
**Gaus Width: 3.6999**



# B1L000S, U1-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

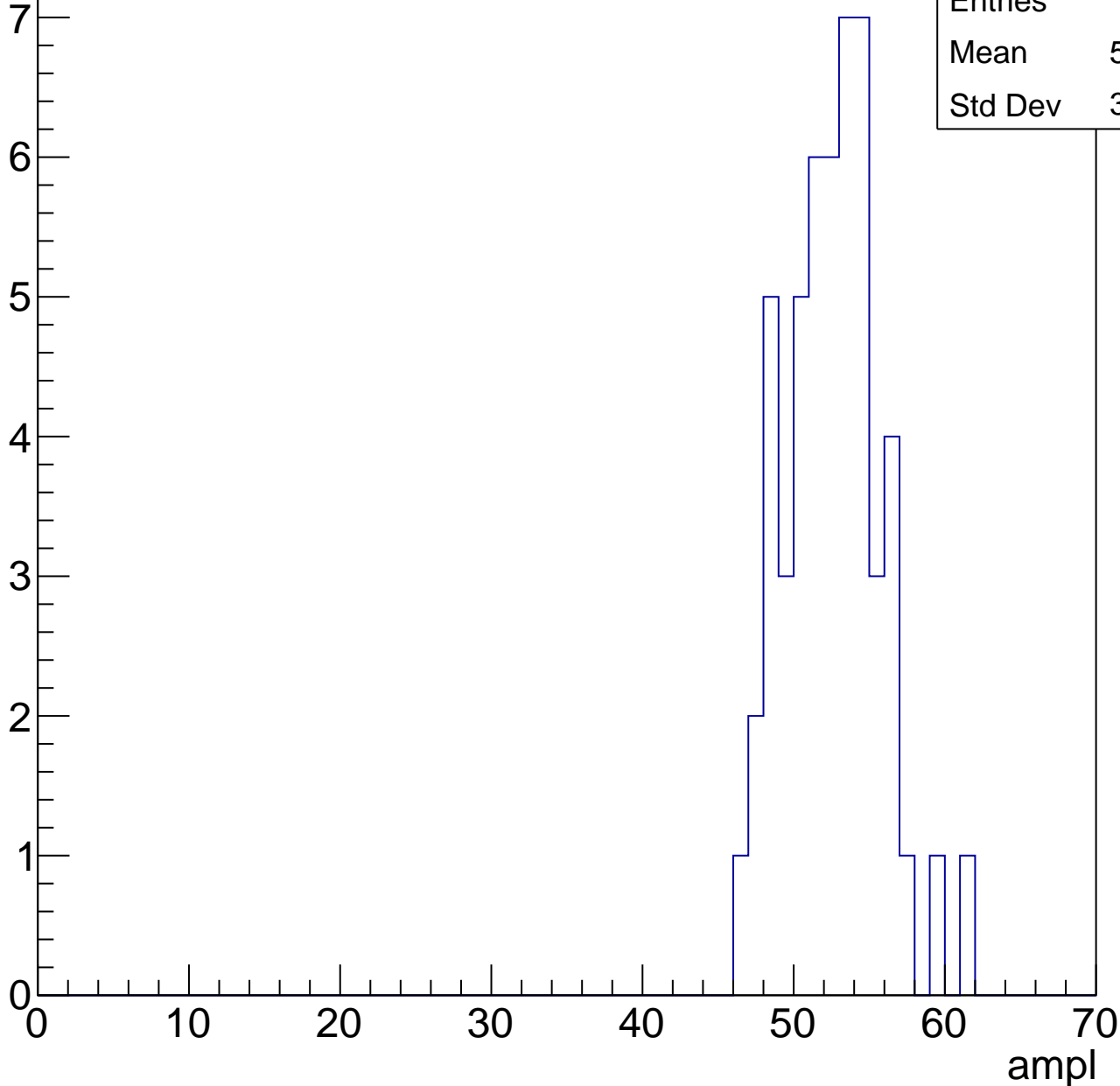


# B1L000S, U1-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

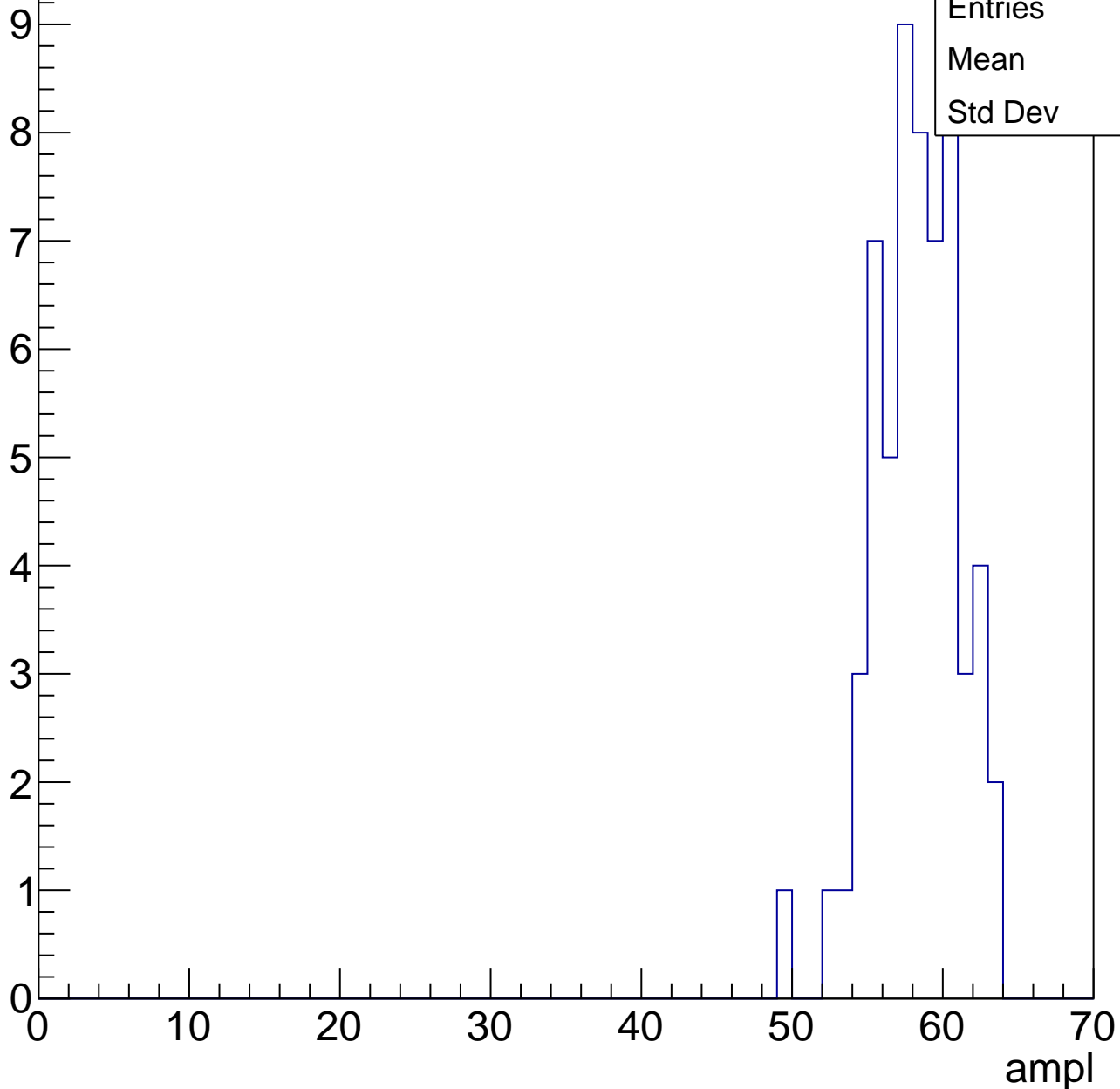
Entries	52
Mean	52.12
Std Dev	3.099



# B1L000S, U1-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



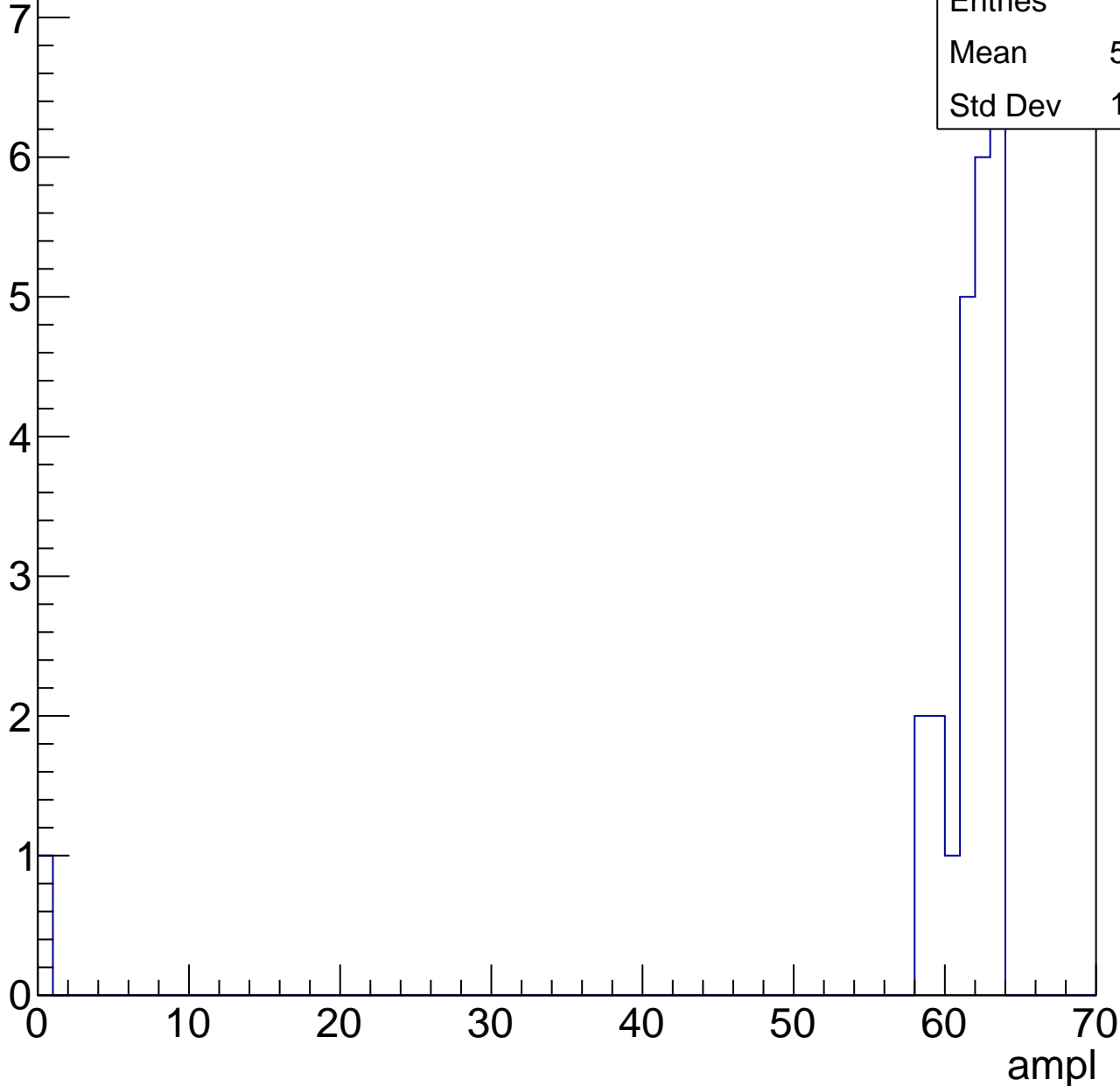
Entries	60
Mean	57.8
Std Dev	2.78

# B1L000S, U1-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.83
Std Dev	12.36





# B1L000S, U1-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch100, adc0

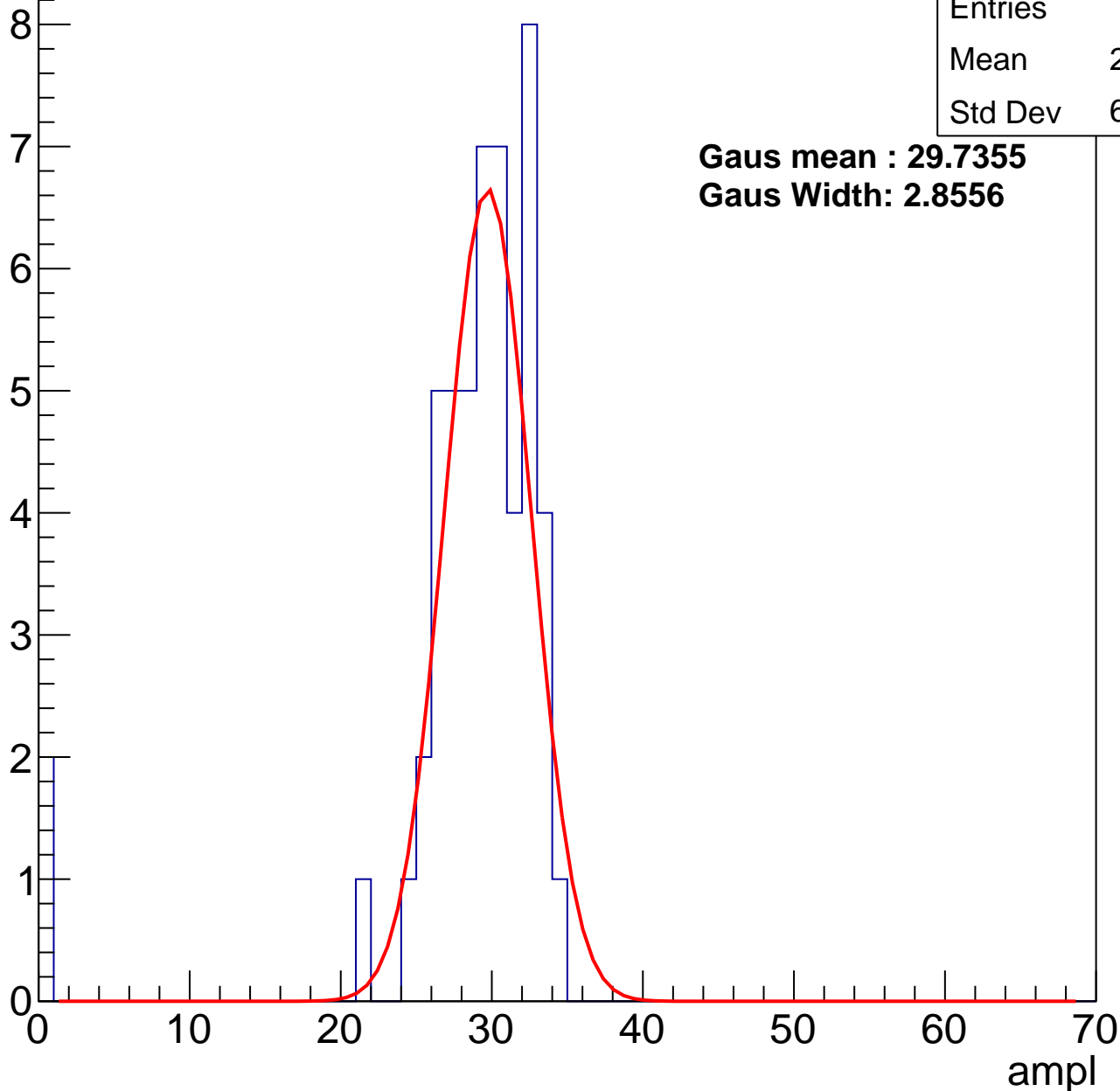
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	28.06
Std Dev	6.219

**Gaus mean : 29.7355**

**Gaus Width: 2.8556**



# B1L000S, U1-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	35.13
Std Dev	3.129

**Gaus mean : 35.3342**

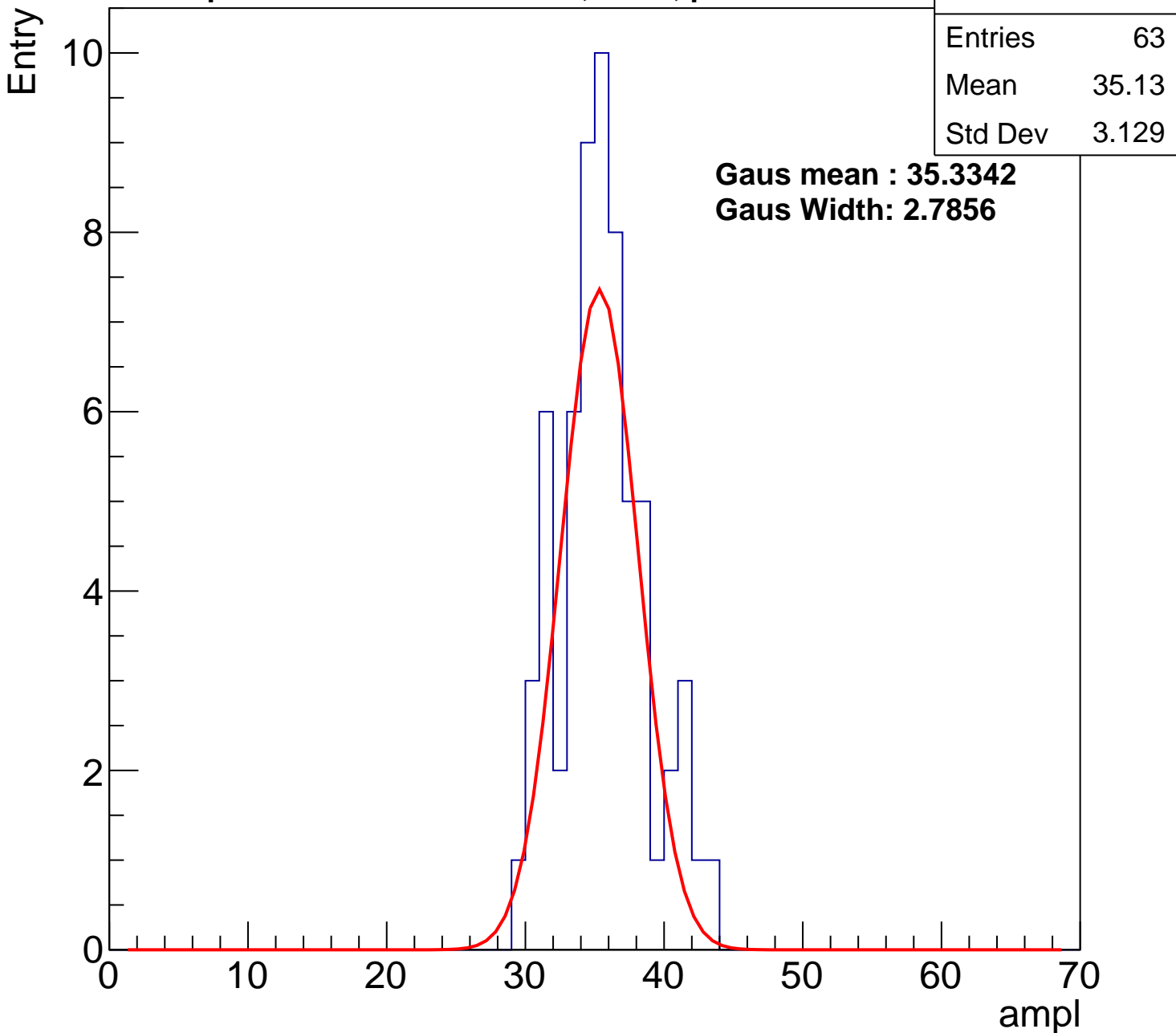
**Gaus Width: 2.7856**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch100, adc2

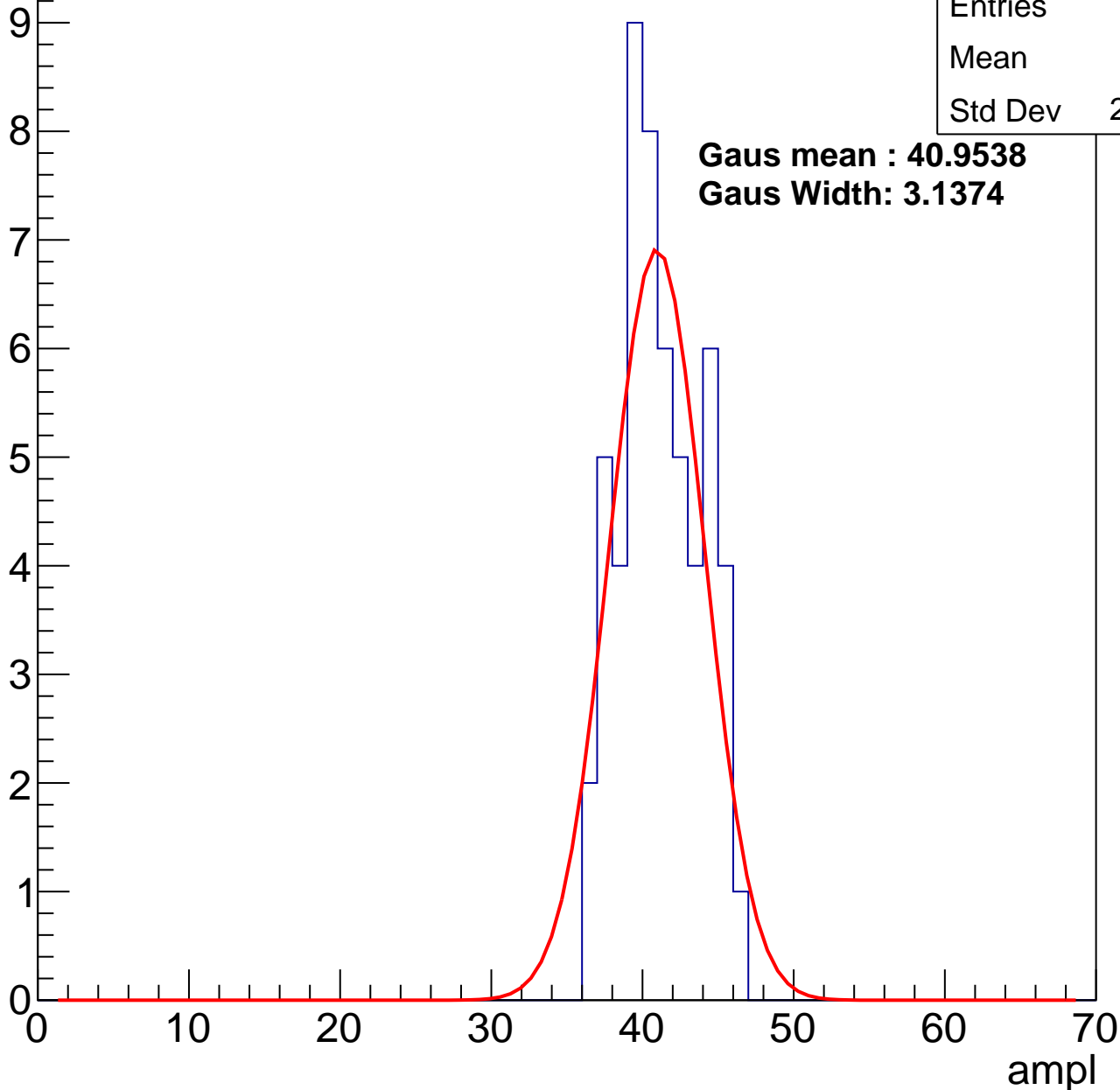
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	40.7
Std Dev	2.615

**Gaus mean : 40.9538**

**Gaus Width: 3.1374**

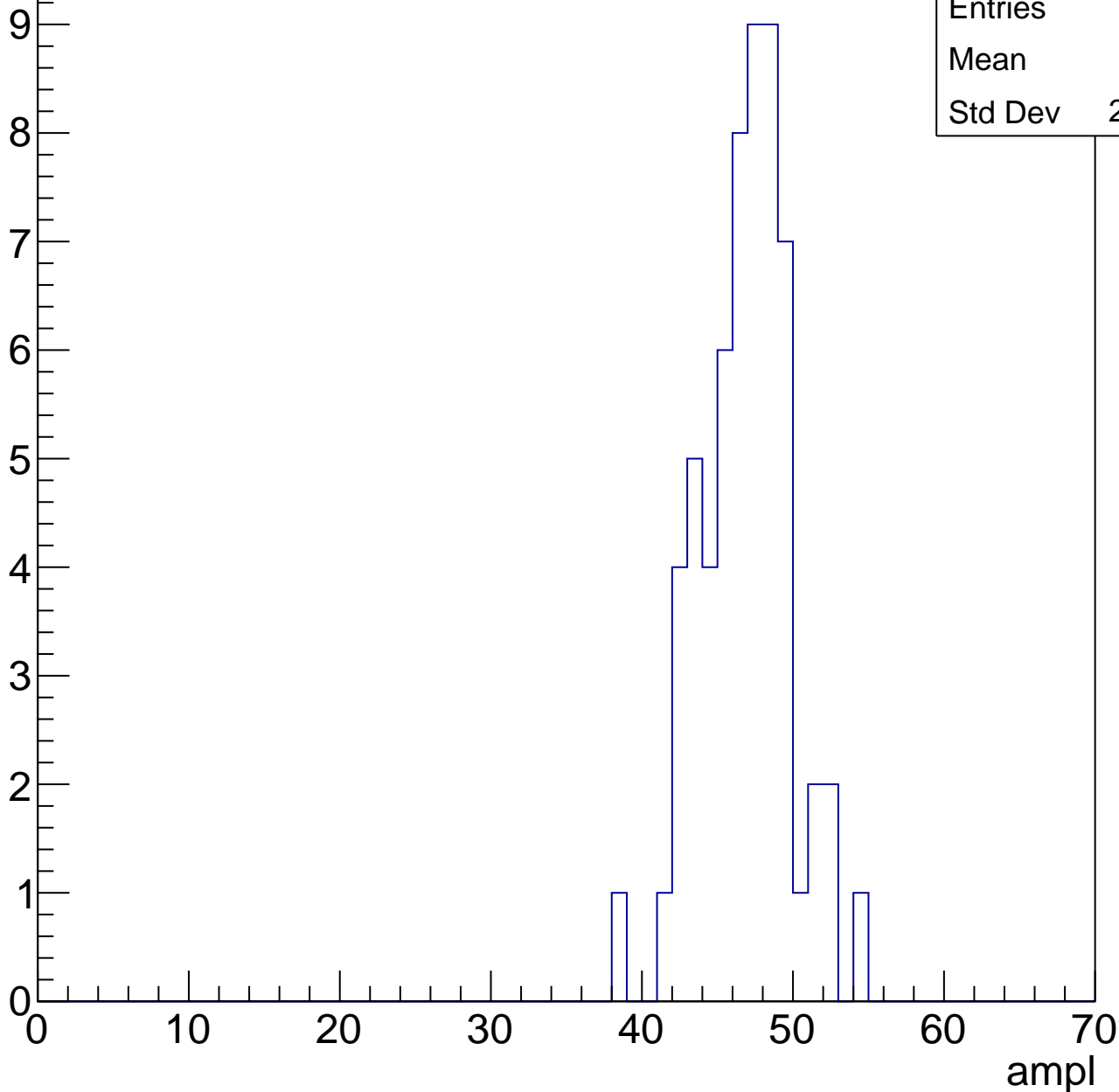


# B1L000S, U1-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

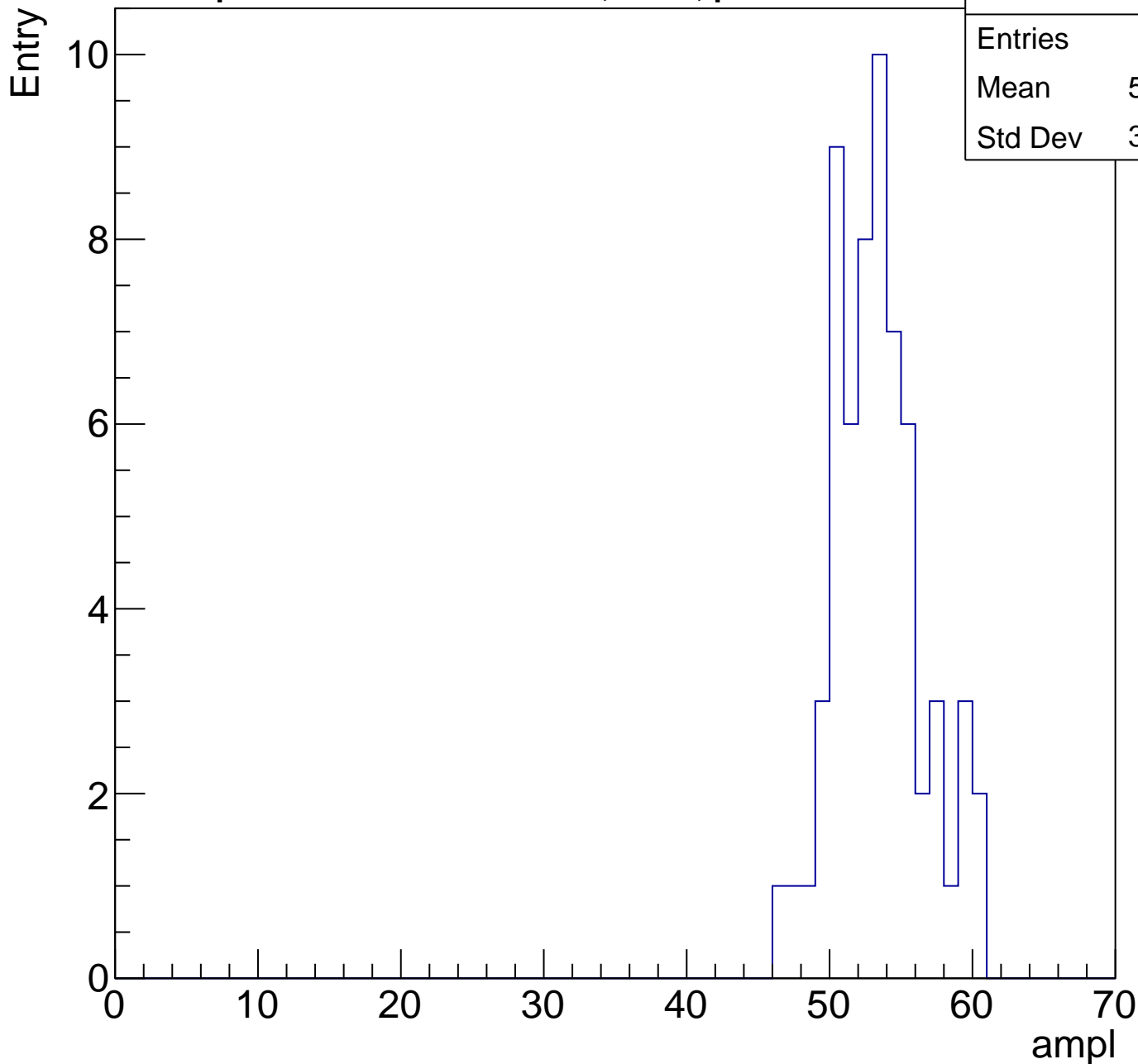
Entries	60
Mean	46.4
Std Dev	2.945



# B1L000S, U1-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	52.95
Std Dev	3.083

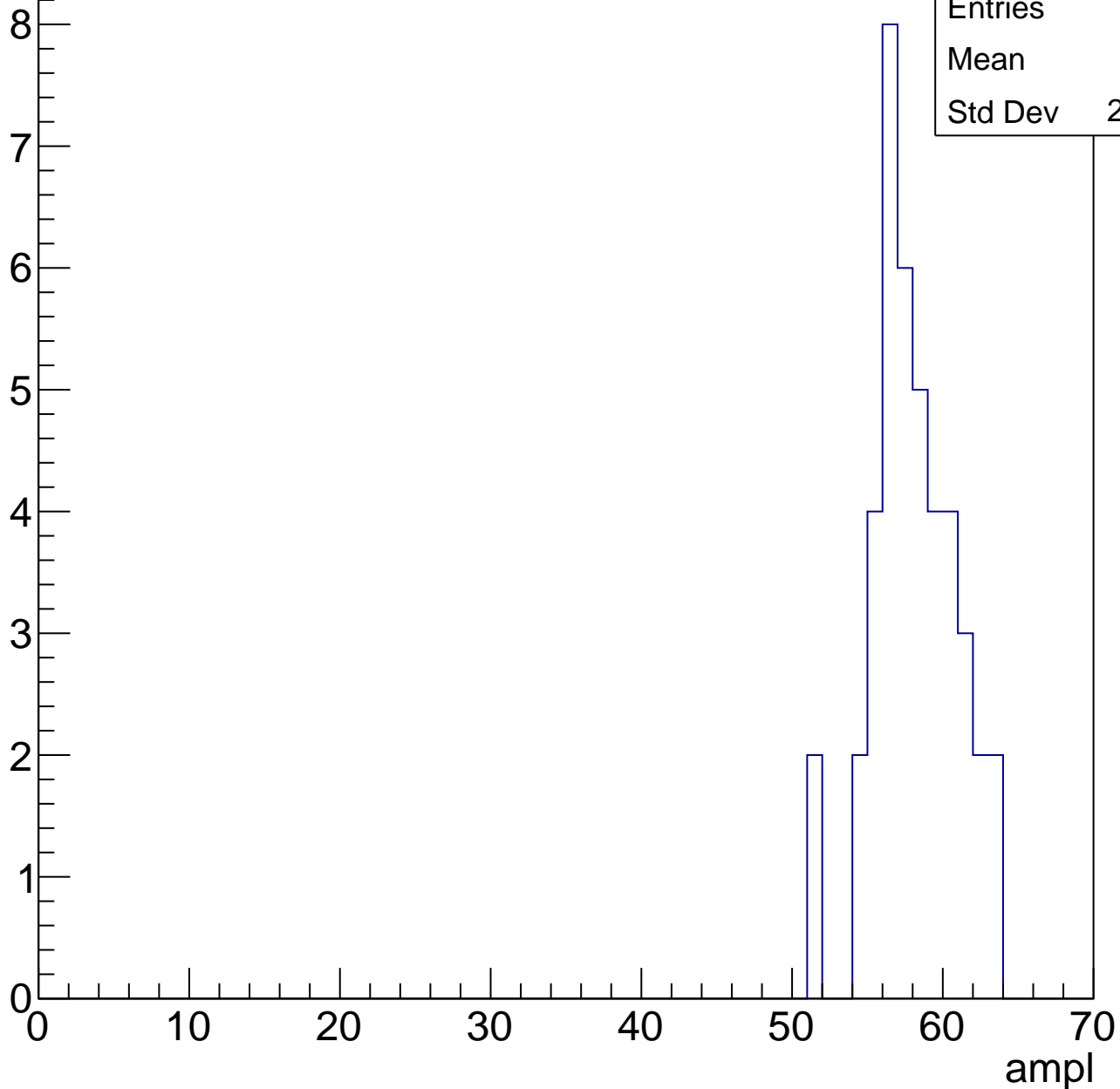


# B1L000S, U1-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	57.6
Std Dev	2.787

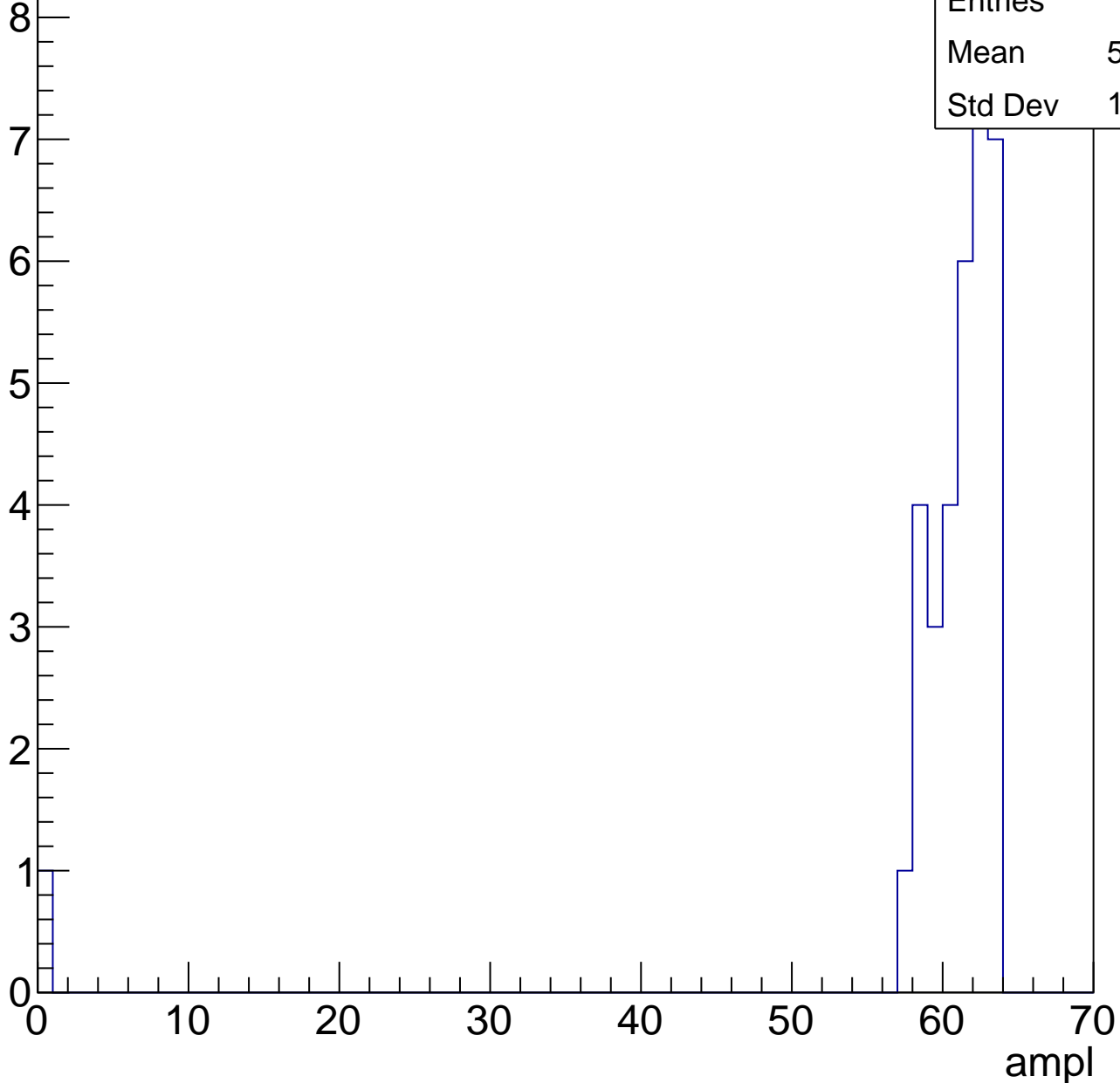


# B1L000S, U1-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	34
Mean	59.09
Std Dev	10.43

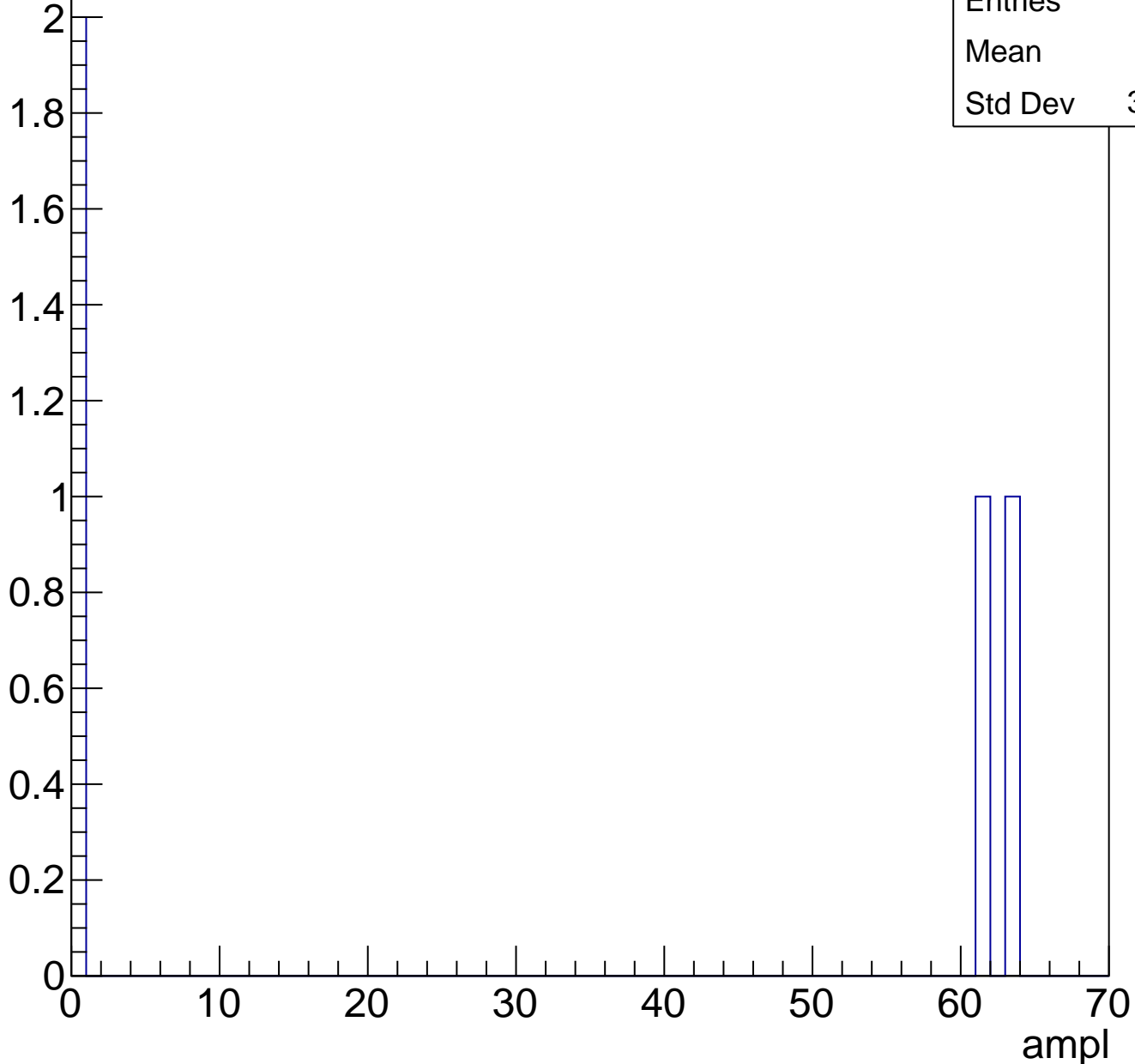




# B1L000S, U1-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	31
Std Dev	31.01

# B1L000S, U1-ch101, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	28.36
Std Dev	3.485

**Gaus mean : 28.7962**

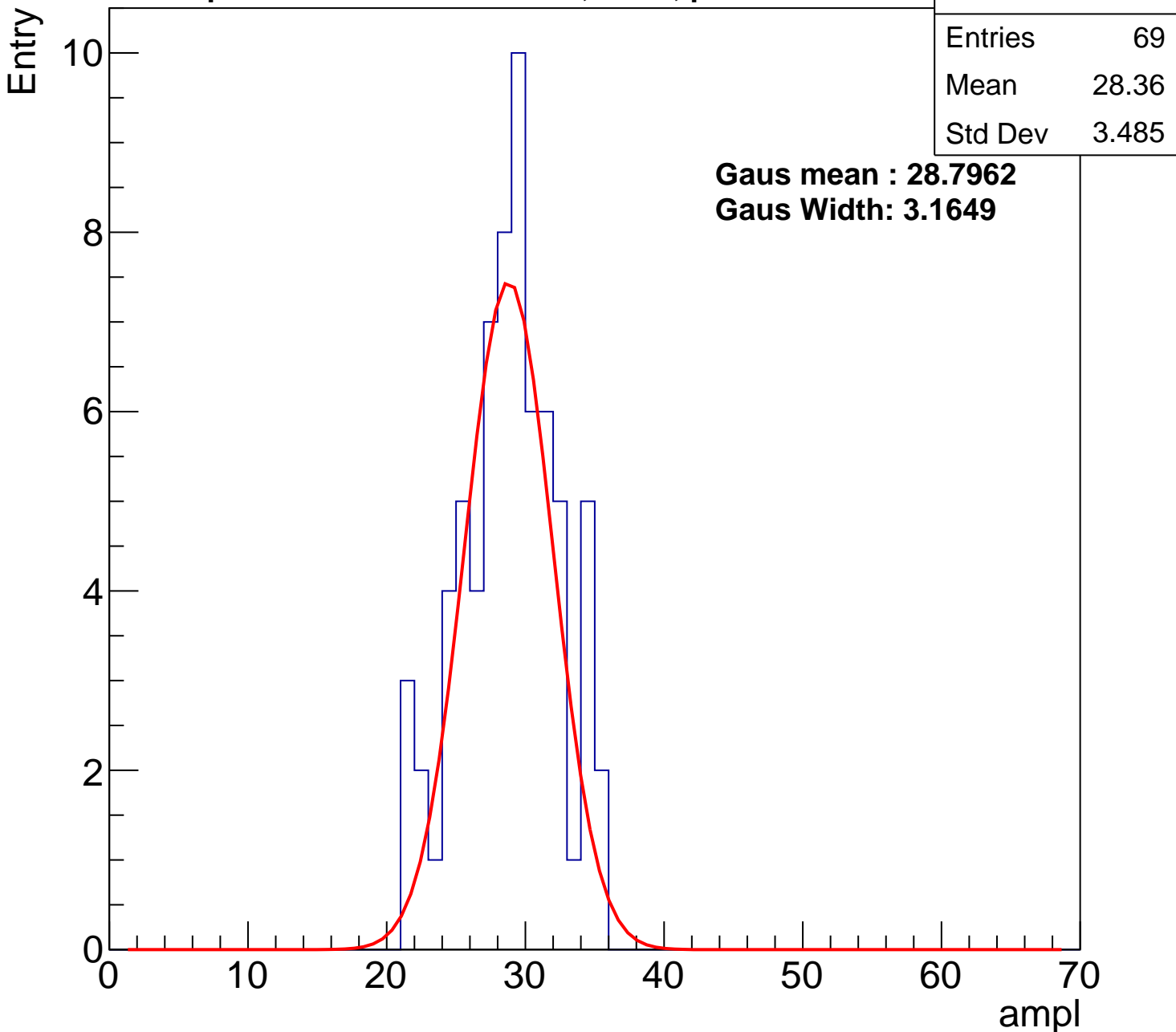
**Gaus Width: 3.1649**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch101, adc1

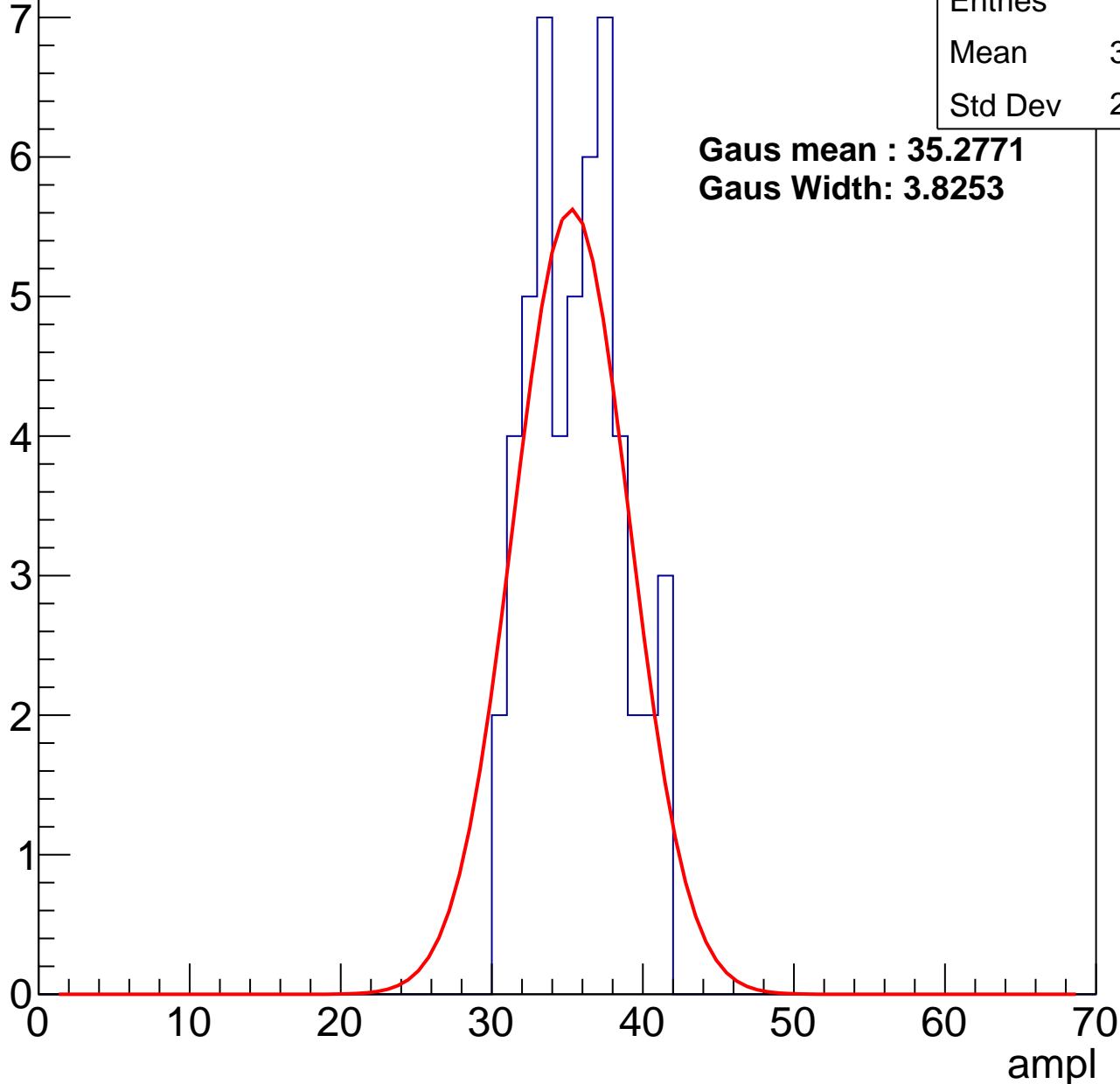
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	35.18
Std Dev	2.969

**Gaus mean : 35.2771**

**Gaus Width: 3.8253**



# B1L000S, U1-ch101, adc2

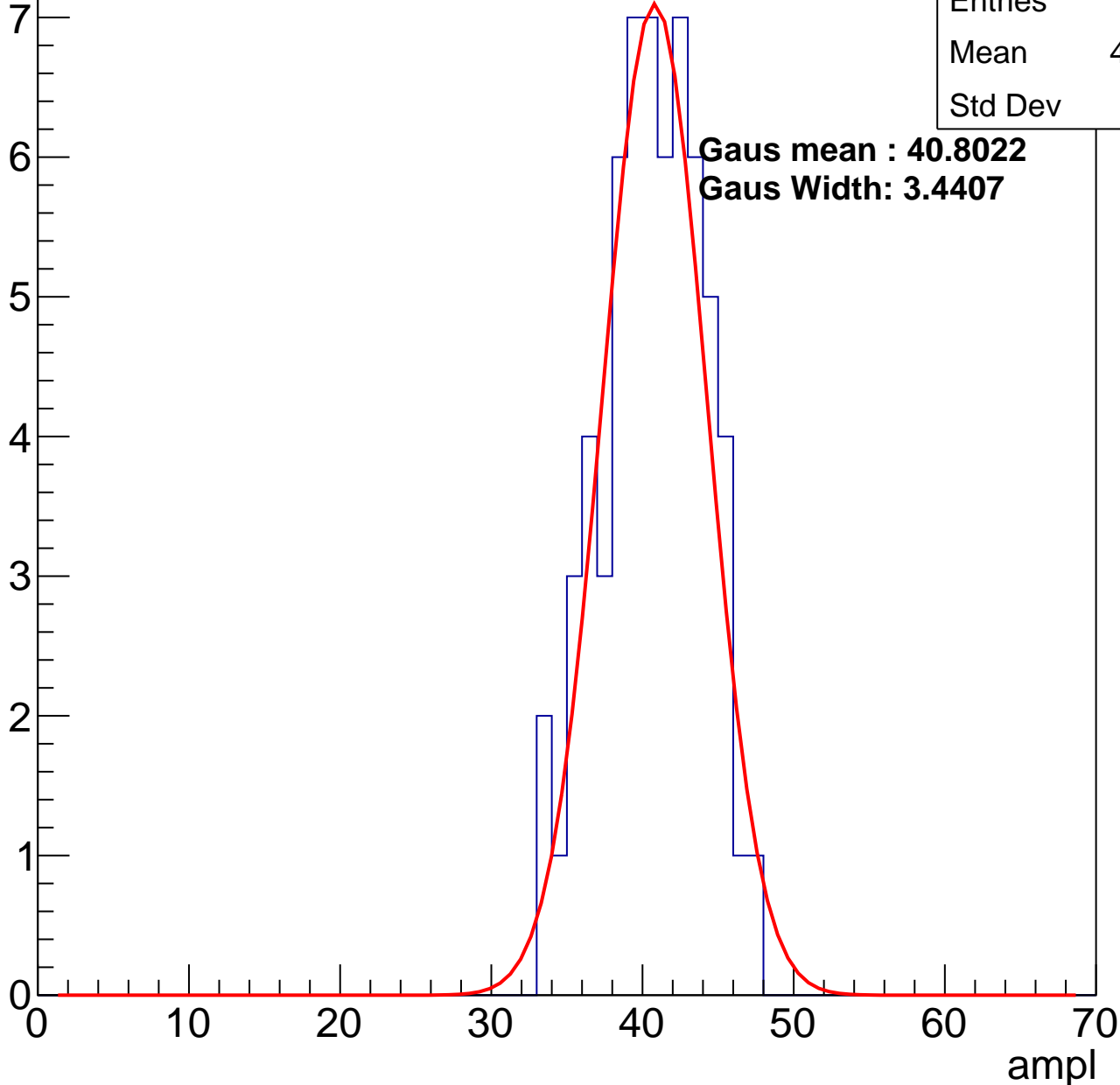
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	40.19
Std Dev	3.29

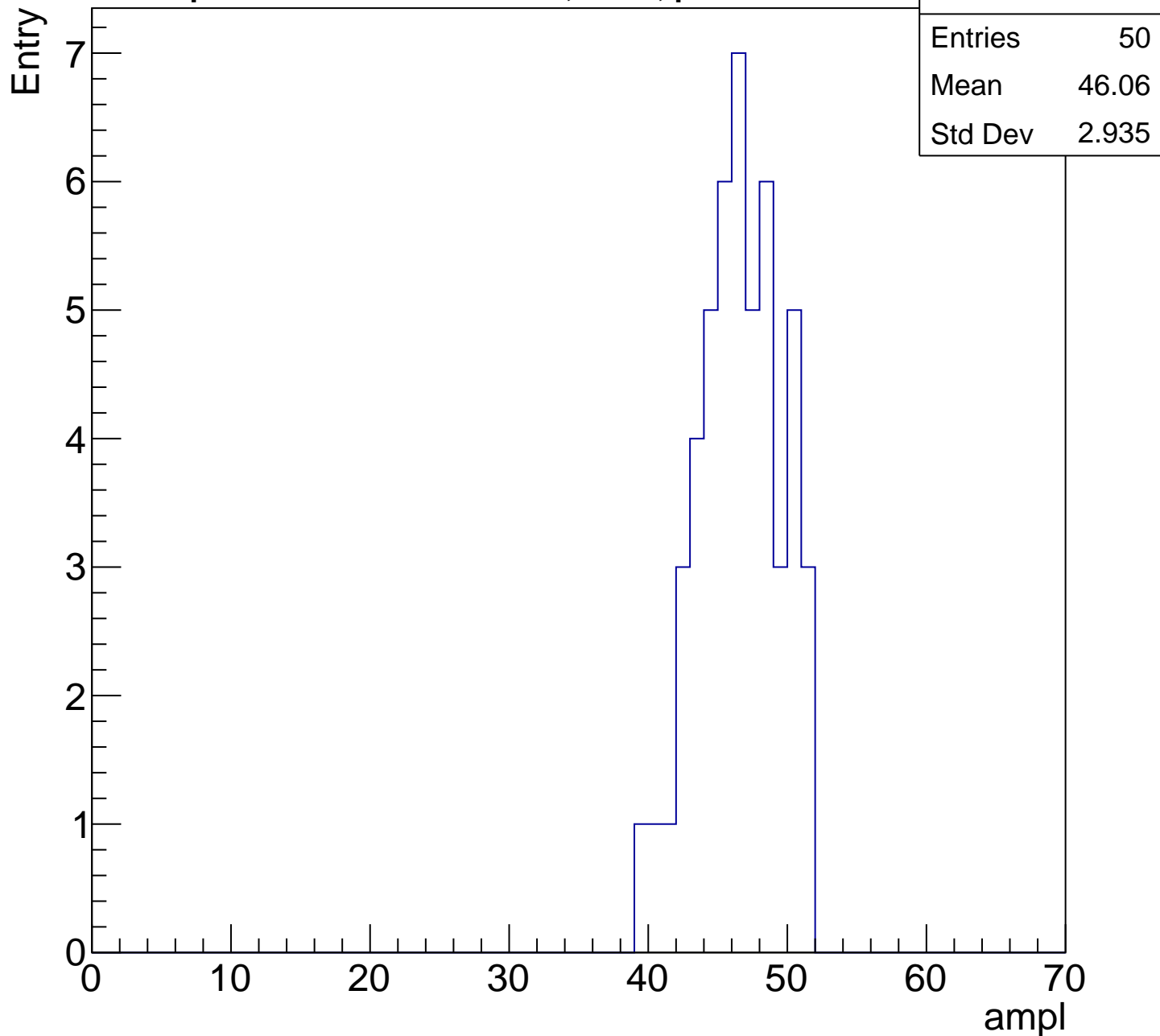
**Gaus mean : 40.8022**

**Gaus Width: 3.4407**



# B1L000S, U1-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

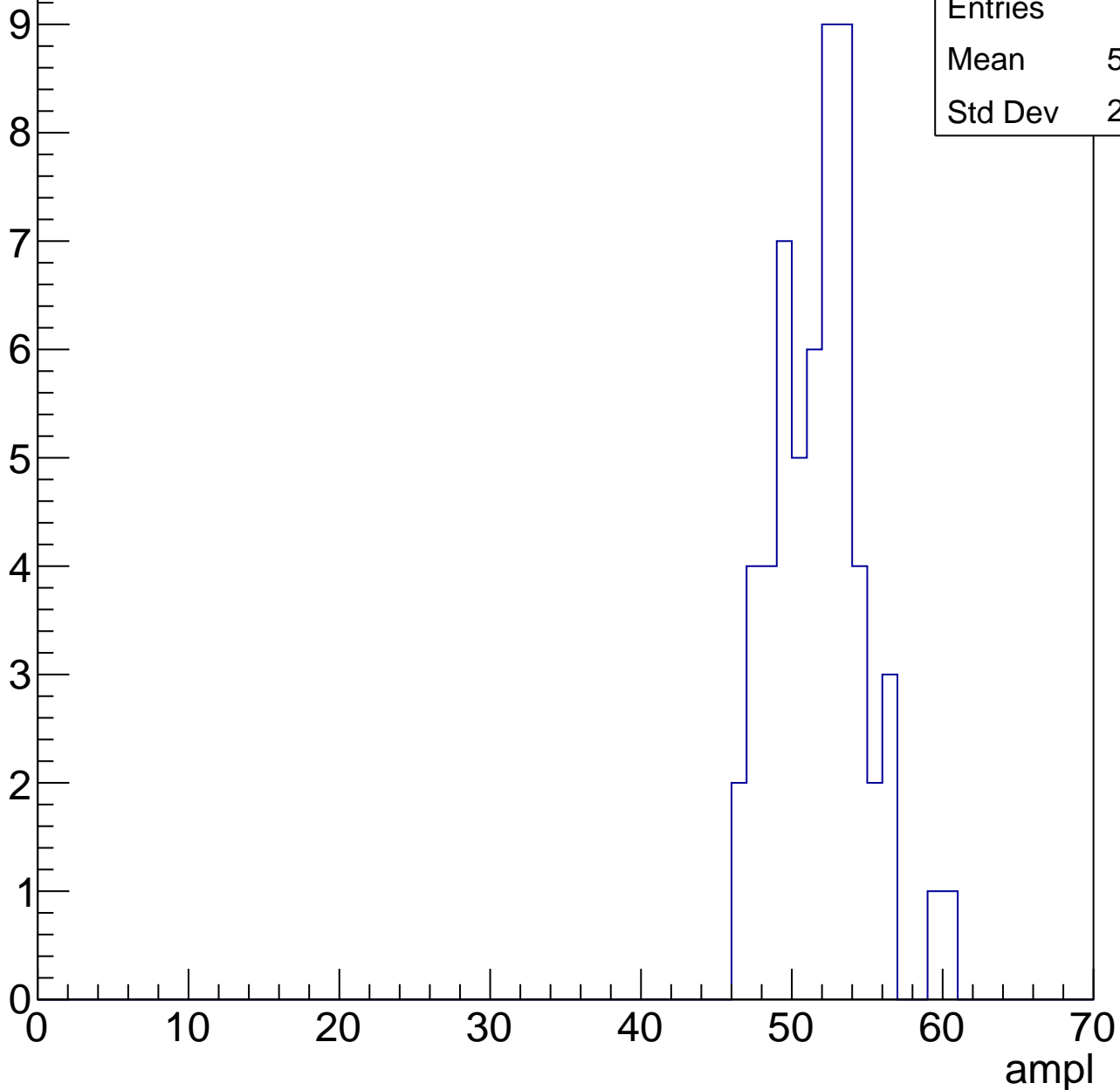


# B1L000S, U1-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

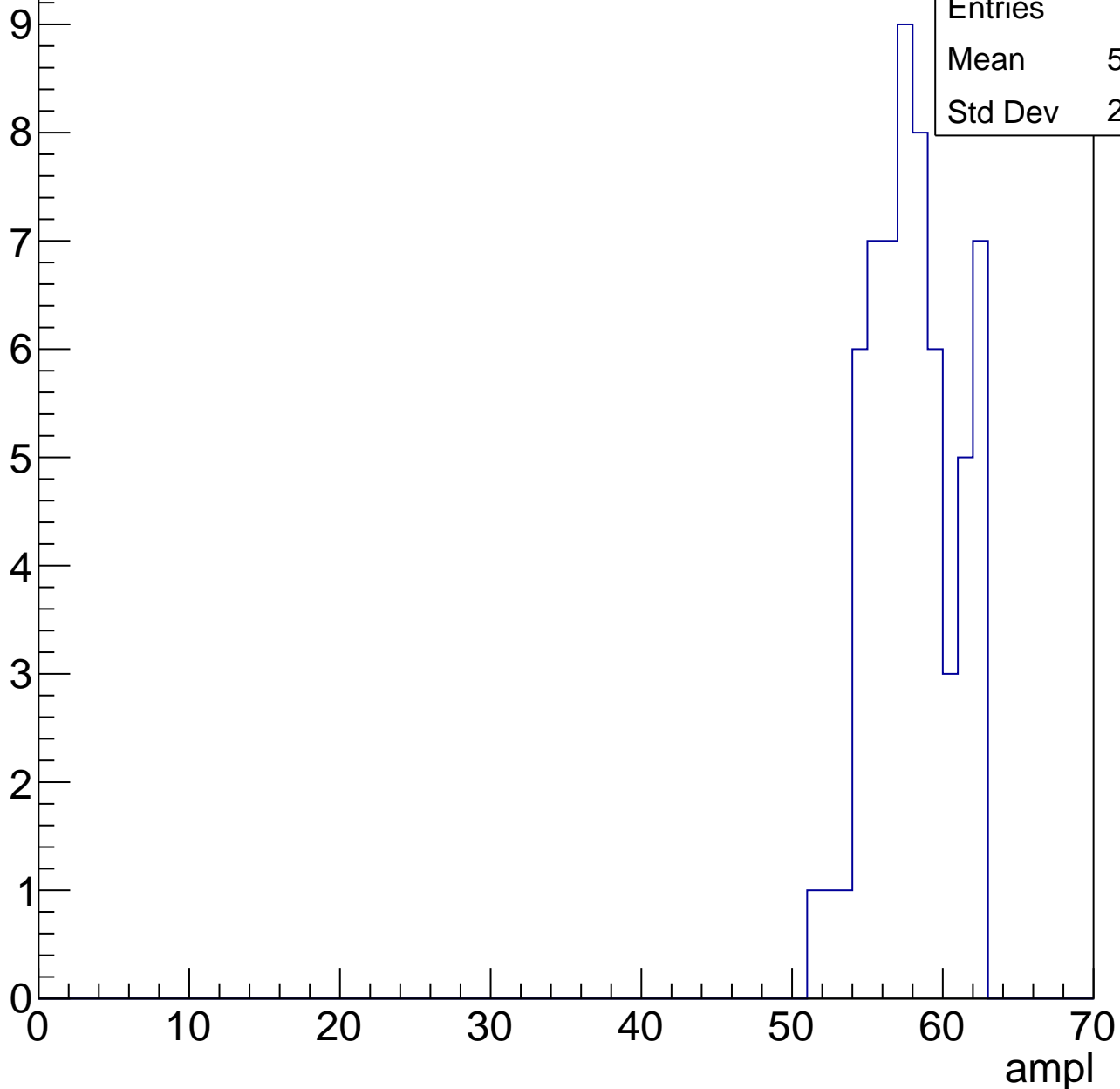
Entries	57
Mean	51.39
Std Dev	2.984



# B1L000S, U1-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

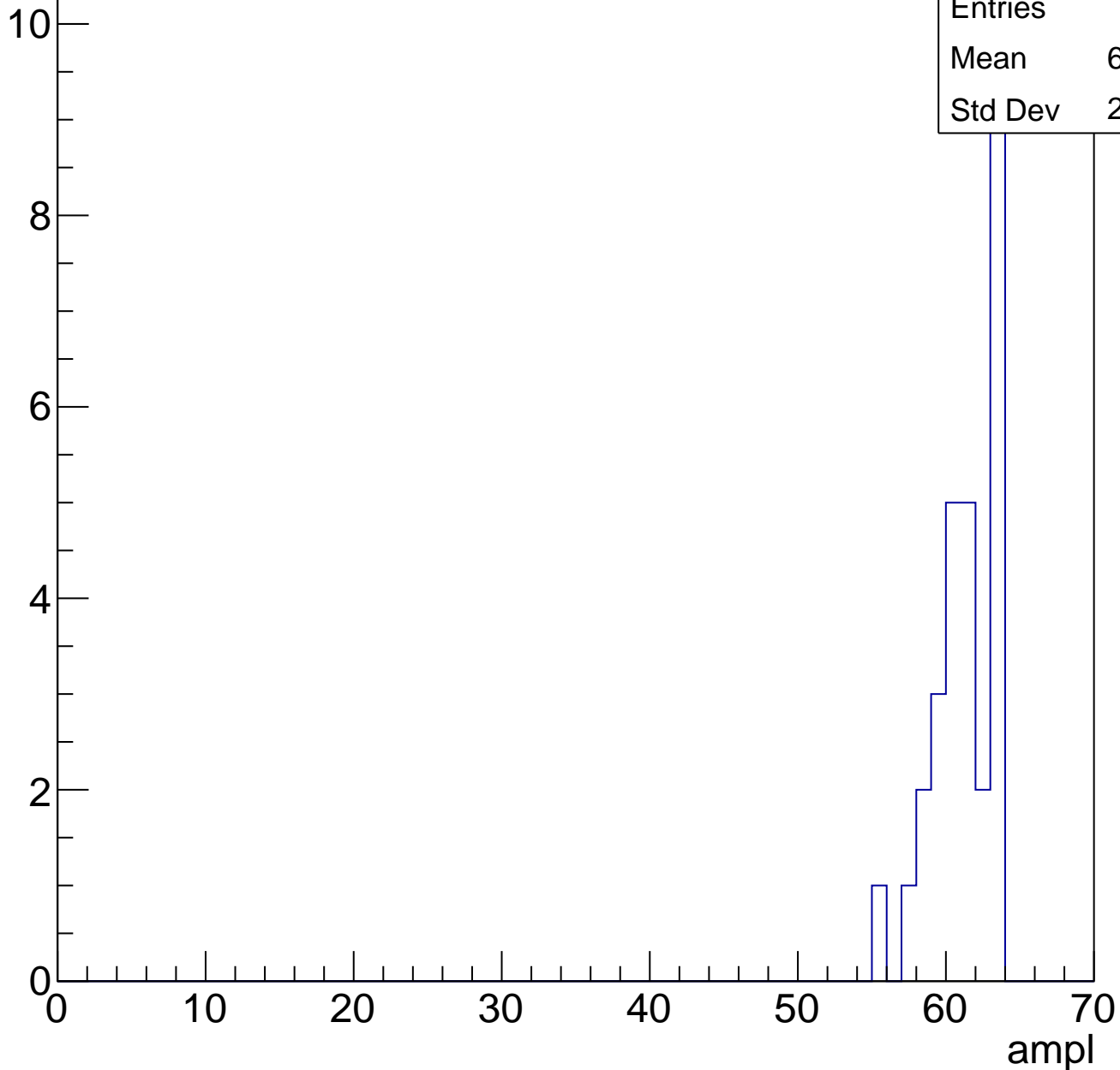


# B1L000S, U1-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	29
Mean	60.83
Std Dev	2.102

Entry

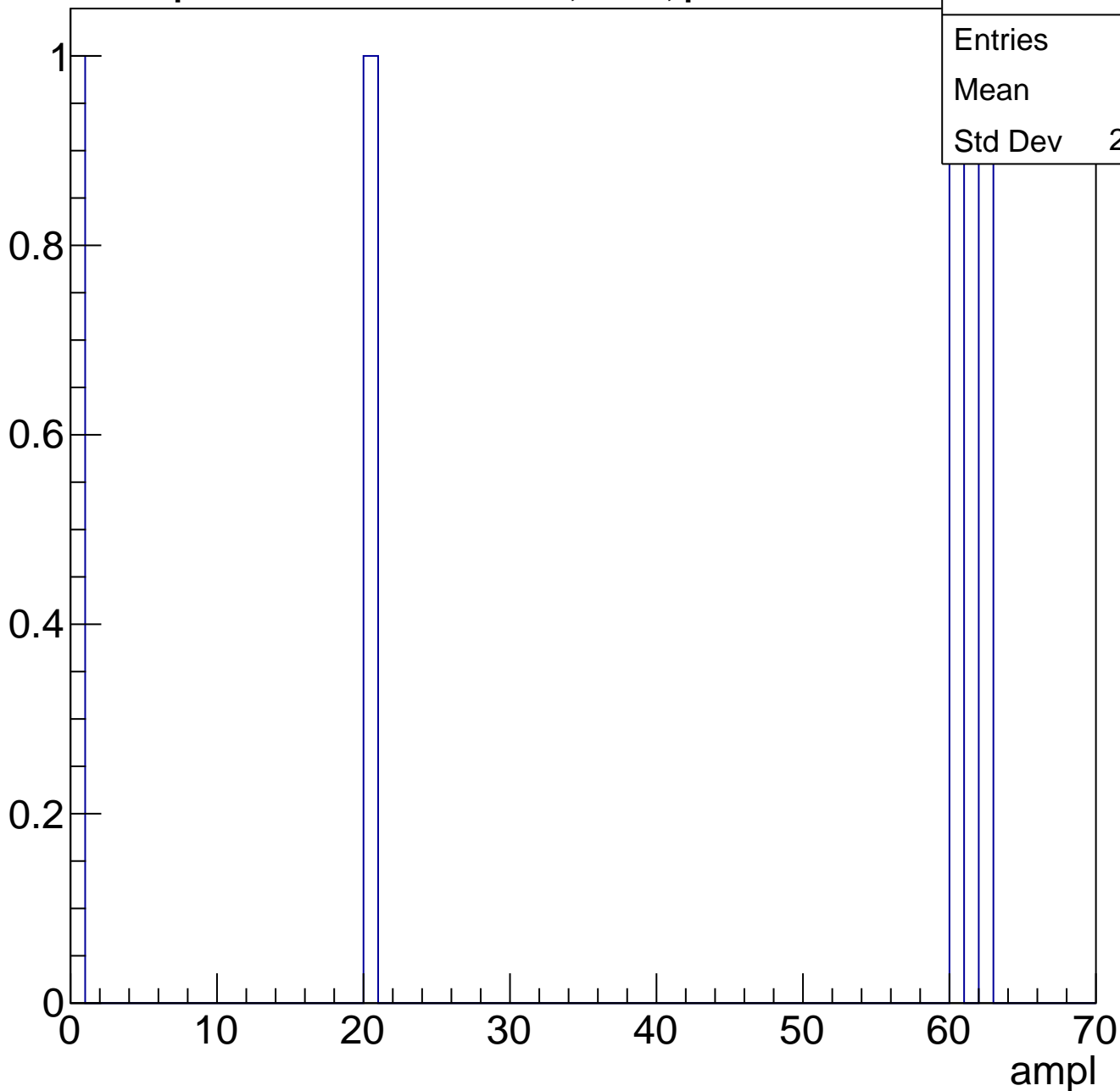




# B1L000S, U1-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	35.5
Std Dev	26.47

# B1L000S, U1-ch102, adc0

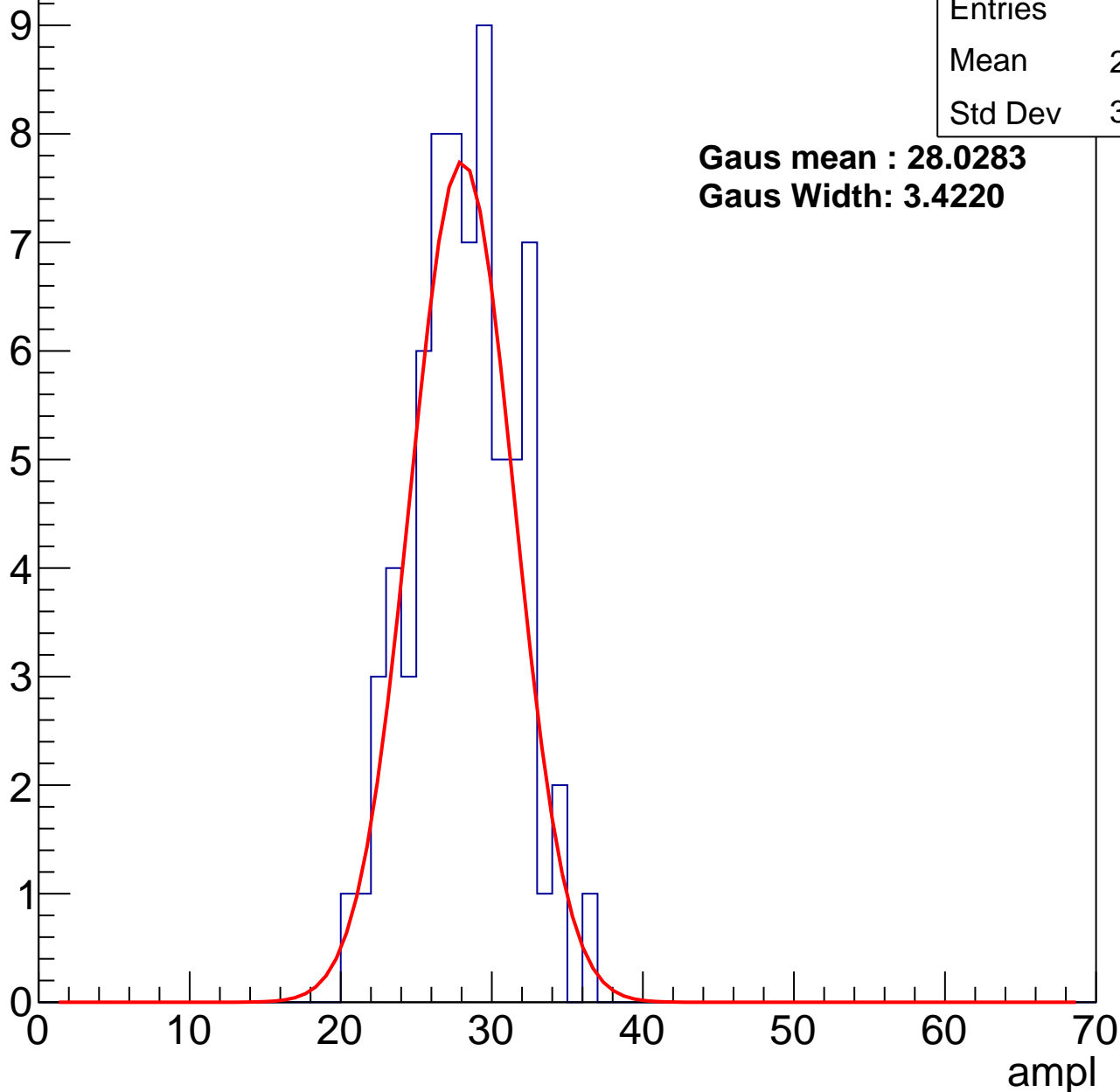
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	27.72
Std Dev	3.366

**Gaus mean : 28.0283**

**Gaus Width: 3.4220**



# B1L000S, U1-ch102, adc1

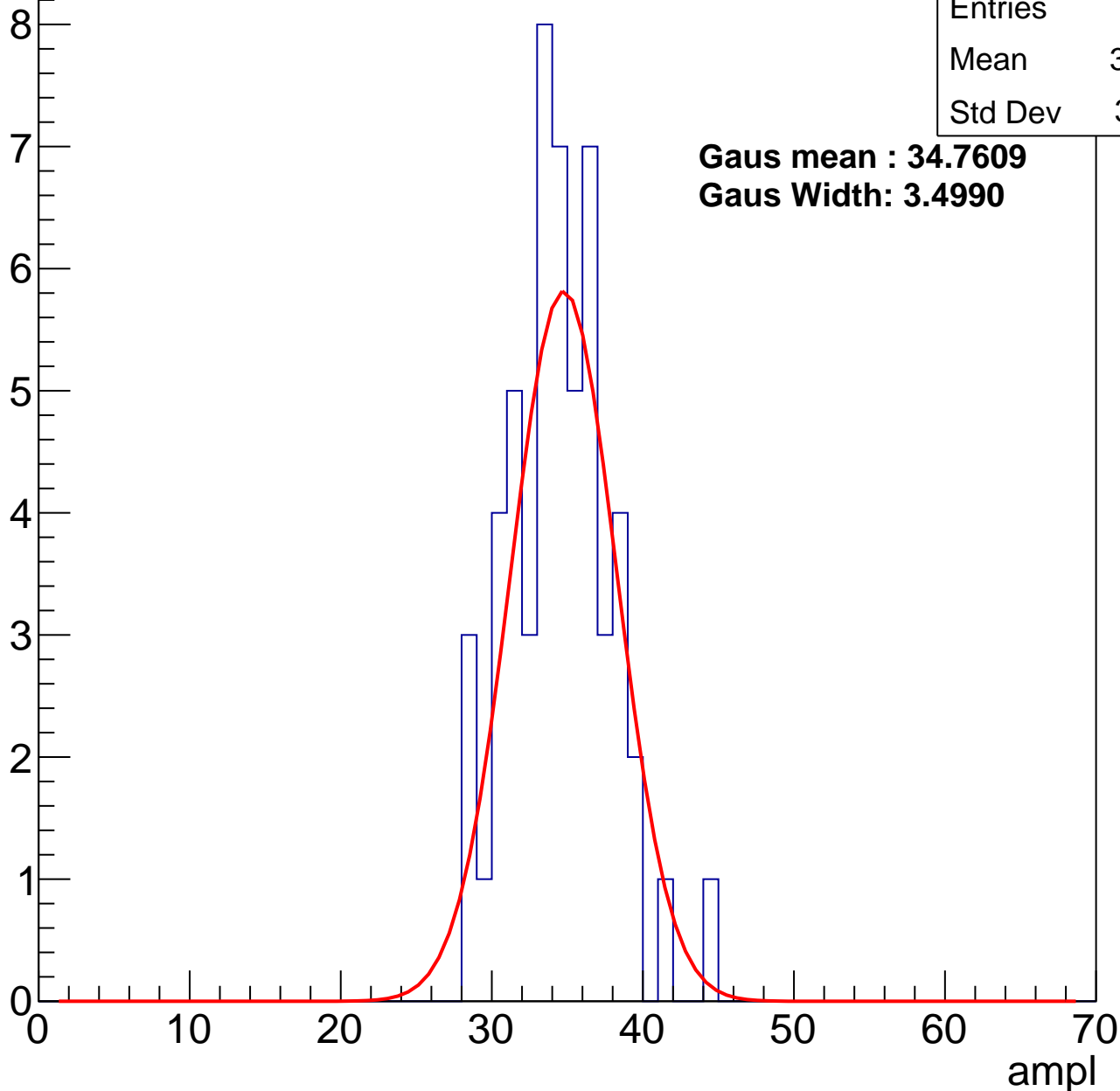
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	34.06
Std Dev	3.291

**Gaus mean : 34.7609**

**Gaus Width: 3.4990**



# B1L000S, U1-ch102, adc2

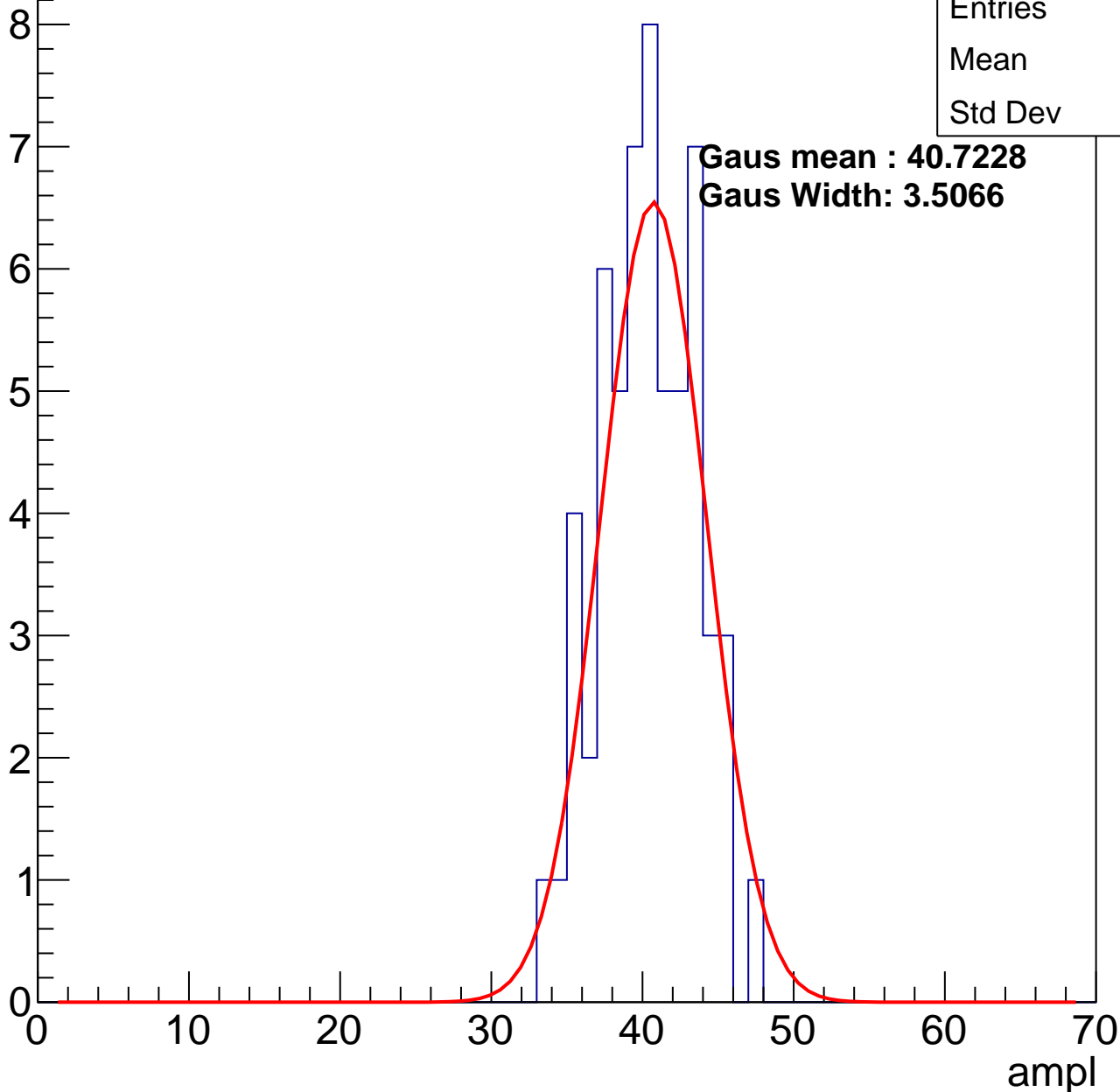
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	39.9
Std Dev	3.1

**Gaus mean : 40.7228**

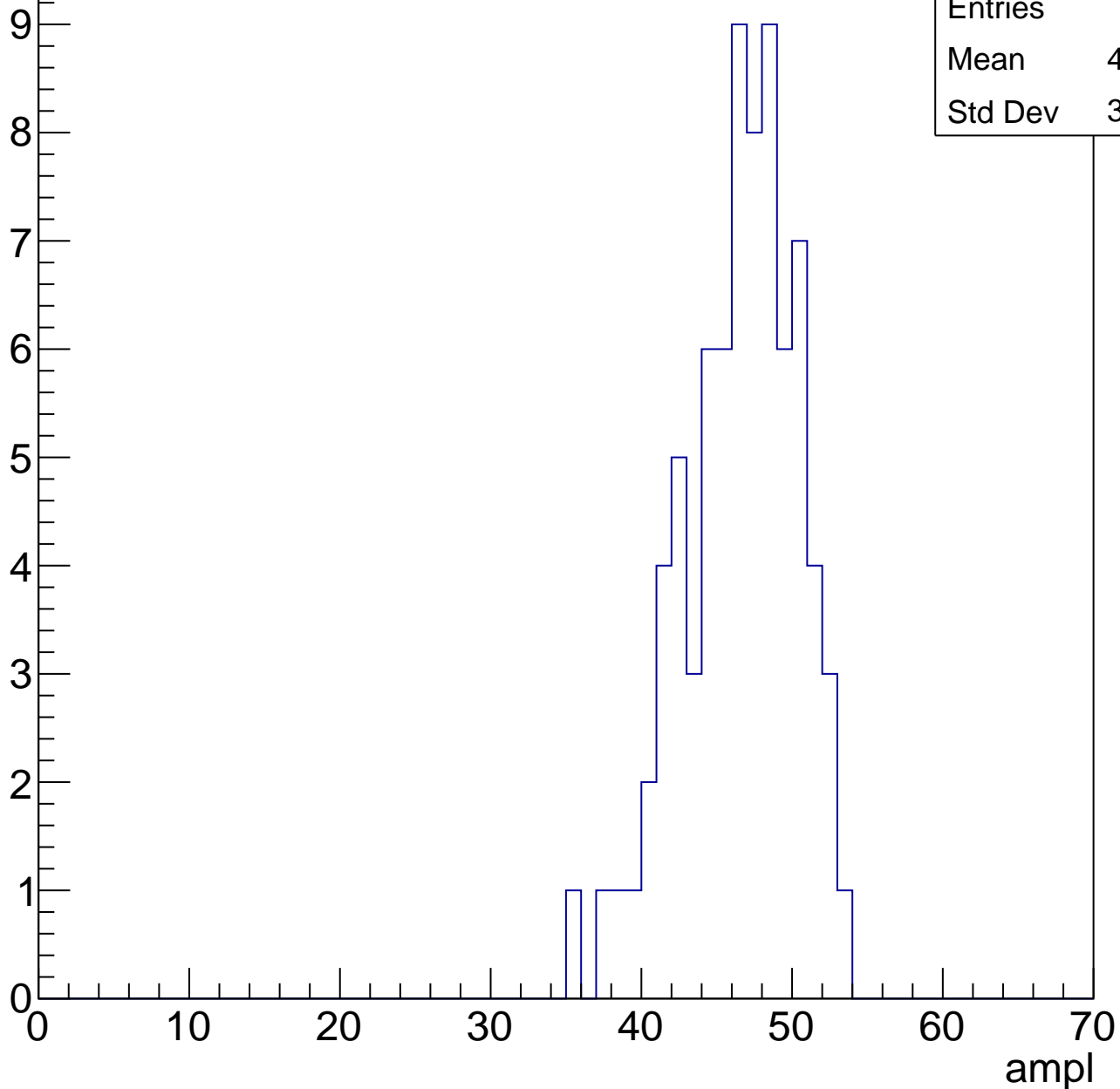
**Gaus Width: 3.5066**



# B1L000S, U1-ch102, adc3

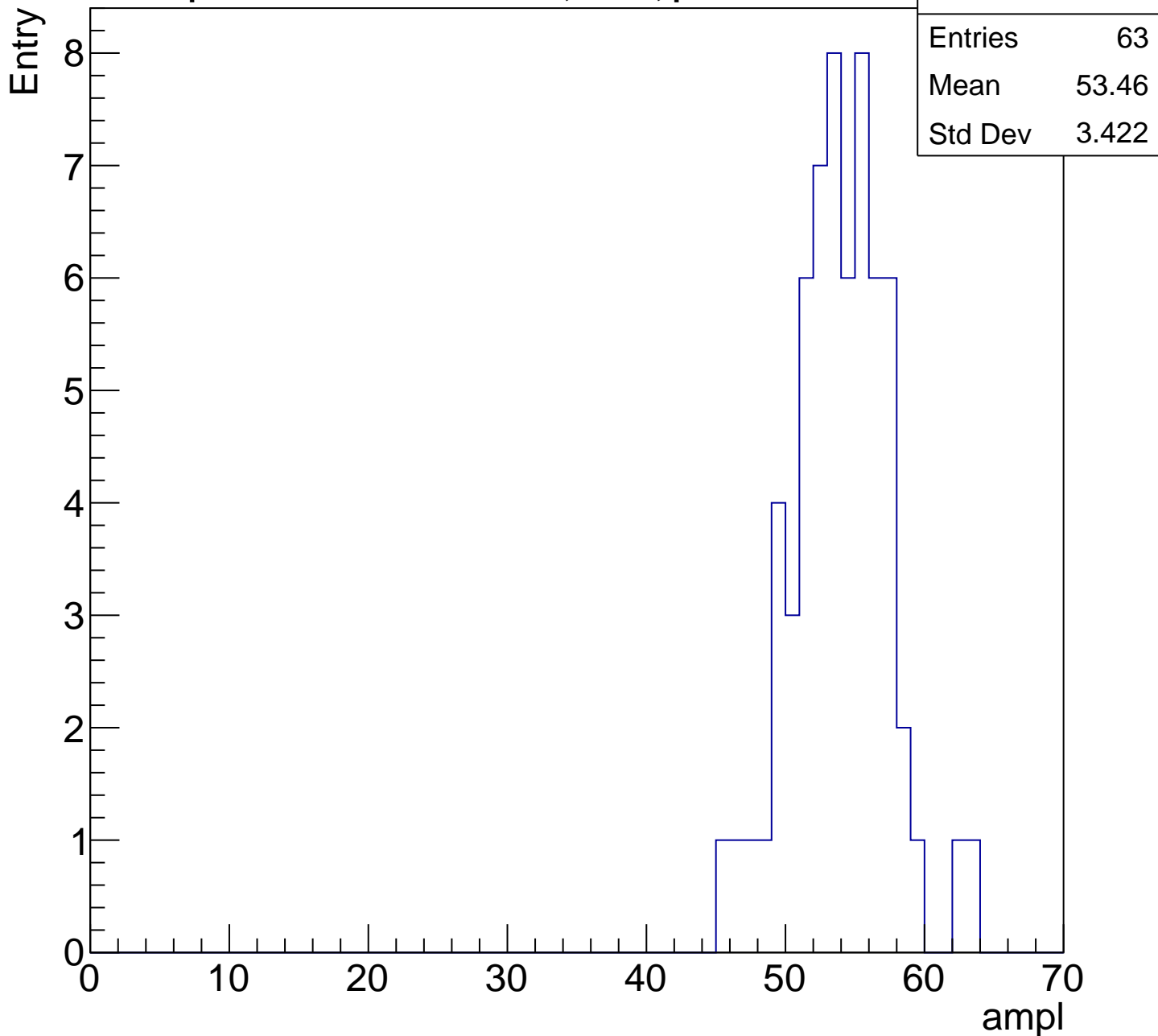
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

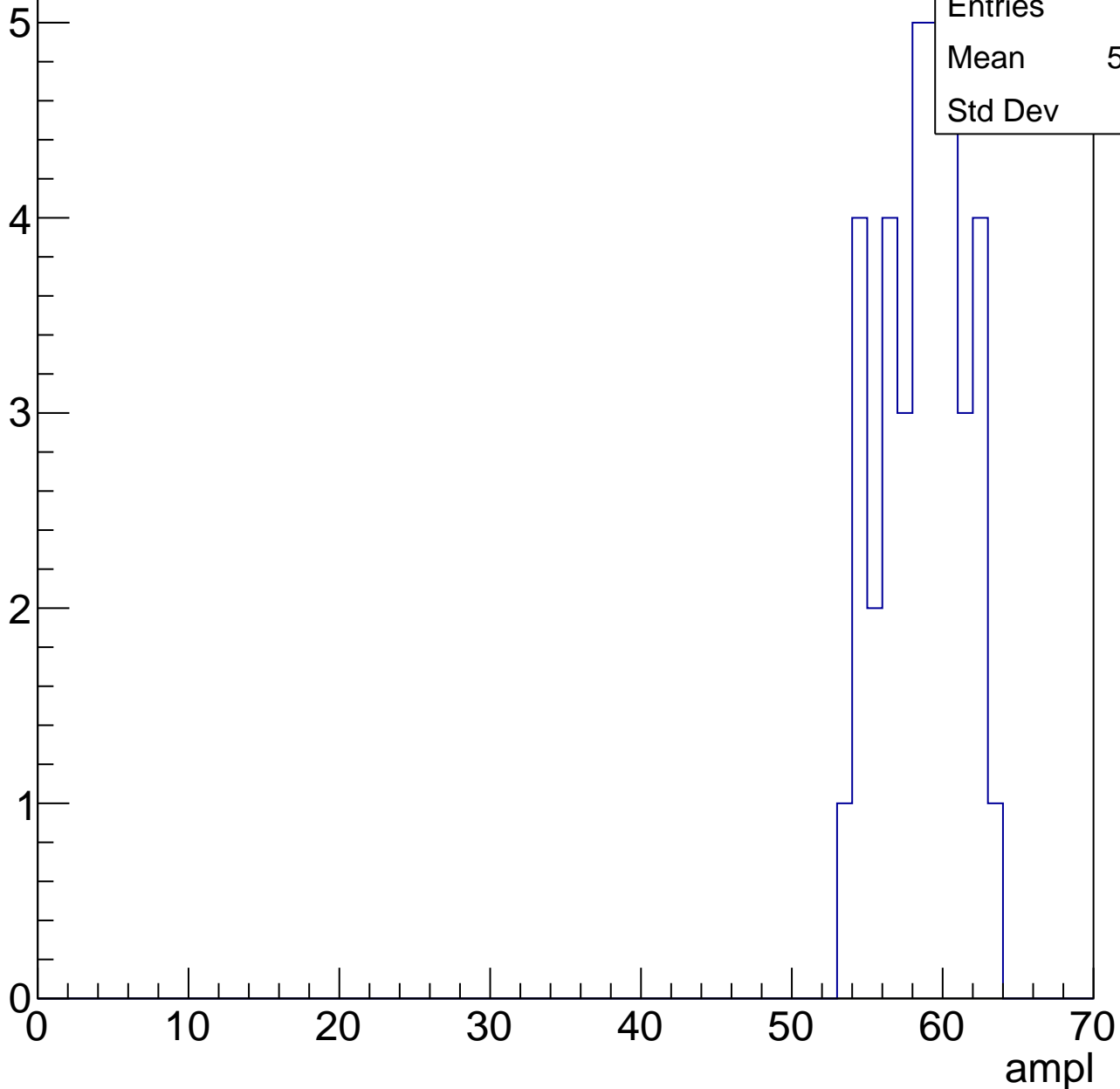


# B1L000S, U1-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	37
Mean	58.19
Std Dev	2.68

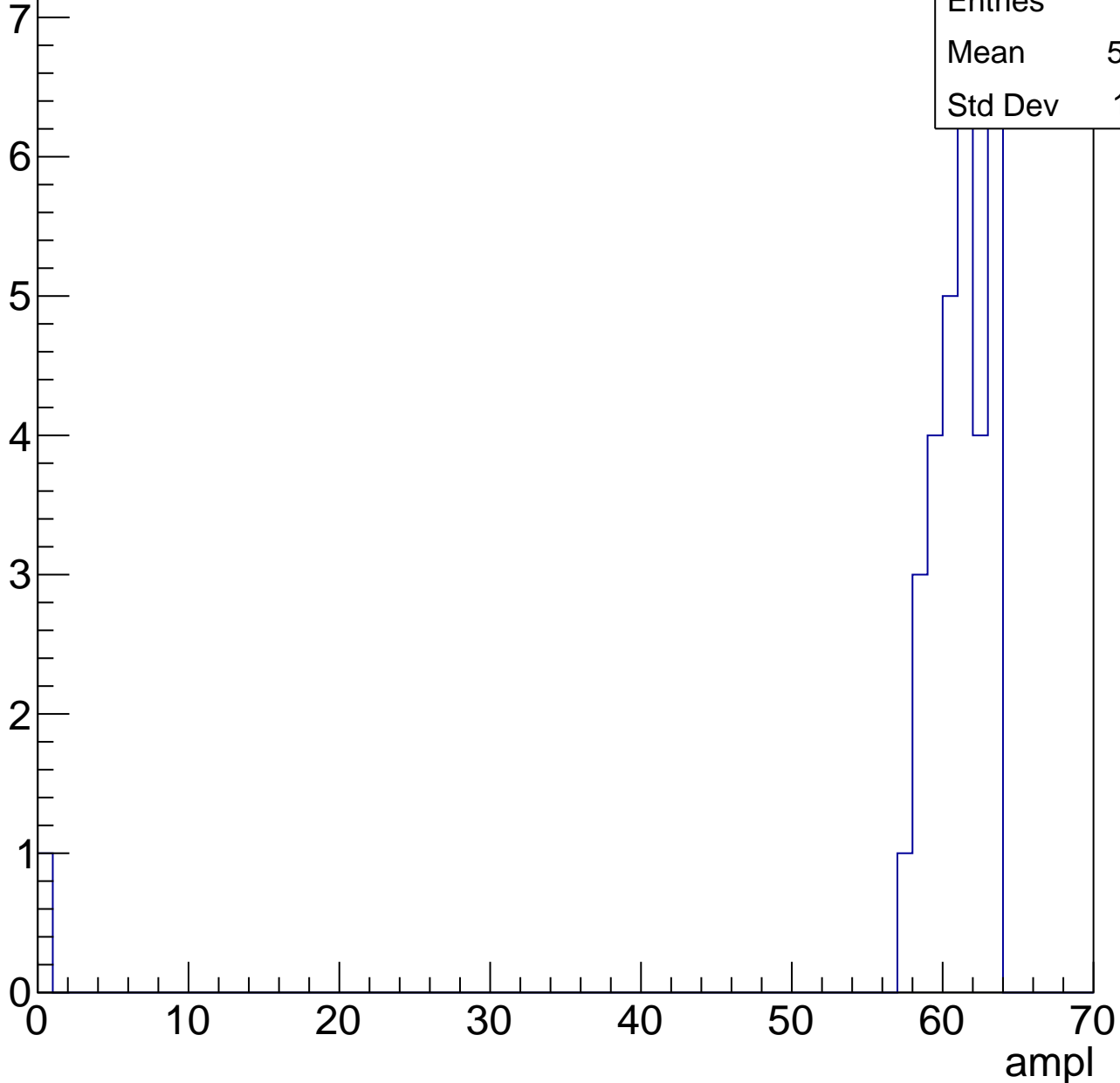


# B1L000S, U1-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	32
Mean	58.84
Std Dev	10.71





# B1L000S, U1-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch103, adc0

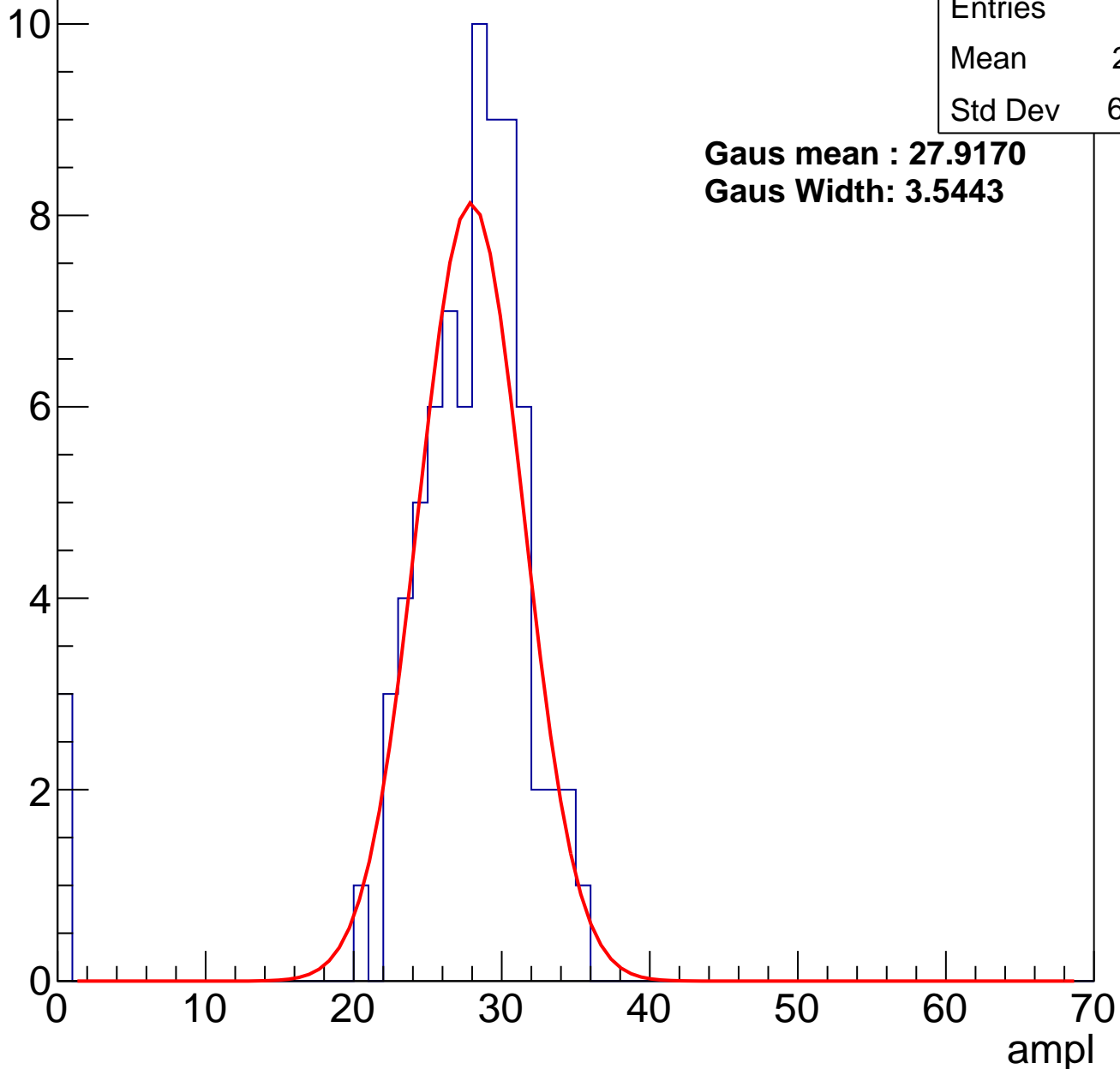
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	26.61
Std Dev	6.226

**Gaus mean : 27.9170**

**Gaus Width: 3.5443**

Entry



# B1L000S, U1-ch103, adc1

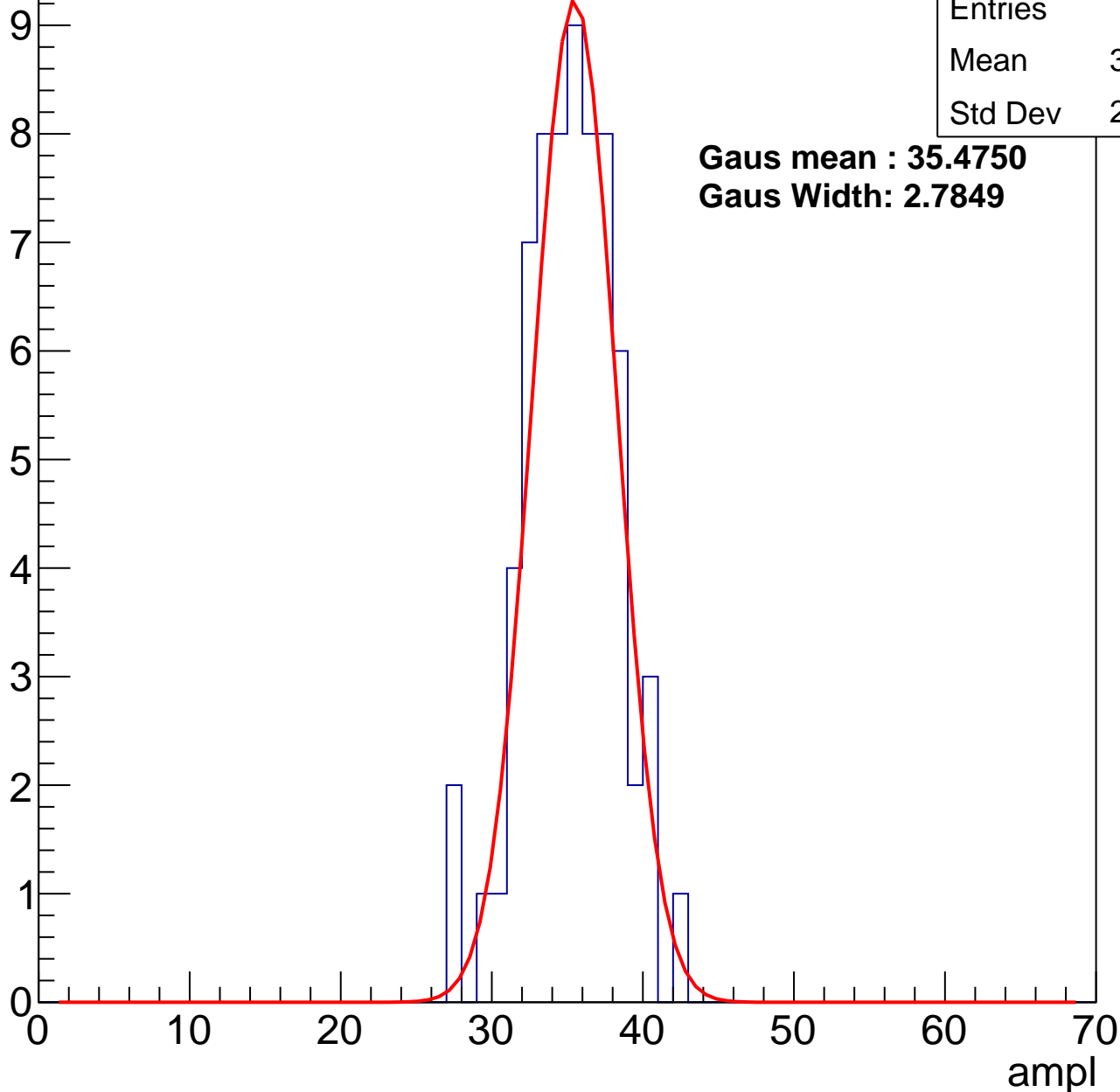
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	34.76
Std Dev	2.976

**Gaus mean : 35.4750**

**Gaus Width: 2.7849**



# B1L000S, U1-ch103, adc2

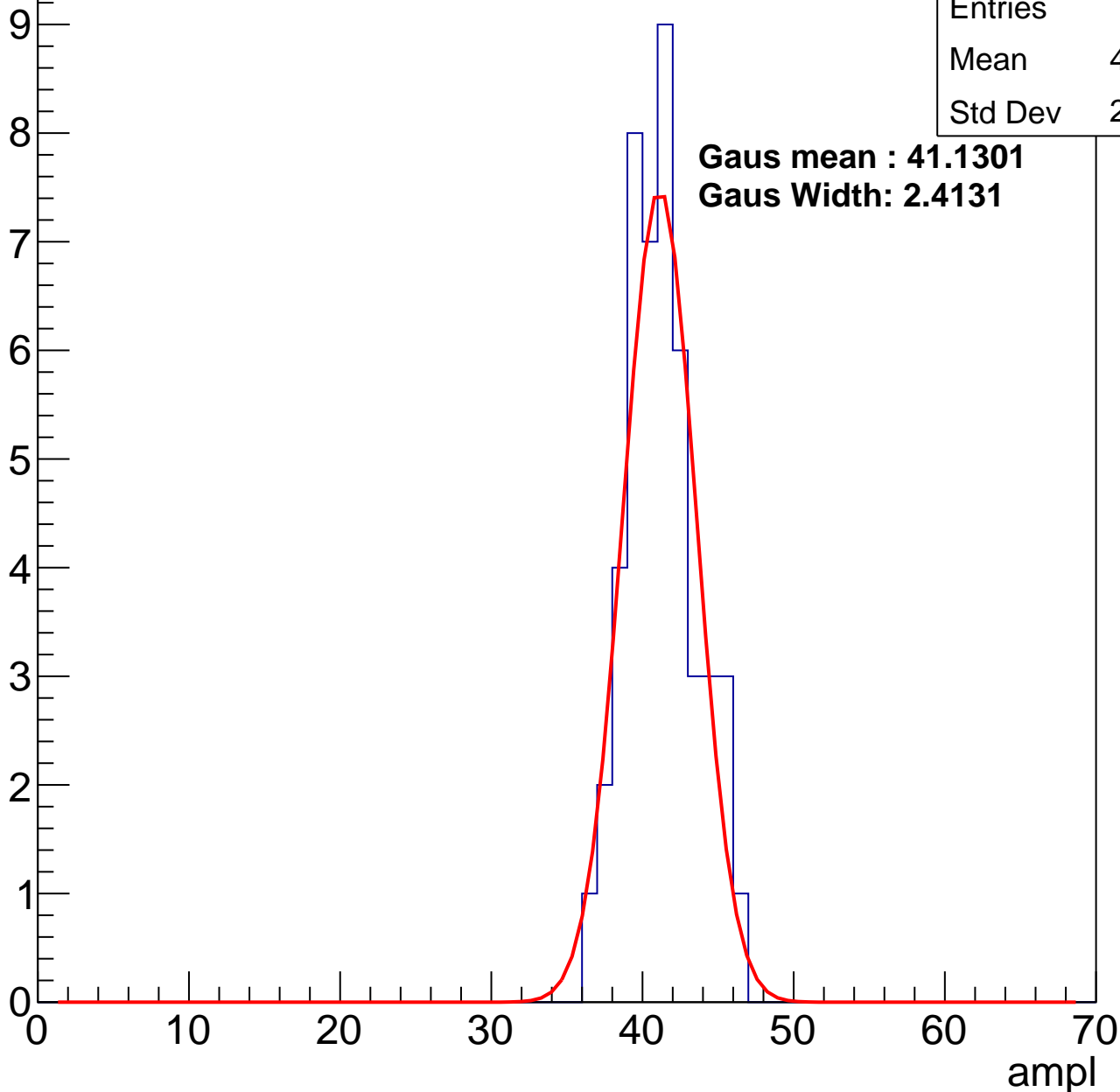
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	40.79
Std Dev	2.296

**Gaus mean : 41.1301**

**Gaus Width: 2.4131**



# B1L000S, U1-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

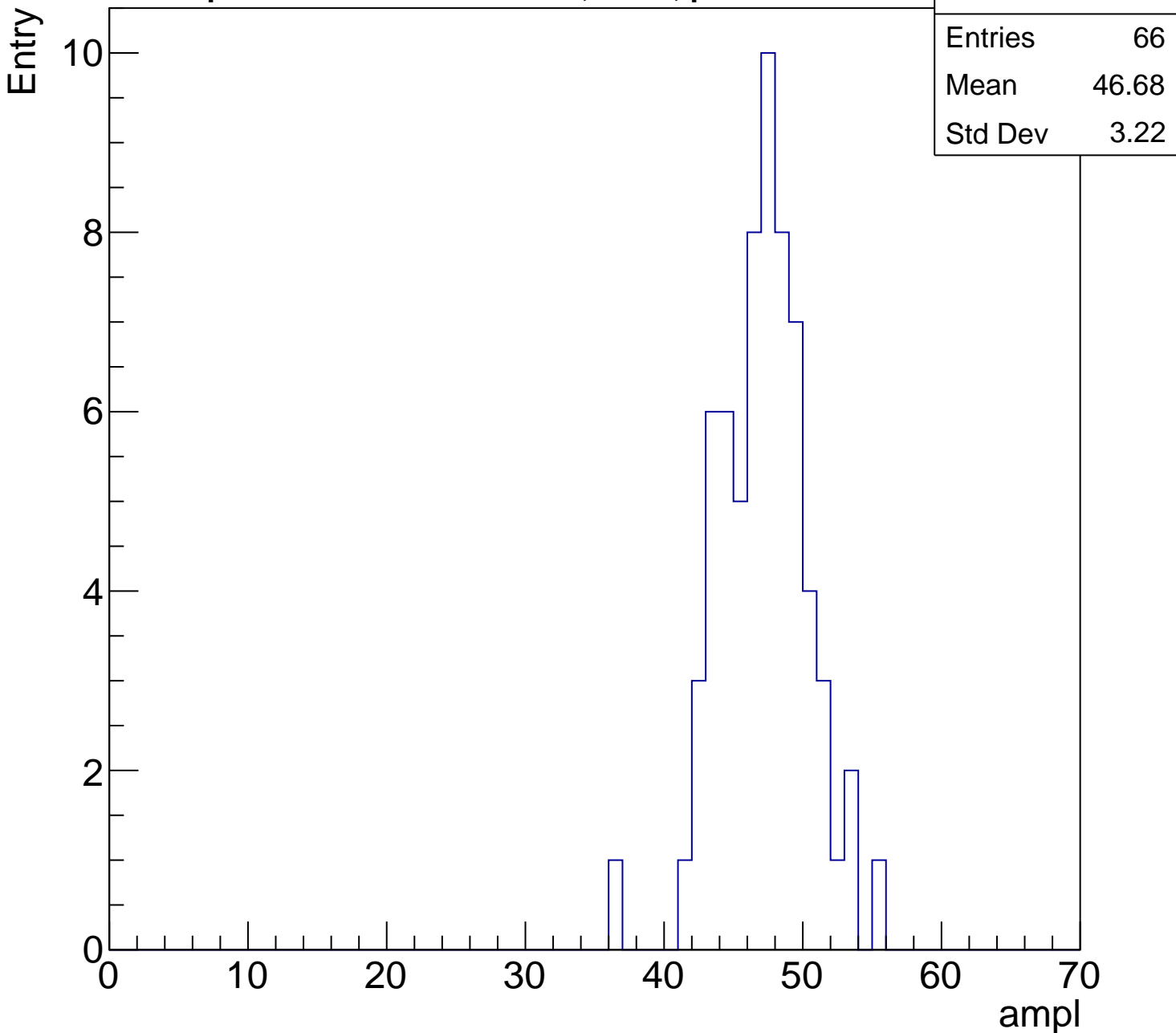
Entries	66
Mean	46.68
Std Dev	3.22

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

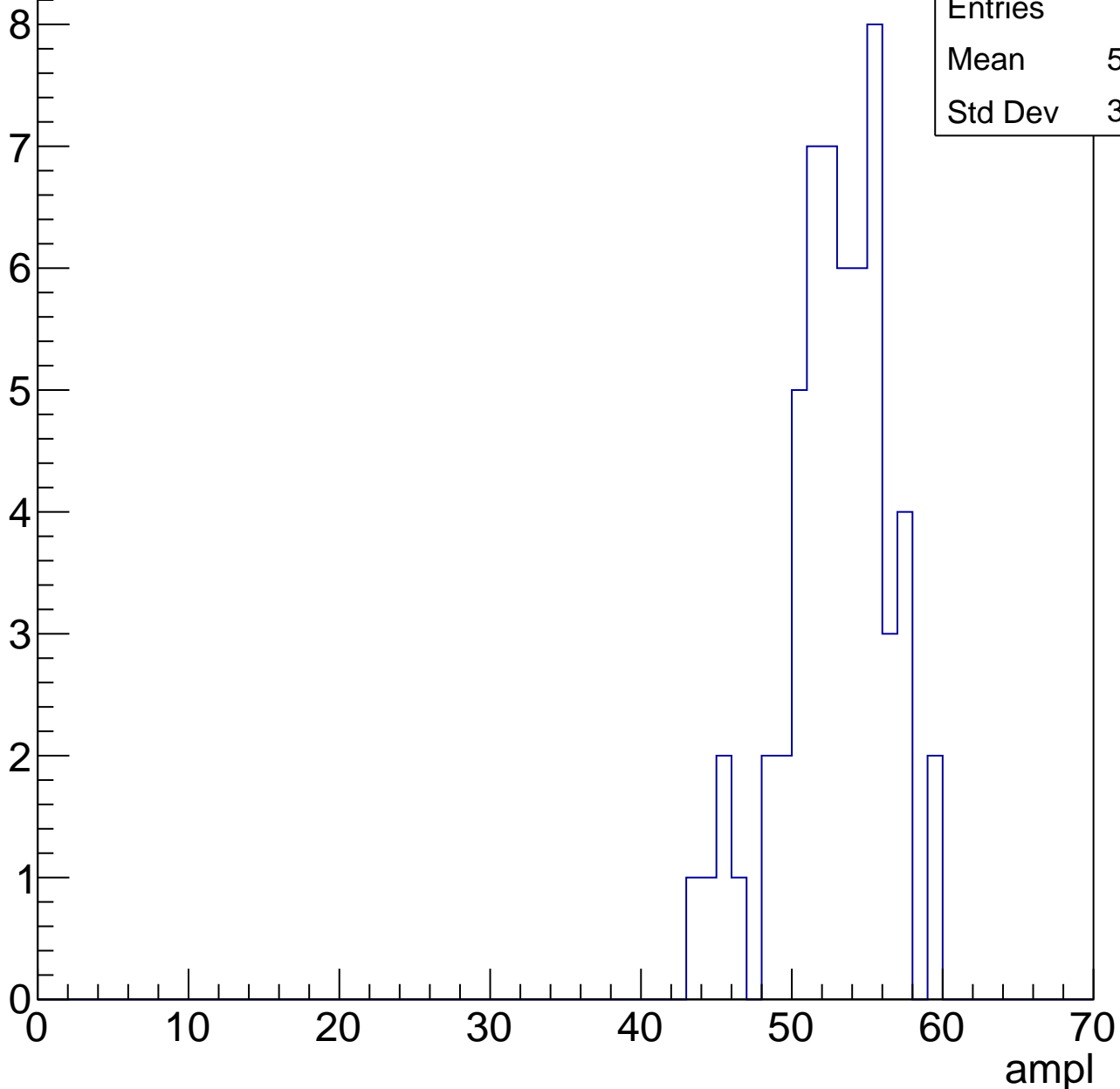


# B1L000S, U1-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	52.35
Std Dev	3.497

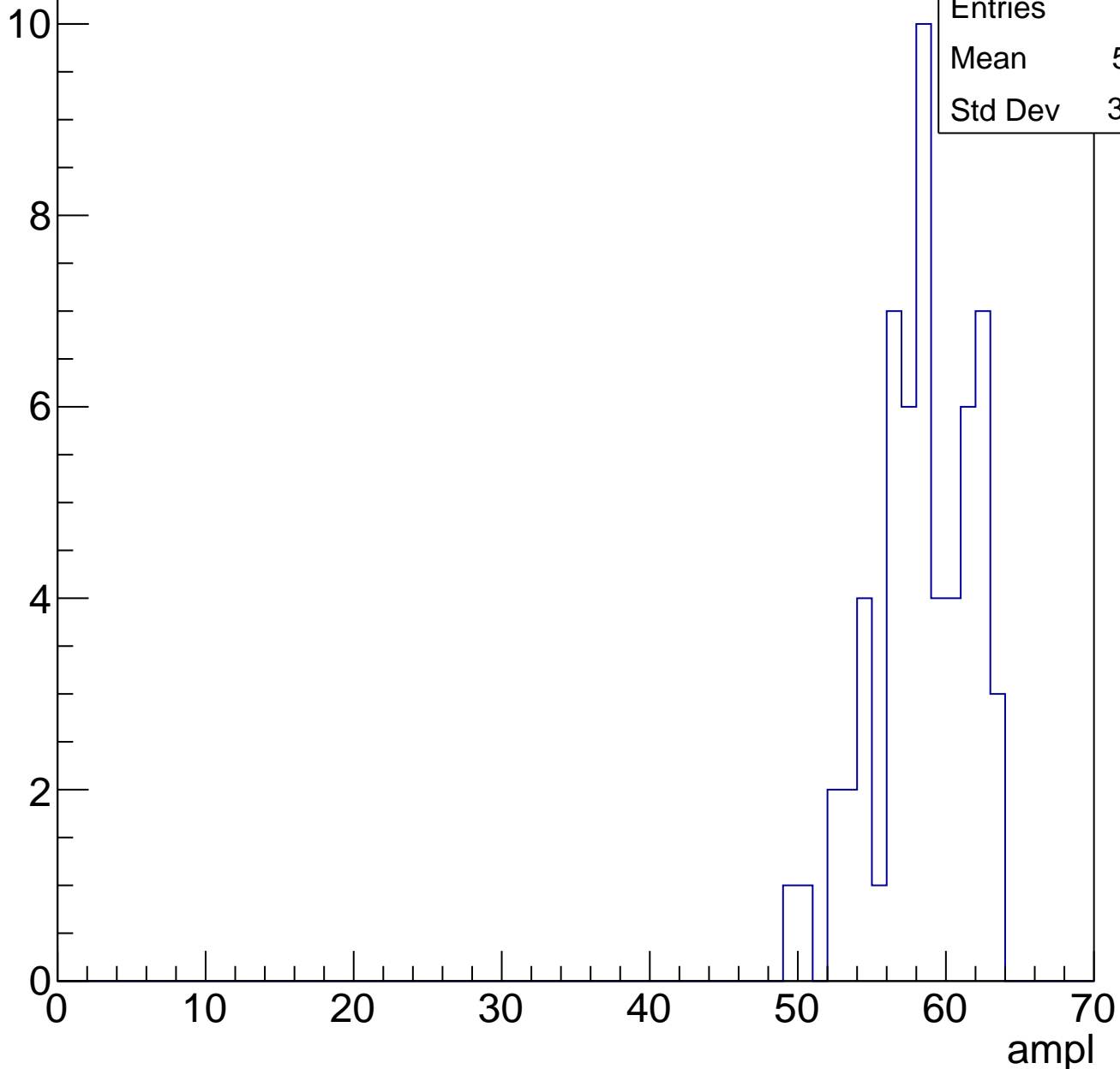


# B1L000S, U1-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	58
Mean	57.91
Std Dev	3.308

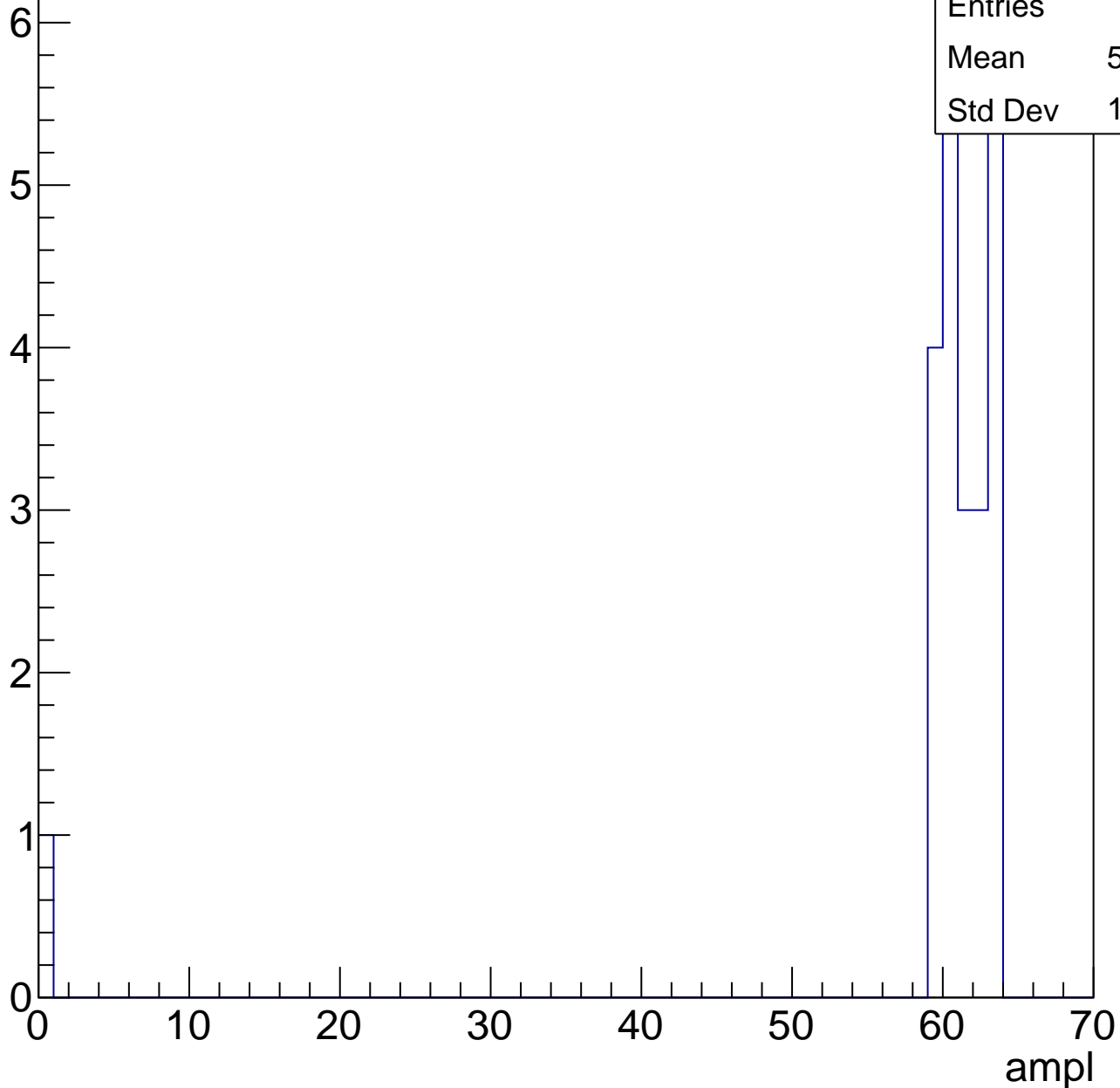
Entry



# B1L000S, U1-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

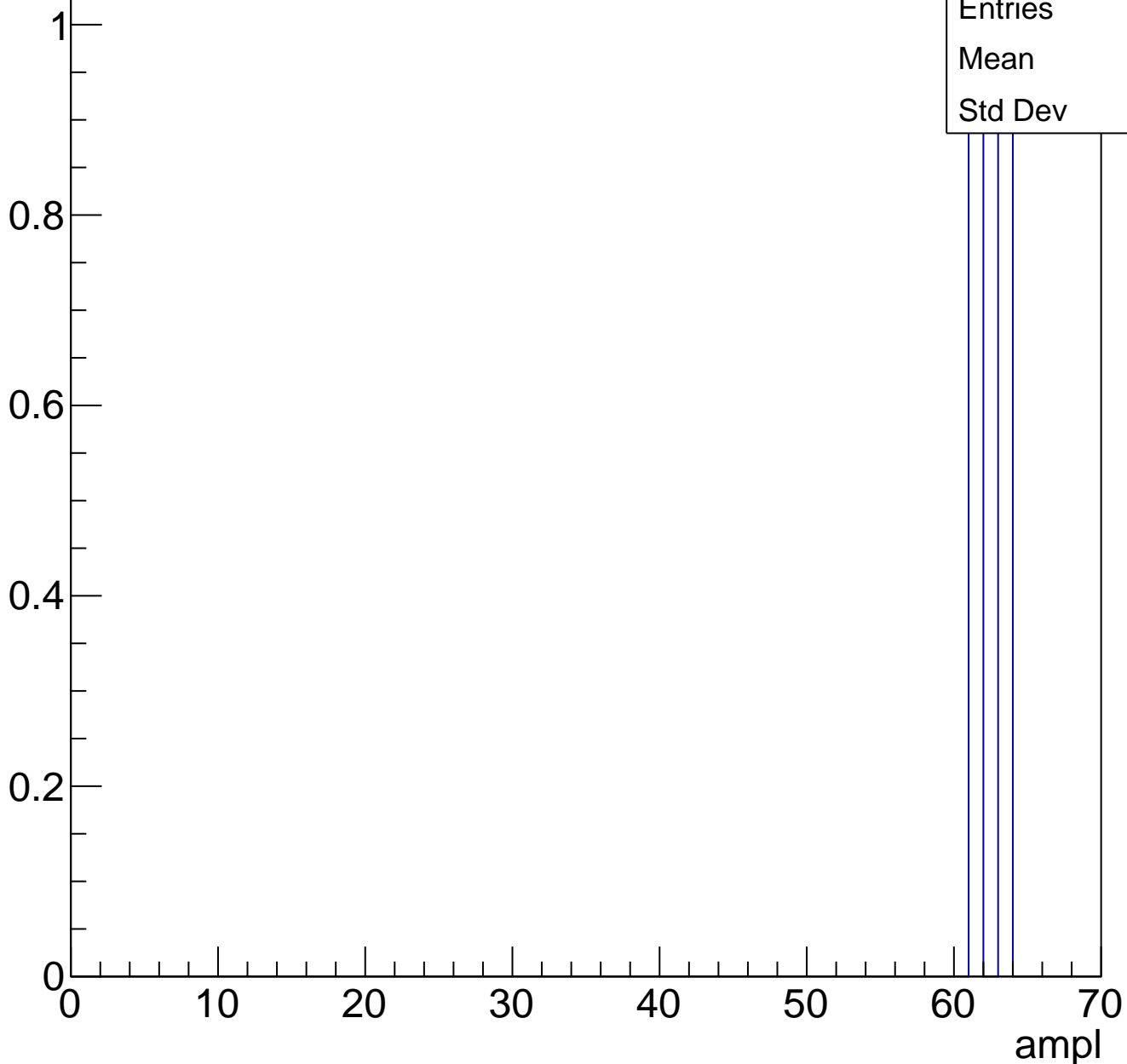




# B1L000S, U1-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch104, adc0

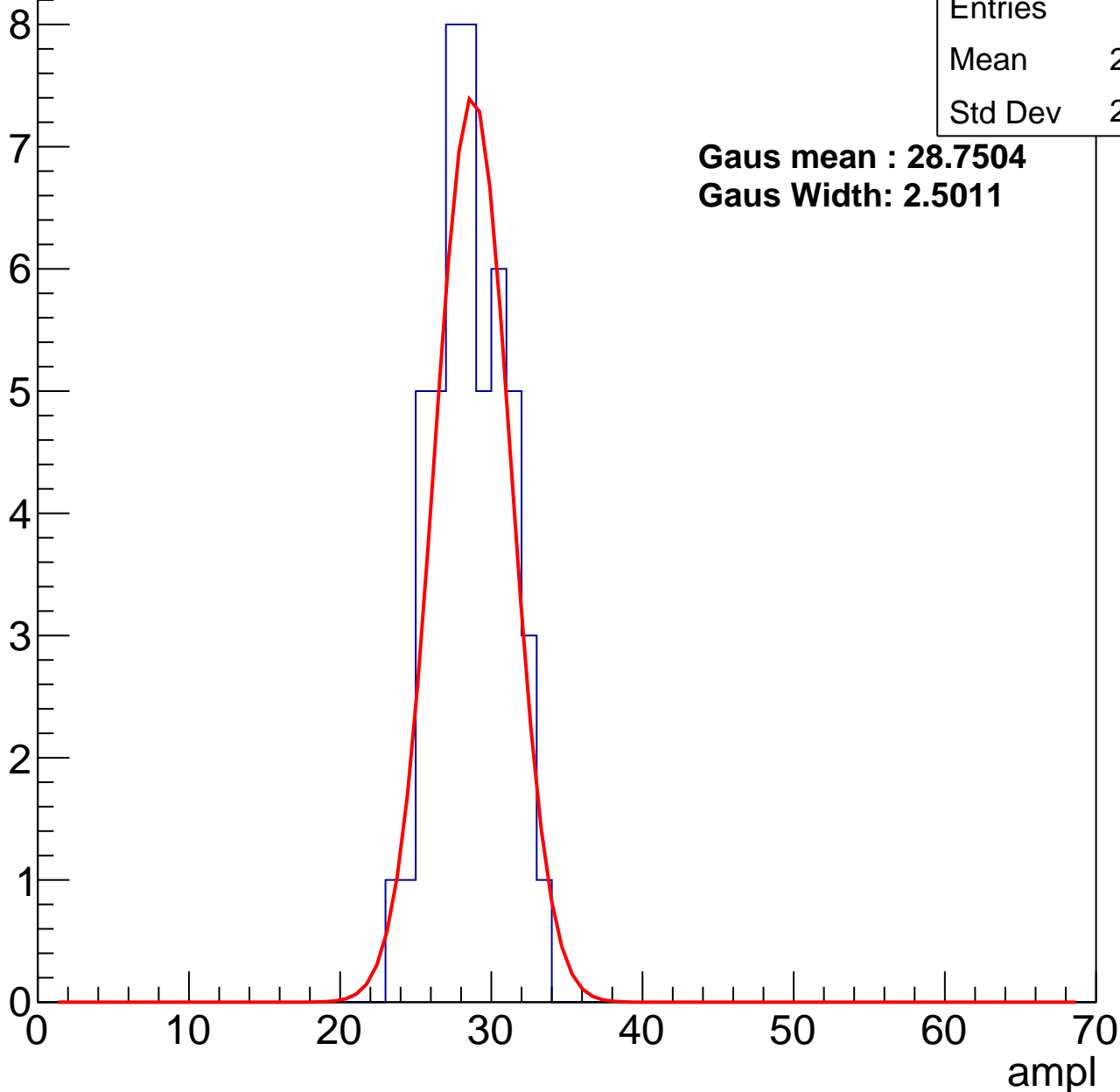
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	28.15
Std Dev	2.327

**Gaus mean : 28.7504**

**Gaus Width: 2.5011**



# B1L000S, U1-ch104, adc1

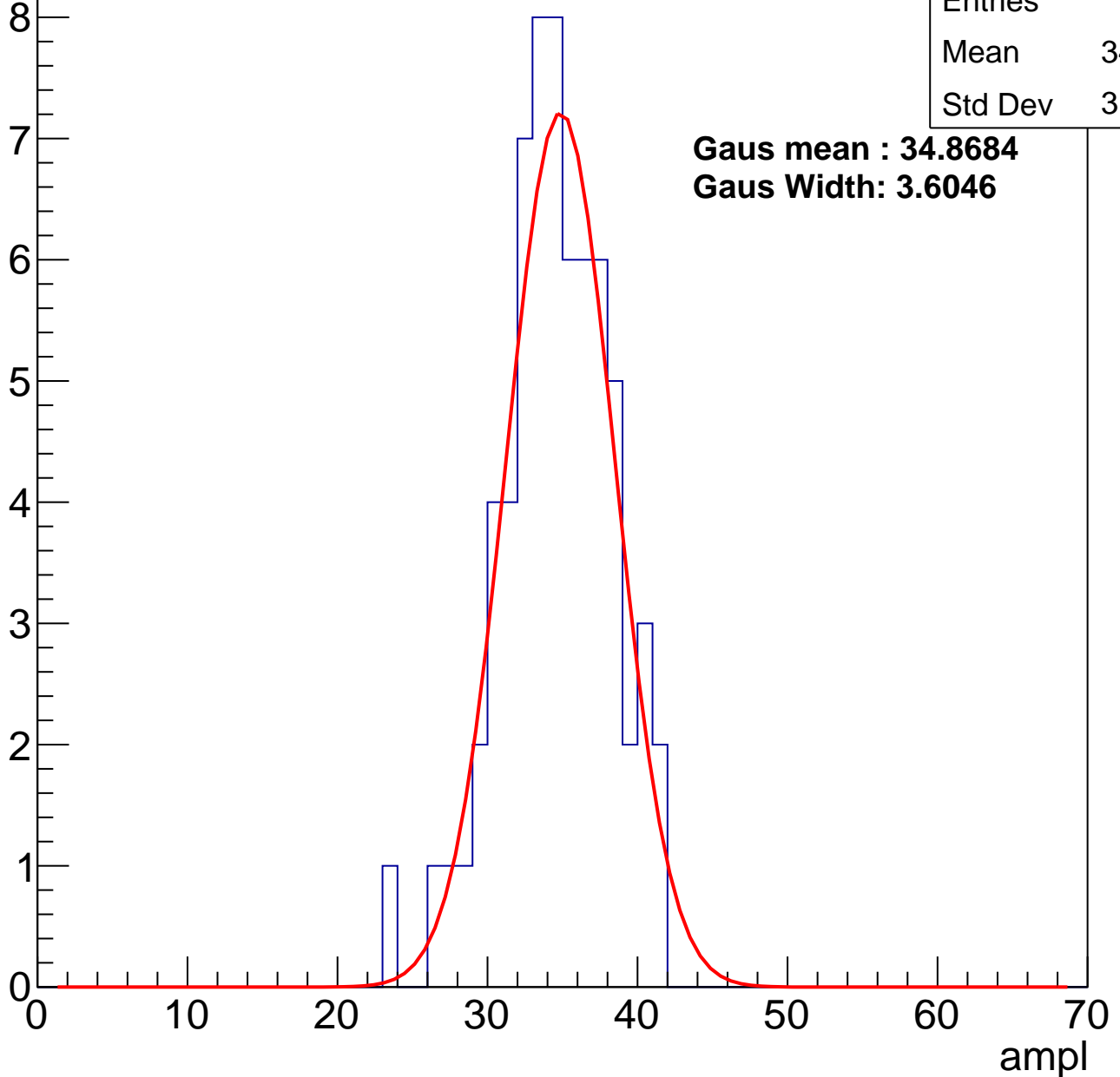
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.09
Std Dev	3.615

**Gaus mean : 34.8684**

**Gaus Width: 3.6046**



# B1L000S, U1-ch104, adc2

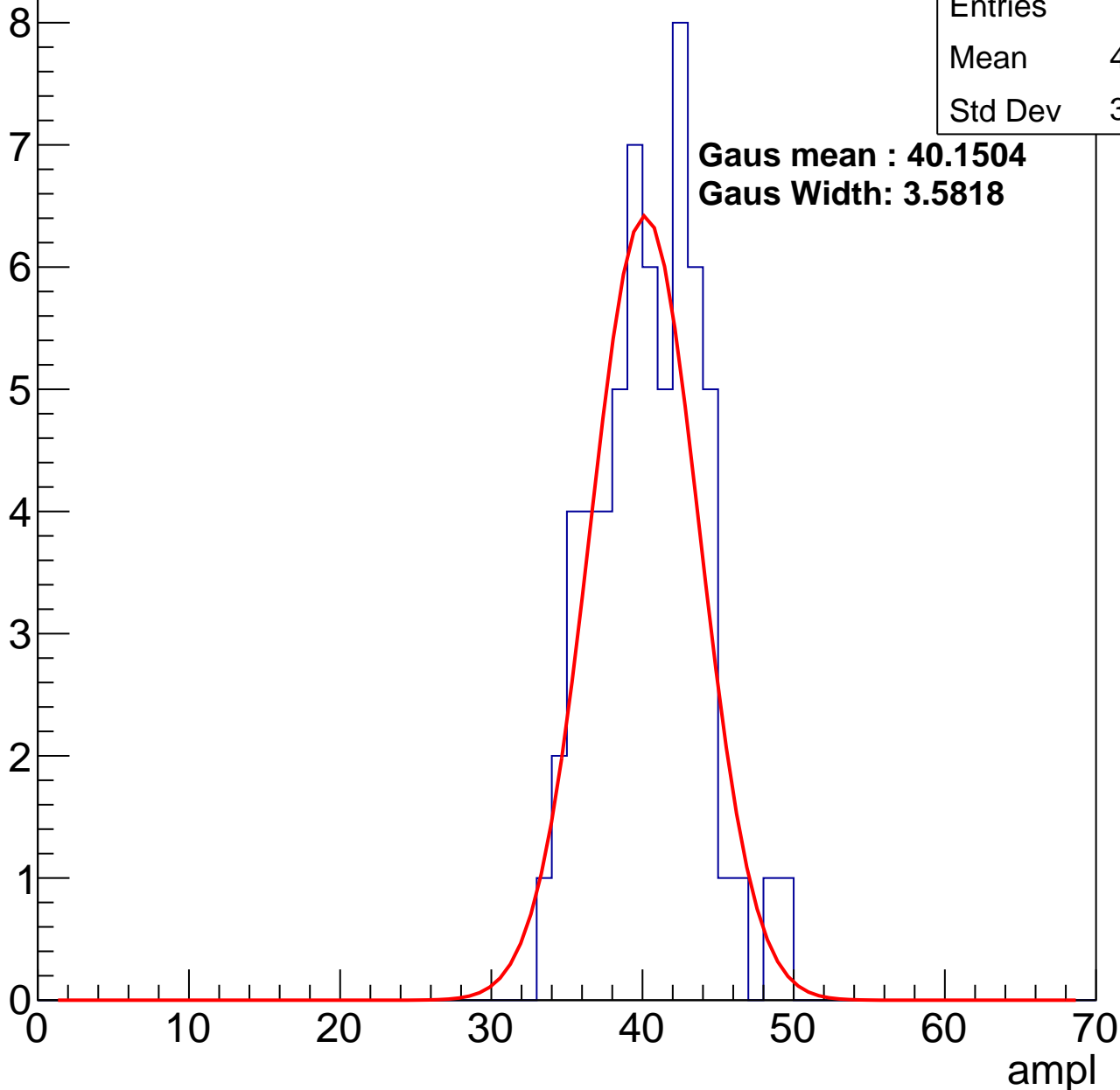
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	40.05
Std Dev	3.447

**Gaus mean : 40.1504**

**Gaus Width: 3.5818**

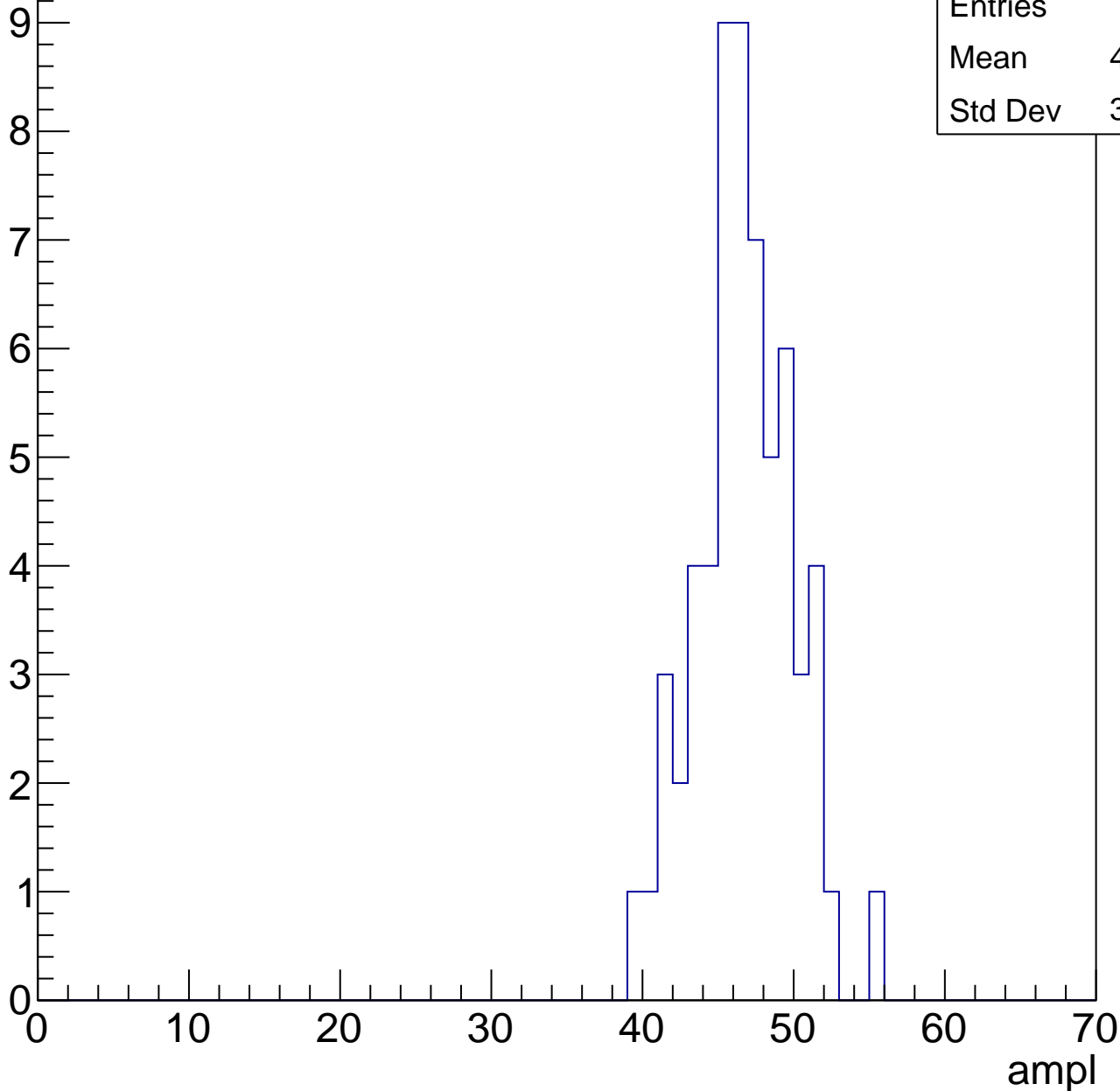


# B1L000S, U1-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	46.28
Std Dev	3.147

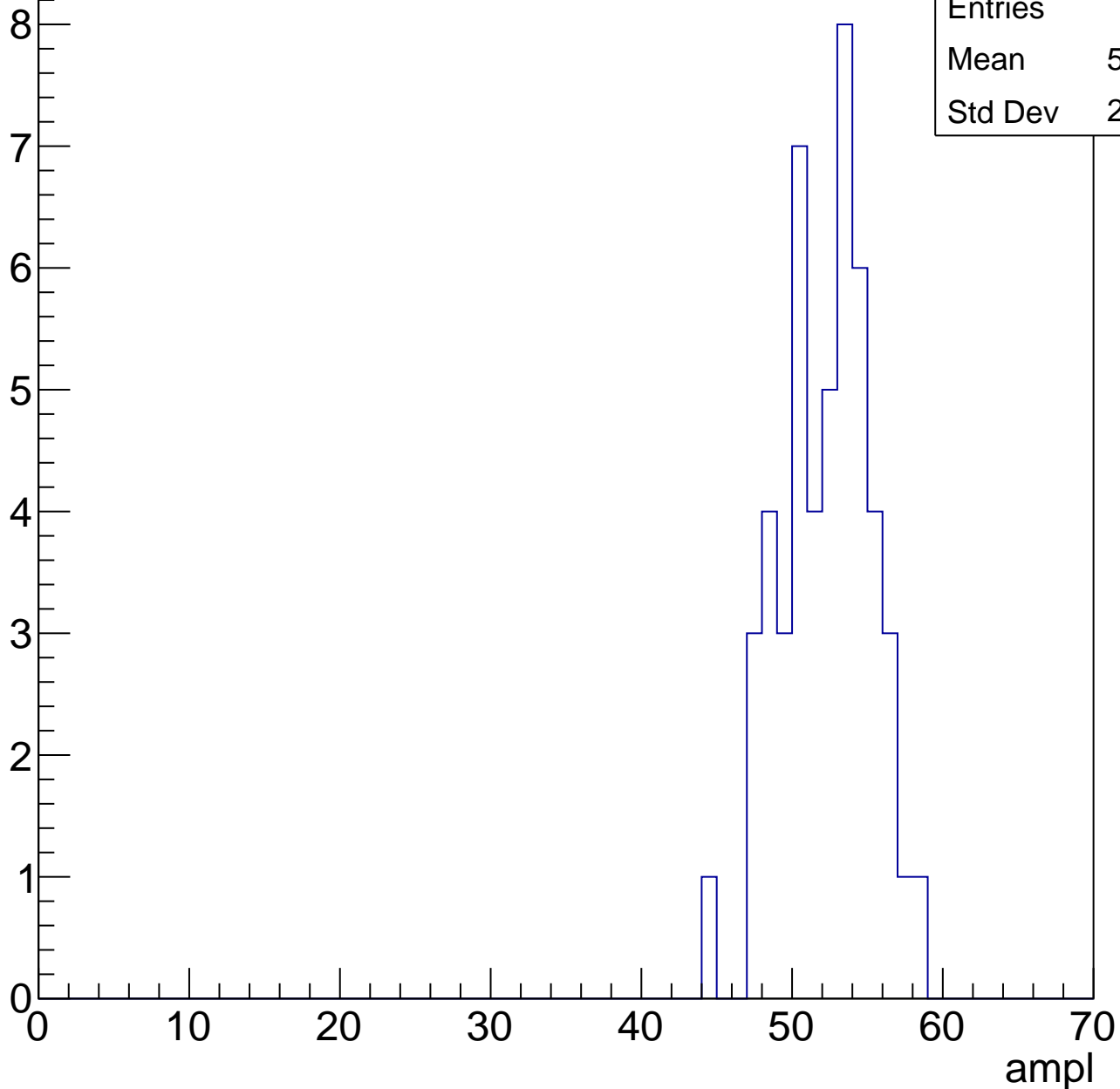


# B1L000S, U1-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

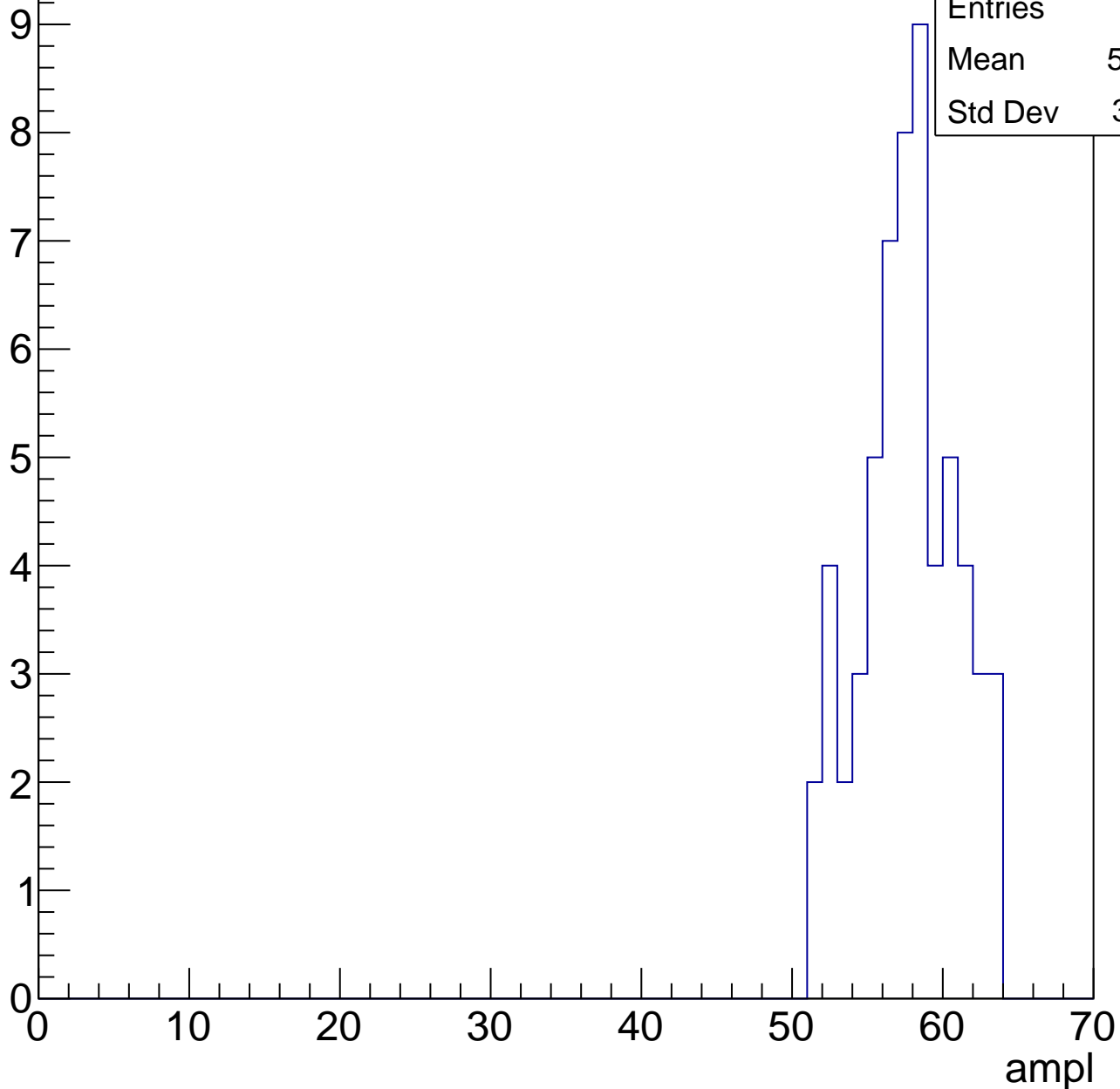
Entries	50
Mean	51.78
Std Dev	2.955



# B1L000S, U1-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

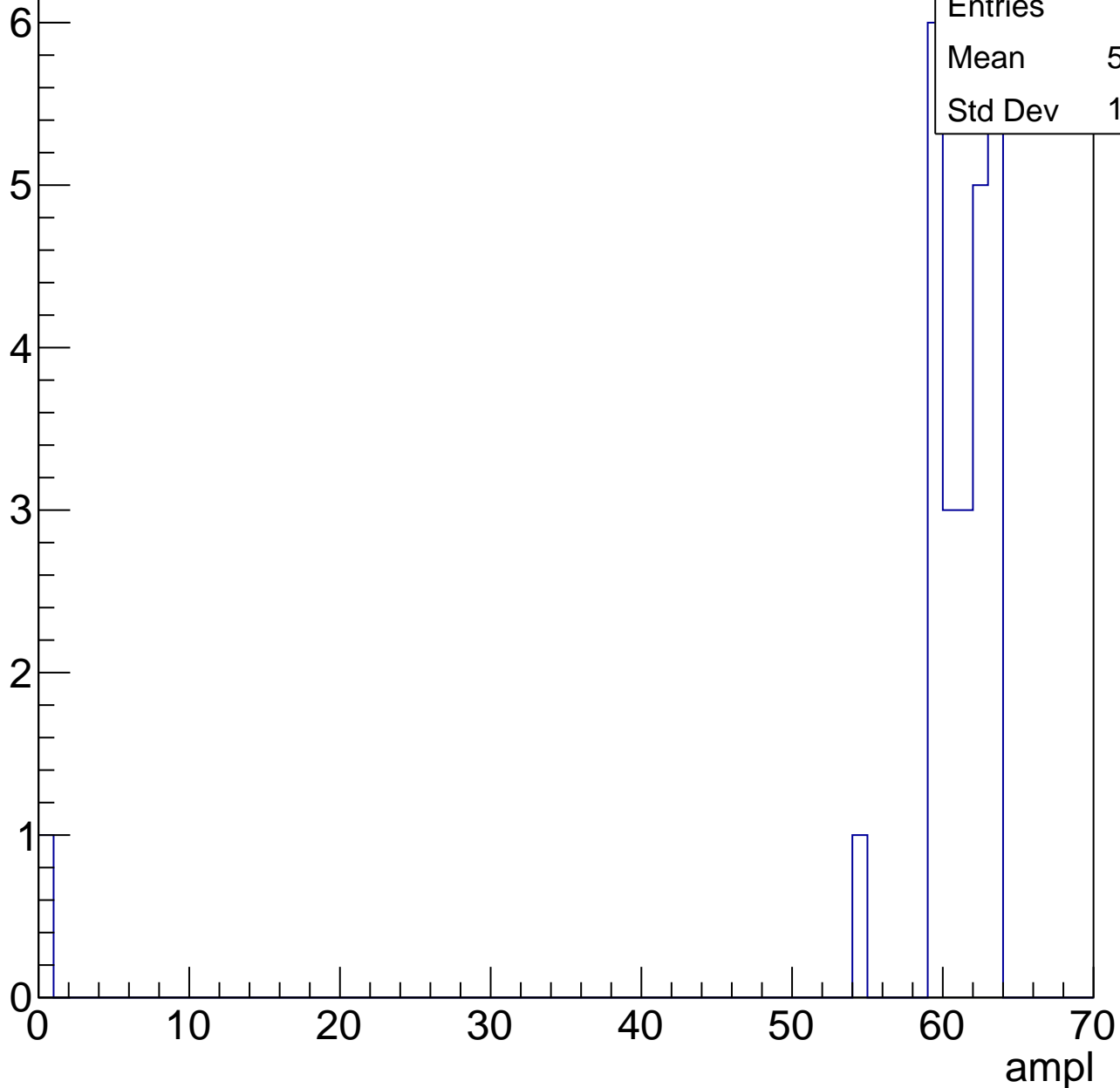
Entry



# B1L000S, U1-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

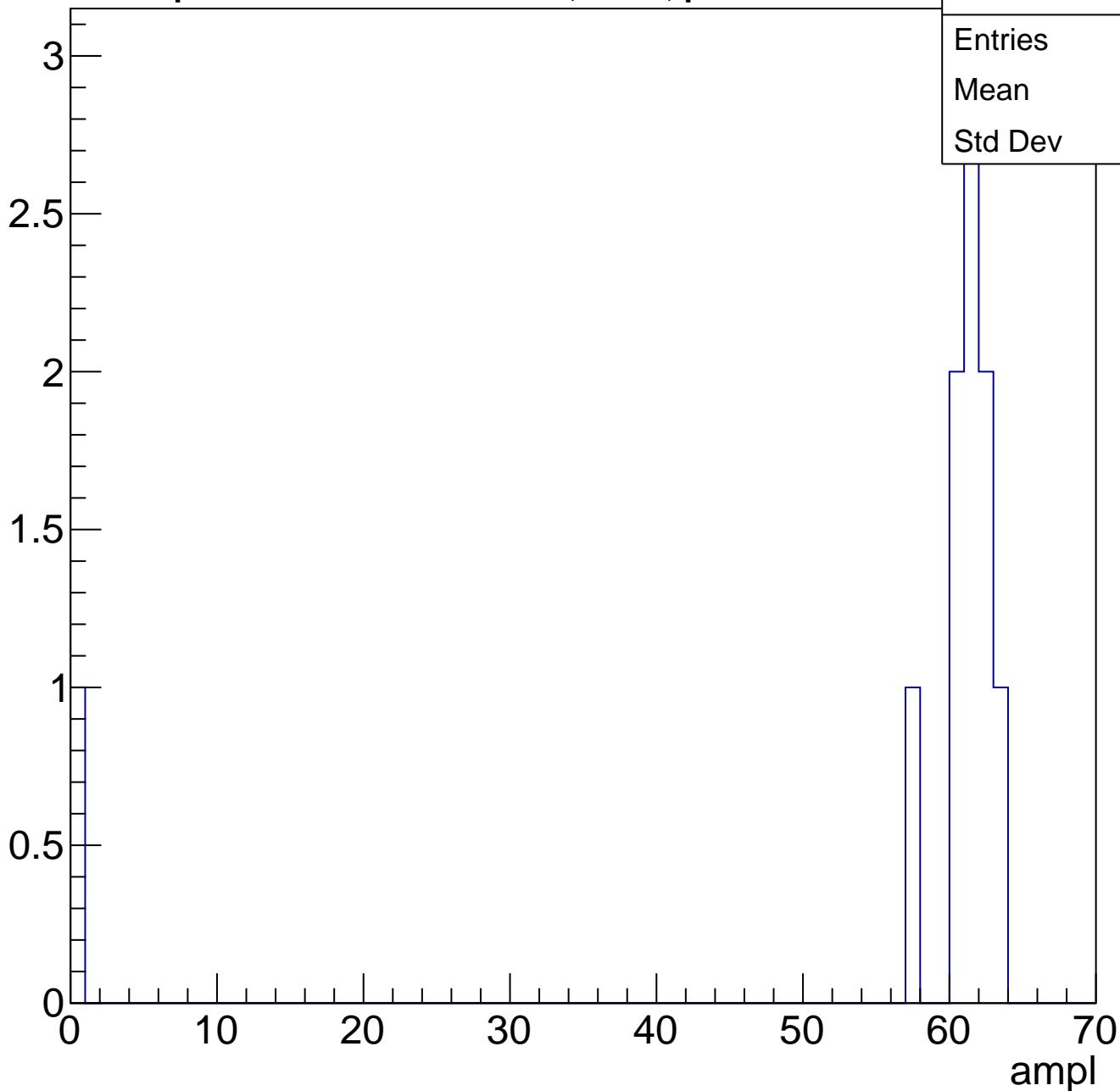




# B1L000S, U1-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch105, adc0

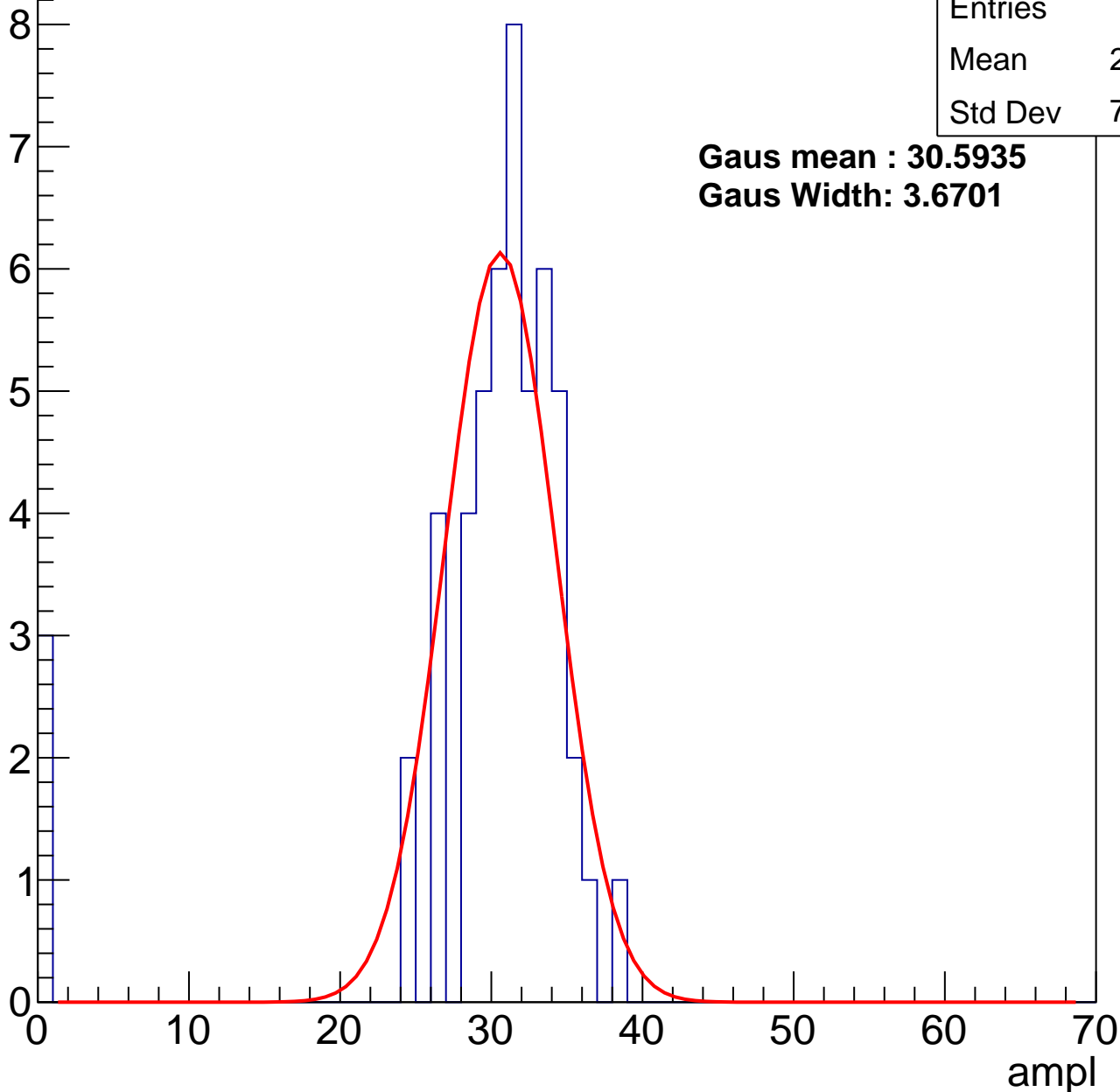
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	29.02
Std Dev	7.745

**Gaus mean : 30.5935**

**Gaus Width: 3.6701**



# B1L000S, U1-ch105, adc1

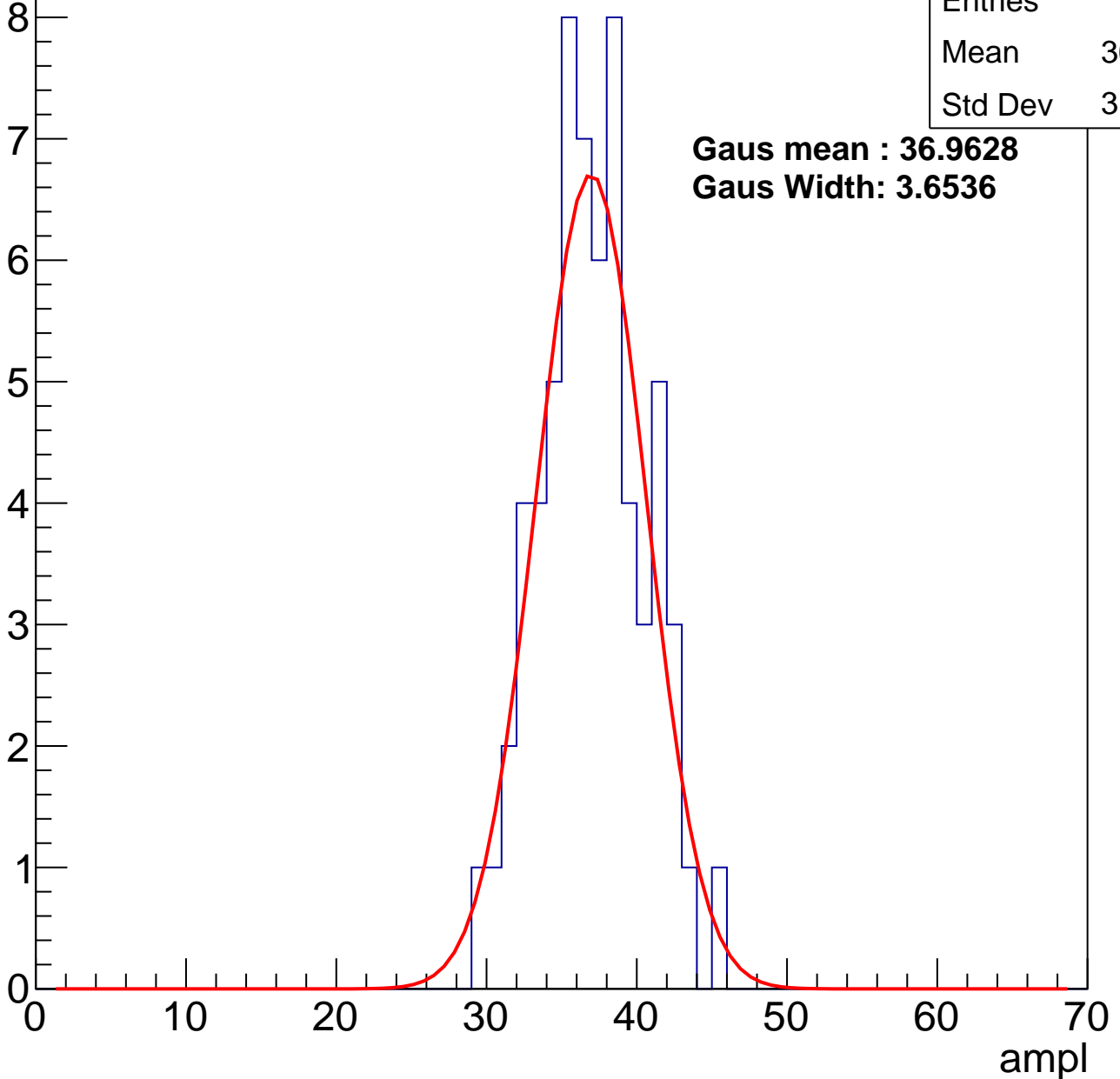
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	36.57
Std Dev	3.398

**Gaus mean : 36.9628**

**Gaus Width: 3.6536**

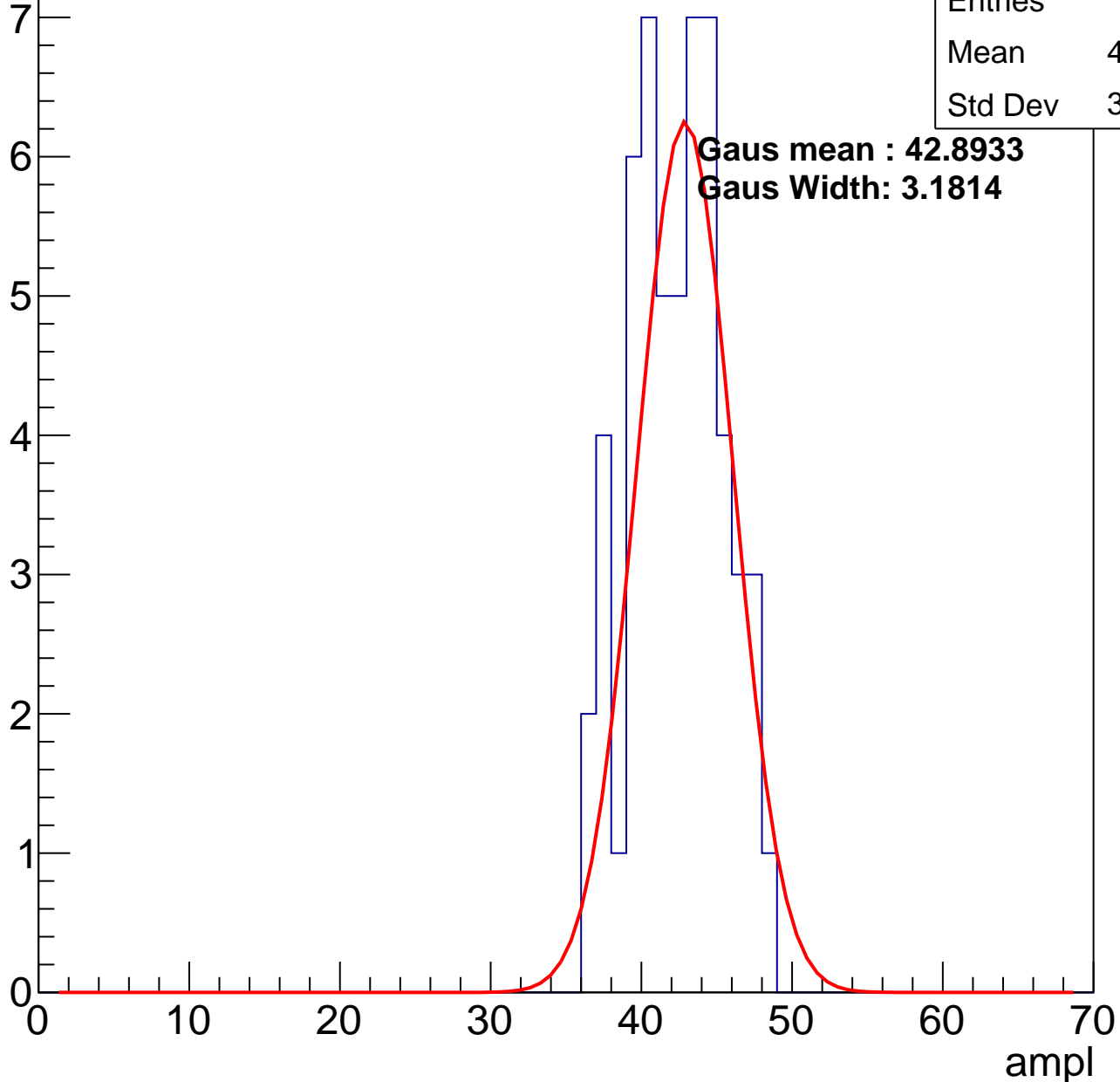


# B1L000S, U1-ch105, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

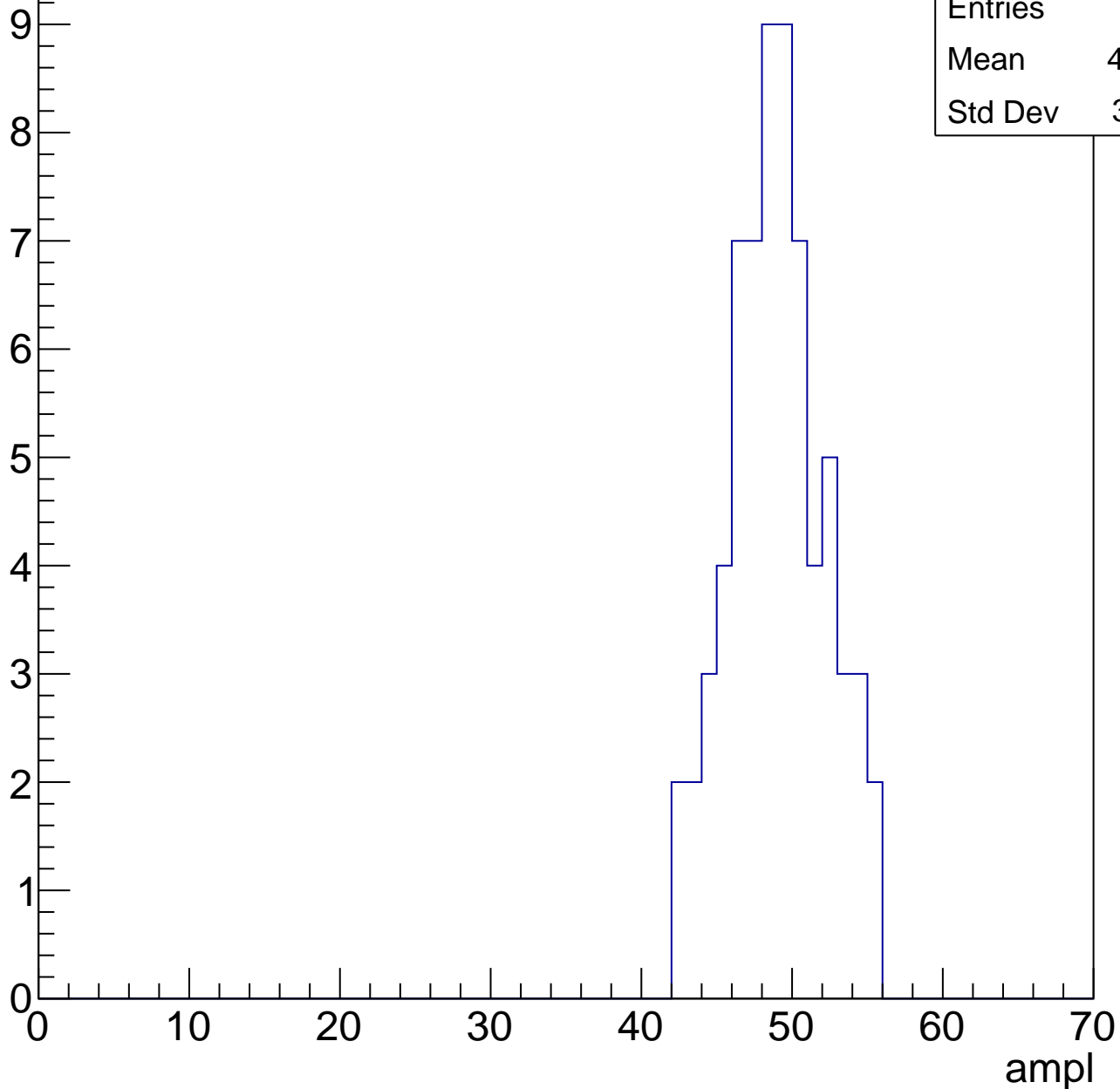
Entries	55
Mean	41.87
Std Dev	3.027



# B1L000S, U1-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

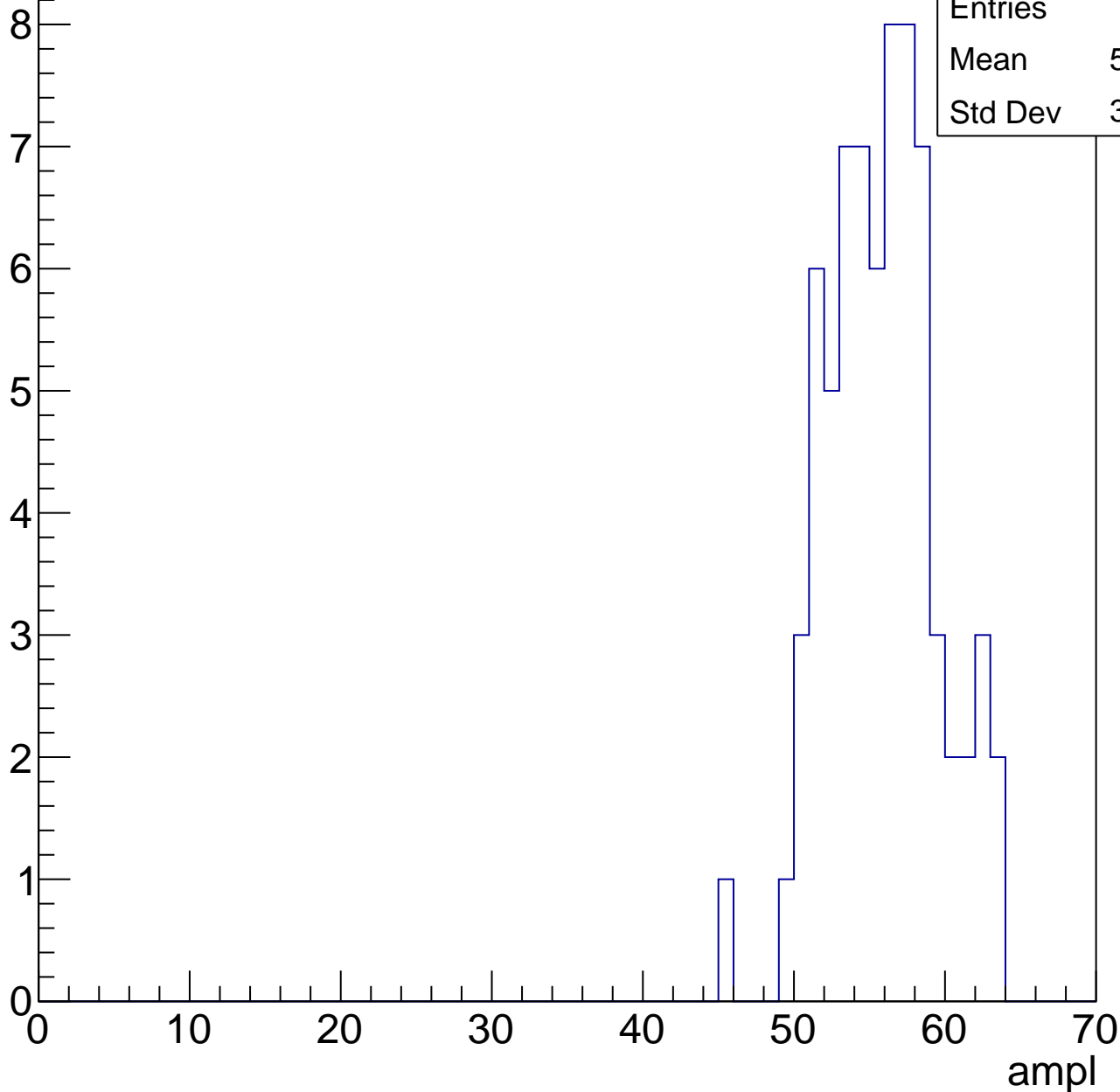


# B1L000S, U1-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

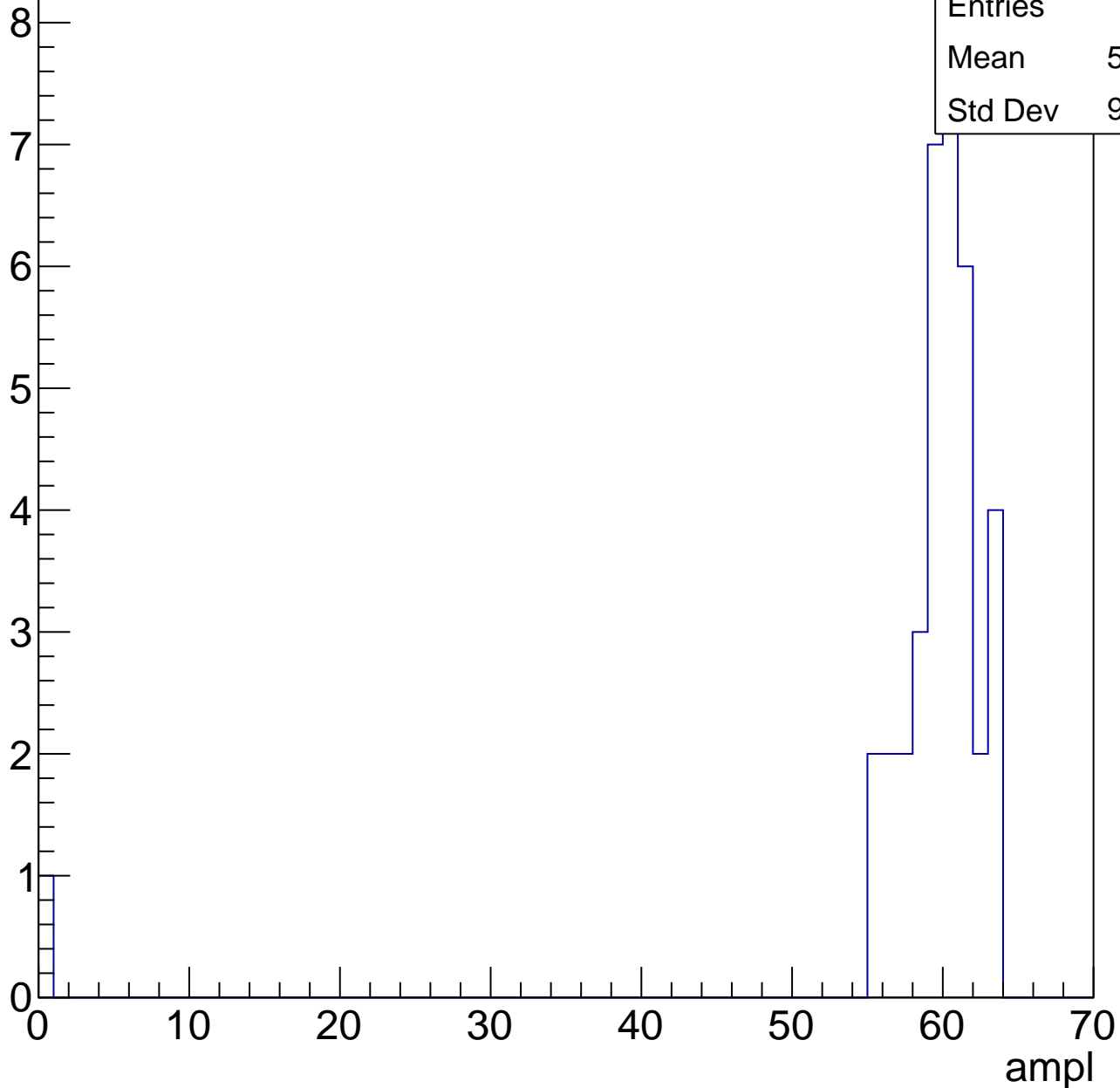
Entries	71
Mean	55.35
Std Dev	3.612



# B1L000S, U1-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



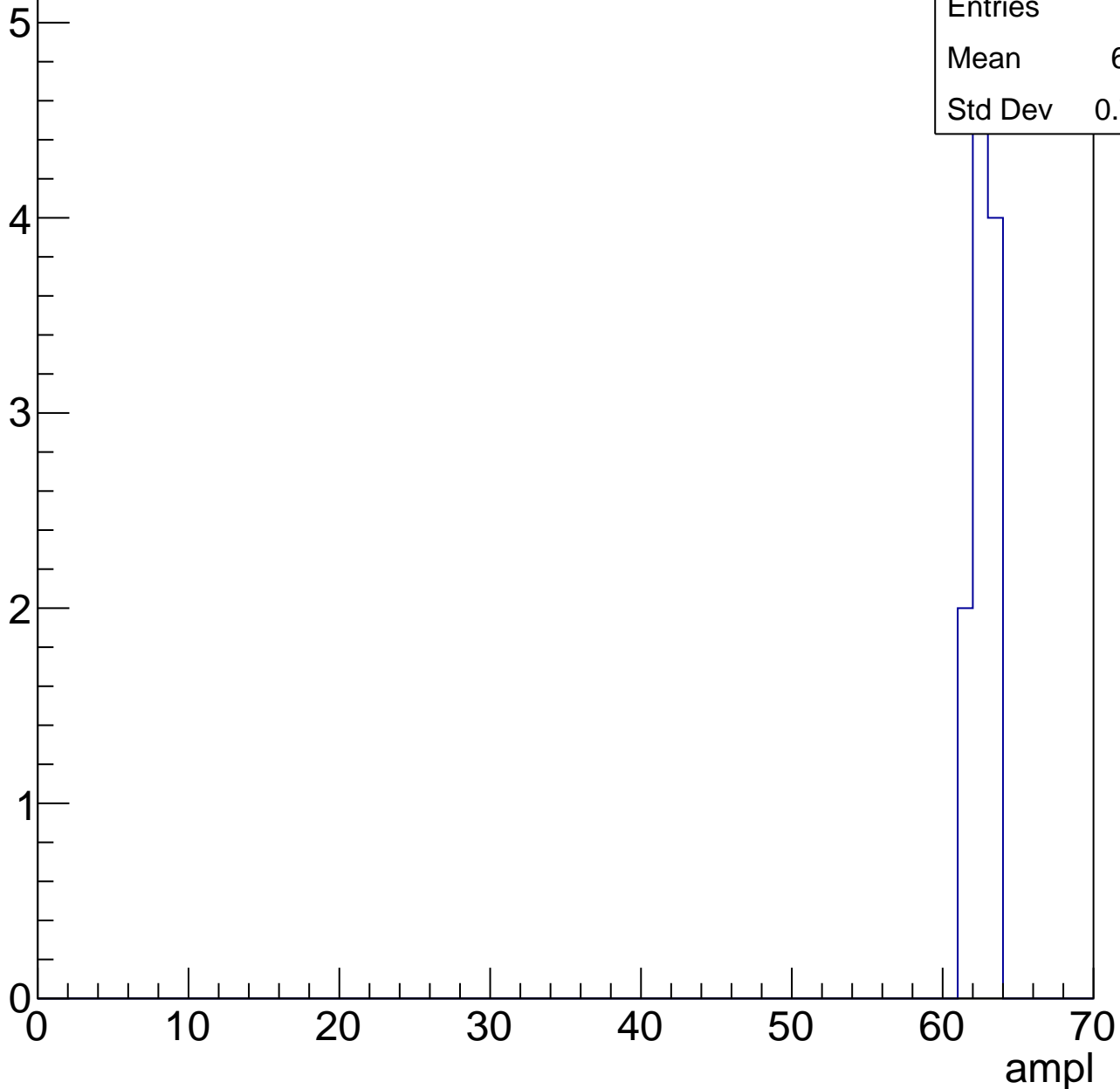
Entries	37
Mean	57.97
Std Dev	9.887

# B1L000S, U1-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	62.18
Std Dev	0.7158





# B1L000S, U1-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch106, adc0

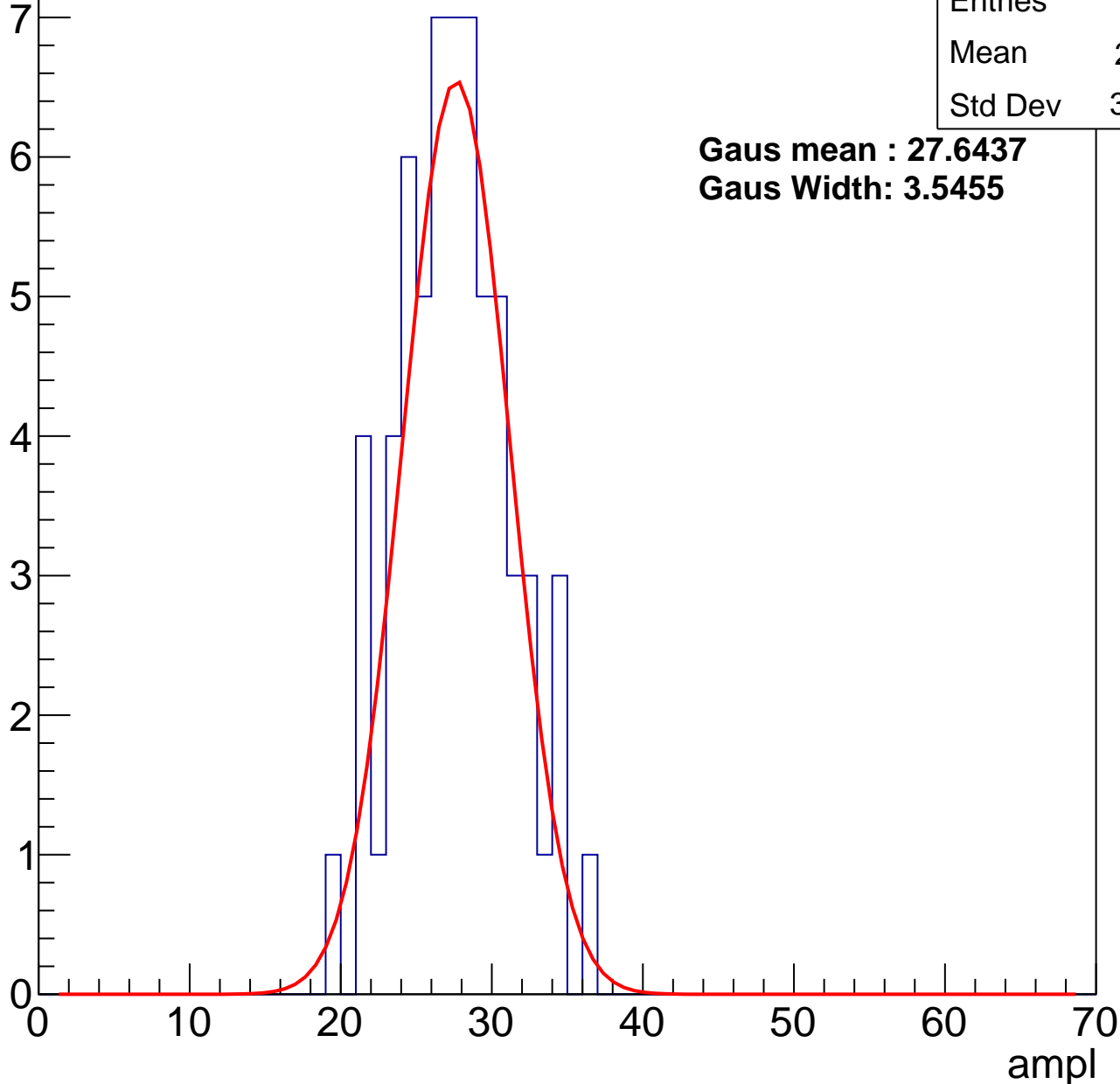
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	27.11
Std Dev	3.643

**Gaus mean : 27.6437**

**Gaus Width: 3.5455**



# B1L000S, U1-ch106, adc1

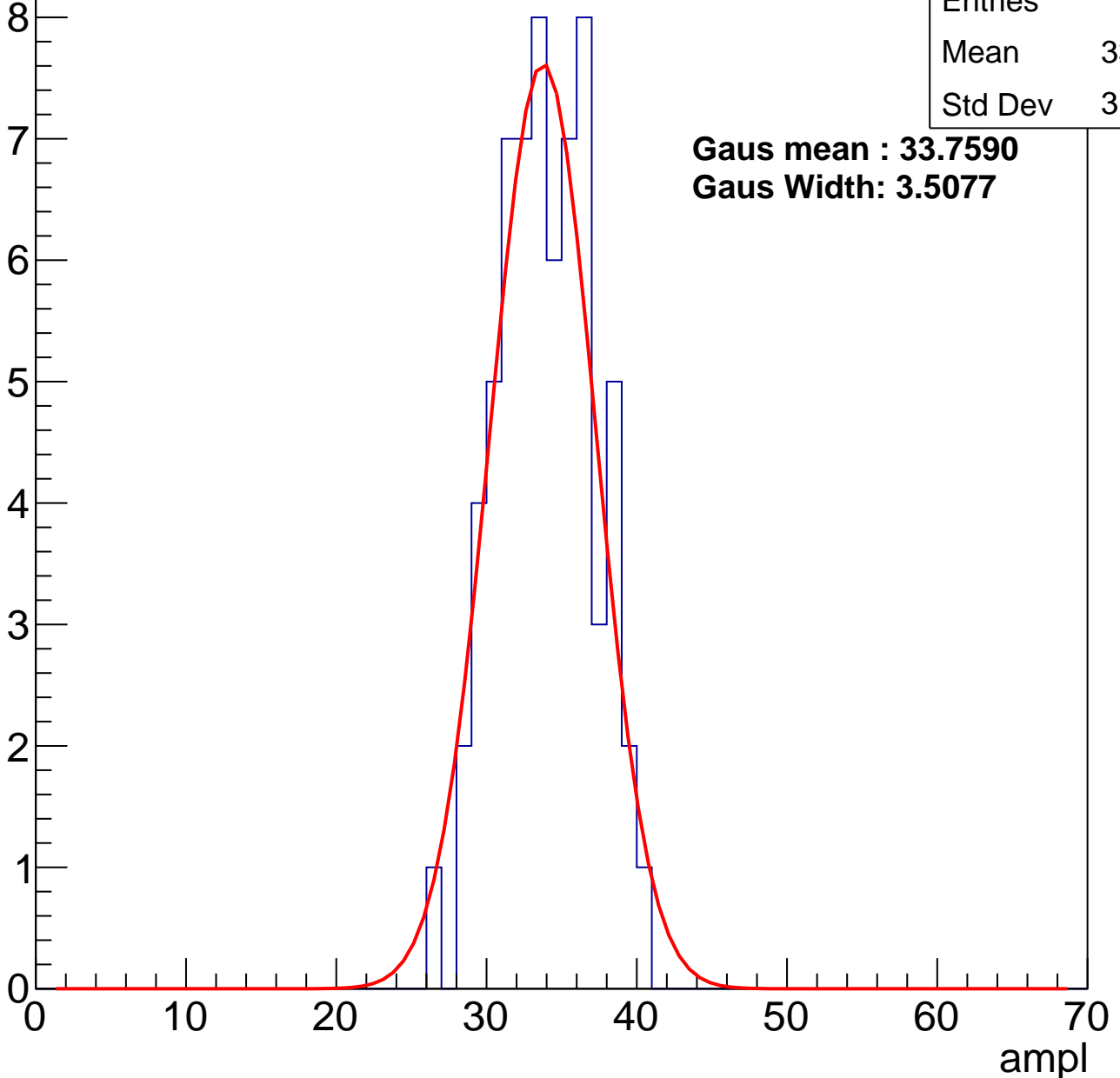
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	33.47
Std Dev	3.086

**Gaus mean : 33.7590**

**Gaus Width: 3.5077**



# B1L000S, U1-ch106, adc2

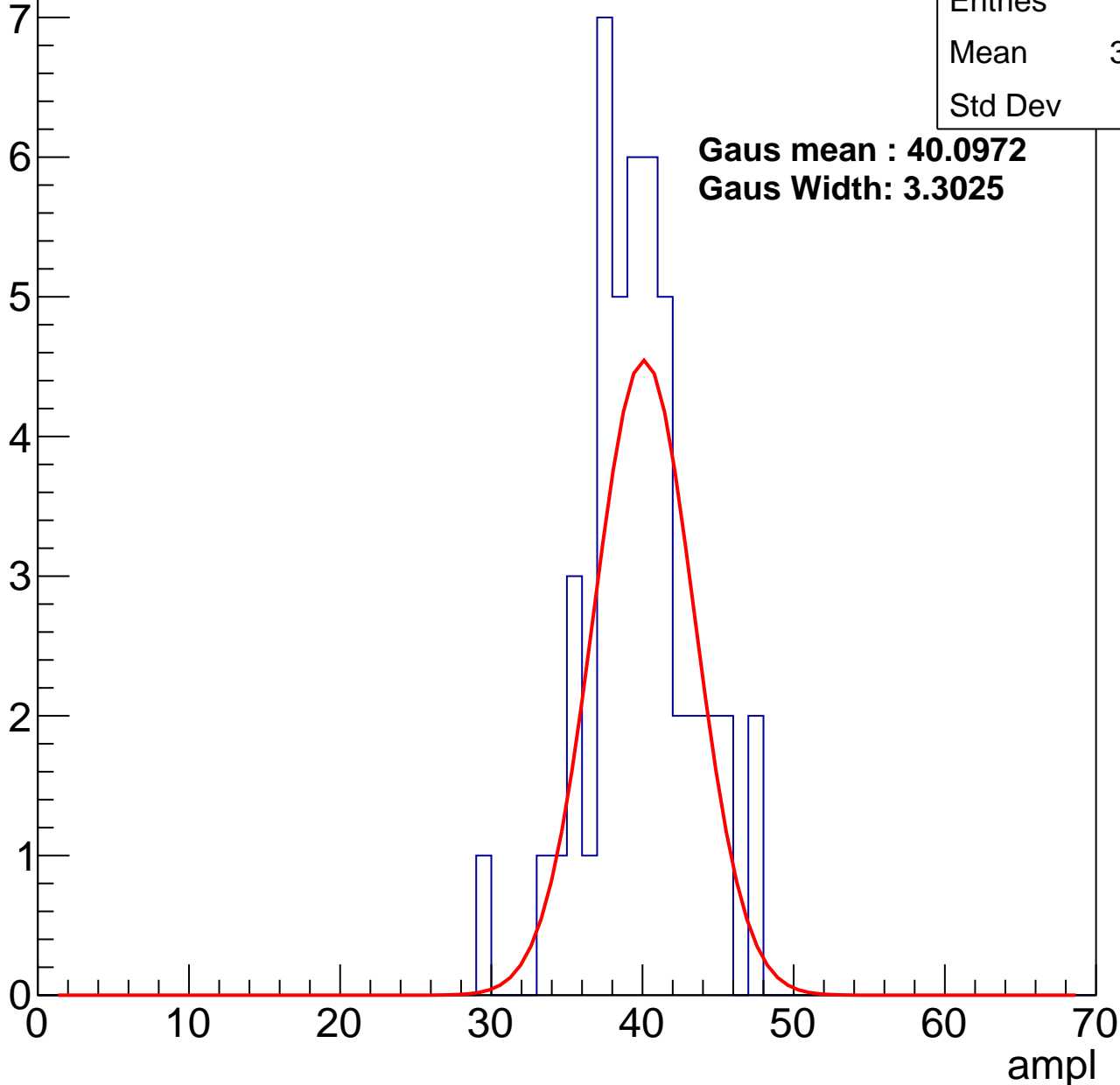
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	39.28
Std Dev	3.53

**Gaus mean : 40.0972**

**Gaus Width: 3.3025**

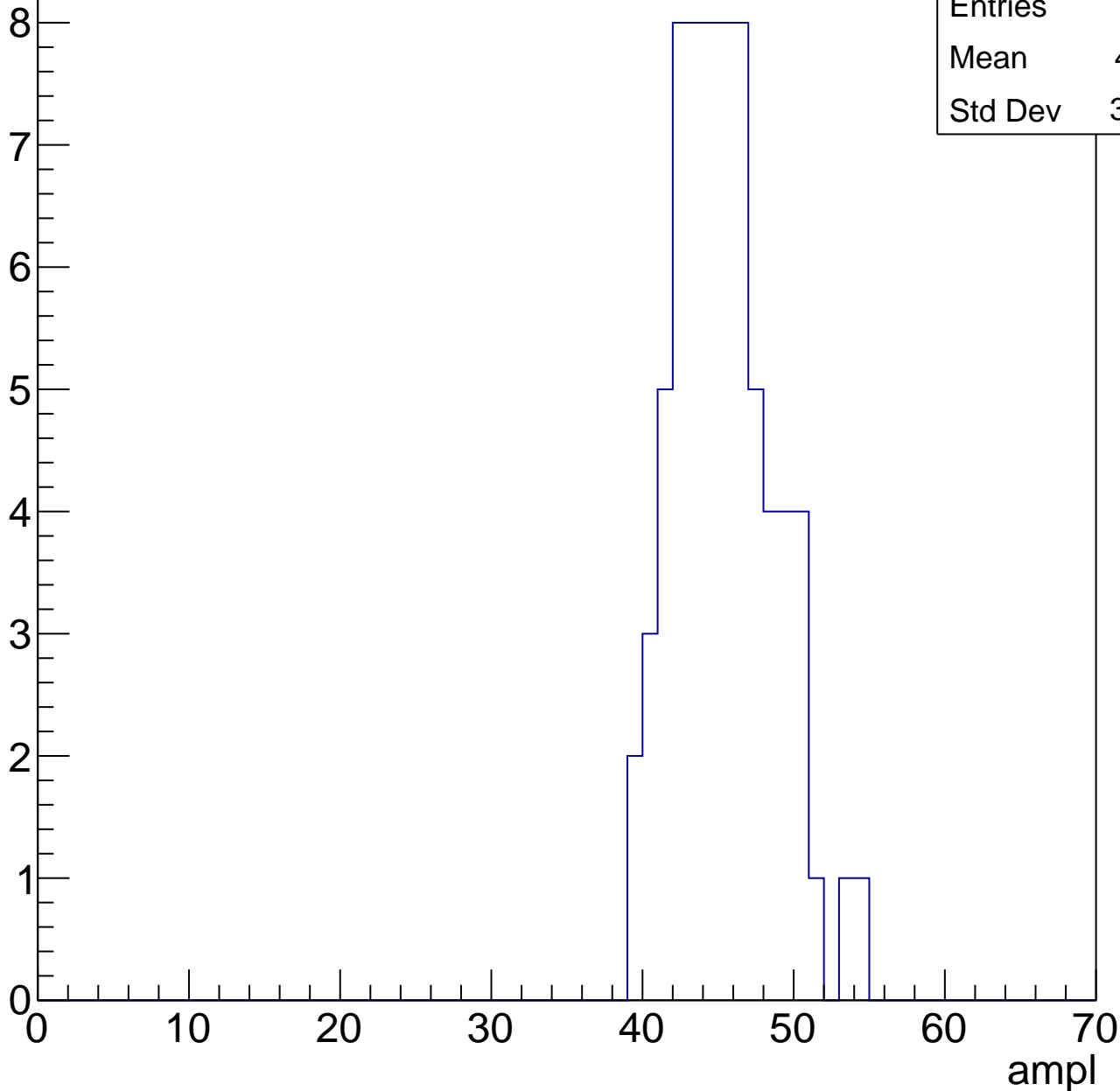


# B1L000S, U1-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	44.91
Std Dev	3.268

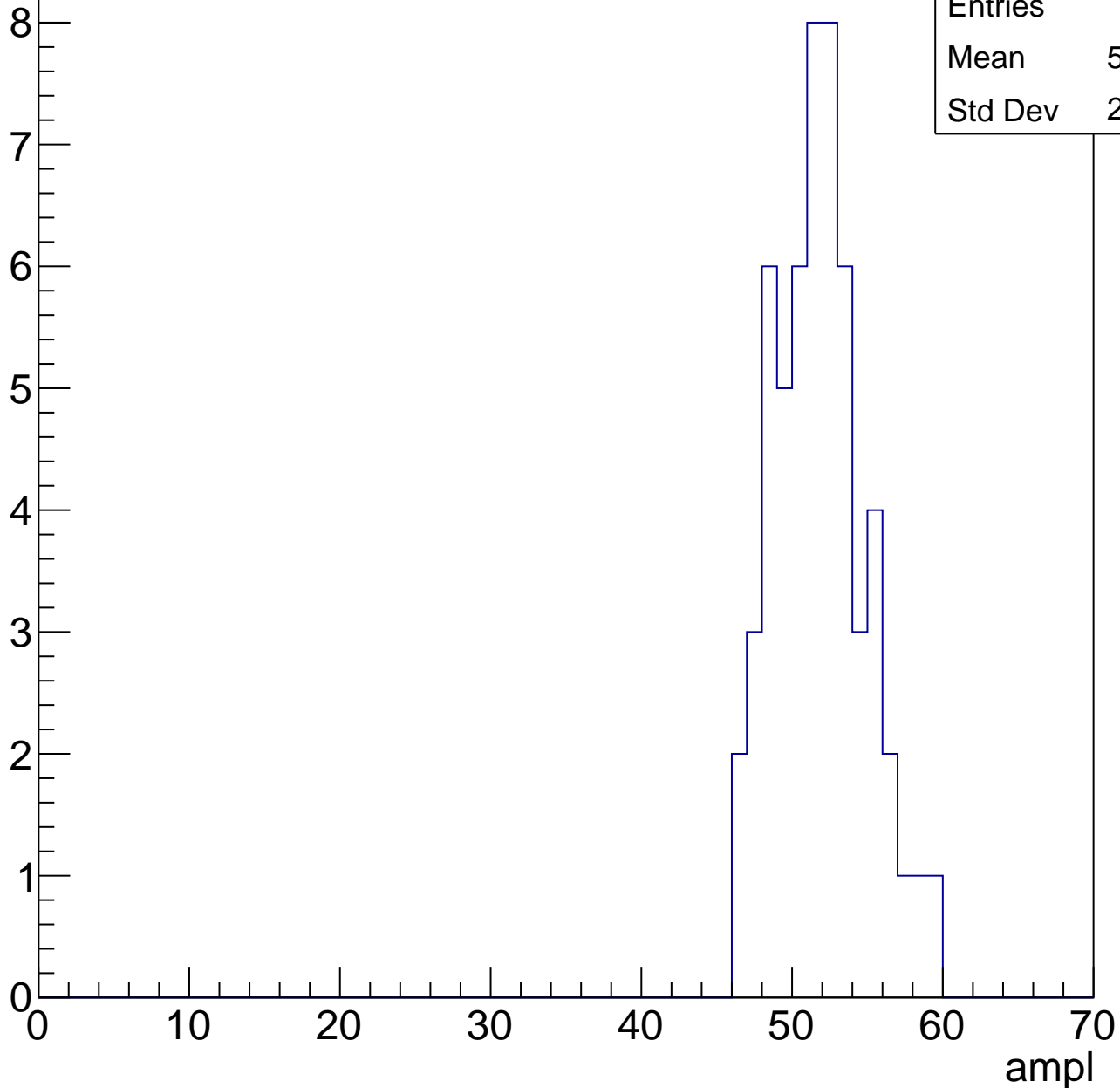


# B1L000S, U1-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

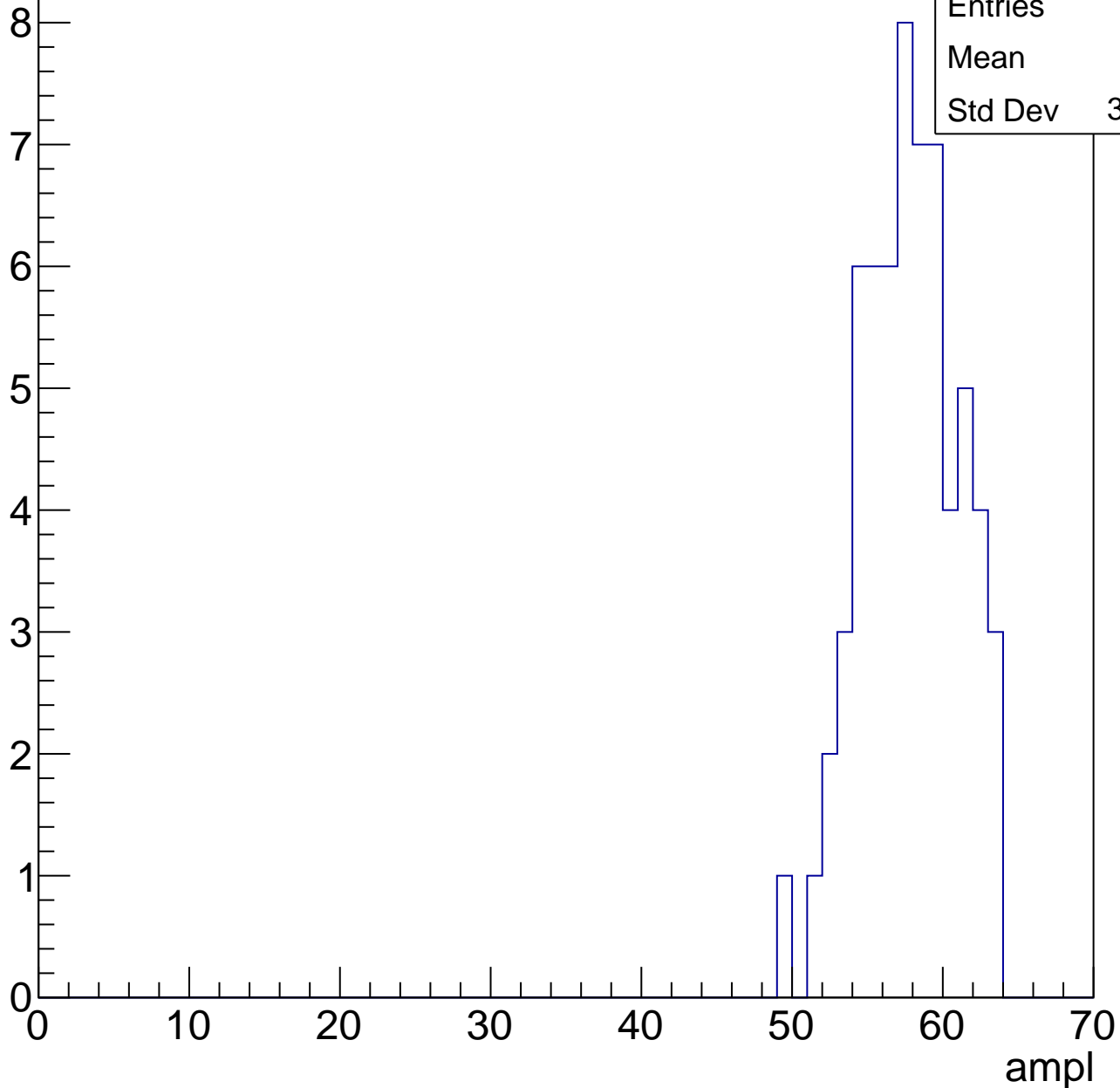
Entries	56
Mean	51.36
Std Dev	2.967



# B1L000S, U1-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

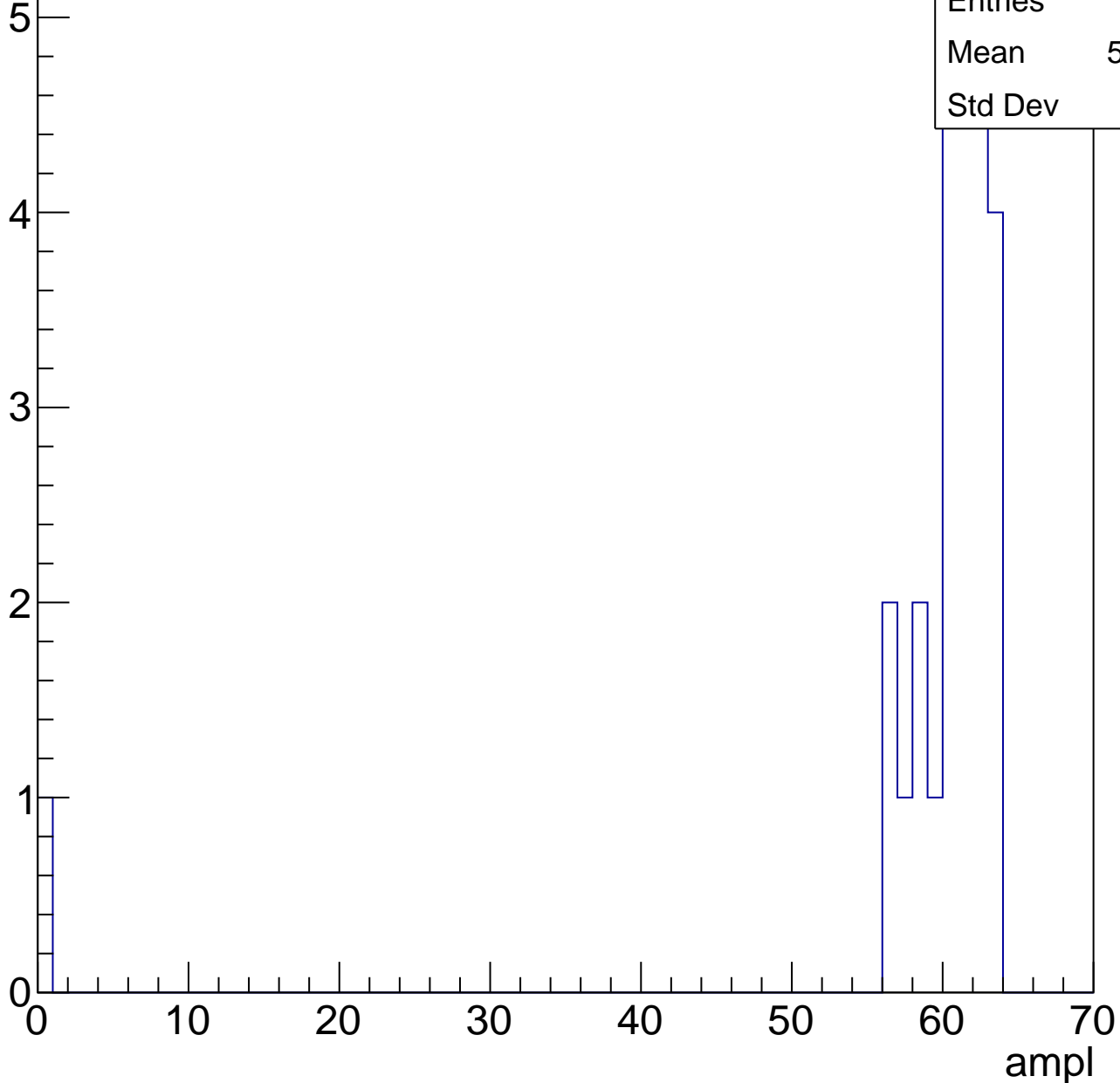


# B1L000S, U1-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.12
Std Dev	11.8

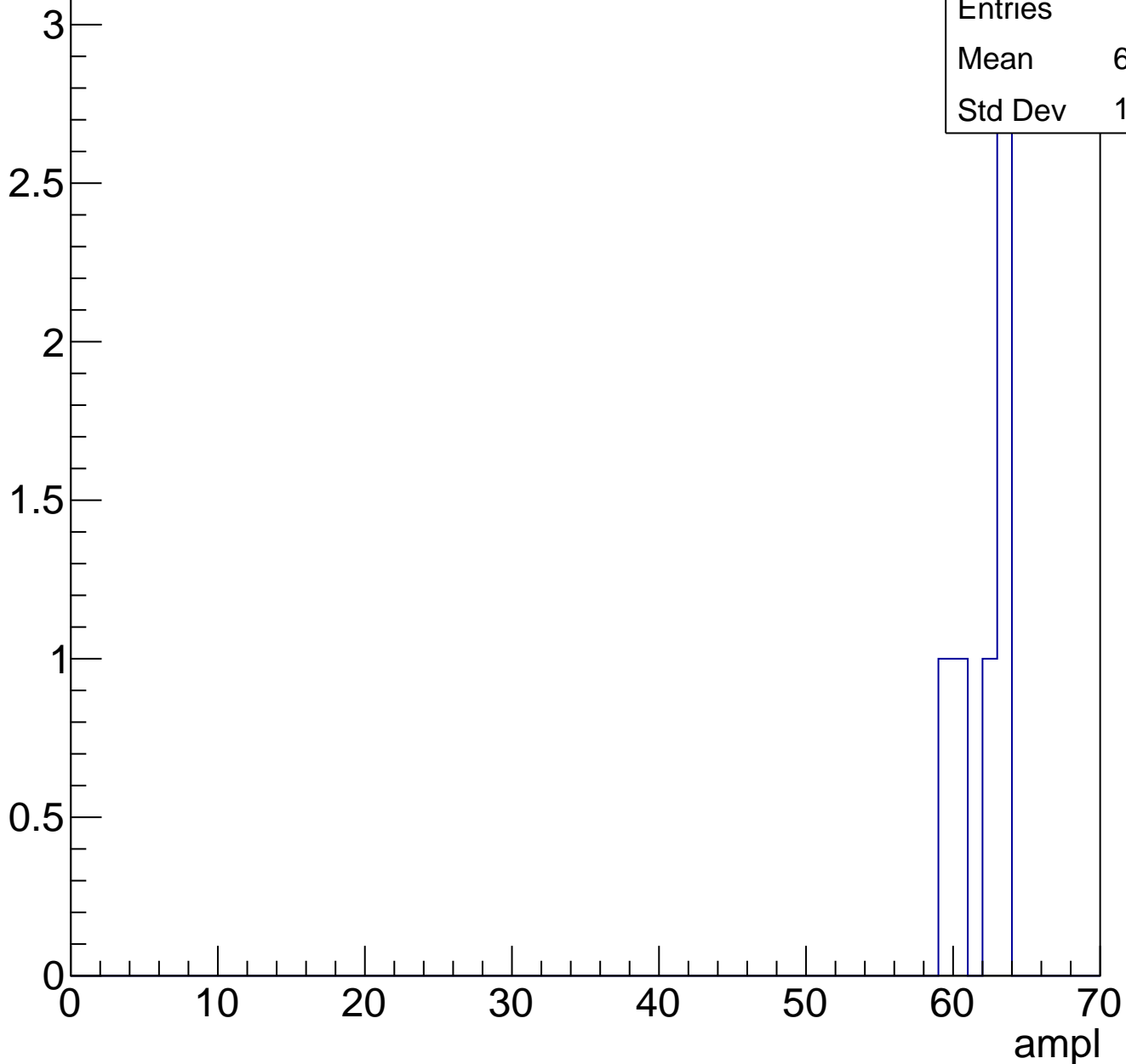




# B1L000S, U1-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	26.52
Std Dev	4.552

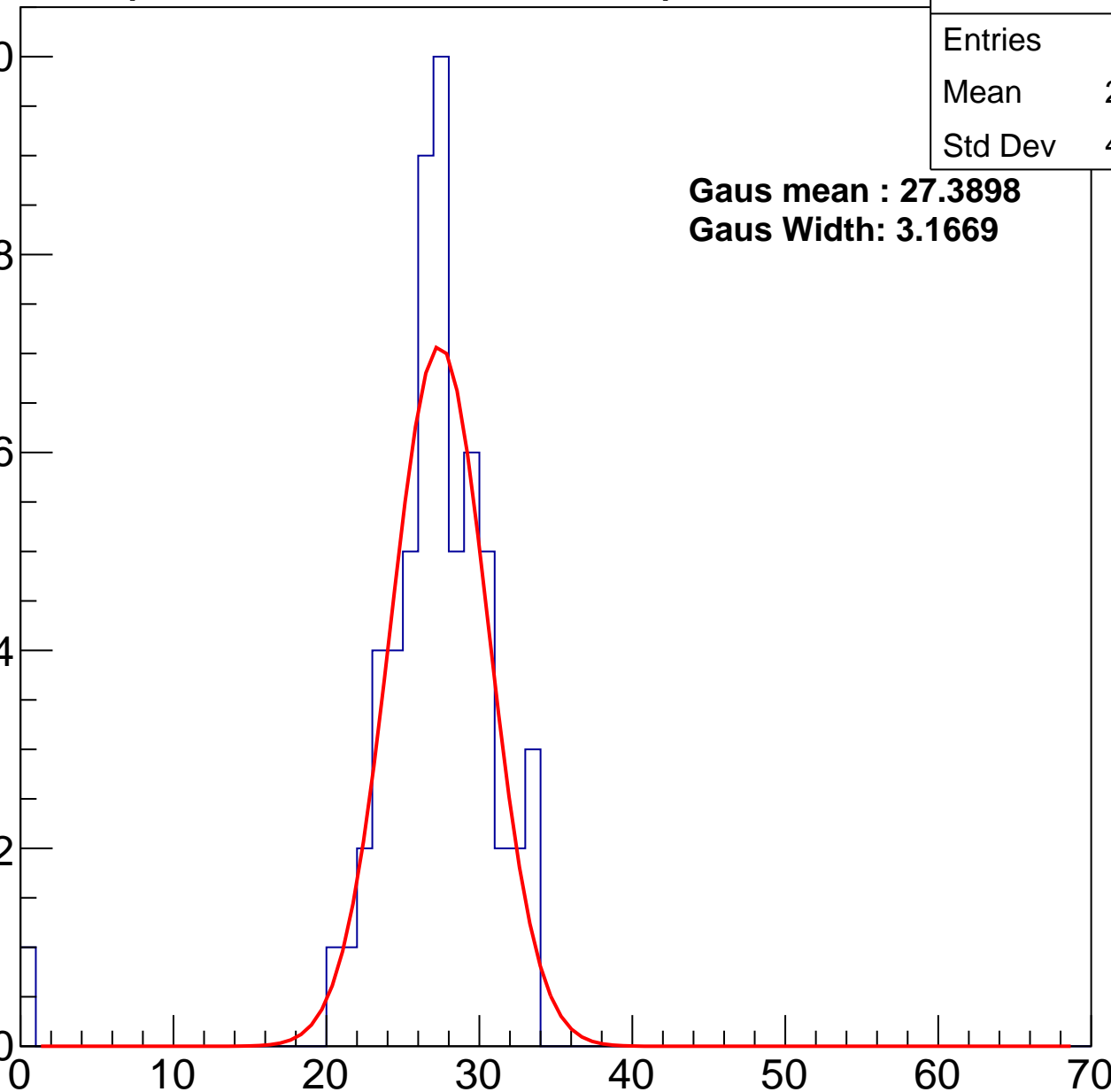
**Gaus mean : 27.3898**

**Gaus Width: 3.1669**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L000S, U1-ch107, adc1

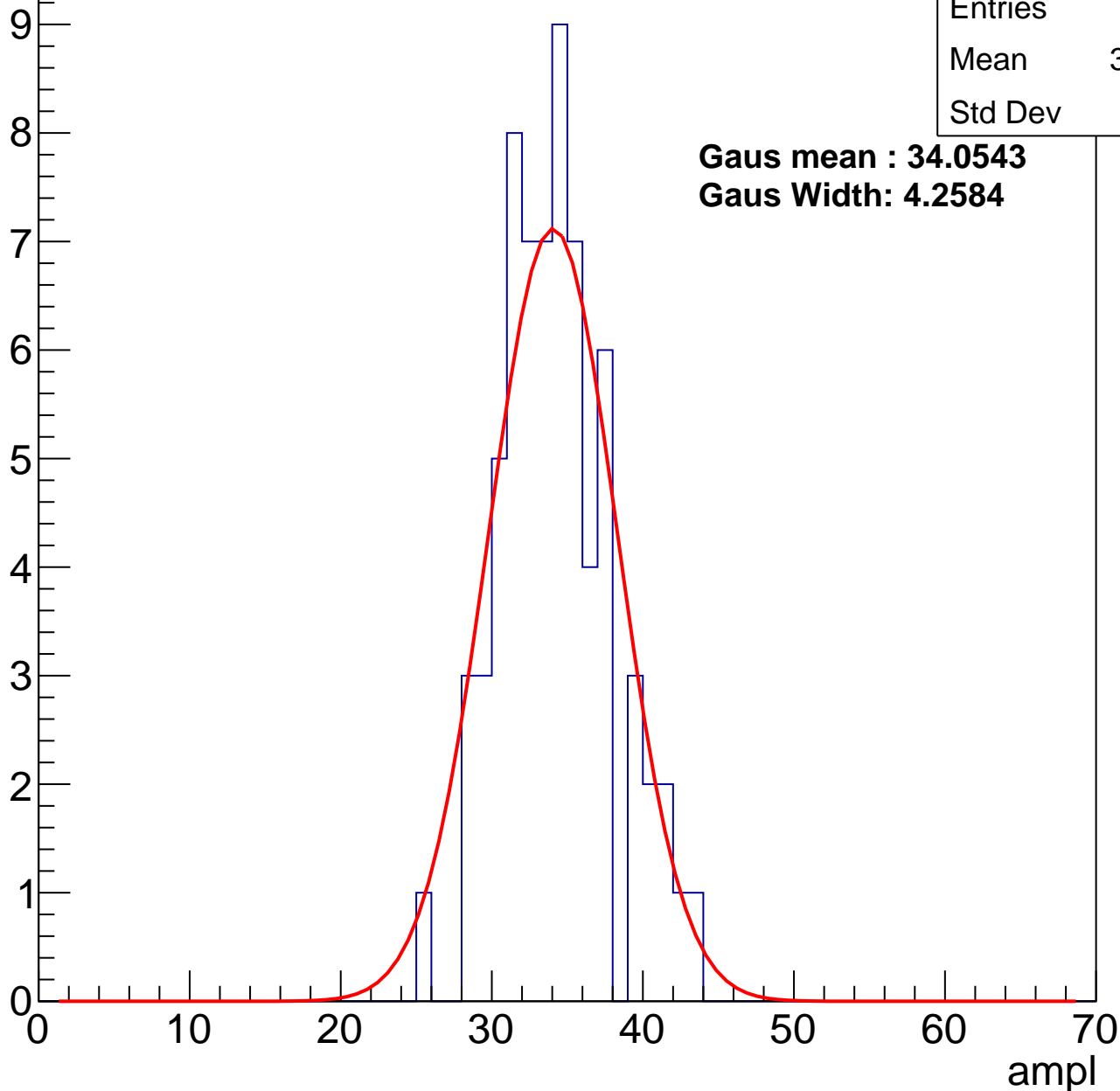
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	33.77
Std Dev	3.64

**Gaus mean : 34.0543**

**Gaus Width: 4.2584**



# B1L000S, U1-ch107, adc2

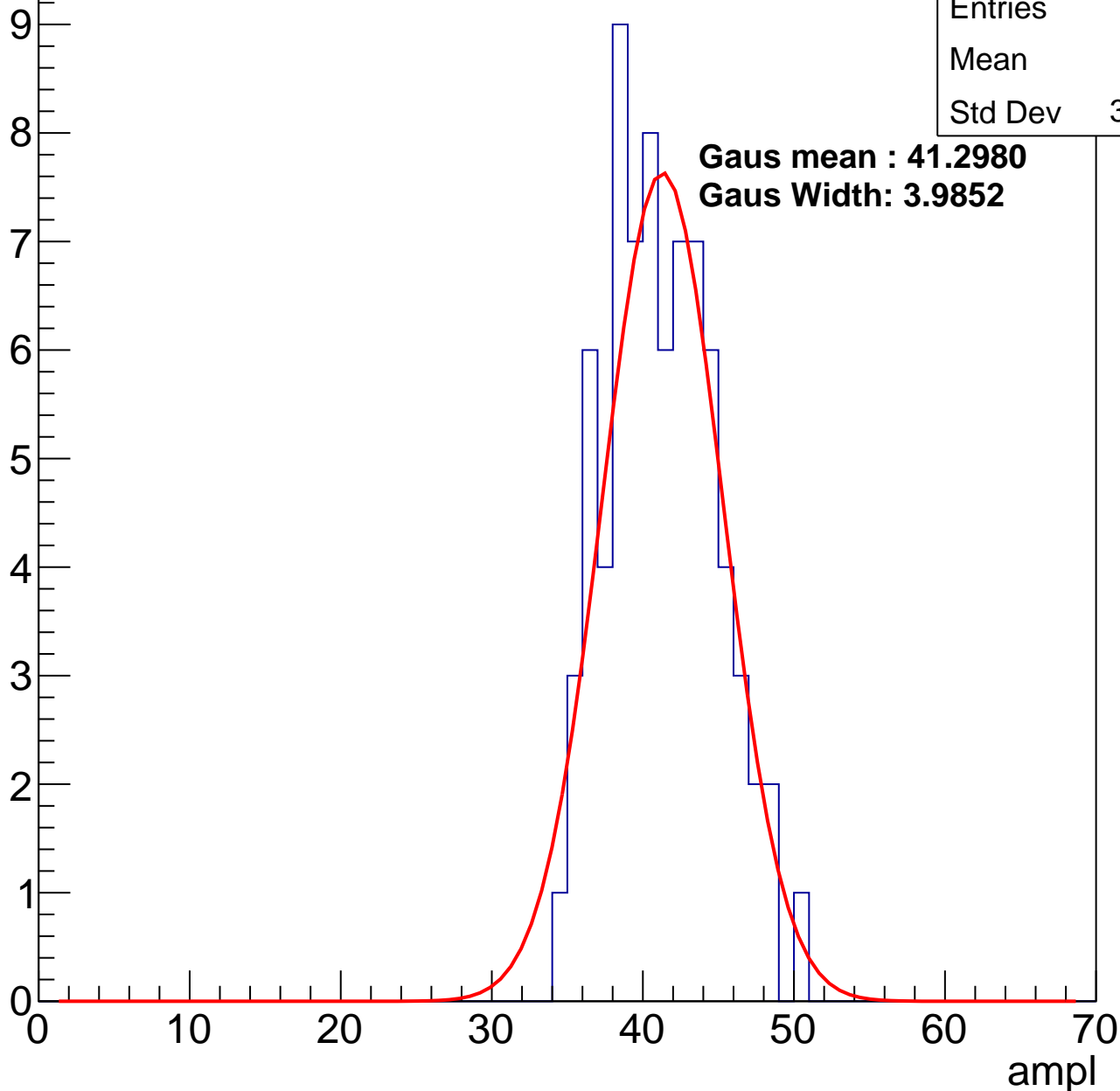
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	40.8
Std Dev	3.562

**Gaus mean : 41.2980**

**Gaus Width: 3.9852**

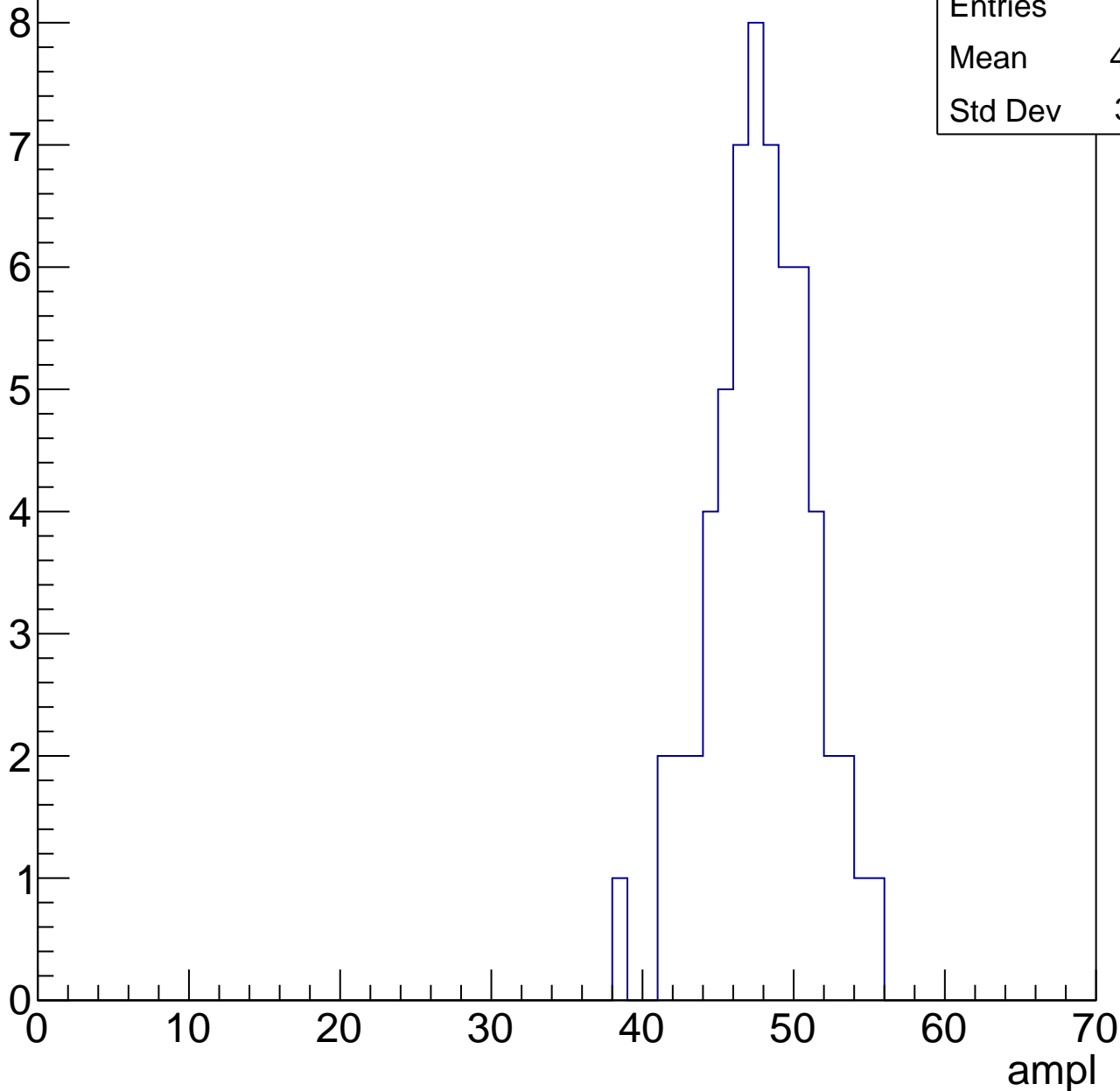


# B1L000S, U1-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

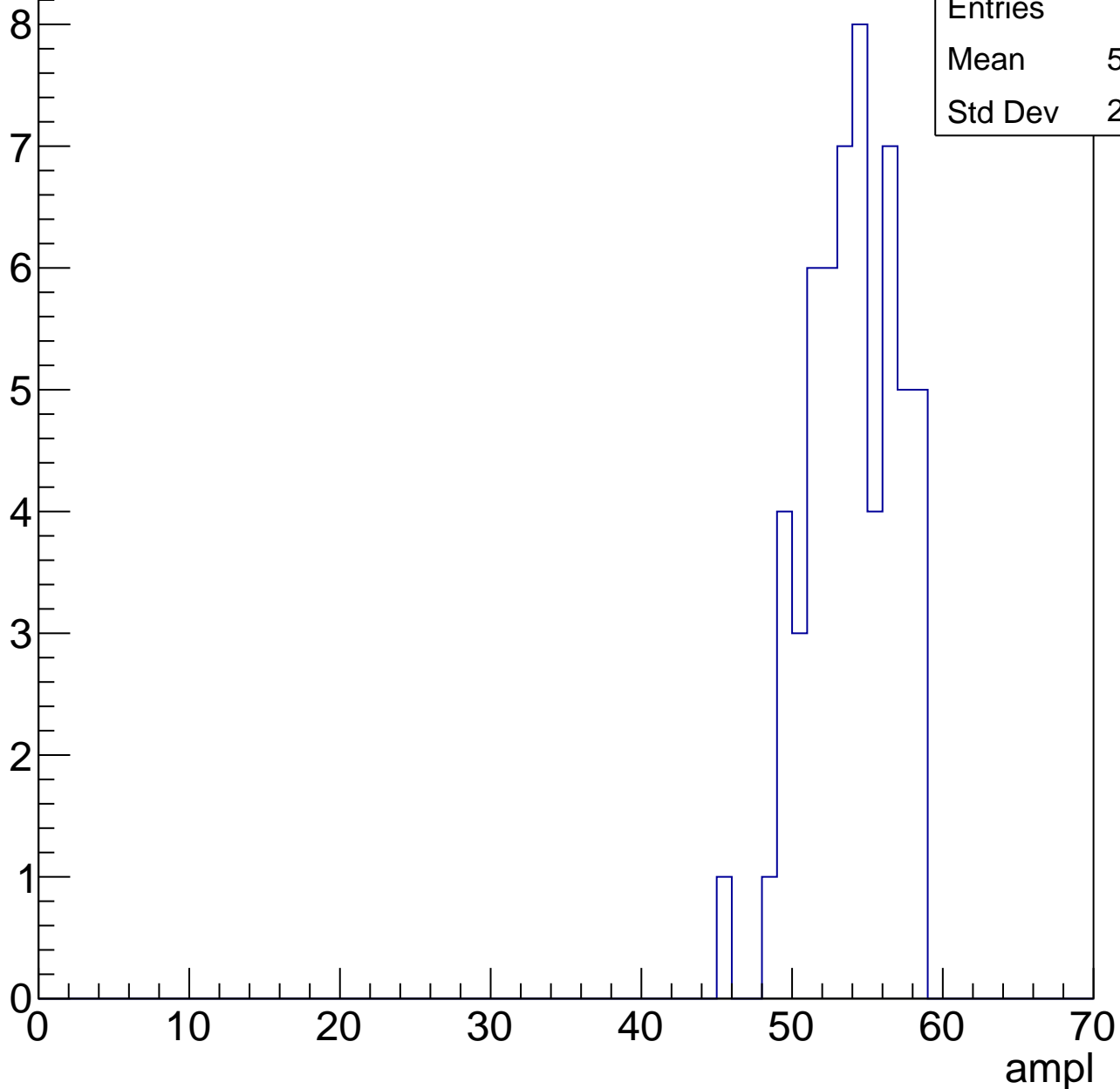
Entries	60
Mean	47.37
Std Dev	3.351



# B1L000S, U1-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

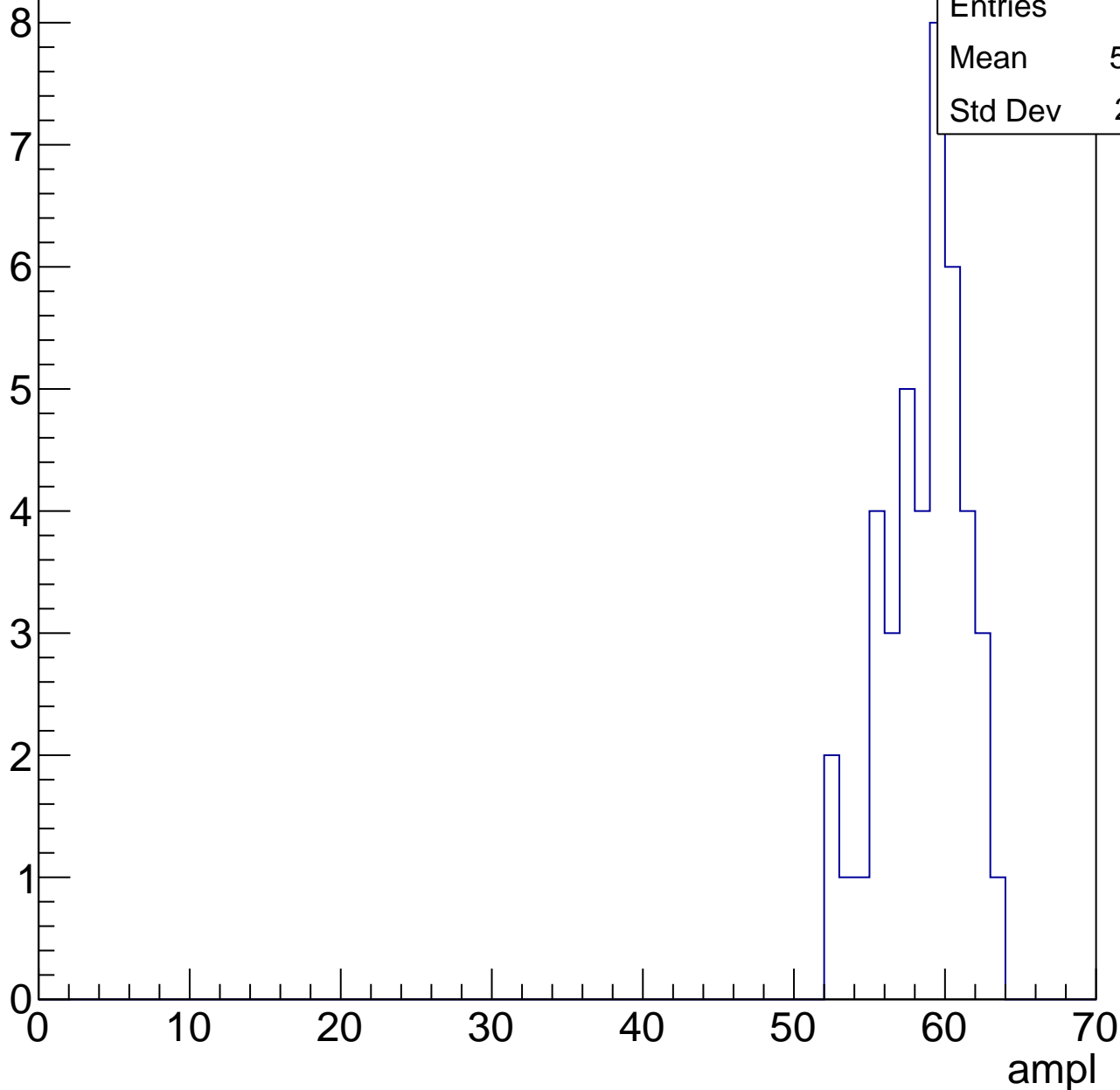


# B1L000S, U1-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	58.12
Std Dev	2.701

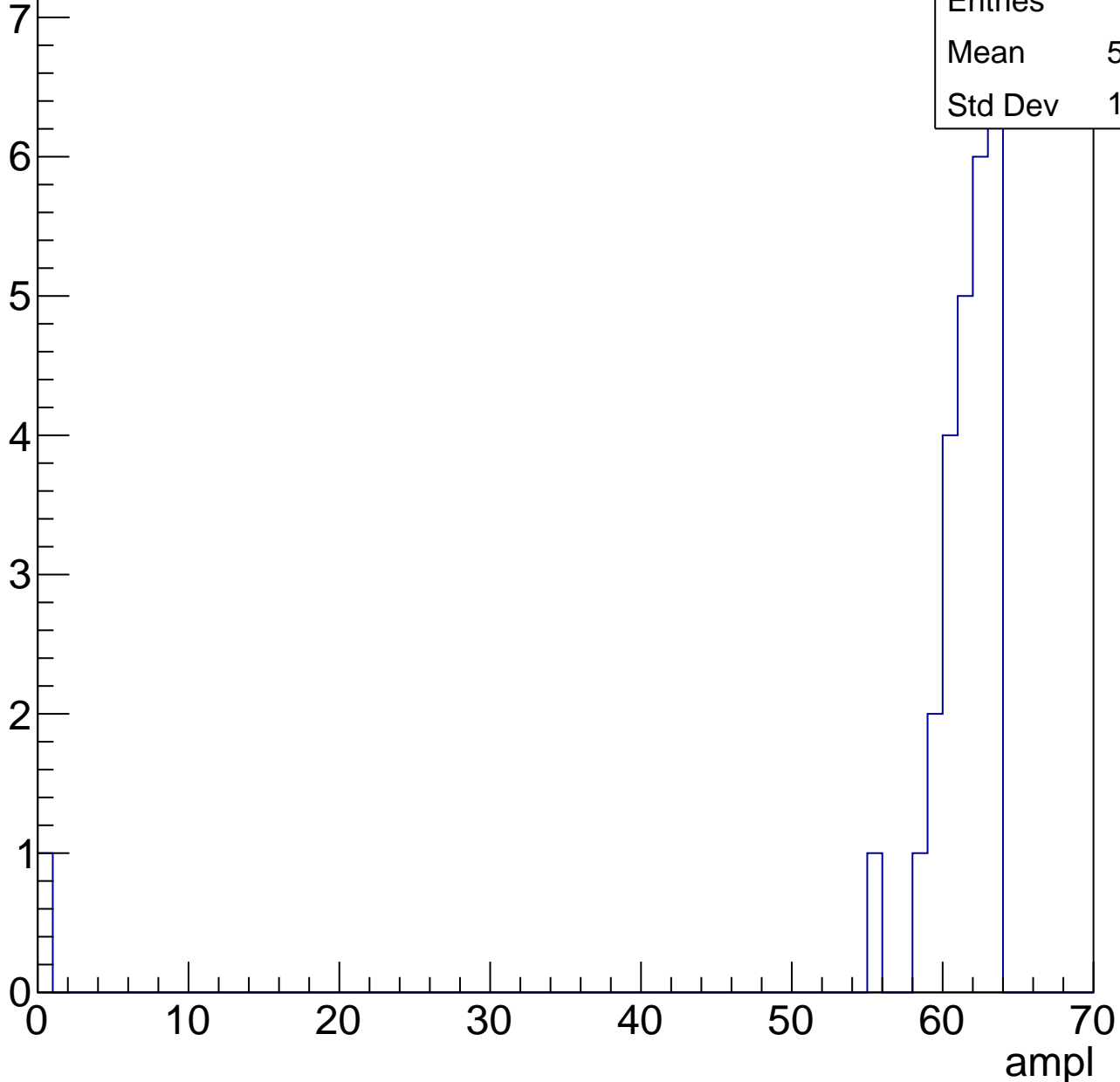


# B1L000S, U1-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	58.85
Std Dev	11.69

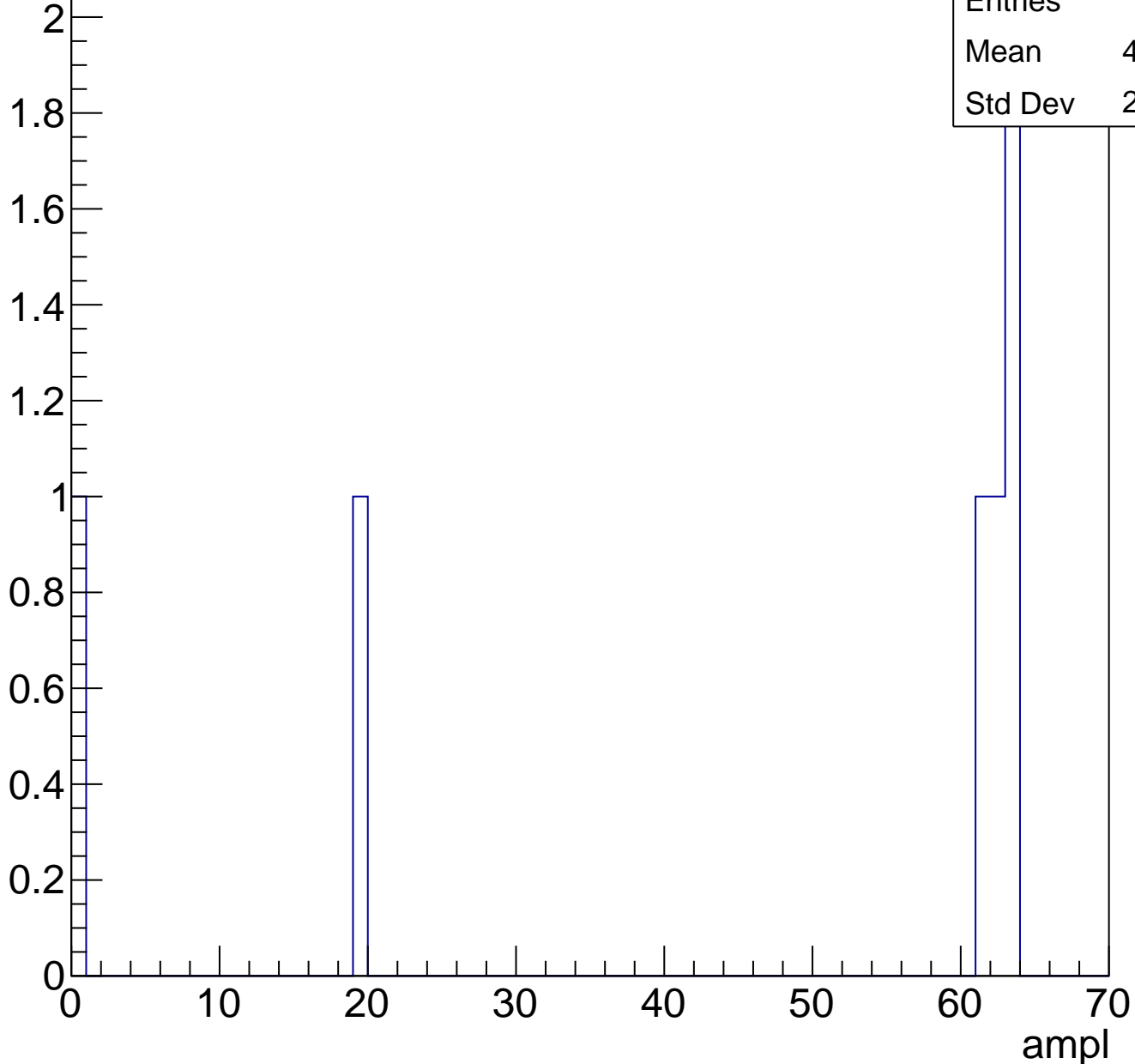




# B1L000S, U1-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	6
Mean	44.67
Std Dev	25.47

# B1L000S, U1-ch108, adc0

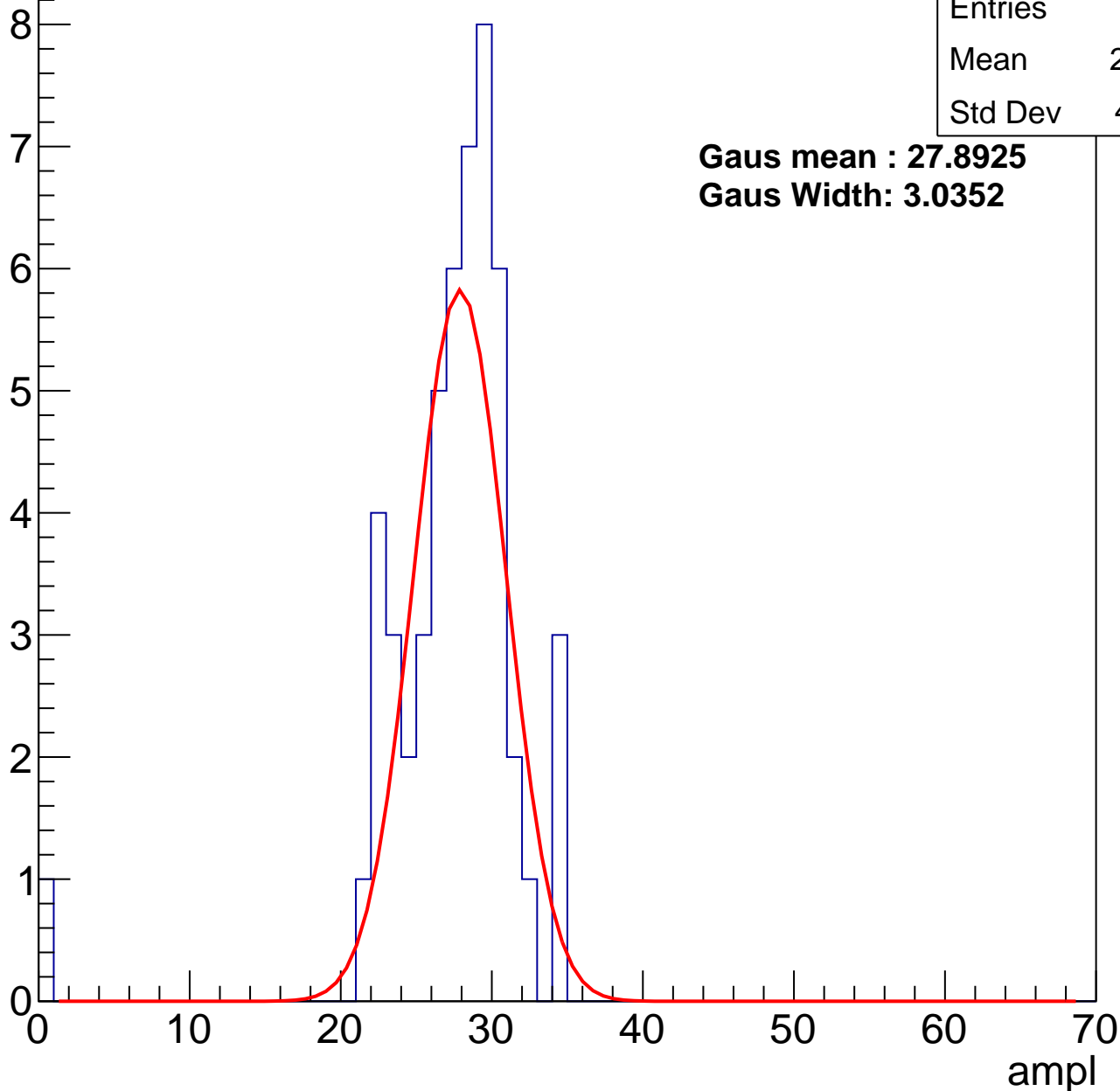
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	26.87
Std Dev	4.891

**Gaus mean : 27.8925**

**Gaus Width: 3.0352**



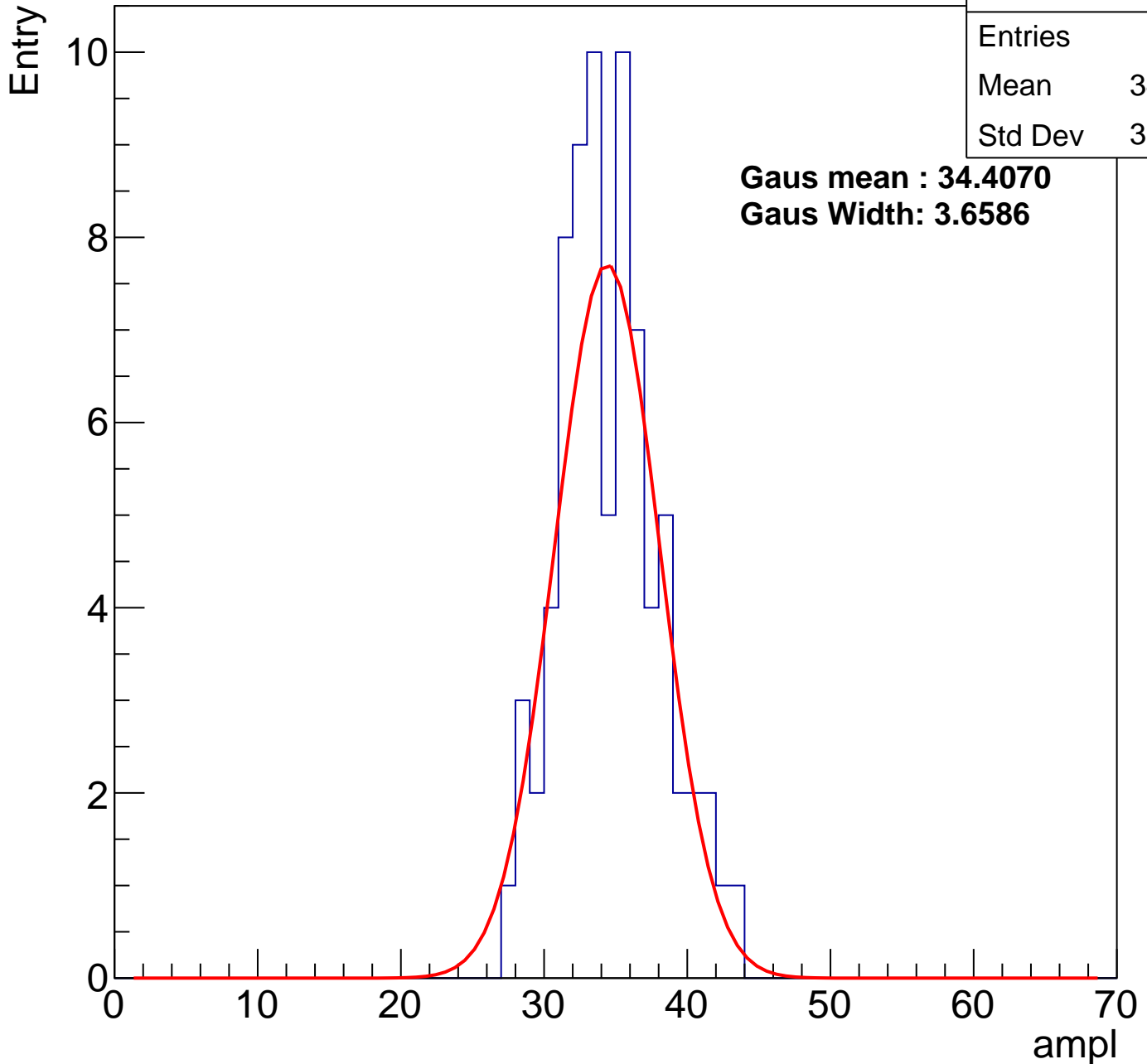
# B1L000S, U1-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	34.08
Std Dev	3.456

**Gaus mean : 34.4070**

**Gaus Width: 3.6586**



# B1L000S, U1-ch108, adc2

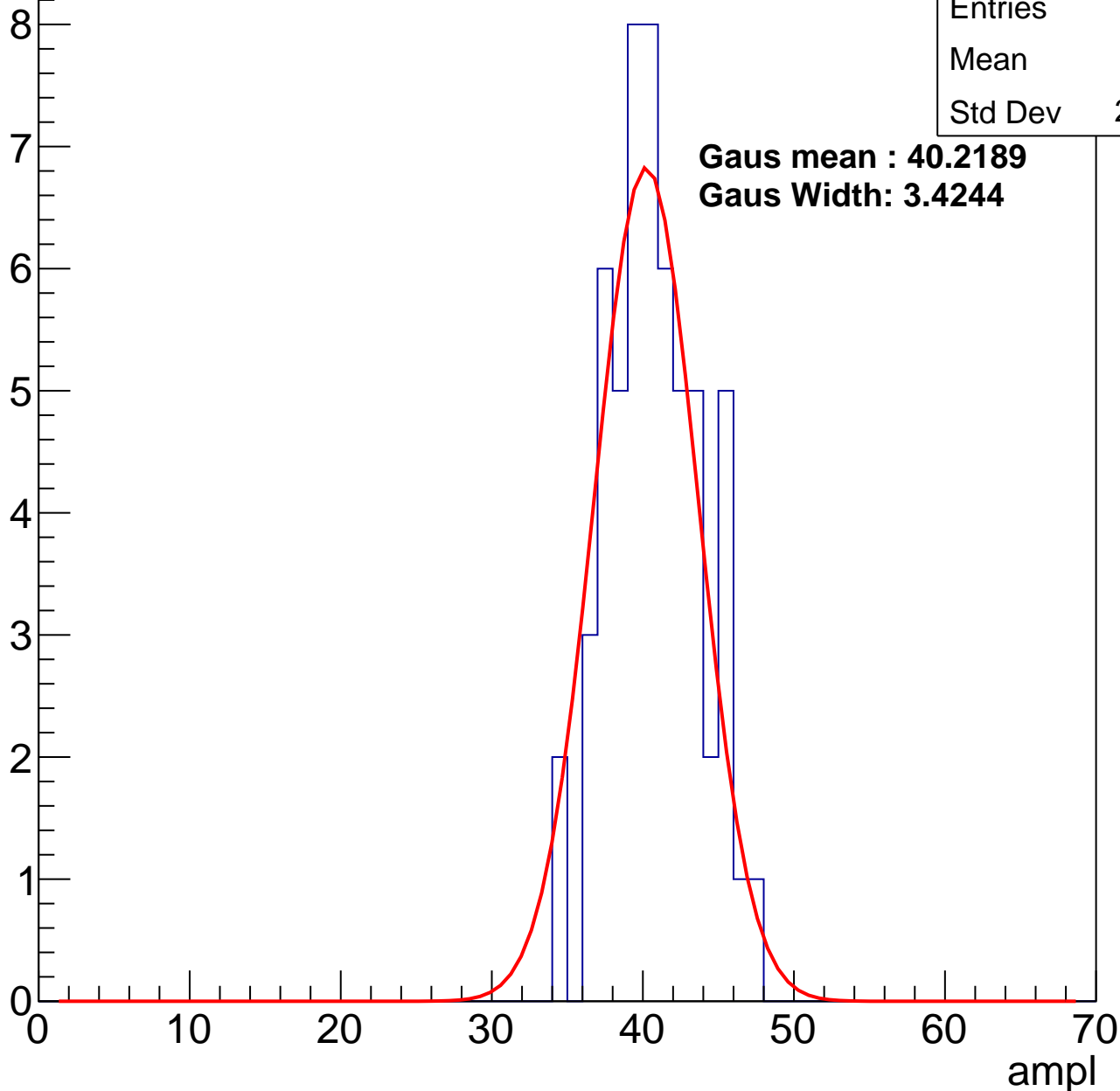
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	40.3
Std Dev	2.991

**Gaus mean : 40.2189**

**Gaus Width: 3.4244**

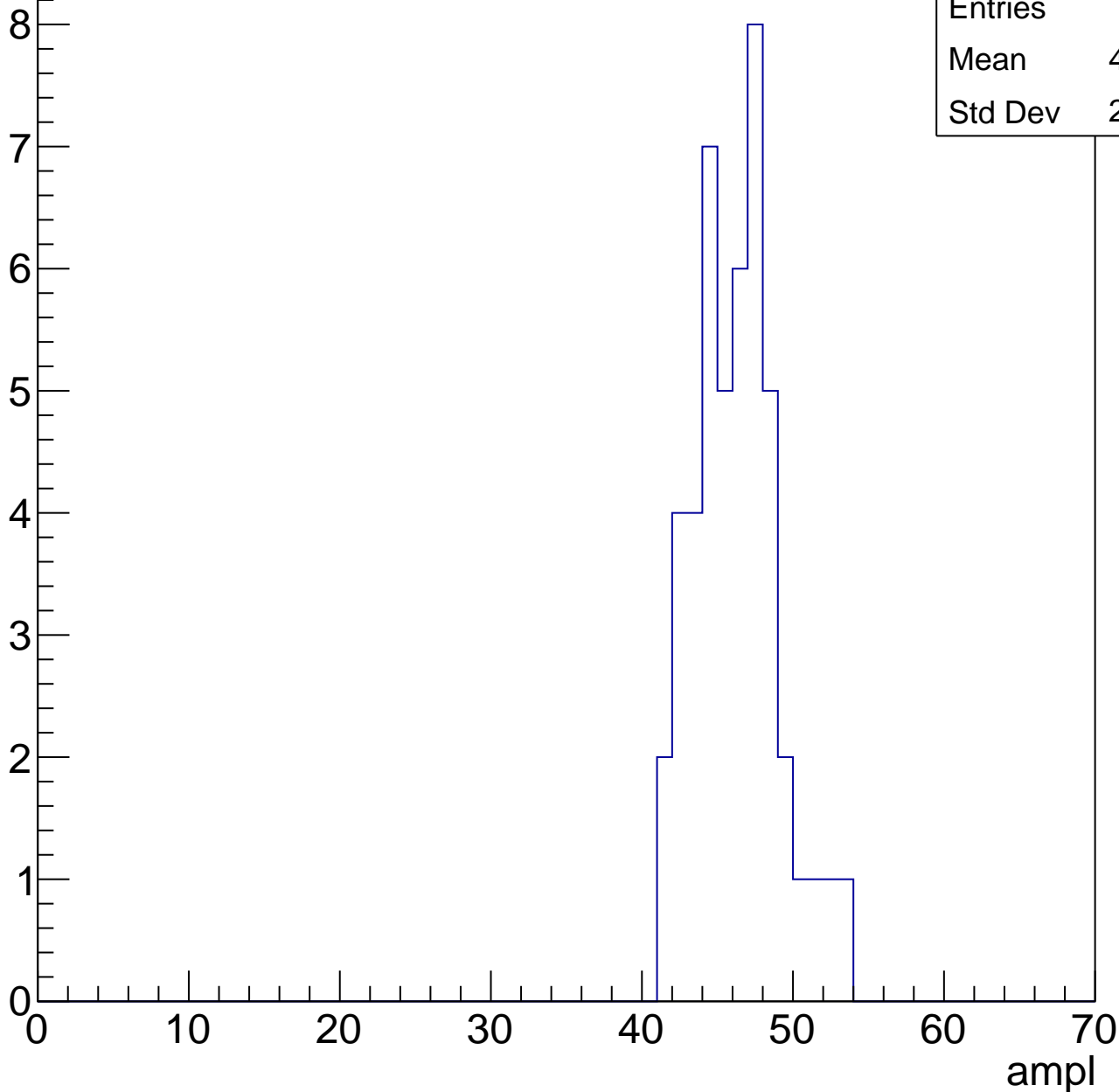


# B1L000S, U1-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	45.77
Std Dev	2.738

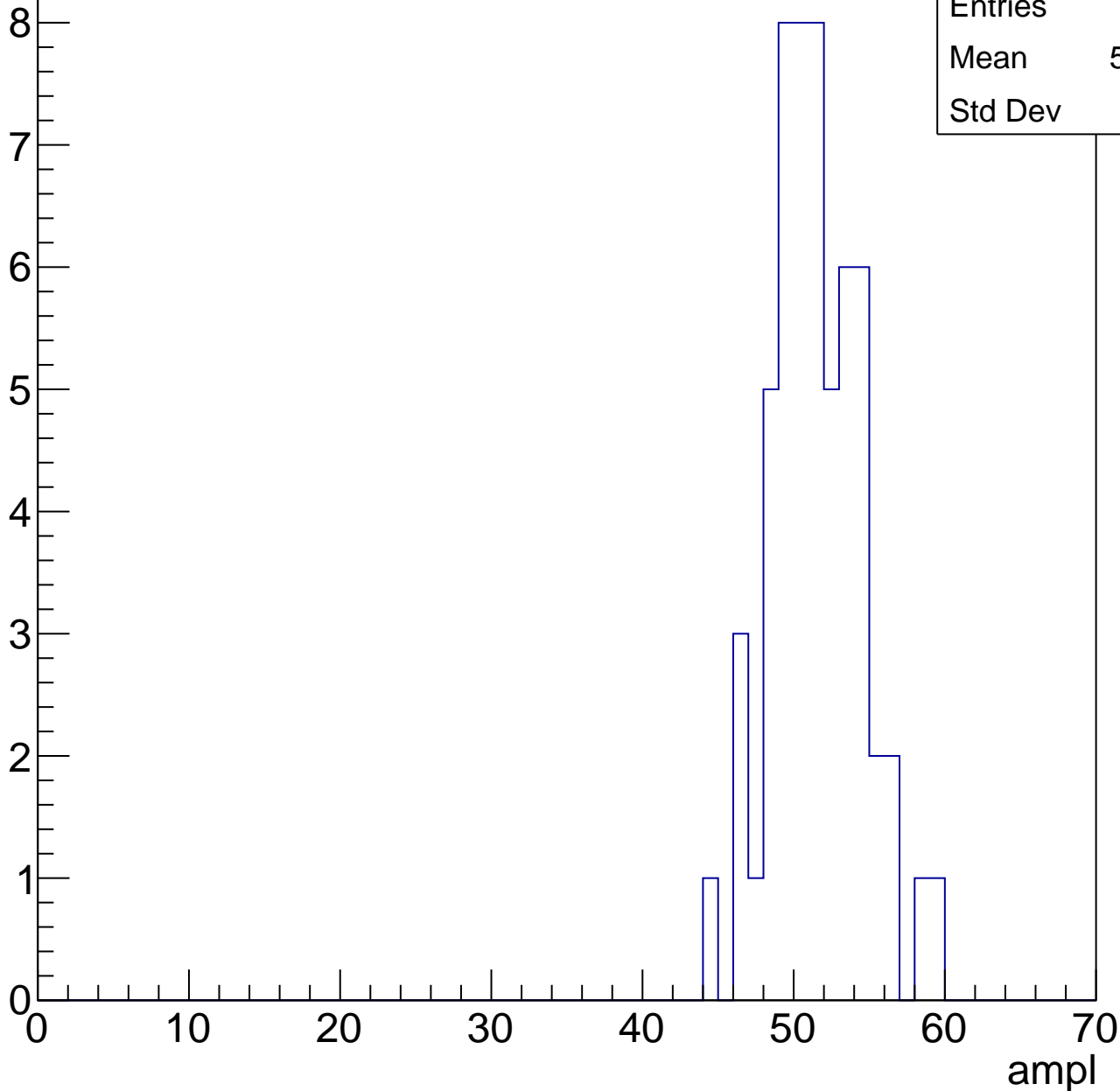


# B1L000S, U1-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	51.05
Std Dev	2.97

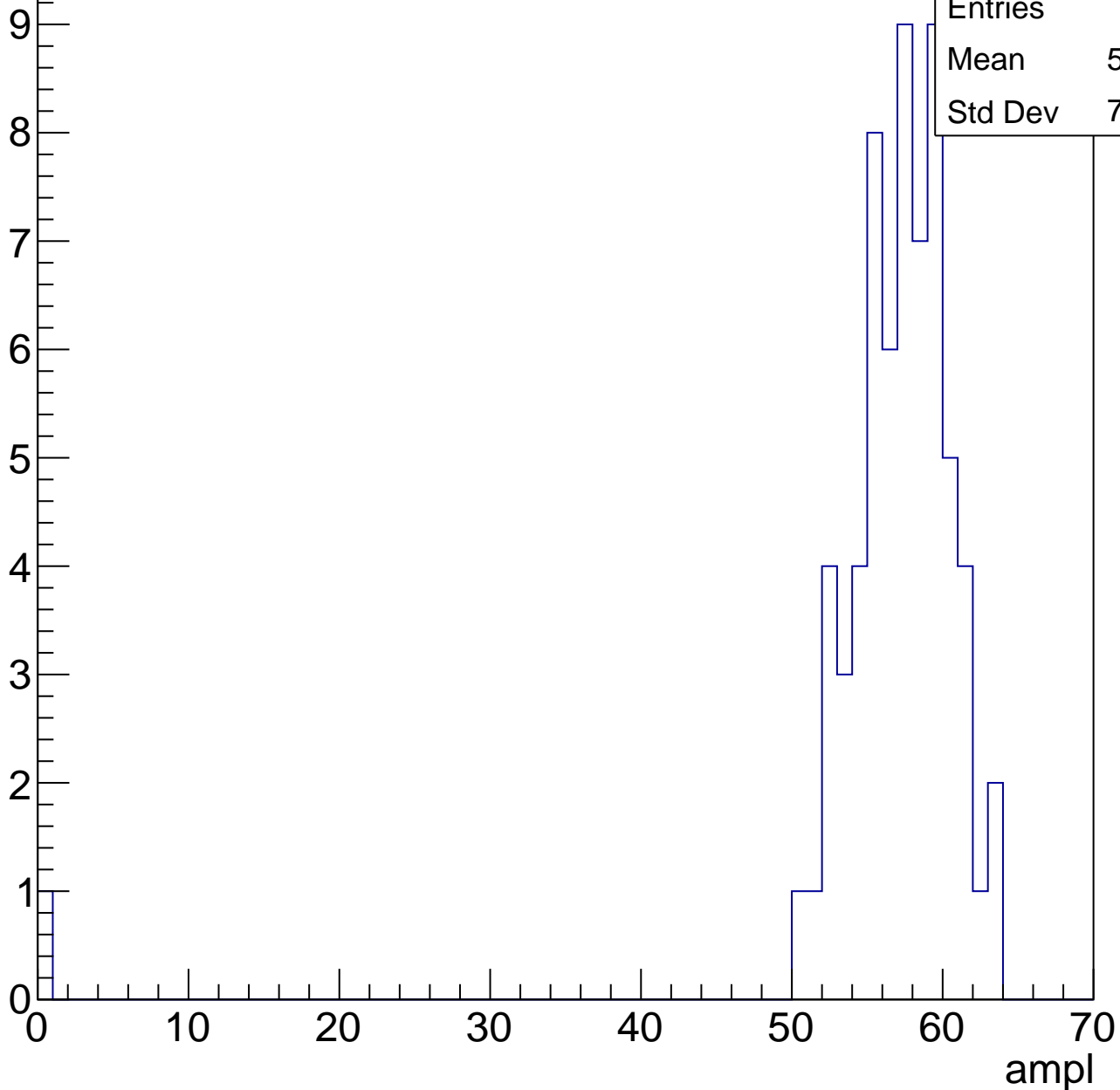


# B1L000S, U1-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	56.03
Std Dev	7.589

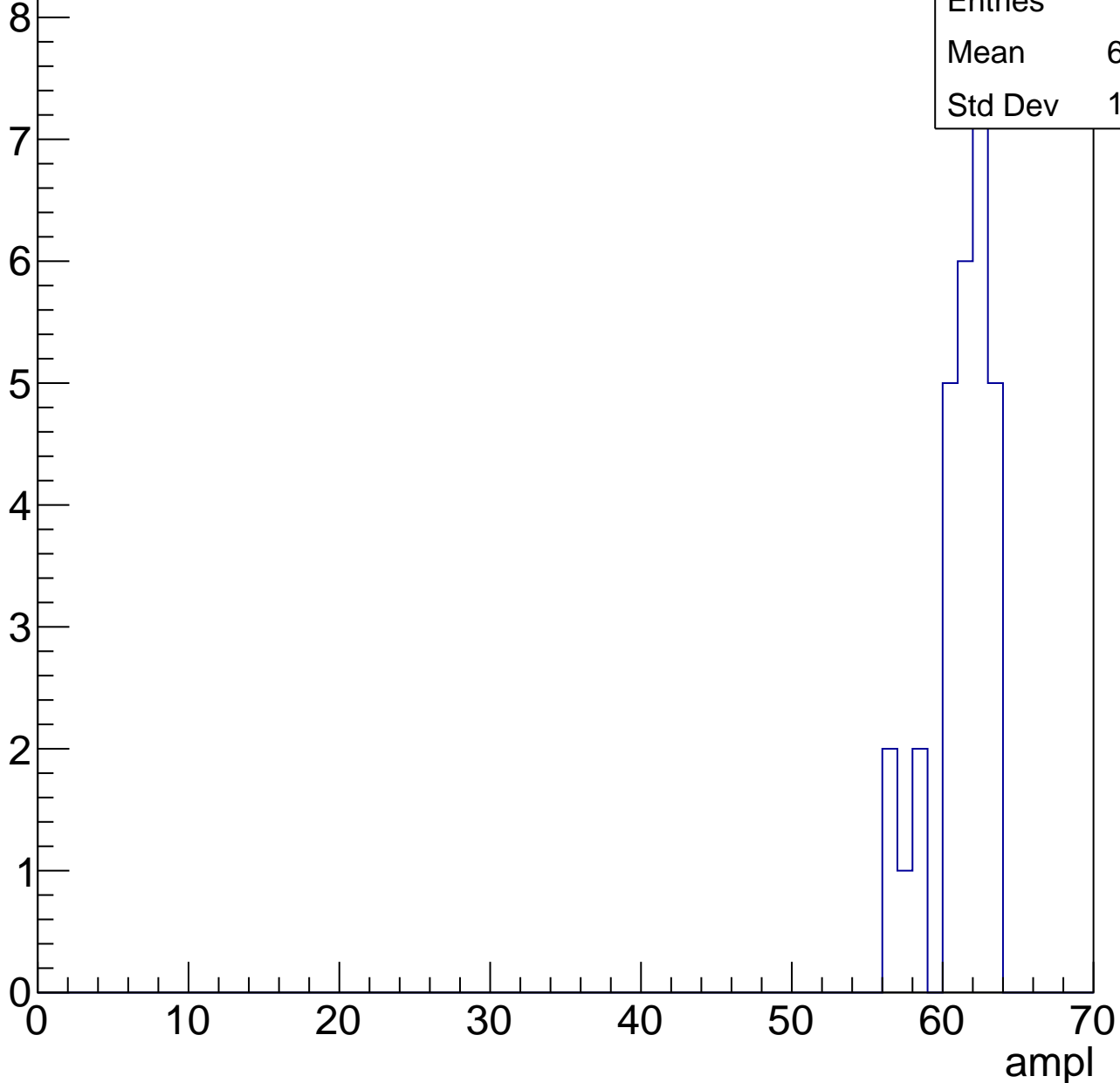


# B1L000S, U1-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	60.76
Std Dev	1.994

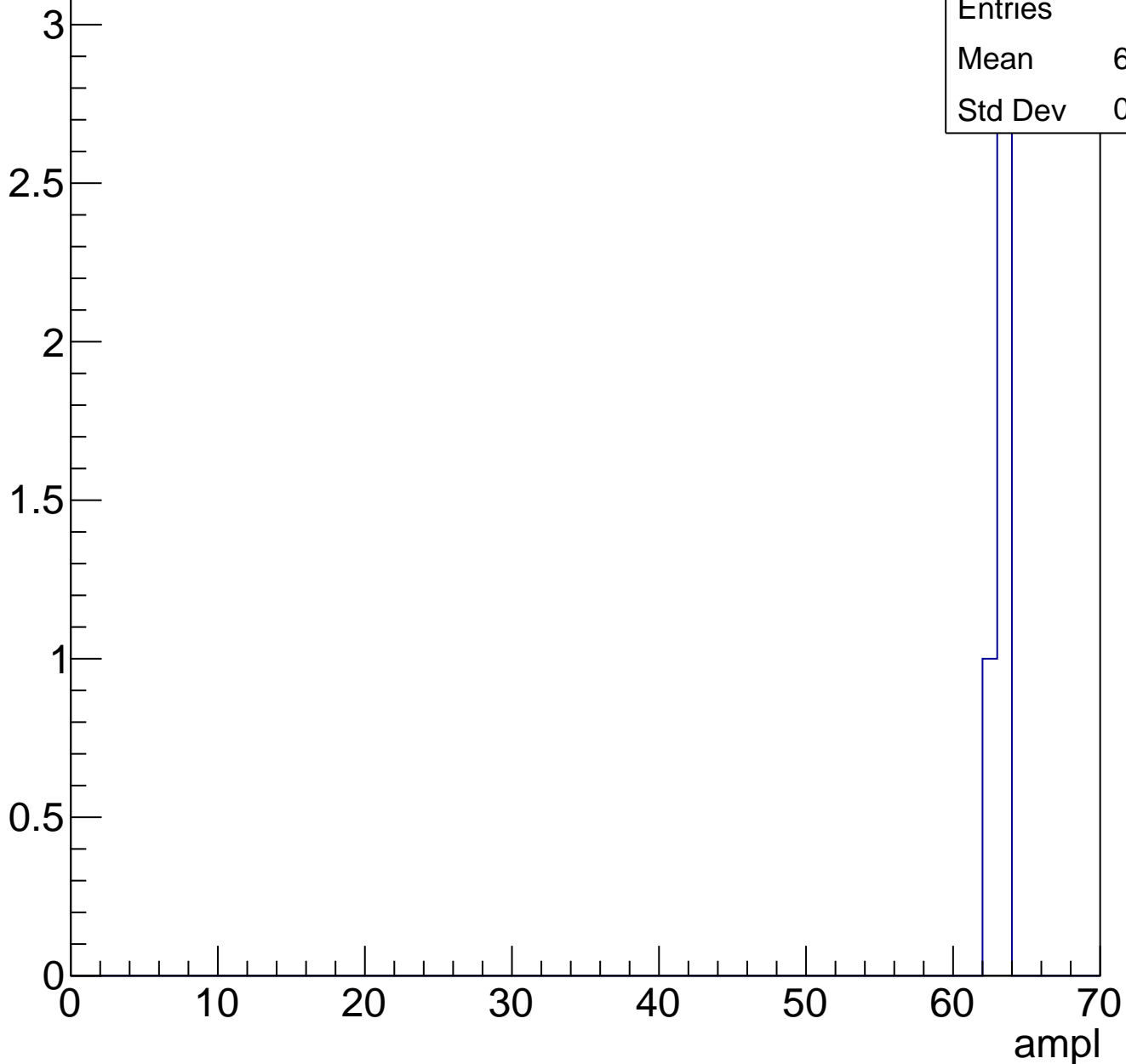




# B1L000S, U1-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch109, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	25.93
Std Dev	6.194

**Gaus mean : 27.2587**

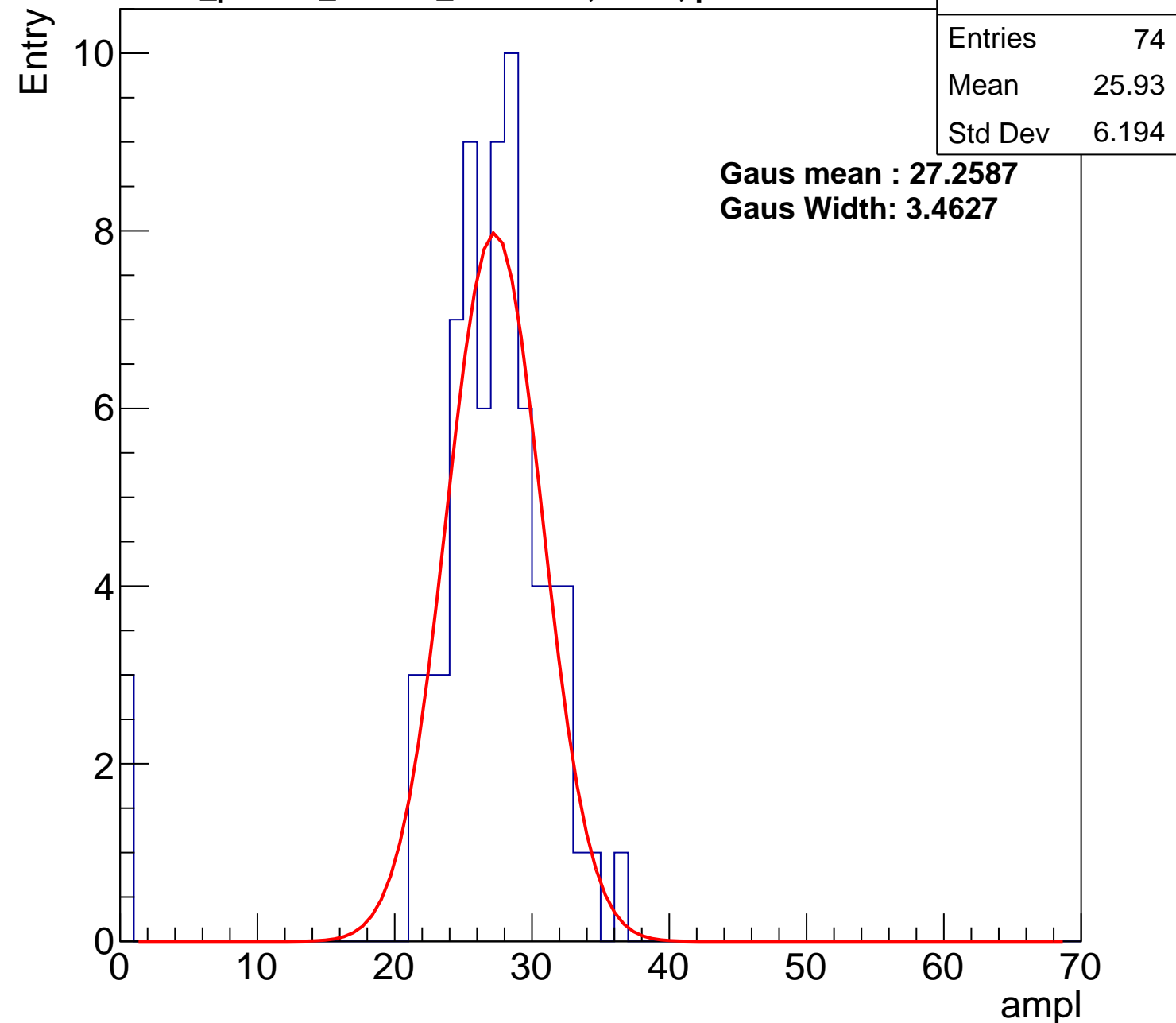
**Gaus Width: 3.4627**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch109, adc1

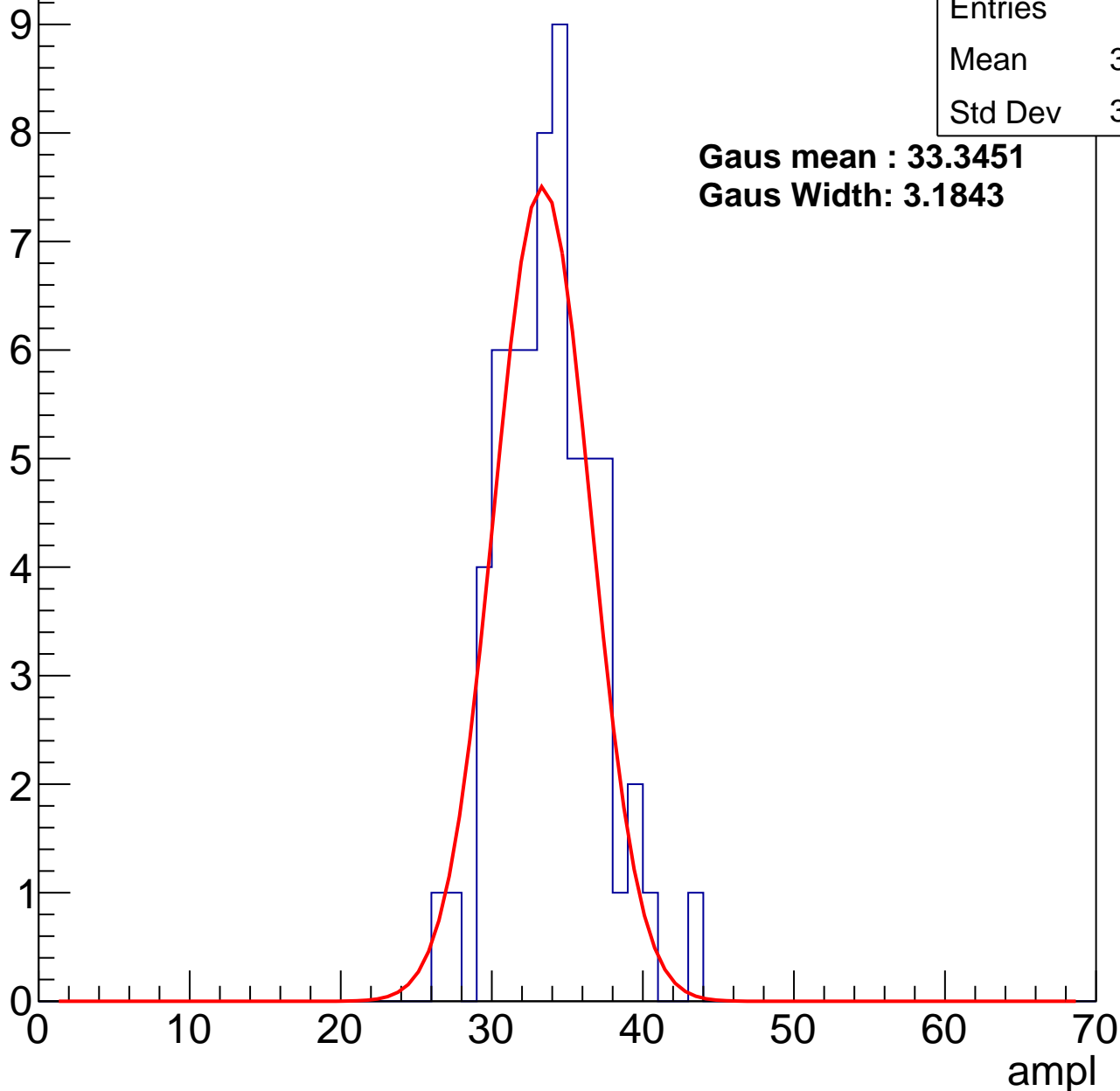
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	33.38
Std Dev	3.199

**Gaus mean : 33.3451**

**Gaus Width: 3.1843**



# B1L000S, U1-ch109, adc2

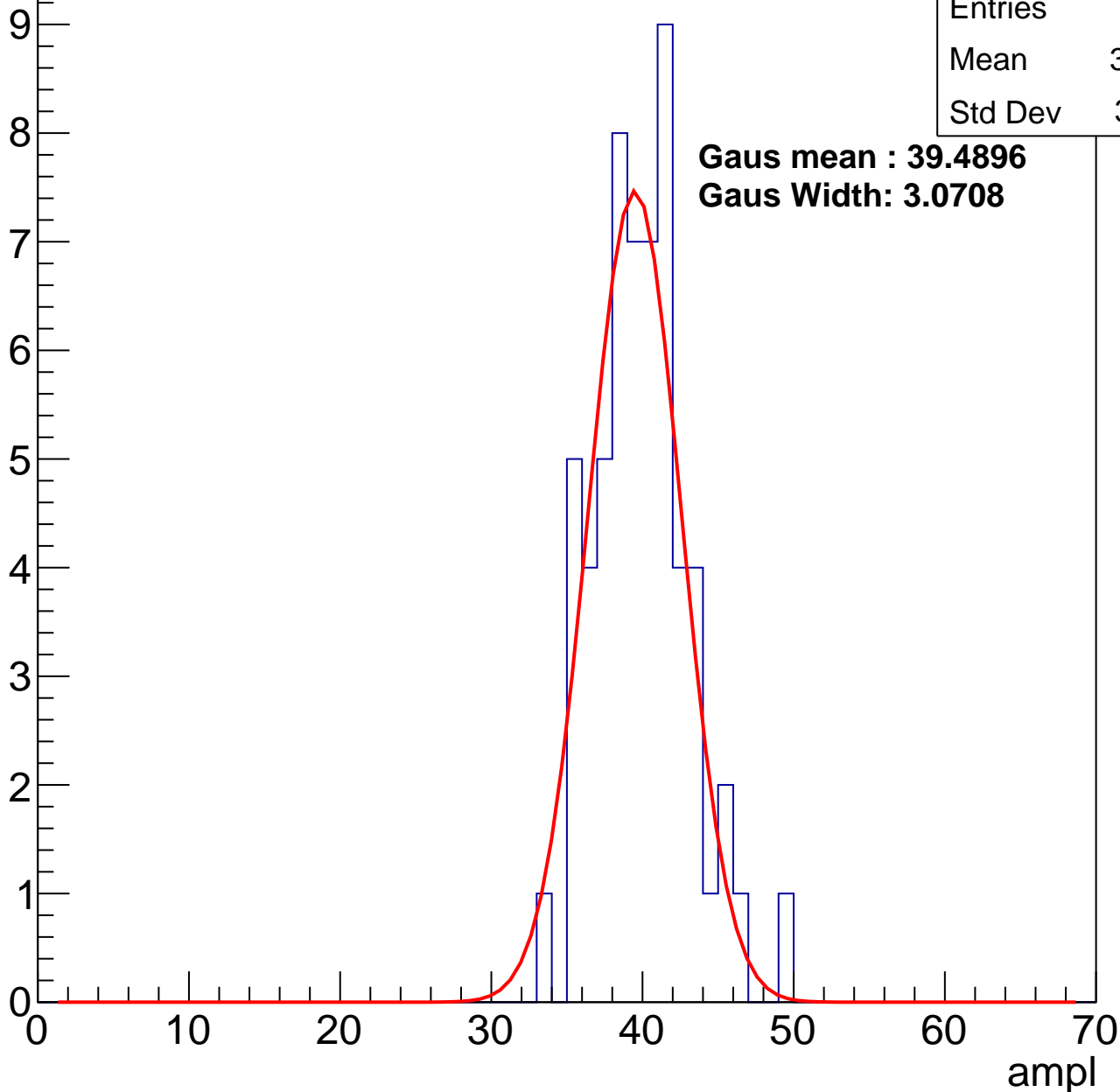
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	39.53
Std Dev	3.061

**Gaus mean : 39.4896**

**Gaus Width: 3.0708**

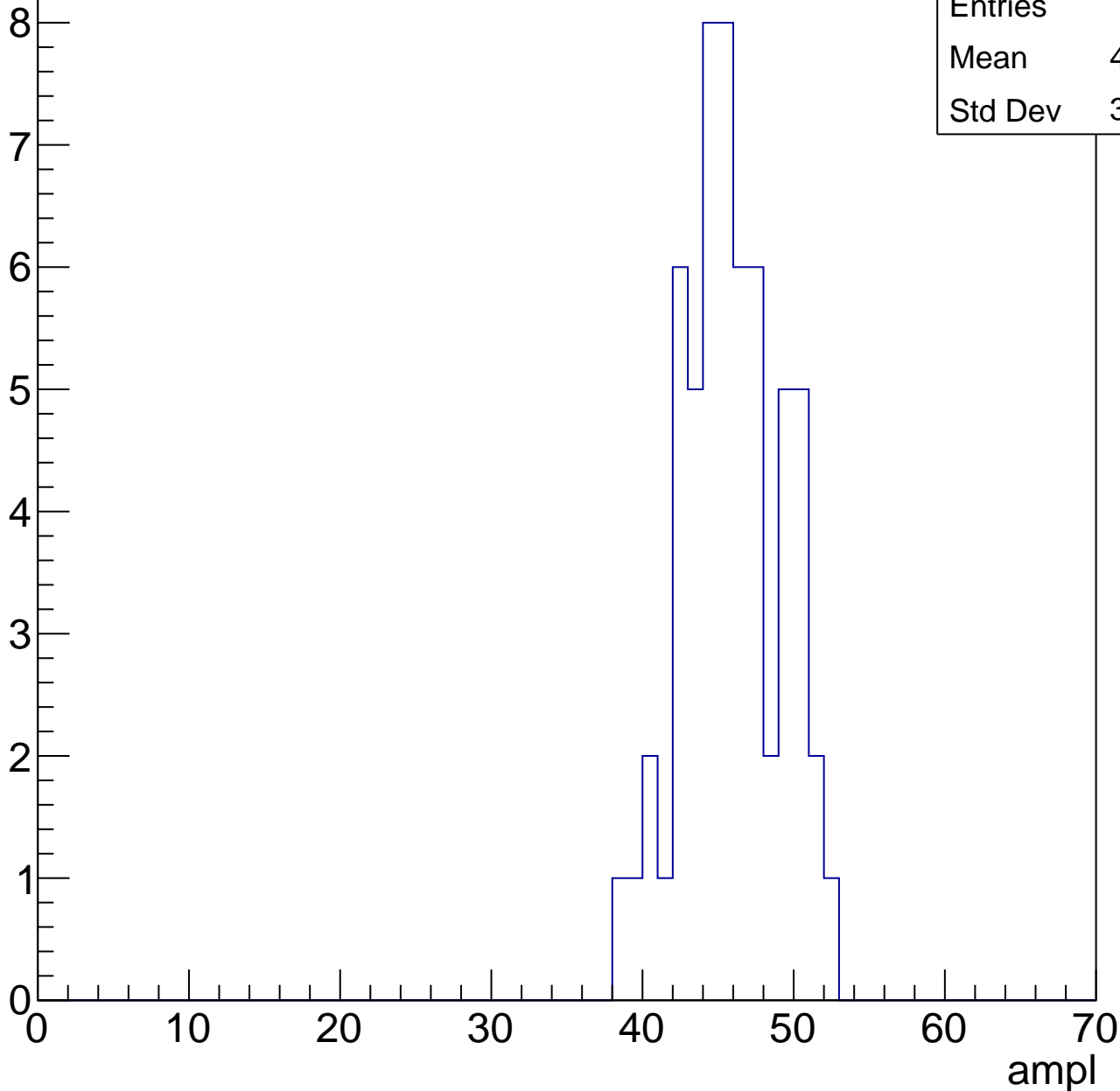


# B1L000S, U1-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	45.42
Std Dev	3.179

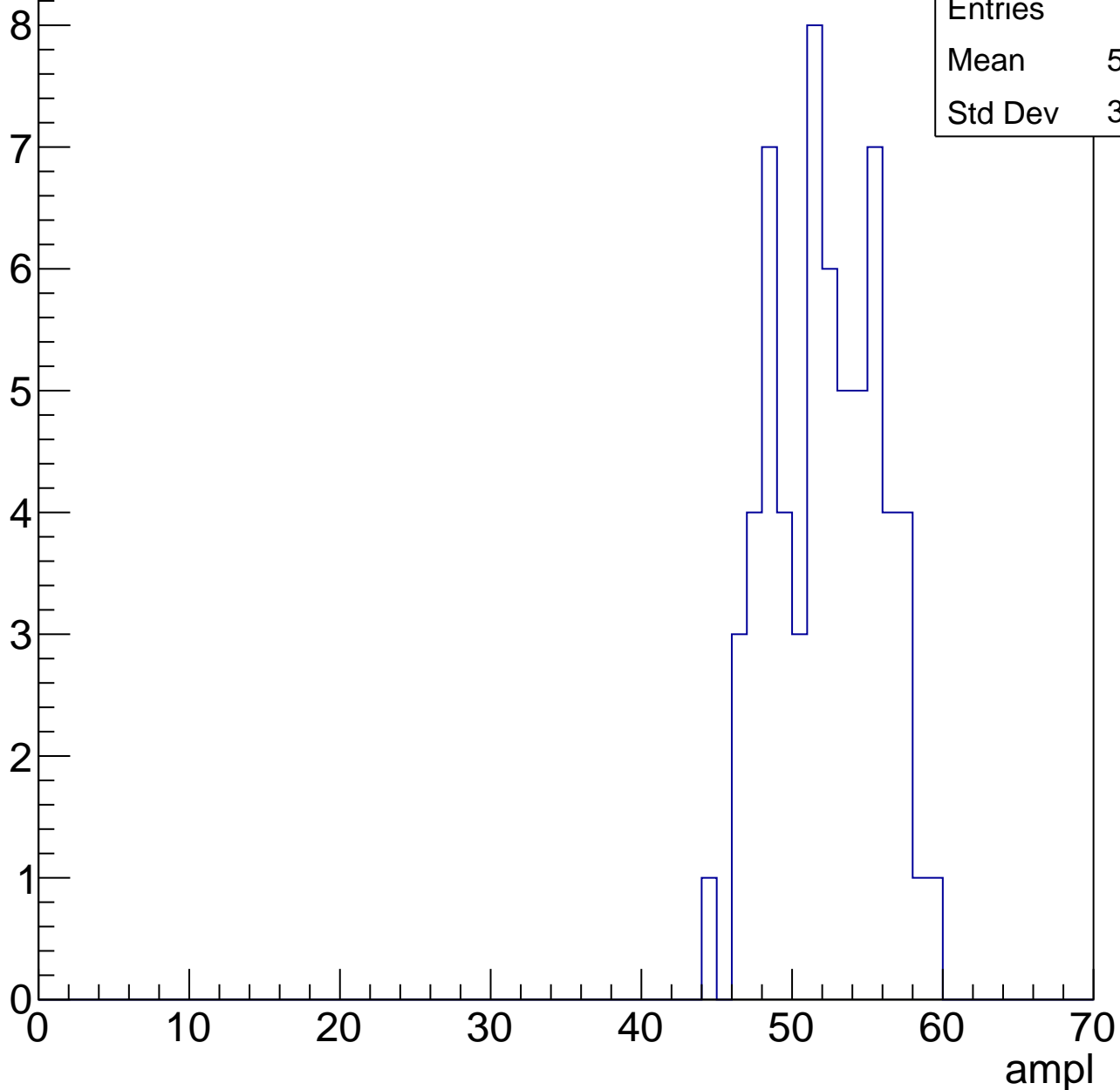


# B1L000S, U1-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	51.76
Std Dev	3.504

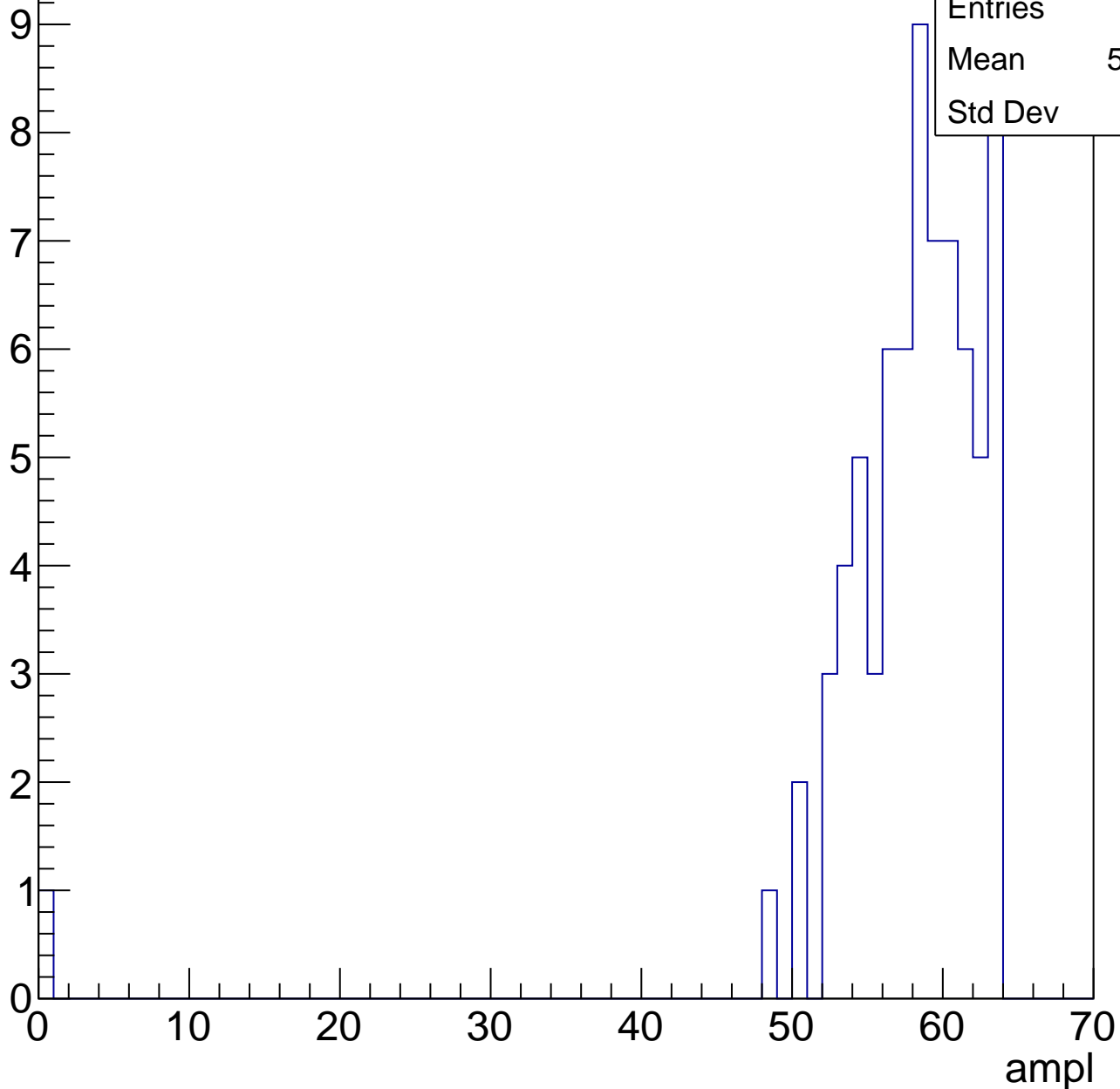


# B1L000S, U1-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	57.04
Std Dev	7.62

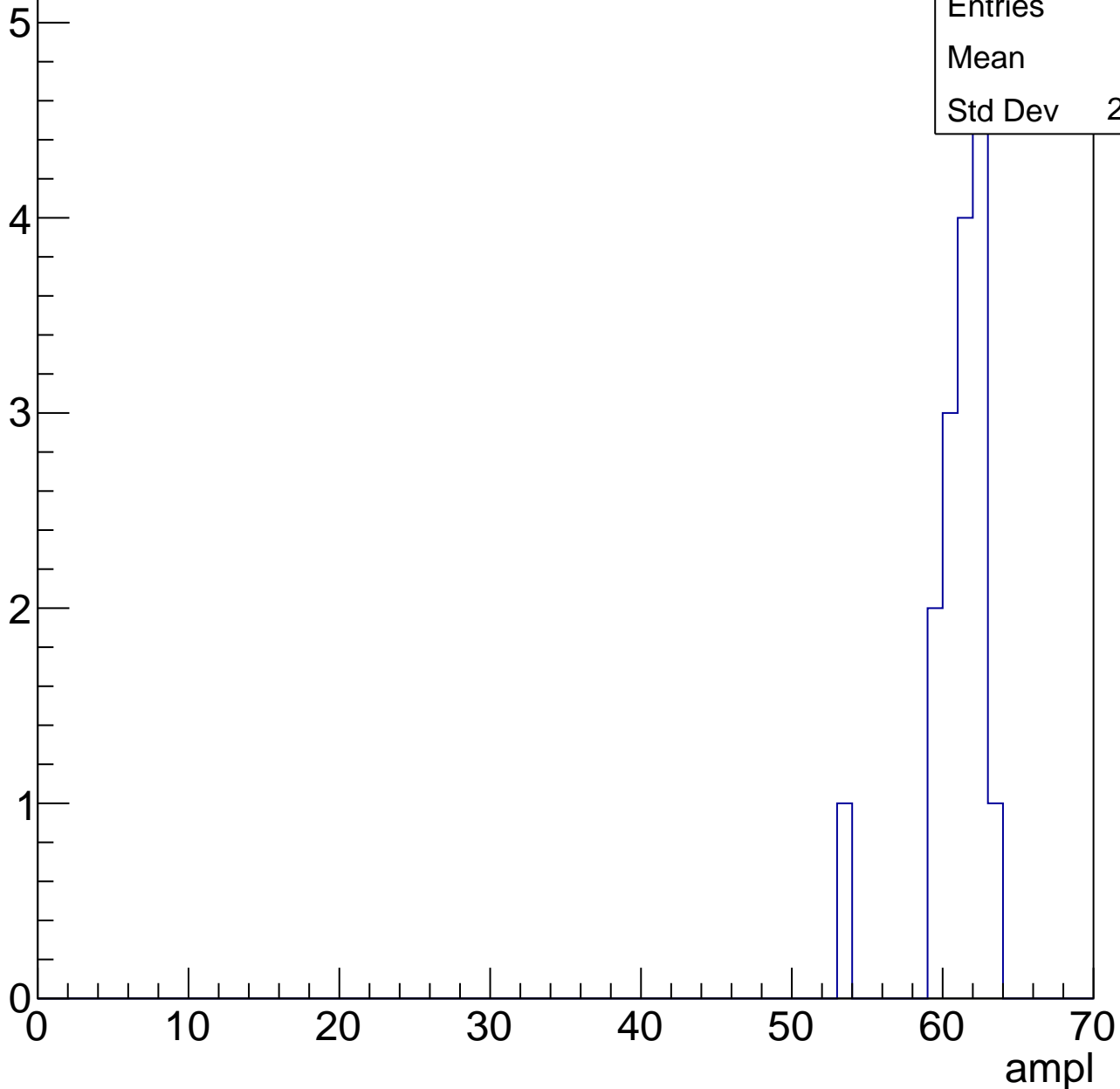


# B1L000S, U1-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	60.5
Std Dev	2.236

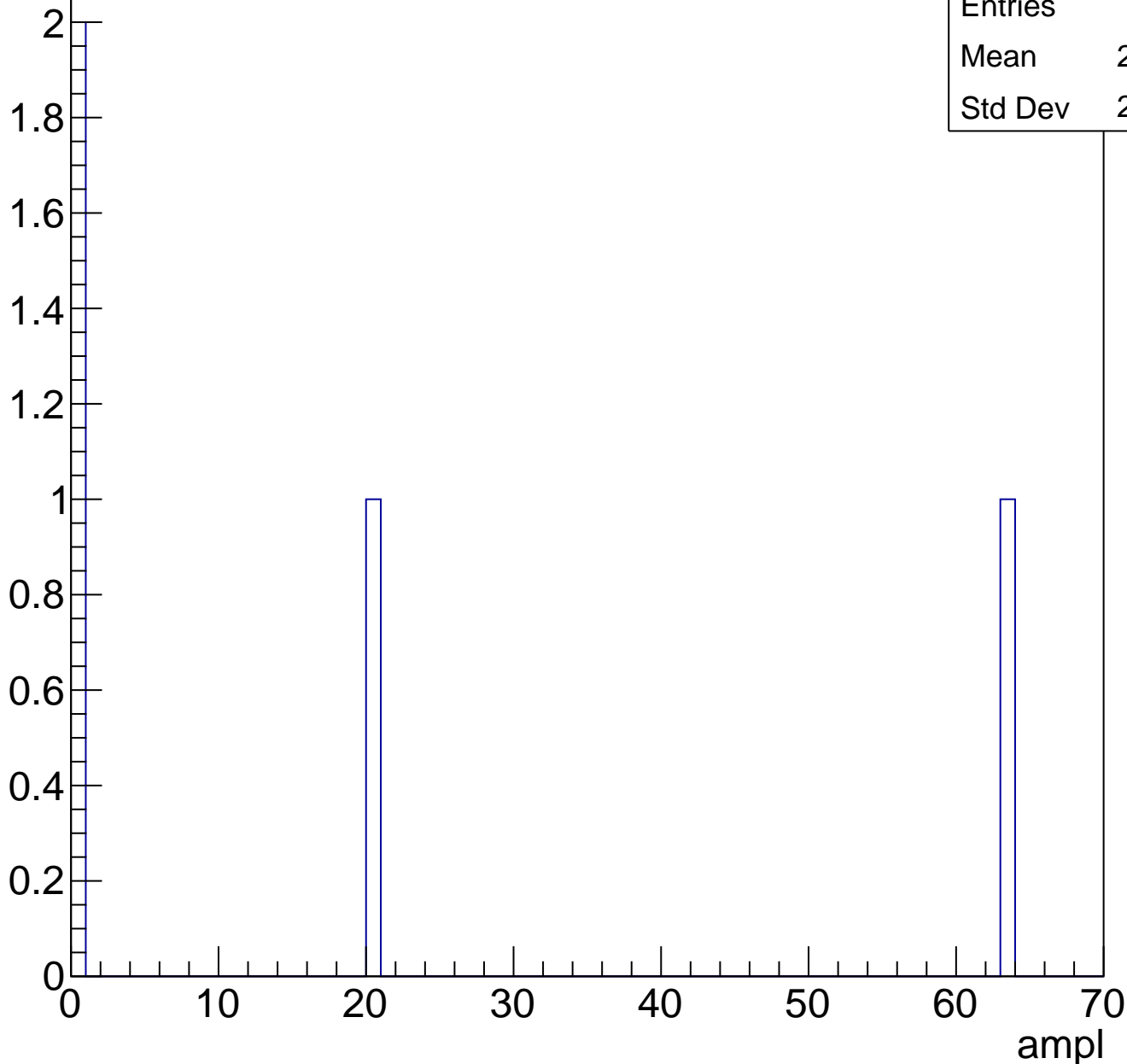




# B1L000S, U1-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	20.75
Std Dev	25.72

# B1L000S, U1-ch110, adc0

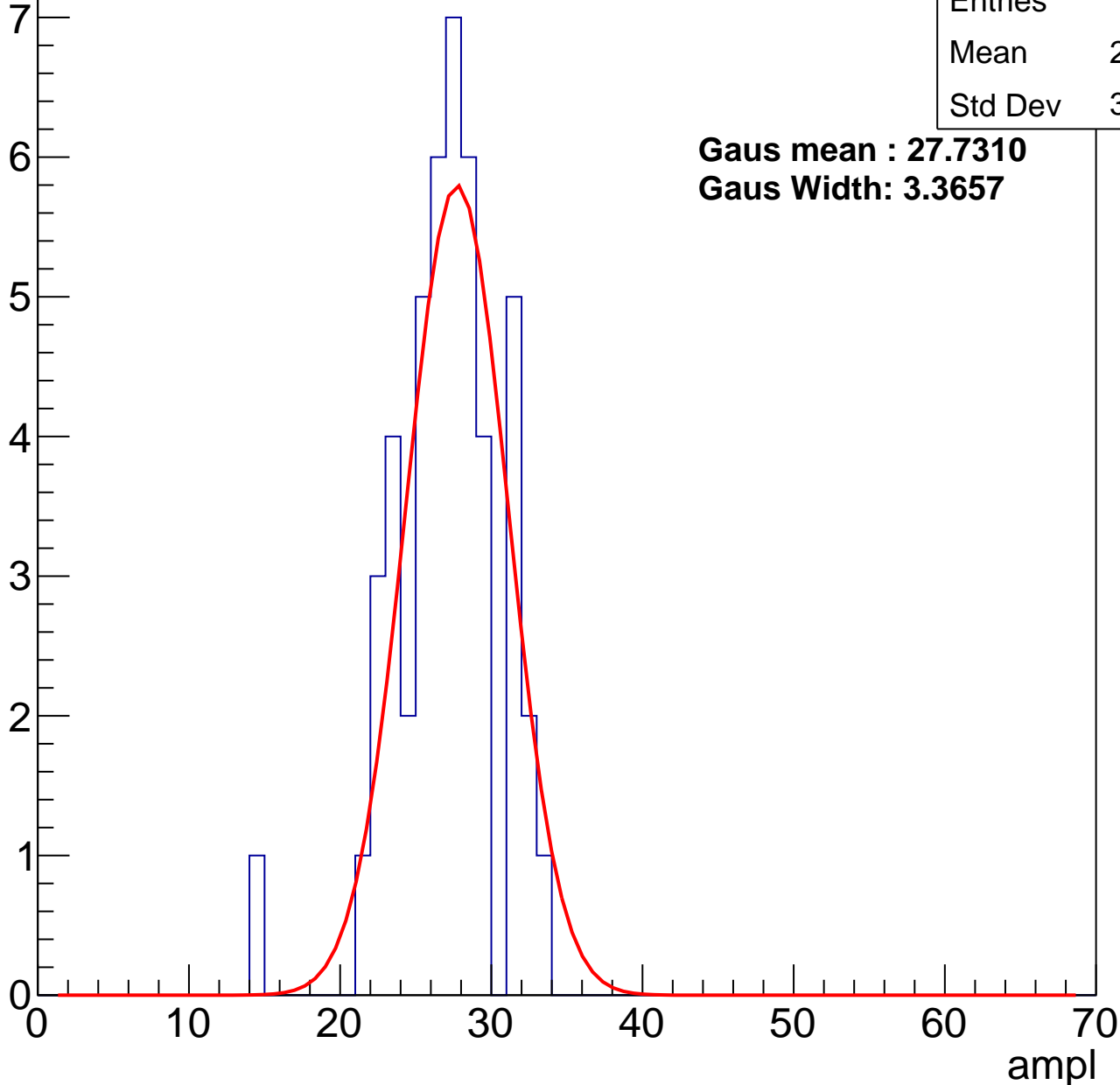
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	26.53
Std Dev	3.469

**Gaus mean : 27.7310**

**Gaus Width: 3.3657**



# B1L000S, U1-ch110, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	88
Mean	32.77
Std Dev	4.025

**Gaus mean : 33.4447**

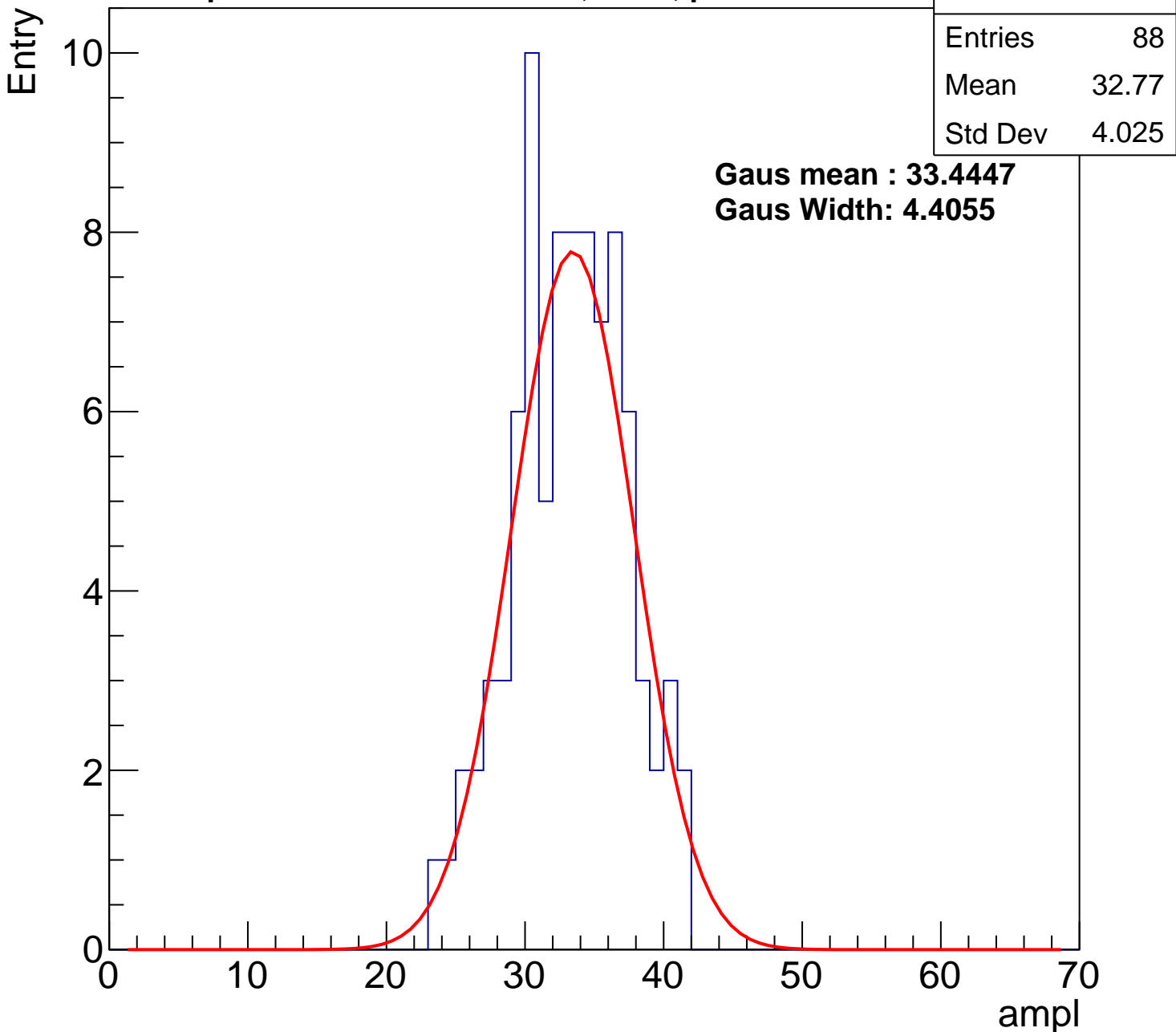
**Gaus Width: 4.4055**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch110, adc2

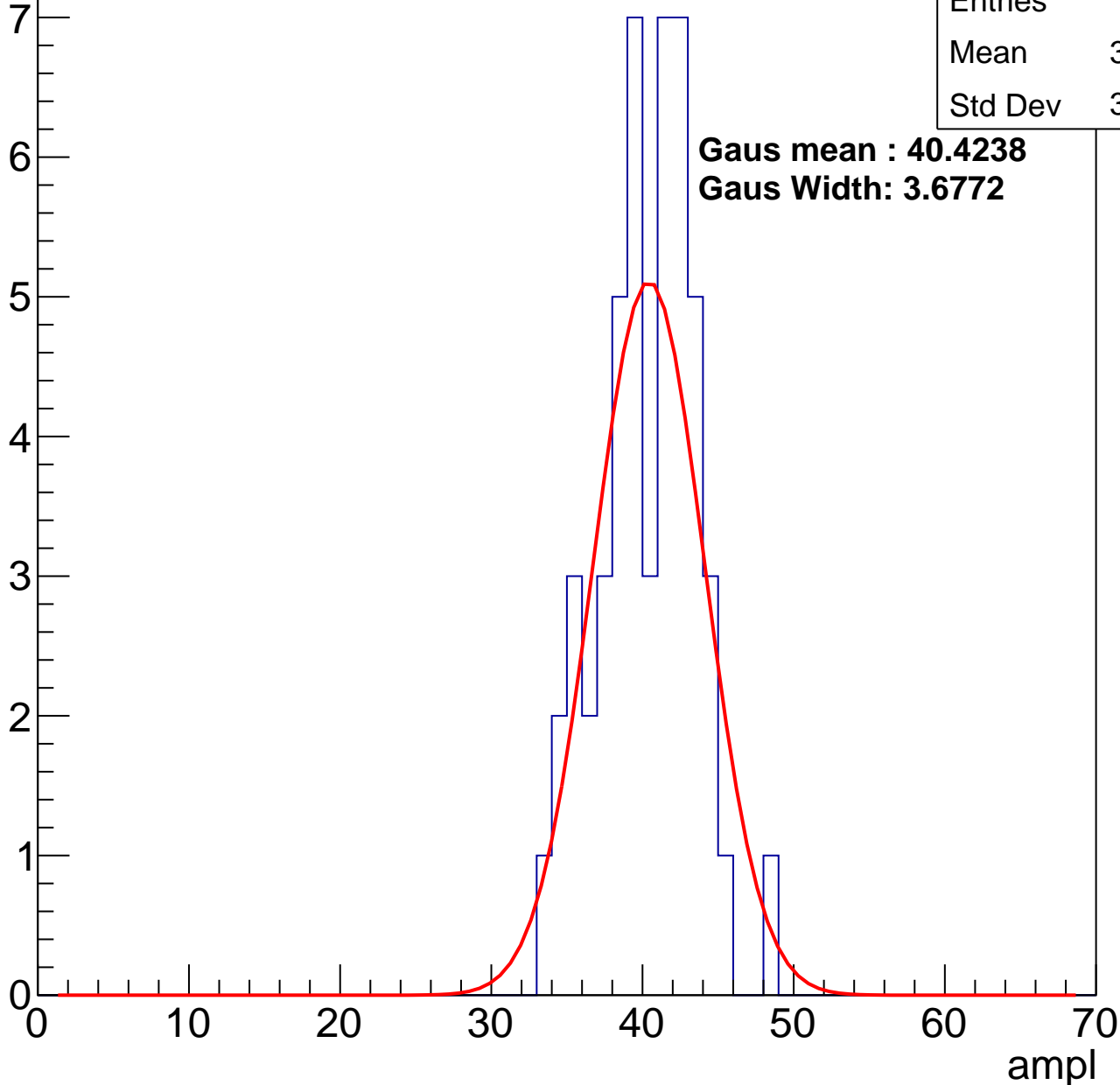
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	39.86
Std Dev	3.156

**Gaus mean : 40.4238**

**Gaus Width: 3.6772**

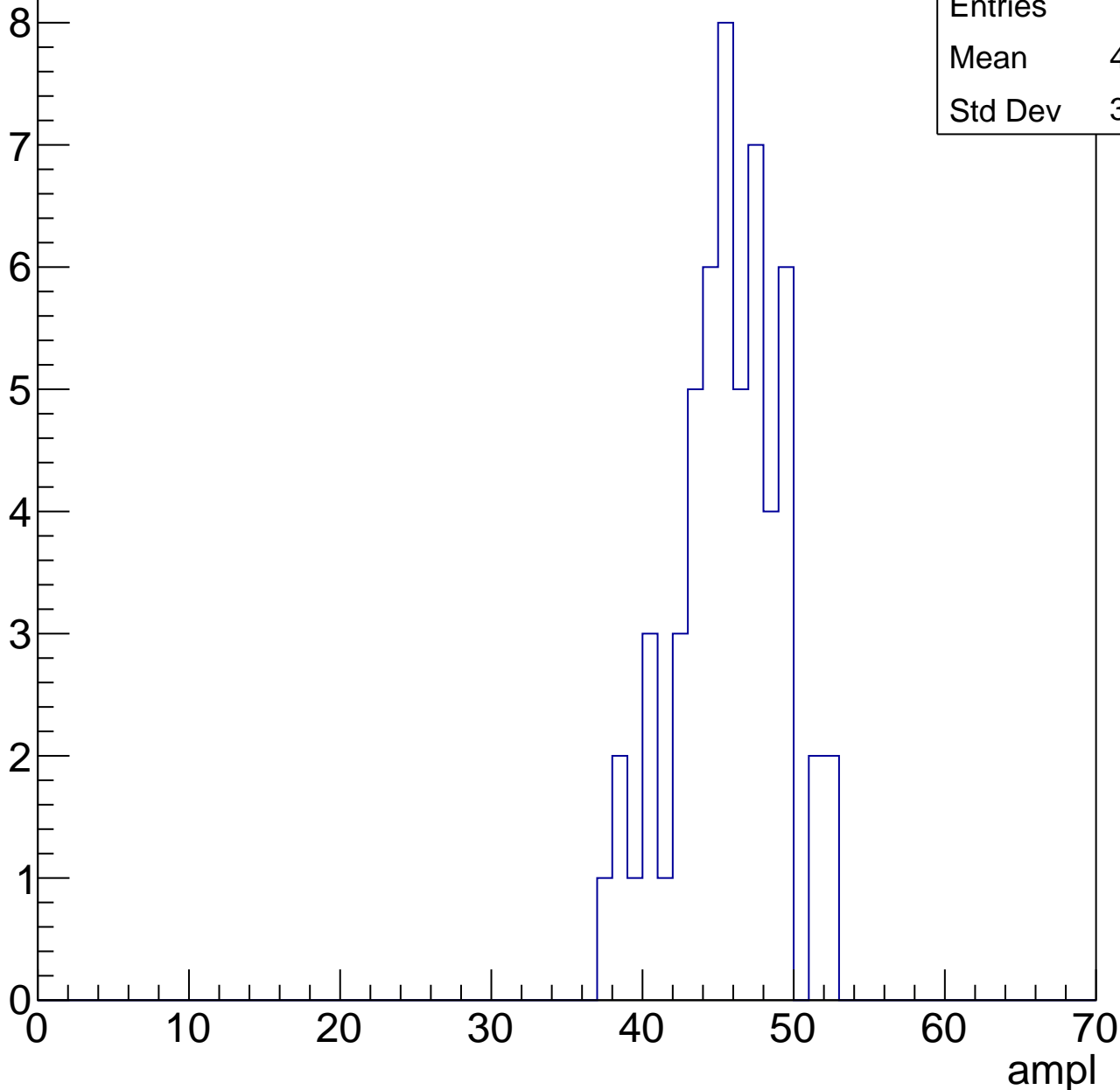


# B1L000S, U1-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	45.16
Std Dev	3.473

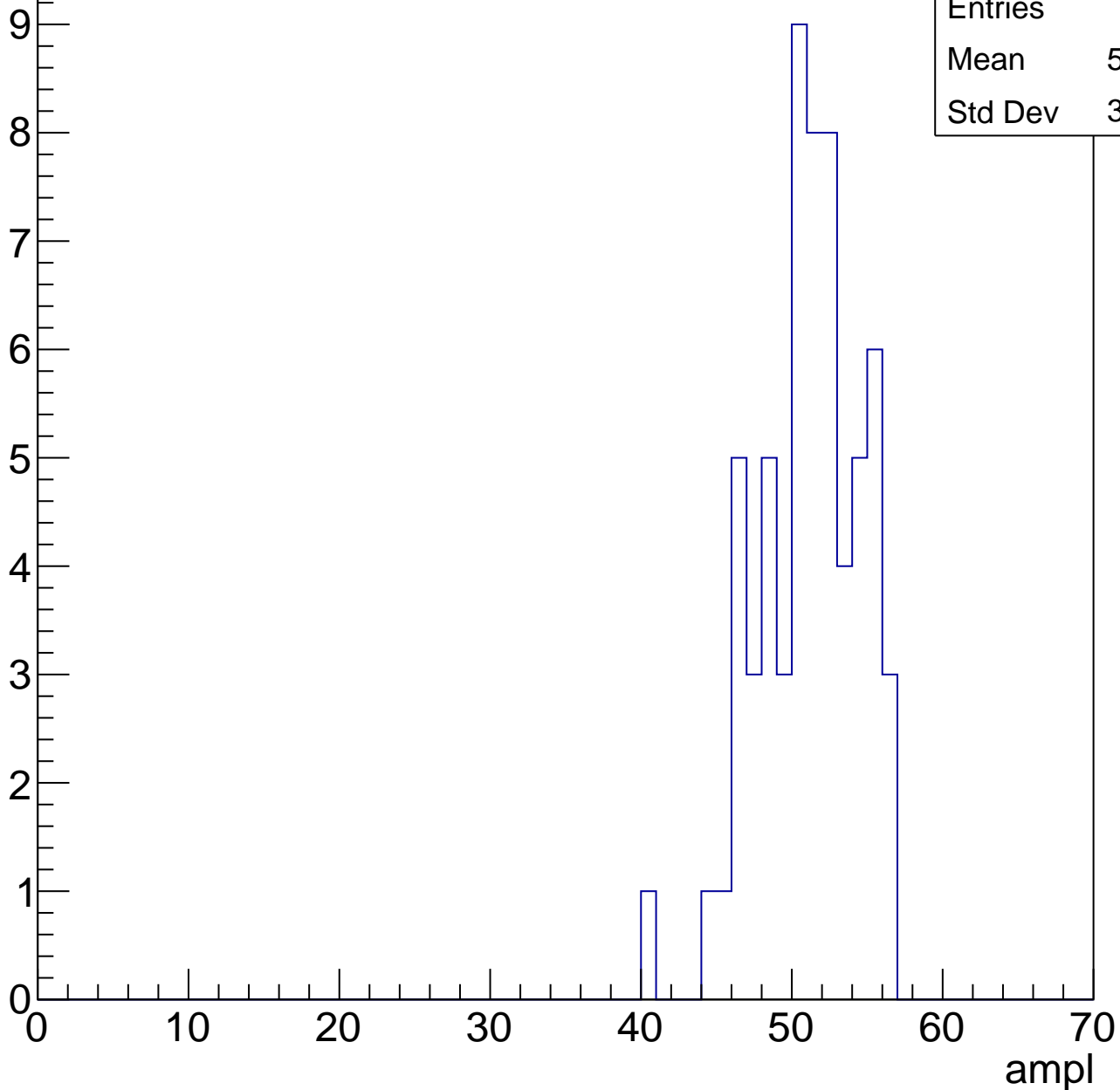


# B1L000S, U1-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	50.66
Std Dev	3.307



# B1L000S, U1-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

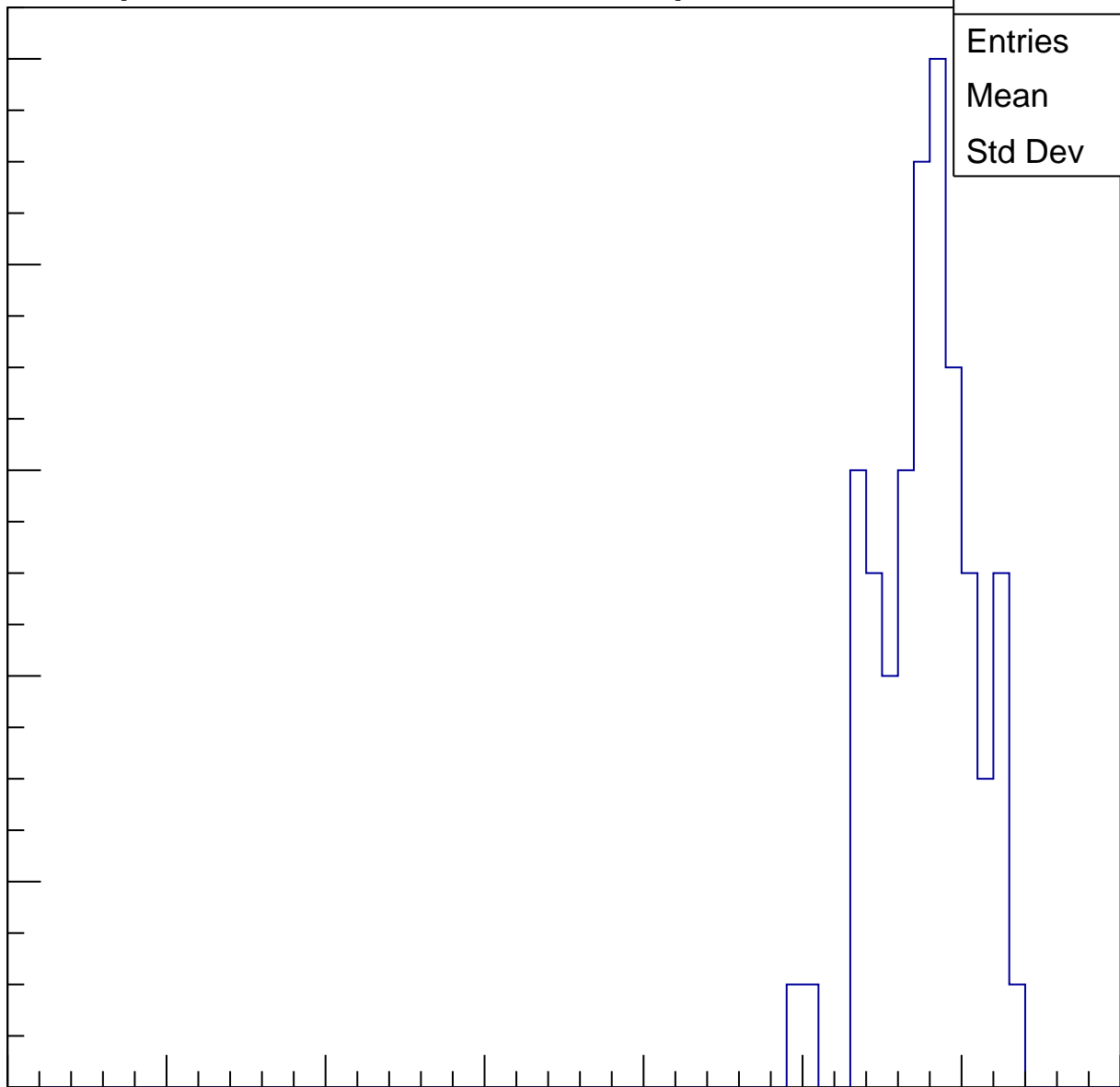
Entries	63
Mean	57.22
Std Dev	2.994

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

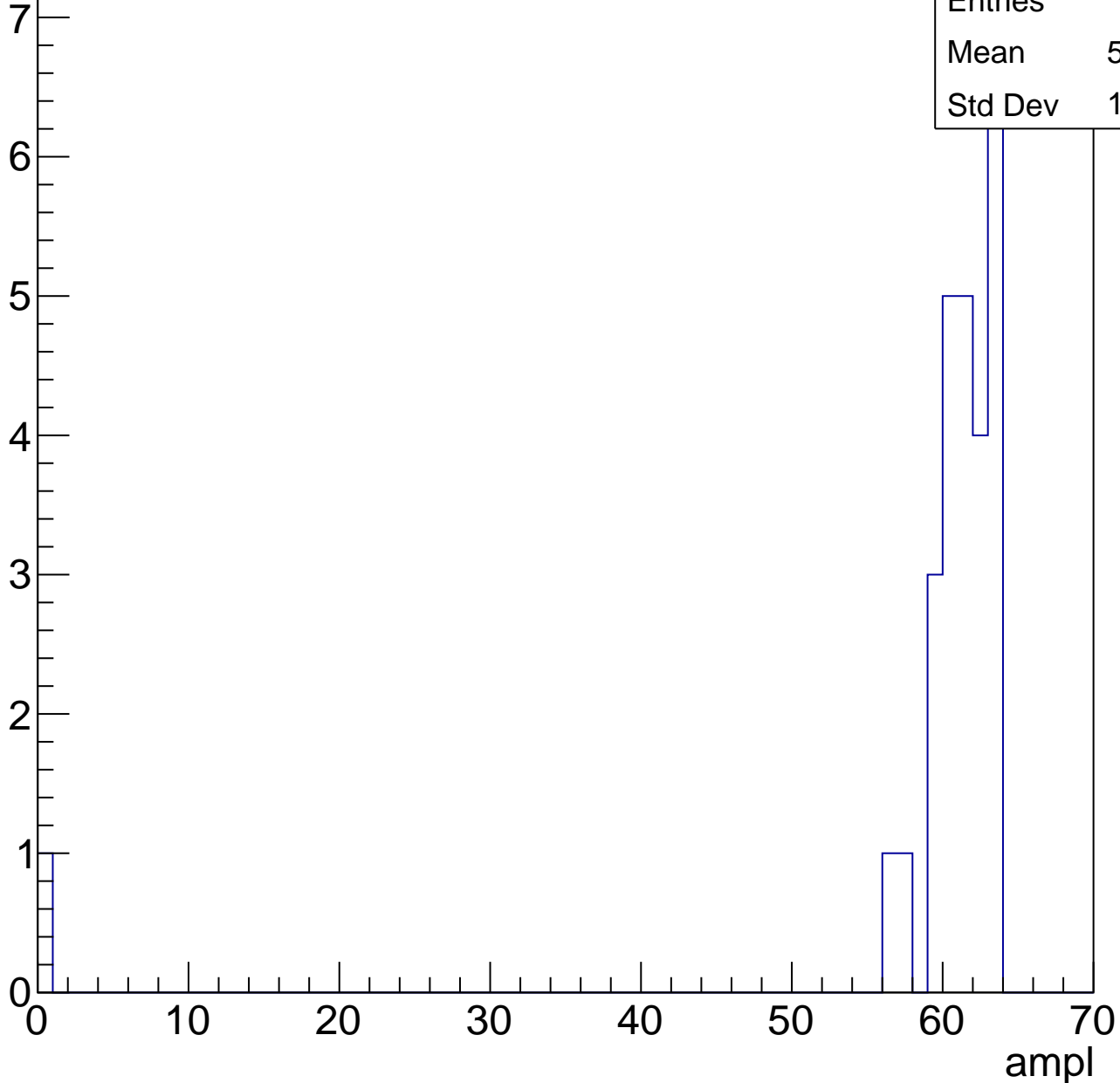


# B1L000S, U1-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	58.67
Std Dev	11.65

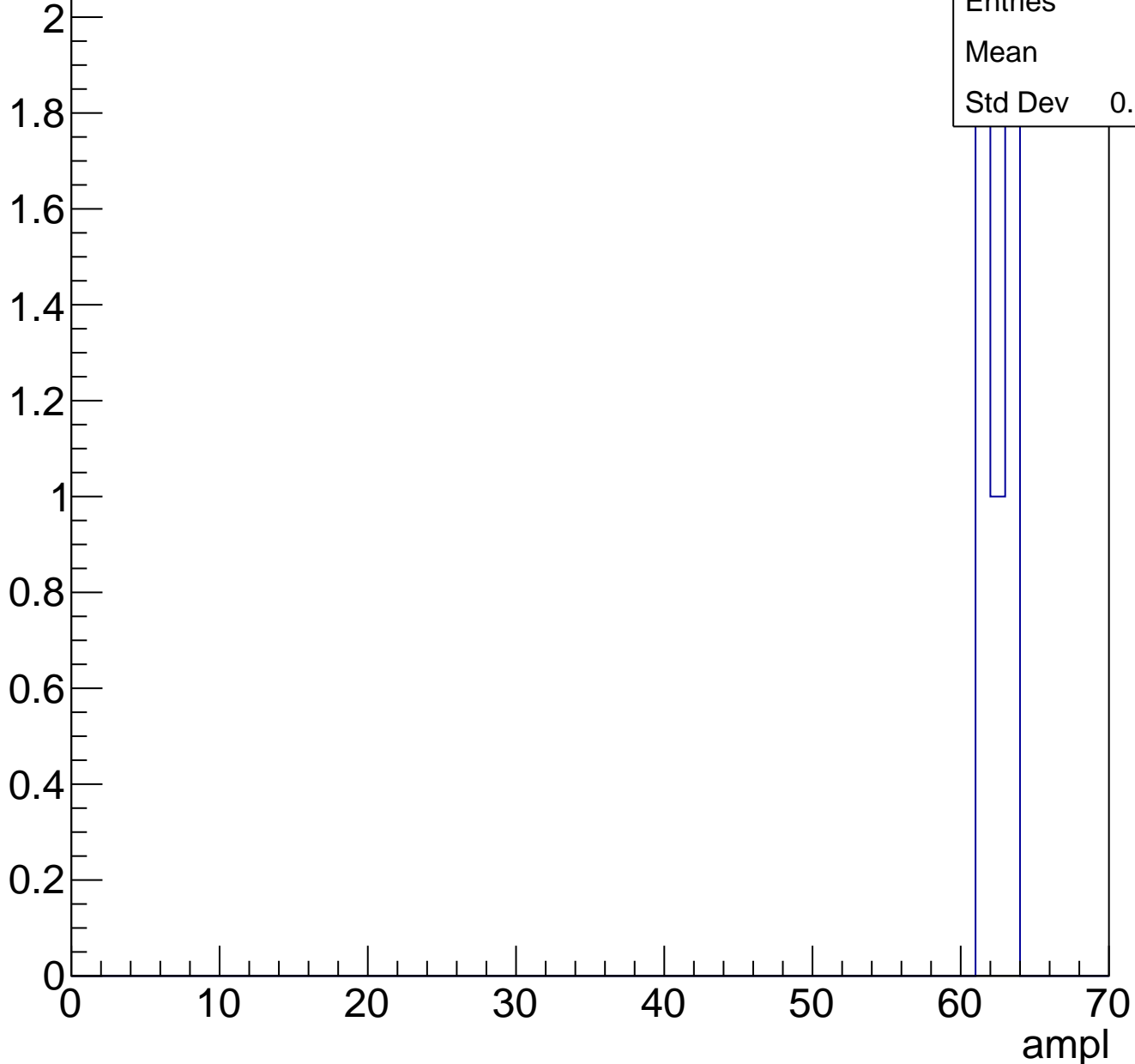




# B1L000S, U1-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch111, adc0

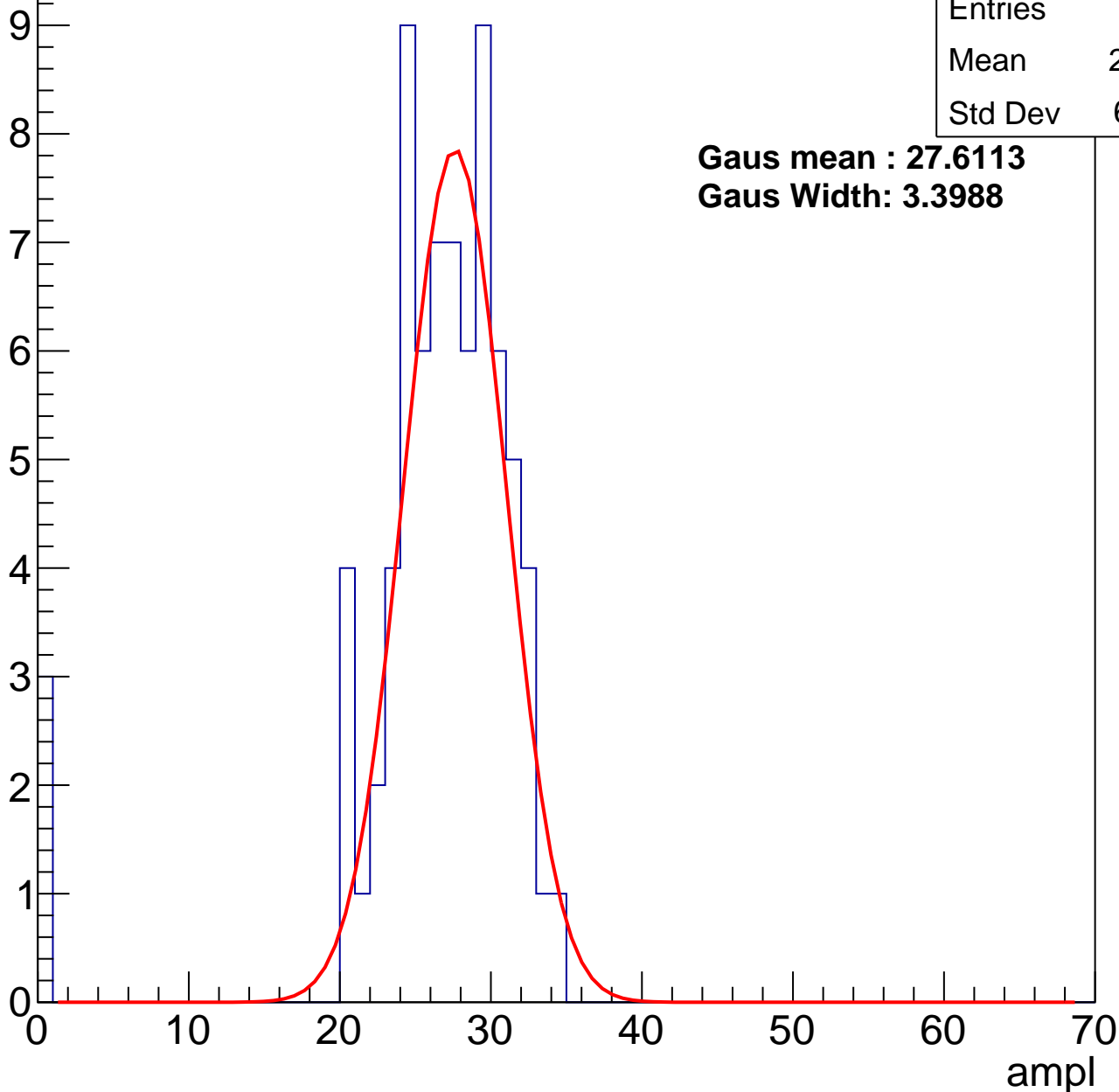
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	25.77
Std Dev	6.211

**Gaus mean : 27.6113**

**Gaus Width: 3.3988**



# B1L000S, U1-ch111, adc1

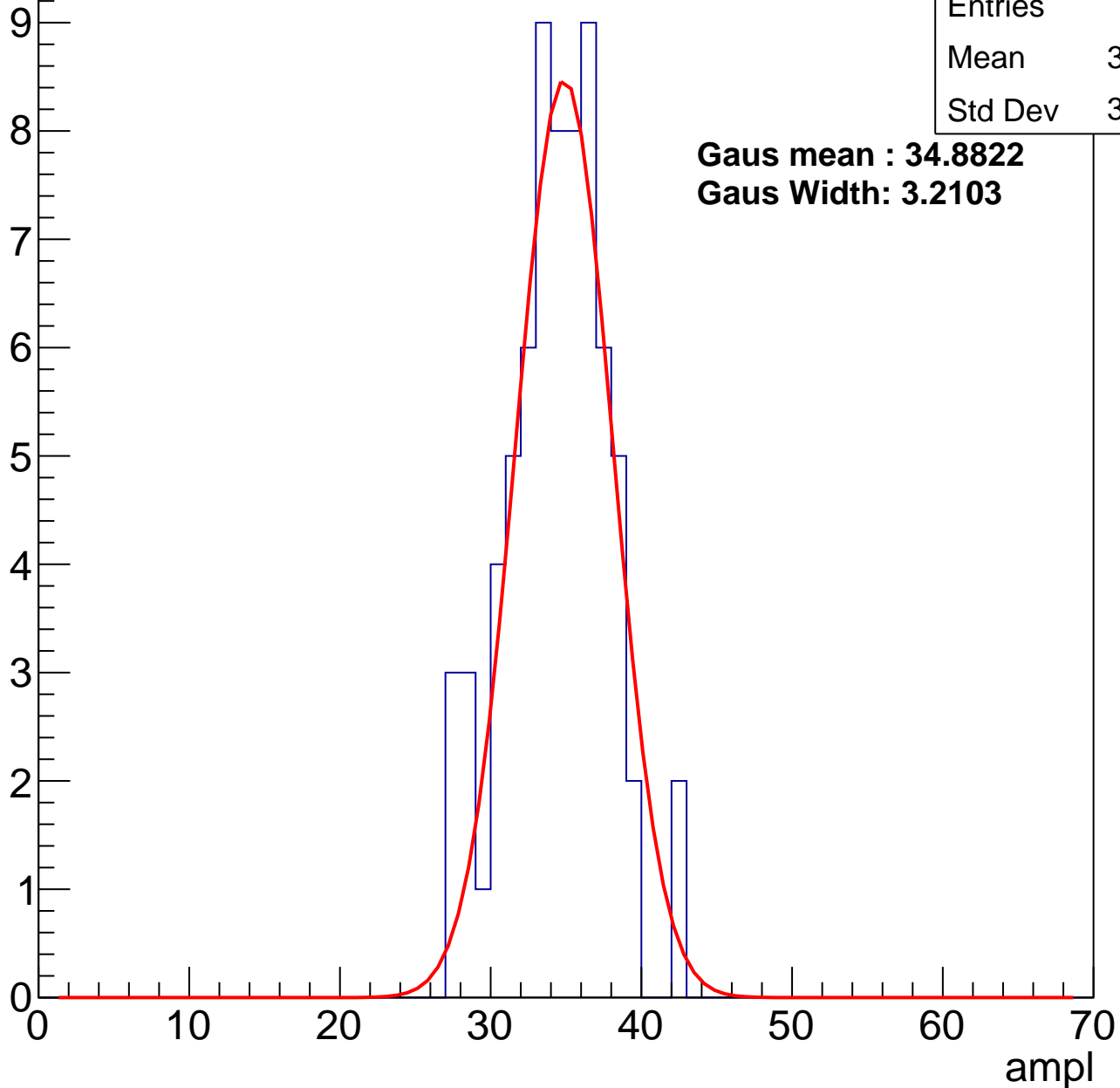
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	33.92
Std Dev	3.309

**Gaus mean : 34.8822**

**Gaus Width: 3.2103**



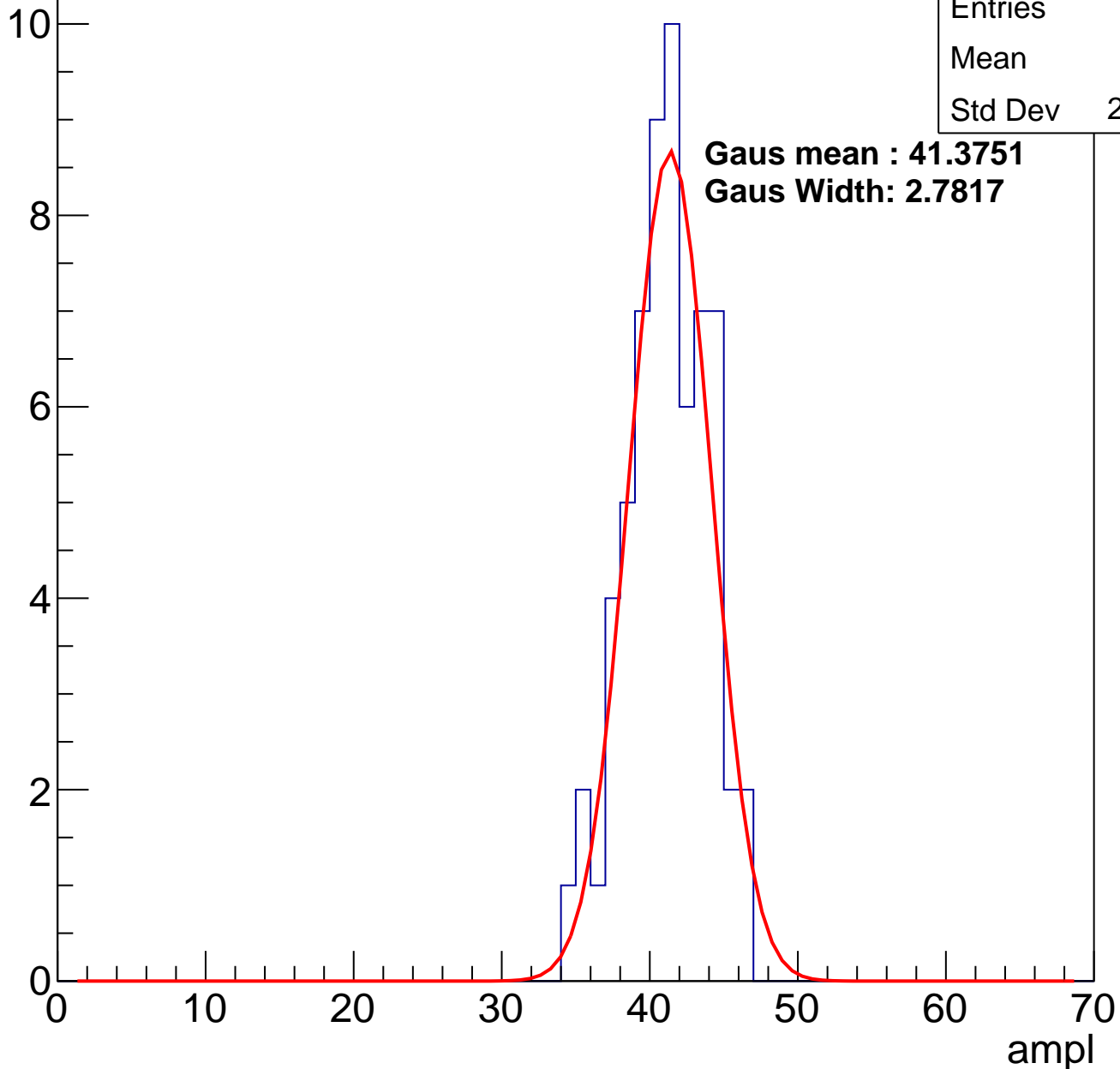
# B1L000S, U1-ch111, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	40.7
Std Dev	2.718

**Gaus mean : 41.3751**  
**Gaus Width: 2.7817**

Entry

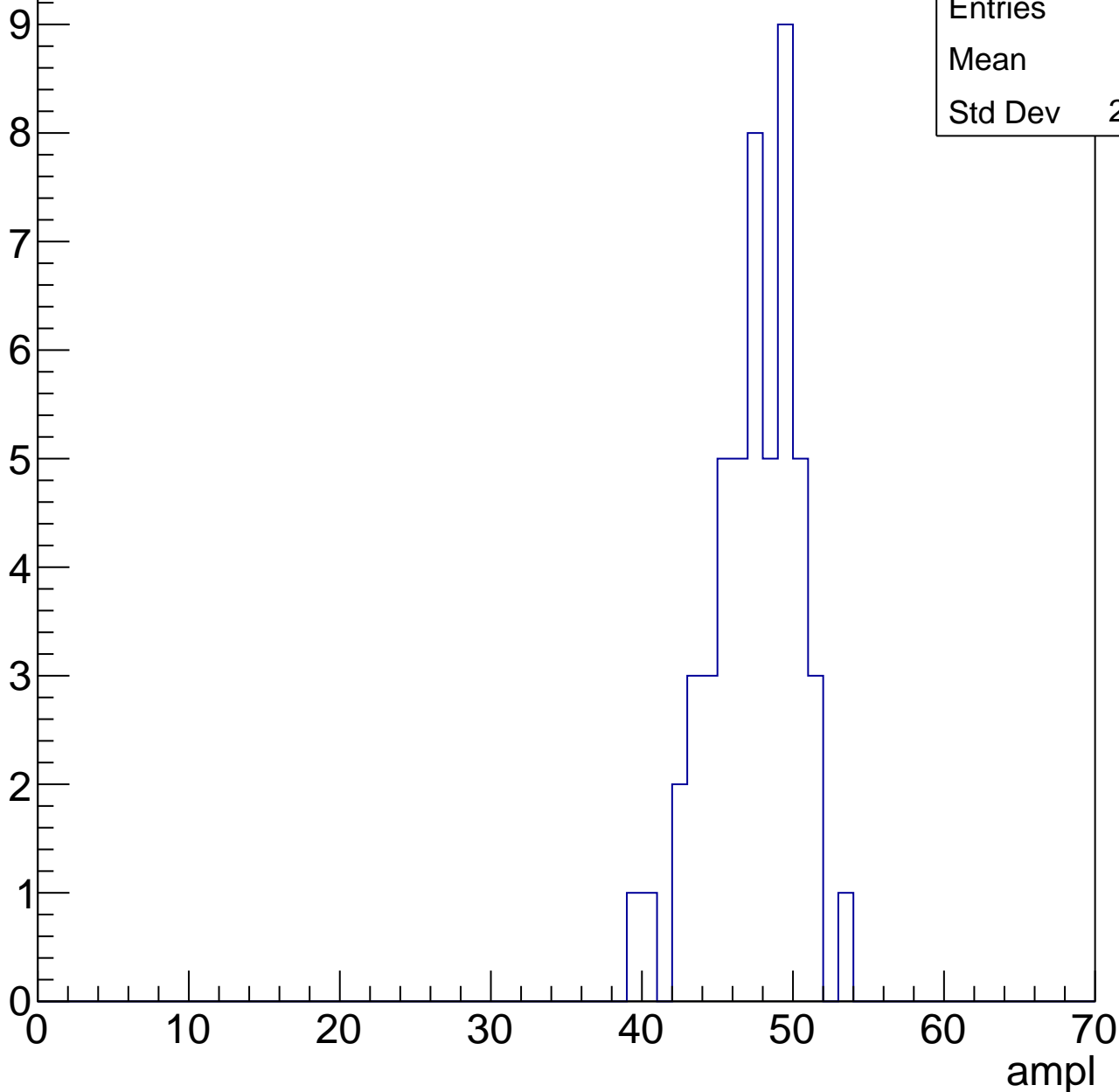


# B1L000S, U1-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	46.9
Std Dev	2.912

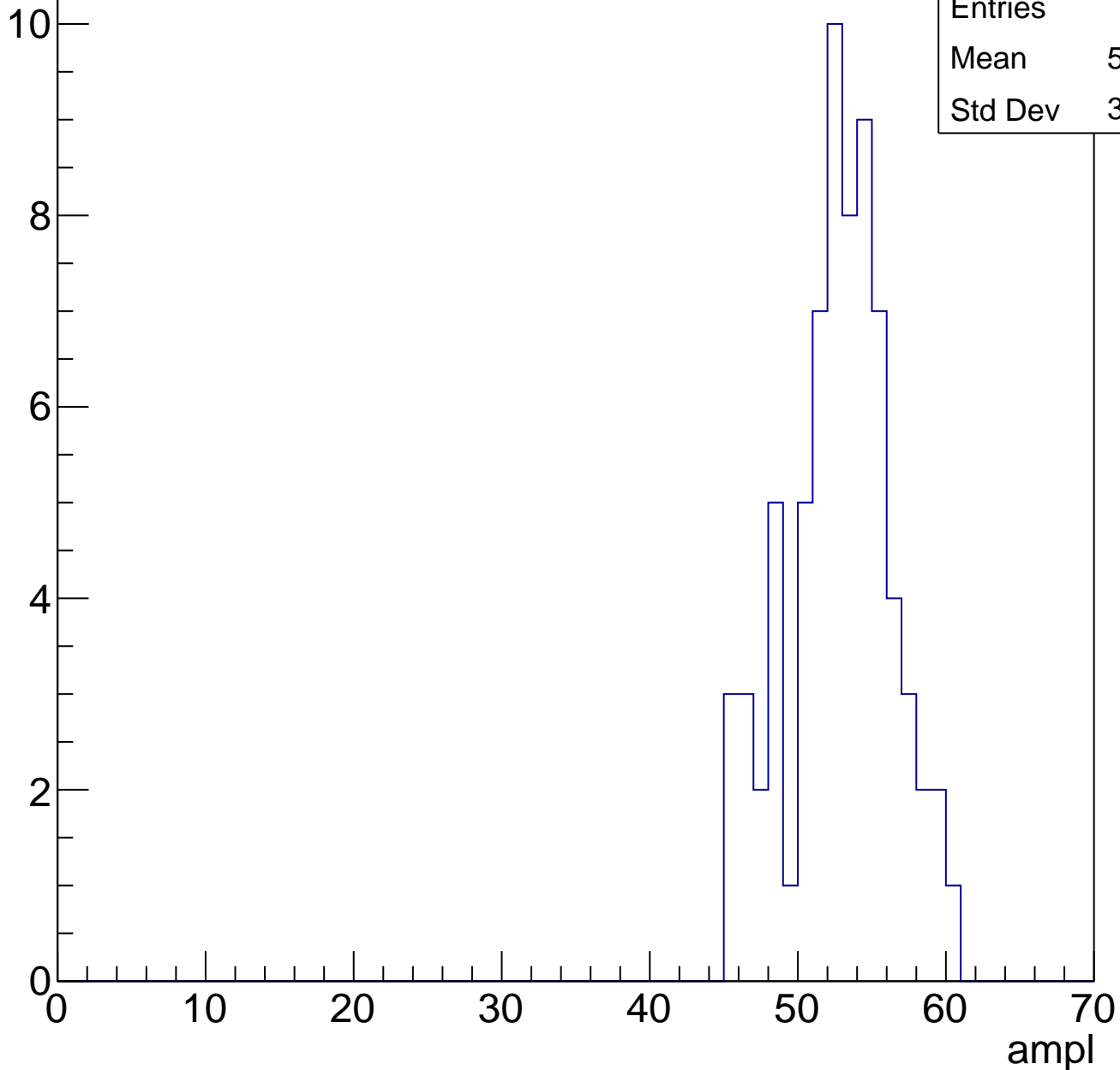


# B1L000S, U1-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	52.32
Std Dev	3.523

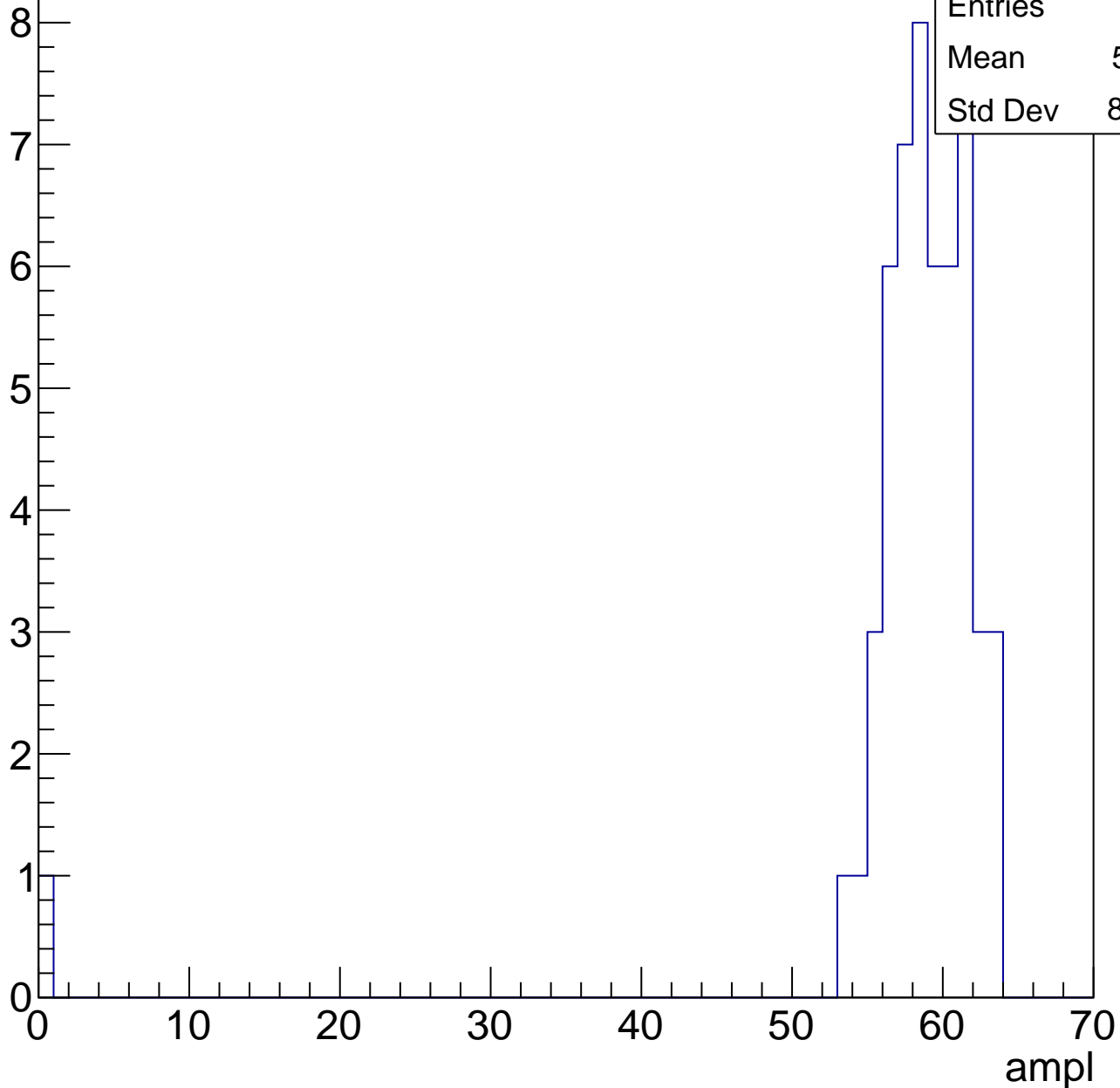
Entry



# B1L000S, U1-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

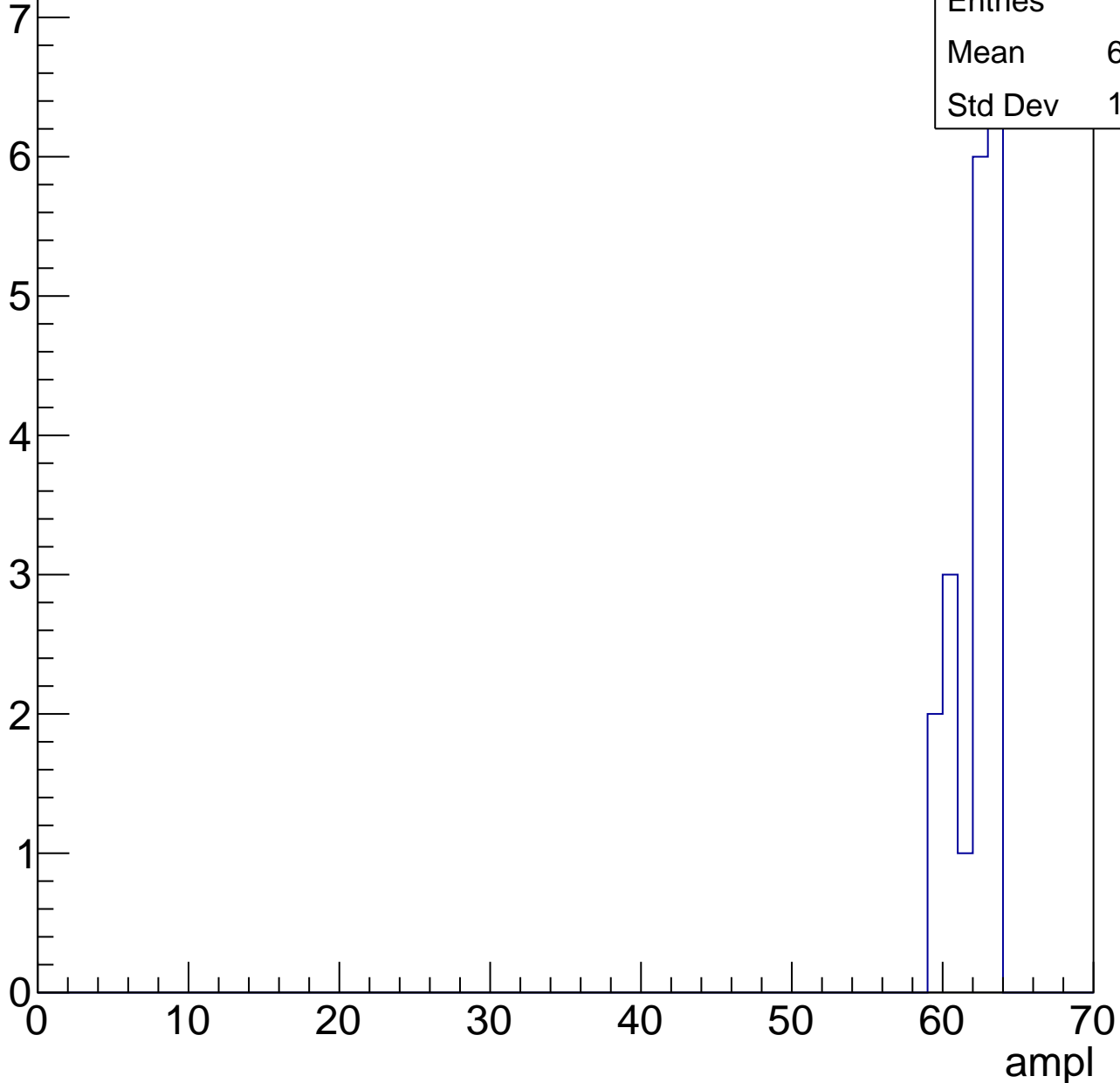


# B1L000S, U1-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	61.68
Std Dev	1.379

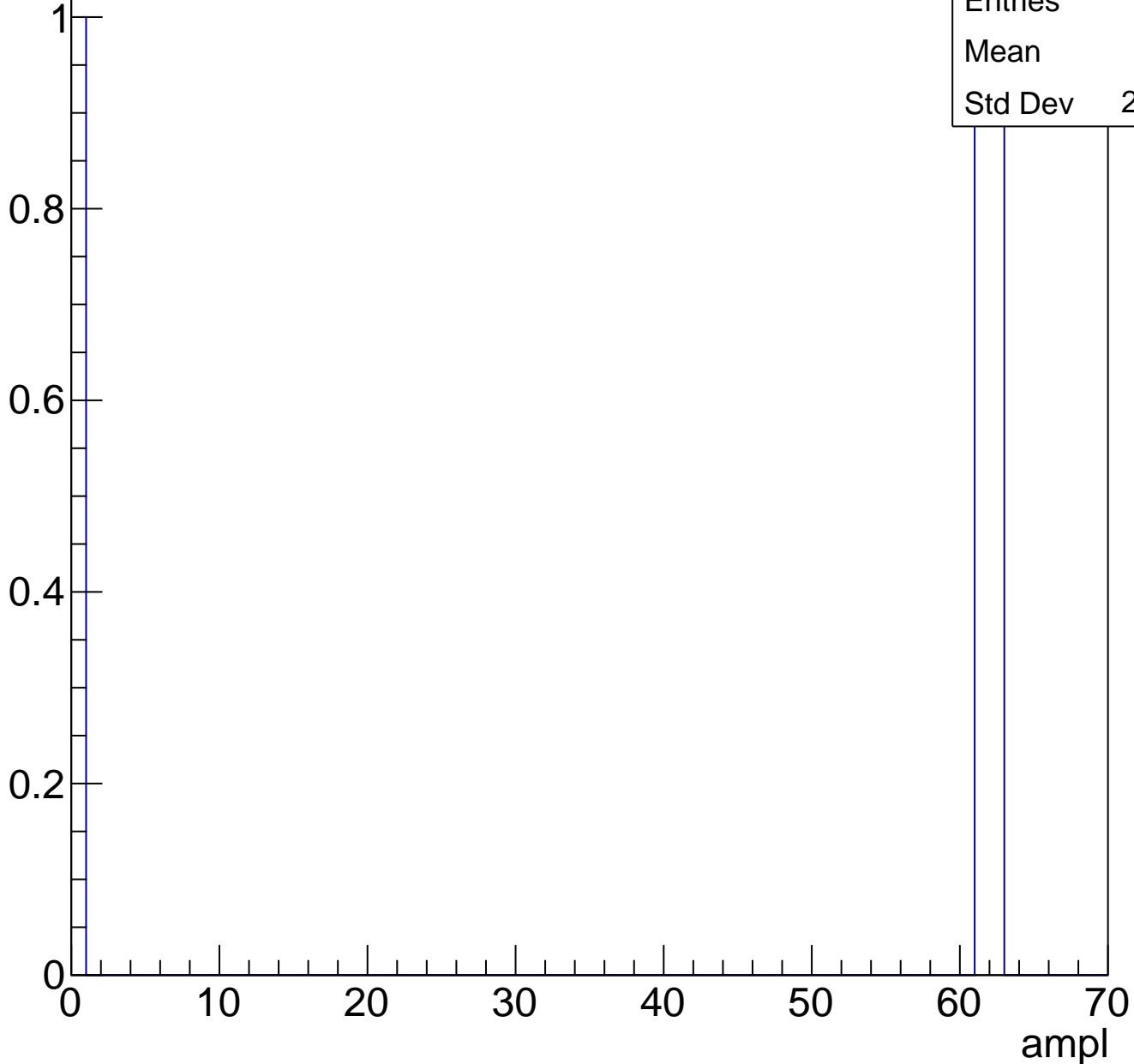




# B1L000S, U1-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch112, adc0

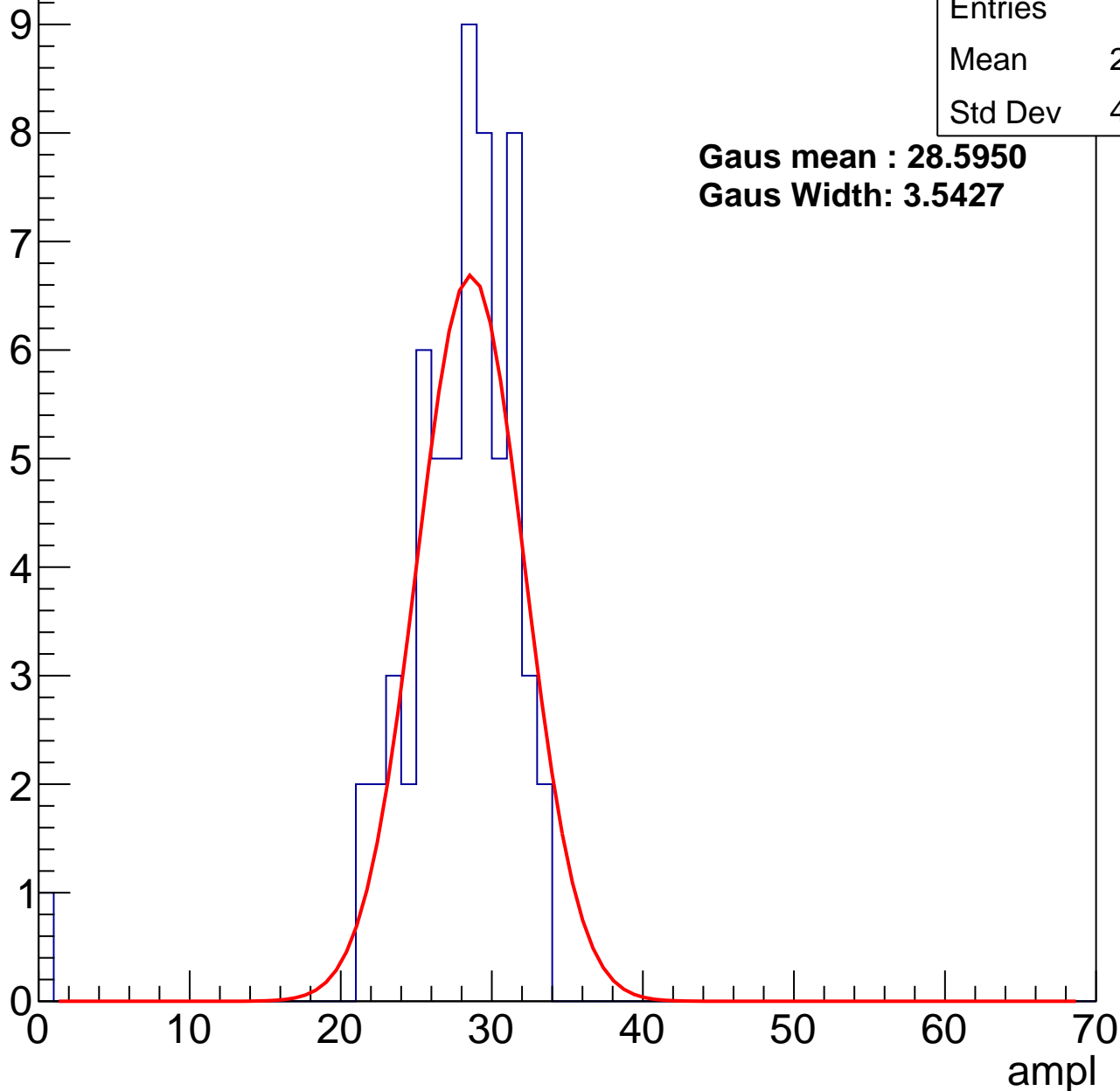
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	27.25
Std Dev	4.622

**Gaus mean : 28.5950**

**Gaus Width: 3.5427**



# B1L000S, U1-ch112, adc1

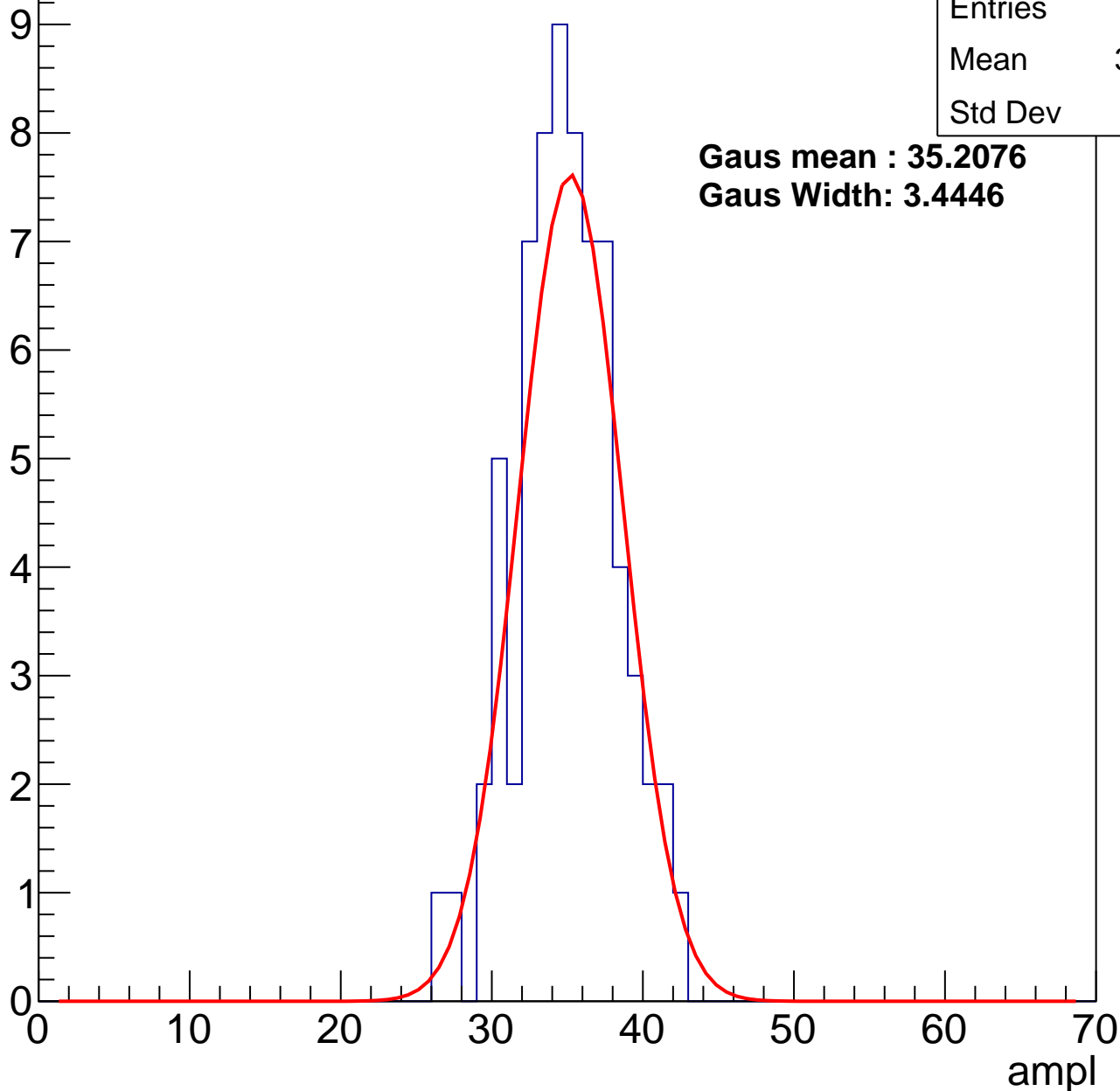
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.51
Std Dev	3.3

**Gaus mean : 35.2076**

**Gaus Width: 3.4446**



# B1L000S, U1-ch112, adc2

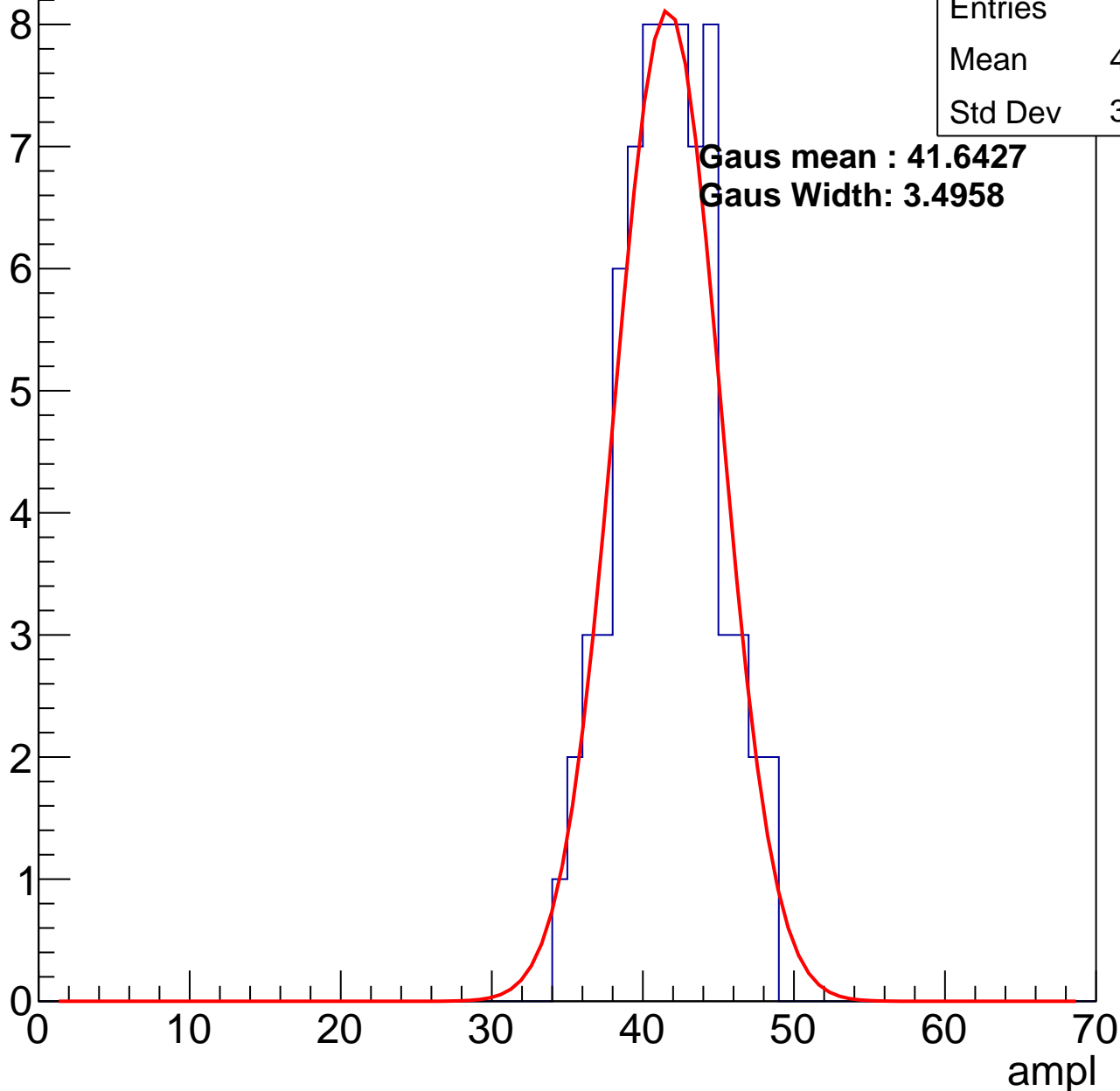
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	41.18
Std Dev	3.212

**Gaus mean : 41.6427**

**Gaus Width: 3.4958**

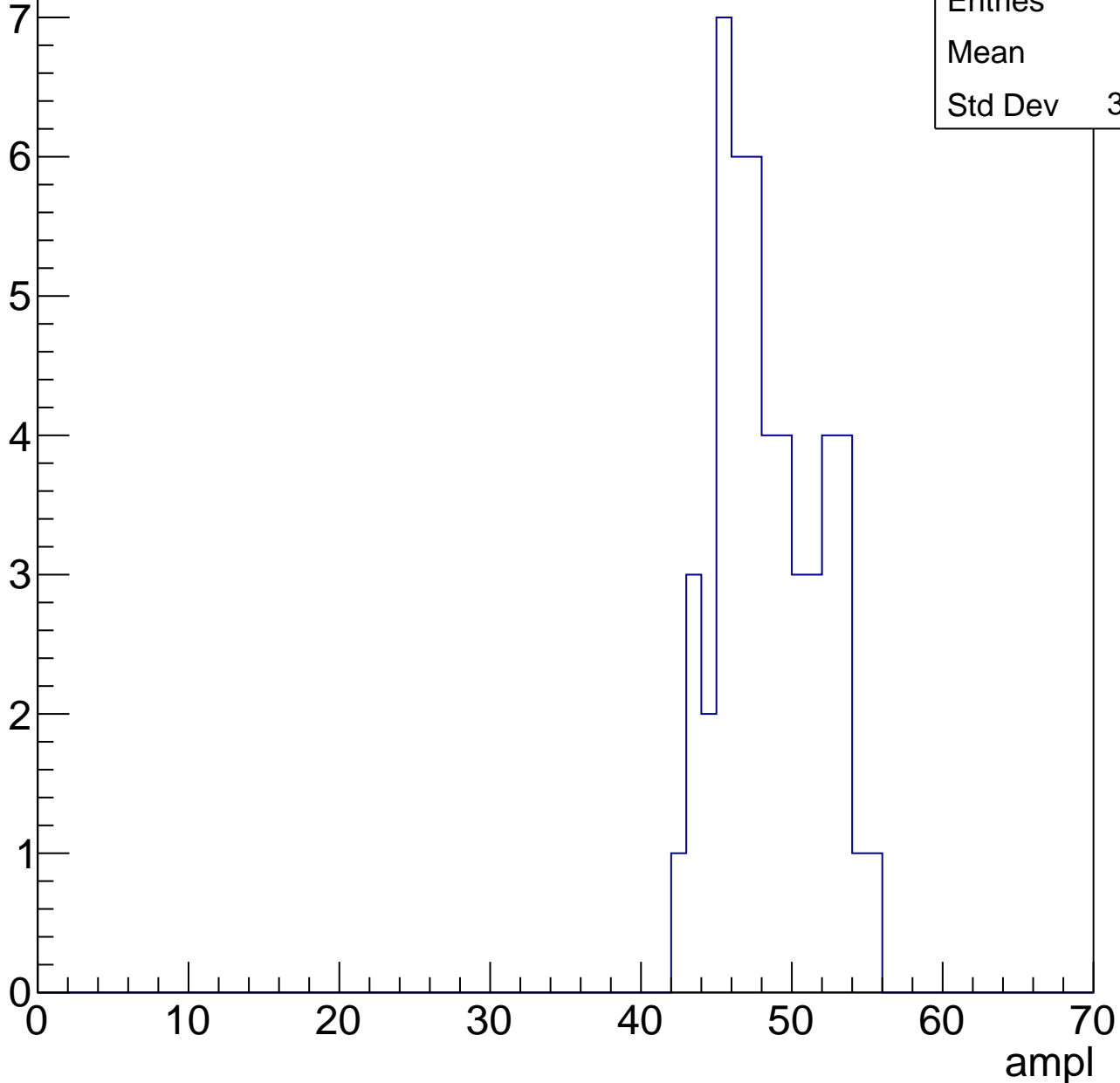


# B1L000S, U1-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

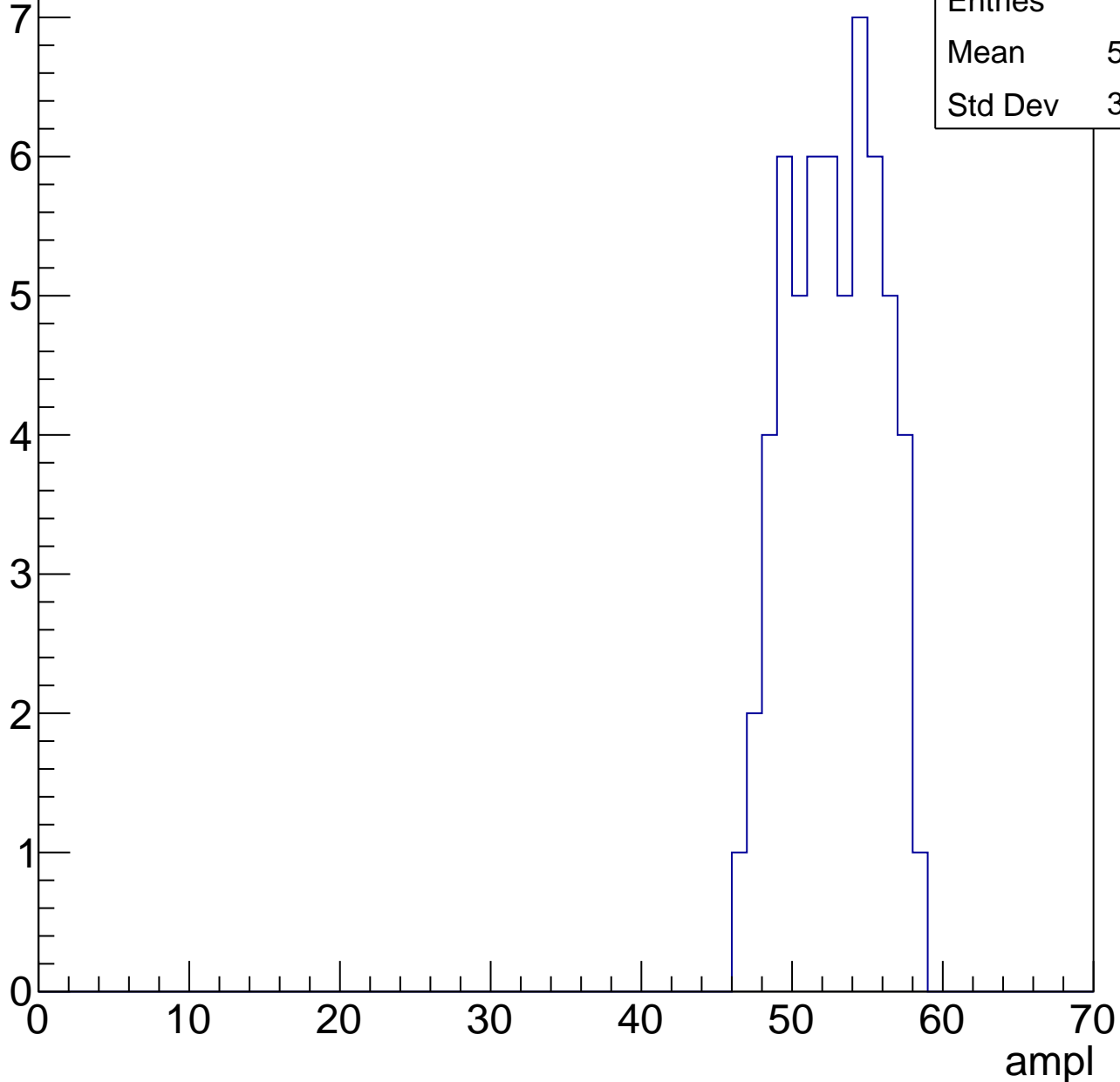
Entries	49
Mean	48
Std Dev	3.283



# B1L000S, U1-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

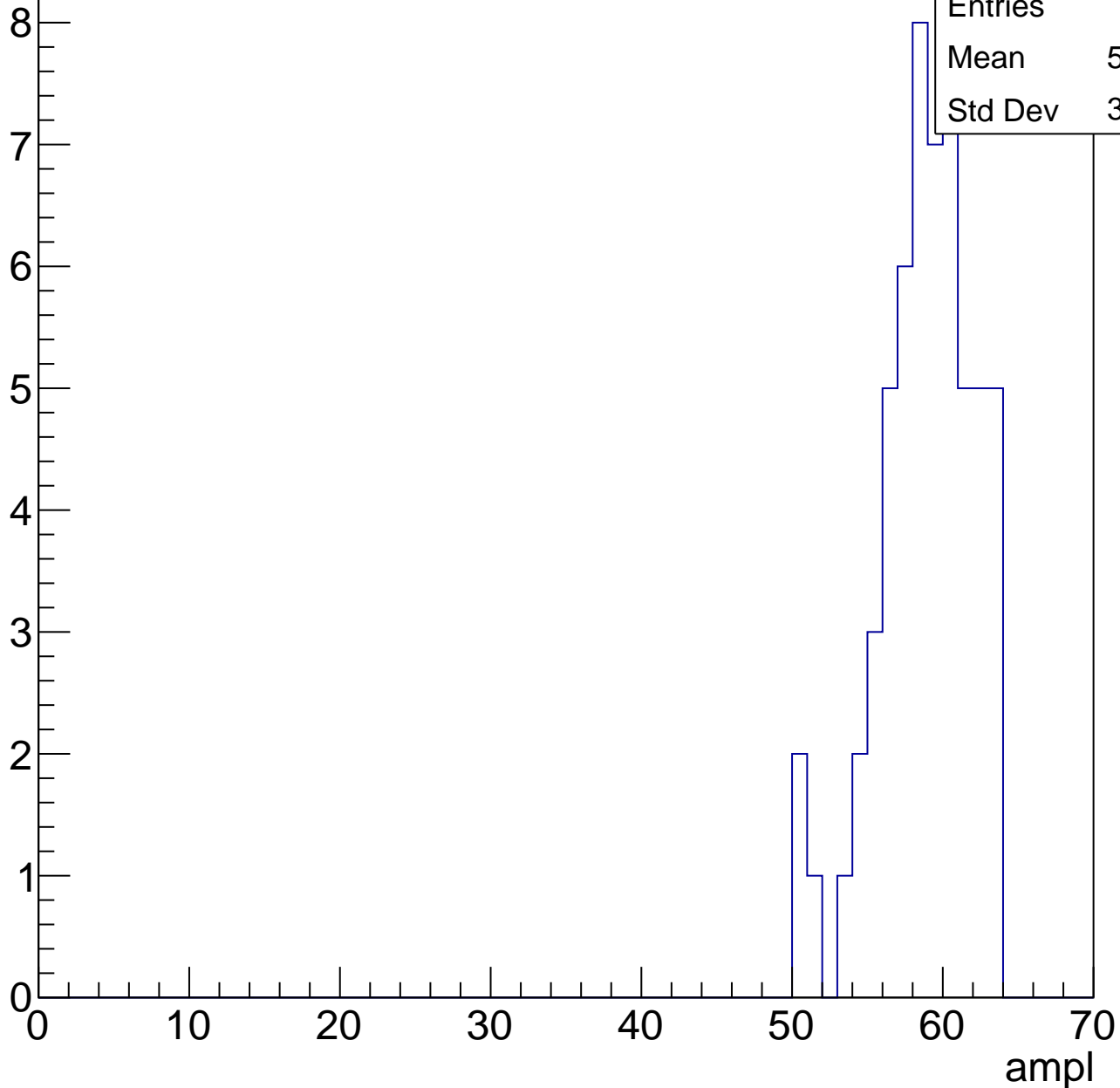


Entries	58
Mean	52.29
Std Dev	3.017

# B1L000S, U1-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



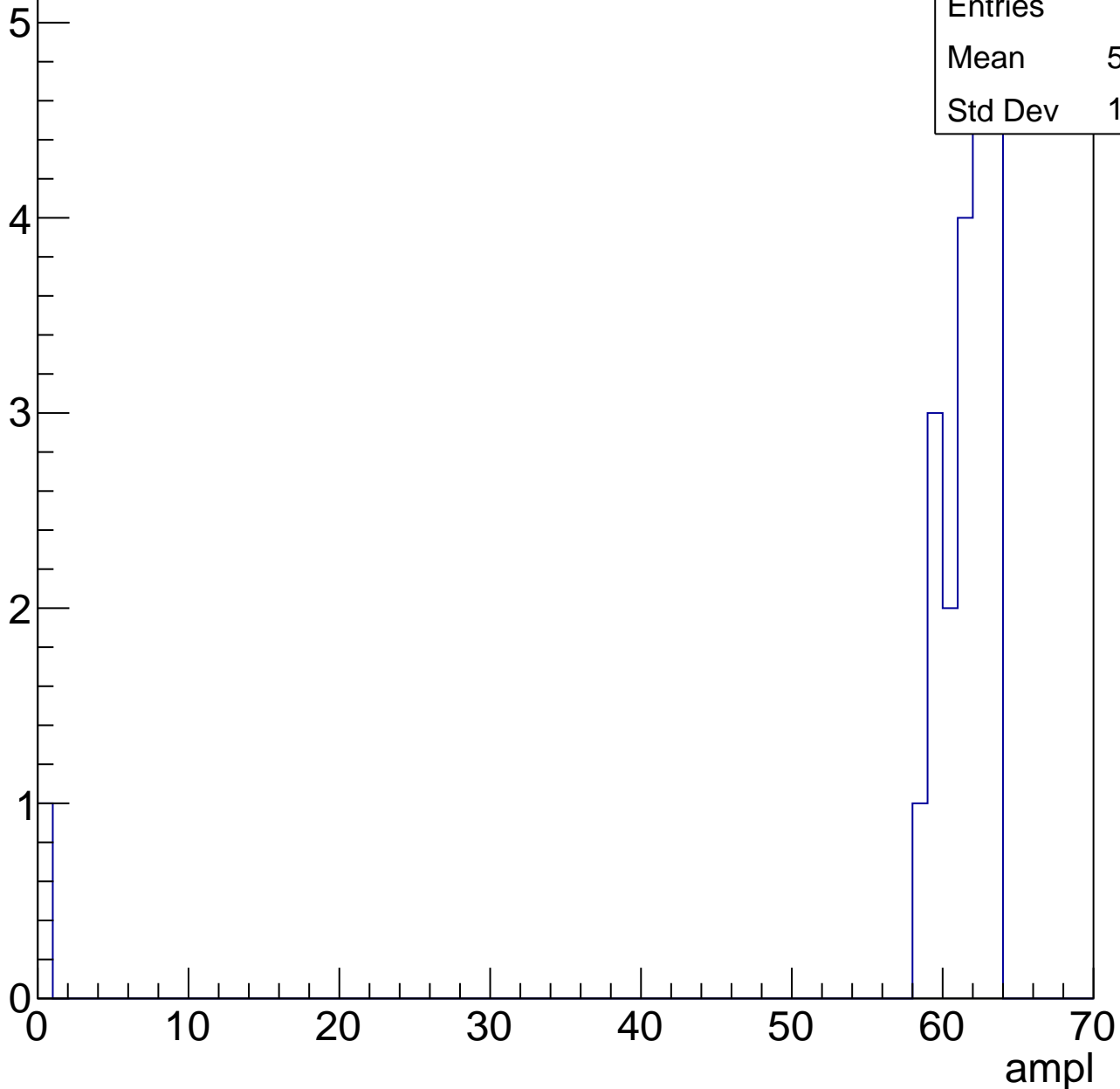
Entries	58
Mean	58.38
Std Dev	3.128

# B1L000S, U1-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.29
Std Dev	13.12





# B1L000S, U1-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	20.33
Std Dev	28.76

# B1L000S, U1-ch113, adc0

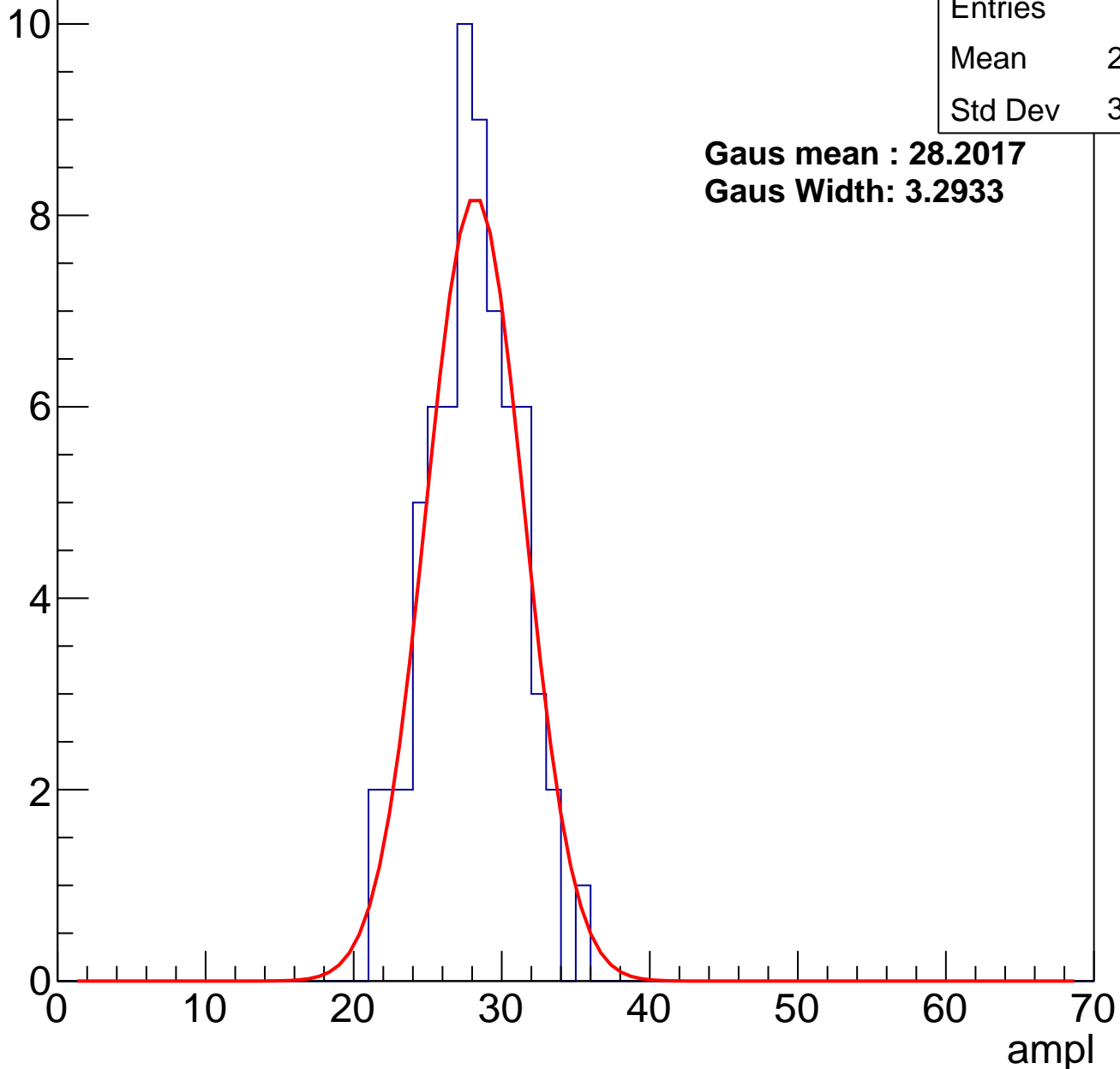
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	27.55
Std Dev	3.009

**Gaus mean : 28.2017**

**Gaus Width: 3.2933**

Entry

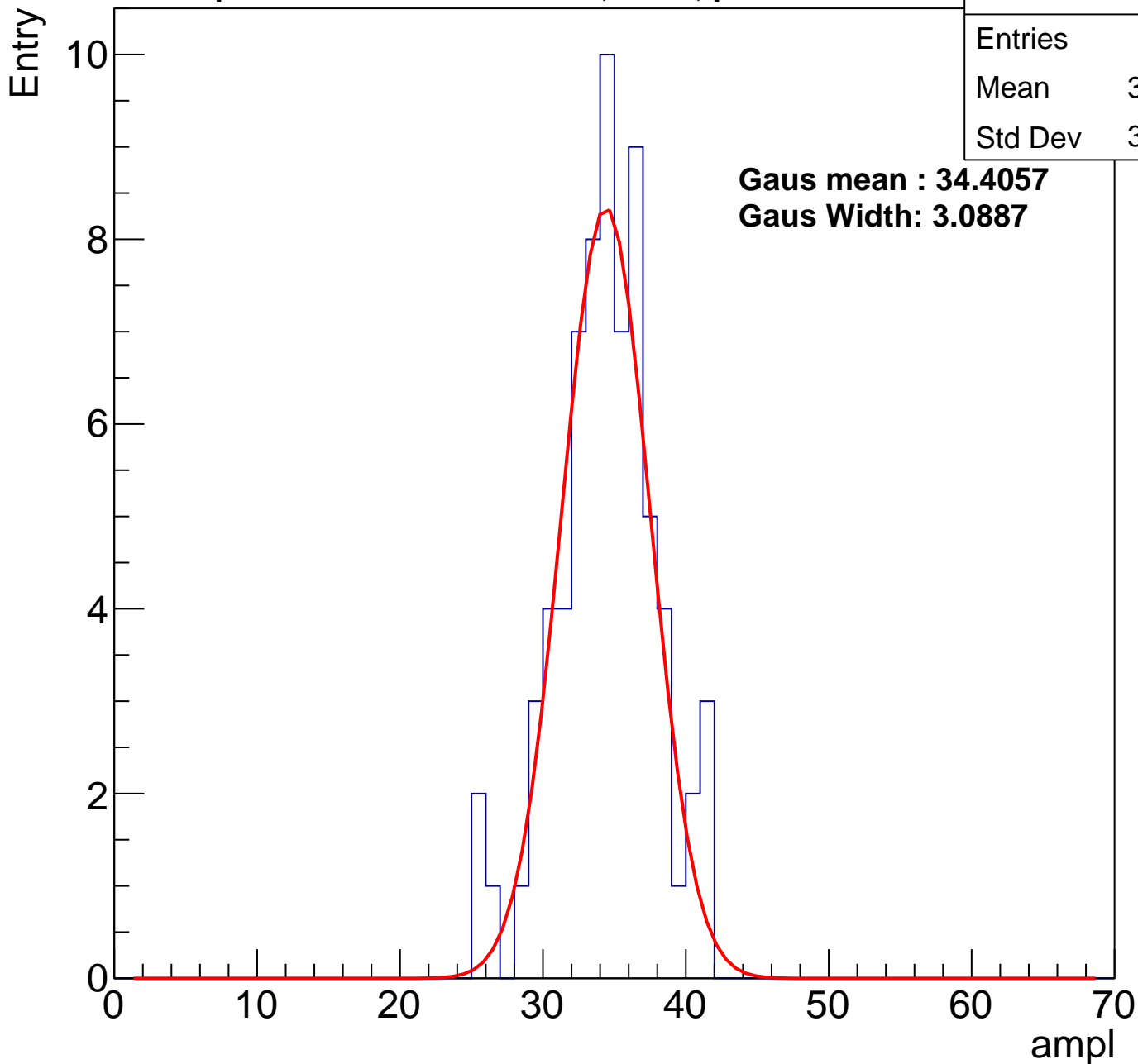


# B1L000S, U1-ch113, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	33.96
Std Dev	3.498

**Gaus mean : 34.4057**  
**Gaus Width: 3.0887**



# B1L000S, U1-ch113, adc2

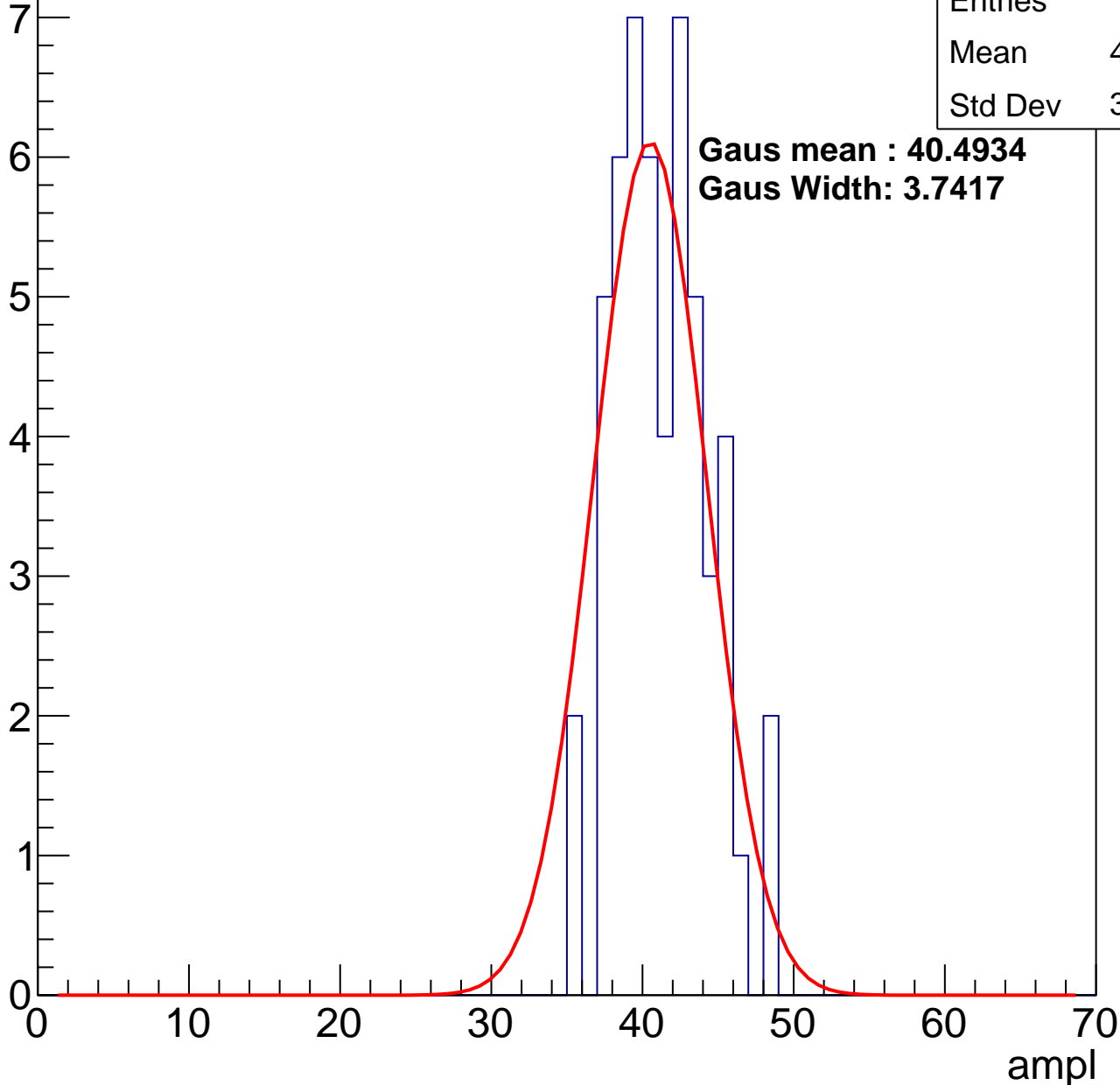
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	40.83
Std Dev	3.036

**Gaus mean : 40.4934**

**Gaus Width: 3.7417**

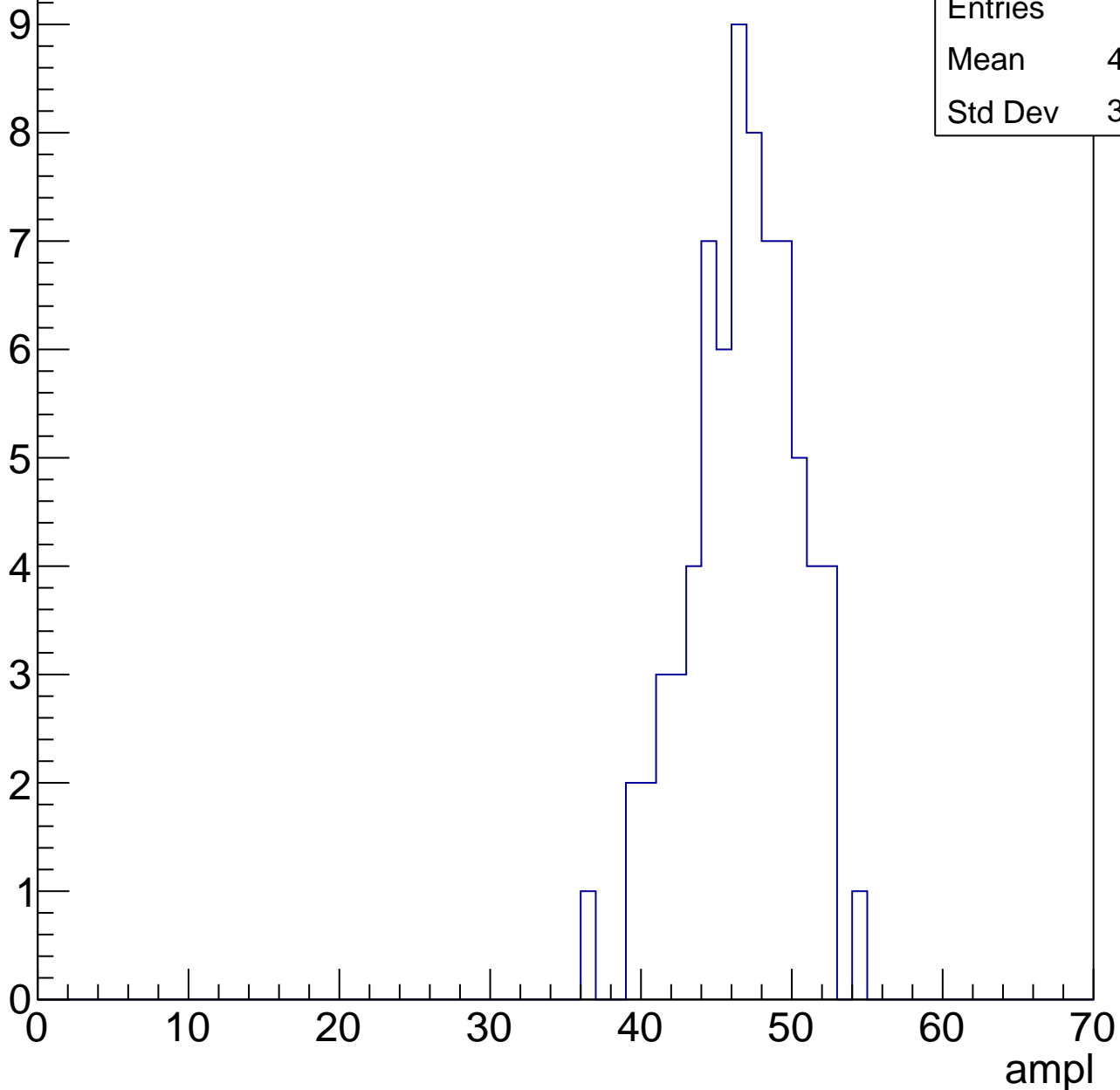


# B1L000S, U1-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	46.27
Std Dev	3.597

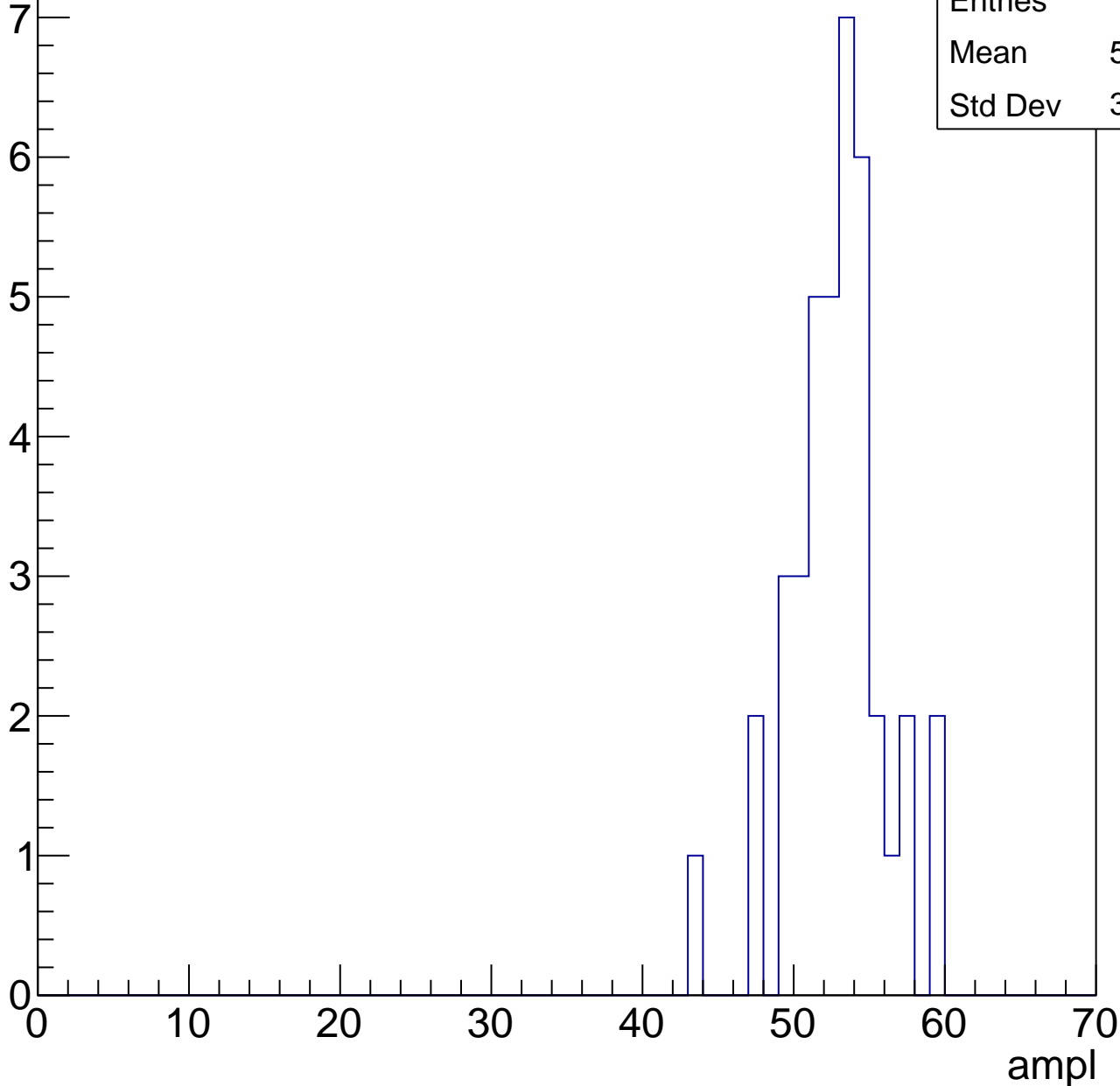


# B1L000S, U1-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	52.36
Std Dev	3.134

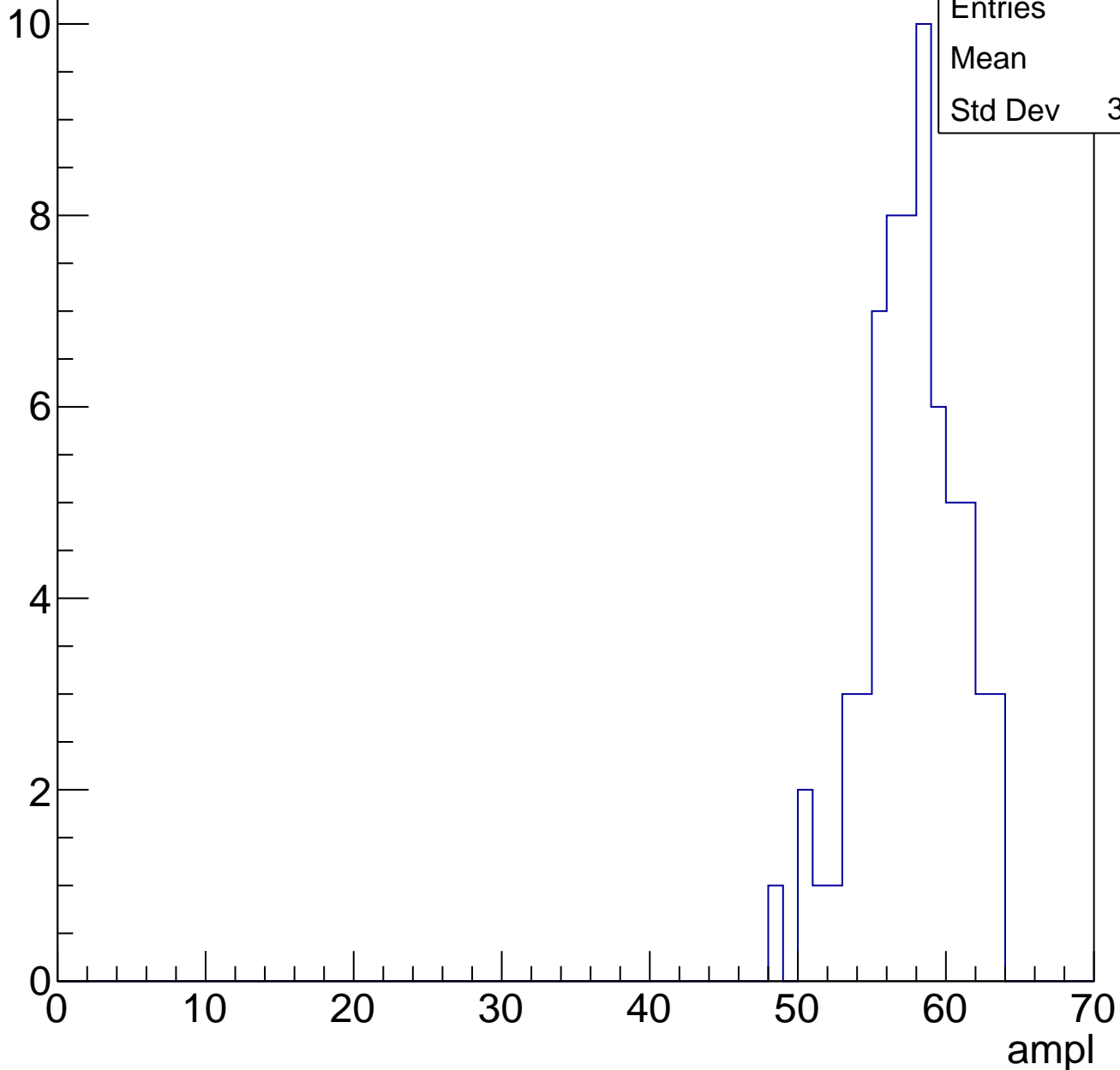


# B1L000S, U1-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	57.2
Std Dev	3.234

Entry

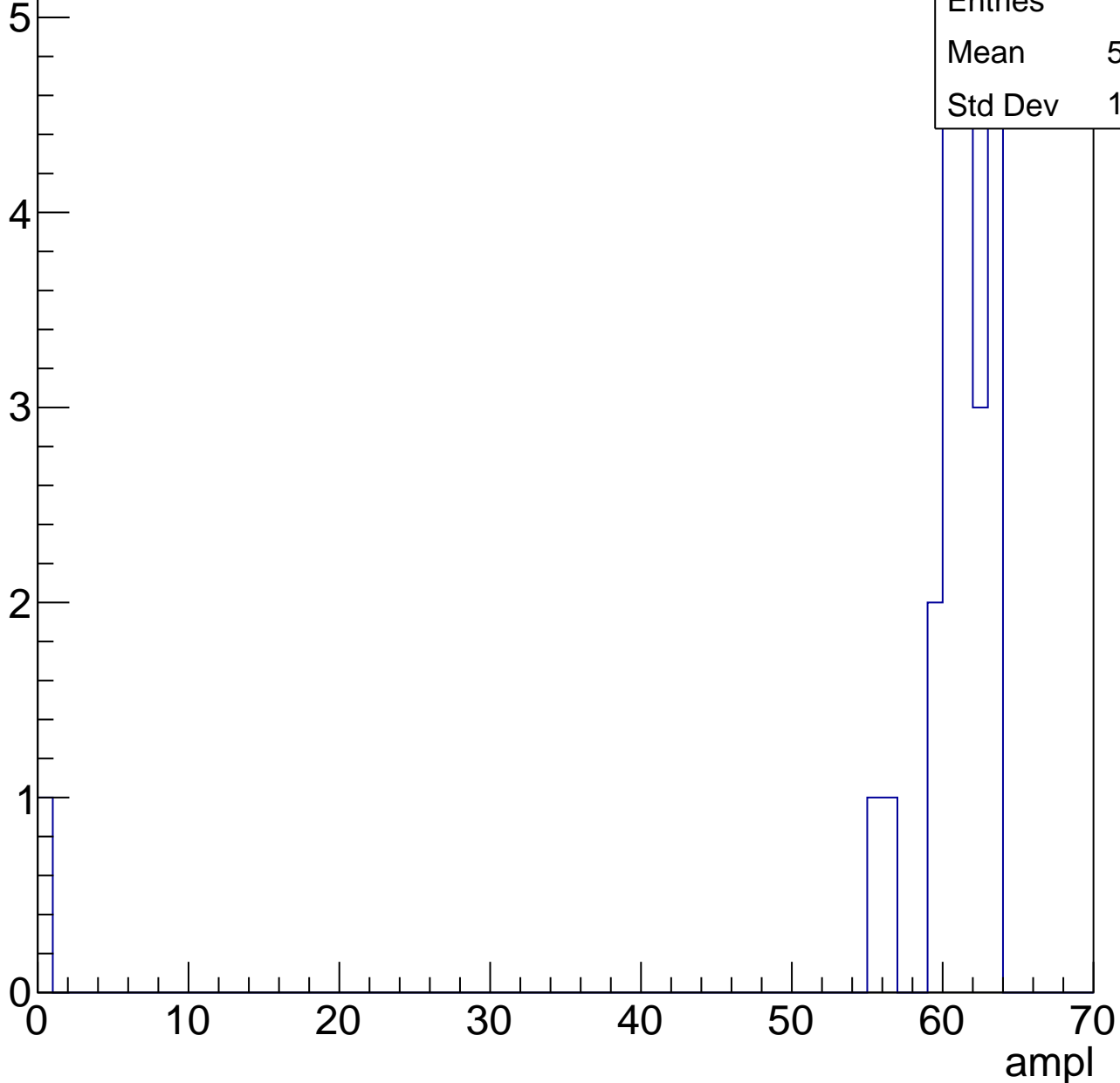


# B1L000S, U1-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	58.04
Std Dev	12.54

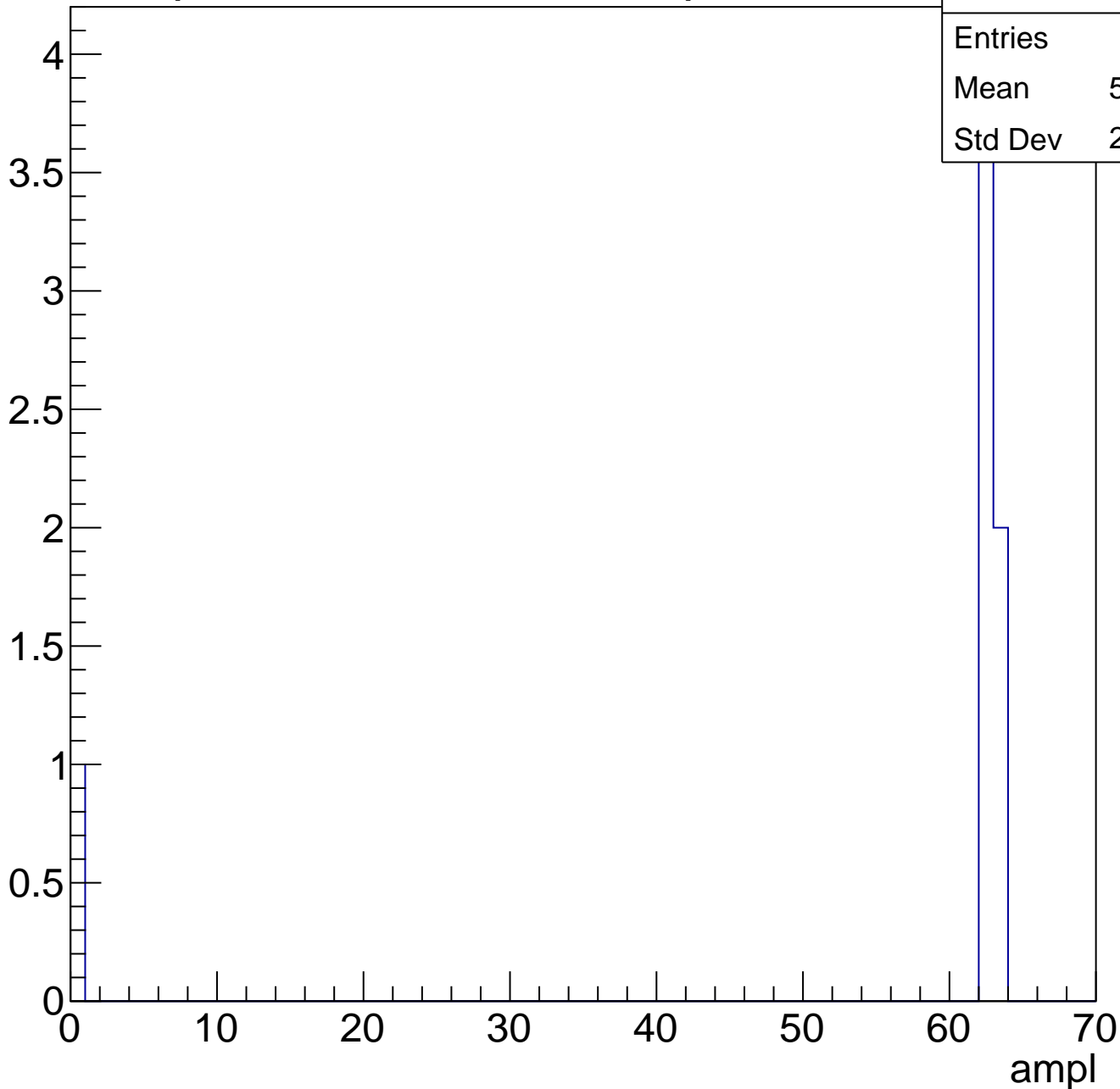




# B1L000S, U1-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch114, adc0

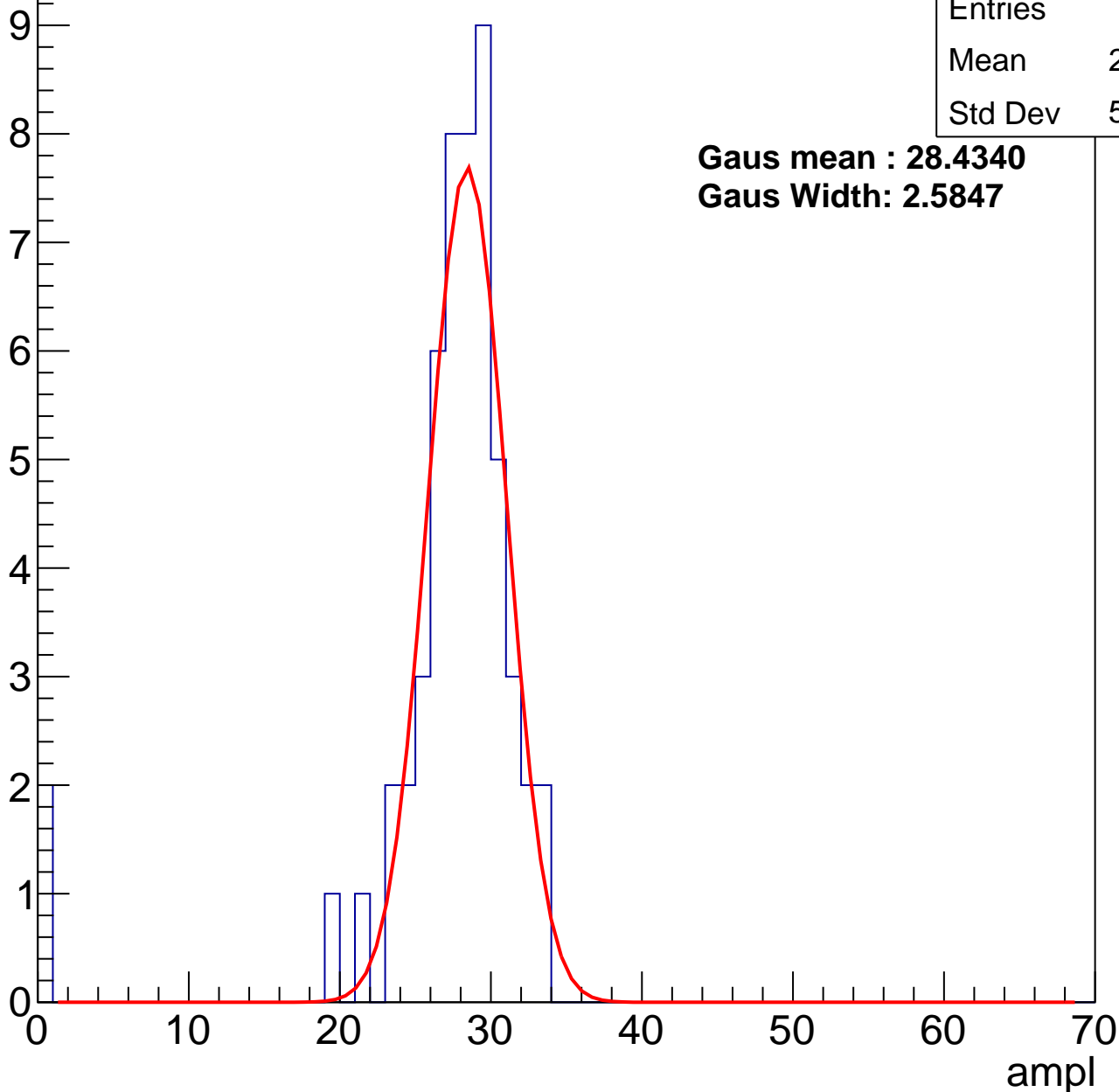
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	26.65
Std Dev	5.898

**Gaus mean : 28.4340**

**Gaus Width: 2.5847**



# B1L000S, U1-ch114, adc1

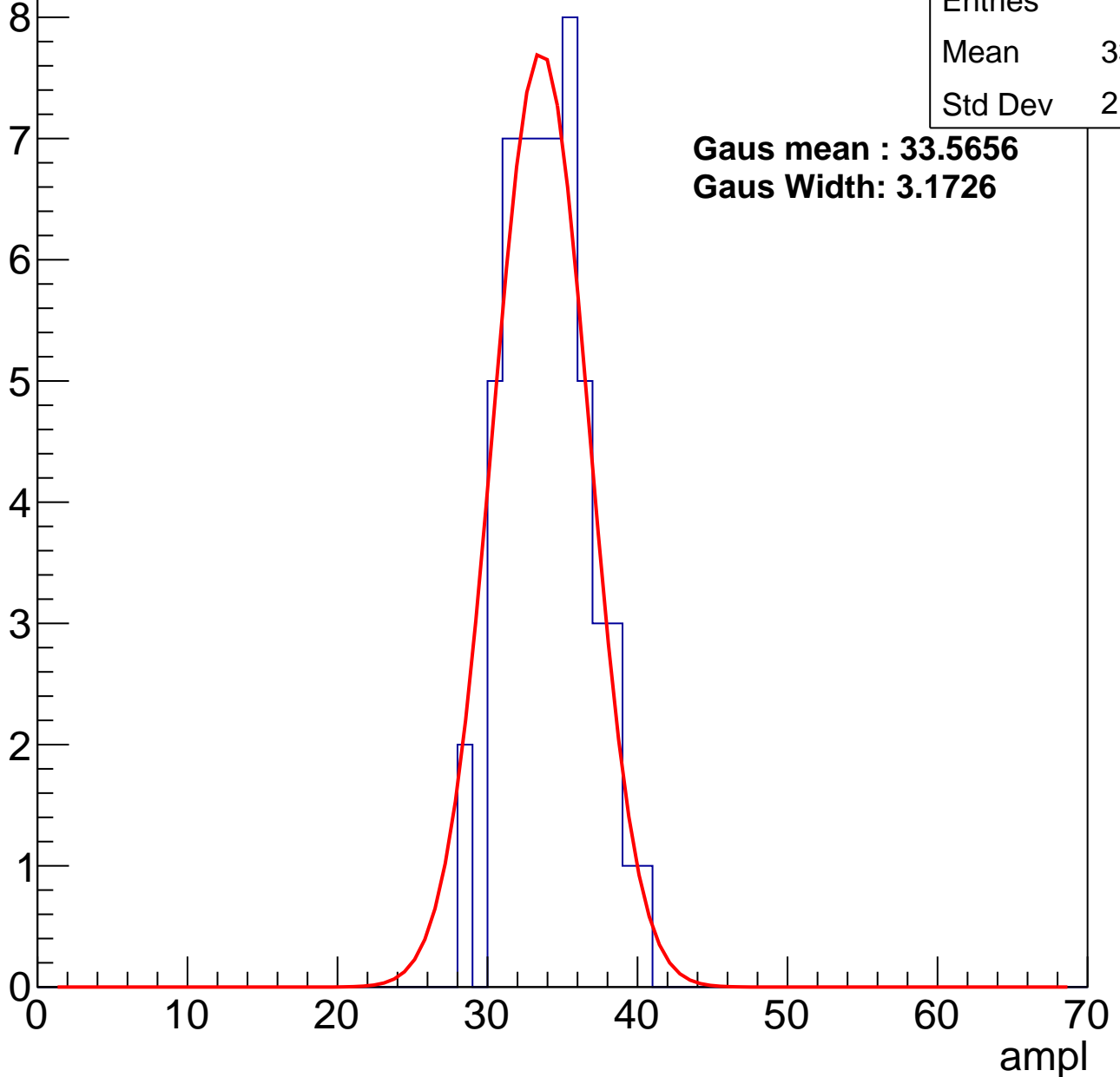
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	33.57
Std Dev	2.685

**Gaus mean : 33.5656**

**Gaus Width: 3.1726**



# B1L000S, U1-ch114, adc2

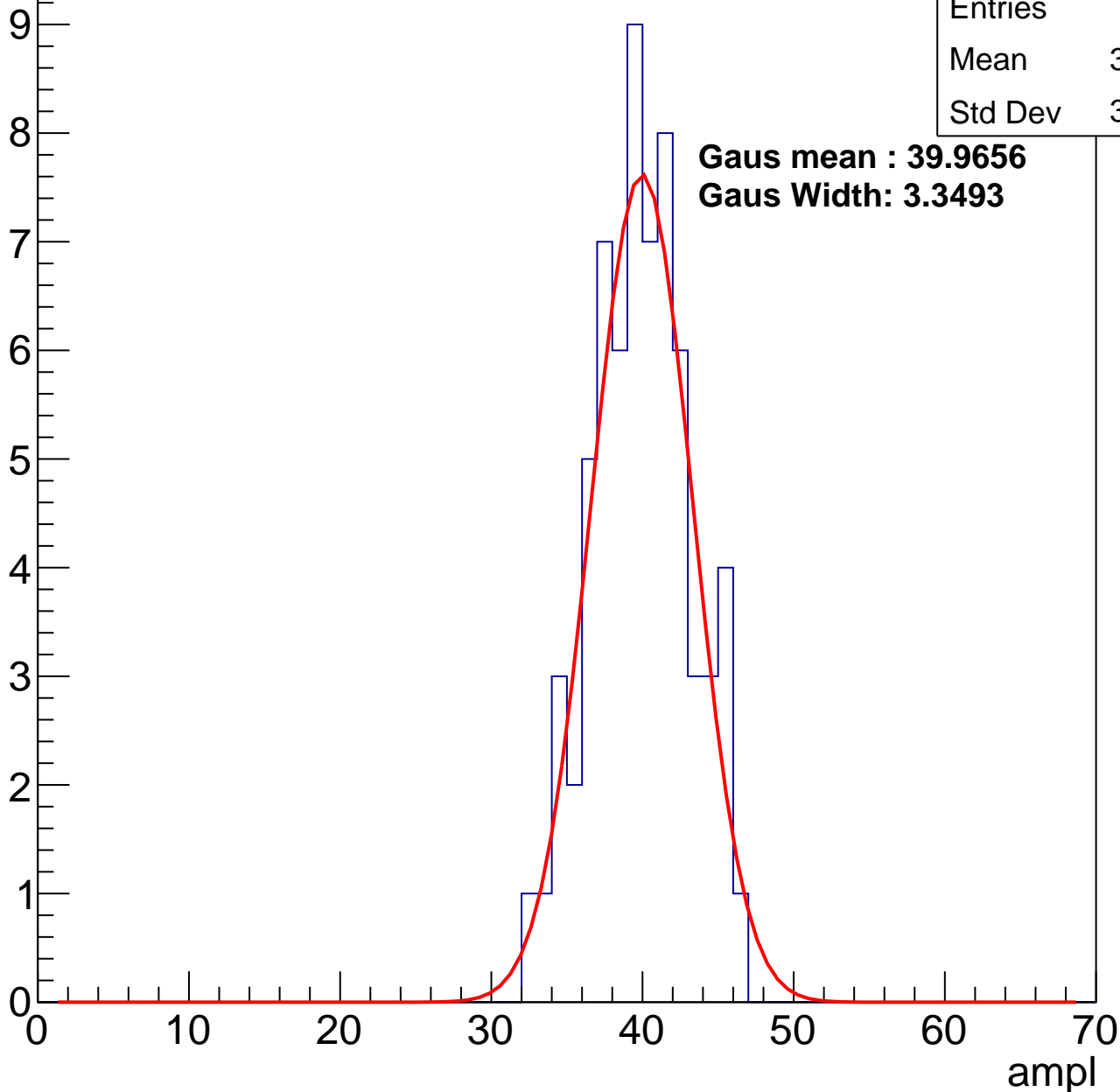
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	39.42
Std Dev	3.182

**Gaus mean : 39.9656**

**Gaus Width: 3.3493**

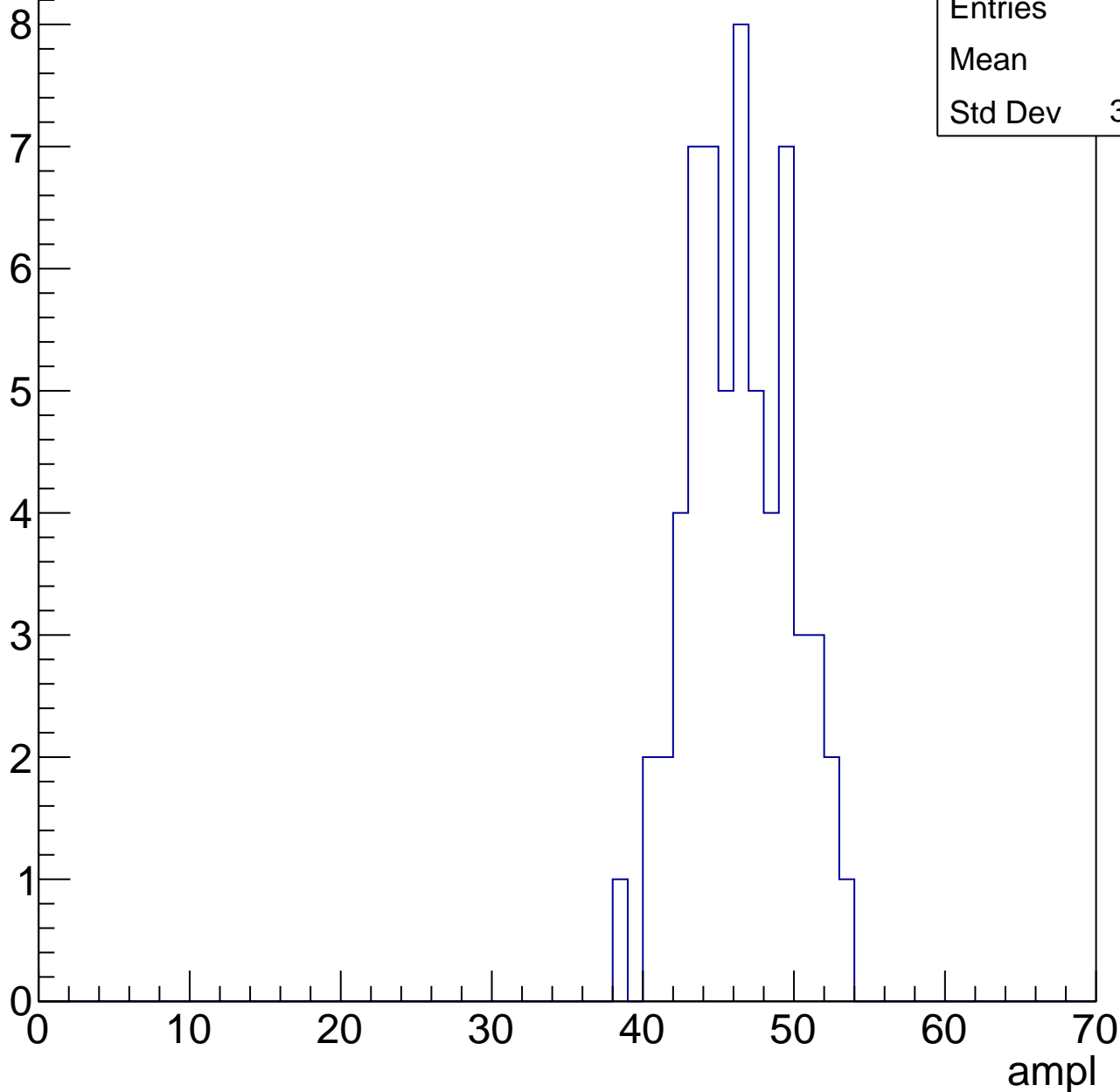


# B1L000S, U1-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	45.9
Std Dev	3.323

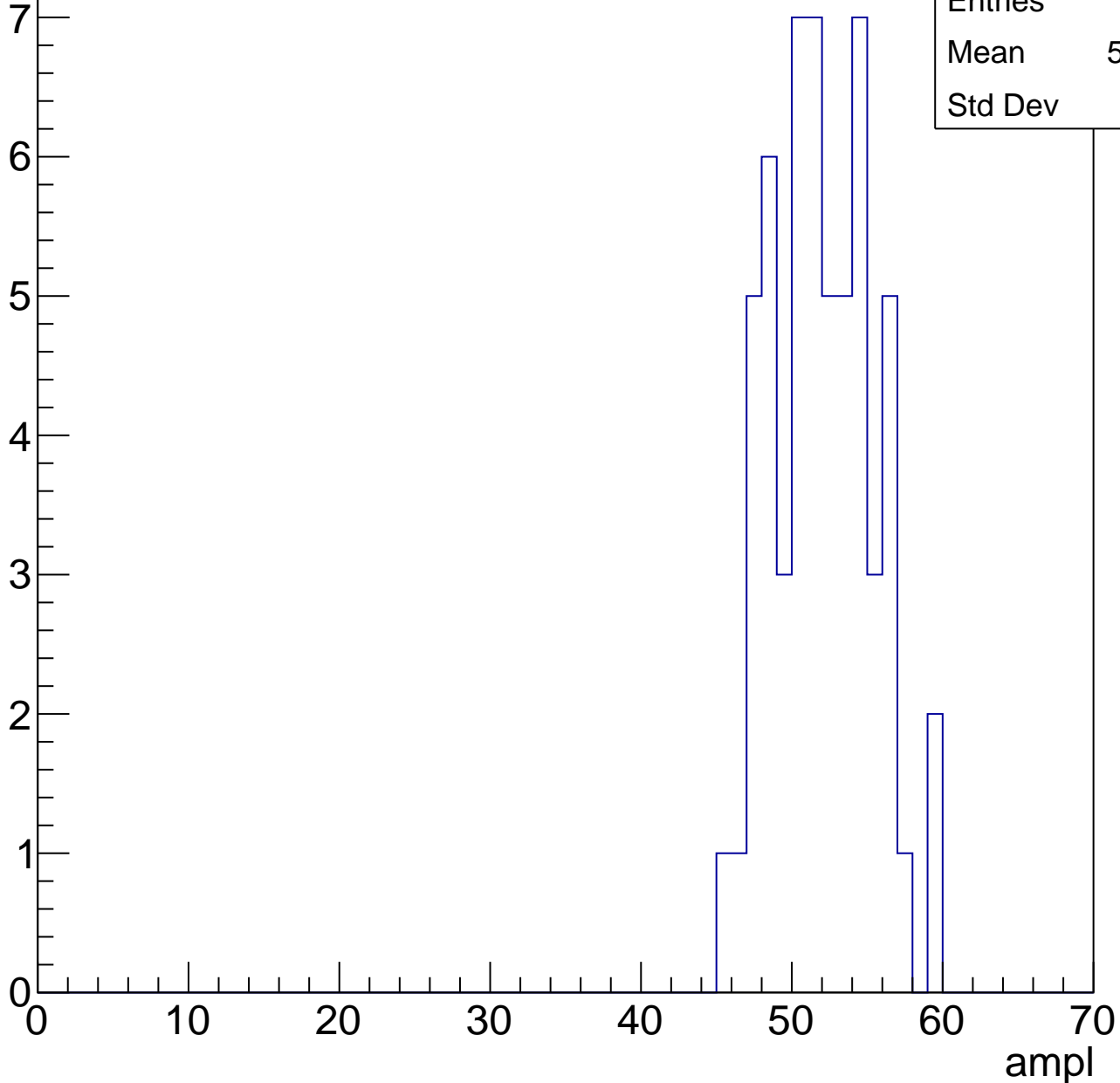


# B1L000S, U1-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	51.57
Std Dev	3.27

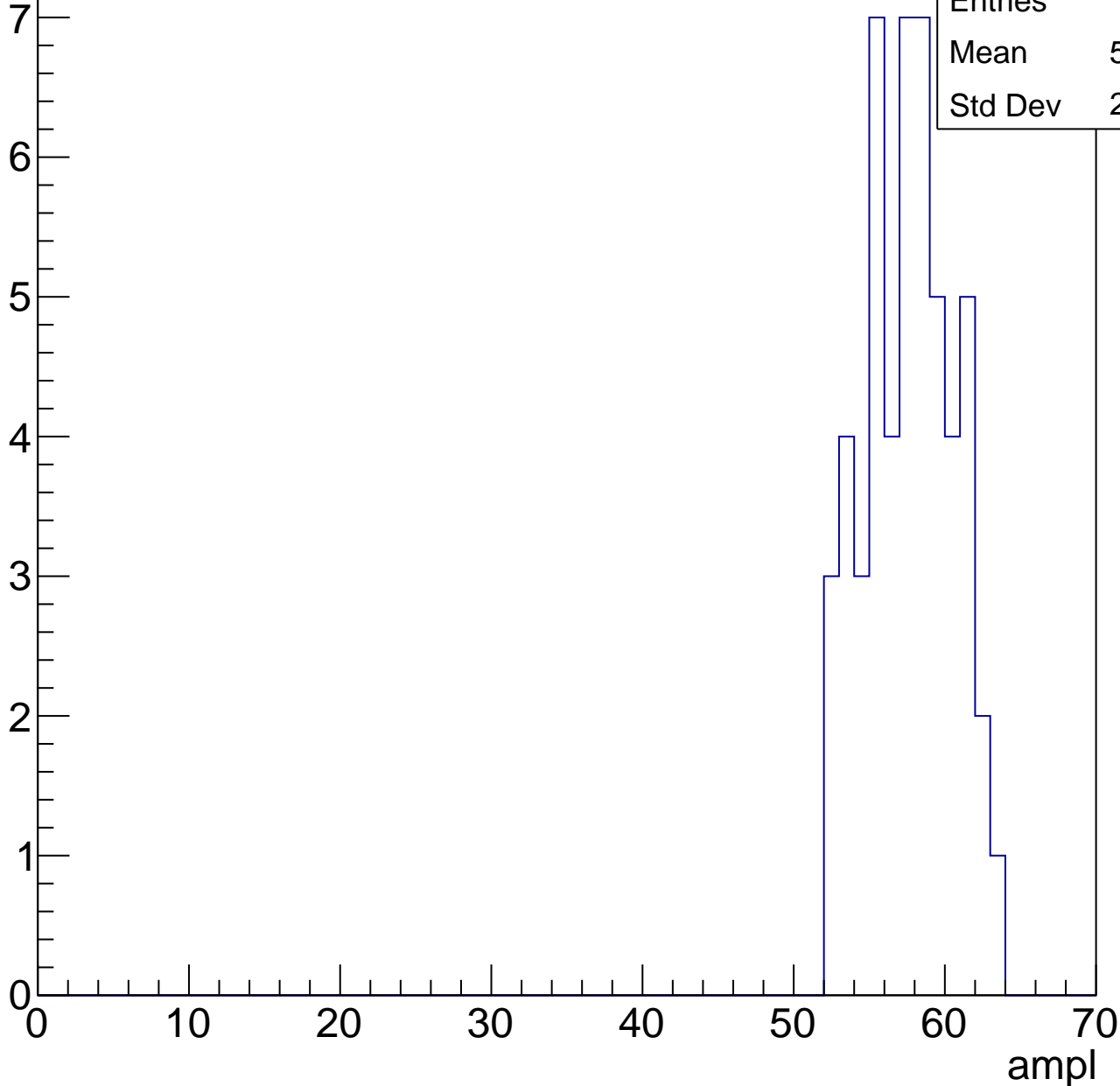


# B1L000S, U1-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	57.13
Std Dev	2.862

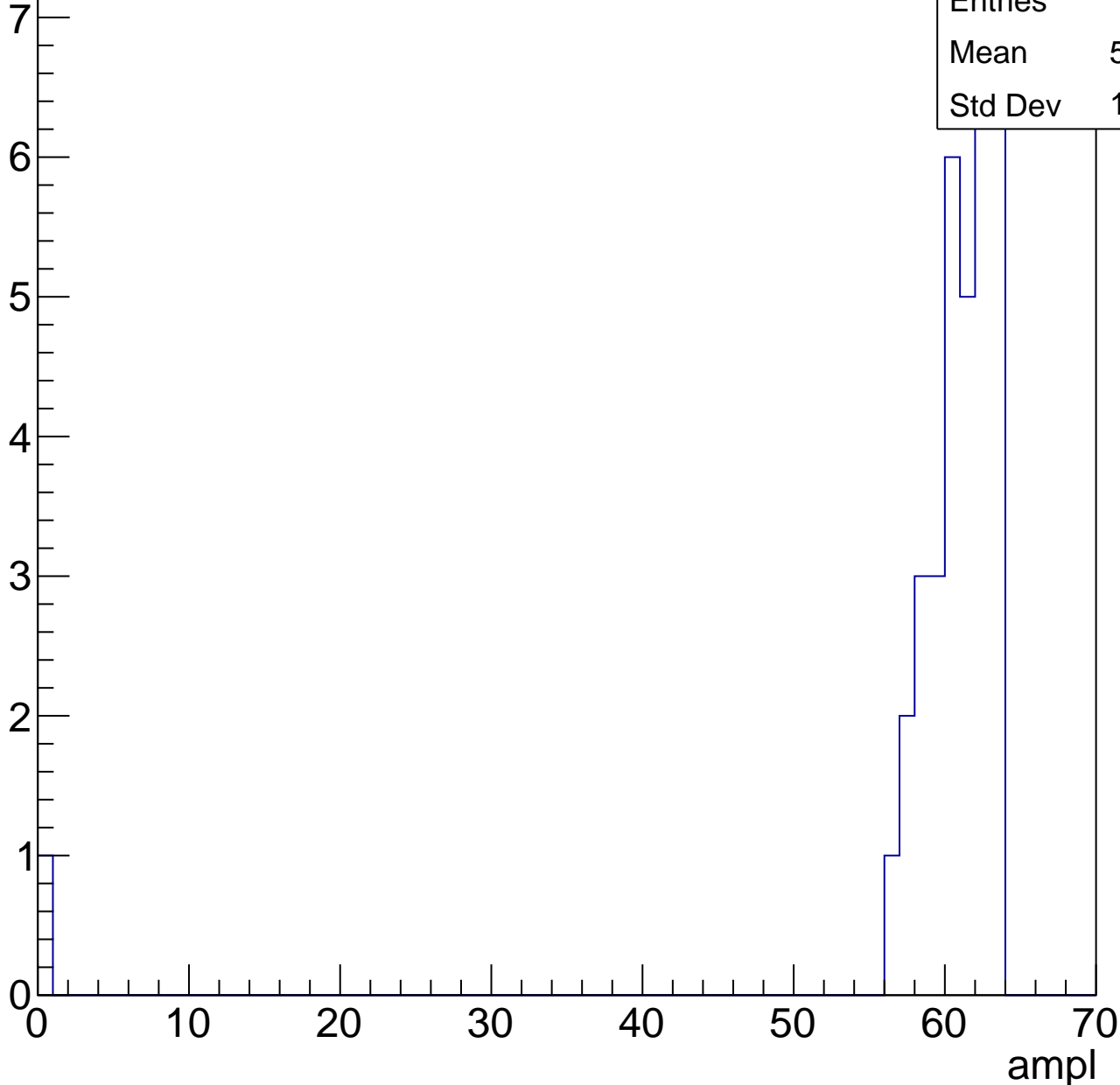


# B1L000S, U1-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	35
Mean	58.89
Std Dev	10.28

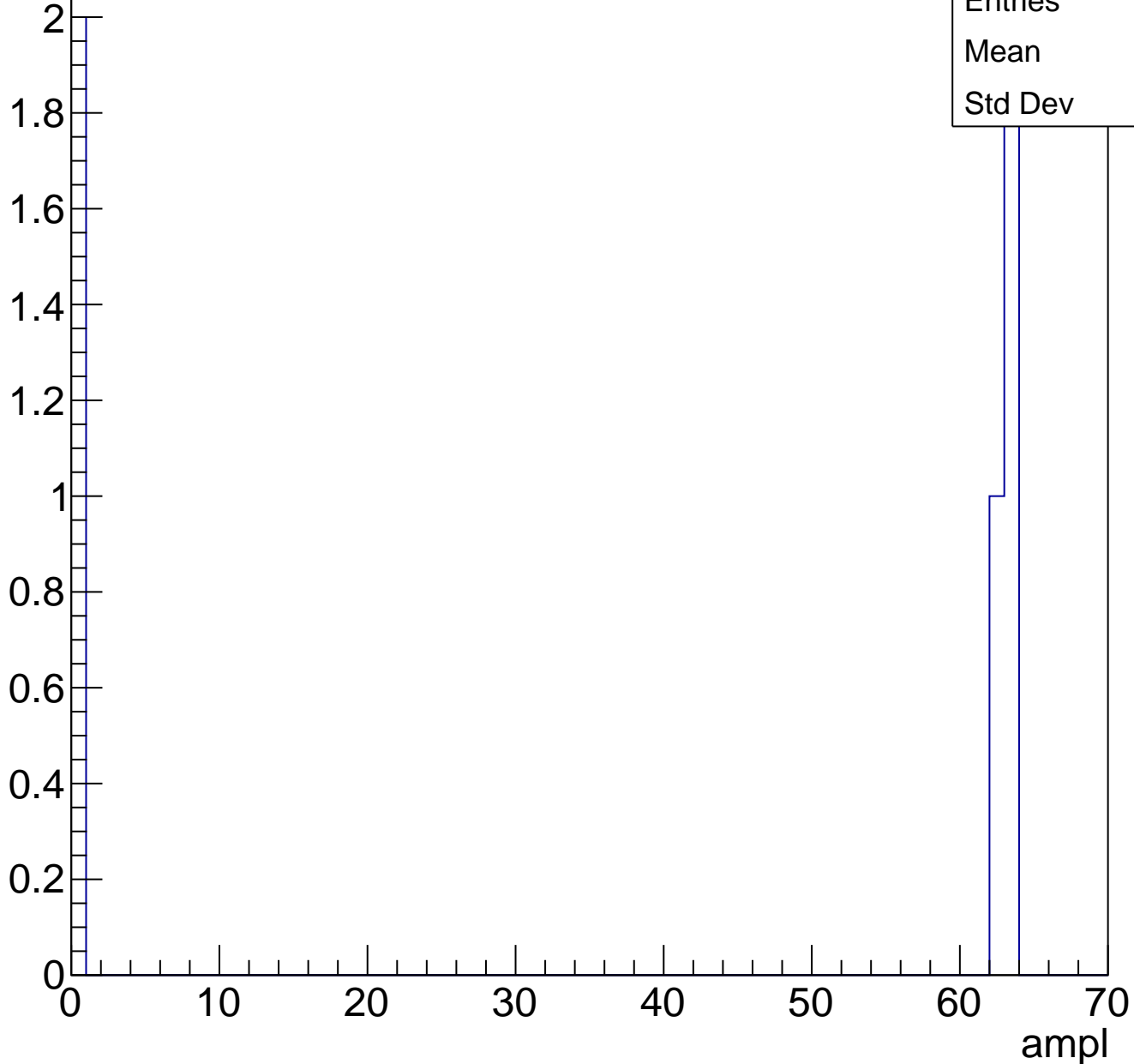




# B1L000S, U1-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch115, adc0

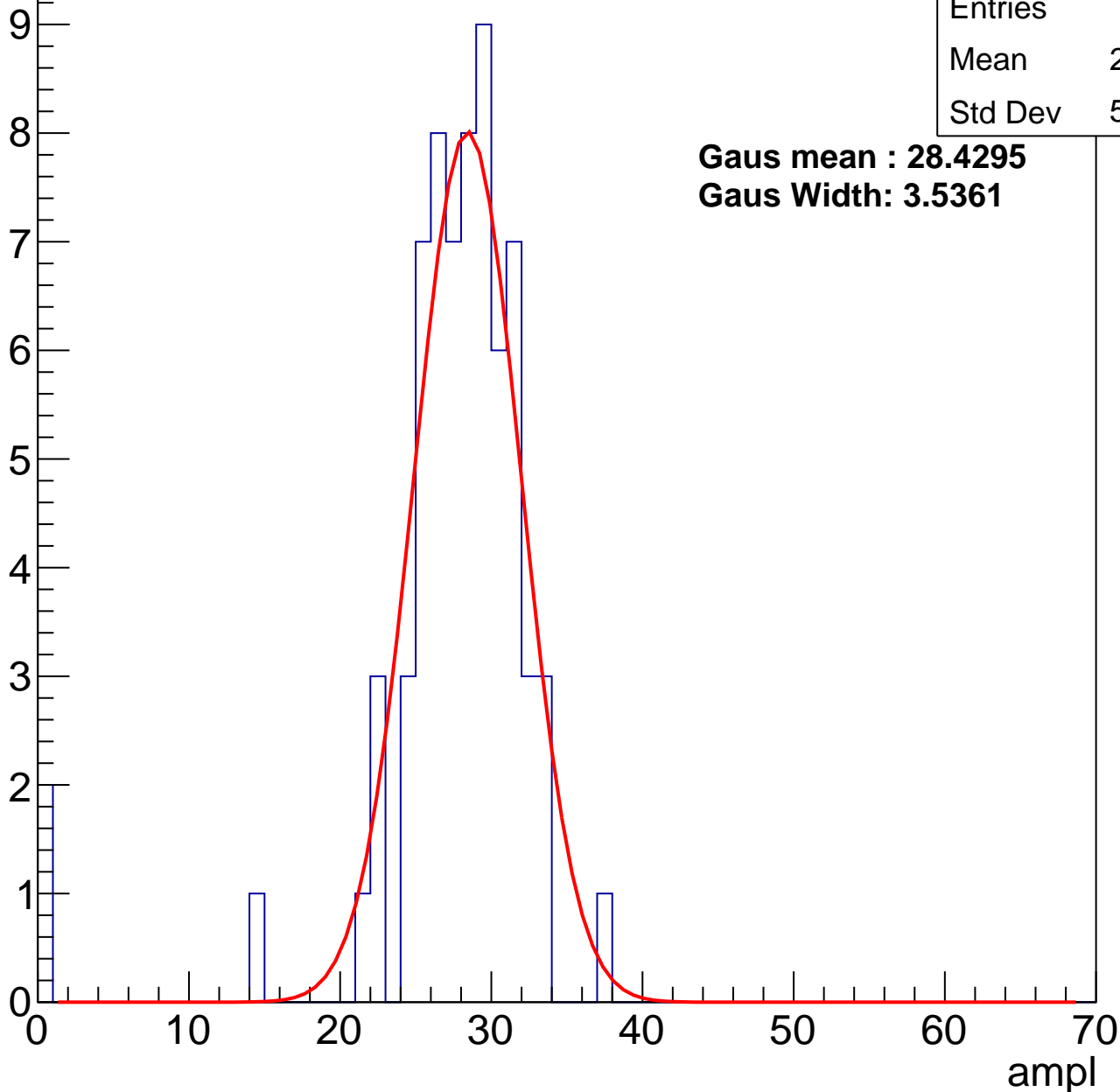
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	26.94
Std Dev	5.766

**Gaus mean : 28.4295**

**Gaus Width: 3.5361**



# B1L000S, U1-ch115, adc1

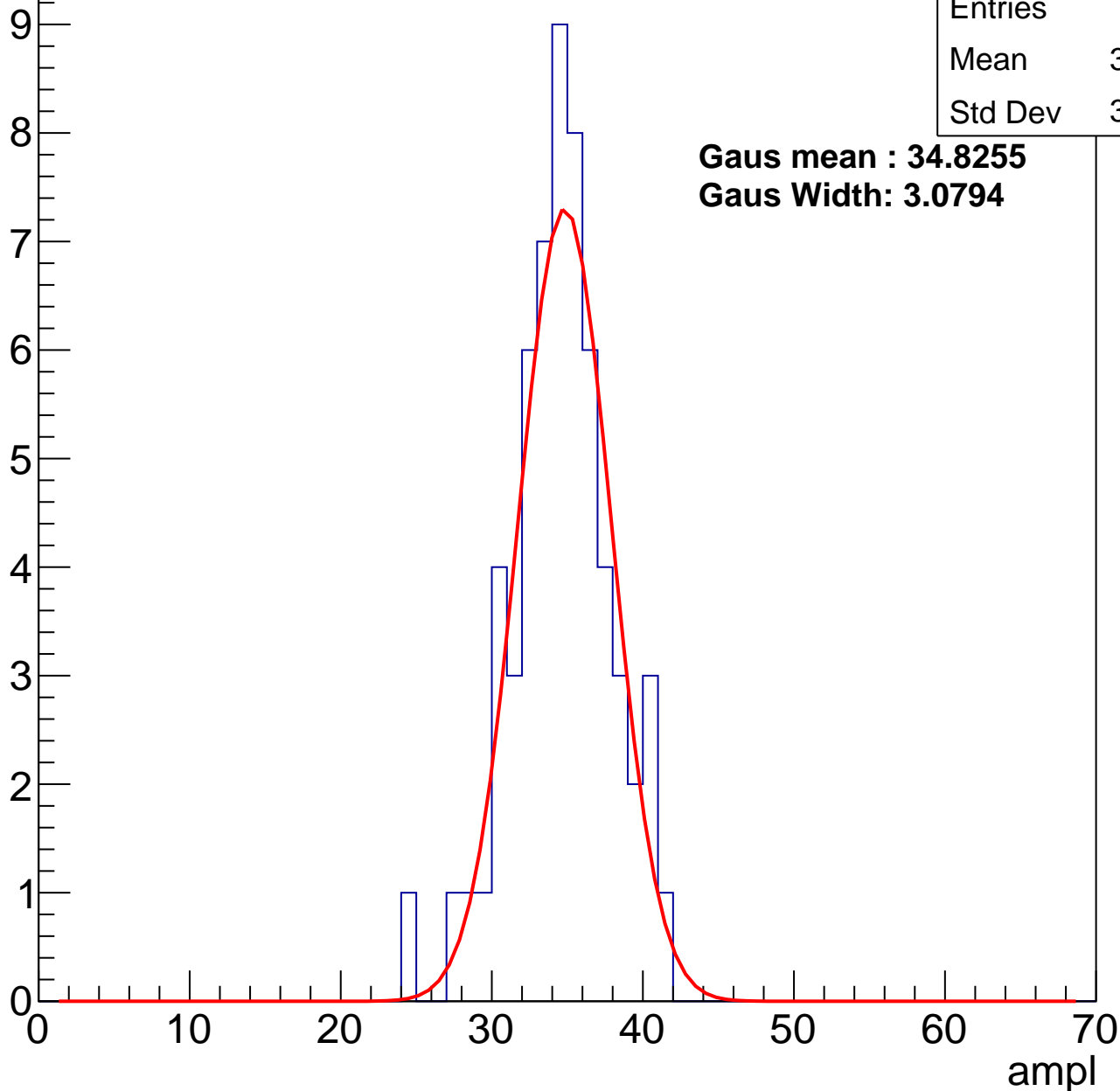
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	34.12
Std Dev	3.302

**Gaus mean : 34.8255**

**Gaus Width: 3.0794**



# B1L000S, U1-ch115, adc2

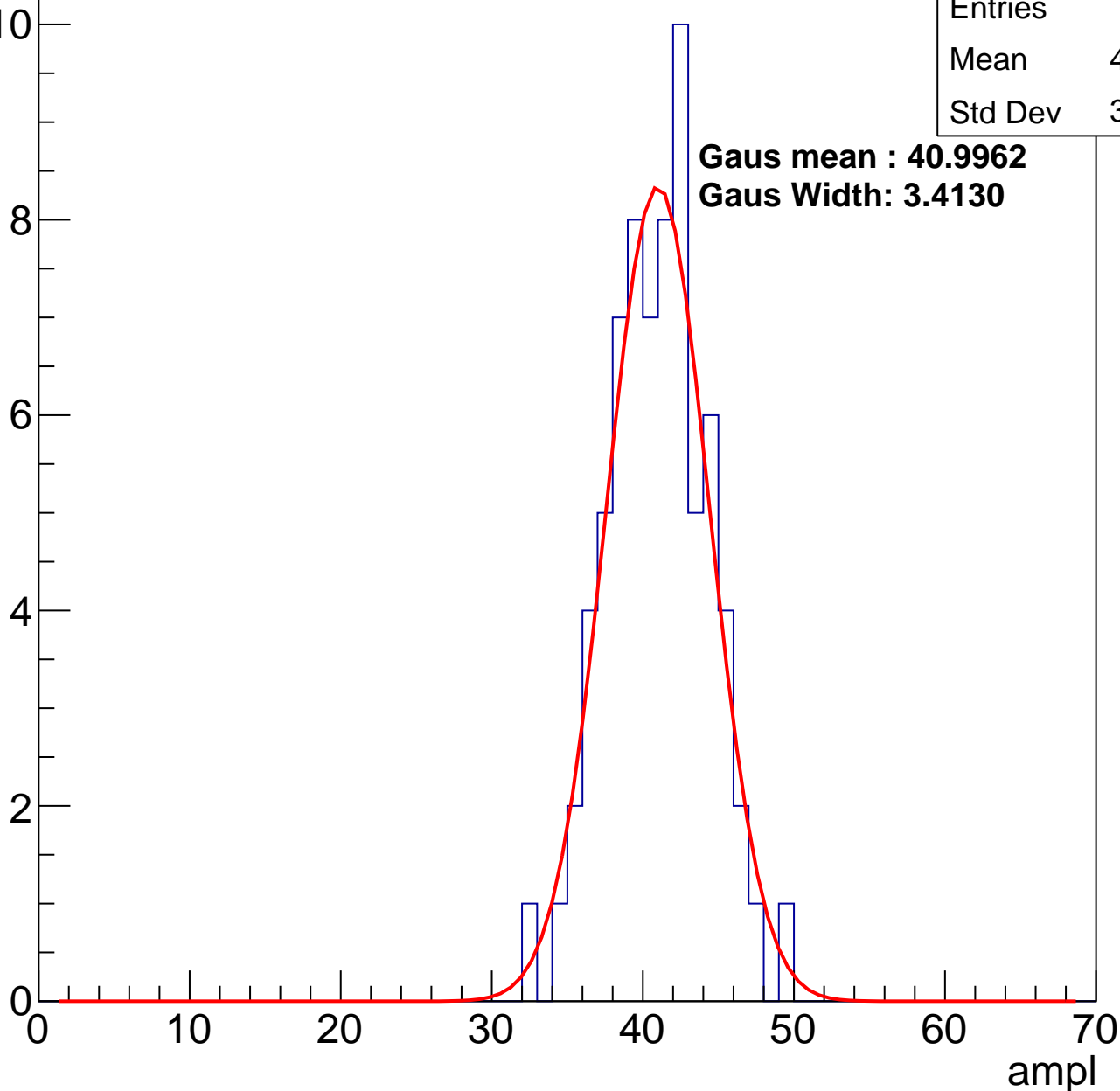
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	40.53
Std Dev	3.262

**Gaus mean : 40.9962**

**Gaus Width: 3.4130**

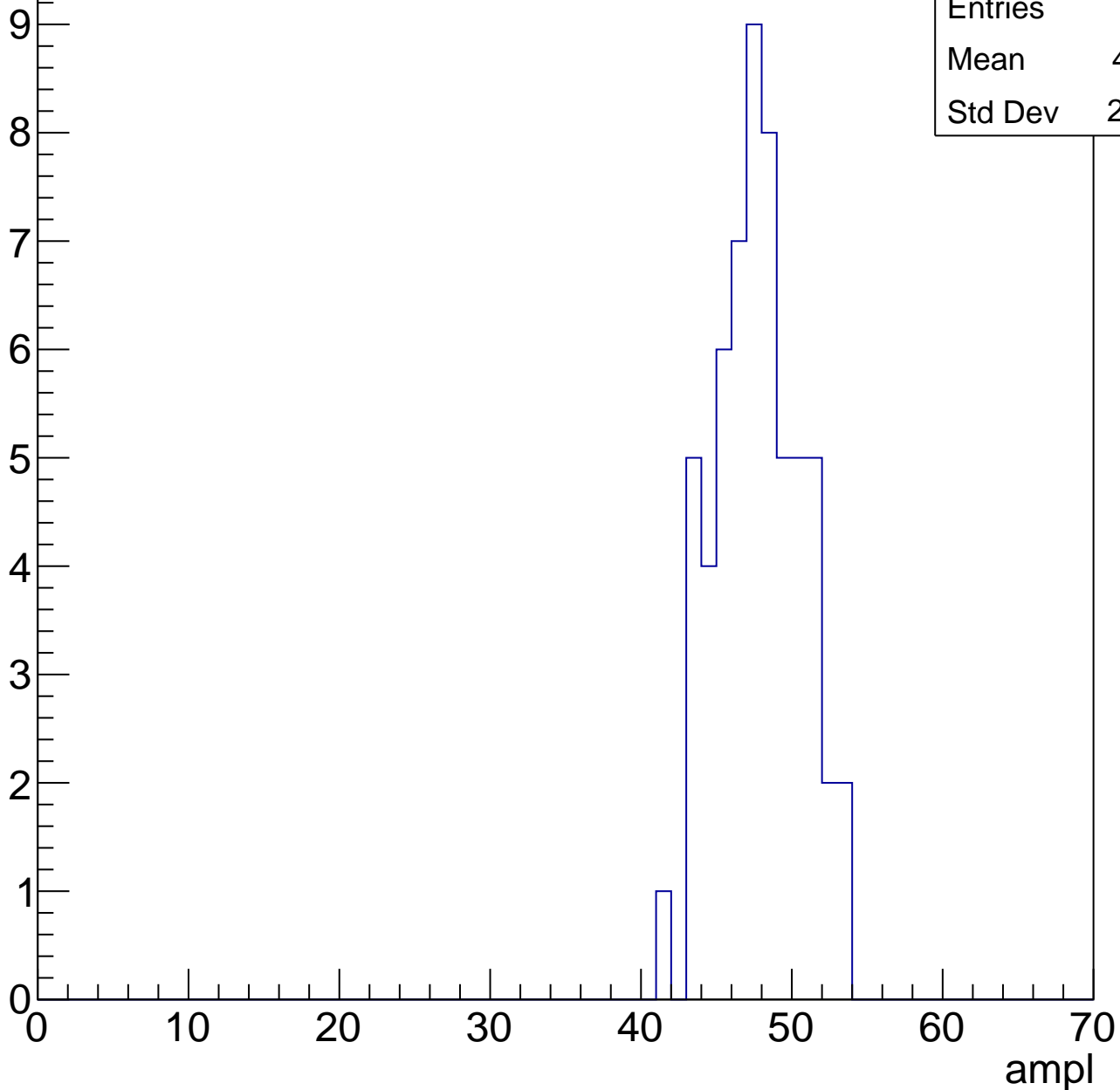


# B1L000S, U1-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.31
Std Dev	2.769

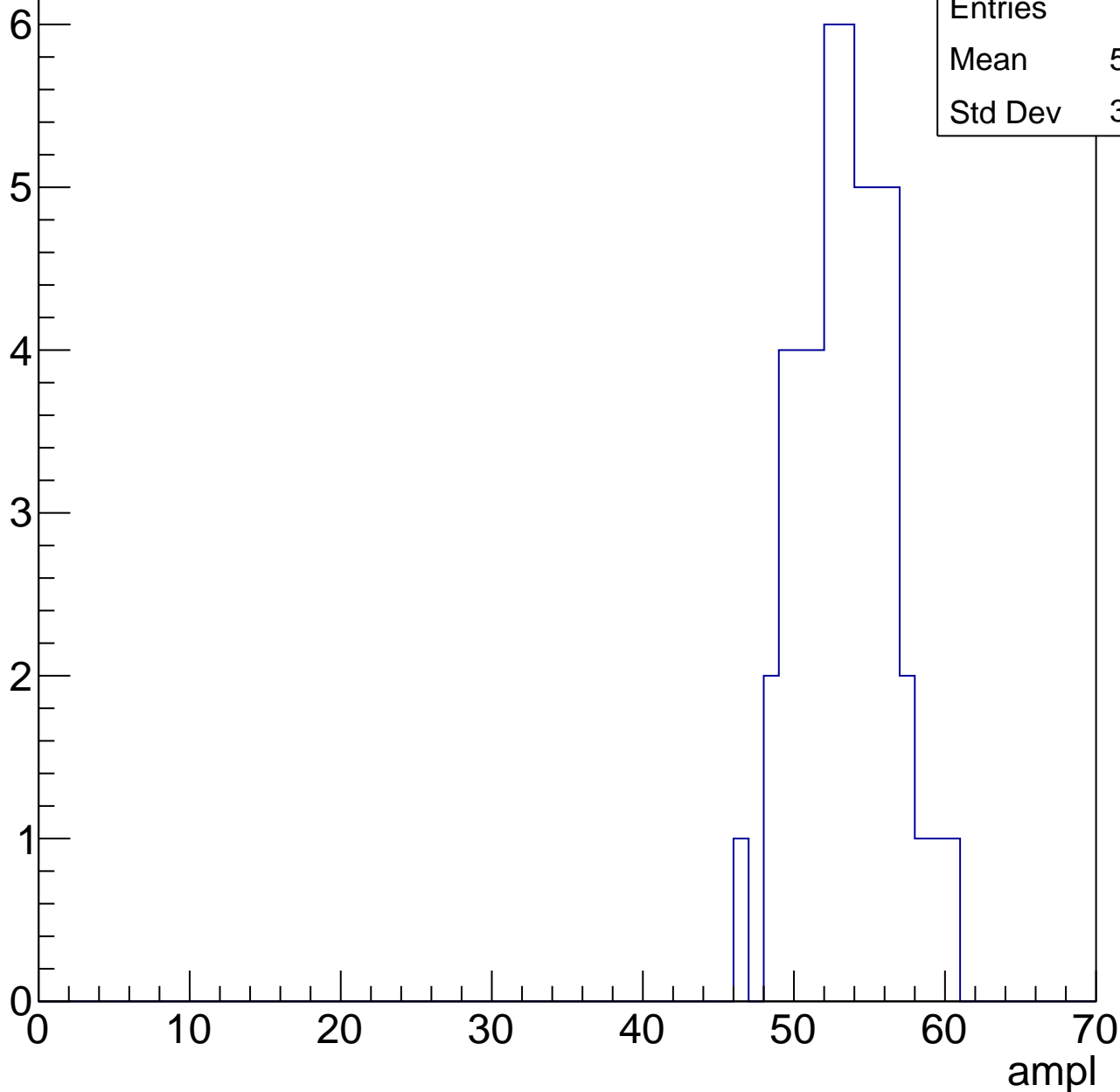


# B1L000S, U1-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	52.94
Std Dev	3.035

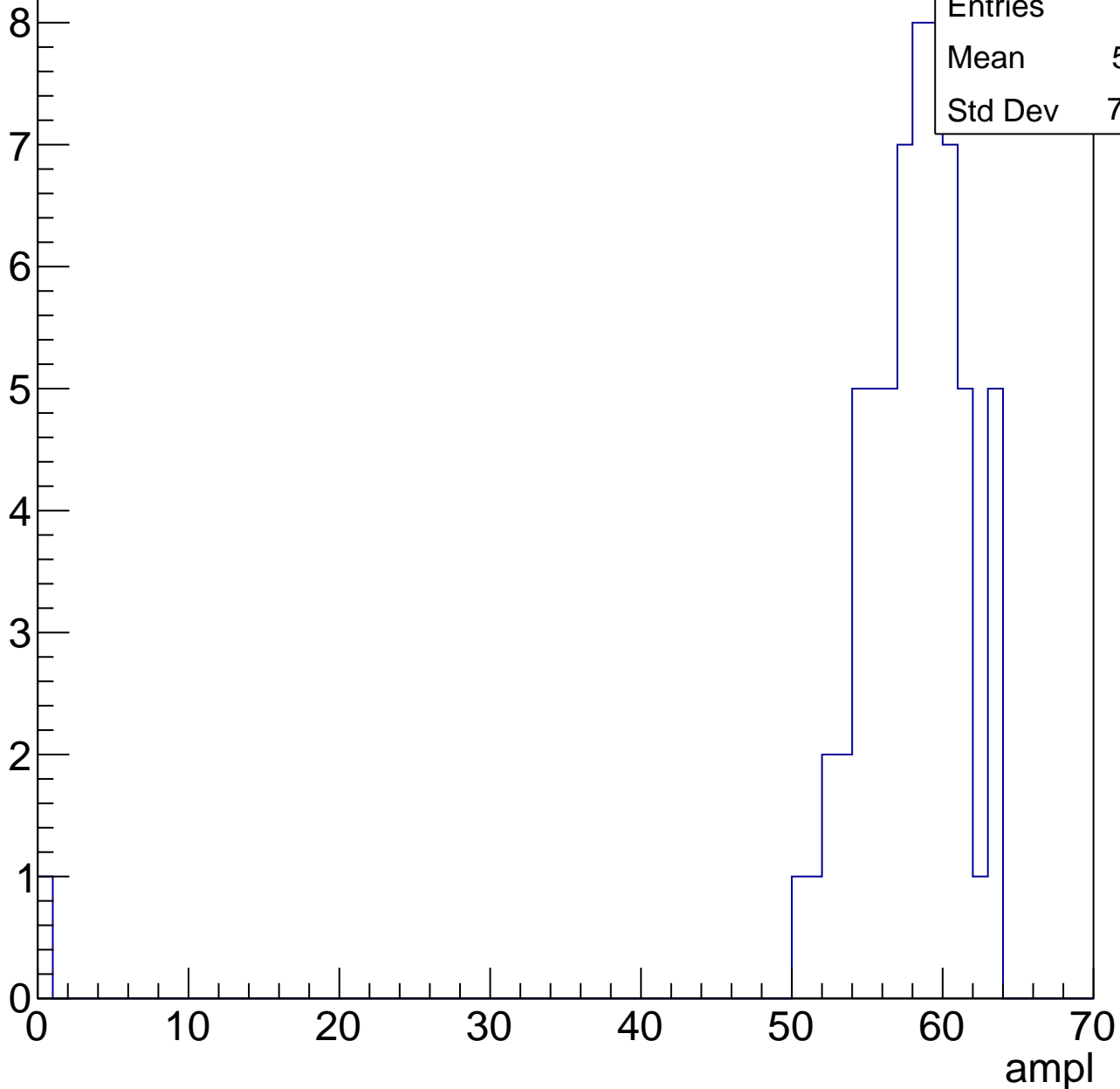


# B1L000S, U1-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	56.71
Std Dev	7.836

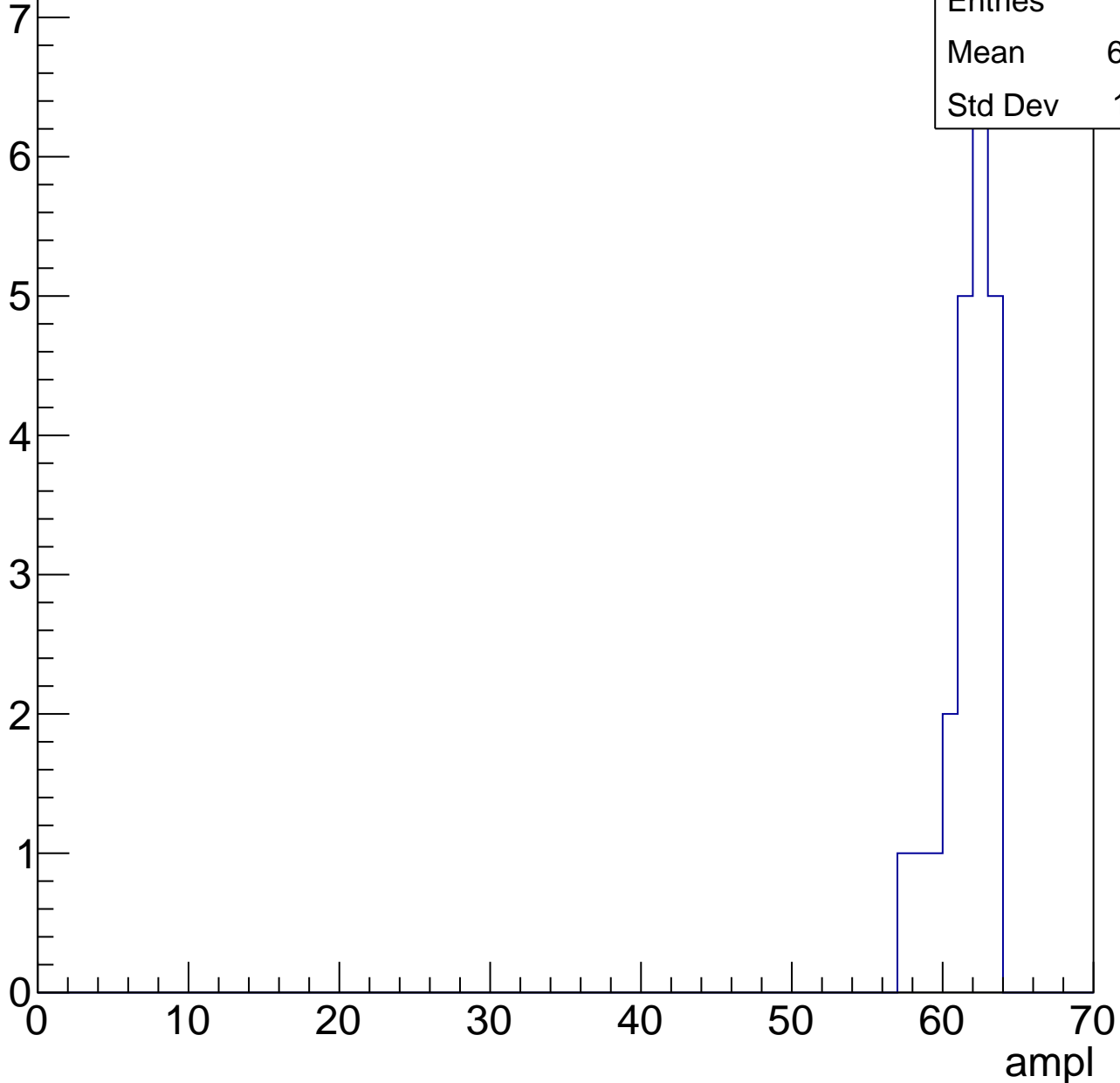


# B1L000S, U1-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	61.27
Std Dev	1.601





# B1L000S, U1-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch116, adc0

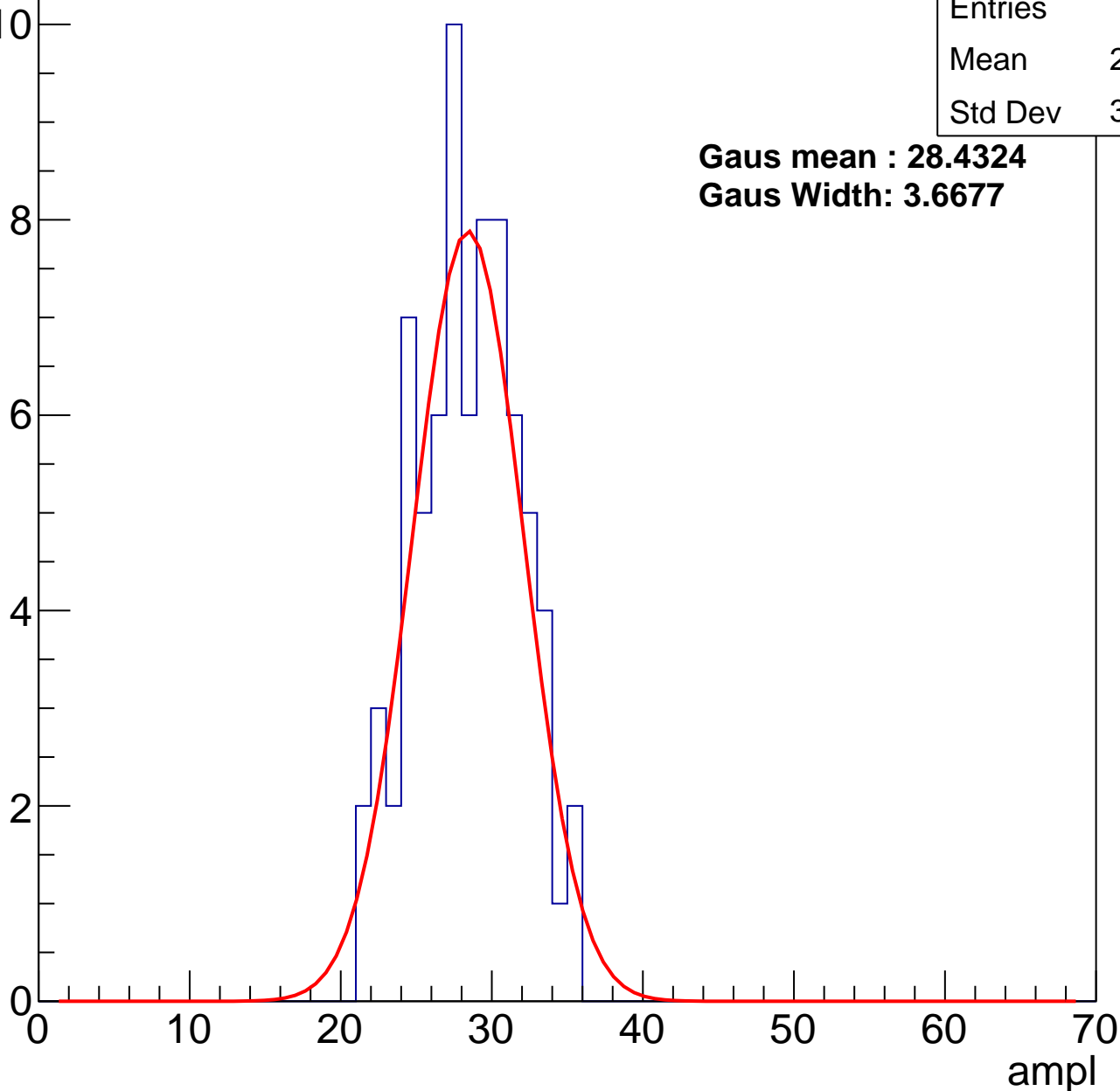
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	27.93
Std Dev	3.376

**Gaus mean : 28.4324**

**Gaus Width: 3.6677**



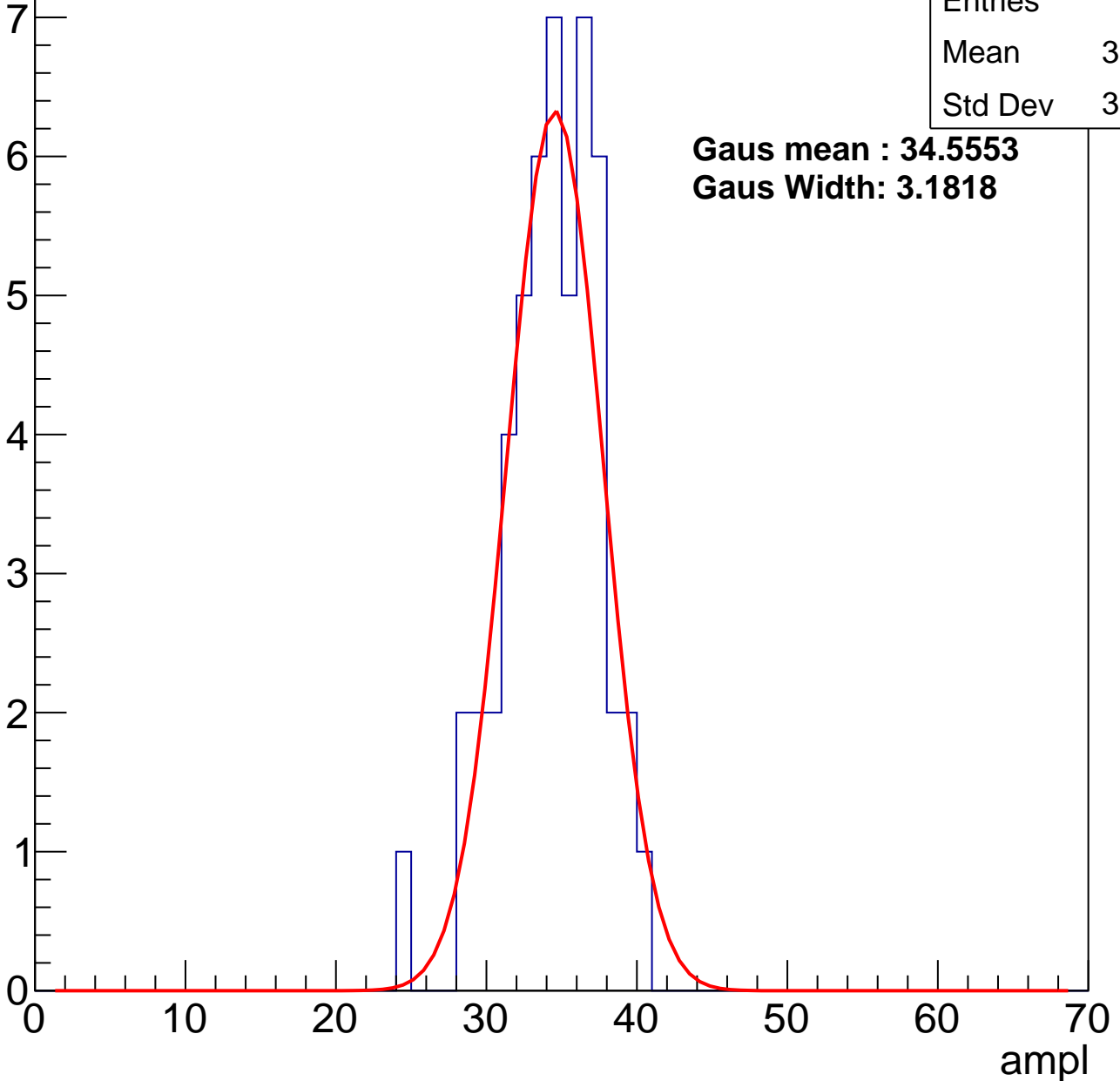
# B1L000S, U1-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	33.87
Std Dev	3.162

**Gaus mean : 34.5553**  
**Gaus Width: 3.1818**



# B1L000S, U1-ch116, adc2

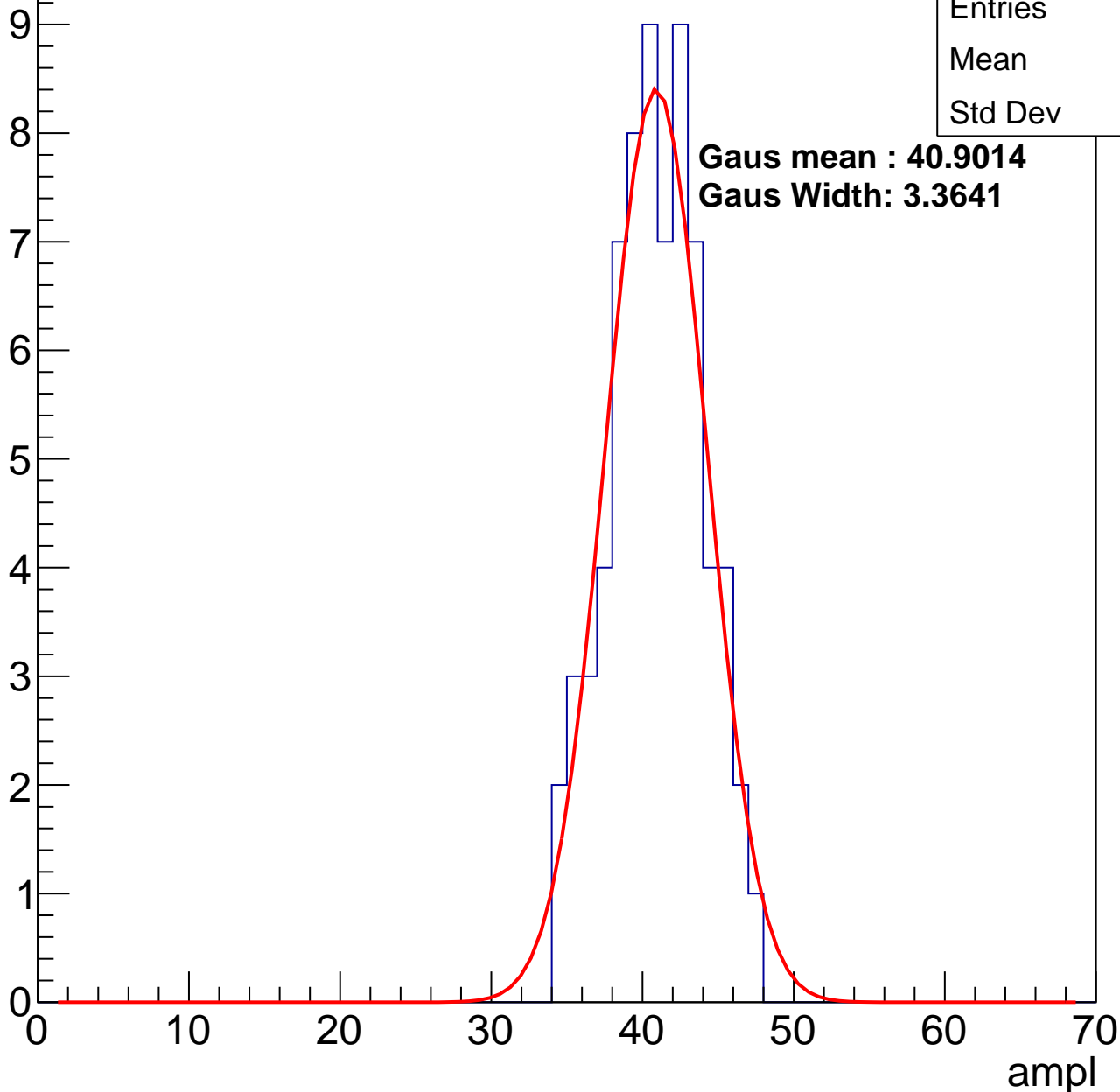
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	40.4
Std Dev	3.04

**Gaus mean : 40.9014**

**Gaus Width: 3.3641**

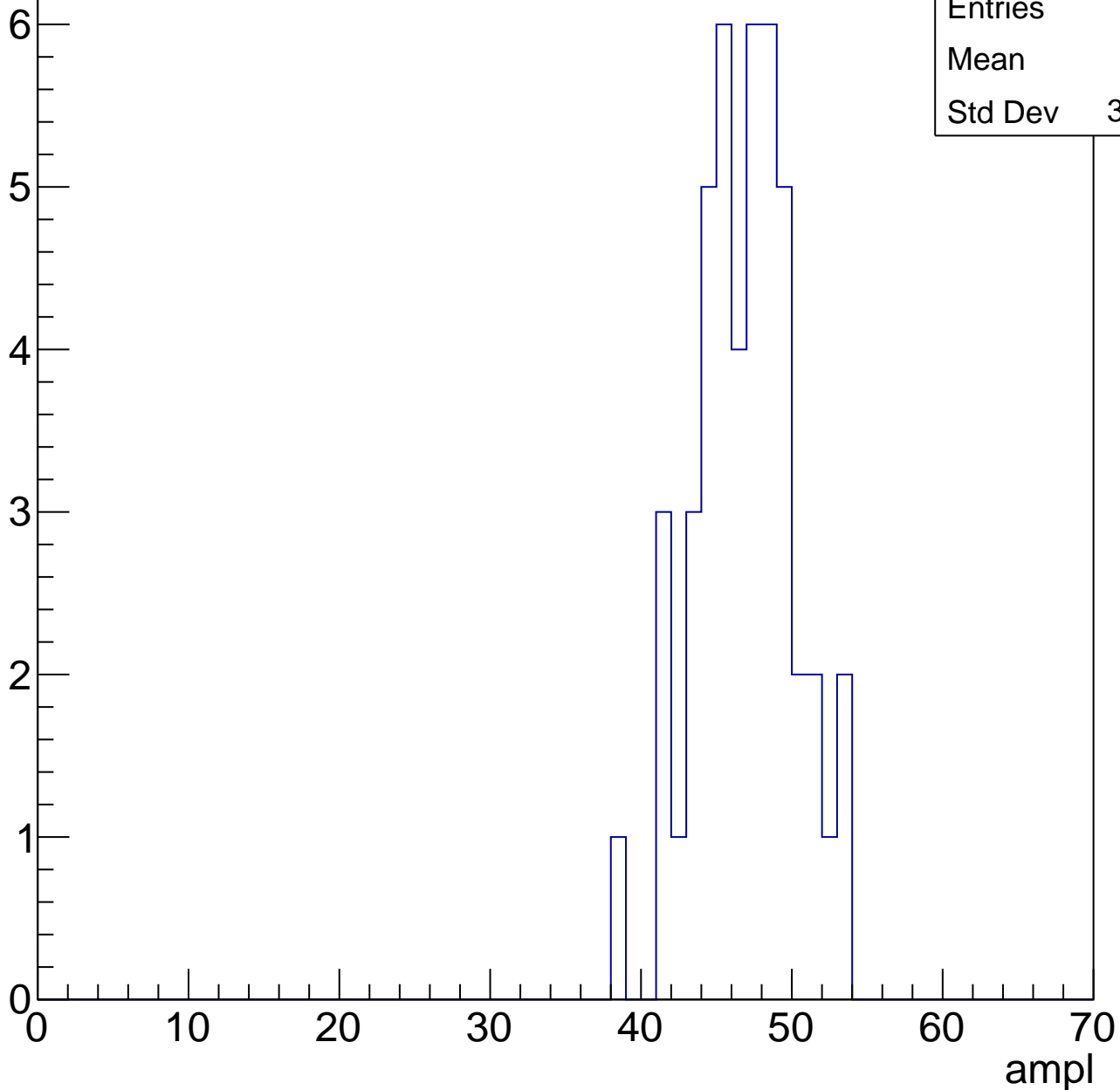


# B1L000S, U1-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	46.4
Std Dev	3.233

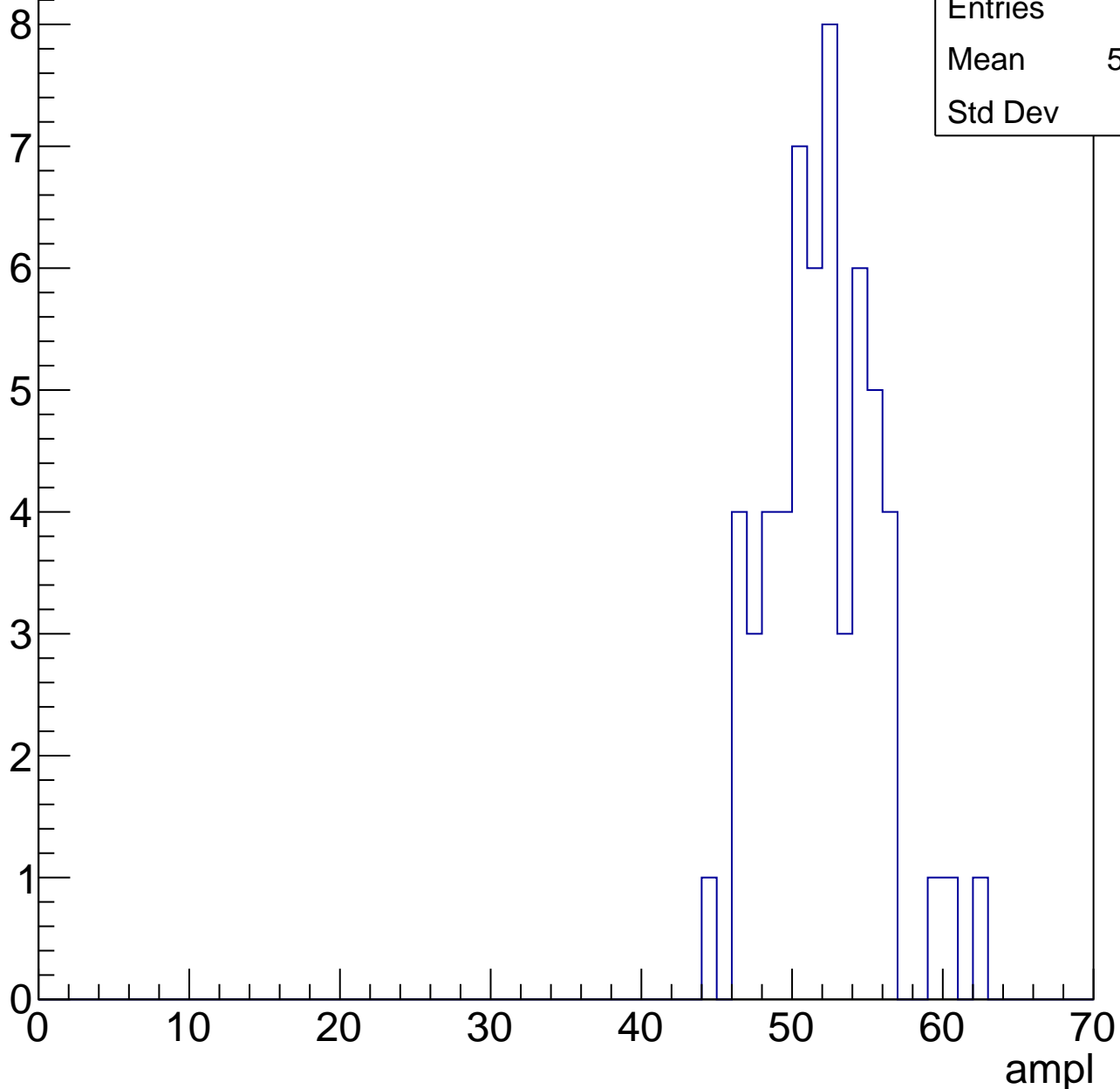


# B1L000S, U1-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	51.59
Std Dev	3.61

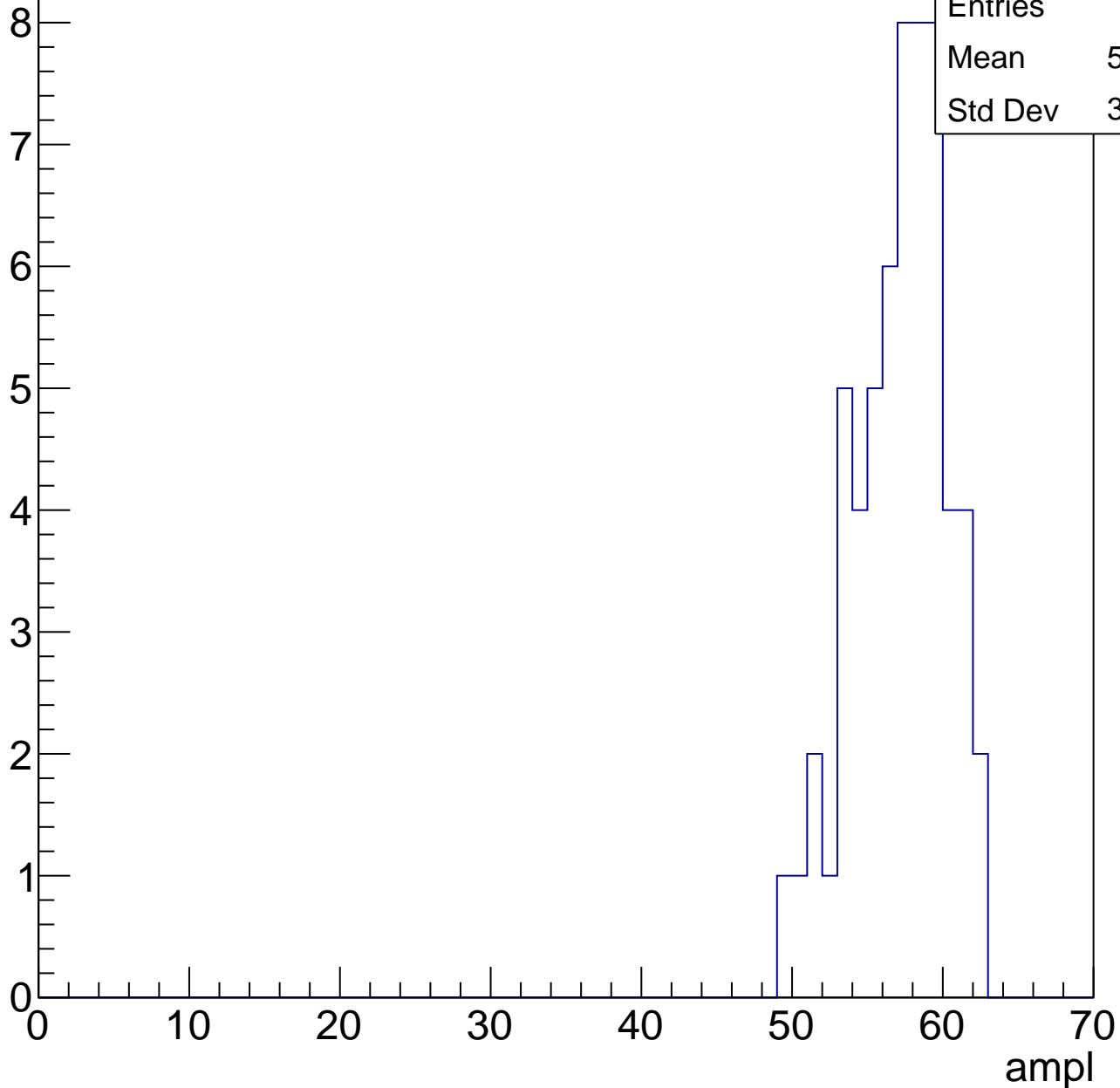


# B1L000S, U1-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	56.69
Std Dev	3.016

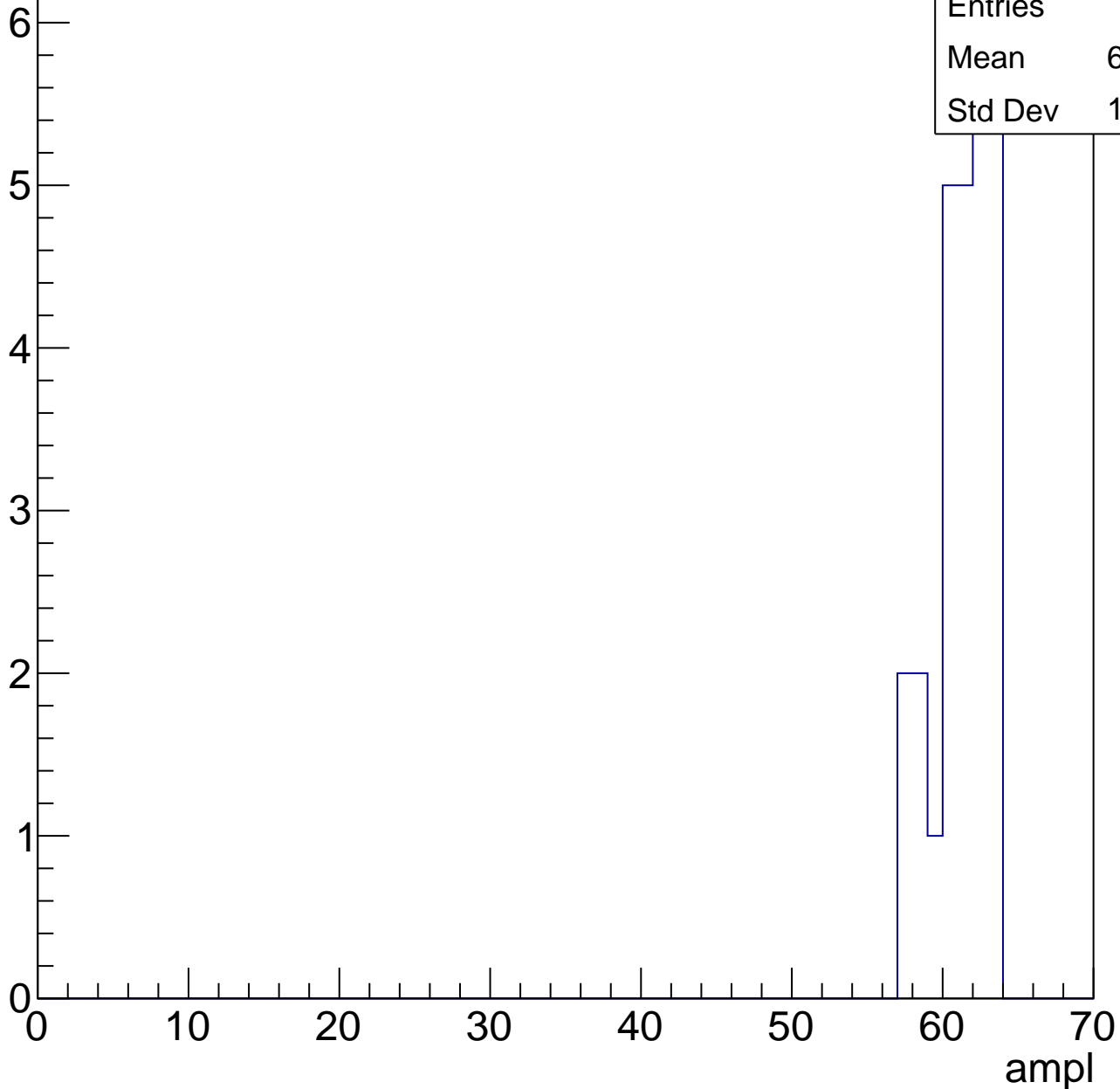


# B1L000S, U1-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	60.89
Std Dev	1.812

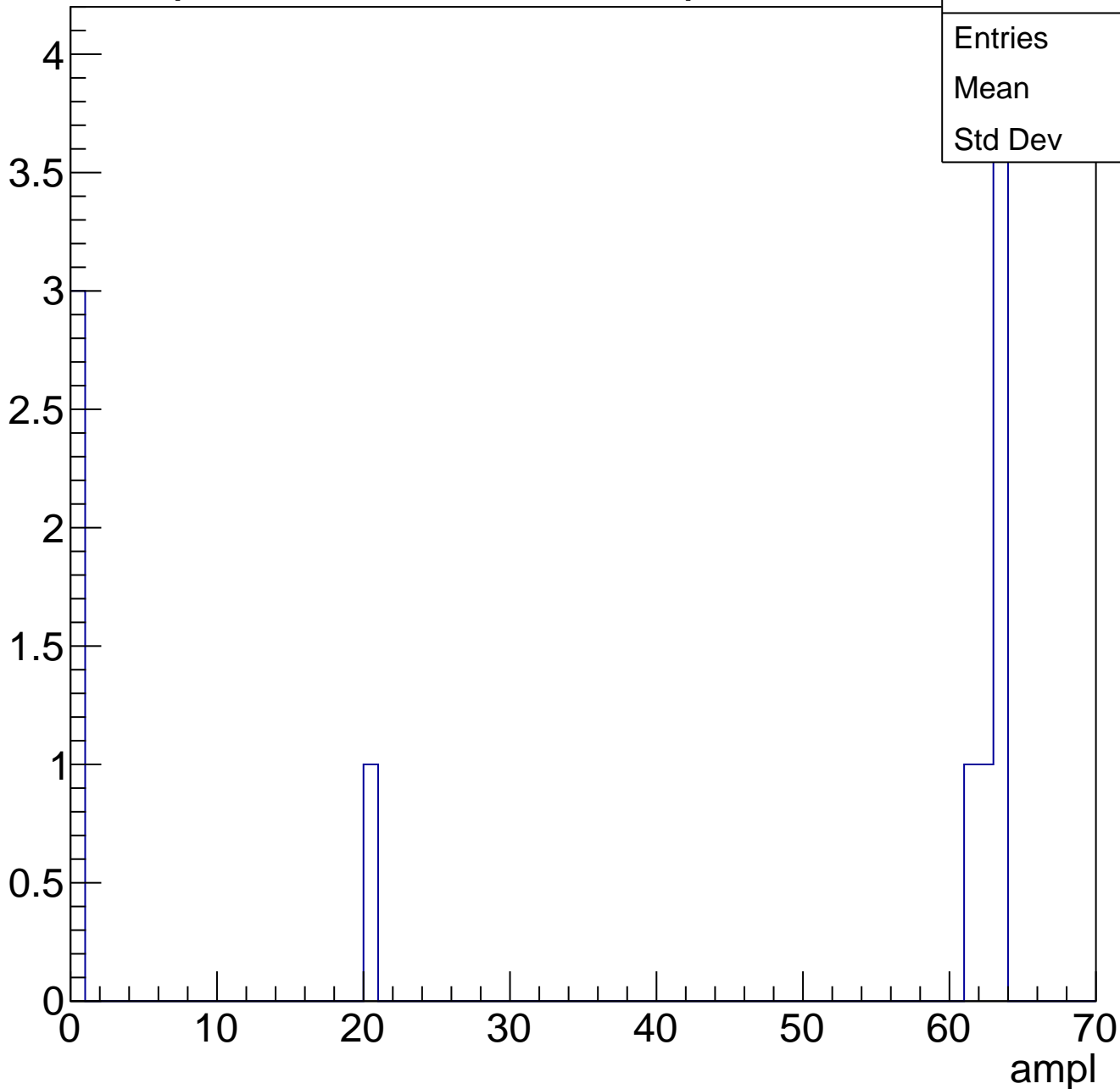




# B1L000S, U1-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	10
Mean	39.5
Std Dev	28.7

# B1L000S, U1-ch117, adc0

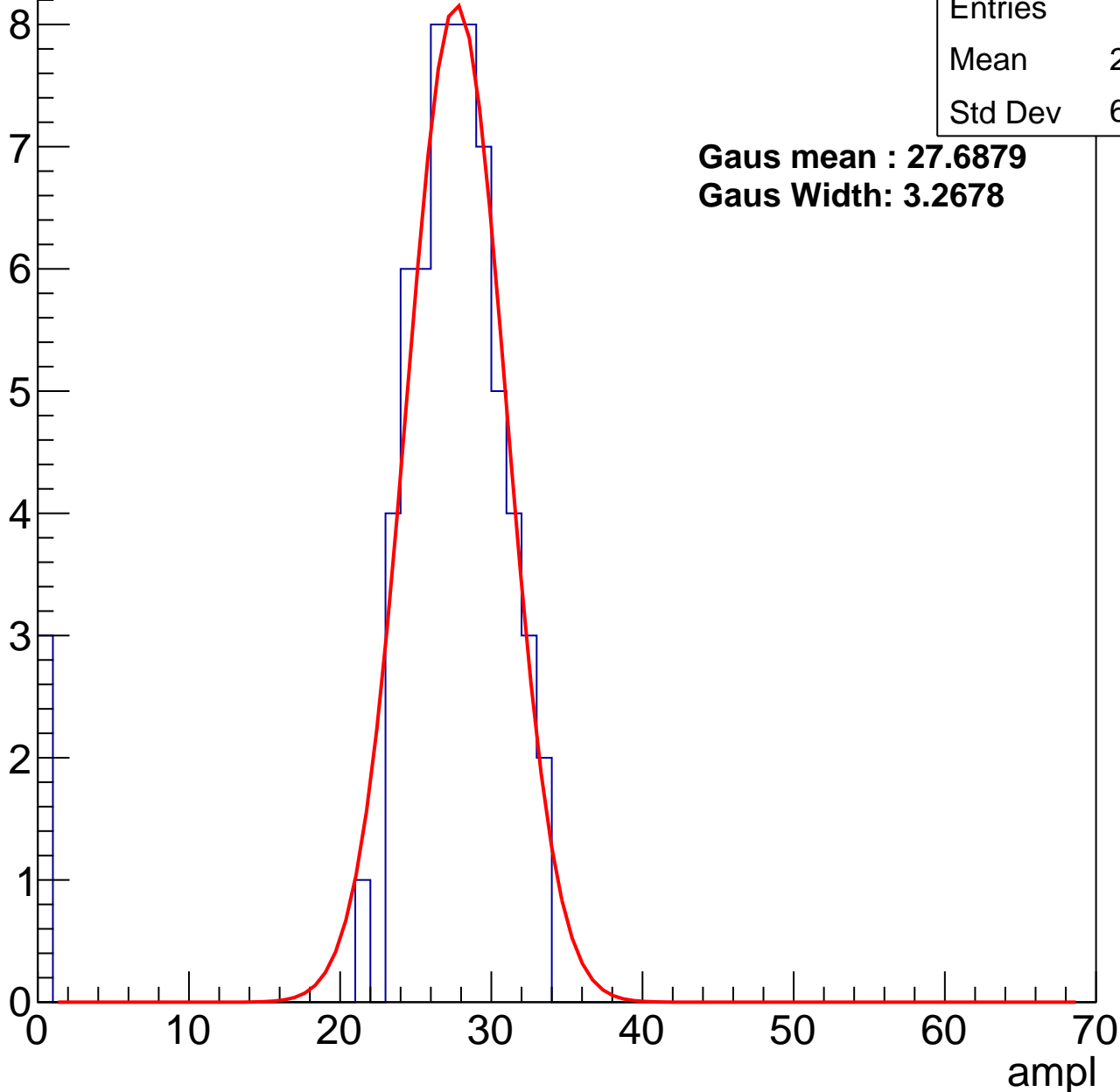
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	26.06
Std Dev	6.334

**Gaus mean : 27.6879**

**Gaus Width: 3.2678**



# B1L000S, U1-ch117, adc1

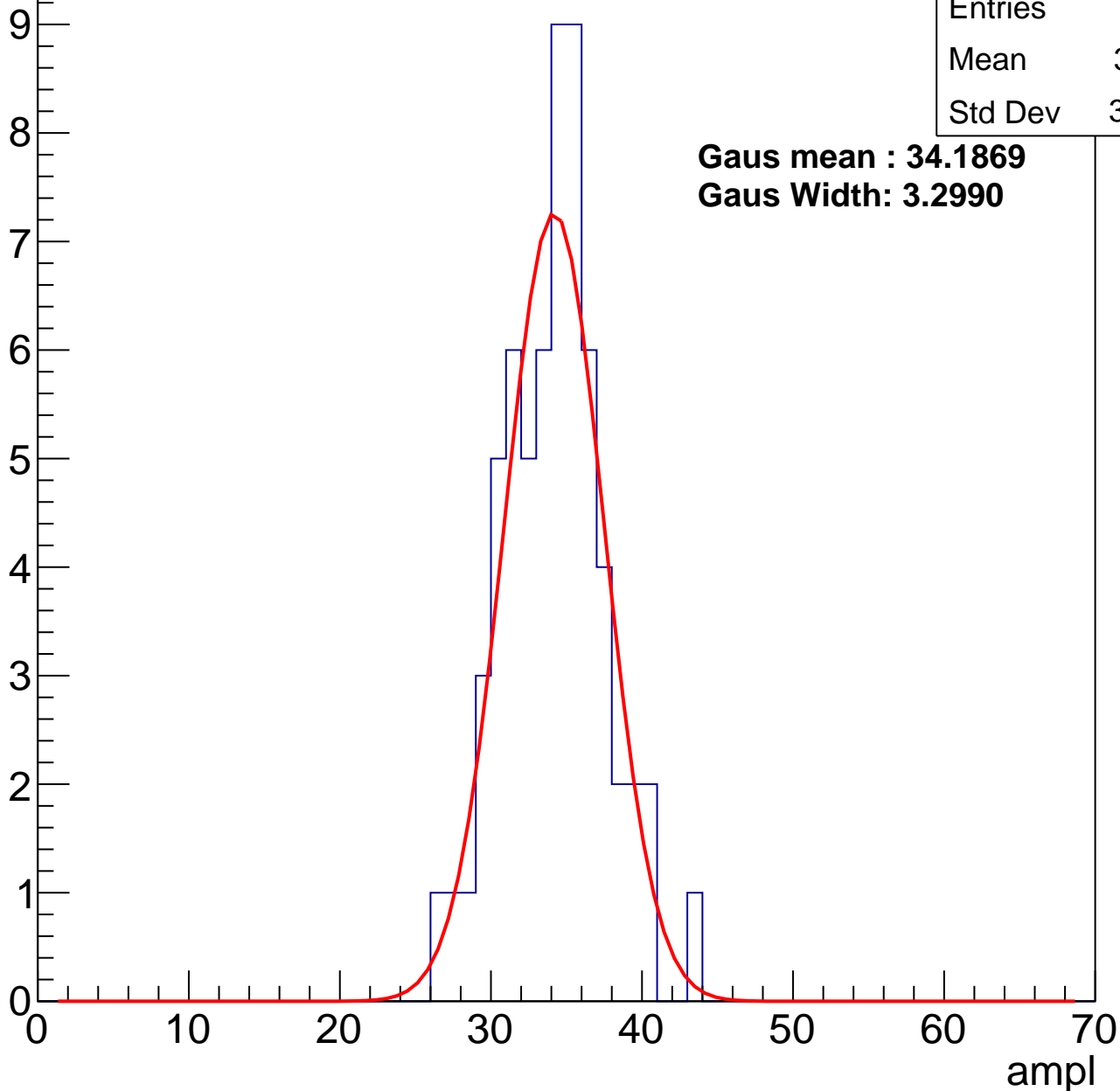
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	33.71
Std Dev	3.292

**Gaus mean : 34.1869**

**Gaus Width: 3.2990**



# B1L000S, U1-ch117, adc2

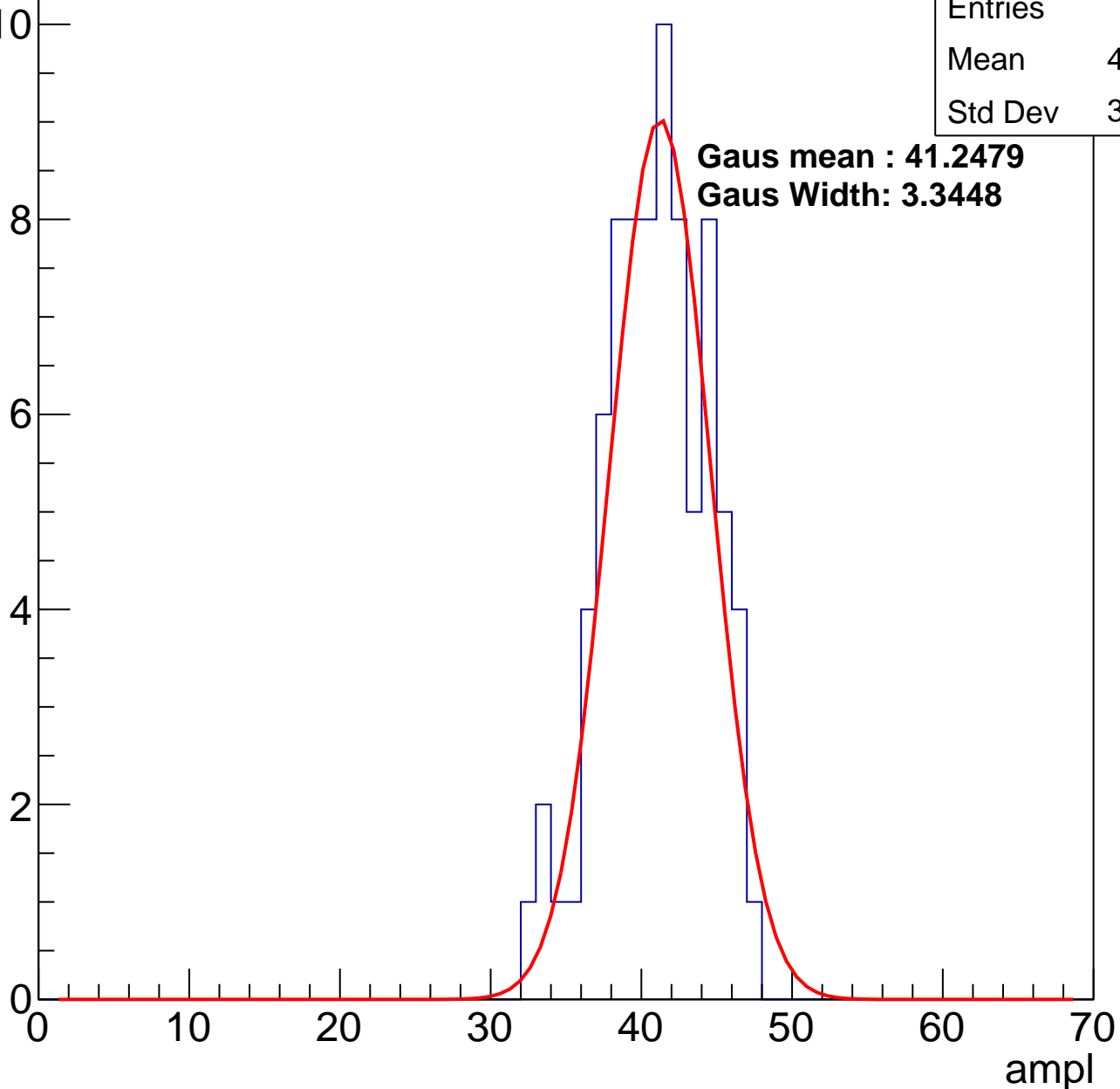
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	40.48
Std Dev	3.347

**Gaus mean : 41.2479**

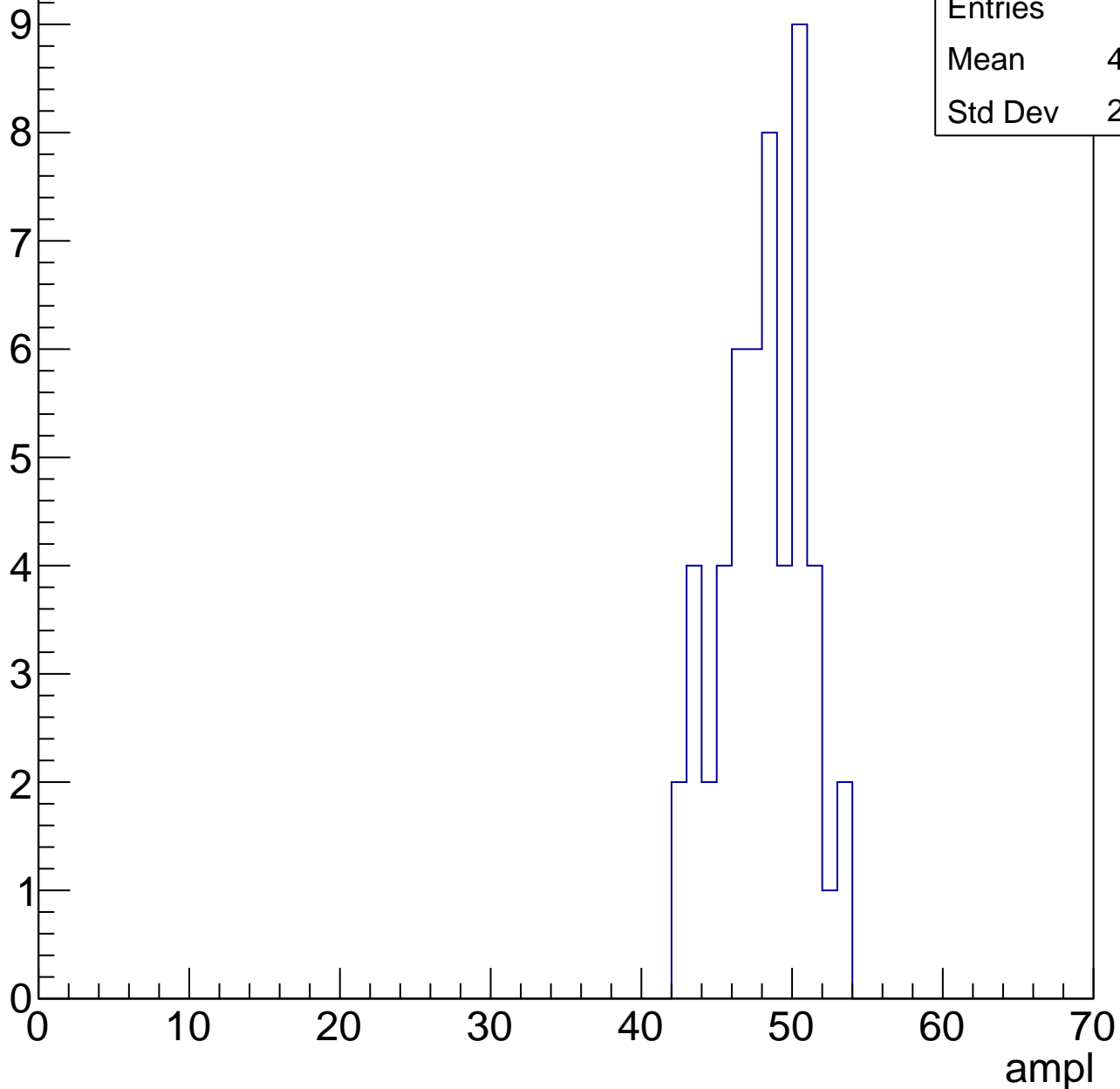
**Gaus Width: 3.3448**



# B1L000S, U1-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



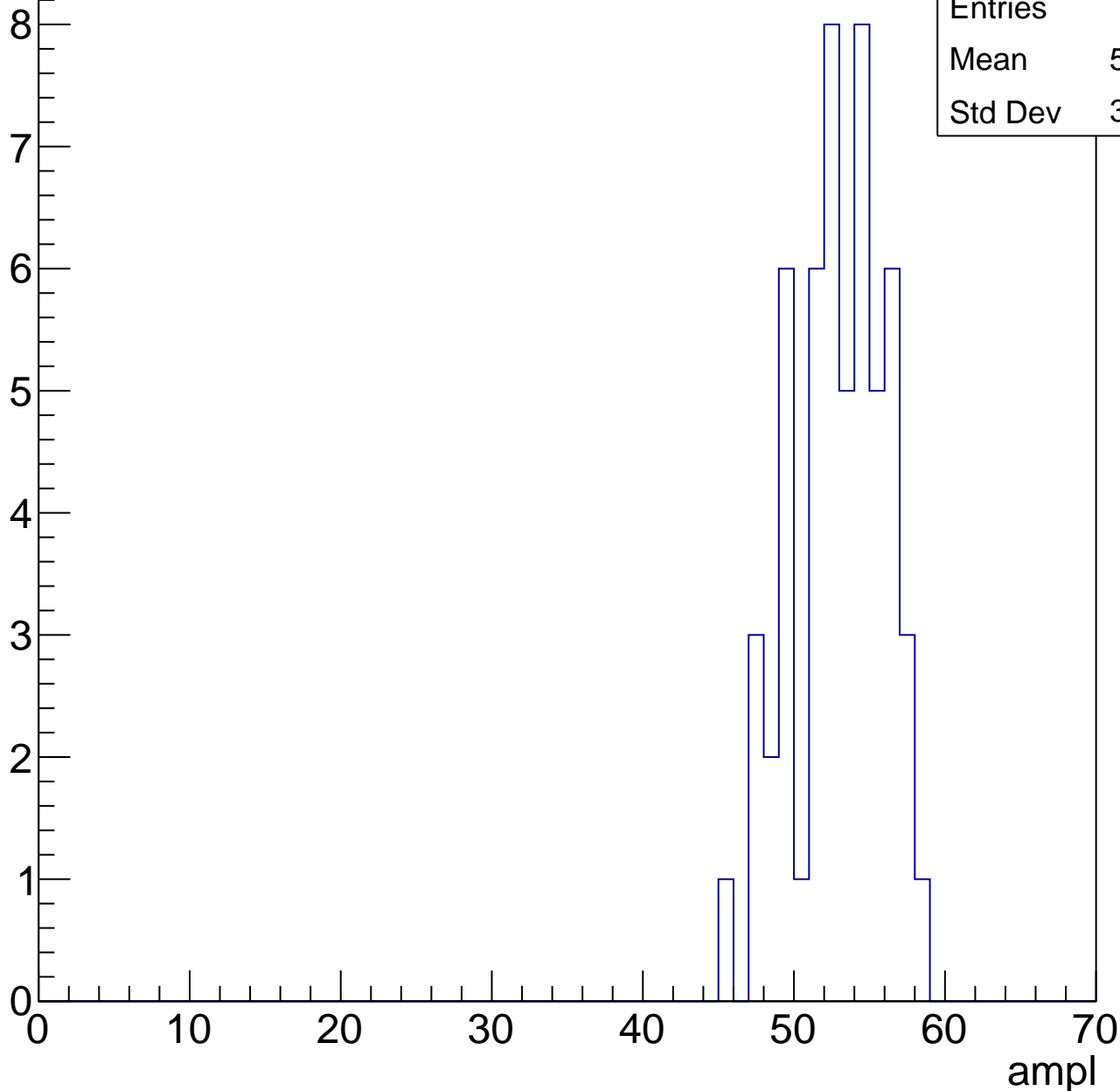
Entries	52
Mean	47.58
Std Dev	2.783

# B1L000S, U1-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	52.45
Std Dev	3.008

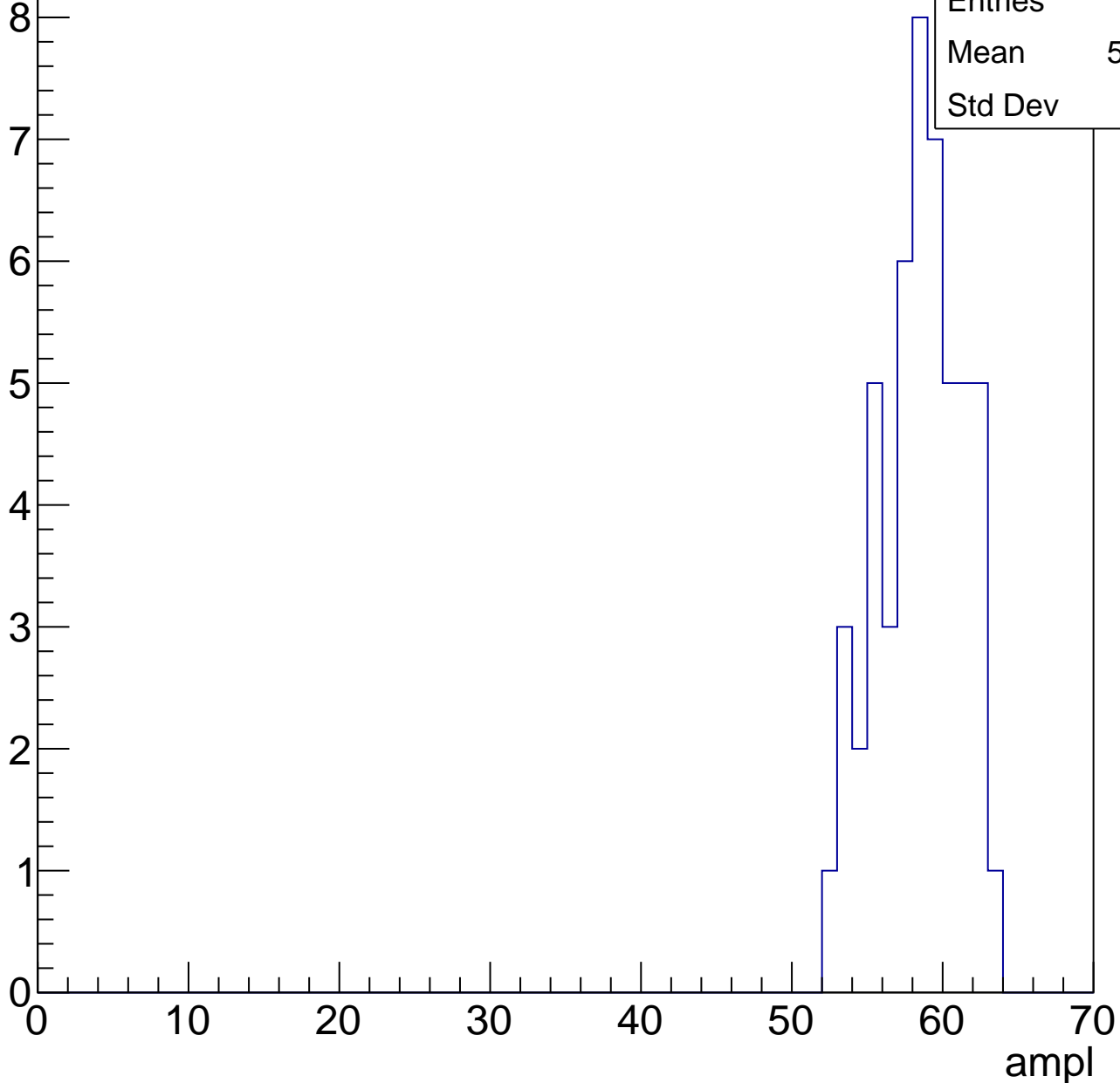


# B1L000S, U1-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	58.02
Std Dev	2.74

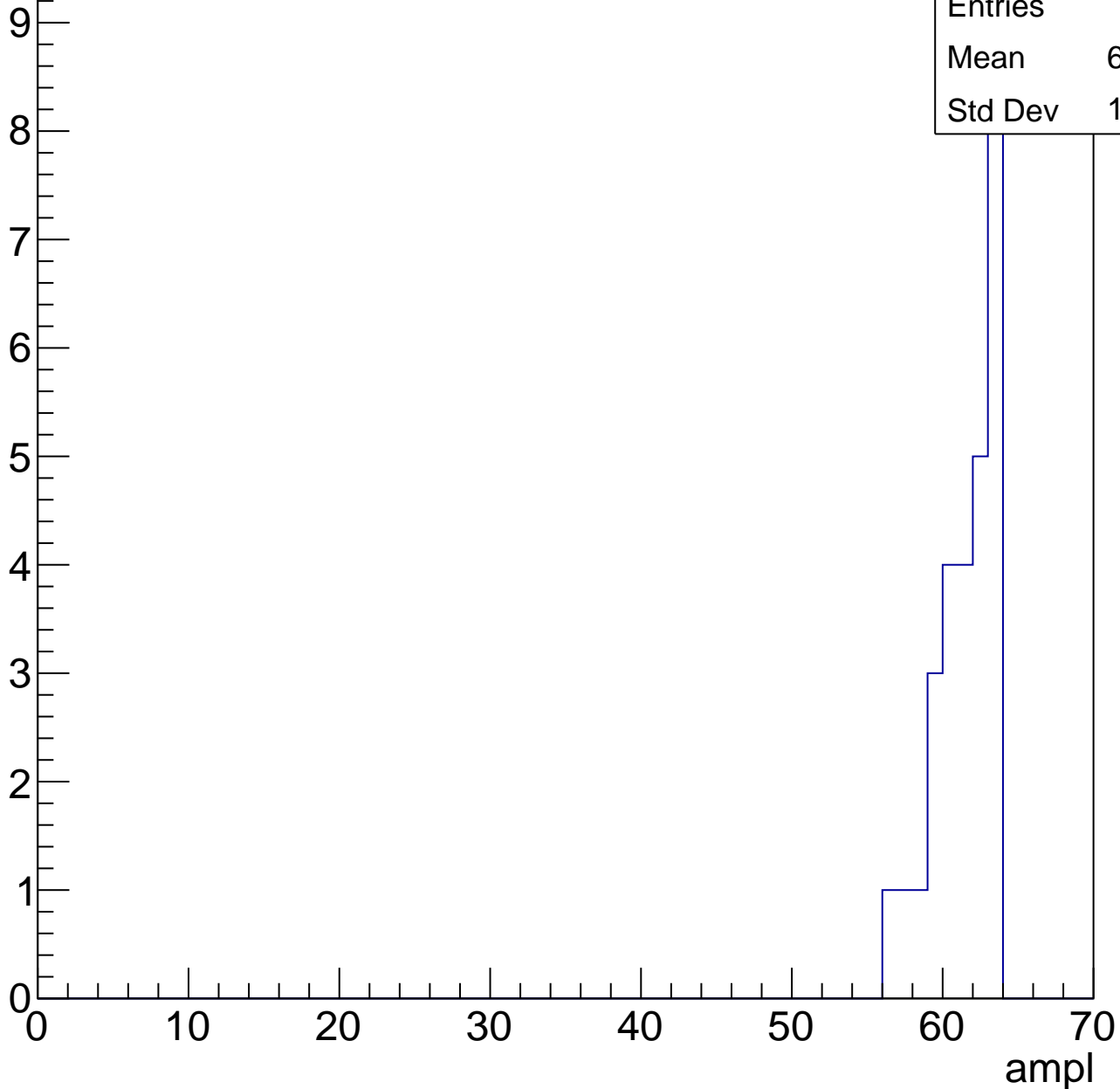


# B1L000S, U1-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	28
Mean	61.04
Std Dev	1.955

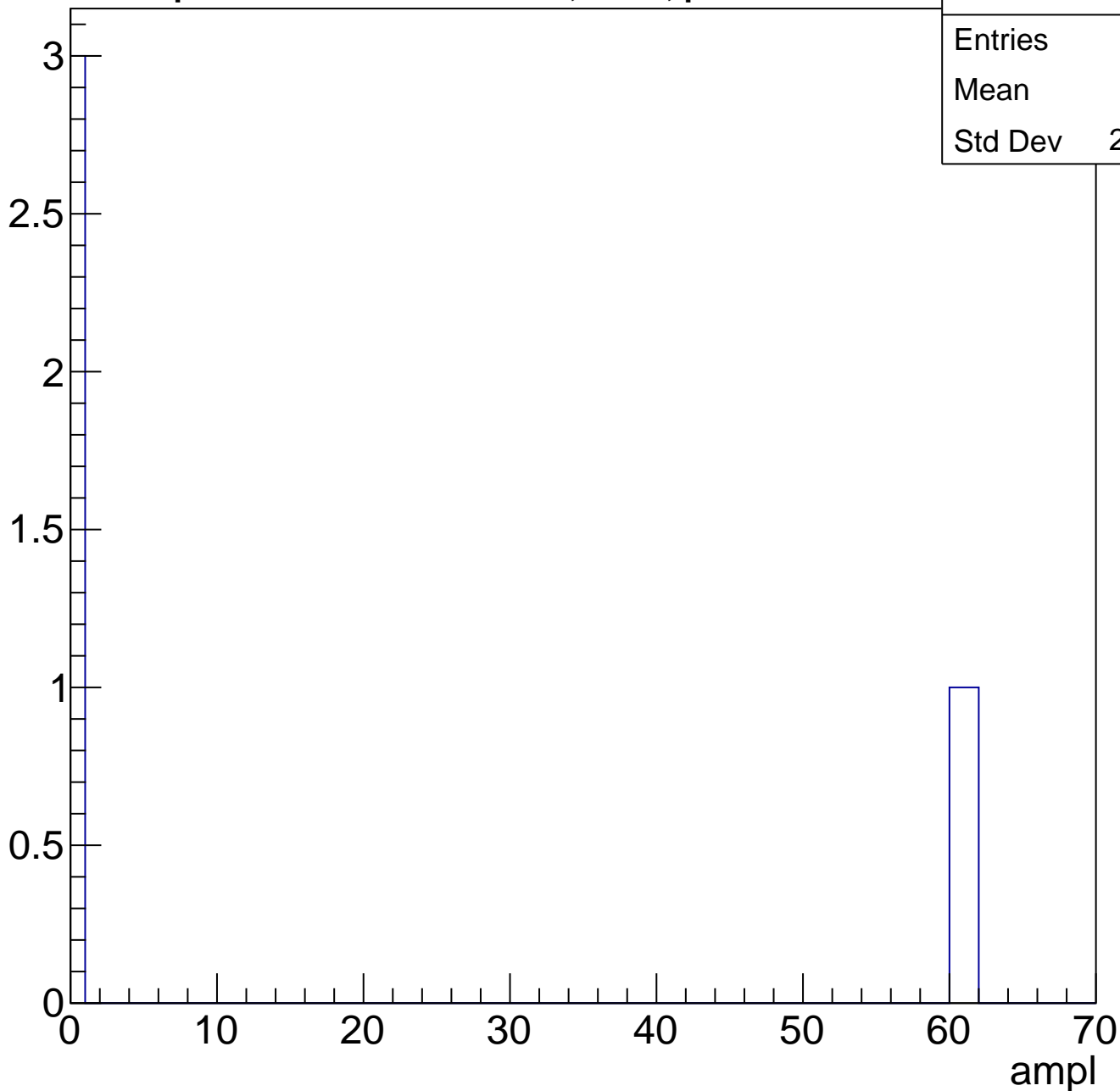




# B1L000S, U1-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch118, adc0

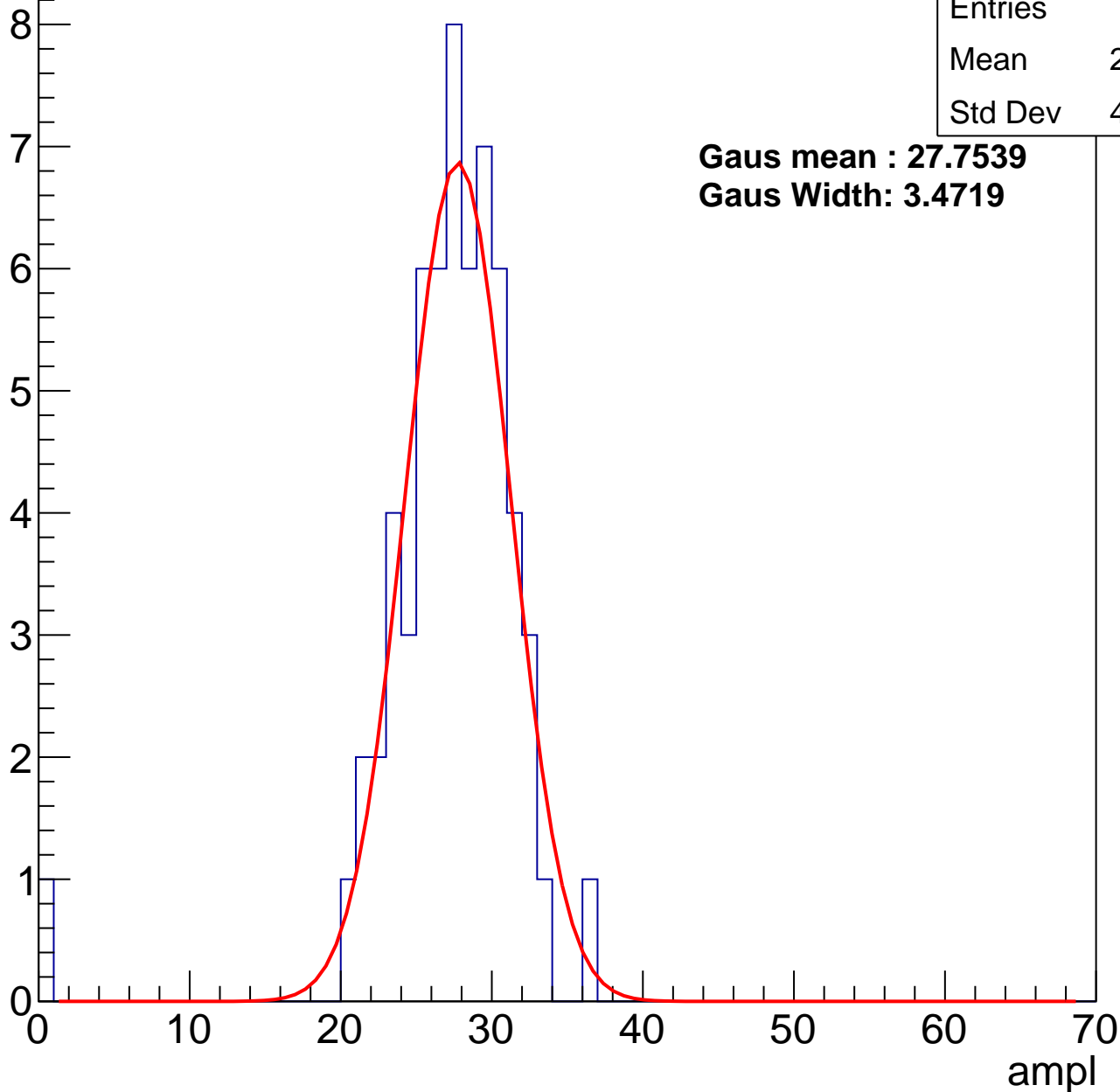
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	26.75
Std Dev	4.724

**Gaus mean : 27.7539**

**Gaus Width: 3.4719**



# B1L000S, U1-ch118, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	33.55
Std Dev	3.211

**Gaus mean : 33.5707**

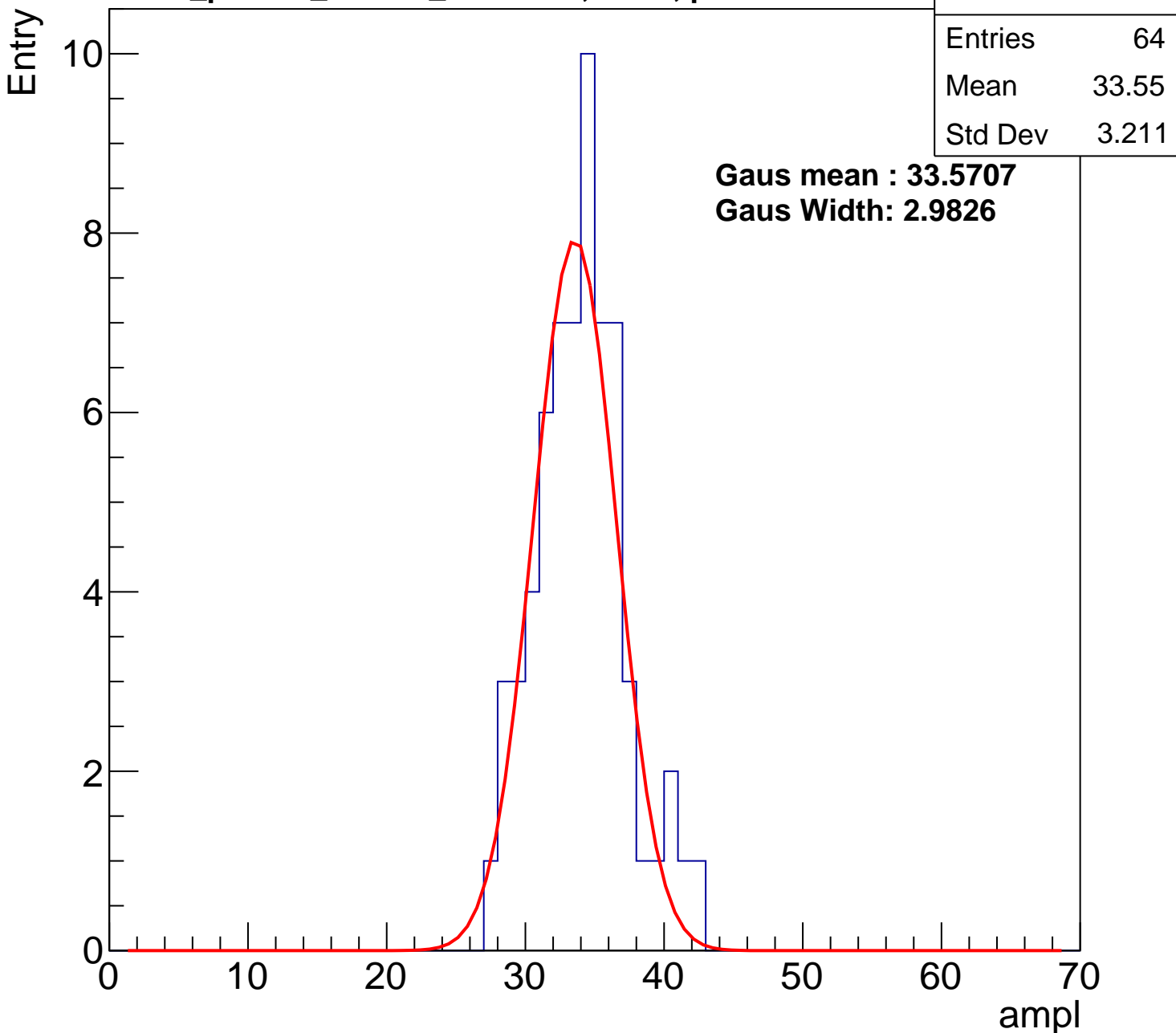
**Gaus Width: 2.9826**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U1-ch118, adc2

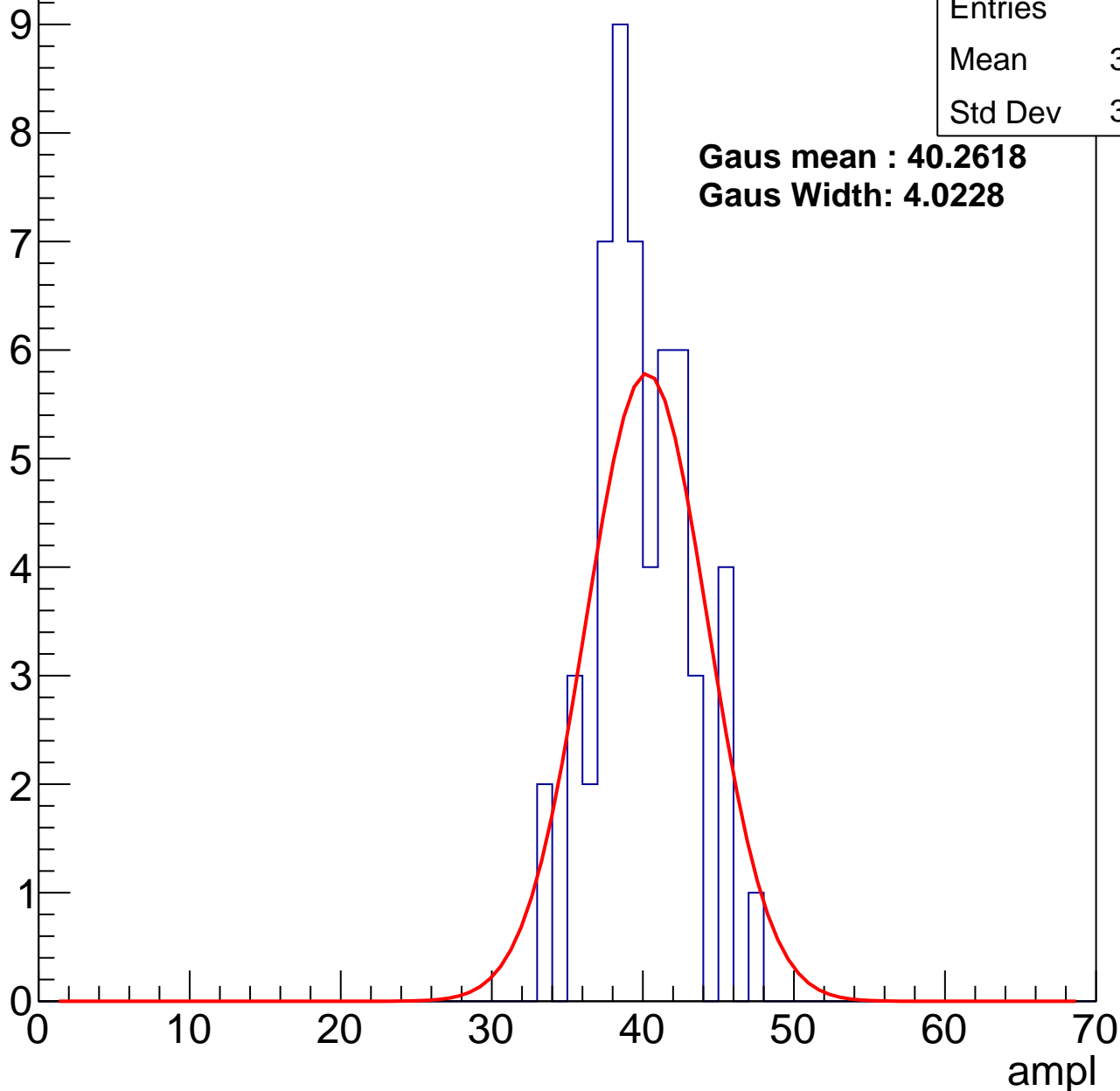
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	39.46
Std Dev	3.047

**Gaus mean : 40.2618**

**Gaus Width: 4.0228**



# B1L000S, U1-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

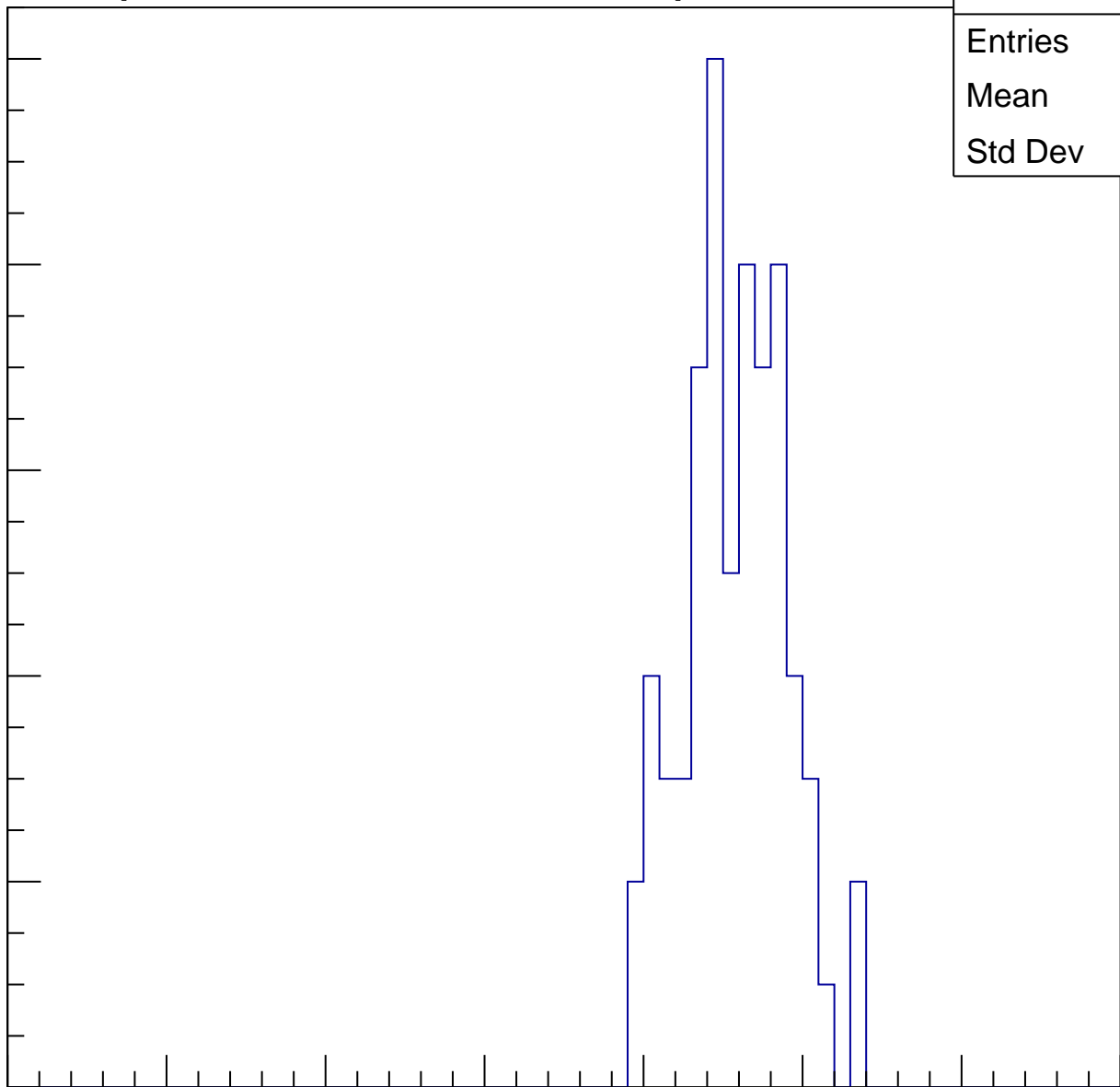
Entries	67
Mean	45.33
Std Dev	3.206

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

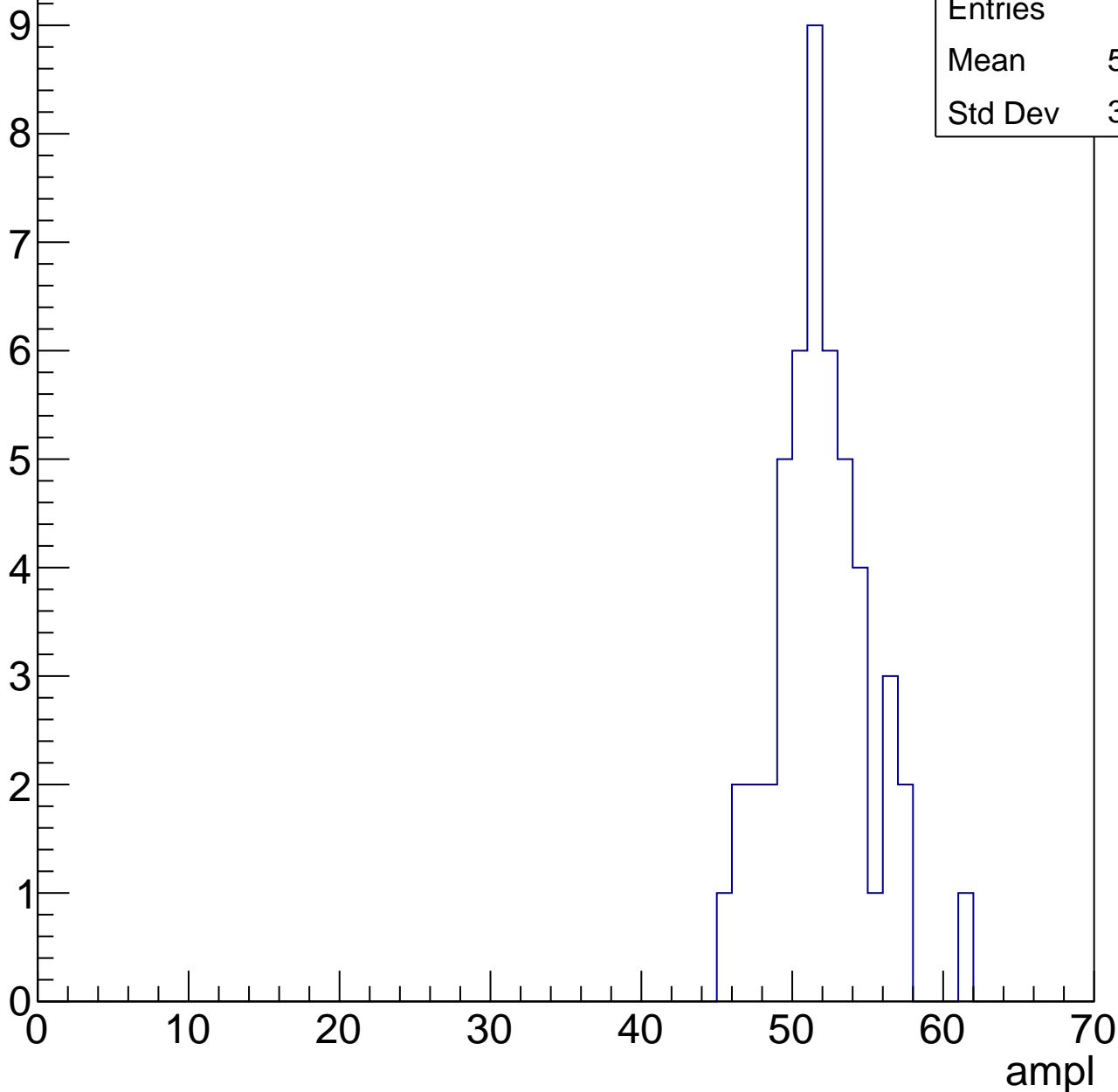


# B1L000S, U1-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	51.47
Std Dev	3.117

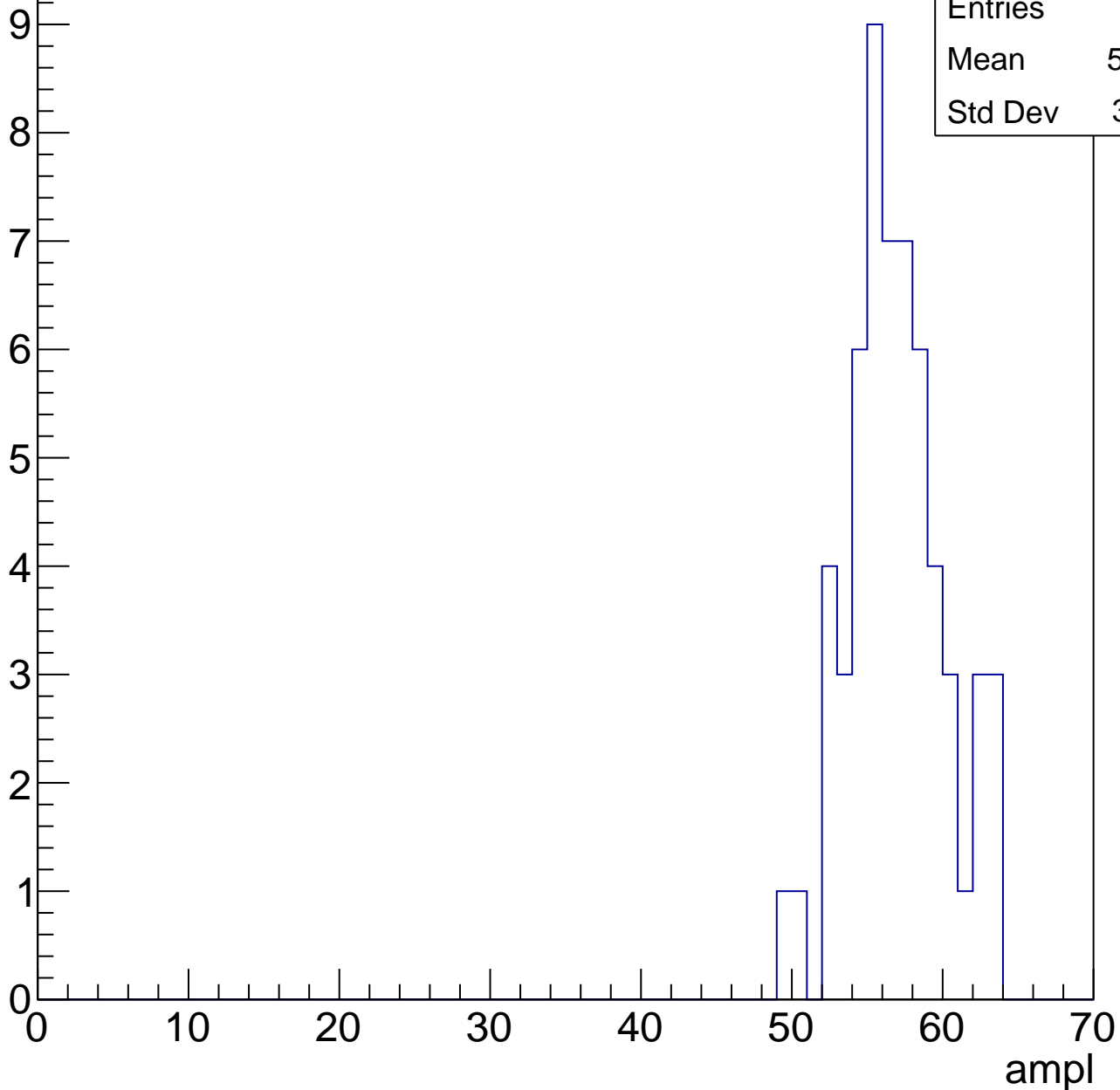


# B1L000S, U1-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	56.48
Std Dev	3.191

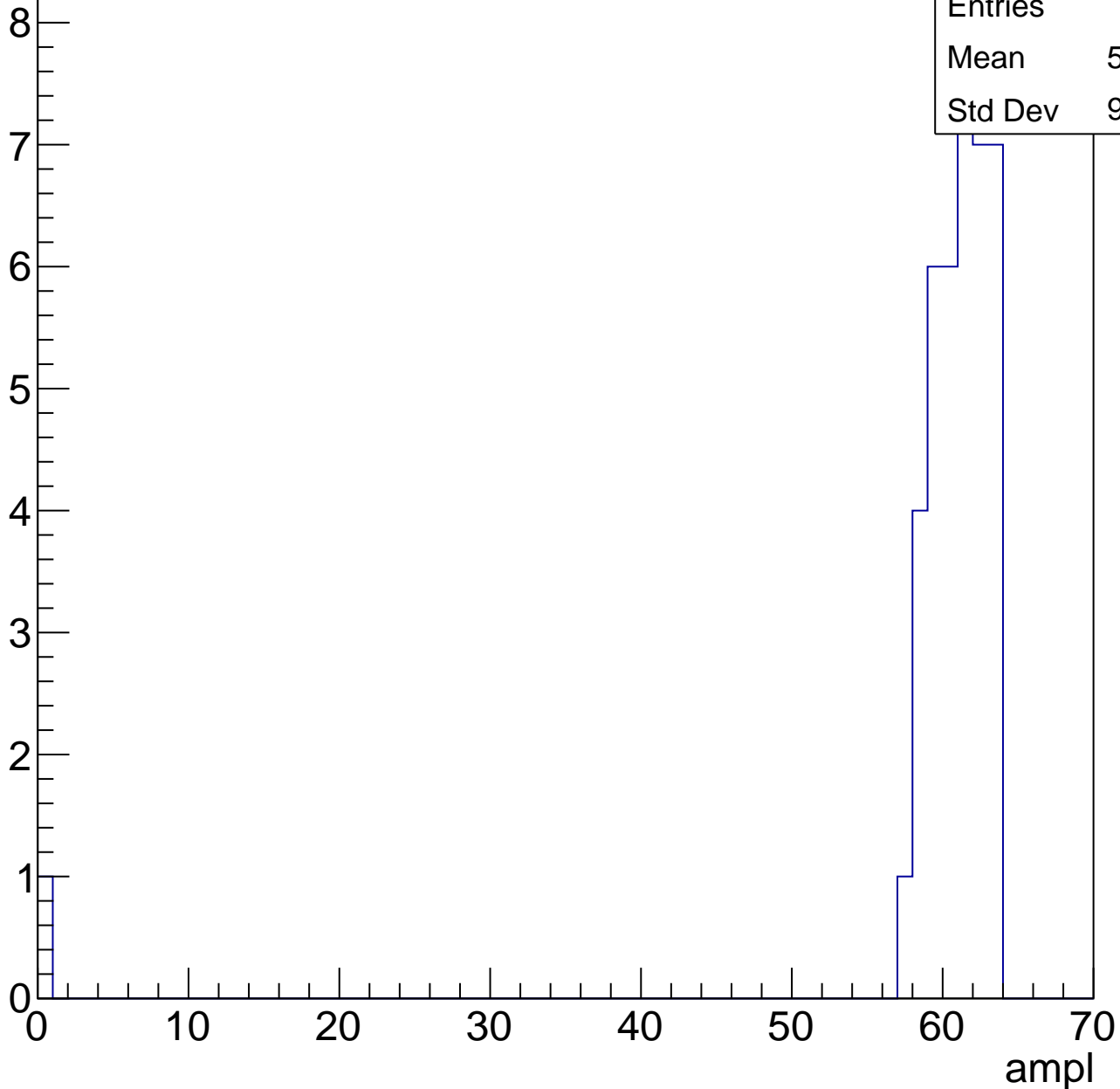


# B1L000S, U1-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	59.15
Std Dev	9.619





# B1L000S, U1-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch119, adc0

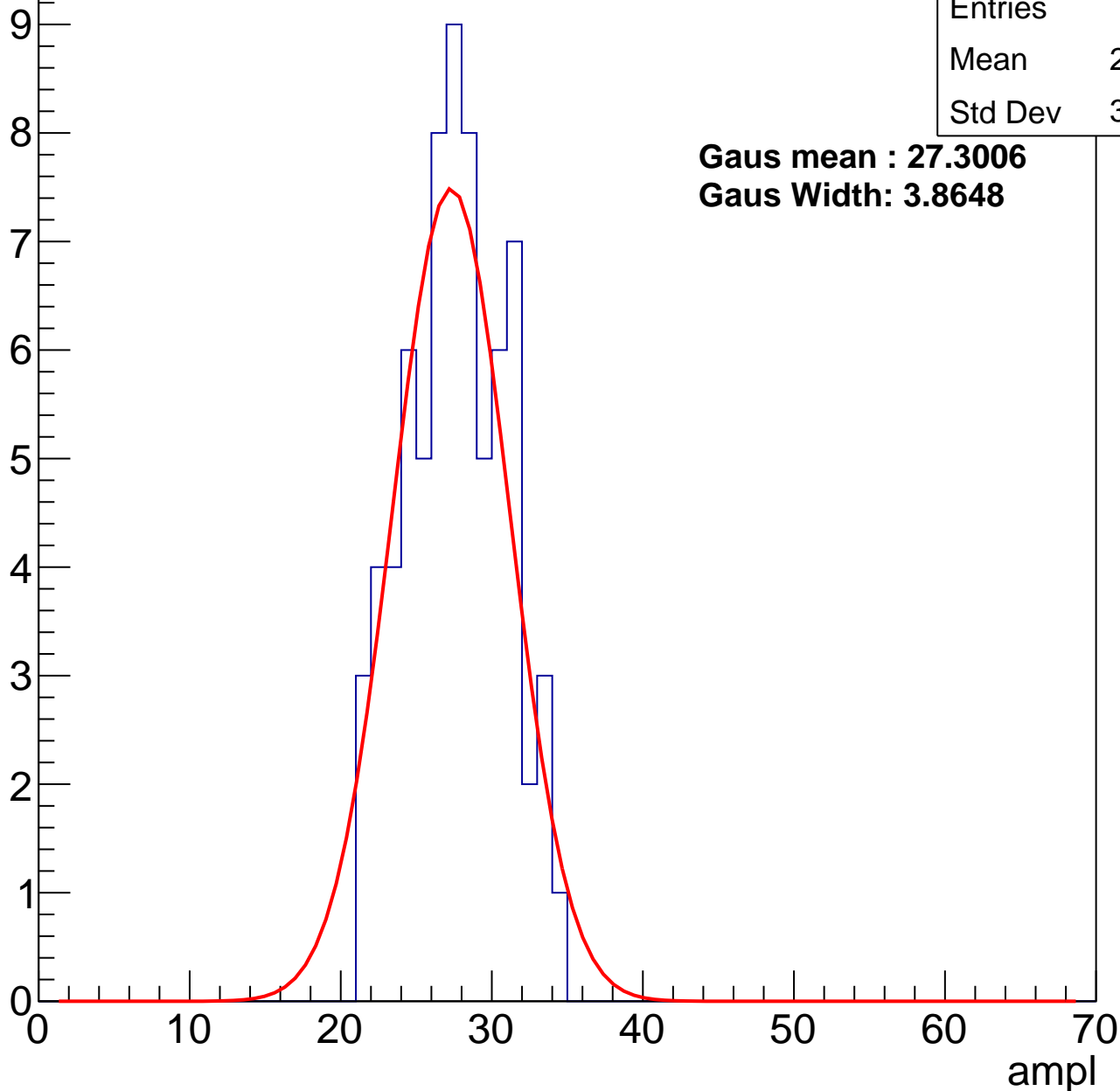
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	27.13
Std Dev	3.258

**Gaus mean : 27.3006**

**Gaus Width: 3.8648**



# B1L000S, U1-ch119, adc1

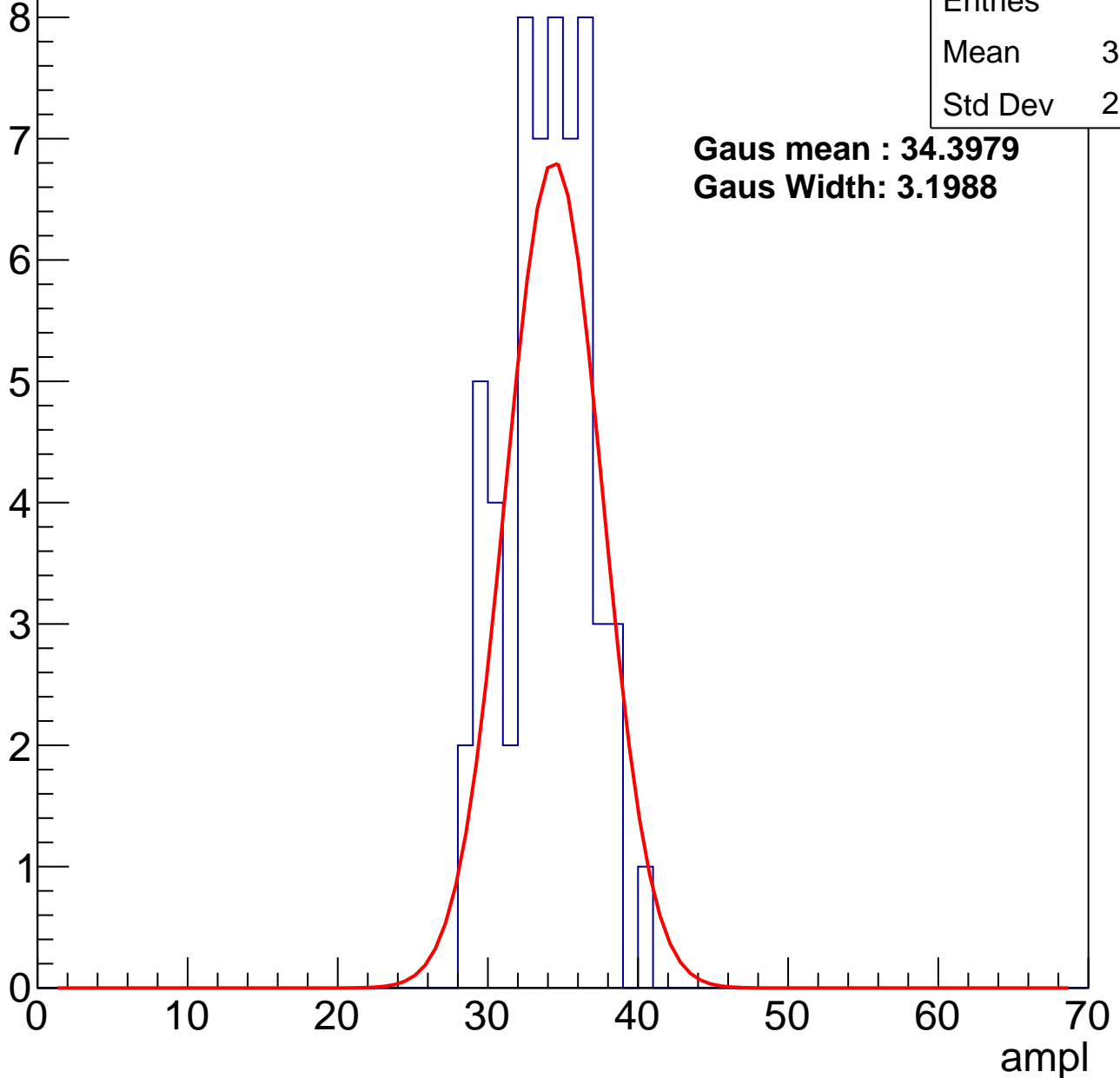
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	33.45
Std Dev	2.786

**Gaus mean : 34.3979**

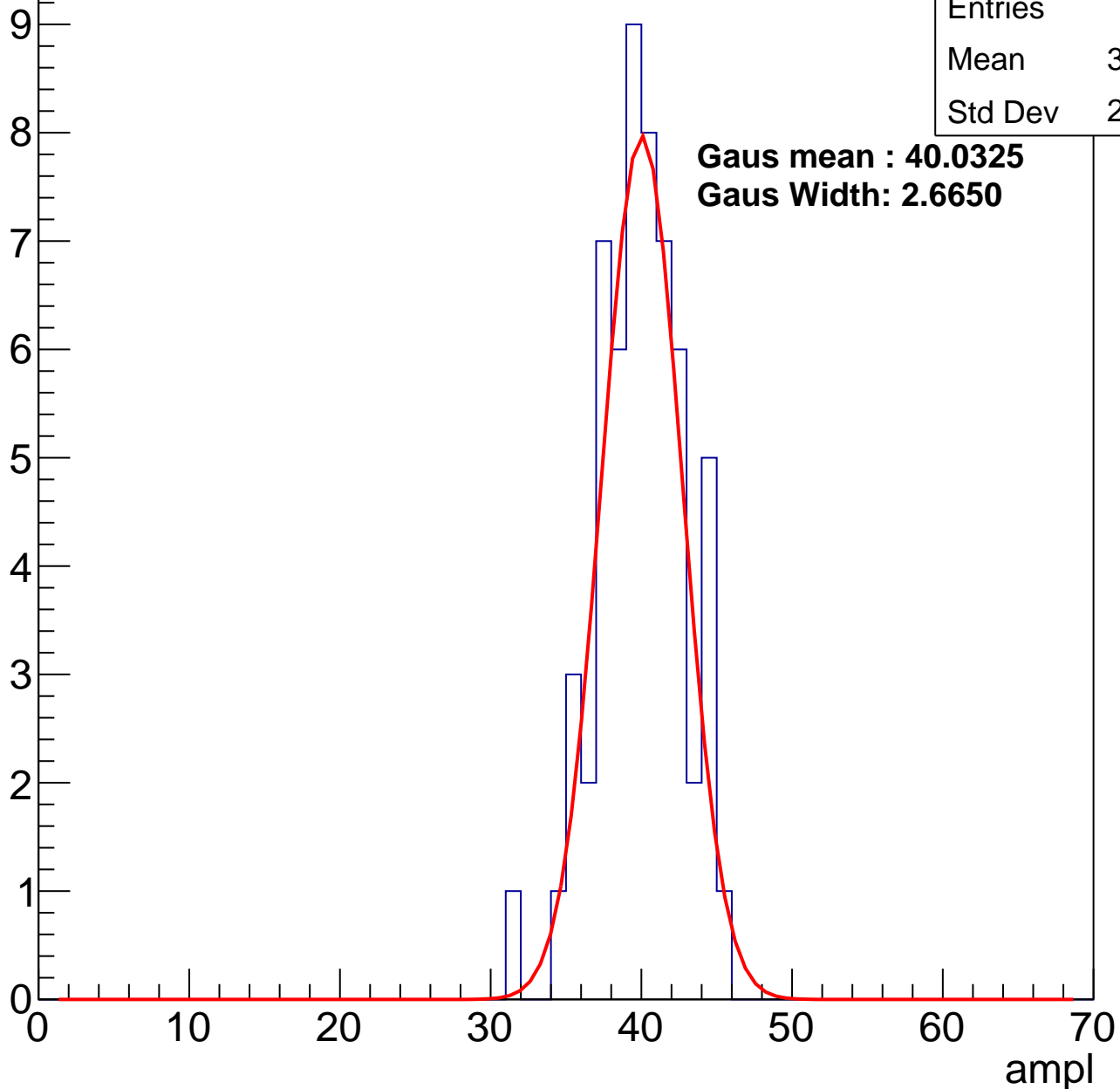
**Gaus Width: 3.1988**



# B1L000S, U1-ch119, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	58
Mean	39.48
Std Dev	2.812

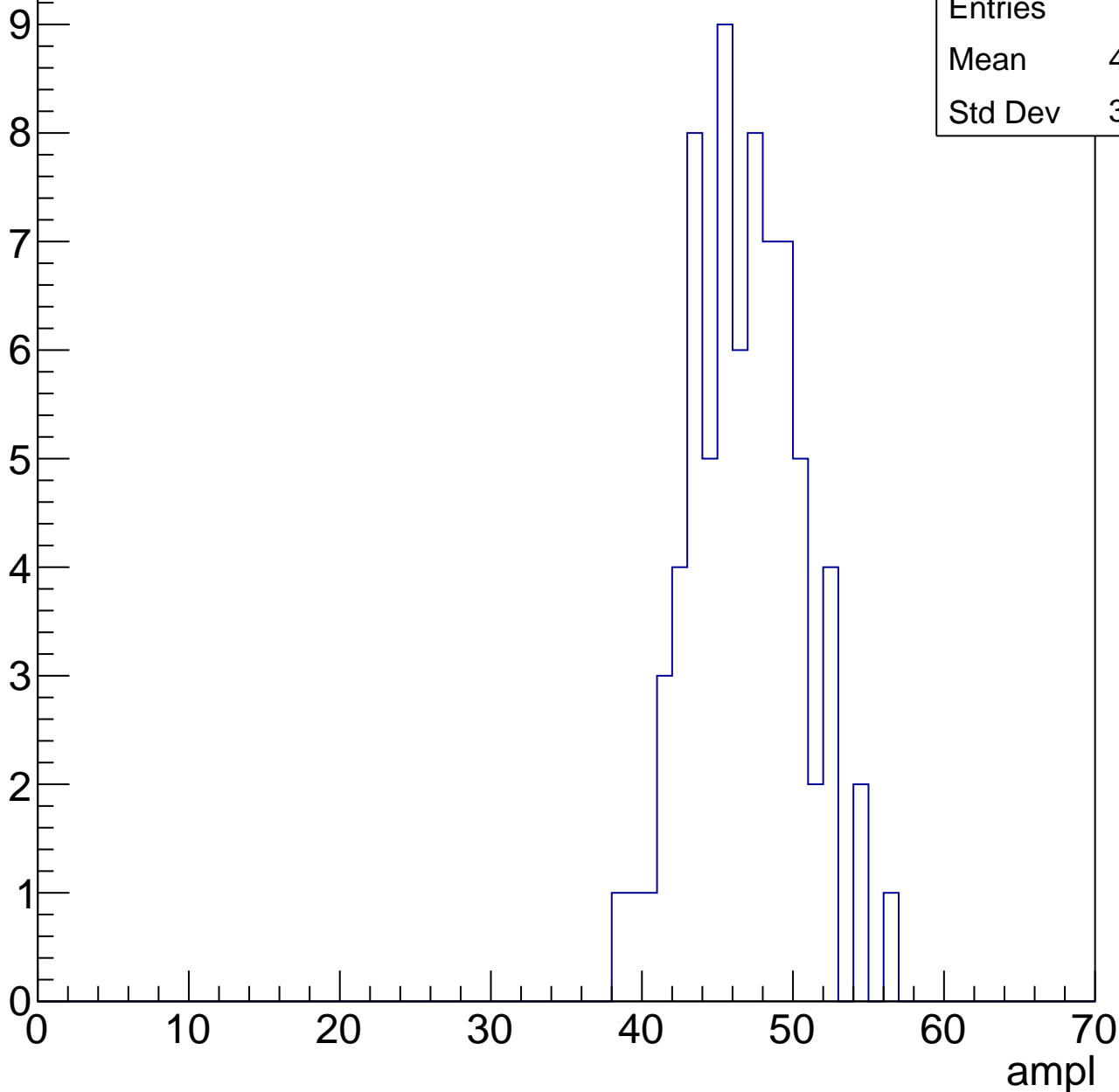
**Gaus mean : 40.0325**  
**Gaus Width: 2.6650**

# B1L000S, U1-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	46.38
Std Dev	3.638

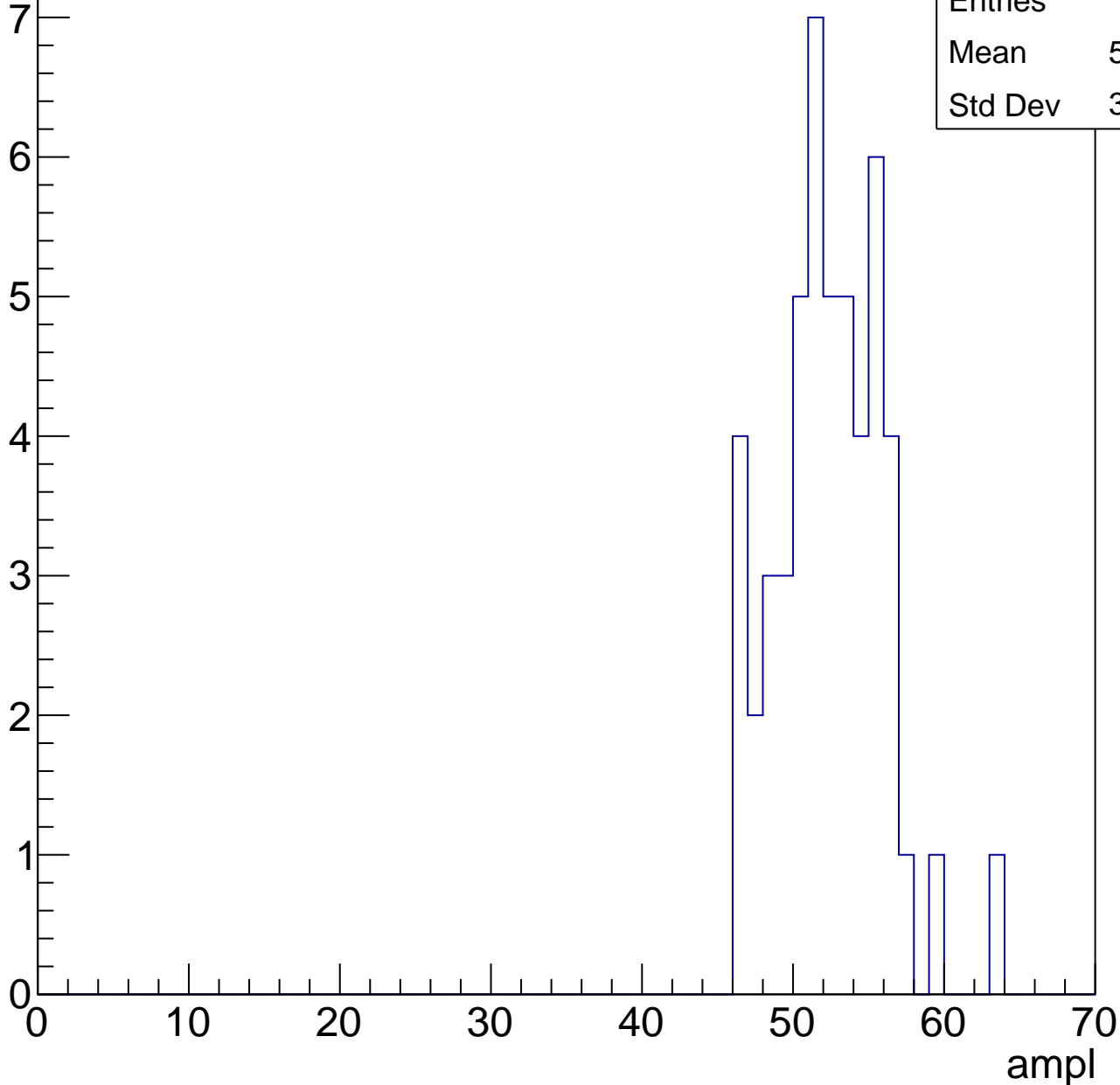


# B1L000S, U1-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	51.96
Std Dev	3.514

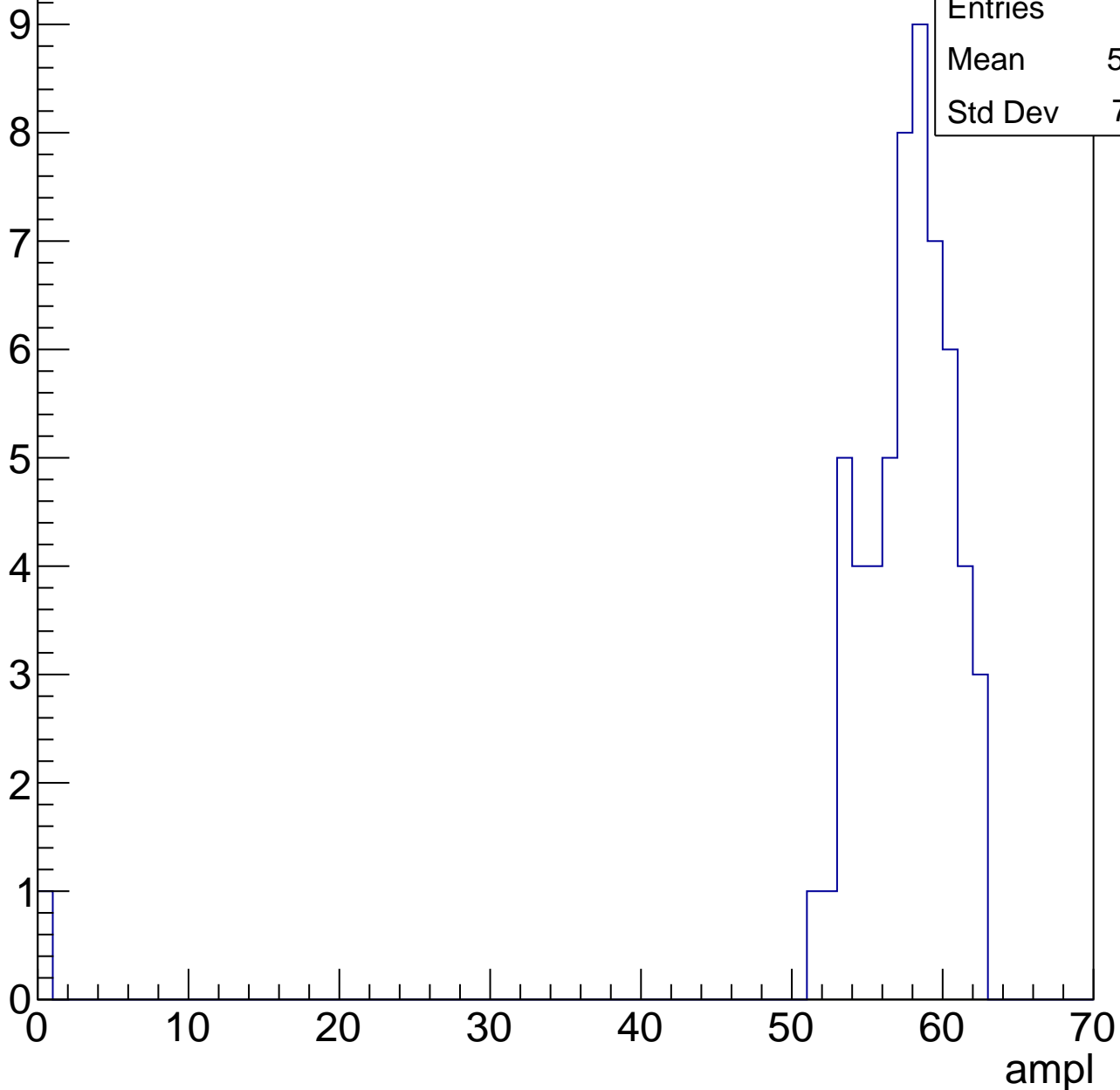


# B1L000S, U1-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

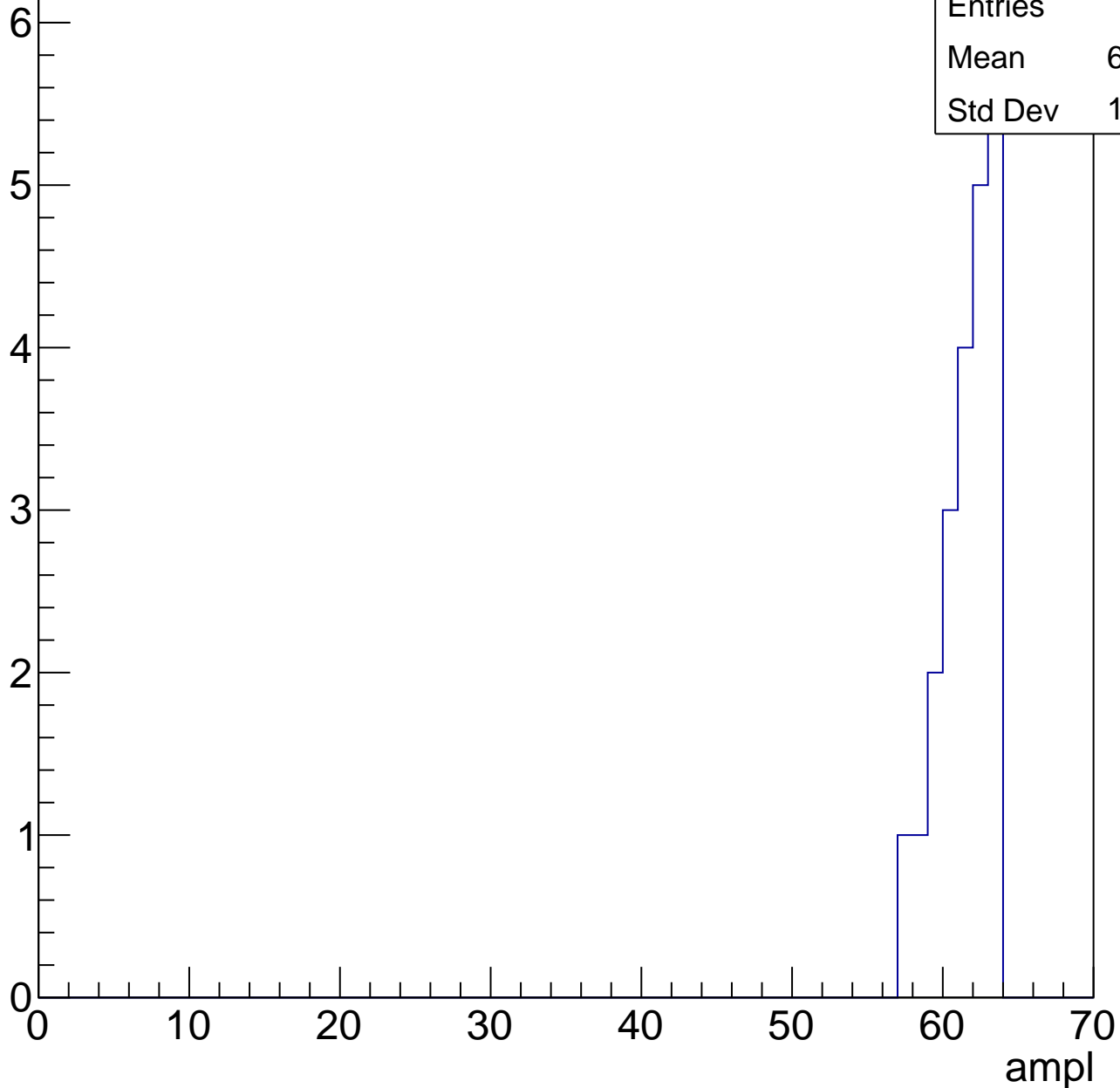
Entries	58
Mean	56.29
Std Dev	7.931



# B1L000S, U1-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

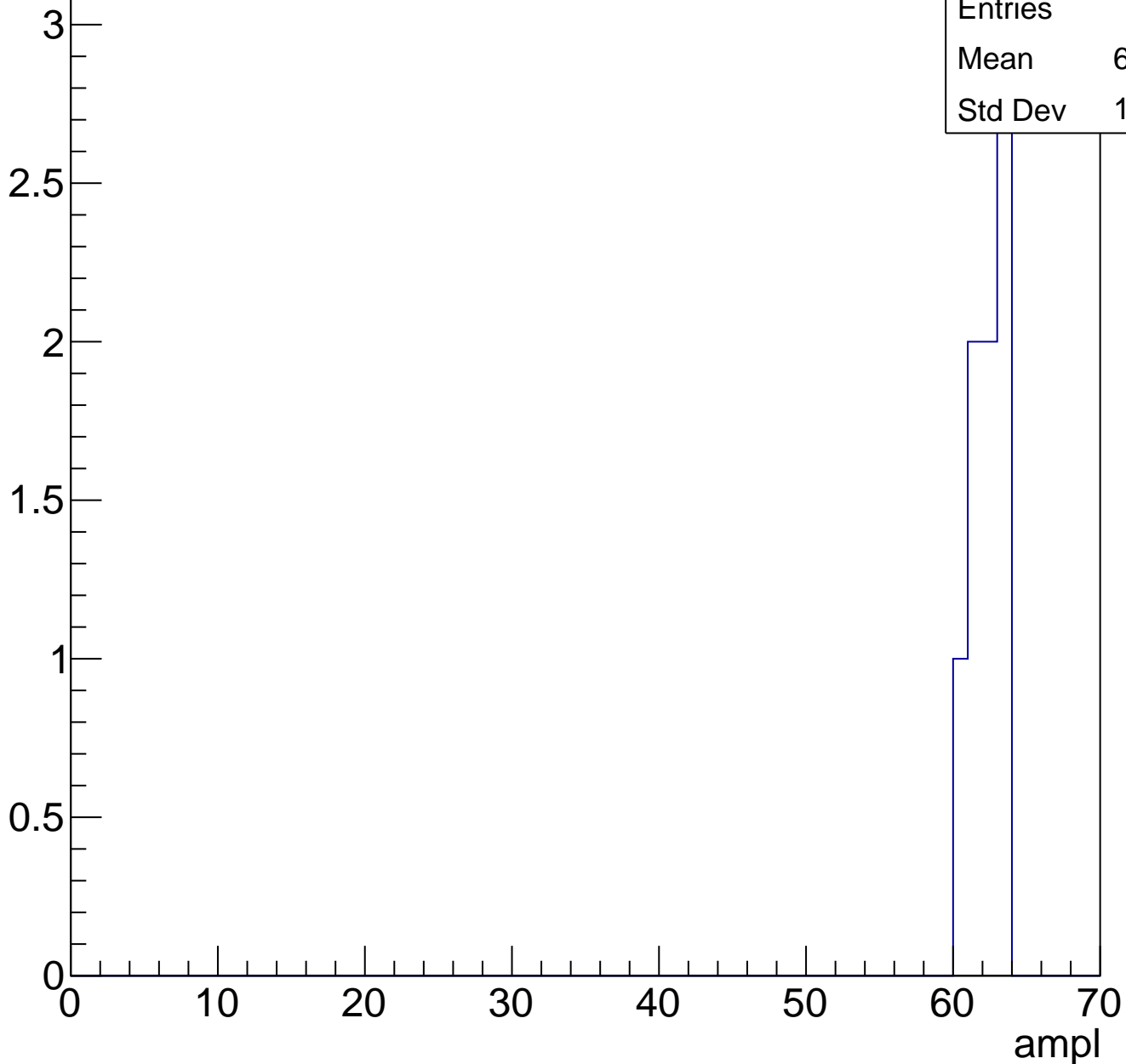




# B1L000S, U1-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch120, adc0

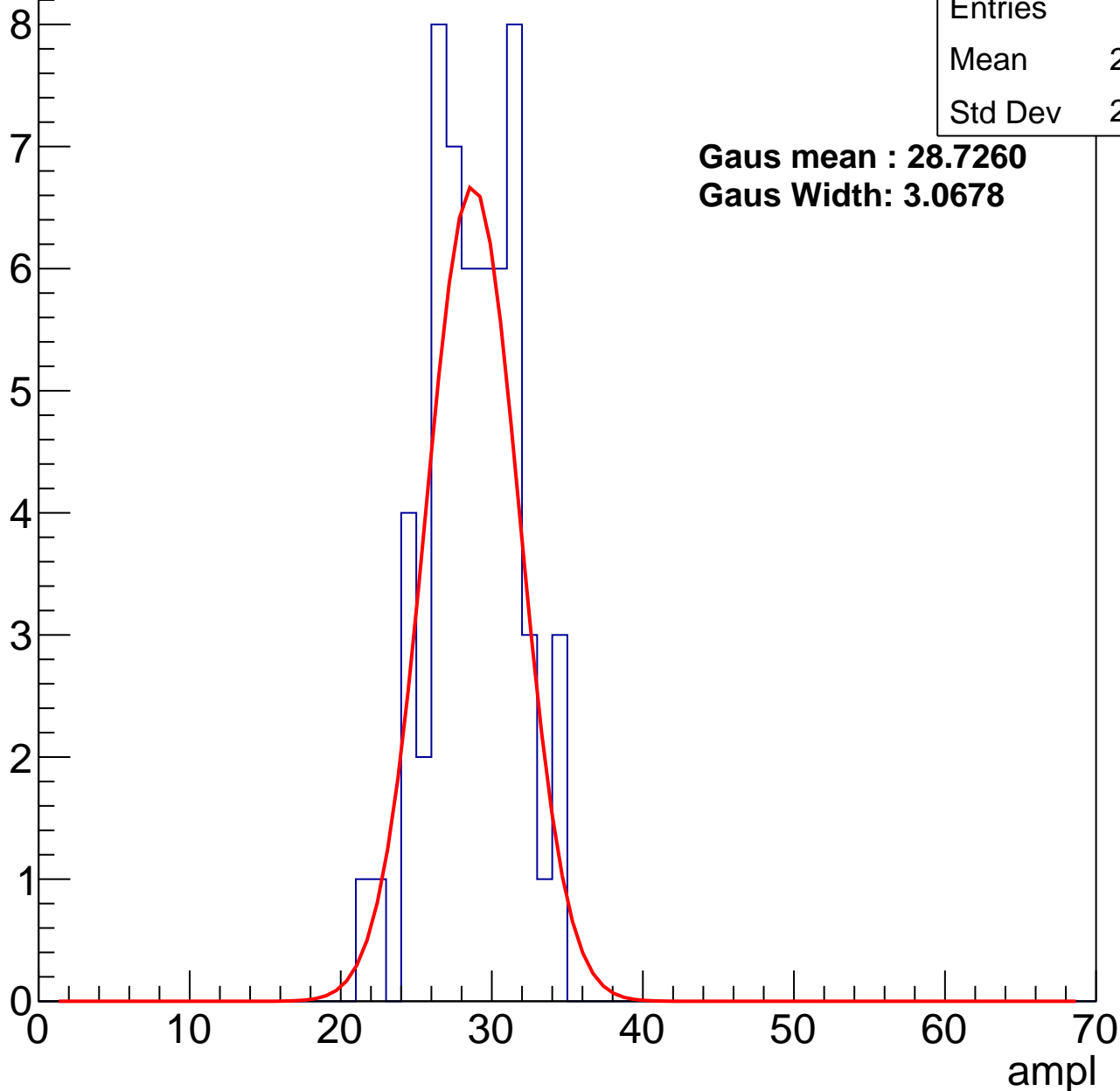
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	28.34
Std Dev	2.935

**Gaus mean : 28.7260**

**Gaus Width: 3.0678**



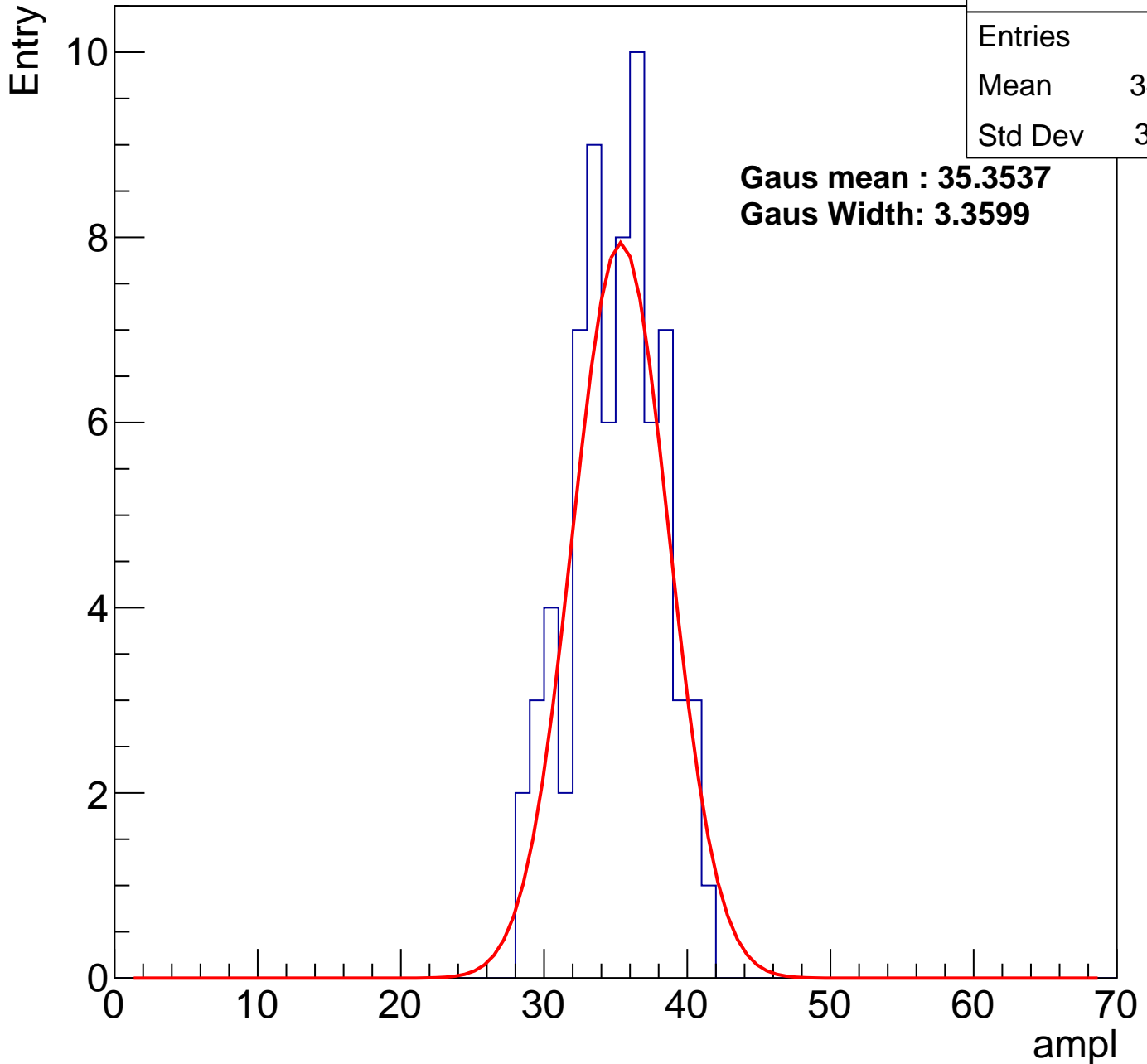
# B1L000S, U1-ch120, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	34.59
Std Dev	3.111

**Gaus mean : 35.3537**

**Gaus Width: 3.3599**



# B1L000S, U1-ch120, adc2

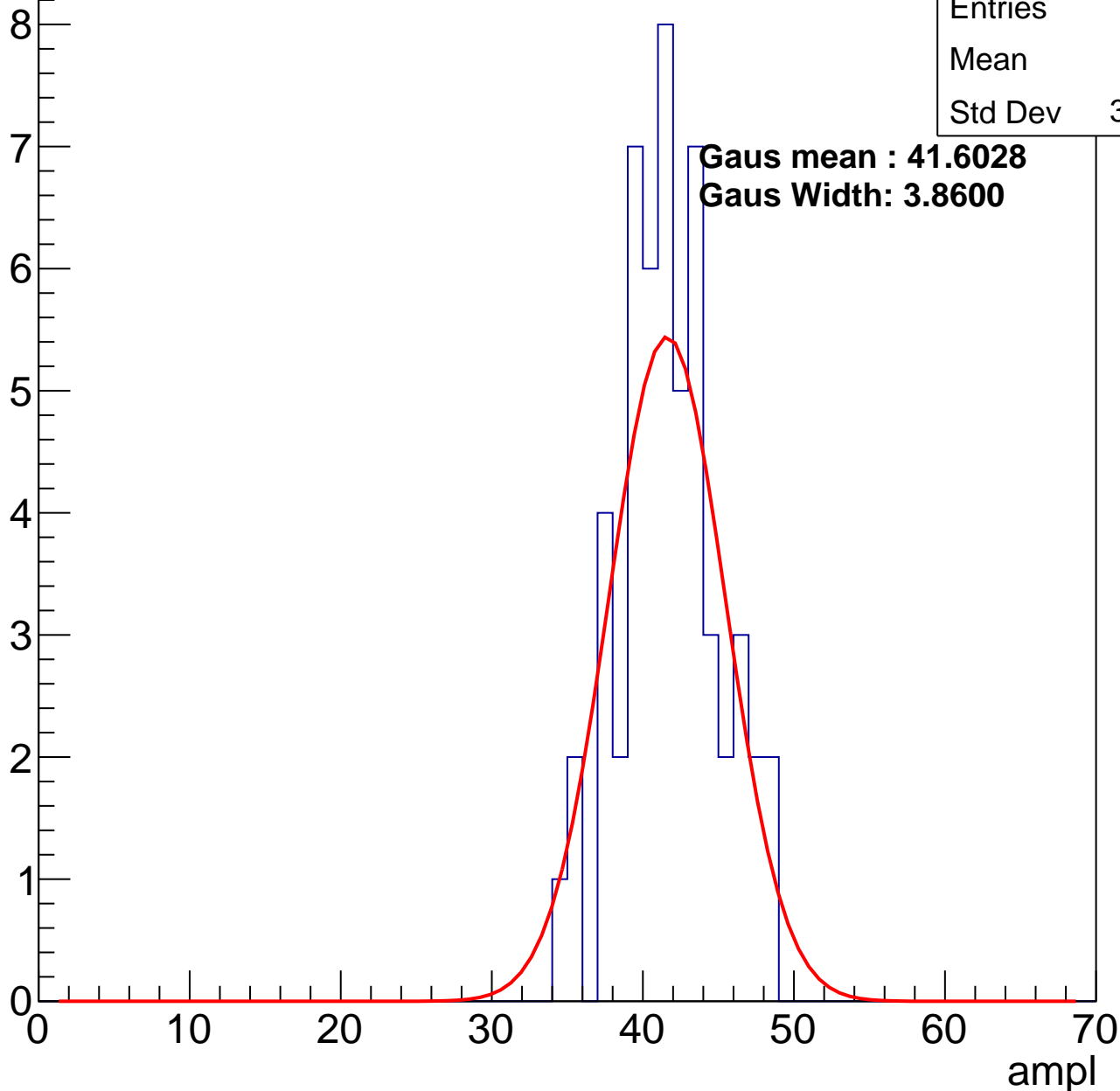
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.3
Std Dev	3.247

**Gaus mean : 41.6028**

**Gaus Width: 3.8600**

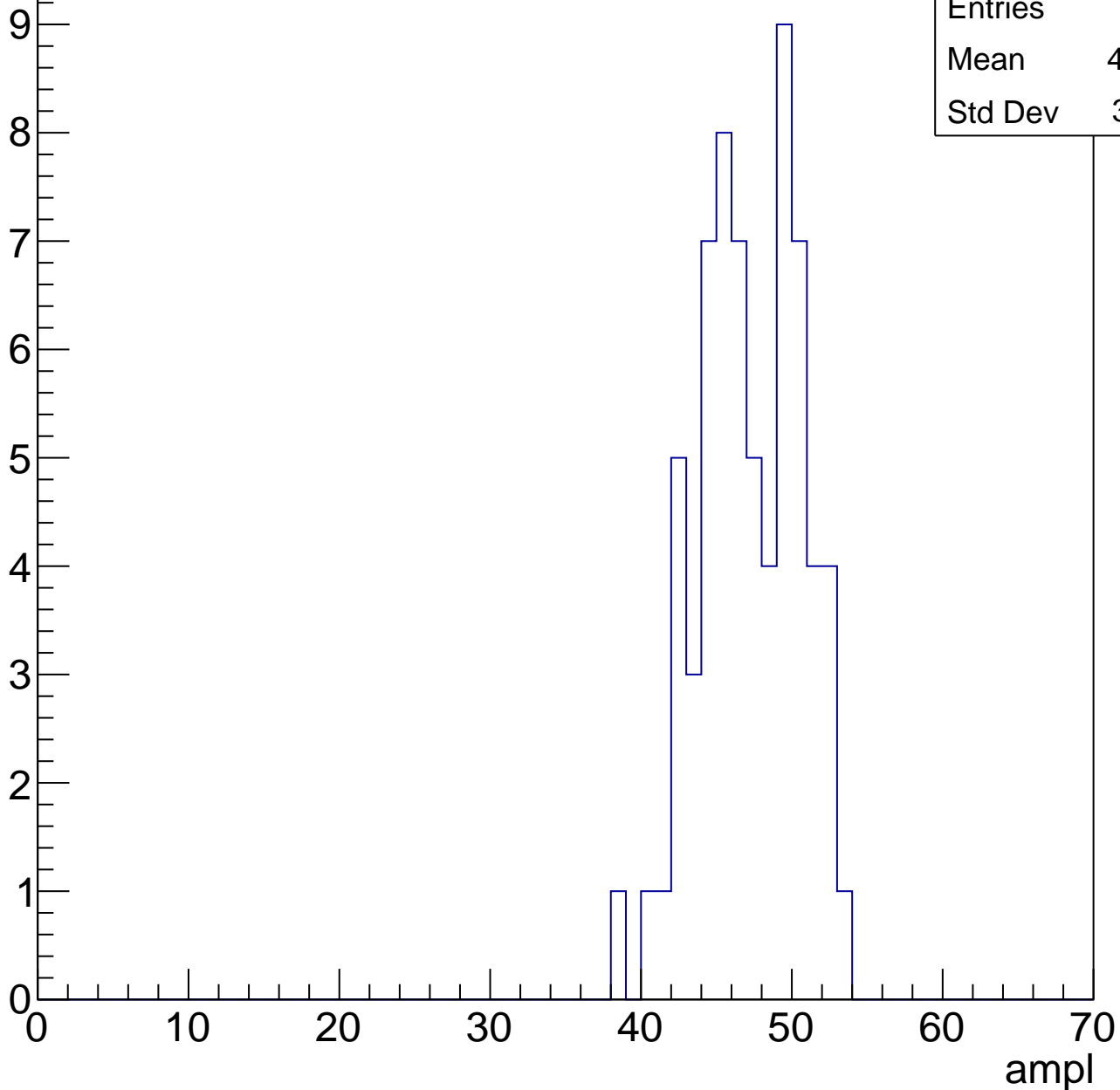


# B1L000S, U1-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	46.73
Std Dev	3.321

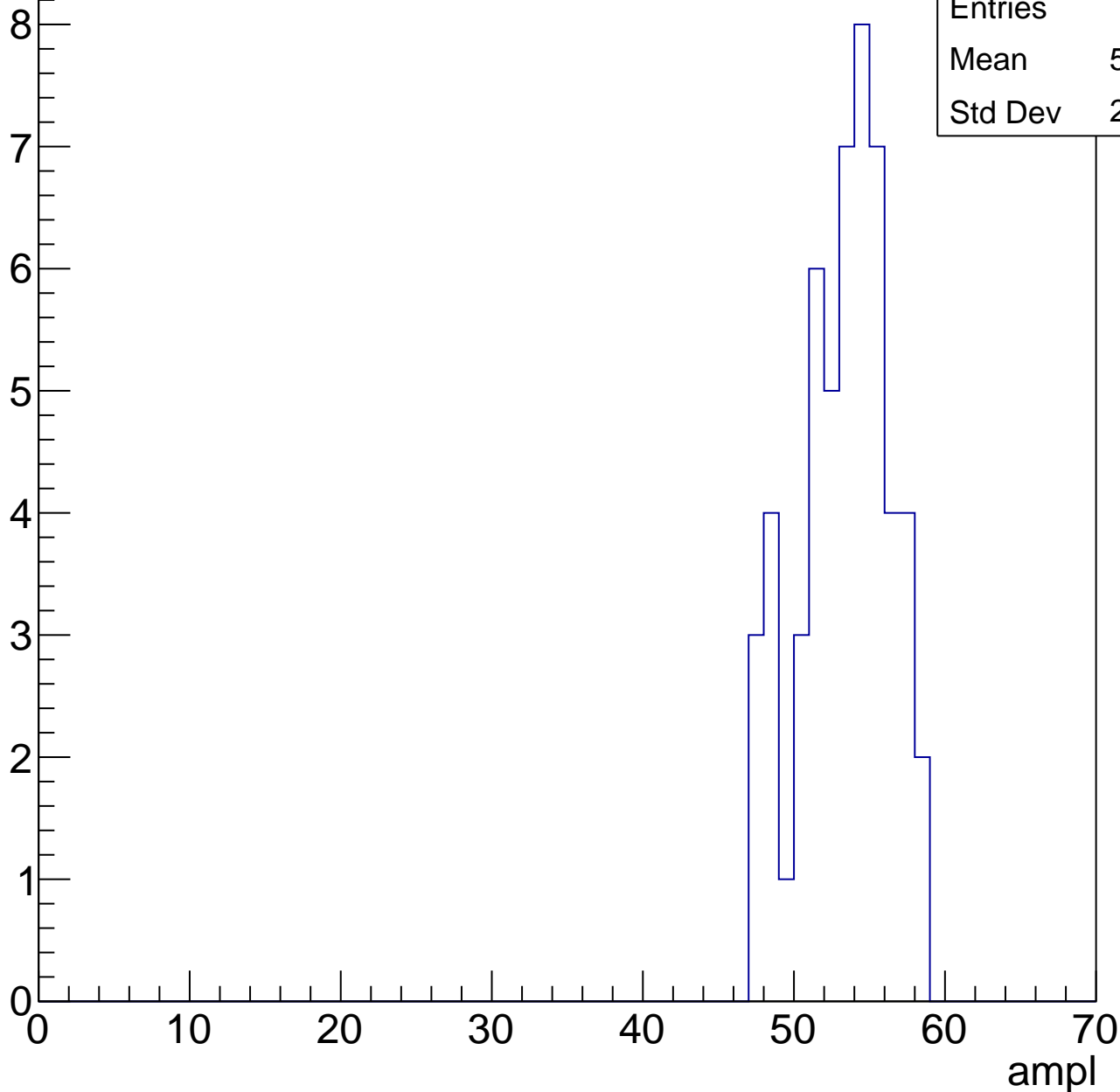


# B1L000S, U1-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	52.85
Std Dev	2.934

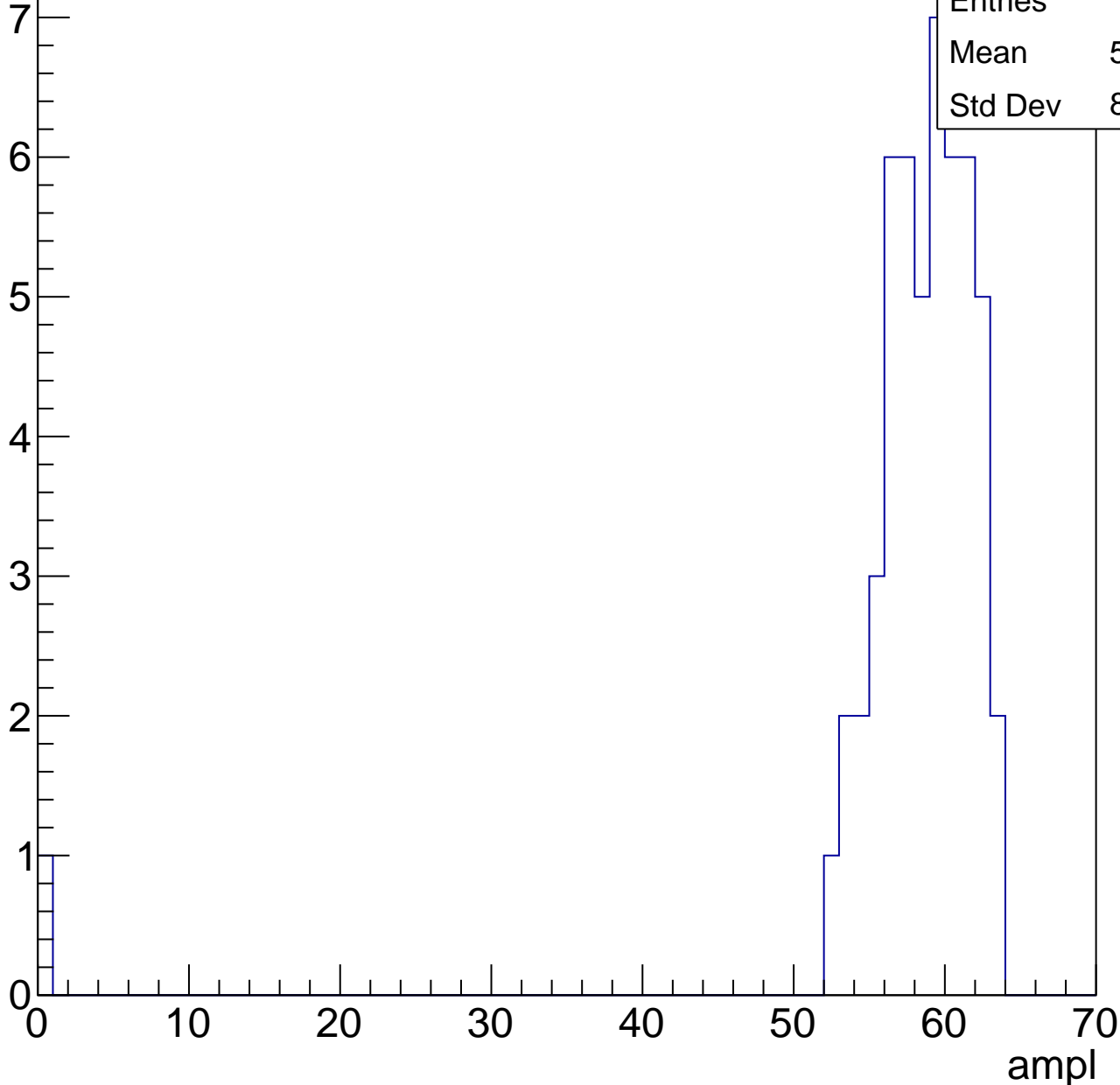


# B1L000S, U1-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	57.19
Std Dev	8.458

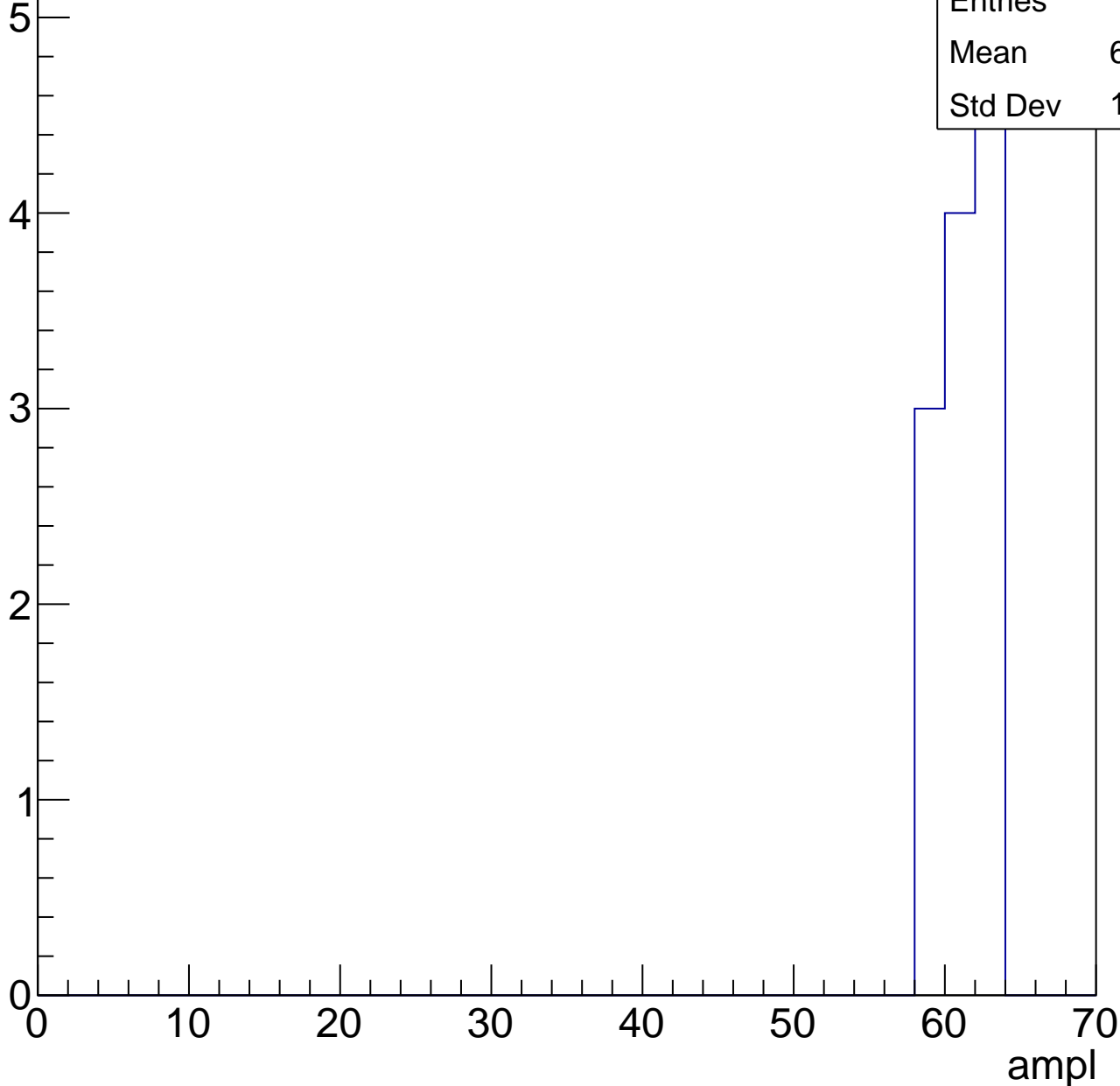


# B1L000S, U1-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	60.83
Std Dev	1.675

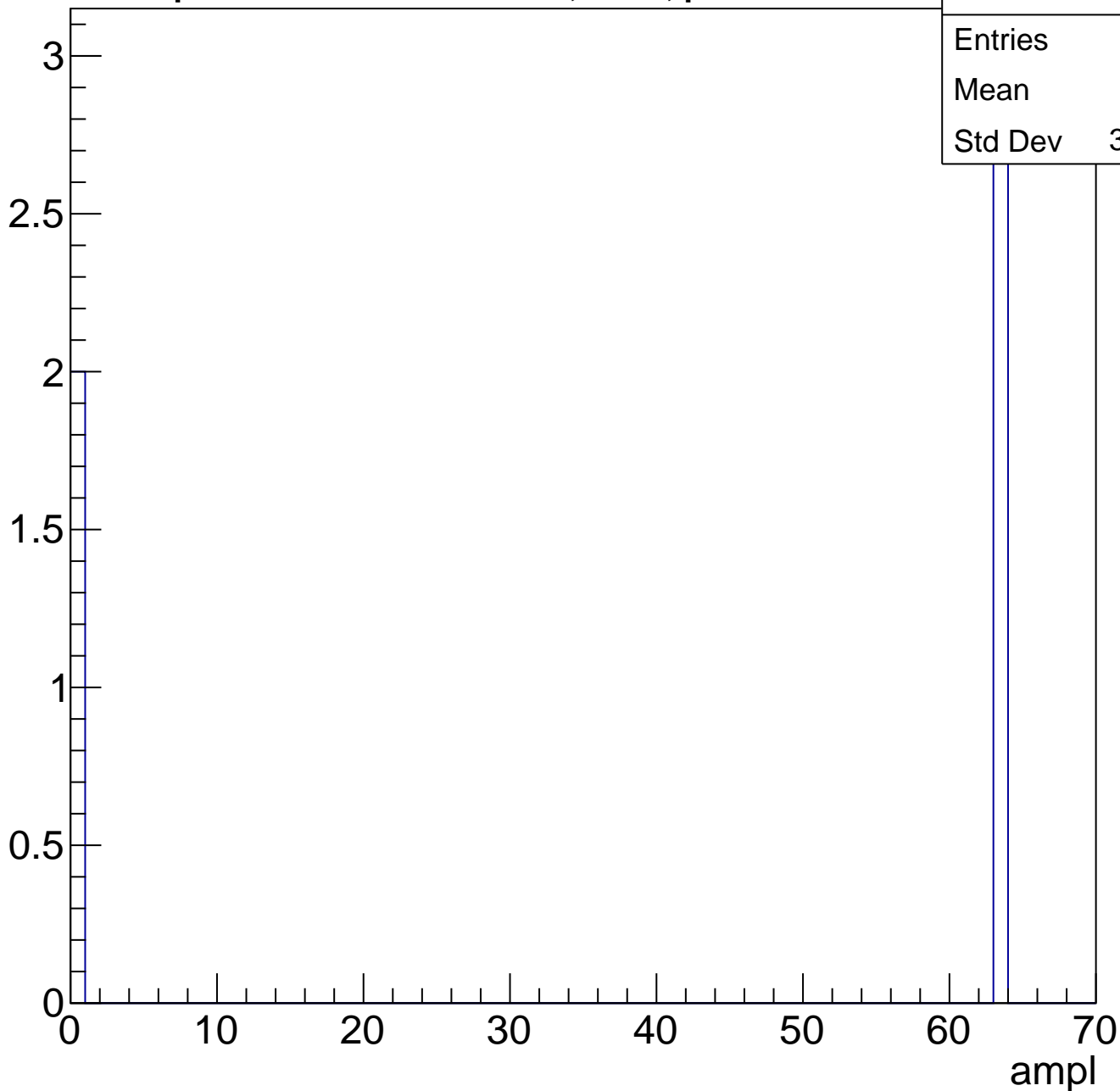




# B1L000S, U1-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch121, adc0

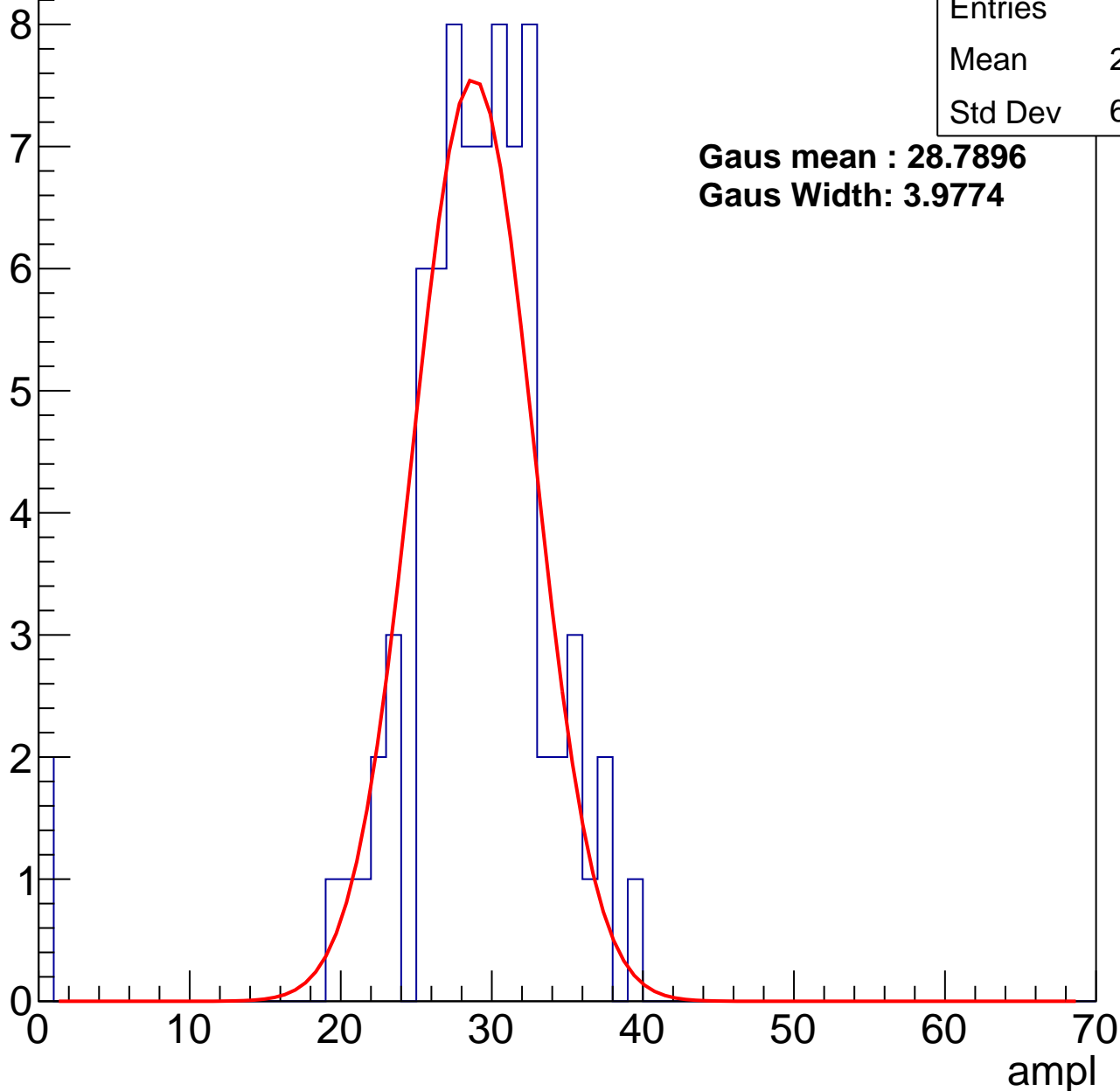
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	28.14
Std Dev	6.034

**Gaus mean : 28.7896**

**Gaus Width: 3.9774**



# B1L000S, U1-ch121, adc1

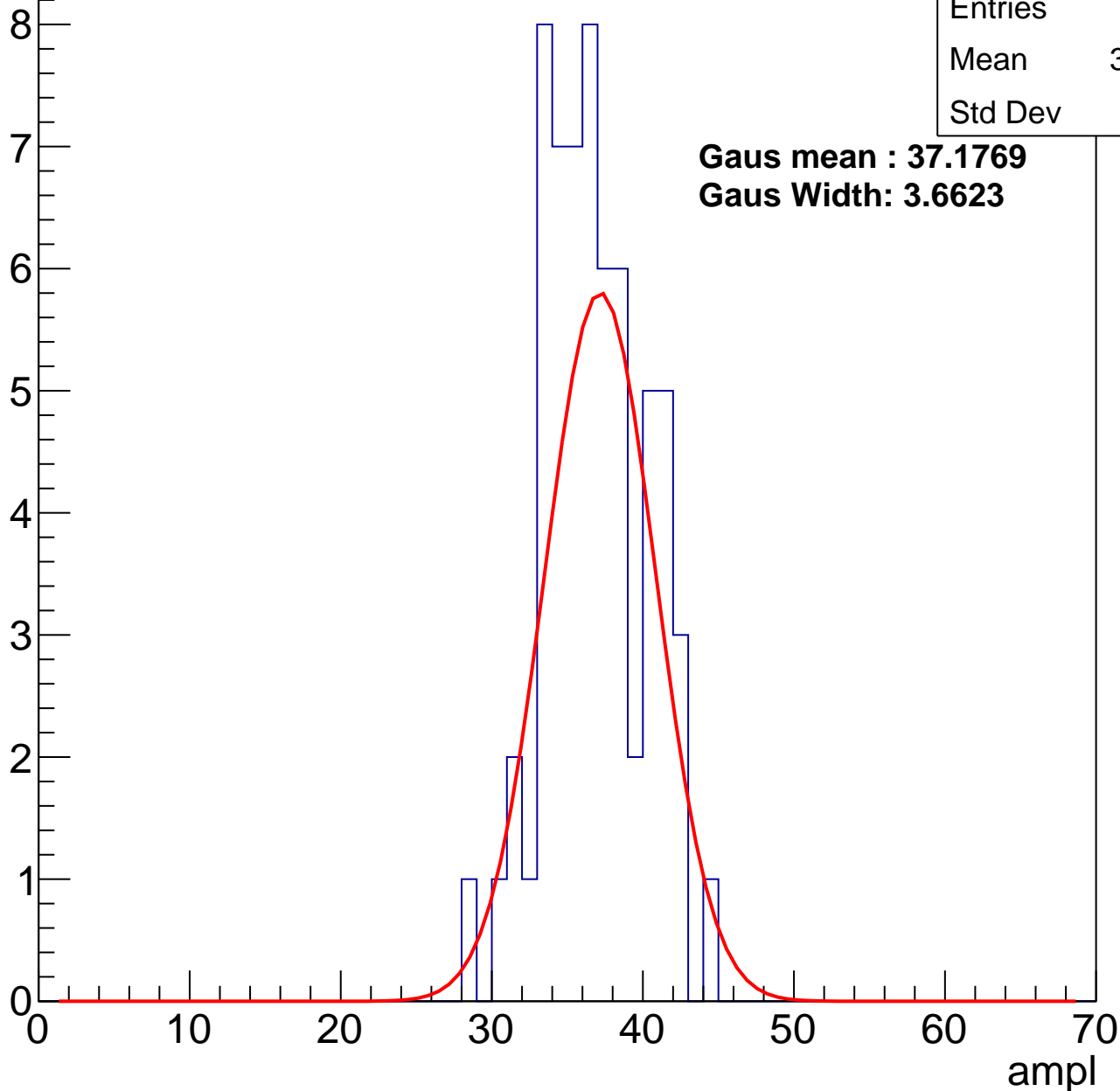
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	36.35
Std Dev	3.31

**Gaus mean : 37.1769**

**Gaus Width: 3.6623**



# B1L000S, U1-ch121, adc2

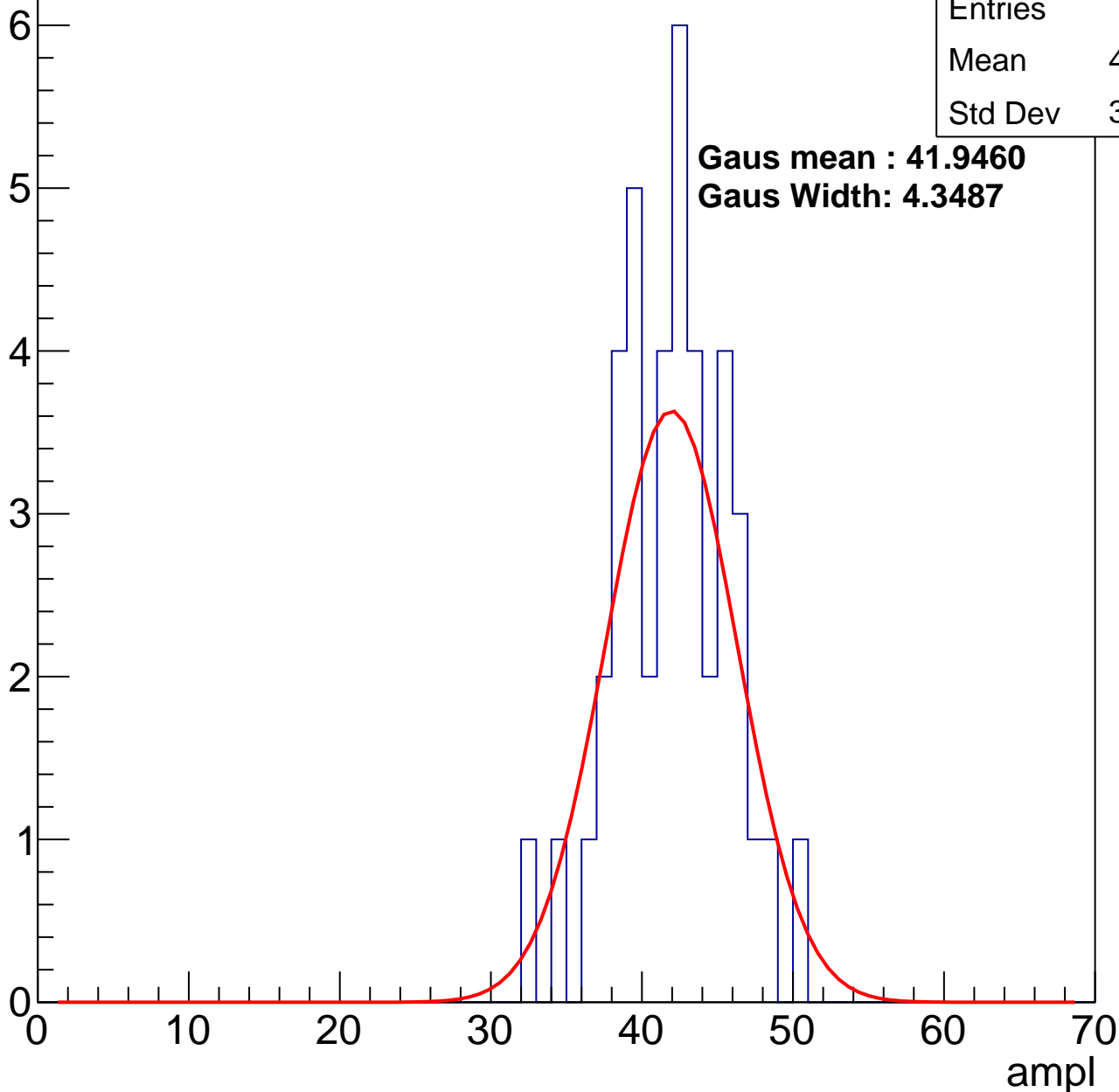
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	41.48
Std Dev	3.718

**Gaus mean : 41.9460**

**Gaus Width: 4.3487**

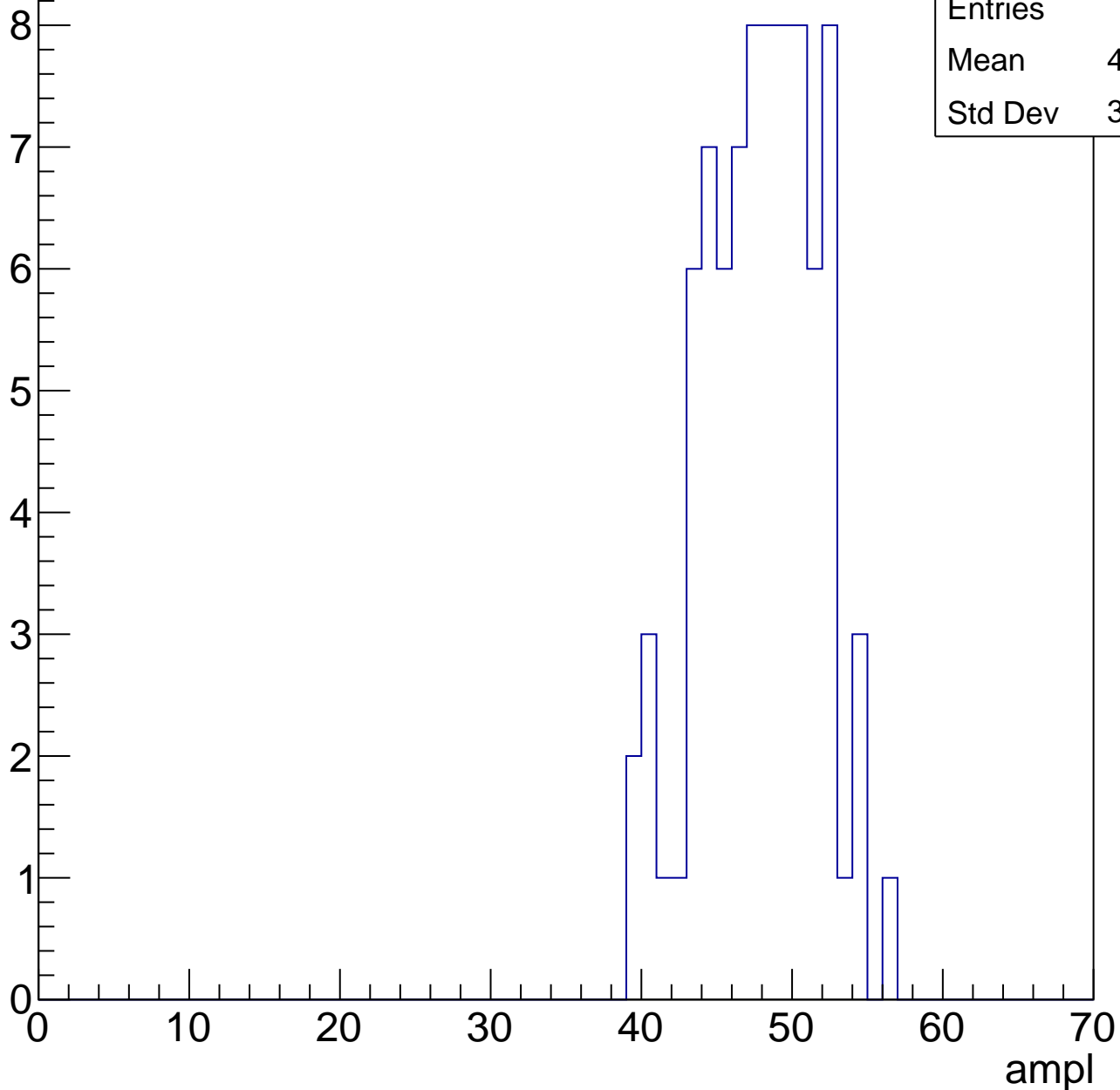


# B1L000S, U1-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

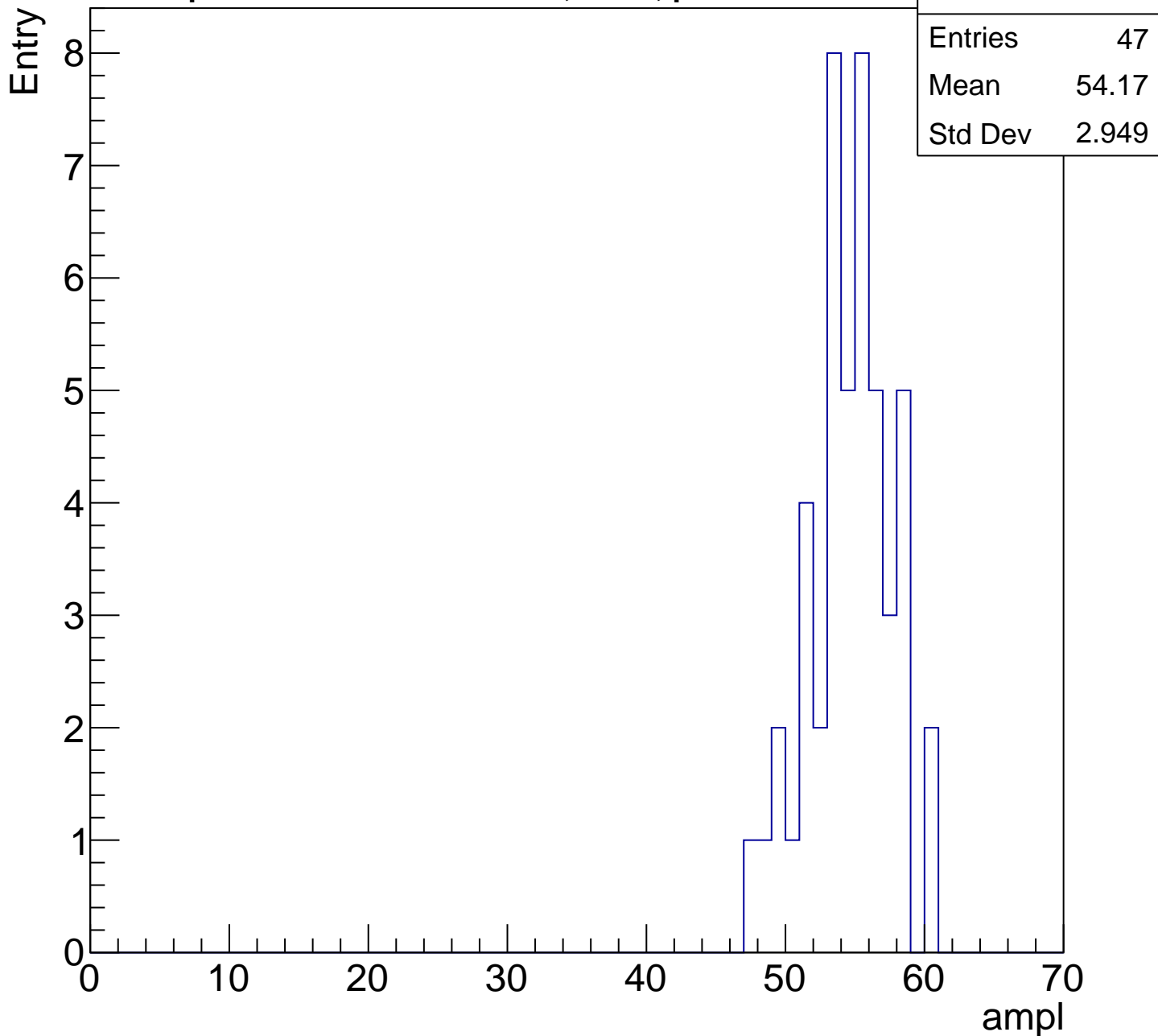
Entry

Entries	84
Mean	47.43
Std Dev	3.752



# B1L000S, U1-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U1-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 50

Mean 58.92

Std Dev 2.591

8

6

4

2

0

0

10

20

30

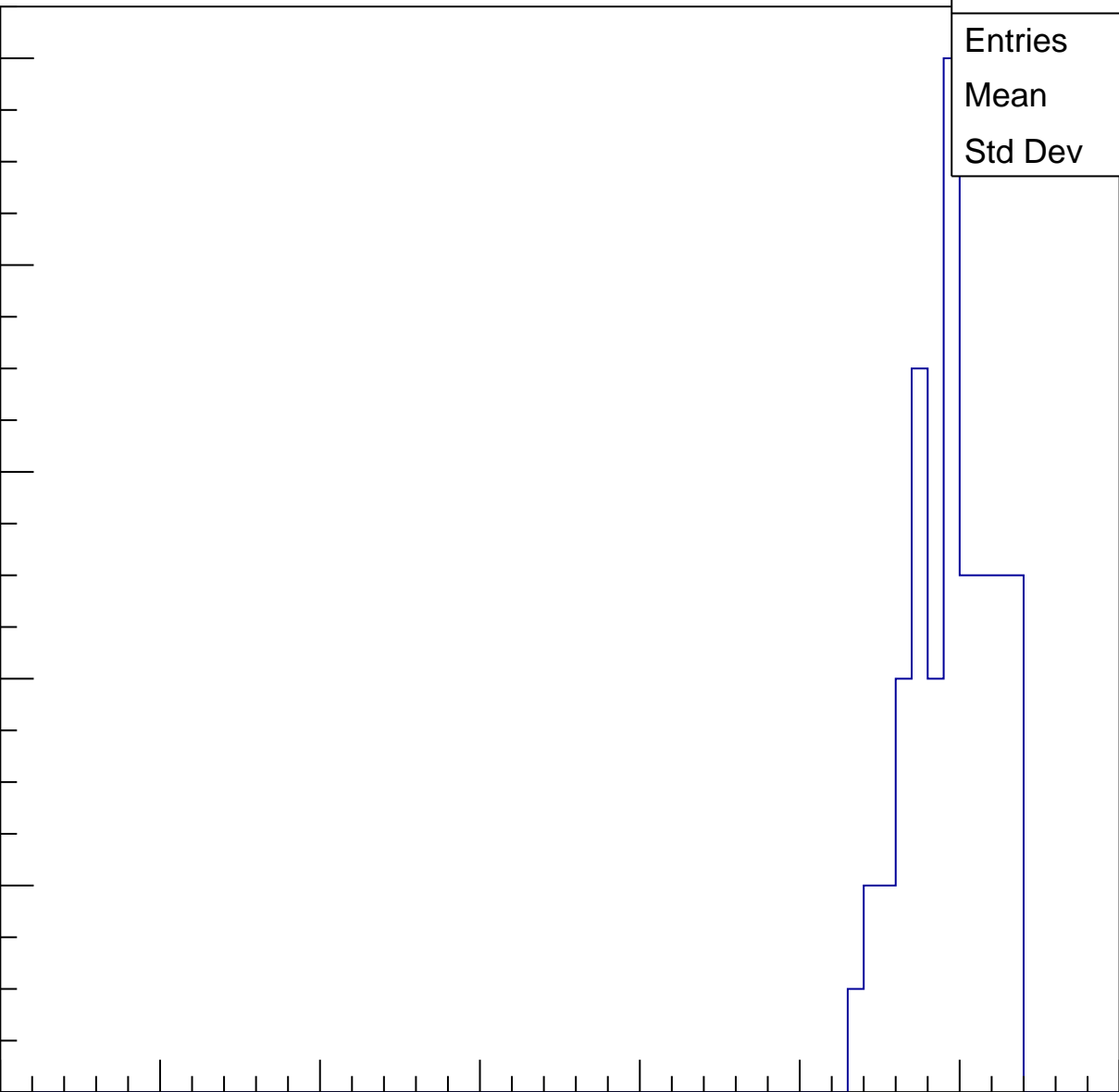
40

50

60

ampl

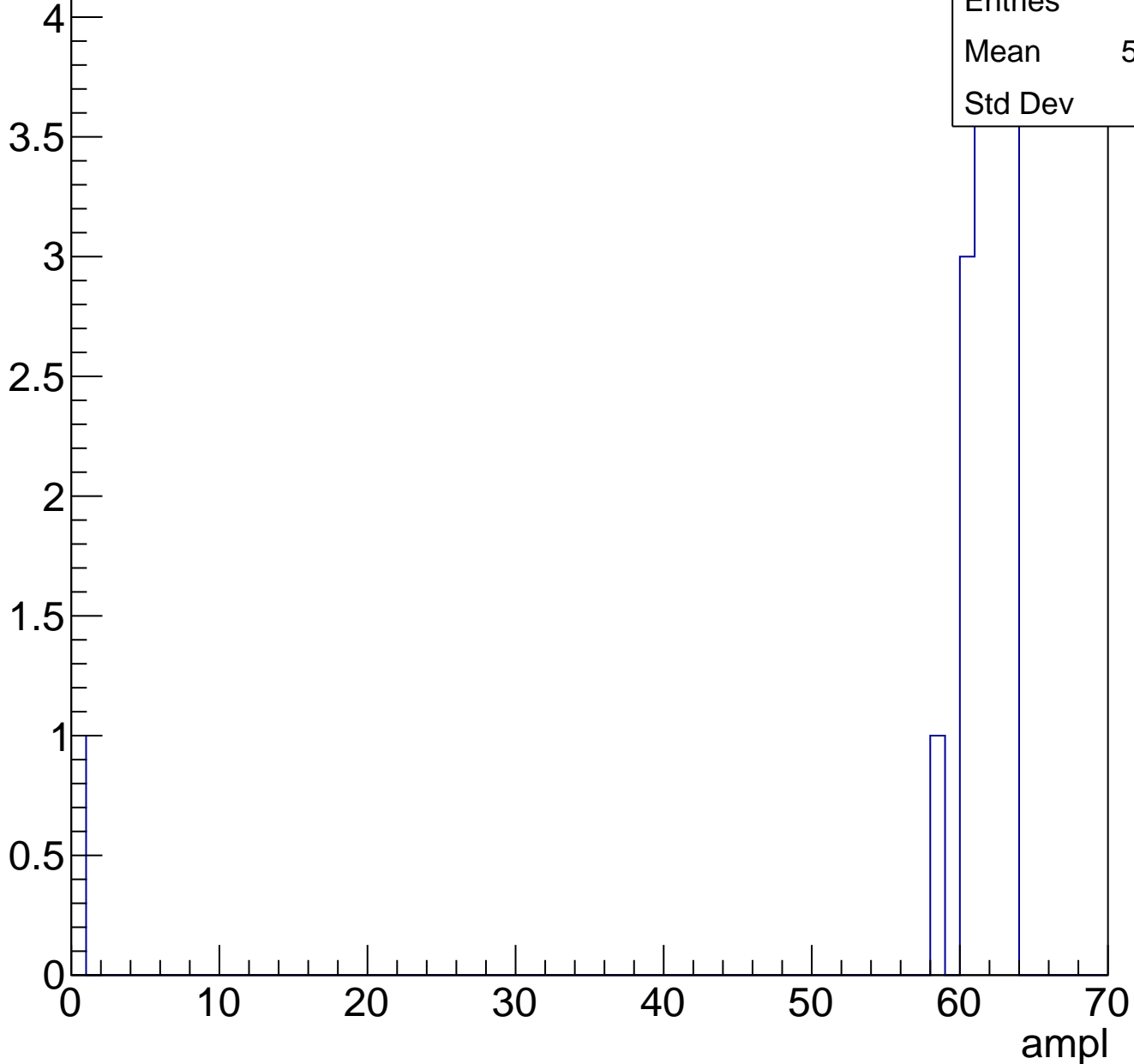
70



# B1L000S, U1-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



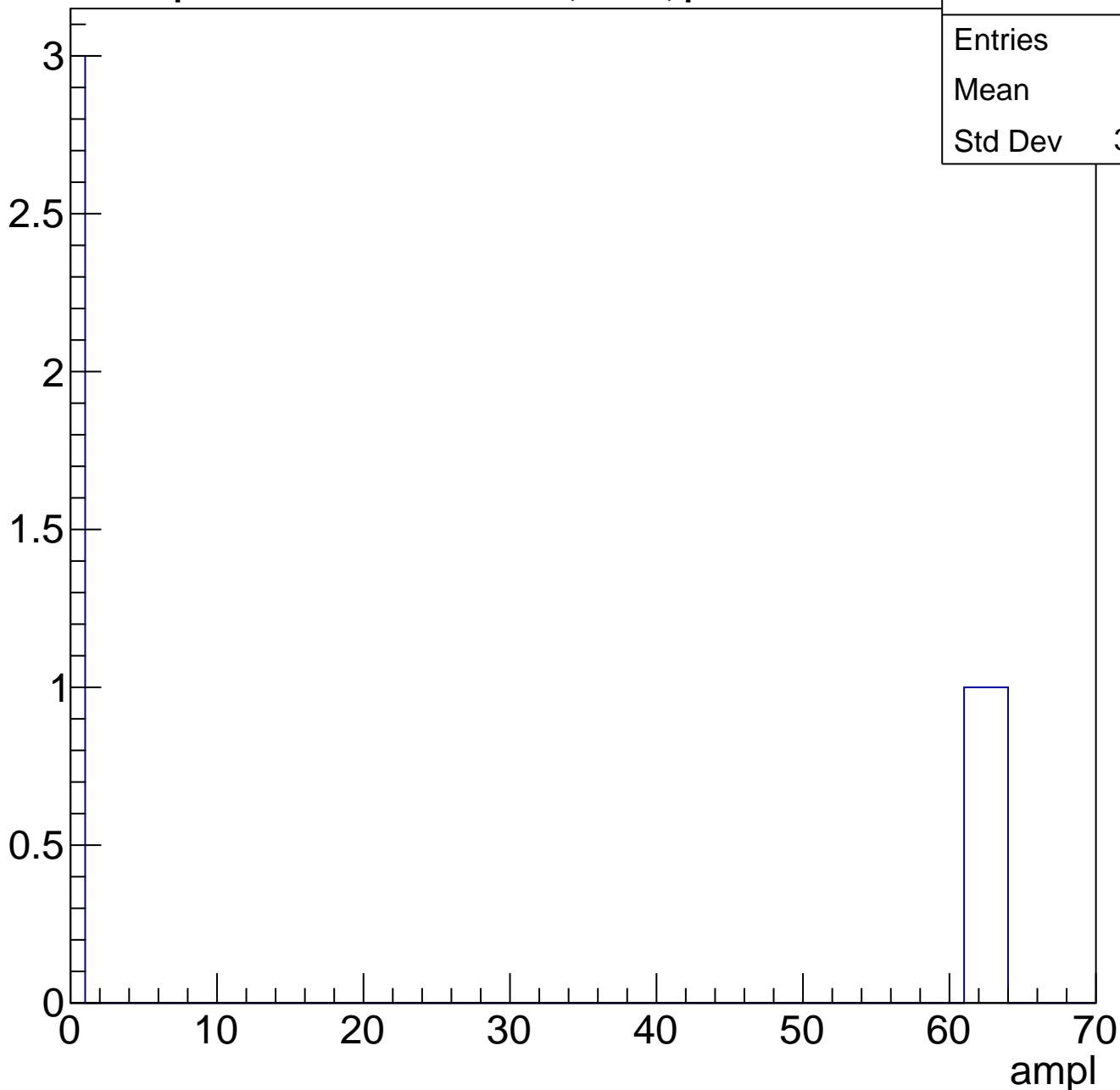
Entries	17
Mean	57.76
Std Dev	14.5



# B1L000S, U1-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch122, adc0

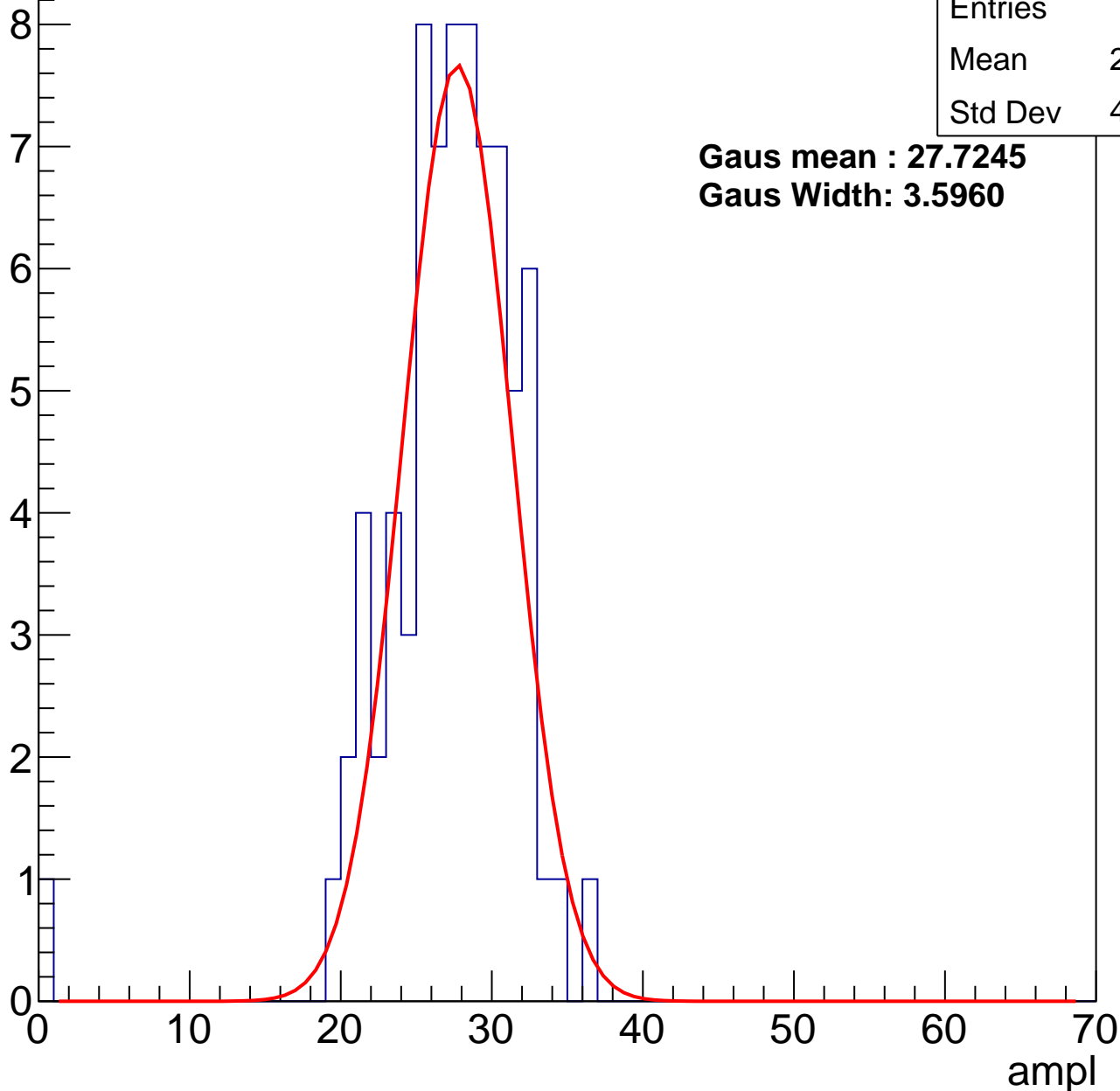
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	26.79
Std Dev	4.739

**Gaus mean : 27.7245**

**Gaus Width: 3.5960**



# B1L000S, U1-ch122, adc1

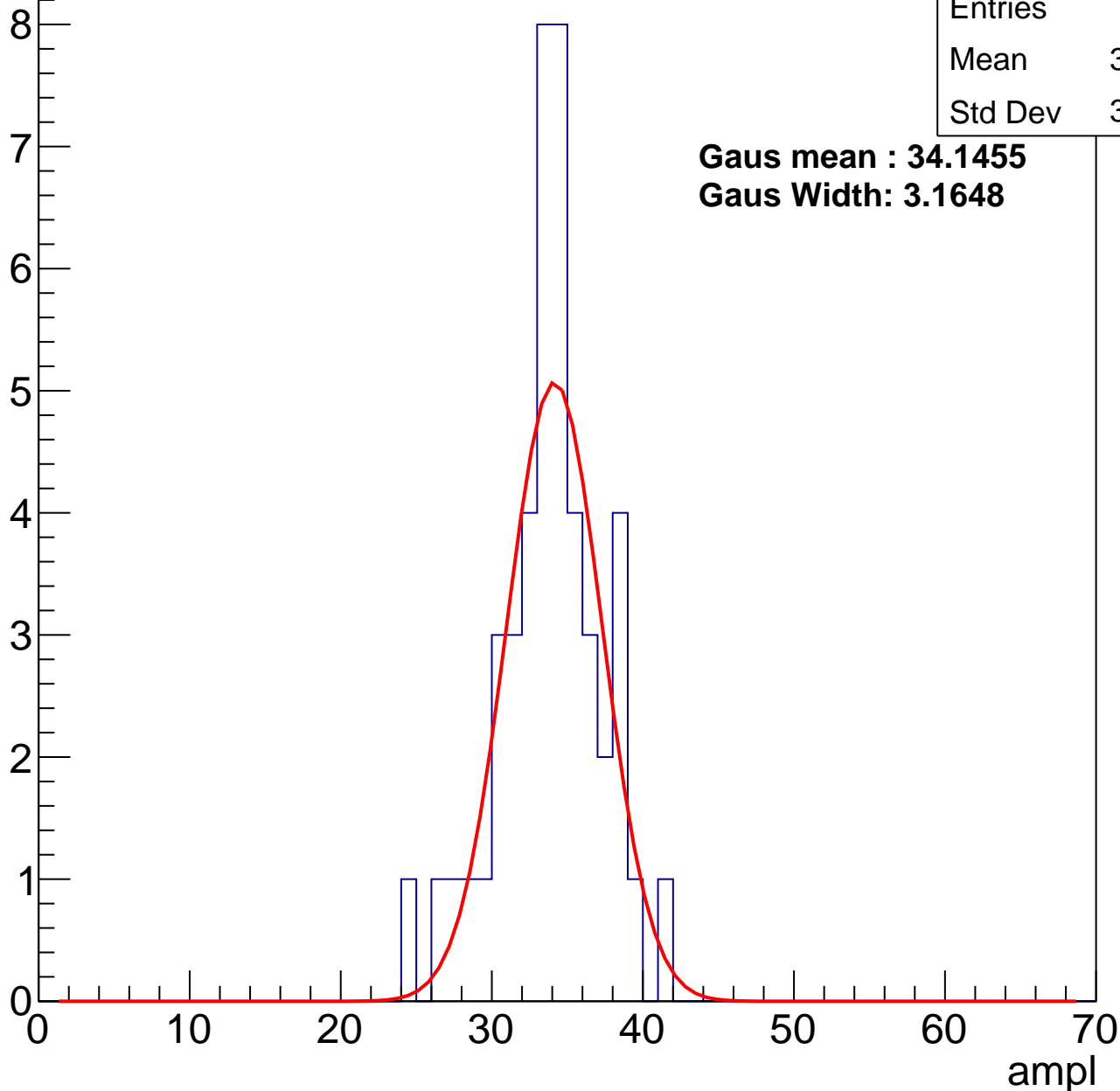
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	33.37
Std Dev	3.384

**Gaus mean : 34.1455**

**Gaus Width: 3.1648**



# B1L000S, U1-ch122, adc2

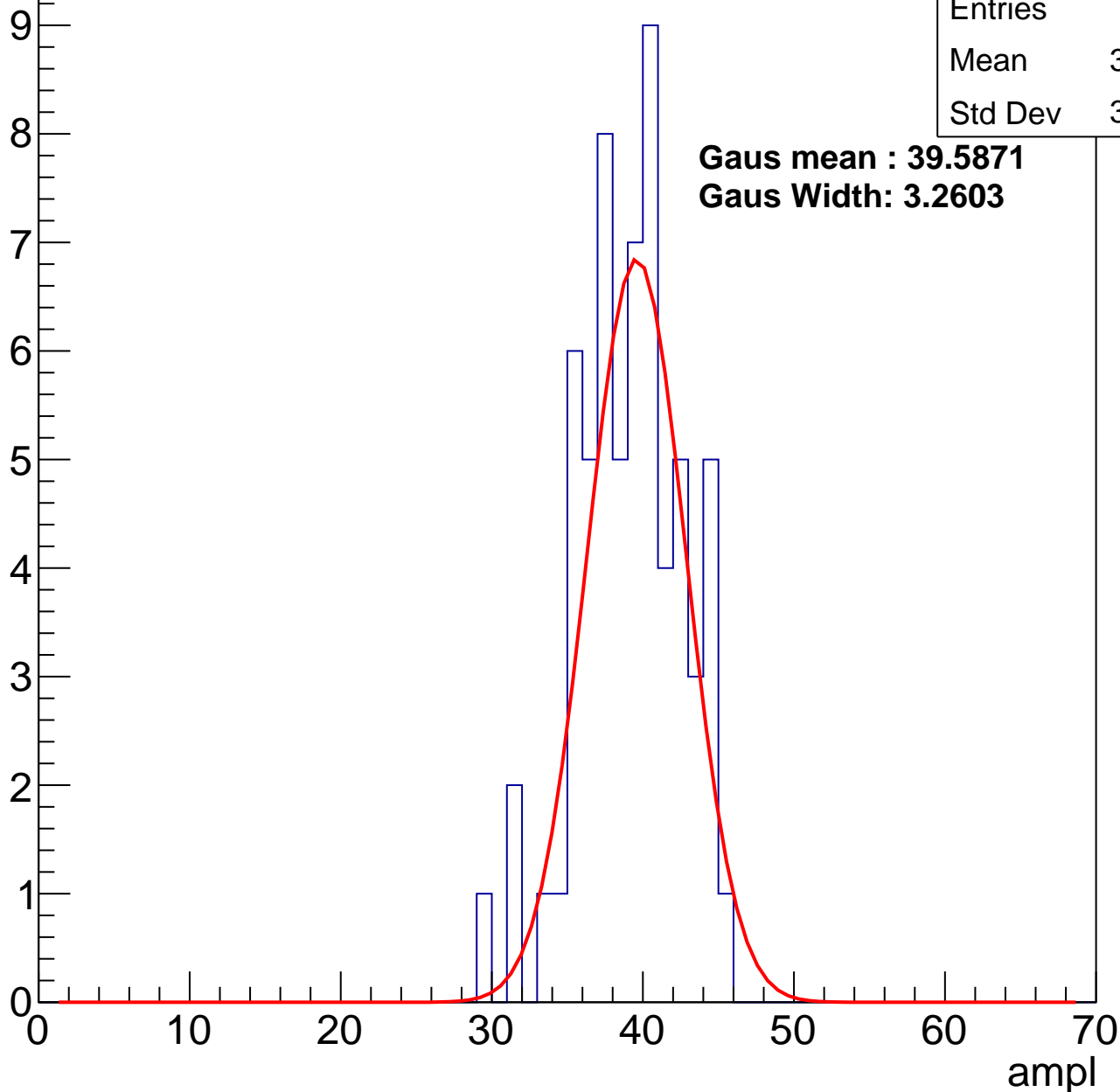
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	38.65
Std Dev	3.423

**Gaus mean : 39.5871**

**Gaus Width: 3.2603**

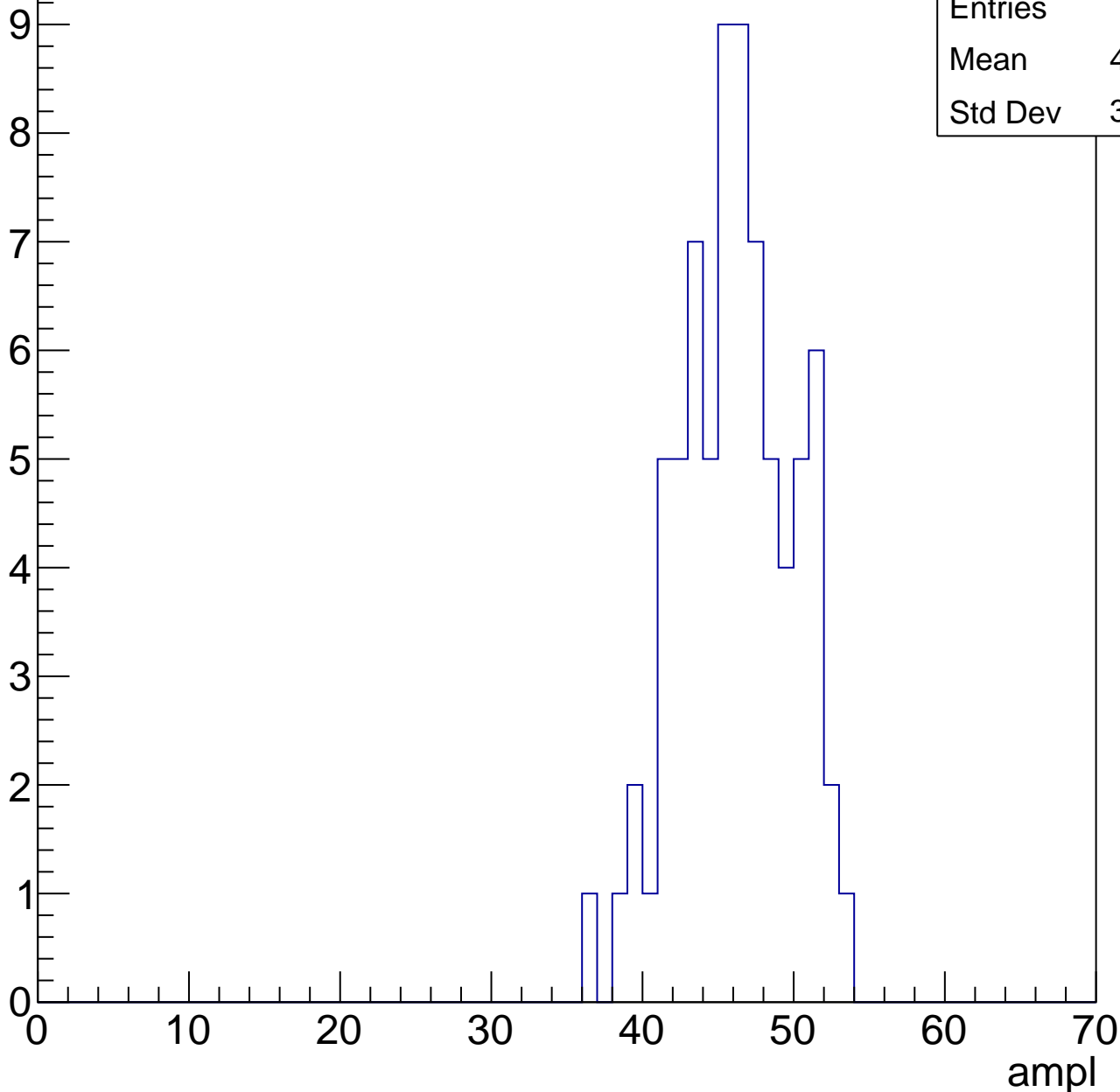


# B1L000S, U1-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	45.67
Std Dev	3.653

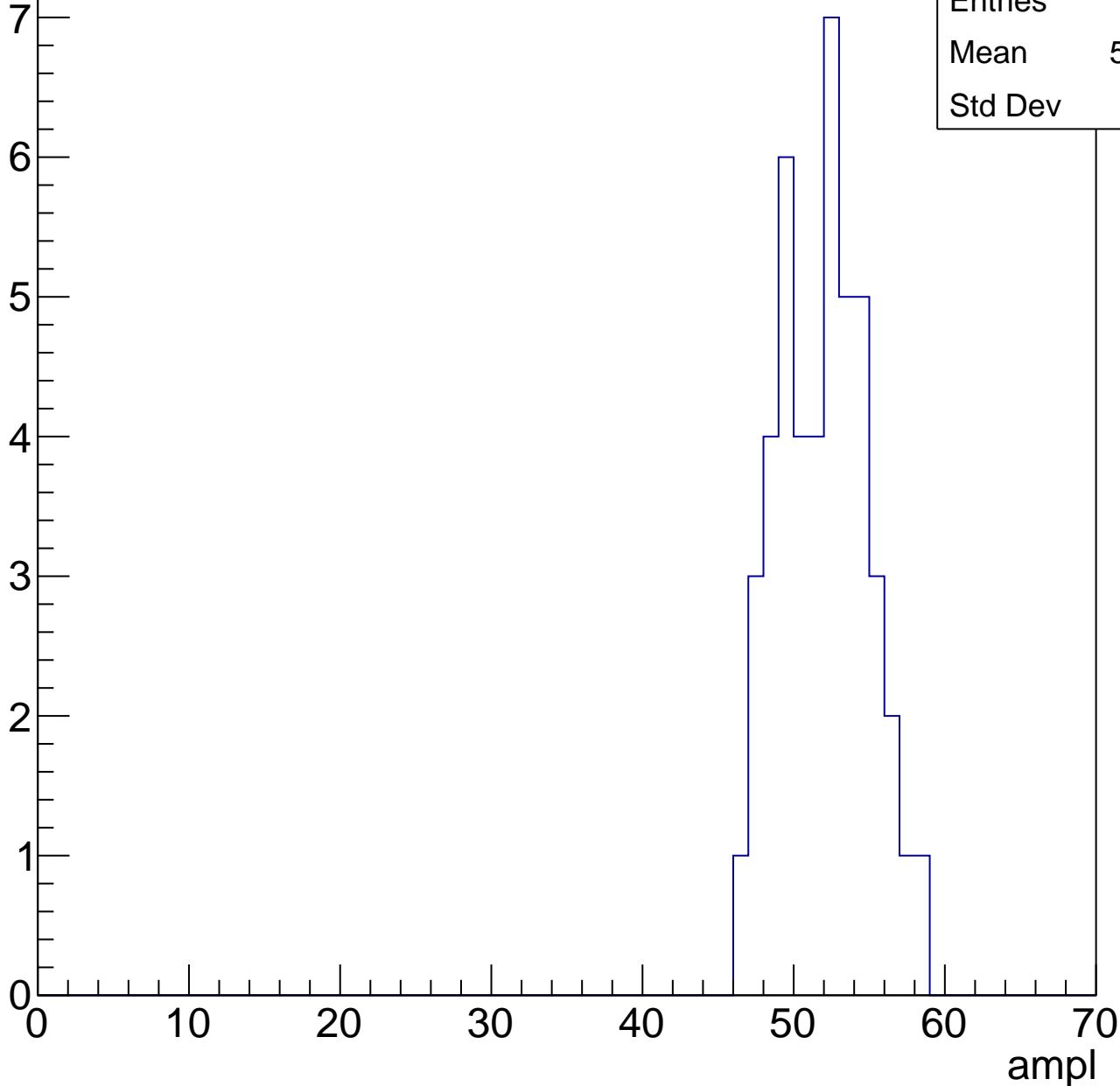


# B1L000S, U1-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

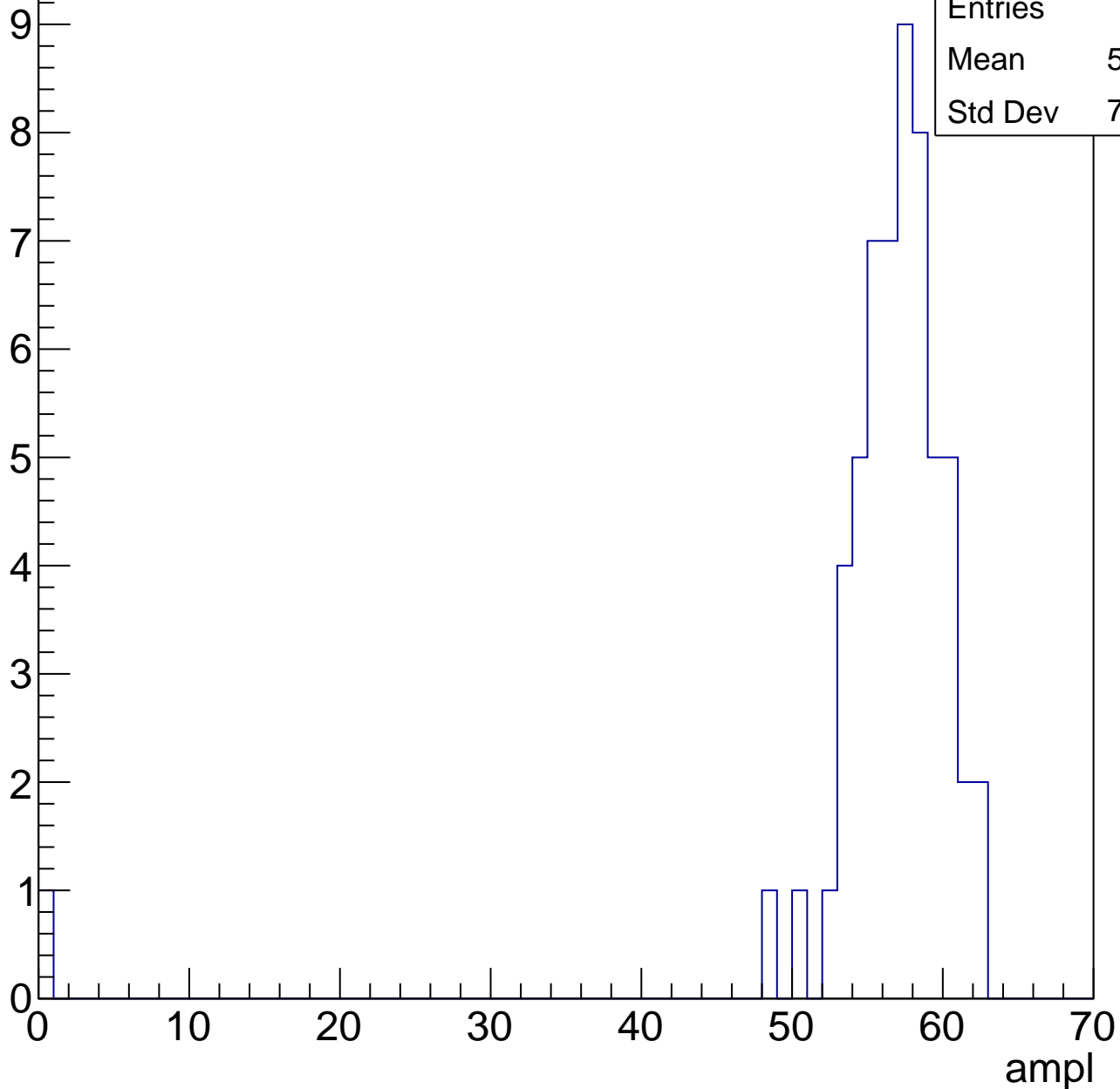
Entries	46
Mean	51.48
Std Dev	2.88



# B1L000S, U1-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

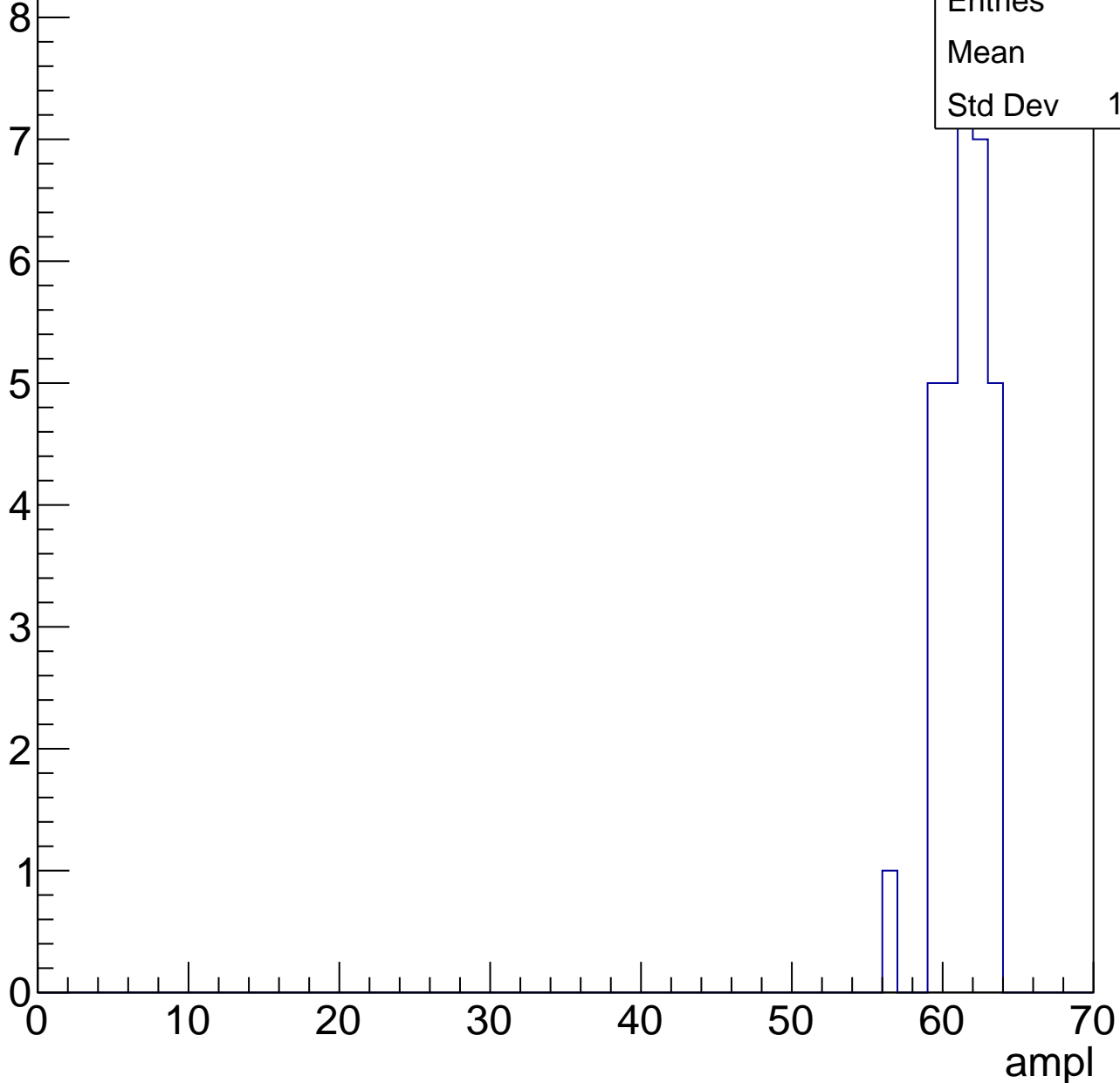


# B1L000S, U1-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	31
Mean	60.9
Std Dev	1.573



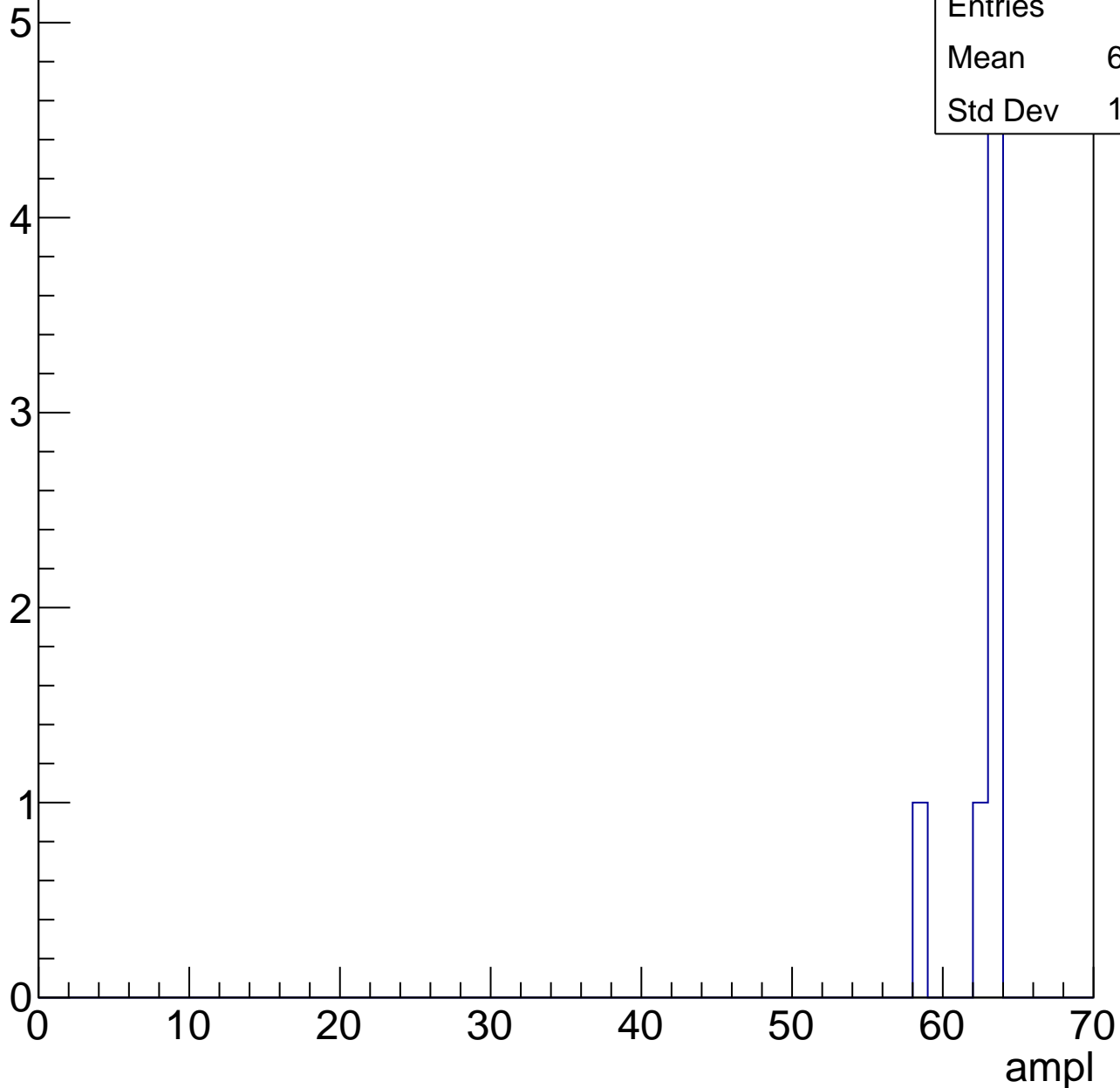


# B1L000S, U1-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	7
Mean	62.14
Std Dev	1.726



# B1L000S, U1-ch123, adc0

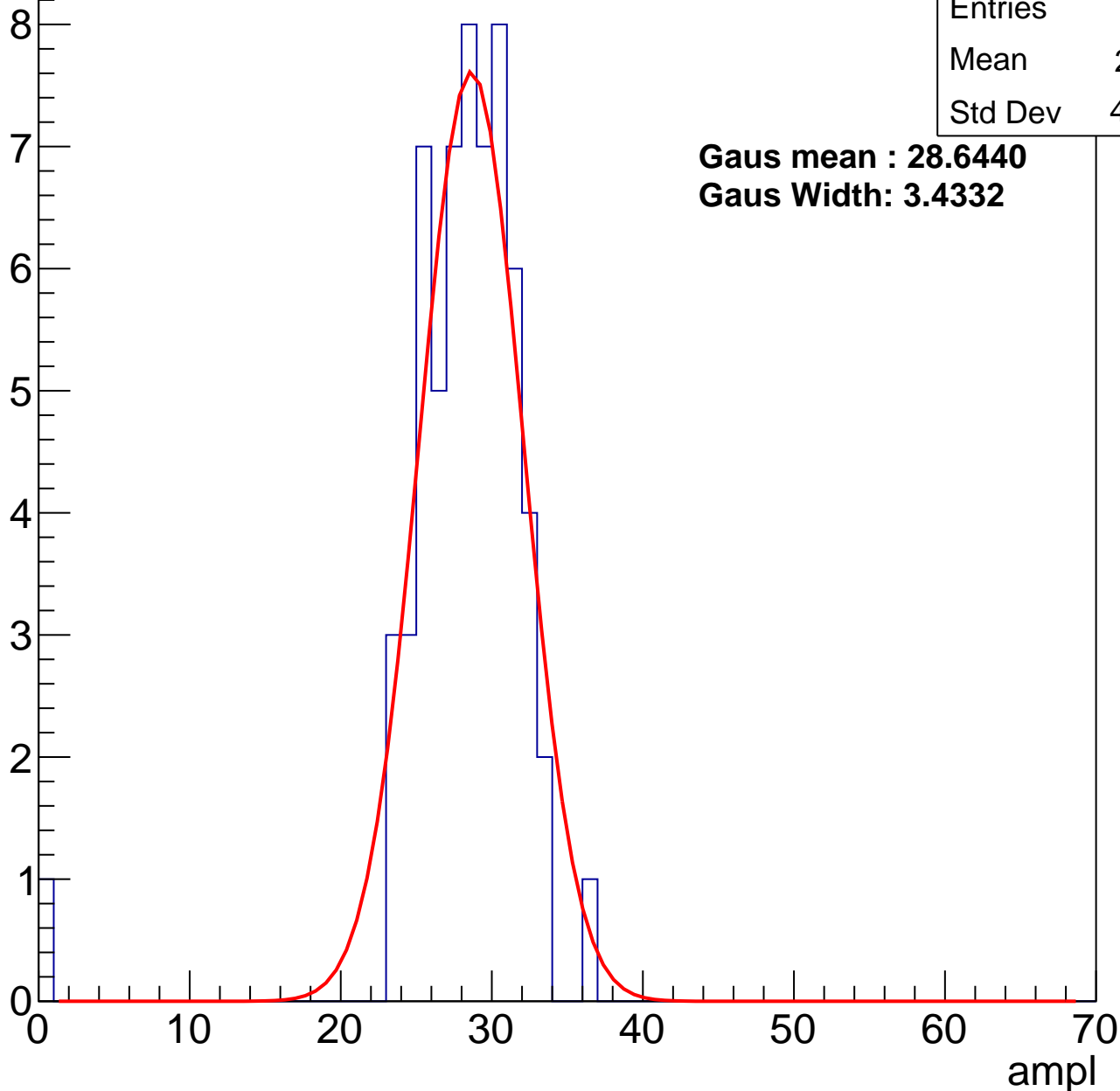
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	27.71
Std Dev	4.513

**Gaus mean : 28.6440**

**Gaus Width: 3.4332**



# B1L000S, U1-ch123, adc1

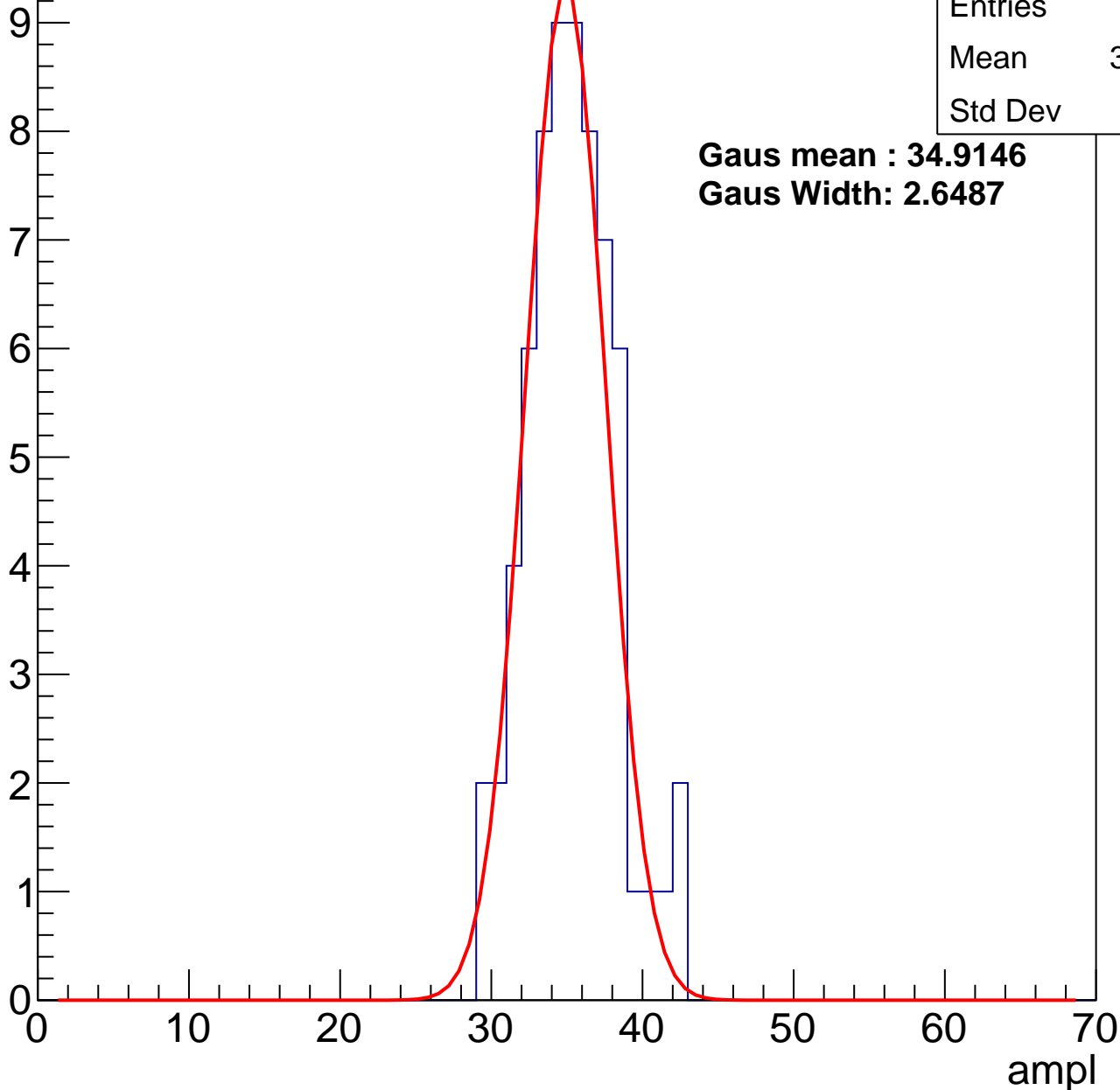
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.82
Std Dev	2.87

**Gaus mean : 34.9146**

**Gaus Width: 2.6487**



# B1L000S, U1-ch123, adc2

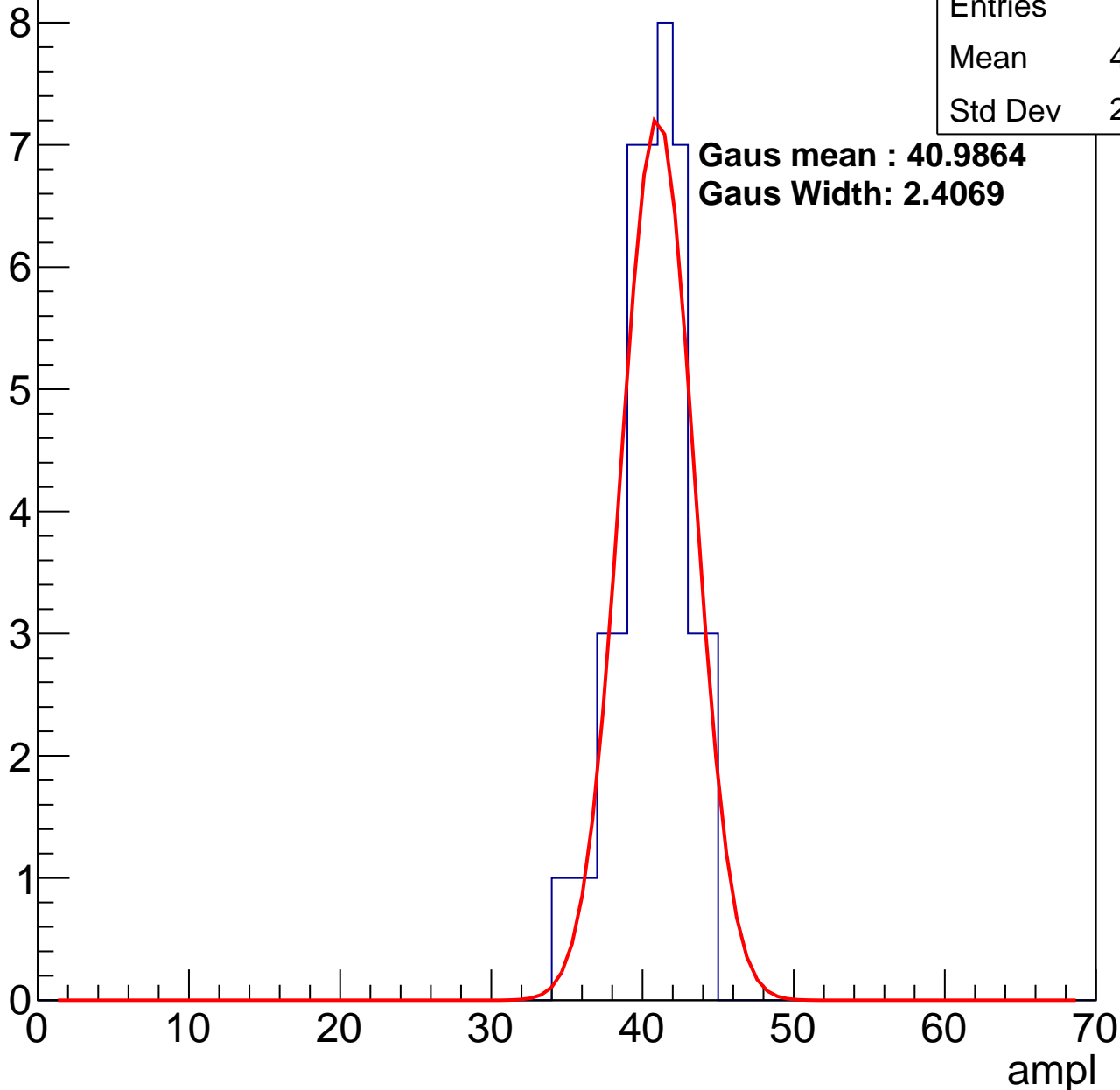
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	40.14
Std Dev	2.302

**Gaus mean : 40.9864**

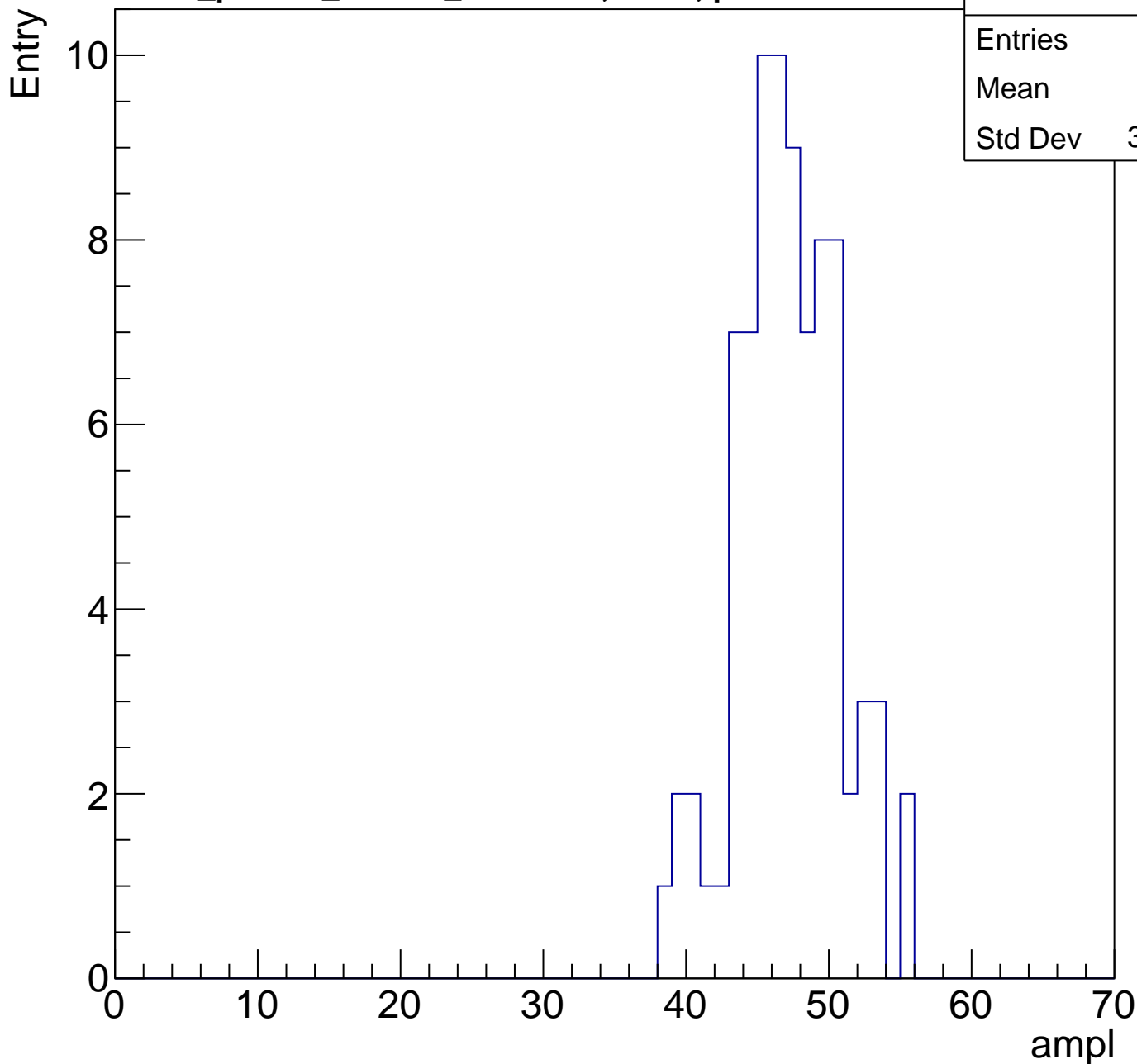
**Gaus Width: 2.4069**



# B1L000S, U1-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	46.7
Std Dev	3.539

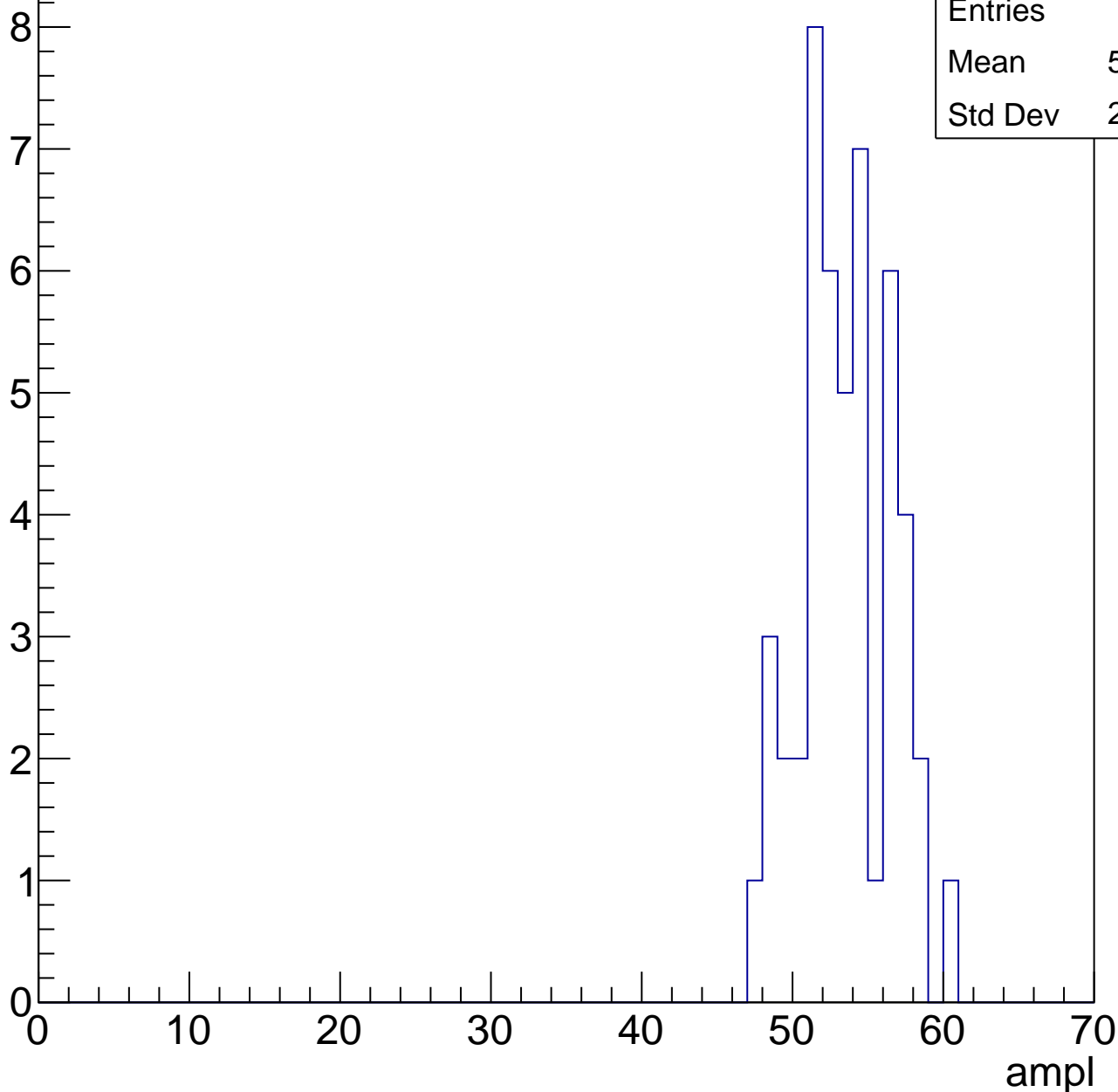


# B1L000S, U1-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

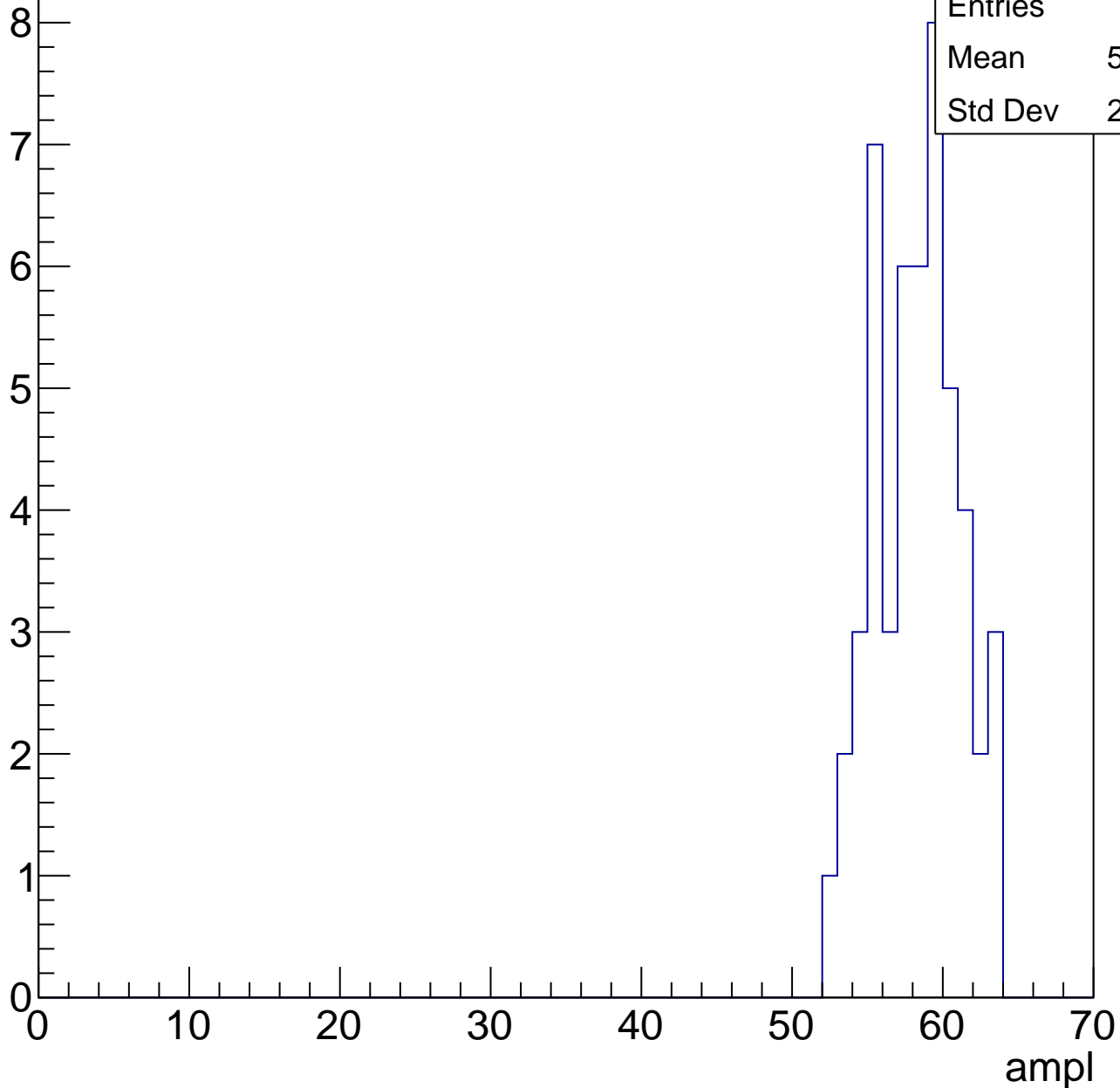
Entries	48
Mean	53.06
Std Dev	2.982



# B1L000S, U1-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

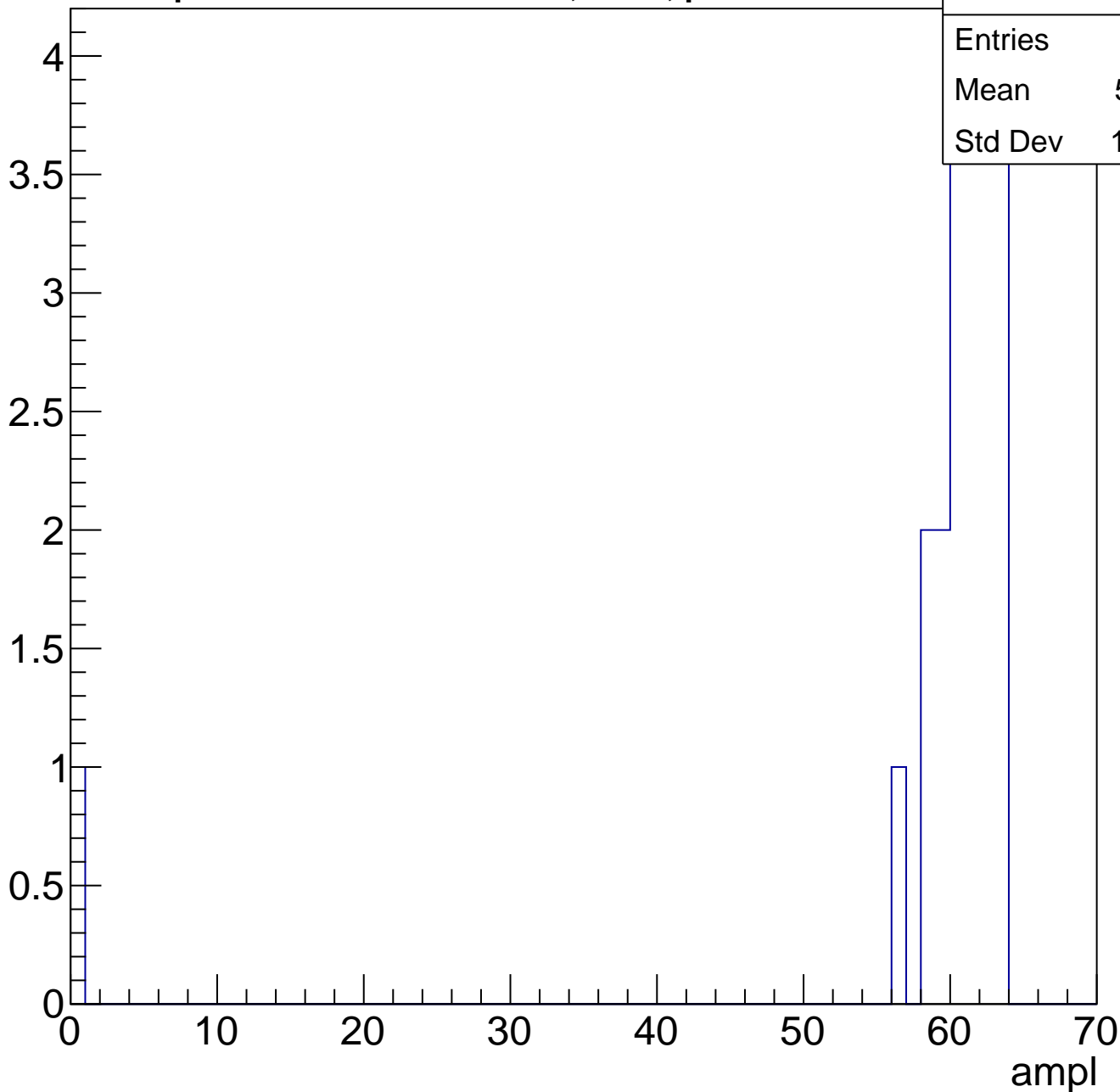


Entries	50
Mean	57.84
Std Dev	2.774

# B1L000S, U1-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

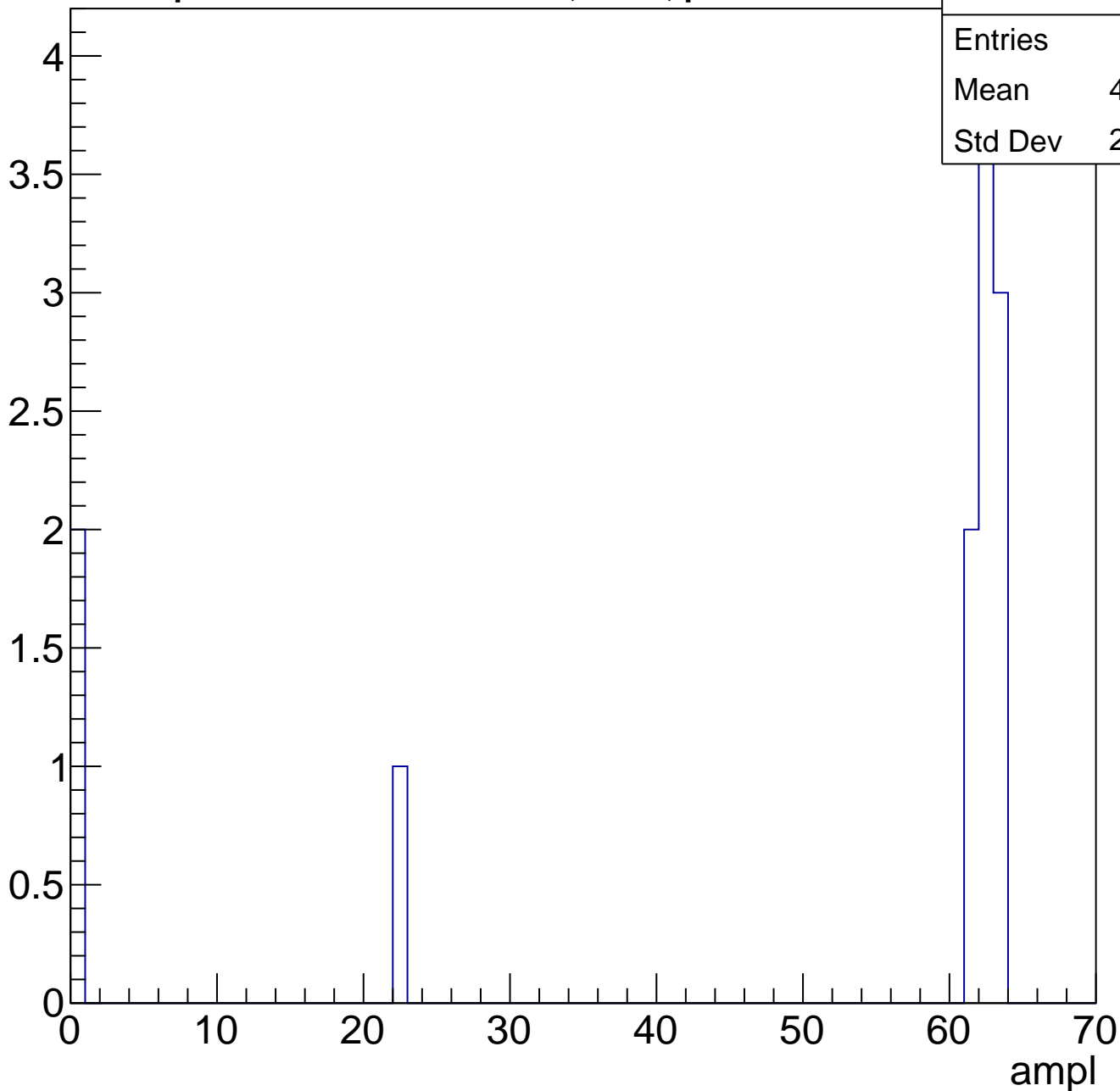




# B1L000S, U1-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch124, adc0

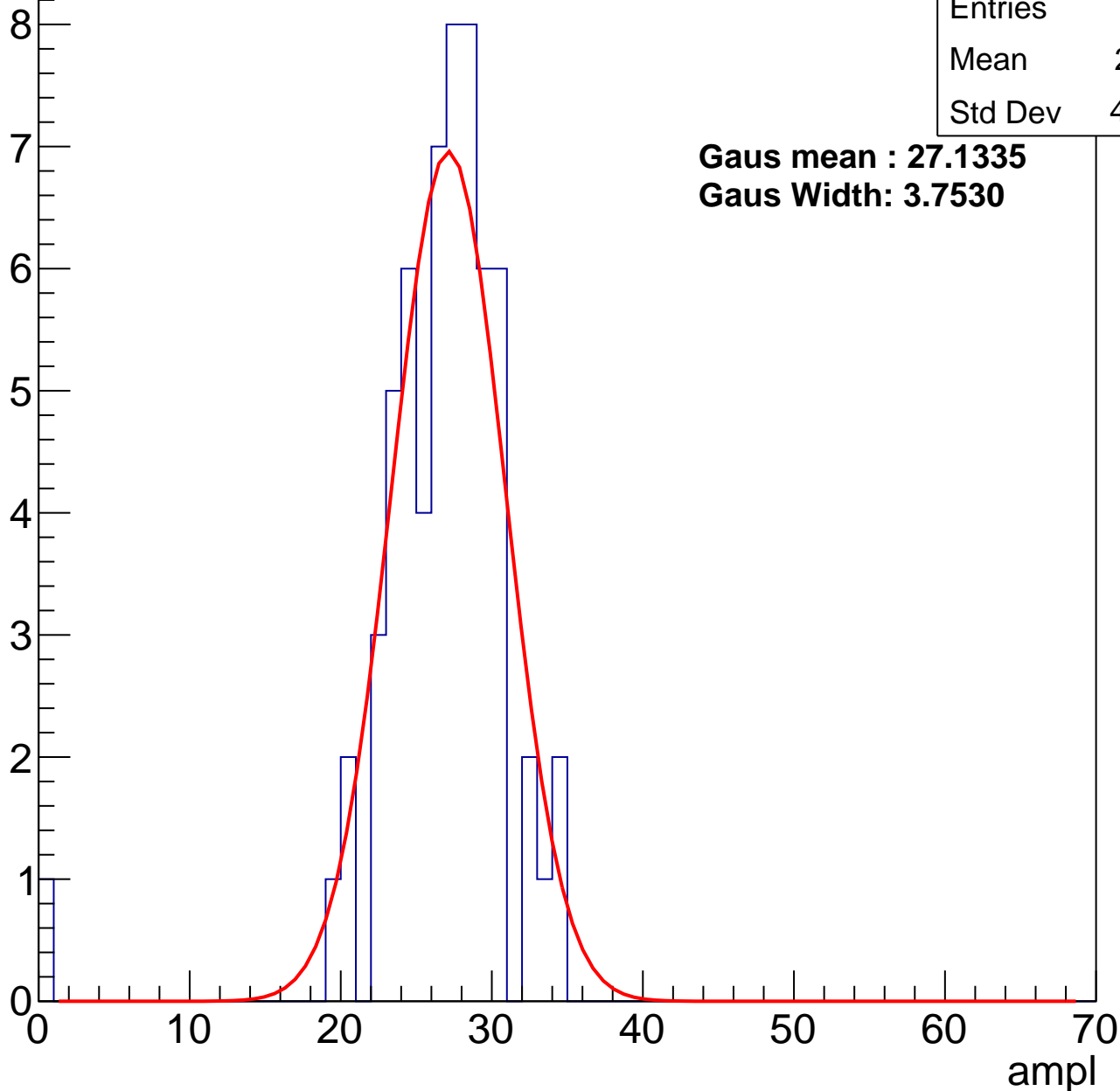
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	26.21
Std Dev	4.674

**Gaus mean : 27.1335**

**Gaus Width: 3.7530**



# B1L000S, U1-ch124, adc1

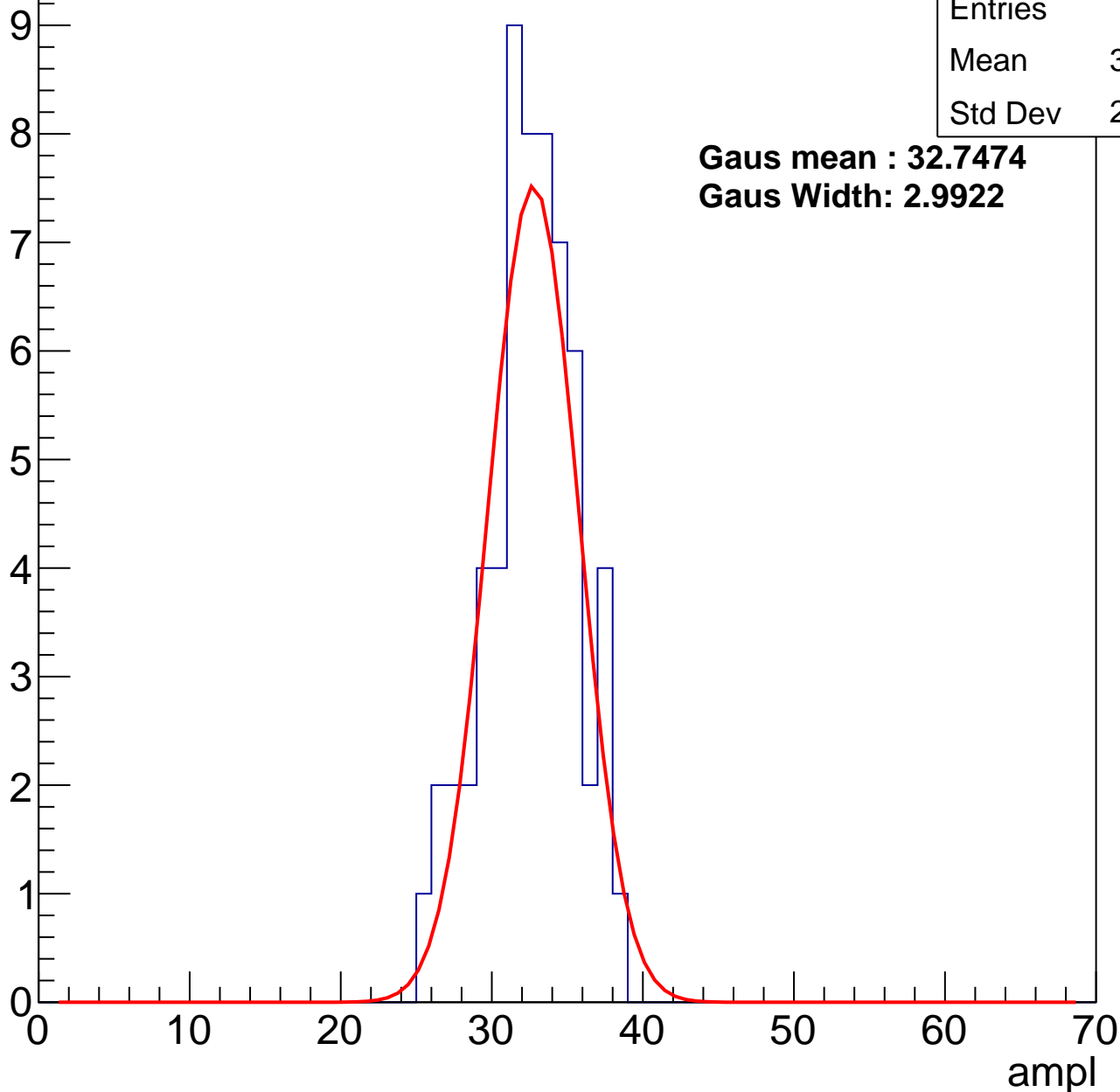
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	32.13
Std Dev	2.947

**Gaus mean : 32.7474**

**Gaus Width: 2.9922**



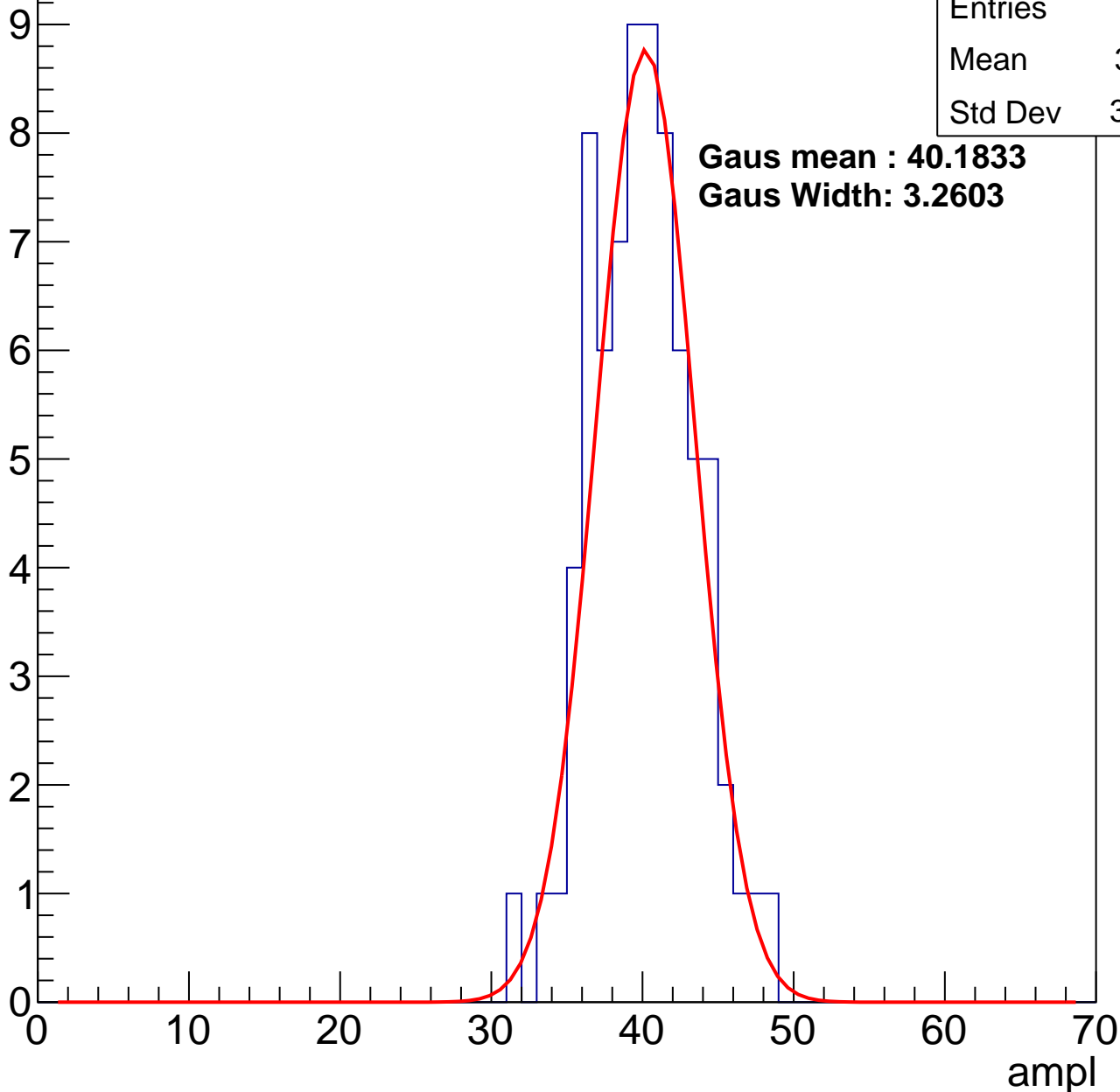
# B1L000S, U1-ch124, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	39.61
Std Dev	3.326

**Gaus mean : 40.1833**  
**Gaus Width: 3.2603**

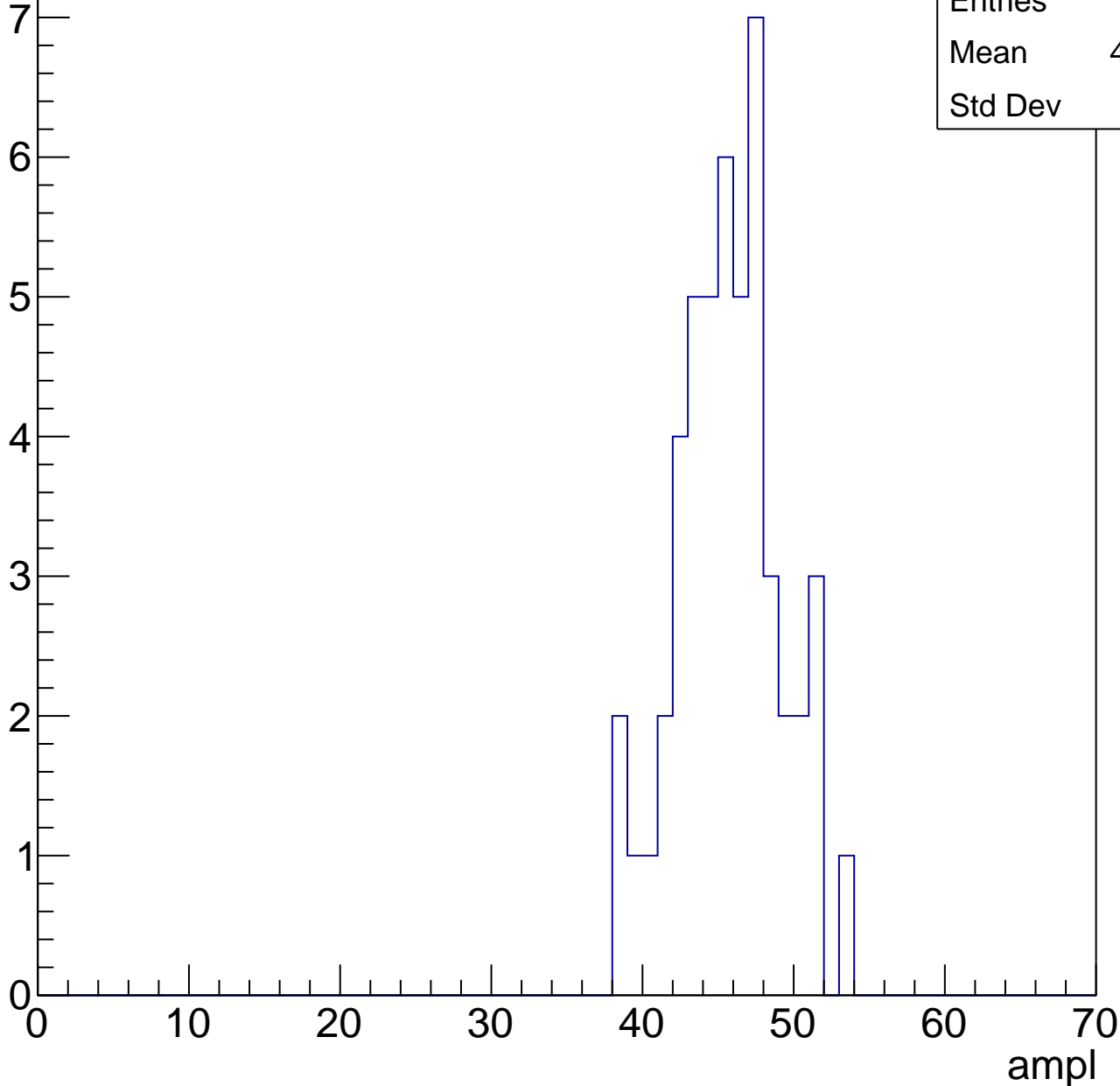


# B1L000S, U1-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	45.24
Std Dev	3.39

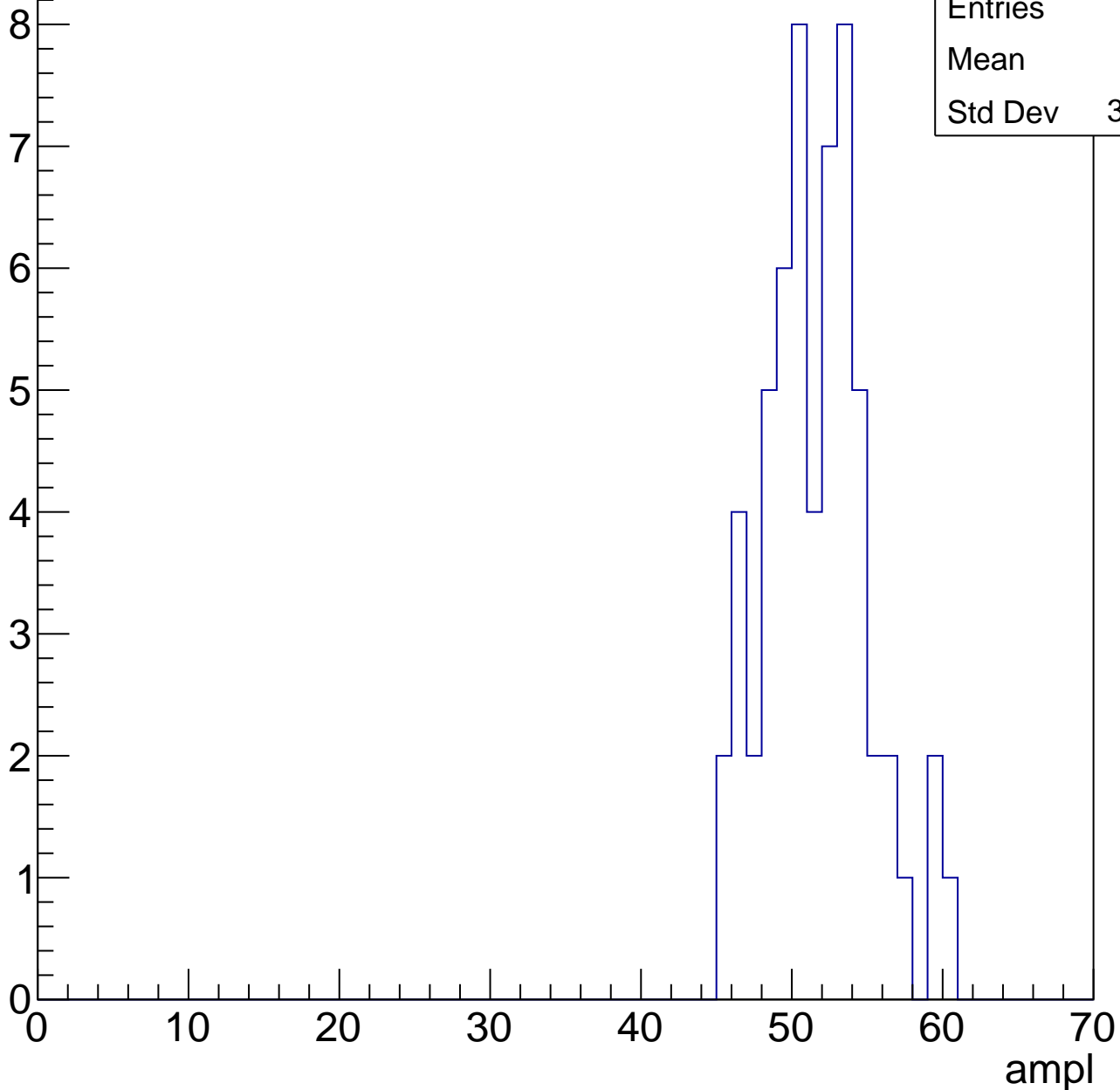


# B1L000S, U1-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	51.2
Std Dev	3.419

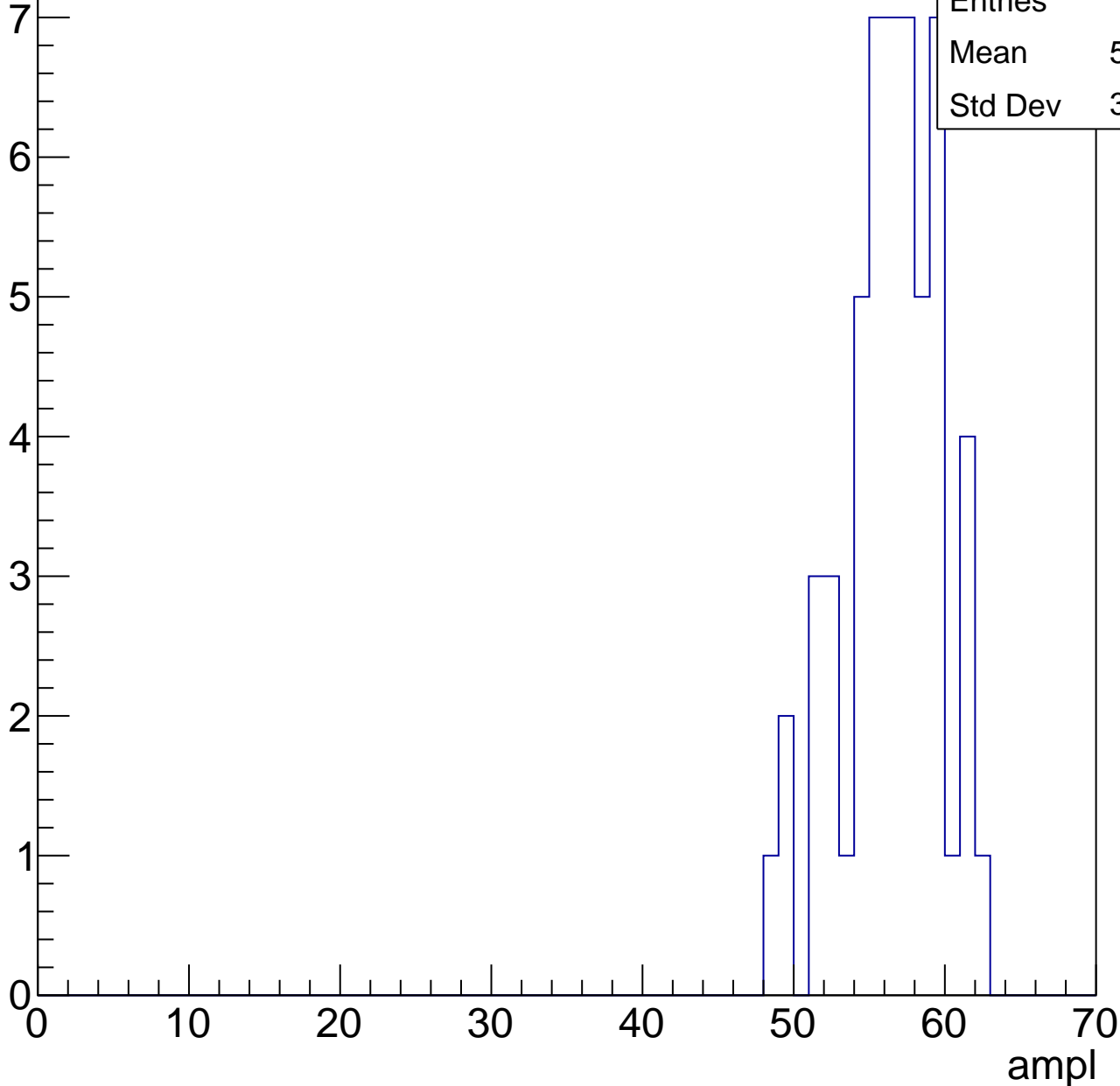


# B1L000S, U1-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	55.98
Std Dev	3.229

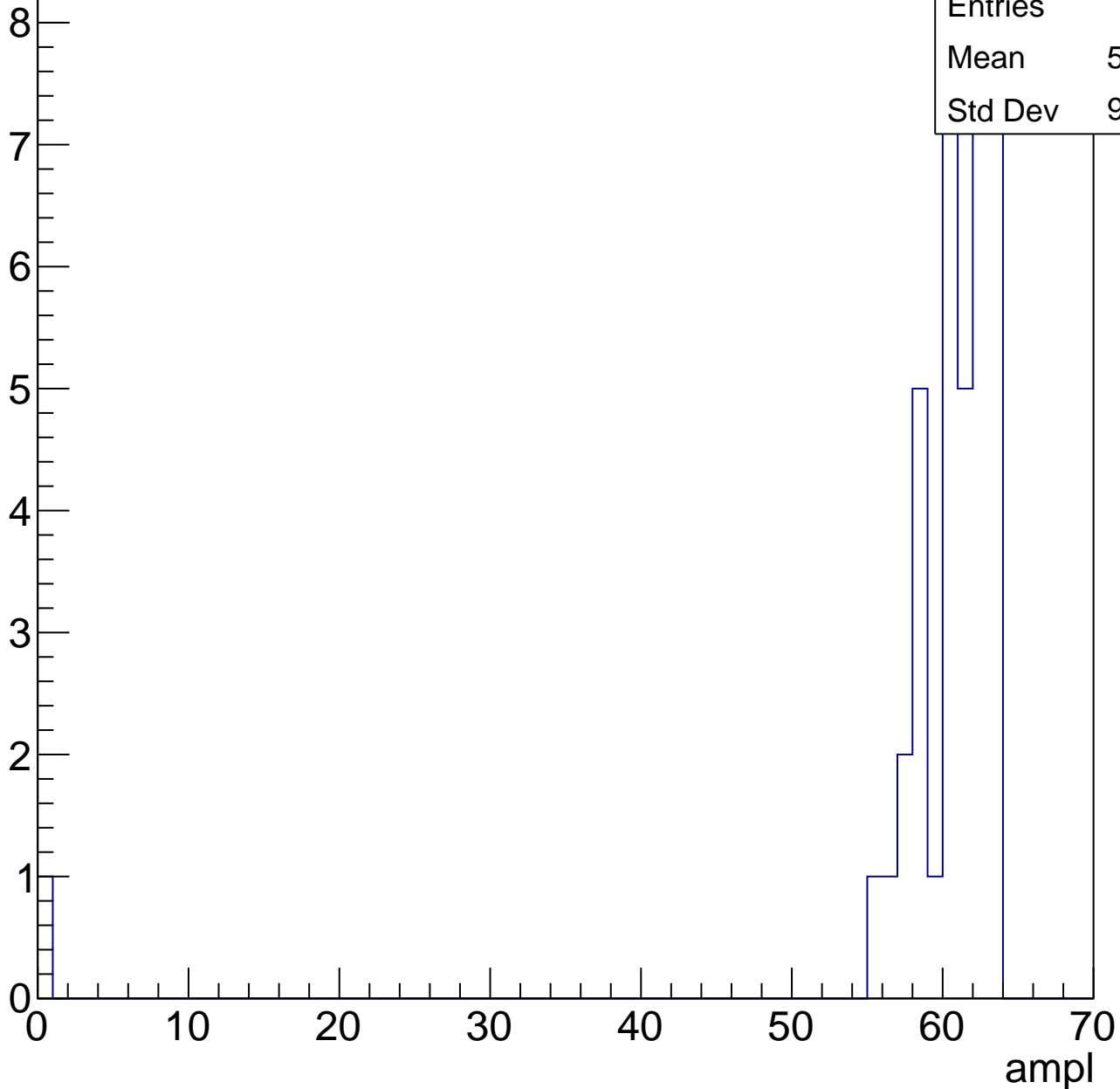


# B1L000S, U1-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	58.98
Std Dev	9.679

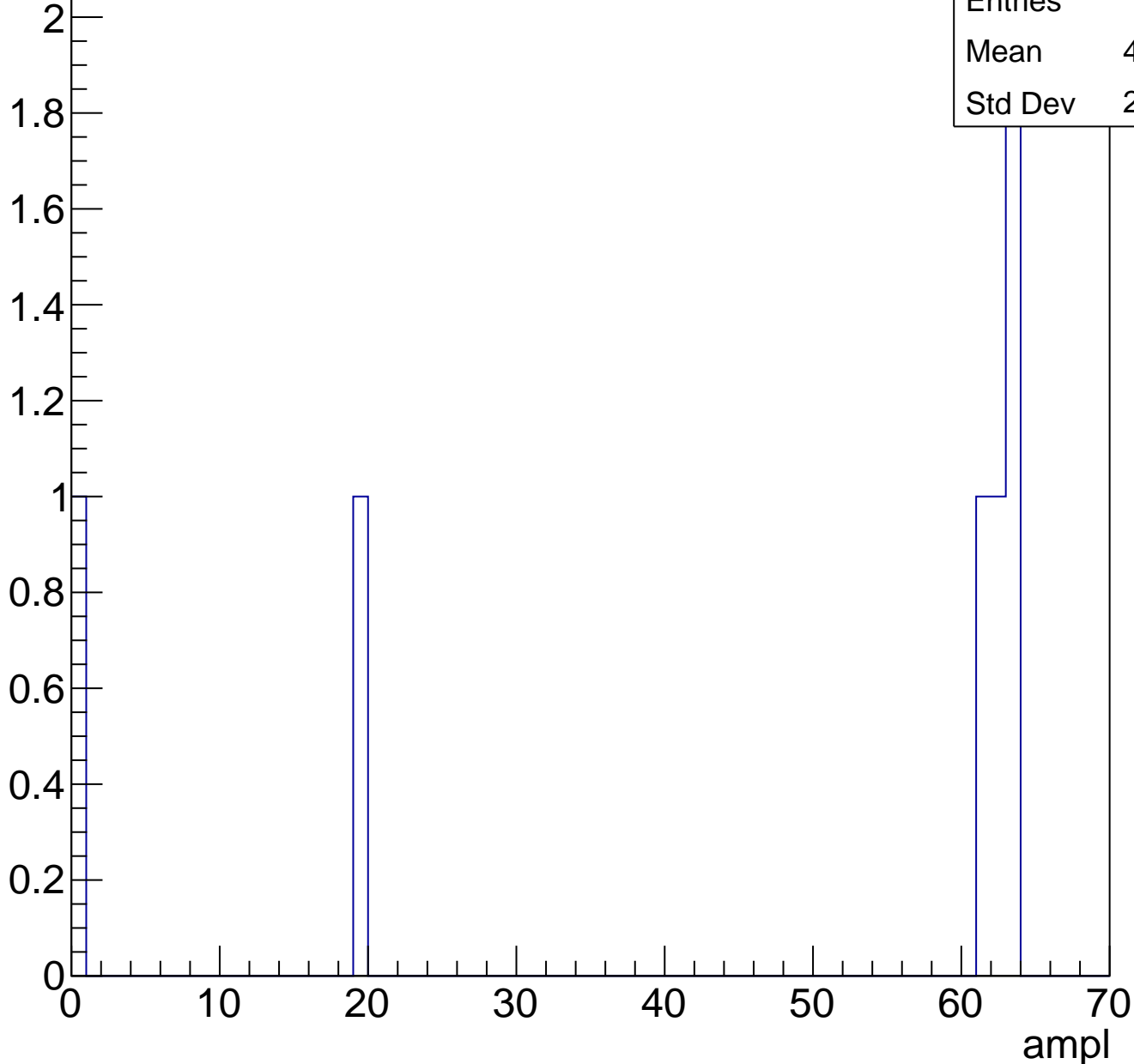




# B1L000S, U1-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch125, adc0

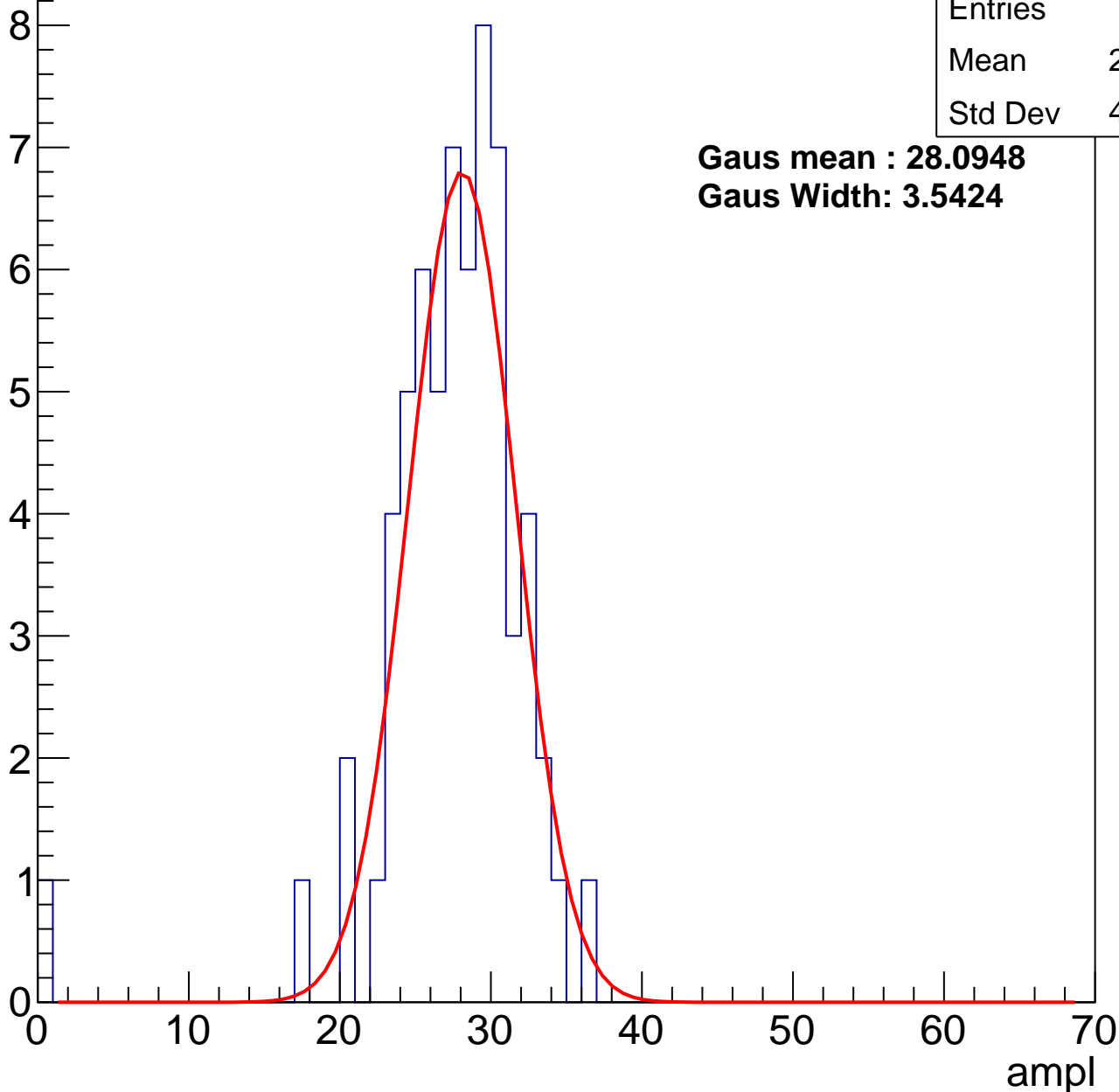
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	26.98
Std Dev	4.907

**Gaus mean : 28.0948**

**Gaus Width: 3.5424**



# B1L000S, U1-ch125, adc1

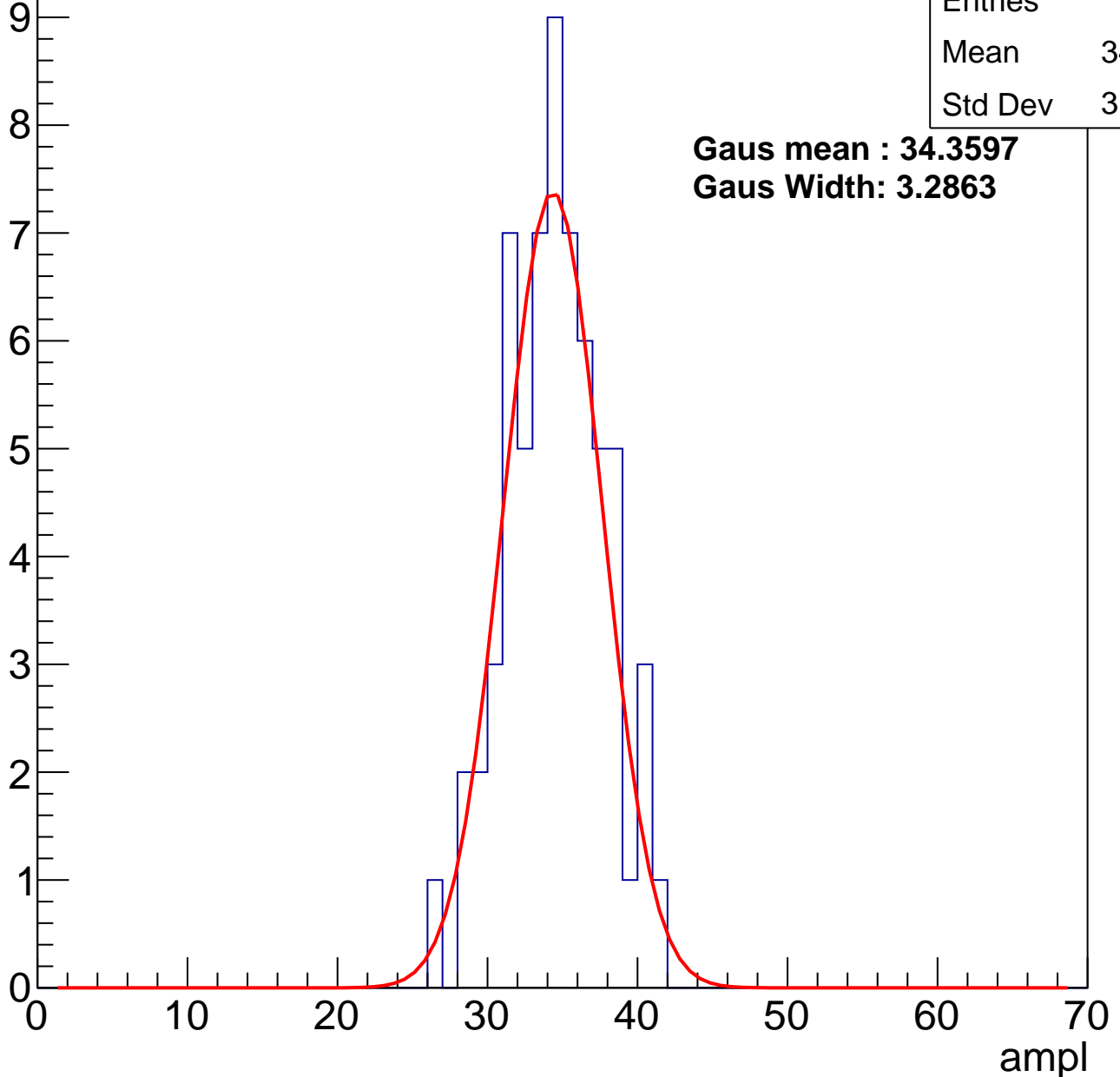
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	34.06
Std Dev	3.216

**Gaus mean : 34.3597**

**Gaus Width: 3.2863**



# B1L000S, U1-ch125, adc2

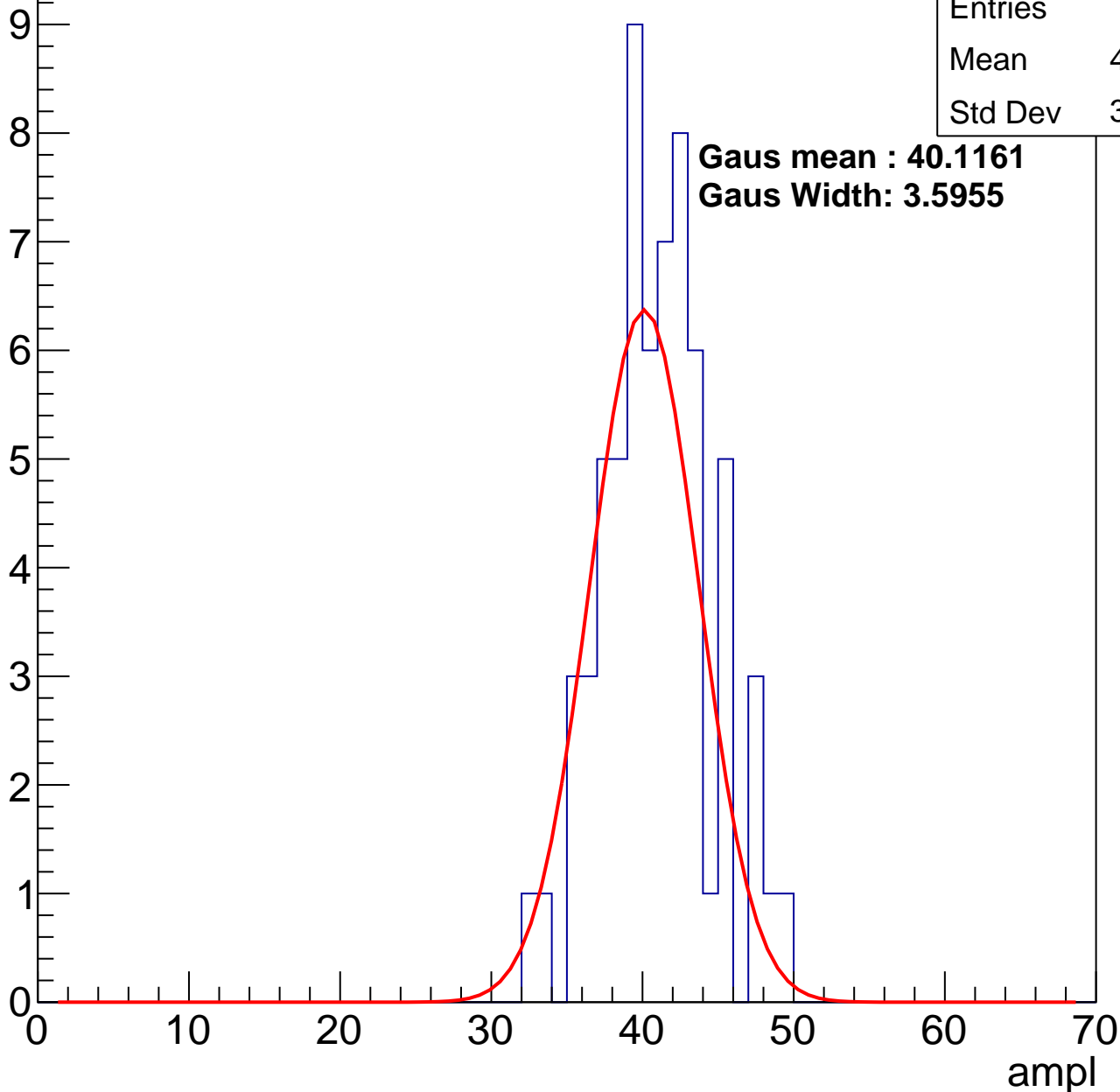
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	40.49
Std Dev	3.552

**Gaus mean : 40.1161**

**Gaus Width: 3.5955**

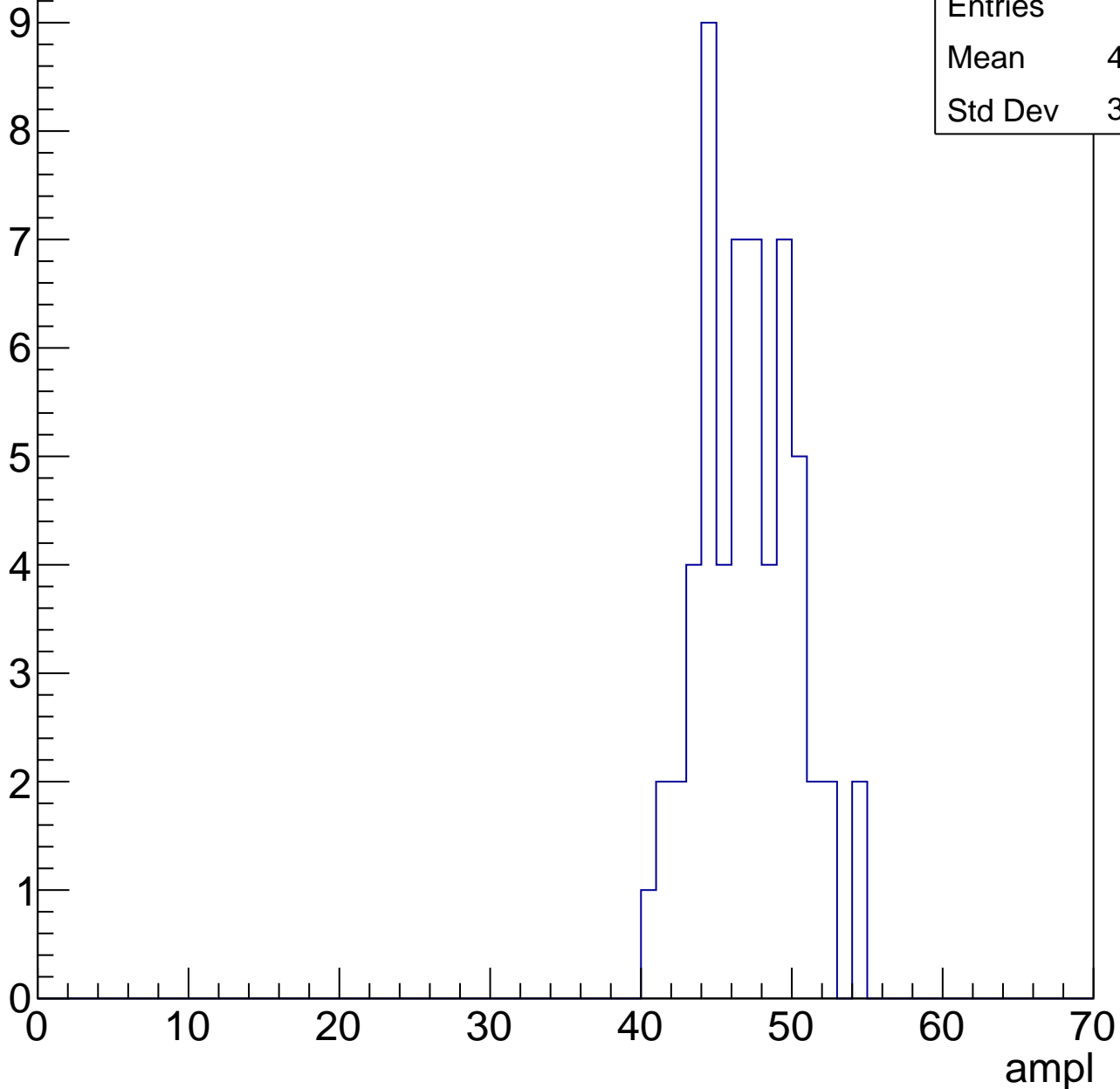


# B1L000S, U1-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	46.62
Std Dev	3.183

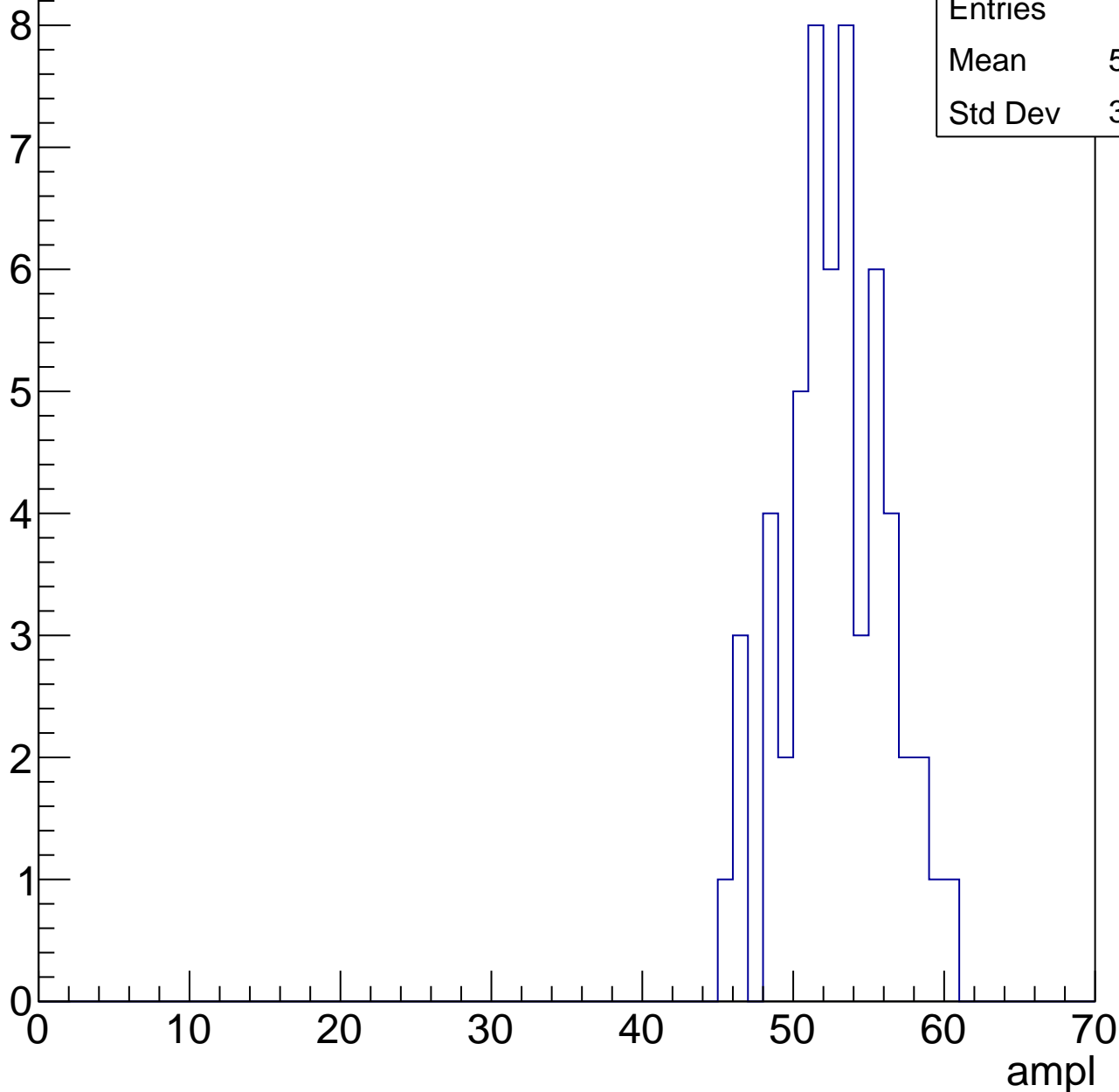


# B1L000S, U1-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	52.36
Std Dev	3.362

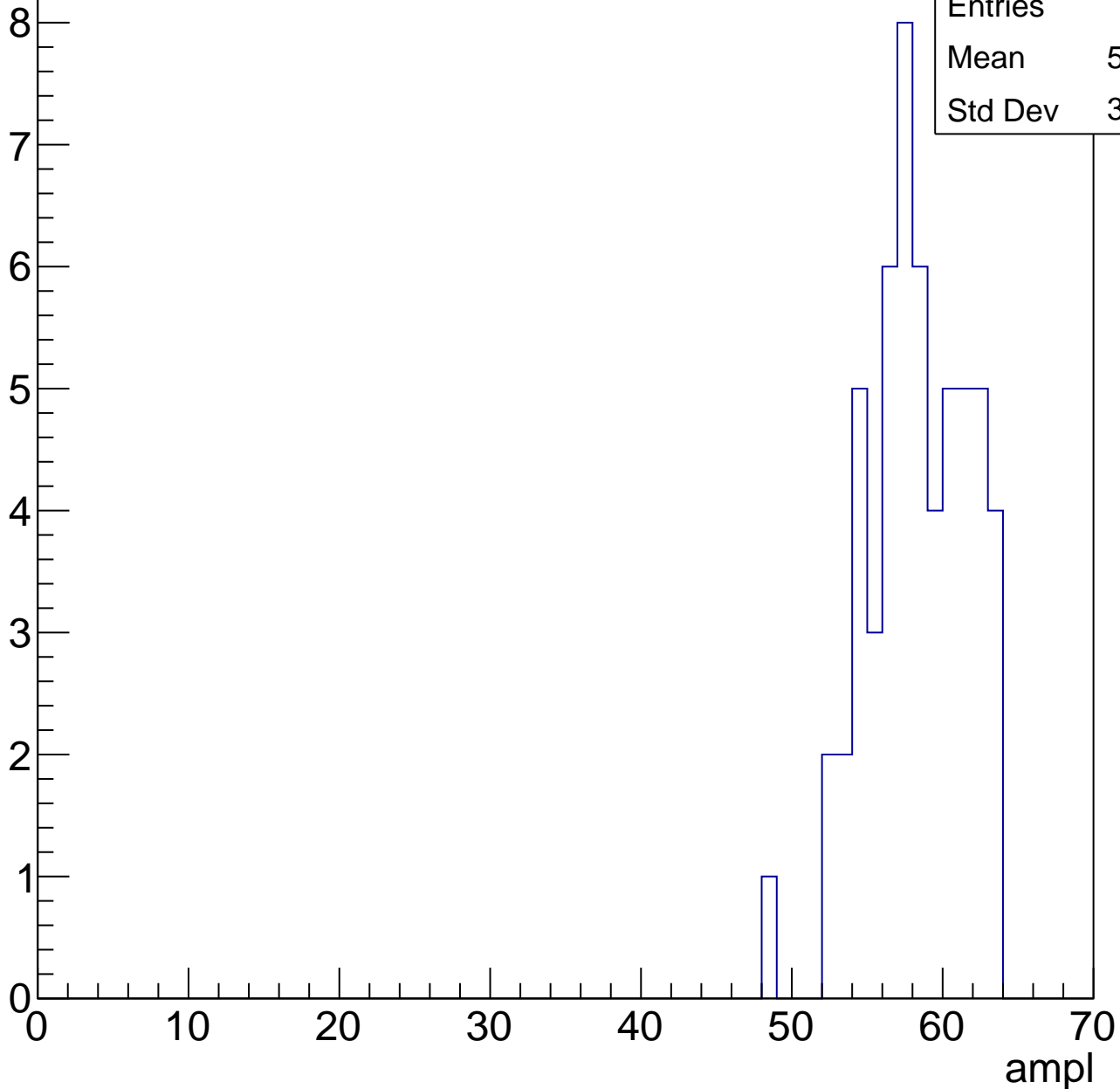


# B1L000S, U1-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	57.79
Std Dev	3.293

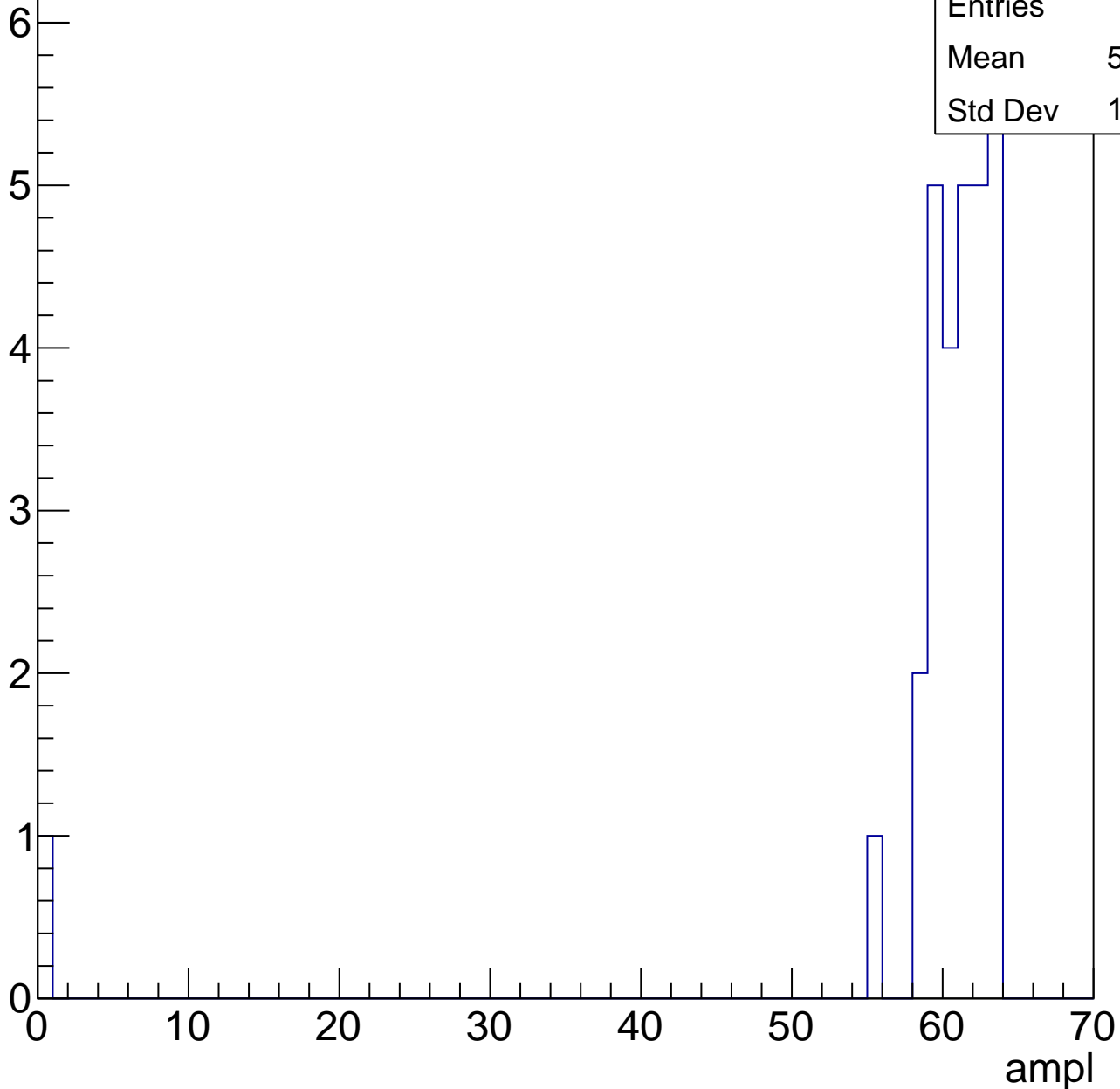


# B1L000S, U1-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	58.59
Std Dev	11.23





# B1L000S, U1-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch126, adc0

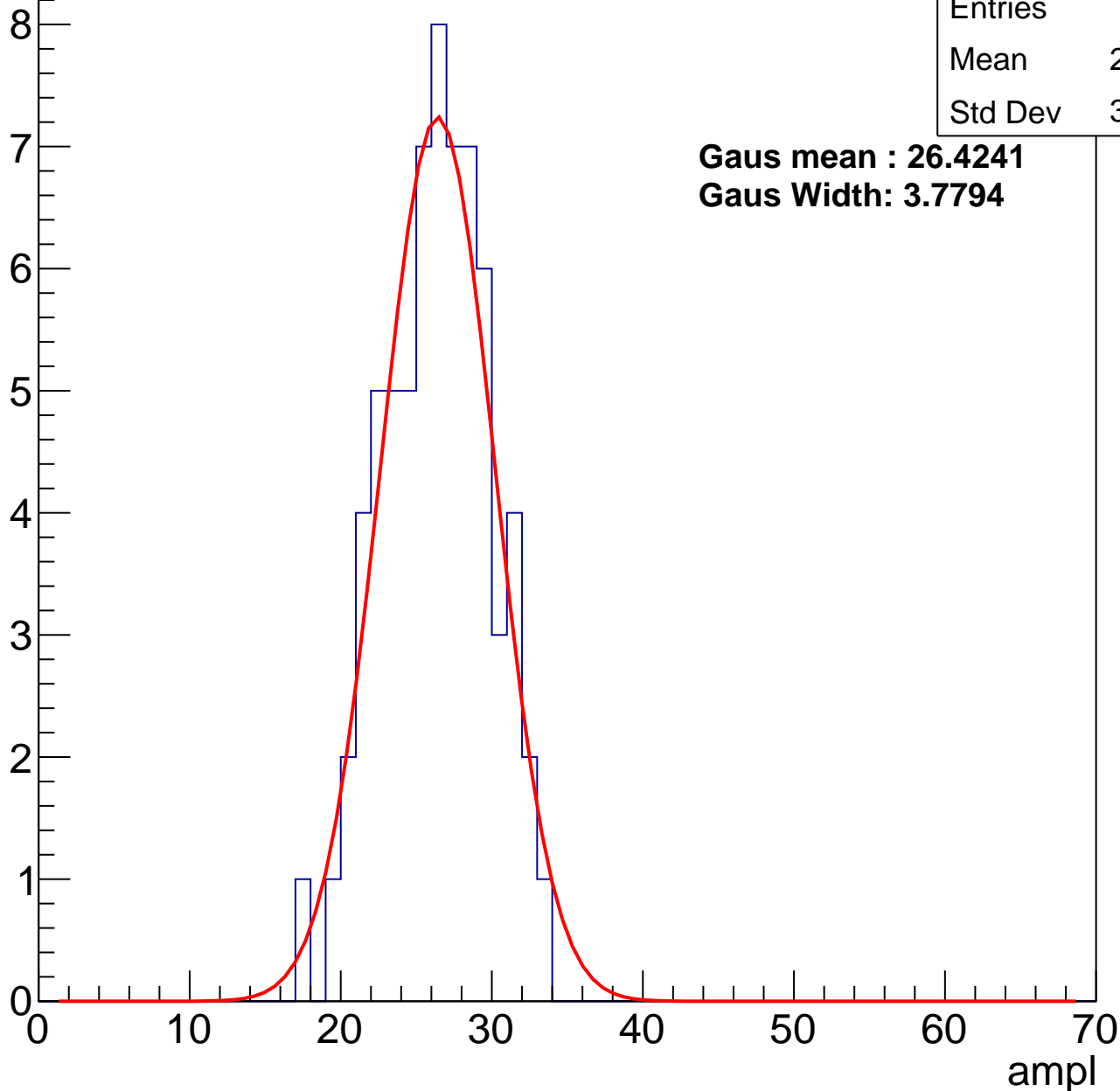
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	25.85
Std Dev	3.452

**Gaus mean : 26.4241**

**Gaus Width: 3.7794**



# B1L000S, U1-ch126, adc1

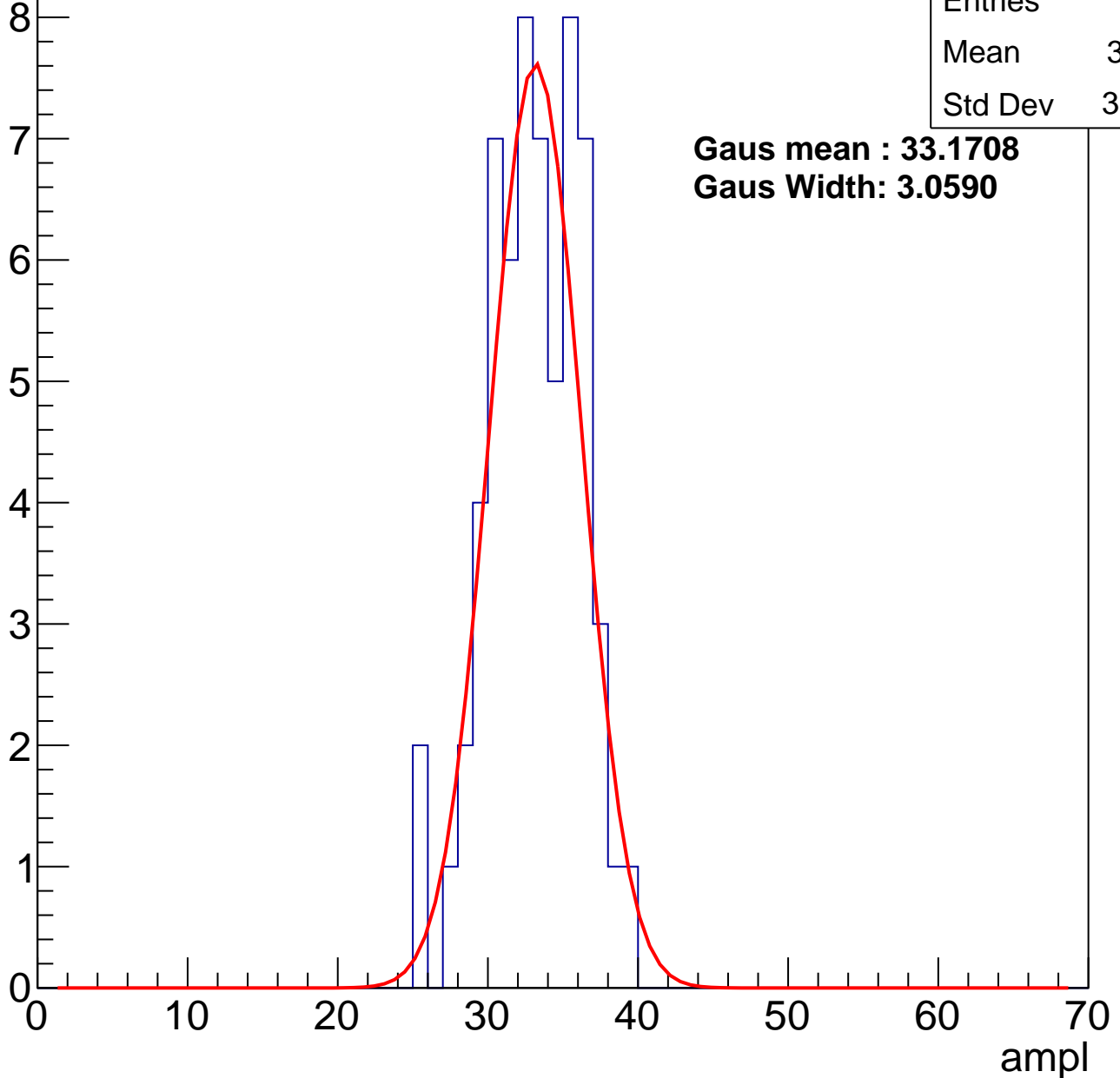
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	32.61
Std Dev	3.039

**Gaus mean : 33.1708**

**Gaus Width: 3.0590**



# B1L000S, U1-ch126, adc2

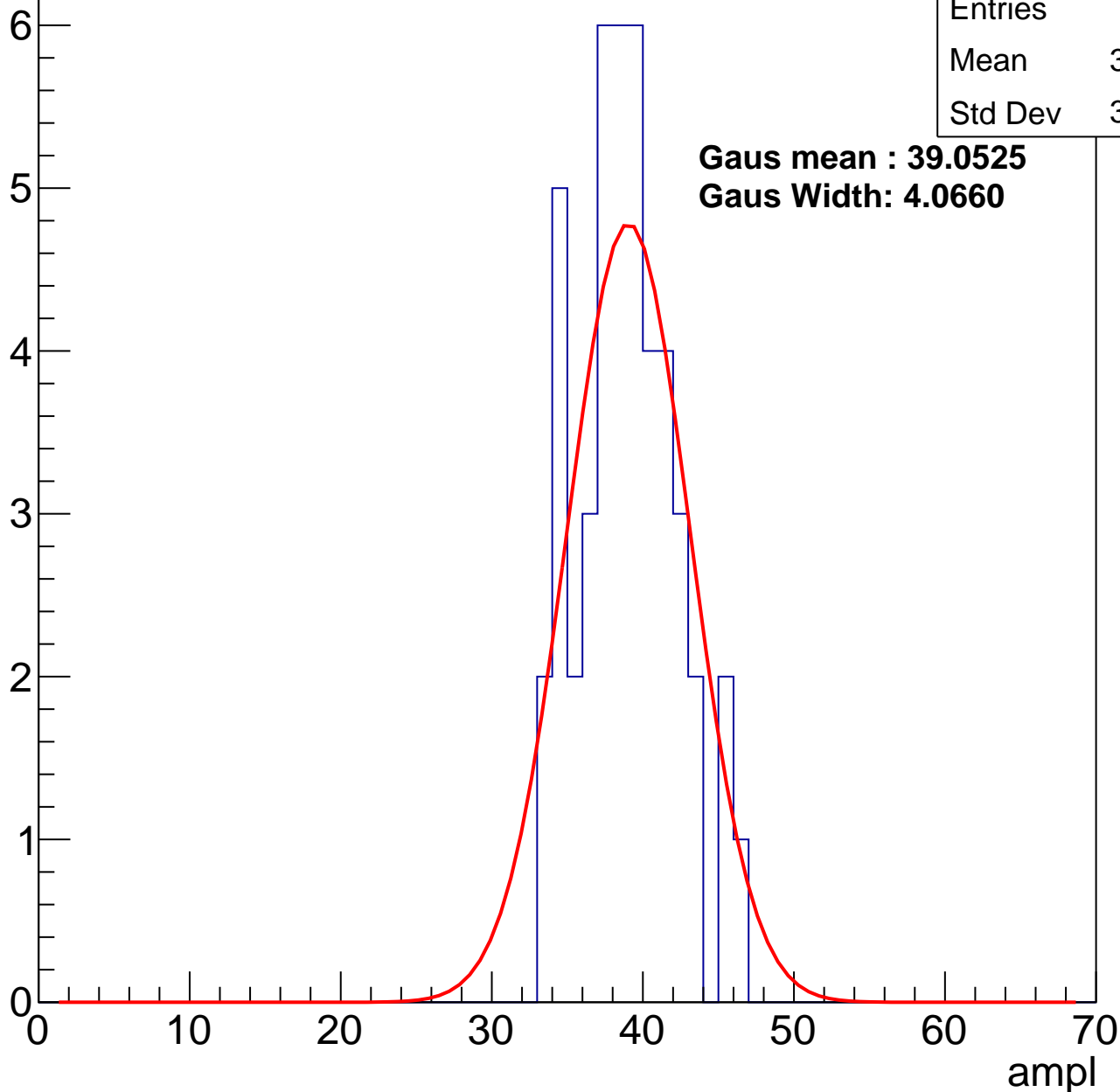
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	38.48
Std Dev	3.208

**Gaus mean : 39.0525**

**Gaus Width: 4.0660**

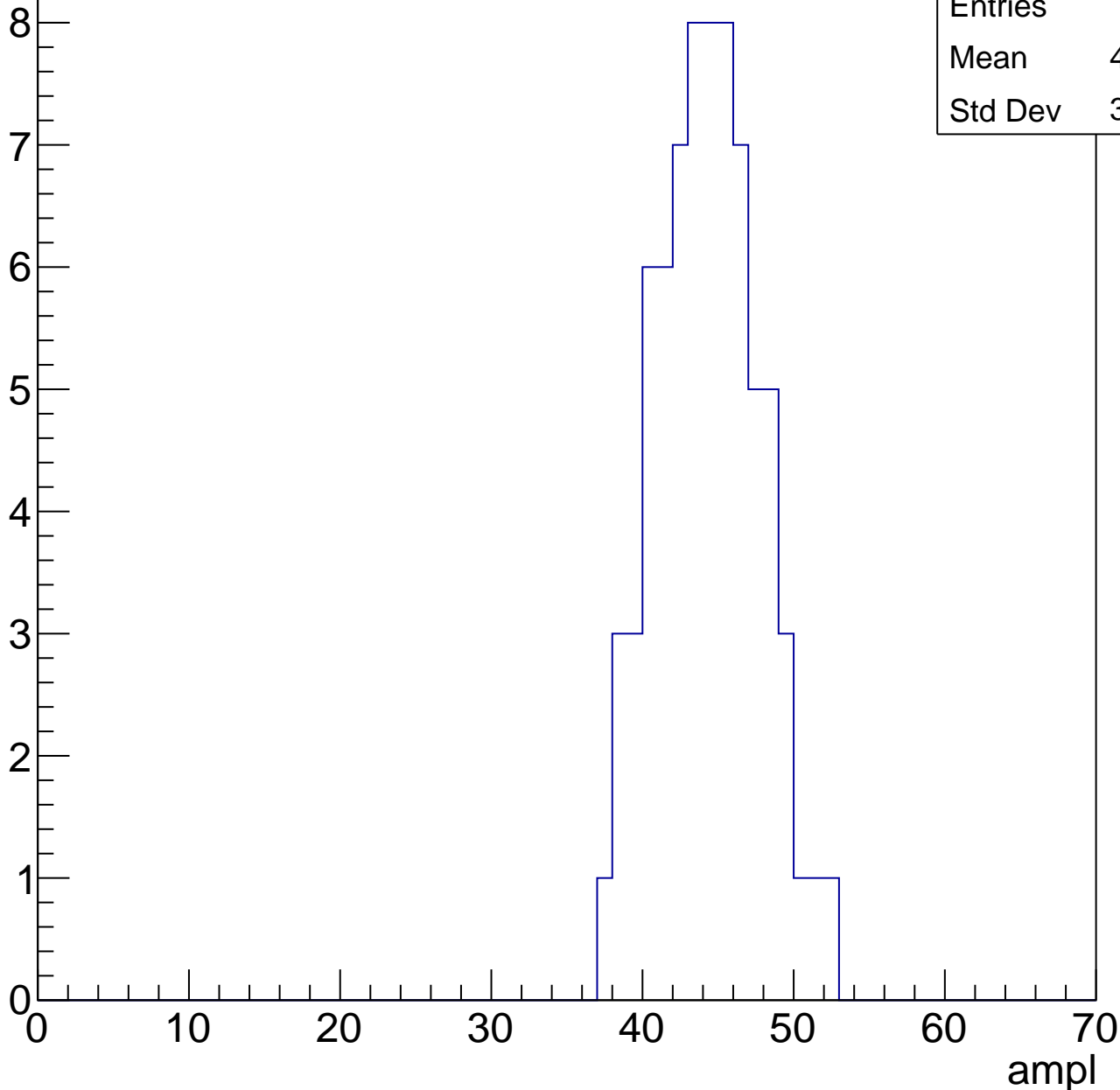


# B1L000S, U1-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	43.85
Std Dev	3.313

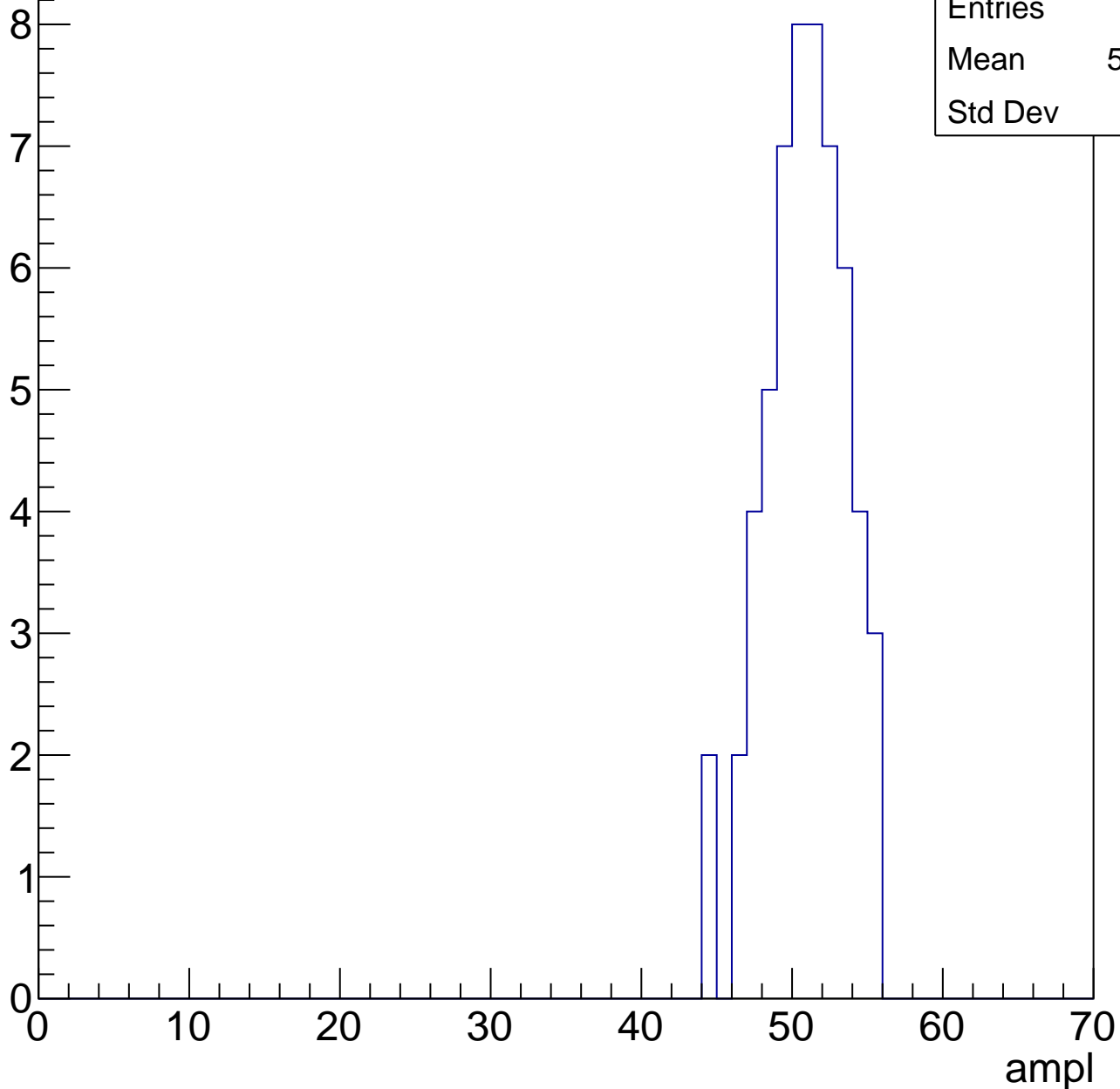


# B1L000S, U1-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	50.39
Std Dev	2.63



# B1L000S, U1-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

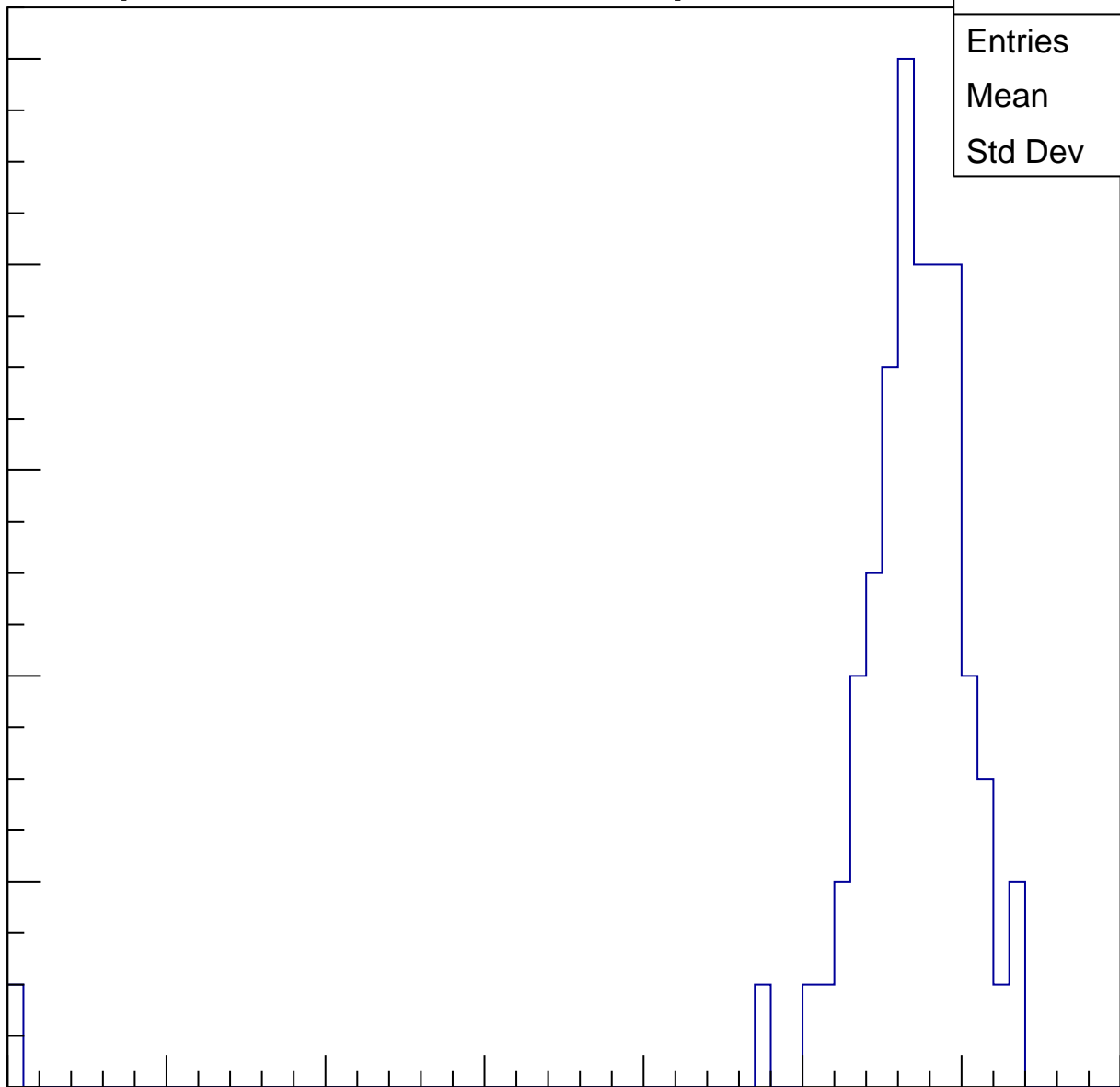
Entries	66
Mean	55.79
Std Dev	7.543

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

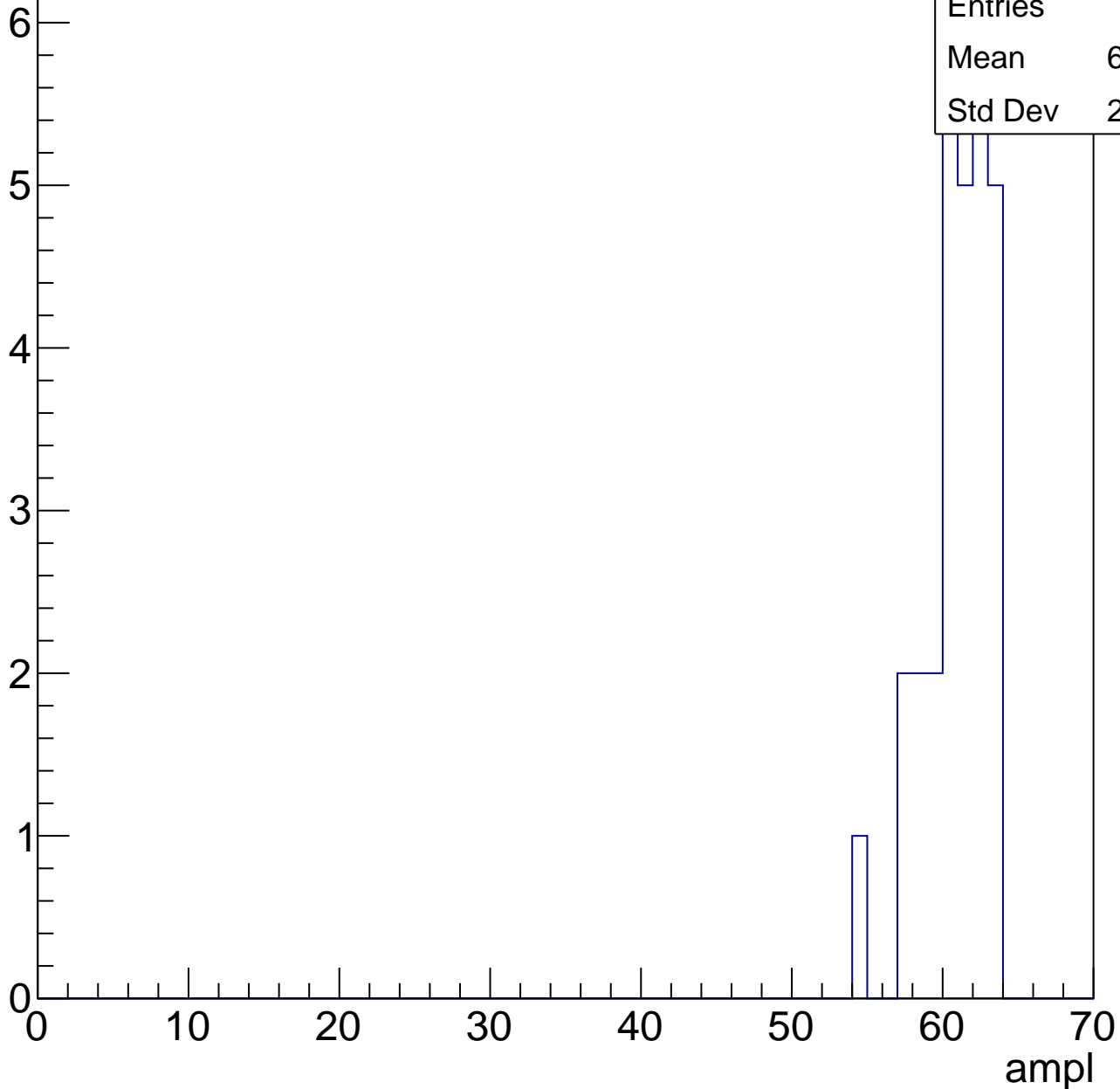


# B1L000S, U1-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	29
Mean	60.48
Std Dev	2.127

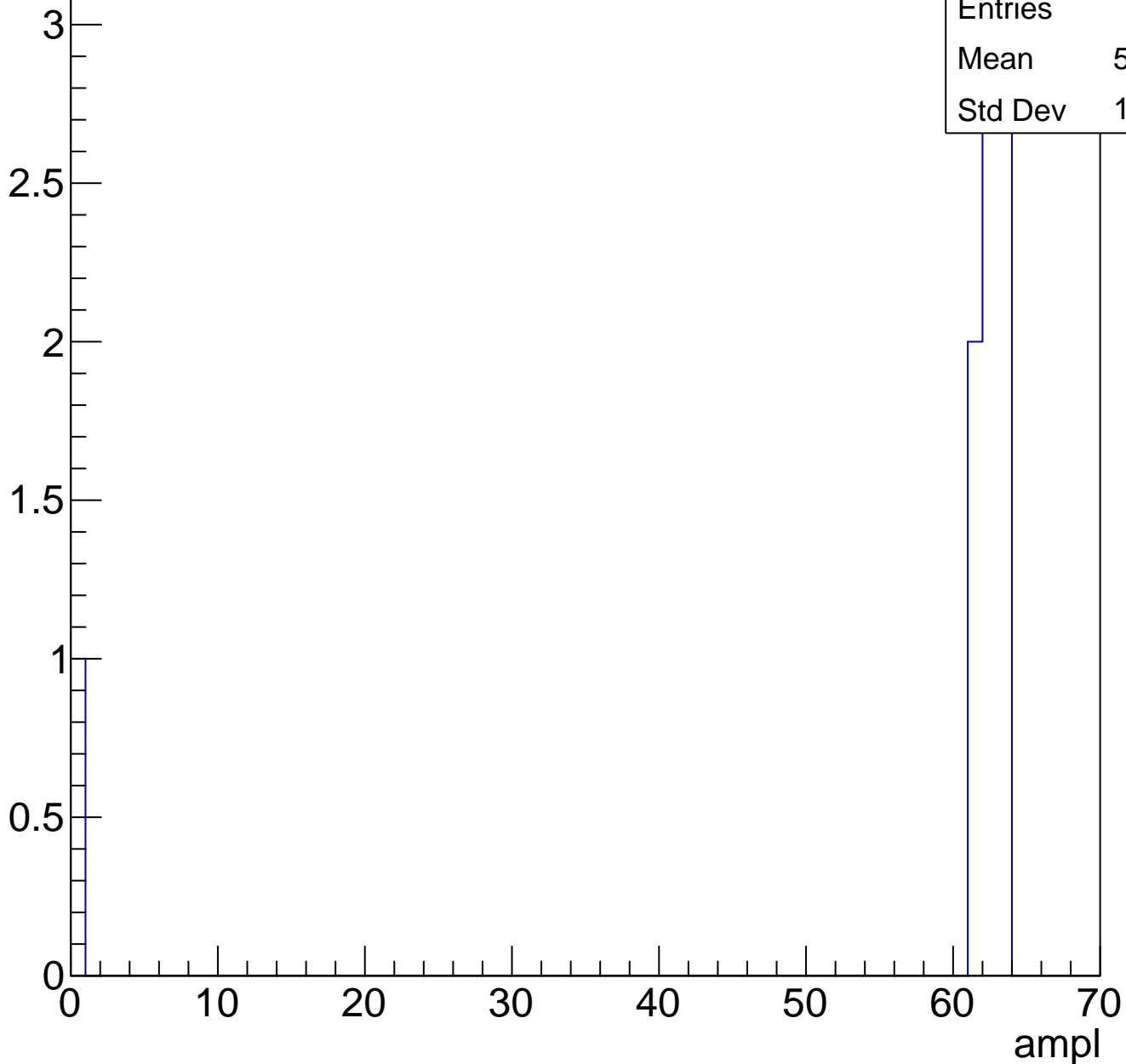




# B1L000S, U1-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch127, adc0

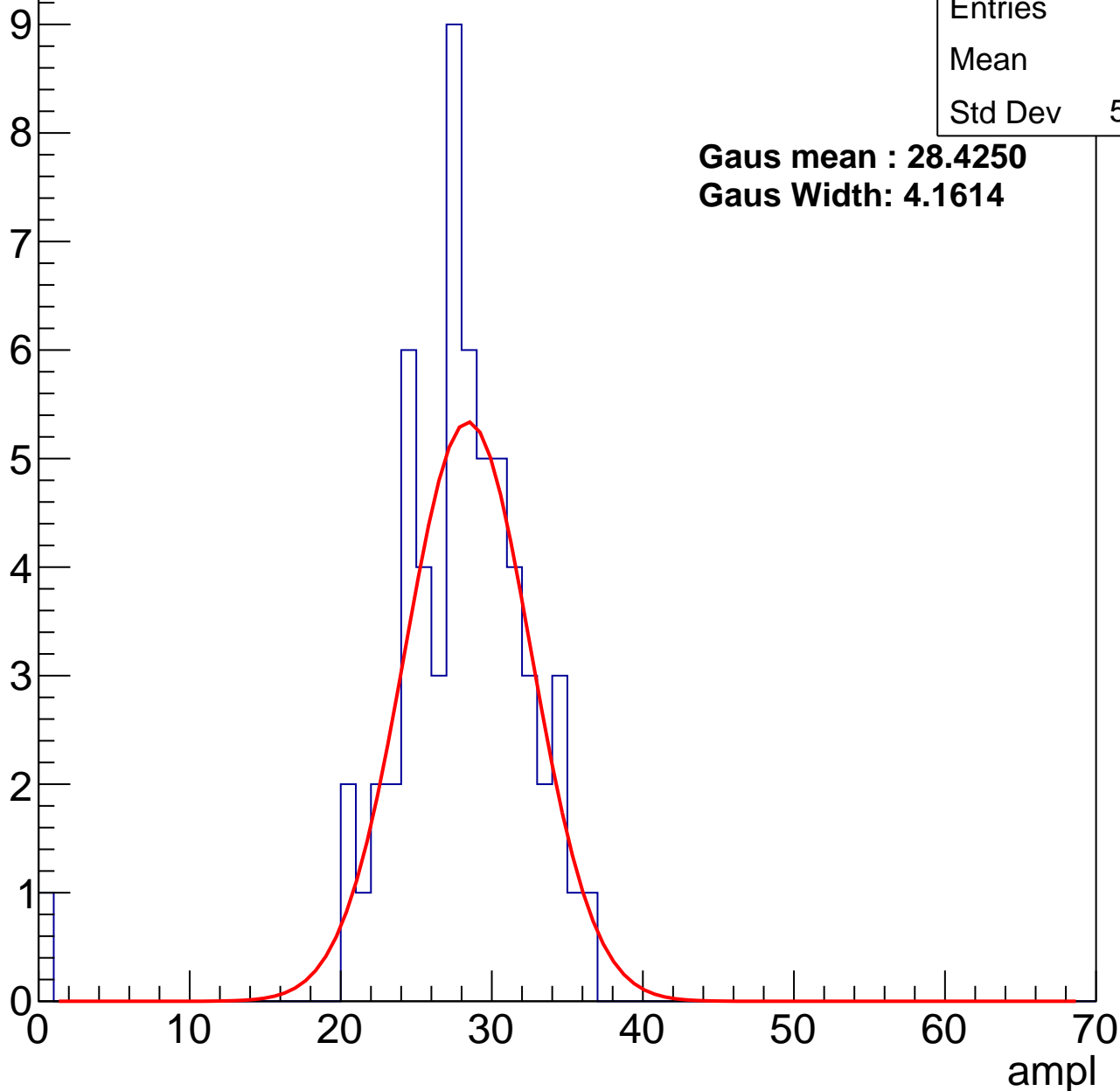
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.3
Std Dev	5.142

**Gaus mean : 28.4250**

**Gaus Width: 4.1614**



# B1L000S, U1-ch127, adc1

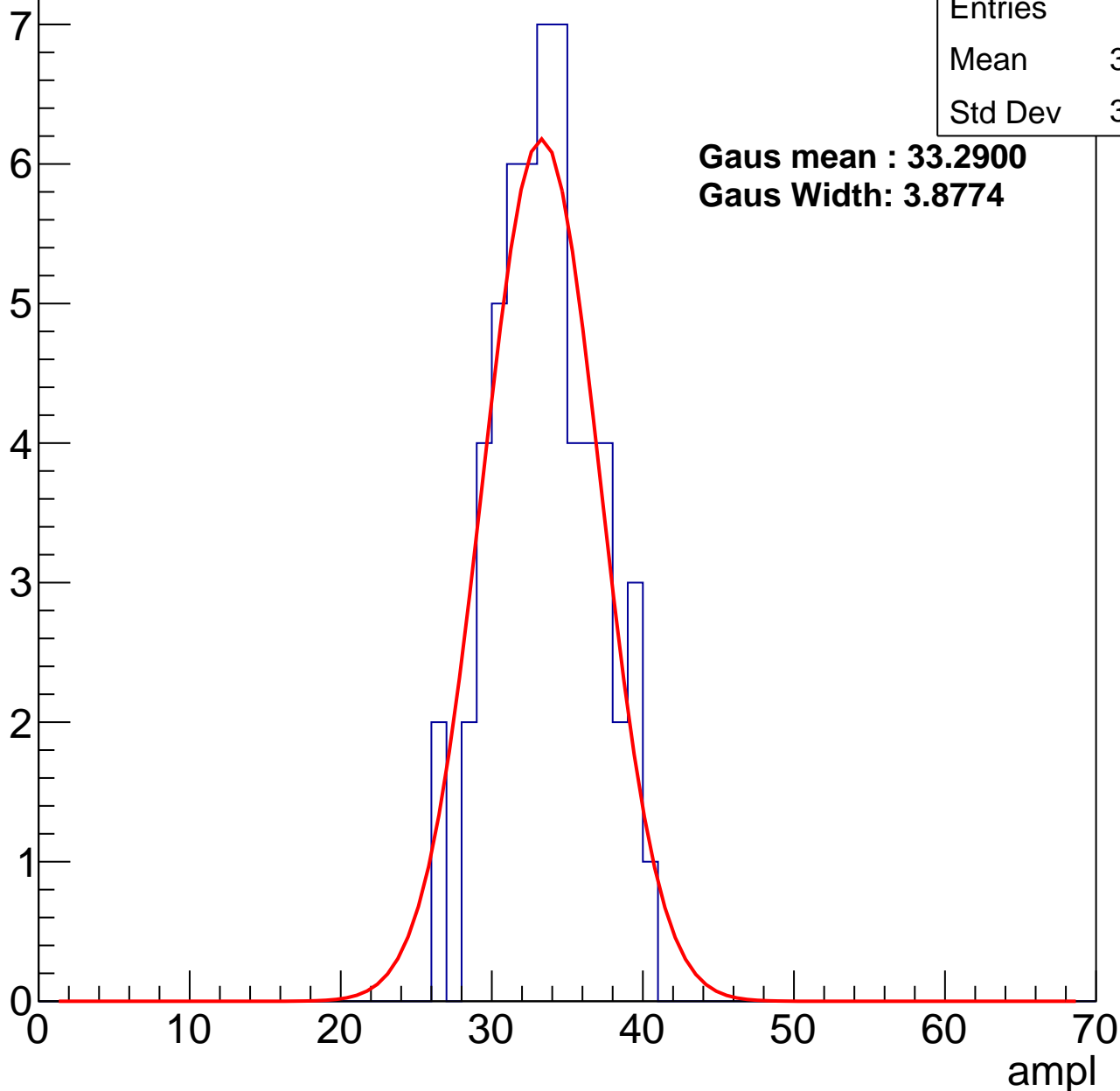
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	33.09
Std Dev	3.289

**Gaus mean : 33.2900**

**Gaus Width: 3.8774**



# B1L000S, U1-ch127, adc2

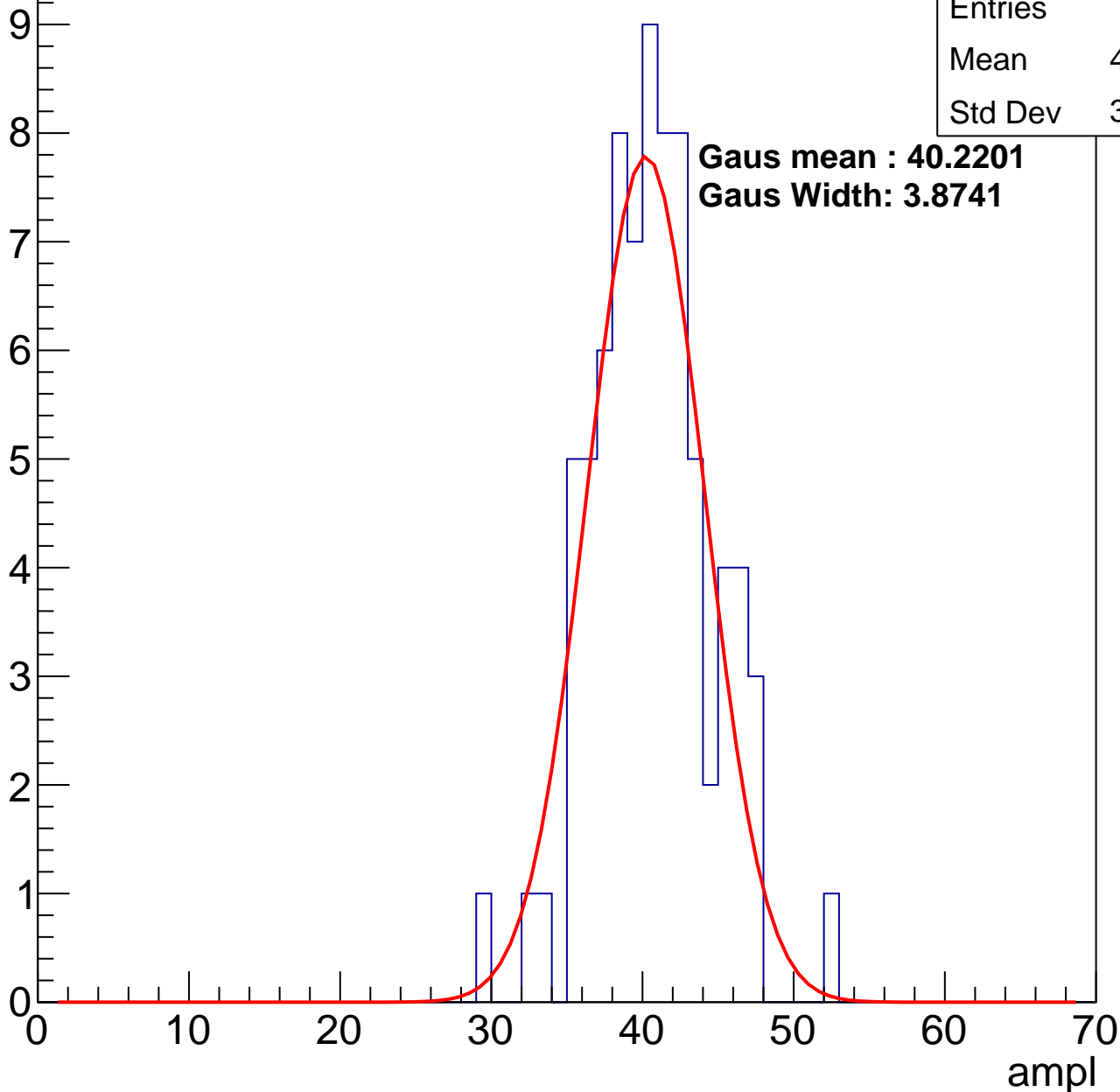
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	40.15
Std Dev	3.893

**Gaus mean : 40.2201**

**Gaus Width: 3.8741**



# B1L000S, U1-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

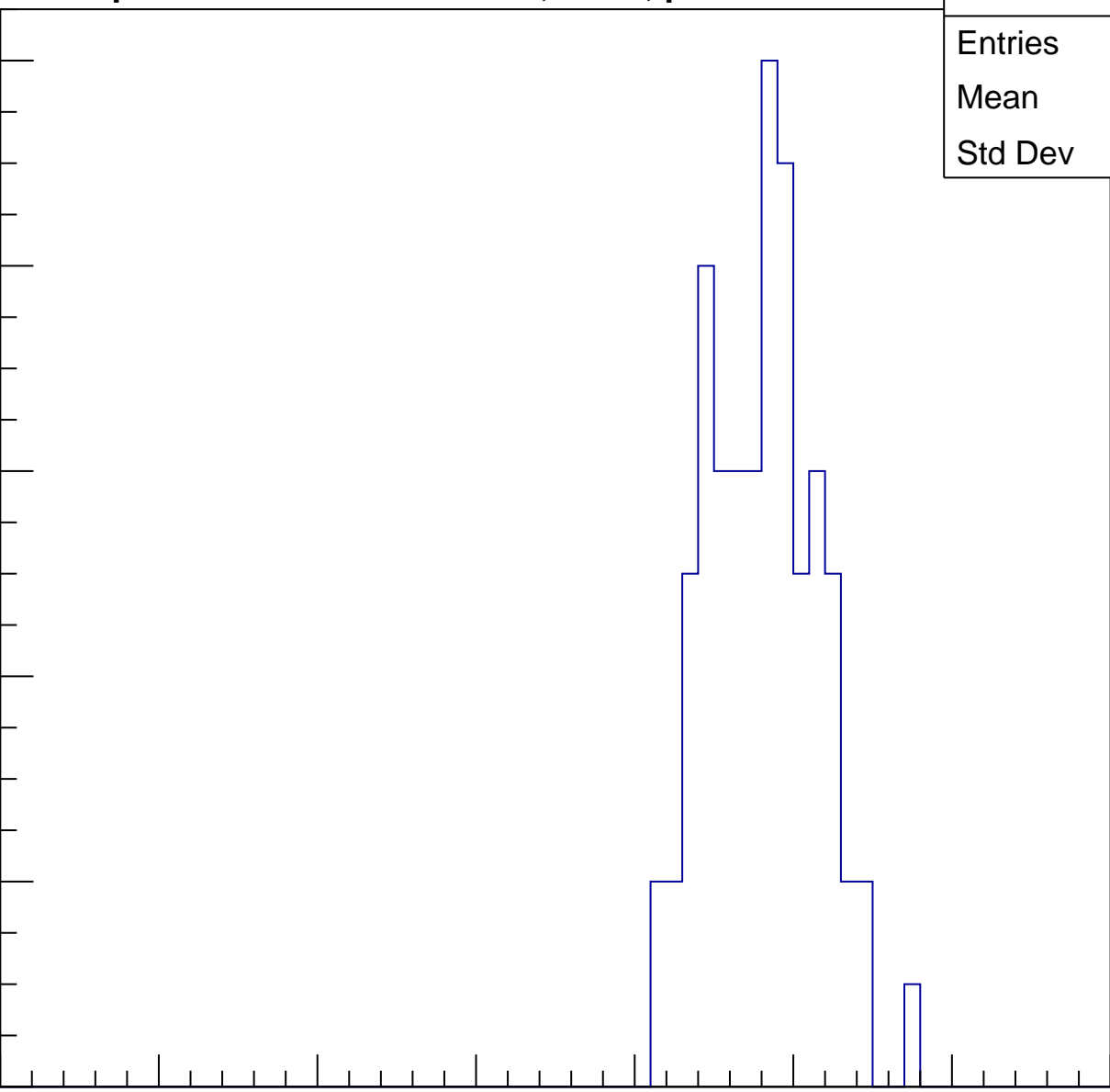
Entries	75
Mean	47.59
Std Dev	3.387

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

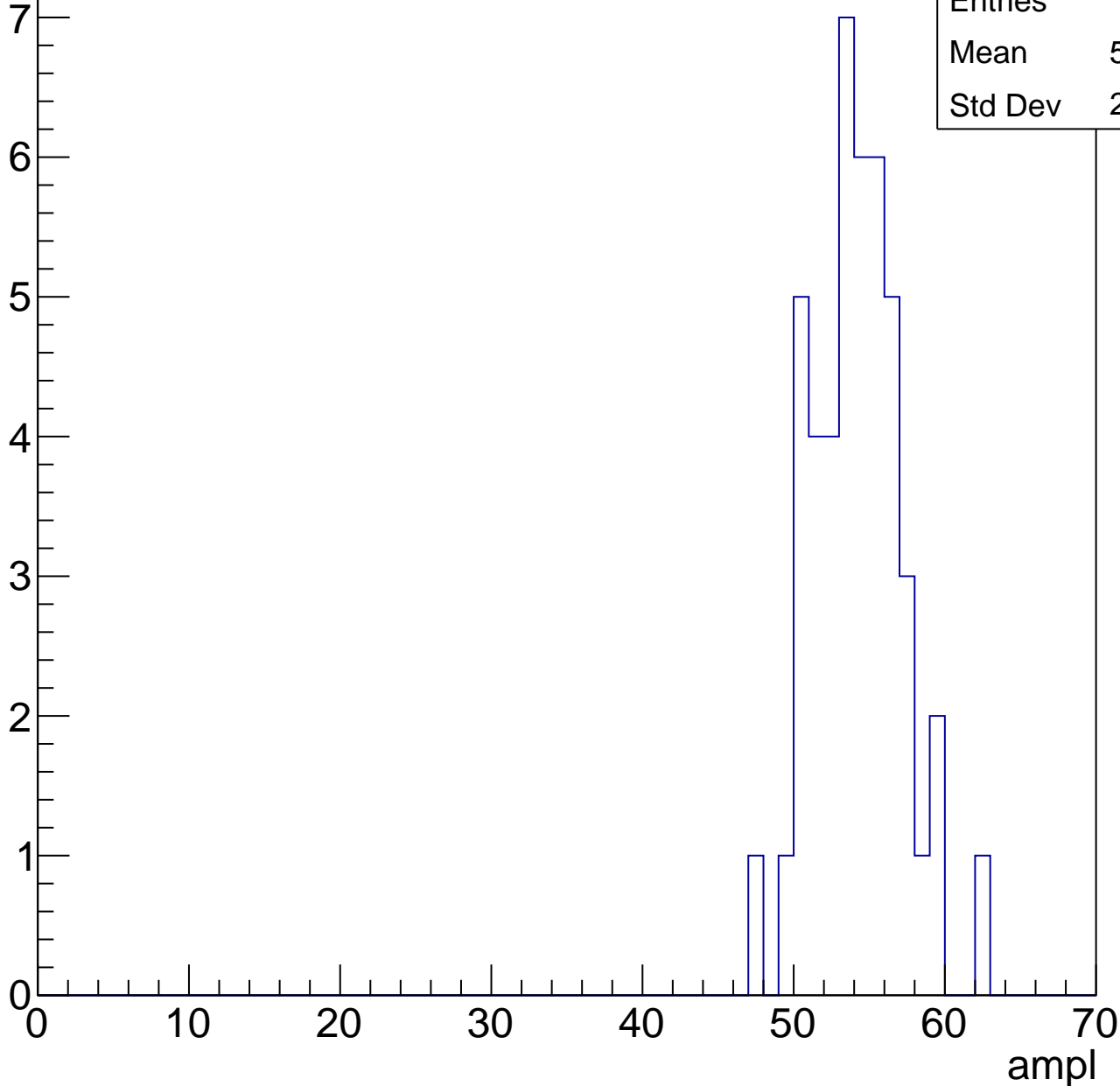


# B1L000S, U1-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	53.74
Std Dev	2.922



# B1L000S, U1-ch127, adc5

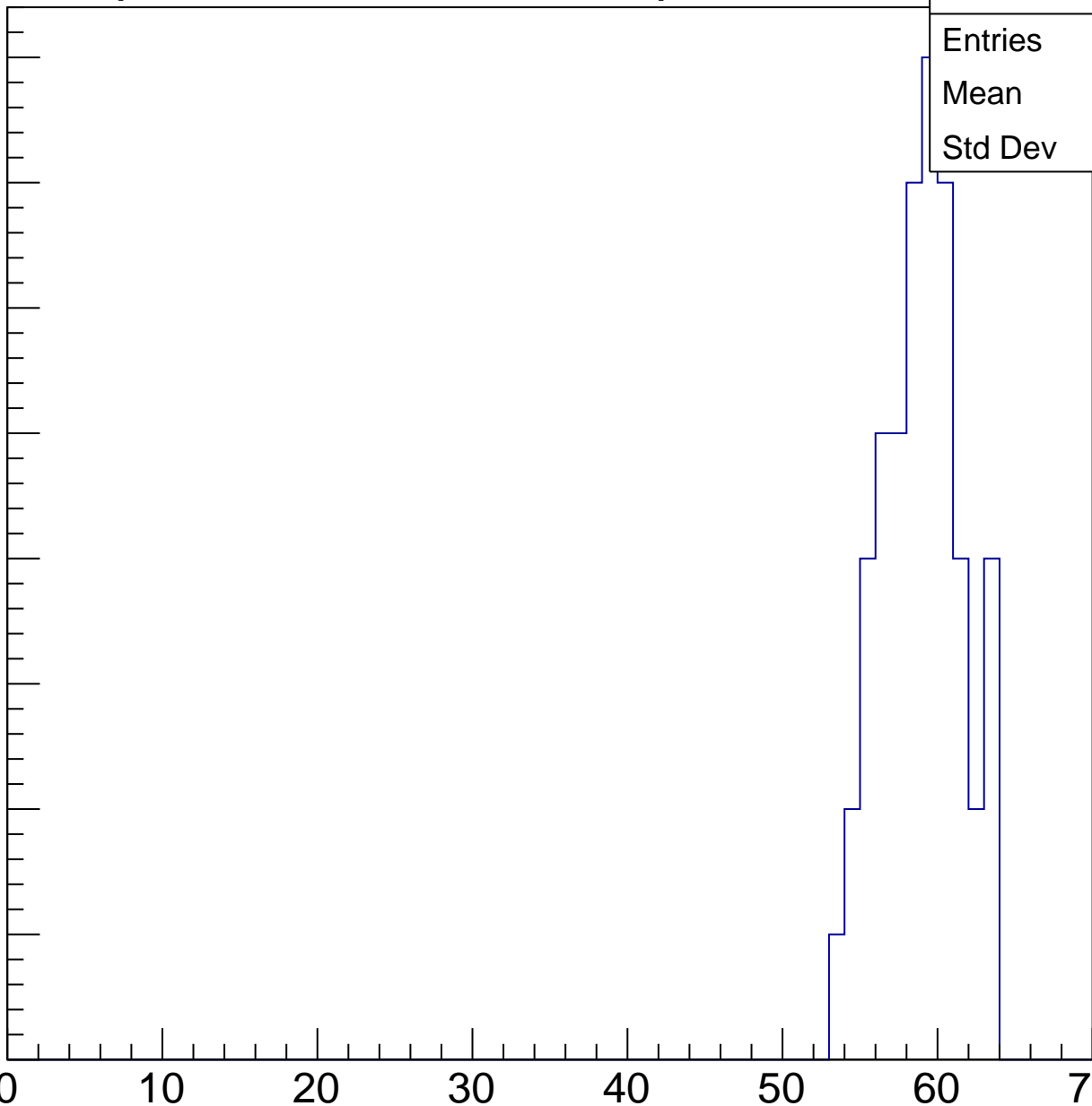
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.45
Std Dev	2.524

ampl

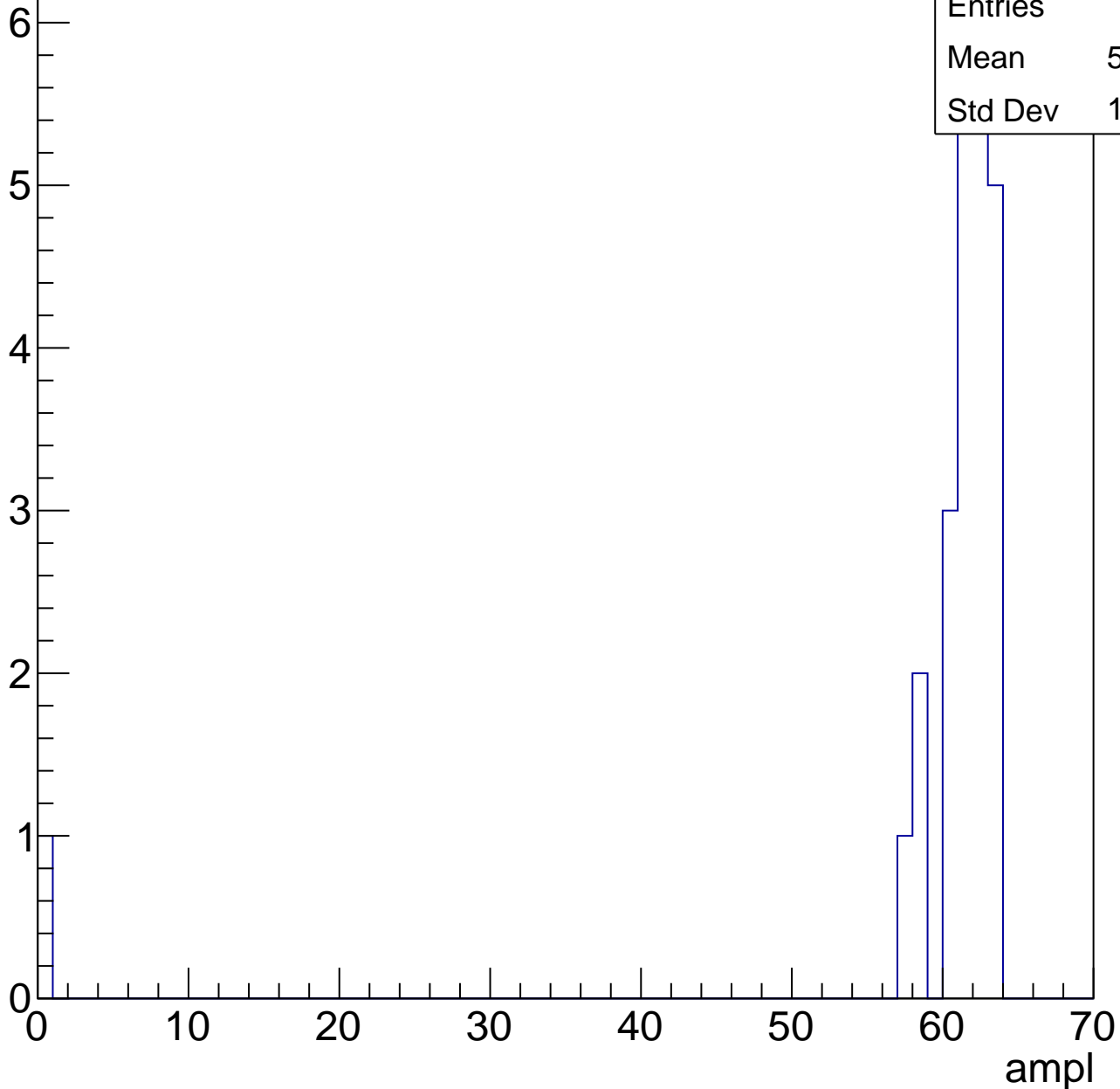


# B1L000S, U1-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.58
Std Dev	12.32

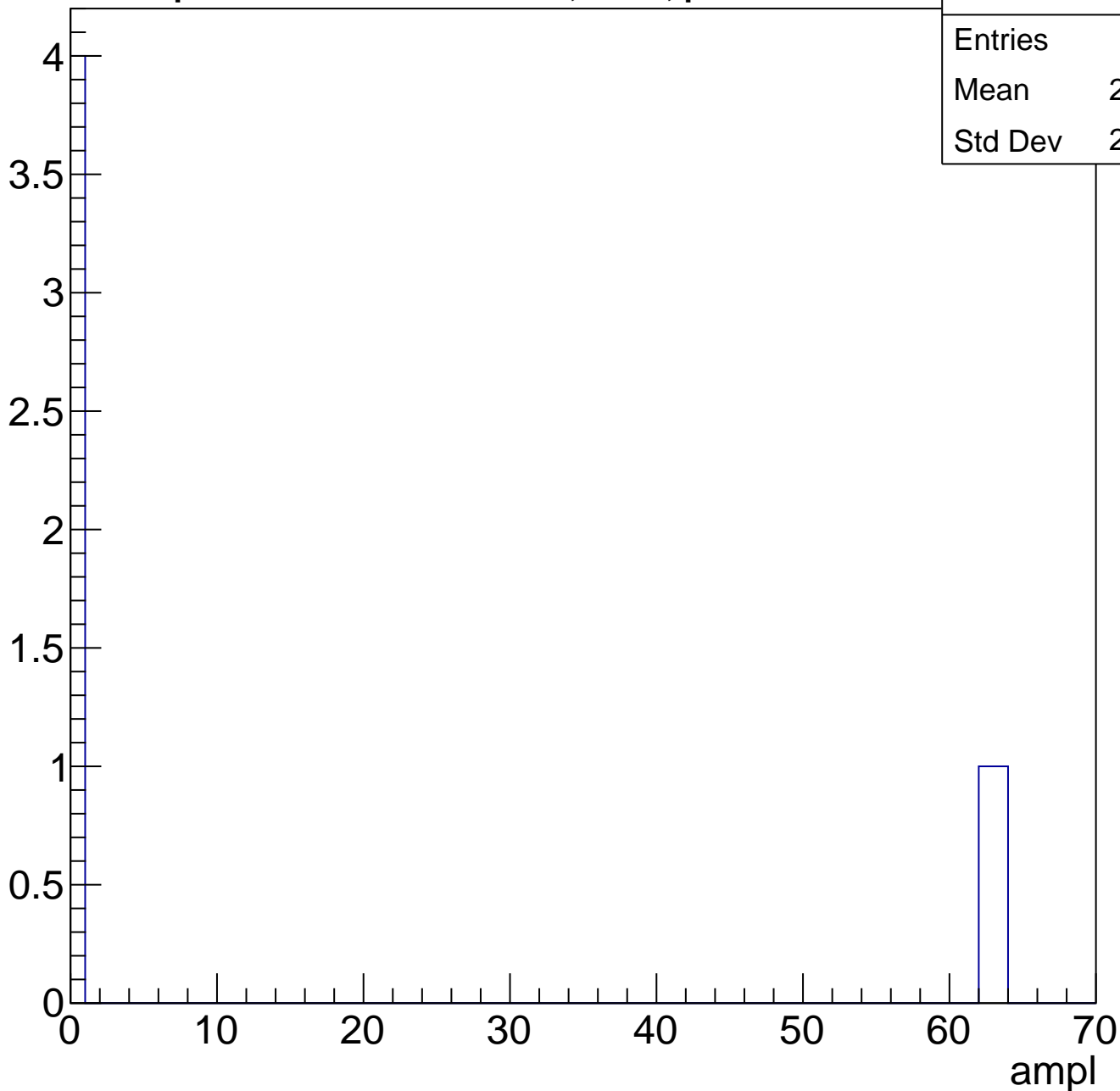




# B1L000S, U1-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U1-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

