

# B0L100S, channel entry distribution (ampl > 54)

calib\_packv5\_042123\_1354.root, FC#6, port A1

Events

1600  
1400  
1200  
1000  
800  
600  
400  
200  
0

Entries	3328
Mean	67.51
Std Dev	22.5

0 50 100 150 200 250

Channel entry

