# B1L003S, channel entry distribution (ampl > 54)

**calib_packv5_042523_0143.root, FC#13, port D2**
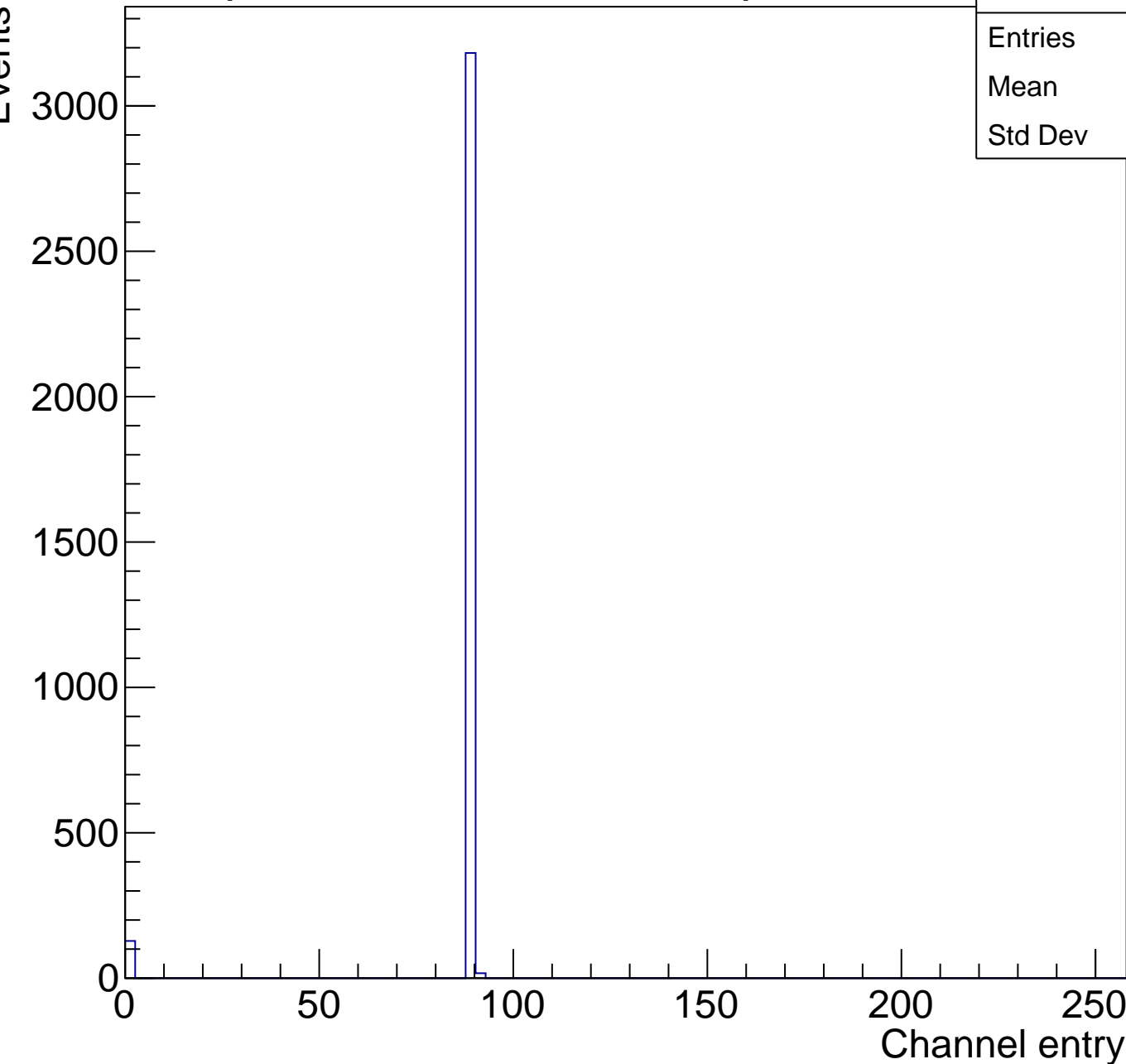


| | |
|---|---|
| Entries | 3328 |
| Mean | 86.54 |
| Std Dev | 17.31 |

Channel entry