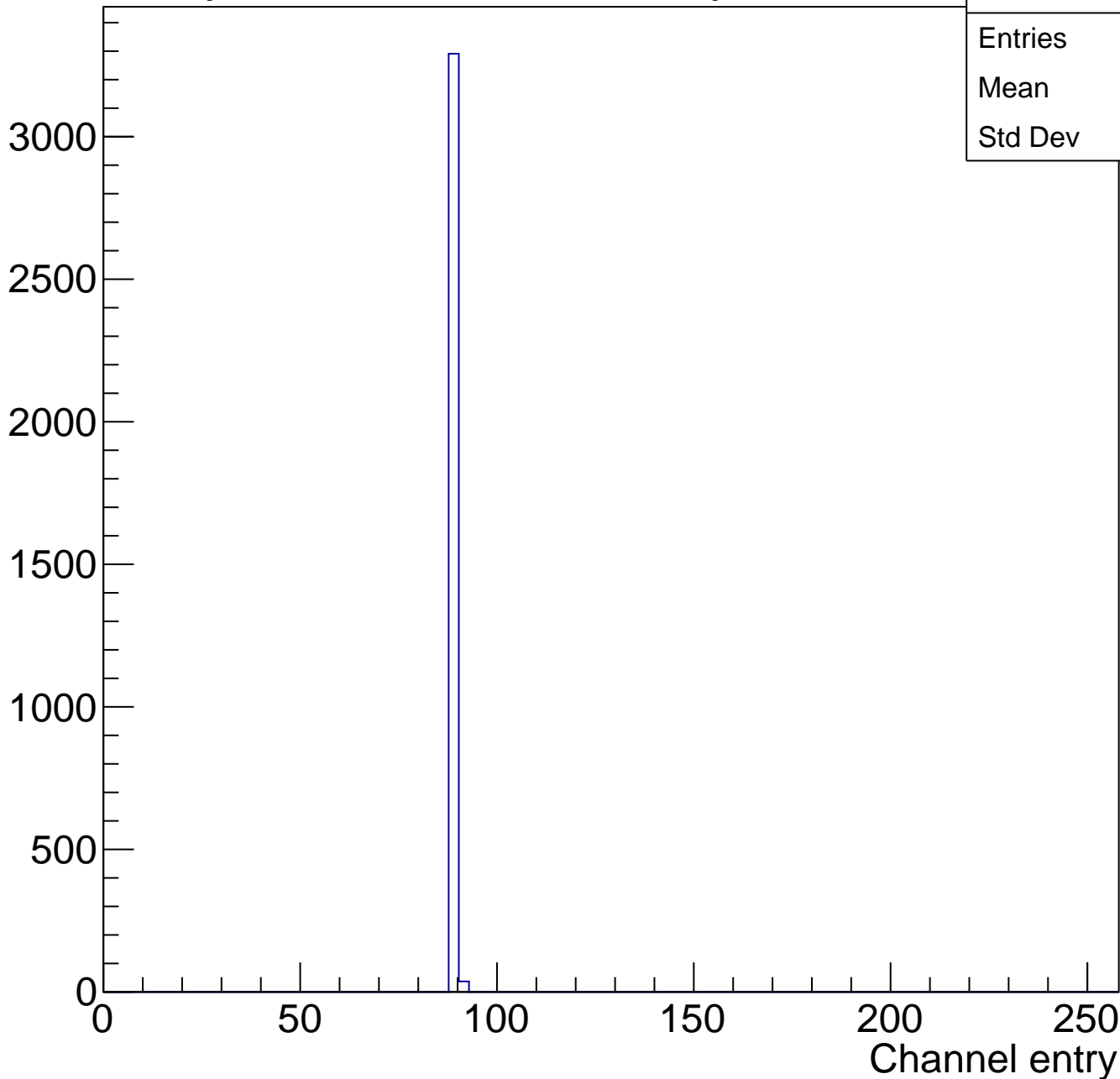


B1L101S, channel entry distribution (ampl > 54)

calib_packv5_042123_1354.root, FC#0, port D2

Events



Entries	3328
Mean	90.01
Std Dev	0.1303