

B1L000S, U26-ch0

calib_packv5_042523_0143.root, FC#3, port B3

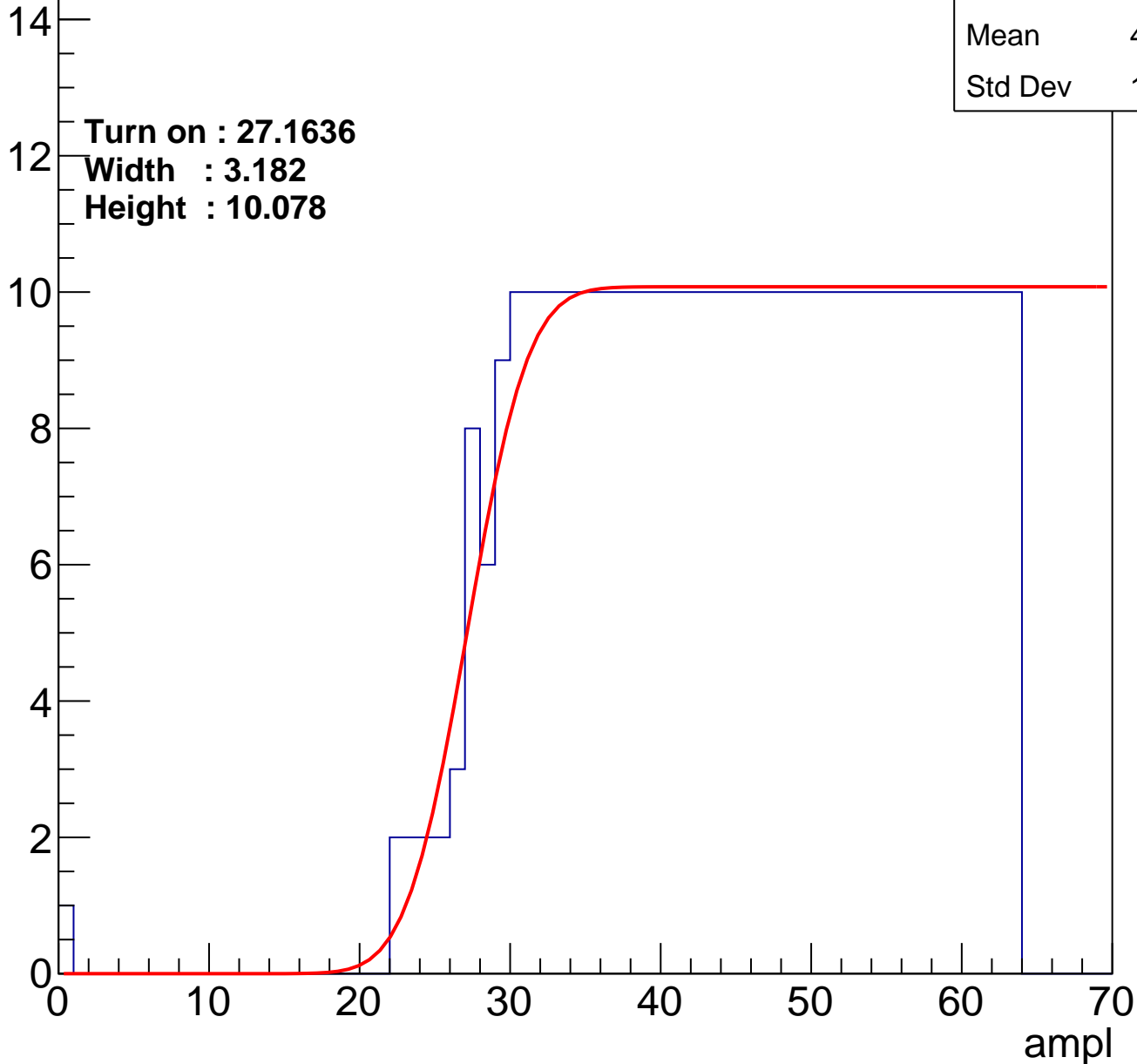
Entry

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.59 |
| Std Dev | 11.18 |

Turn on : 27.1636

Width : 3.182

Height : 10.078



B1L000S, U26-ch1

calib_packv5_042523_0143.root, FC#3, port B3

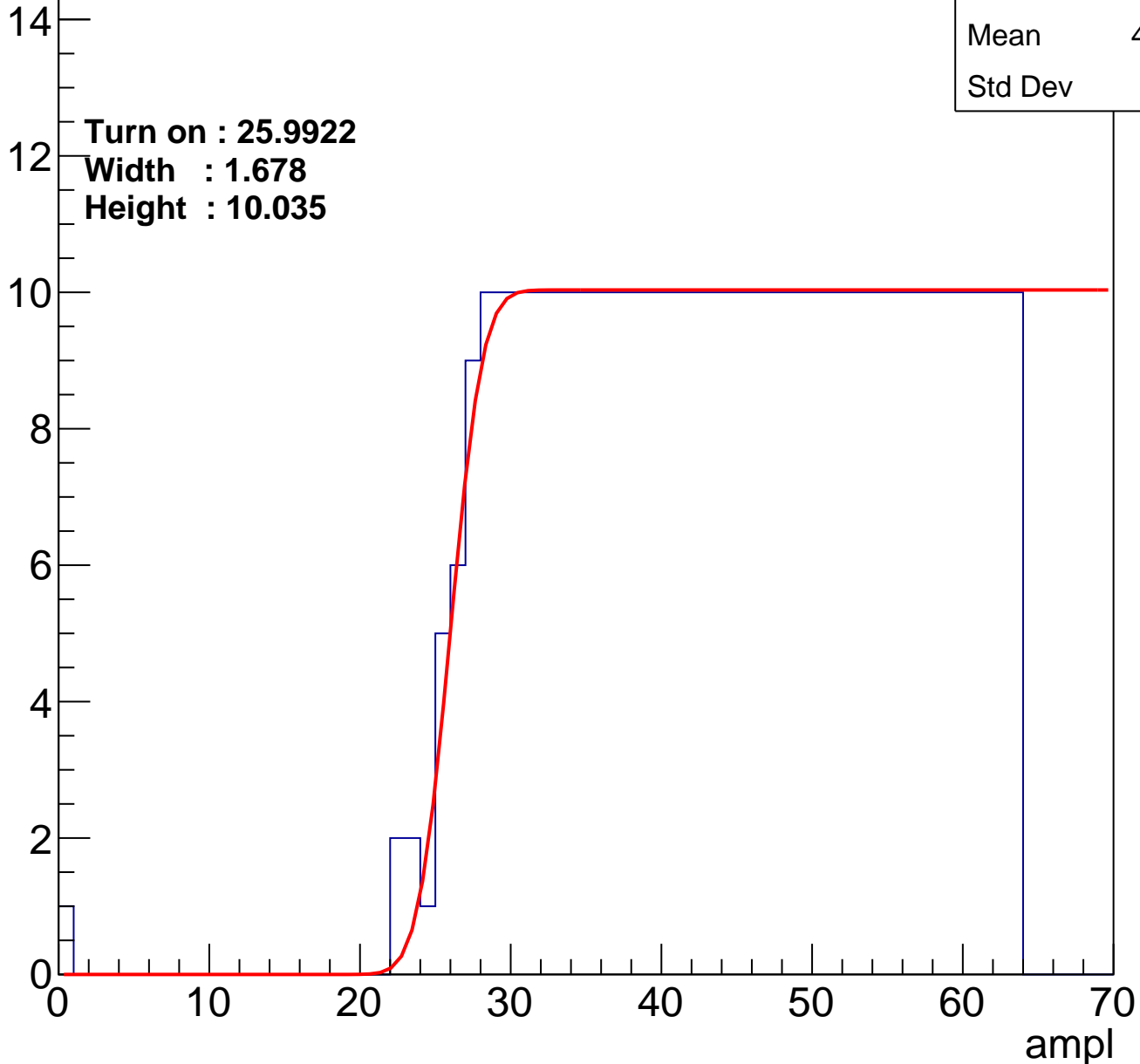
| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.09 |
| Std Dev | 11.4 |

Turn on : 25.9922

Width : 1.678

Height : 10.035

Entry



B1L000S, U26-ch2

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.86 |
| Std Dev | 11.56 |

Turn on : 25.4034

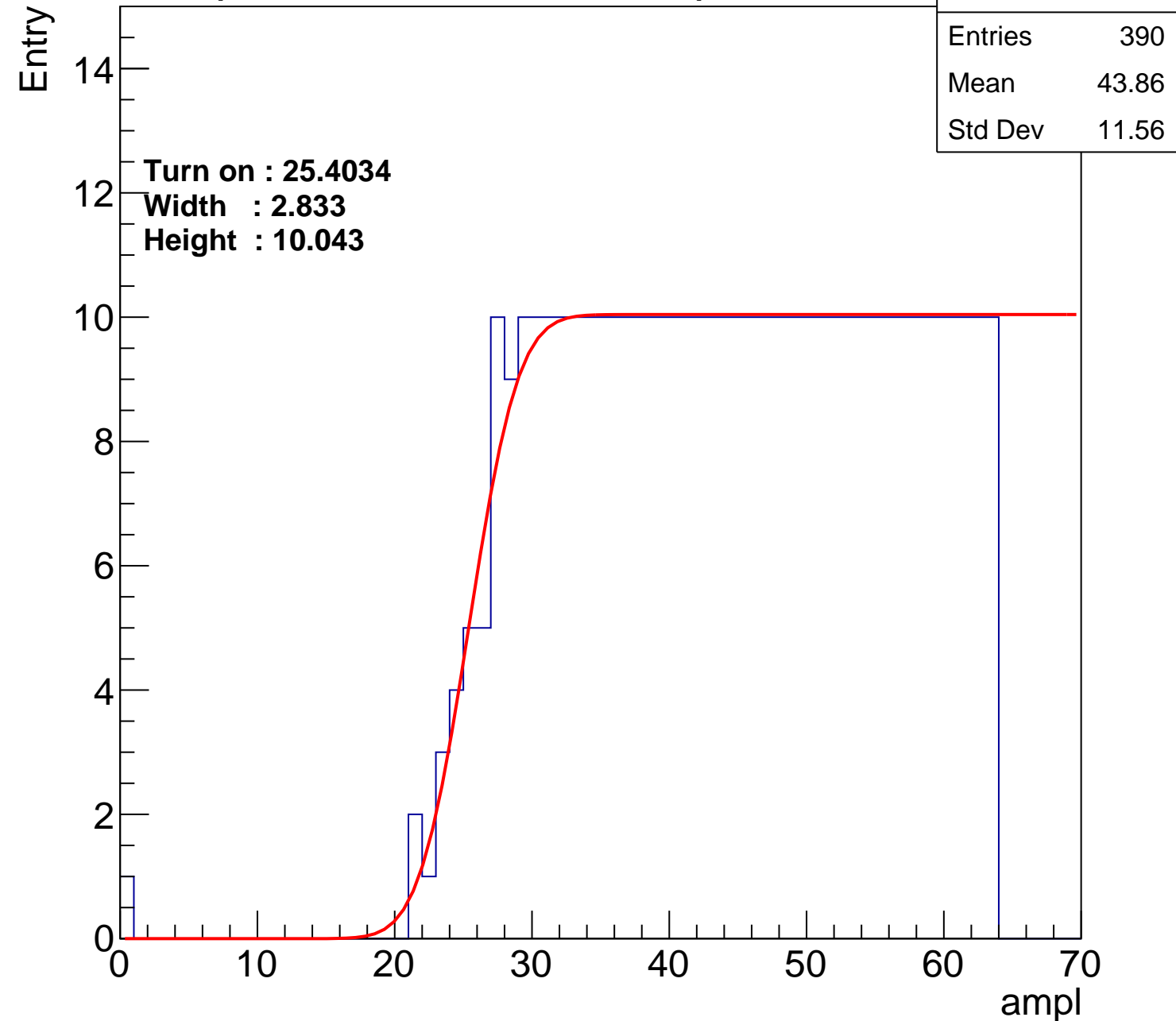
Width : 2.833

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch3

calib_packv5_042523_0143.root, FC#3, port B3

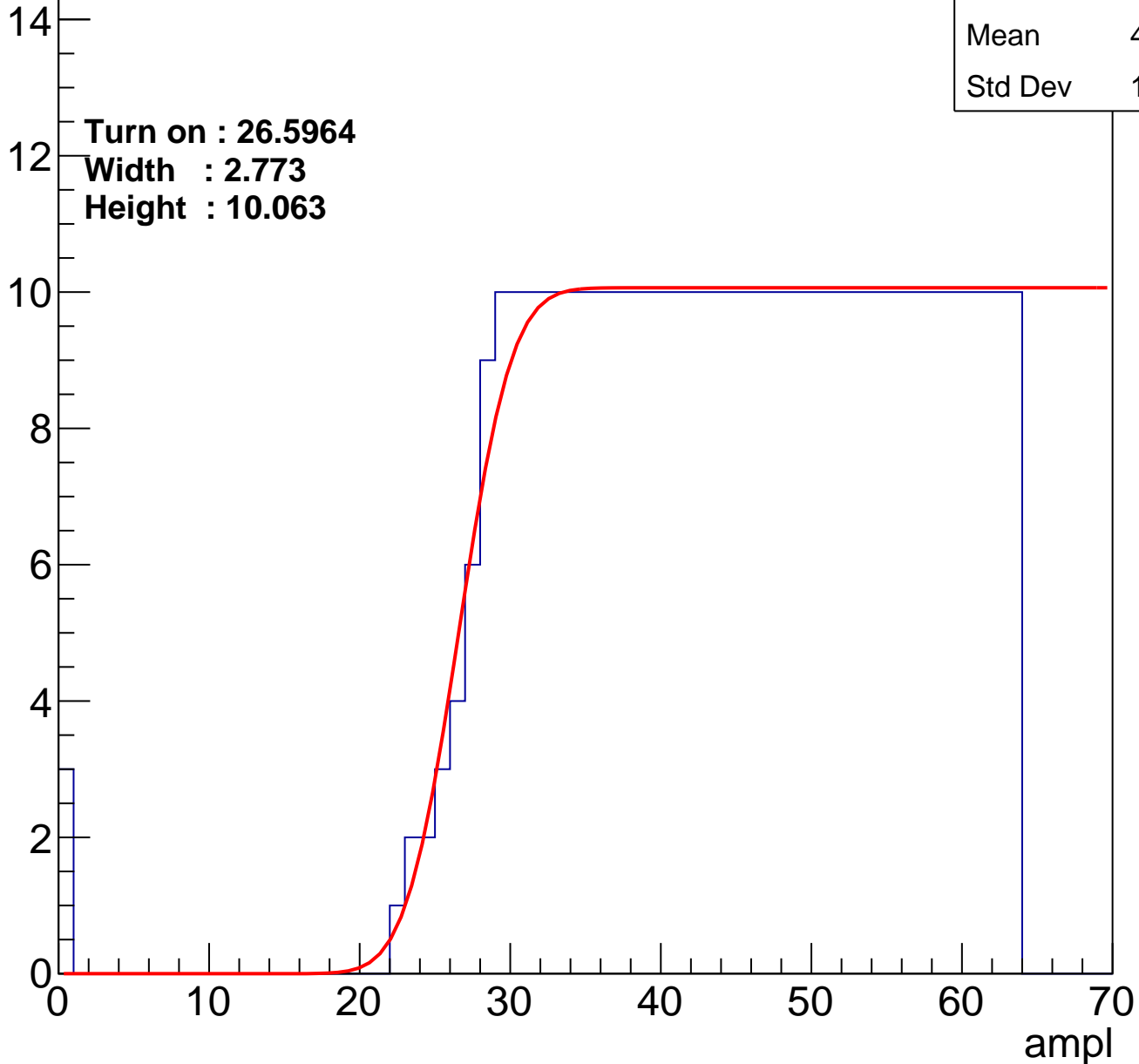
| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.23 |
| Std Dev | 11.64 |

Turn on : 26.5964

Width : 2.773

Height : 10.063

Entry



B1L000S, U26-ch4

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.39 |
| Std Dev | 11.42 |

Turn on : 25.9072

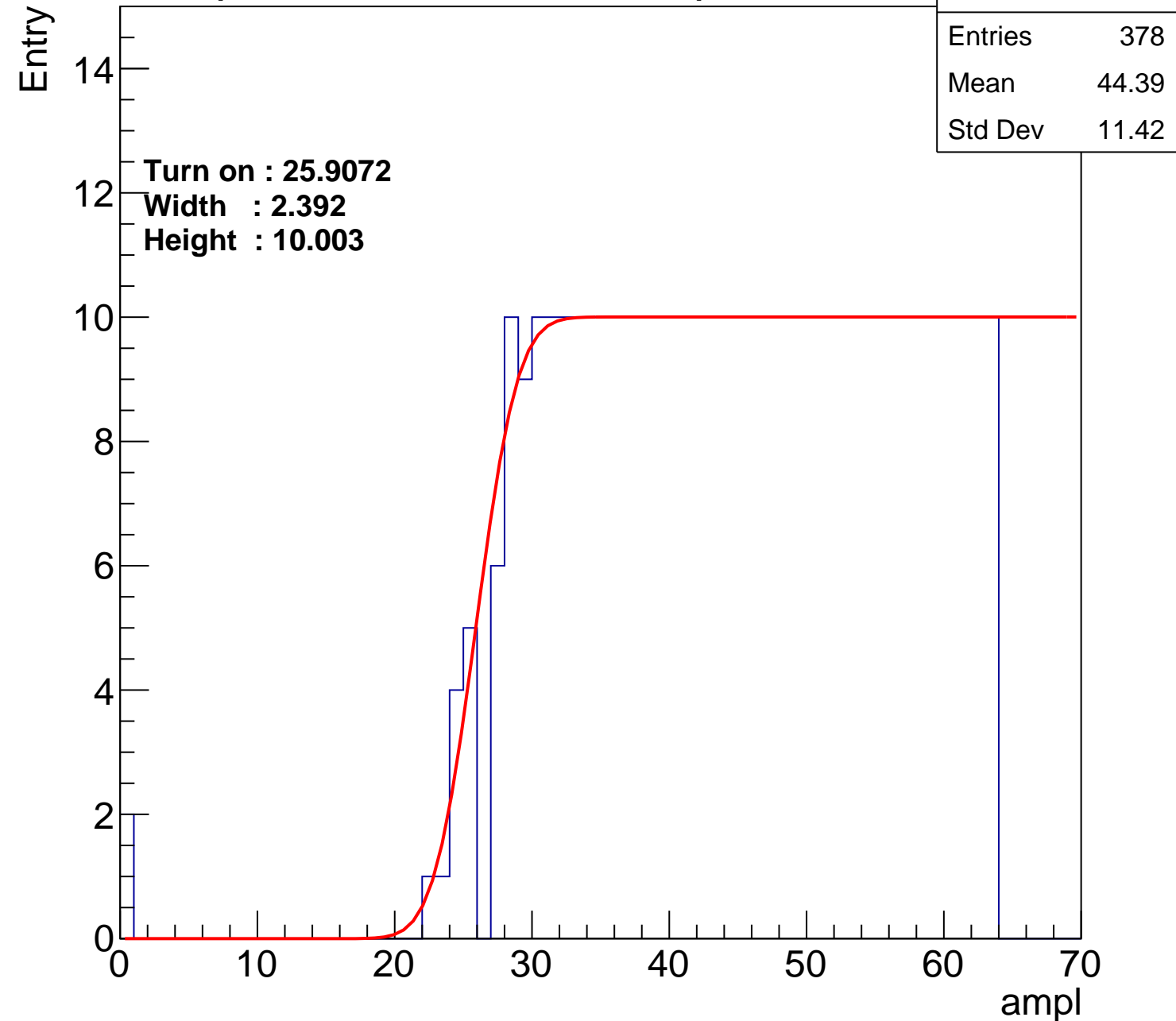
Width : 2.392

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch5

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 44 |
| Std Dev | 11.61 |

Turn on : 25.4470

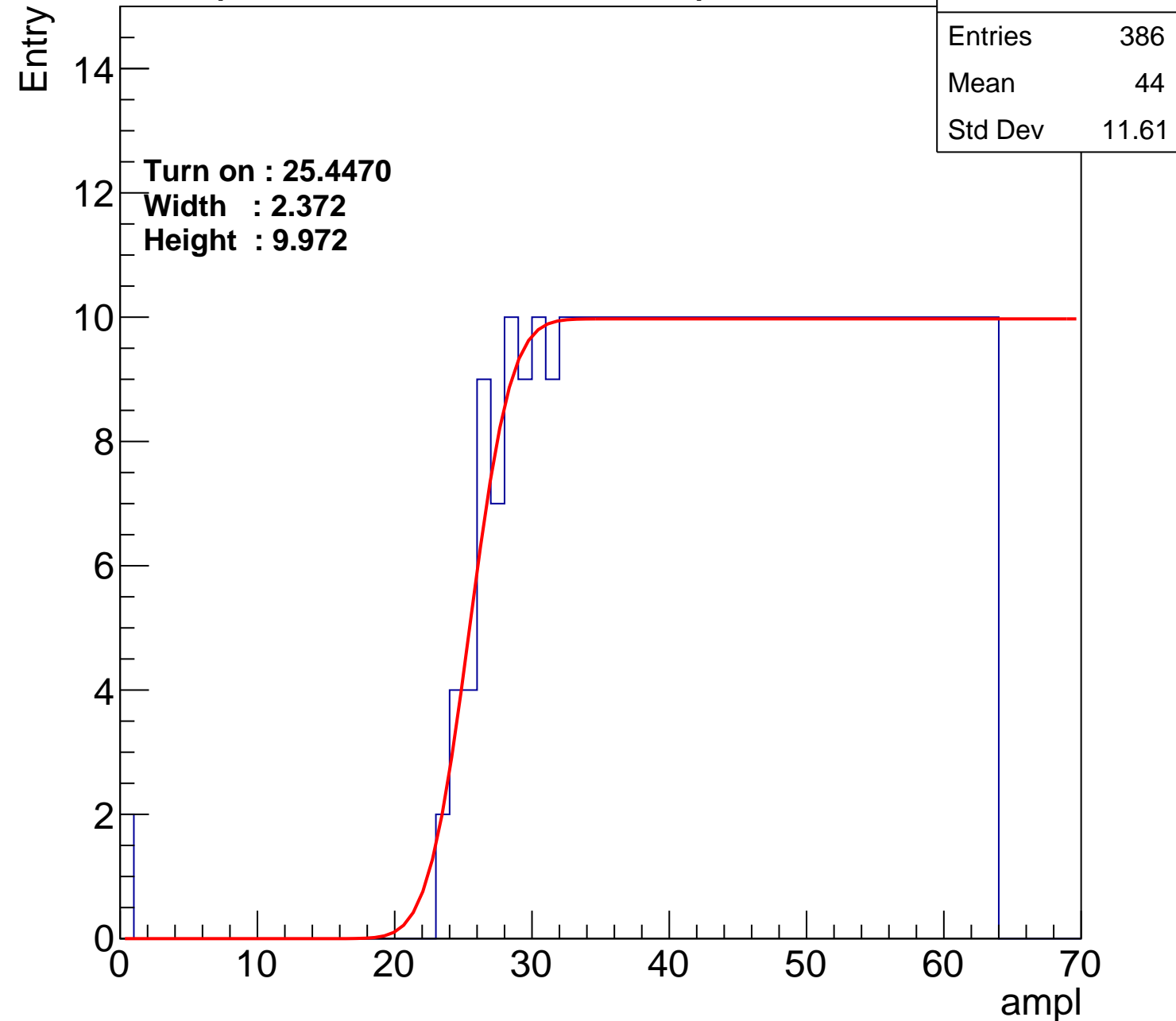
Width : 2.372

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch6

calib_packv5_042523_0143.root, FC#3, port B3

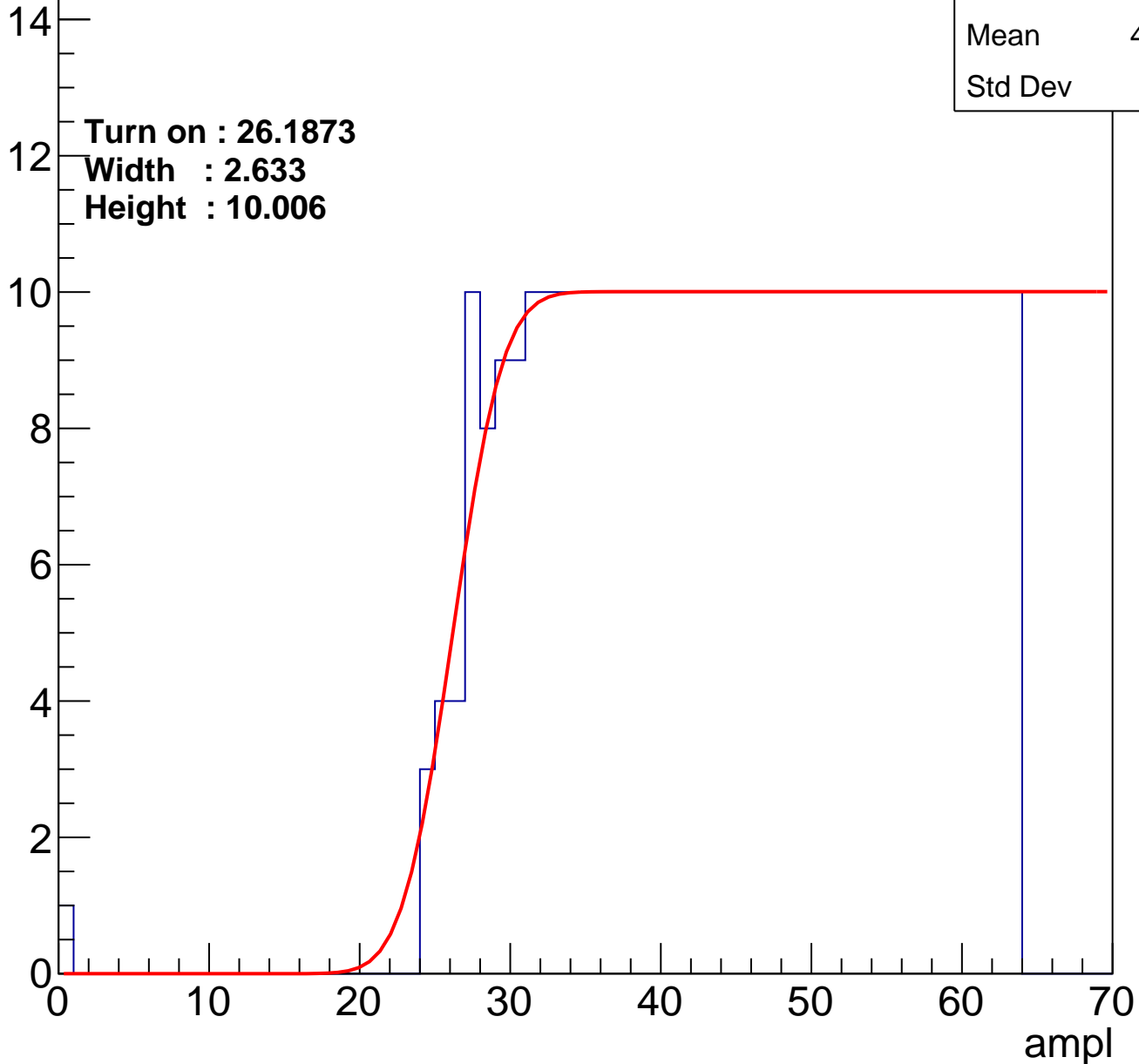
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.47 |
| Std Dev | 11.2 |

Turn on : 26.1873

Width : 2.633

Height : 10.006

Entry



B1L000S, U26-ch7

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.79 |
| Std Dev | 11.57 |

Turn on : 25.0945

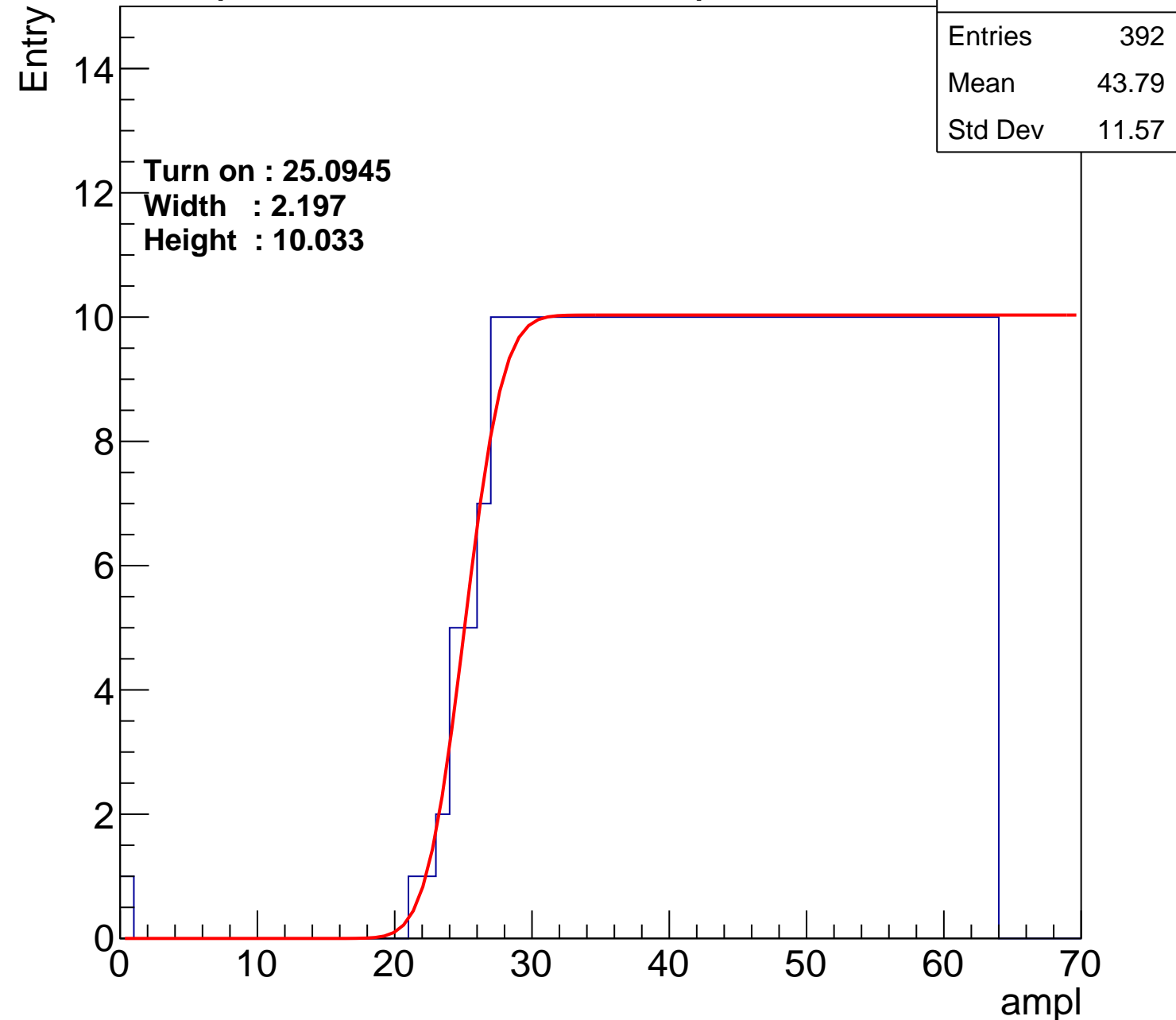
Width : 2.197

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch8

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.06 |
| Std Dev | 11.62 |

Turn on : 26.2484

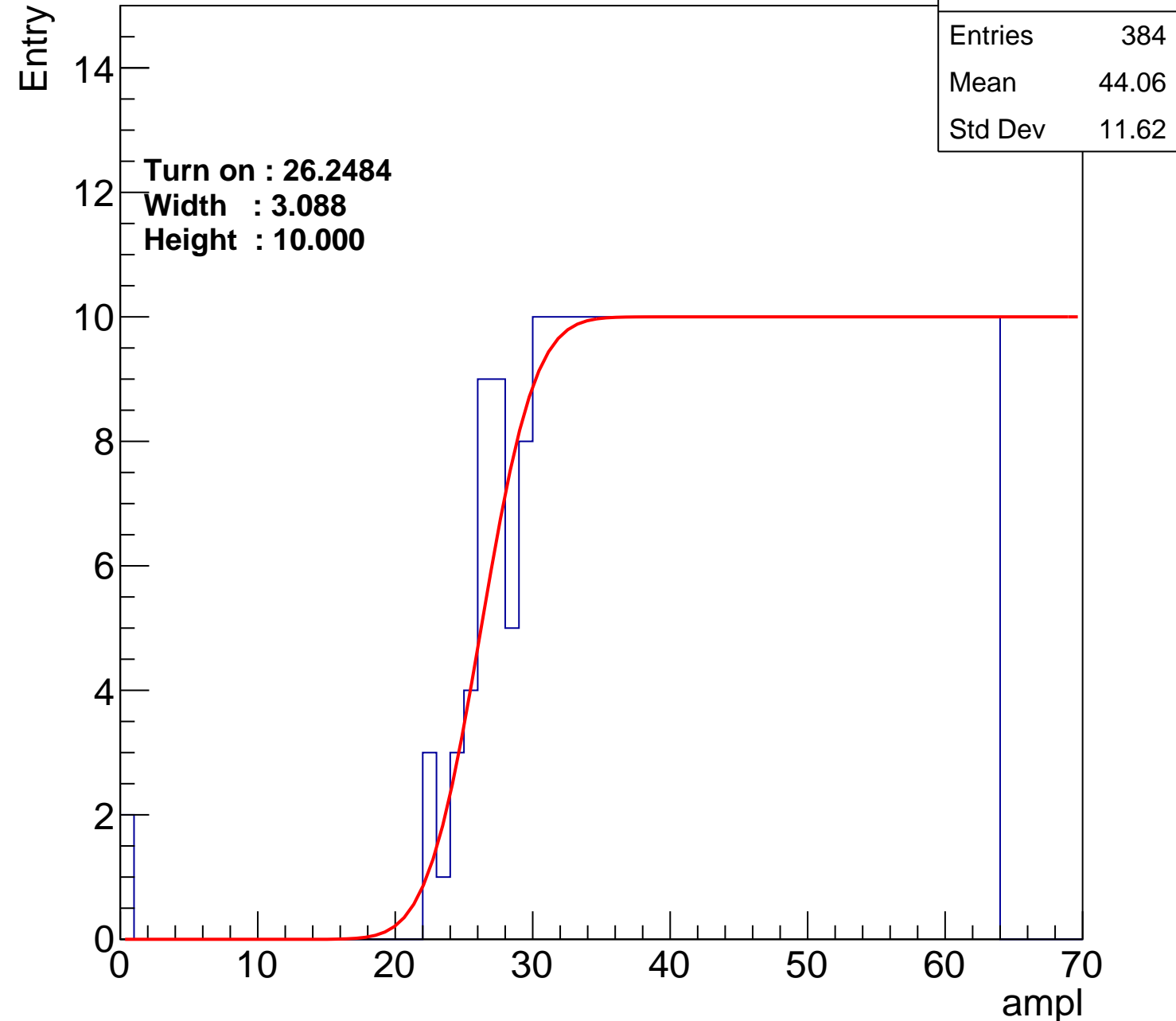
Width : 3.088

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch9

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.44 |
| Std Dev | 11.53 |

Turn on : 26.6828

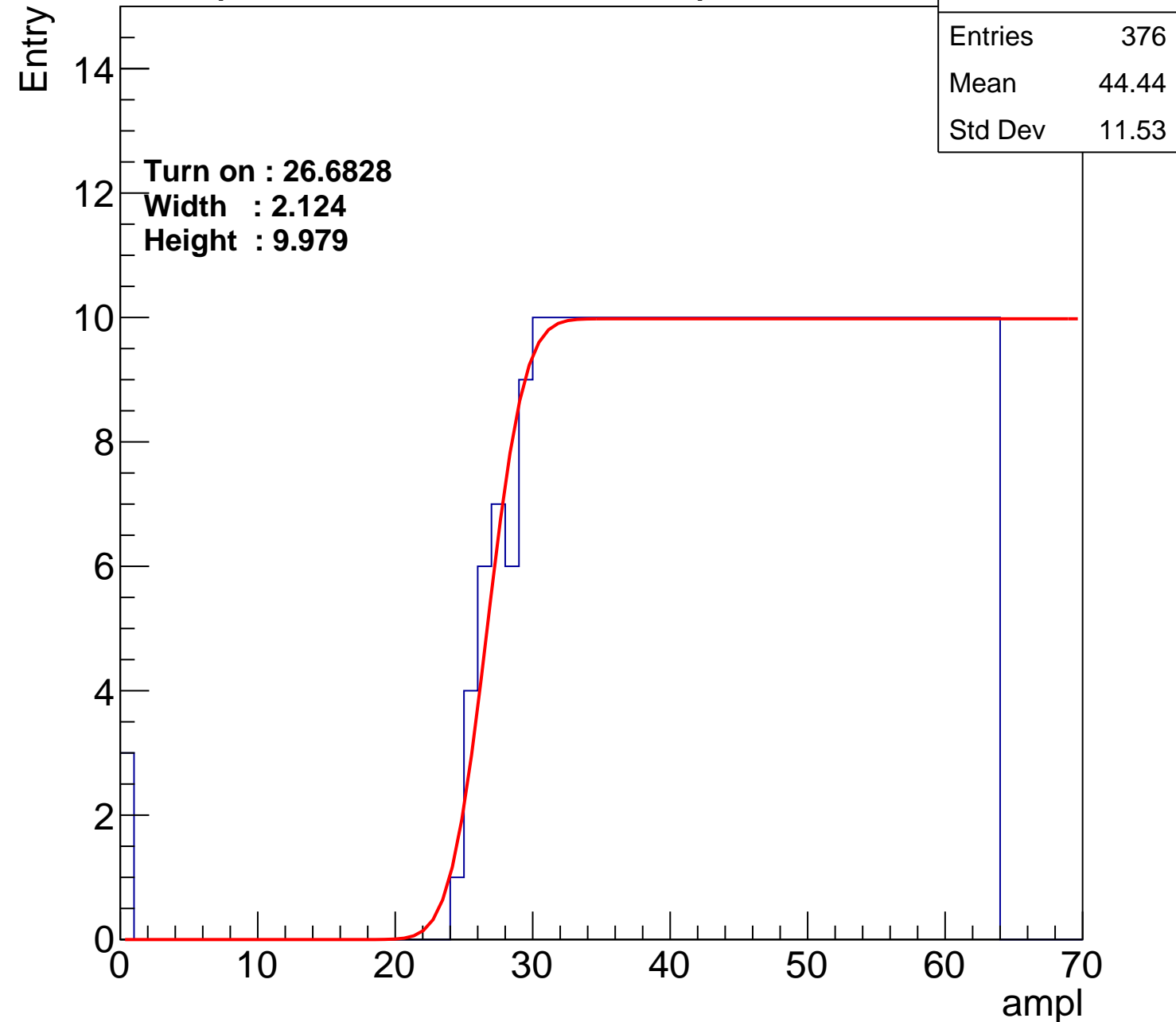
Width : 2.124

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch10

calib_packv5_042523_0143.root, FC#3, port B3

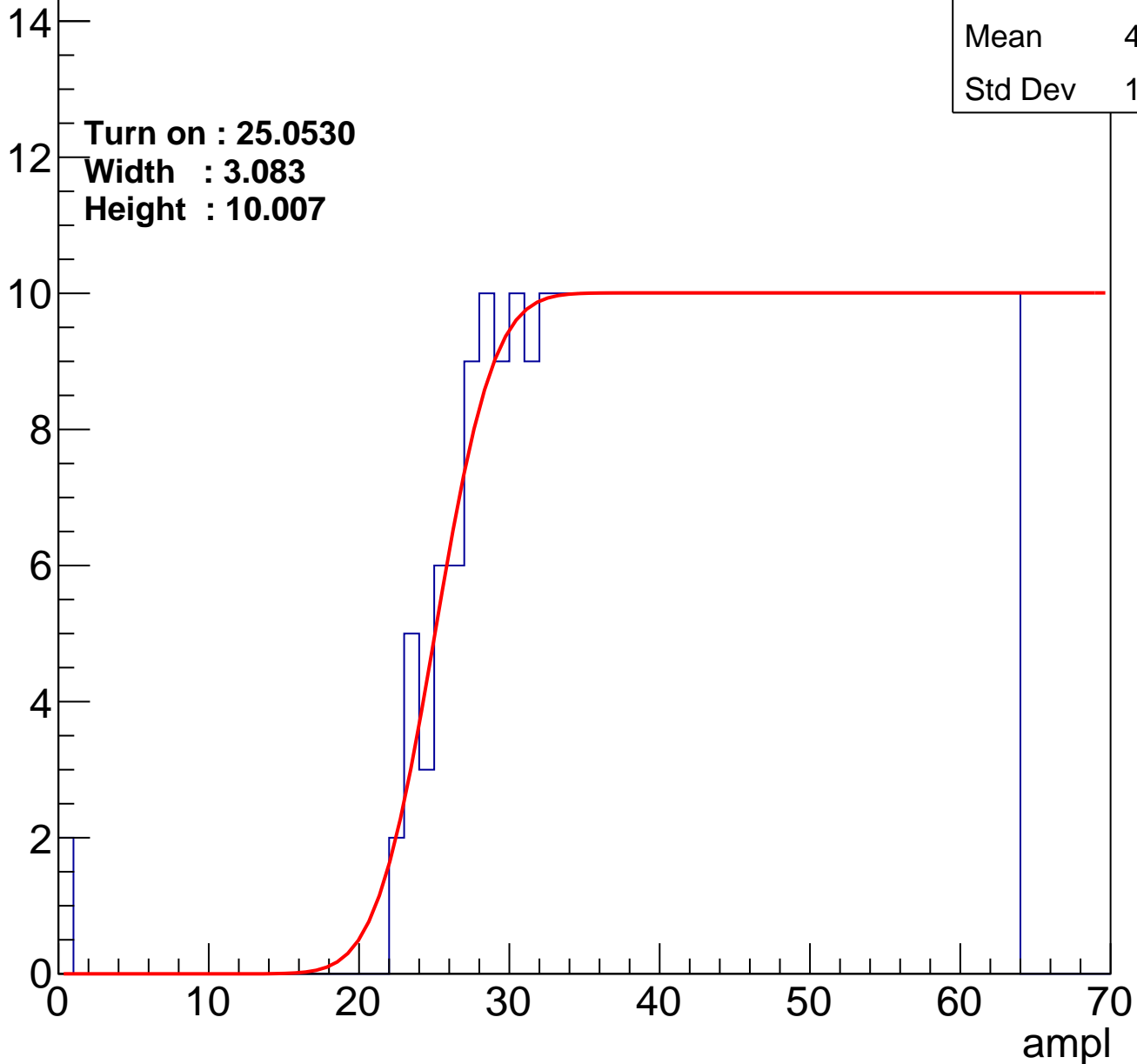
| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.73 |
| Std Dev | 11.77 |

Turn on : 25.0530

Width : 3.083

Height : 10.007

Entry



B1L000S, U26-ch11

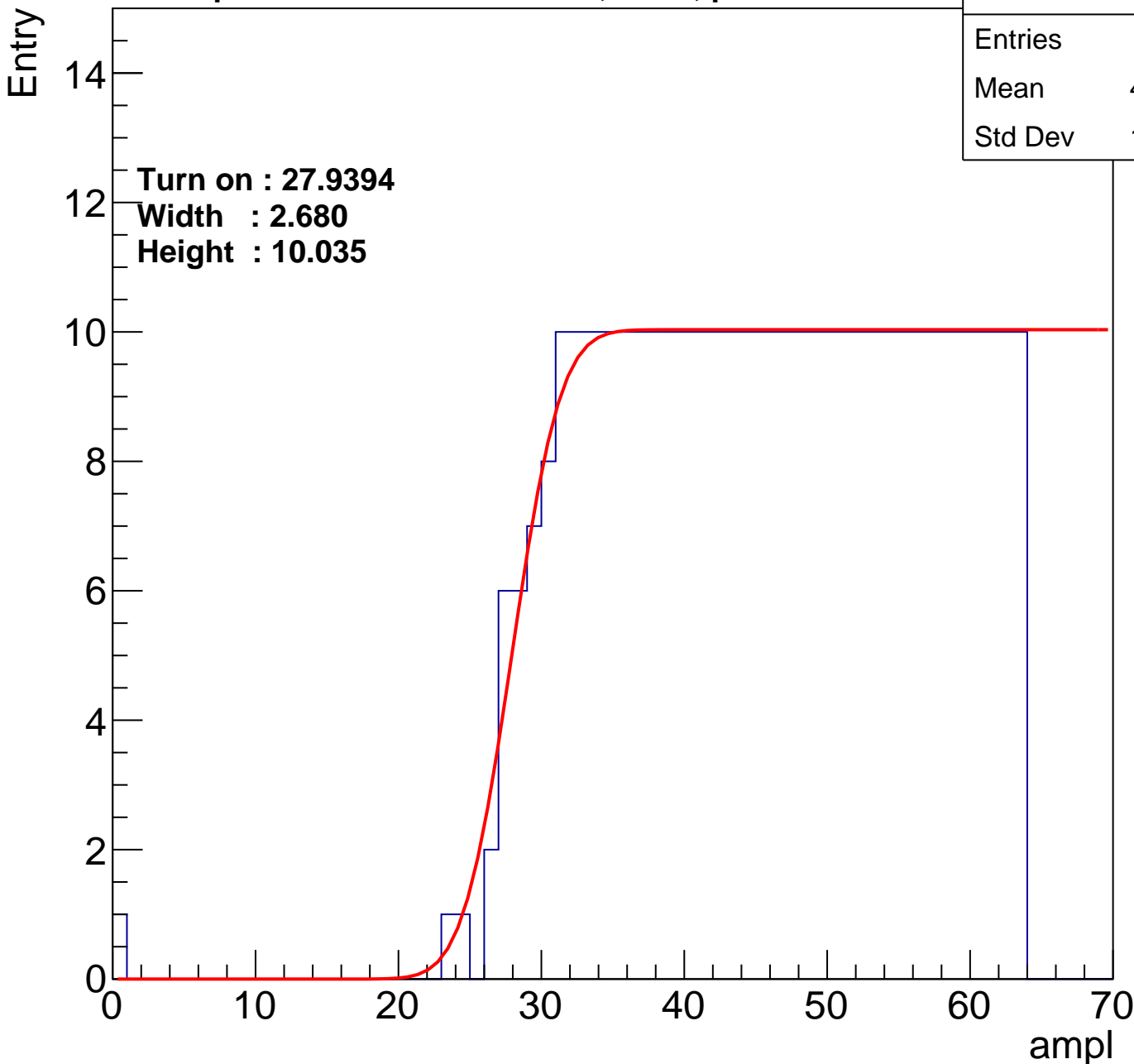
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.25 |
| Std Dev | 10.79 |

Turn on : 27.9394

Width : 2.680

Height : 10.035



B1L000S, U26-ch12

calib_packv5_042523_0143.root, FC#3, port B3

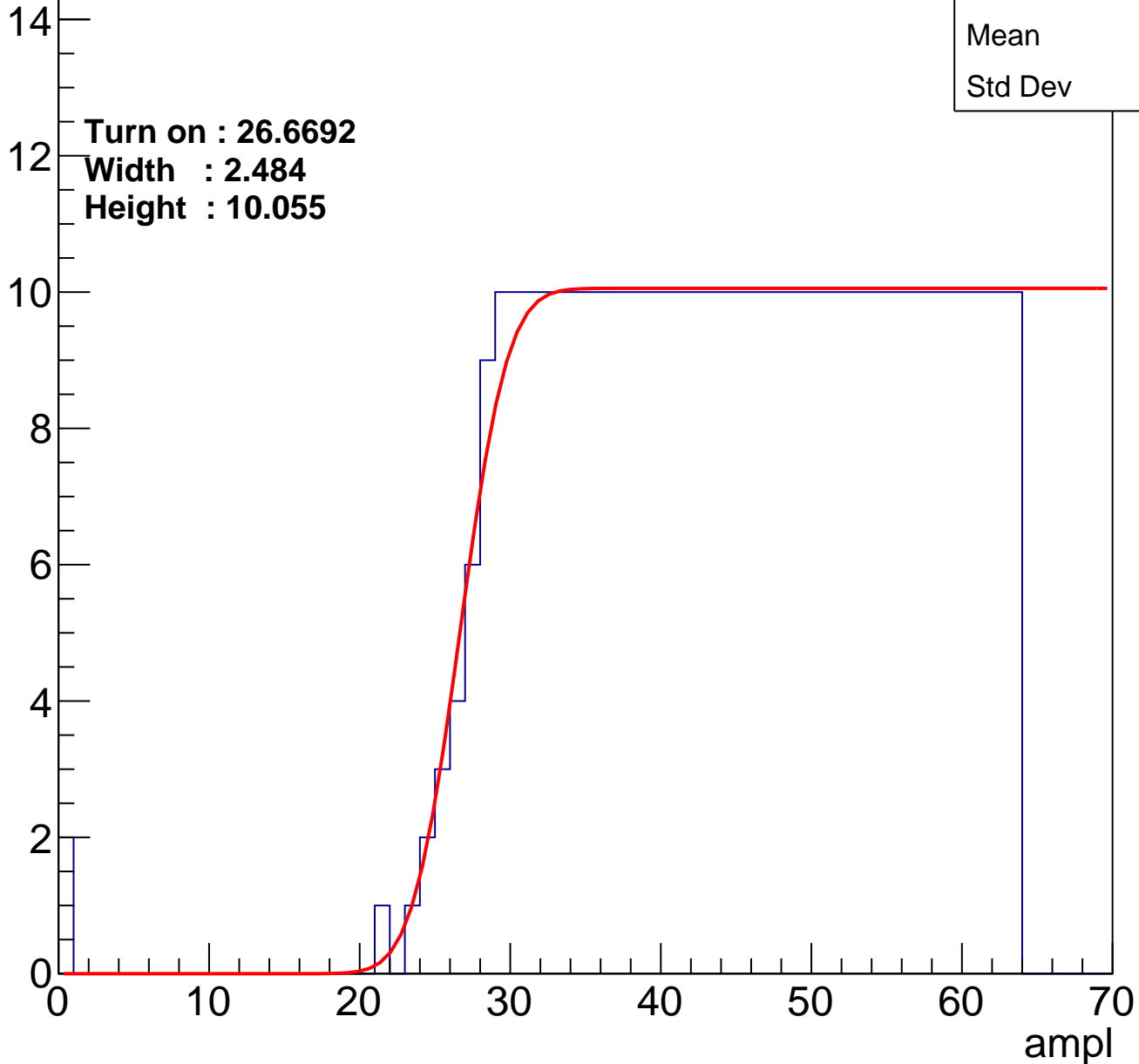
| | |
|---------|------|
| Entries | 378 |
| Mean | 44.4 |
| Std Dev | 11.4 |

Turn on : 26.6692

Width : 2.484

Height : 10.055

Entry



B1L000S, U26-ch13

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.38 |
| Std Dev | 11.44 |

Turn on : 26.8946

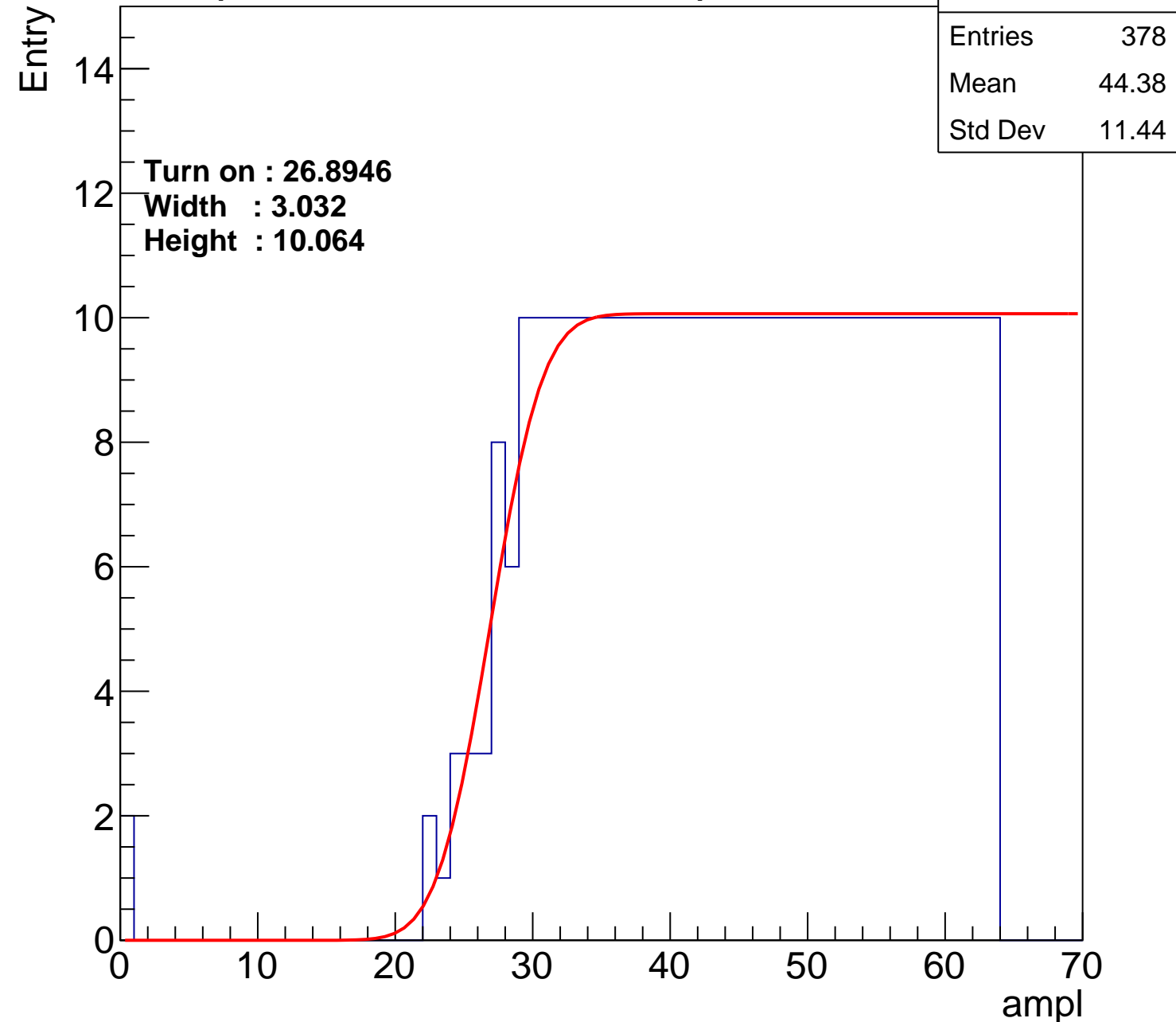
Width : 3.032

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch14

calib_packv5_042523_0143.root, FC#3, port B3

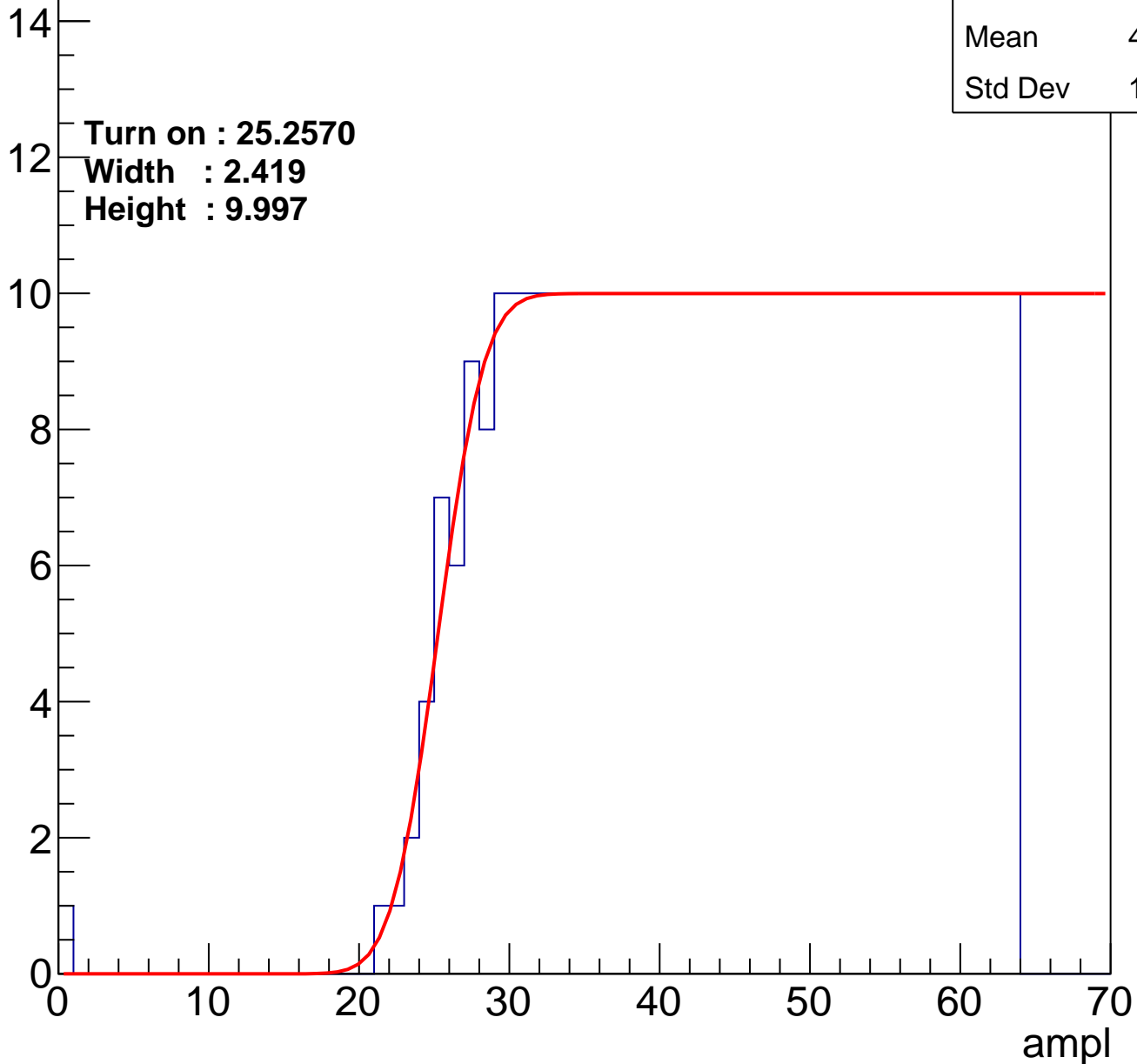
| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.92 |
| Std Dev | 11.52 |

Turn on : 25.2570

Width : 2.419

Height : 9.997

Entry



B1L000S, U26-ch15

calib_packv5_042523_0143.root, FC#3, port B3

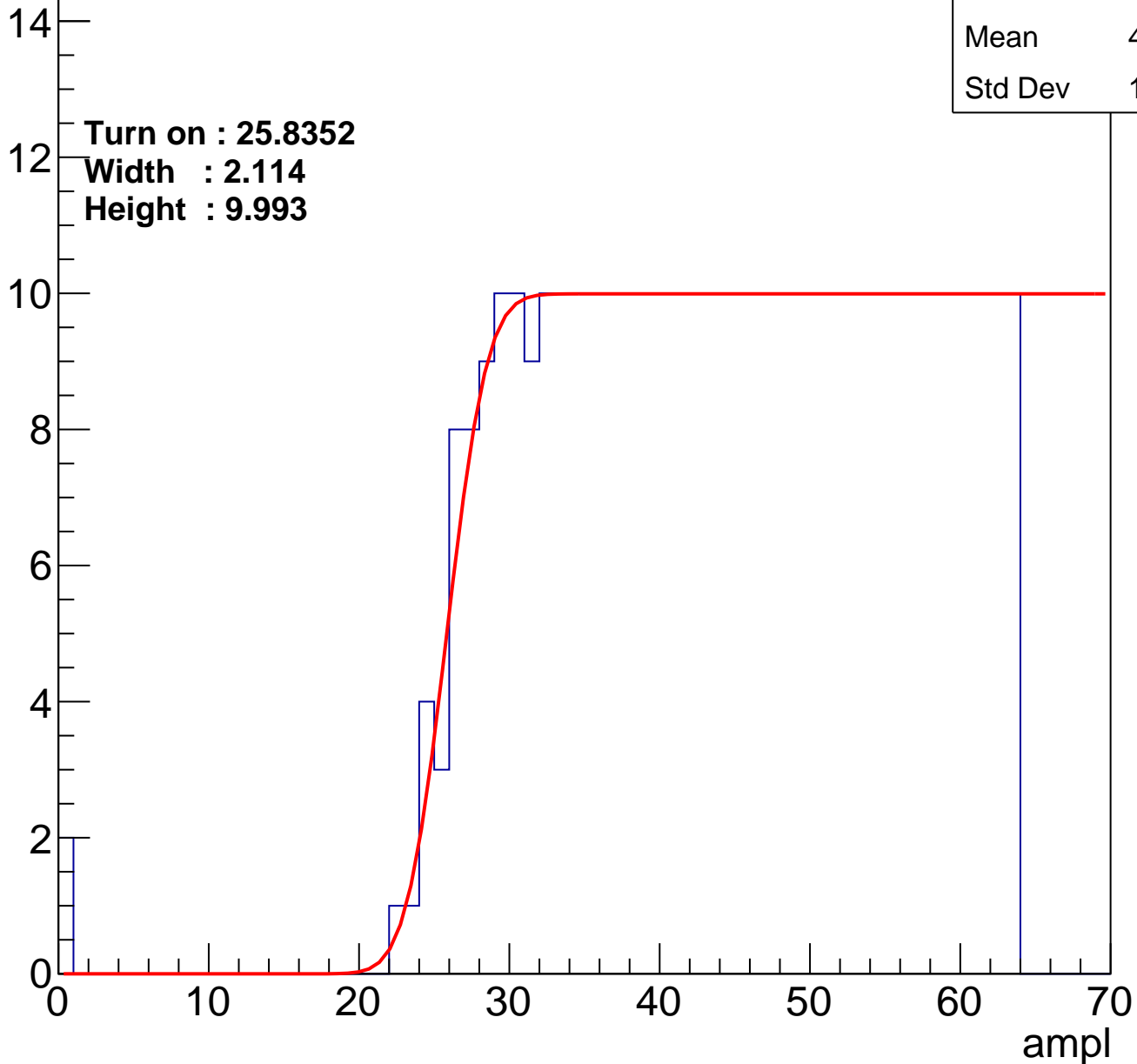
| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.05 |
| Std Dev | 11.58 |

Turn on : 25.8352

Width : 2.114

Height : 9.993

Entry



B1L000S, U26-ch16

calib_packv5_042523_0143.root, FC#3, port B3

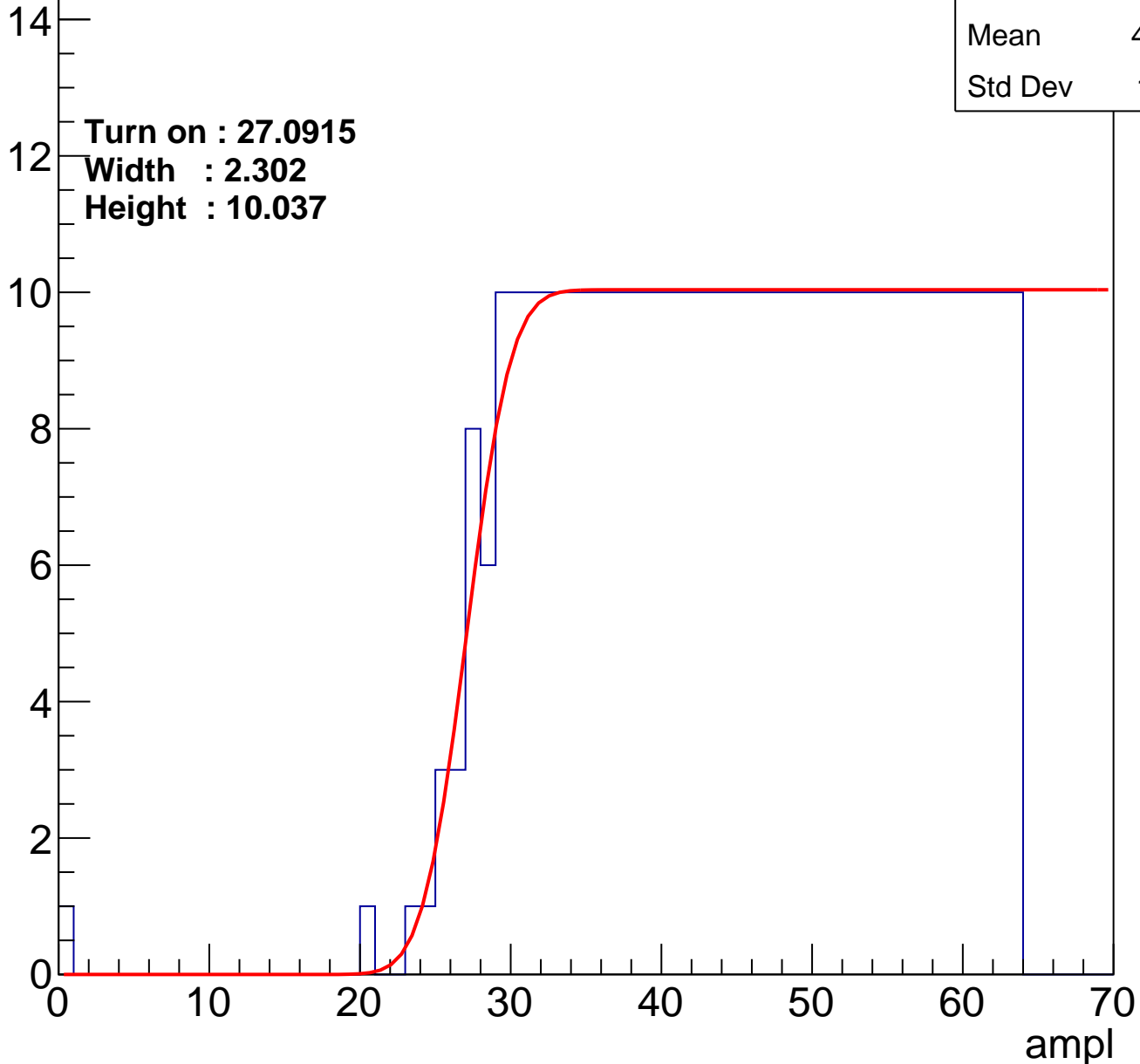
| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.66 |
| Std Dev | 11.11 |

Turn on : 27.0915

Width : 2.302

Height : 10.037

Entry



B1L000S, U26-ch17

calib_packv5_042523_0143.root, FC#3, port B3

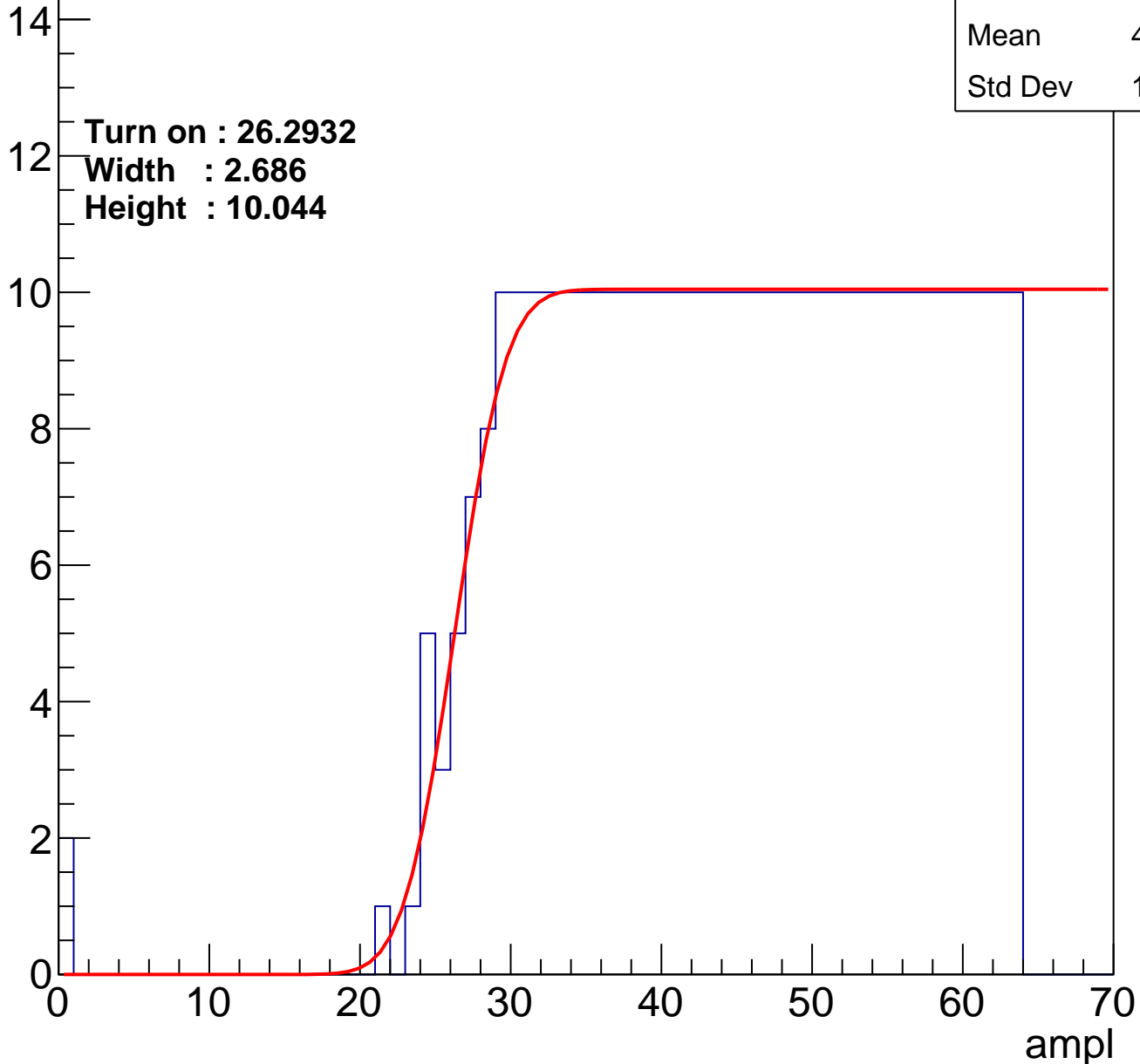
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.19 |
| Std Dev | 11.52 |

Turn on : 26.2932

Width : 2.686

Height : 10.044

Entry



B1L000S, U26-ch18

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.24 |
| Std Dev | 11.62 |

Turn on : 26.0770

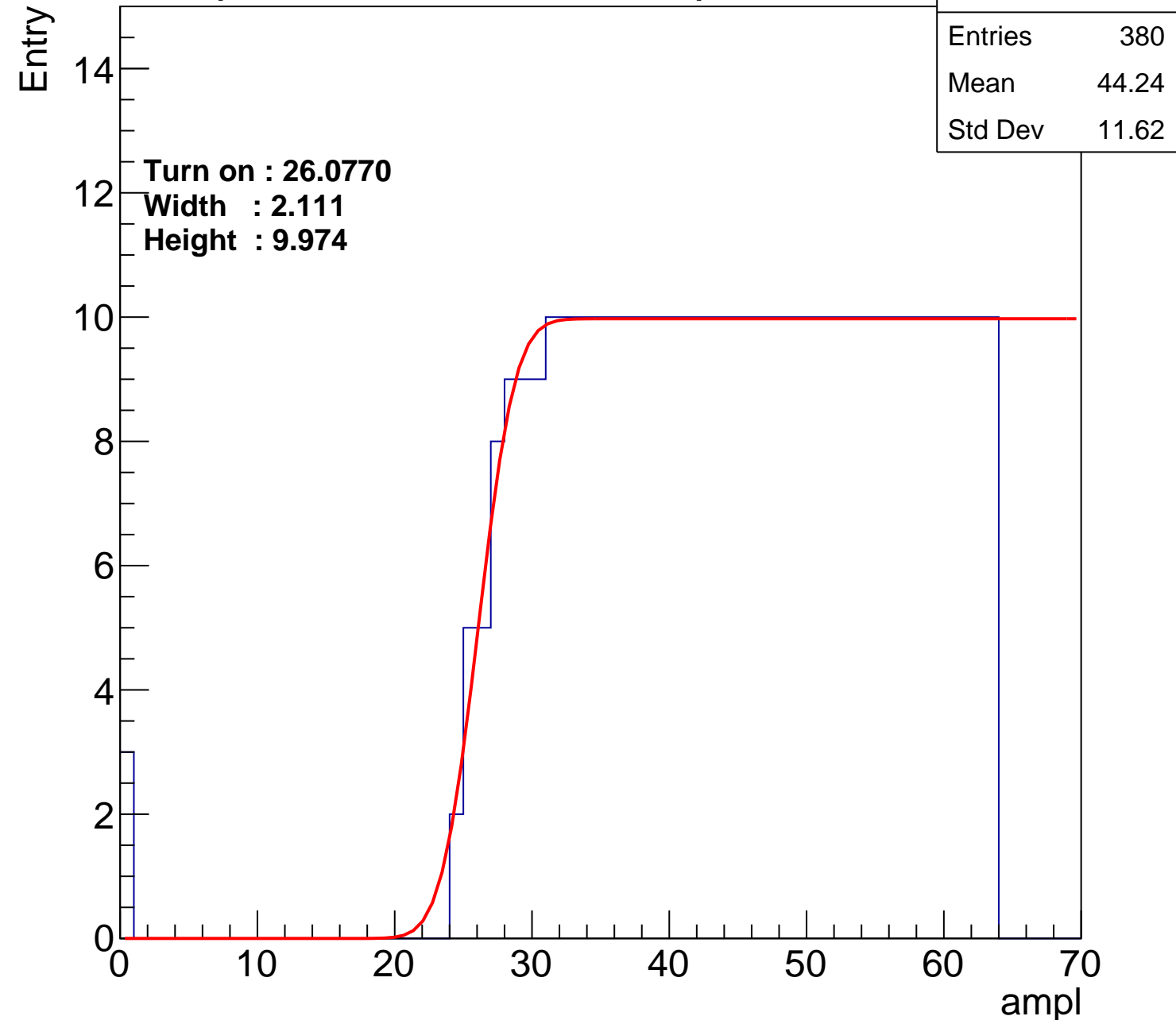
Width : 2.111

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch19

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.03 |
| Std Dev | 11.79 |

Turn on : 26.7619

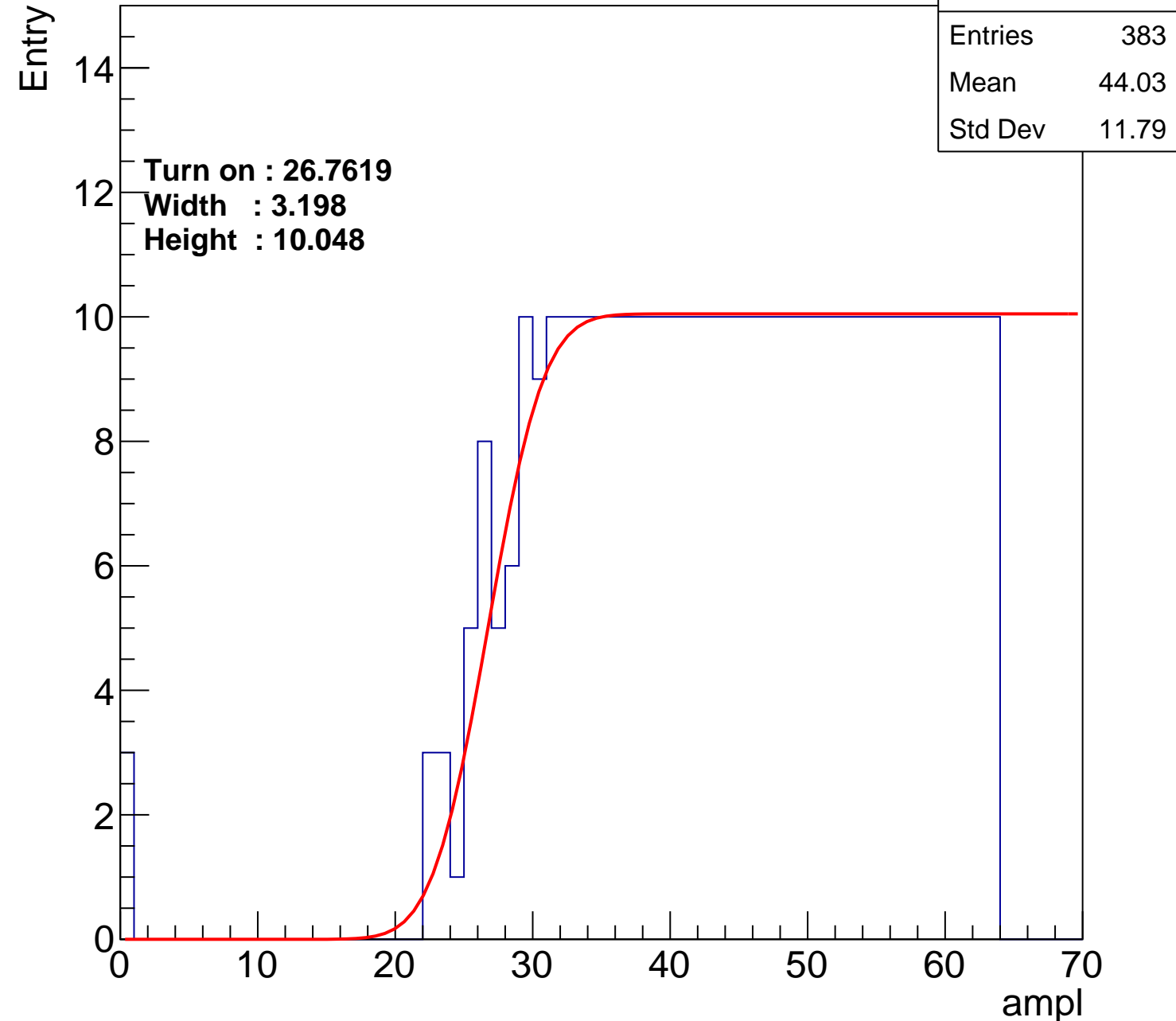
Width : 3.198

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch20

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.54 |
| Std Dev | 11.15 |

Turn on : 26.4270

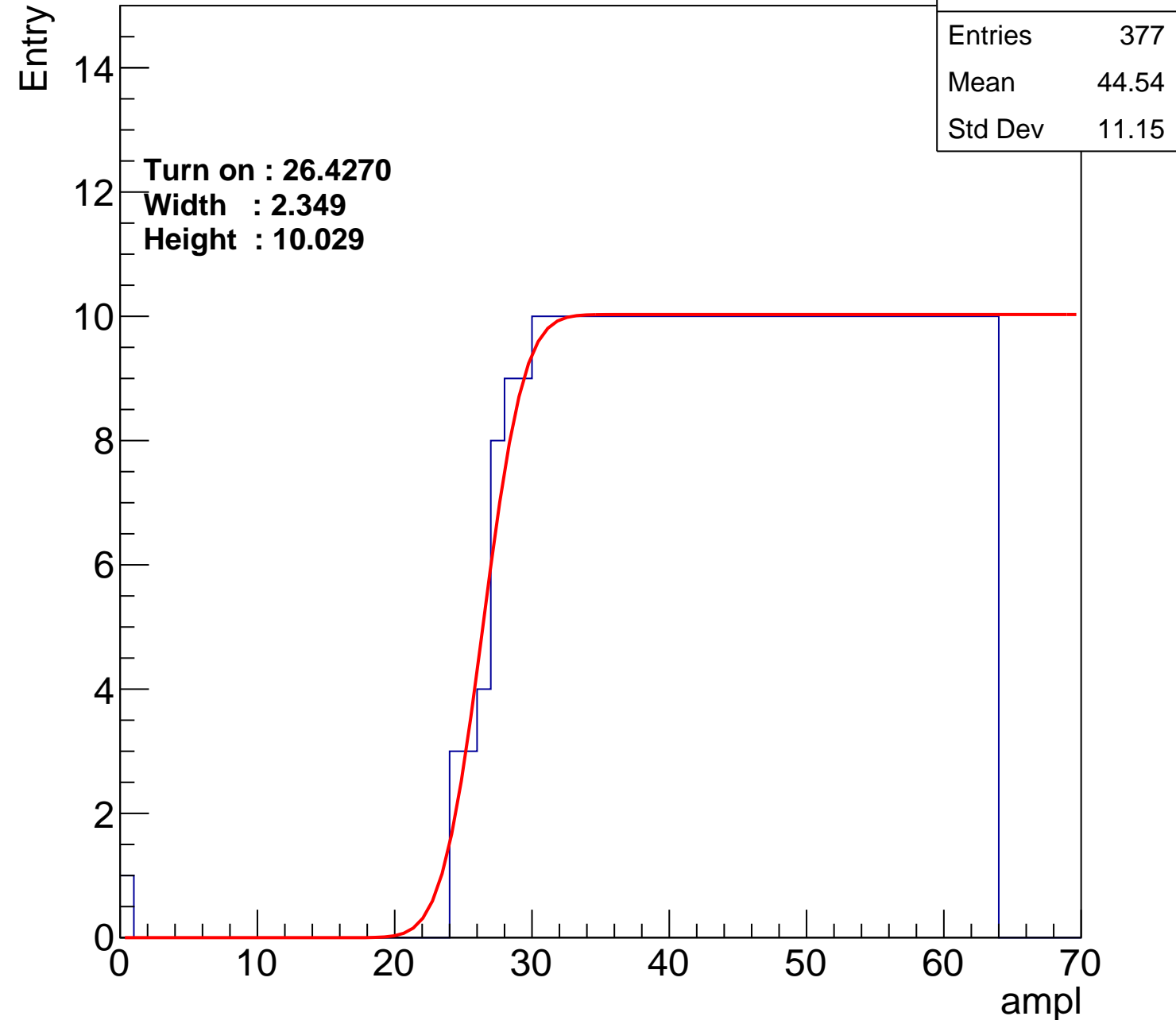
Width : 2.349

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch21

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.74 |
| Std Dev | 11.11 |

Turn on : 27.1382

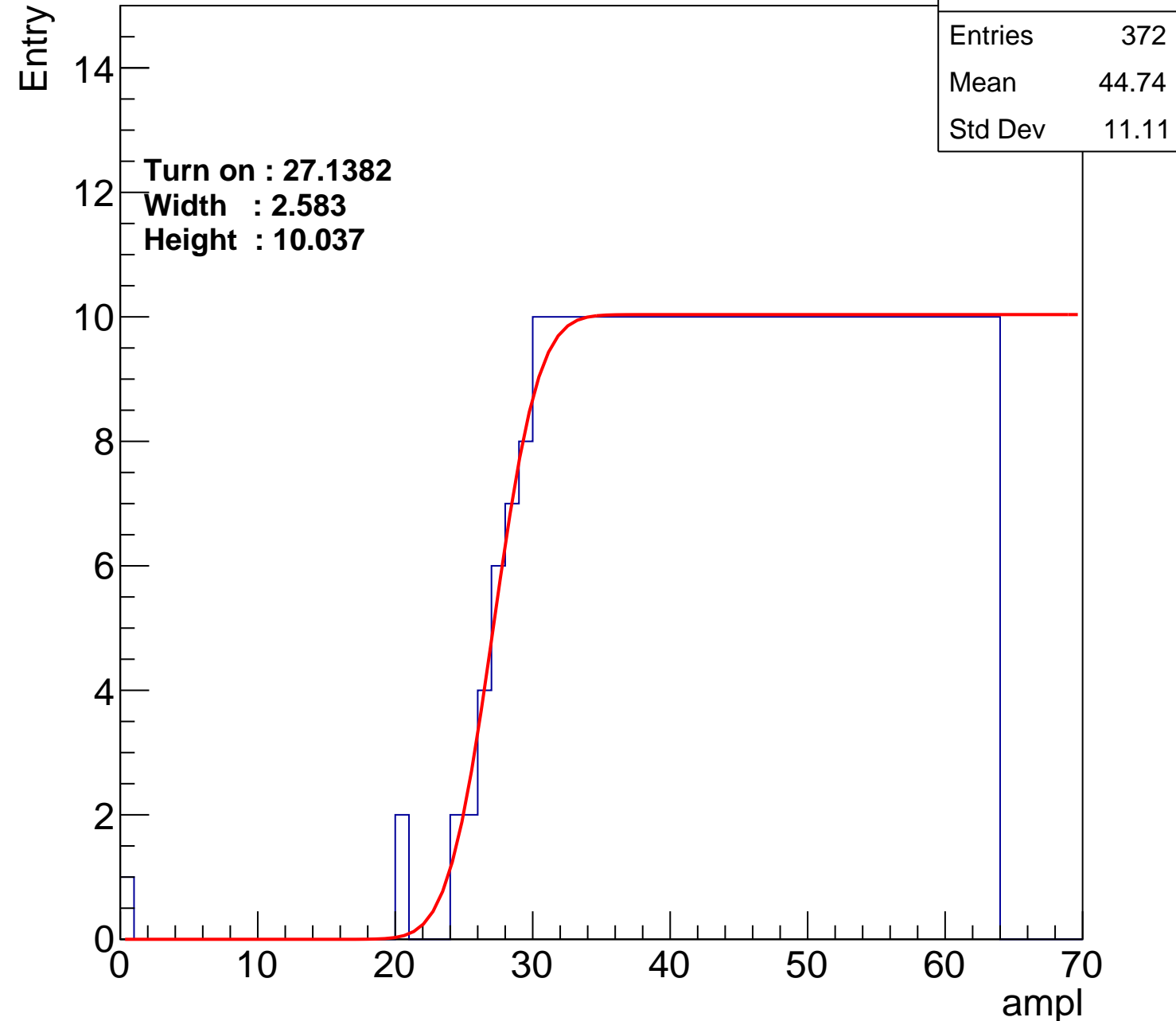
Width : 2.583

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch22

calib_packv5_042523_0143.root, FC#3, port B3

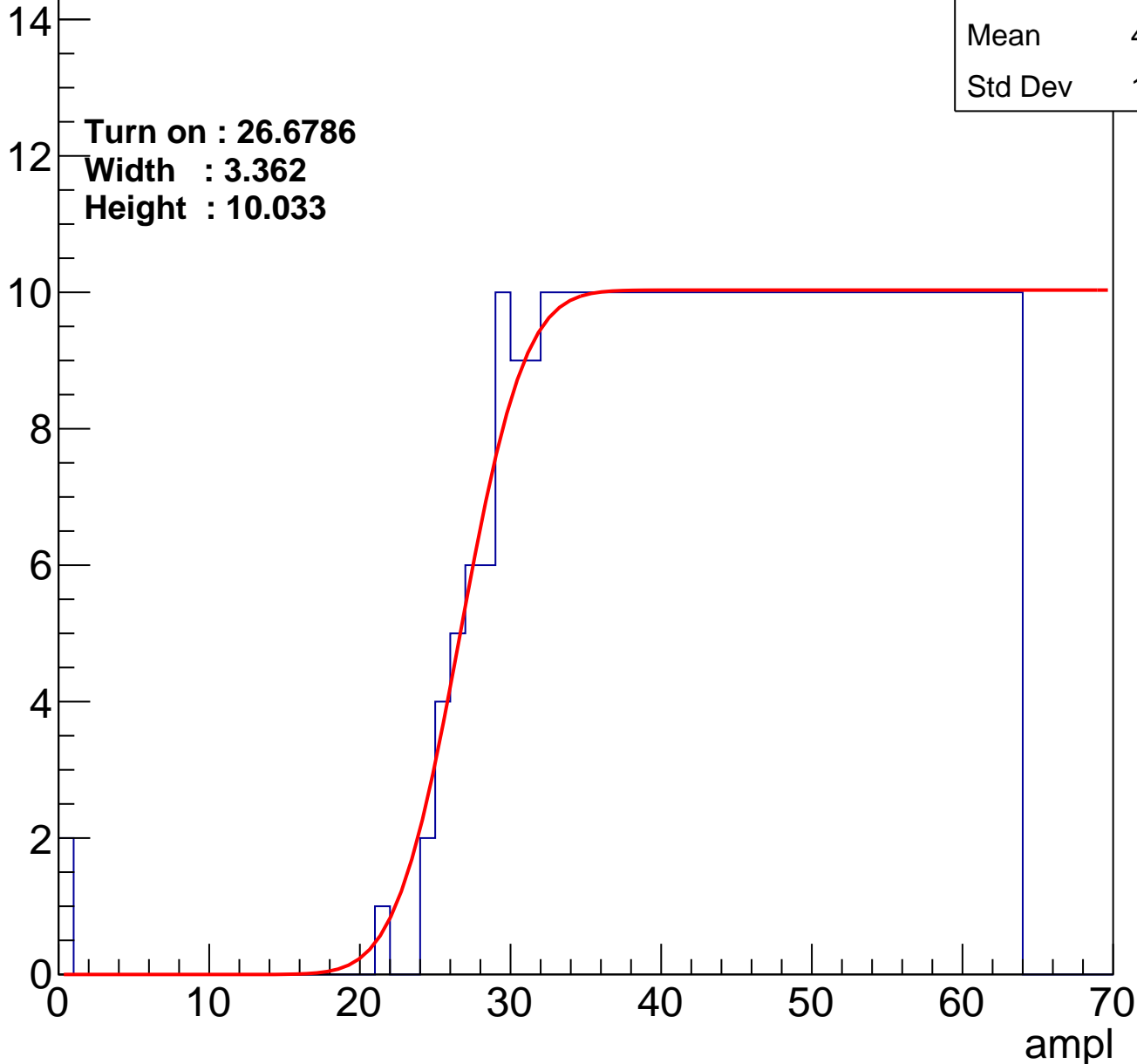
| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.57 |
| Std Dev | 11.35 |

Turn on : 26.6786

Width : 3.362

Height : 10.033

Entry



B1L000S, U26-ch23

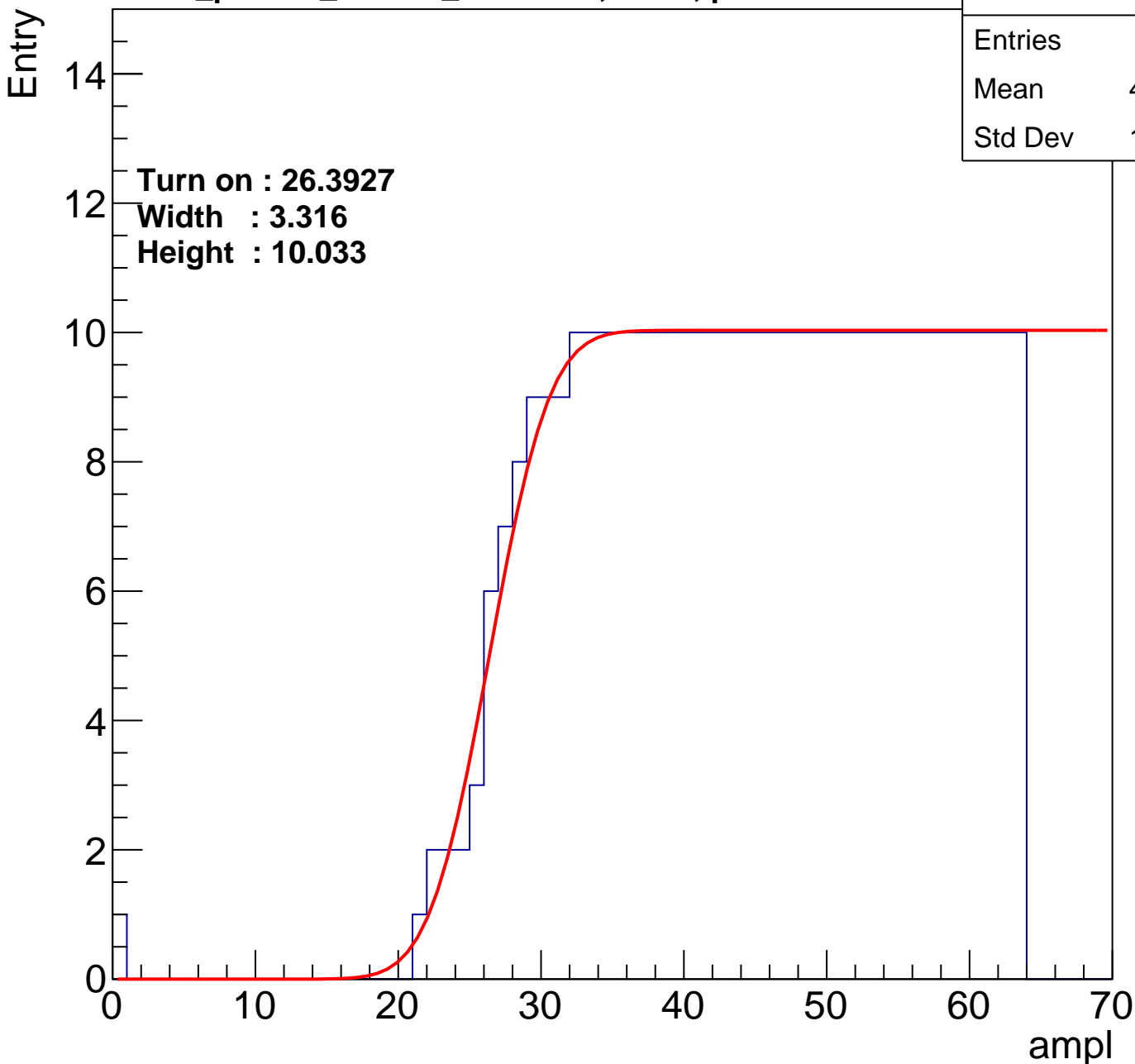
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.36 |
| Std Dev | 11.33 |

Turn on : 26.3927

Width : 3.316

Height : 10.033



B1L000S, U26-ch24

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.52 |
| Std Dev | 11.18 |

Turn on : 26.5777

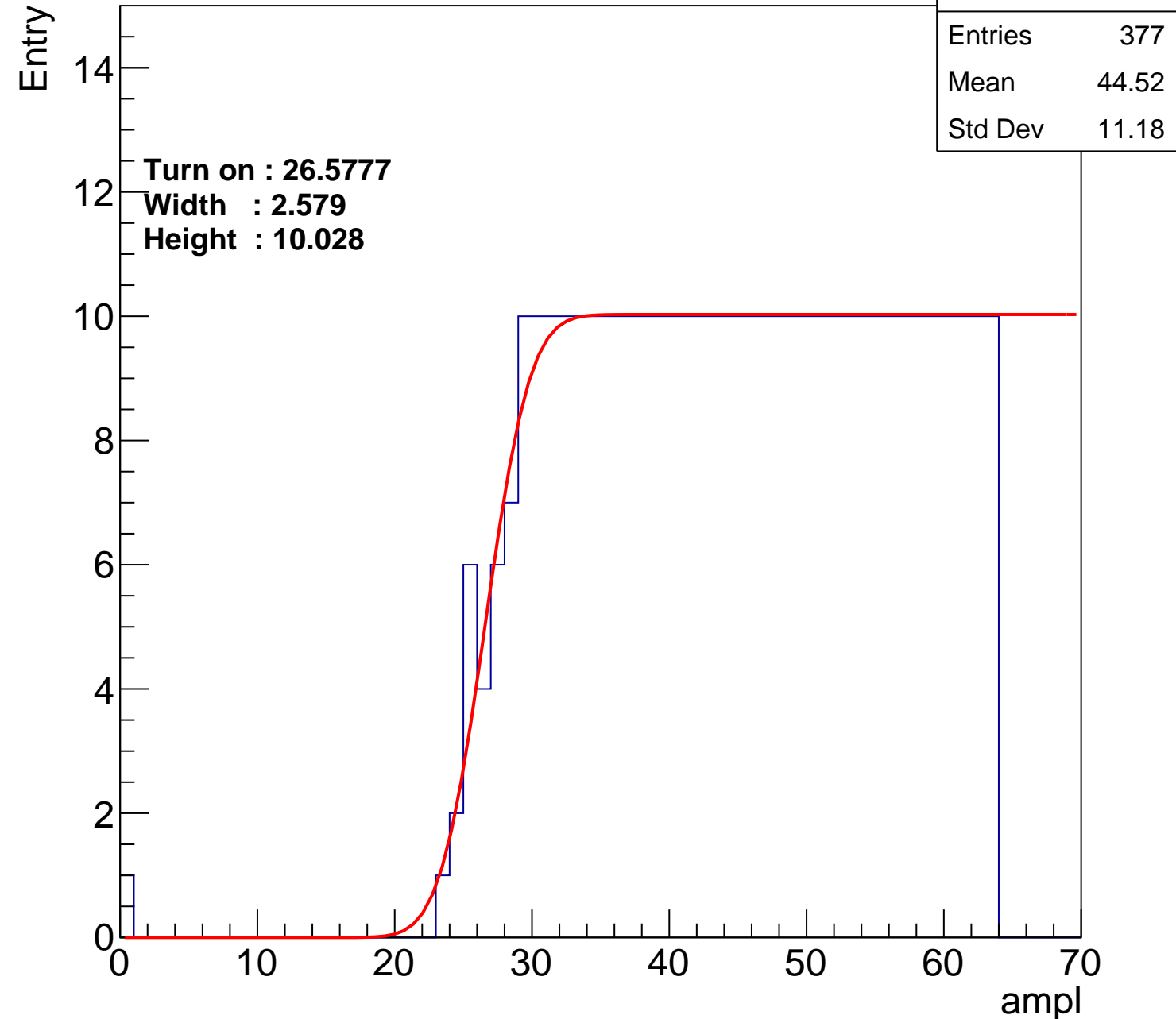
Width : 2.579

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch25

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.31 |
| Std Dev | 11.3 |

Turn on : 26.0705

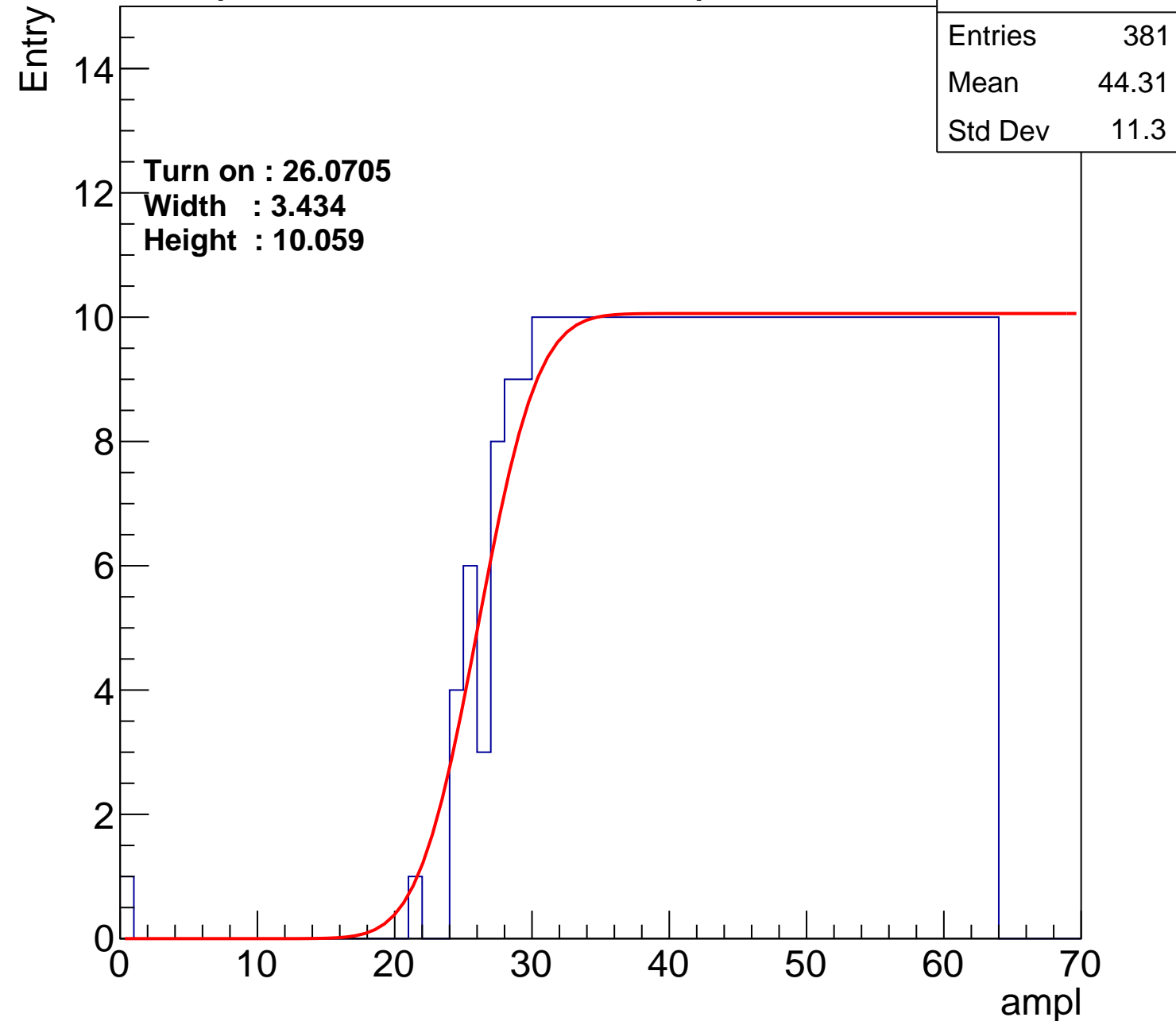
Width : 3.434

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch26

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.11 |
| Std Dev | 11.55 |

Turn on : 25.6334

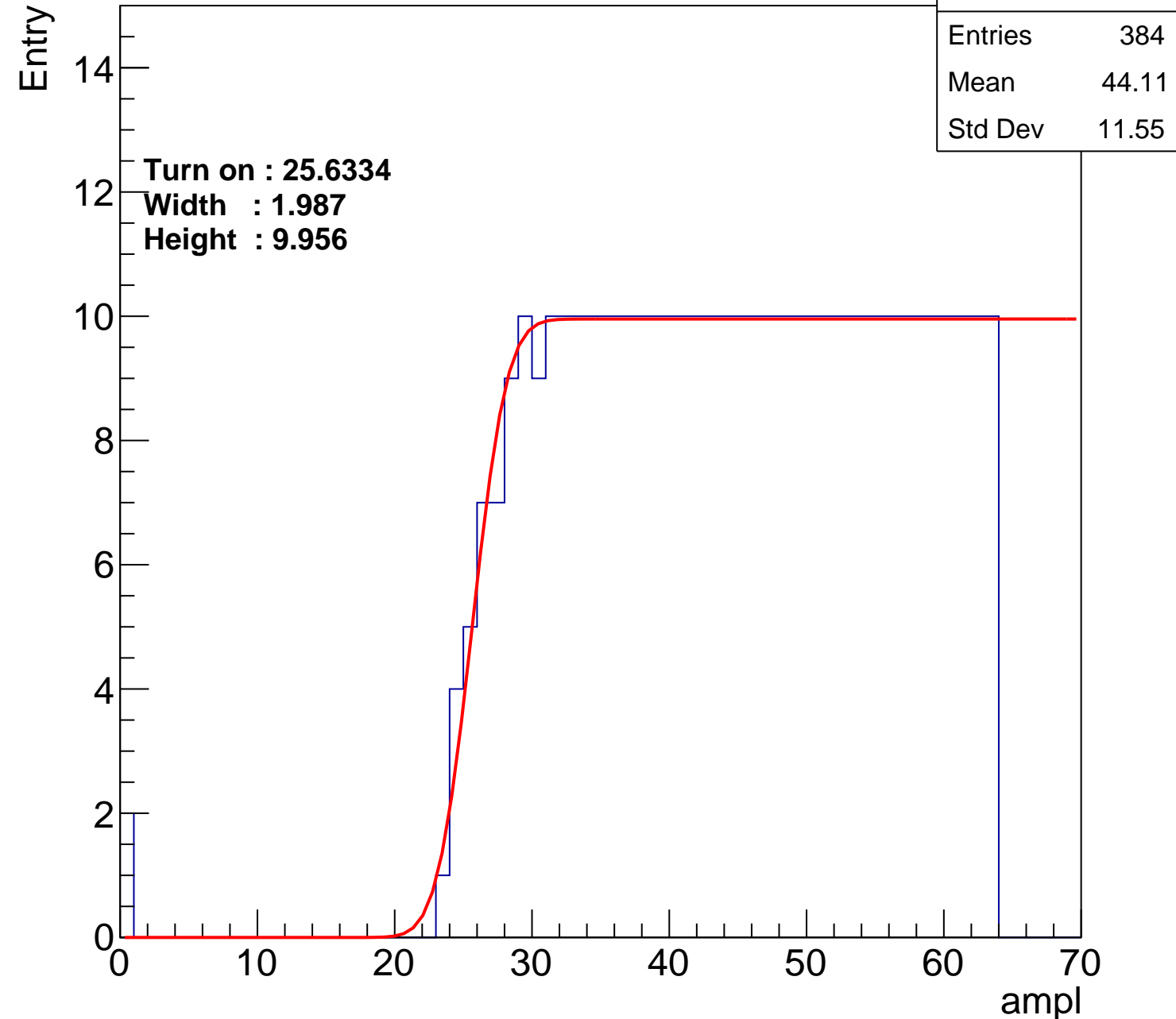
Width : 1.987

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch27

calib_packv5_042523_0143.root, FC#3, port B3

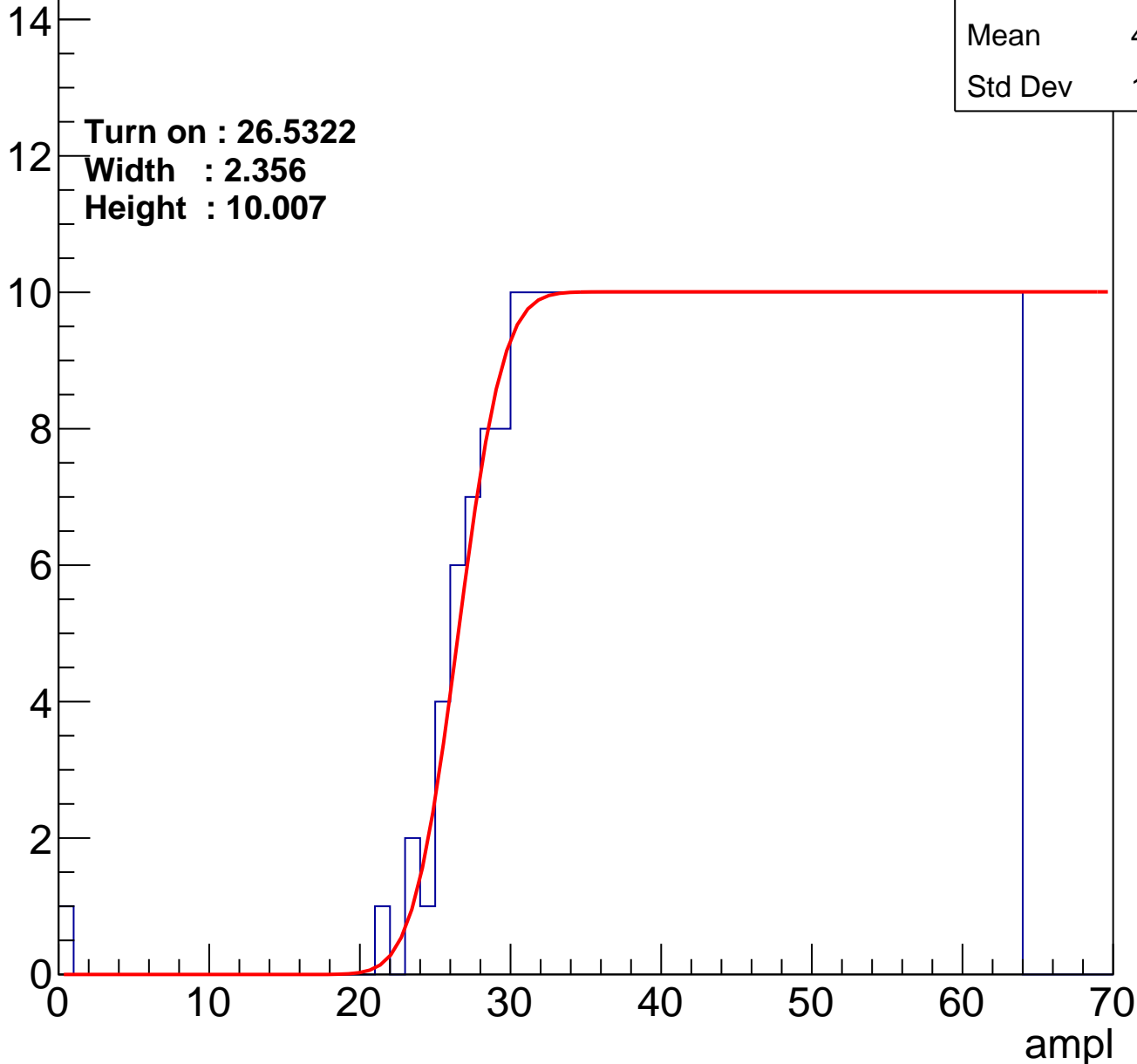
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.45 |
| Std Dev | 11.24 |

Turn on : 26.5322

Width : 2.356

Height : 10.007

Entry



B1L000S, U26-ch28

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.64 |
| Std Dev | 11.83 |

Turn on : 25.0622

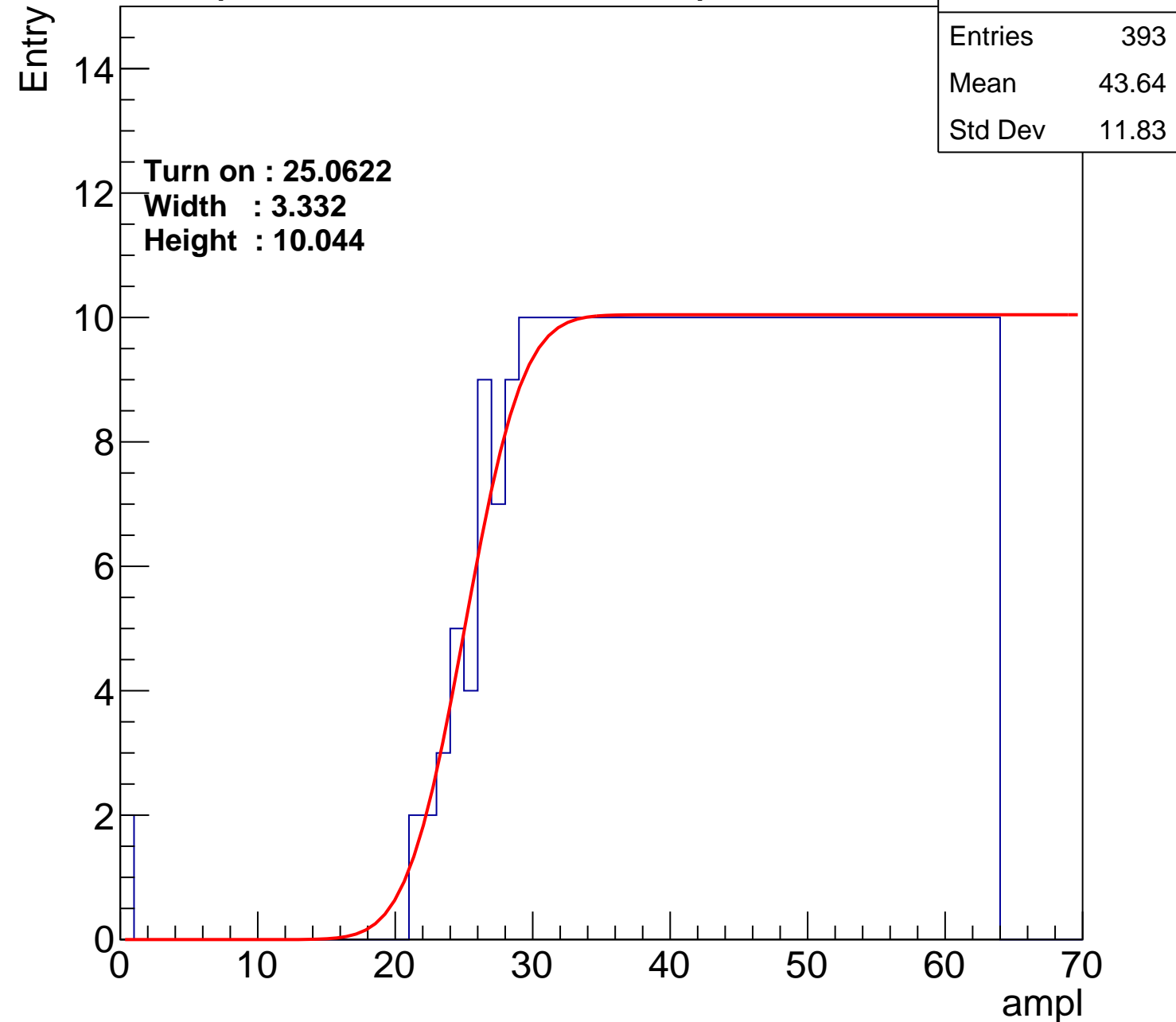
Width : 3.332

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch29

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.78 |
| Std Dev | 11.41 |

Turn on : 28.1699

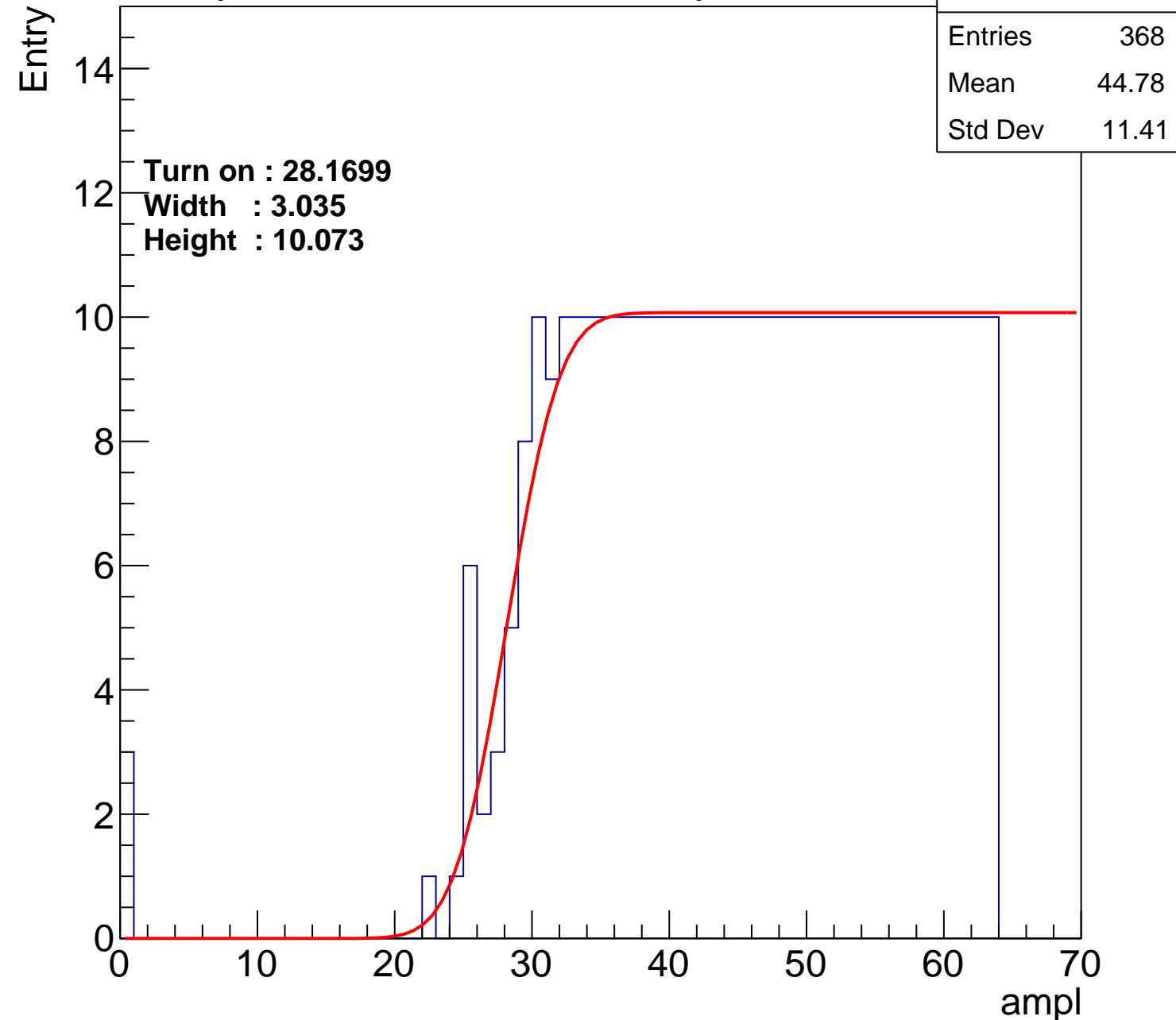
Width : 3.035

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch30

calib_packv5_042523_0143.root, FC#3, port B3

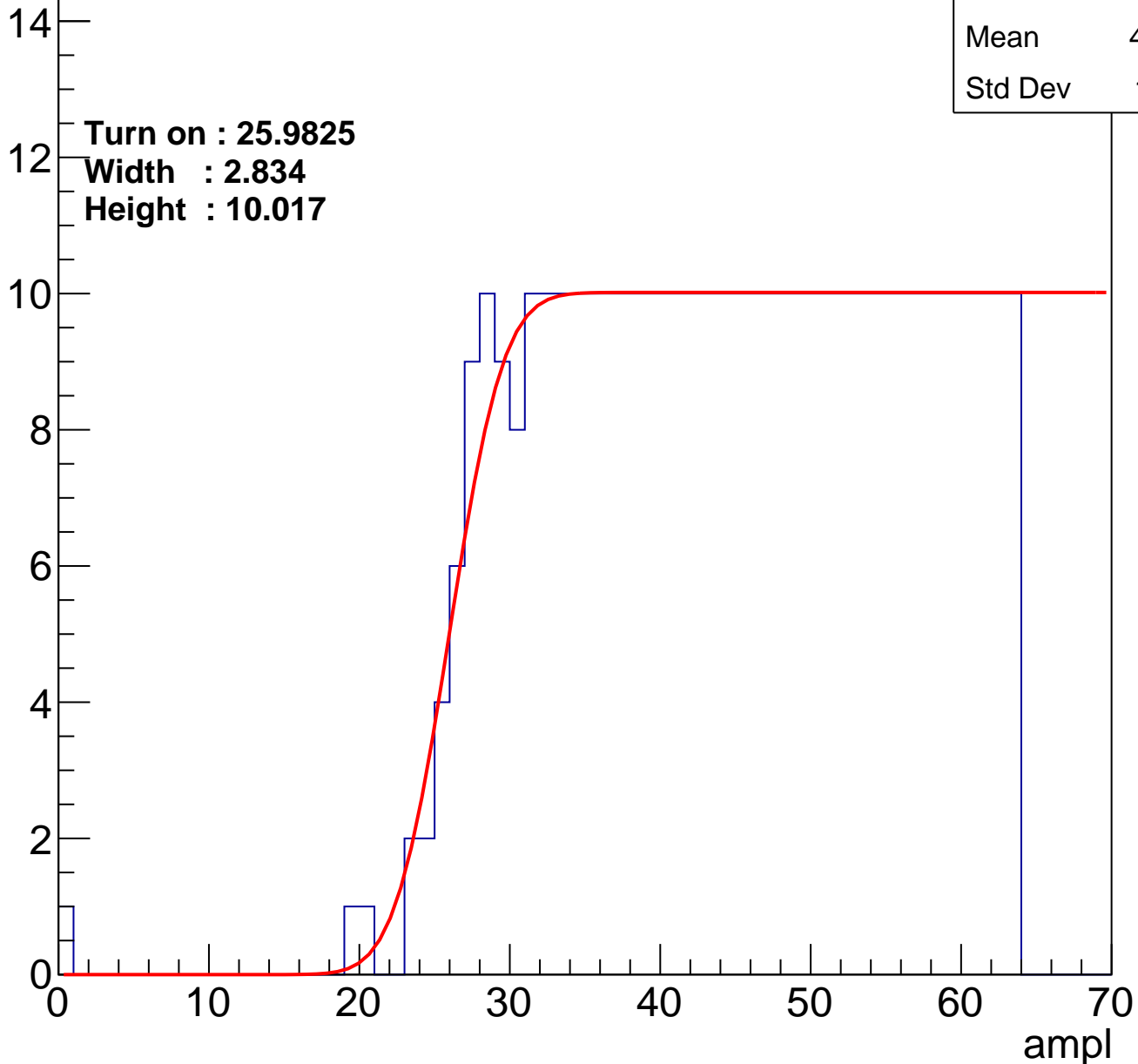
| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.19 |
| Std Dev | 11.41 |

Turn on : 25.9825

Width : 2.834

Height : 10.017

Entry



B1L000S, U26-ch31

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.2 |
| Std Dev | 10.81 |

Turn on : 27.8131

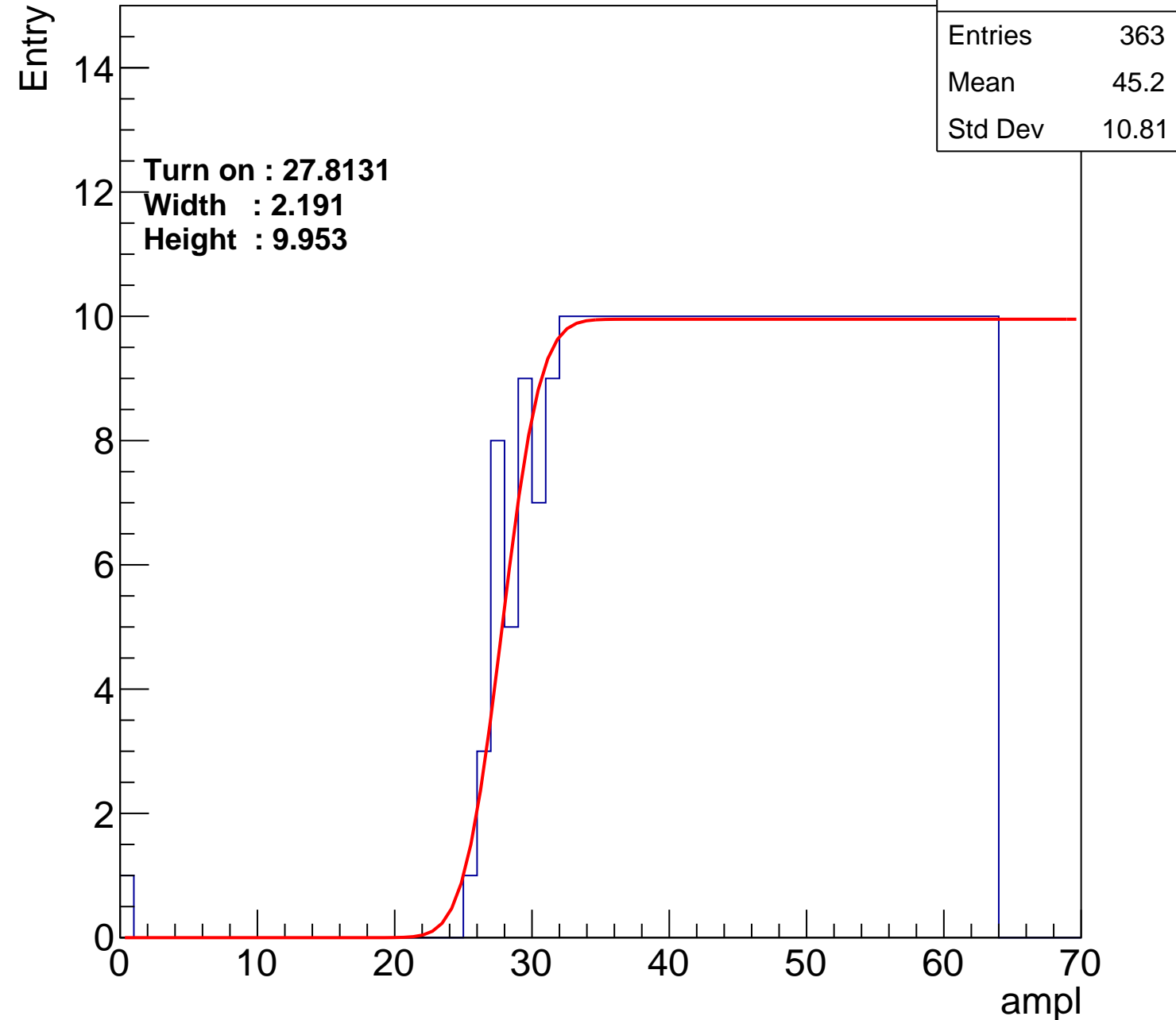
Width : 2.191

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch32

calib_packv5_042523_0143.root, FC#3, port B3

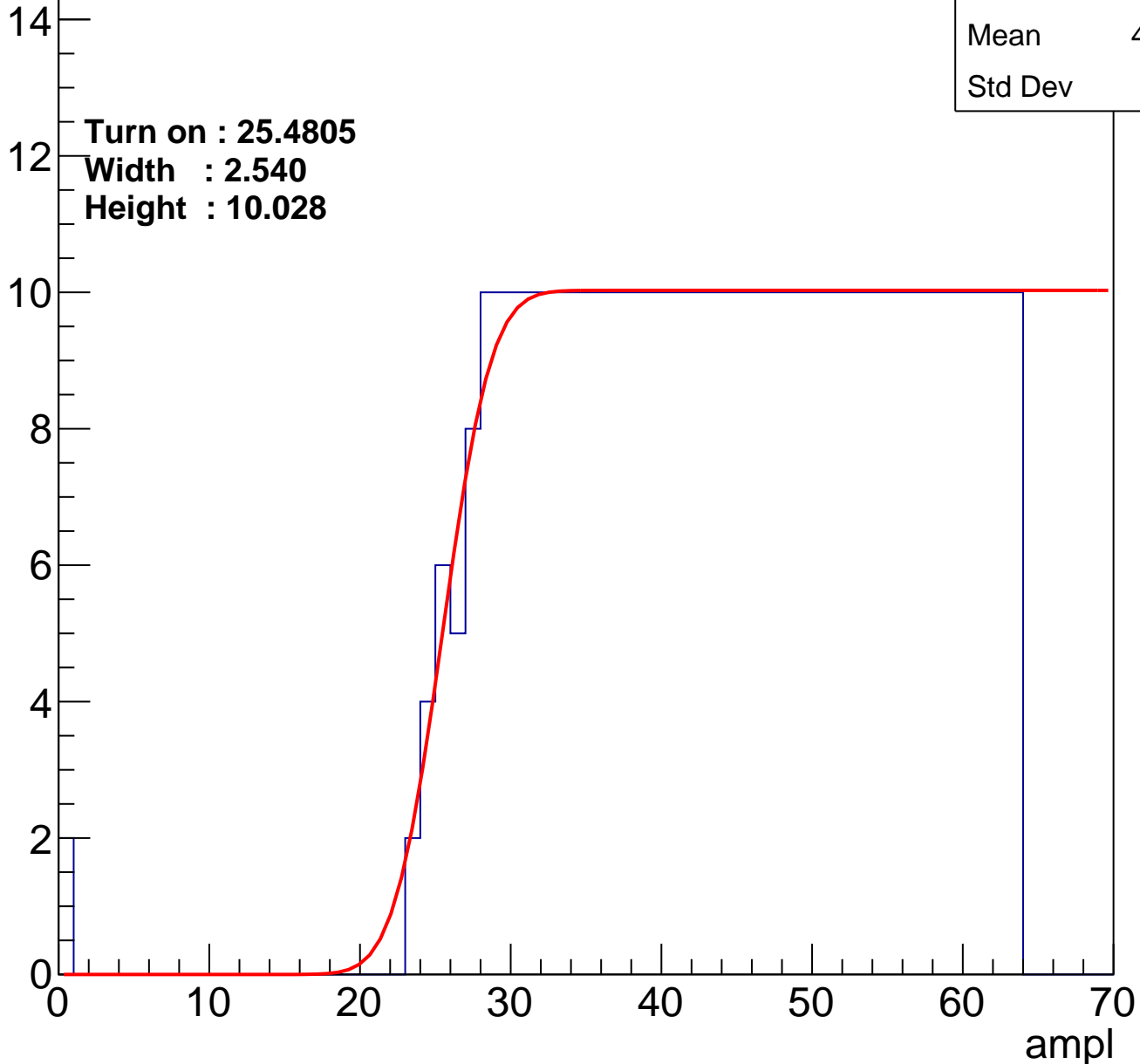
| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.97 |
| Std Dev | 11.6 |

Turn on : 25.4805

Width : 2.540

Height : 10.028

Entry



B1L000S, U26-ch33

calib_packv5_042523_0143.root, FC#3, port B3

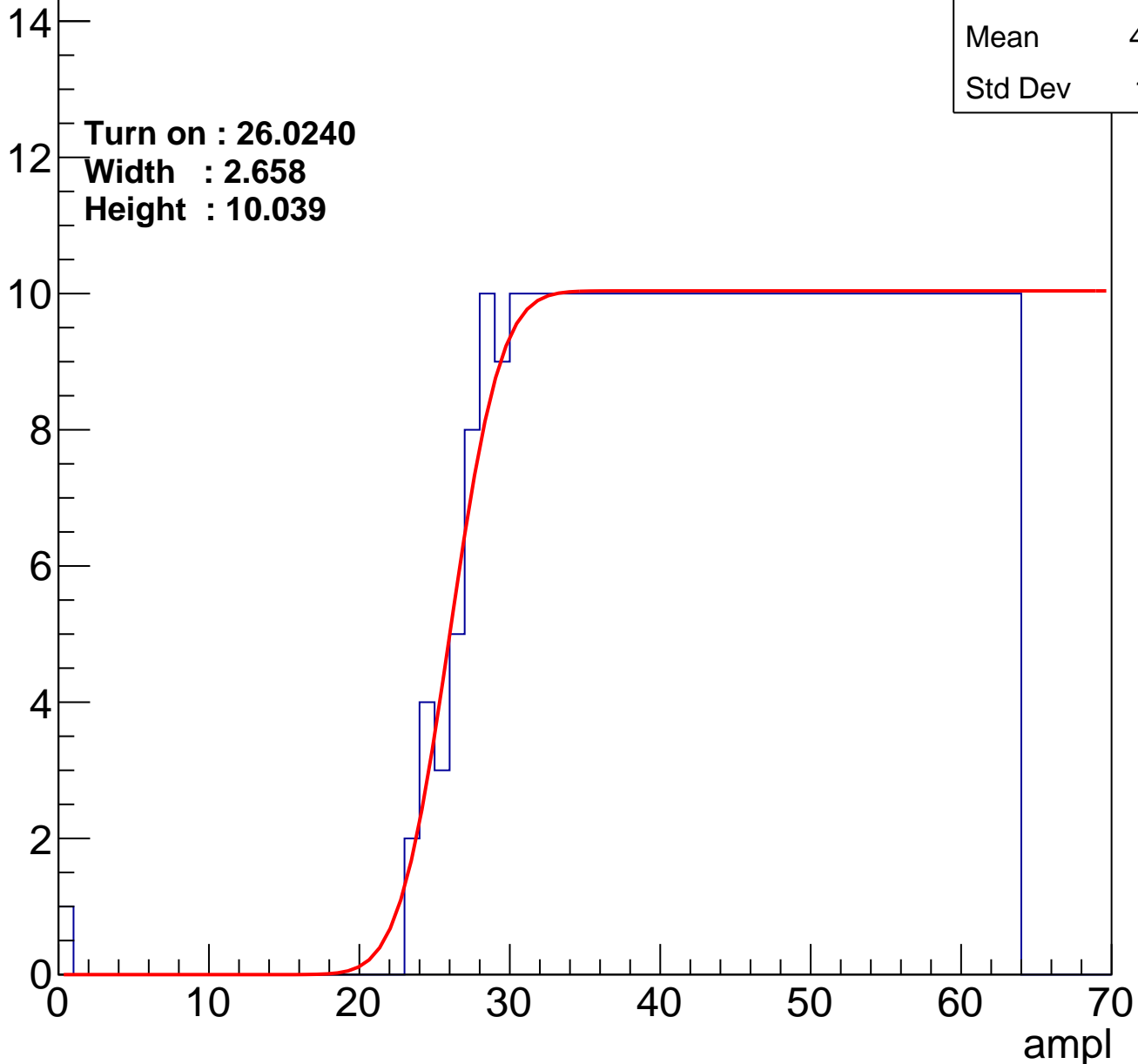
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.28 |
| Std Dev | 11.31 |

Turn on : 26.0240

Width : 2.658

Height : 10.039

Entry



B1L000S, U26-ch34

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.89 |
| Std Dev | 11.68 |

Turn on : 25.5424

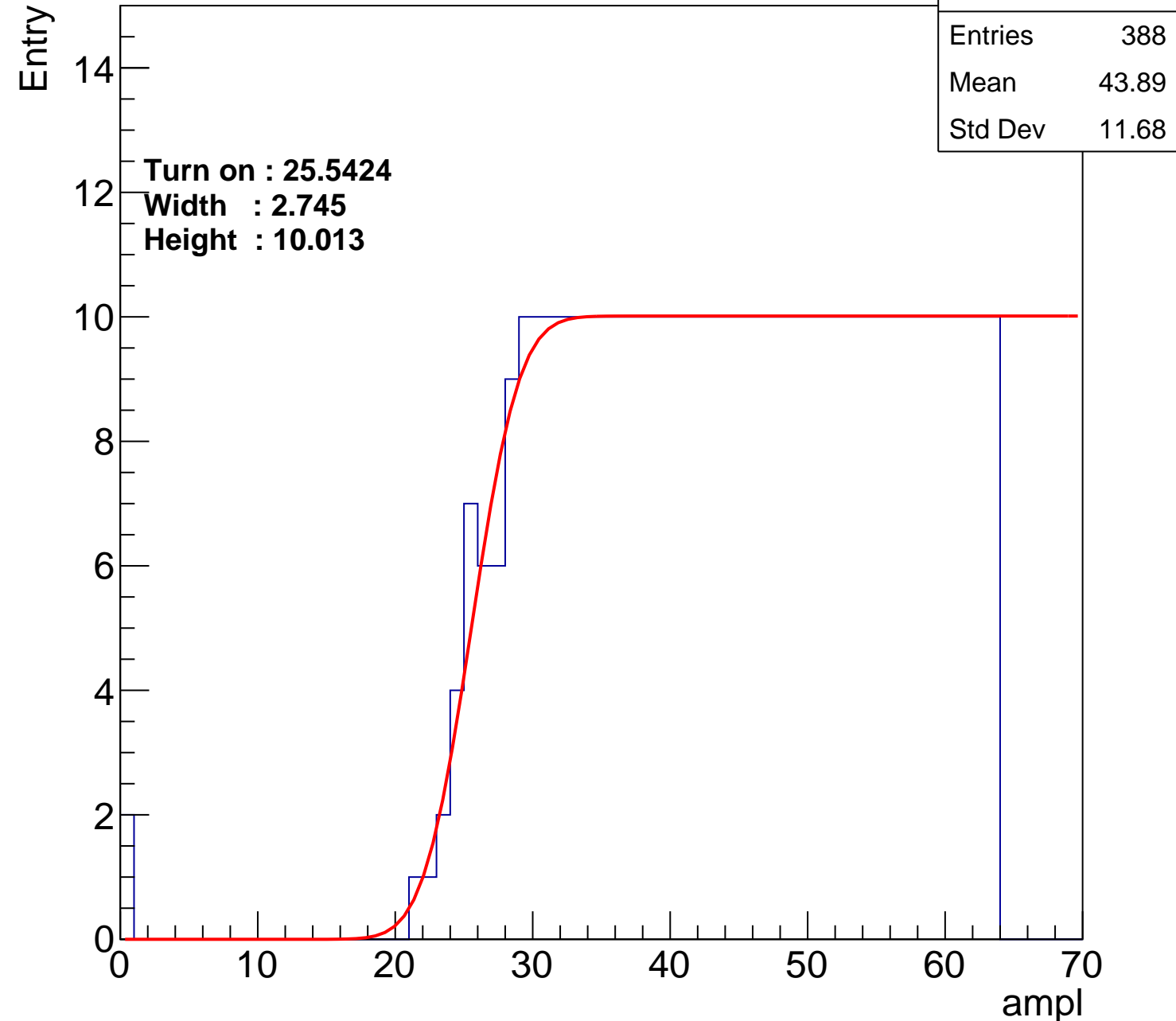
Width : 2.745

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch35

calib_packv5_042523_0143.root, FC#3, port B3

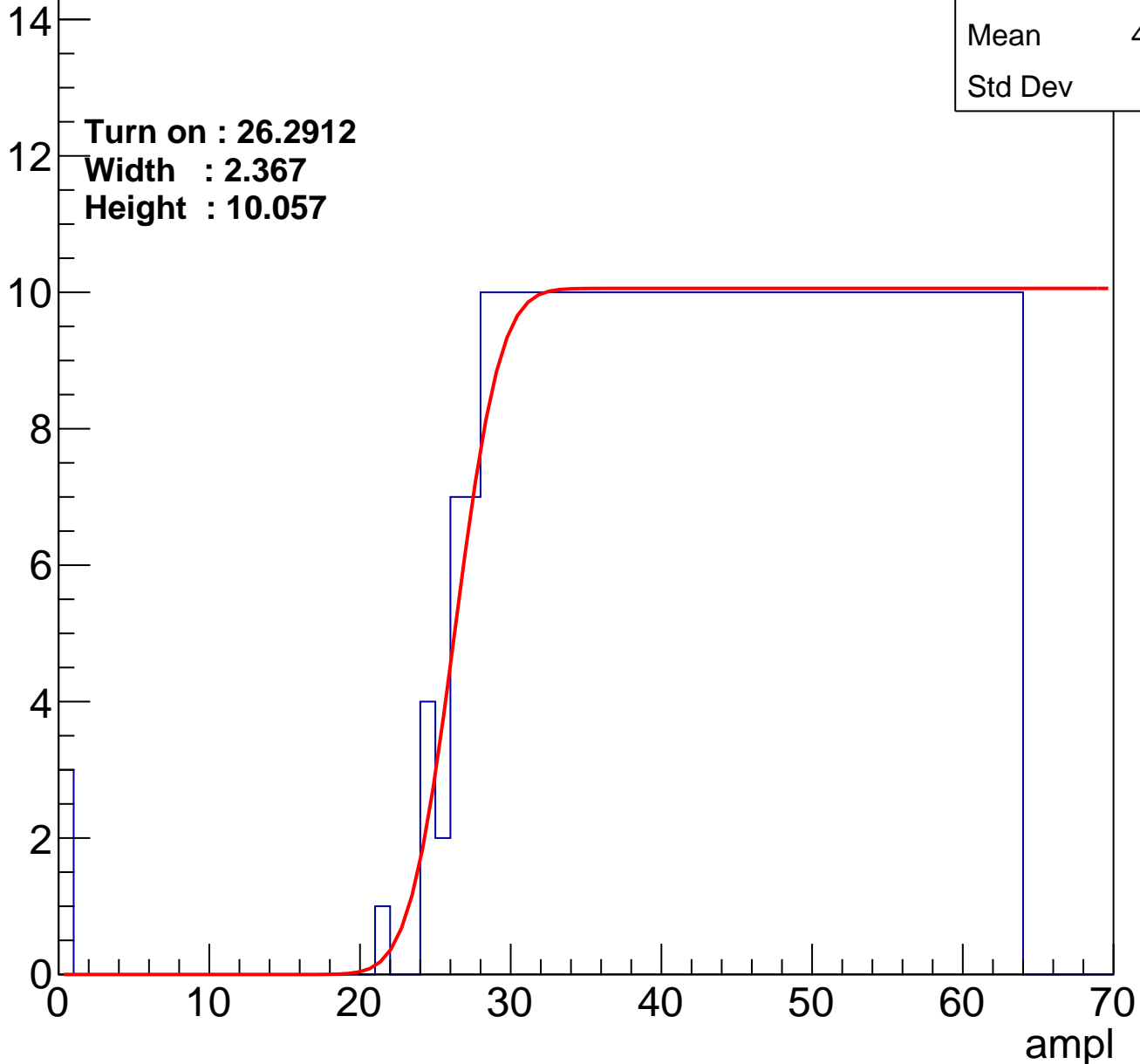
| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.06 |
| Std Dev | 11.7 |

Turn on : 26.2912

Width : 2.367

Height : 10.057

Entry



B1L000S, U26-ch36

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.45 |
| Std Dev | 11.38 |

Turn on : 26.5443

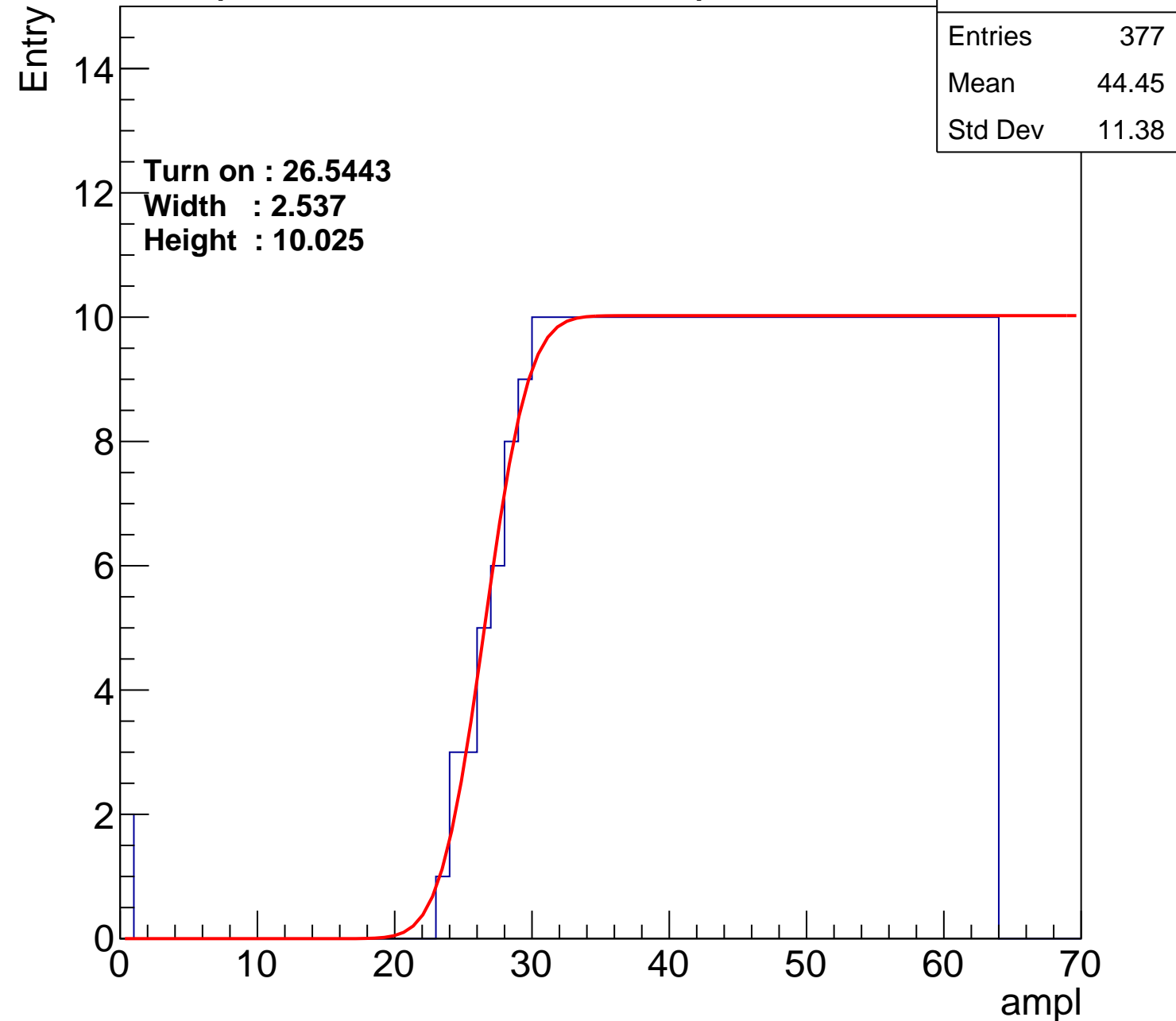
Width : 2.537

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch37

calib_packv5_042523_0143.root, FC#3, port B3

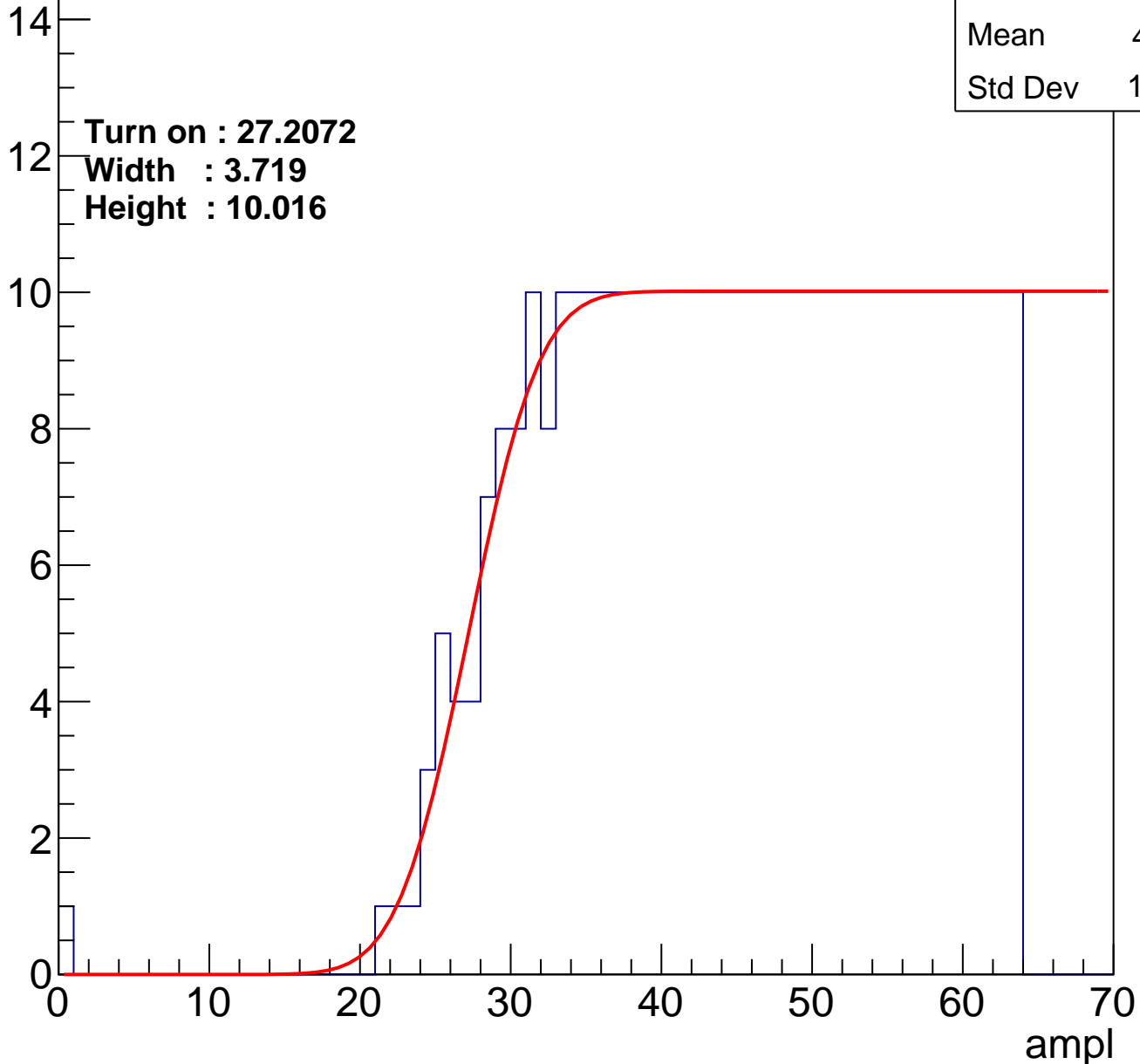
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.71 |
| Std Dev | 11.19 |

Turn on : 27.2072

Width : 3.719

Height : 10.016

Entry



B1L000S, U26-ch38

calib_packv5_042523_0143.root, FC#3, port B3

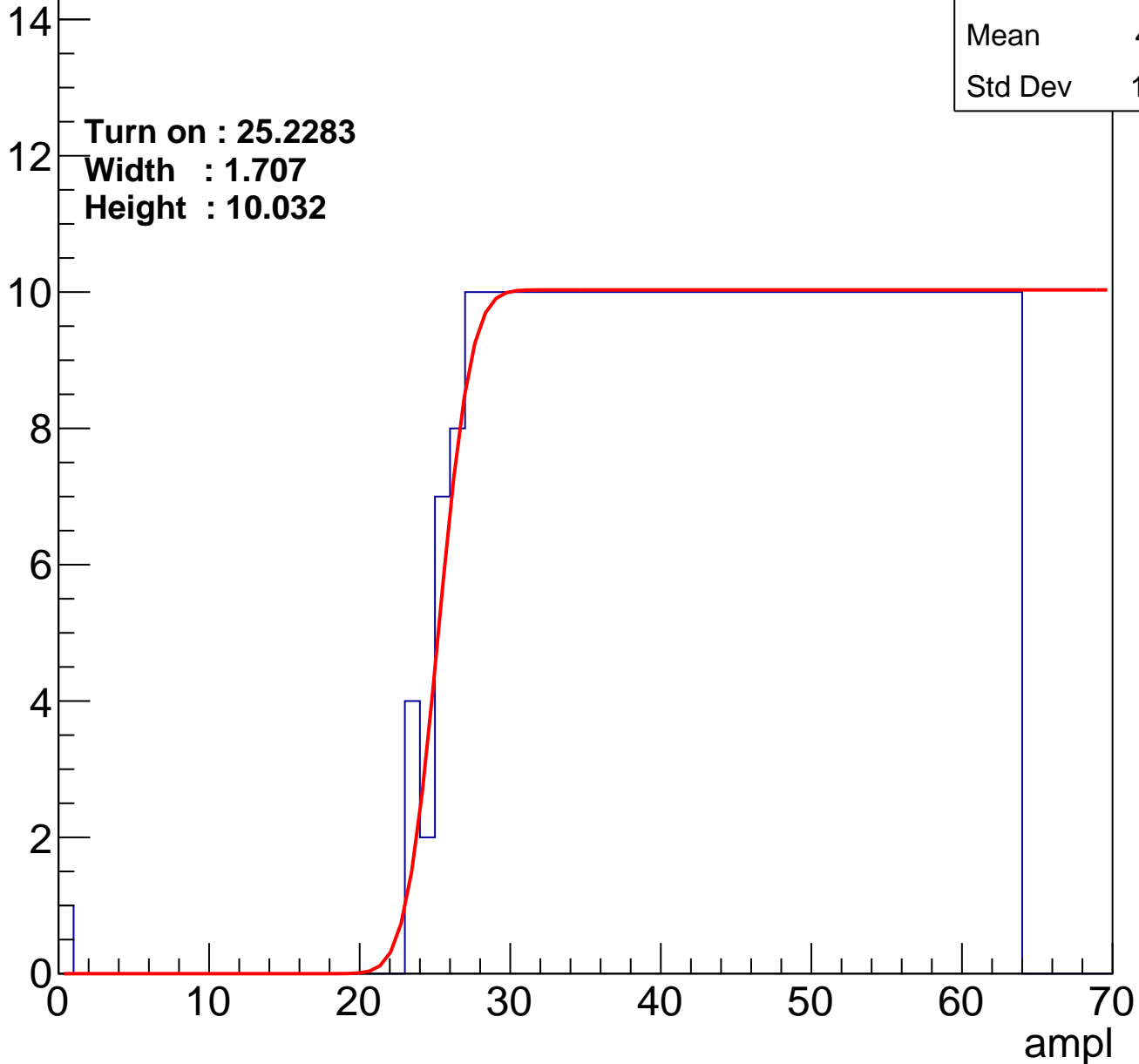
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.81 |
| Std Dev | 11.53 |

Turn on : 25.2283

Width : 1.707

Height : 10.032

Entry



B1L000S, U26-ch39

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.93 |
| Std Dev | 11.29 |

Turn on : 28.2191

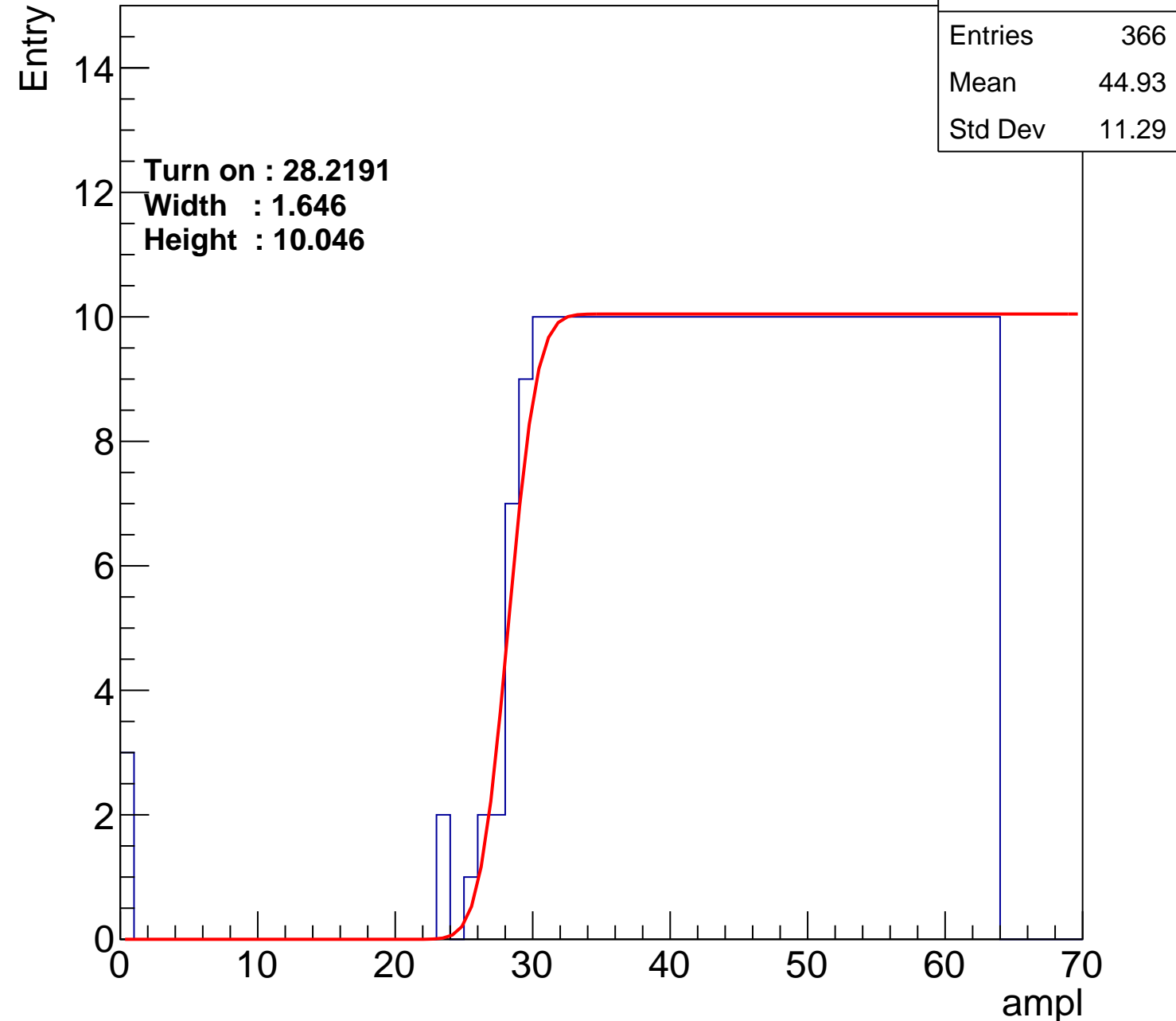
Width : 1.646

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch40

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.77 |
| Std Dev | 11.72 |

Turn on : 24.9710

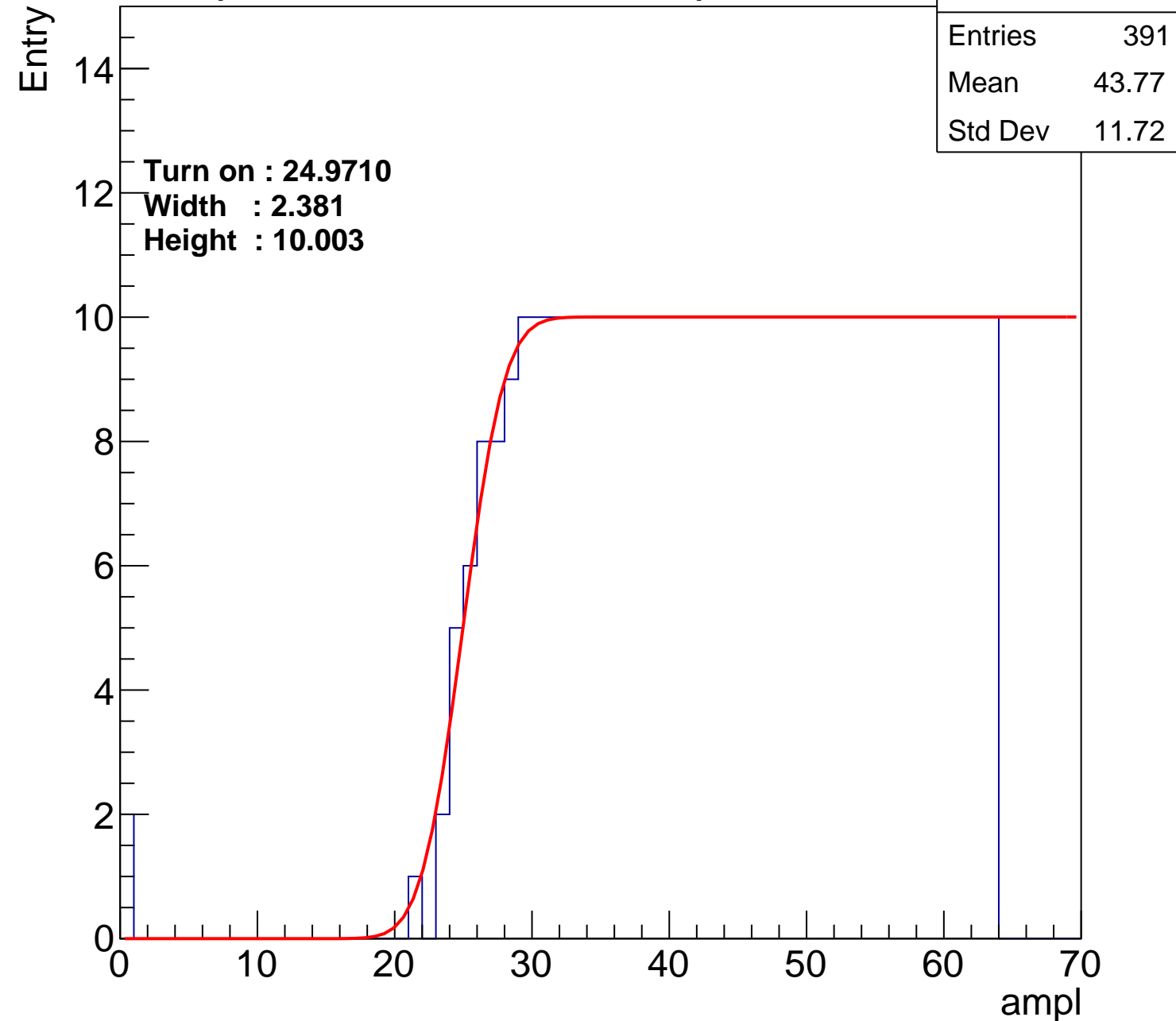
Width : 2.381

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch41

calib_packv5_042523_0143.root, FC#3, port B3

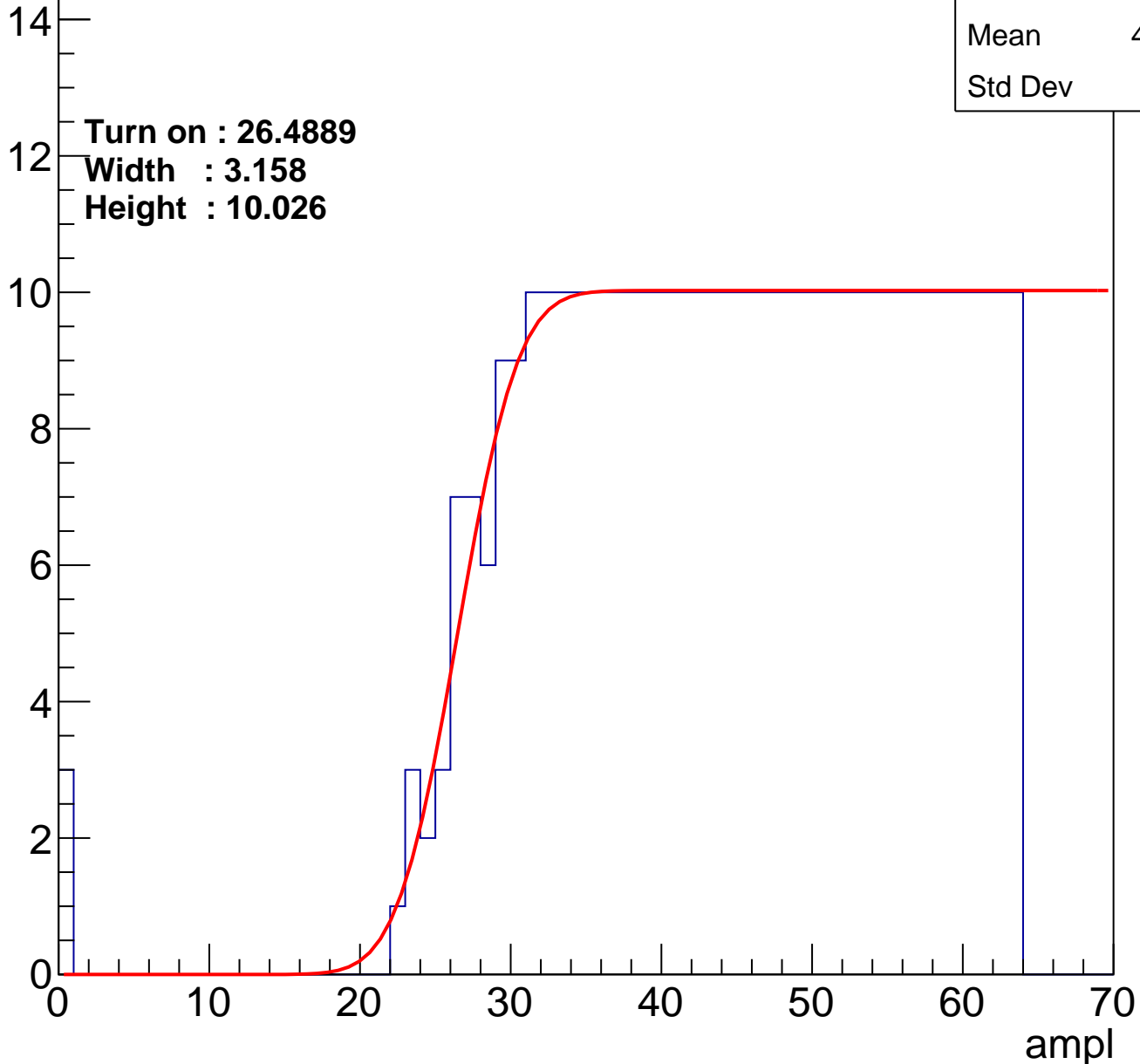
| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.19 |
| Std Dev | 11.7 |

Turn on : 26.4889

Width : 3.158

Height : 10.026

Entry



B1L000S, U26-ch42

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.49 |
| Std Dev | 11.24 |

Turn on : 26.6660

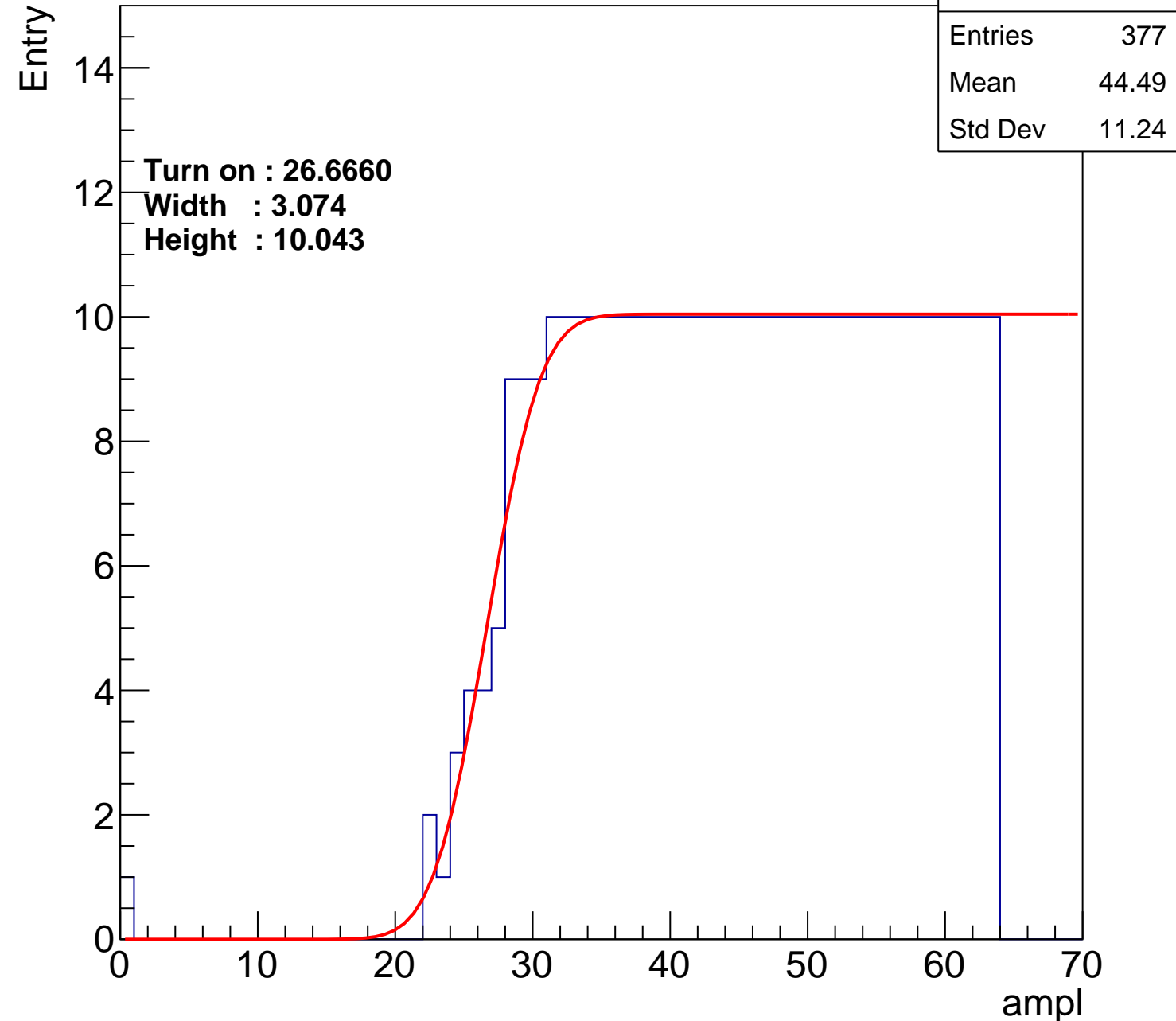
Width : 3.074

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch43

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.23 |
| Std Dev | 12.13 |

Turn on : 27.0384

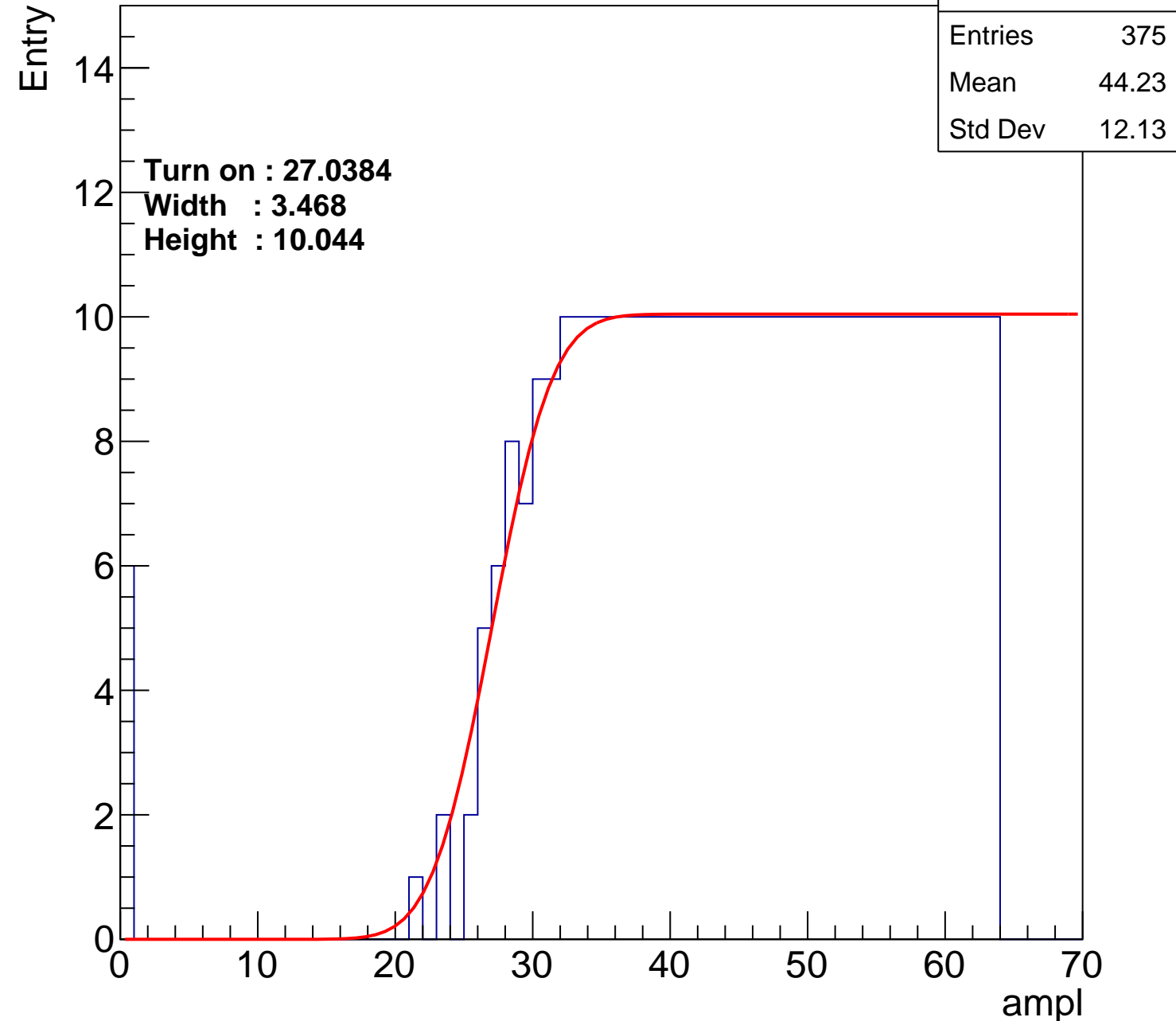
Width : 3.468

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch44

calib_packv5_042523_0143.root, FC#3, port B3

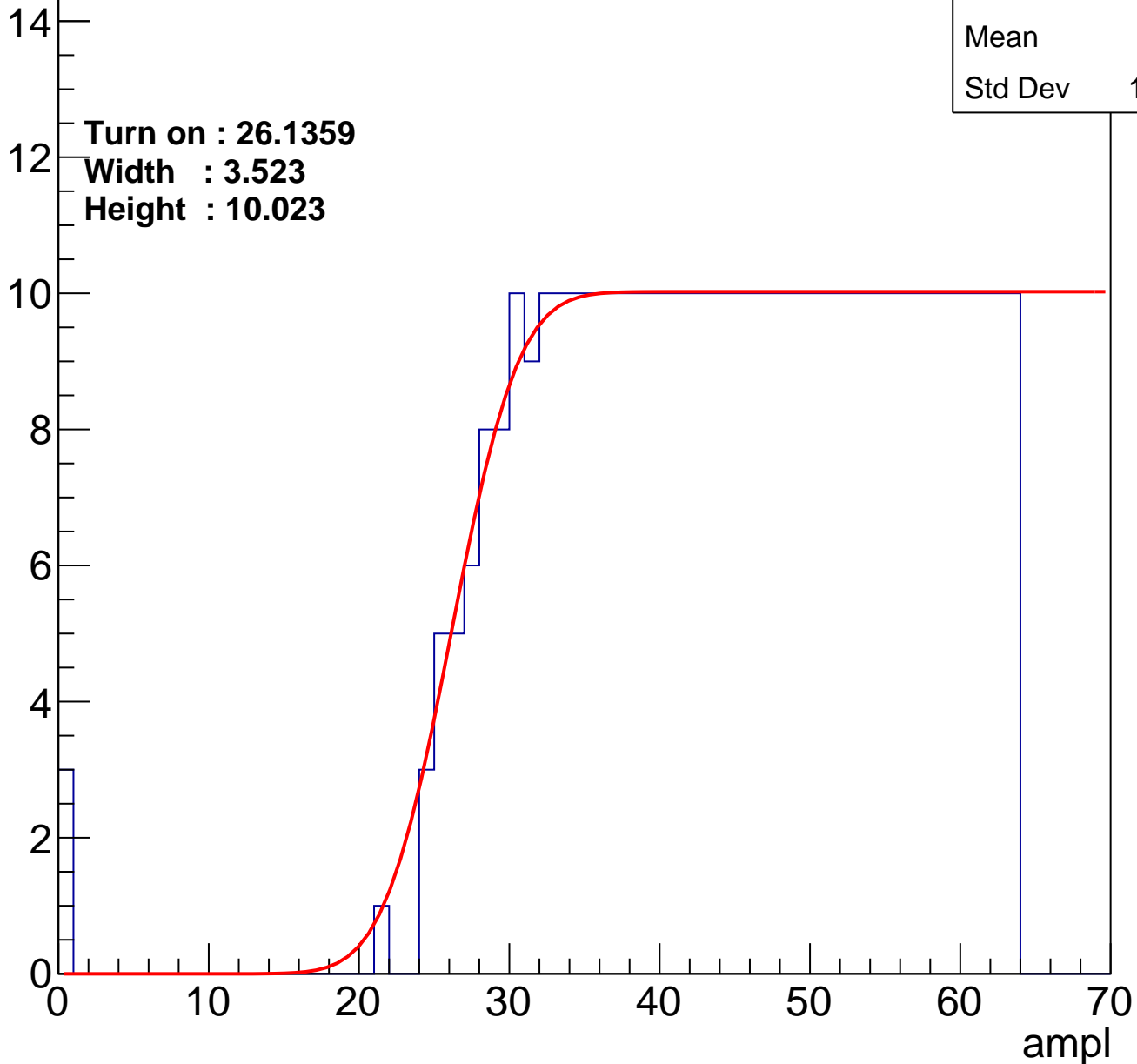
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.3 |
| Std Dev | 11.64 |

Turn on : 26.1359

Width : 3.523

Height : 10.023

Entry



B1L000S, U26-ch45

calib_packv5_042523_0143.root, FC#3, port B3

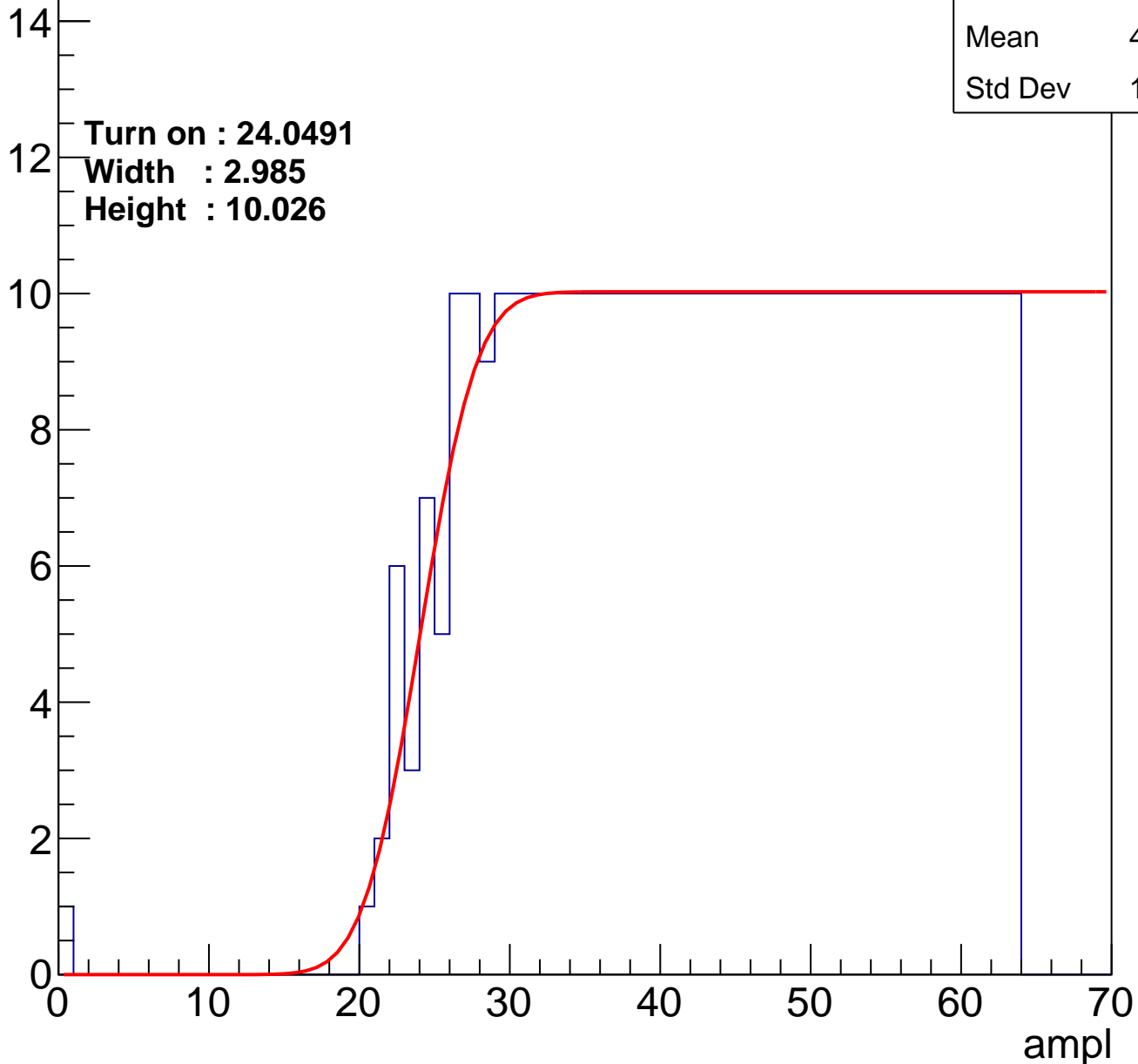
| | |
|---------|-------|
| Entries | 404 |
| Mean | 43.16 |
| Std Dev | 11.95 |

Turn on : 24.0491

Width : 2.985

Height : 10.026

Entry



B1L000S, U26-ch46

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.09 |
| Std Dev | 11.7 |

Turn on : 26.4107

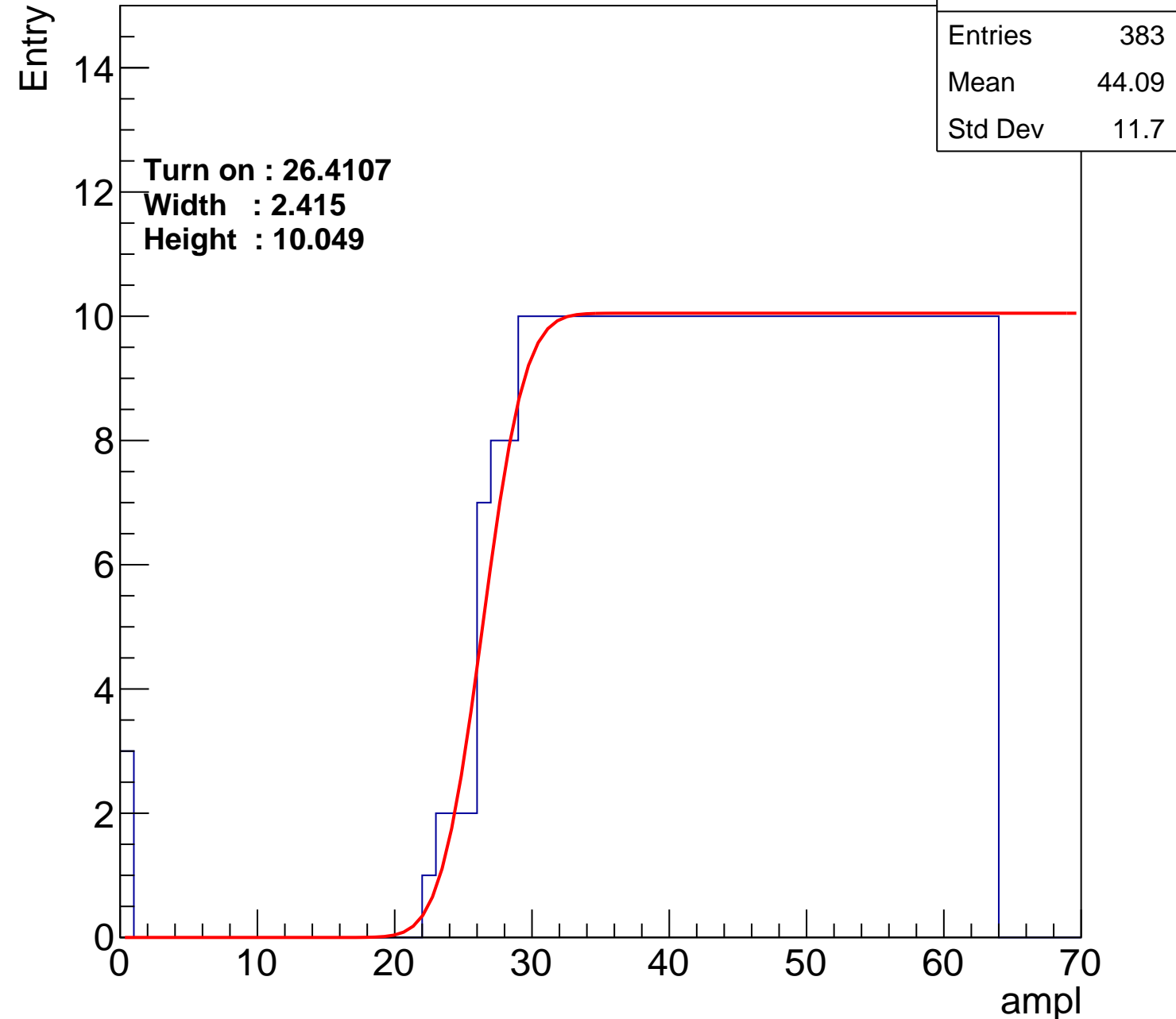
Width : 2.415

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch47

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.77 |
| Std Dev | 11.23 |

Turn on : 26.8738

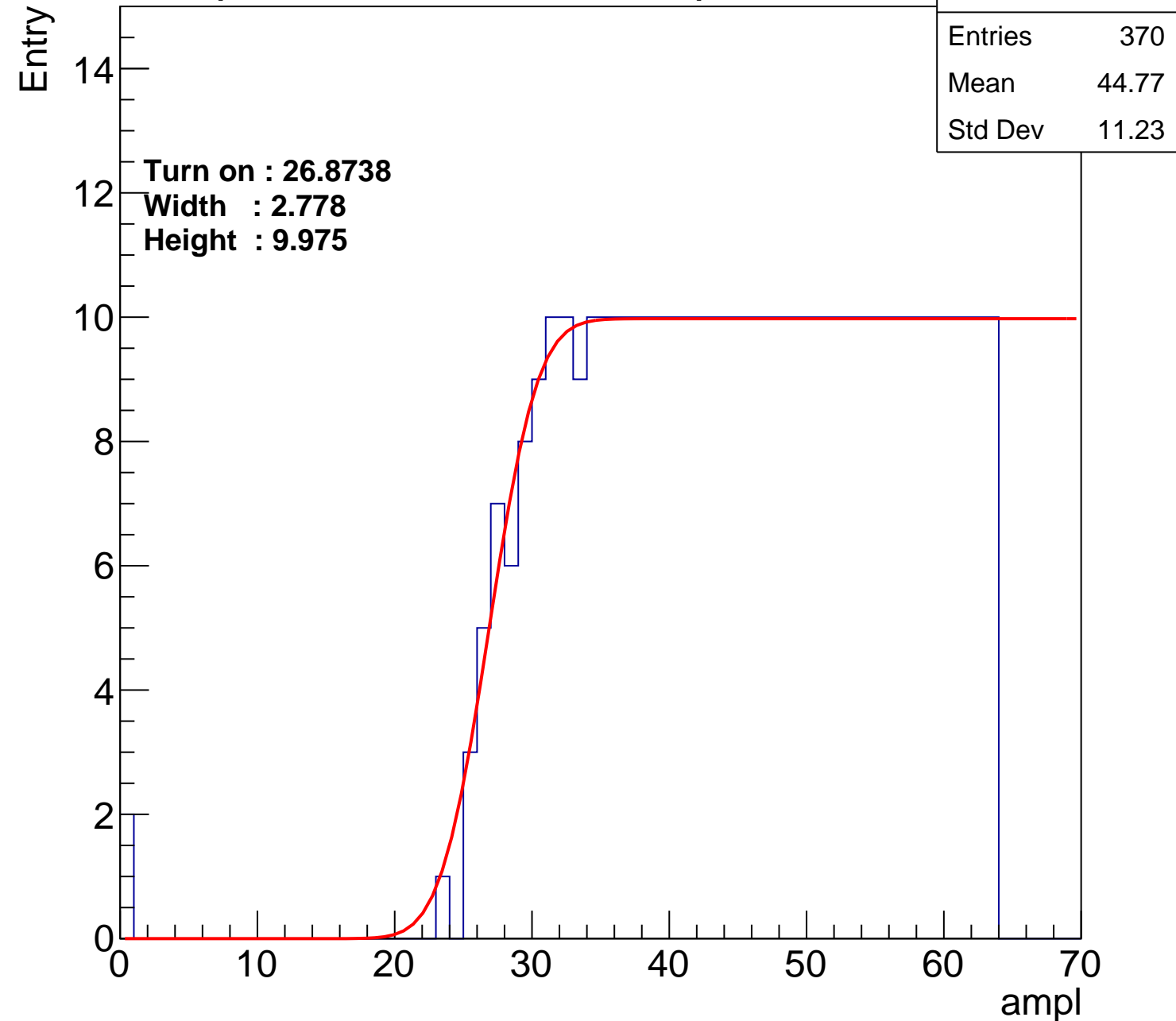
Width : 2.778

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch48

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.24 |
| Std Dev | 11.37 |

Turn on : 25.9973

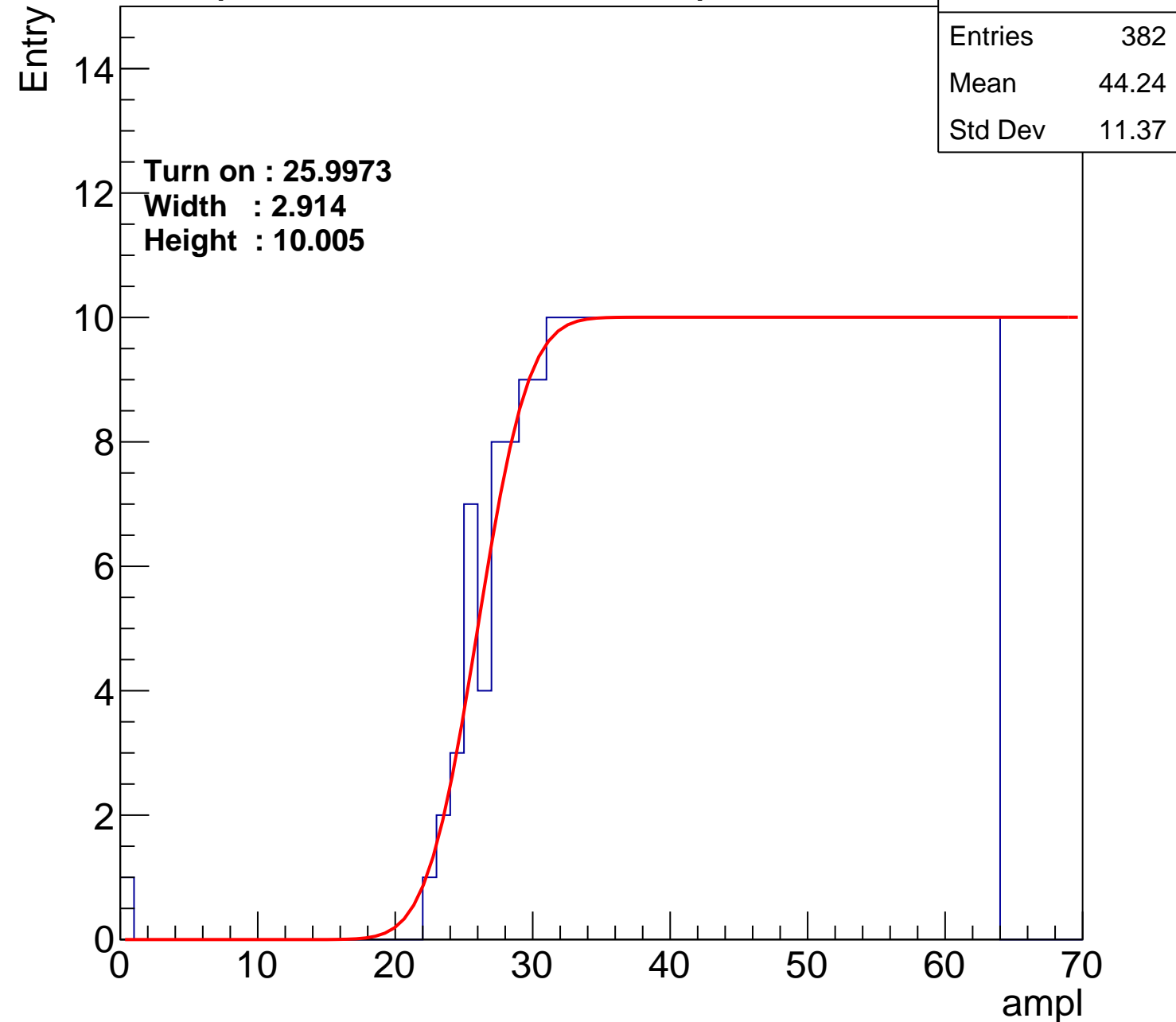
Width : 2.914

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch49

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.95 |
| Std Dev | 11.65 |

Turn on : 25.7362

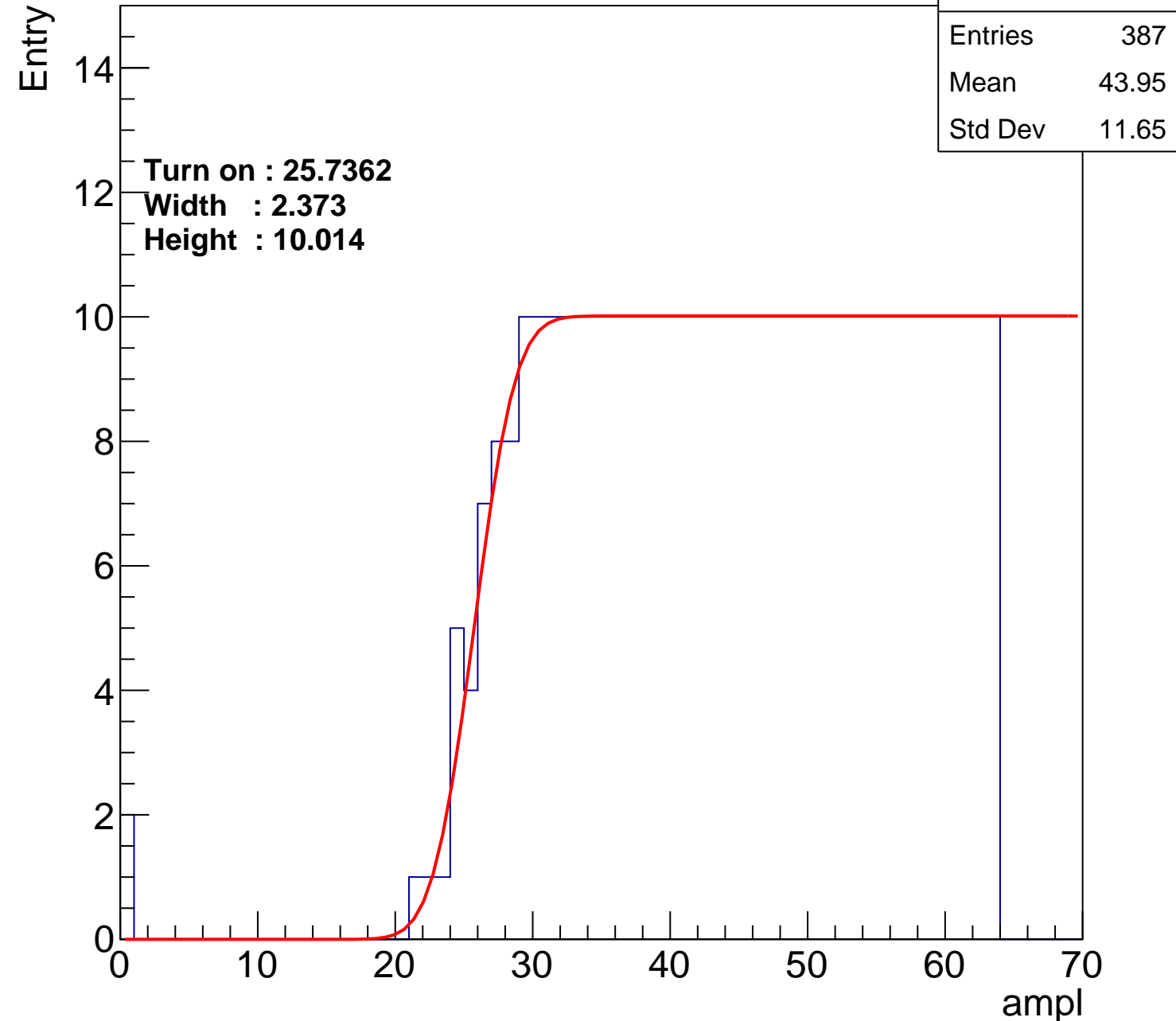
Width : 2.373

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch50

calib_packv5_042523_0143.root, FC#3, port B3

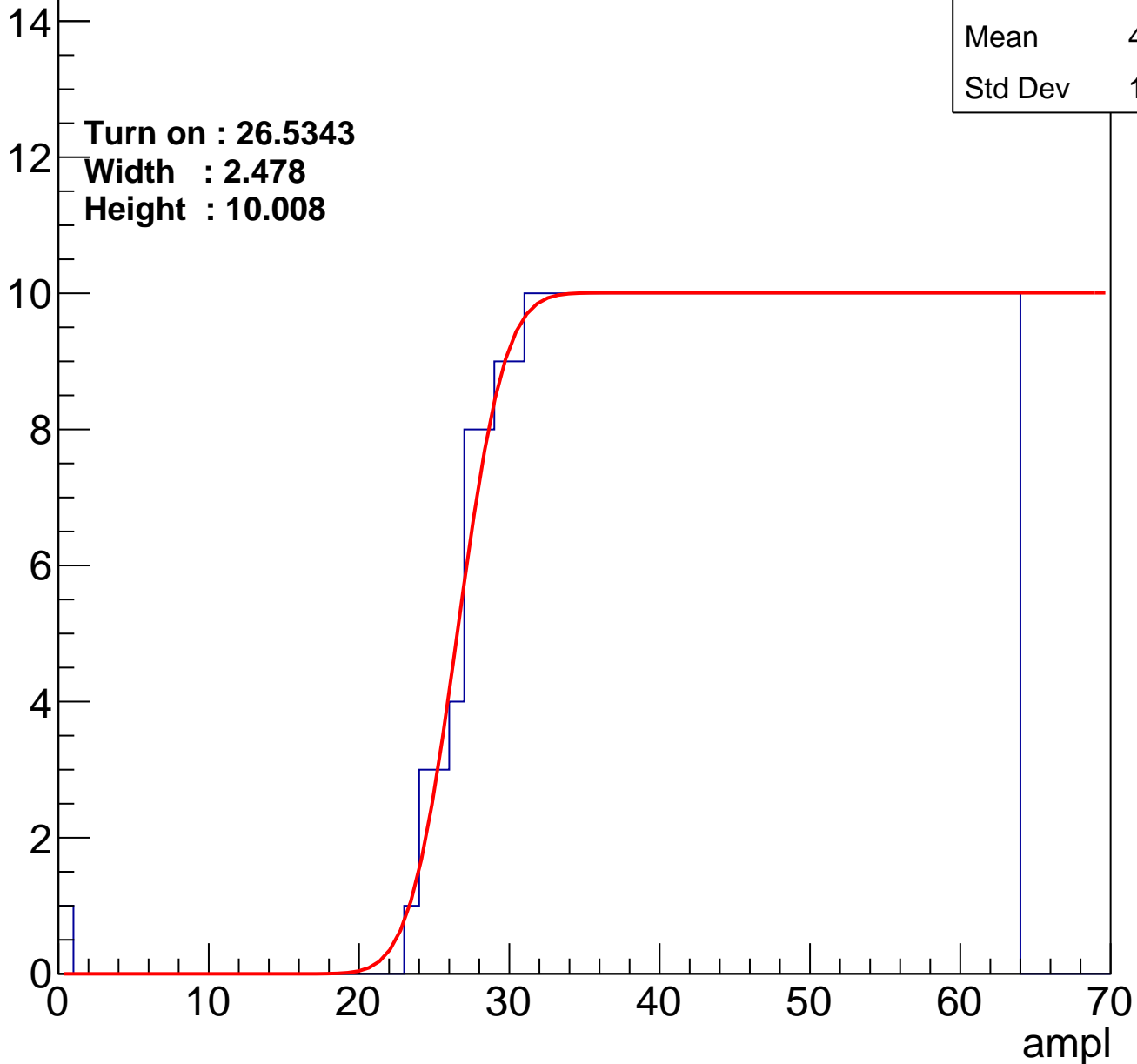
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.56 |
| Std Dev | 11.17 |

Turn on : 26.5343

Width : 2.478

Height : 10.008

Entry



B1L000S, U26-ch51

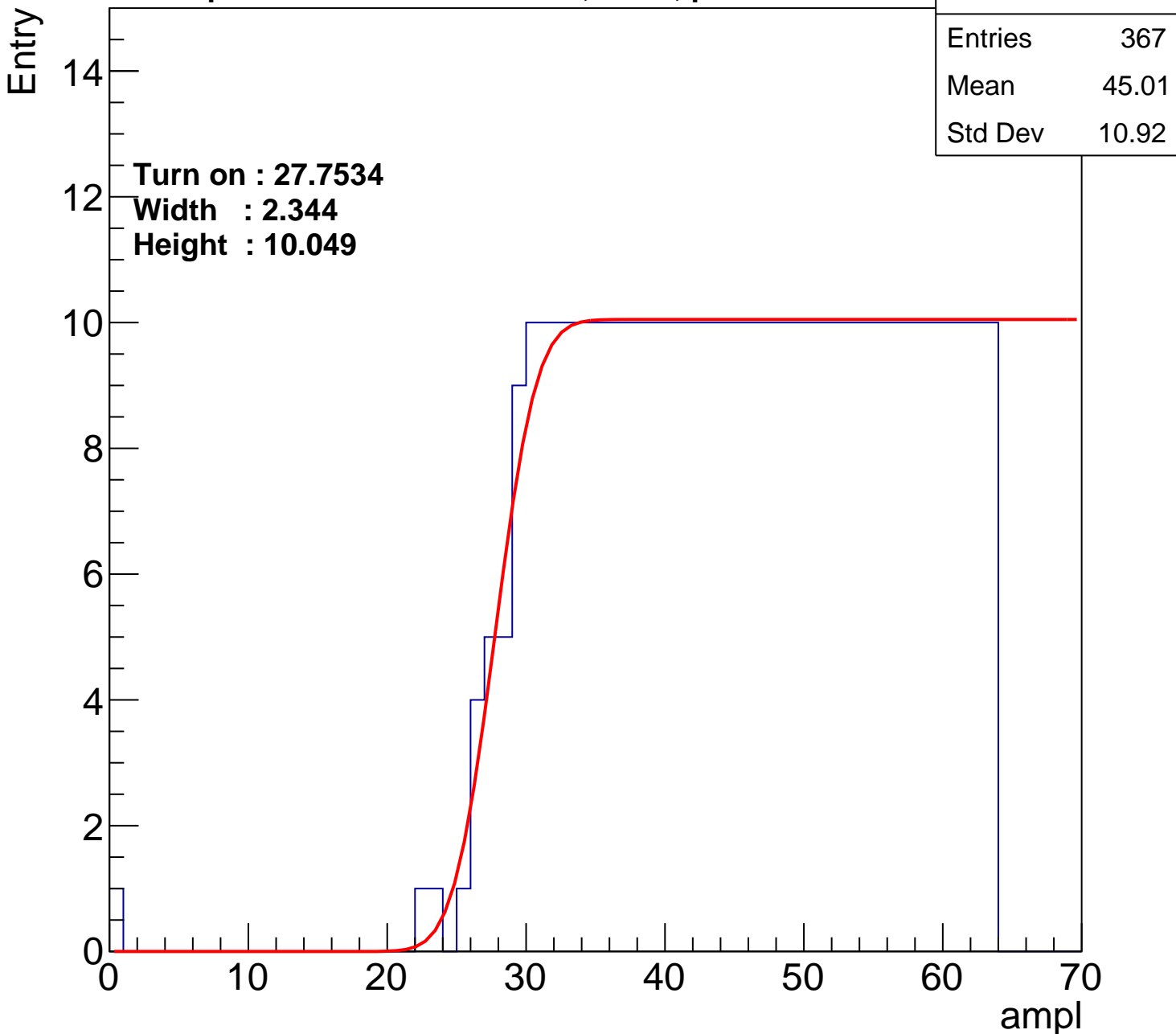
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 27.7534

Width : 2.344

Height : 10.049

| | |
|---------|-------|
| Entries | 367 |
| Mean | 45.01 |
| Std Dev | 10.92 |



B1L000S, U26-ch52

calib_packv5_042523_0143.root, FC#3, port B3

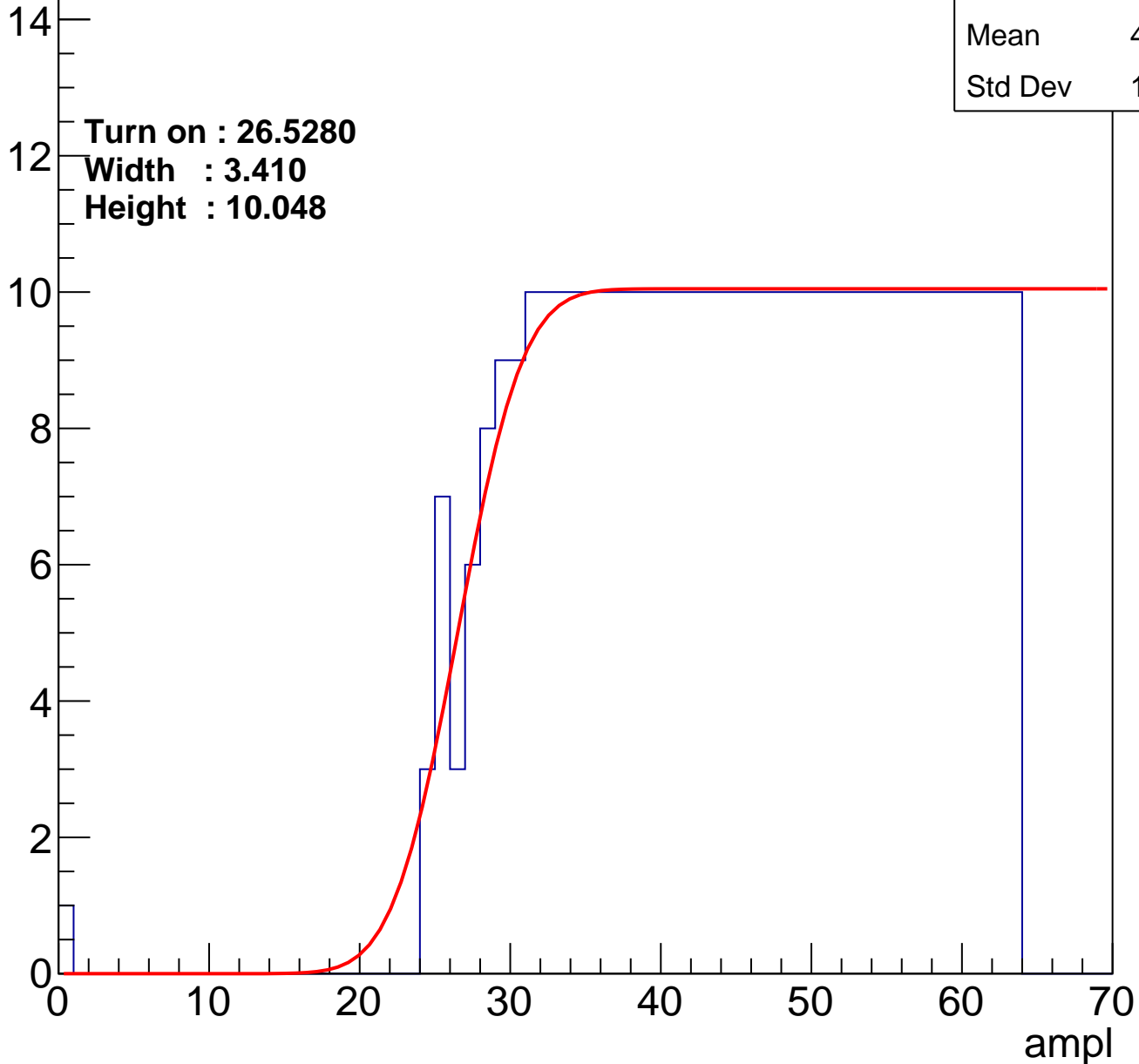
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.55 |
| Std Dev | 11.18 |

Turn on : 26.5280

Width : 3.410

Height : 10.048

Entry



B1L000S, U26-ch53

calib_packv5_042523_0143.root, FC#3, port B3

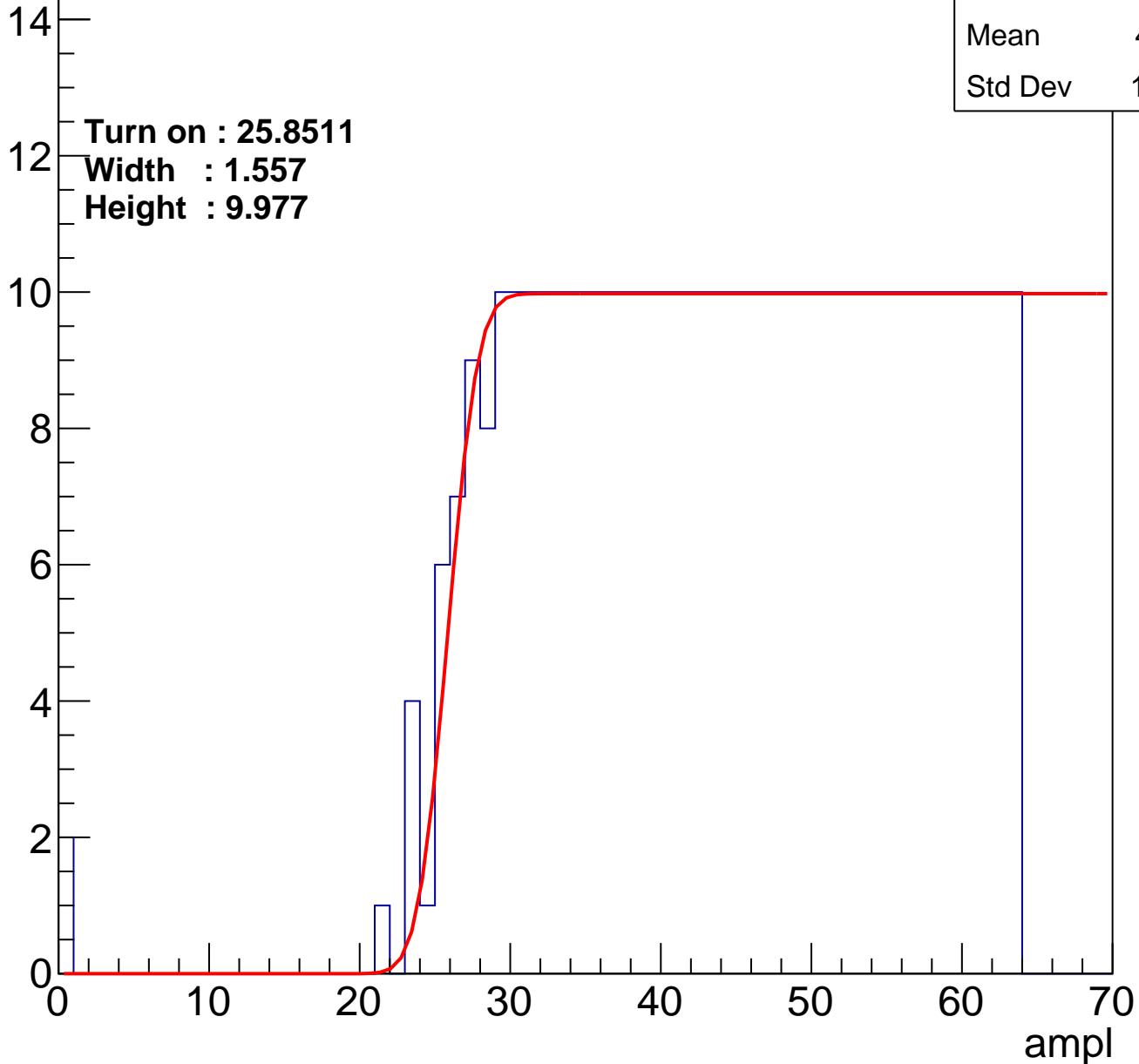
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.91 |
| Std Dev | 11.66 |

Turn on : 25.8511

Width : 1.557

Height : 9.977

Entry



B1L000S, U26-ch54

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|------|
| Entries | 382 |
| Mean | 44.2 |
| Std Dev | 11.5 |

Turn on : 25.9928

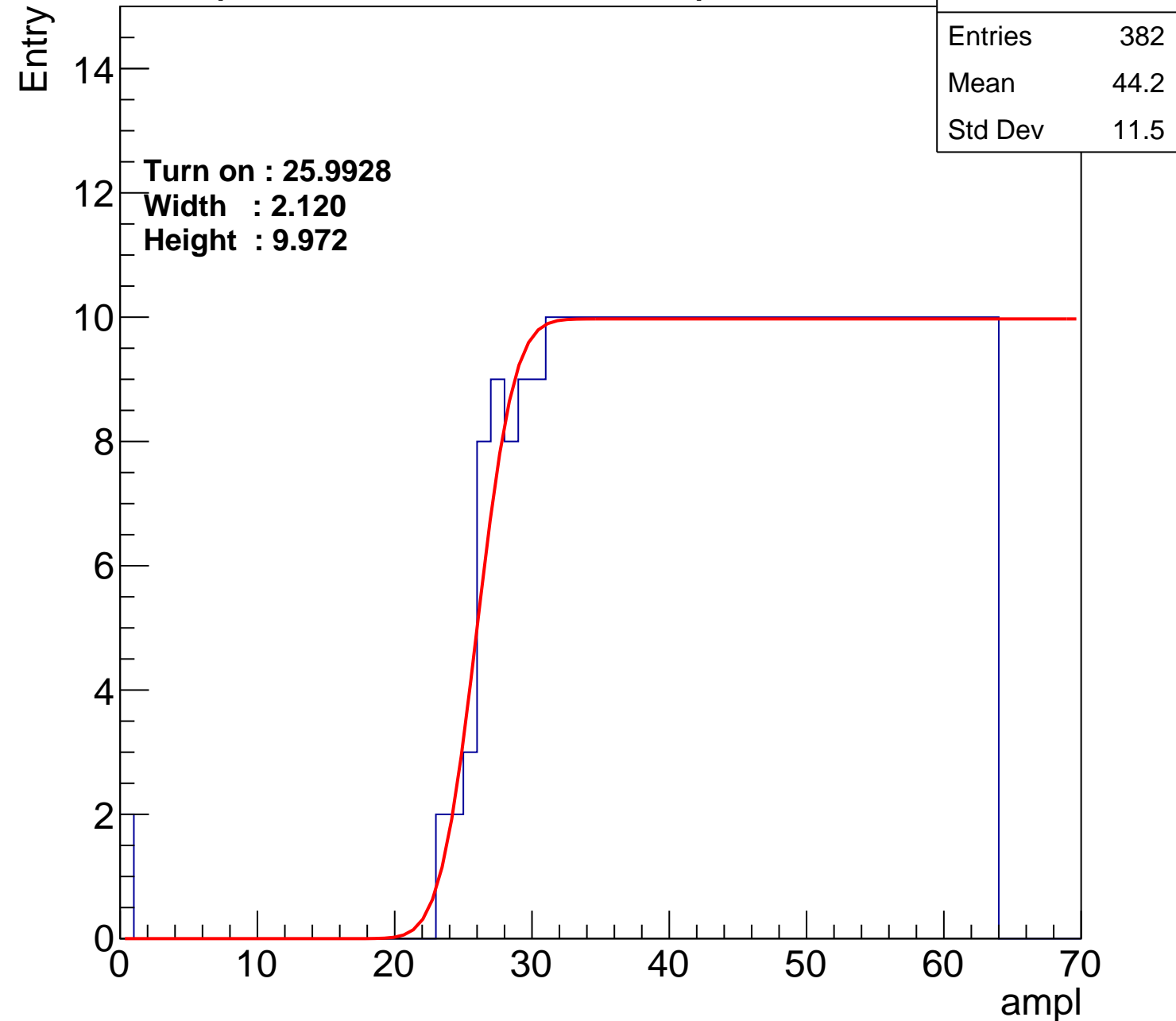
Width : 2.120

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch55

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.74 |
| Std Dev | 11.04 |

Turn on : 26.9632

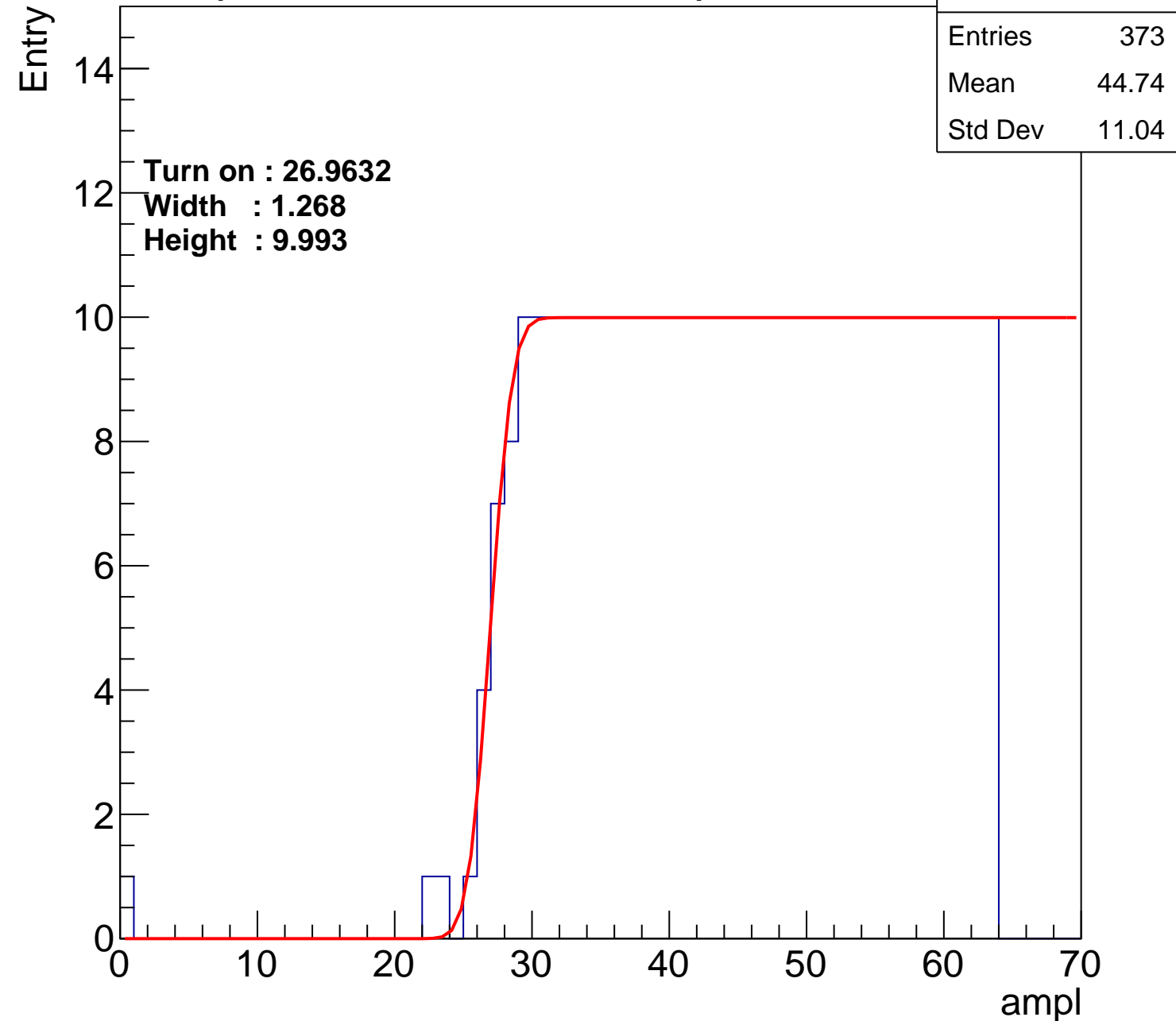
Width : 1.268

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch56

calib_packv5_042523_0143.root, FC#3, port B3

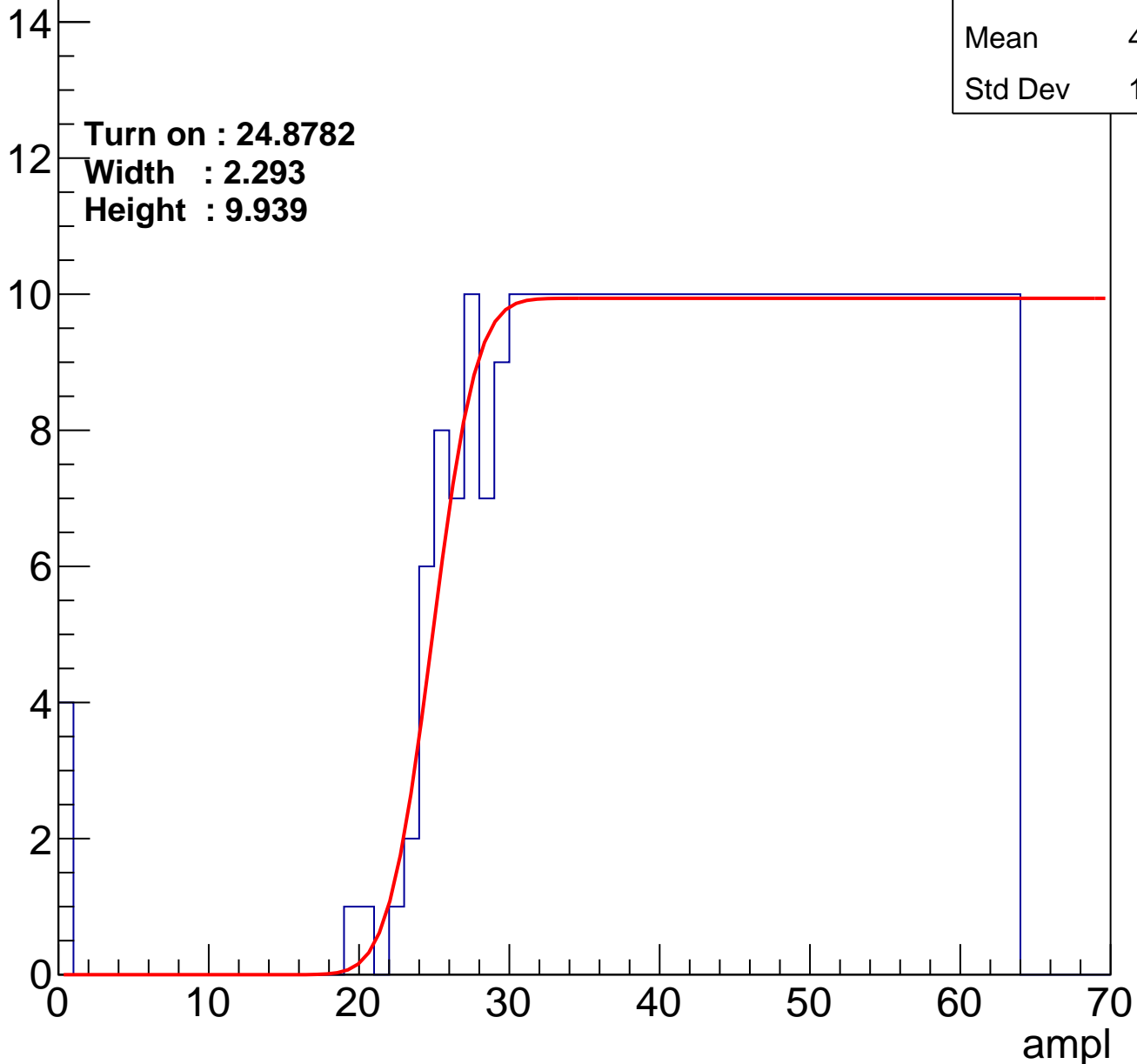
| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.36 |
| Std Dev | 12.23 |

Turn on : 24.8782

Width : 2.293

Height : 9.939

Entry



B1L000S, U26-ch57

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 363 |
| Mean | 44.99 |
| Std Dev | 11.36 |

Turn on : 28.2762

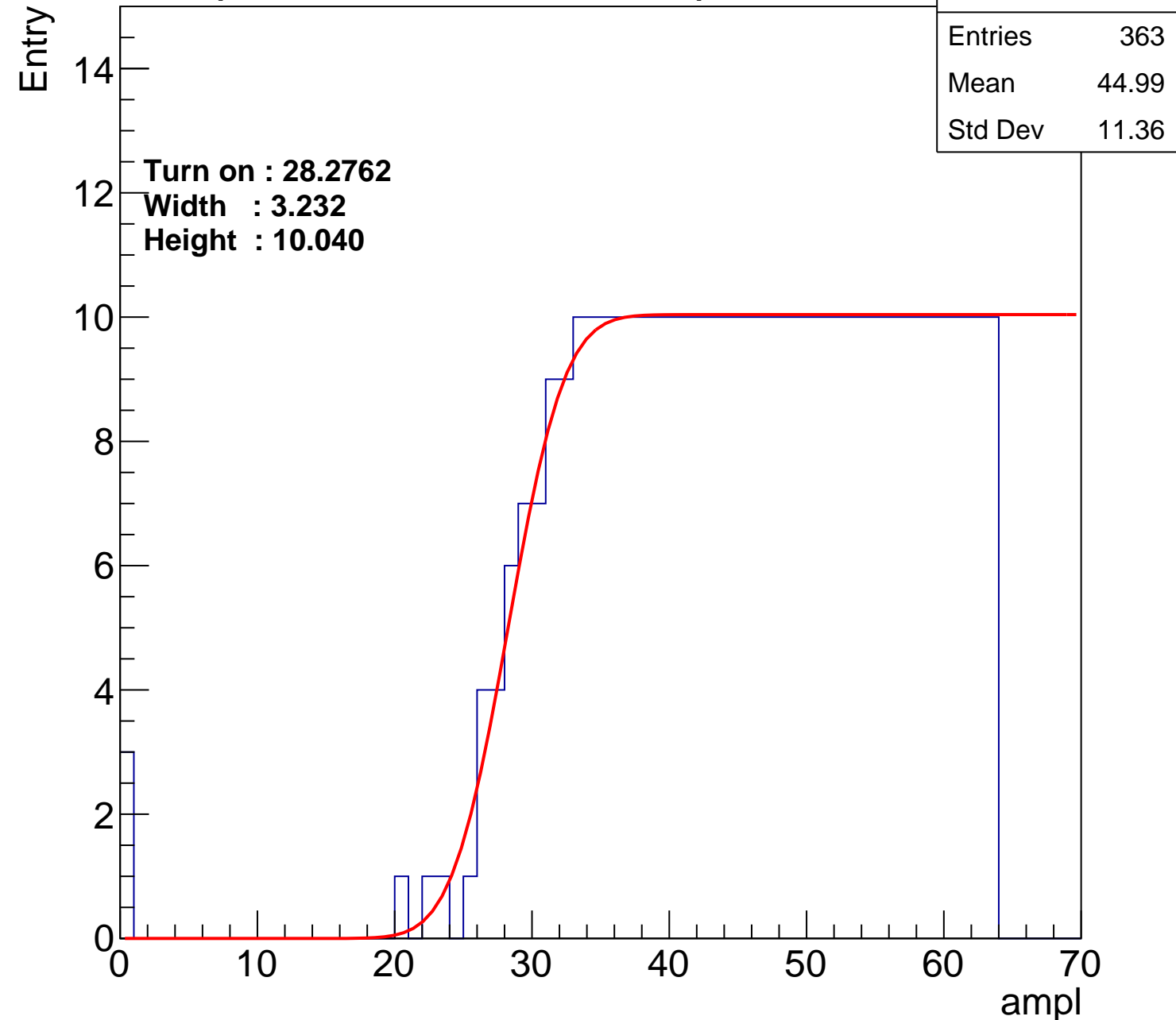
Width : 3.232

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch58

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.67 |
| Std Dev | 11.29 |

Turn on : 27.2242

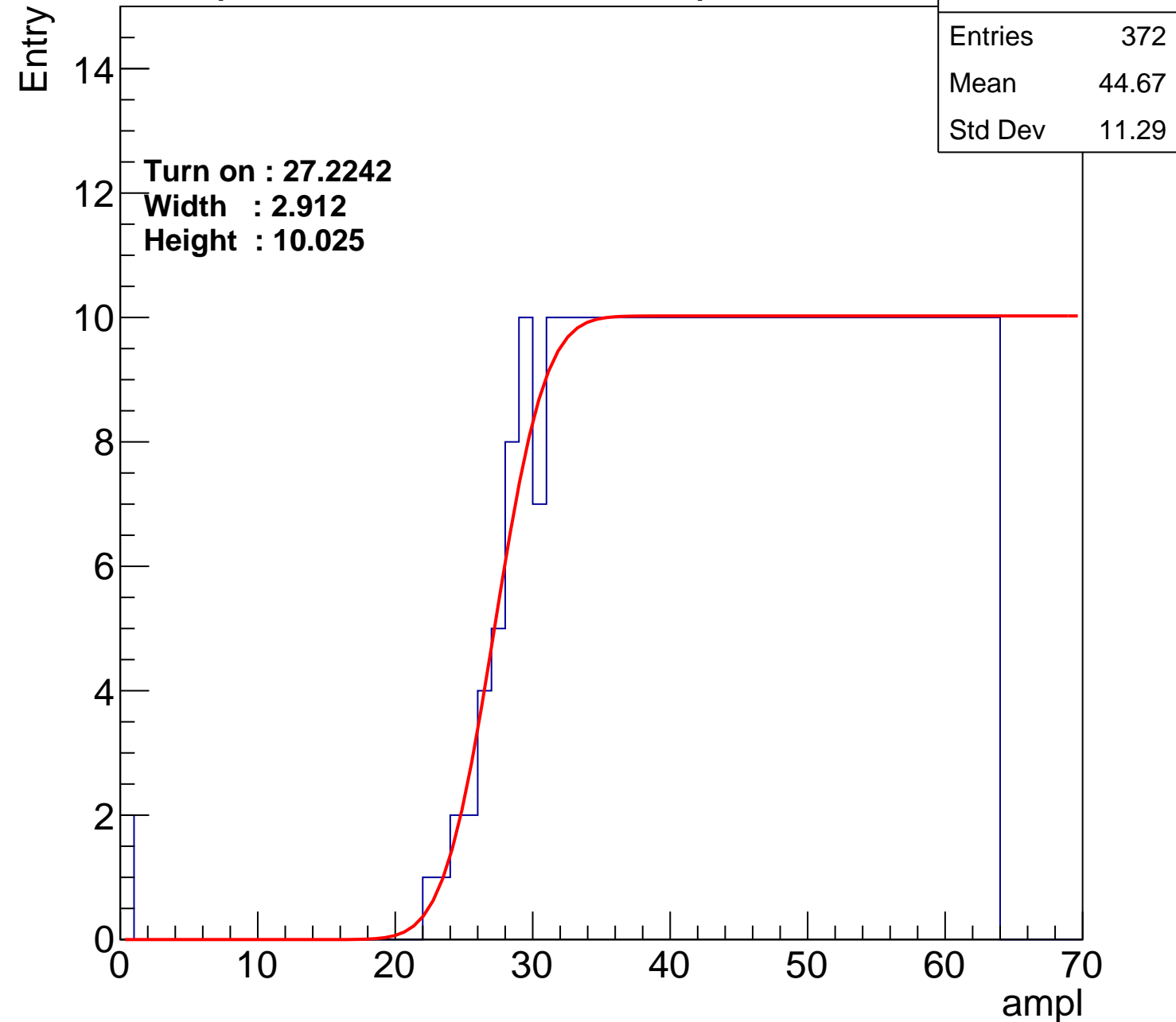
Width : 2.912

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl

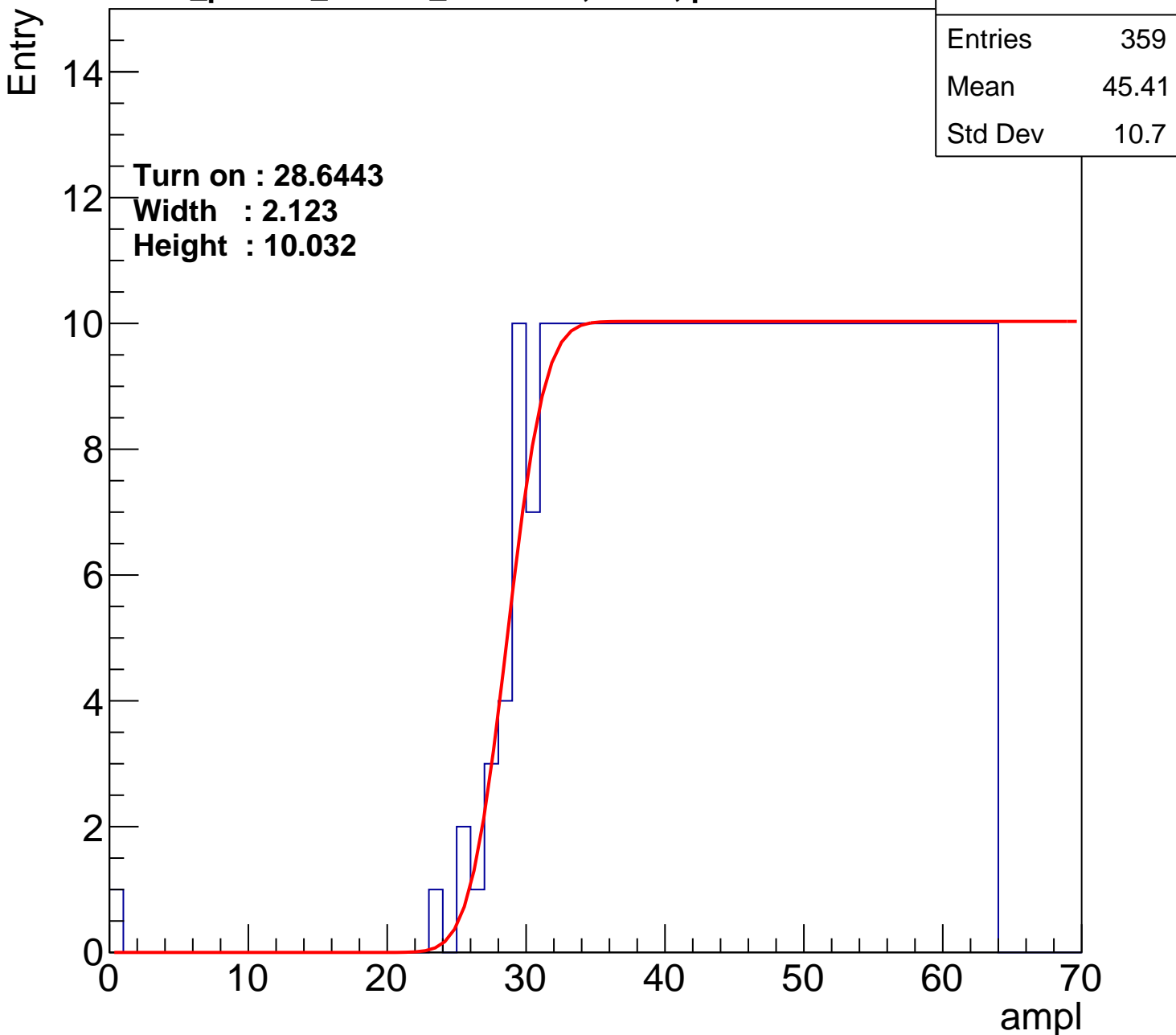


calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.41 |
| Std Dev | 10.7 |

Height : 10.032



B1L000S, U26-ch60

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.64 |
| Std Dev | 11.28 |

Turn on : 27.4457

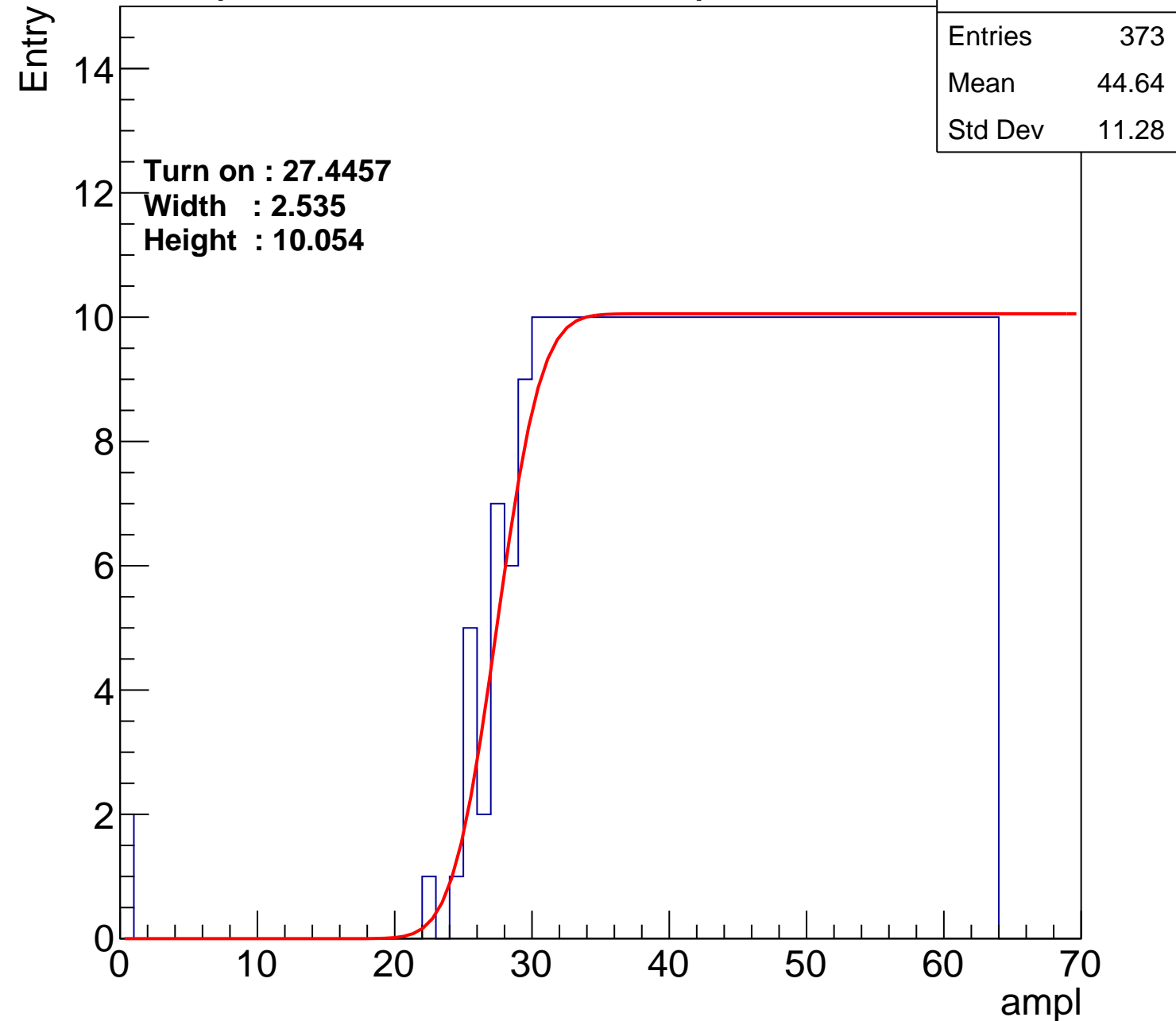
Width : 2.535

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch61

calib_packv5_042523_0143.root, FC#3, port B3

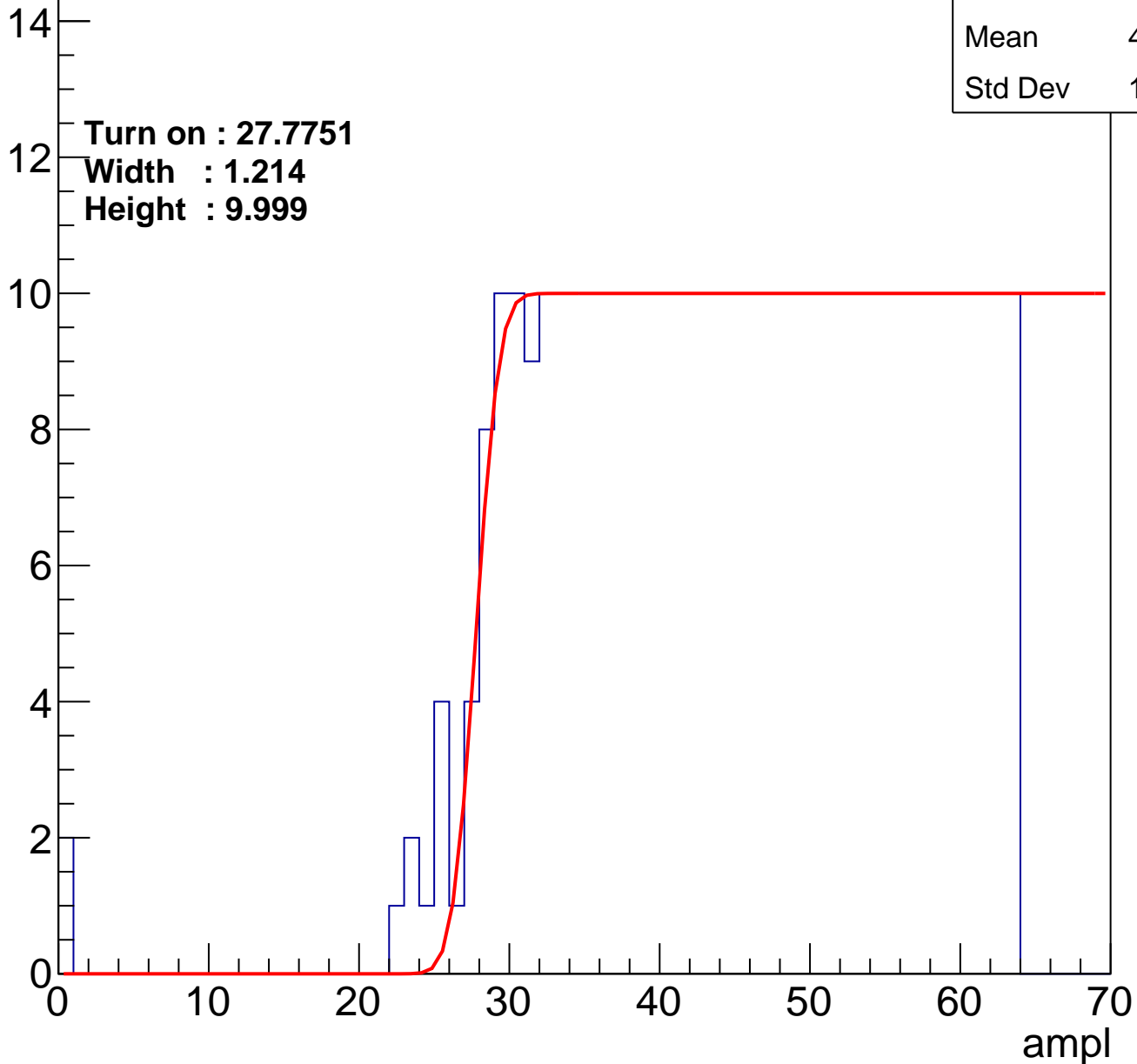
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.67 |
| Std Dev | 11.28 |

Turn on : 27.7751

Width : 1.214

Height : 9.999

Entry



B1L000S, U26-ch62

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.02 |
| Std Dev | 11.77 |

Turn on : 25.6037

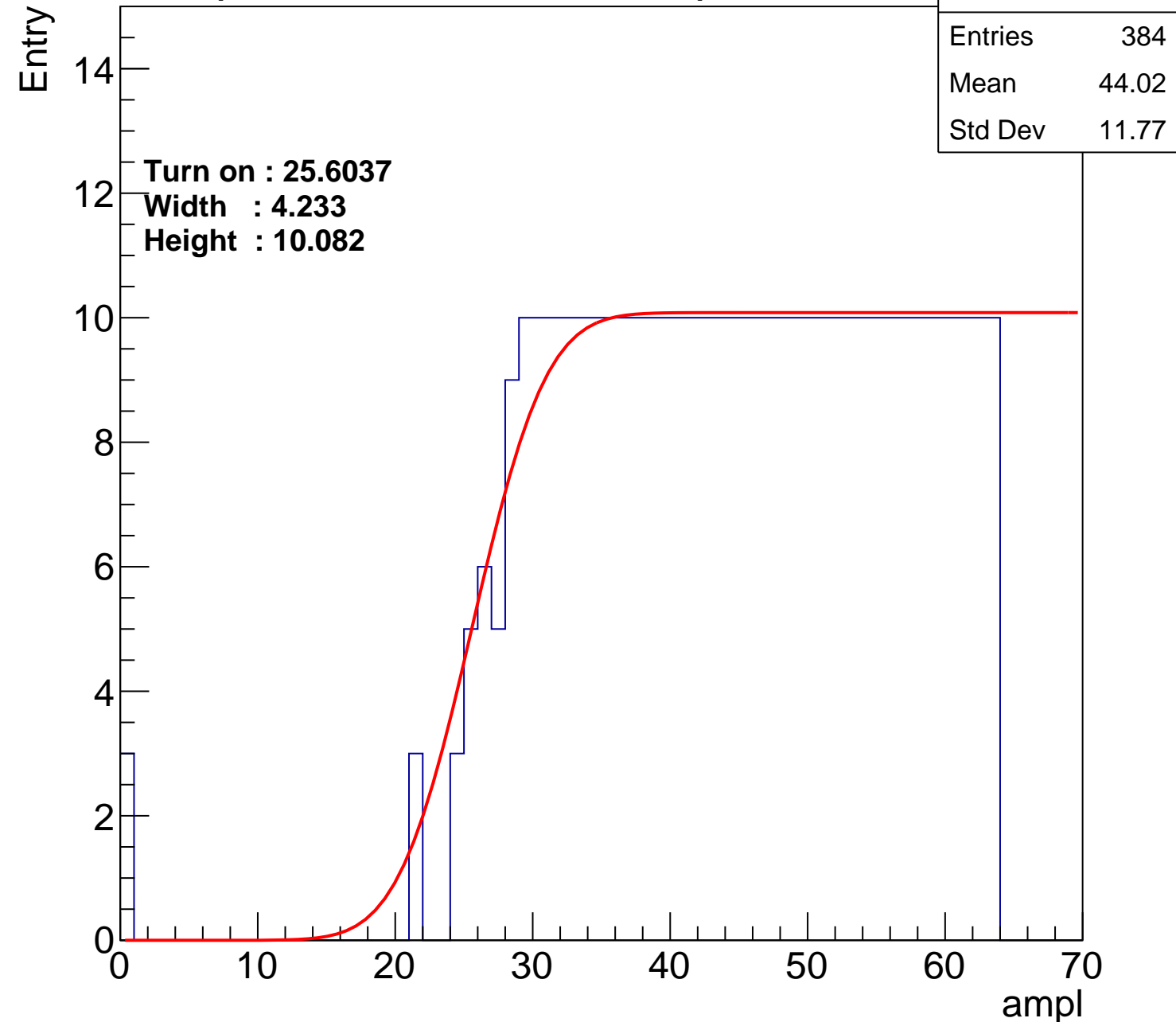
Width : 4.233

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch63

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.84 |
| Std Dev | 11.56 |

Turn on : 28.1739

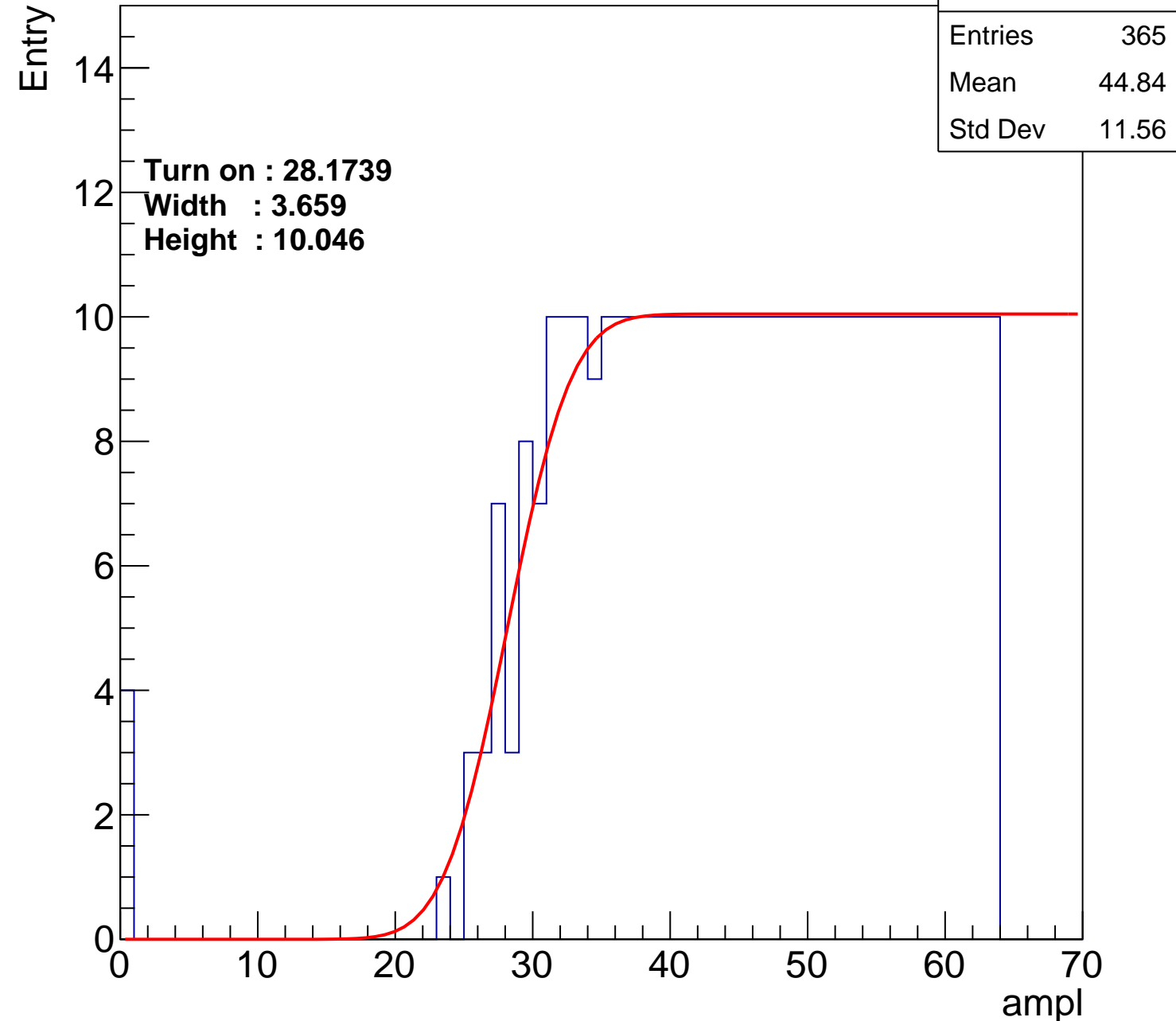
Width : 3.659

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch64

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.92 |
| Std Dev | 11.1 |

Turn on : 27.4292

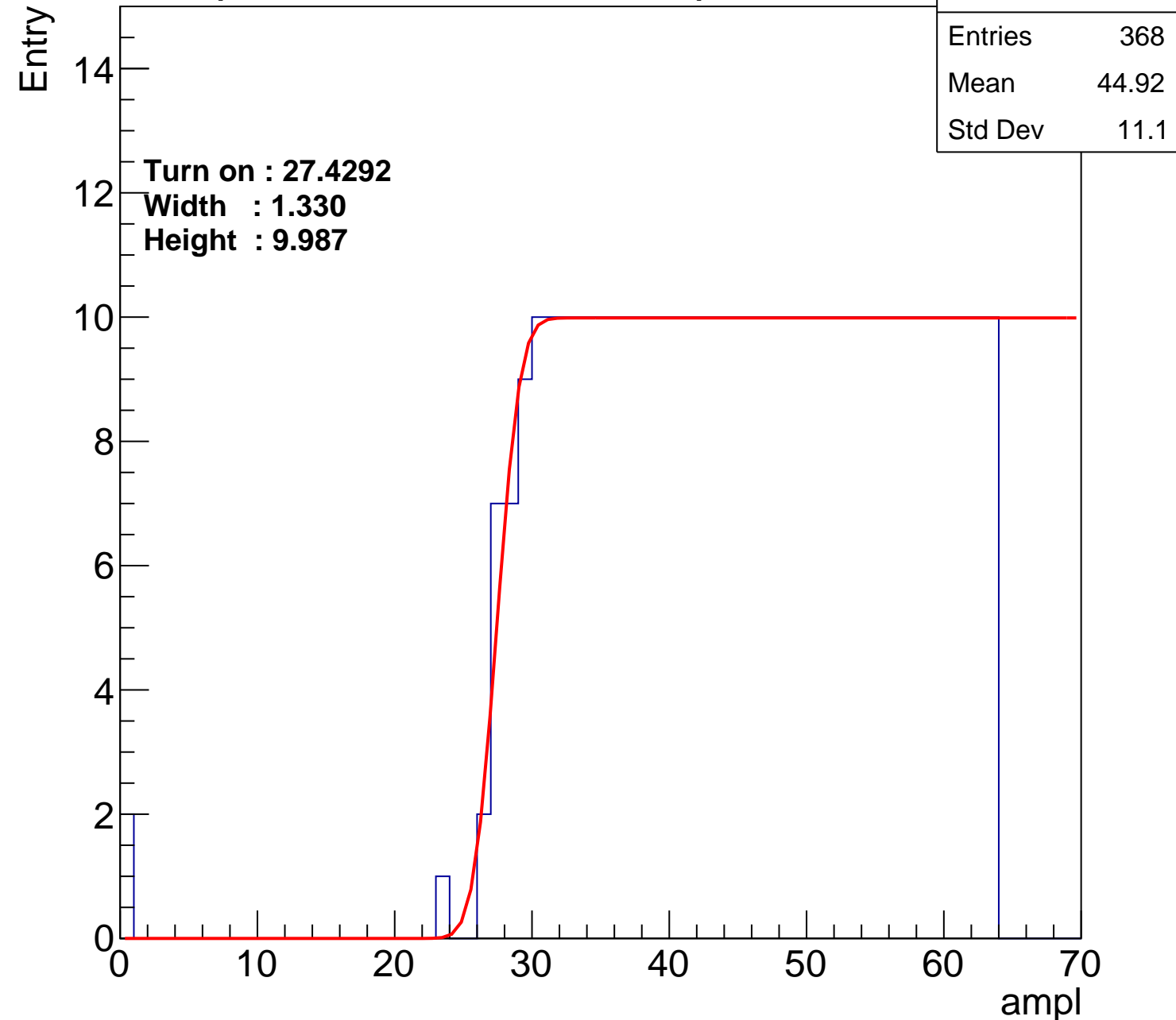
Width : 1.330

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch65

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.06 |
| Std Dev | 10.9 |

Turn on : 27.7474

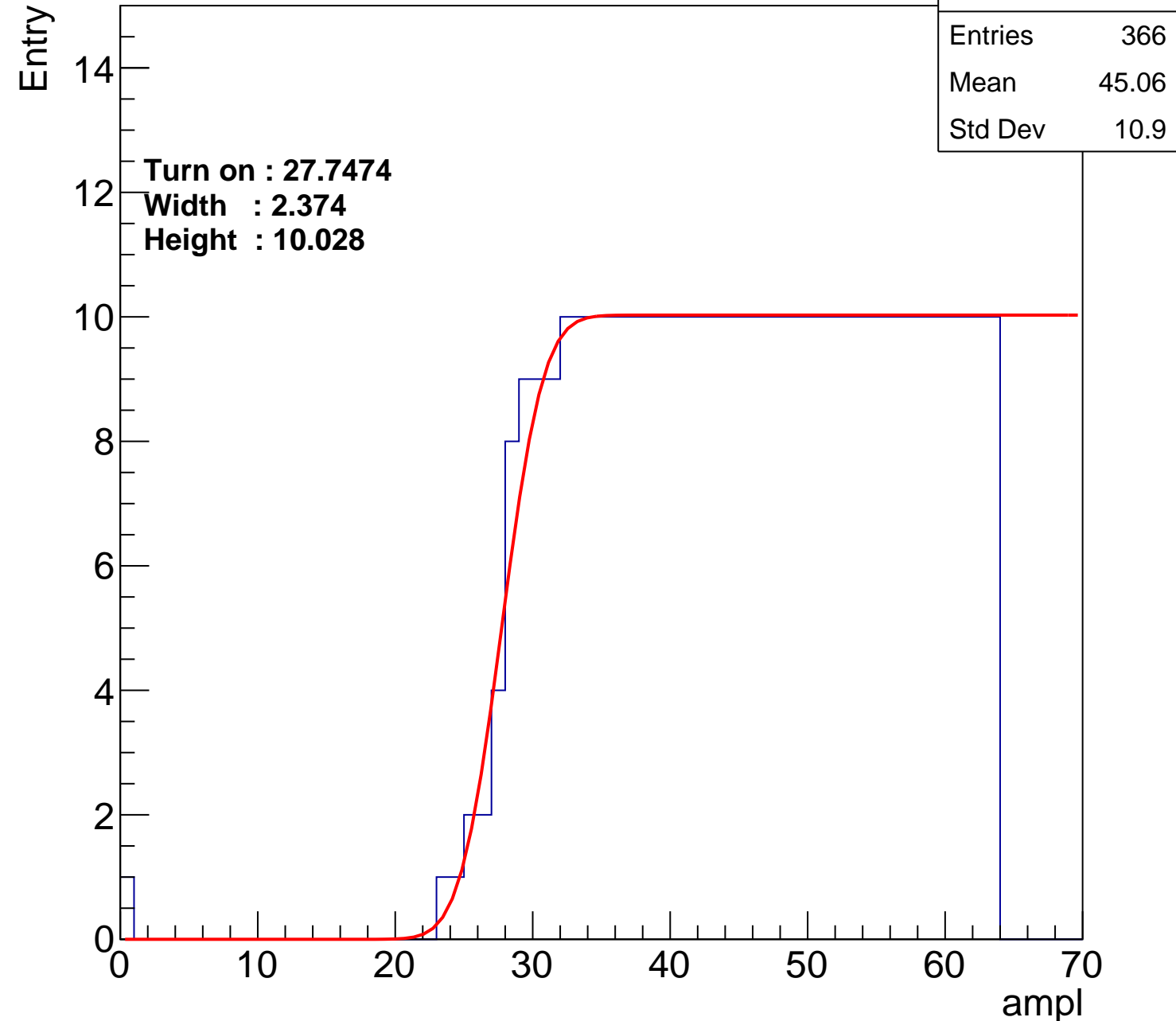
Width : 2.374

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch66

calib_packv5_042523_0143.root, FC#3, port B3

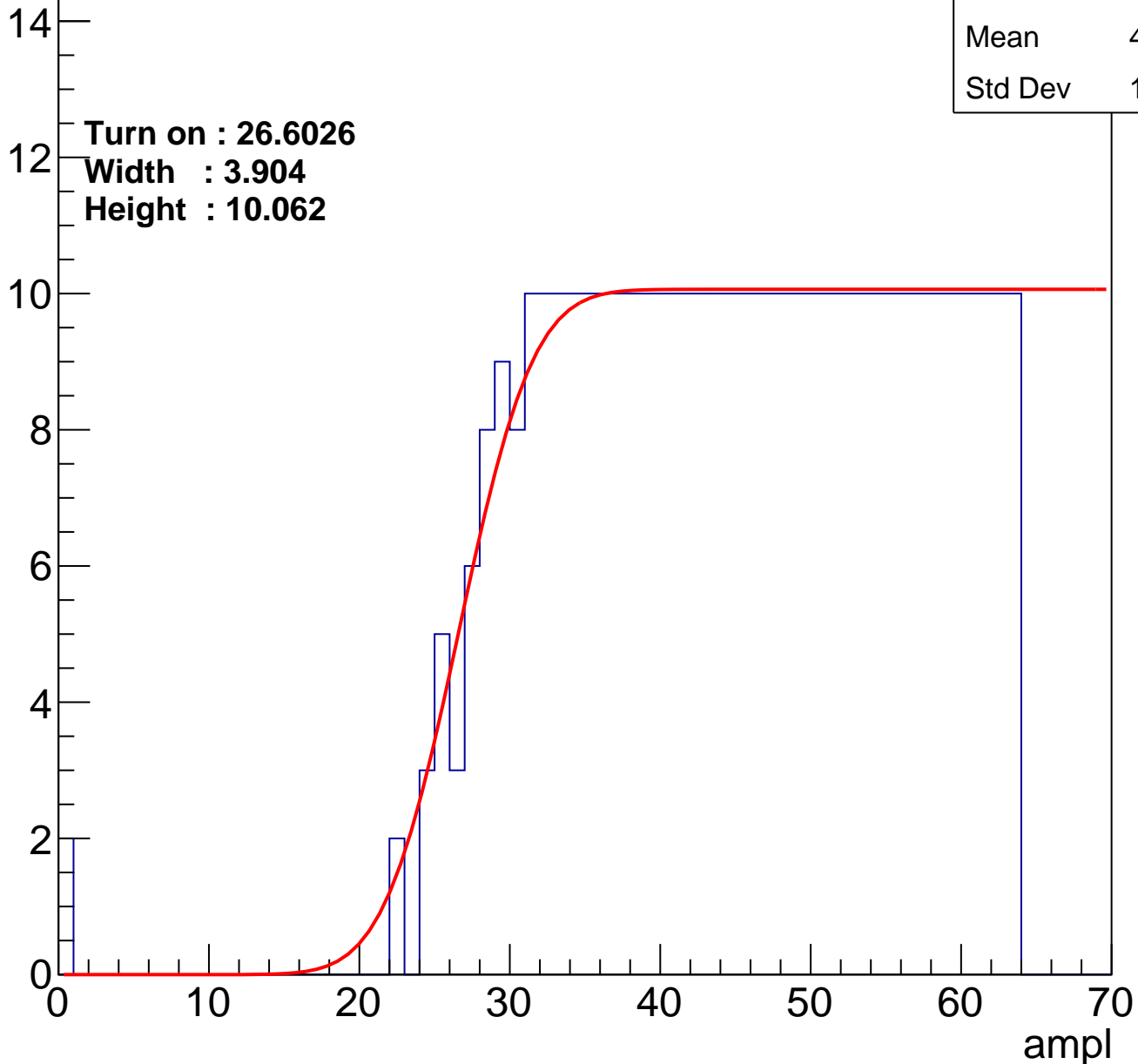
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.46 |
| Std Dev | 11.42 |

Turn on : 26.6026

Width : 3.904

Height : 10.062

Entry



B1L000S, U26-ch67

calib_packv5_042523_0143.root, FC#3, port B3

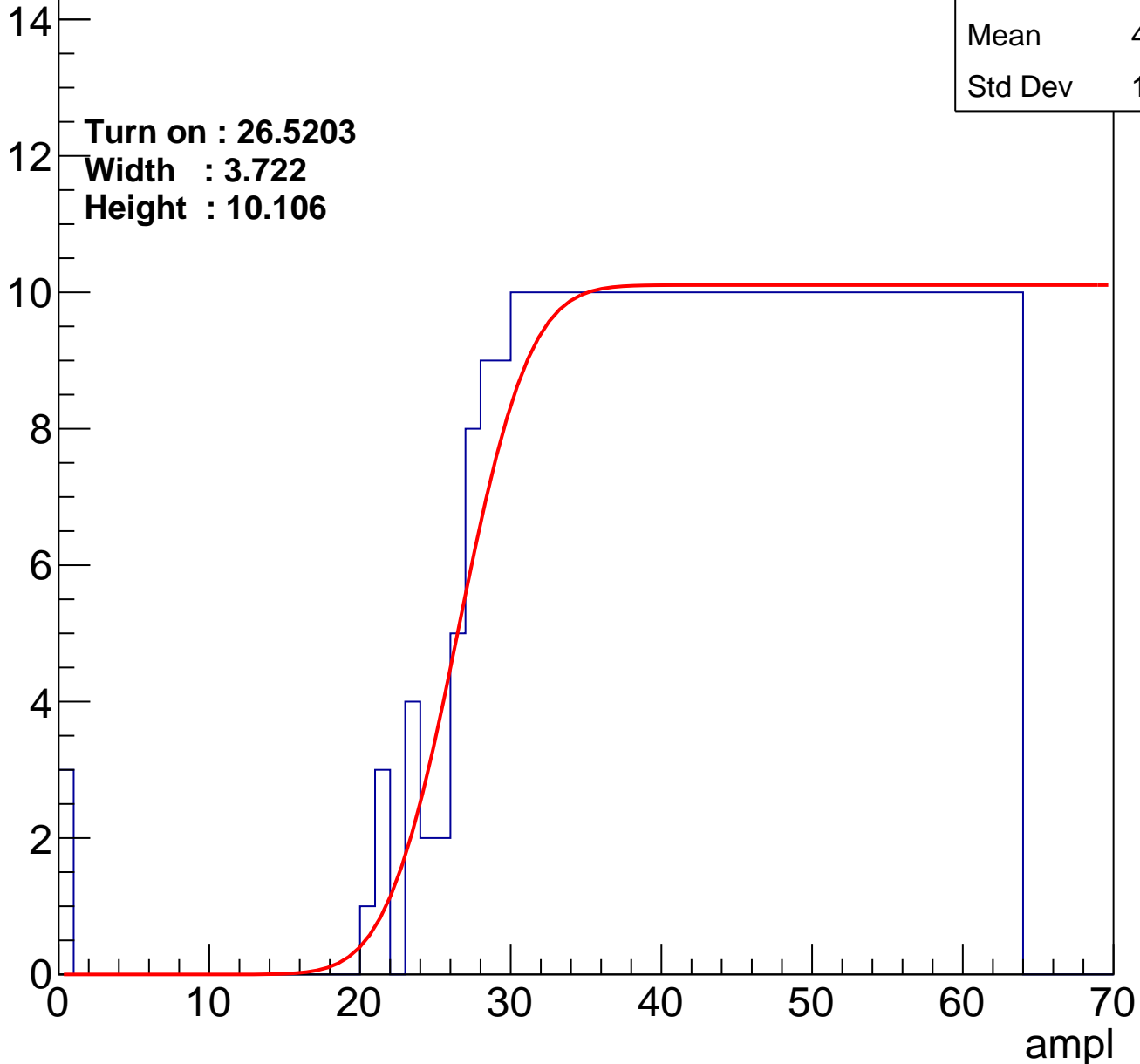
| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.89 |
| Std Dev | 11.87 |

Turn on : 26.5203

Width : 3.722

Height : 10.106

Entry



B1L000S, U26-ch68

calib_packv5_042523_0143.root, FC#3, port B3

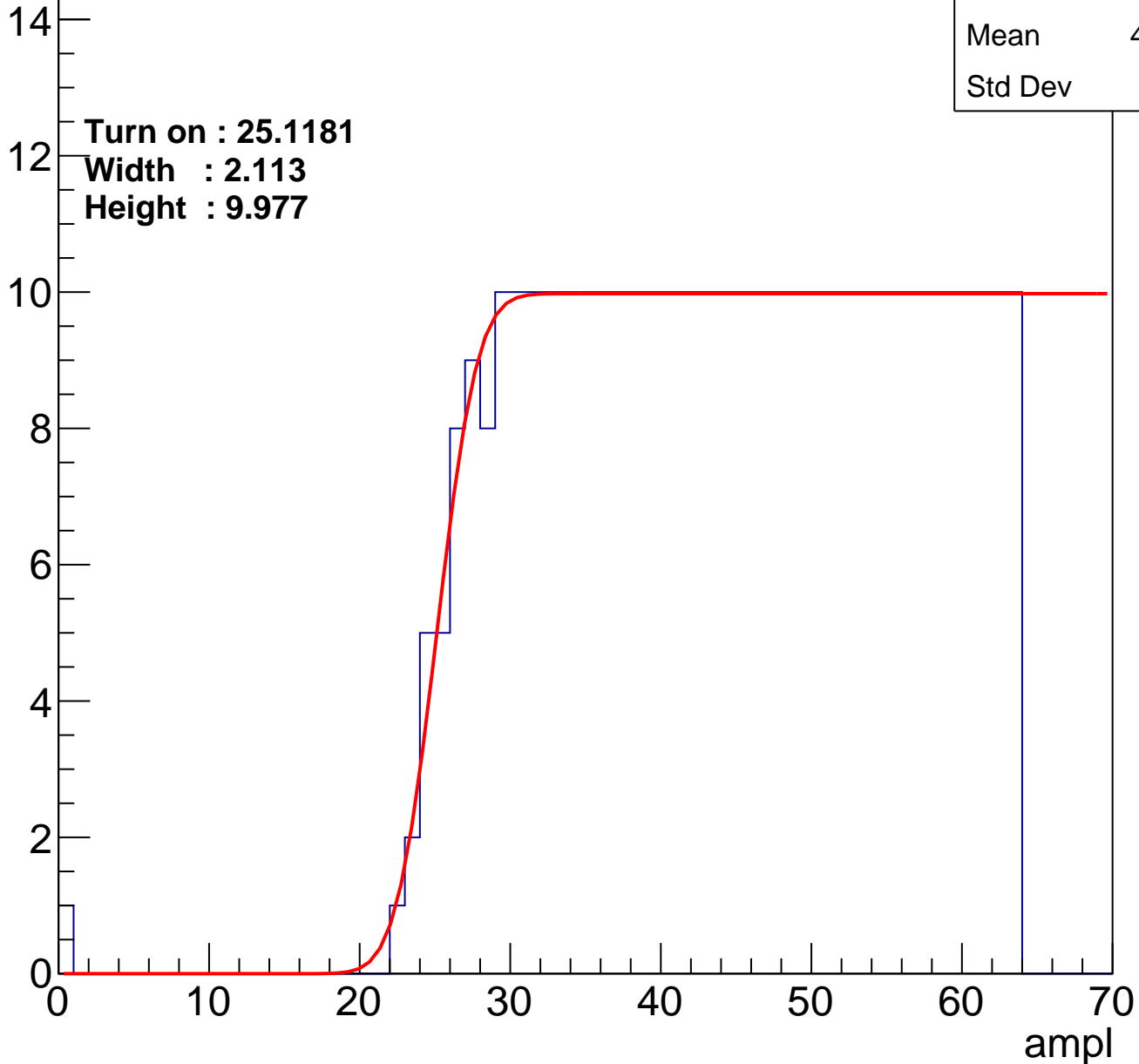
| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.93 |
| Std Dev | 11.5 |

Turn on : 25.1181

Width : 2.113

Height : 9.977

Entry



B1L000S, U26-ch69

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.67 |
| Std Dev | 11.1 |

Turn on : 26.5194

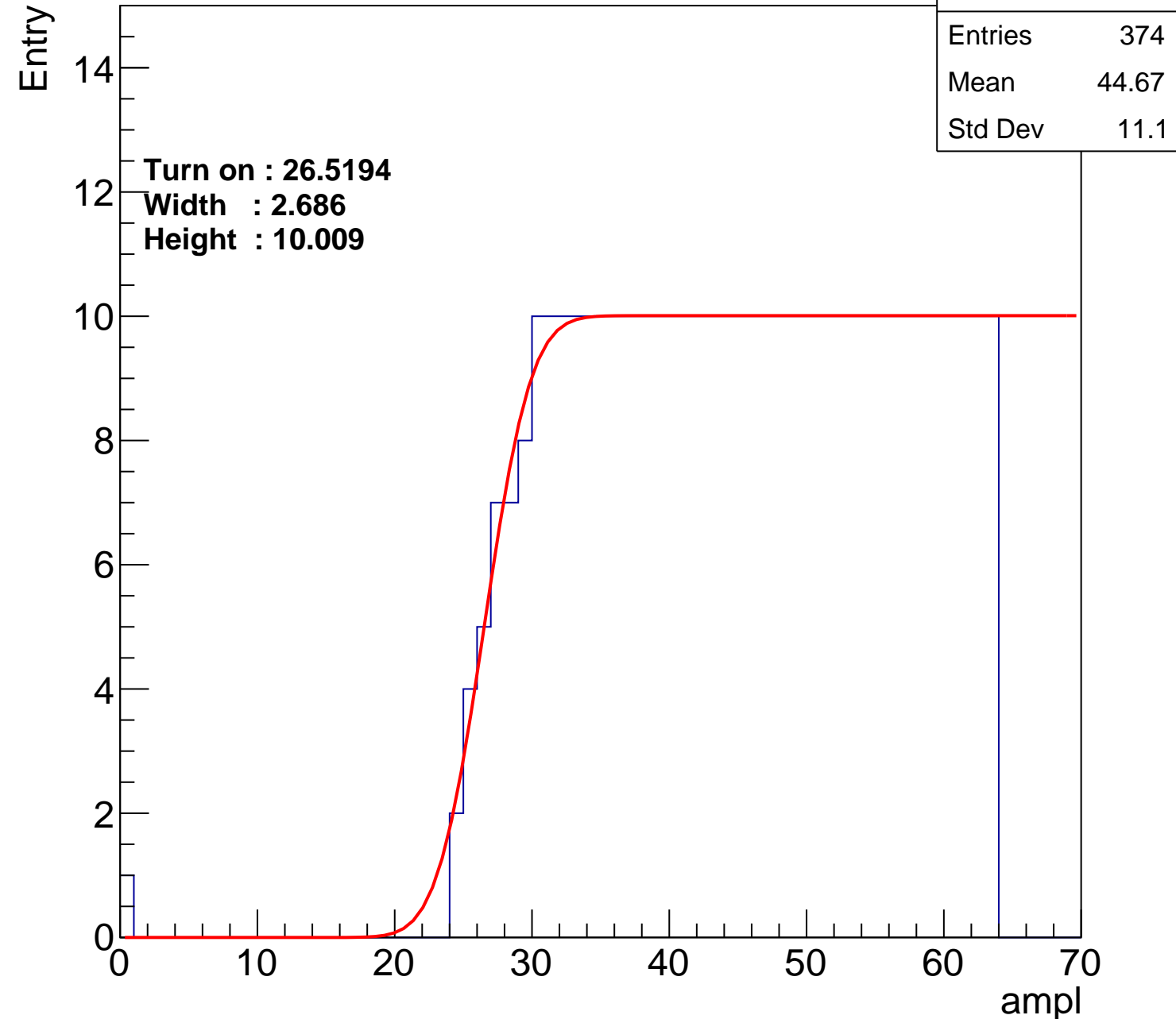
Width : 2.686

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch70

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.9 |
| Std Dev | 11.84 |

Turn on : 25.9924

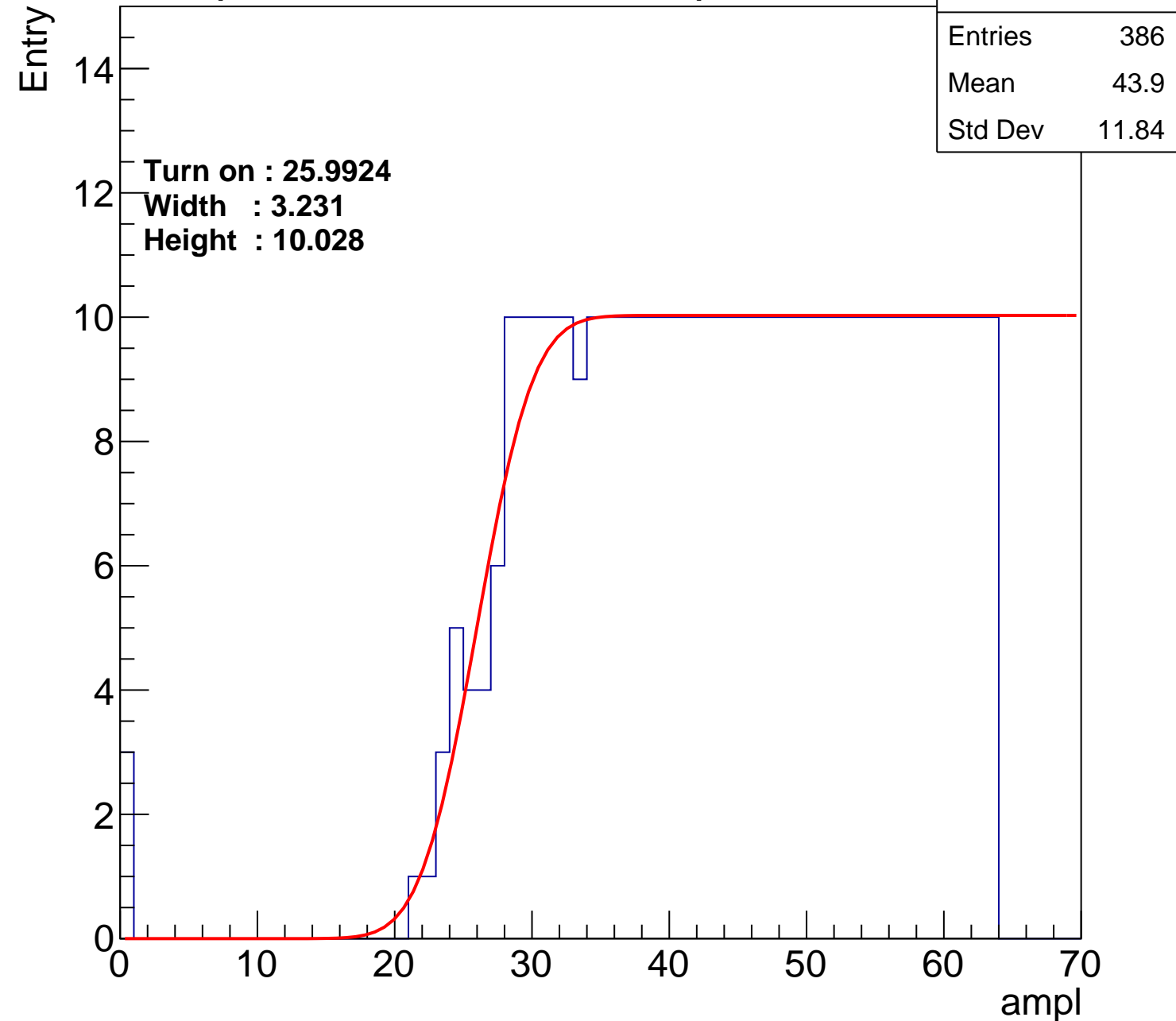
Width : 3.231

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch71

calib_packv5_042523_0143.root, FC#3, port B3

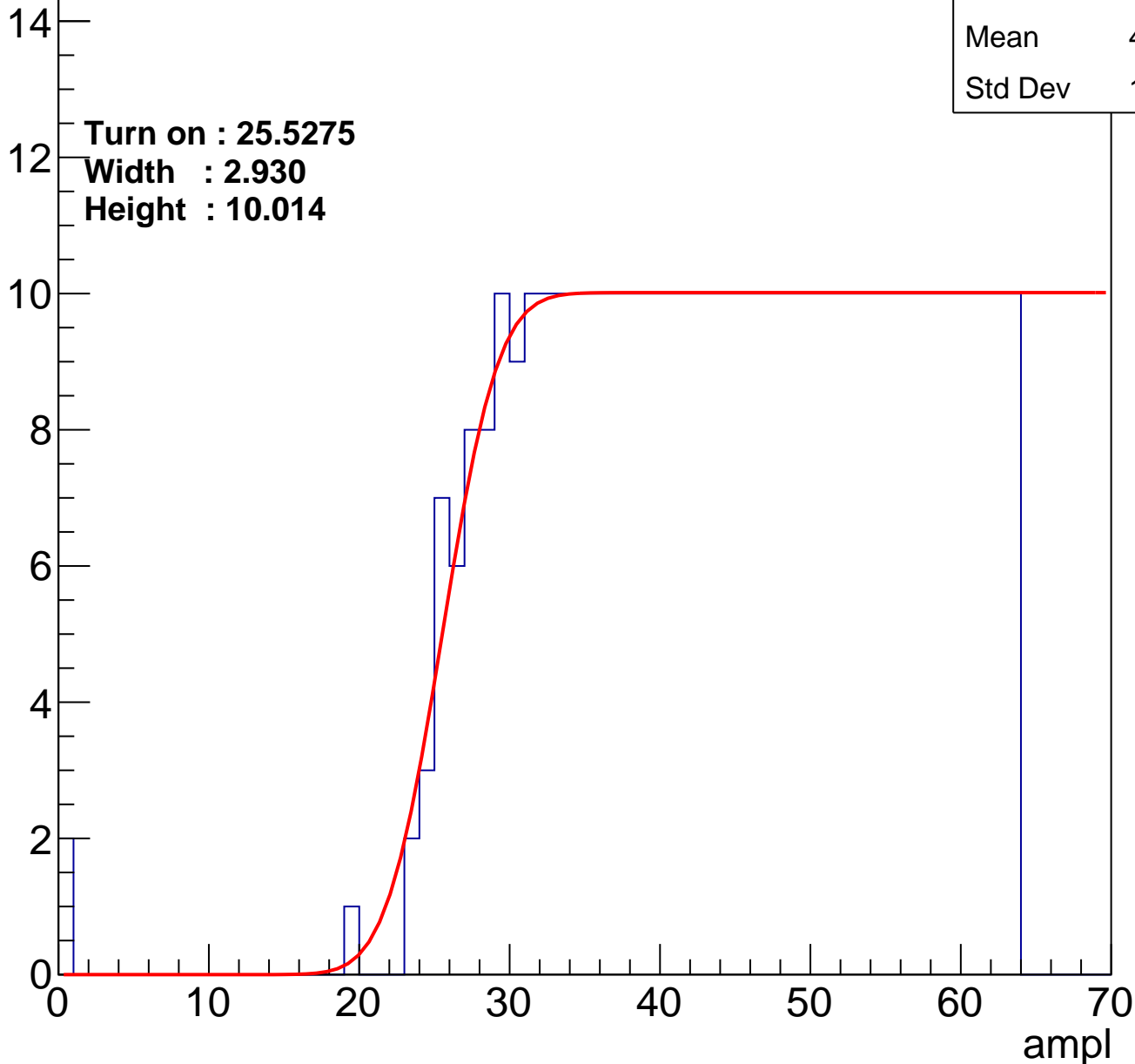
| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.98 |
| Std Dev | 11.64 |

Turn on : 25.5275

Width : 2.930

Height : 10.014

Entry



B1L000S, U26-ch72

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.21 |
| Std Dev | 11.17 |

Turn on : 28.2901

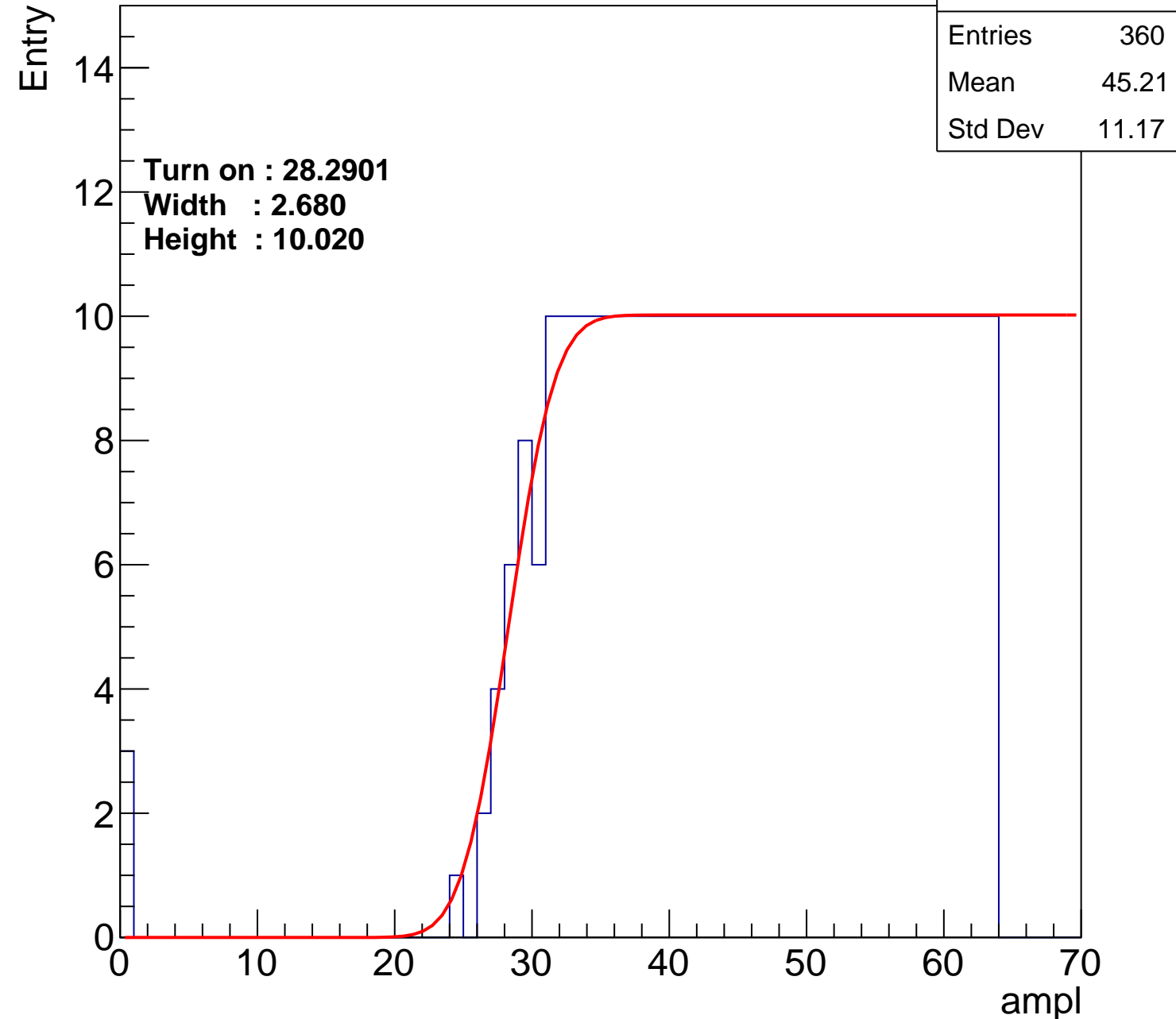
Width : 2.680

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch73

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.96 |
| Std Dev | 11.55 |

Turn on : 25.7820

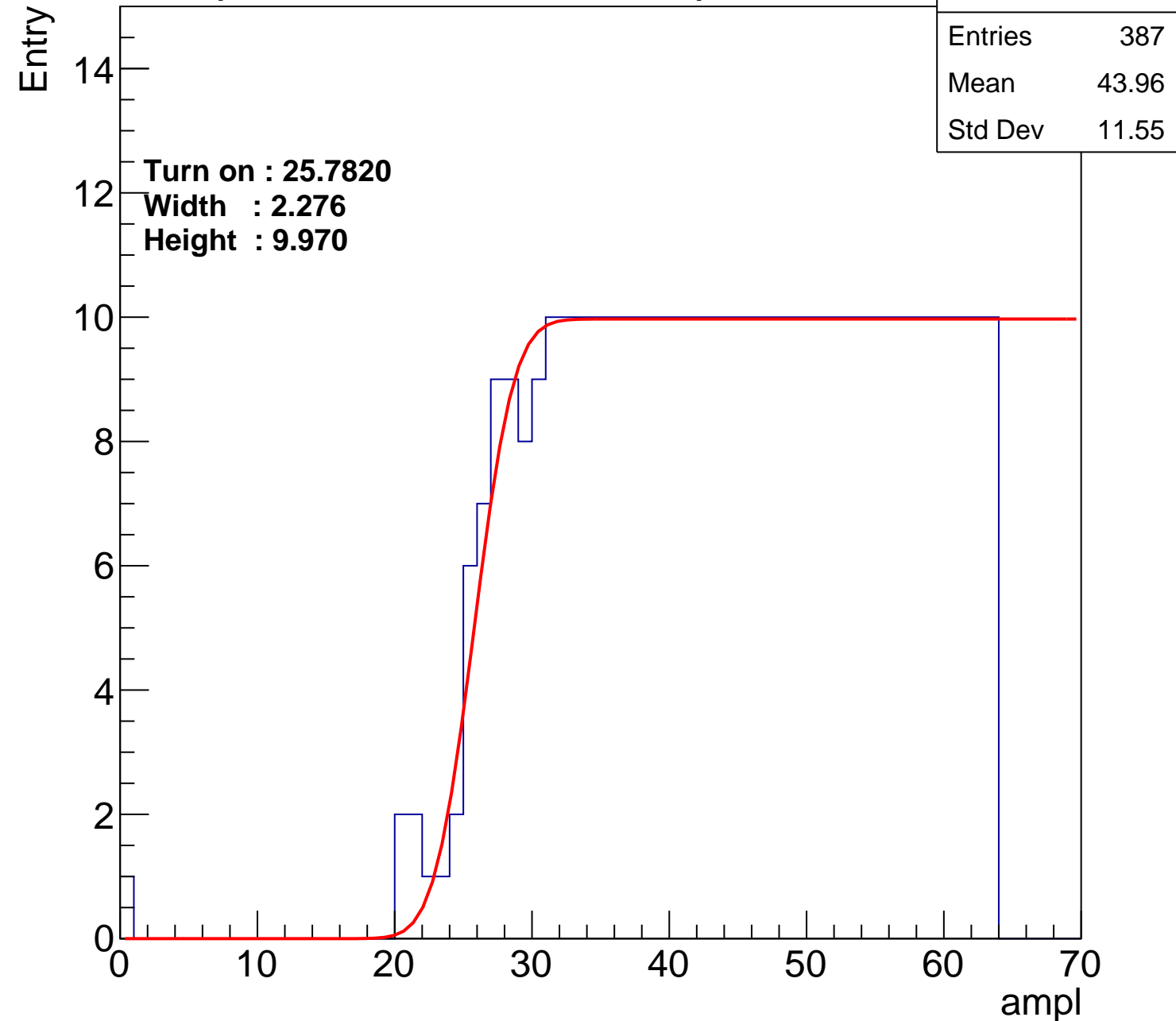
Width : 2.276

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch74

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.9 |
| Std Dev | 11.72 |

Turn on : 25.6306

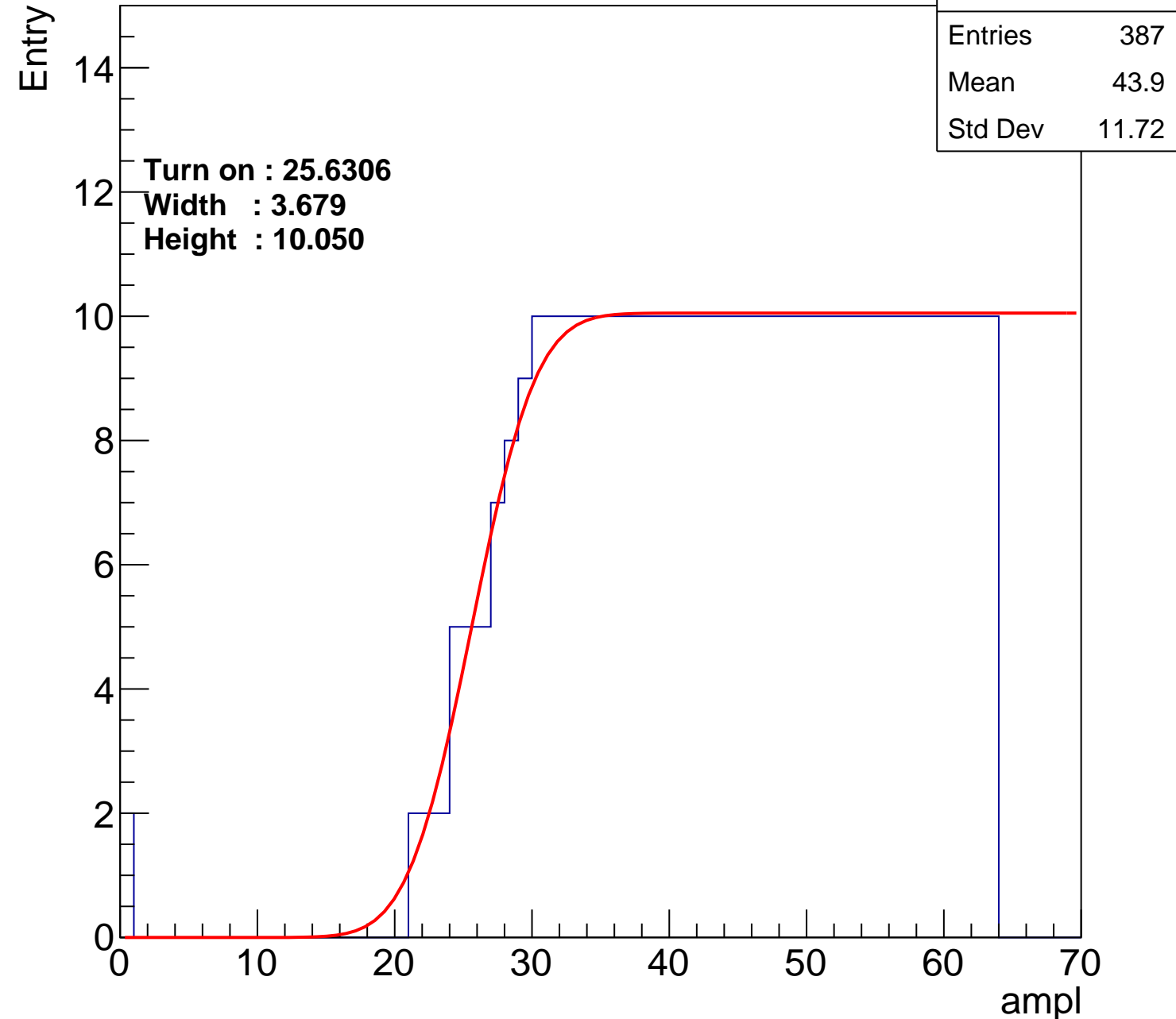
Width : 3.679

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch75

calib_packv5_042523_0143.root, FC#3, port B3

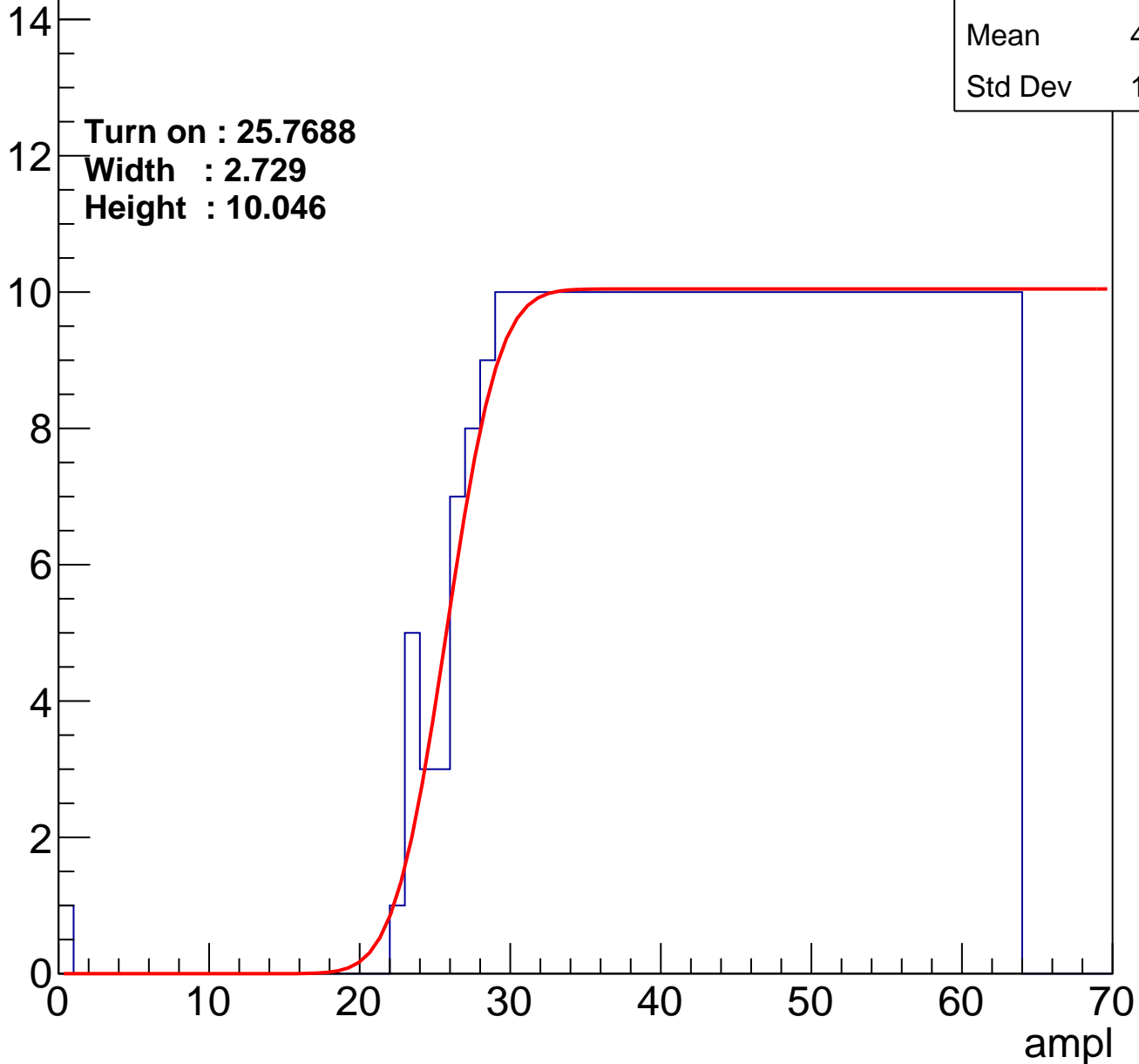
| | |
|---------|-------|
| Entries | 387 |
| Mean | 44.02 |
| Std Dev | 11.47 |

Turn on : 25.7688

Width : 2.729

Height : 10.046

Entry



B1L000S, U26-ch76

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.79 |
| Std Dev | 11.61 |

Turn on : 25.0985

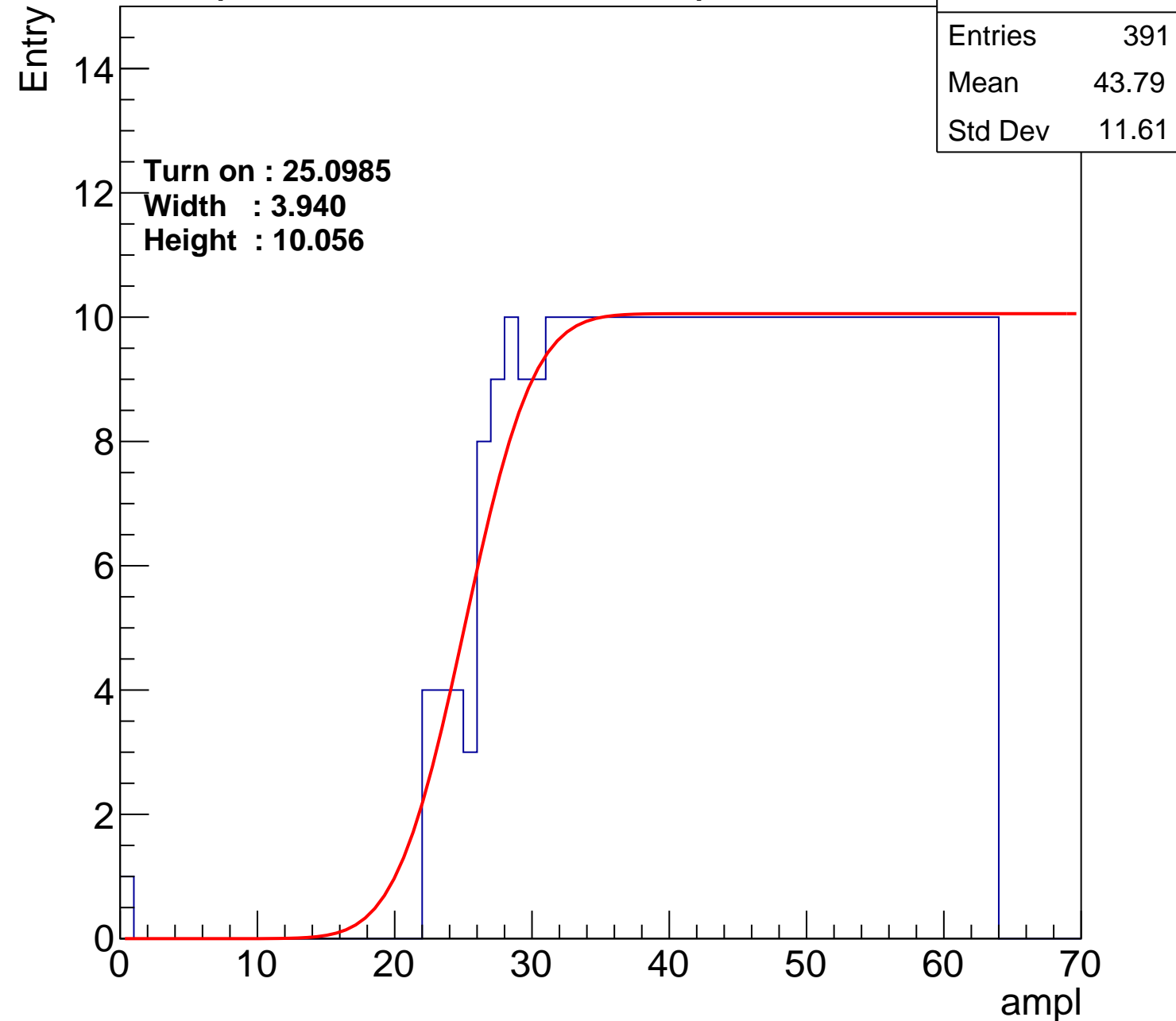
Width : 3.940

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch77

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.01 |
| Std Dev | 11.77 |

Turn on : 26.0506

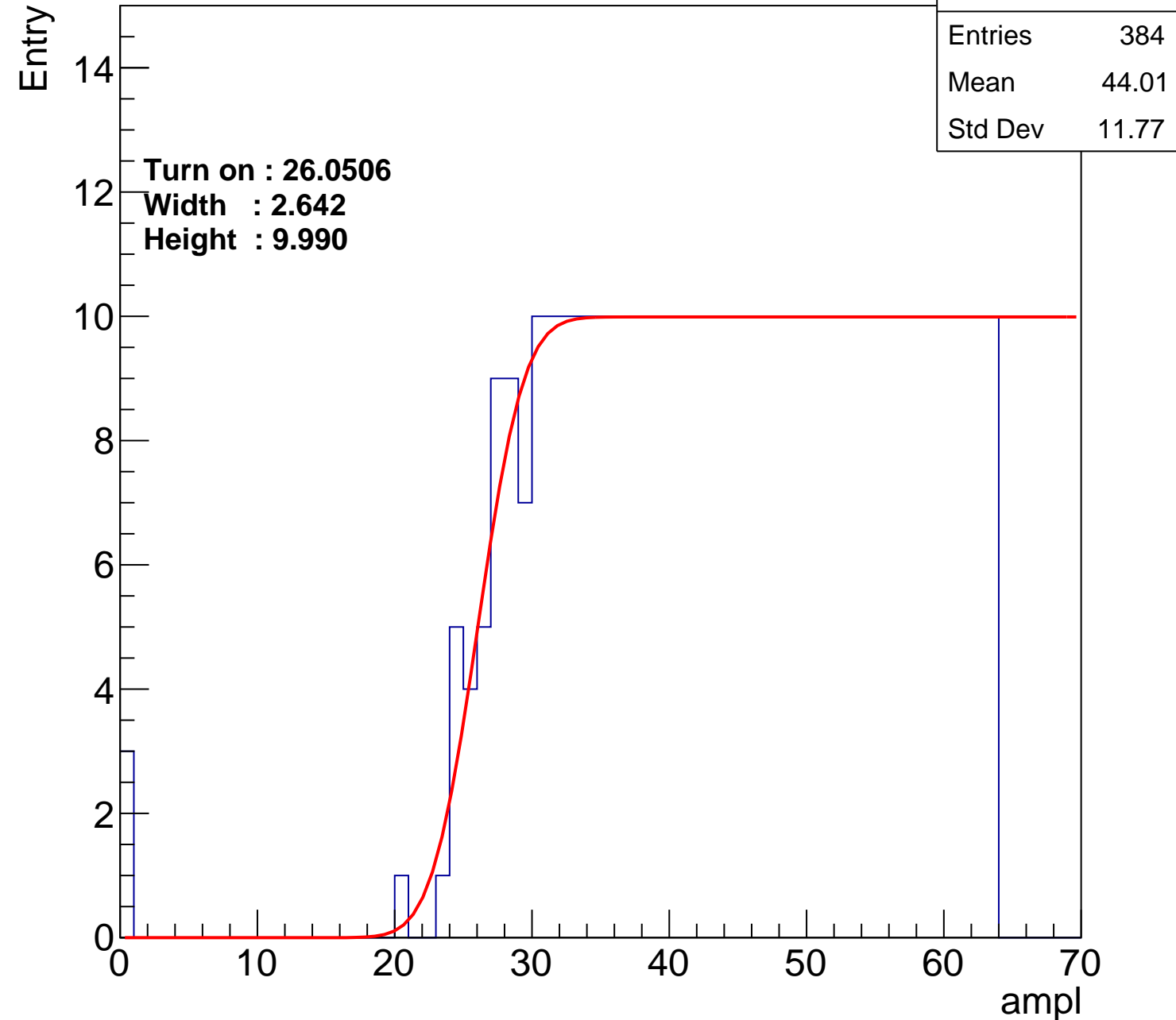
Width : 2.642

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch78

calib_packv5_042523_0143.root, FC#3, port B3

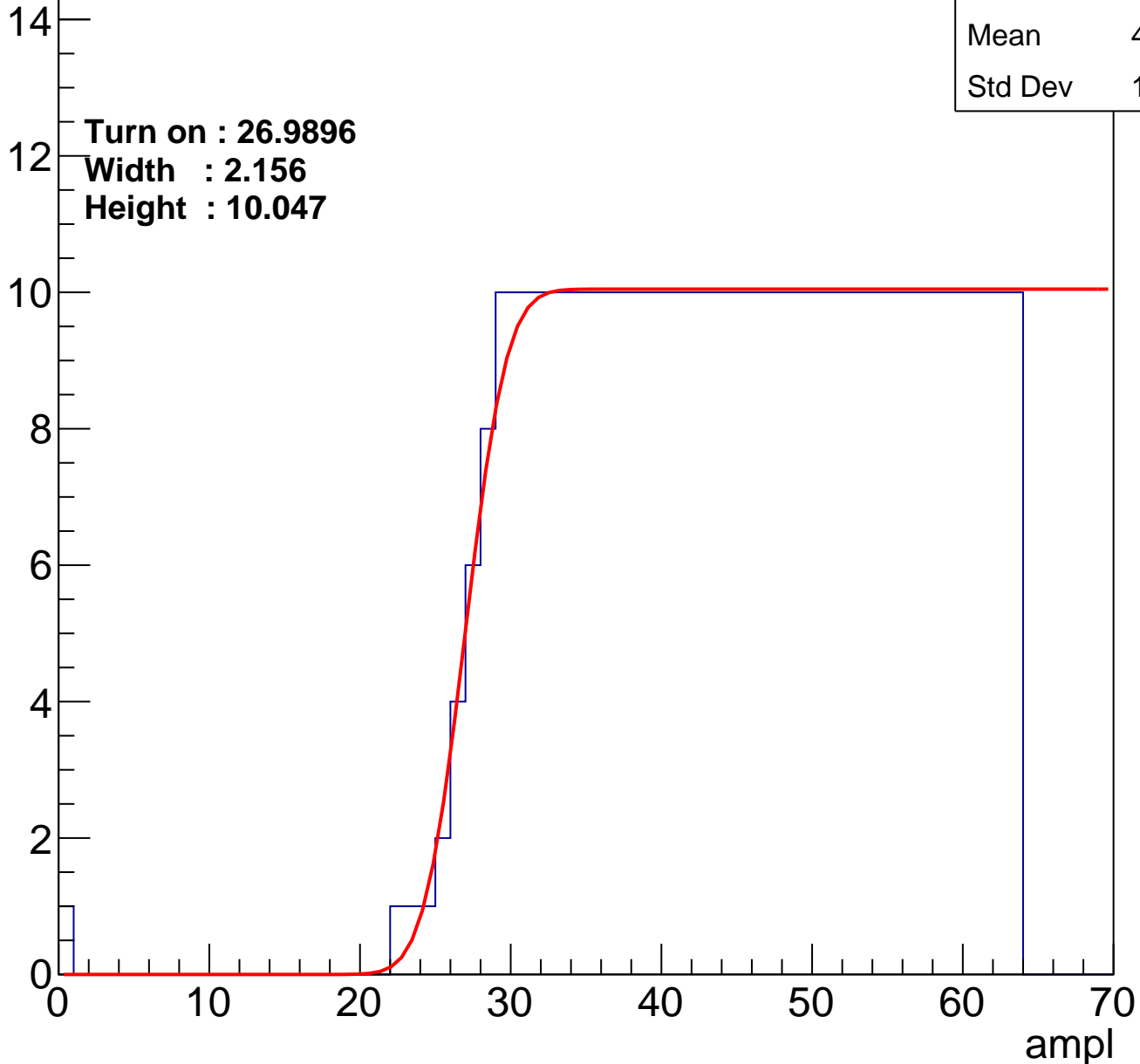
| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.68 |
| Std Dev | 11.09 |

Turn on : 26.9896

Width : 2.156

Height : 10.047

Entry



B1L000S, U26-ch79

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.63 |
| Std Dev | 11.16 |

Turn on : 26.4511

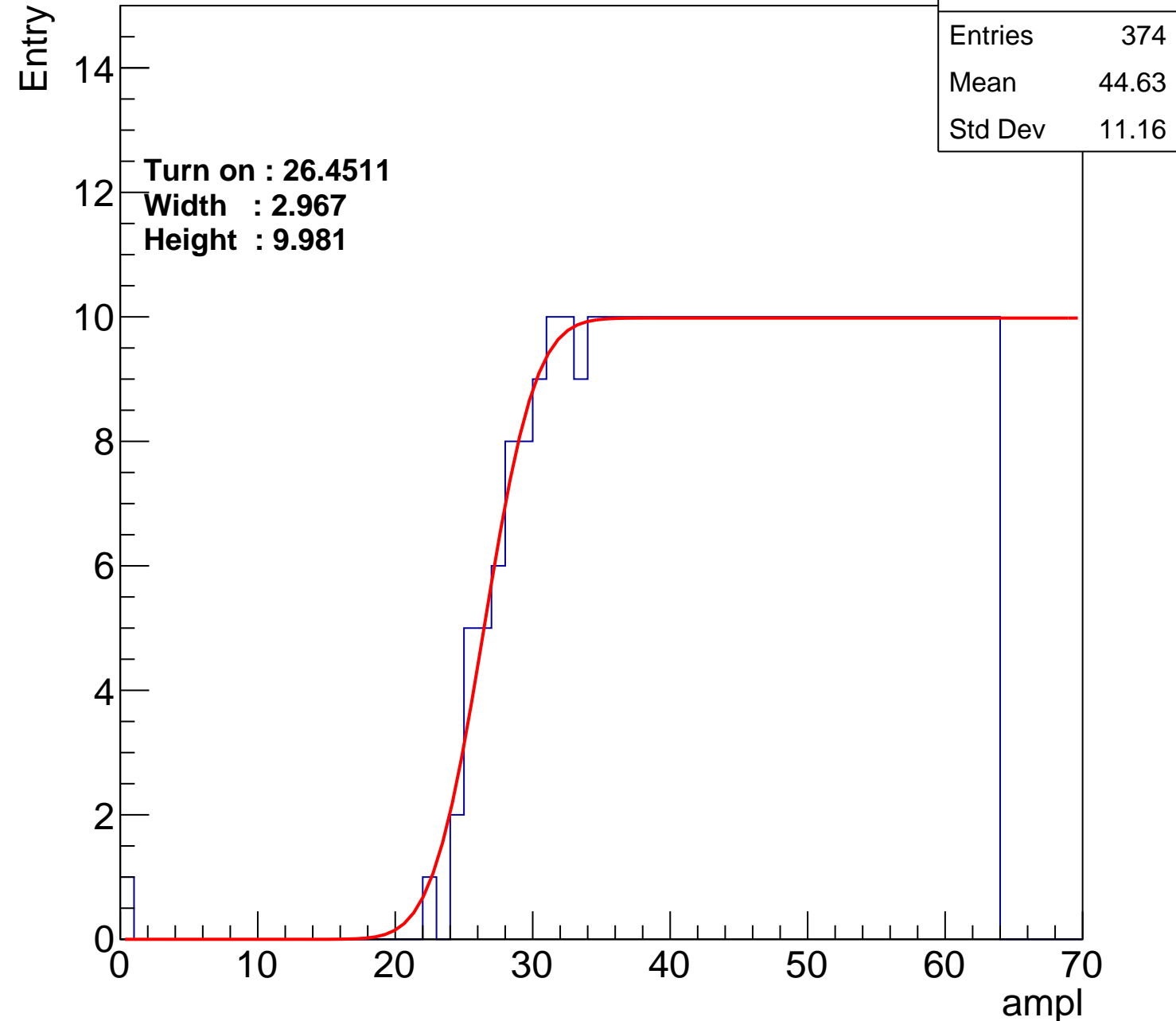
Width : 2.967

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch80

calib_packv5_042523_0143.root, FC#3, port B3

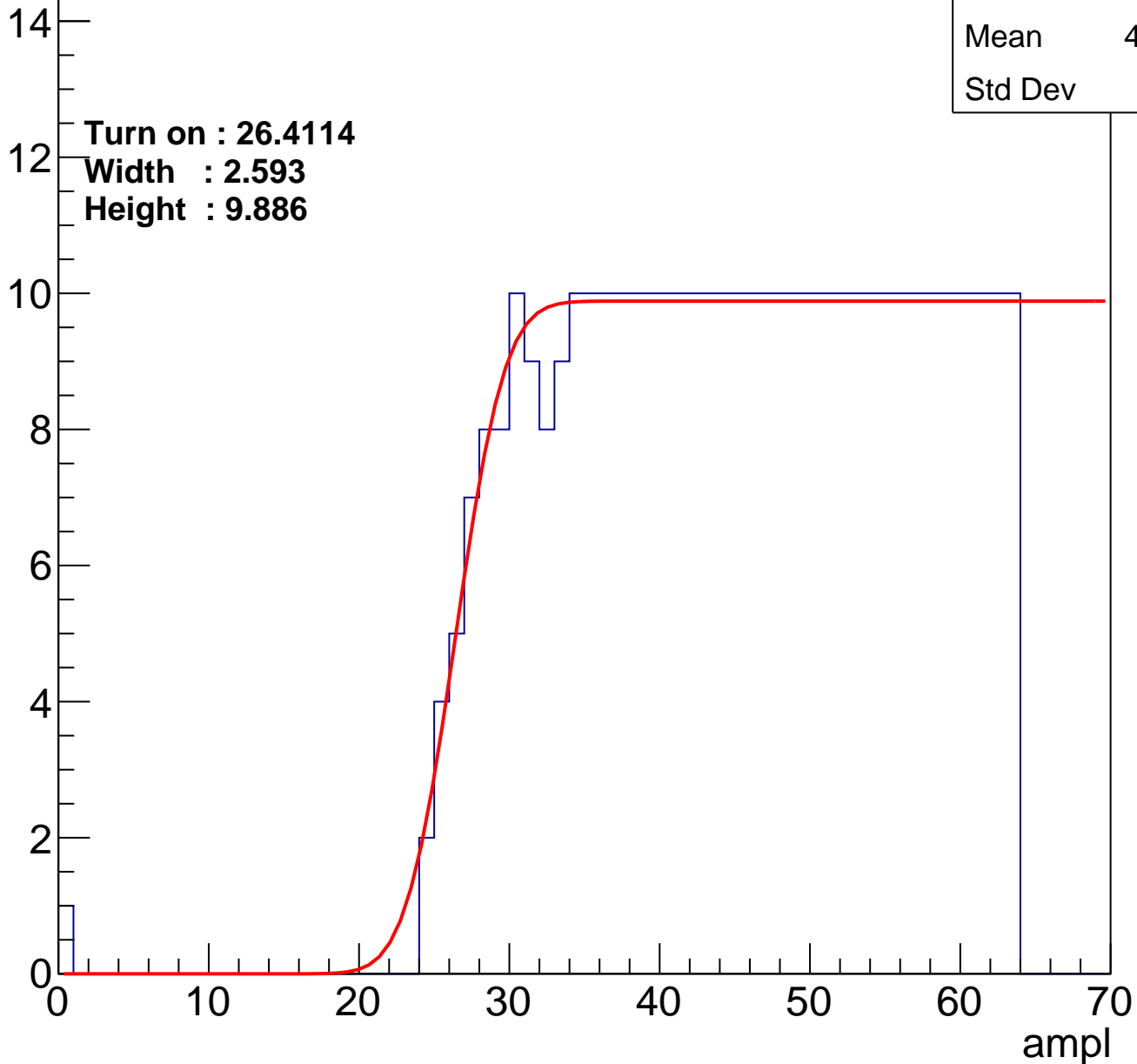
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.76 |
| Std Dev | 11.1 |

Turn on : 26.4114

Width : 2.593

Height : 9.886

Entry



B1L000S, U26-ch81

calib_packv5_042523_0143.root, FC#3, port B3

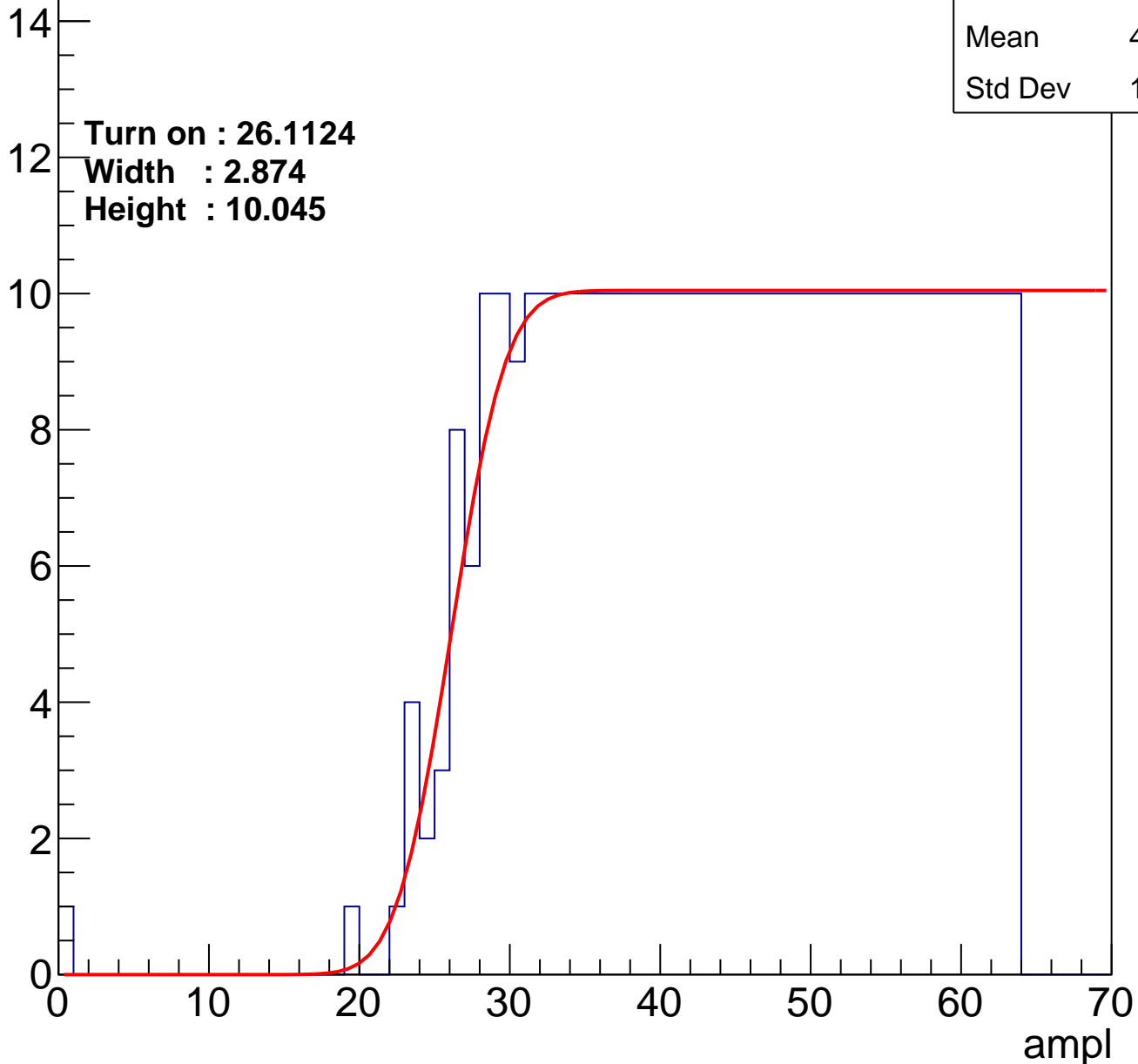
| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.09 |
| Std Dev | 11.45 |

Turn on : 26.1124

Width : 2.874

Height : 10.045

Entry



B1L000S, U26-ch82

calib_packv5_042523_0143.root, FC#3, port B3

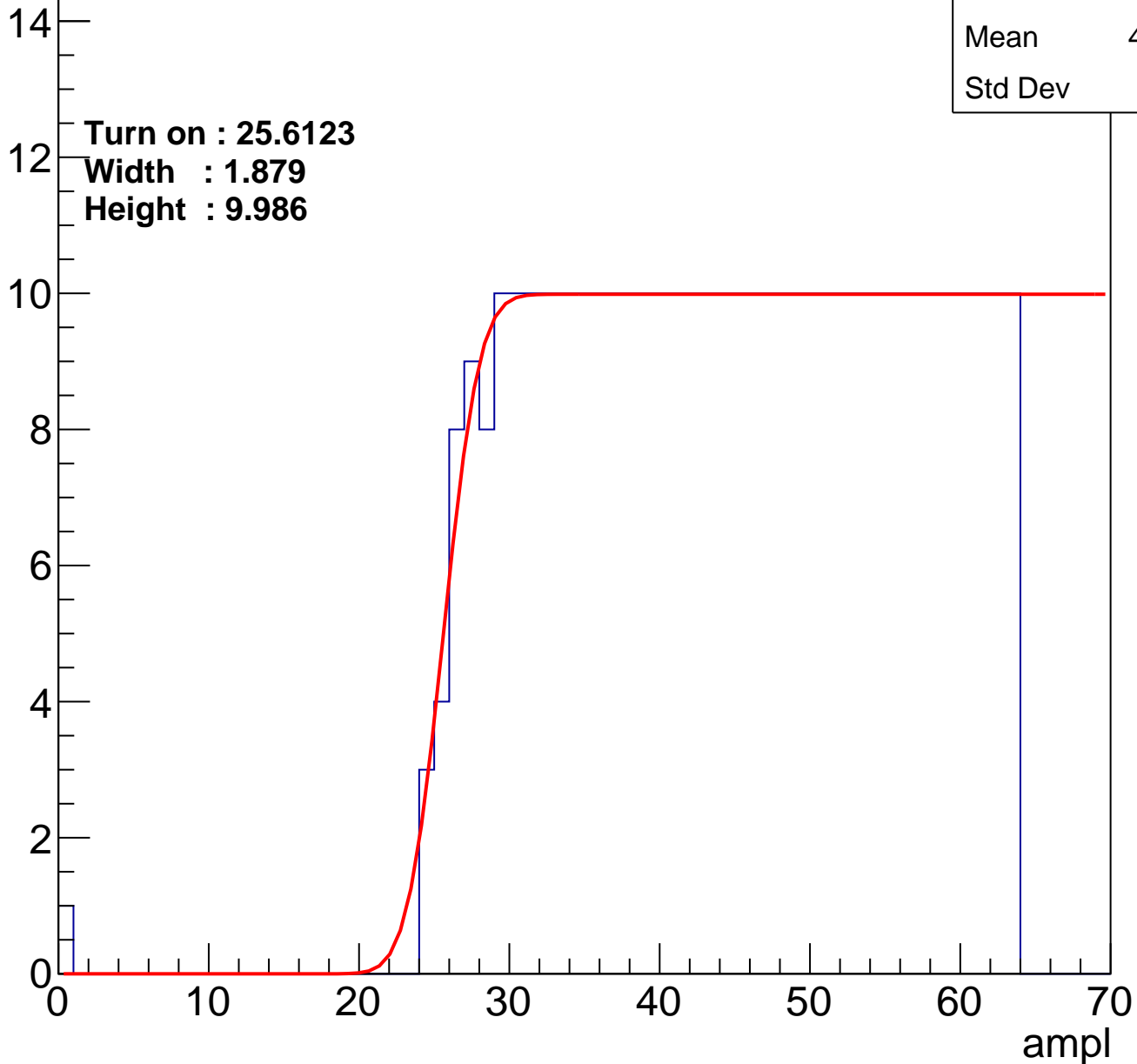
| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.25 |
| Std Dev | 11.3 |

Turn on : 25.6123

Width : 1.879

Height : 9.986

Entry



B1L000S, U26-ch83

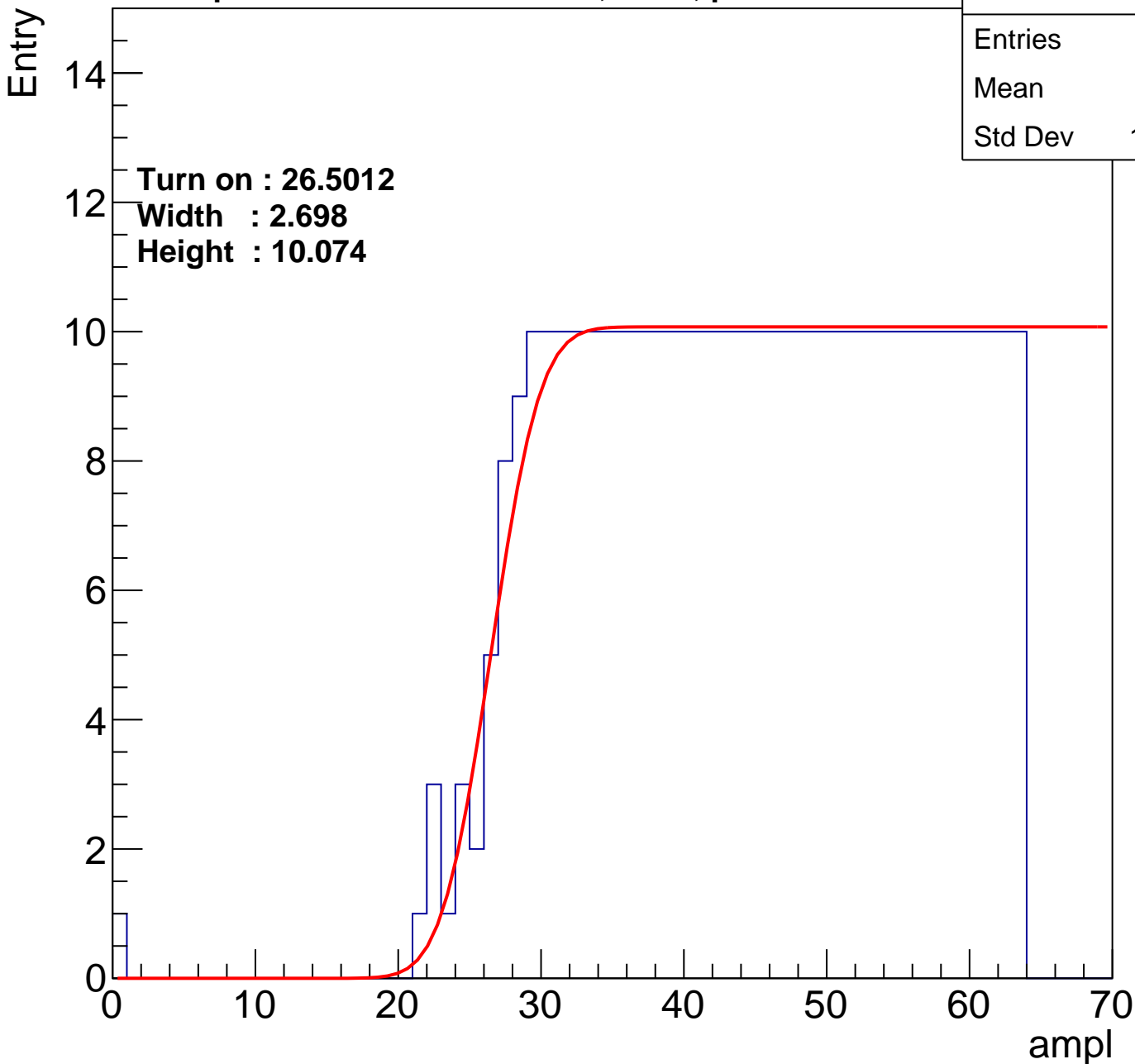
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.2 |
| Std Dev | 11.38 |

Turn on : 26.5012

Width : 2.698

Height : 10.074



B1L000S, U26-ch84

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 353 |
| Mean | 45.59 |
| Std Dev | 10.84 |

Turn on : 29.3768

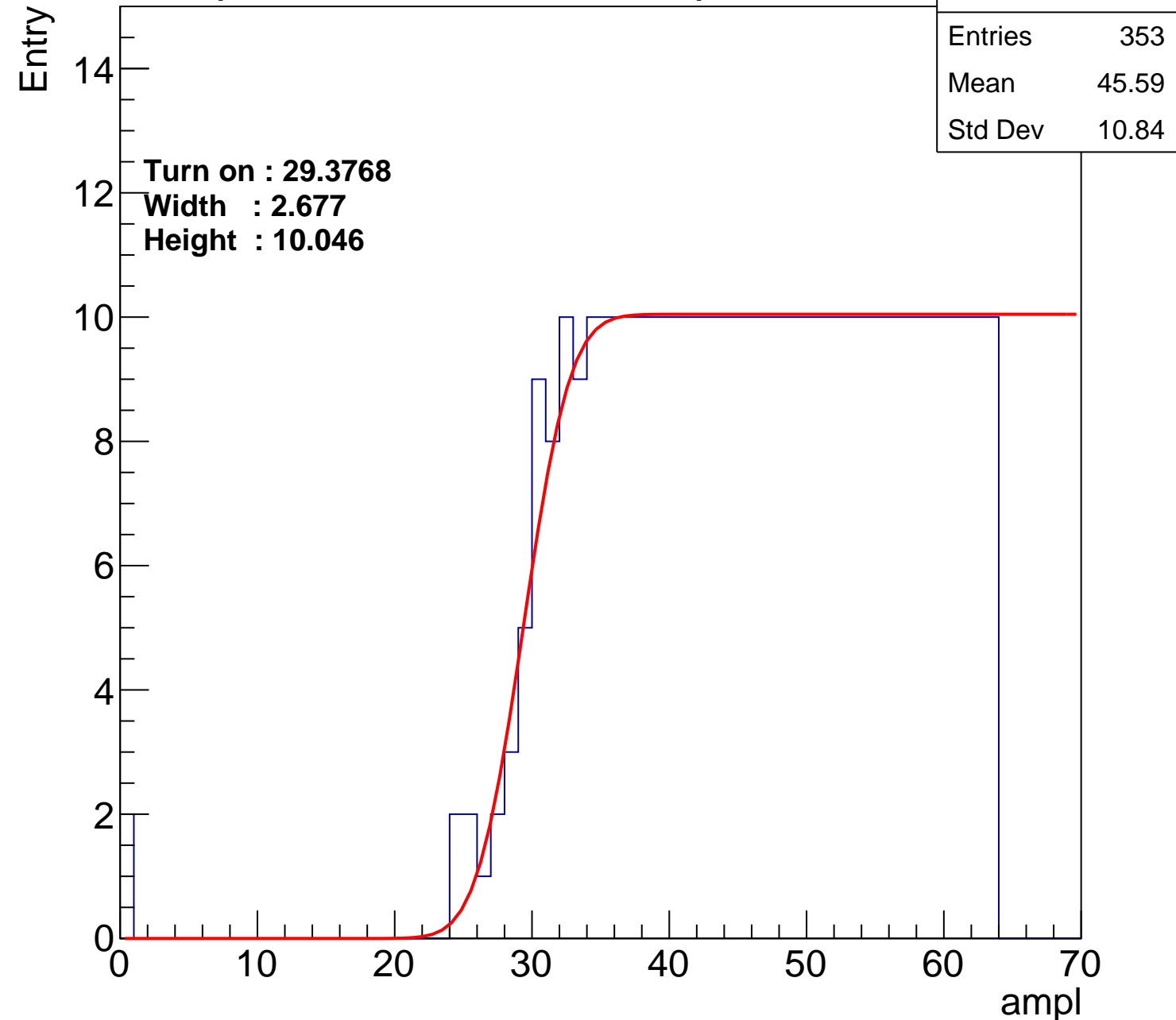
Width : 2.677

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch85

calib_packv5_042523_0143.root, FC#3, port B3

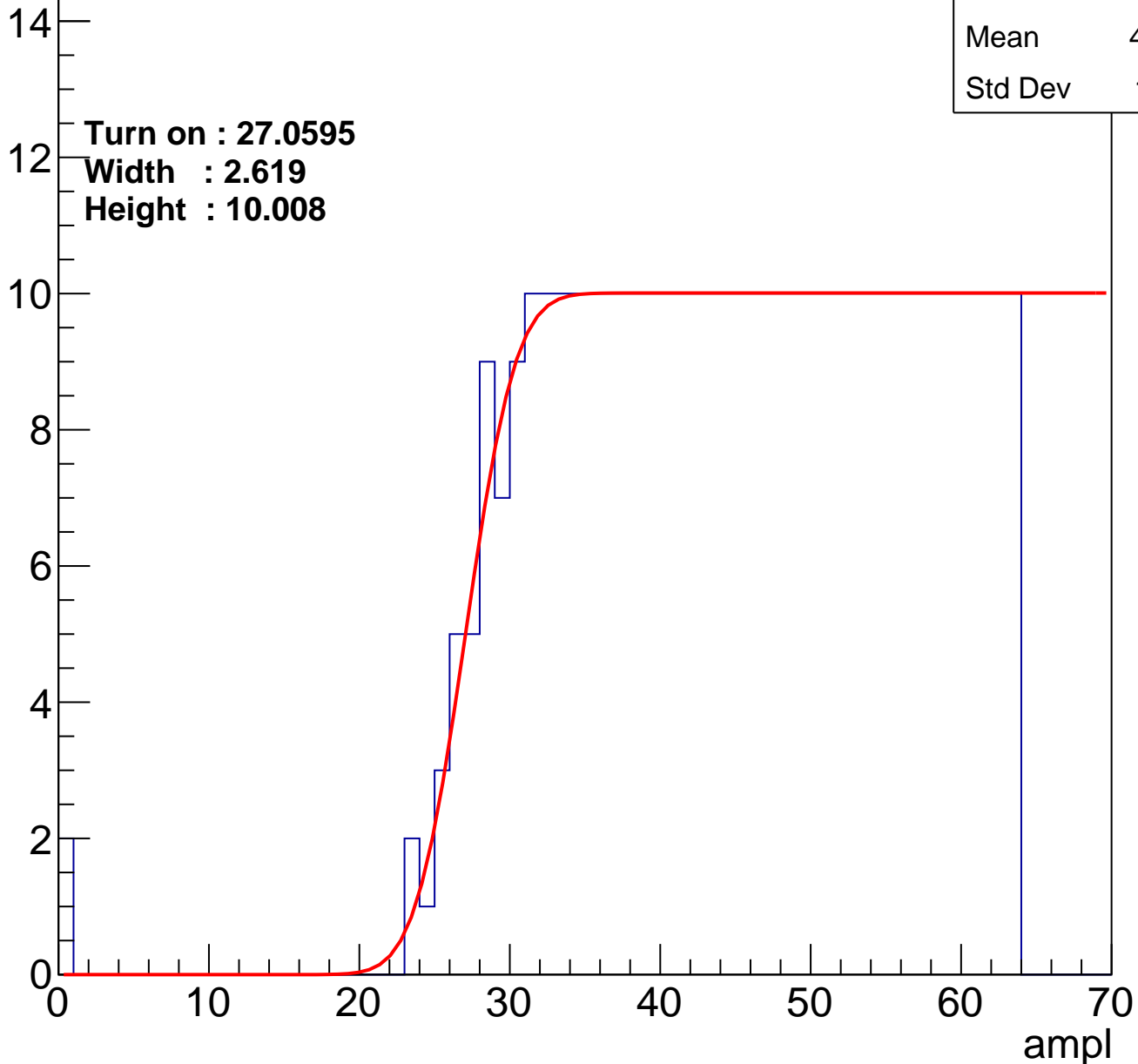
| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.62 |
| Std Dev | 11.31 |

Turn on : 27.0595

Width : 2.619

Height : 10.008

Entry



B1L000S, U26-ch86

calib_packv5_042523_0143.root, FC#3, port B3

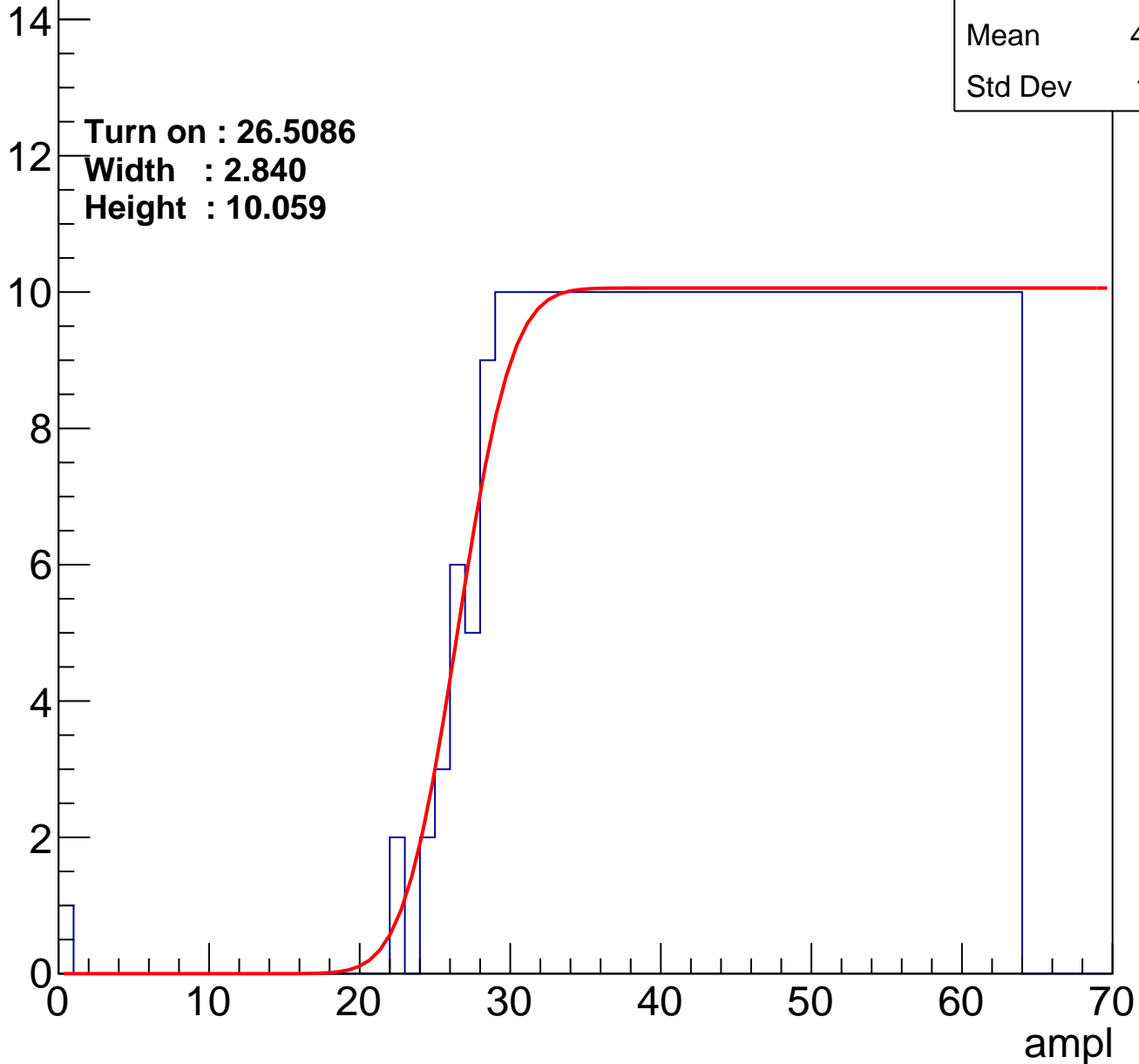
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.47 |
| Std Dev | 11.21 |

Turn on : 26.5086

Width : 2.840

Height : 10.059

Entry



B1L000S, U26-ch87

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.4 |
| Std Dev | 11.46 |

Turn on : 26.6279

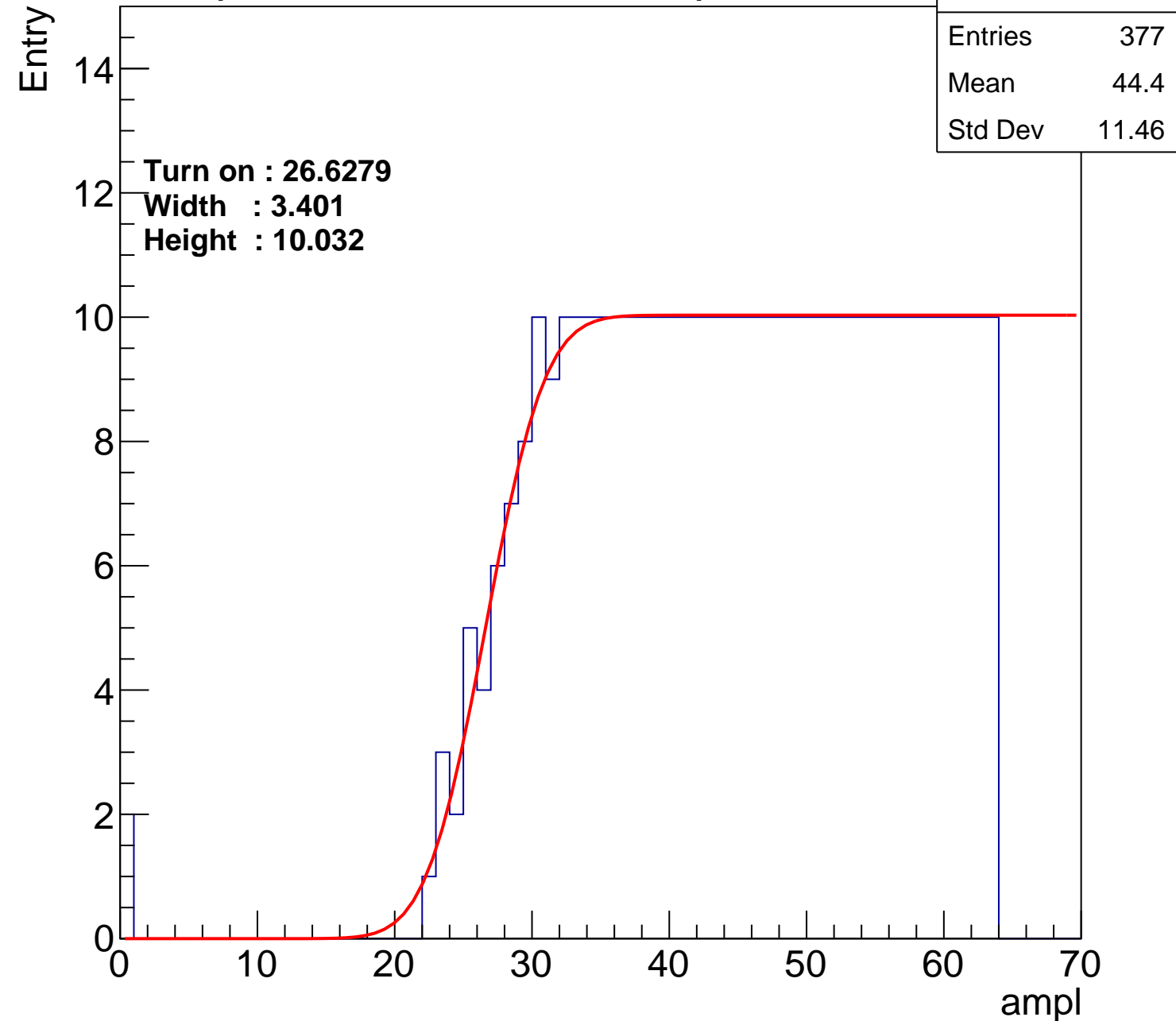
Width : 3.401

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch88

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.73 |
| Std Dev | 12.02 |

Turn on : 26.1370

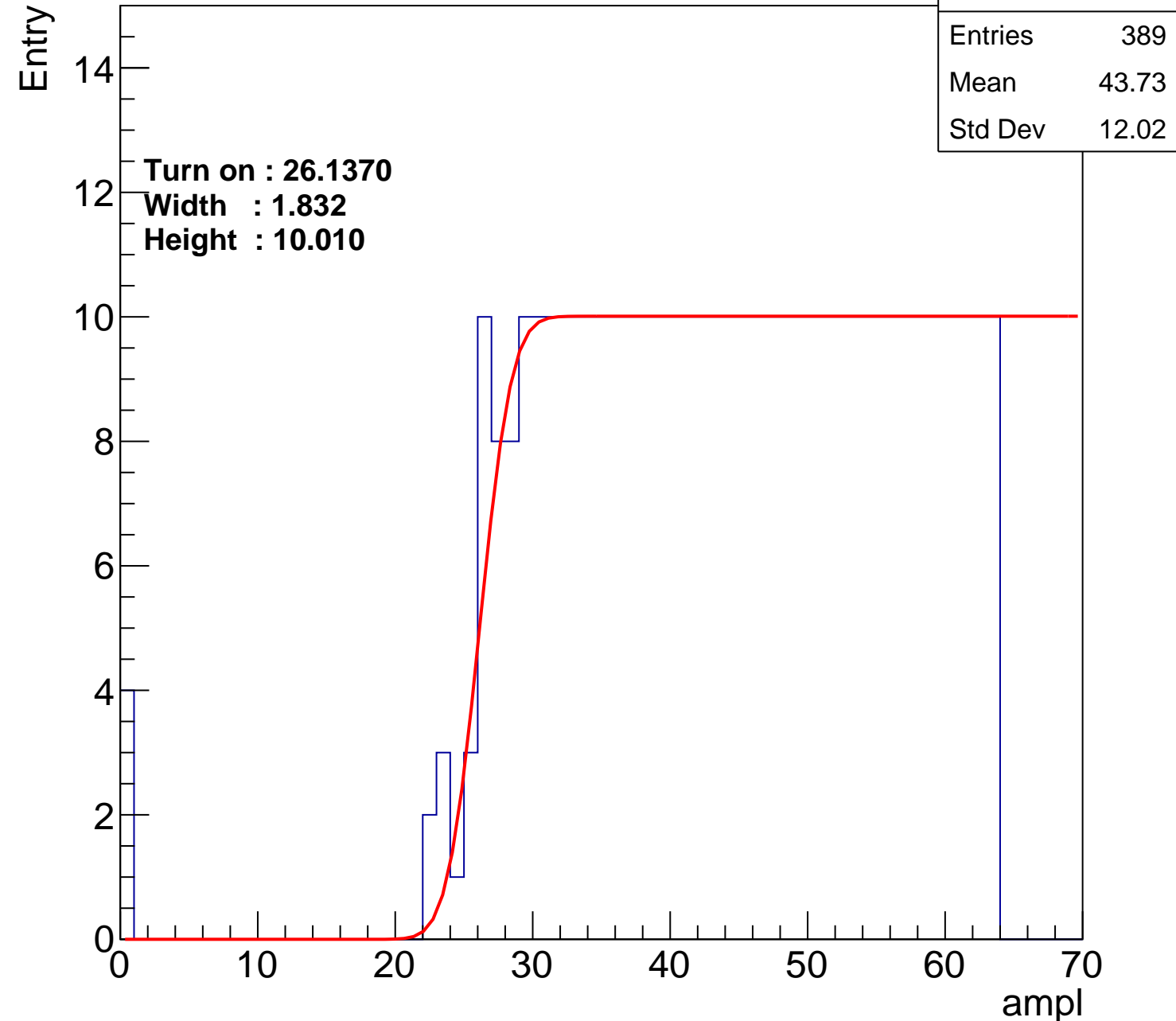
Width : 1.832

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch89

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.51 |
| Std Dev | 11.51 |

Turn on : 26.9380

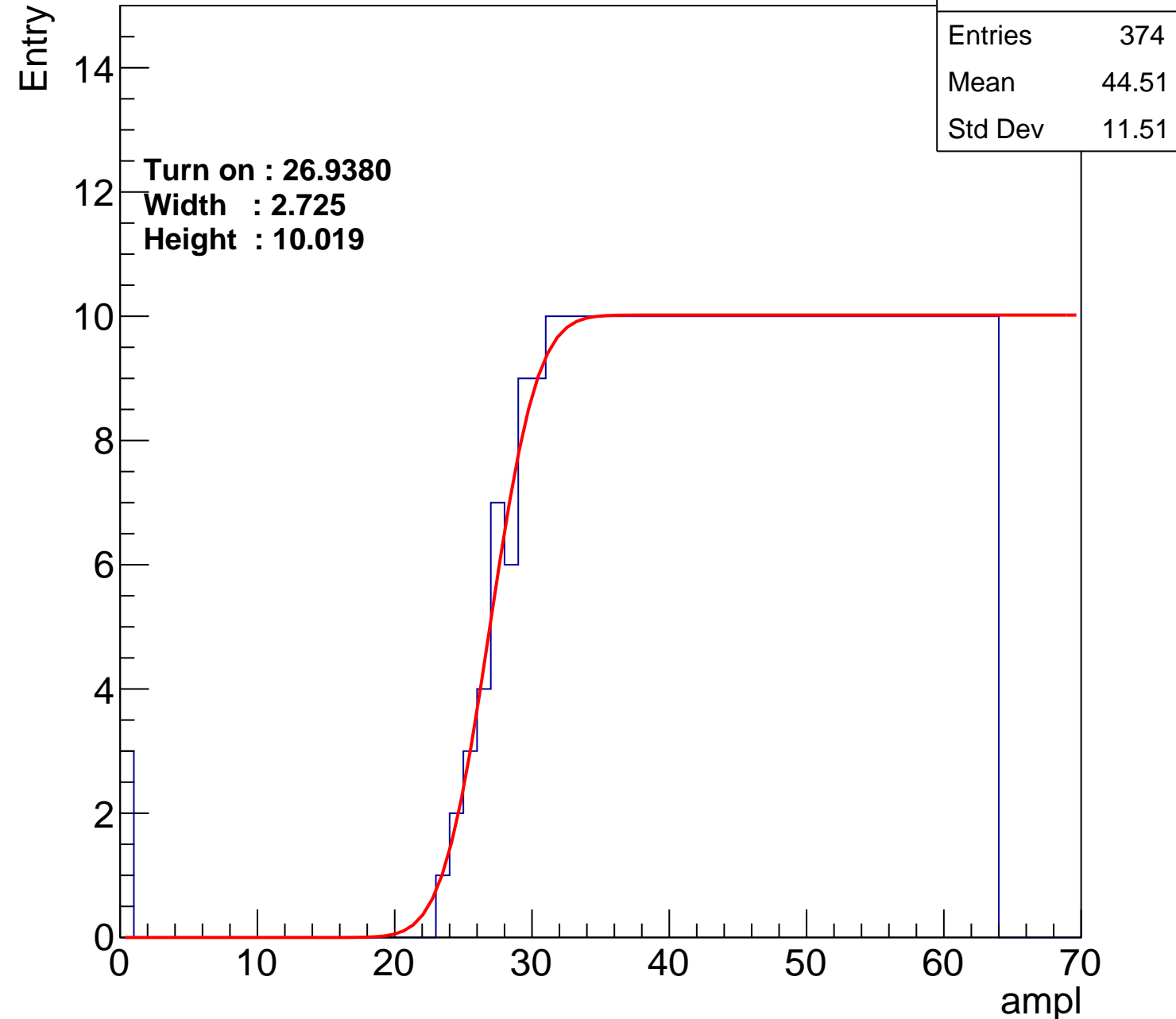
Width : 2.725

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch90

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.22 |
| Std Dev | 11.66 |

Turn on : 26.6350

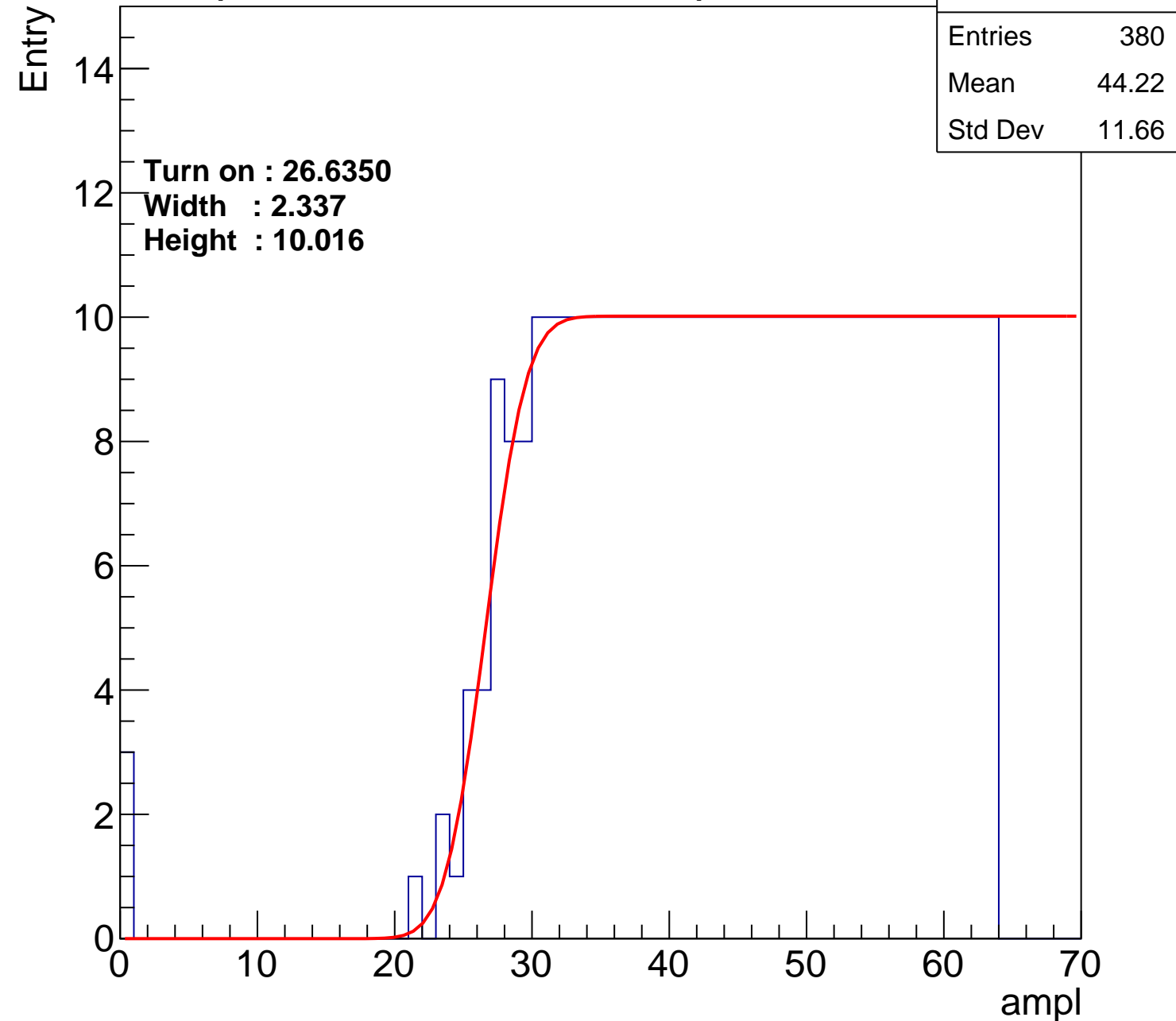
Width : 2.337

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch91

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.49 |
| Std Dev | 11.37 |

Turn on : 26.6850

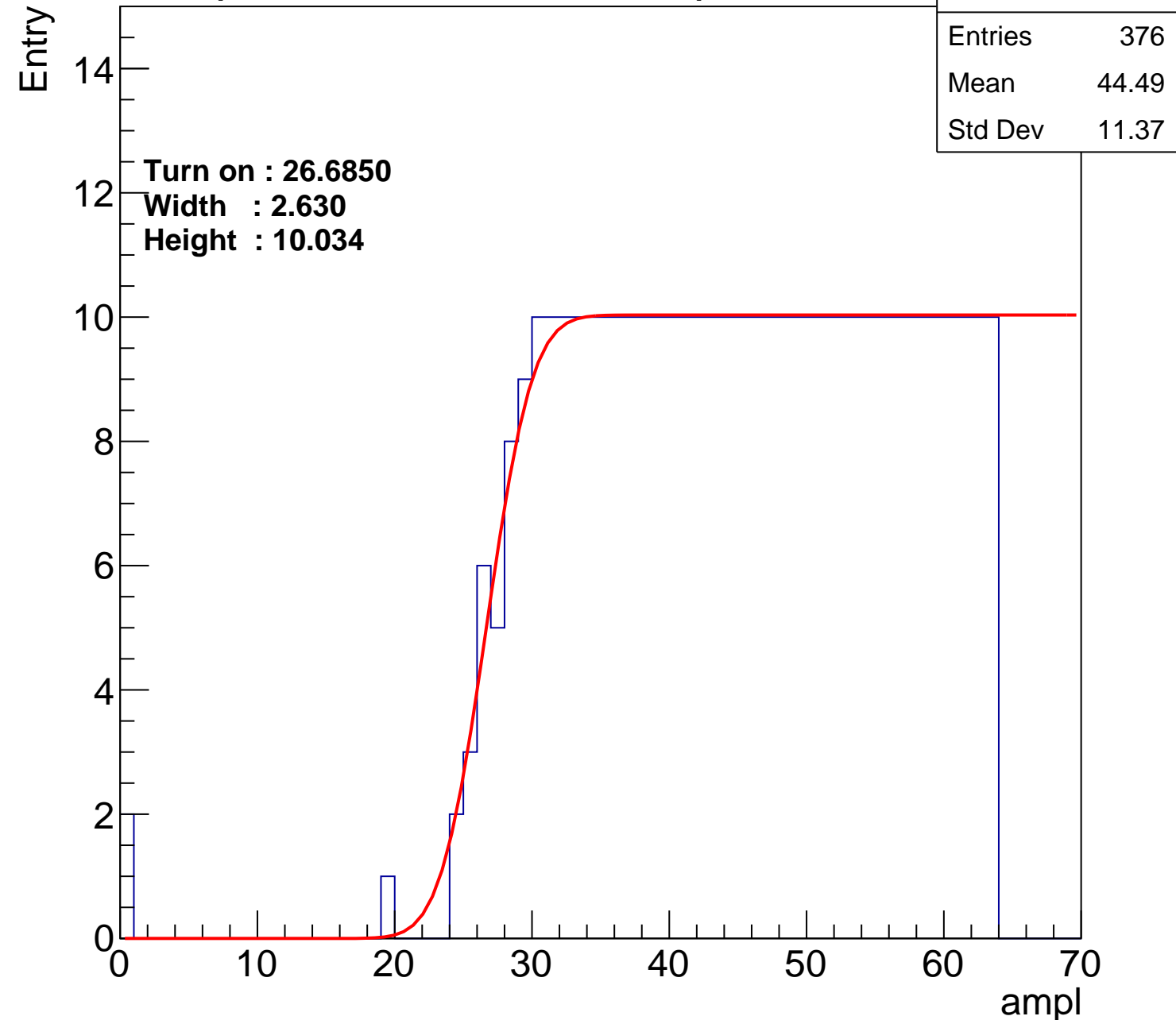
Width : 2.630

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch92

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.28 |
| Std Dev | 11.36 |

Turn on : 26.0710

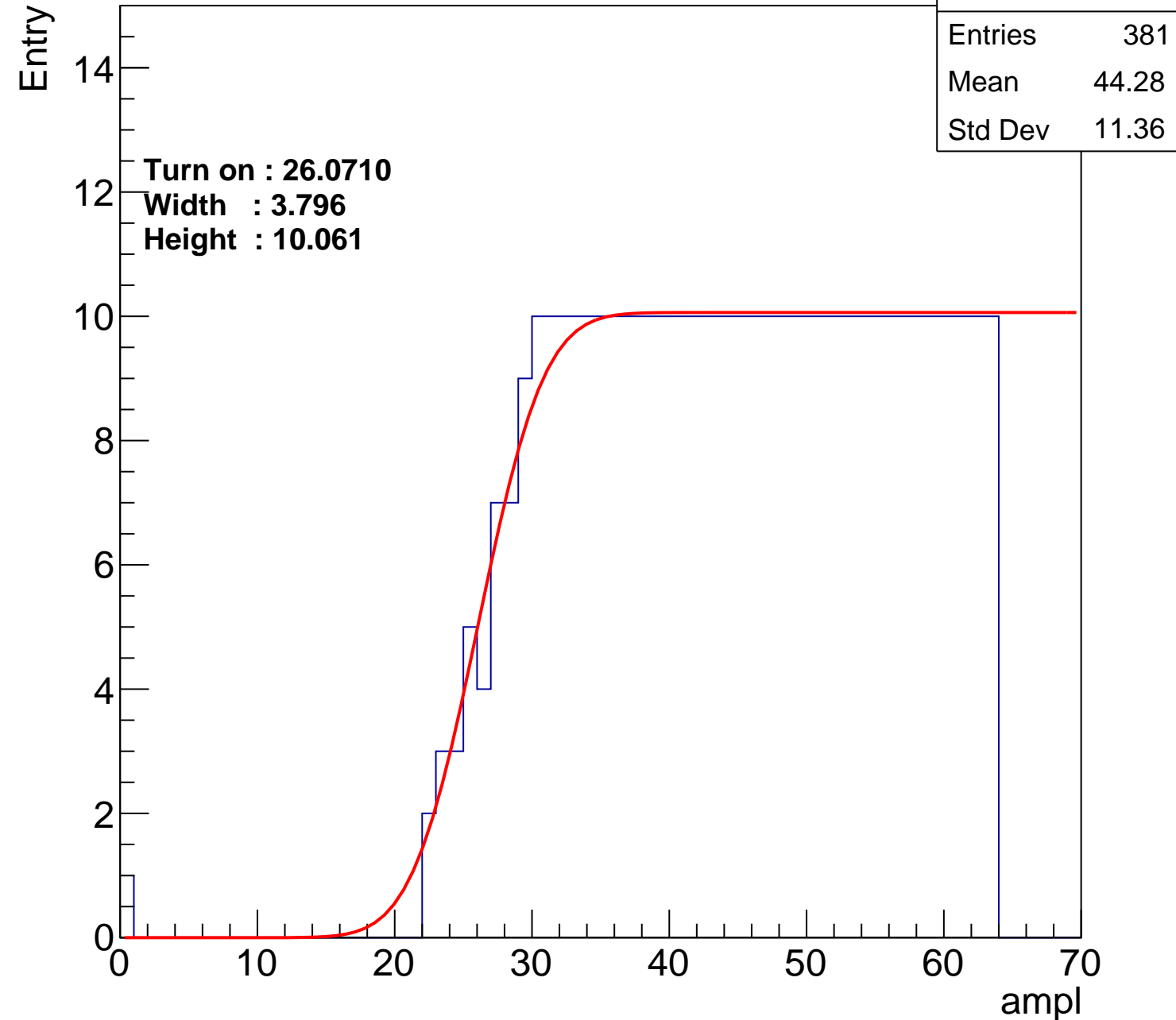
Width : 3.796

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch93

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.72 |
| Std Dev | 11.62 |

Turn on : 24.9382

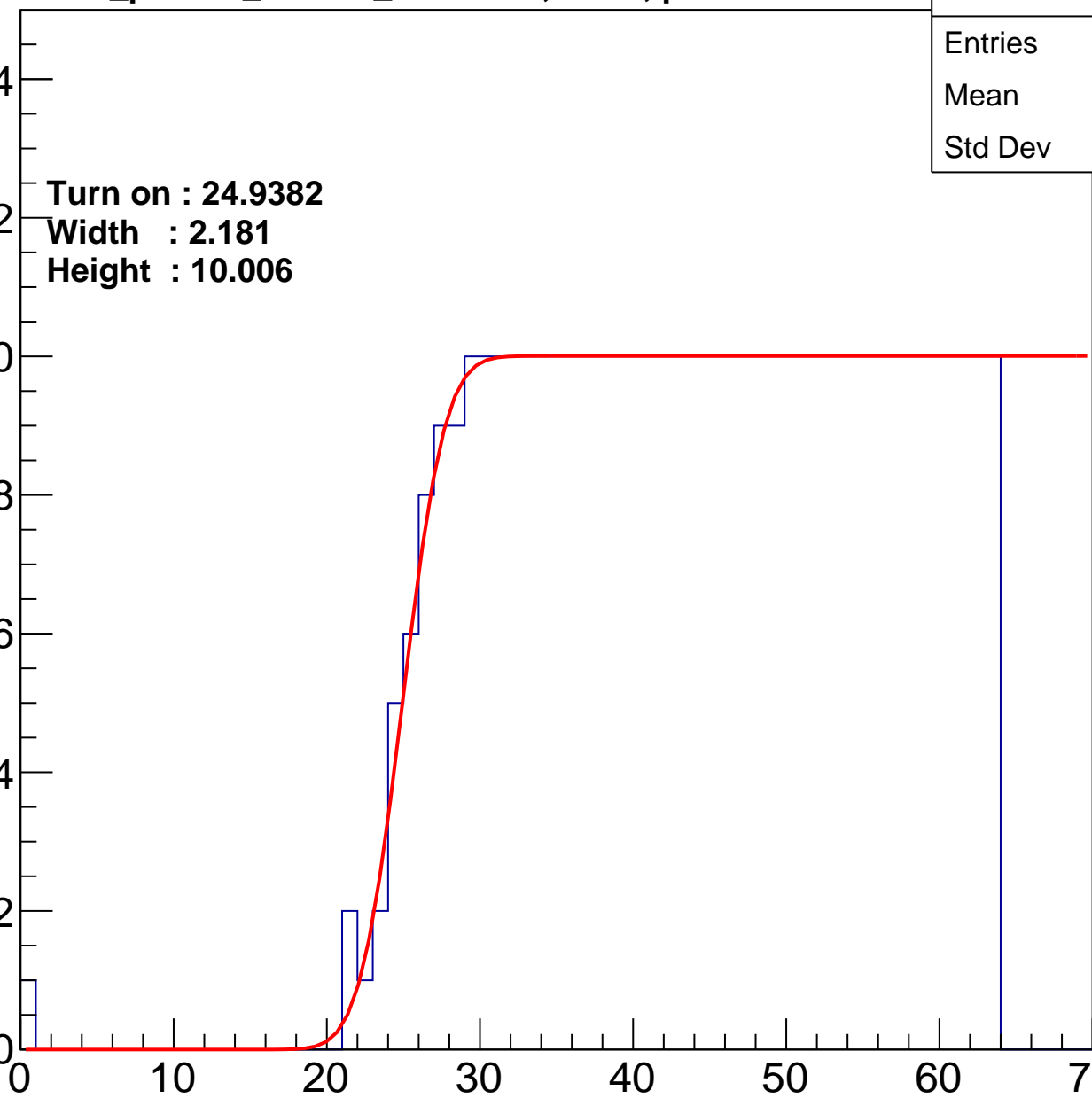
Width : 2.181

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch94

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.56 |
| Std Dev | 11.73 |

Turn on : 24.7707

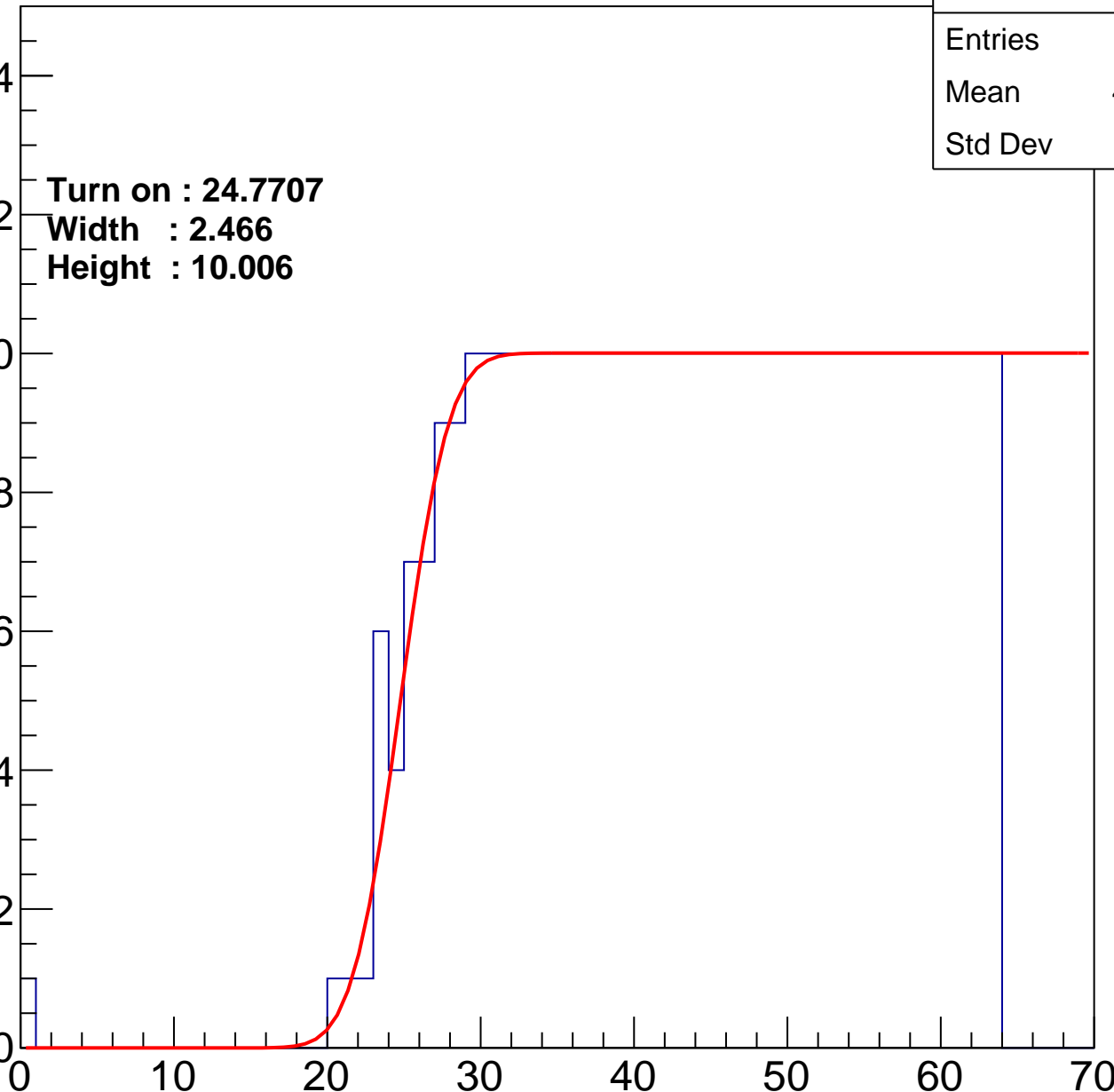
Width : 2.466

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch95

calib_packv5_042523_0143.root, FC#3, port B3

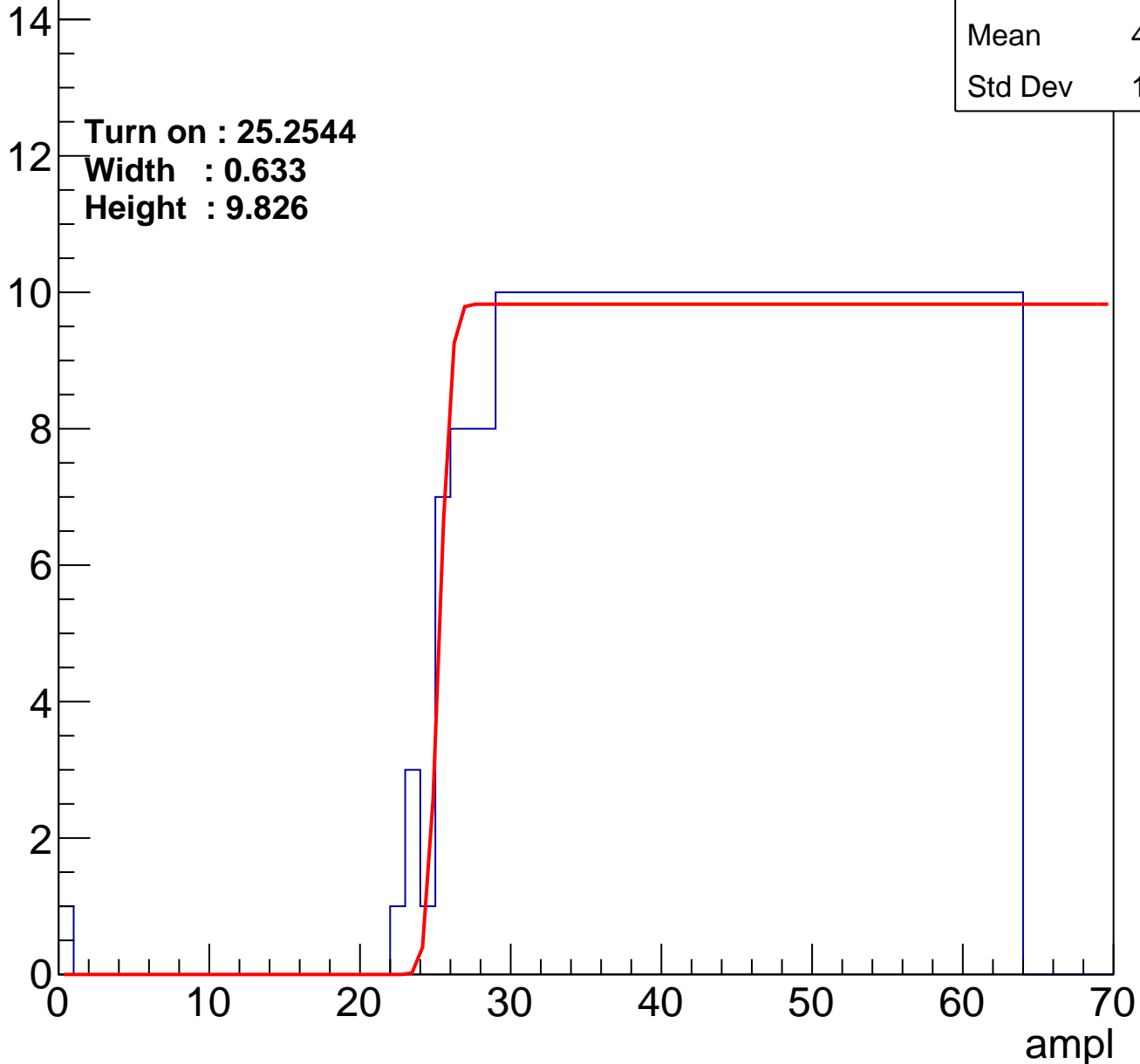
| | |
|---------|-------|
| Entries | 387 |
| Mean | 44.03 |
| Std Dev | 11.45 |

Turn on : 25.2544

Width : 0.633

Height : 9.826

Entry



B1L000S, U26-ch96

calib_packv5_042523_0143.root, FC#3, port B3

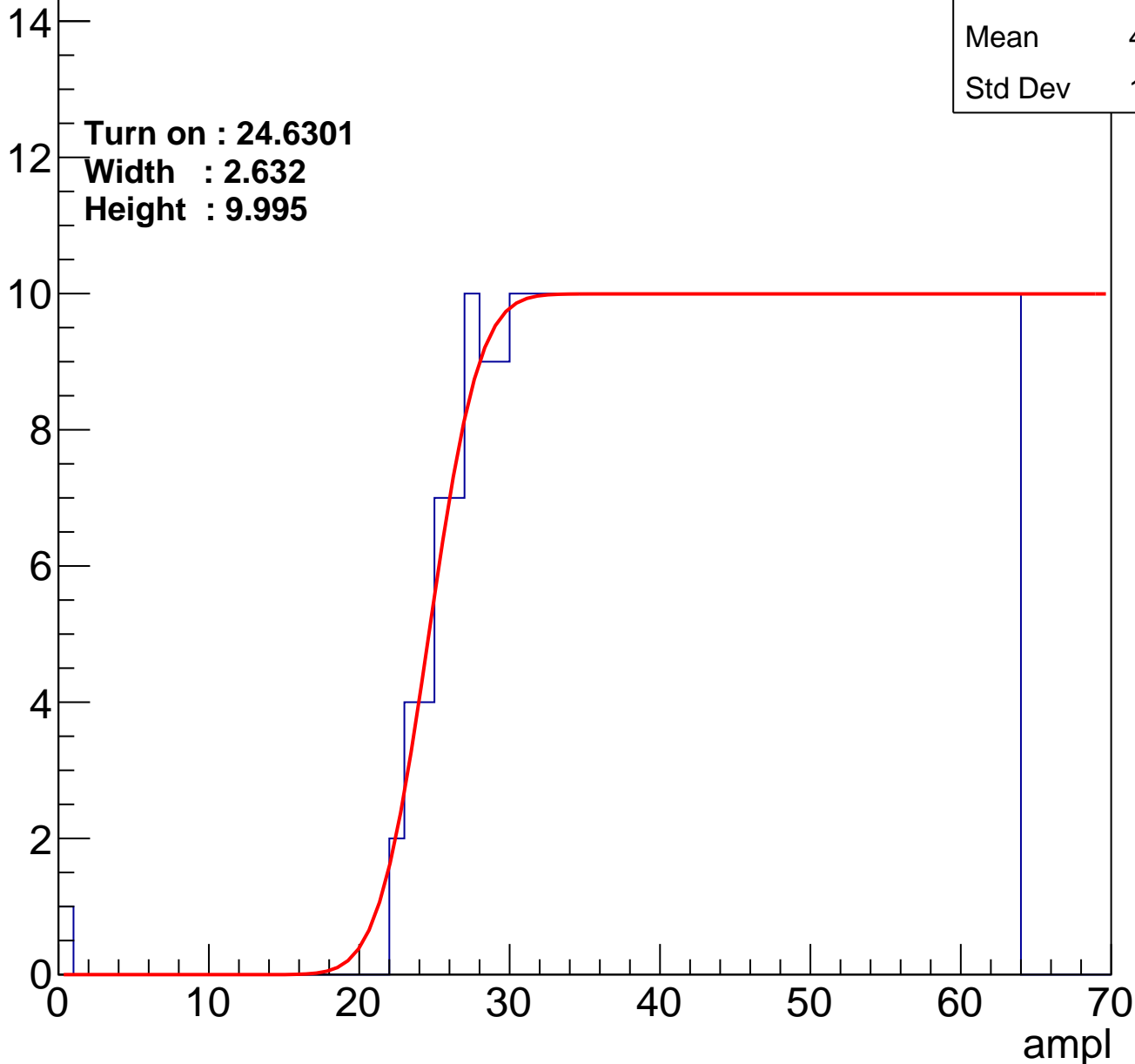
| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.72 |
| Std Dev | 11.62 |

Turn on : 24.6301

Width : 2.632

Height : 9.995

Entry



B1L000S, U26-ch97

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.03 |
| Std Dev | 11.57 |

Turn on : 25.5320

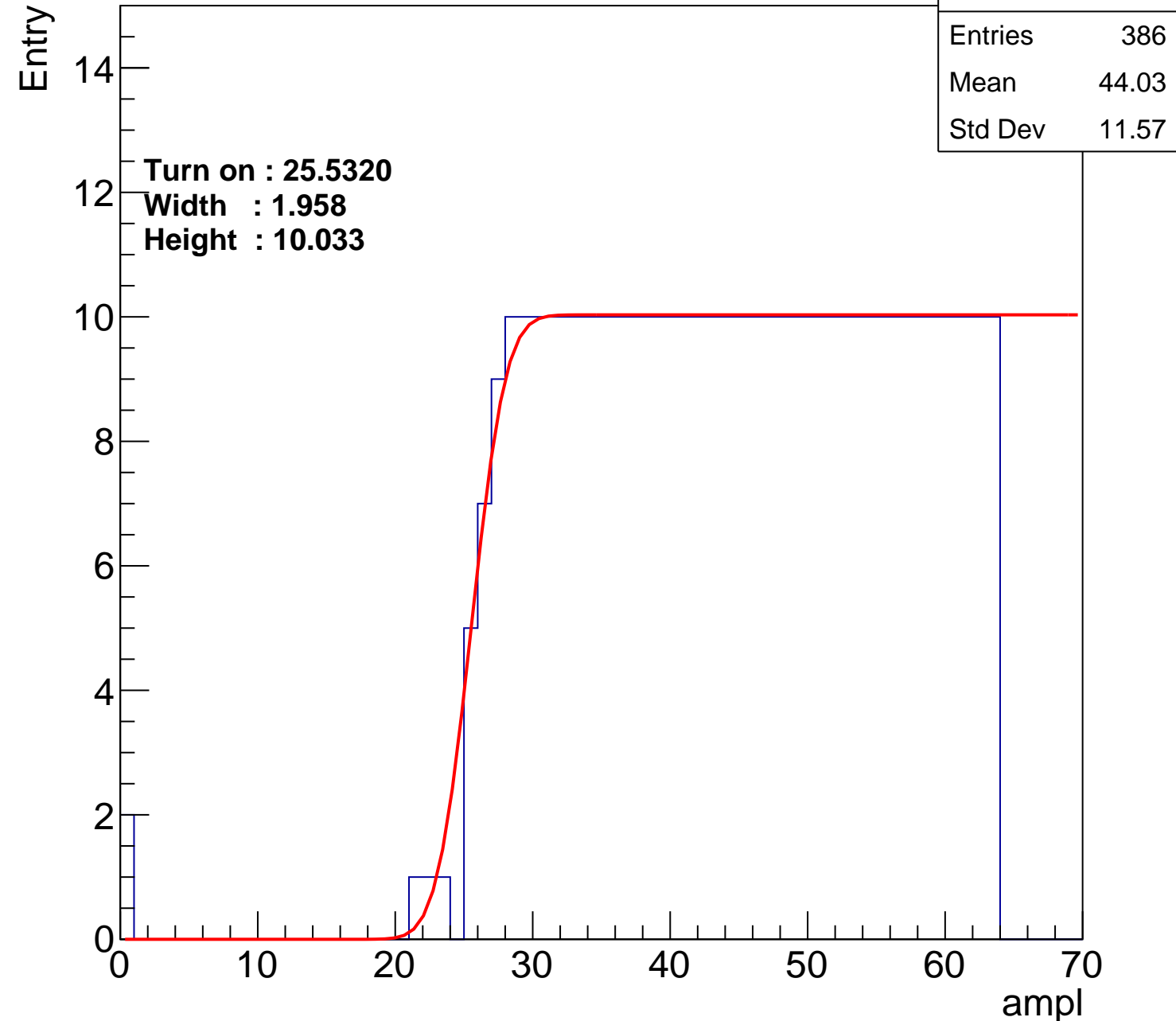
Width : 1.958

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch98

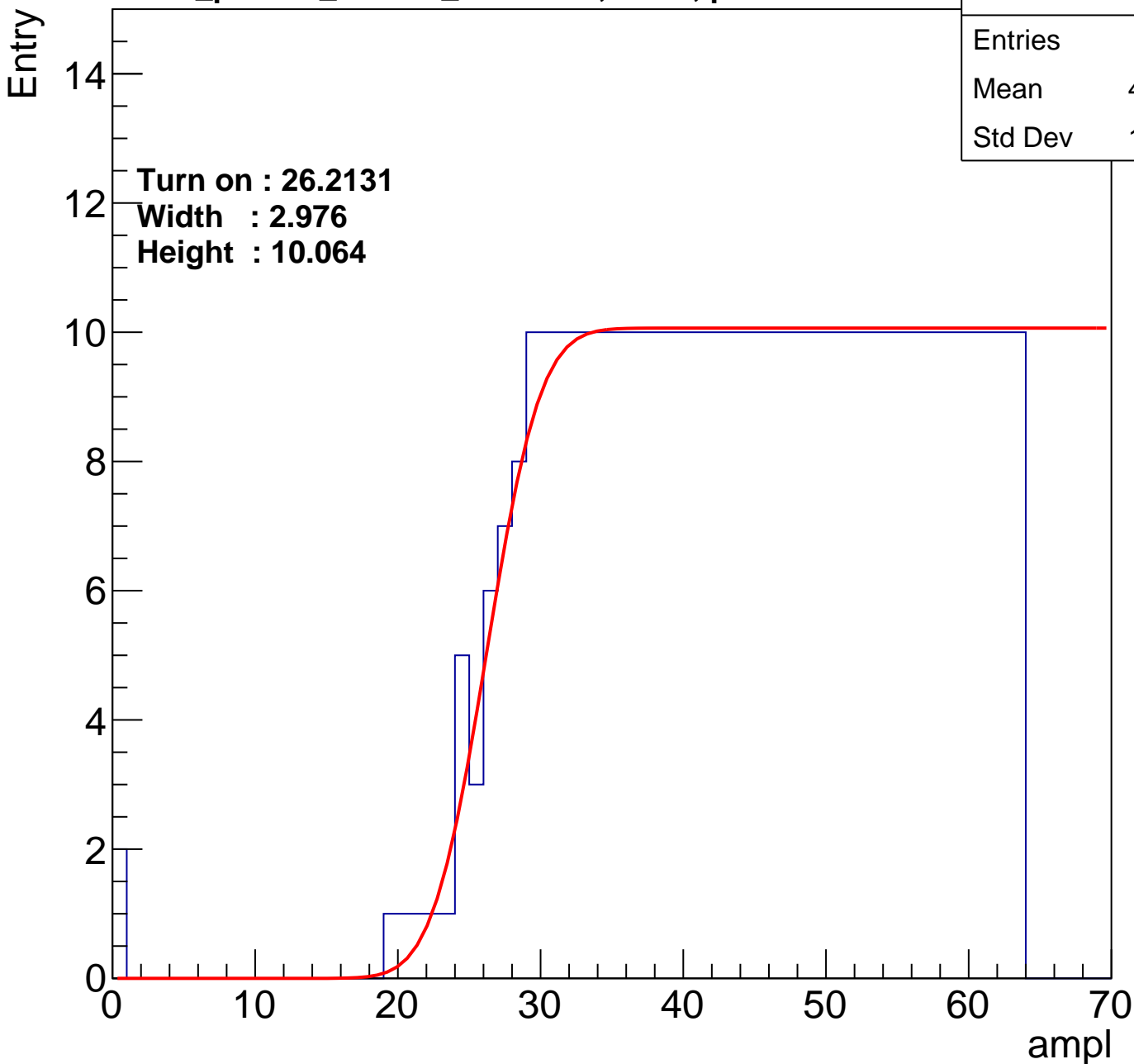
calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.96 |
| Std Dev | 11.69 |

Turn on : 26.2131

Width : 2.976

Height : 10.064



B1L000S, U26-ch99

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.32 |
| Std Dev | 11.47 |

Turn on : 26.7169

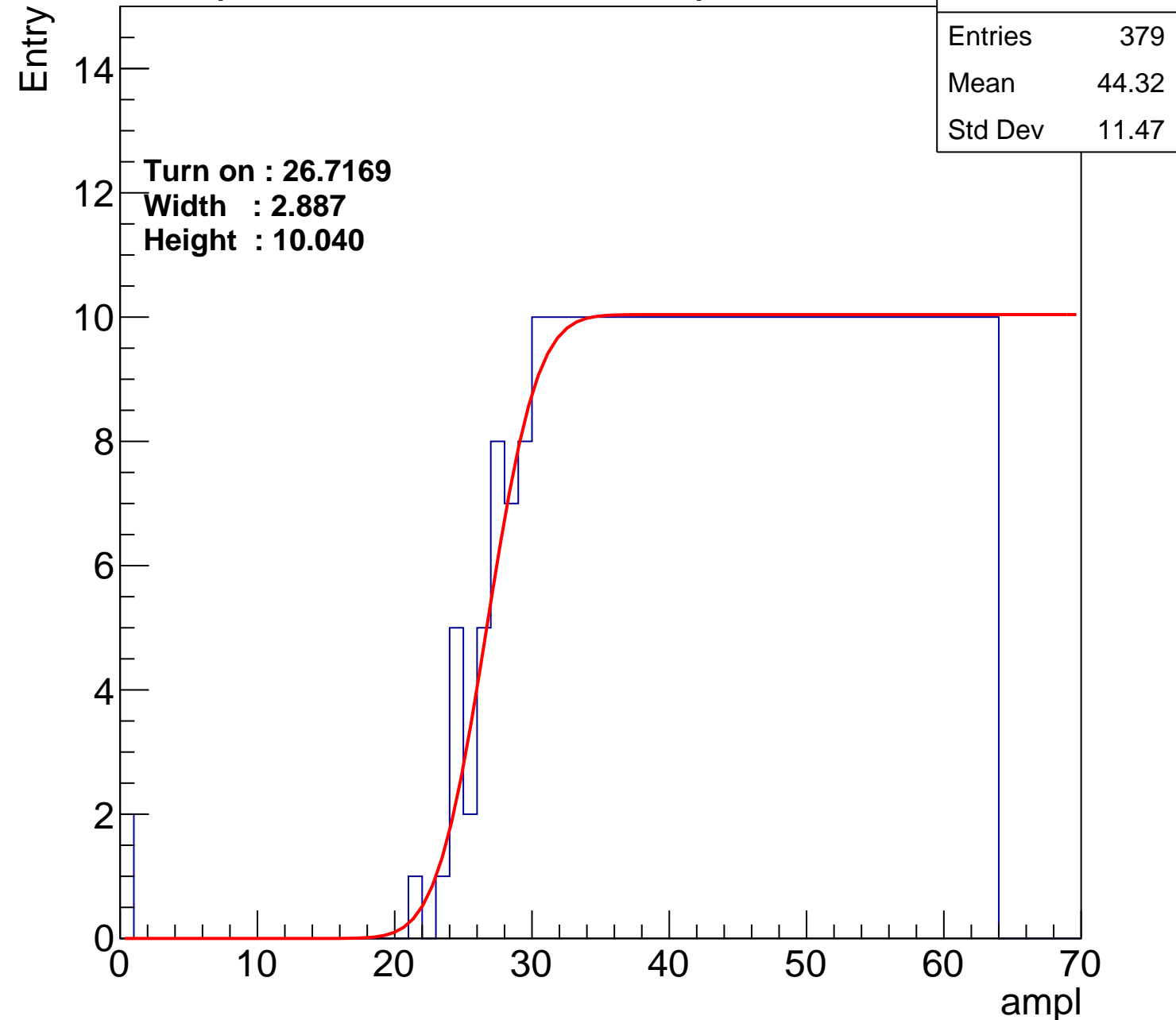
Width : 2.887

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch100

calib_packv5_042523_0143.root, FC#3, port B3

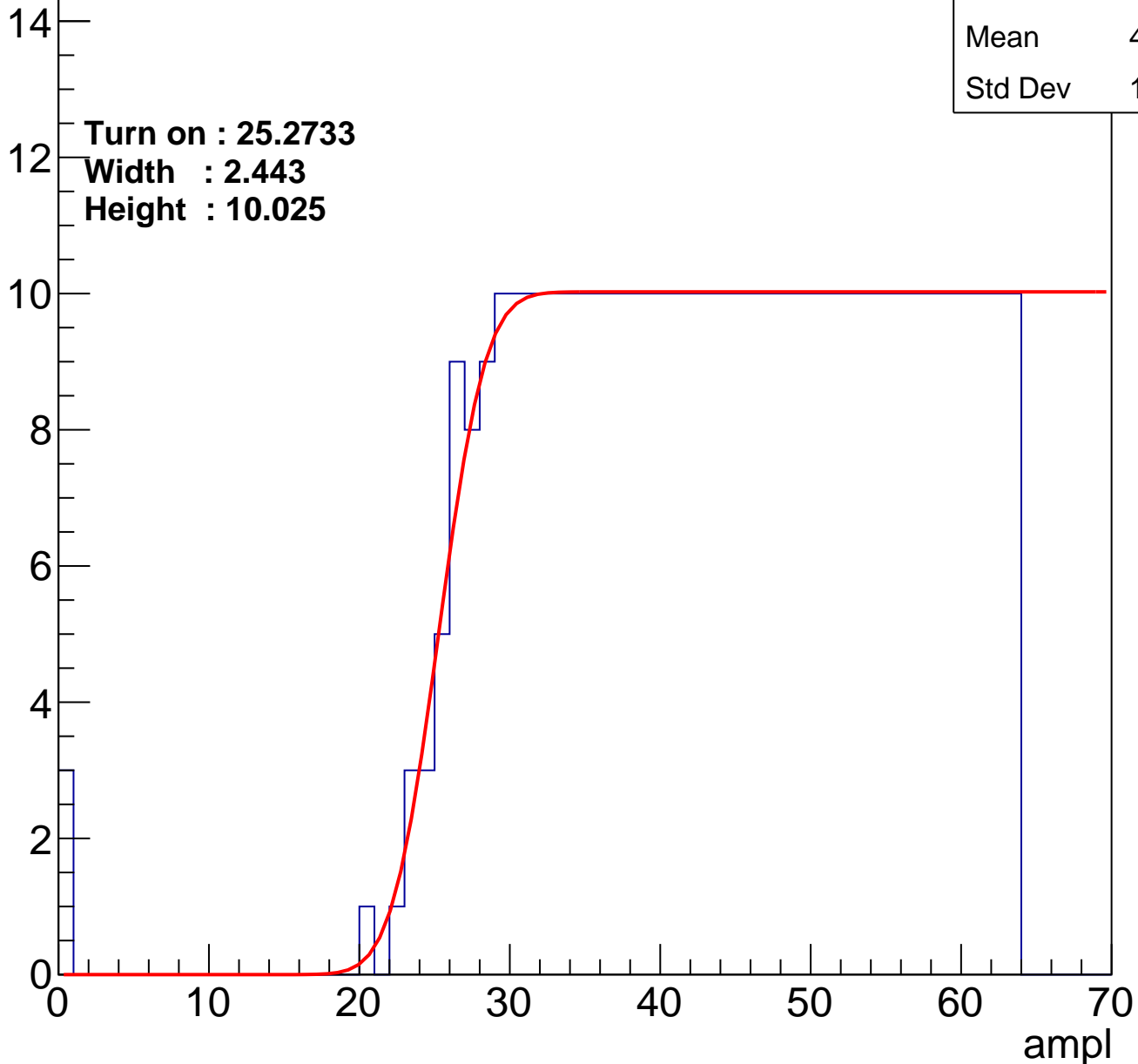
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.65 |
| Std Dev | 11.93 |

Turn on : 25.2733

Width : 2.443

Height : 10.025

Entry



B1L000S, U26-ch101

calib_packv5_042523_0143.root, FC#3, port B3

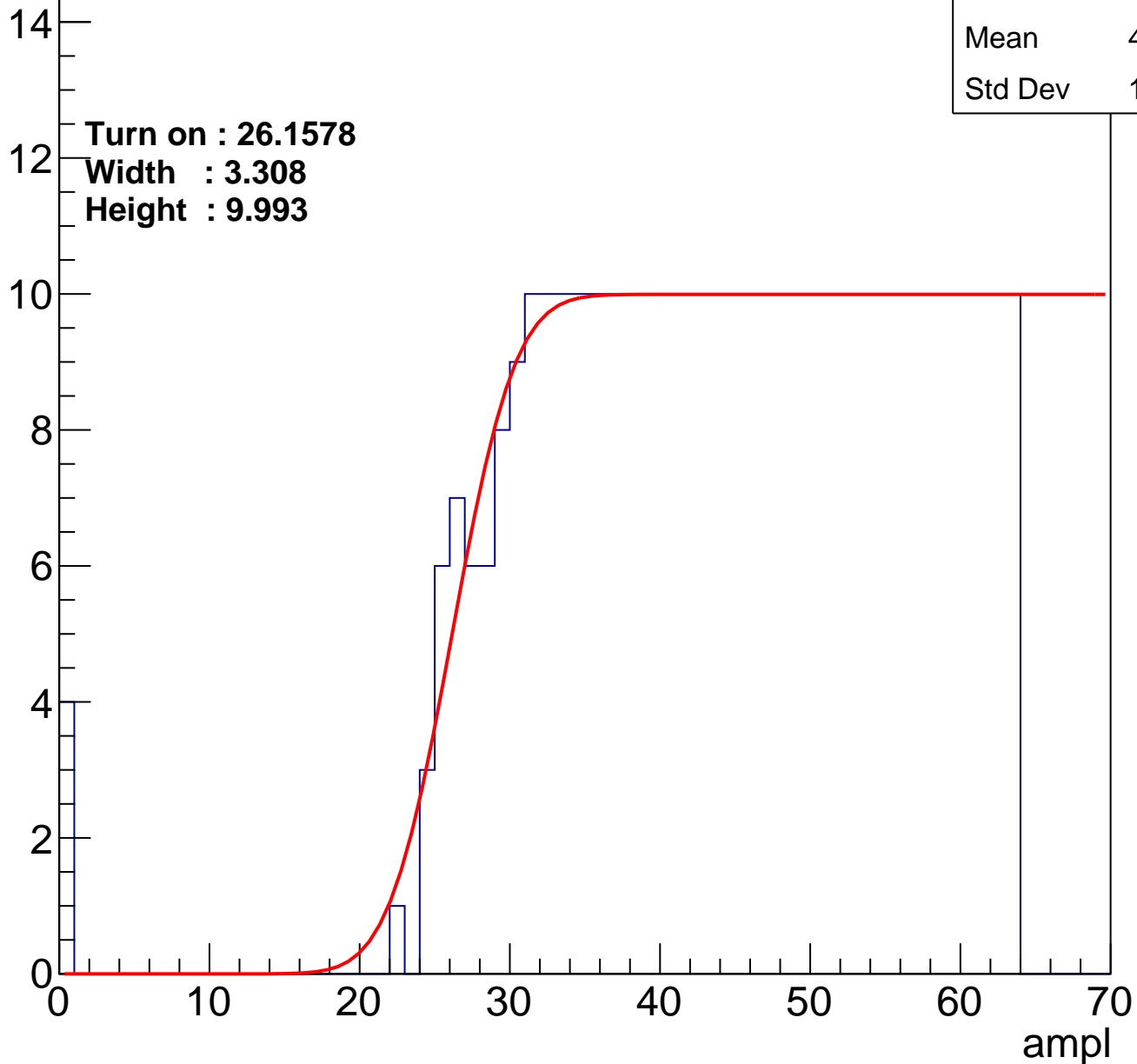
| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.13 |
| Std Dev | 11.87 |

Turn on : 26.1578

Width : 3.308

Height : 9.993

Entry



B1L000S, U26-ch102

calib_packv5_042523_0143.root, FC#3, port B3

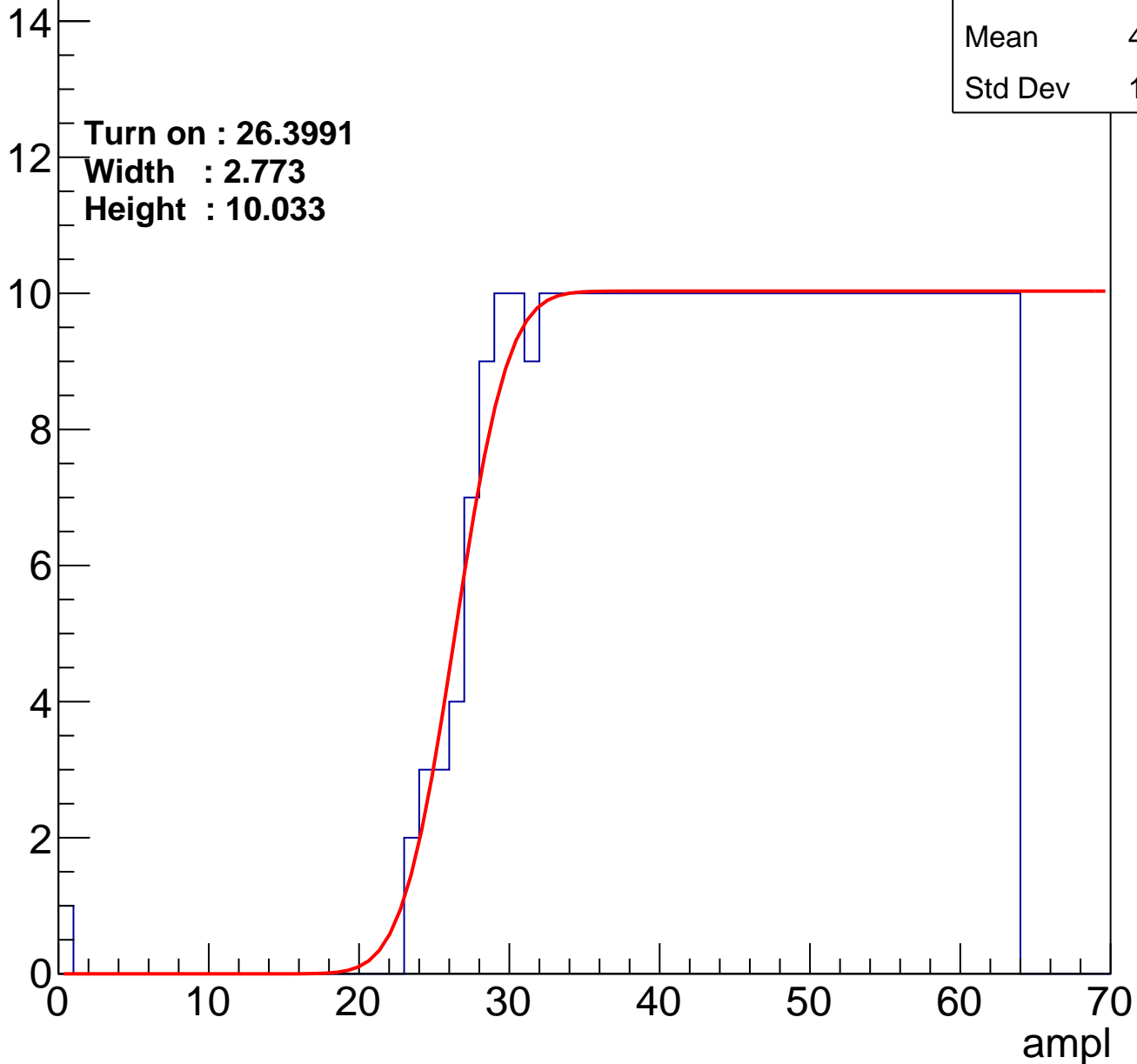
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.46 |
| Std Dev | 11.22 |

Turn on : 26.3991

Width : 2.773

Height : 10.033

Entry



B1L000S, U26-ch103

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.43 |
| Std Dev | 11.41 |

Turn on : 26.7543

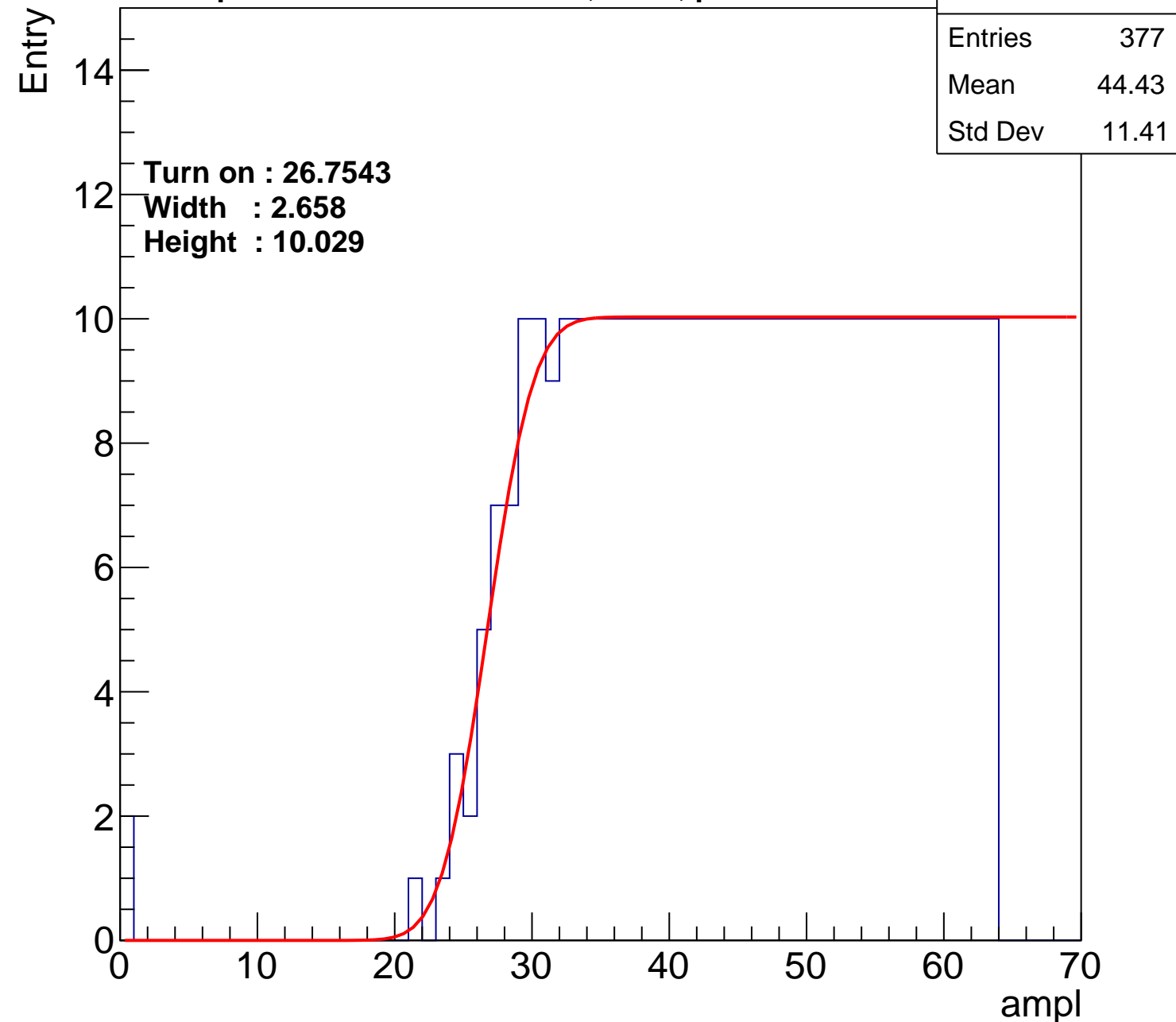
Width : 2.658

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch104

calib_packv5_042523_0143.root, FC#3, port B3

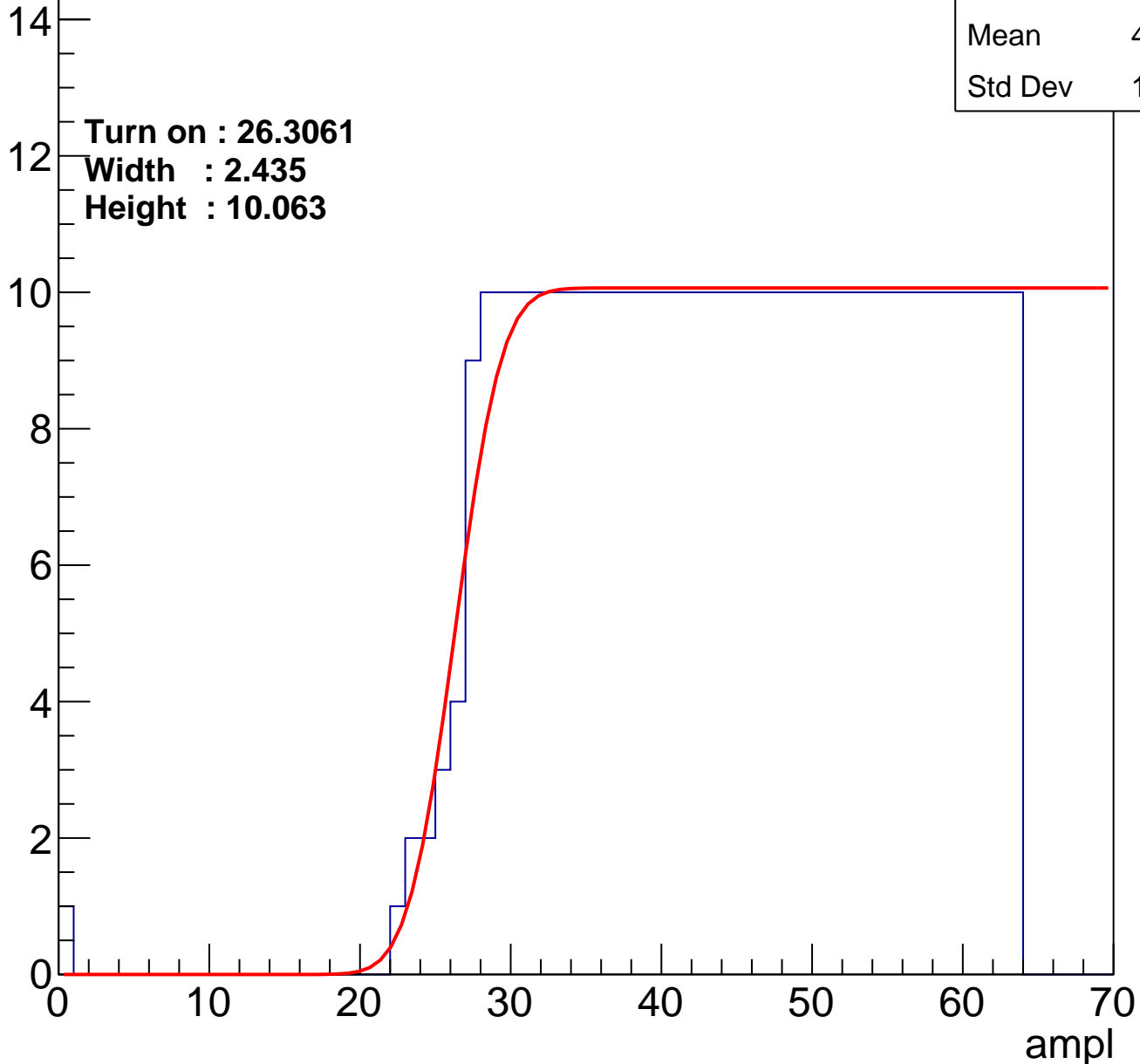
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.29 |
| Std Dev | 11.29 |

Turn on : 26.3061

Width : 2.435

Height : 10.063

Entry



B1L000S, U26-ch105

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.33 |
| Std Dev | 11.46 |

Turn on : 26.5170

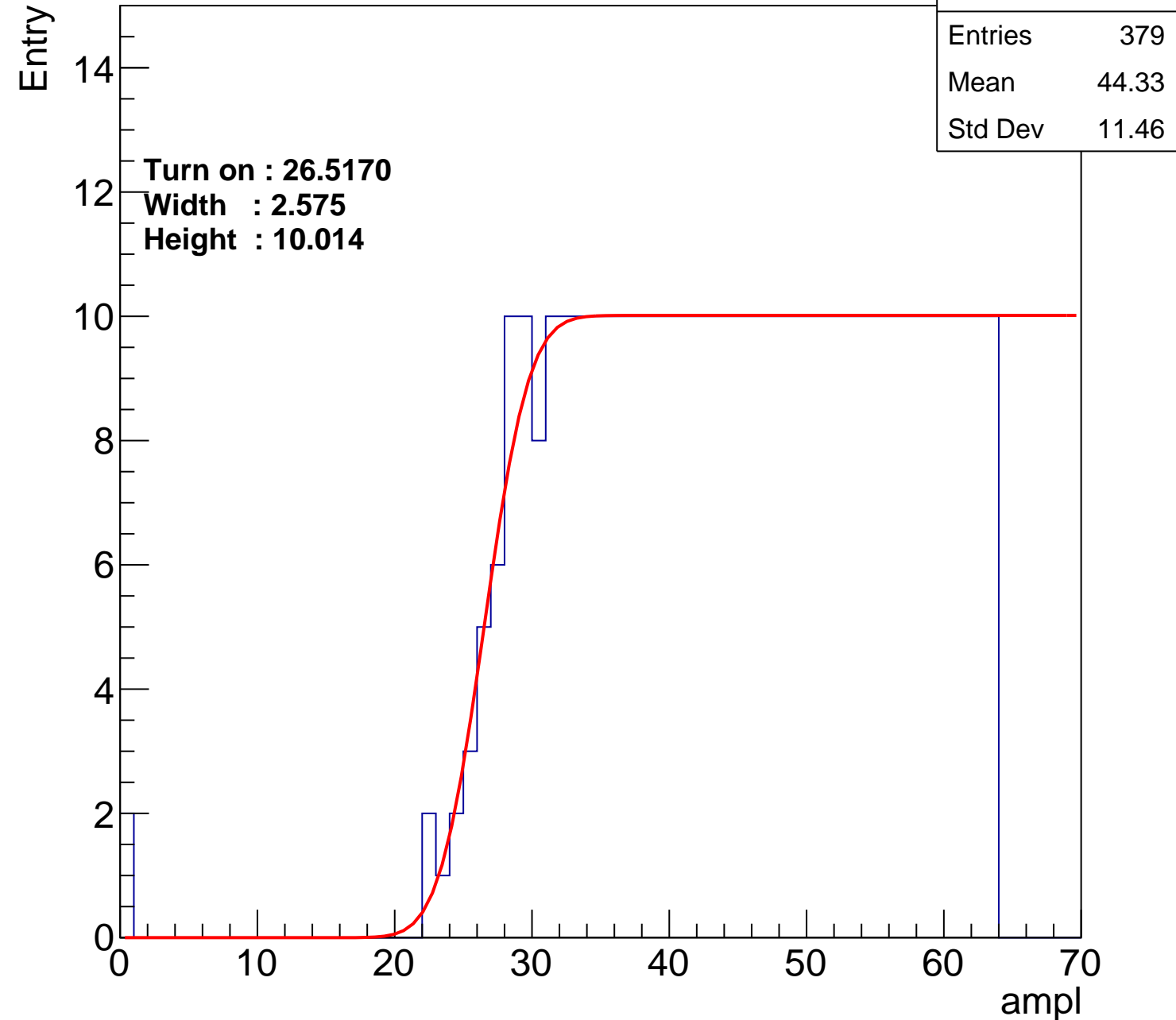
Width : 2.575

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch106

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.14 |
| Std Dev | 11.02 |

Turn on : 28.2172

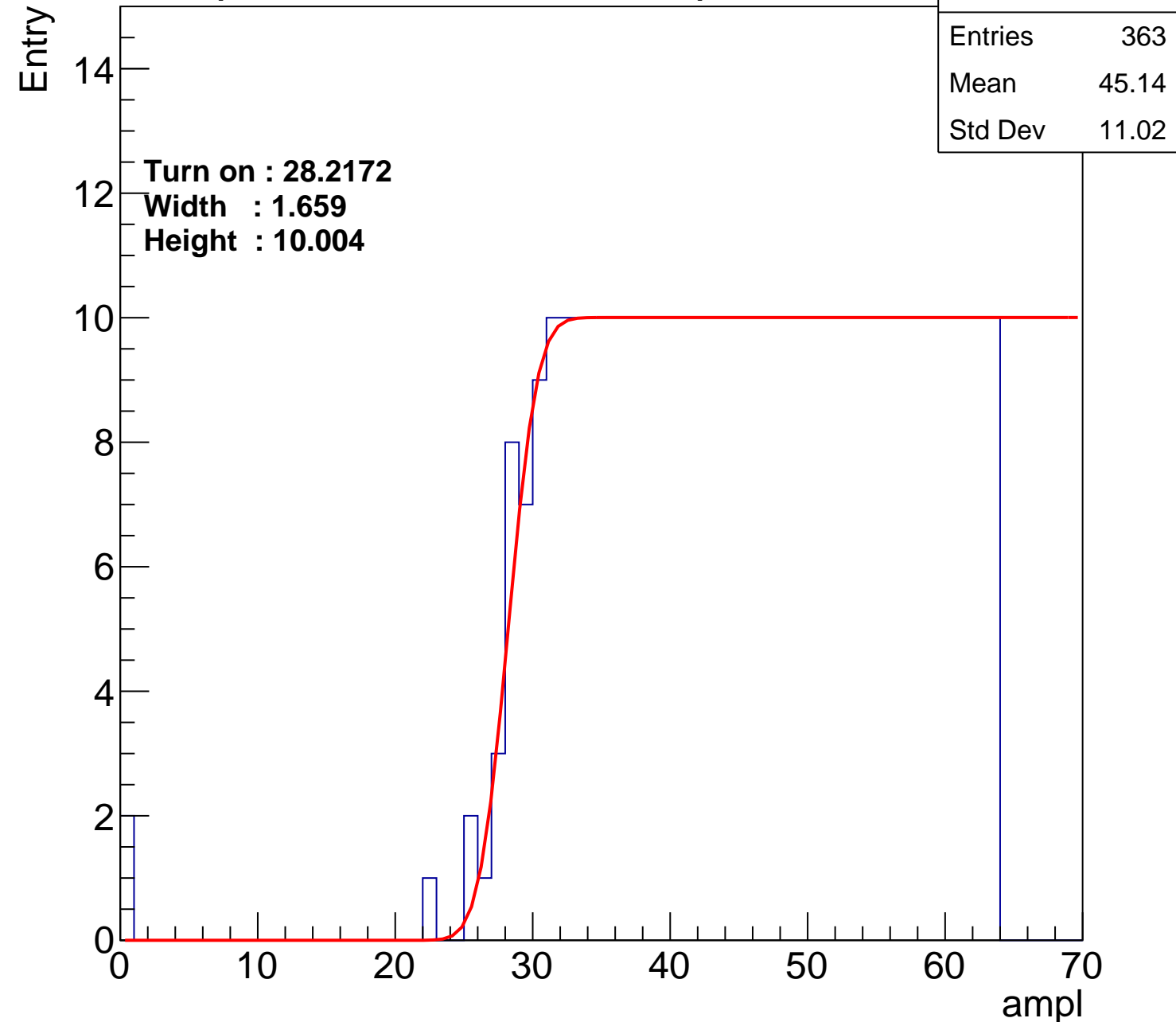
Width : 1.659

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch107

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.71 |
| Std Dev | 11.09 |

Turn on : 27.4718

Width : 2.125

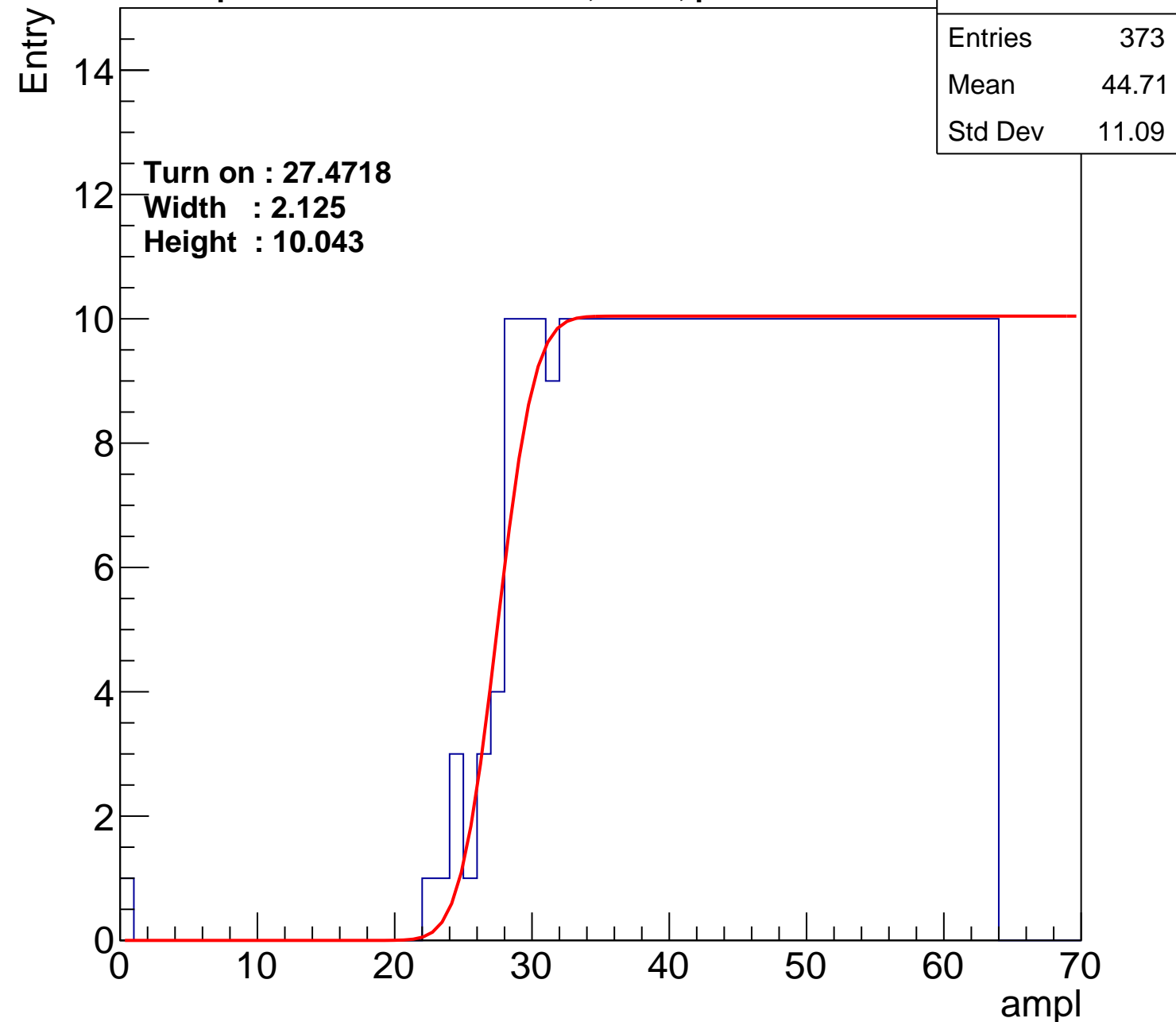
Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U26-ch108

calib_packv5_042523_0143.root, FC#3, port B3

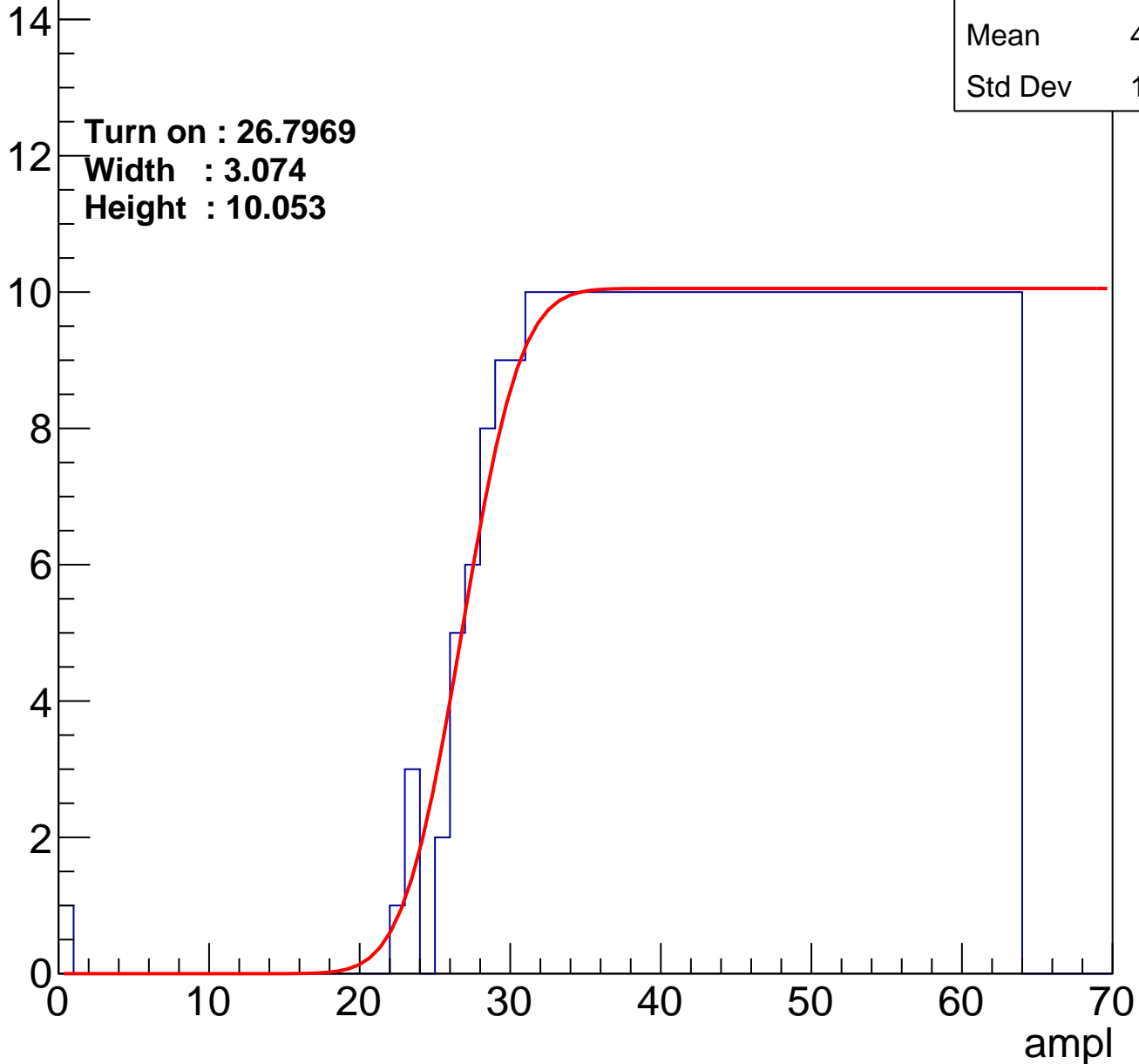
| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.65 |
| Std Dev | 11.14 |

Turn on : 26.7969

Width : 3.074

Height : 10.053

Entry



B1L000S, U26-ch109

calib_packv5_042523_0143.root, FC#3, port B3

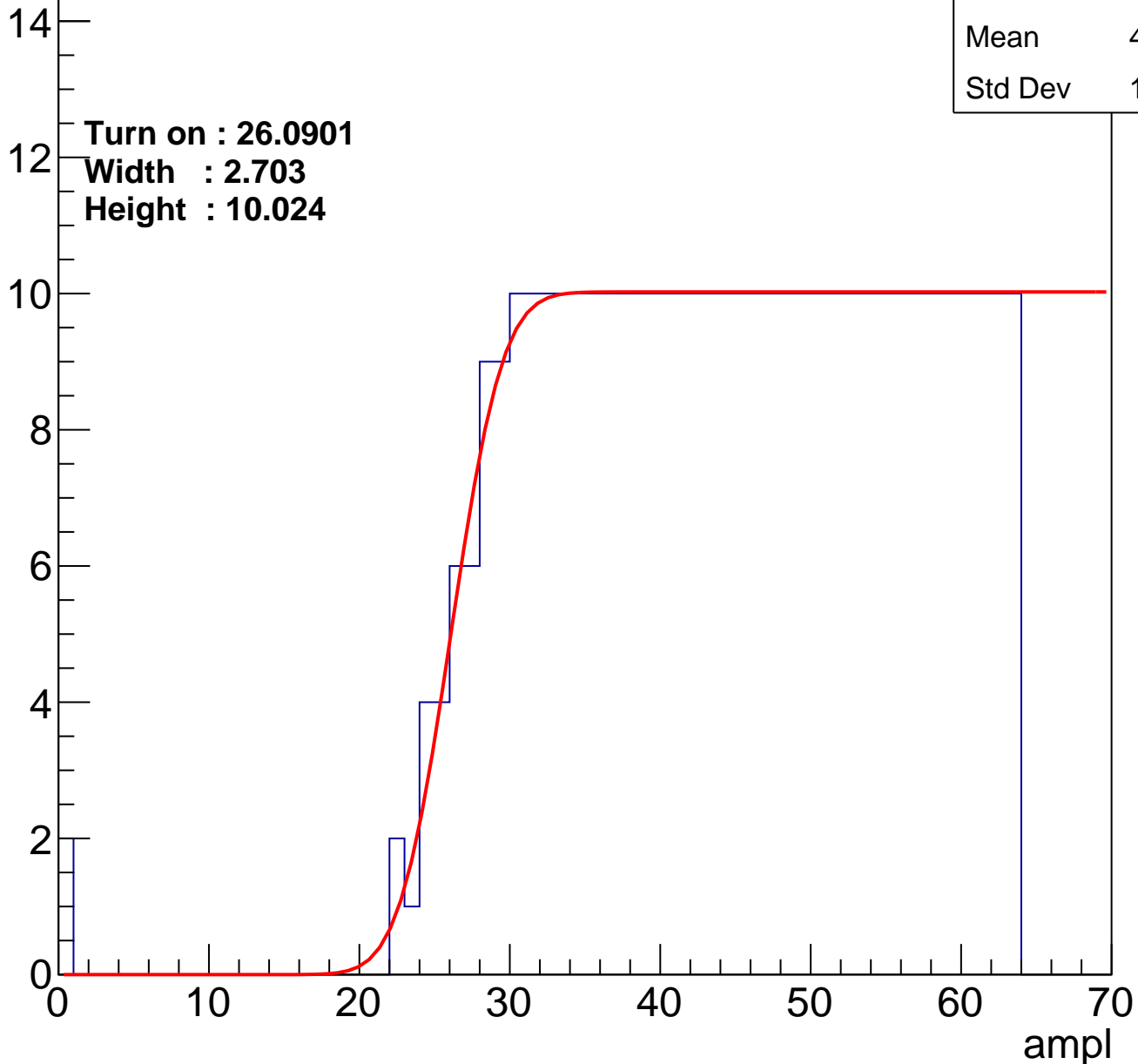
| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.14 |
| Std Dev | 11.56 |

Turn on : 26.0901

Width : 2.703

Height : 10.024

Entry



B1L000S, U26-ch110

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.66 |
| Std Dev | 11.91 |

Turn on : 24.9511

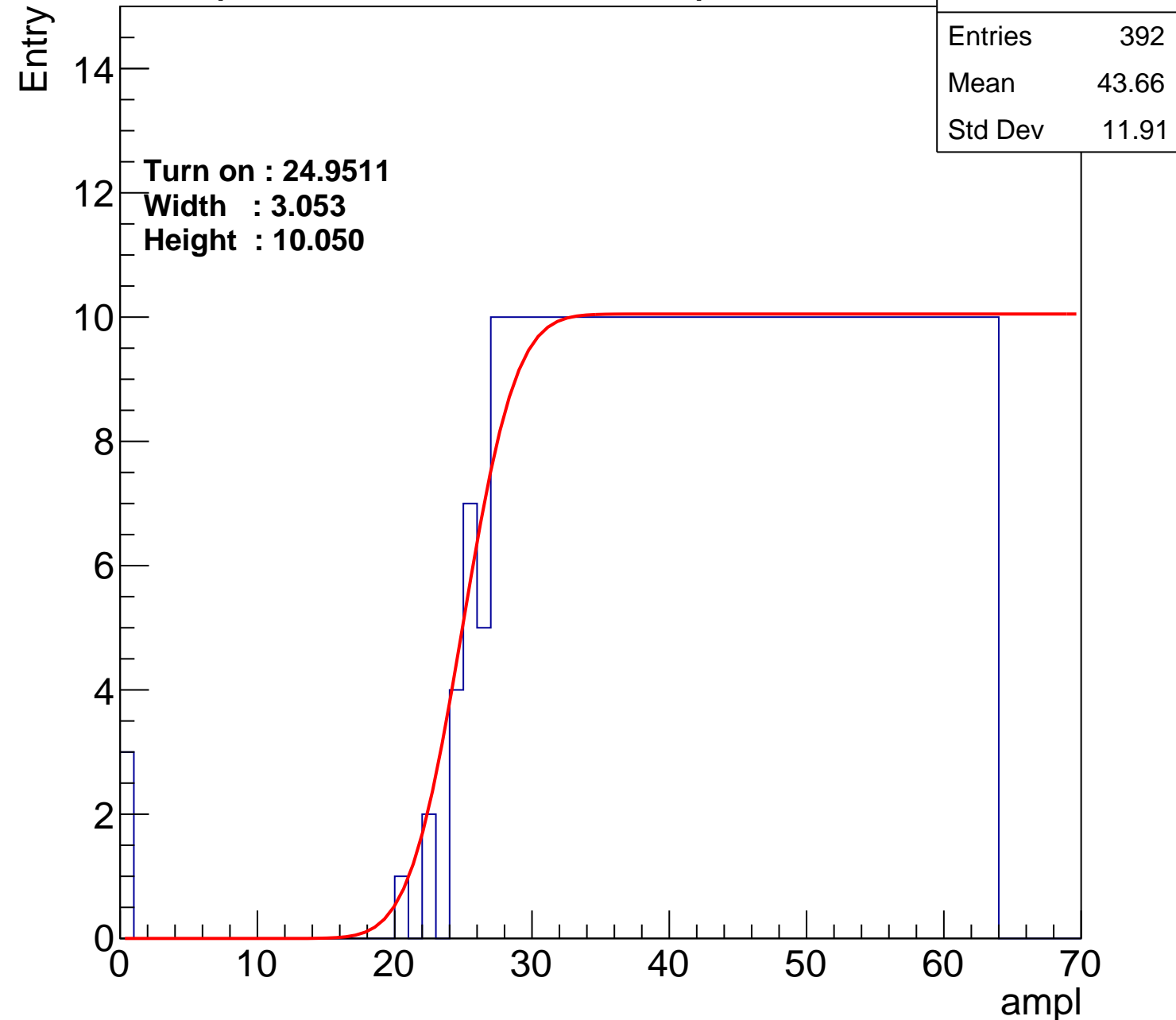
Width : 3.053

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch111

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-----|
| Entries | 360 |
|---------|-----|

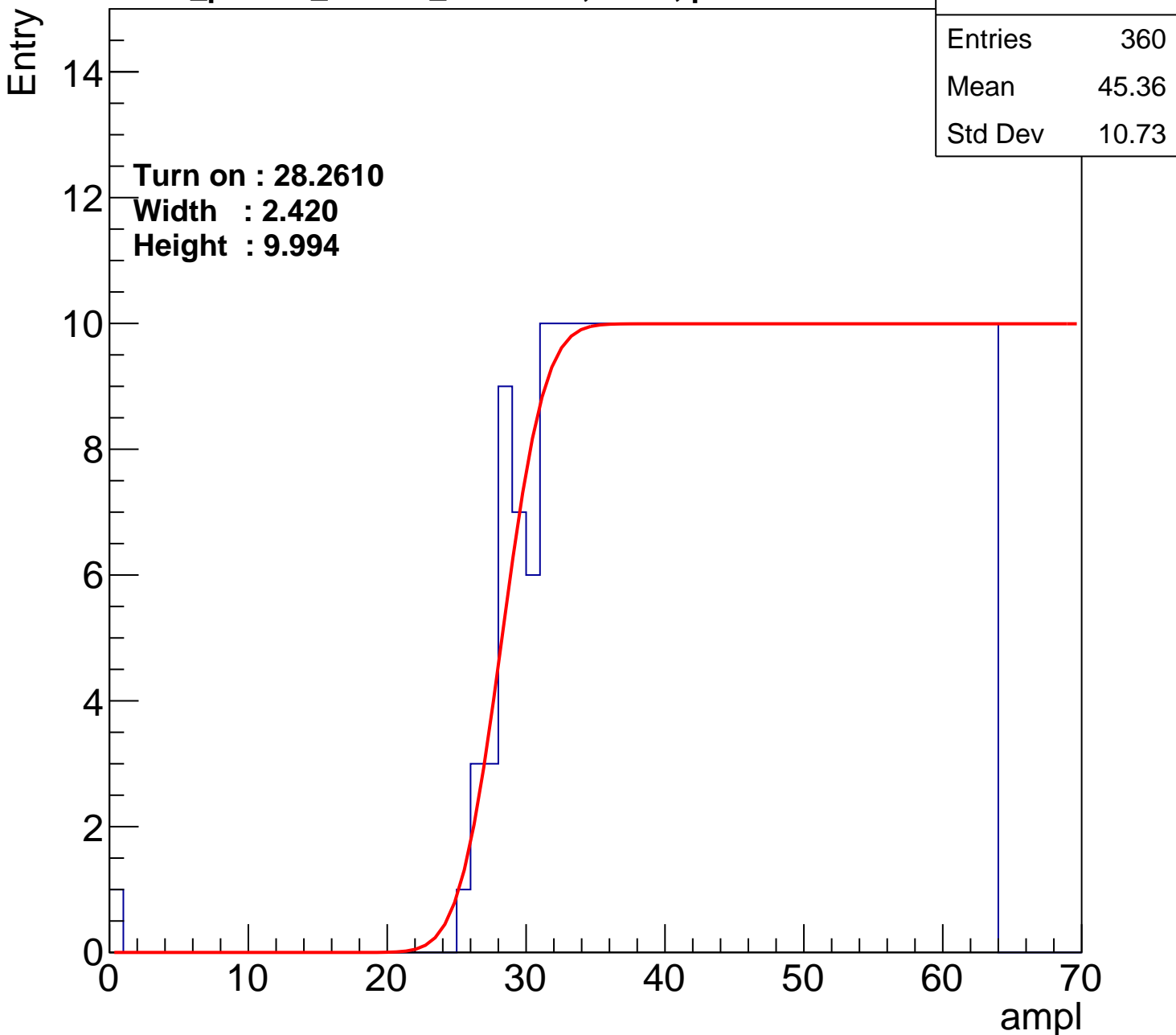
| | |
|------|-------|
| Mean | 45.36 |
|------|-------|

| | |
|---------|-------|
| Std Dev | 10.73 |
|---------|-------|

Turn on : 28.2610

Width : 2.420

Height : 9.994



B1L000S, U26-ch112

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.64 |
| Std Dev | 11.66 |

Turn on : 27.5366

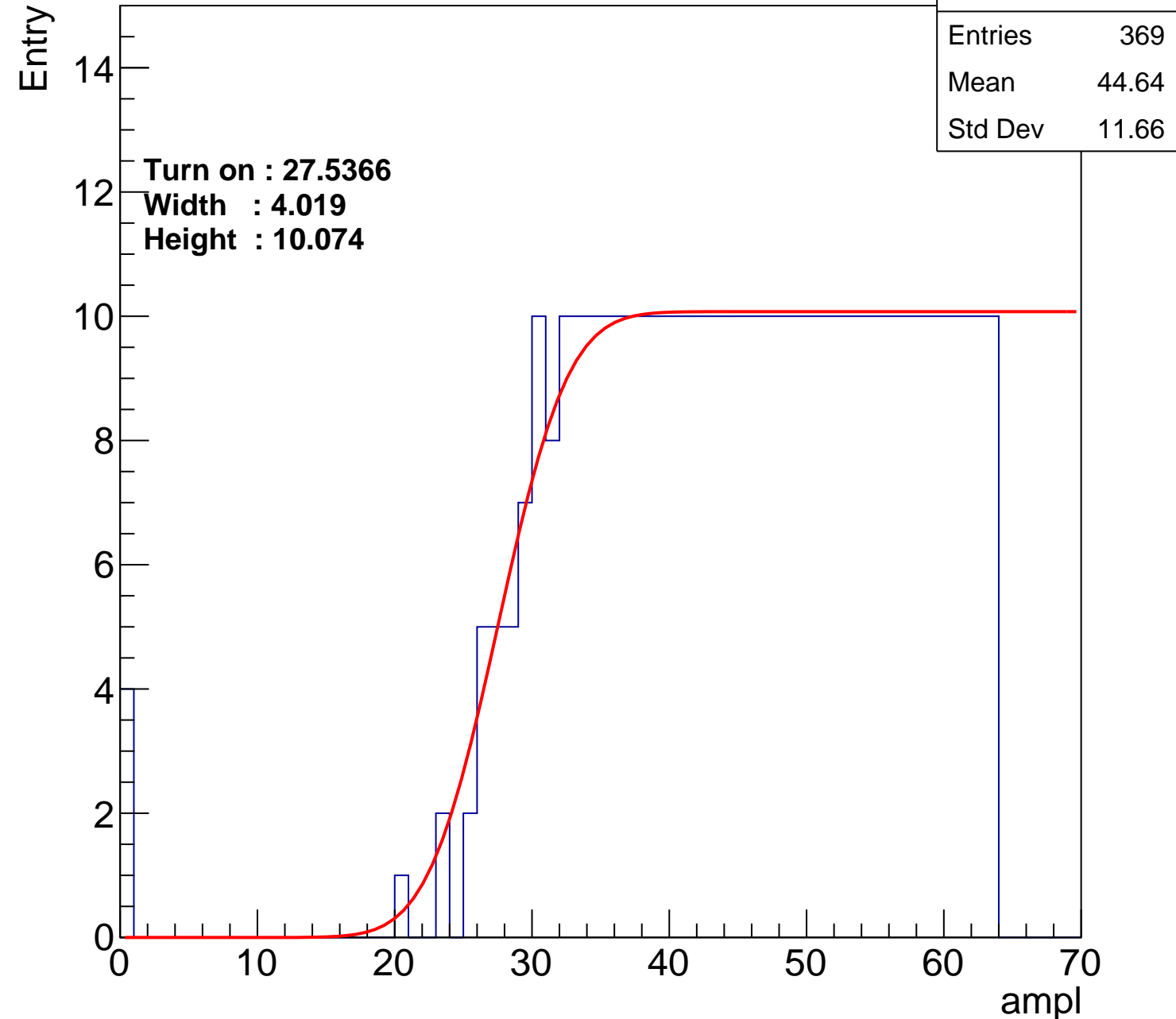
Width : 4.019

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch113

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.11 |
| Std Dev | 12.02 |

Turn on : 27.1842

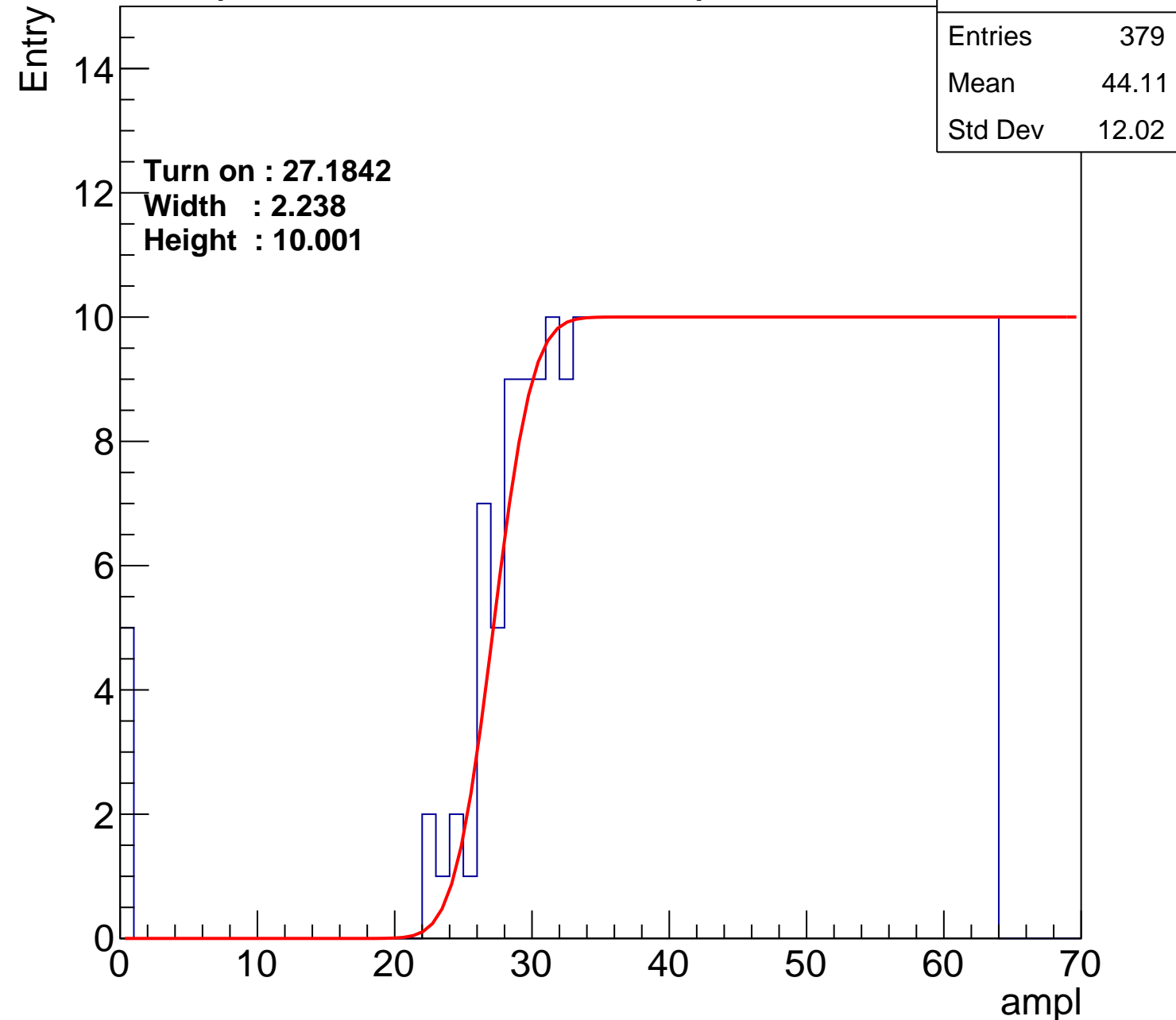
Width : 2.238

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch114

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.29 |
| Std Dev | 11.59 |

Turn on : 26.1424

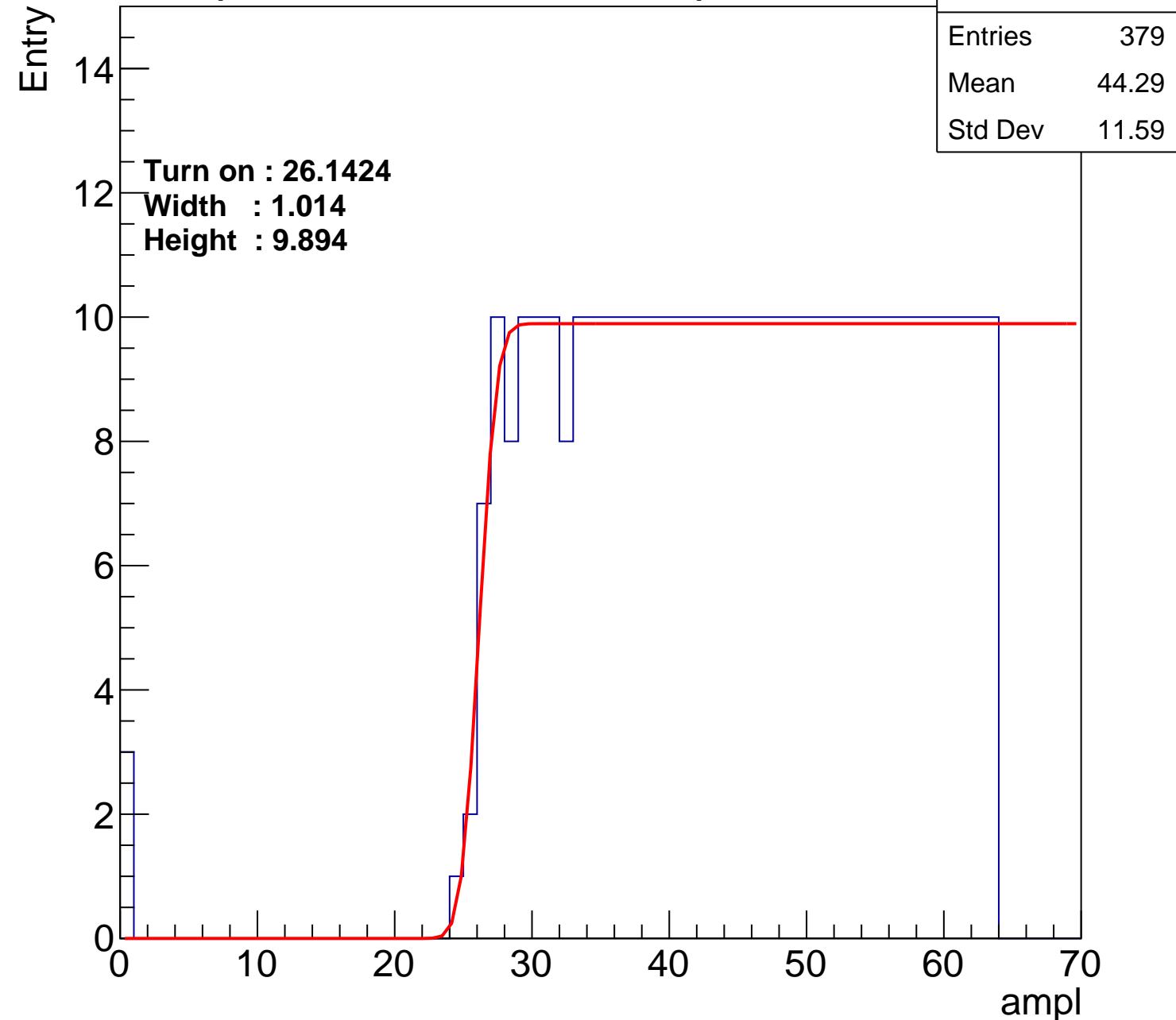
Width : 1.014

Height : 9.894

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch115

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|------|
| Entries | 391 |
| Mean | 43.8 |
| Std Dev | 11.6 |

Turn on : 25.0859

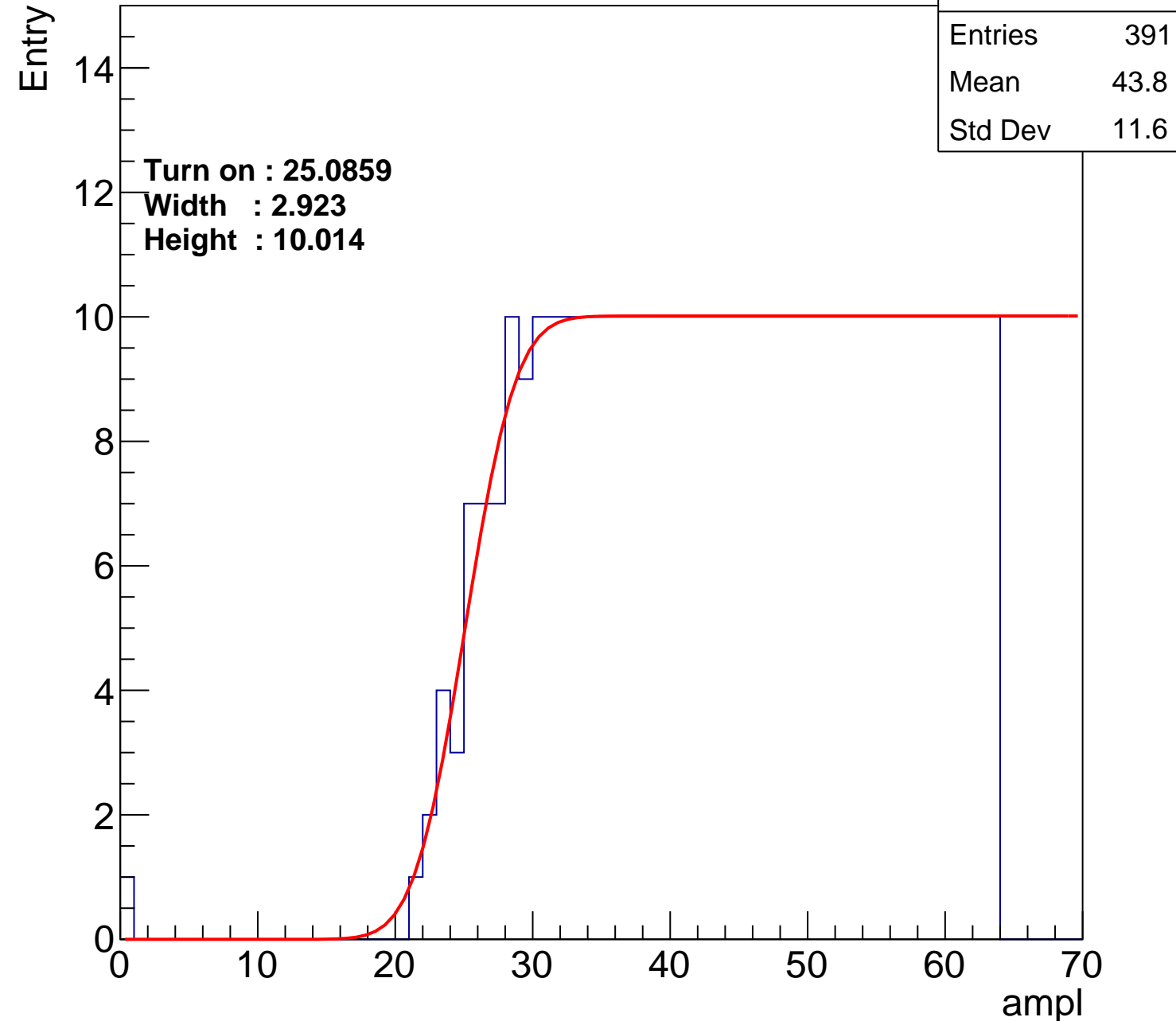
Width : 2.923

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch116

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.34 |
| Std Dev | 11.31 |

Turn on : 26.2889

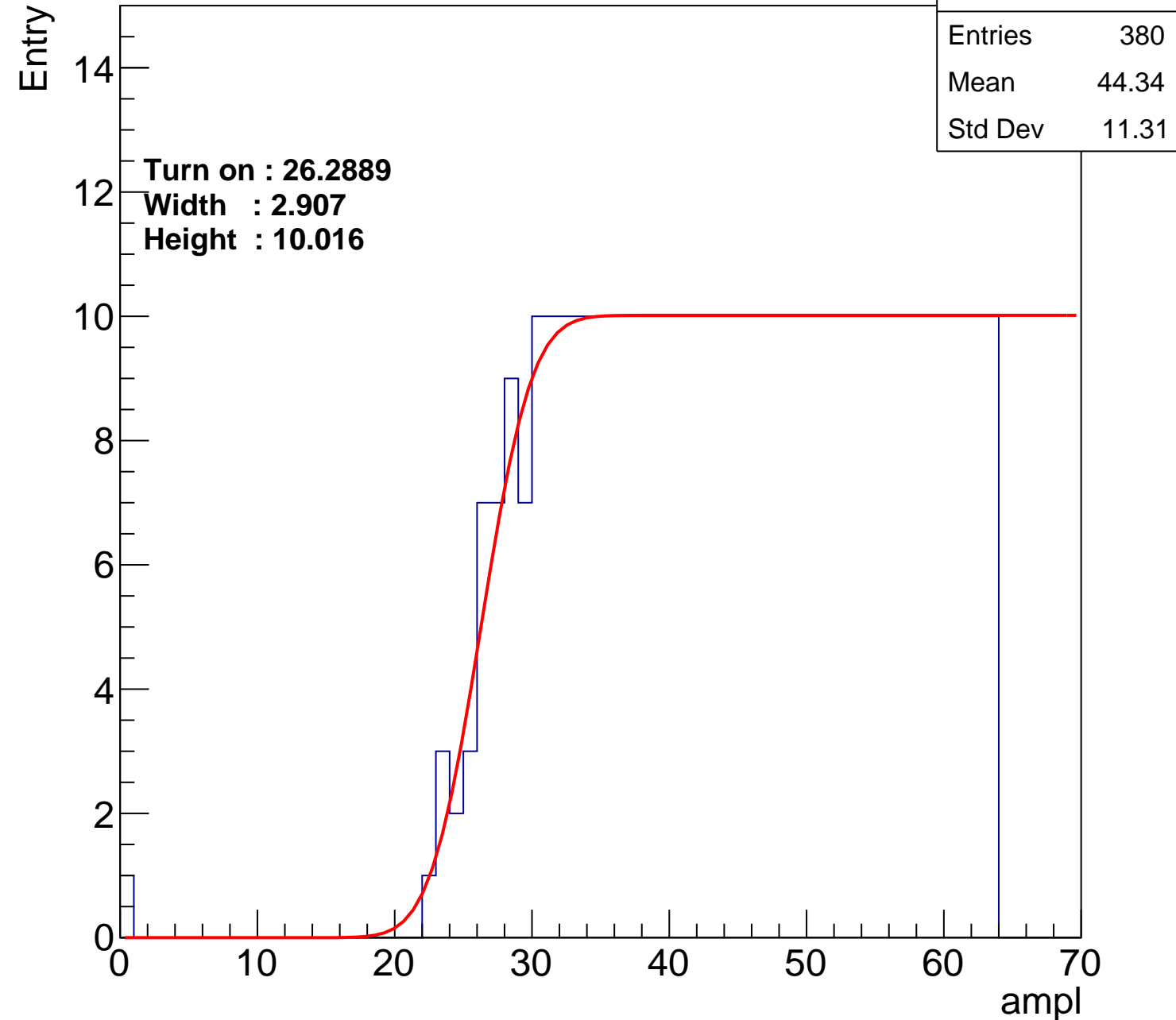
Width : 2.907

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch117

calib_packv5_042523_0143.root, FC#3, port B3

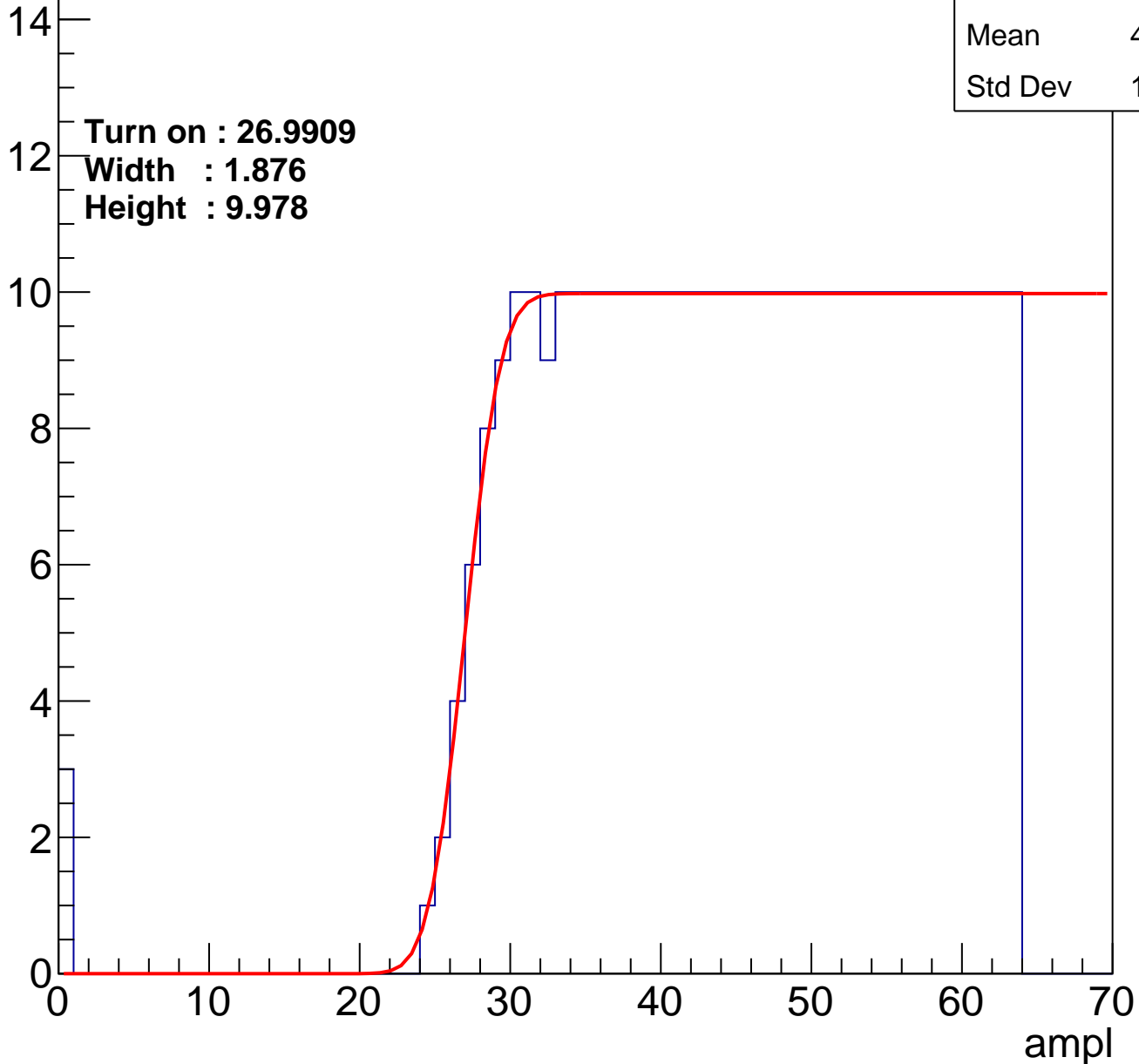
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.63 |
| Std Dev | 11.43 |

Turn on : 26.9909

Width : 1.876

Height : 9.978

Entry



B1L000S, U26-ch118

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.08 |
| Std Dev | 11.72 |

Turn on : 26.1623

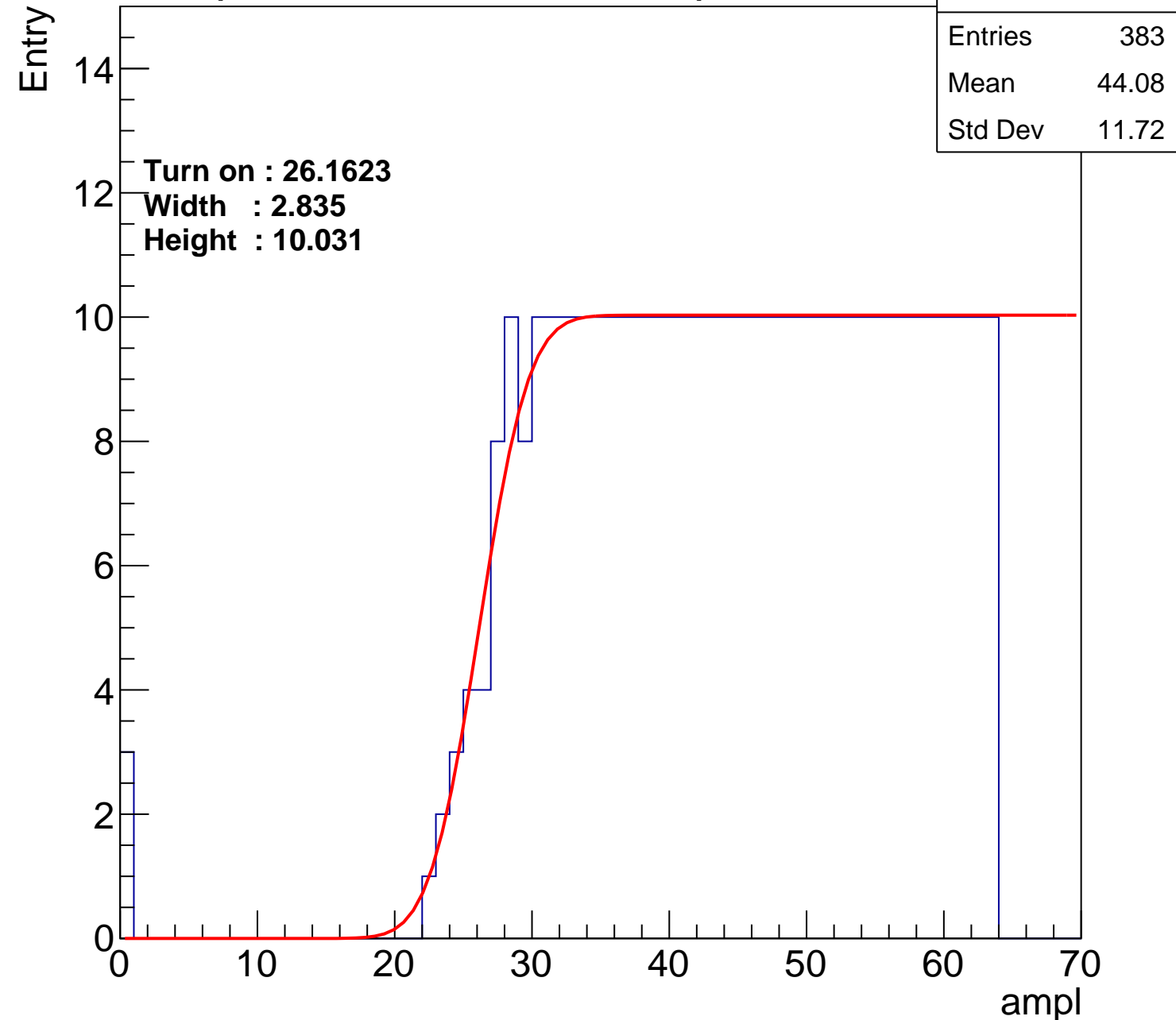
Width : 2.835

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch119

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.3 |
| Std Dev | 11.32 |

Turn on : 28.8905

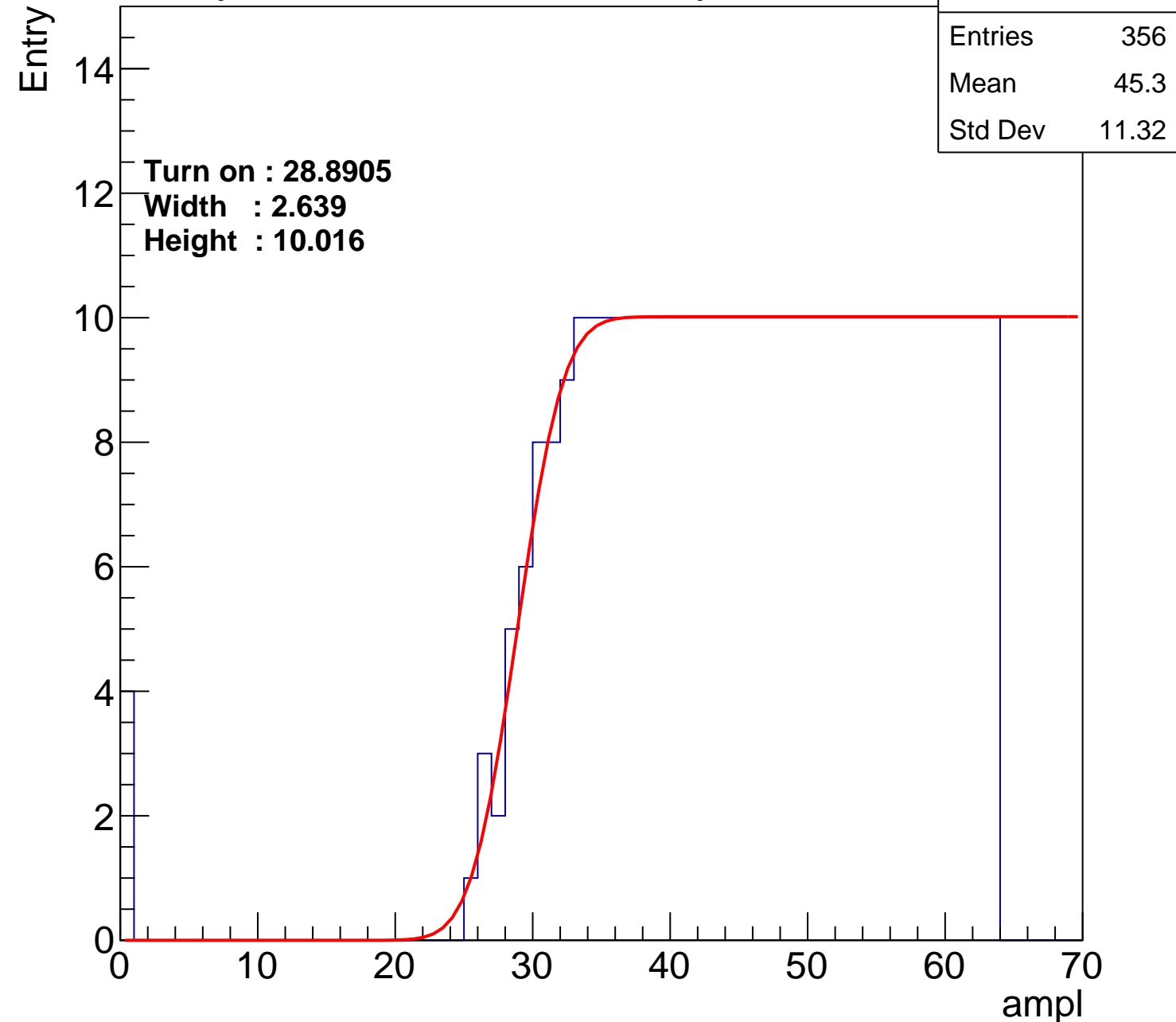
Width : 2.639

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch120

calib_packv5_042523_0143.root, FC#3, port B3

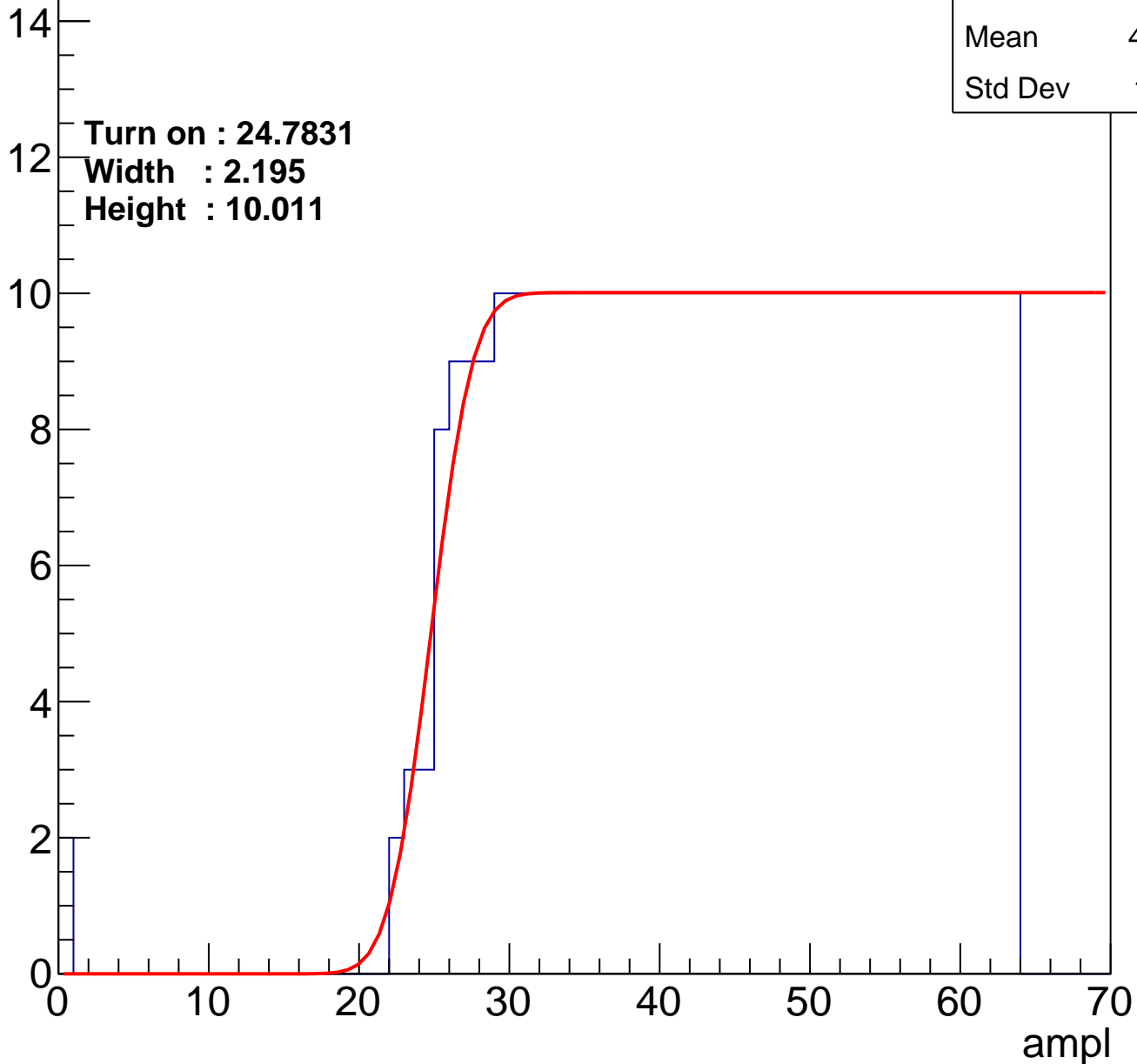
| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.58 |
| Std Dev | 11.81 |

Turn on : 24.7831

Width : 2.195

Height : 10.011

Entry



B1L000S, U26-ch121

calib_packv5_042523_0143.root, FC#3, port B3

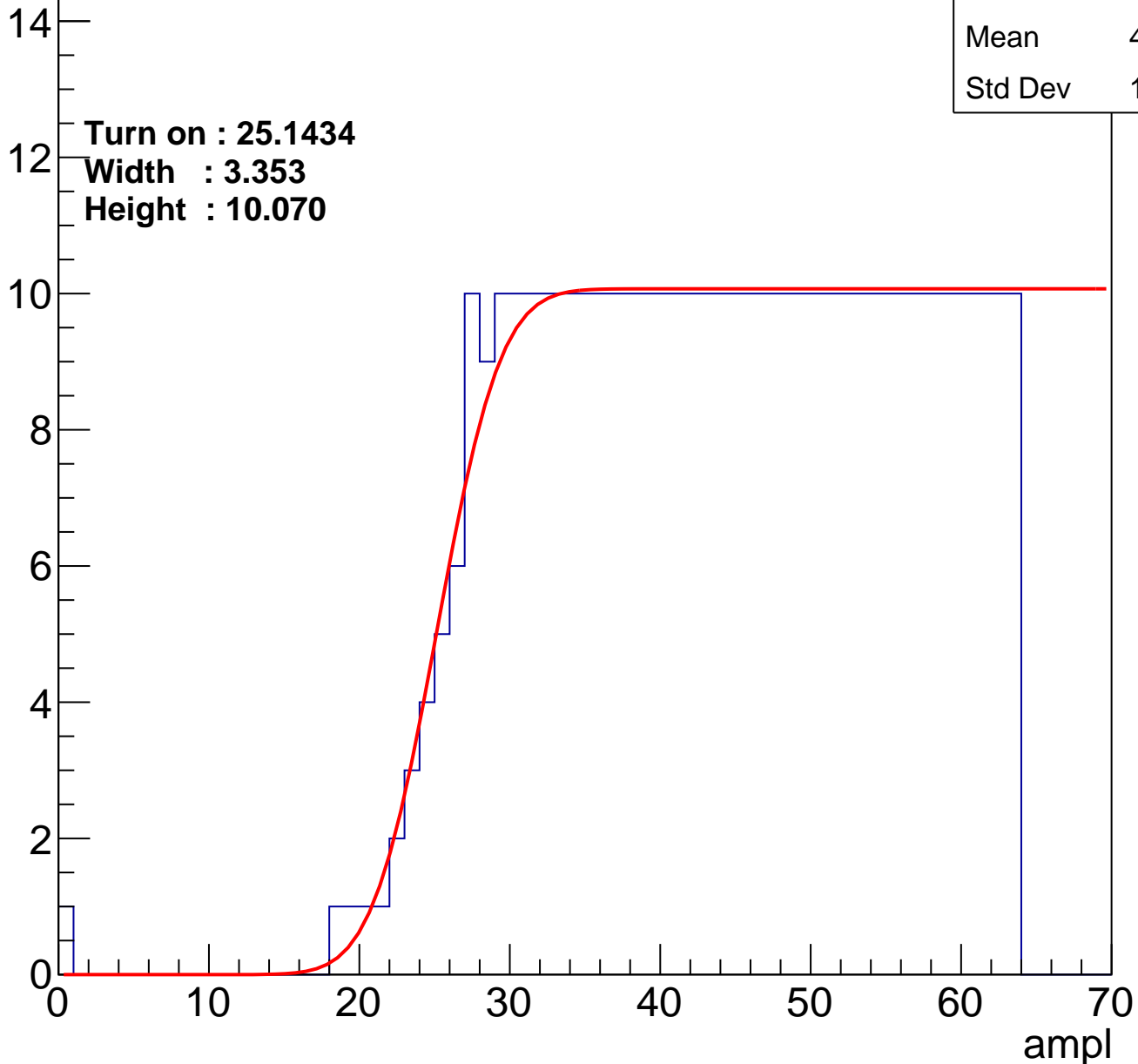
| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.63 |
| Std Dev | 11.73 |

Turn on : 25.1434

Width : 3.353

Height : 10.070

Entry



B1L000S, U26-ch122

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.59 |
| Std Dev | 11.31 |

Turn on : 26.6888

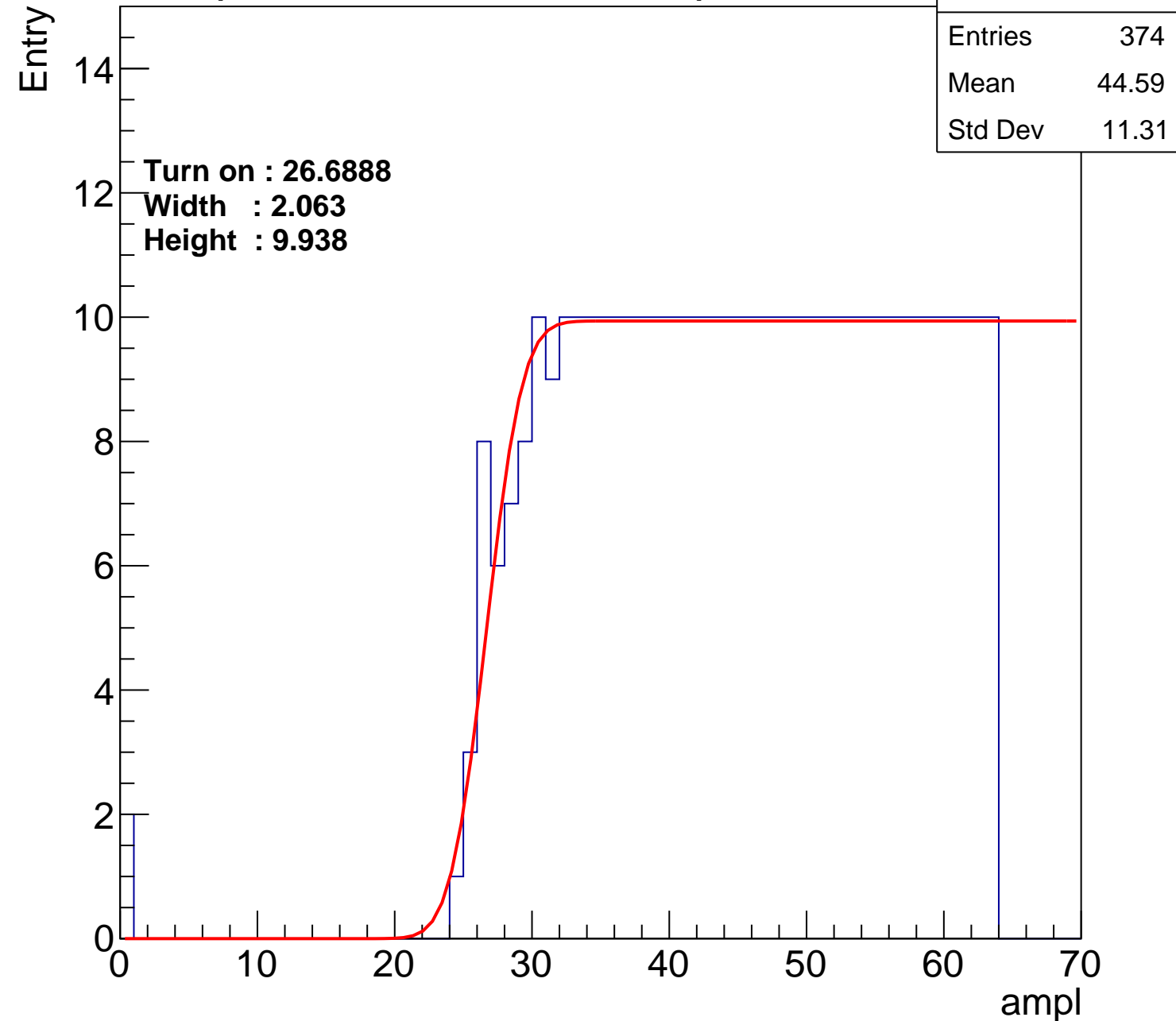
Width : 2.063

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch123

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.82 |
| Std Dev | 11.21 |

Turn on : 27.2780

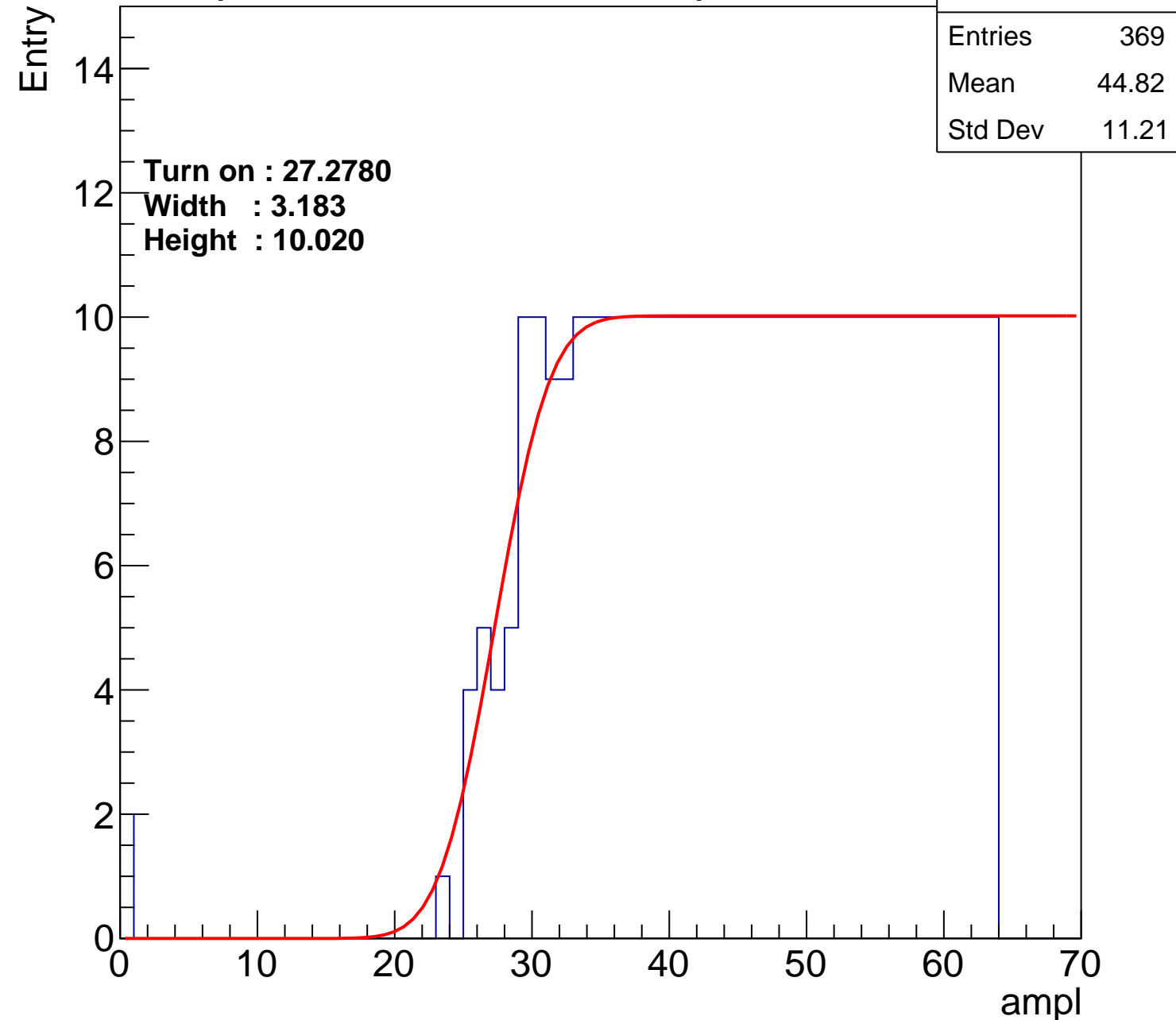
Width : 3.183

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch124

calib_packv5_042523_0143.root, FC#3, port B3

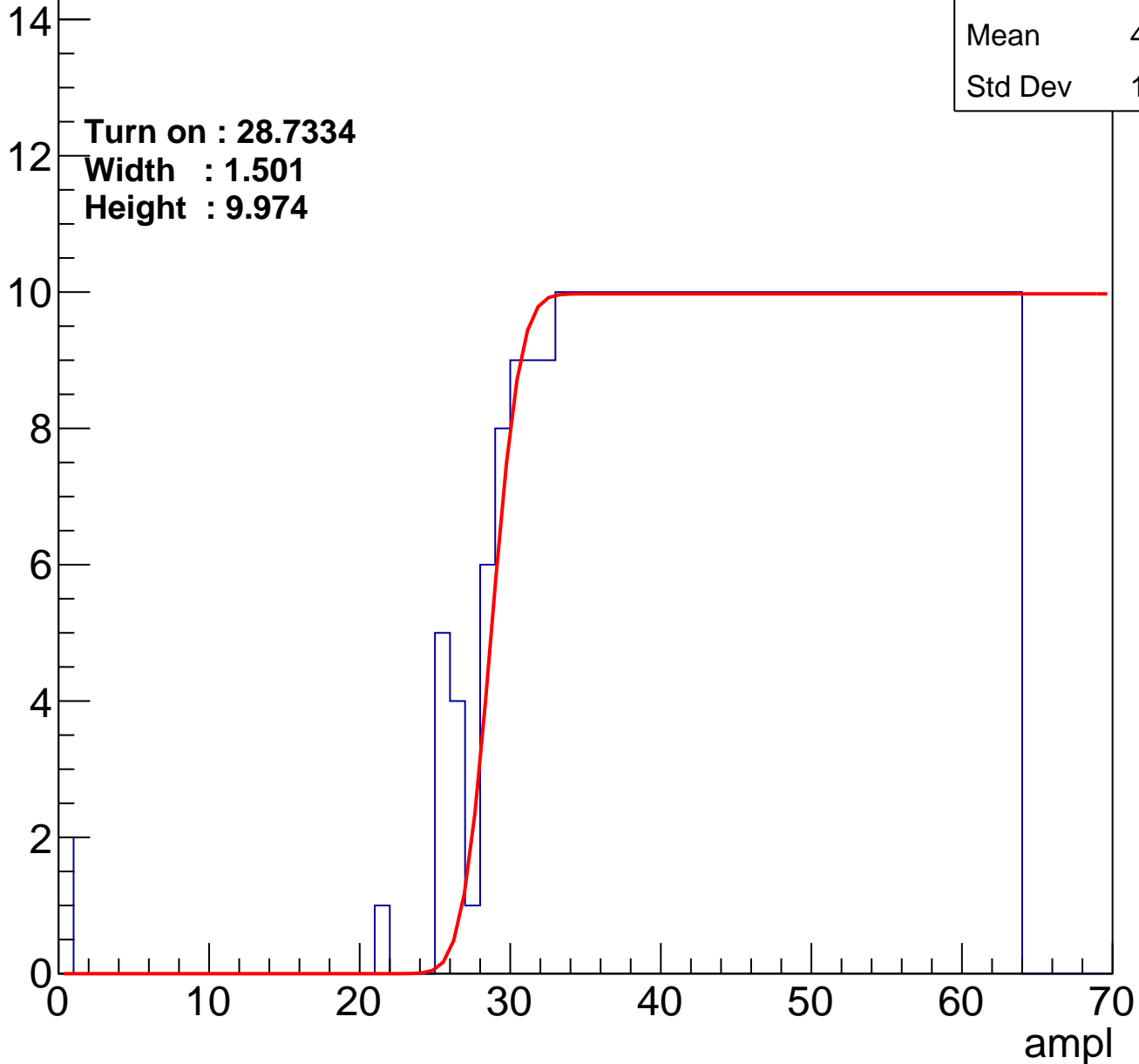
| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.04 |
| Std Dev | 11.13 |

Turn on : 28.7334

Width : 1.501

Height : 9.974

Entry



B1L000S, U26-ch125

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.77 |
| Std Dev | 11.37 |

Turn on : 27.3508

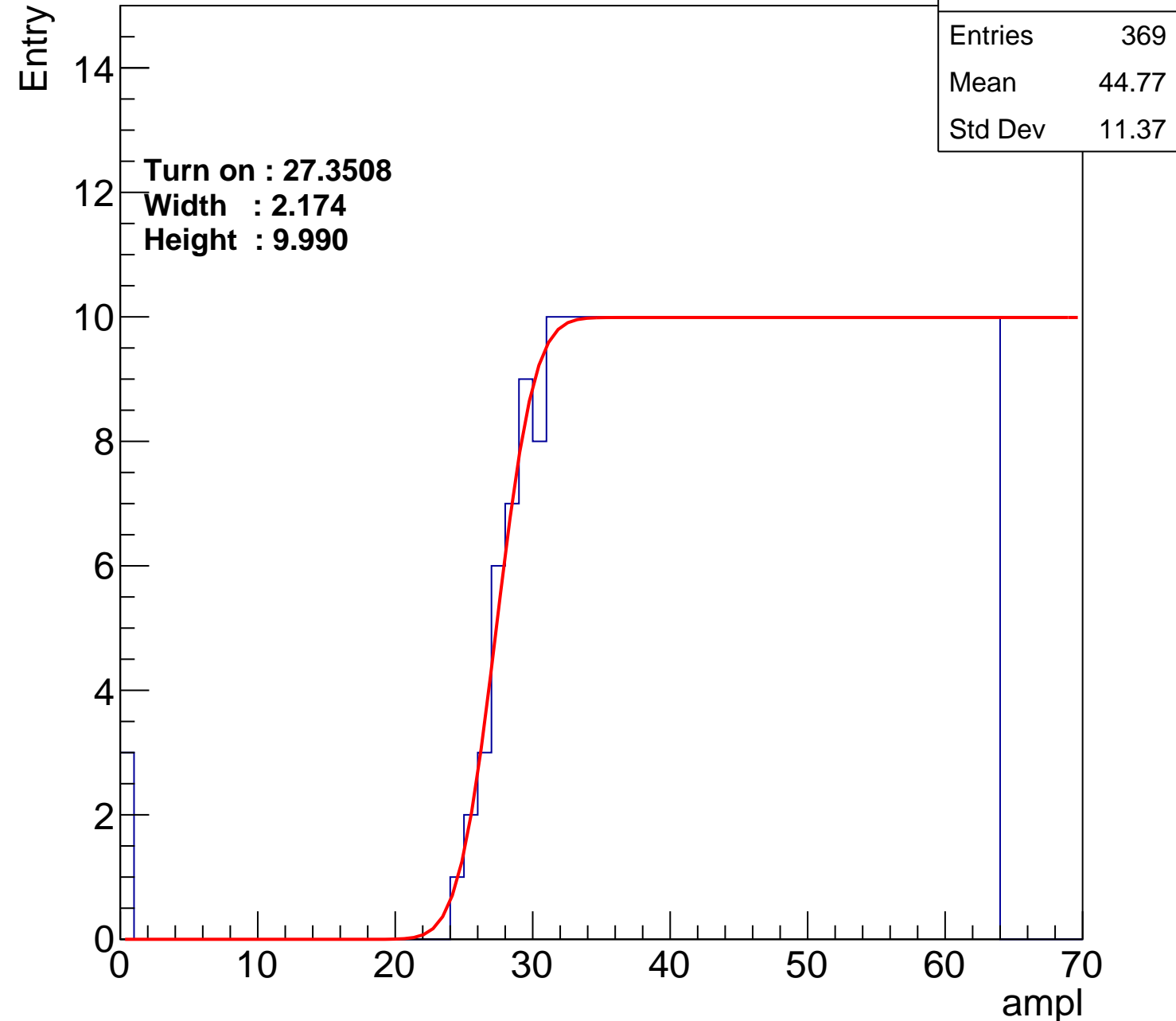
Width : 2.174

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U26-ch126

calib_packv5_042523_0143.root, FC#3, port B3

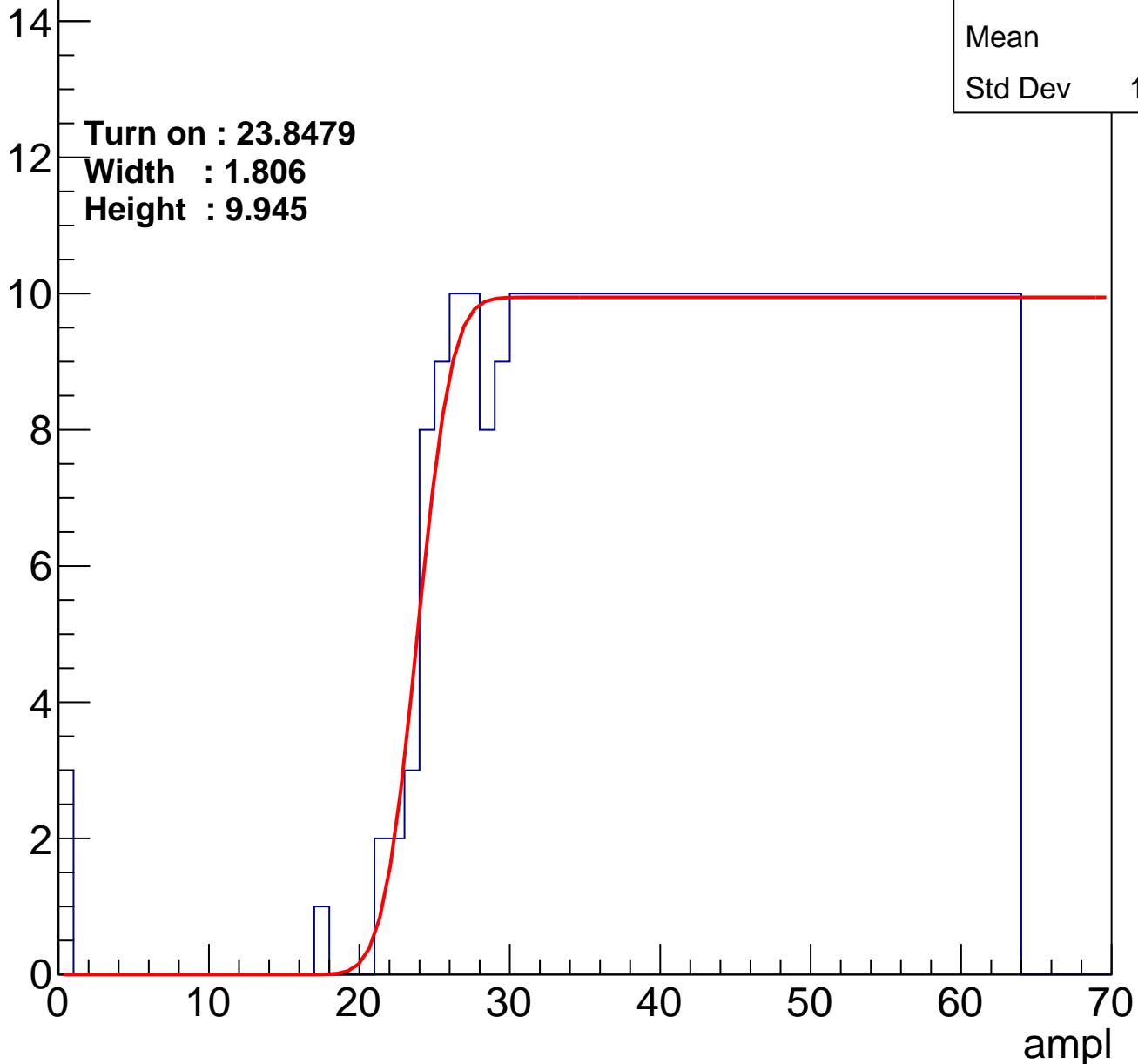
| | |
|---------|-------|
| Entries | 405 |
| Mean | 43 |
| Std Dev | 12.27 |

Turn on : 23.8479

Width : 1.806

Height : 9.945

Entry



B1L000S, U26-ch127

calib_packv5_042523_0143.root, FC#3, port B3

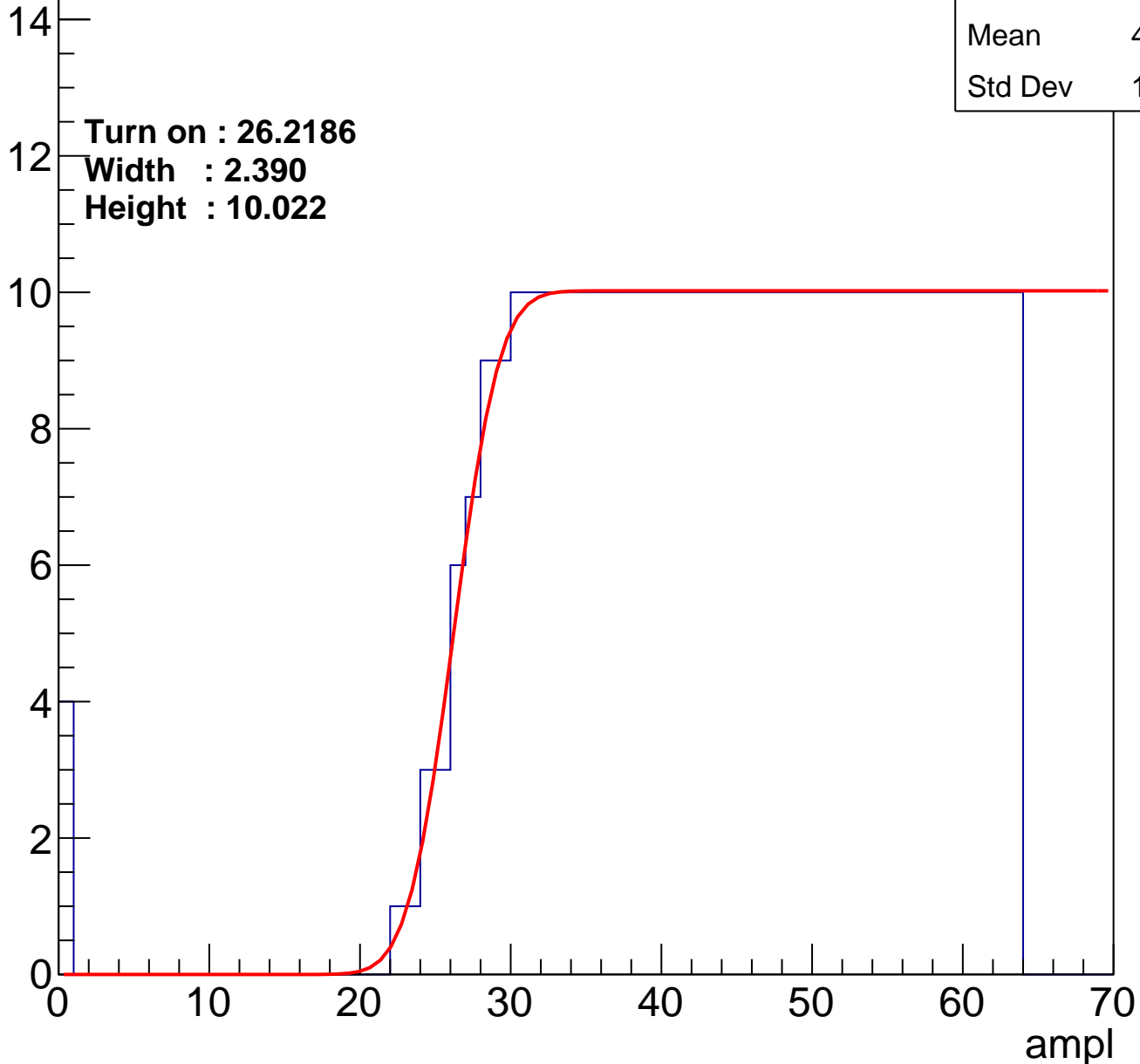
| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.02 |
| Std Dev | 11.88 |

Turn on : 26.2186

Width : 2.390

Height : 10.022

Entry



B1L000S, U26-ch127

calib_packv5_042523_0143.root, FC#3, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.02 |
| Std Dev | 11.88 |

Turn on : 26.2186

Width : 2.390

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl

