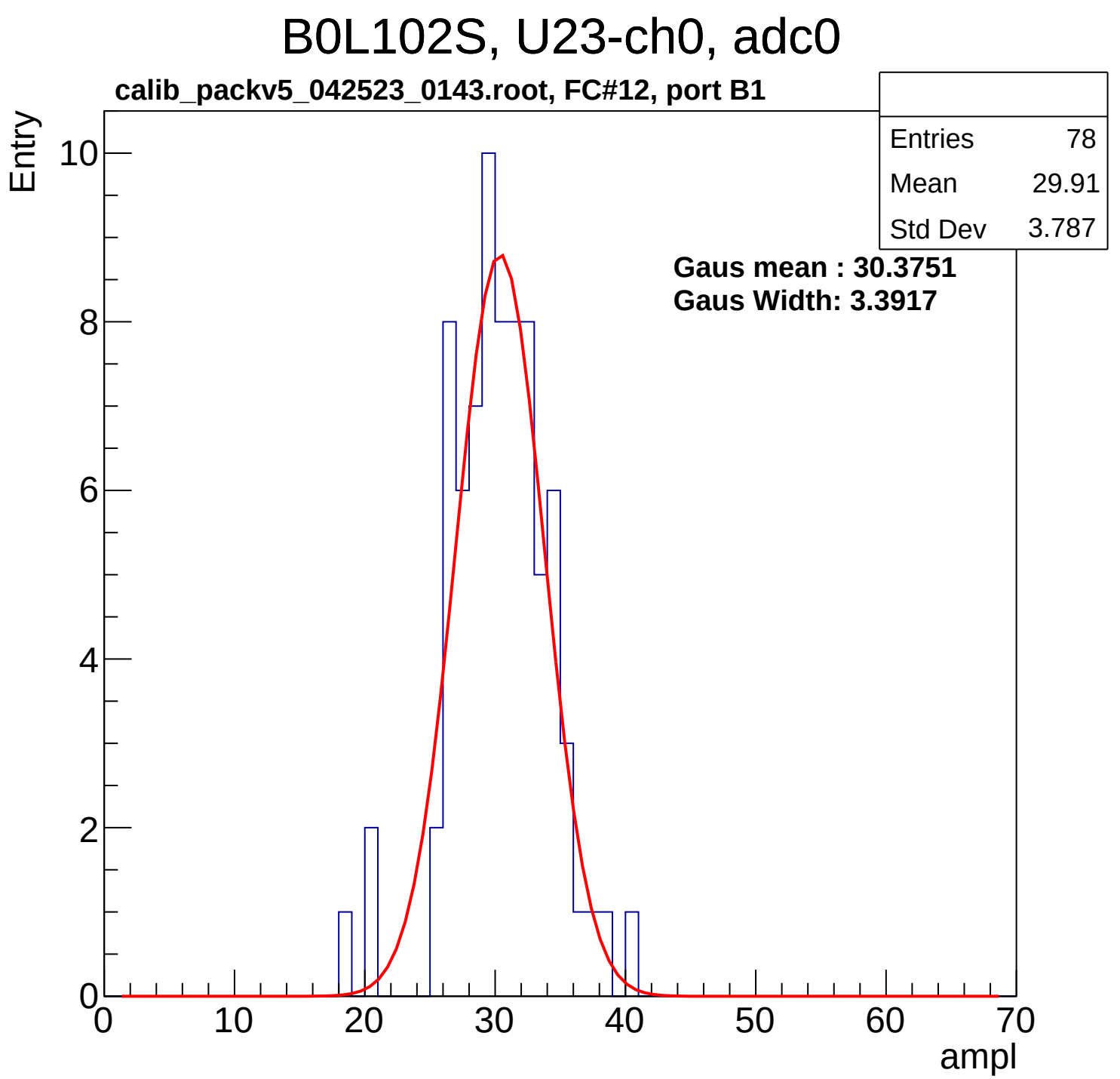
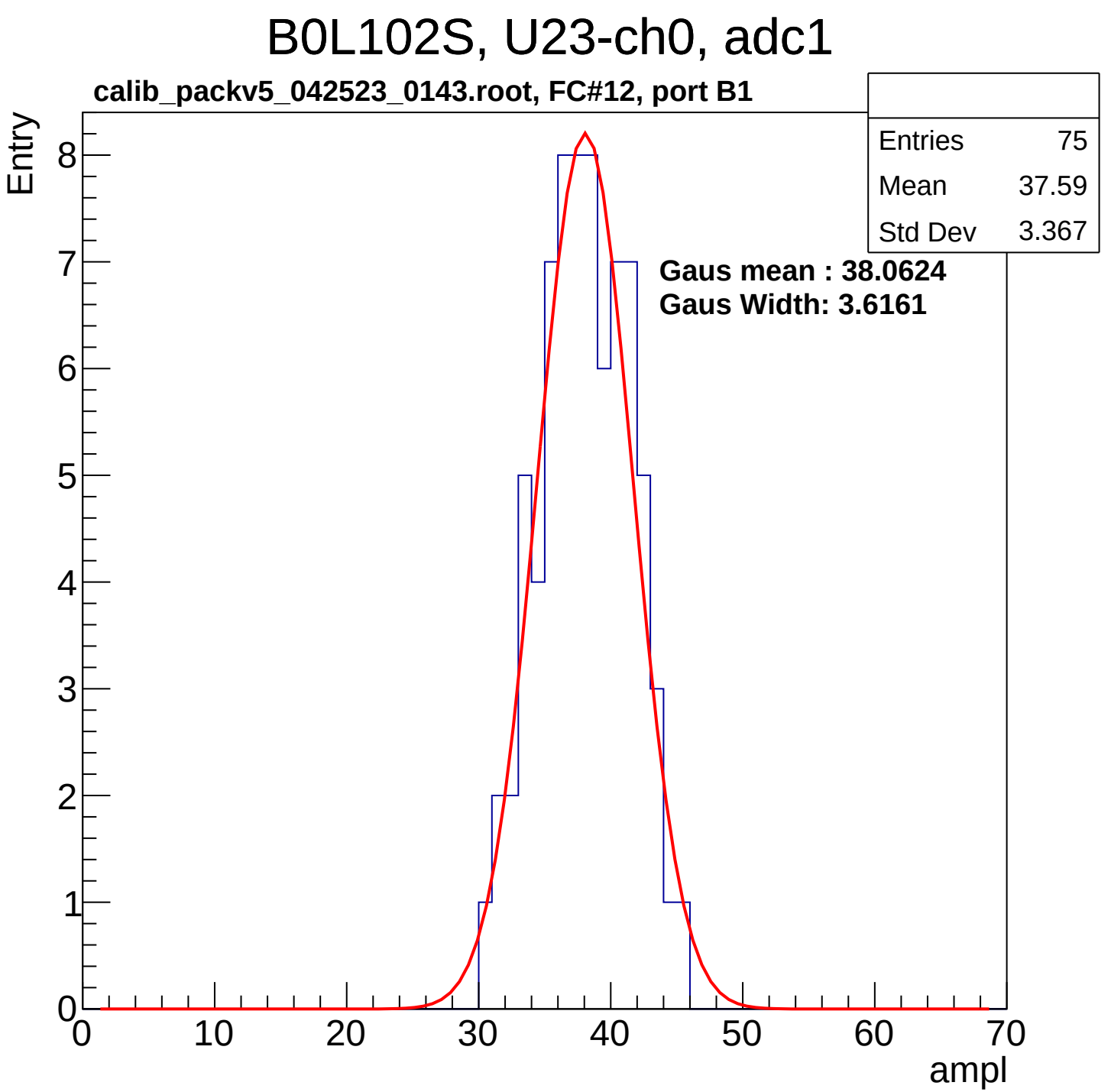

B0L102S, U23-ch0, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 29.91
Std Dev 3.787
Gaus mean : 30.3751
Gaus Width: 3.3917
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch0, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.59
Std Dev 3.367
Gaus mean : 38.0624
Gaus Width: 3.6161
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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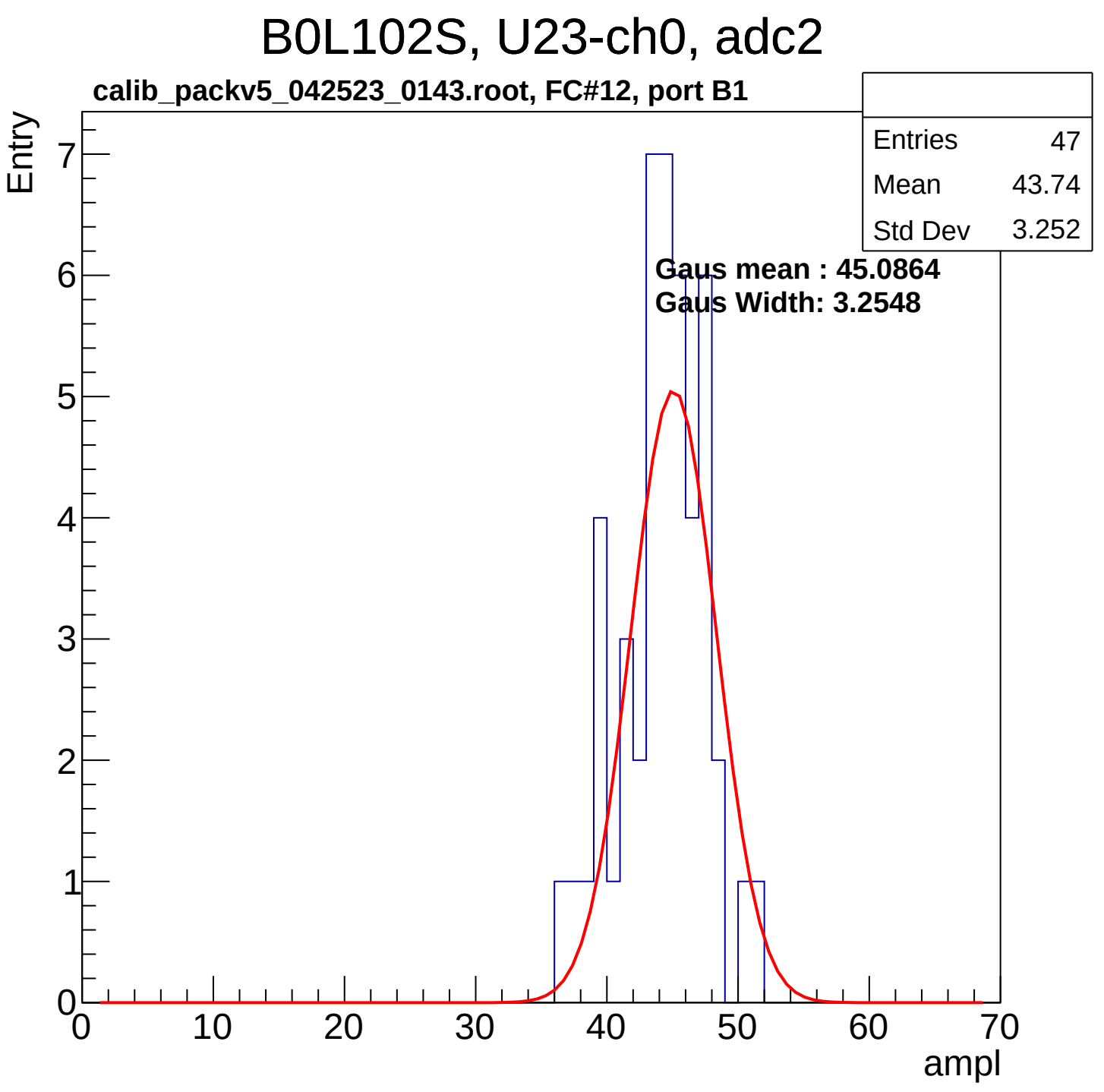
B0L102S, U23-ch0, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 43.74
Std Dev 3.252
Gaus mean : 45.0864
Gaus Width: 3.2548
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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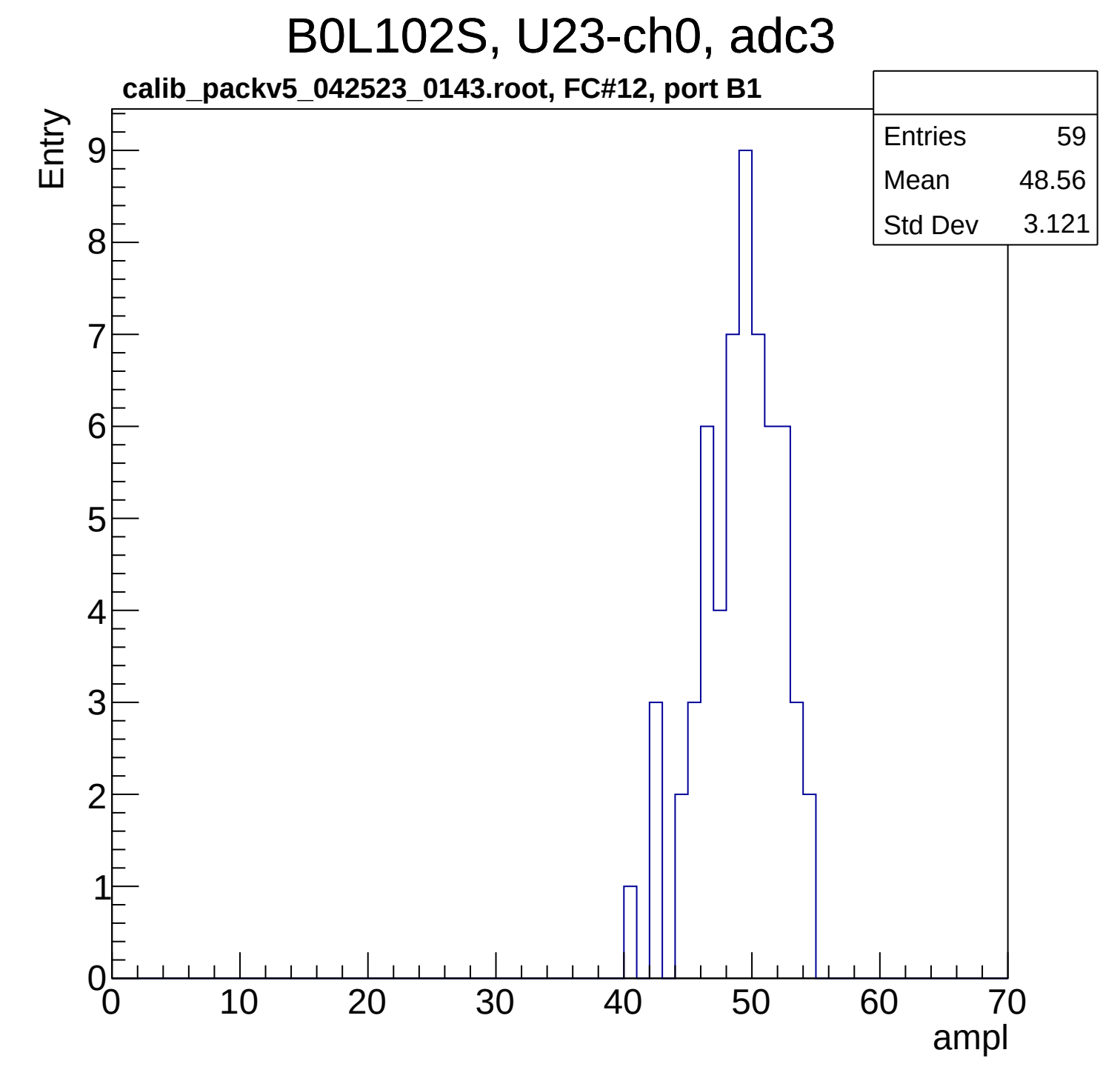
B0L102S, U23-ch0, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.56
Std Dev 3.121
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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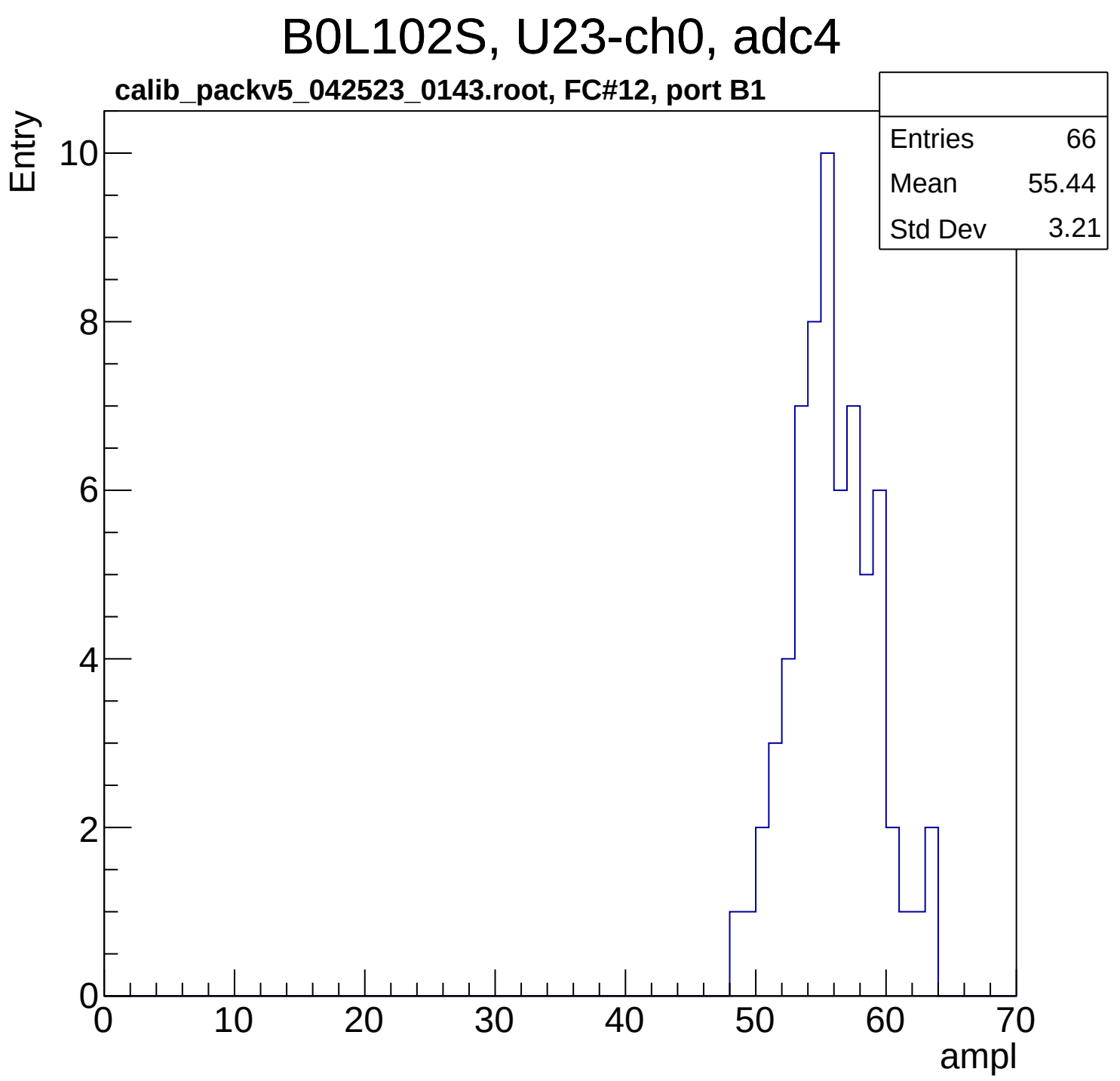
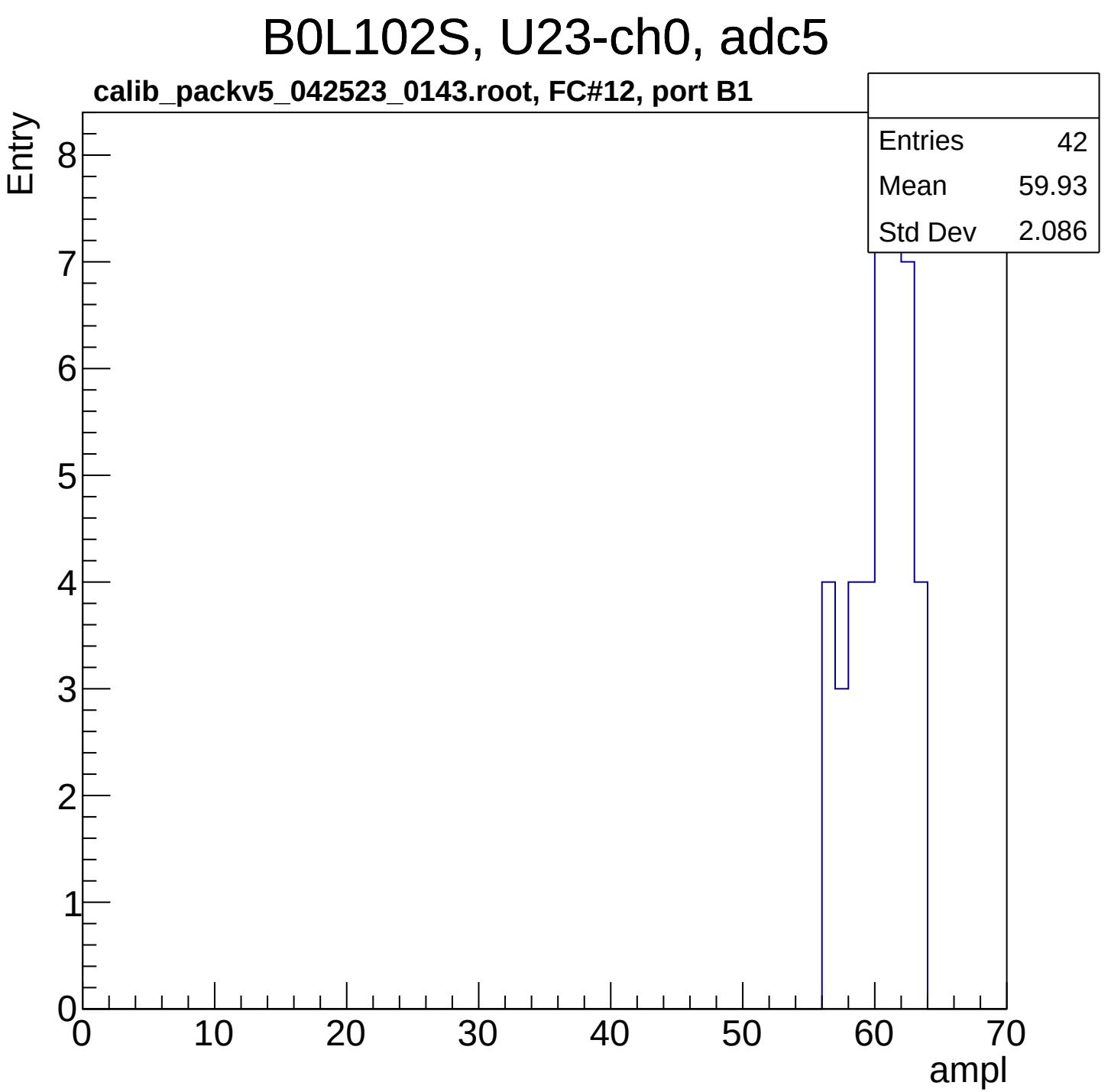

B0L102S, U23-ch0, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 55.44
Std Dev 3.21
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch0, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 59.93
Std Dev 2.086
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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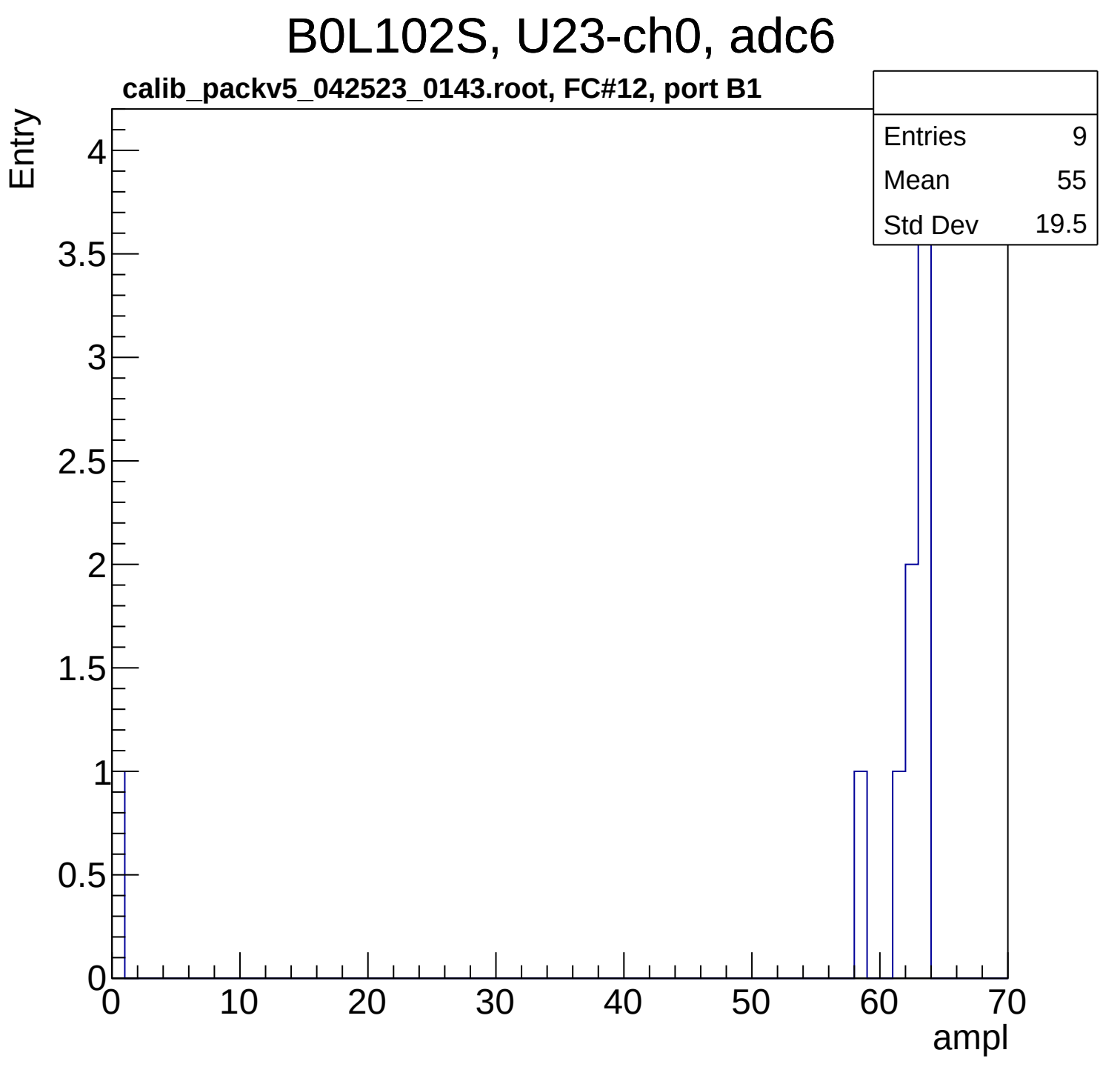
B0L102S, U23-ch0, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 55
Std Dev 19.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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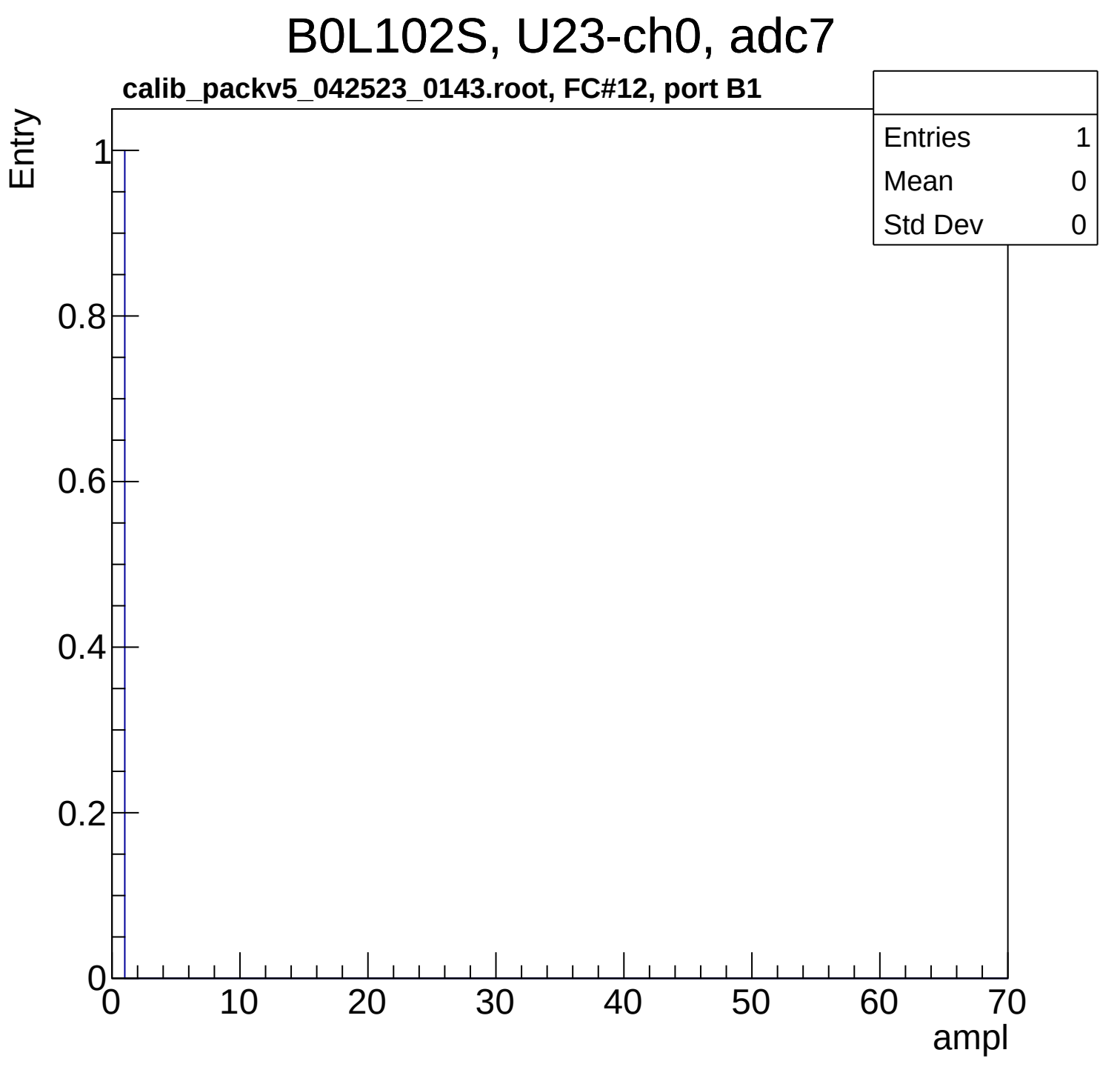
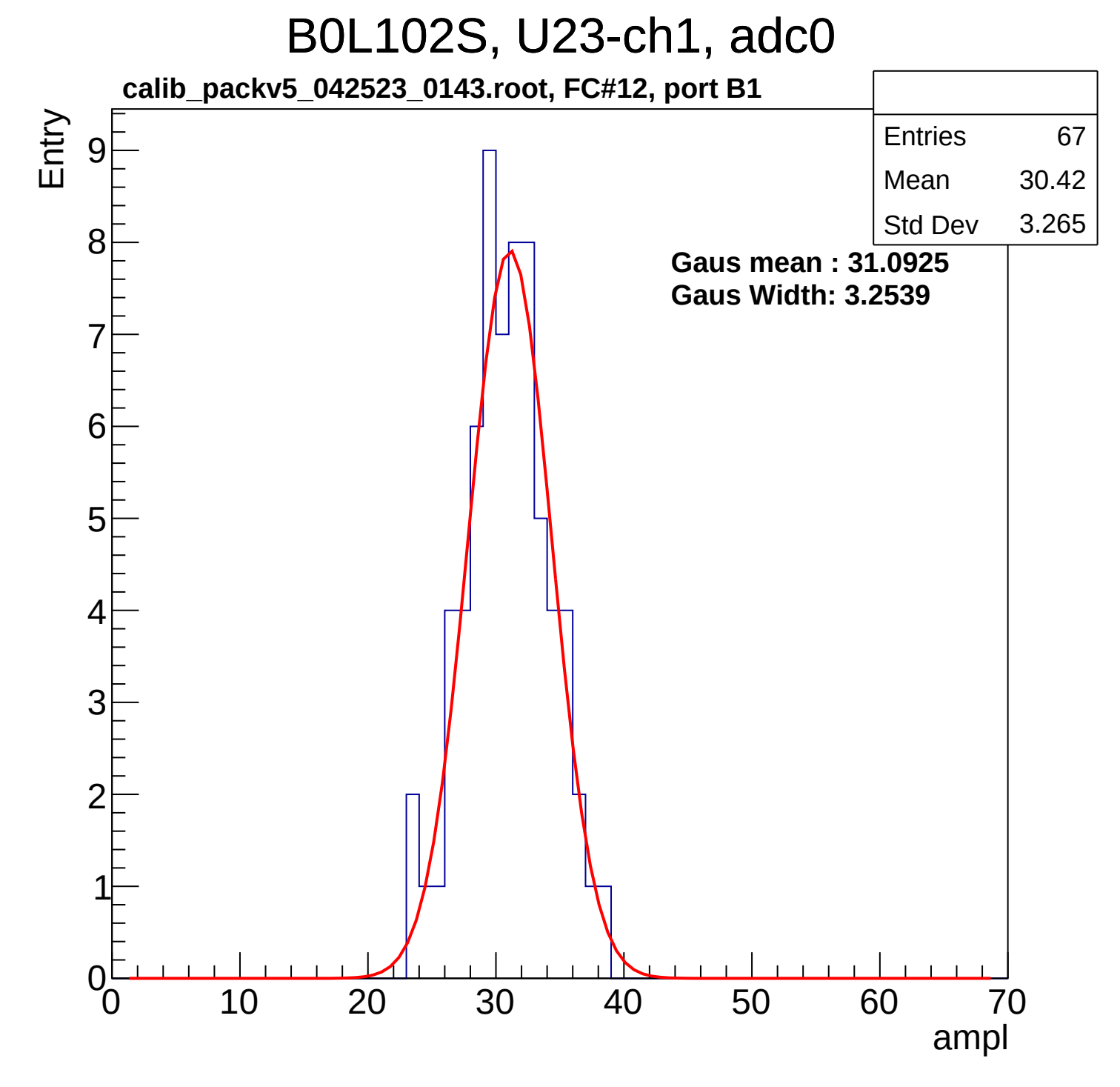
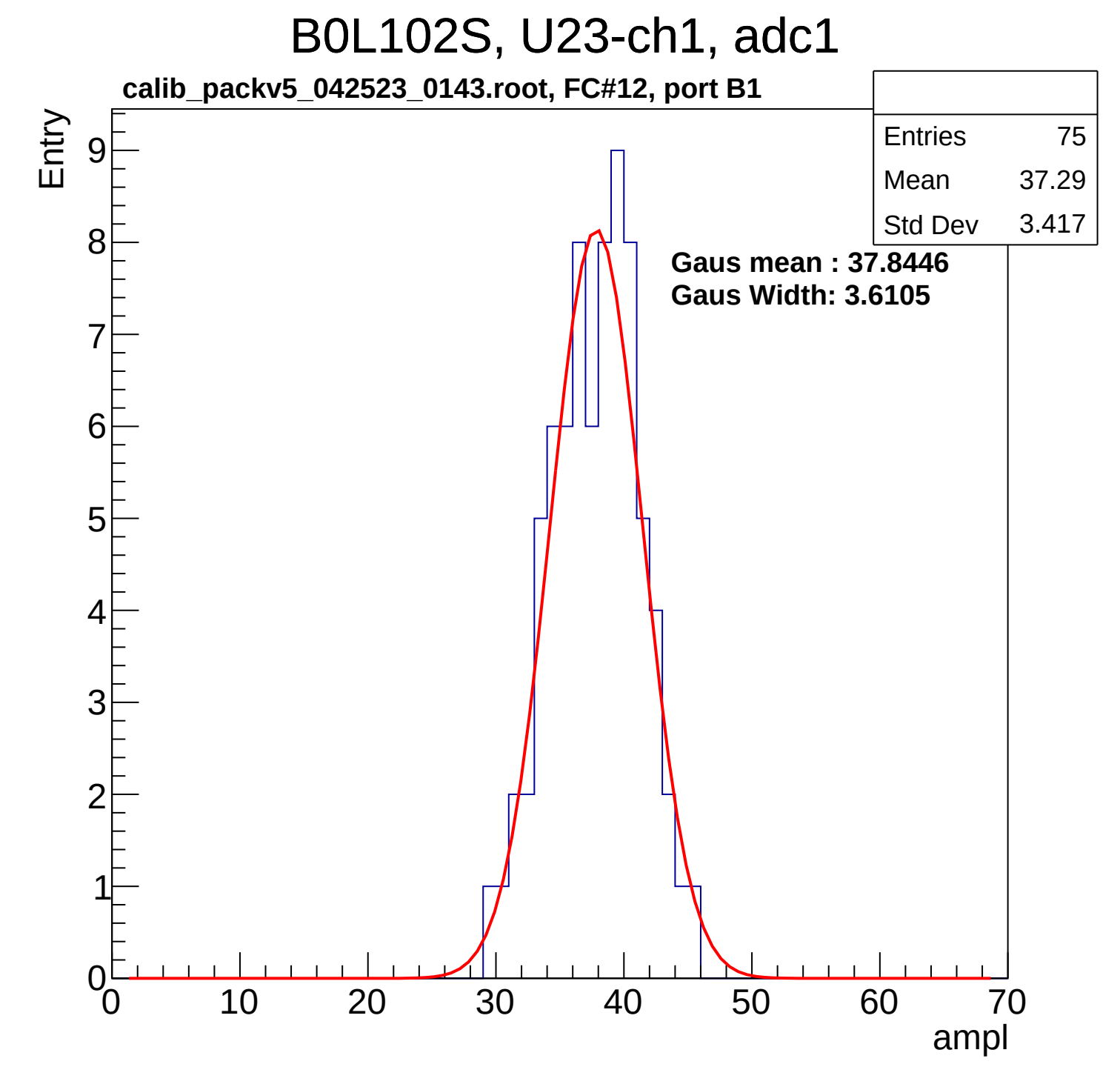
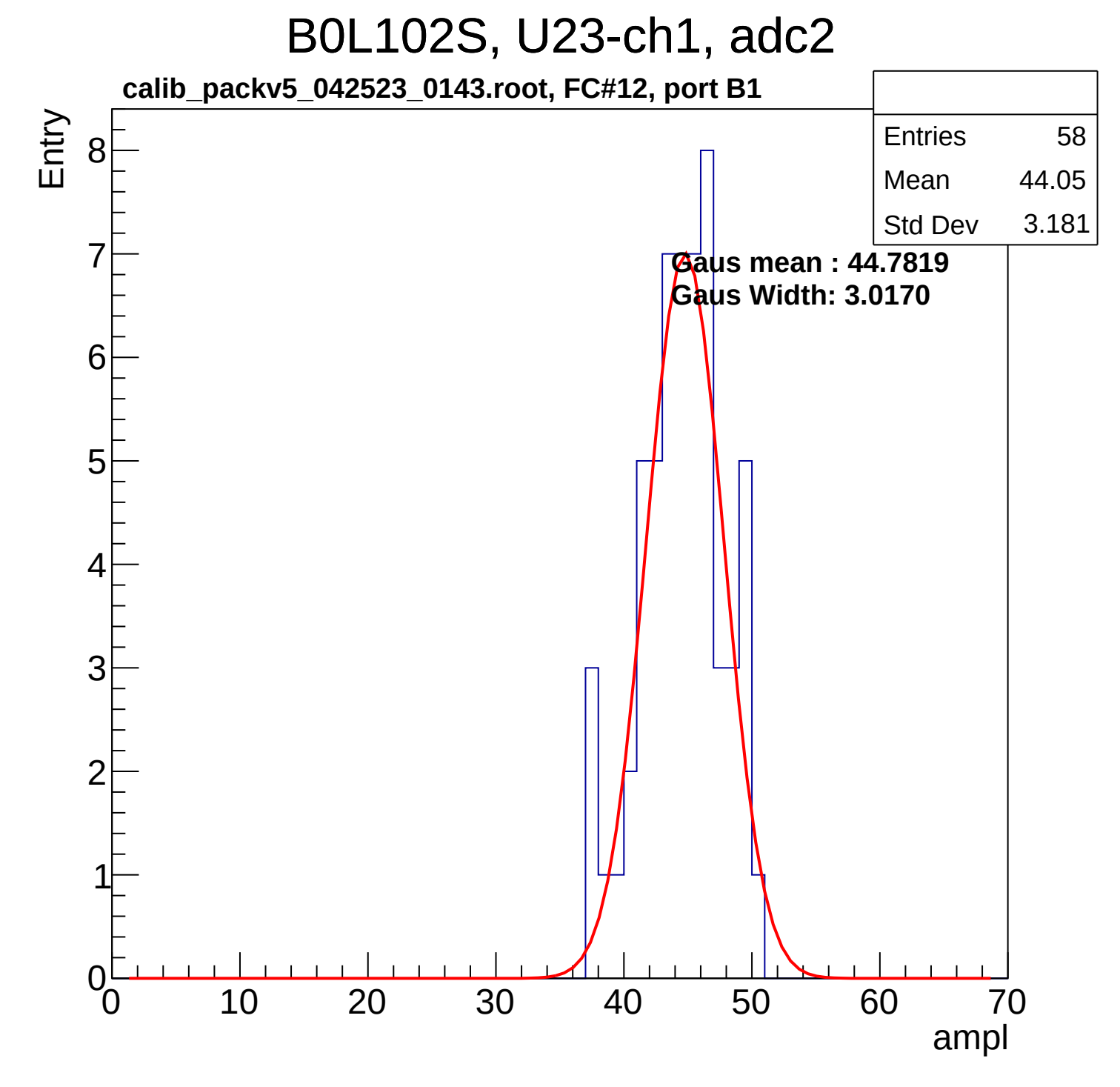

B0L102S, U23-ch0, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch1, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 30.42
Std Dev 3.265
Gaus mean : 31.0925
Gaus Width: 3.2539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch1, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.29
Std Dev 3.417
Gaus mean : 37.8446
Gaus Width: 3.6105
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch1, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 44.05
Std Dev 3.181
Gaus mean : 44.7819
Gaus Width: 3.0170
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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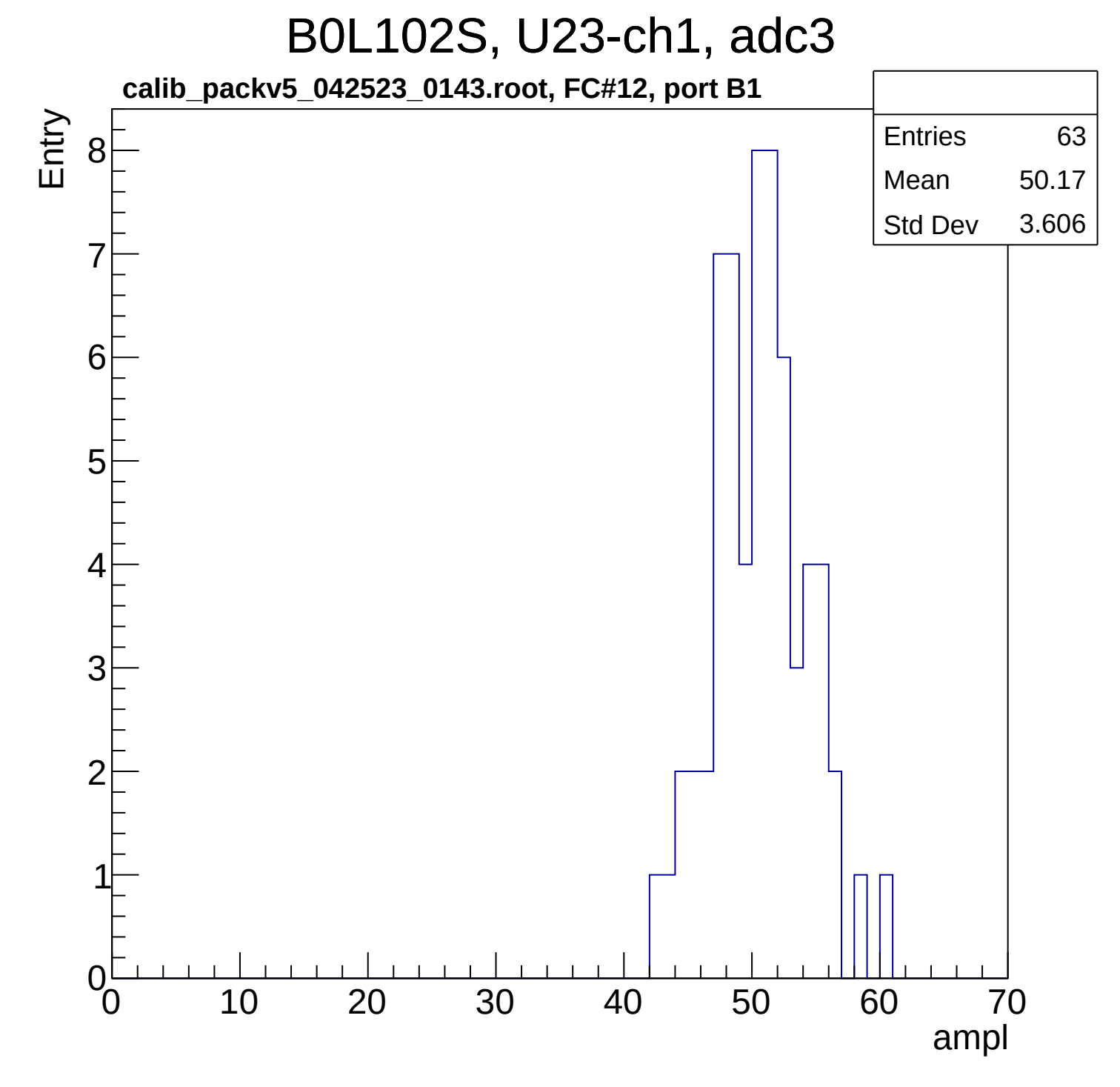
B0L102S, U23-ch1, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 50.17
Std Dev 3.606
Entry
ampl
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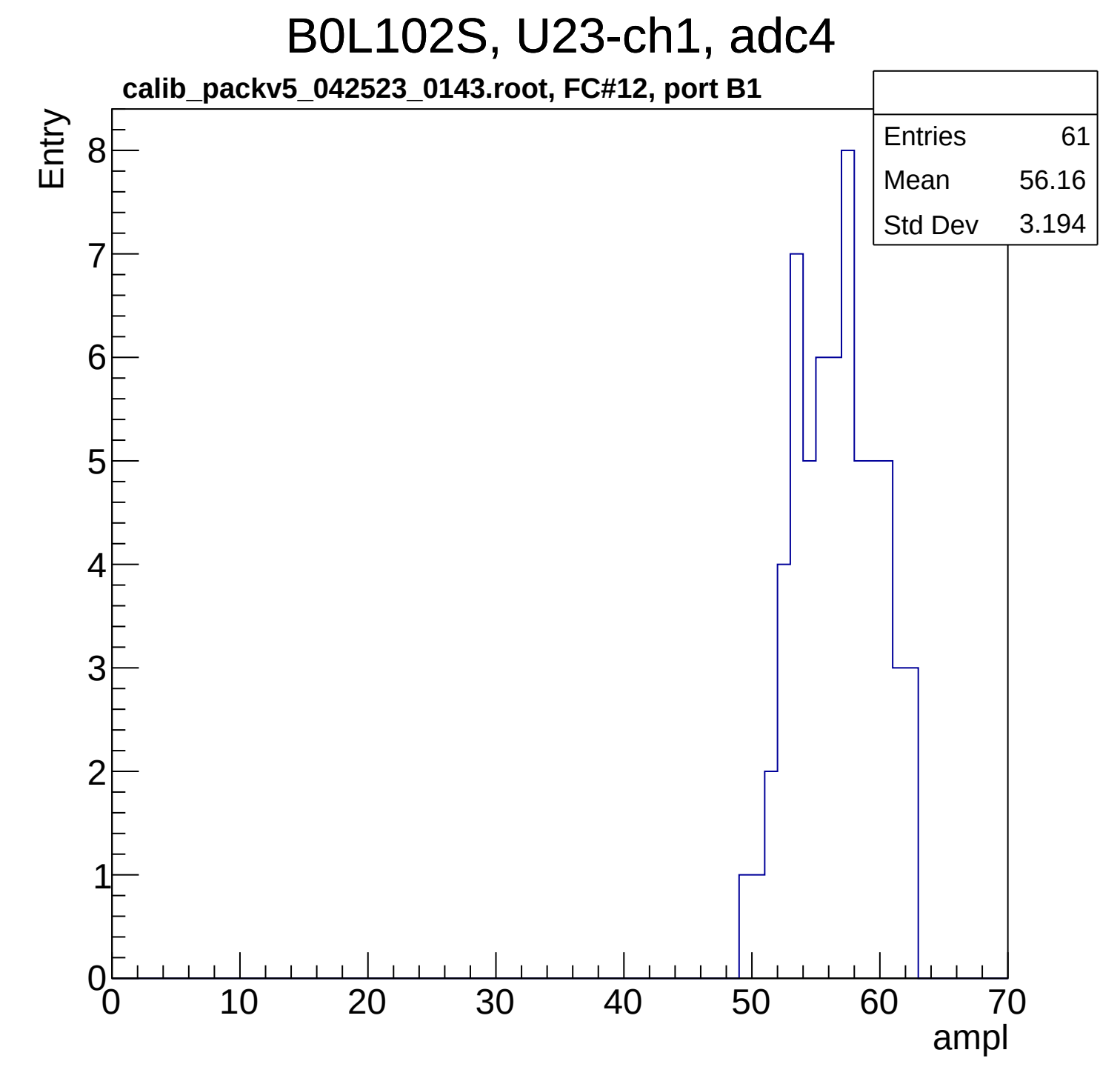
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch1, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 56.16
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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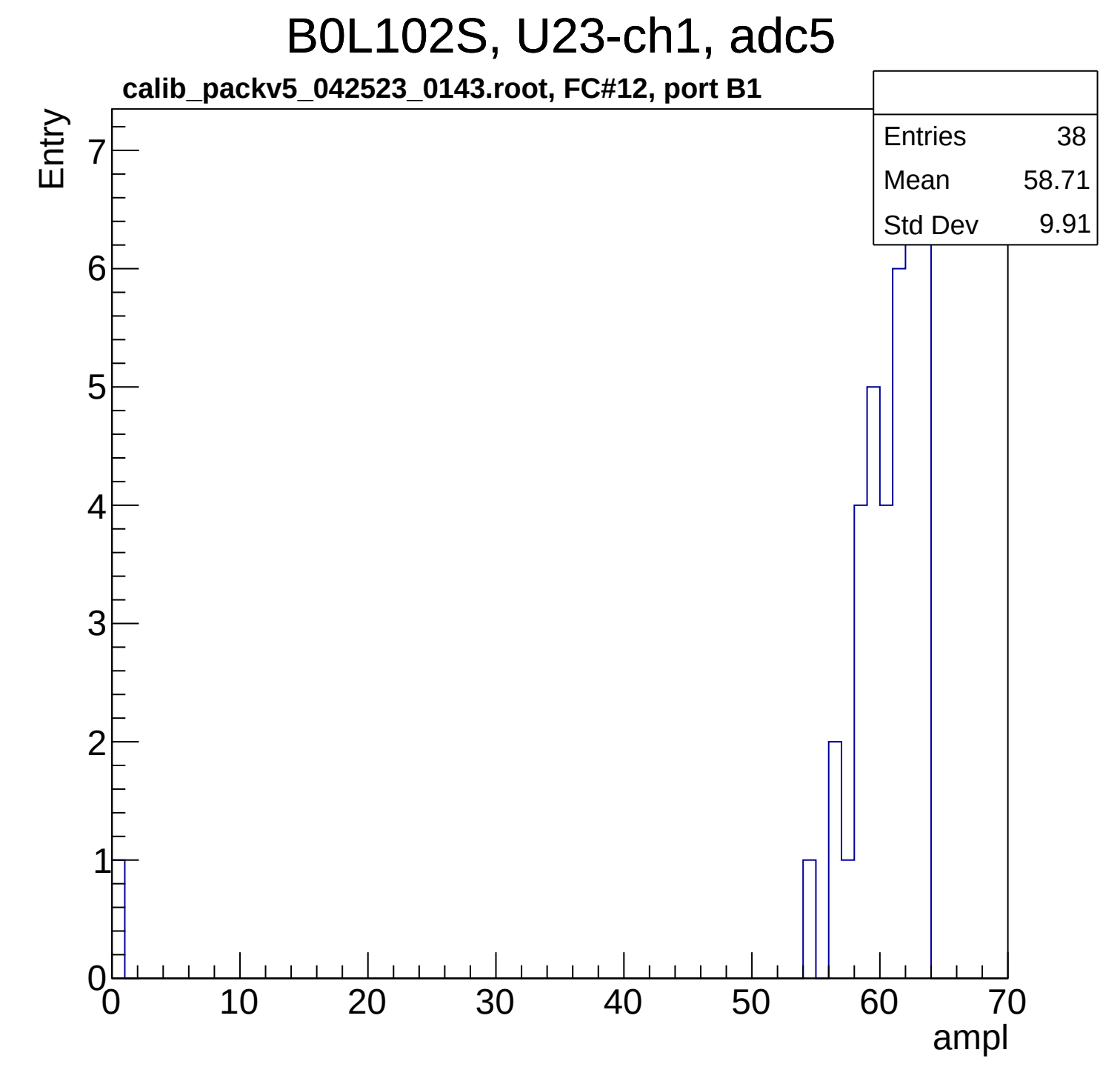
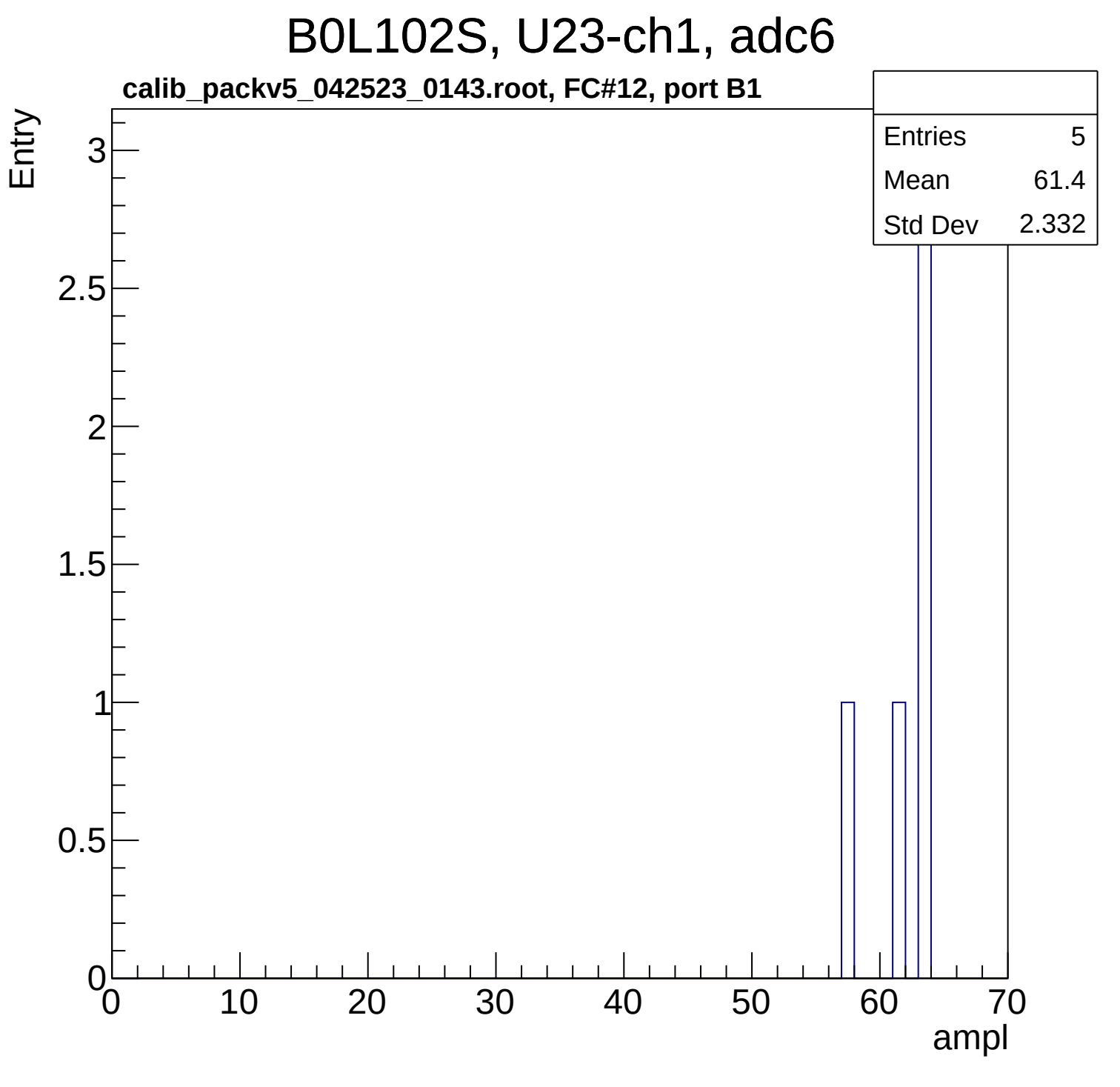

B0L102S, U23-ch1, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.71
Std Dev 9.91
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch1, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.4
Std Dev 2.332
Entry
ampl
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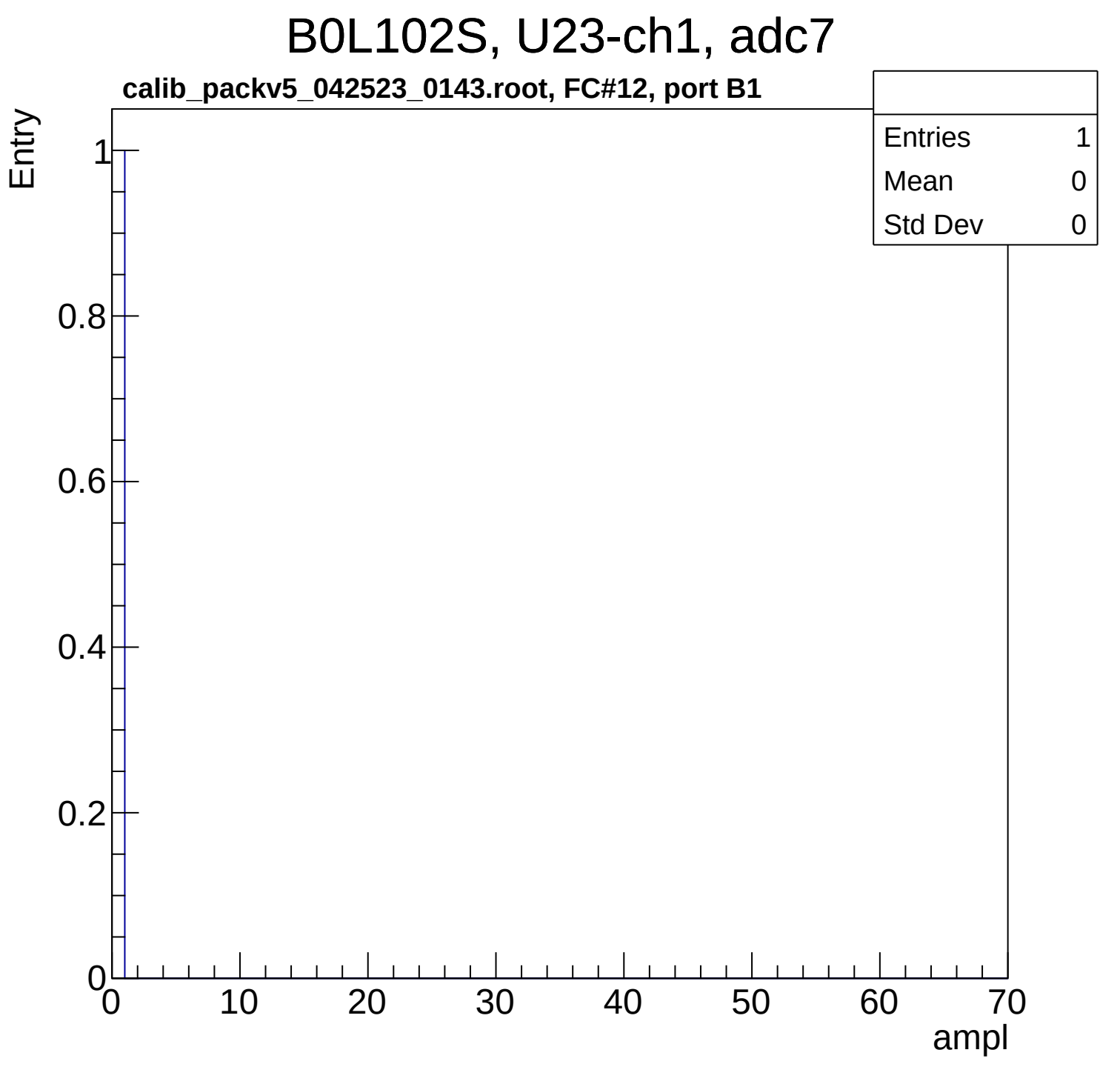
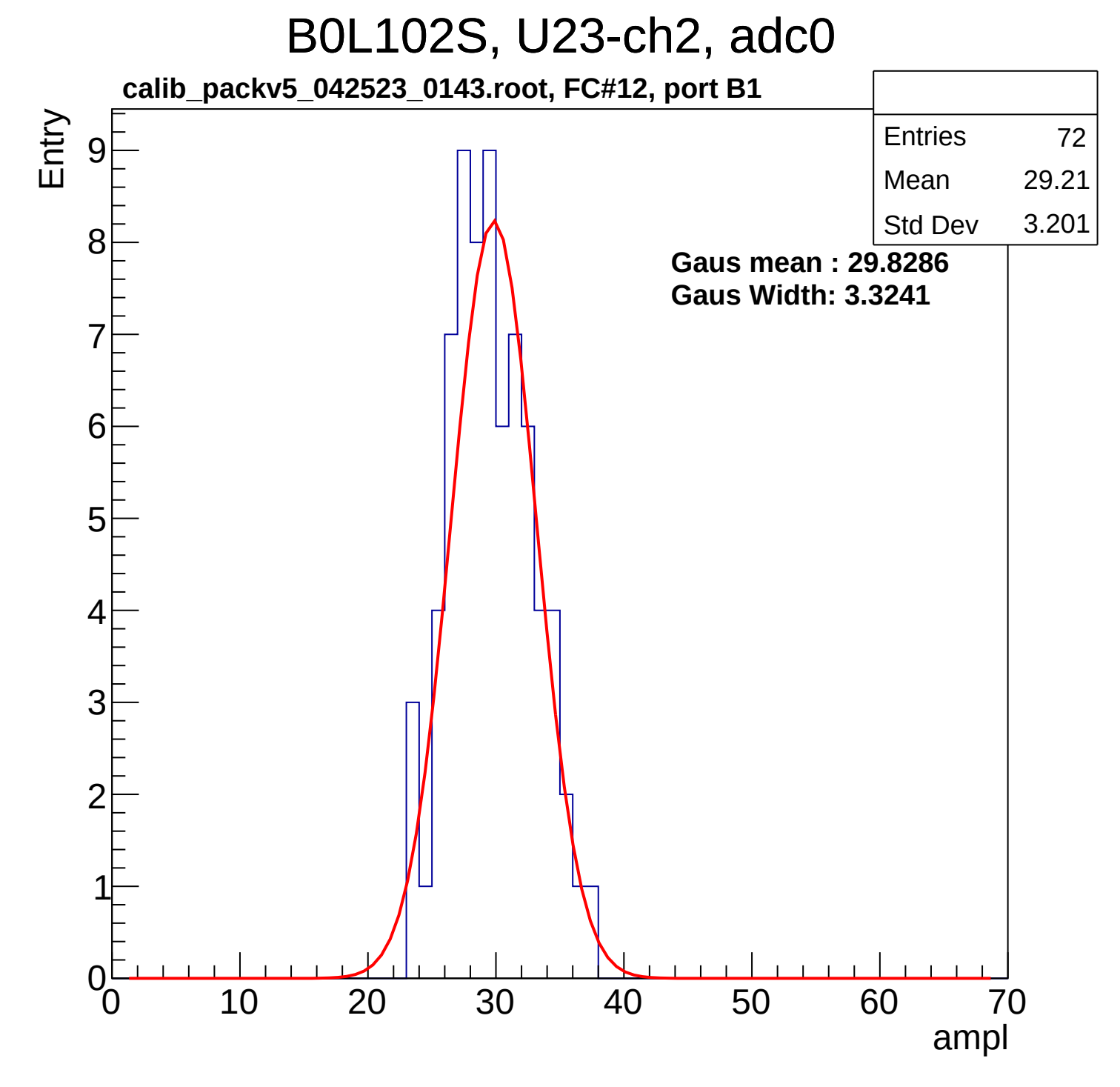
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch1, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch2, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 29.21
Std Dev 3.201
Gaus mean : 29.8286
Gaus Width: 3.3241
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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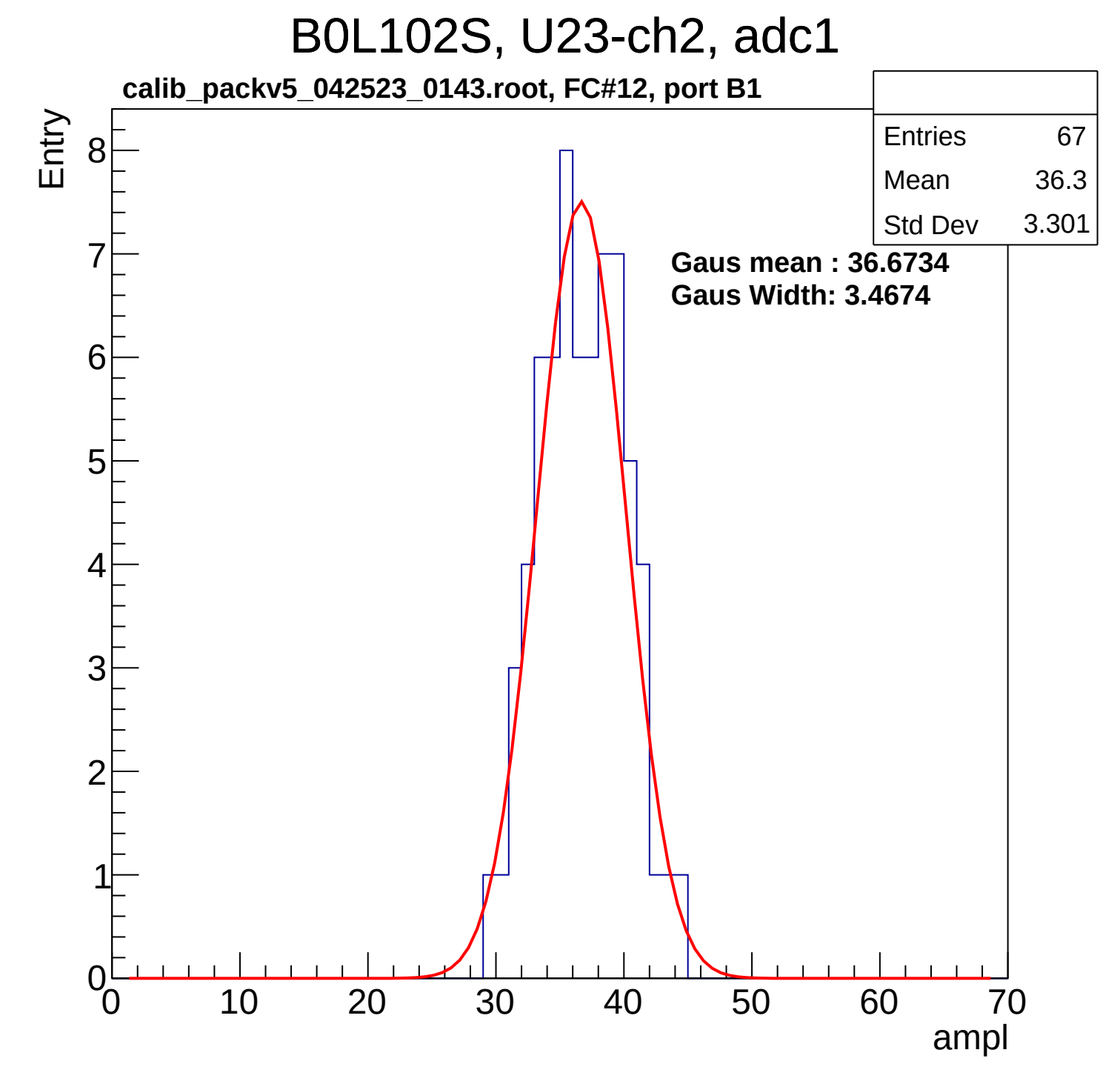
60
70


B0L102S, U23-ch2, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 36.3
Std Dev 3.301
Gaus mean : 36.6734
Gaus Width: 3.4674
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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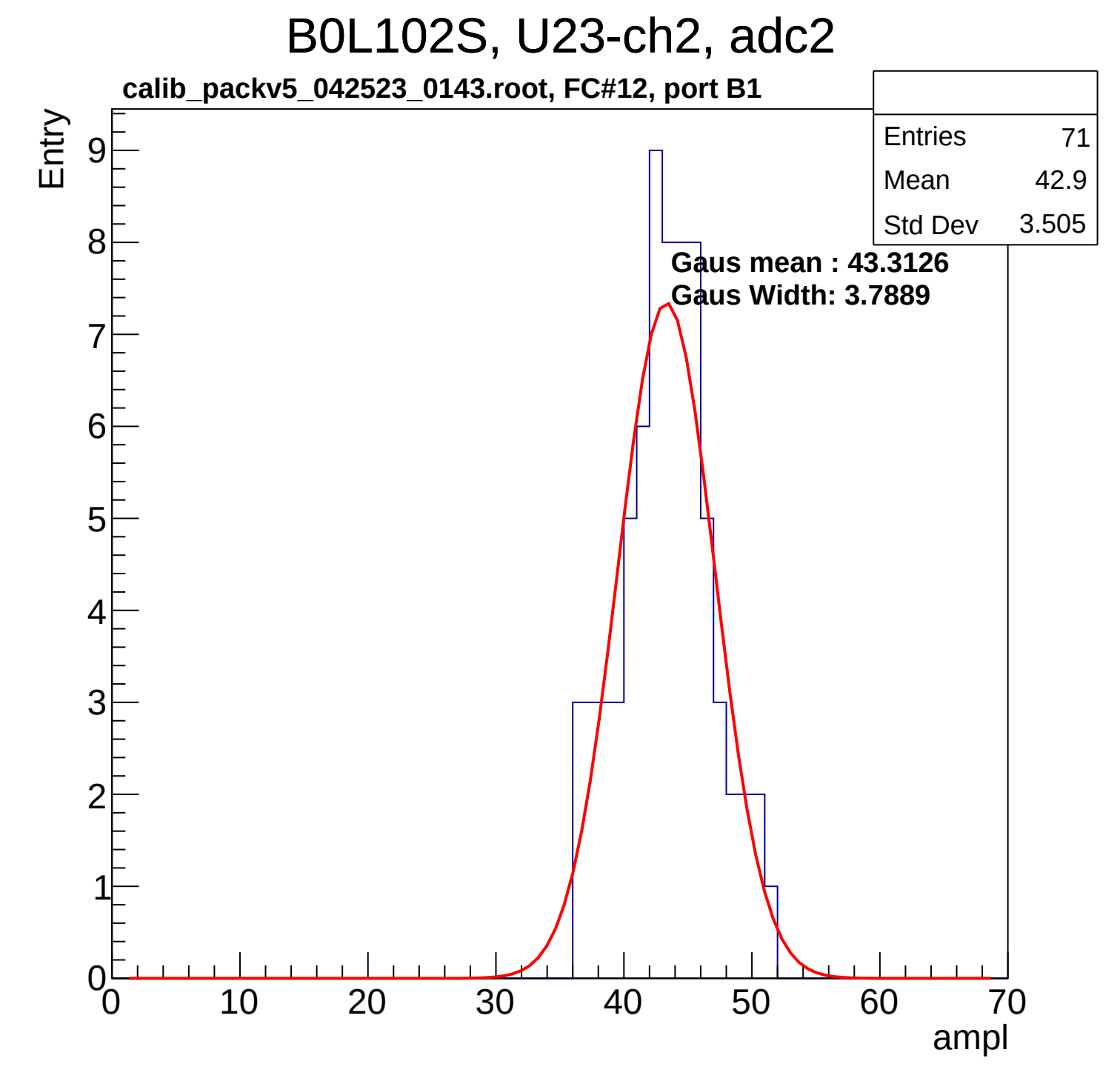
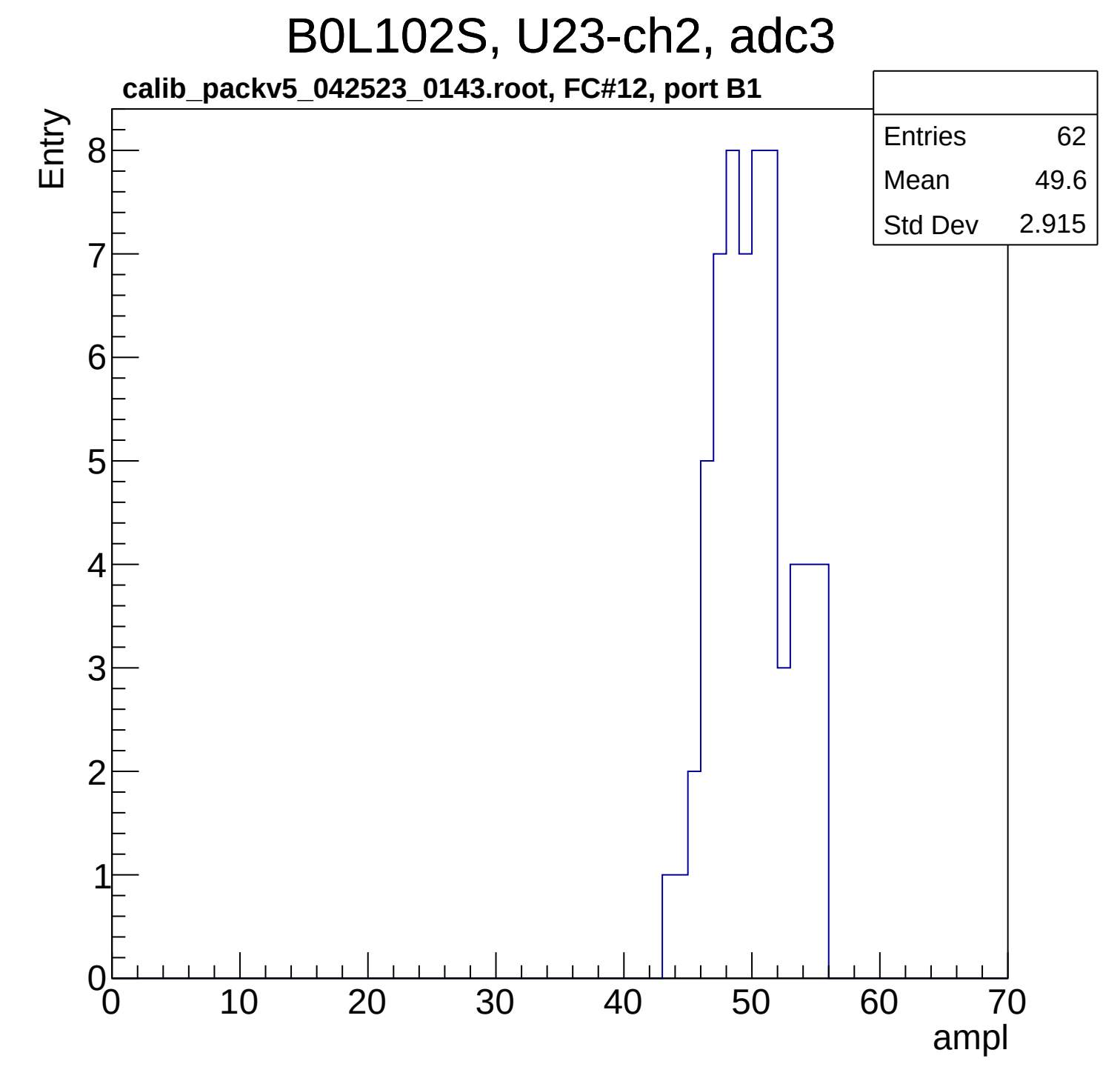
40
50
60
70


B0L102S, U23-ch2, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 42.9
Std Dev 3.505
Gaus mean : 43.3126
Gaus Width: 3.7889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch2, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 49.6
Std Dev 2.915
Entry
ampl
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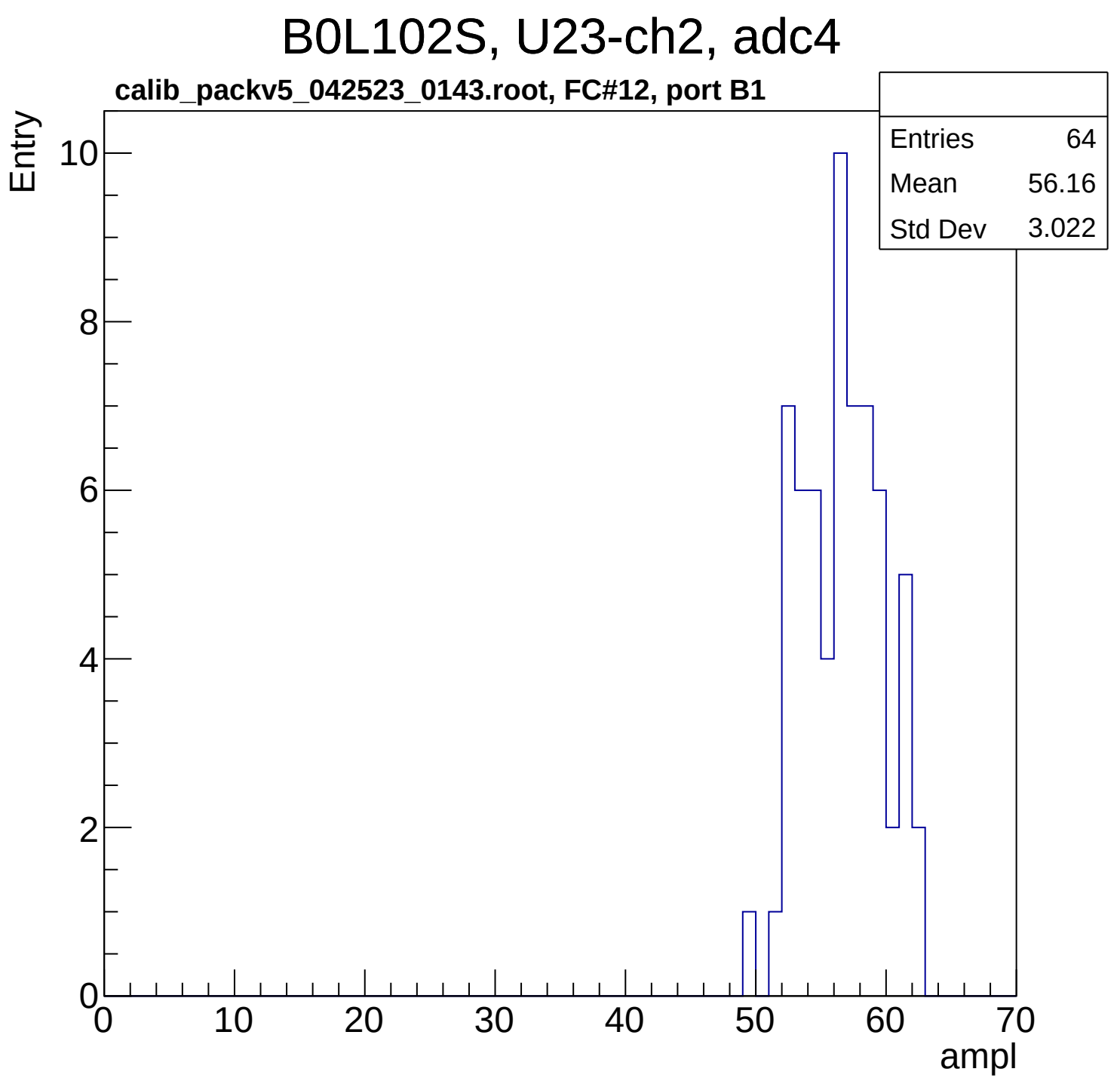
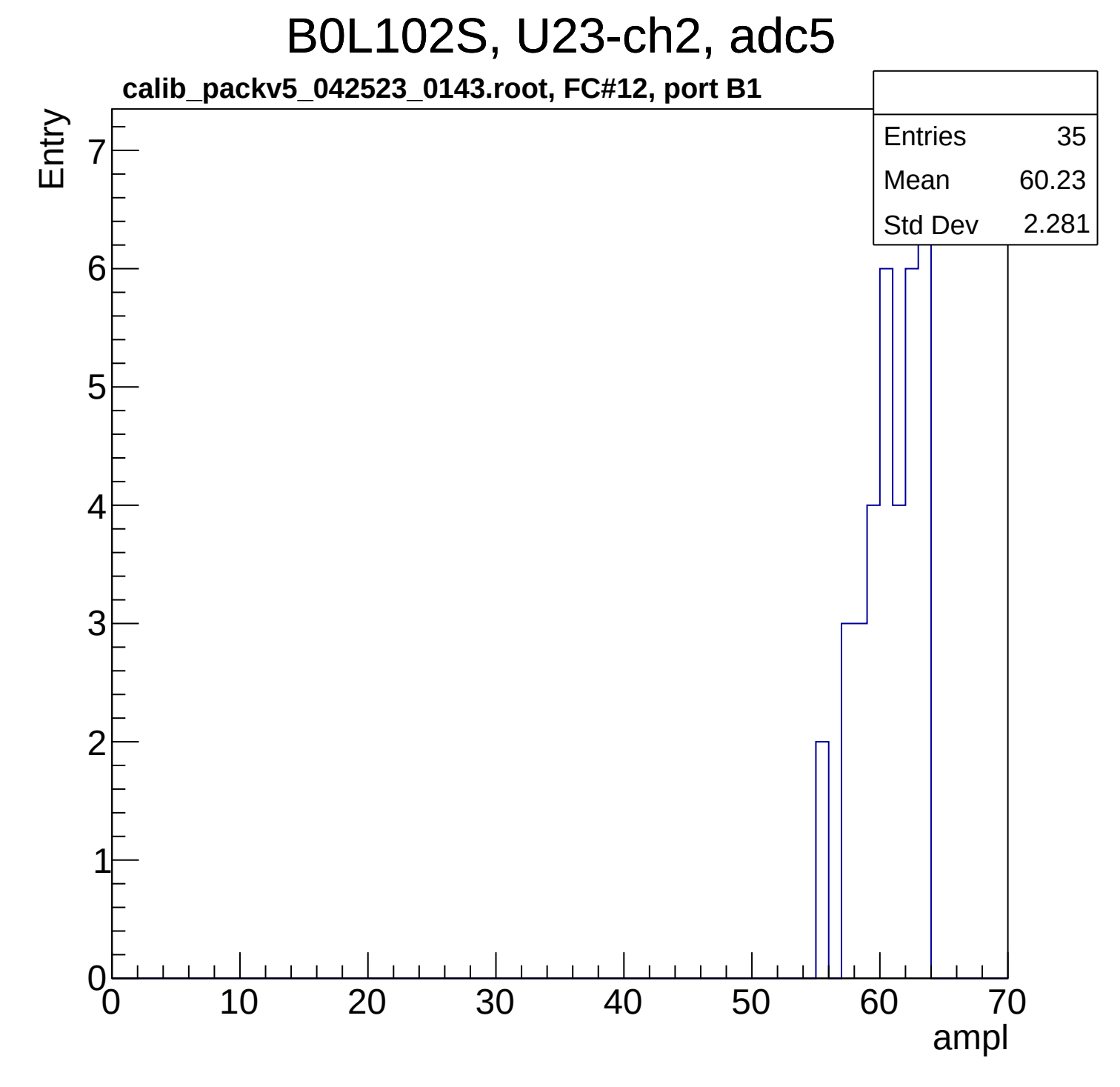
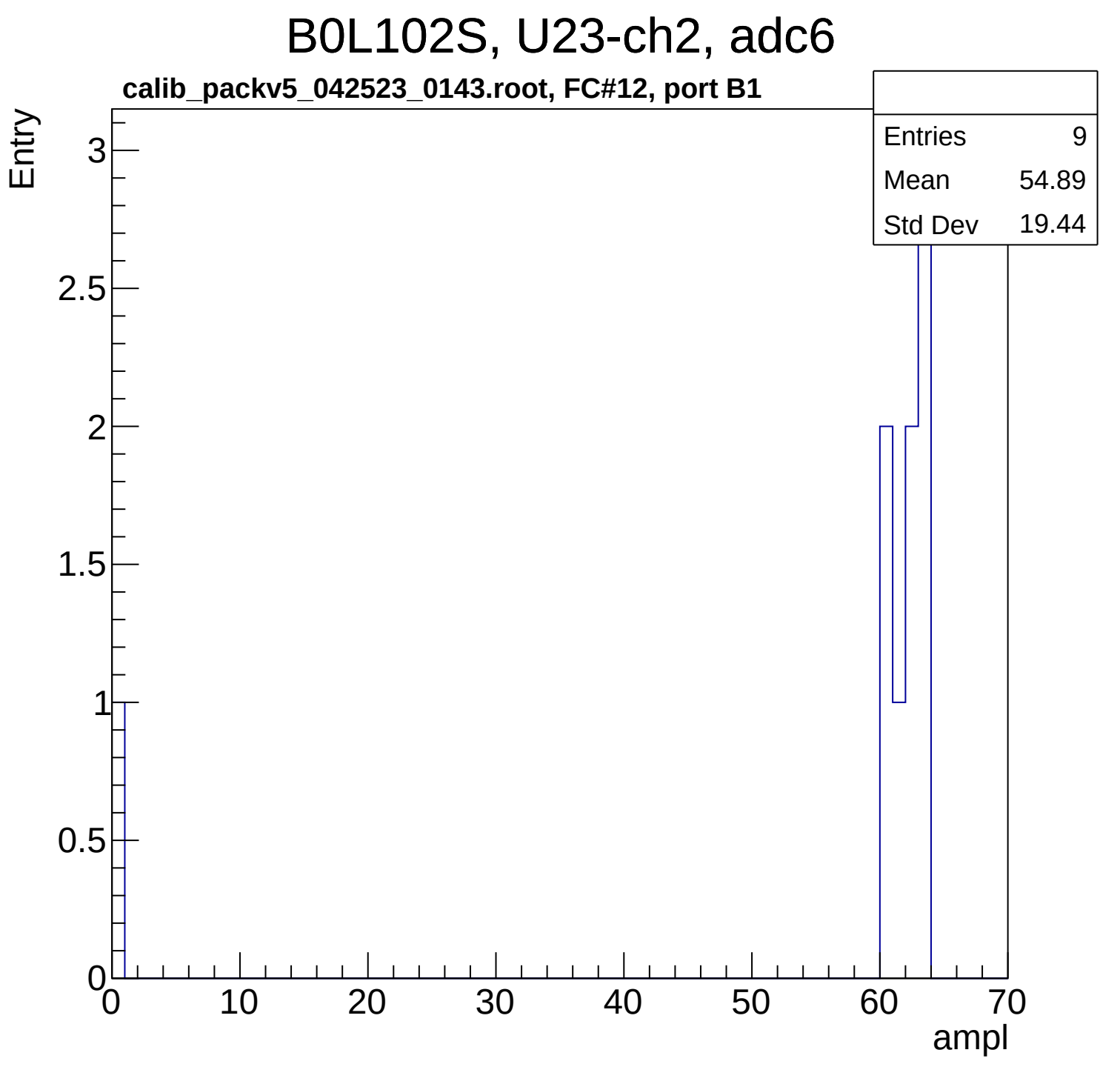
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch2, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.16
Std Dev 3.022
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch2, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 60.23
Std Dev 2.281
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch2, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
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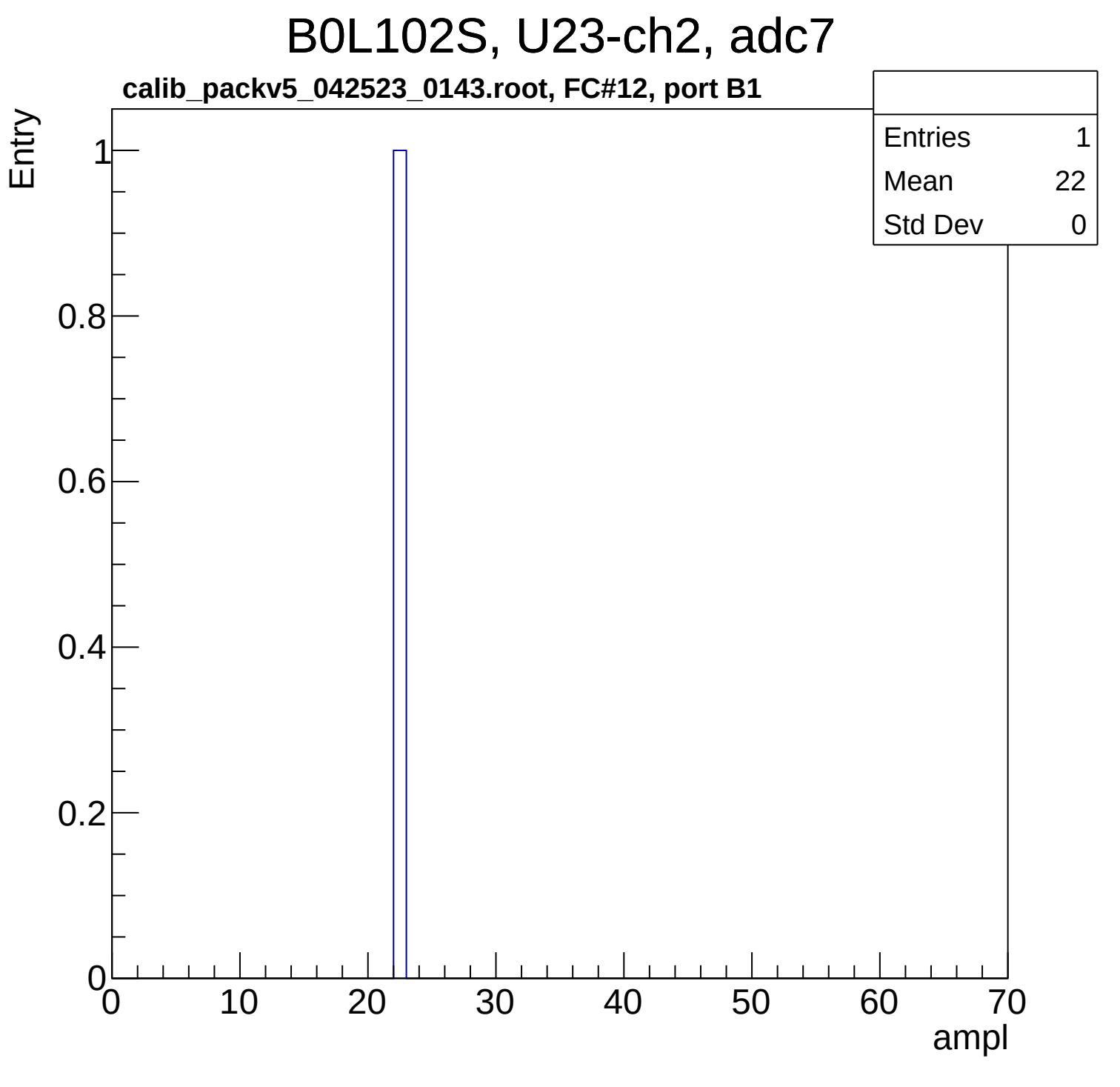
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch2, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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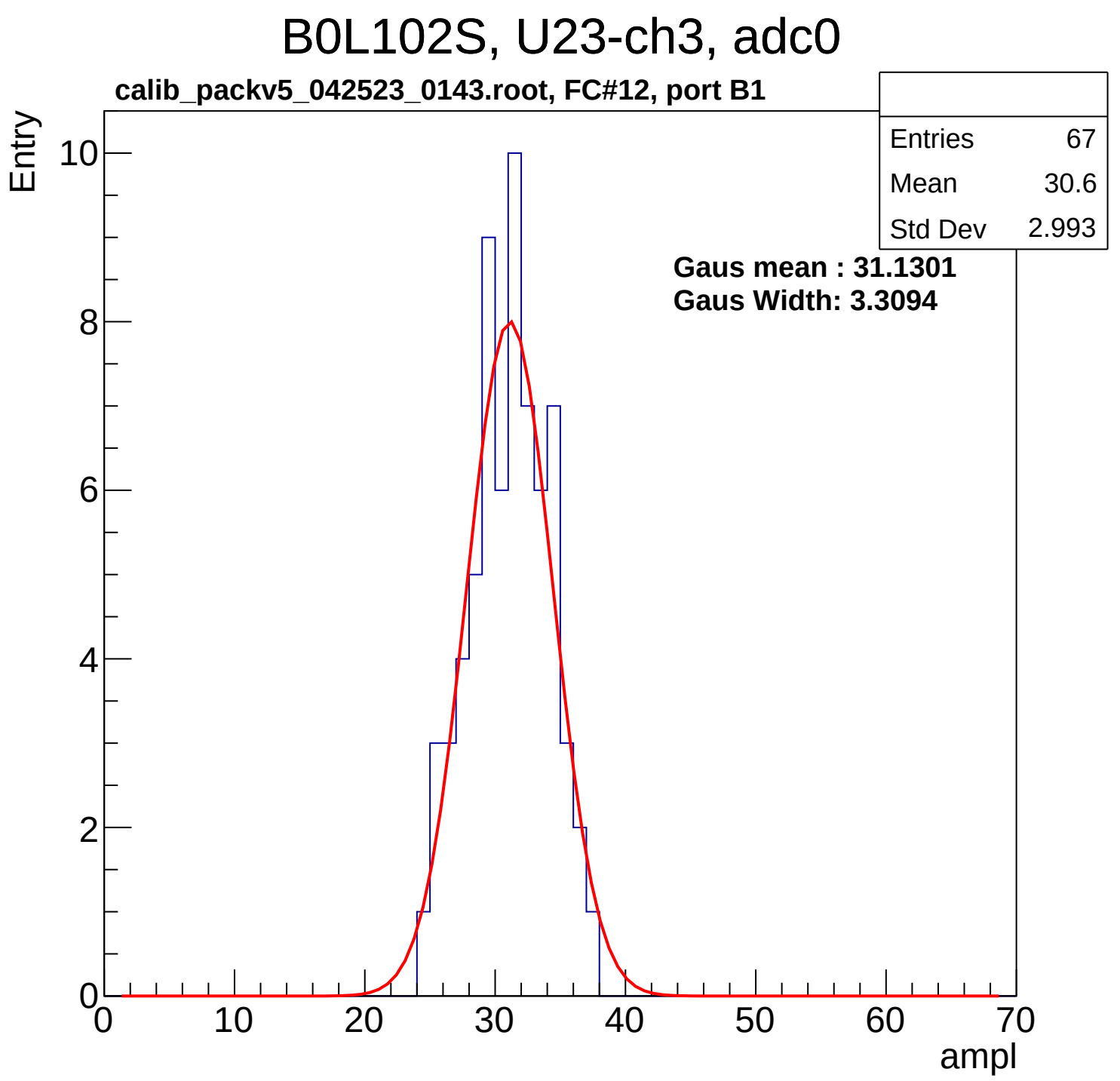
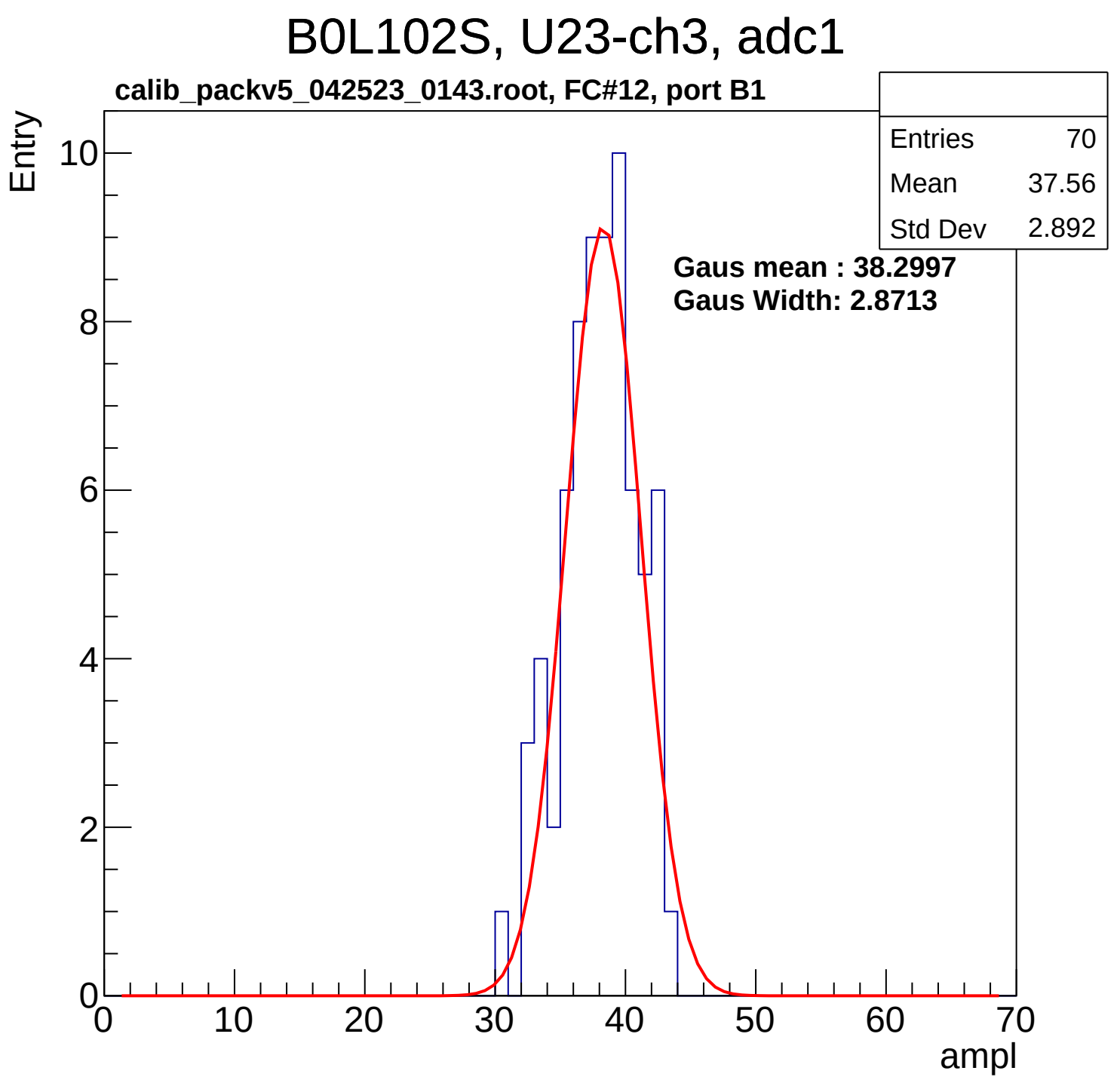
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch3, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 30.6
Std Dev 2.993
Gaus mean : 31.1301
Gaus Width: 3.3094
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch3, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 37.56
Std Dev 2.892
Gaus mean : 38.2997
Gaus Width: 2.8713
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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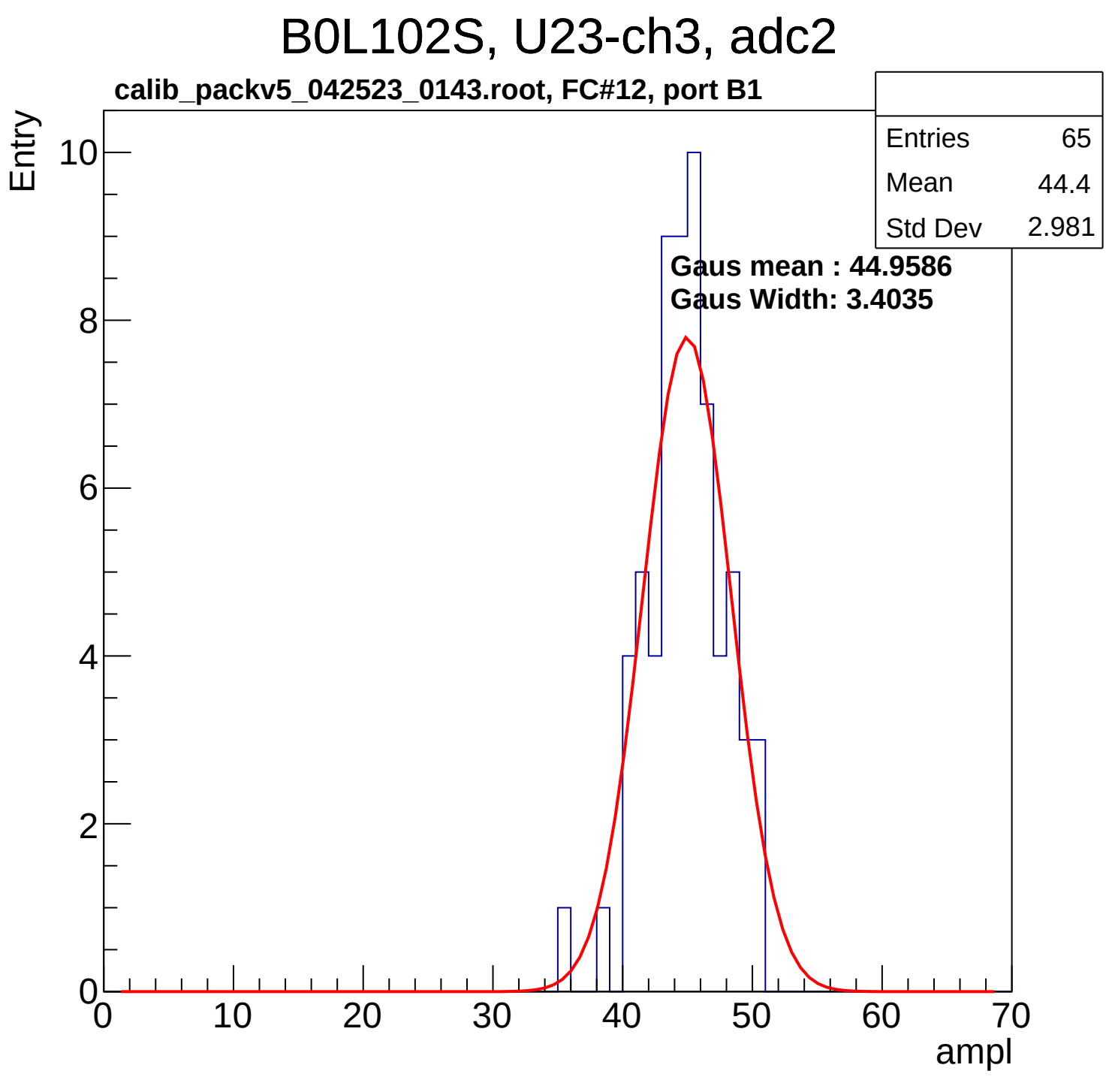
B0L102S, U23-ch3, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 44.4
Std Dev 2.981
Gaus mean : 44.9586
Gaus Width: 3.4035
Entry
ampl
0
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4
6
8
10
0
10
20
30
40
50
60
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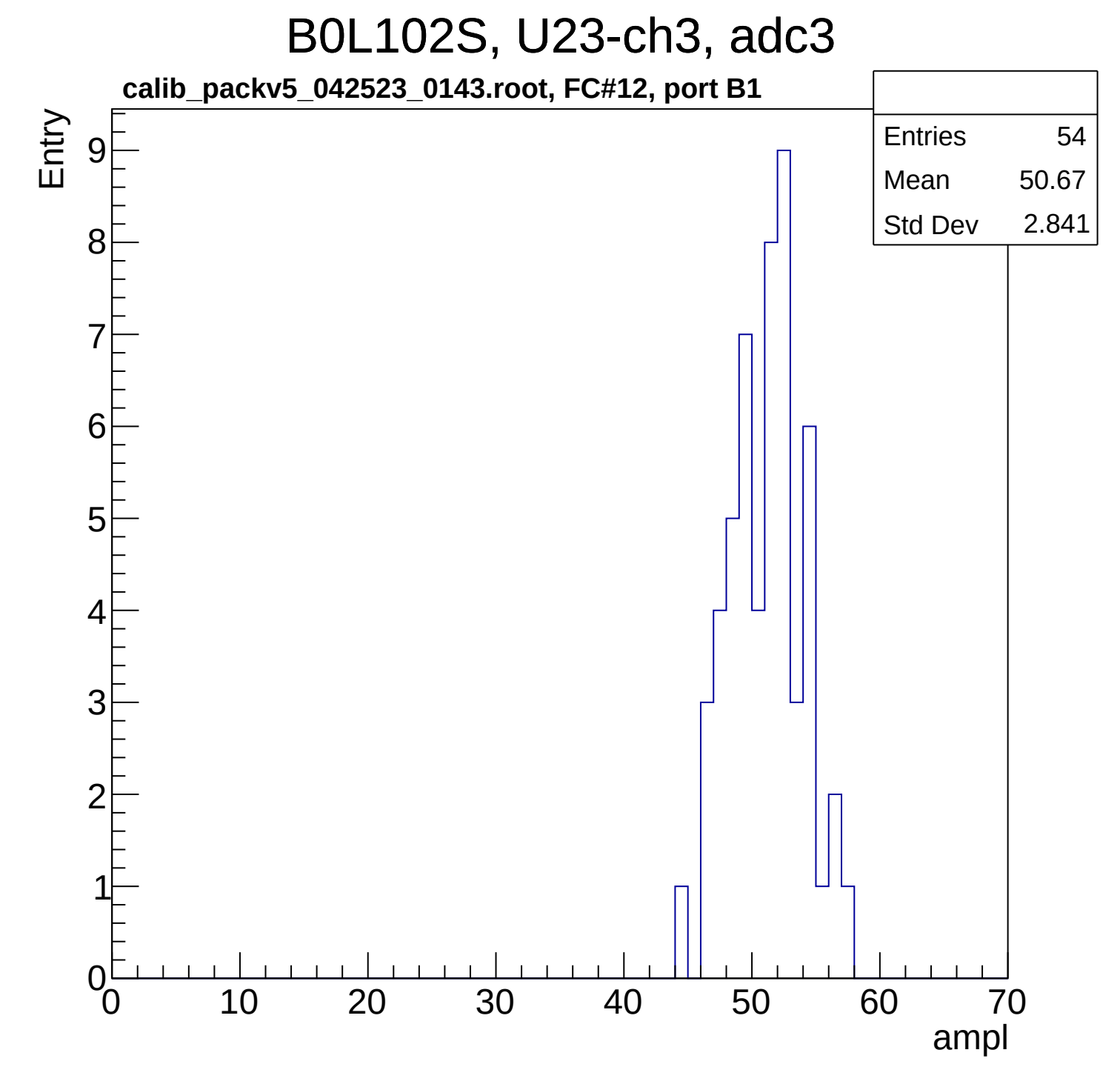
B0L102S, U23-ch3, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 50.67
Std Dev 2.841
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
50
60
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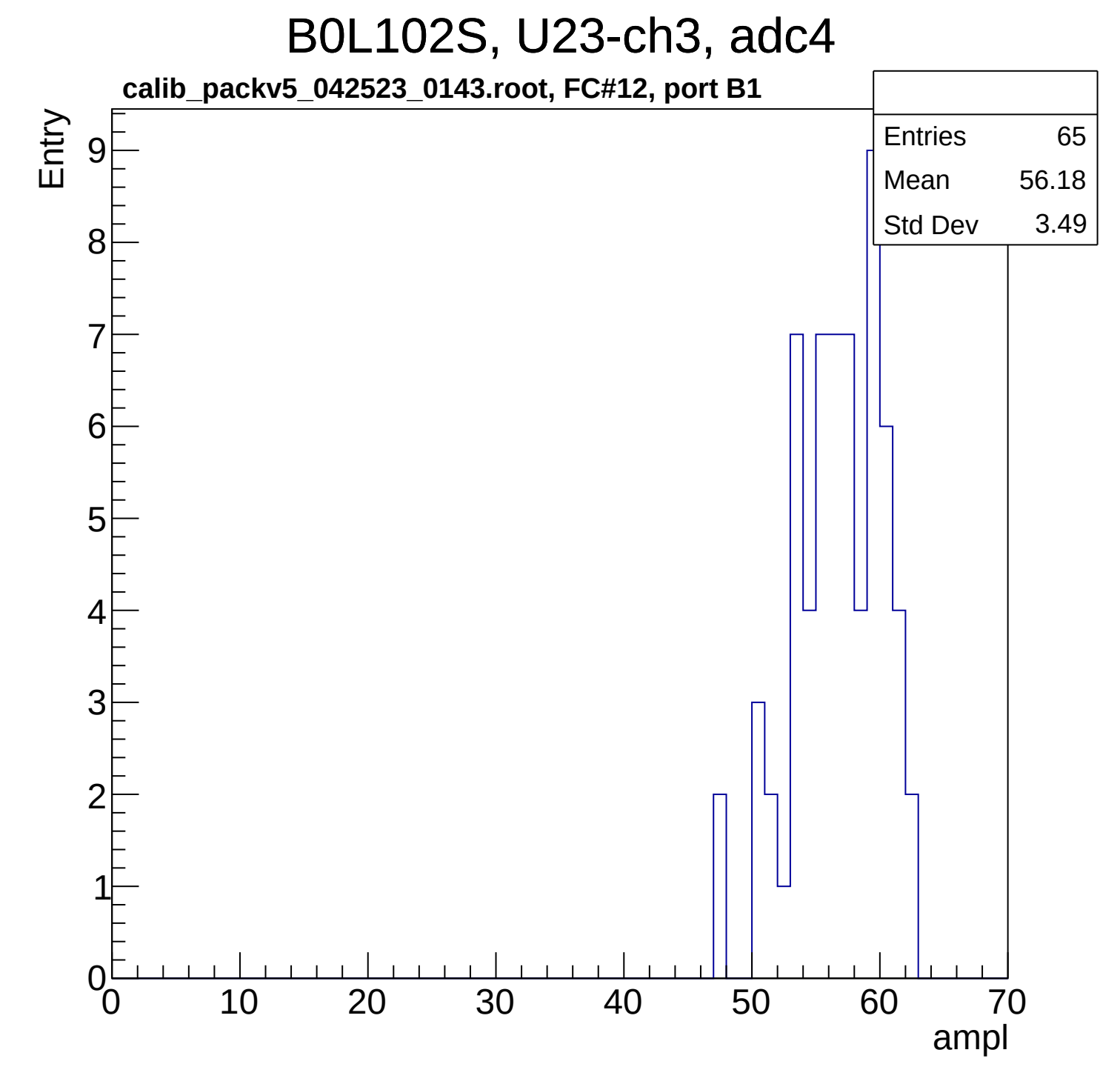
B0L102S, U23-ch3, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 56.18
Std Dev 3.49
Entry
ampl
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5
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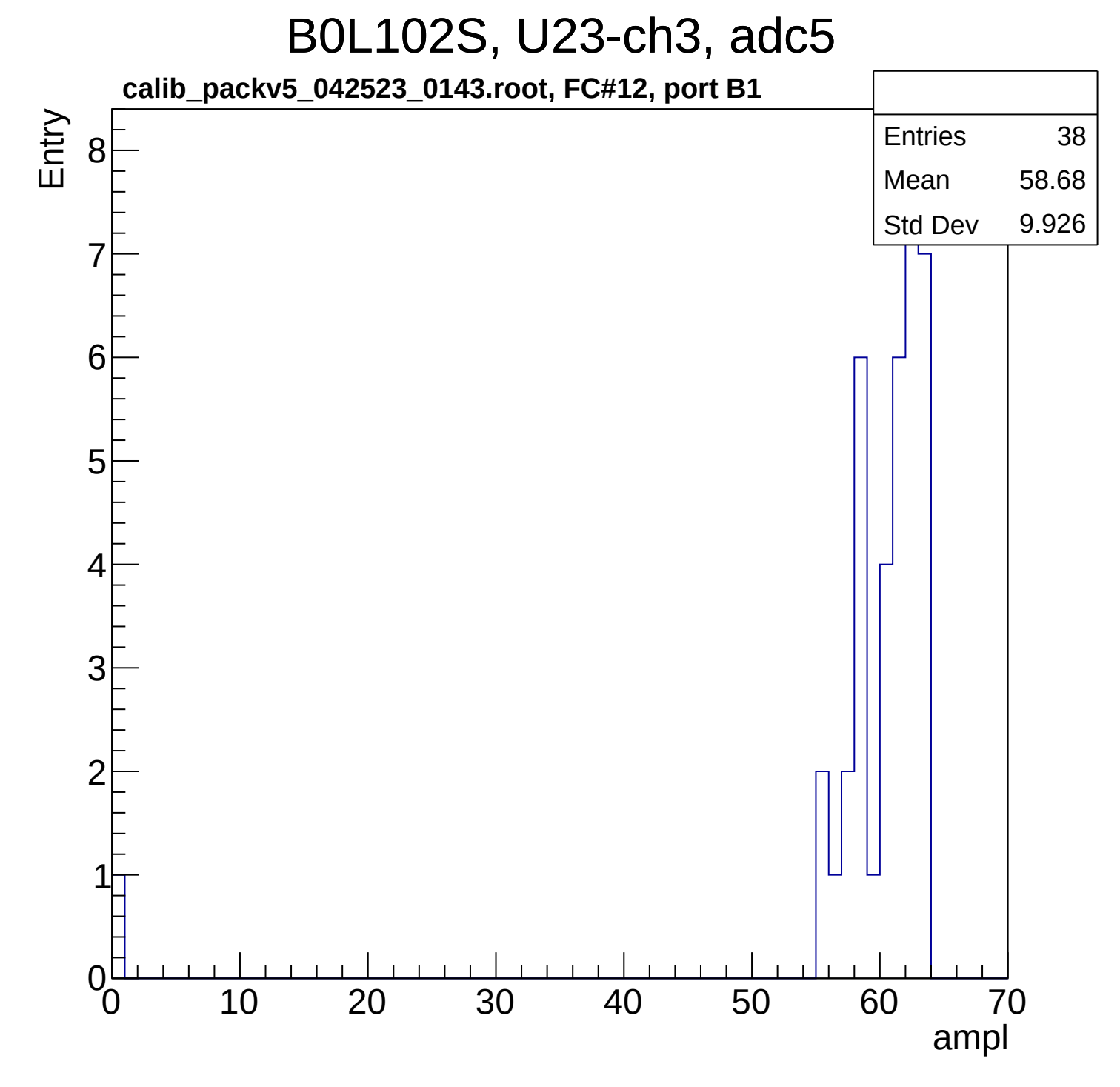
B0L102S, U23-ch3, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.68
Std Dev 9.926
Entry
ampl
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3
4
5
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0
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40
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60
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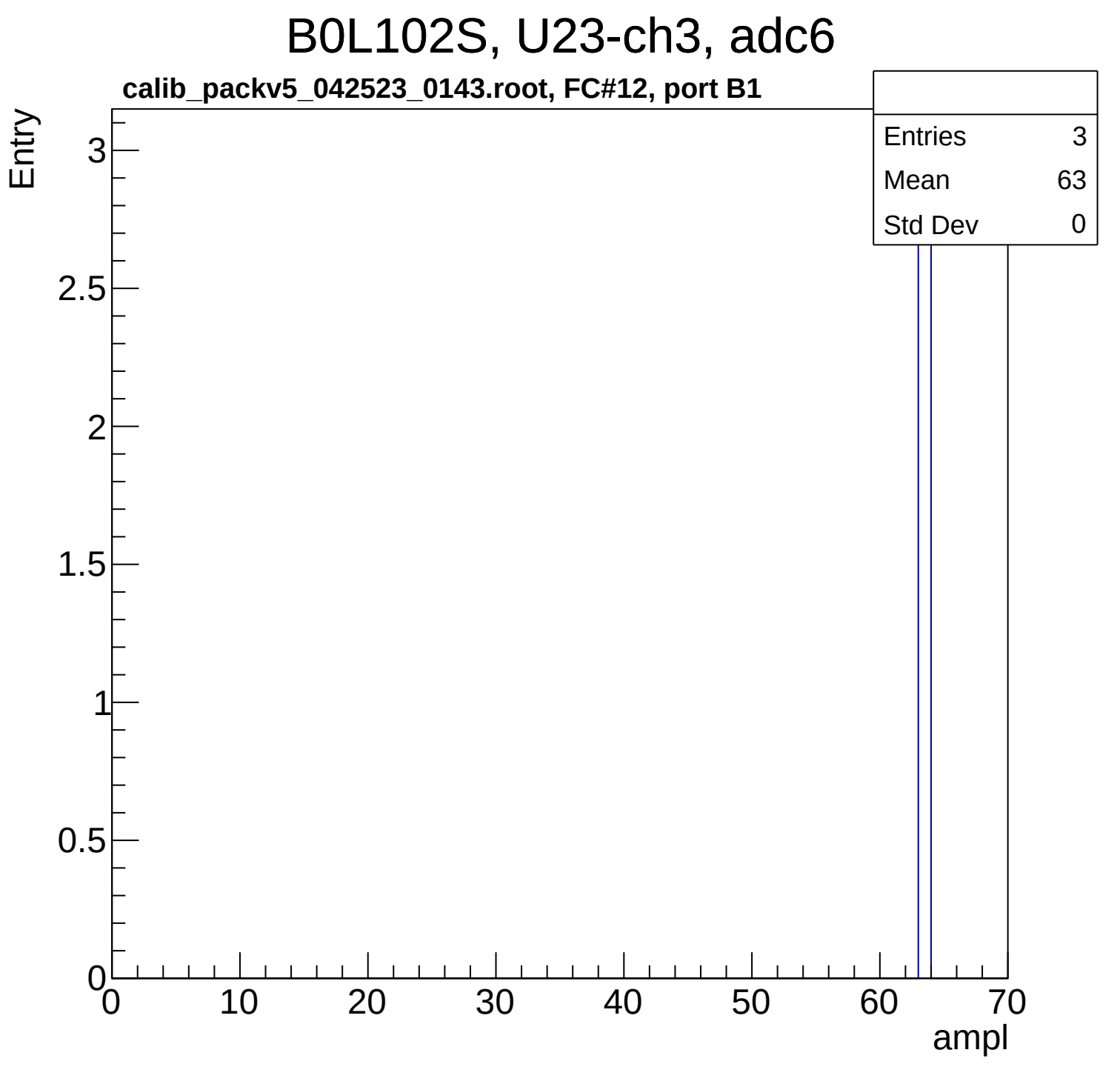
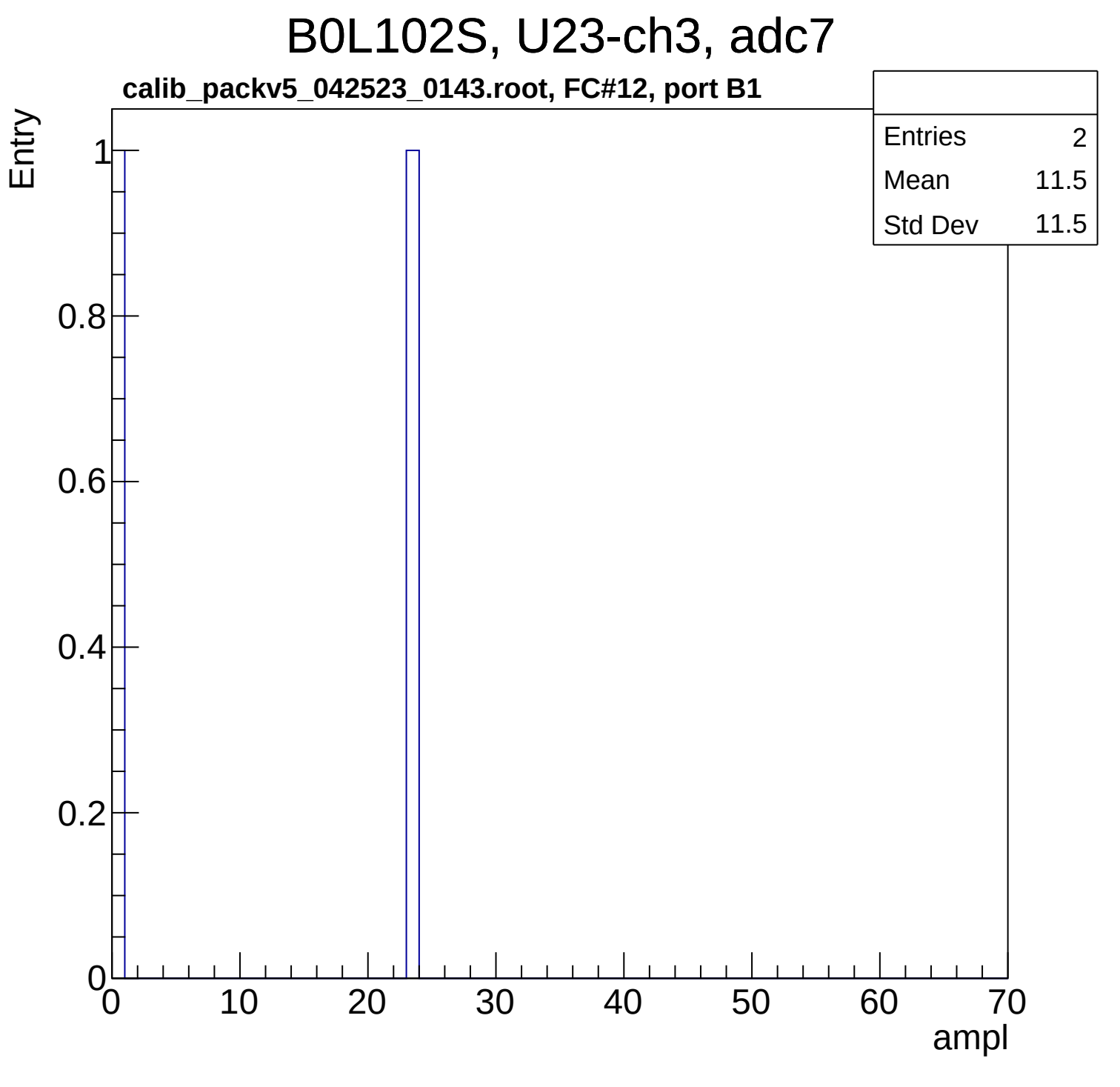

B0L102S, U23-ch3, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch3, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
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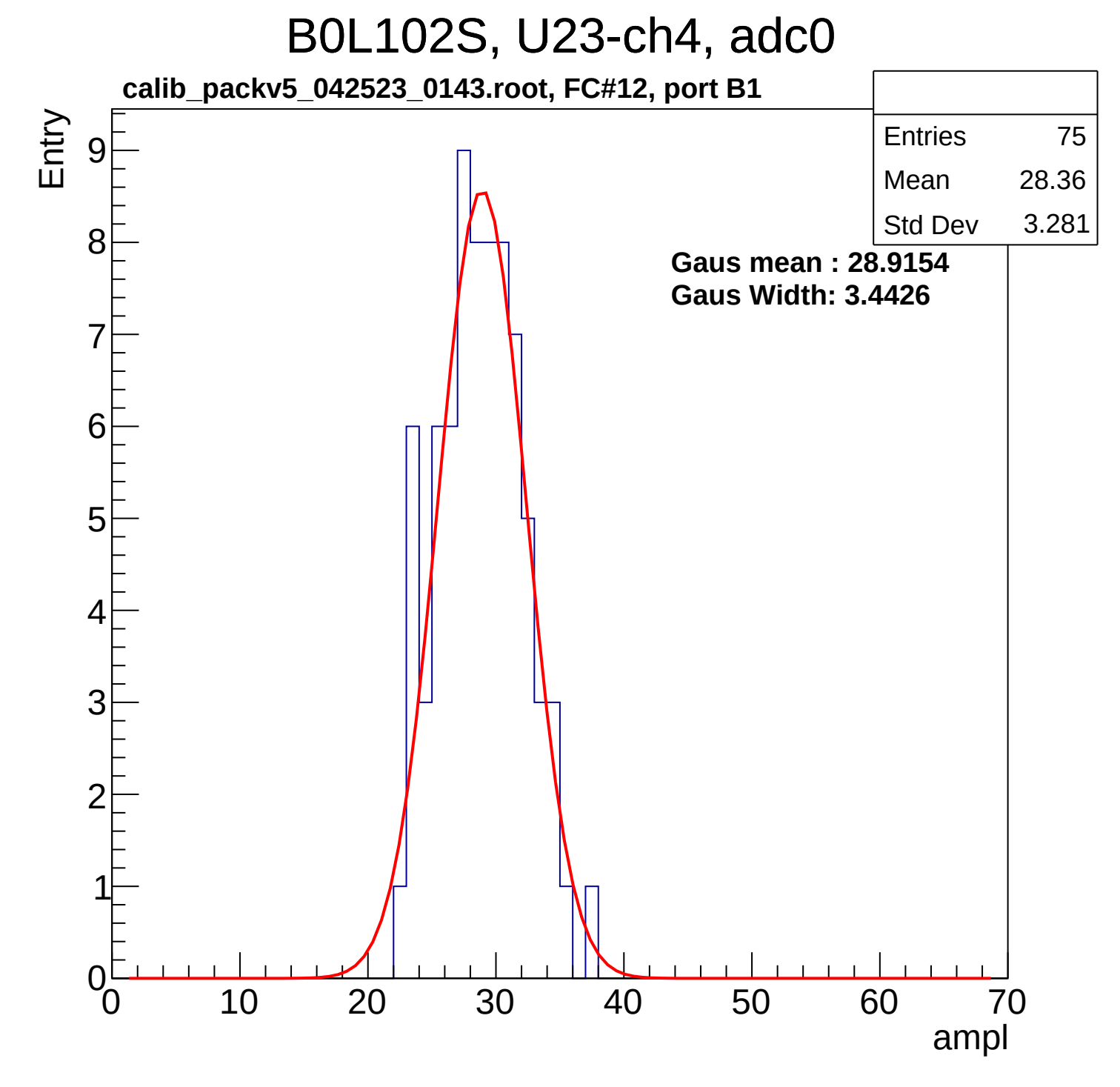
70
ampl


B0L102S, U23-ch4, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 28.36
Std Dev 3.281
Gaus mean : 28.9154
Gaus Width: 3.4426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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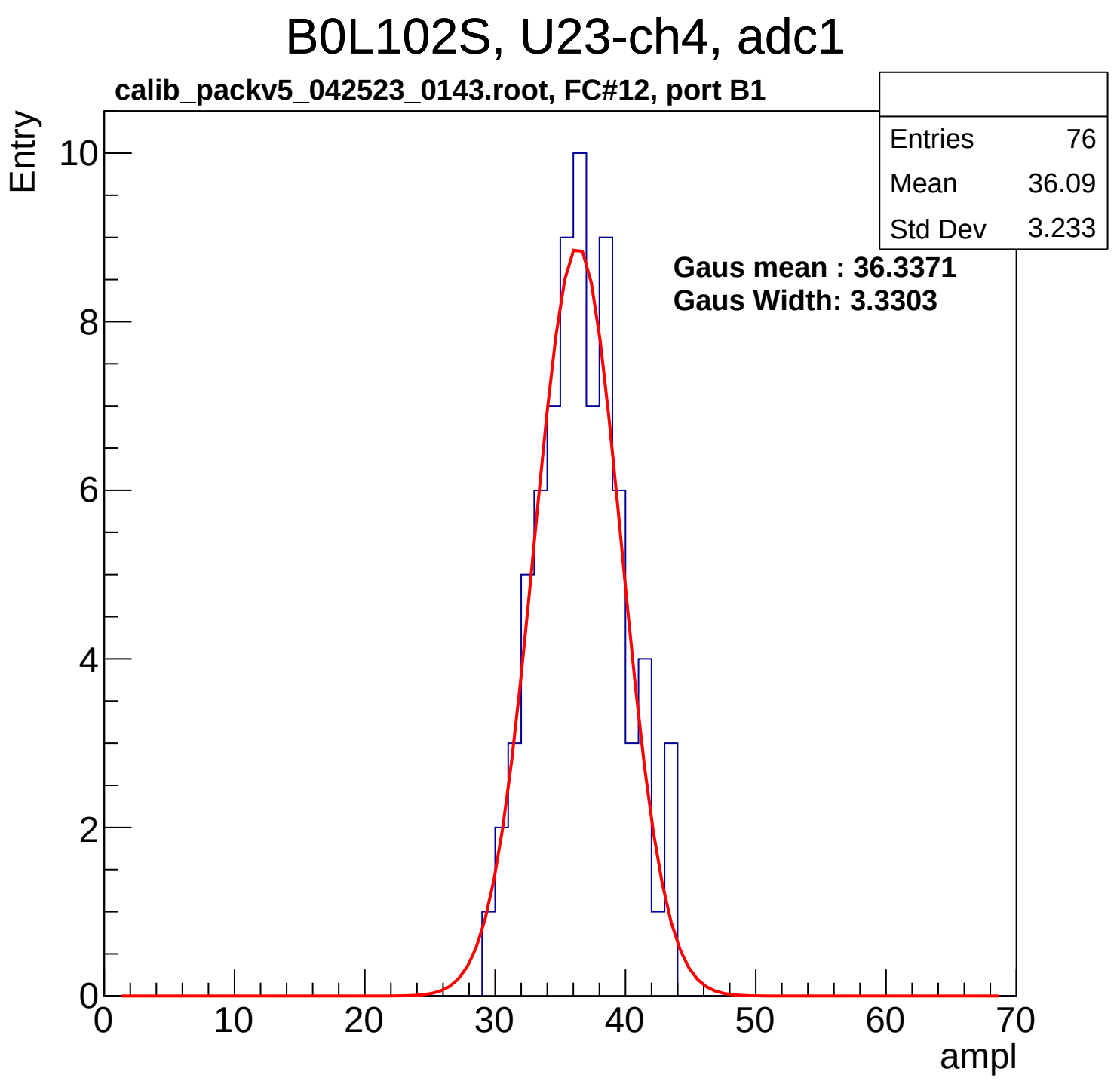
B0L102S, U23-ch4, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 36.09
Std Dev 3.233
Gaus mean : 36.3371
Gaus Width: 3.3303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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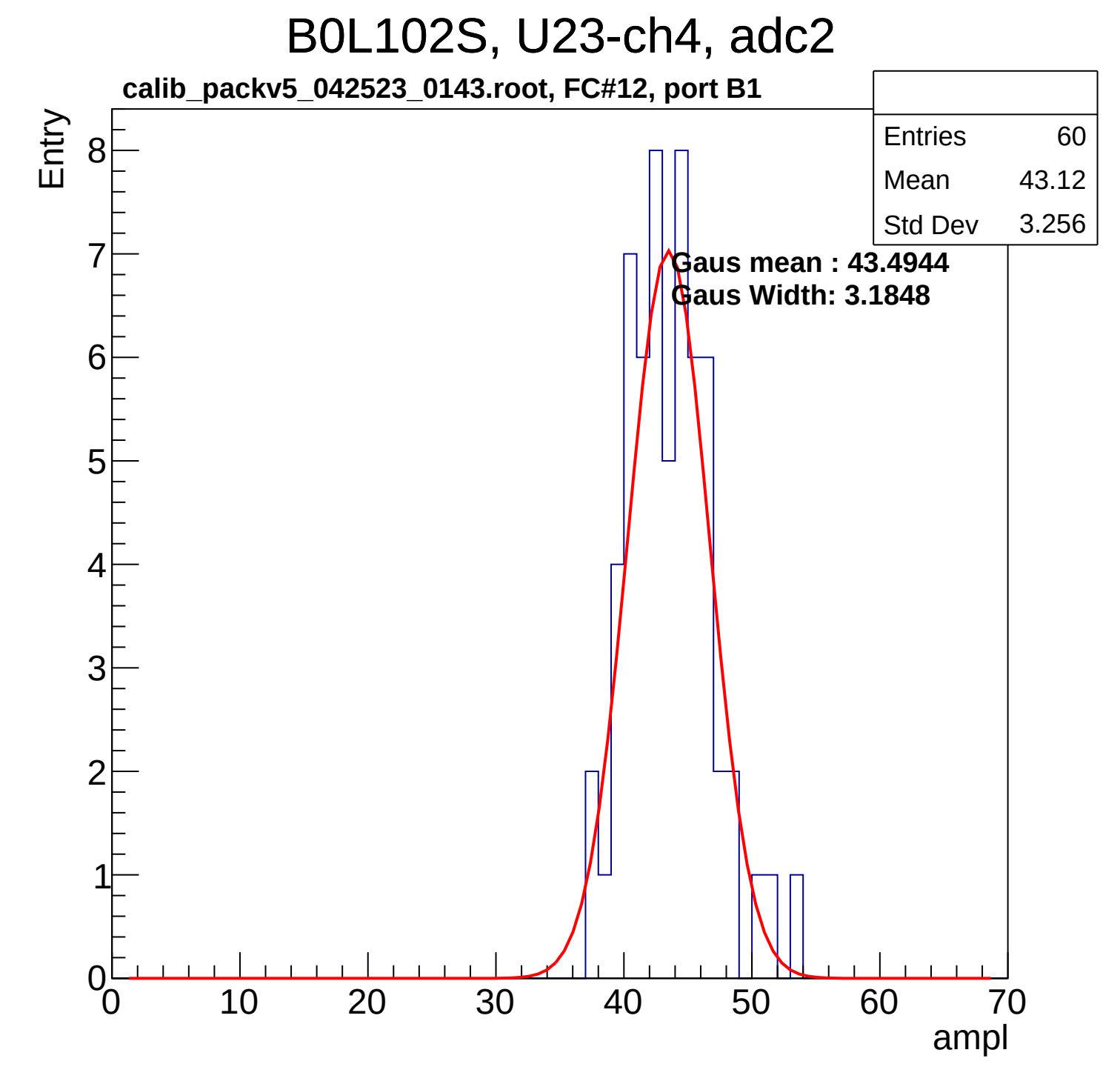
B0L102S, U23-ch4, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 43.12
Std Dev 3.256
Gaus mean : 43.4944
Gaus Width: 3.1848
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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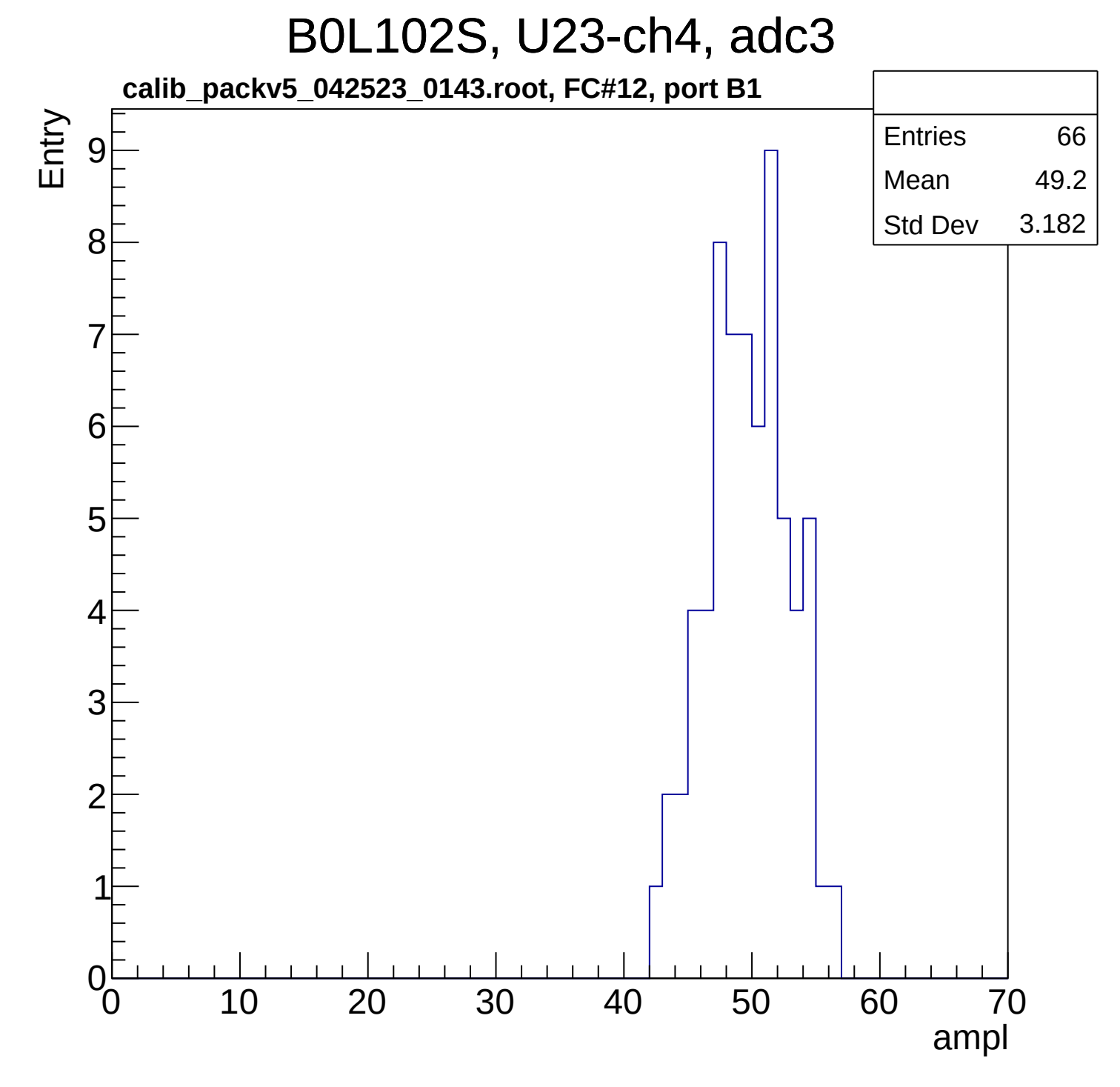
B0L102S, U23-ch4, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.2
Std Dev 3.182
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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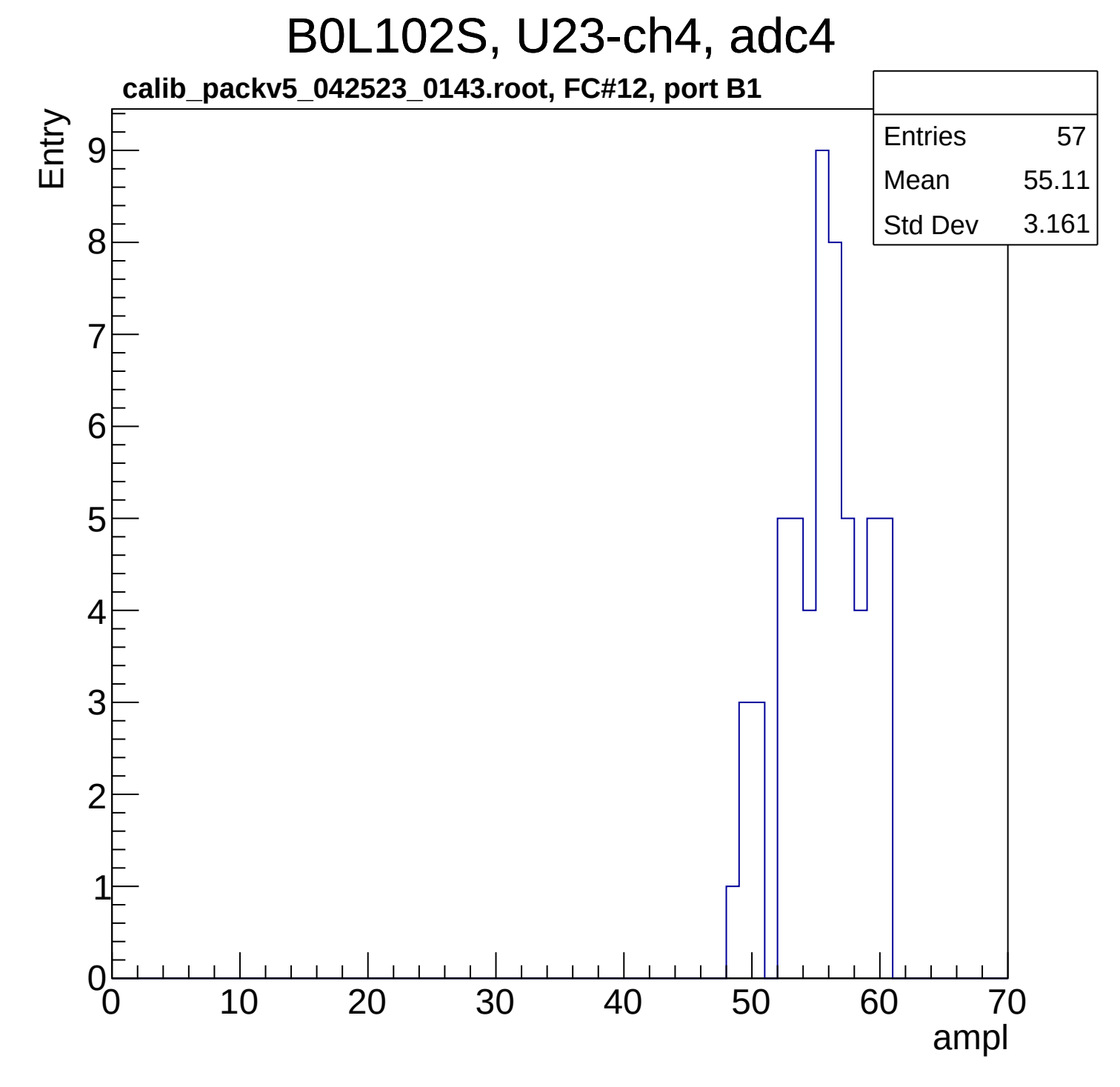
B0L102S, U23-ch4, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 55.11
Std Dev 3.161
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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60
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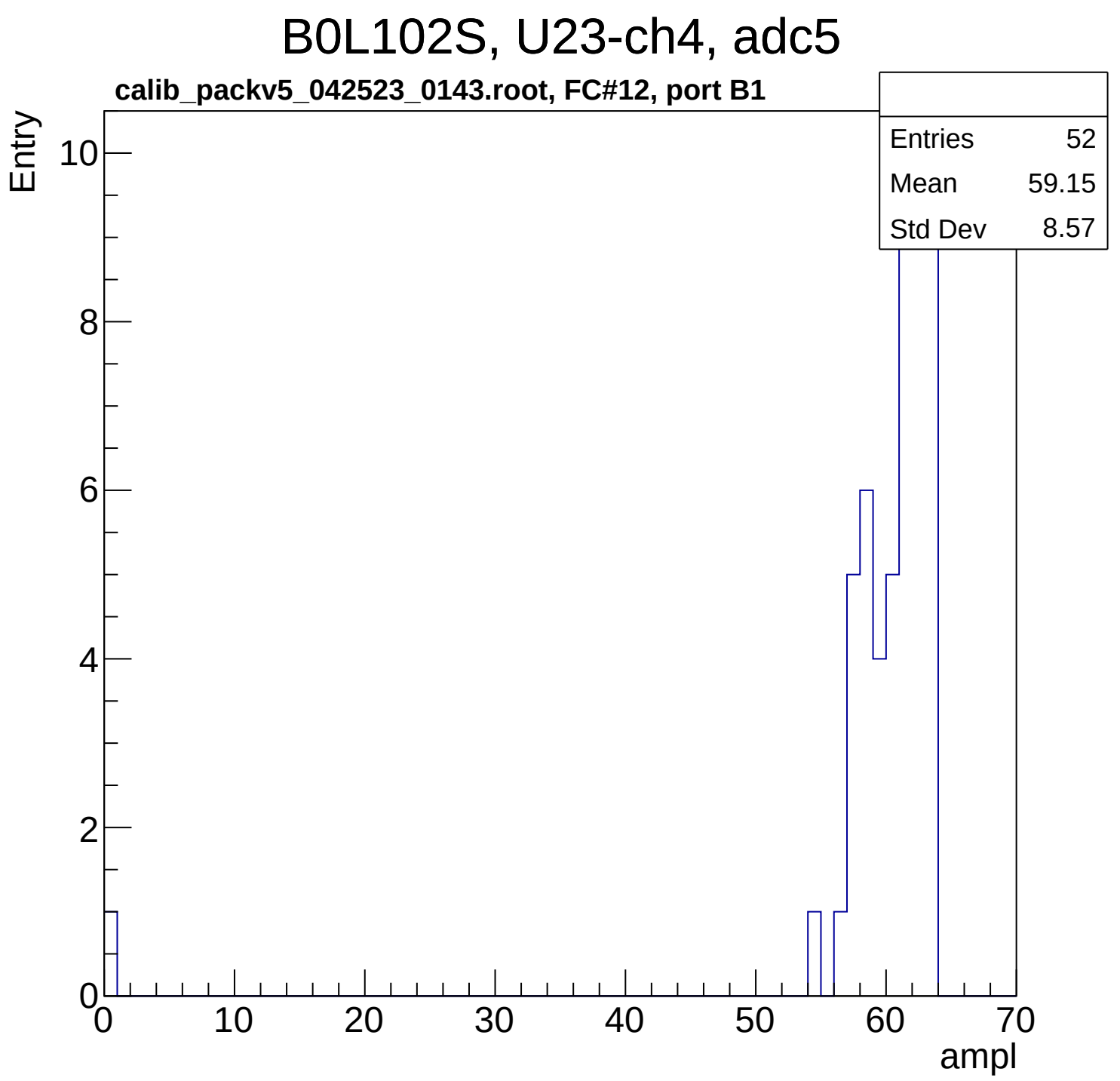
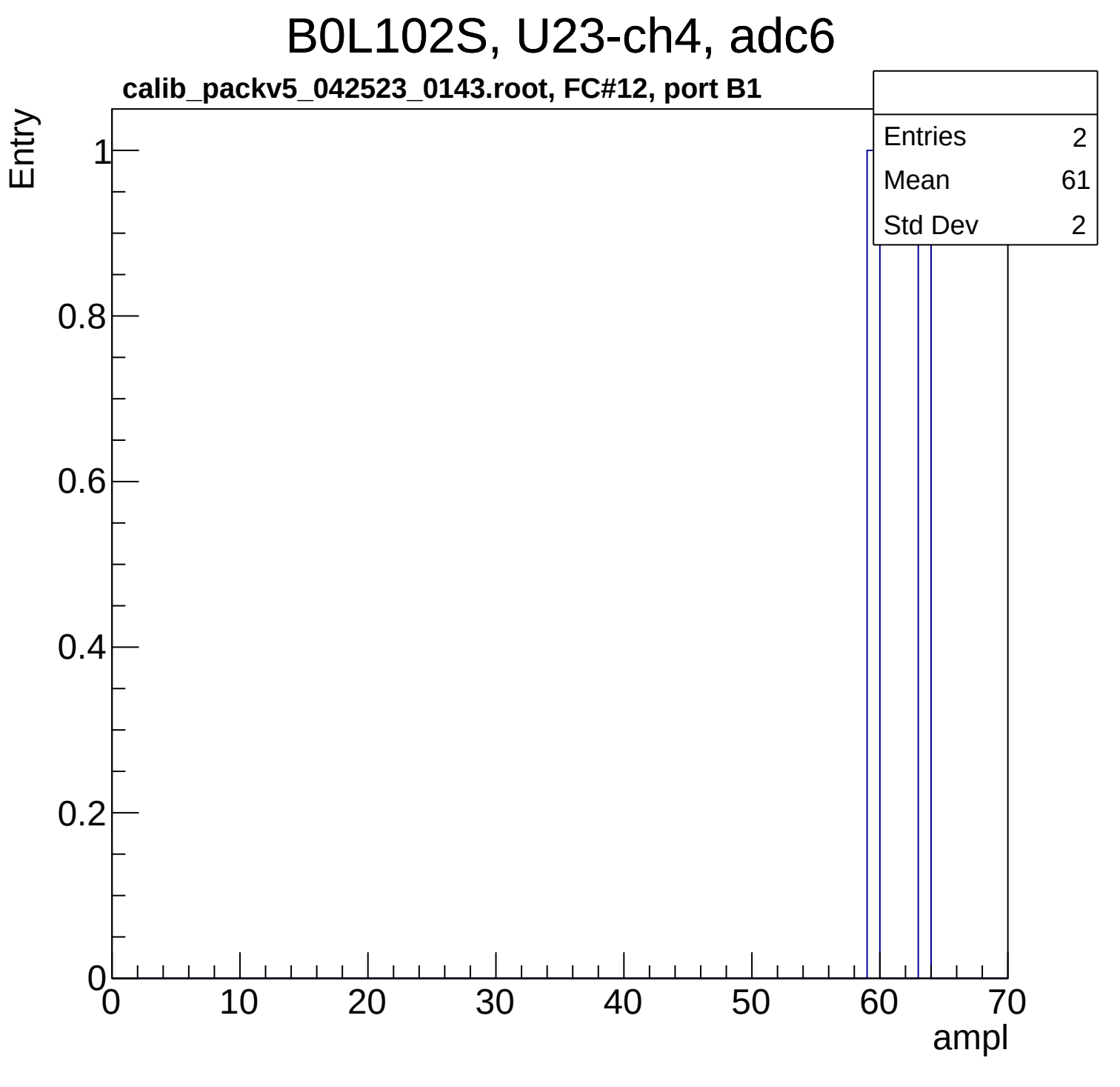

B0L102S, U23-ch4, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 59.15
Std Dev 8.57
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch4, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61
Std Dev 2
Entry
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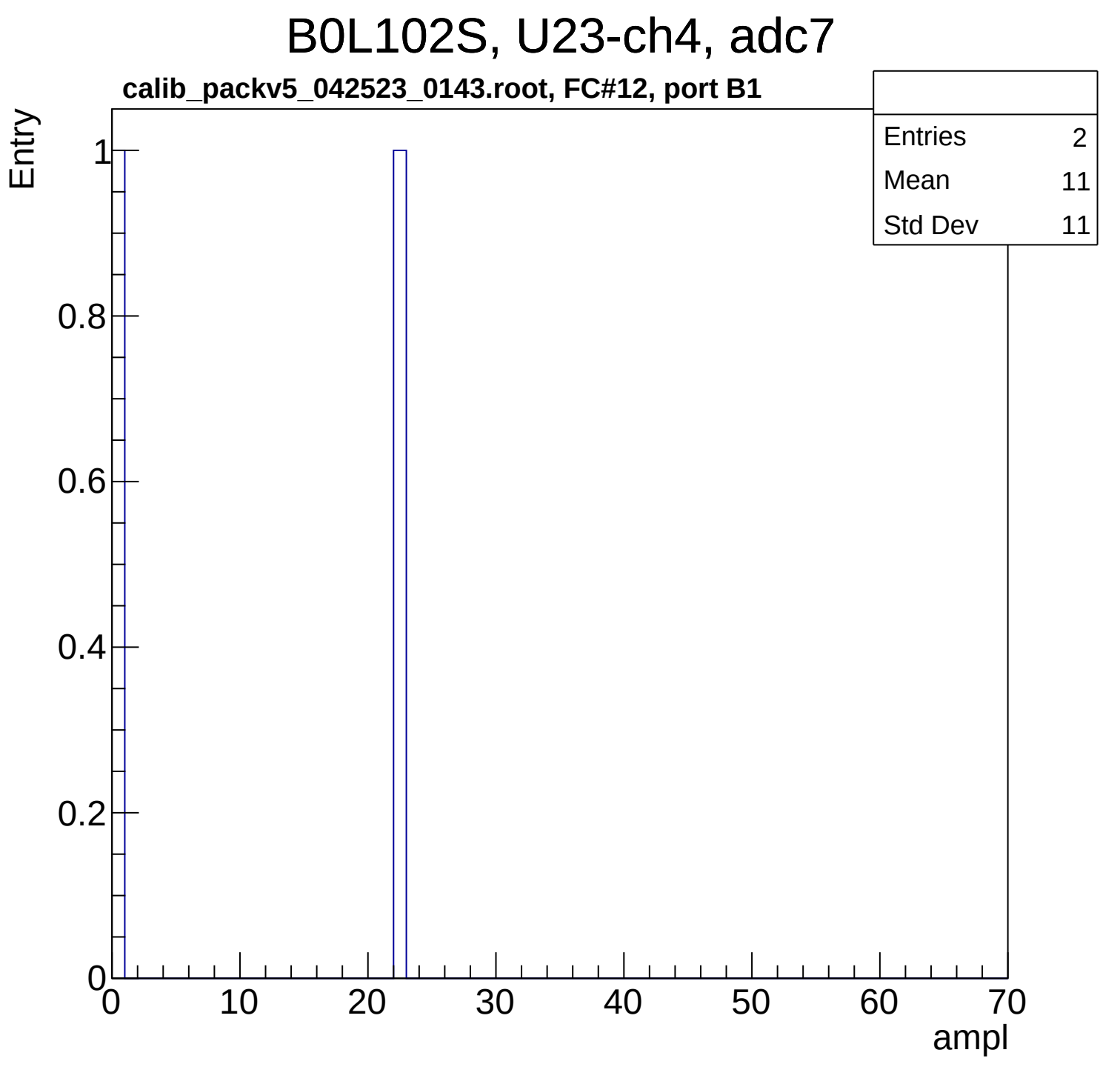
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch4, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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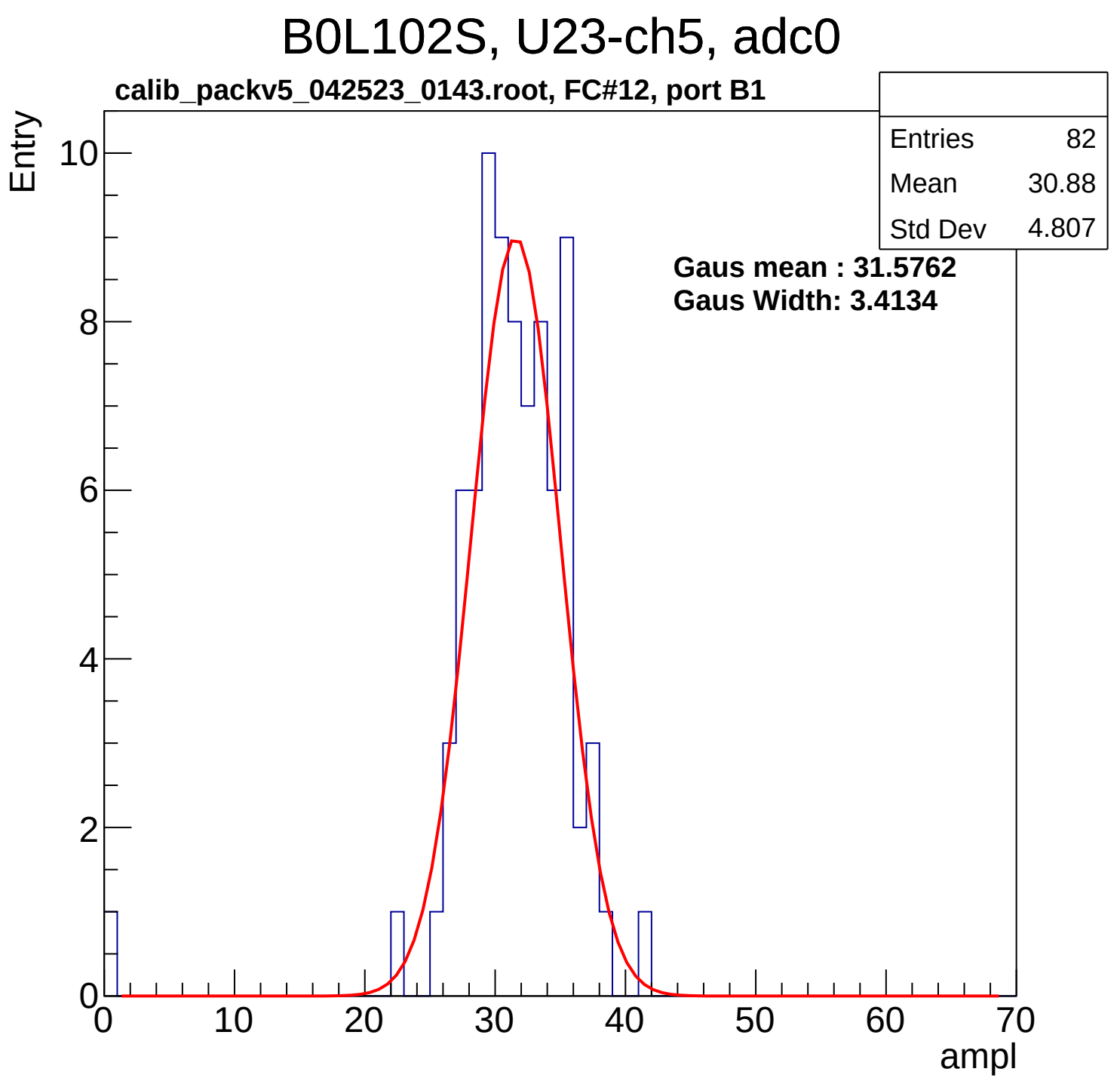
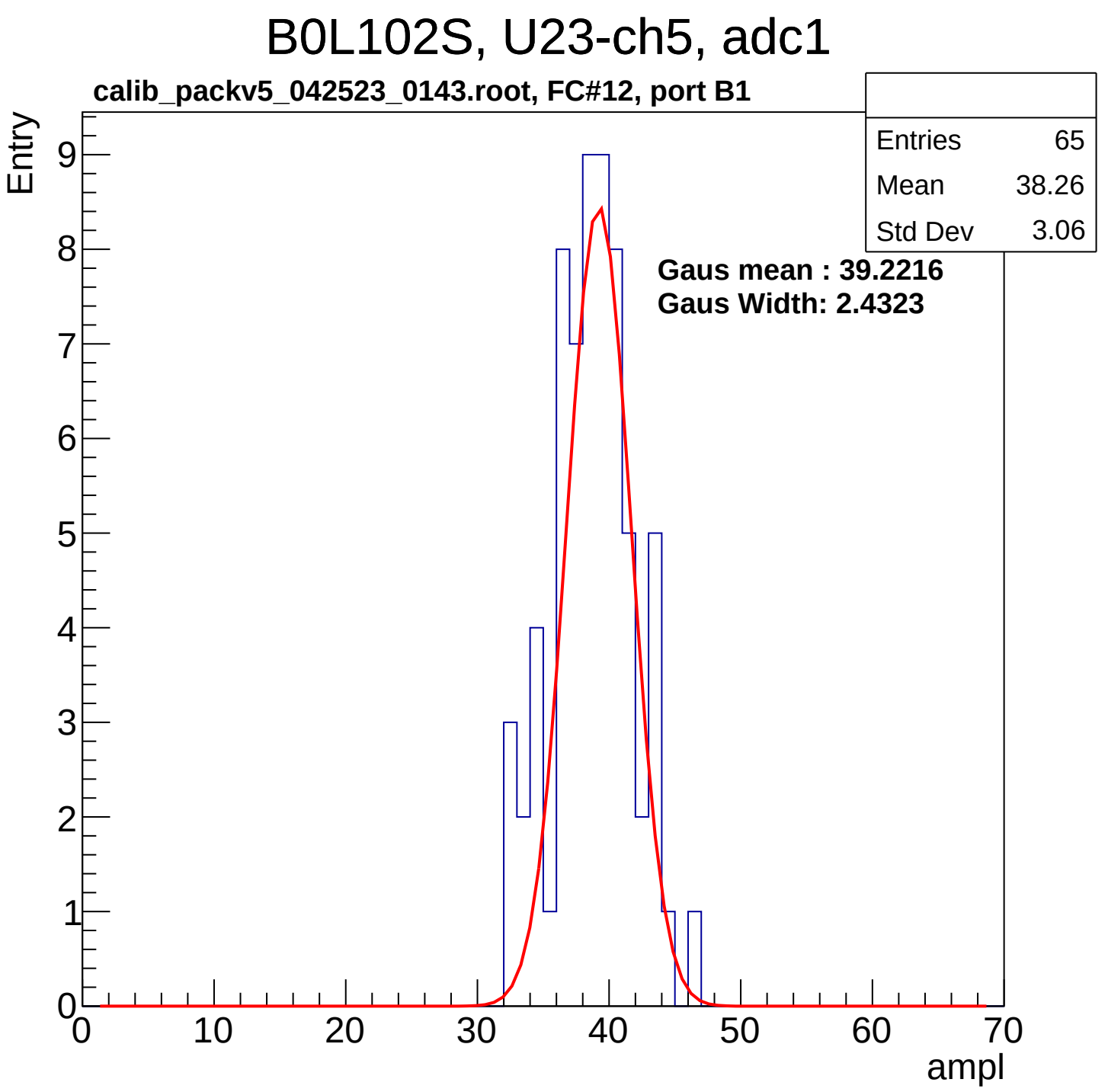
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch5, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 30.88
Std Dev 4.807
Gaus mean : 31.5762
Gaus Width: 3.4134
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch5, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 38.26
Std Dev 3.06
Gaus mean : 39.2216
Gaus Width: 2.4323
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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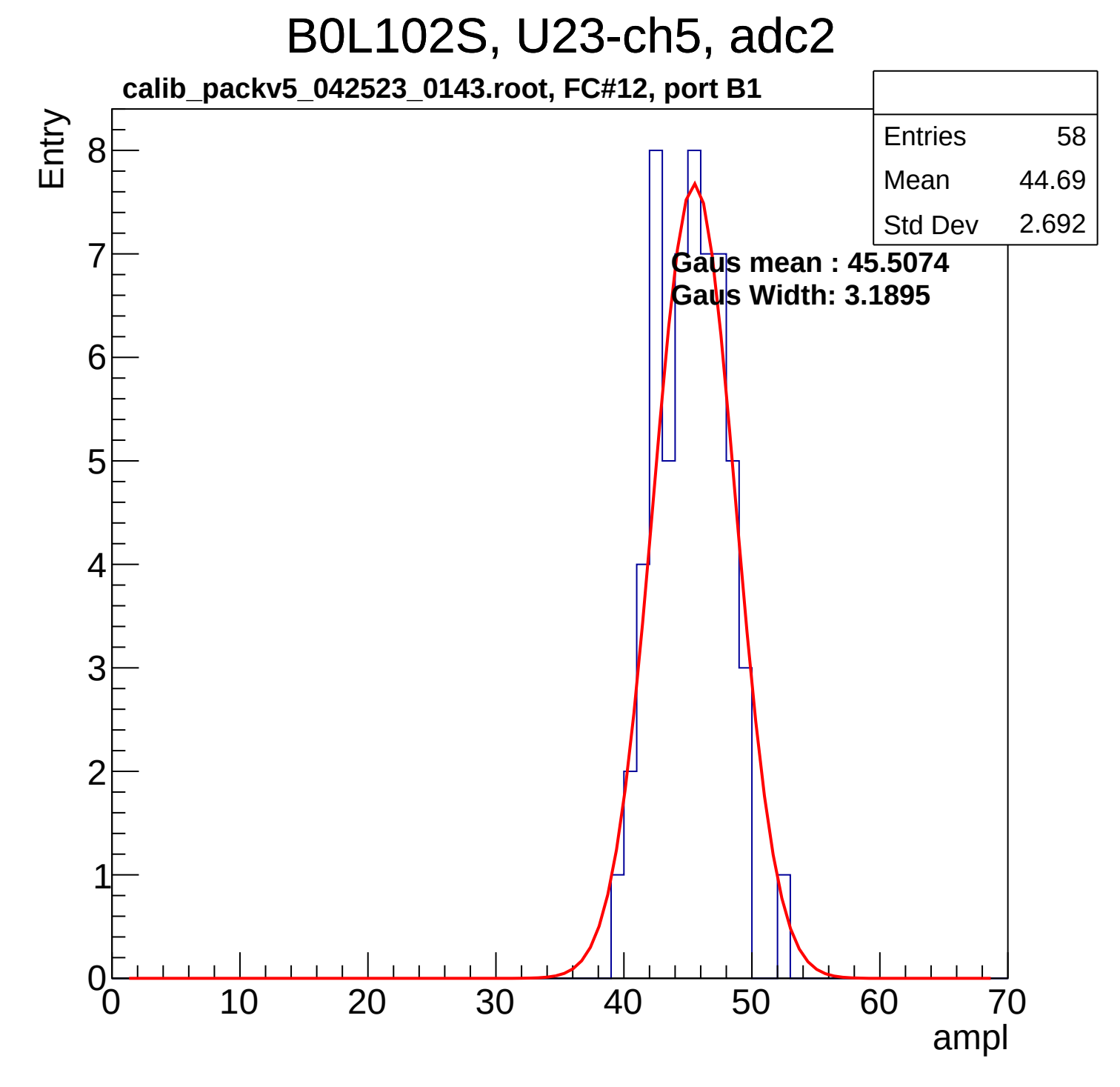
B0L102S, U23-ch5, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 44.69
Std Dev 2.692
Gaus mean : 45.5074
Gaus Width: 3.1895
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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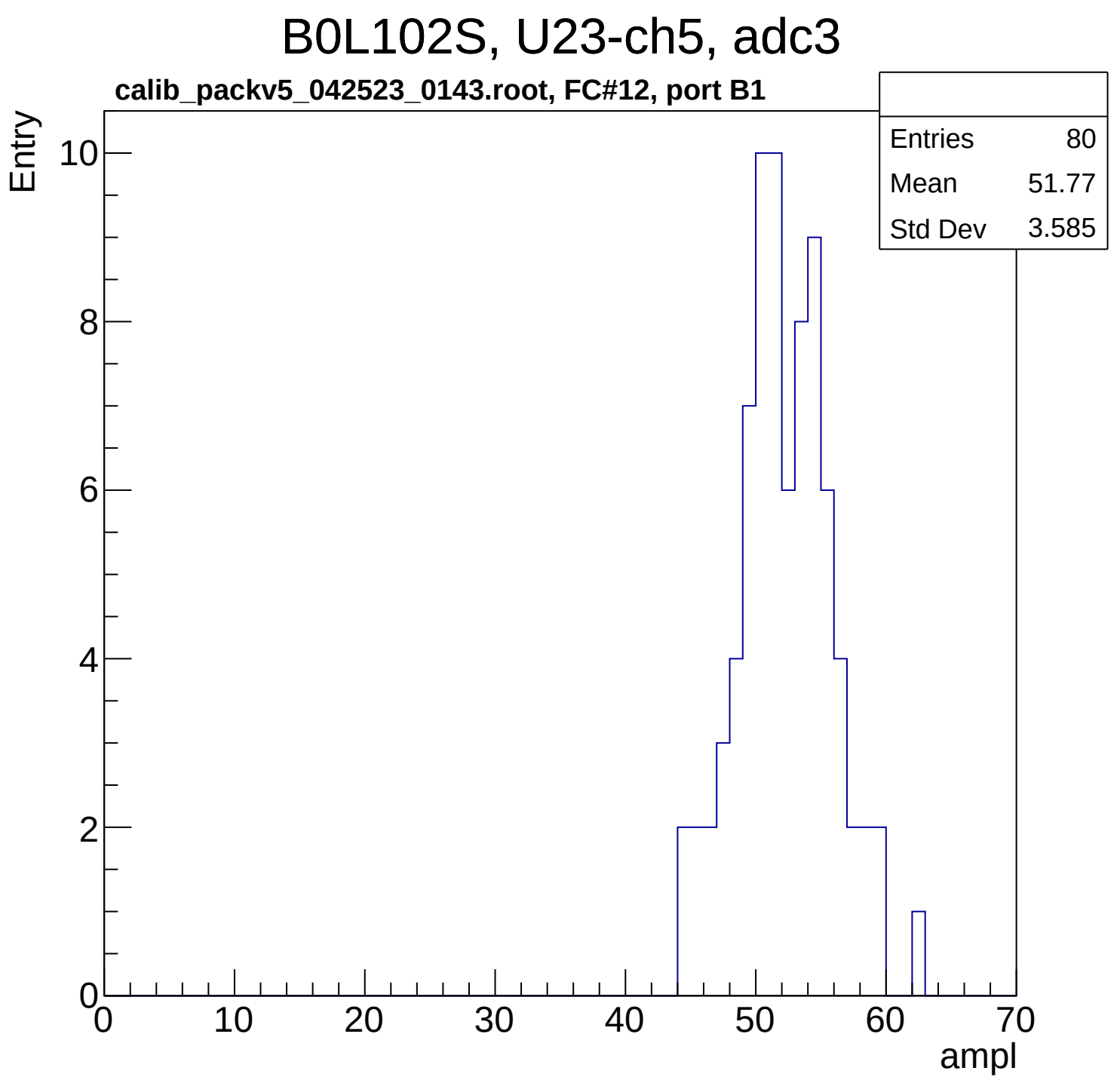
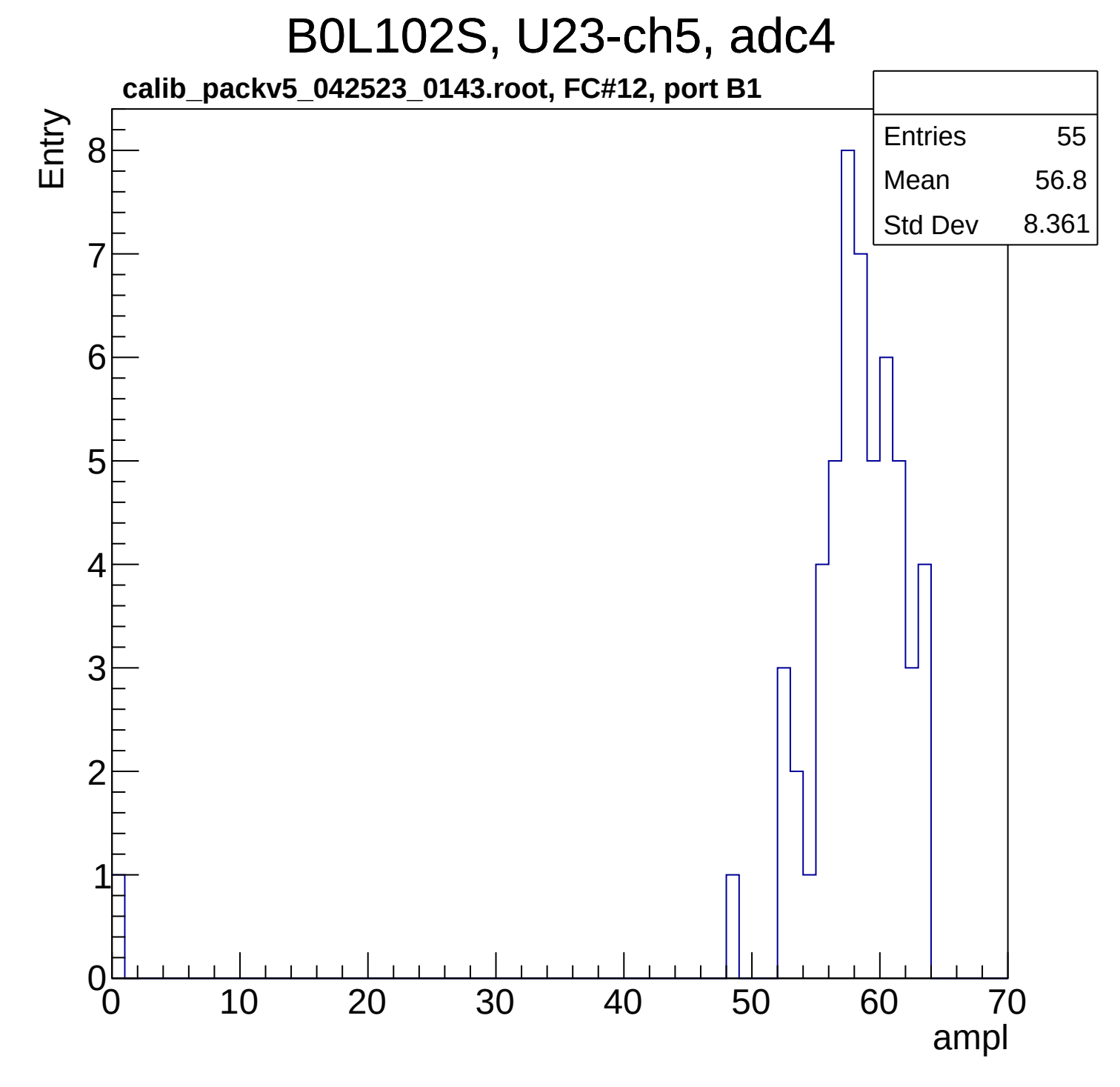

B0L102S, U23-ch5, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 51.77
Std Dev 3.585
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch5, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.8
Std Dev 8.361
Entry
ampl
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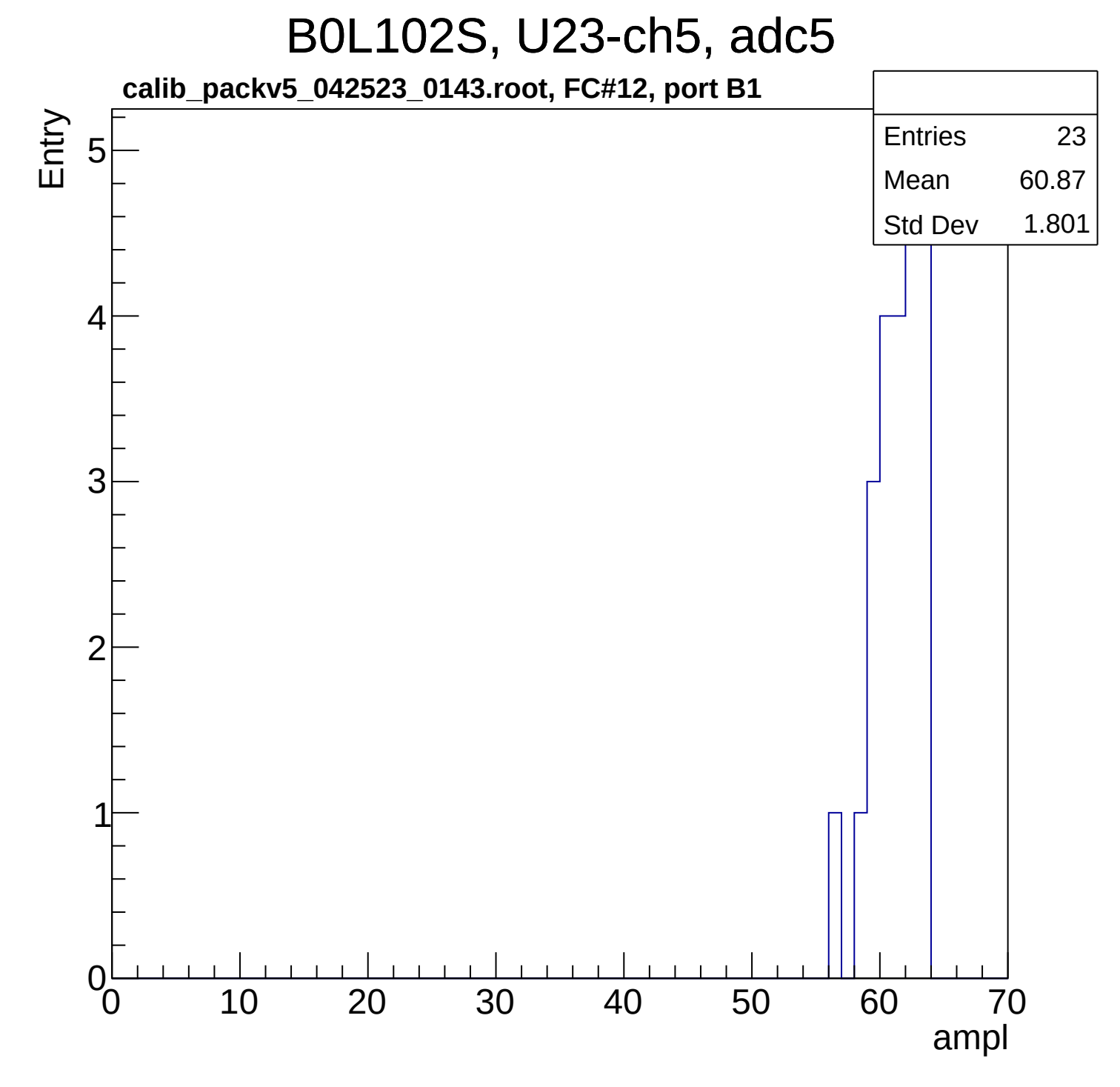
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch5, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 23
Mean 60.87
Std Dev 1.801
Entry
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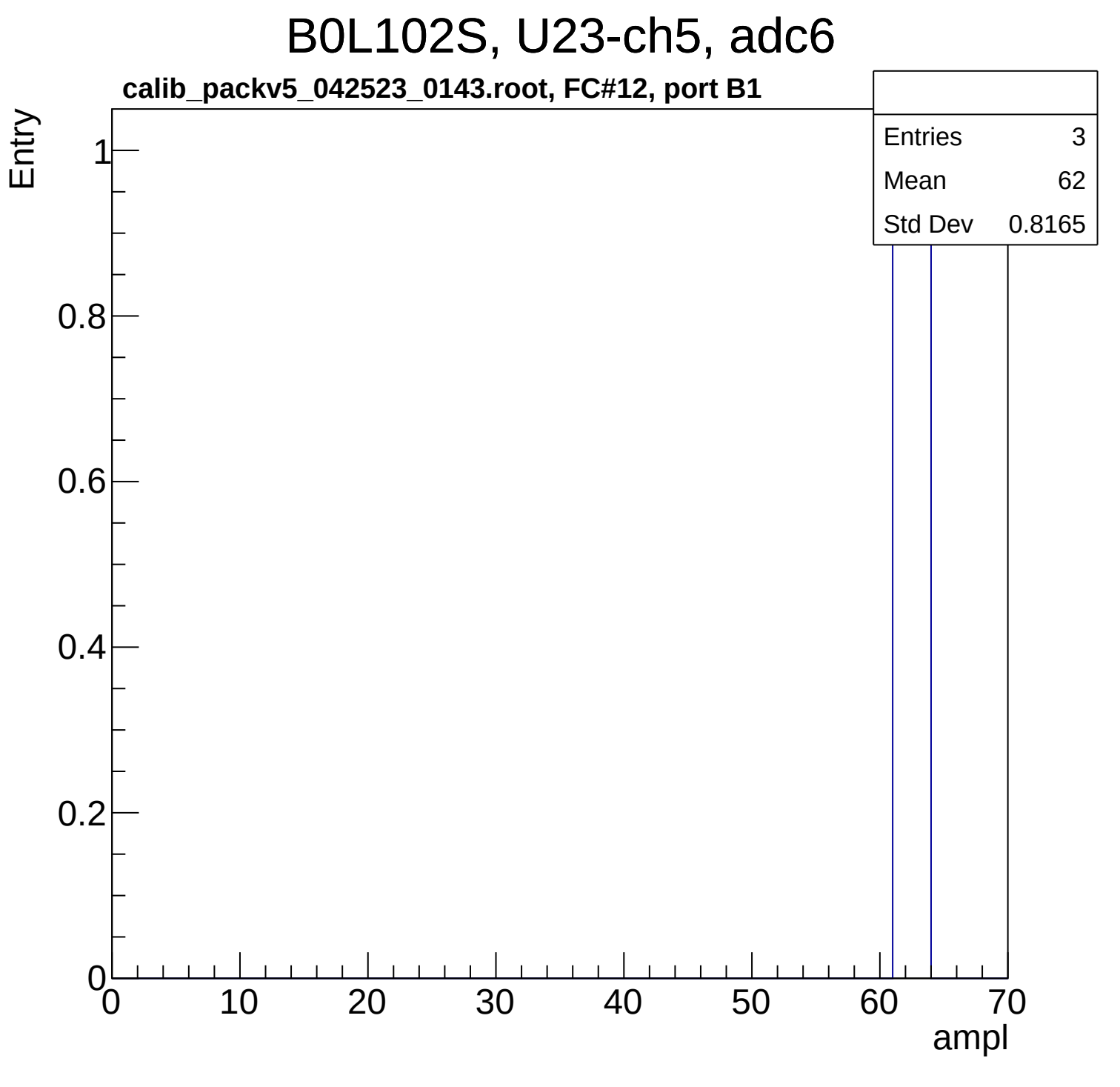
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch5, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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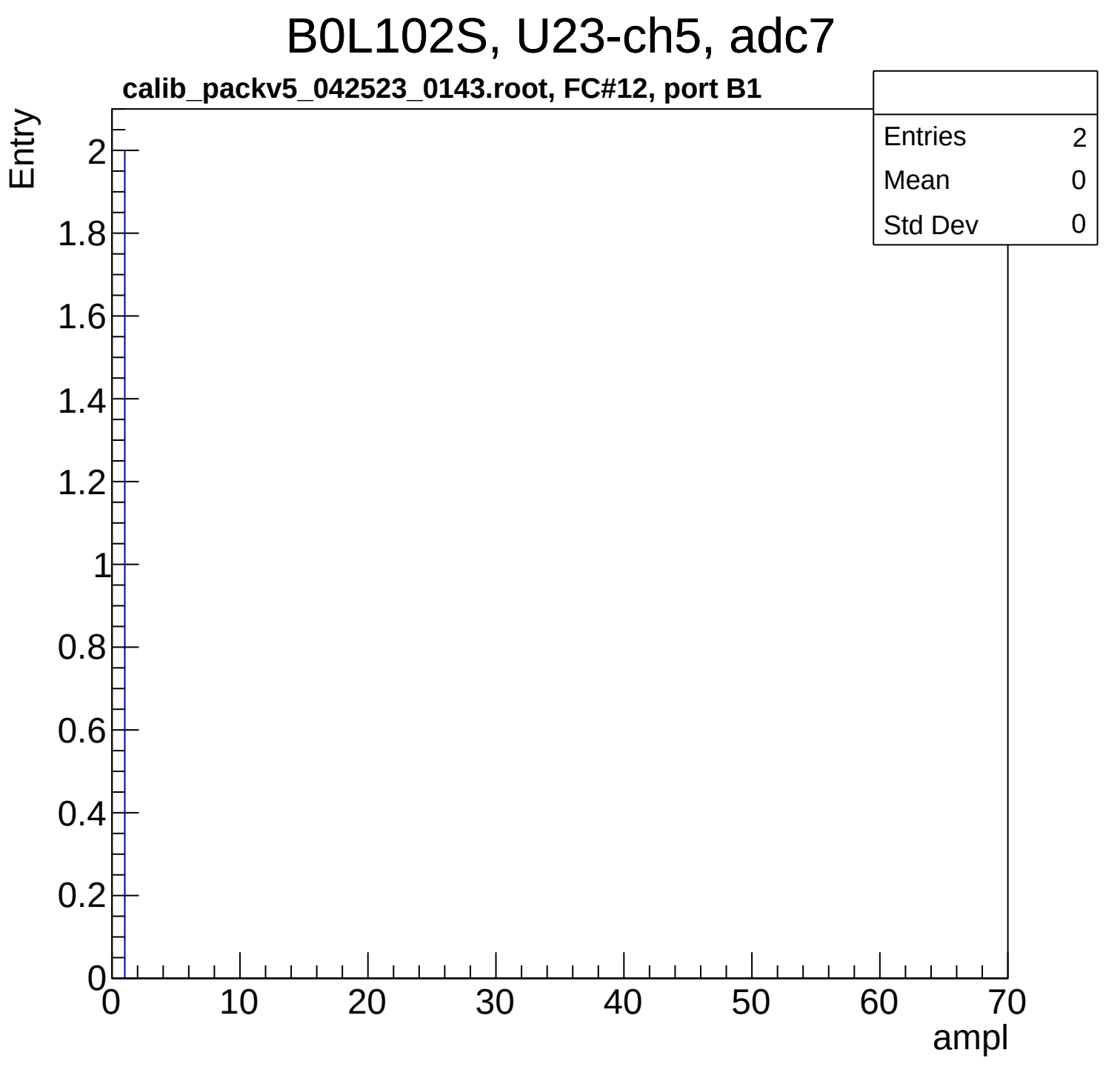
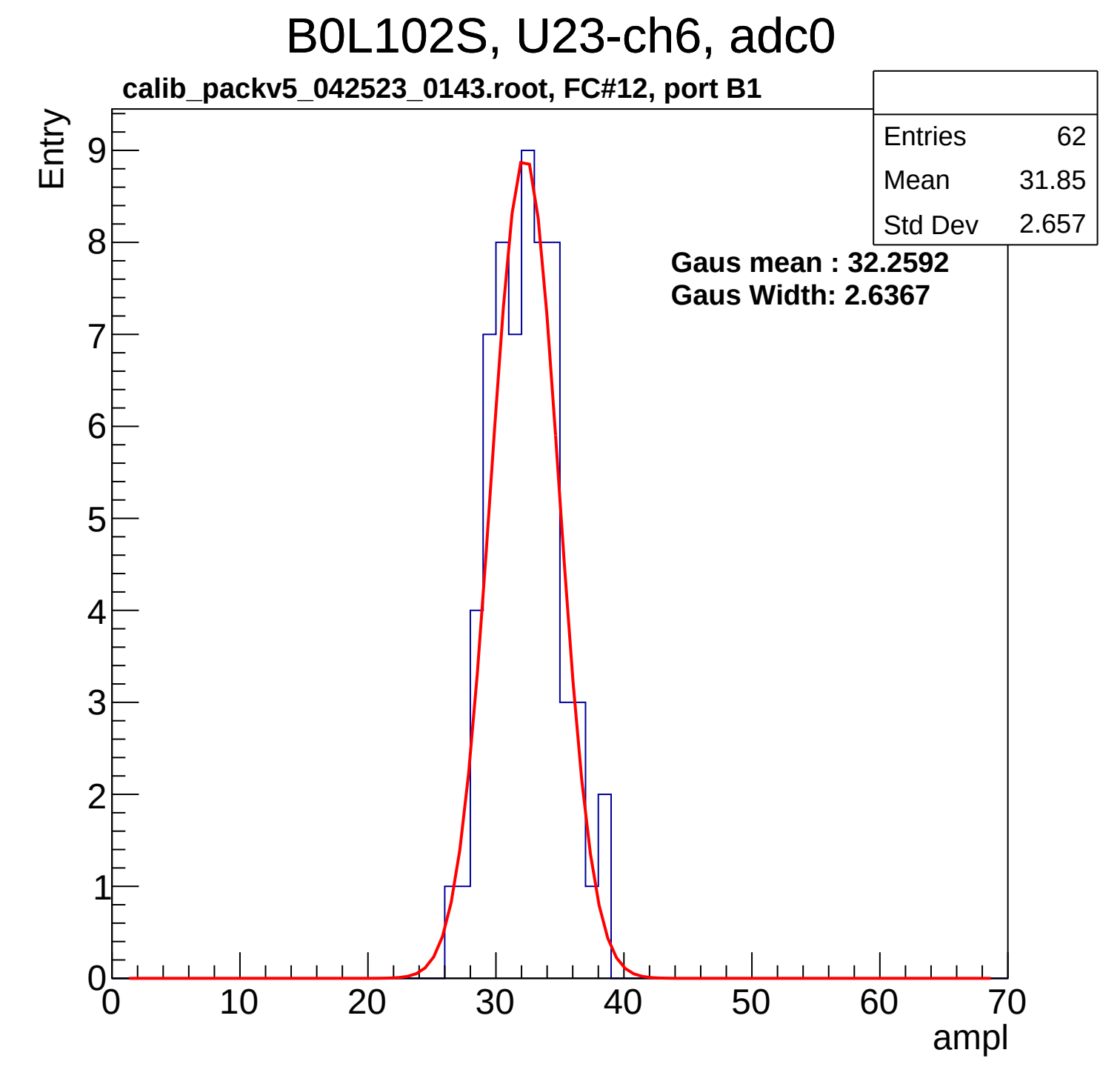
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch5, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch6, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 31.85
Std Dev 2.657
Gaus mean : 32.2592
Gaus Width: 2.6367
Entry
ampl
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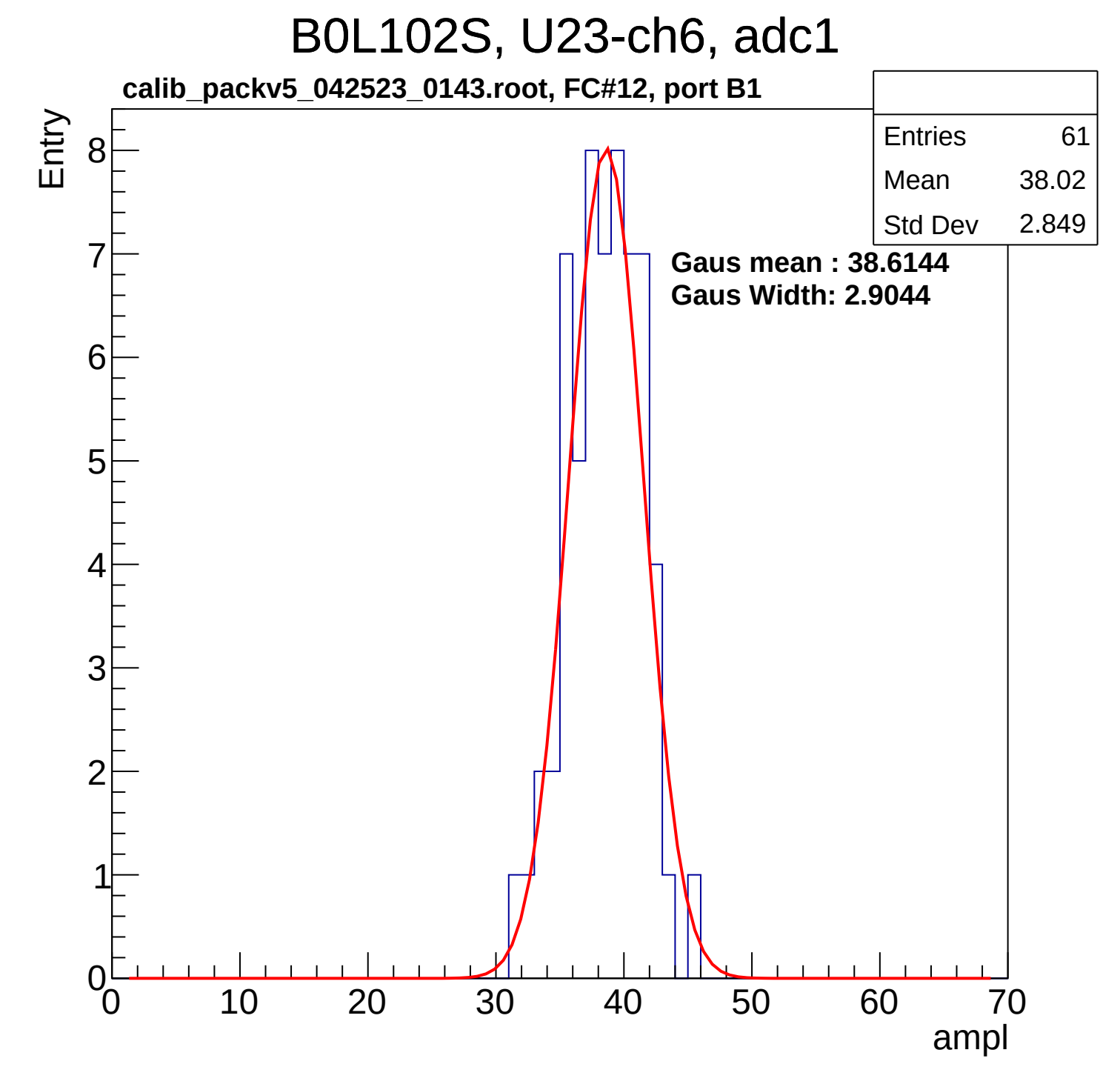
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch6, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 38.02
Std Dev 2.849
Gaus mean : 38.6144
Gaus Width: 2.9044
Entry
ampl
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1
2
3
4
5
6
7
8
0
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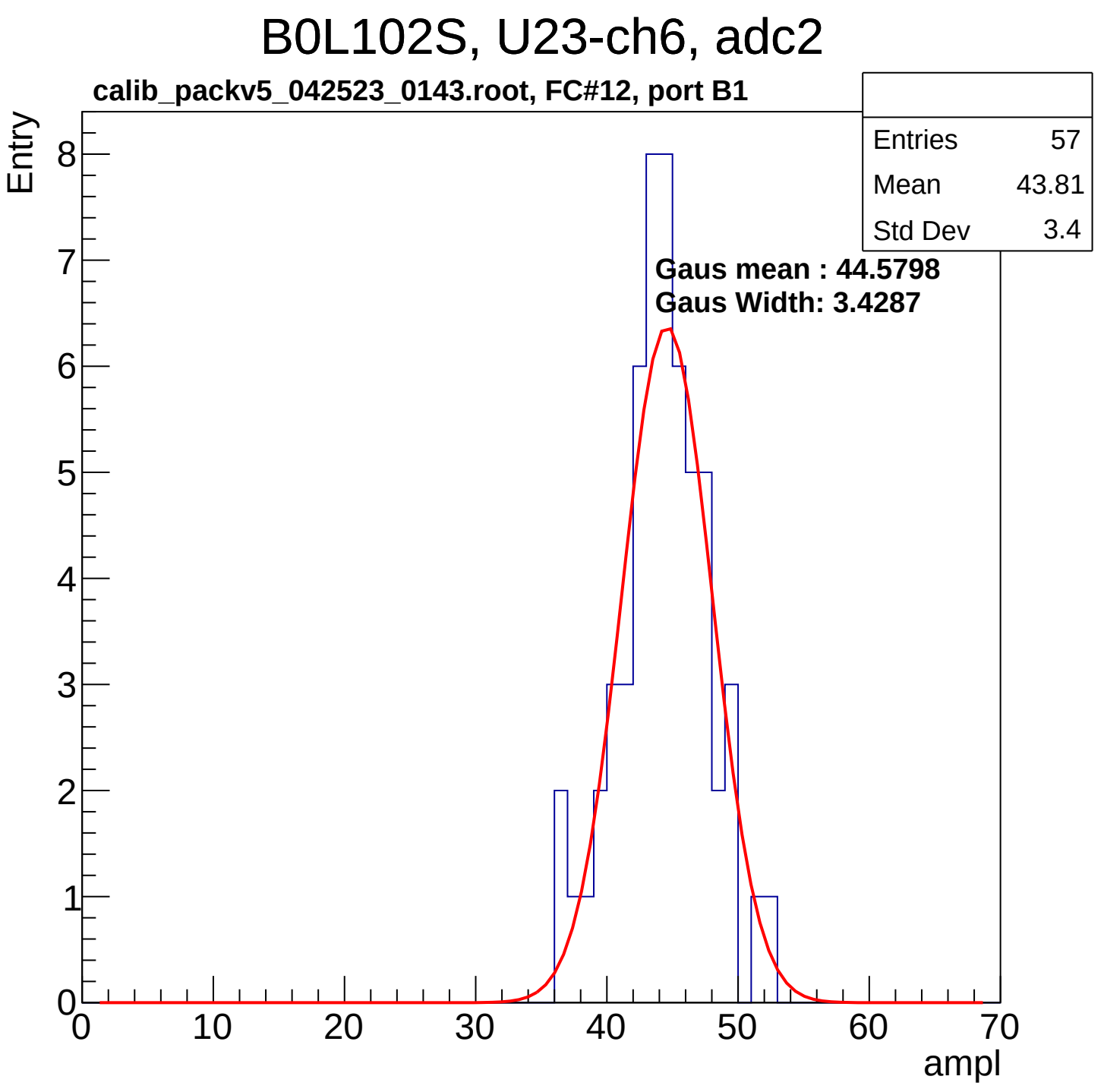
20
30
40
50
60
70


B0L102S, U23-ch6, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.81
Std Dev 3.4
Gaus mean : 44.5798
Gaus Width: 3.4287
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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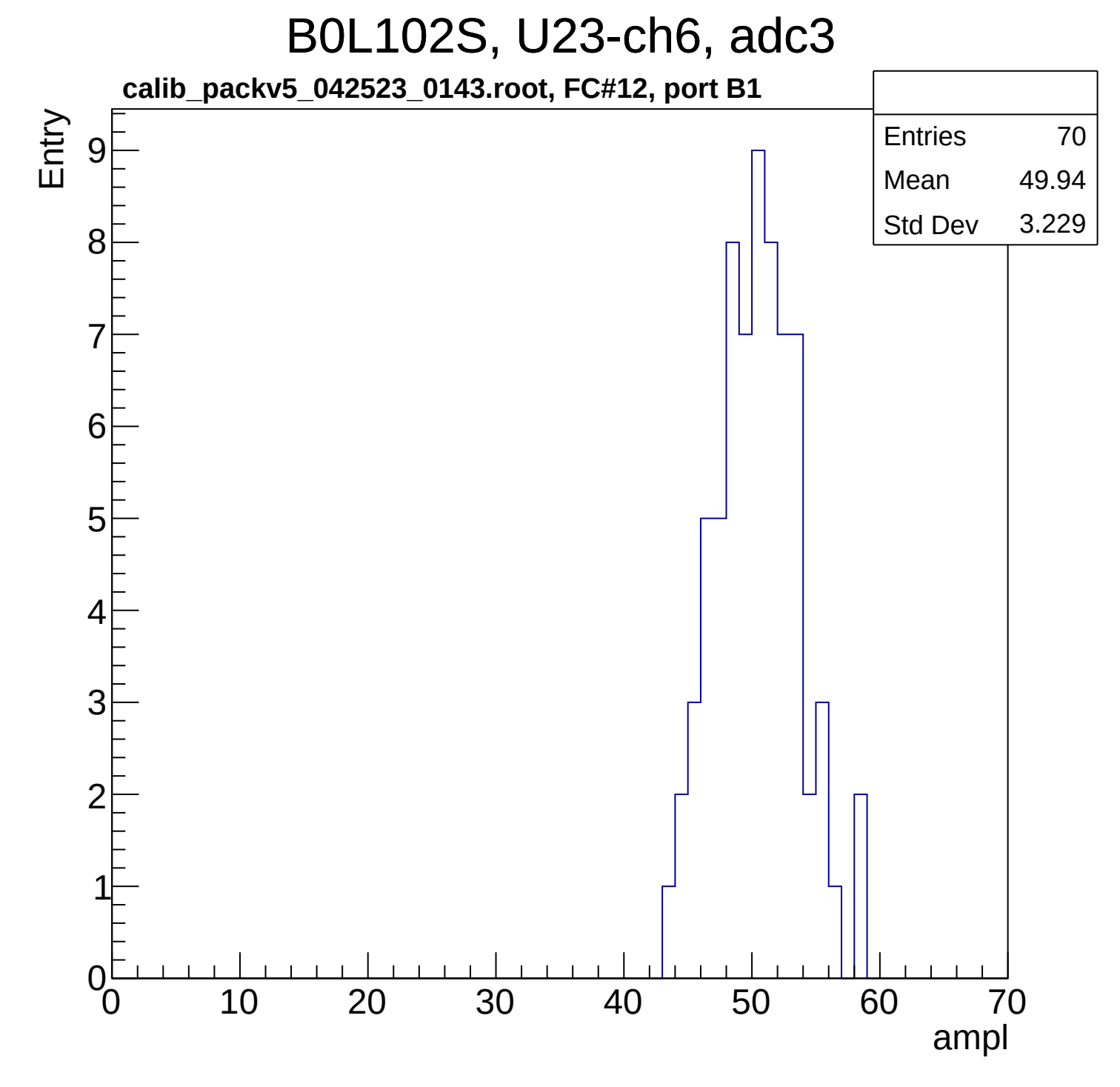
B0L102S, U23-ch6, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 49.94
Std Dev 3.229
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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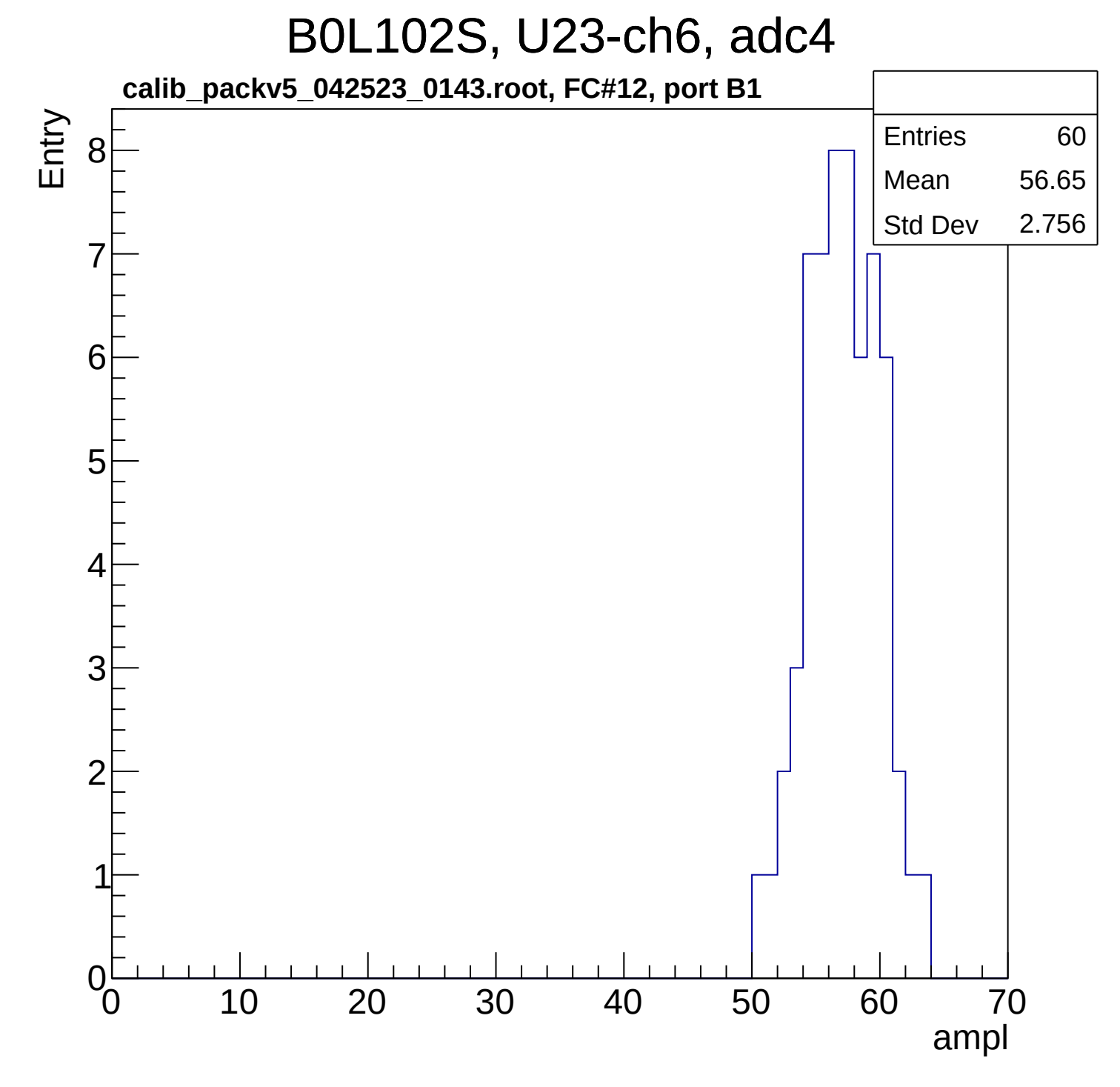
B0L102S, U23-ch6, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.65
Std Dev 2.756
Entry
ampl
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4
5
6
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0
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40
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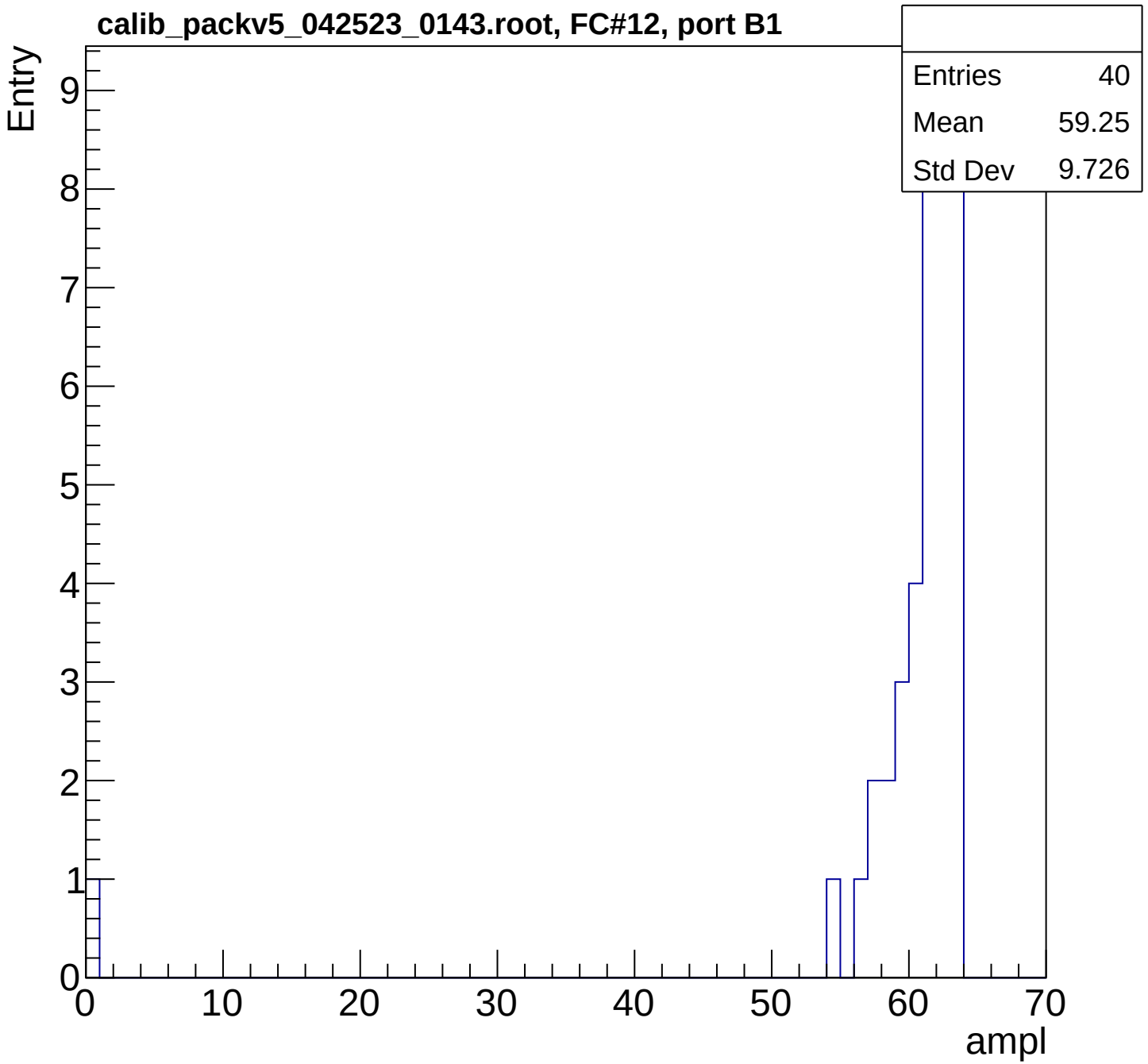
60
70


B0L102S, U23-ch6, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 59.25
Std Dev 9.726
Entry
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3
4
5
6
7
8
9
0
10
20
30
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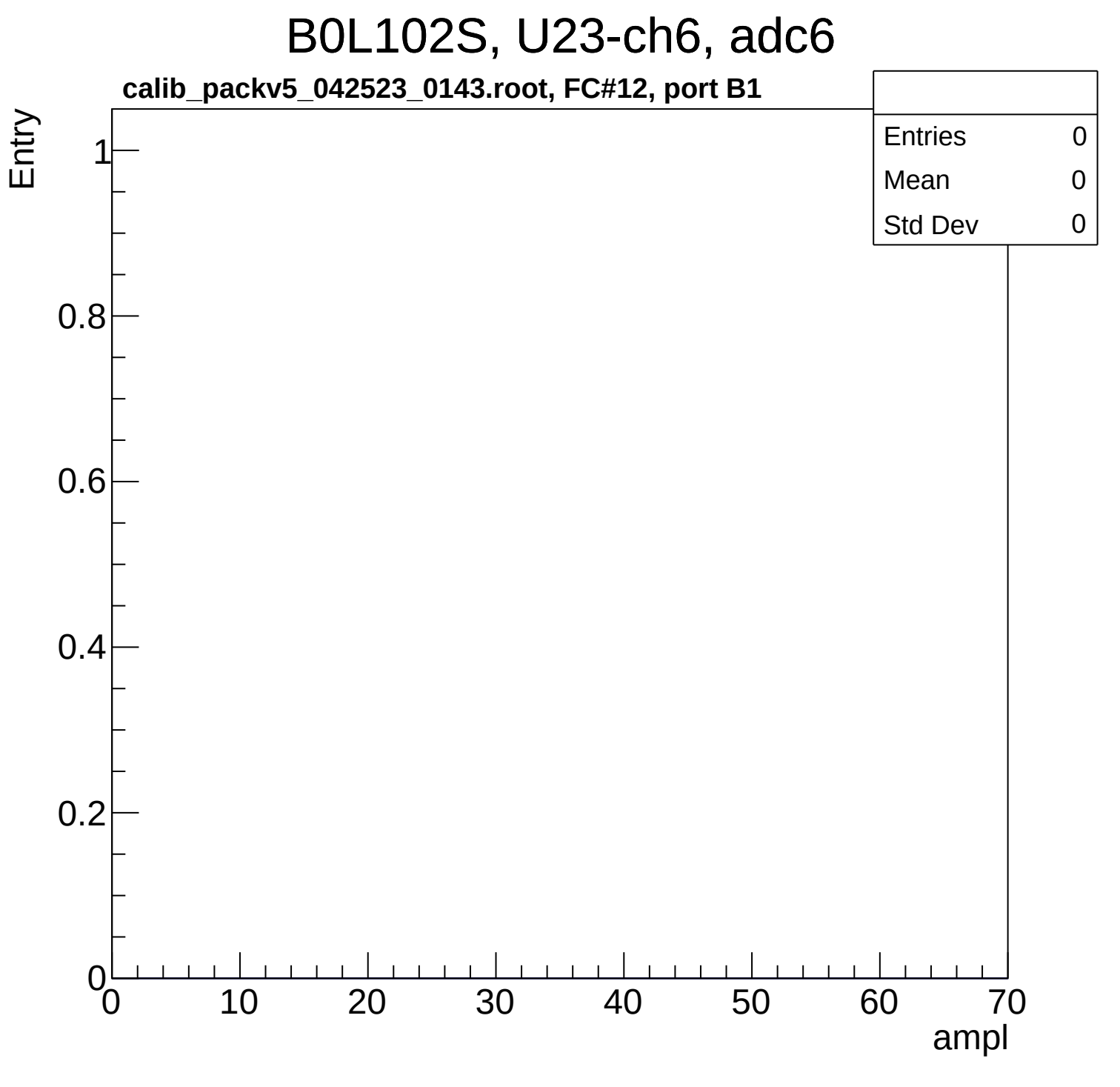
50
60
70
ampl


B0L102S, U23-ch6, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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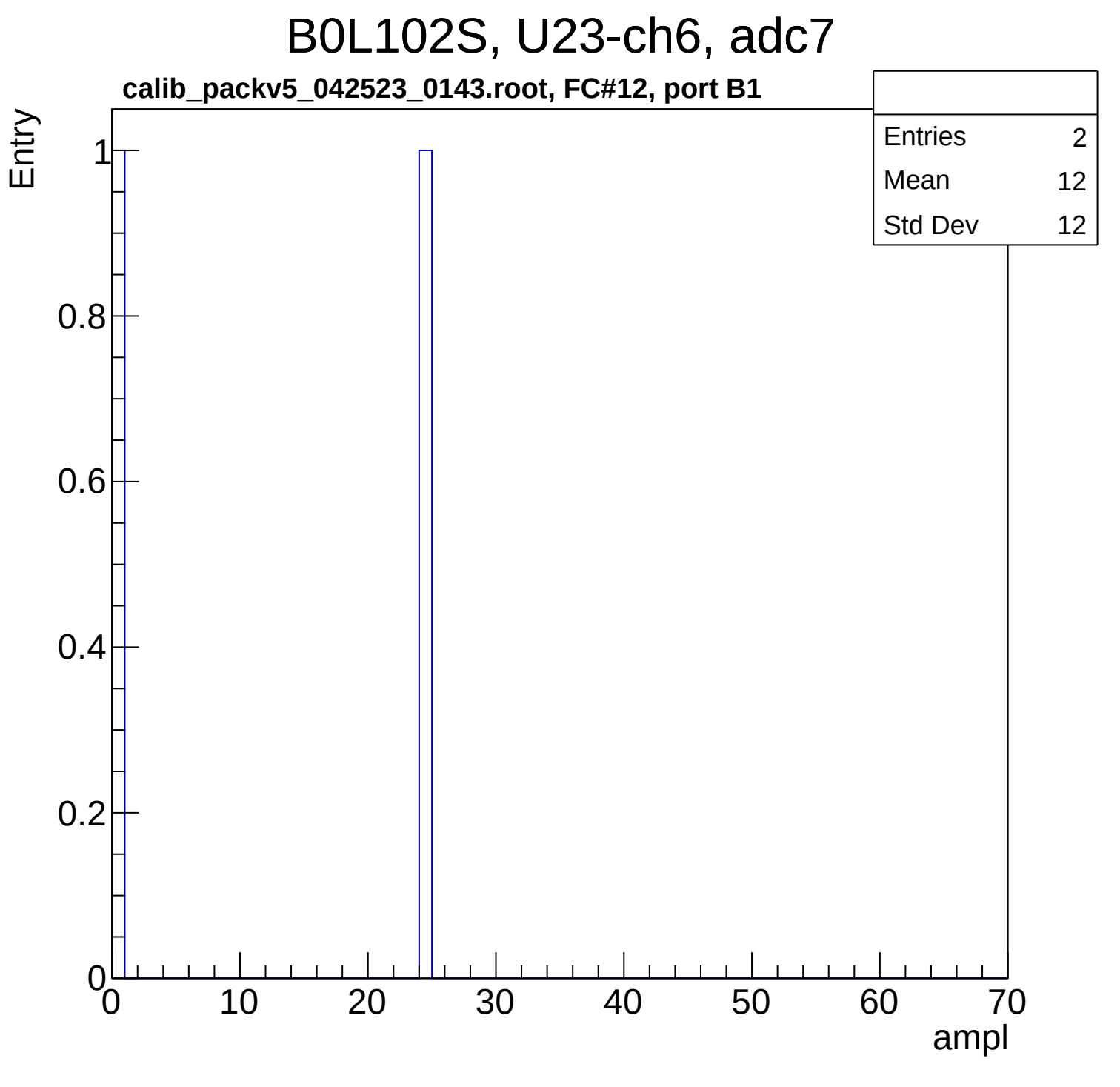
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch6, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
0.2
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0
10
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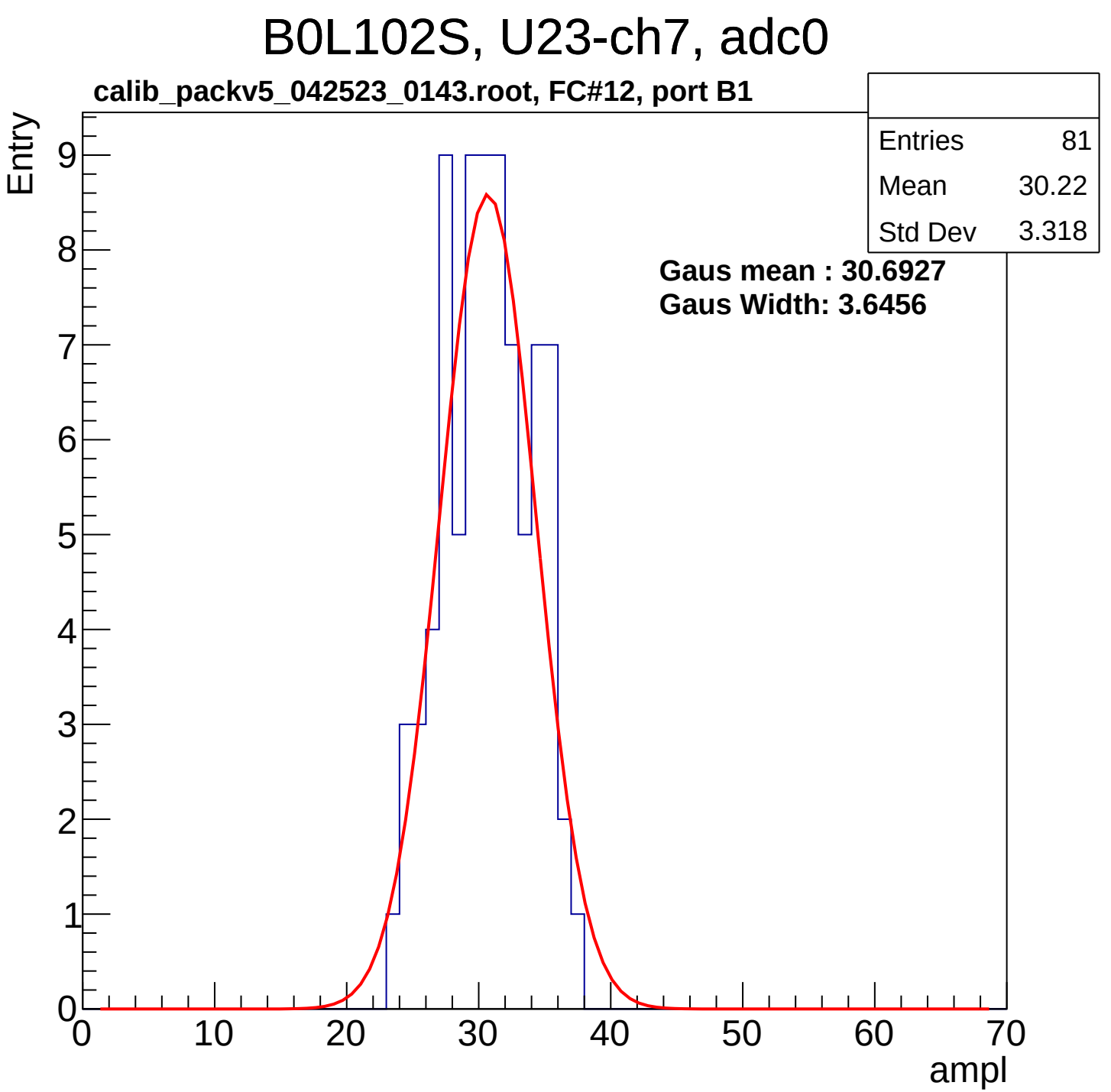
30
40
50
60
70
ampl


B0L102S, U23-ch7, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 30.22
Std Dev 3.318
Gaus mean : 30.6927
Gaus Width: 3.6456
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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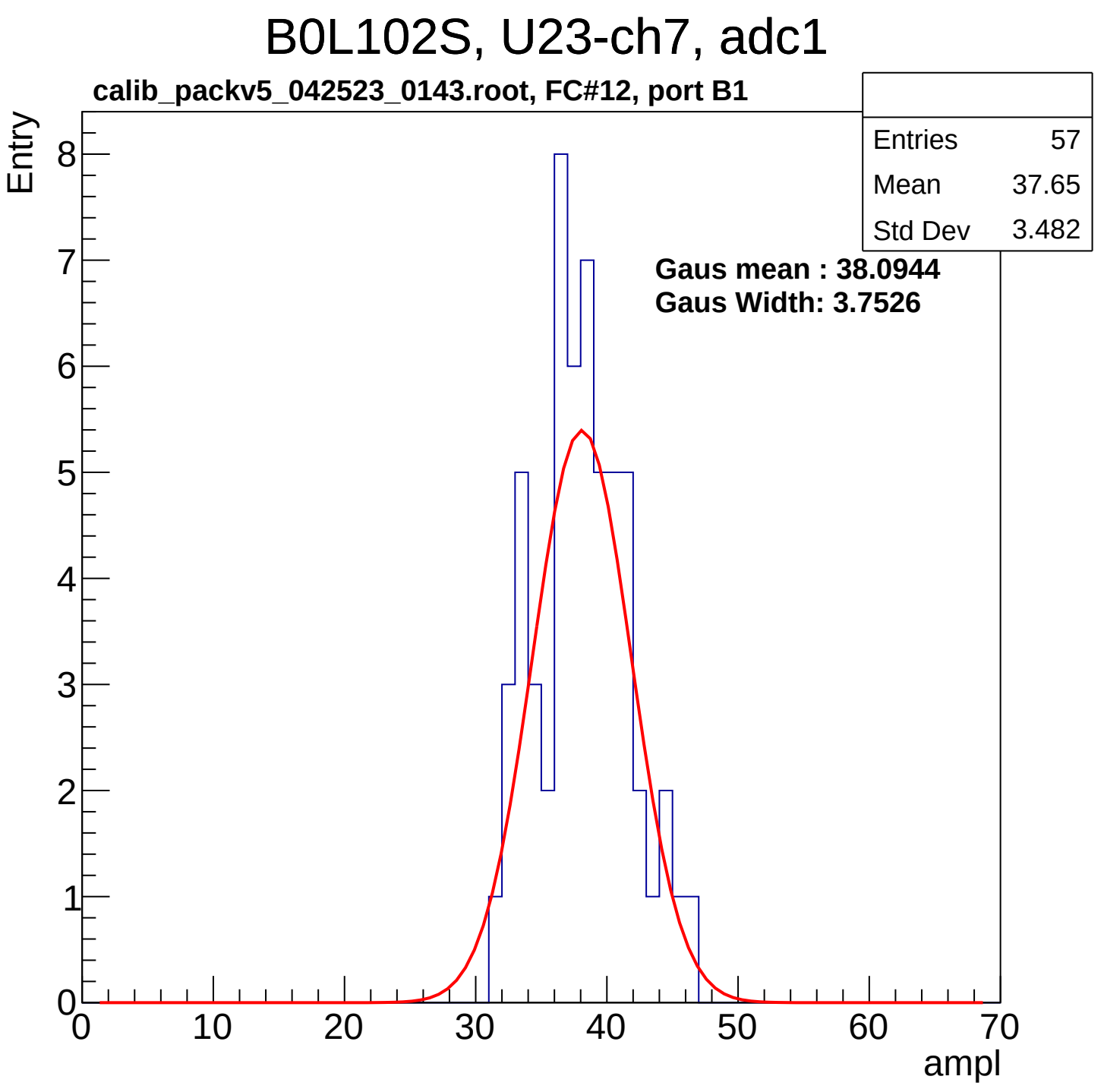
B0L102S, U23-ch7, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 37.65
Std Dev 3.482
Gaus mean : 38.0944
Gaus Width: 3.7526
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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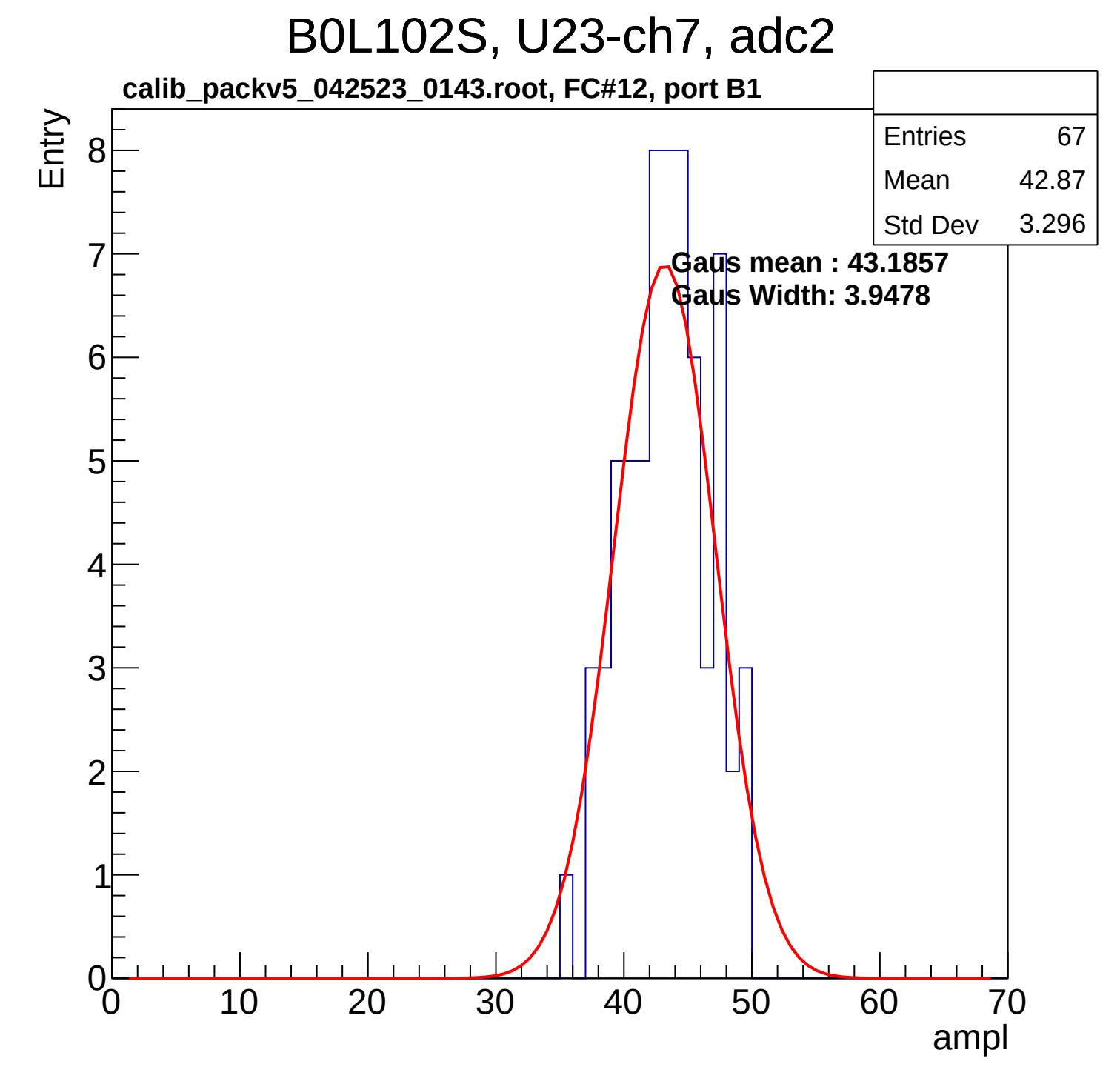
B0L102S, U23-ch7, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 42.87
Std Dev 3.296
Gaus mean : 43.1857
Gaus Width: 3.9478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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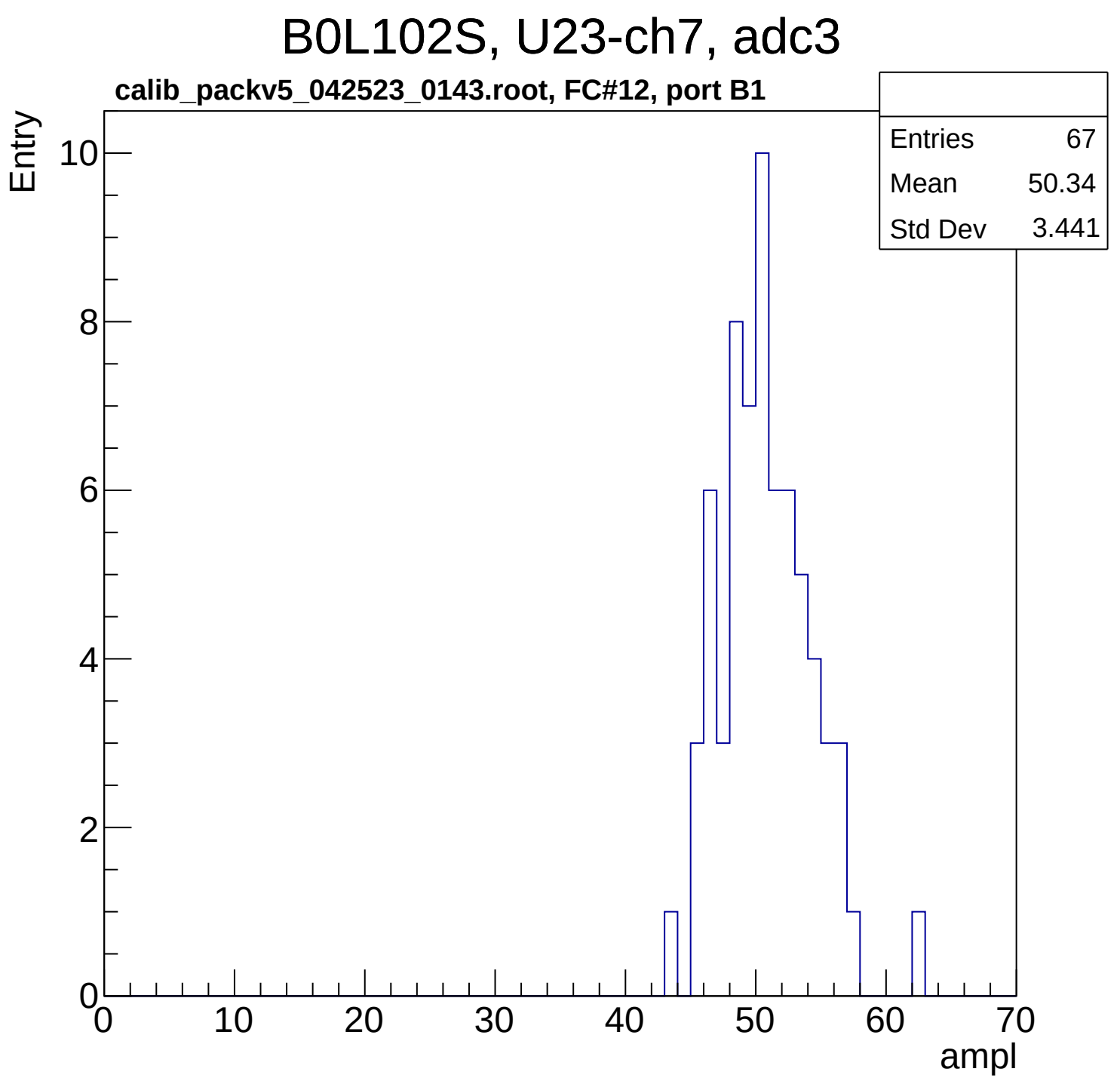
B0L102S, U23-ch7, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.34
Std Dev 3.441
Entry
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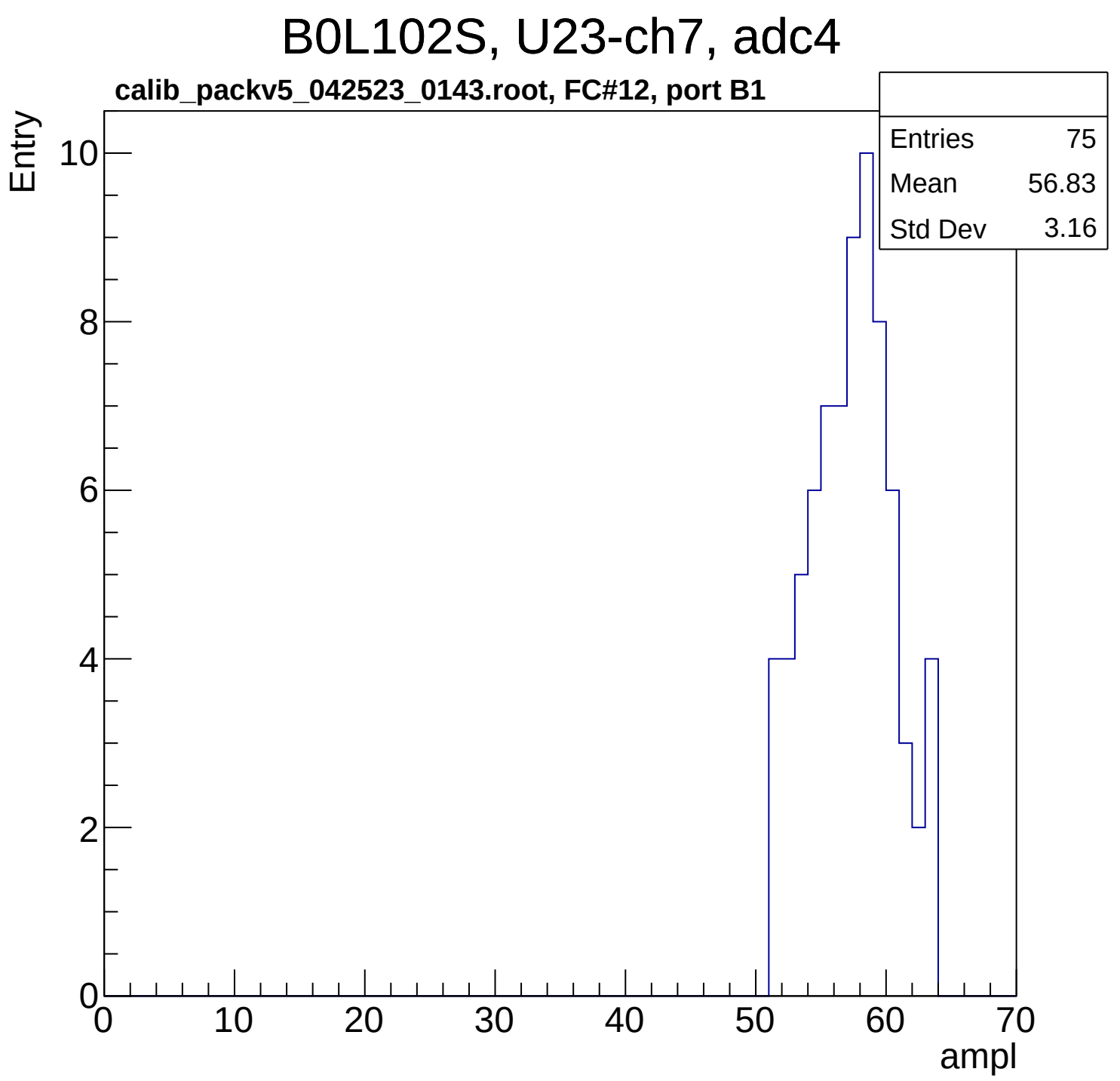
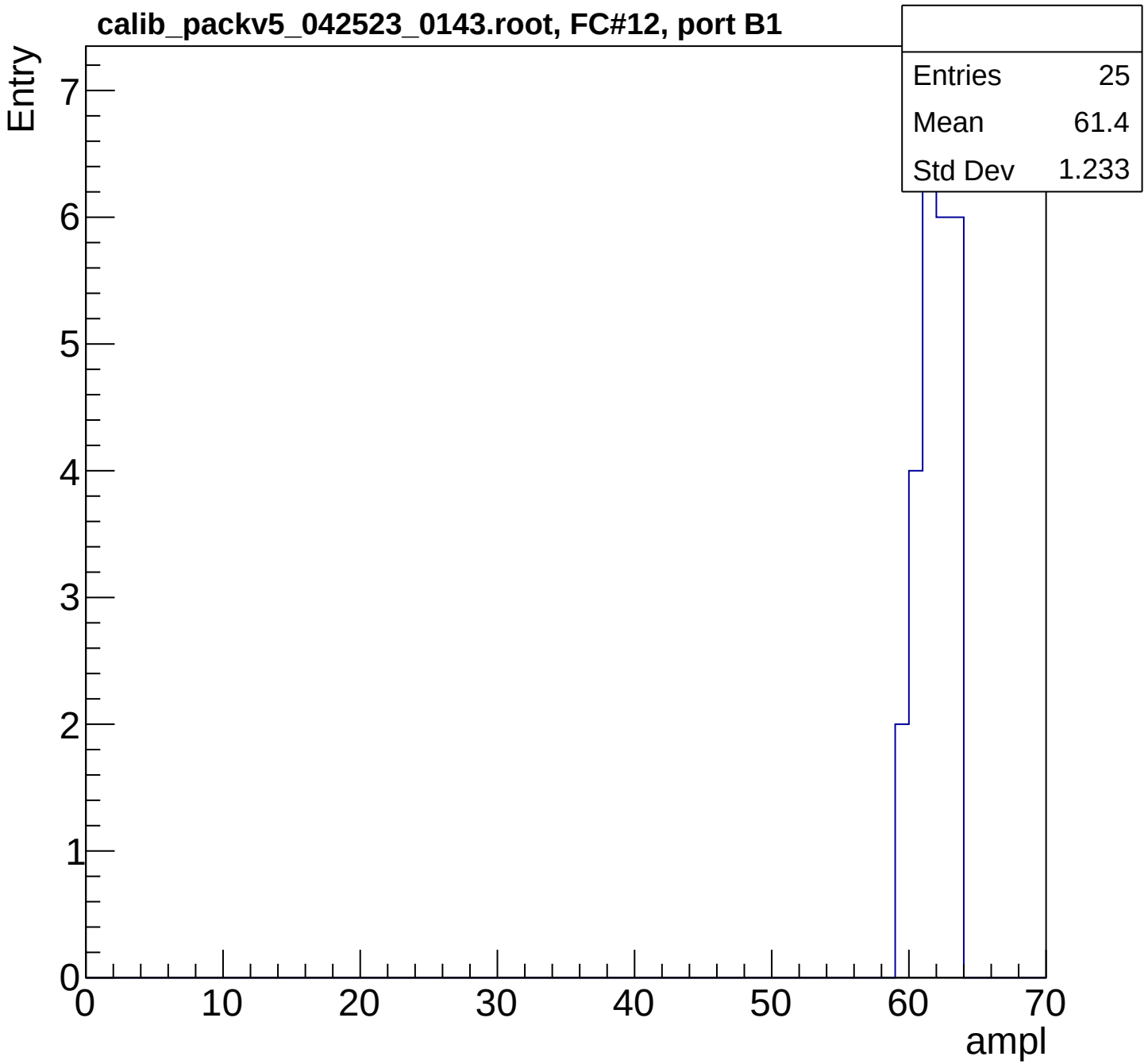
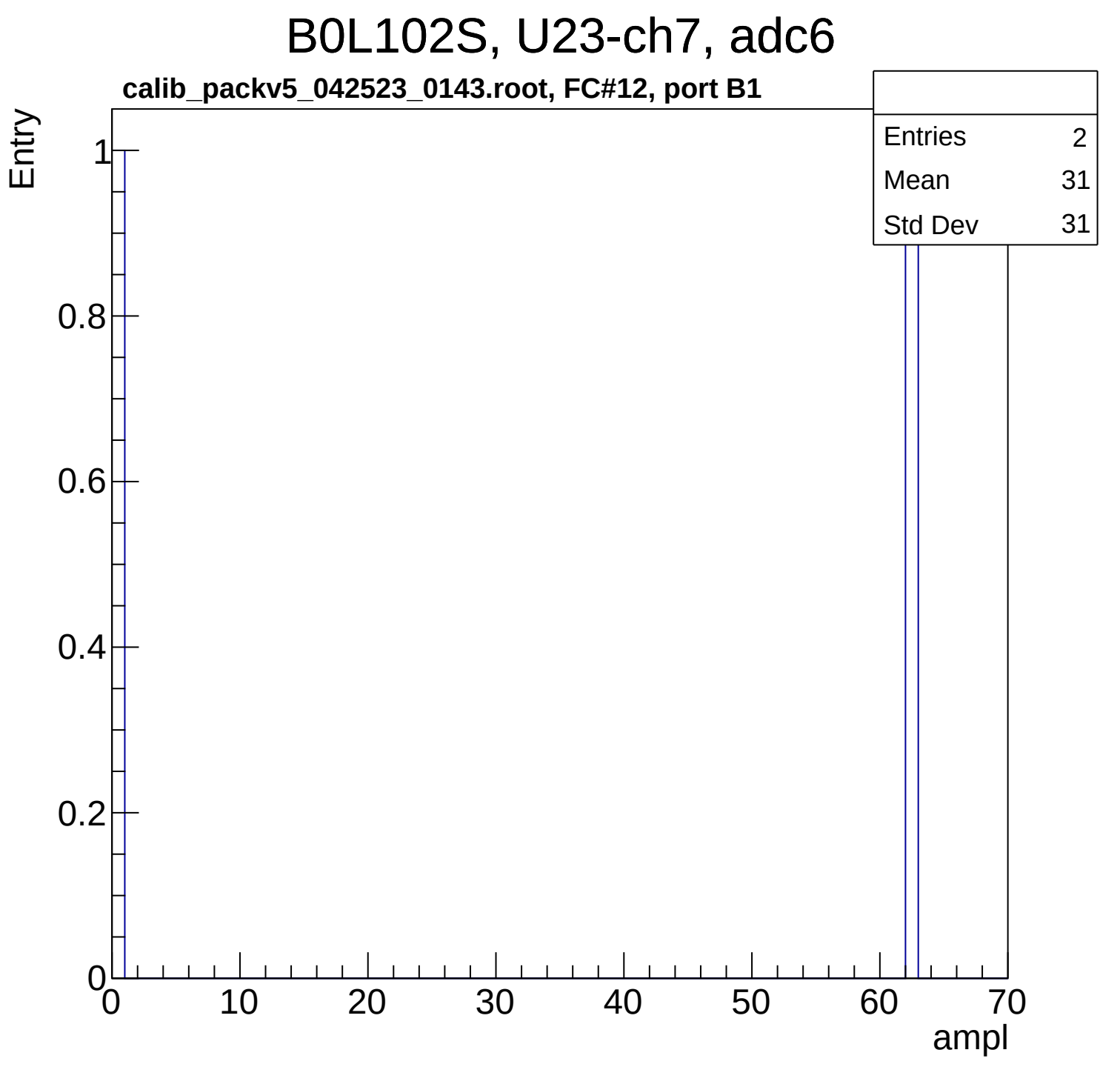
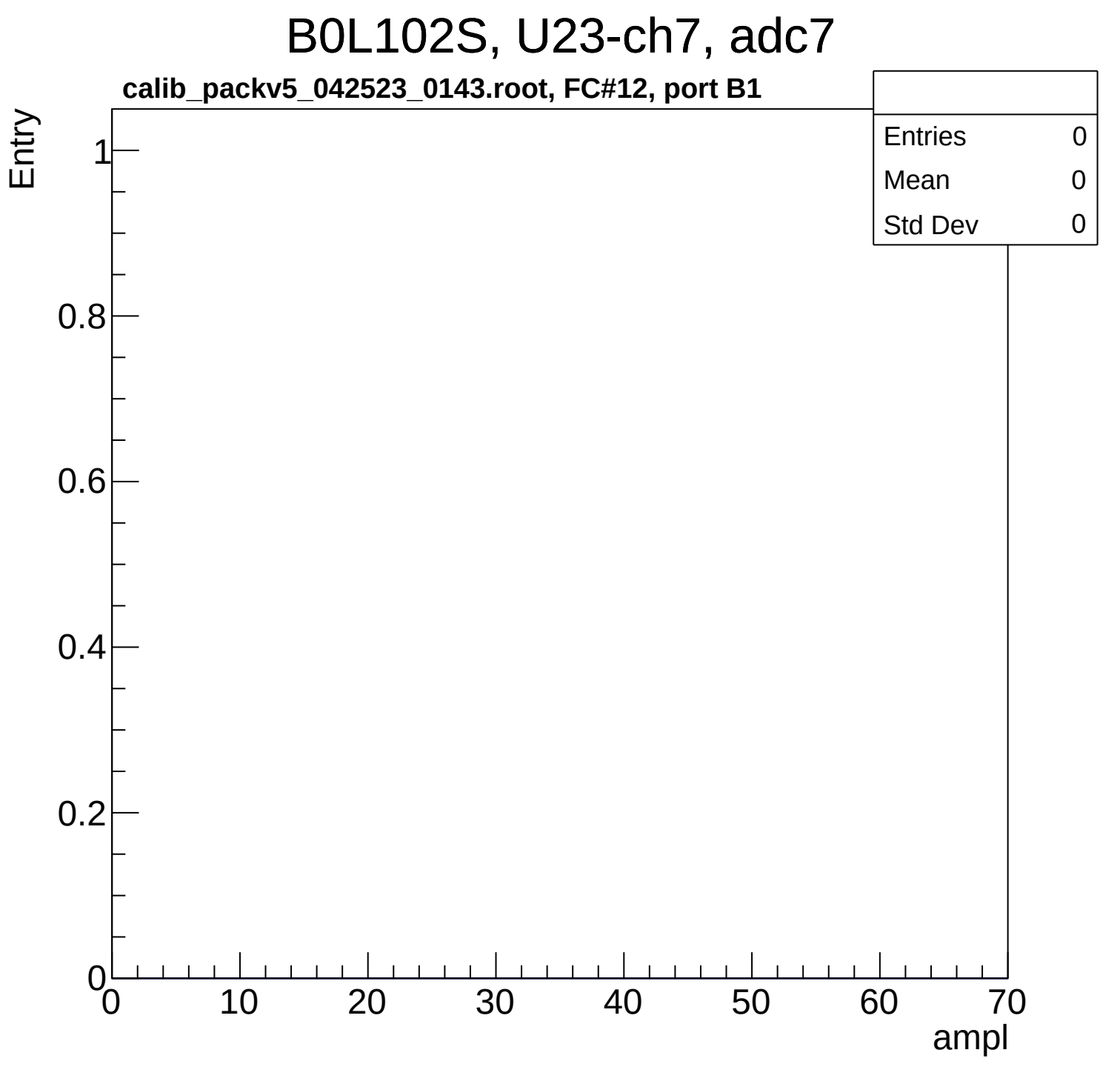
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch7, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 56.83
Std Dev 3.16
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch7, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 25
Mean 61.4
Std Dev 1.233
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch7, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch7, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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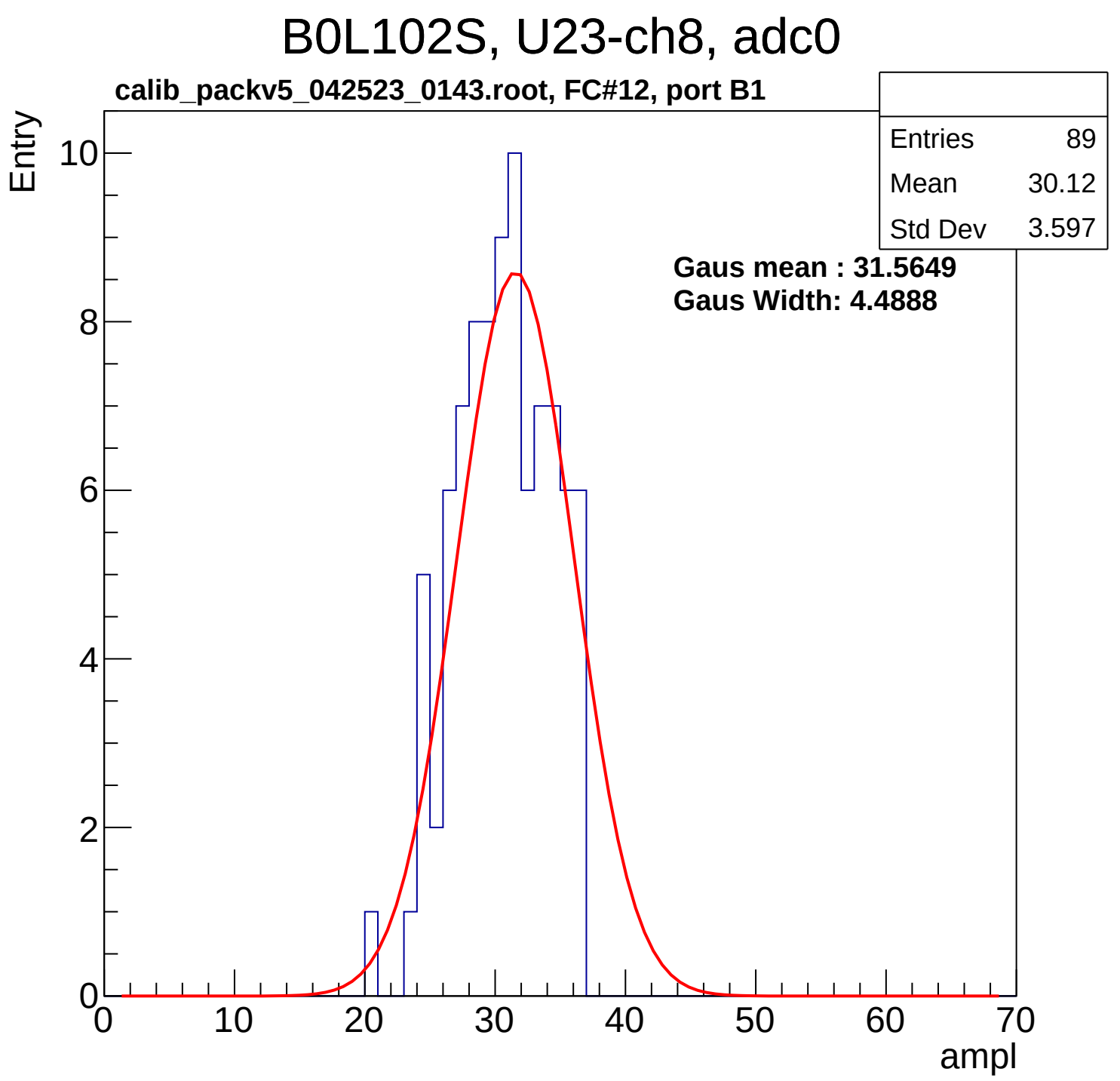
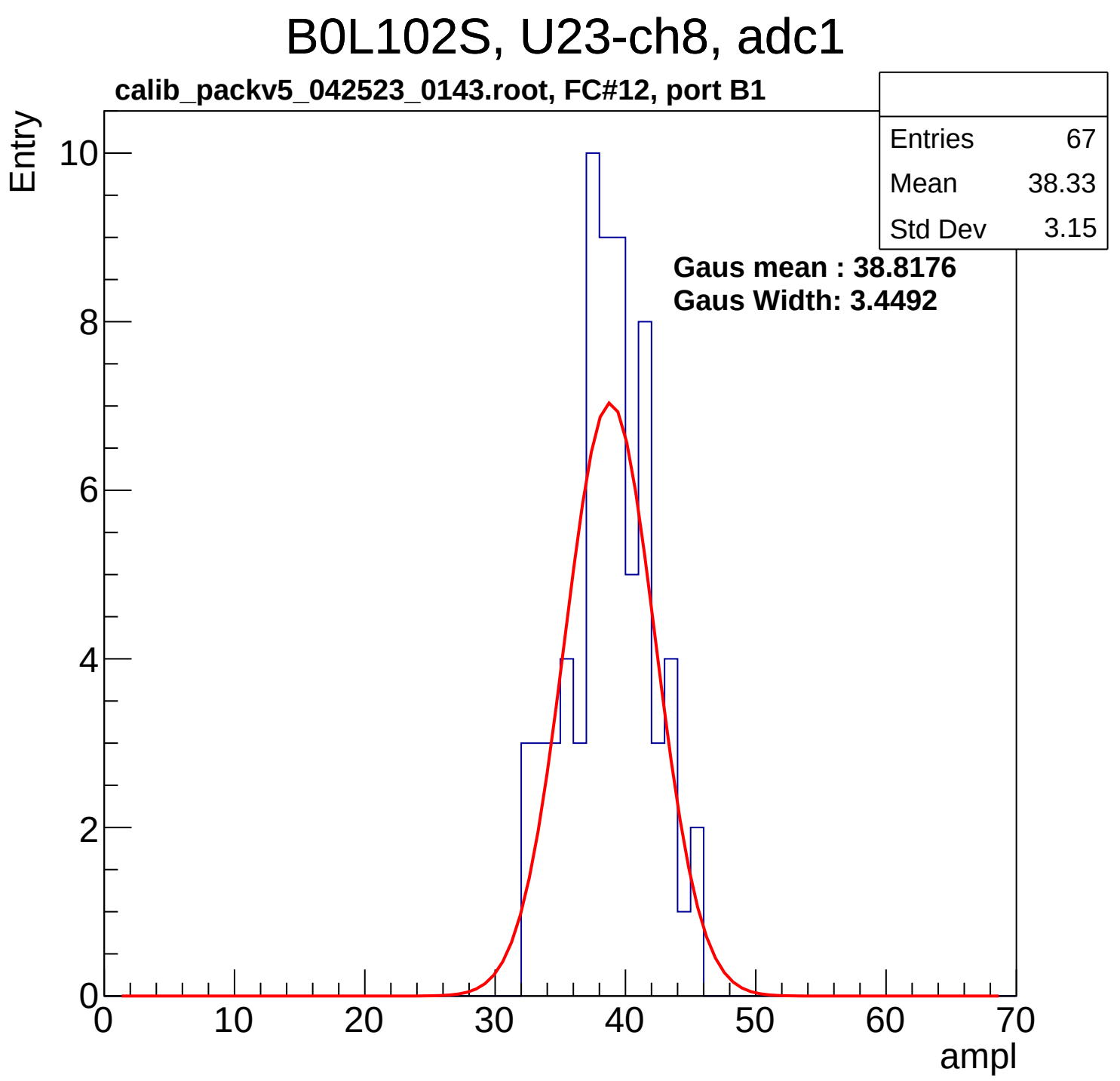
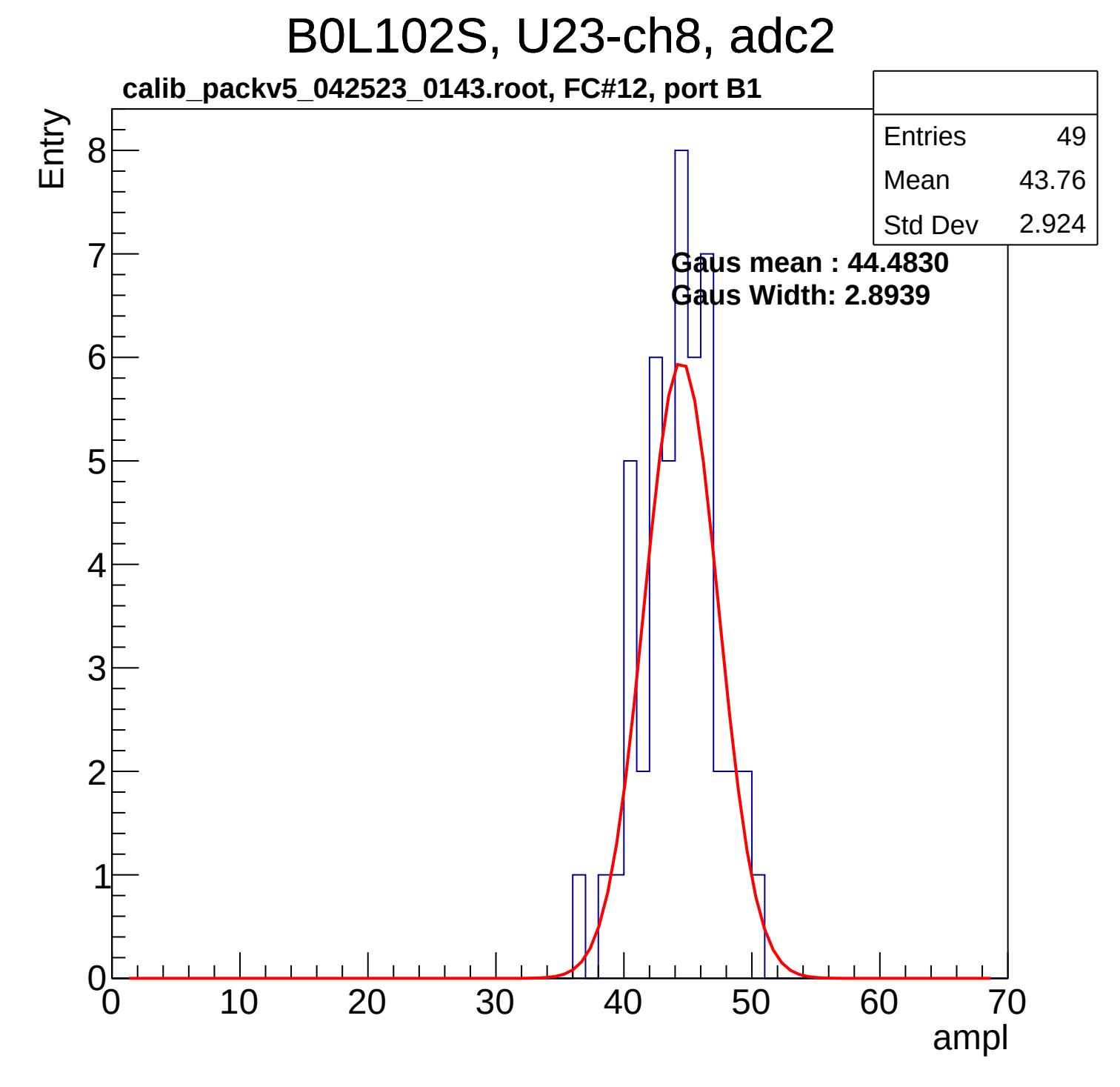
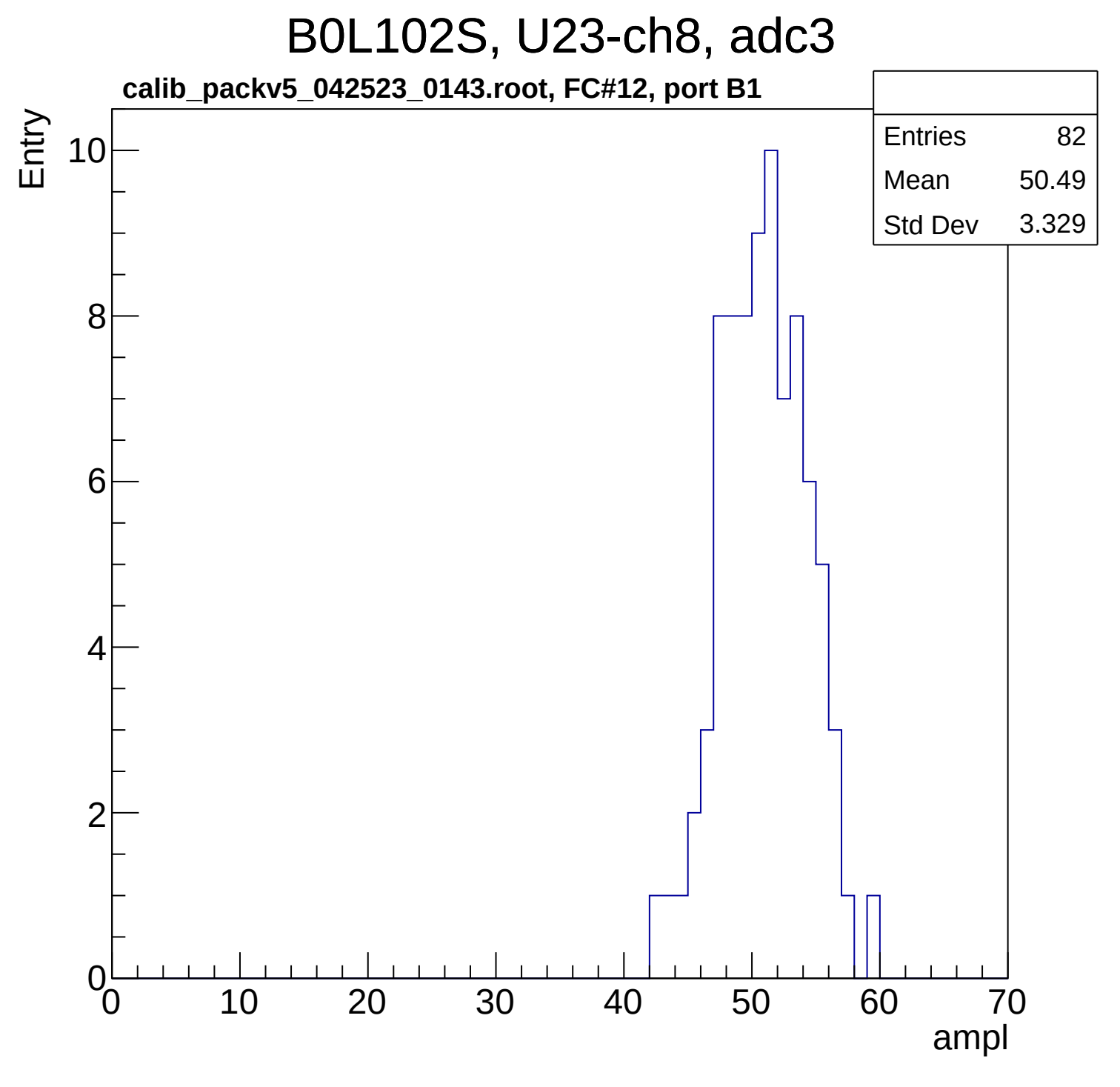
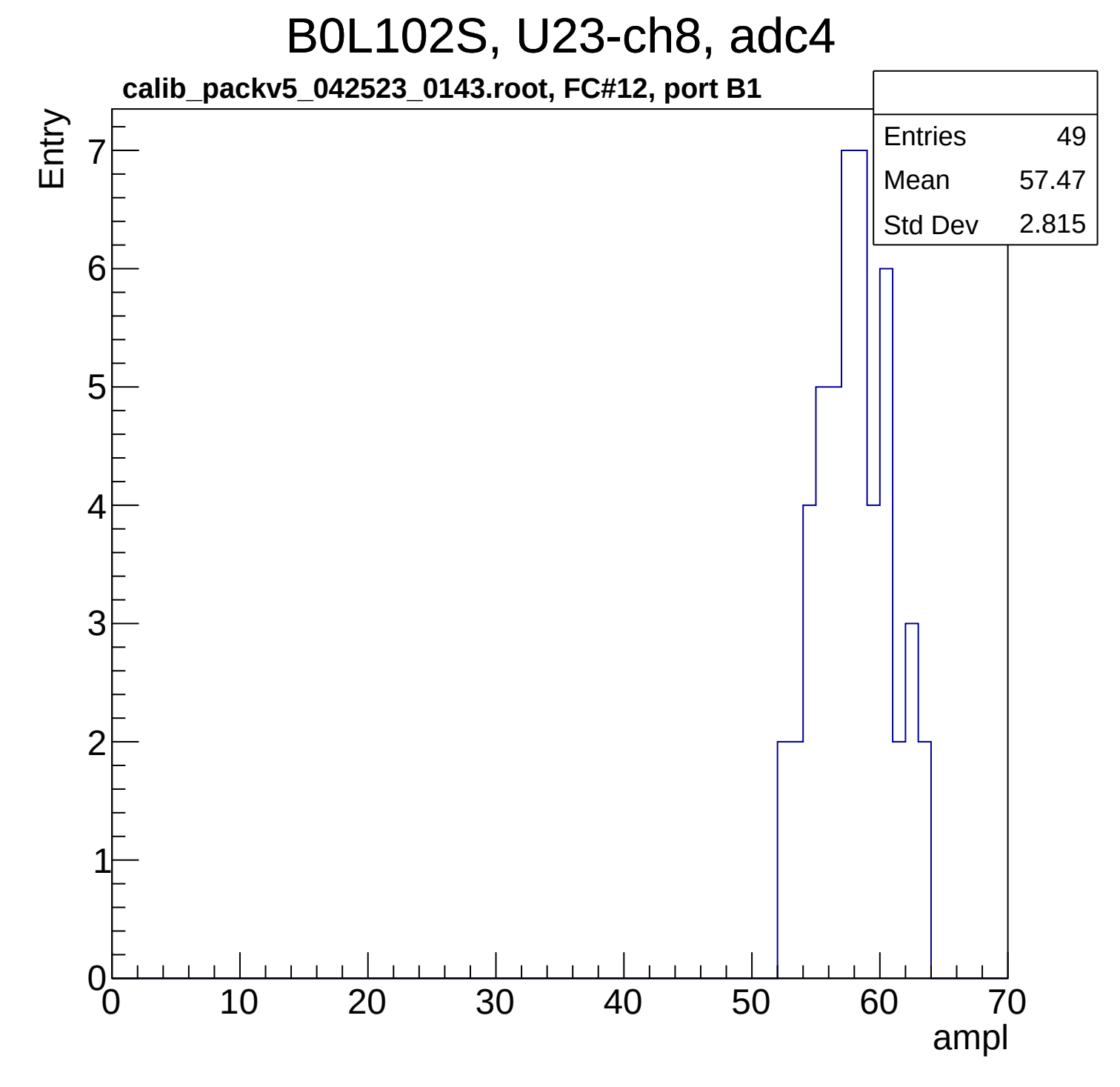
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

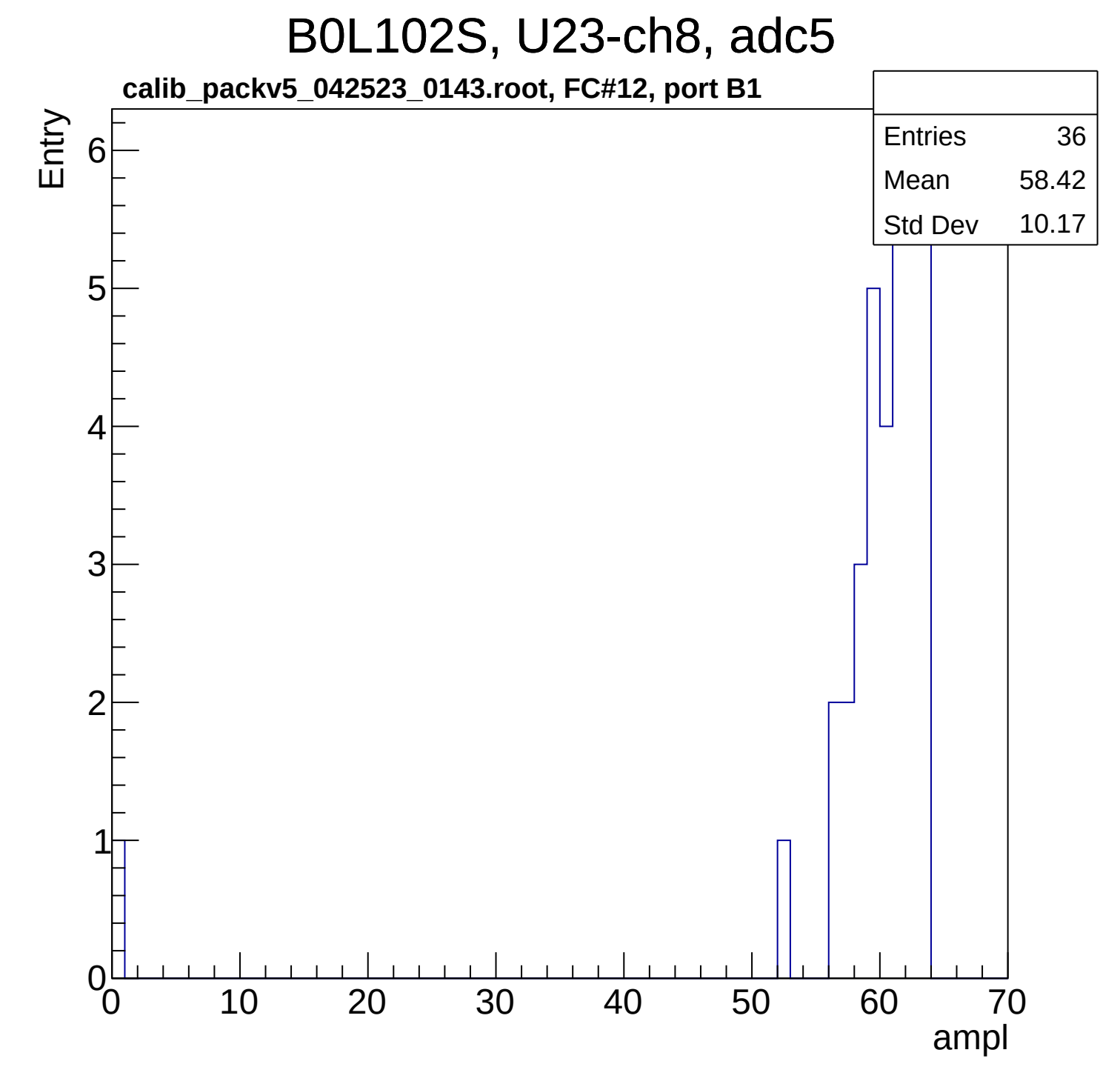

B0L102S, U23-ch8, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 30.12
Std Dev 3.597
Gaus mean : 31.5649
Gaus Width: 4.4888
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

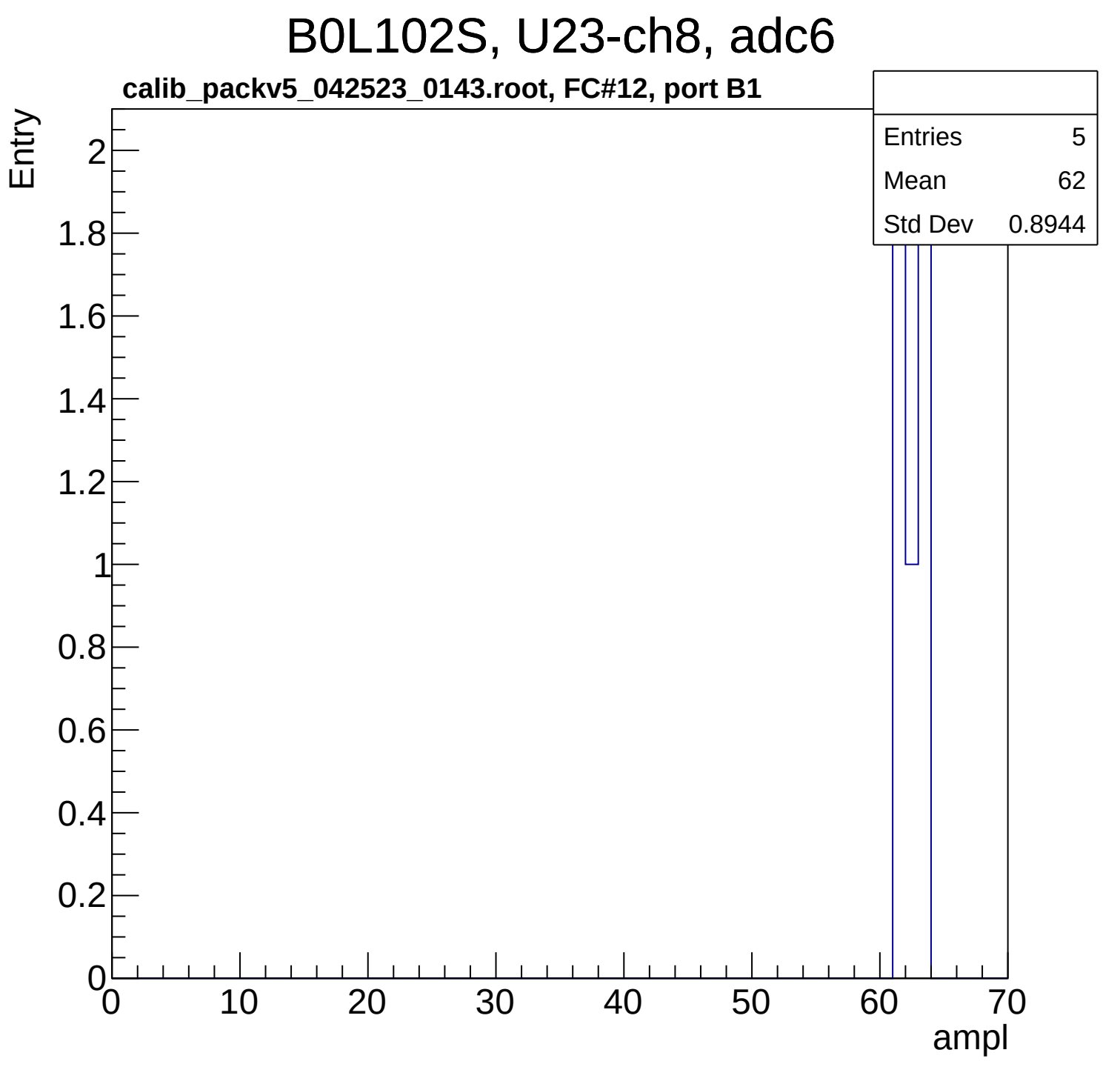

B0L102S, U23-ch8, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 38.33
Std Dev 3.15
Gaus mean : 38.8176
Gaus Width: 3.4492
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

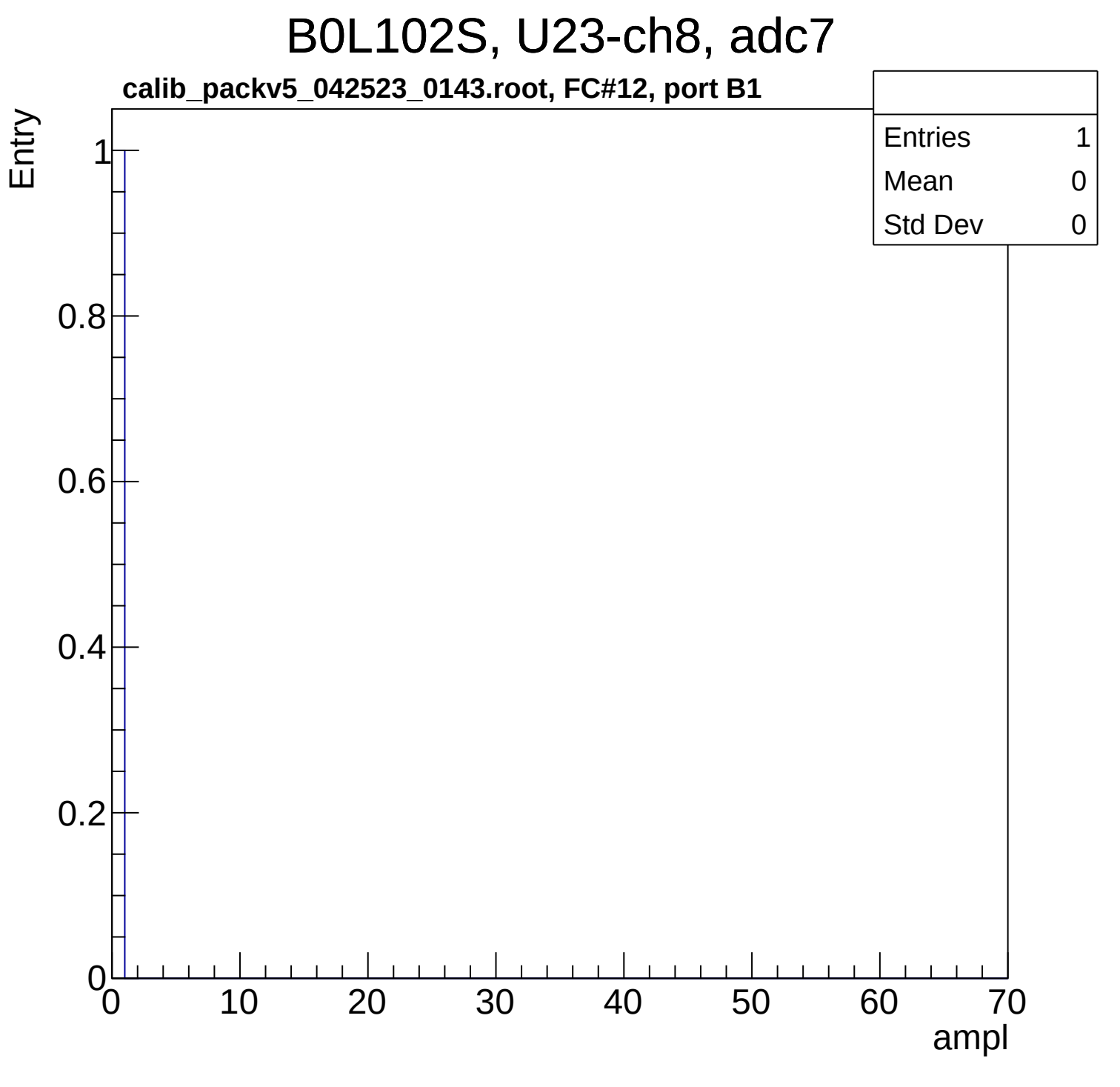

B0L102S, U23-ch8, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 43.76
Std Dev 2.924
Gaus mean : 44.4830
Gaus Width: 2.8939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

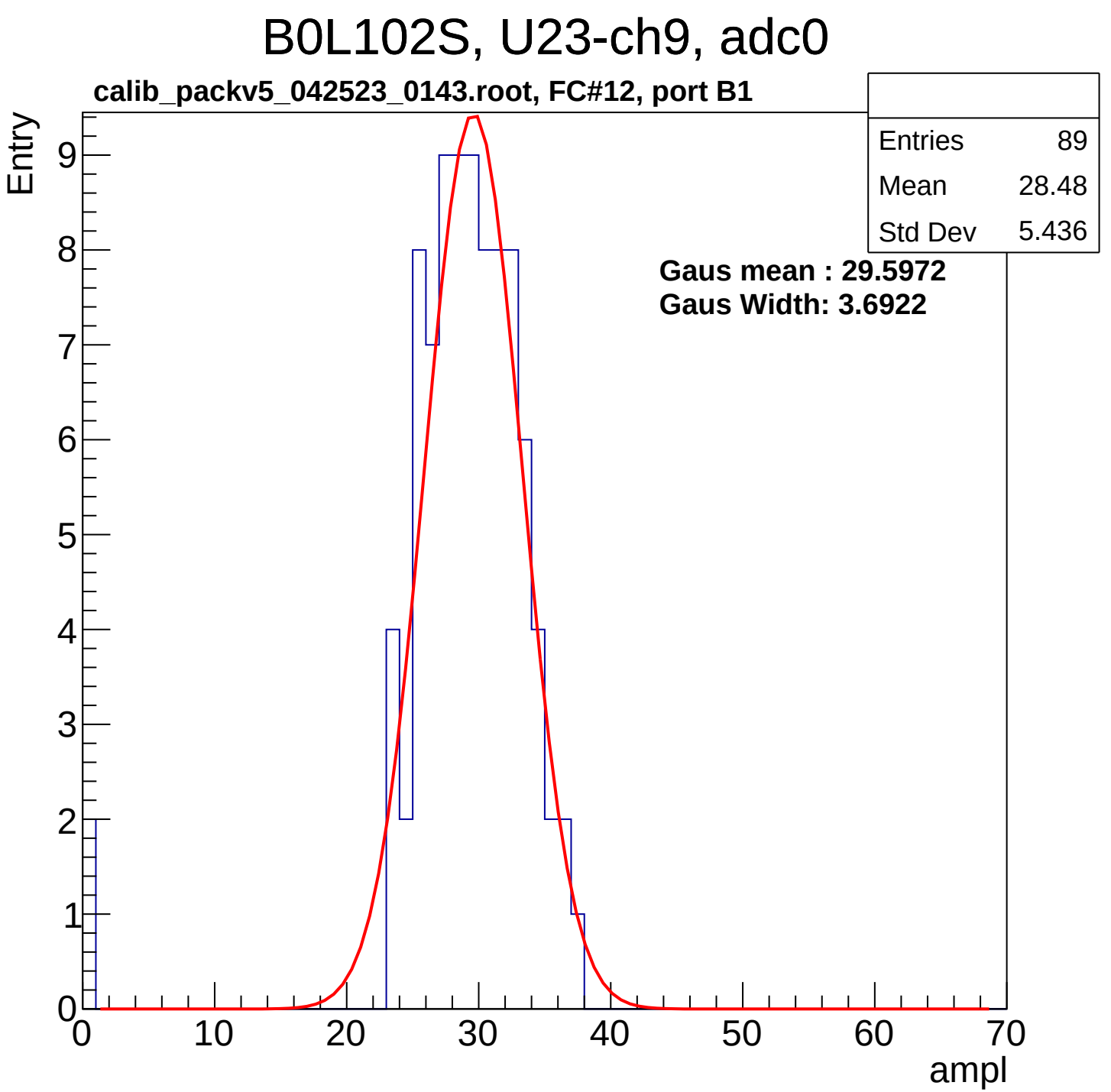

B0L102S, U23-ch8, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 50.49
Std Dev 3.329
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

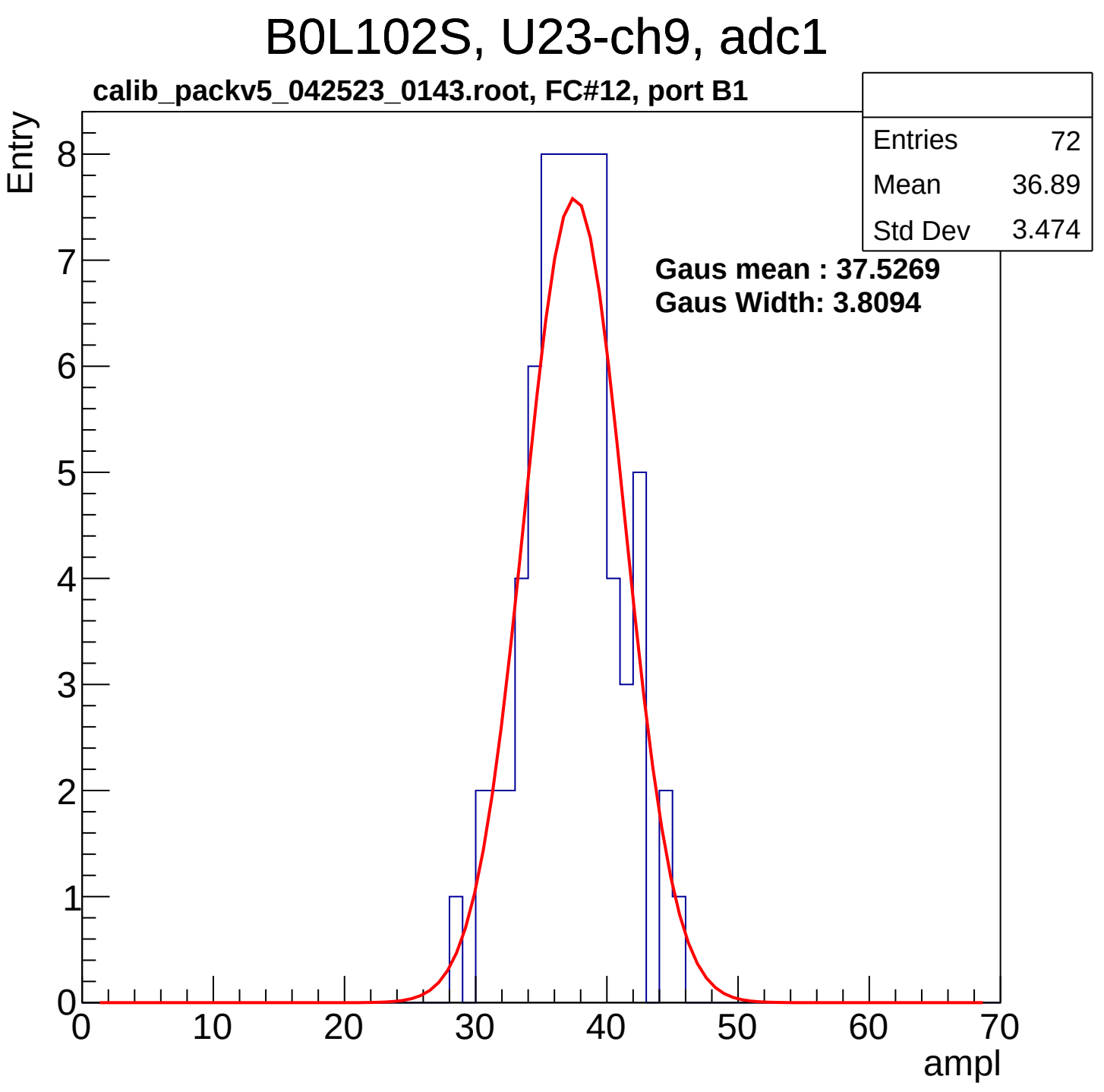

B0L102S, U23-ch8, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 57.47
Std Dev 2.815
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

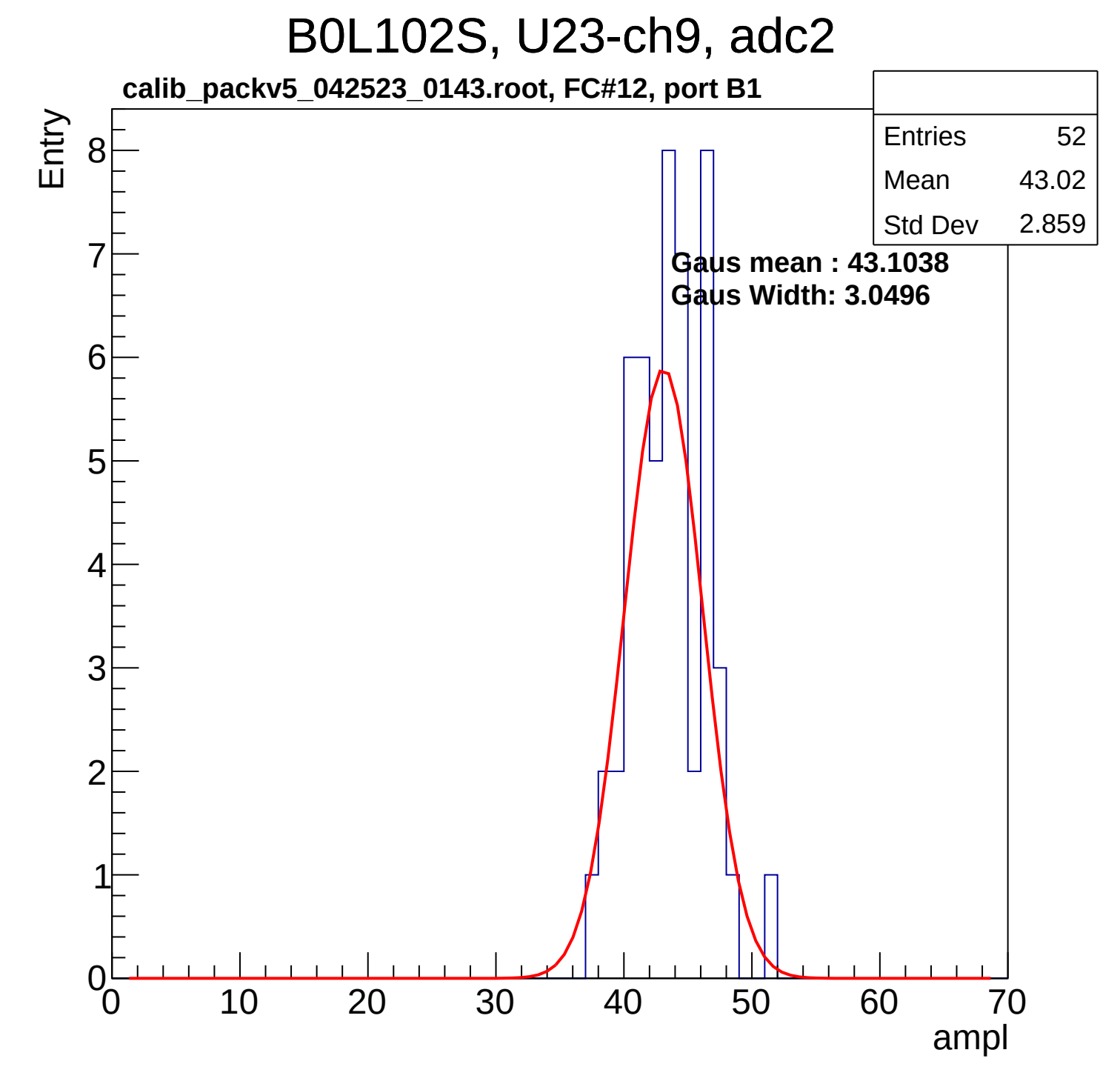

B0L102S, U23-ch8, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.42
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

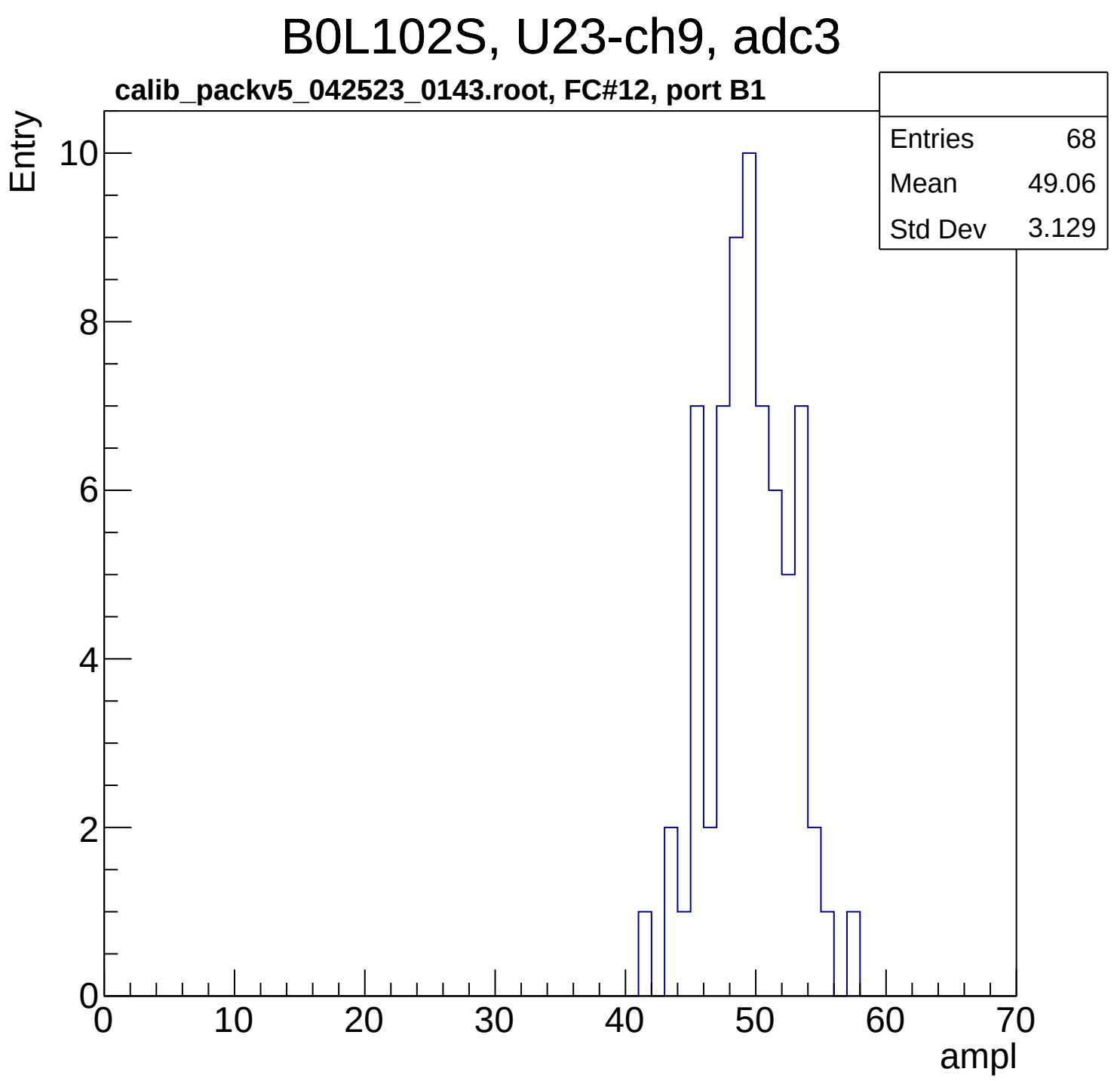

B0L102S, U23-ch8, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

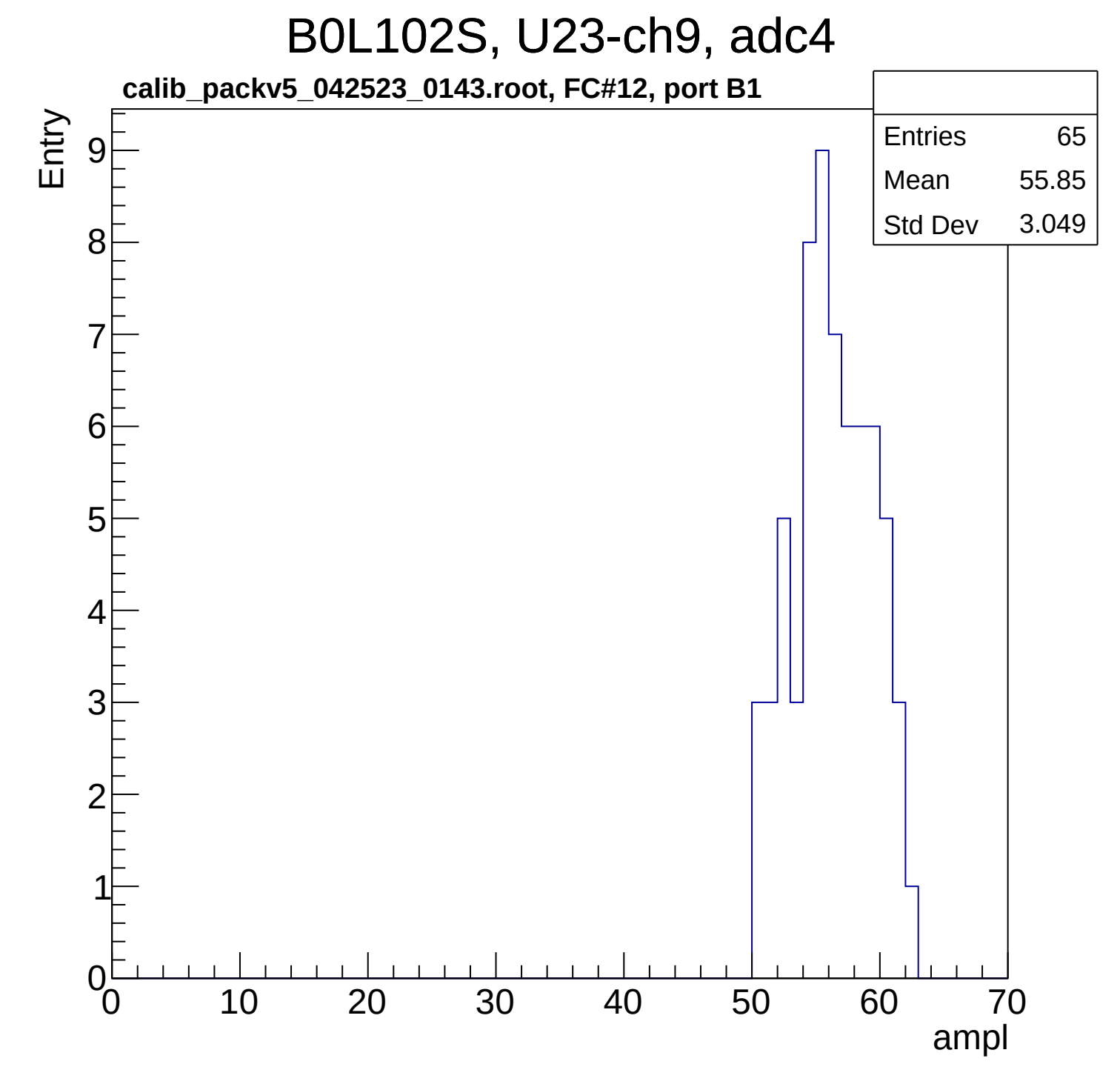

B0L102S, U23-ch8, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

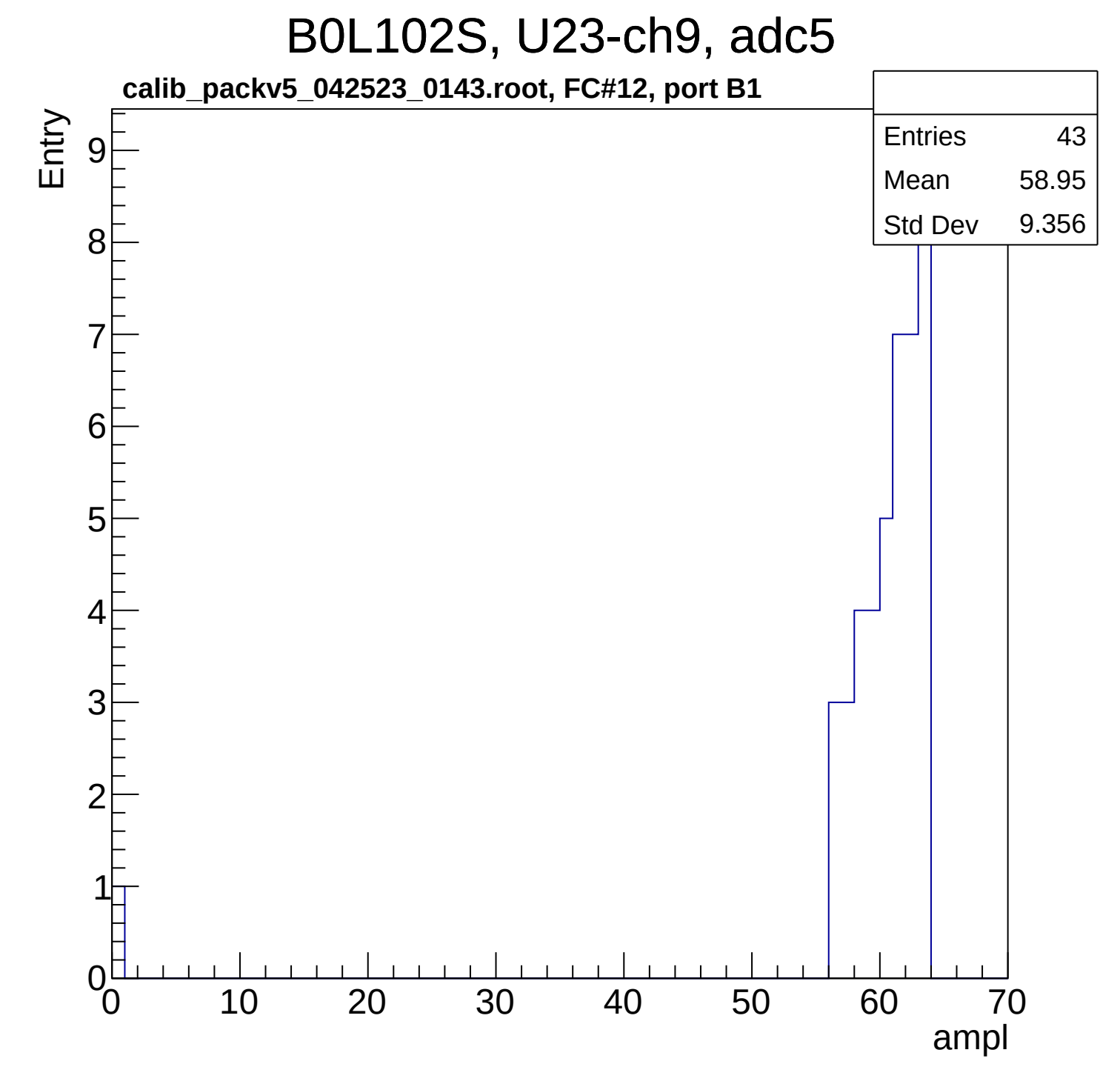

B0L102S, U23-ch9, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 28.48
Std Dev 5.436
Gaus mean : 29.5972
Gaus Width: 3.6922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch9, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 36.89
Std Dev 3.474
Gaus mean : 37.5269
Gaus Width: 3.8094
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch9, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 43.02
Std Dev 2.859
Gaus mean : 43.1038
Gaus Width: 3.0496
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch9, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.06
Std Dev 3.129
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch9, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 55.85
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch9, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.95
Std Dev 9.356
Entry
ampl
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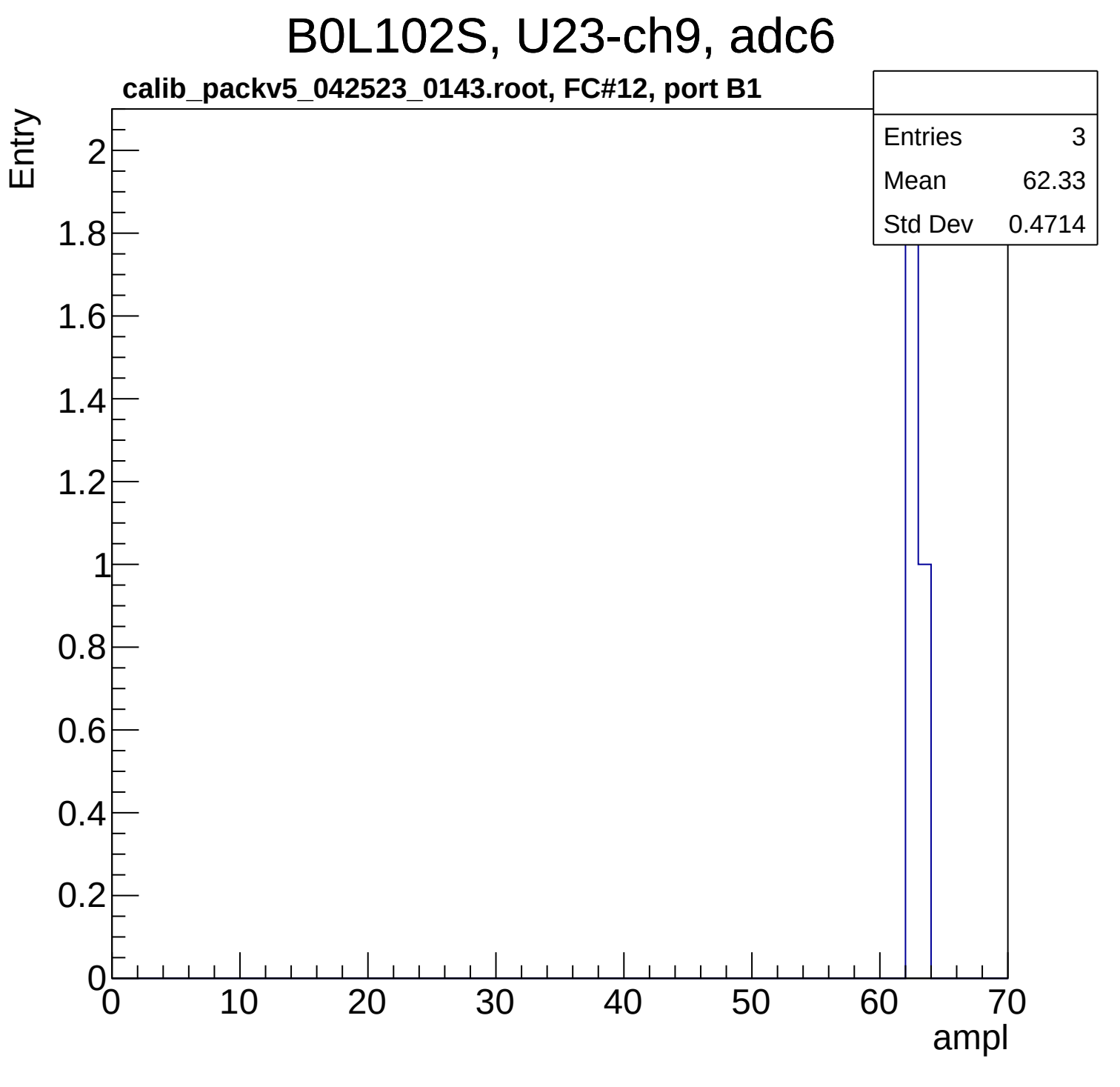
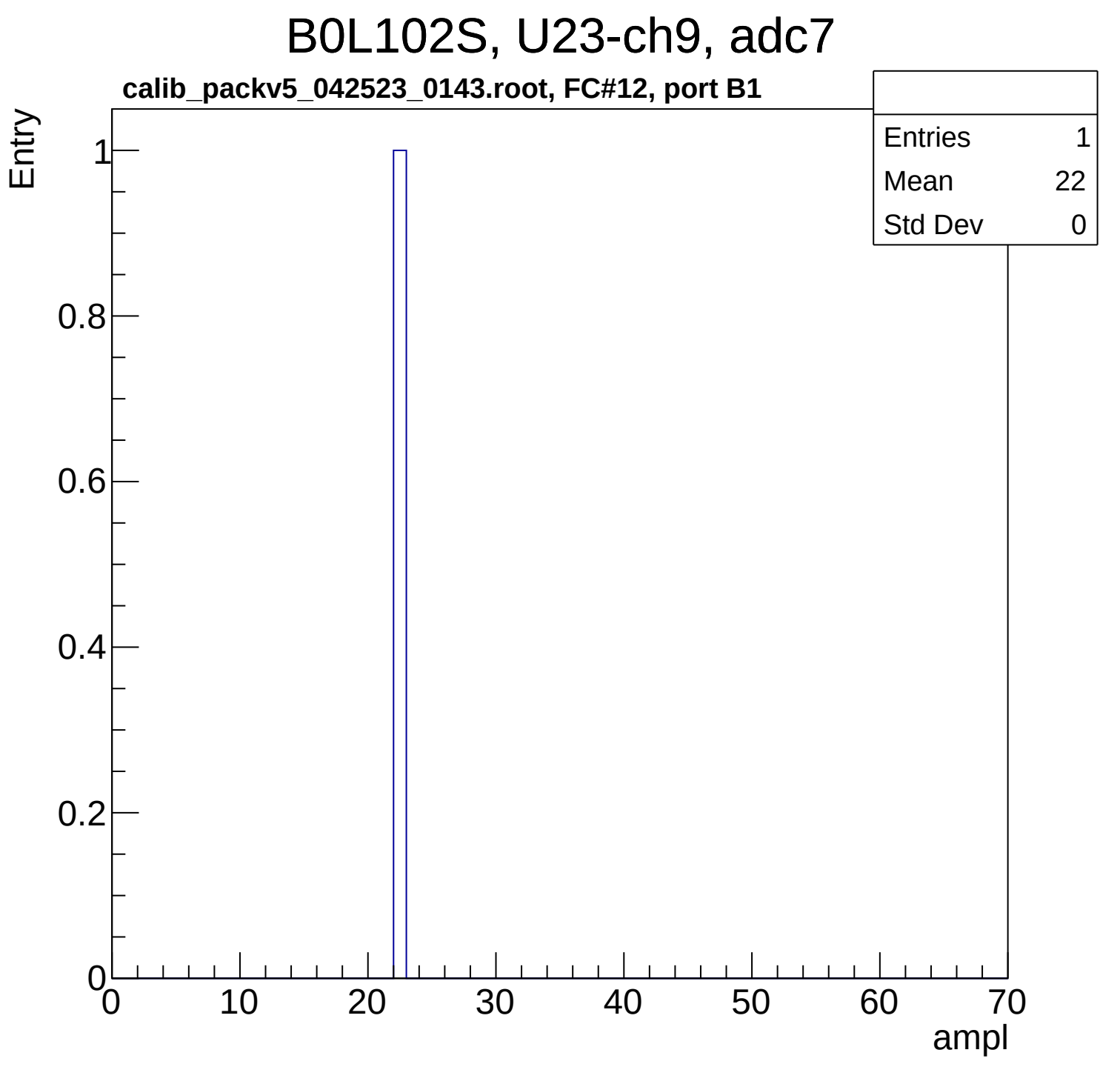
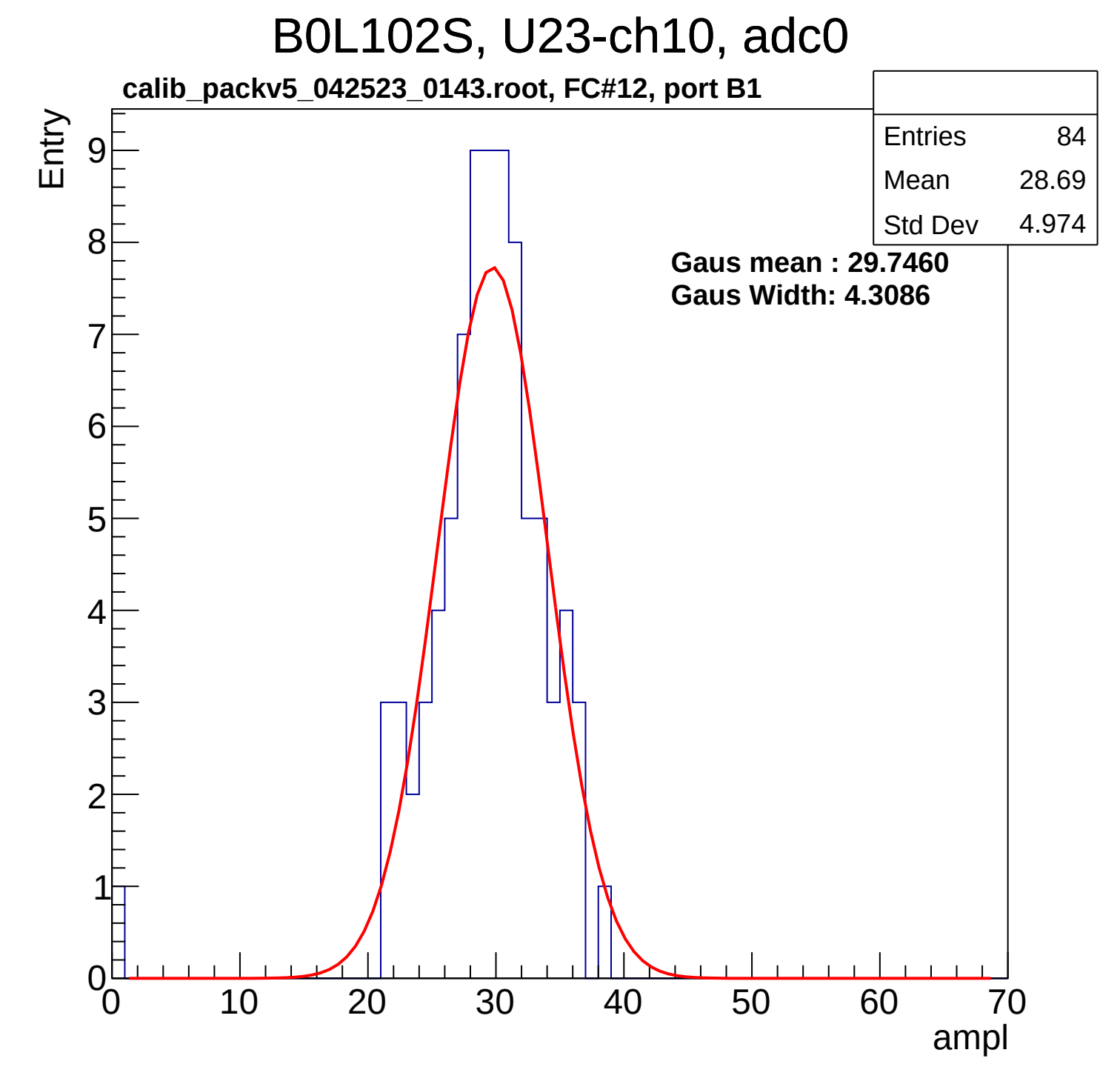
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch9, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch9, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch10, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 28.69
Std Dev 4.974
Gaus mean : 29.7460
Gaus Width: 4.3086
Entry
ampl
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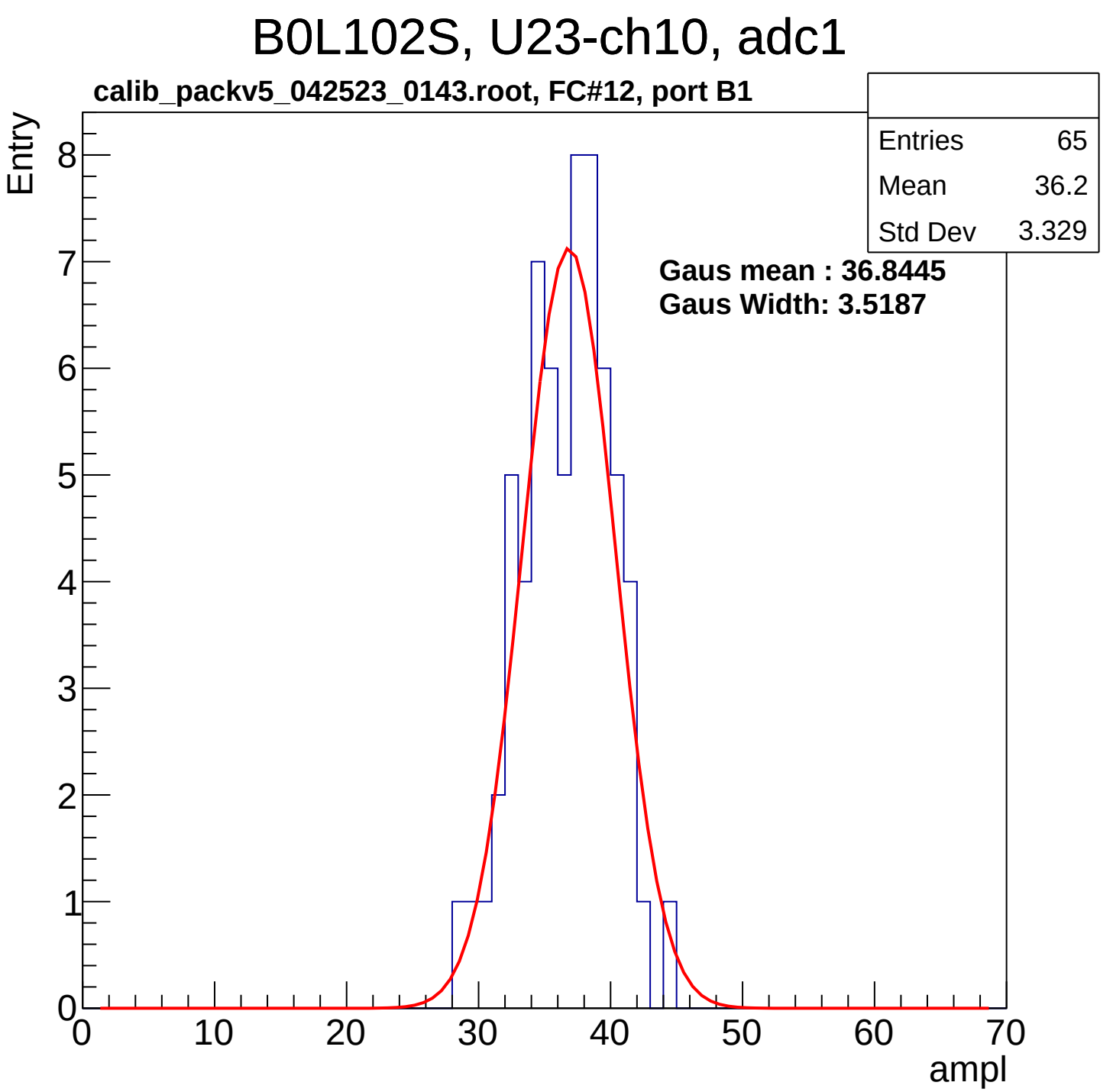
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch10, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 36.2
Std Dev 3.329
Gaus mean : 36.8445
Gaus Width: 3.5187
Entry
ampl
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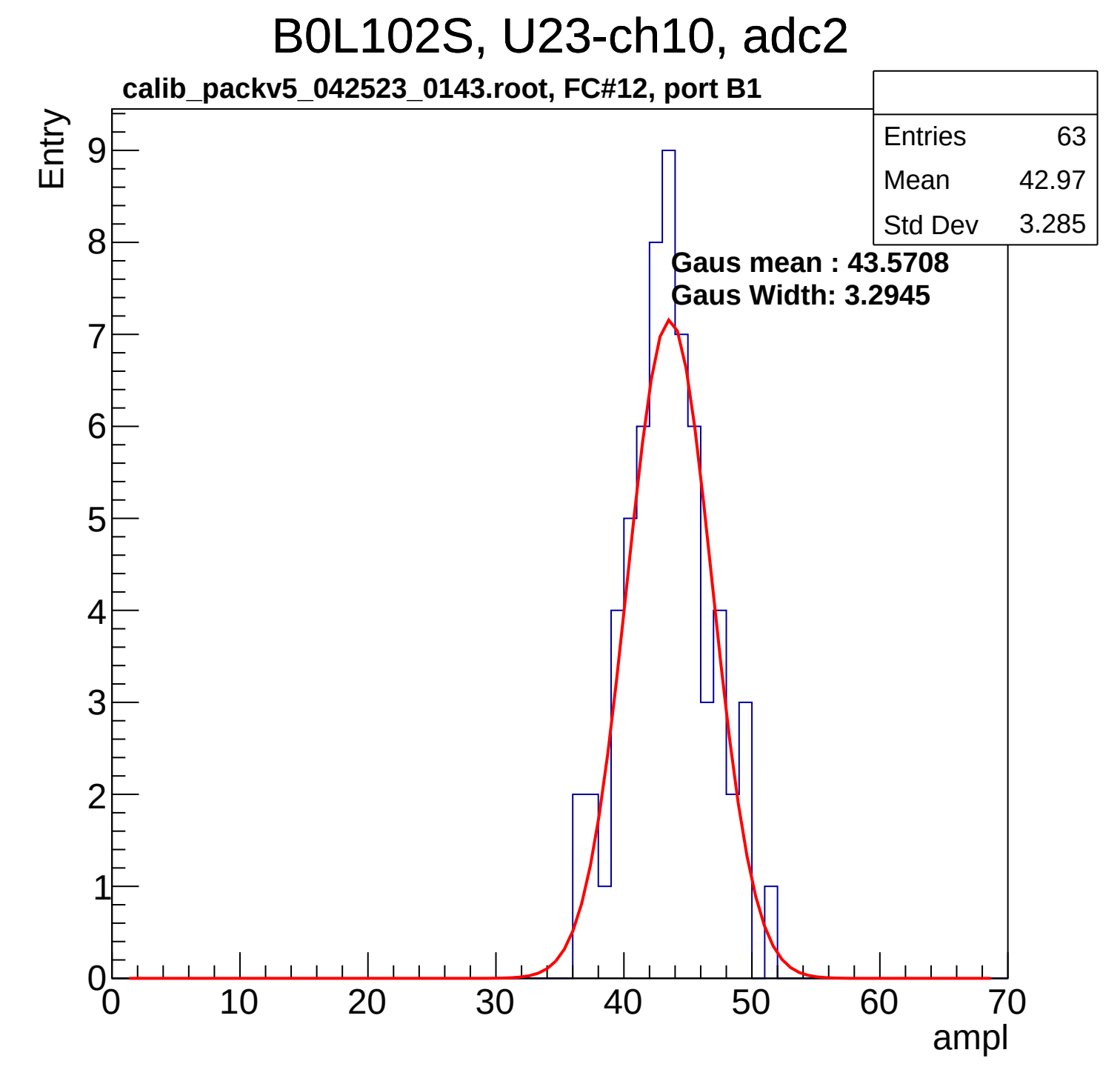
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch10, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 42.97
Std Dev 3.285
Gaus mean : 43.5708
Gaus Width: 3.2945
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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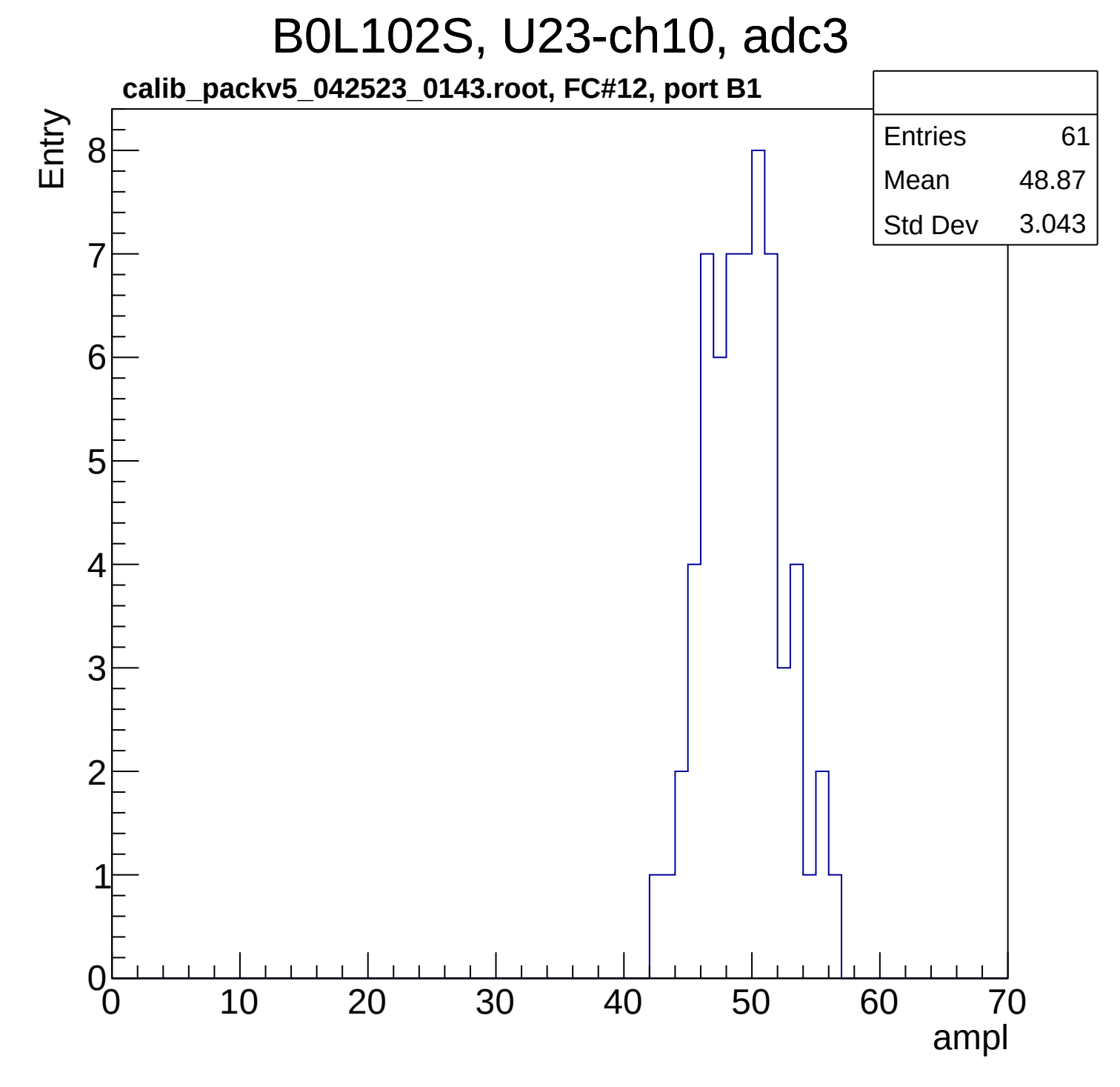
B0L102S, U23-ch10, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 48.87
Std Dev 3.043
Entry
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4
5
6
7
8
0
10
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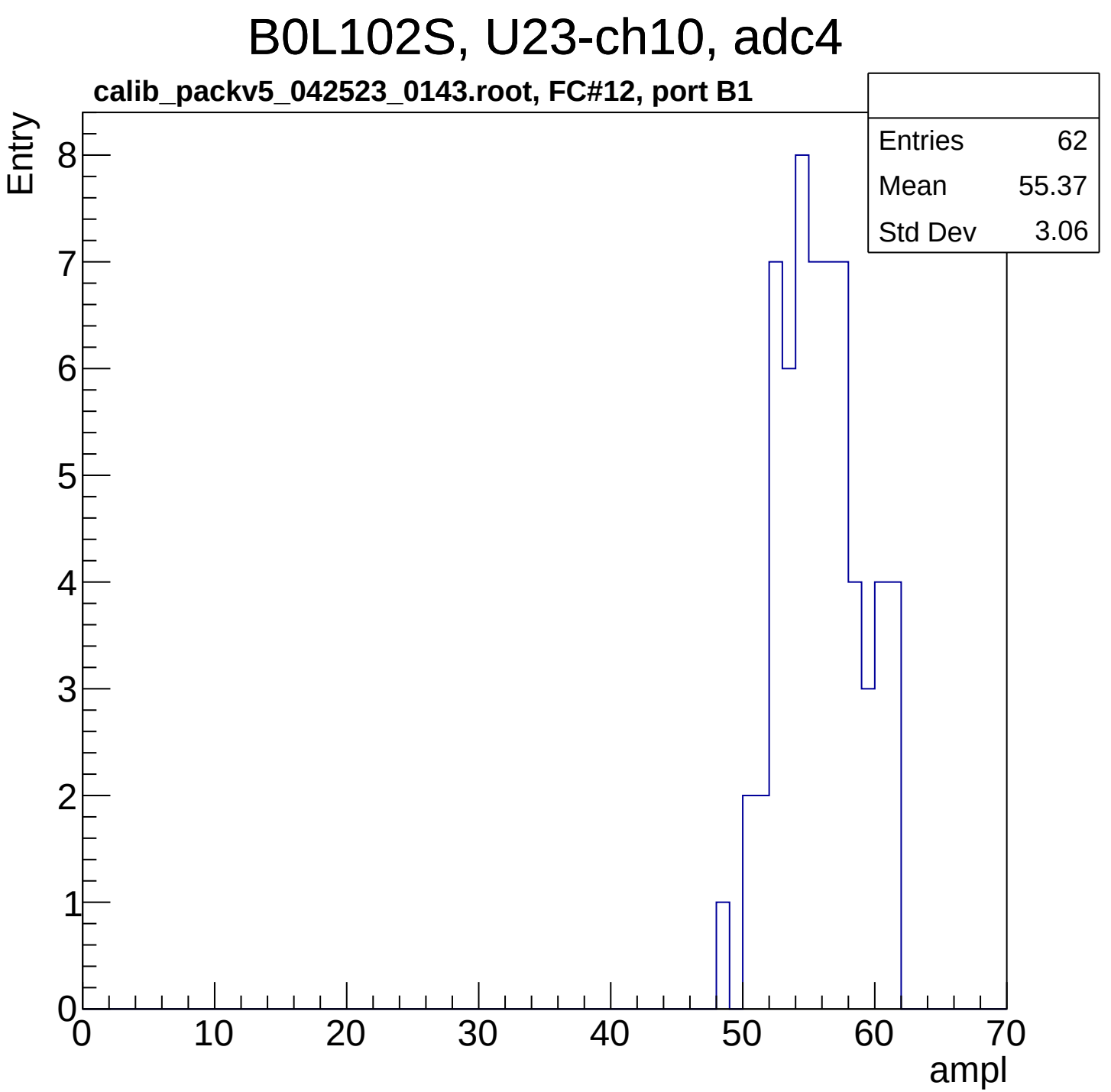
30
40
50
60
70
ampl


B0L102S, U23-ch10, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 55.37
Std Dev 3.06
Entry
ampl
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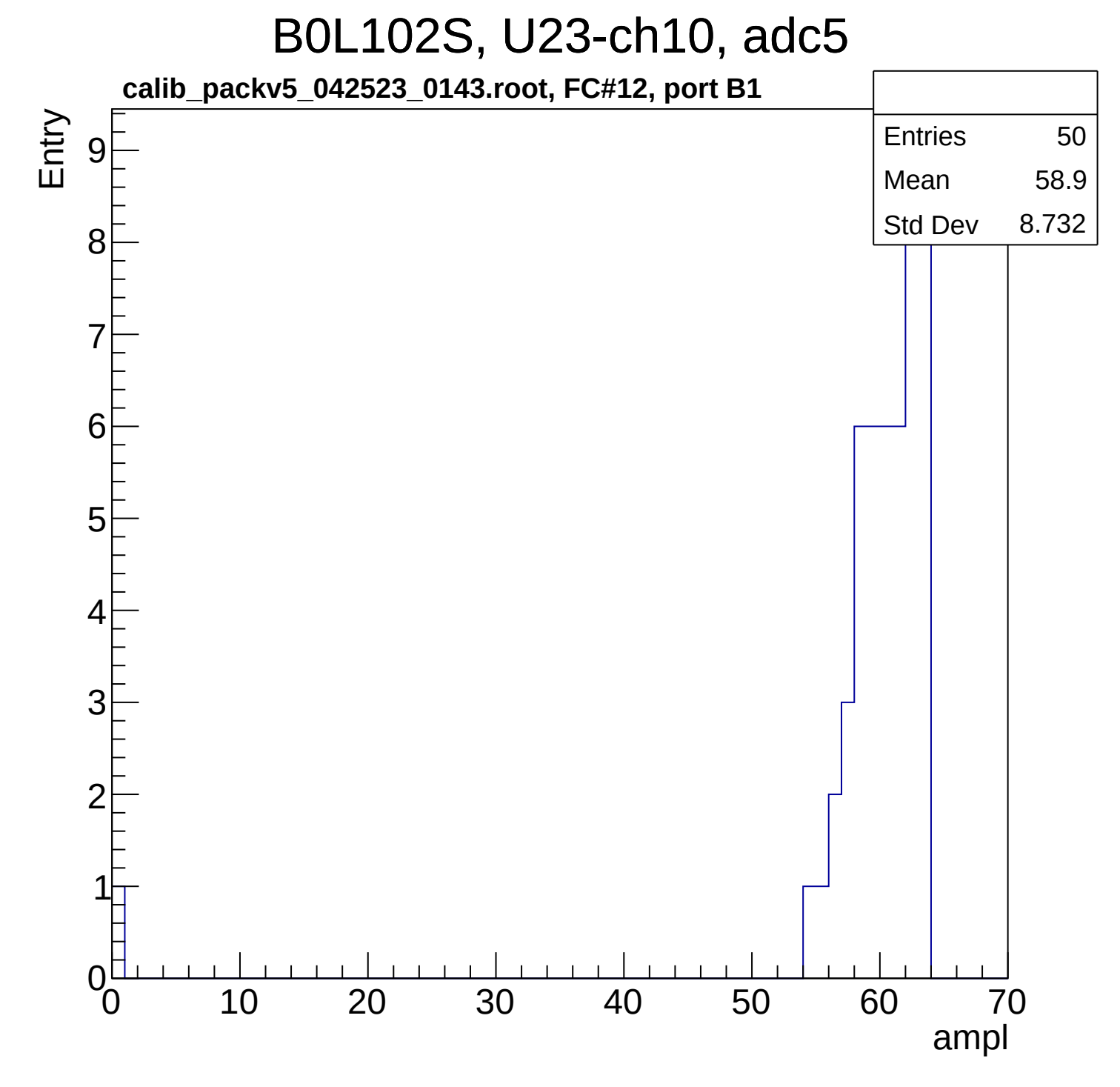
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch10, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.9
Std Dev 8.732
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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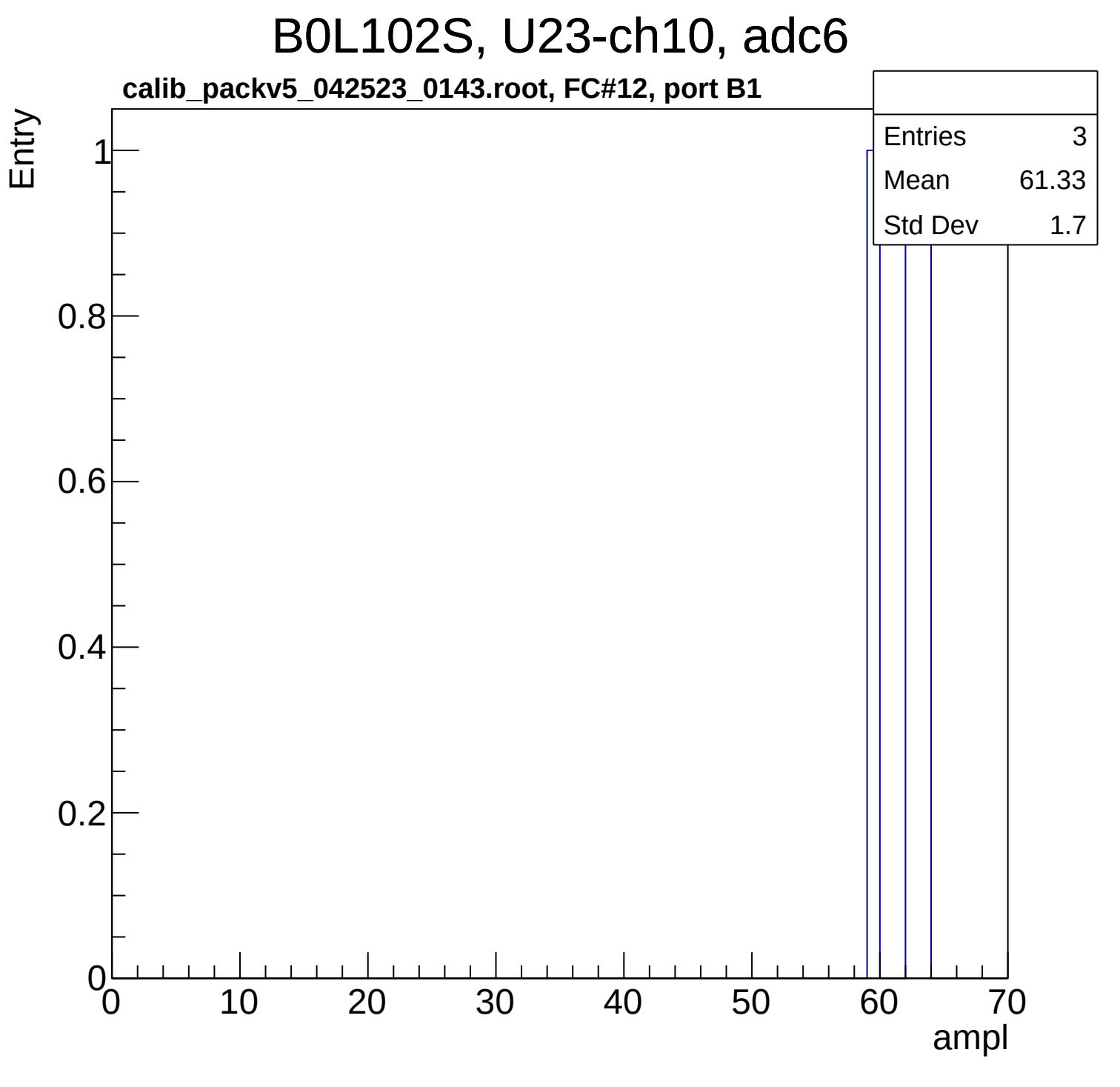
50
60
70


B0L102S, U23-ch10, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.33
Std Dev 1.7
Entry
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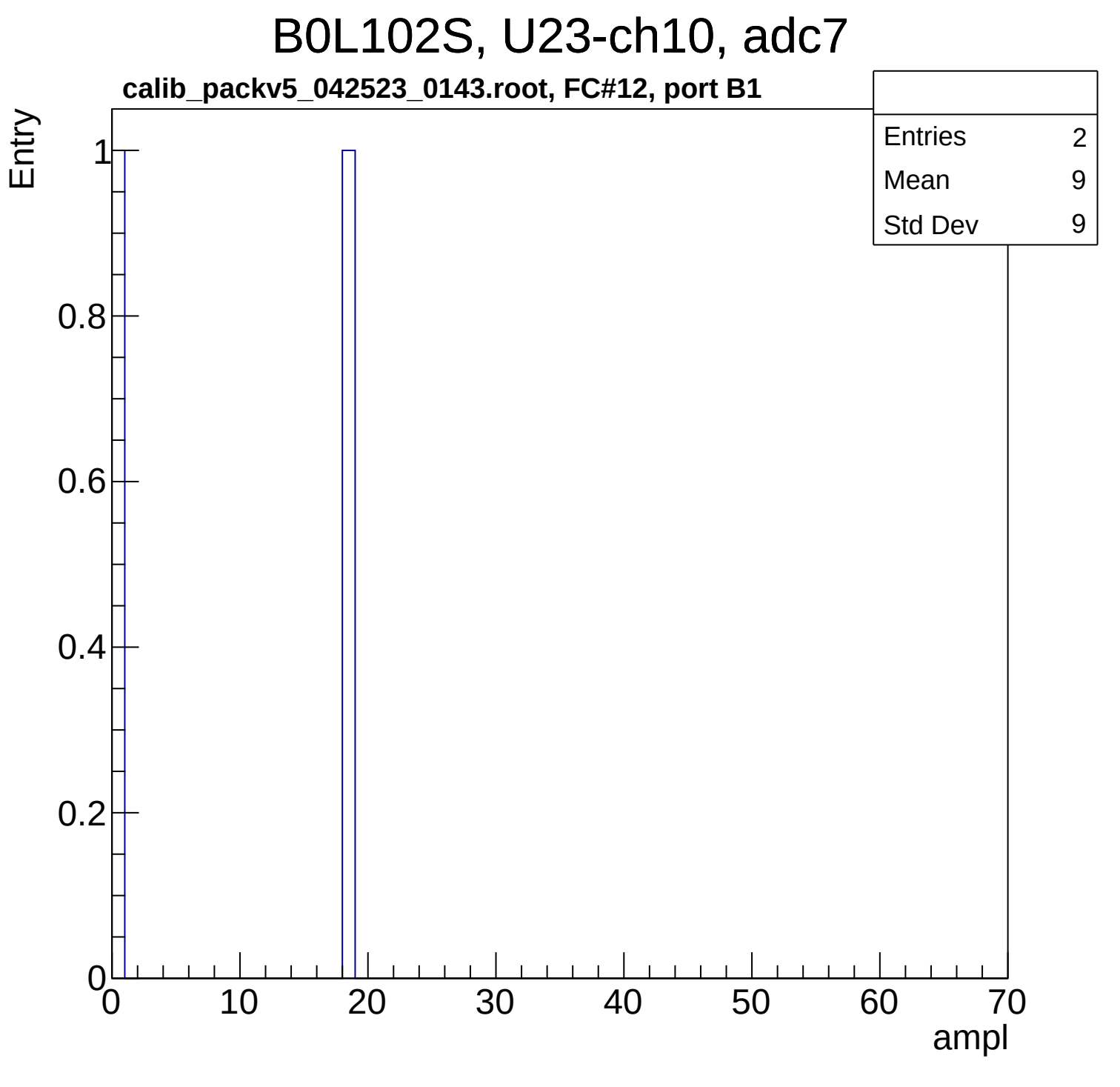
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch10, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 9
Std Dev 9
Entry
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0.6
0.4
0.2
0
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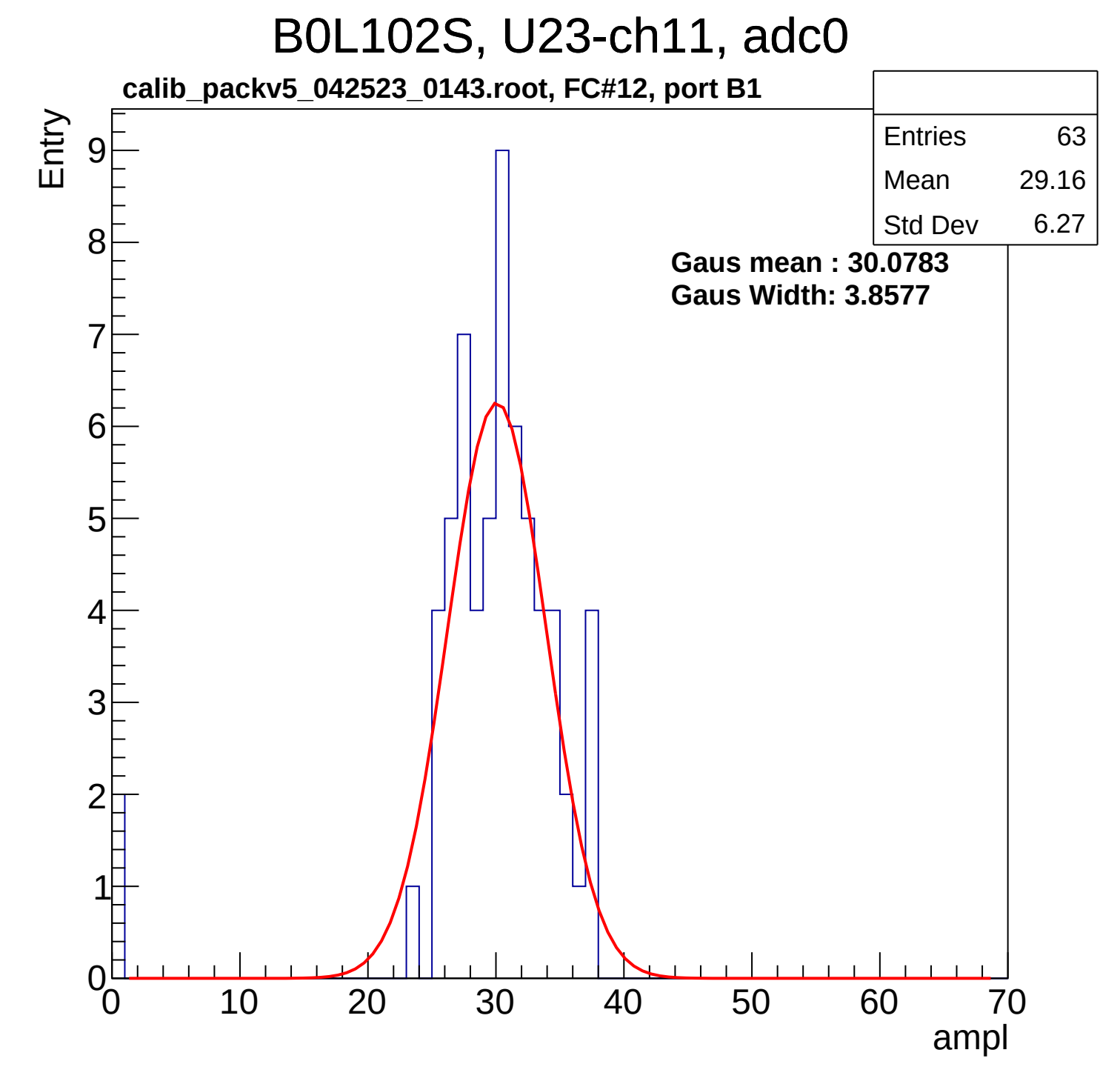
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch11, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 29.16
Std Dev 6.27
Gaus mean : 30.0783
Gaus Width: 3.8577
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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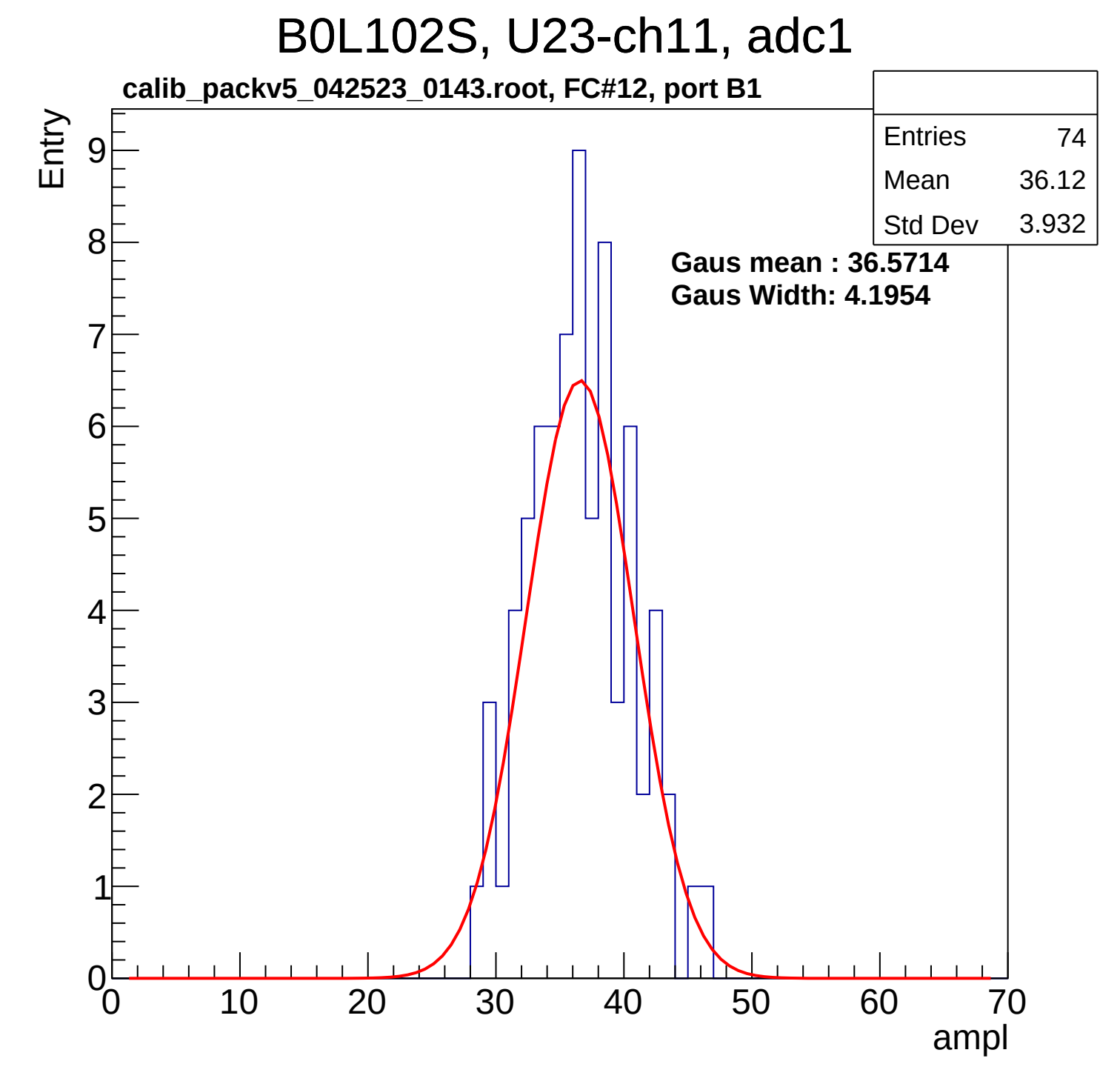
B0L102S, U23-ch11, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.12
Std Dev 3.932
Gaus mean : 36.5714
Gaus Width: 4.1954
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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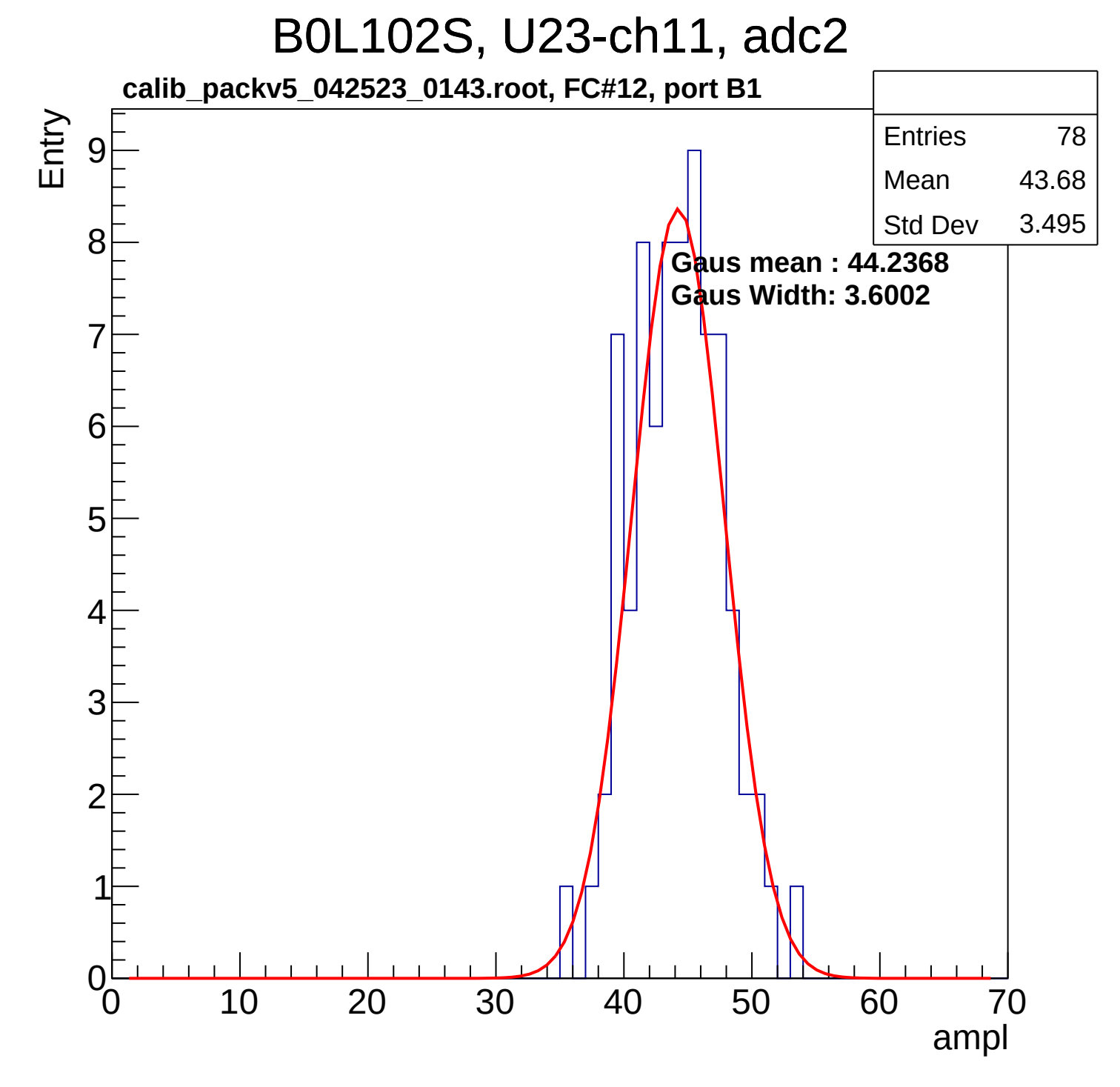
B0L102S, U23-ch11, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 43.68
Std Dev 3.495
Gaus mean : 44.2368
Gaus Width: 3.6002
Entry
ampl
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2
3
4
5
6
7
8
9
0
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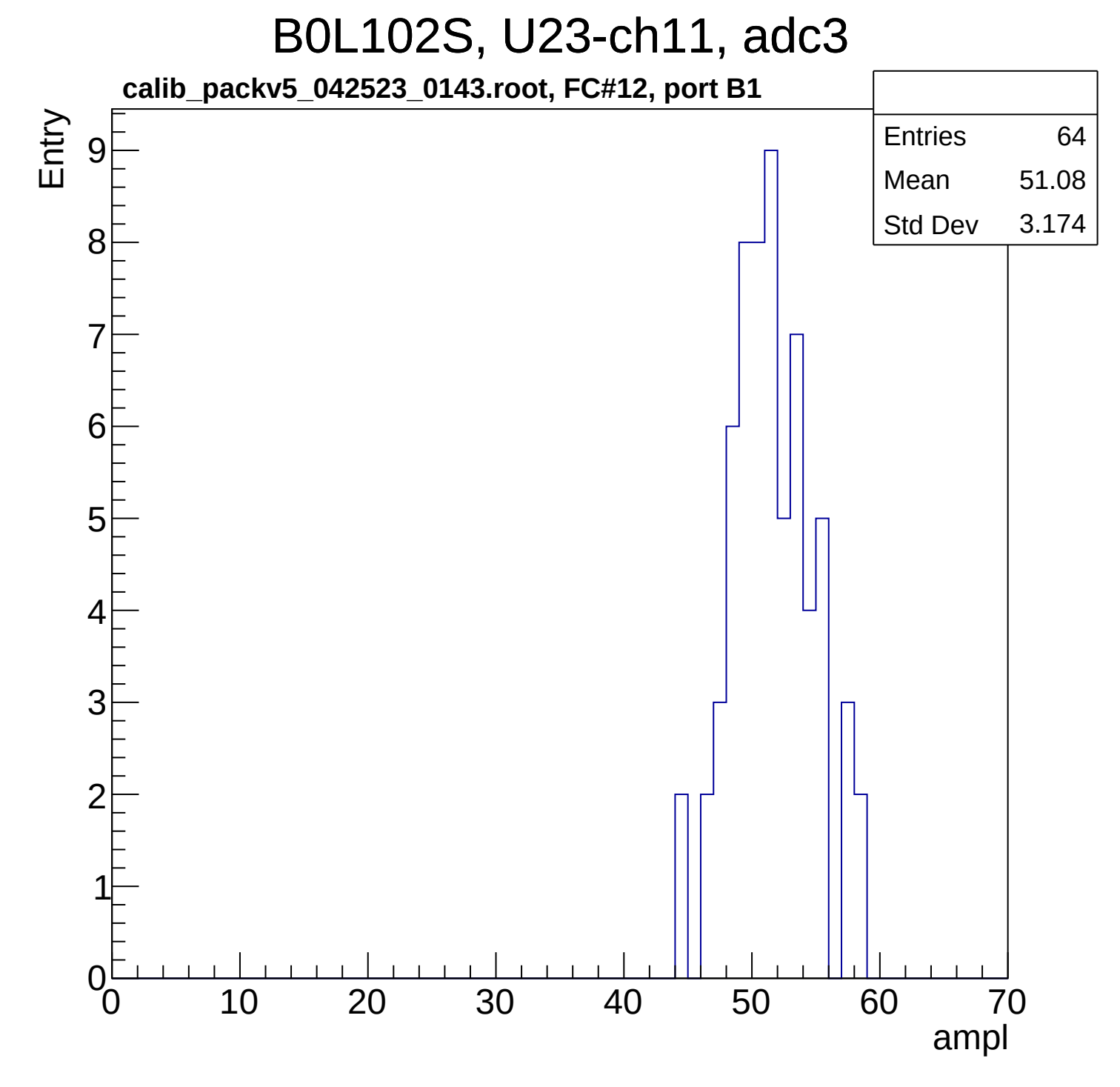
B0L102S, U23-ch11, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 51.08
Std Dev 3.174
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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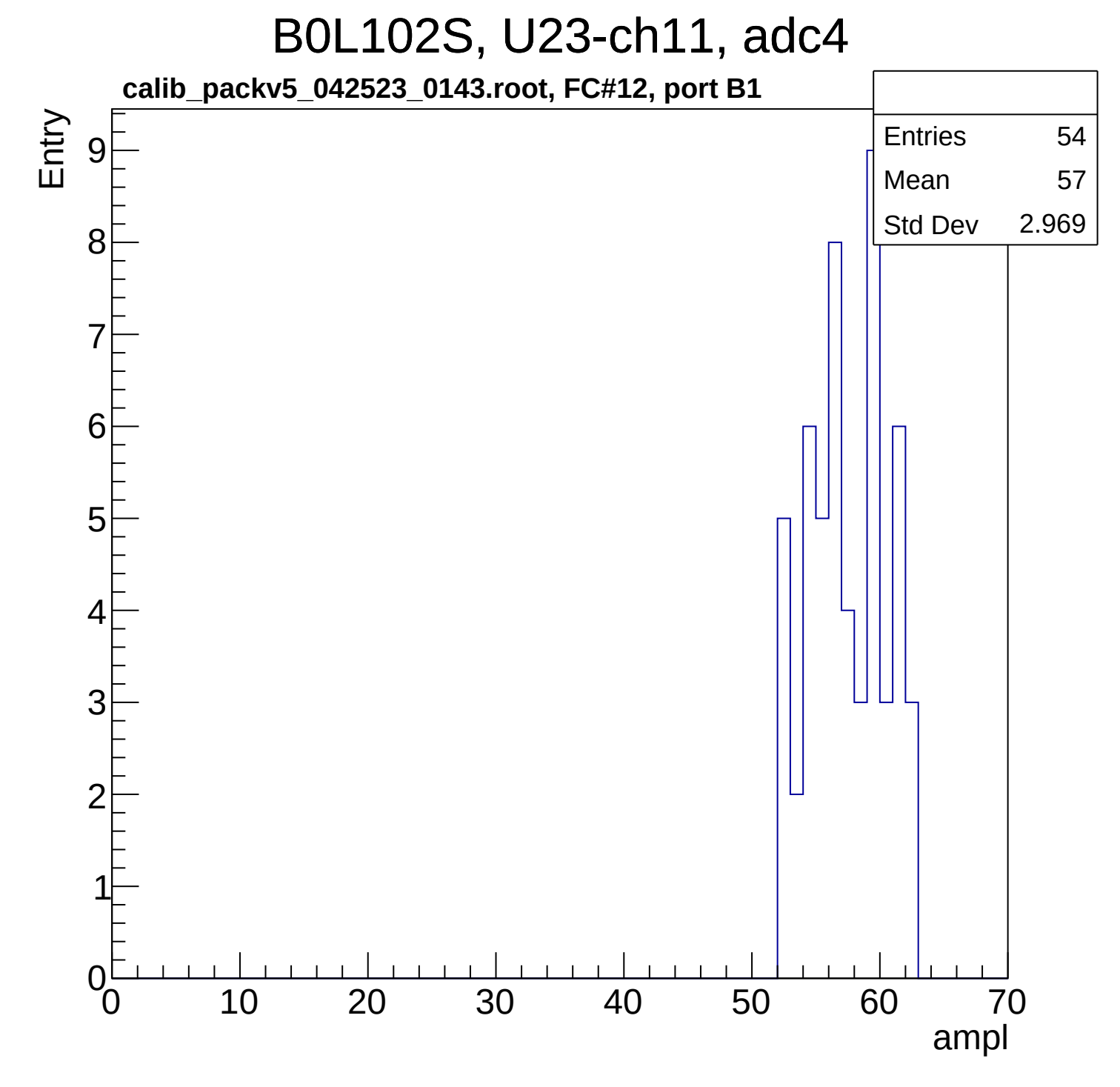
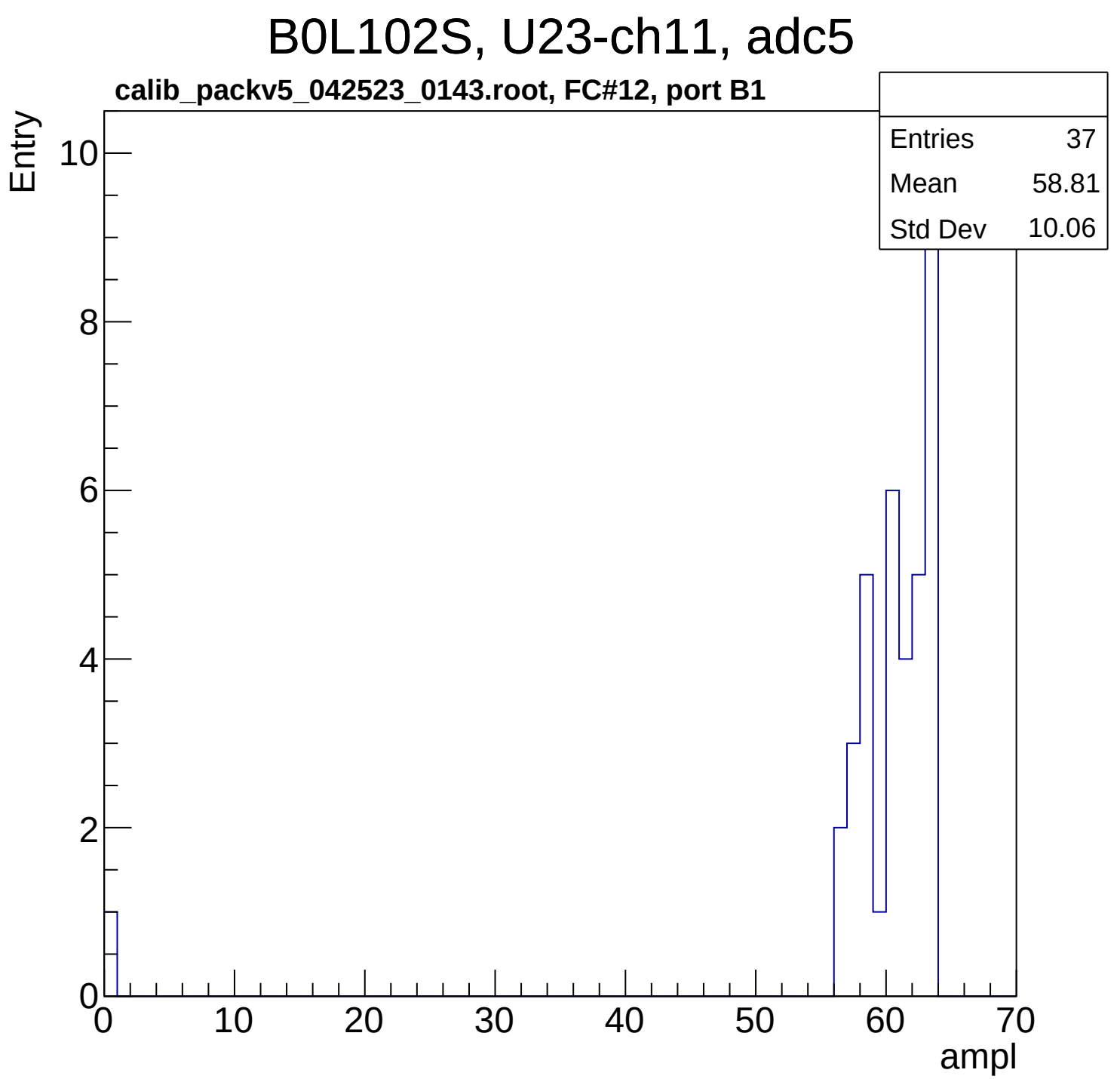
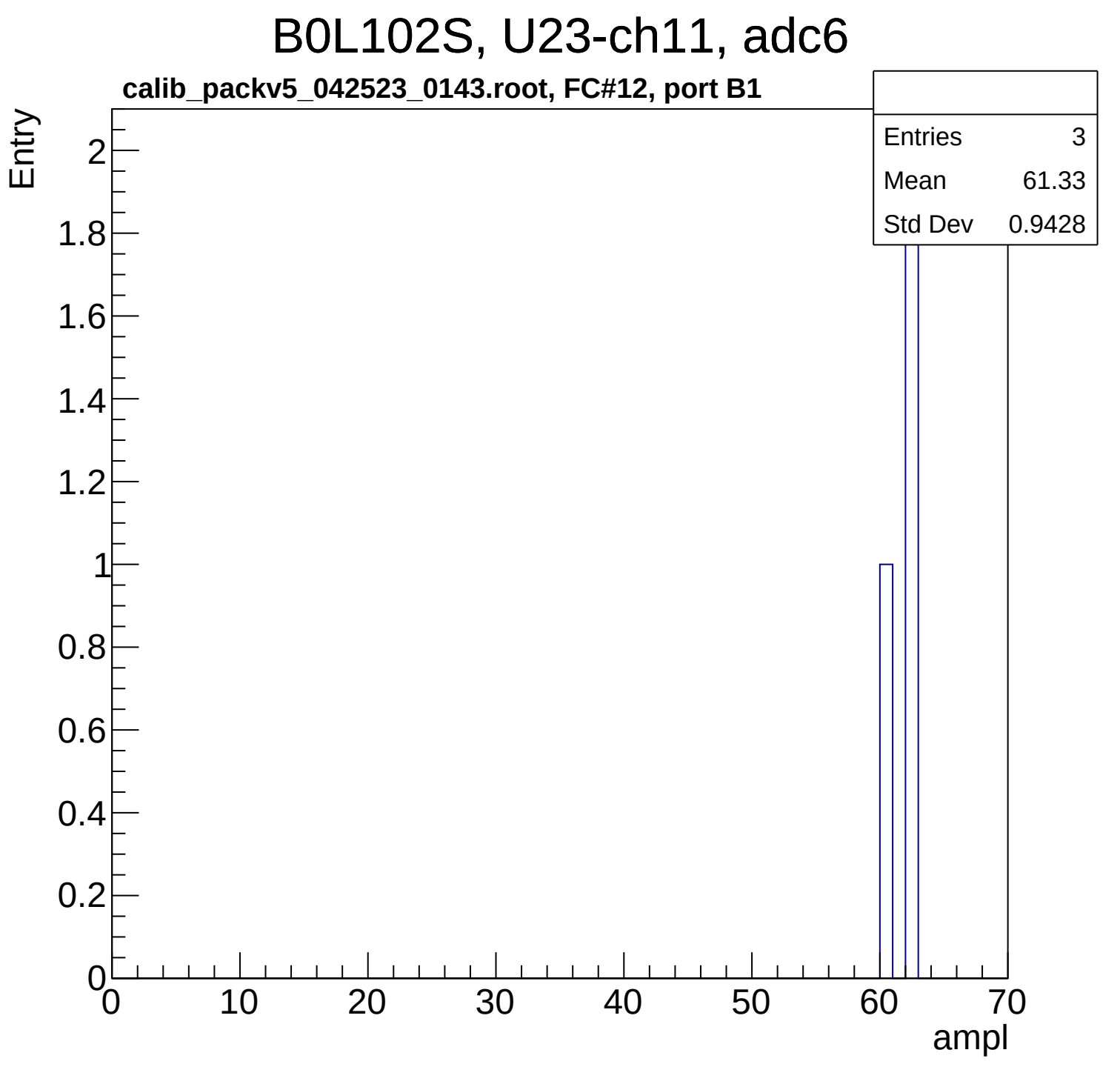
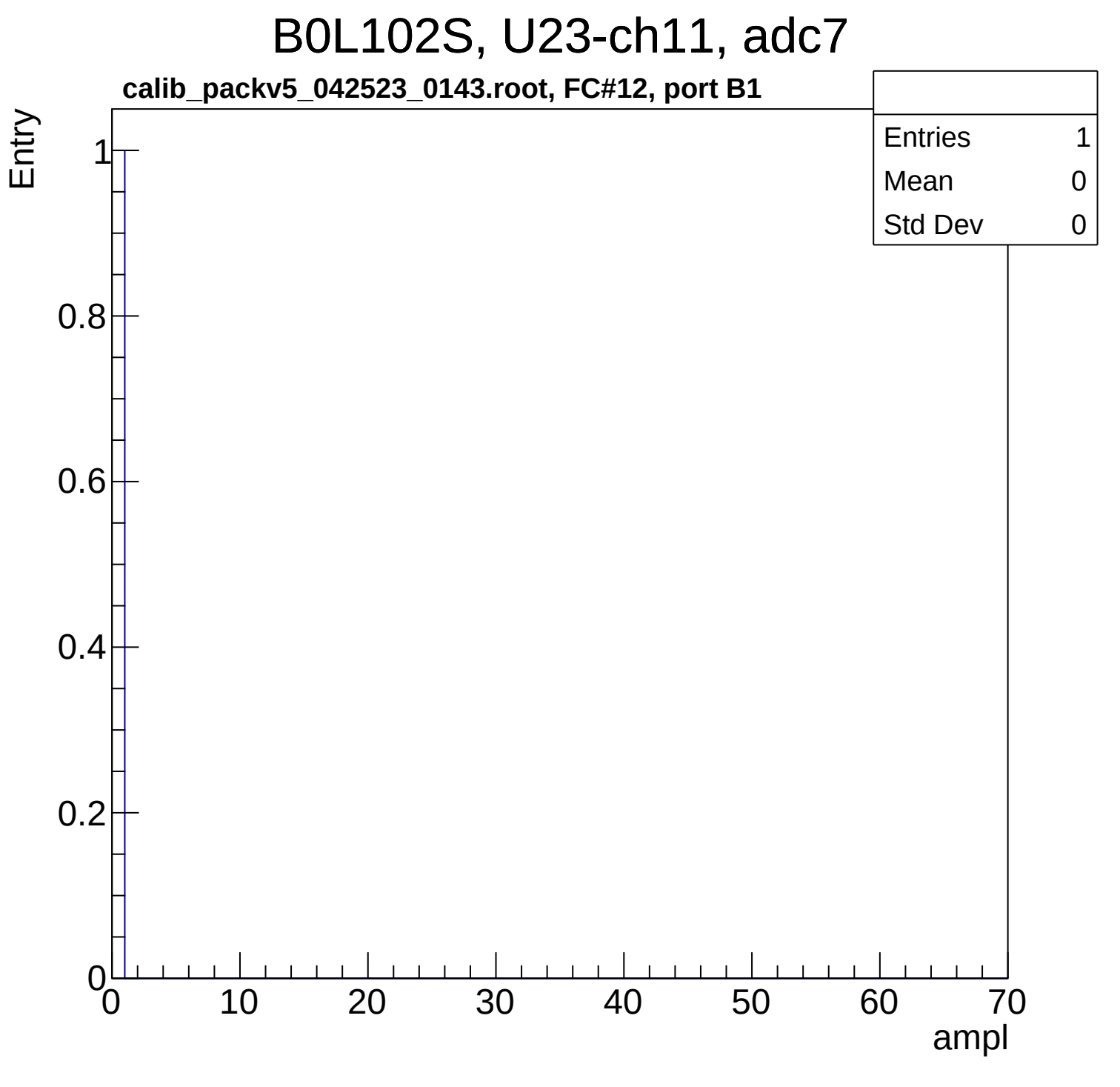
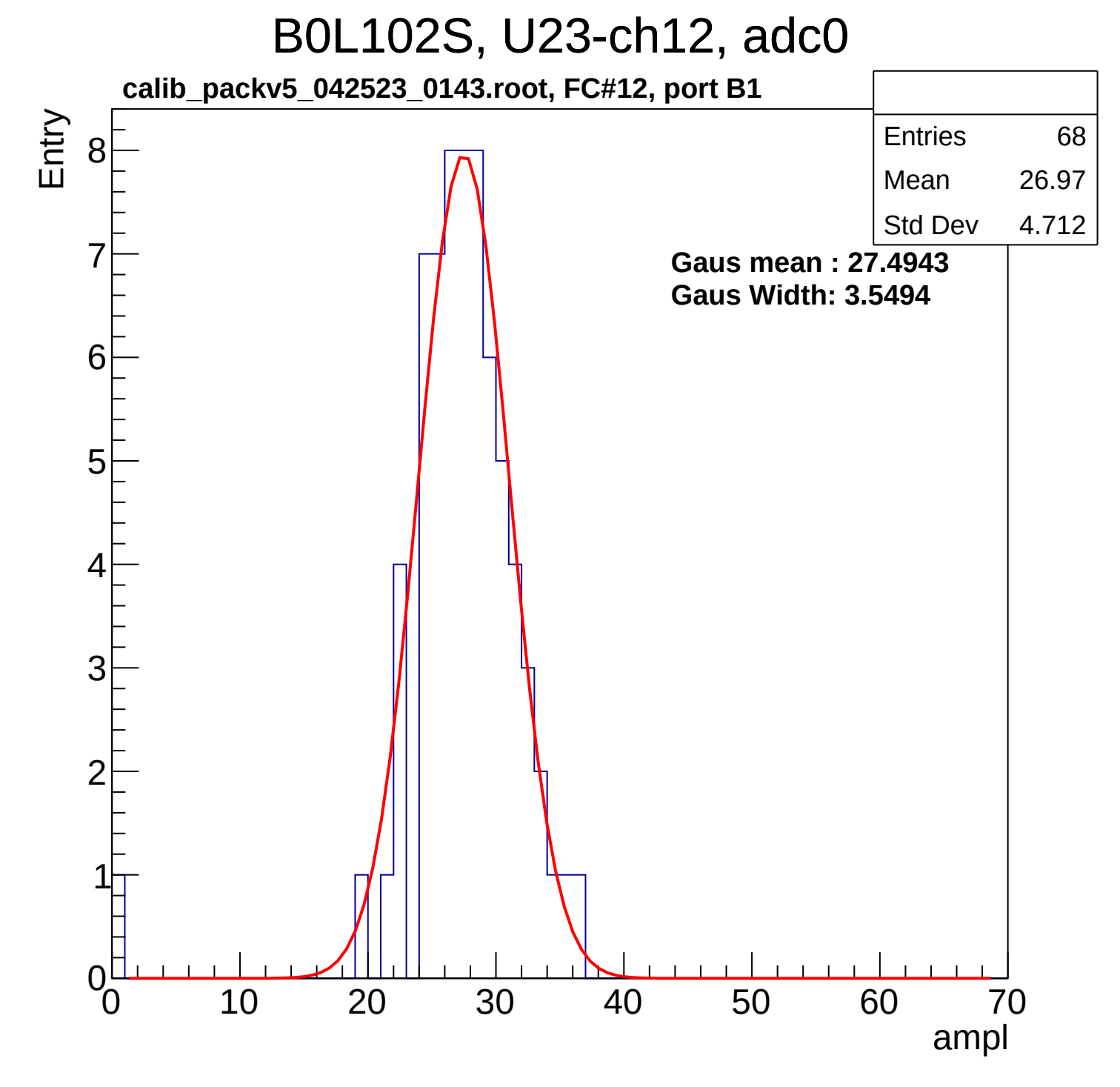
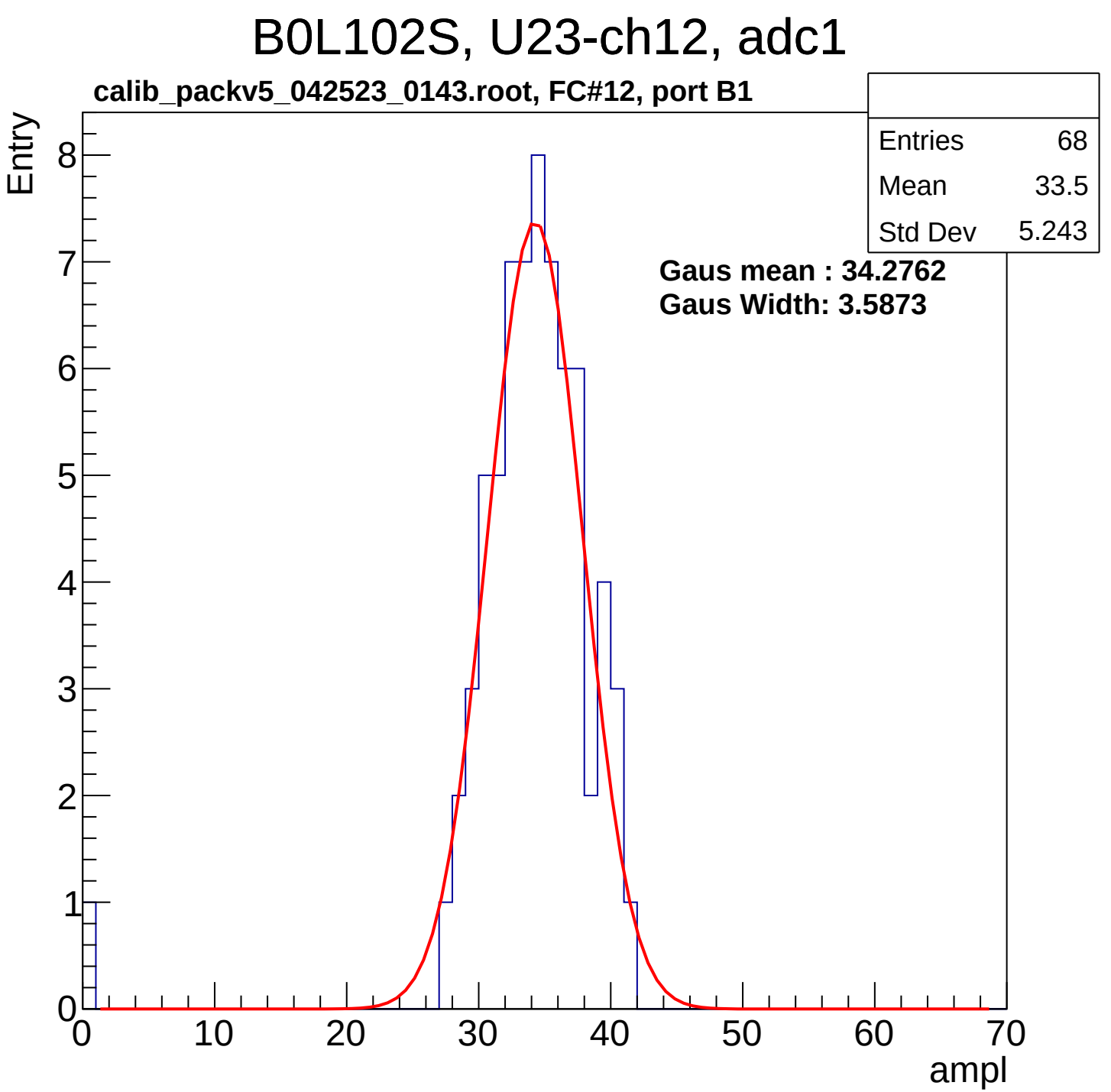

B0L102S, U23-ch11, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 57
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch11, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 58.81
Std Dev 10.06
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch11, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch11, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch12, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 26.97
Std Dev 4.712
Gaus mean : 27.4943
Gaus Width: 3.5494
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch12, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 33.5
Std Dev 5.243
Gaus mean : 34.2762
Gaus Width: 3.5873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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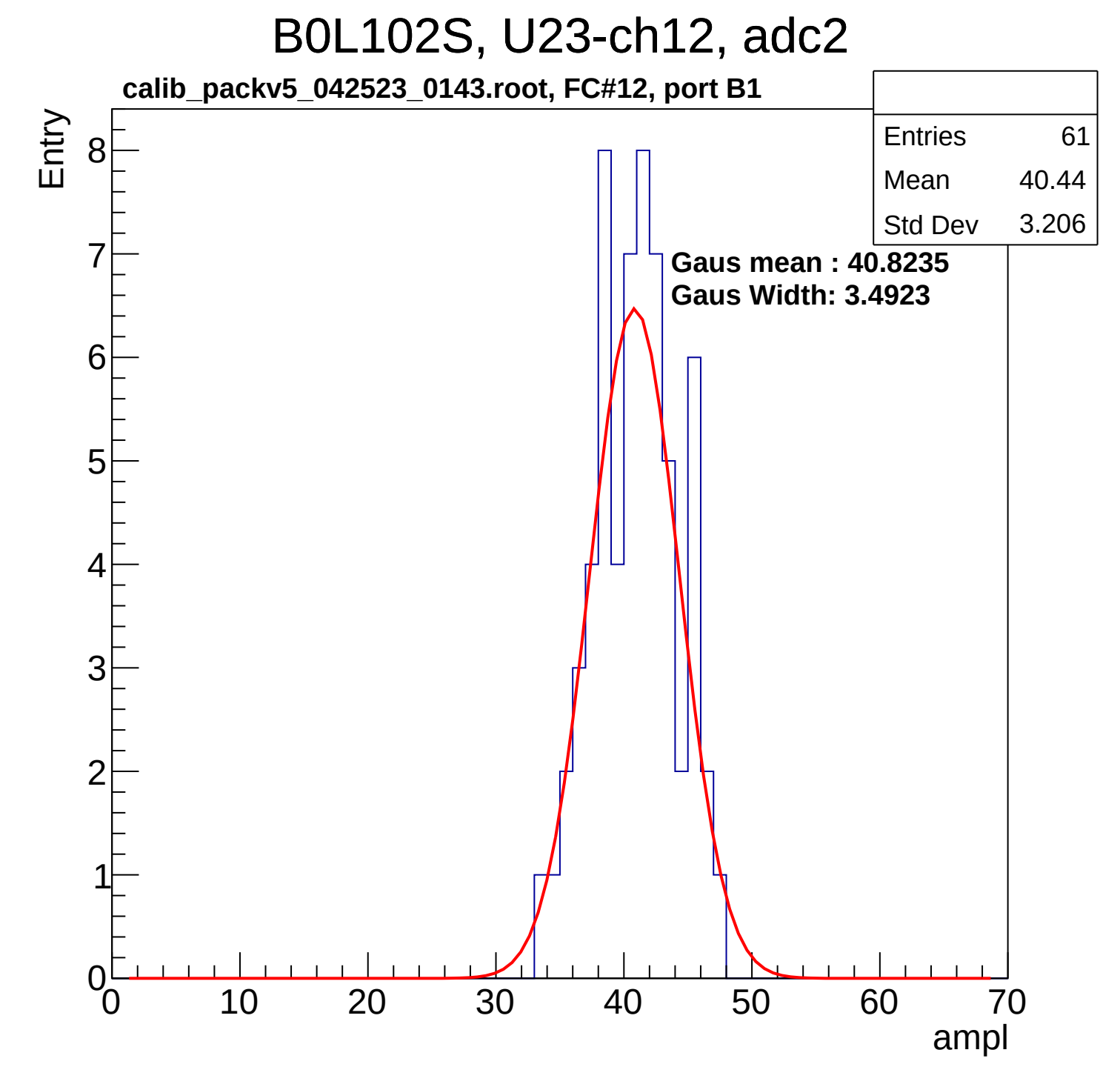
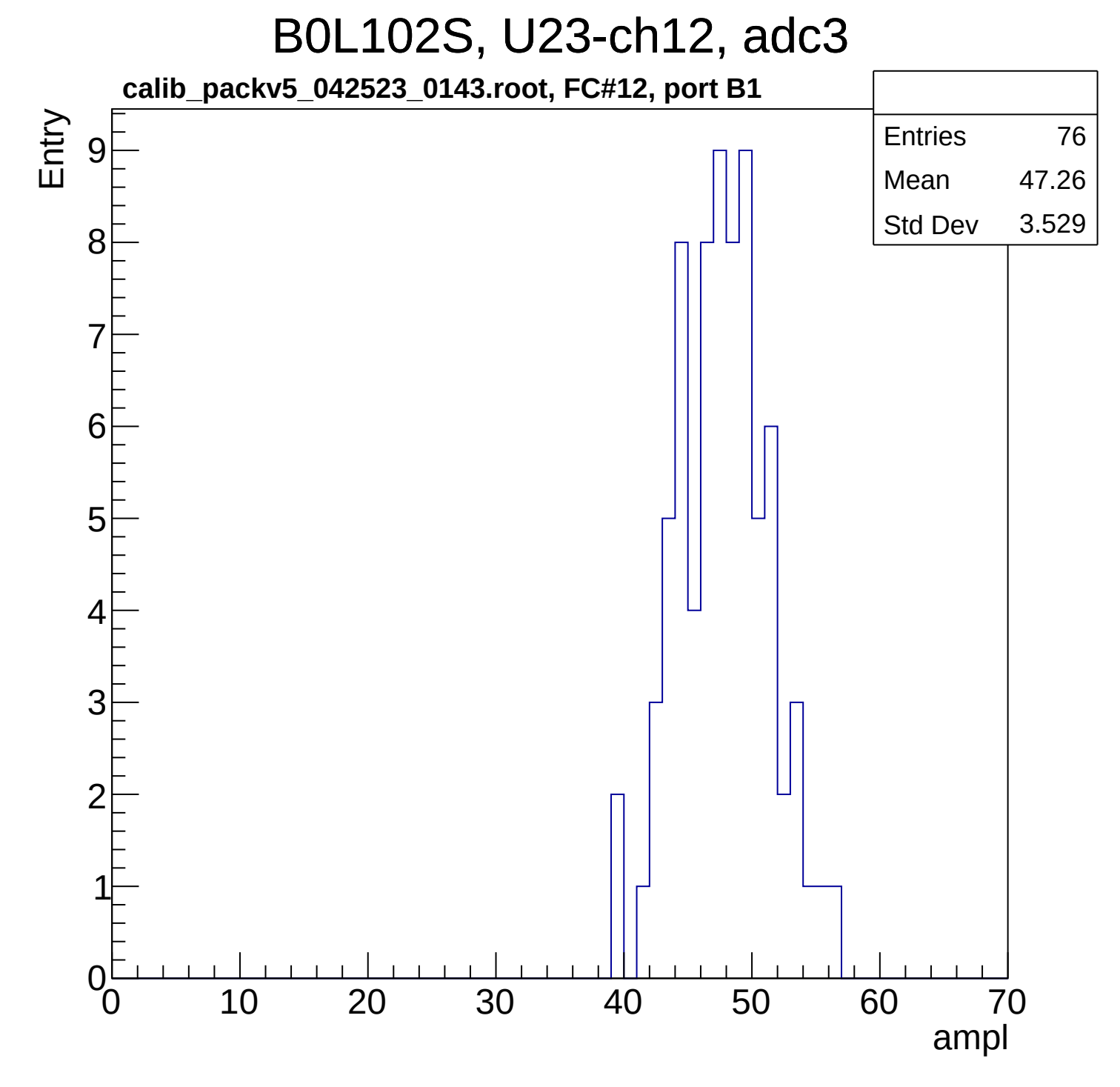

B0L102S, U23-ch12, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 40.44
Std Dev 3.206
Gaus mean : 40.8235
Gaus Width: 3.4923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch12, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 47.26
Std Dev 3.529
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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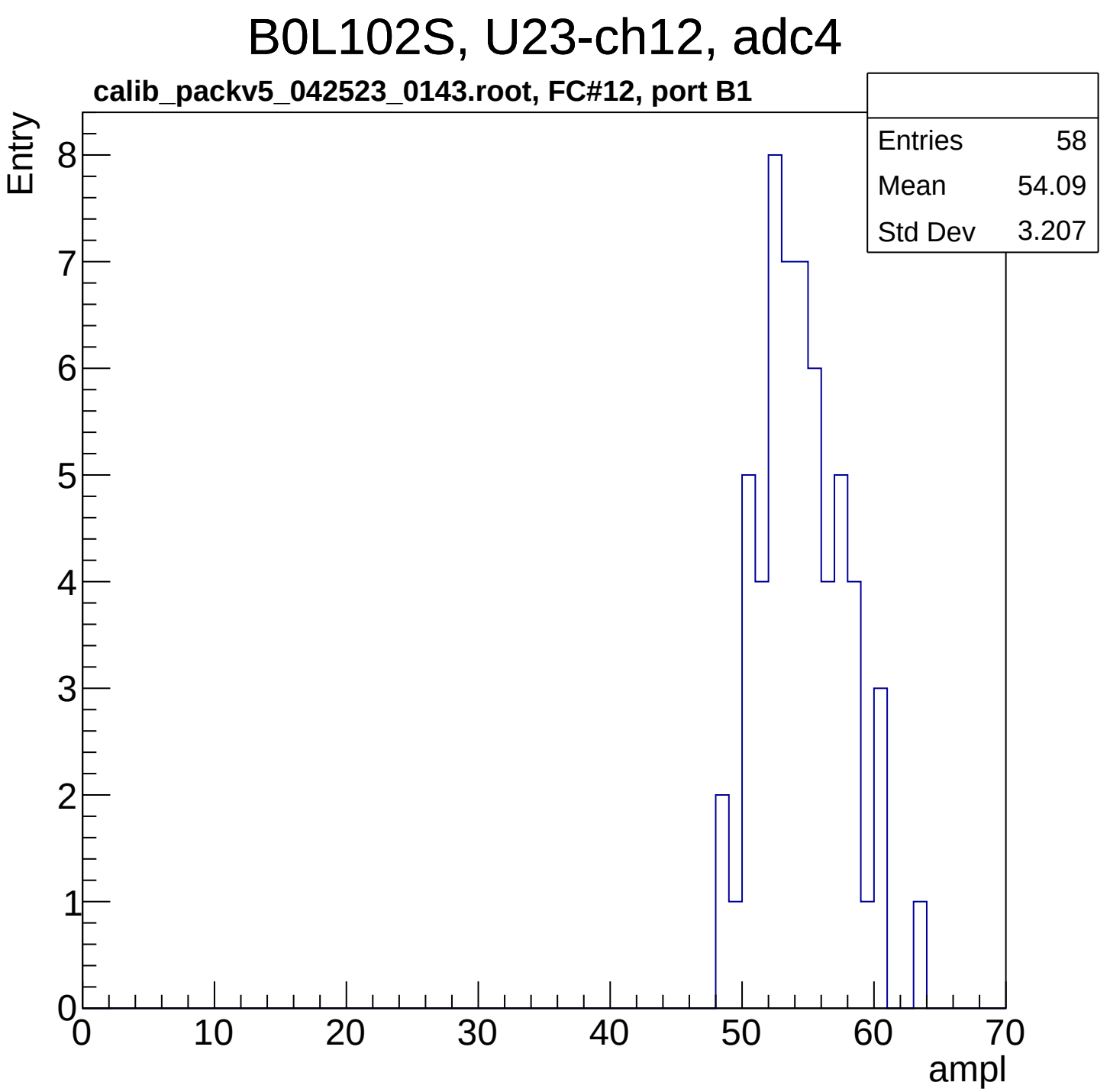
B0L102S, U23-ch12, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 54.09
Std Dev 3.207
Entry
ampl
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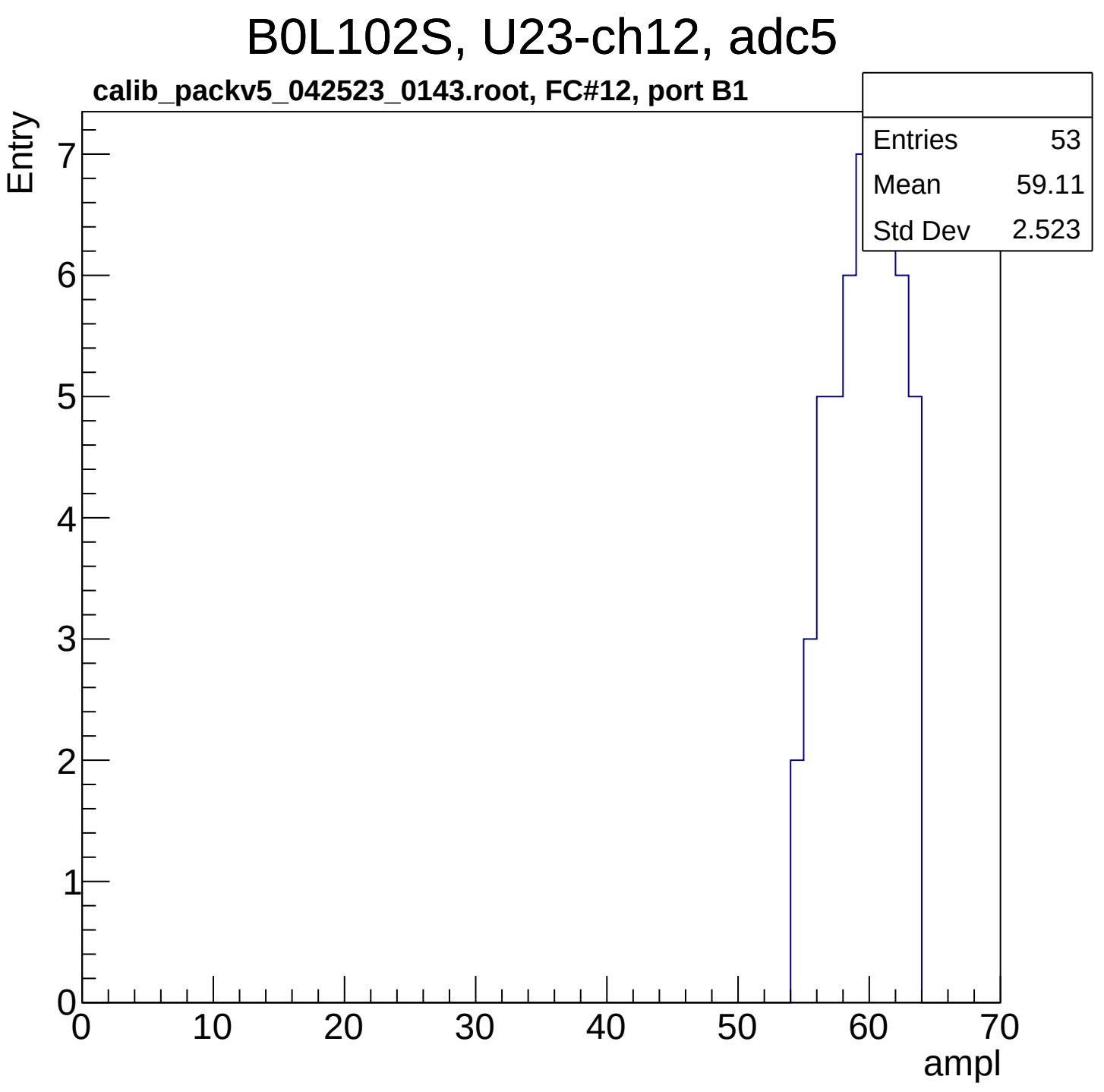
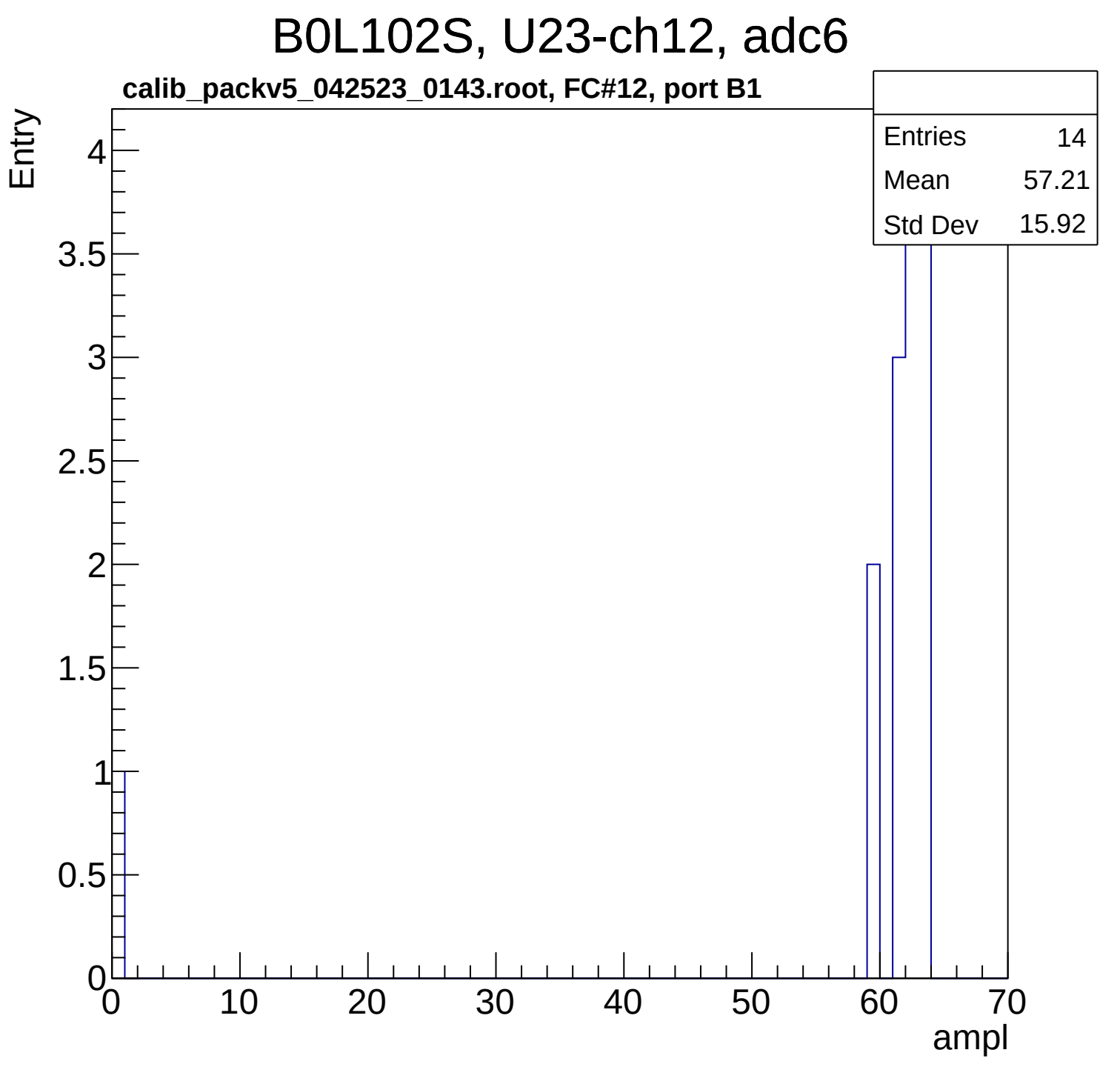
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch12, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 59.11
Std Dev 2.523
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch12, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 57.21
Std Dev 15.92
Entry
ampl
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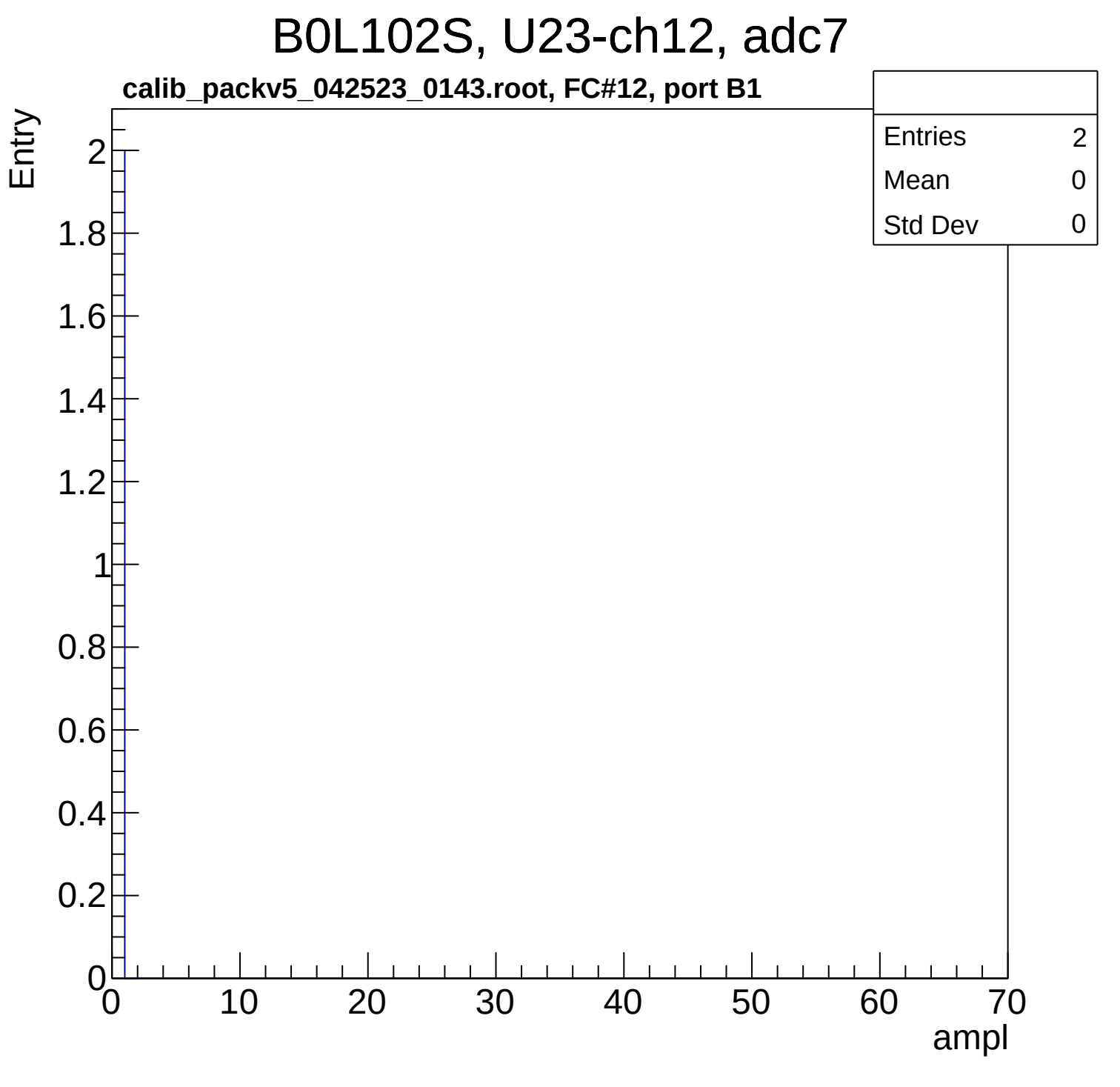
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch12, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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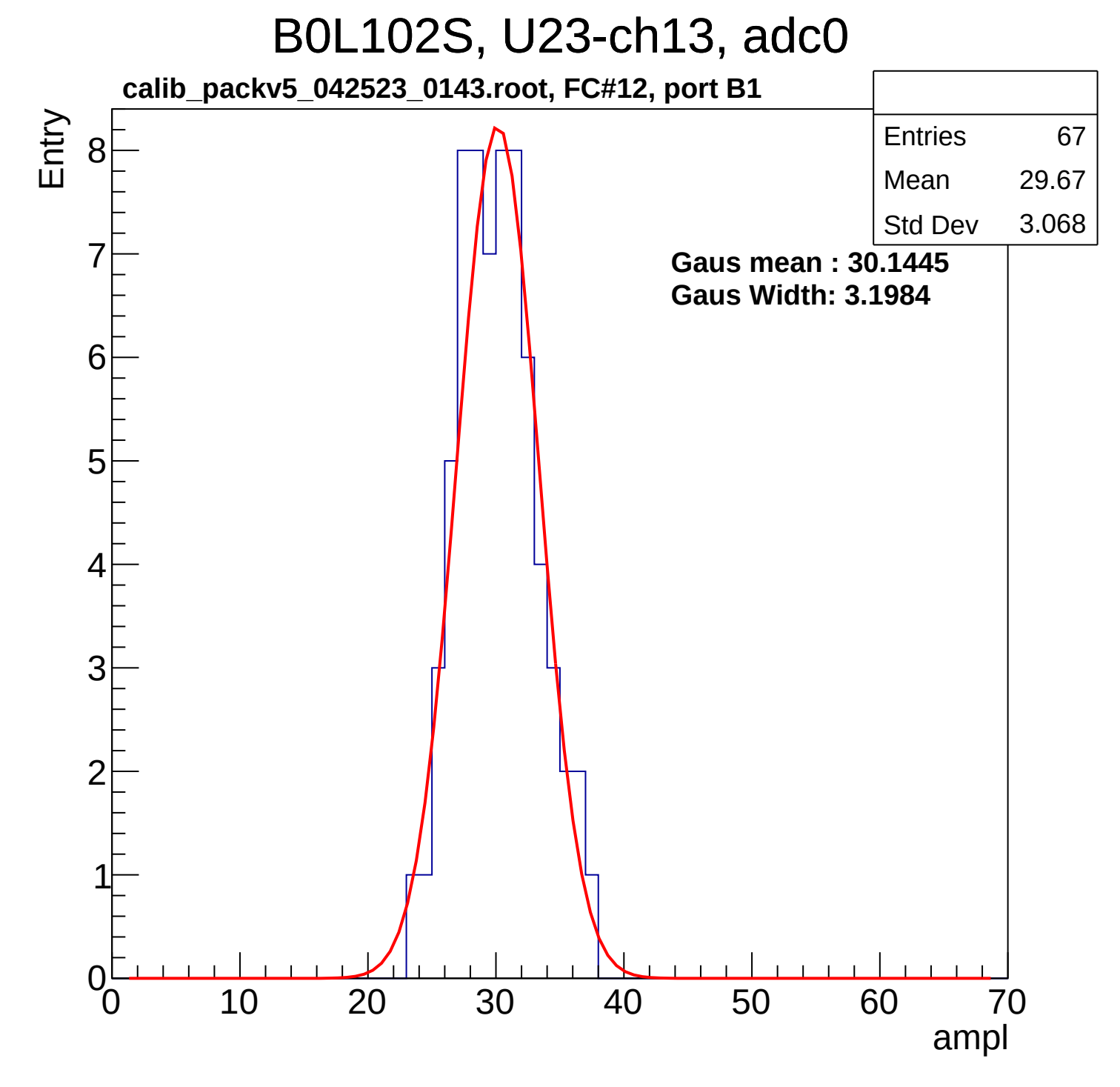
B0L102S, U23-ch13, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.67
Std Dev 3.068
Gaus mean : 30.1445
Gaus Width: 3.1984
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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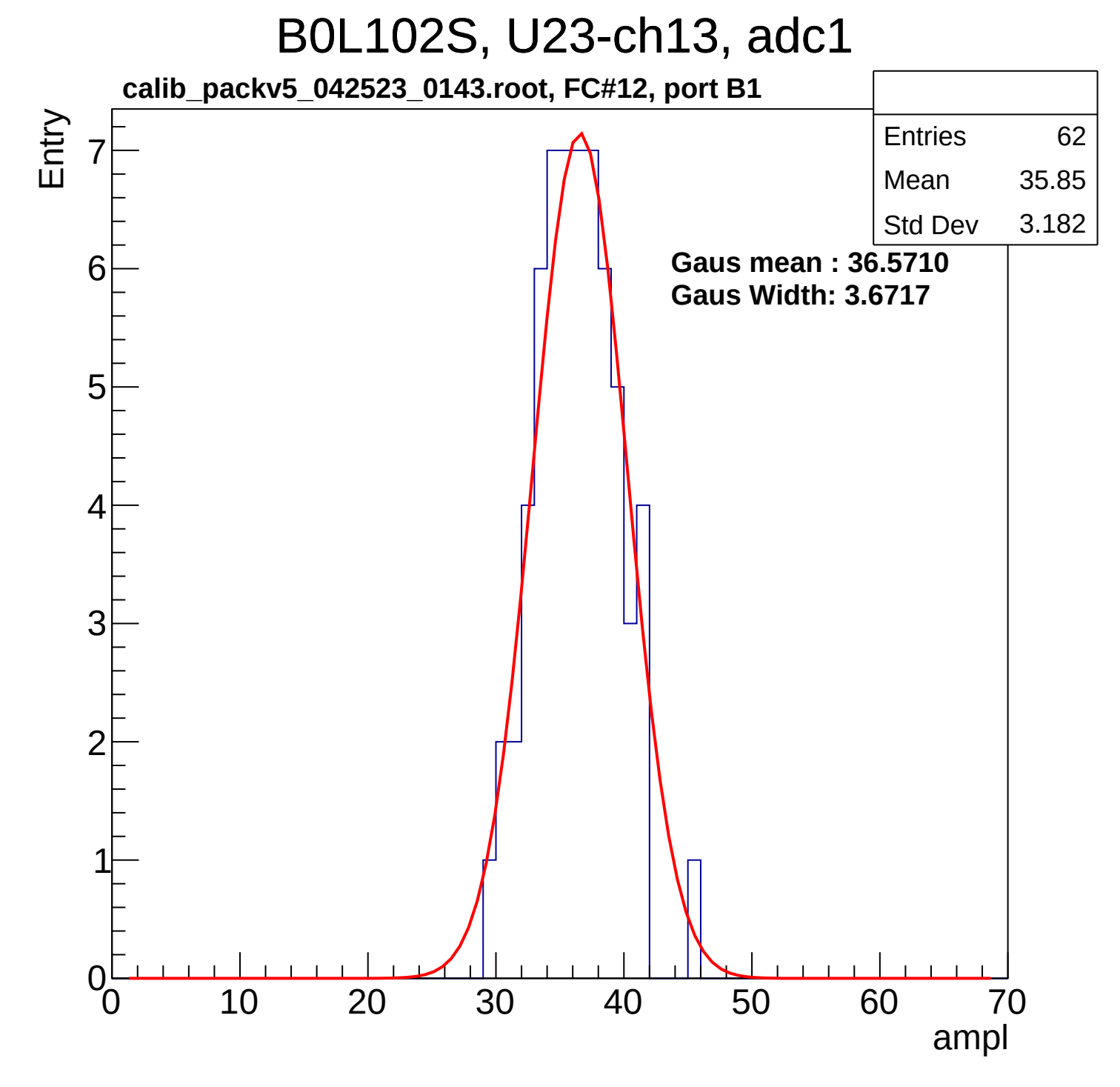
B0L102S, U23-ch13, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 35.85
Std Dev 3.182
Gaus mean : 36.5710
Gaus Width: 3.6717
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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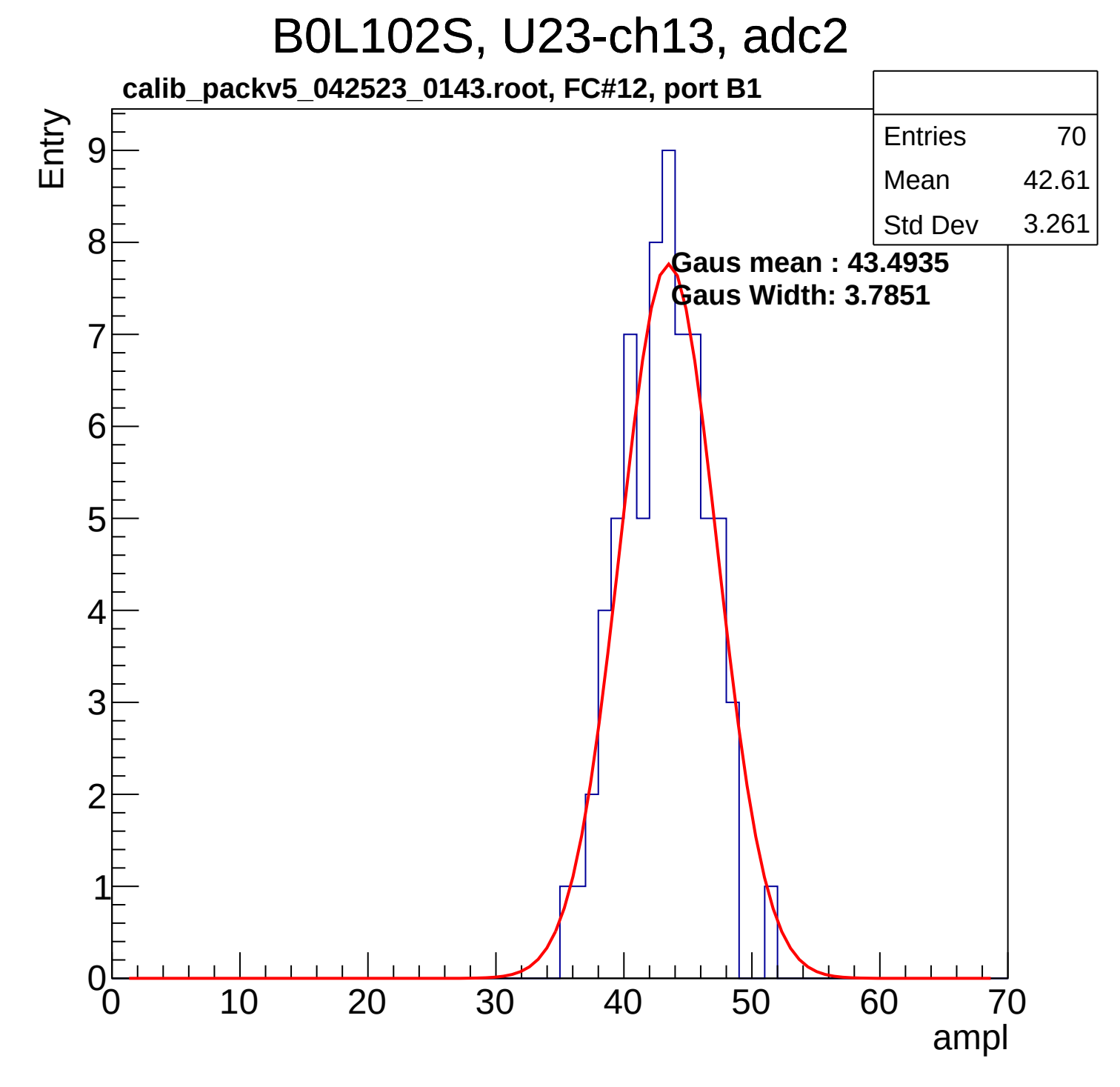
B0L102S, U23-ch13, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 42.61
Std Dev 3.261
Gaus mean : 43.4935
Gaus Width: 3.7851
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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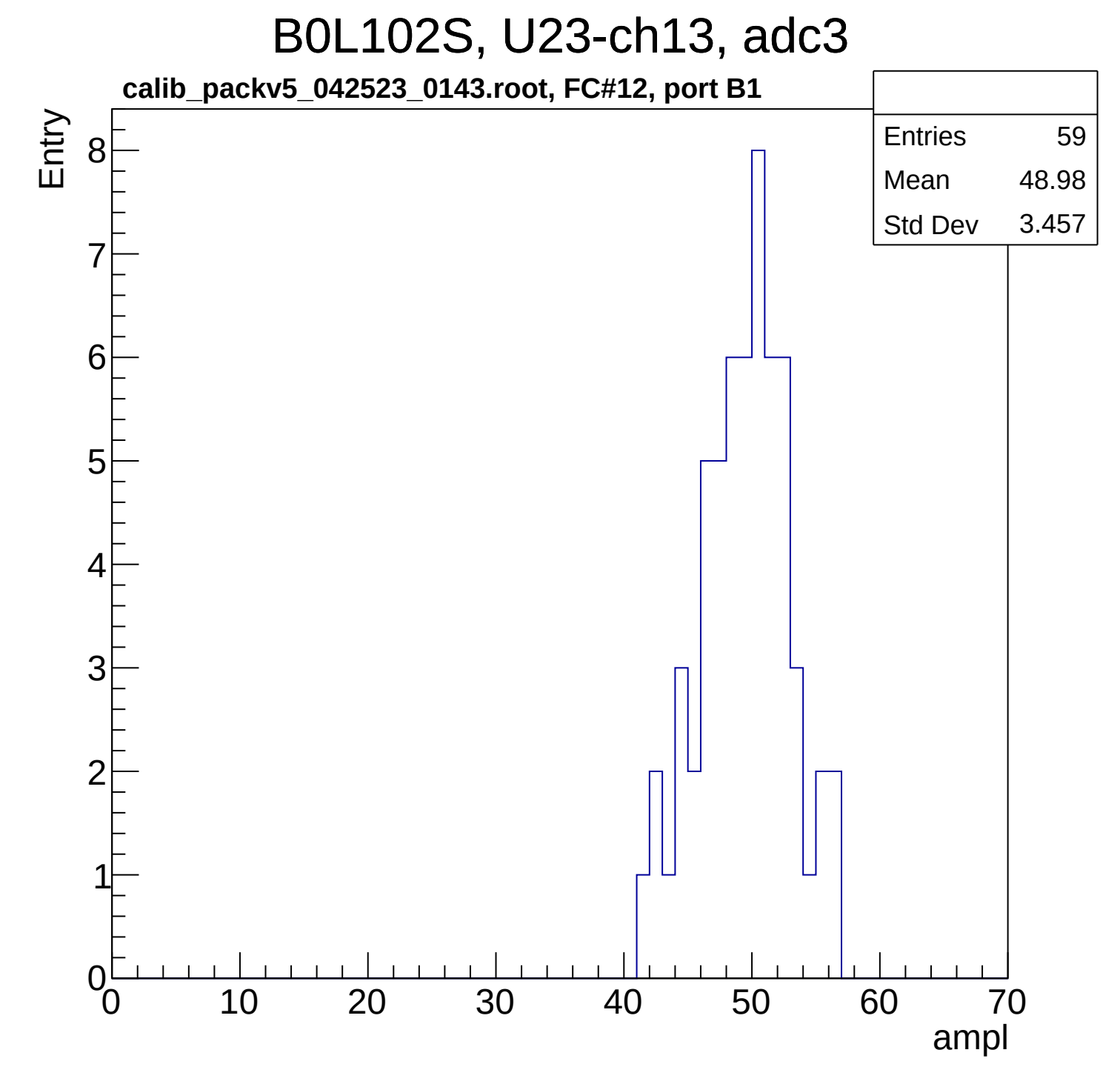
B0L102S, U23-ch13, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.98
Std Dev 3.457
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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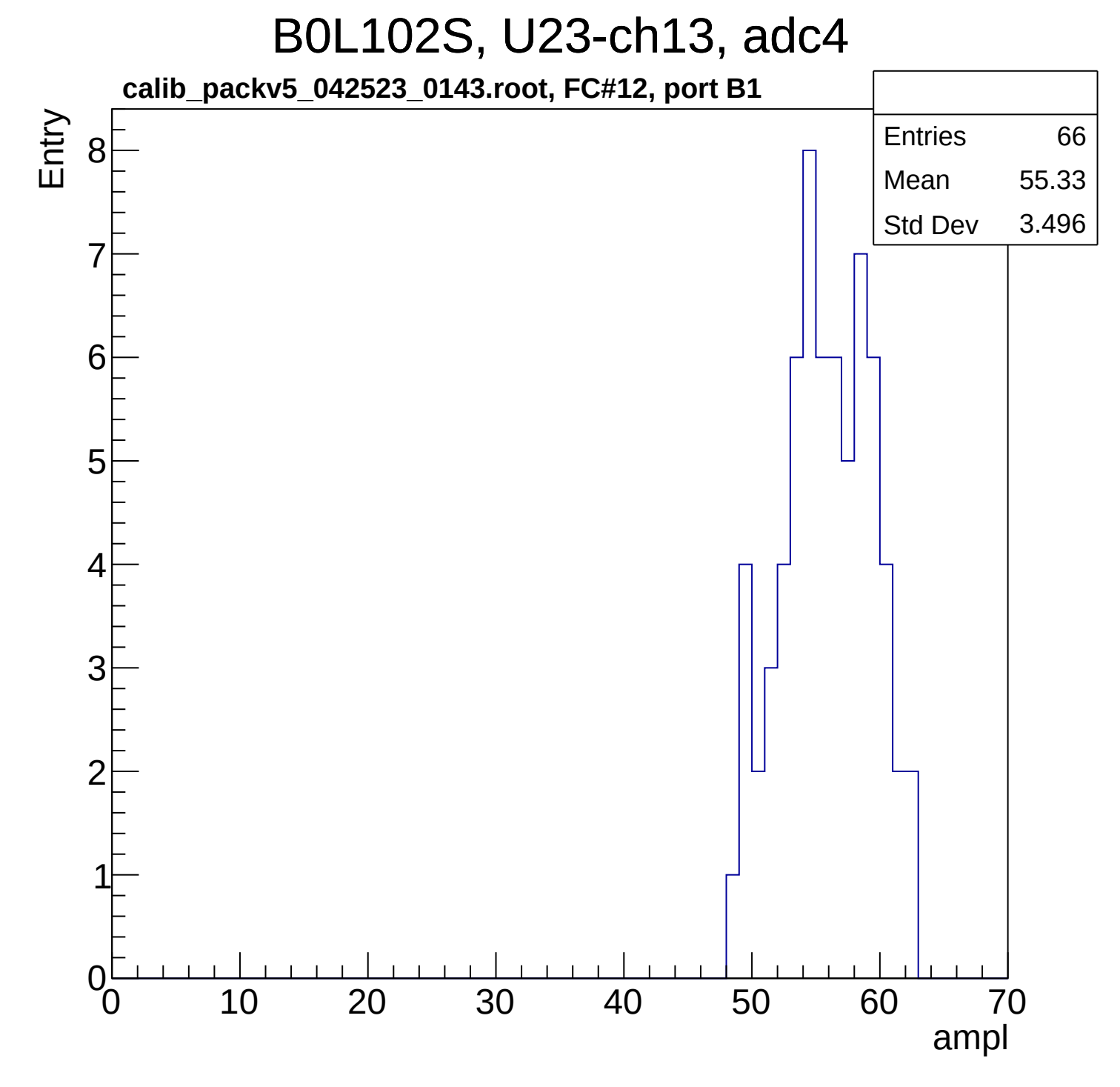
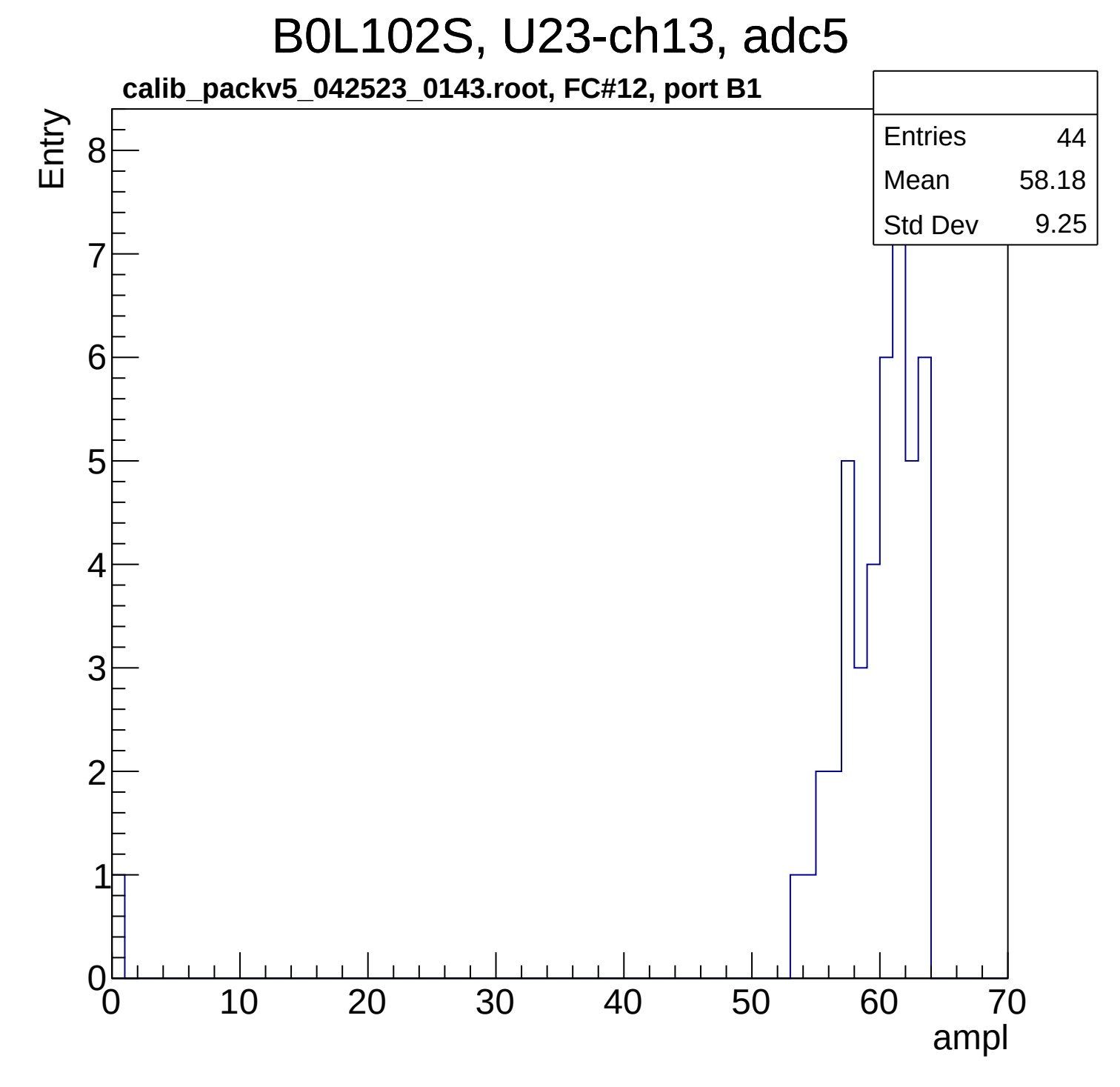

B0L102S, U23-ch13, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 55.33
Std Dev 3.496
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch13, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.18
Std Dev 9.25
Entry
ampl
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1
2
3
4
5
6
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0
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60
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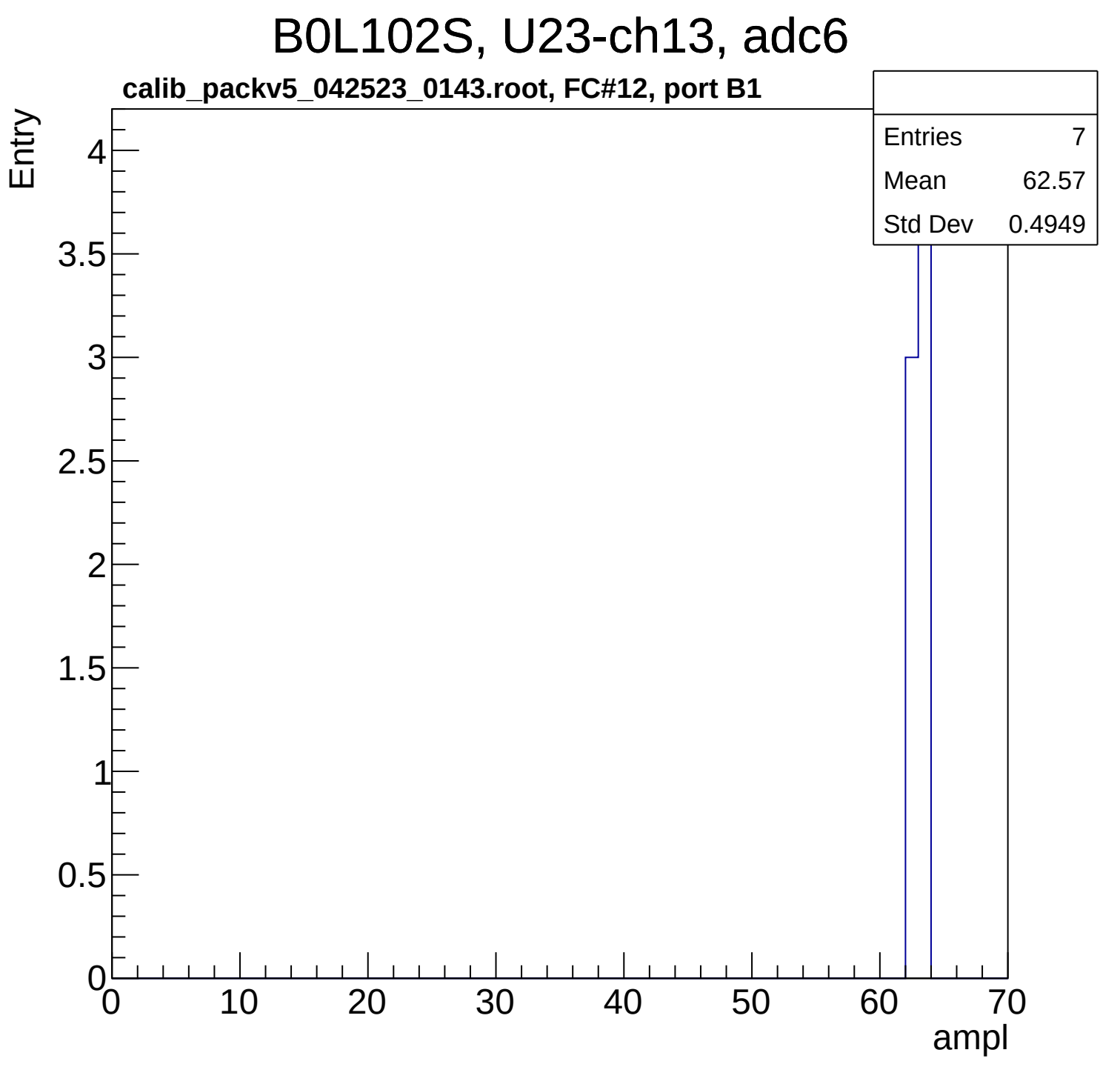
B0L102S, U23-ch13, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
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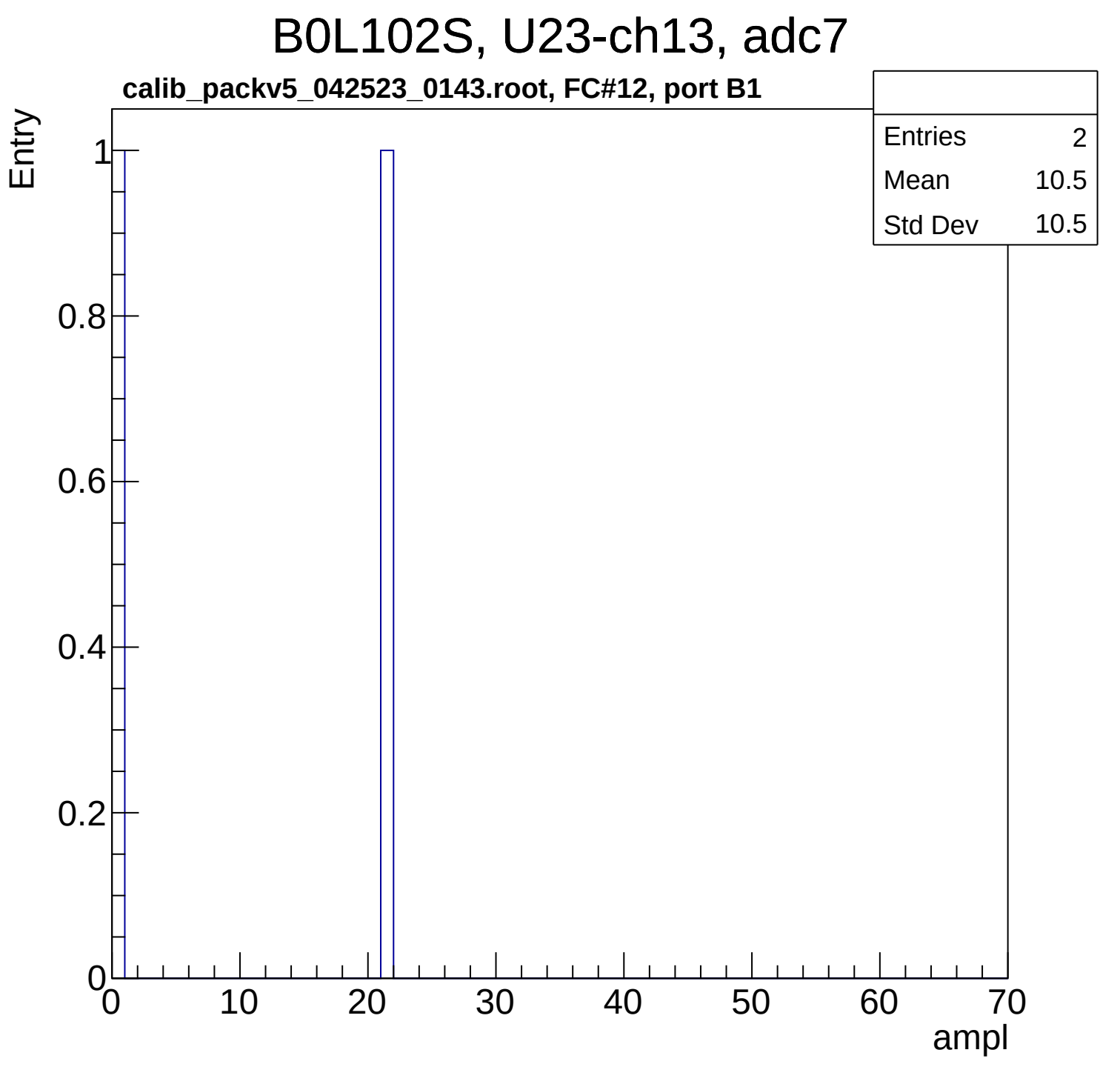
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch13, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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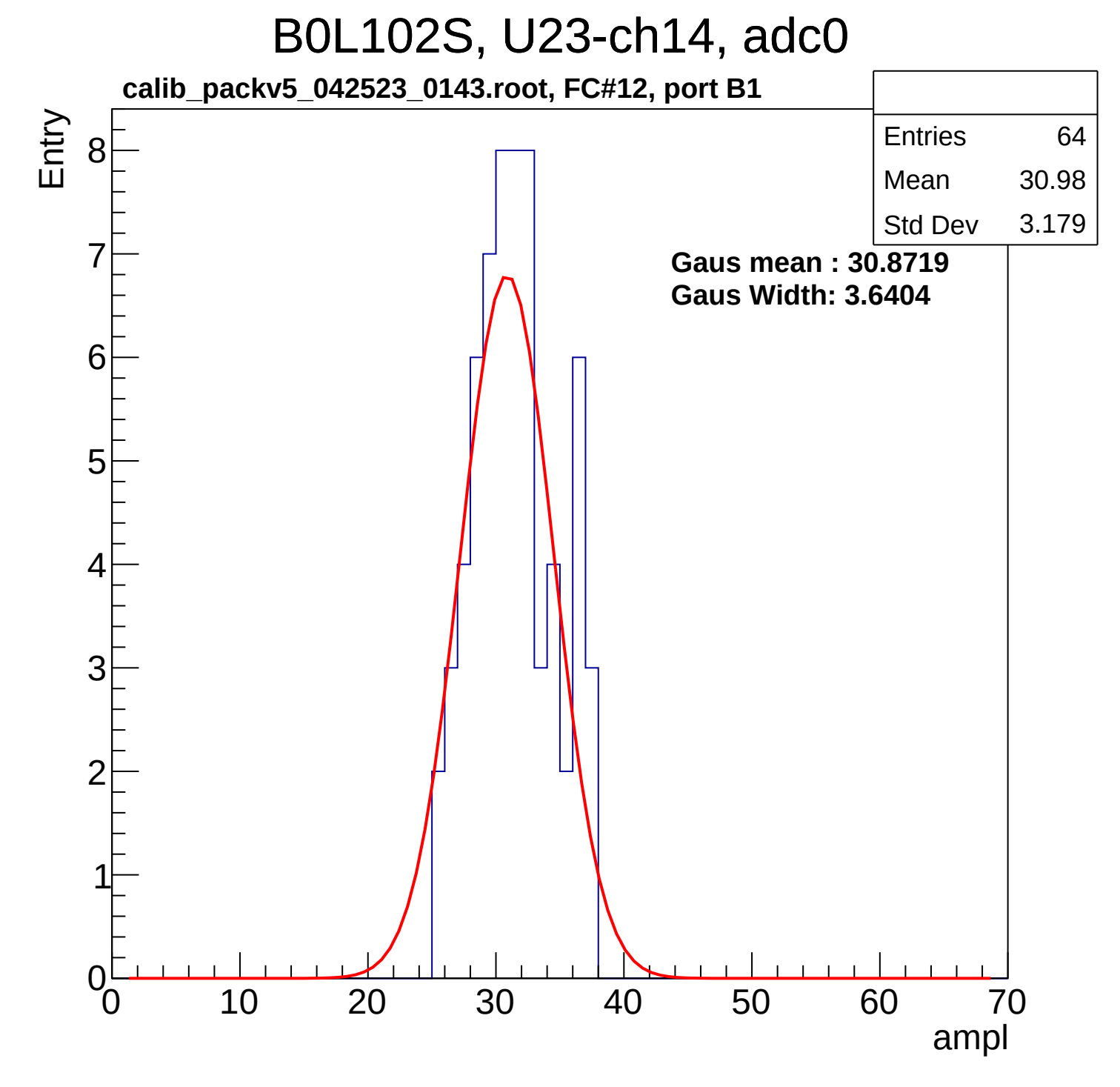
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch14, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 30.98
Std Dev 3.179
Gaus mean : 30.8719
Gaus Width: 3.6404
Entry
ampl
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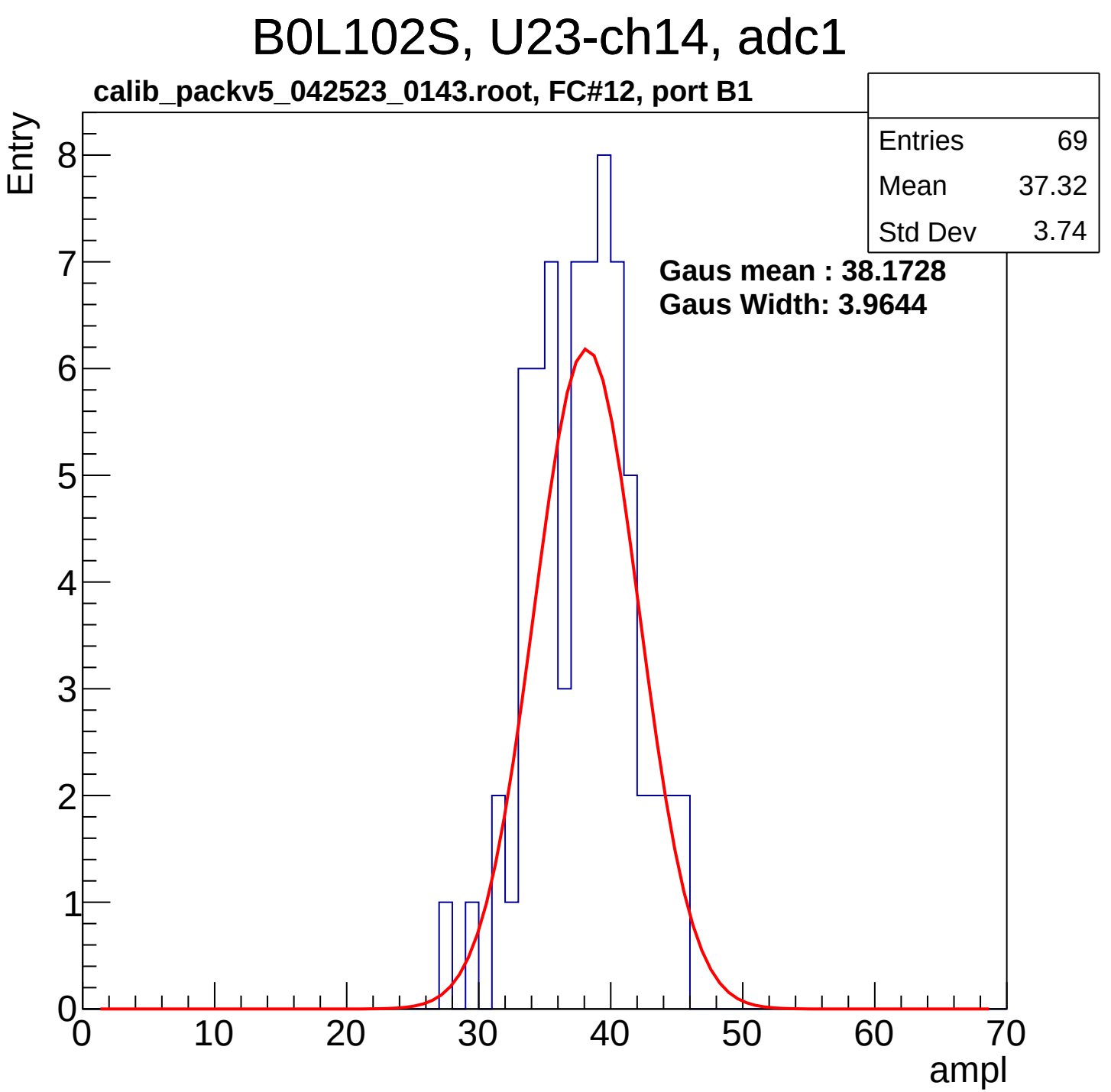
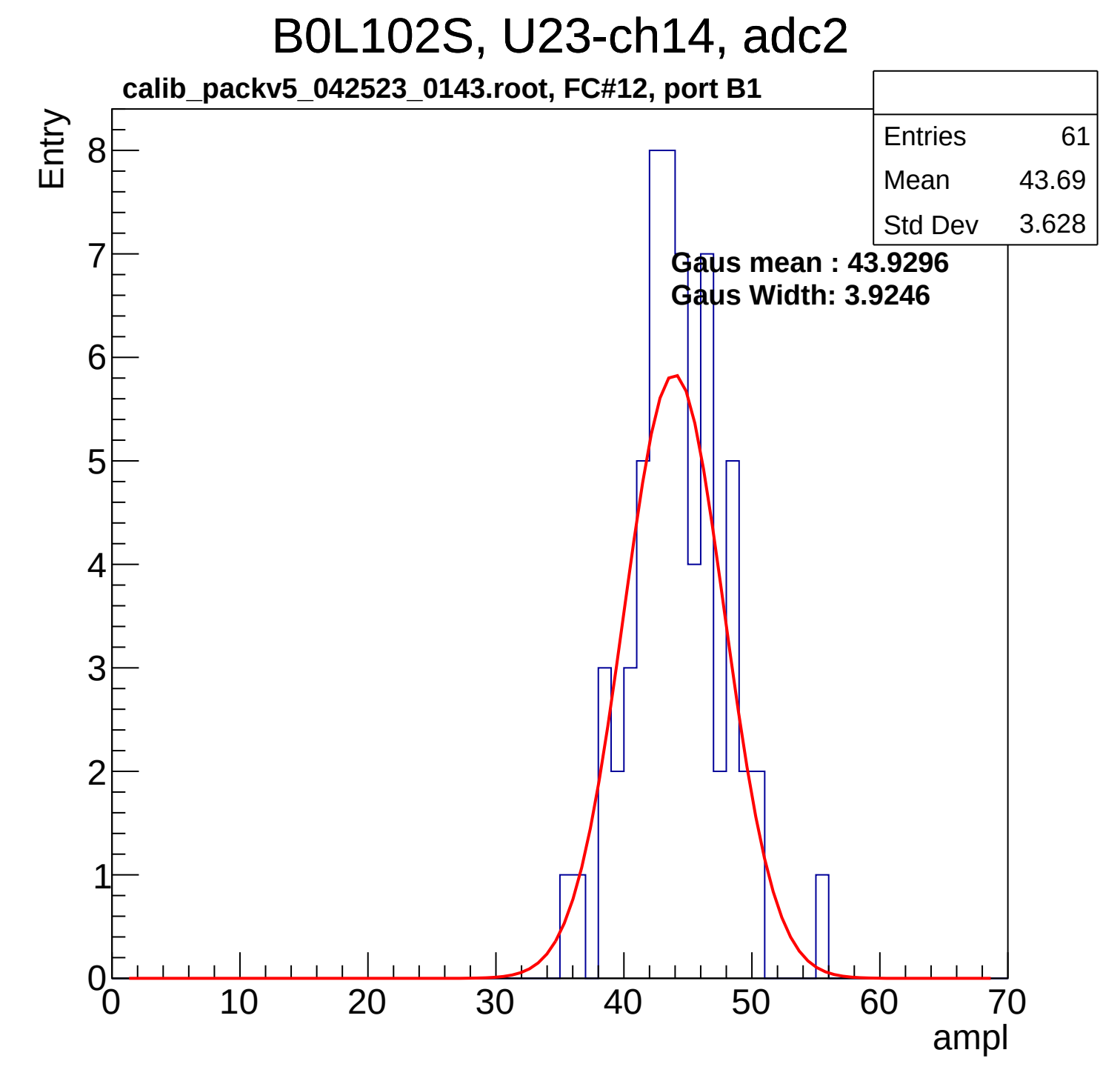
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch14, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 37.32
Std Dev 3.74
Gaus mean : 38.1728
Gaus Width: 3.9644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch14, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 43.69
Std Dev 3.628
Gaus mean : 43.9296
Gaus Width: 3.9246
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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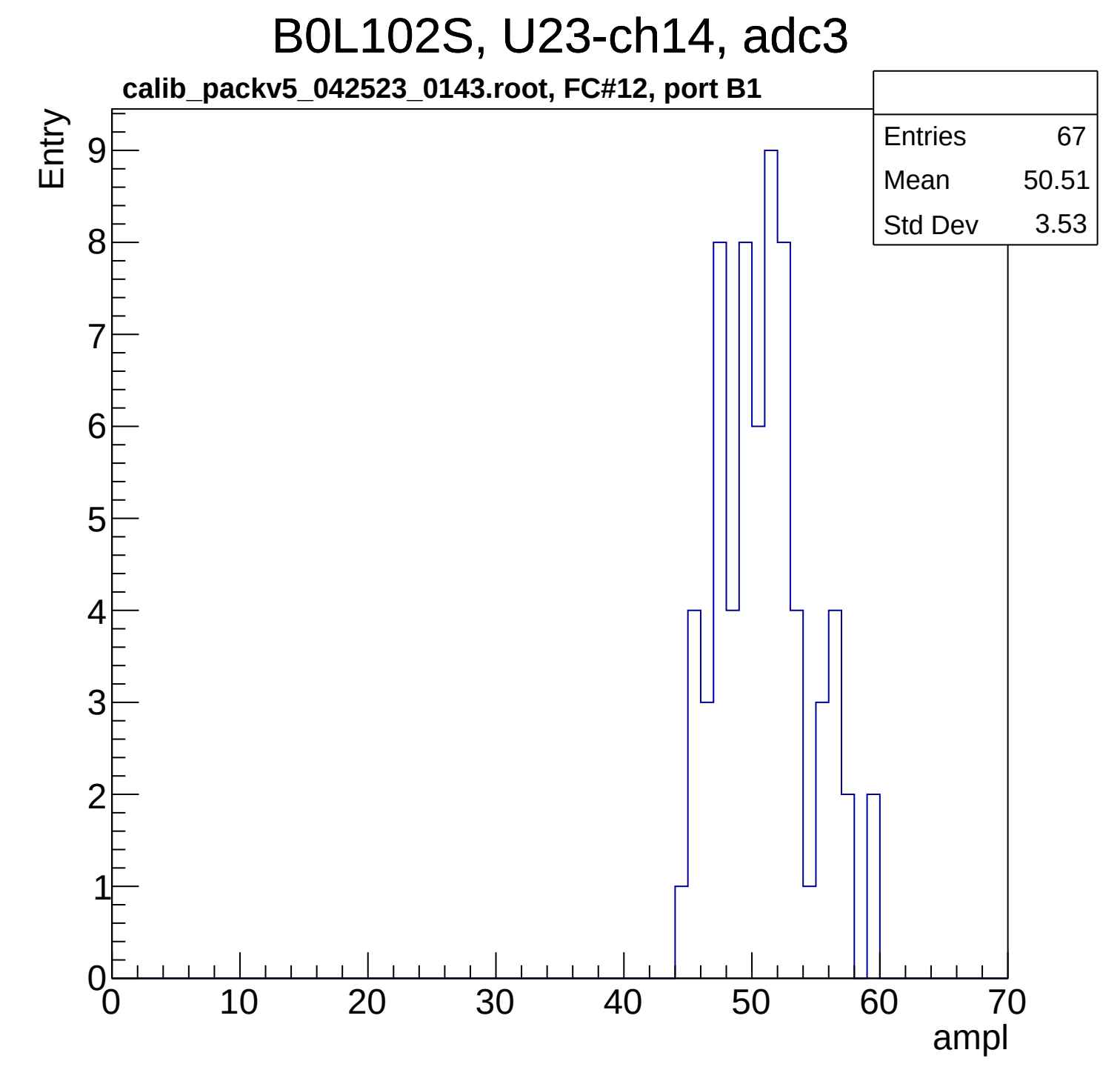
B0L102S, U23-ch14, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.51
Std Dev 3.53
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
50
60
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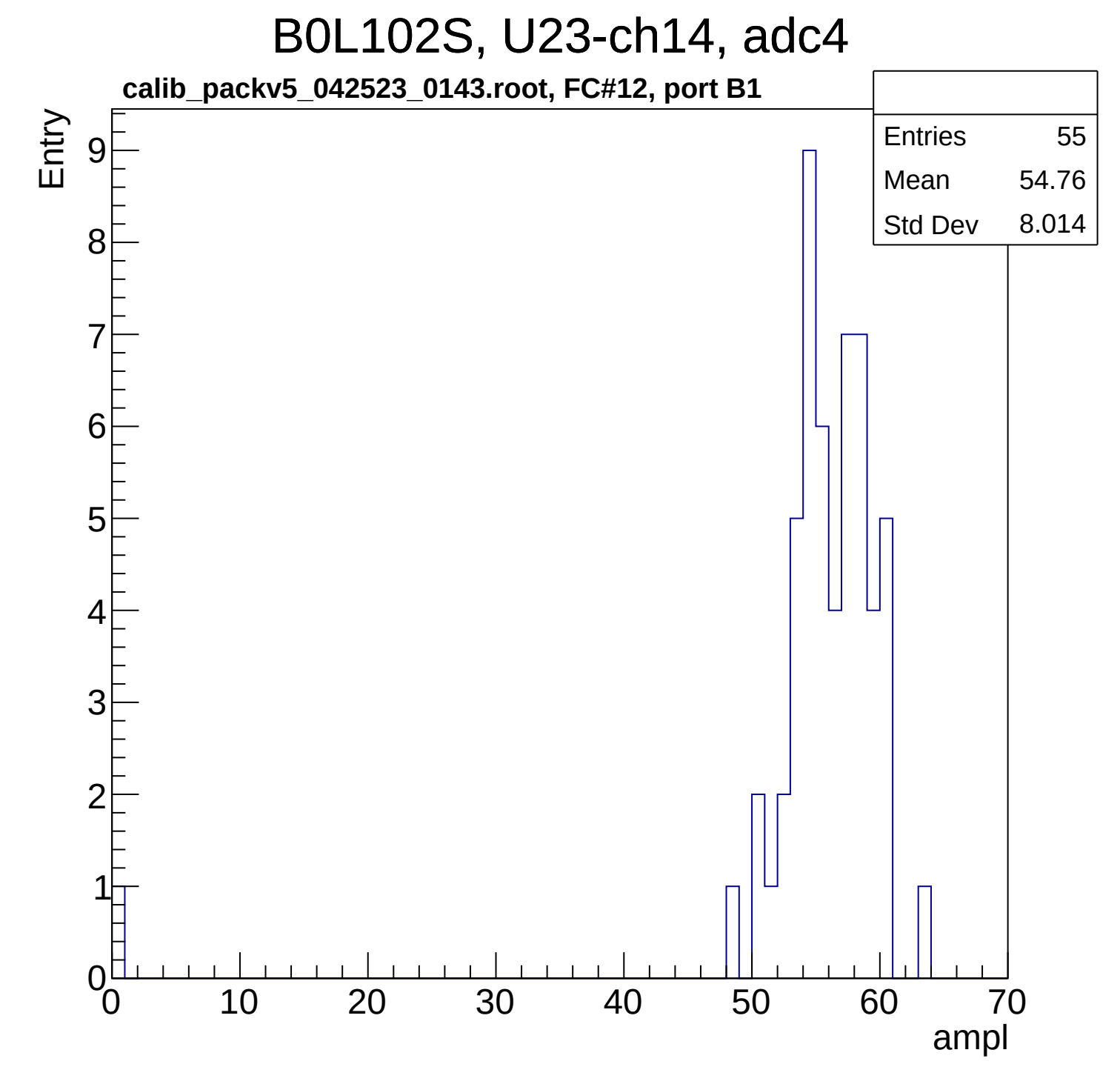
B0L102S, U23-ch14, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 54.76
Std Dev 8.014
Entry
ampl
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6
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0
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40
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60
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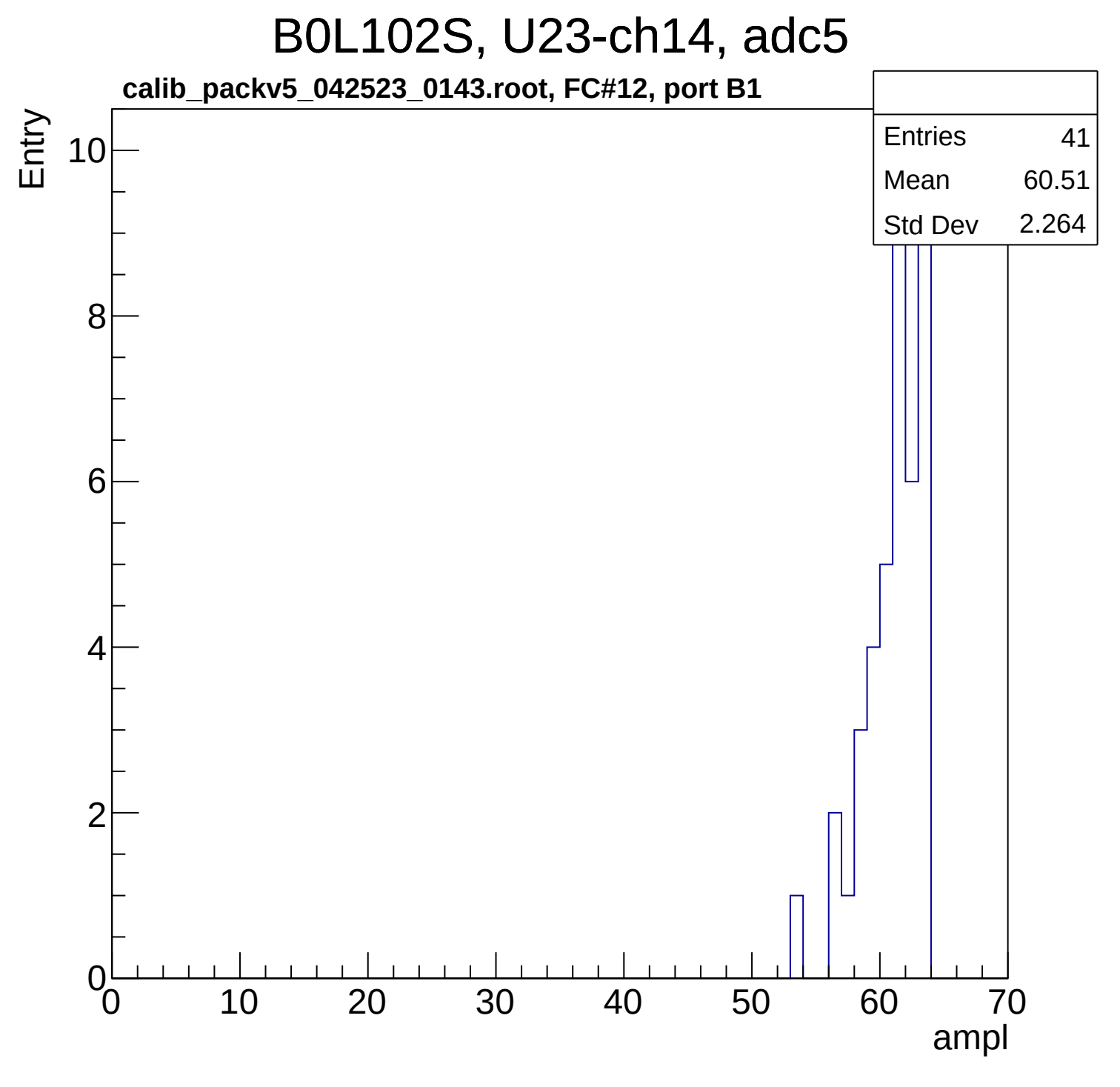
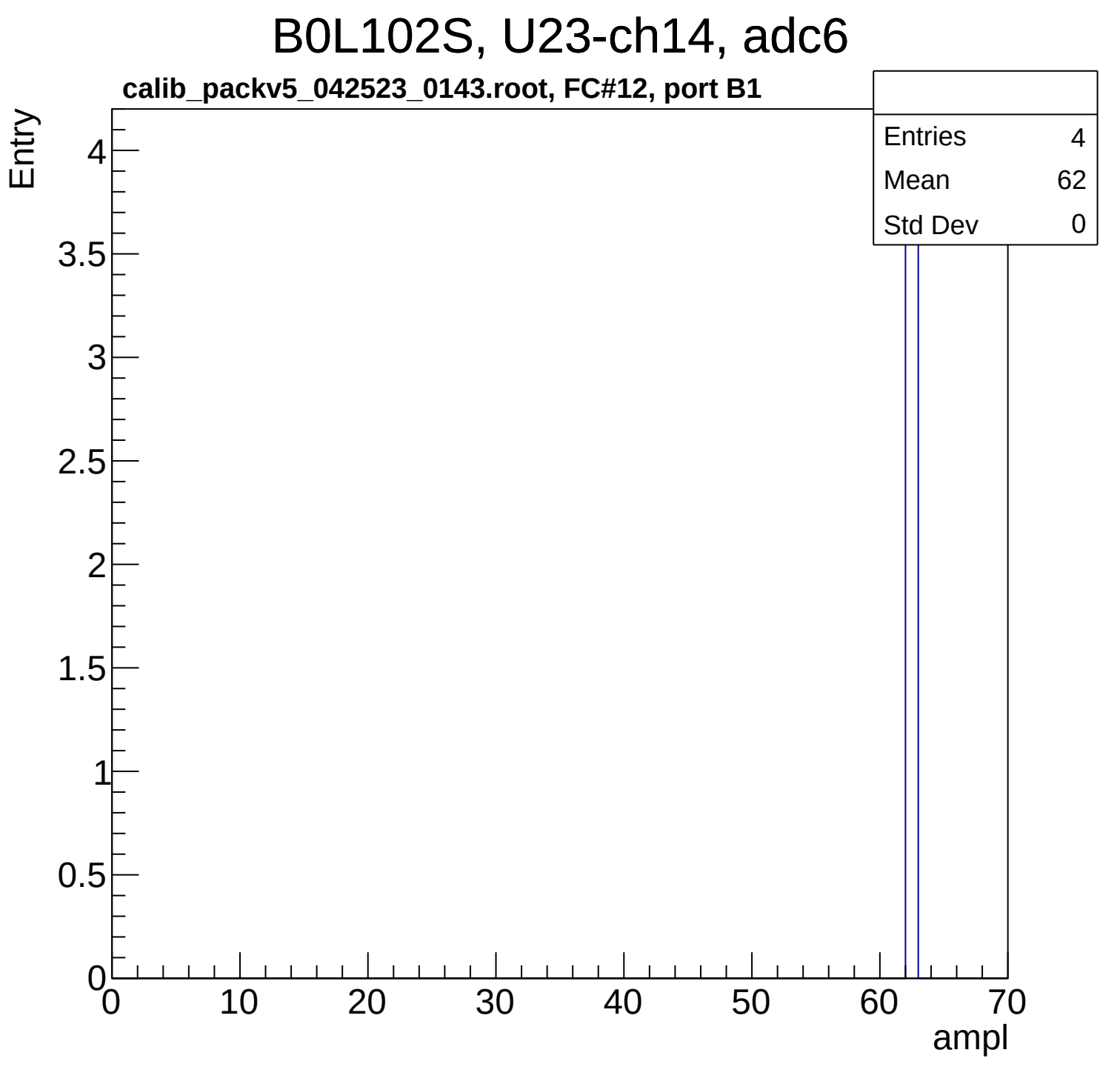

B0L102S, U23-ch14, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 60.51
Std Dev 2.264
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch14, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62
Std Dev 0
Entry
ampl
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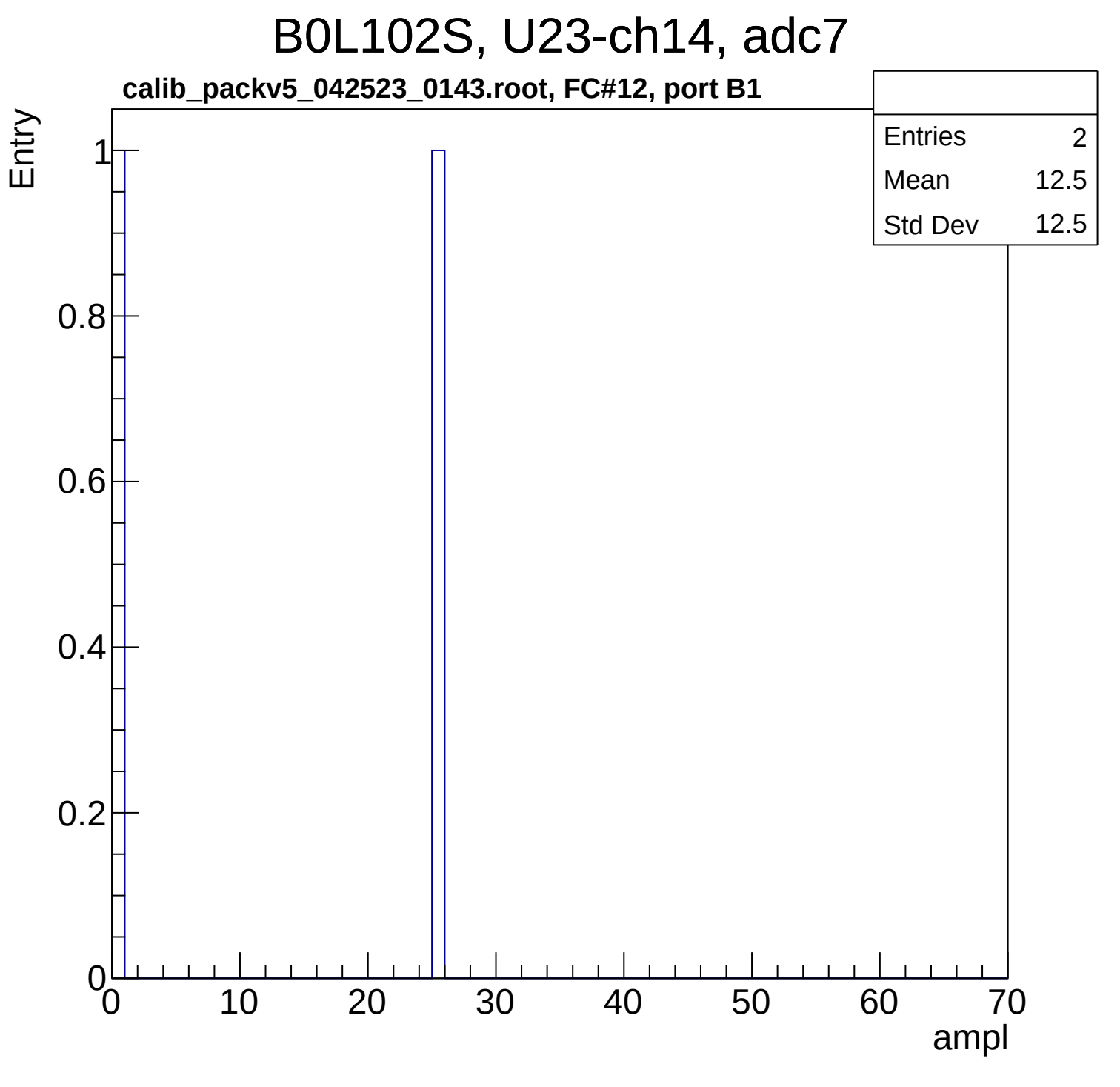
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch14, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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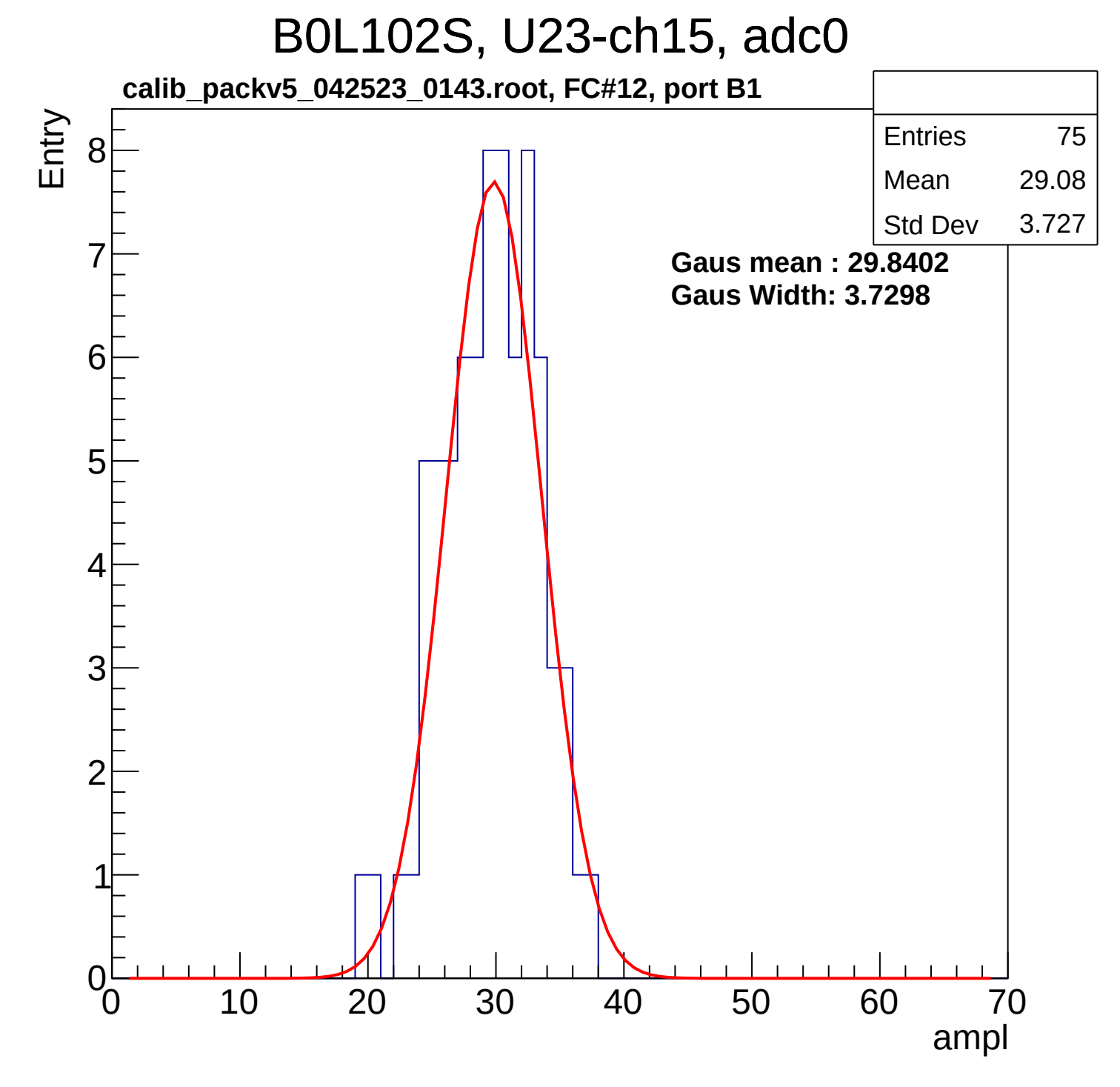
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch15, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 29.08
Std Dev 3.727
Gaus mean : 29.8402
Gaus Width: 3.7298
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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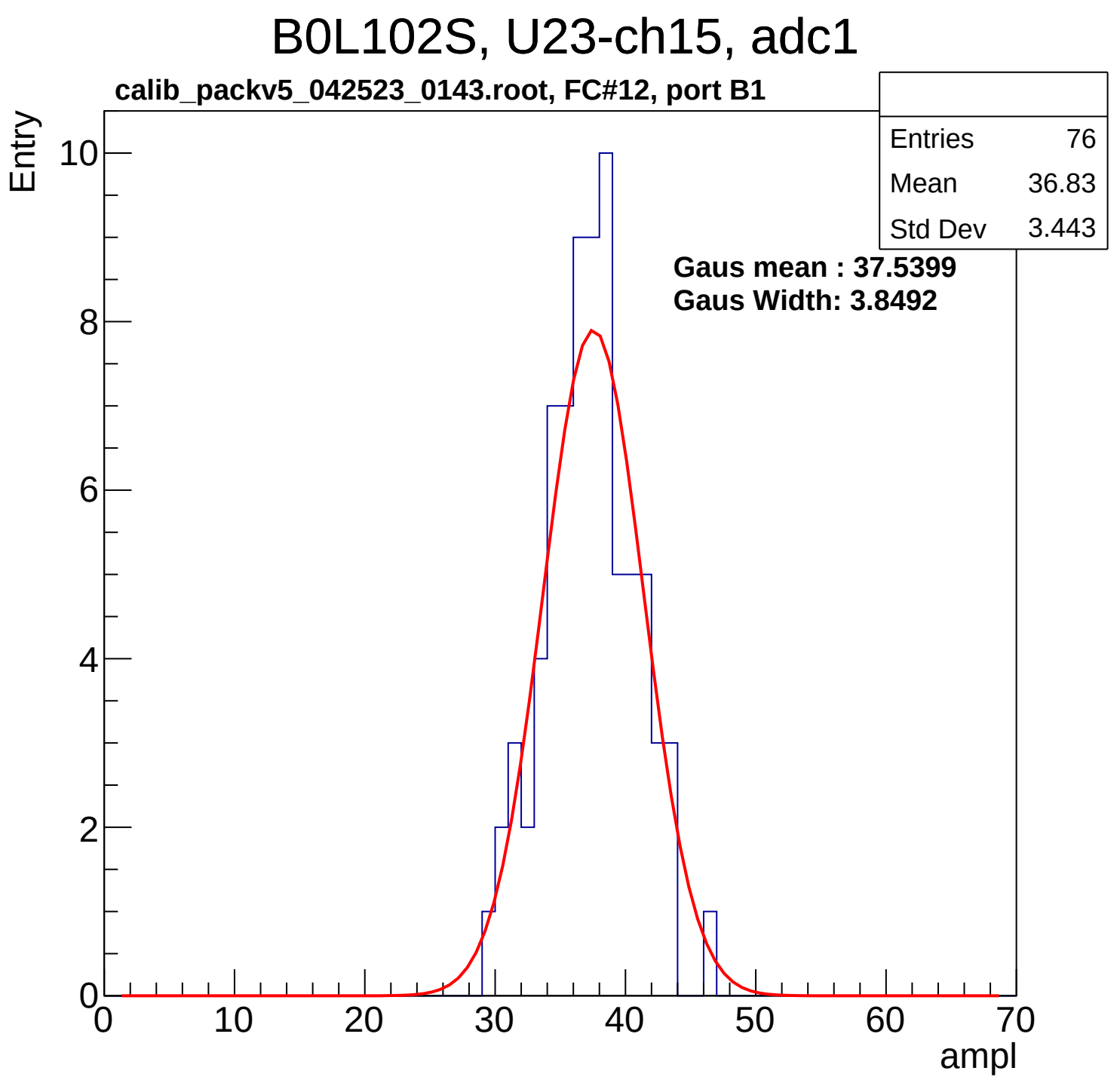
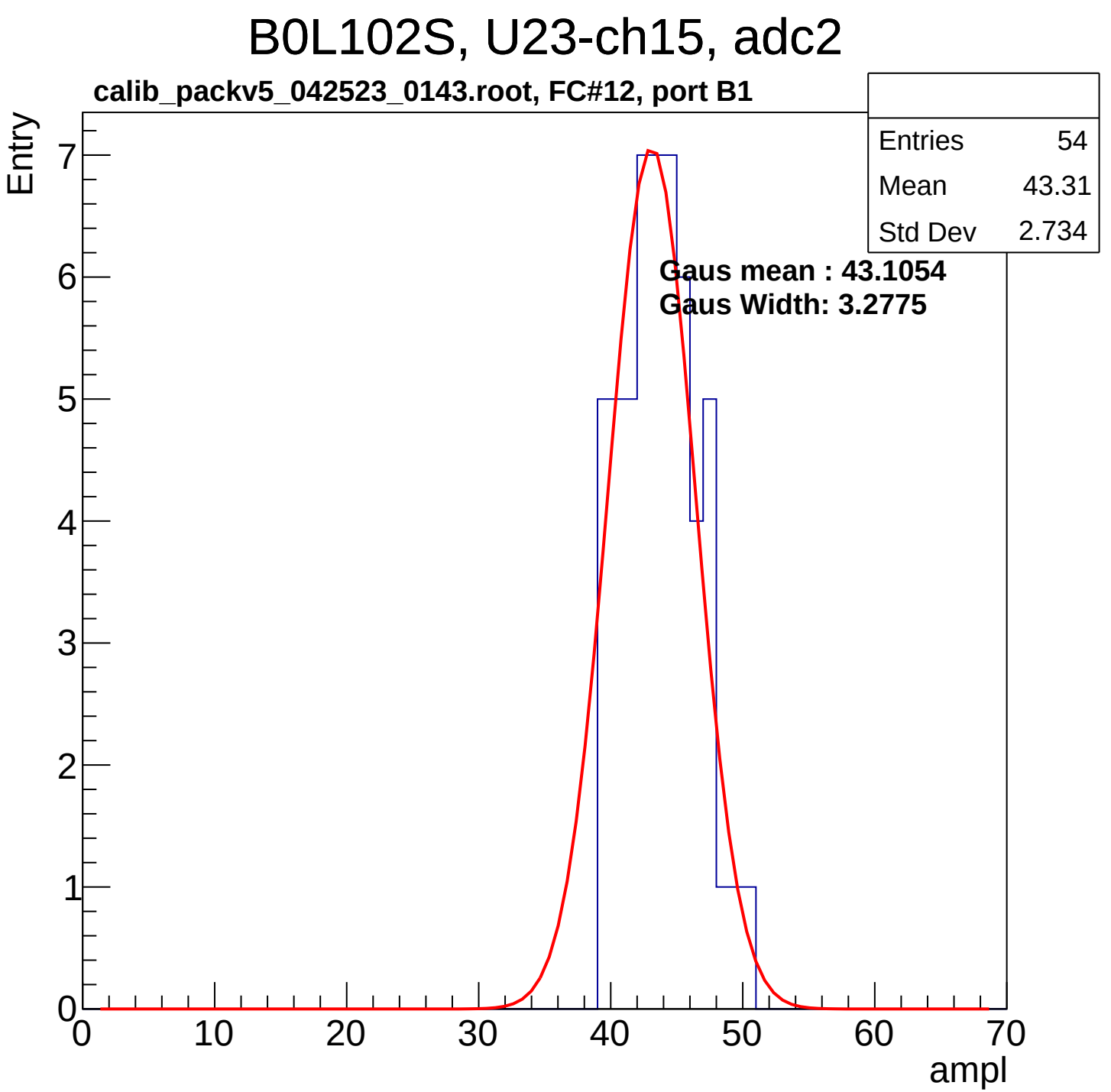

B0L102S, U23-ch15, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 36.83
Std Dev 3.443
Gaus mean : 37.5399
Gaus Width: 3.8492
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch15, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43.31
Std Dev 2.734
Gaus mean : 43.1054
Gaus Width: 3.2775
Entry
ampl
0
1
2
3
4
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0
10
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60
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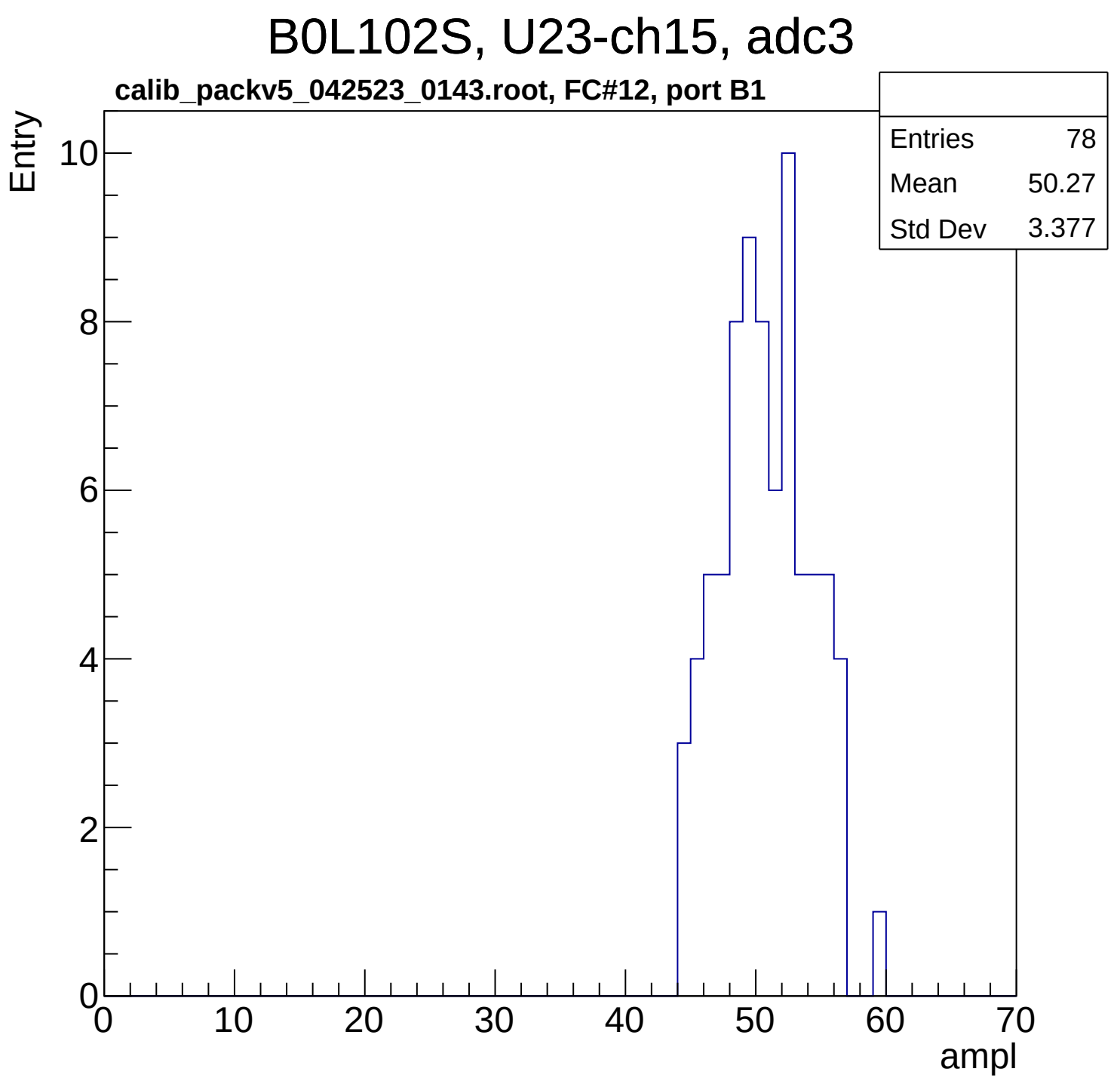
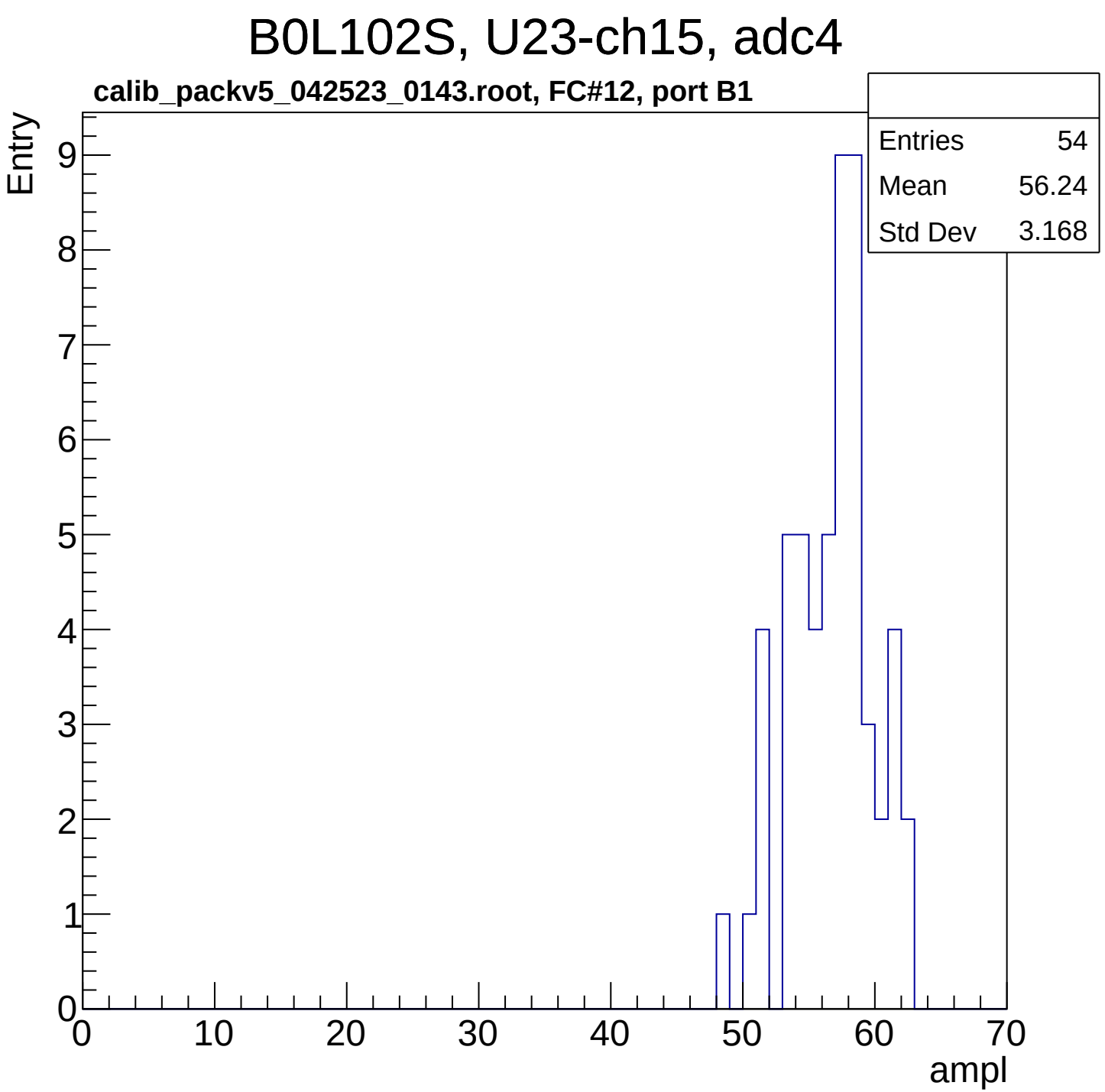

B0L102S, U23-ch15, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 50.27
Std Dev 3.377
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch15, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 56.24
Std Dev 3.168
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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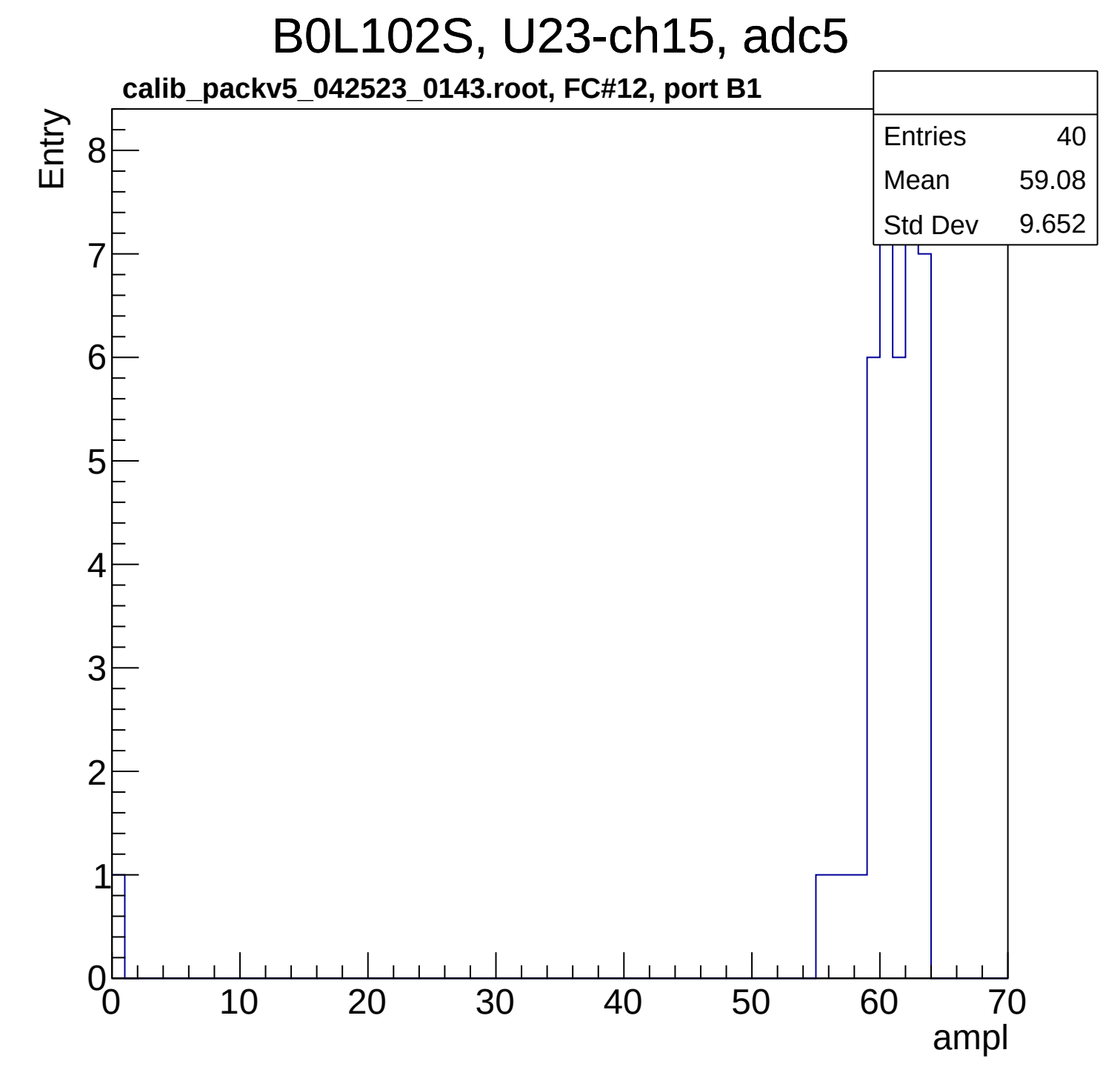
60
70


B0L102S, U23-ch15, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 59.08
Std Dev 9.652
Entry
0
1
2
3
4
5
6
7
8
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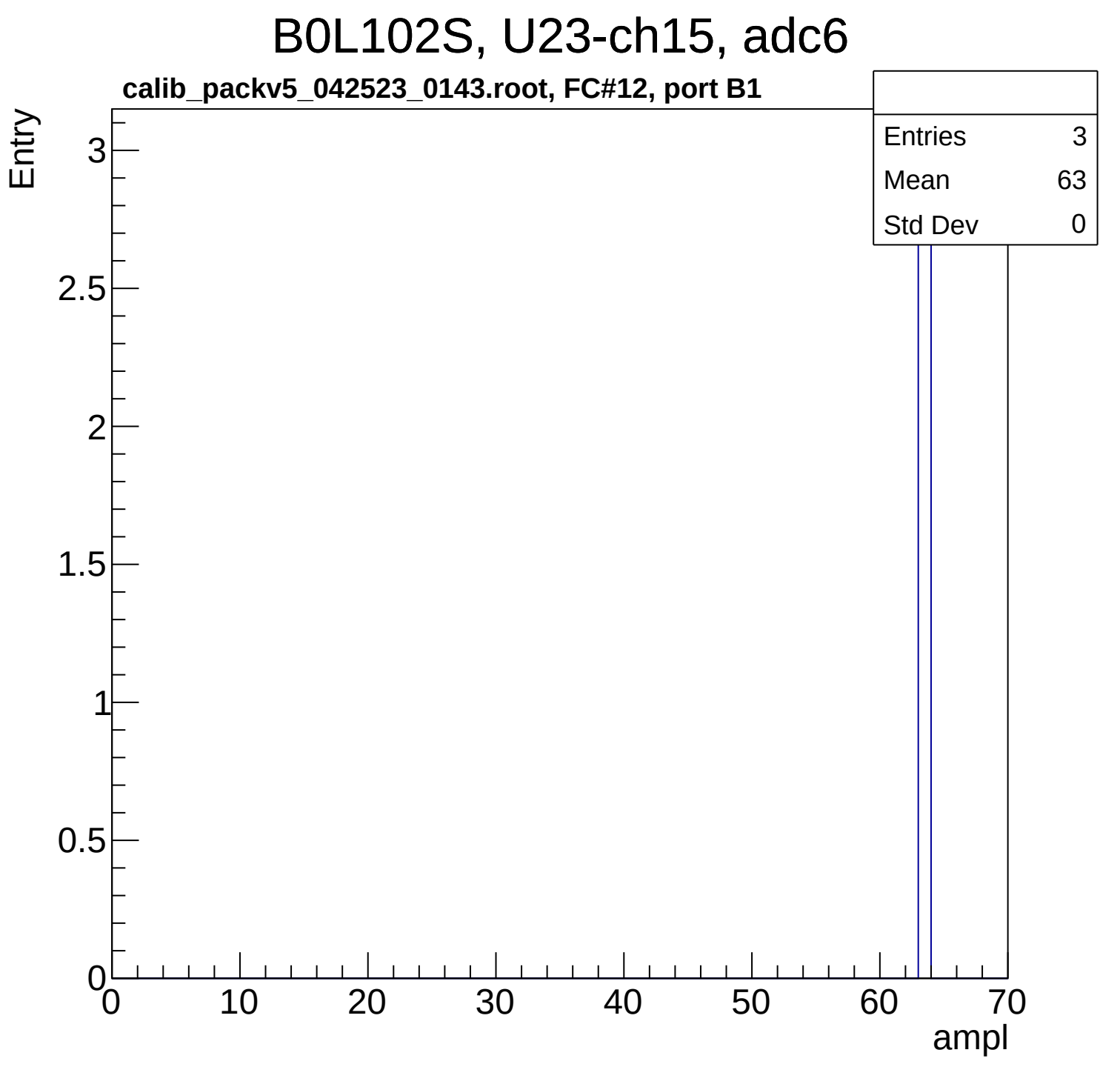
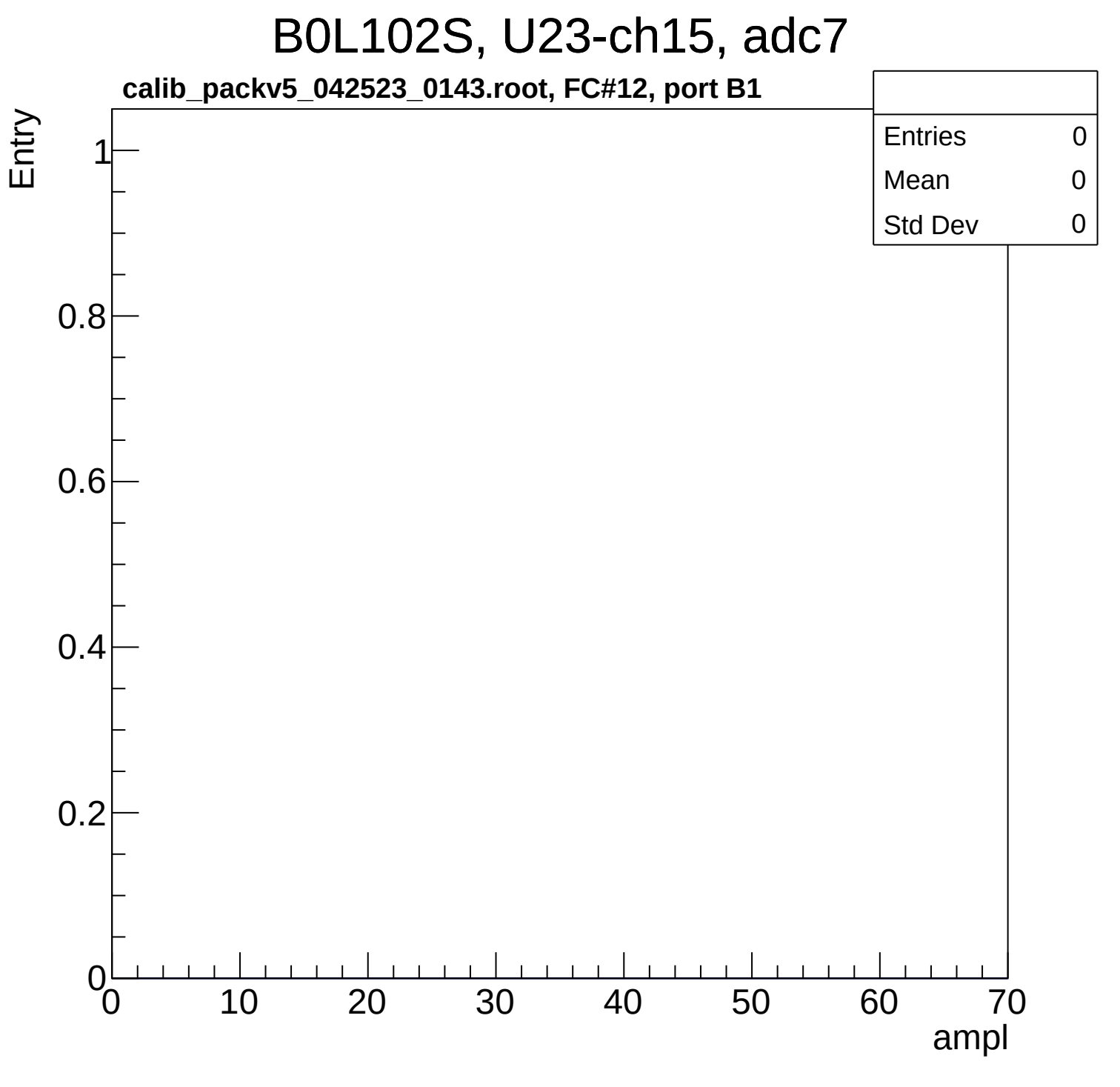
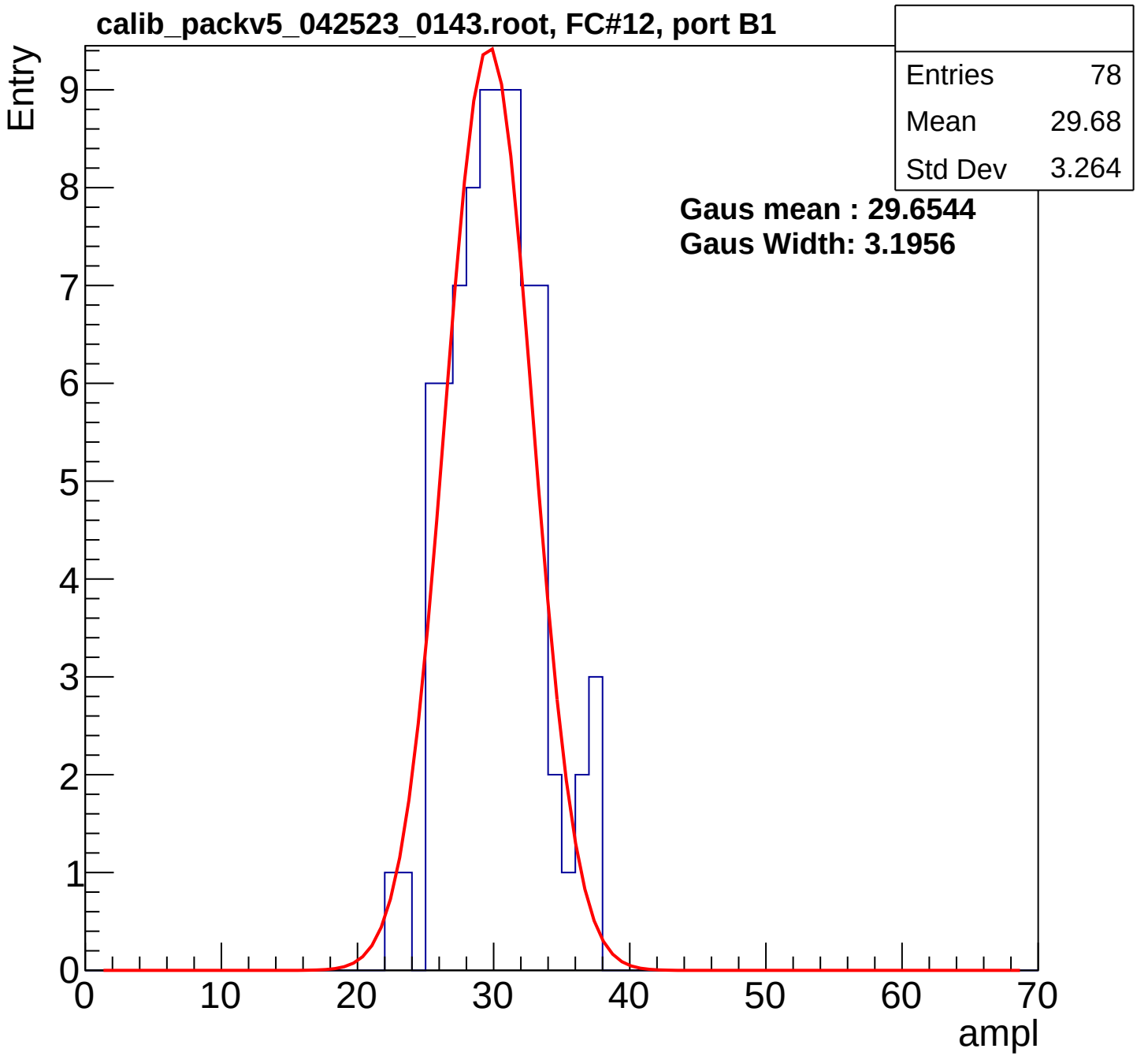
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch15, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch15, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch16, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 29.68
Std Dev 3.264
Gaus mean : 29.6544
Gaus Width: 3.1956
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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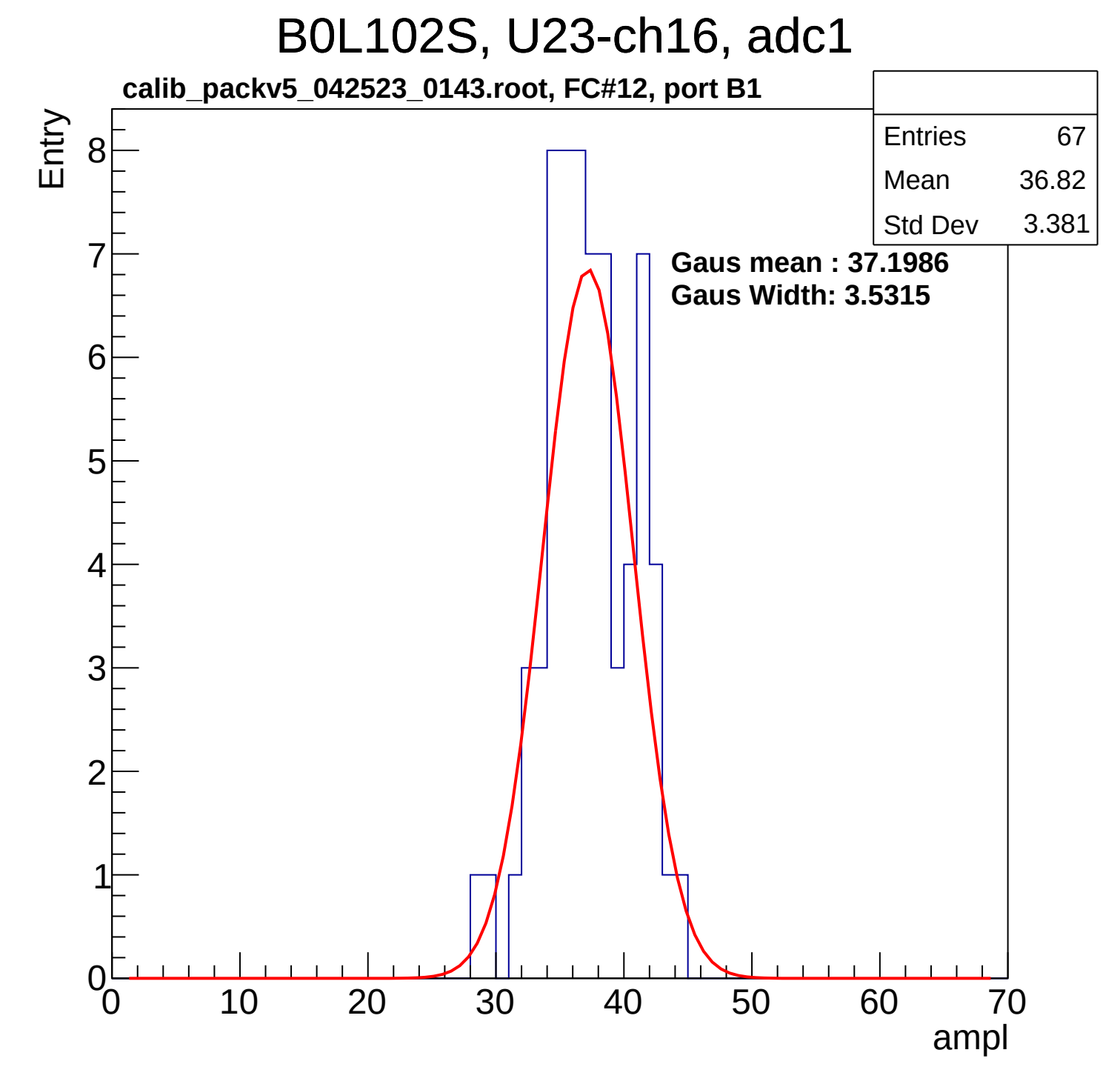
60
70


B0L102S, U23-ch16, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 36.82
Std Dev 3.381
Gaus mean : 37.1986
Gaus Width: 3.5315
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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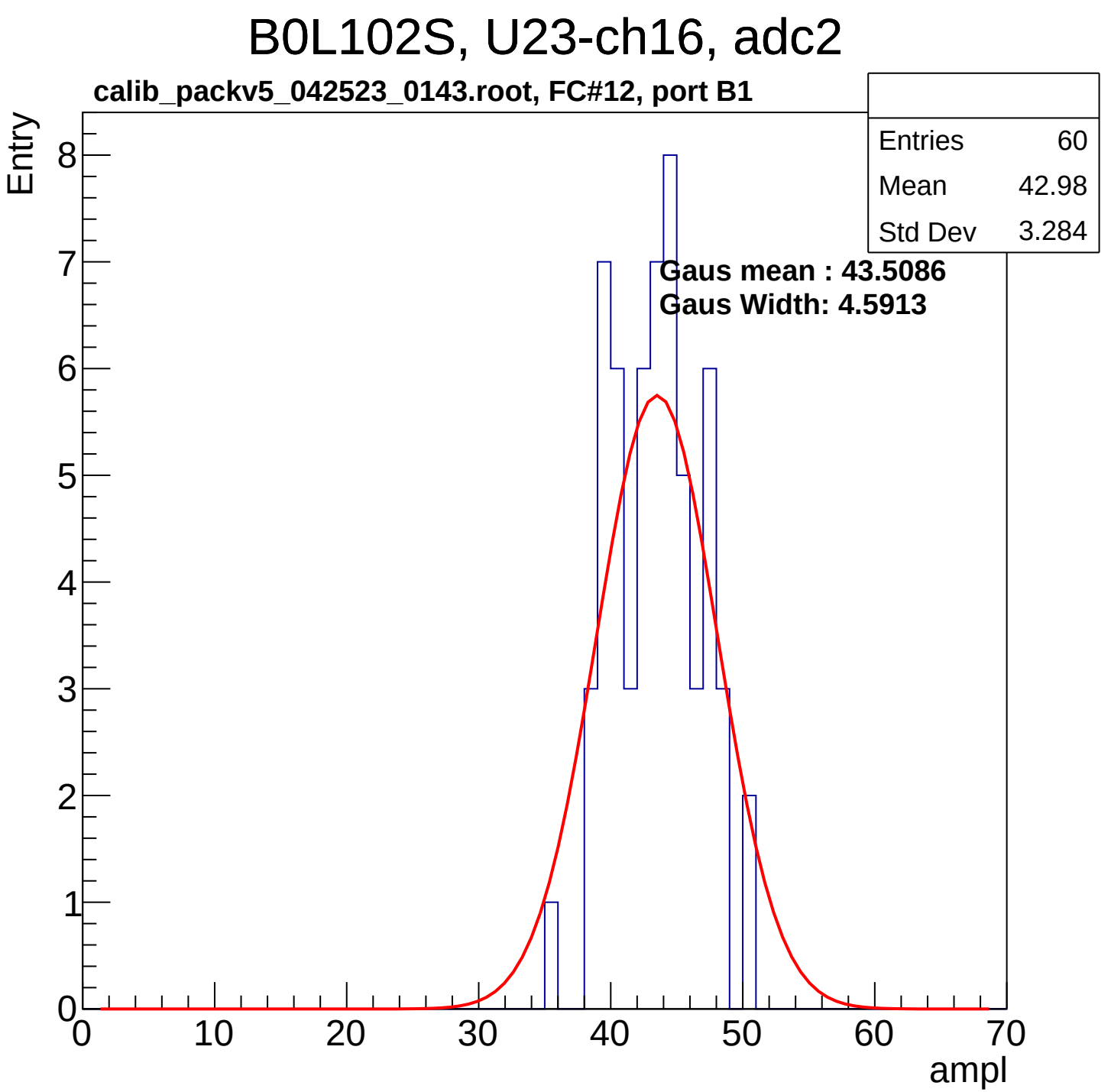
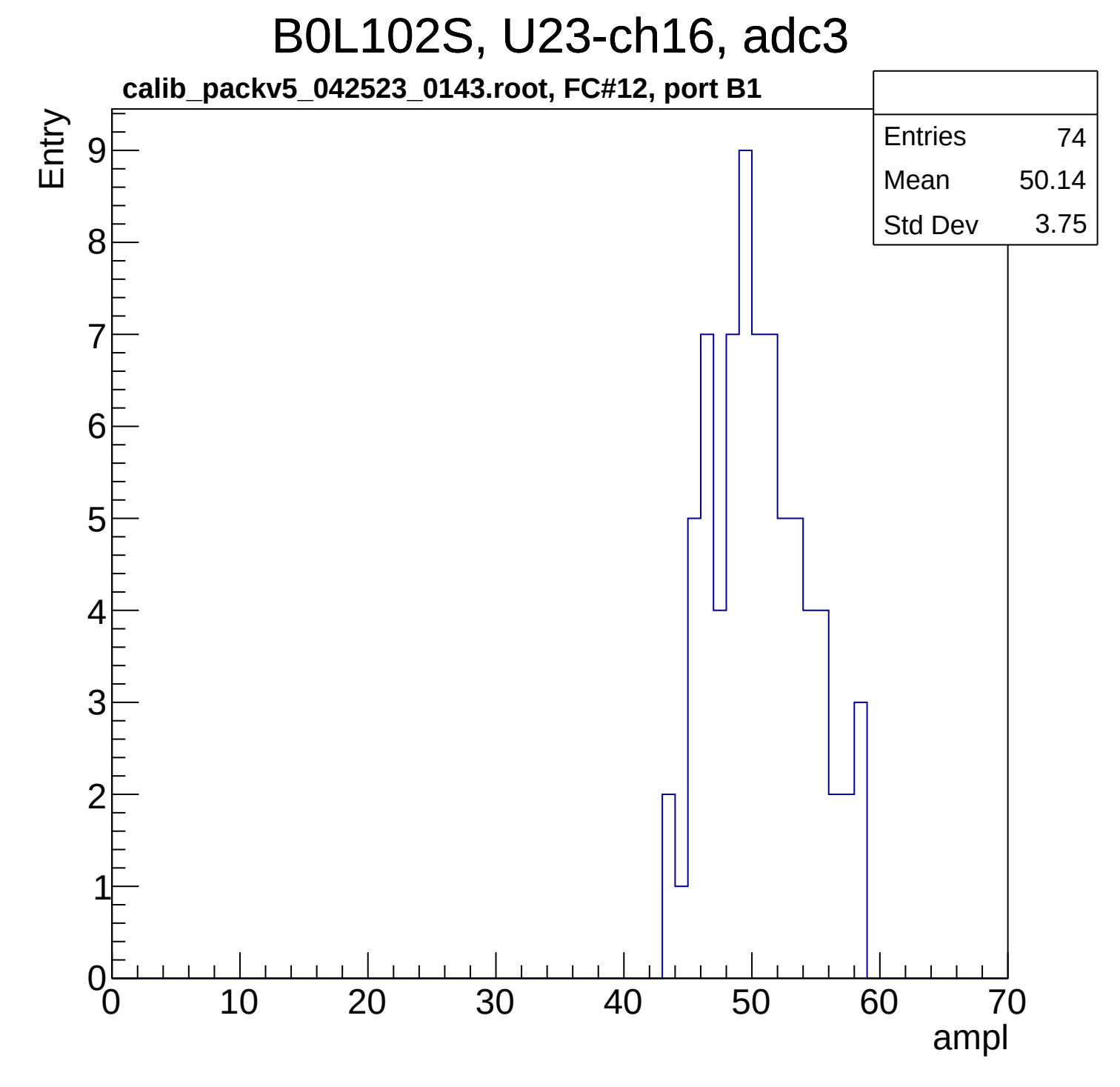
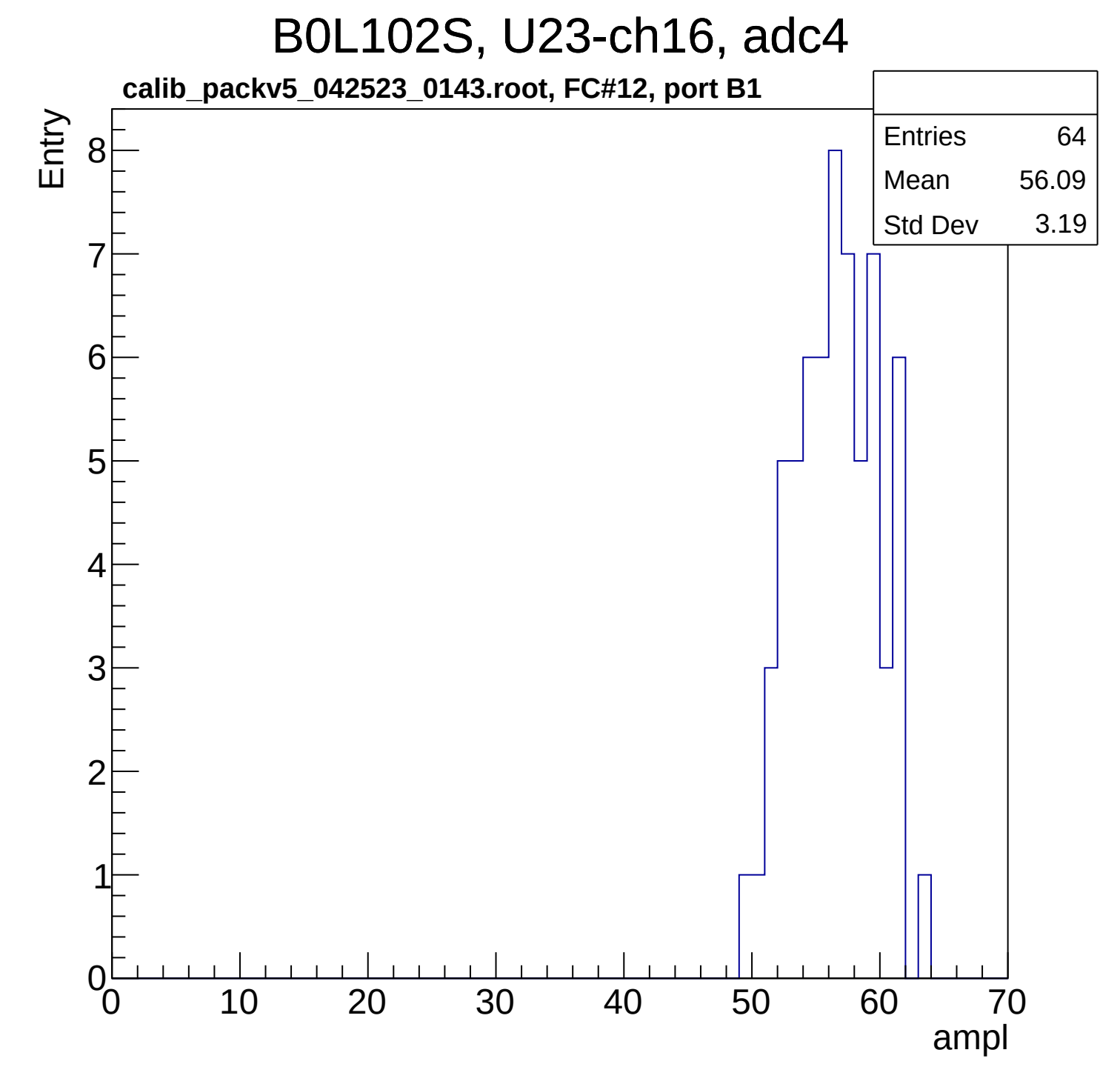
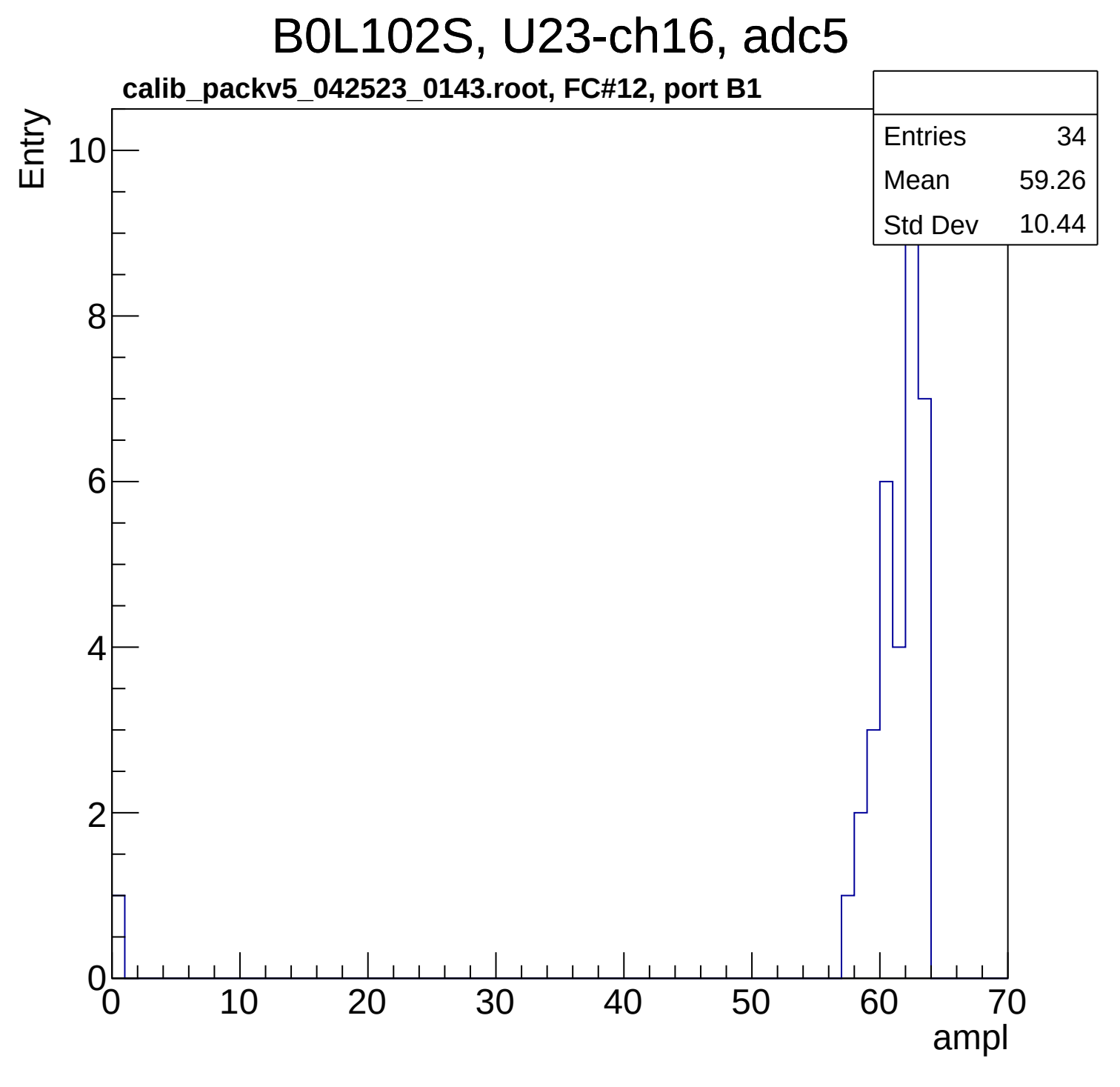
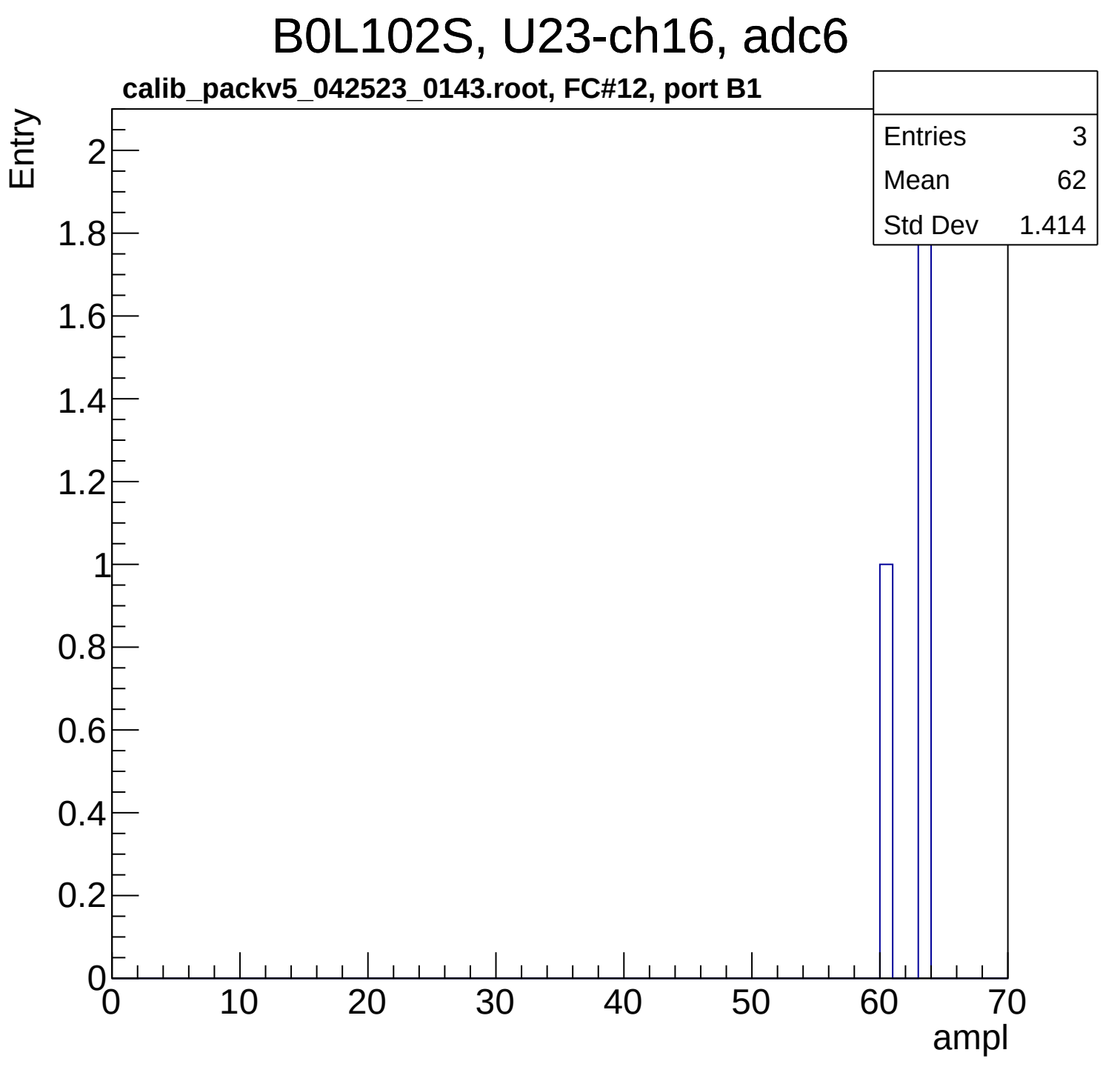
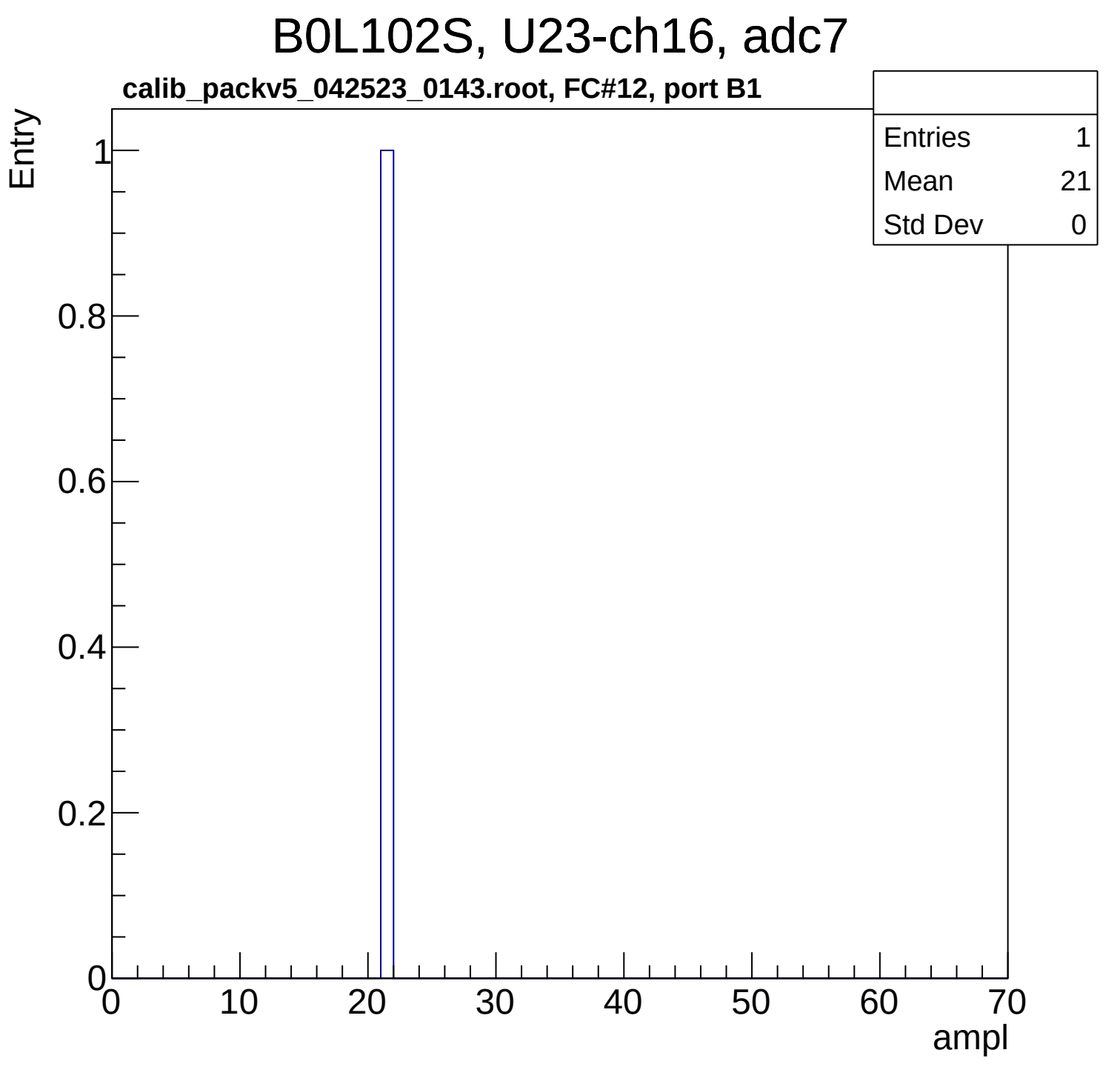

B0L102S, U23-ch16, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 42.98
Std Dev 3.284
Gaus mean : 43.5086
Gaus Width: 4.5913
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

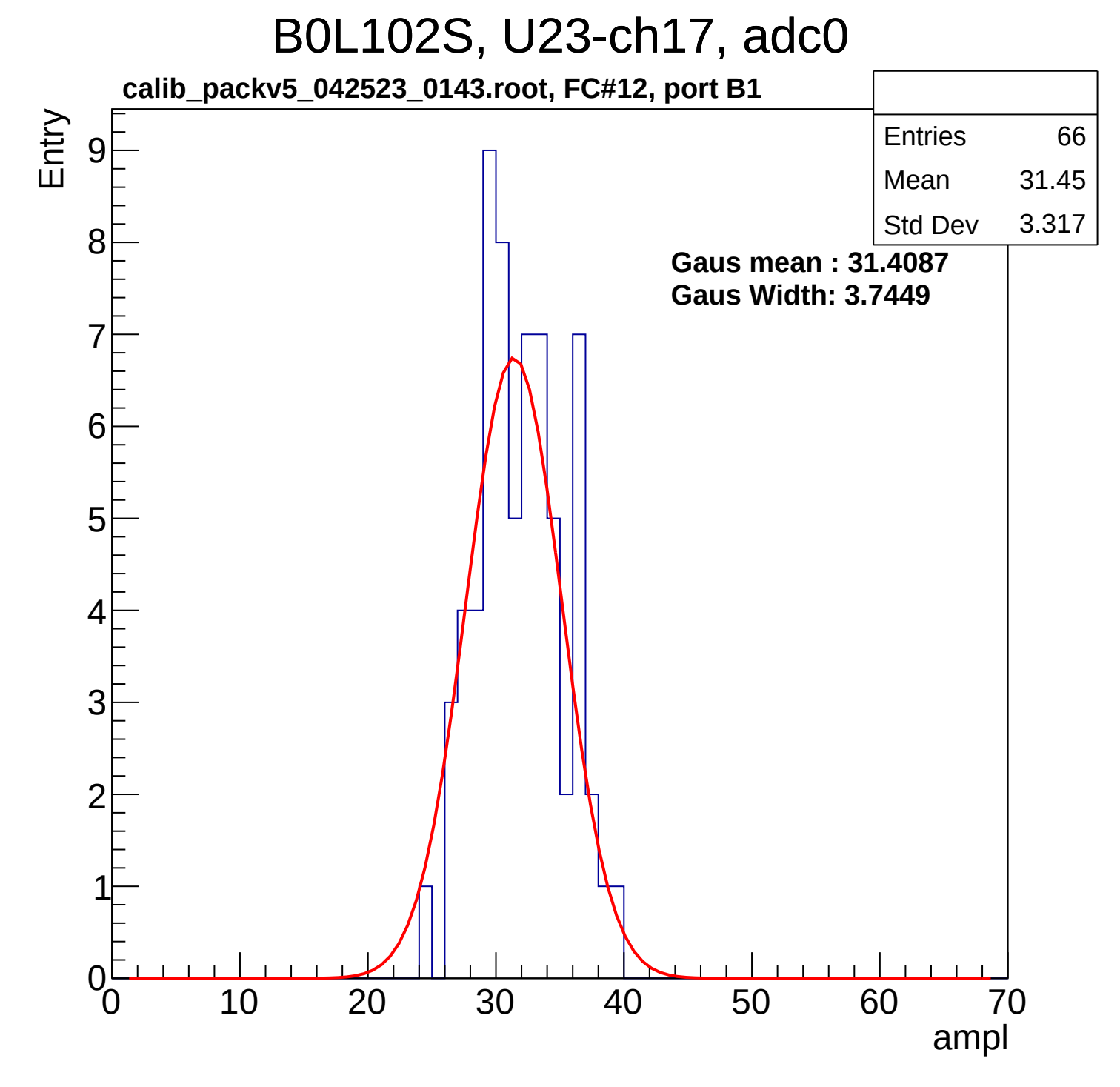

B0L102S, U23-ch16, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 50.14
Std Dev 3.75
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

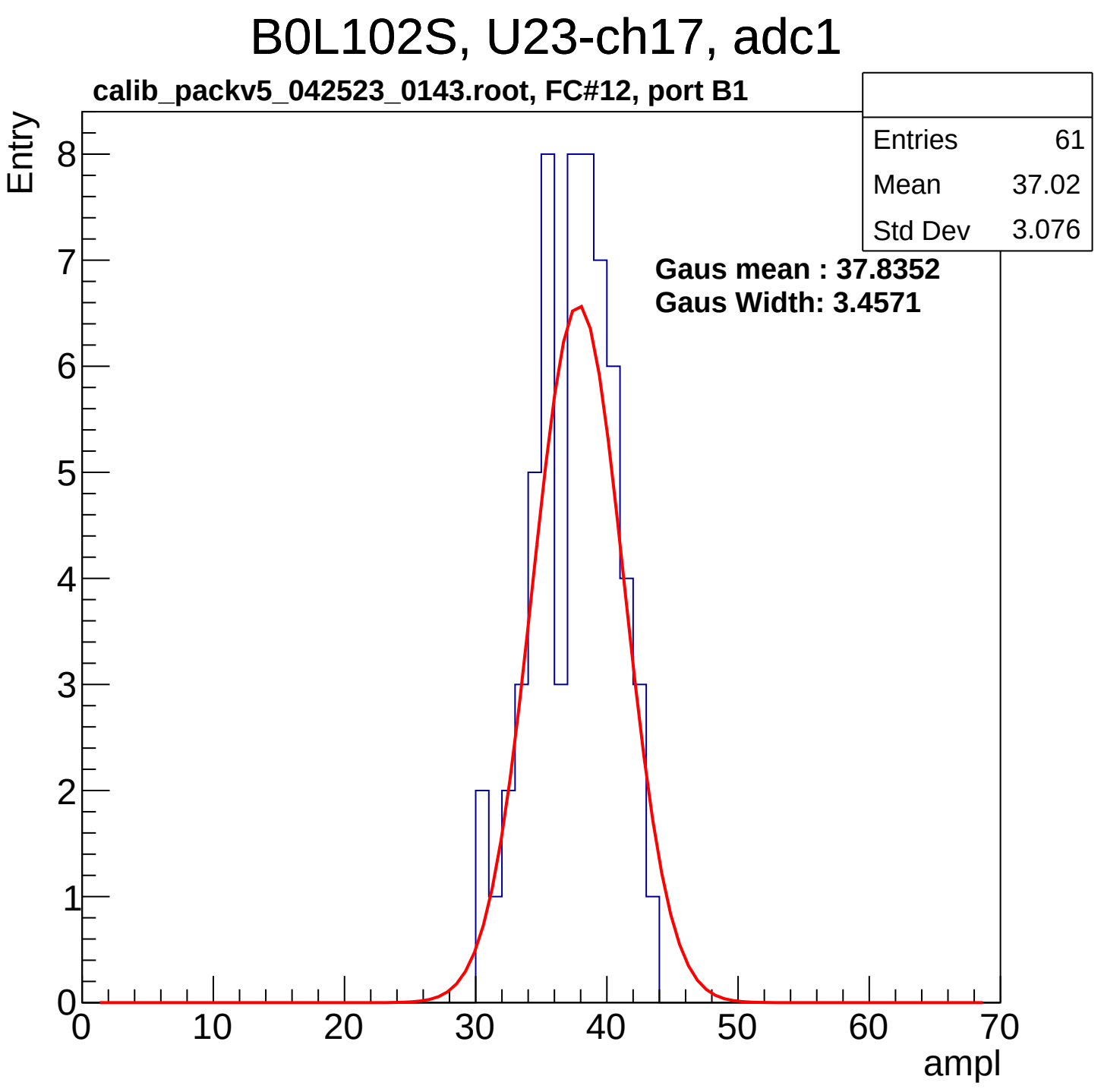

B0L102S, U23-ch16, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.09
Std Dev 3.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch16, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 59.26
Std Dev 10.44
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch16, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch16, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch17, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 31.45
Std Dev 3.317
Gaus mean : 31.4087
Gaus Width: 3.7449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch17, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 37.02
Std Dev 3.076
Gaus mean : 37.8352
Gaus Width: 3.4571
Entry
ampl
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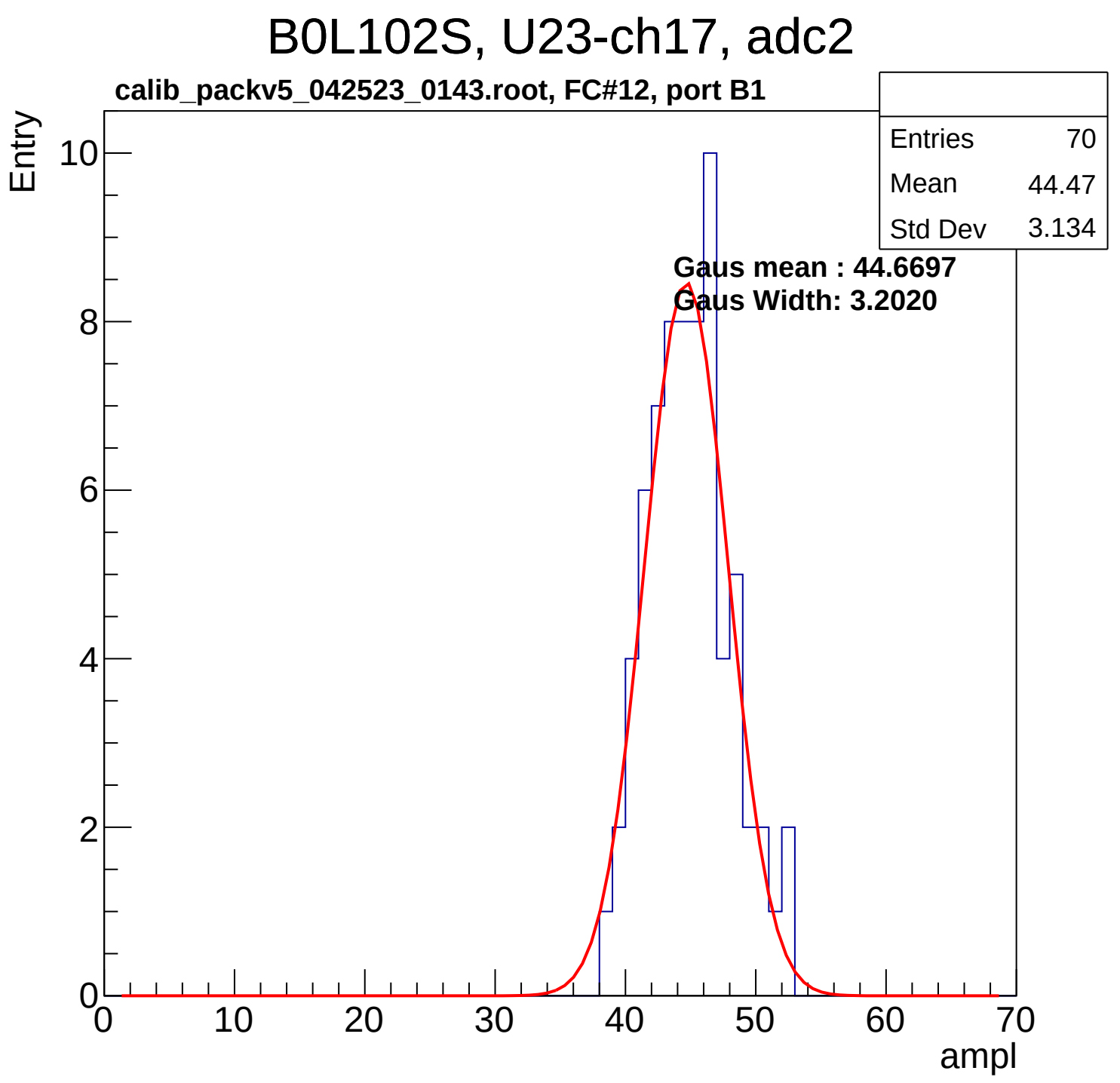
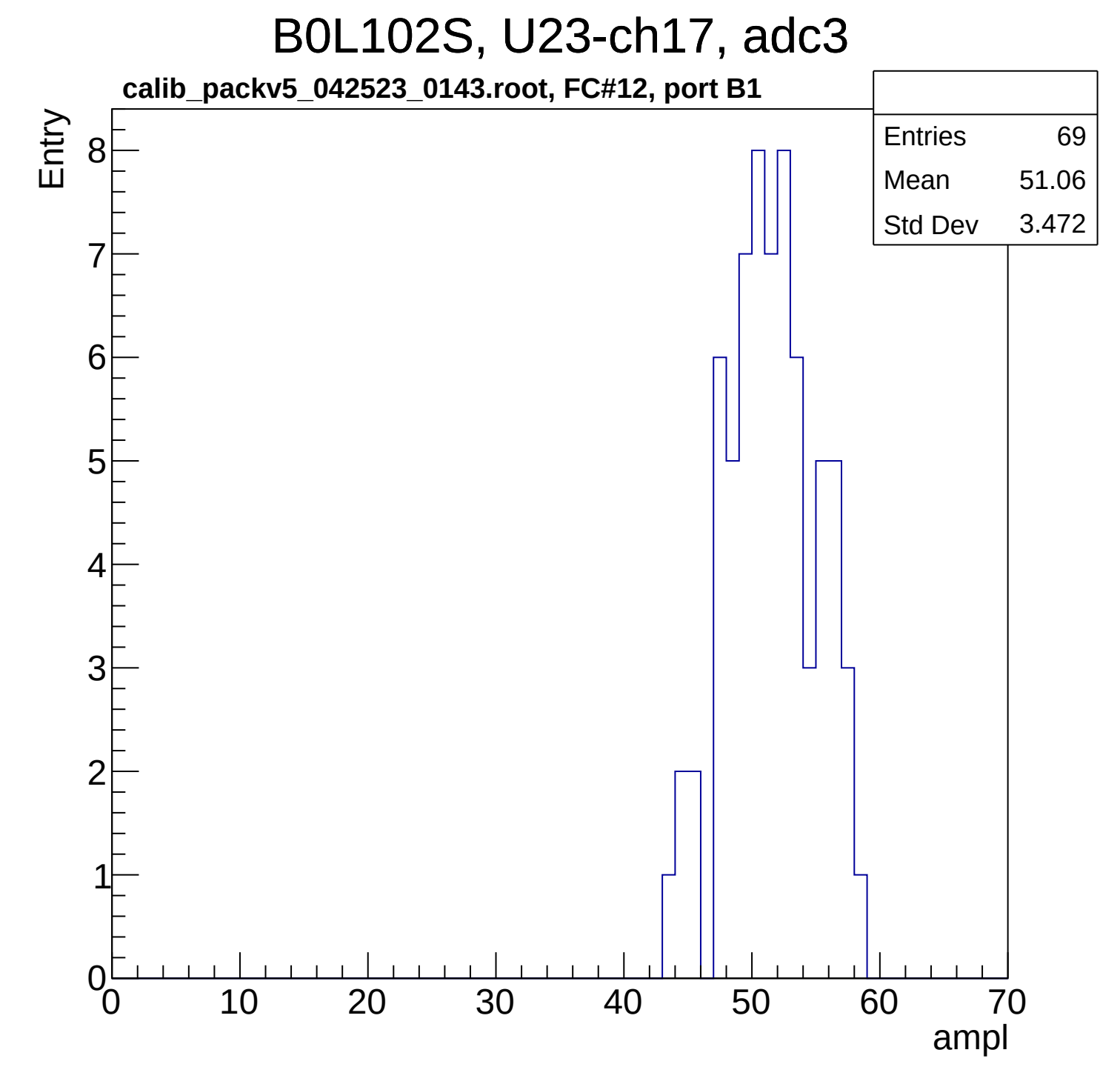
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch17, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 44.47
Std Dev 3.134
Gaus mean : 44.6697
Gaus Width: 3.2020
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch17, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 51.06
Std Dev 3.472
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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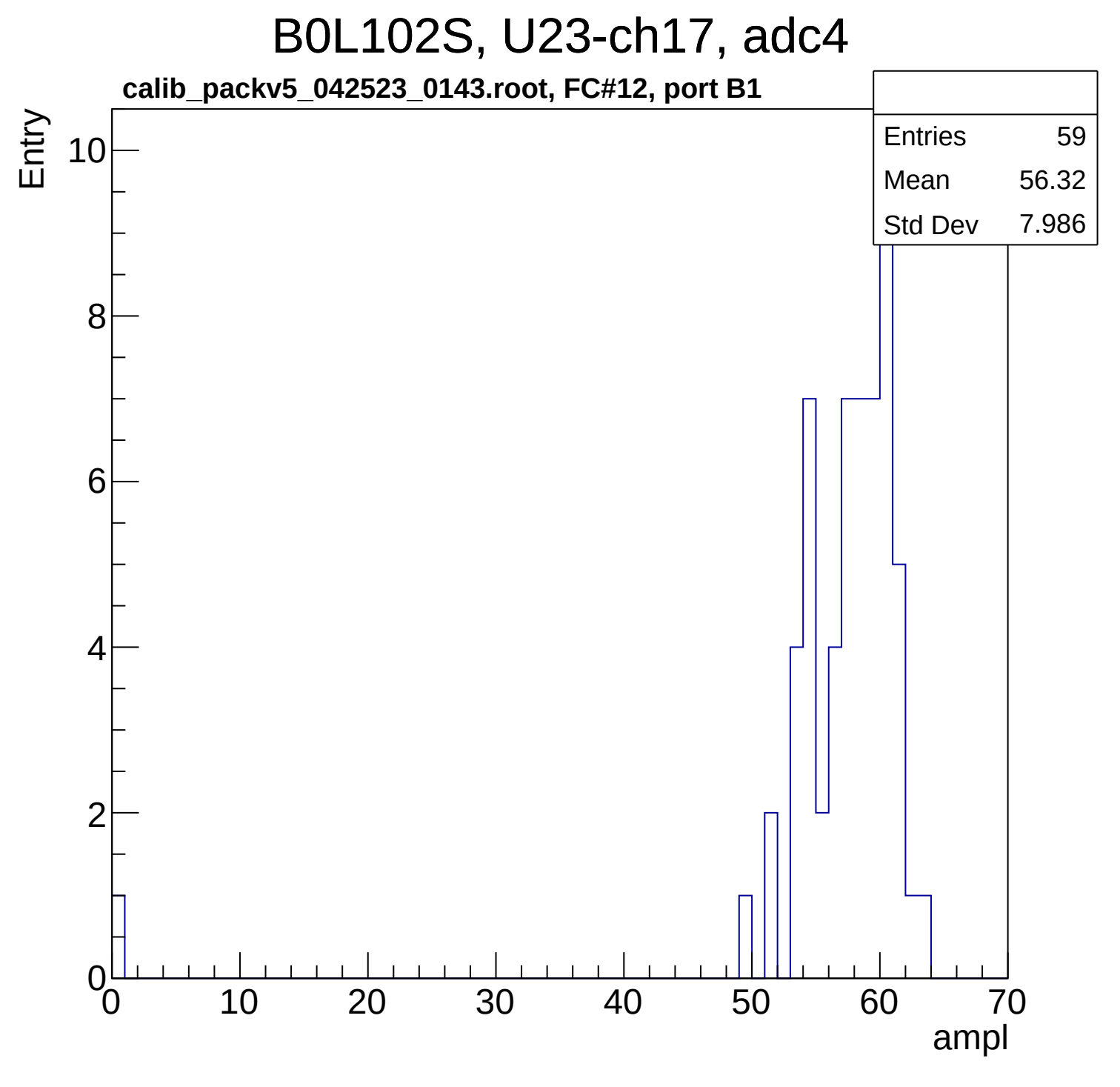
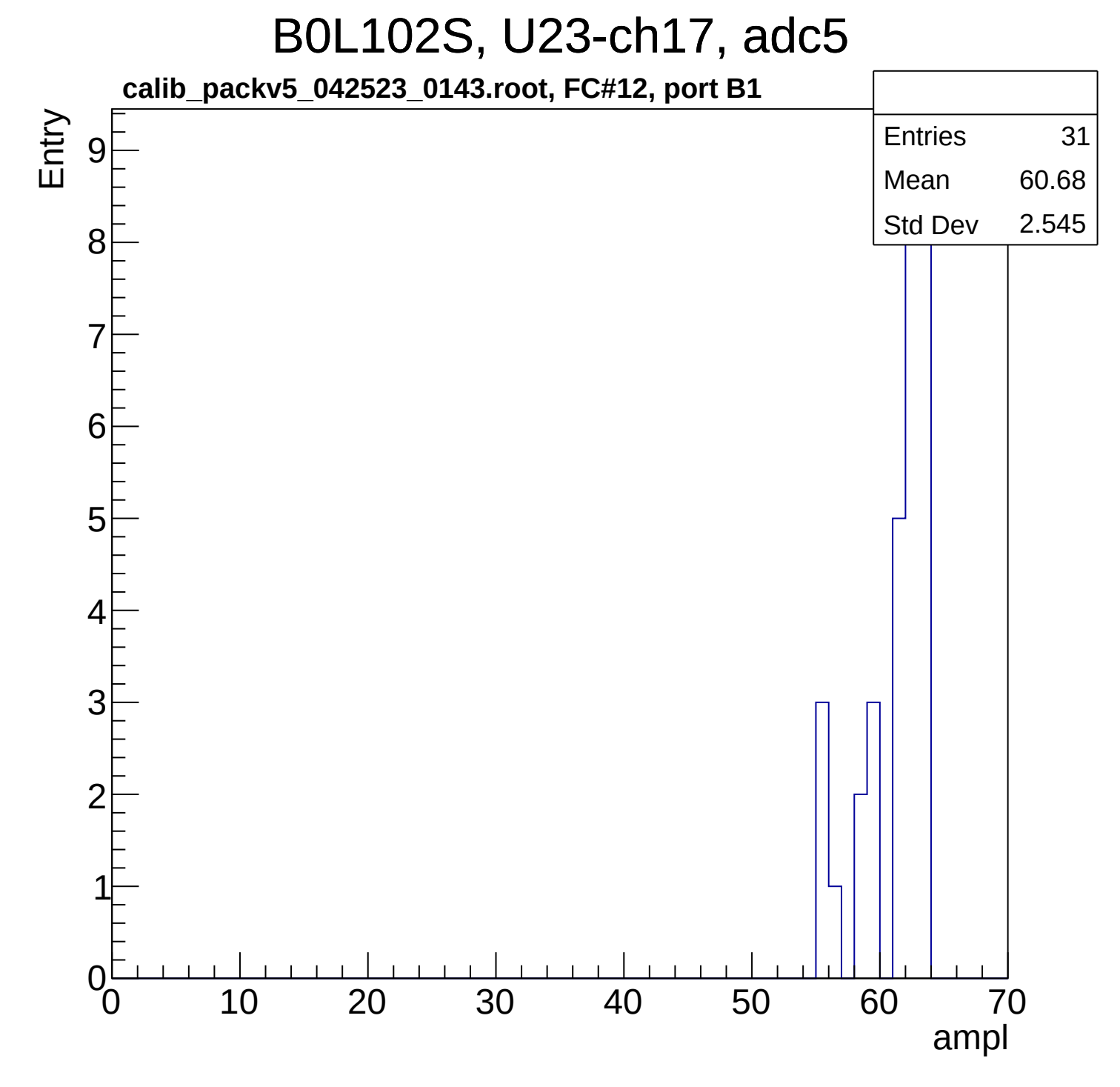
40
50
60
70


B0L102S, U23-ch17, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 56.32
Std Dev 7.986
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch17, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 60.68
Std Dev 2.545
Entry
ampl
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3
4
5
6
7
8
9
0
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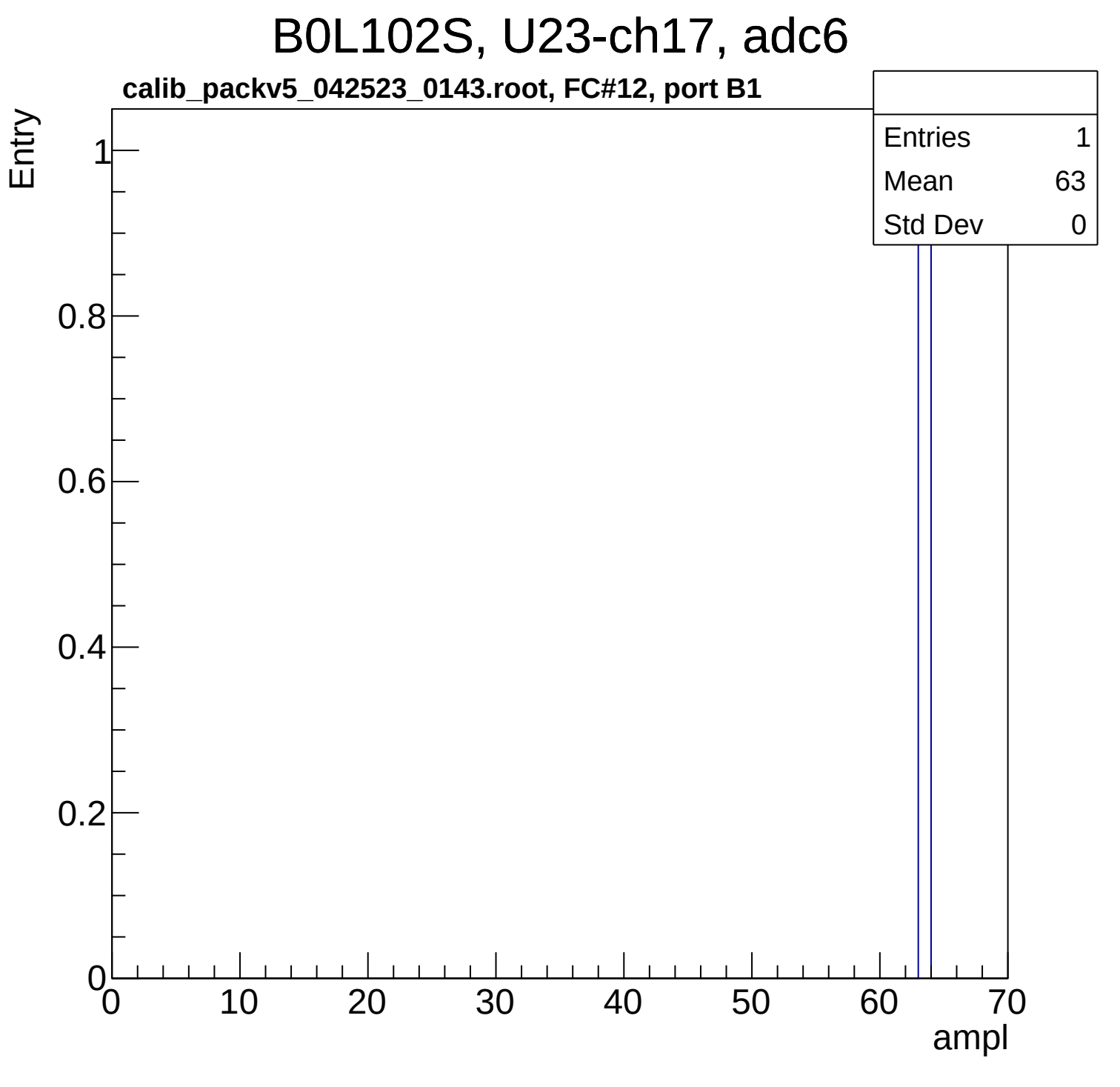
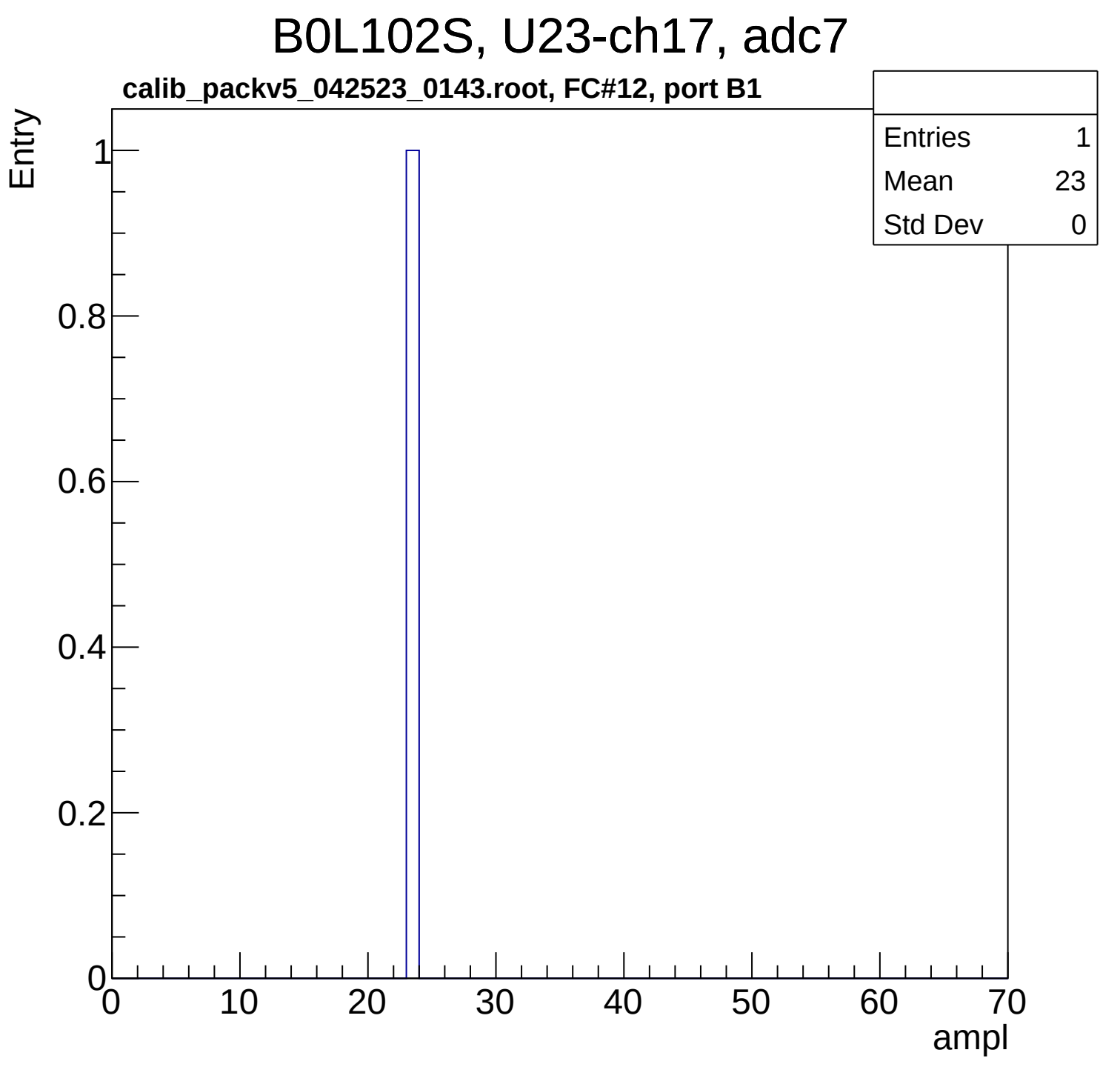
20
30
40
50
60
70


B0L102S, U23-ch17, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch17, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.4
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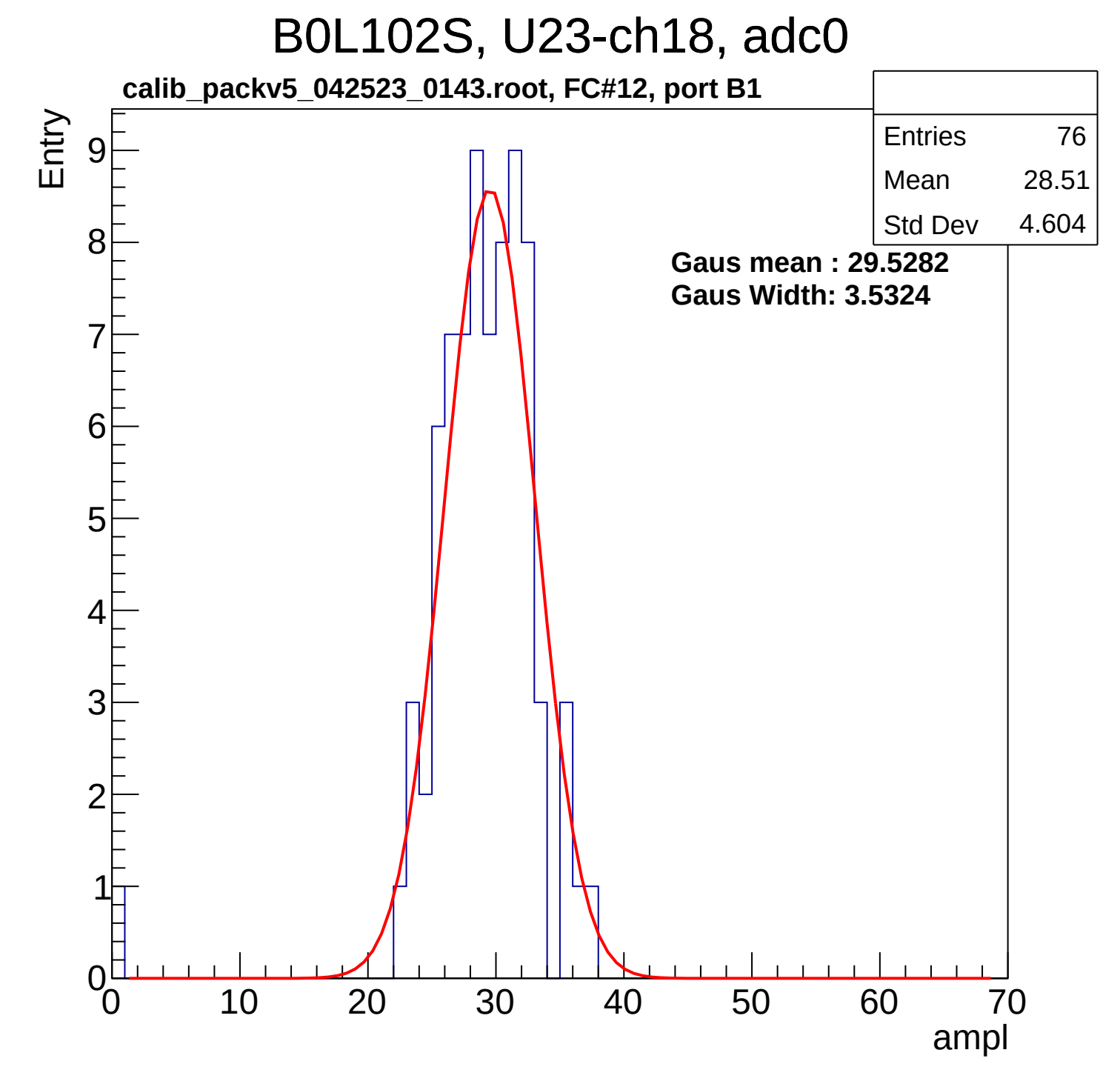
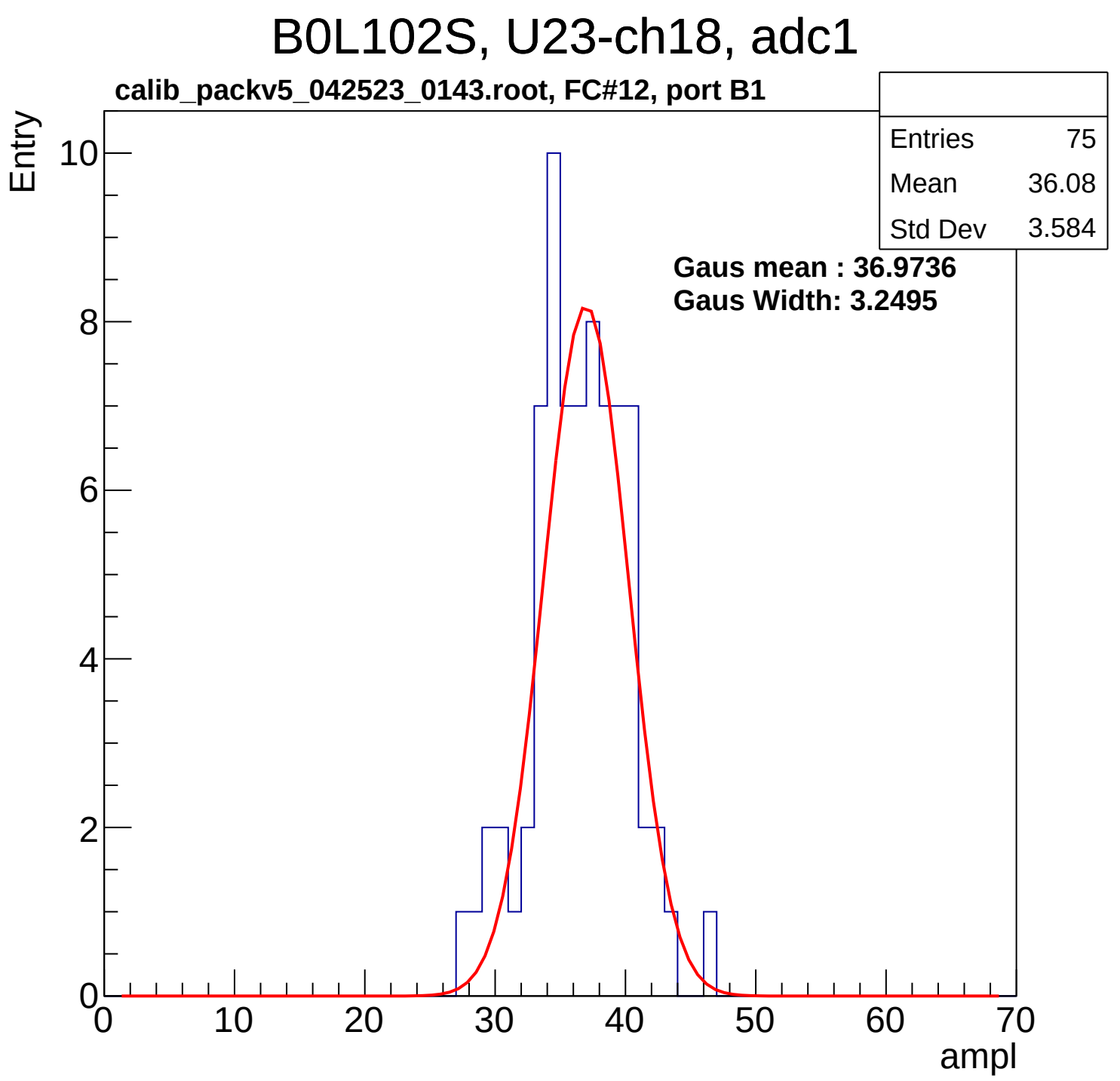
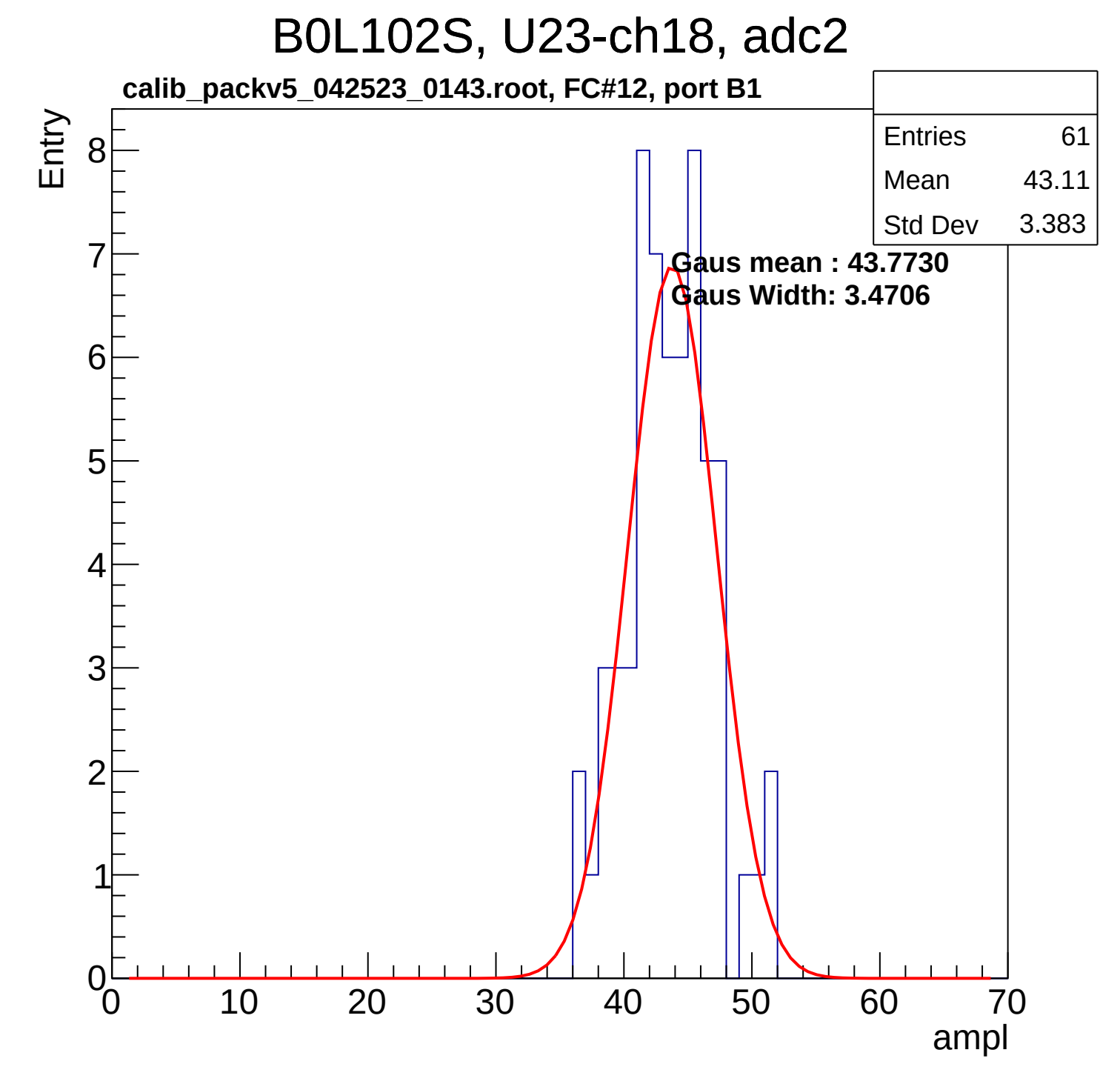
50
60
70
ampl


B0L102S, U23-ch18, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 28.51
Std Dev 4.604
Gaus mean : 29.5282
Gaus Width: 3.5324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch18, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.08
Std Dev 3.584
Gaus mean : 36.9736
Gaus Width: 3.2495
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch18, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 43.11
Std Dev 3.383
Gaus mean : 43.7730
Gaus Width: 3.4706
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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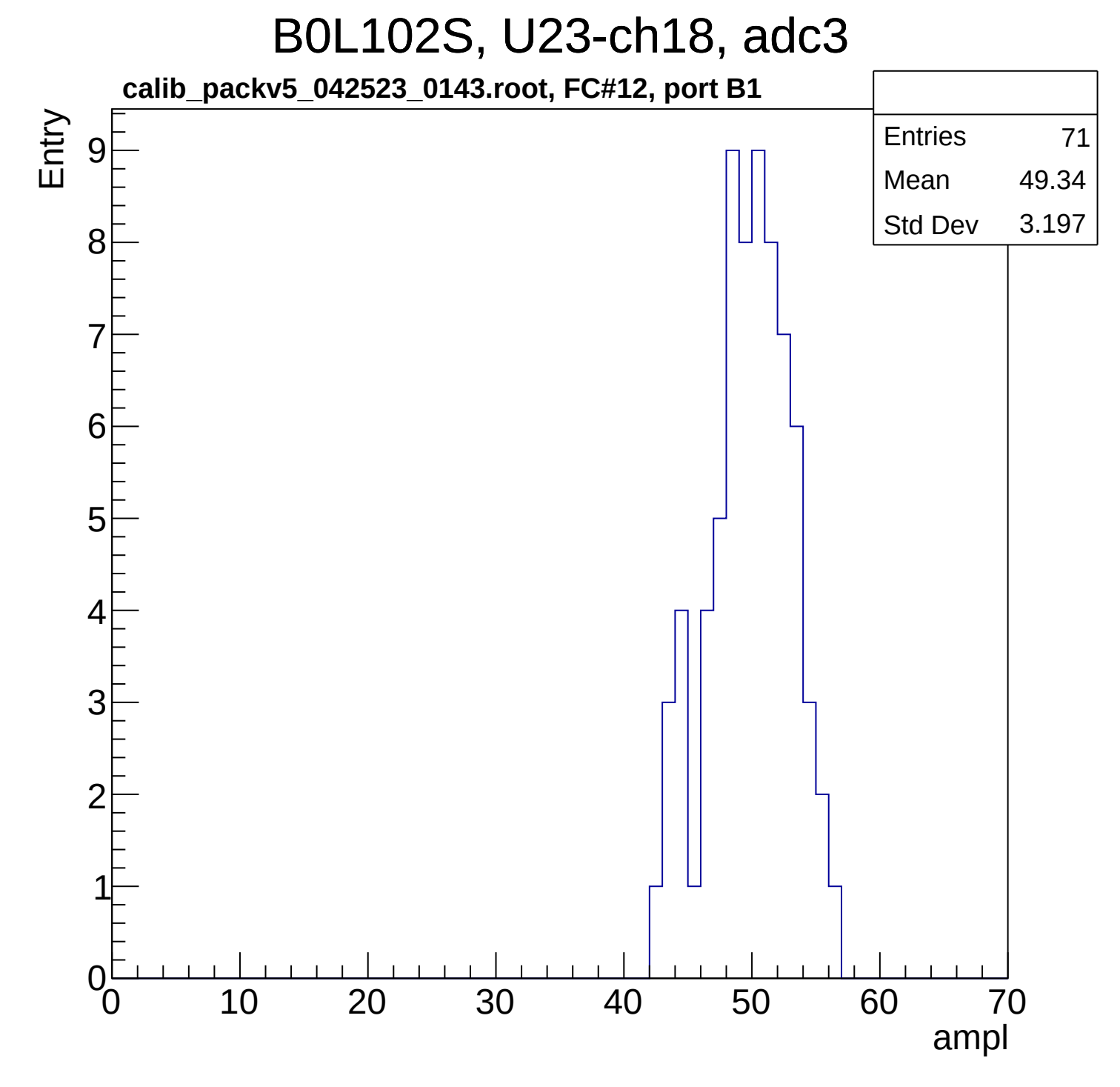
B0L102S, U23-ch18, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 49.34
Std Dev 3.197
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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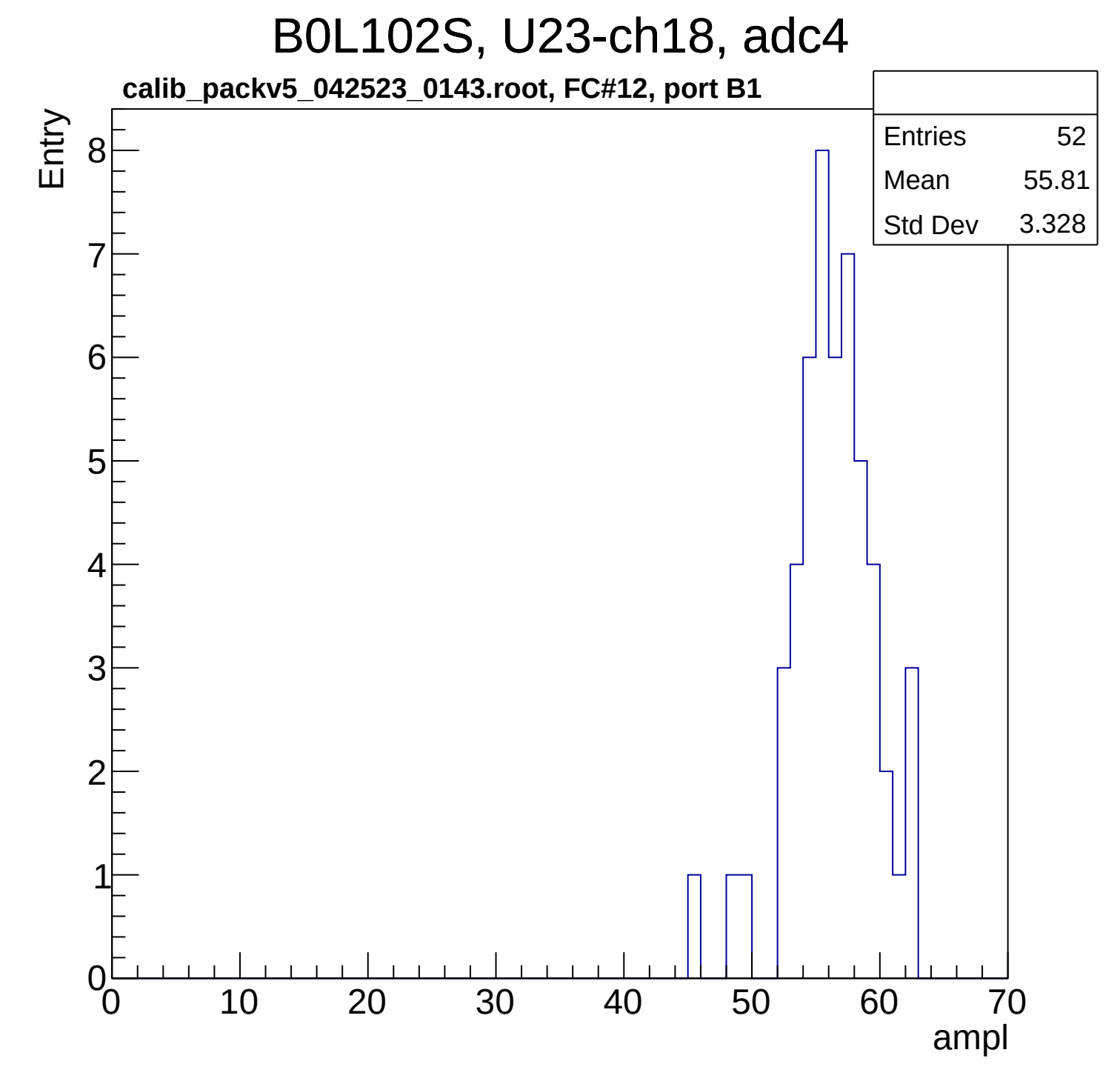
B0L102S, U23-ch18, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 55.81
Std Dev 3.328
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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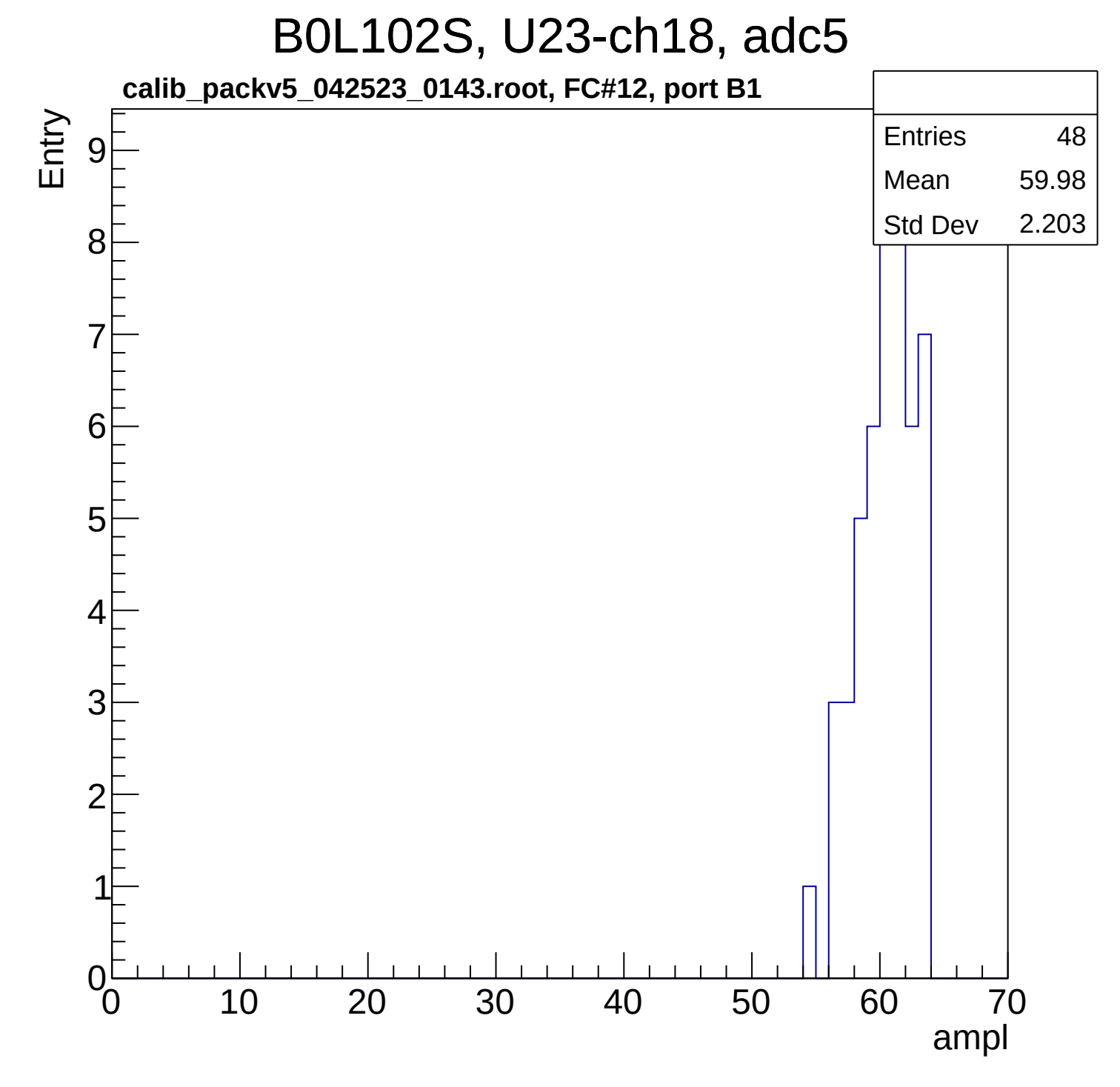
B0L102S, U23-ch18, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 59.98
Std Dev 2.203
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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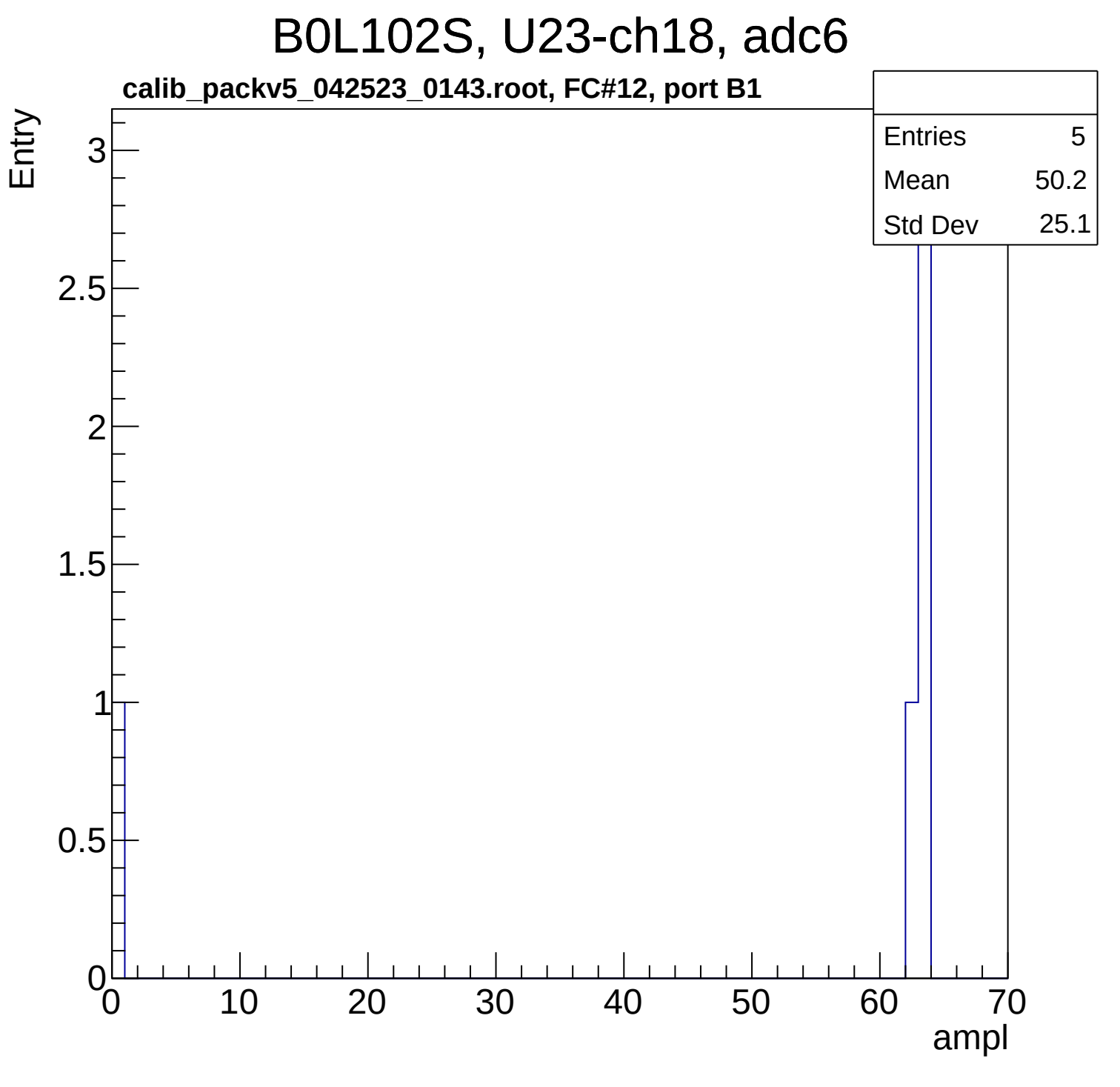
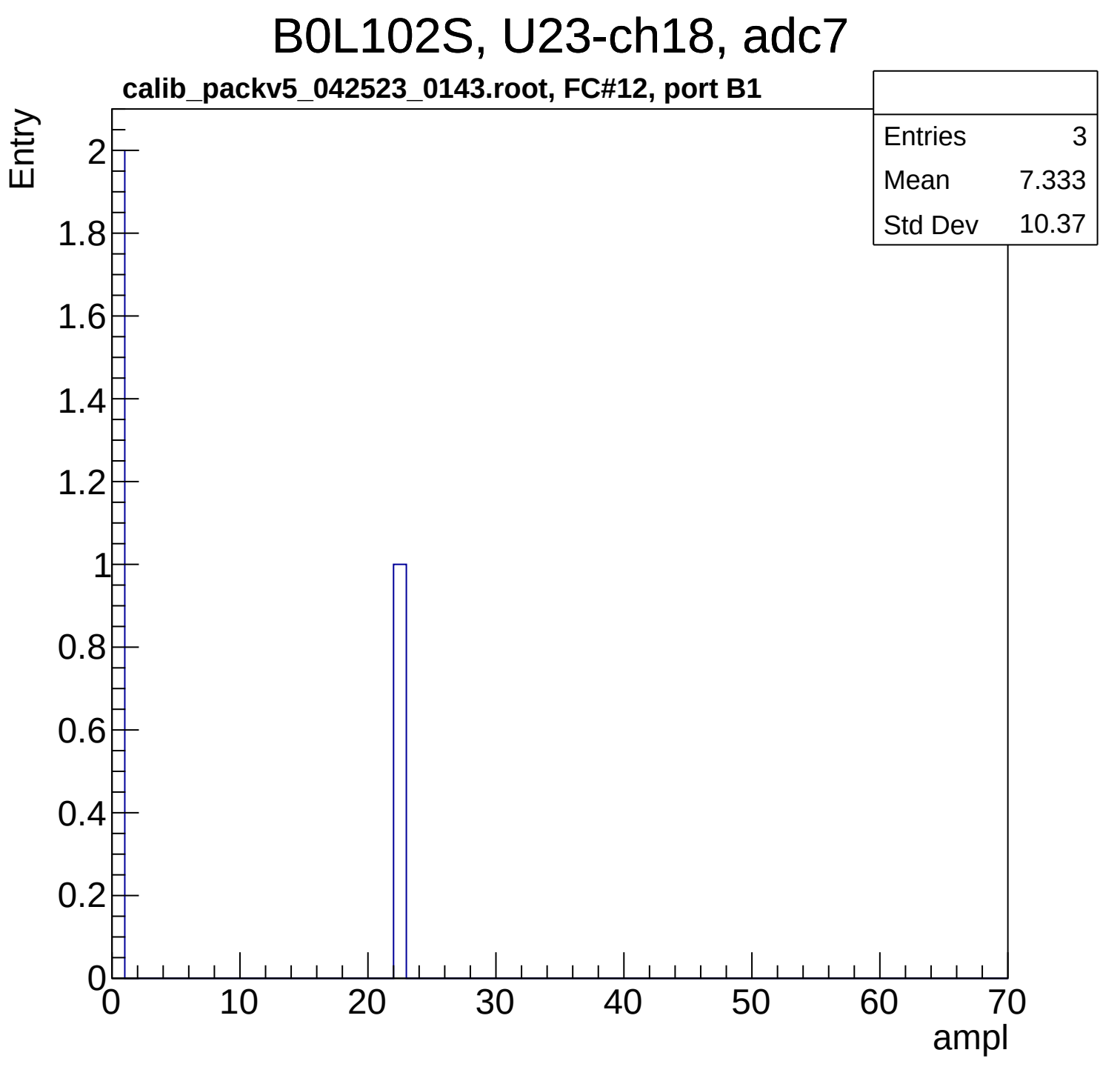

B0L102S, U23-ch18, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 50.2
Std Dev 25.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch18, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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60
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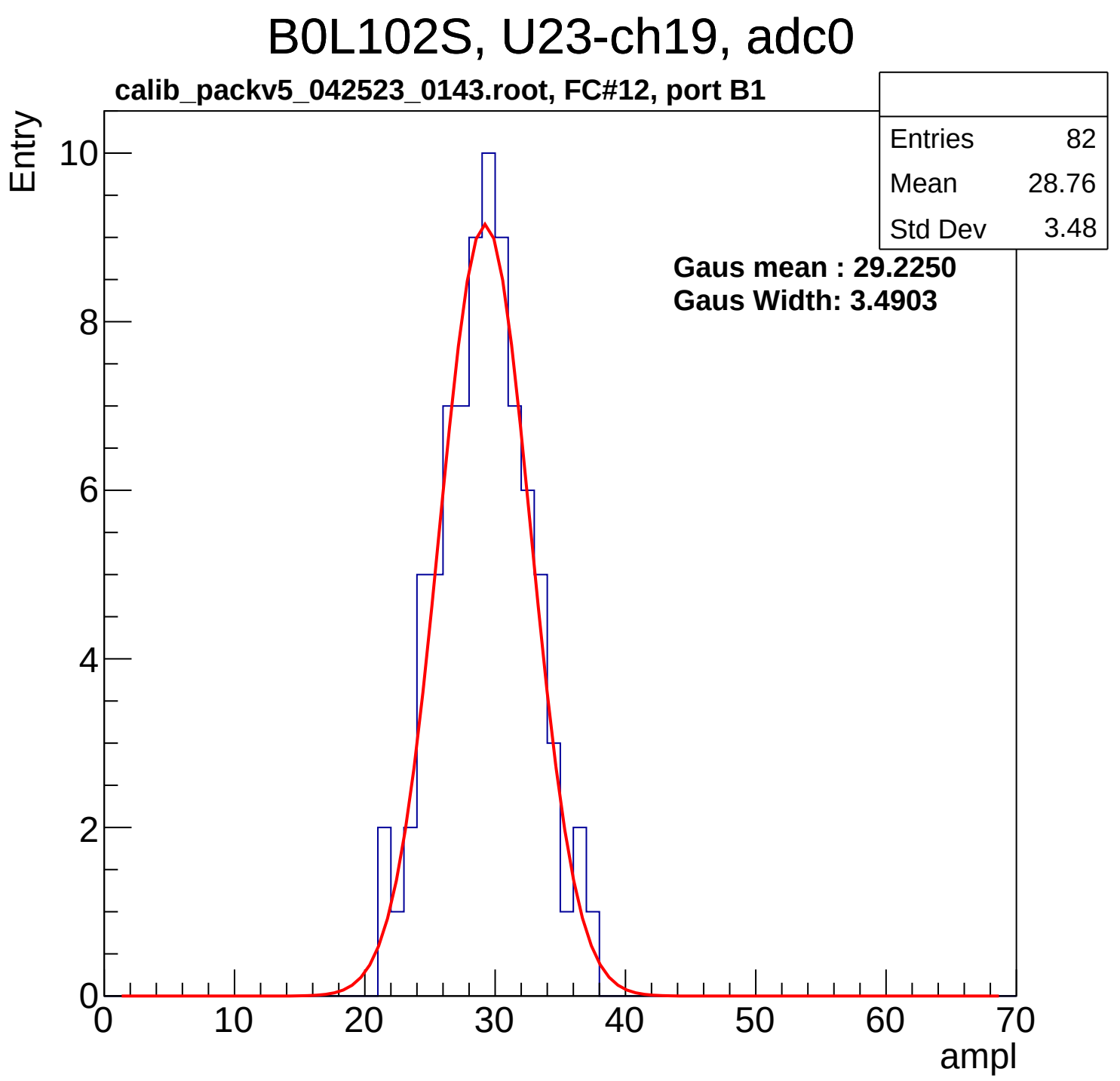
B0L102S, U23-ch19, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 28.76
Std Dev 3.48
Gaus mean : 29.2250
Gaus Width: 3.4903
Entry
ampl
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2
4
6
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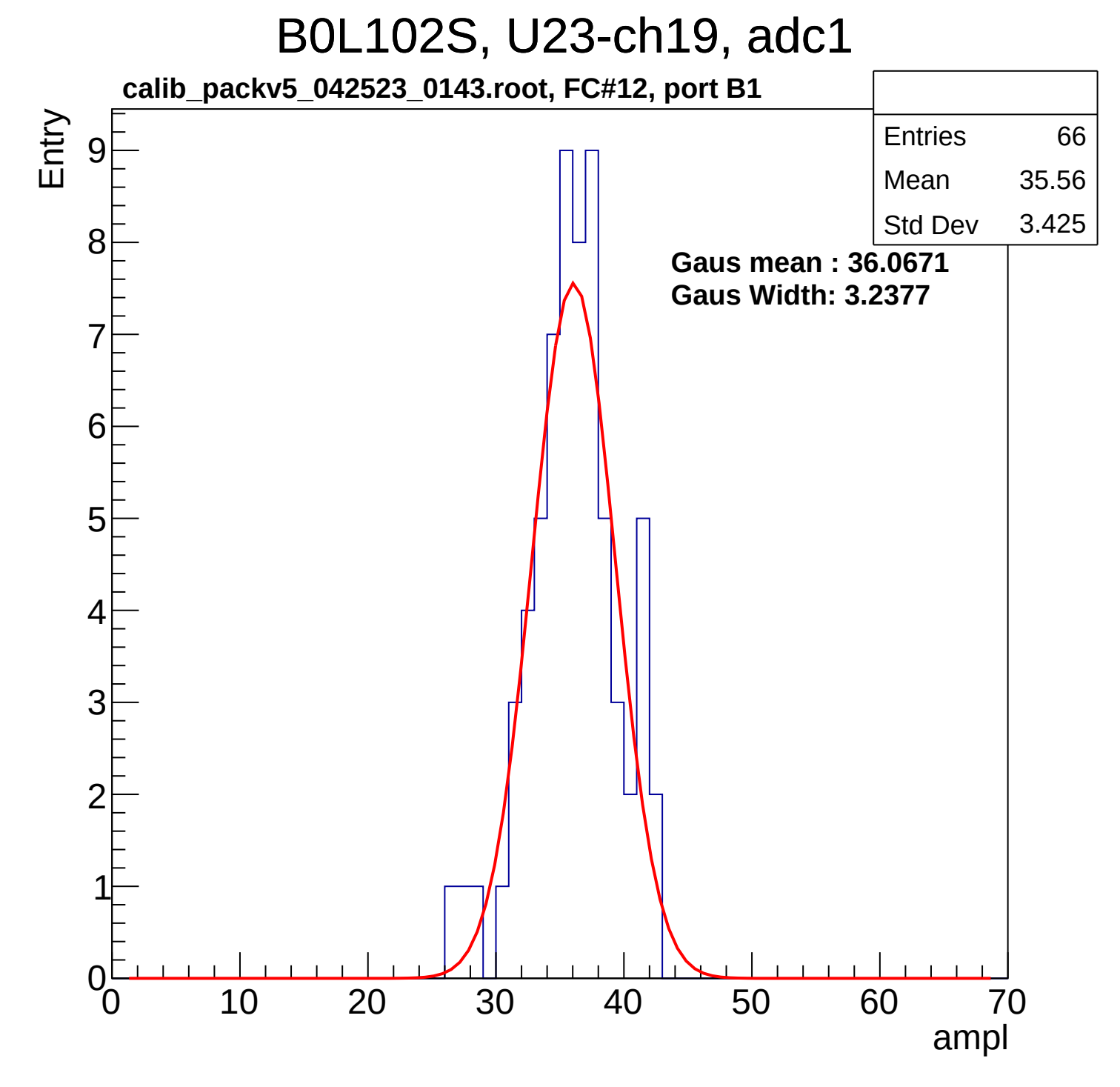
B0L102S, U23-ch19, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 35.56
Std Dev 3.425
Gaus mean : 36.0671
Gaus Width: 3.2377
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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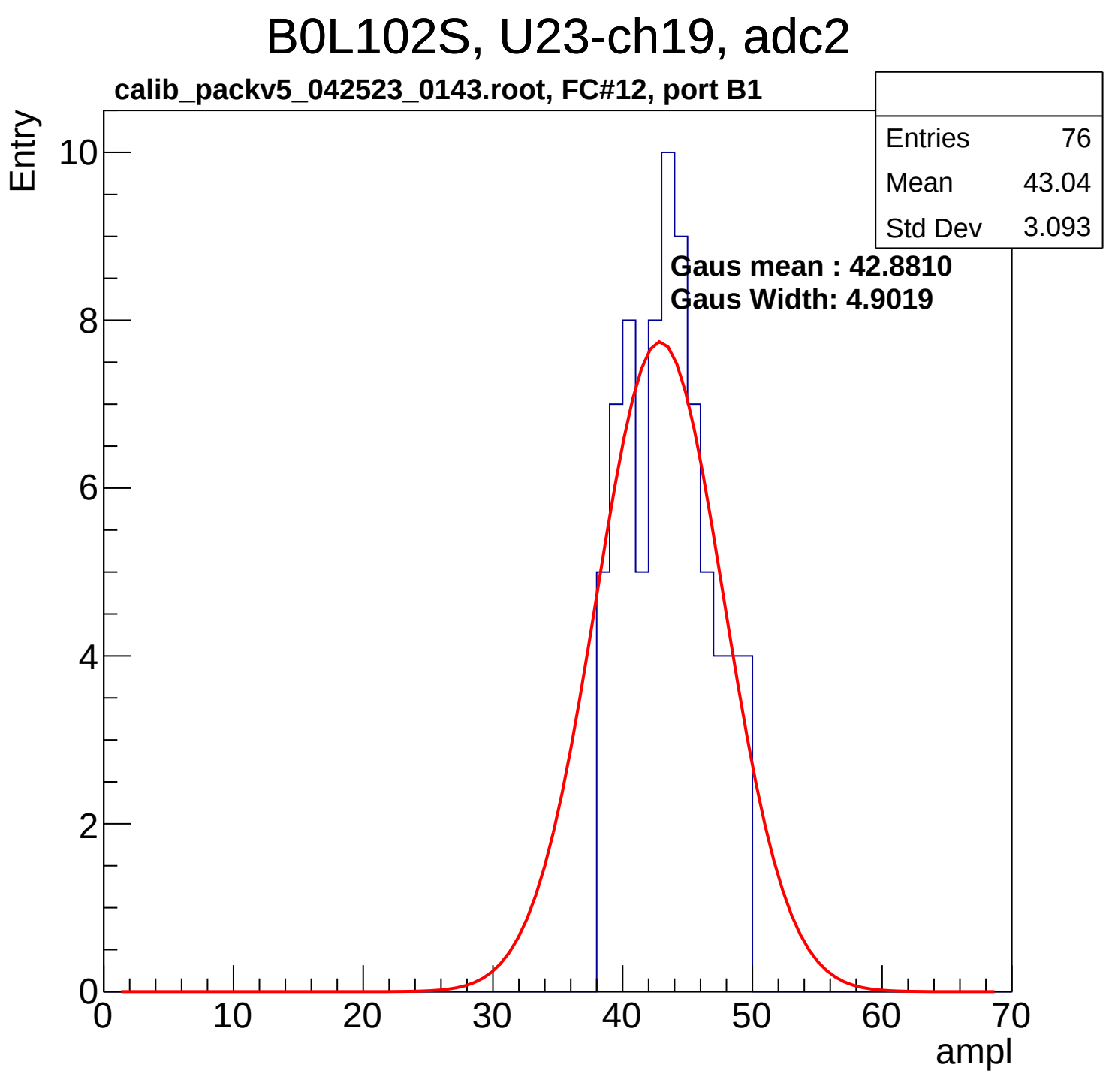
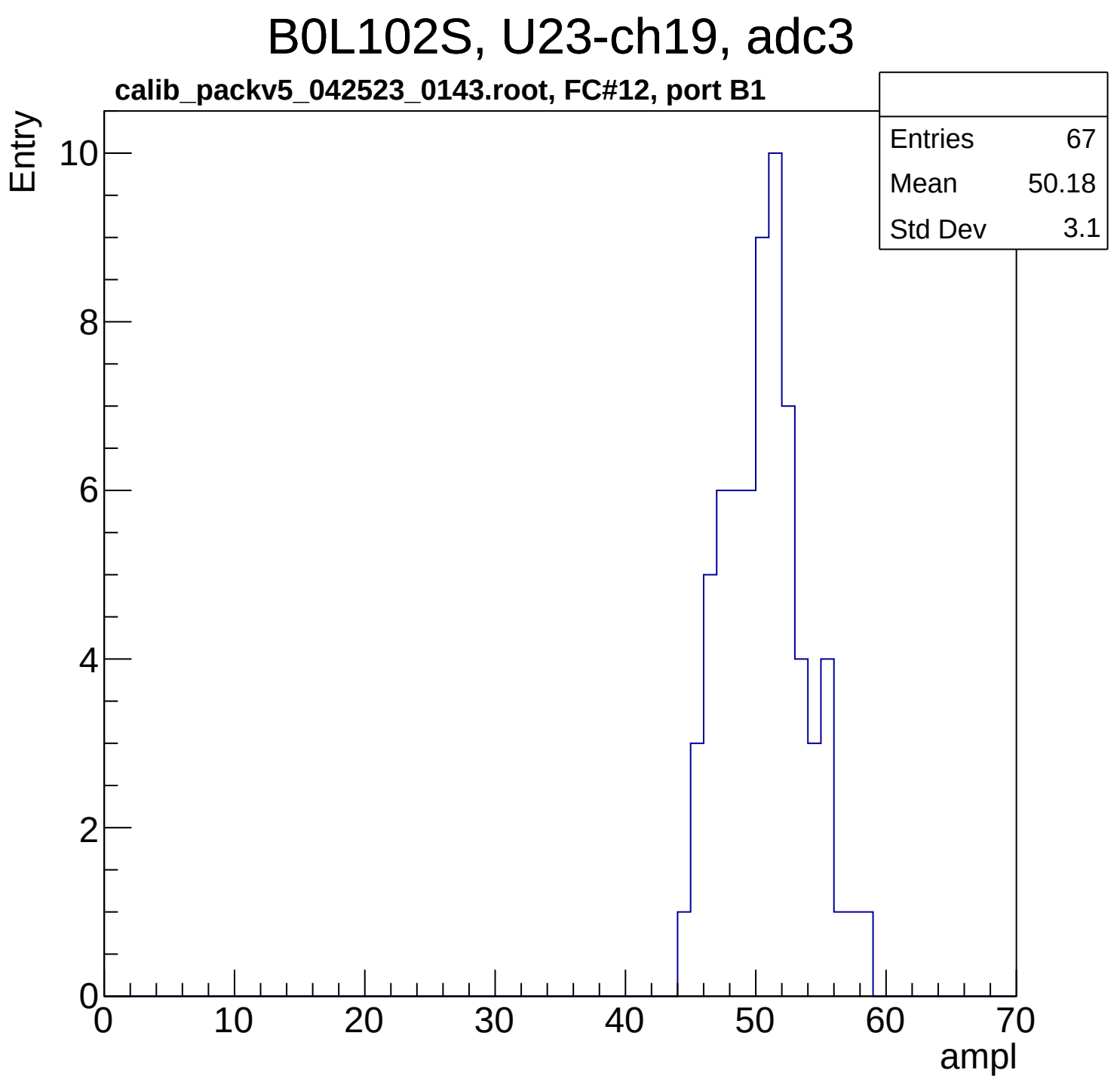
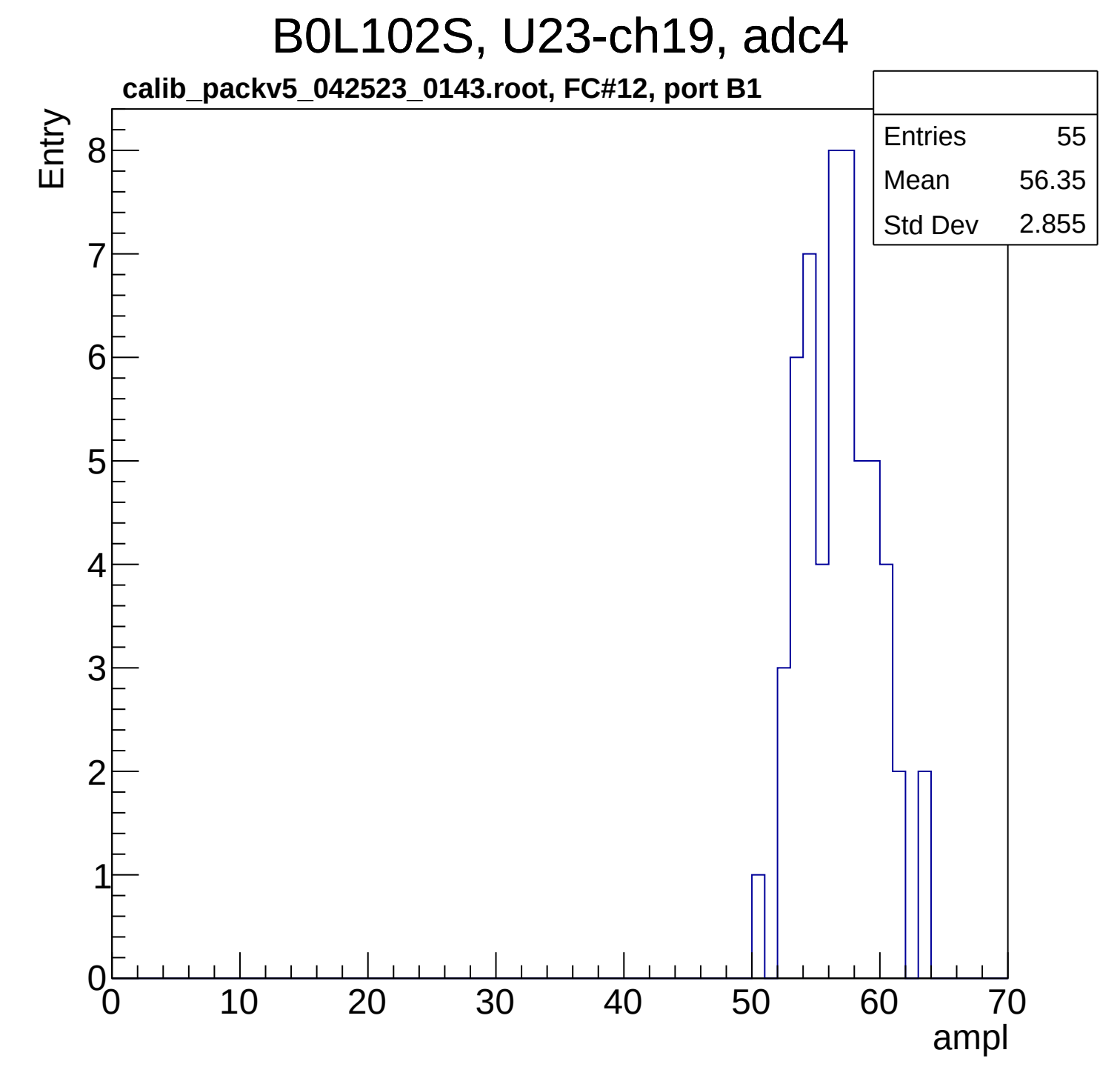
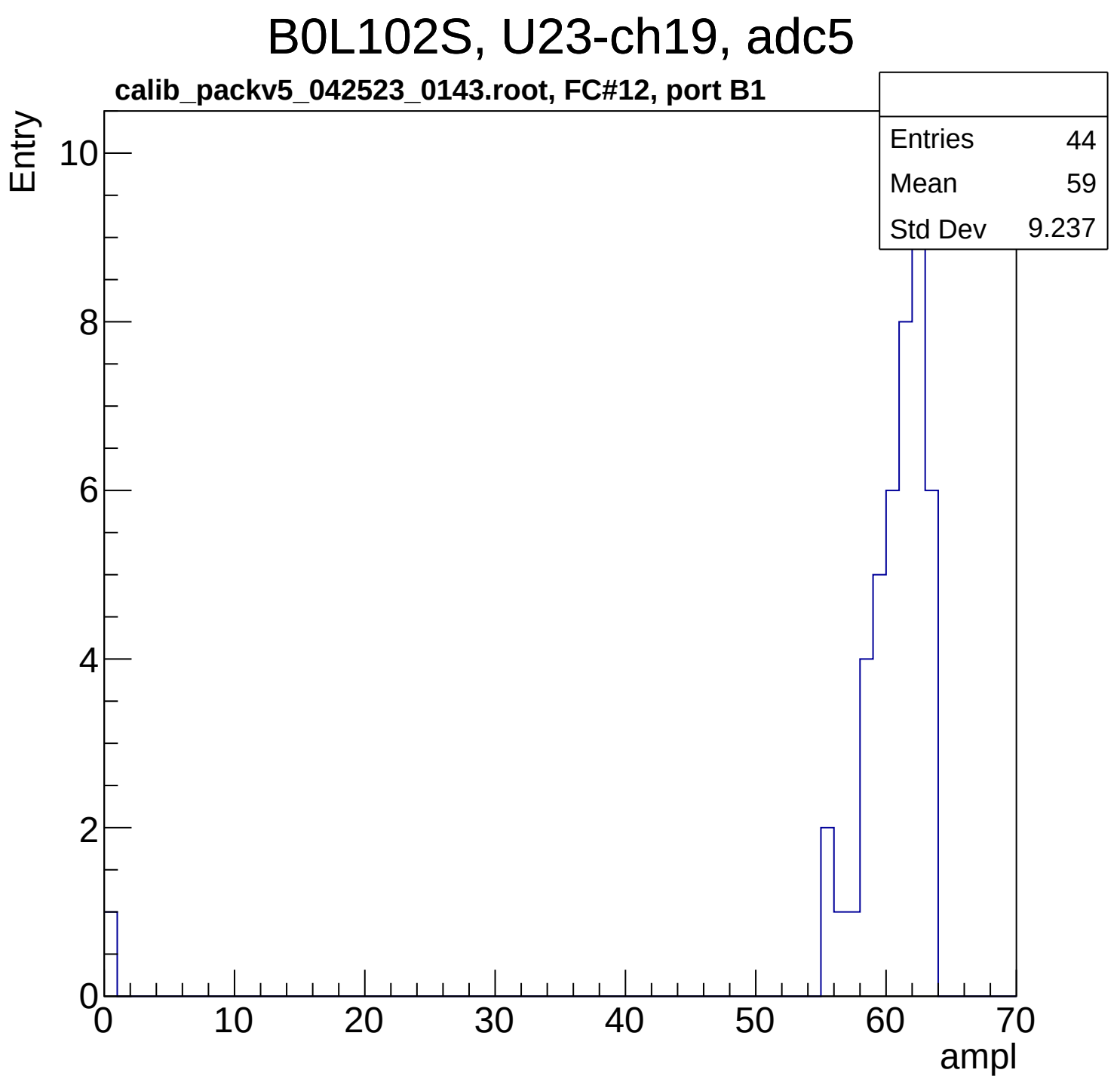
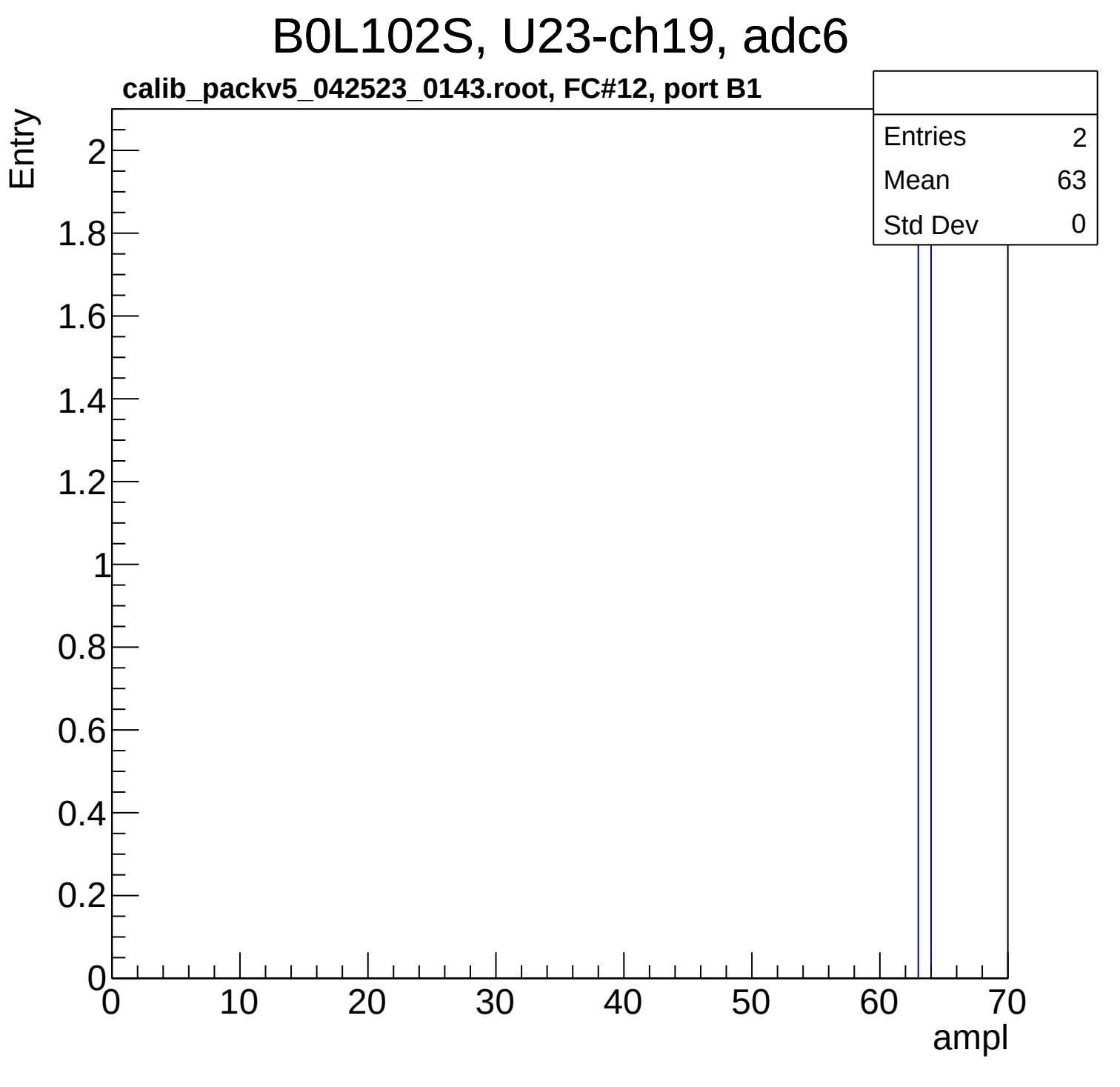
60
70

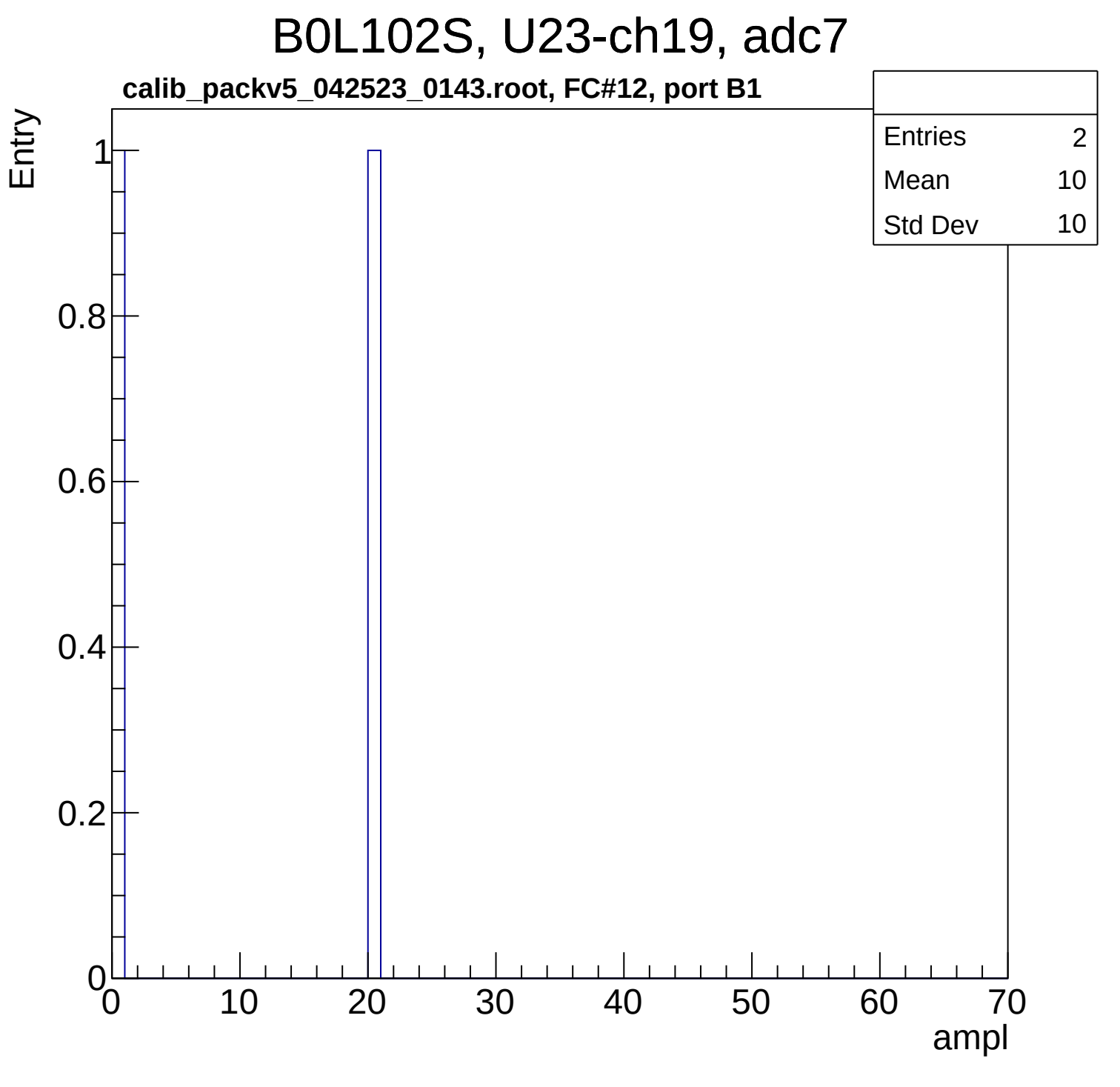

B0L102S, U23-ch19, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 43.04
Std Dev 3.093
Gaus mean : 42.8810
Gaus Width: 4.9019
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

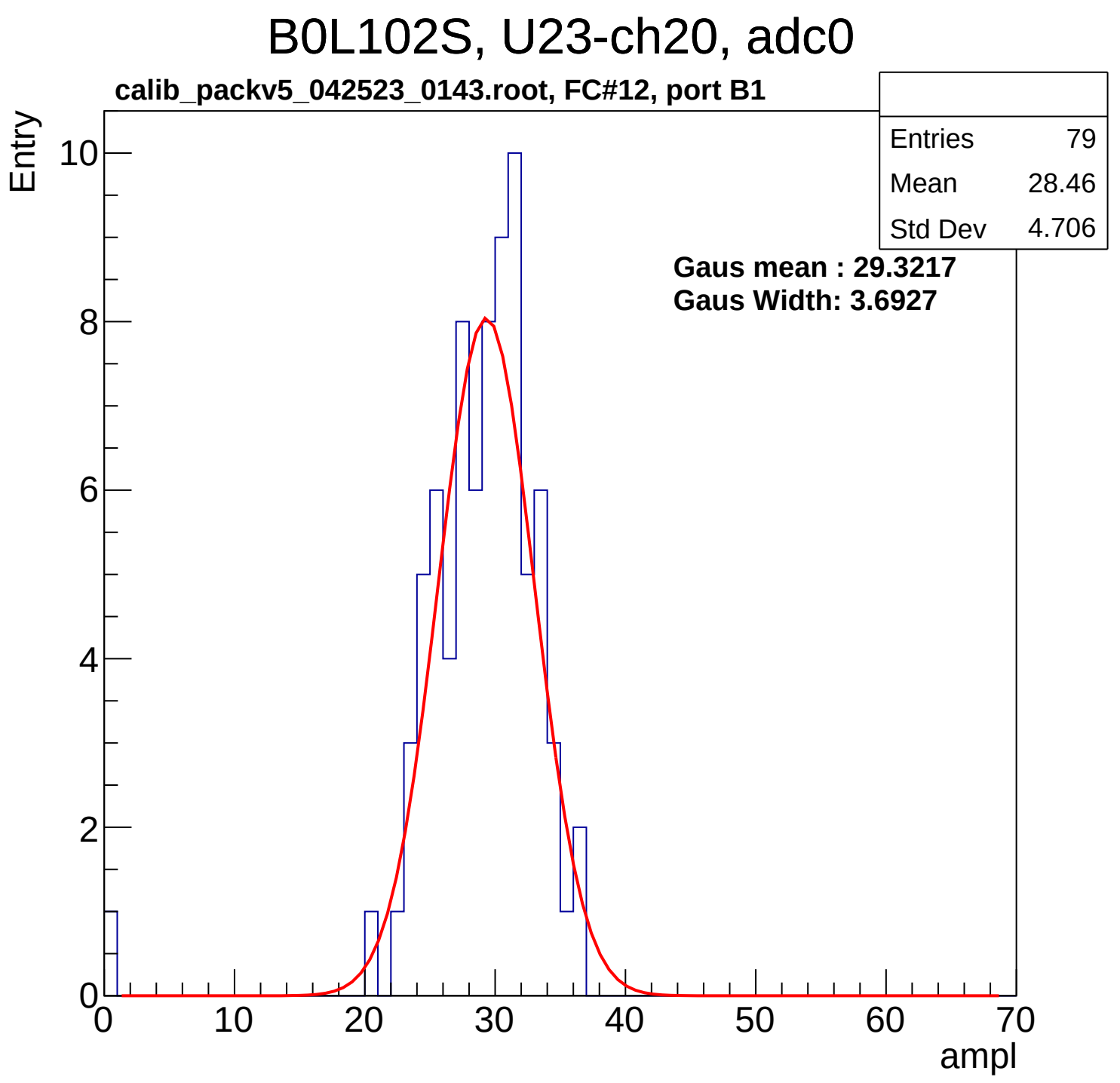

B0L102S, U23-ch19, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.18
Std Dev 3.1
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

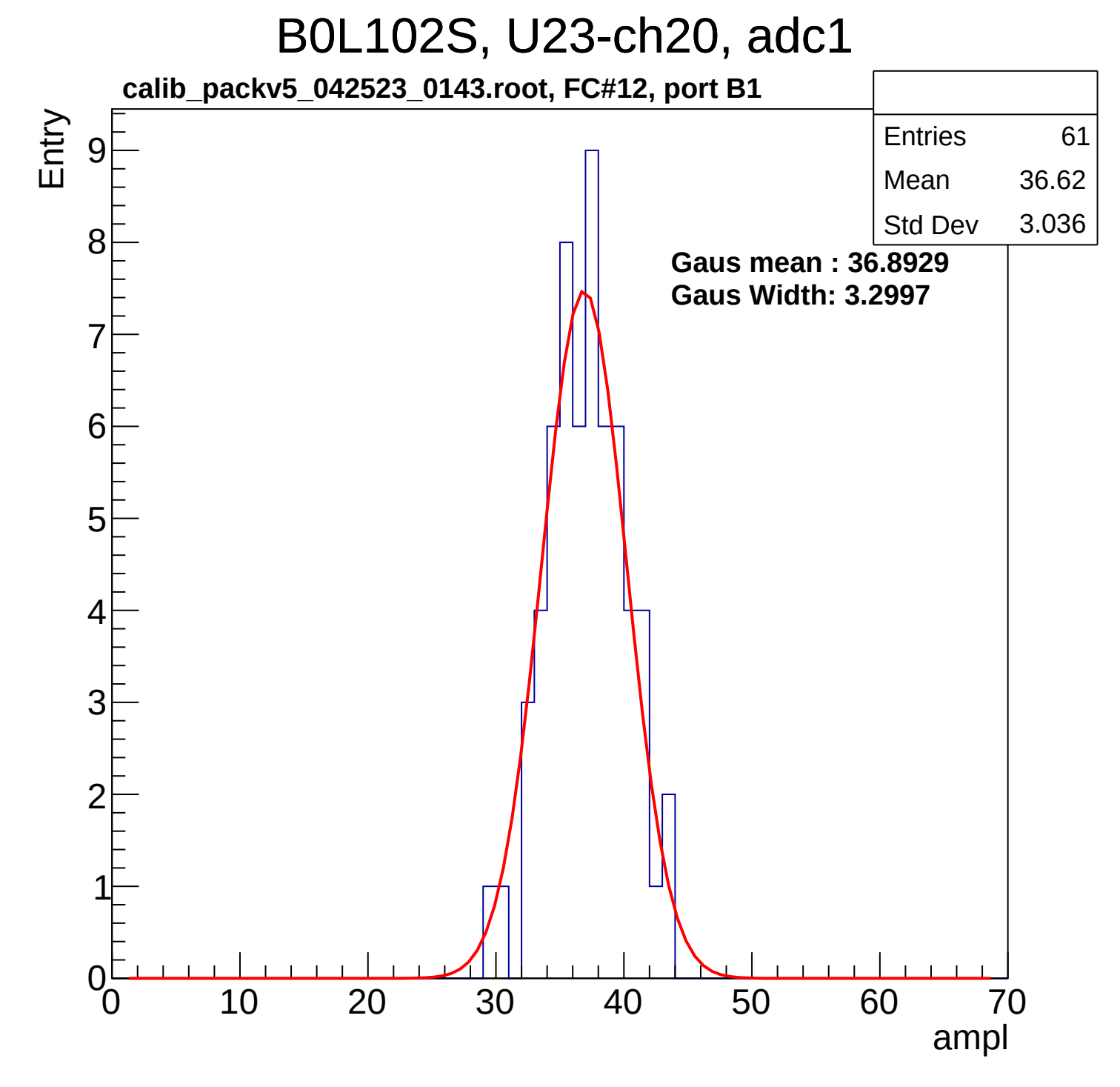

B0L102S, U23-ch19, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.35
Std Dev 2.855
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

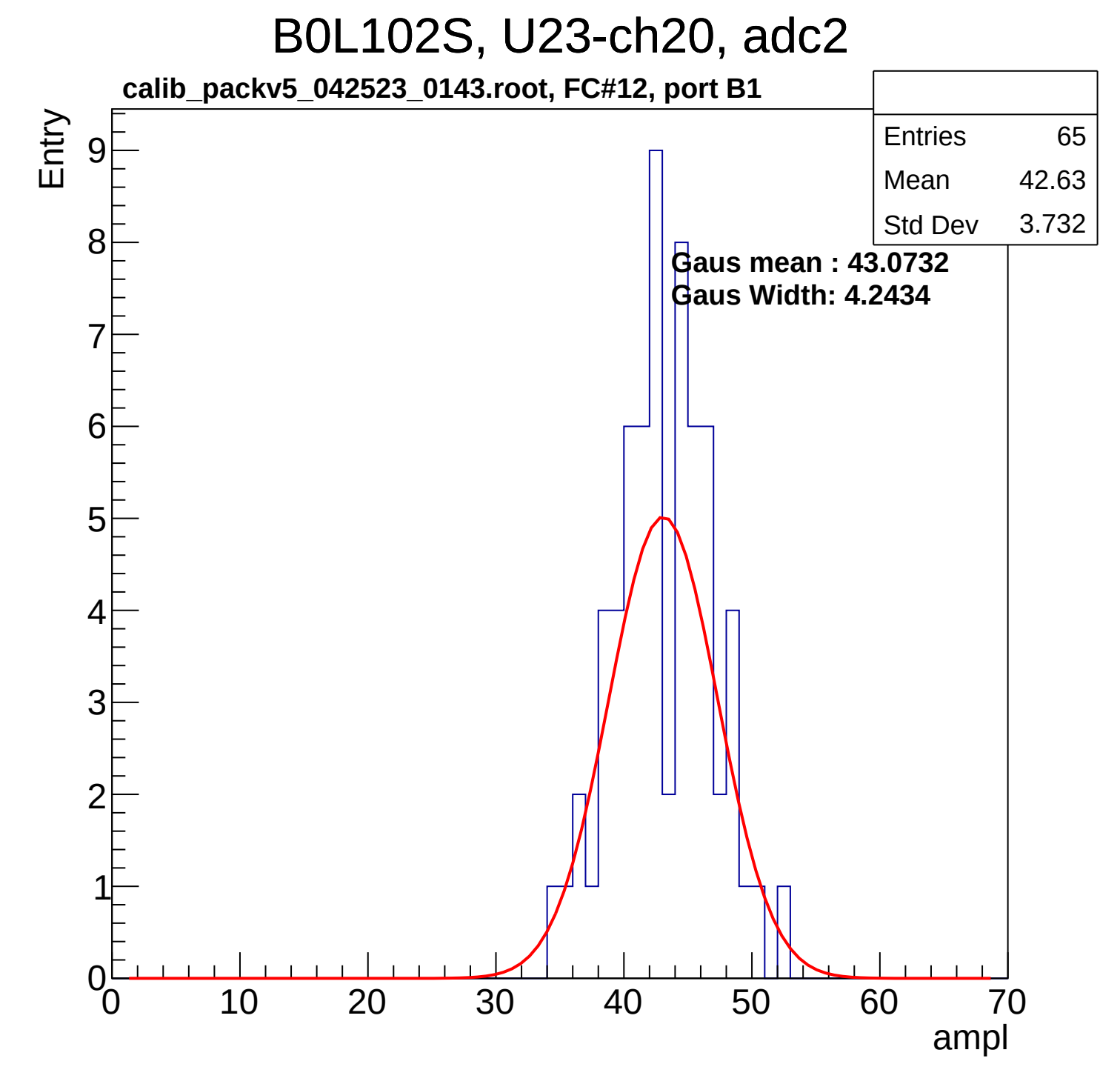

B0L102S, U23-ch19, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59
Std Dev 9.237
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

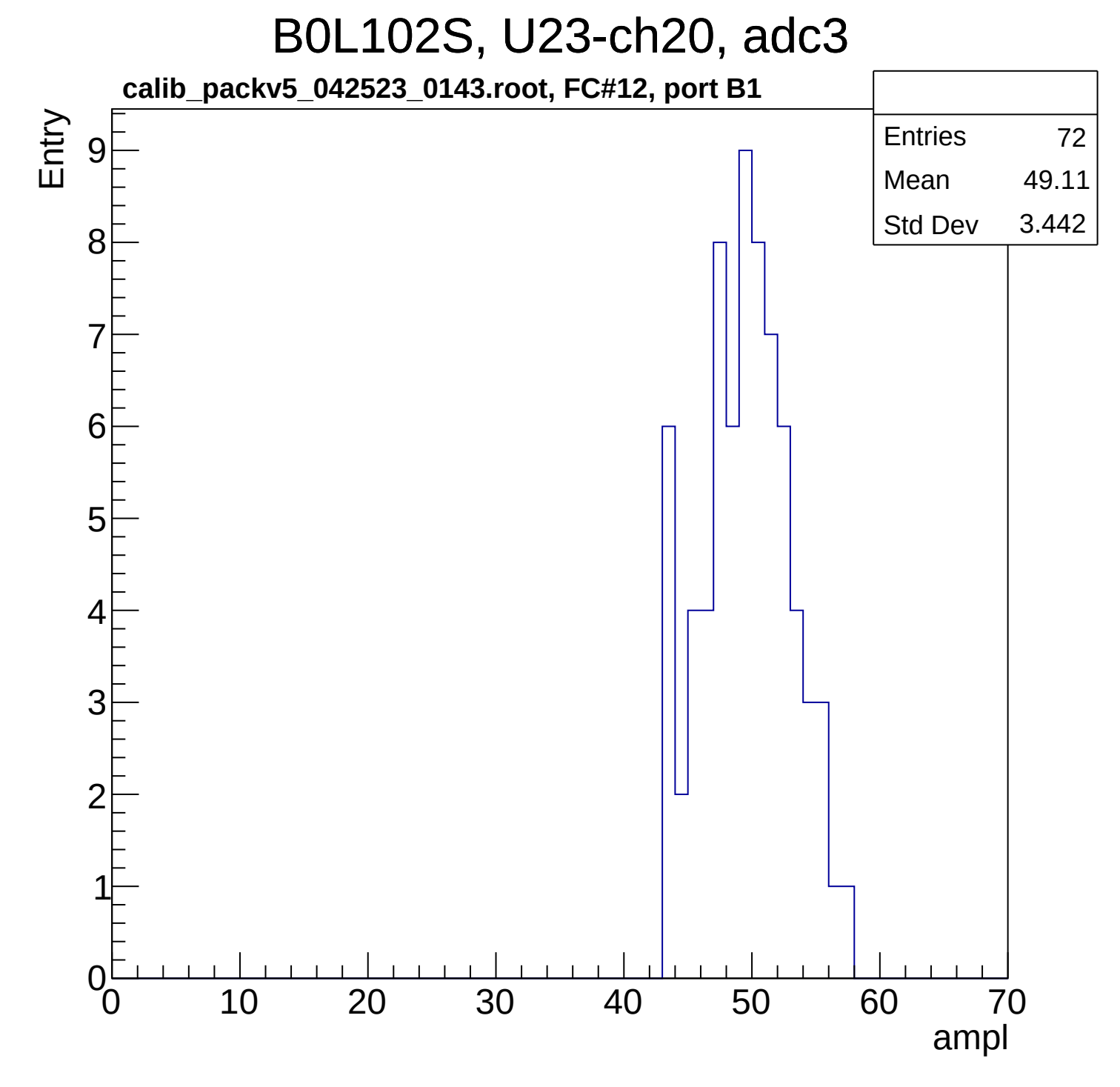

B0L102S, U23-ch19, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

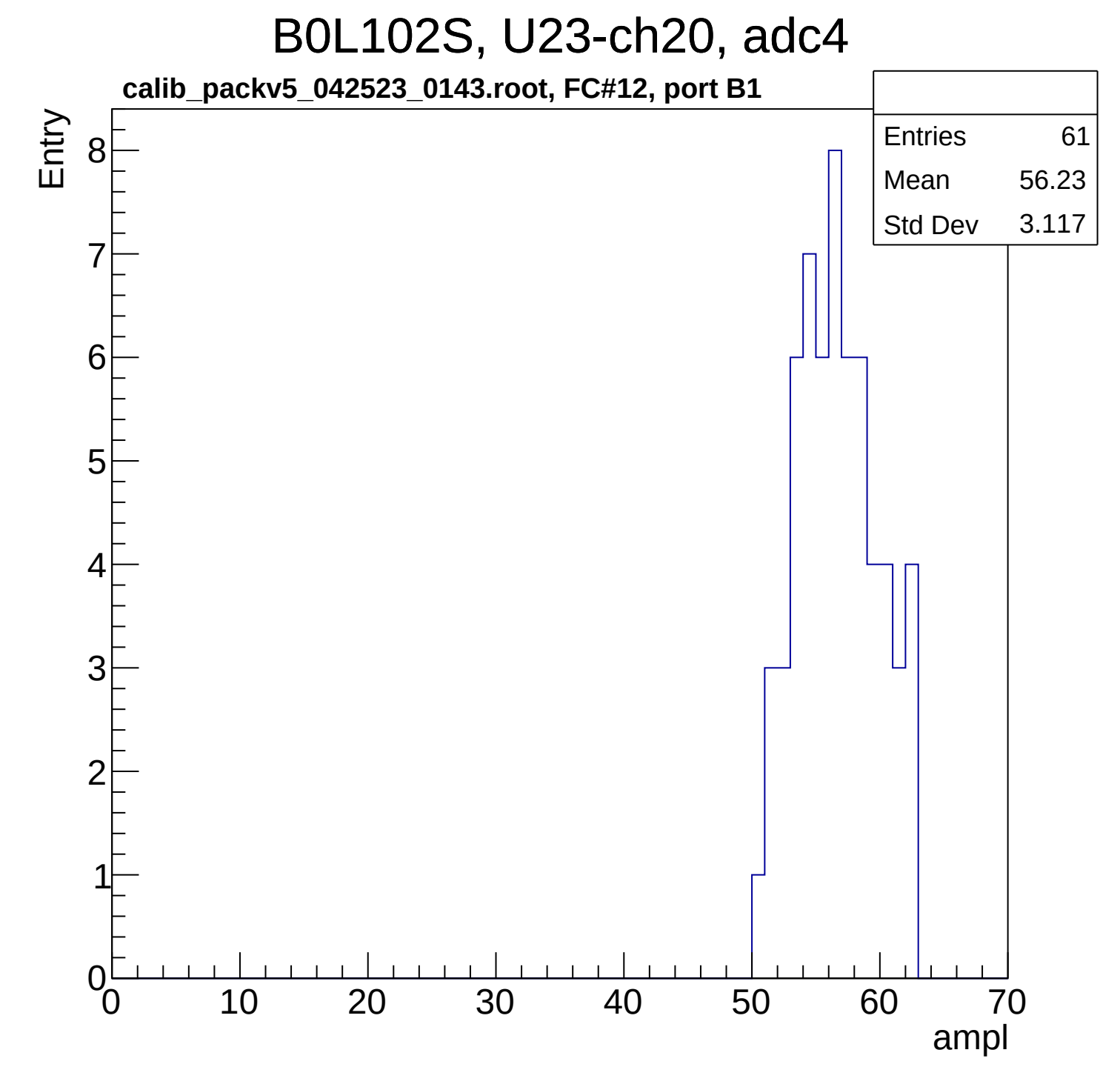

B0L102S, U23-ch19, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

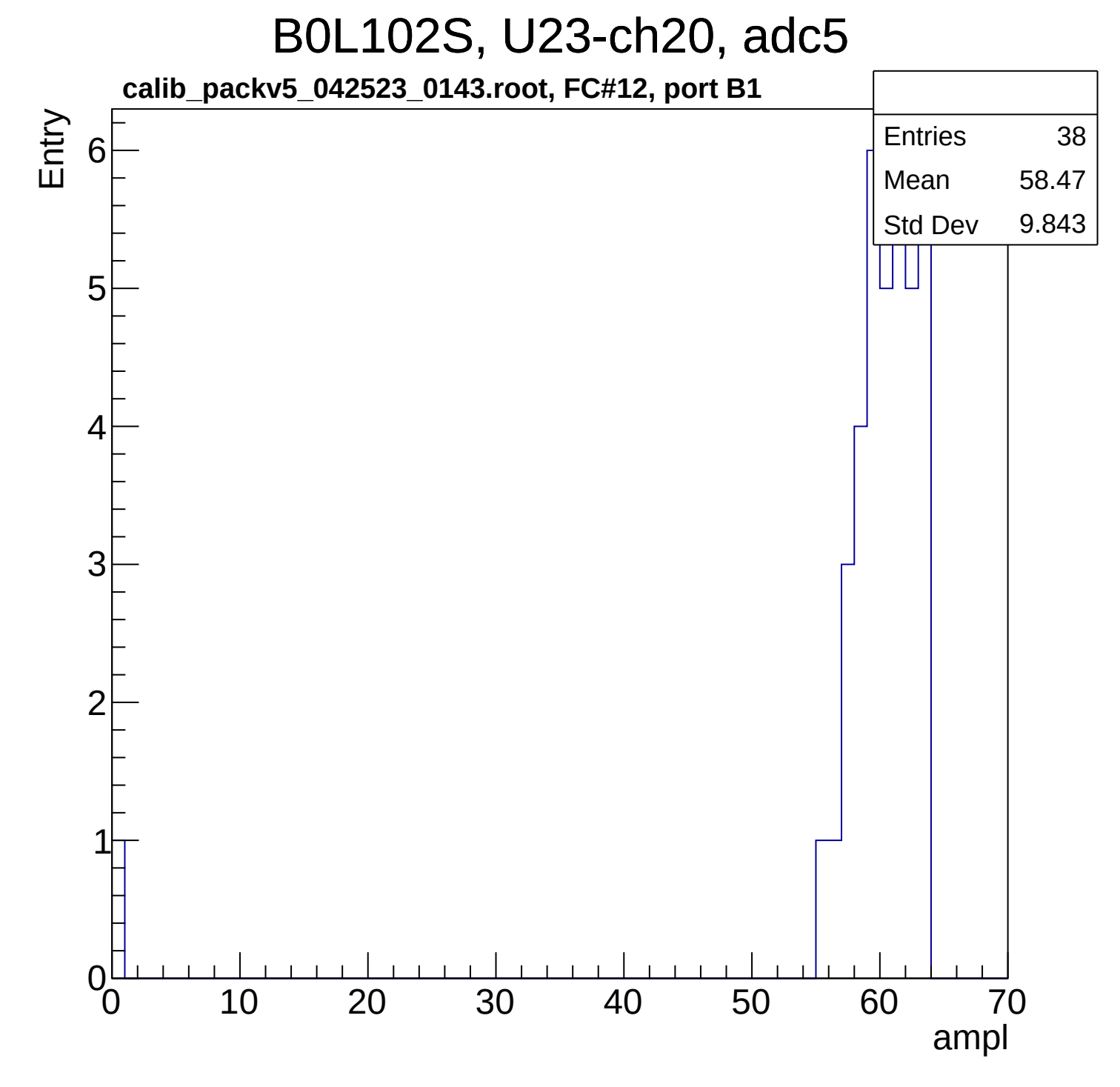

B0L102S, U23-ch20, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 28.46
Std Dev 4.706
Gaus mean : 29.3217
Gaus Width: 3.6927
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

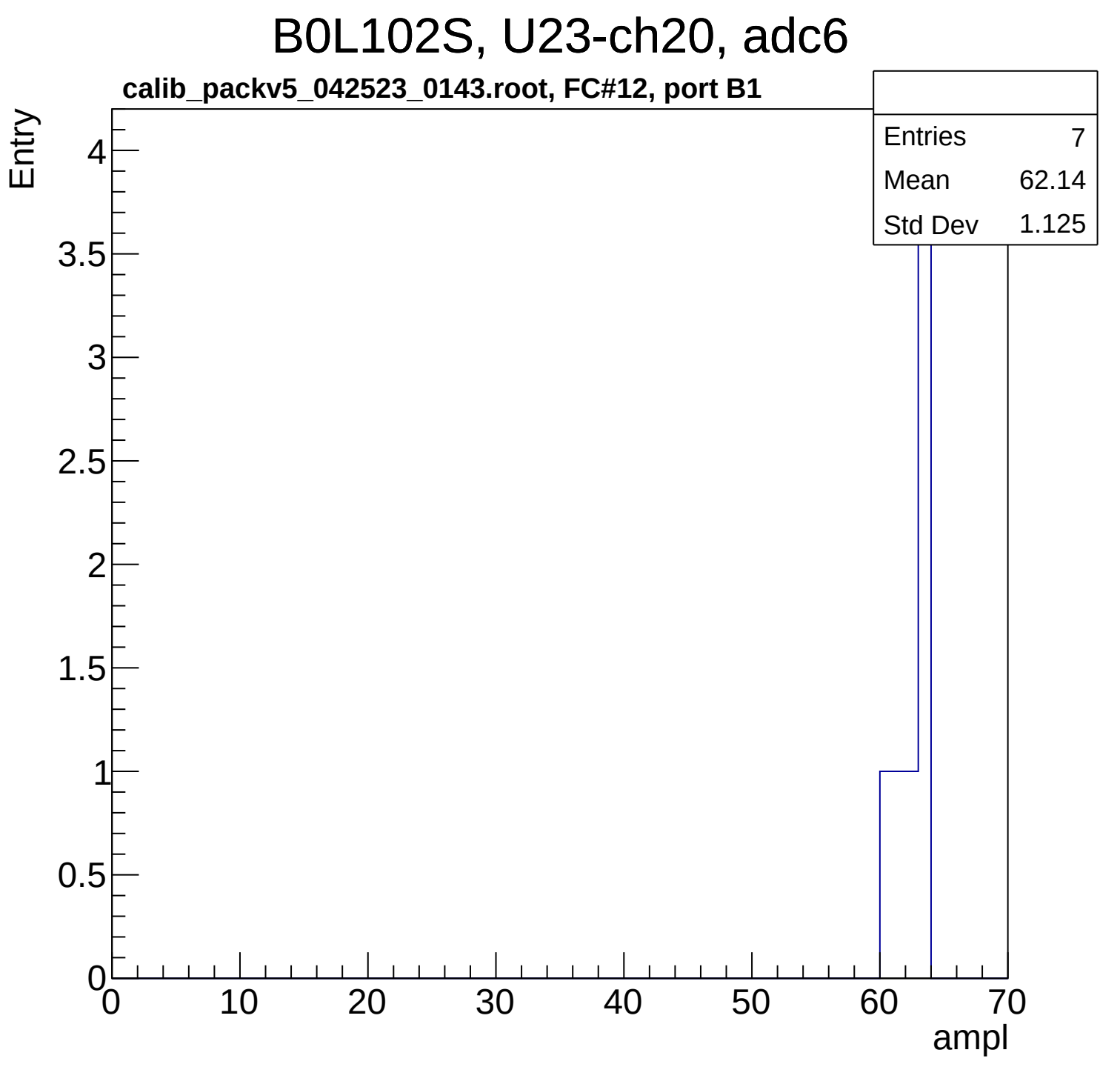

B0L102S, U23-ch20, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 36.62
Std Dev 3.036
Gaus mean : 36.8929
Gaus Width: 3.2997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

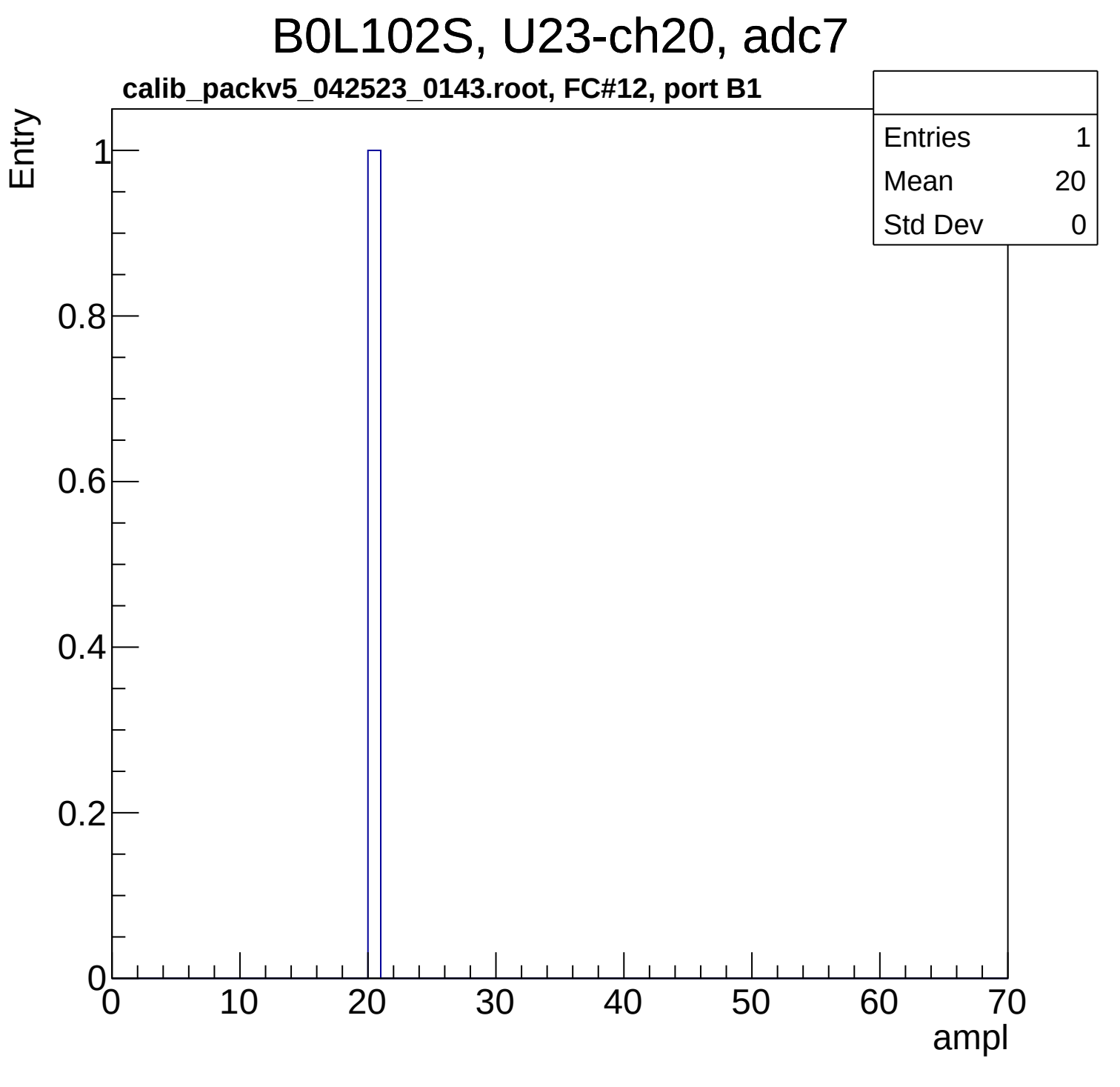

B0L102S, U23-ch20, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 42.63
Std Dev 3.732
Gaus mean : 43.0732
Gaus Width: 4.2434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

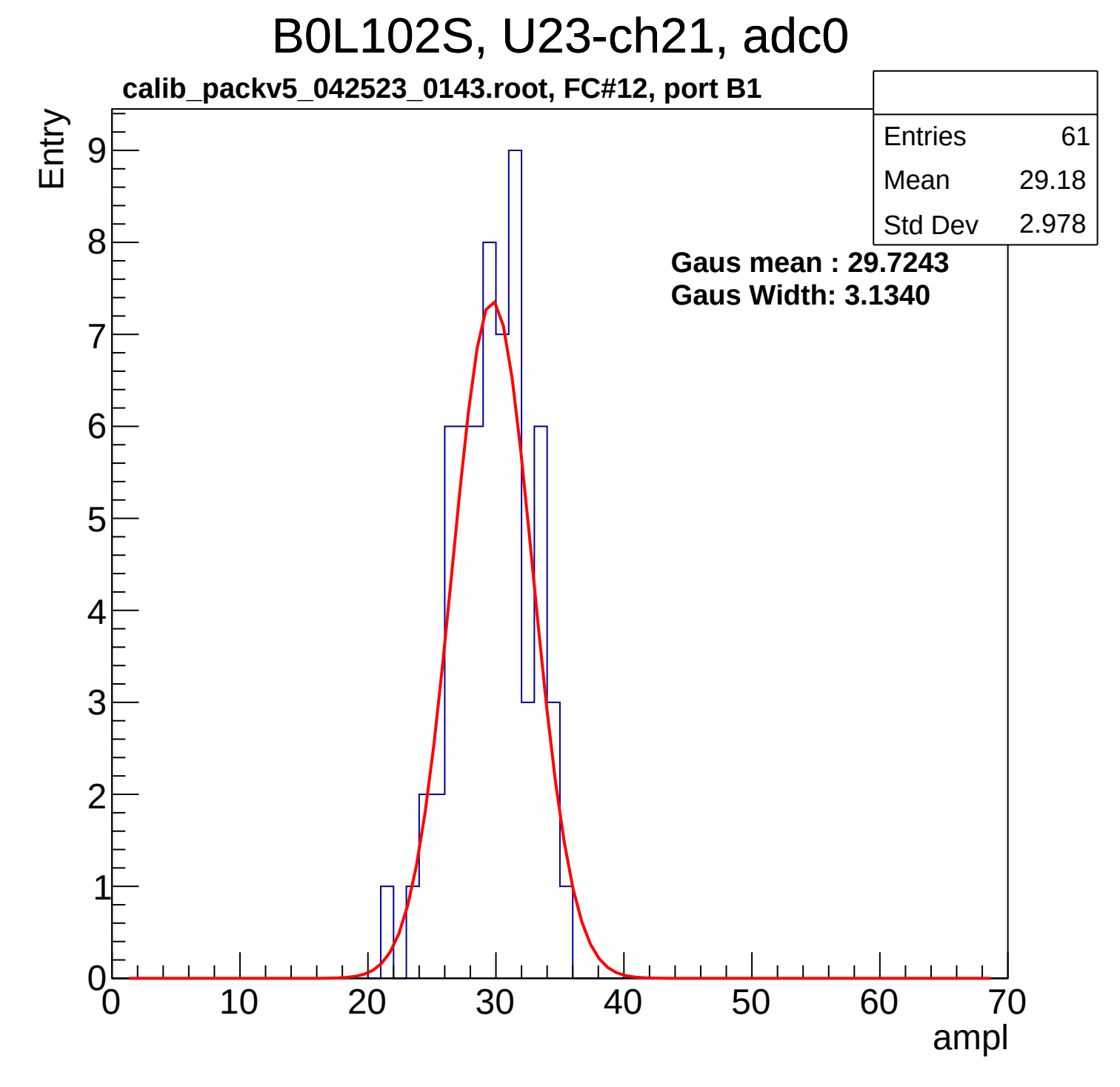

B0L102S, U23-ch20, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 49.11
Std Dev 3.442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

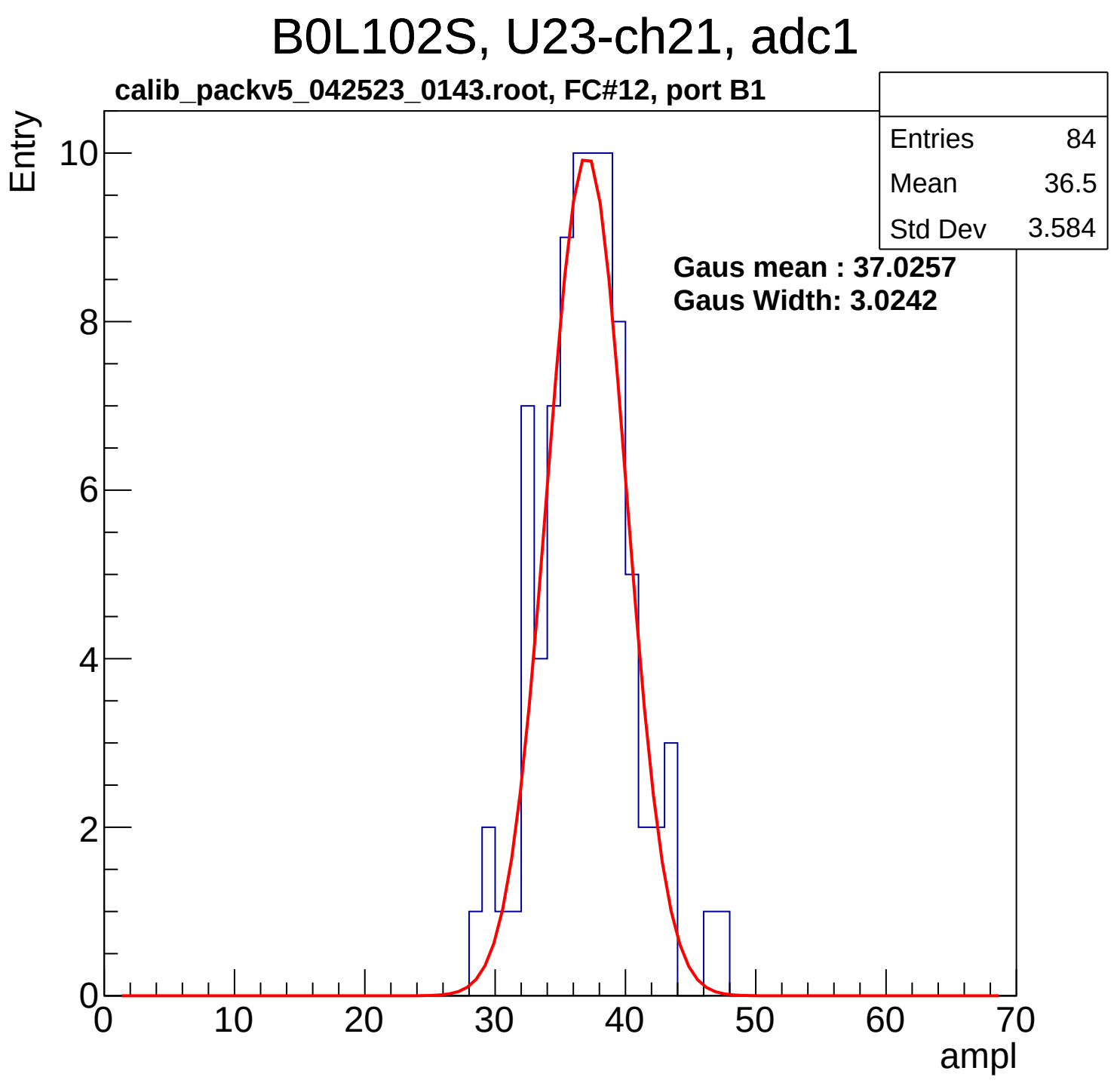

B0L102S, U23-ch20, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 56.23
Std Dev 3.117
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

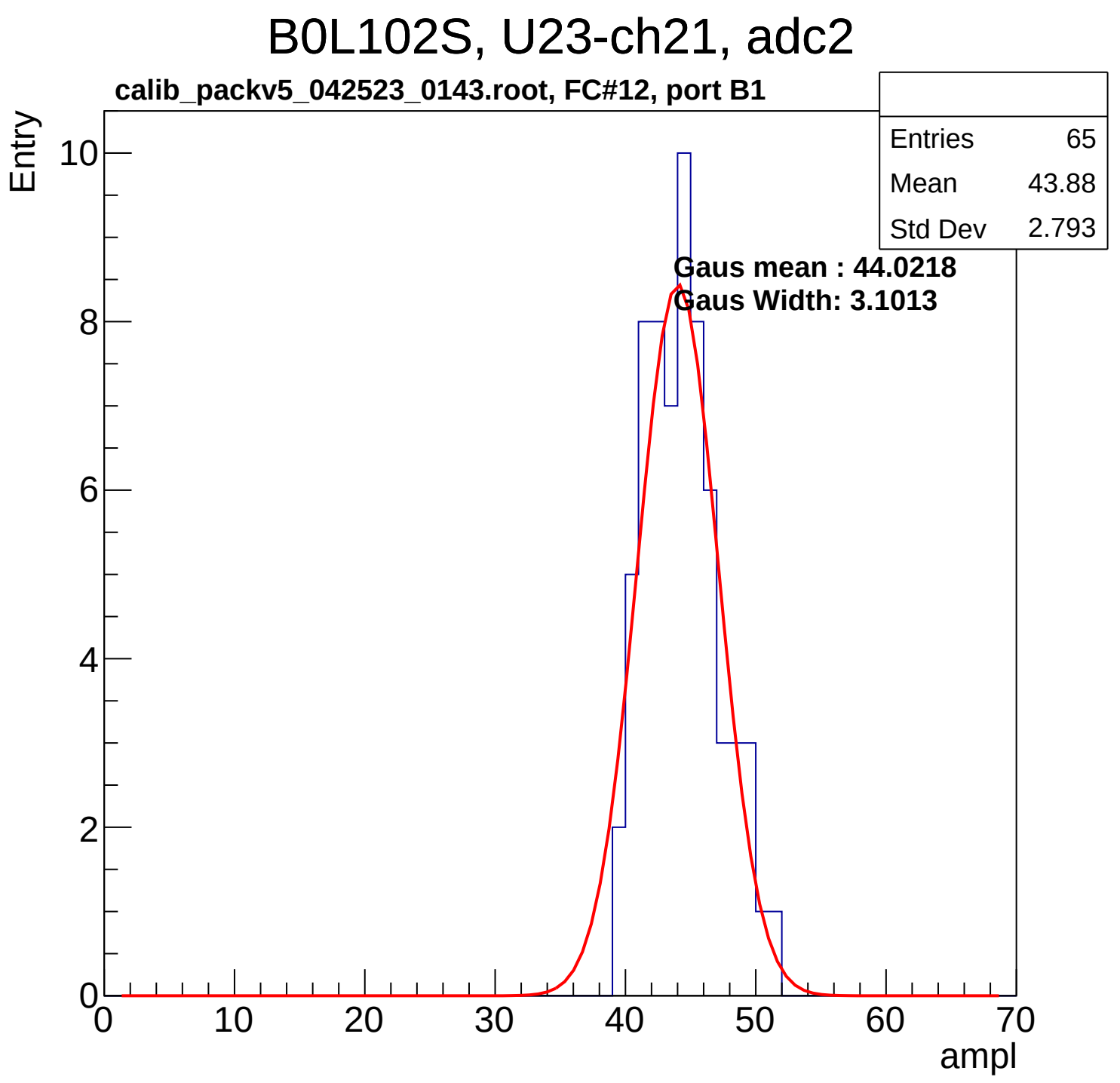

B0L102S, U23-ch20, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.47
Std Dev 9.843
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

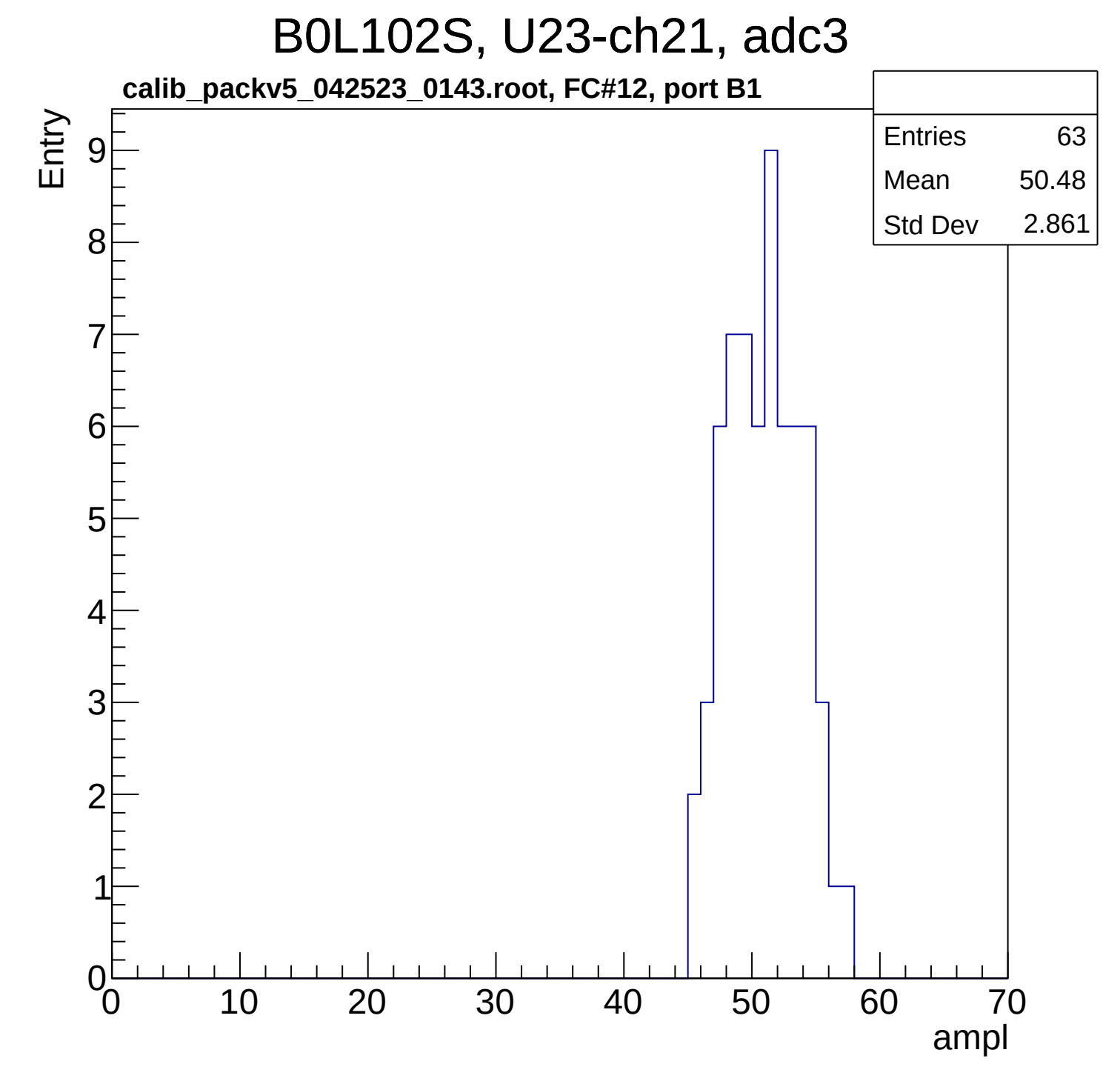

B0L102S, U23-ch20, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.14
Std Dev 1.125
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

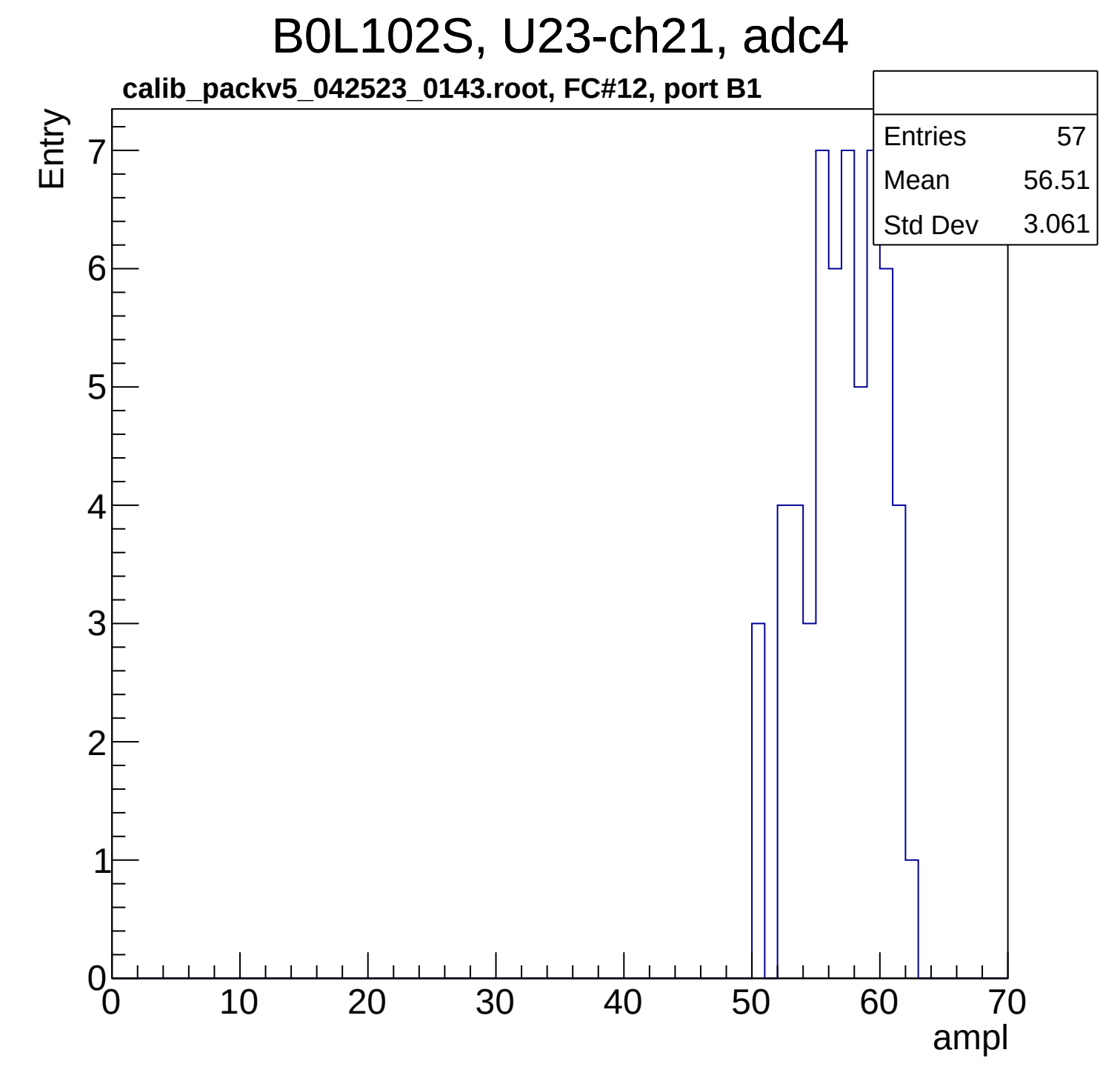

B0L102S, U23-ch20, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

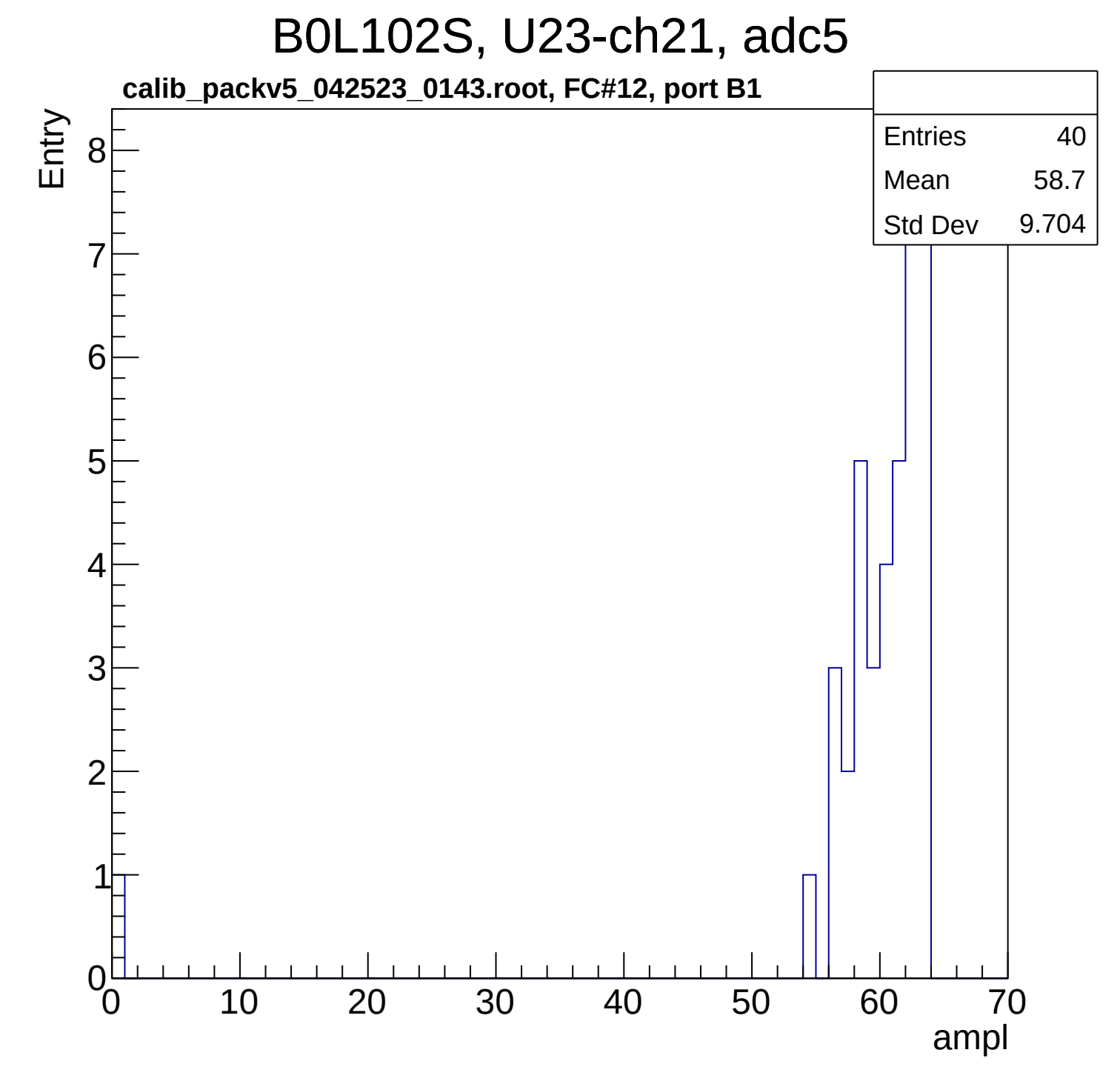

B0L102S, U23-ch21, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 29.18
Std Dev 2.978
Gaus mean : 29.7243
Gaus Width: 3.1340
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch21, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 36.5
Std Dev 3.584
Gaus mean : 37.0257
Gaus Width: 3.0242
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch21, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.88
Std Dev 2.793
Gaus mean : 44.0218
Gaus Width: 3.1013
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch21, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 50.48
Std Dev 2.861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch21, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 56.51
Std Dev 3.061
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch21, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 58.7
Std Dev 9.704
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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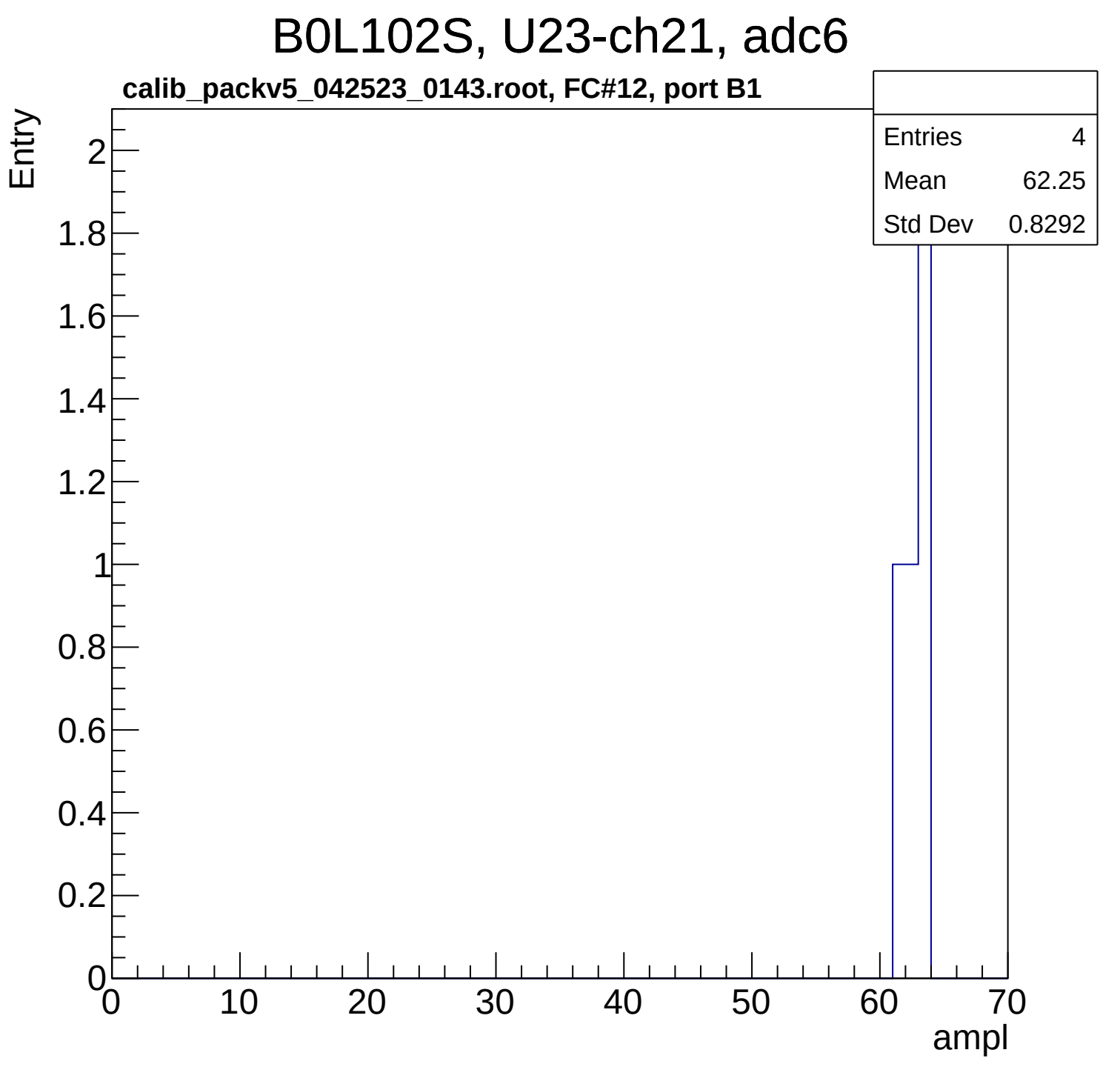
40
50
60
70


B0L102S, U23-ch21, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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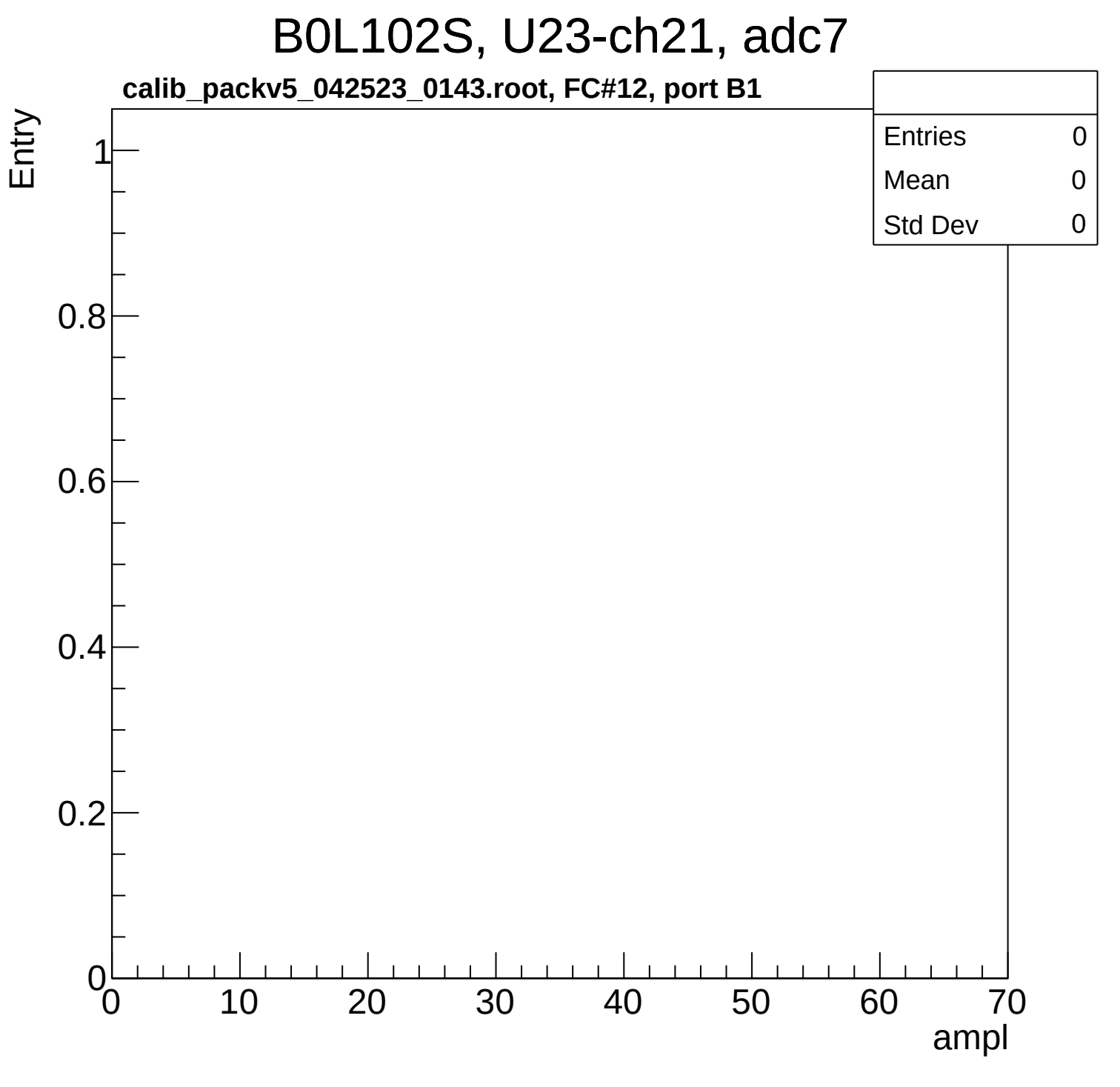
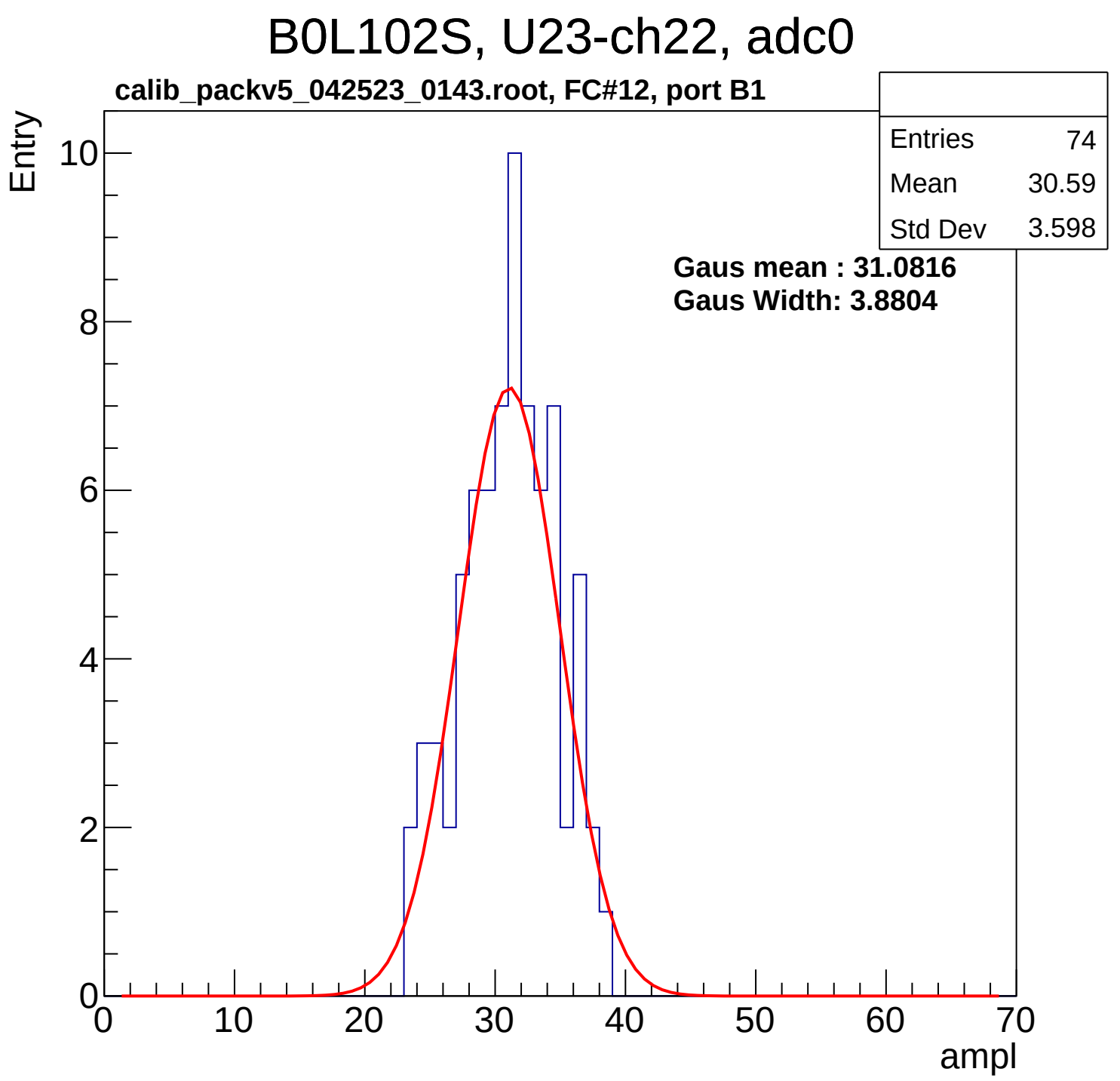
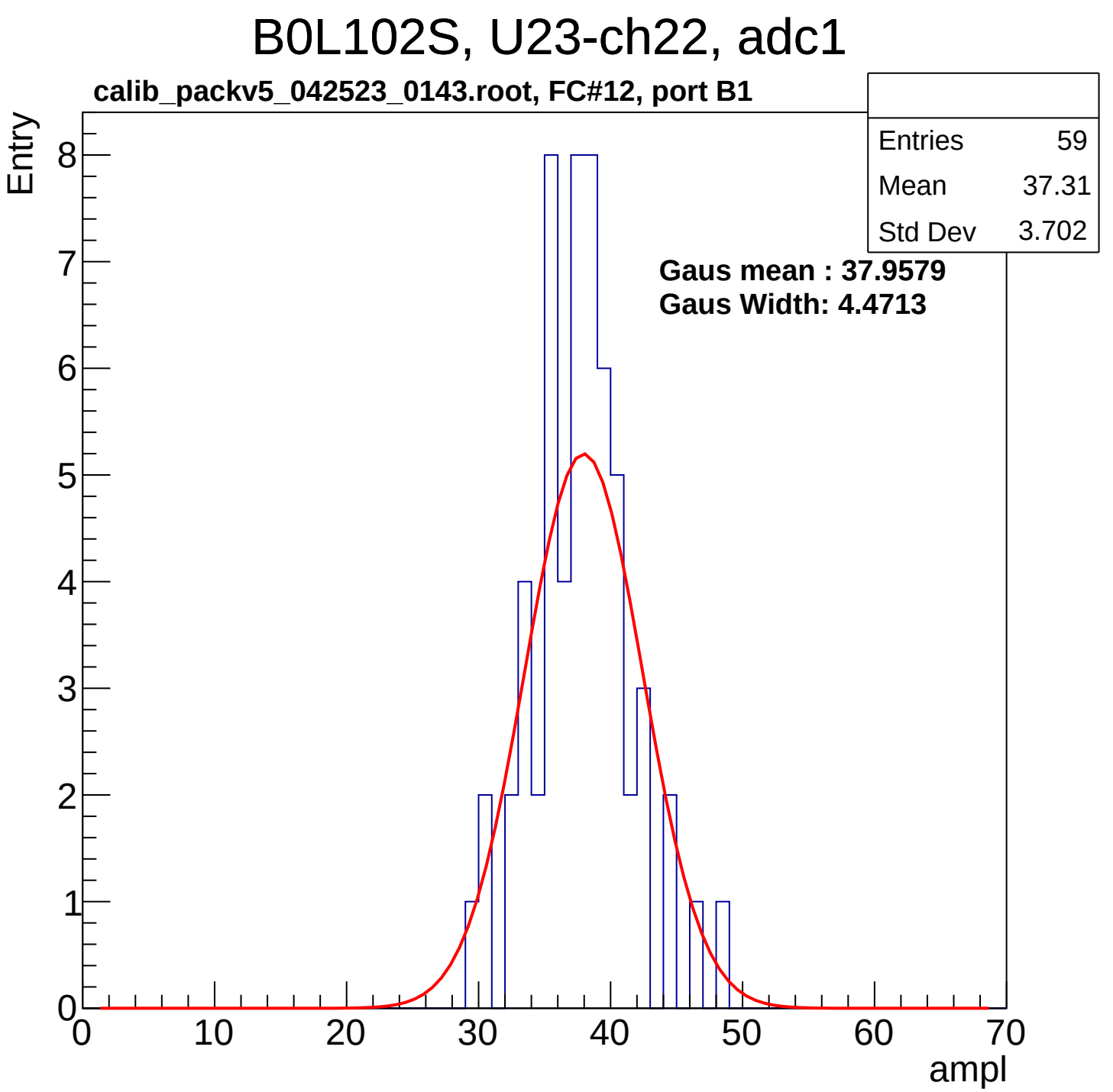
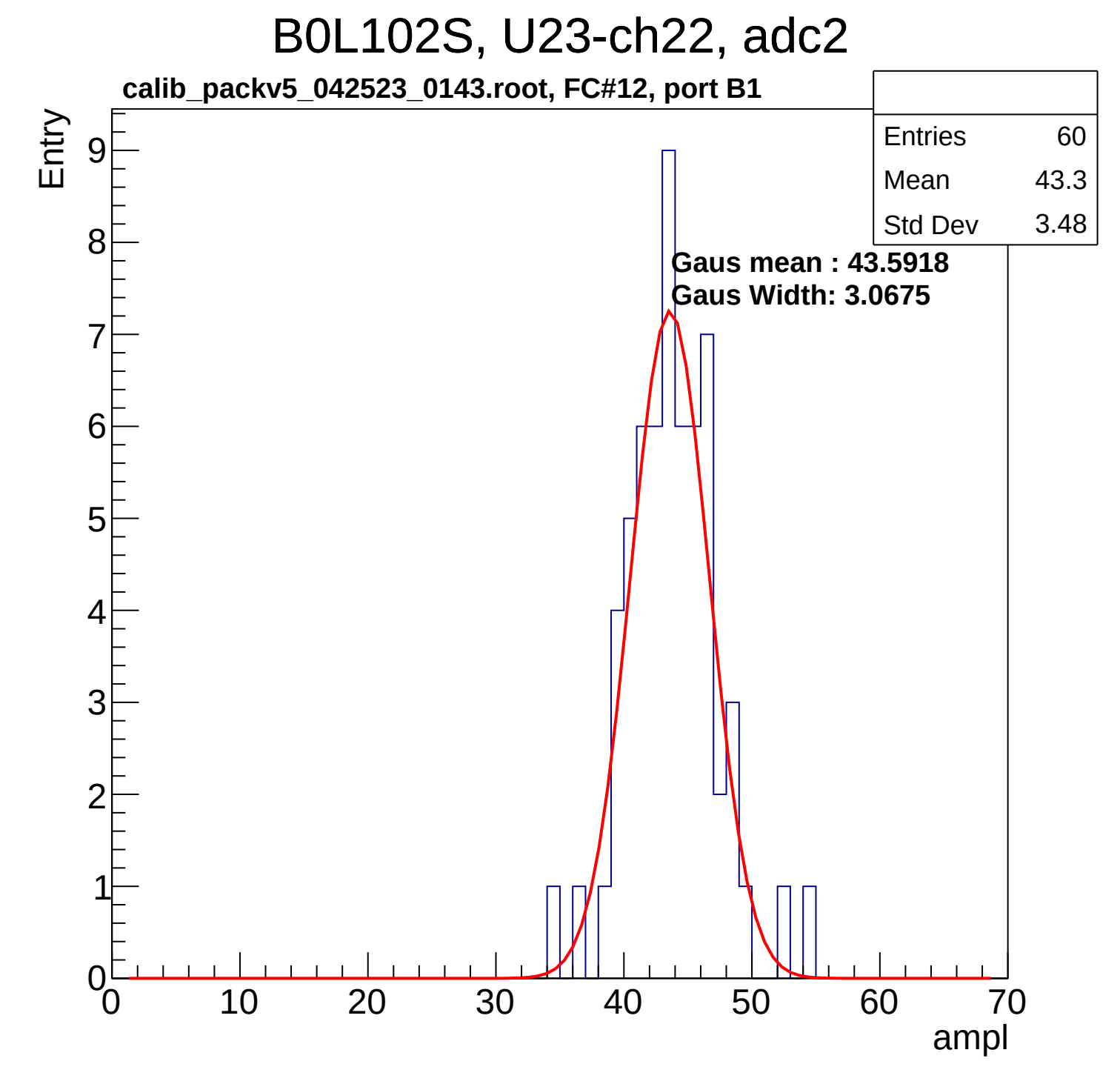
30
40
50
60
70


B0L102S, U23-ch21, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch22, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 30.59
Std Dev 3.598
Gaus mean : 31.0816
Gaus Width: 3.8804
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch22, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 37.31
Std Dev 3.702
Gaus mean : 37.9579
Gaus Width: 4.4713
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch22, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 43.3
Std Dev 3.48
Gaus mean : 43.5918
Gaus Width: 3.0675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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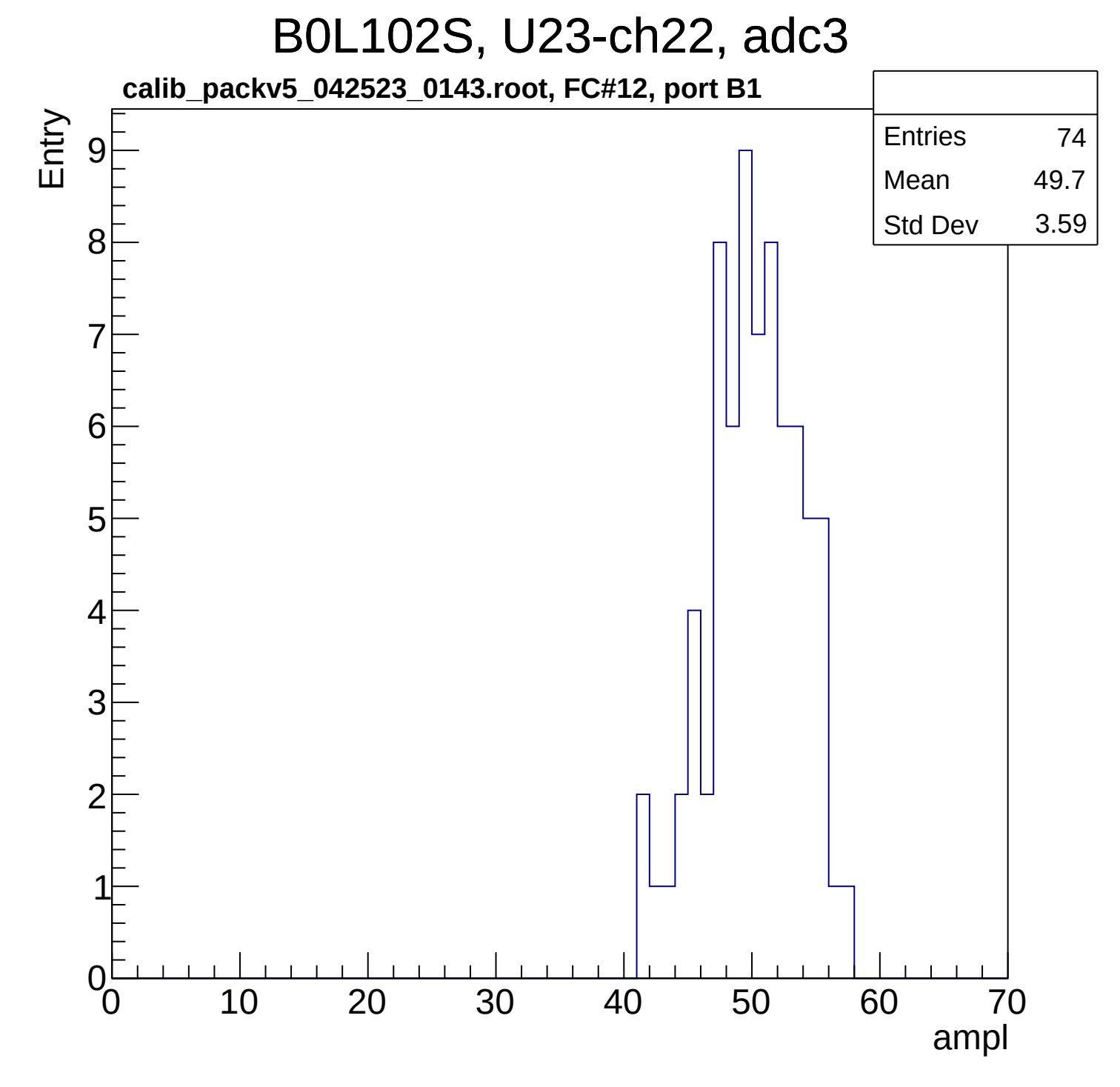
B0L102S, U23-ch22, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 49.7
Std Dev 3.59
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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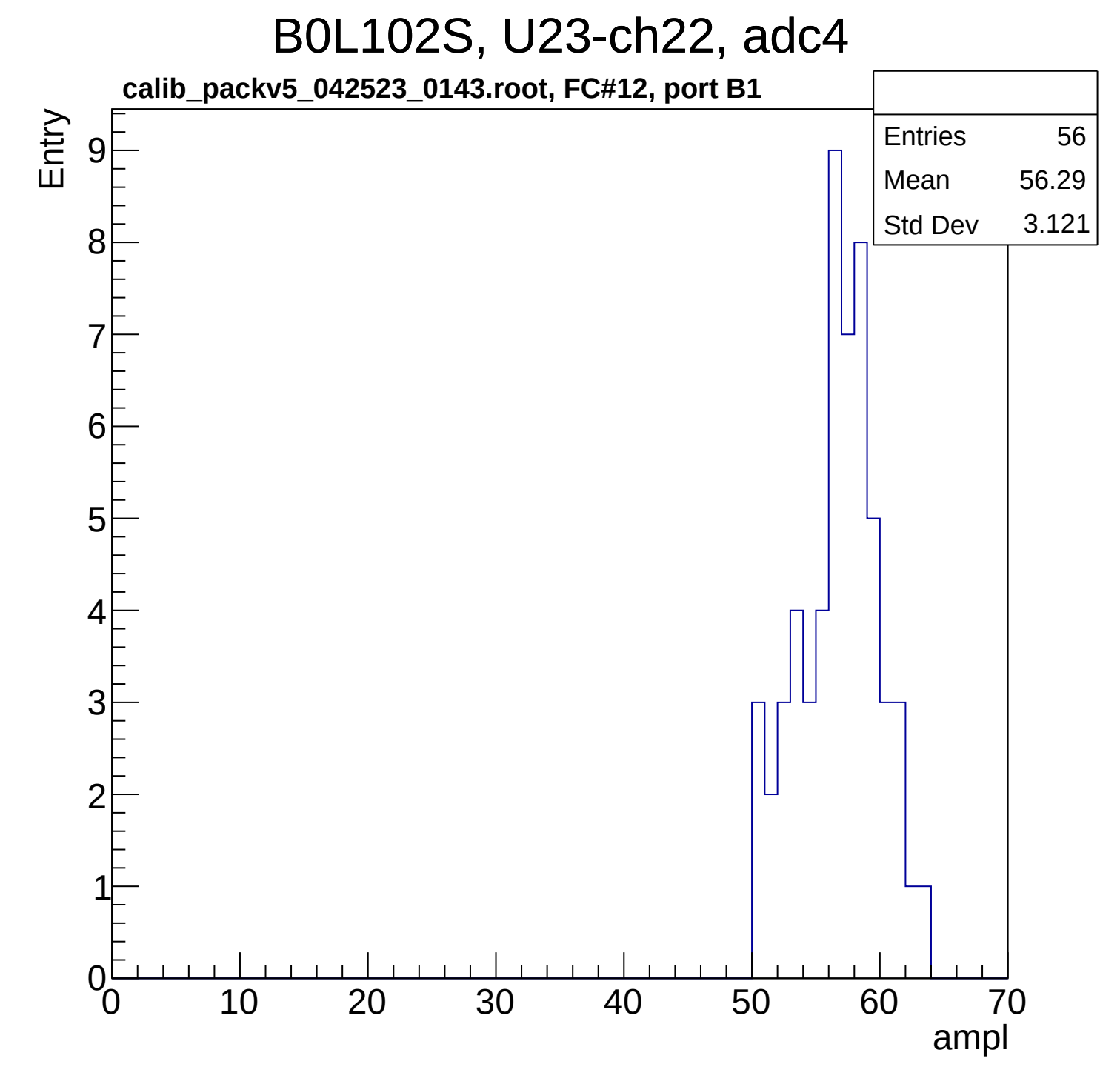
B0L102S, U23-ch22, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 56.29
Std Dev 3.121
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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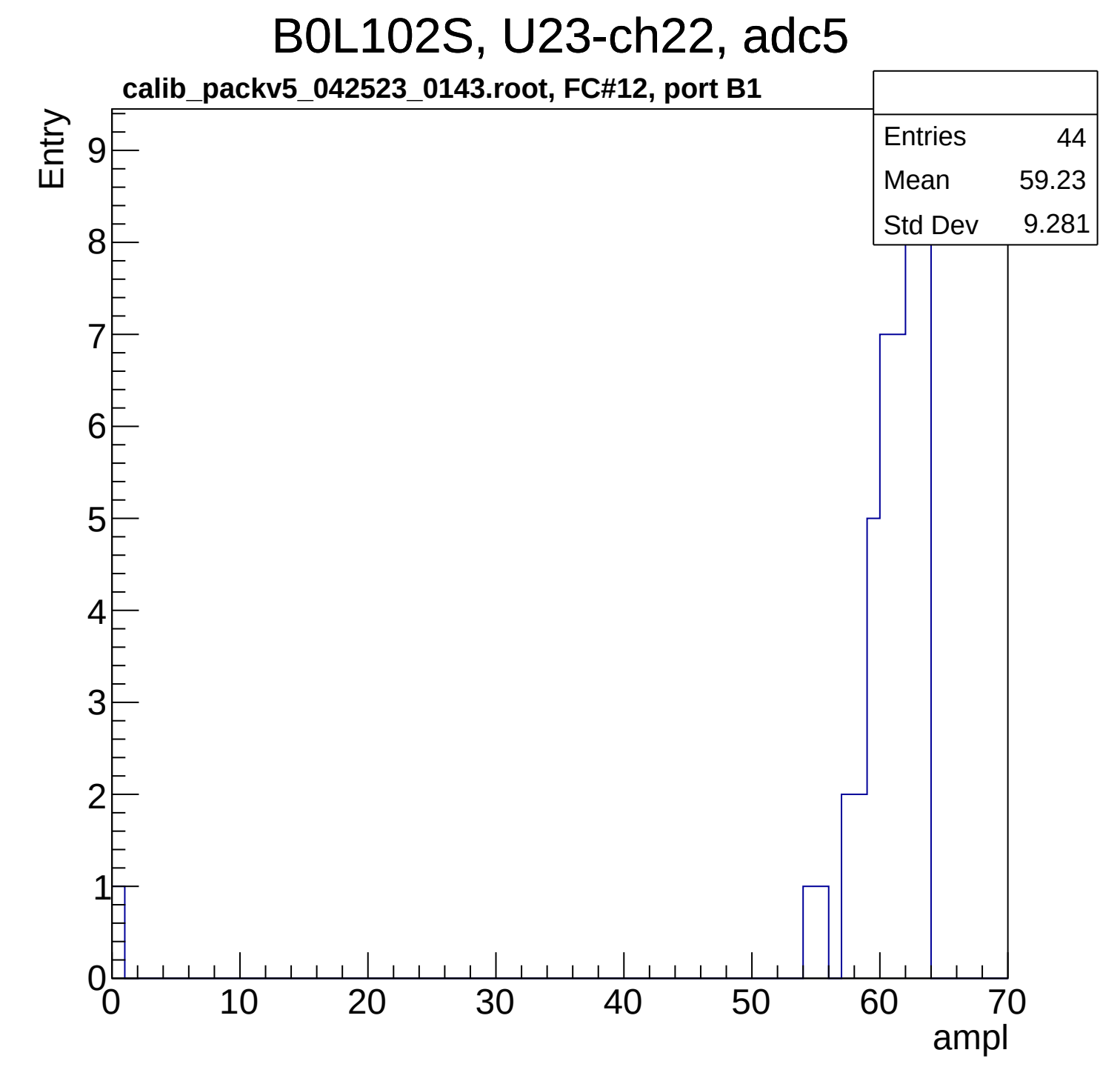
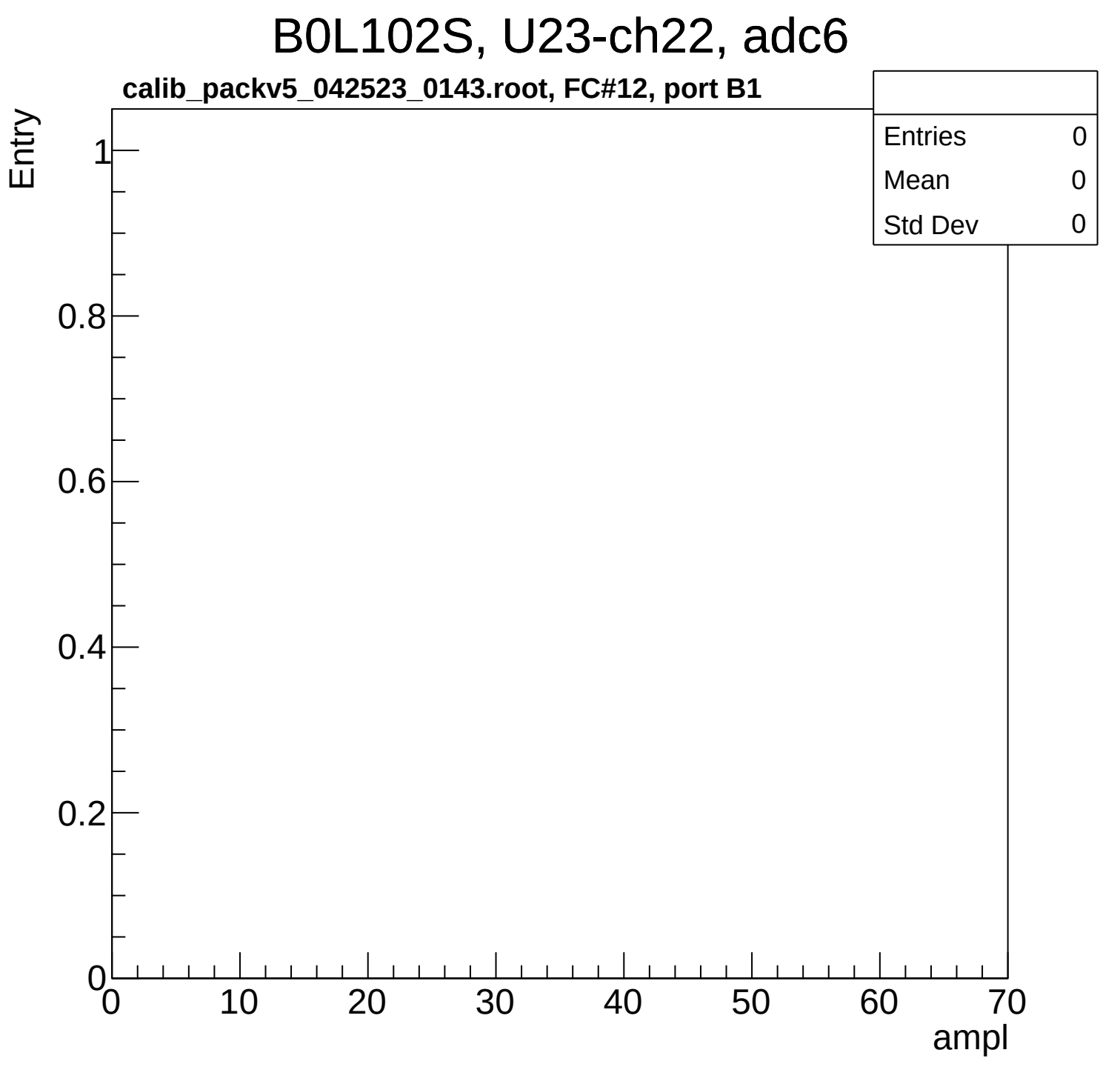
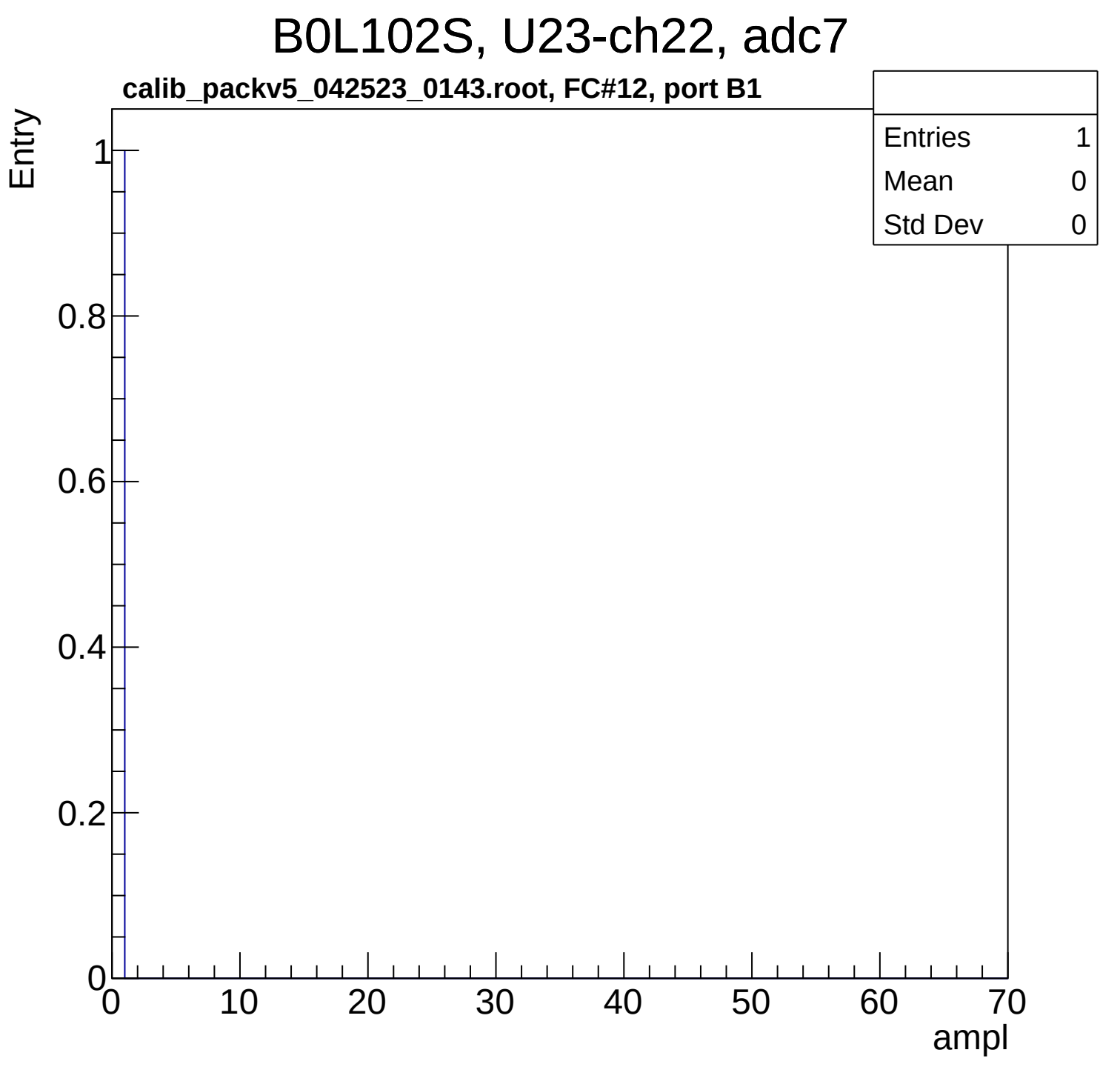

B0L102S, U23-ch22, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.23
Std Dev 9.281
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch22, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch22, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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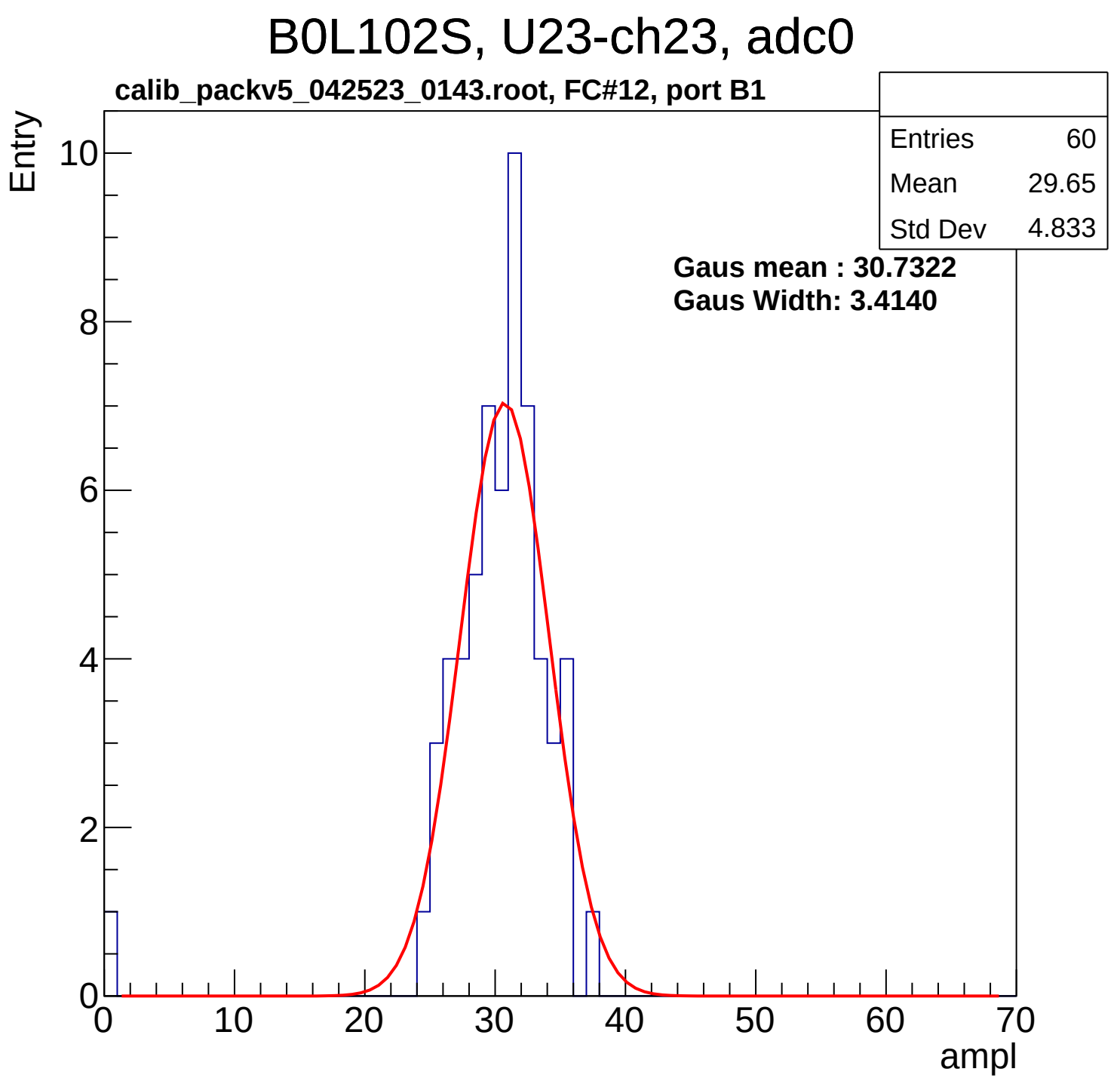
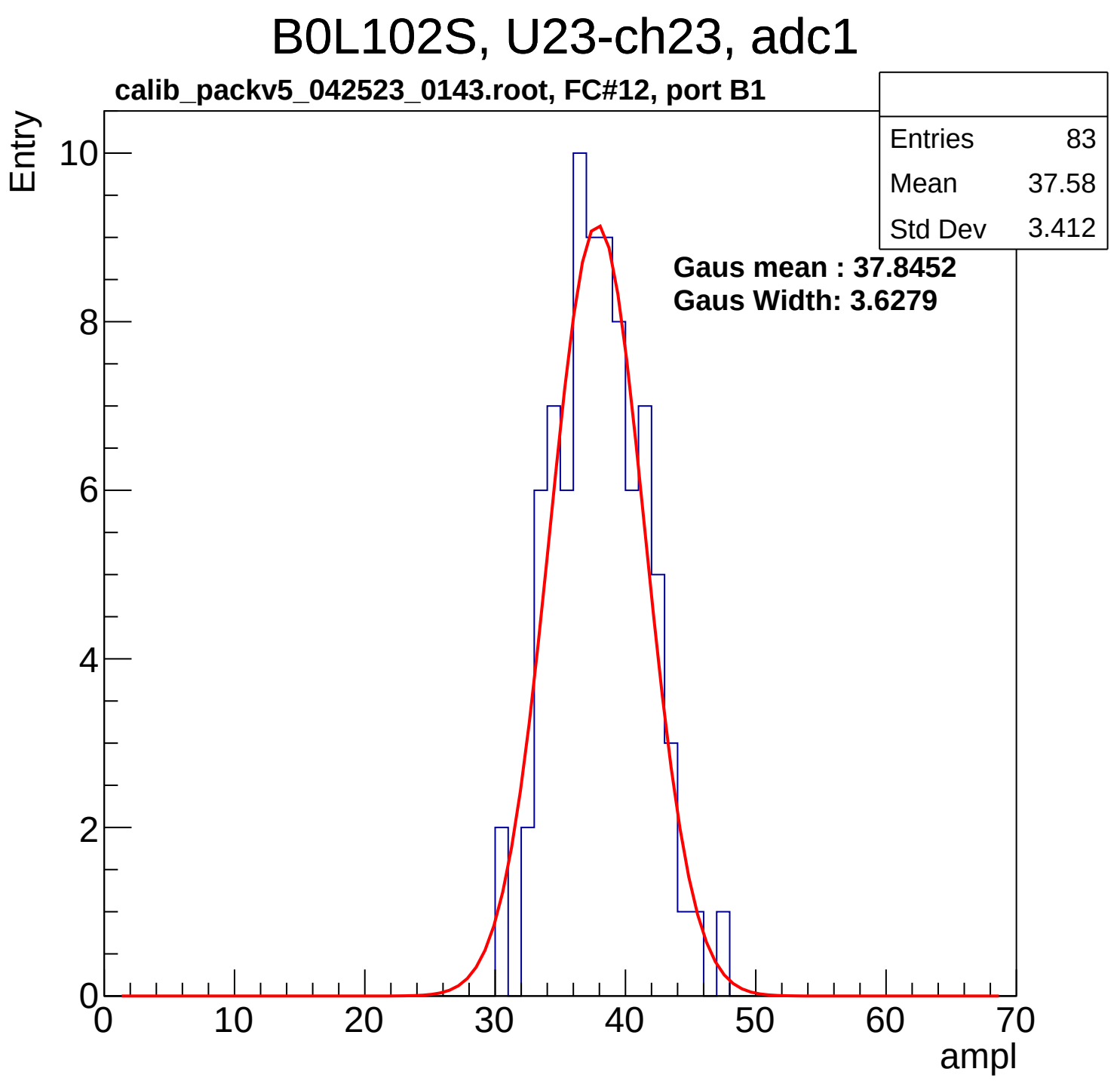
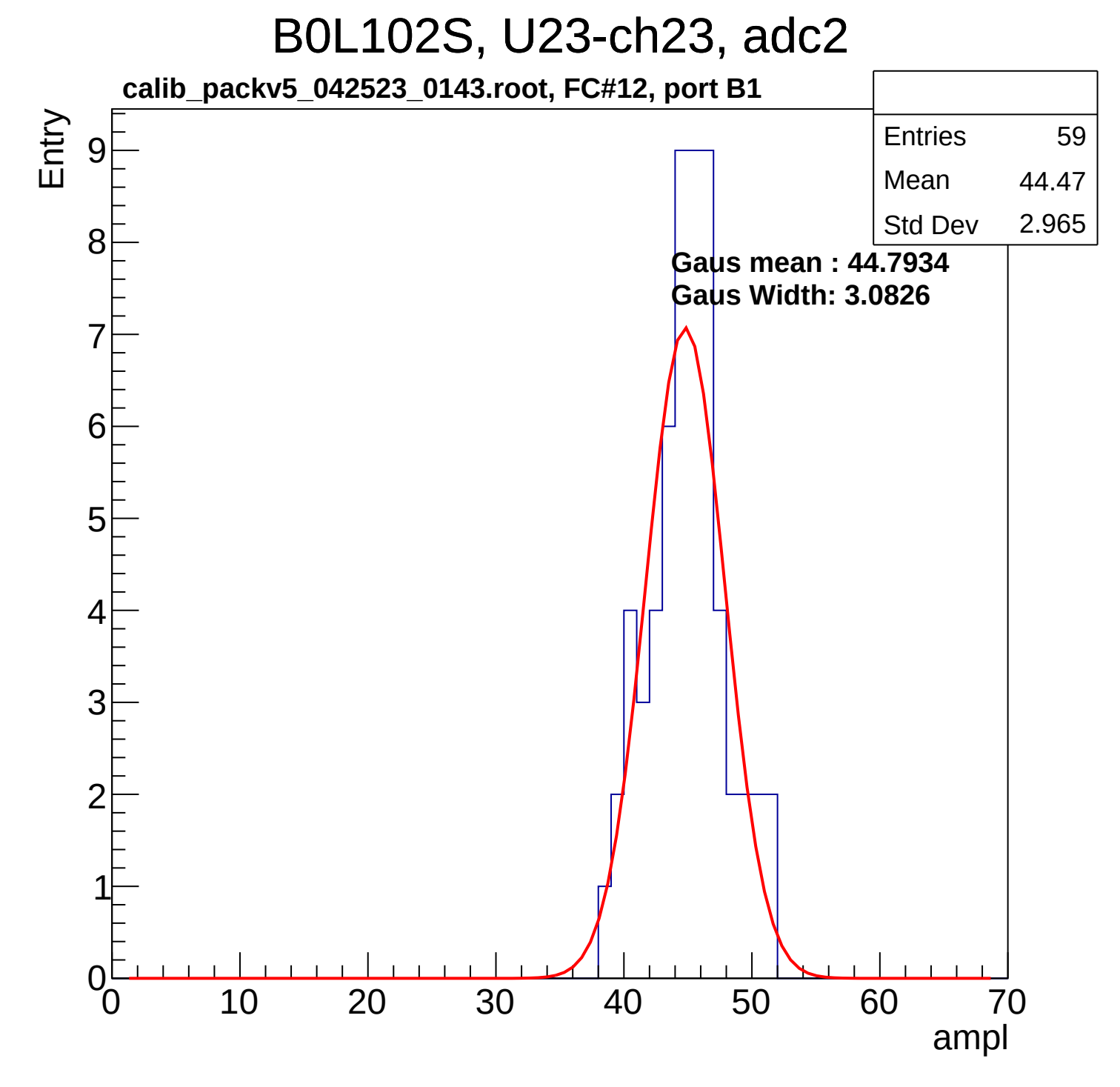
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch23, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 29.65
Std Dev 4.833
Gaus mean : 30.7322
Gaus Width: 3.4140
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch23, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 37.58
Std Dev 3.412
Gaus mean : 37.8452
Gaus Width: 3.6279
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch23, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 44.47
Std Dev 2.965
Gaus mean : 44.7934
Gaus Width: 3.0826
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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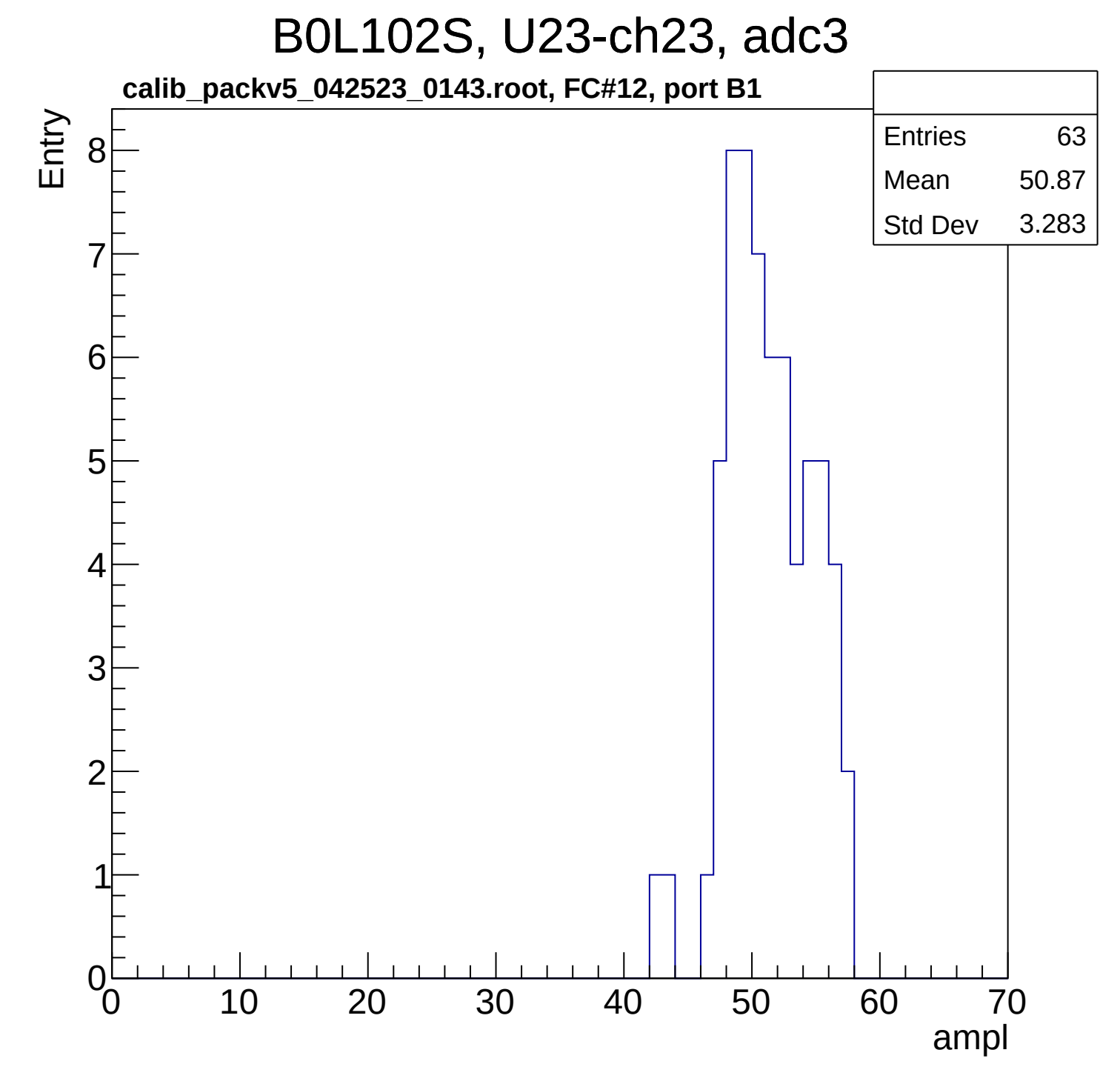
B0L102S, U23-ch23, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 50.87
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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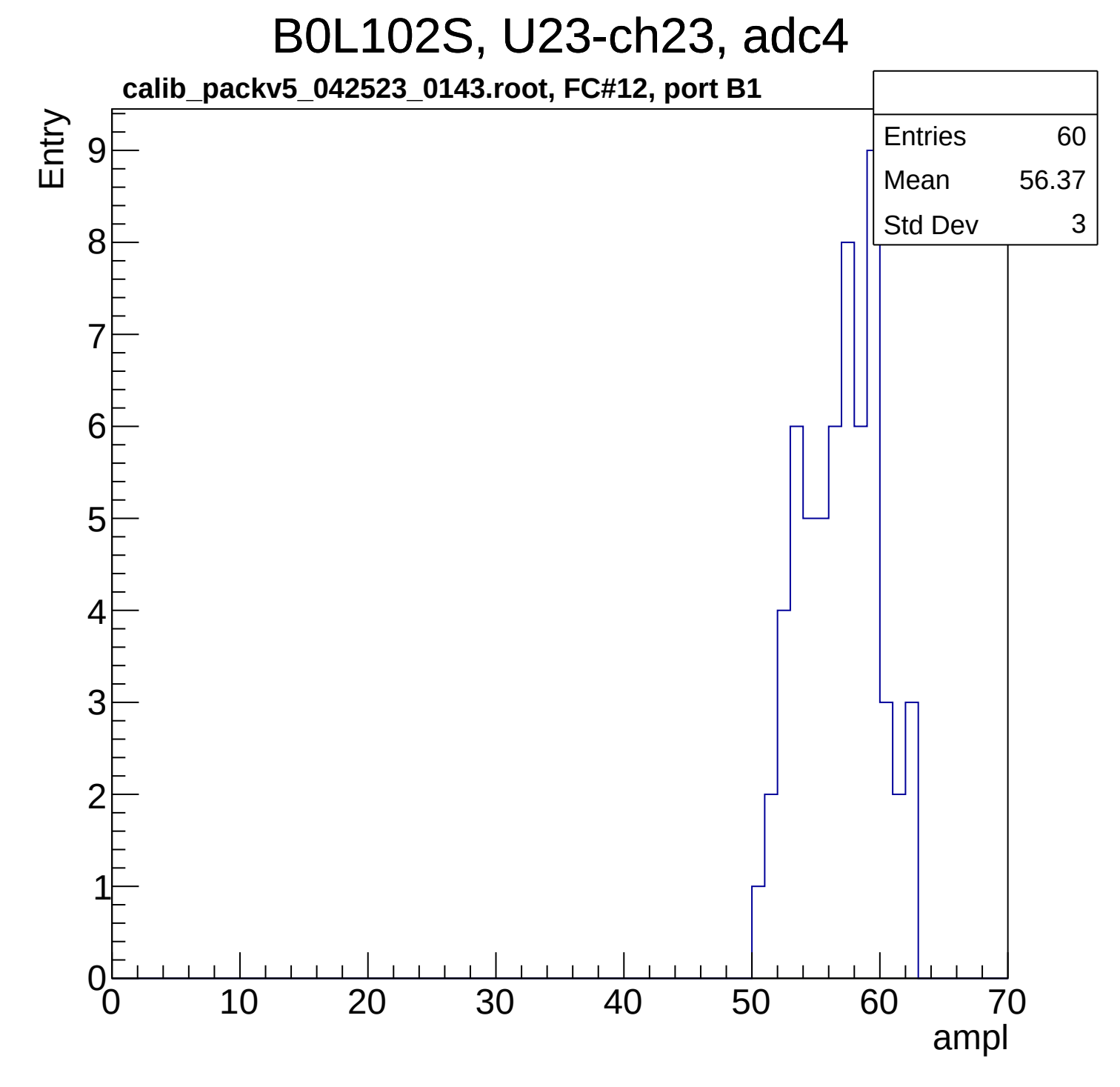
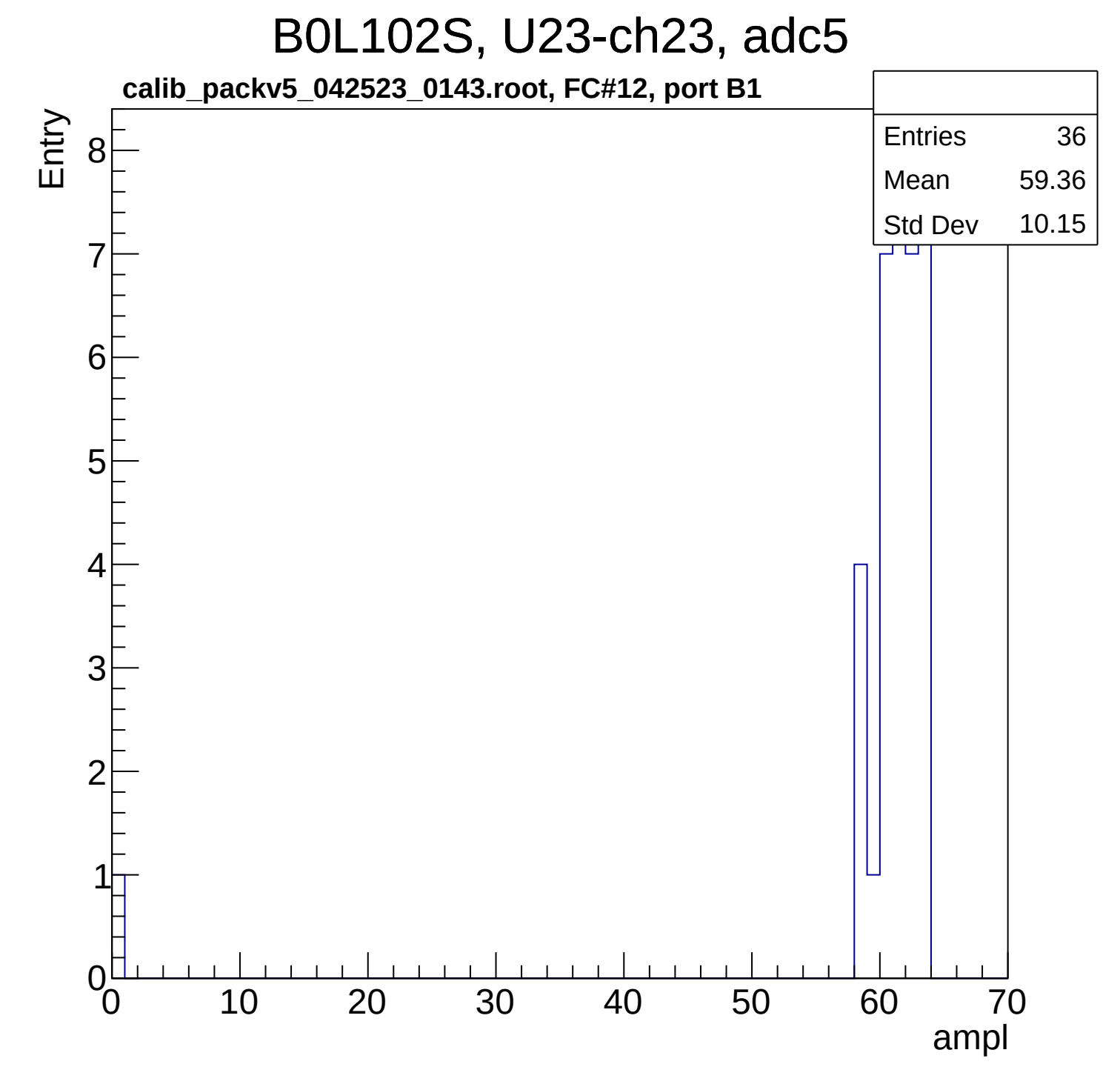

B0L102S, U23-ch23, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.37
Std Dev 3
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch23, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 59.36
Std Dev 10.15
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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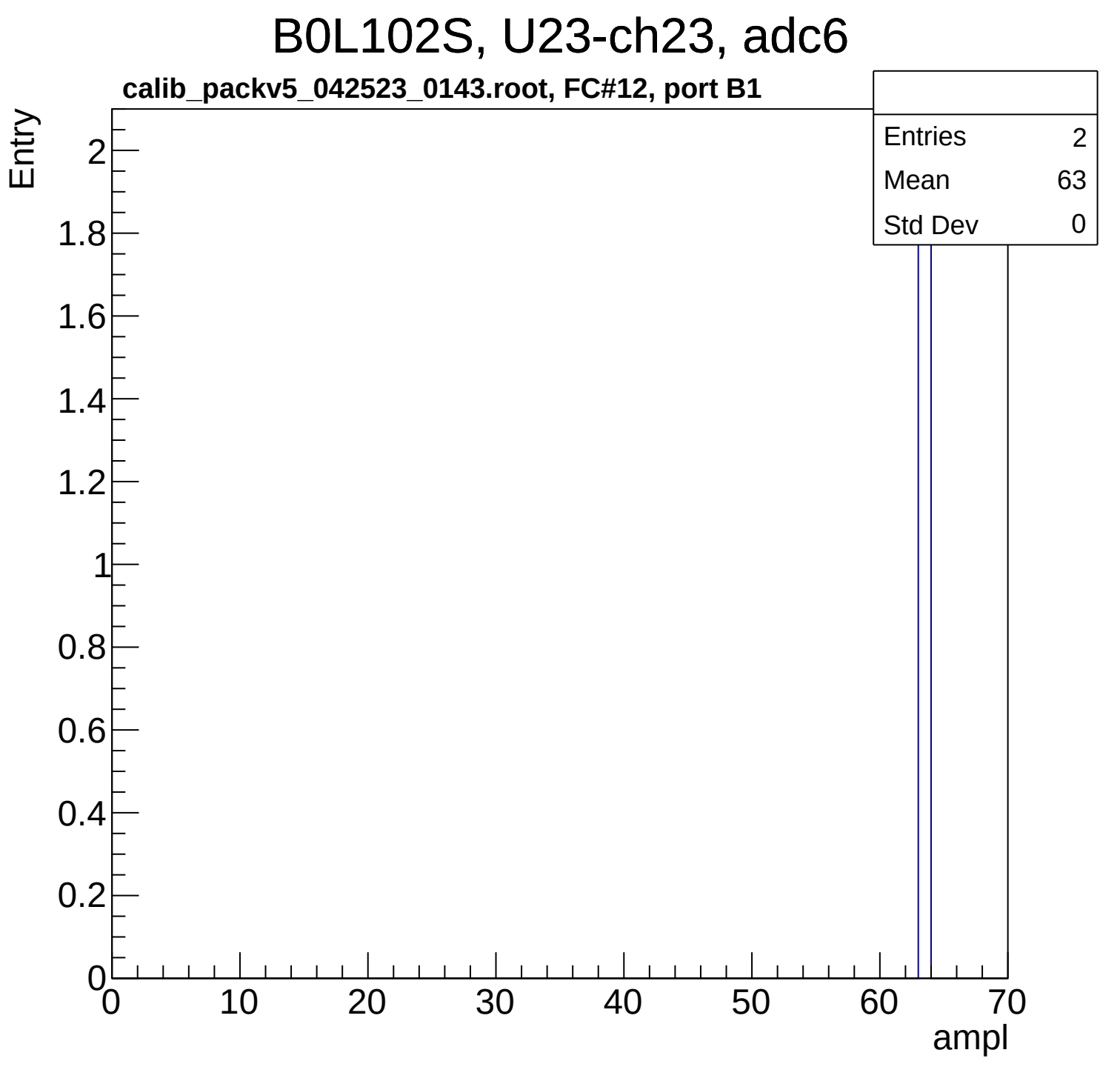
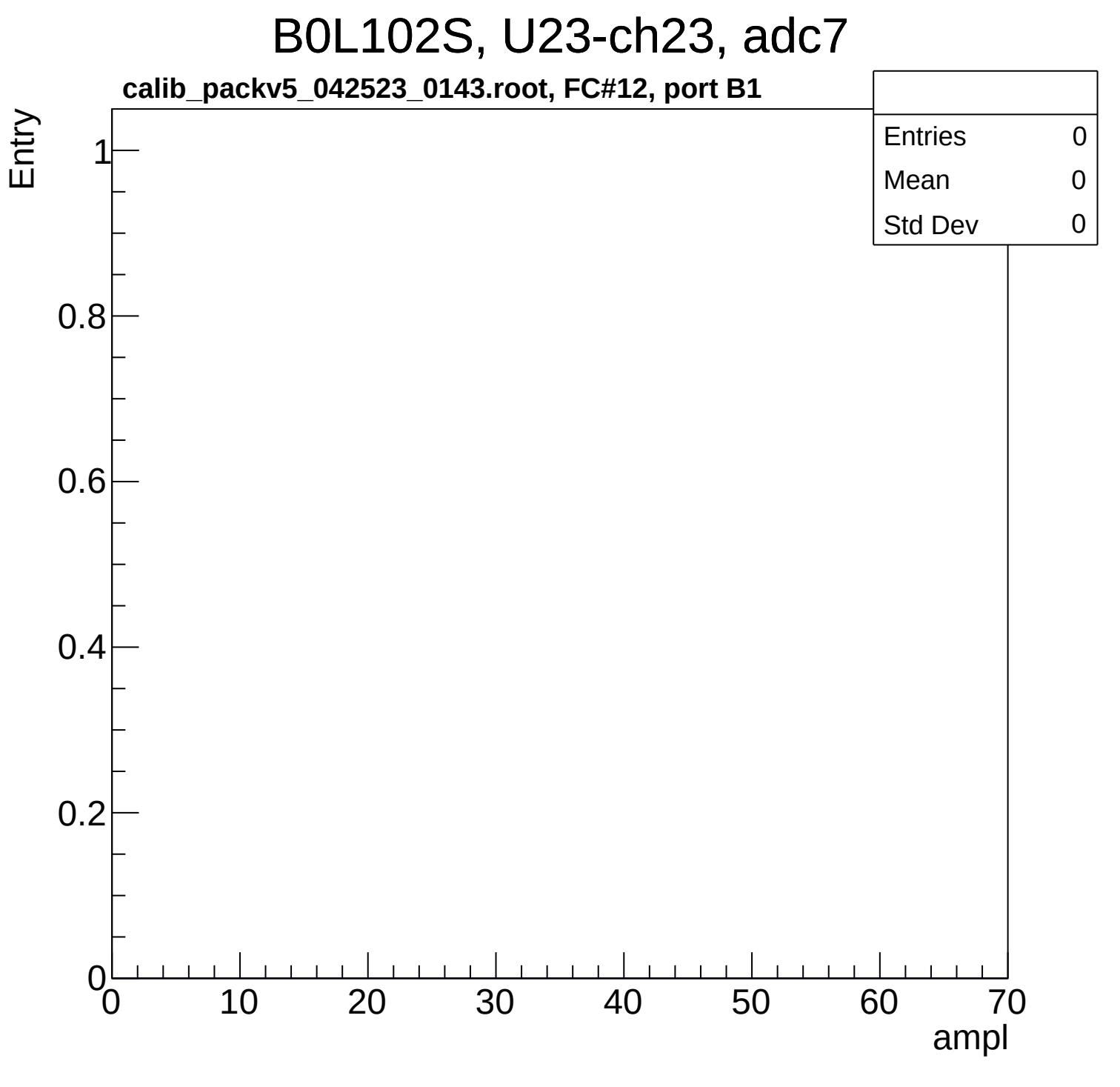
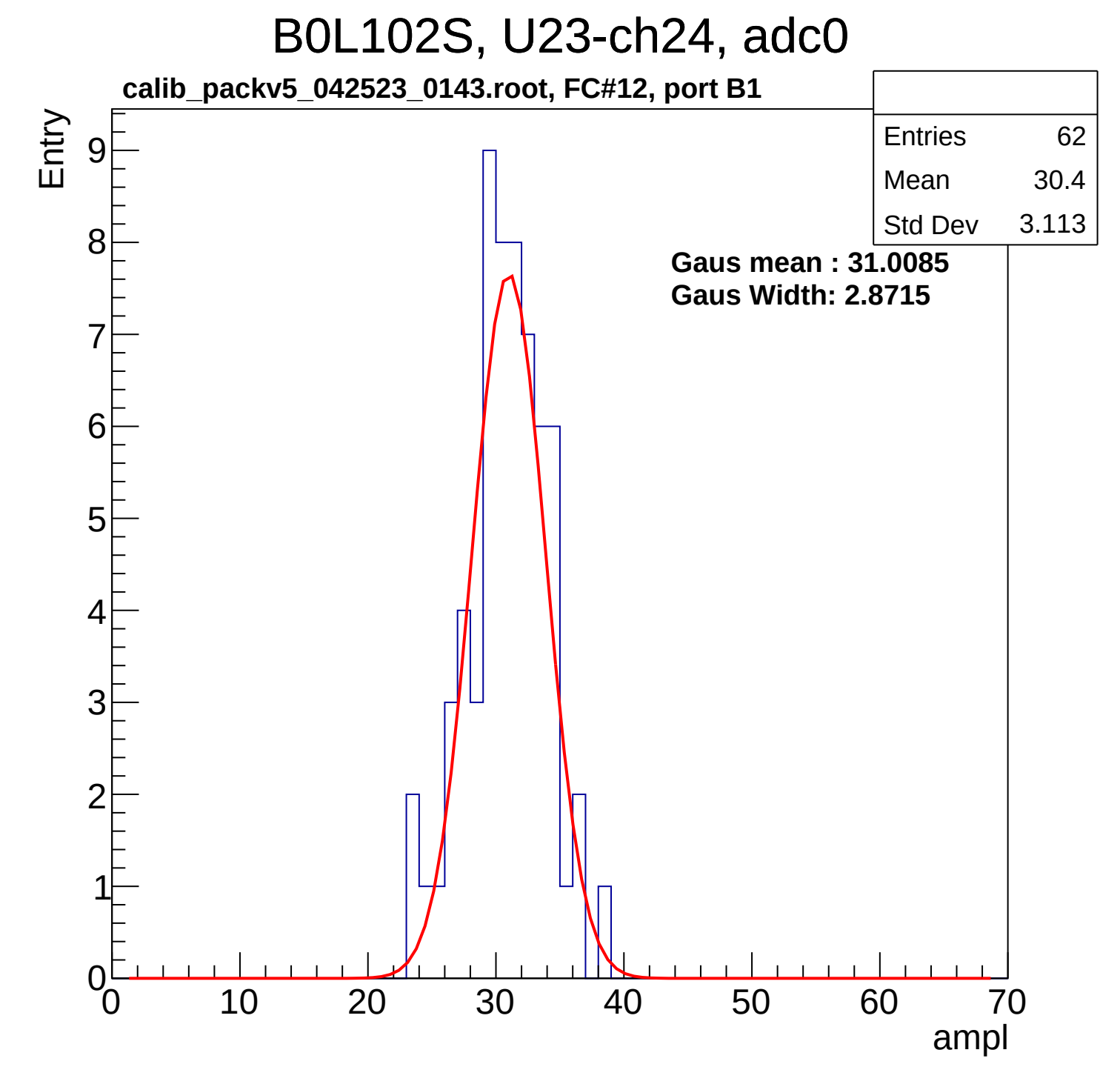
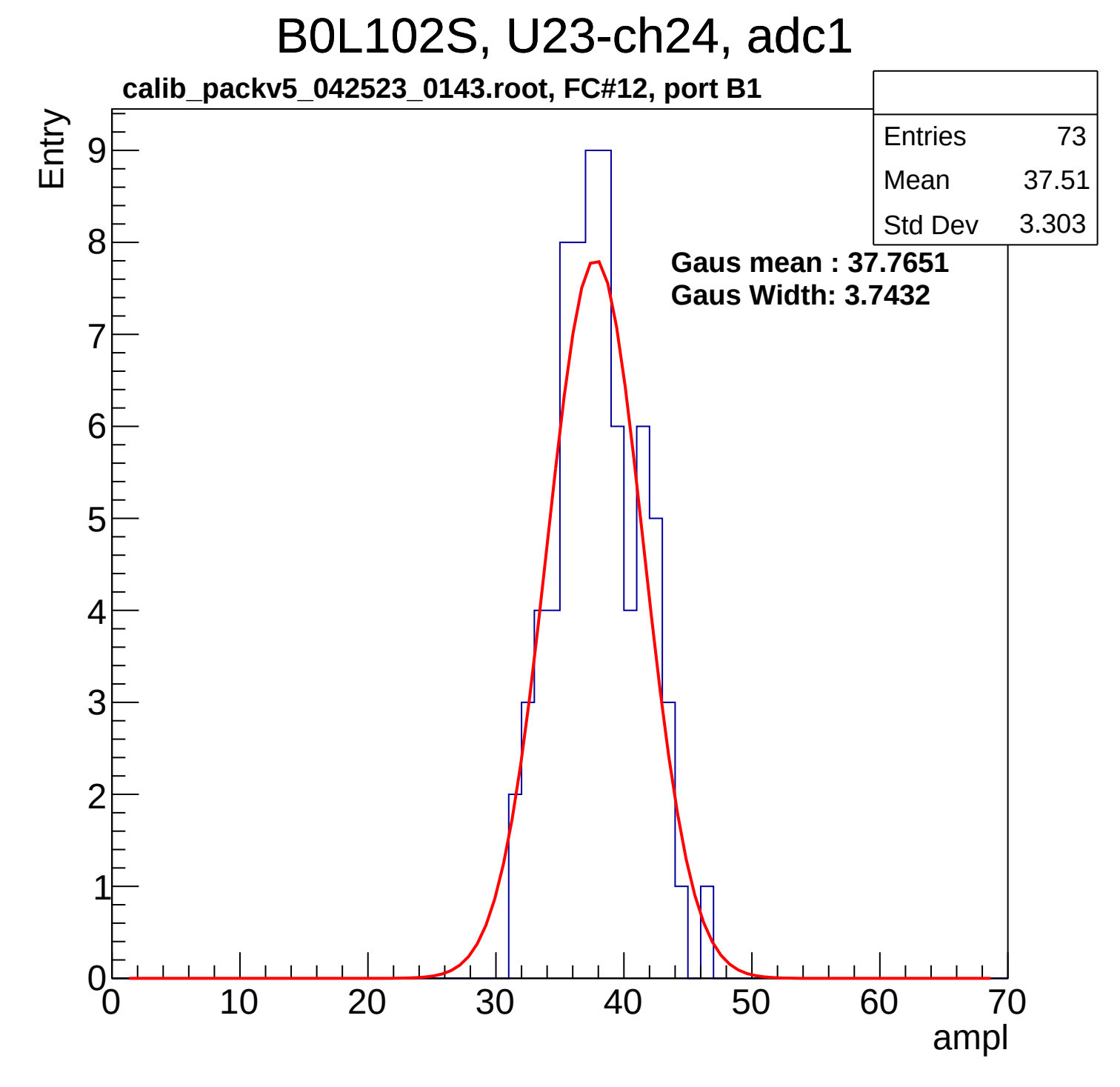
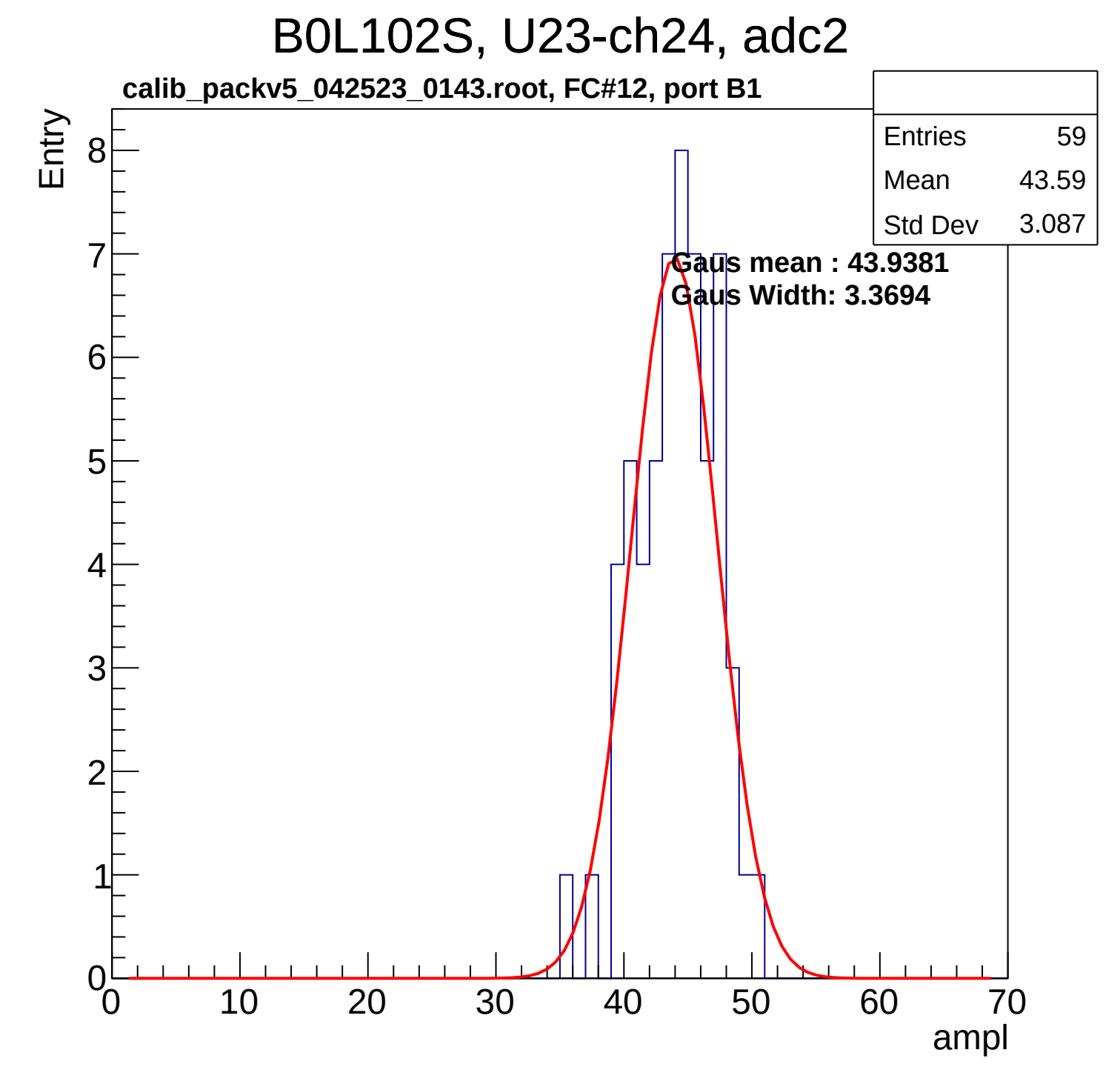

B0L102S, U23-ch23, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch23, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch24, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 30.4
Std Dev 3.113
Gaus mean : 31.0085
Gaus Width: 2.8715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch24, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 37.51
Std Dev 3.303
Gaus mean : 37.7651
Gaus Width: 3.7432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch24, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 43.59
Std Dev 3.087
Gaus mean : 43.9381
Gaus Width: 3.3694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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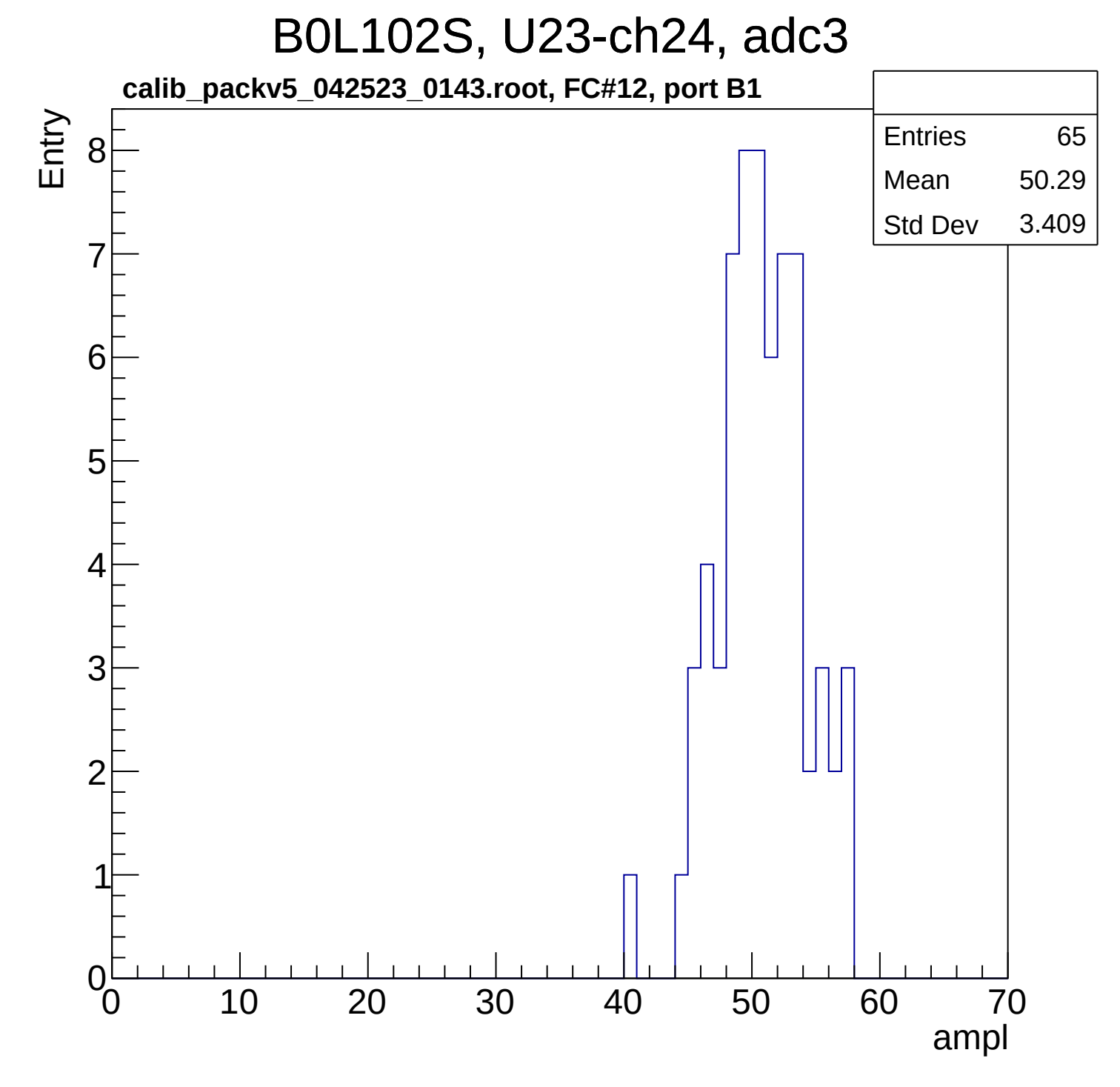
B0L102S, U23-ch24, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 50.29
Std Dev 3.409
Entry
ampl
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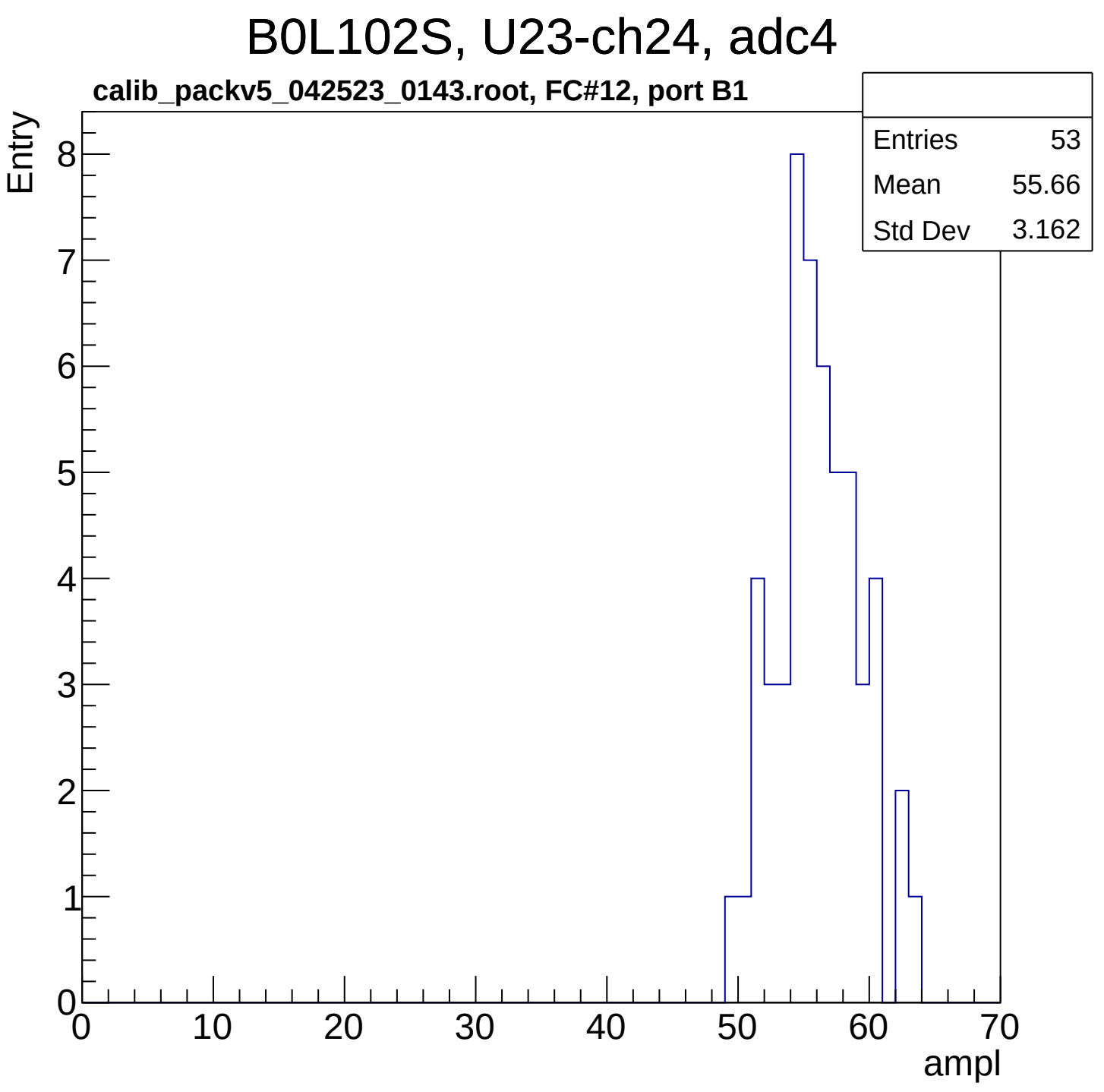
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch24, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 55.66
Std Dev 3.162
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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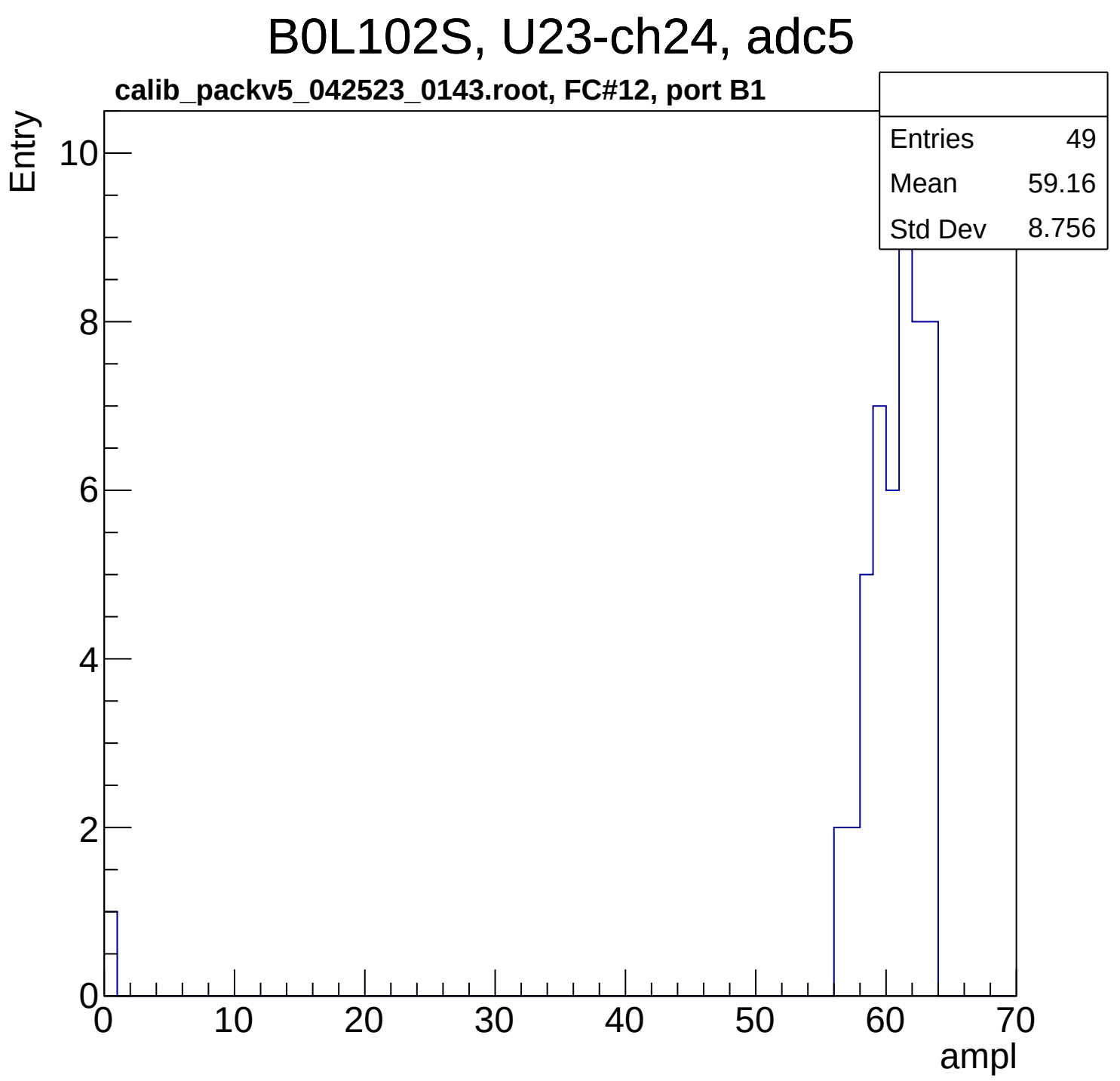
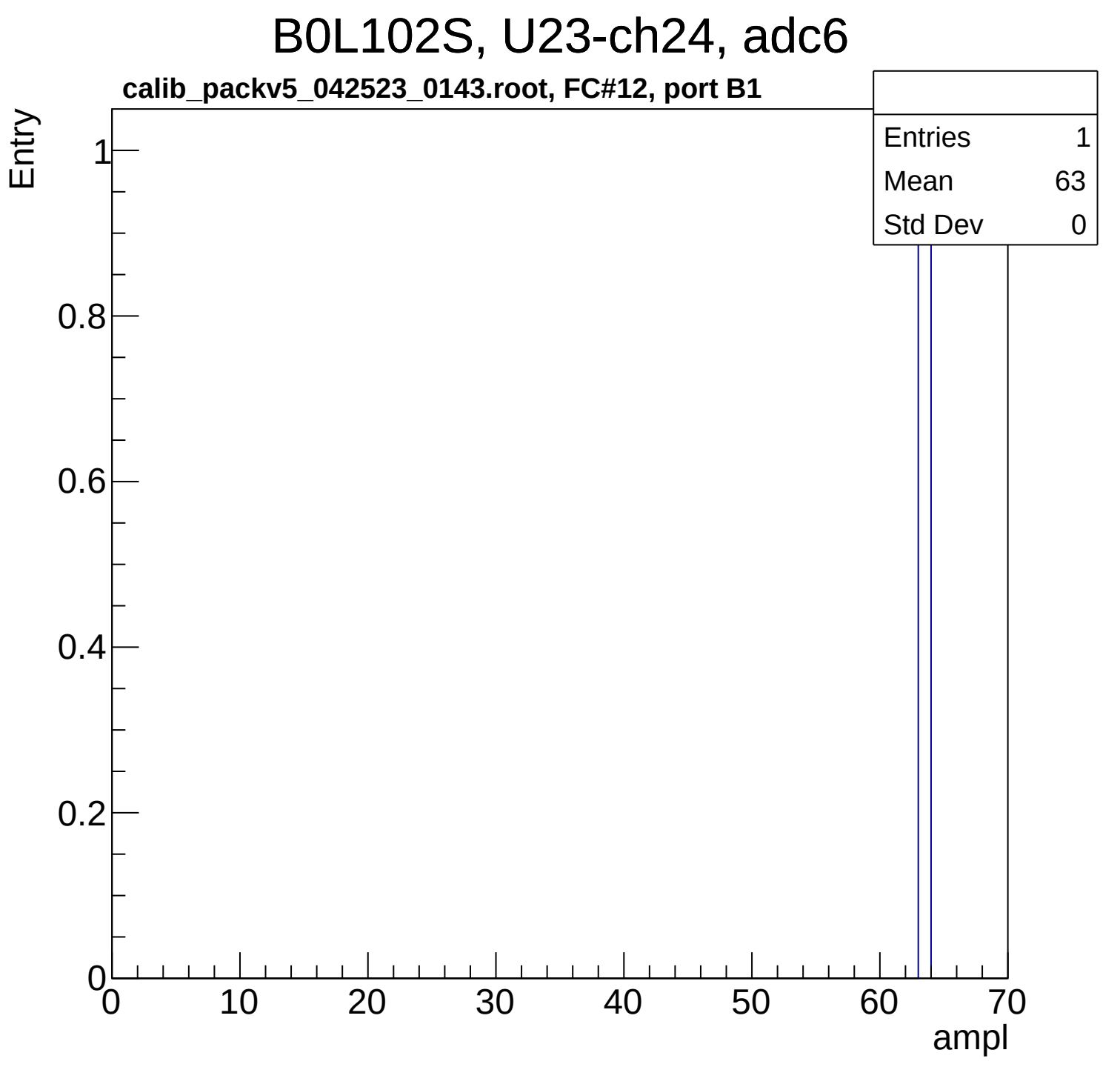
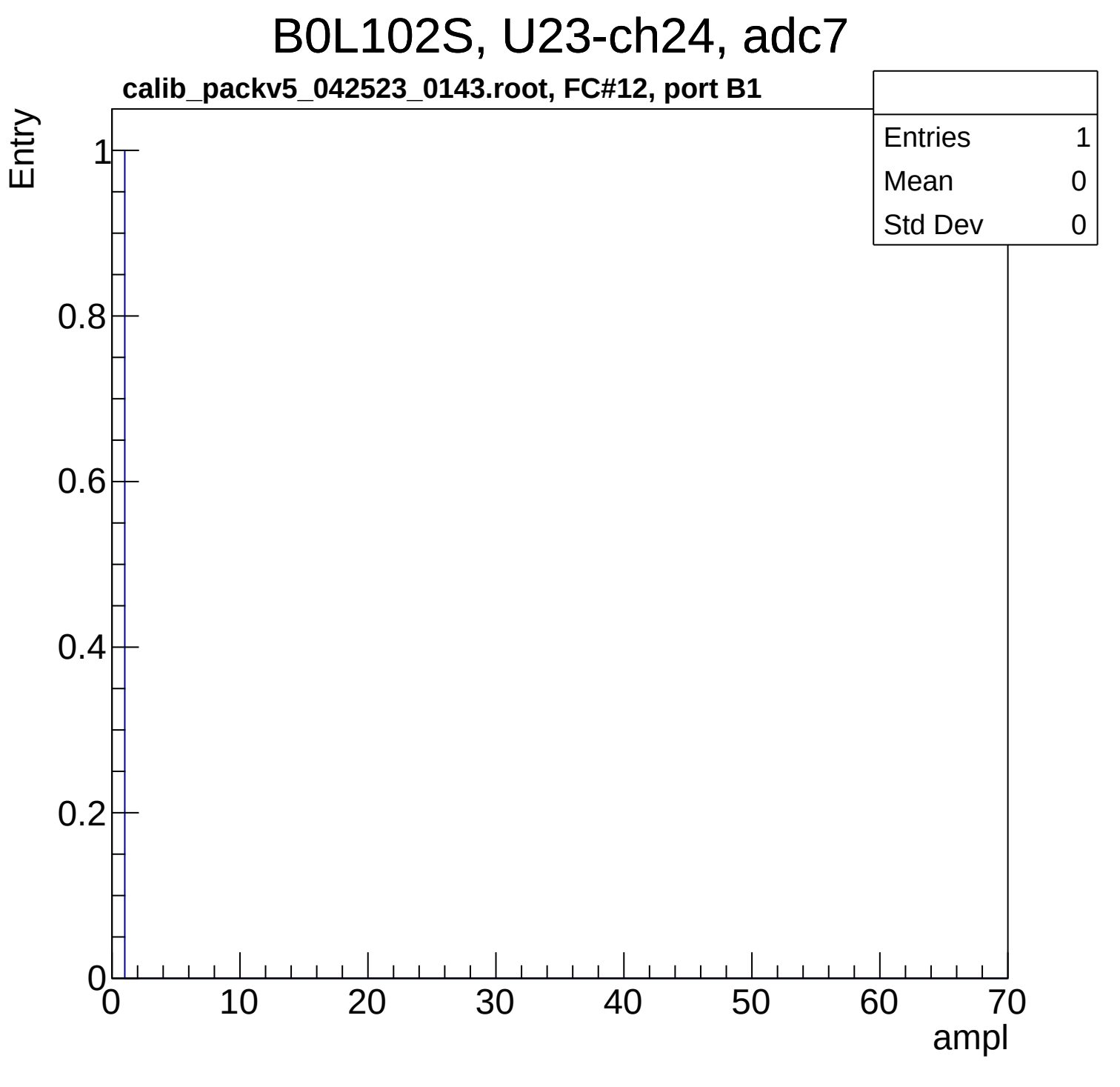
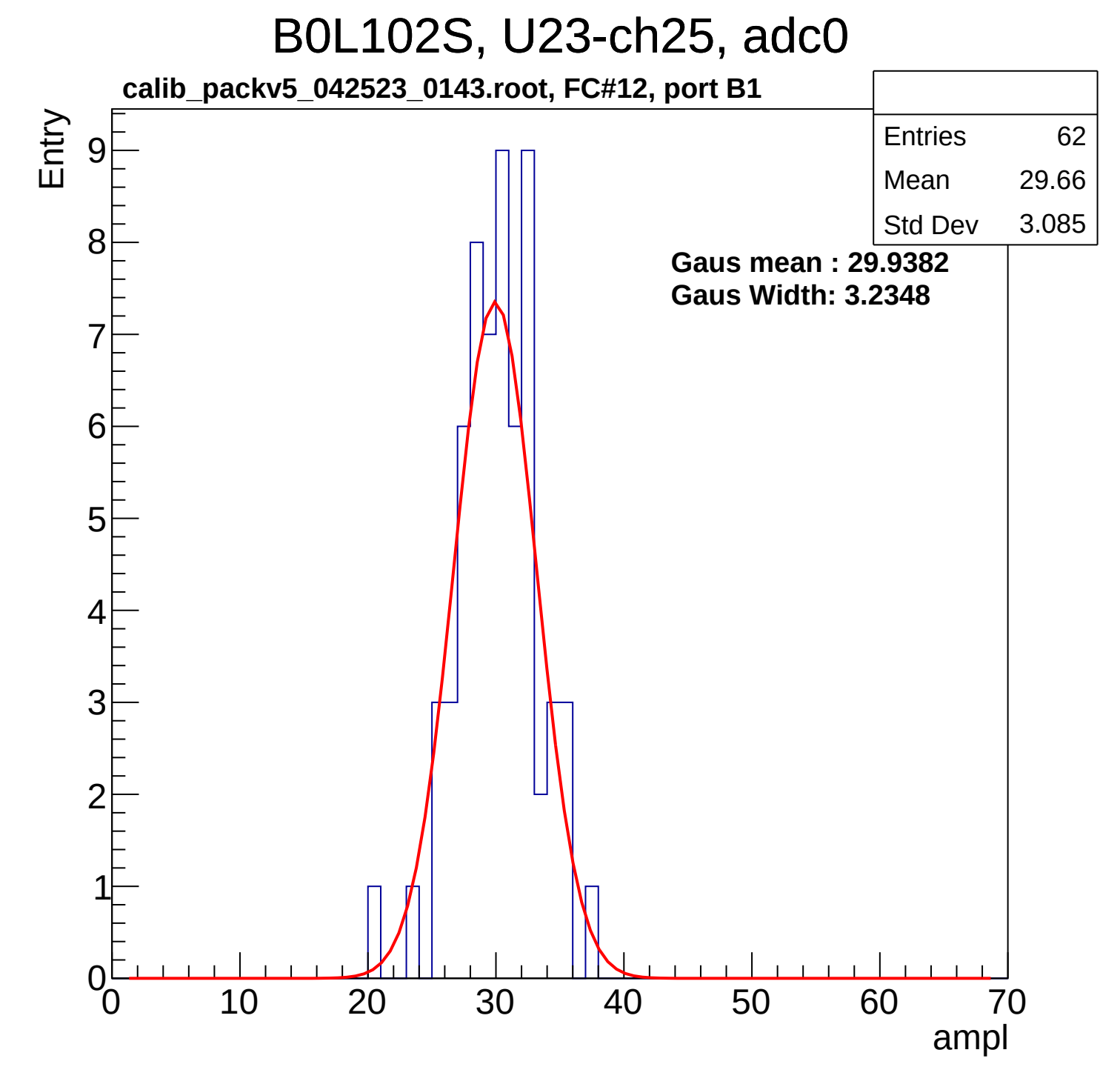

B0L102S, U23-ch24, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 59.16
Std Dev 8.756
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch24, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch24, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch25, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 29.66
Std Dev 3.085
Gaus mean : 29.9382
Gaus Width: 3.2348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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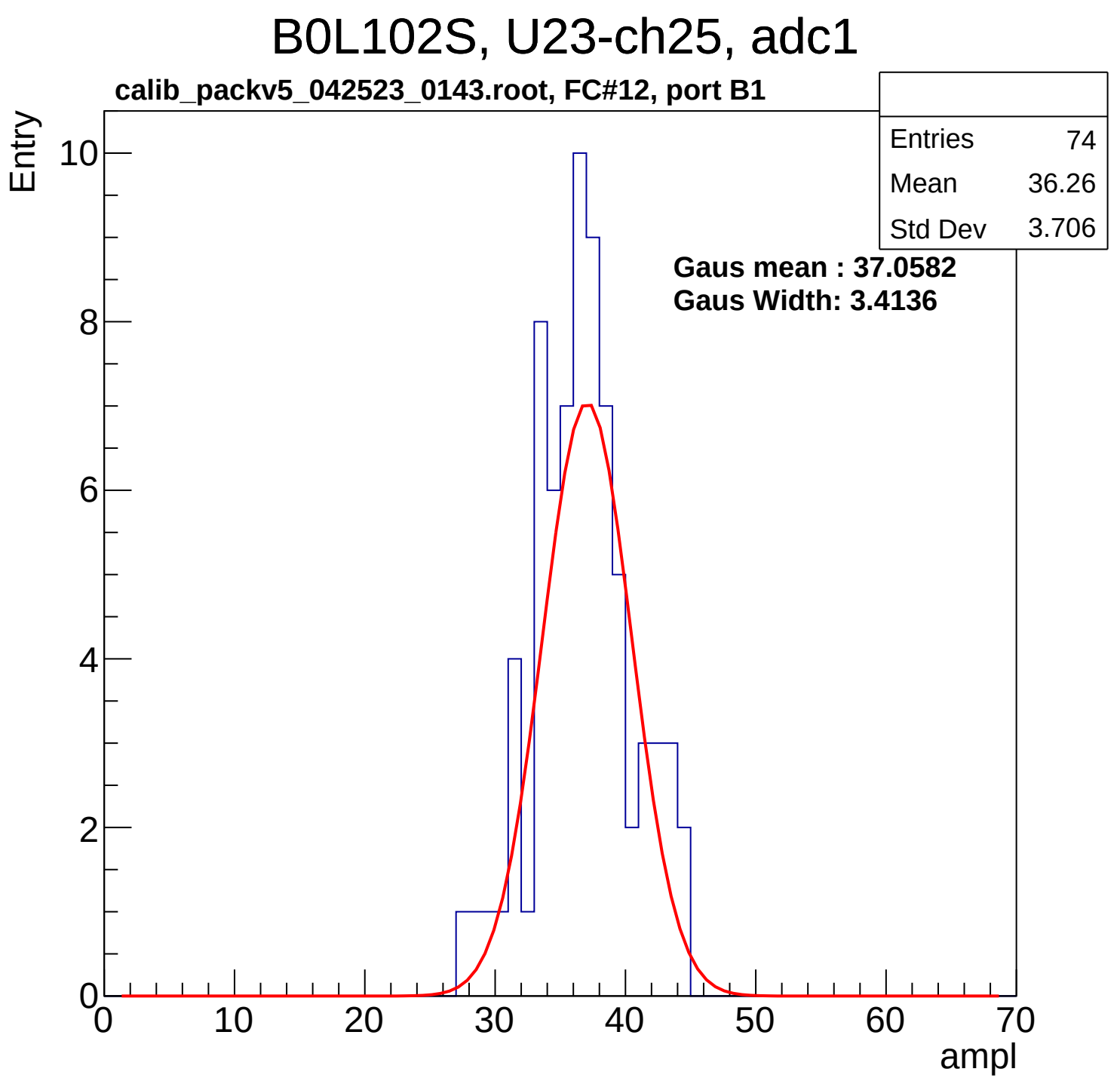
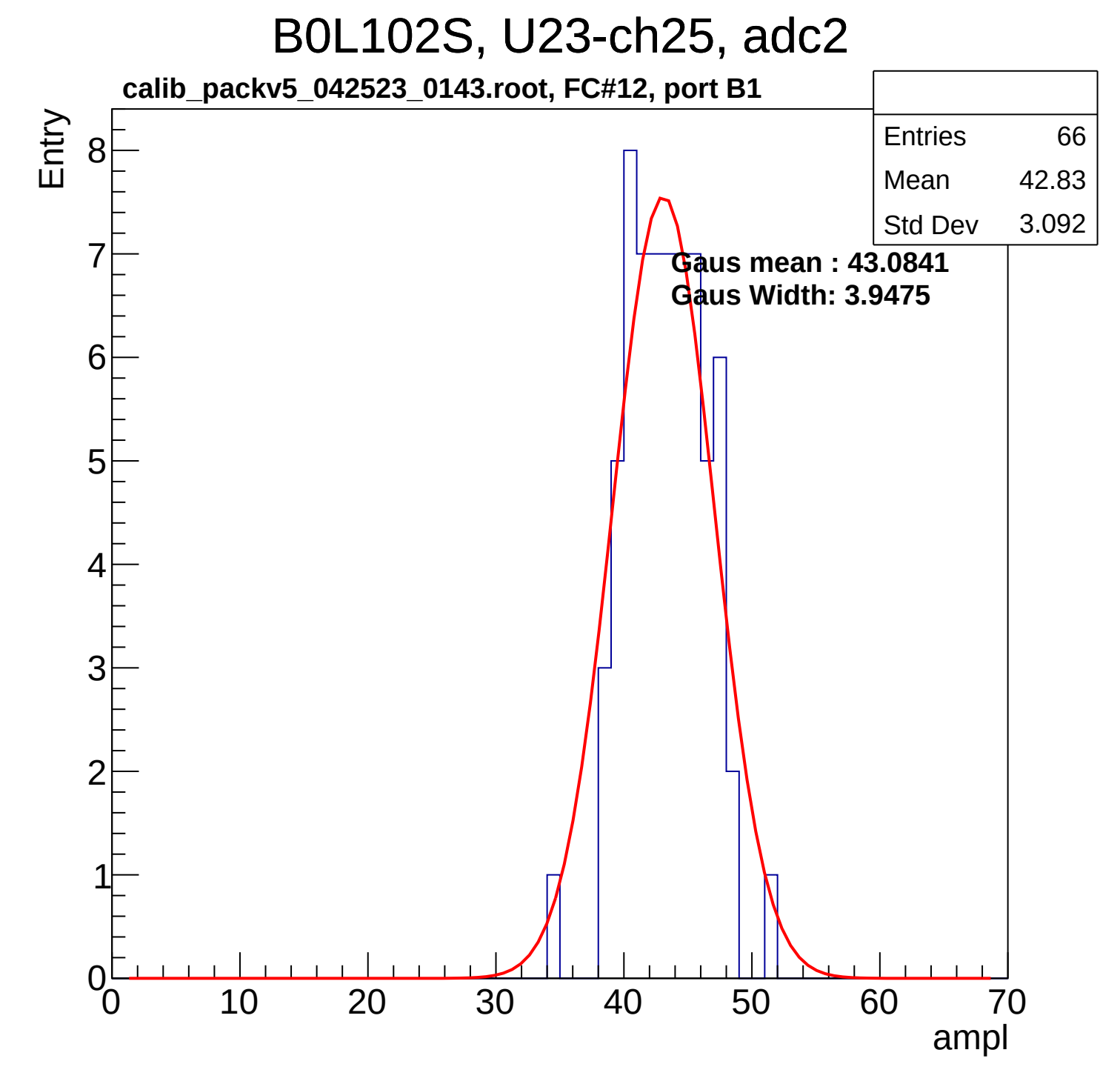
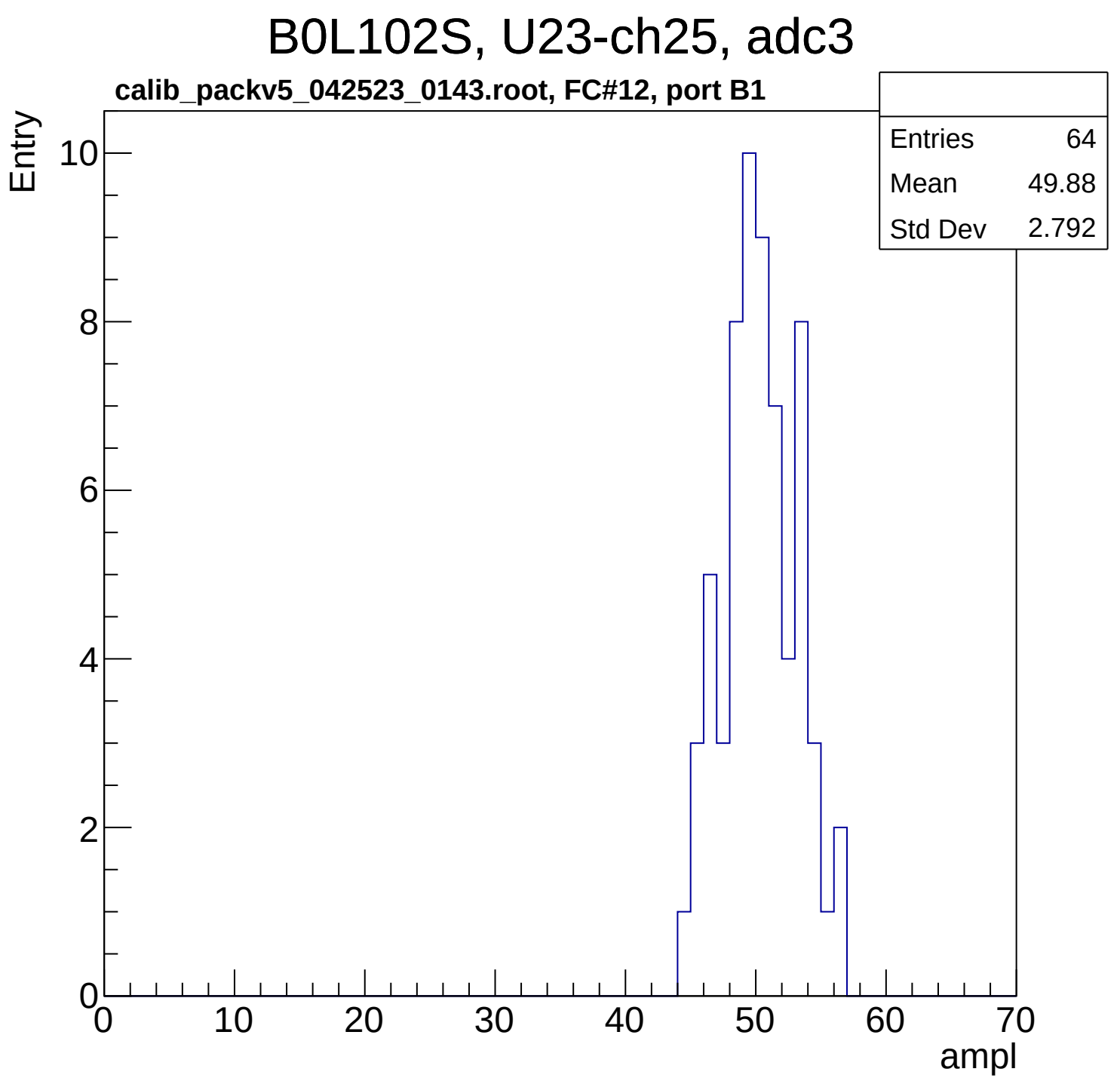
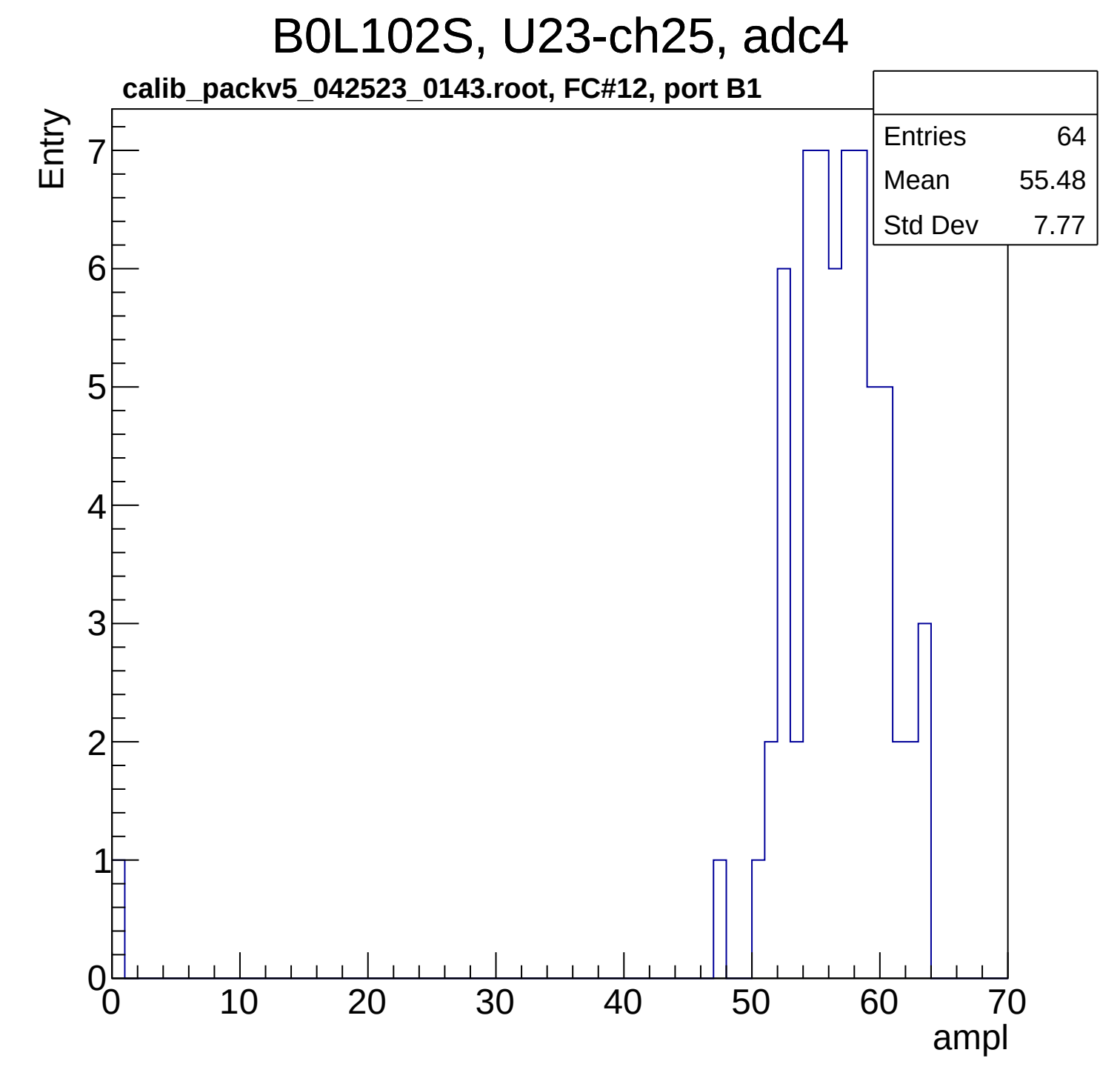

B0L102S, U23-ch25, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 36.26
Std Dev 3.706
Gaus mean : 37.0582
Gaus Width: 3.4136
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch25, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 42.83
Std Dev 3.092
Gaus mean : 43.0841
Gaus Width: 3.9475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch25, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.88
Std Dev 2.792
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch25, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 55.48
Std Dev 7.77
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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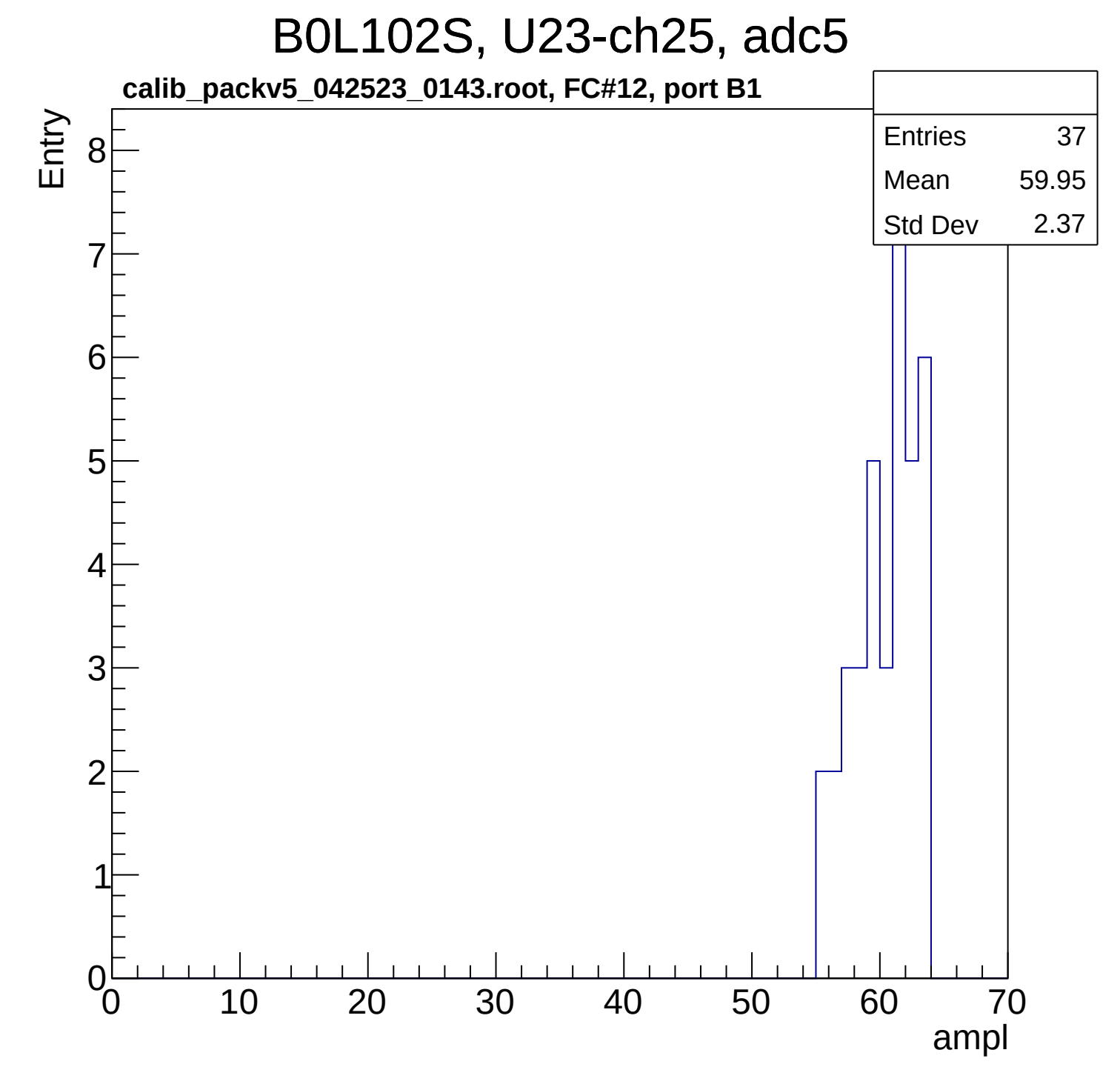
B0L102S, U23-ch25, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 59.95
Std Dev 2.37
Entry
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2
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4
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7
8
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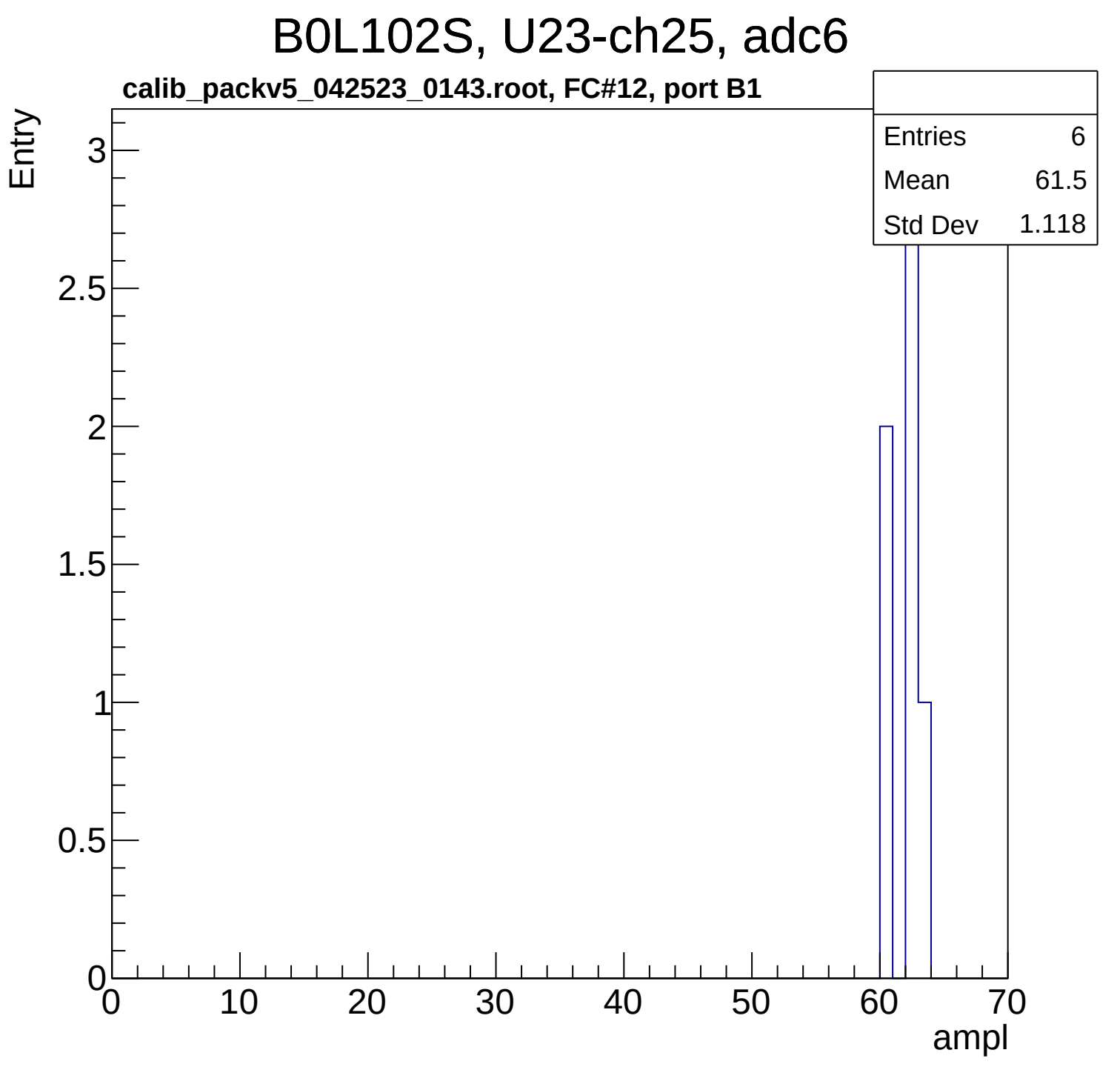
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch25, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.5
Std Dev 1.118
Entry
ampl
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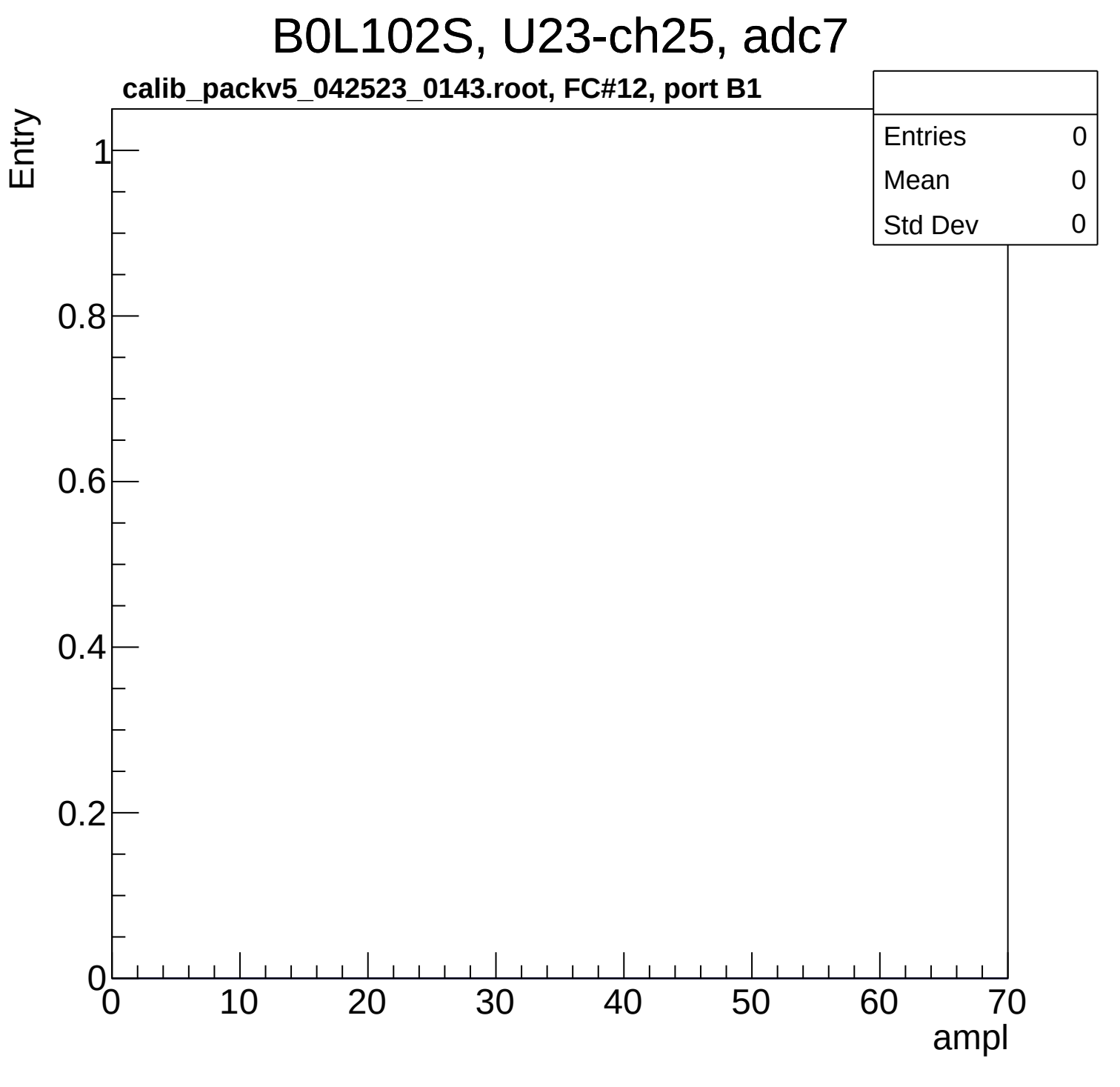
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch25, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
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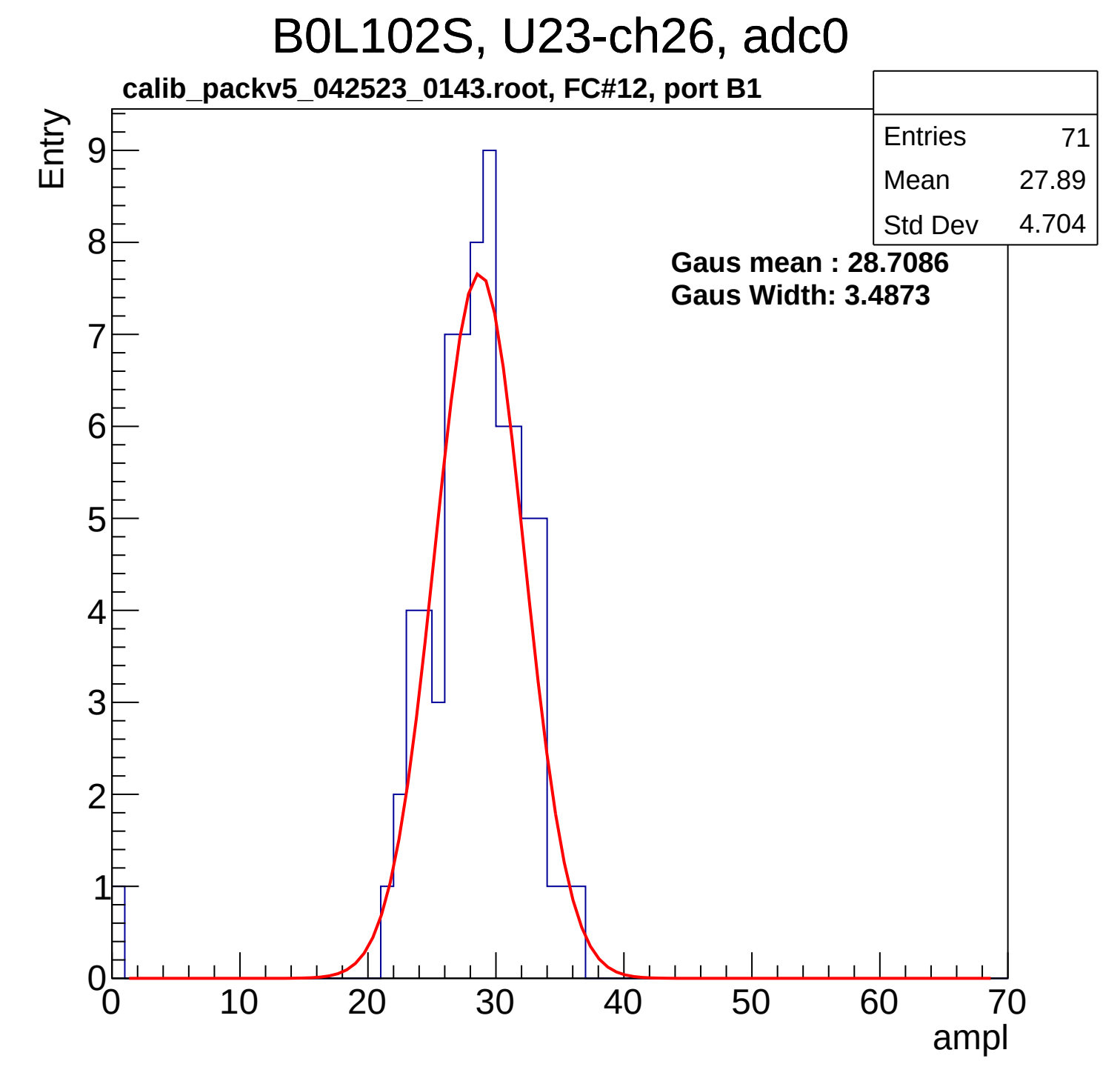
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch26, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 27.89
Std Dev 4.704
Gaus mean : 28.7086
Gaus Width: 3.4873
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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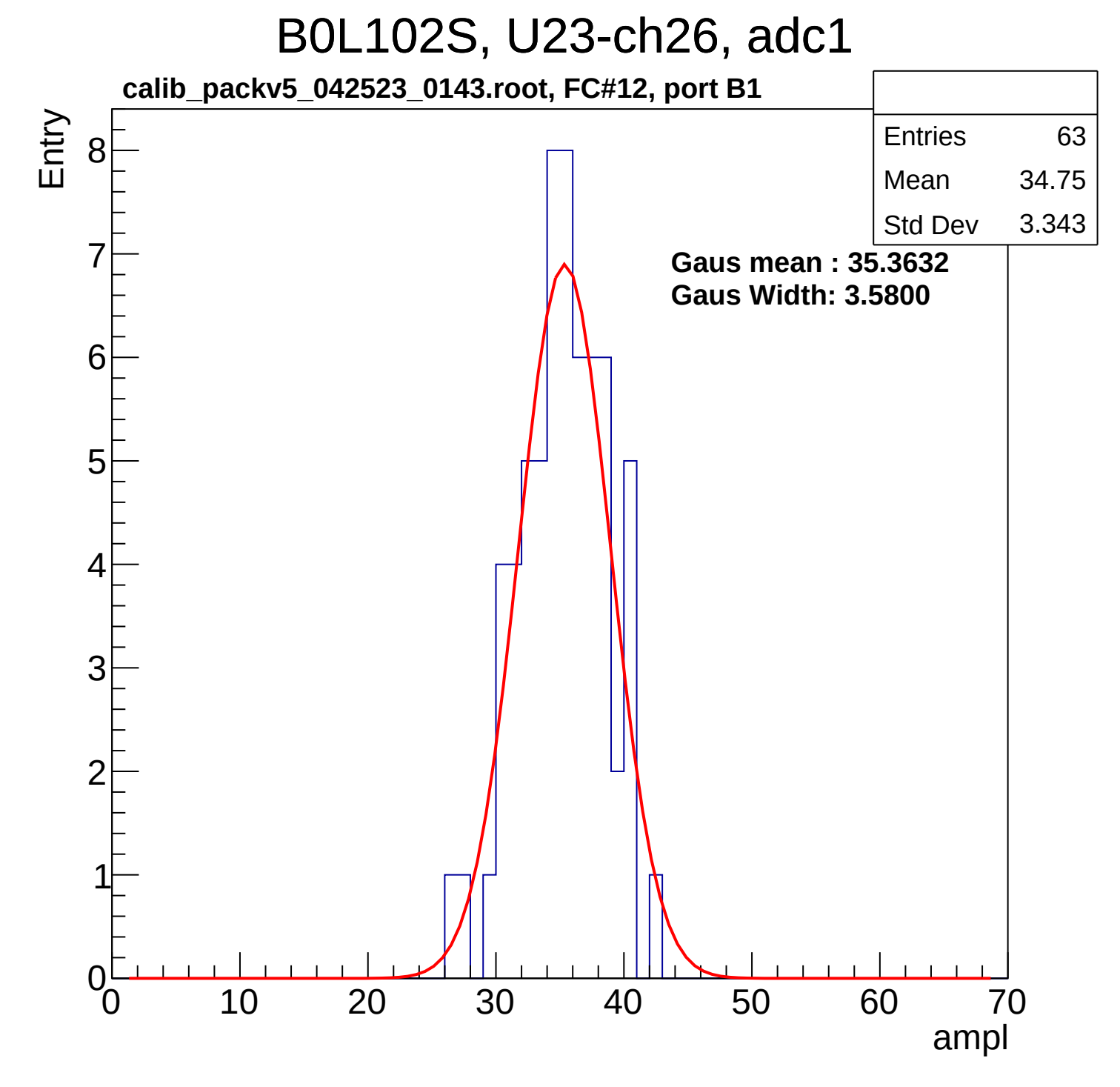
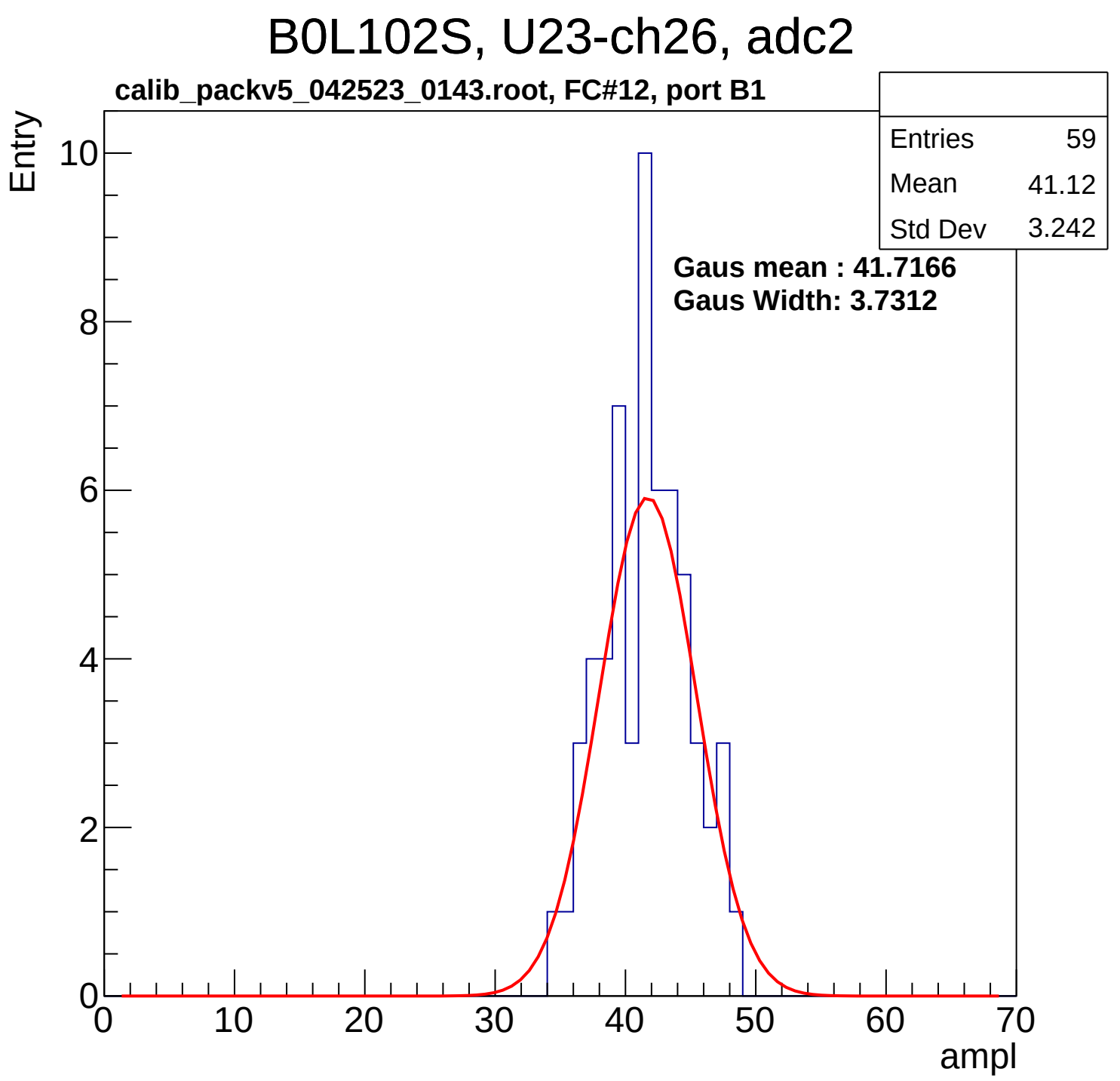
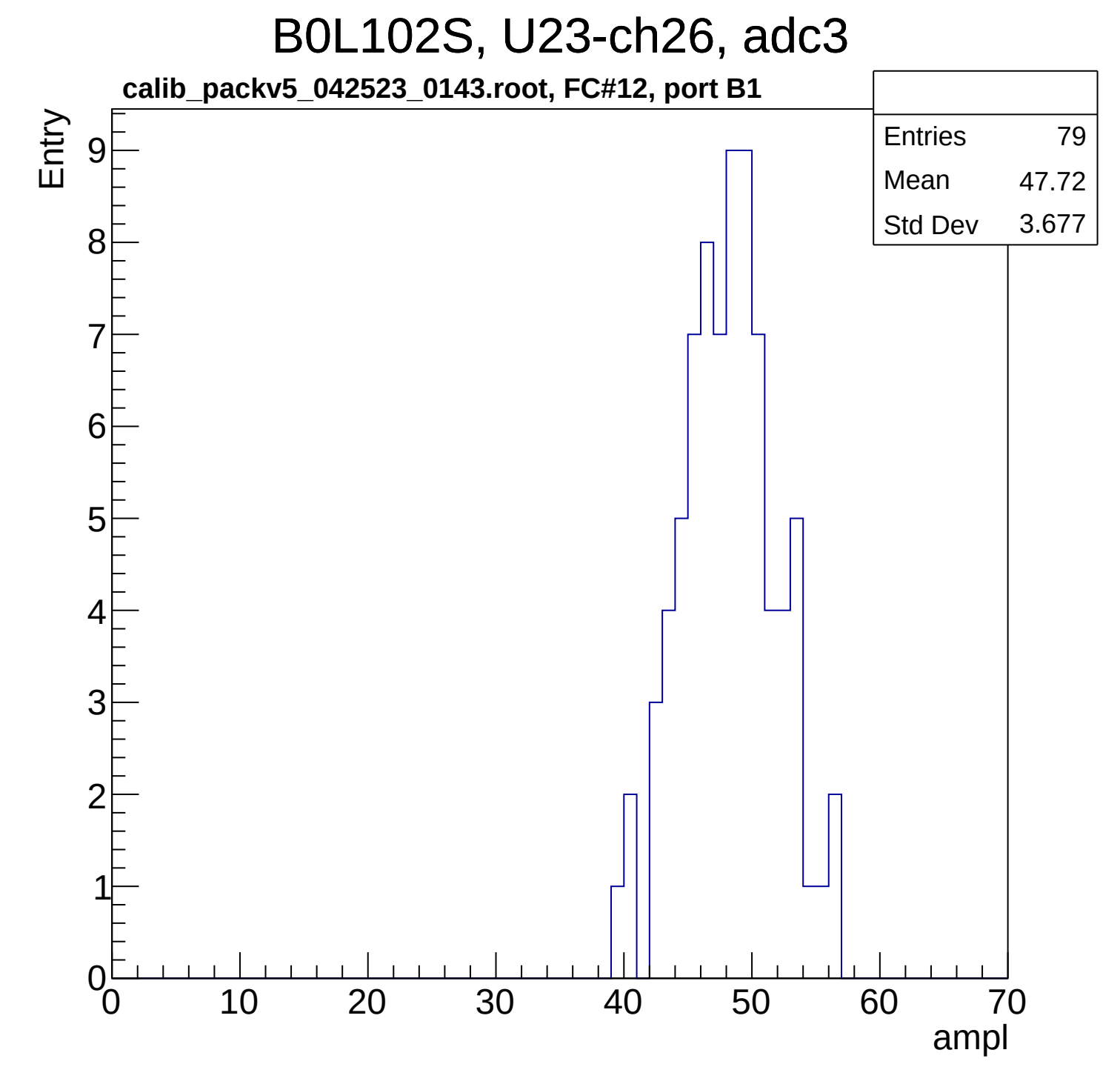

B0L102S, U23-ch26, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 34.75
Std Dev 3.343
Gaus mean : 35.3632
Gaus Width: 3.5800
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch26, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 41.12
Std Dev 3.242
Gaus mean : 41.7166
Gaus Width: 3.7312
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch26, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 47.72
Std Dev 3.677
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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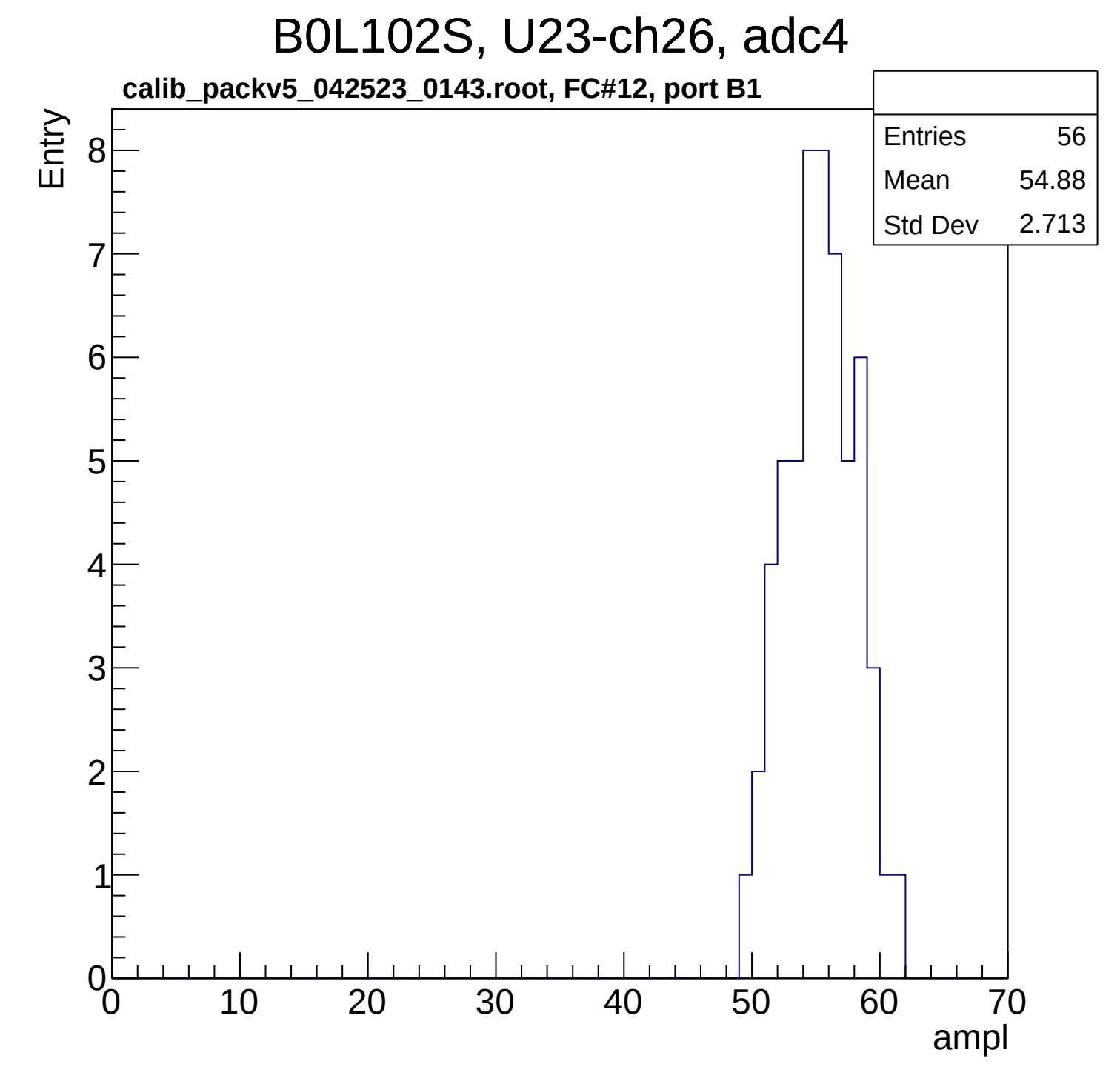
B0L102S, U23-ch26, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 54.88
Std Dev 2.713
Entry
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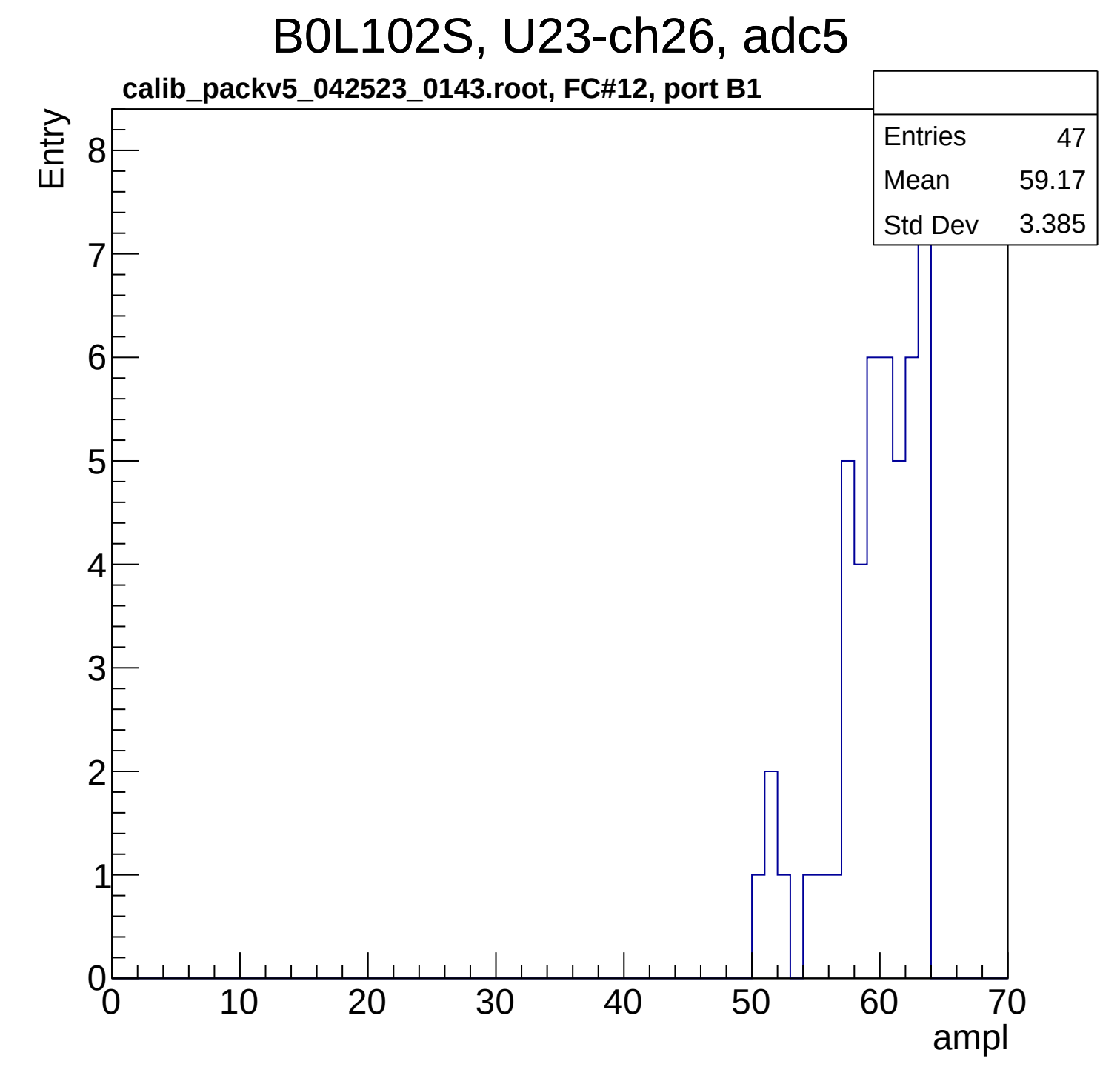
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch26, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.17
Std Dev 3.385
Entry
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2
3
4
5
6
7
8
0
10
20
30
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50
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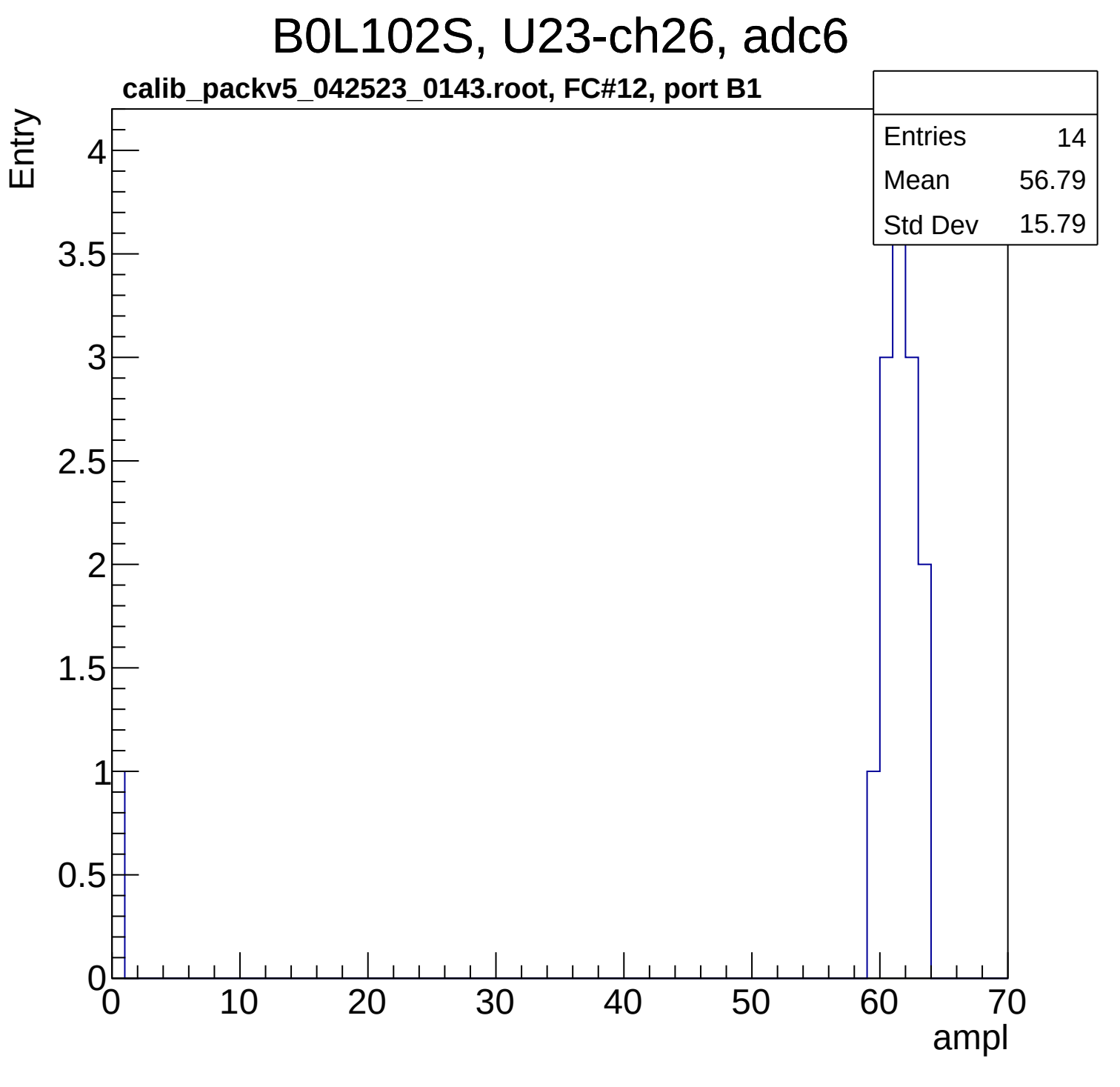
70
ampl


B0L102S, U23-ch26, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 56.79
Std Dev 15.79
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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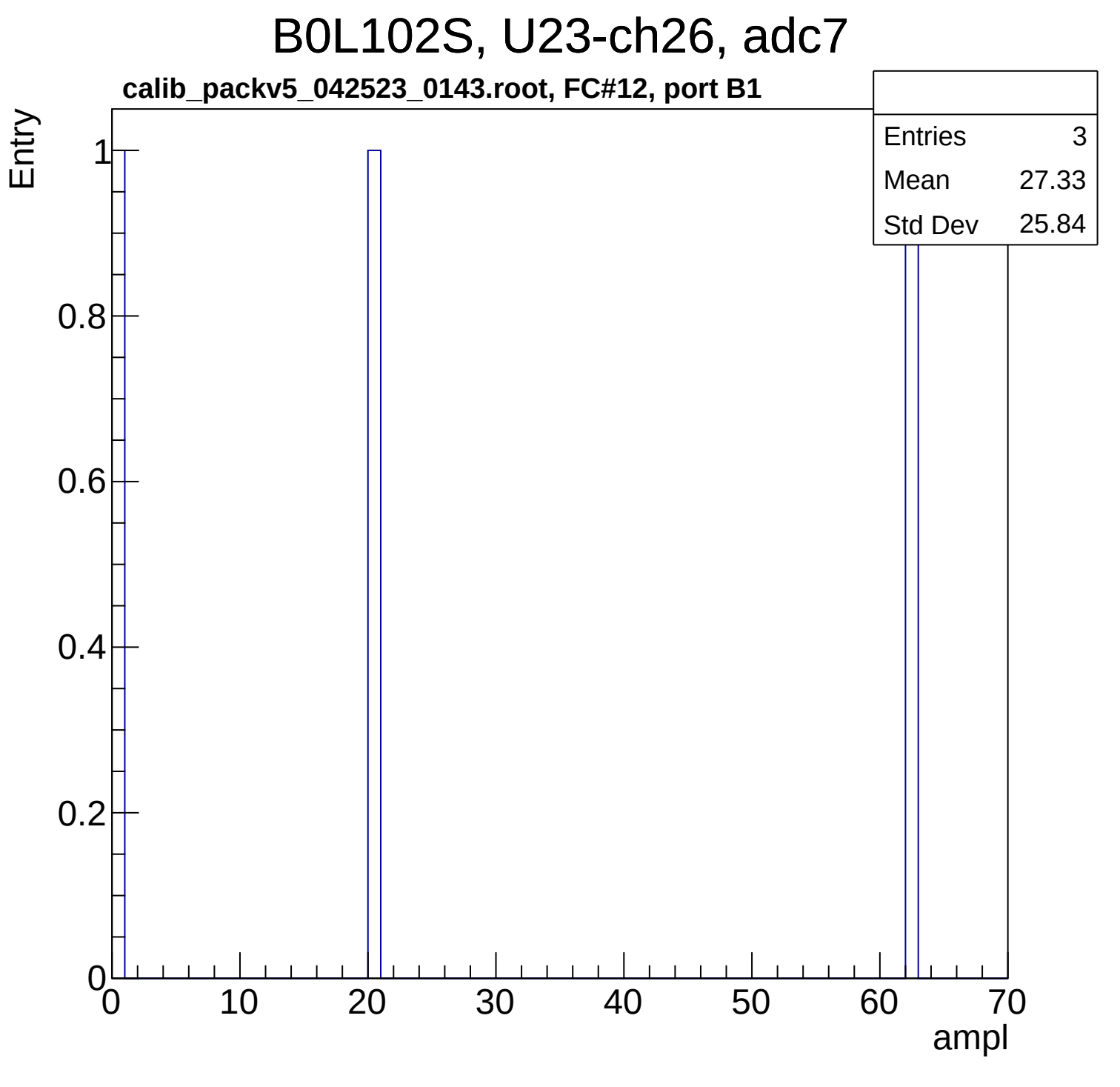
40
50
60
70


B0L102S, U23-ch26, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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0.2
0.4
0.6
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1
0
10
20
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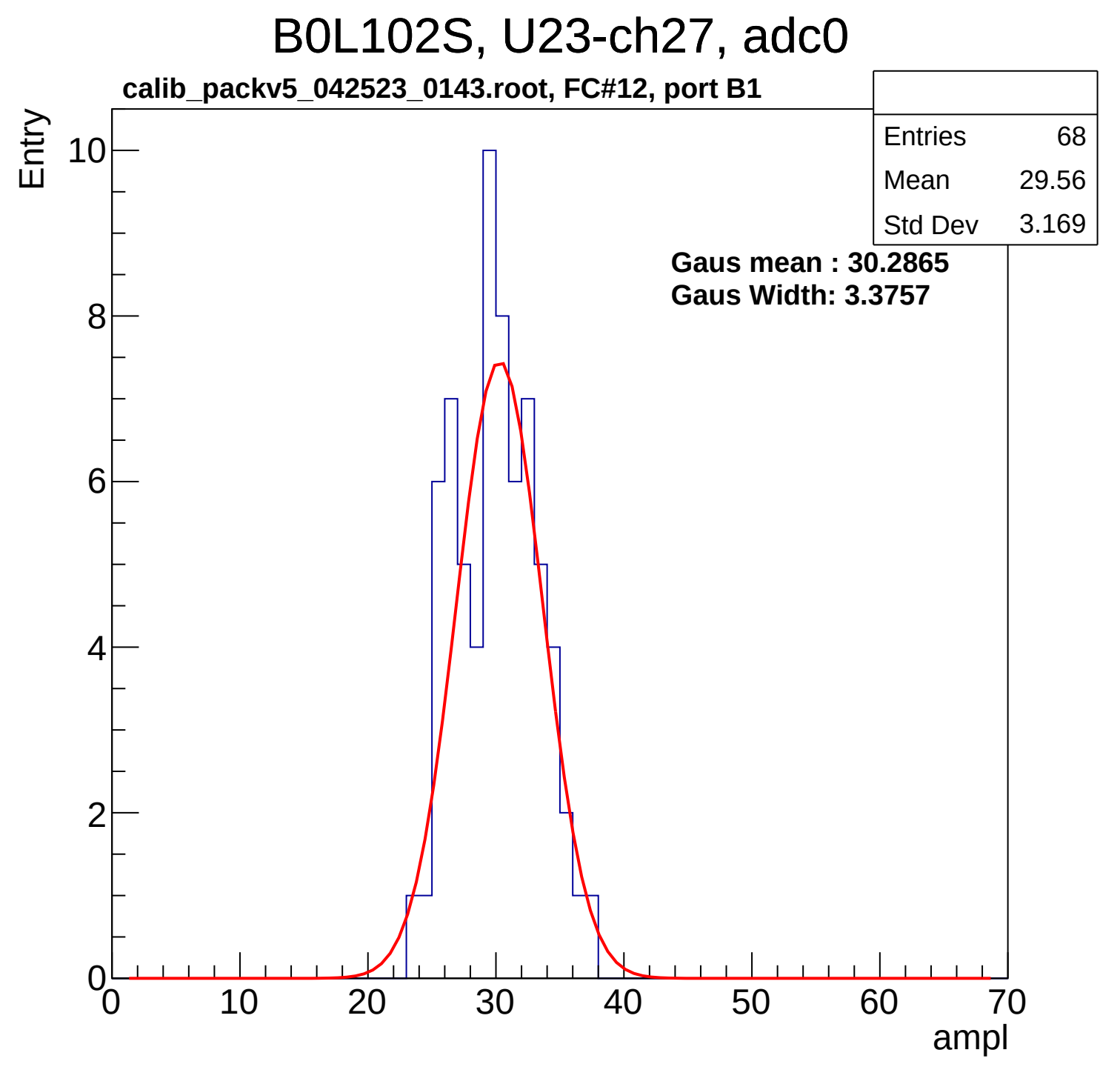
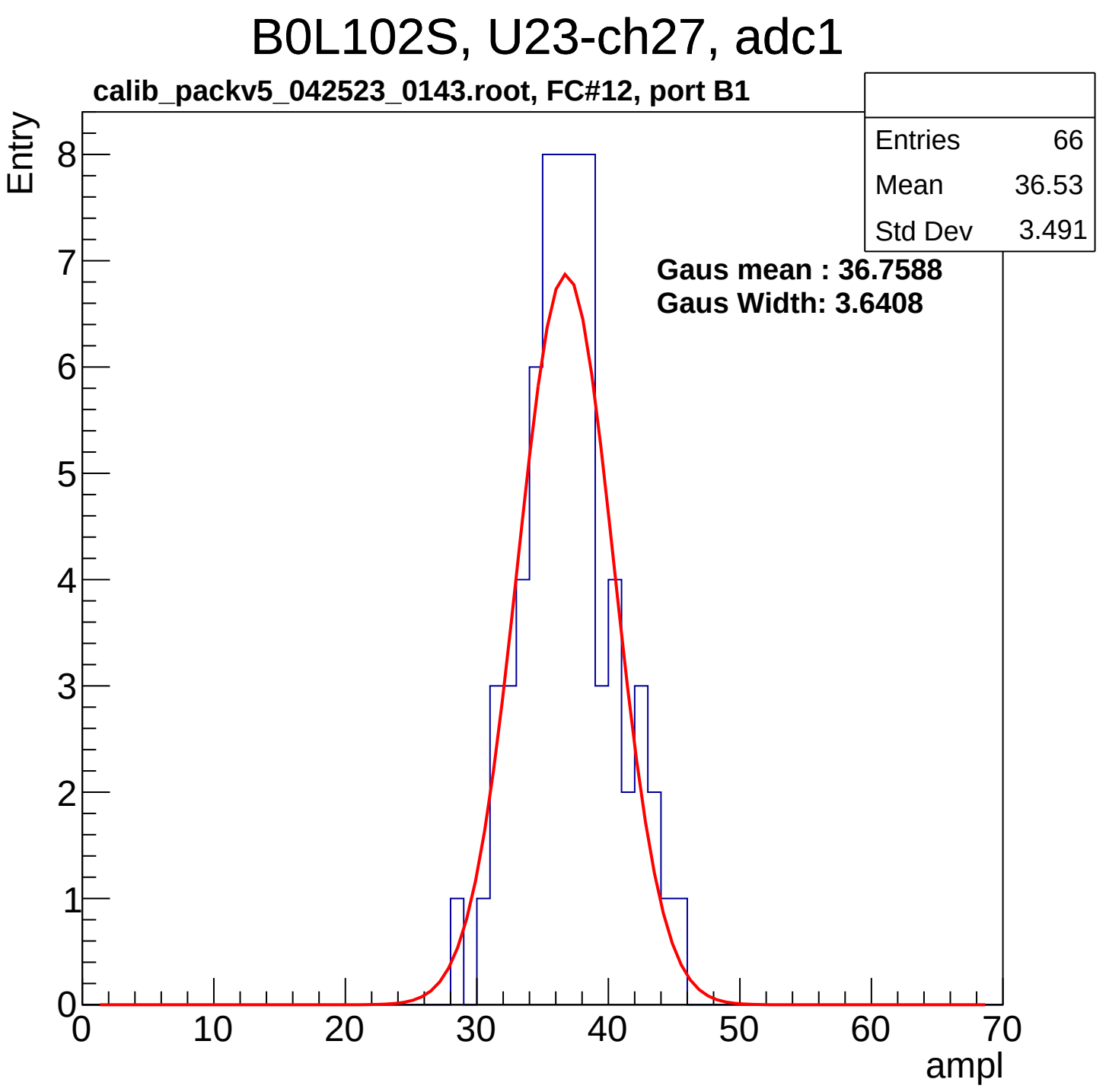
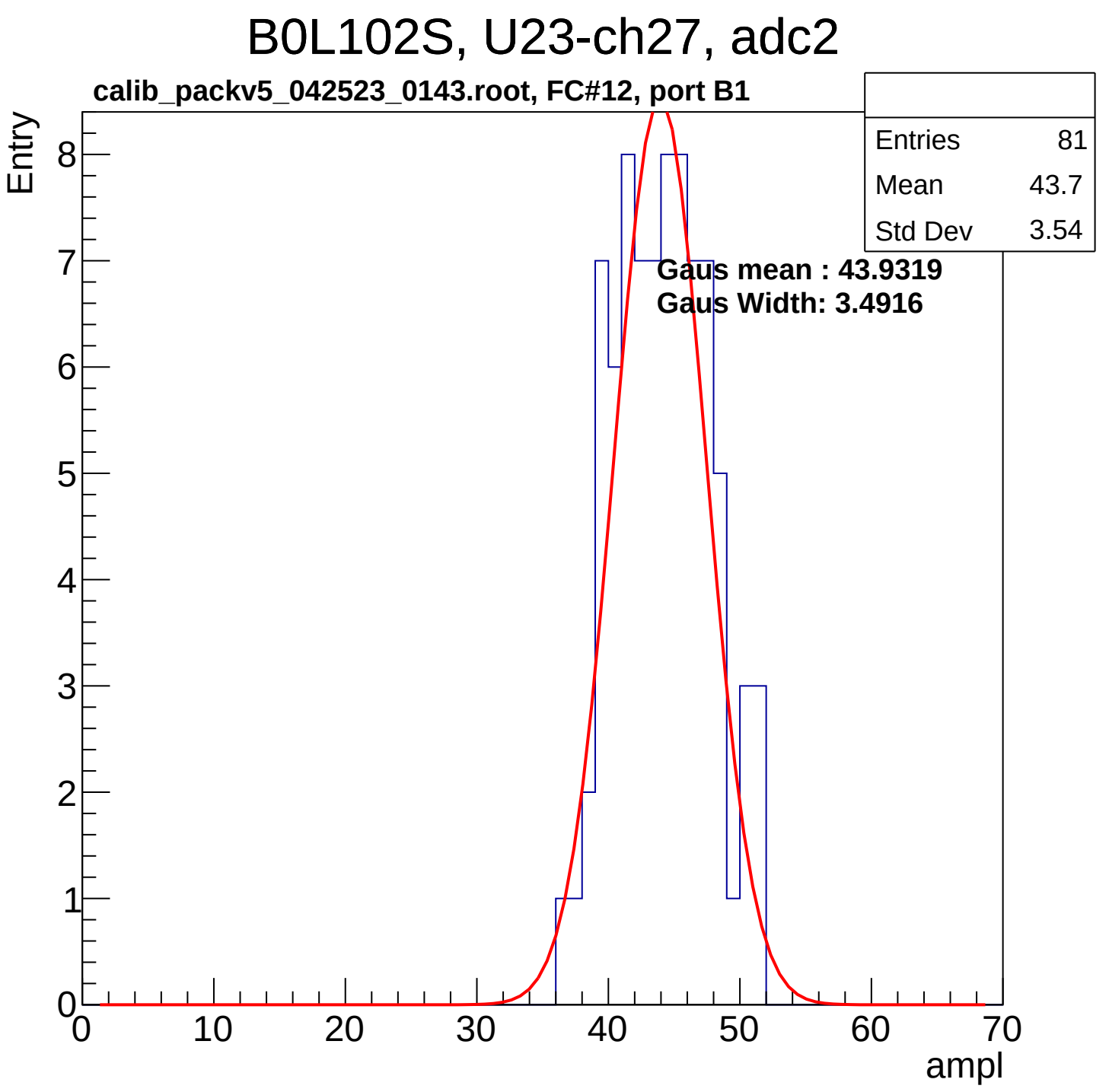
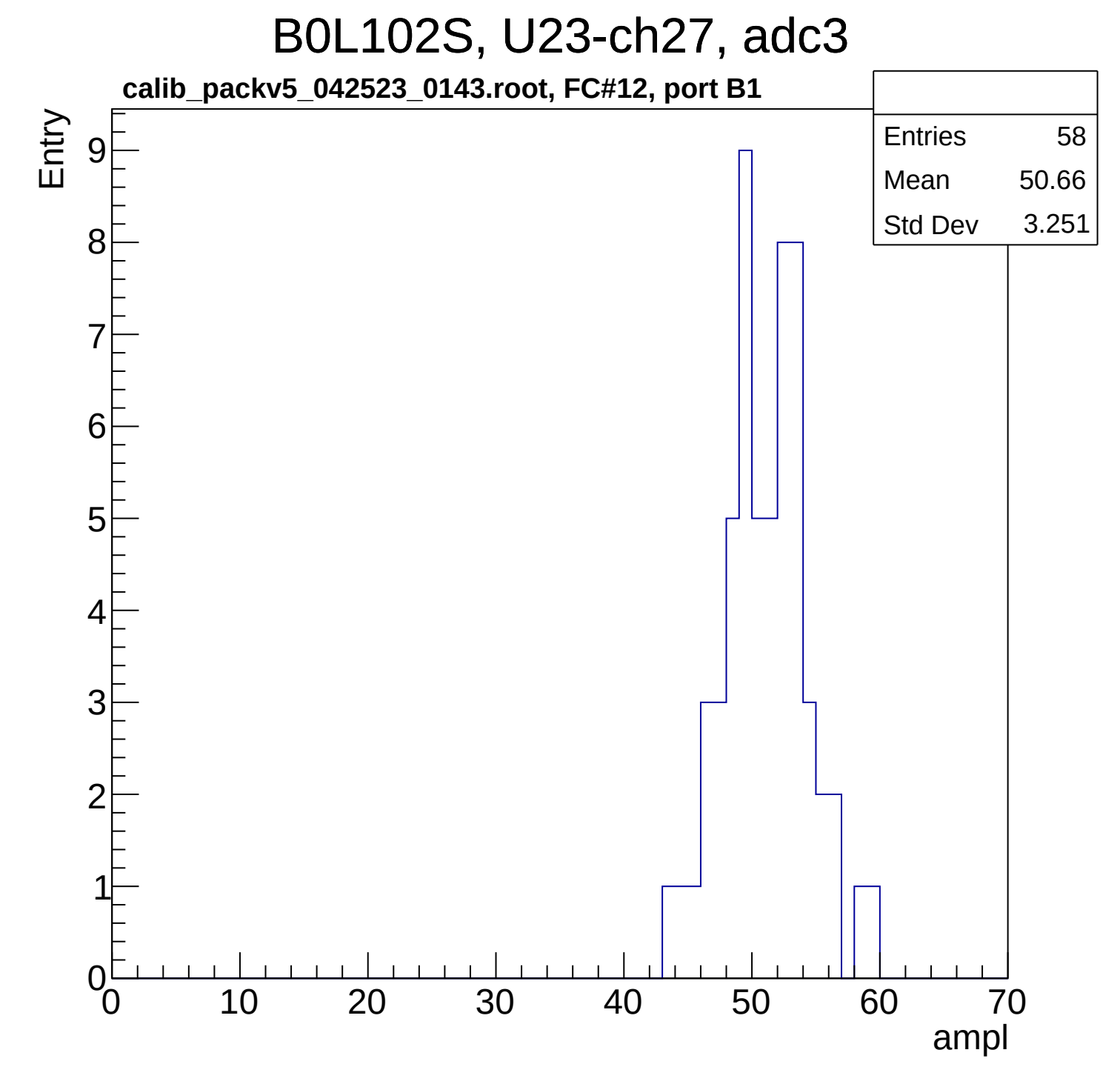
40
50
60
70


B0L102S, U23-ch27, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 29.56
Std Dev 3.169
Gaus mean : 30.2865
Gaus Width: 3.3757
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch27, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.53
Std Dev 3.491
Gaus mean : 36.7588
Gaus Width: 3.6408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch27, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 43.7
Std Dev 3.54
Gaus mean : 43.9319
Gaus Width: 3.4916
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch27, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 50.66
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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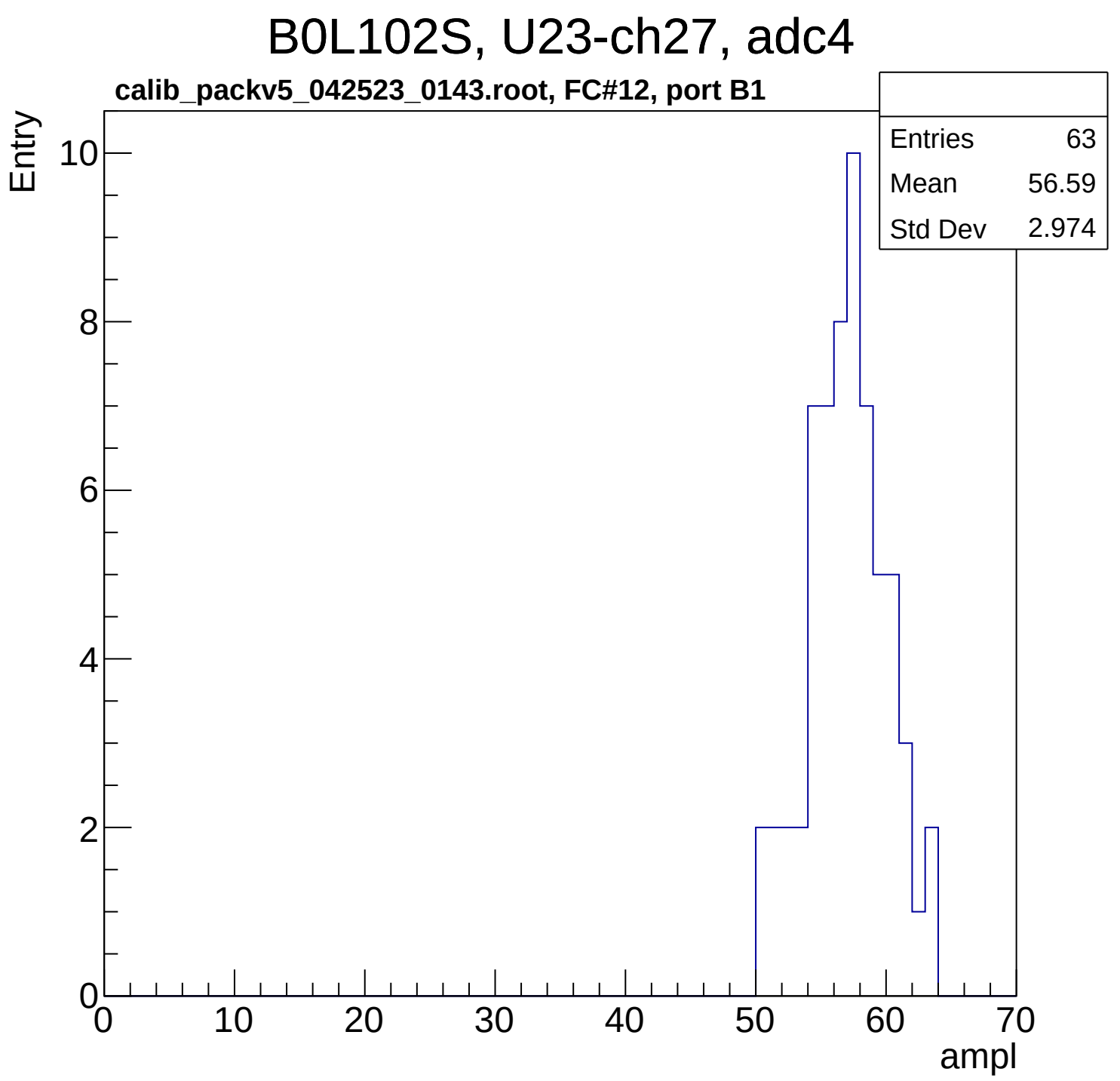
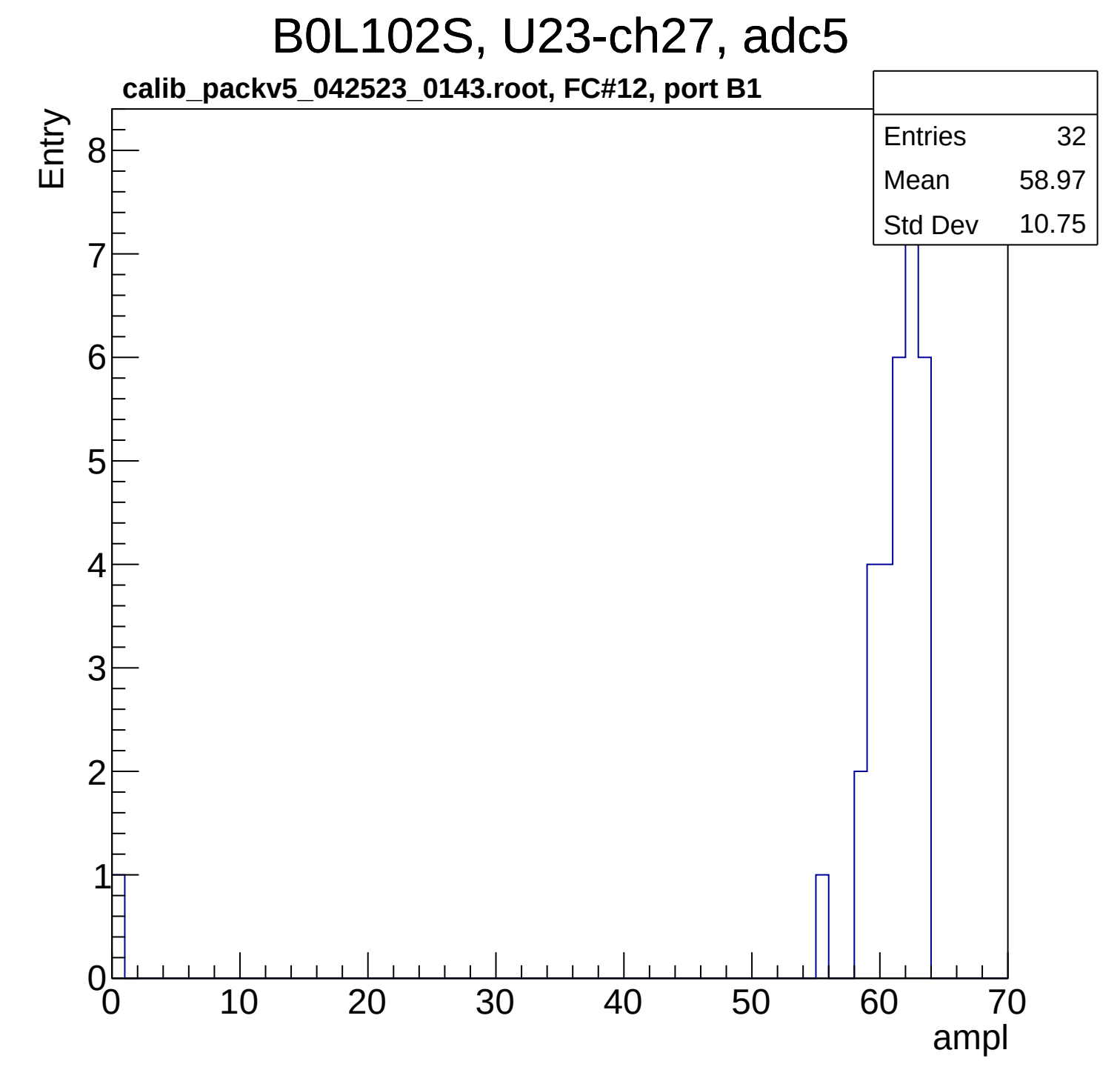
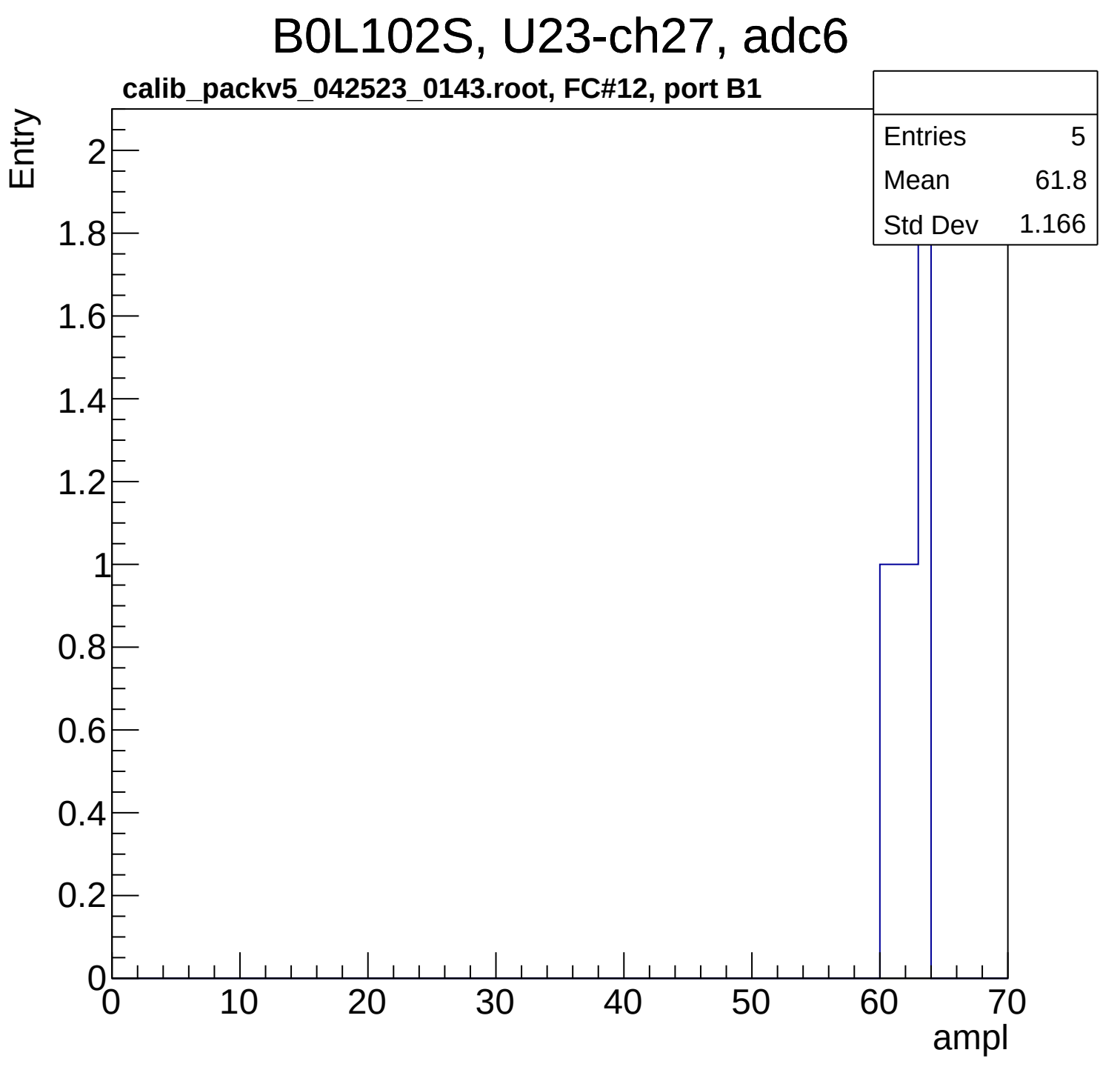
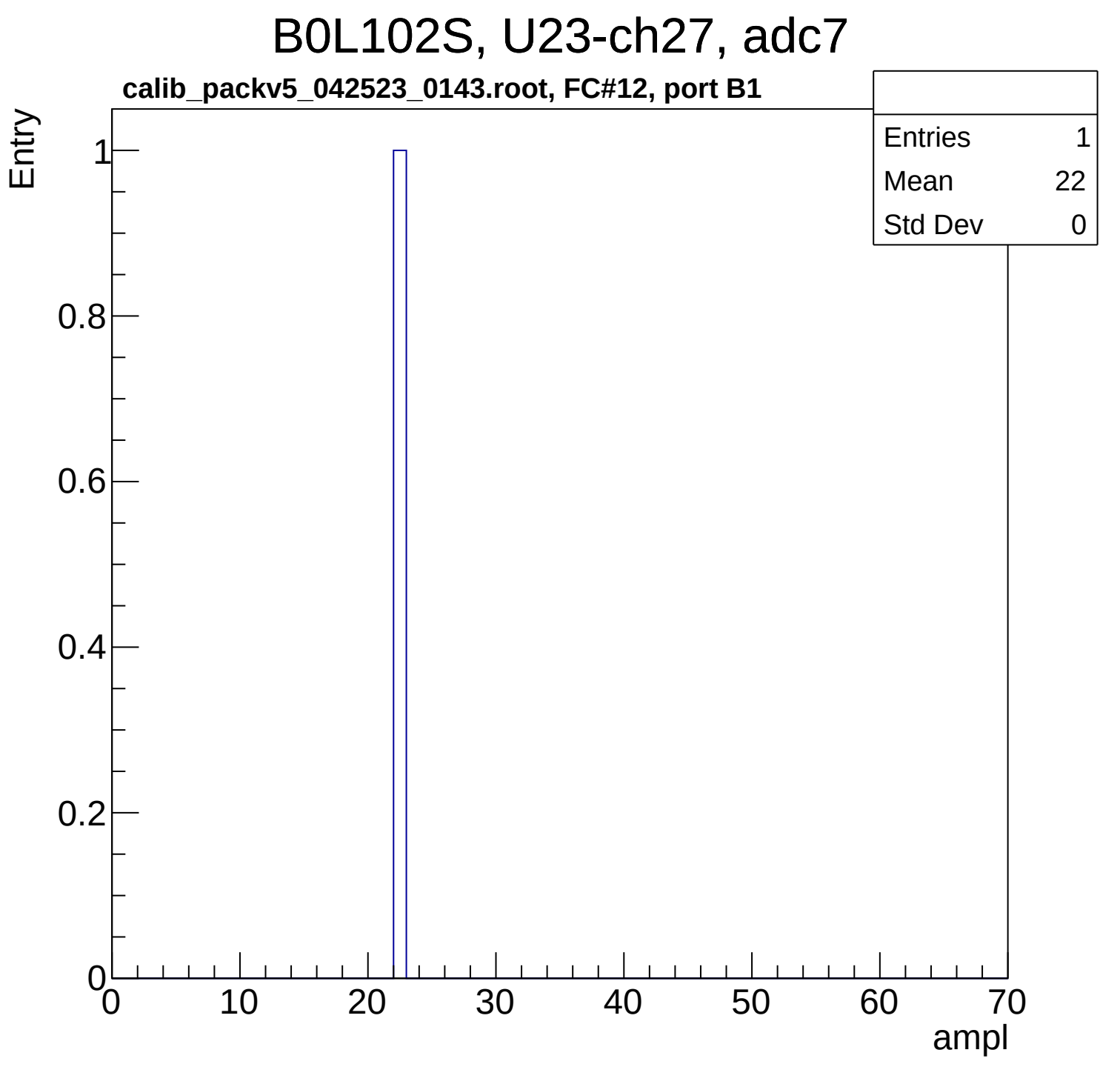
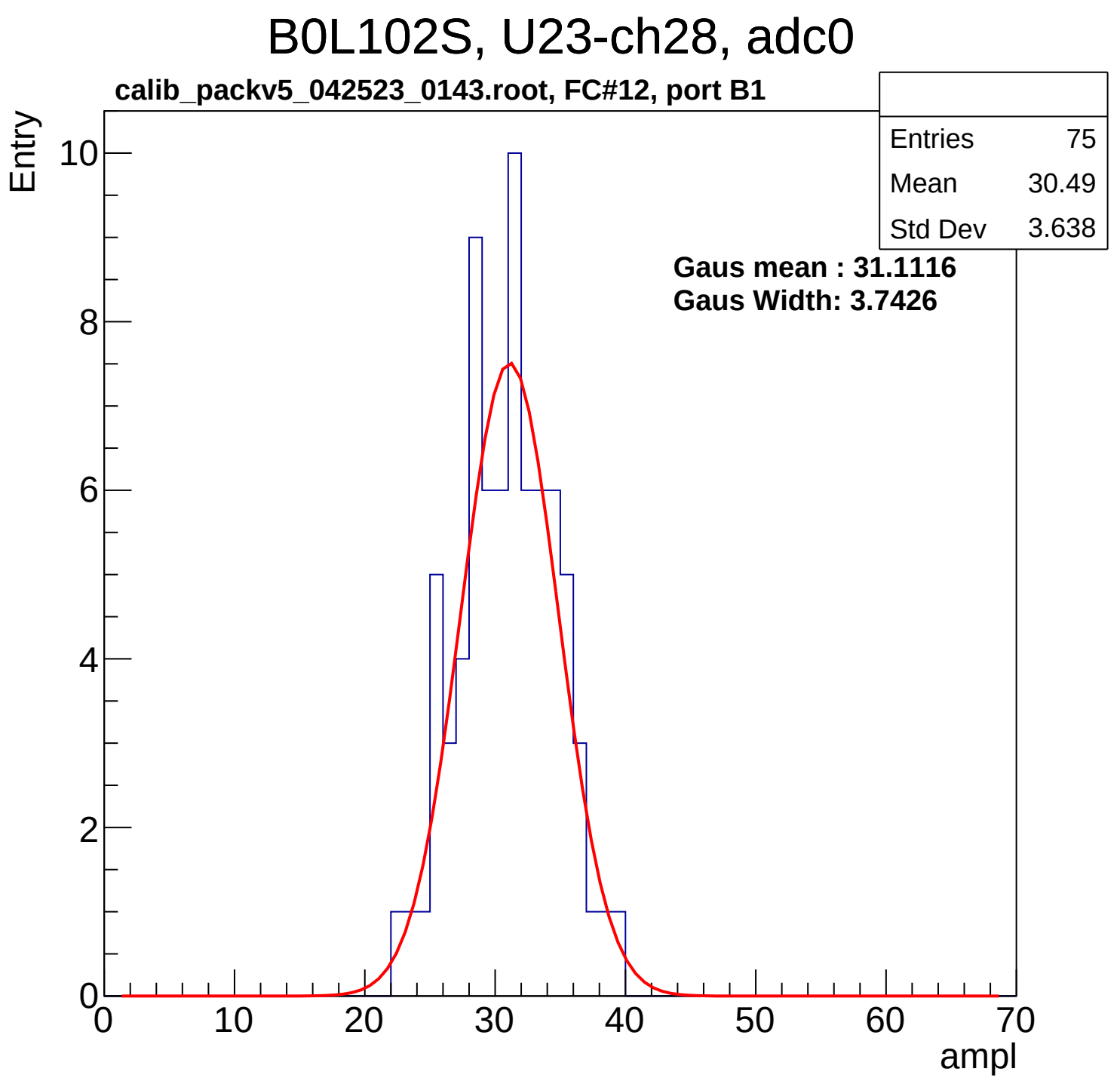
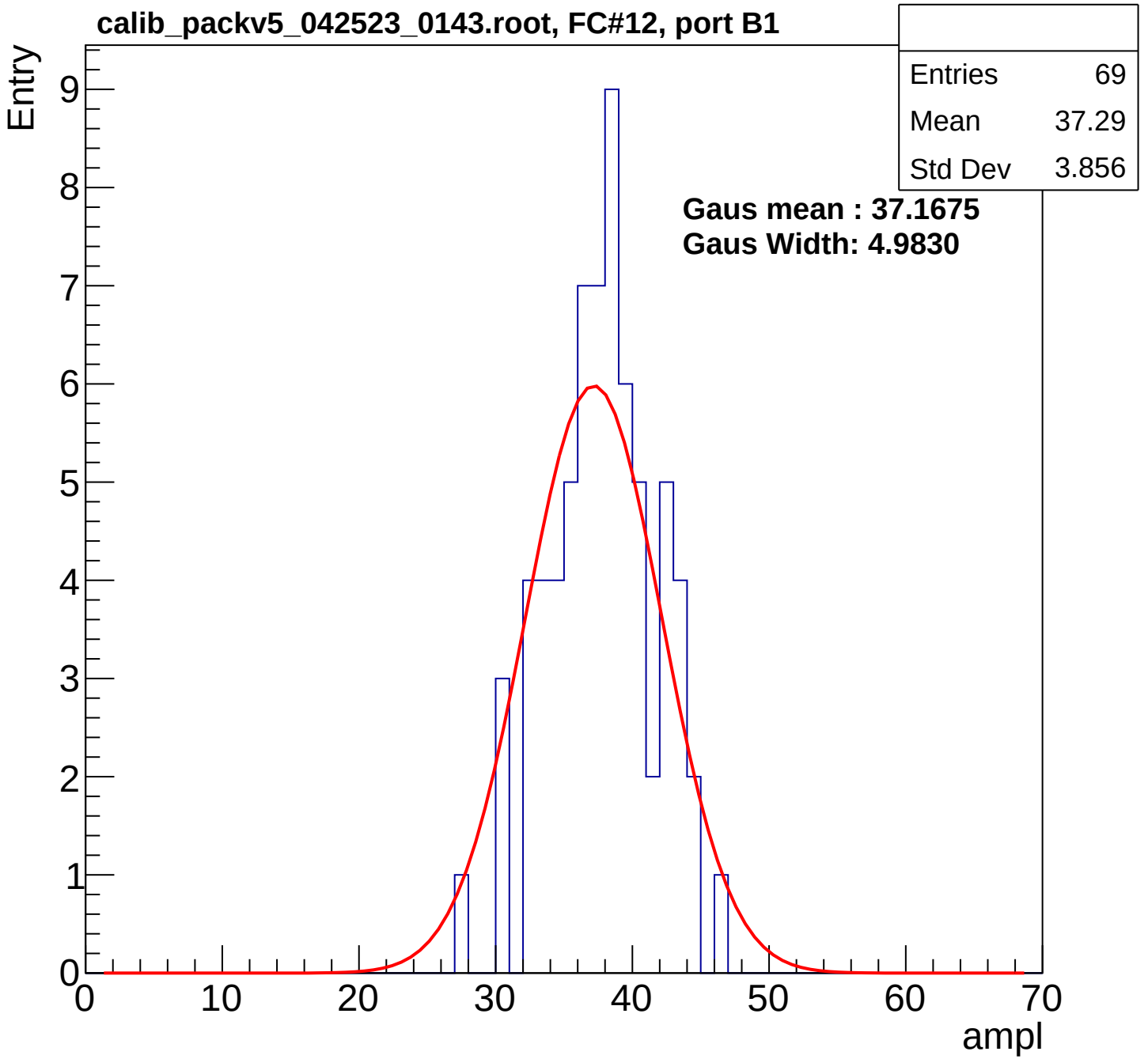

B0L102S, U23-ch27, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 56.59
Std Dev 2.974
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

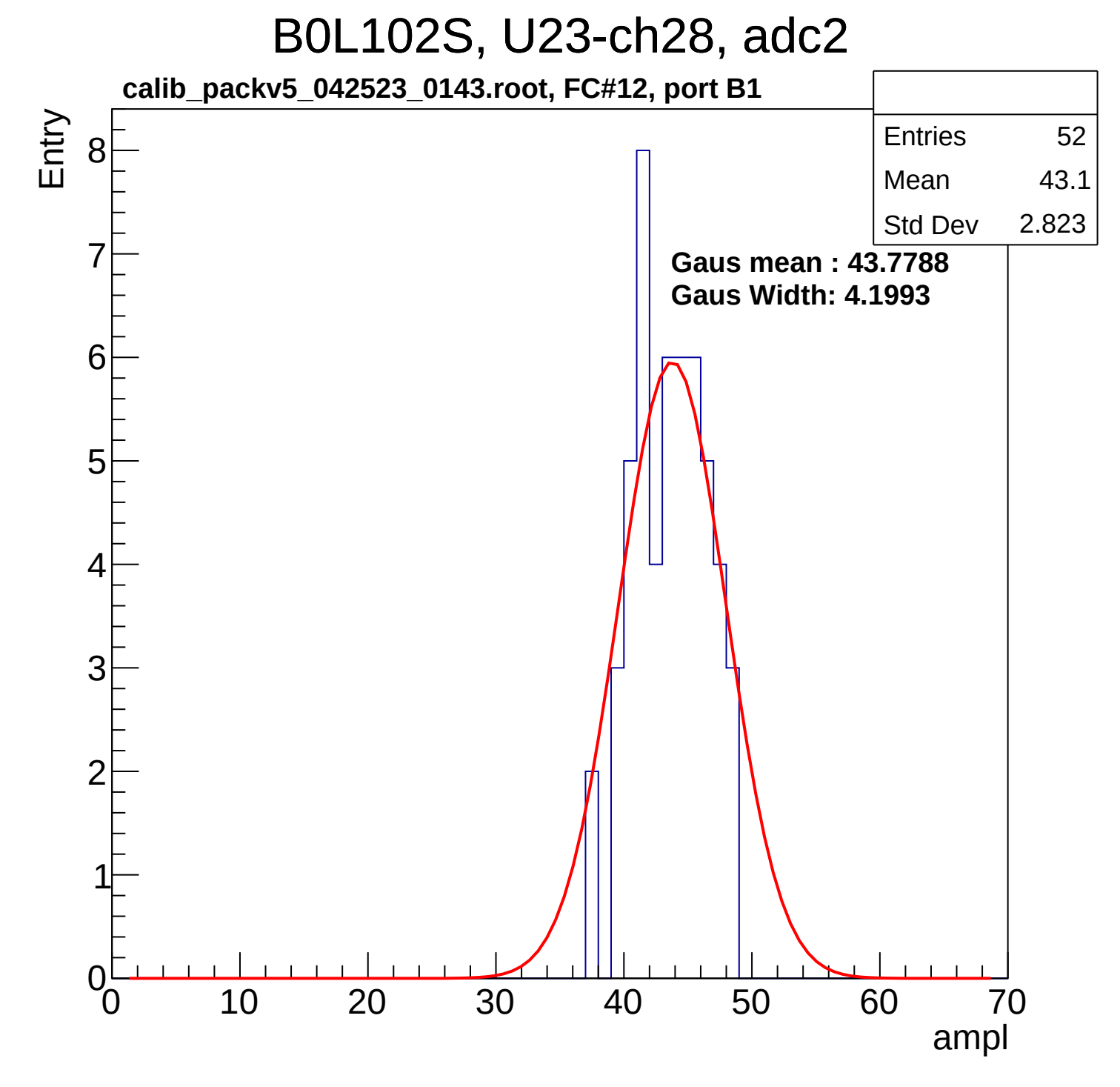

B0L102S, U23-ch27, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 58.97
Std Dev 10.75
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch27, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch27, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch28, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 30.49
Std Dev 3.638
Gaus mean : 31.1116
Gaus Width: 3.7426
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch28, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 37.29
Std Dev 3.856
Gaus mean : 37.1675
Gaus Width: 4.9830
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch28, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 43.1
Std Dev 2.823
Gaus mean : 43.7788
Gaus Width: 4.1993
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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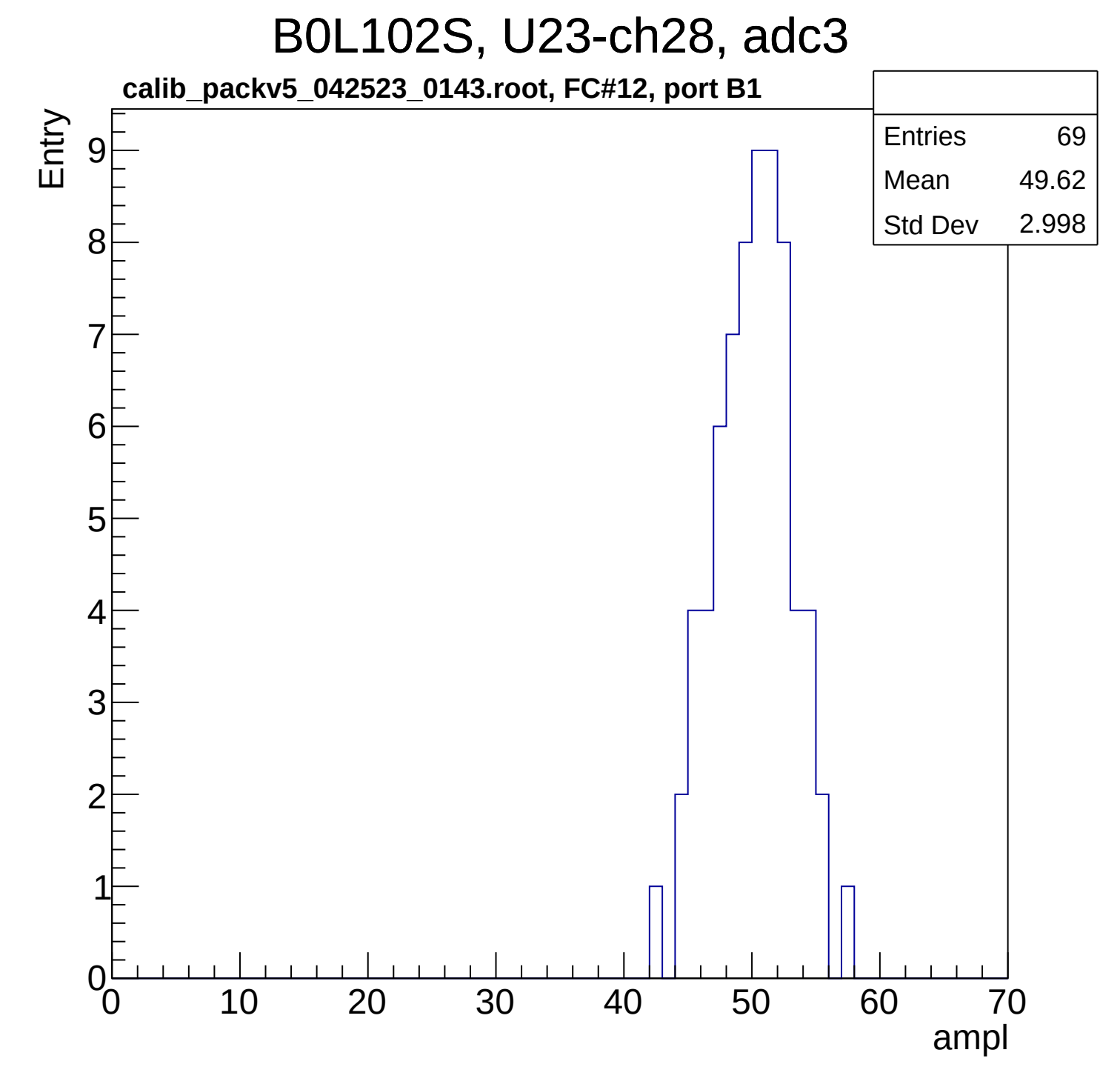
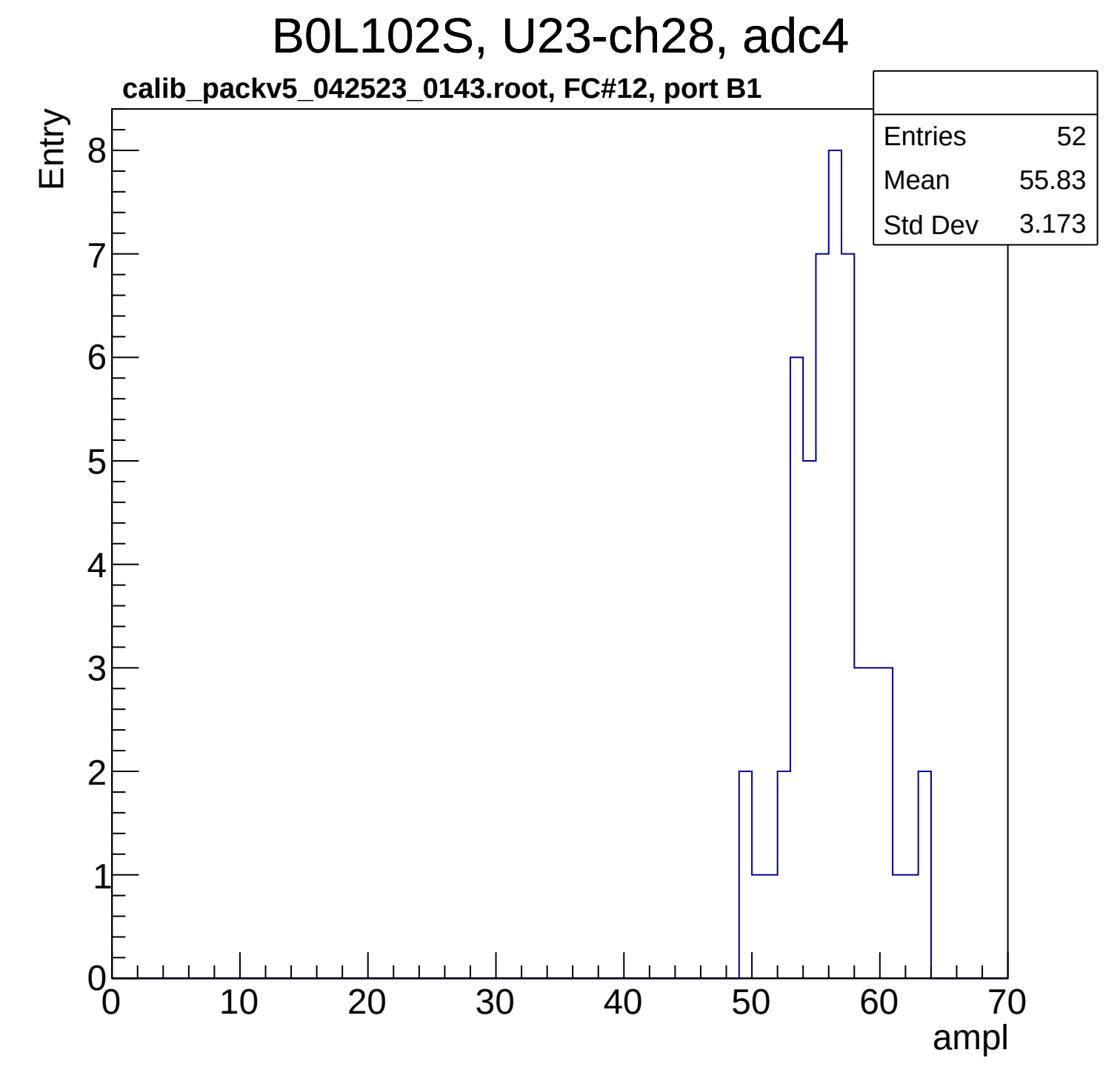
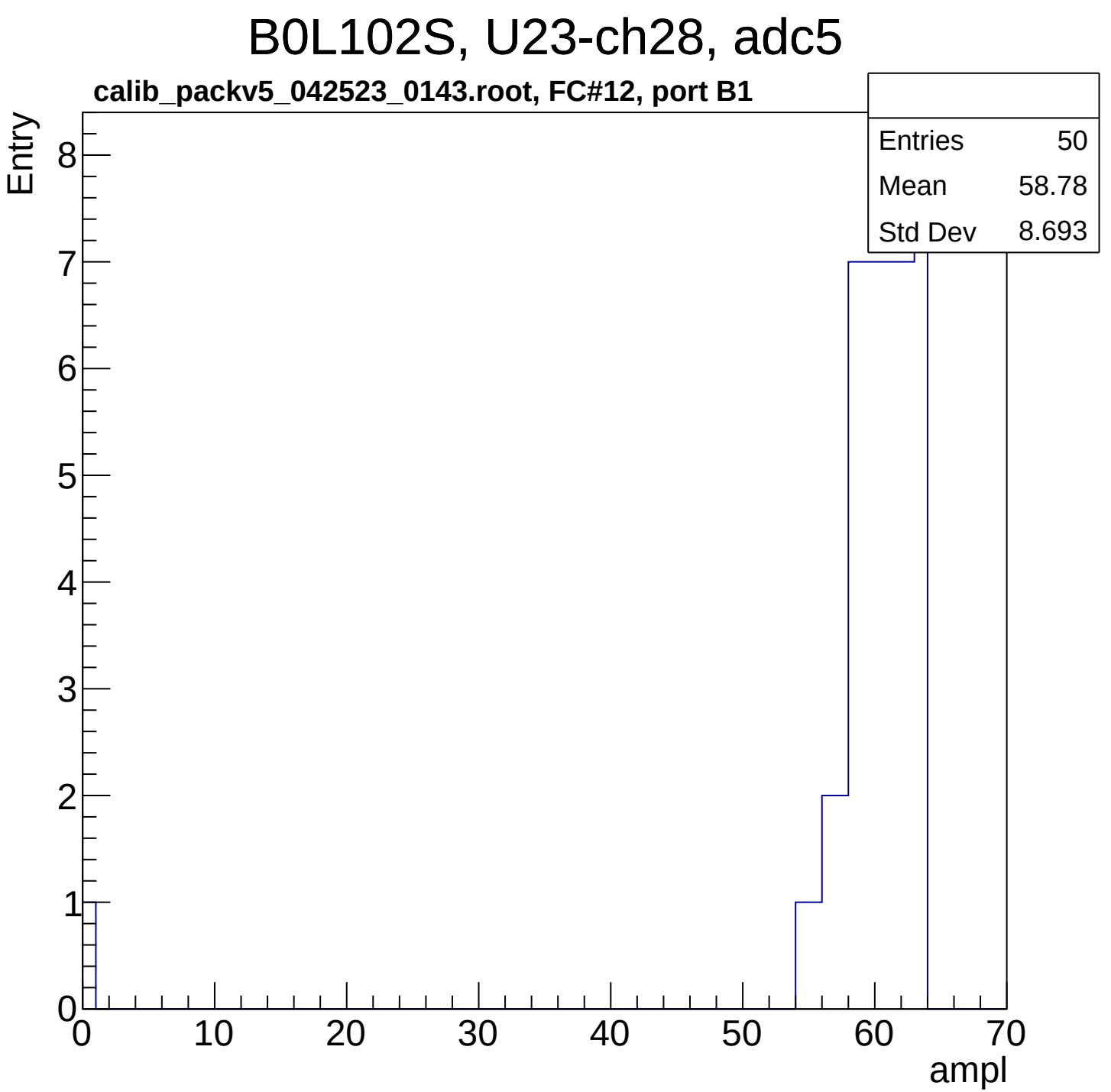
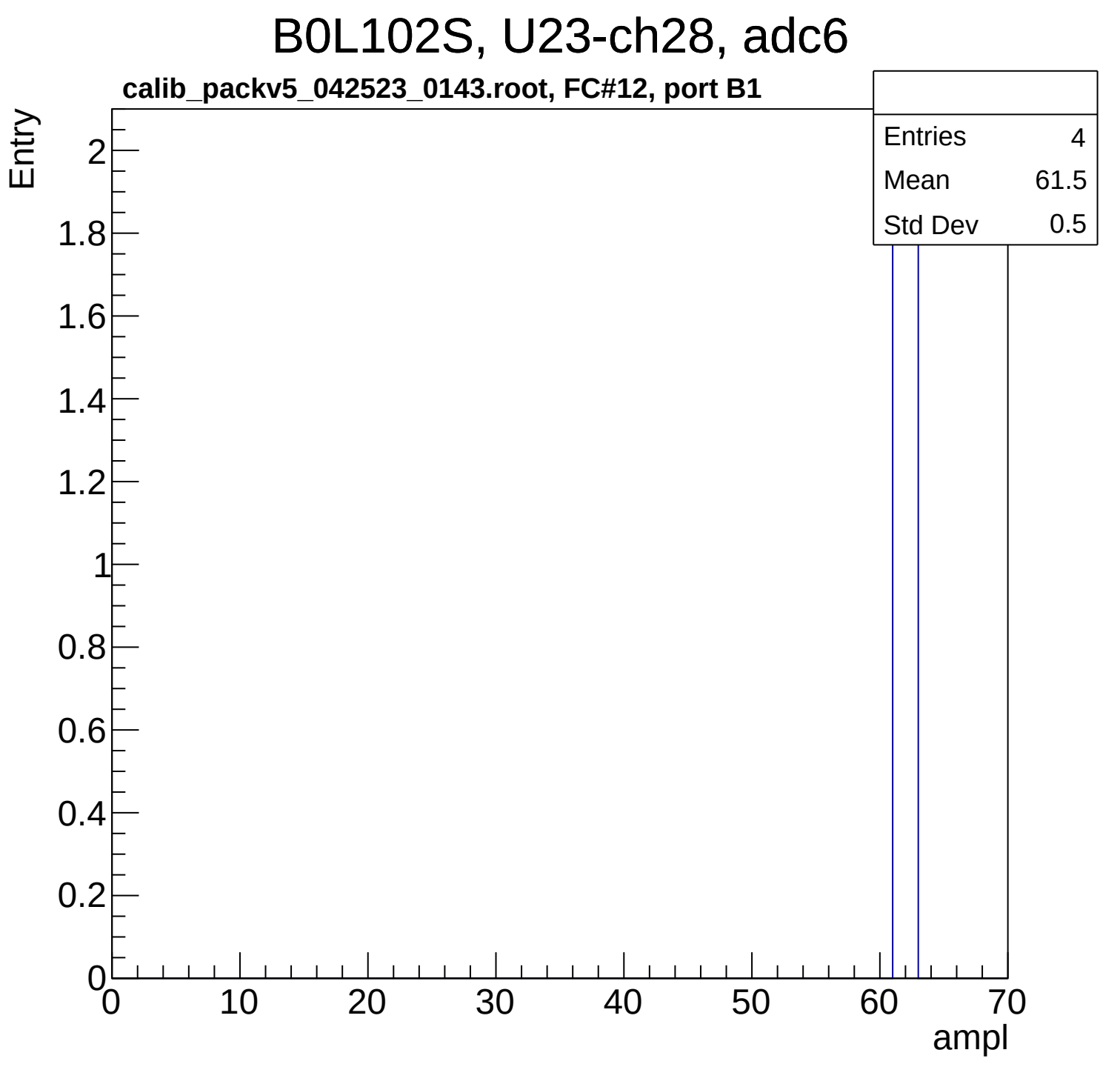
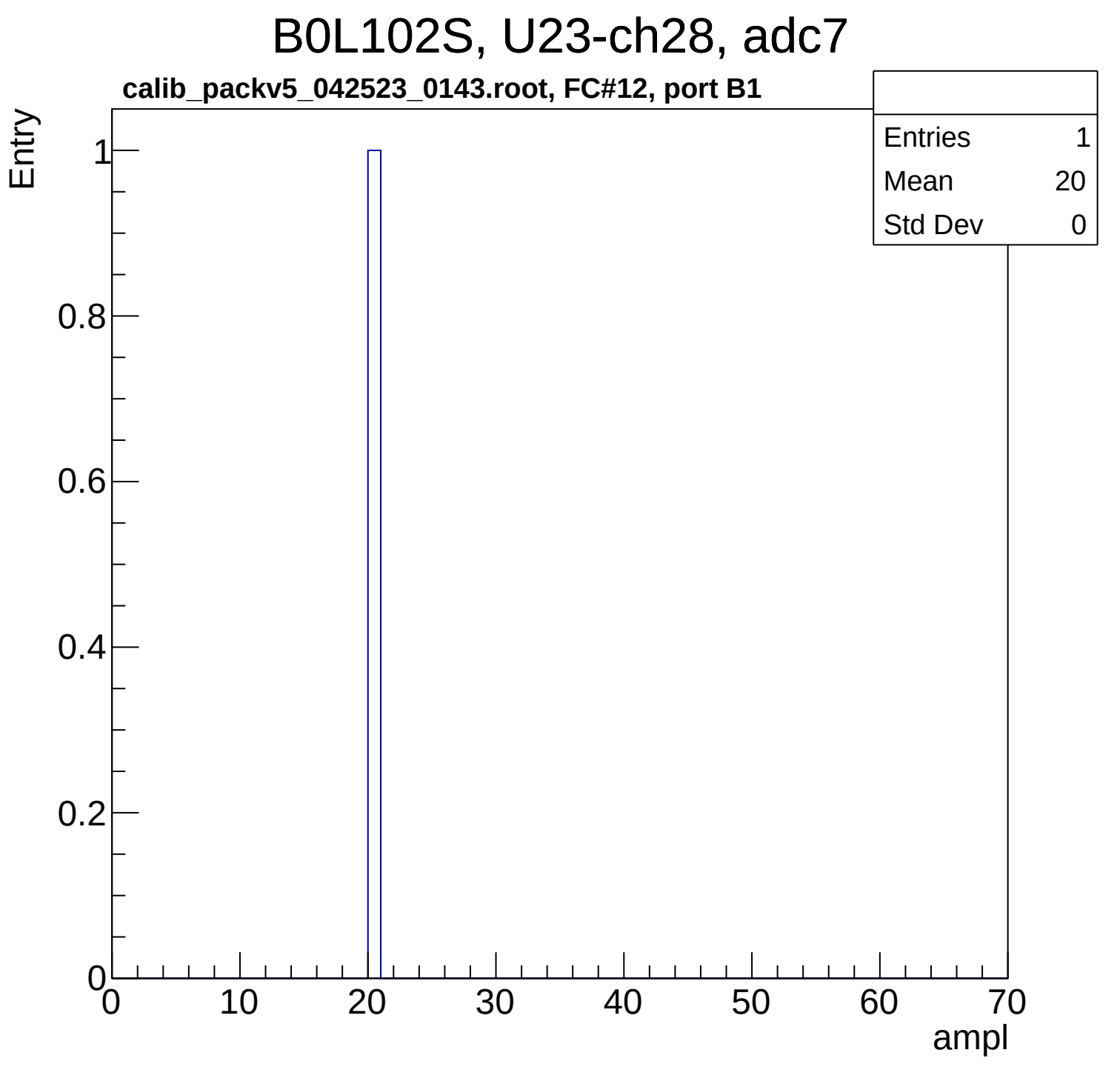
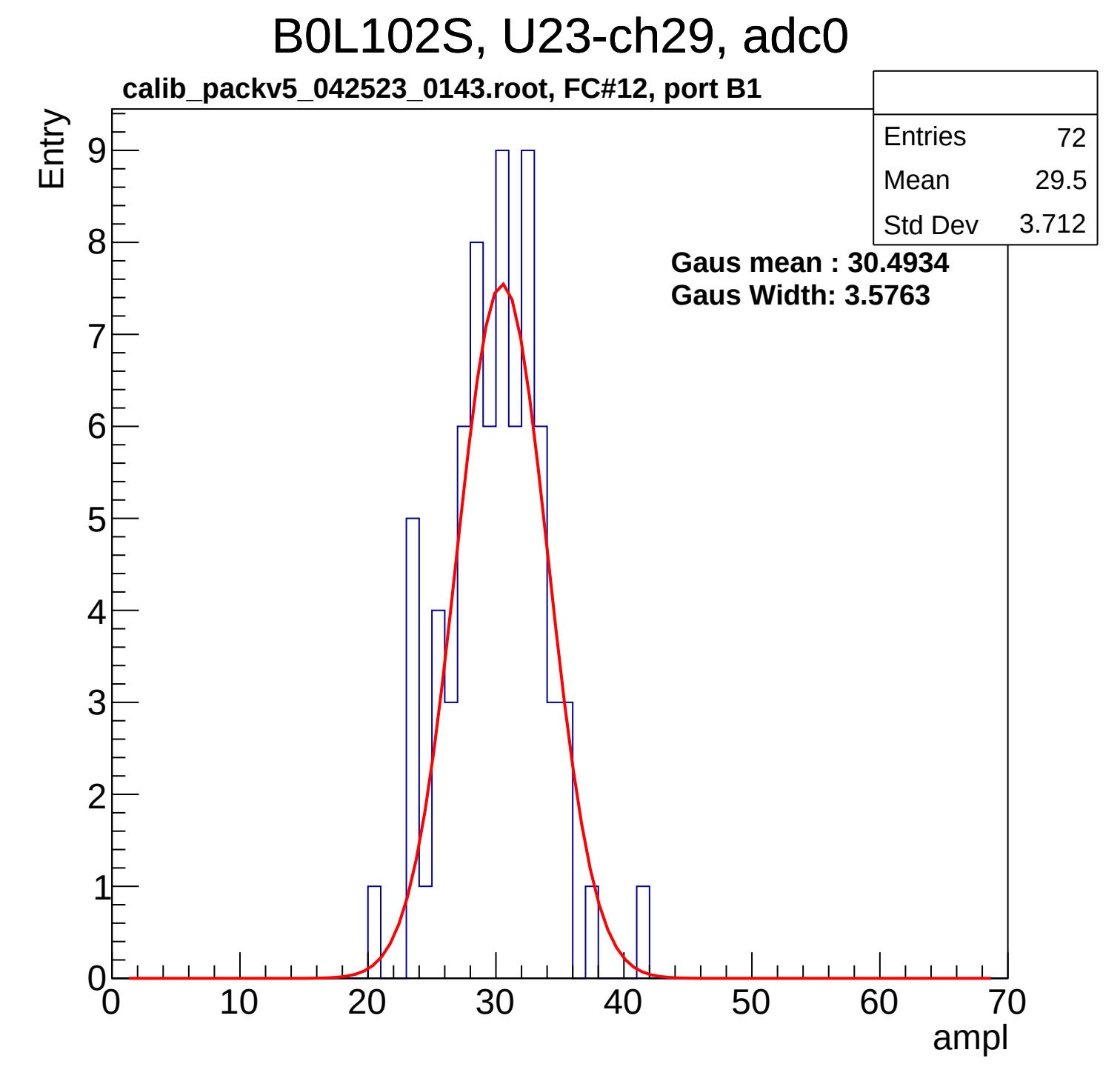

B0L102S, U23-ch28, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 49.62
Std Dev 2.998
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

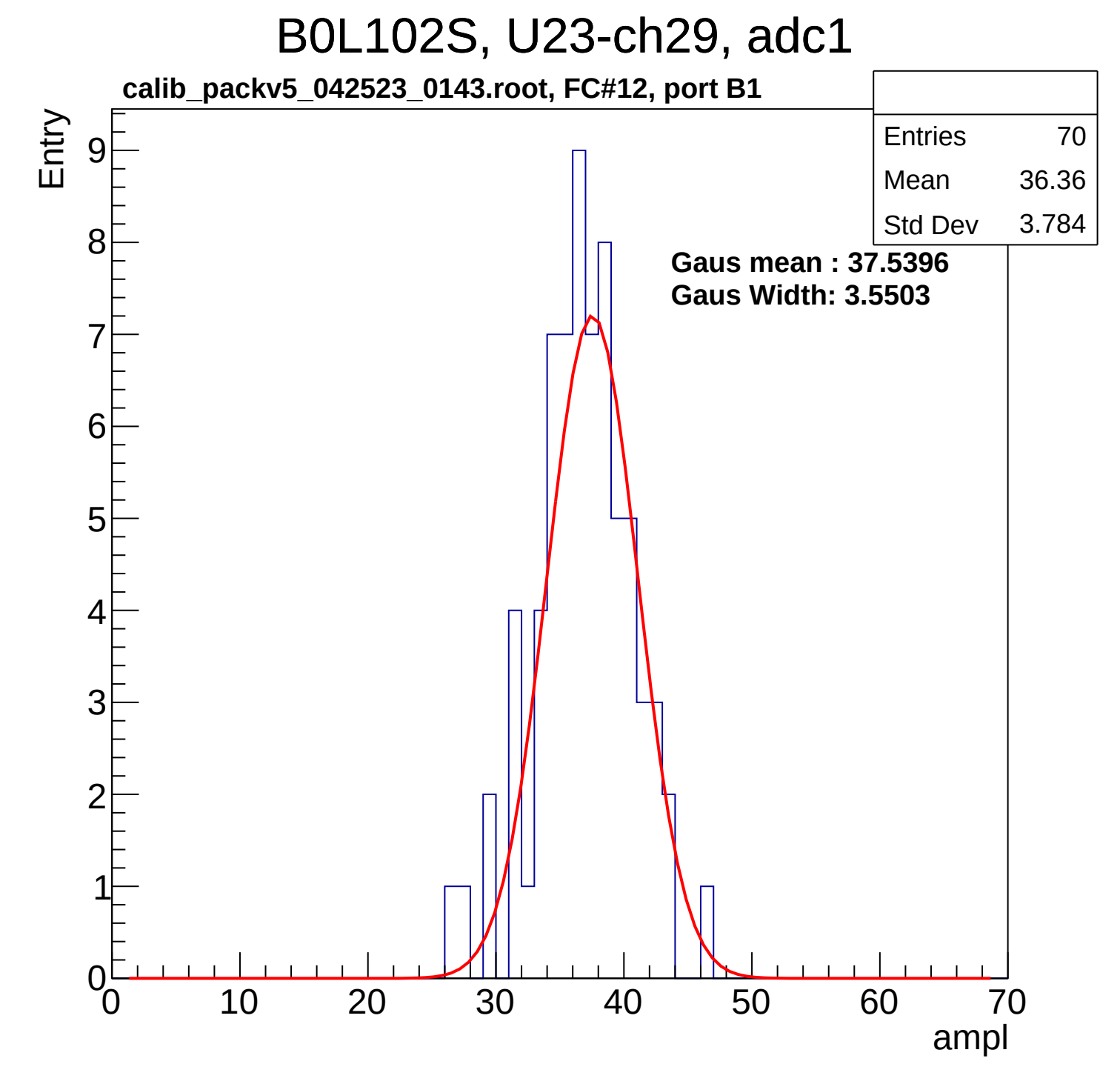

B0L102S, U23-ch28, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 55.83
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

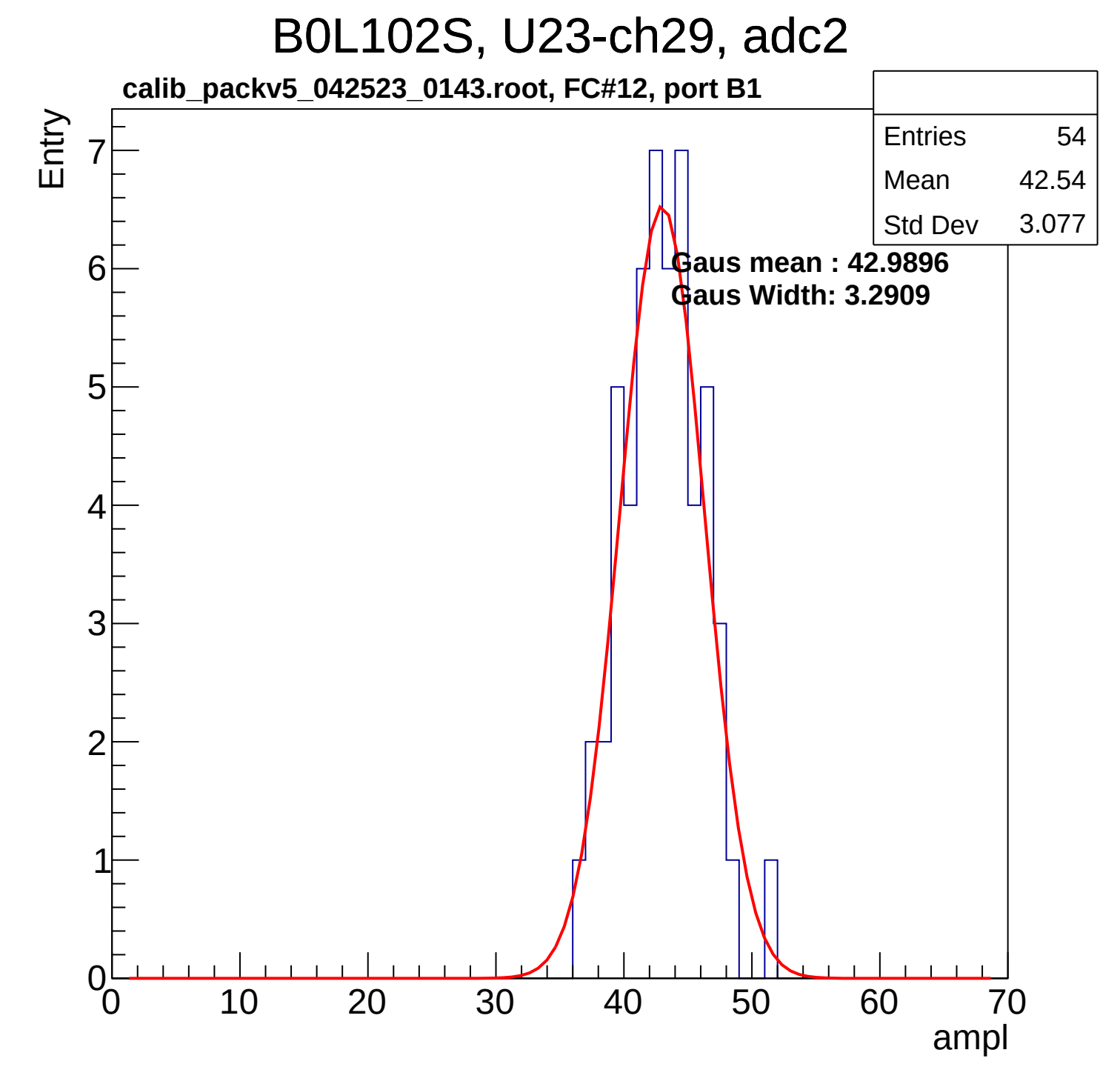

B0L102S, U23-ch28, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.78
Std Dev 8.693
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

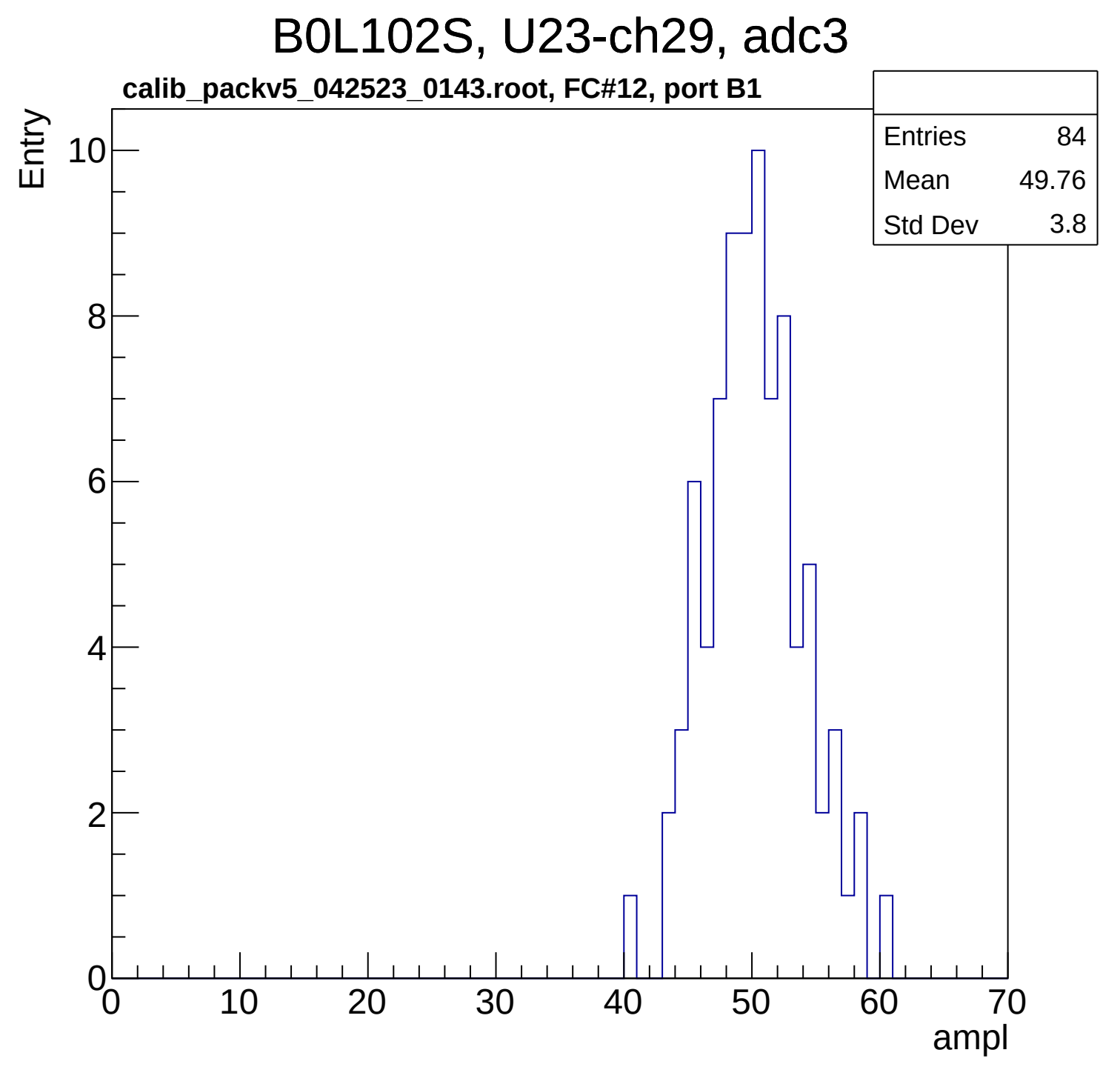

B0L102S, U23-ch28, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

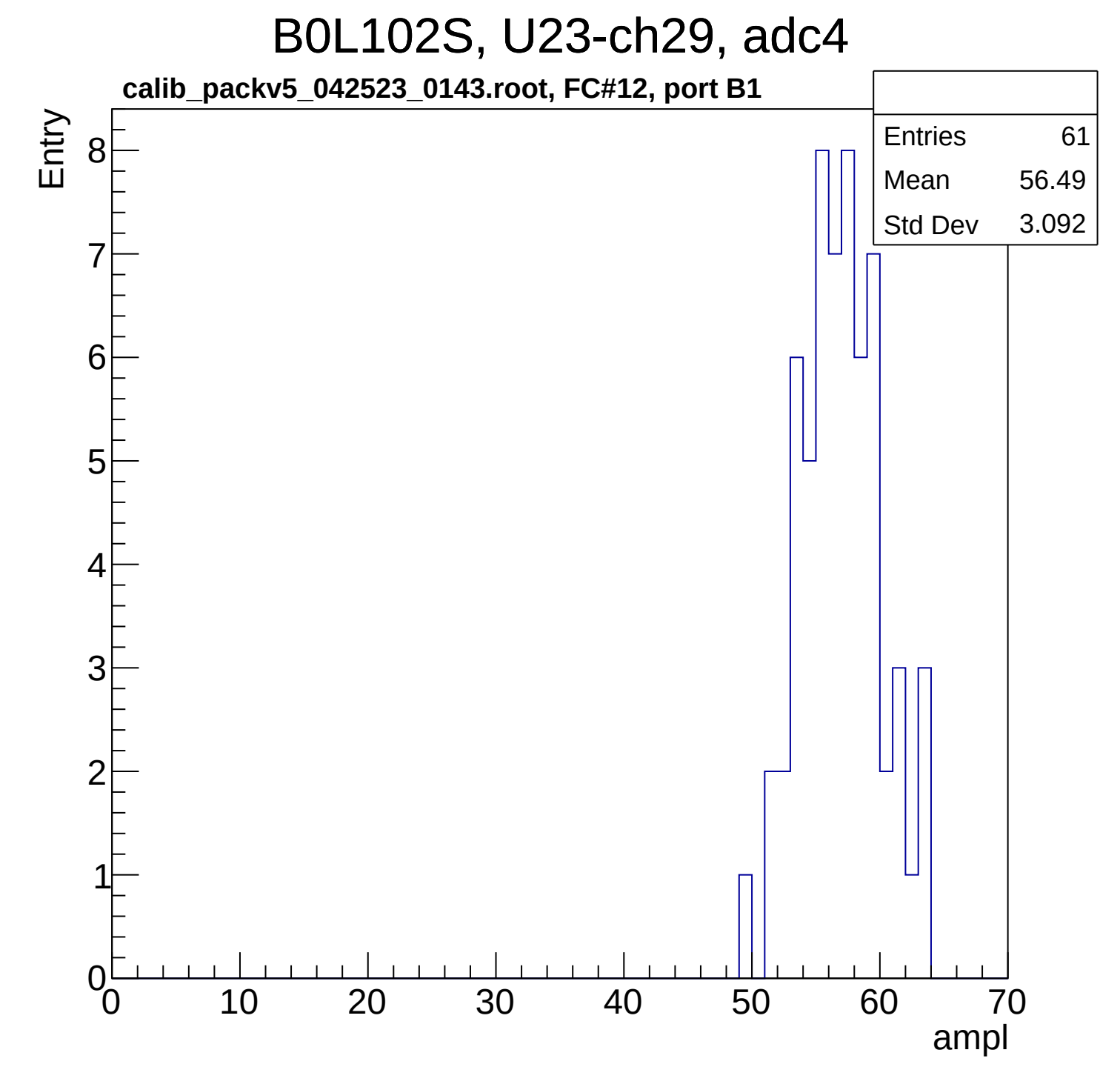

B0L102S, U23-ch28, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

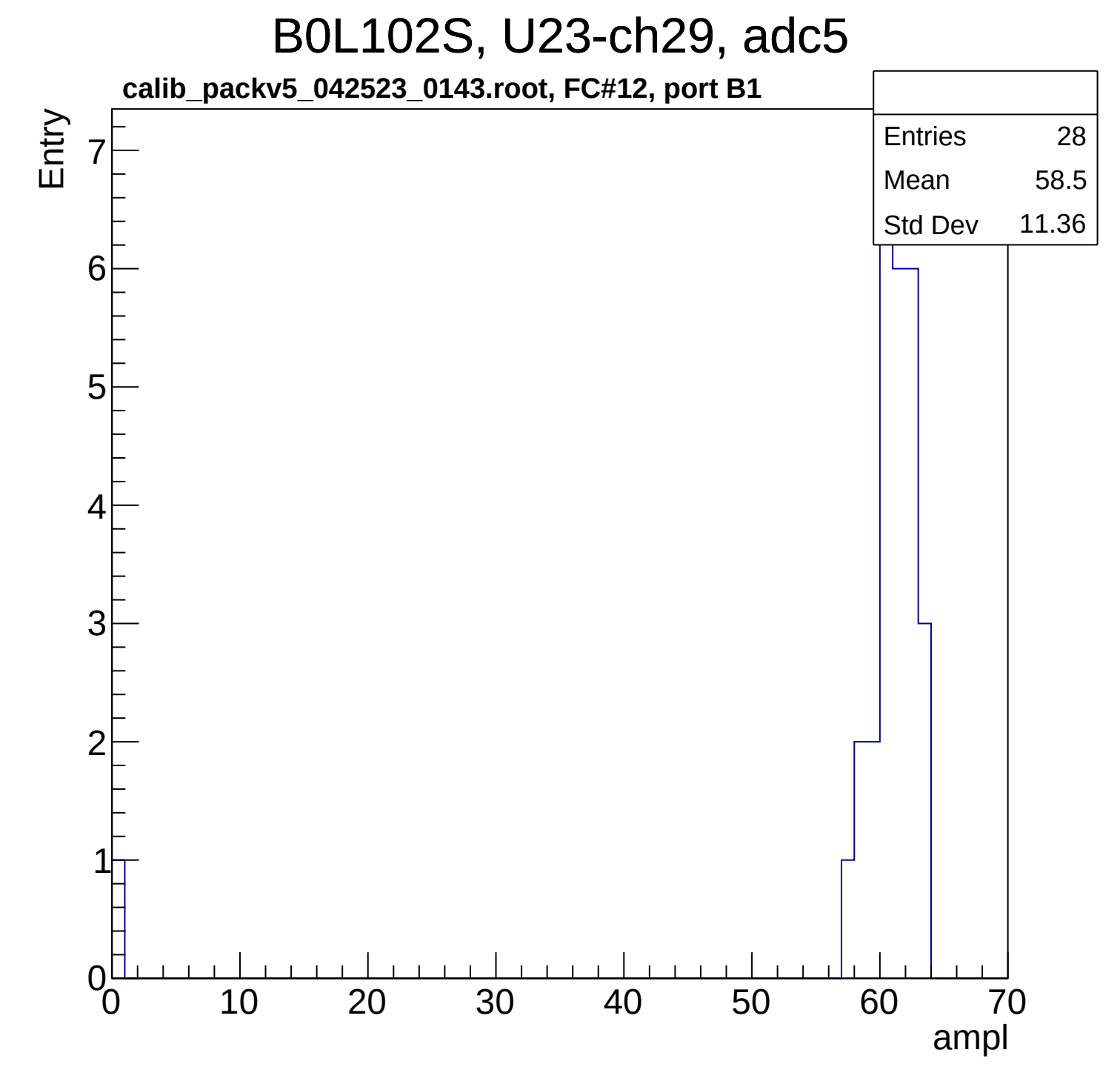

B0L102S, U23-ch29, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 29.5
Std Dev 3.712
Gaus mean : 30.4934
Gaus Width: 3.5763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch29, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 36.36
Std Dev 3.784
Gaus mean : 37.5396
Gaus Width: 3.5503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch29, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 42.54
Std Dev 3.077
Gaus mean : 42.9896
Gaus Width: 3.2909
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch29, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 49.76
Std Dev 3.8
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch29, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 56.49
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch29, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 28
Mean 58.5
Std Dev 11.36
Entry
ampl
0
1
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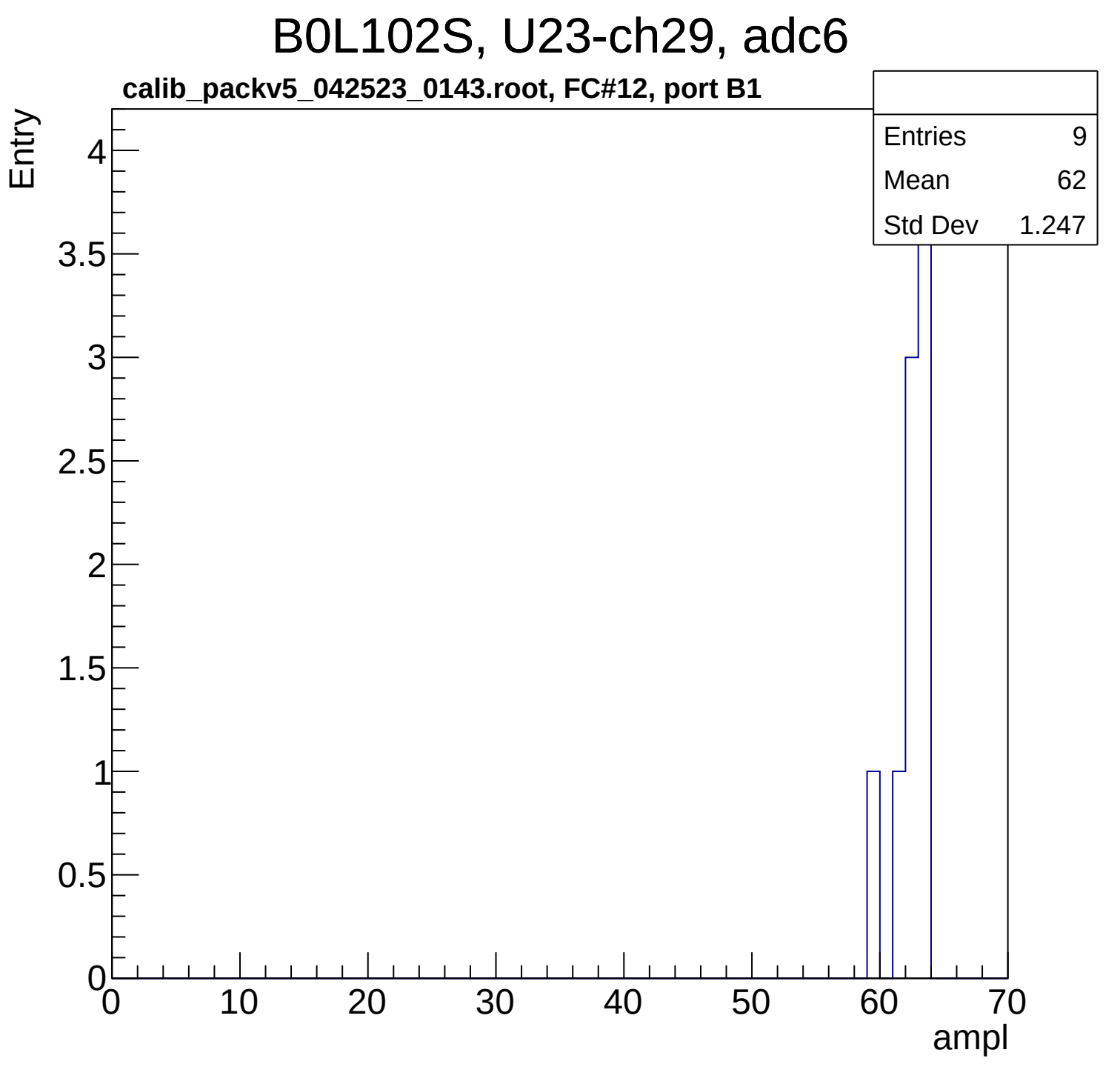
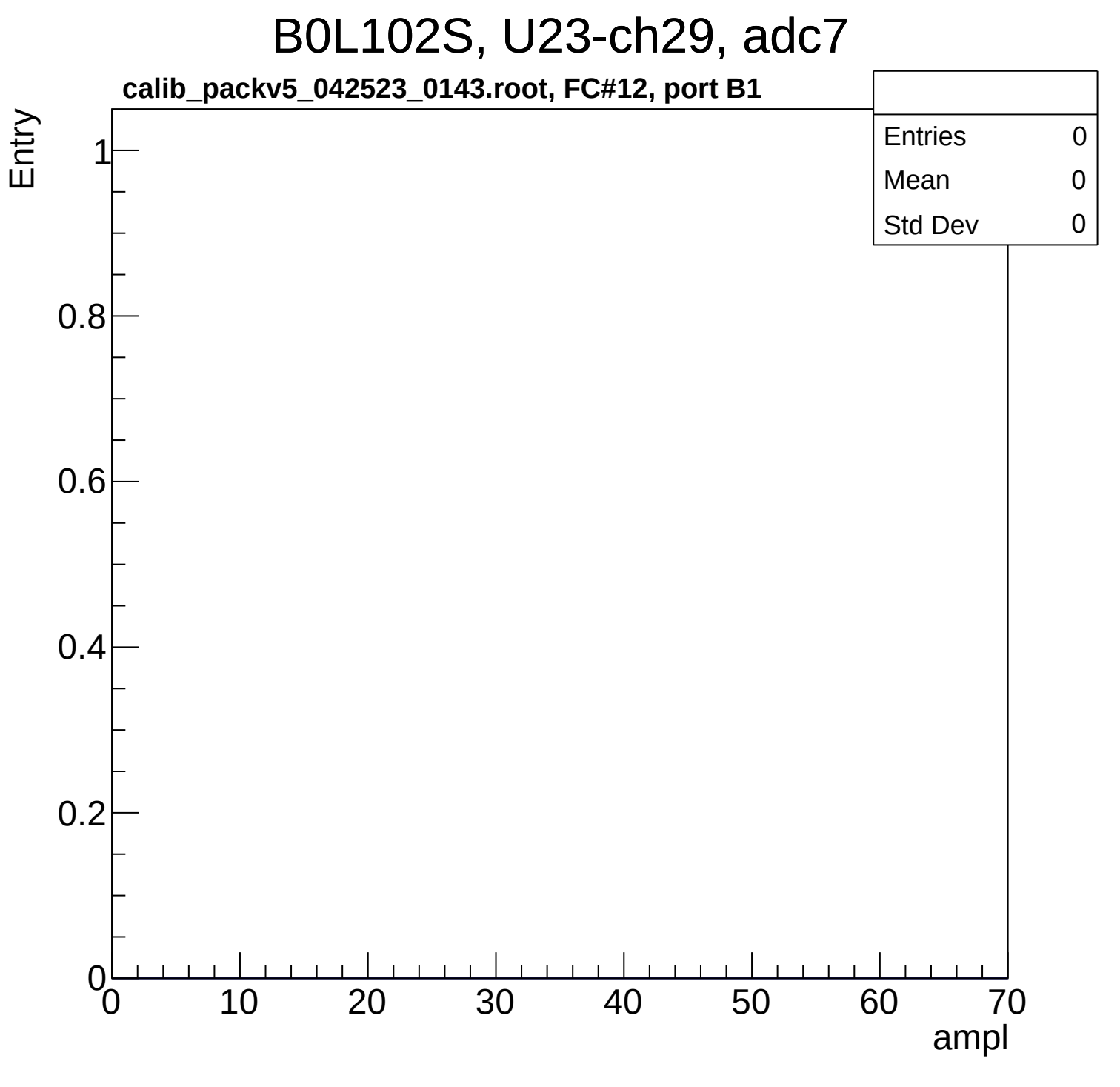
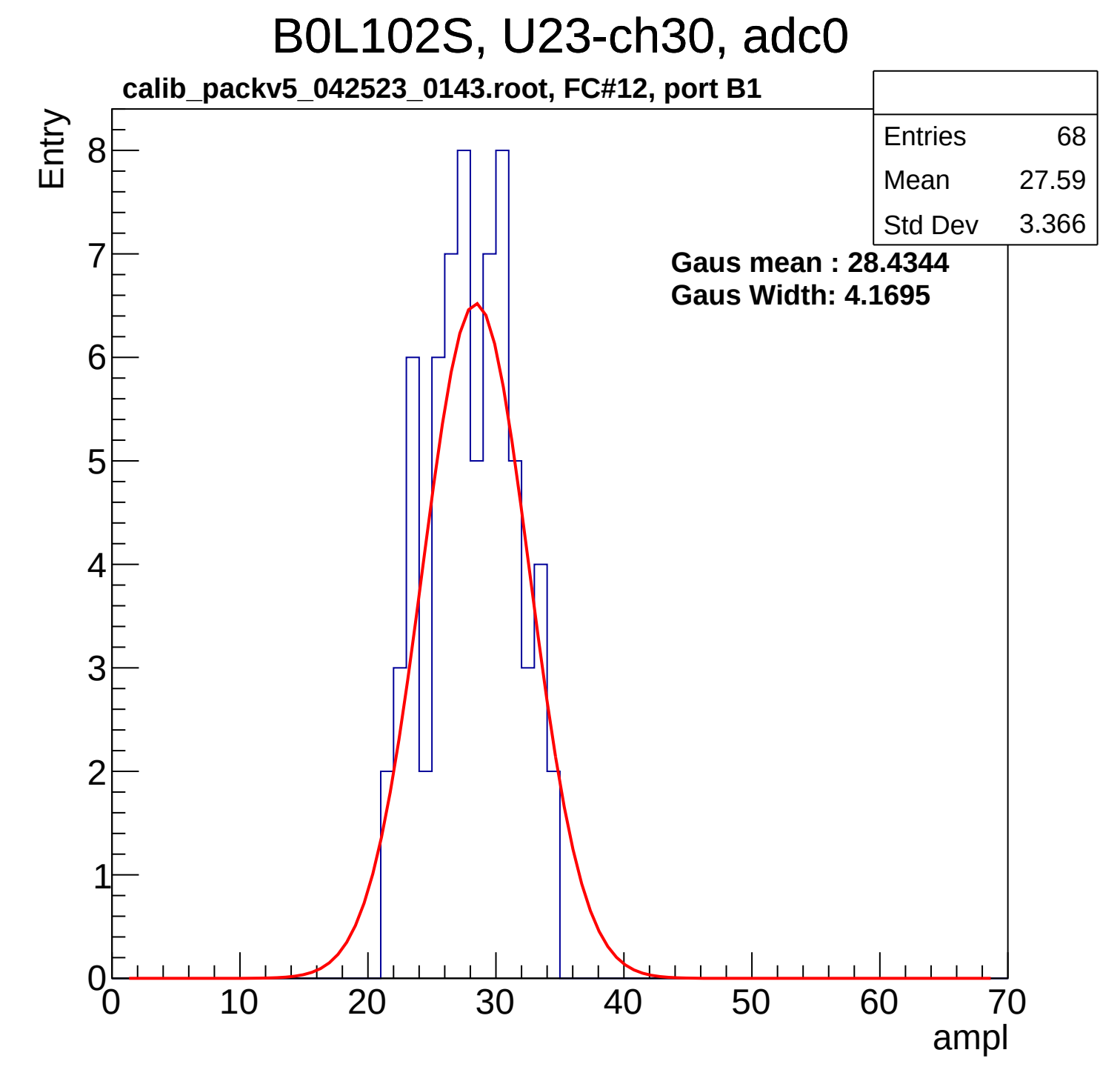
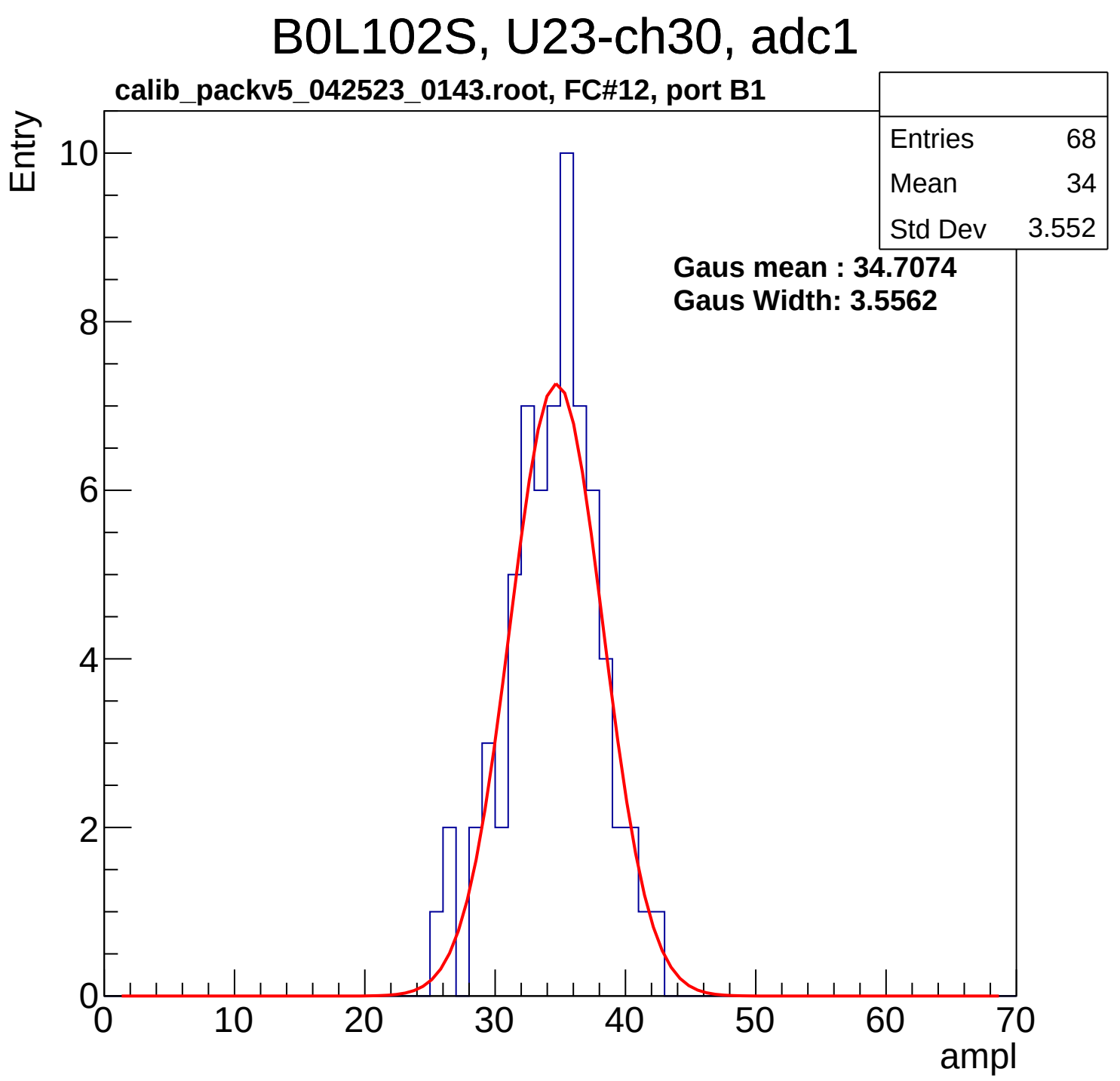
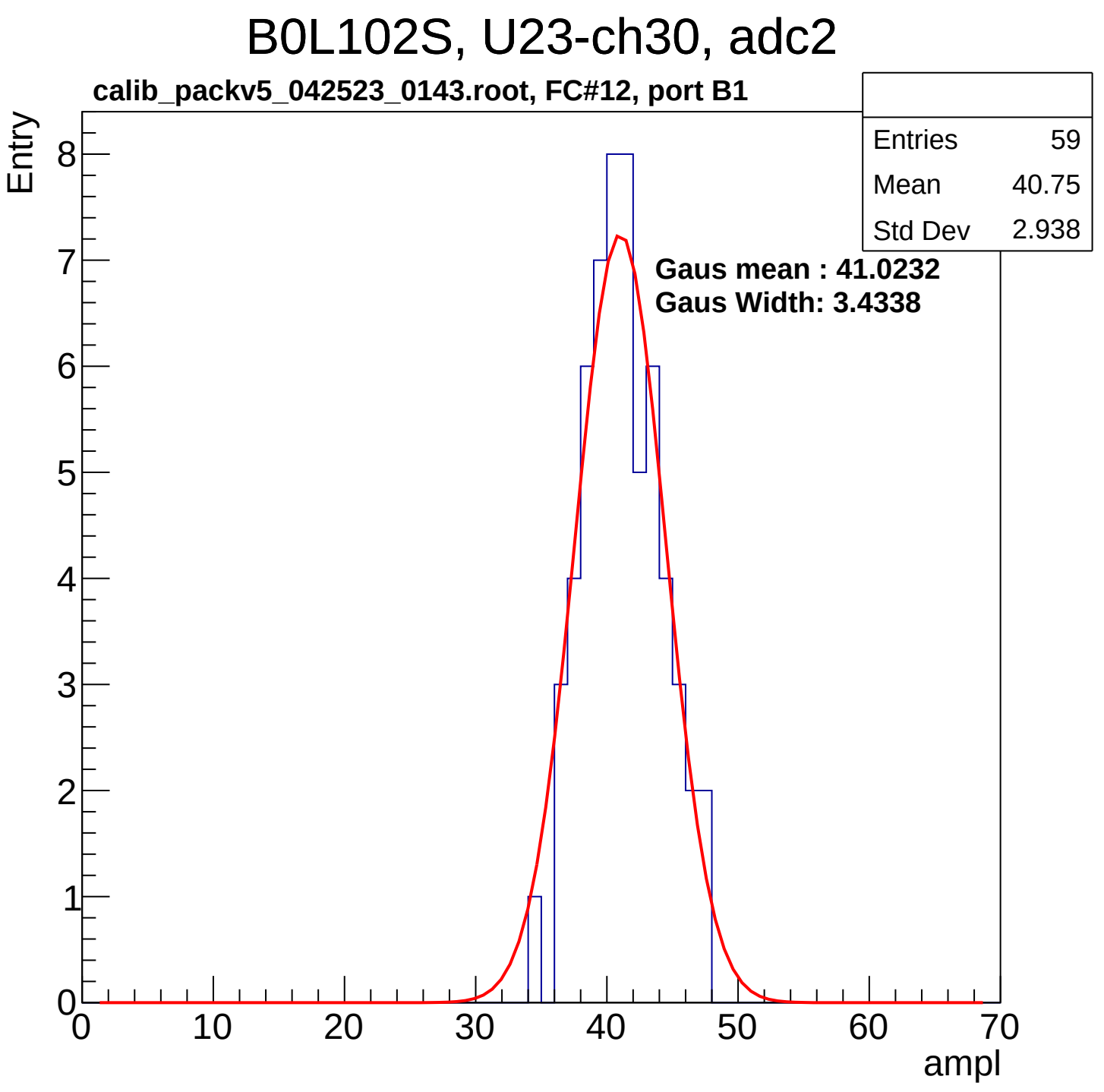
3
4
5
6
7
0
10
20
30
40
50
60
70

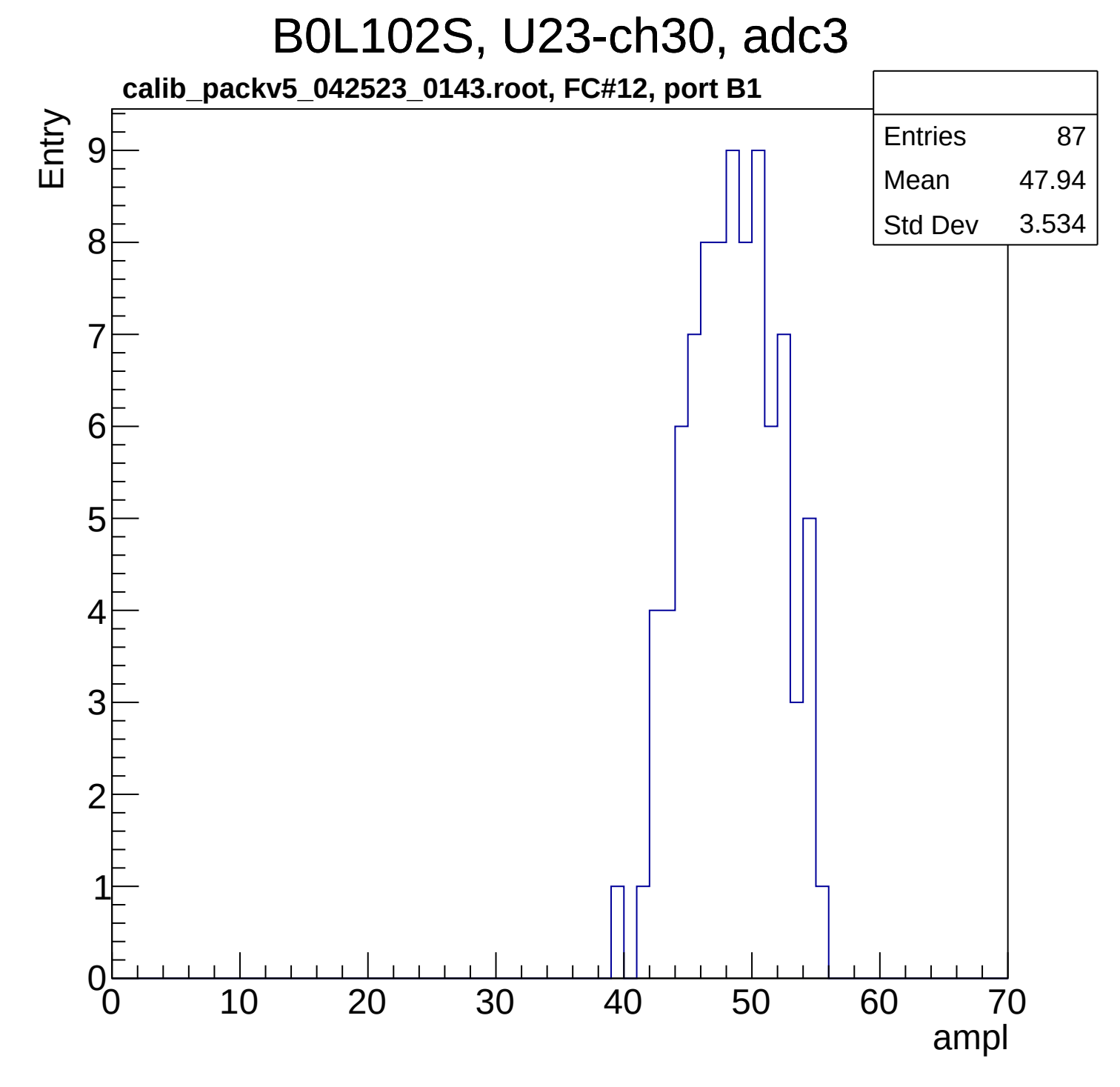

B0L102S, U23-ch29, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 62
Std Dev 1.247
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch29, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch30, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 27.59
Std Dev 3.366
Gaus mean : 28.4344
Gaus Width: 4.1695
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch30, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 34
Std Dev 3.552
Gaus mean : 34.7074
Gaus Width: 3.5562
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch30, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 40.75
Std Dev 2.938
Gaus mean : 41.0232
Gaus Width: 3.4338
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch30, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 87
Mean 47.94
Std Dev 3.534
Entry
ampl
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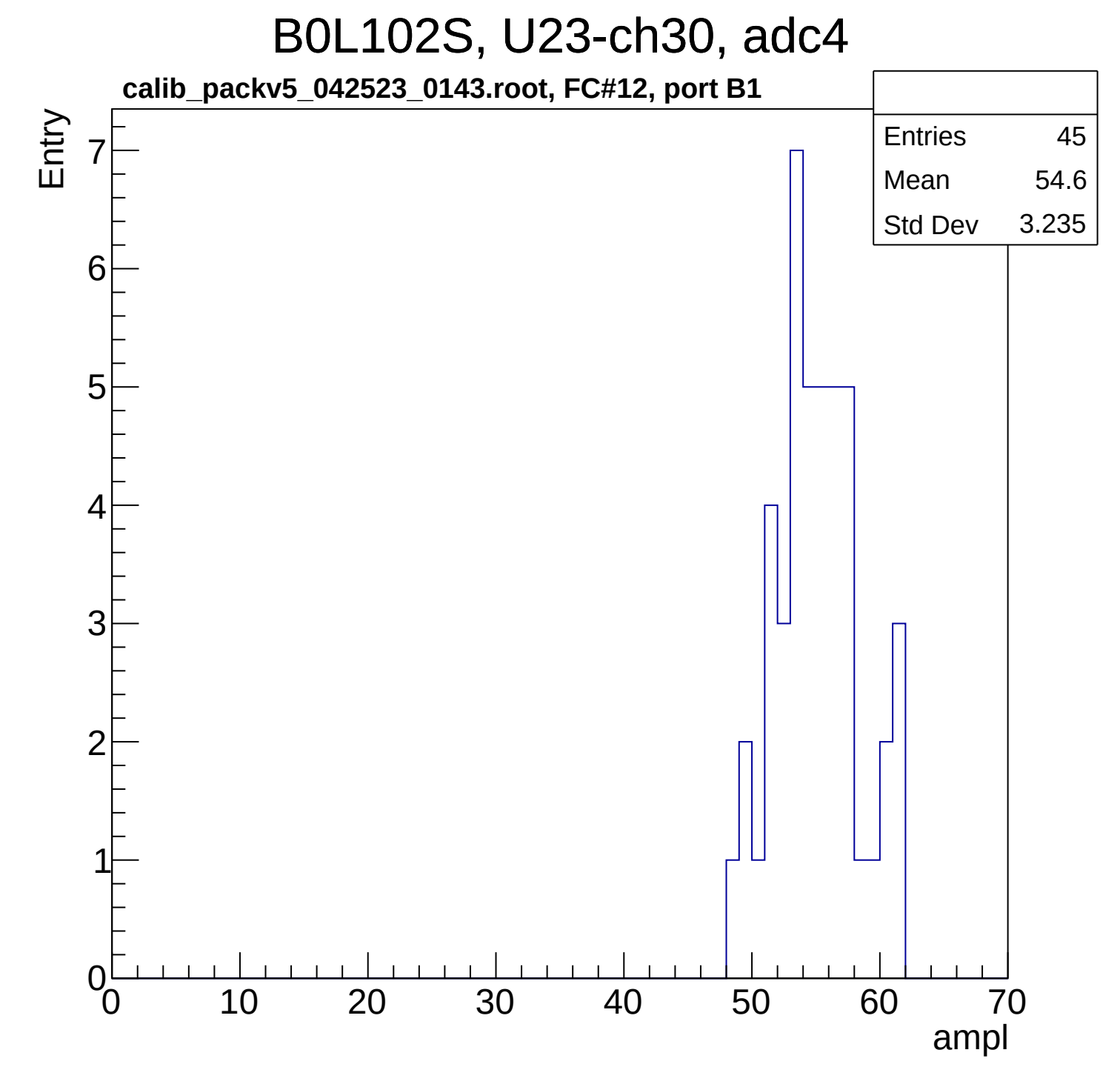
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch30, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 54.6
Std Dev 3.235
Entry
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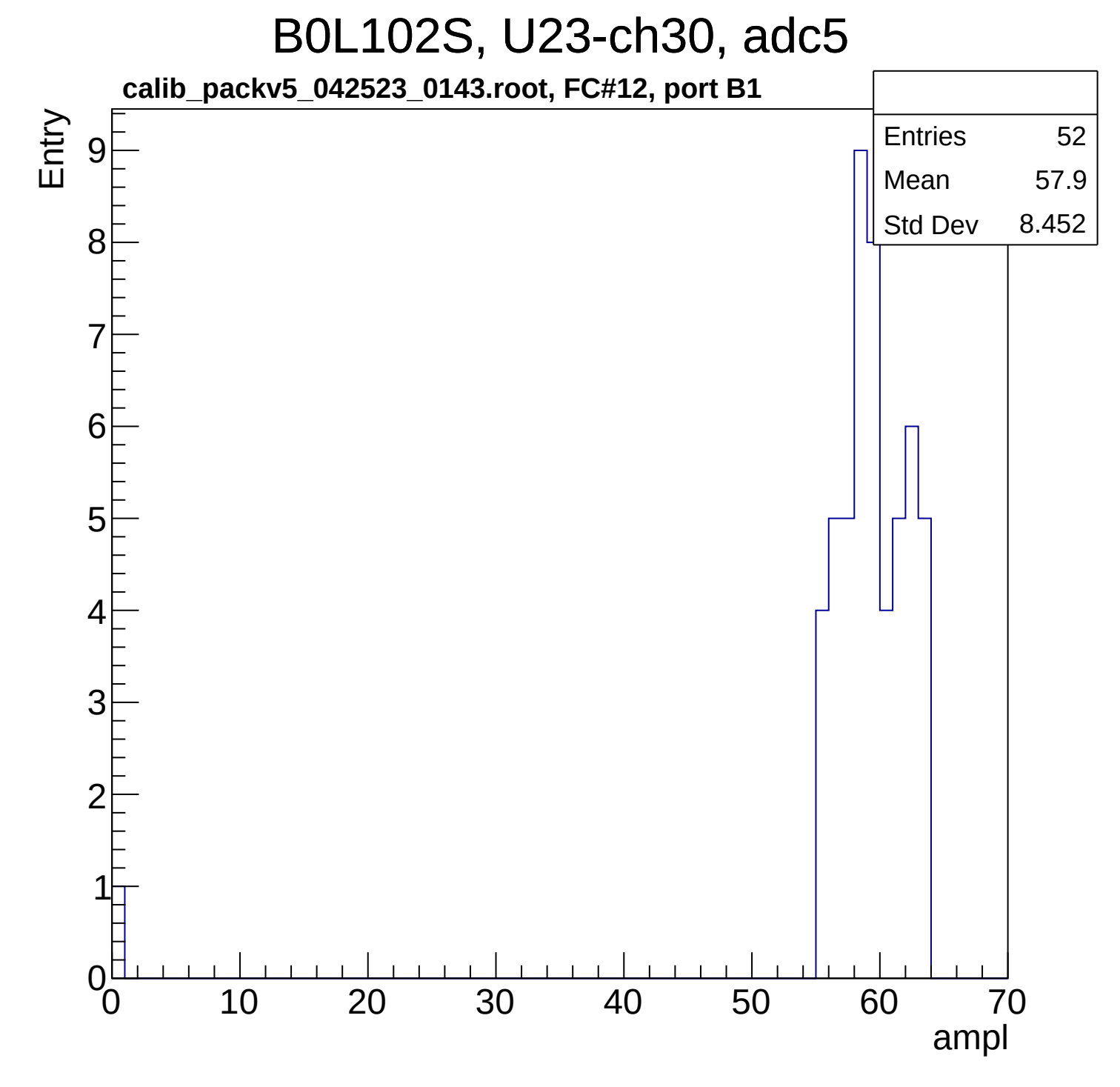
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch30, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 57.9
Std Dev 8.452
Entry
ampl
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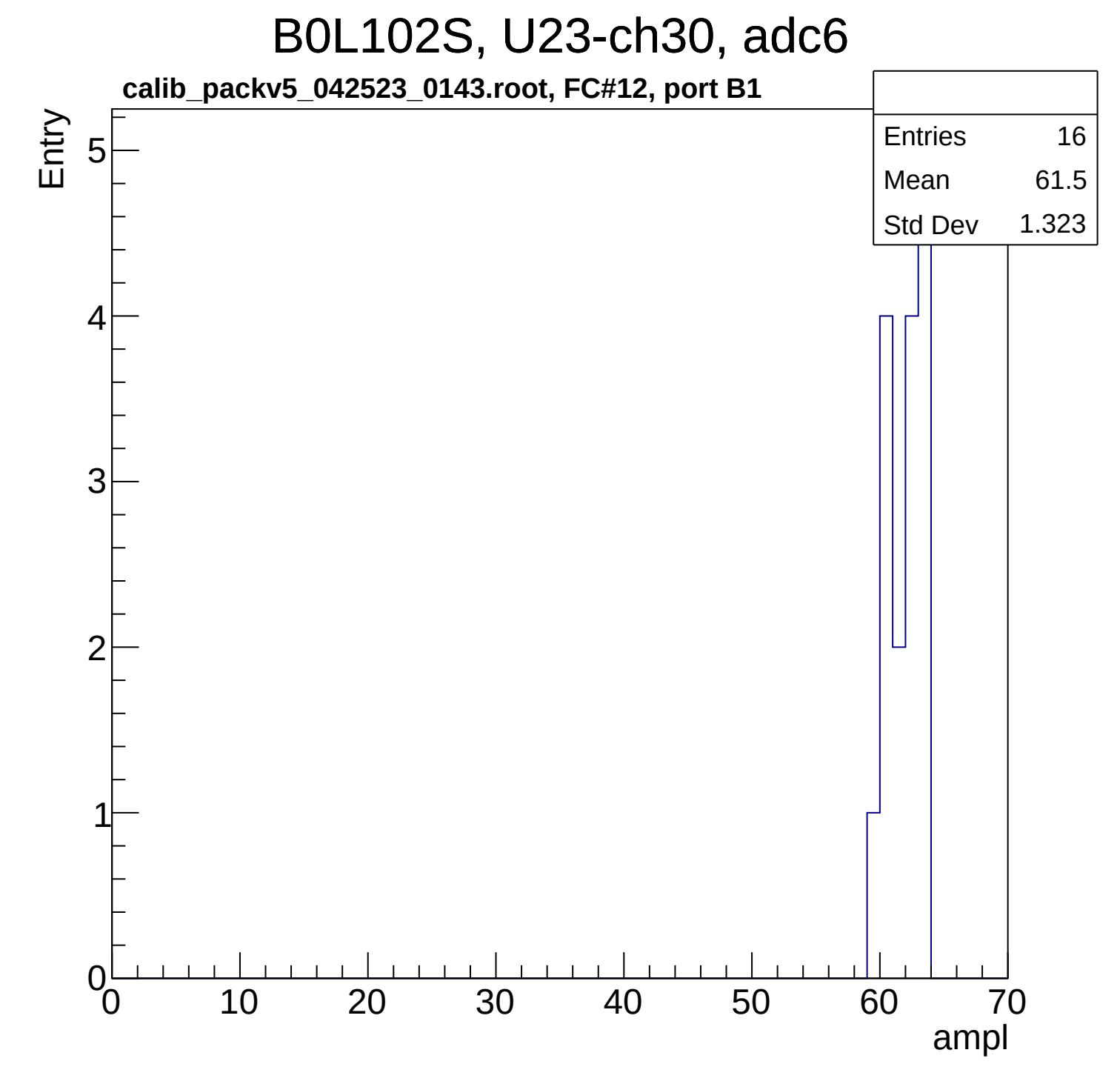
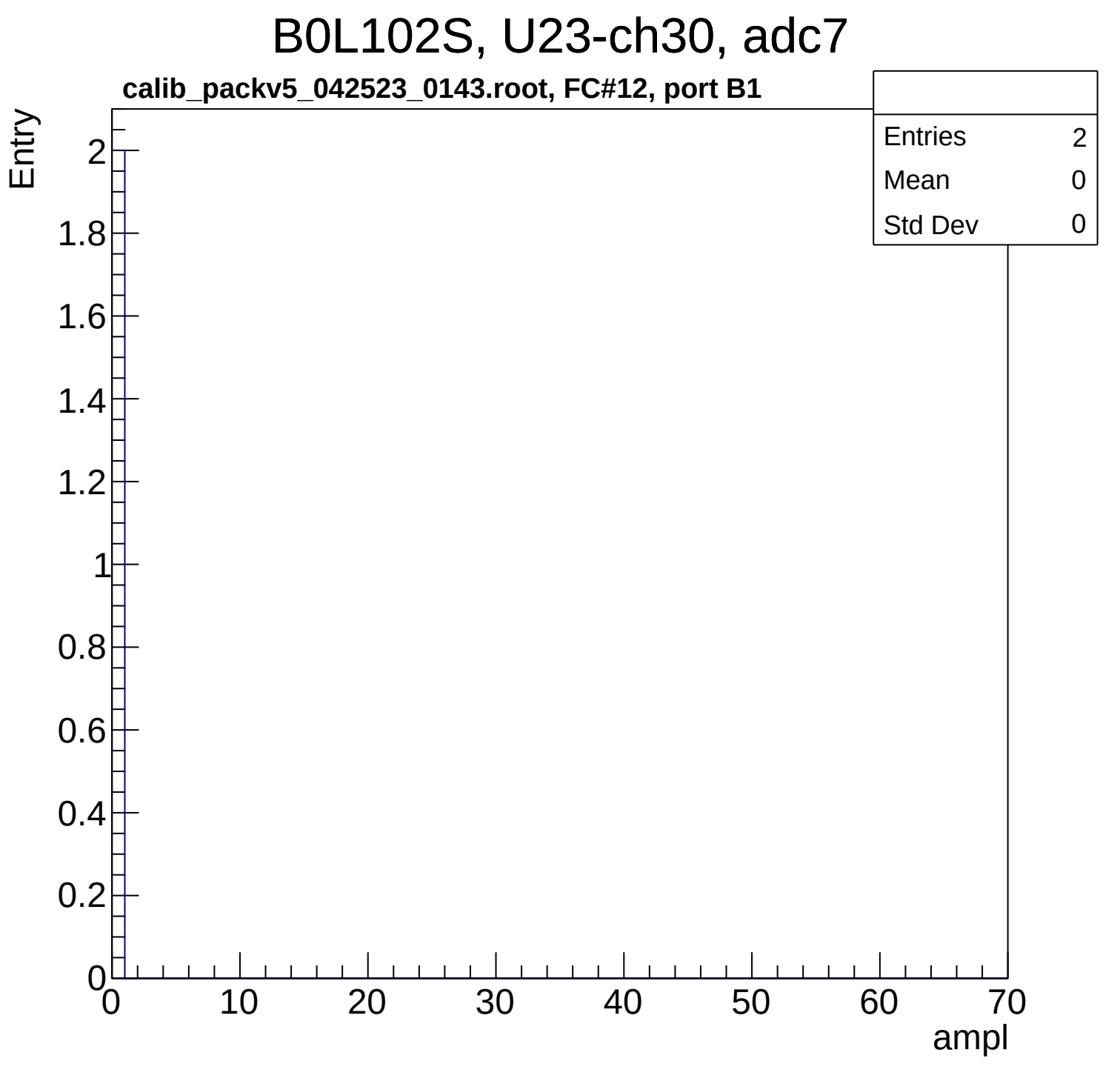
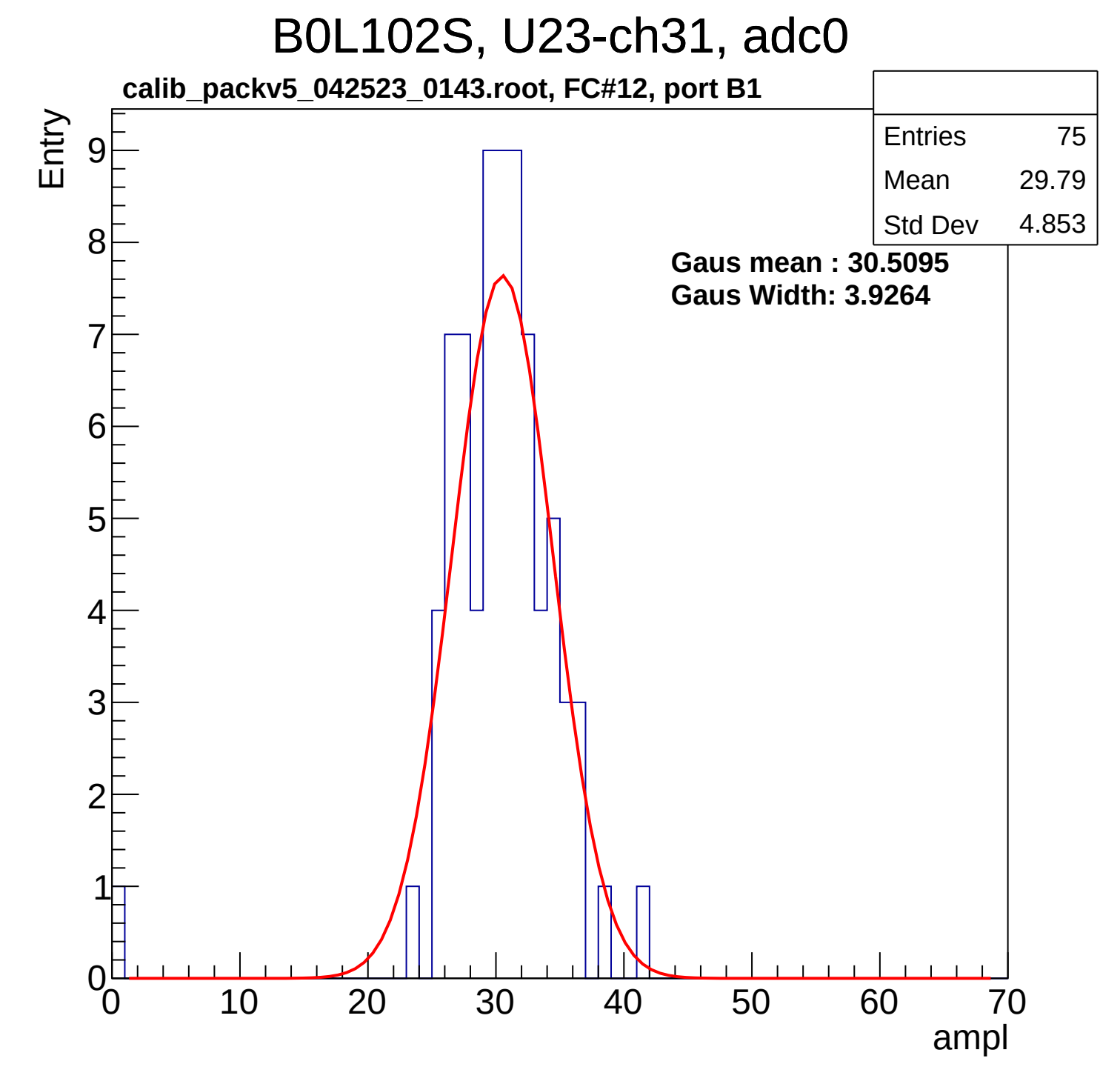
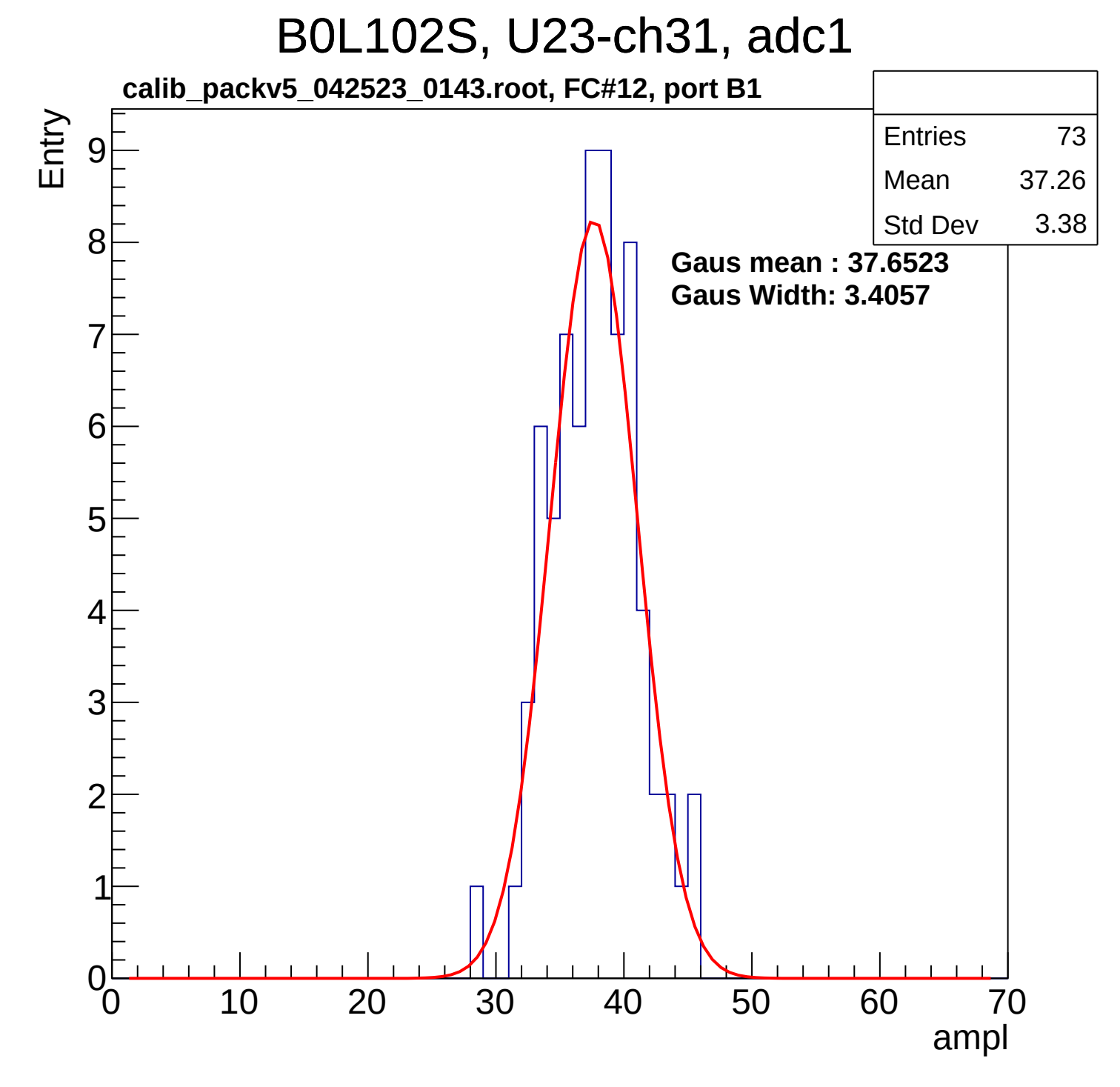
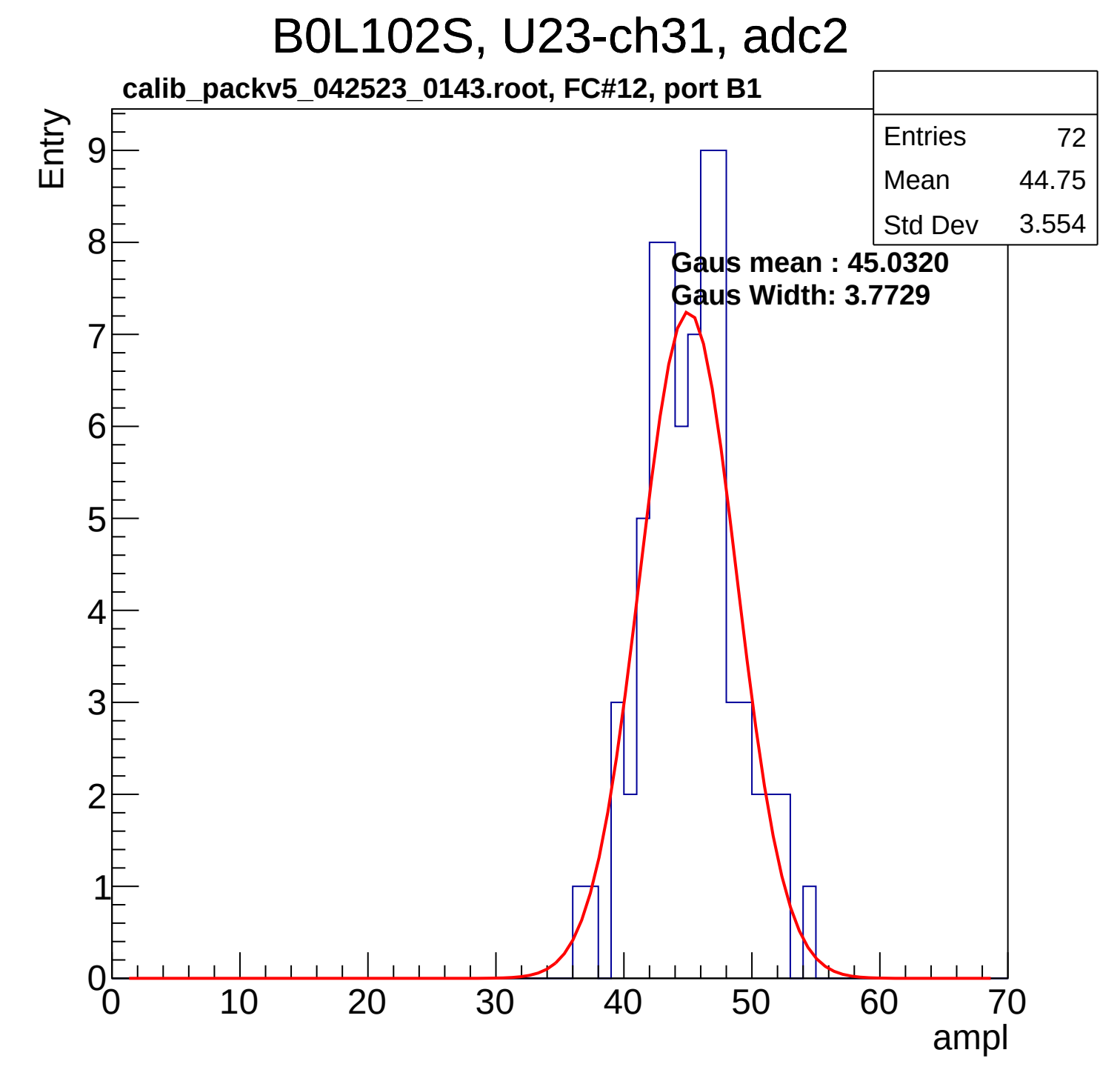
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch30, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 16
Mean 61.5
Std Dev 1.323
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch30, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch31, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 29.79
Std Dev 4.853
Gaus mean : 30.5095
Gaus Width: 3.9264
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch31, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 37.26
Std Dev 3.38
Gaus mean : 37.6523
Gaus Width: 3.4057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch31, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 44.75
Std Dev 3.554
Gaus mean : 45.0320
Gaus Width: 3.7729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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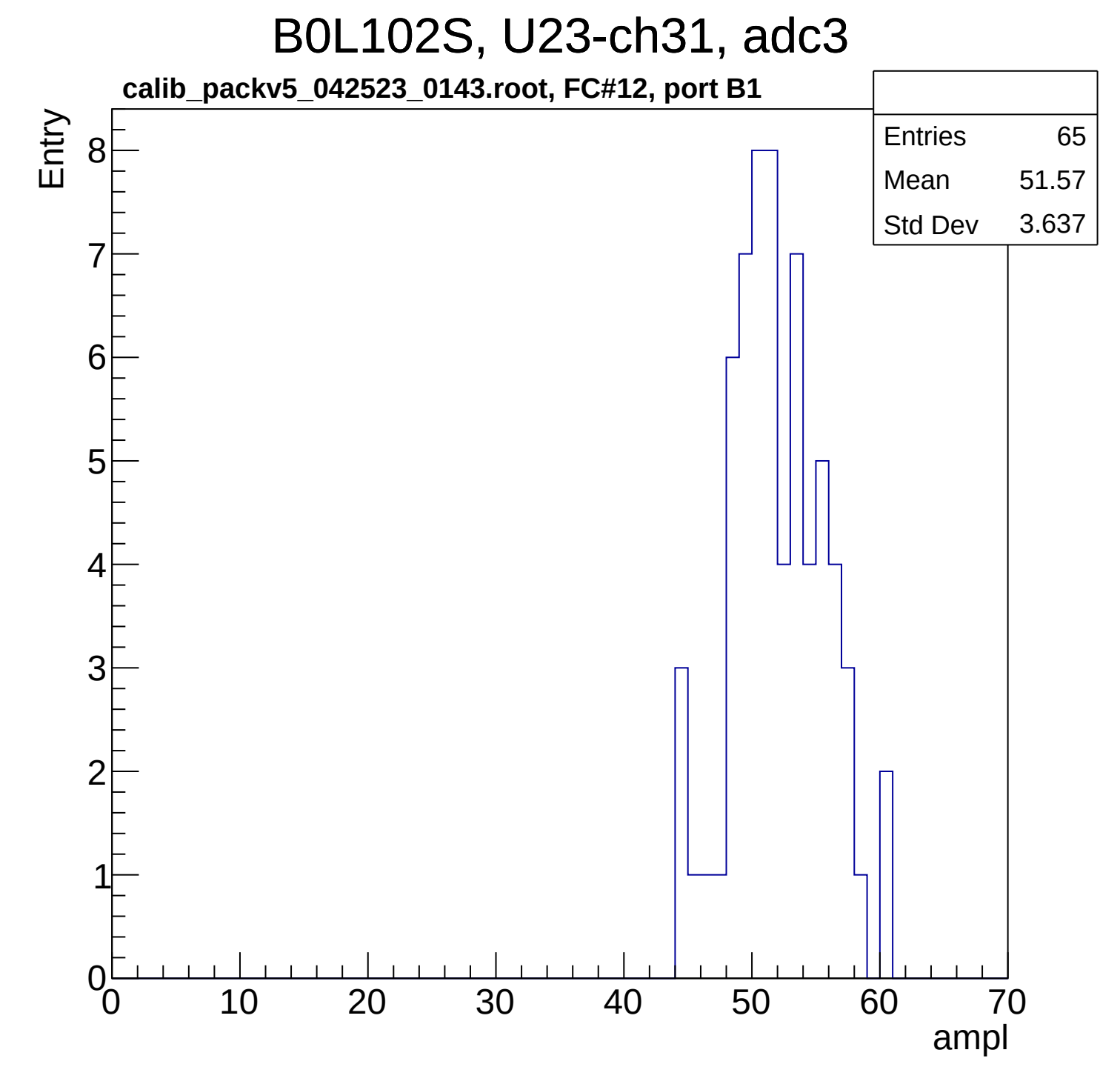
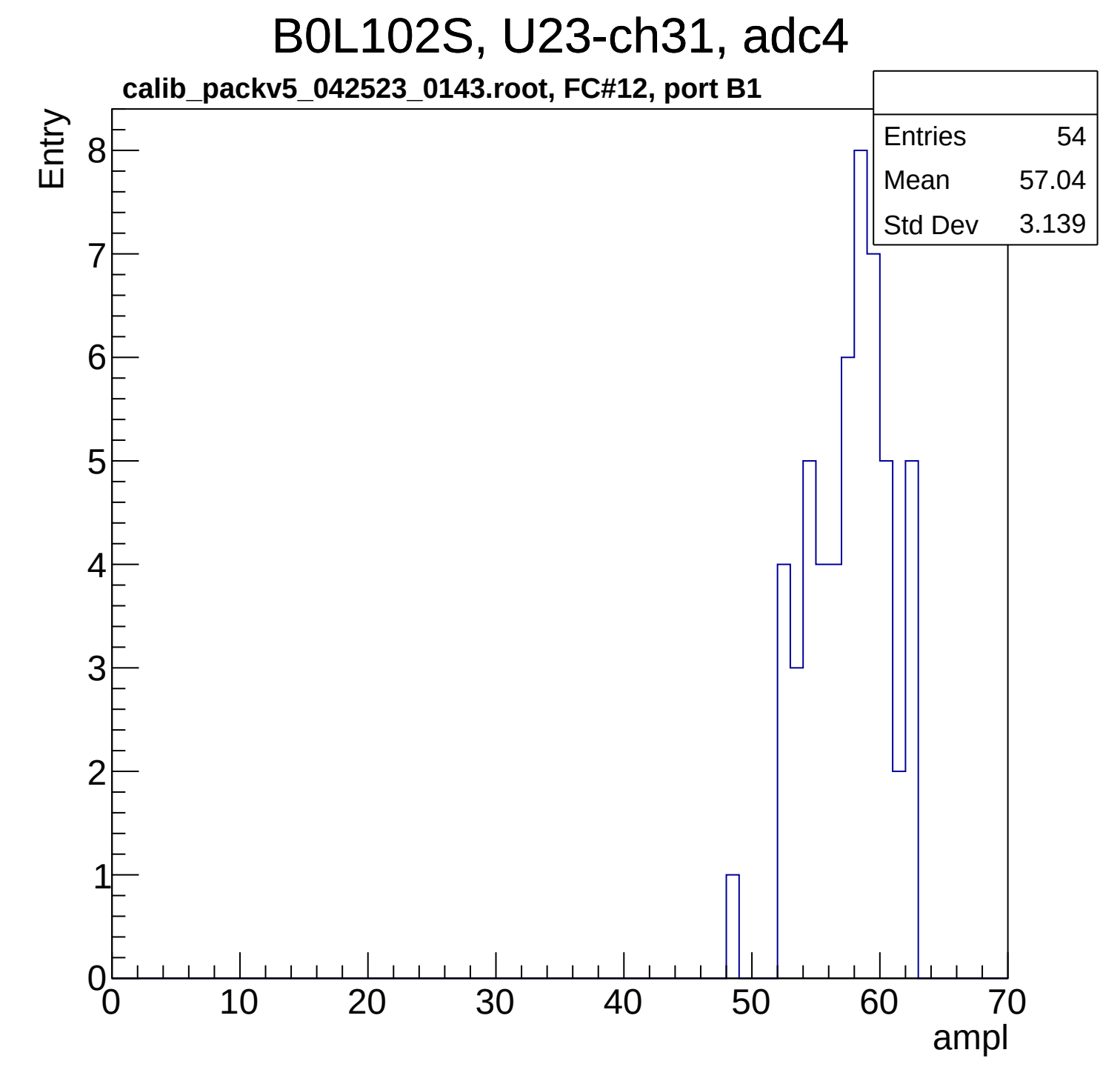
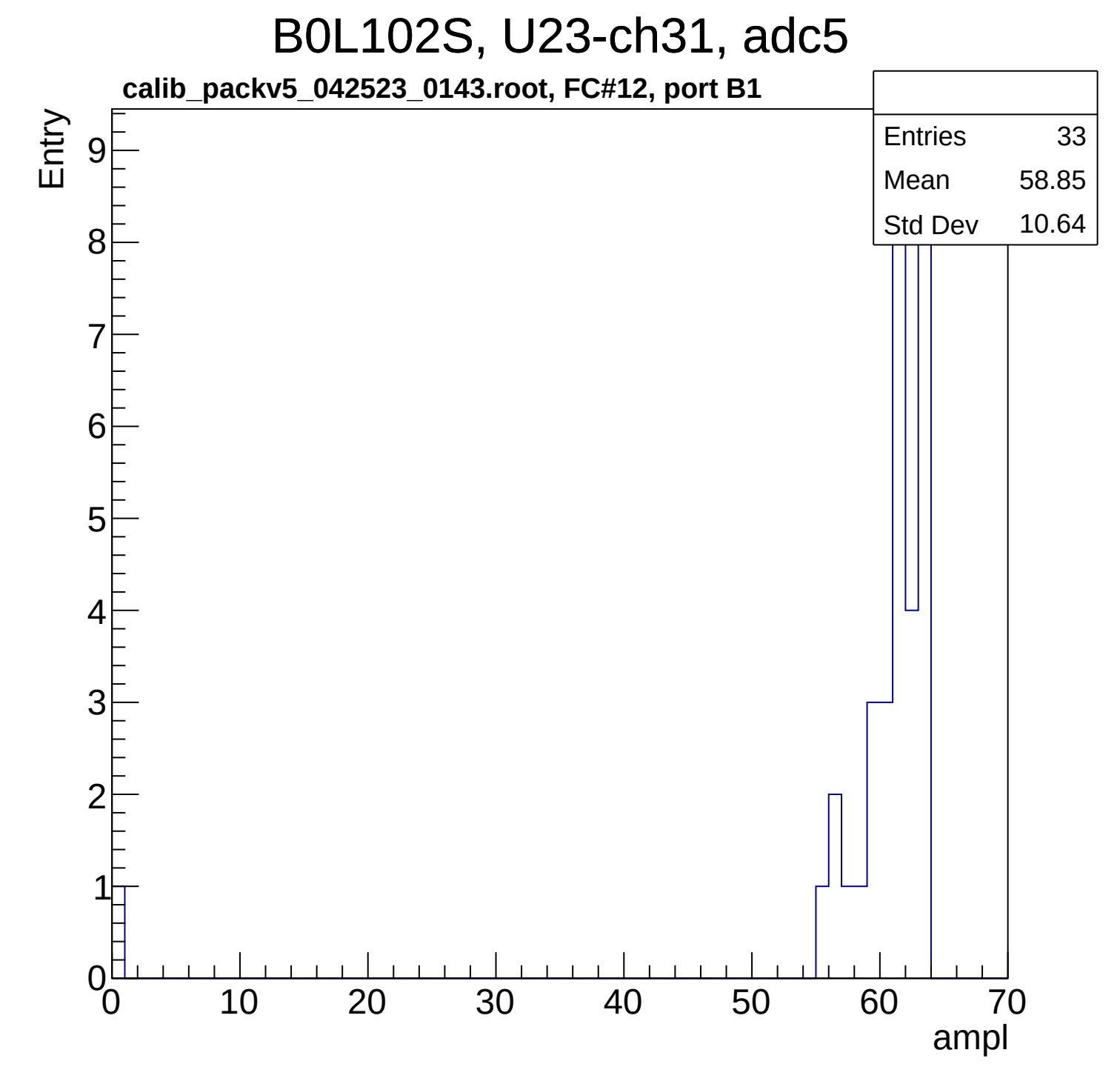
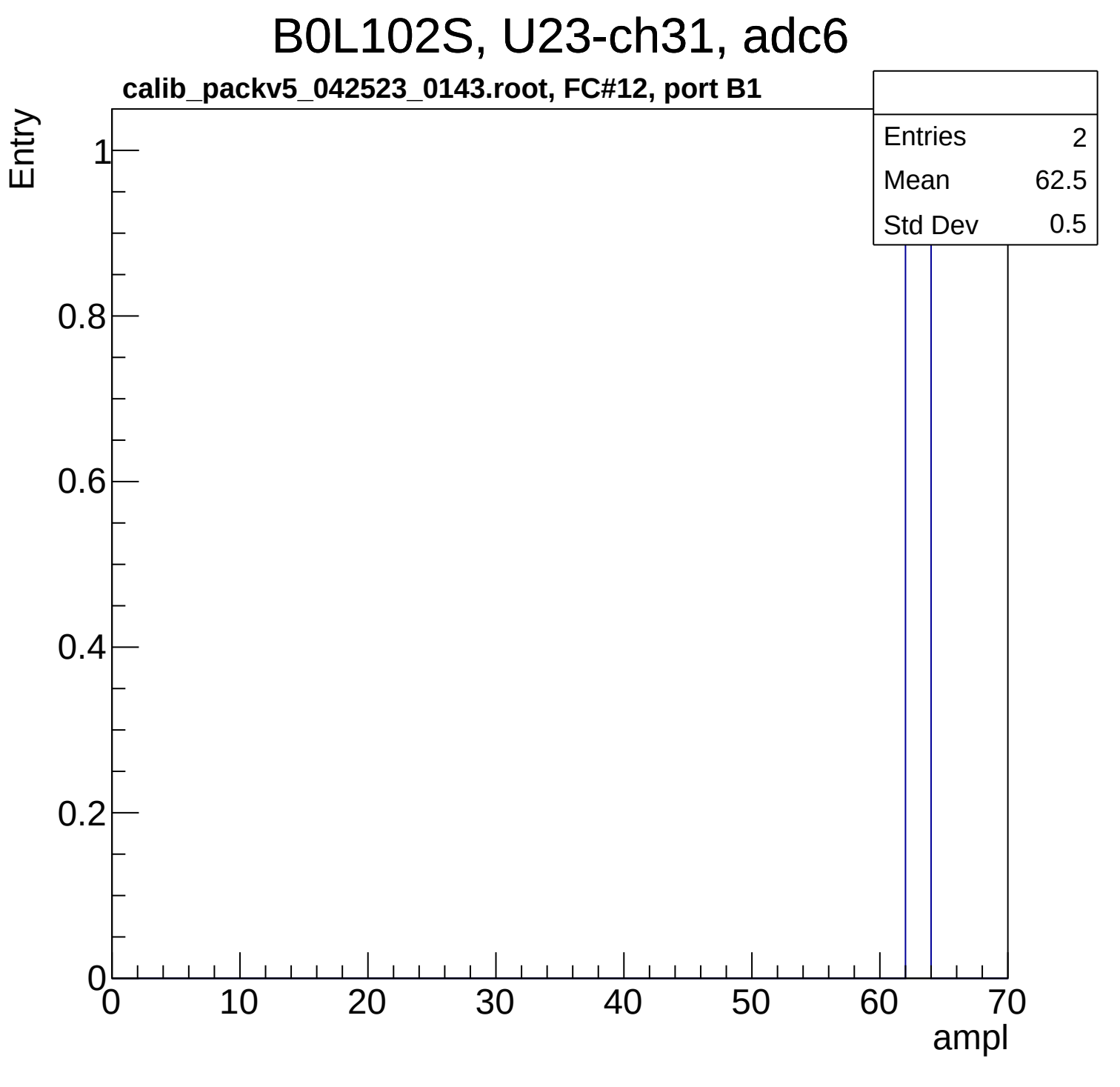
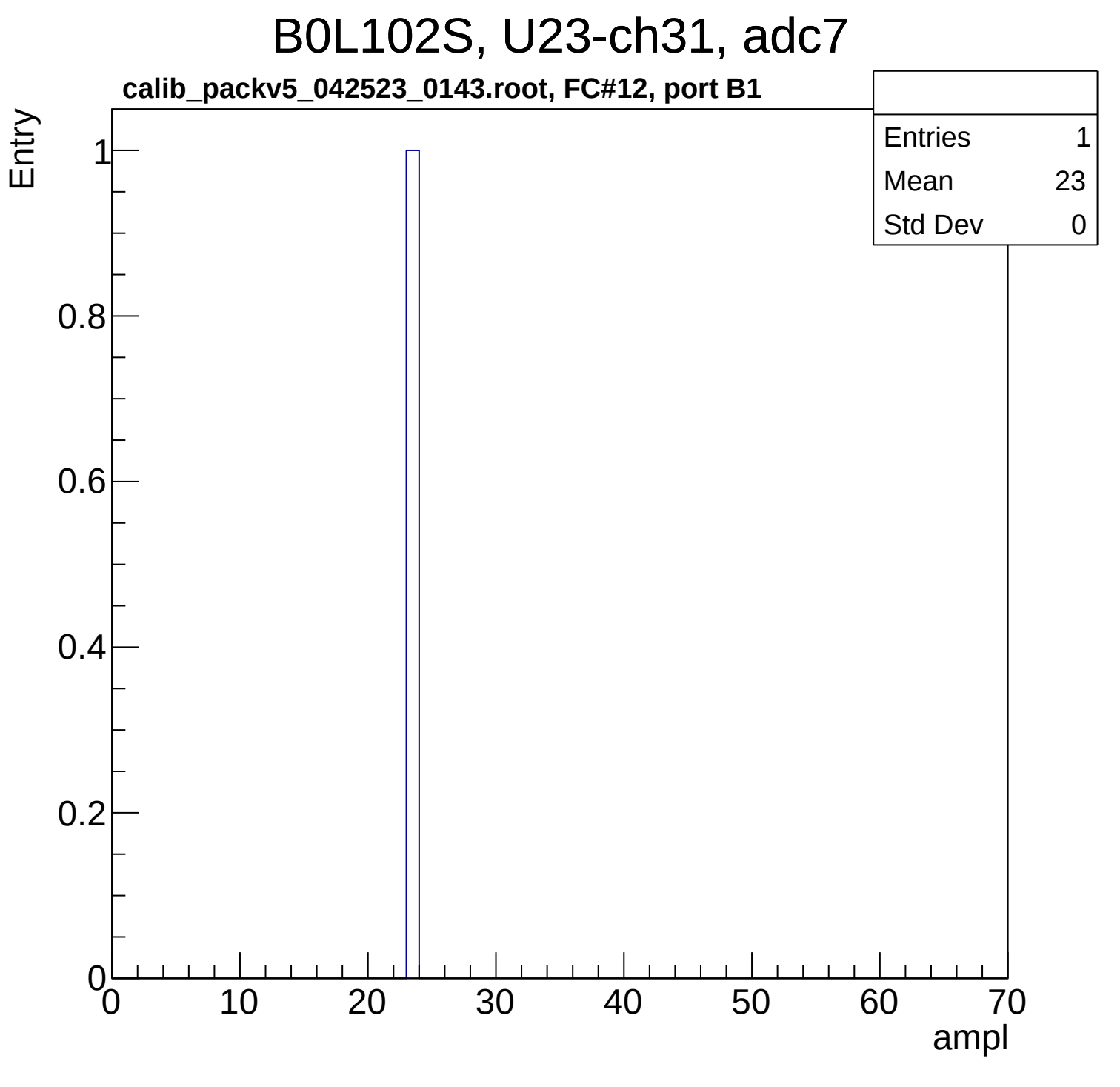
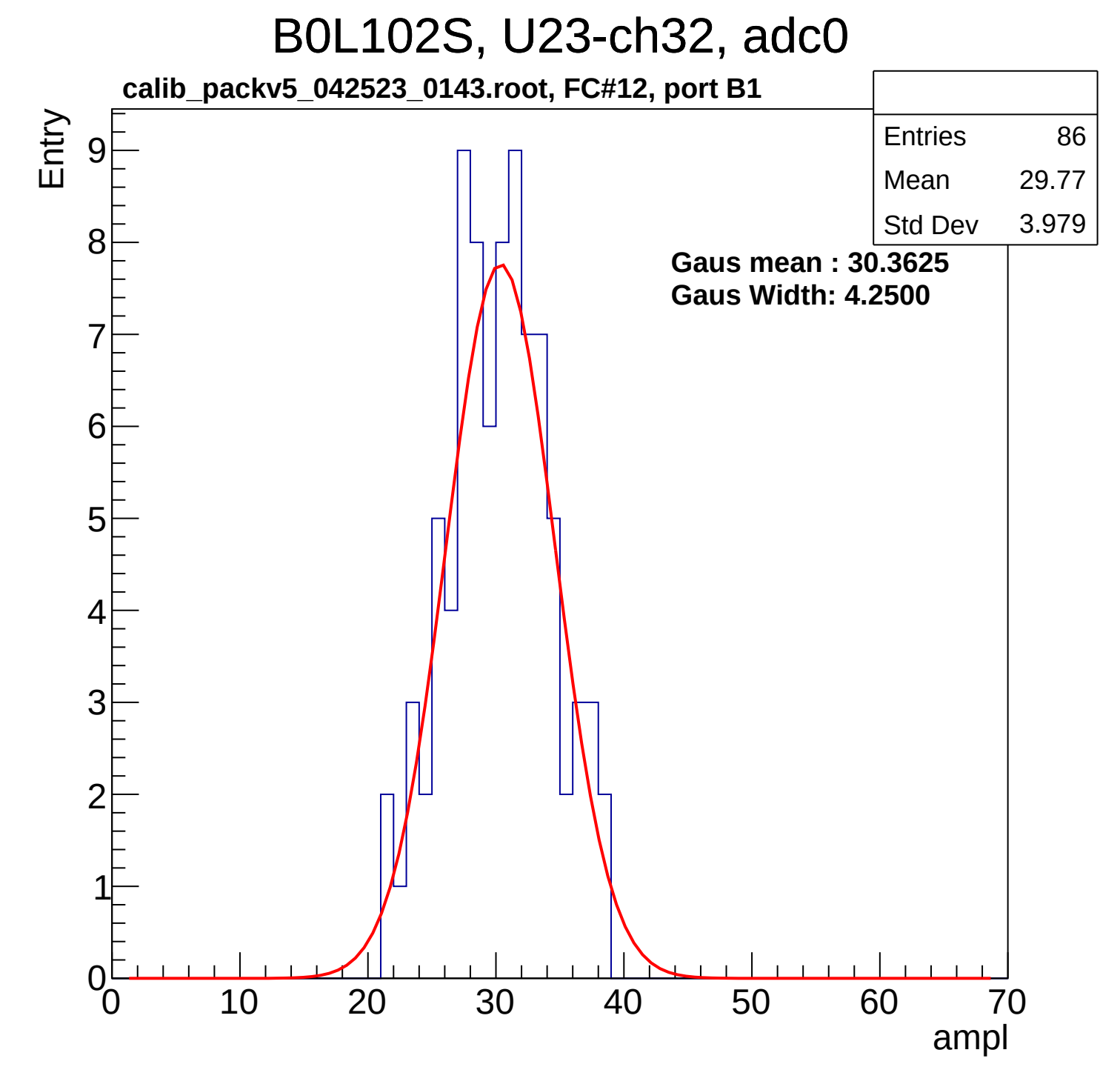

B0L102S, U23-ch31, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 51.57
Std Dev 3.637
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

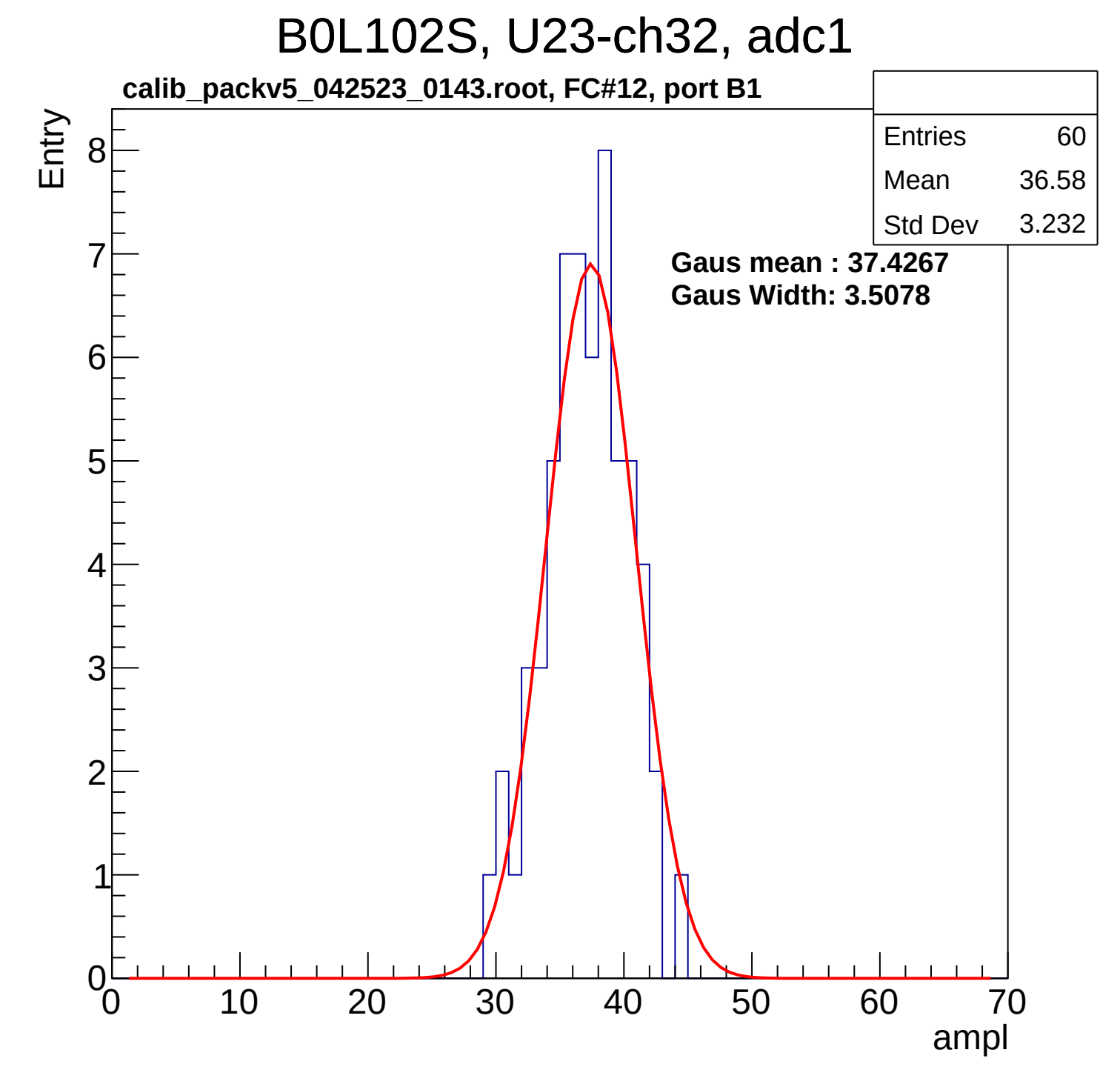

B0L102S, U23-ch31, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 57.04
Std Dev 3.139
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

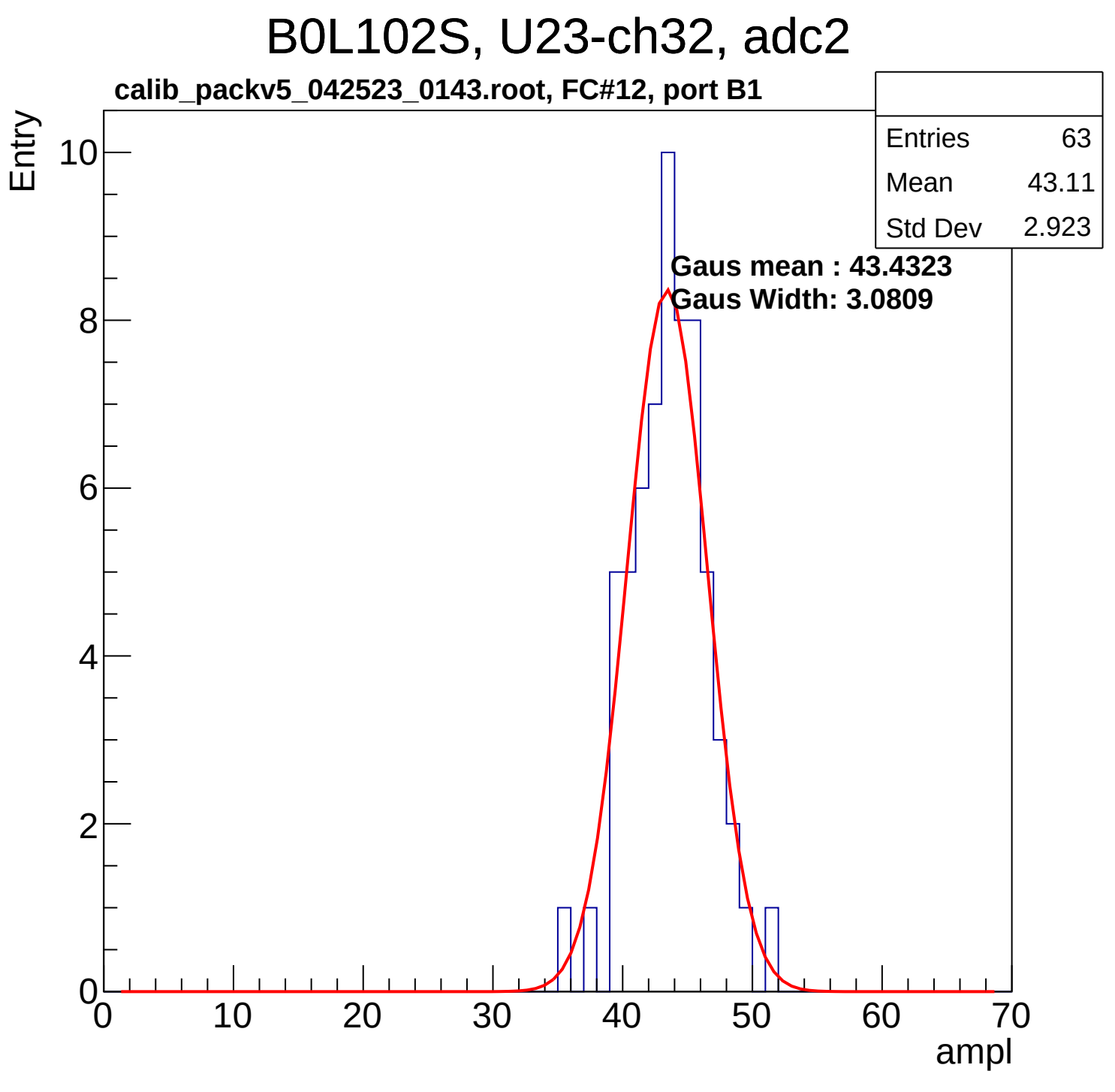

B0L102S, U23-ch31, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 58.85
Std Dev 10.64
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

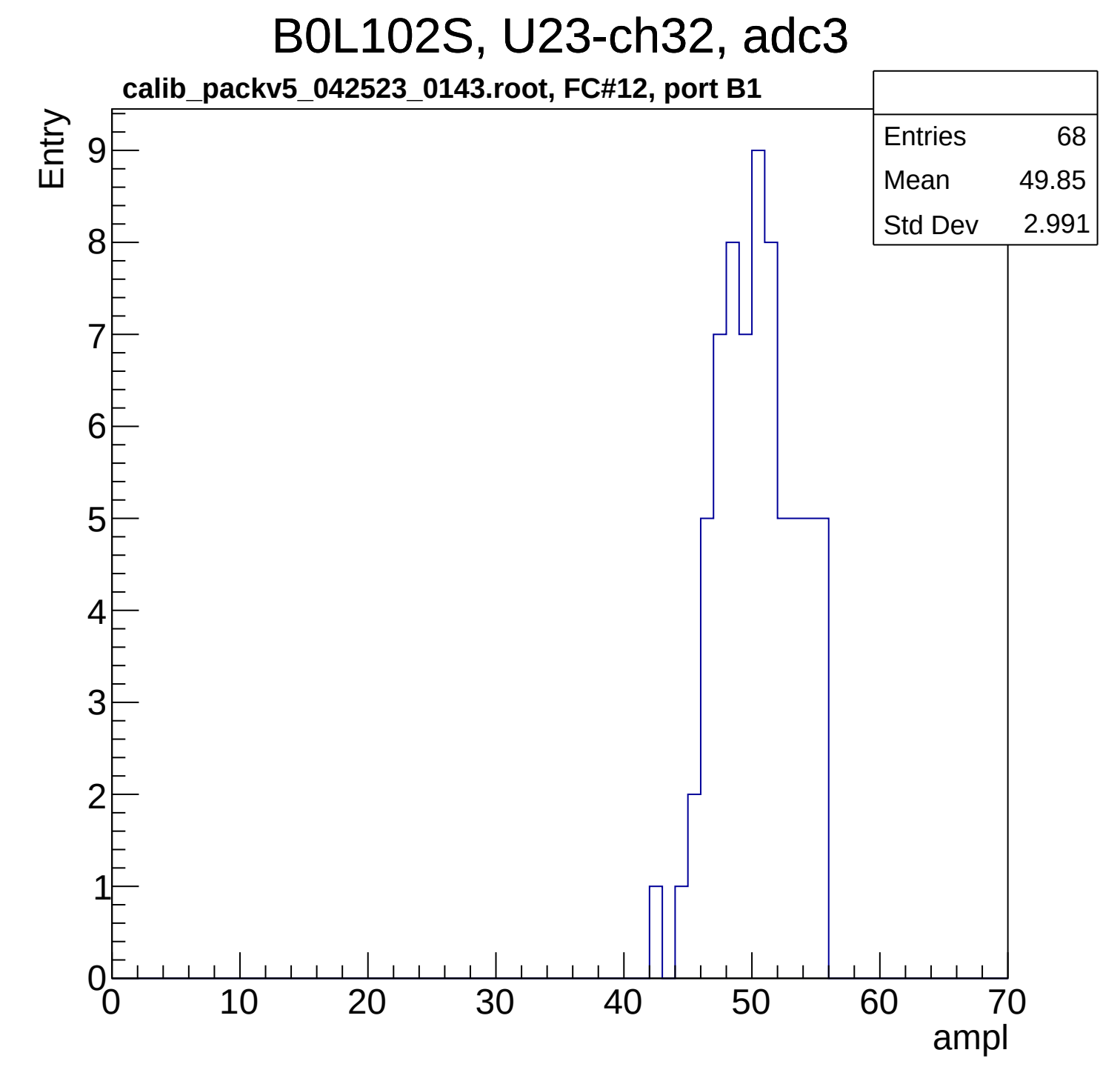

B0L102S, U23-ch31, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

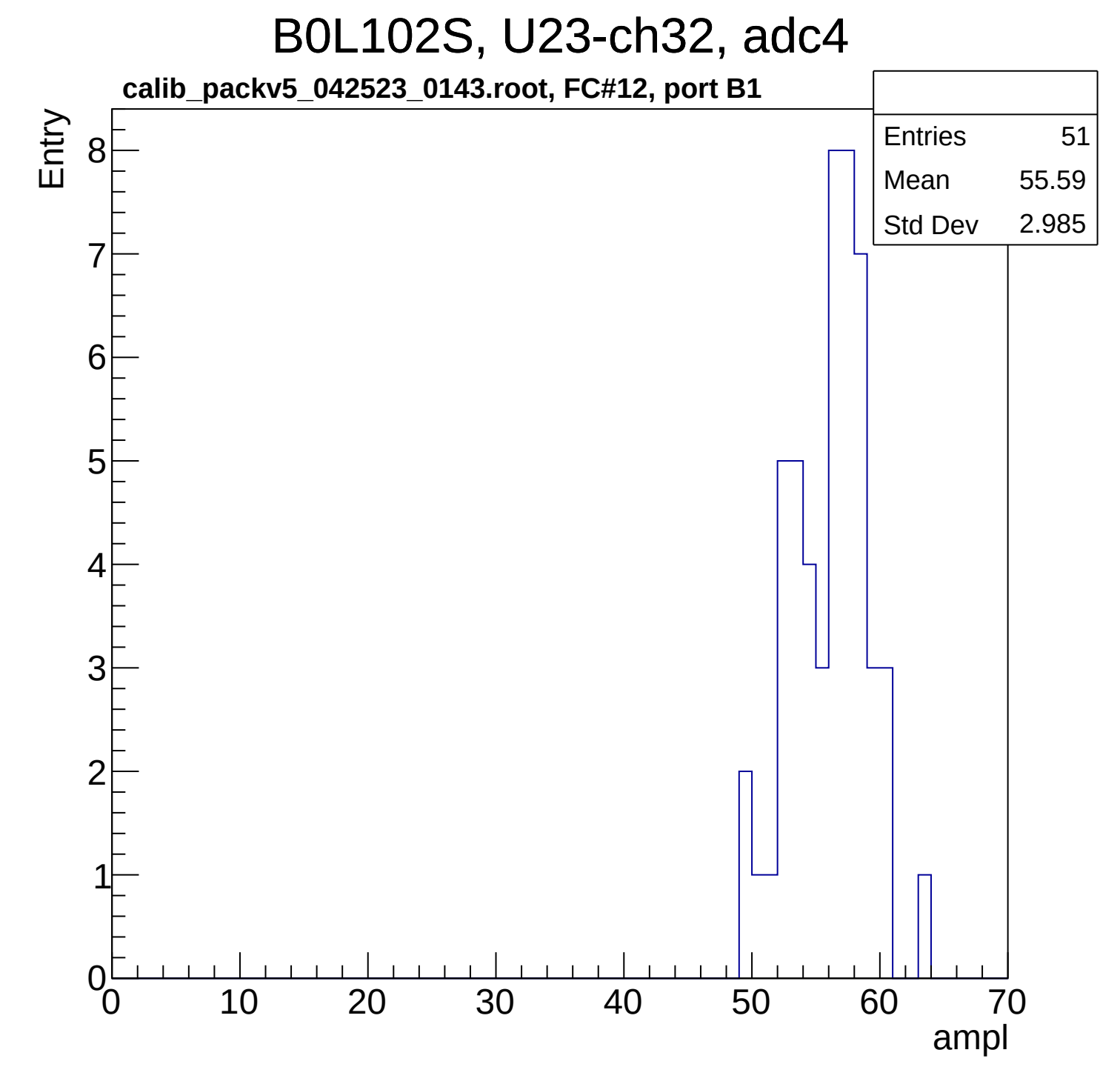

B0L102S, U23-ch31, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

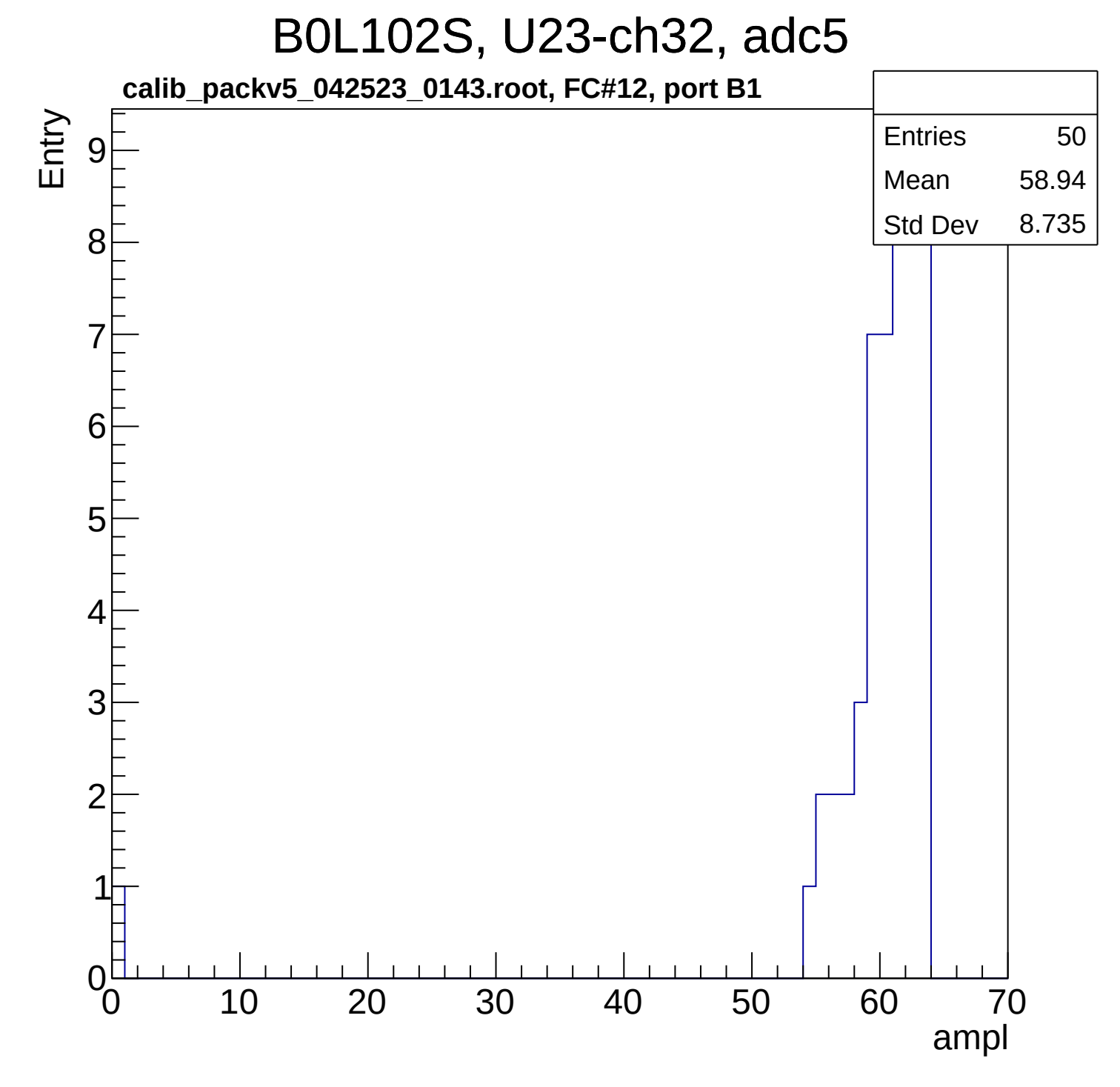

B0L102S, U23-ch32, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 29.77
Std Dev 3.979
Gaus mean : 30.3625
Gaus Width: 4.2500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

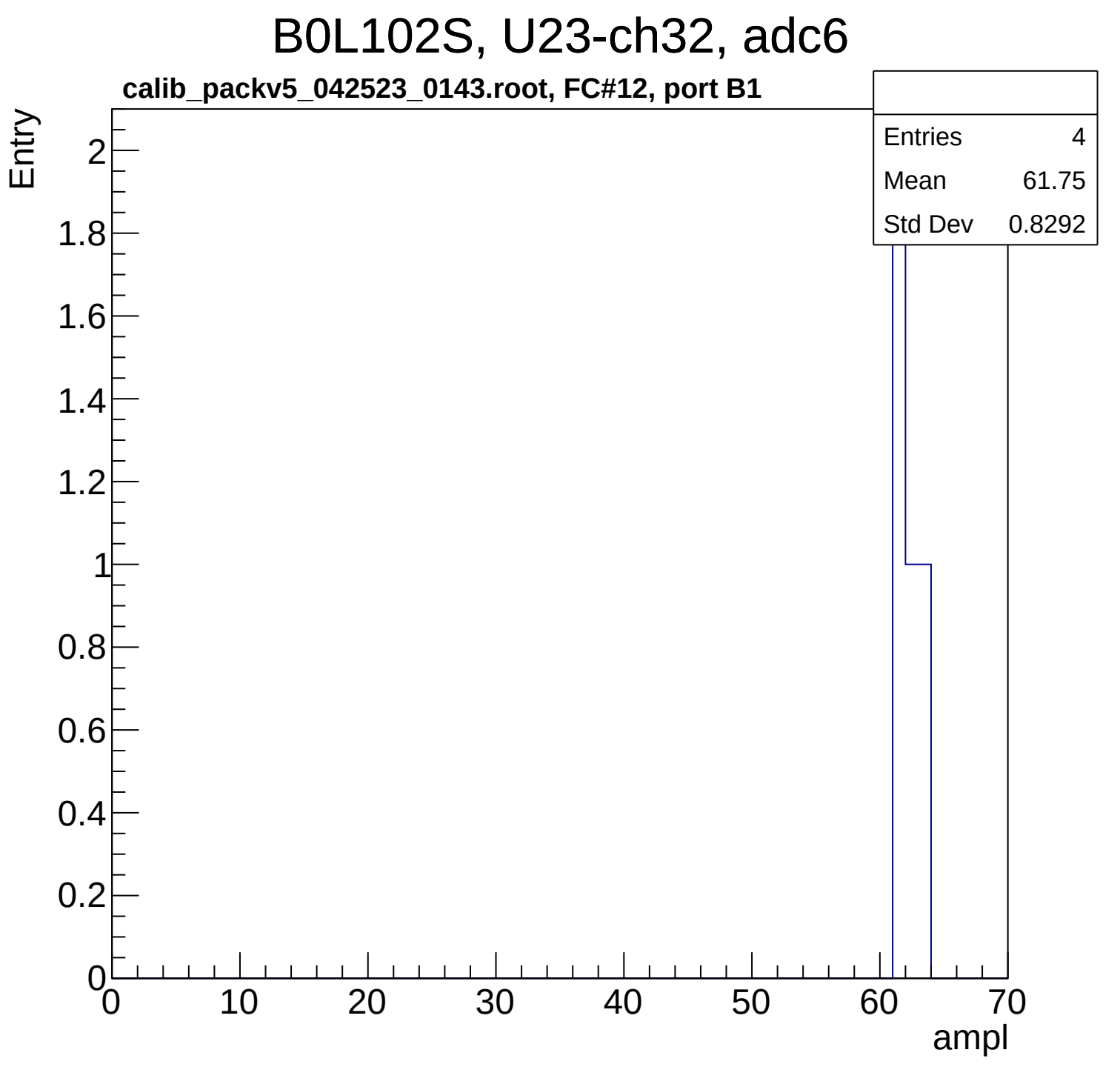

B0L102S, U23-ch32, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 36.58
Std Dev 3.232
Gaus mean : 37.4267
Gaus Width: 3.5078
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

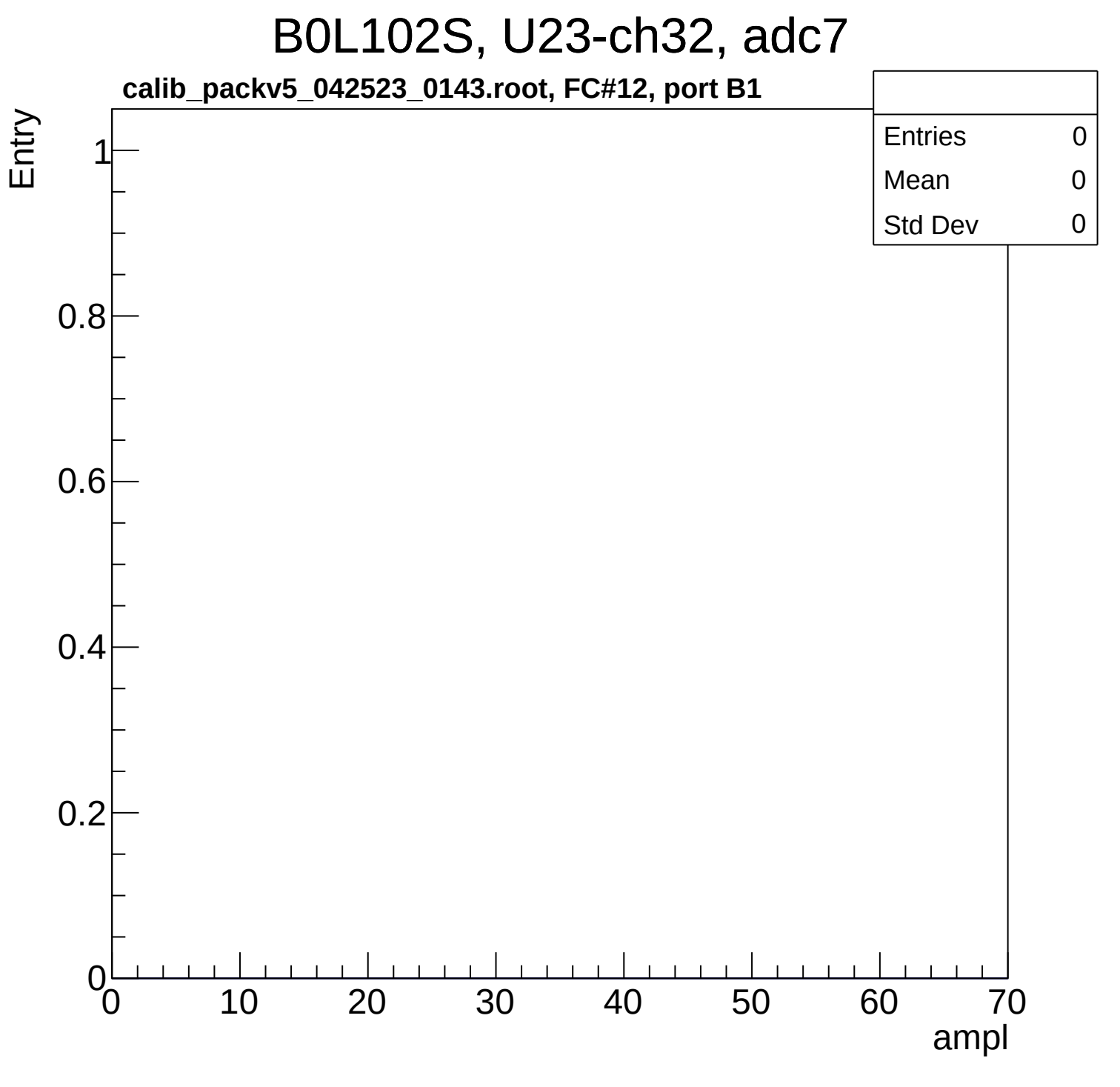

B0L102S, U23-ch32, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 43.11
Std Dev 2.923
Gaus mean : 43.4323
Gaus Width: 3.0809
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

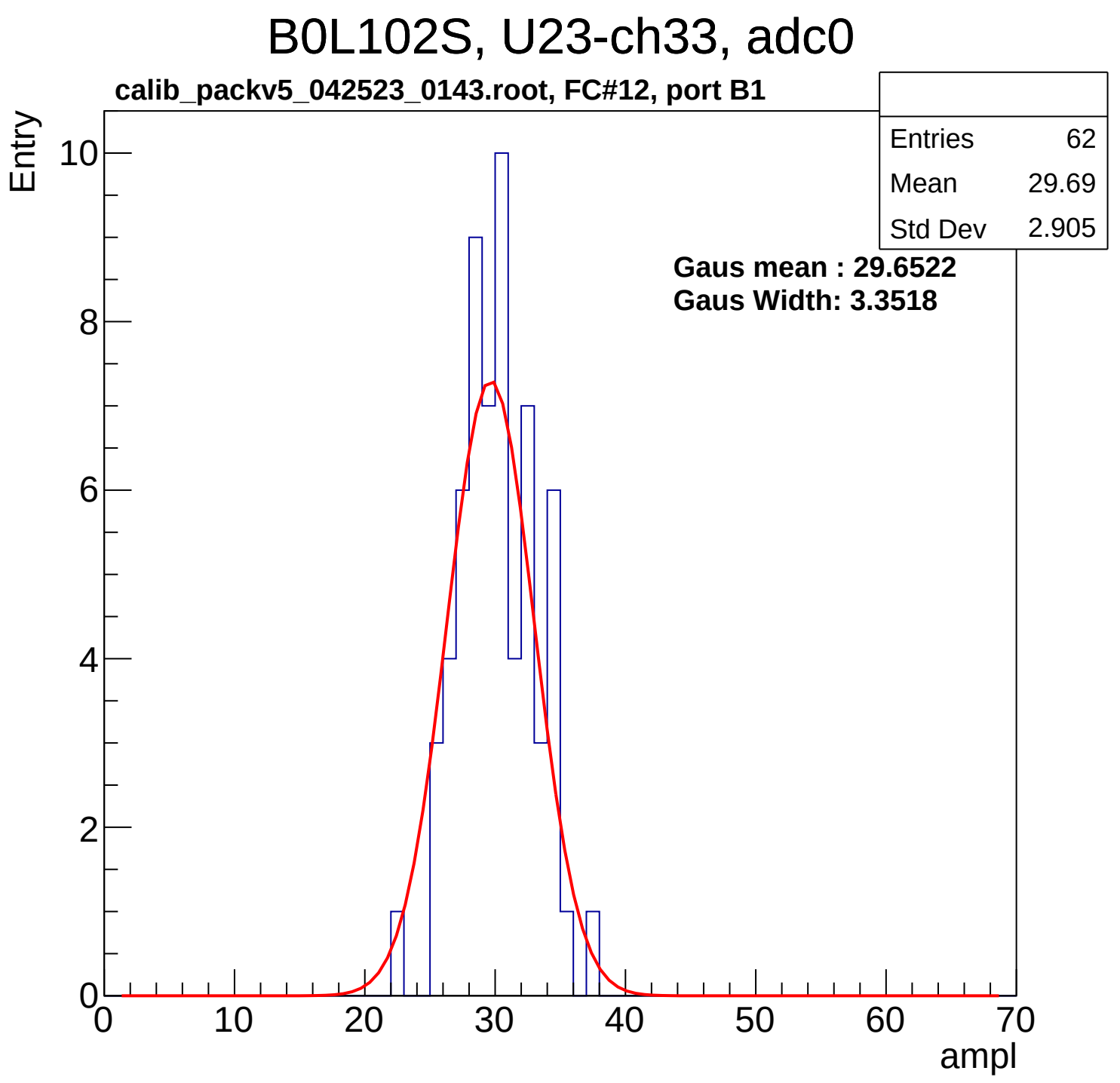

B0L102S, U23-ch32, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.85
Std Dev 2.991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

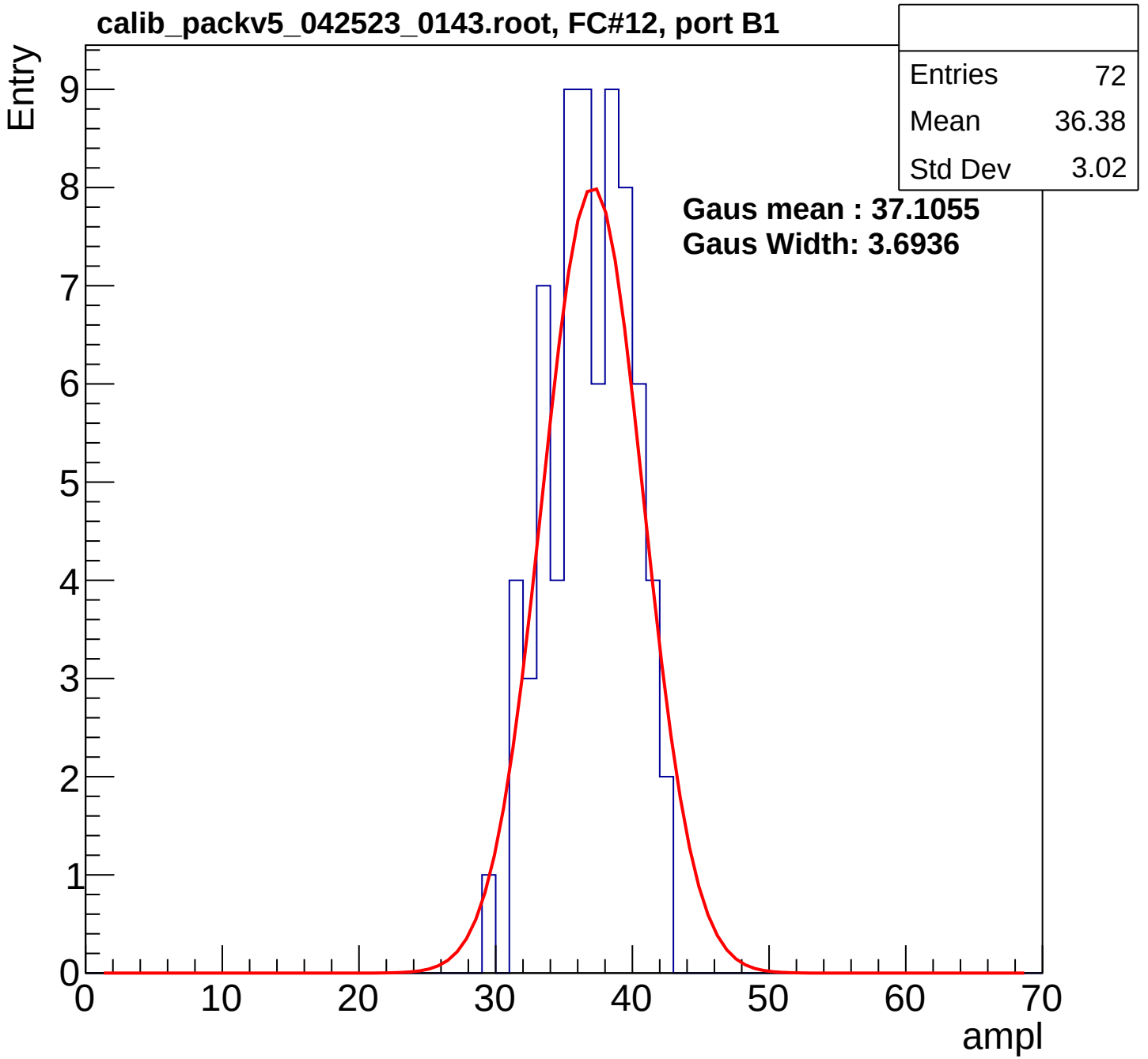

B0L102S, U23-ch32, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 55.59
Std Dev 2.985
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch32, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.94
Std Dev 8.735
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch32, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch32, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch33, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 29.69
Std Dev 2.905
Gaus mean : 29.6522
Gaus Width: 3.3518
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch33, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 36.38
Std Dev 3.02
Gaus mean : 37.1055
Gaus Width: 3.6936
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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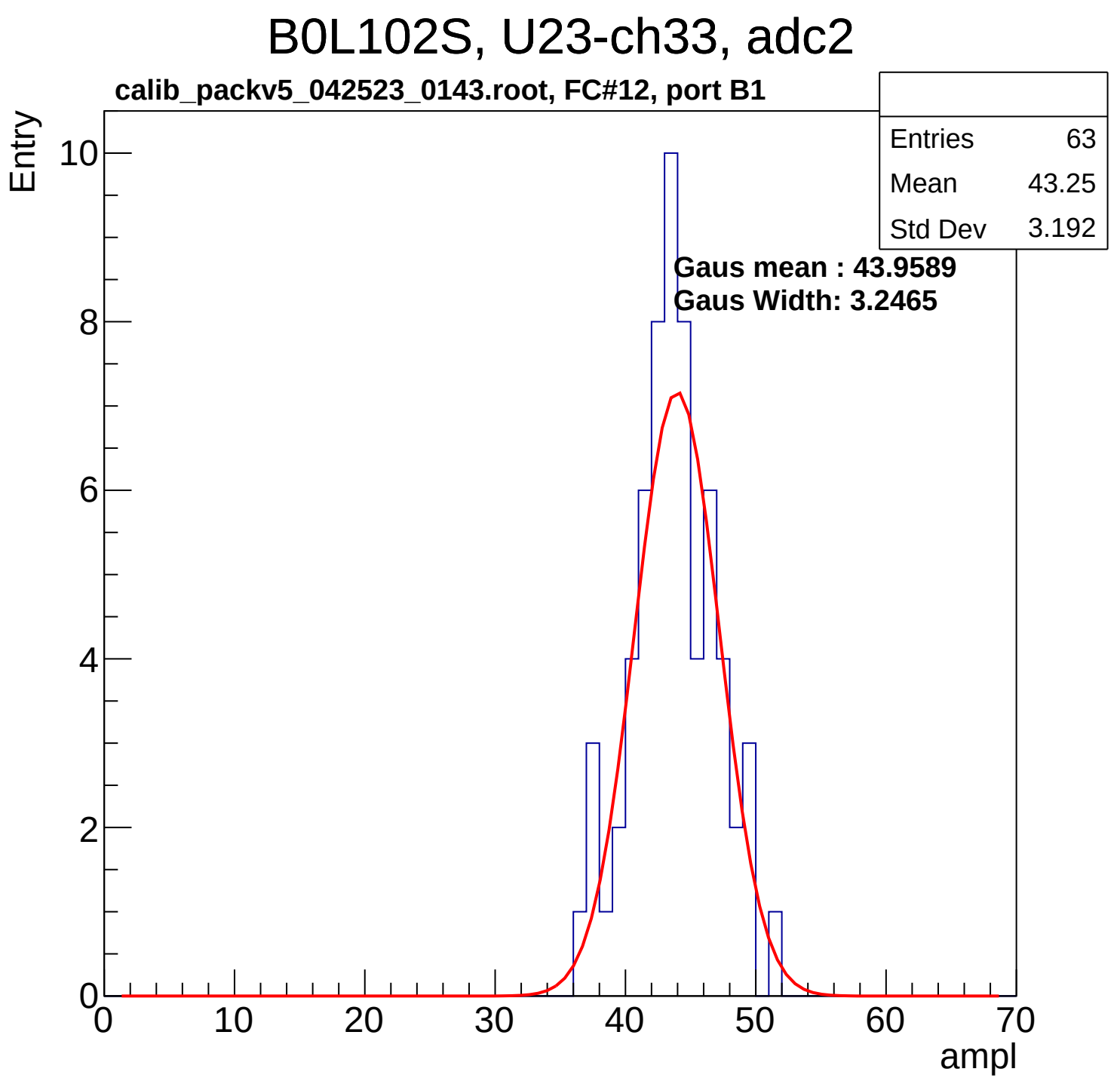
20
30
40
50
60
70


B0L102S, U23-ch33, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 43.25
Std Dev 3.192
Gaus mean : 43.9589
Gaus Width: 3.2465
Entry
ampl
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2
4
6
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10
0
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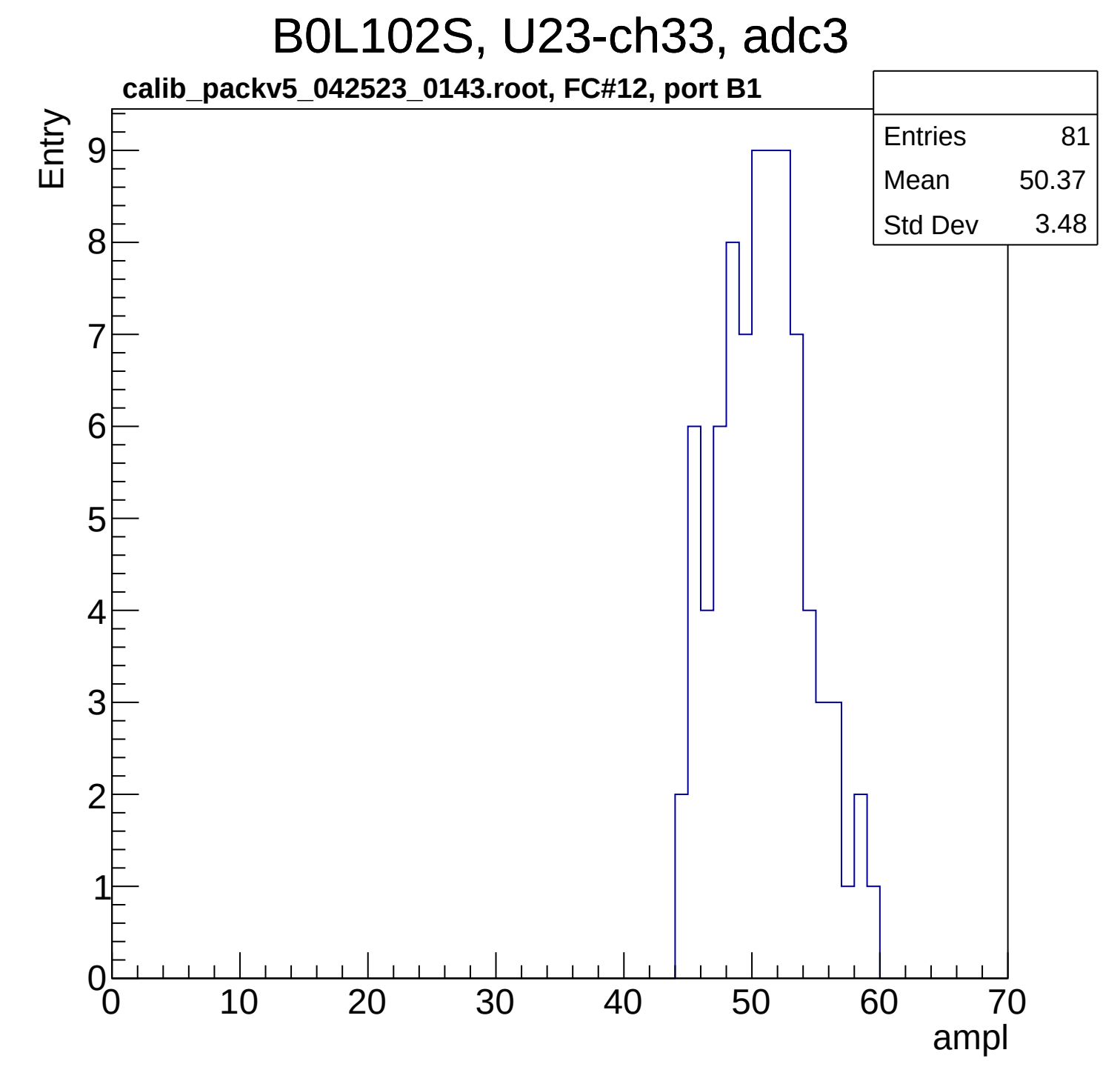
40
50
60
70


B0L102S, U23-ch33, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 50.37
Std Dev 3.48
Entry
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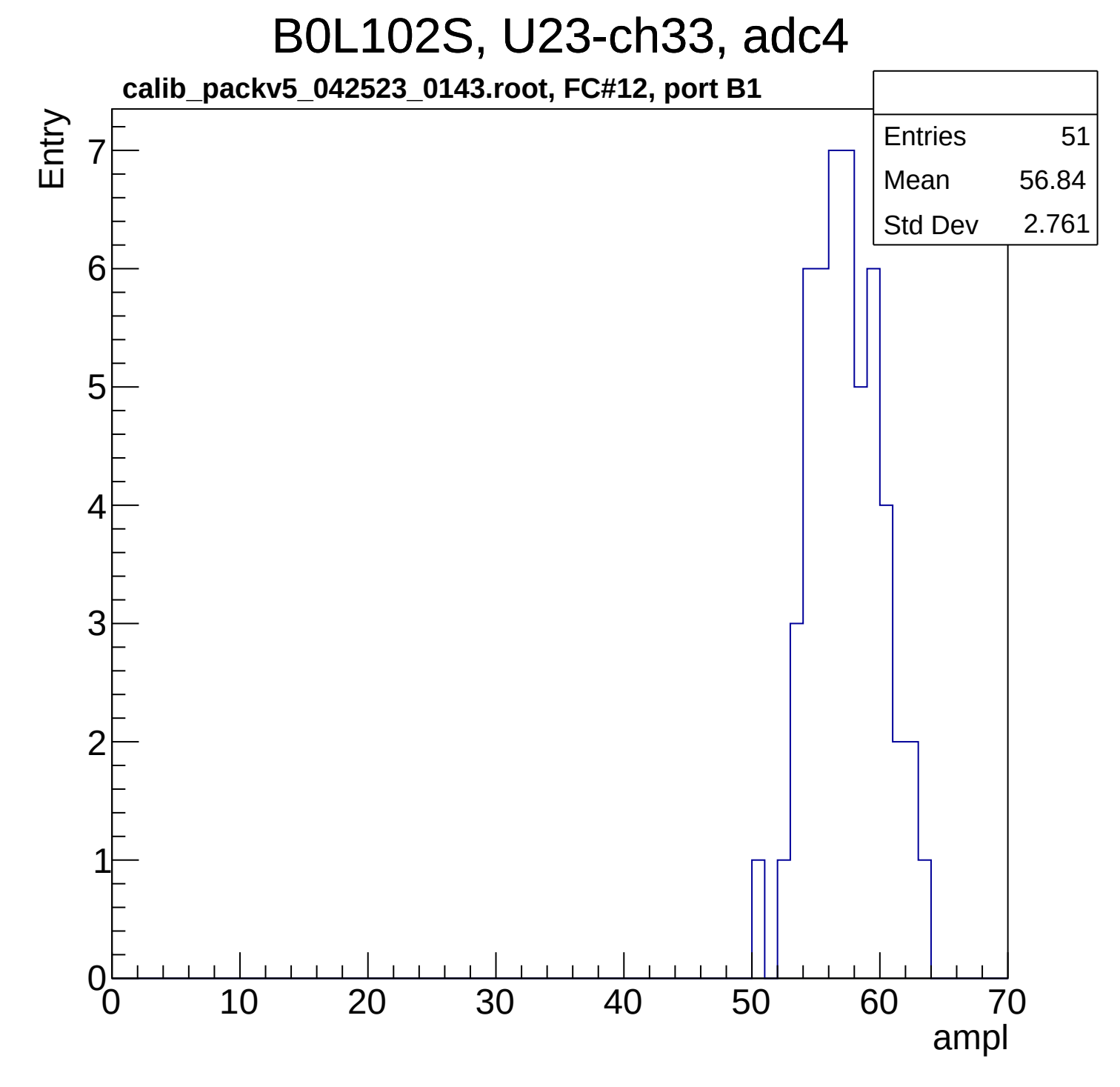
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch33, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 56.84
Std Dev 2.761
Entry
ampl
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1
2
3
4
5
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7
0
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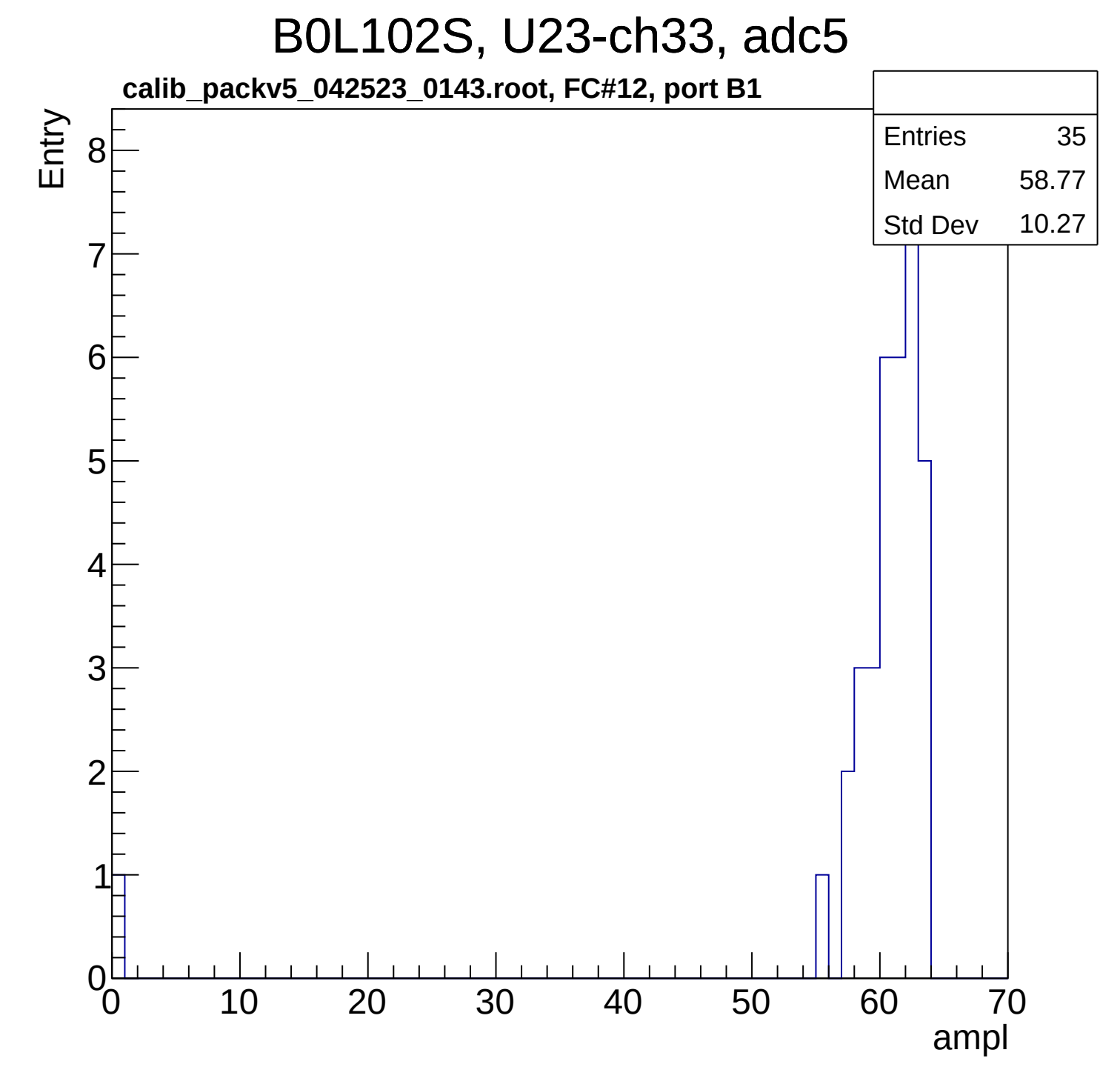
40
50
60
70


B0L102S, U23-ch33, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 58.77
Std Dev 10.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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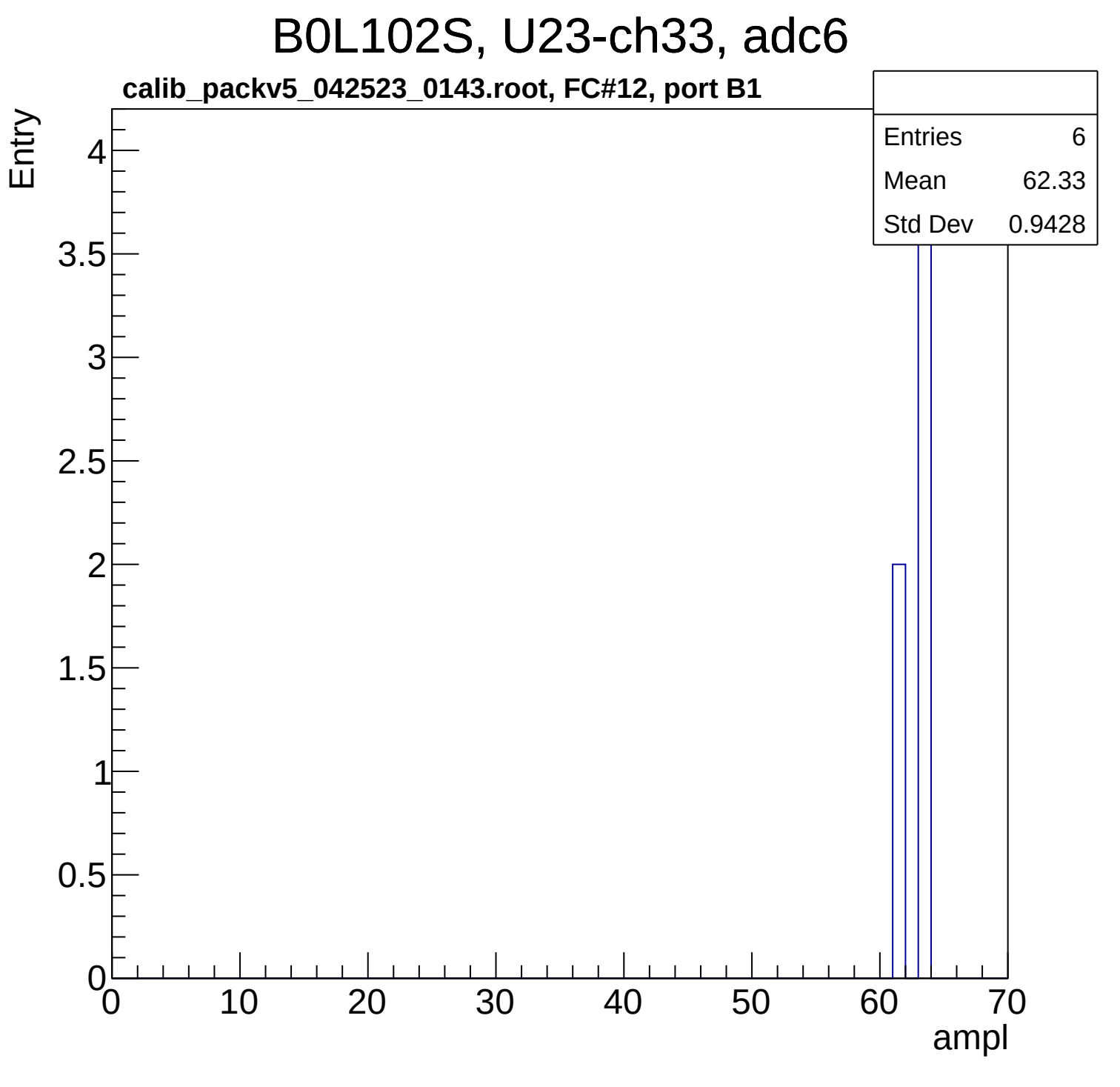
B0L102S, U23-ch33, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.33
Std Dev 0.9428
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
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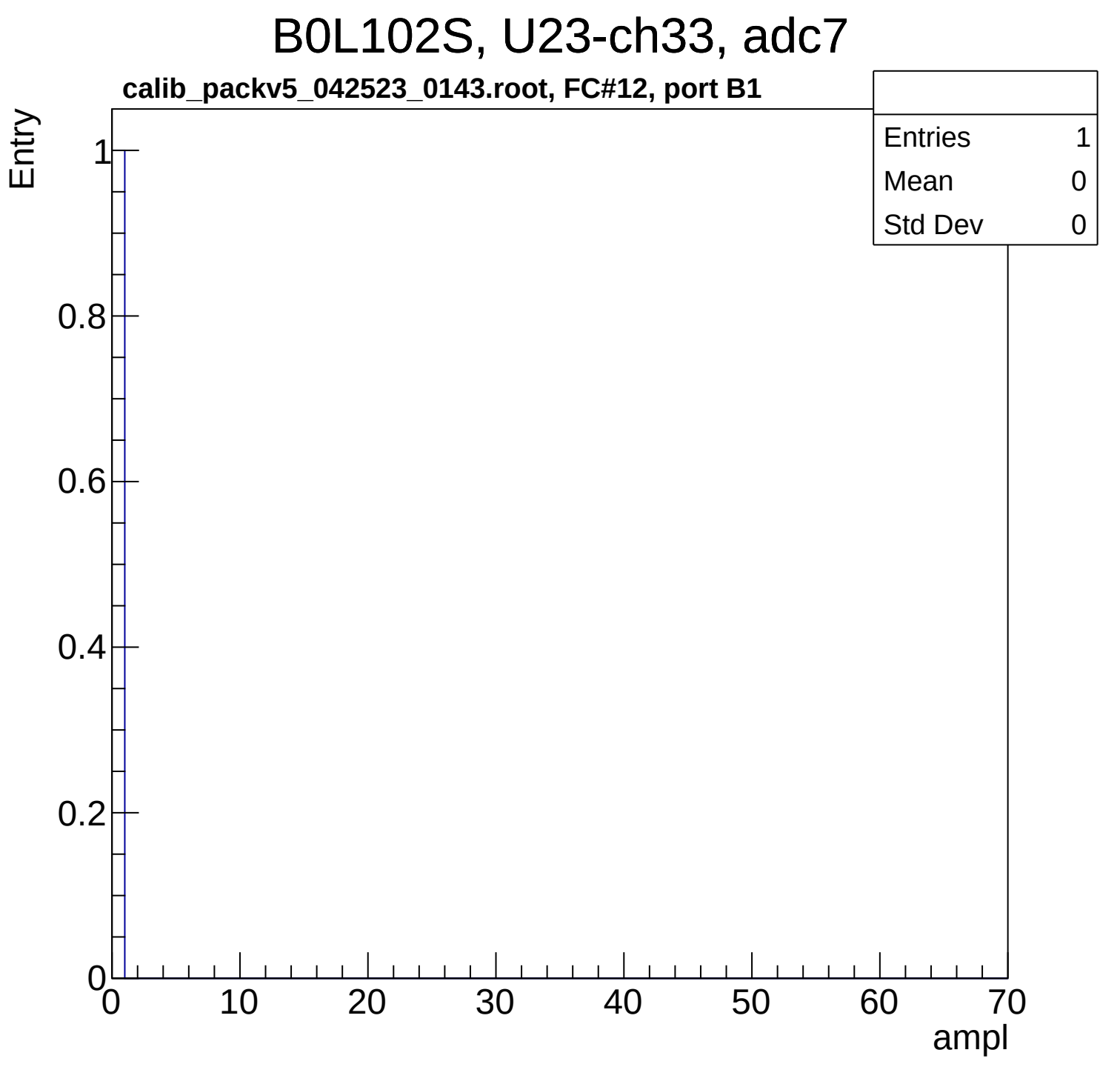
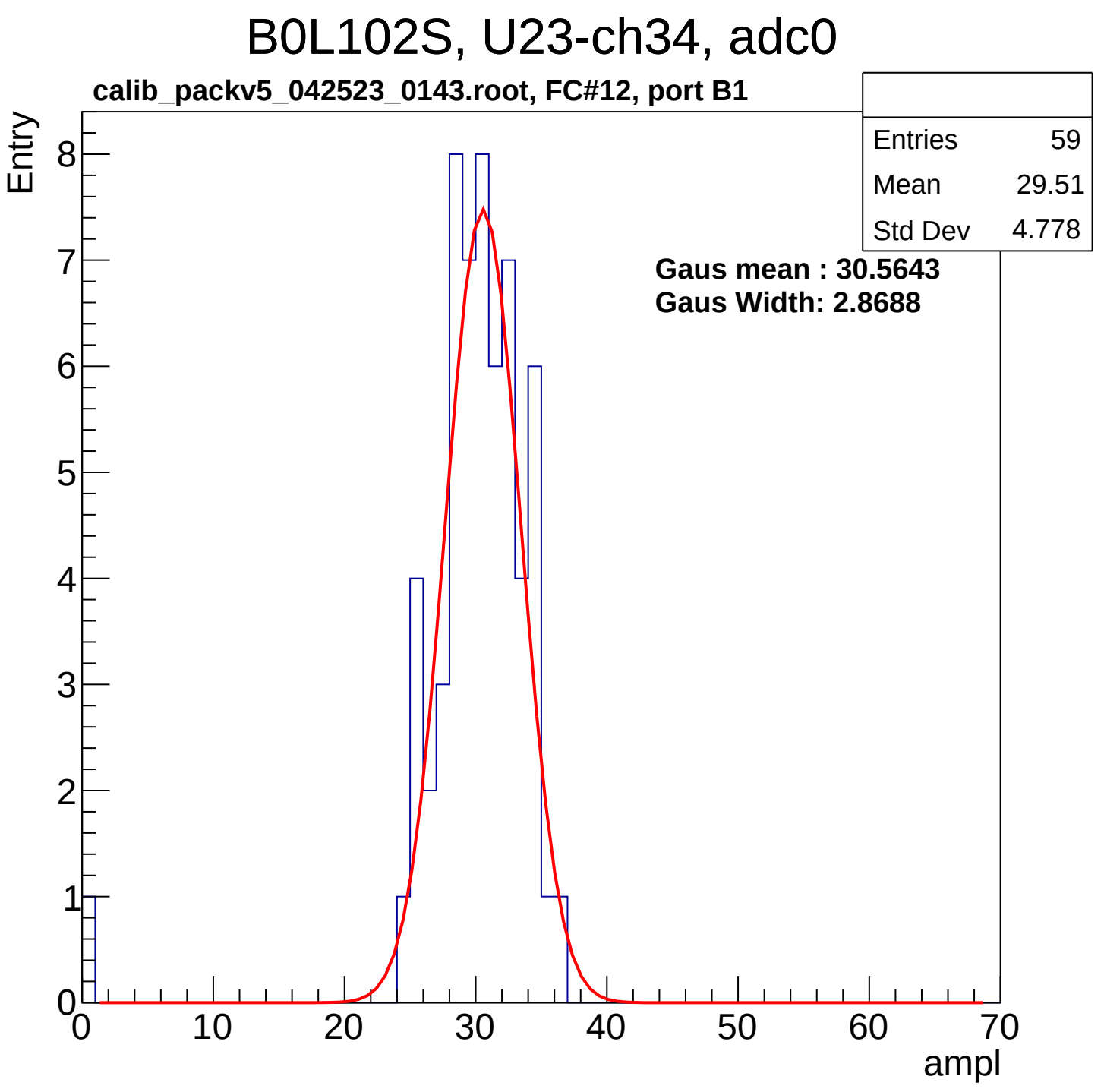
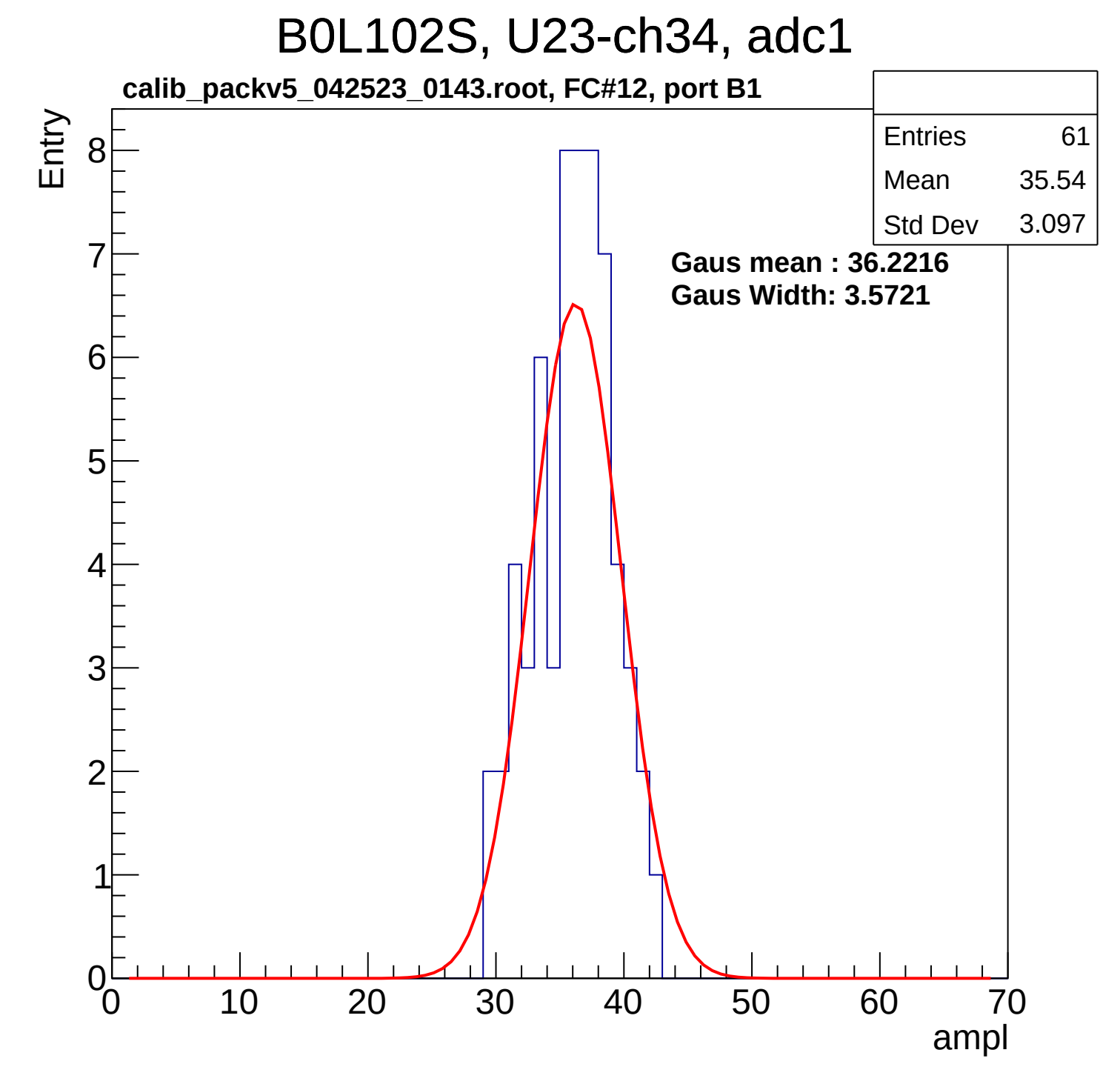
30
40
50
60
70


B0L102S, U23-ch33, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch34, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 29.51
Std Dev 4.778
Gaus mean : 30.5643
Gaus Width: 2.8688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch34, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 35.54
Std Dev 3.097
Gaus mean : 36.2216
Gaus Width: 3.5721
Entry
ampl
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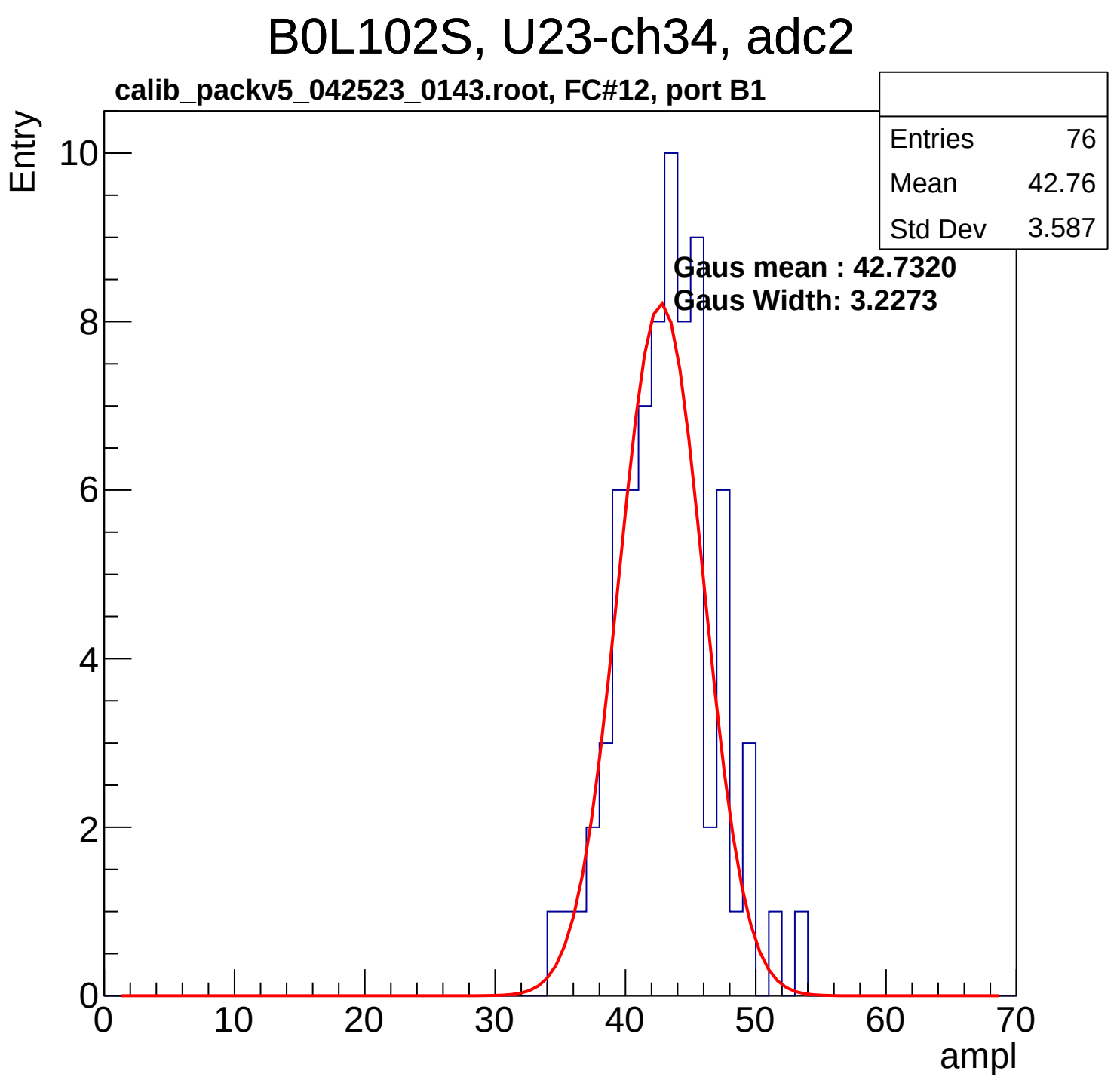
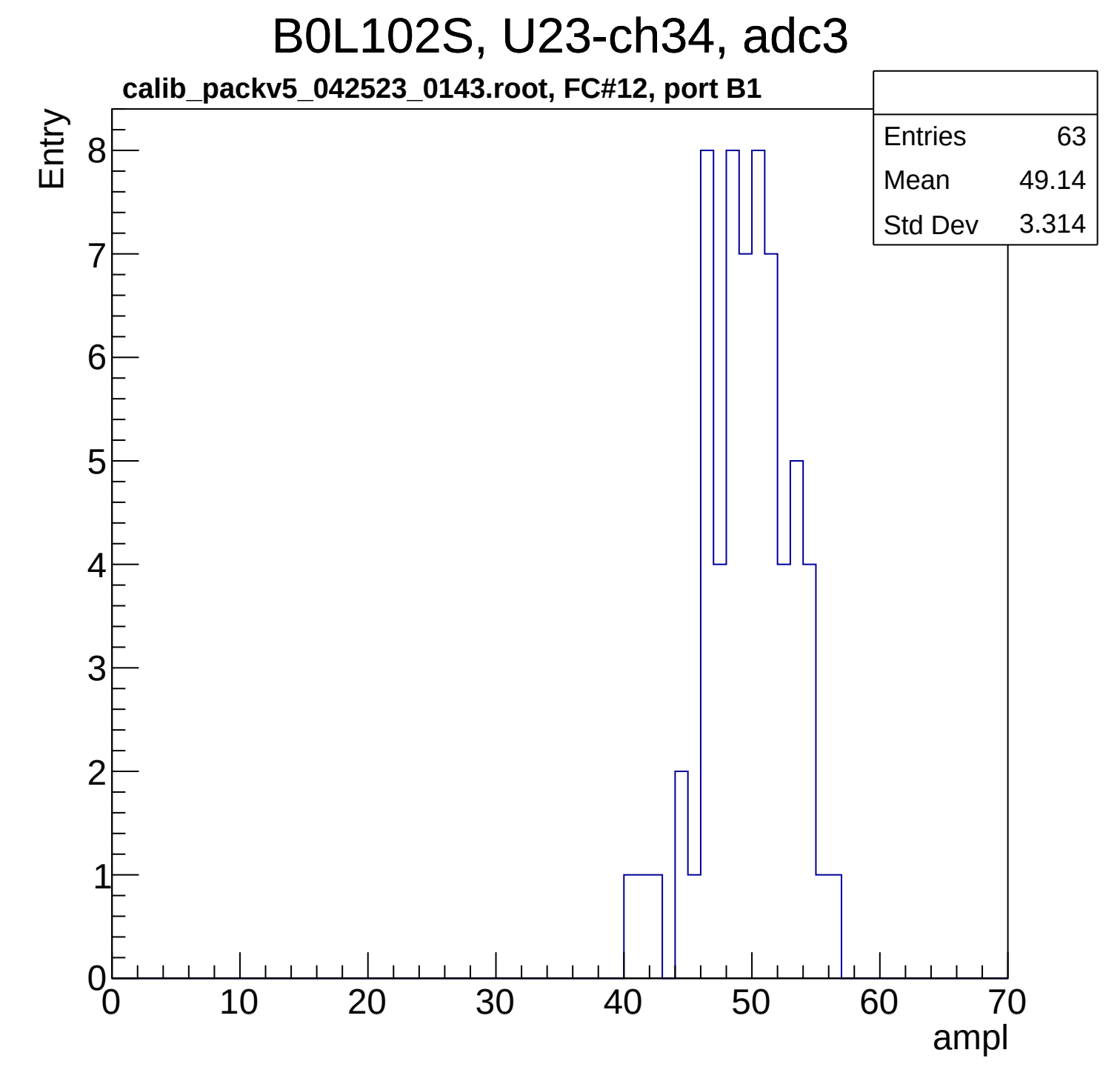
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch34, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 42.76
Std Dev 3.587
Gaus mean : 42.7320
Gaus Width: 3.2273
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch34, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 49.14
Std Dev 3.314
Entry
ampl
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1
2
3
4
5
6
7
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0
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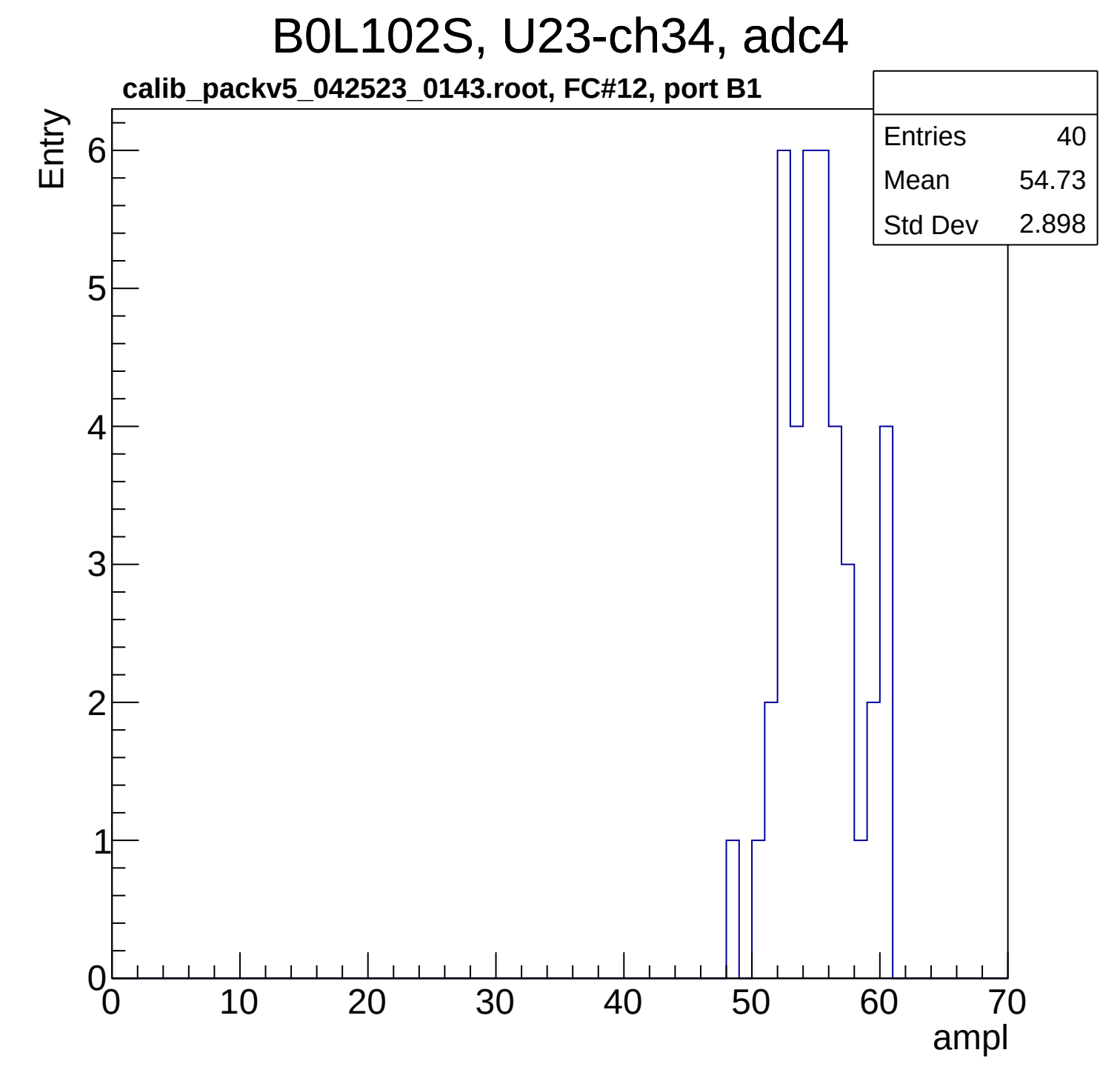
B0L102S, U23-ch34, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 40
Mean 54.73
Std Dev 2.898
Entry
ampl
0
1
2
3
4
5
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0
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40
50
60
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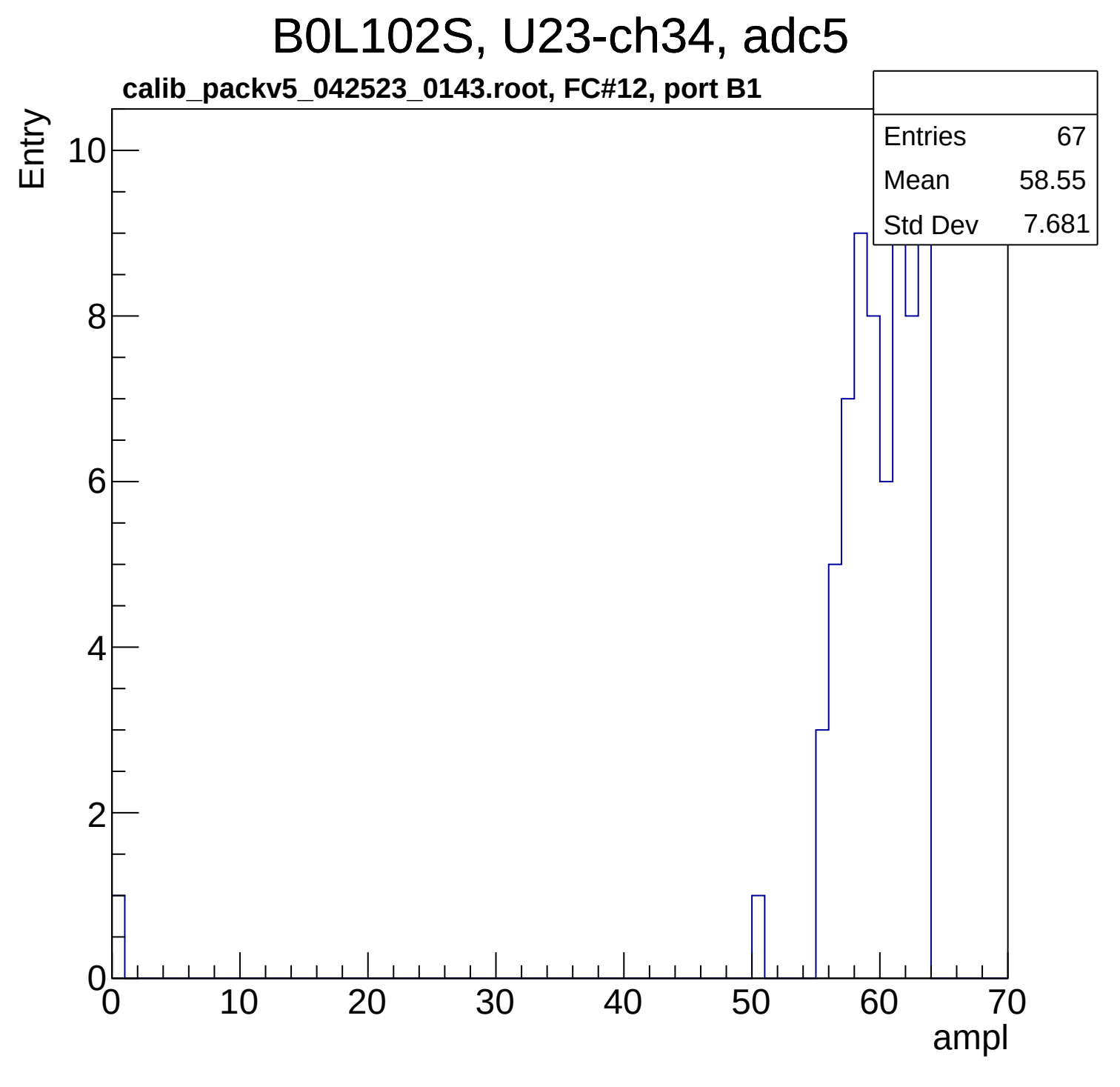
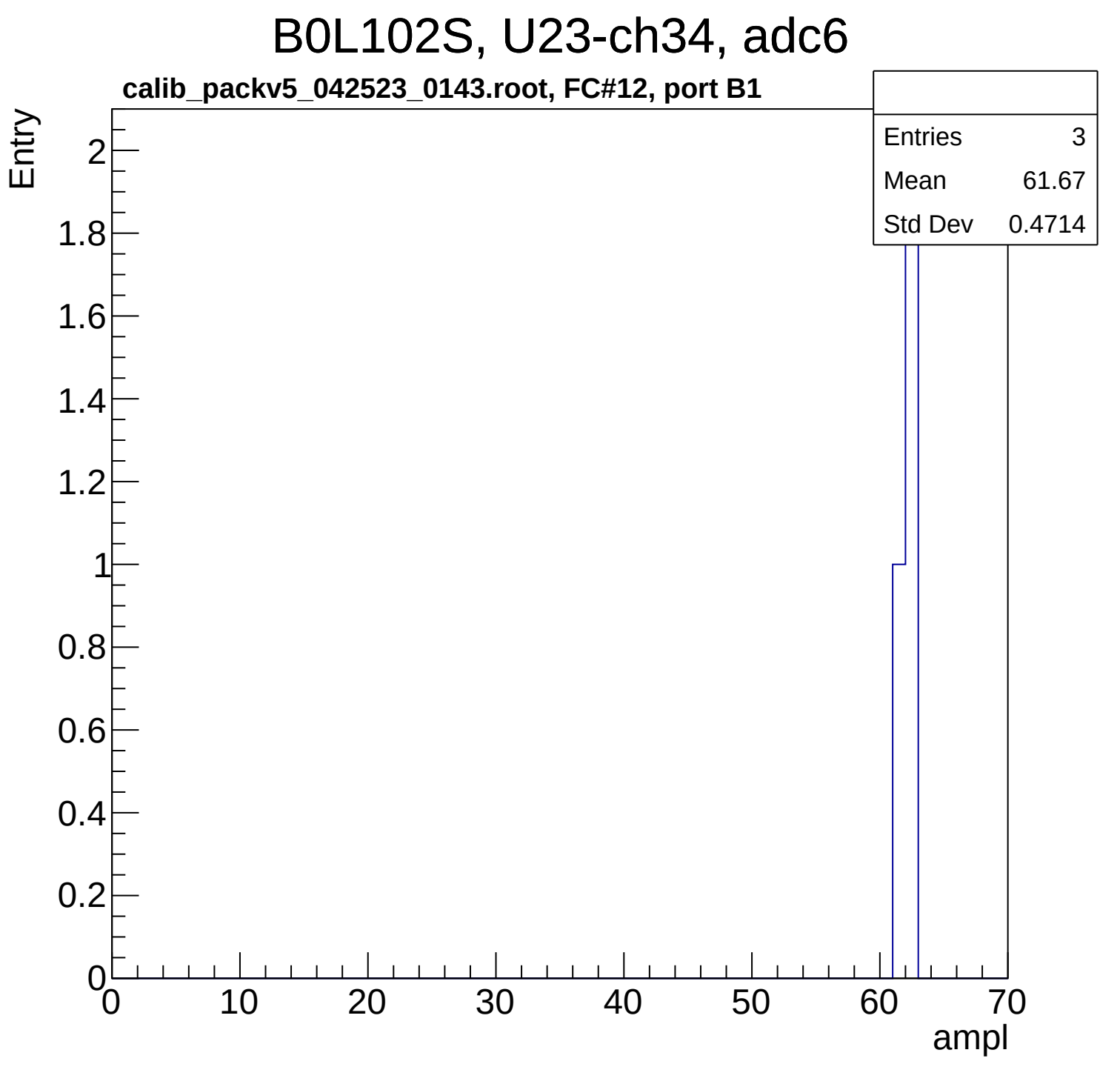
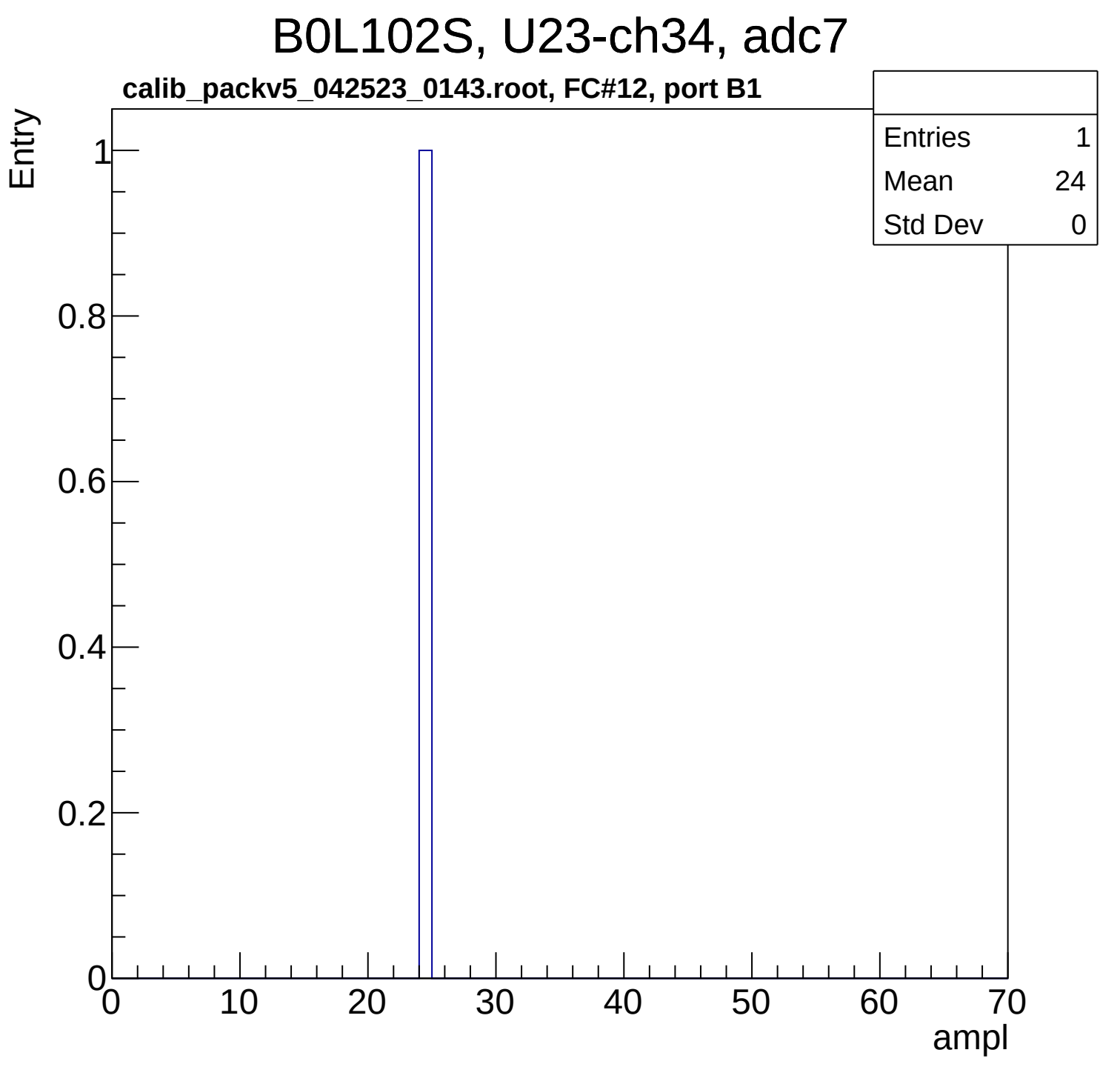
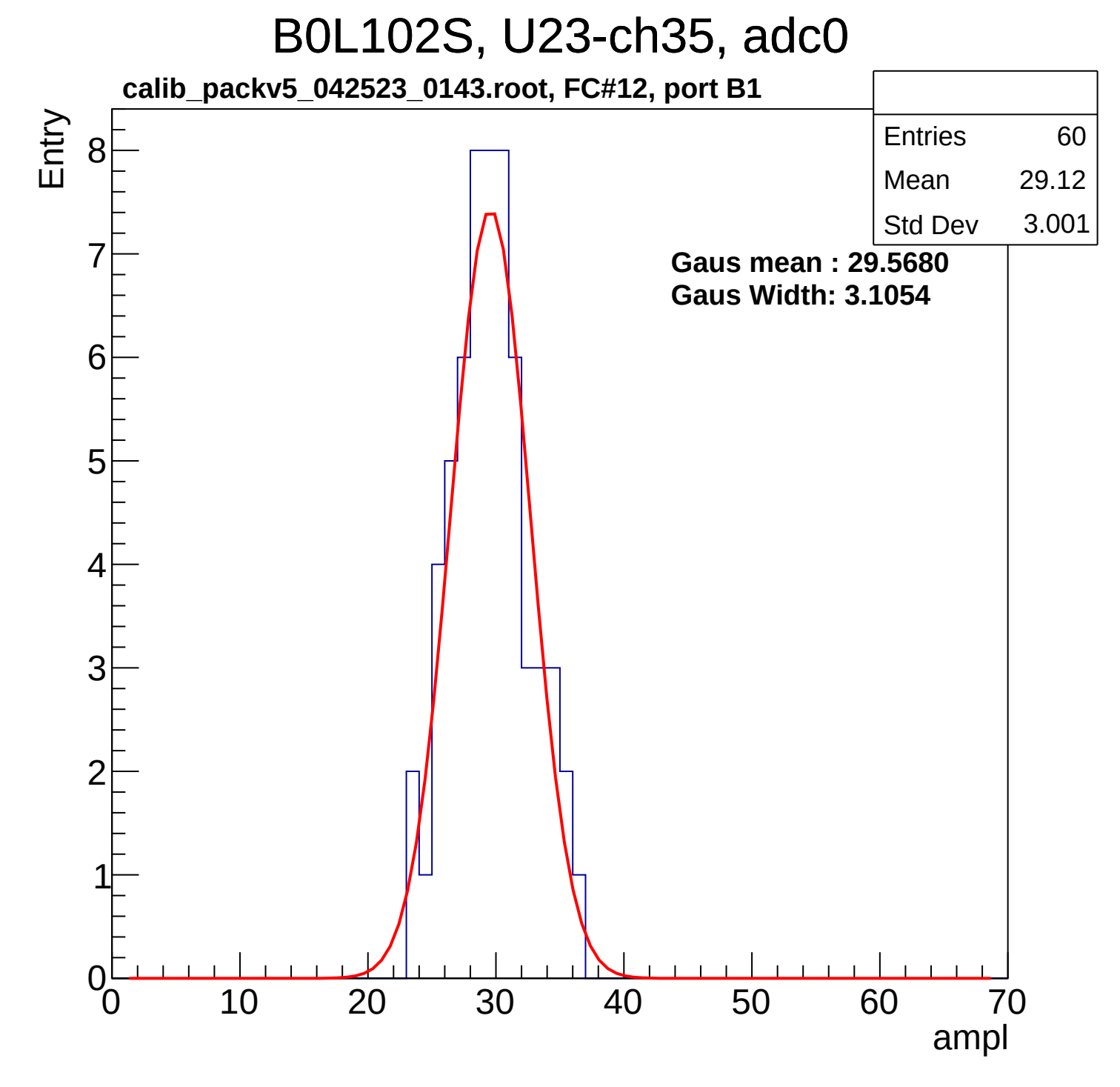
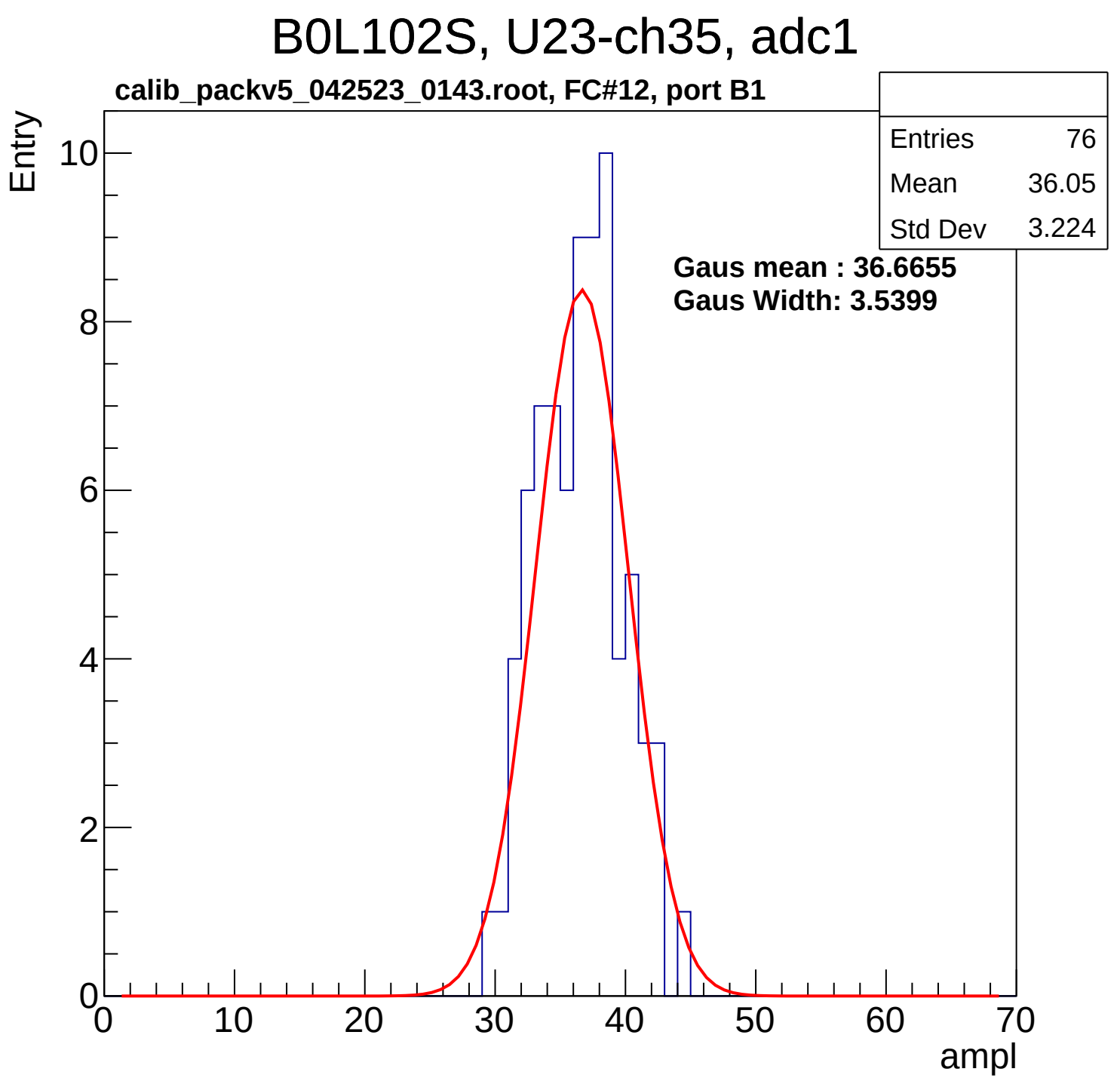
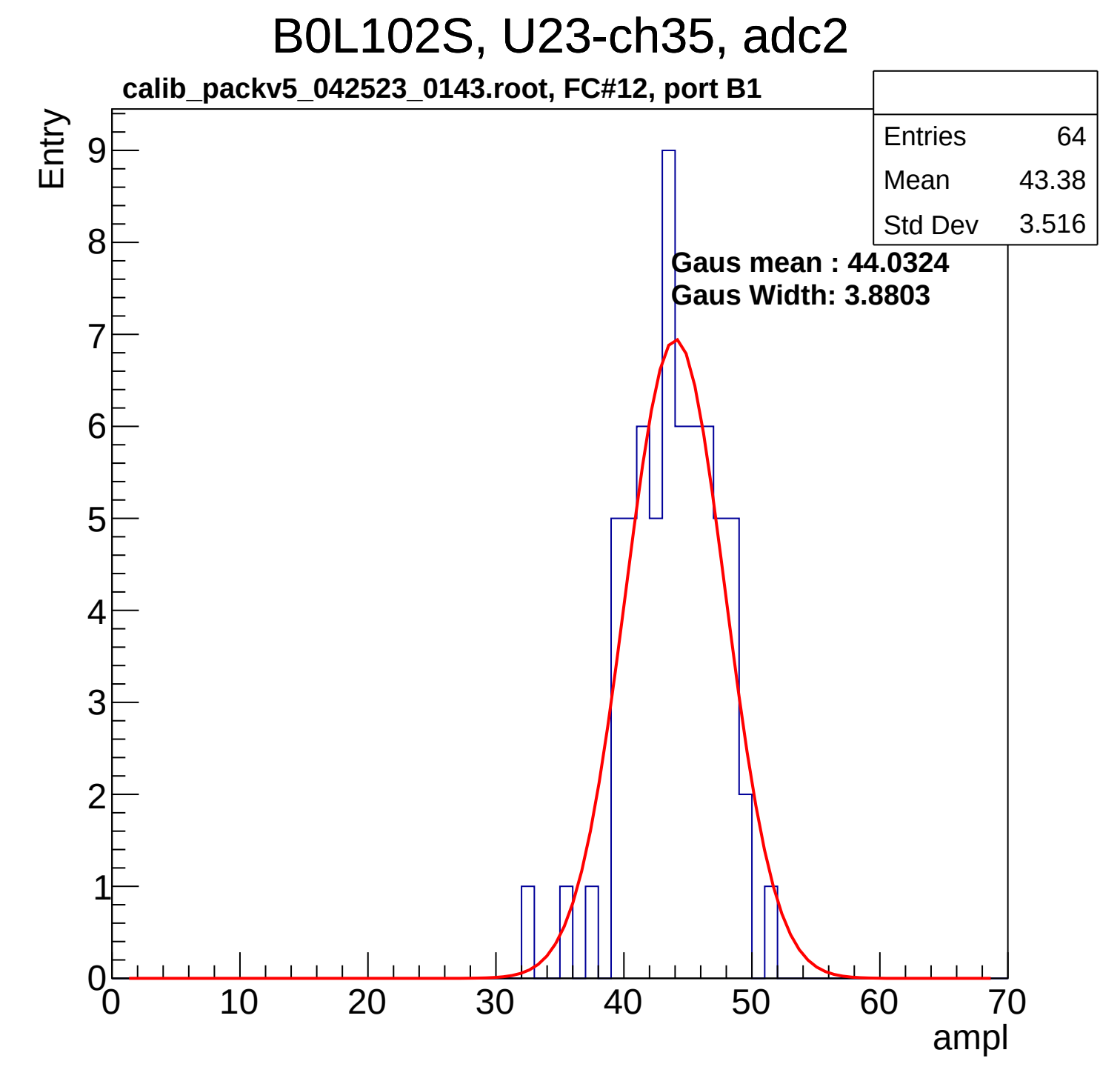

B0L102S, U23-ch34, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 58.55
Std Dev 7.681
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch34, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch34, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch35, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 29.12
Std Dev 3.001
Gaus mean : 29.5680
Gaus Width: 3.1054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch35, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 36.05
Std Dev 3.224
Gaus mean : 36.6655
Gaus Width: 3.5399
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch35, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.38
Std Dev 3.516
Gaus mean : 44.0324
Gaus Width: 3.8803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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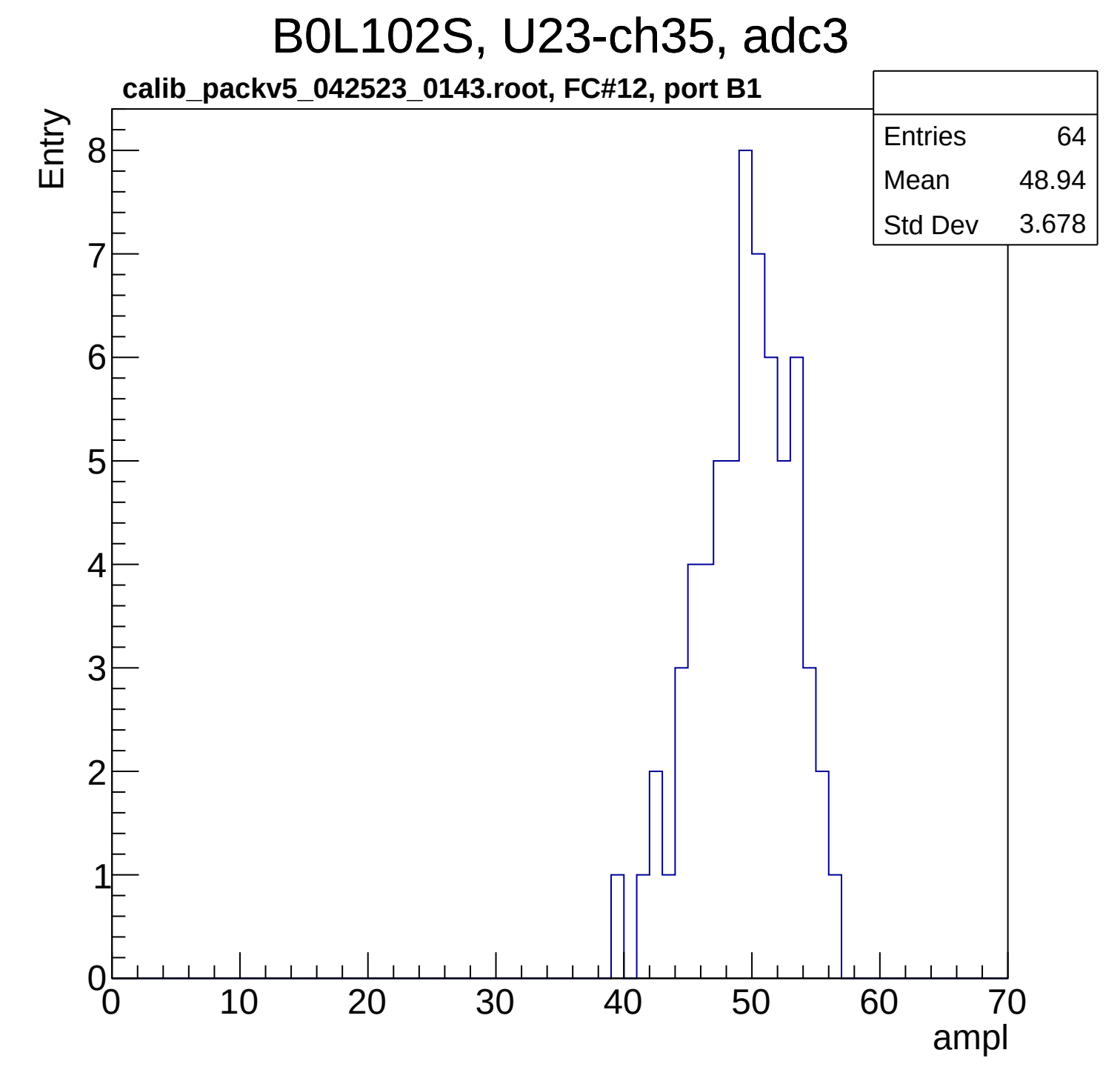
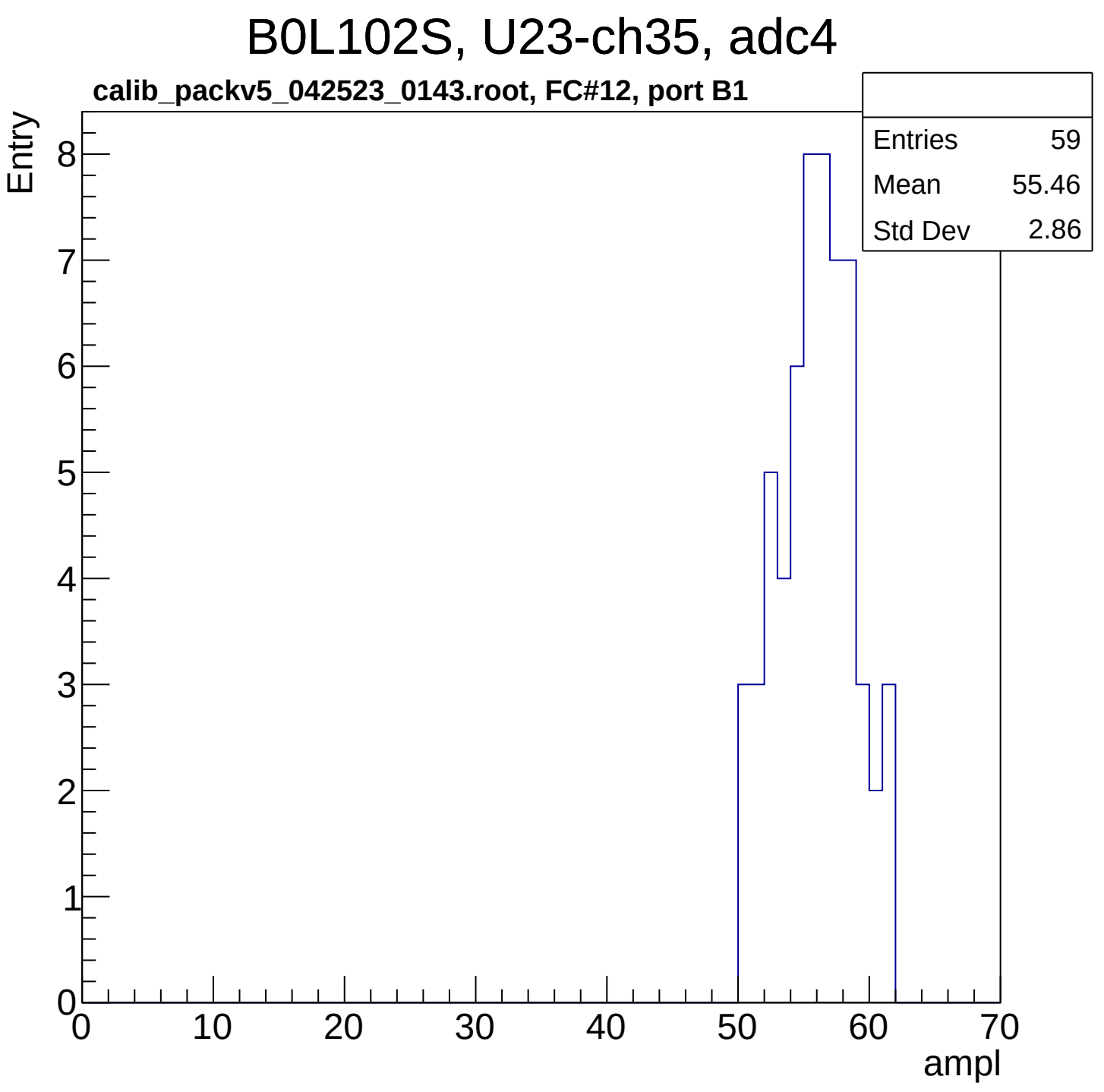
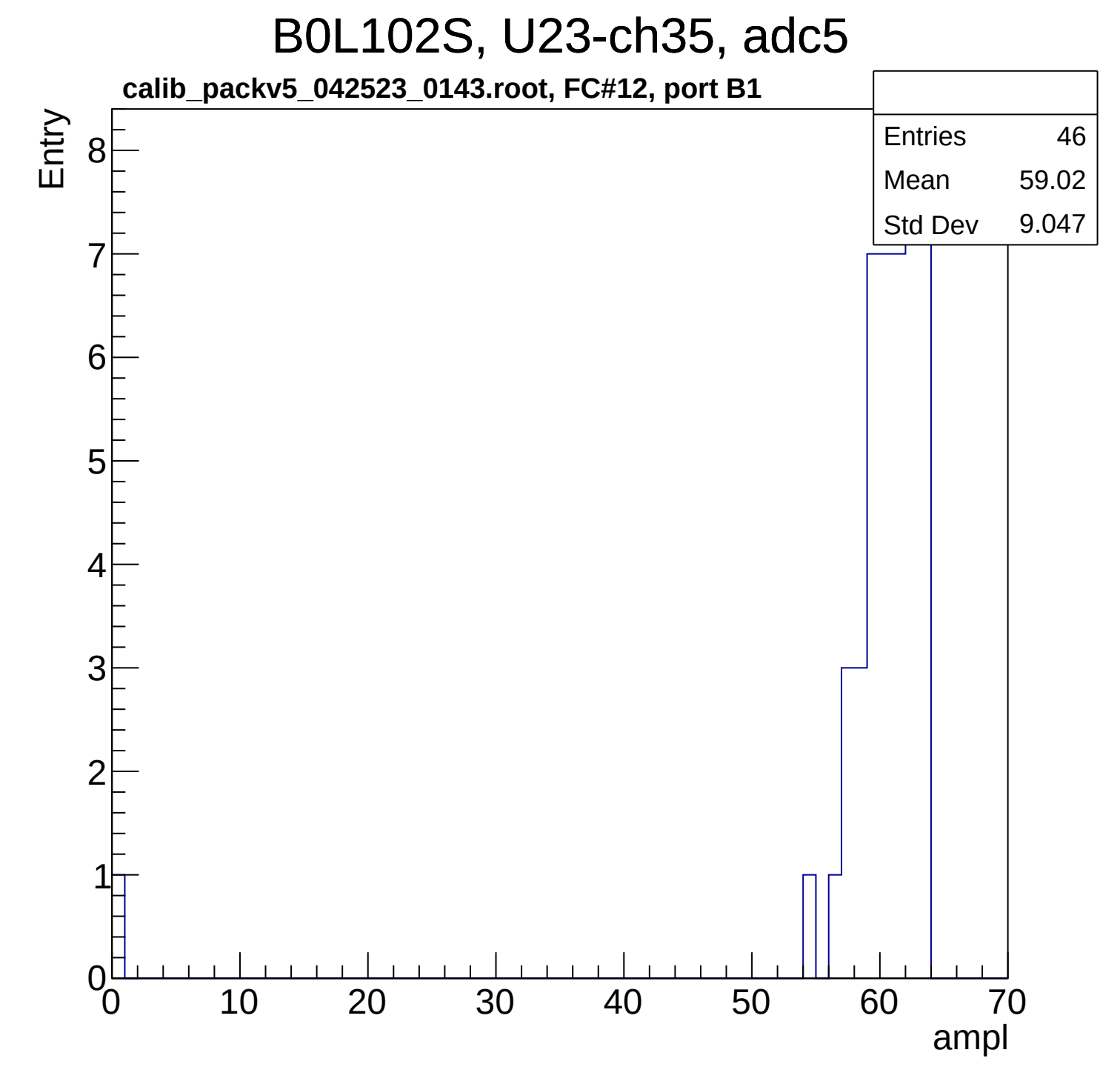

B0L102S, U23-ch35, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 48.94
Std Dev 3.678
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch35, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.46
Std Dev 2.86
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch35, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 59.02
Std Dev 9.047
Entry
ampl
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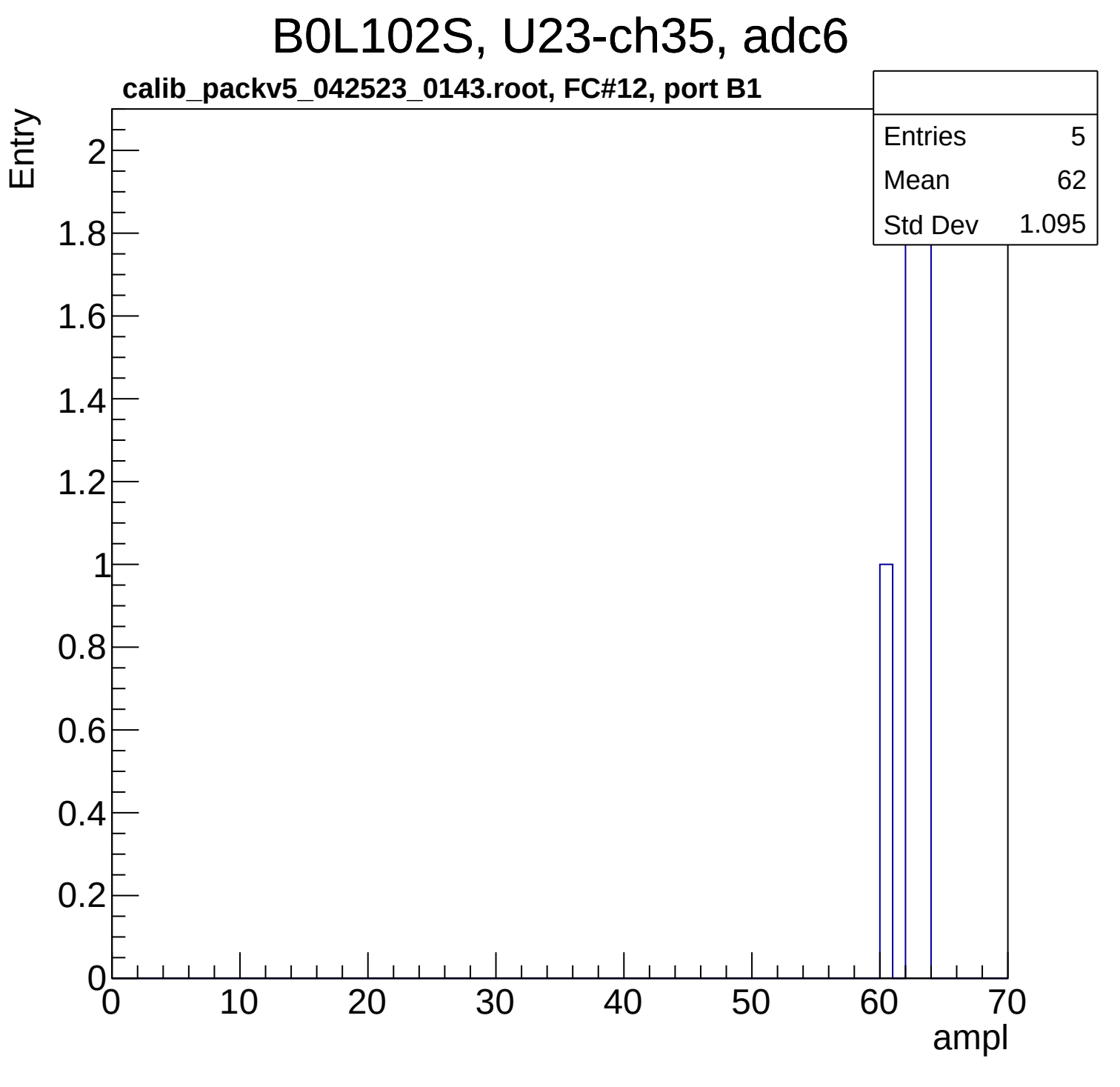
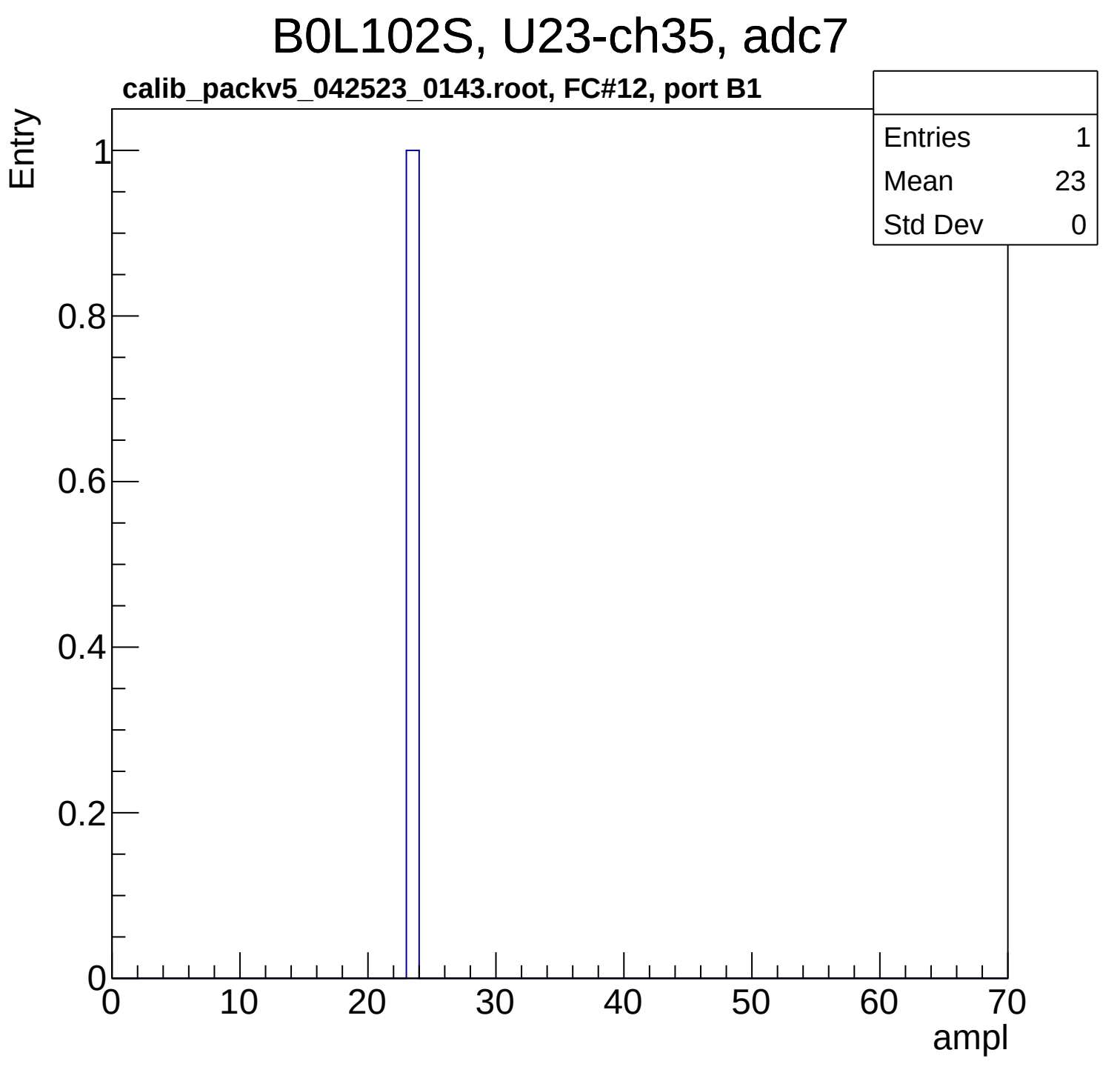
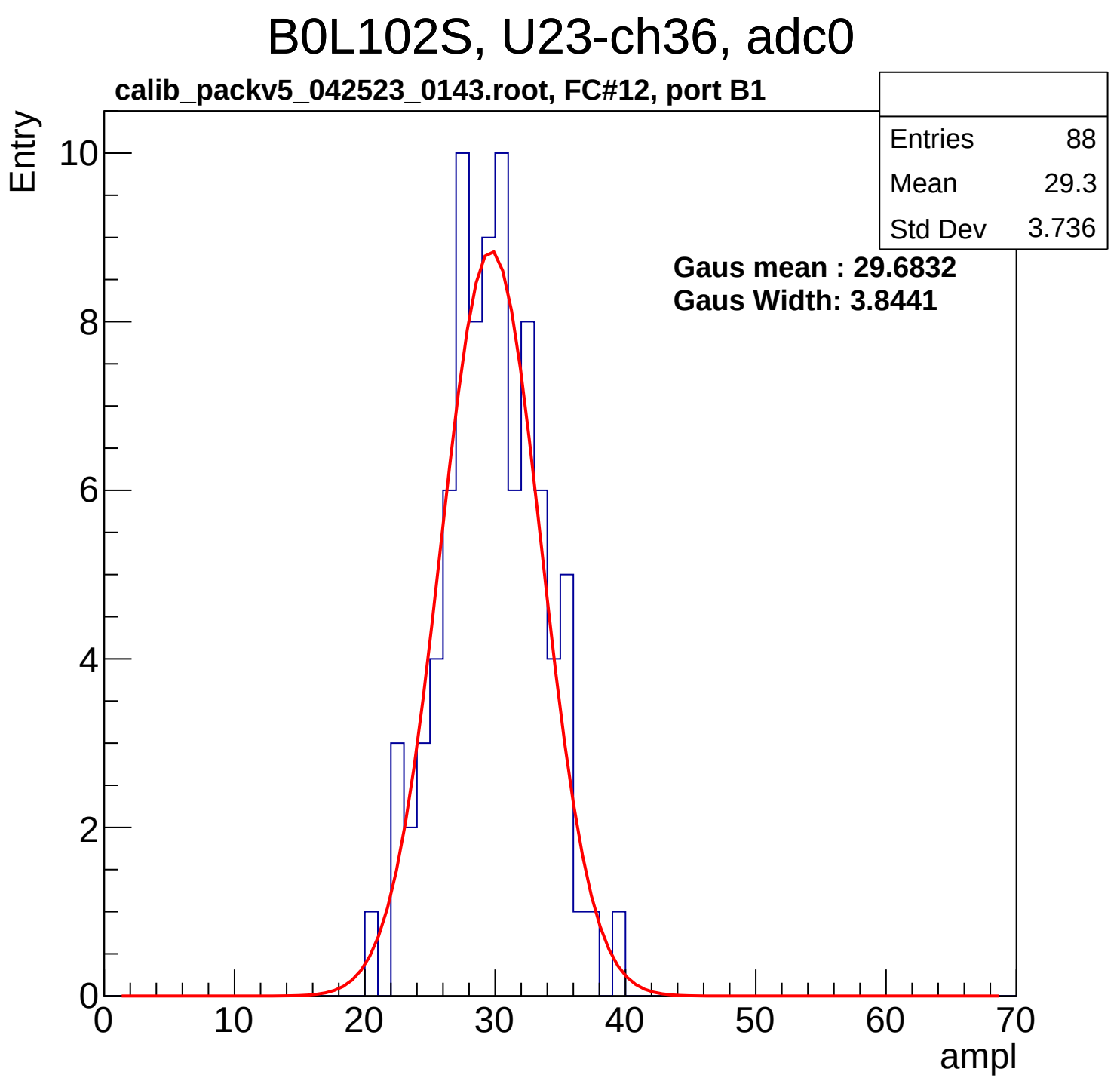
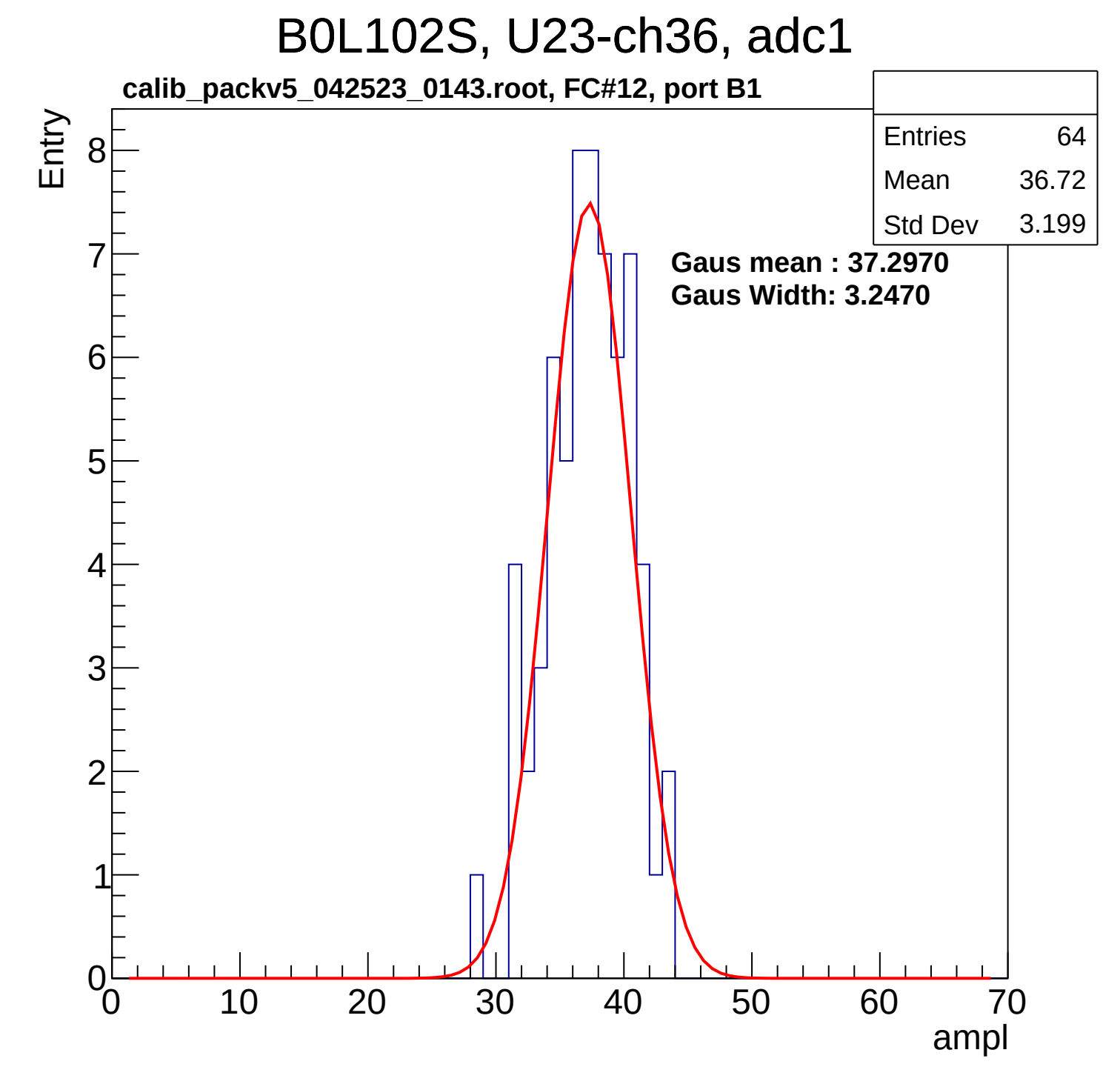
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch35, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch35, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch36, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 29.3
Std Dev 3.736
Gaus mean : 29.6832
Gaus Width: 3.8441
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch36, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.72
Std Dev 3.199
Gaus mean : 37.2970
Gaus Width: 3.2470
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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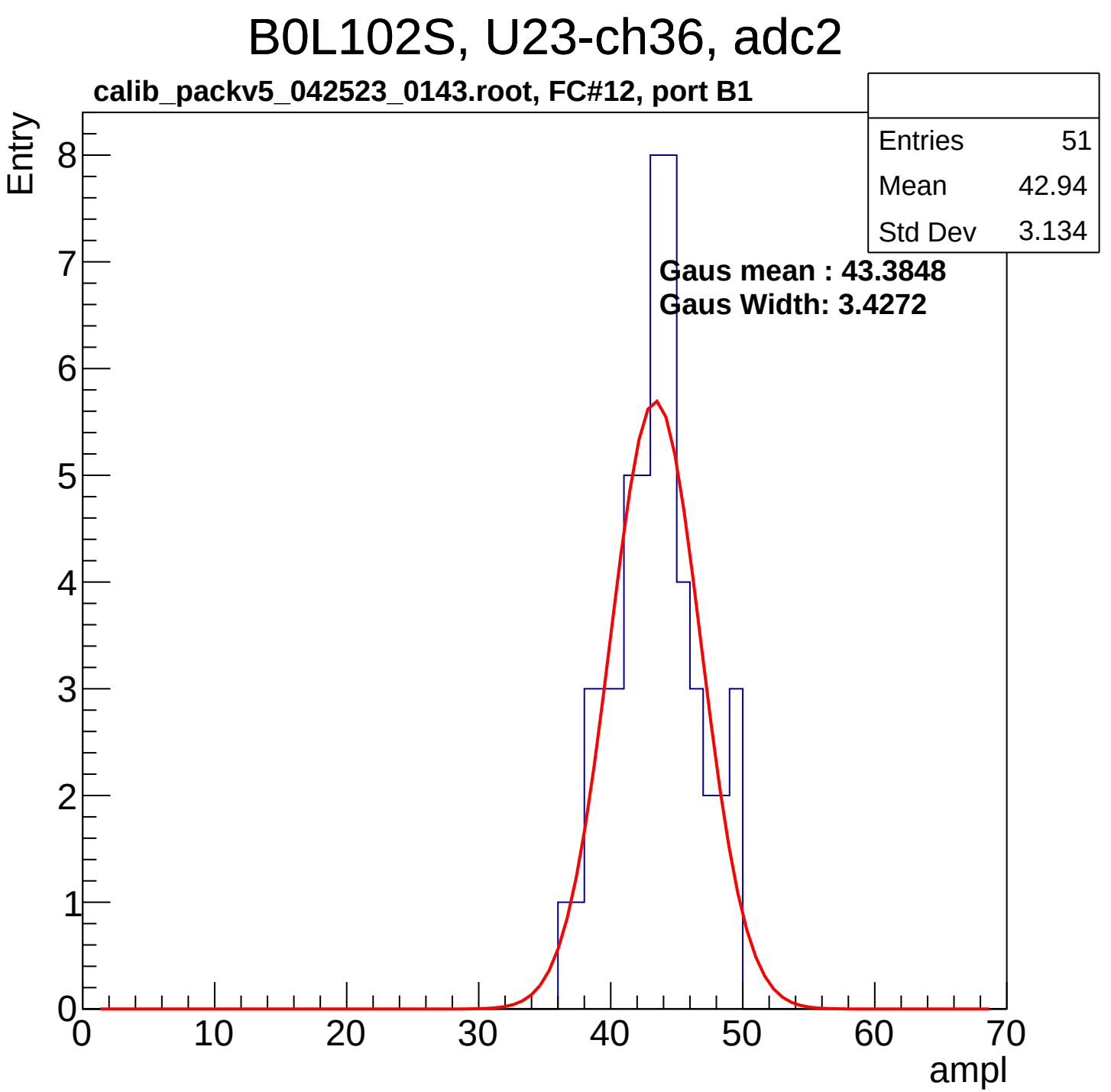
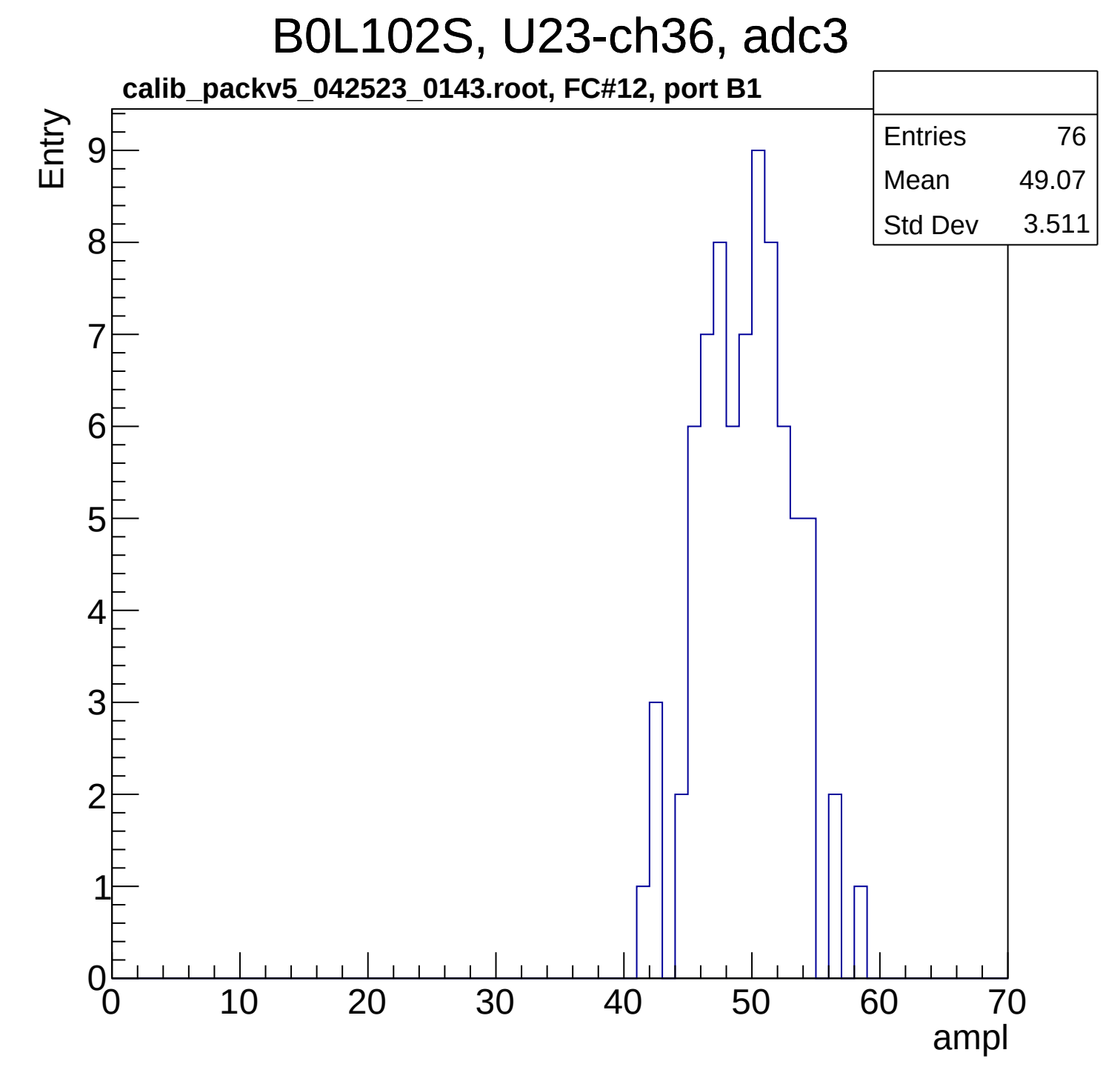
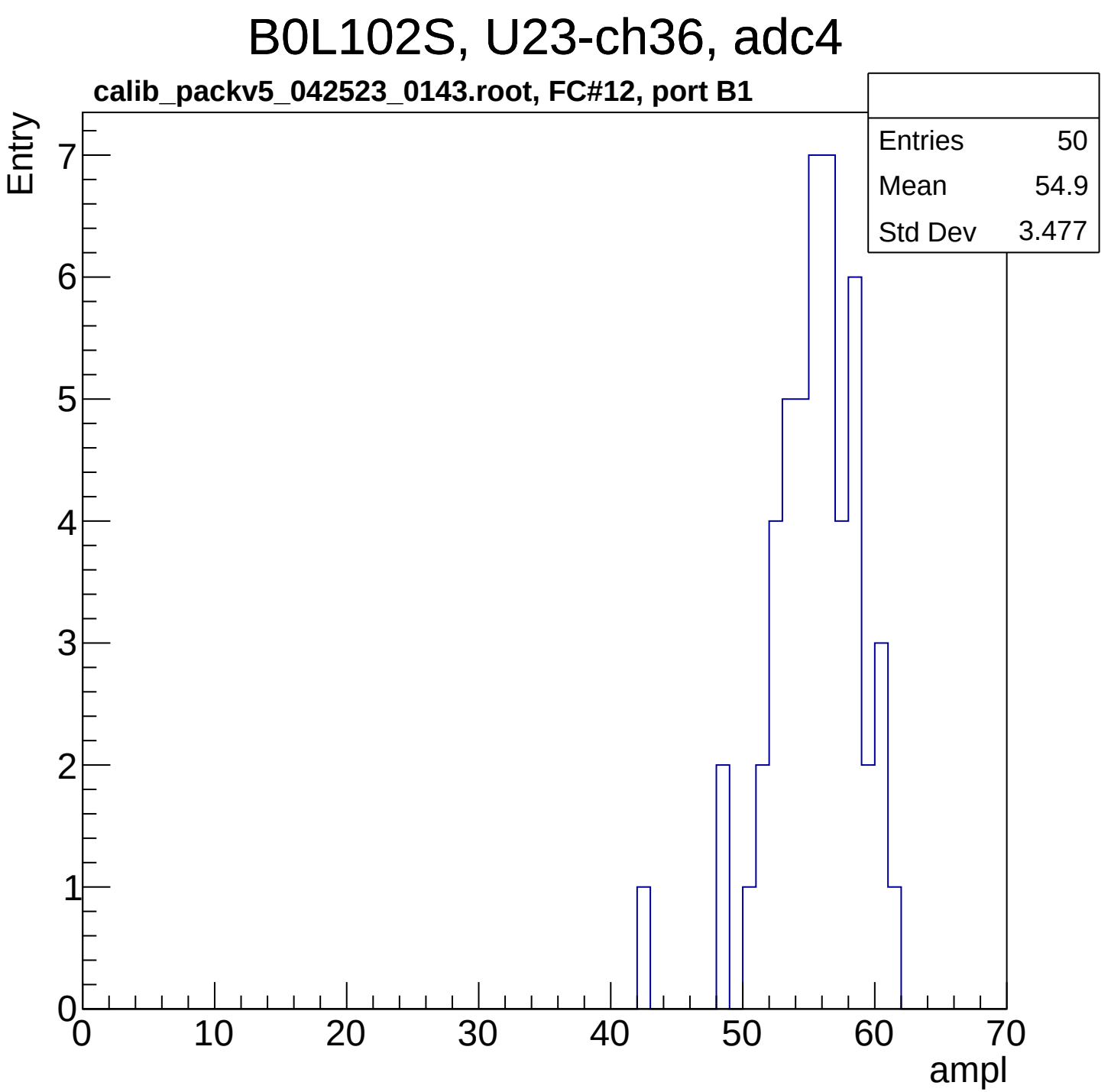
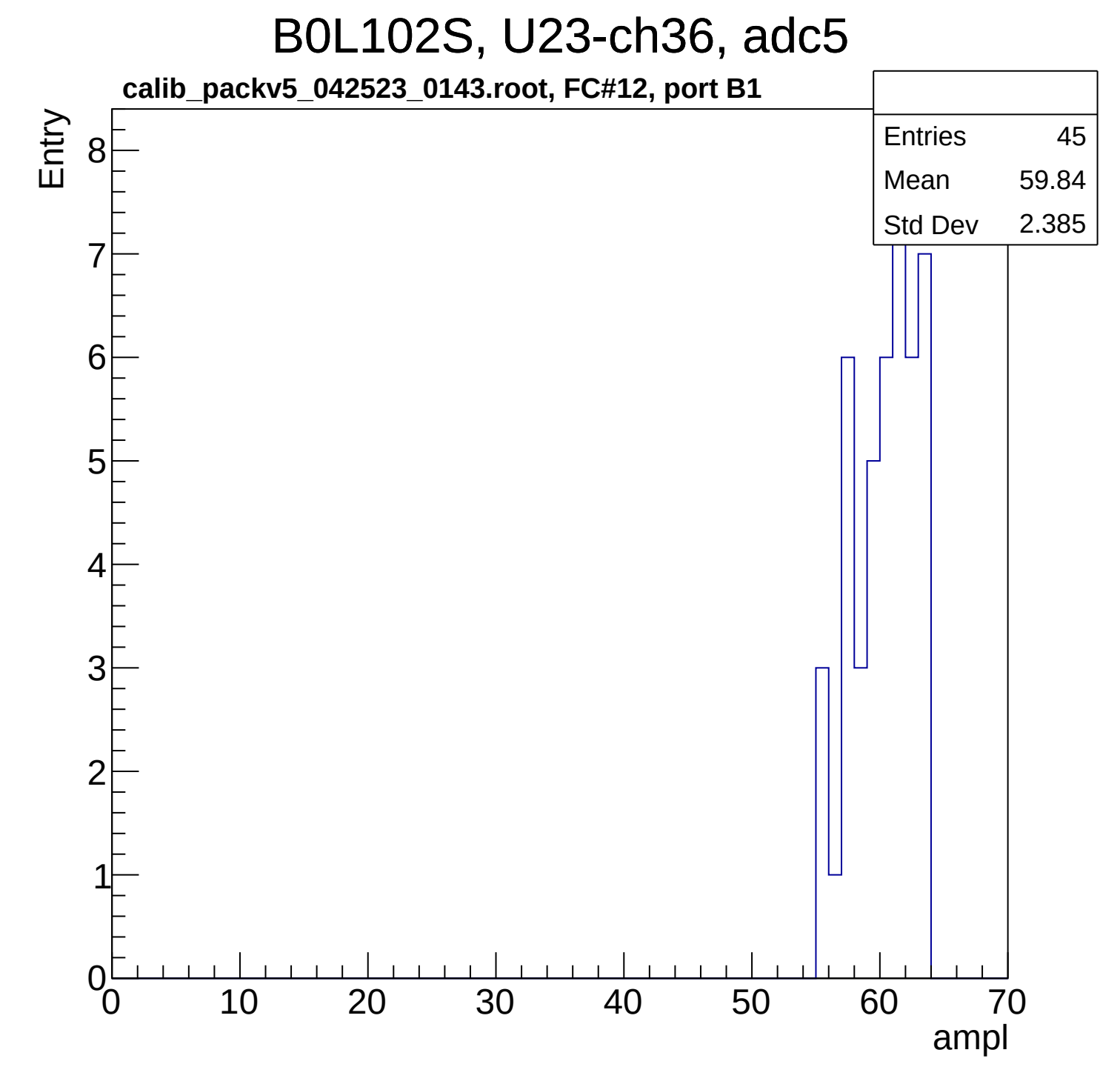
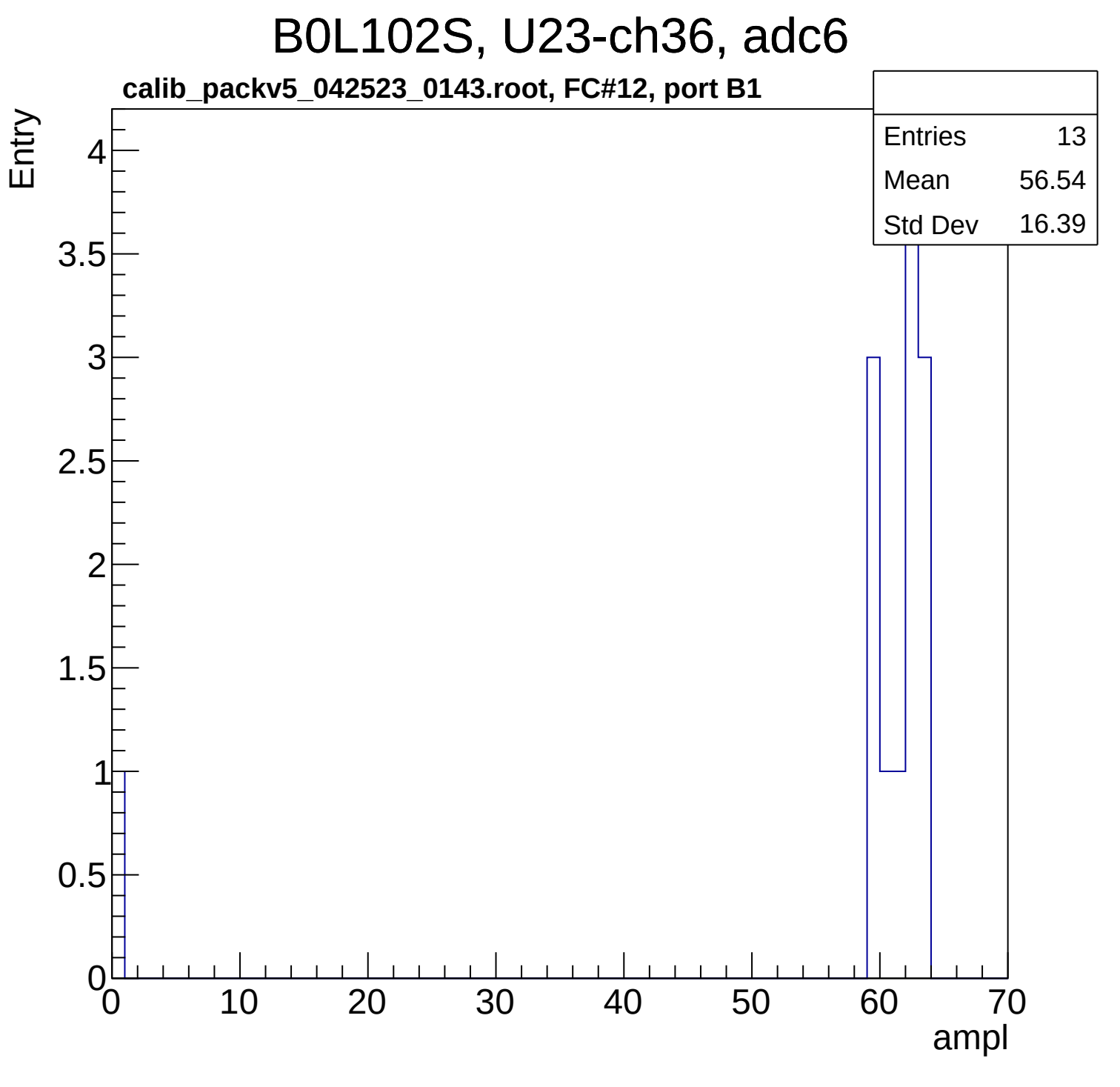
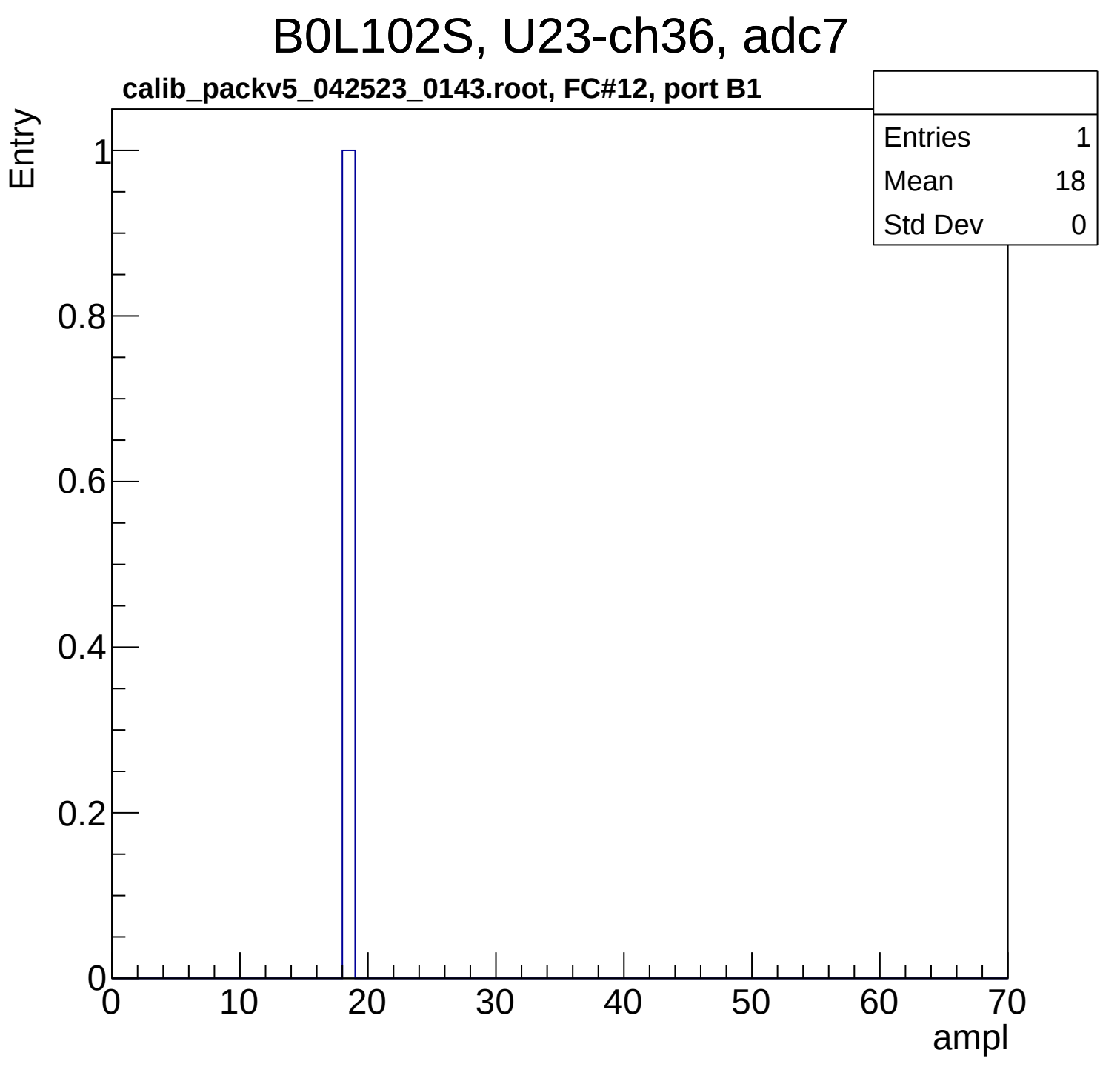

B0L102S, U23-ch36, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 42.94
Std Dev 3.134
Gaus mean : 43.3848
Gaus Width: 3.4272
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

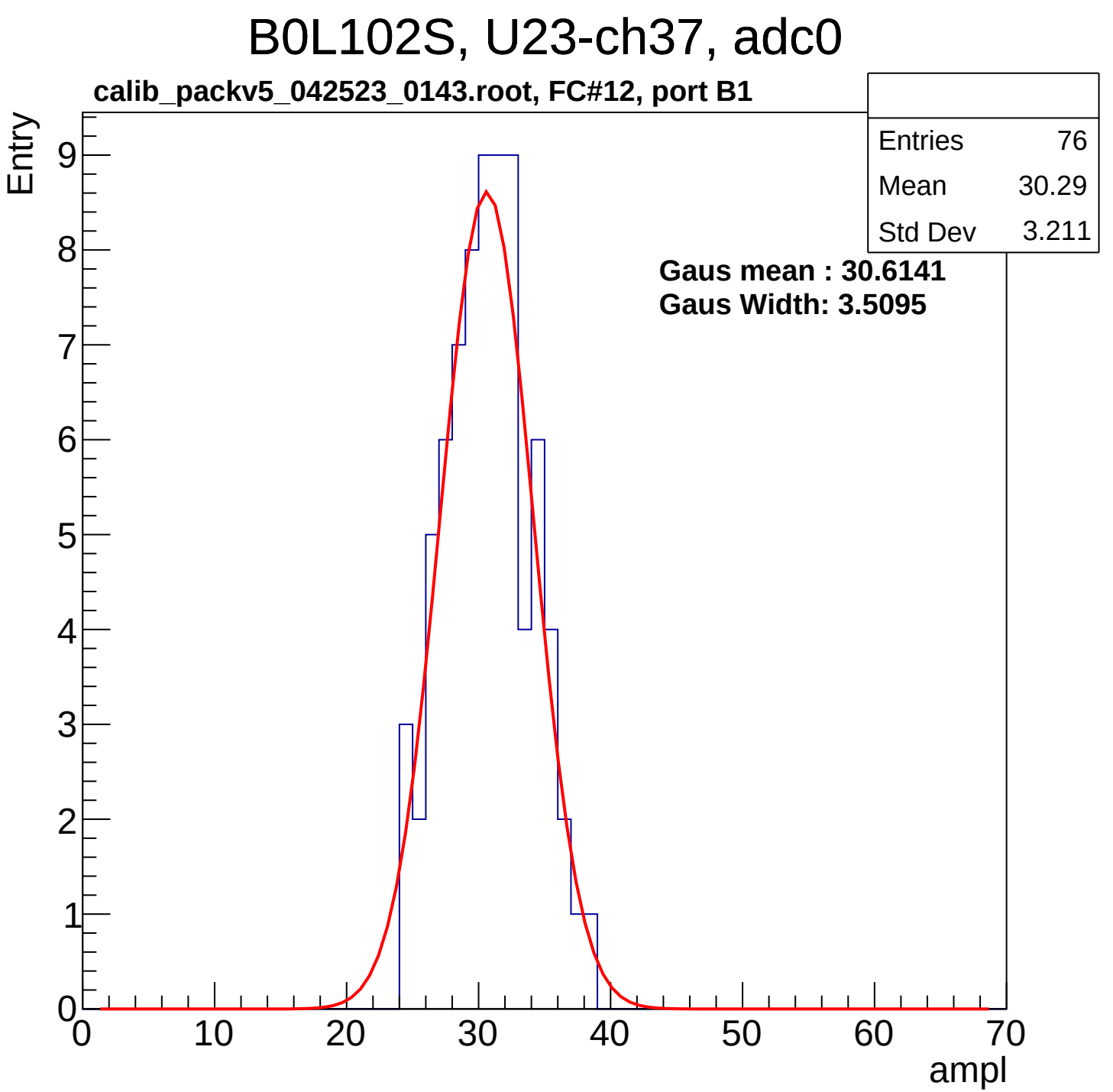

B0L102S, U23-ch36, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 49.07
Std Dev 3.511
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

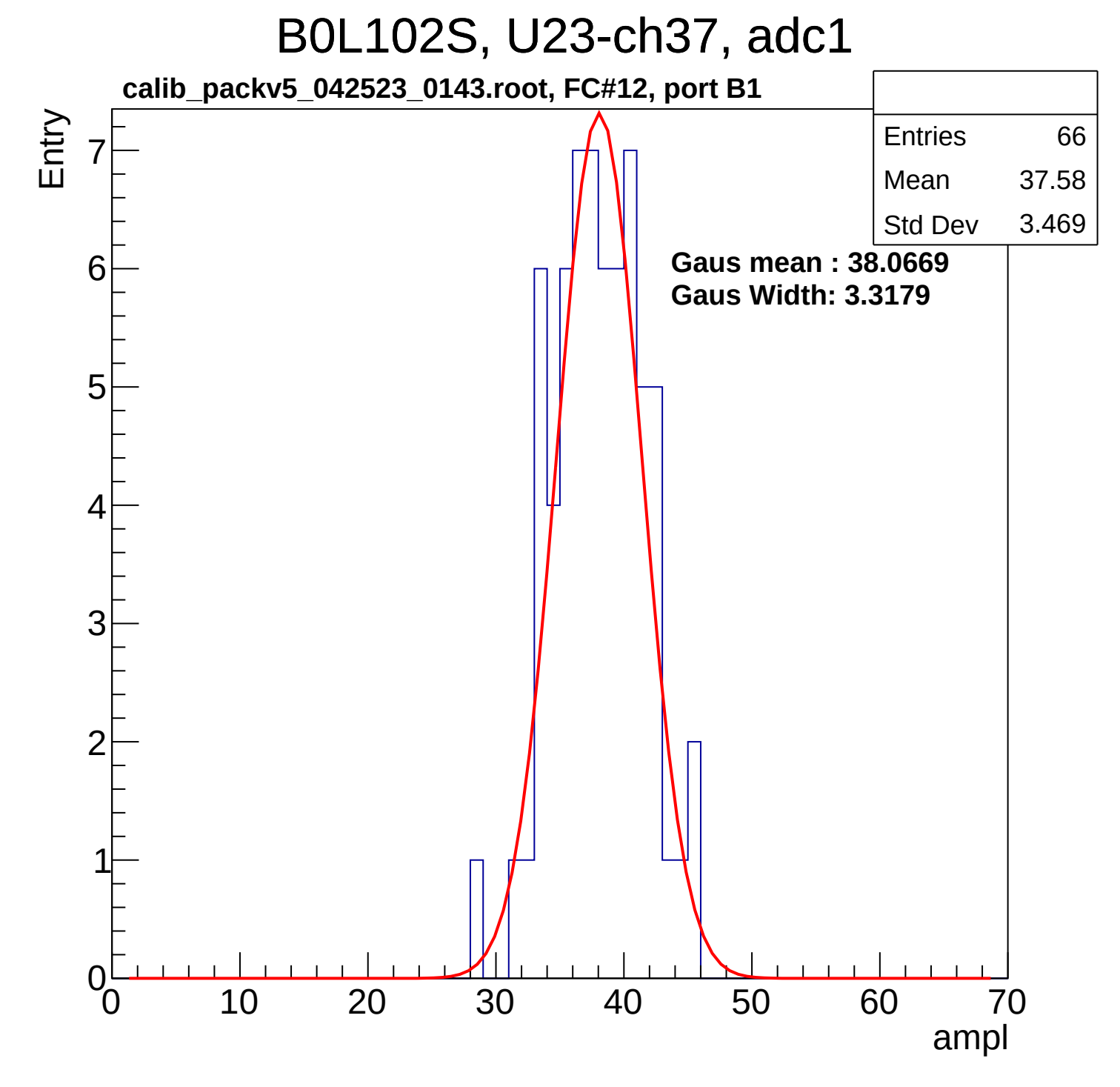

B0L102S, U23-ch36, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 54.9
Std Dev 3.477
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

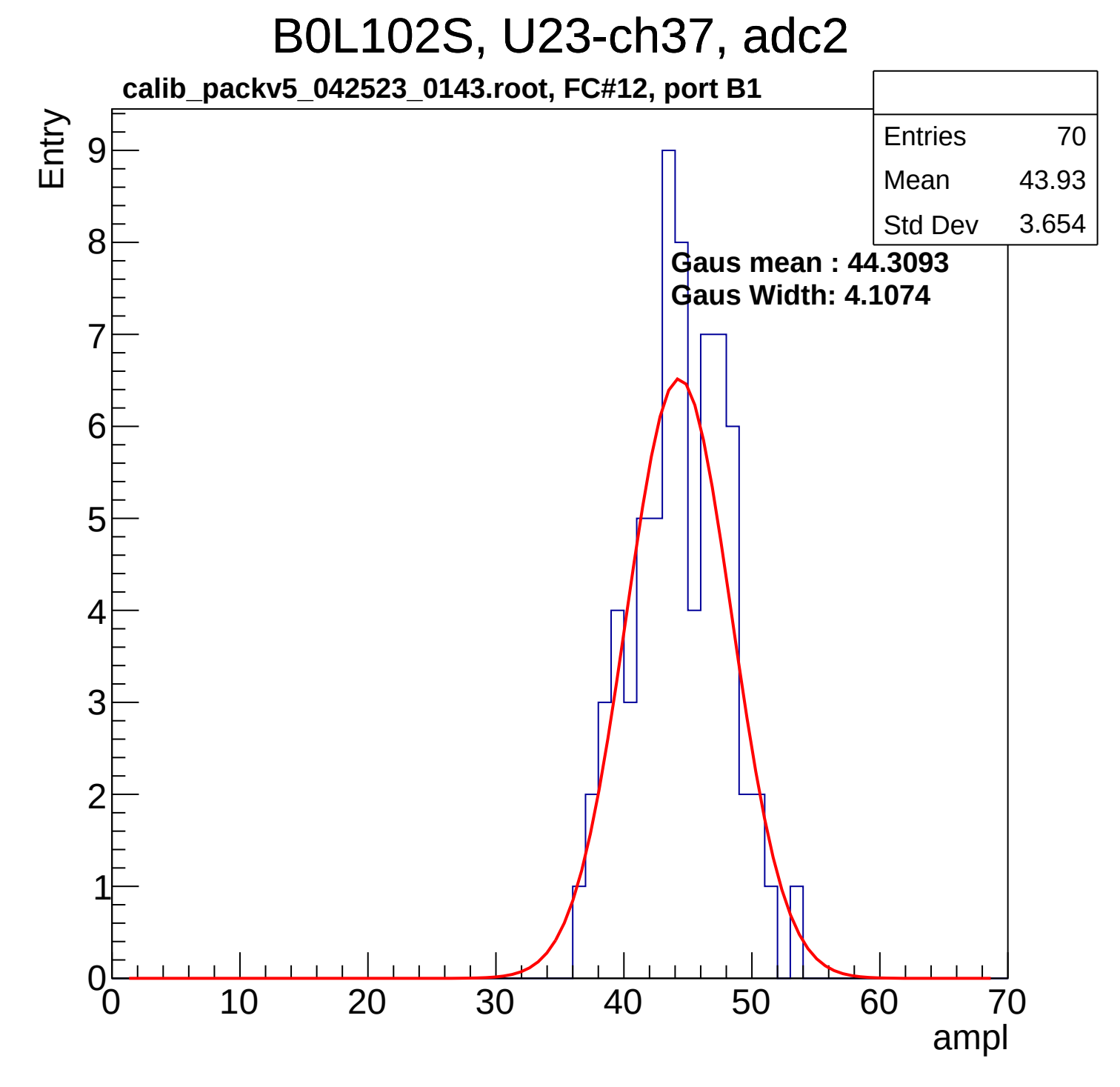

B0L102S, U23-ch36, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 59.84
Std Dev 2.385
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

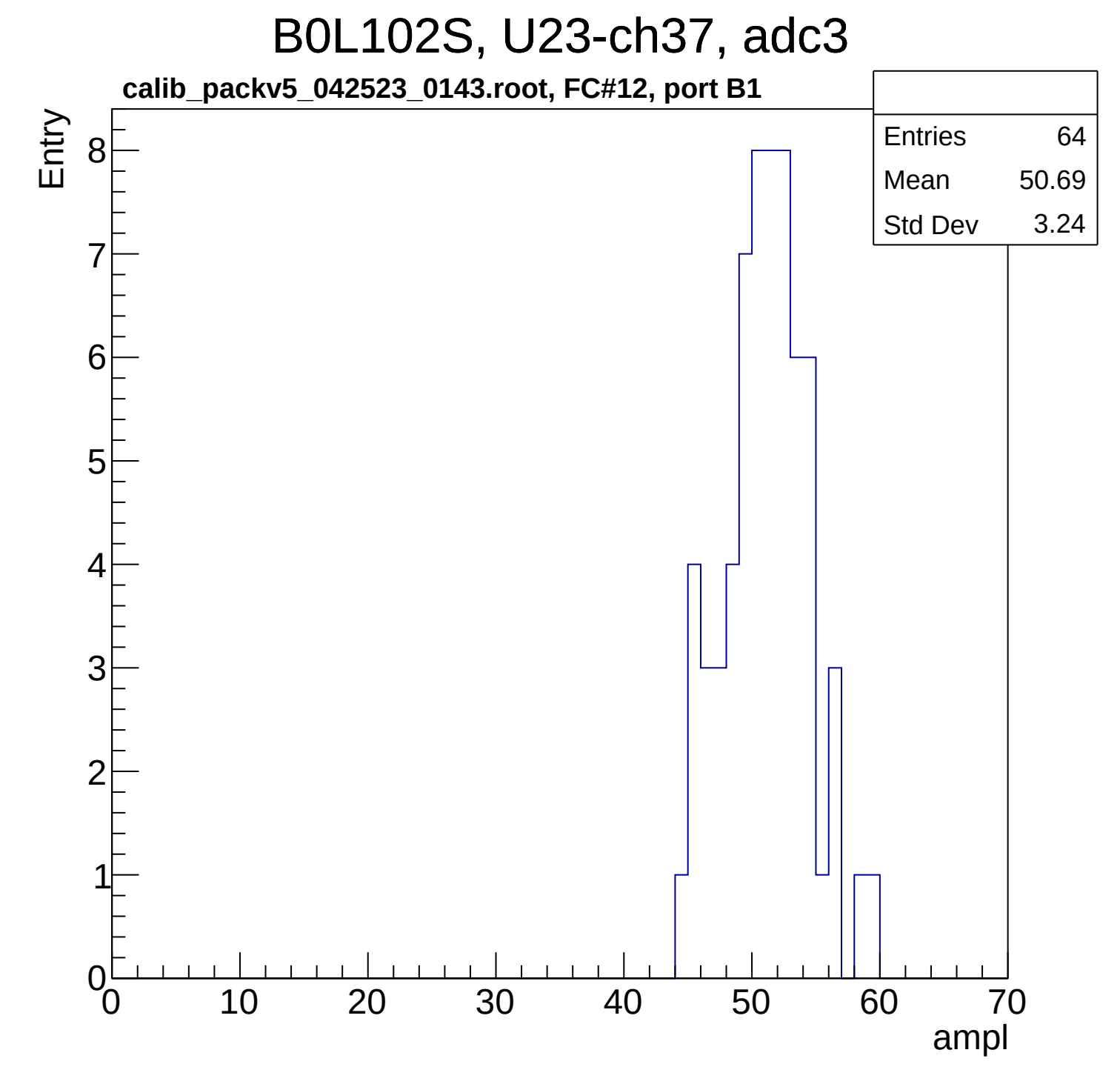

B0L102S, U23-ch36, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.54
Std Dev 16.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch36, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch37, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 30.29
Std Dev 3.211
Gaus mean : 30.6141
Gaus Width: 3.5095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch37, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 37.58
Std Dev 3.469
Gaus mean : 38.0669
Gaus Width: 3.3179
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch37, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 43.93
Std Dev 3.654
Gaus mean : 44.3093
Gaus Width: 4.1074
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch37, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 50.69
Std Dev 3.24
Entry
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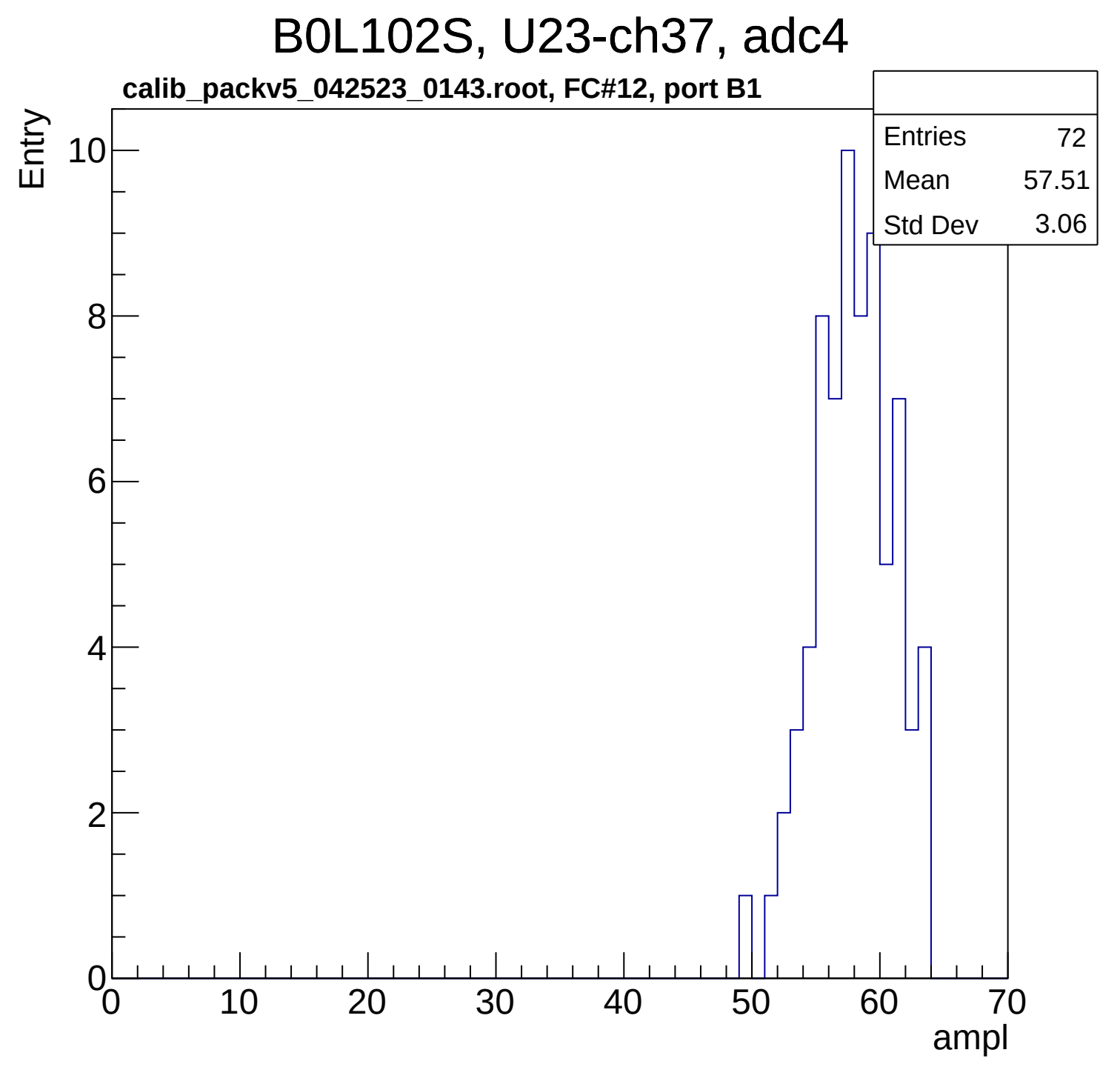
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch37, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 57.51
Std Dev 3.06
Entry
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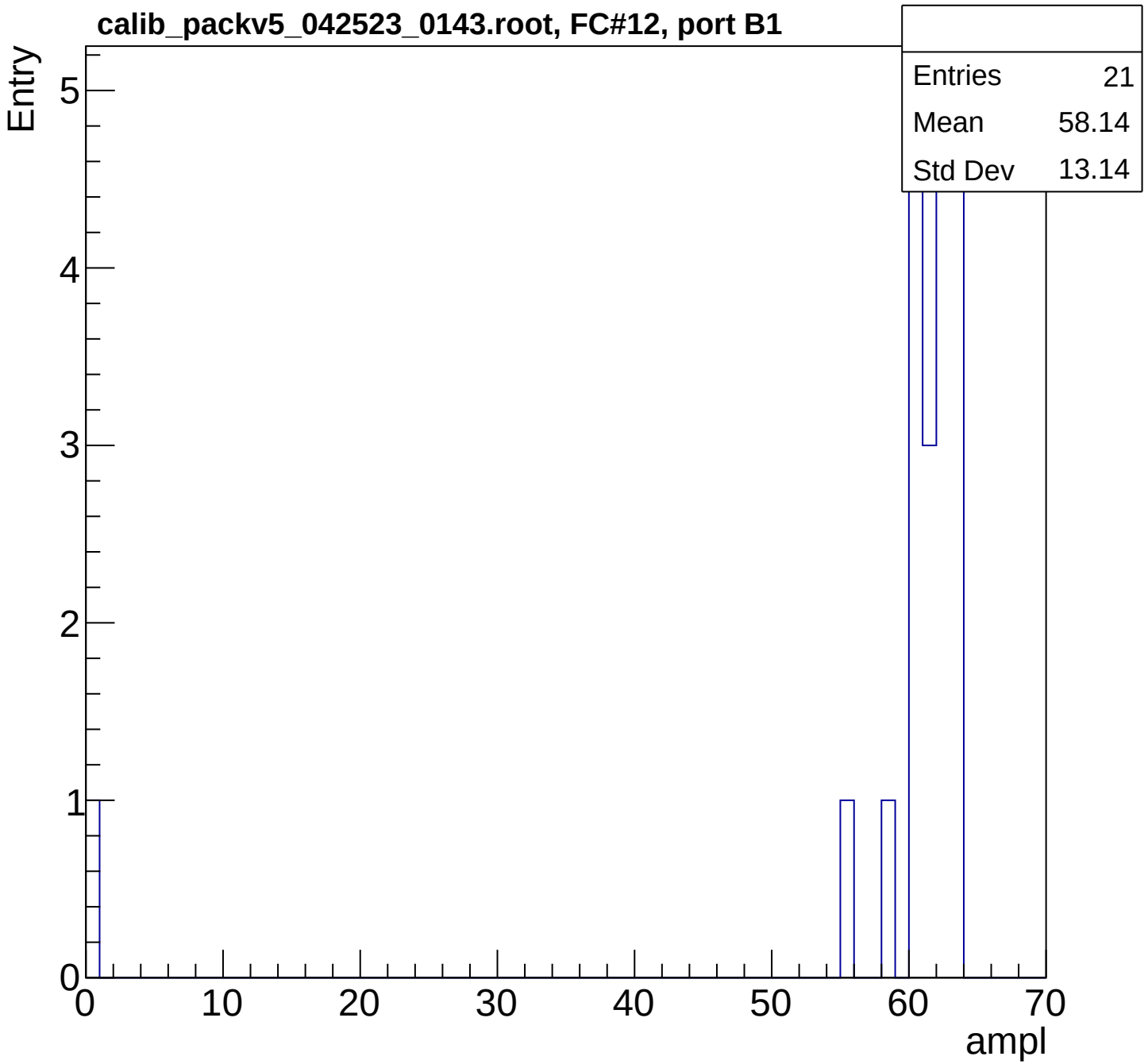
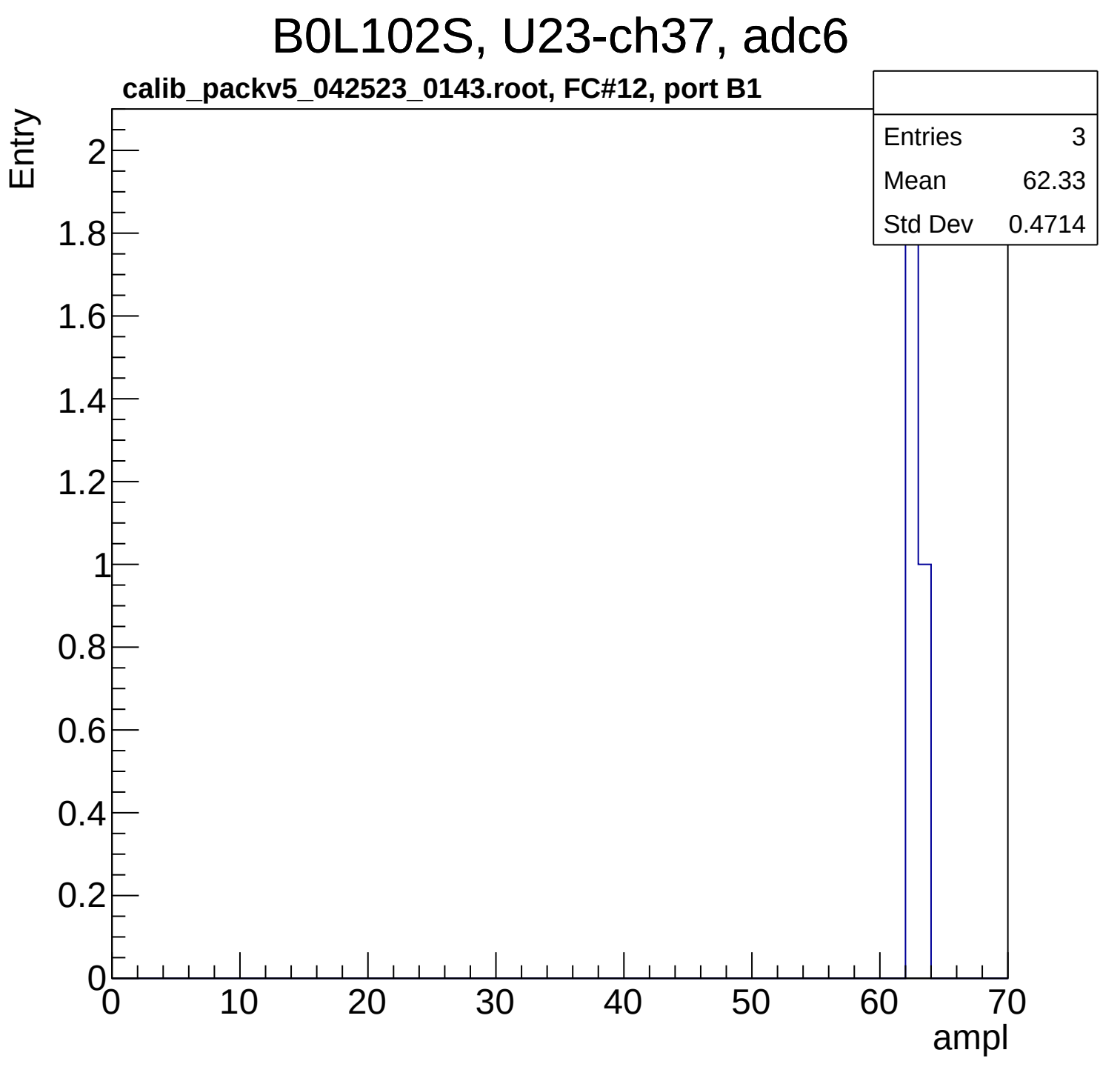
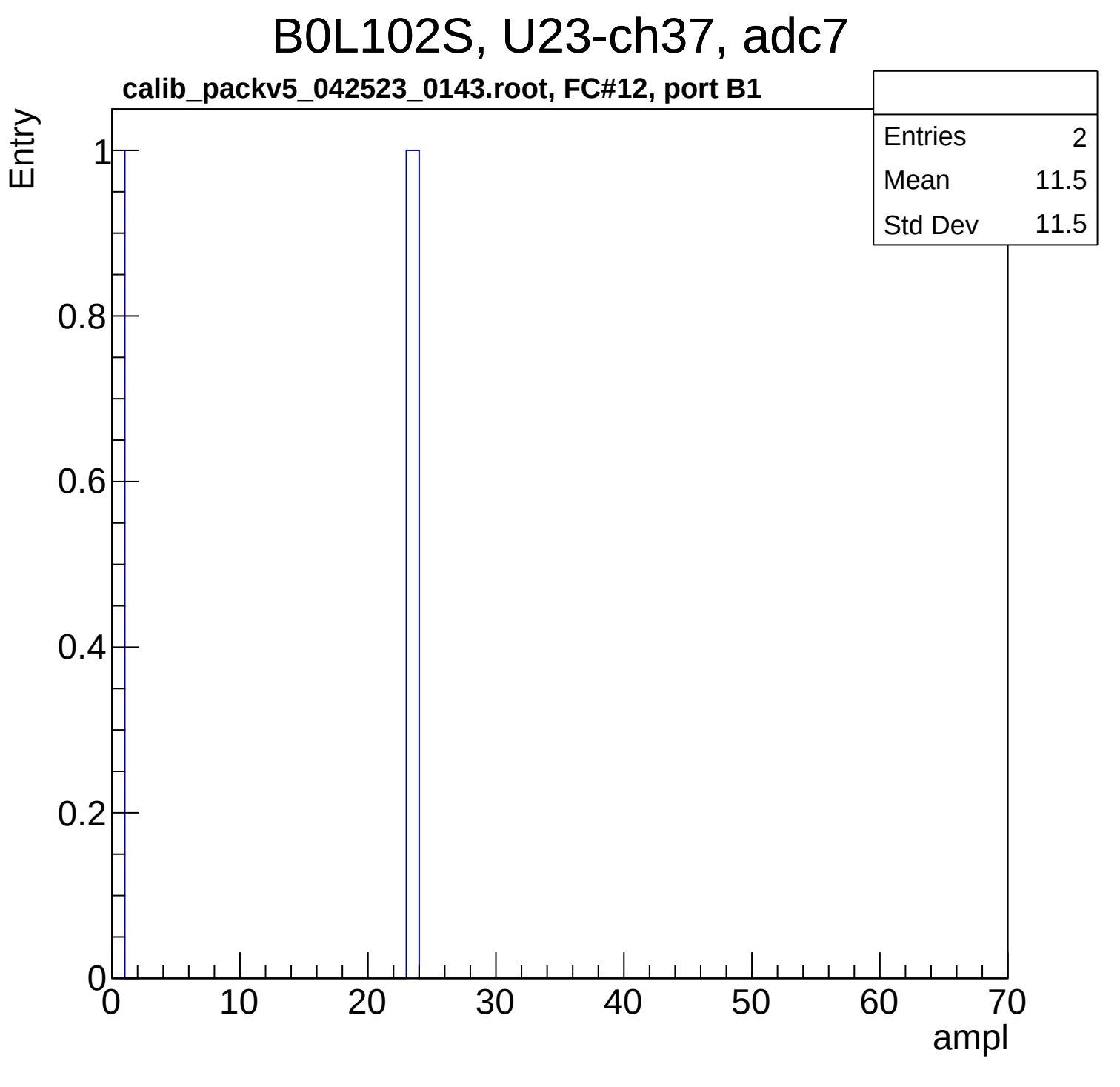
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch37, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 21
Mean 58.14
Std Dev 13.14
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch37, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch37, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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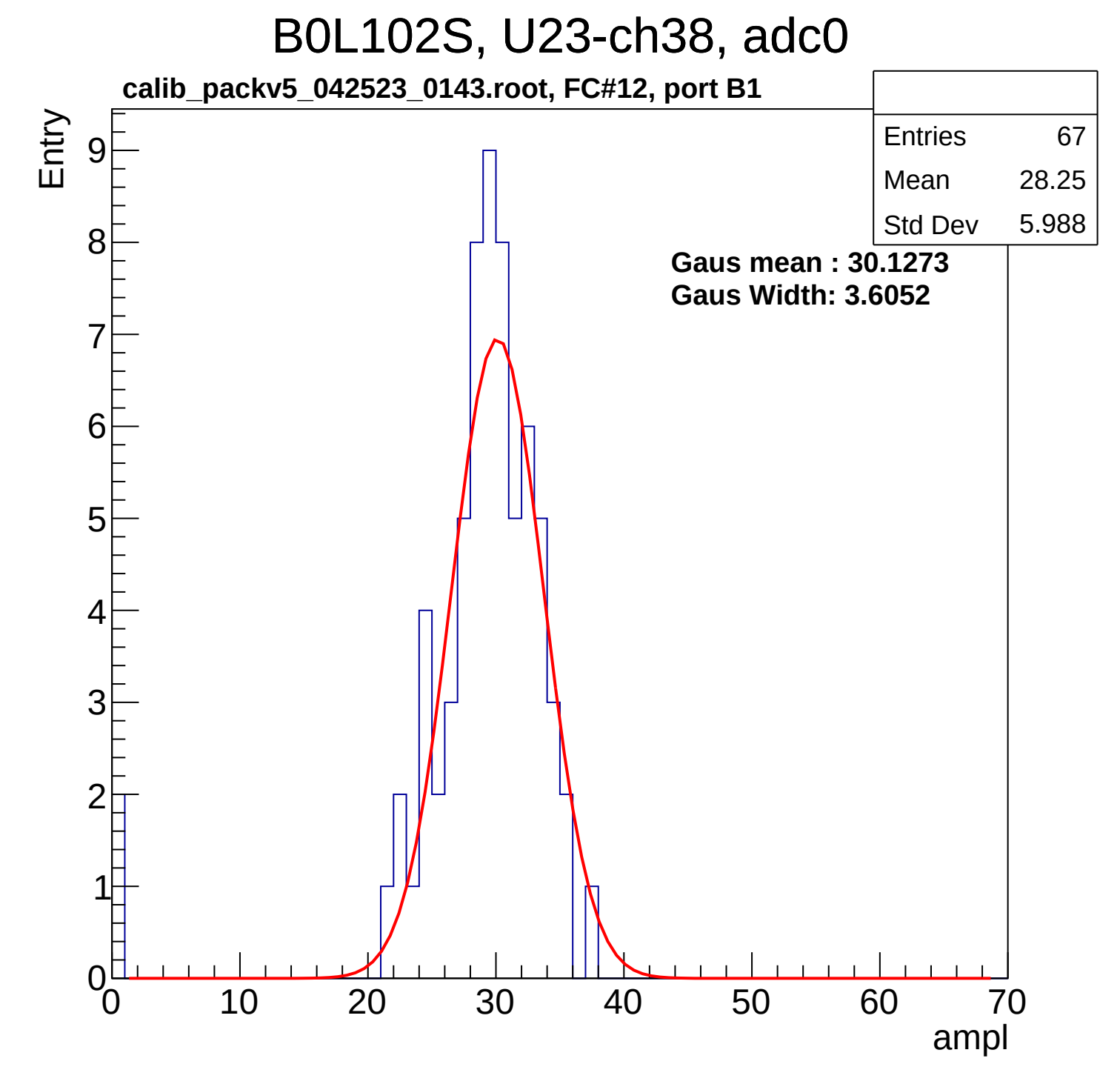
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch38, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.25
Std Dev 5.988
Gaus mean : 30.1273
Gaus Width: 3.6052
Entry
ampl
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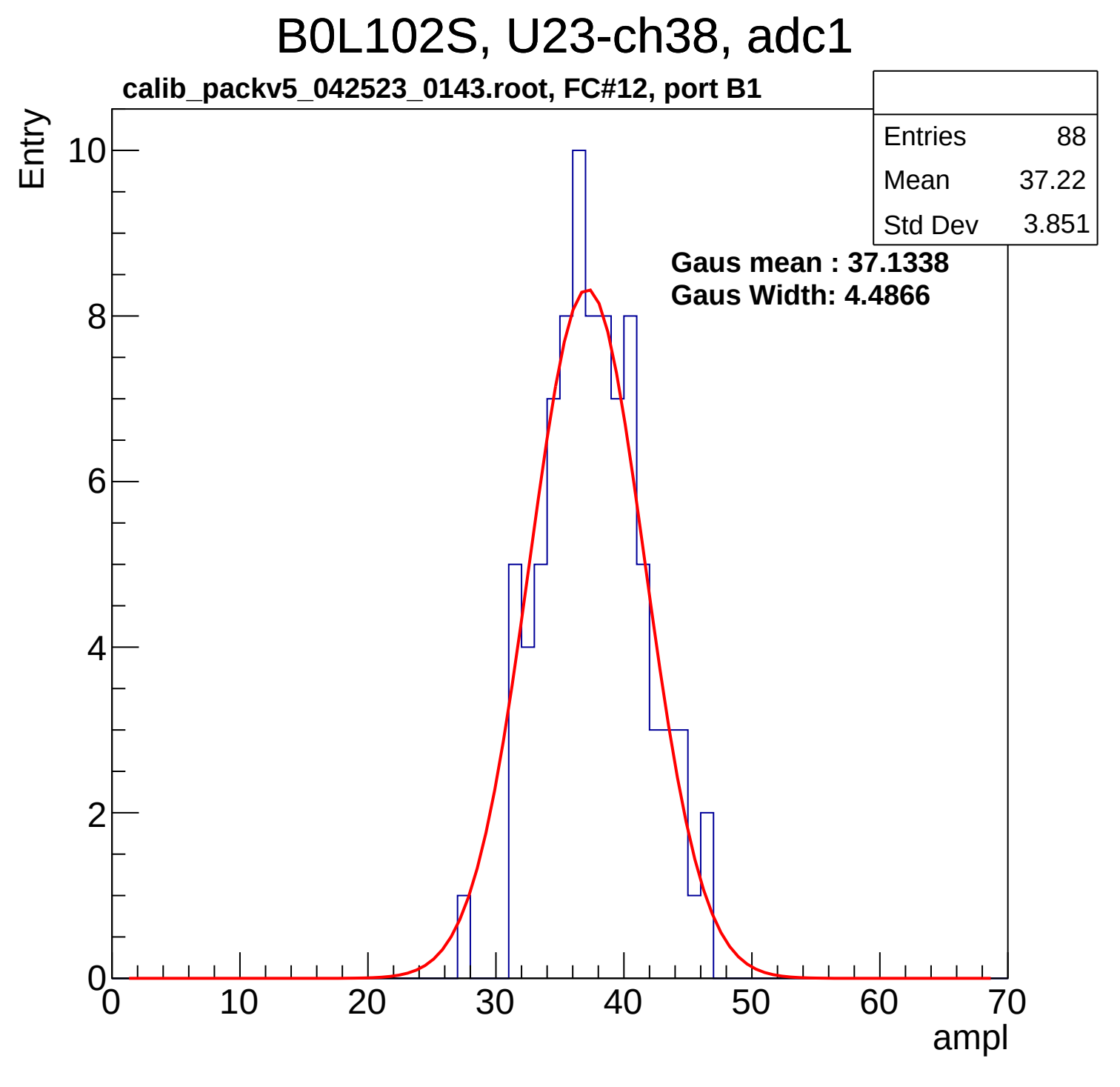
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch38, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 37.22
Std Dev 3.851
Gaus mean : 37.1338
Gaus Width: 4.4866
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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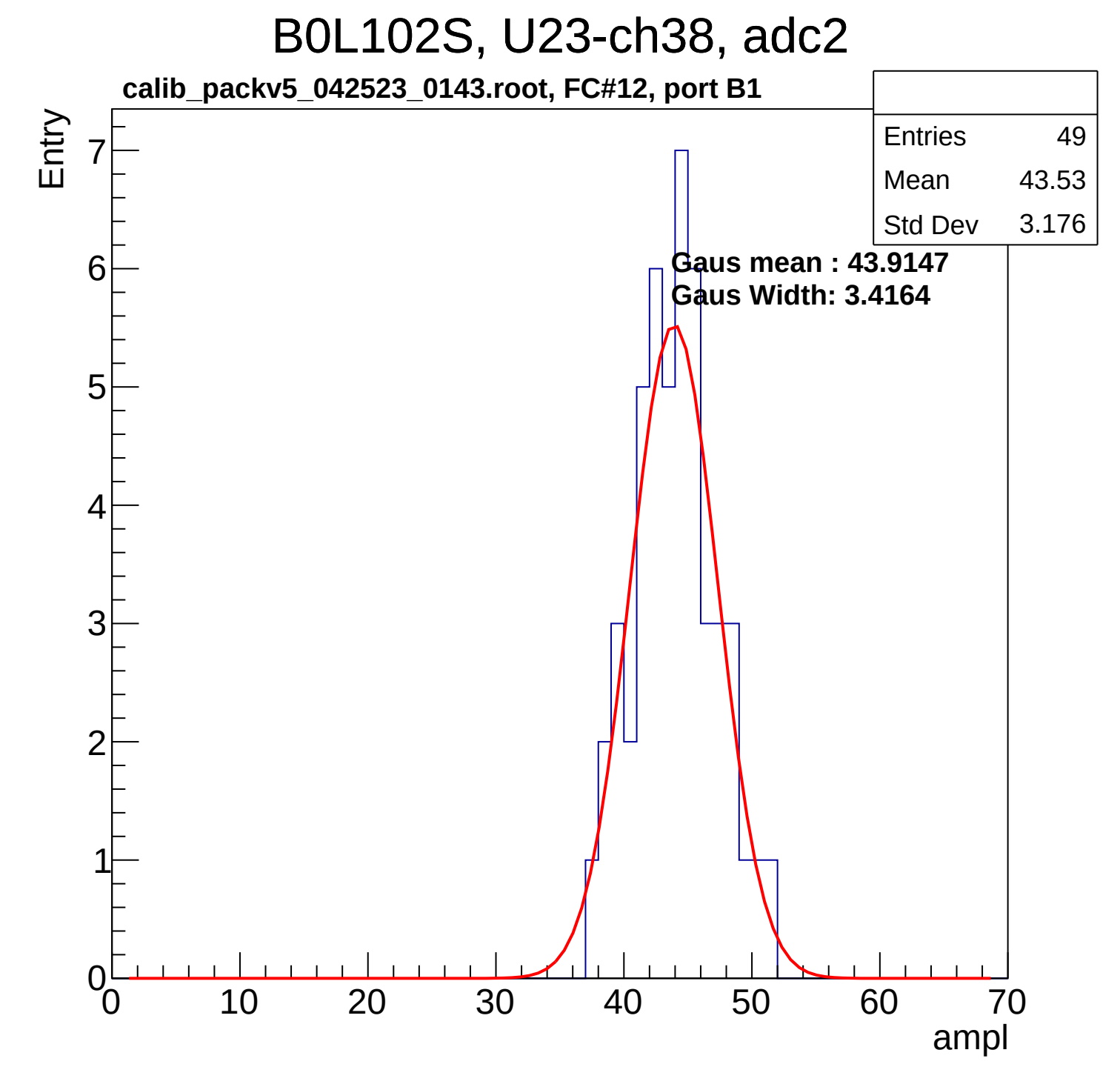
B0L102S, U23-ch38, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 43.53
Std Dev 3.176
Gaus mean : 43.9147
Gaus Width: 3.4164
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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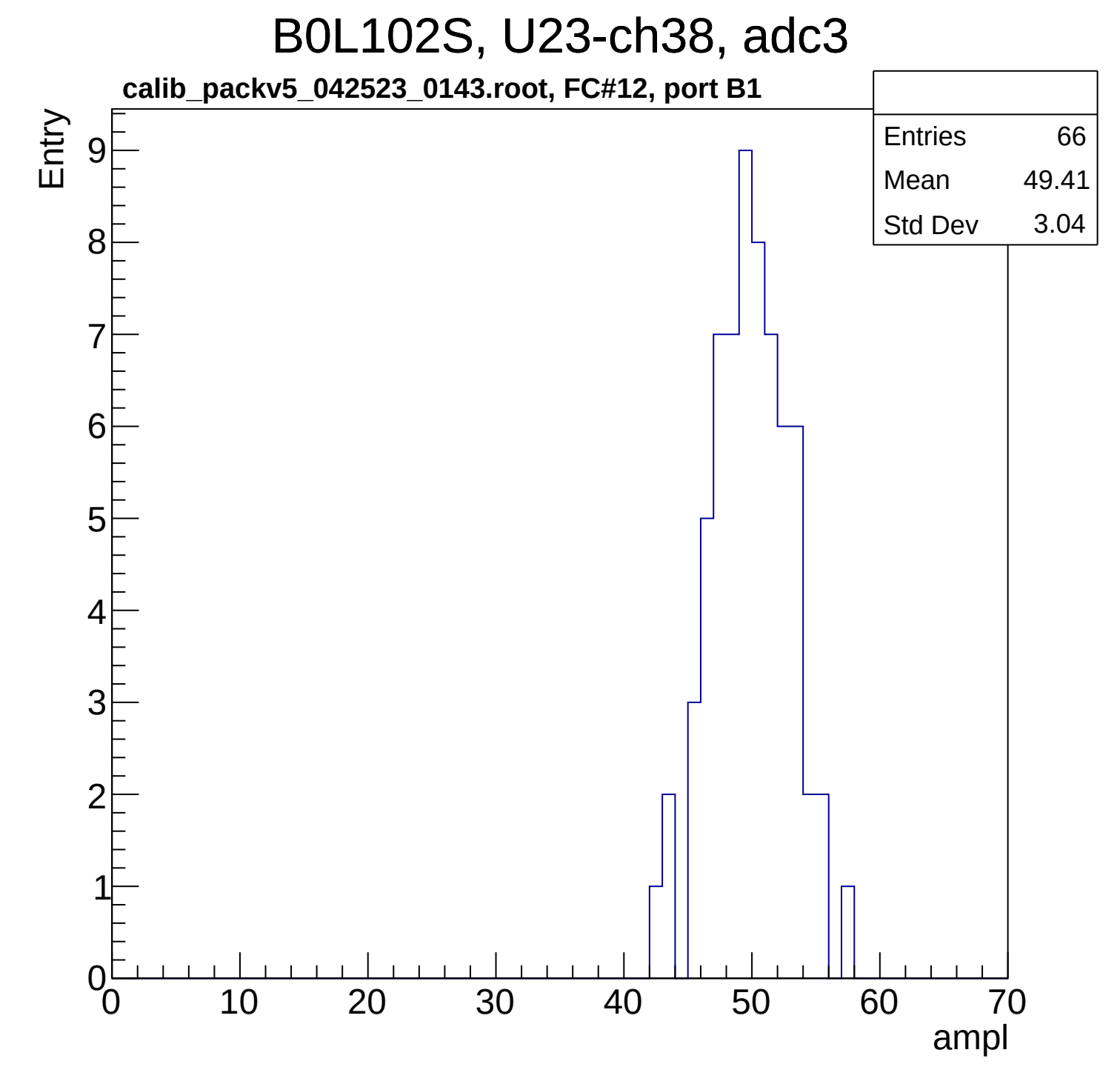
B0L102S, U23-ch38, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.41
Std Dev 3.04
Entry
ampl
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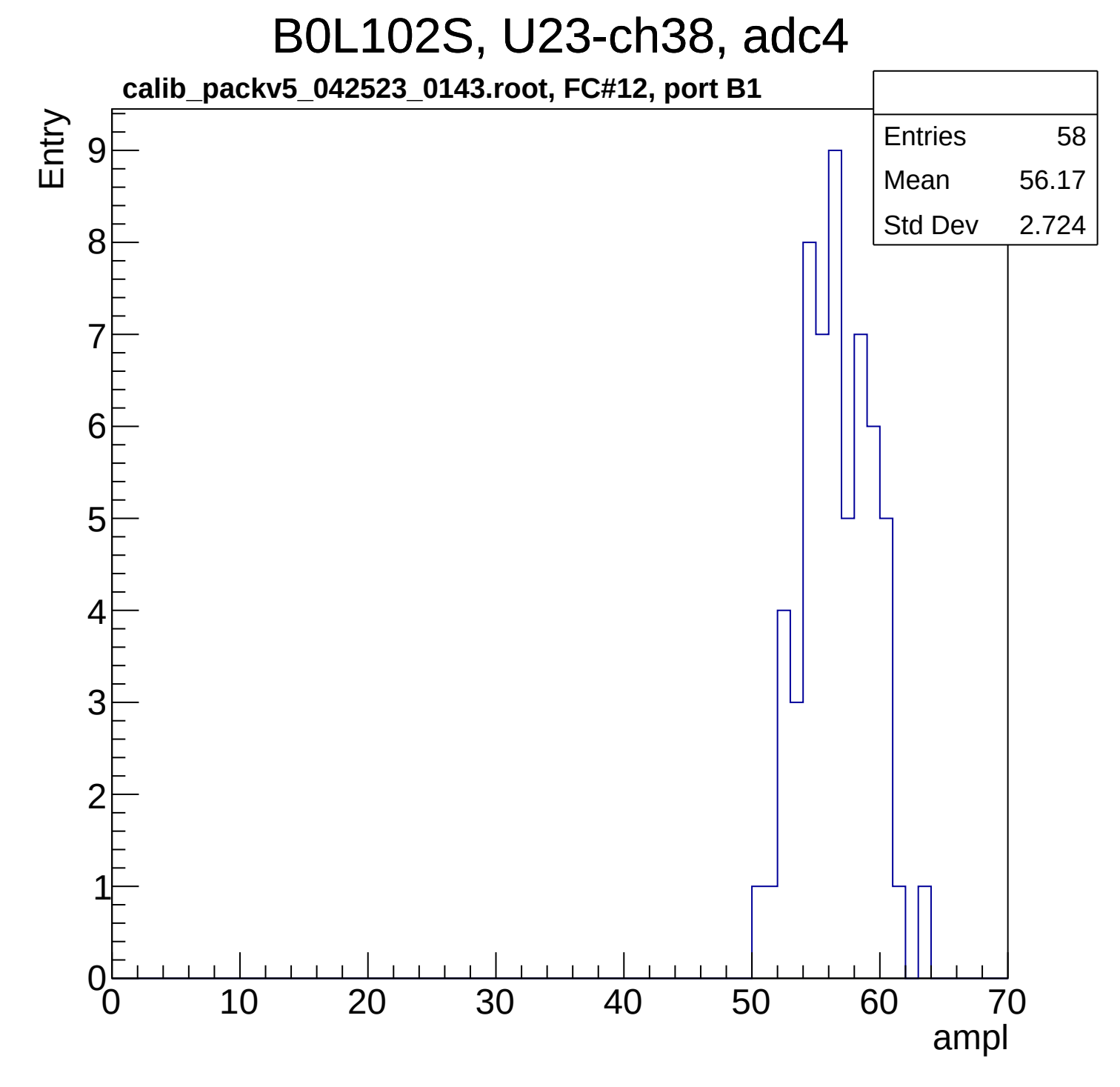
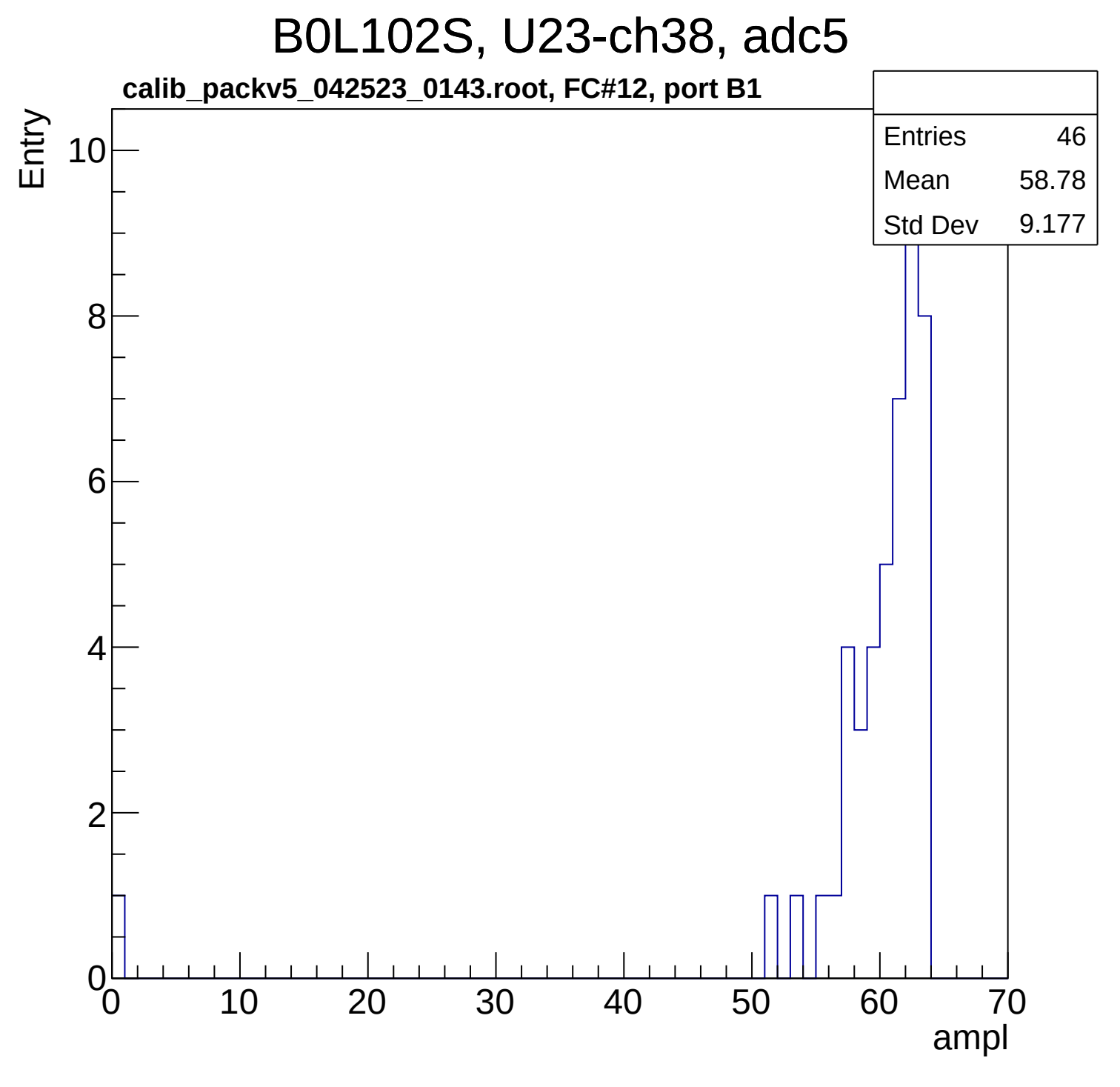
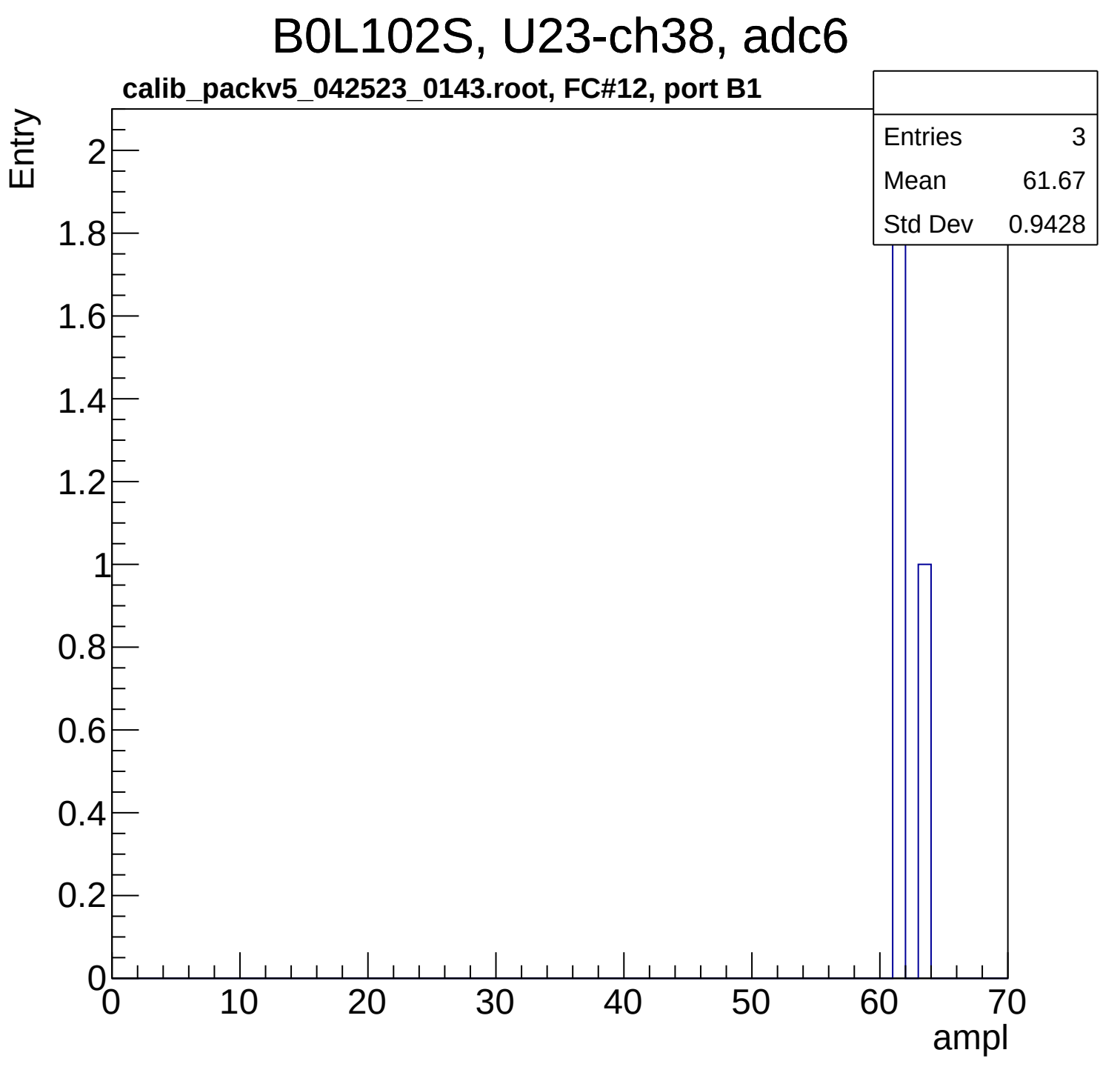

B0L102S, U23-ch38, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 56.17
Std Dev 2.724
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch38, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 58.78
Std Dev 9.177
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch38, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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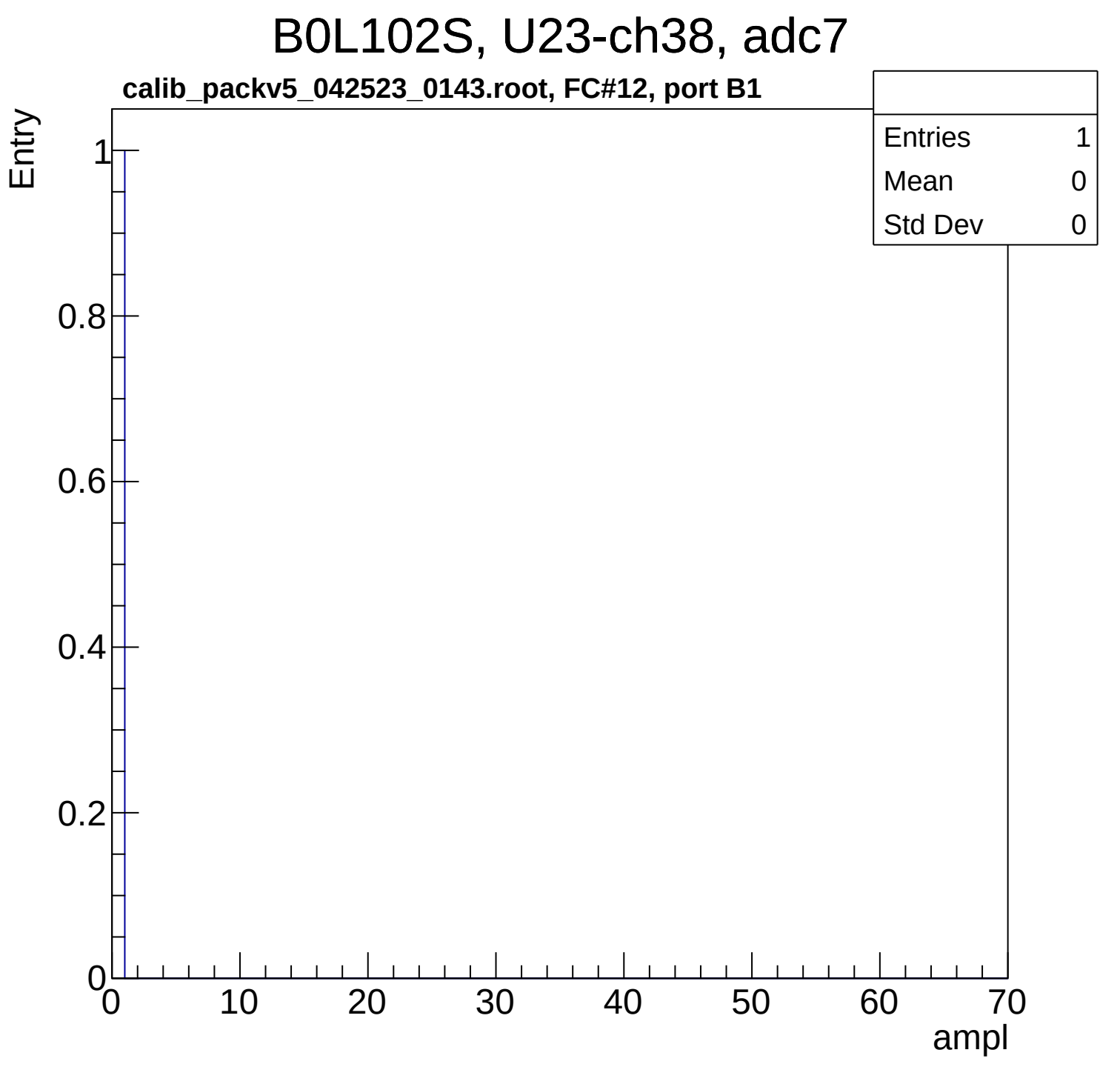
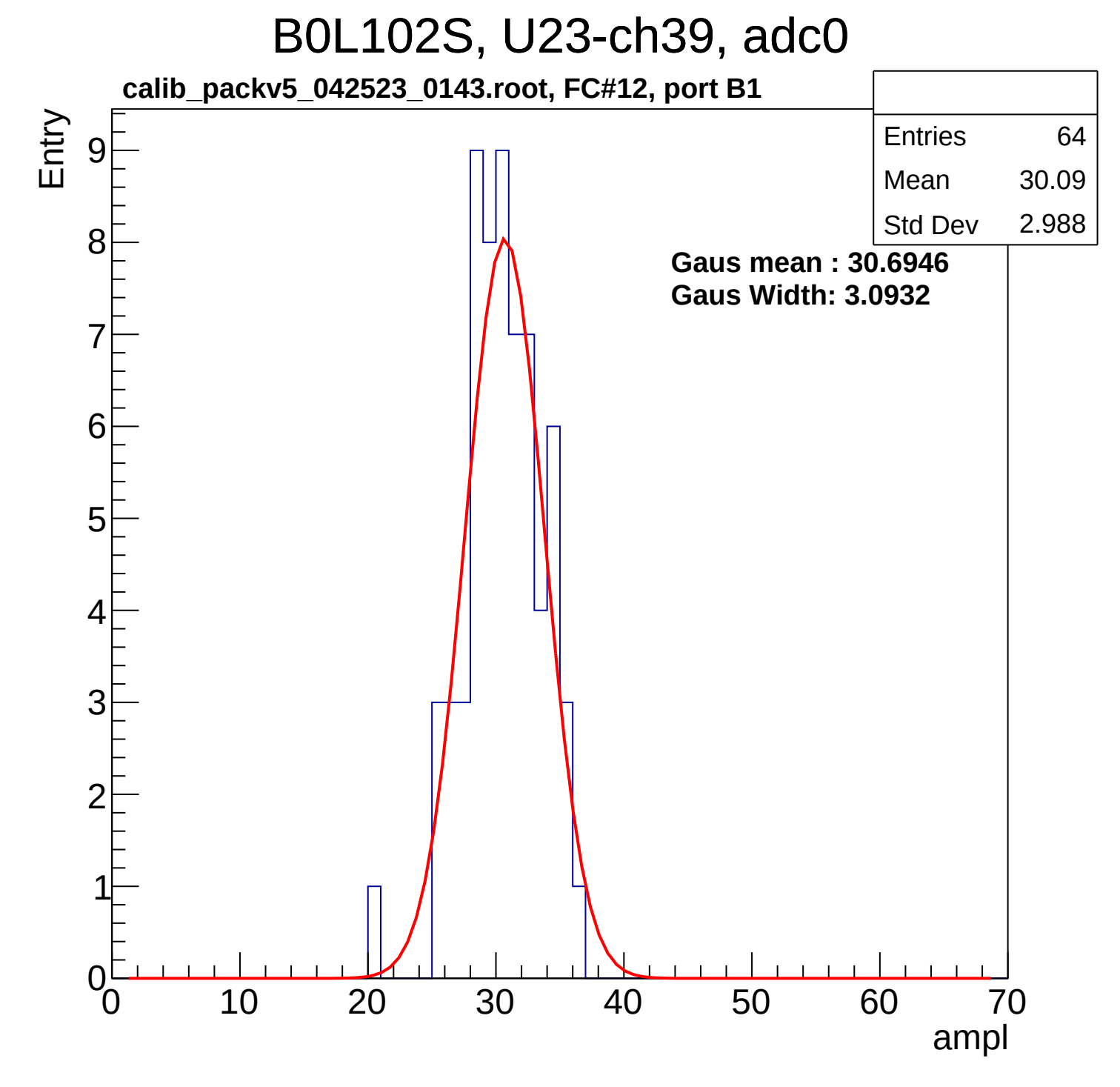
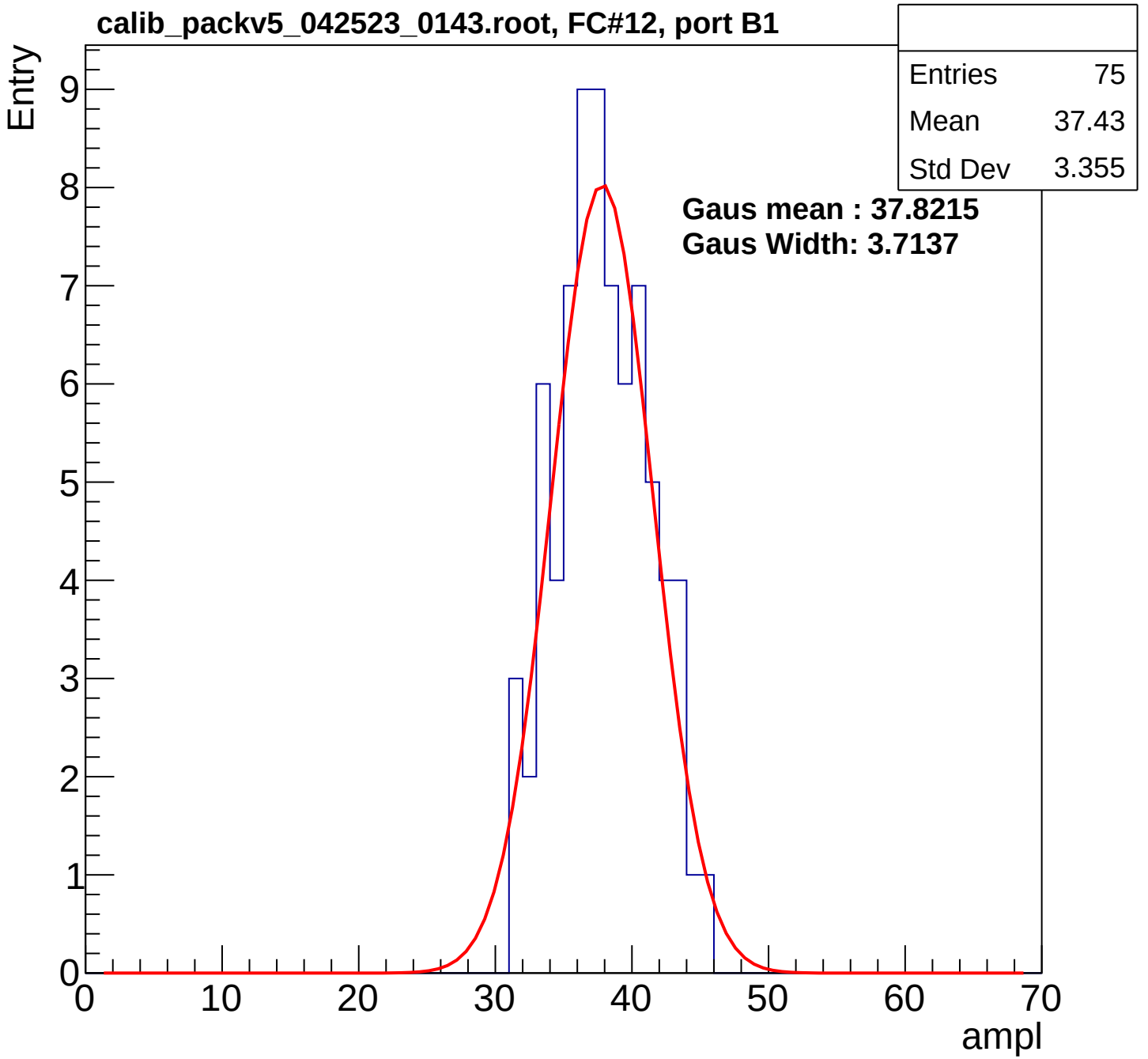
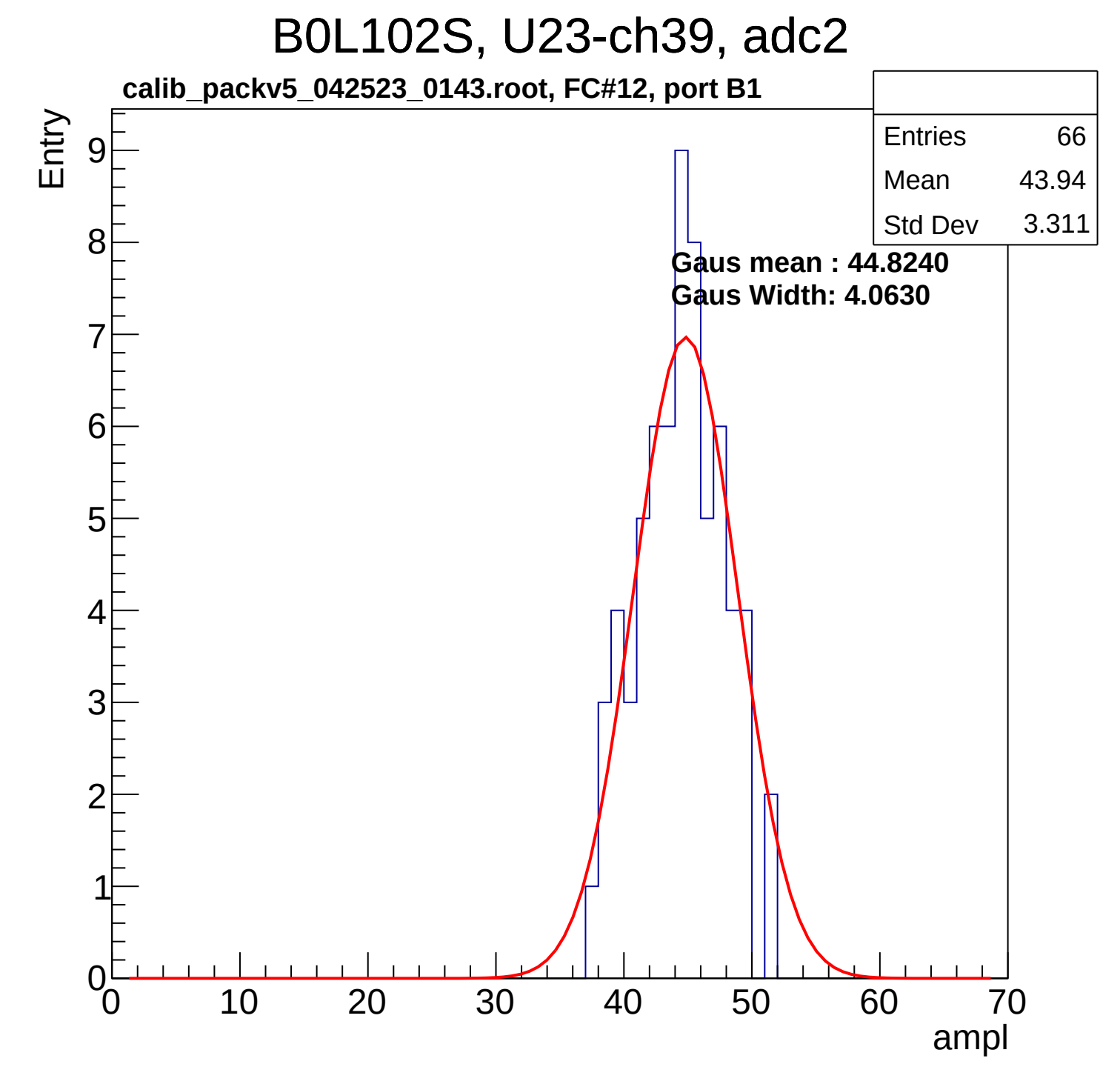
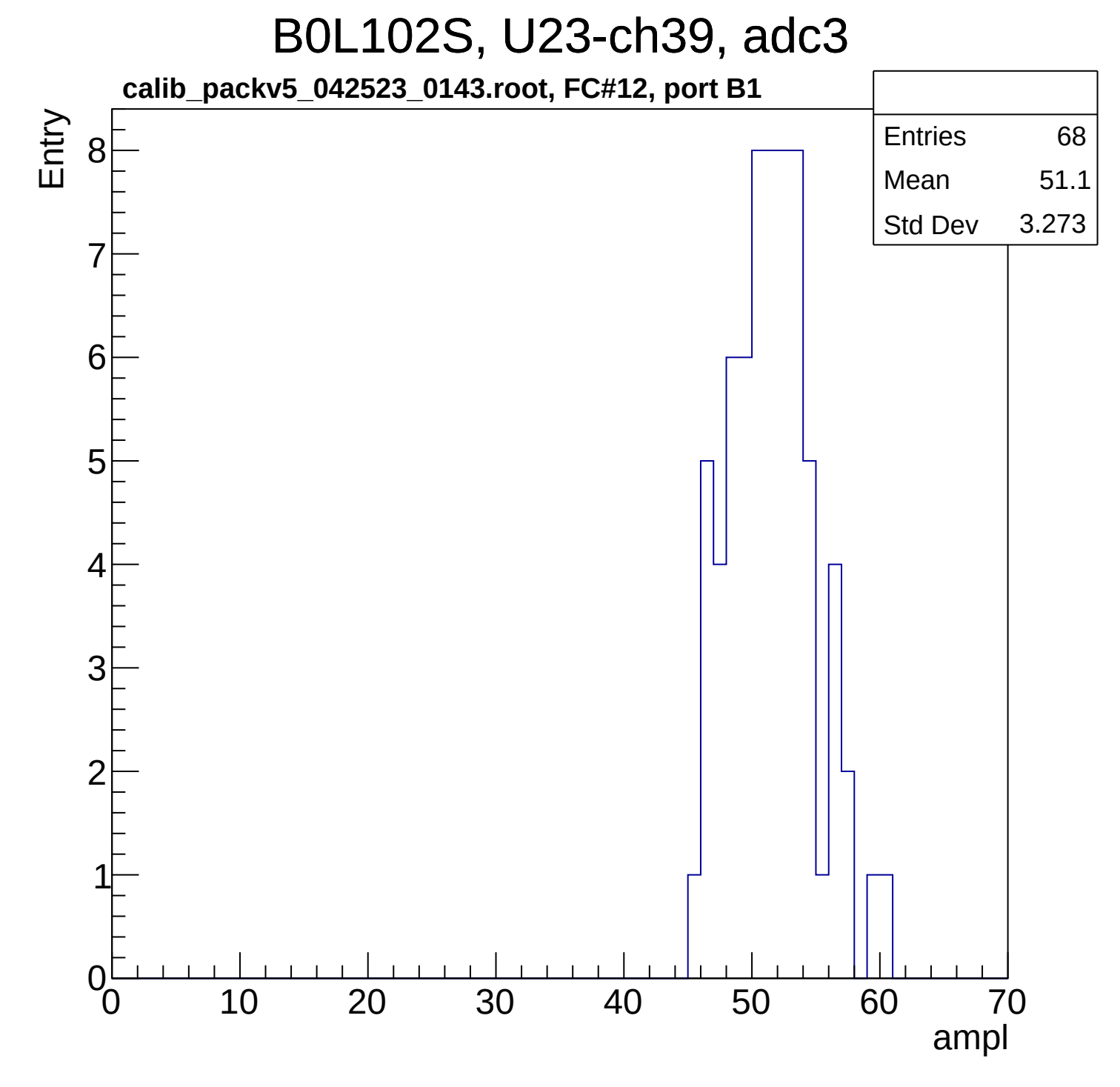
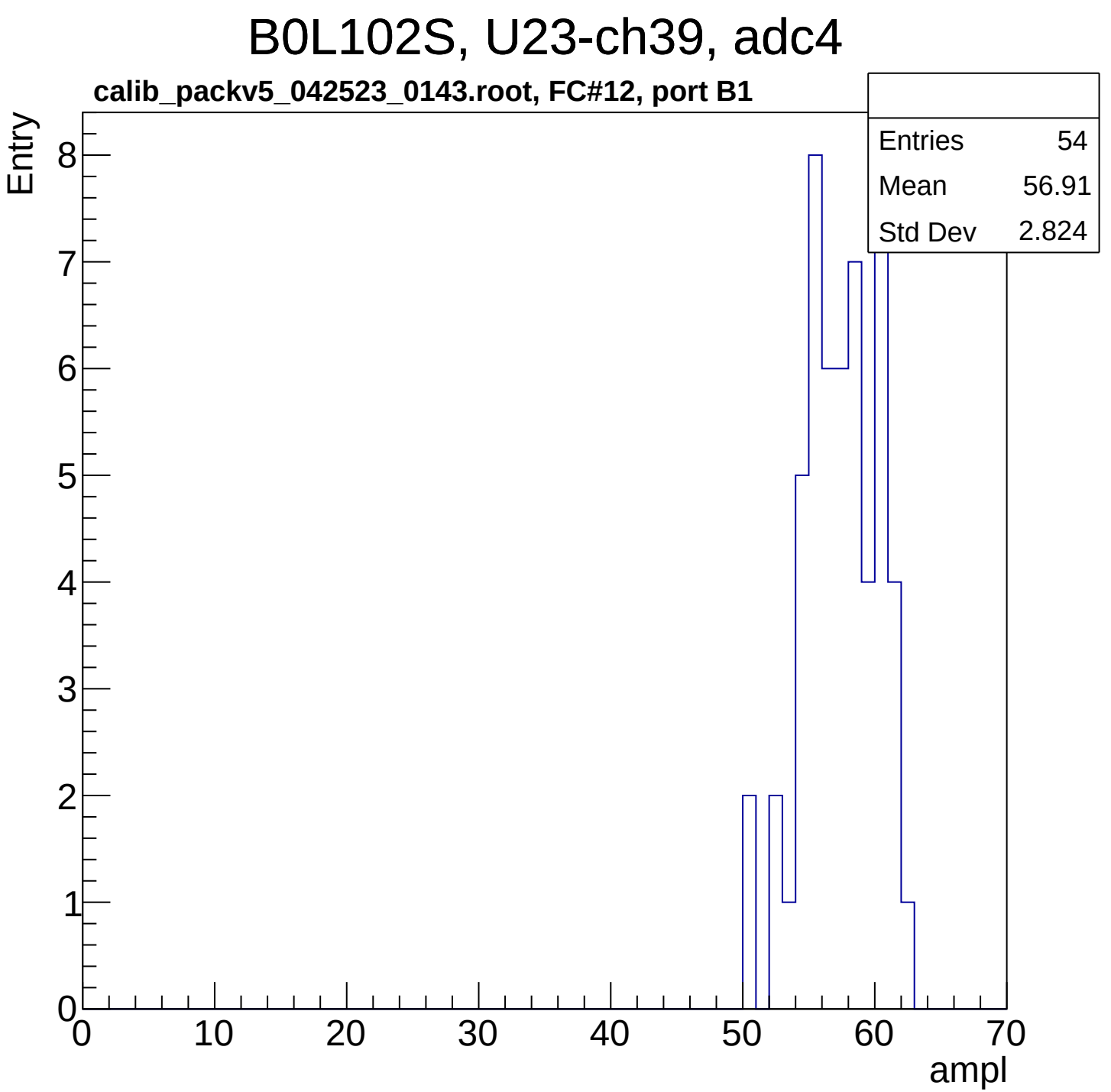

B0L102S, U23-ch38, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

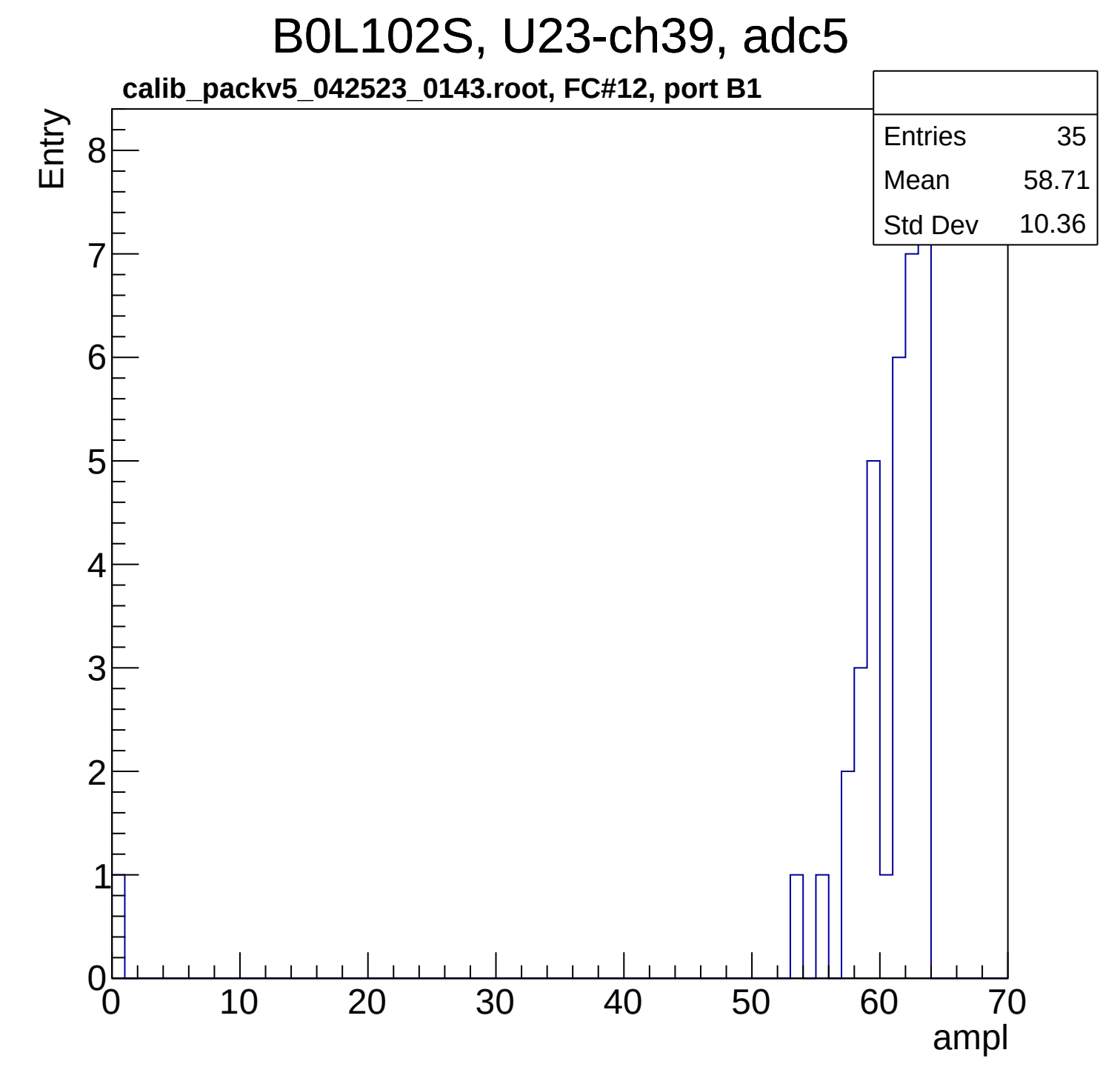

B0L102S, U23-ch39, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 30.09
Std Dev 2.988
Gaus mean : 30.6946
Gaus Width: 3.0932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

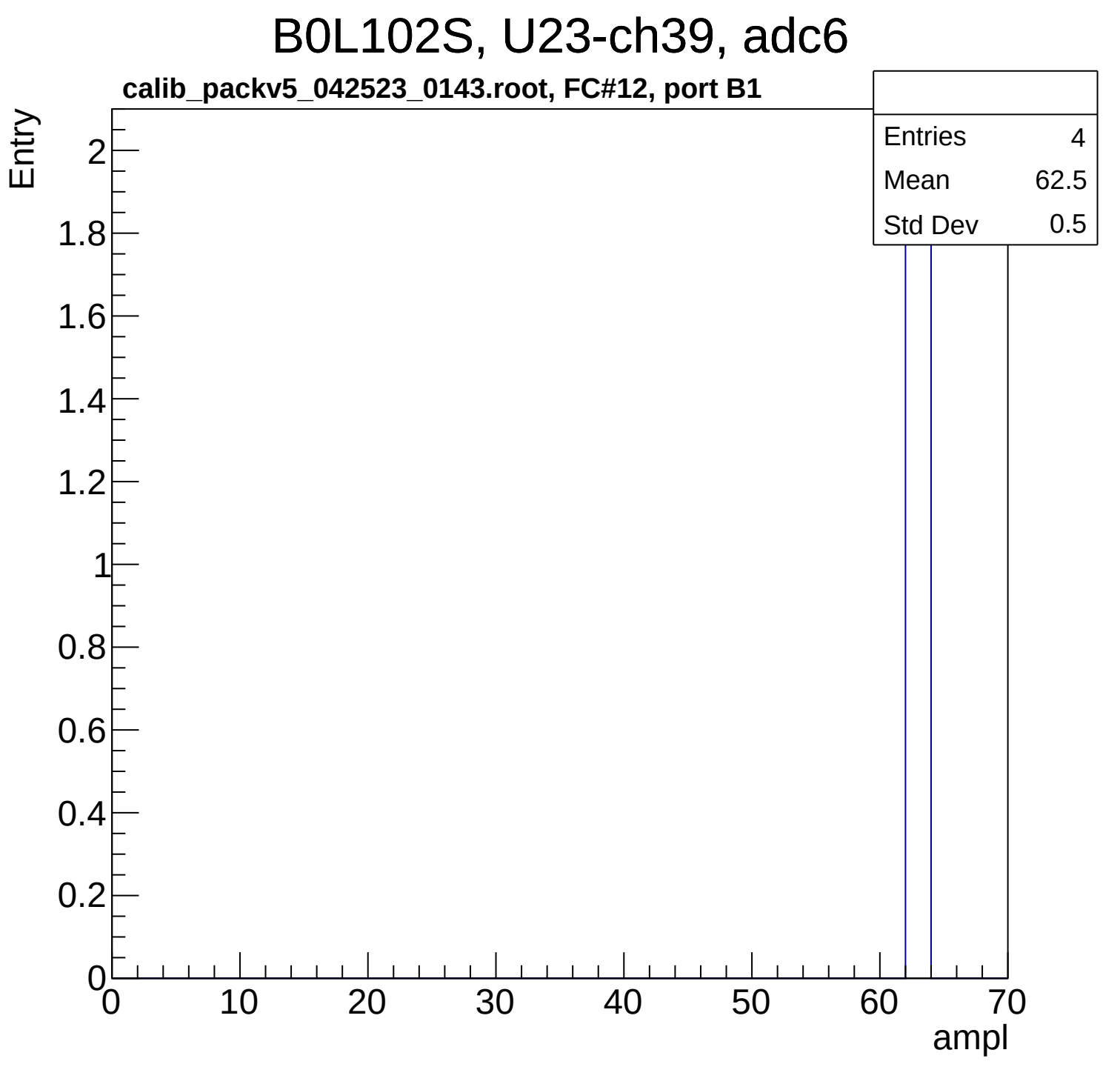

B0L102S, U23-ch39, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.43
Std Dev 3.355
Gaus mean : 37.8215
Gaus Width: 3.7137
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

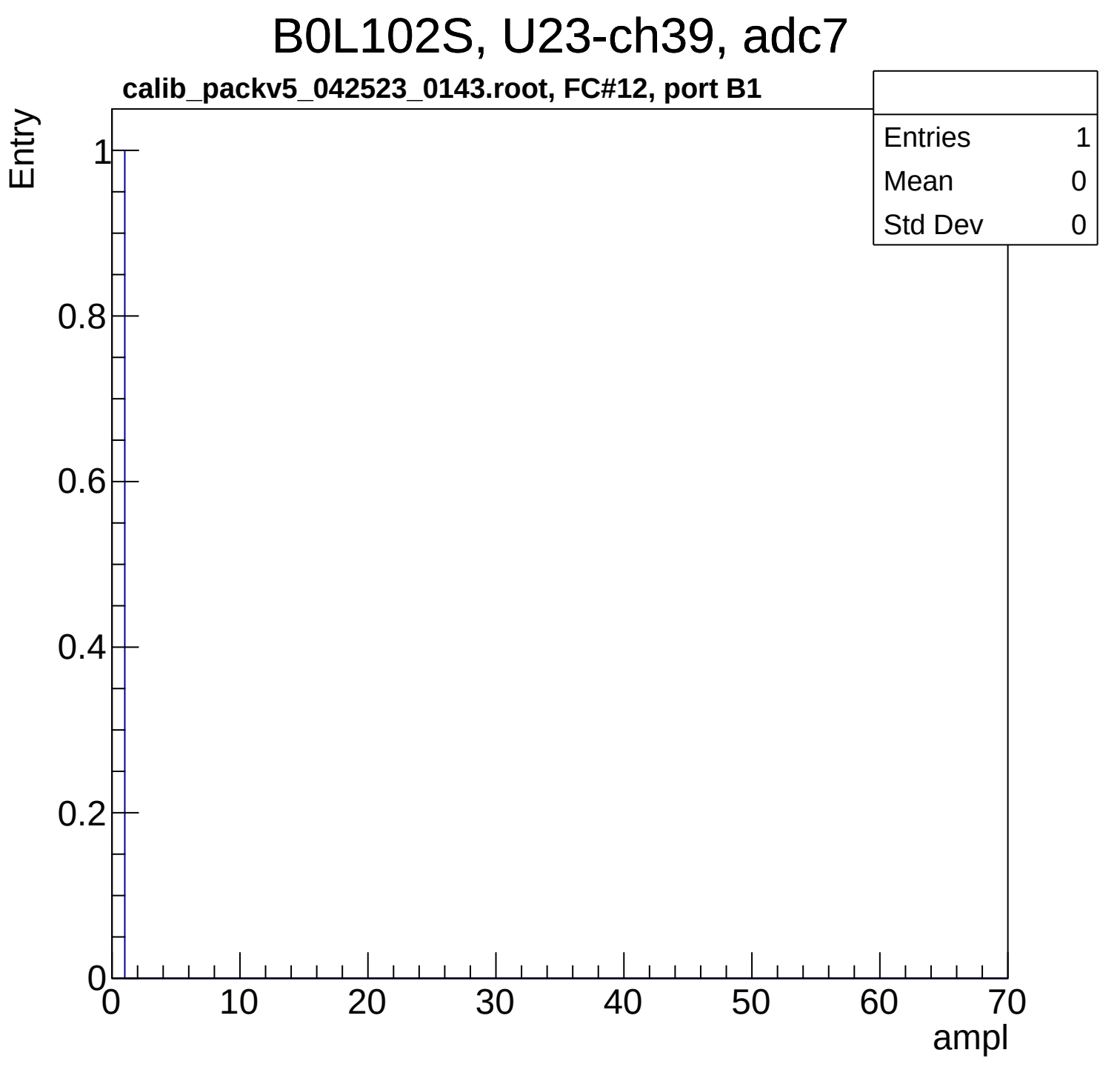

B0L102S, U23-ch39, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.94
Std Dev 3.311
Gaus mean : 44.8240
Gaus Width: 4.0630
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

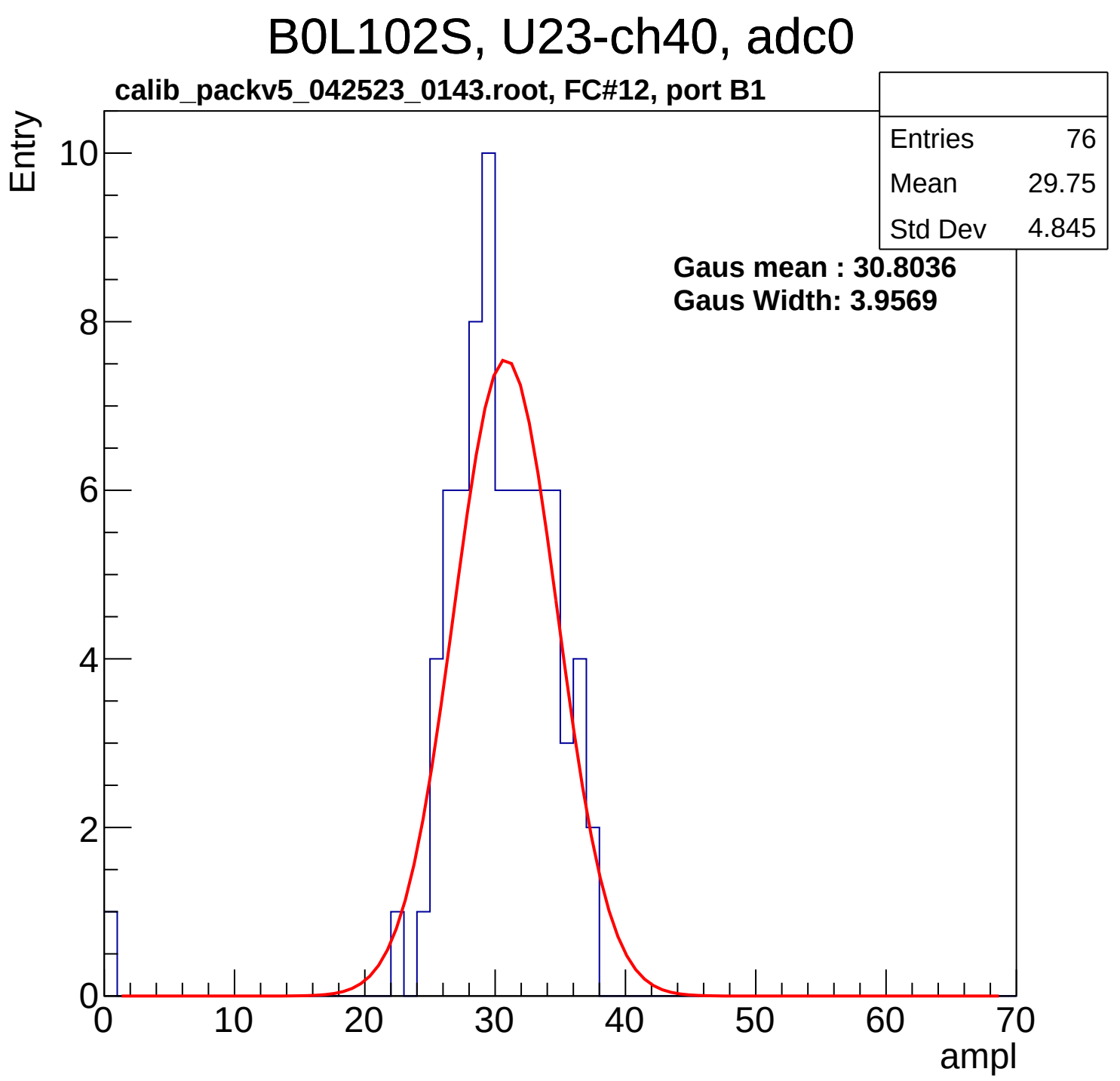

B0L102S, U23-ch39, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 51.1
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

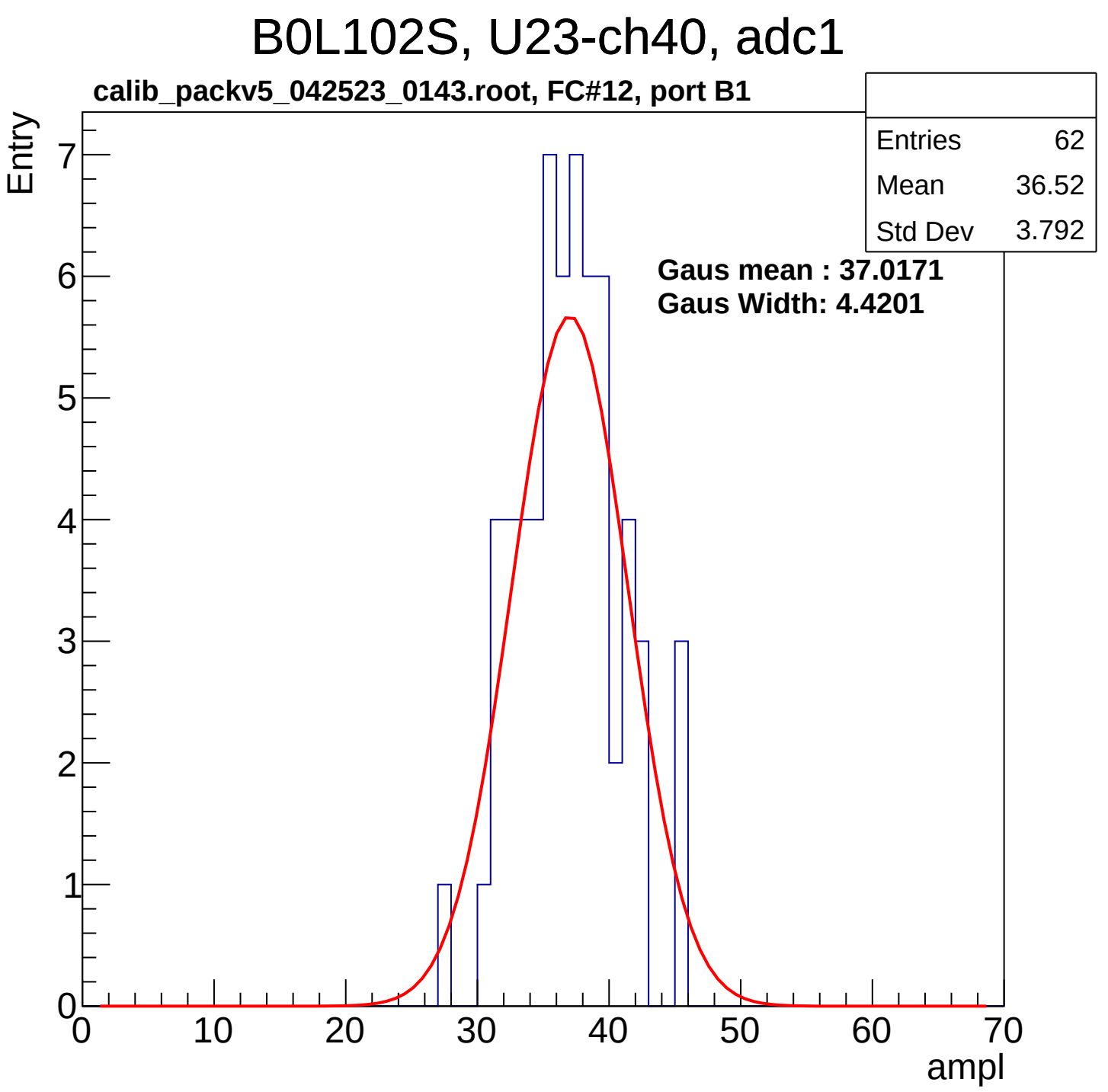

B0L102S, U23-ch39, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 56.91
Std Dev 2.824
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

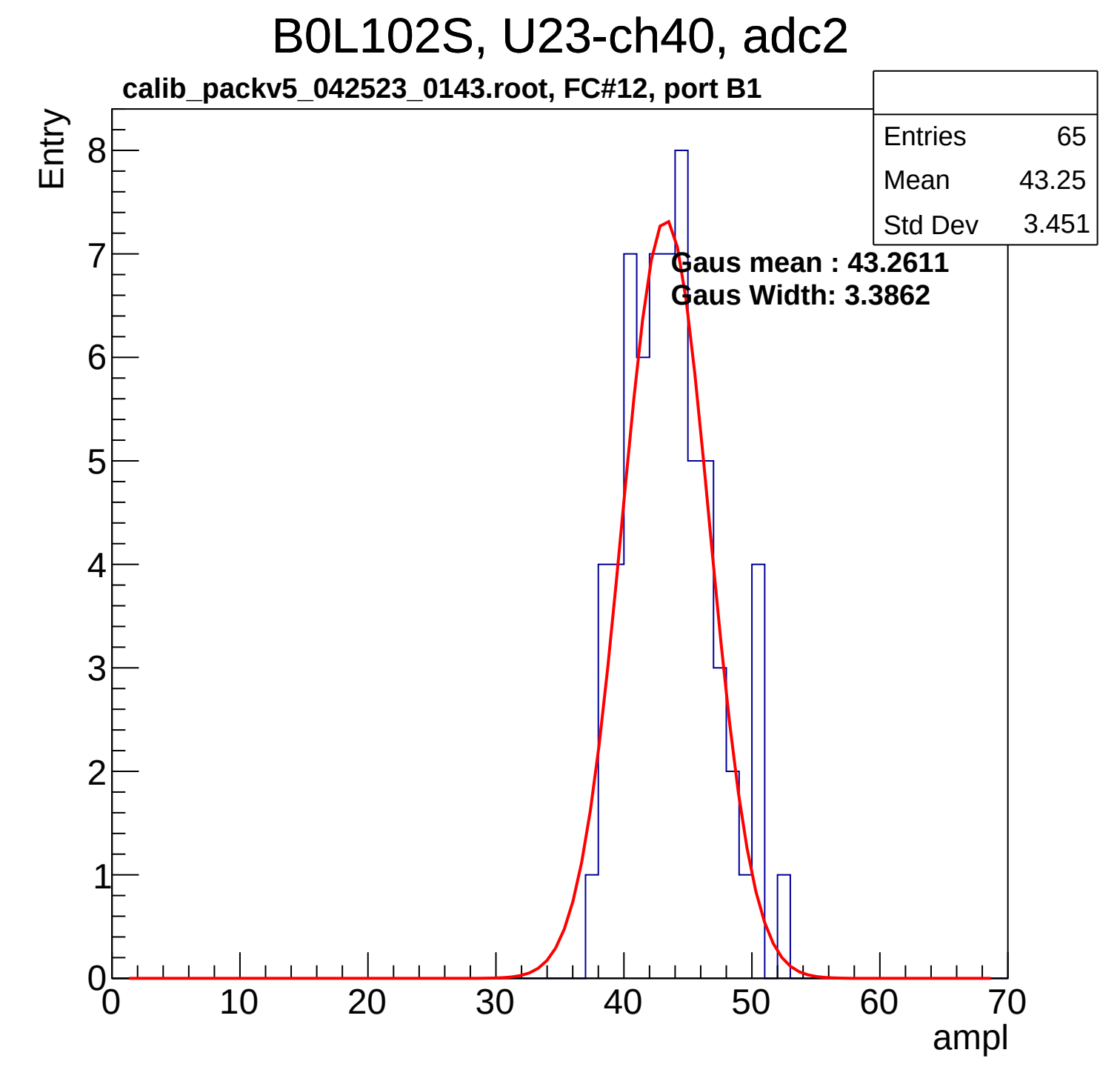

B0L102S, U23-ch39, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 58.71
Std Dev 10.36
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch39, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch39, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch40, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.75
Std Dev 4.845
Gaus mean : 30.8036
Gaus Width: 3.9569
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch40, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 36.52
Std Dev 3.792
Gaus mean : 37.0171
Gaus Width: 4.4201
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch40, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 43.25
Std Dev 3.451
Gaus mean : 43.2611
Gaus Width: 3.3862
Entry
ampl
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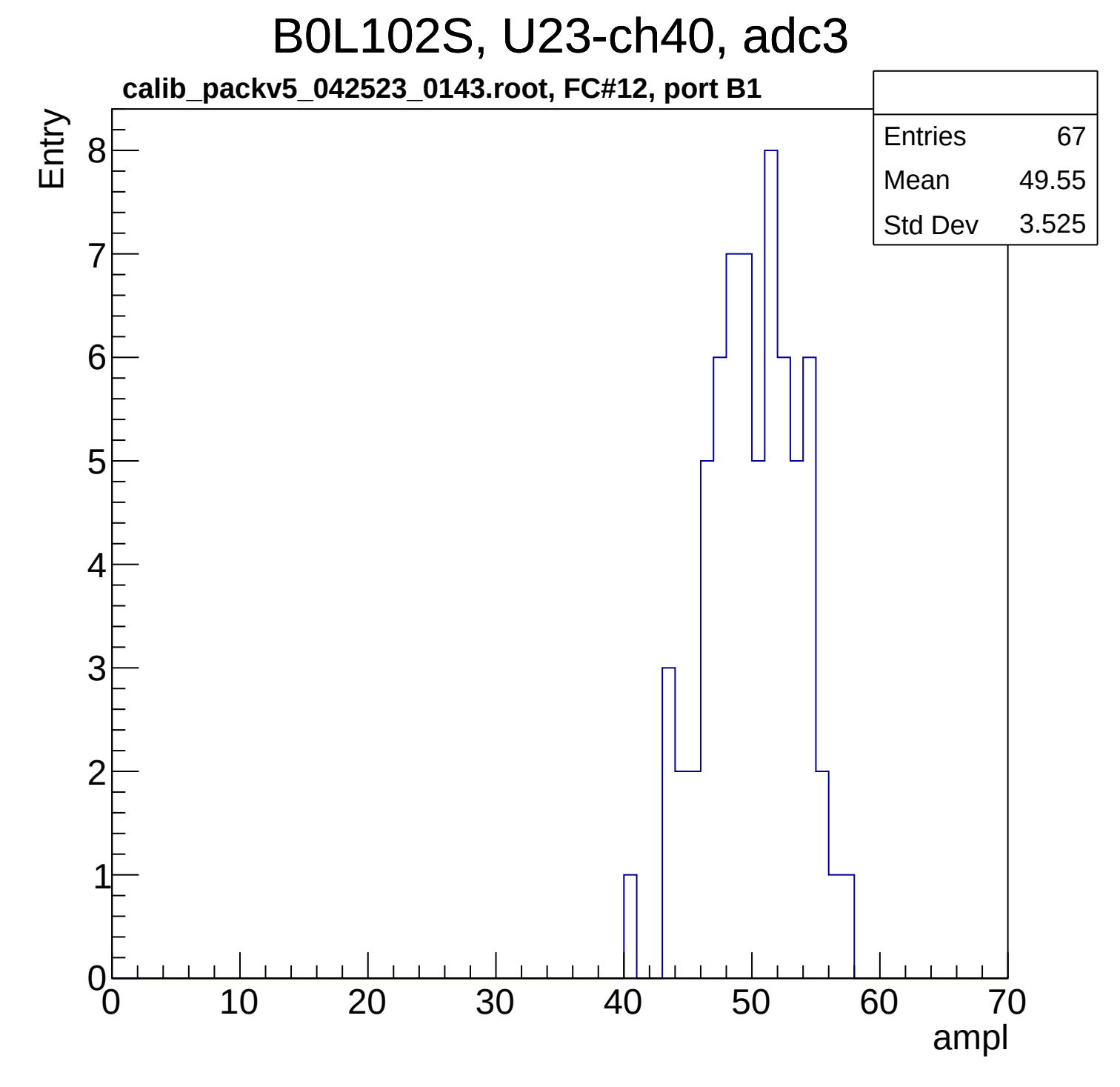
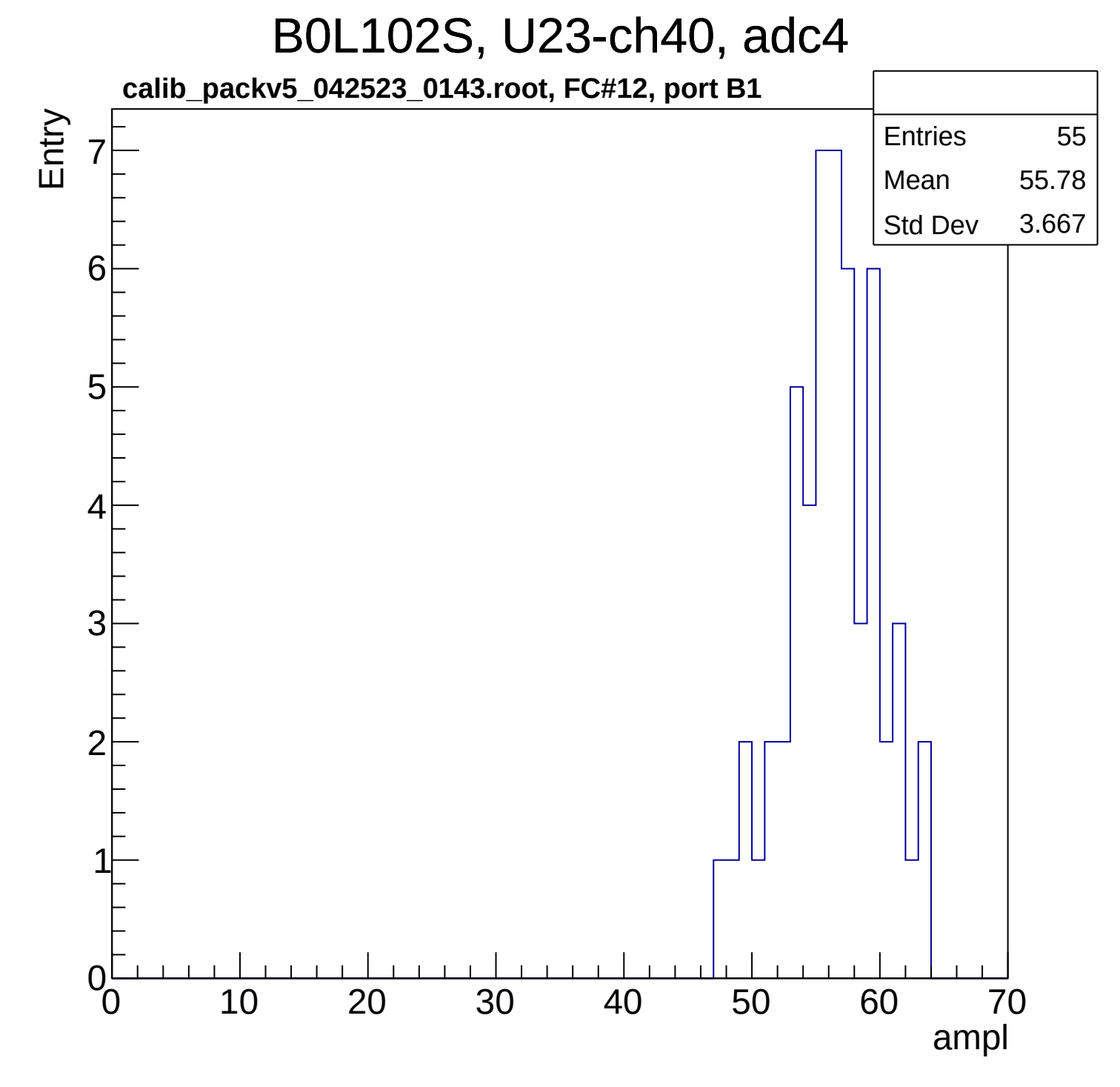
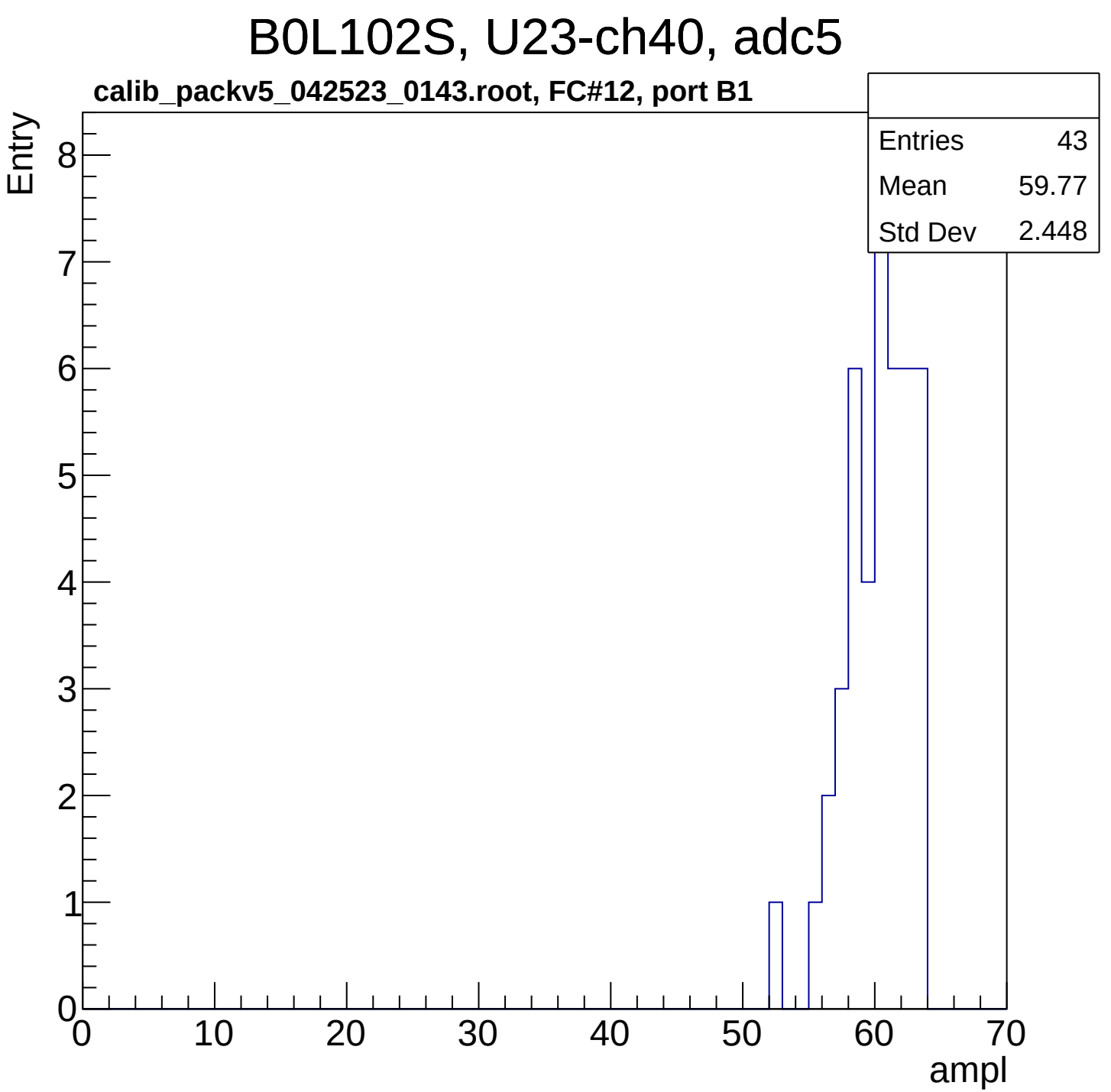
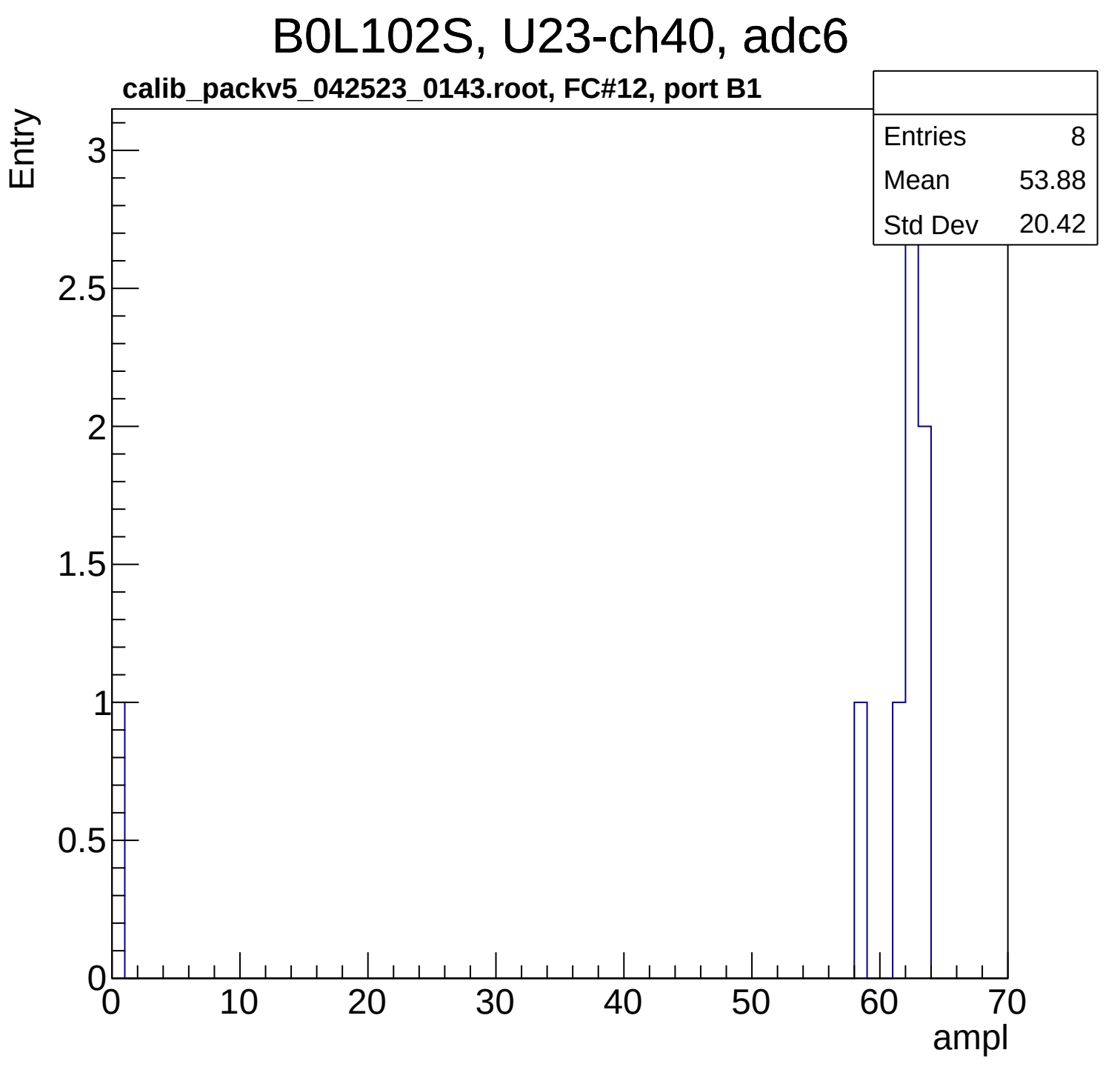
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch40, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 49.55
Std Dev 3.525
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch40, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.78
Std Dev 3.667
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch40, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 59.77
Std Dev 2.448
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch40, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 53.88
Std Dev 20.42
Entry
ampl
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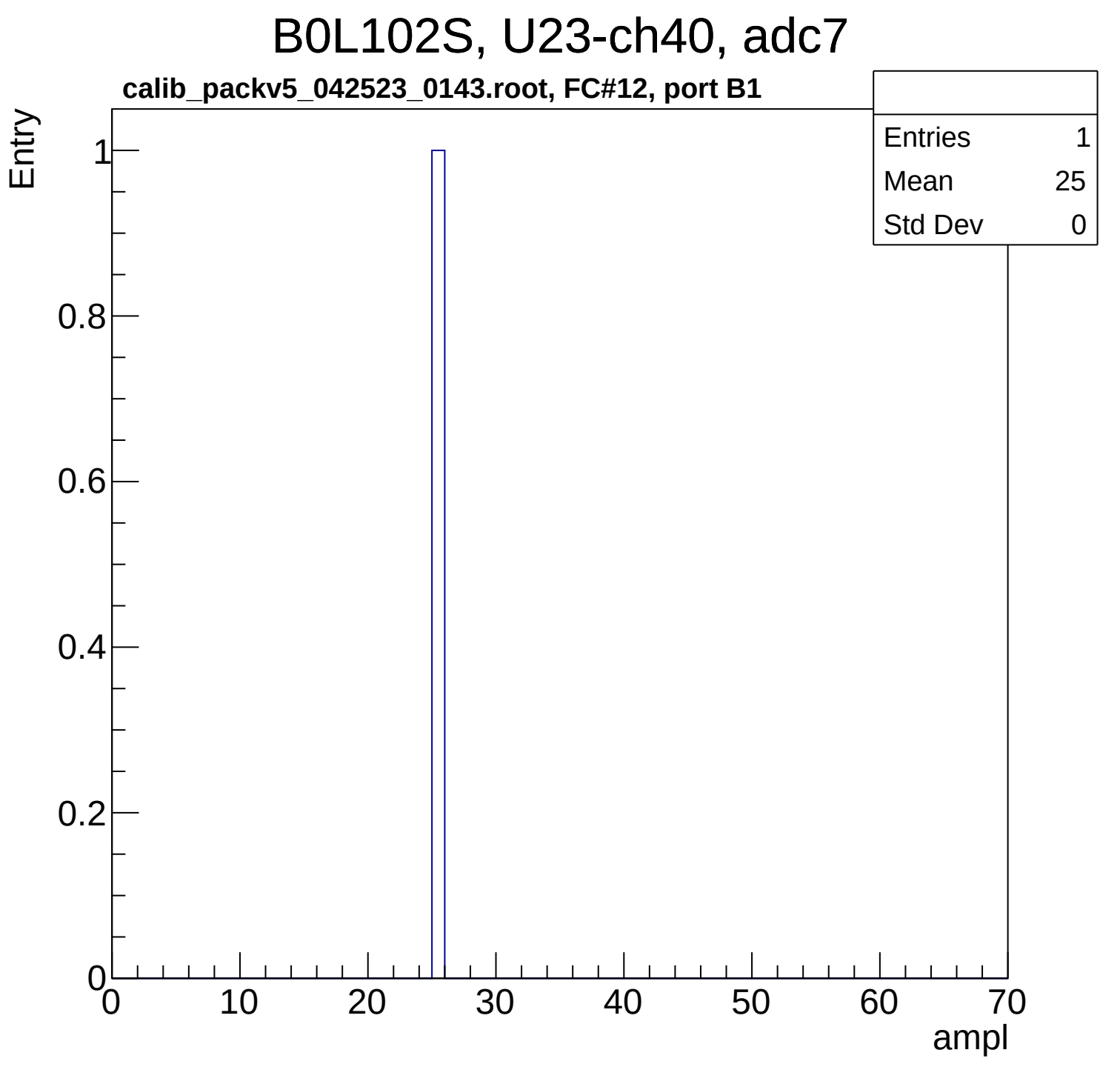
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch40, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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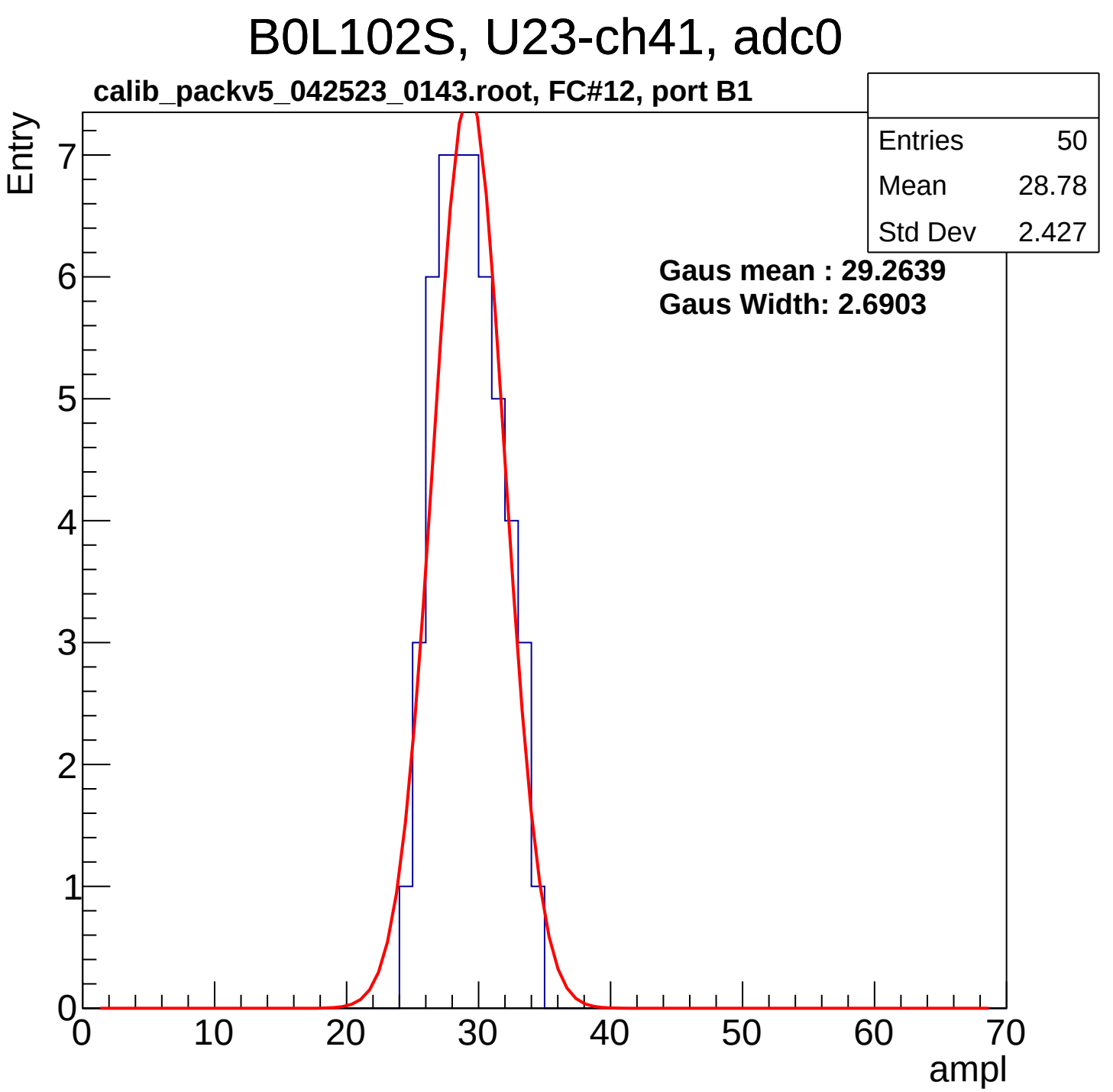
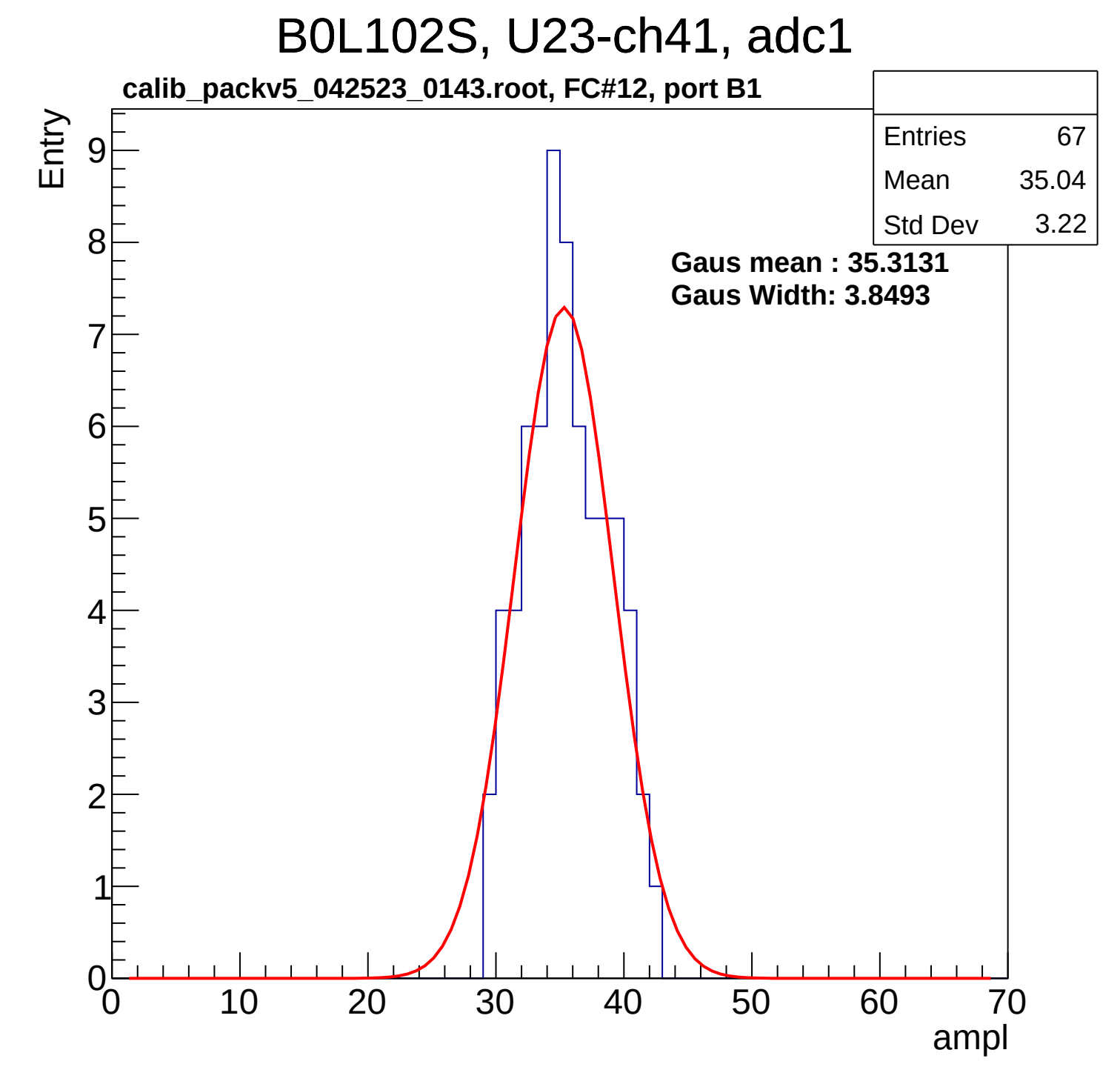
30
40
50
60
70
ampl


B0L102S, U23-ch41, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 28.78
Std Dev 2.427
Gaus mean : 29.2639
Gaus Width: 2.6903
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch41, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.04
Std Dev 3.22
Gaus mean : 35.3131
Gaus Width: 3.8493
Entry
ampl
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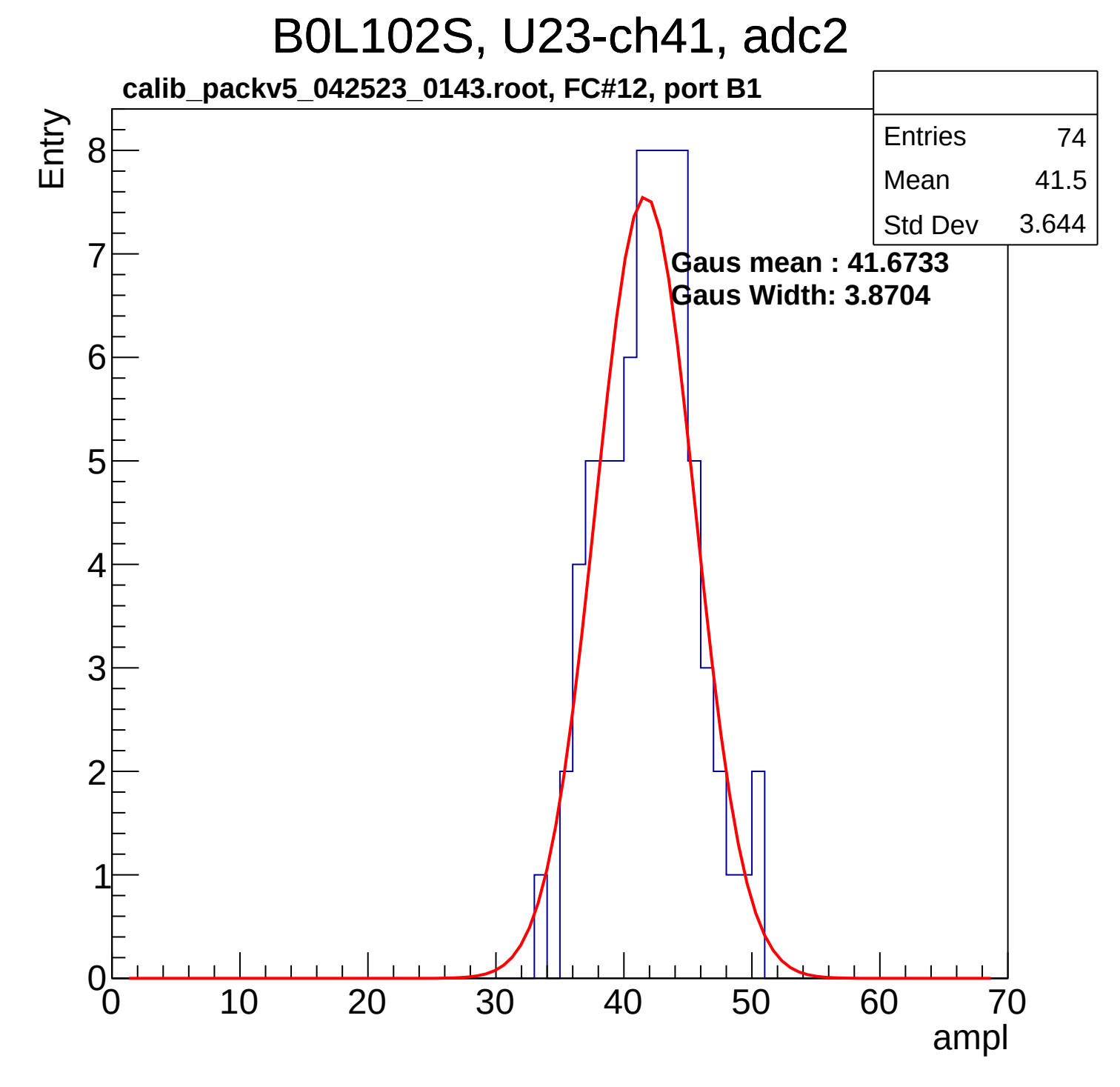
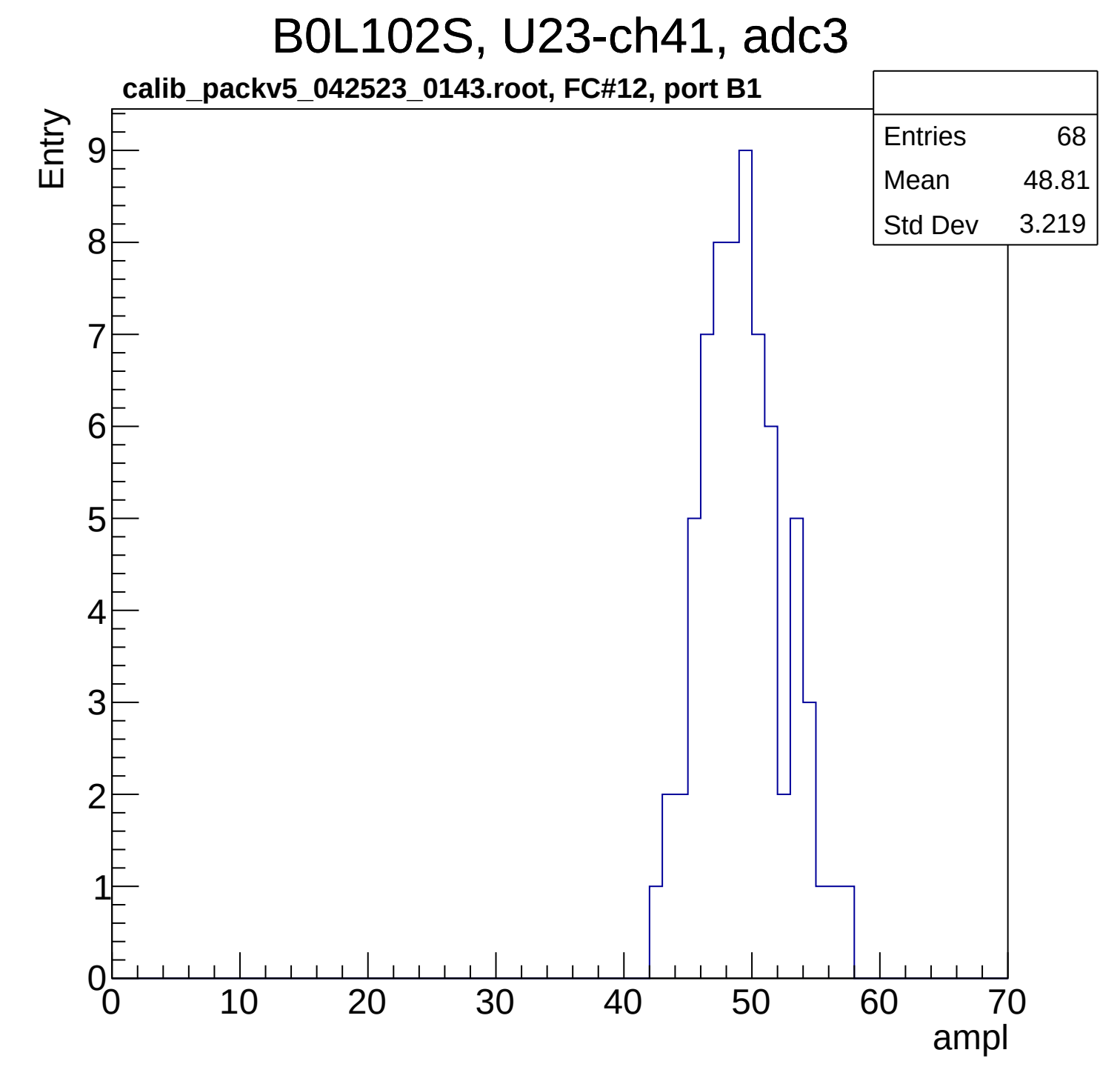
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch41, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 41.5
Std Dev 3.644
Gaus mean : 41.6733
Gaus Width: 3.8704
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch41, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.81
Std Dev 3.219
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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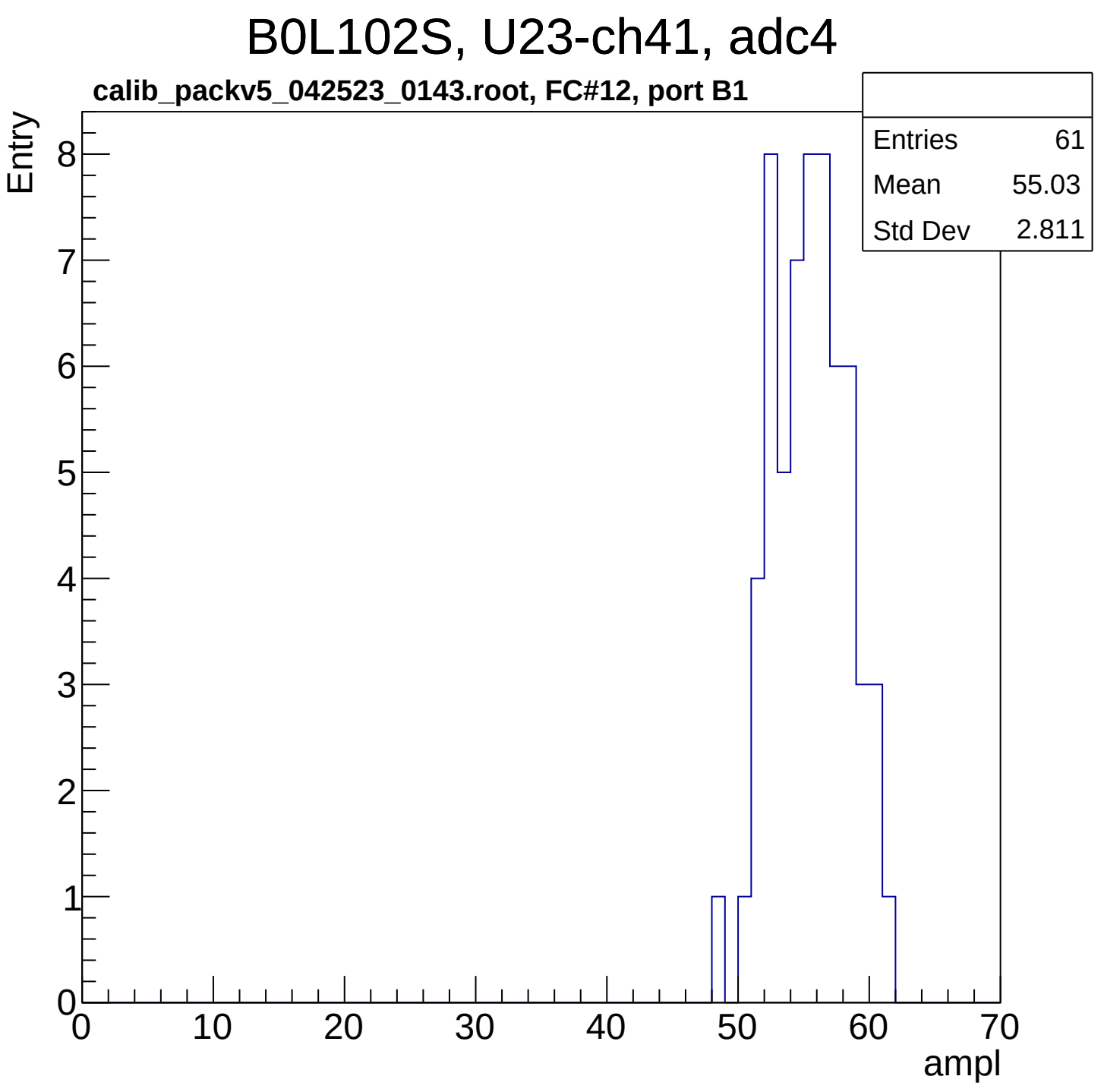
B0L102S, U23-ch41, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.03
Std Dev 2.811
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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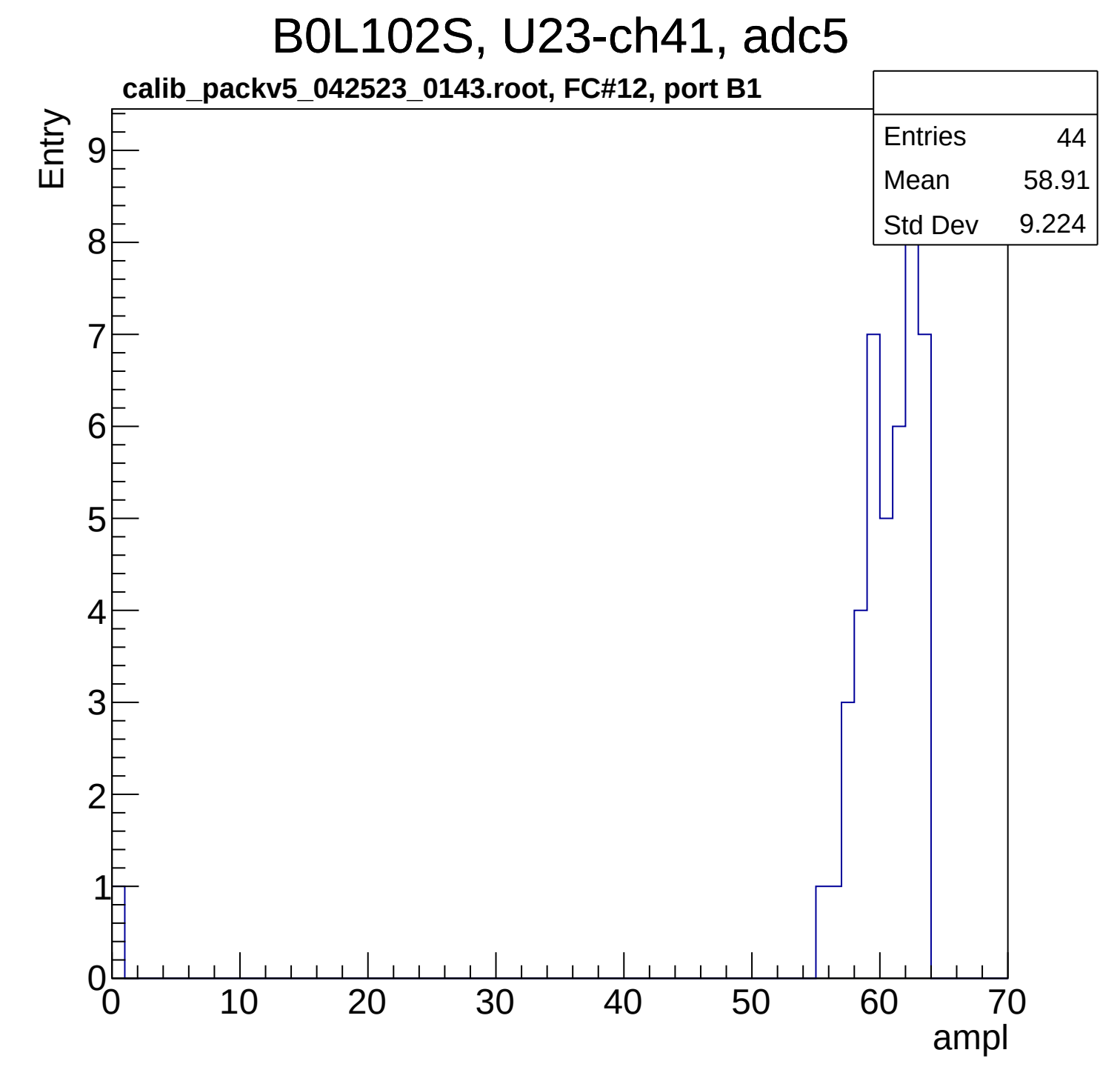
B0L102S, U23-ch41, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.91
Std Dev 9.224
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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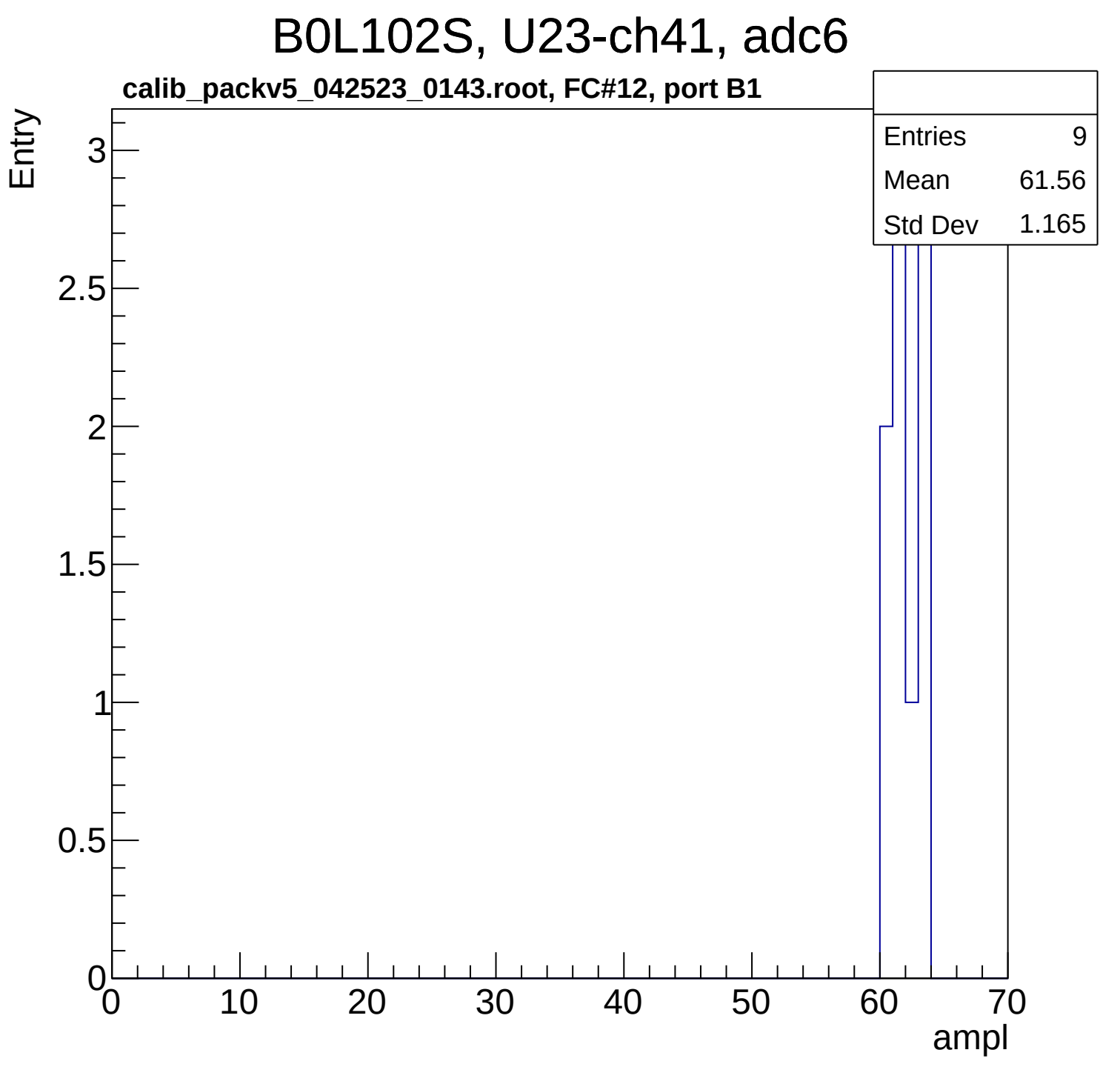
B0L102S, U23-ch41, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.56
Std Dev 1.165
Entry
ampl
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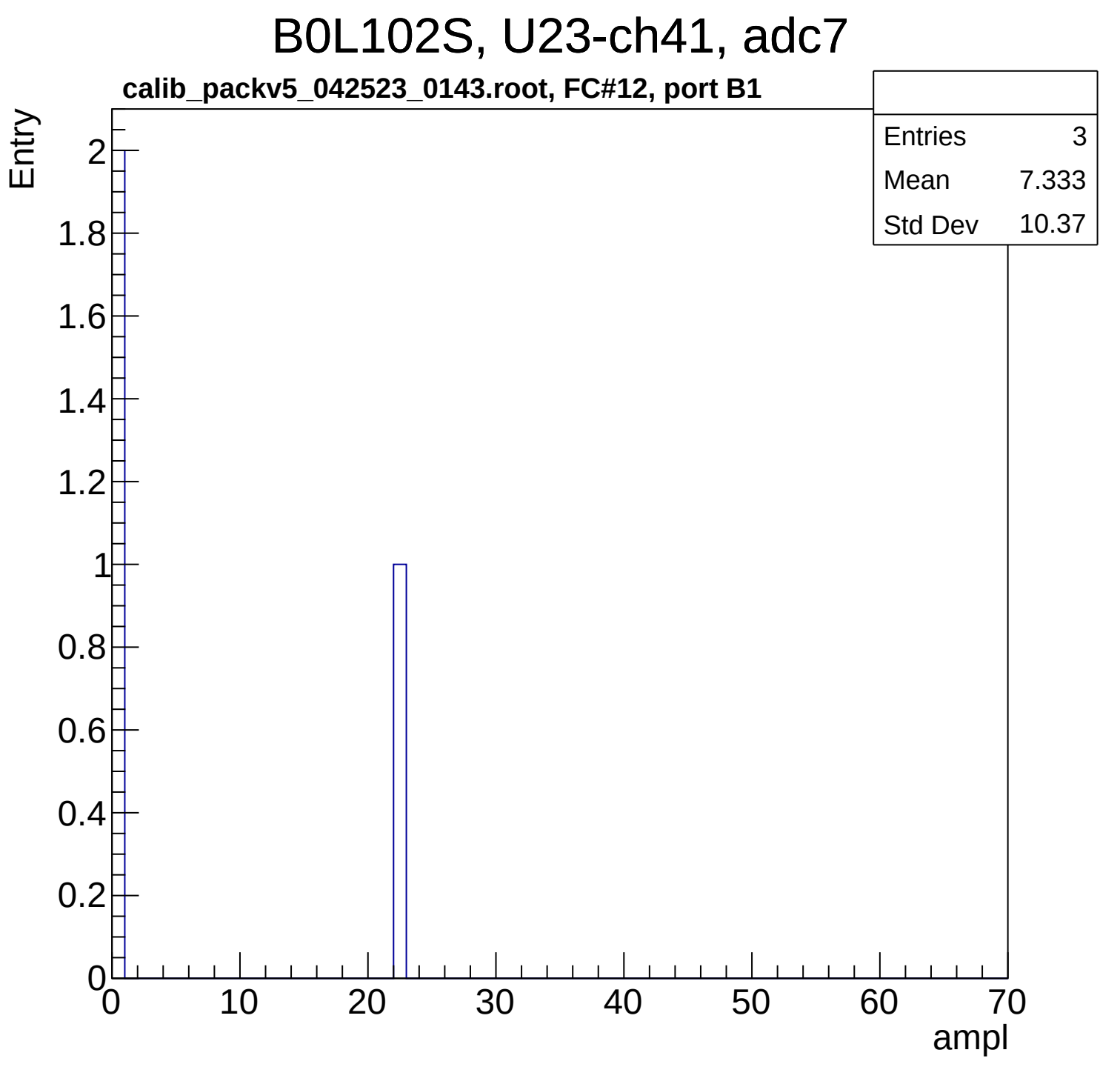
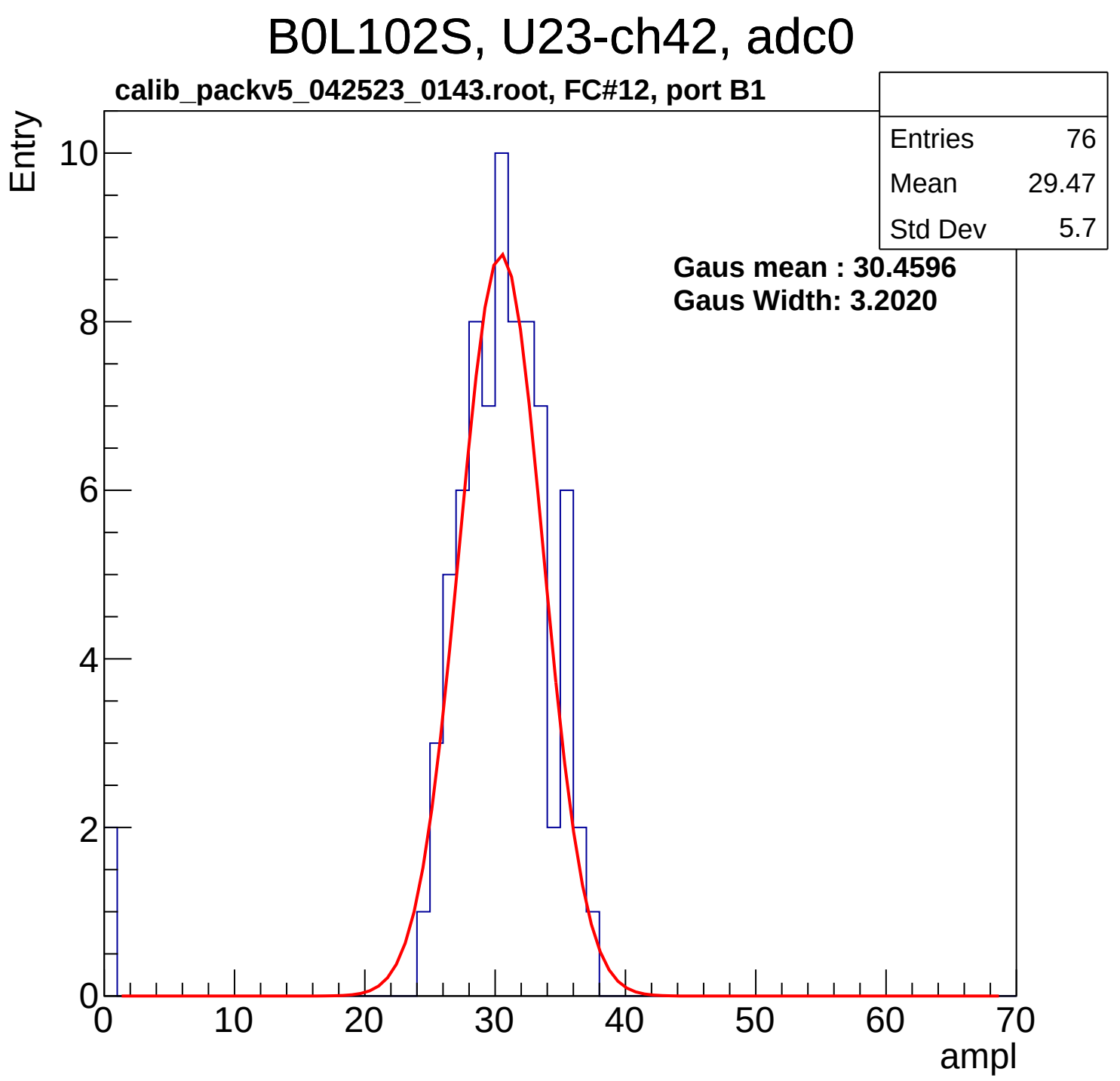
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch41, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch42, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.47
Std Dev 5.7
Gaus mean : 30.4596
Gaus Width: 3.2020
Entry
ampl
0
2
4
6
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10
0
10
20
30
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50
60
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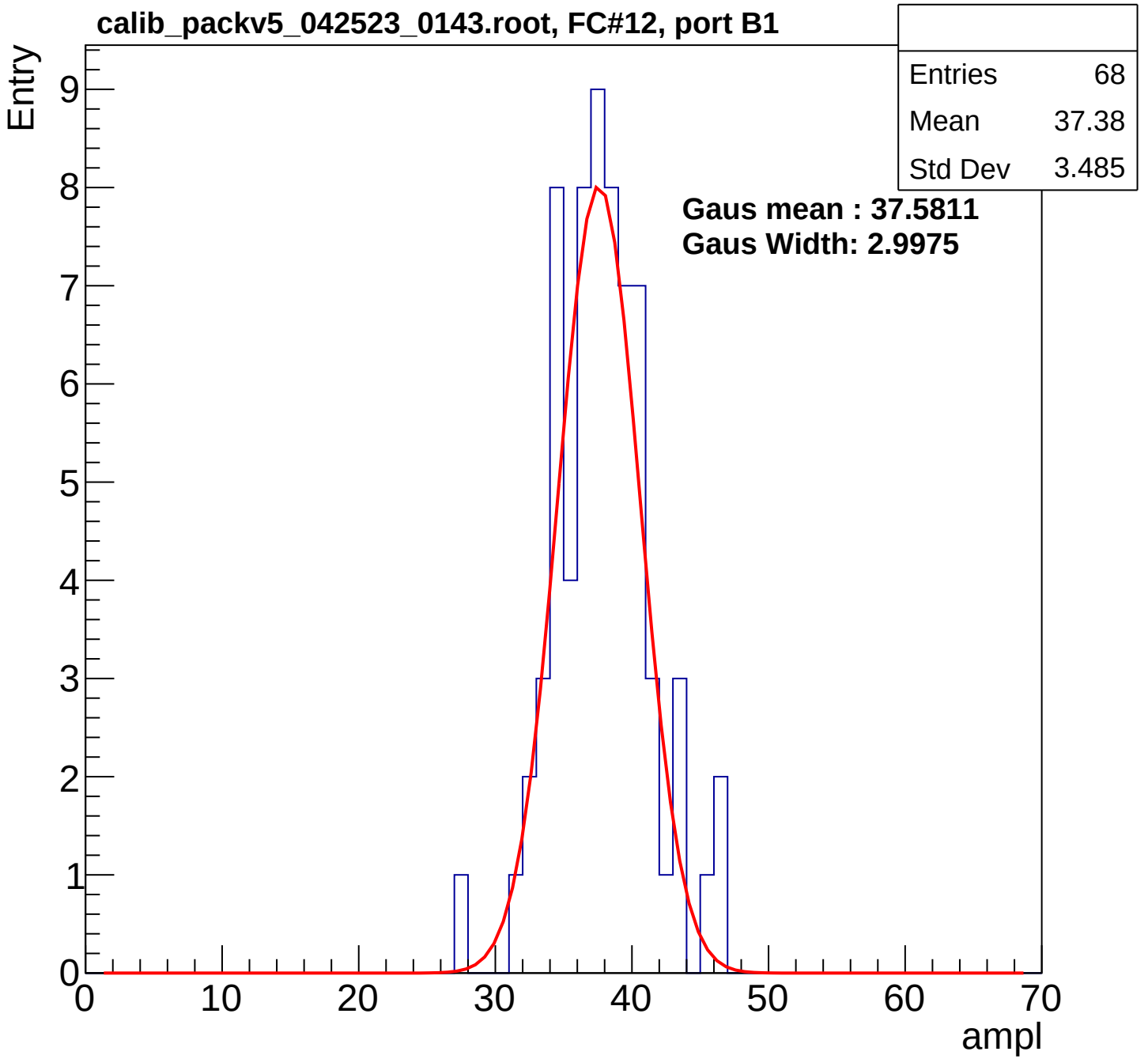
B0L102S, U23-ch42, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 37.38
Std Dev 3.485
Gaus mean : 37.5811
Gaus Width: 2.9975
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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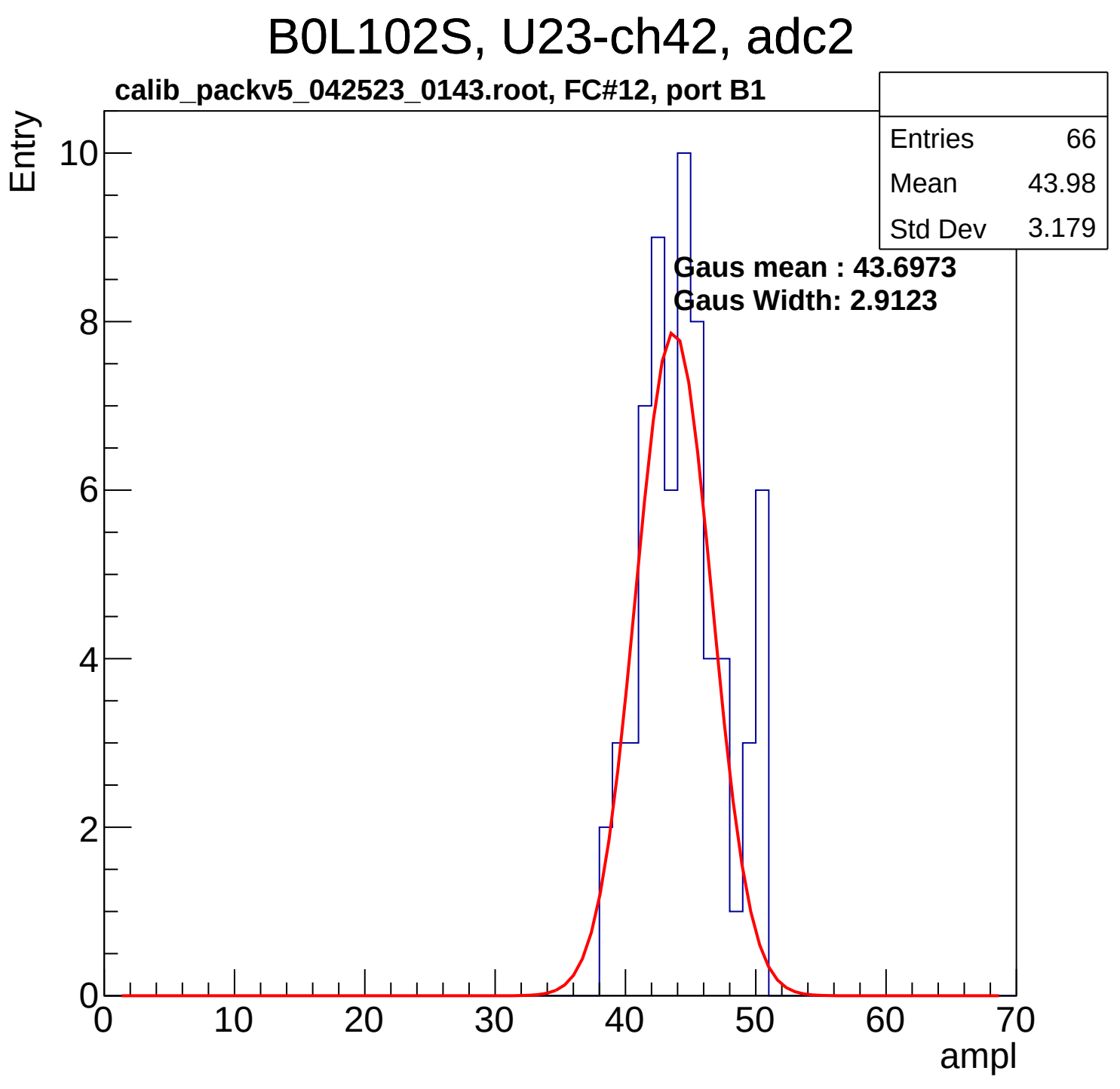
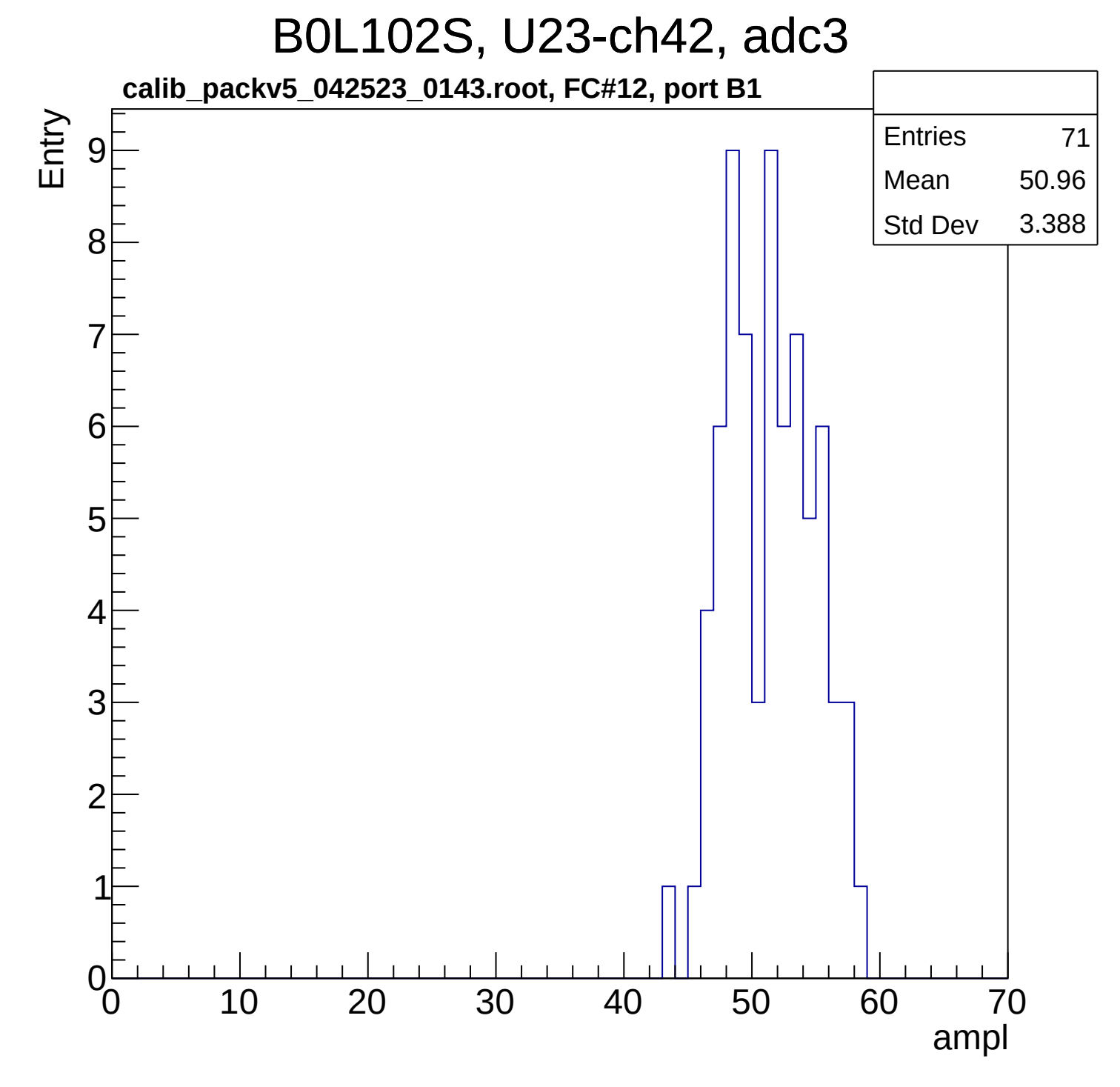

B0L102S, U23-ch42, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 43.98
Std Dev 3.179
Gaus mean : 43.6973
Gaus Width: 2.9123
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch42, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 50.96
Std Dev 3.388
Entry
ampl
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2
3
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6
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9
0
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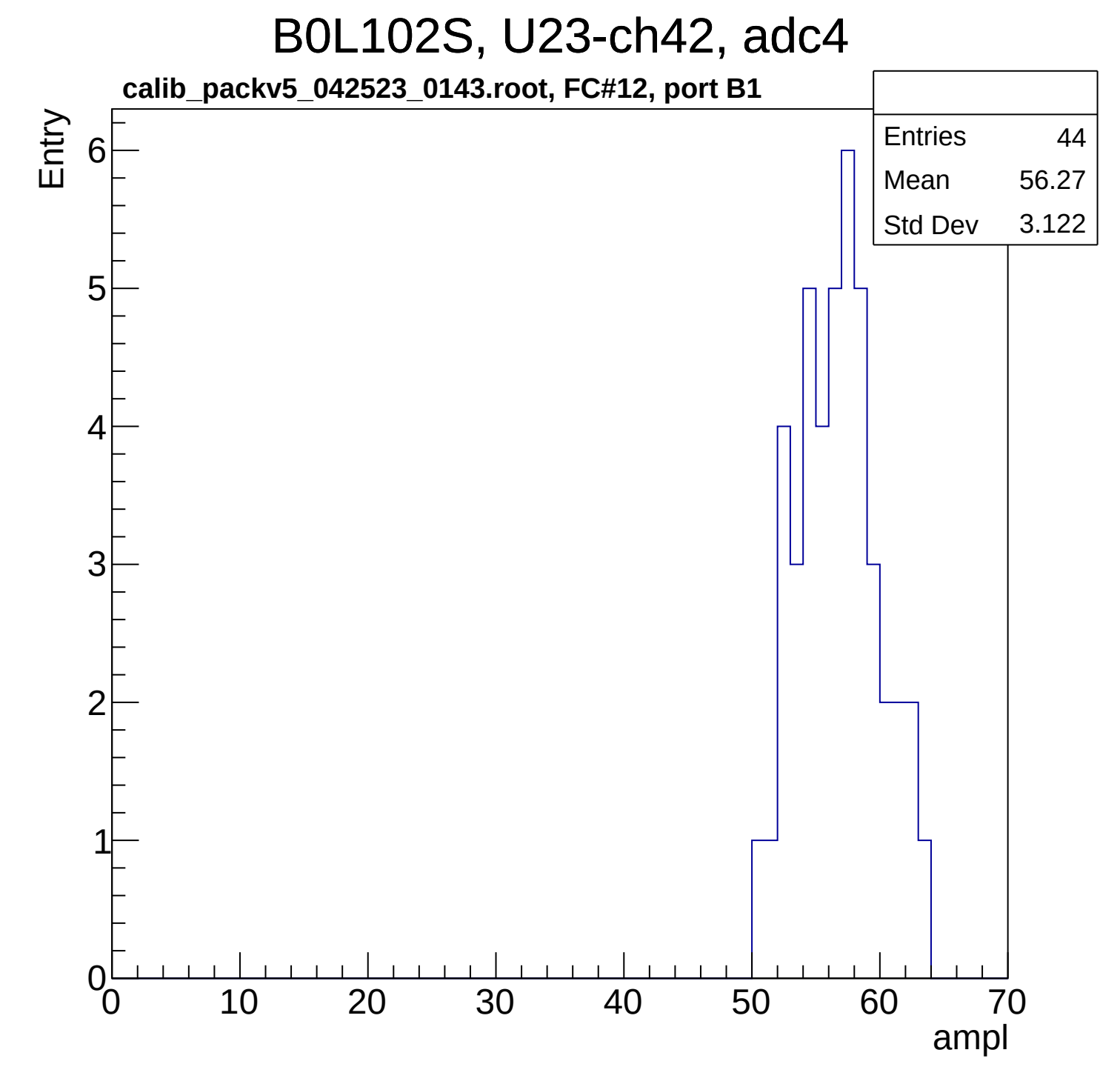
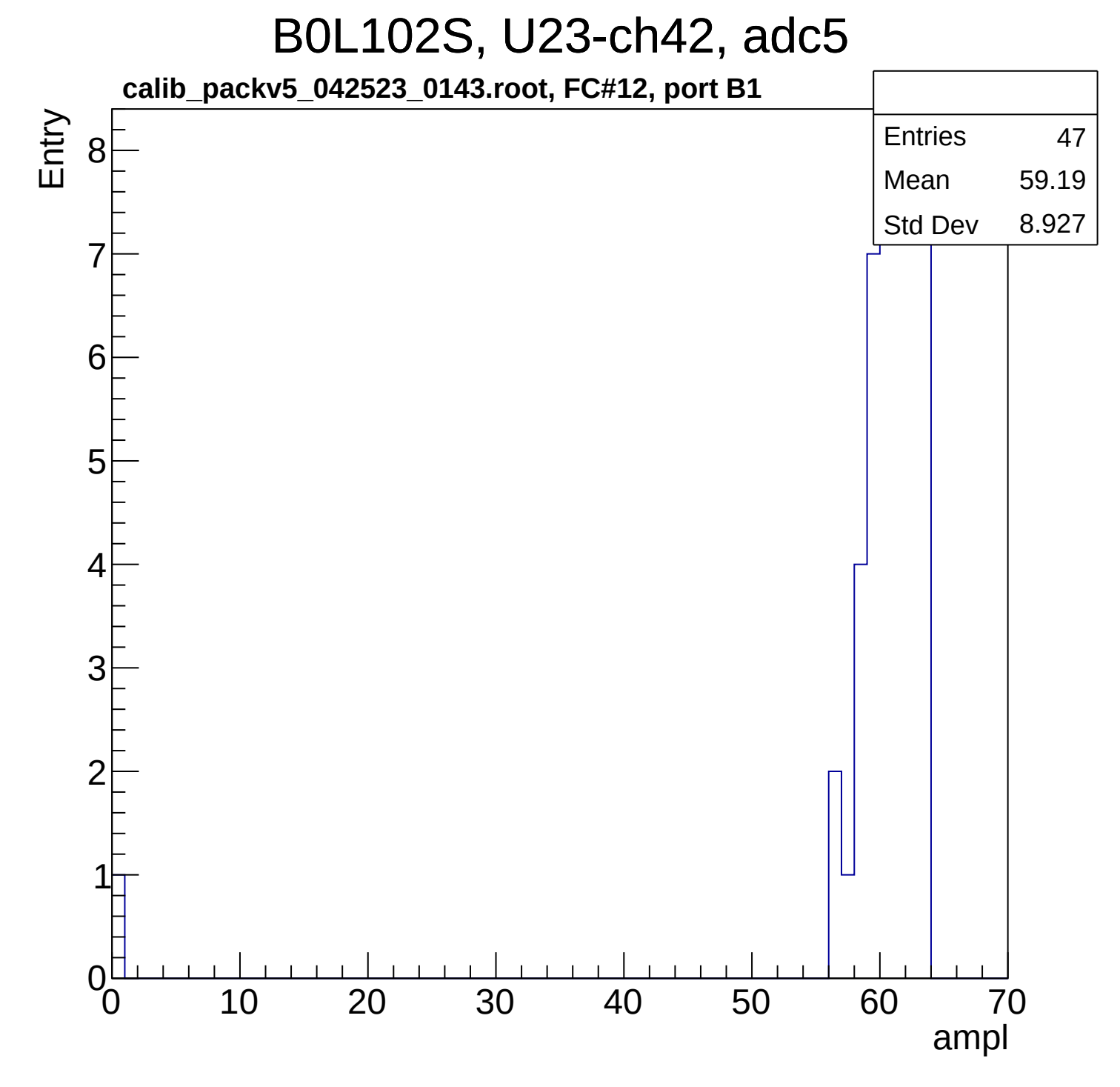

B0L102S, U23-ch42, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 56.27
Std Dev 3.122
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch42, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.19
Std Dev 8.927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
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60
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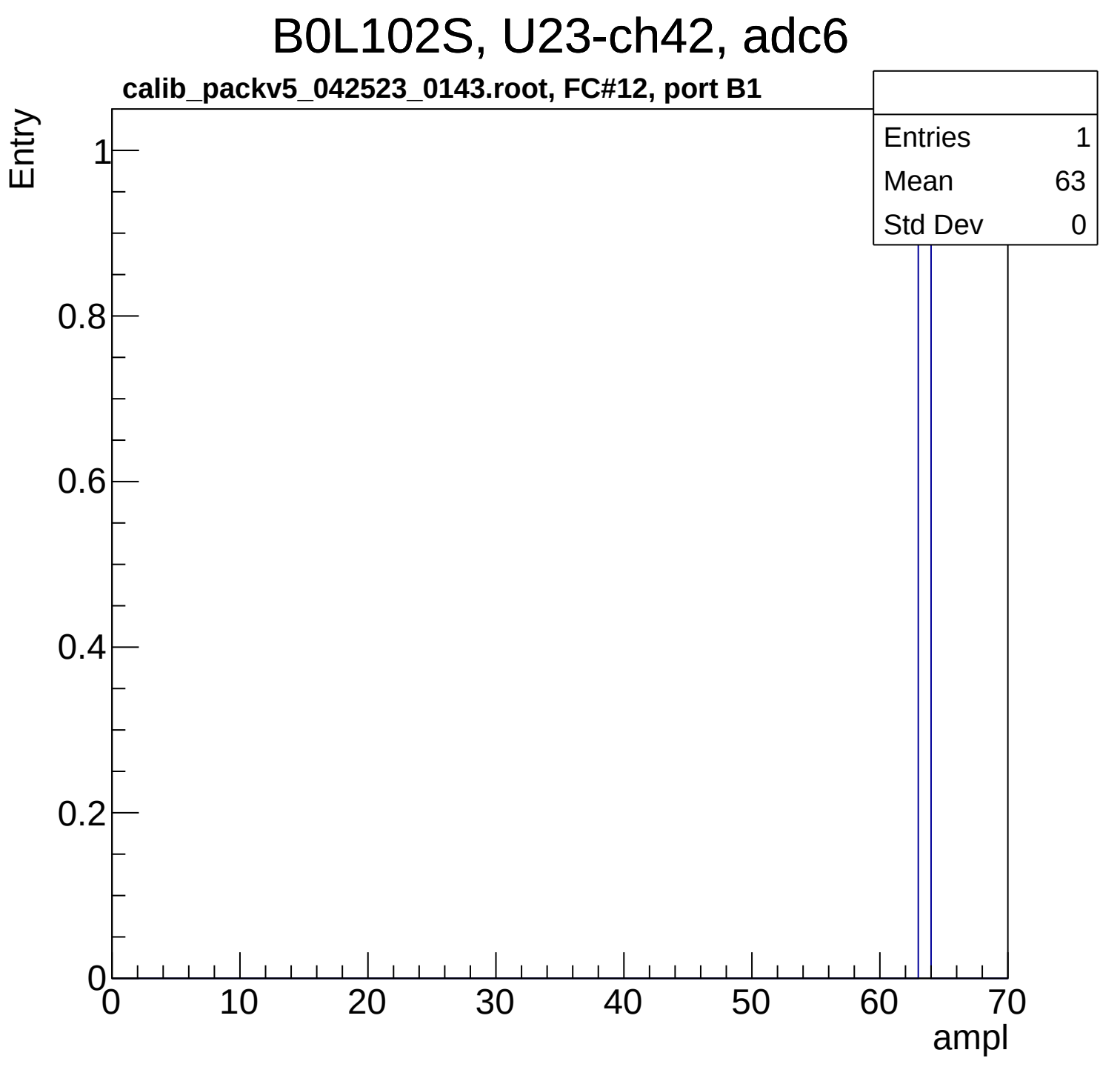
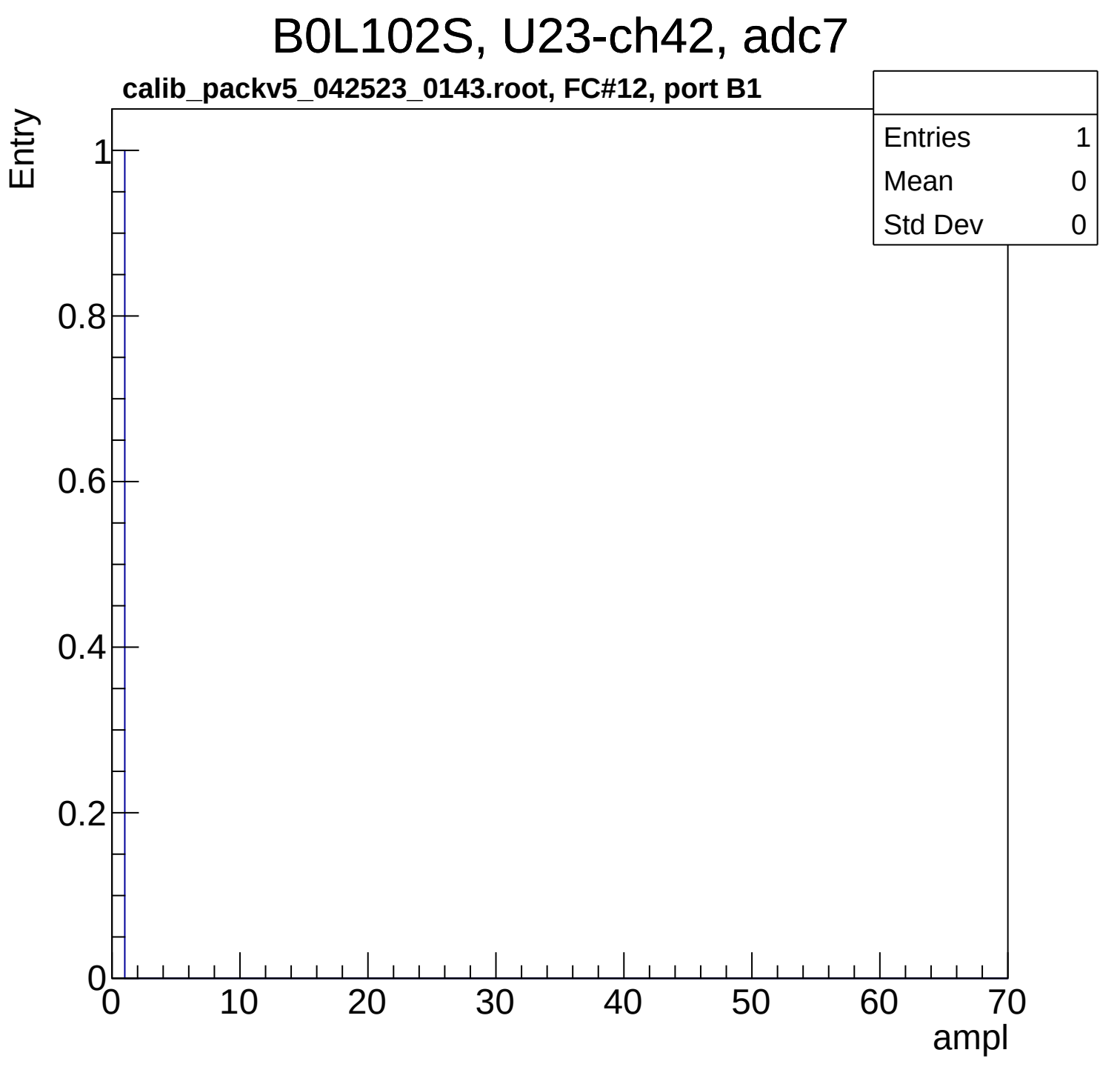
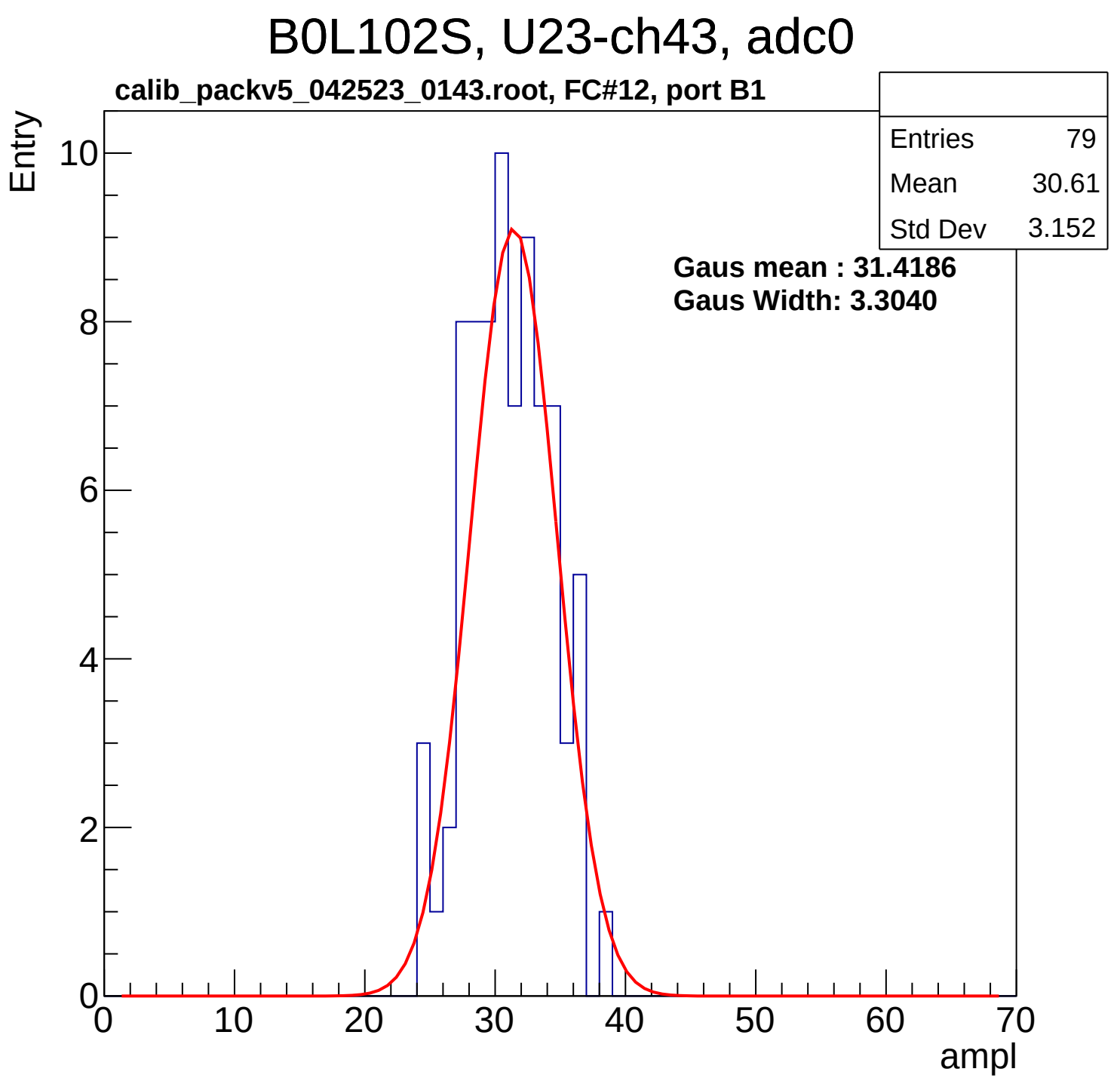
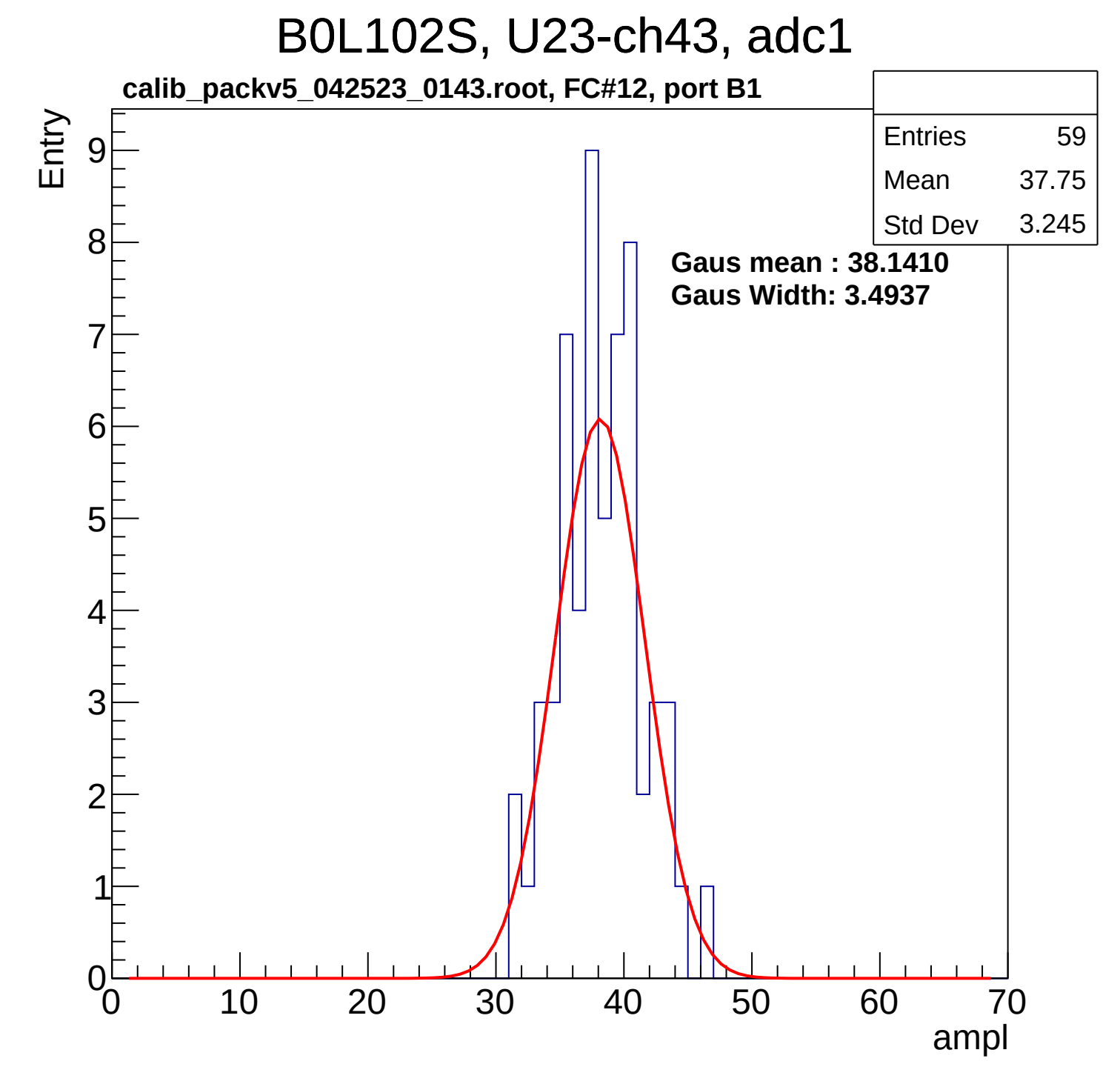

B0L102S, U23-ch42, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch42, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch43, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 30.61
Std Dev 3.152
Gaus mean : 31.4186
Gaus Width: 3.3040
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch43, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 37.75
Std Dev 3.245
Gaus mean : 38.1410
Gaus Width: 3.4937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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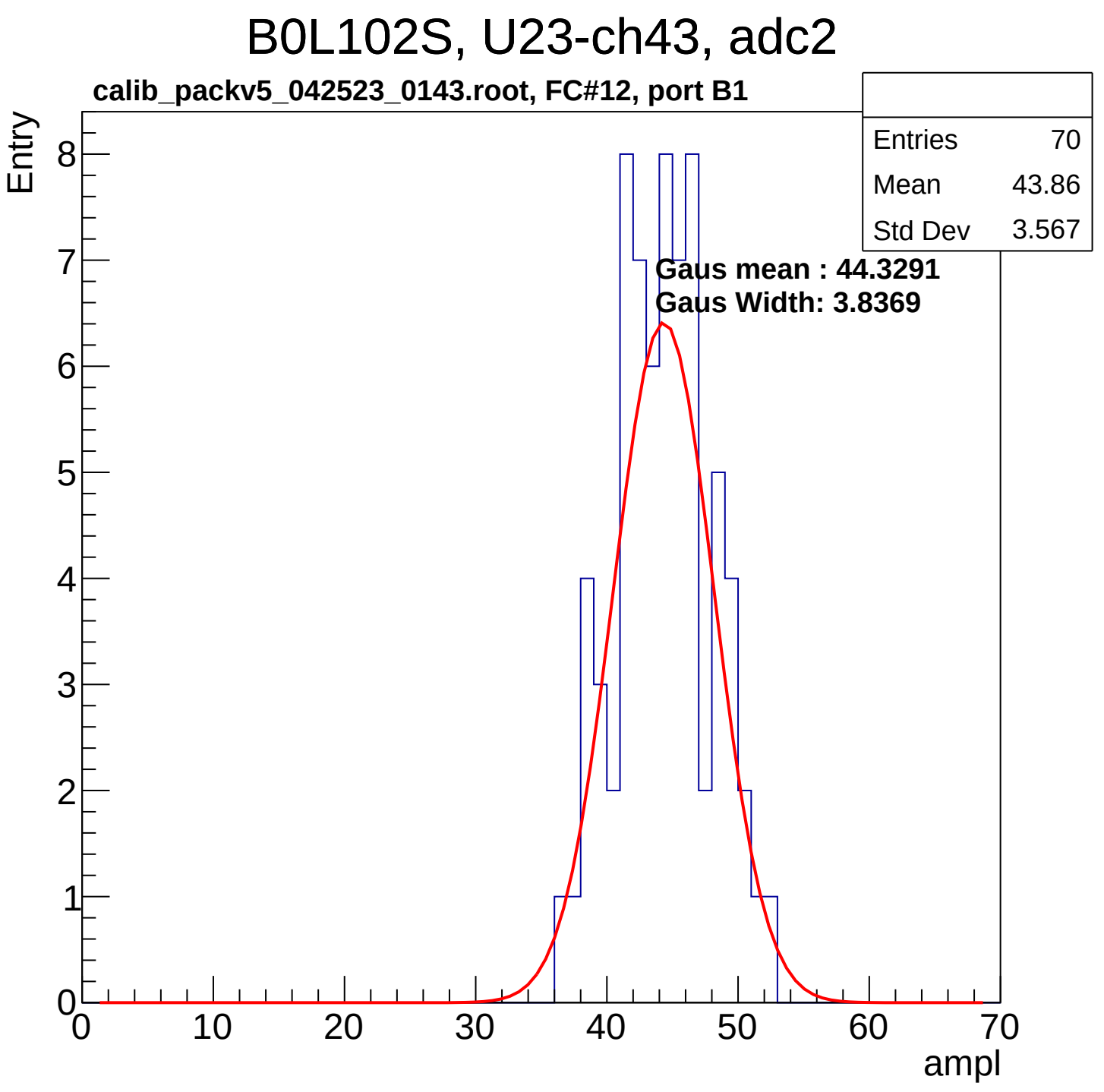
B0L102S, U23-ch43, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 43.86
Std Dev 3.567
Gaus mean : 44.3291
Gaus Width: 3.8369
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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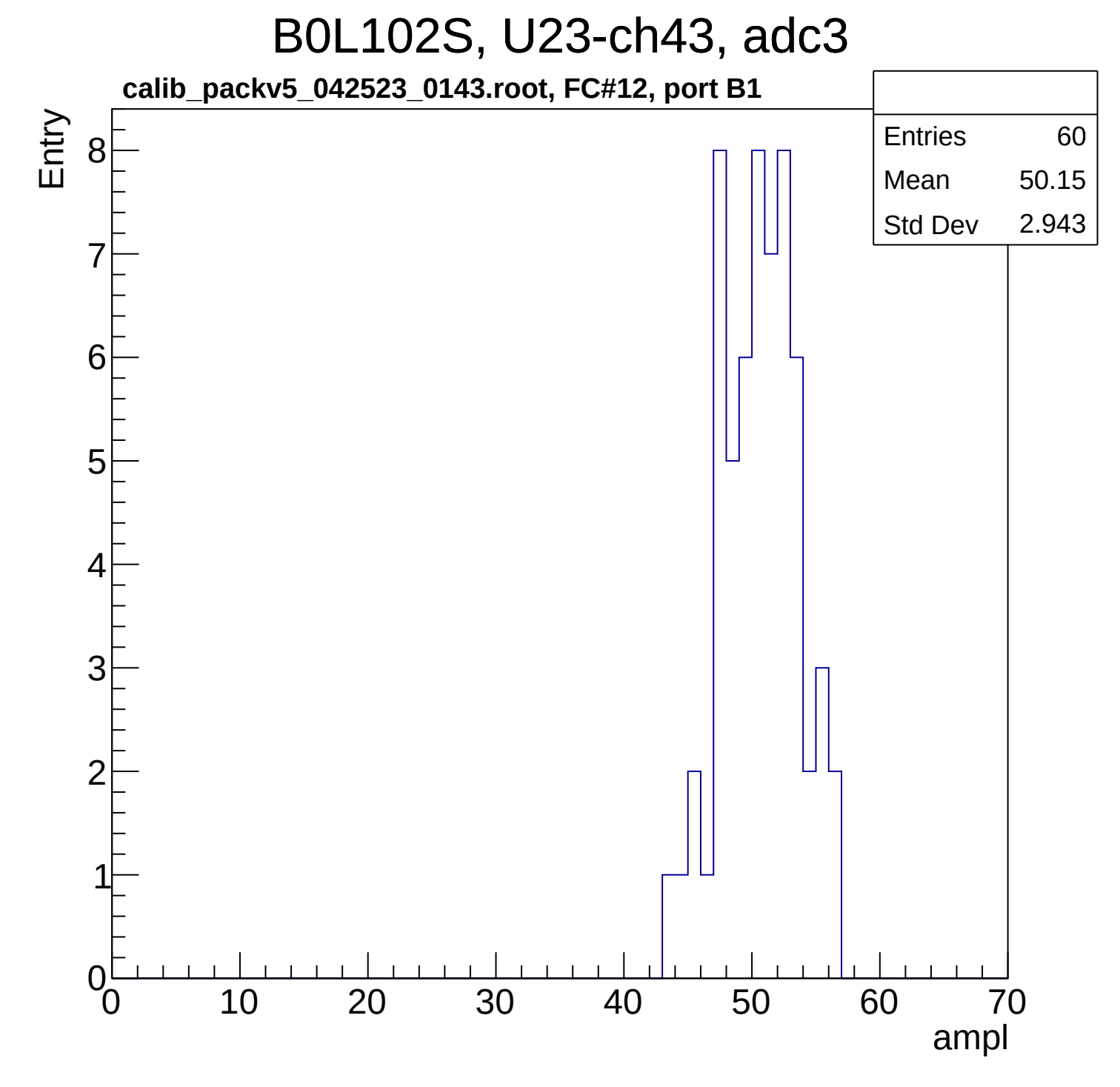
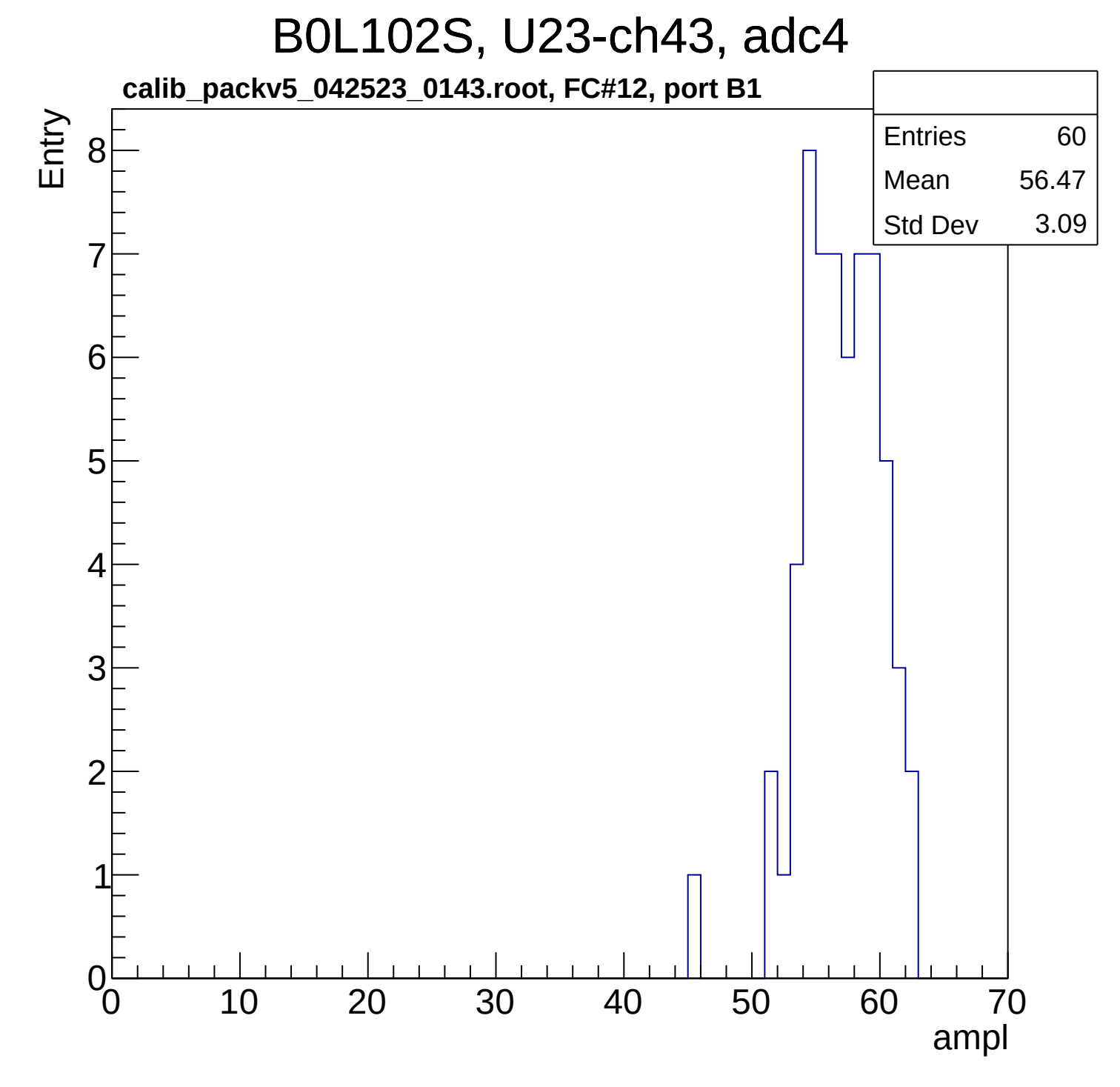
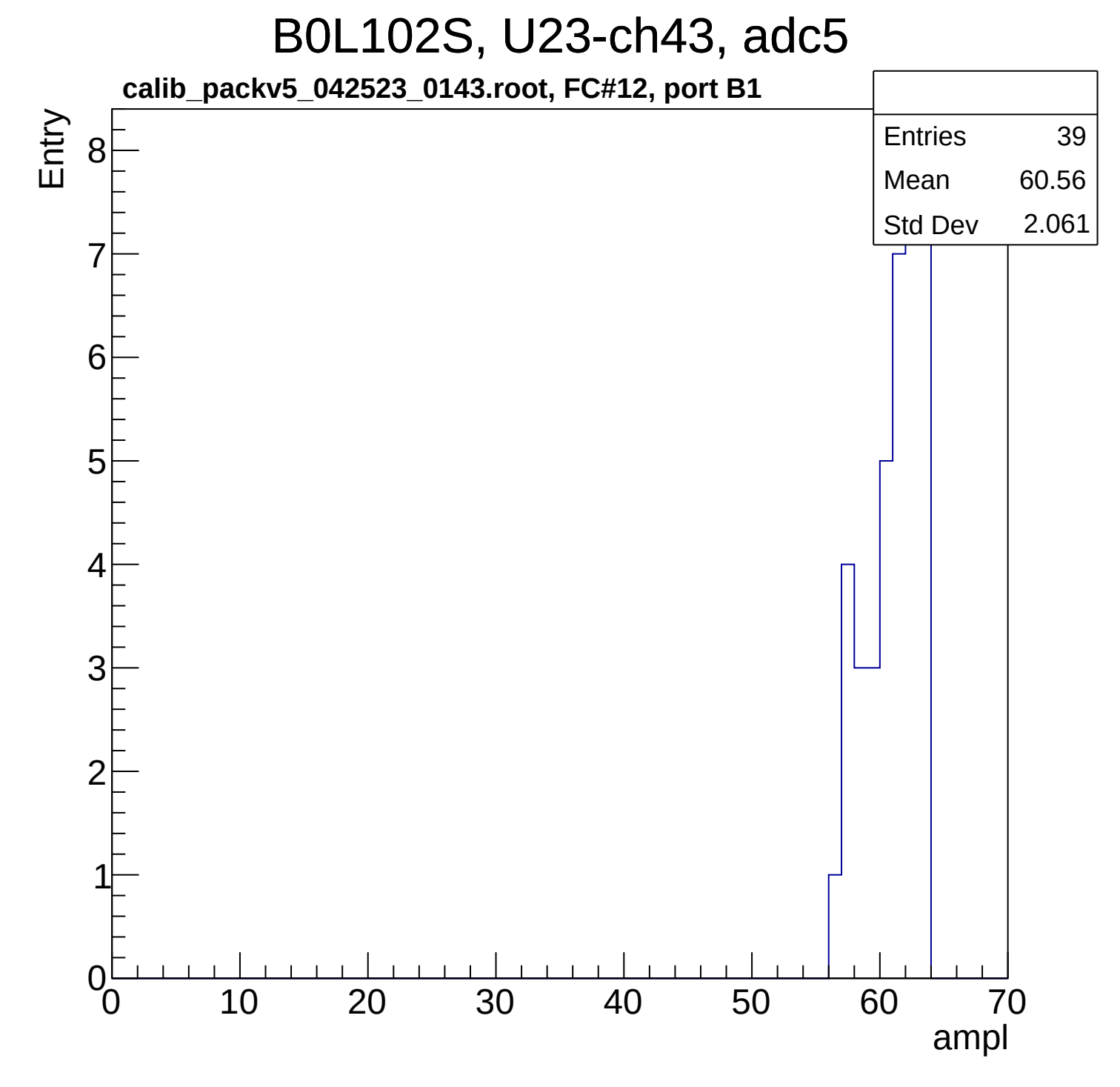
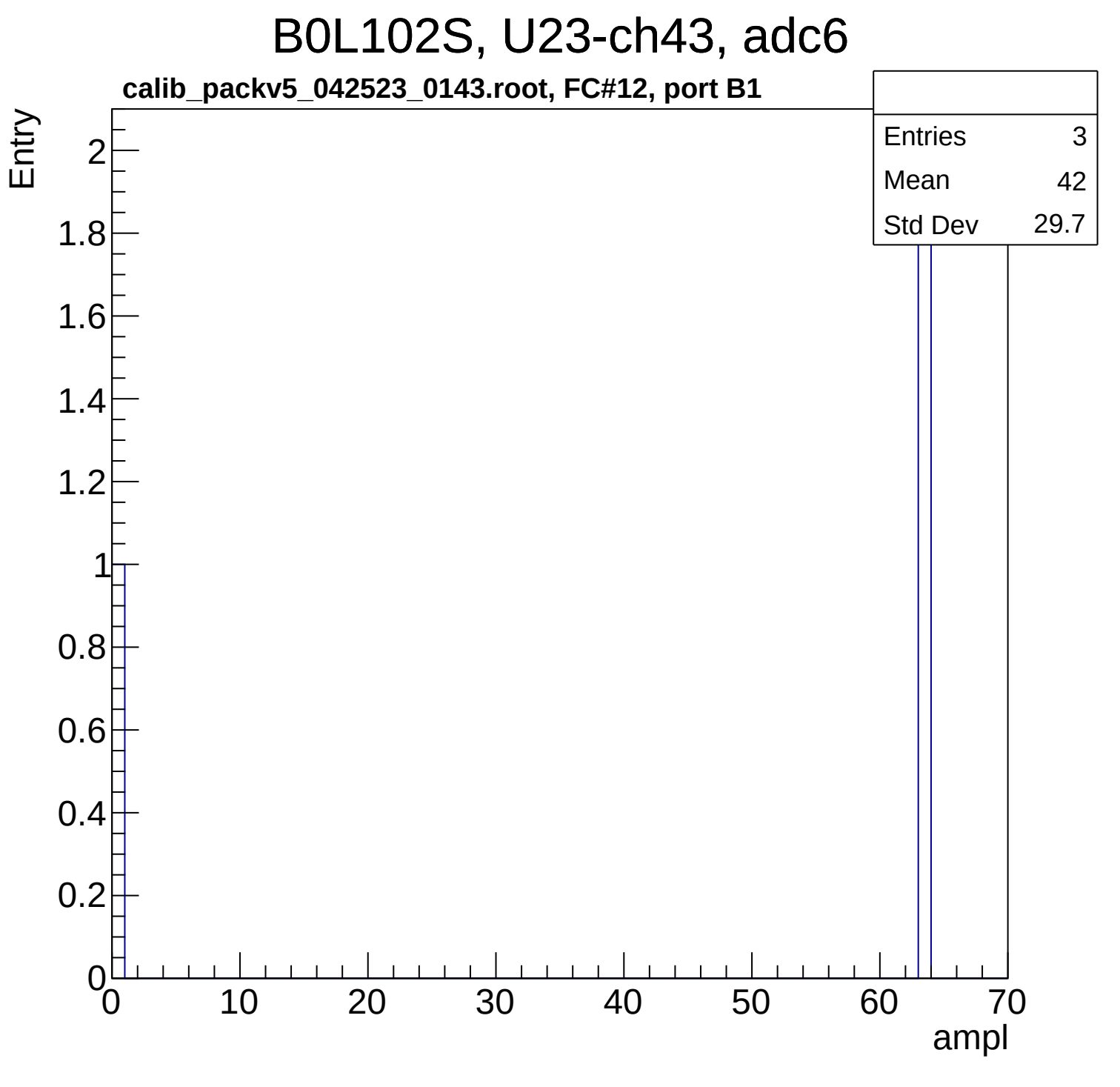
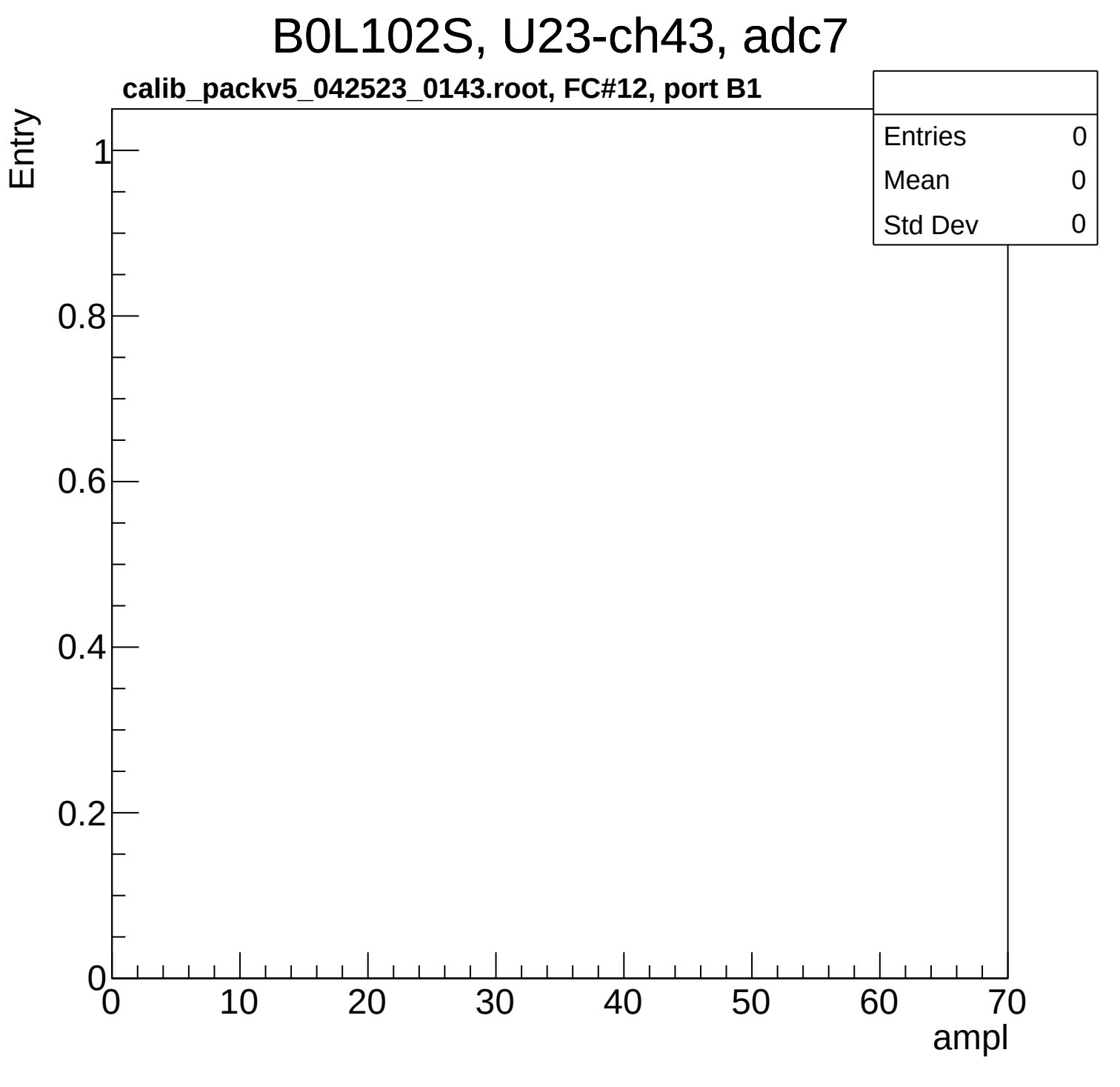
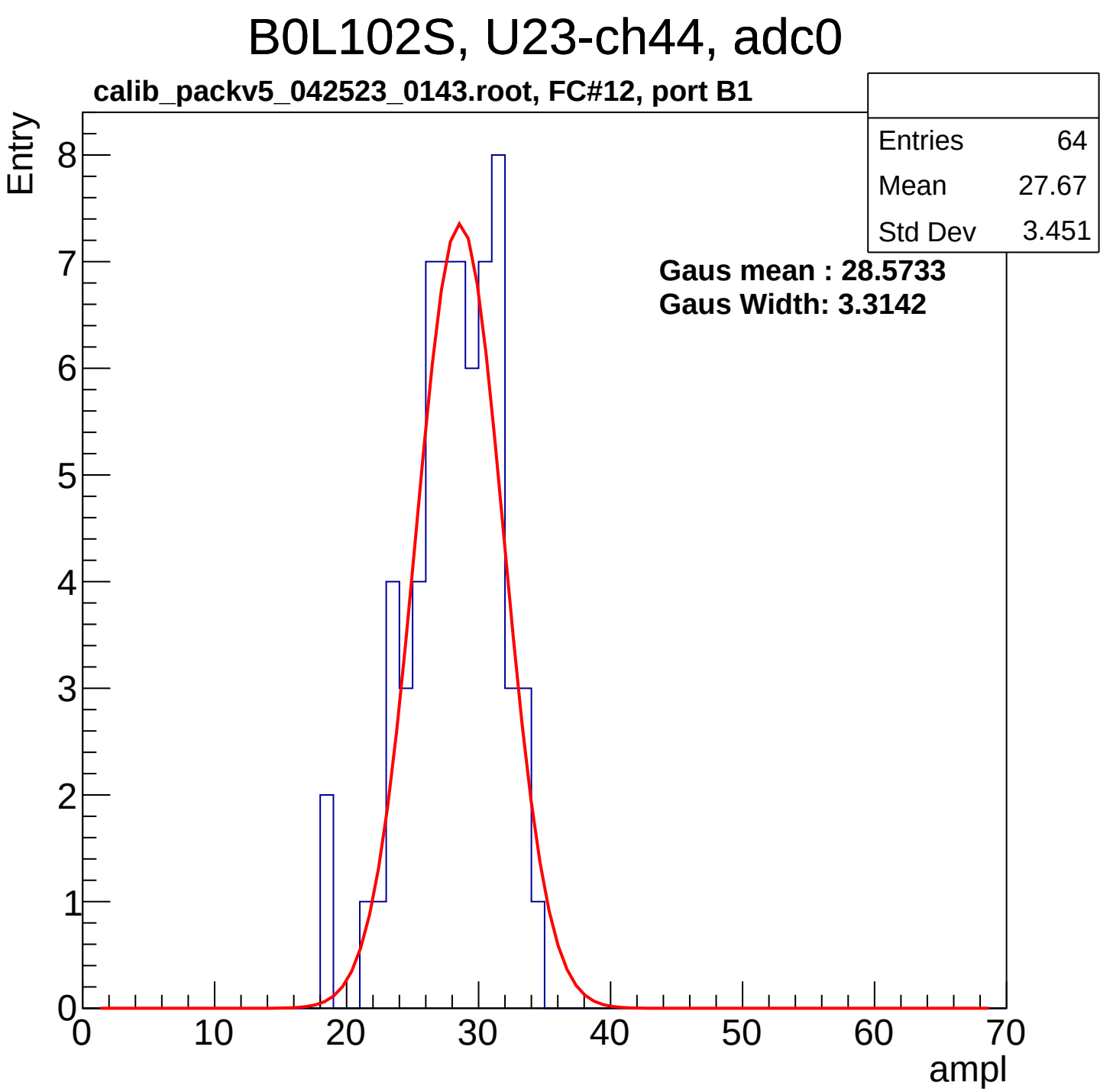

B0L102S, U23-ch43, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 50.15
Std Dev 2.943
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

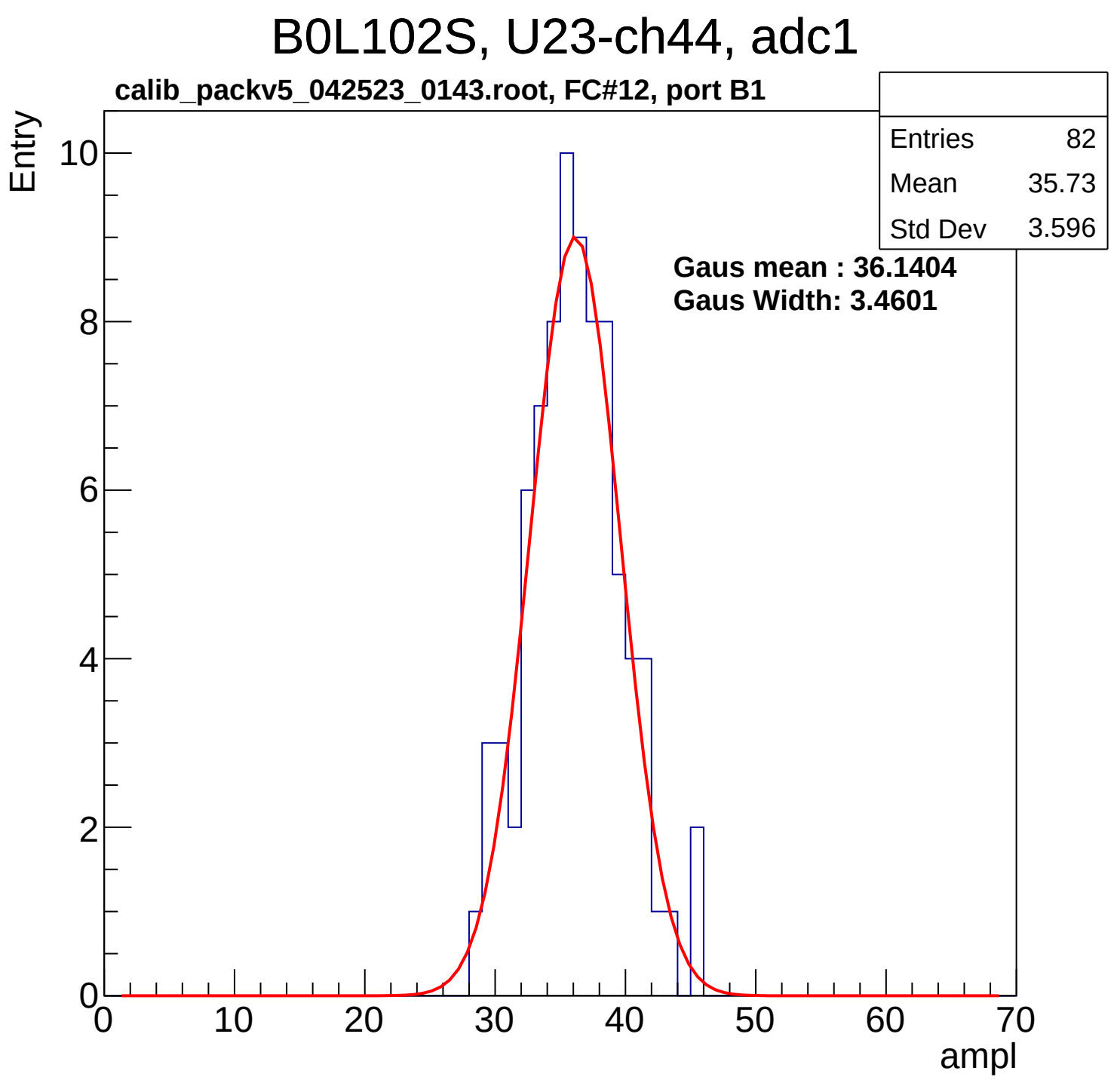

B0L102S, U23-ch43, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.47
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

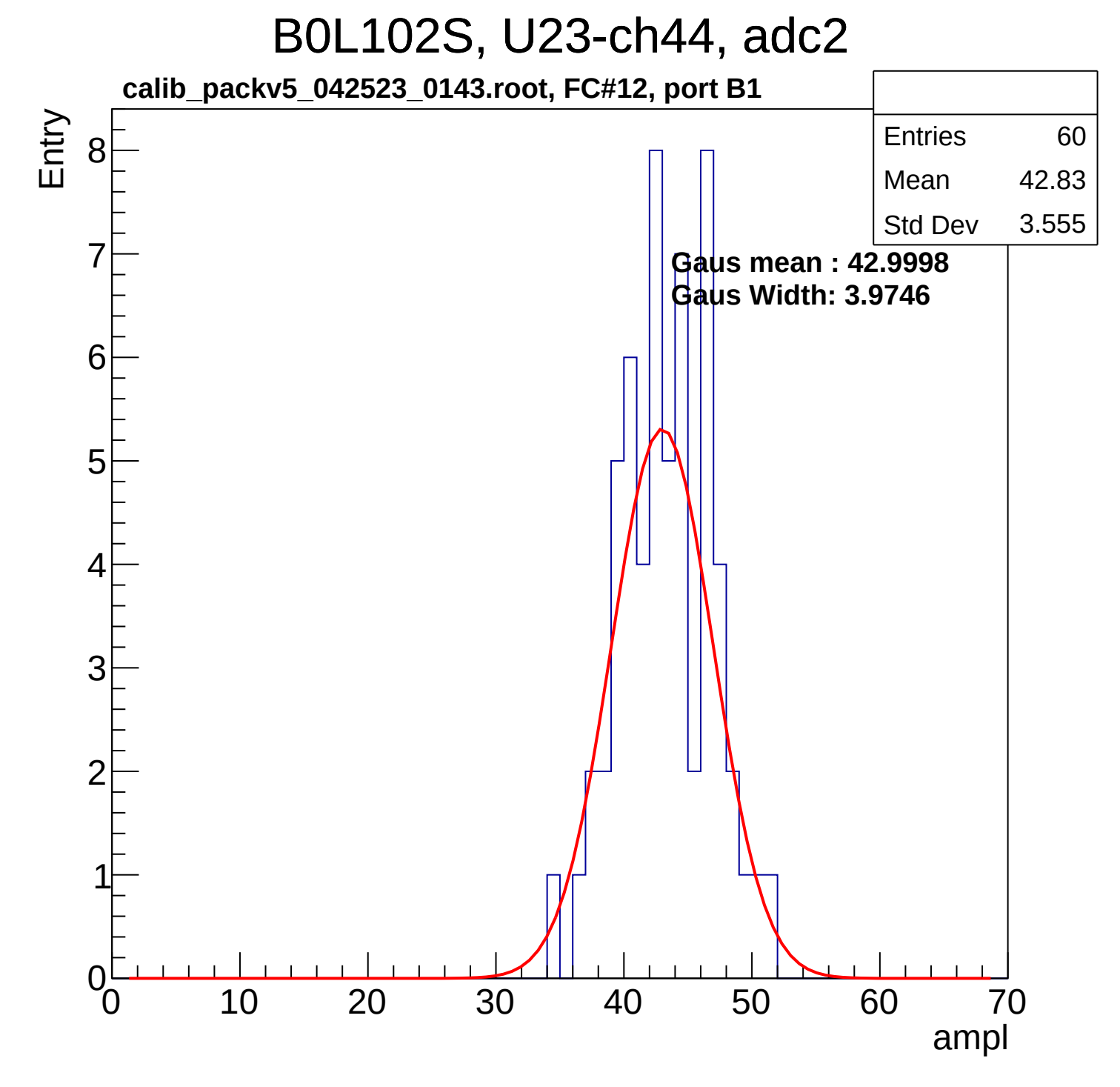

B0L102S, U23-ch43, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 60.56
Std Dev 2.061
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

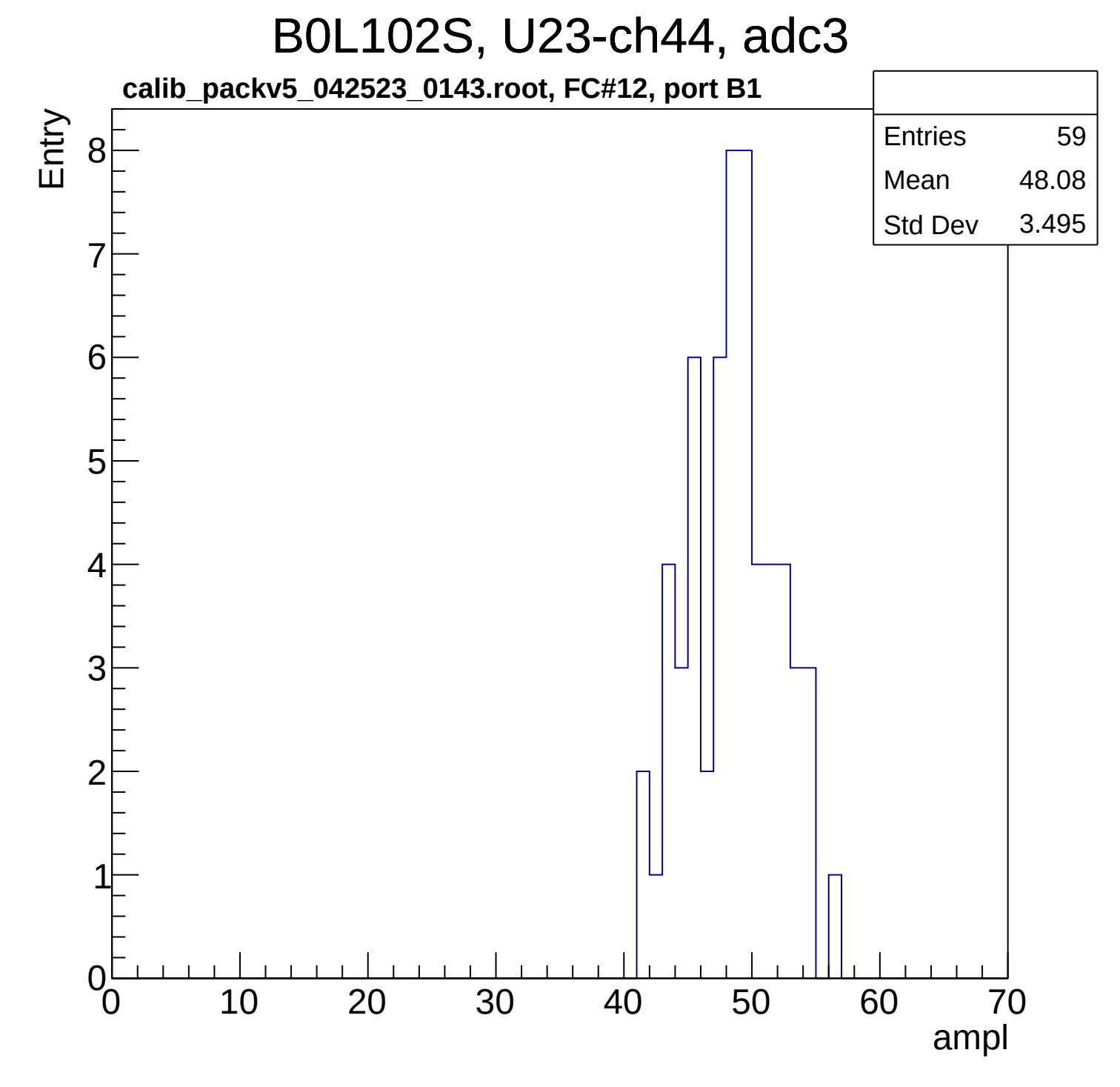

B0L102S, U23-ch43, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch43, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch44, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 27.67
Std Dev 3.451
Gaus mean : 28.5733
Gaus Width: 3.3142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch44, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 35.73
Std Dev 3.596
Gaus mean : 36.1404
Gaus Width: 3.4601
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch44, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 42.83
Std Dev 3.555
Gaus mean : 42.9998
Gaus Width: 3.9746
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch44, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 48.08
Std Dev 3.495
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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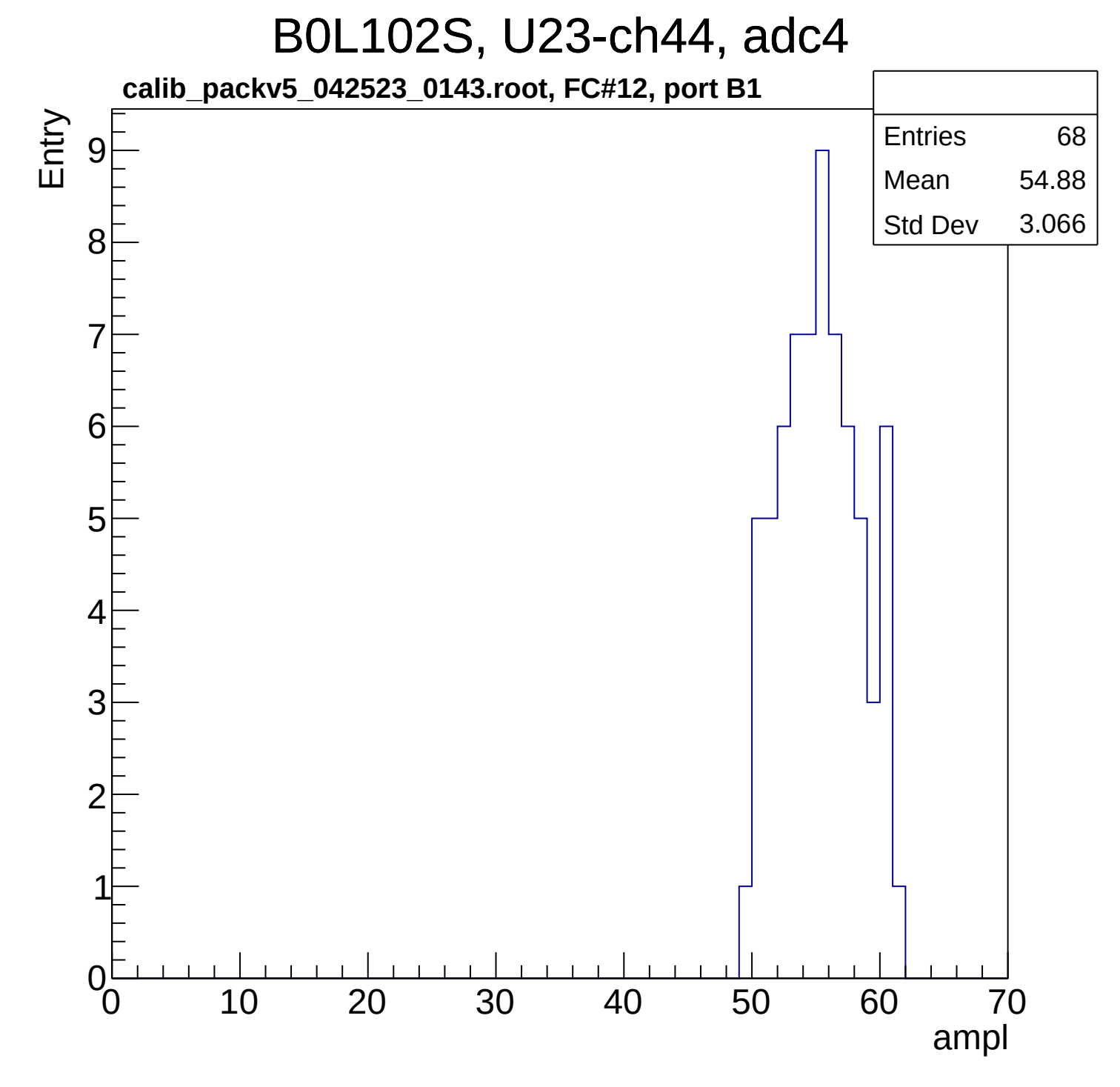
40
50
60
70


B0L102S, U23-ch44, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 54.88
Std Dev 3.066
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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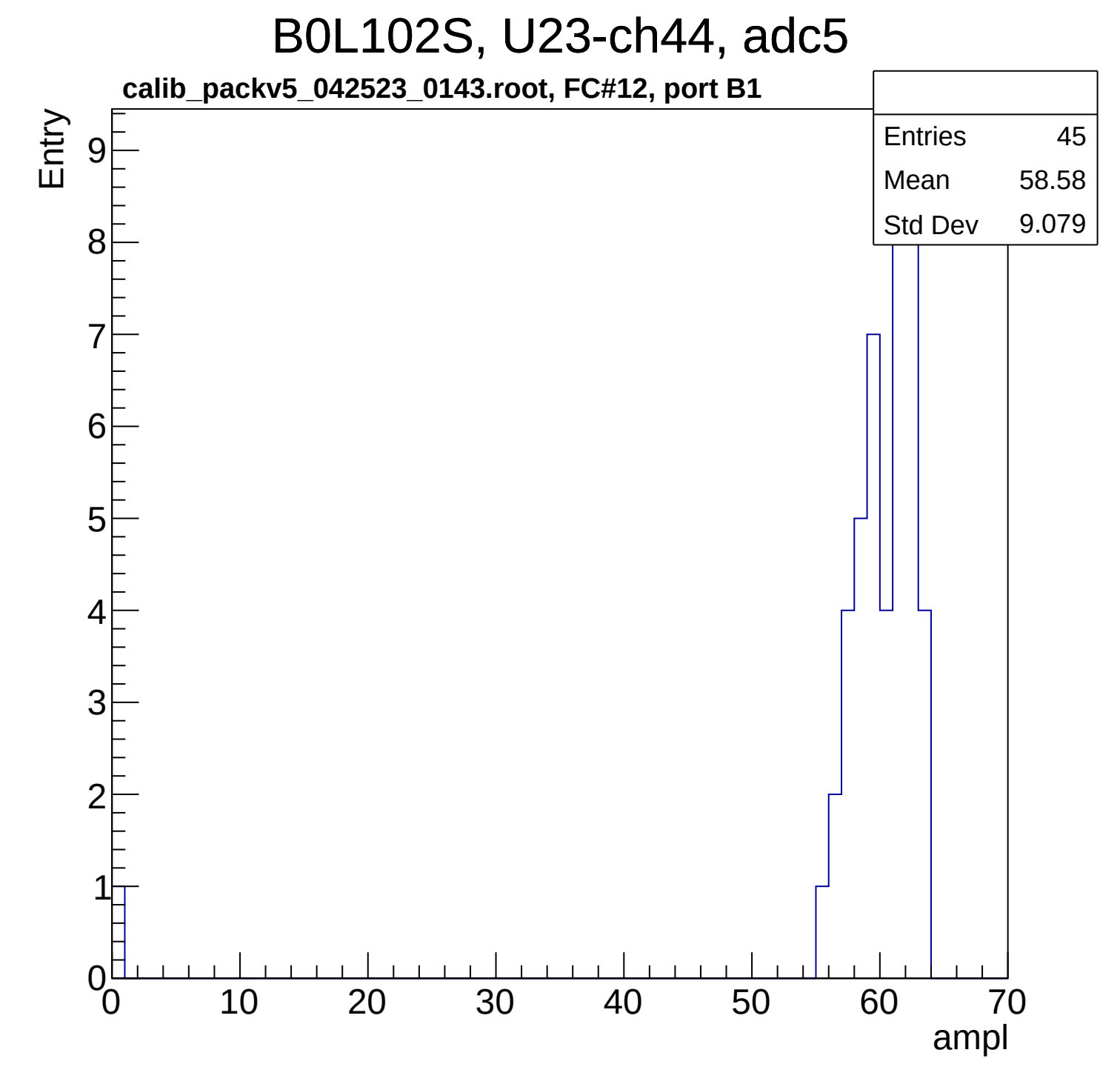
B0L102S, U23-ch44, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.58
Std Dev 9.079
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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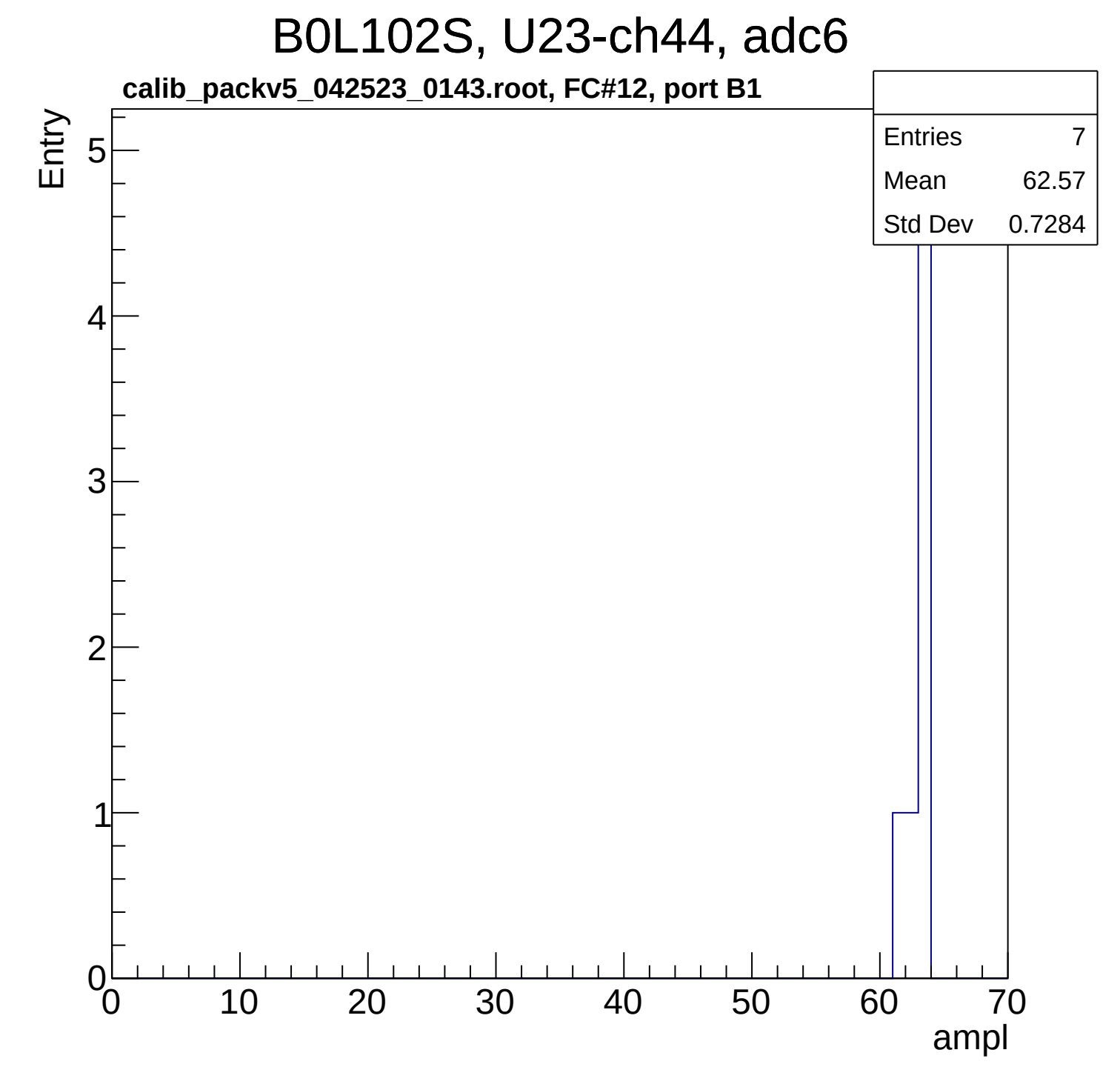
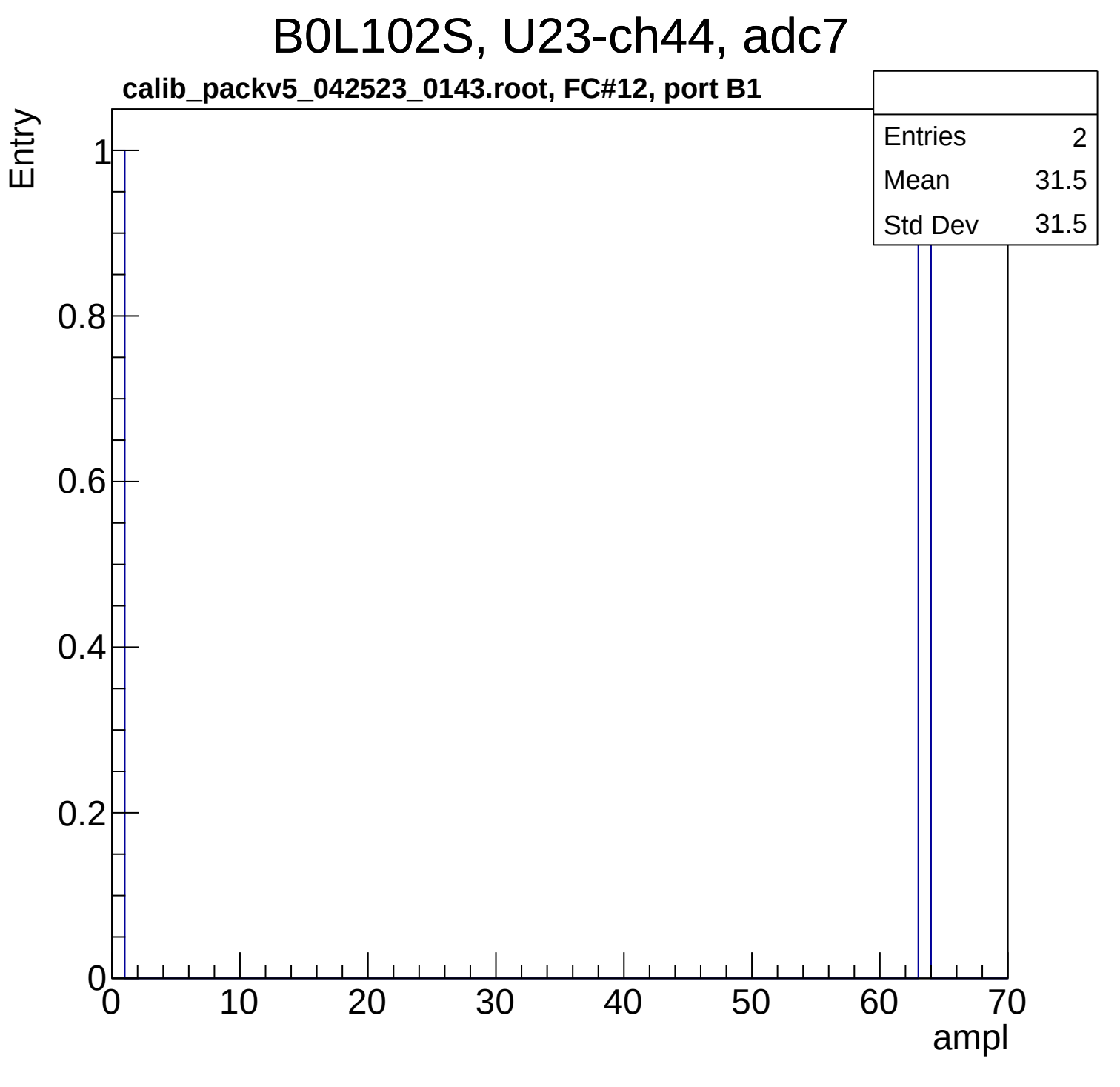

B0L102S, U23-ch44, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.57
Std Dev 0.7284
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch44, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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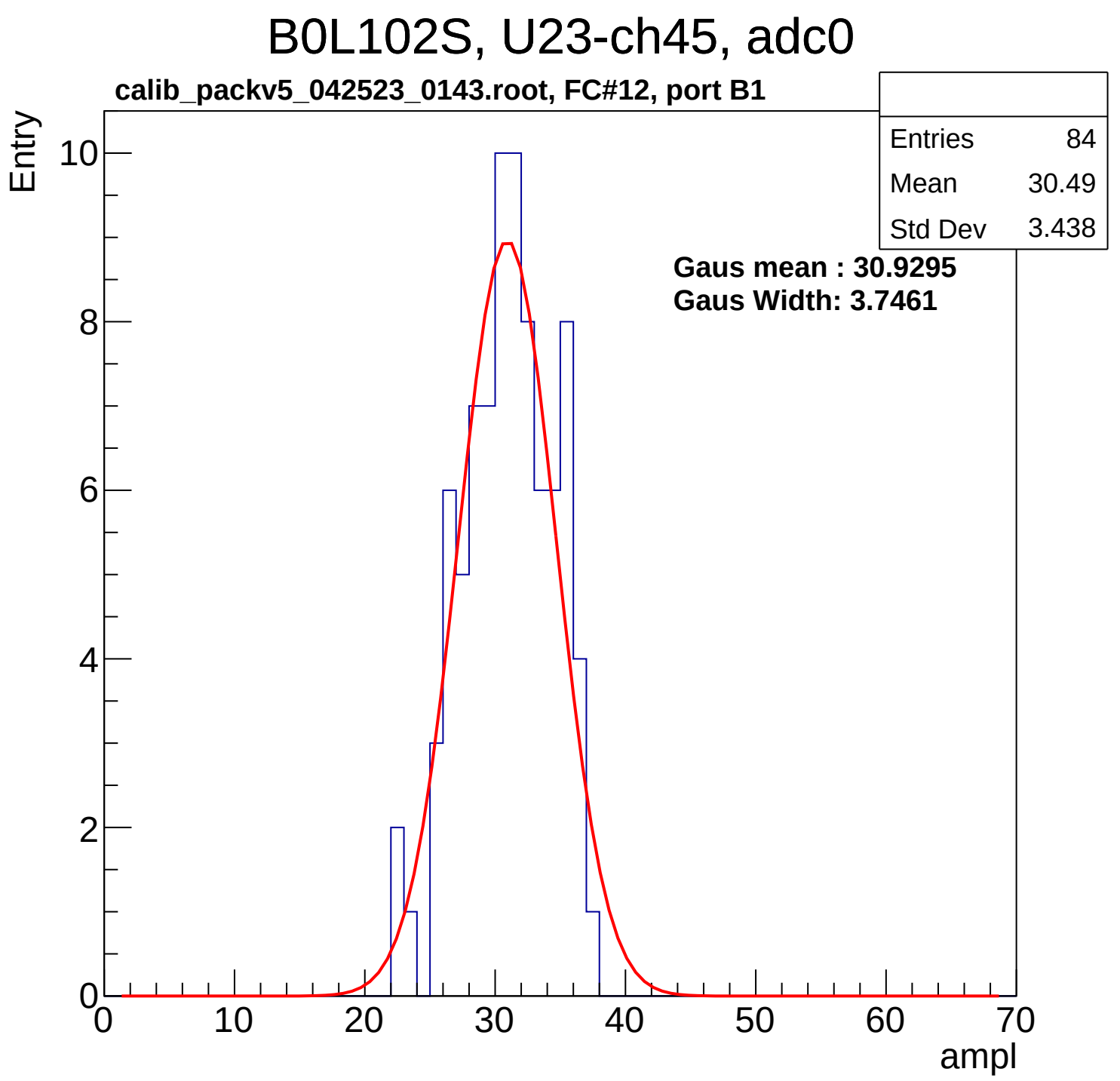
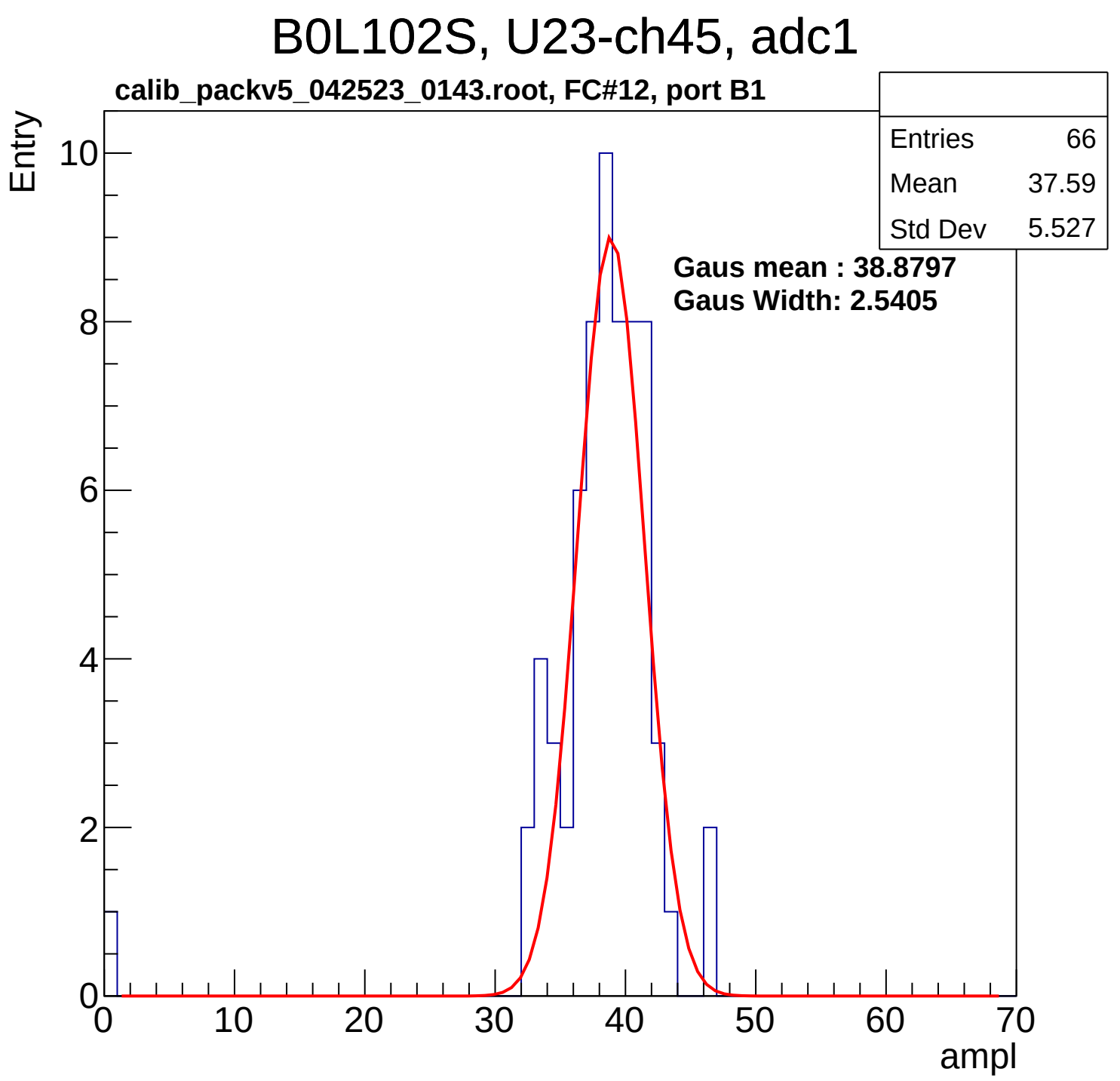
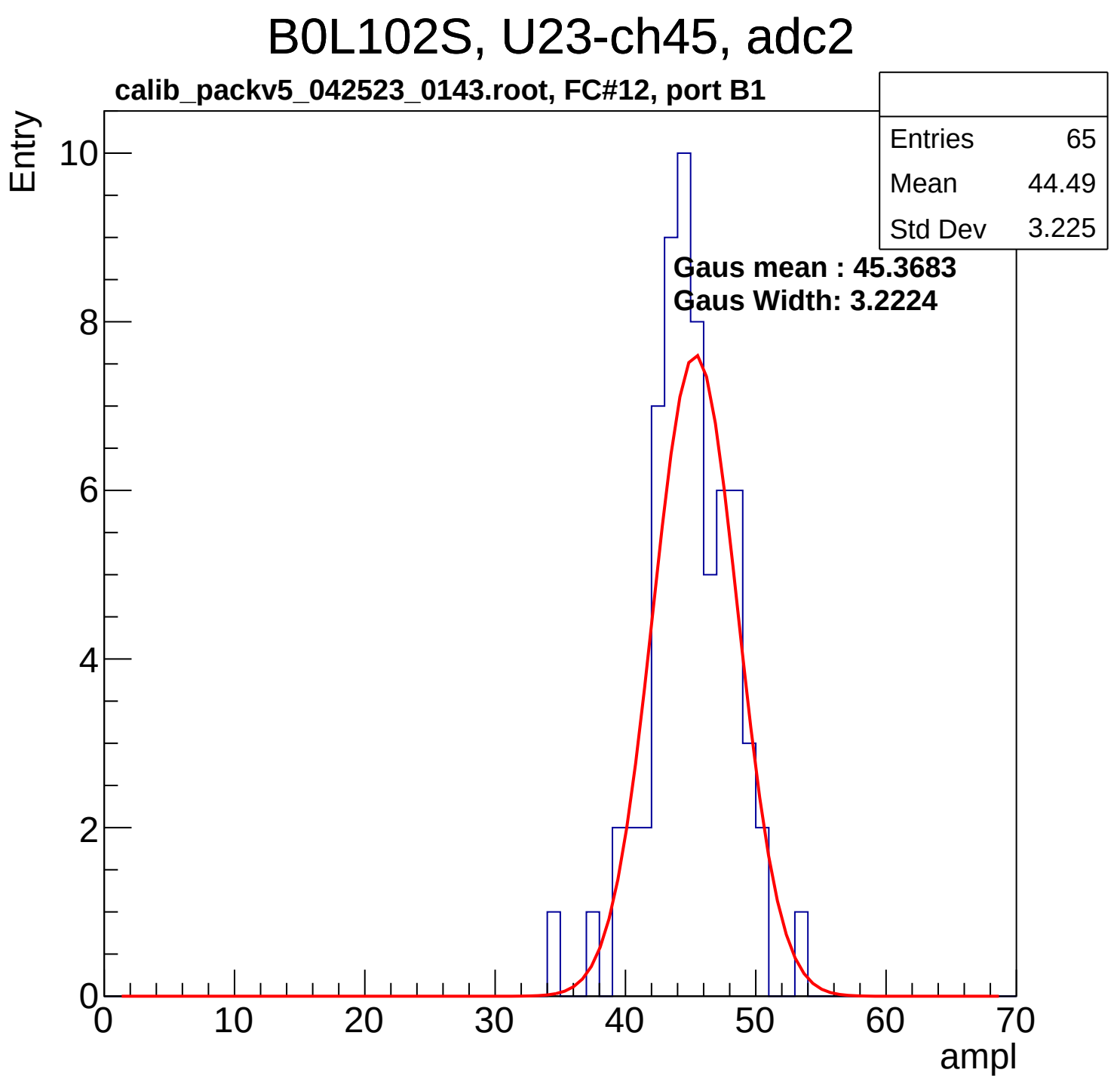
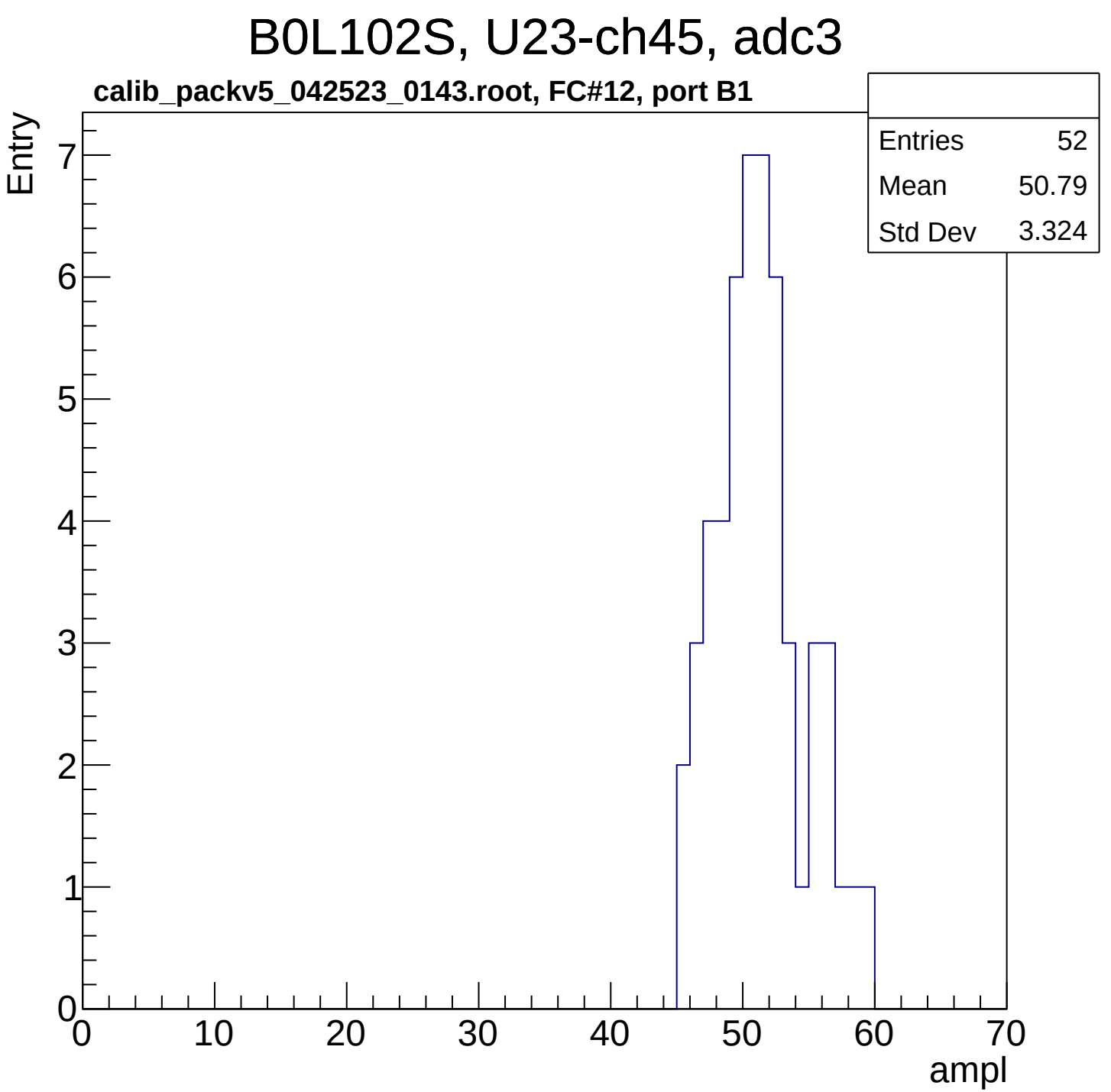
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch45, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 30.49
Std Dev 3.438
Gaus mean : 30.9295
Gaus Width: 3.7461
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch45, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 37.59
Std Dev 5.527
Gaus mean : 38.8797
Gaus Width: 2.5405
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch45, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 44.49
Std Dev 3.225
Gaus mean : 45.3683
Gaus Width: 3.2224
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch45, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 50.79
Std Dev 3.324
Entry
ampl
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3
4
5
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7
0
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20
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40
50
60
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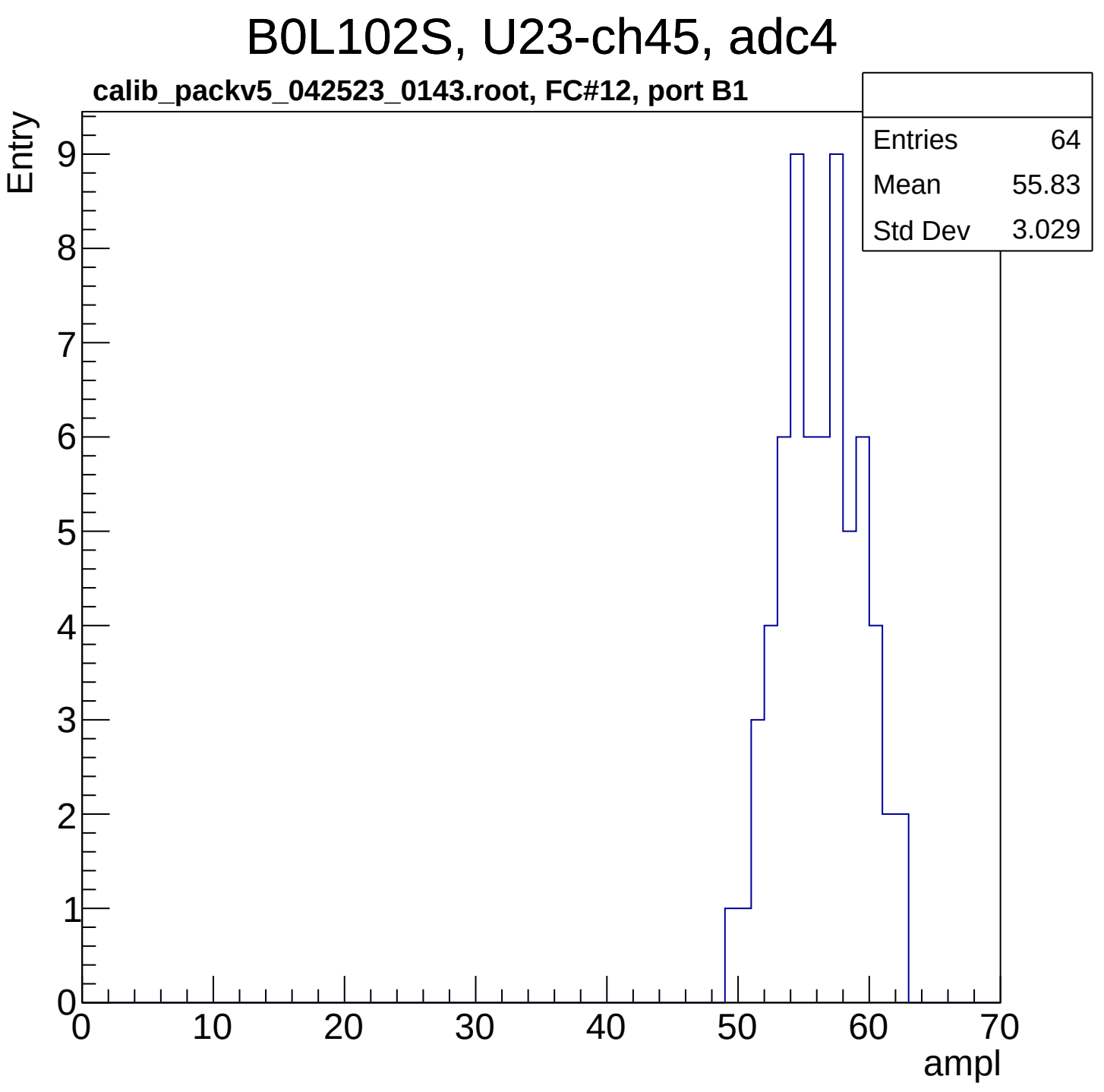
B0L102S, U23-ch45, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 55.83
Std Dev 3.029
Entry
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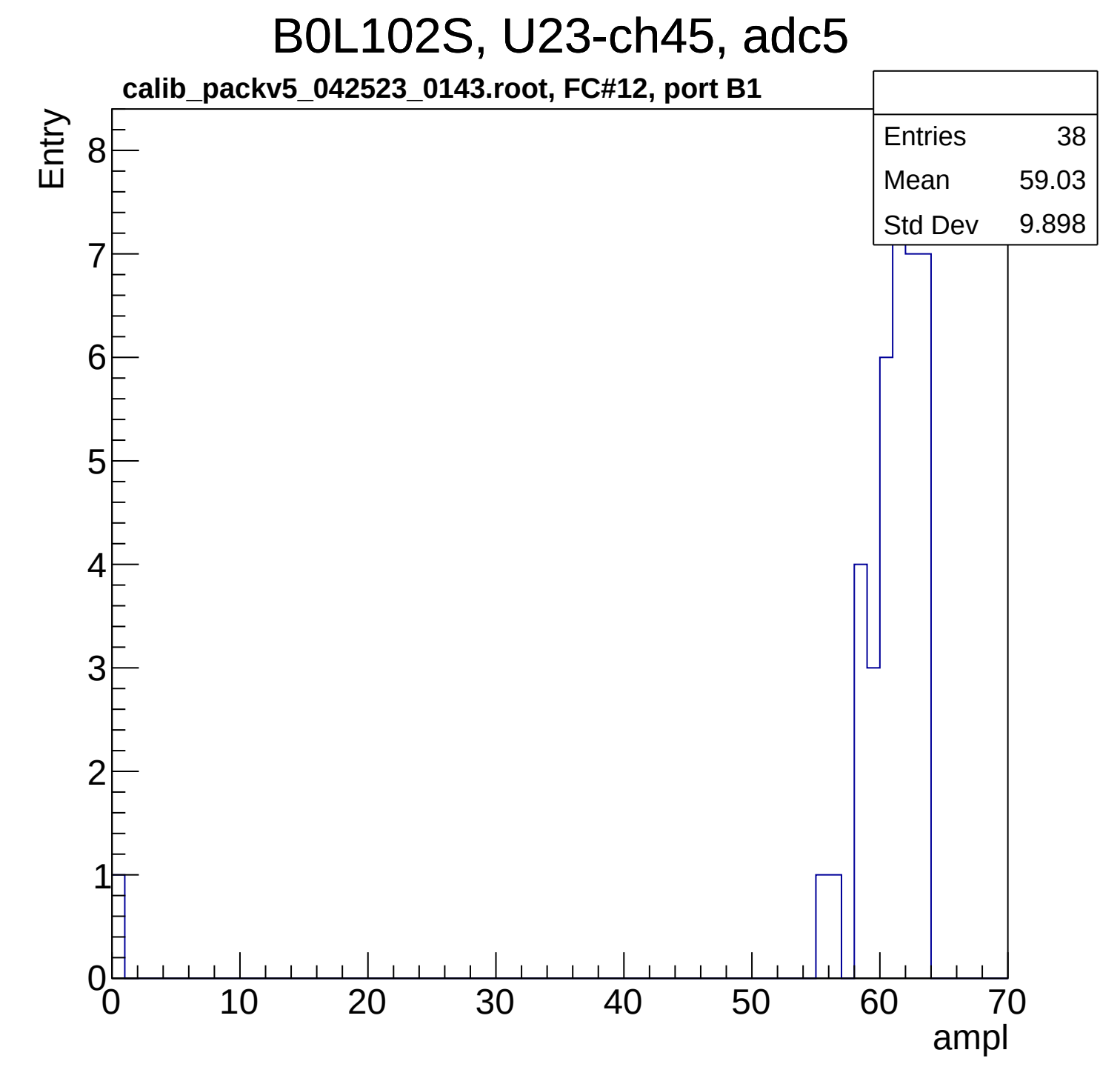
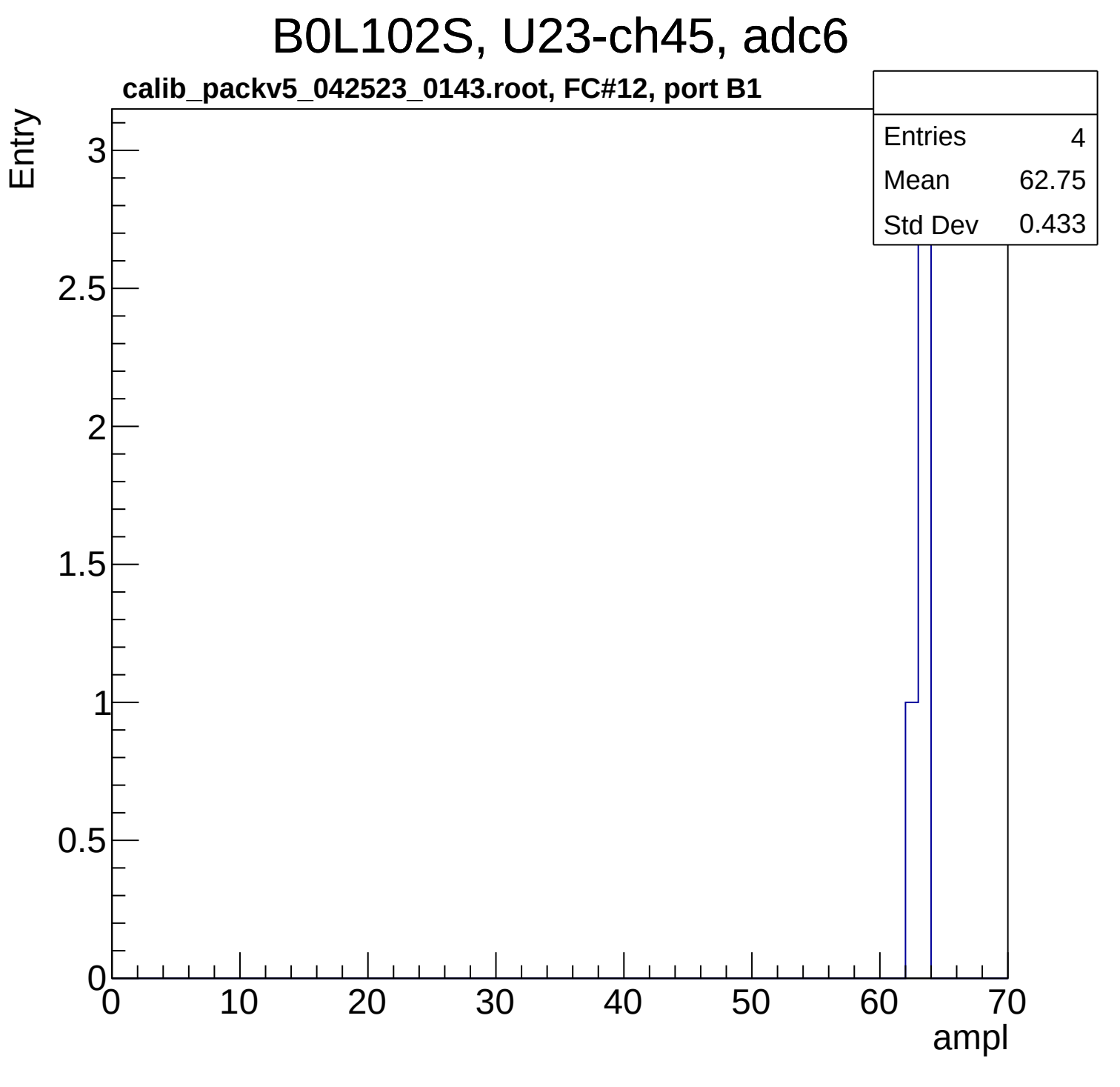
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch45, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 59.03
Std Dev 9.898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch45, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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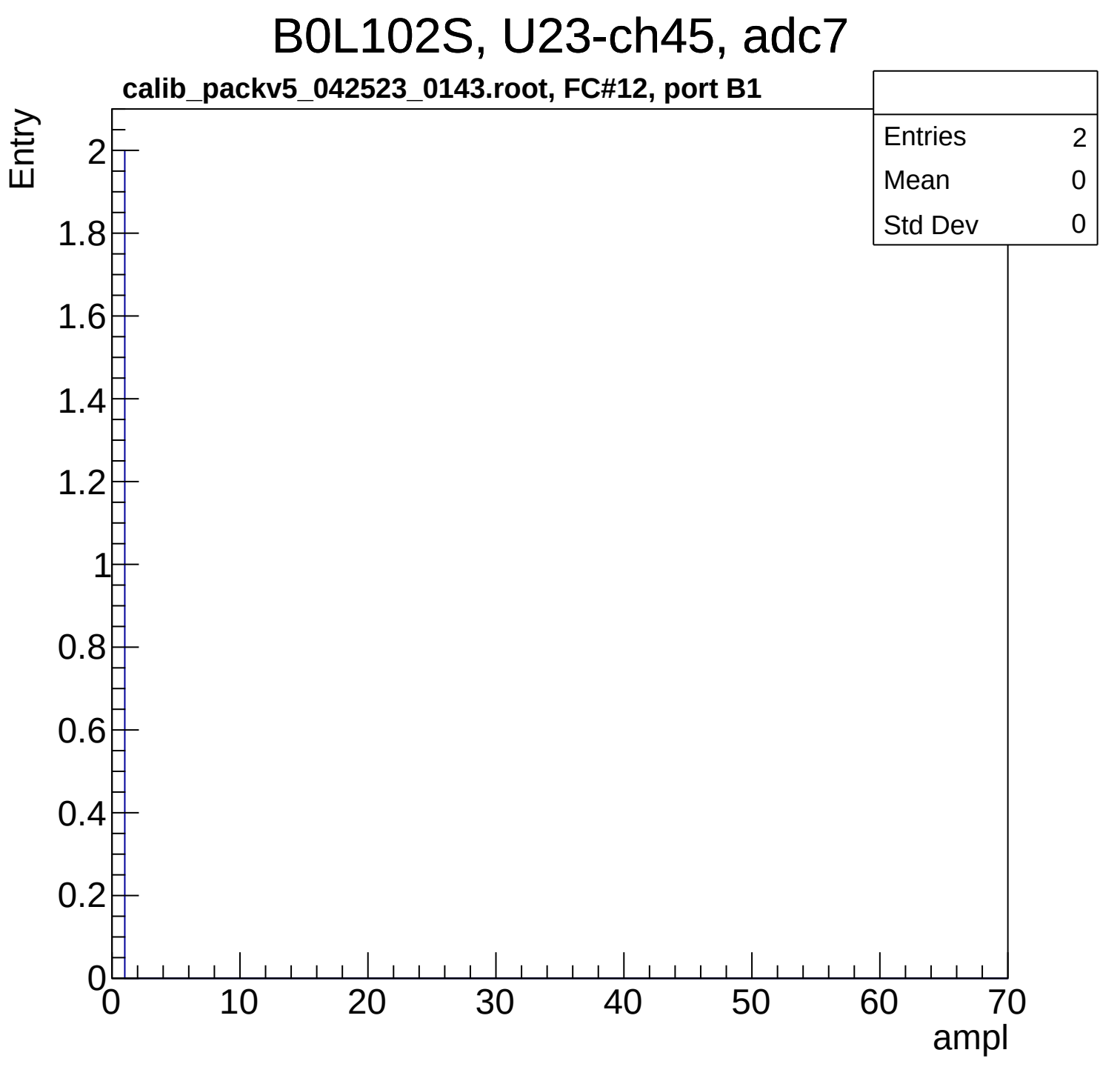
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch45, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
20
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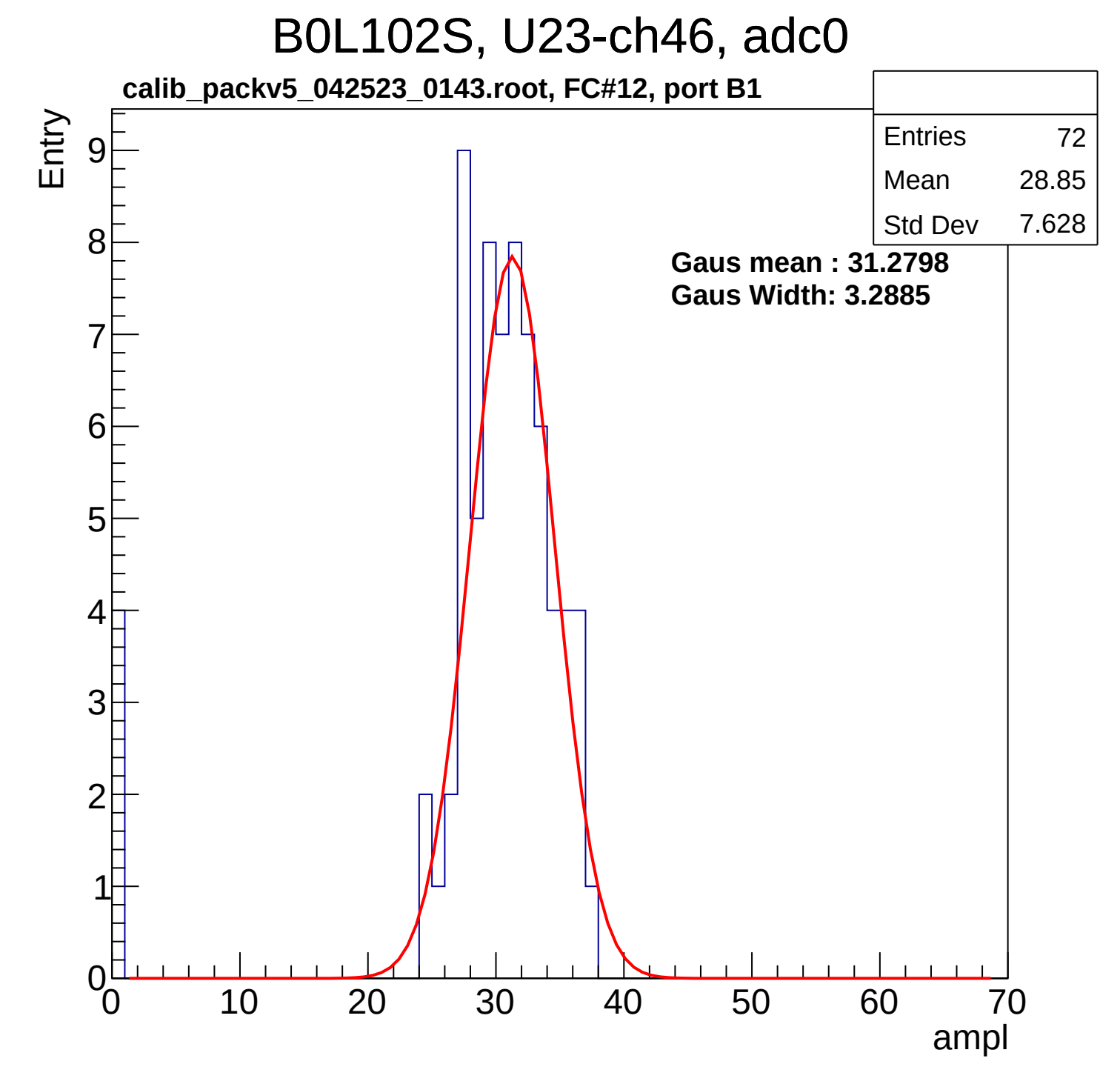
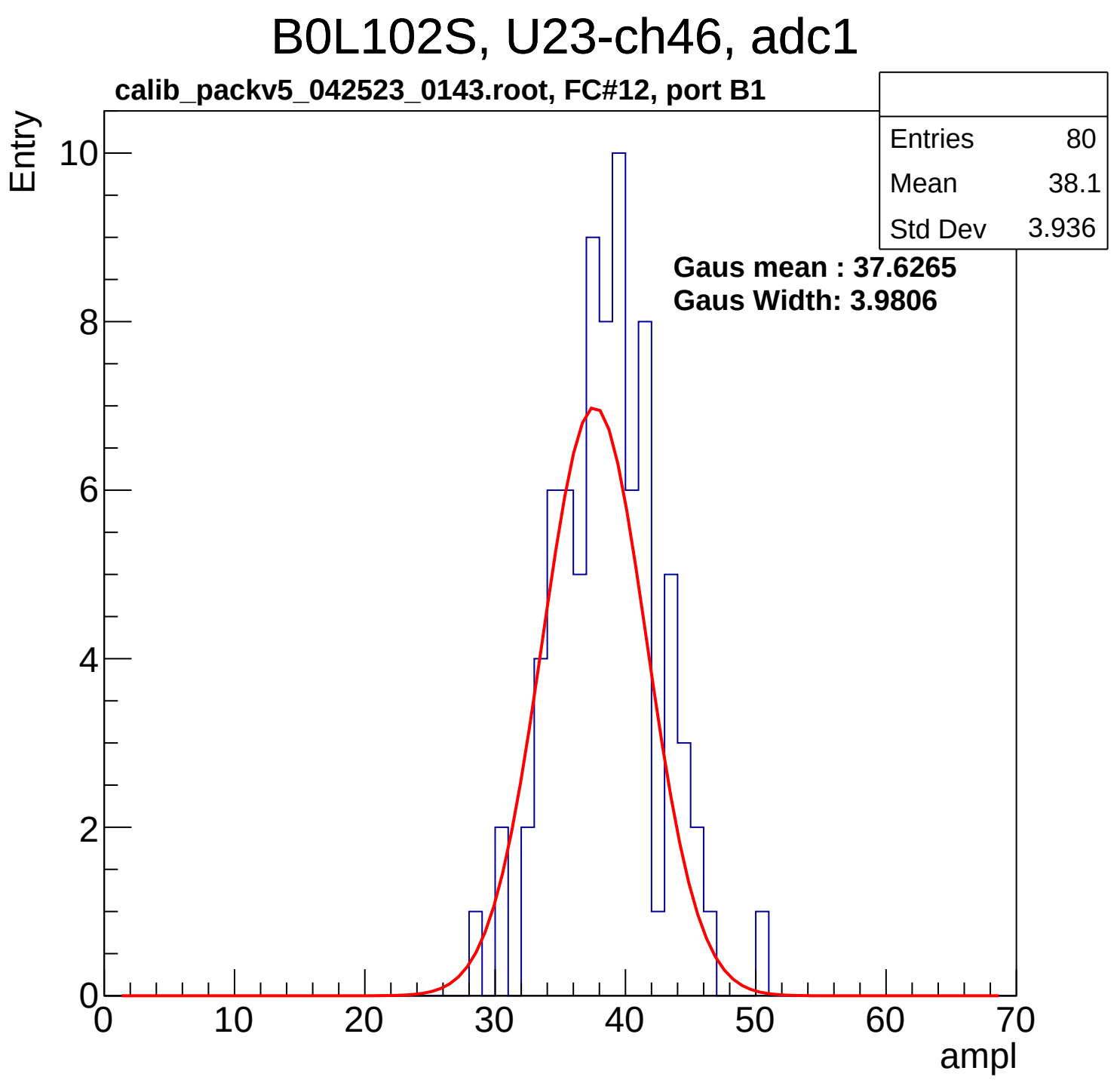
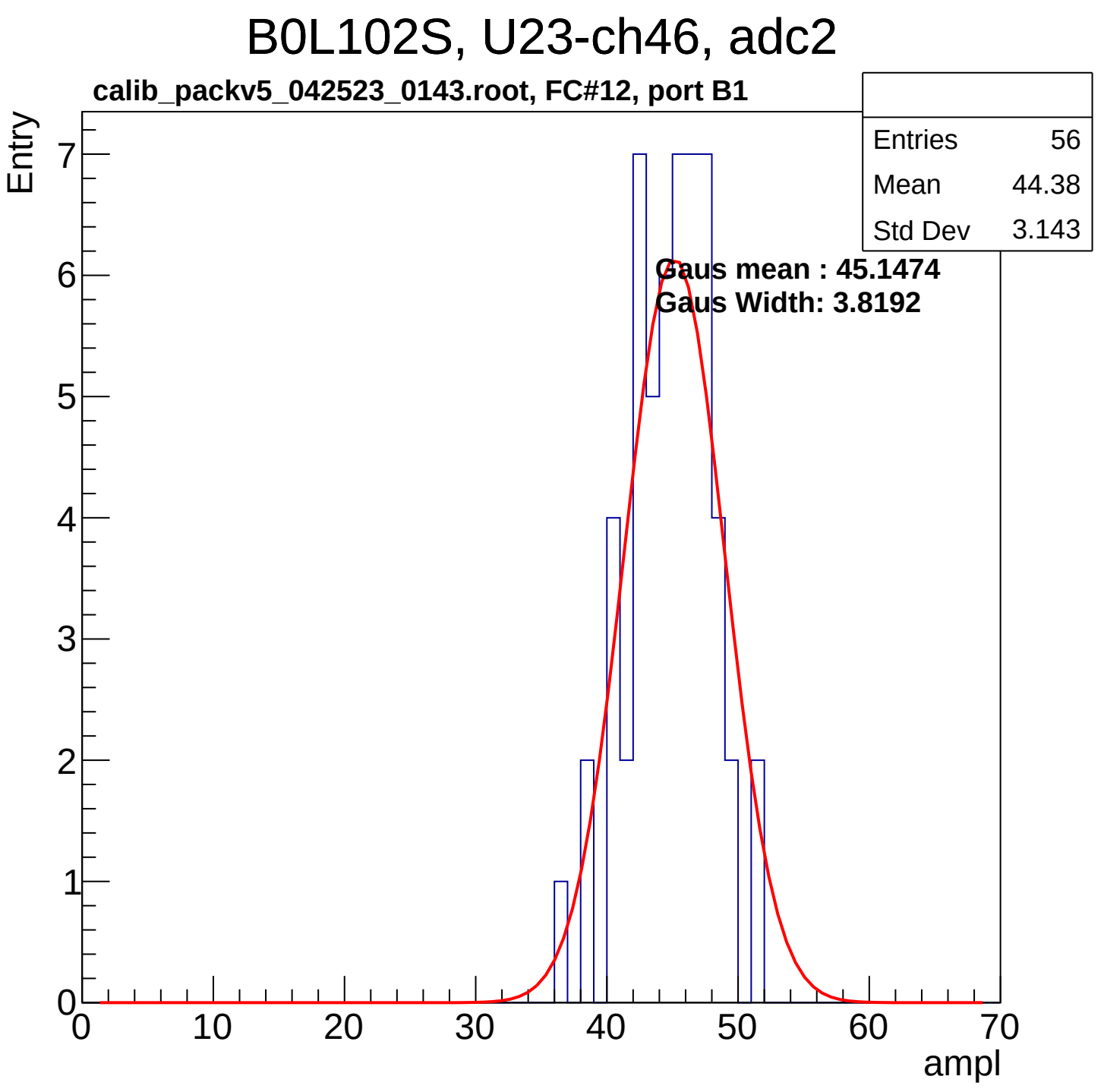
50
60
70
ampl


B0L102S, U23-ch46, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 28.85
Std Dev 7.628
Gaus mean : 31.2798
Gaus Width: 3.2885
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch46, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 38.1
Std Dev 3.936
Gaus mean : 37.6265
Gaus Width: 3.9806
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch46, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 44.38
Std Dev 3.143
Gaus mean : 45.1474
Gaus Width: 3.8192
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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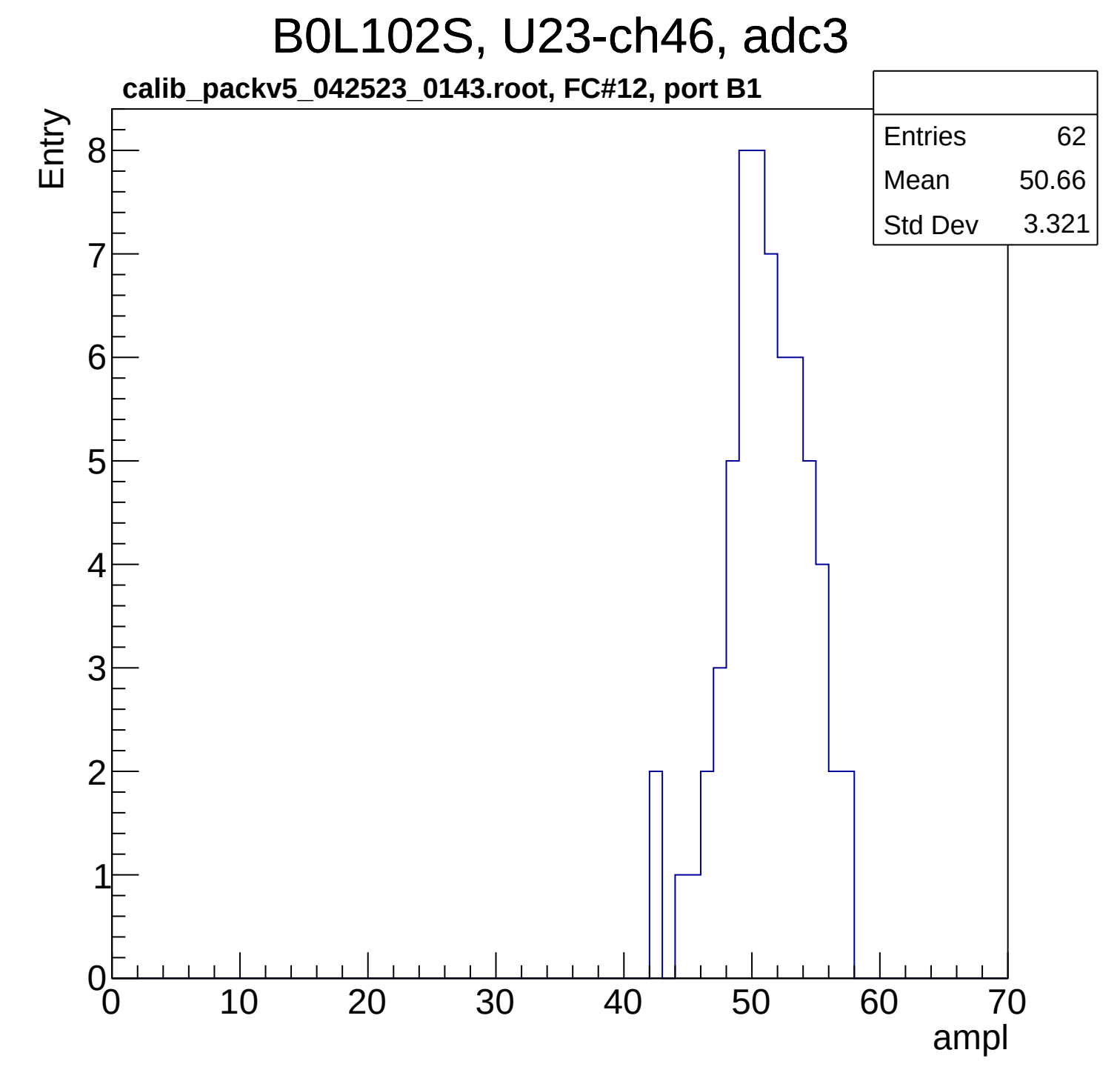
B0L102S, U23-ch46, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 50.66
Std Dev 3.321
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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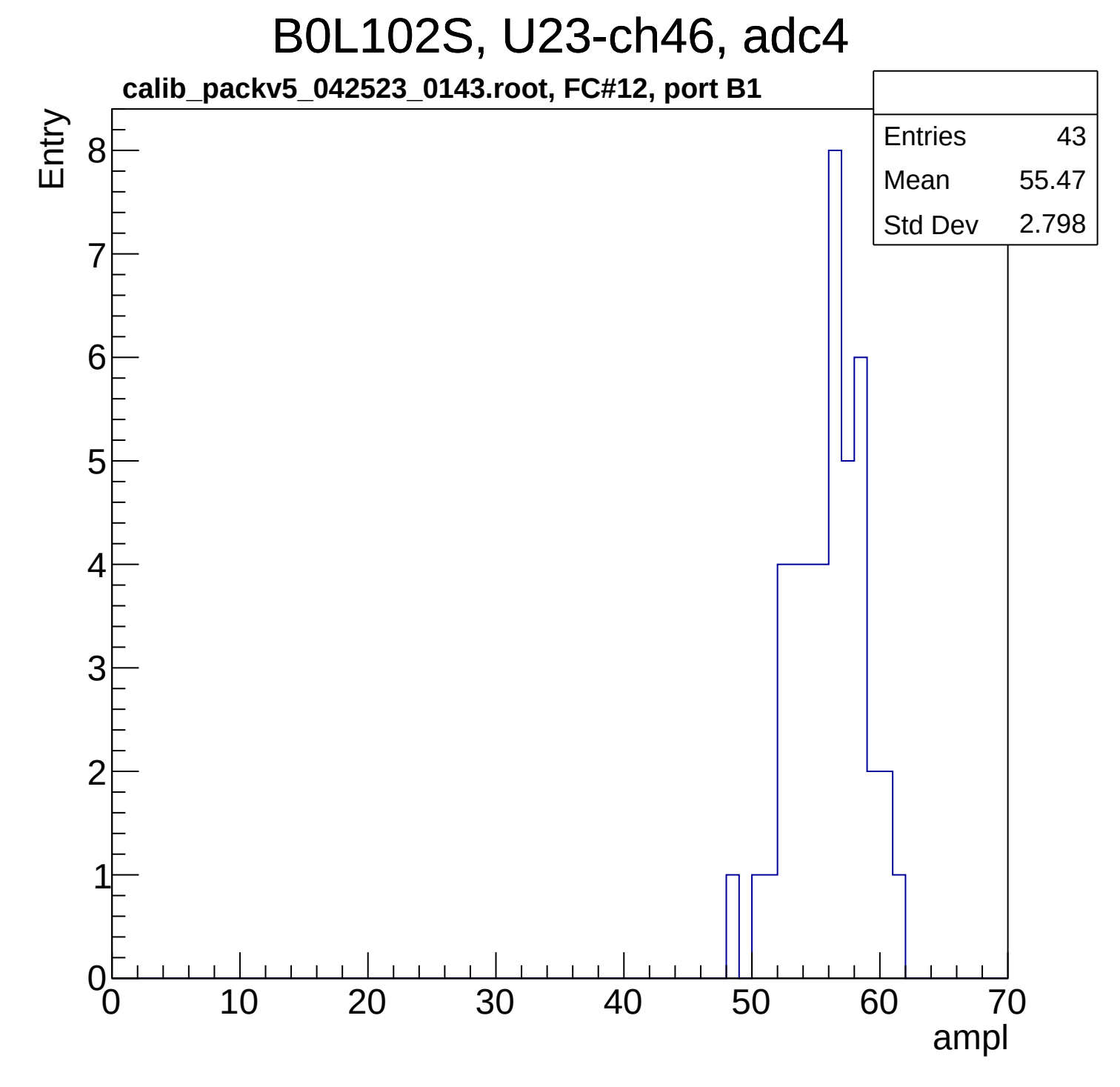
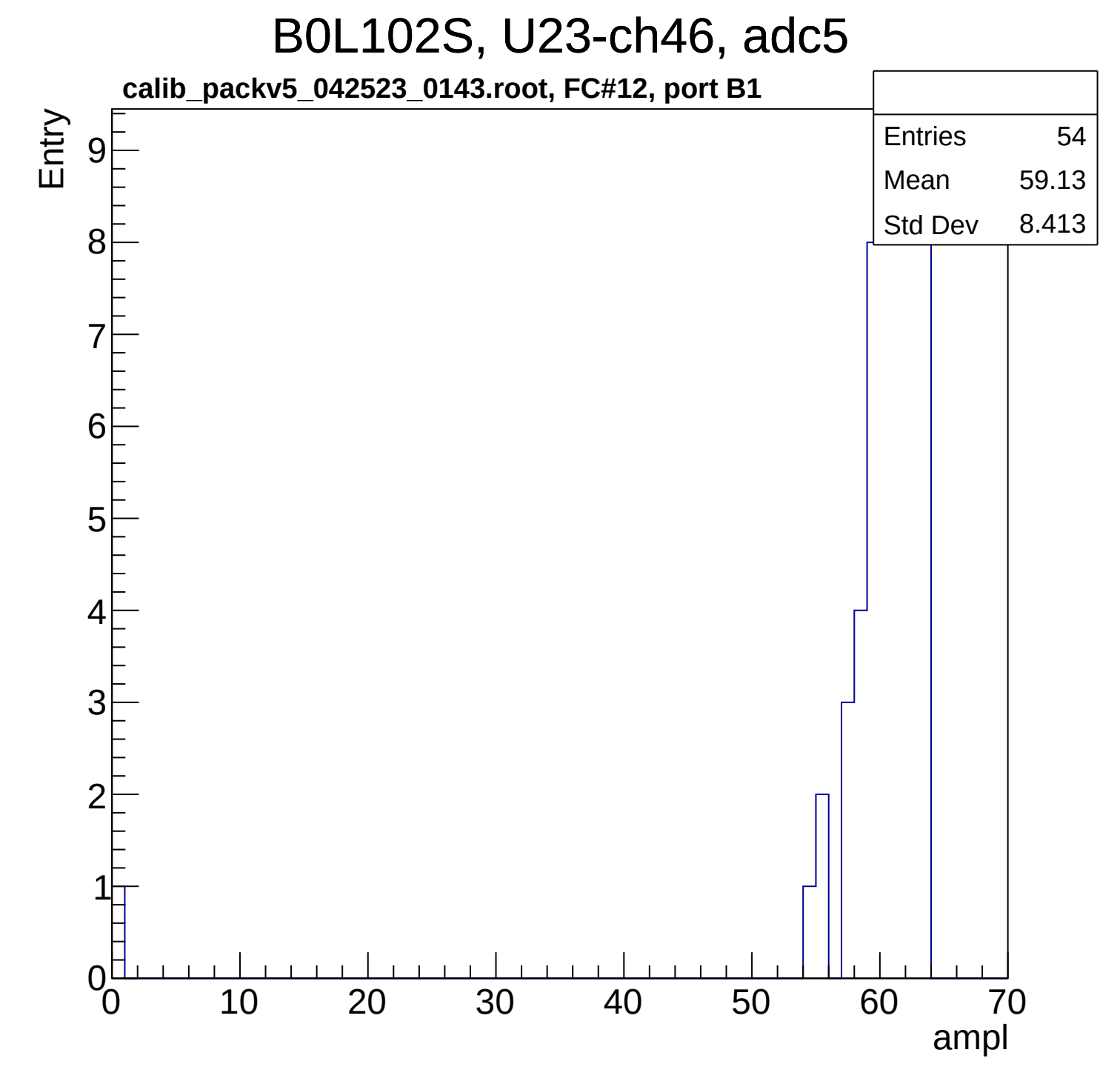

B0L102S, U23-ch46, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.47
Std Dev 2.798
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch46, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 59.13
Std Dev 8.413
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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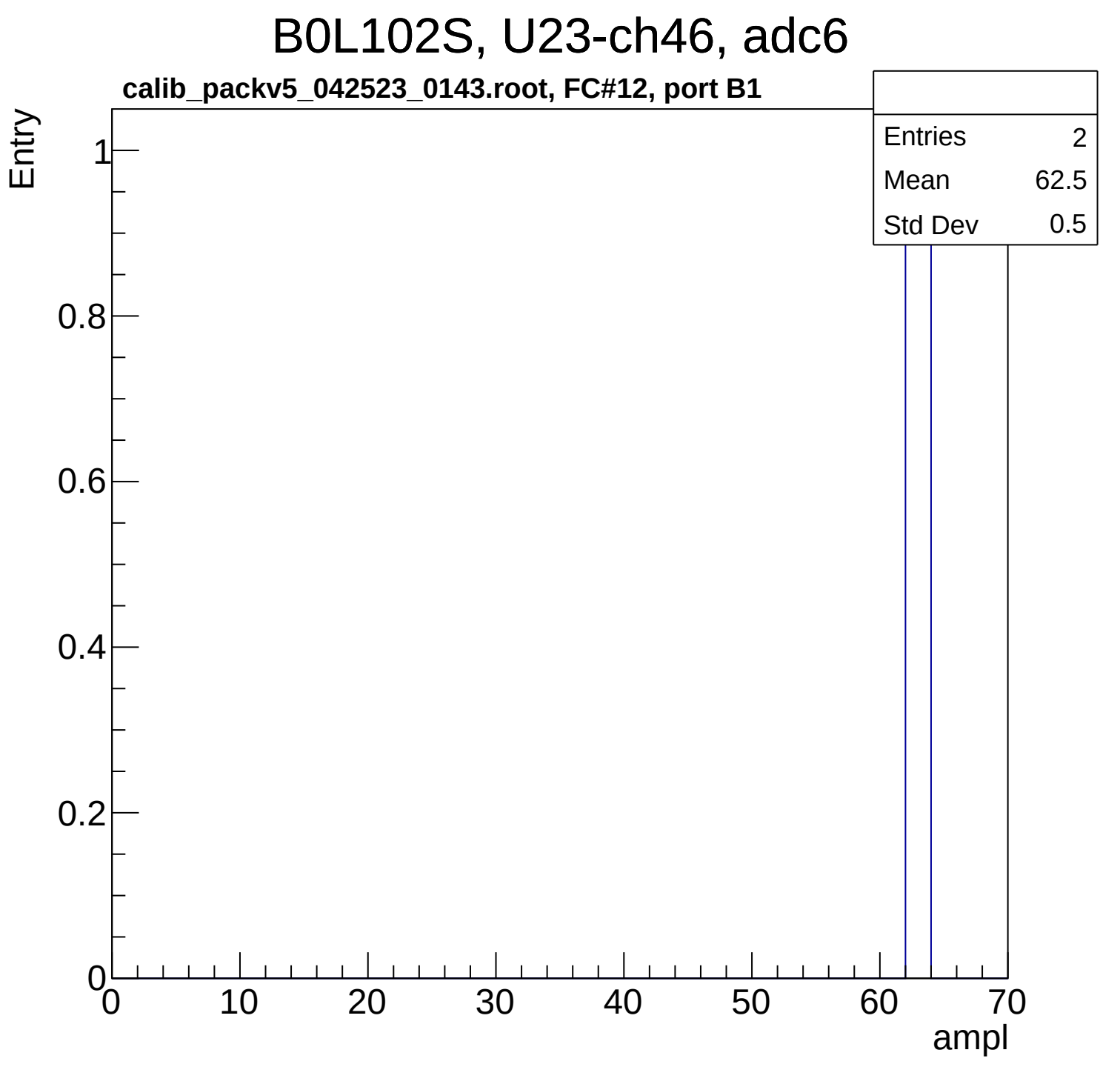
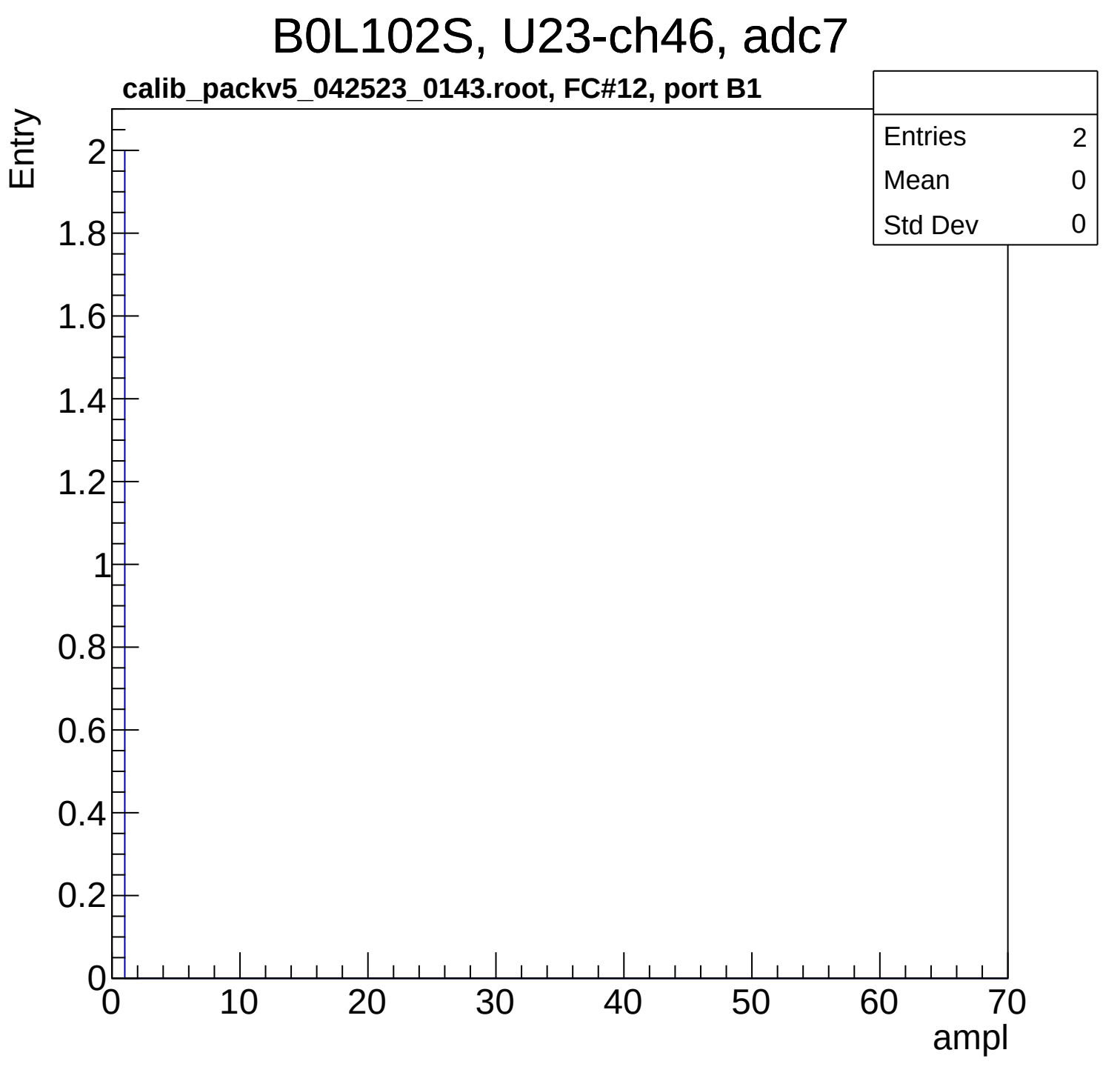
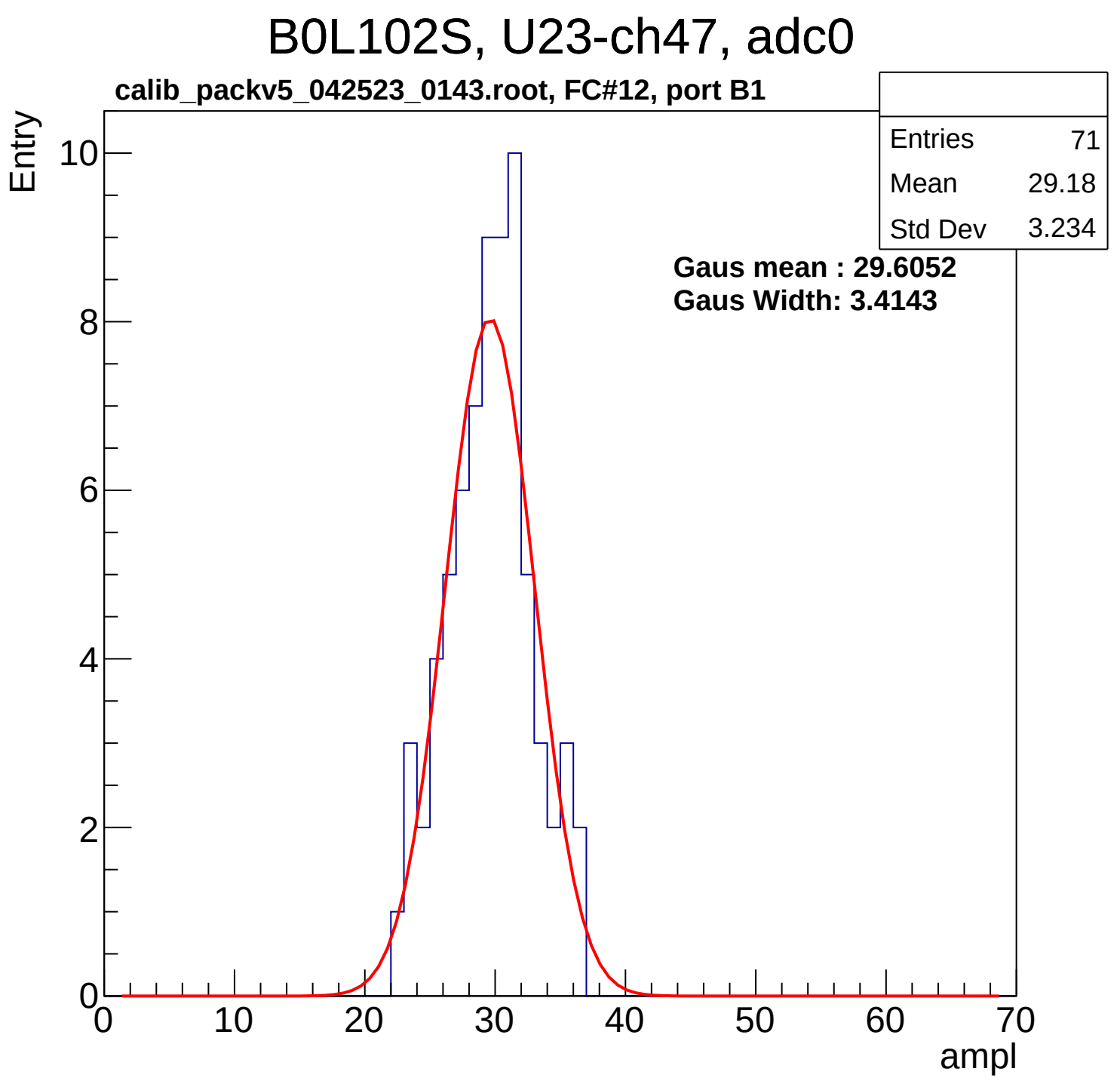
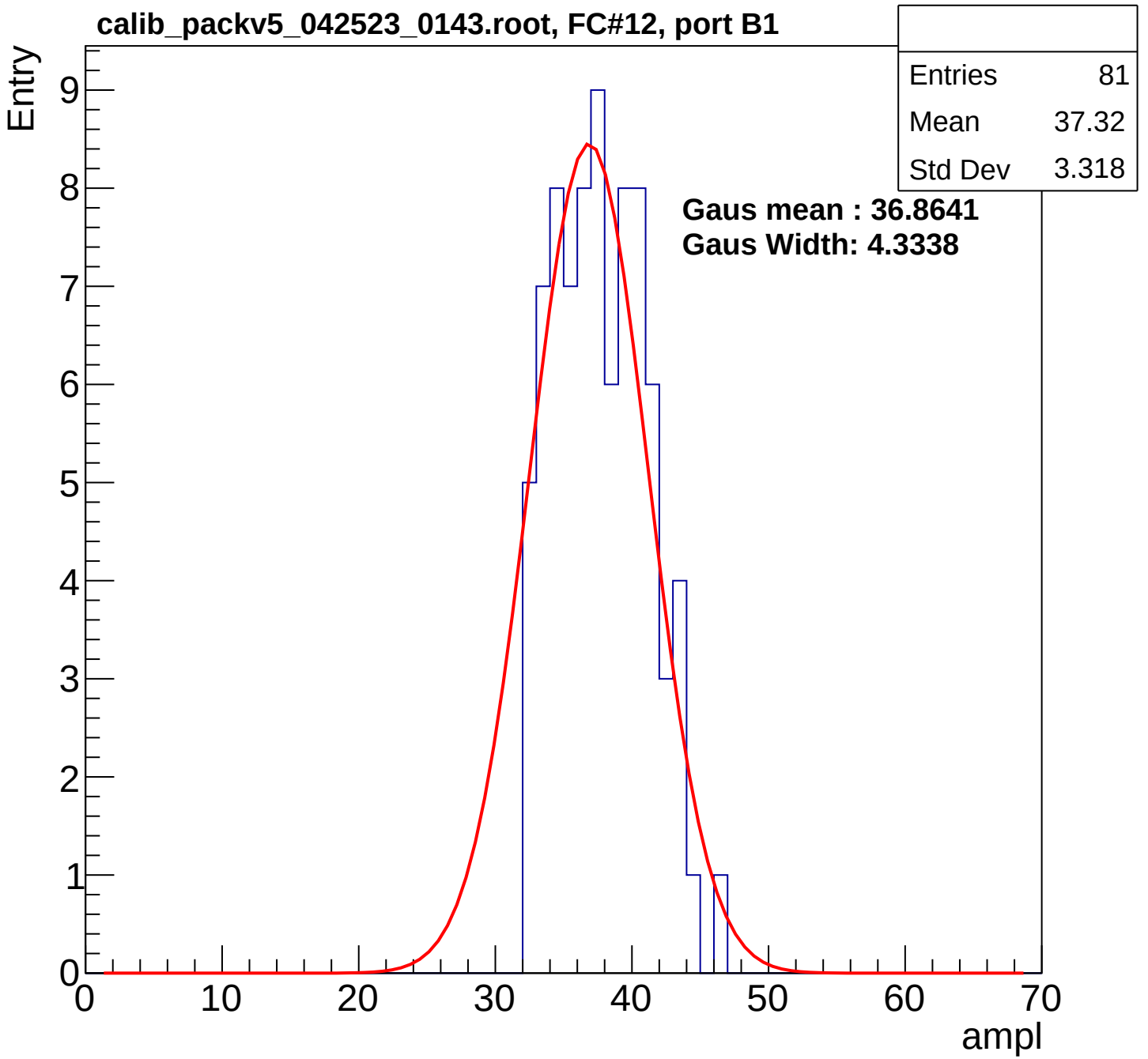
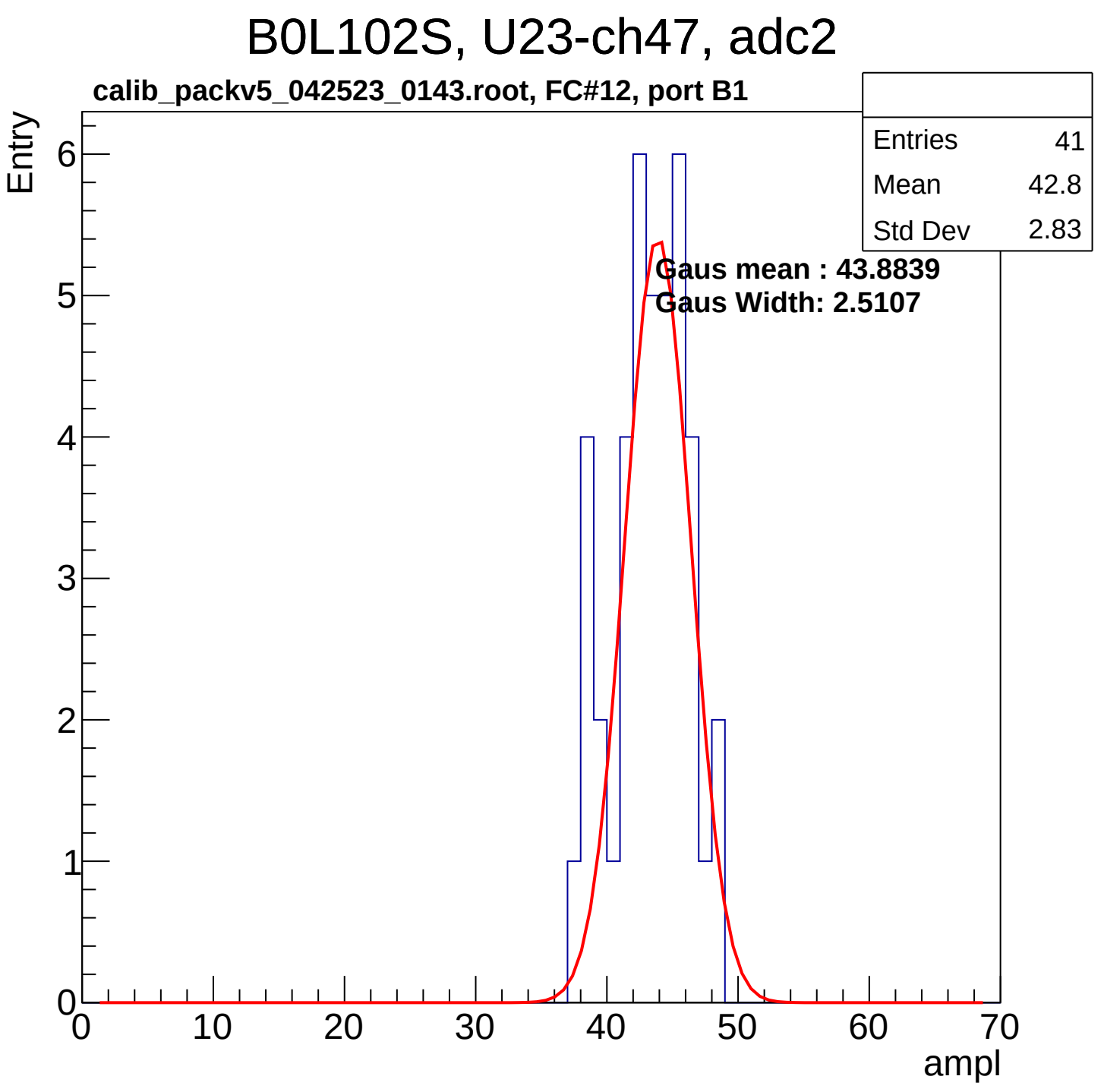
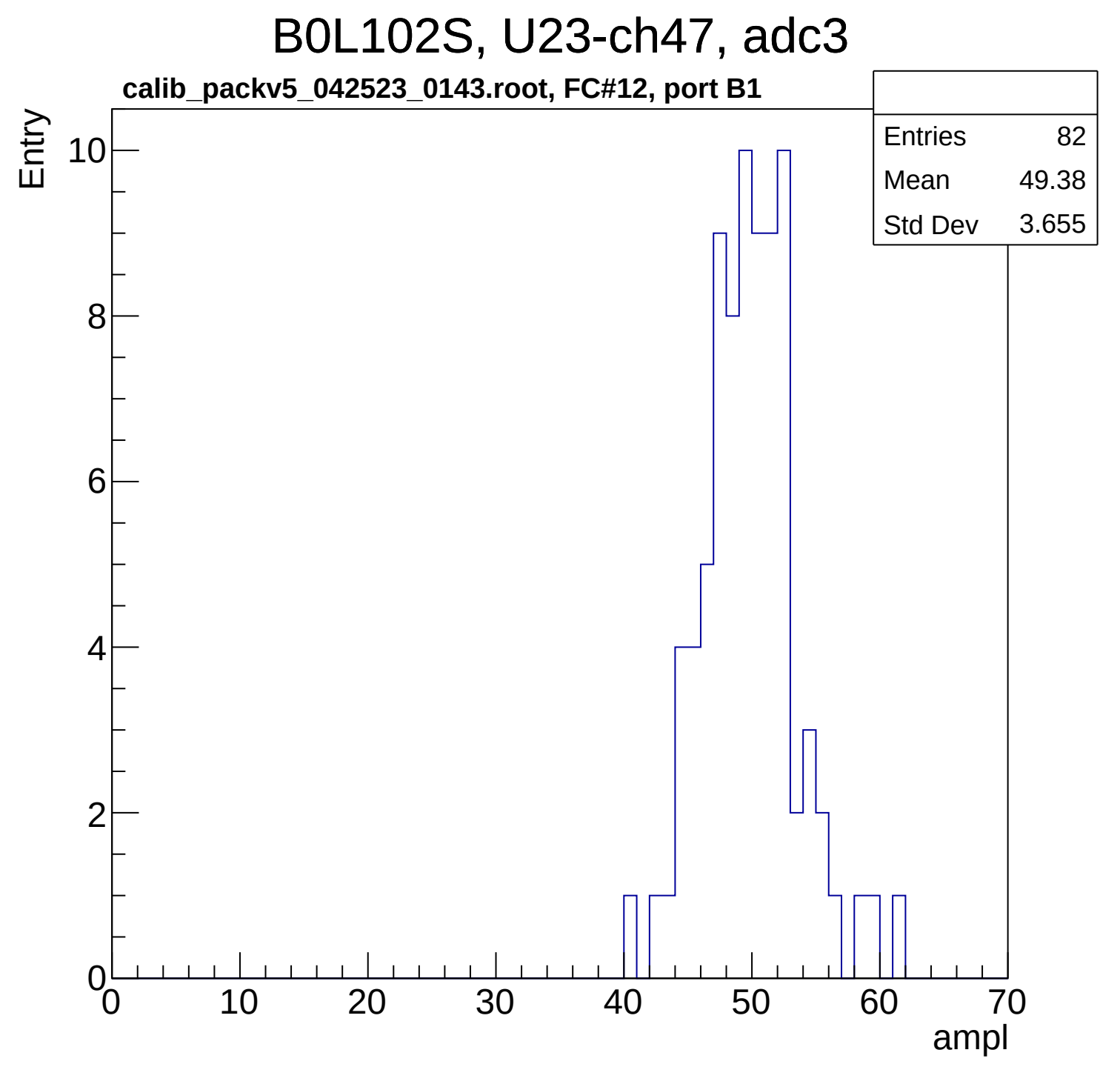

B0L102S, U23-ch46, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

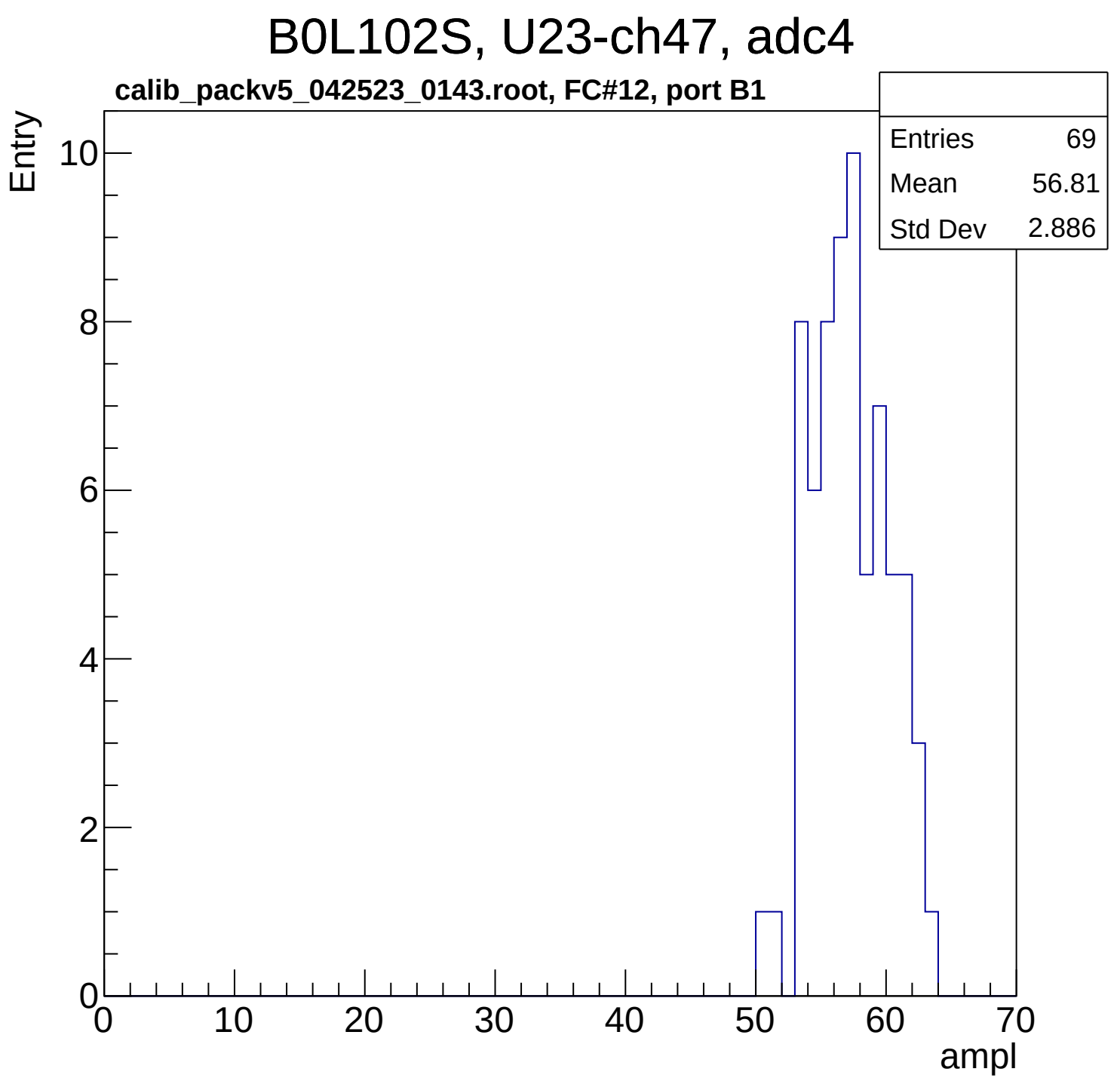

B0L102S, U23-ch46, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

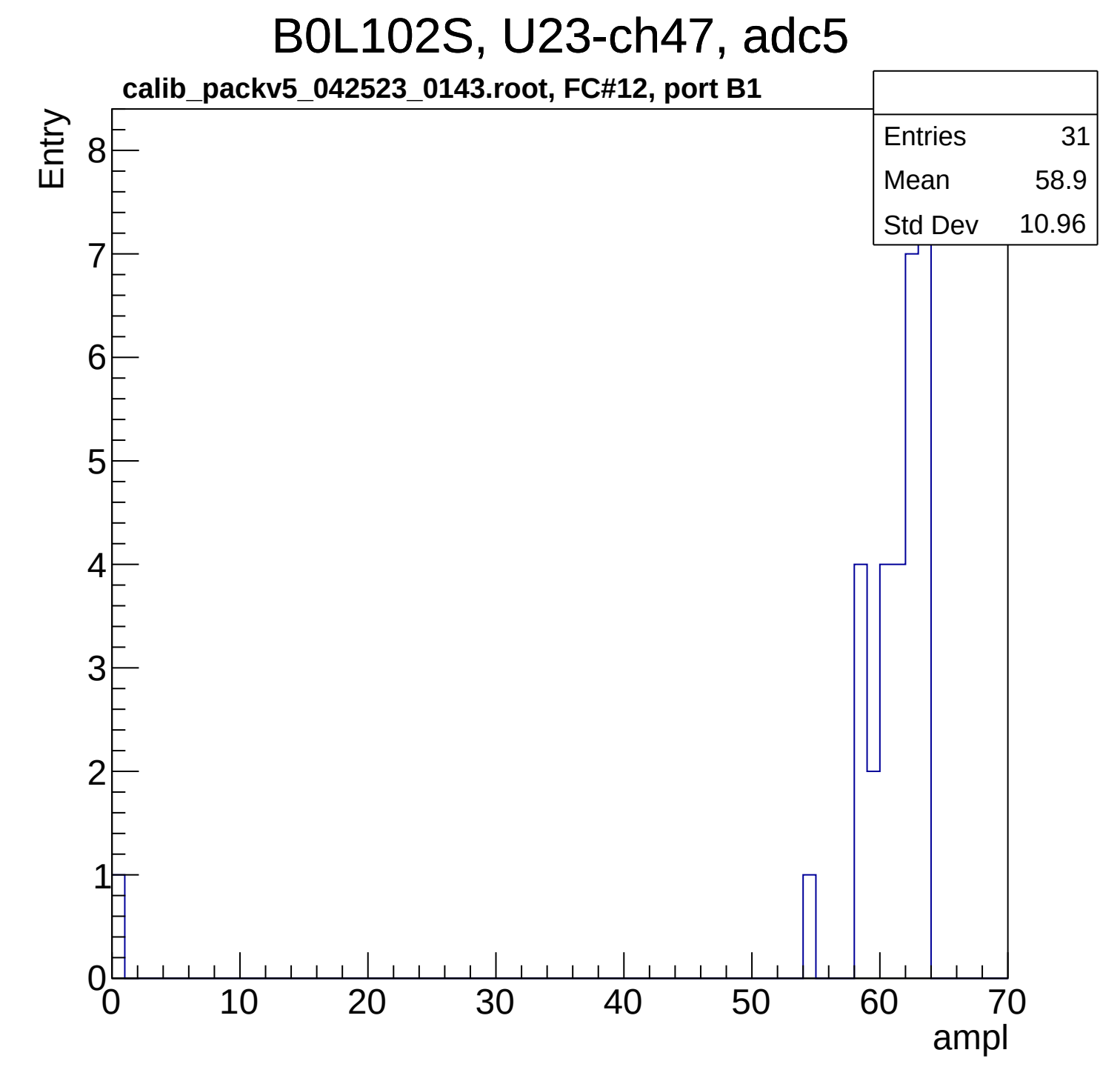

B0L102S, U23-ch47, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 29.18
Std Dev 3.234
Gaus mean : 29.6052
Gaus Width: 3.4143
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

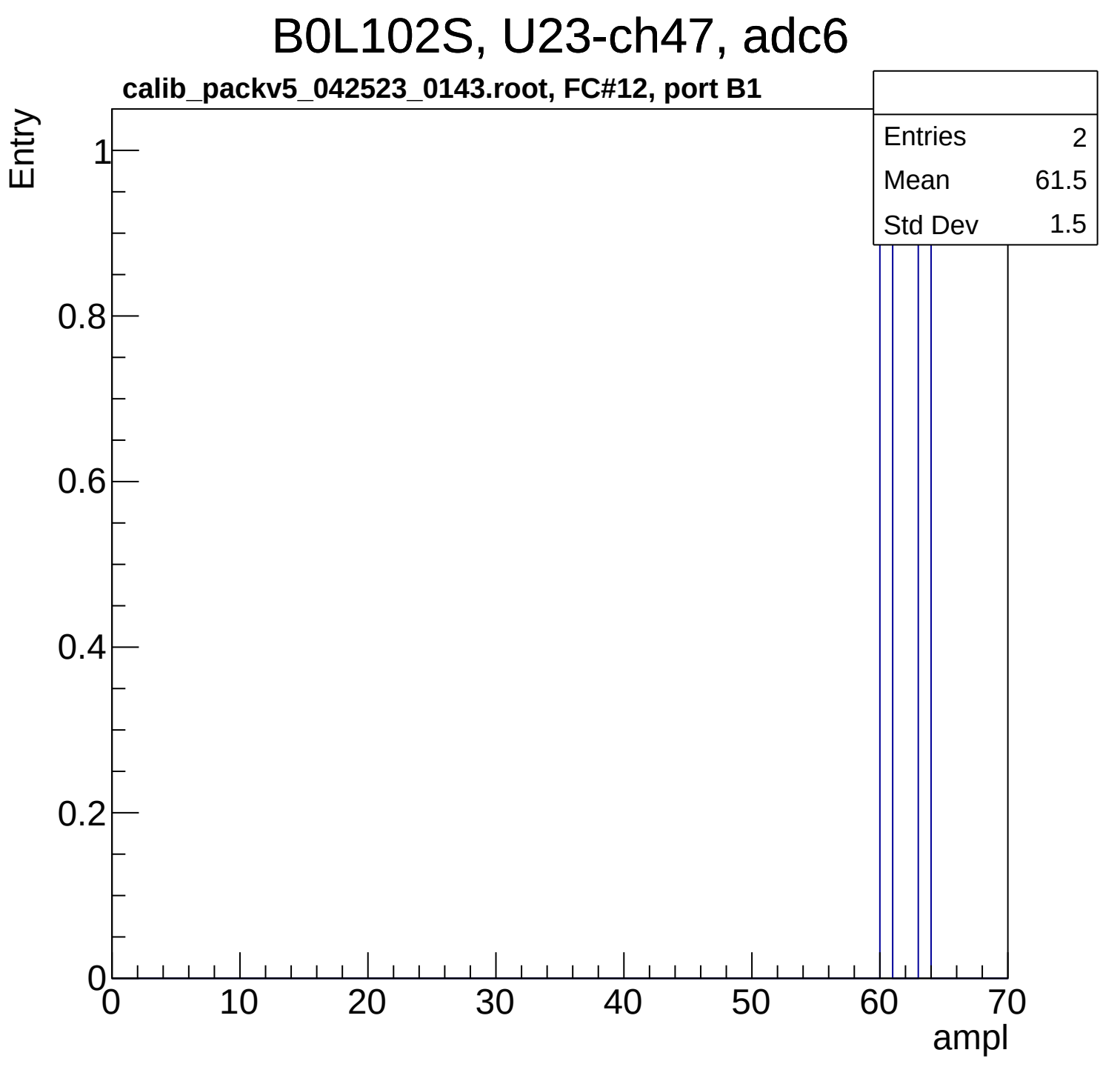

B0L102S, U23-ch47, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 37.32
Std Dev 3.318
Gaus mean : 36.8641
Gaus Width: 4.3338
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

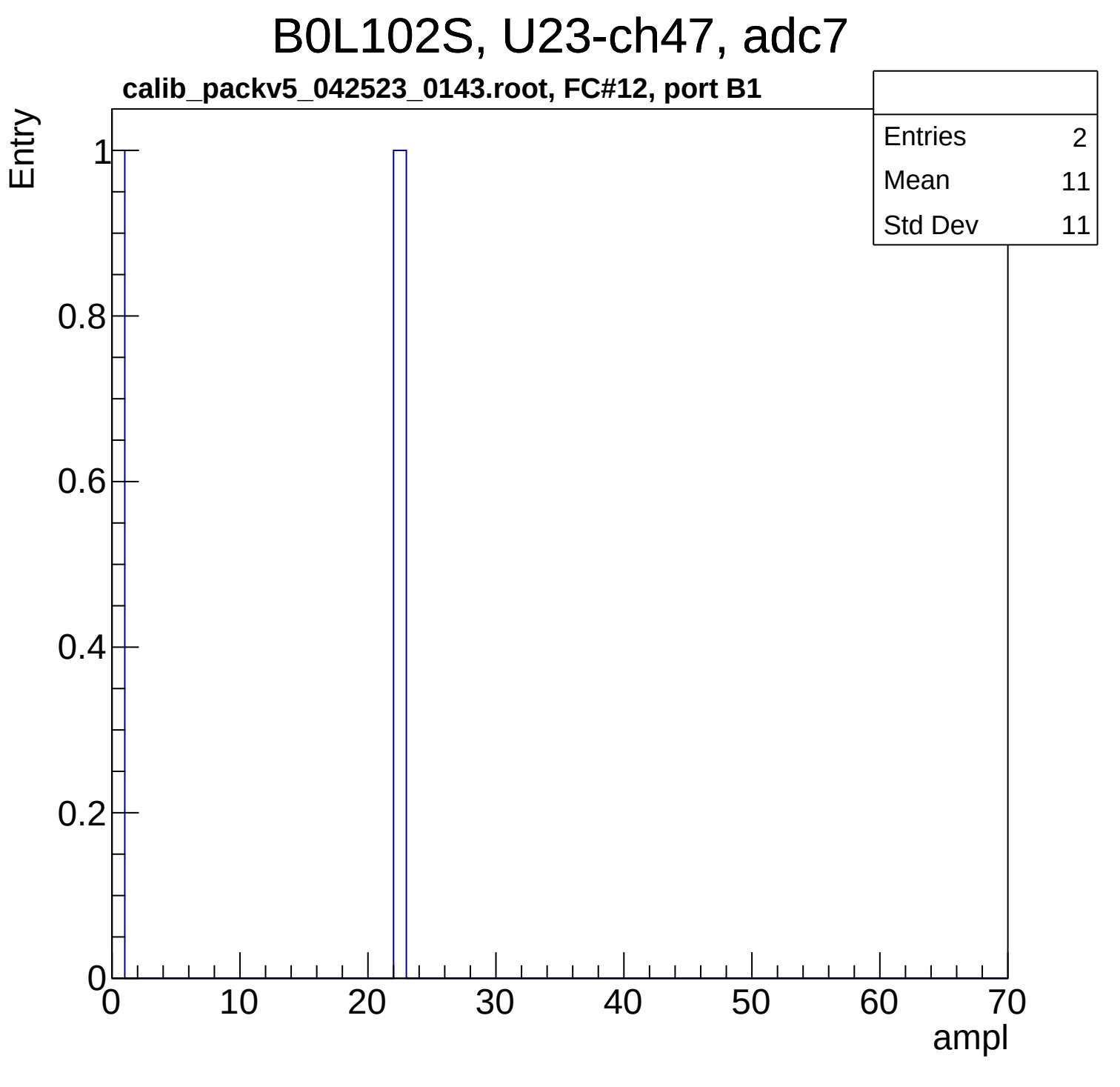

B0L102S, U23-ch47, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 42.8
Std Dev 2.83
Gaus mean : 43.8839
Gaus Width: 2.5107
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

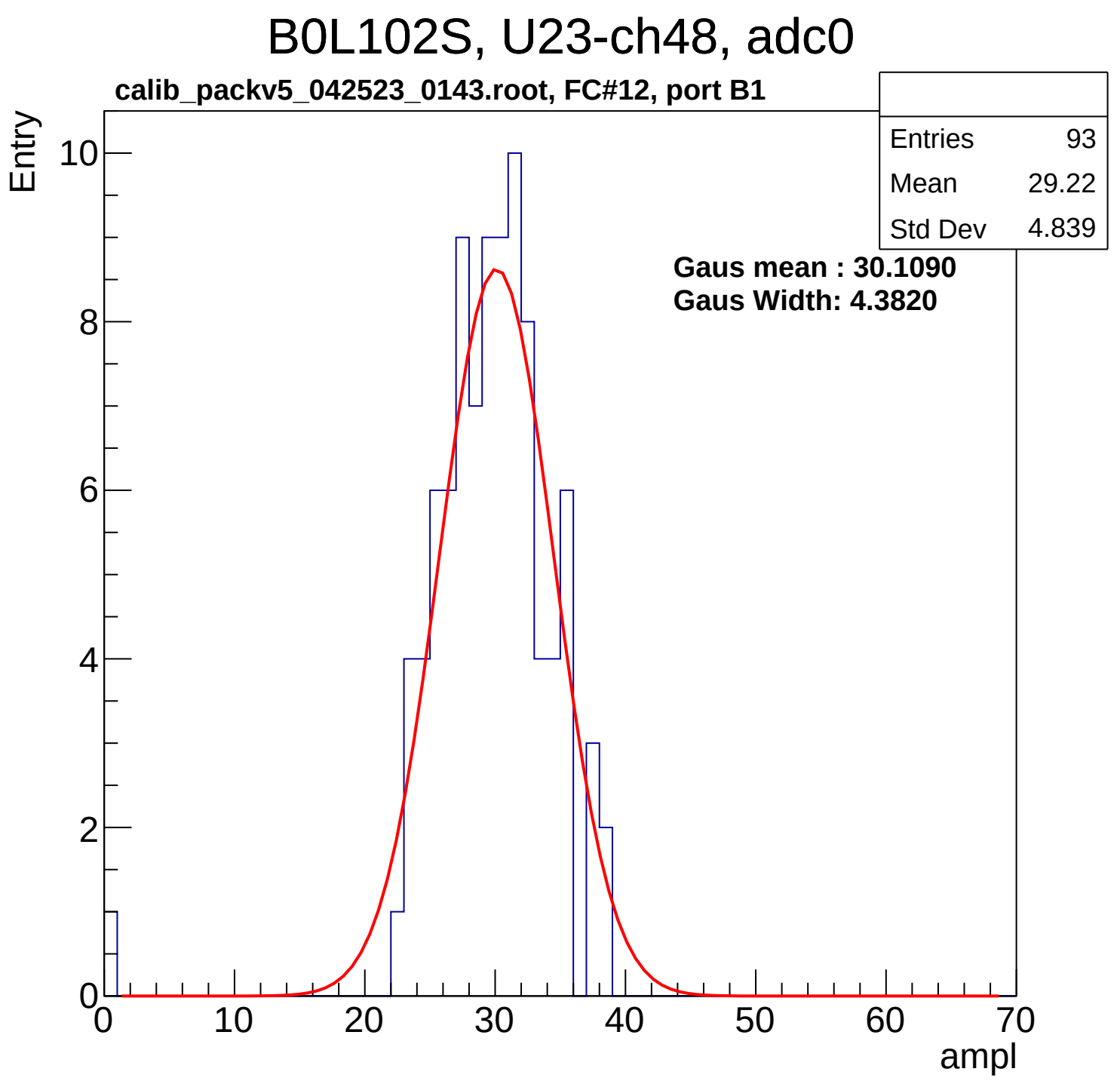

B0L102S, U23-ch47, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 49.38
Std Dev 3.655
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

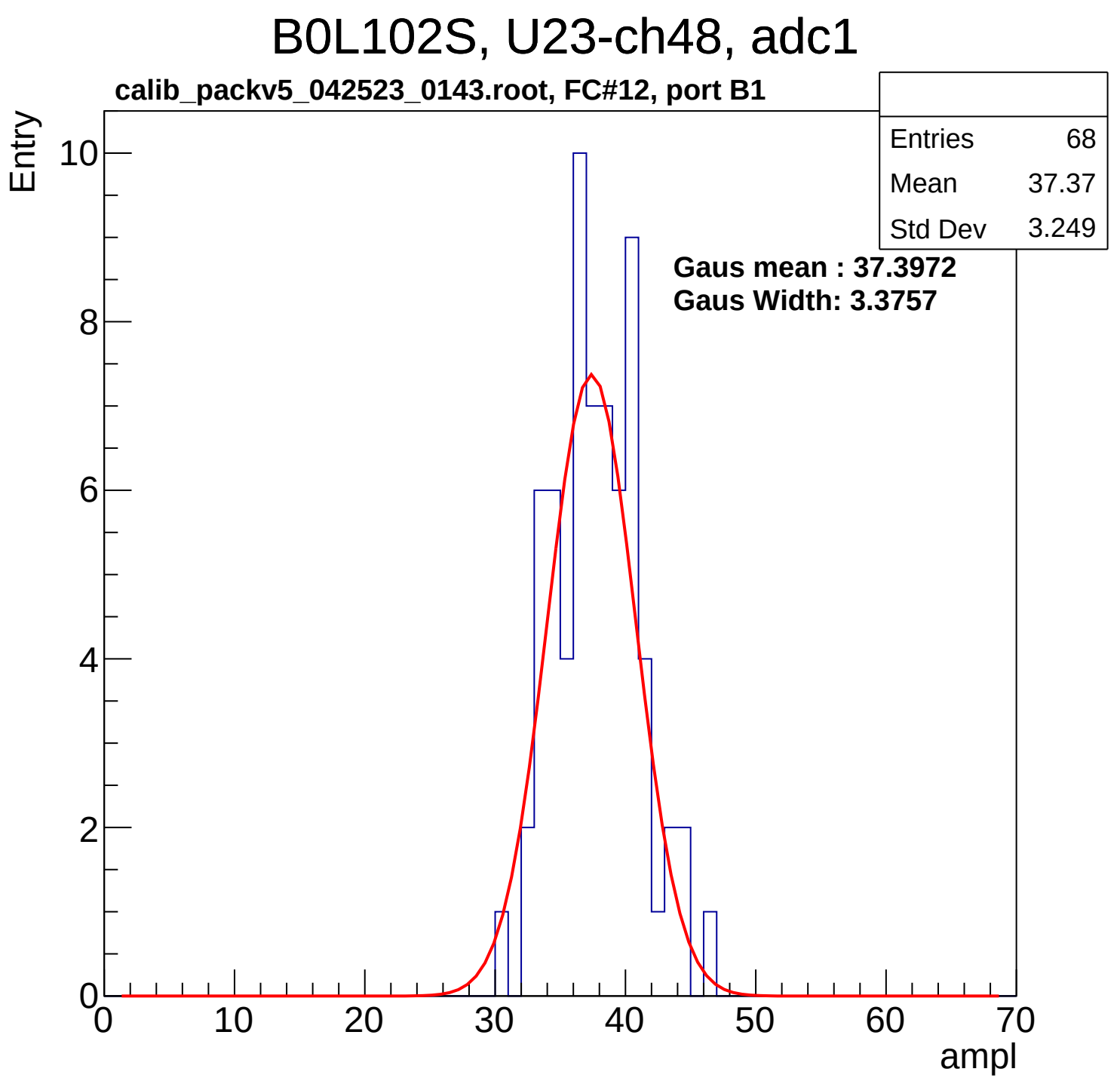

B0L102S, U23-ch47, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 56.81
Std Dev 2.886
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

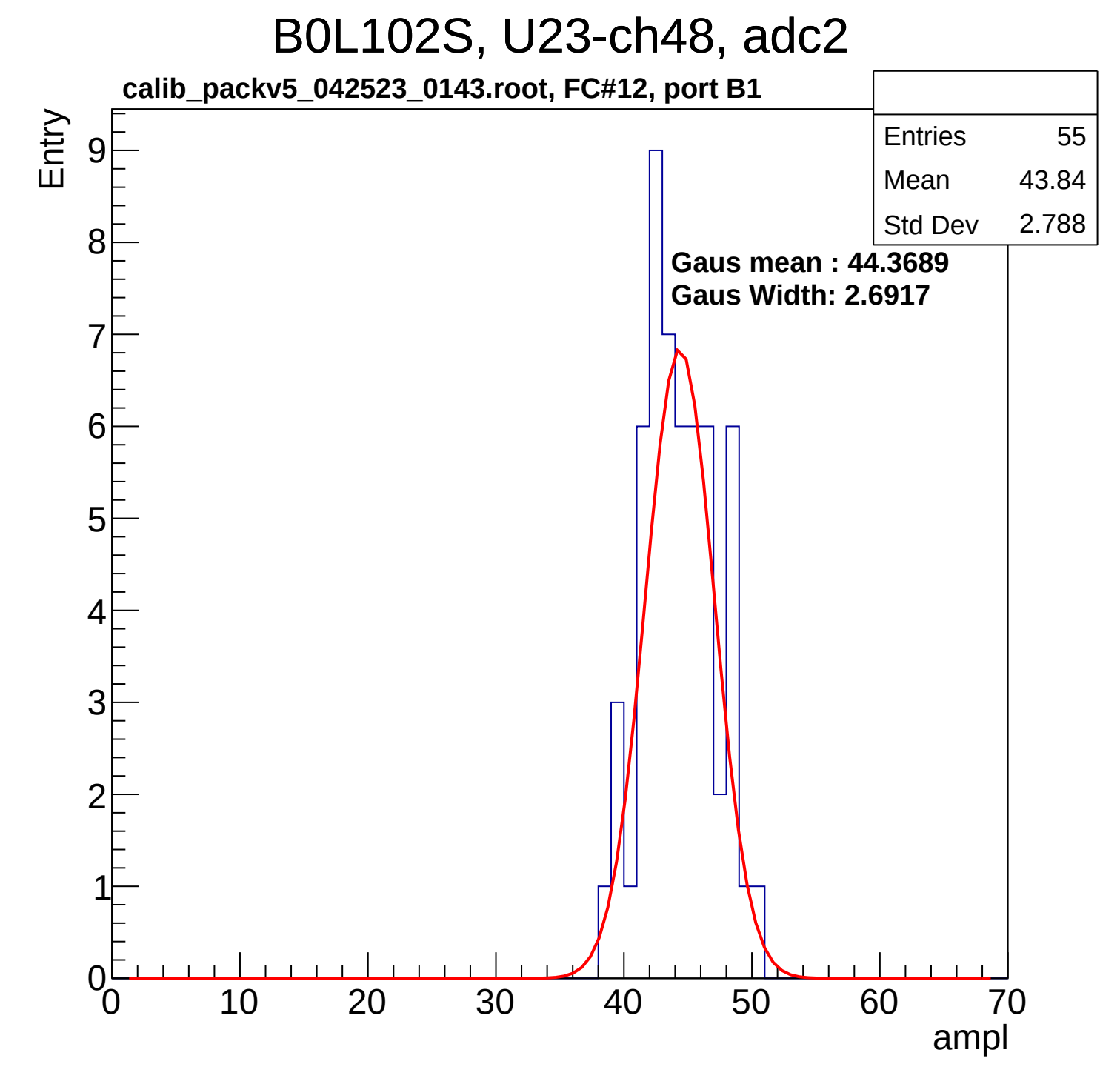

B0L102S, U23-ch47, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 58.9
Std Dev 10.96
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

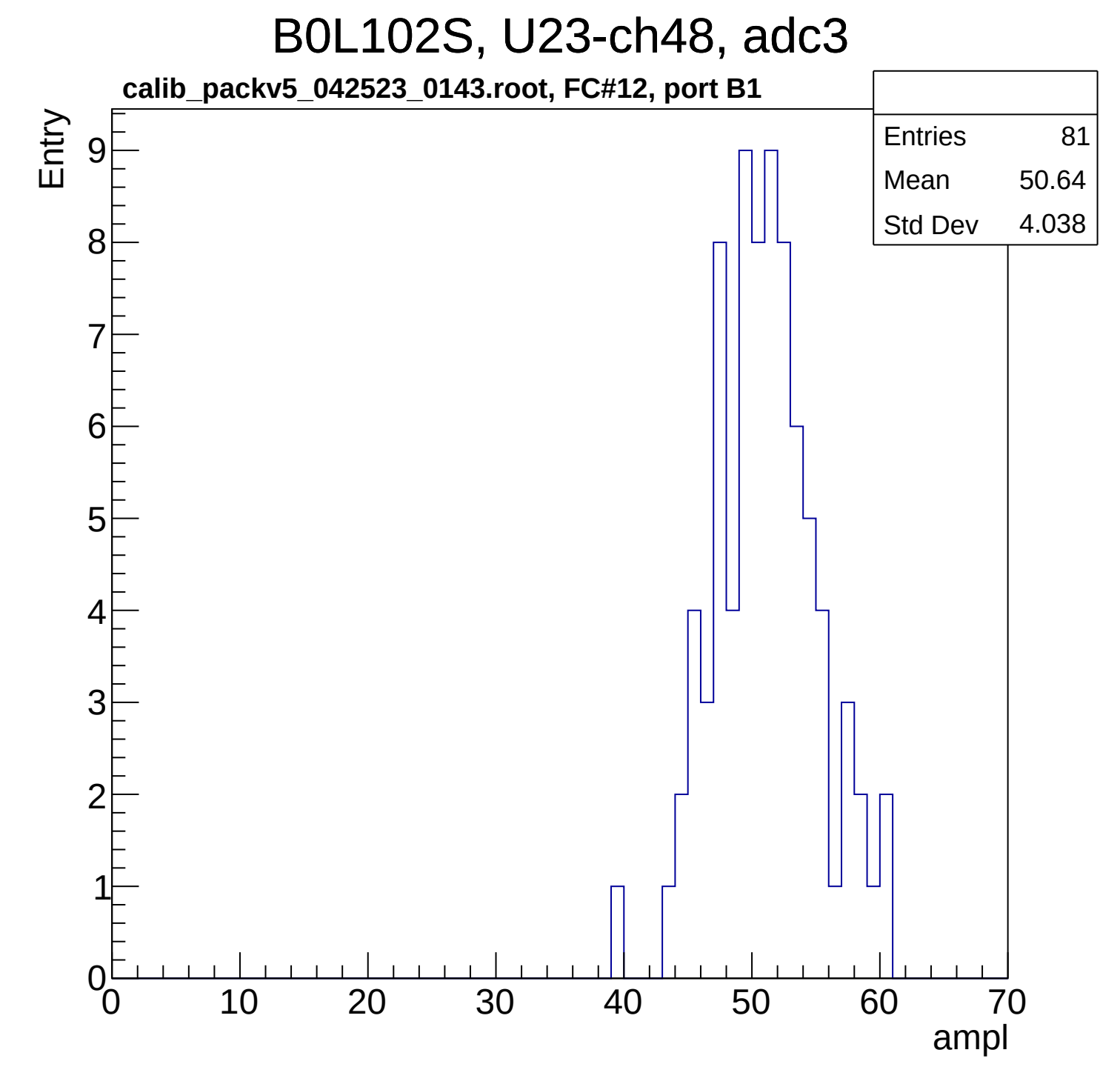

B0L102S, U23-ch47, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch47, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch48, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 93
Mean 29.22
Std Dev 4.839
Gaus mean : 30.1090
Gaus Width: 4.3820
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch48, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 37.37
Std Dev 3.249
Gaus mean : 37.3972
Gaus Width: 3.3757
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch48, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 43.84
Std Dev 2.788
Gaus mean : 44.3689
Gaus Width: 2.6917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch48, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 50.64
Std Dev 4.038
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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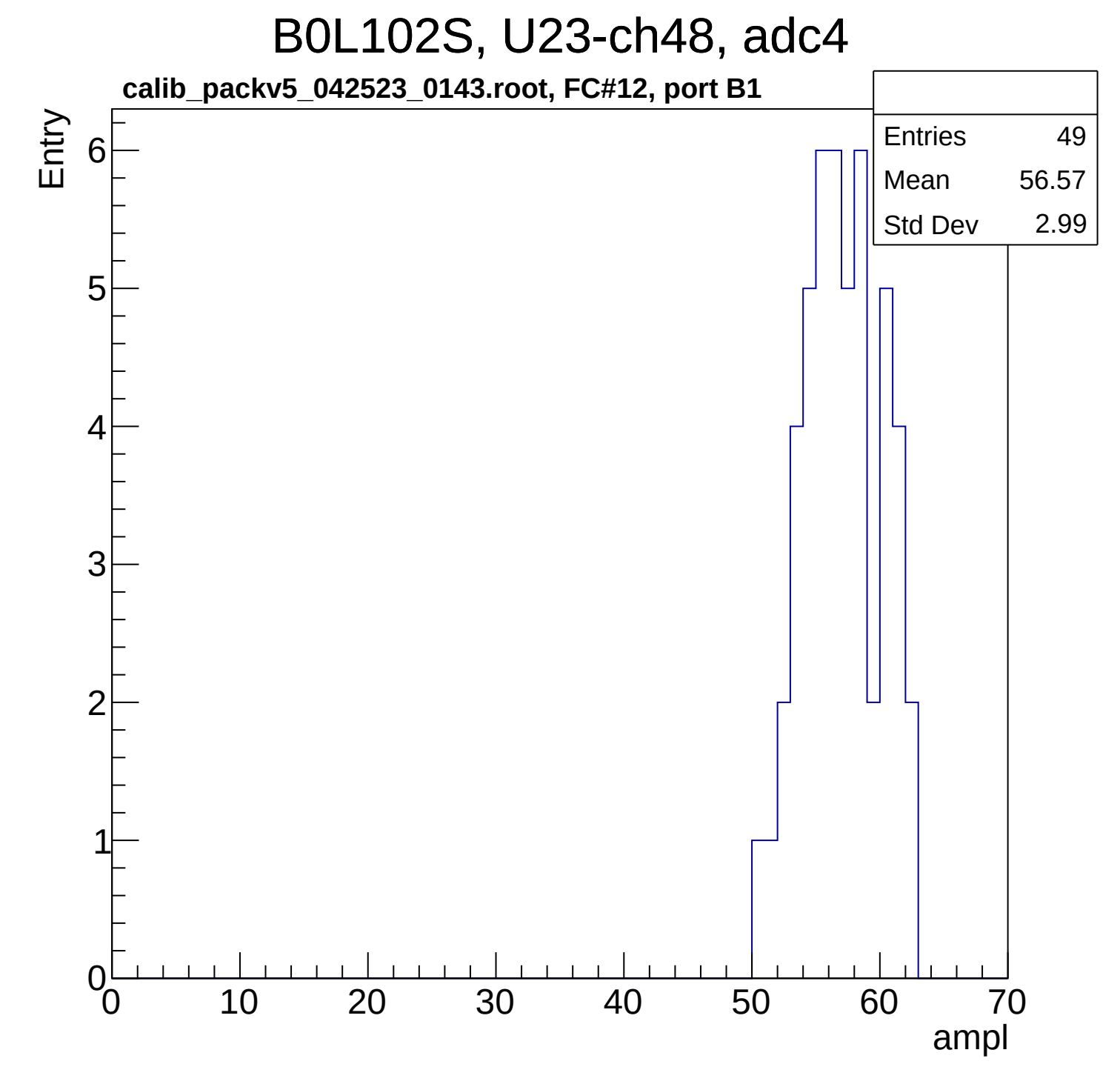
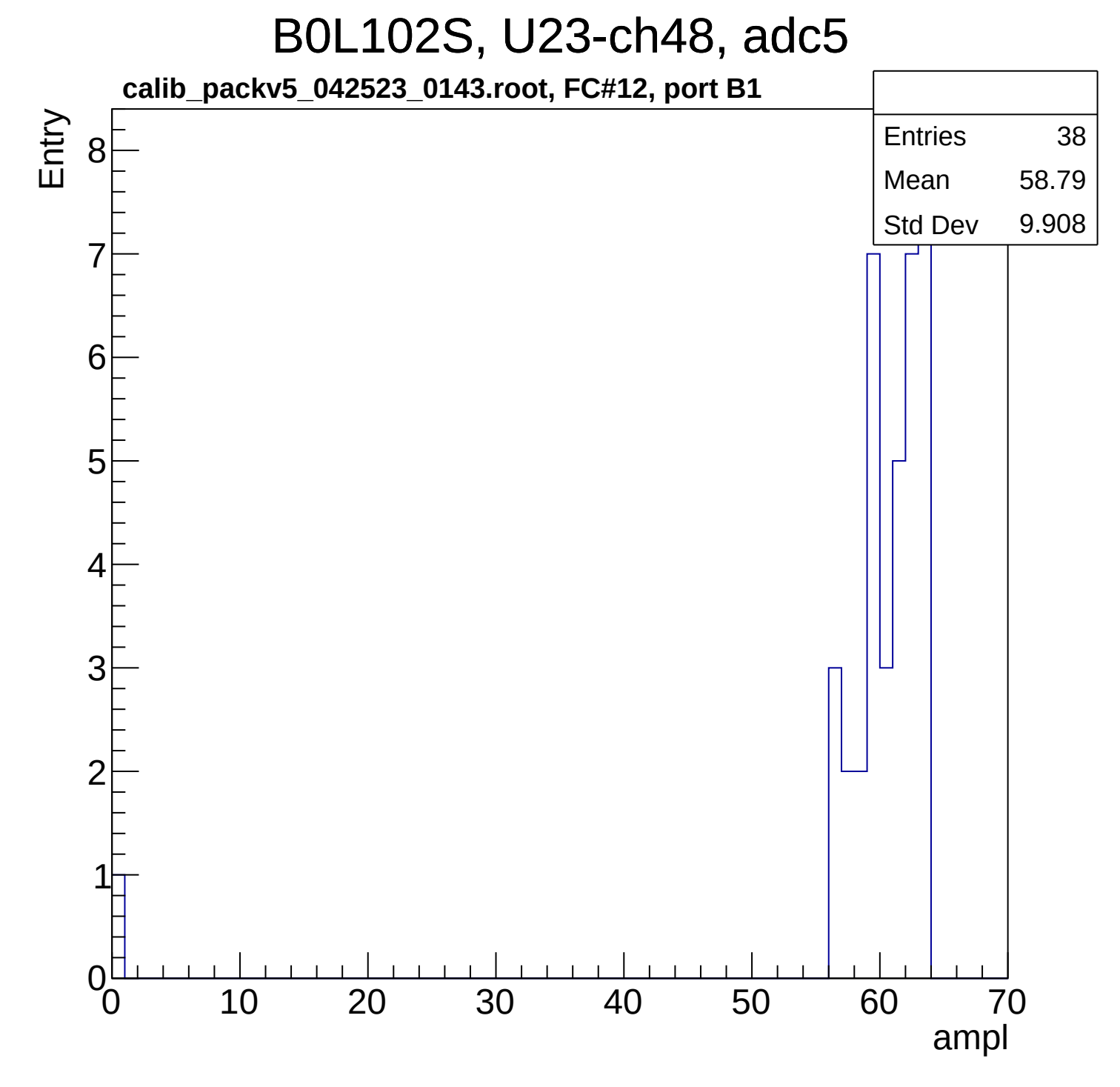
40
50
60
70


B0L102S, U23-ch48, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 56.57
Std Dev 2.99
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch48, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.79
Std Dev 9.908
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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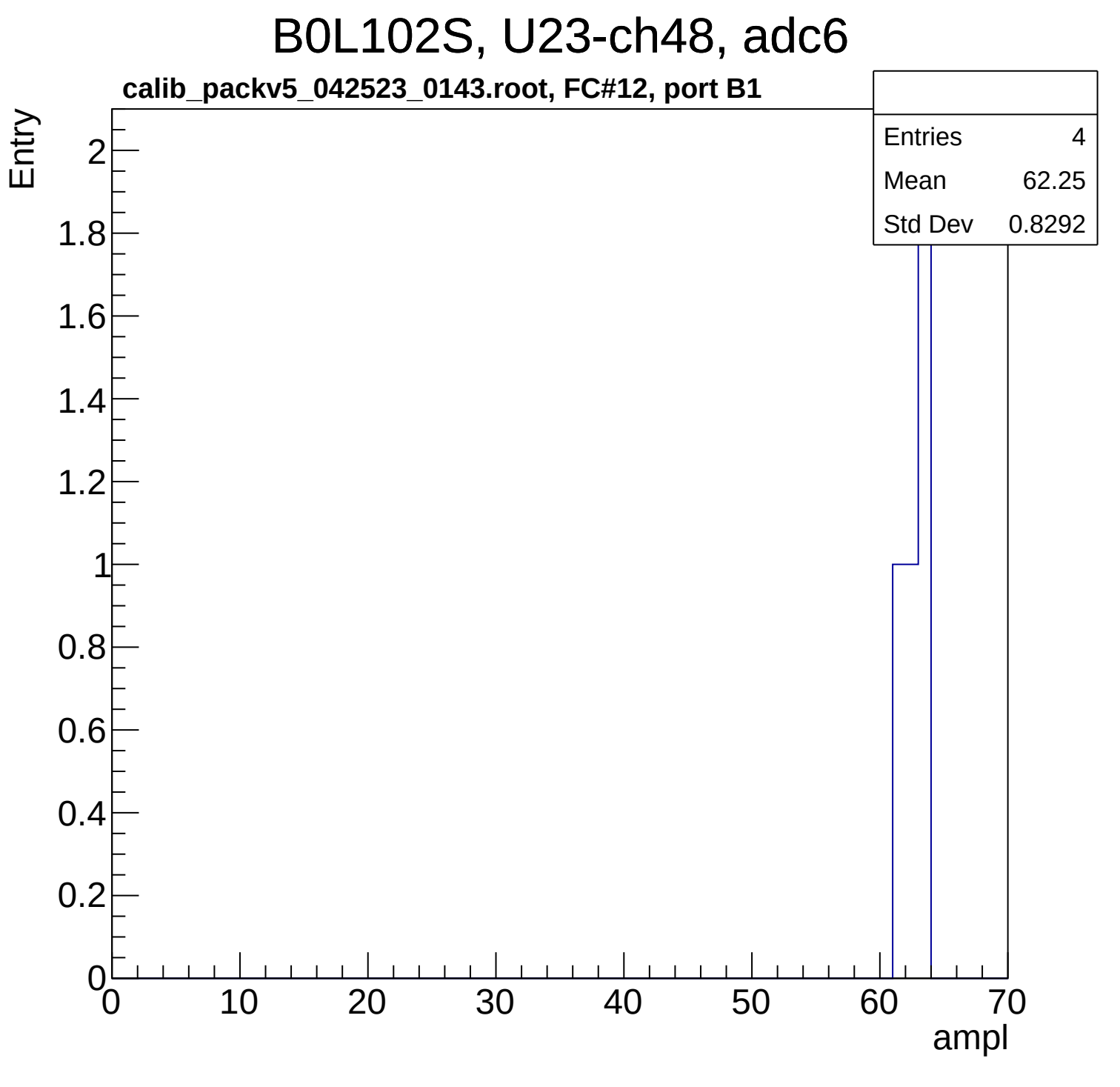
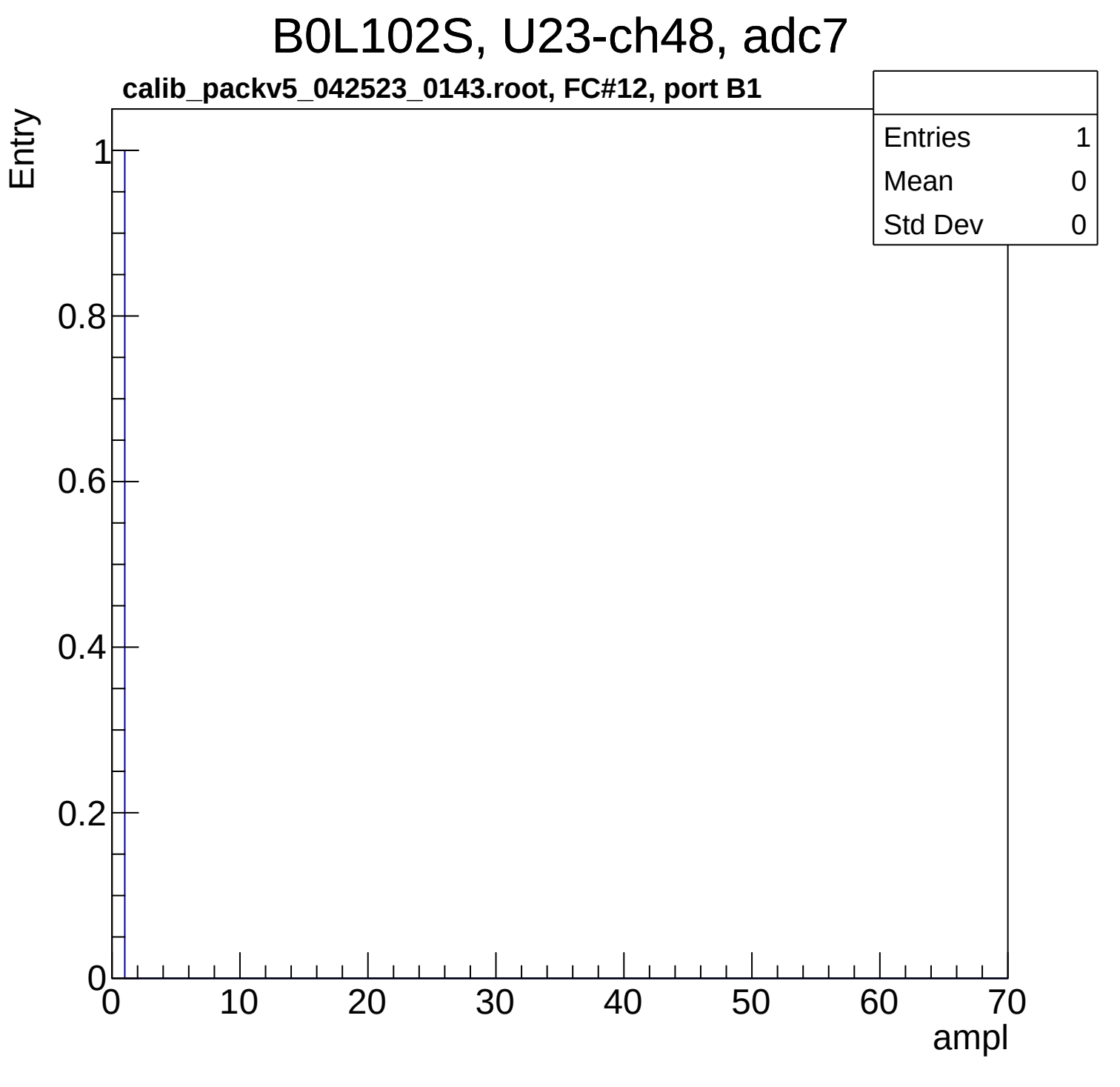
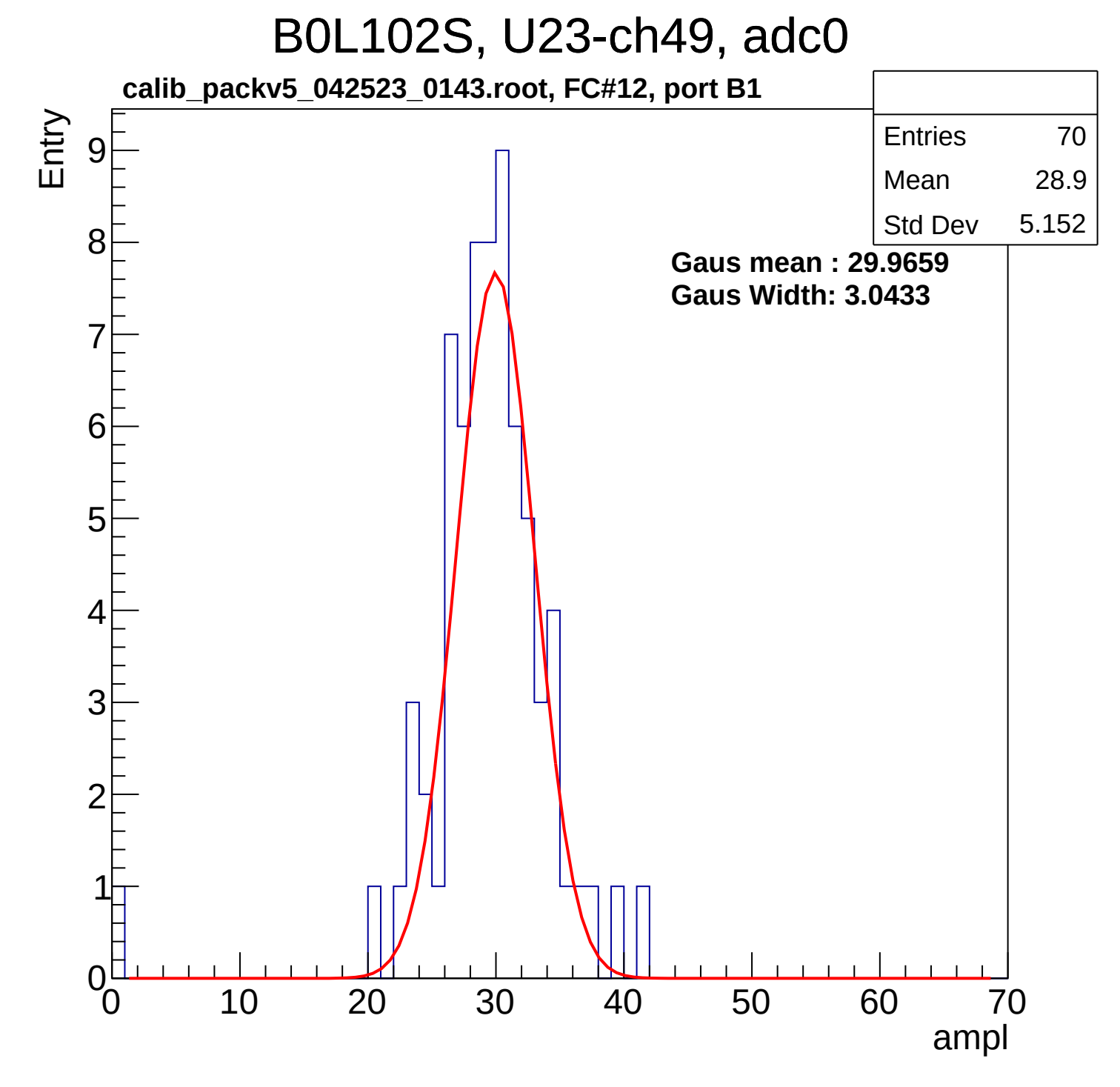

B0L102S, U23-ch48, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch48, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch49, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 28.9
Std Dev 5.152
Gaus mean : 29.9659
Gaus Width: 3.0433
Entry
ampl
0
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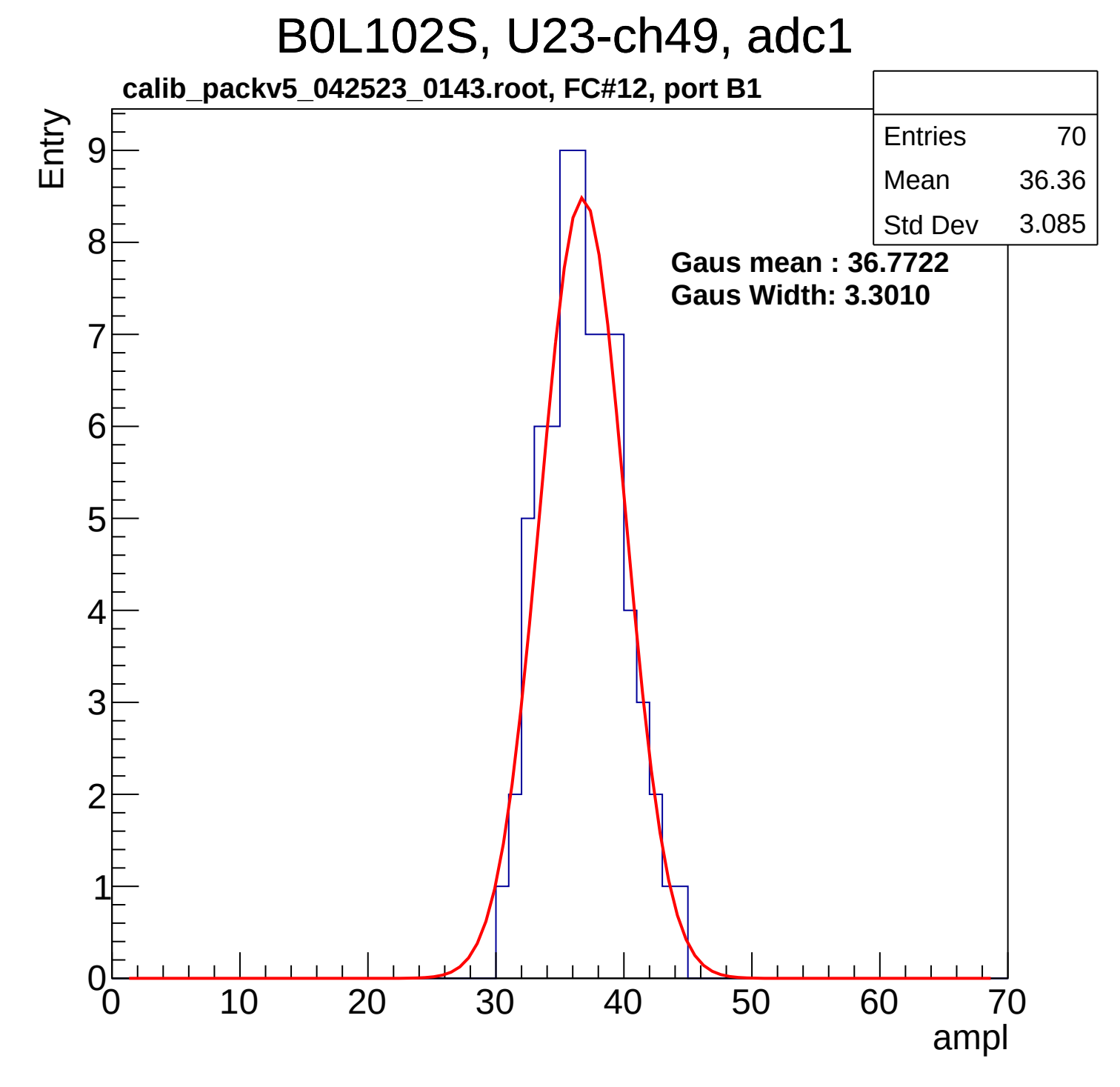
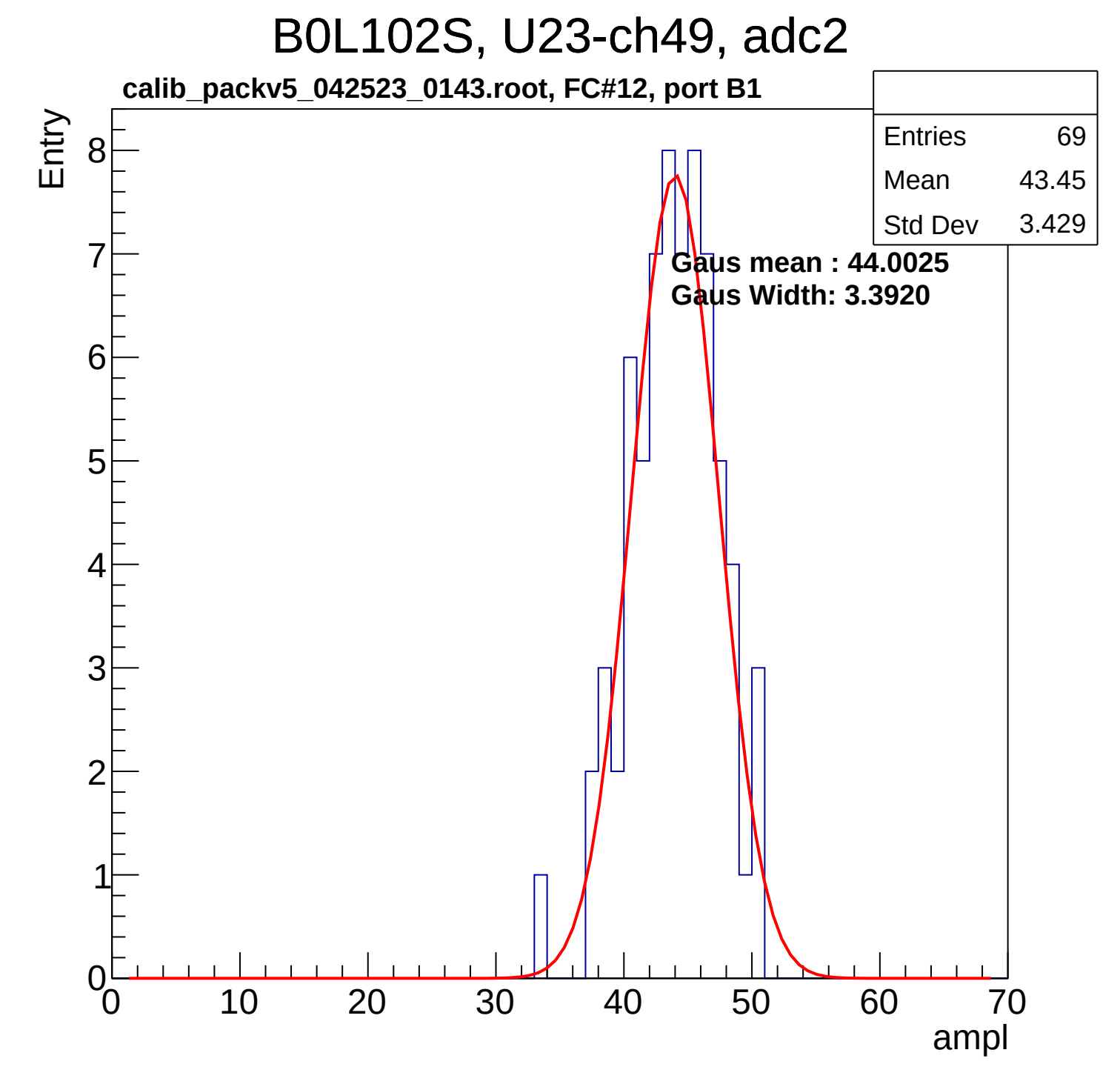
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch49, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 36.36
Std Dev 3.085
Gaus mean : 36.7722
Gaus Width: 3.3010
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch49, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 43.45
Std Dev 3.429
Gaus mean : 44.0025
Gaus Width: 3.3920
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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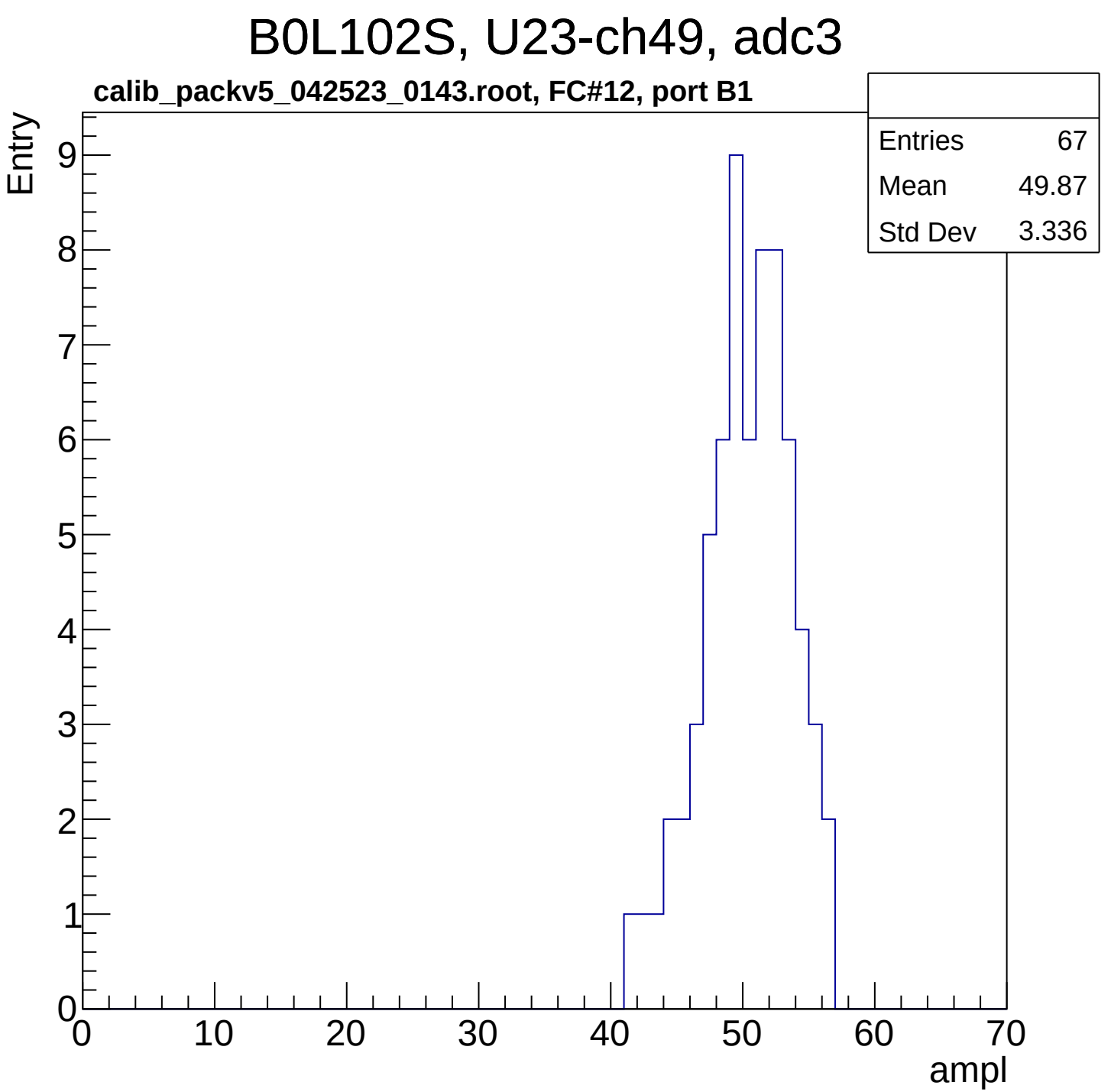
B0L102S, U23-ch49, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 49.87
Std Dev 3.336
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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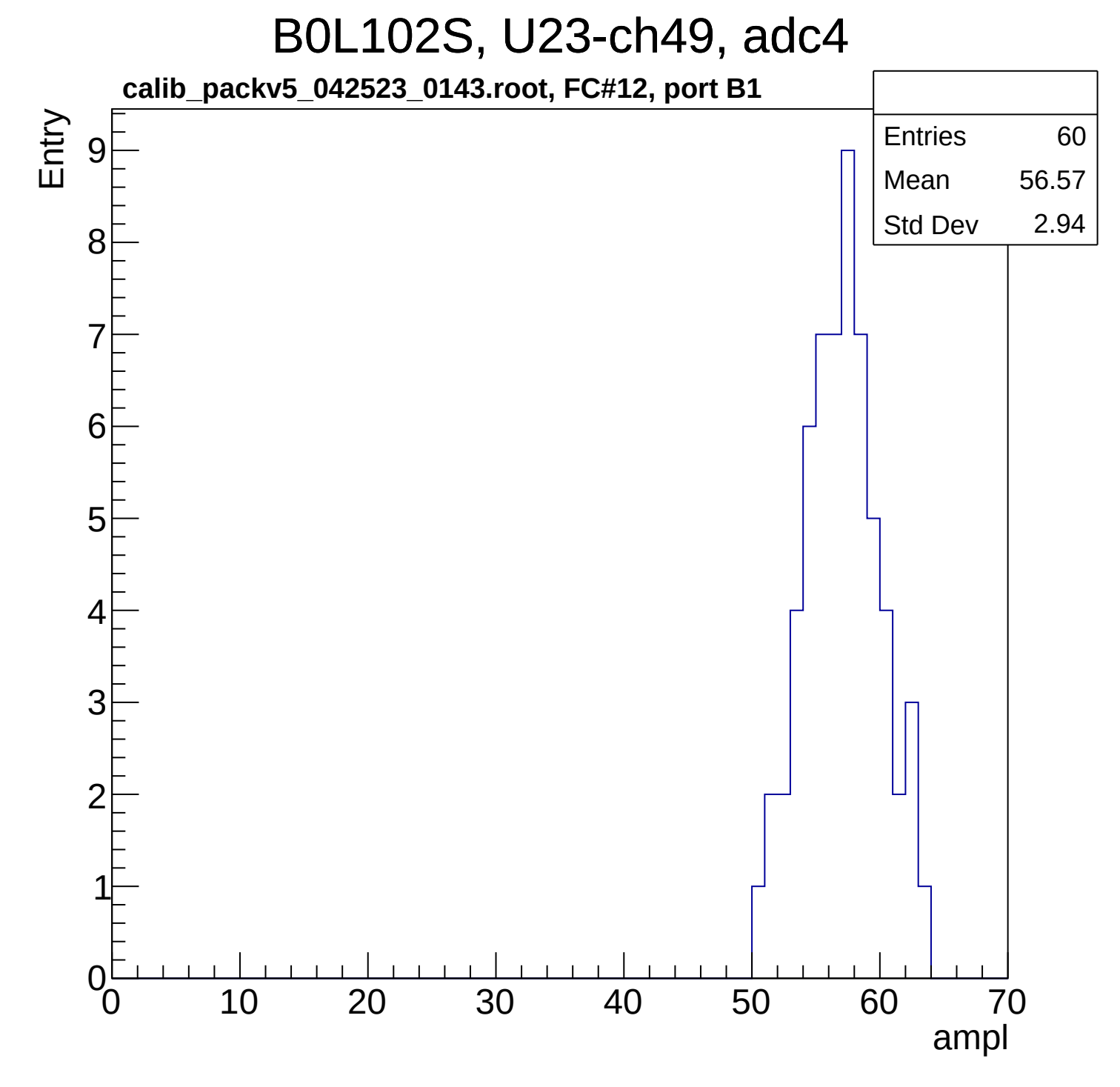
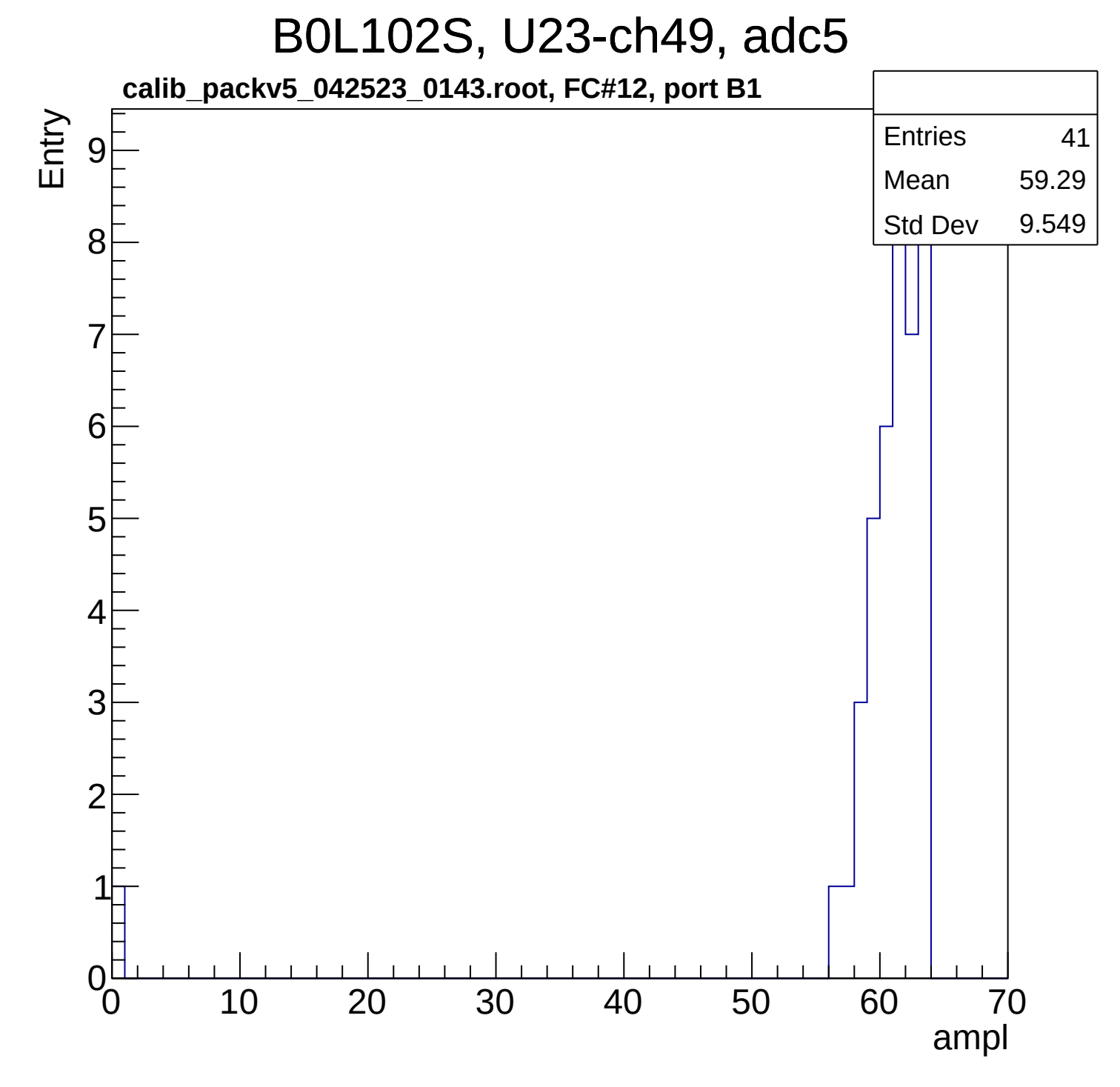

B0L102S, U23-ch49, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.57
Std Dev 2.94
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch49, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 59.29
Std Dev 9.549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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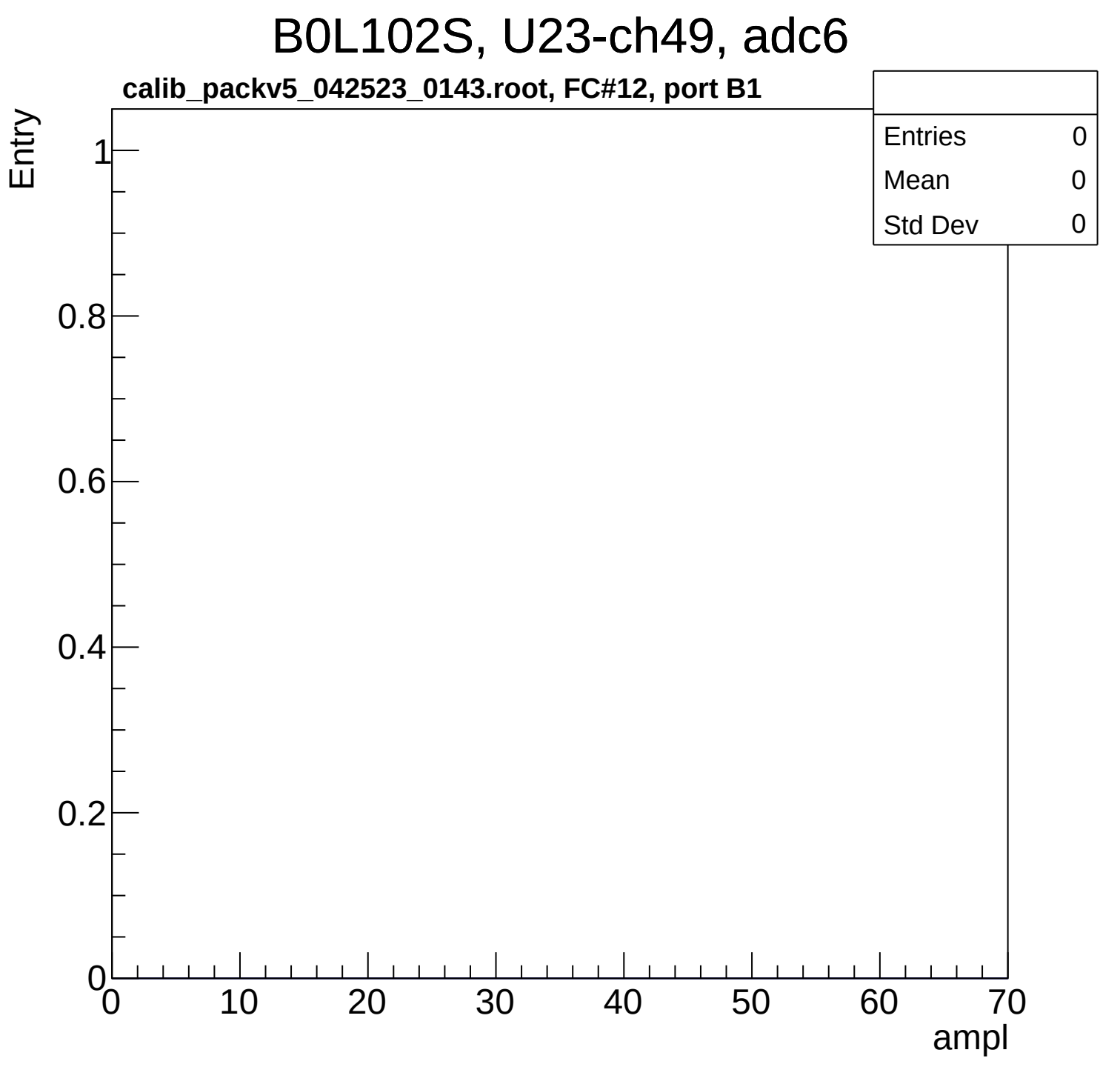
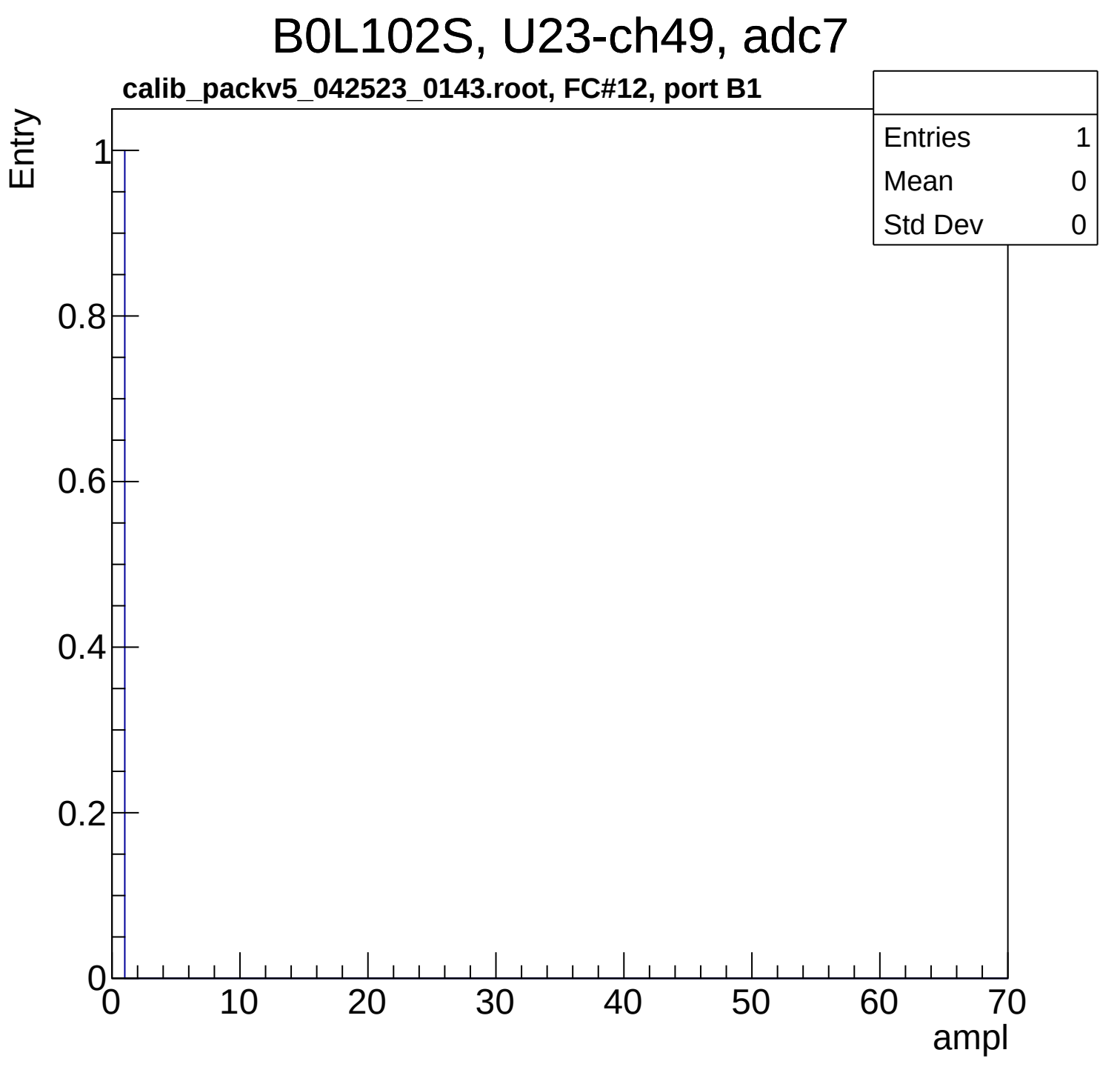
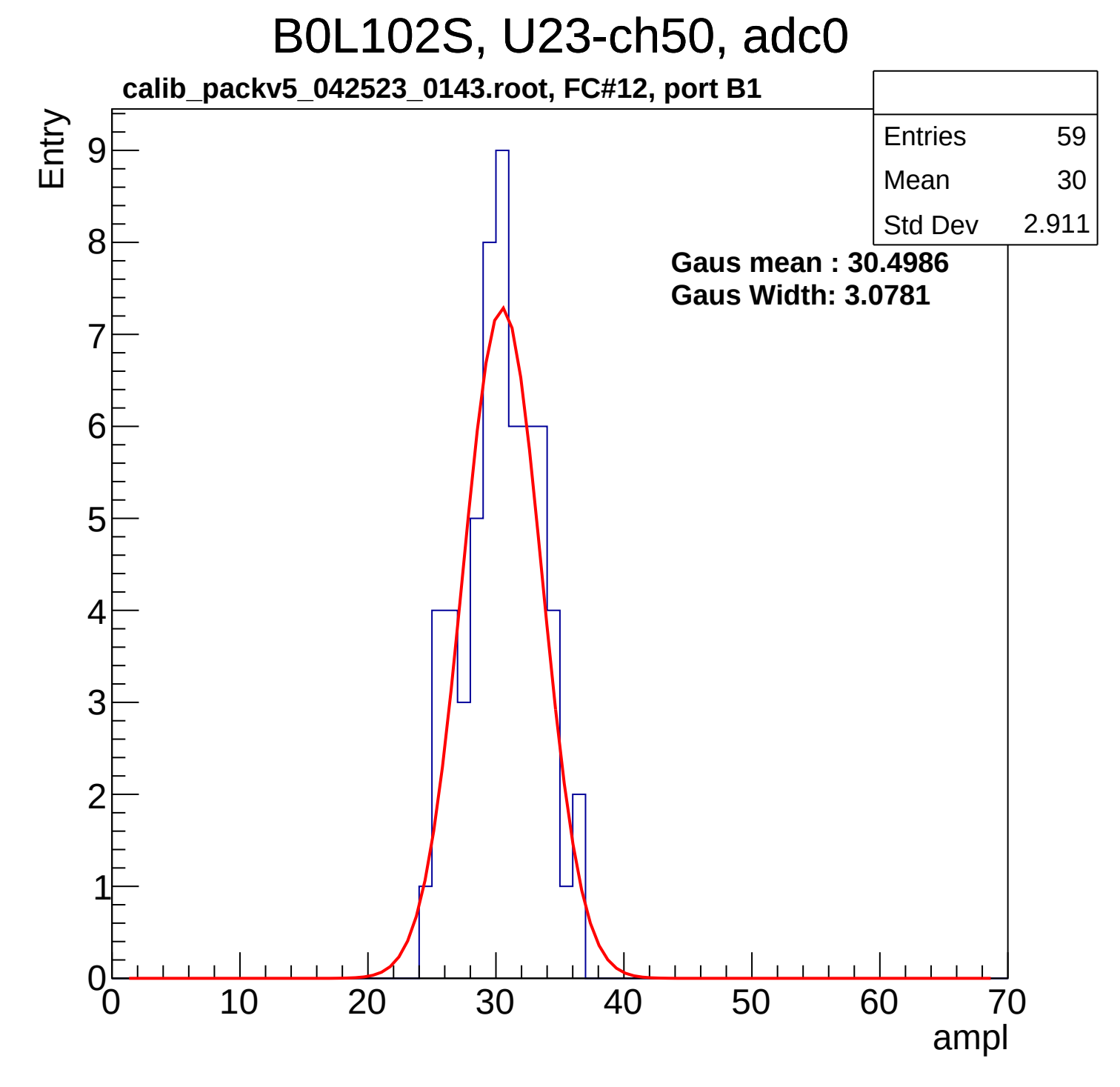
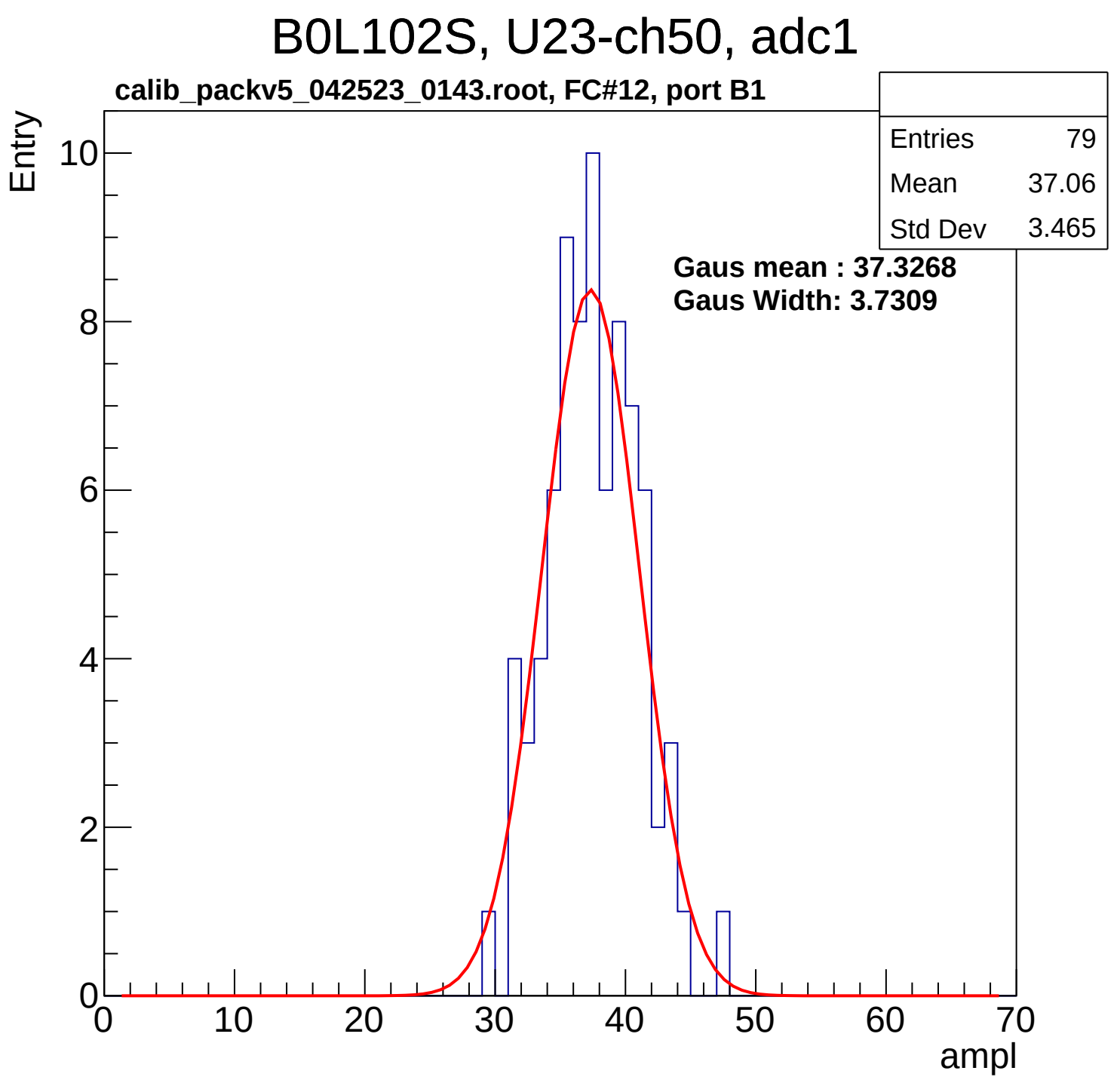

B0L102S, U23-ch49, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch49, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch50, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 30
Std Dev 2.911
Gaus mean : 30.4986
Gaus Width: 3.0781
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch50, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 37.06
Std Dev 3.465
Gaus mean : 37.3268
Gaus Width: 3.7309
Entry
ampl
0
2
4
6
8
10
0
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60
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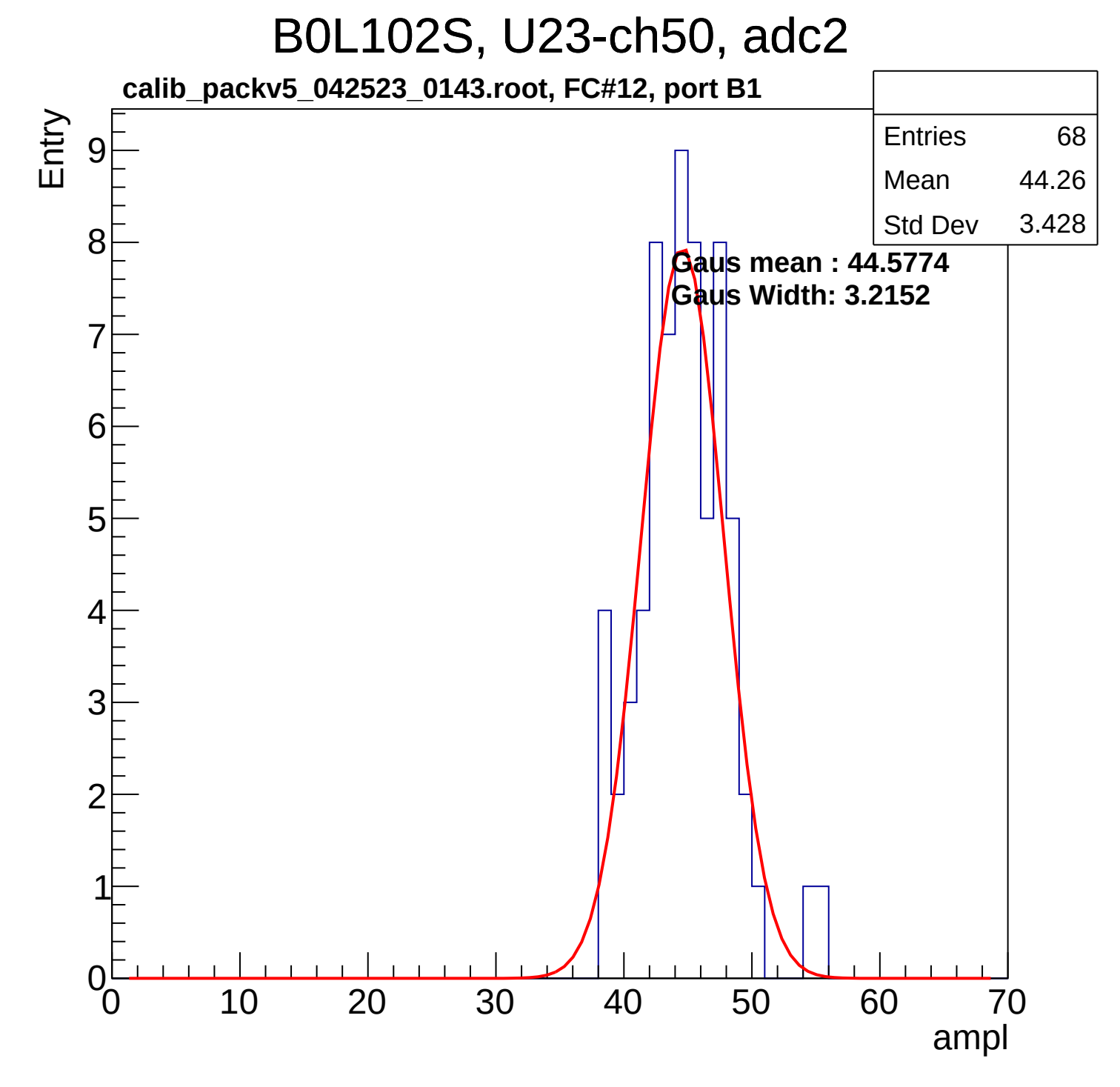
B0L102S, U23-ch50, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 44.26
Std Dev 3.428
Gaus mean : 44.5774
Gaus Width: 3.2152
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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60
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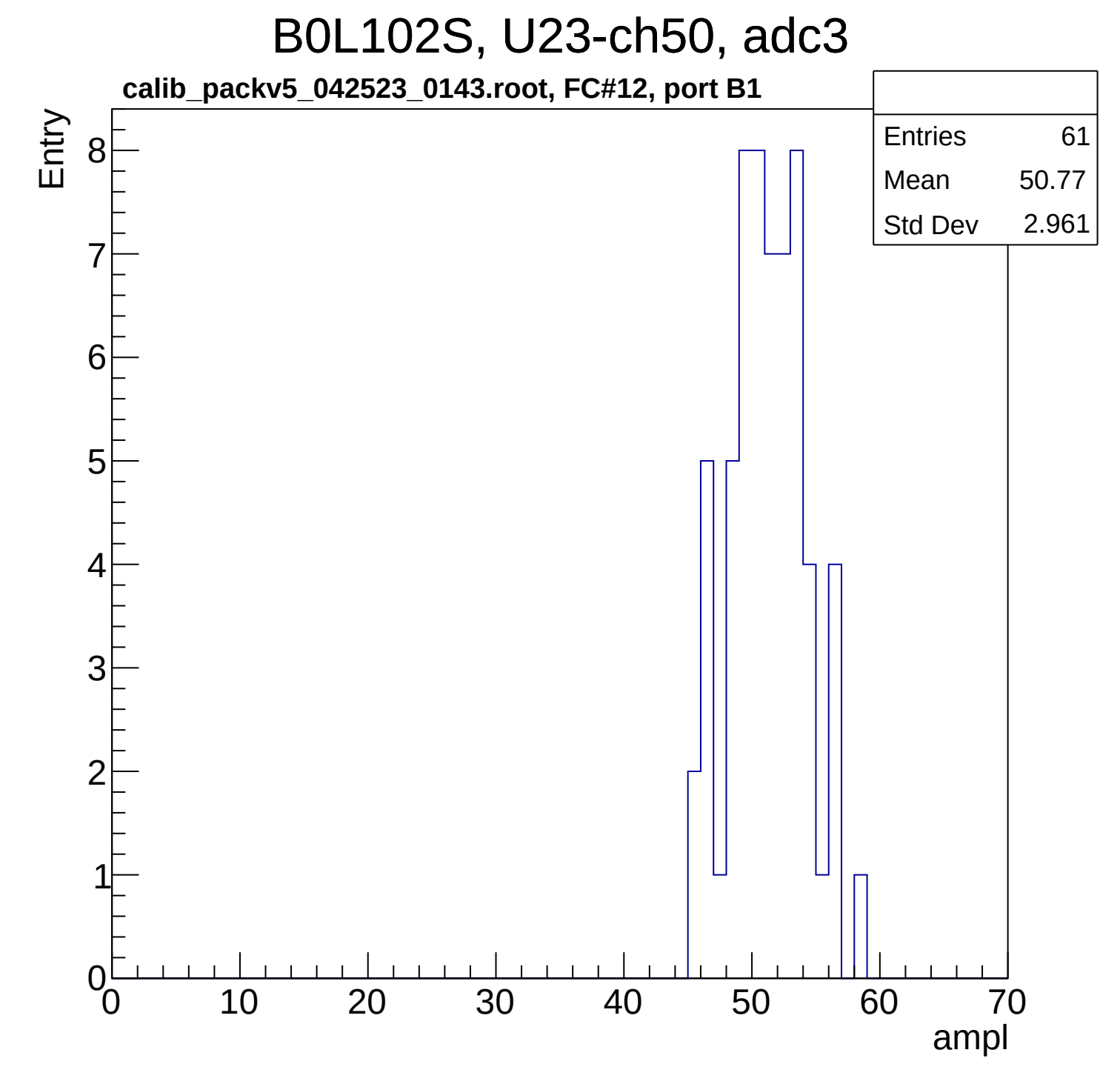
B0L102S, U23-ch50, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 50.77
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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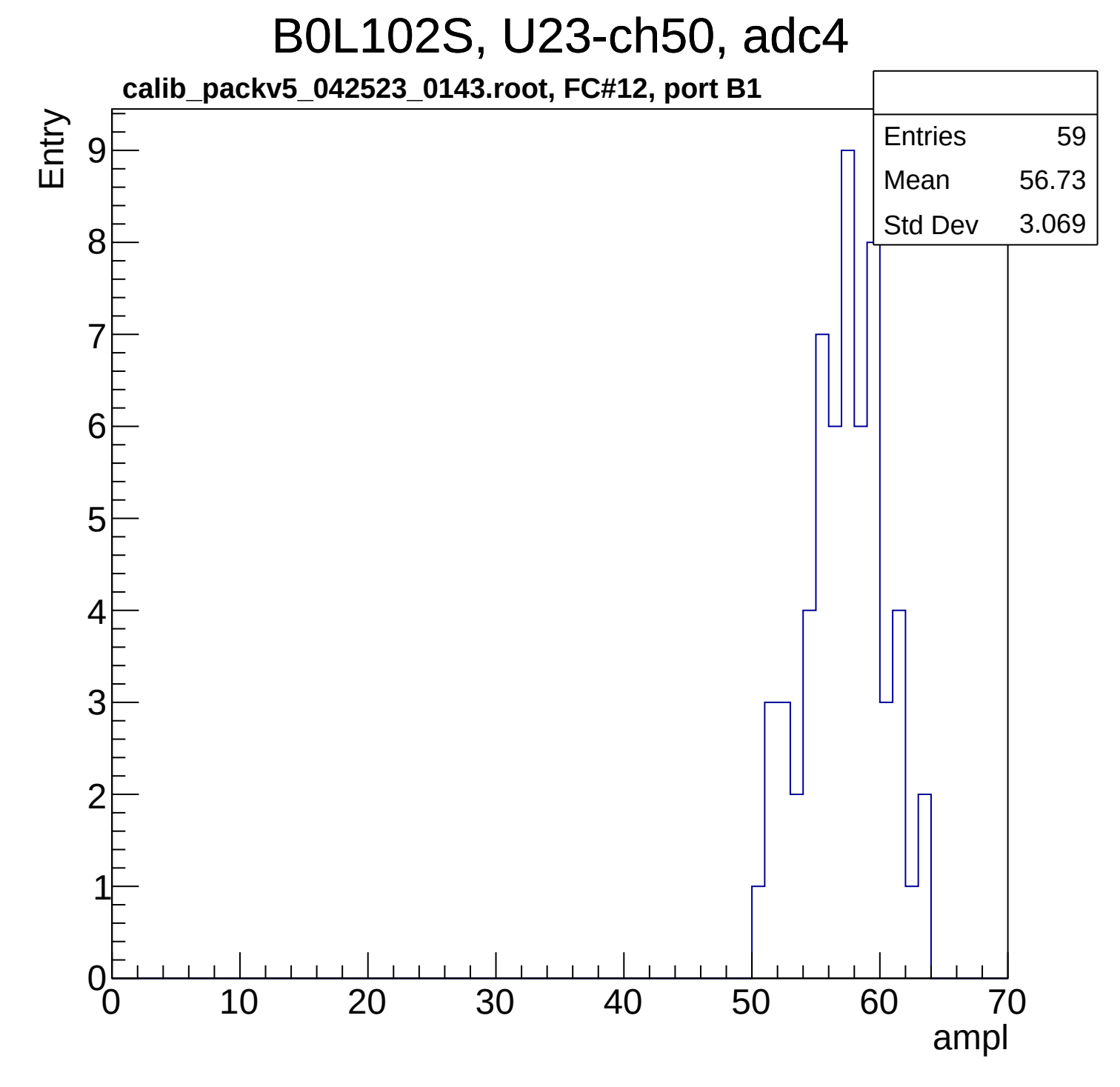
B0L102S, U23-ch50, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 56.73
Std Dev 3.069
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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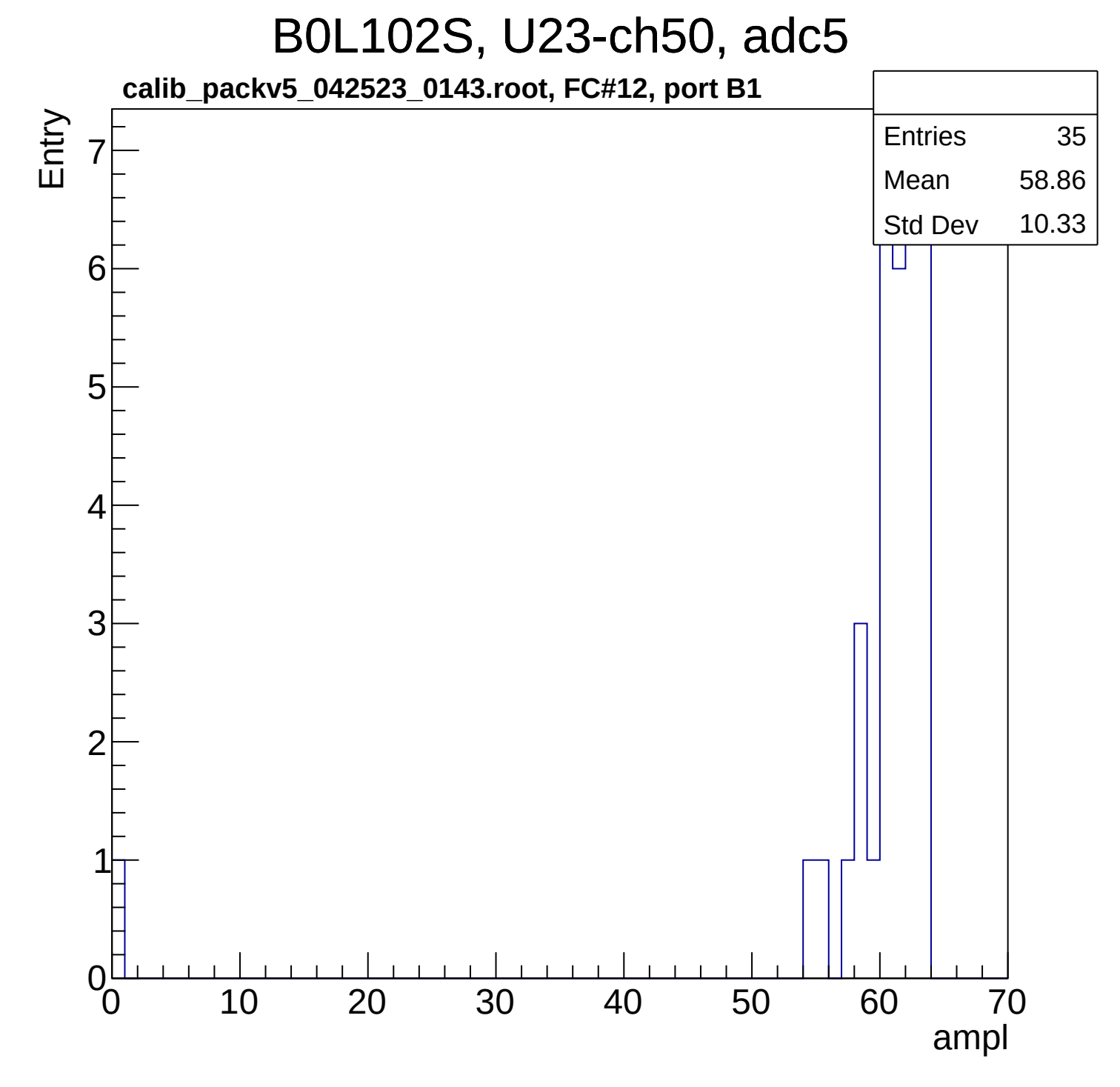
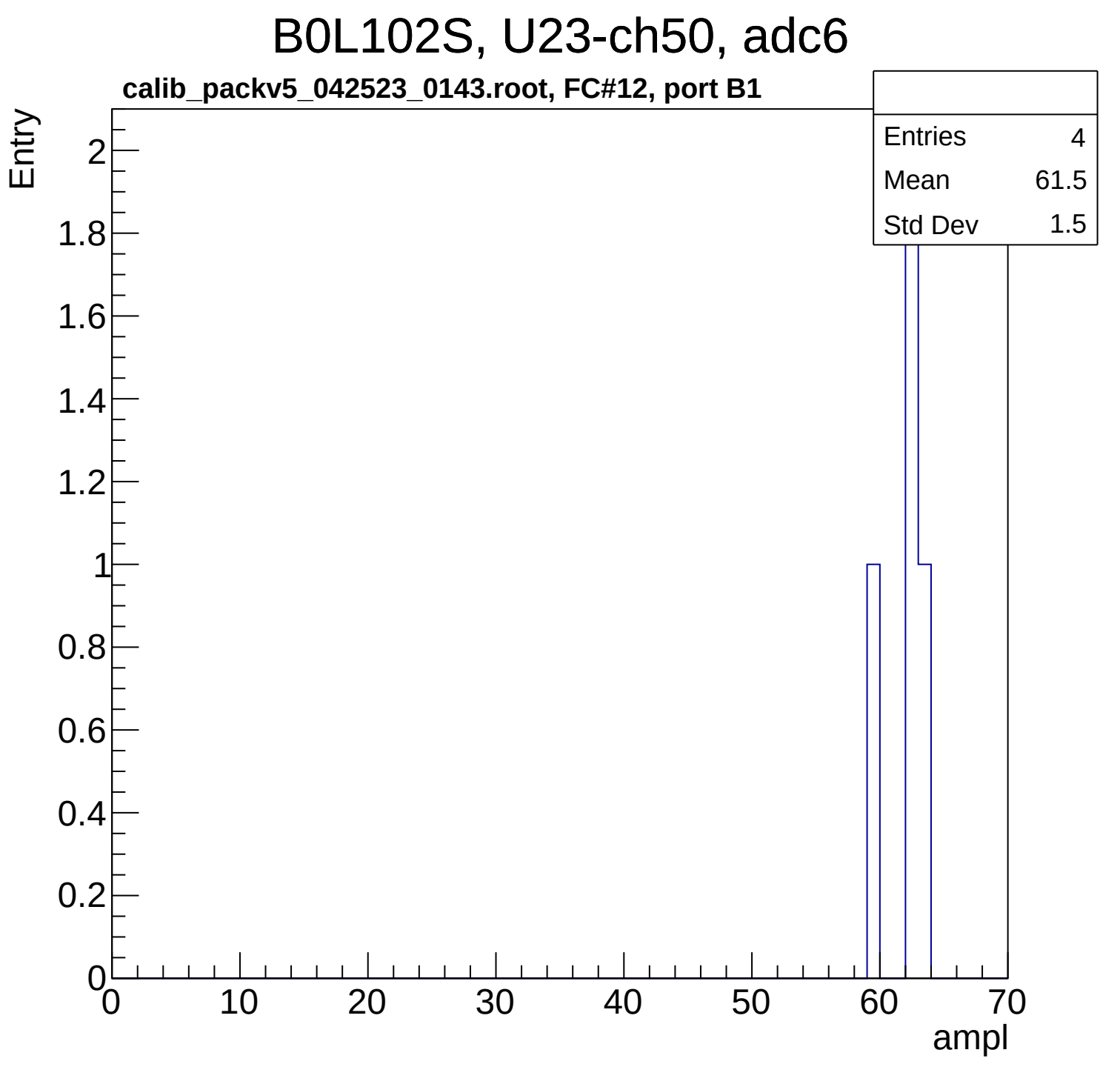

B0L102S, U23-ch50, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 58.86
Std Dev 10.33
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch50, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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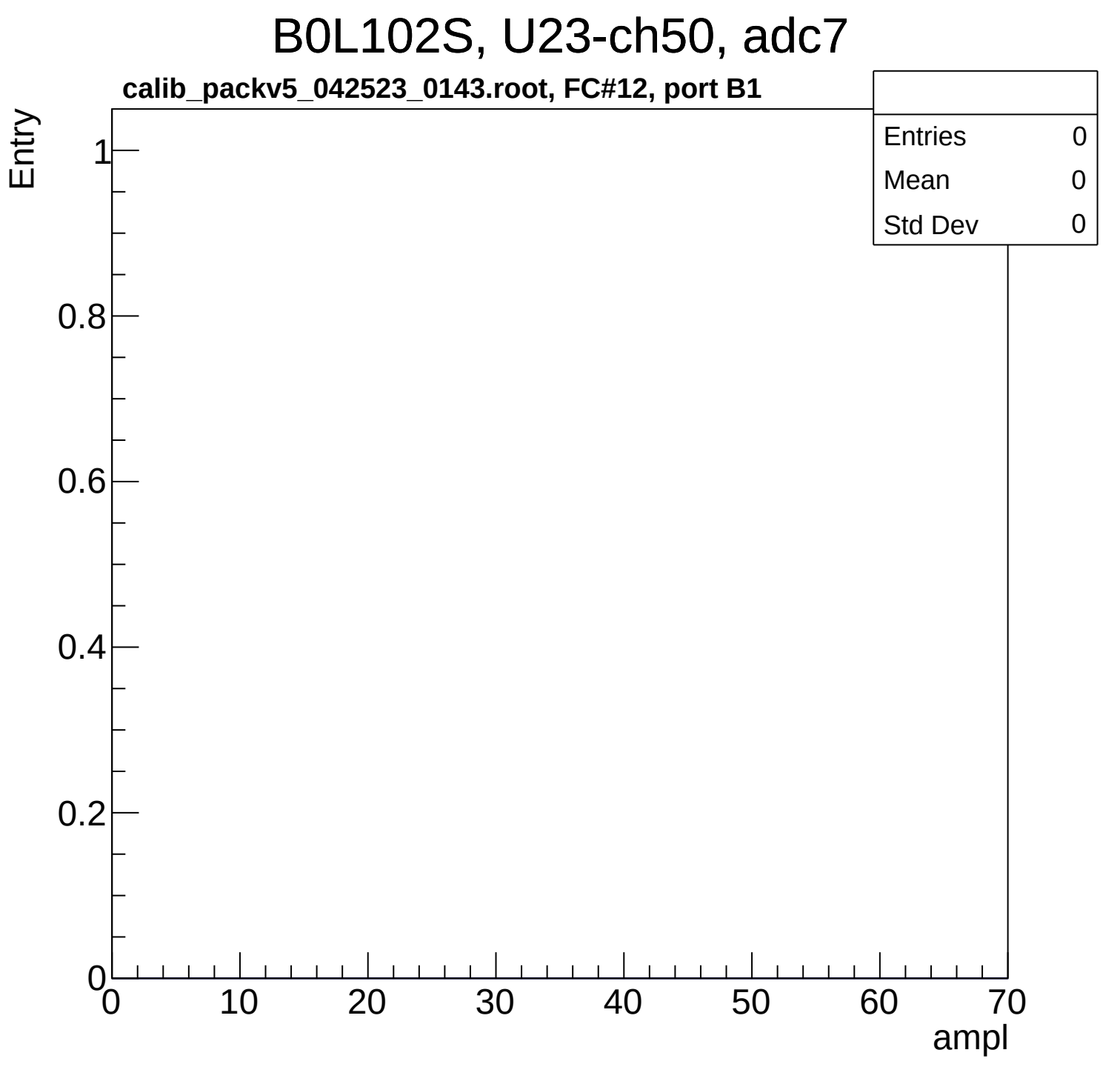
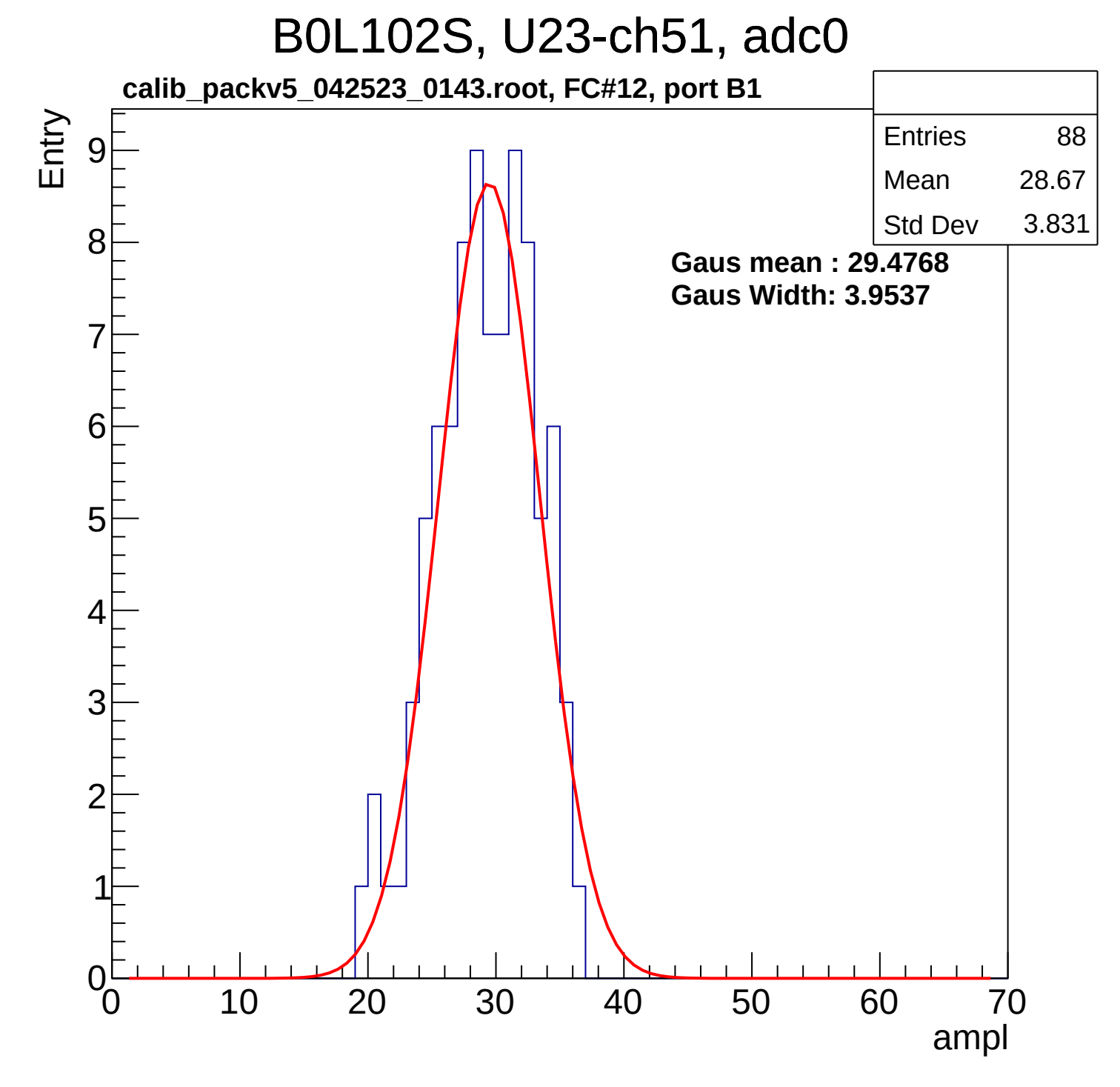

B0L102S, U23-ch50, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch51, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 28.67
Std Dev 3.831
Gaus mean : 29.4768
Gaus Width: 3.9537
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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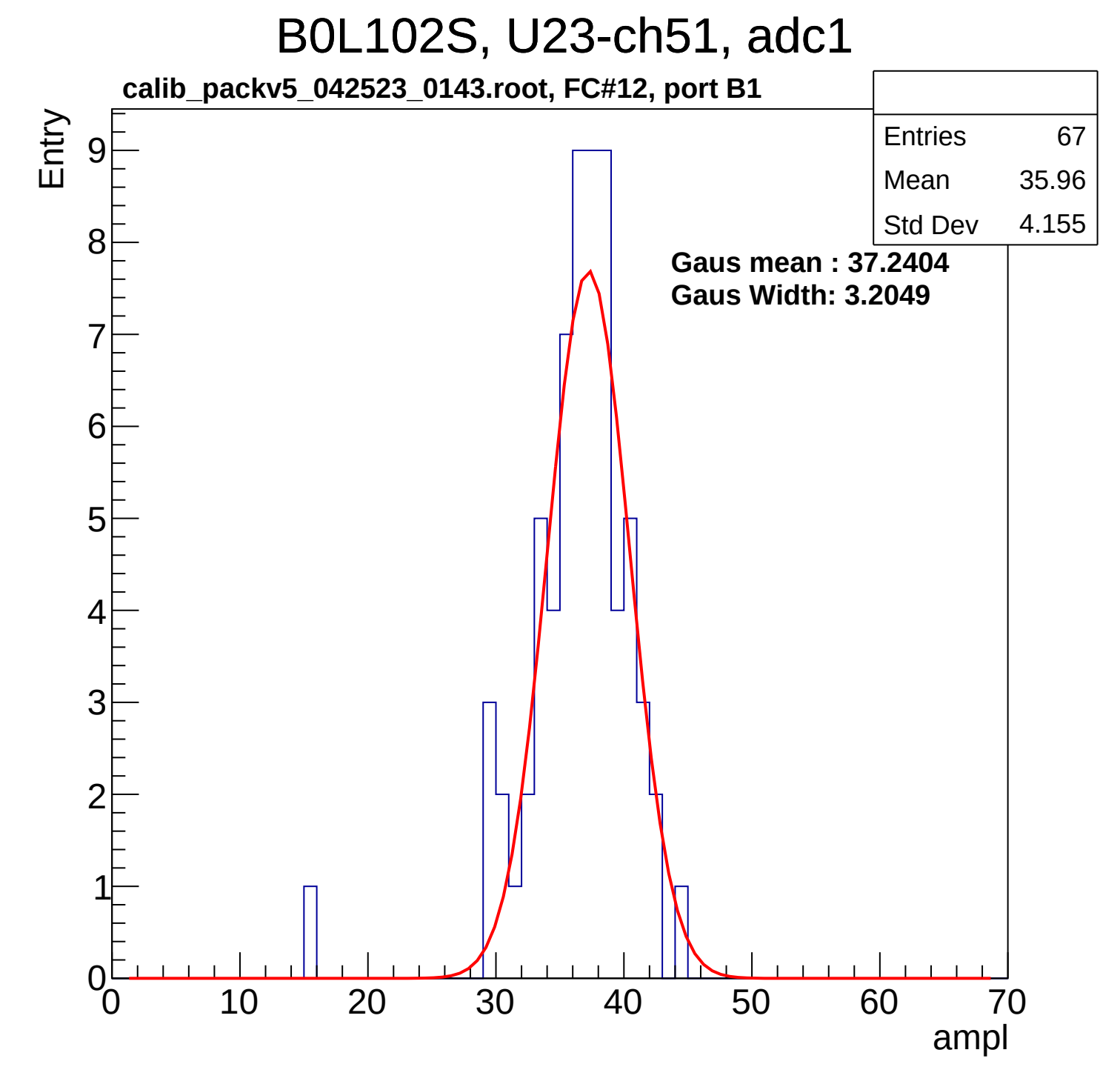
B0L102S, U23-ch51, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.96
Std Dev 4.155
Gaus mean : 37.2404
Gaus Width: 3.2049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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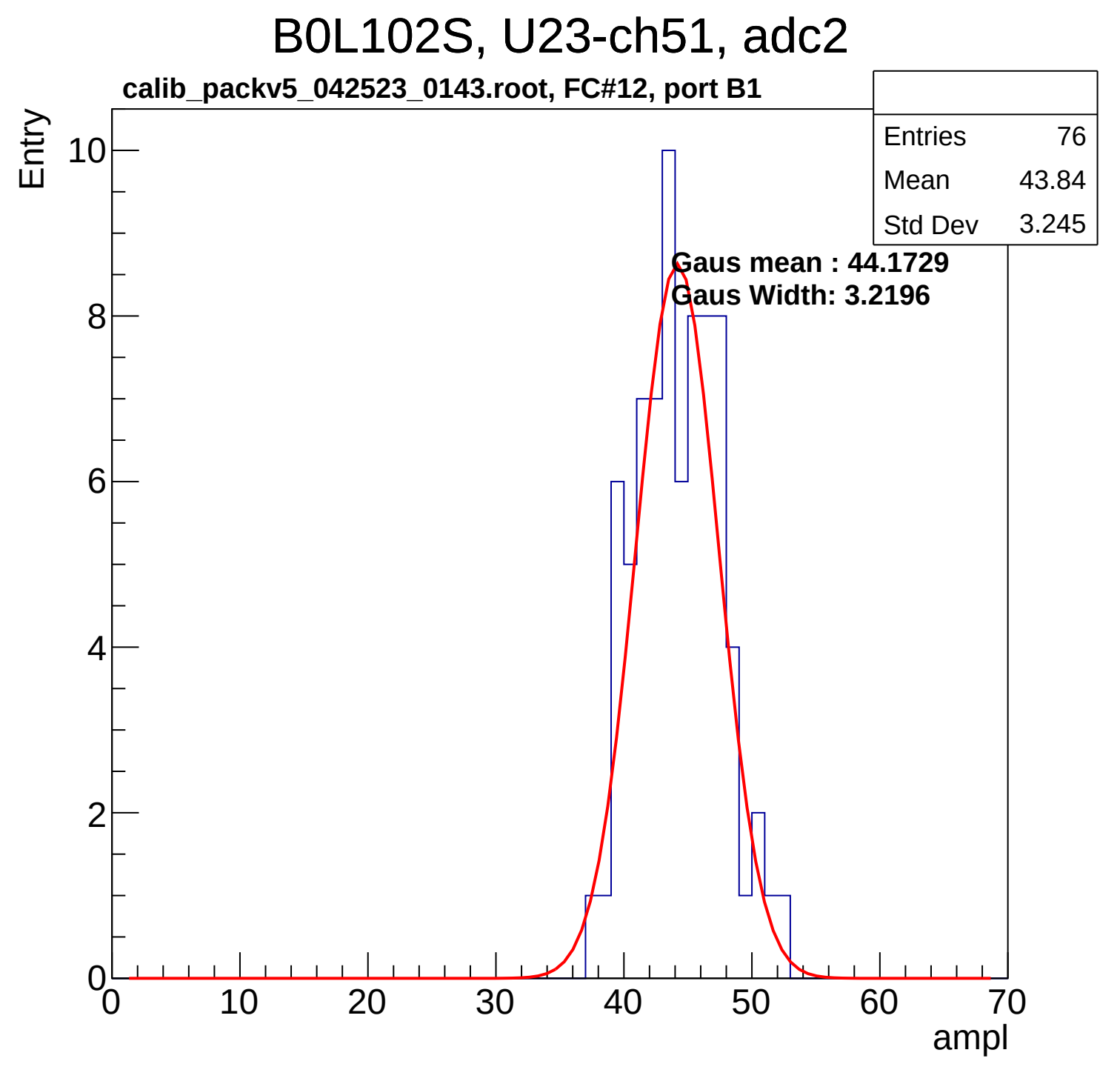
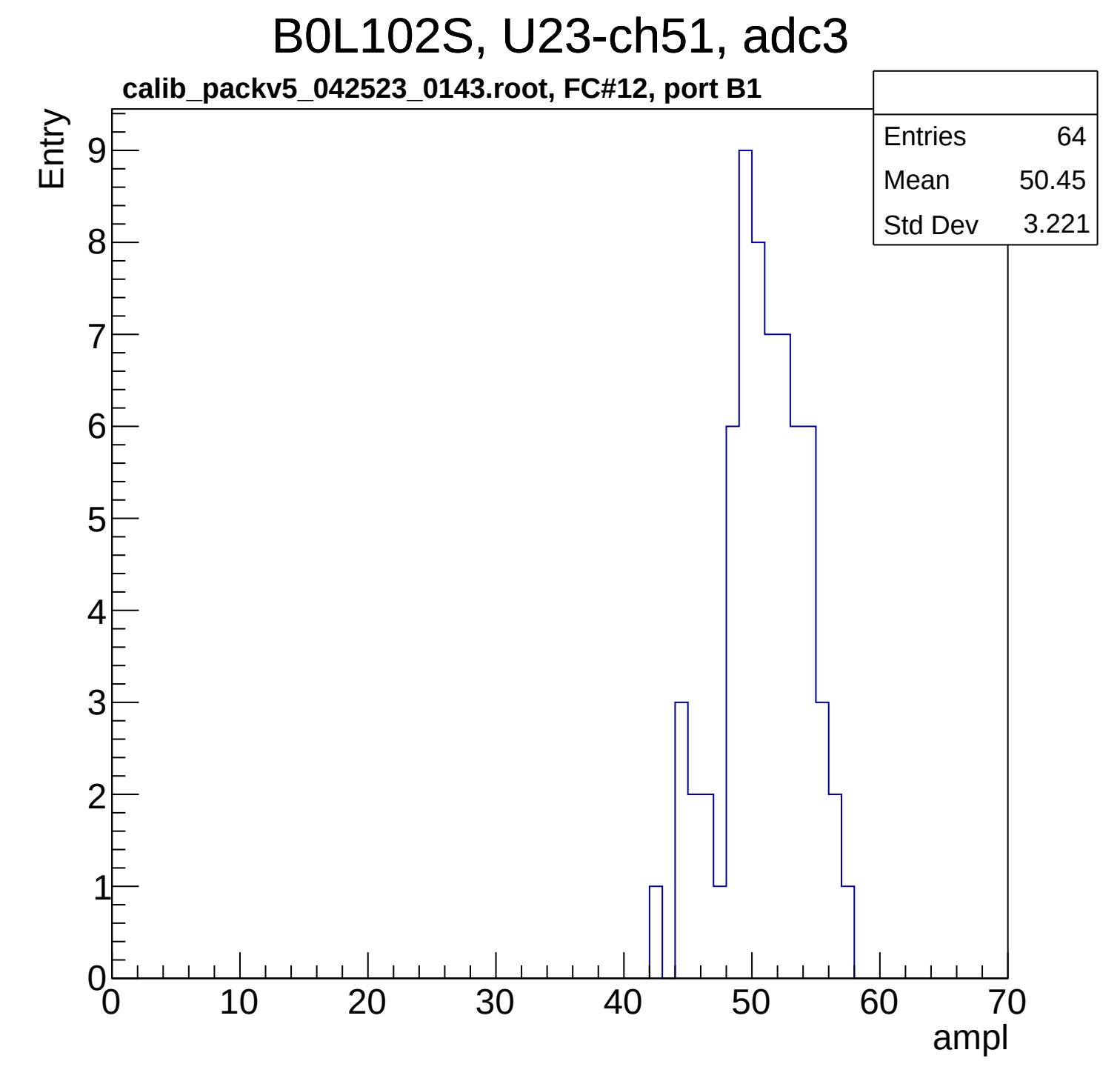

B0L102S, U23-ch51, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 43.84
Std Dev 3.245
Gaus mean : 44.1729
Gaus Width: 3.2196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch51, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 50.45
Std Dev 3.221
Entry
ampl
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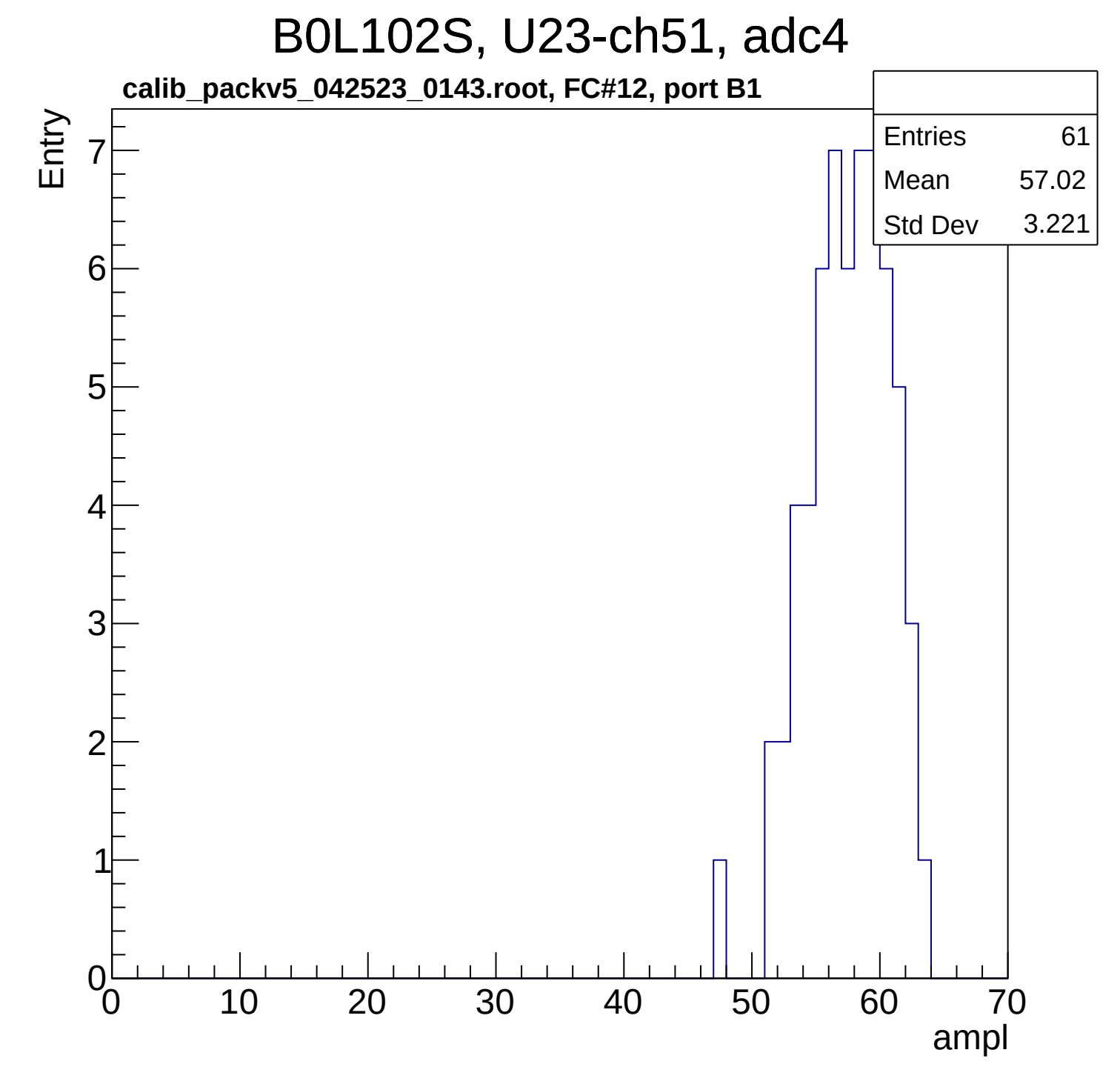
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch51, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 57.02
Std Dev 3.221
Entry
ampl
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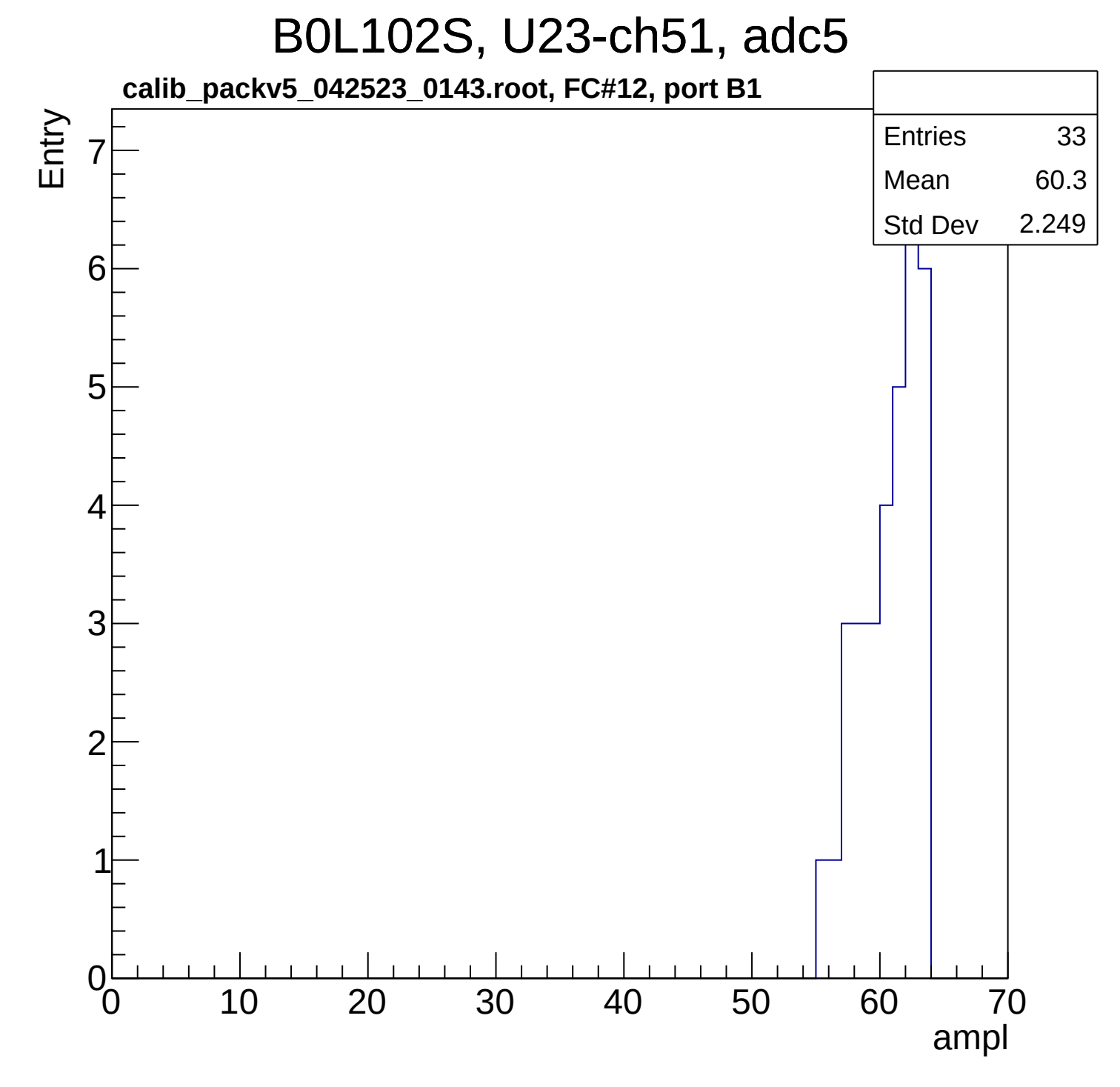
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch51, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 60.3
Std Dev 2.249
Entry
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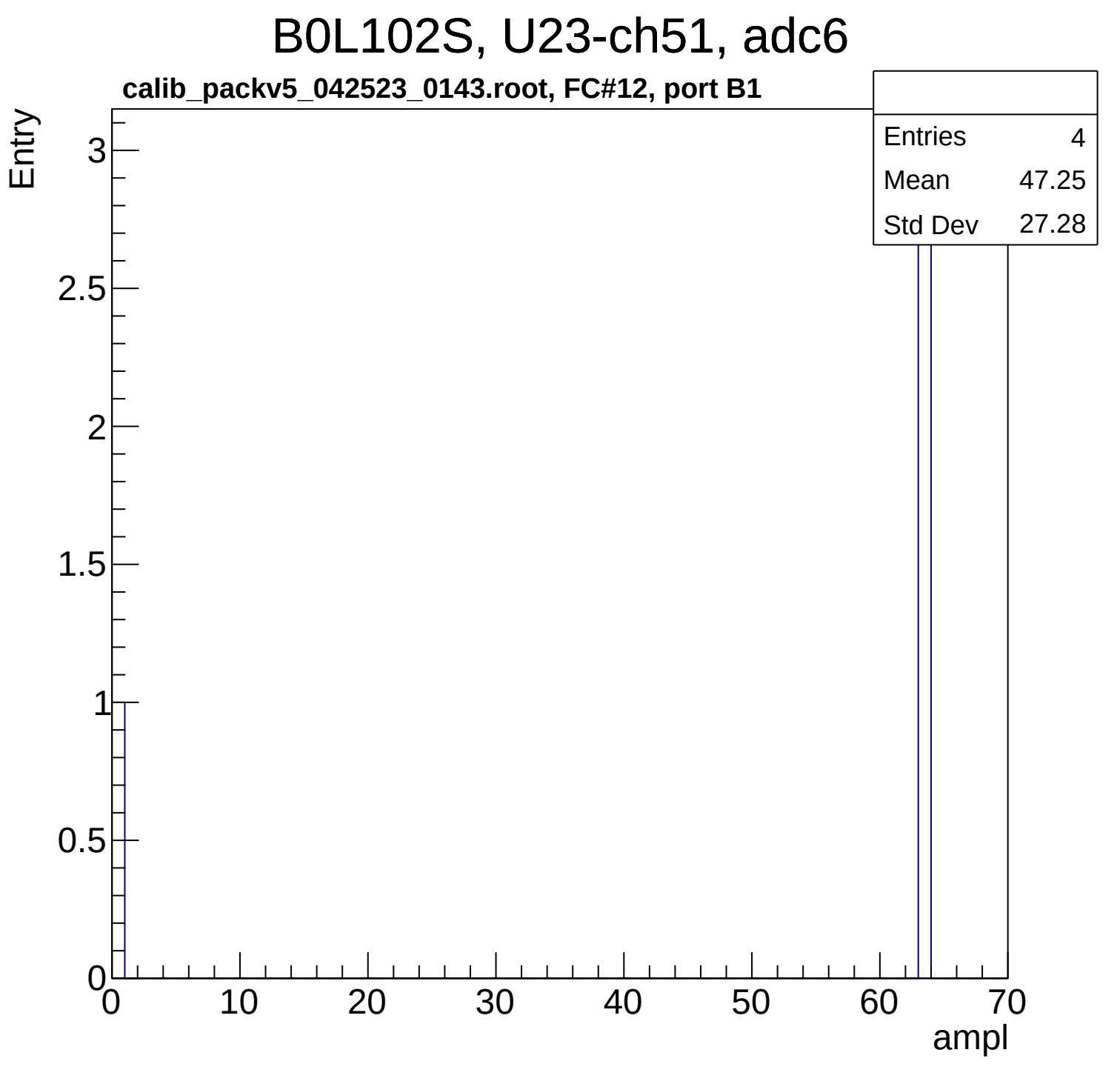
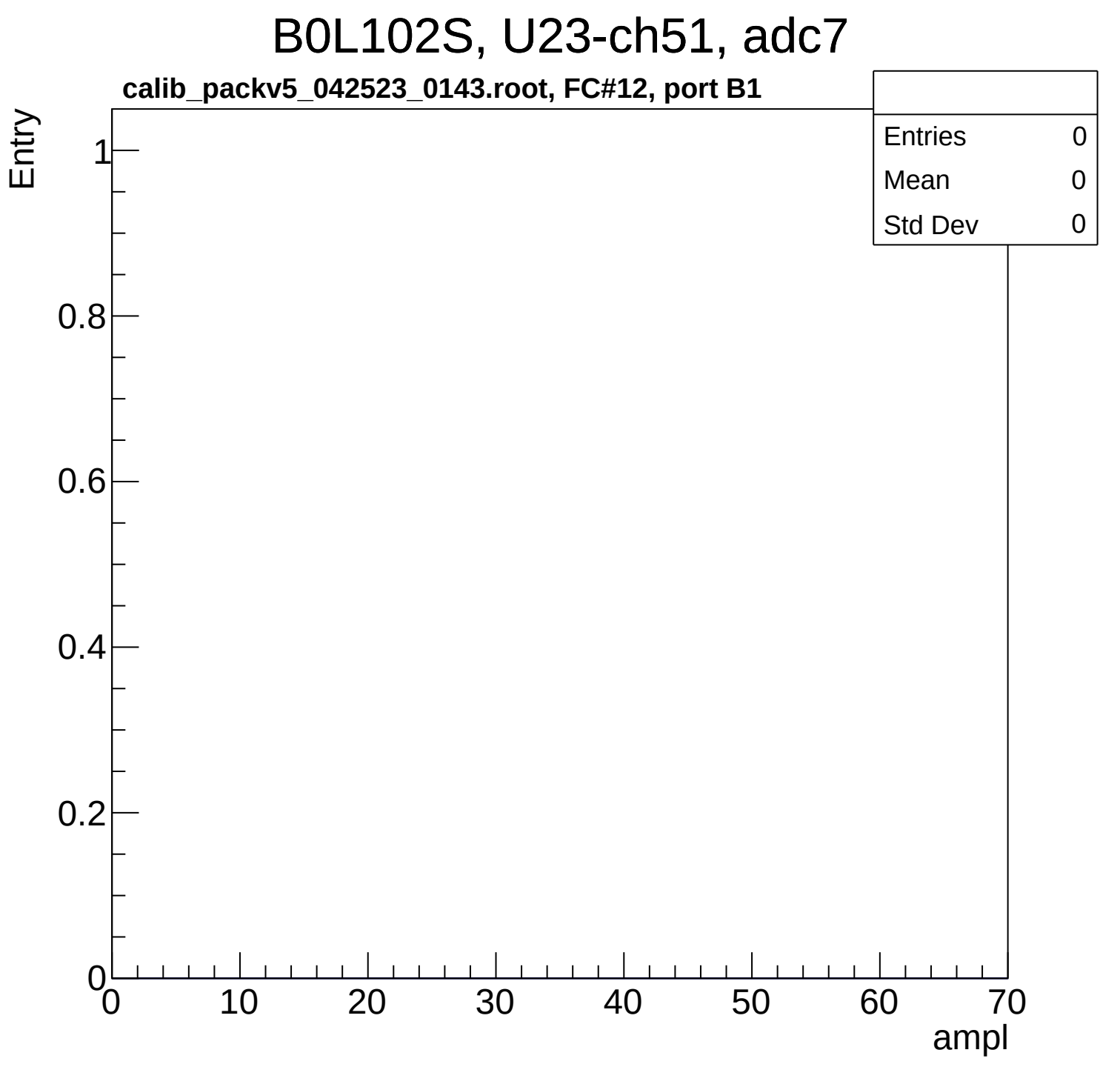
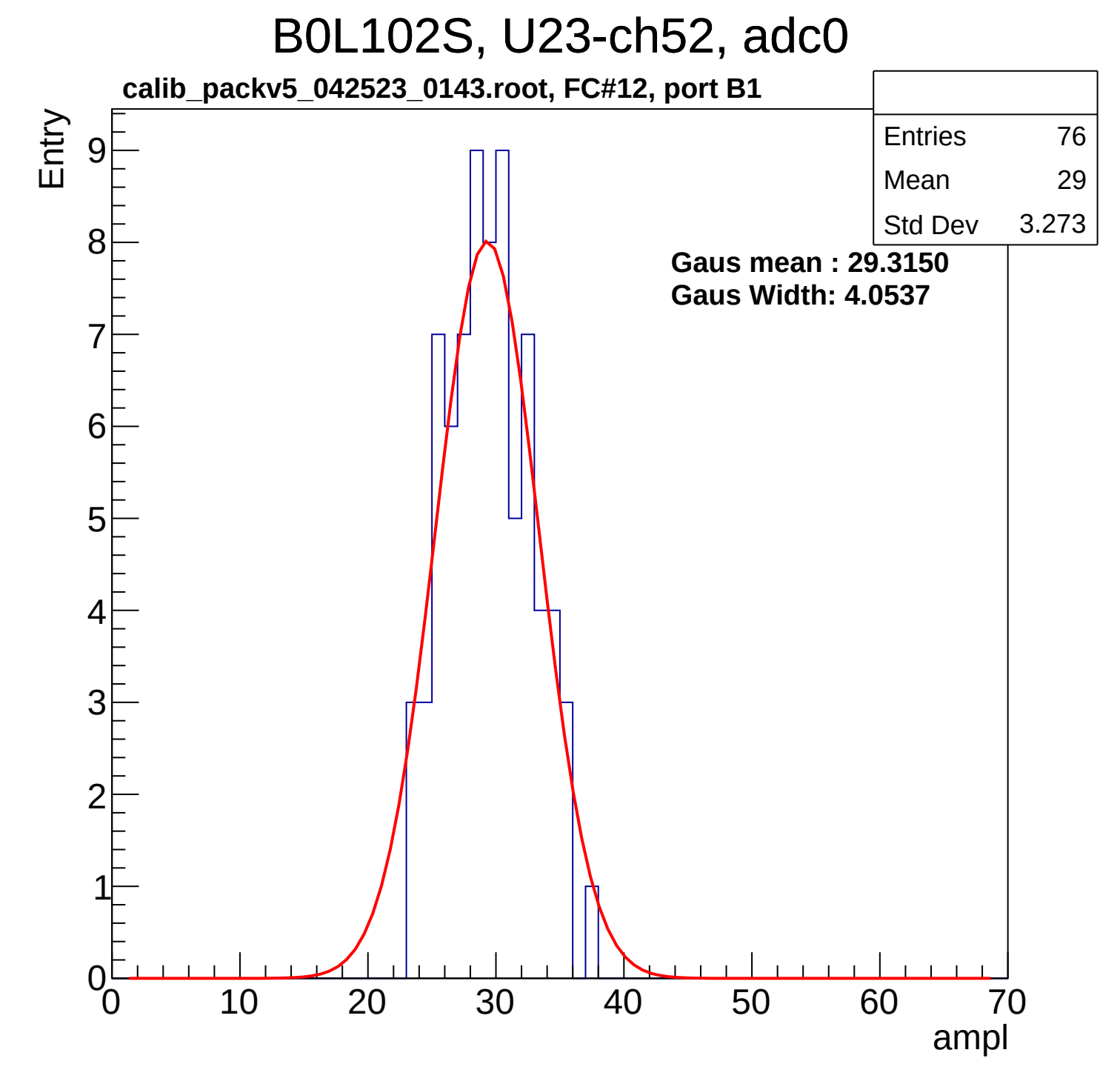
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch51, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch51, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch52, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29
Std Dev 3.273
Gaus mean : 29.3150
Gaus Width: 4.0537
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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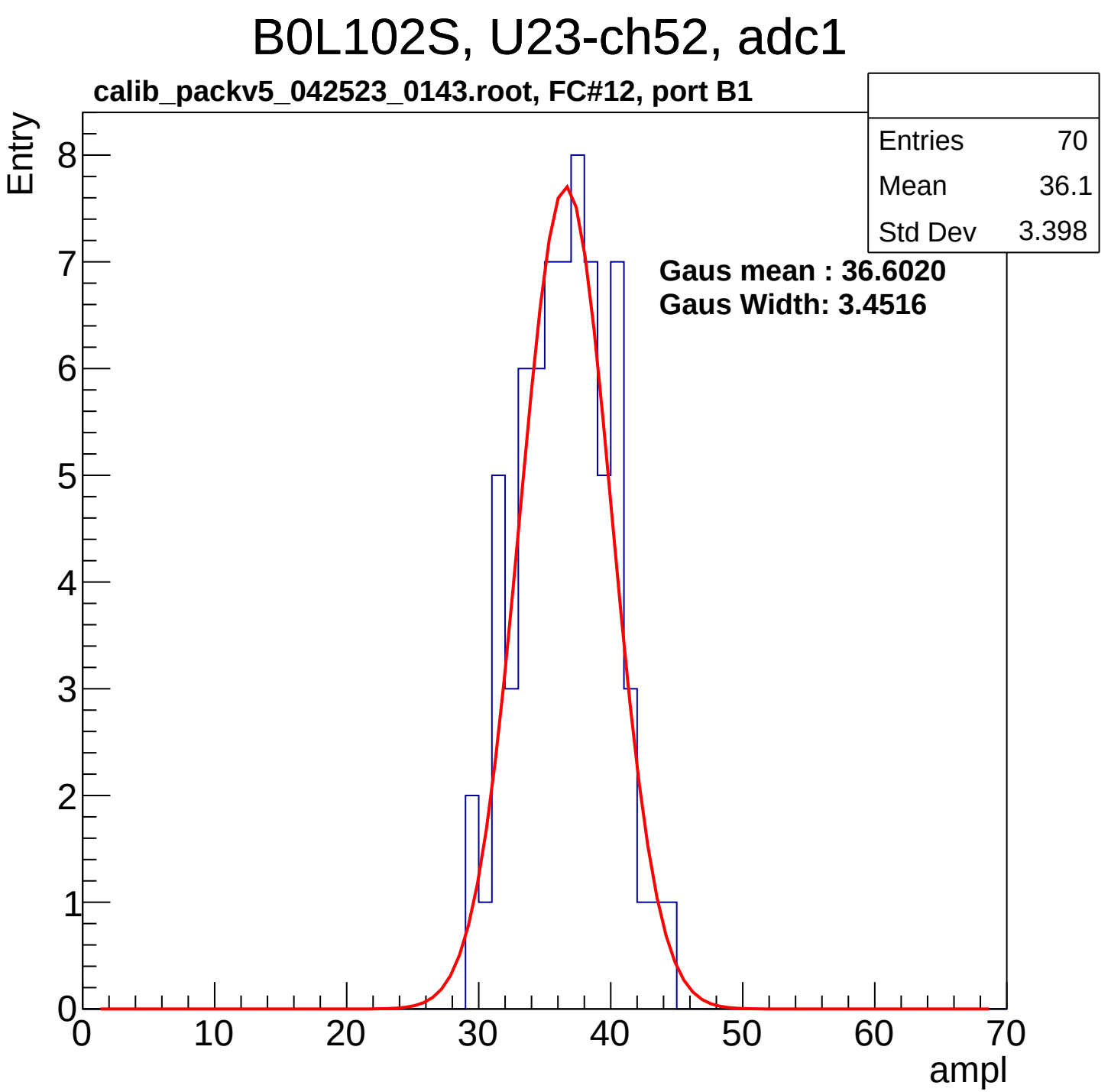
B0L102S, U23-ch52, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 36.1
Std Dev 3.398
Gaus mean : 36.6020
Gaus Width: 3.4516
Entry
ampl
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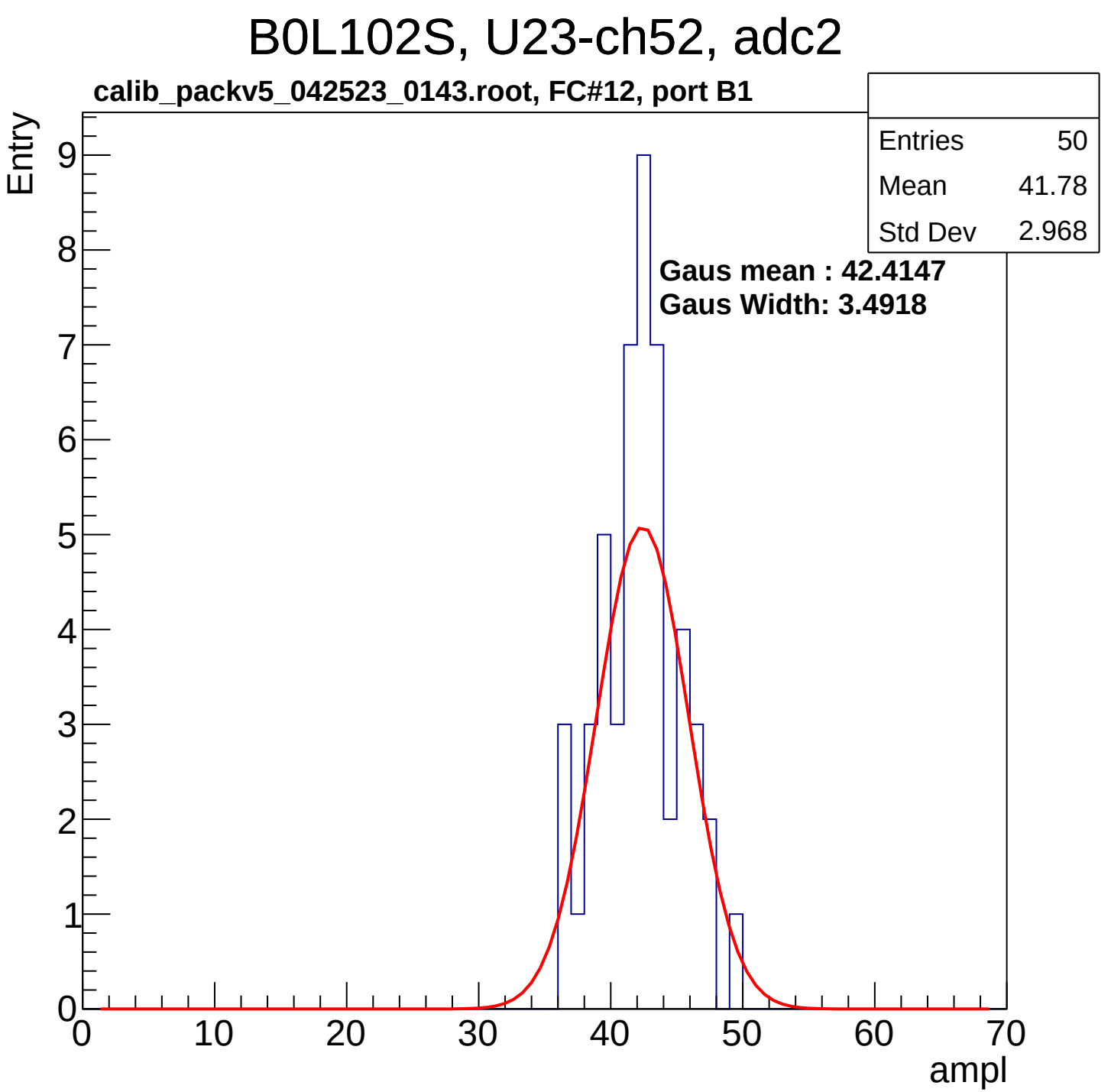
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch52, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 41.78
Std Dev 2.968
Gaus mean : 42.4147
Gaus Width: 3.4918
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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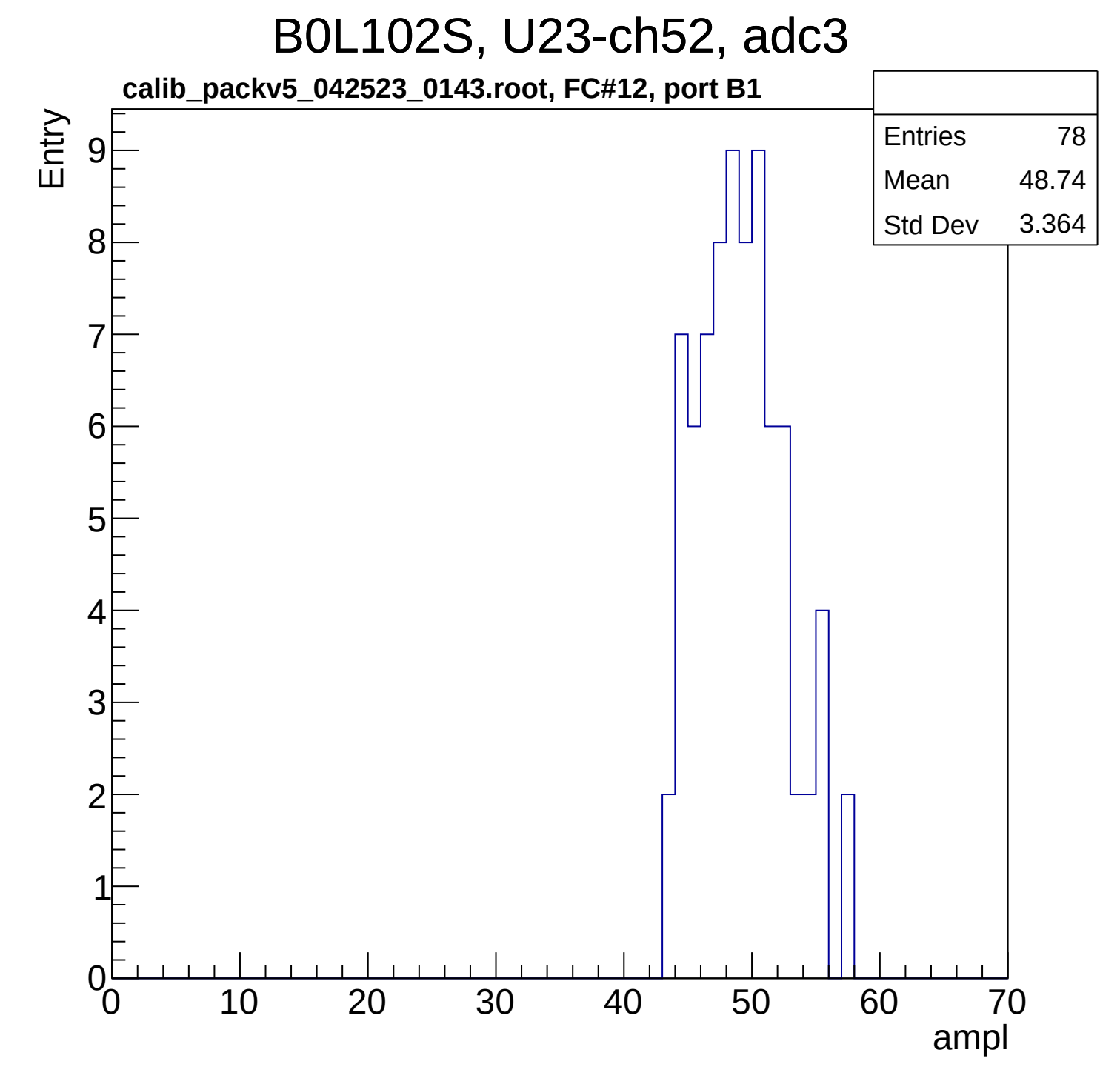
B0L102S, U23-ch52, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 48.74
Std Dev 3.364
Entry
ampl
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4
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6
7
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0
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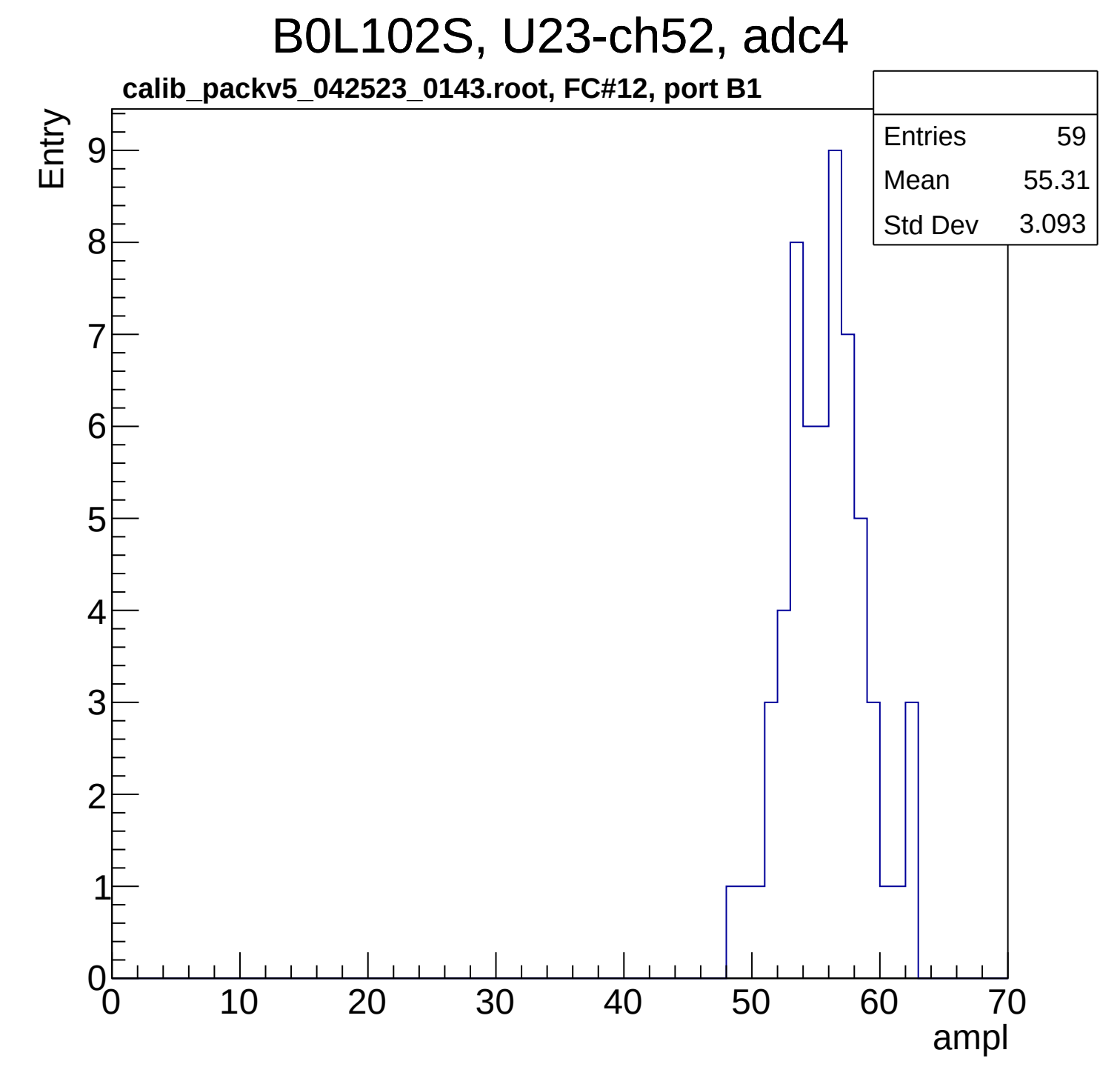
B0L102S, U23-ch52, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.31
Std Dev 3.093
Entry
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7
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9
0
10
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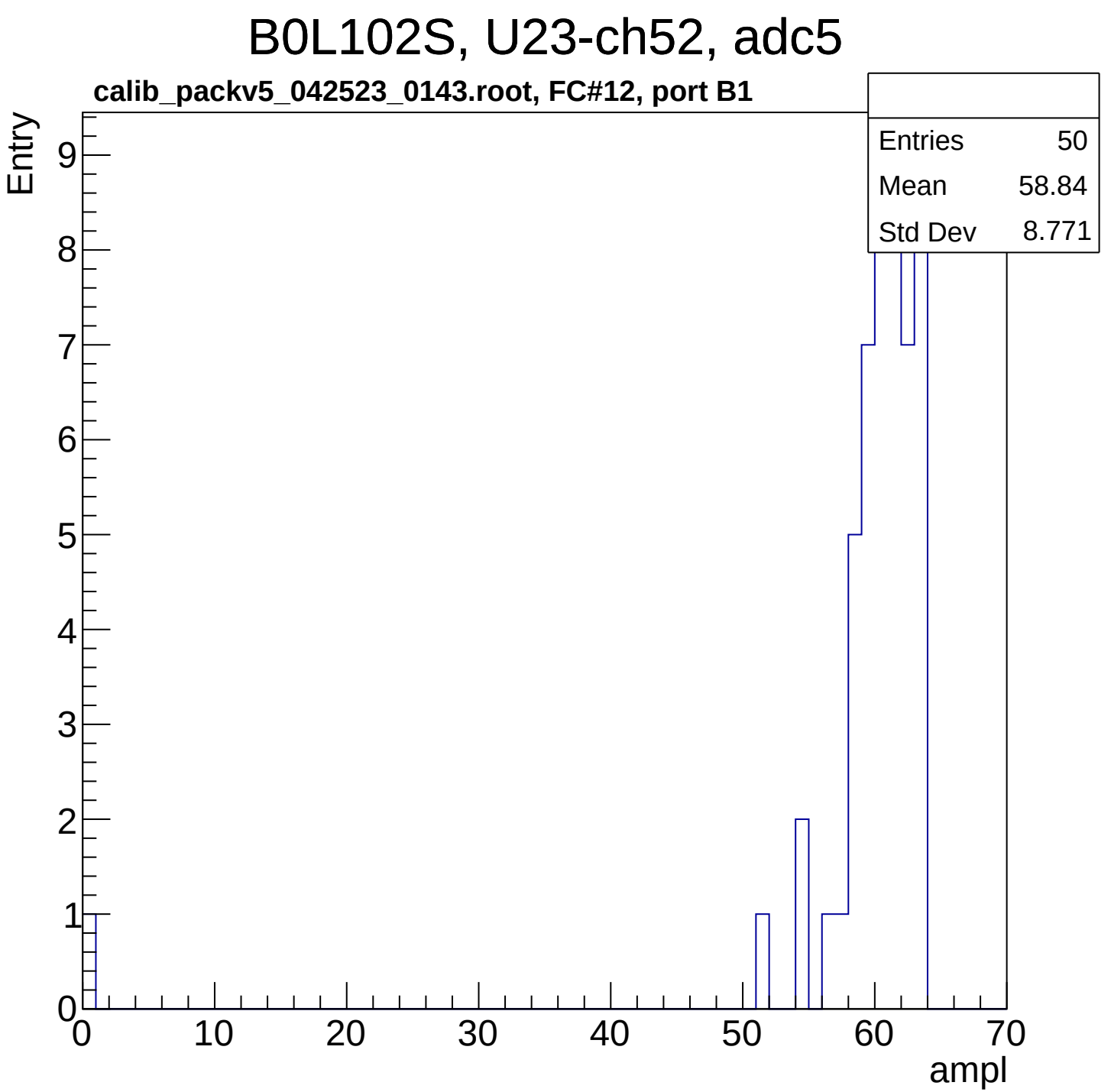
50
60
70
ampl


B0L102S, U23-ch52, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.84
Std Dev 8.771
Entry
ampl
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3
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5
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7
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0
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60
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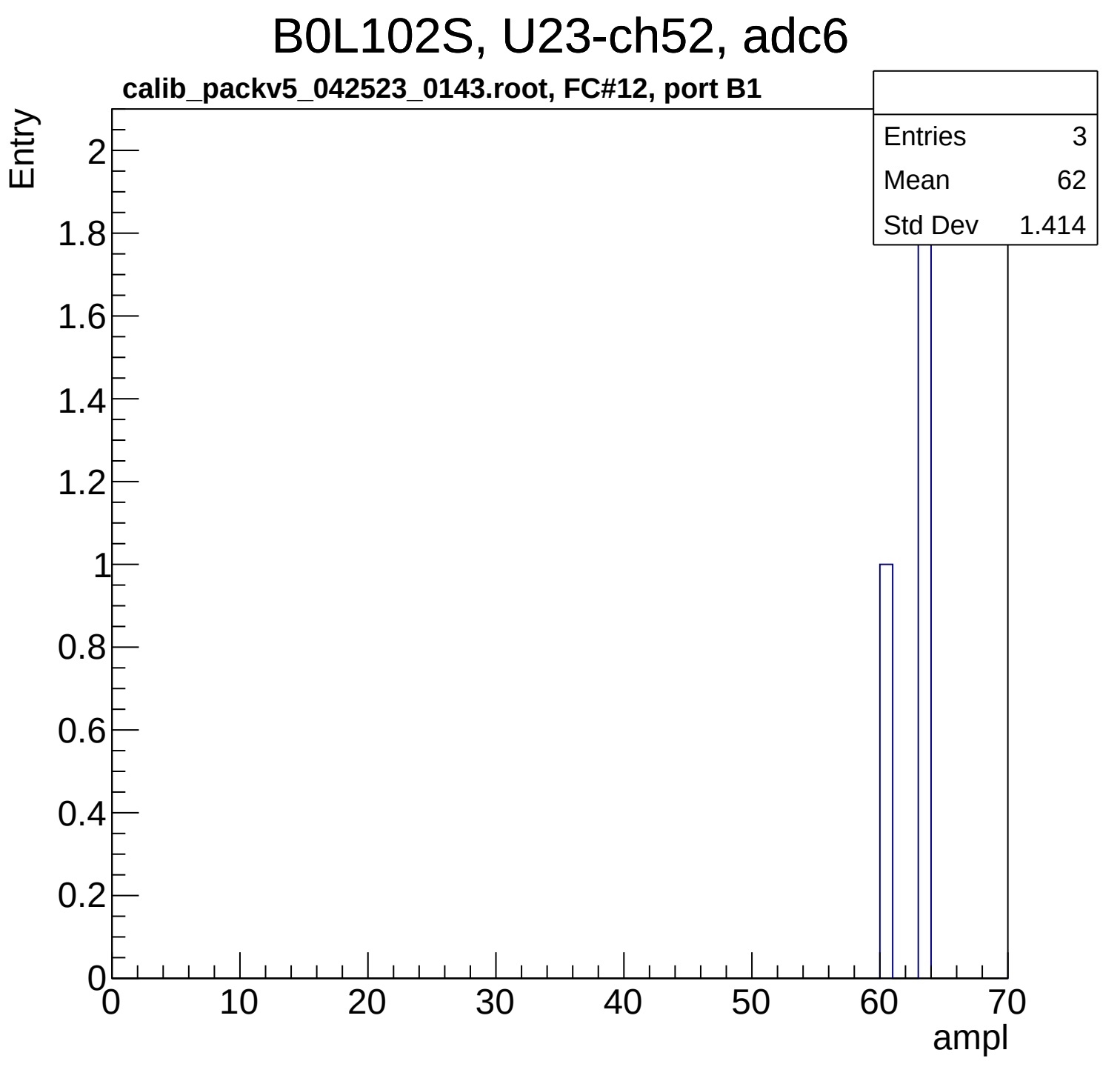
B0L102S, U23-ch52, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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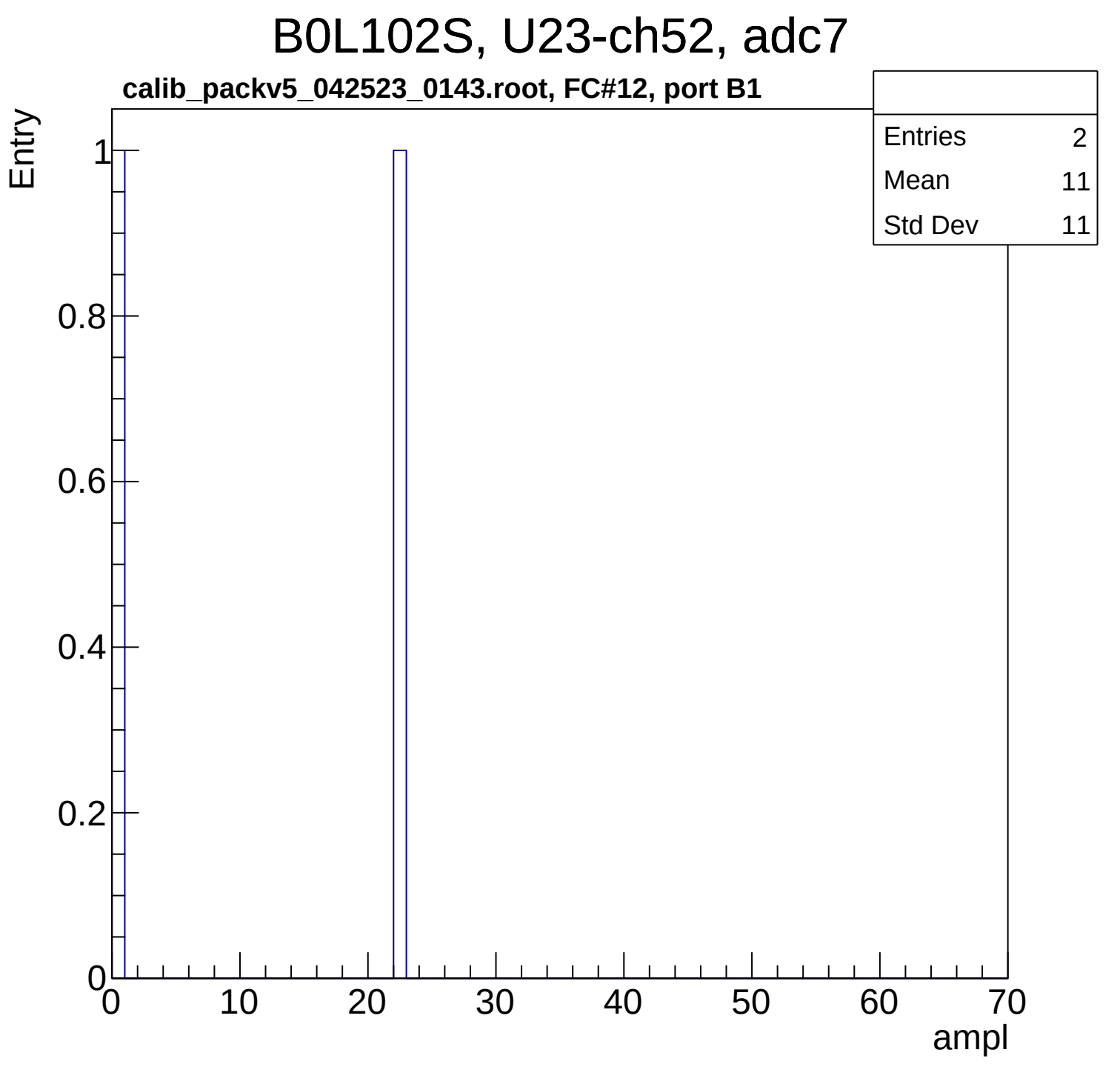
B0L102S, U23-ch52, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
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0
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10
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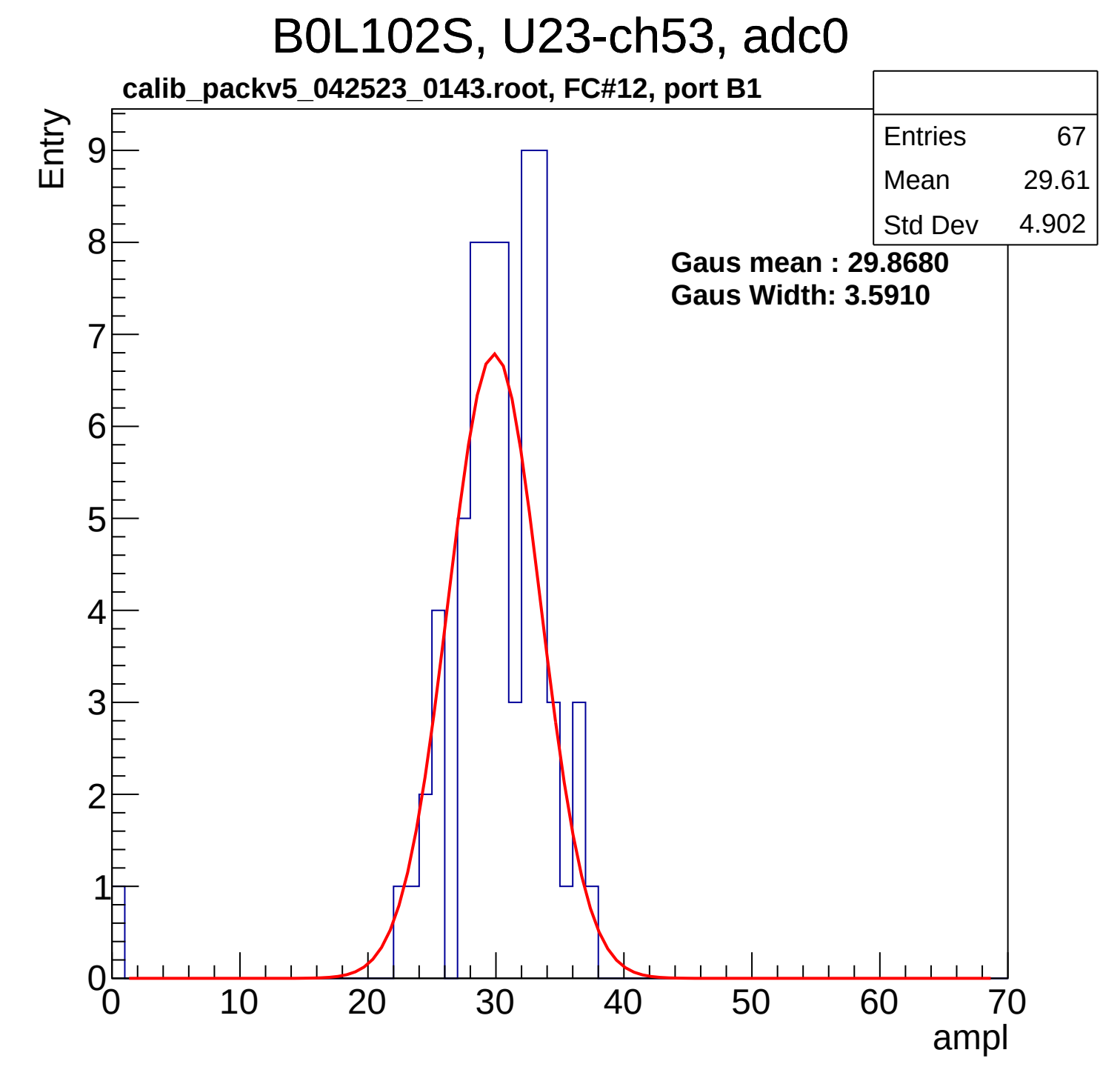
50
60
70
ampl


B0L102S, U23-ch53, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 29.61
Std Dev 4.902
Gaus mean : 29.8680
Gaus Width: 3.5910
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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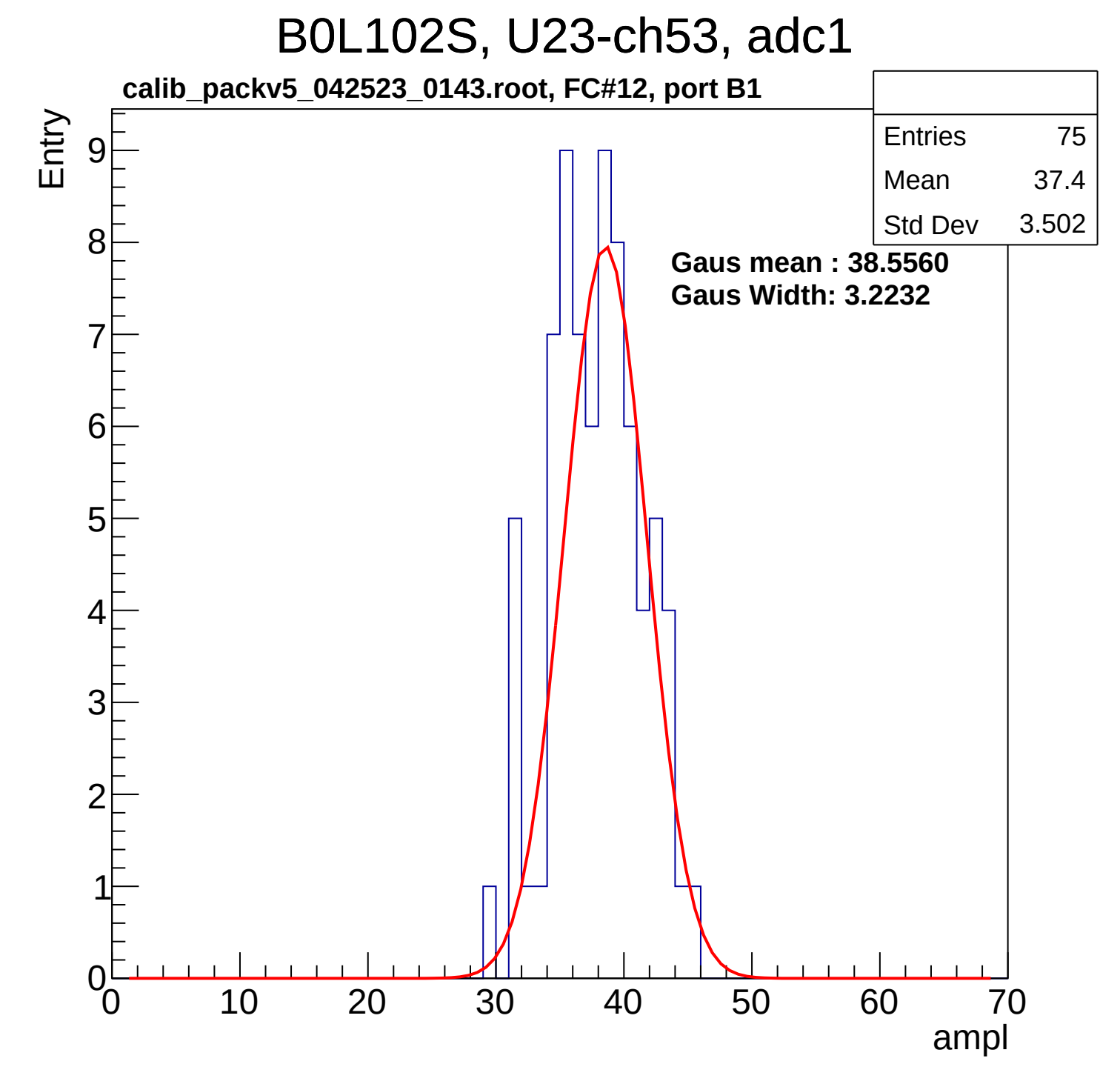
60
70


B0L102S, U23-ch53, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.4
Std Dev 3.502
Gaus mean : 38.5560
Gaus Width: 3.2232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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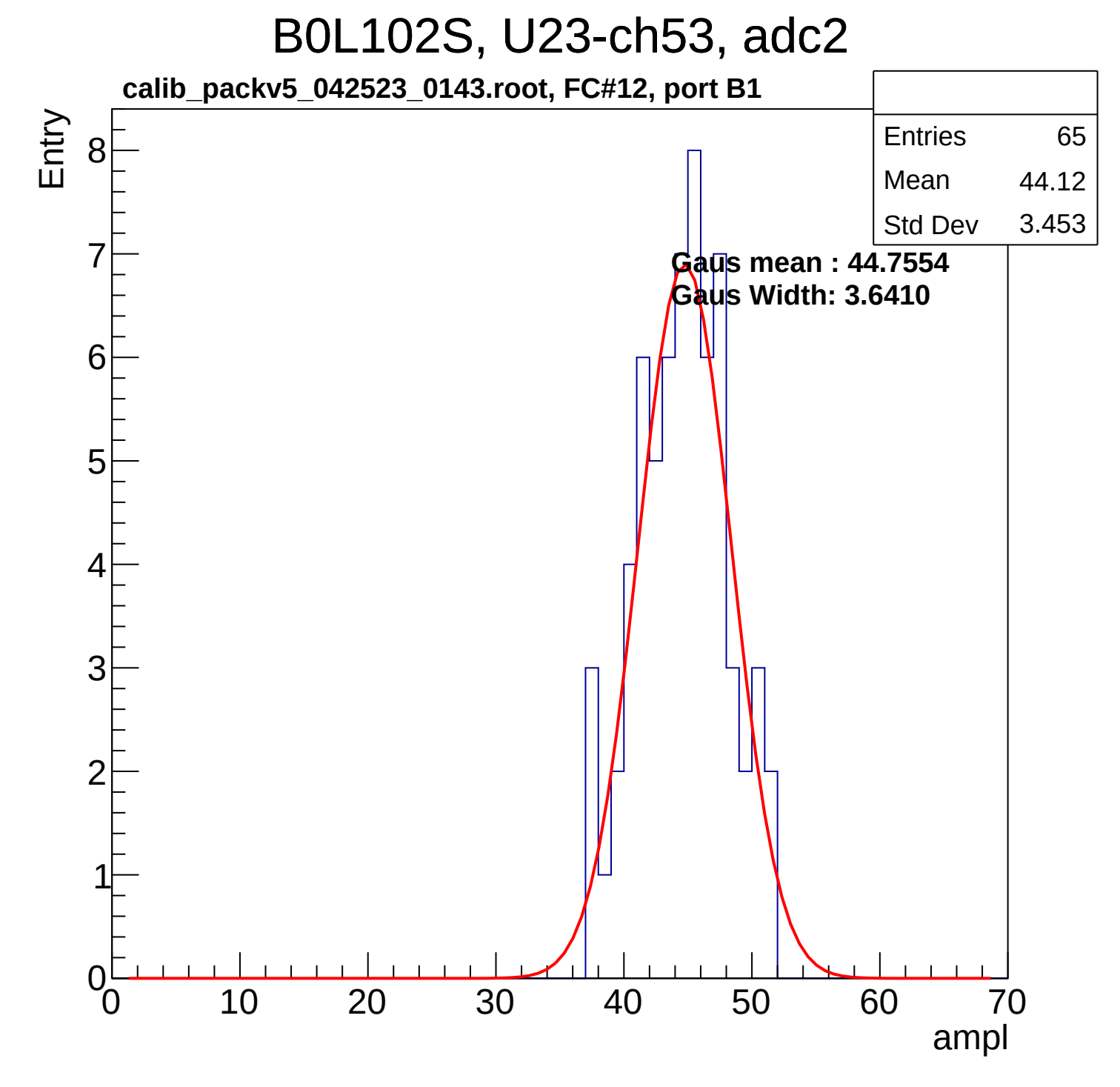
60
70


B0L102S, U23-ch53, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 44.12
Std Dev 3.453
Gaus mean : 44.7554
Gaus Width: 3.6410
Entry
ampl
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2
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5
6
7
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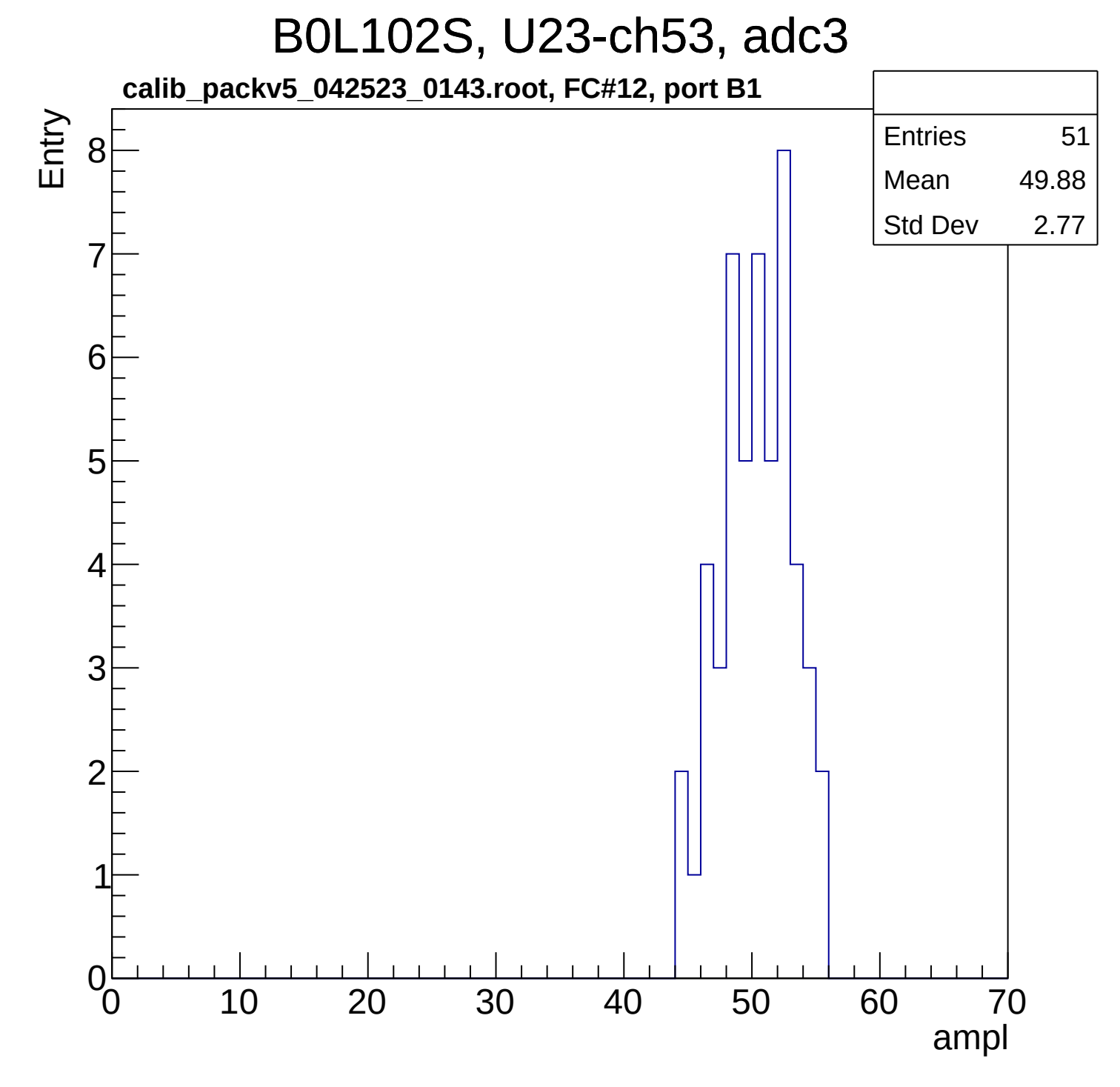
B0L102S, U23-ch53, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 49.88
Std Dev 2.77
Entry
ampl
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5
6
7
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0
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20
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40
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60
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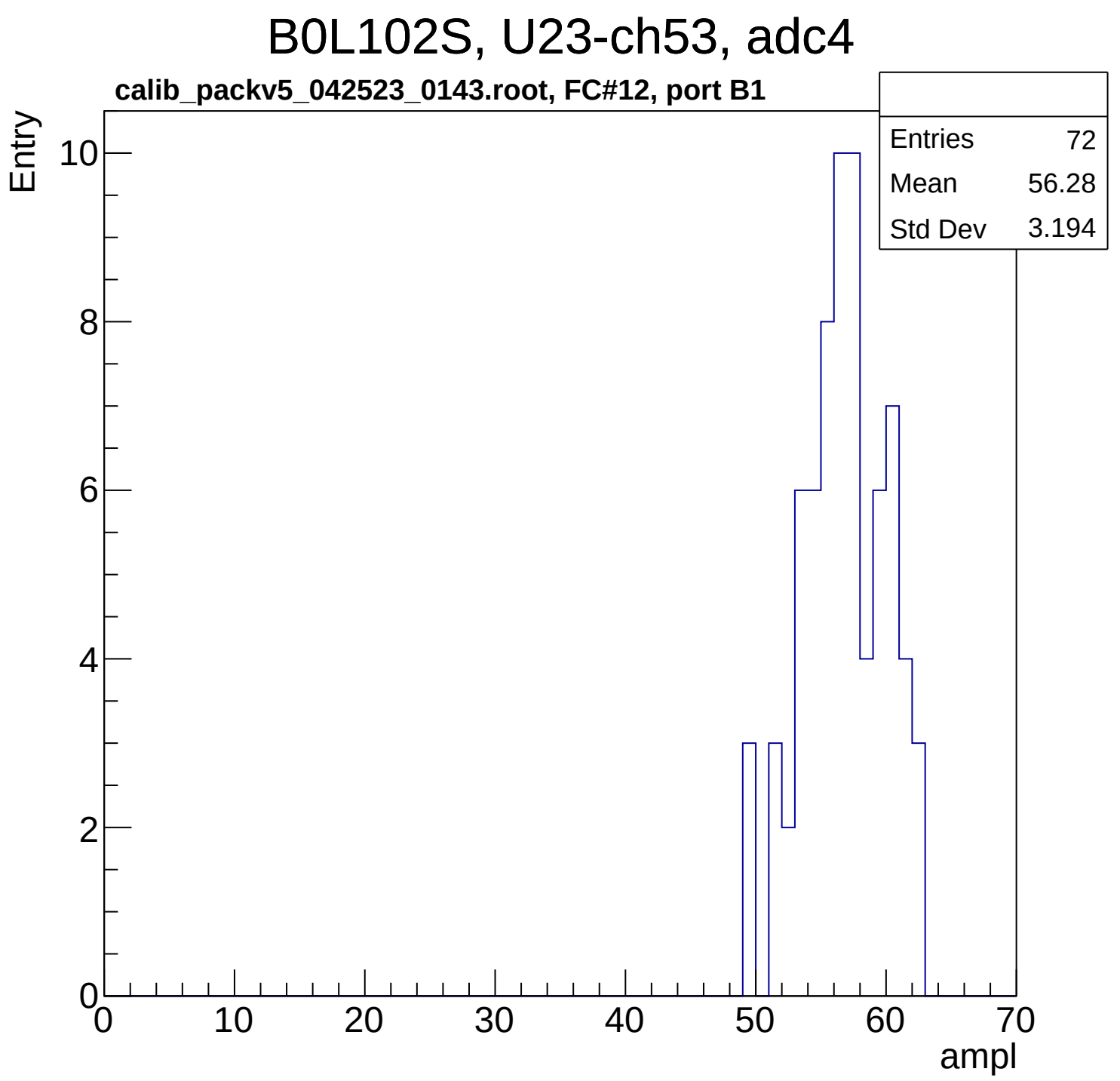
B0L102S, U23-ch53, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 56.28
Std Dev 3.194
Entry
ampl
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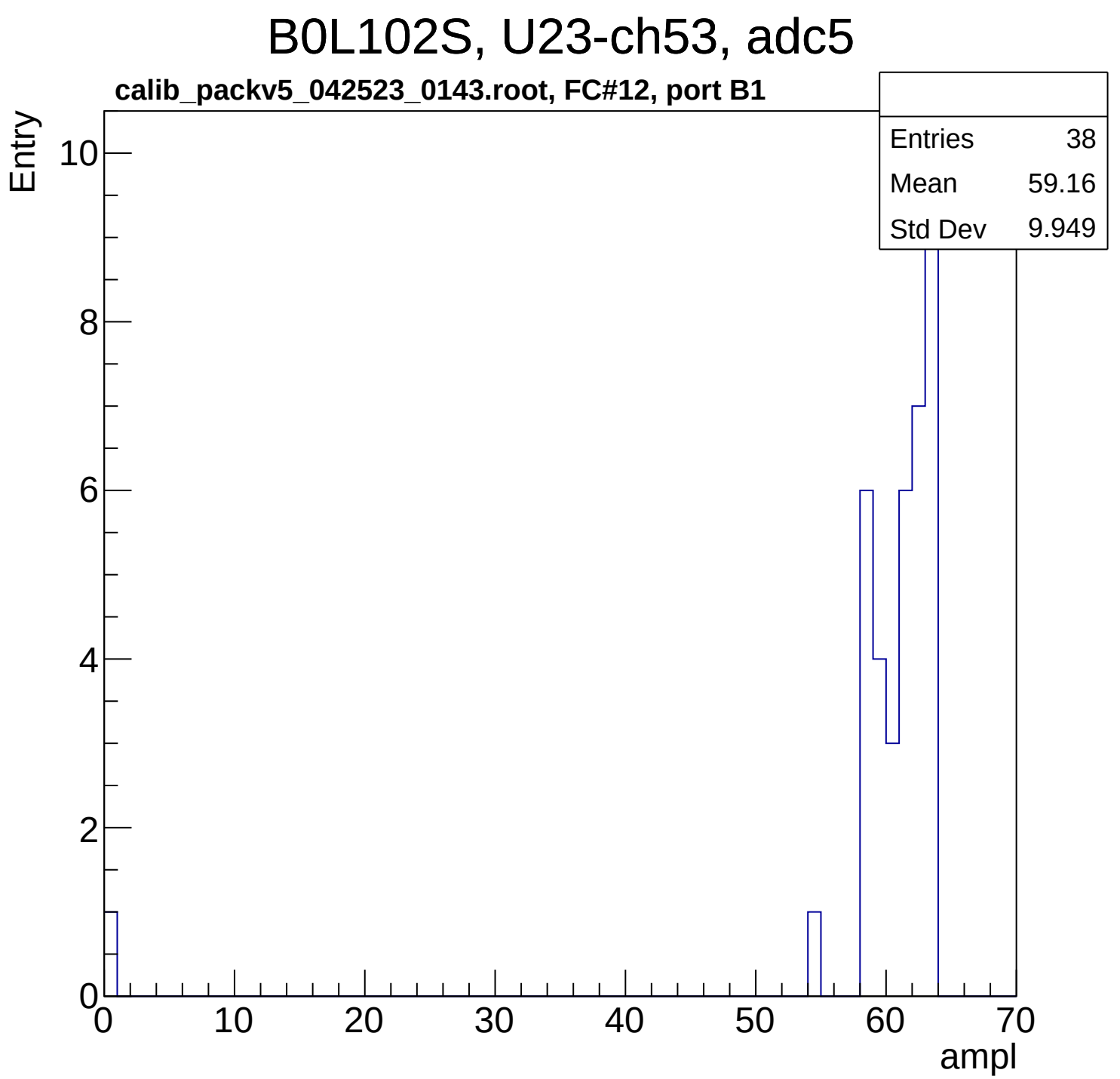
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch53, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 59.16
Std Dev 9.949
Entry
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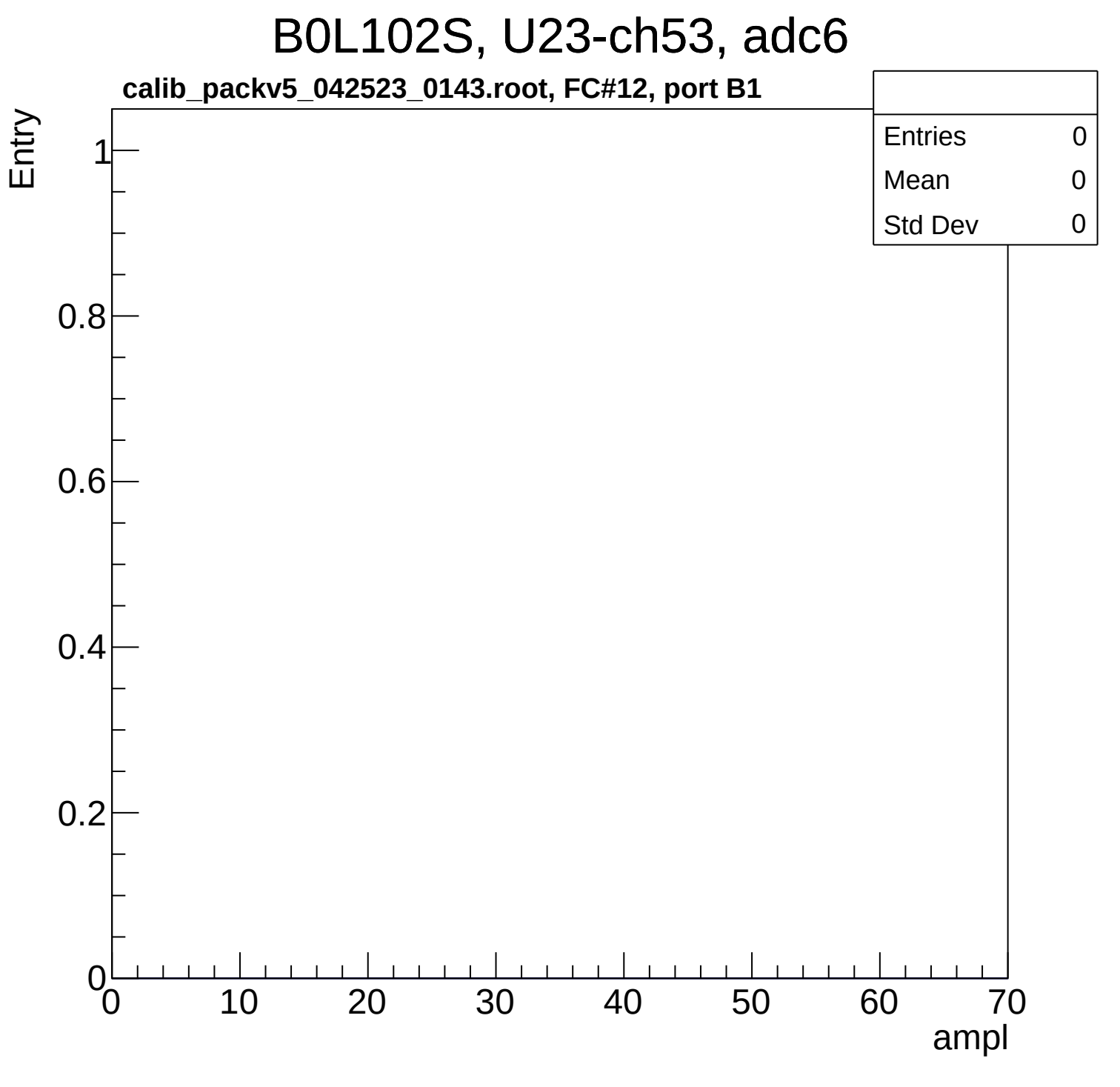
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch53, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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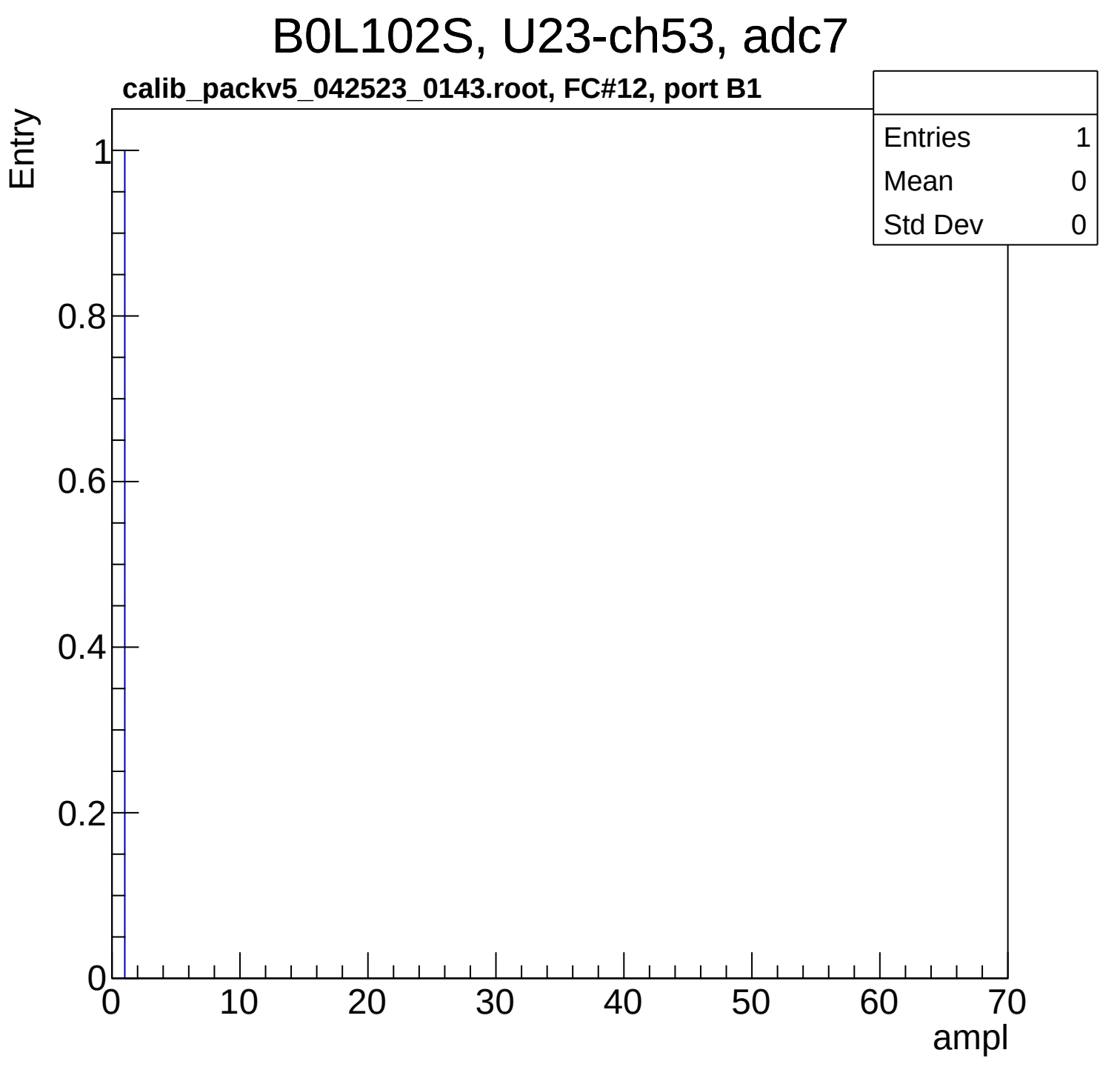
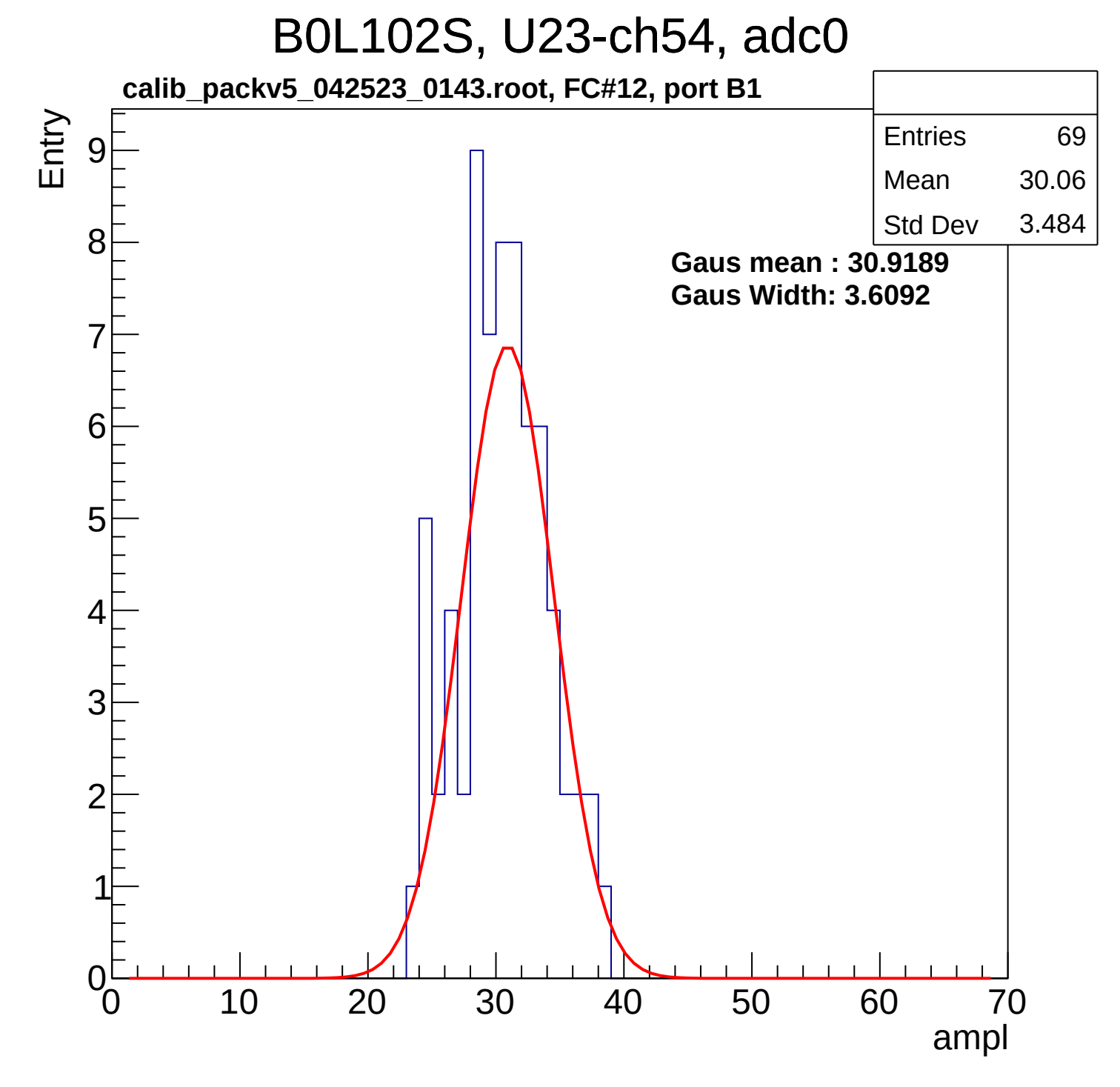
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch53, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch54, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 30.06
Std Dev 3.484
Gaus mean : 30.9189
Gaus Width: 3.6092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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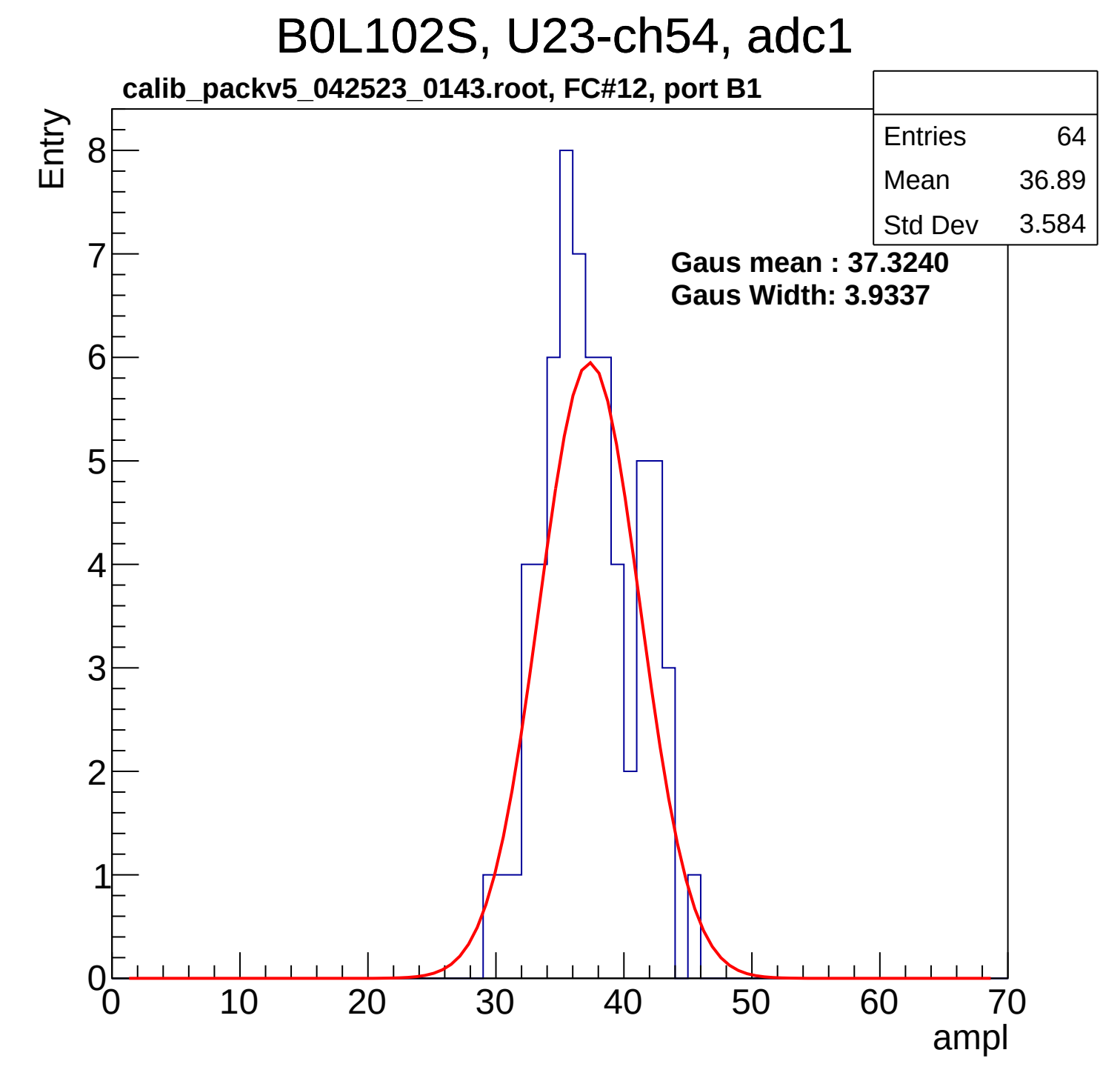
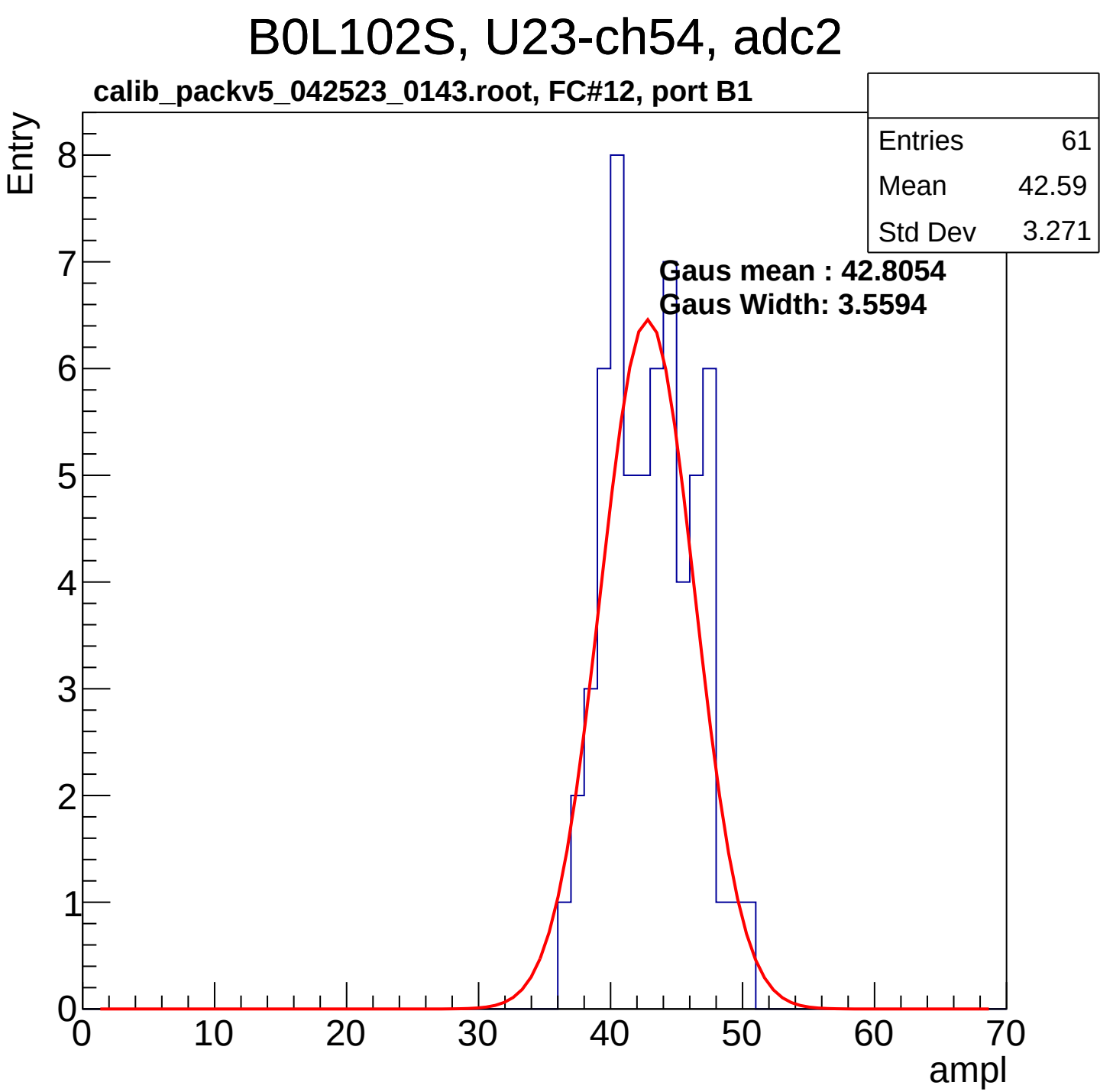
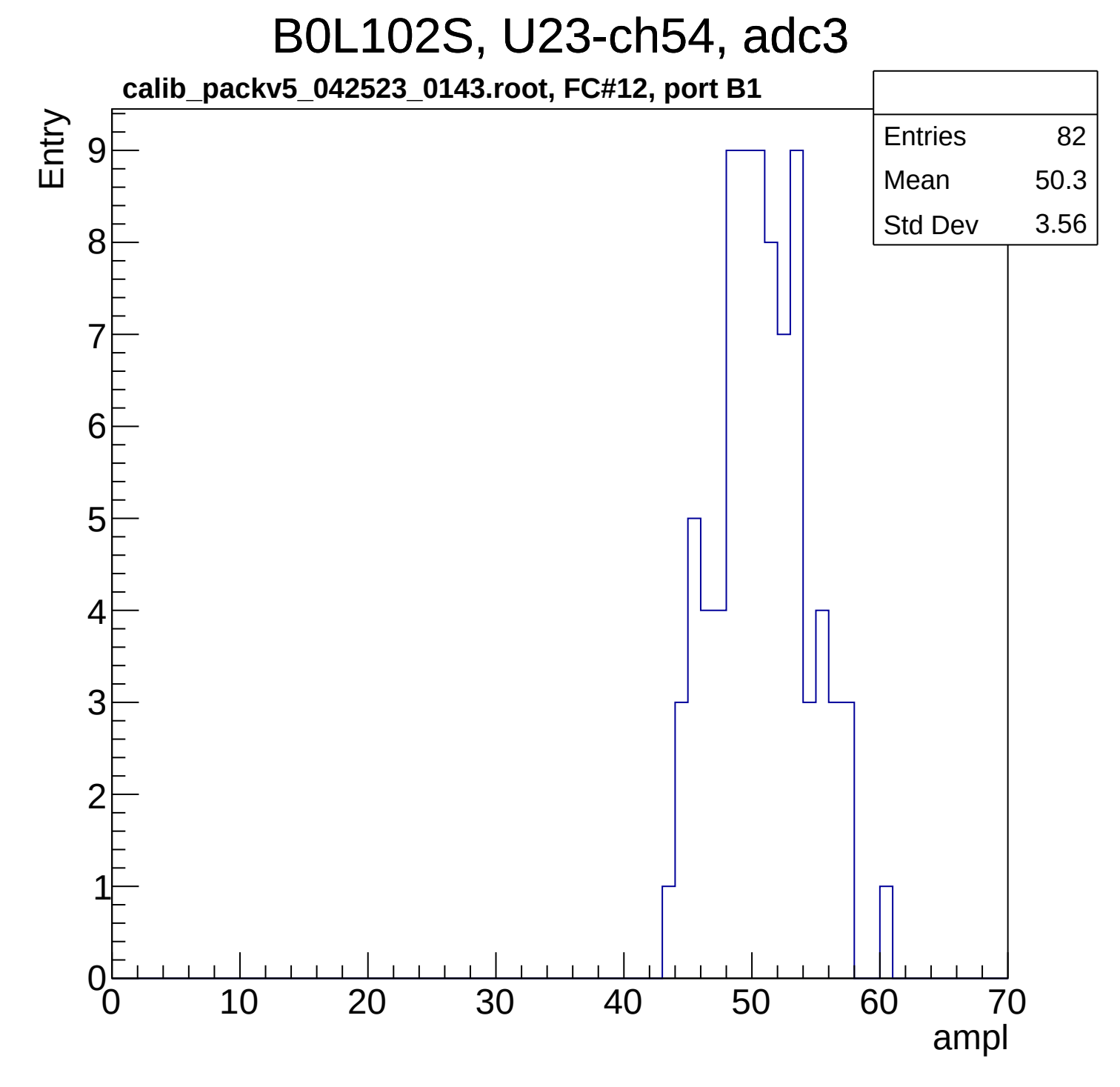
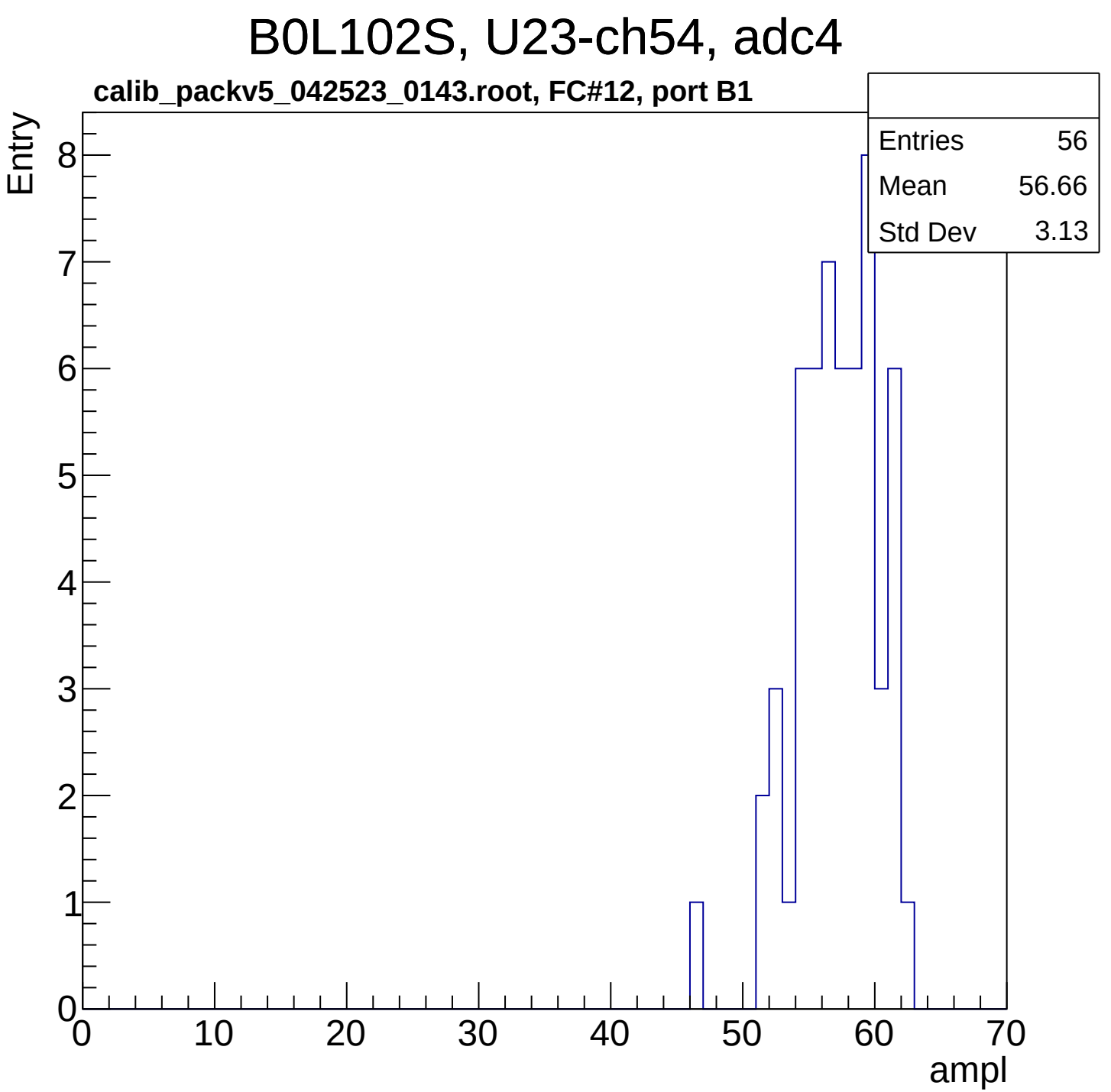
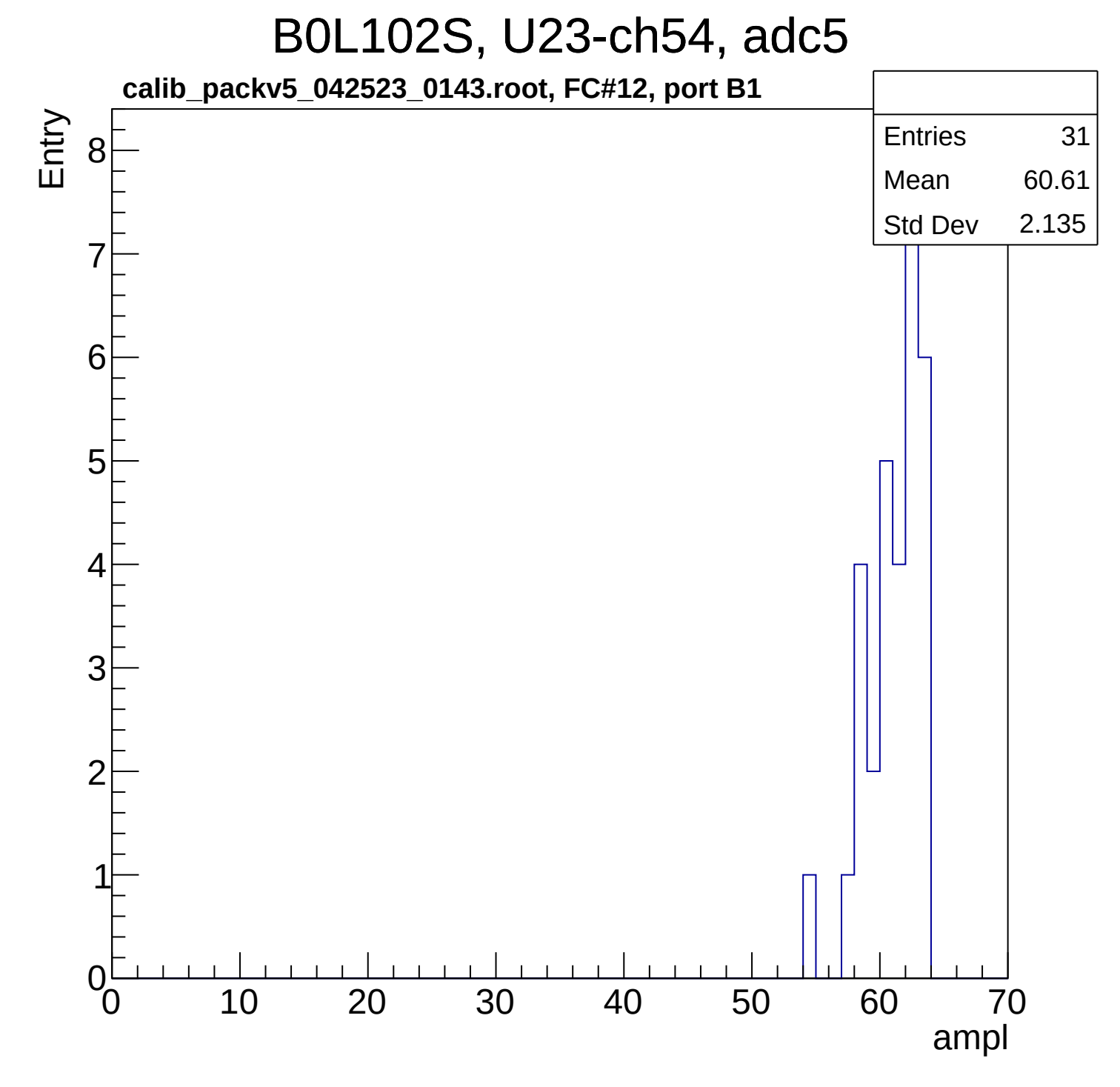
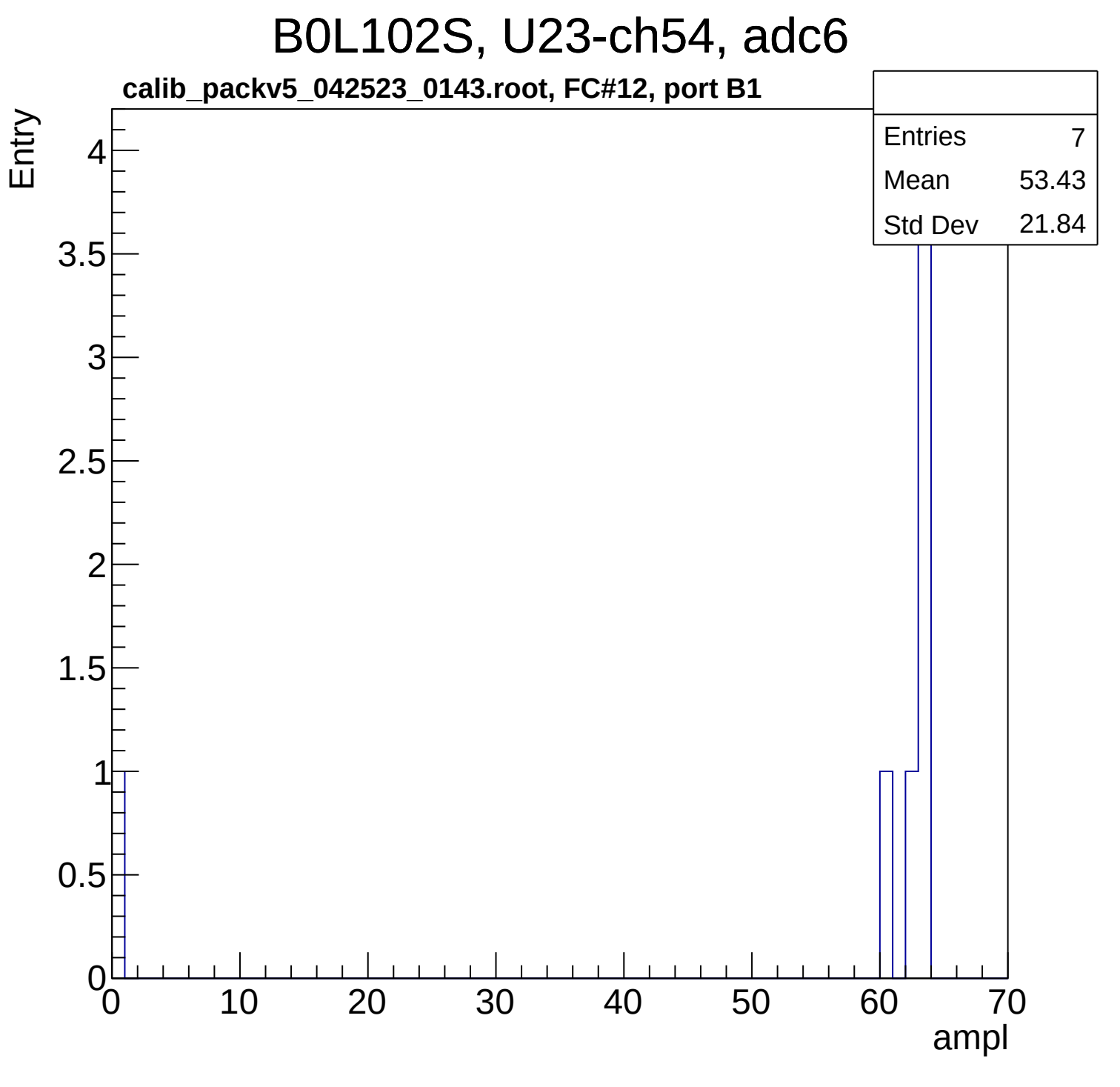

B0L102S, U23-ch54, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.89
Std Dev 3.584
Gaus mean : 37.3240
Gaus Width: 3.9337
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

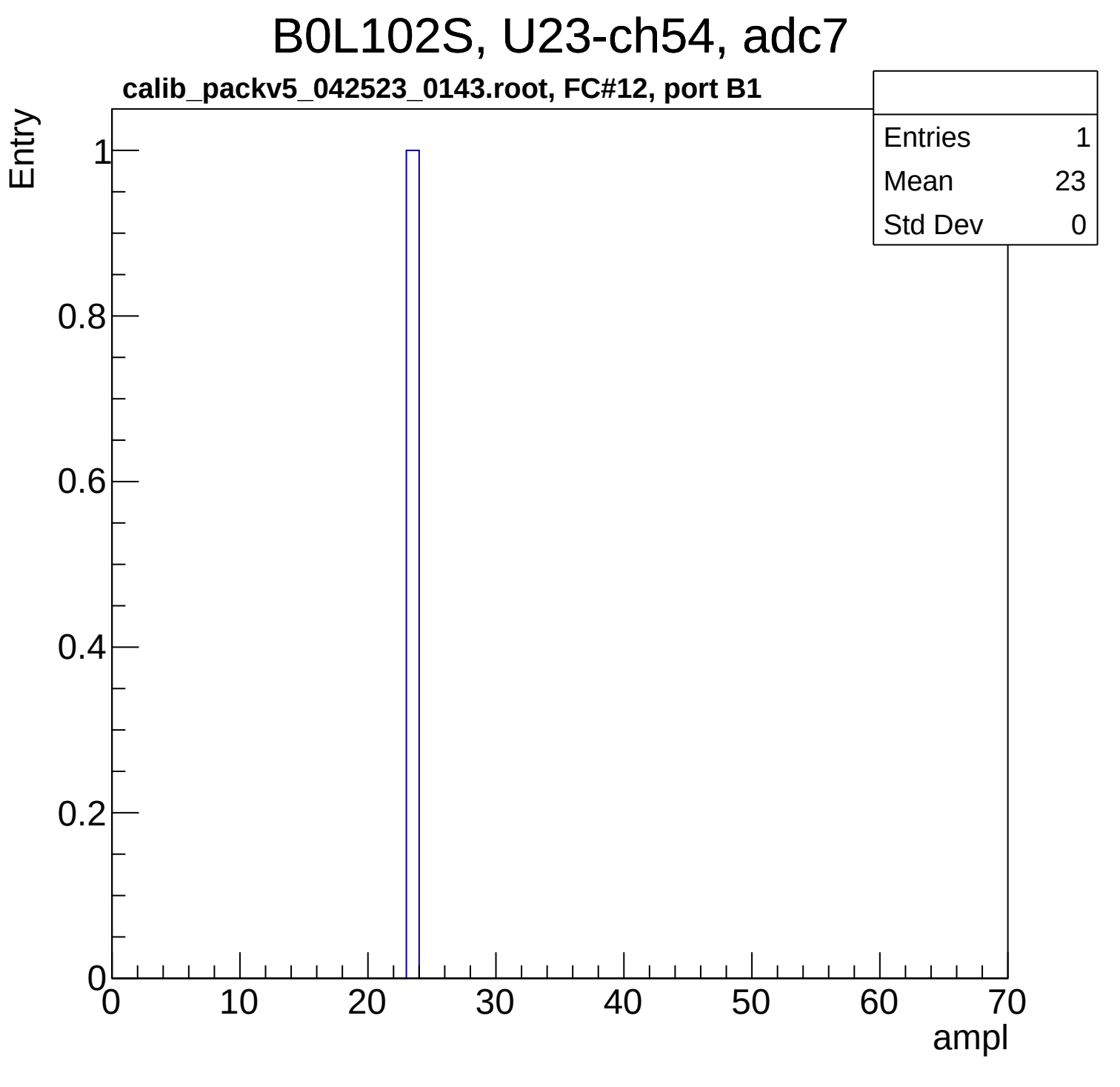

B0L102S, U23-ch54, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 42.59
Std Dev 3.271
Gaus mean : 42.8054
Gaus Width: 3.5594
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

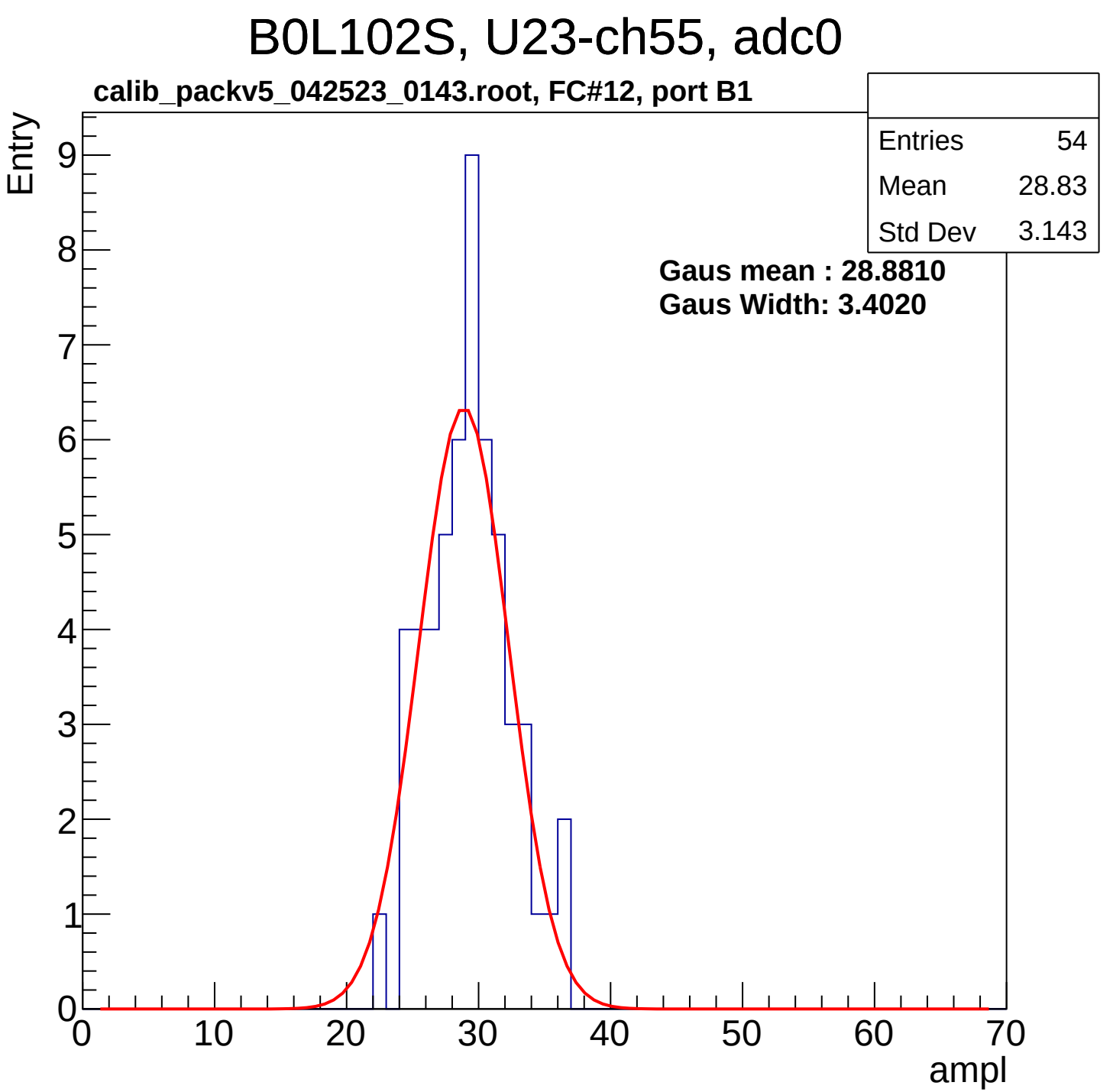

B0L102S, U23-ch54, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 50.3
Std Dev 3.56
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

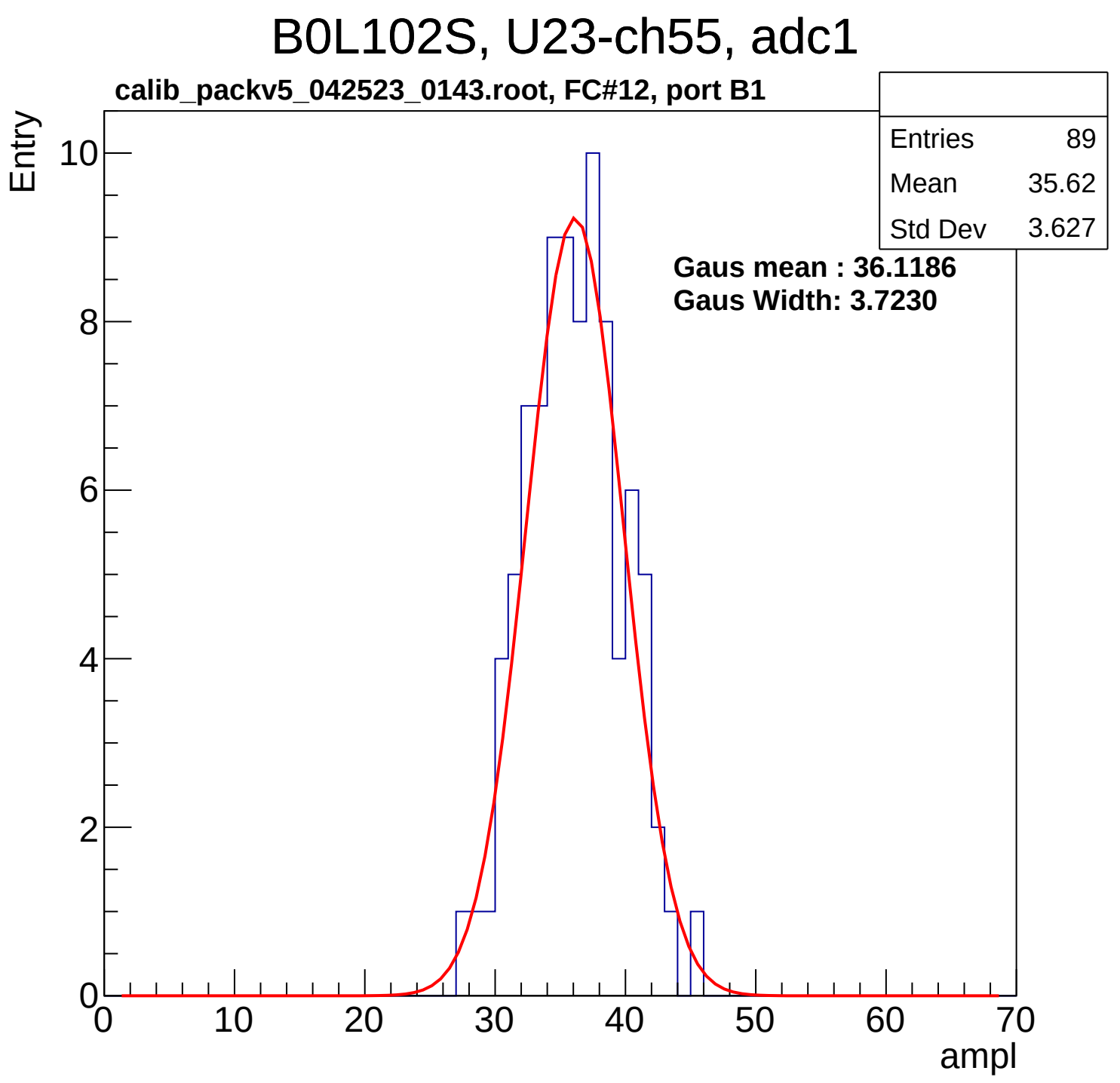

B0L102S, U23-ch54, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 56.66
Std Dev 3.13
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

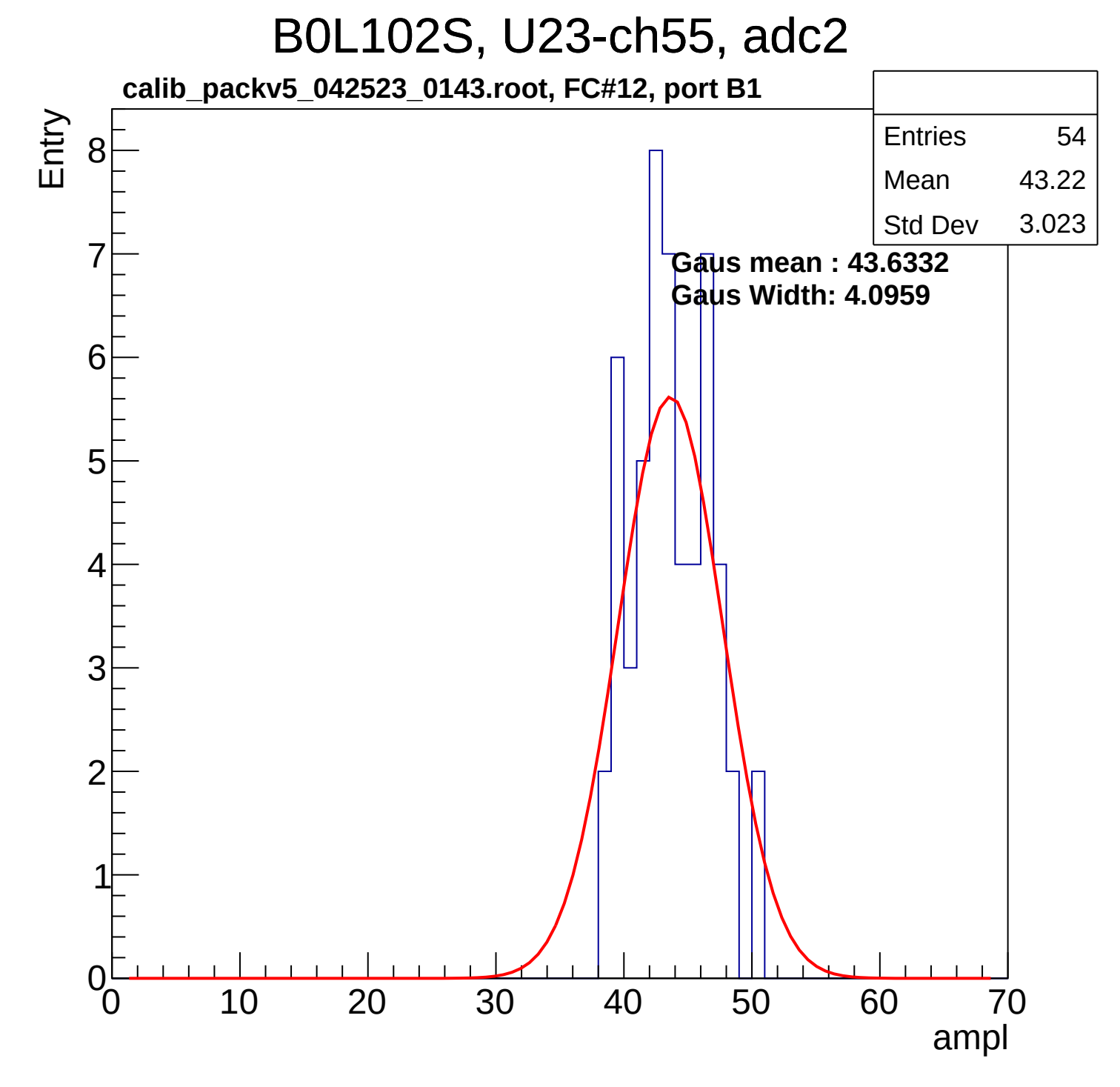

B0L102S, U23-ch54, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 31
Mean 60.61
Std Dev 2.135
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

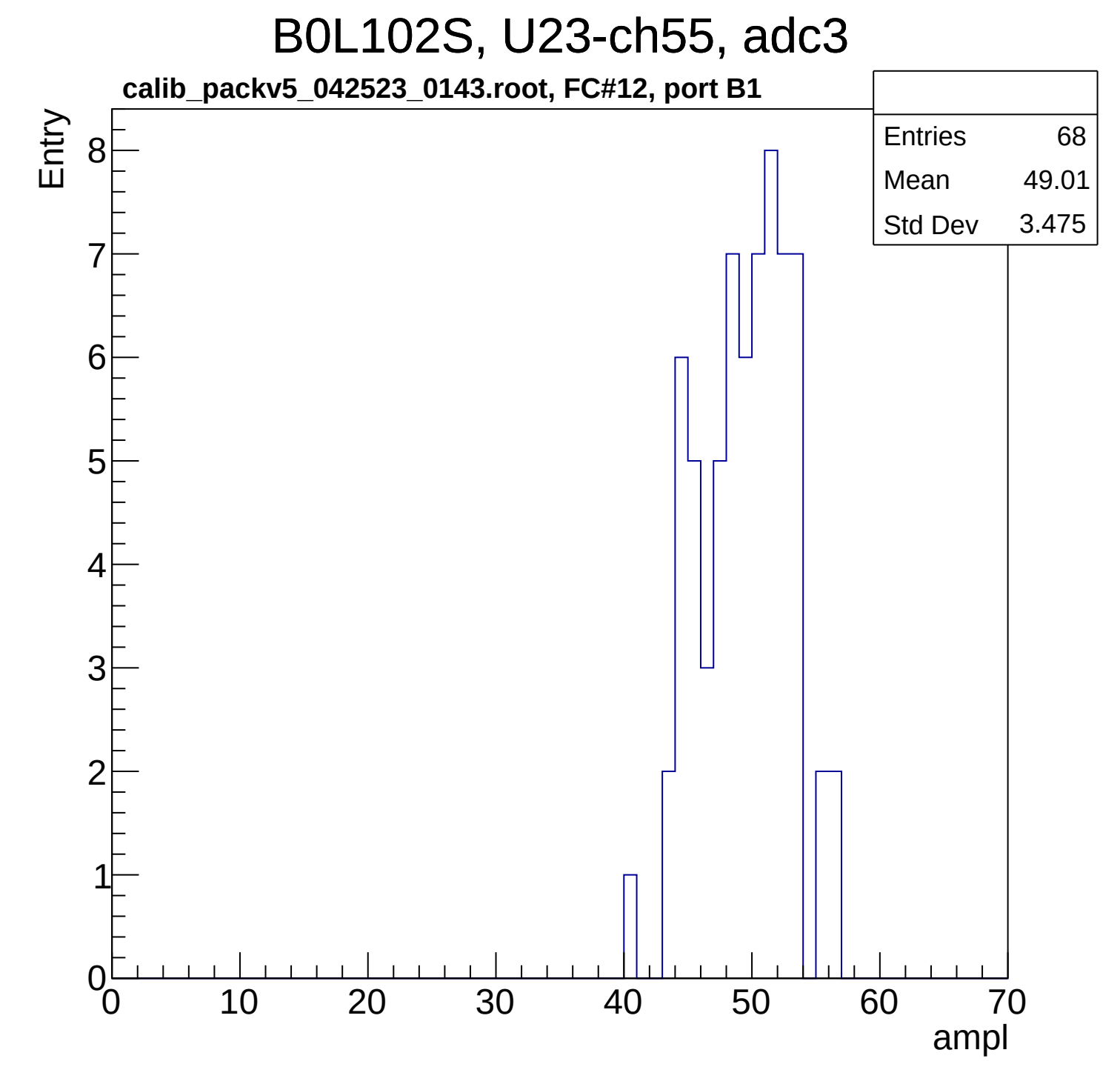

B0L102S, U23-ch54, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 53.43
Std Dev 21.84
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch54, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch55, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 28.83
Std Dev 3.143
Gaus mean : 28.8810
Gaus Width: 3.4020
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch55, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 89
Mean 35.62
Std Dev 3.627
Gaus mean : 36.1186
Gaus Width: 3.7230
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch55, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43.22
Std Dev 3.023
Gaus mean : 43.6332
Gaus Width: 4.0959
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch55, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.01
Std Dev 3.475
Entry
ampl
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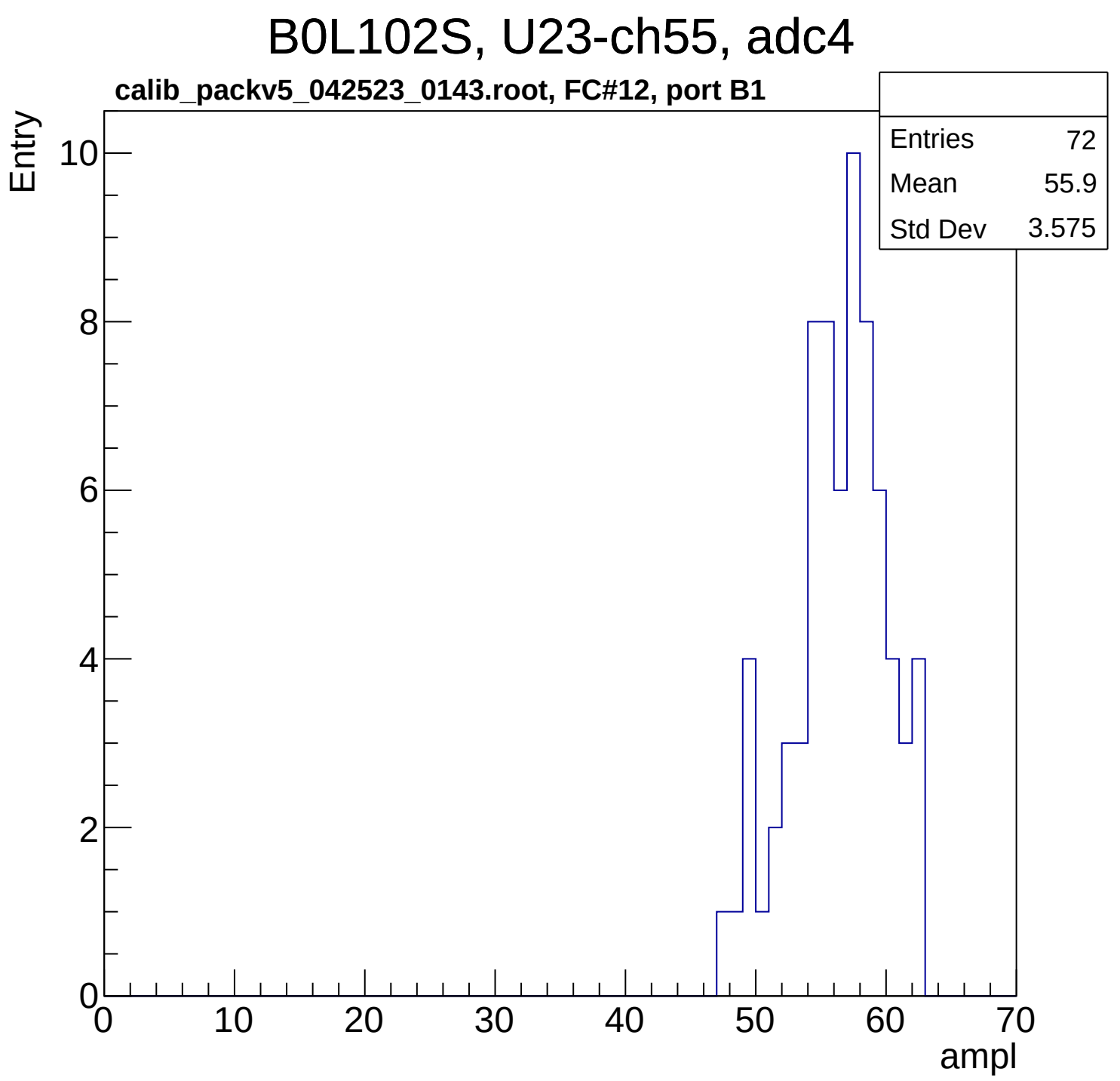
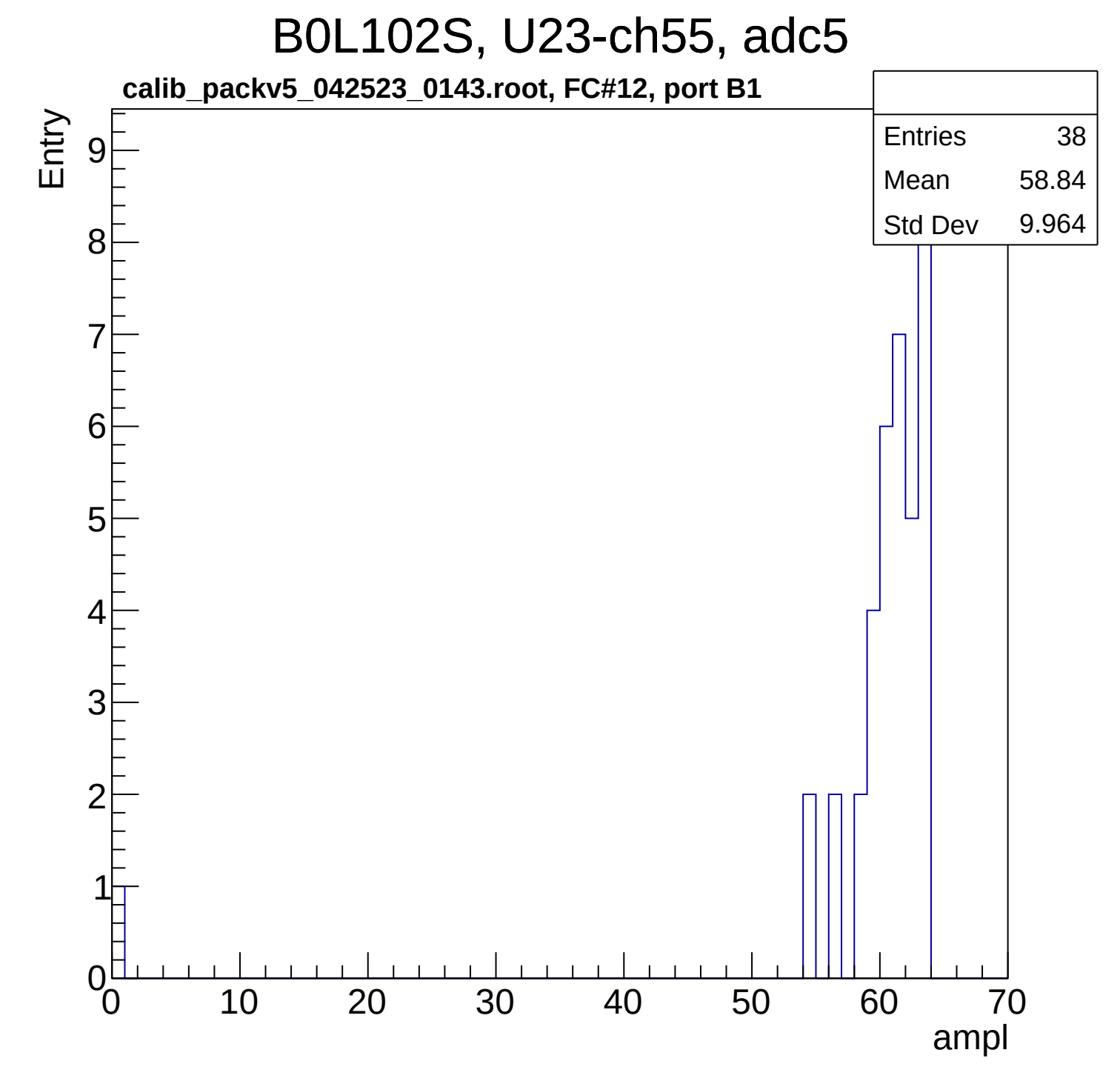
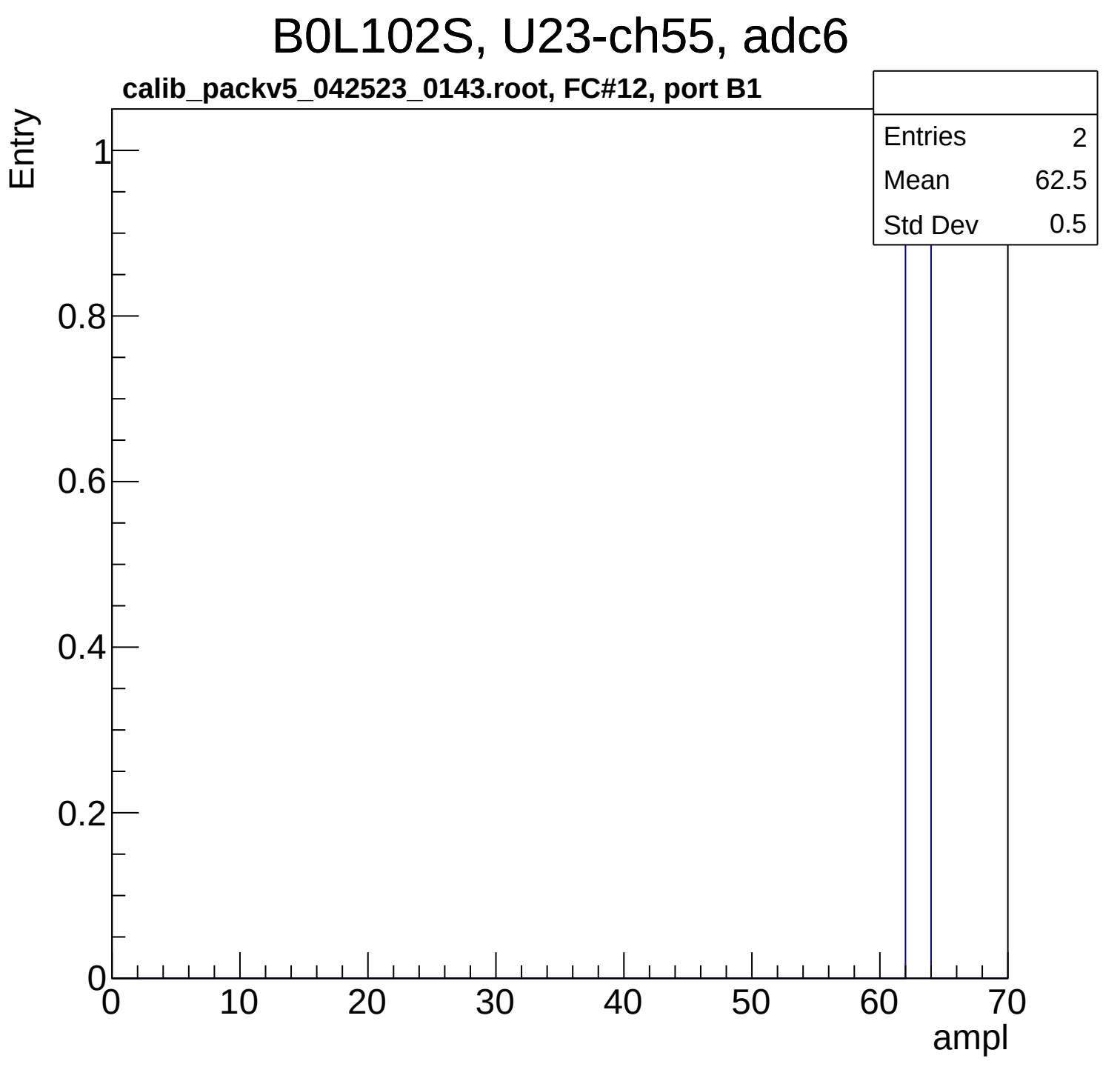
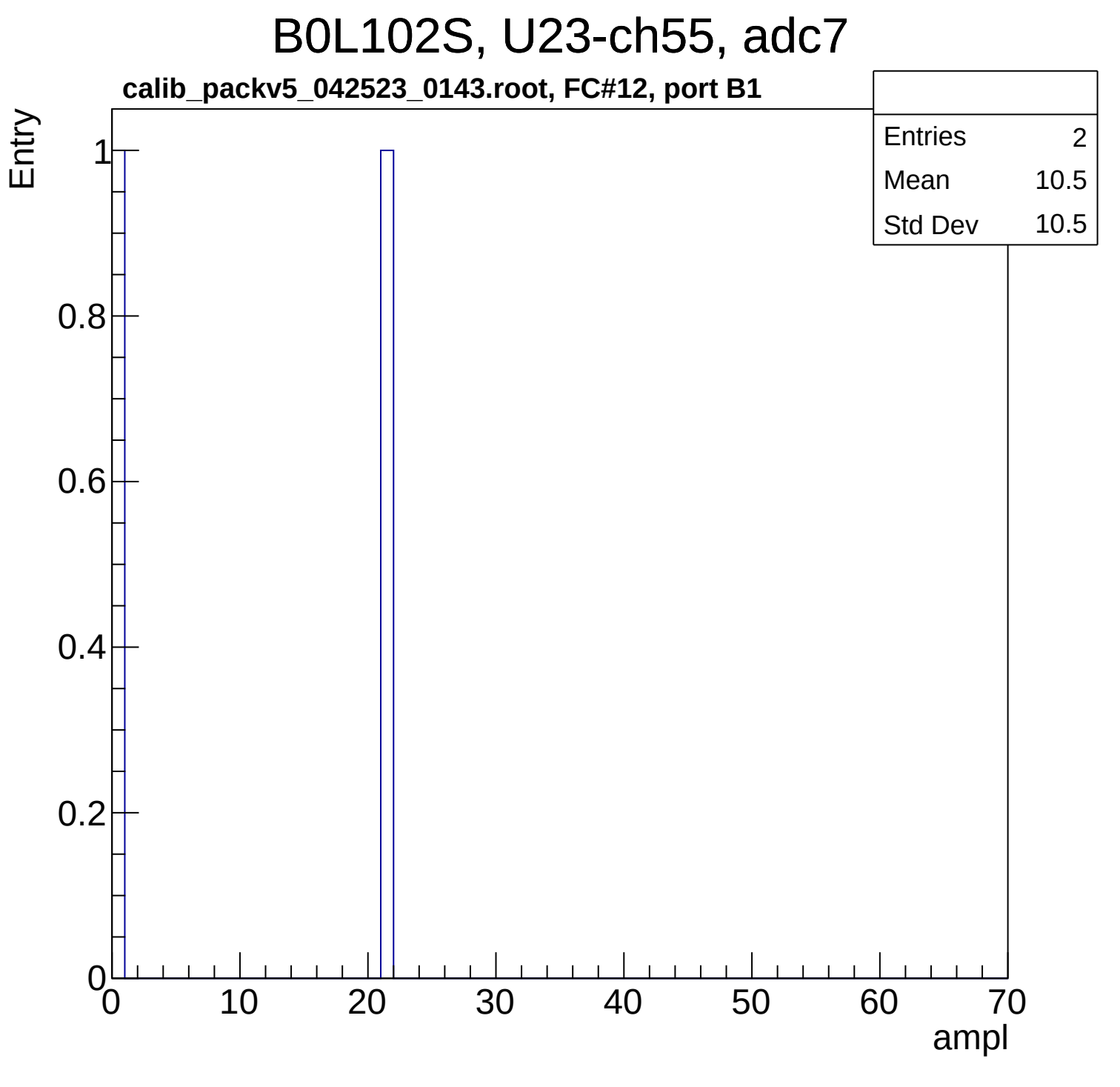
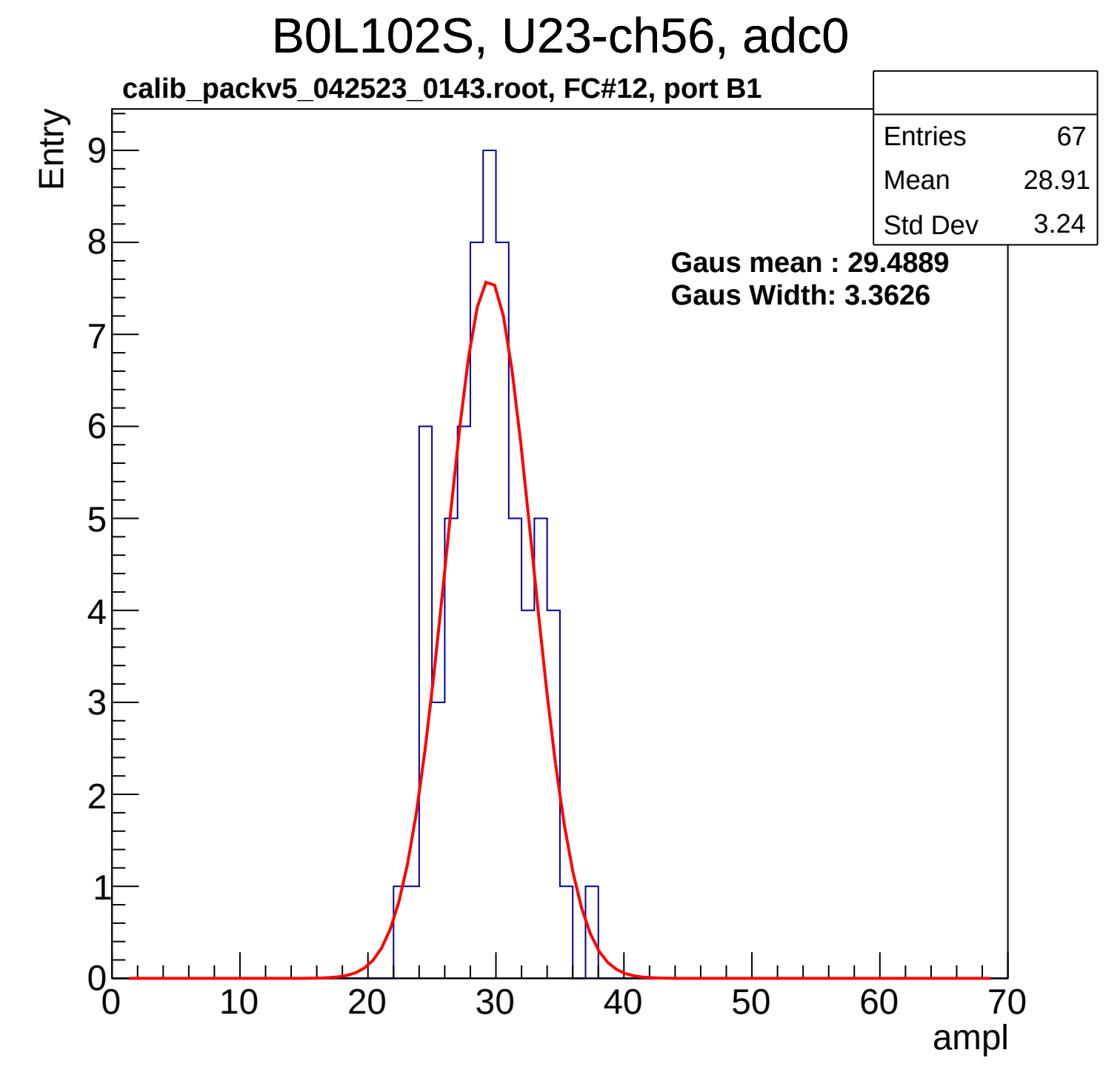
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

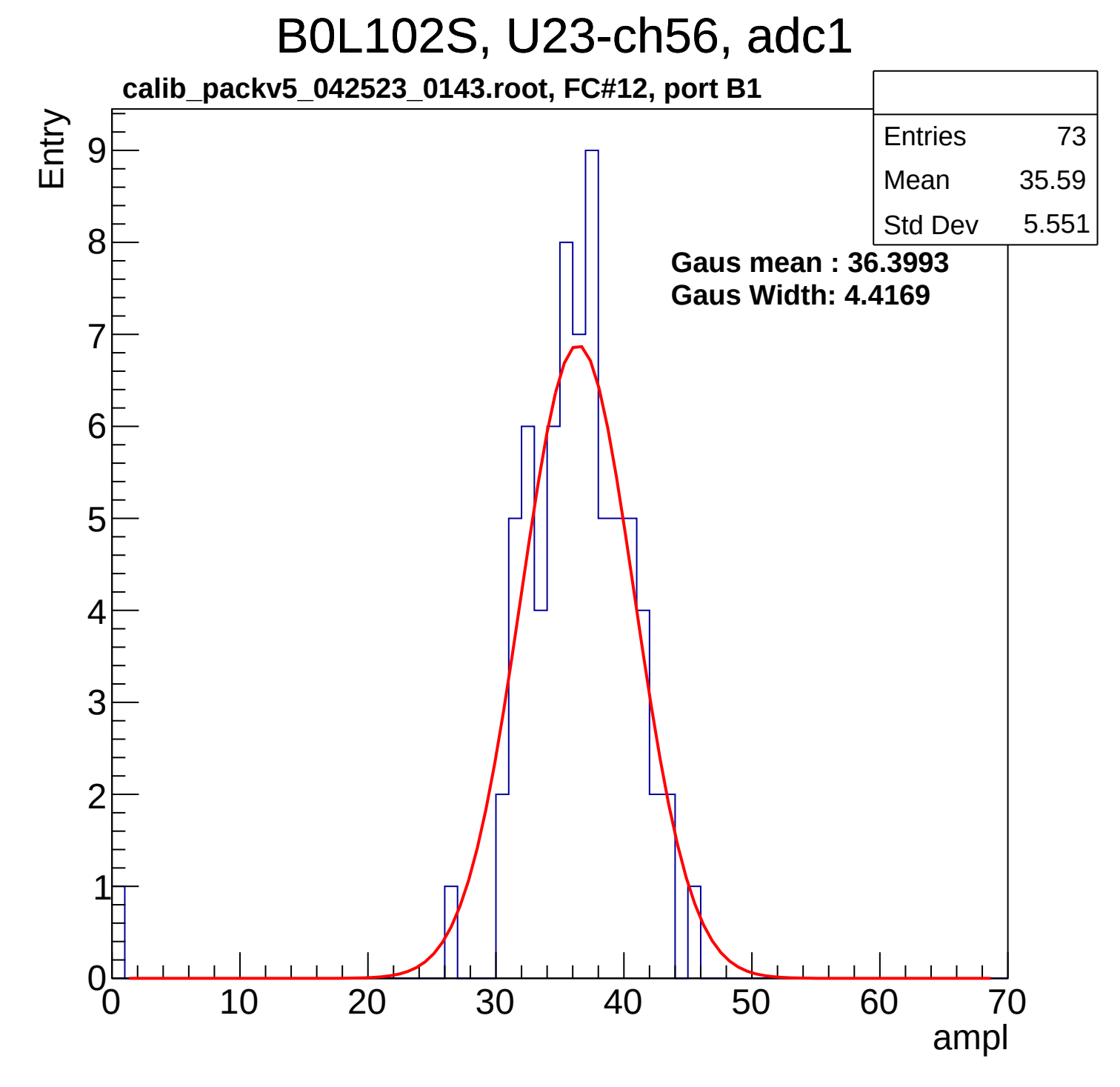

B0L102S, U23-ch55, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 55.9
Std Dev 3.575
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch55, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.84
Std Dev 9.964
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch55, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch55, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch56, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 28.91
Std Dev 3.24
Gaus mean : 29.4889
Gaus Width: 3.3626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch56, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 35.59
Std Dev 5.551
Gaus mean : 36.3993
Gaus Width: 4.4169
Entry
ampl
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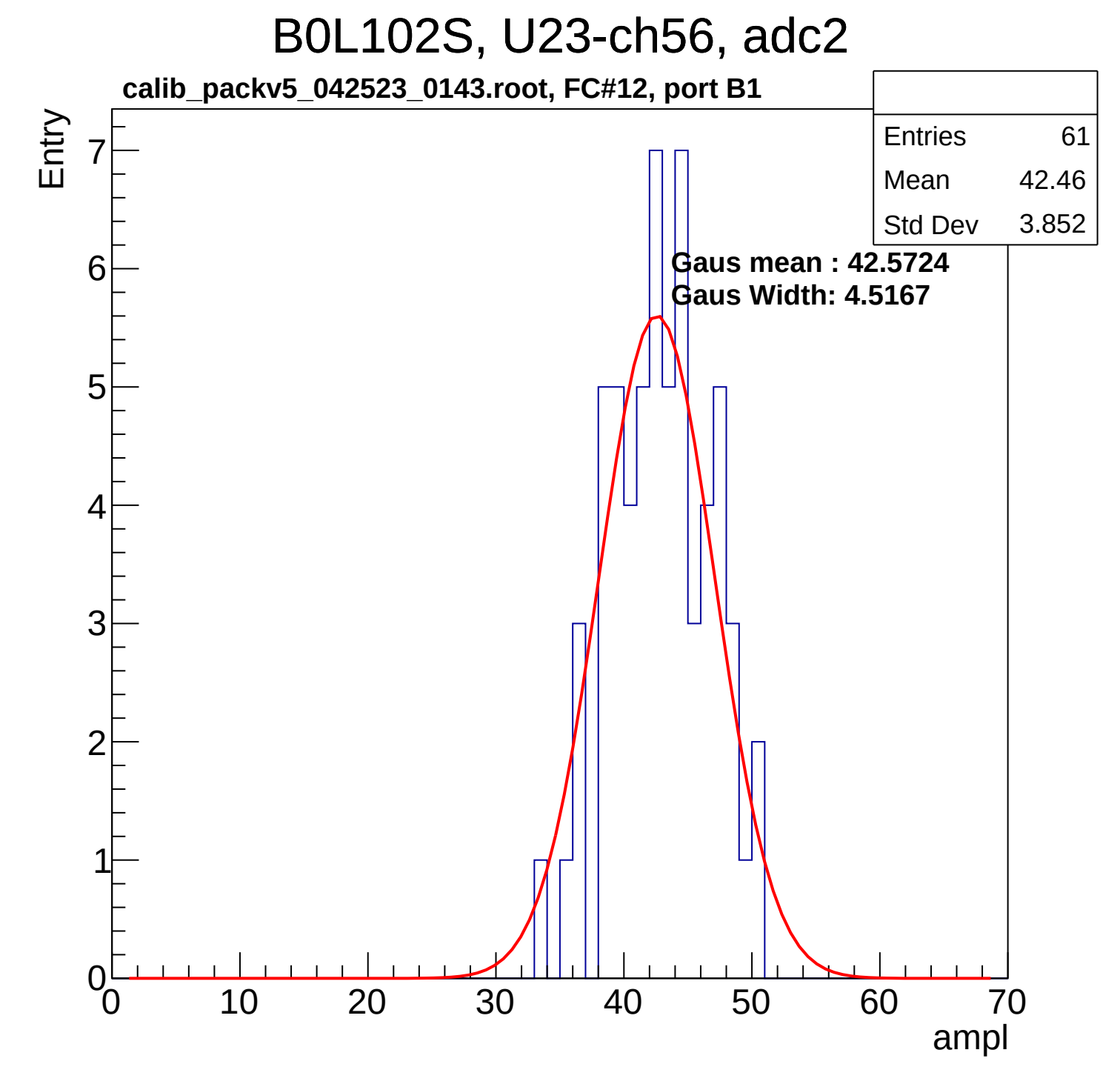
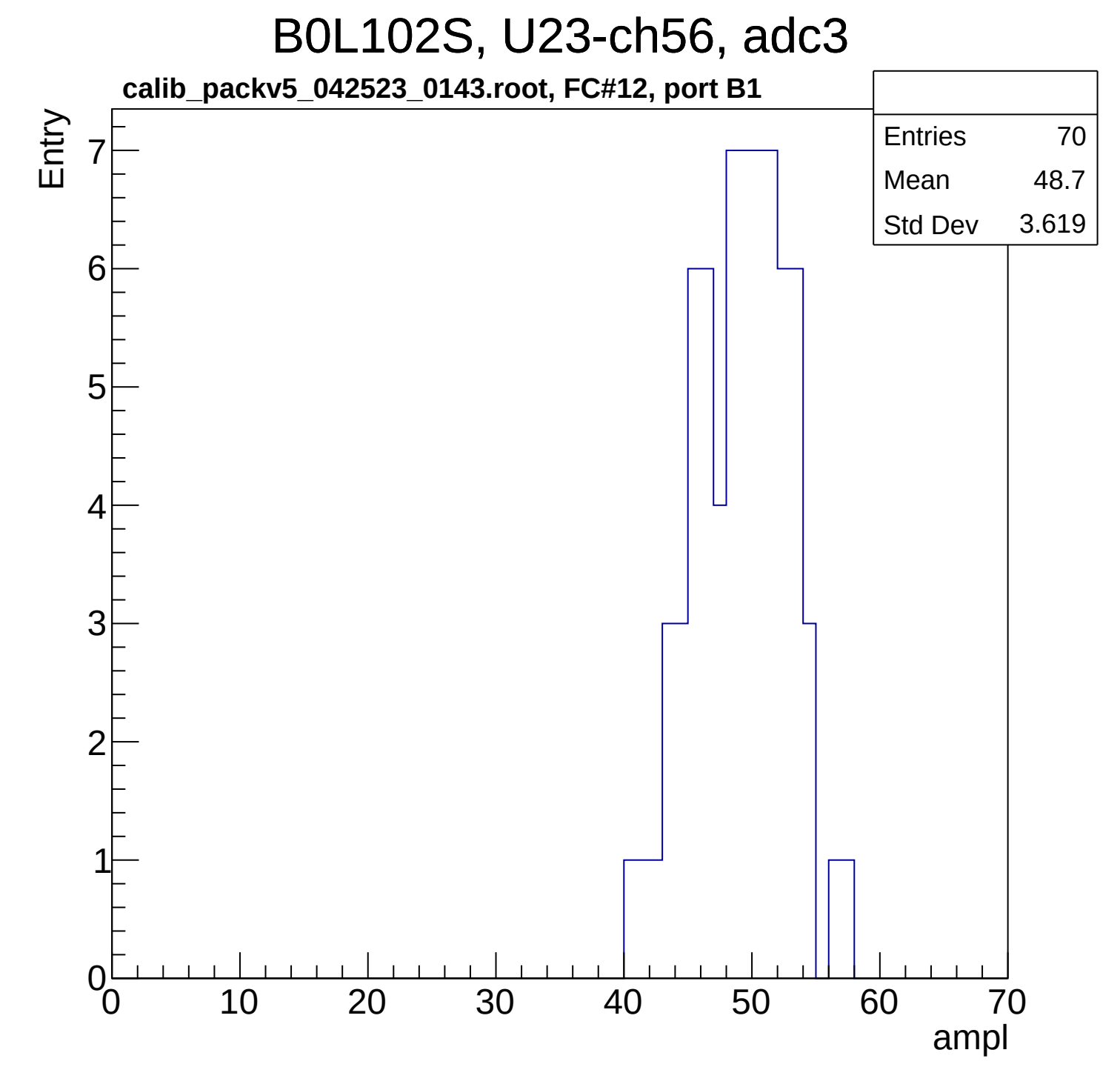
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch56, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 42.46
Std Dev 3.852
Gaus mean : 42.5724
Gaus Width: 4.5167
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch56, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 48.7
Std Dev 3.619
Entry
ampl
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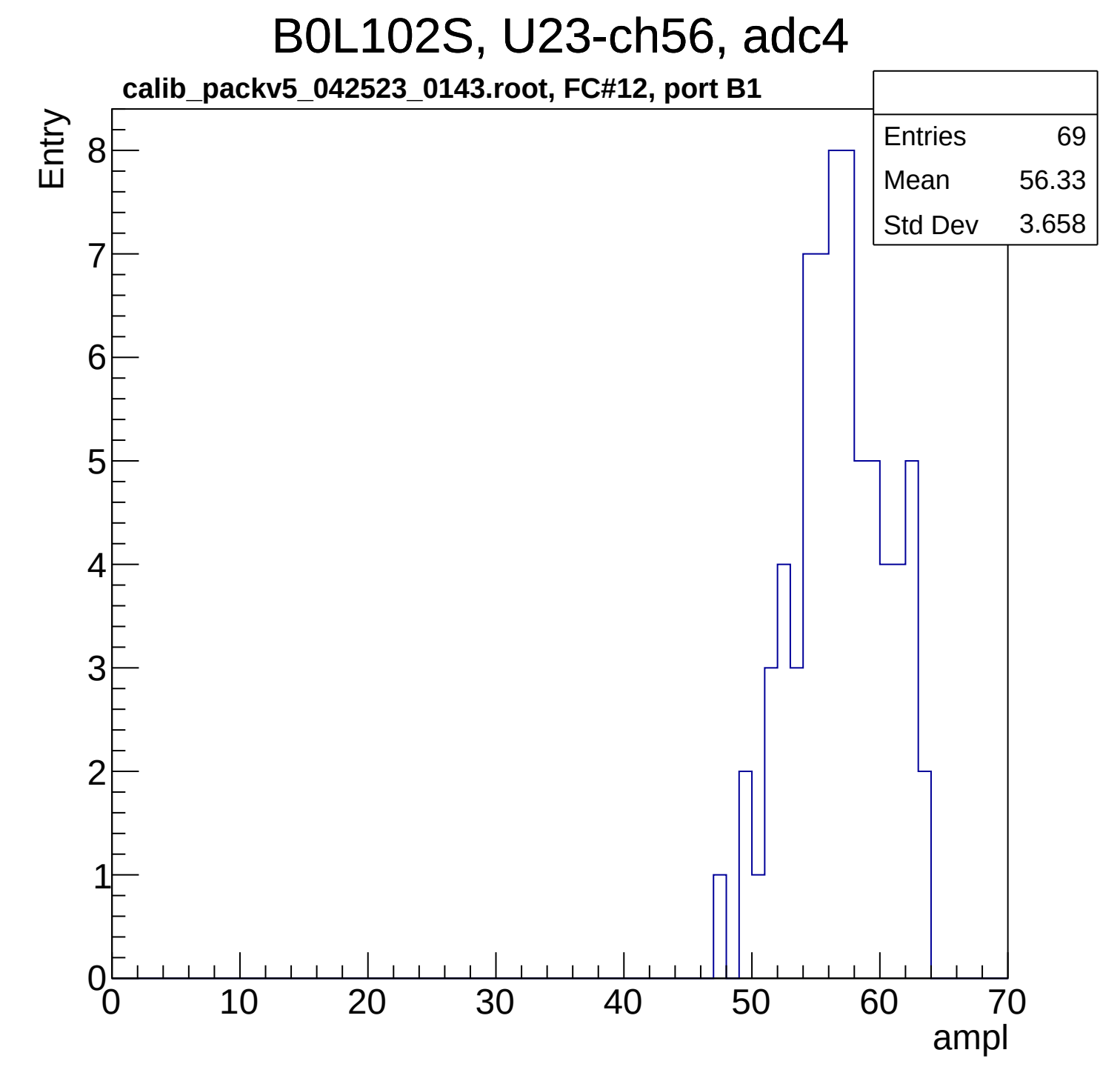
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch56, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 56.33
Std Dev 3.658
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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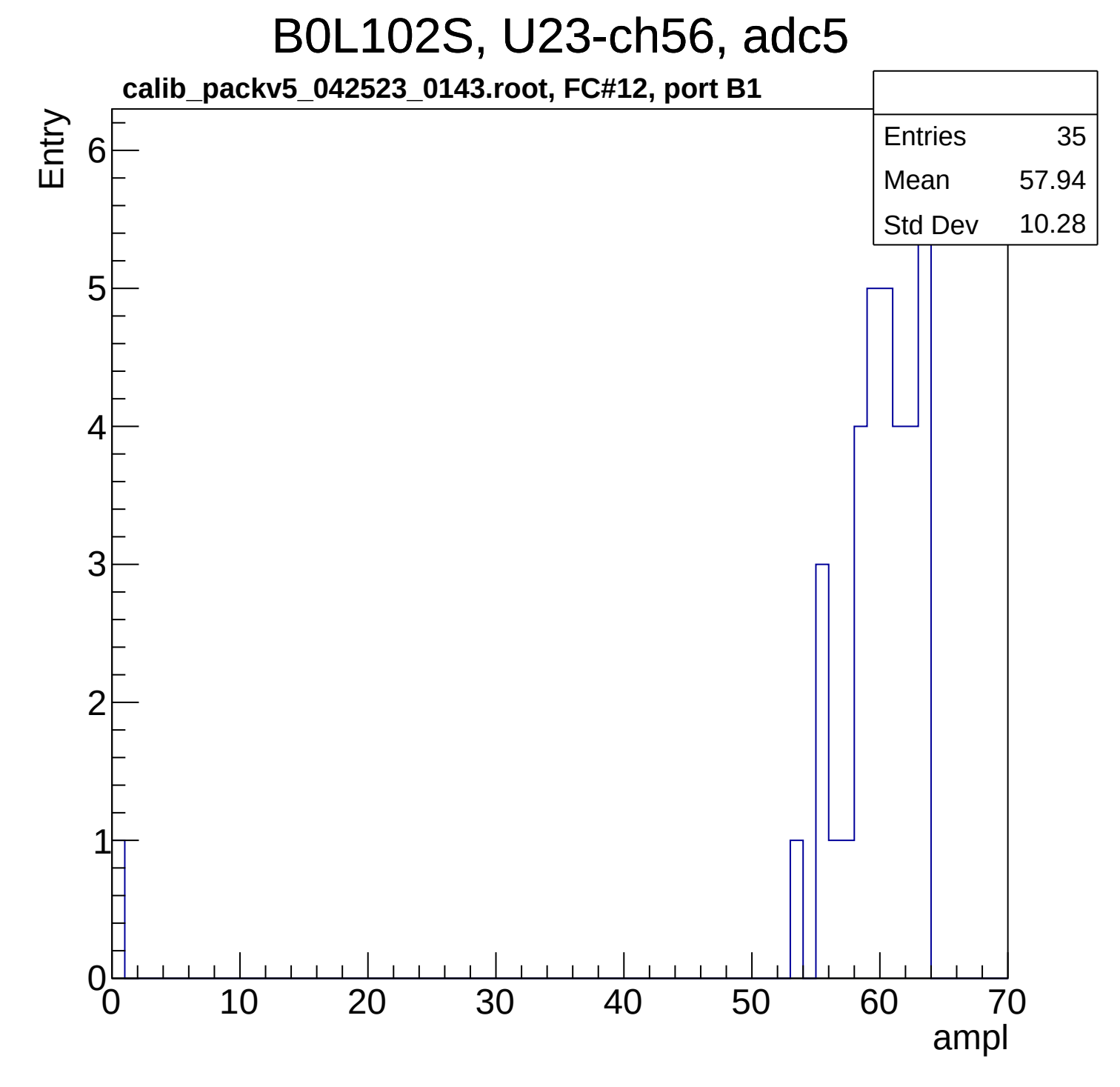
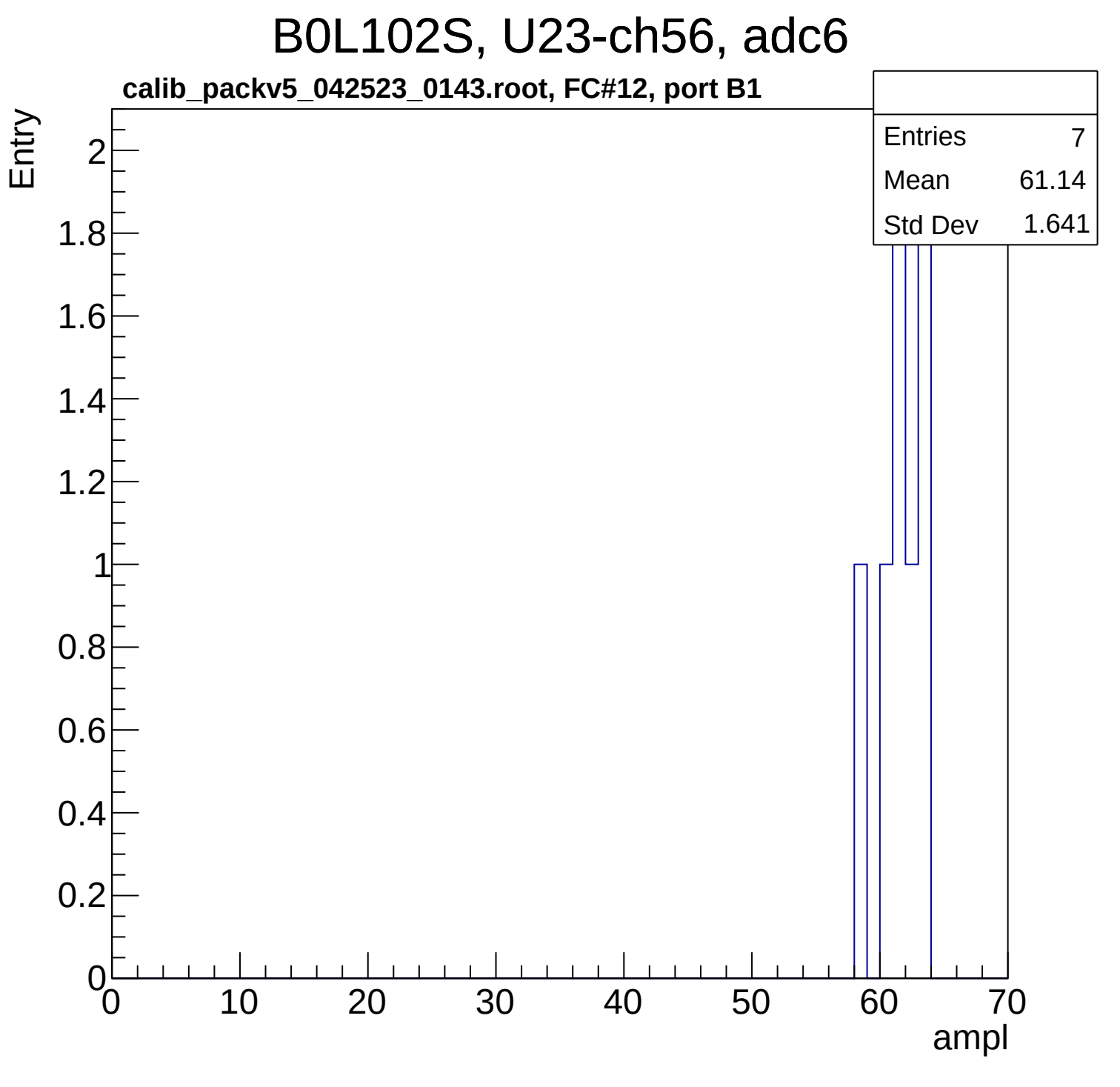
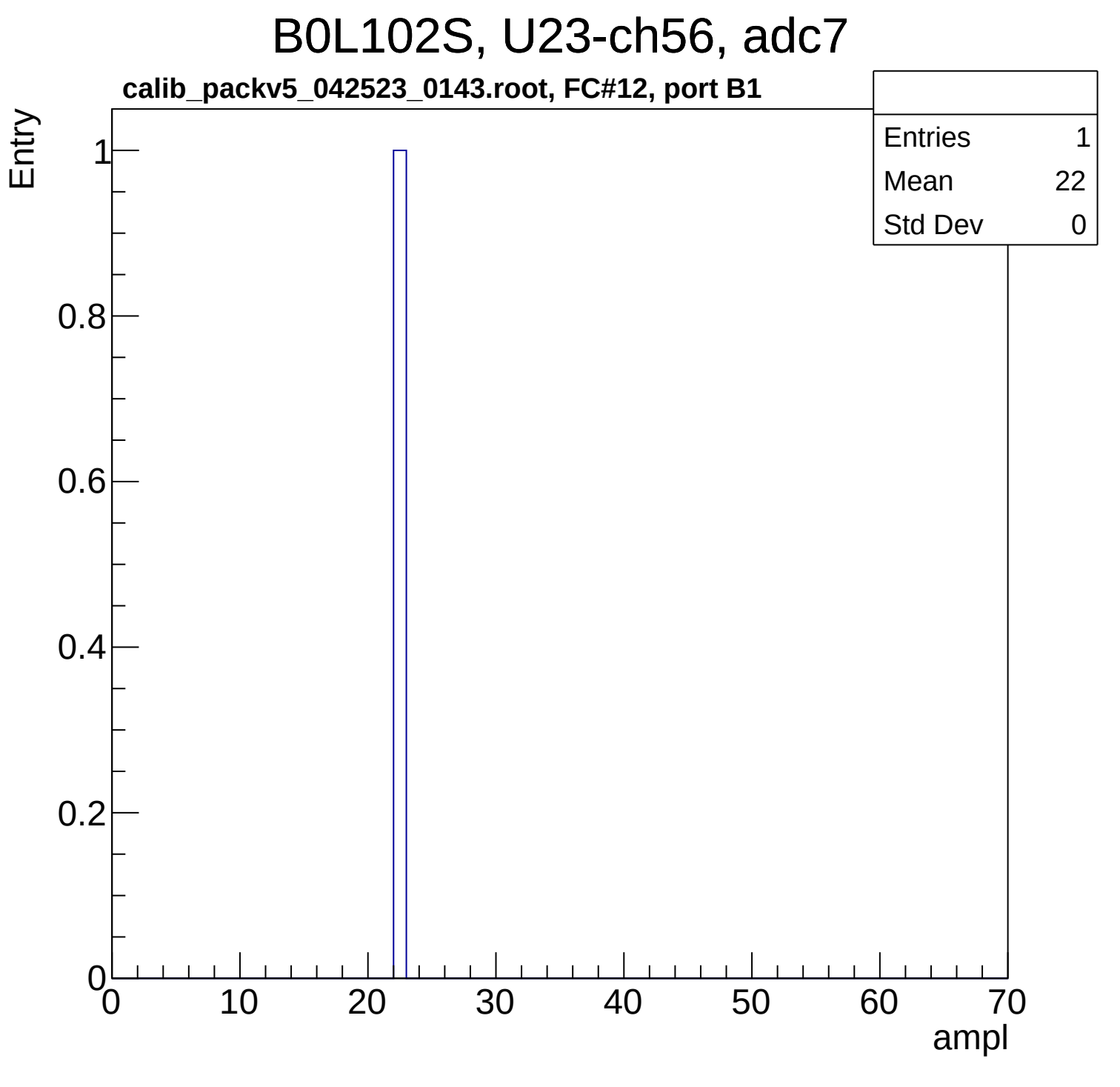
60
70


B0L102S, U23-ch56, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 57.94
Std Dev 10.28
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch56, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.14
Std Dev 1.641
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch56, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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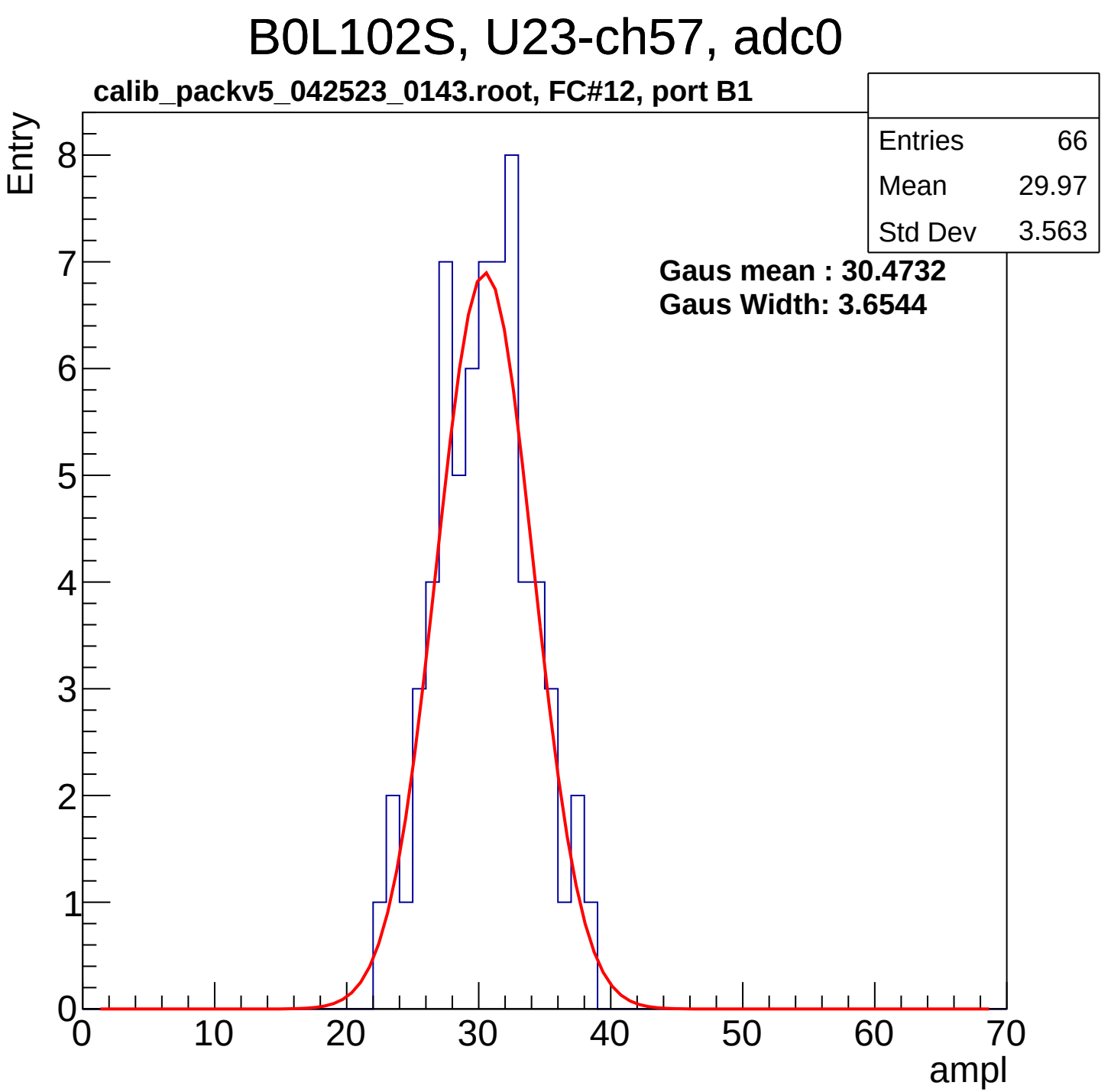
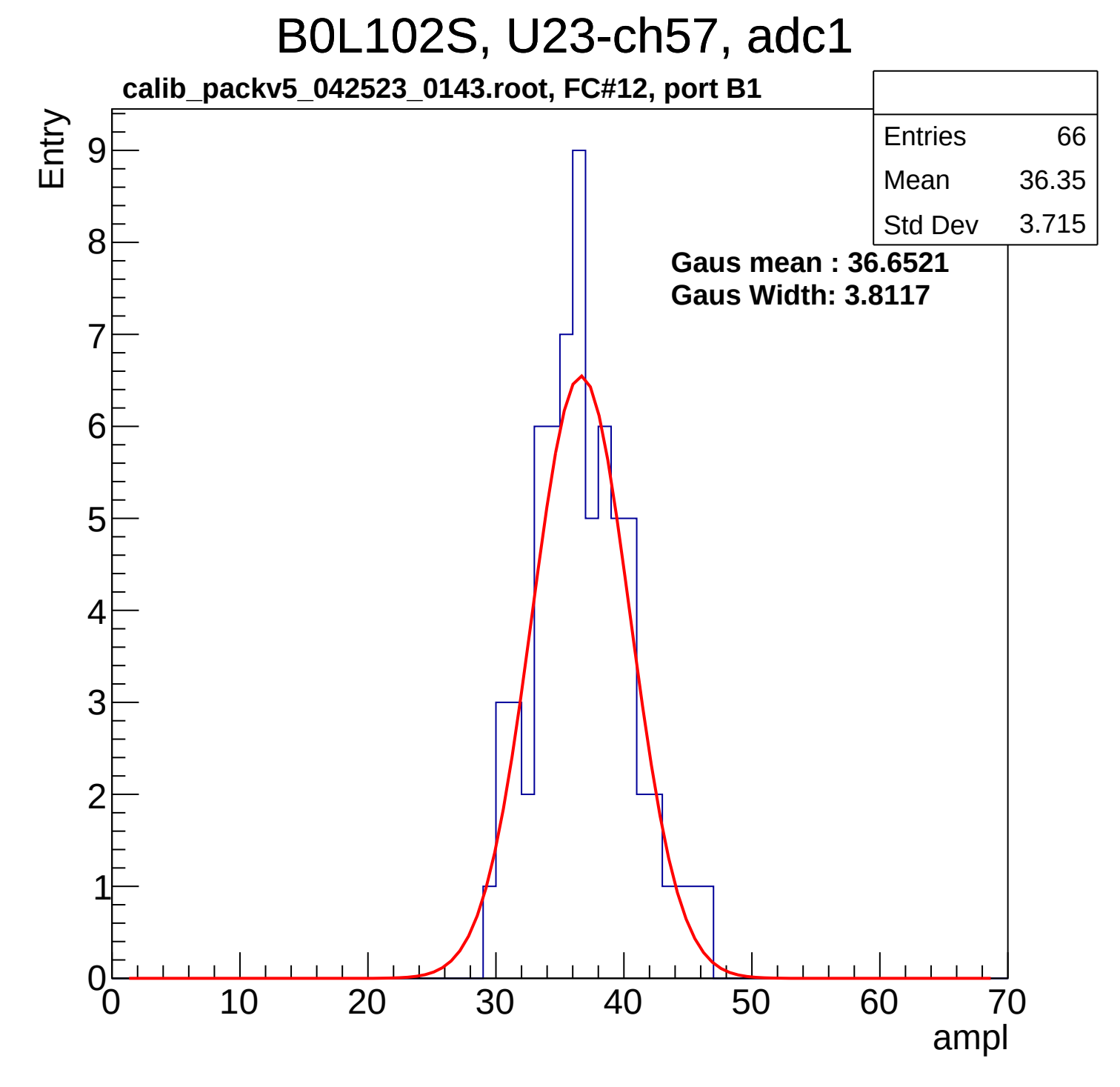
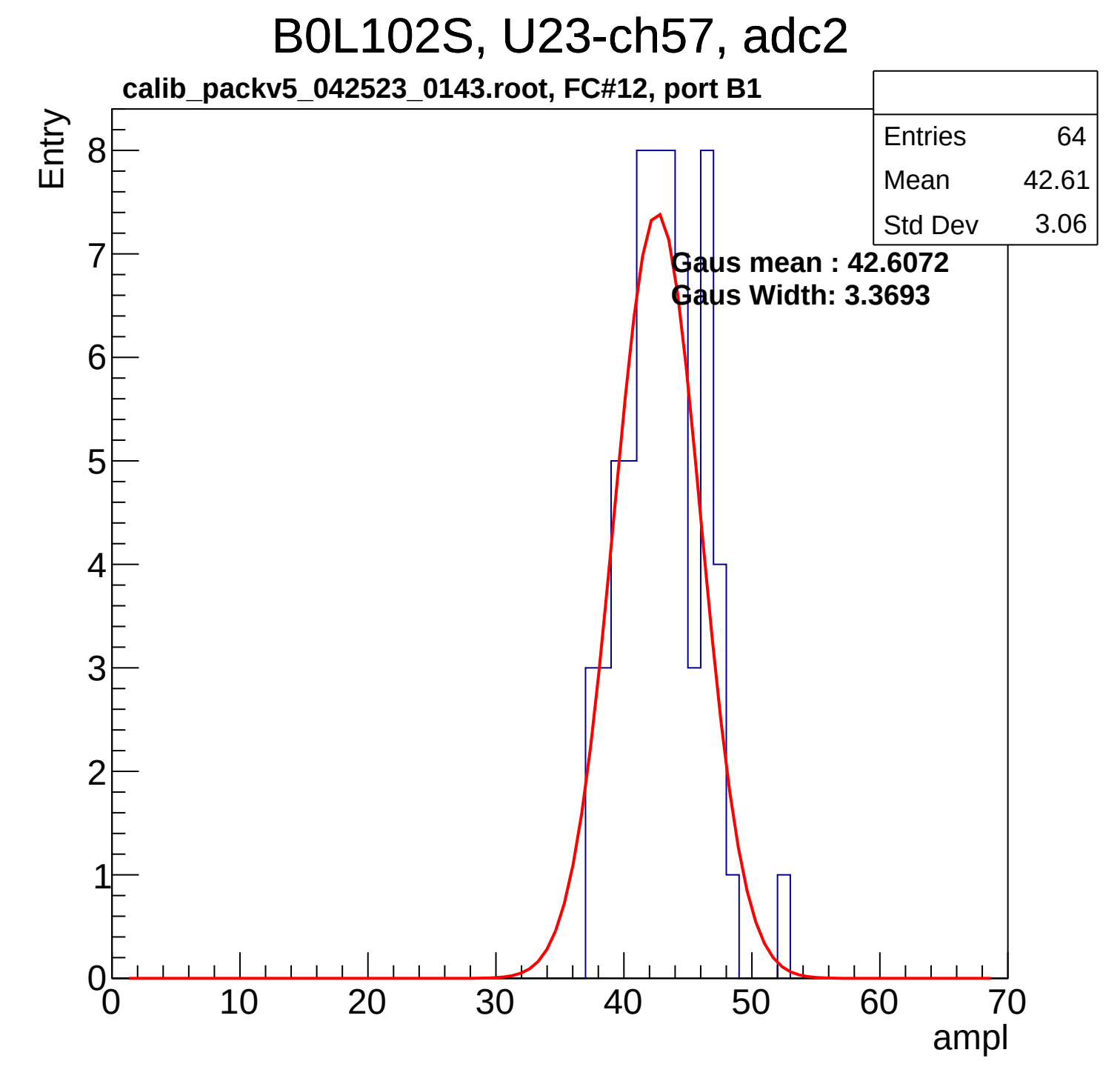
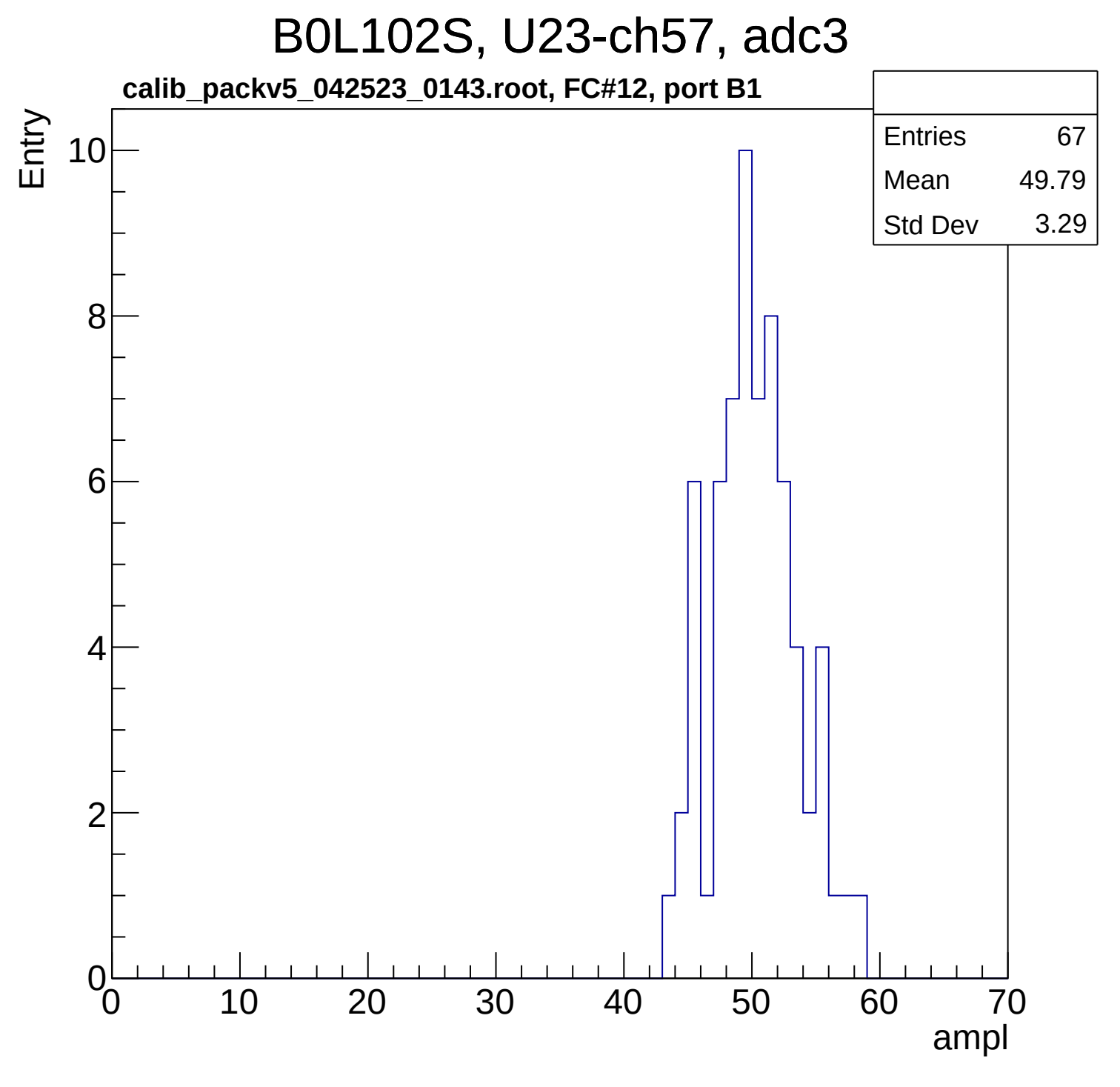
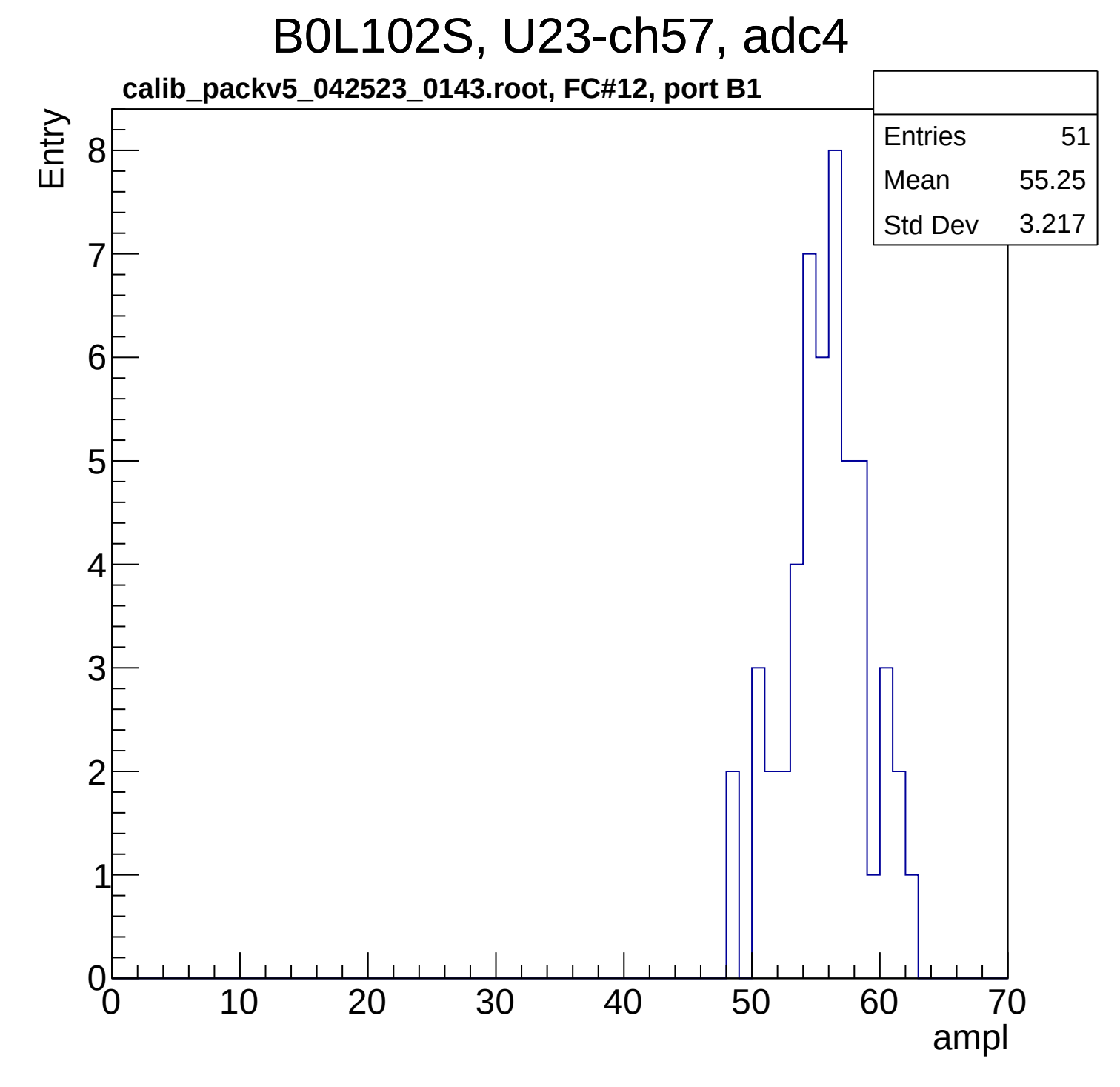
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch57, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 29.97
Std Dev 3.563
Gaus mean : 30.4732
Gaus Width: 3.6544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch57, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 36.35
Std Dev 3.715
Gaus mean : 36.6521
Gaus Width: 3.8117
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch57, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 42.61
Std Dev 3.06
Gaus mean : 42.6072
Gaus Width: 3.3693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch57, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 49.79
Std Dev 3.29
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch57, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 55.25
Std Dev 3.217
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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40
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60
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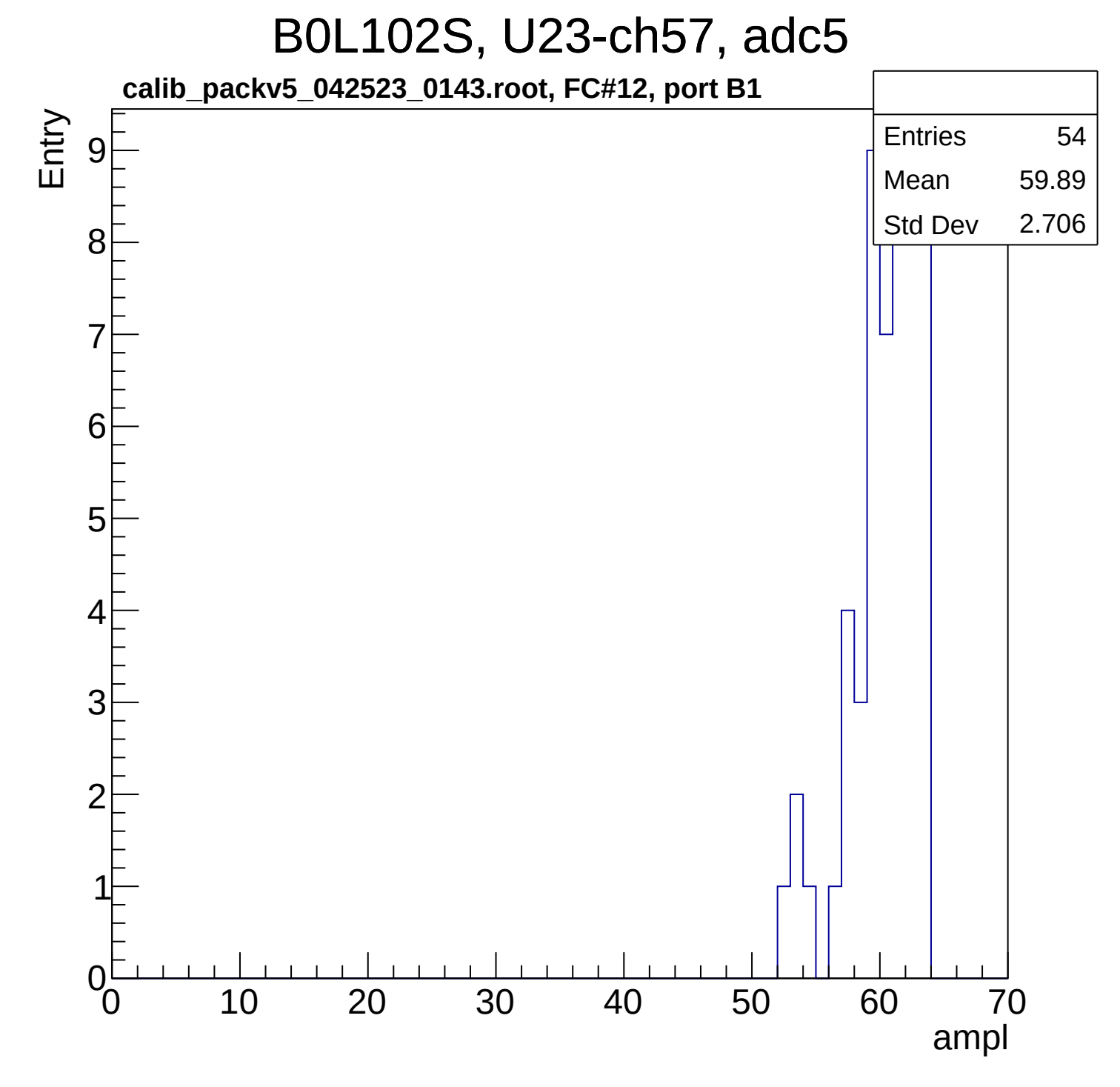
B0L102S, U23-ch57, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 59.89
Std Dev 2.706
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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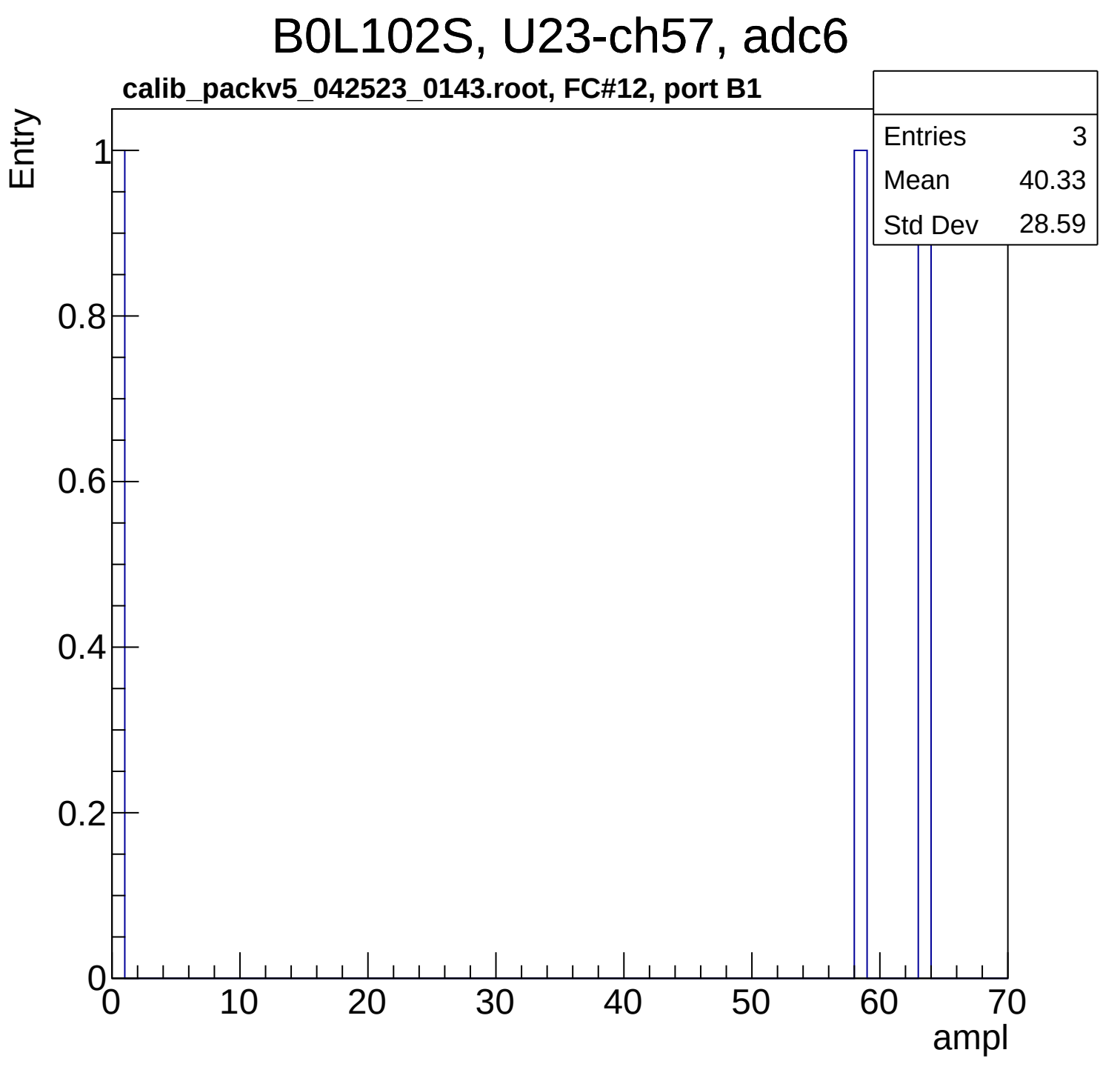
B0L102S, U23-ch57, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 40.33
Std Dev 28.59
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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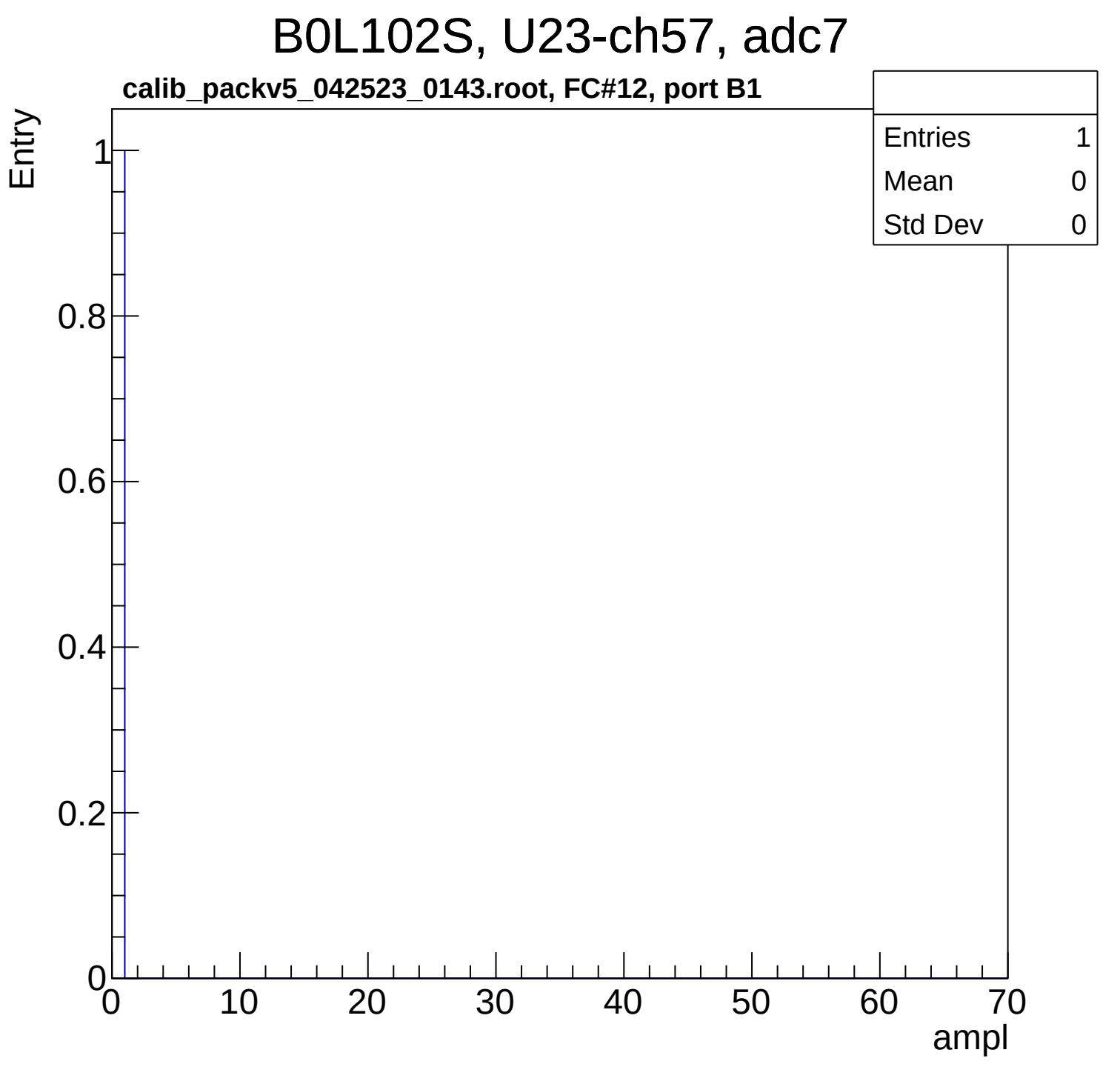
70
ampl


B0L102S, U23-ch57, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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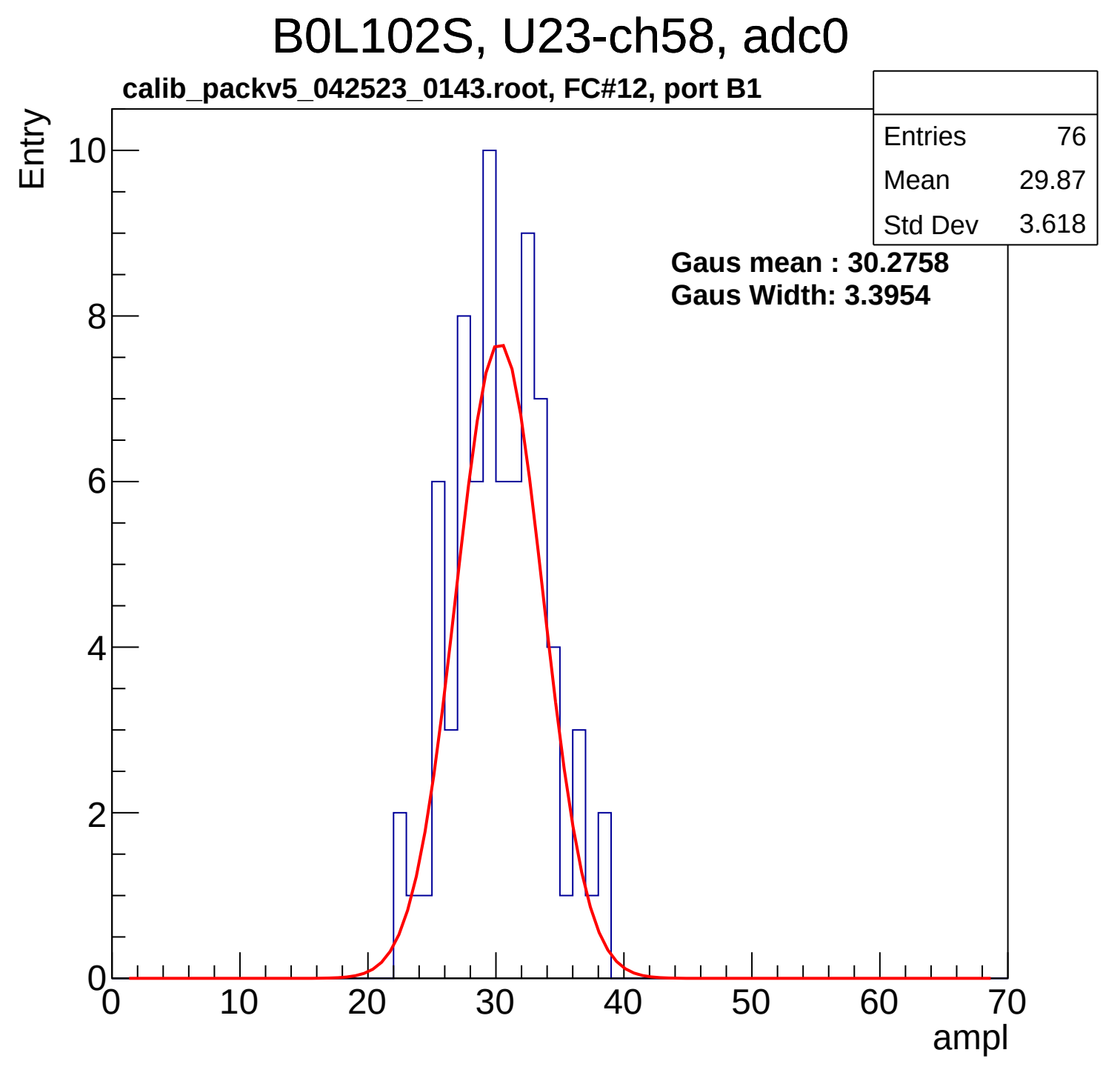
ampl


B0L102S, U23-ch58, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.87
Std Dev 3.618
Gaus mean : 30.2758
Gaus Width: 3.3954
Entry
ampl
0
2
4
6
8
10
0
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60
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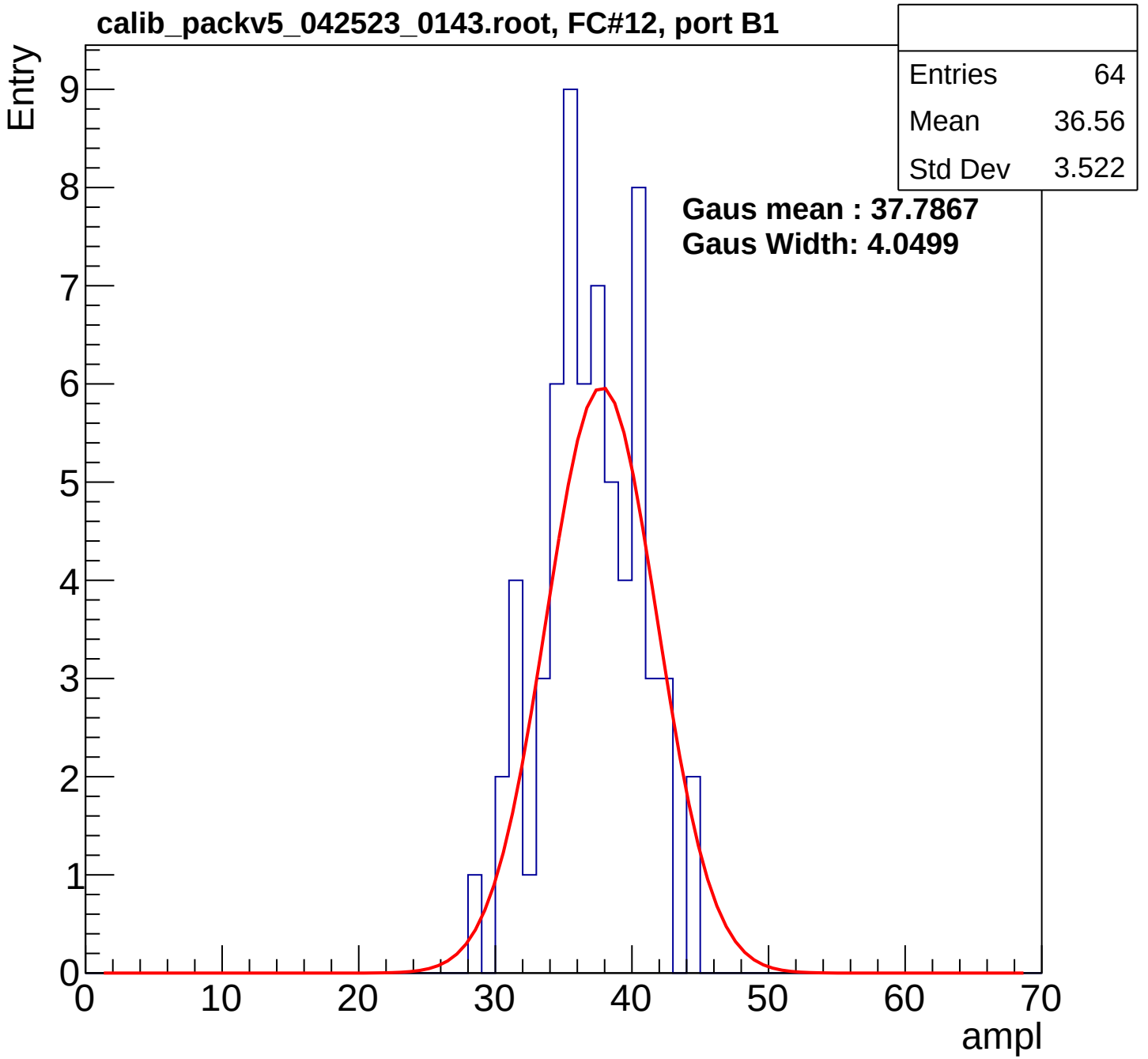
B0L102S, U23-ch58, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.56
Std Dev 3.522
Gaus mean : 37.7867
Gaus Width: 4.0499
Entry
ampl
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5
6
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0
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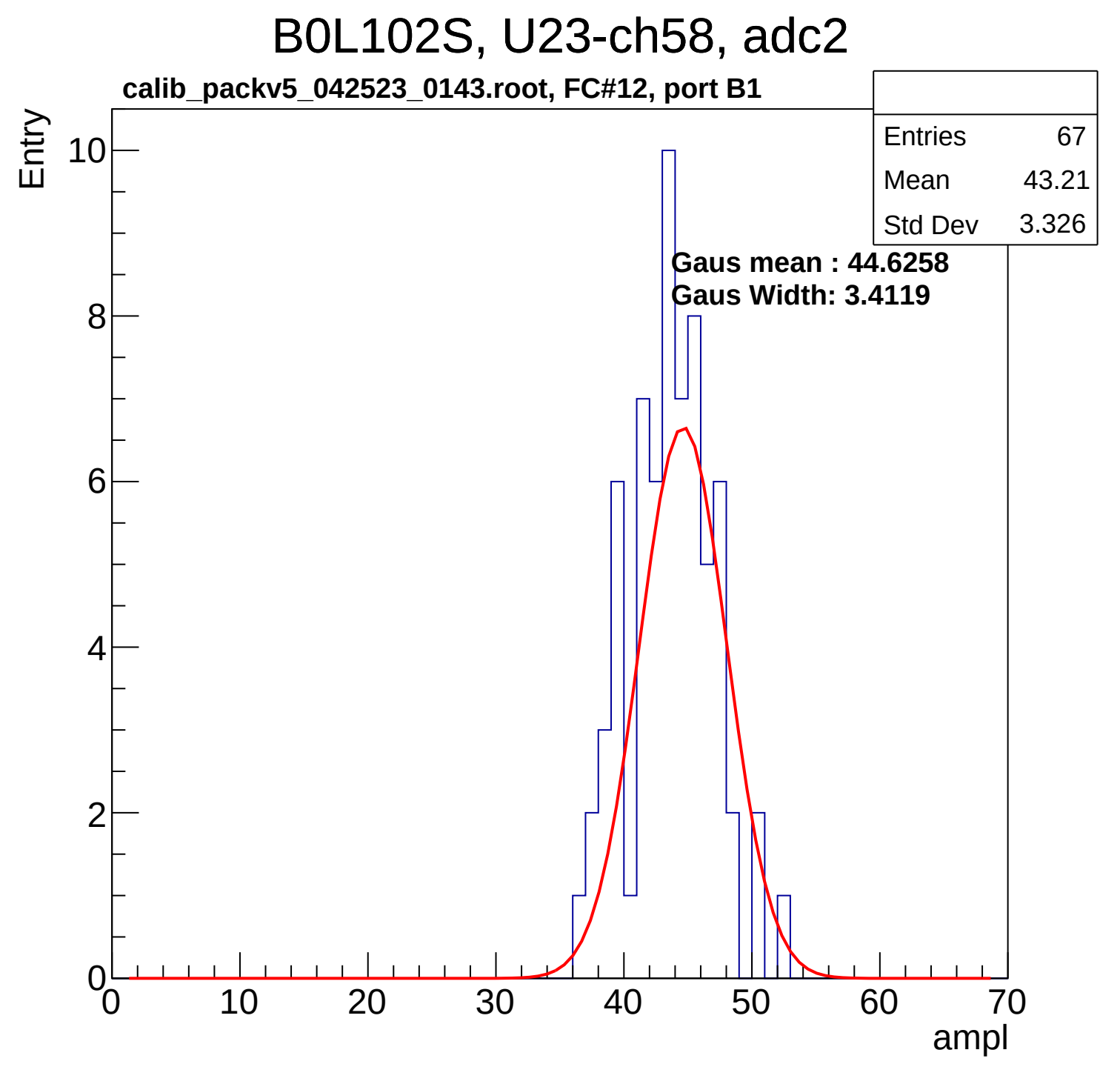
B0L102S, U23-ch58, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 43.21
Std Dev 3.326
Gaus mean : 44.6258
Gaus Width: 3.4119
Entry
ampl
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2
4
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0
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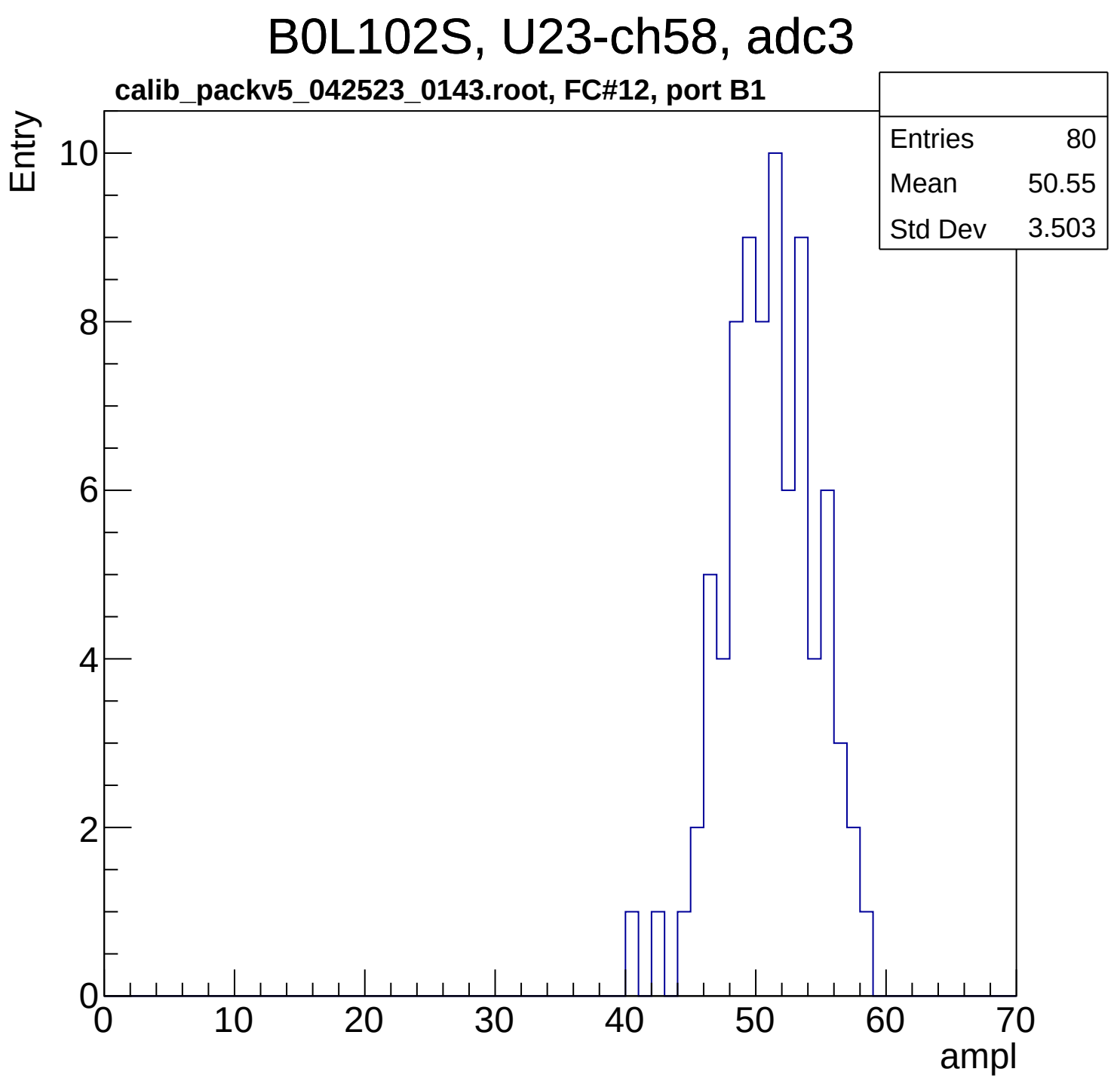
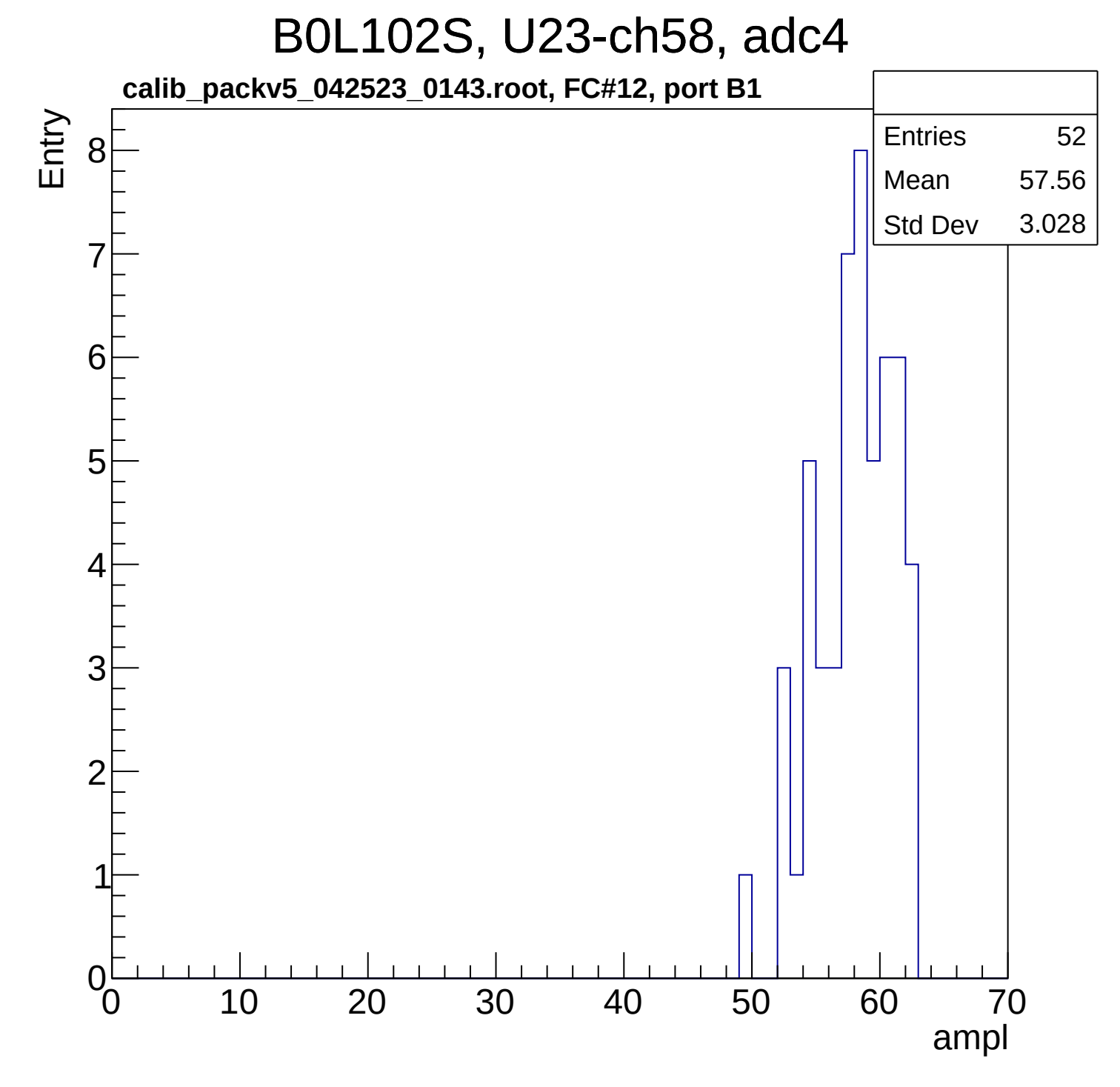

B0L102S, U23-ch58, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 50.55
Std Dev 3.503
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch58, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 57.56
Std Dev 3.028
Entry
ampl
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1
2
3
4
5
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0
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60
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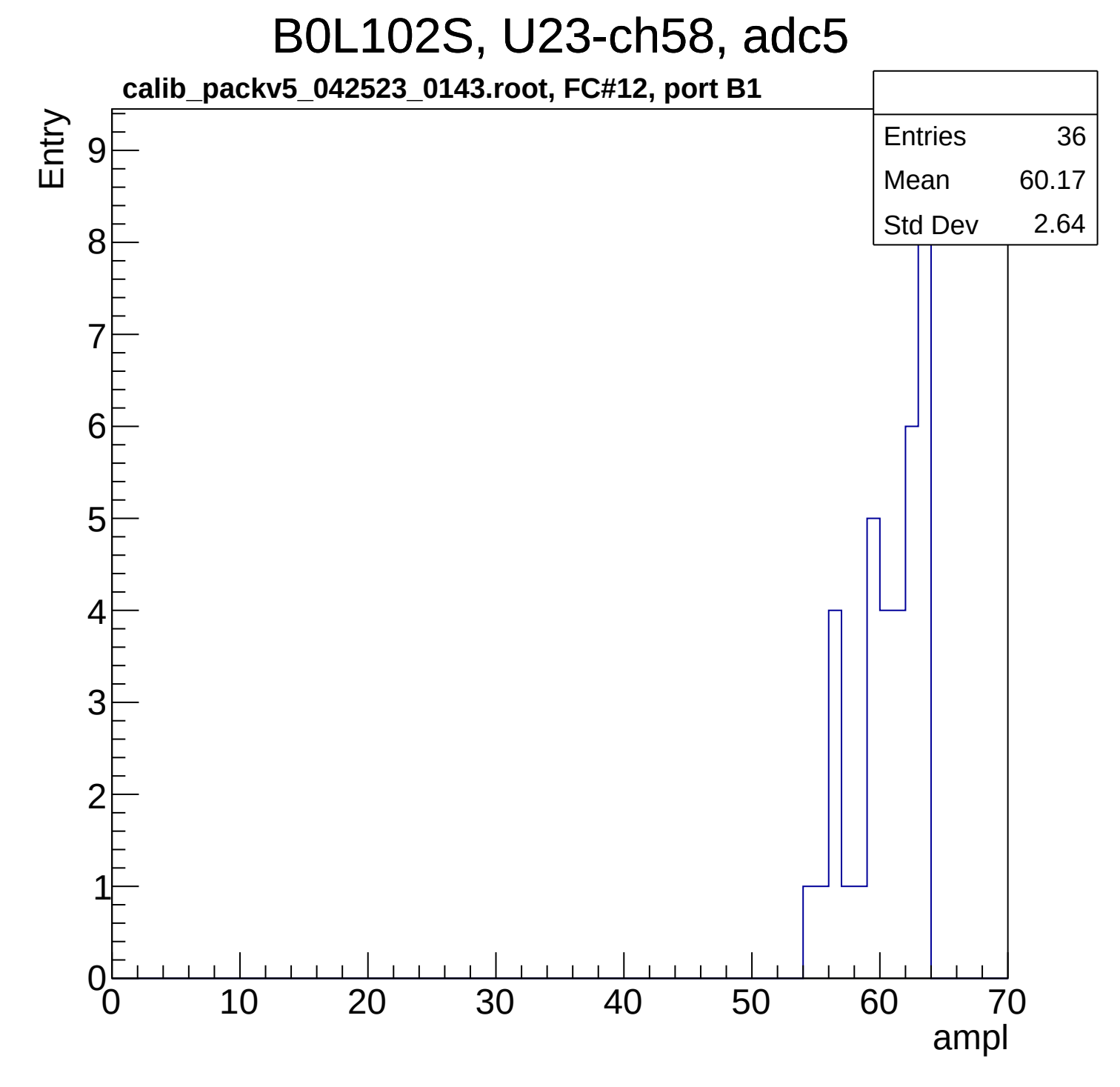
B0L102S, U23-ch58, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 60.17
Std Dev 2.64
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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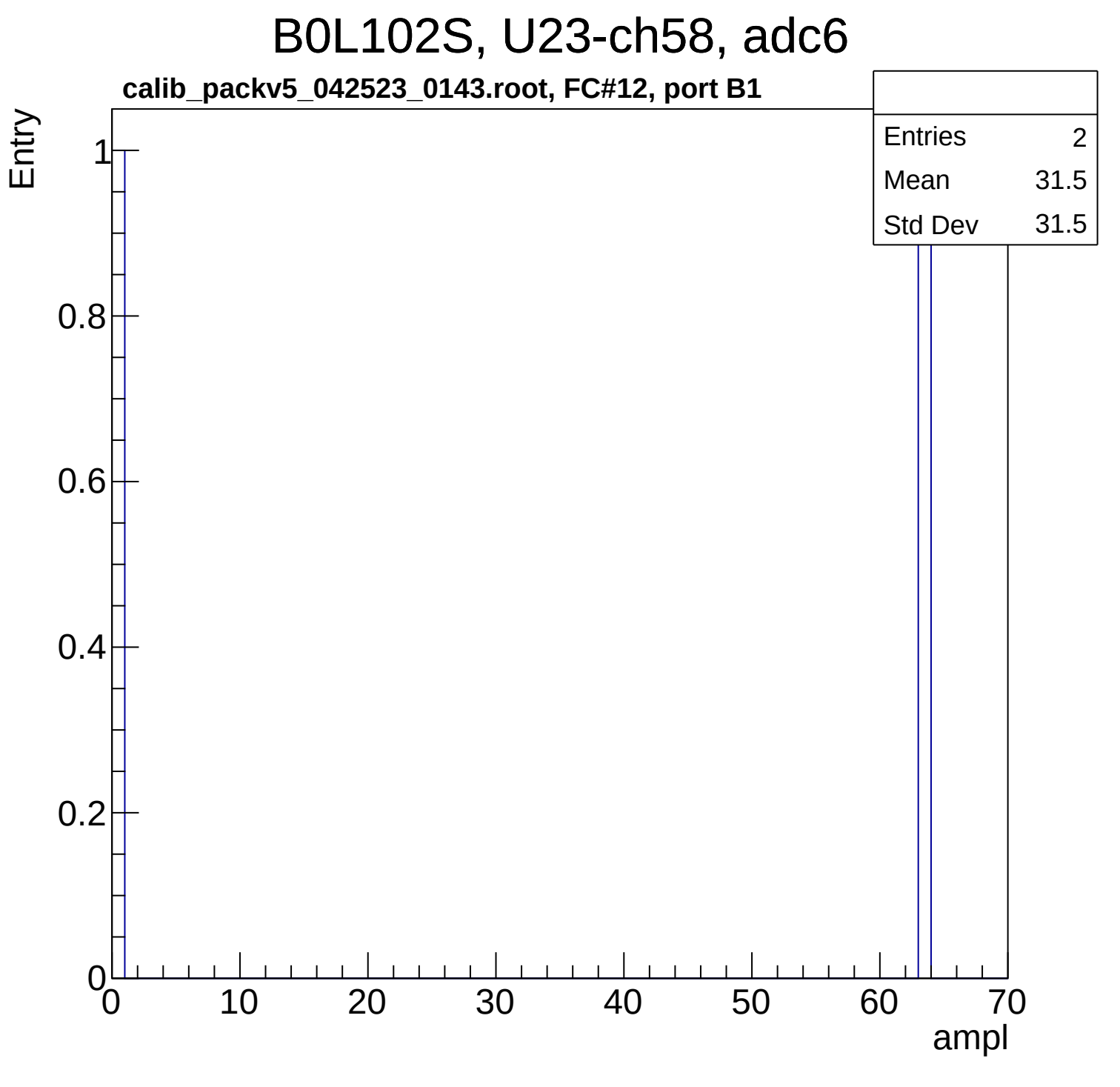
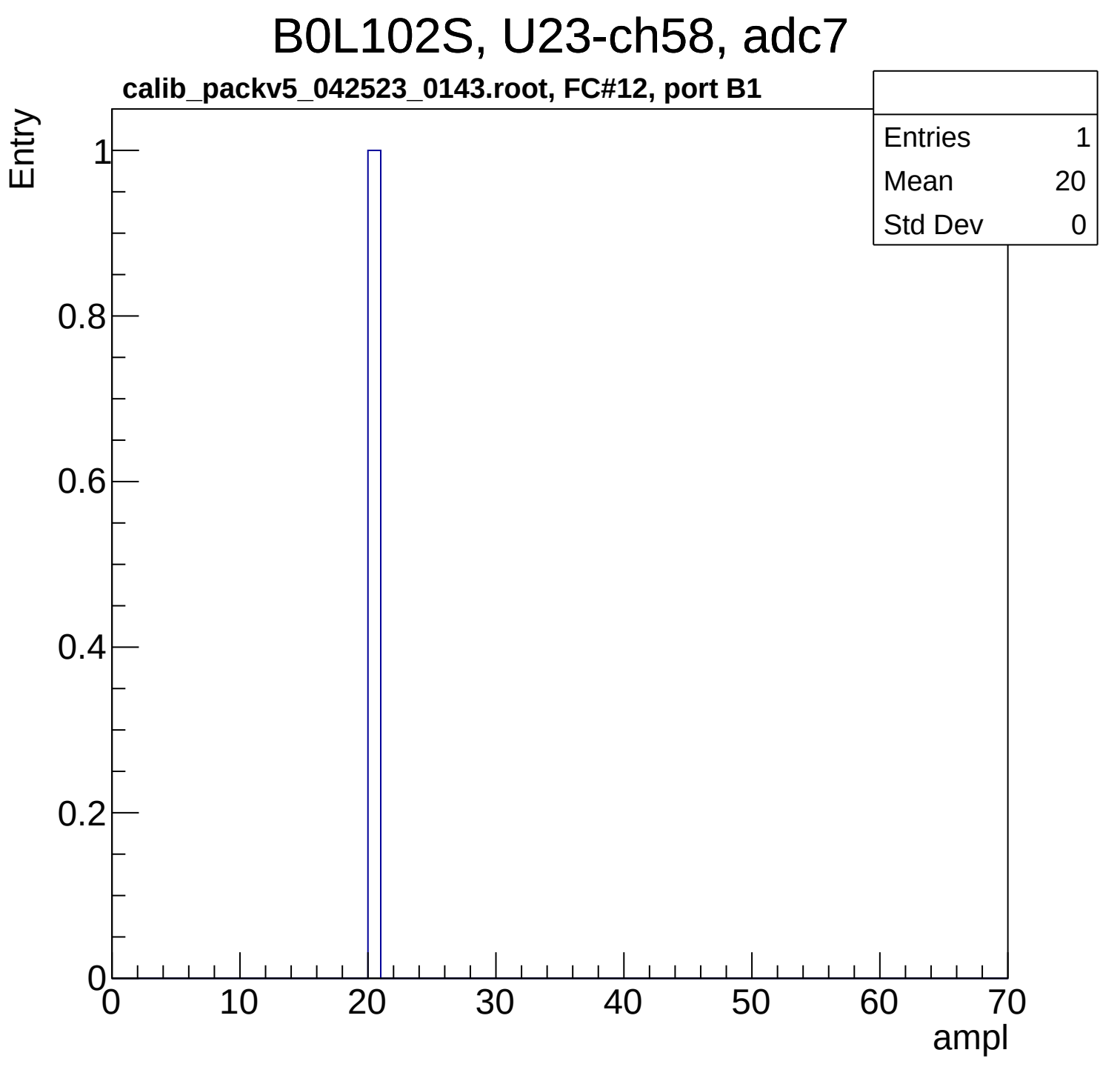
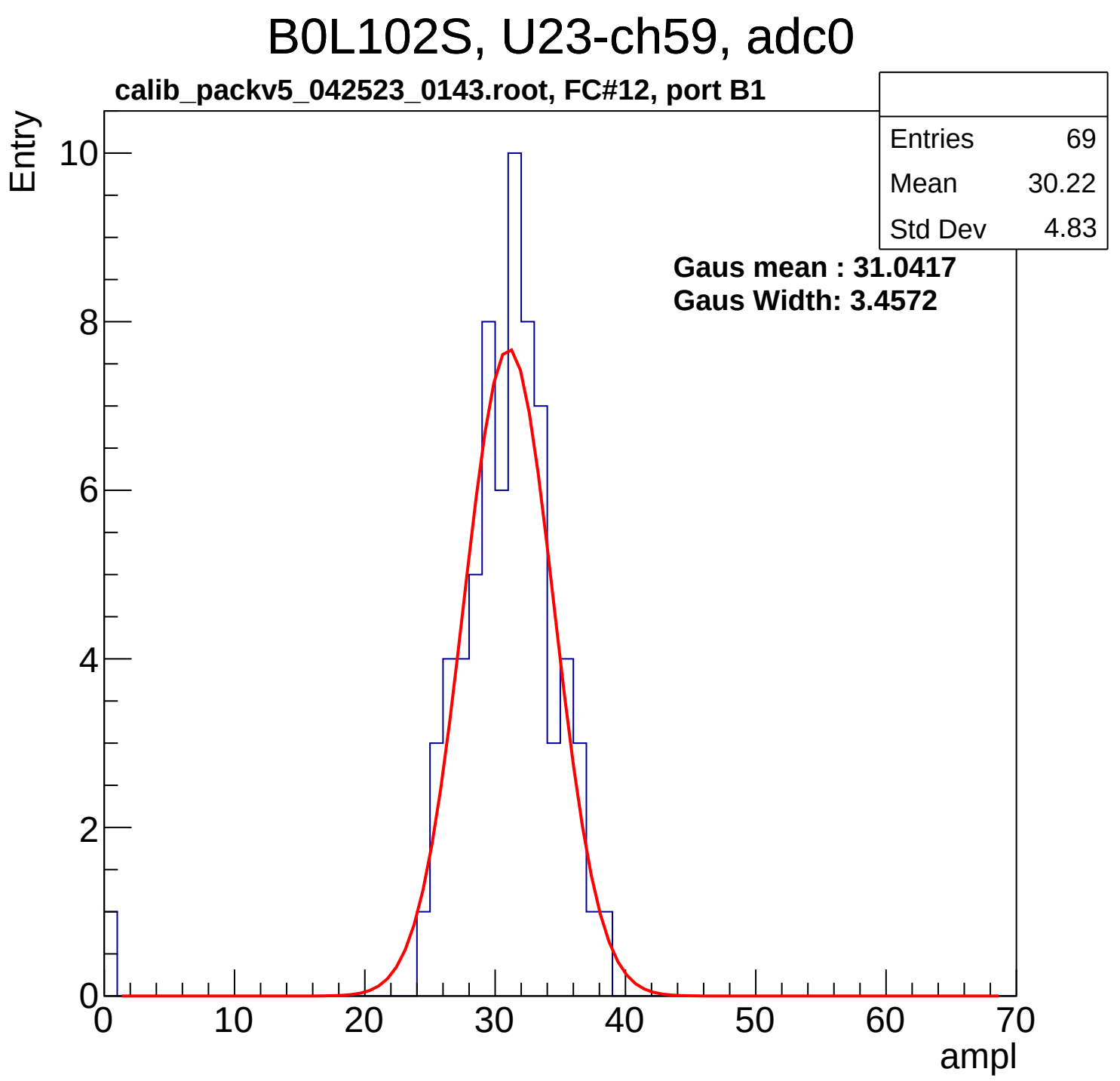
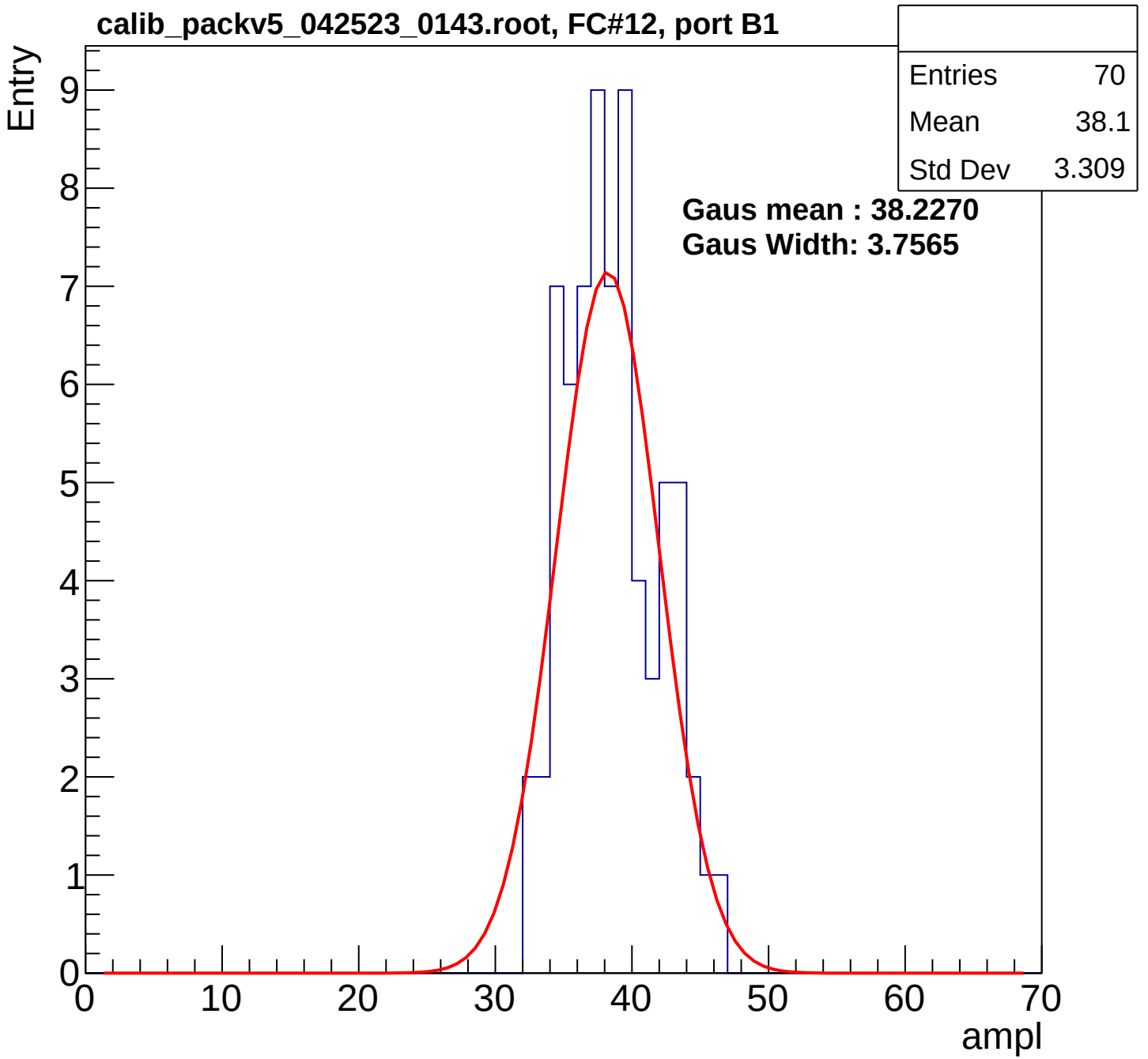

B0L102S, U23-ch58, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch58, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch59, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 30.22
Std Dev 4.83
Gaus mean : 31.0417
Gaus Width: 3.4572
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch59, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 38.1
Std Dev 3.309
Gaus mean : 38.2270
Gaus Width: 3.7565
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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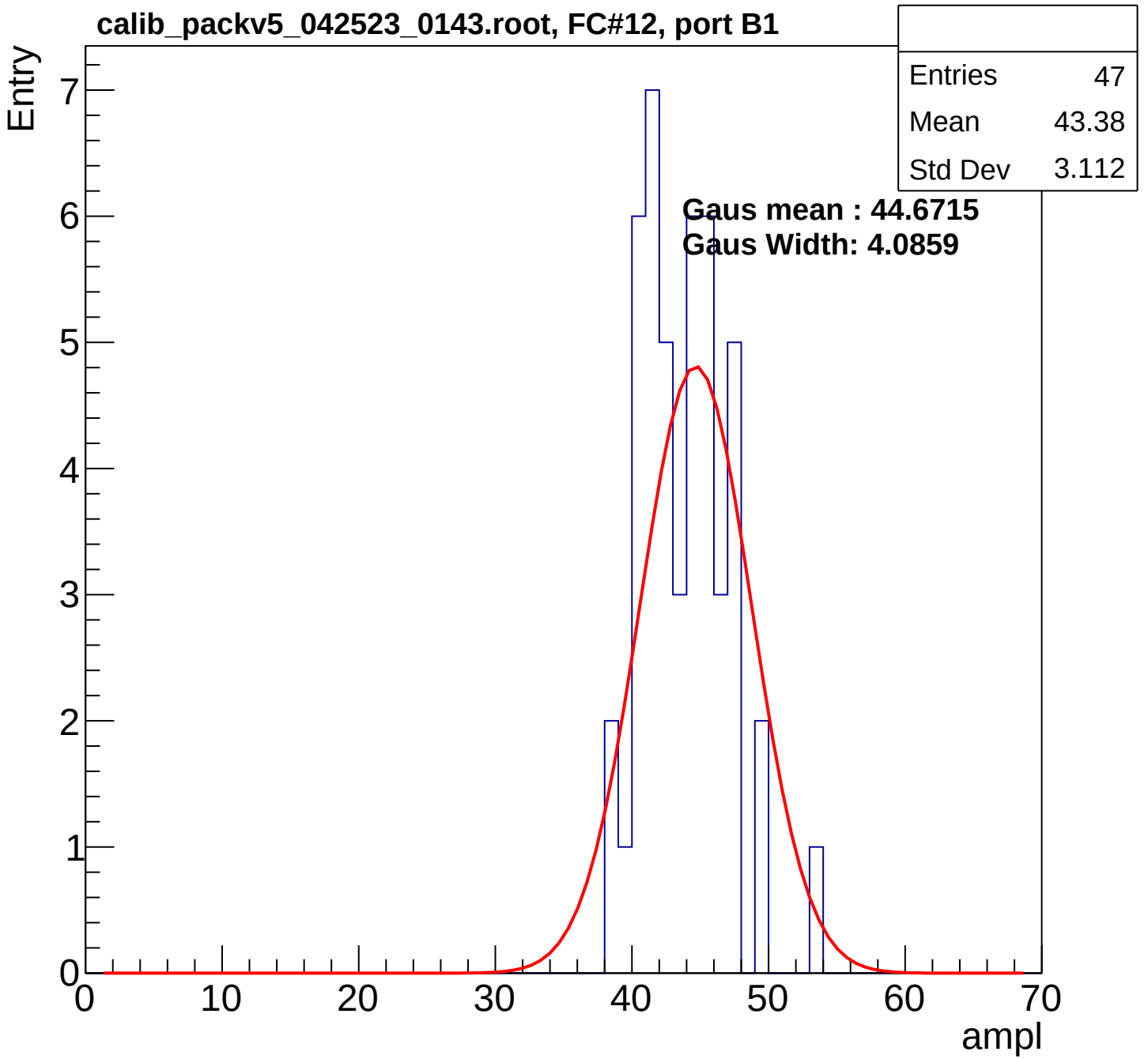
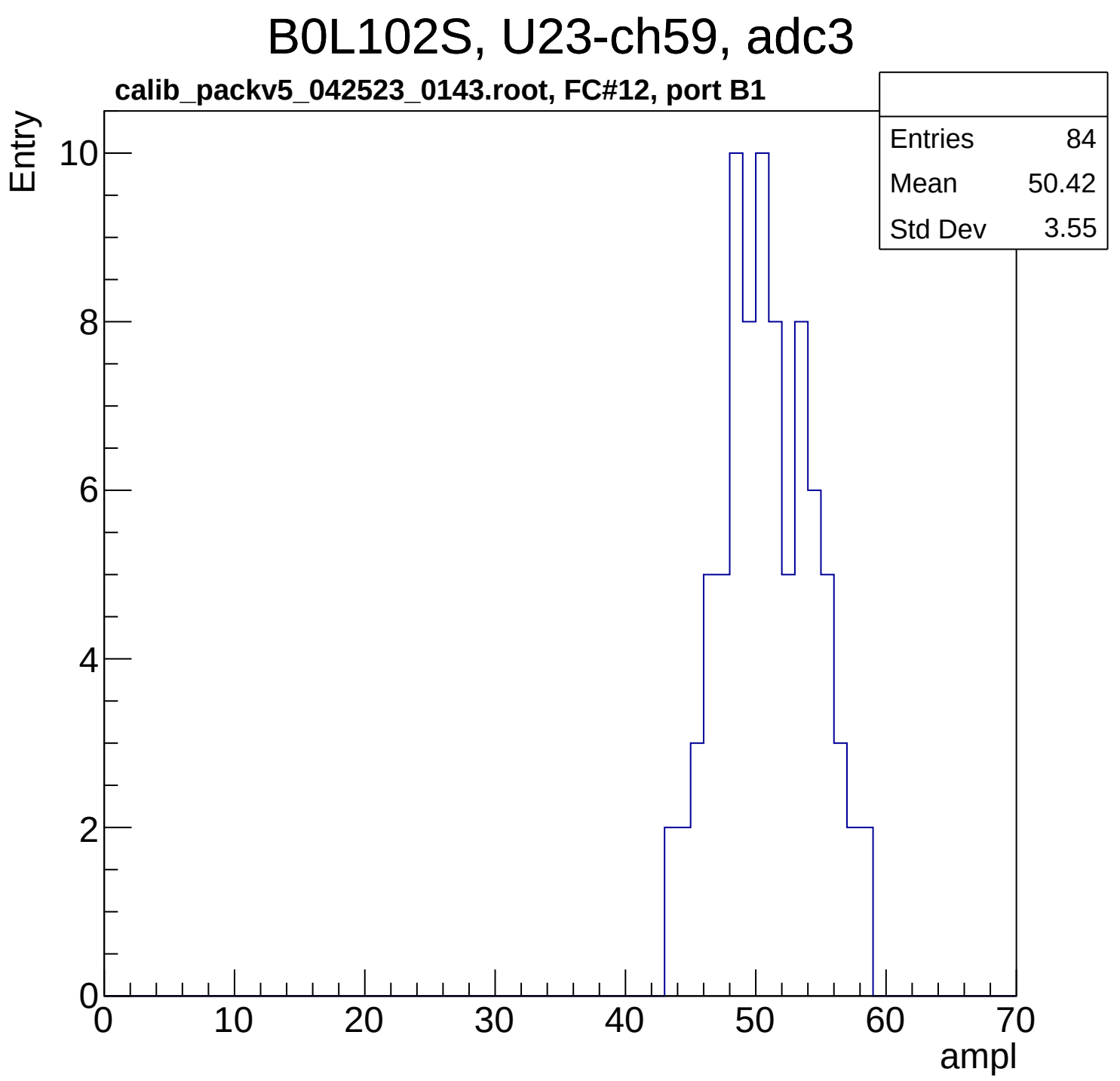
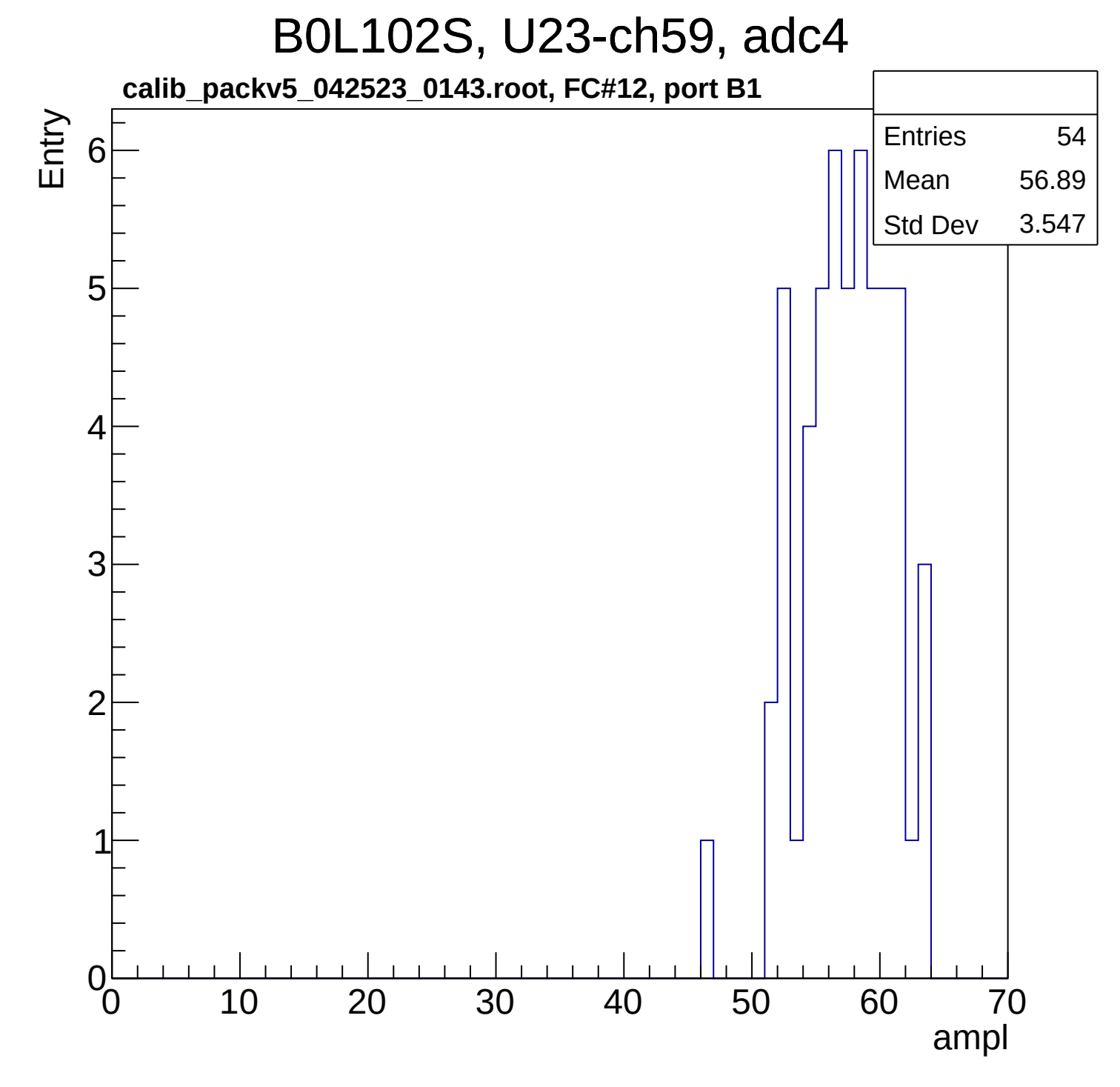
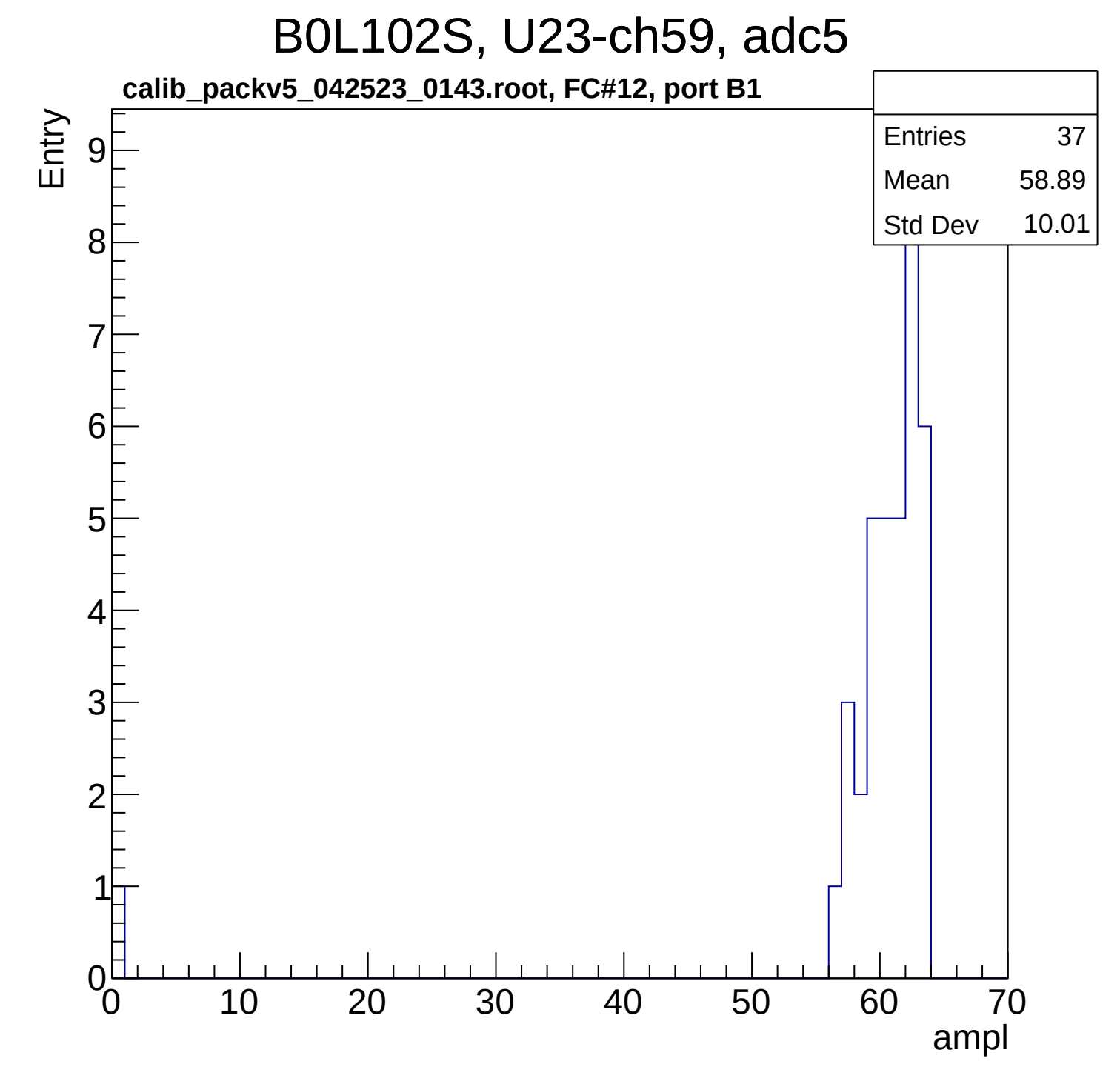

B0L102S, U23-ch59, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 43.38
Std Dev 3.112
Gaus mean : 44.6715
Gaus Width: 4.0859
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch59, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 50.42
Std Dev 3.55
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch59, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 56.89
Std Dev 3.547
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch59, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 58.89
Std Dev 10.01
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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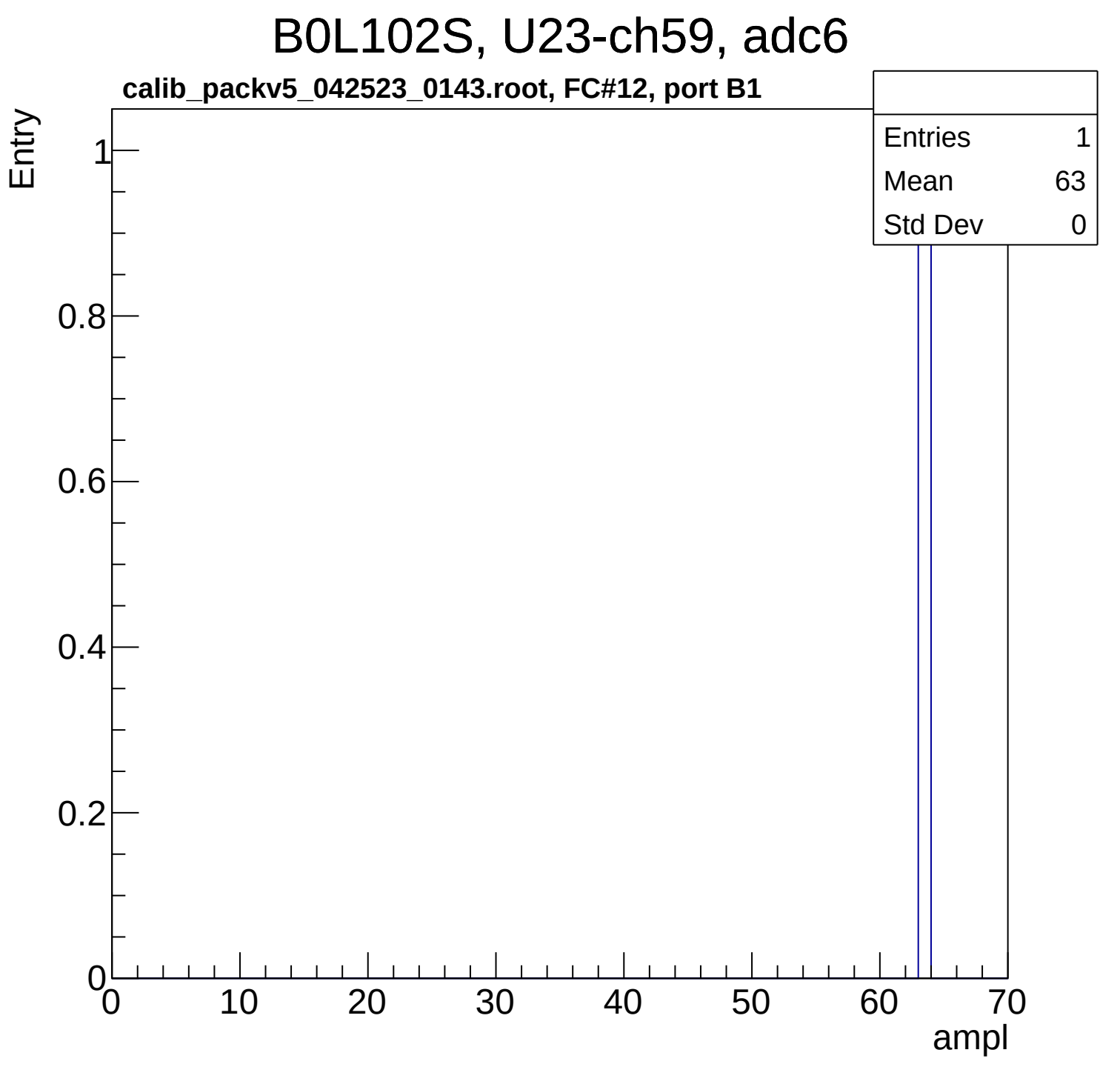
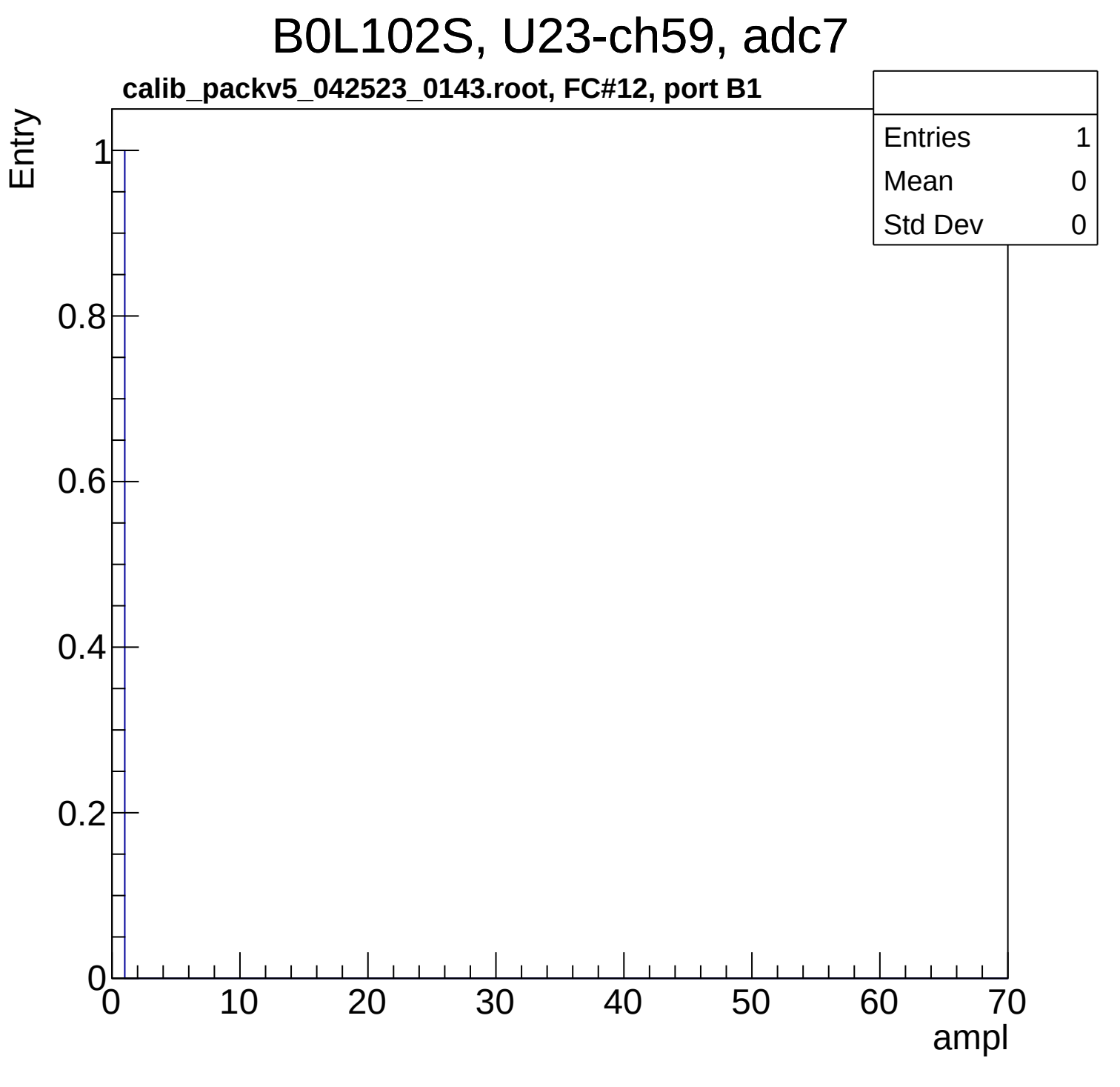
40
50
60
70


B0L102S, U23-ch59, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch59, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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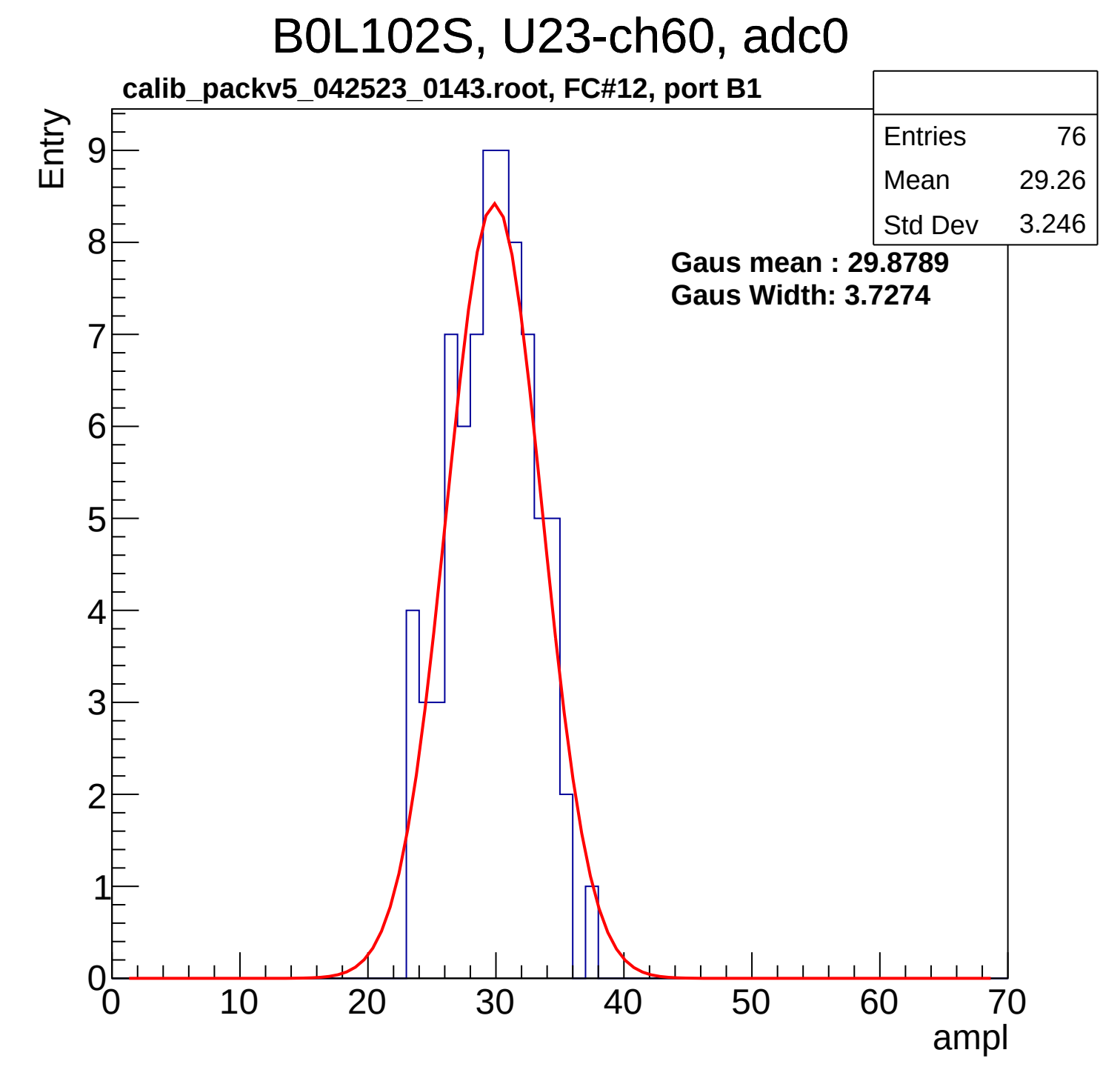
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch60, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.26
Std Dev 3.246
Gaus mean : 29.8789
Gaus Width: 3.7274
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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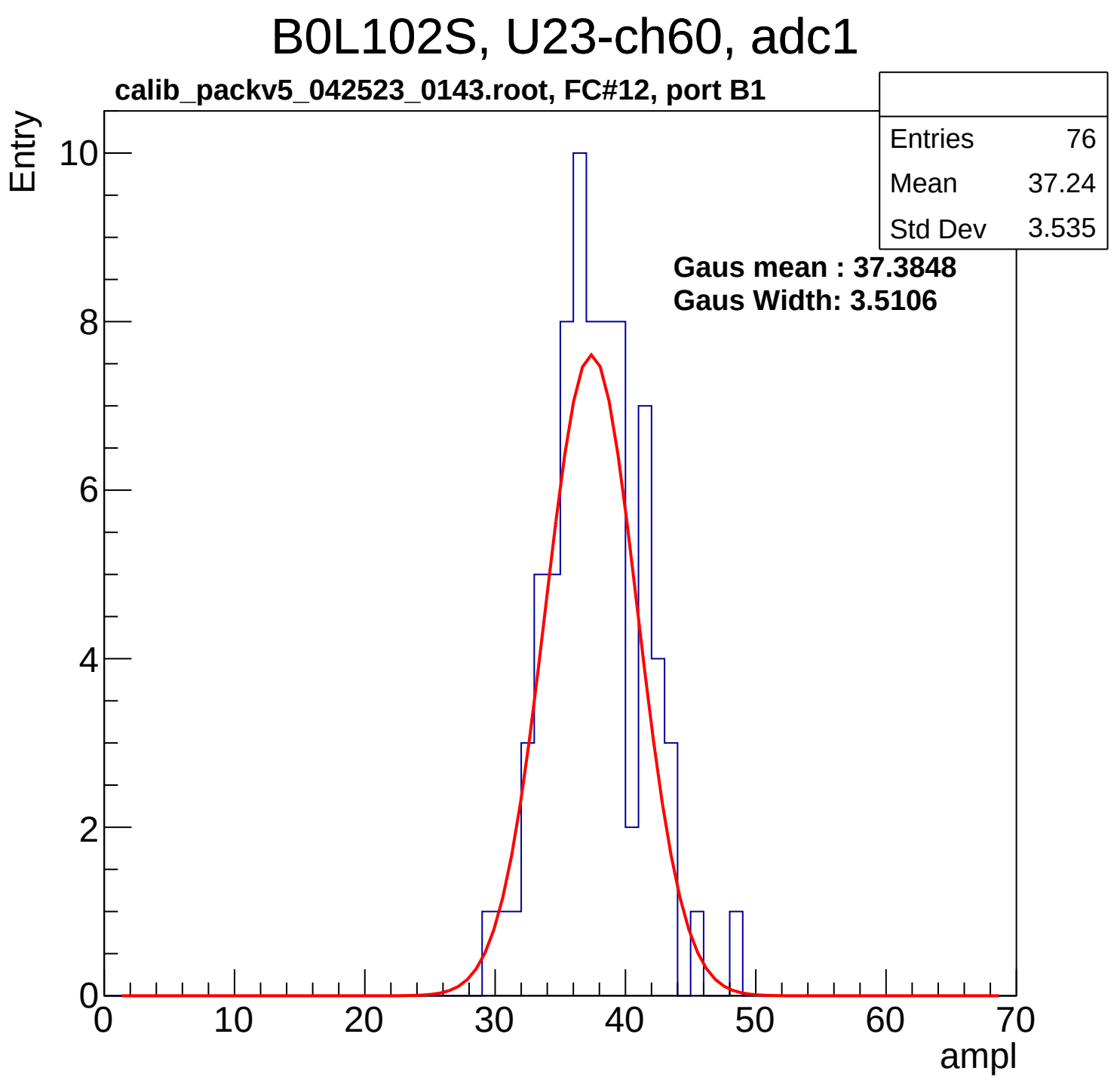
40
50
60
70


B0L102S, U23-ch60, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 37.24
Std Dev 3.535
Gaus mean : 37.3848
Gaus Width: 3.5106
Entry
ampl
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4
6
8
10
0
10
20
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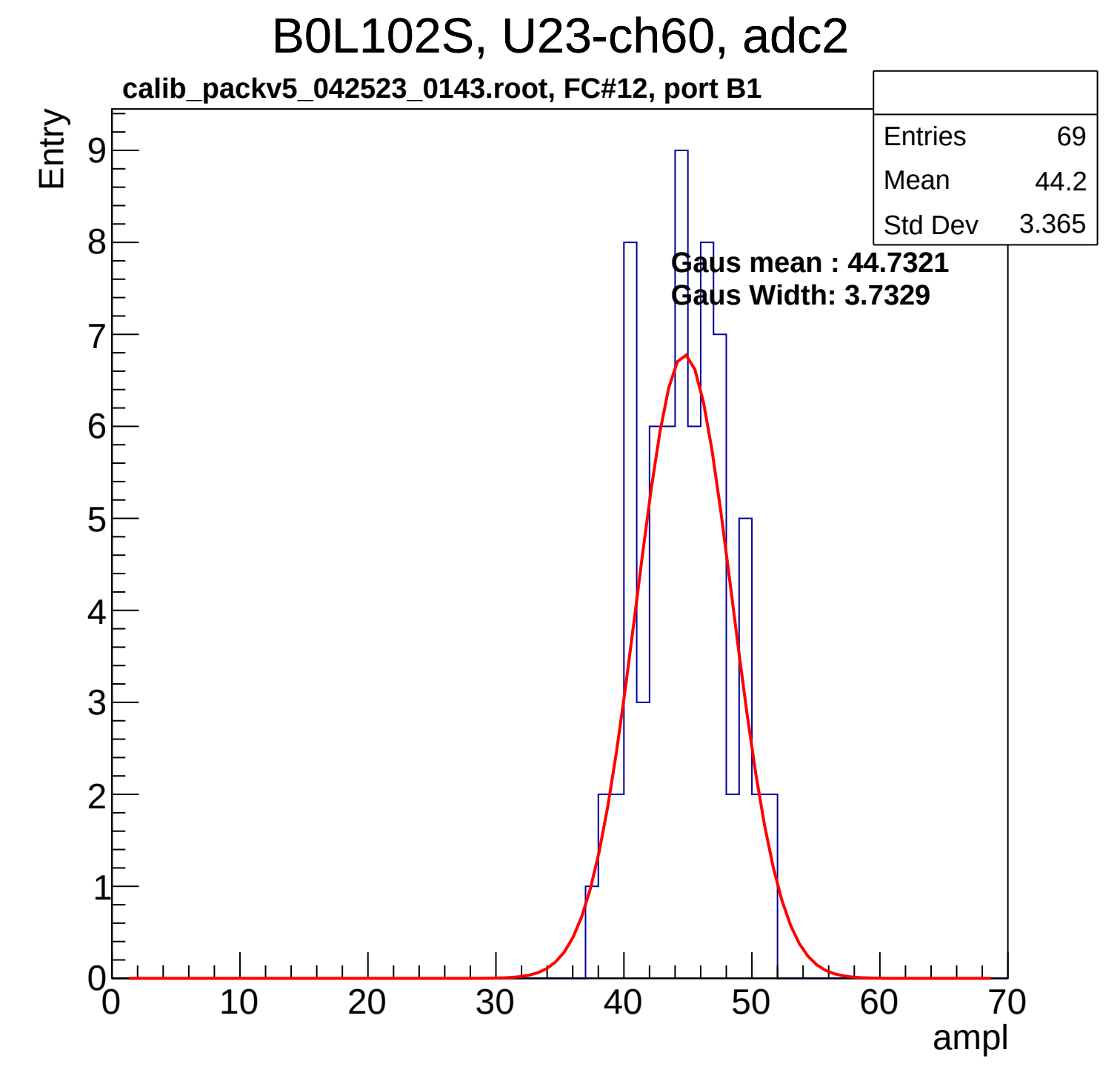
40
50
60
70


B0L102S, U23-ch60, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 44.2
Std Dev 3.365
Gaus mean : 44.7321
Gaus Width: 3.7329
Entry
ampl
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6
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0
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60
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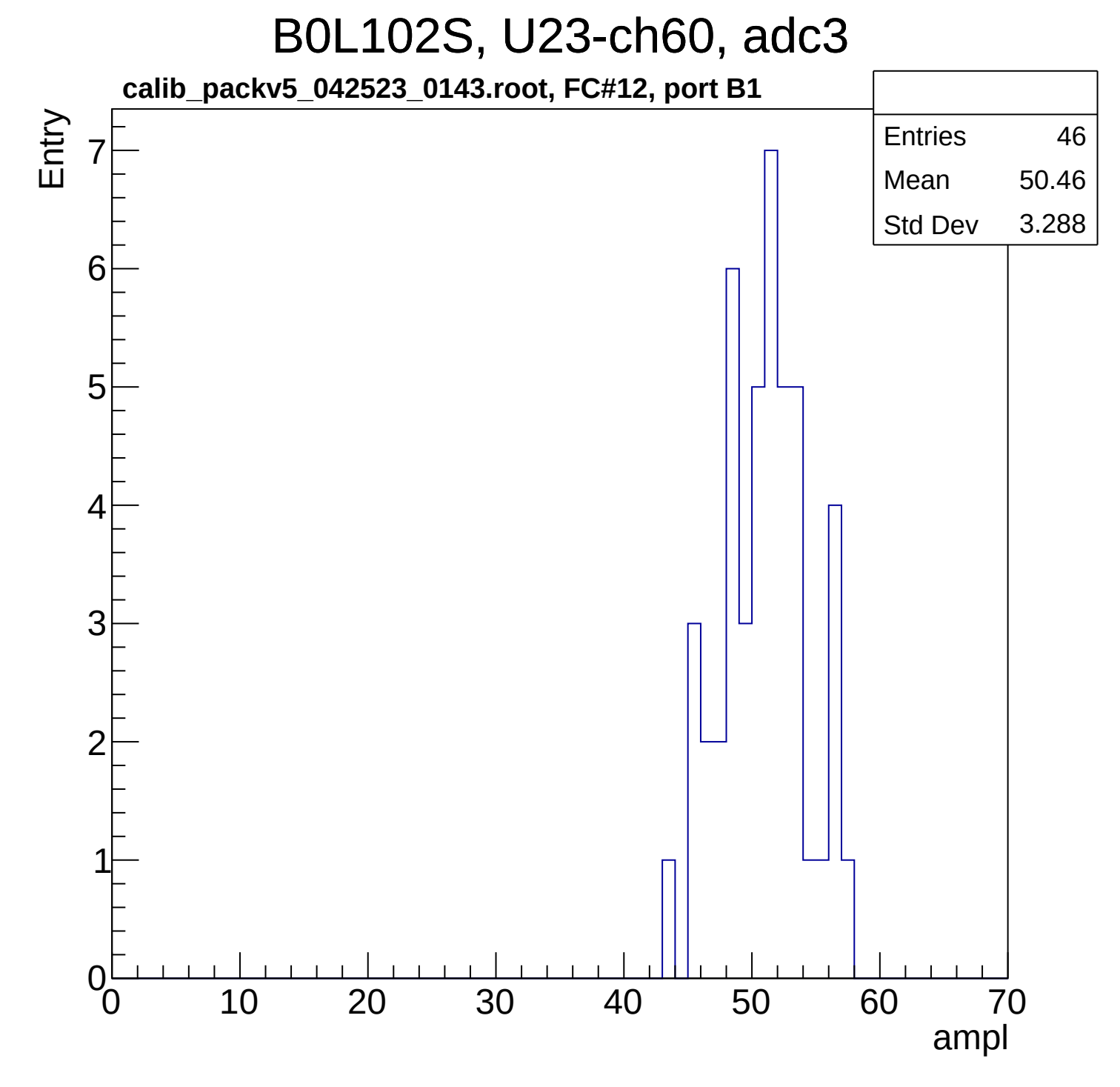
B0L102S, U23-ch60, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 50.46
Std Dev 3.288
Entry
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3
4
5
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10
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30
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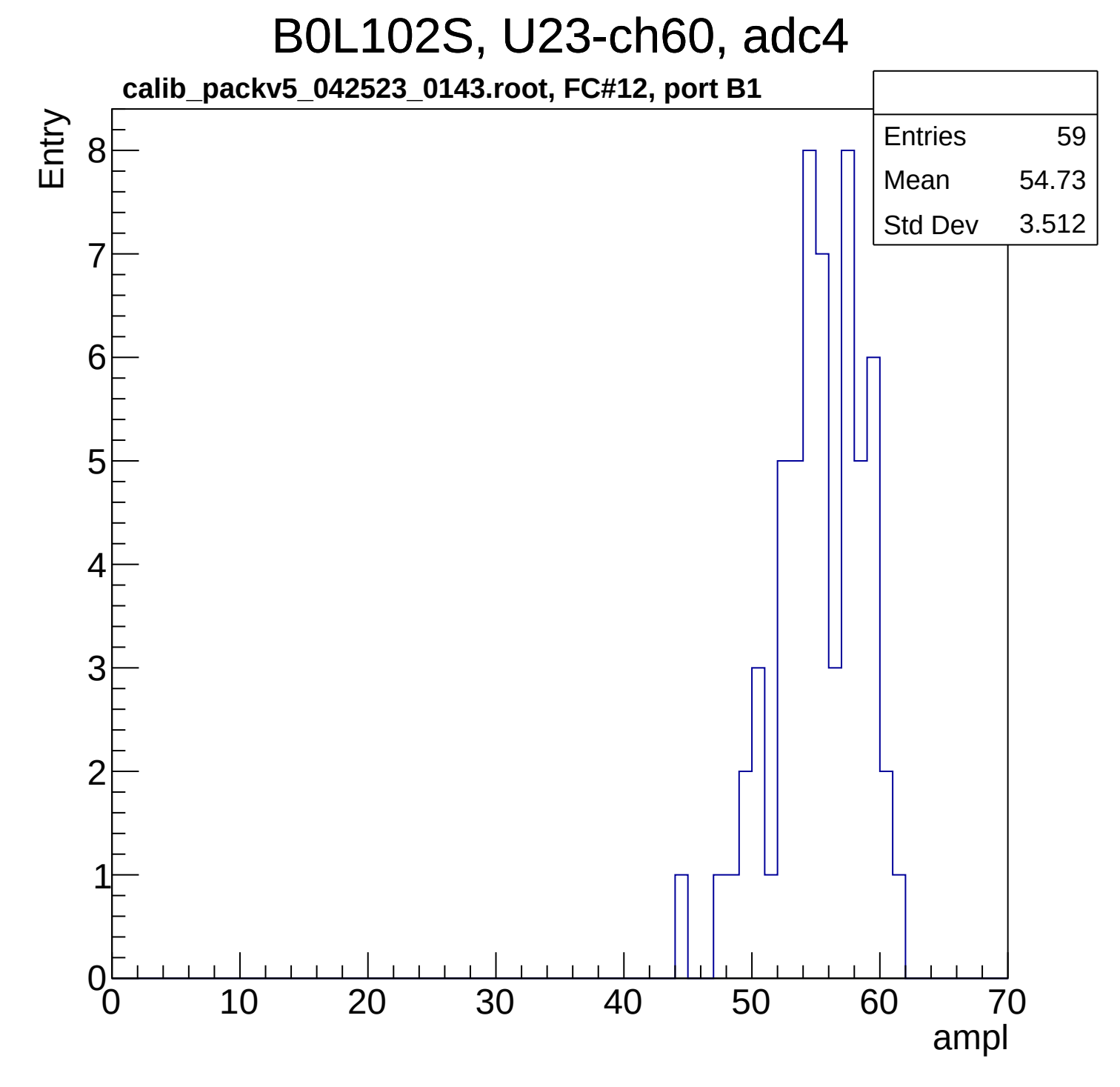
50
60
70
ampl


B0L102S, U23-ch60, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 54.73
Std Dev 3.512
Entry
ampl
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2
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4
5
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7
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0
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20
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60
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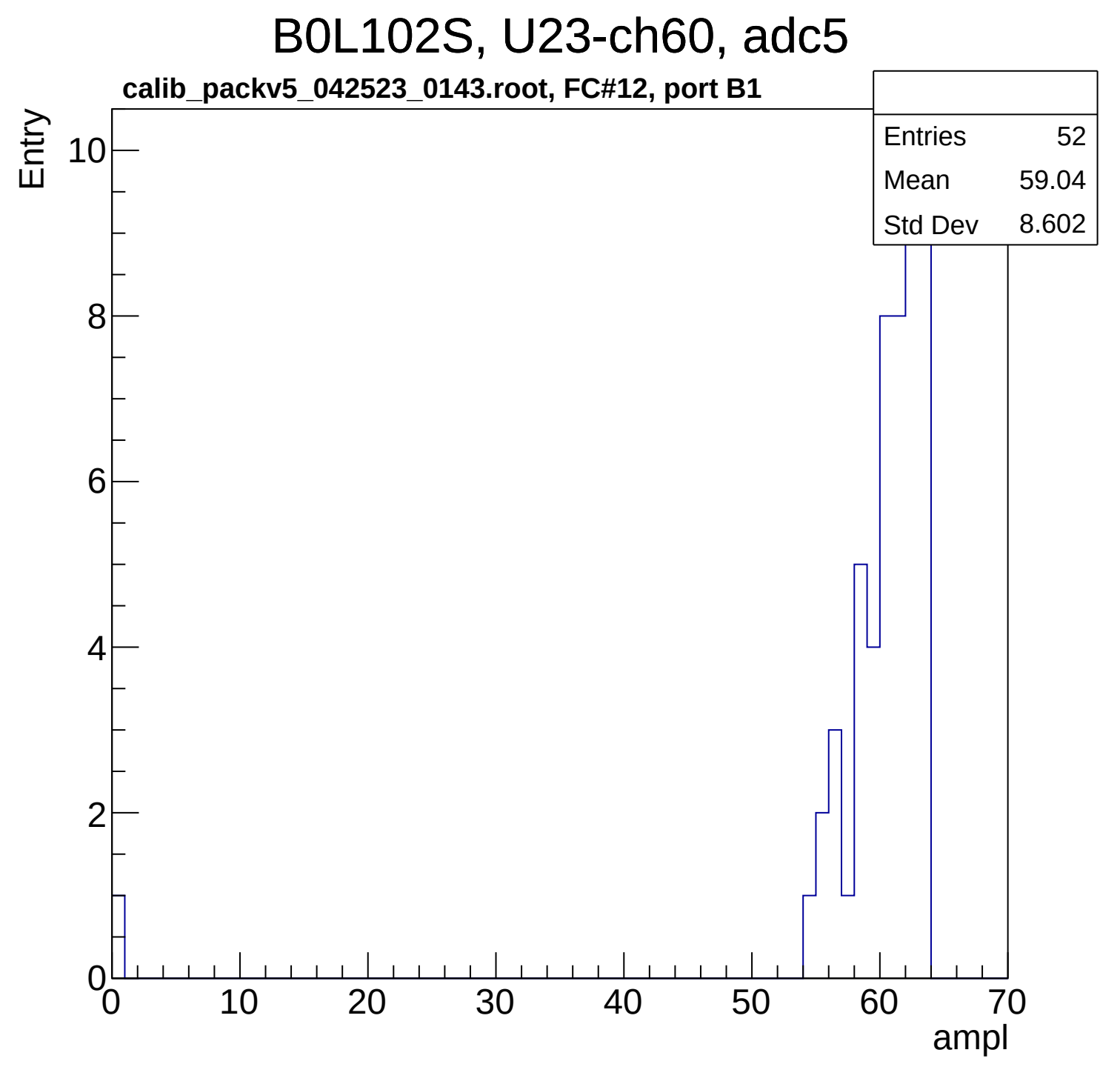
B0L102S, U23-ch60, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 59.04
Std Dev 8.602
Entry
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10
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30
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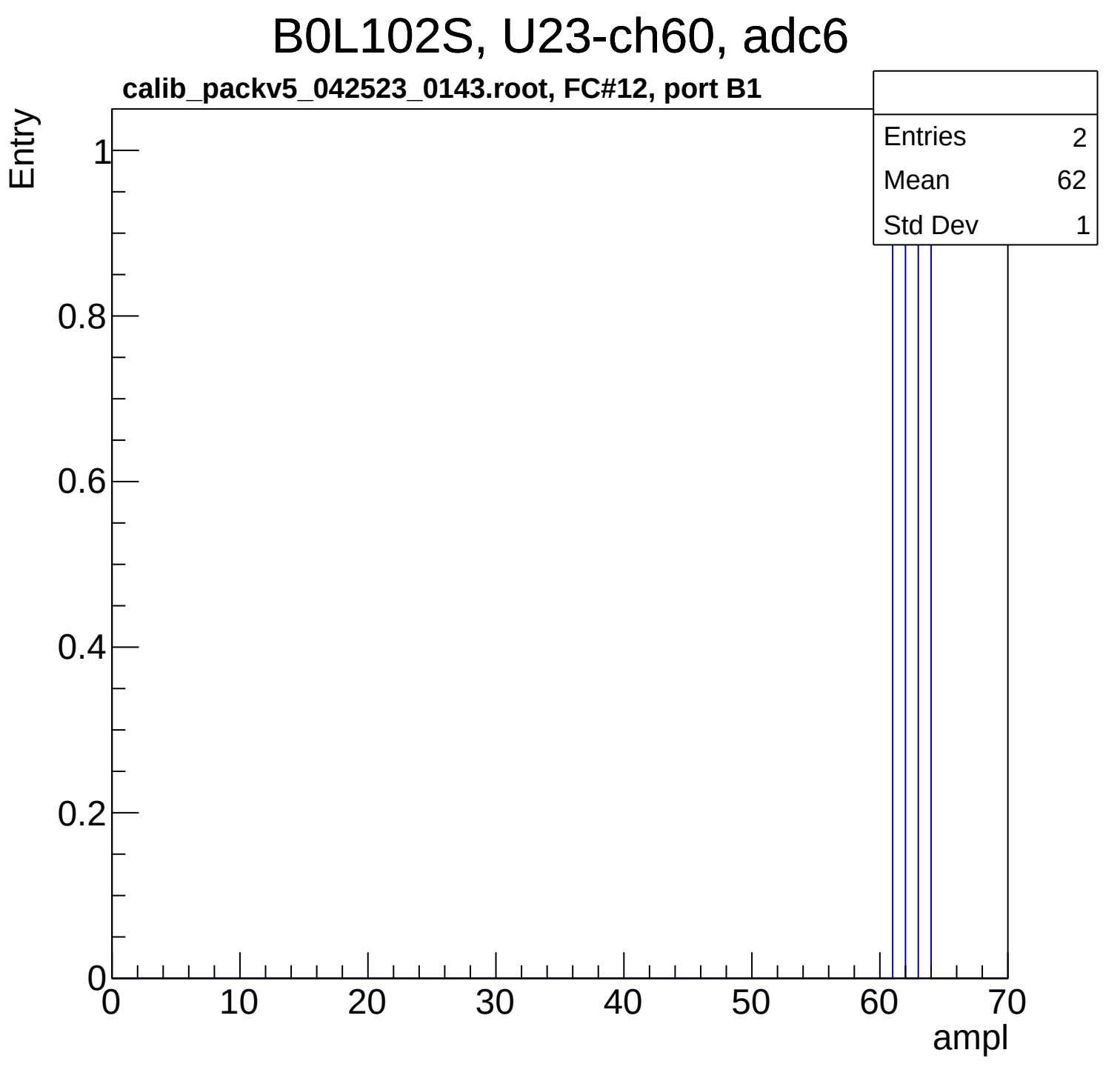
60
70
ampl


B0L102S, U23-ch60, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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0.6
0.4
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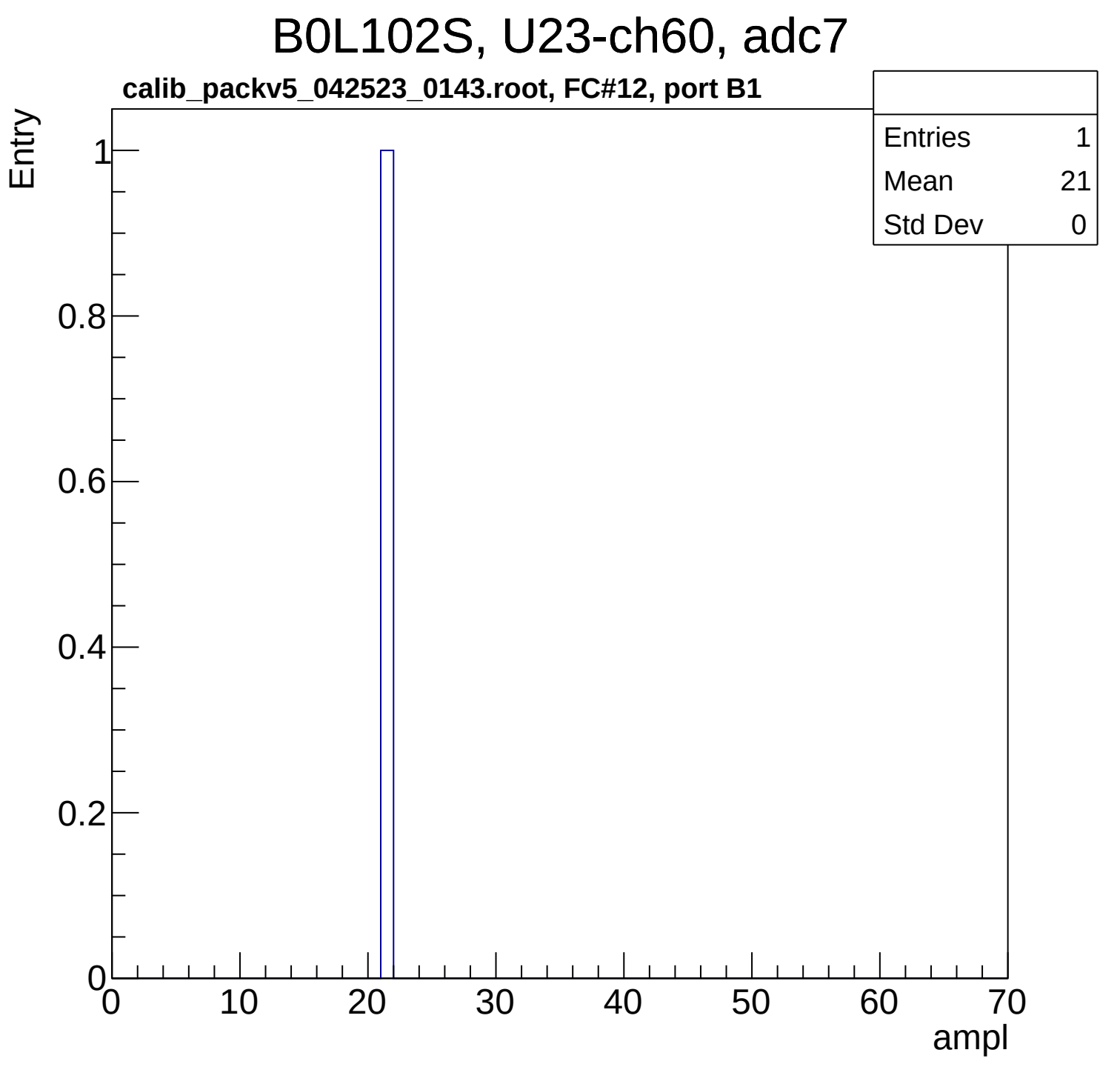
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch60, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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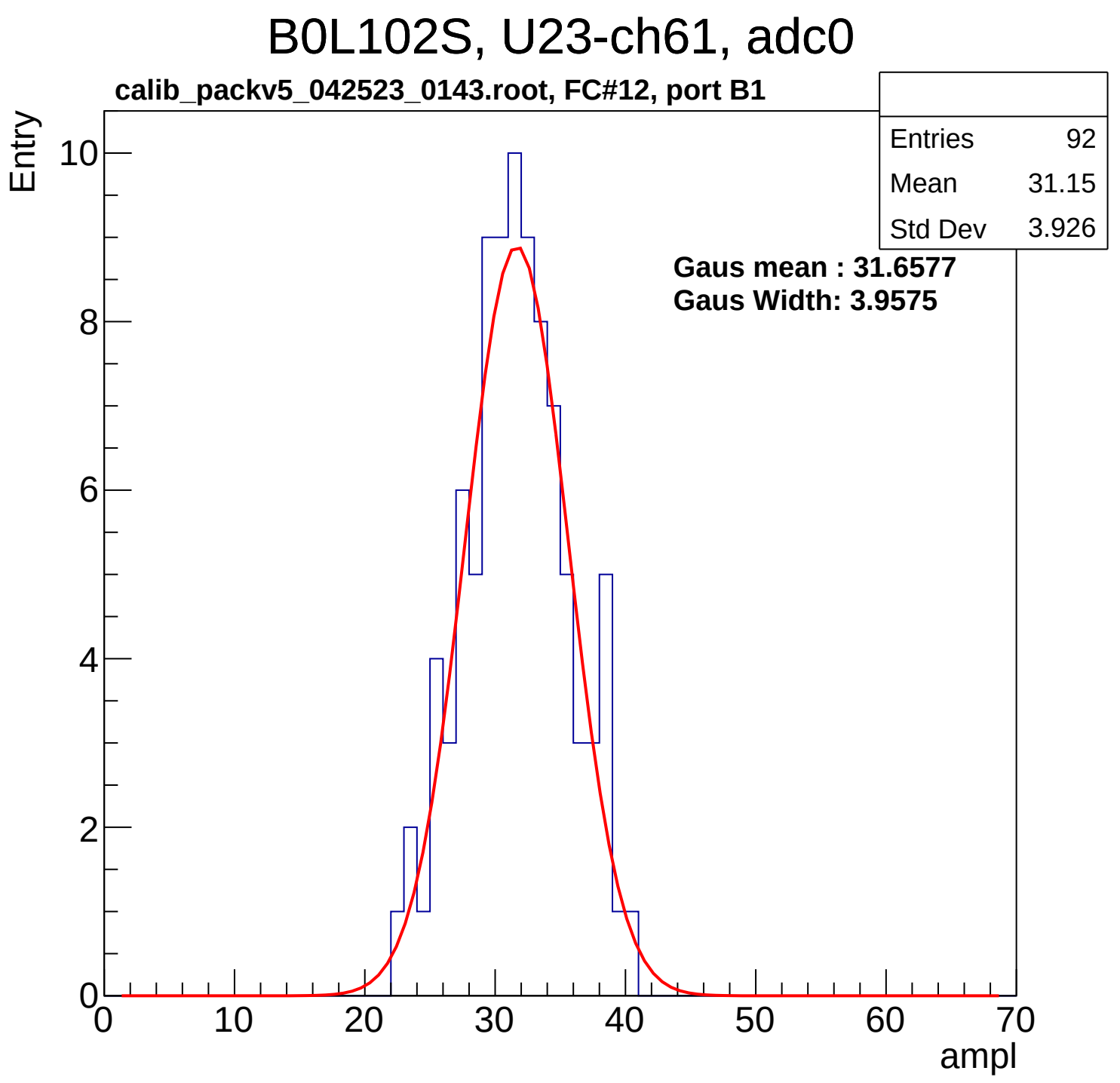
50
60
70
ampl


B0L102S, U23-ch61, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 92
Mean 31.15
Std Dev 3.926
Gaus mean : 31.6577
Gaus Width: 3.9575
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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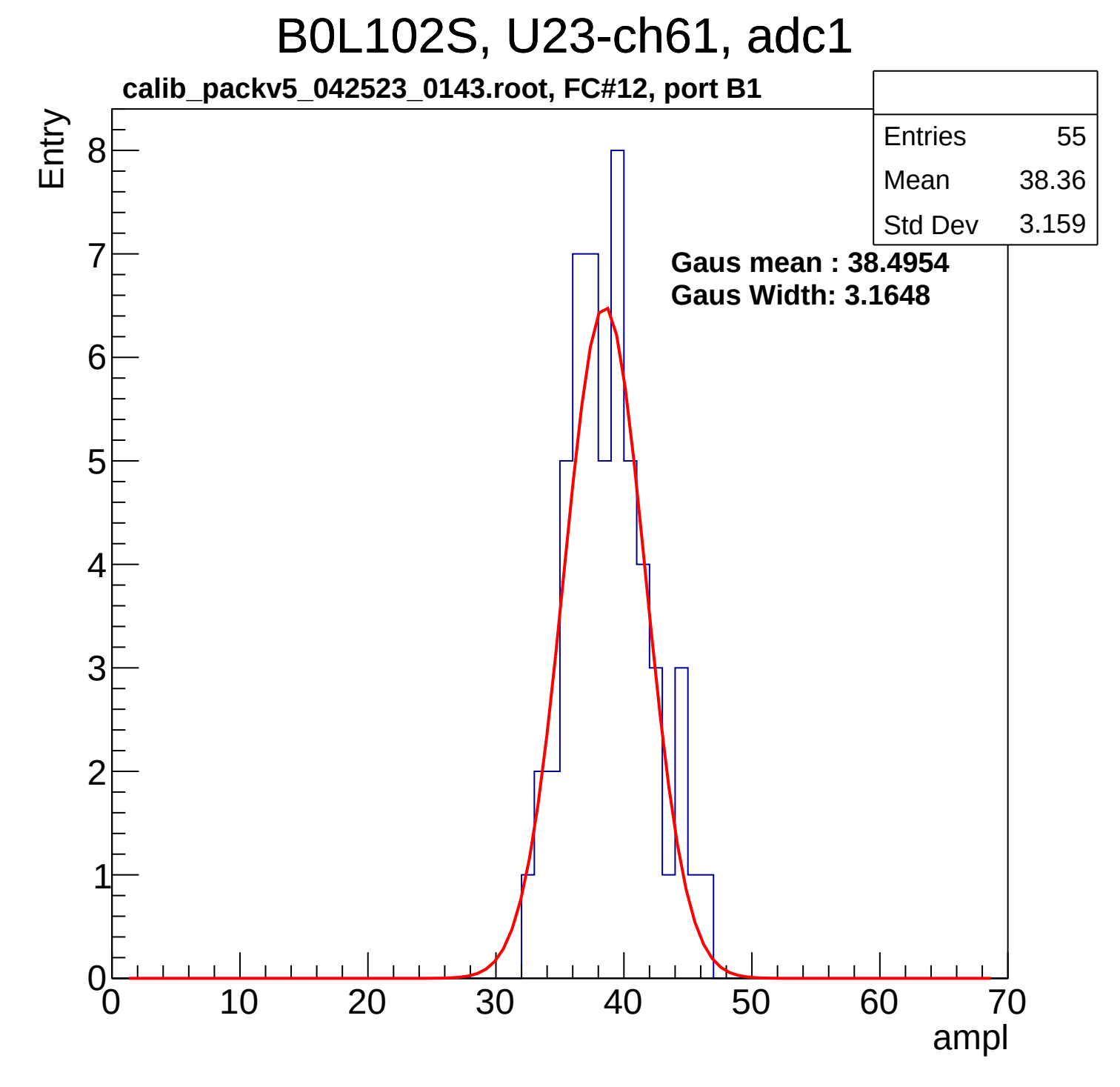
B0L102S, U23-ch61, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 38.36
Std Dev 3.159
Gaus mean : 38.4954
Gaus Width: 3.1648
Entry
ampl
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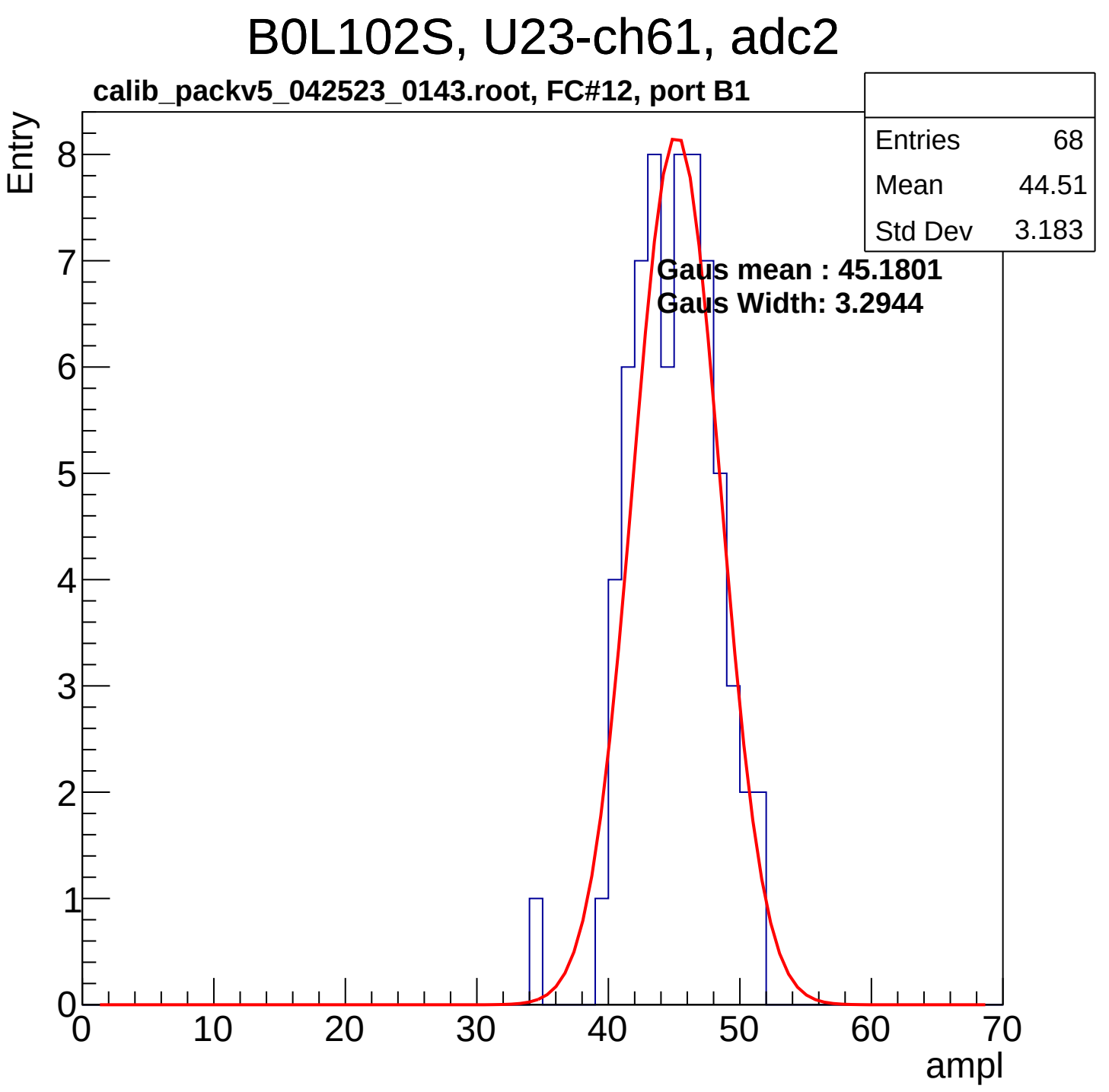
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch61, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 44.51
Std Dev 3.183
Gaus mean : 45.1801
Gaus Width: 3.2944
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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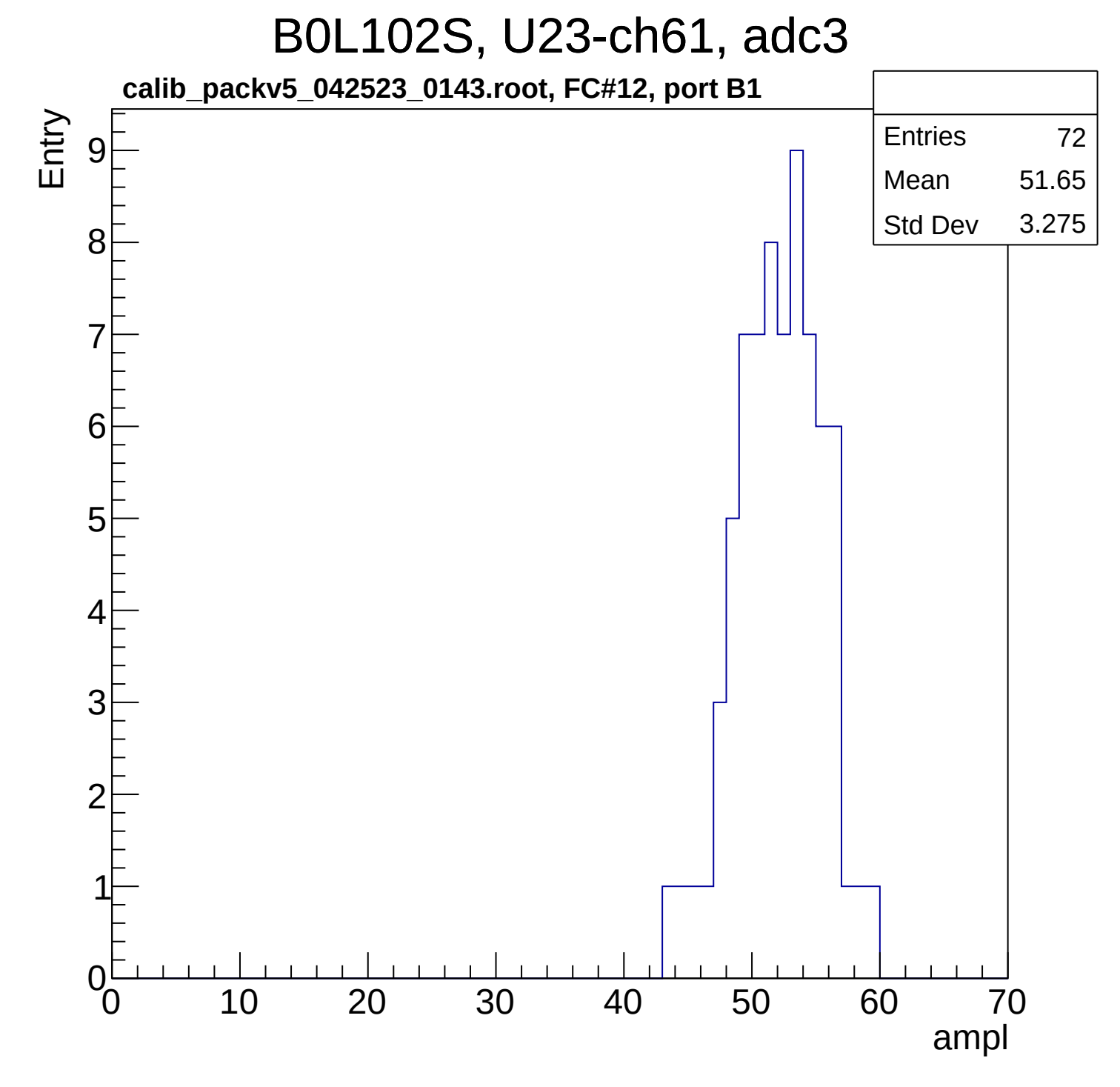
40
50
60
70


B0L102S, U23-ch61, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 51.65
Std Dev 3.275
Entry
ampl
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60
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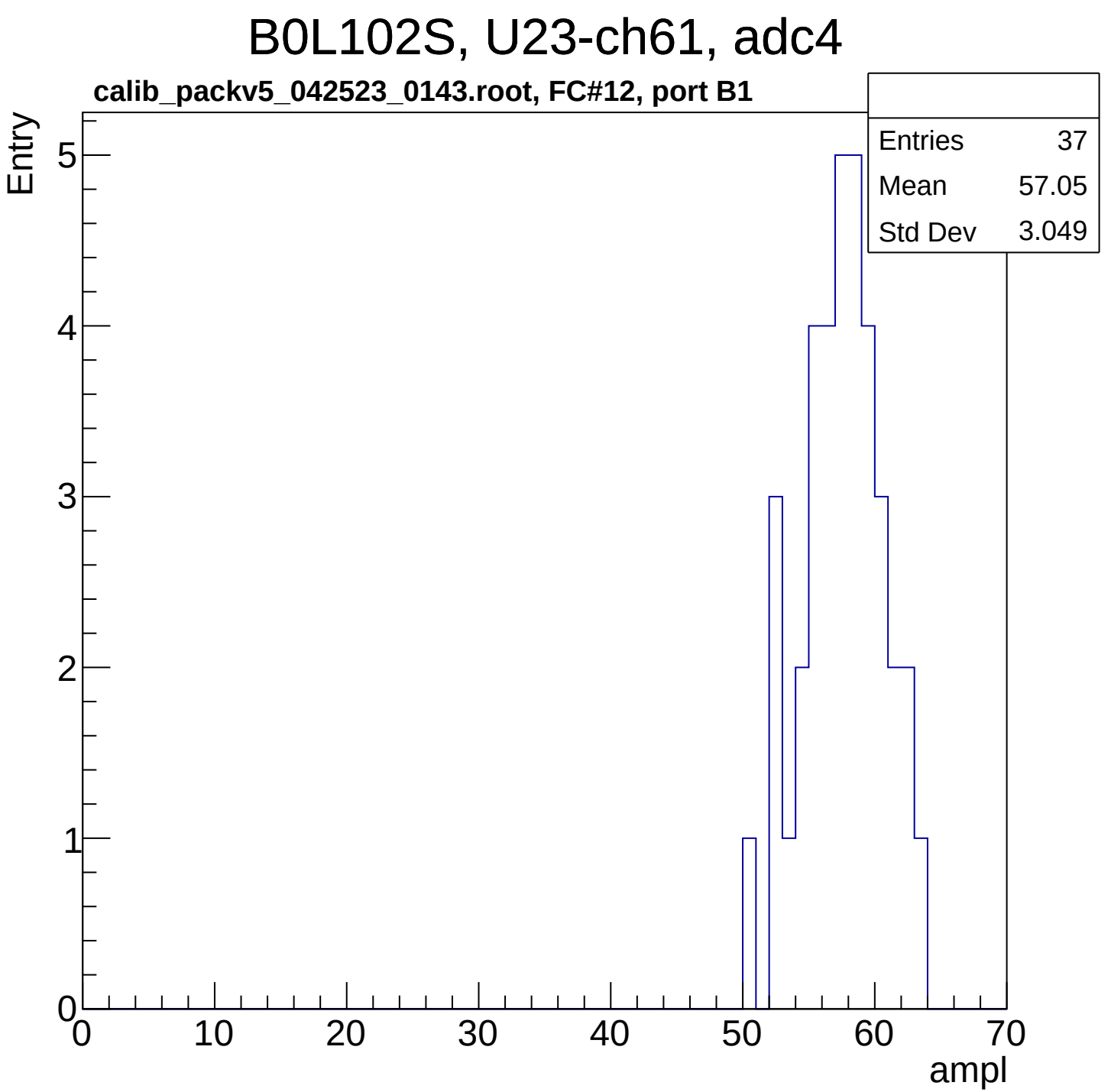
B0L102S, U23-ch61, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 57.05
Std Dev 3.049
Entry
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1
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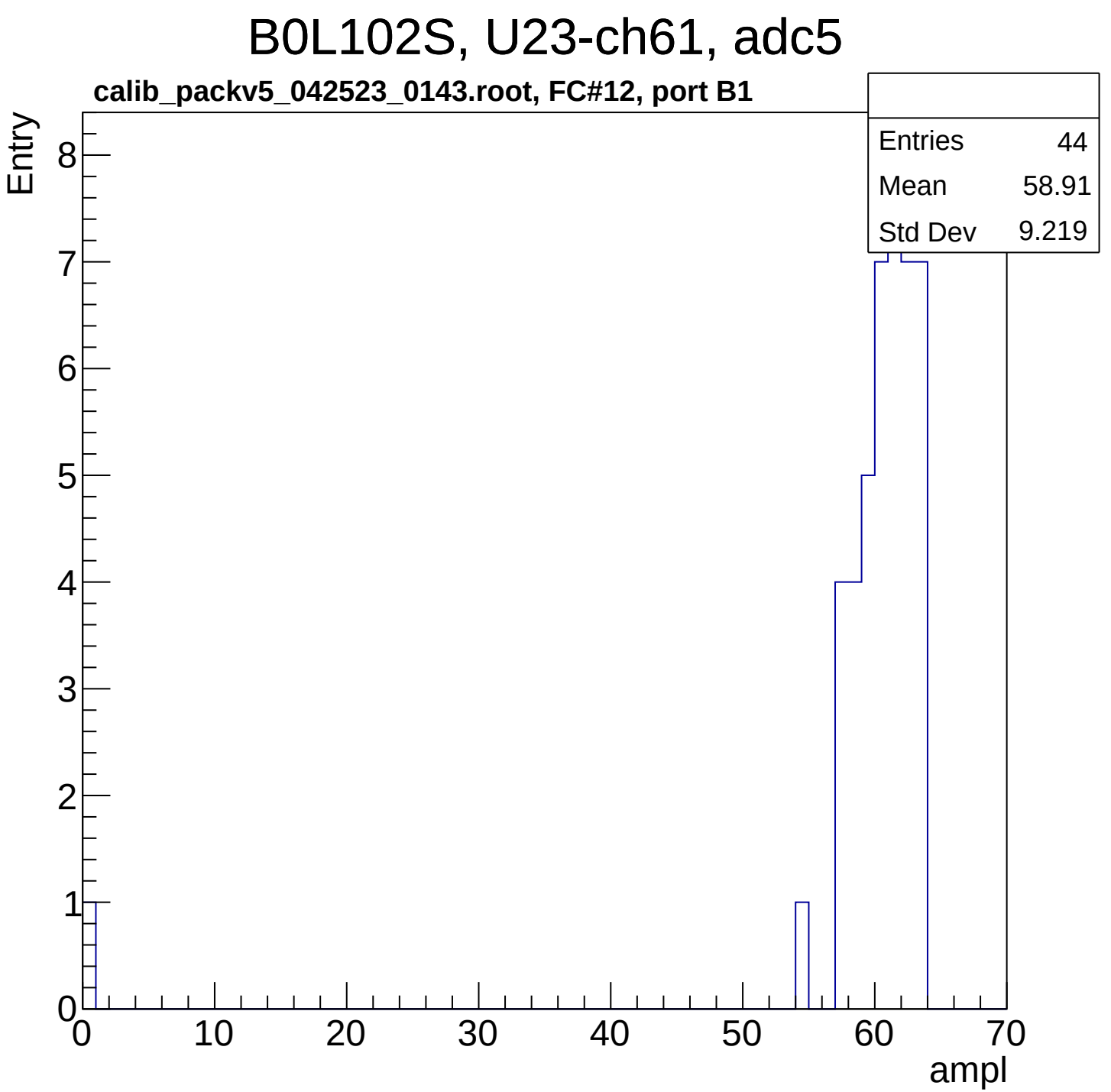
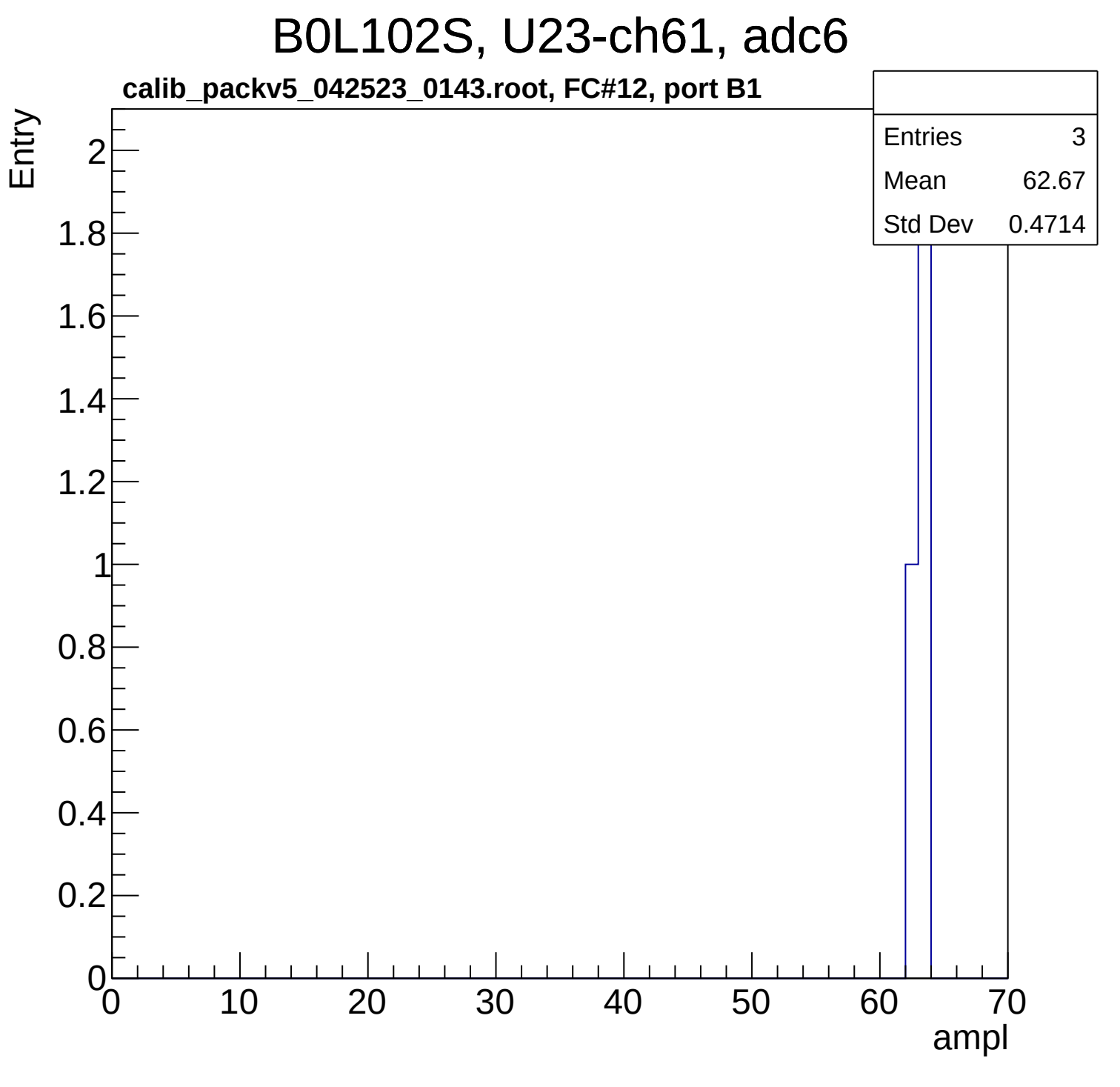
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch61, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.91
Std Dev 9.219
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch61, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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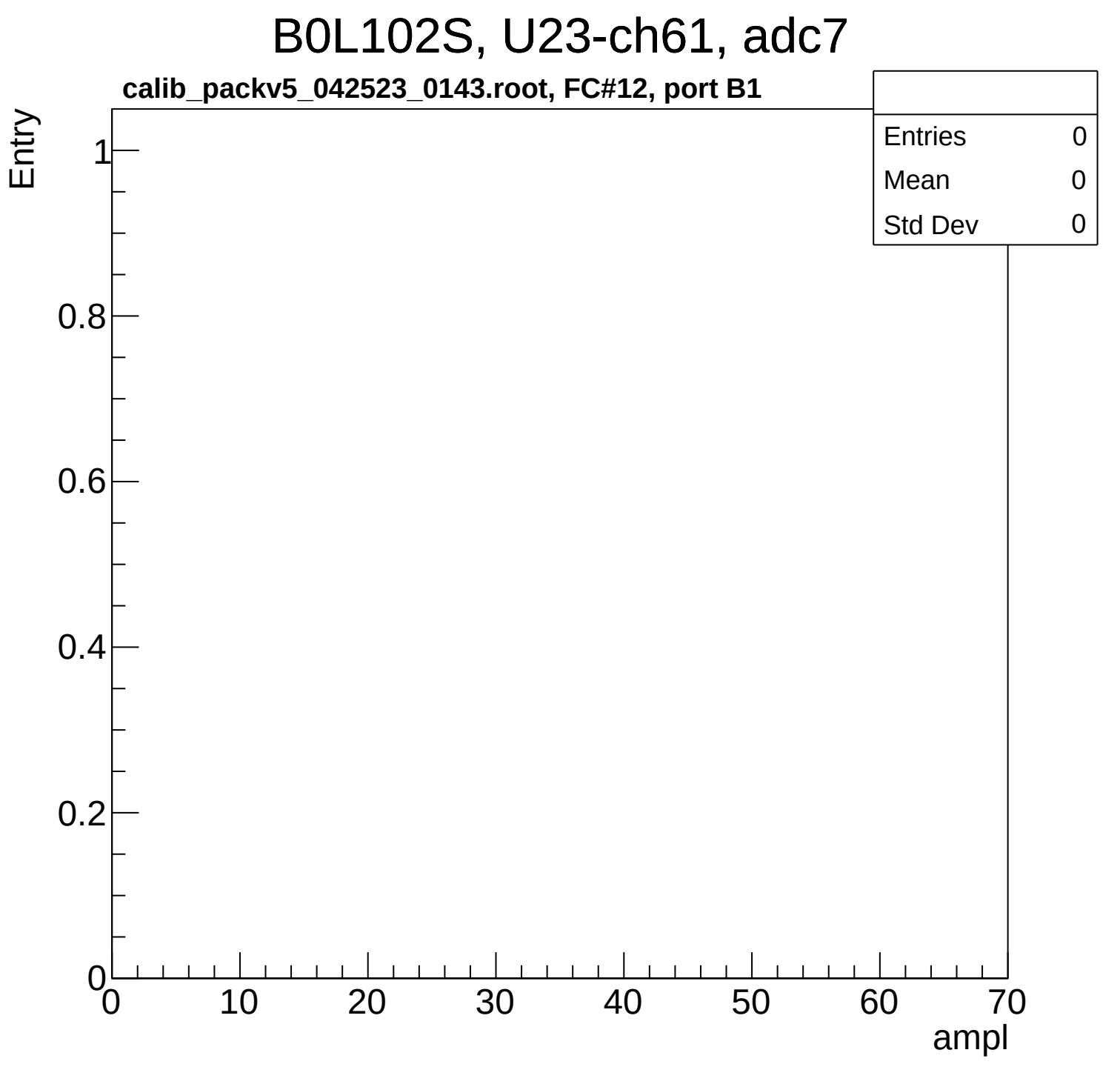
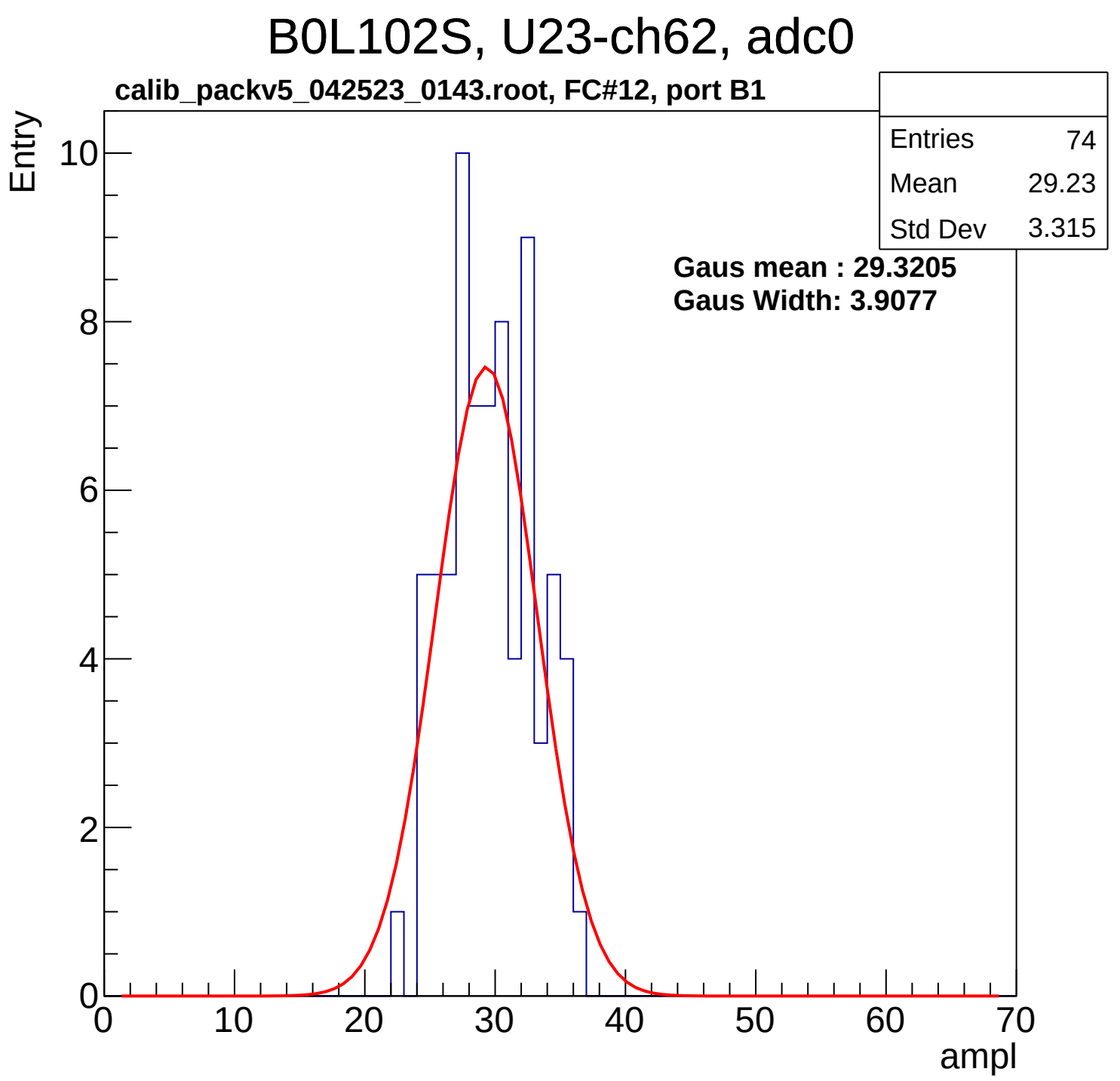
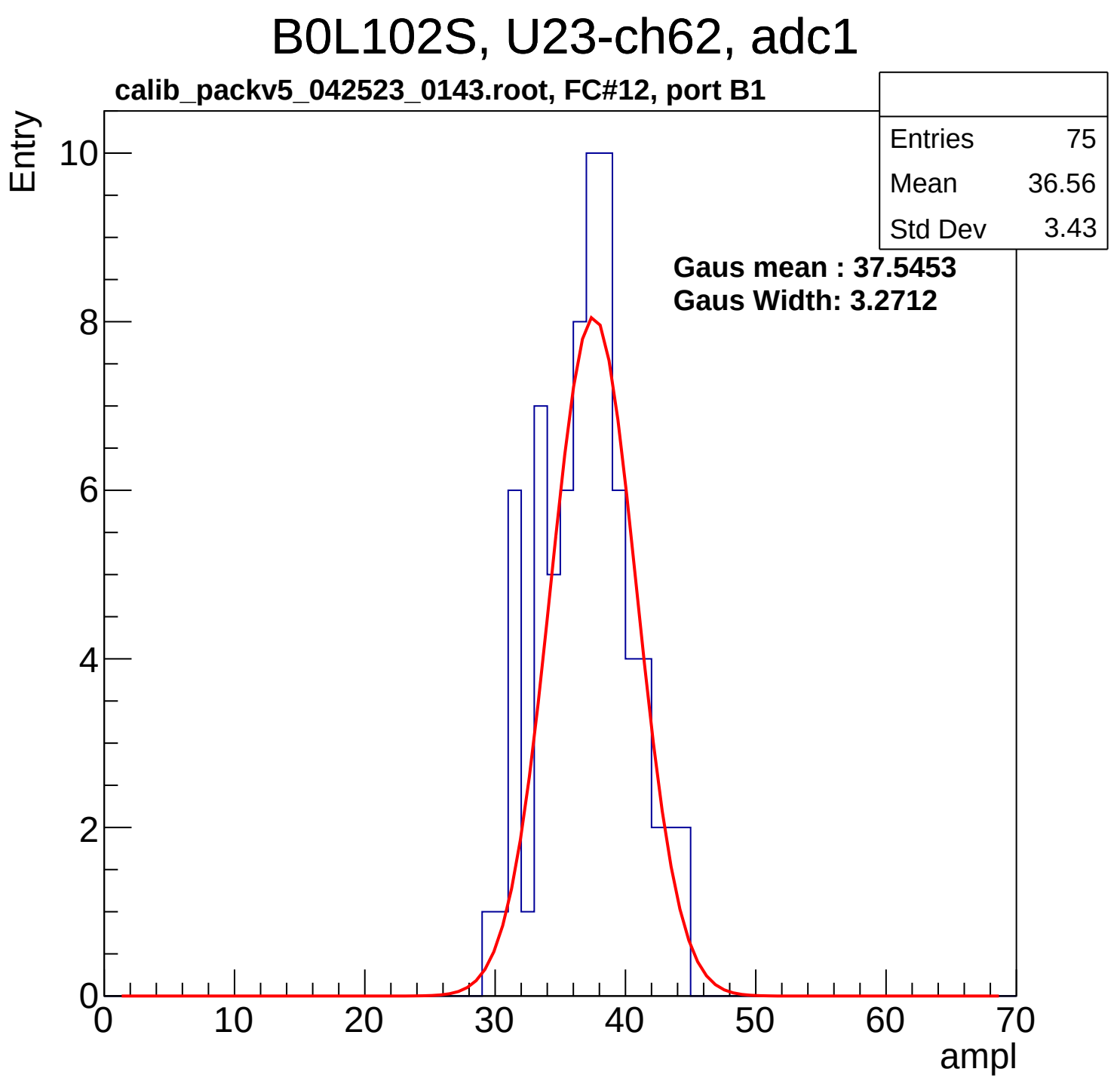
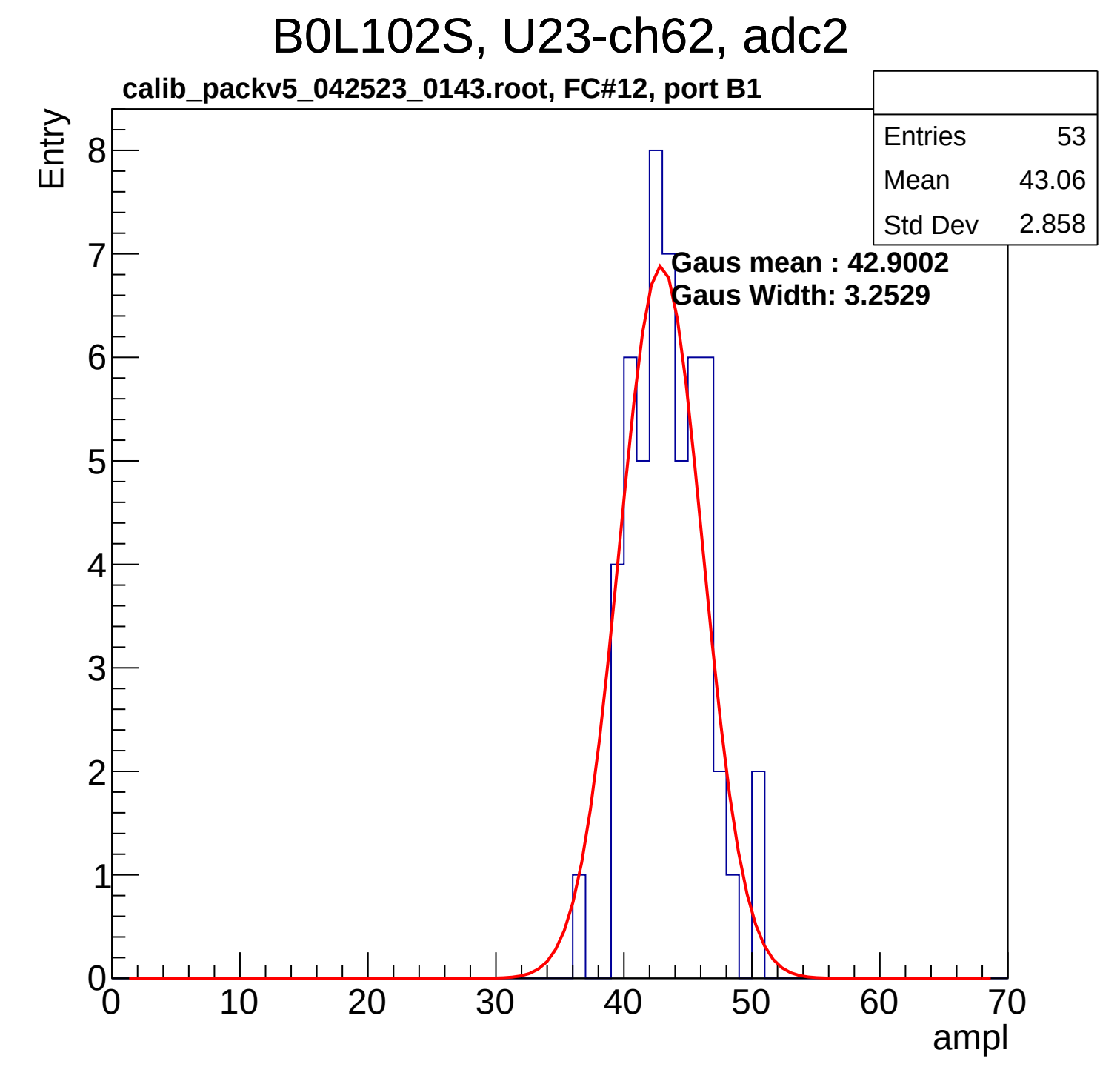

B0L102S, U23-ch61, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch62, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 29.23
Std Dev 3.315
Gaus mean : 29.3205
Gaus Width: 3.9077
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch62, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.56
Std Dev 3.43
Gaus mean : 37.5453
Gaus Width: 3.2712
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch62, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 43.06
Std Dev 2.858
Gaus mean : 42.9002
Gaus Width: 3.2529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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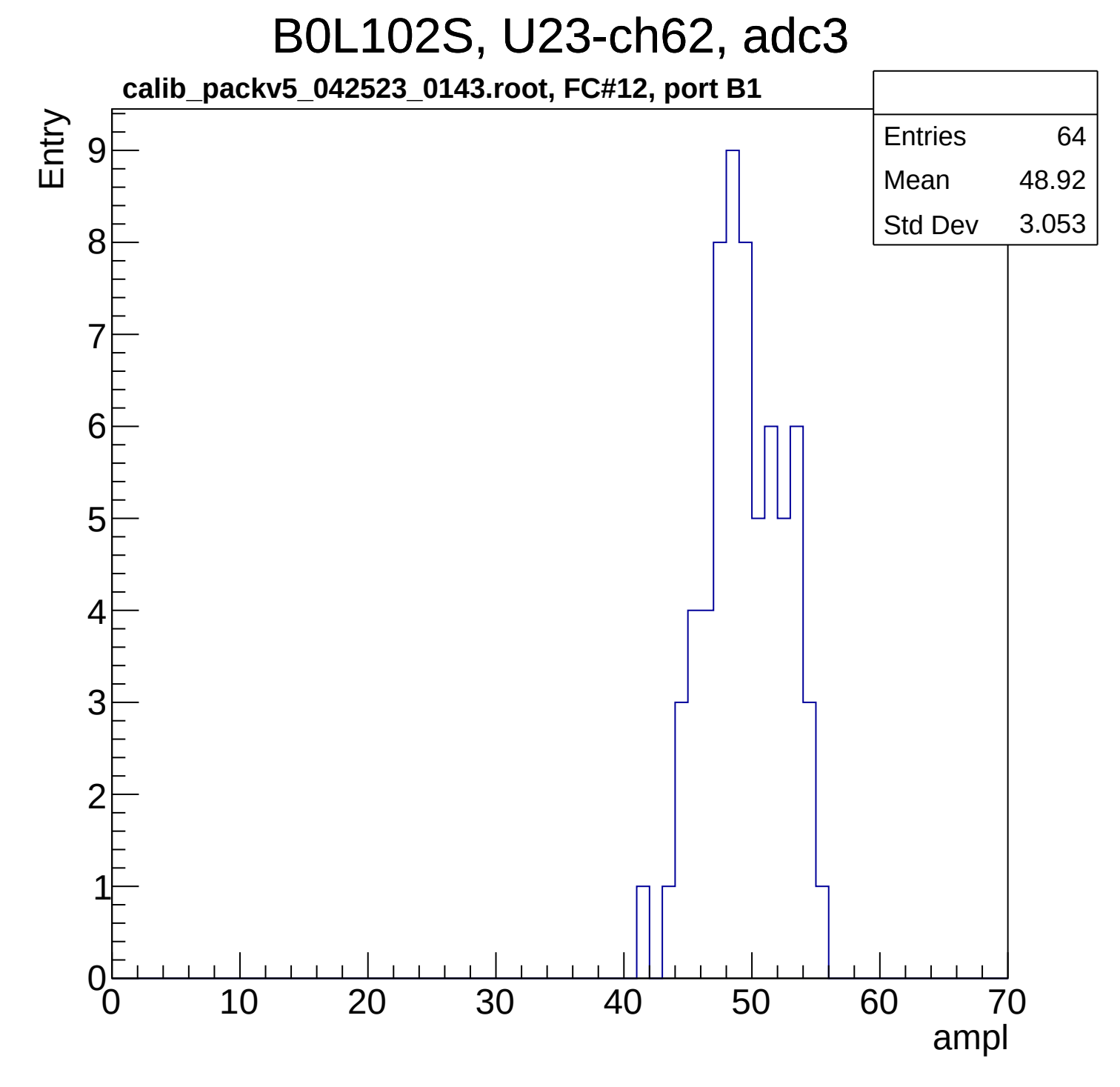
B0L102S, U23-ch62, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 48.92
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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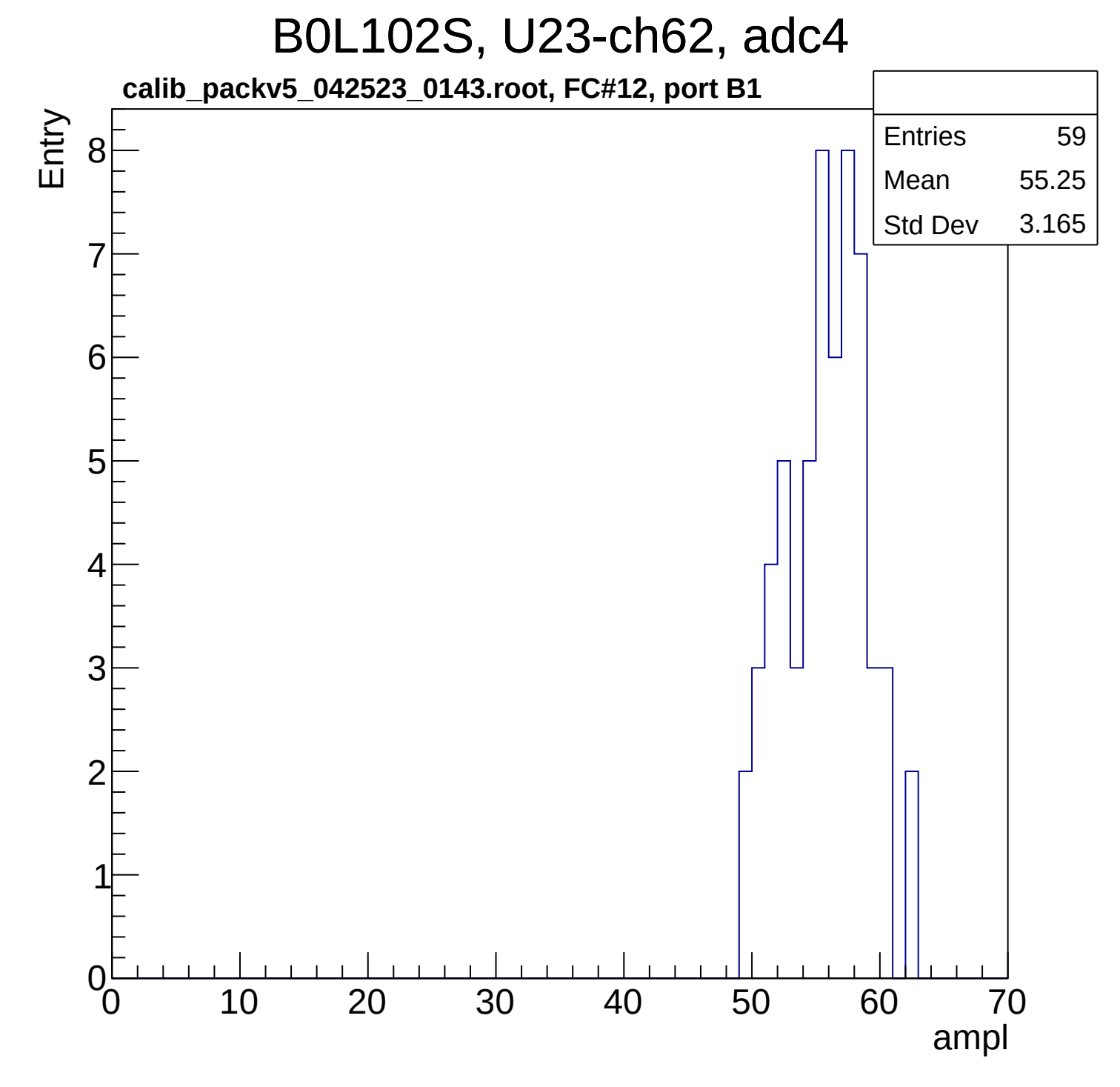
B0L102S, U23-ch62, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 55.25
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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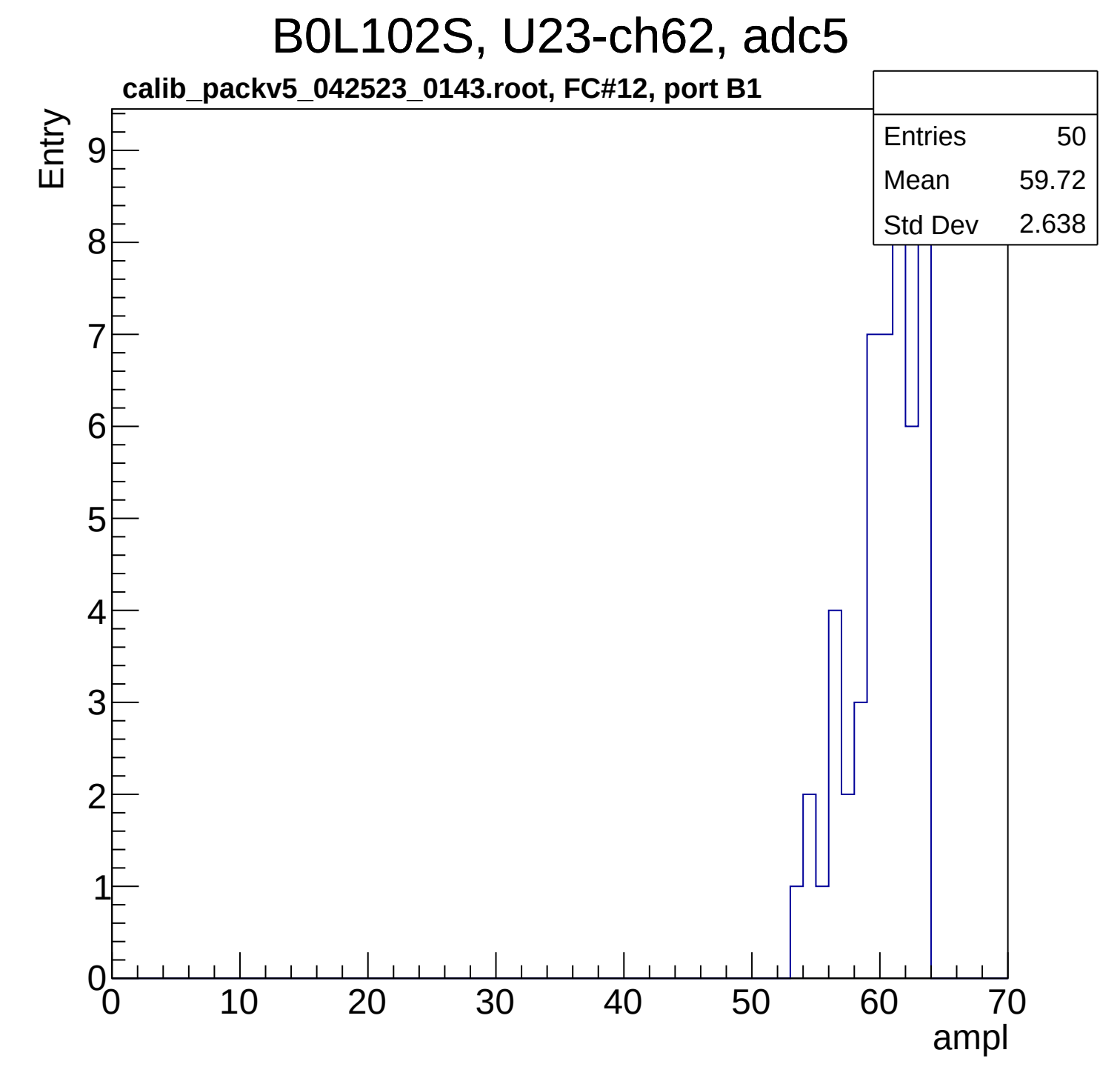
B0L102S, U23-ch62, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 59.72
Std Dev 2.638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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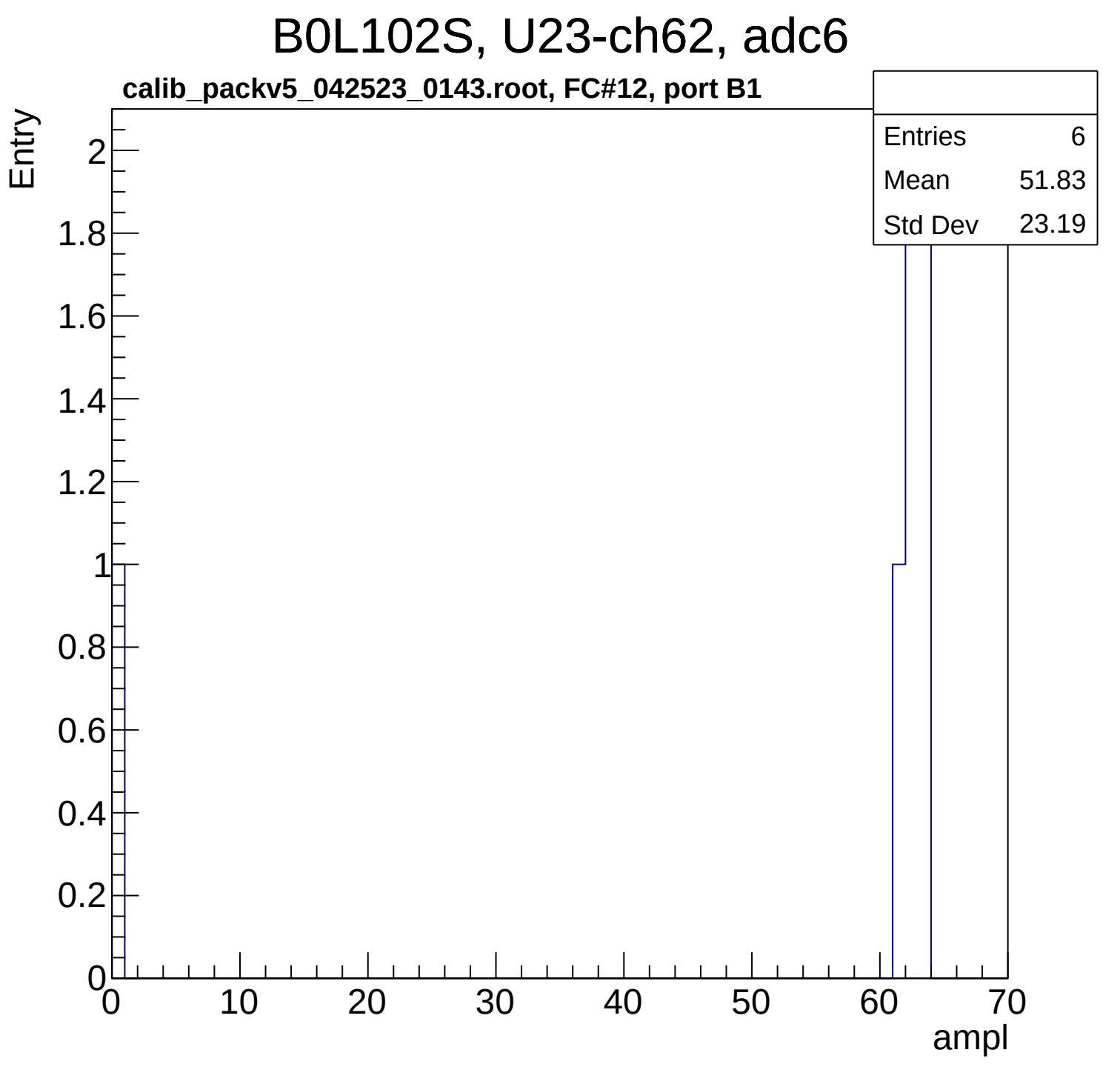
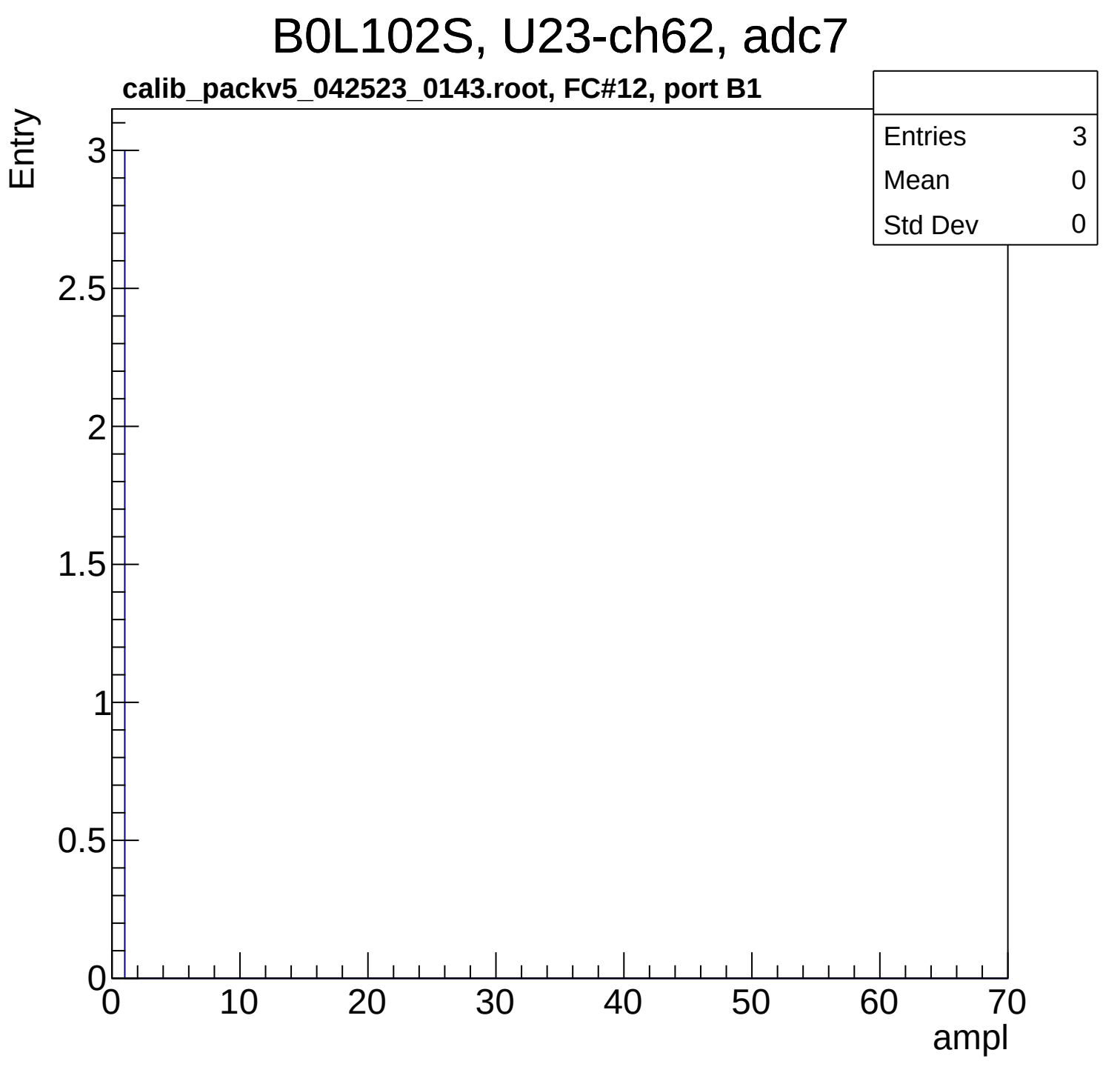
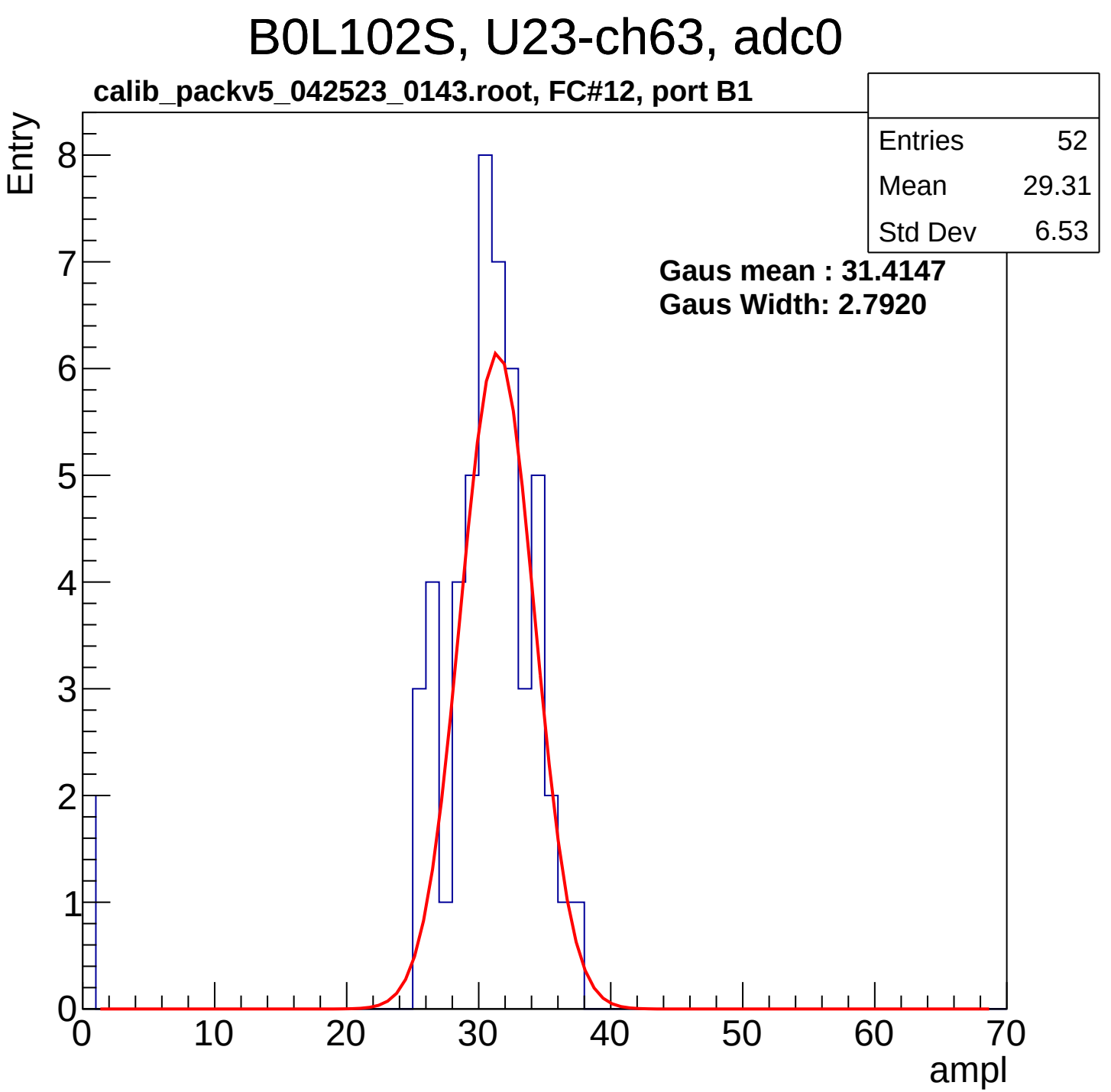
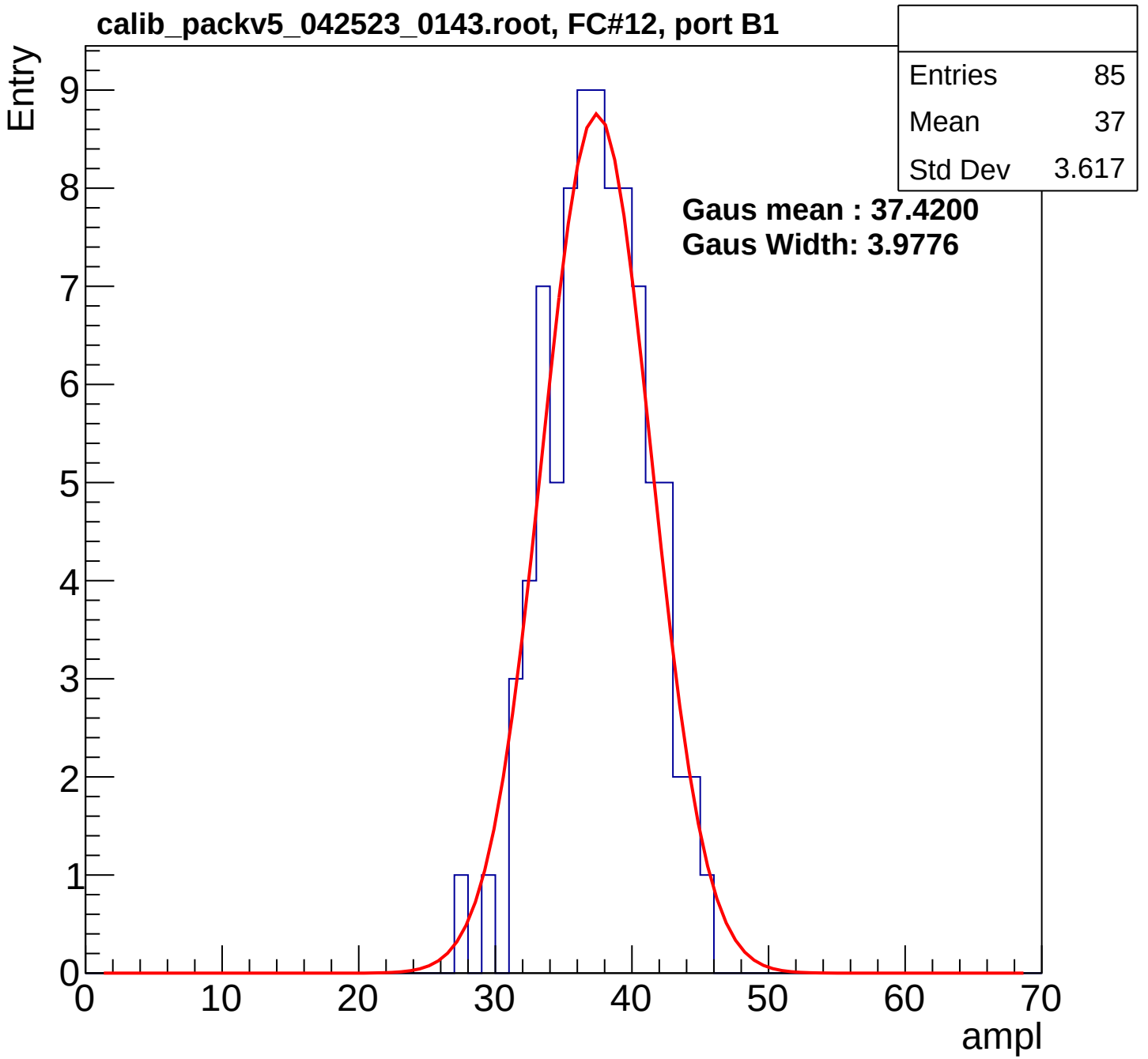

B0L102S, U23-ch62, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch62, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch63, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 29.31
Std Dev 6.53
Gaus mean : 31.4147
Gaus Width: 2.7920
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch63, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 37
Std Dev 3.617
Gaus mean : 37.4200
Gaus Width: 3.9776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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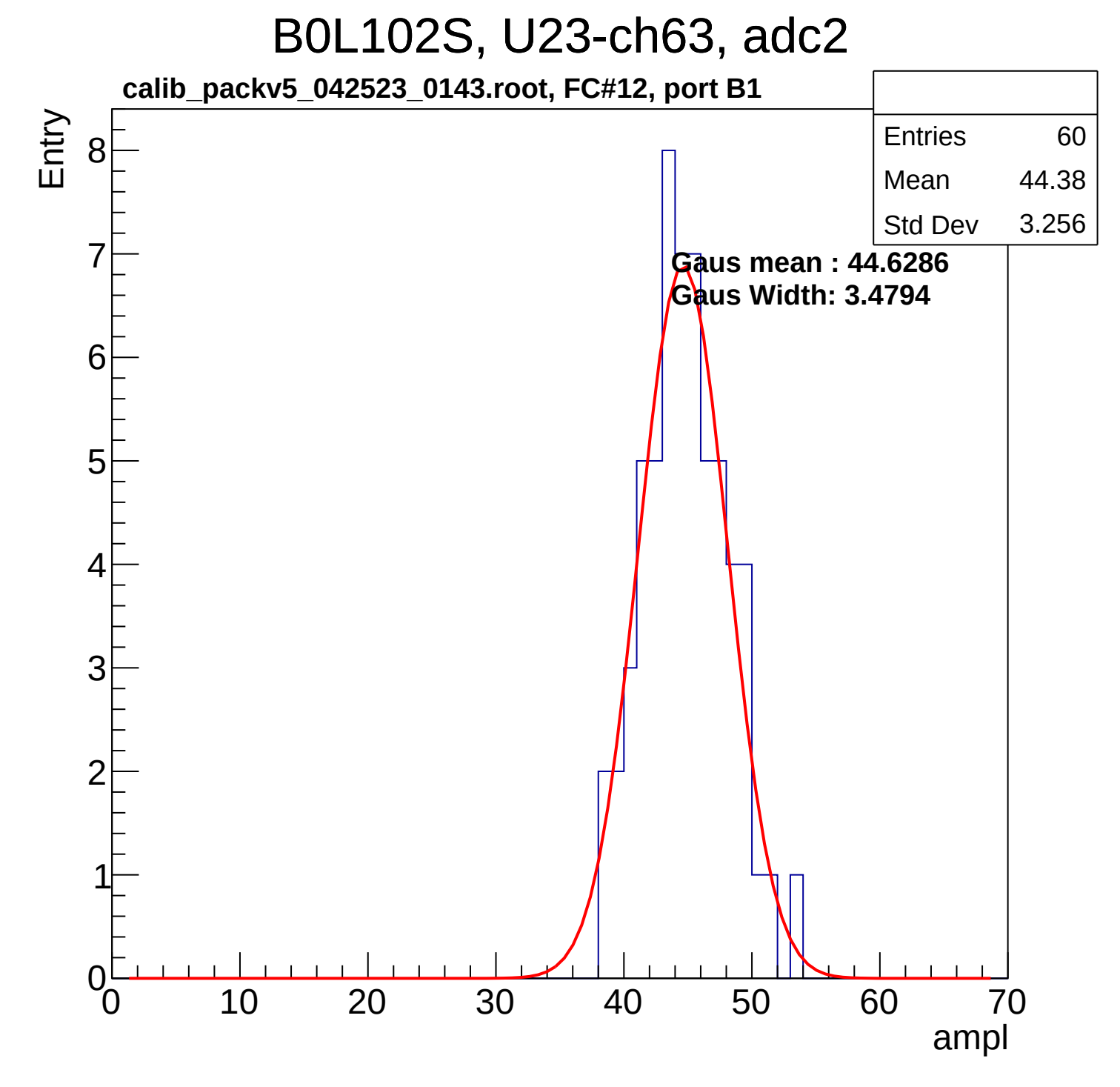
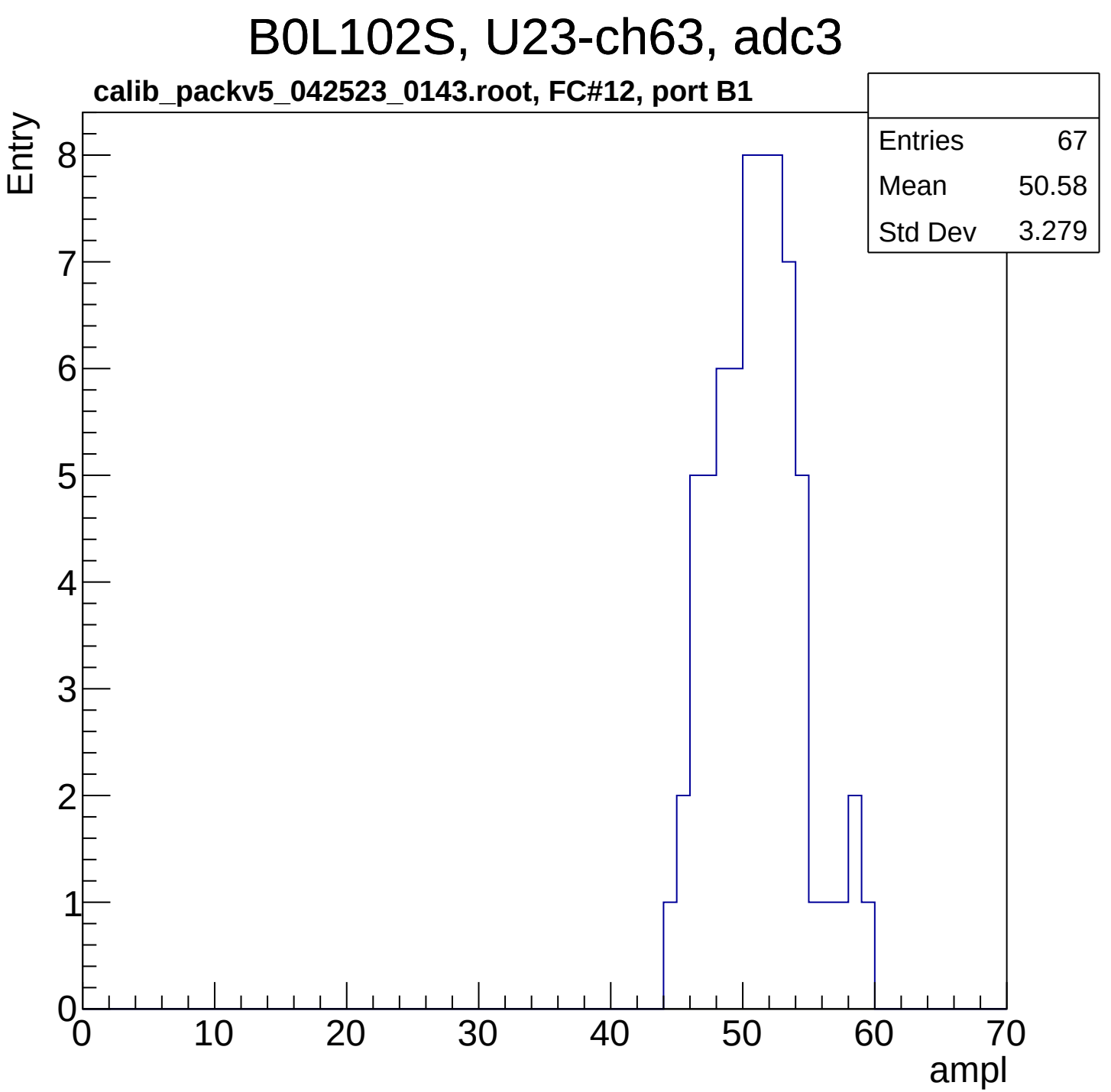
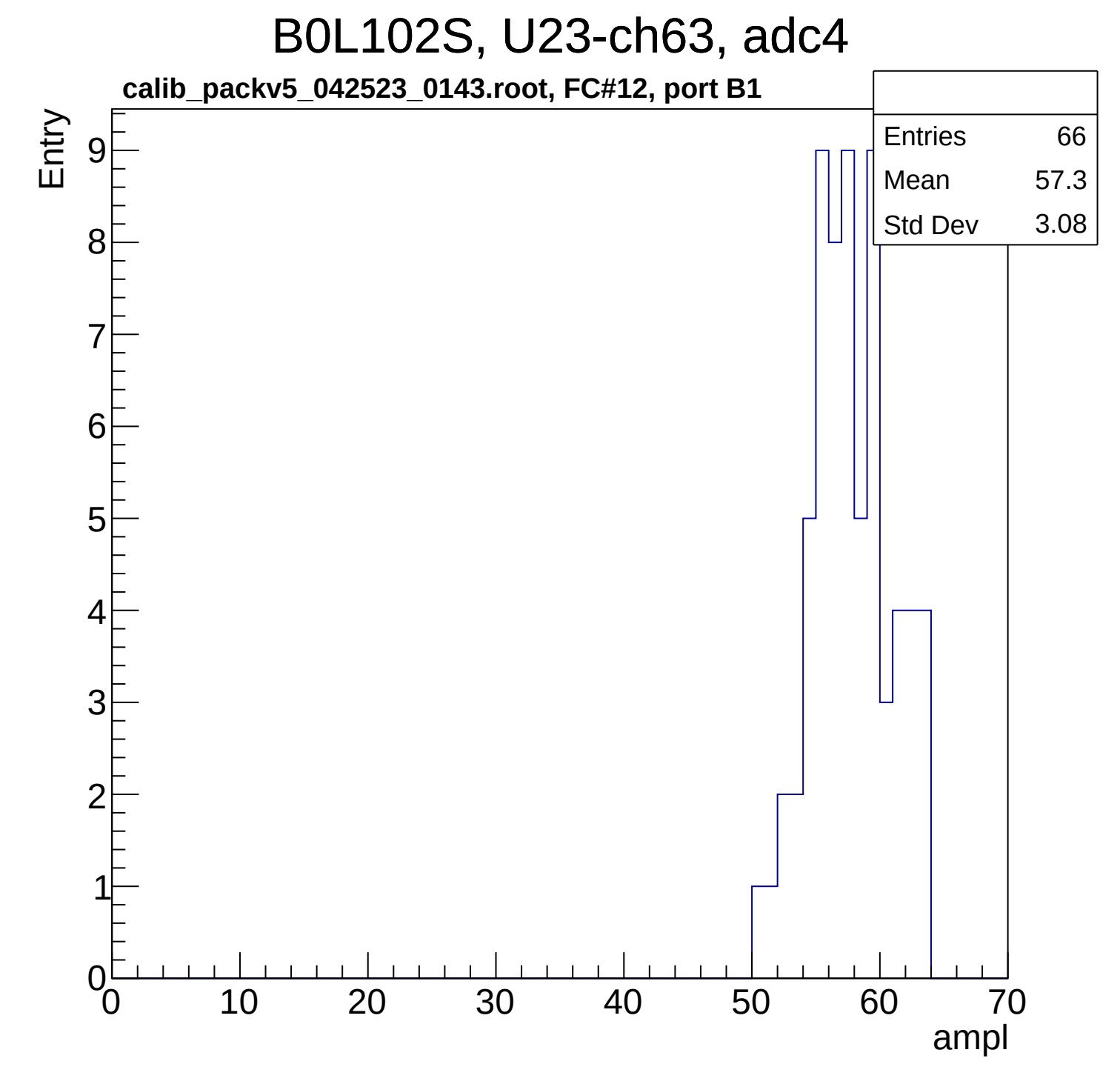
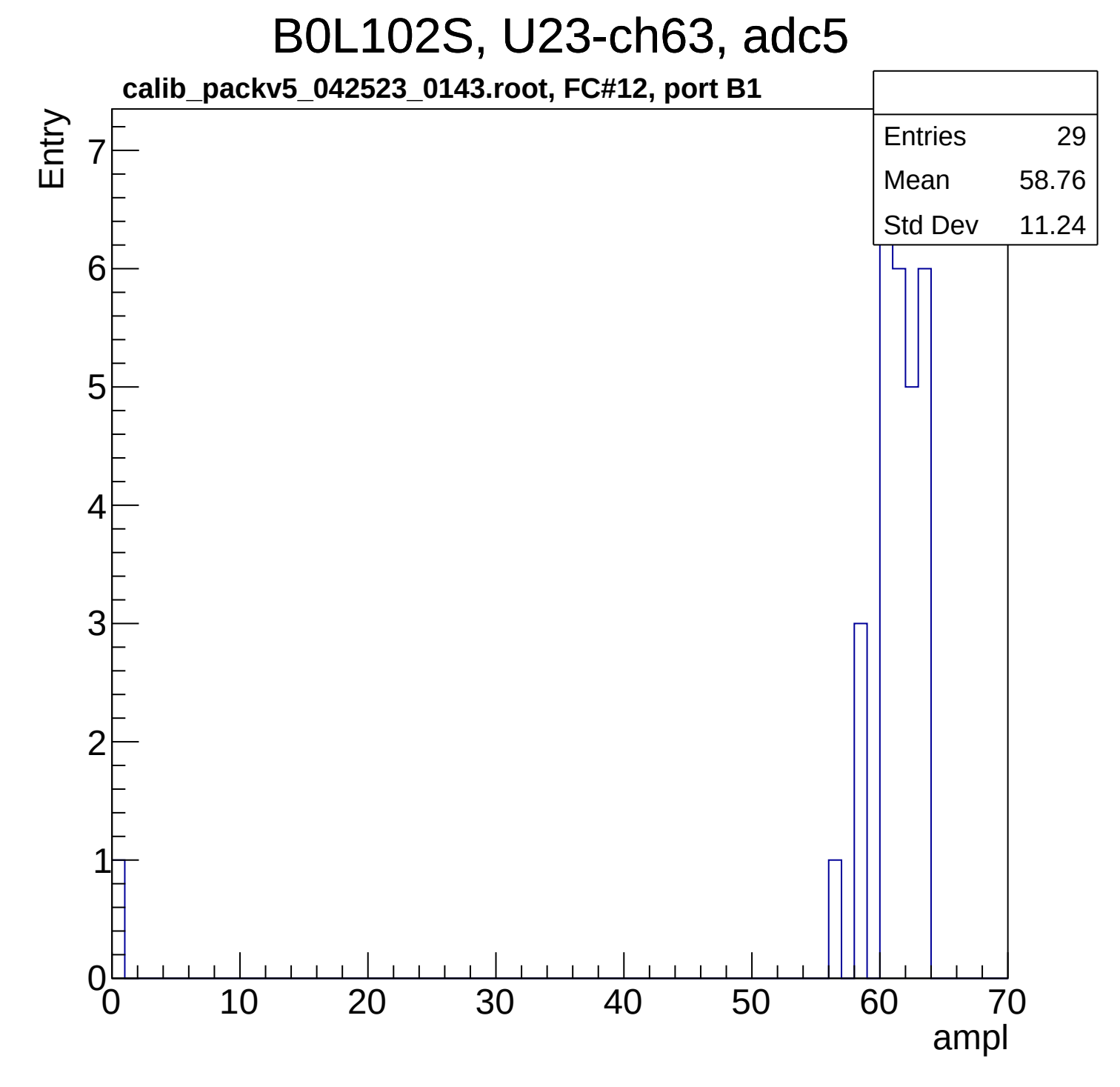
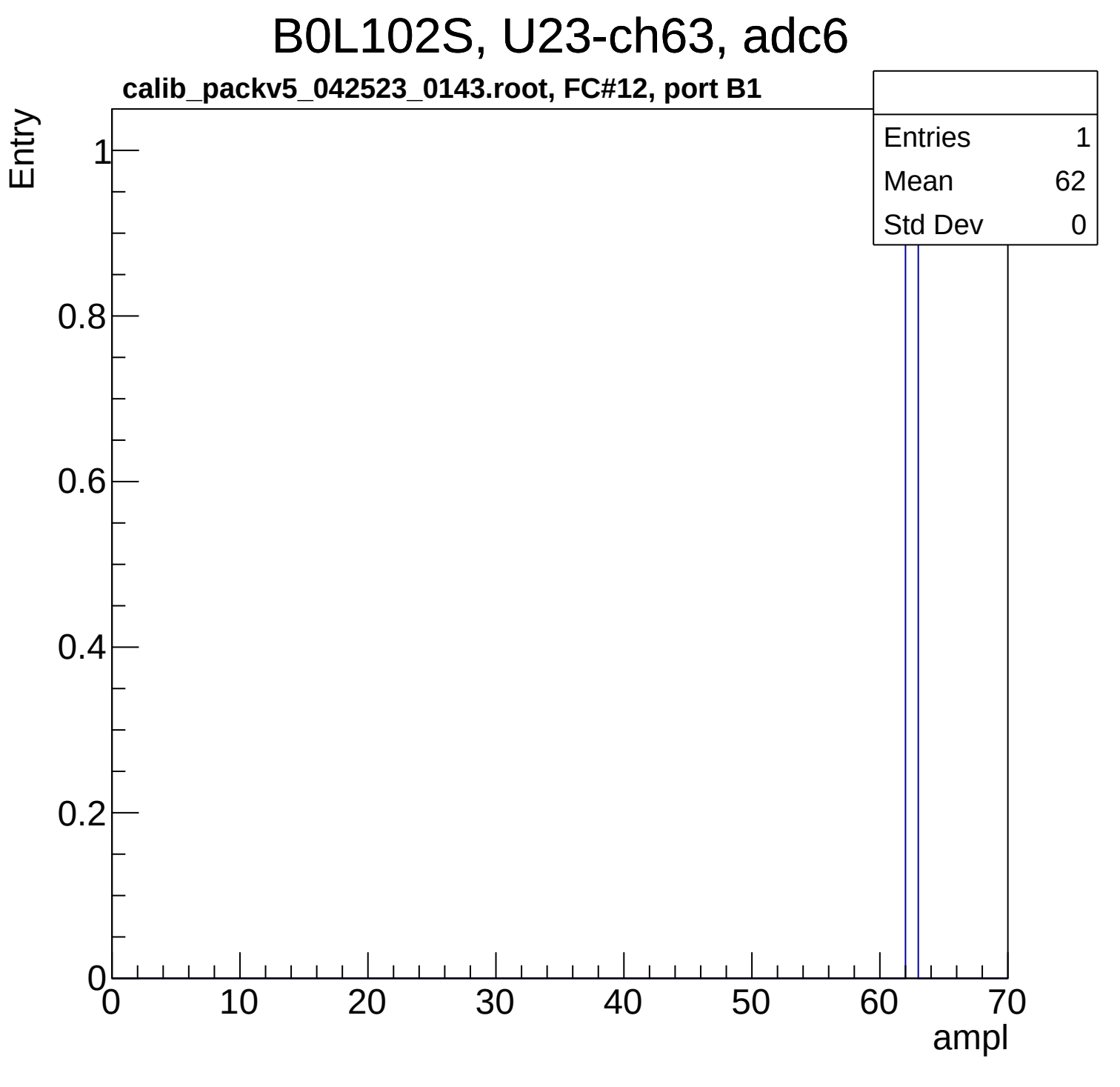
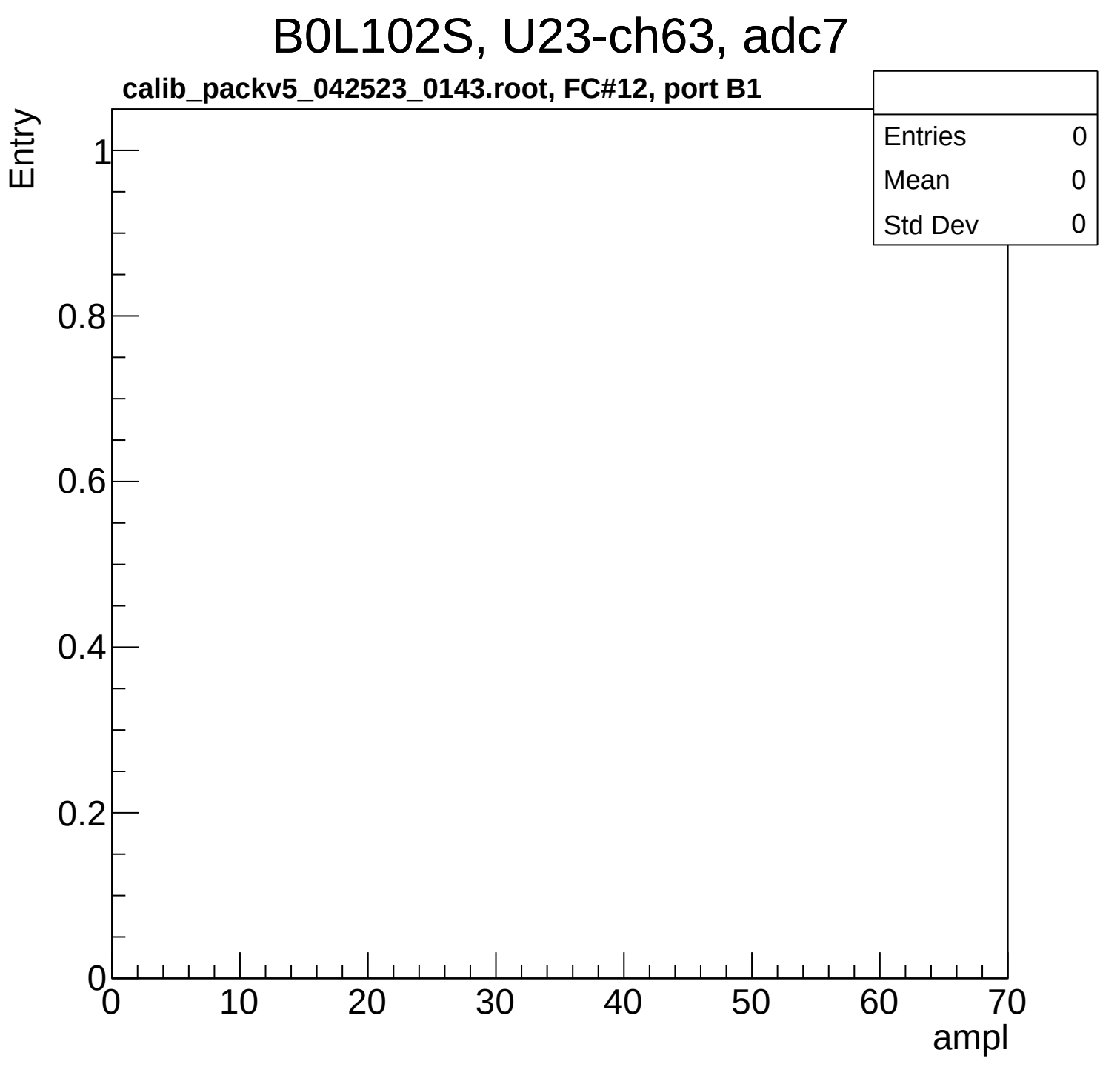

B0L102S, U23-ch63, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 44.38
Std Dev 3.256
Gaus mean : 44.6286
Gaus Width: 3.4794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

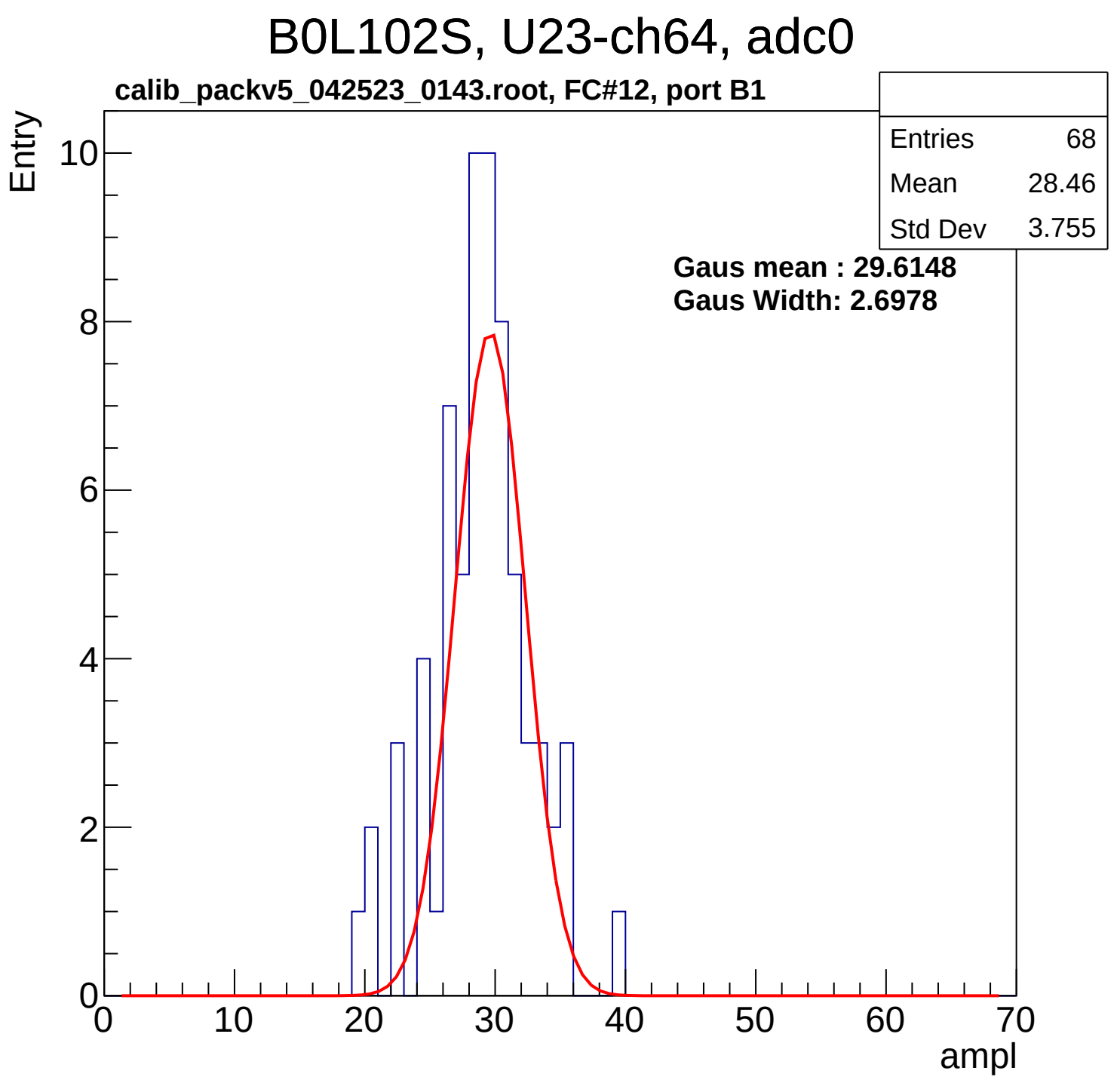

B0L102S, U23-ch63, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 50.58
Std Dev 3.279
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

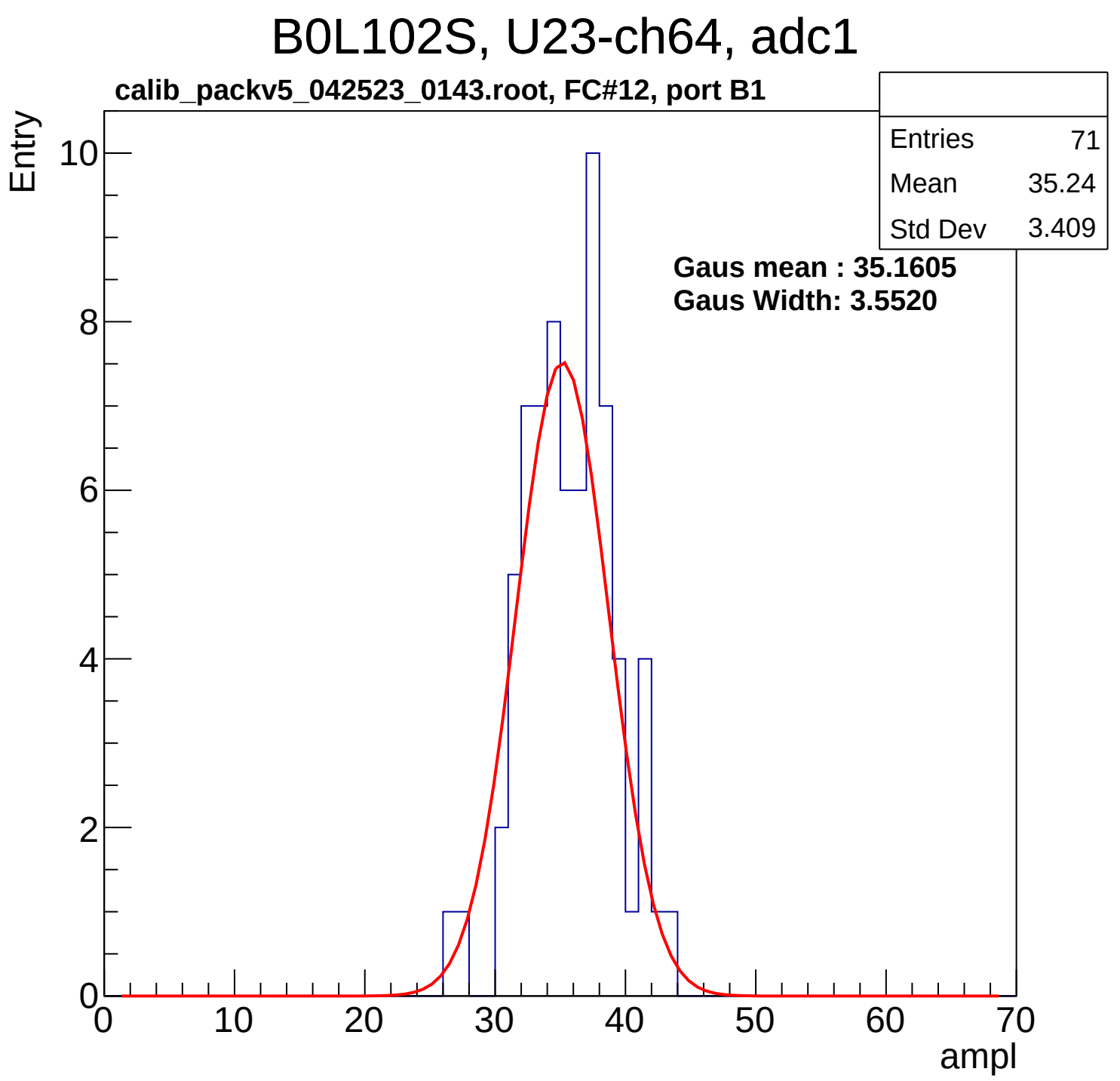

B0L102S, U23-ch63, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 57.3
Std Dev 3.08
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

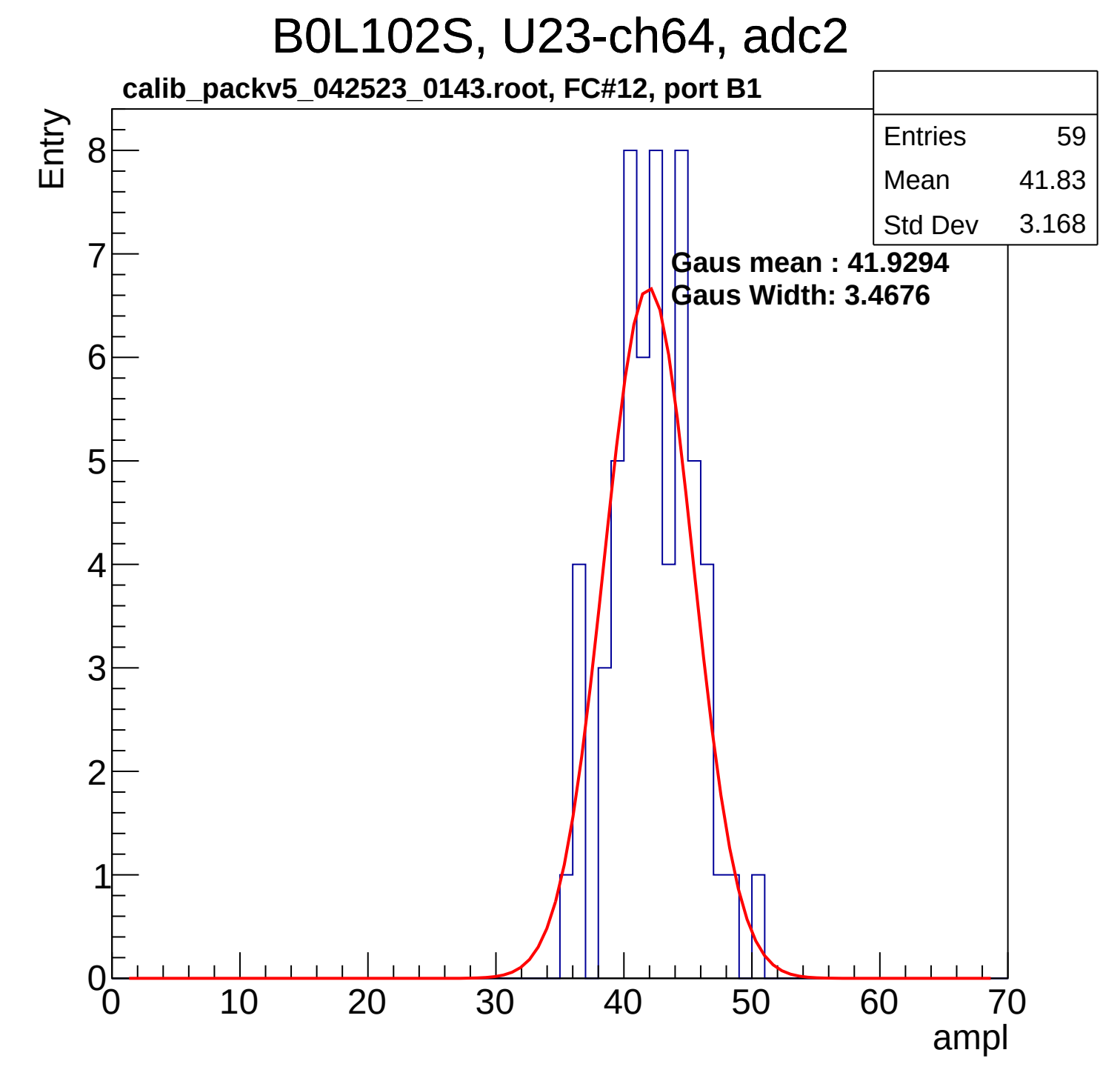

B0L102S, U23-ch63, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 29
Mean 58.76
Std Dev 11.24
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

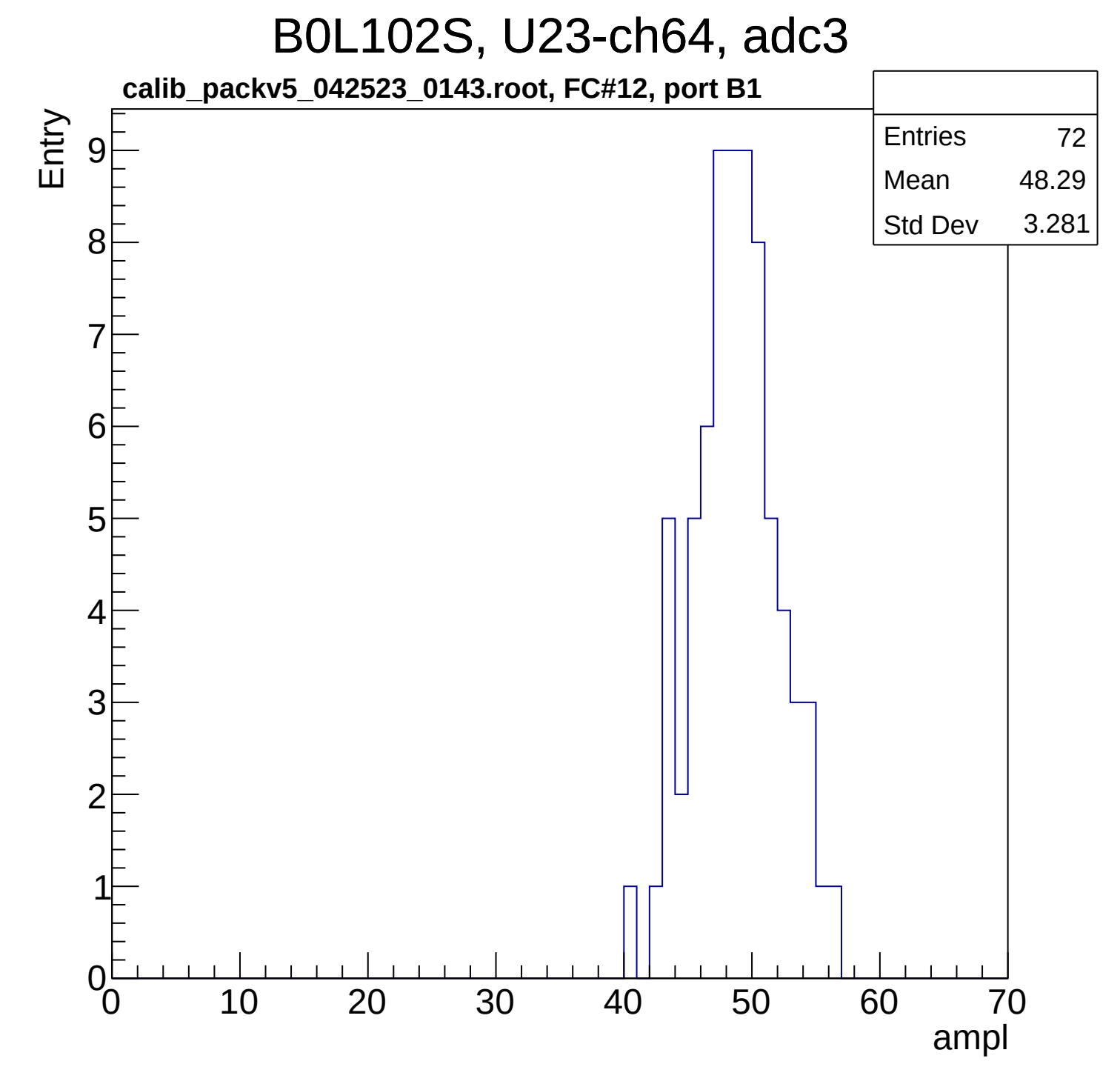

B0L102S, U23-ch63, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch63, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch64, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 28.46
Std Dev 3.755
Gaus mean : 29.6148
Gaus Width: 2.6978
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch64, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 35.24
Std Dev 3.409
Gaus mean : 35.1605
Gaus Width: 3.5520
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch64, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 41.83
Std Dev 3.168
Gaus mean : 41.9294
Gaus Width: 3.4676
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch64, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 48.29
Std Dev 3.281
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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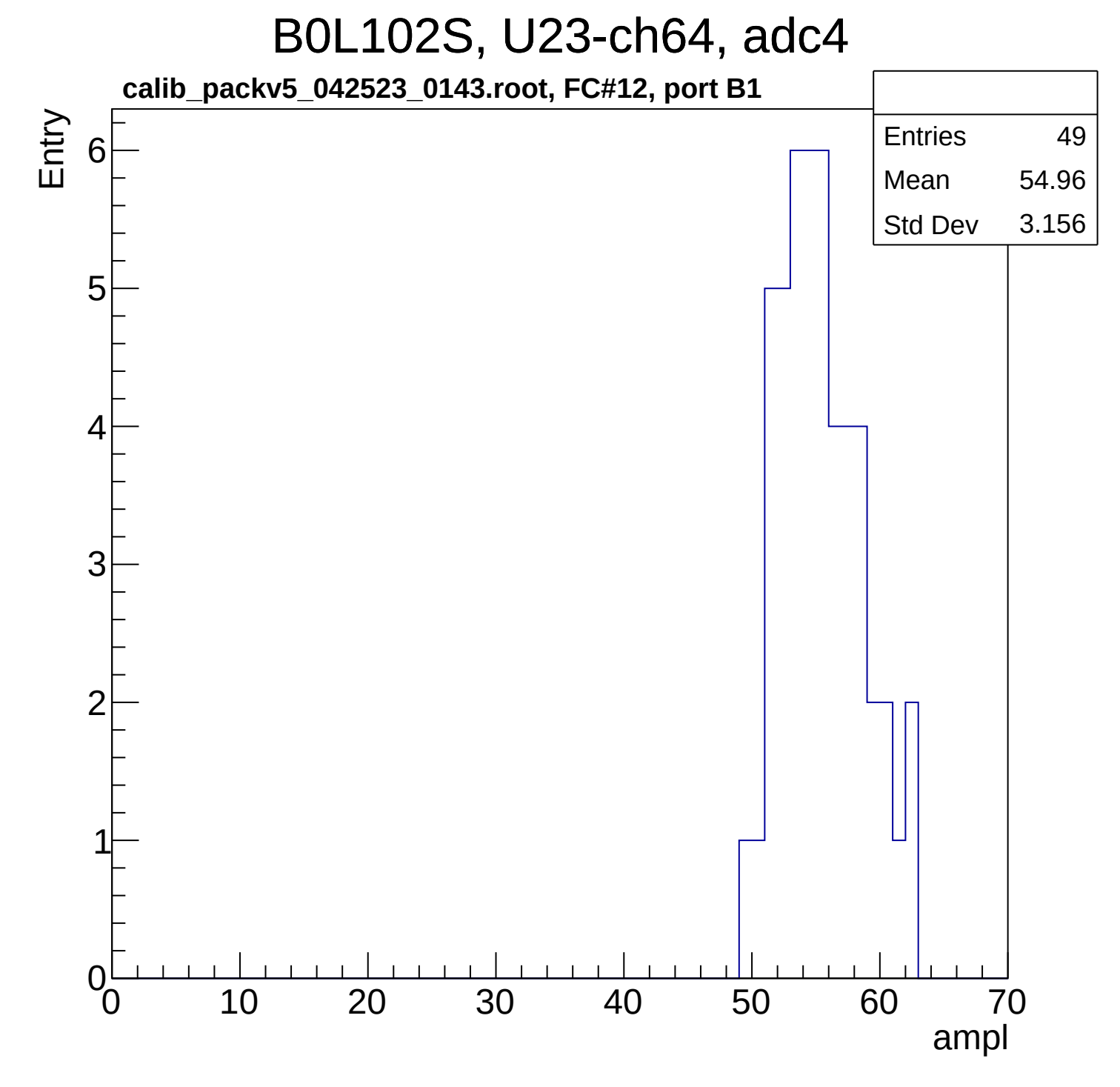
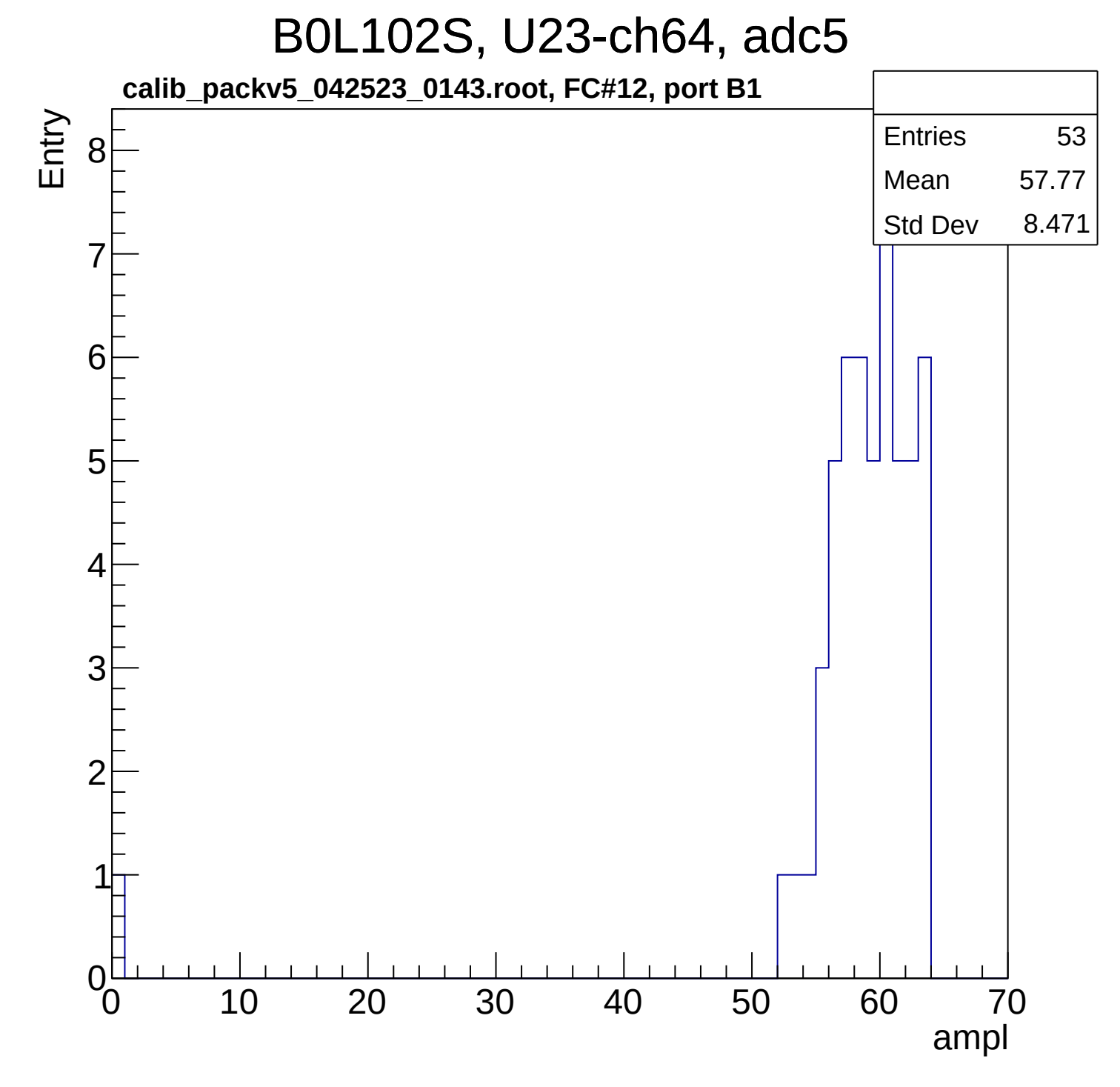
60
70


B0L102S, U23-ch64, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 54.96
Std Dev 3.156
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch64, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 57.77
Std Dev 8.471
Entry
ampl
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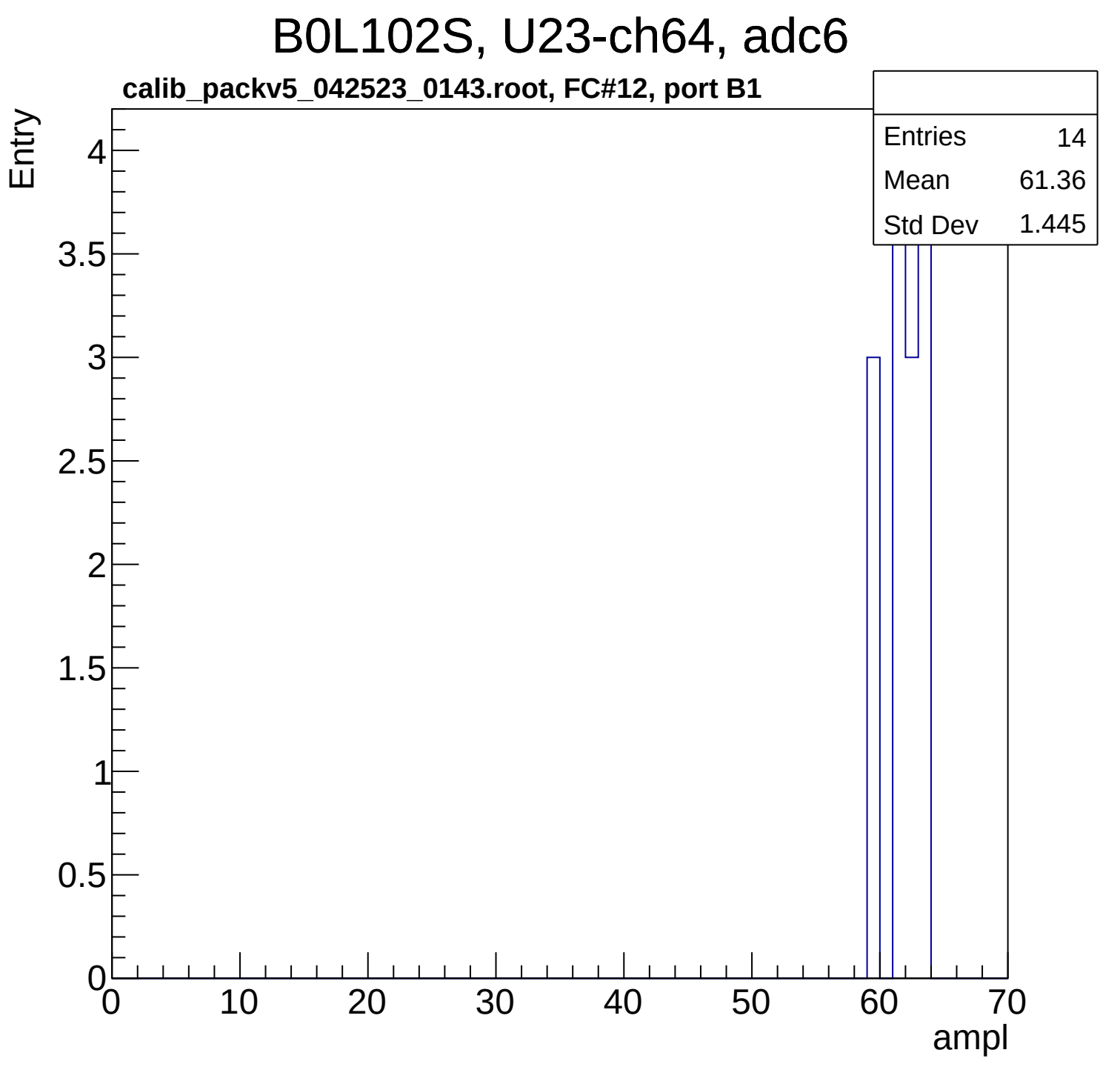
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch64, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61.36
Std Dev 1.445
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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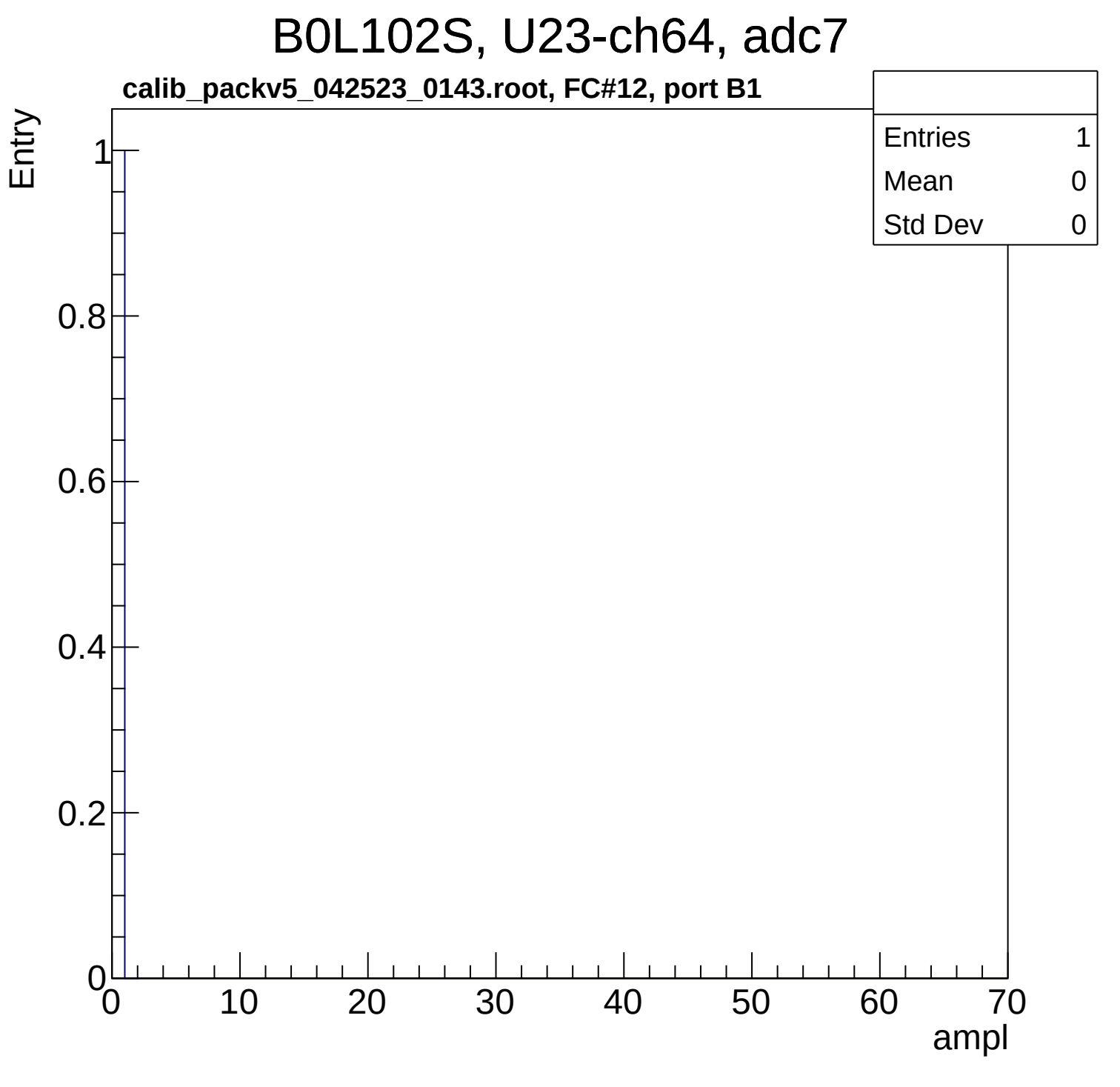
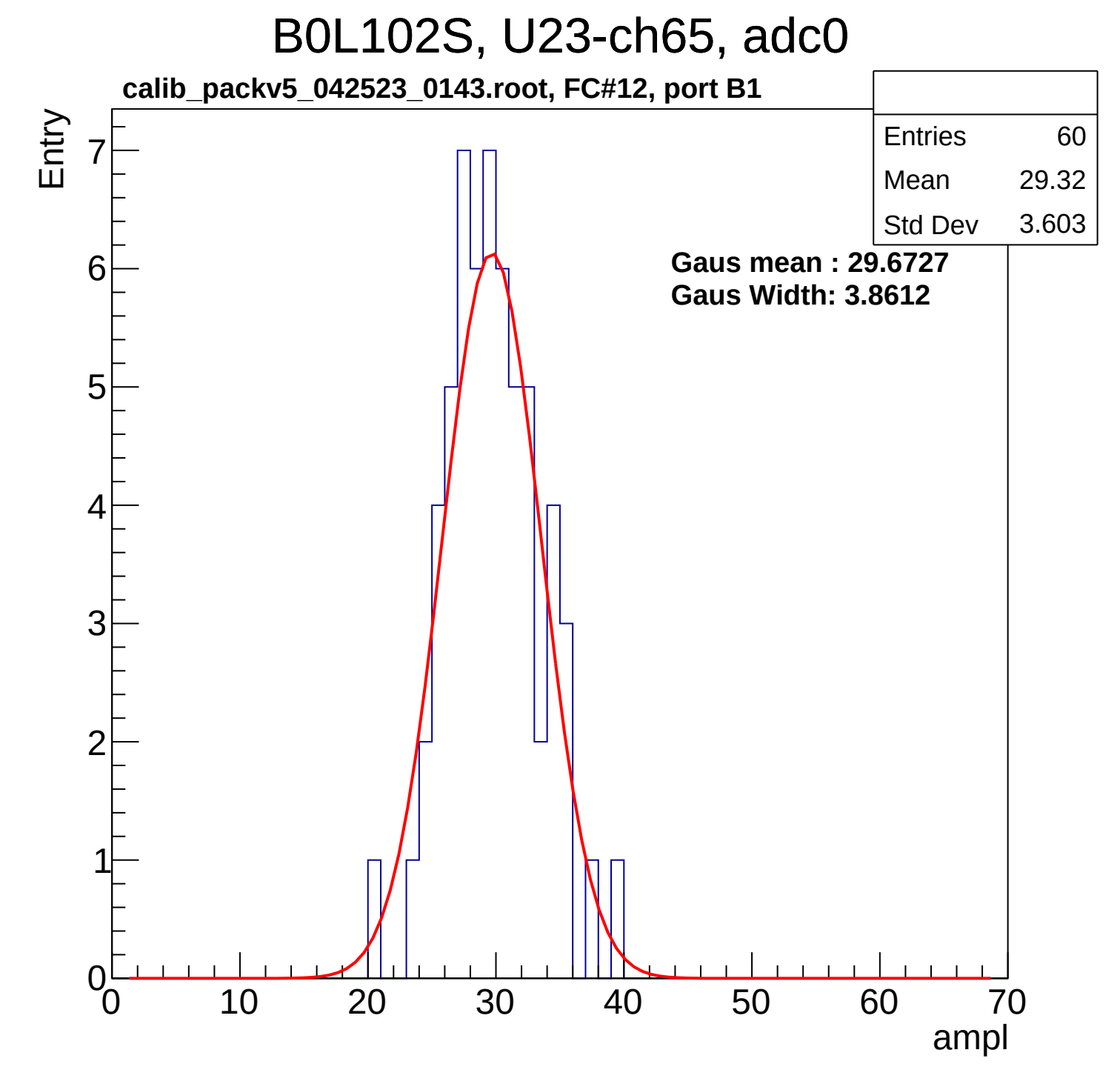
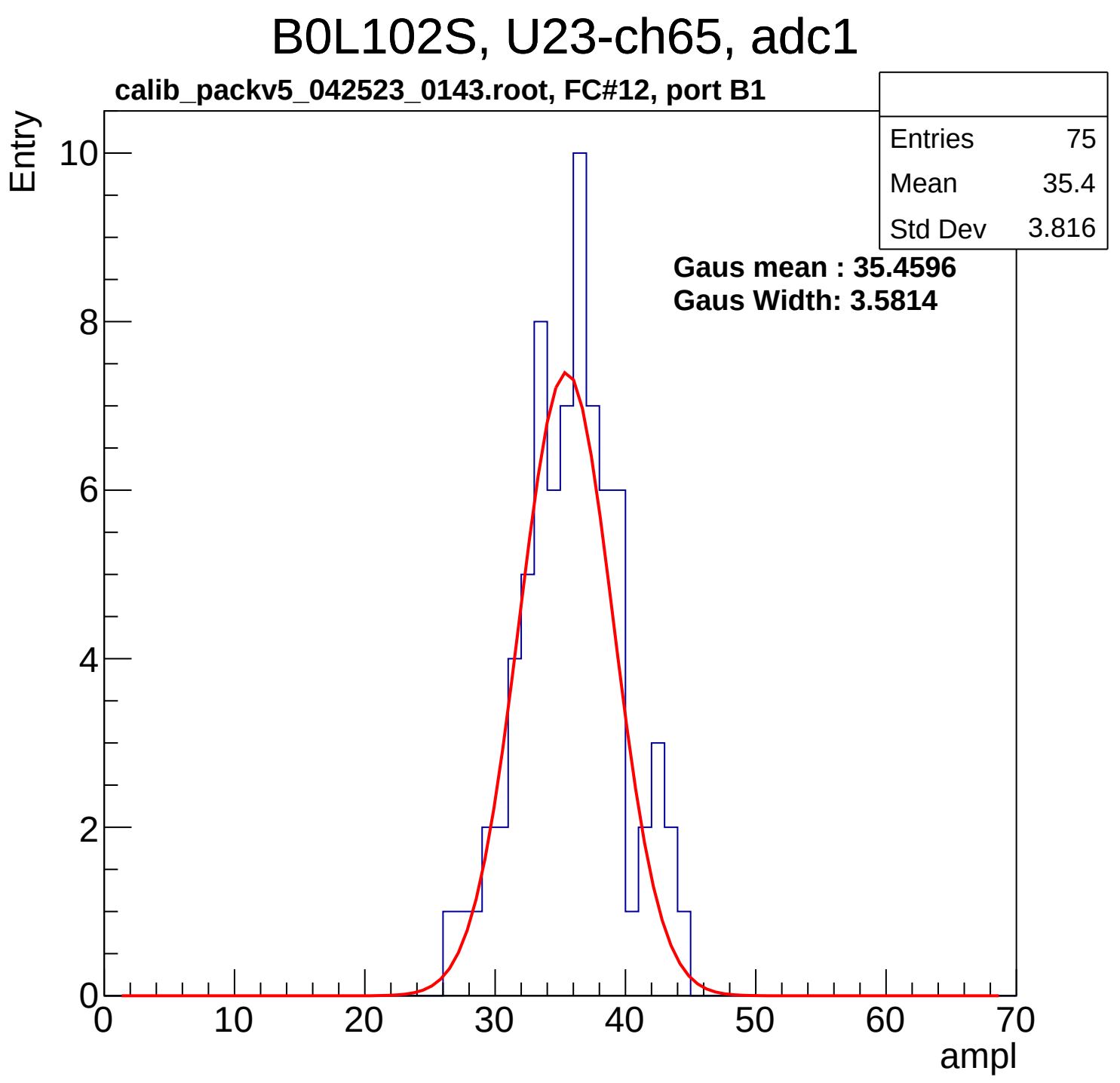
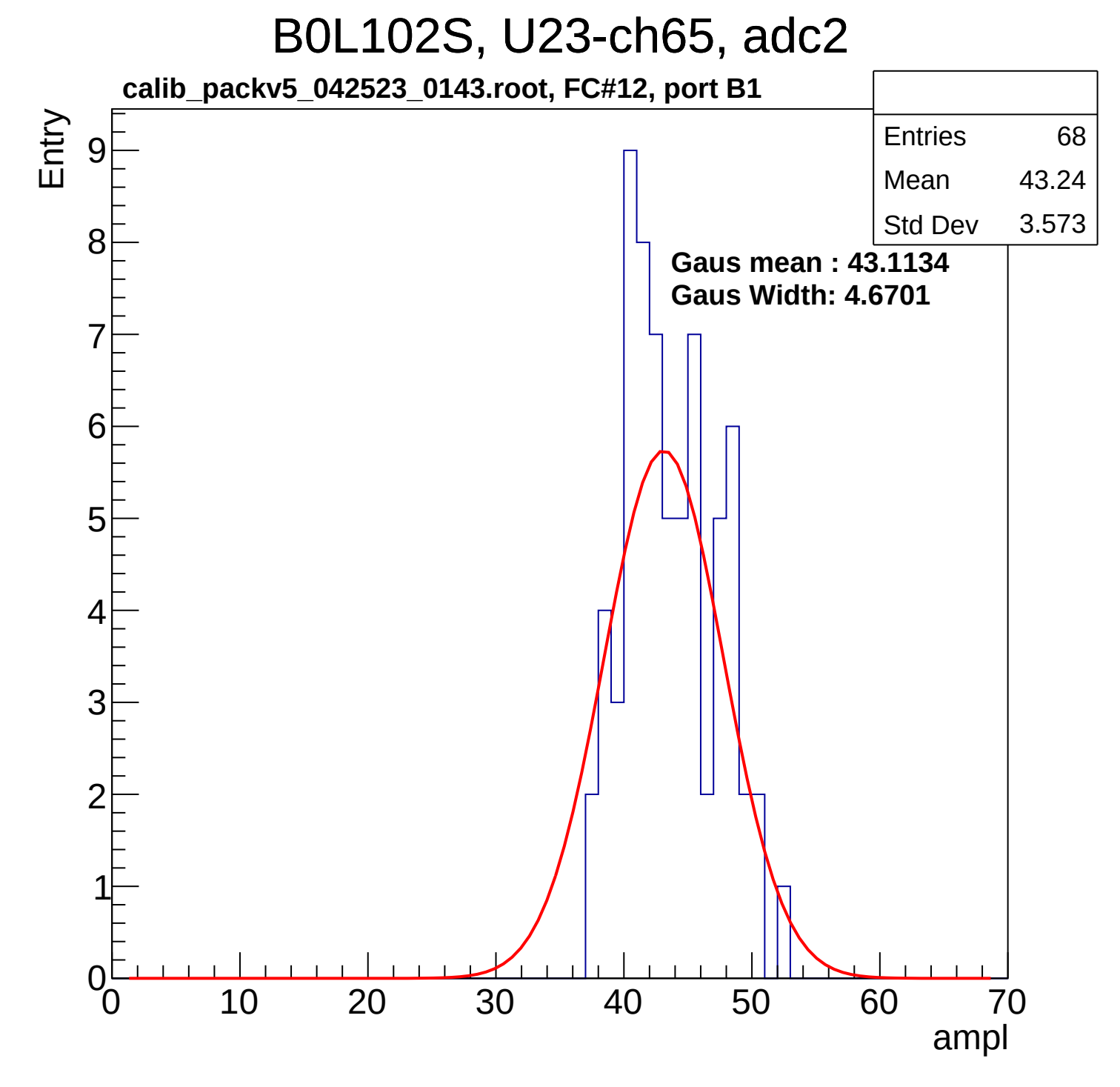
40
50
60
70


B0L102S, U23-ch64, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch65, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 29.32
Std Dev 3.603
Gaus mean : 29.6727
Gaus Width: 3.8612
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch65, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 35.4
Std Dev 3.816
Gaus mean : 35.4596
Gaus Width: 3.5814
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch65, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 43.24
Std Dev 3.573
Gaus mean : 43.1134
Gaus Width: 4.6701
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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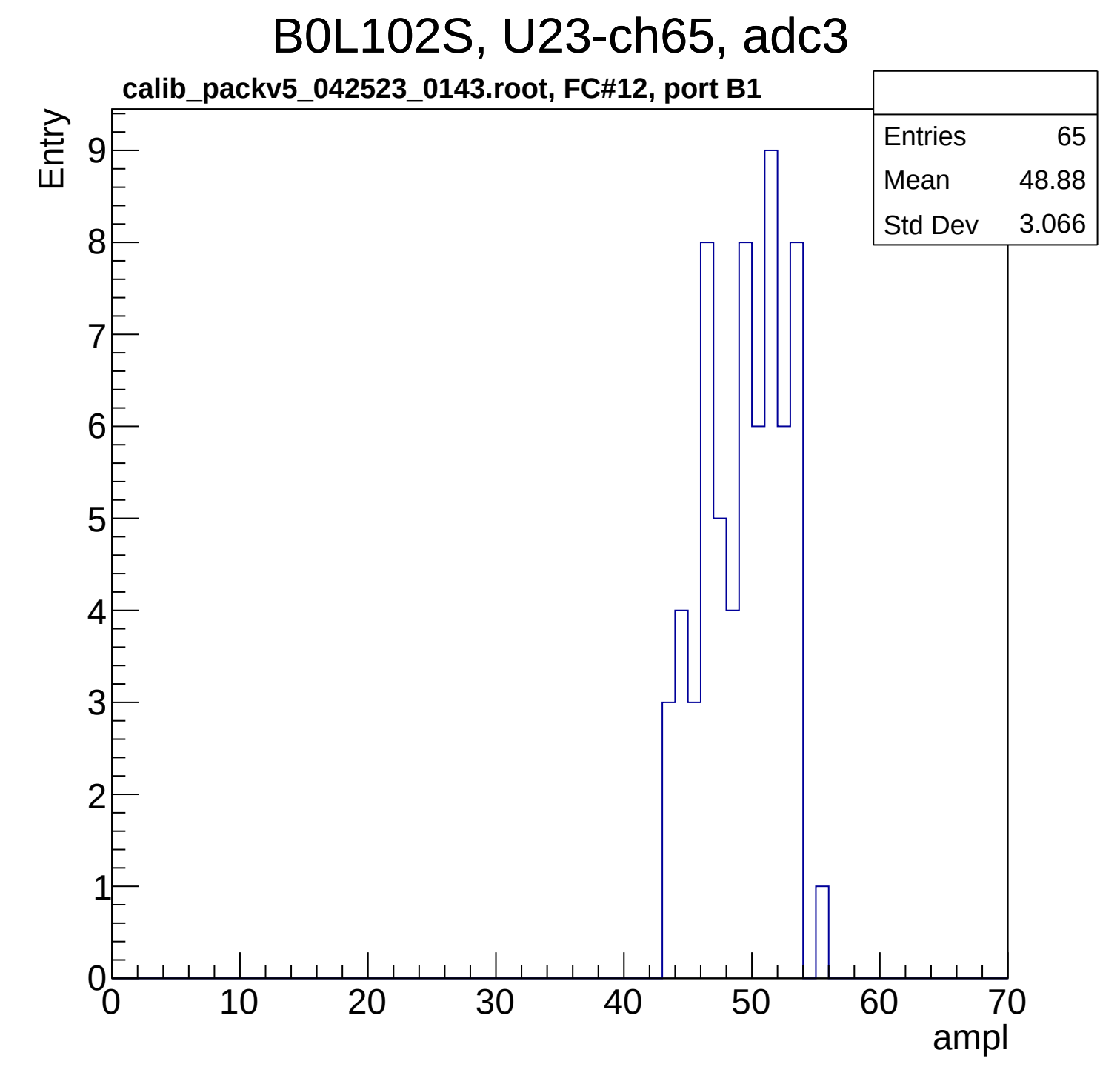
B0L102S, U23-ch65, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.88
Std Dev 3.066
Entry
ampl
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2
3
4
5
6
7
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9
0
10
20
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40
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60
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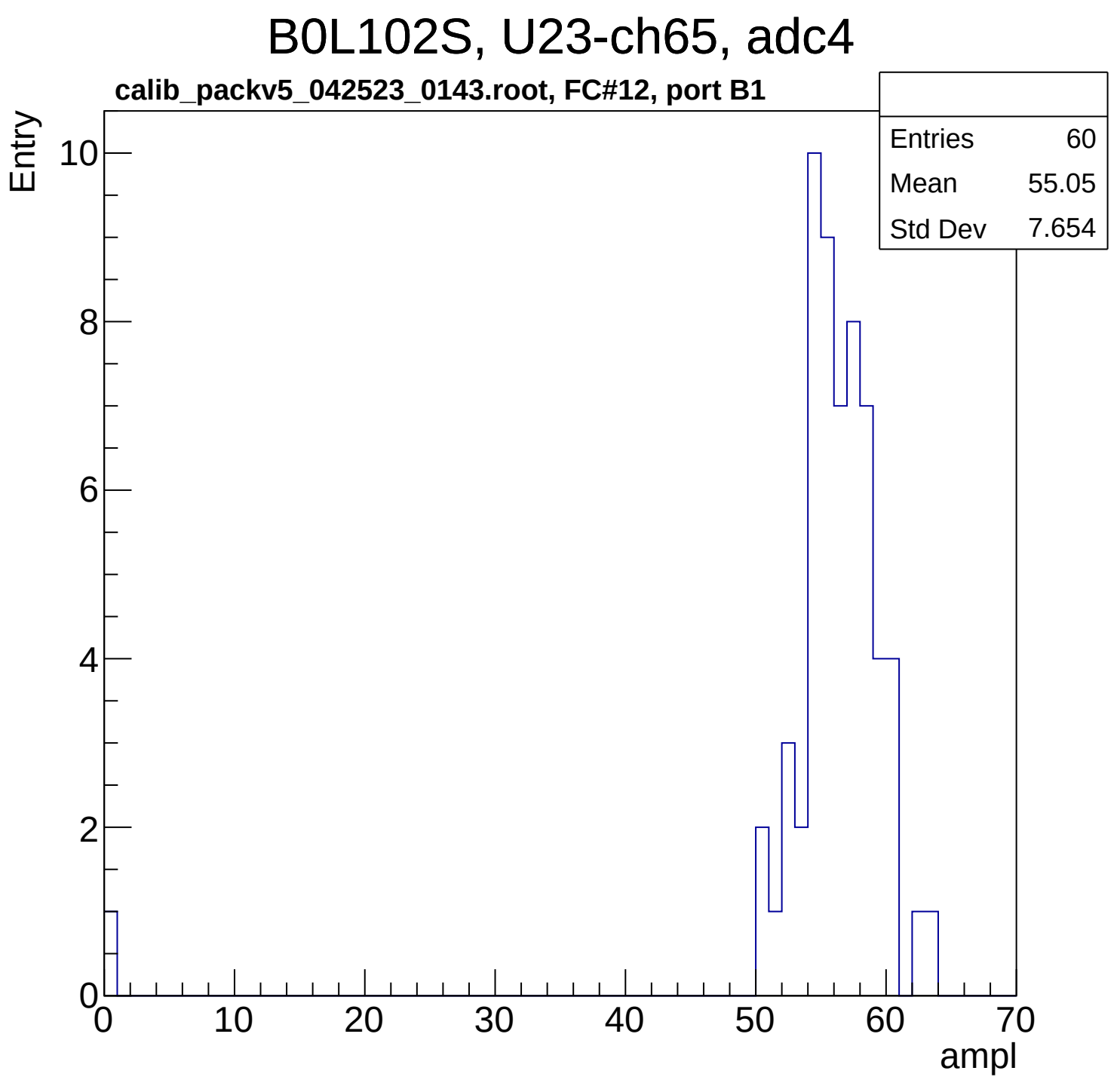
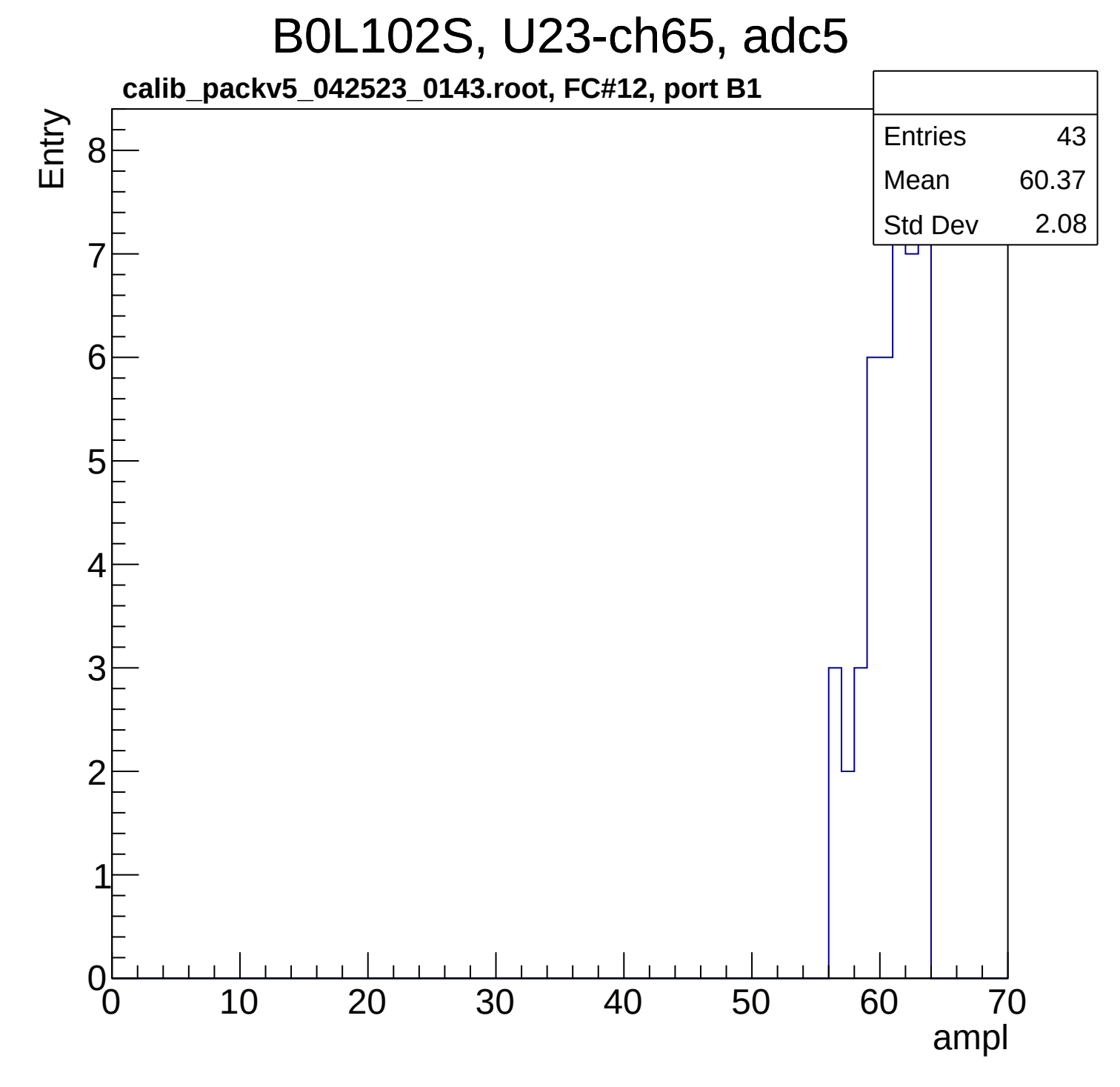
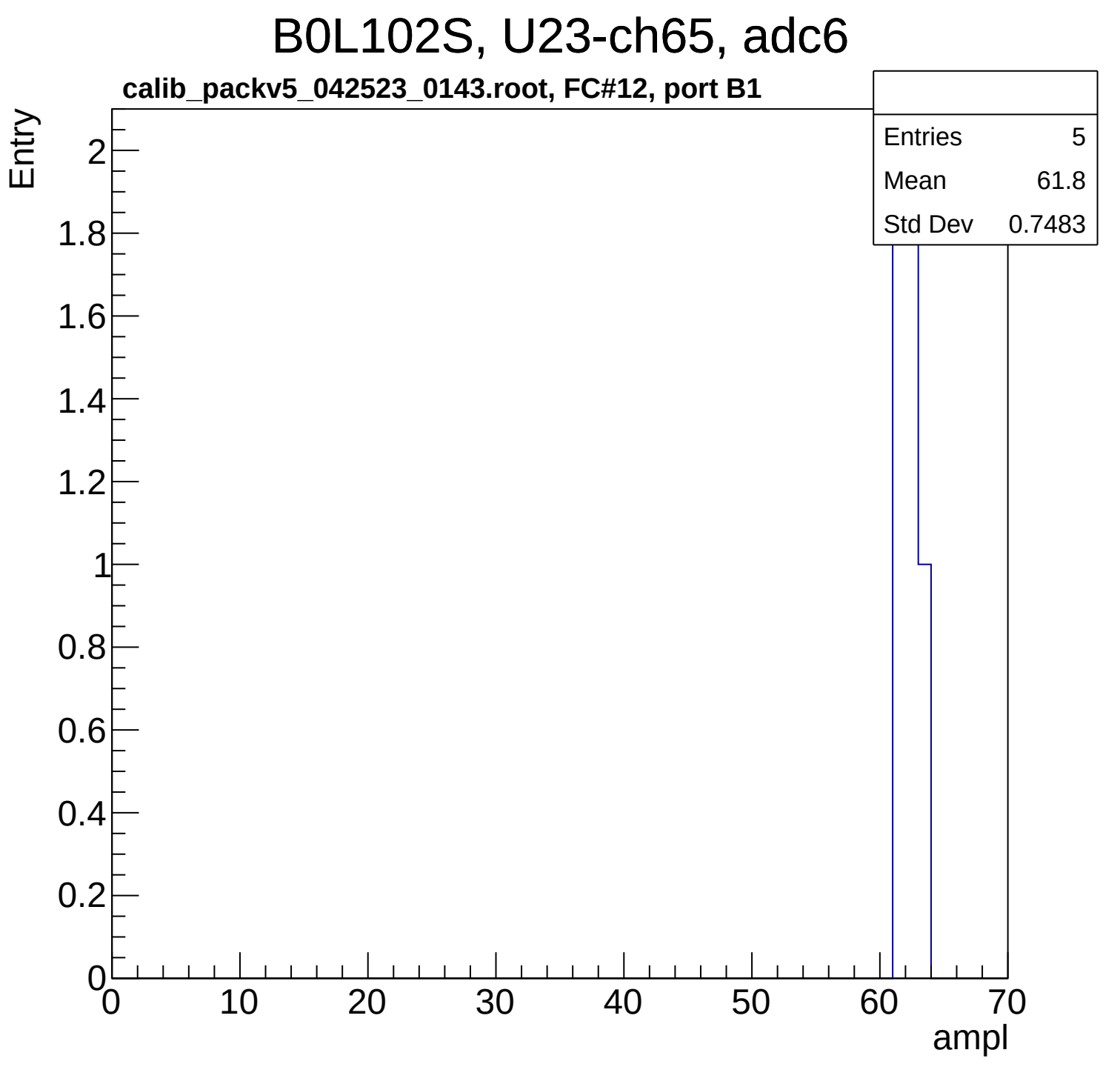

B0L102S, U23-ch65, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 55.05
Std Dev 7.654
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch65, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 60.37
Std Dev 2.08
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch65, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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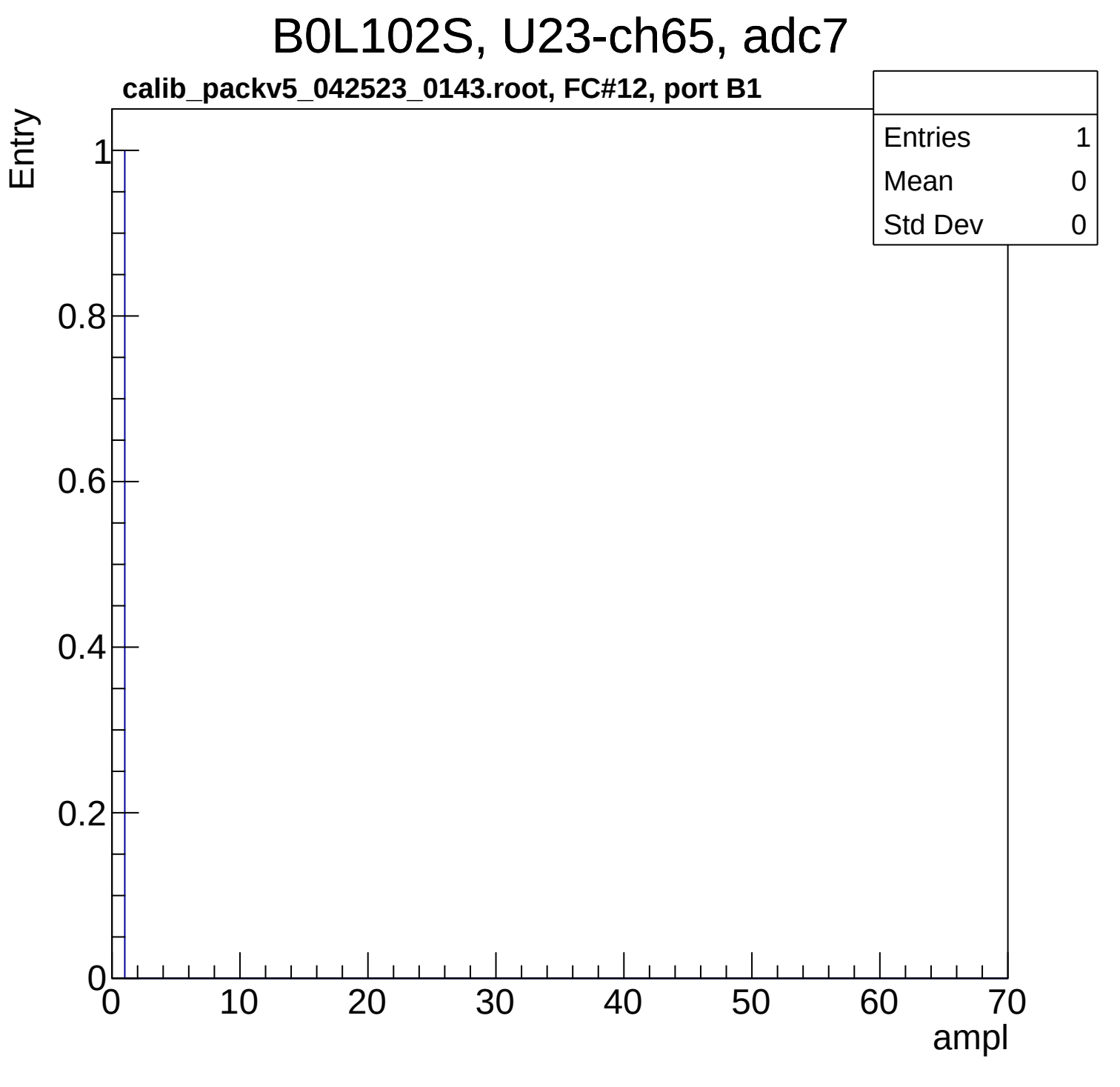
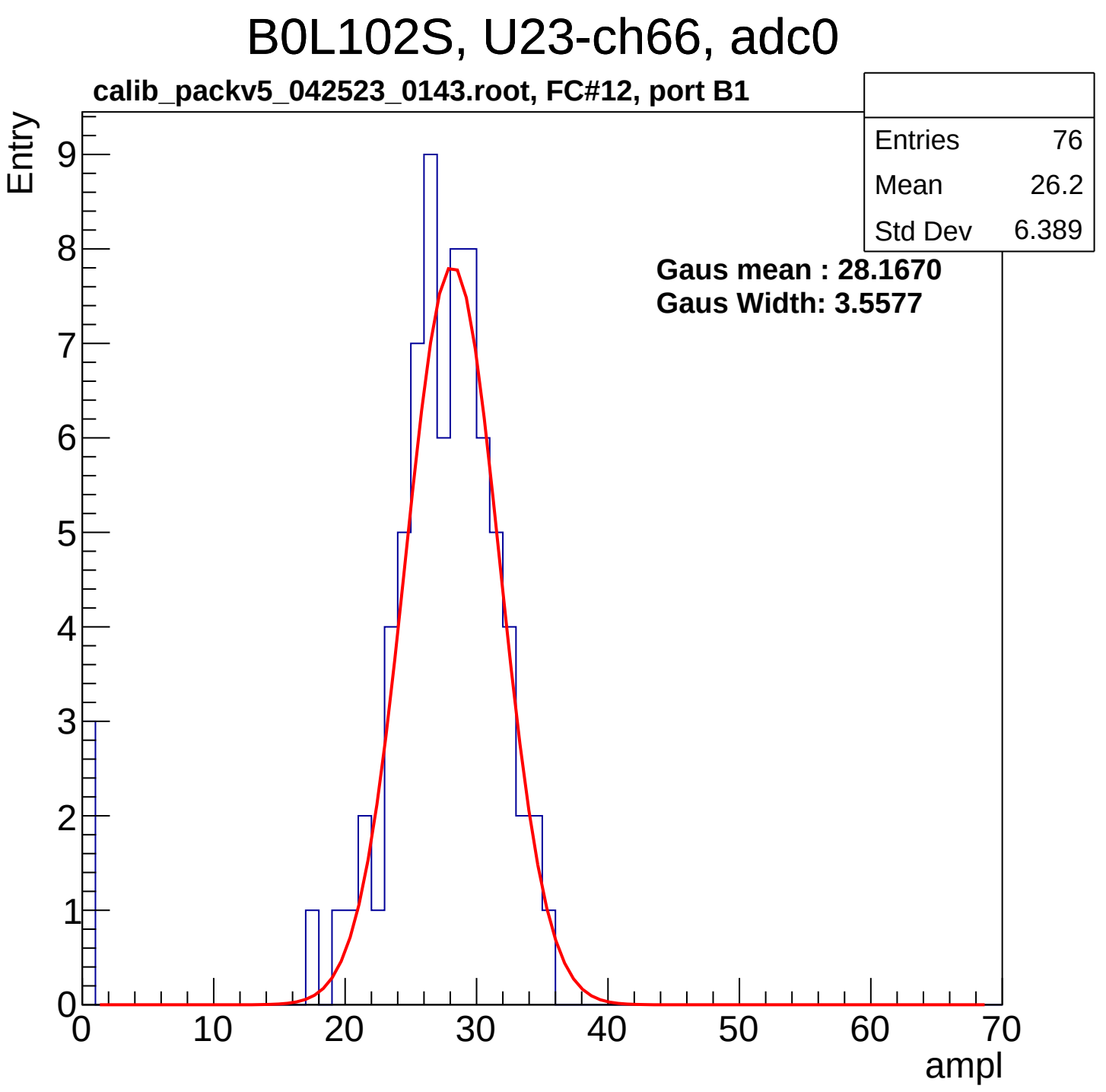
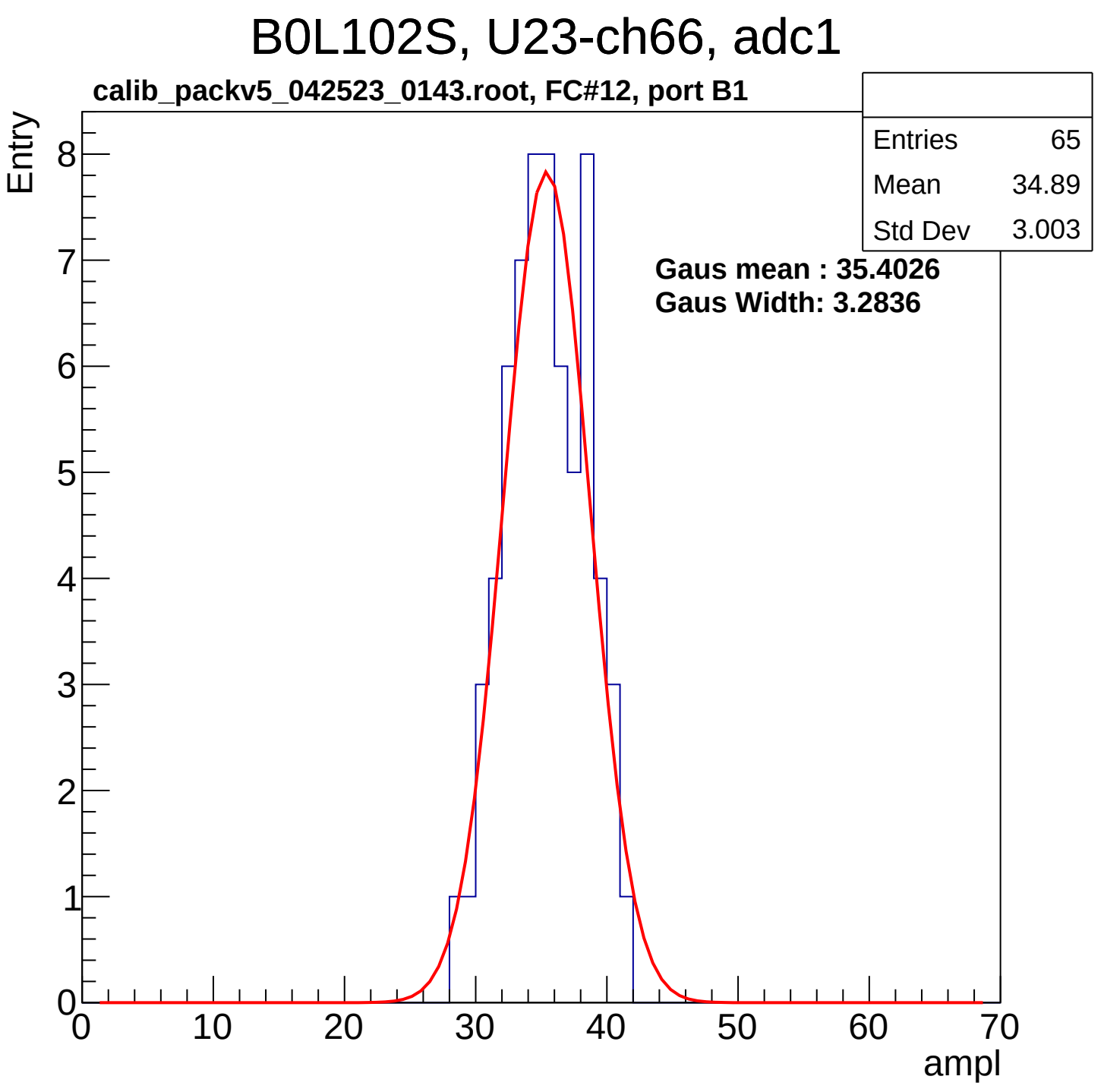
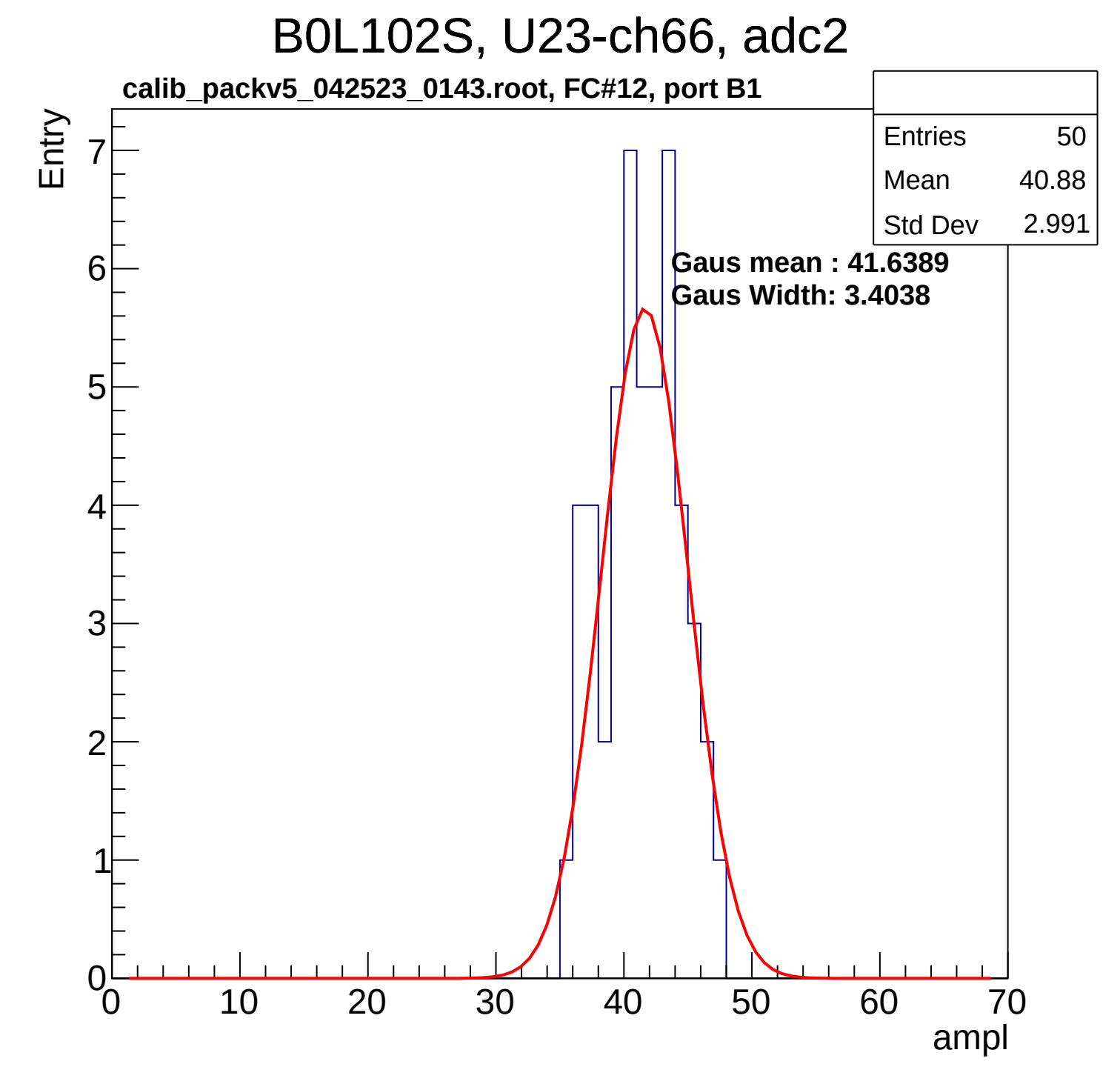

B0L102S, U23-ch65, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch66, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 26.2
Std Dev 6.389
Gaus mean : 28.1670
Gaus Width: 3.5577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch66, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 34.89
Std Dev 3.003
Gaus mean : 35.4026
Gaus Width: 3.2836
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch66, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 40.88
Std Dev 2.991
Gaus mean : 41.6389
Gaus Width: 3.4038
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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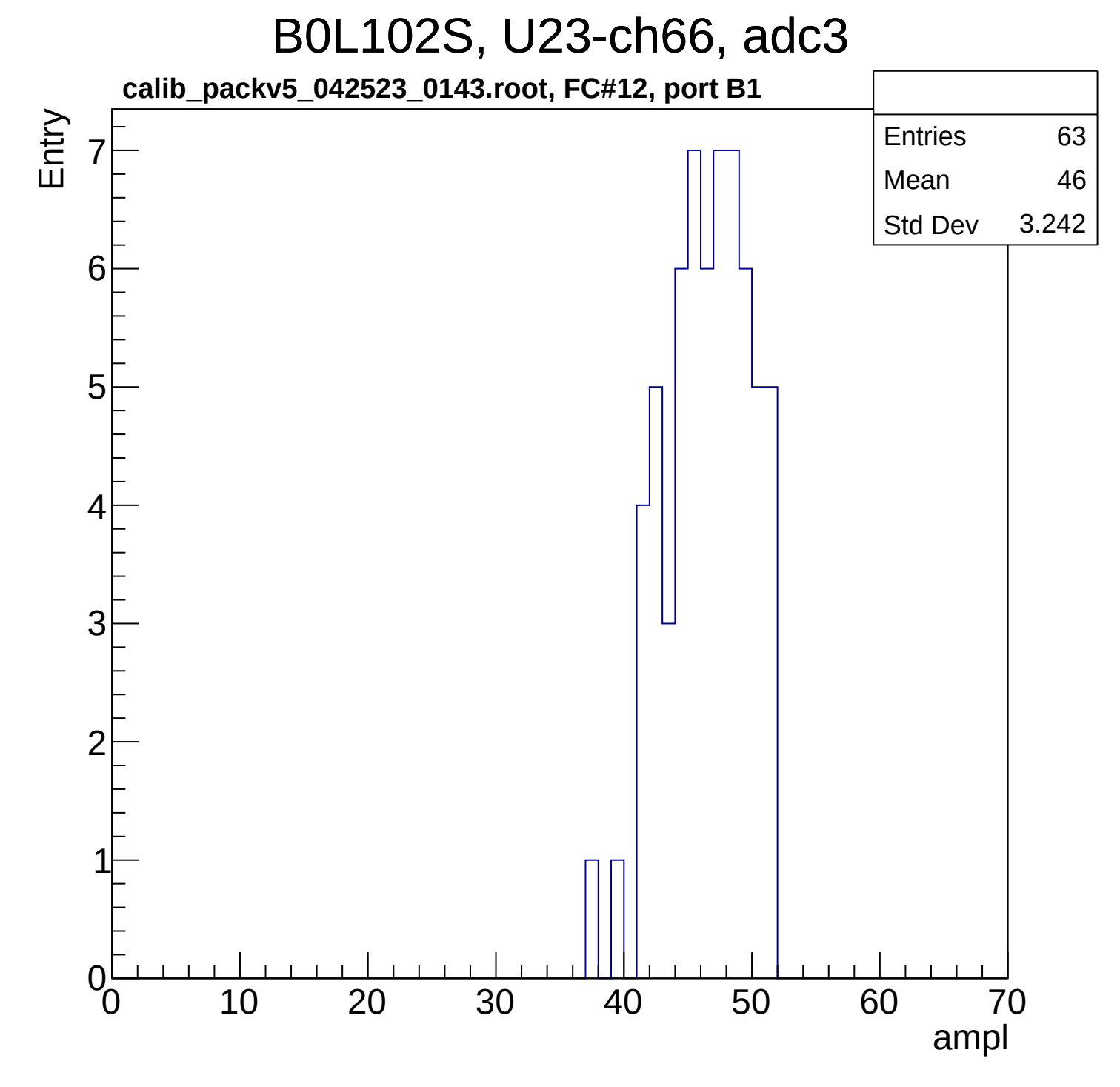
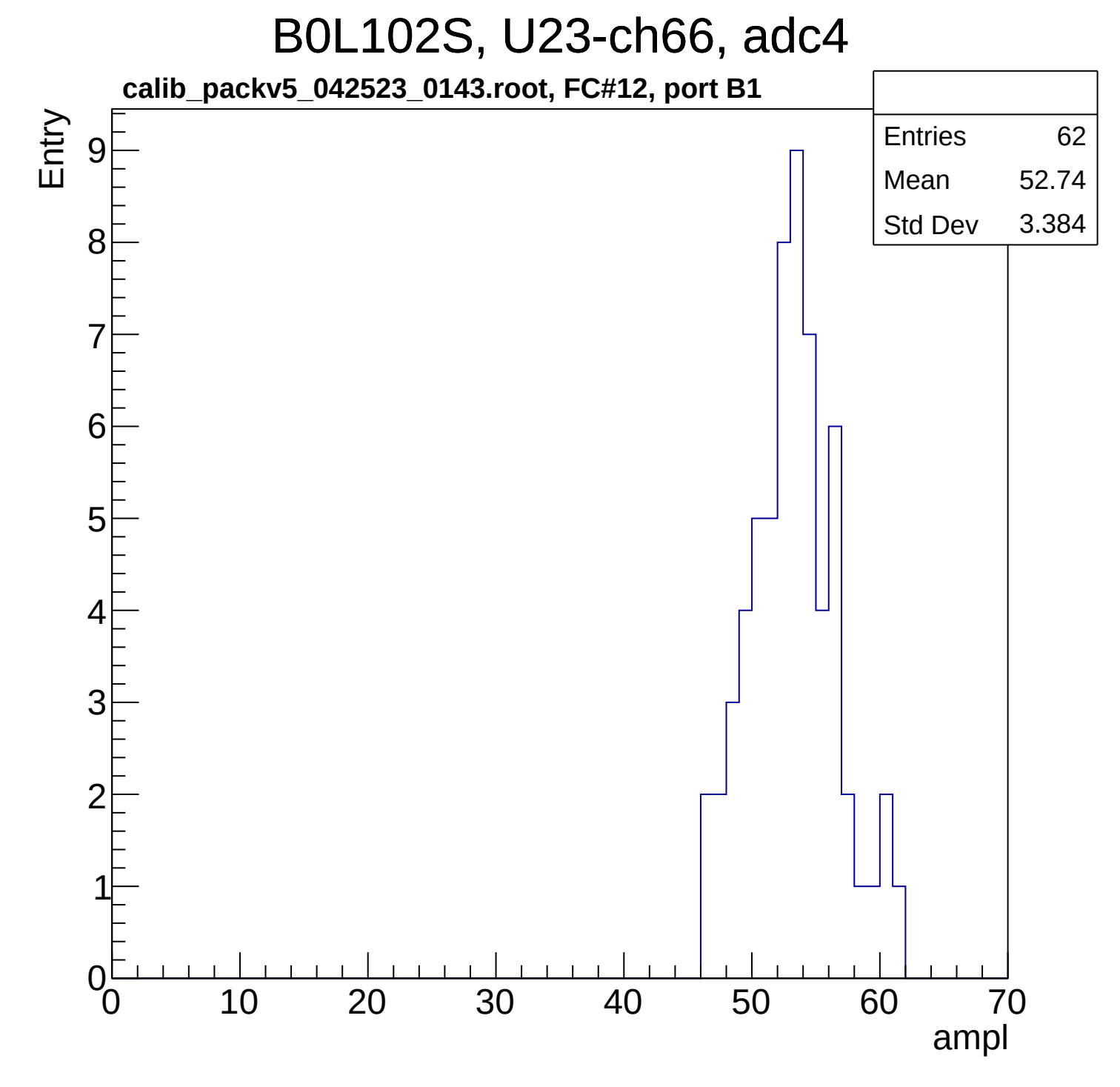

B0L102S, U23-ch66, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 46
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch66, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 52.74
Std Dev 3.384
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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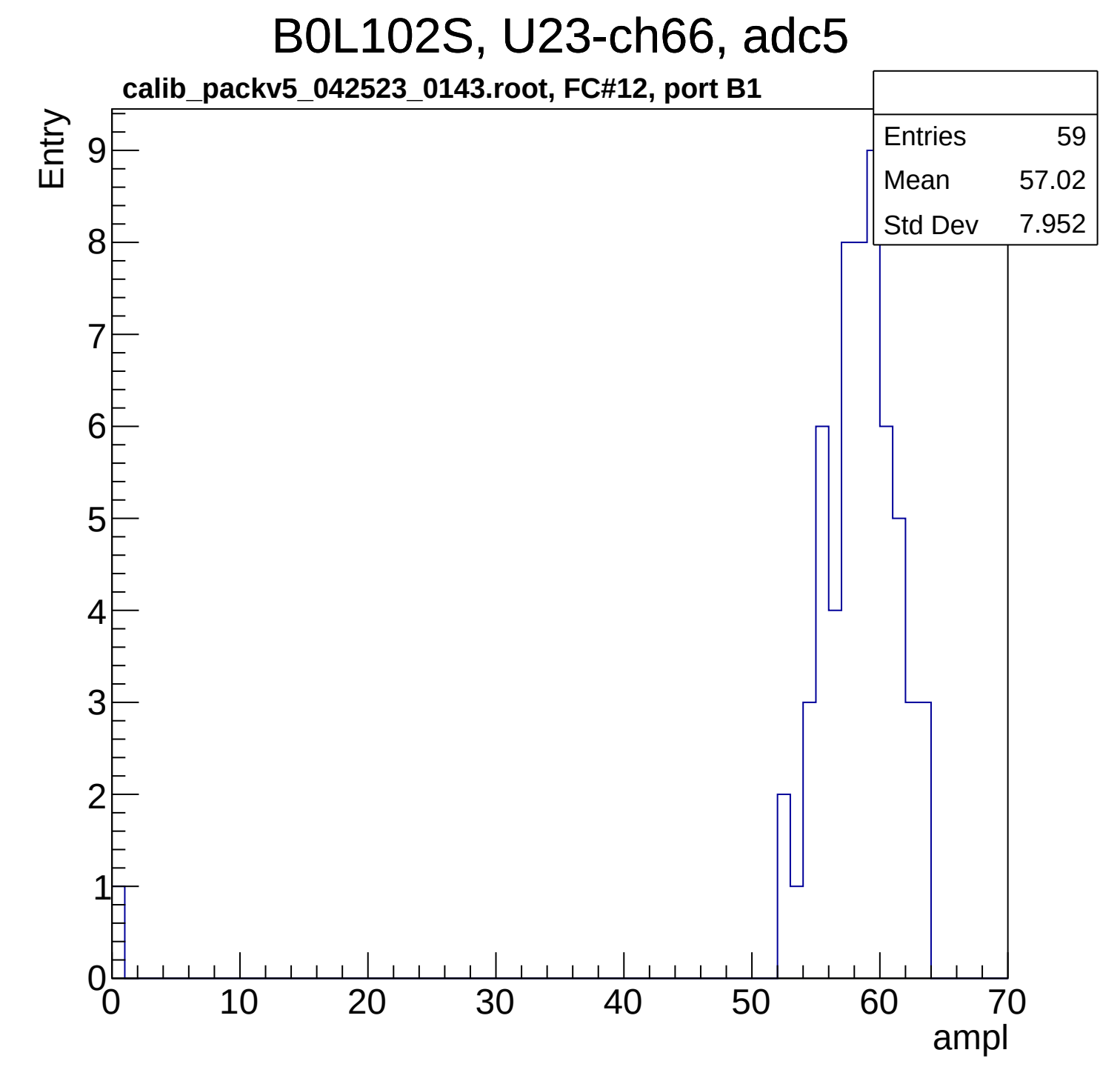
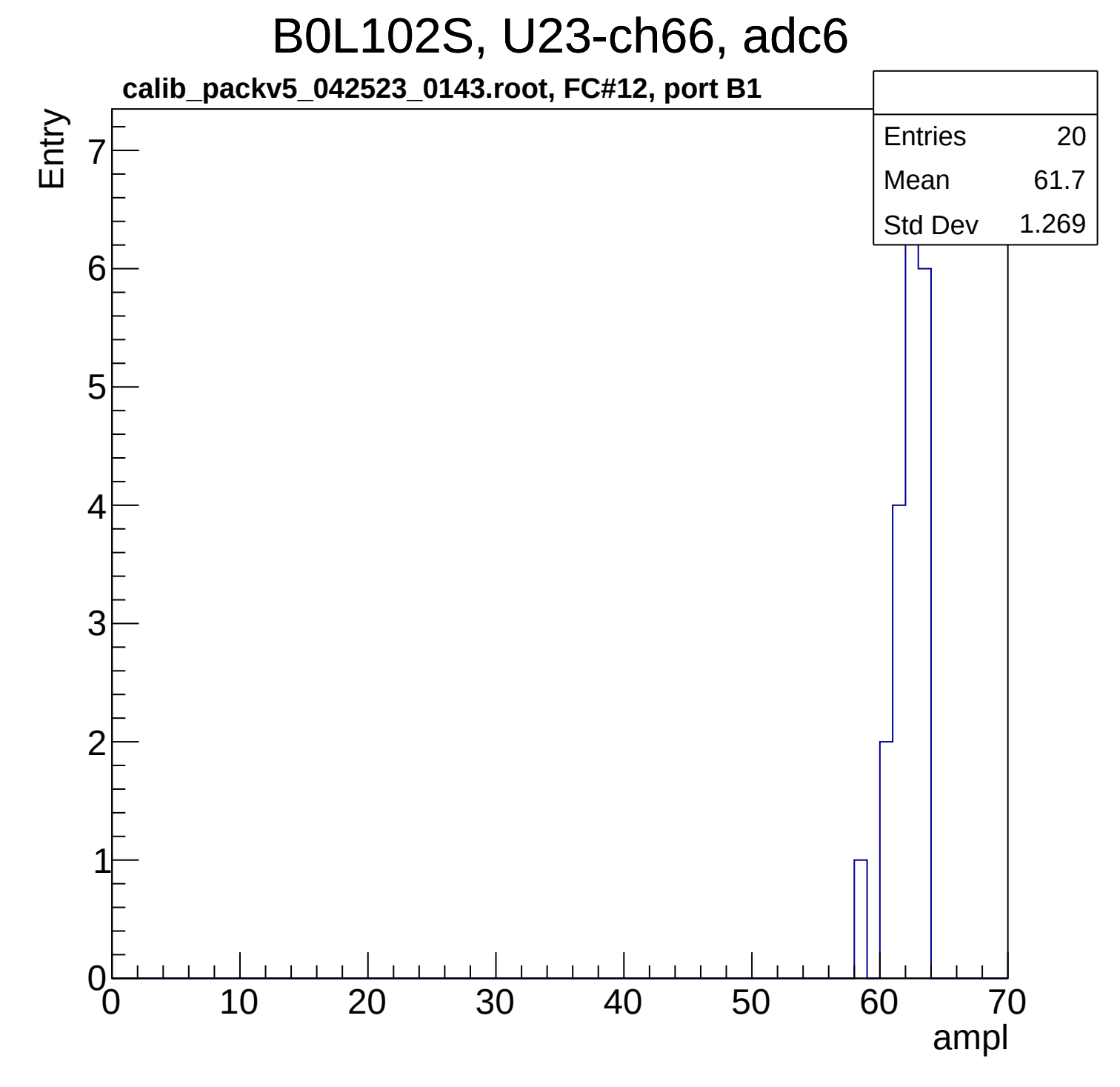
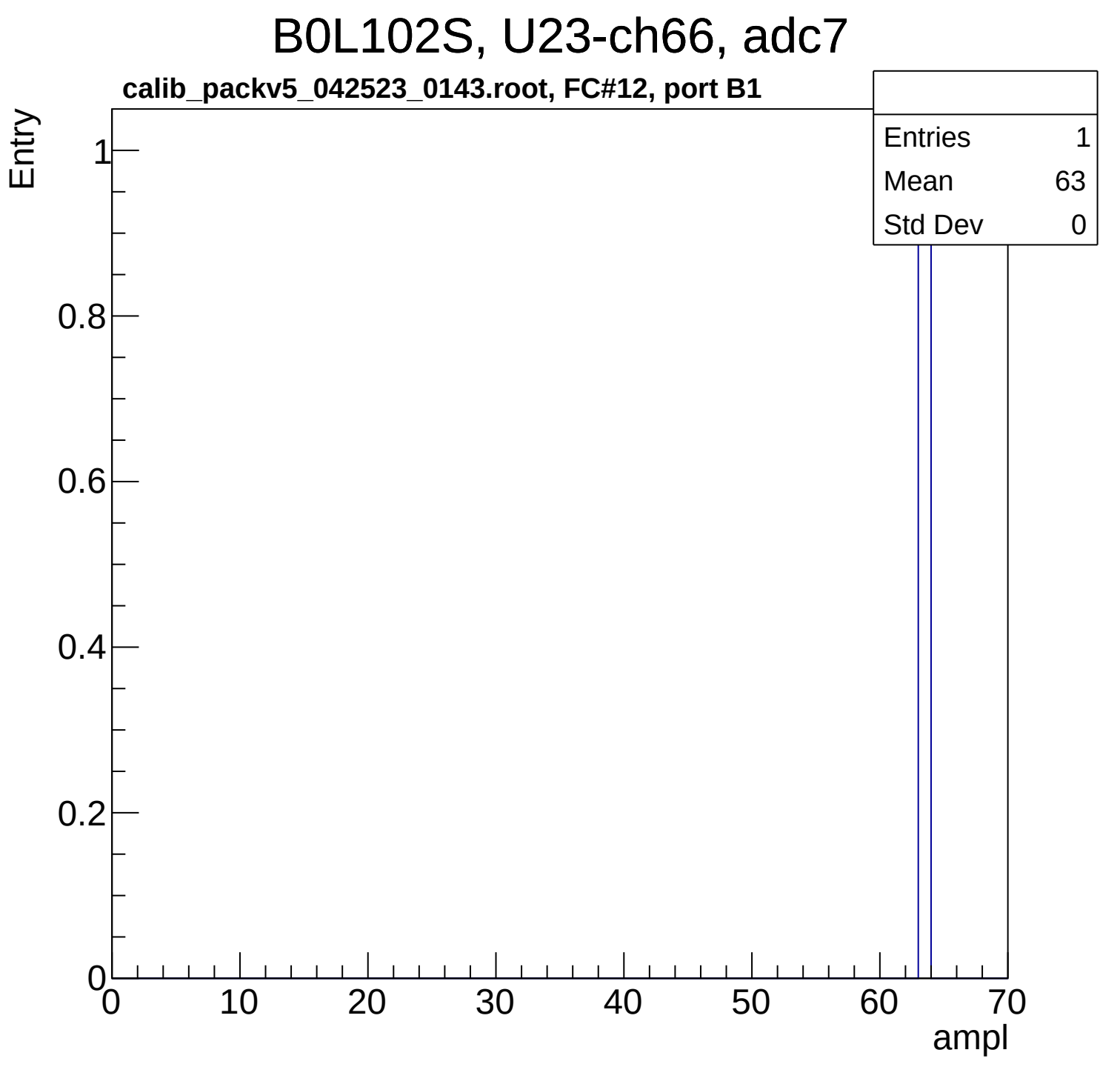
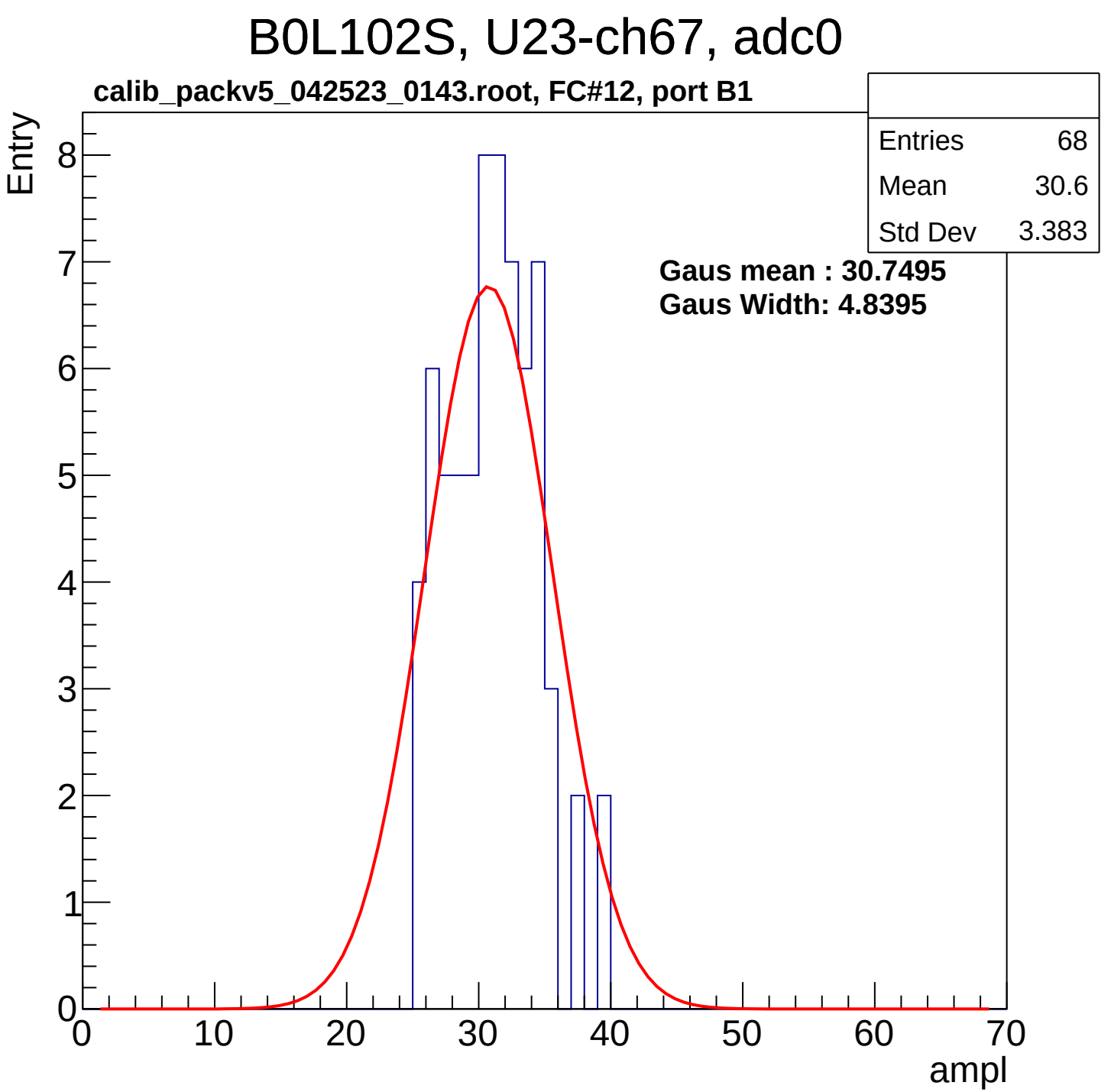
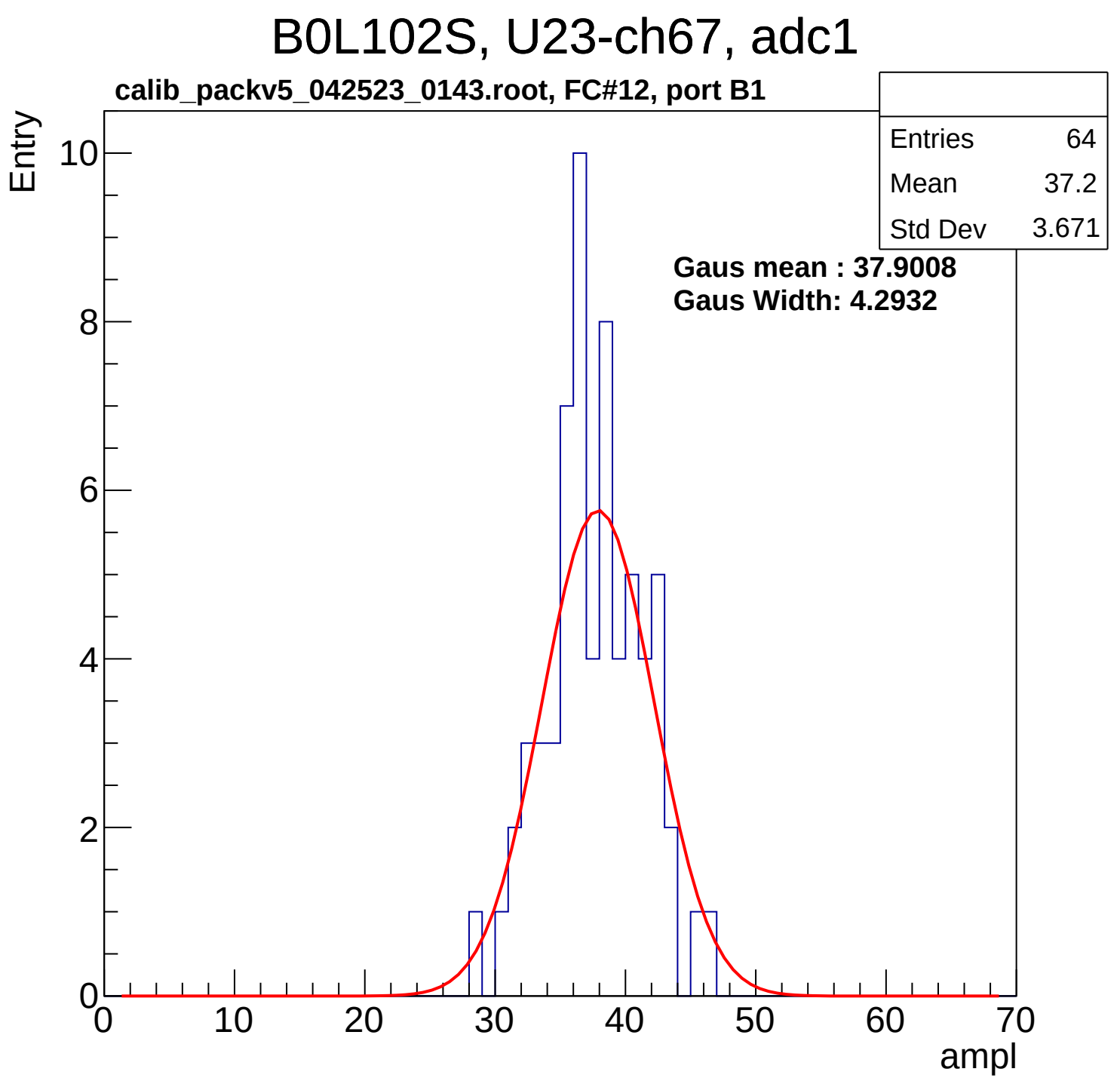
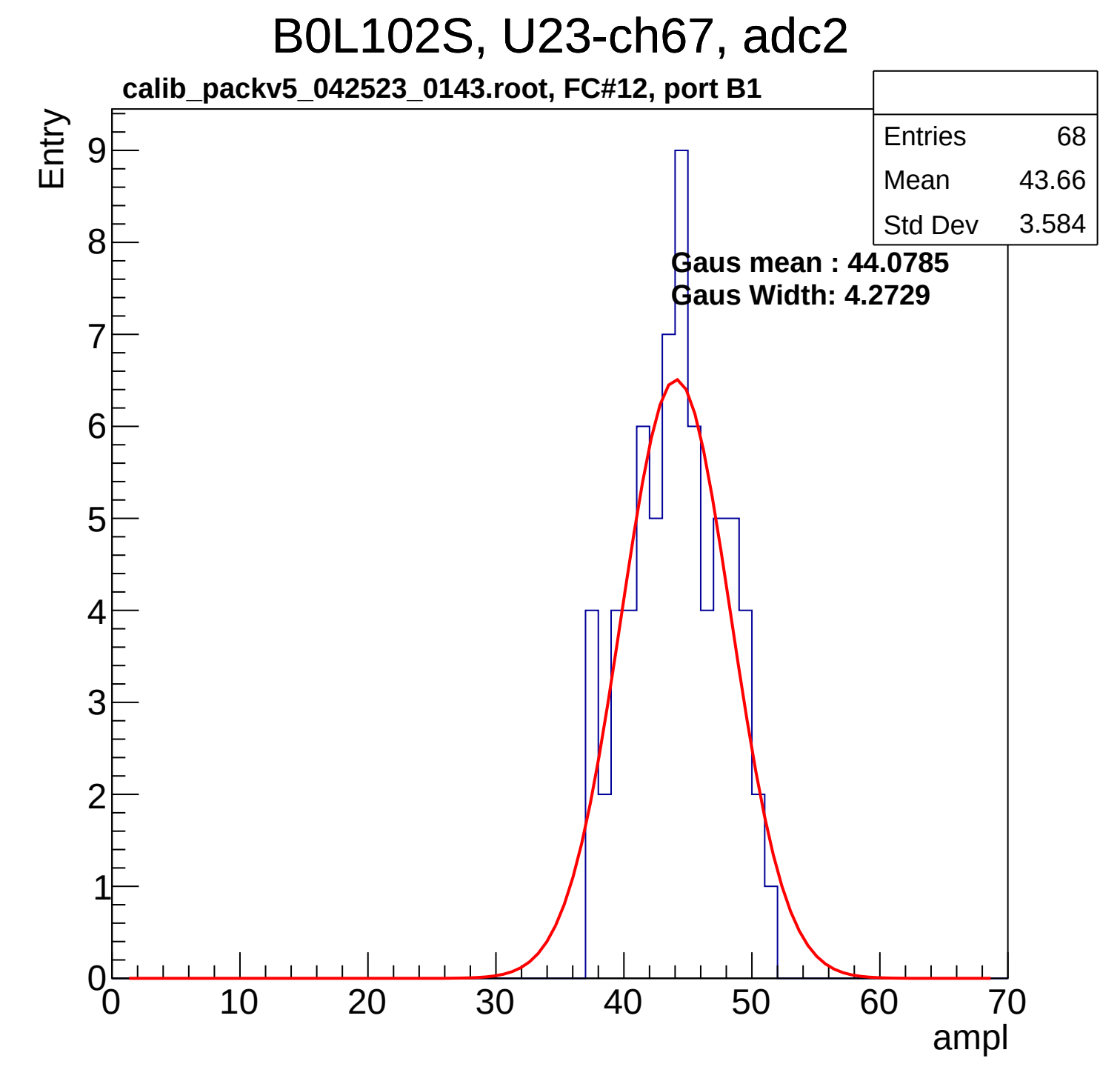

B0L102S, U23-ch66, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 57.02
Std Dev 7.952
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

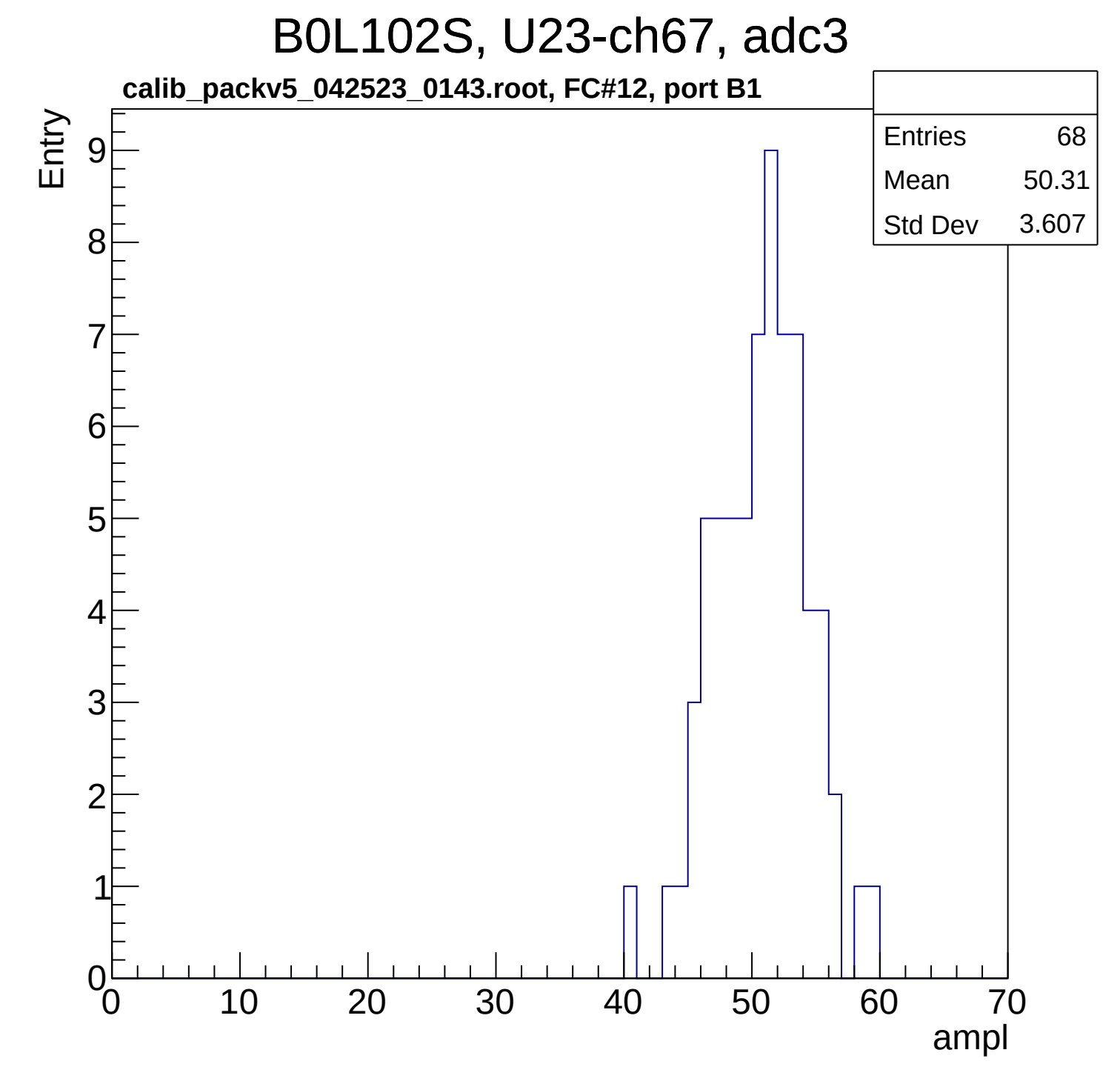

B0L102S, U23-ch66, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 20
Mean 61.7
Std Dev 1.269
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

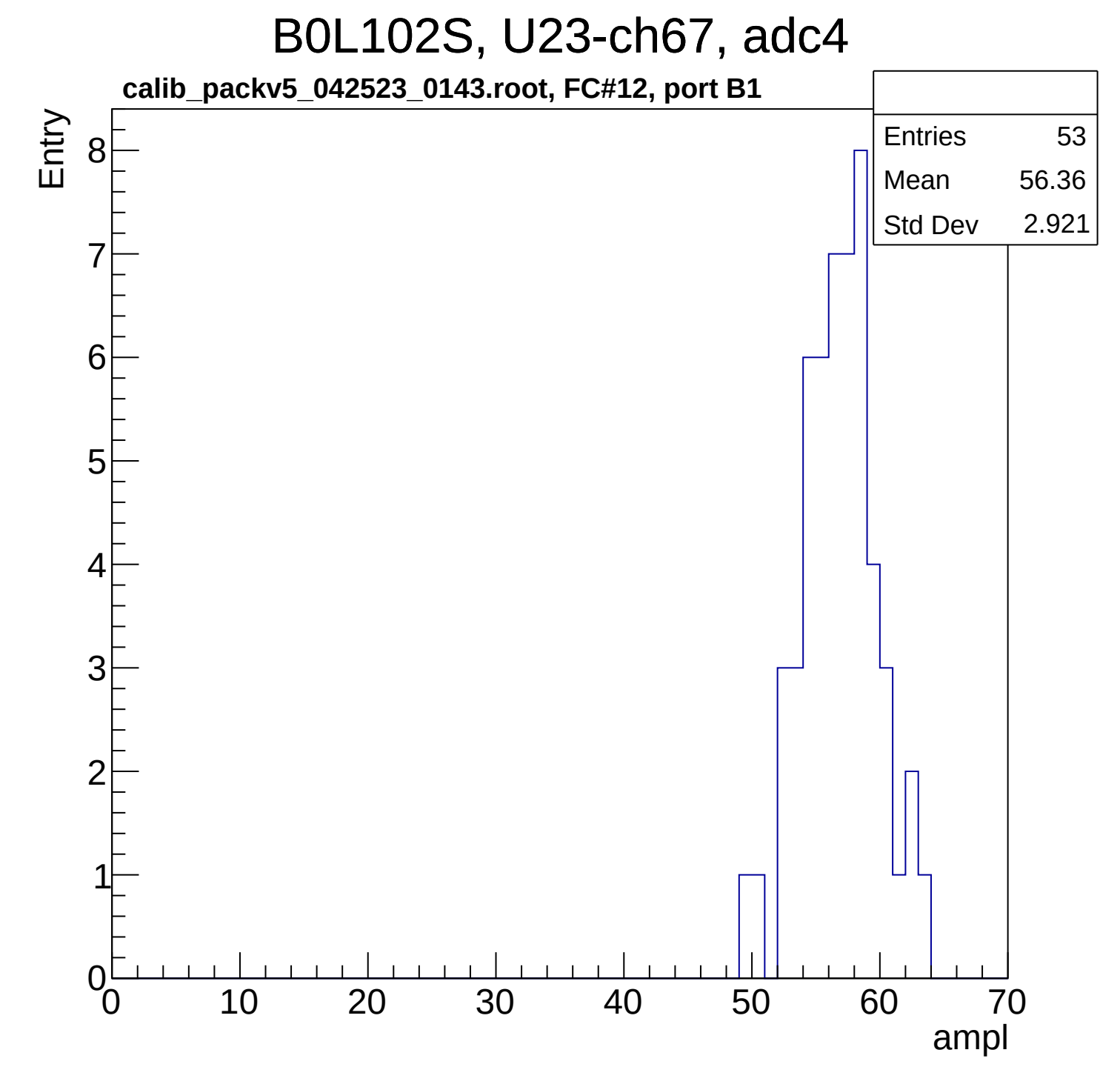

B0L102S, U23-ch66, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

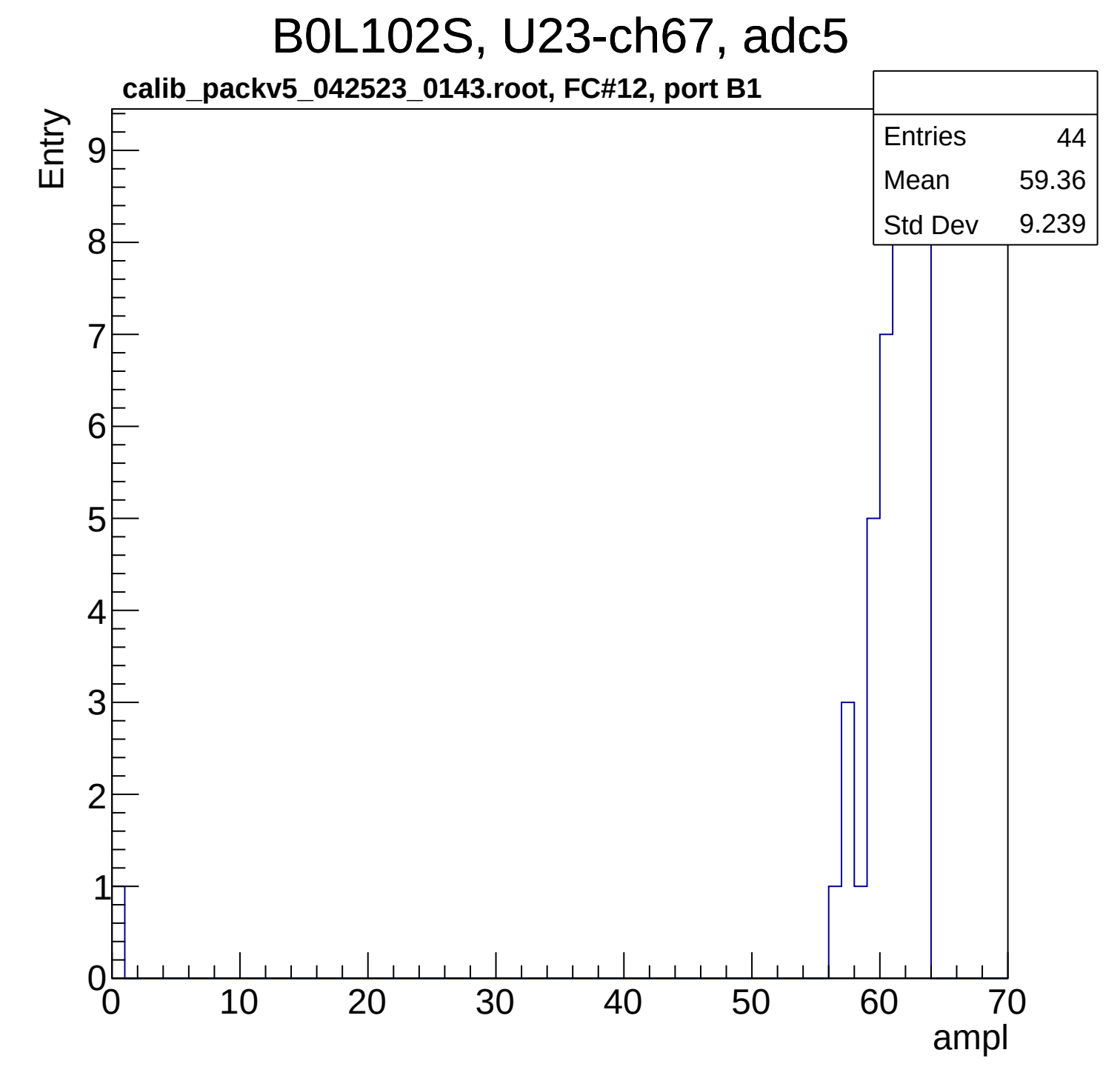

B0L102S, U23-ch67, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 30.6
Std Dev 3.383
Gaus mean : 30.7495
Gaus Width: 4.8395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

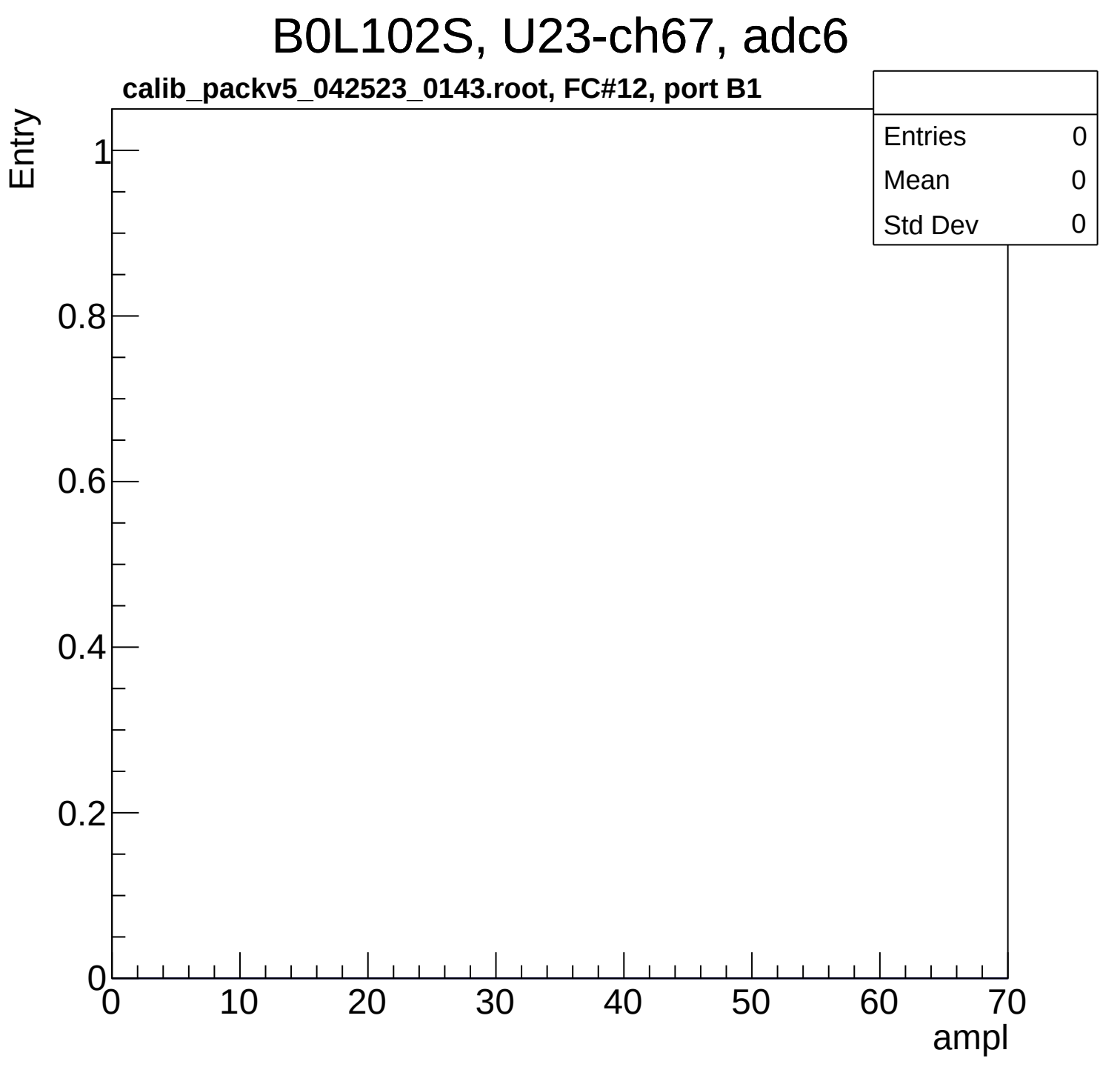

B0L102S, U23-ch67, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 37.2
Std Dev 3.671
Gaus mean : 37.9008
Gaus Width: 4.2932
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

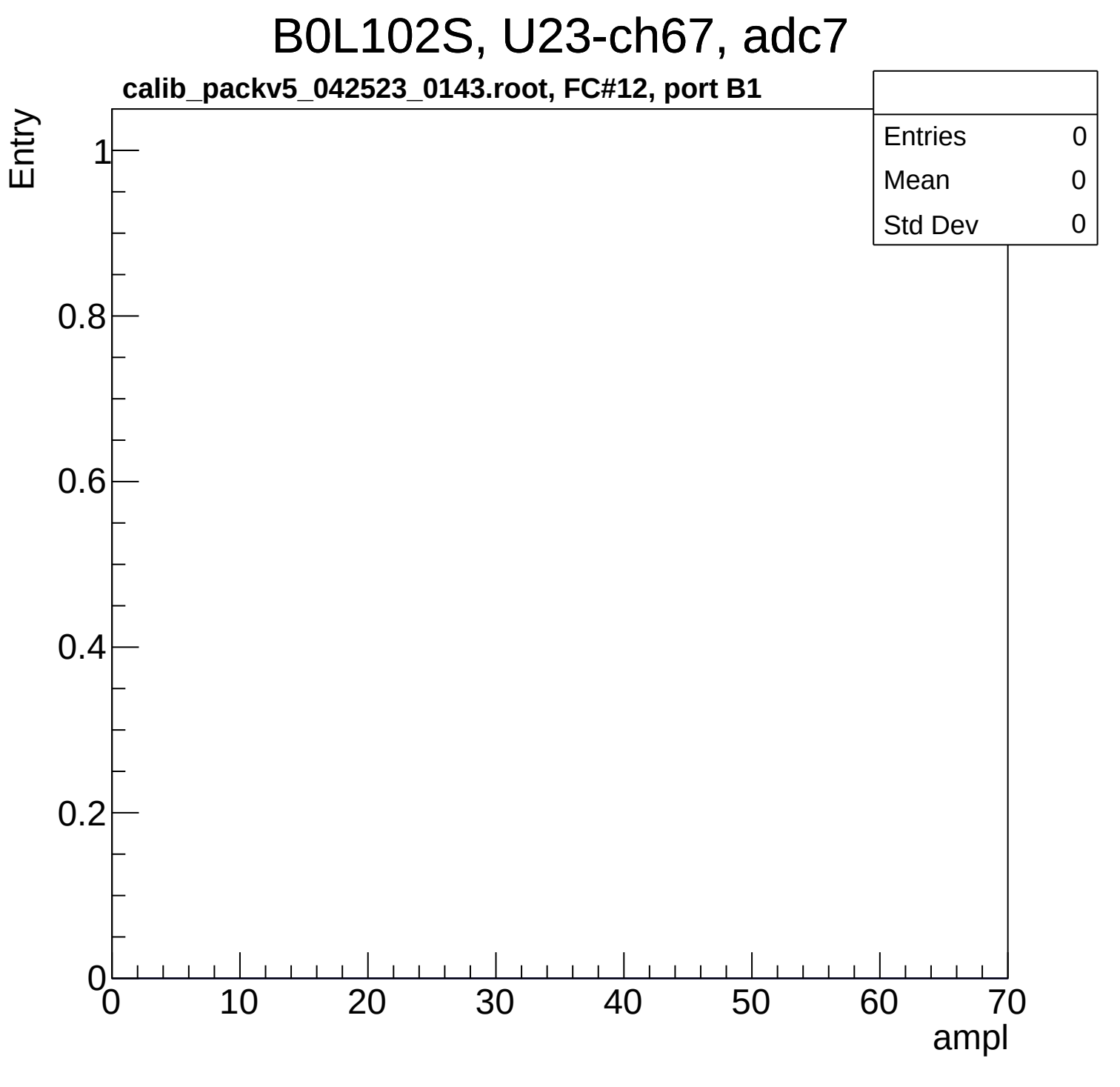

B0L102S, U23-ch67, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 43.66
Std Dev 3.584
Gaus mean : 44.0785
Gaus Width: 4.2729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch67, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 50.31
Std Dev 3.607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch67, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 56.36
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch67, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 59.36
Std Dev 9.239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch67, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch67, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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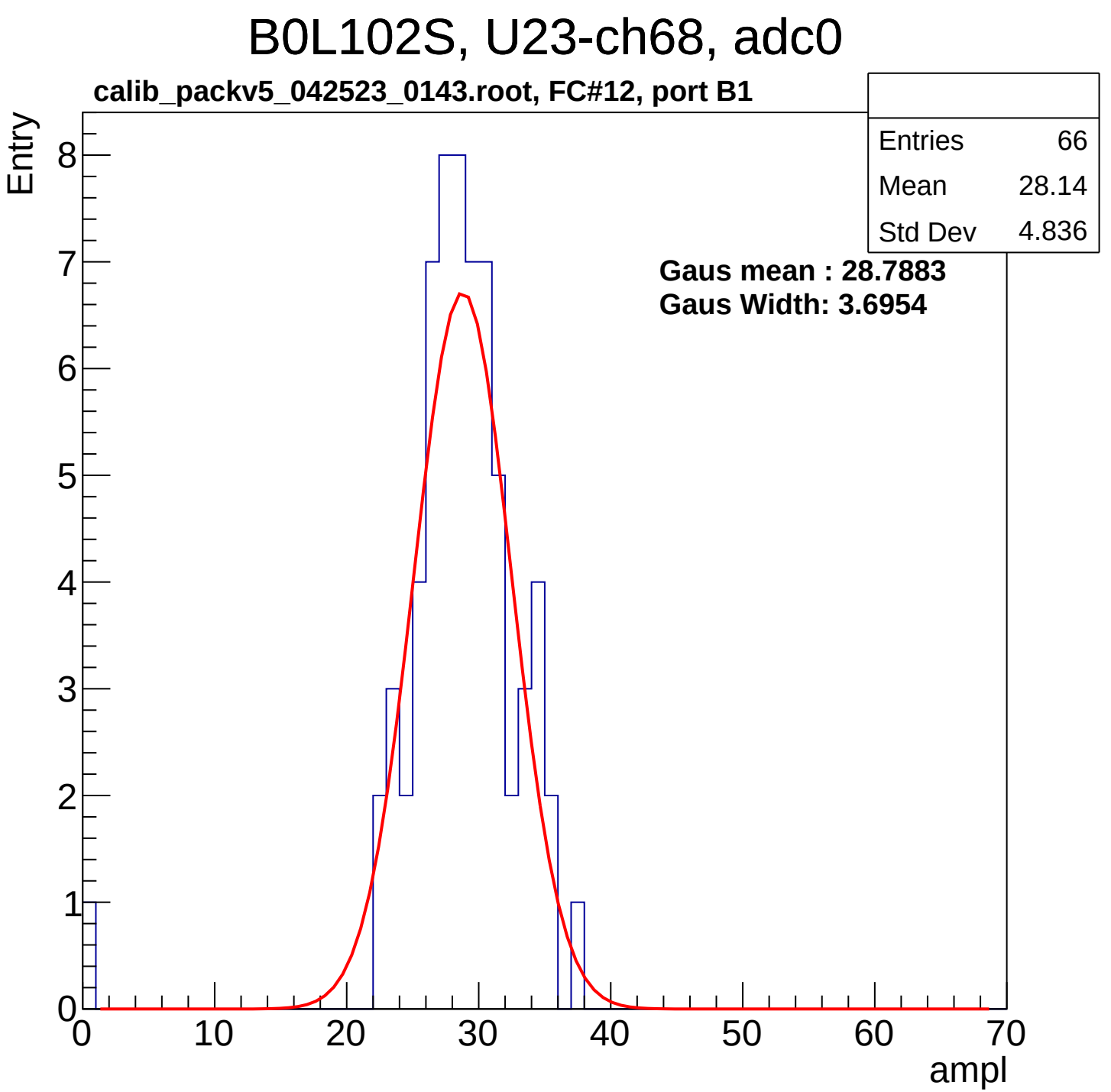
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch68, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 28.14
Std Dev 4.836
Gaus mean : 28.7883
Gaus Width: 3.6954
Entry
ampl
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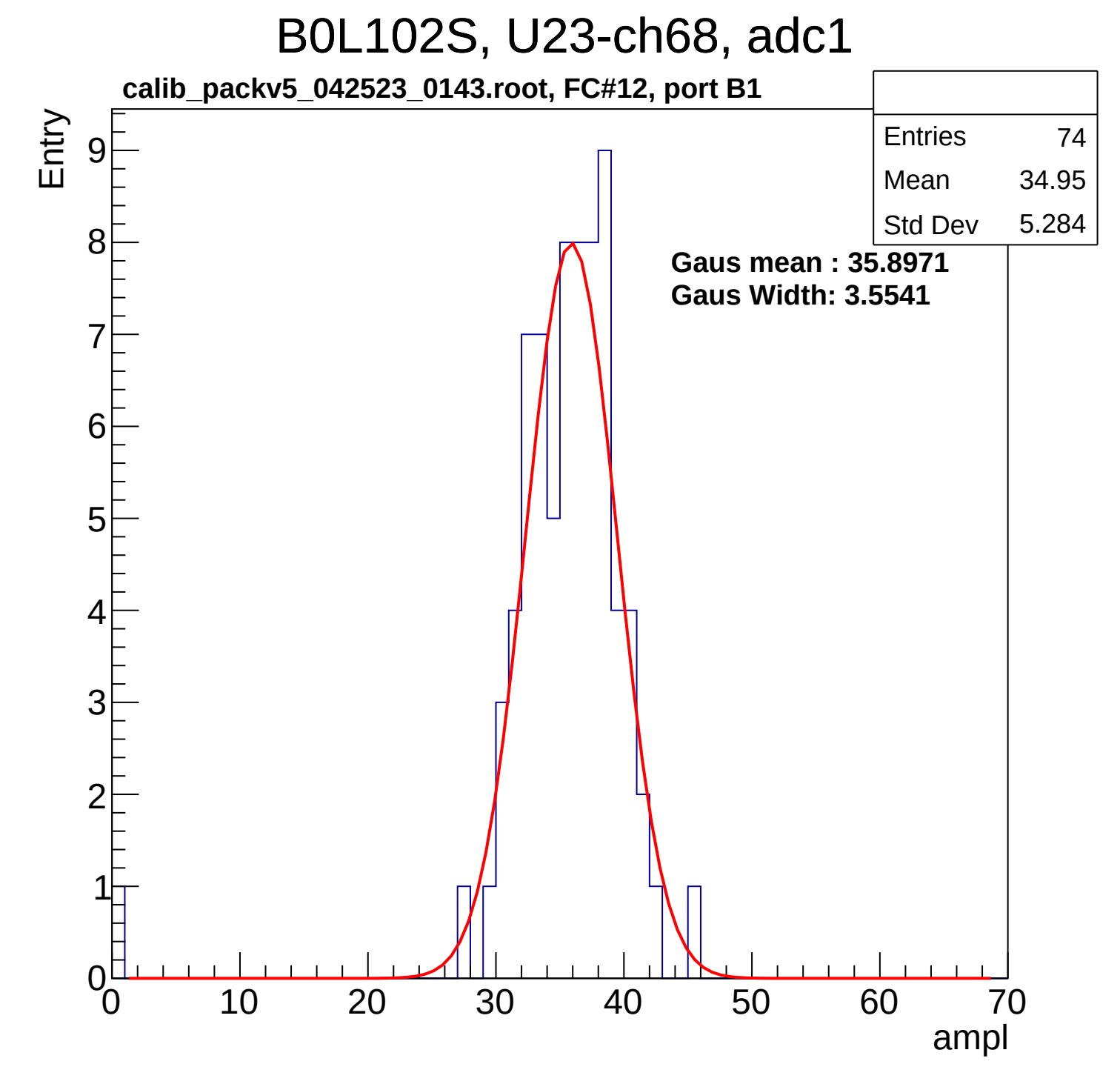
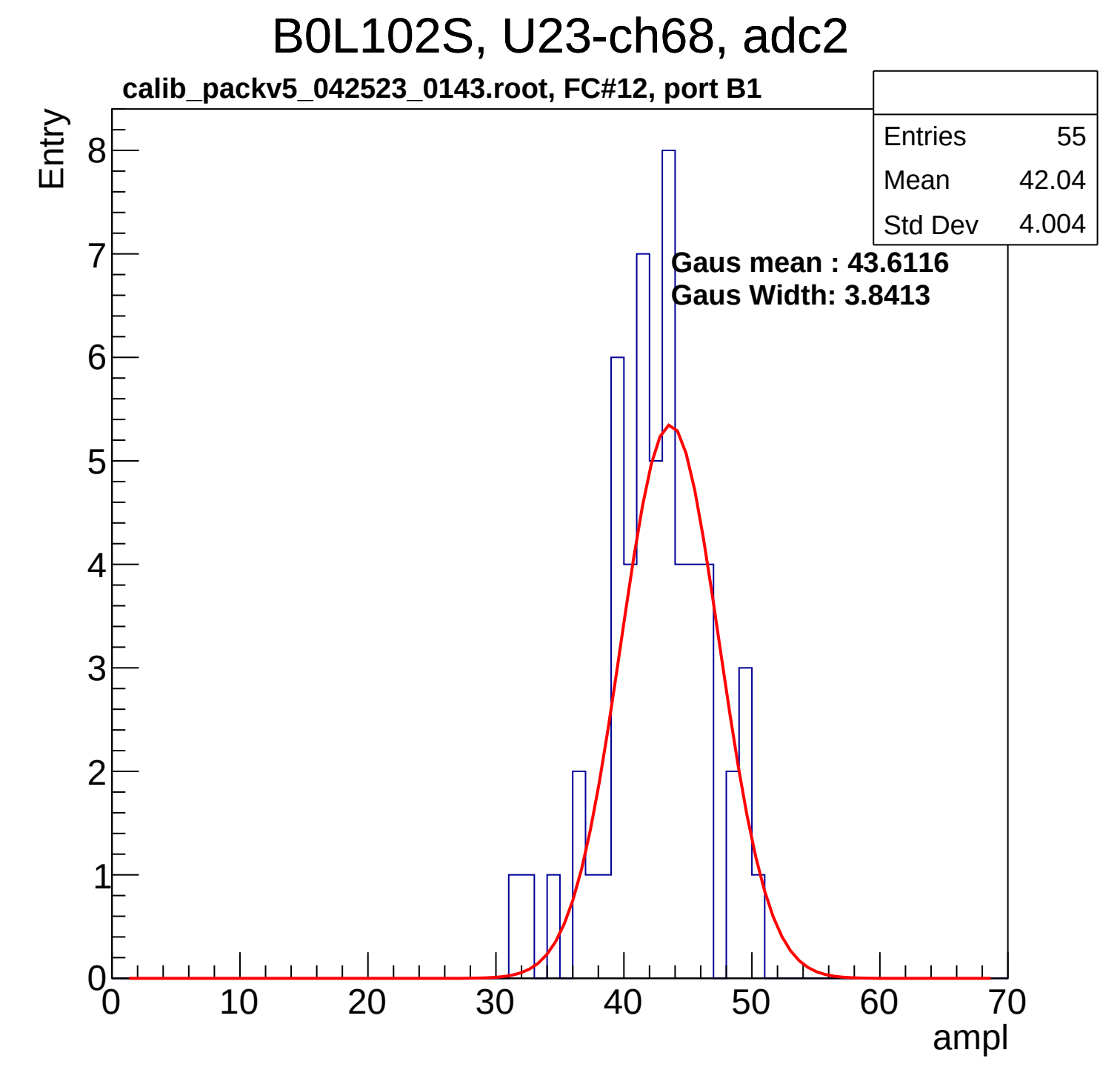
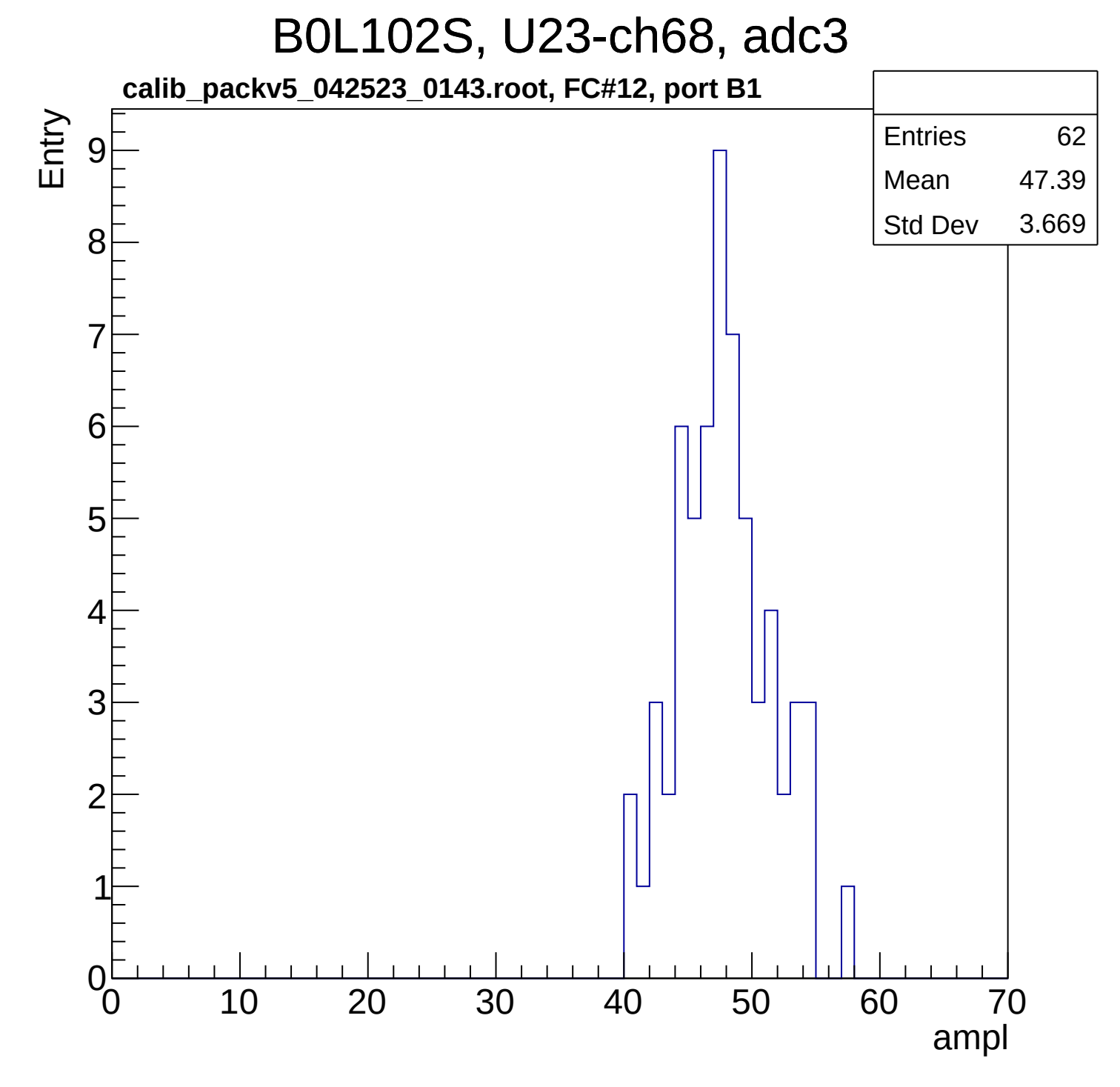
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch68, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 34.95
Std Dev 5.284
Gaus mean : 35.8971
Gaus Width: 3.5541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch68, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 42.04
Std Dev 4.004
Gaus mean : 43.6116
Gaus Width: 3.8413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch68, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 47.39
Std Dev 3.669
Entry
ampl
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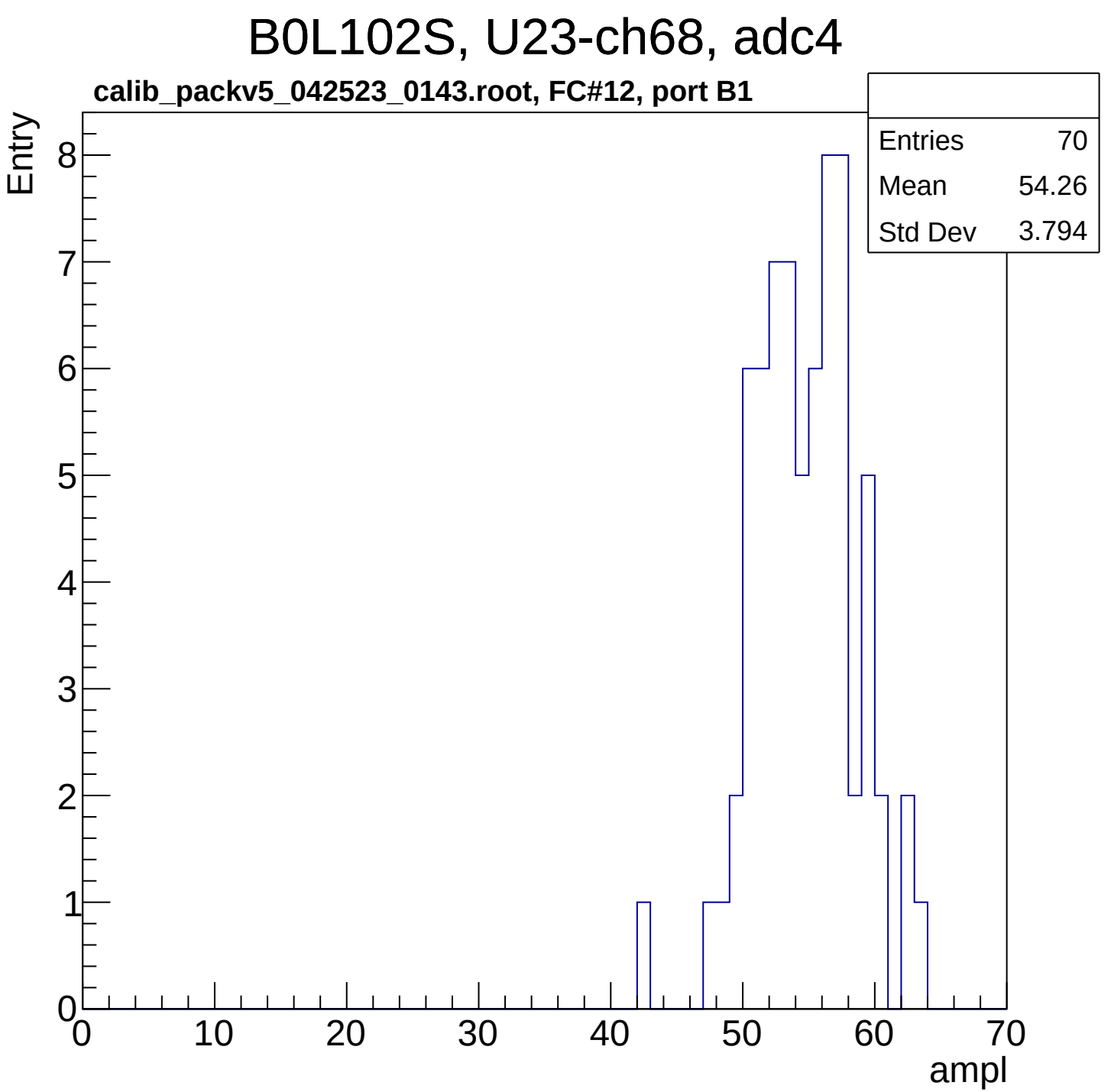
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch68, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 54.26
Std Dev 3.794
Entry
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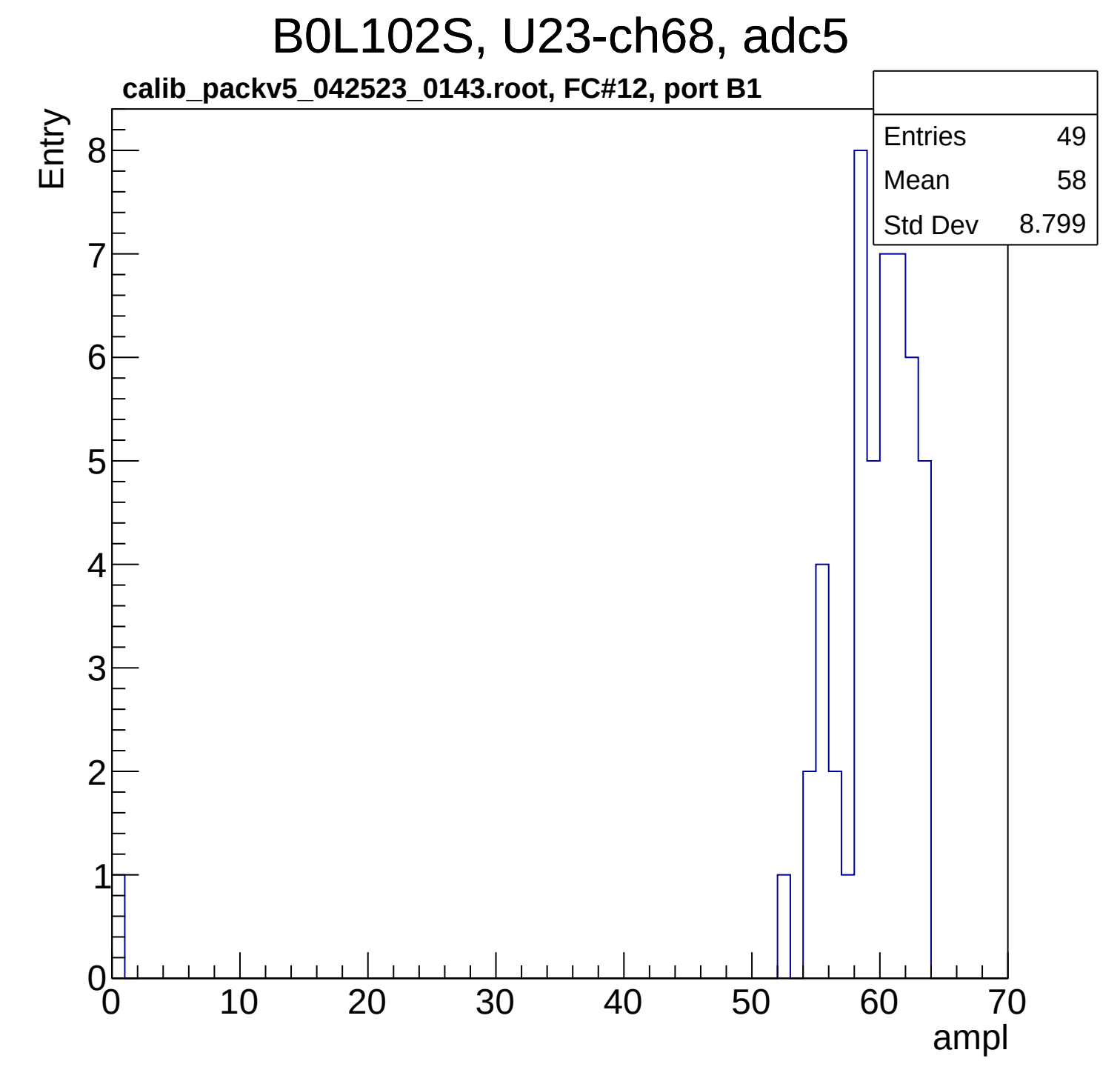
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch68, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 58
Std Dev 8.799
Entry
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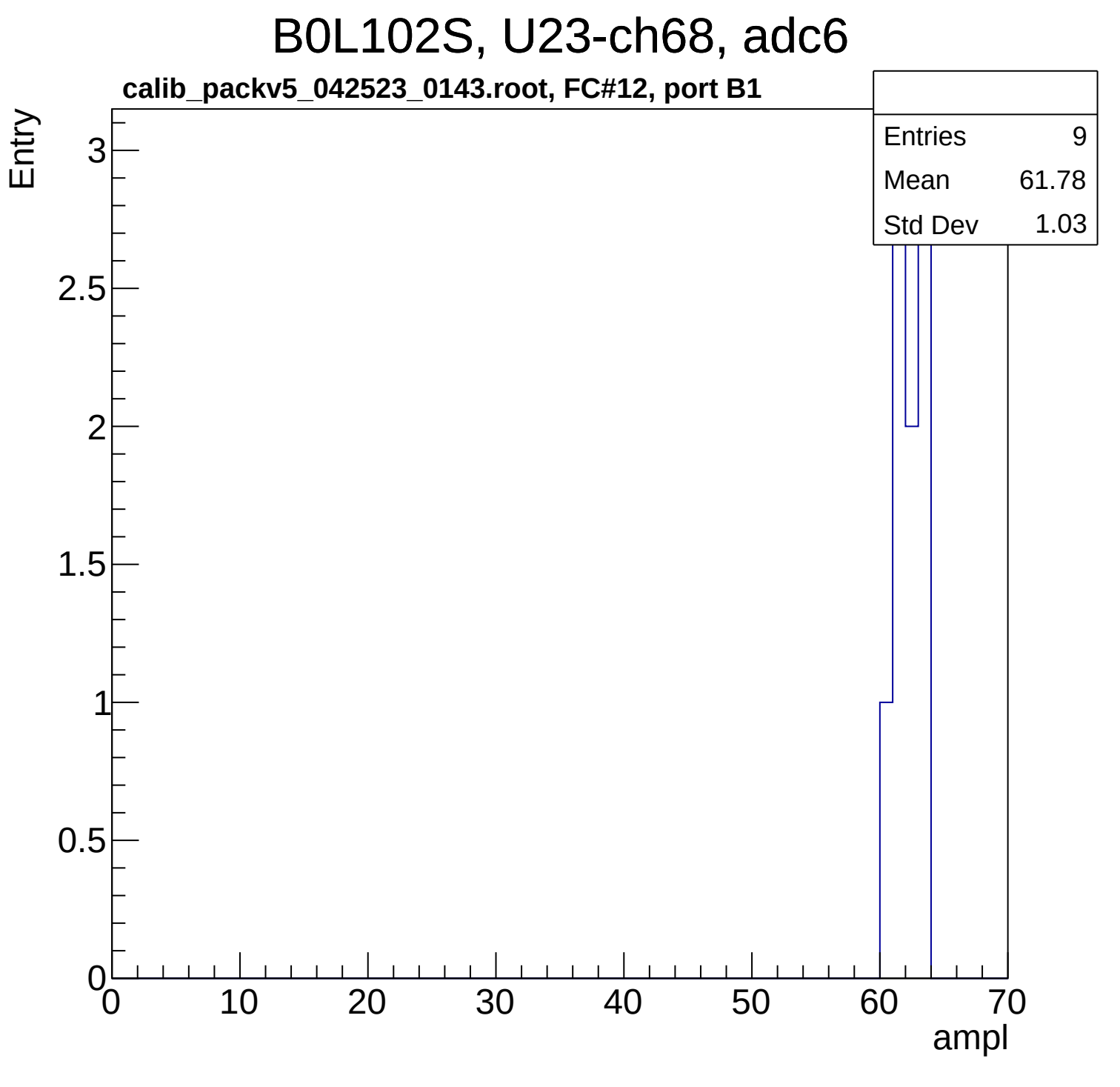
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch68, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 61.78
Std Dev 1.03
Entry
ampl
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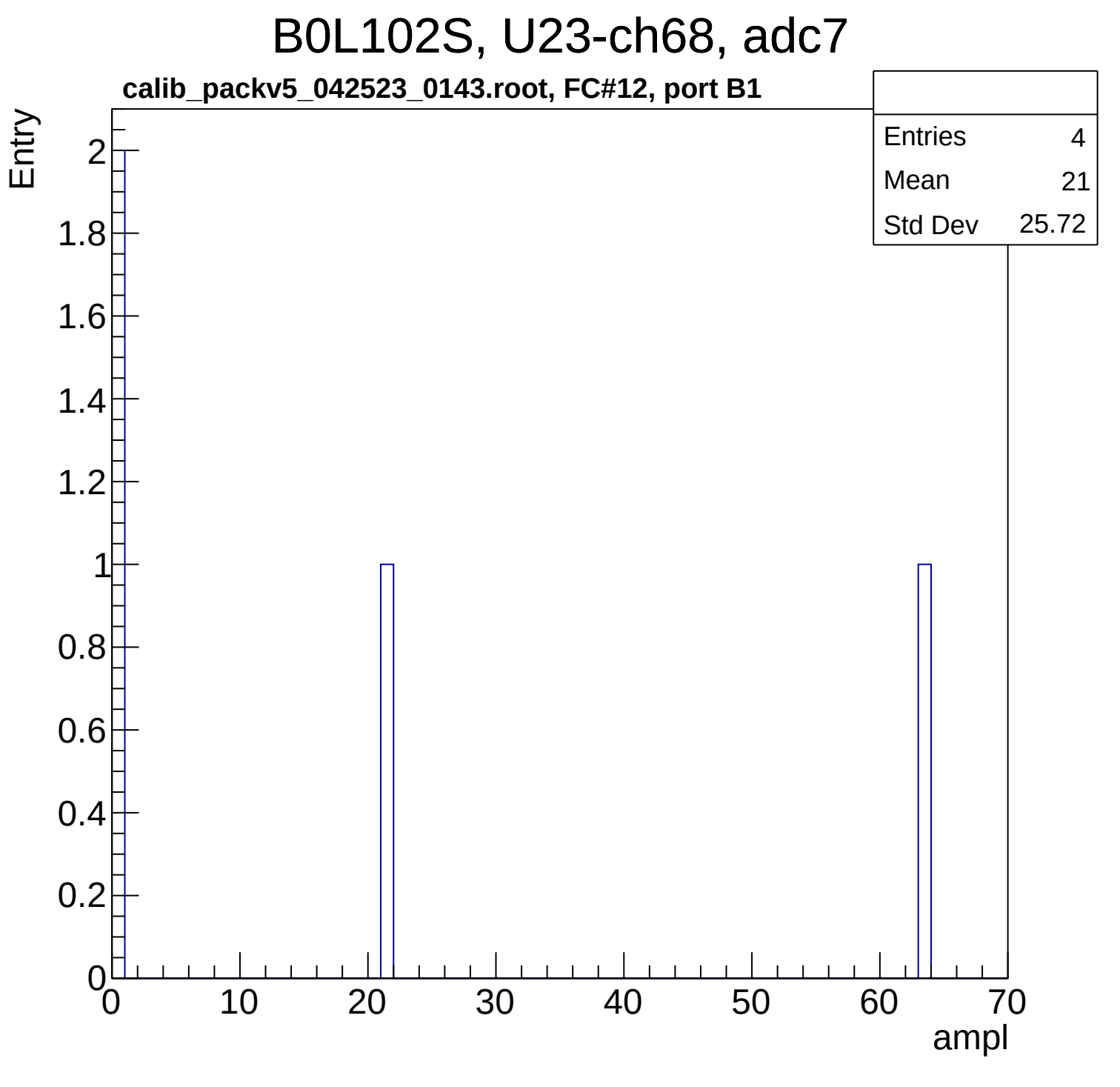
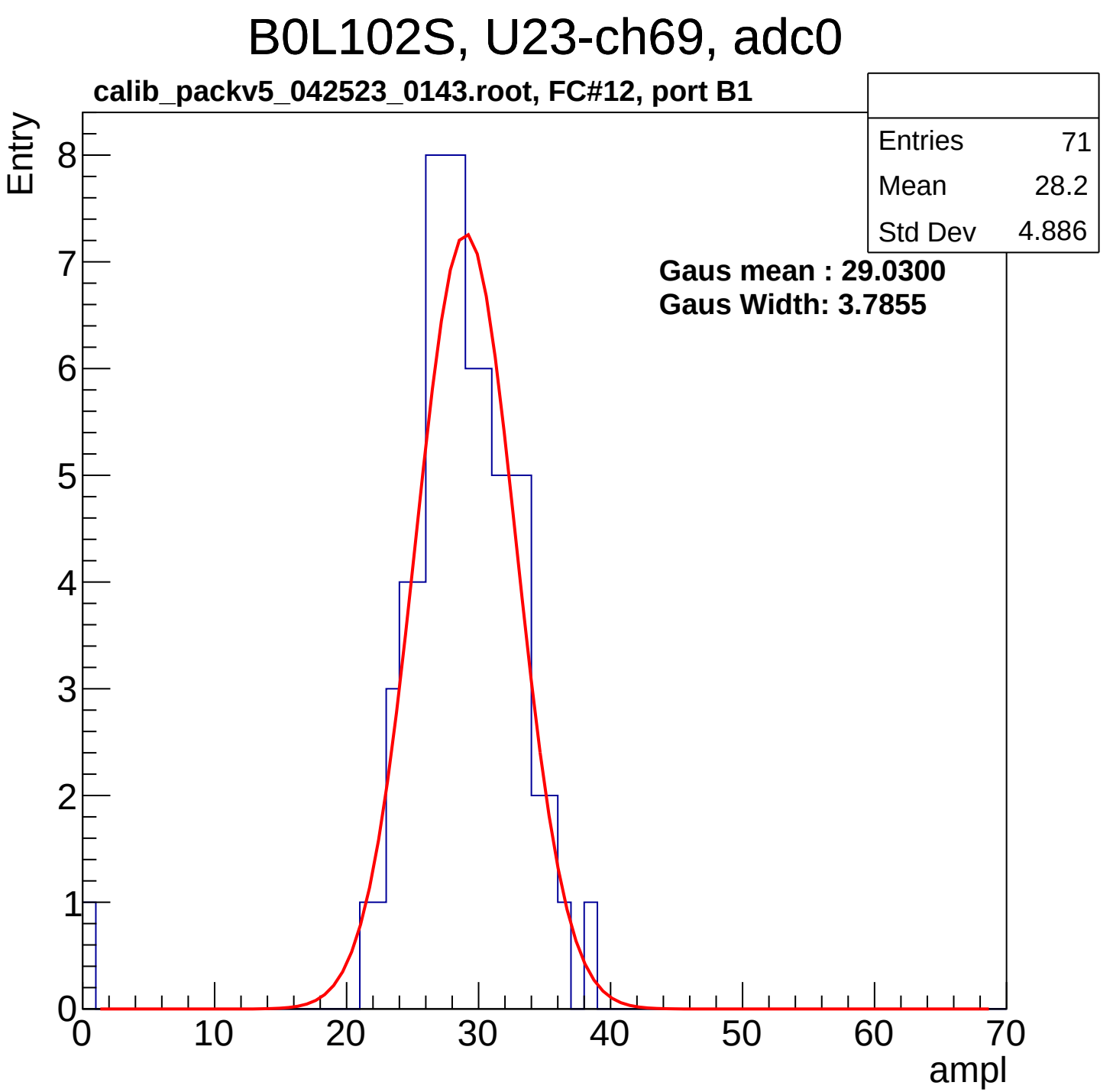
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch68, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 21
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch69, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 28.2
Std Dev 4.886
Gaus mean : 29.0300
Gaus Width: 3.7855
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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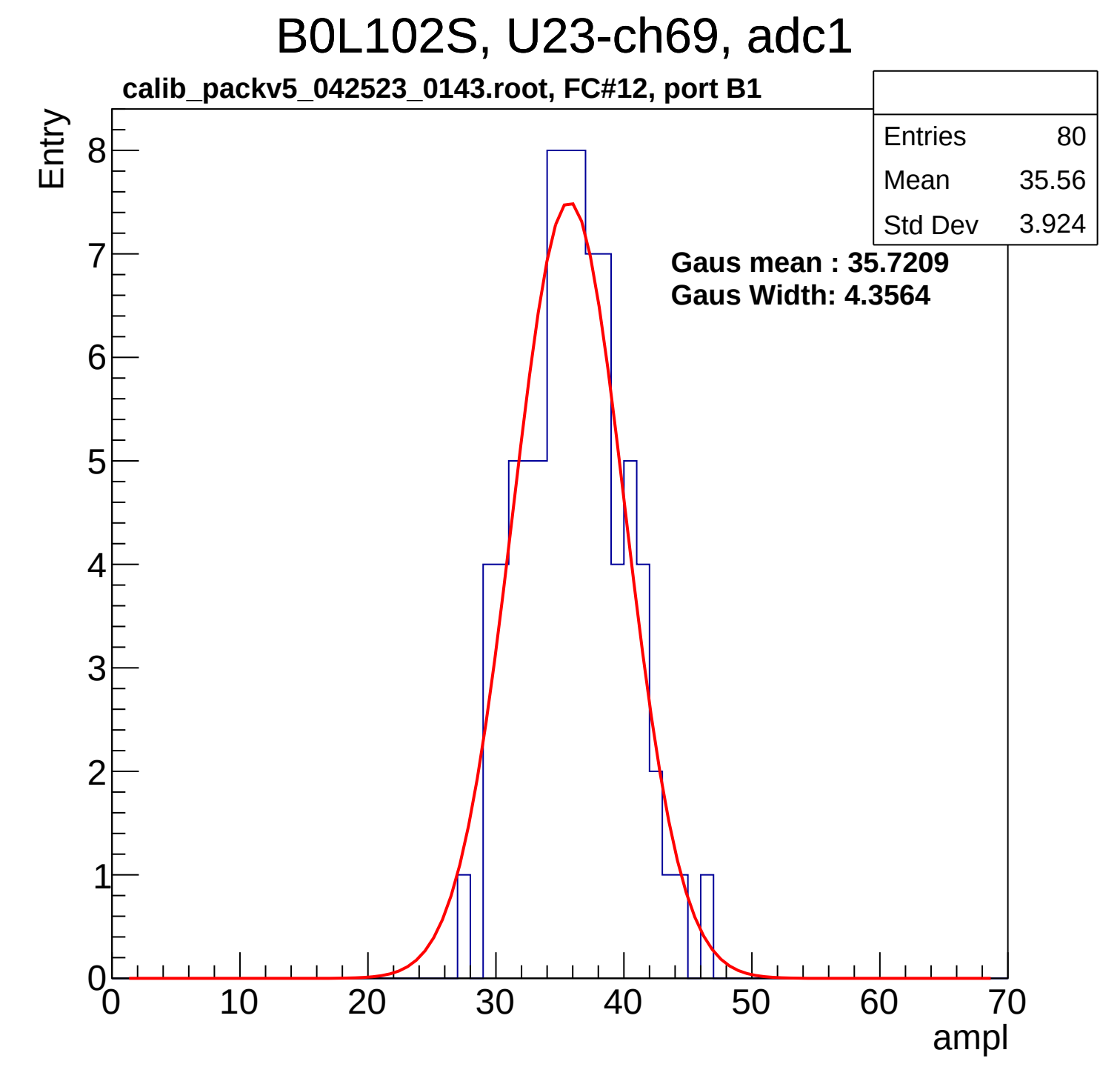
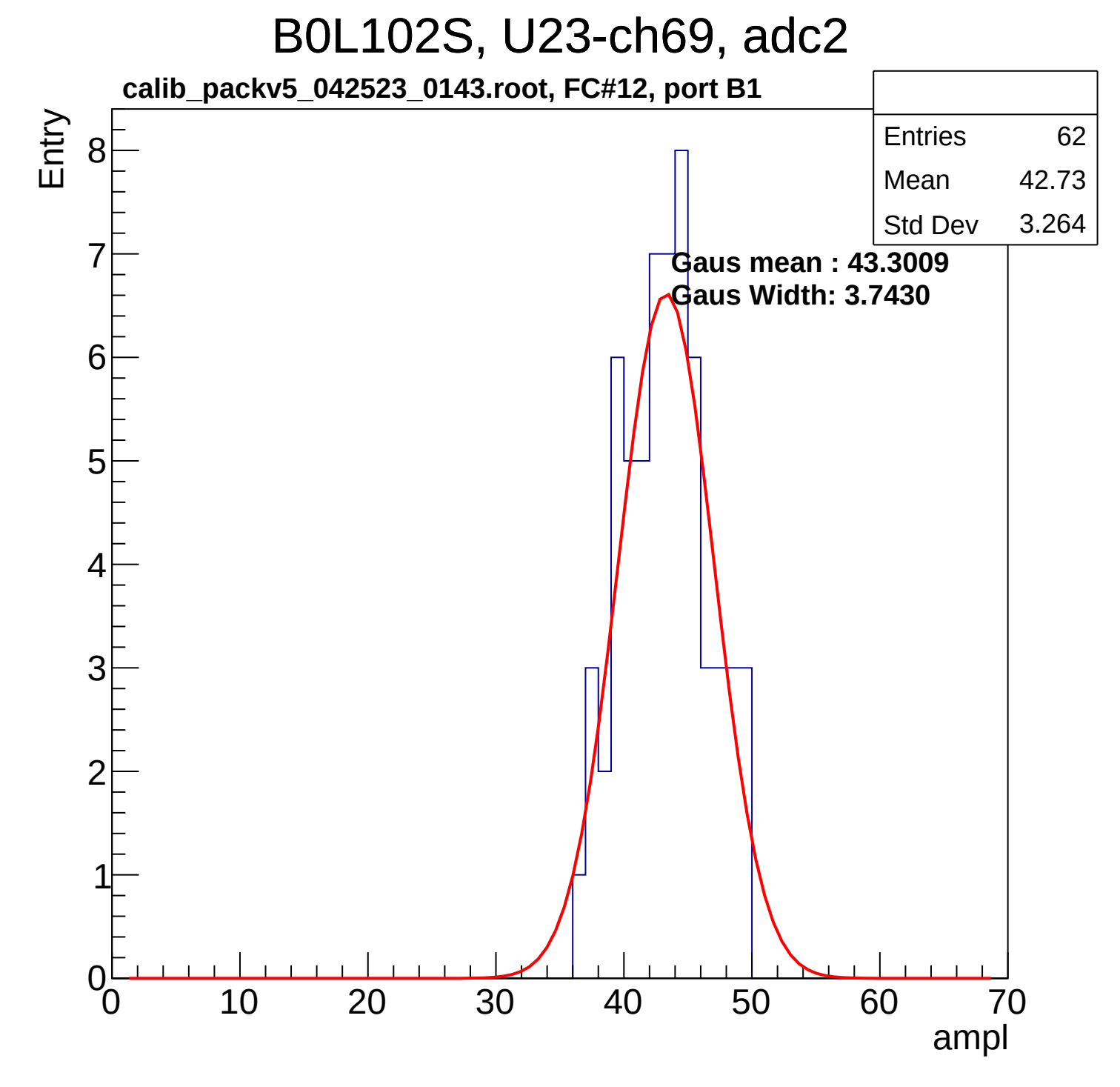
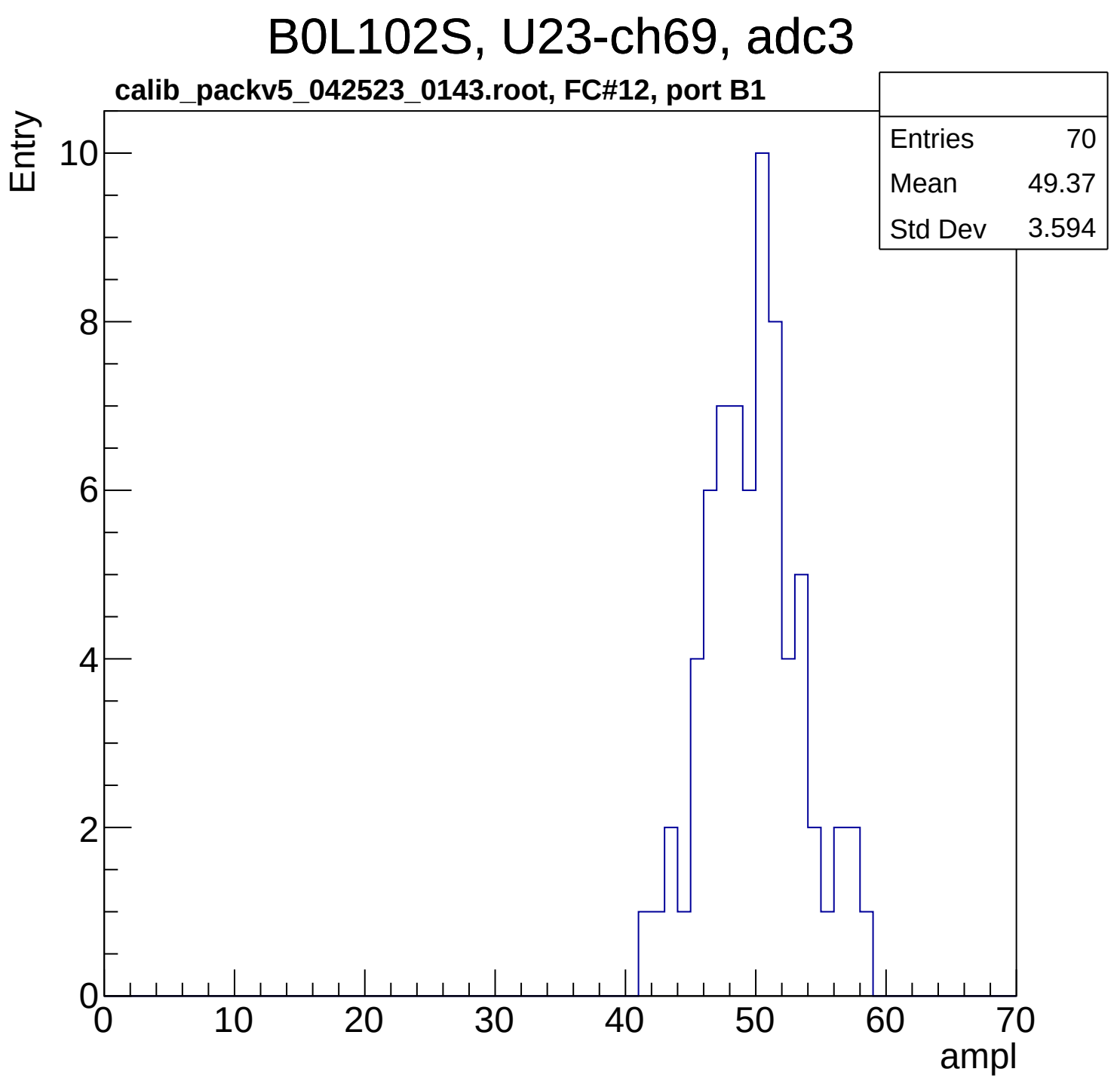
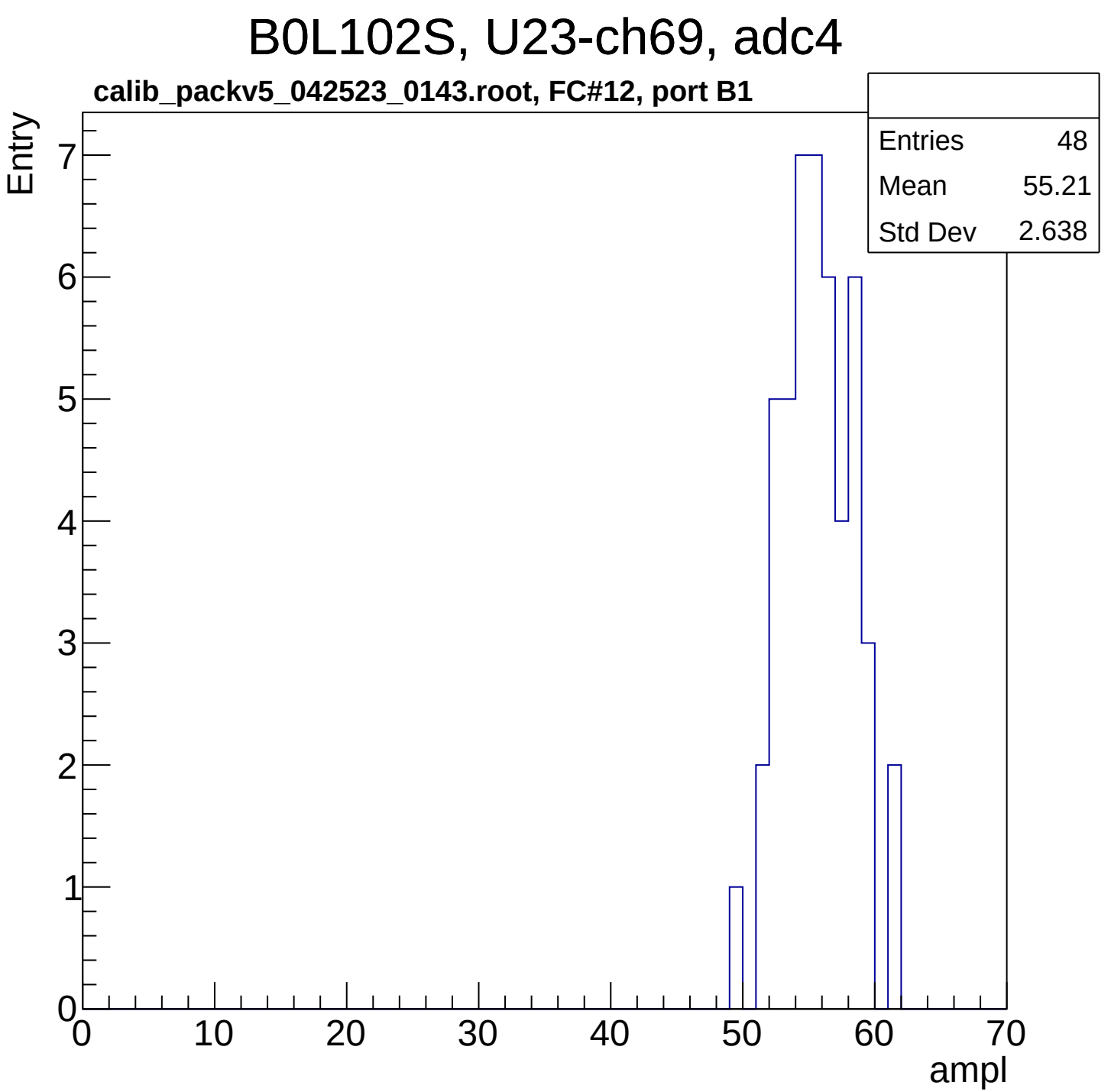

B0L102S, U23-ch69, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 35.56
Std Dev 3.924
Gaus mean : 35.7209
Gaus Width: 4.3564
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch69, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.73
Std Dev 3.264
Gaus mean : 43.3009
Gaus Width: 3.7430
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch69, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 49.37
Std Dev 3.594
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch69, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 55.21
Std Dev 2.638
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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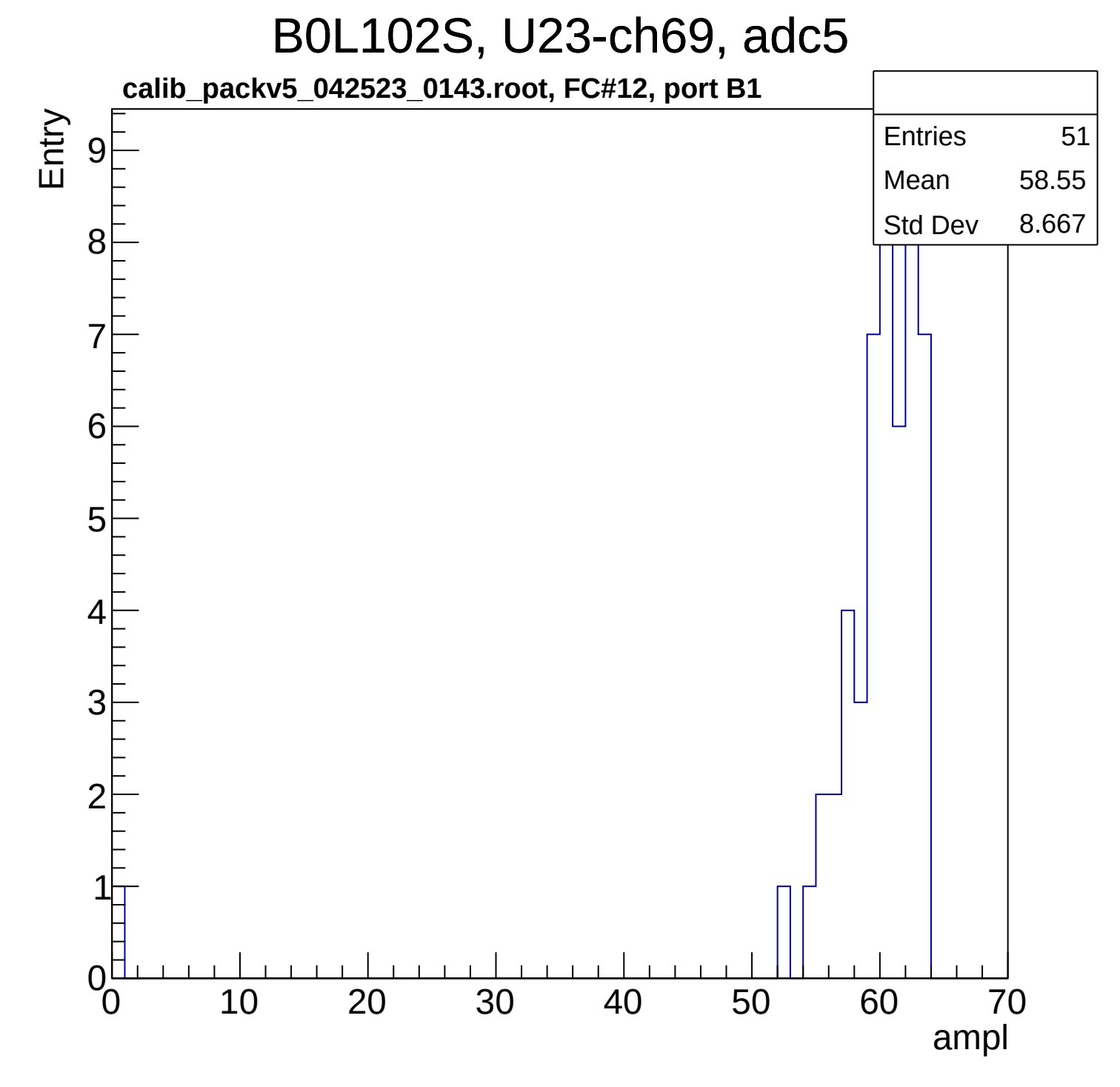
B0L102S, U23-ch69, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 58.55
Std Dev 8.667
Entry
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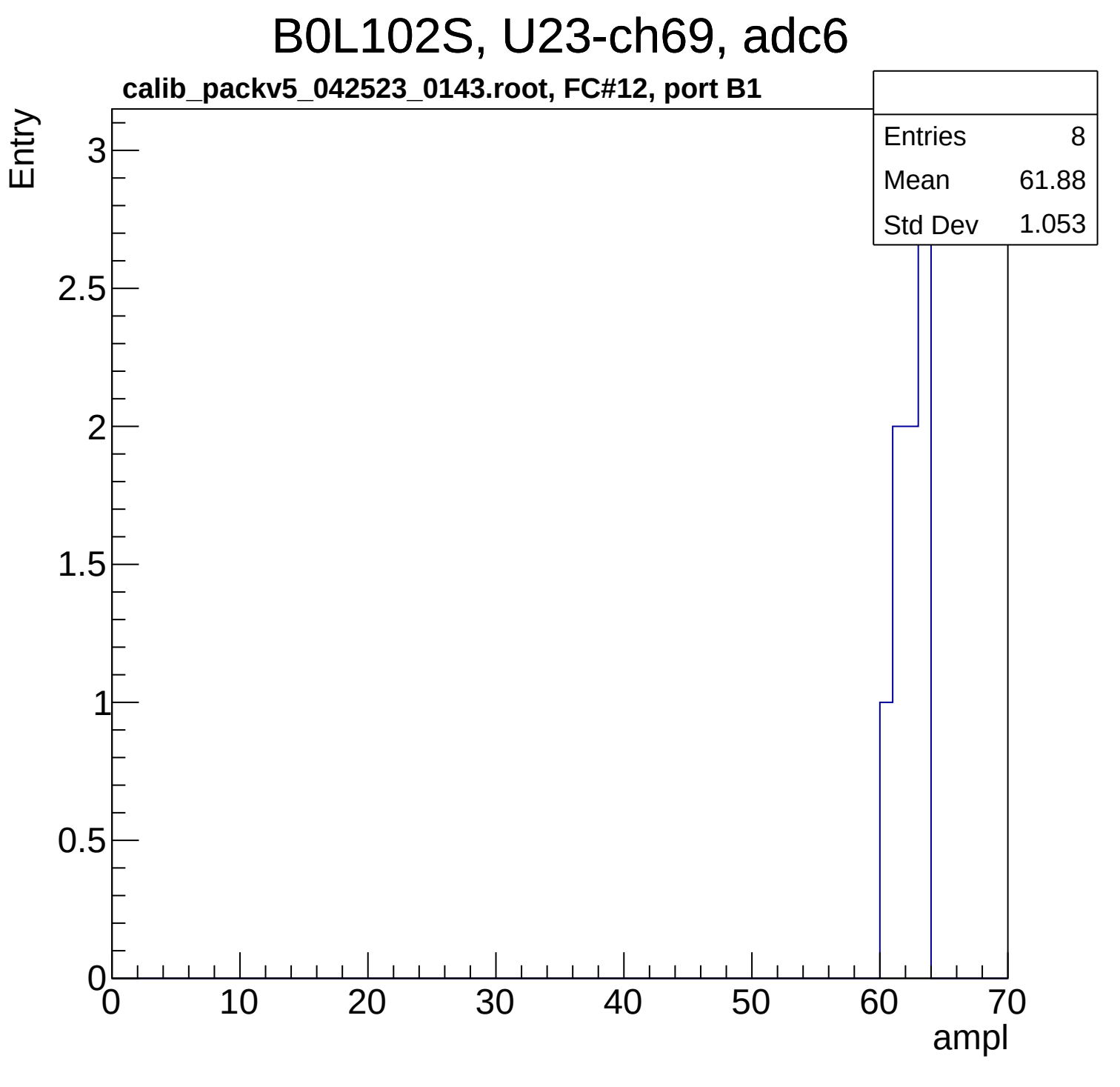
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch69, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.88
Std Dev 1.053
Entry
ampl
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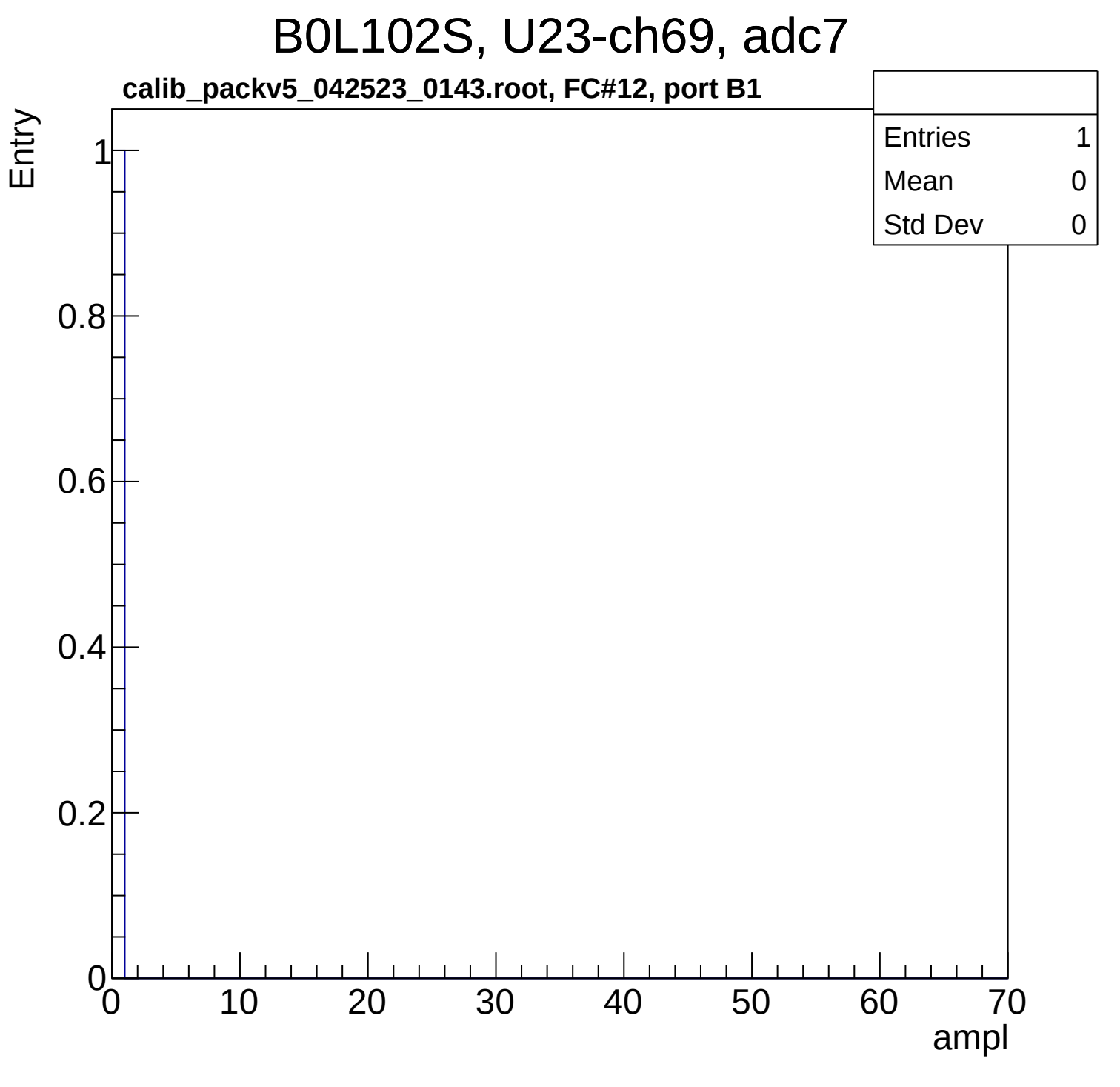
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch69, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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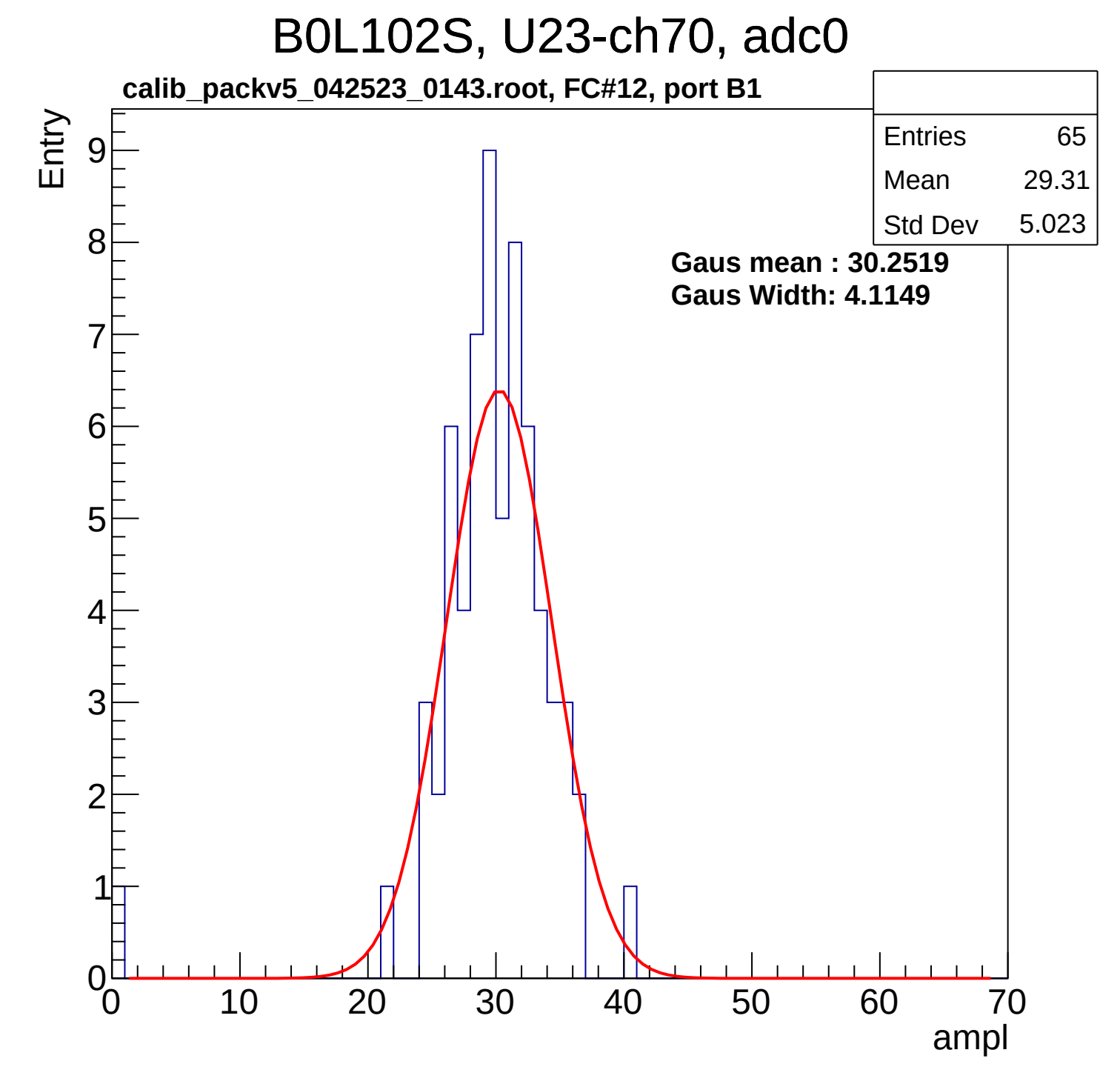
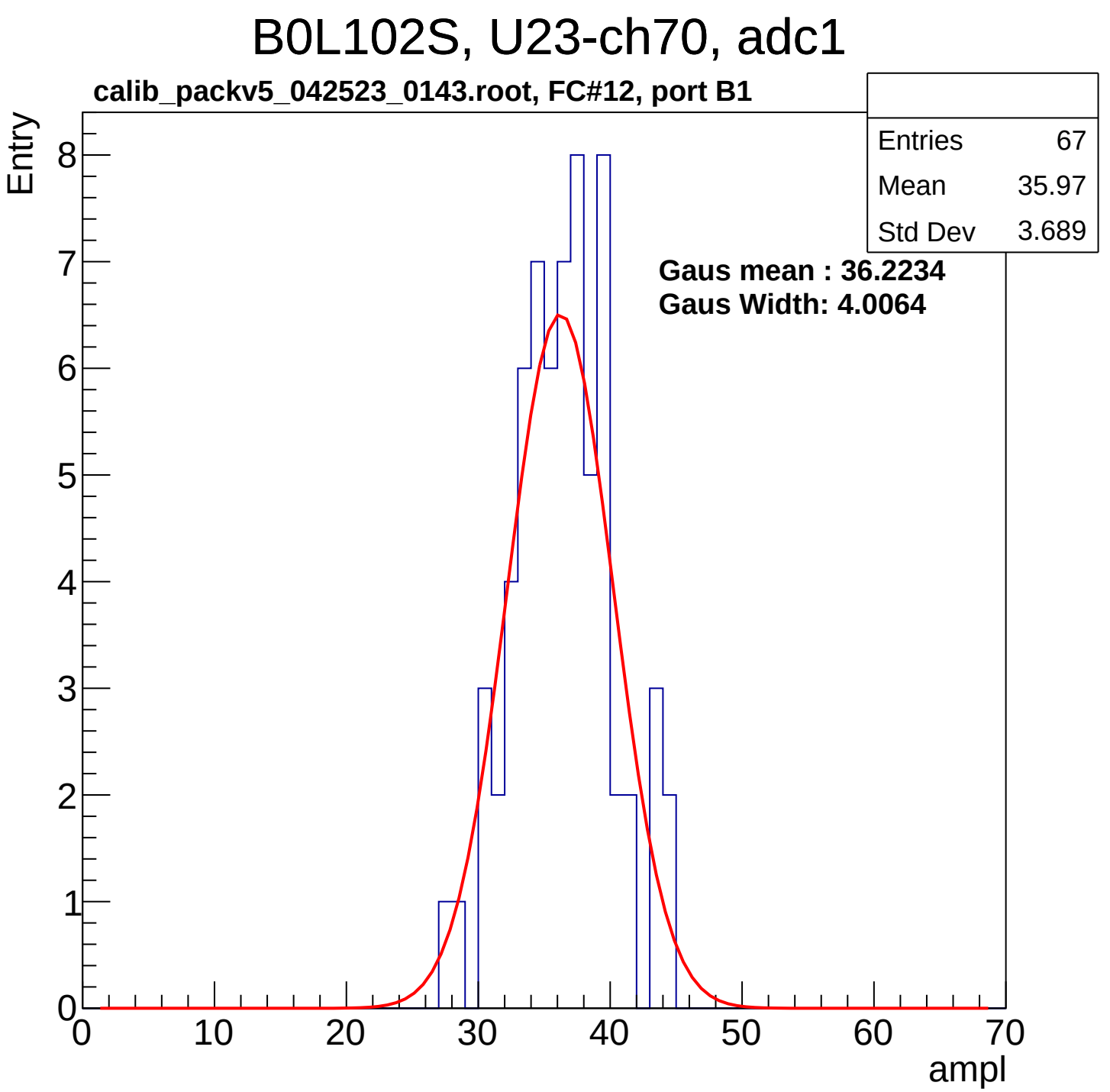
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch70, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 29.31
Std Dev 5.023
Gaus mean : 30.2519
Gaus Width: 4.1149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch70, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.97
Std Dev 3.689
Gaus mean : 36.2234
Gaus Width: 4.0064
Entry
ampl
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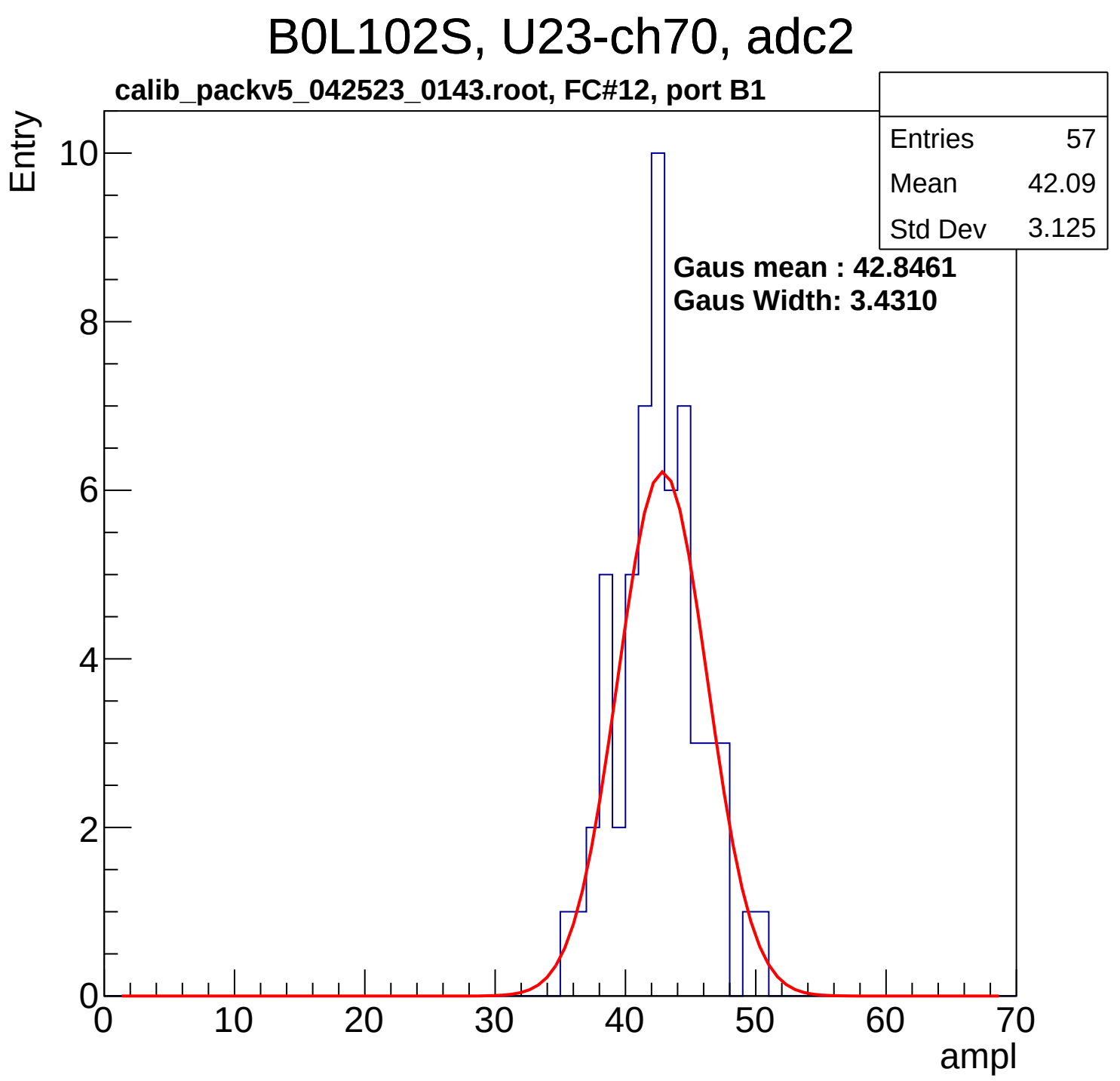
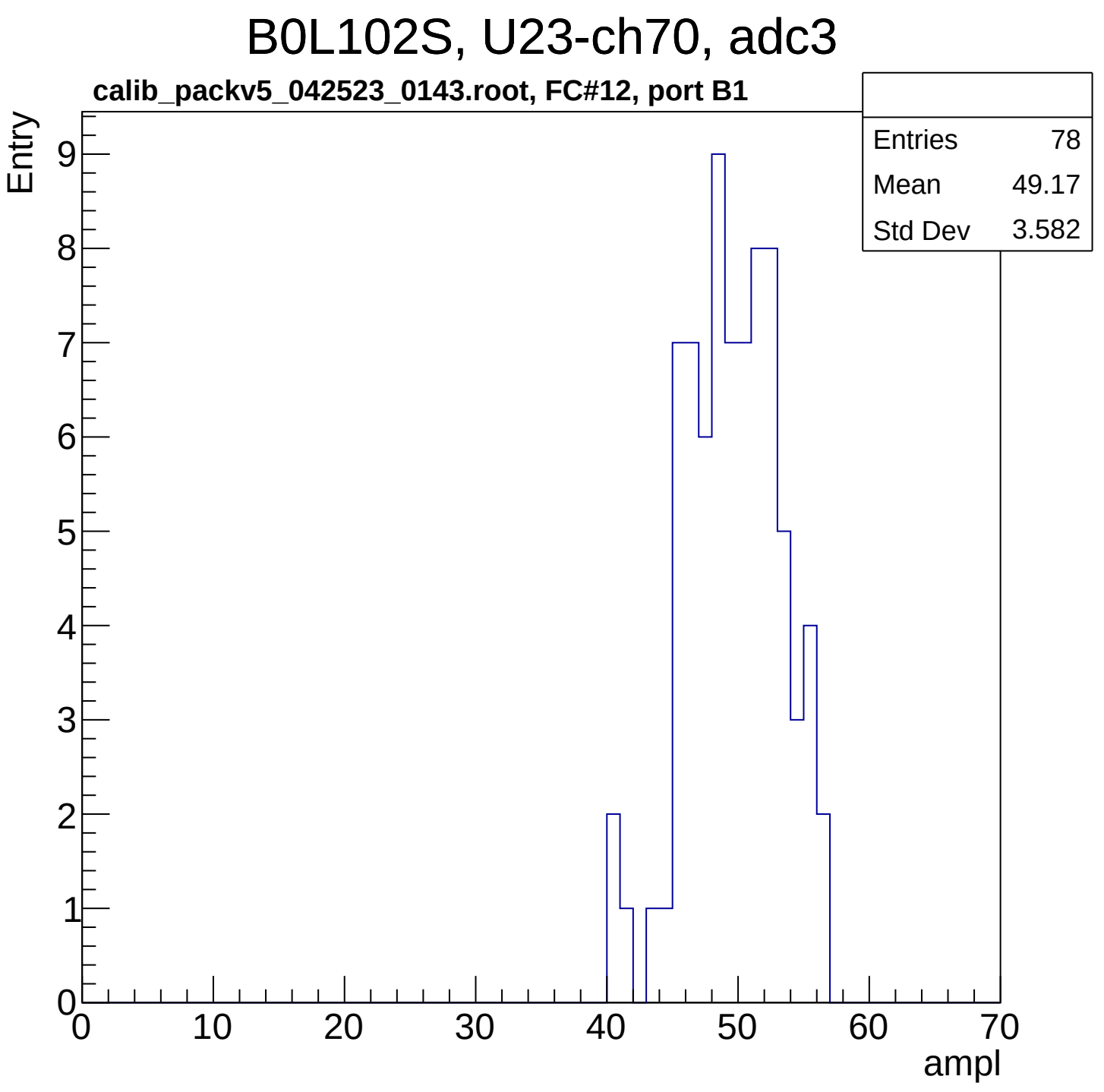
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch70, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 42.09
Std Dev 3.125
Gaus mean : 42.8461
Gaus Width: 3.4310
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch70, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 49.17
Std Dev 3.582
Entry
ampl
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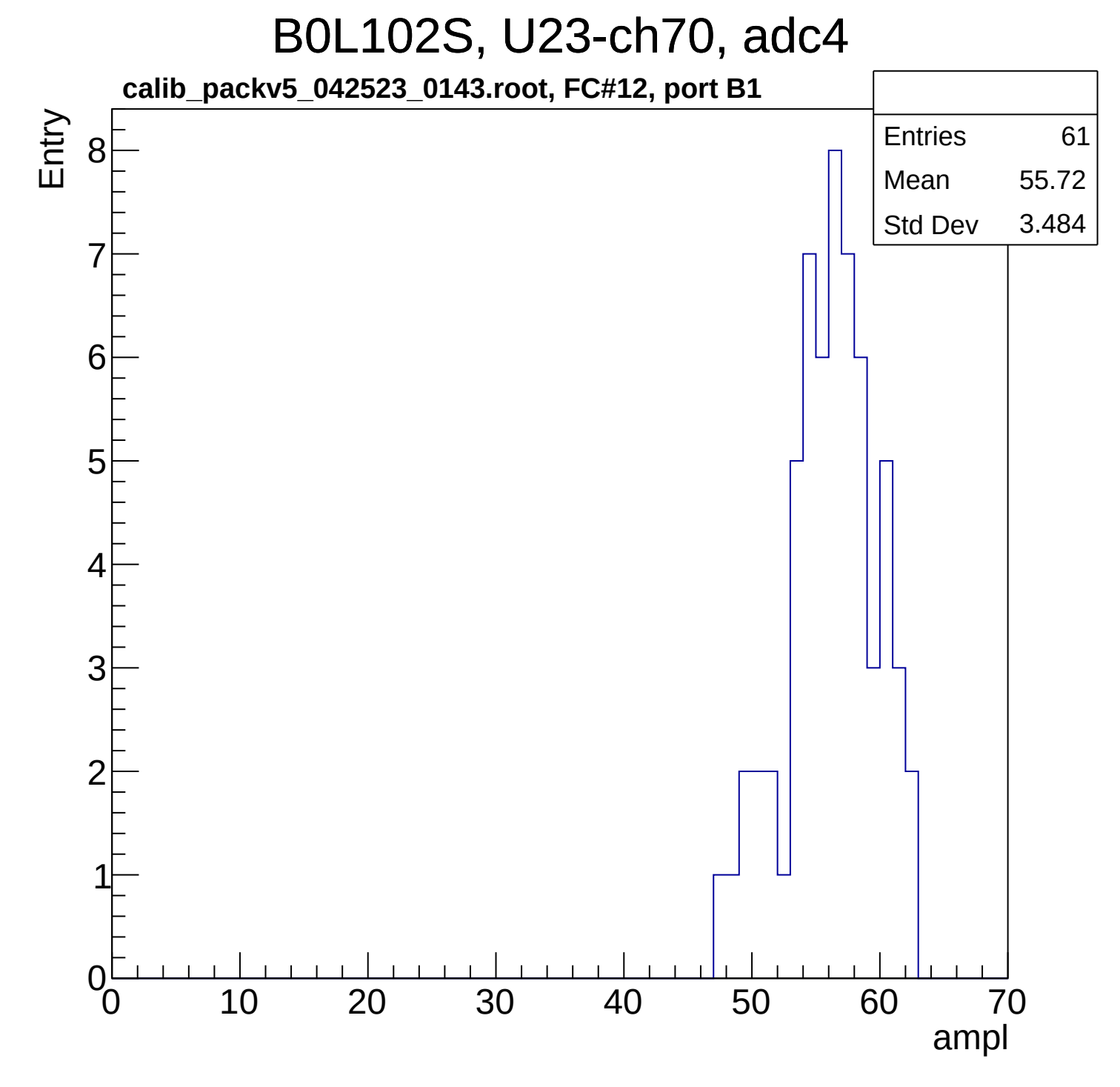
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch70, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.72
Std Dev 3.484
Entry
ampl
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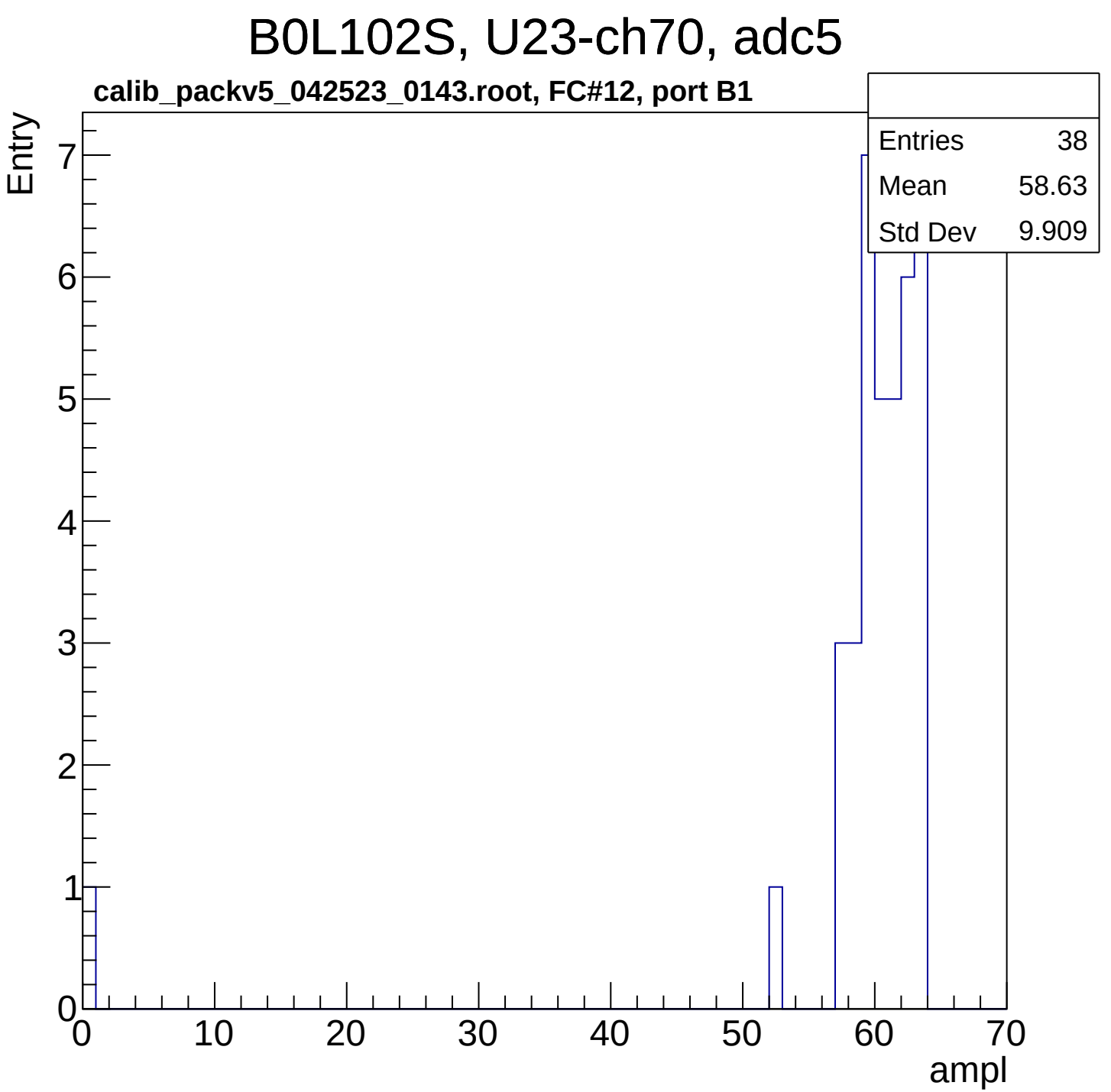
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch70, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.63
Std Dev 9.909
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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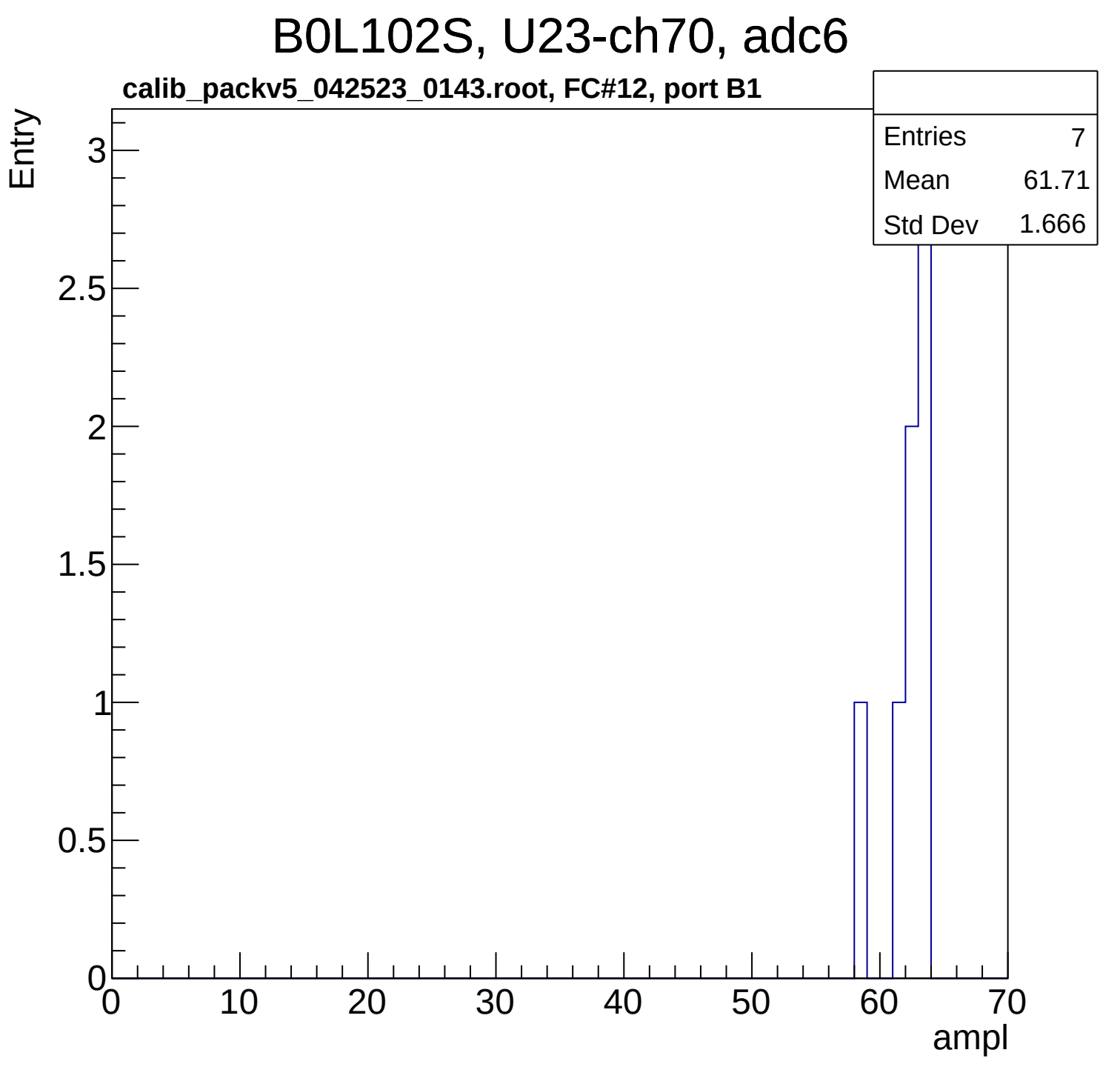
B0L102S, U23-ch70, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 61.71
Std Dev 1.666
Entry
ampl
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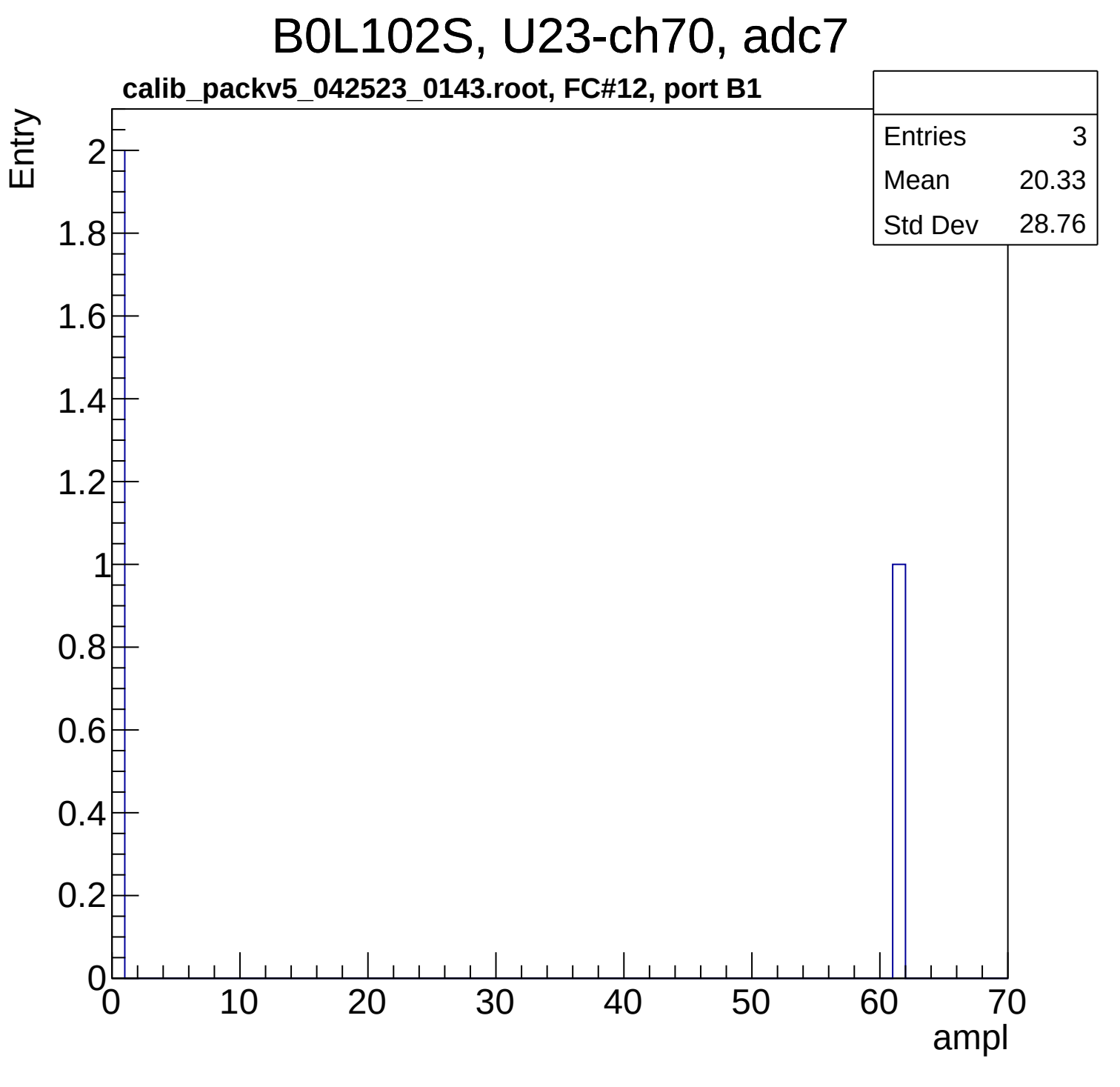
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch70, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
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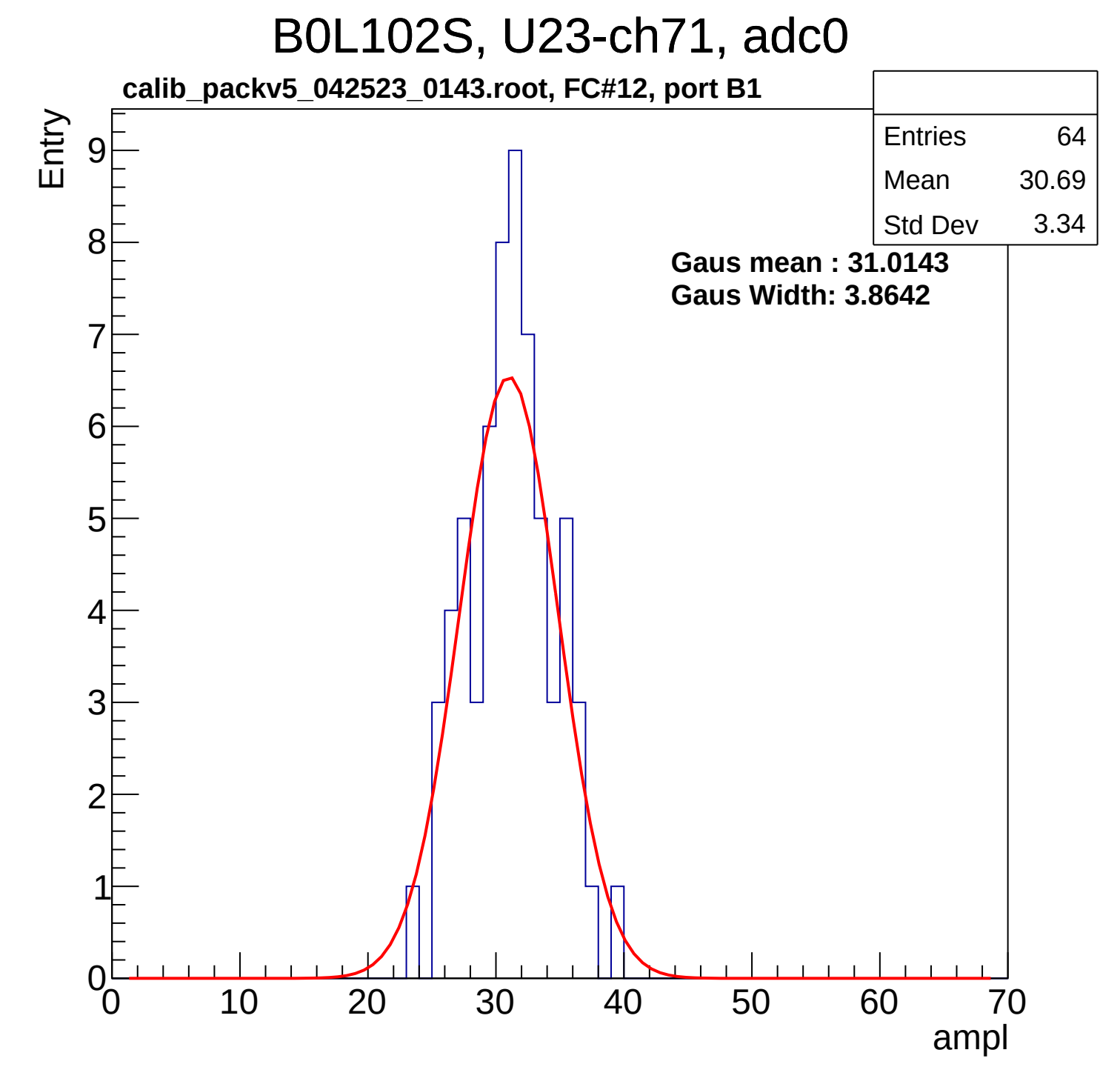
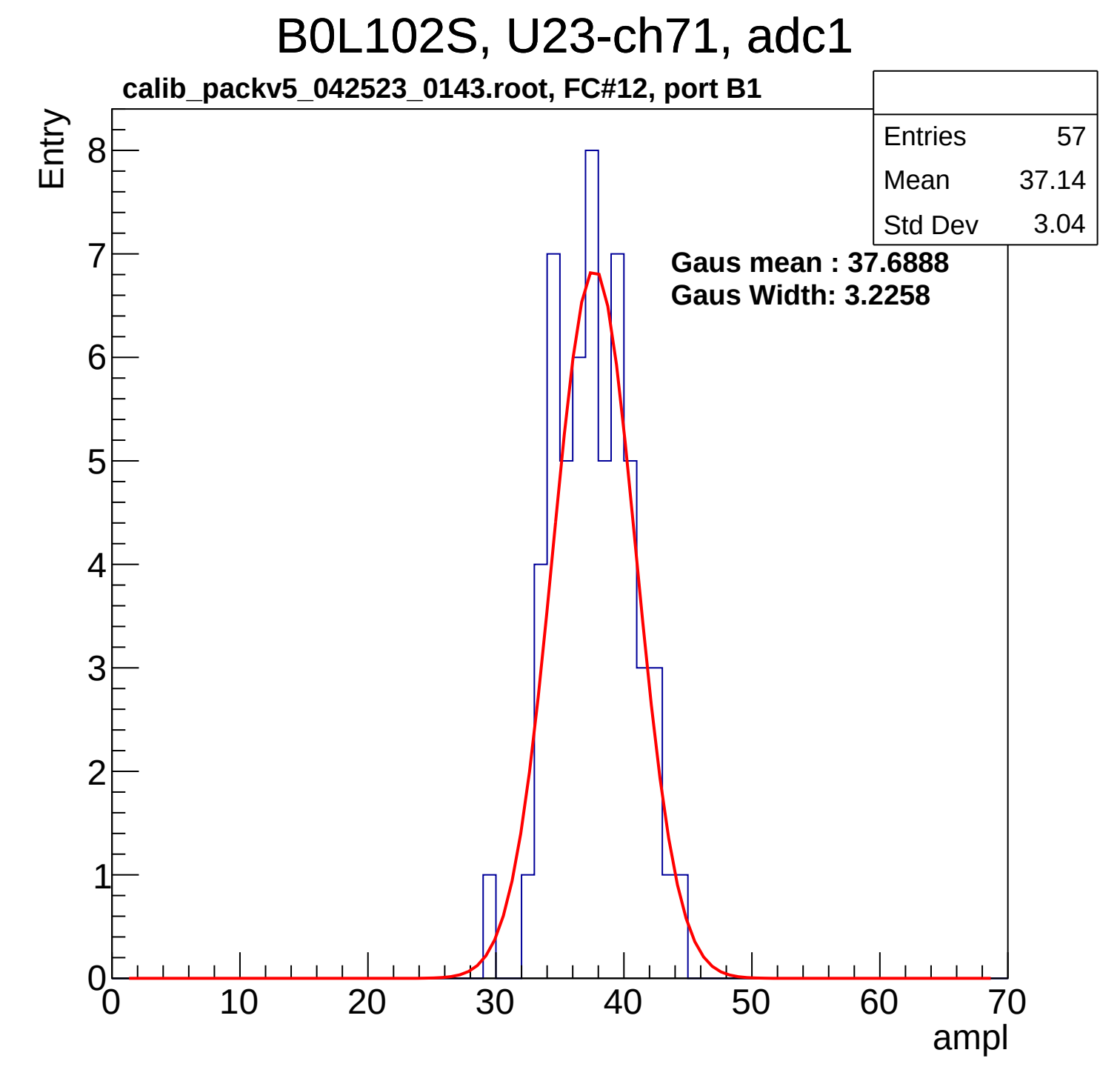
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch71, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 30.69
Std Dev 3.34
Gaus mean : 31.0143
Gaus Width: 3.8642
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch71, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 37.14
Std Dev 3.04
Gaus mean : 37.6888
Gaus Width: 3.2258
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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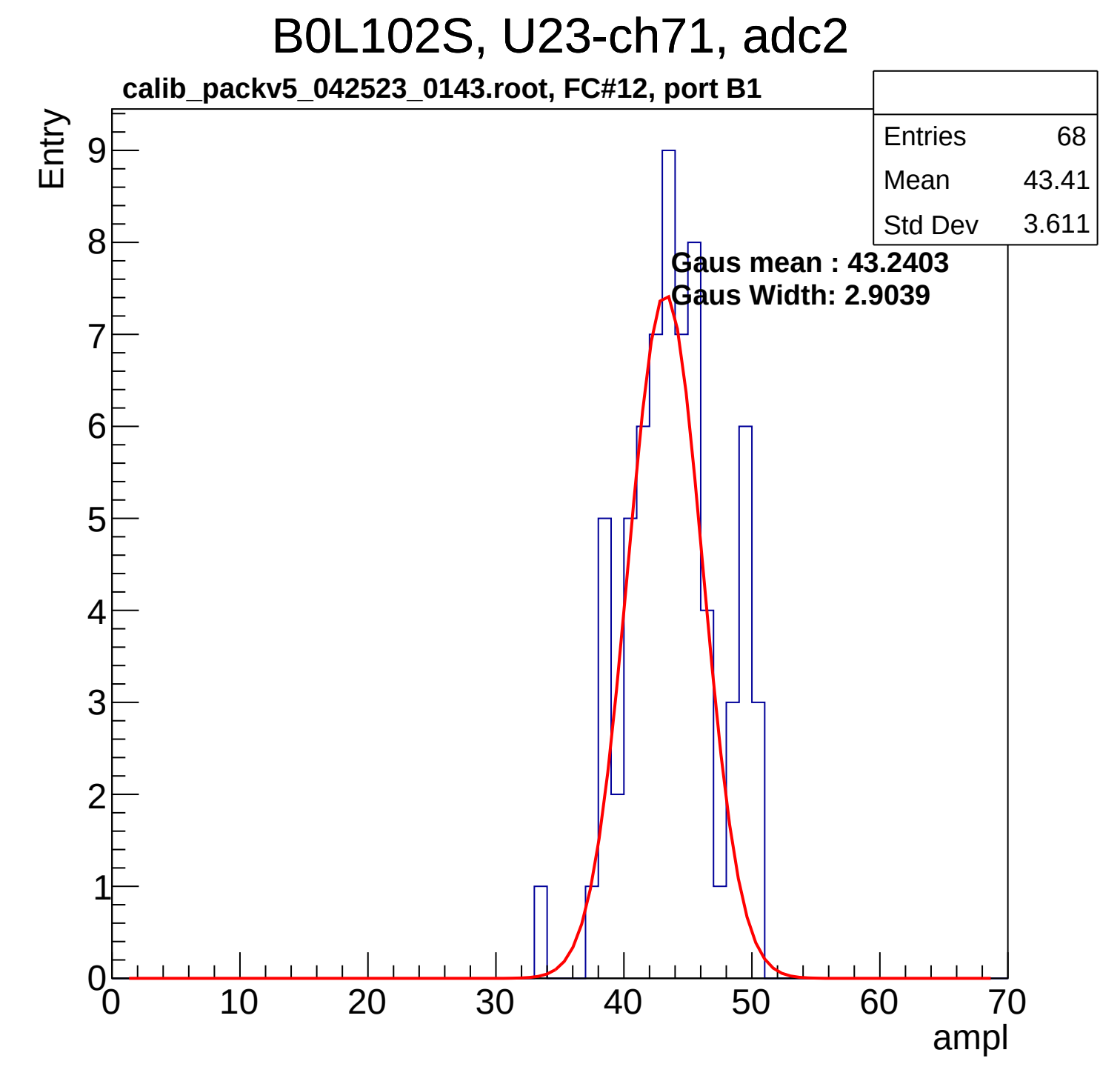
B0L102S, U23-ch71, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 43.41
Std Dev 3.611
Gaus mean : 43.2403
Gaus Width: 2.9039
Entry
ampl
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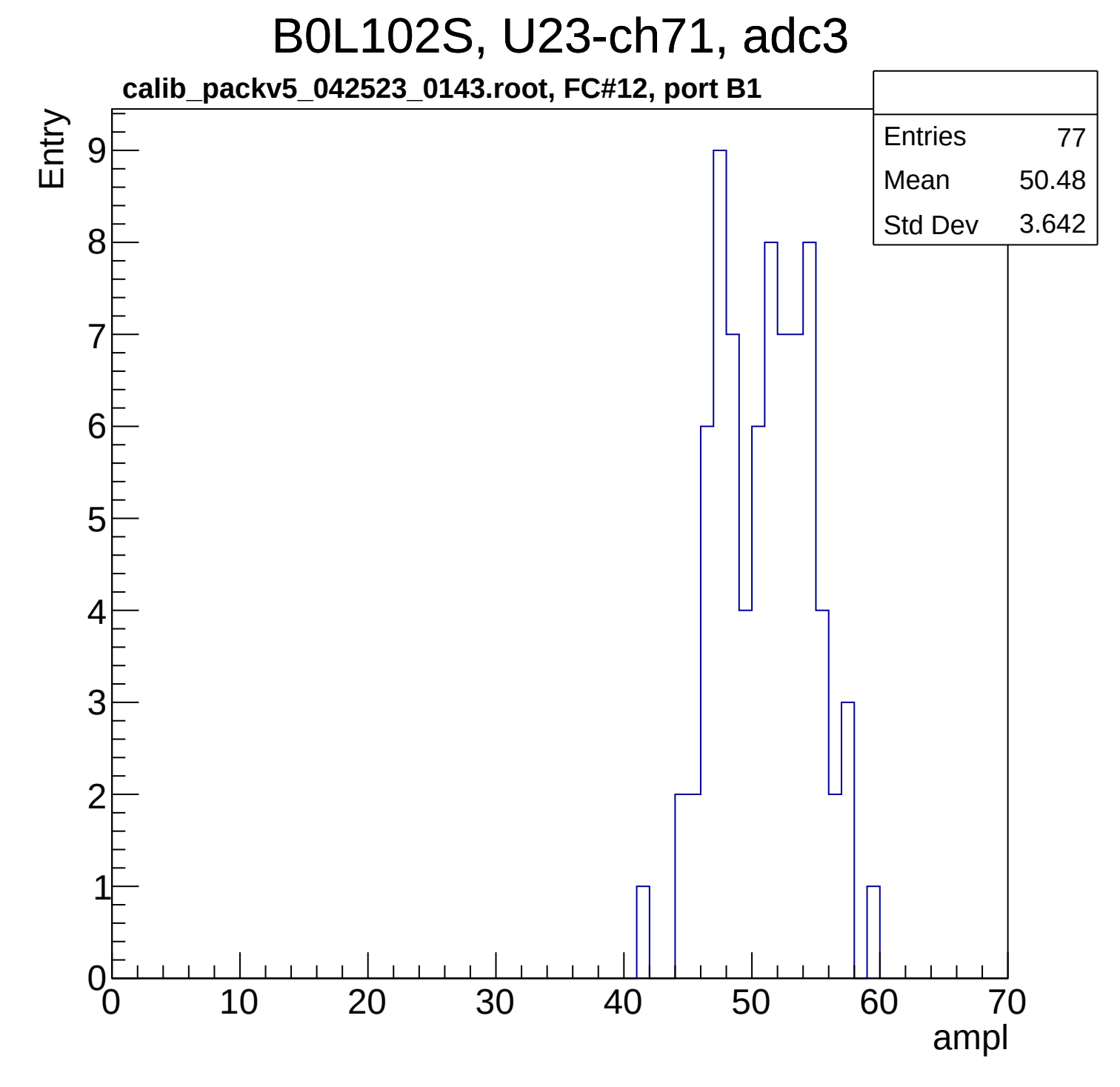
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch71, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 50.48
Std Dev 3.642
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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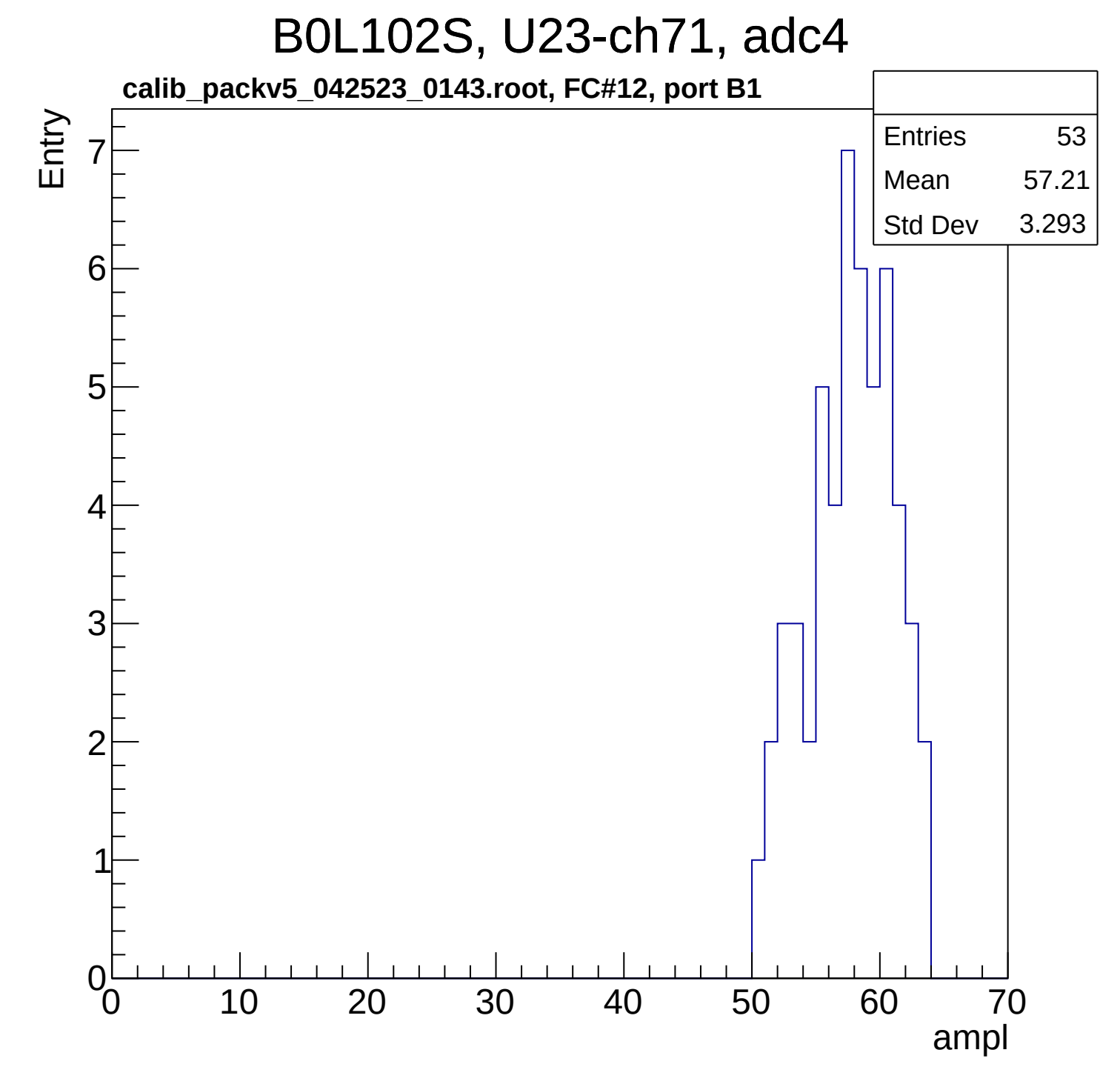
B0L102S, U23-ch71, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 57.21
Std Dev 3.293
Entry
ampl
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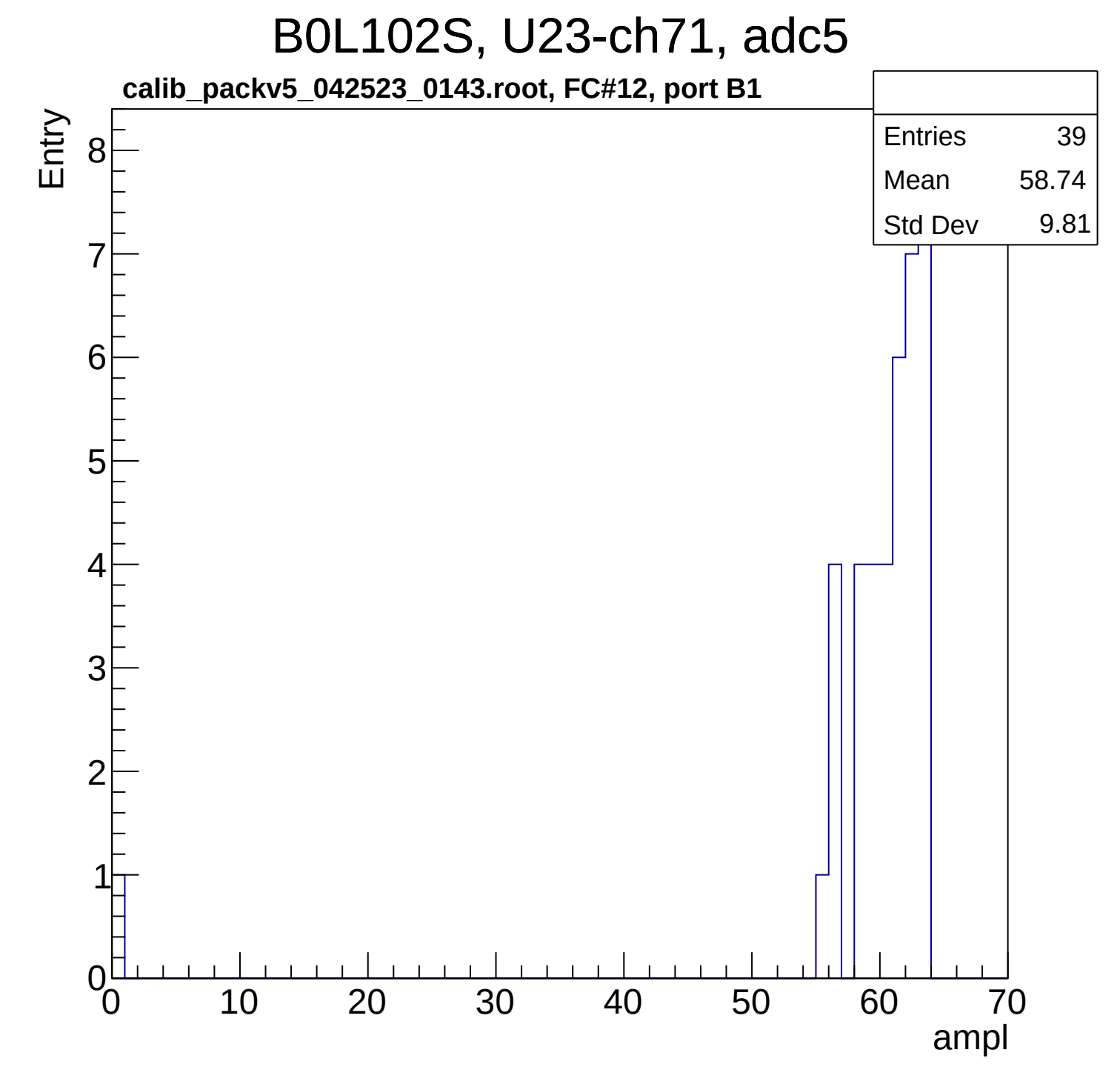
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch71, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 58.74
Std Dev 9.81
Entry
ampl
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2
3
4
5
6
7
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0
10
20
30
40
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60
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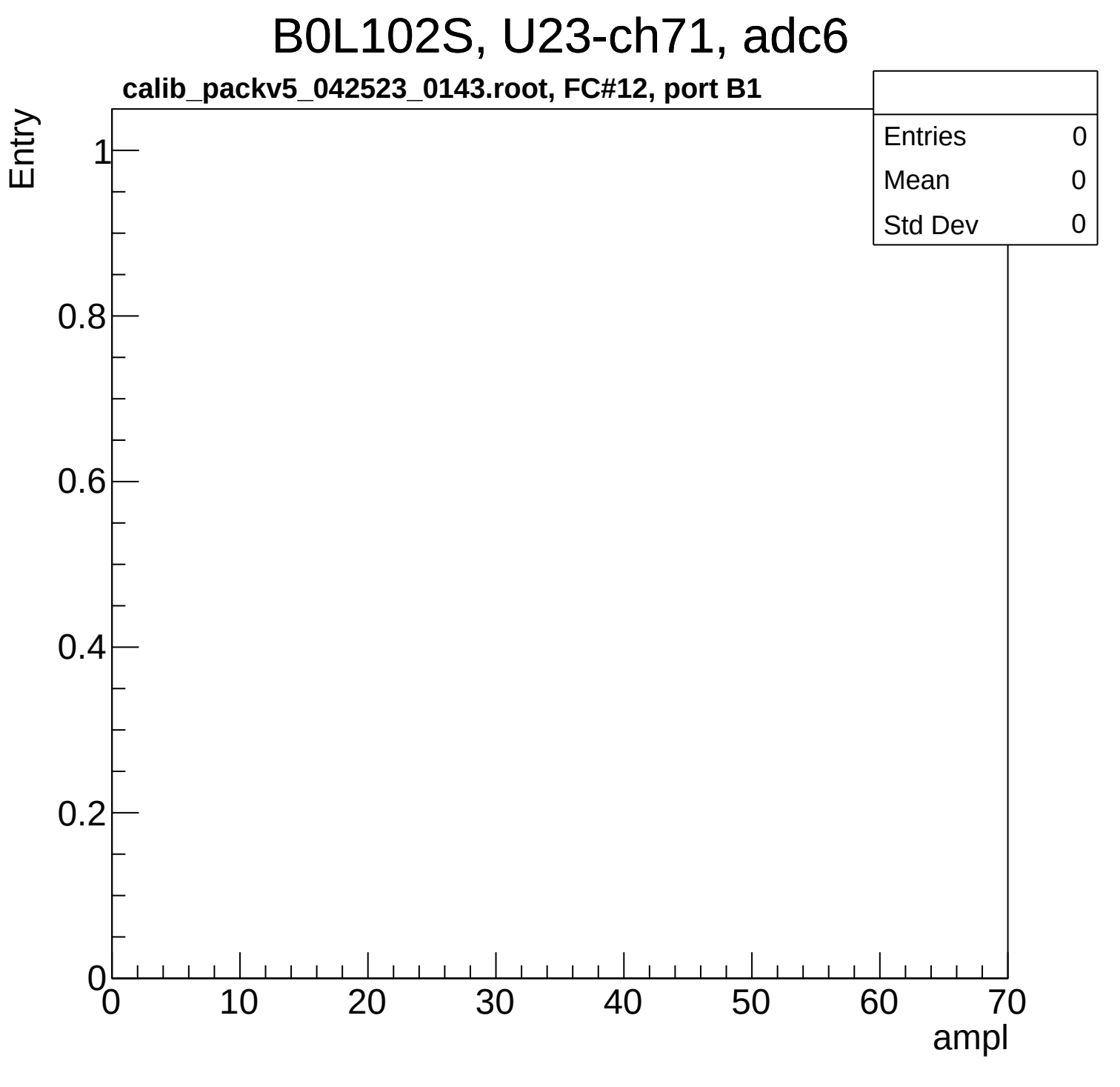
B0L102S, U23-ch71, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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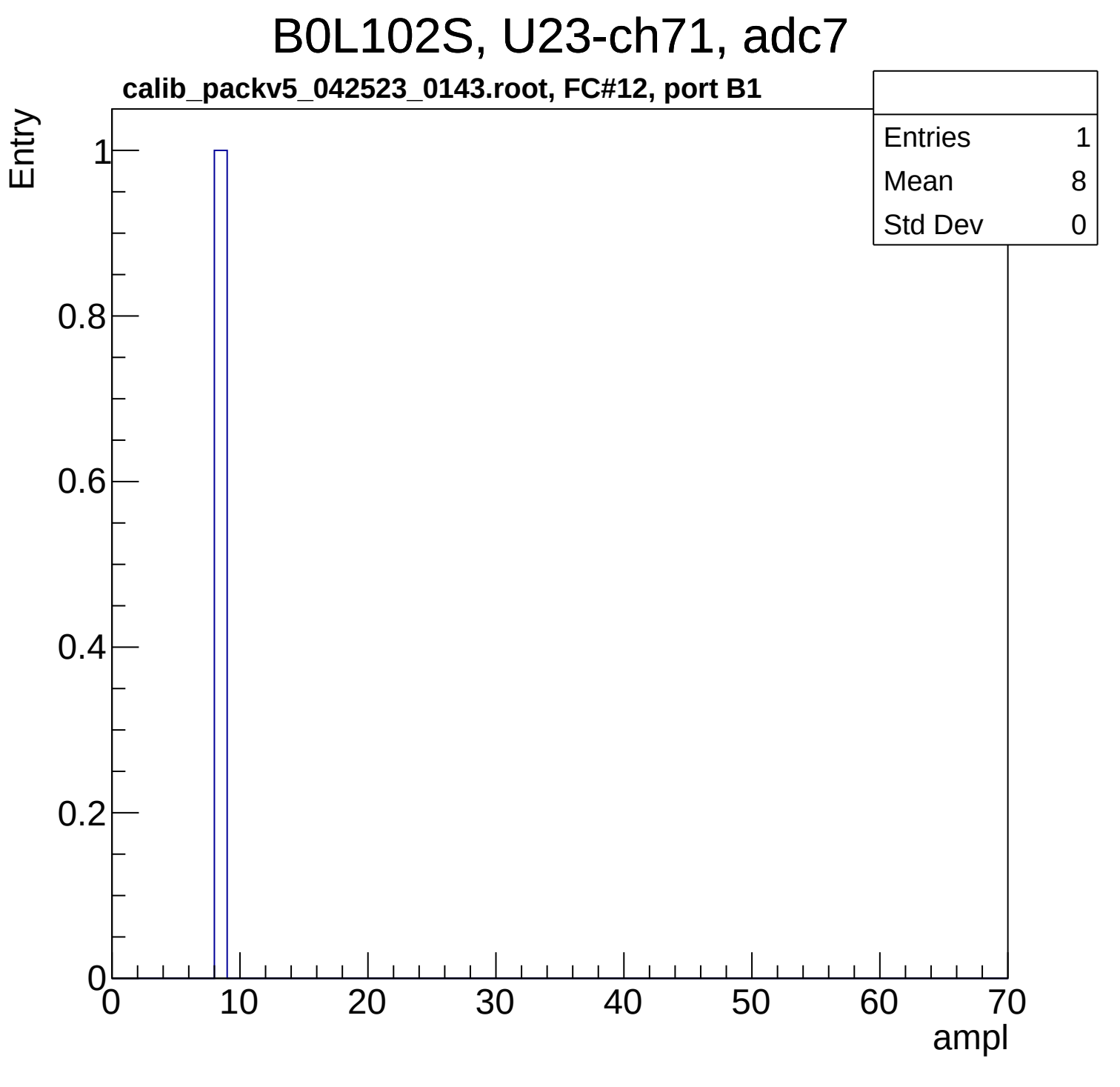
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch71, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 8
Std Dev 0
Entry
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0.6
0.4
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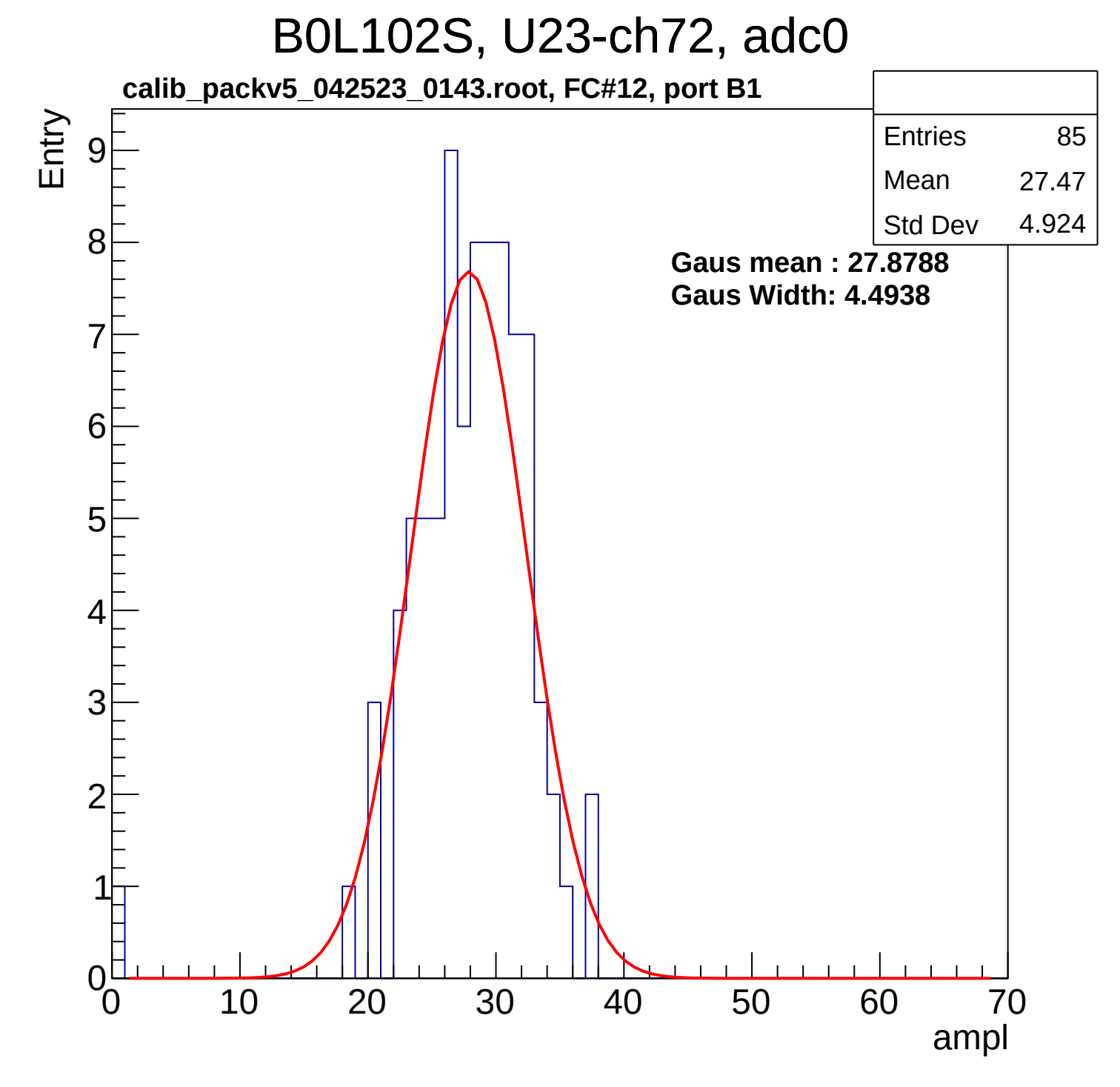
50
60
70
ampl


B0L102S, U23-ch72, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 27.47
Std Dev 4.924
Gaus mean : 27.8788
Gaus Width: 4.4938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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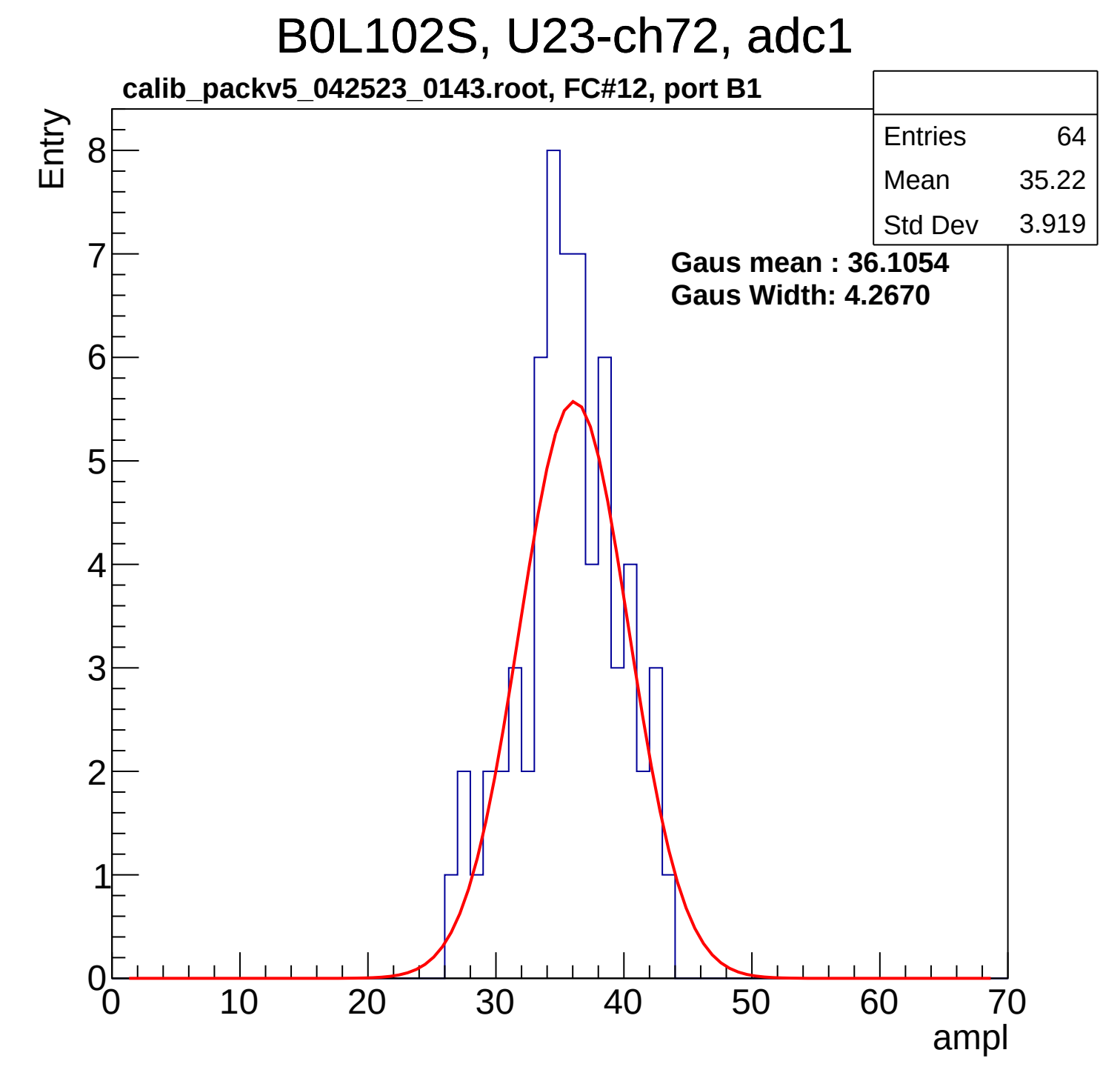
B0L102S, U23-ch72, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 35.22
Std Dev 3.919
Gaus mean : 36.1054
Gaus Width: 4.2670
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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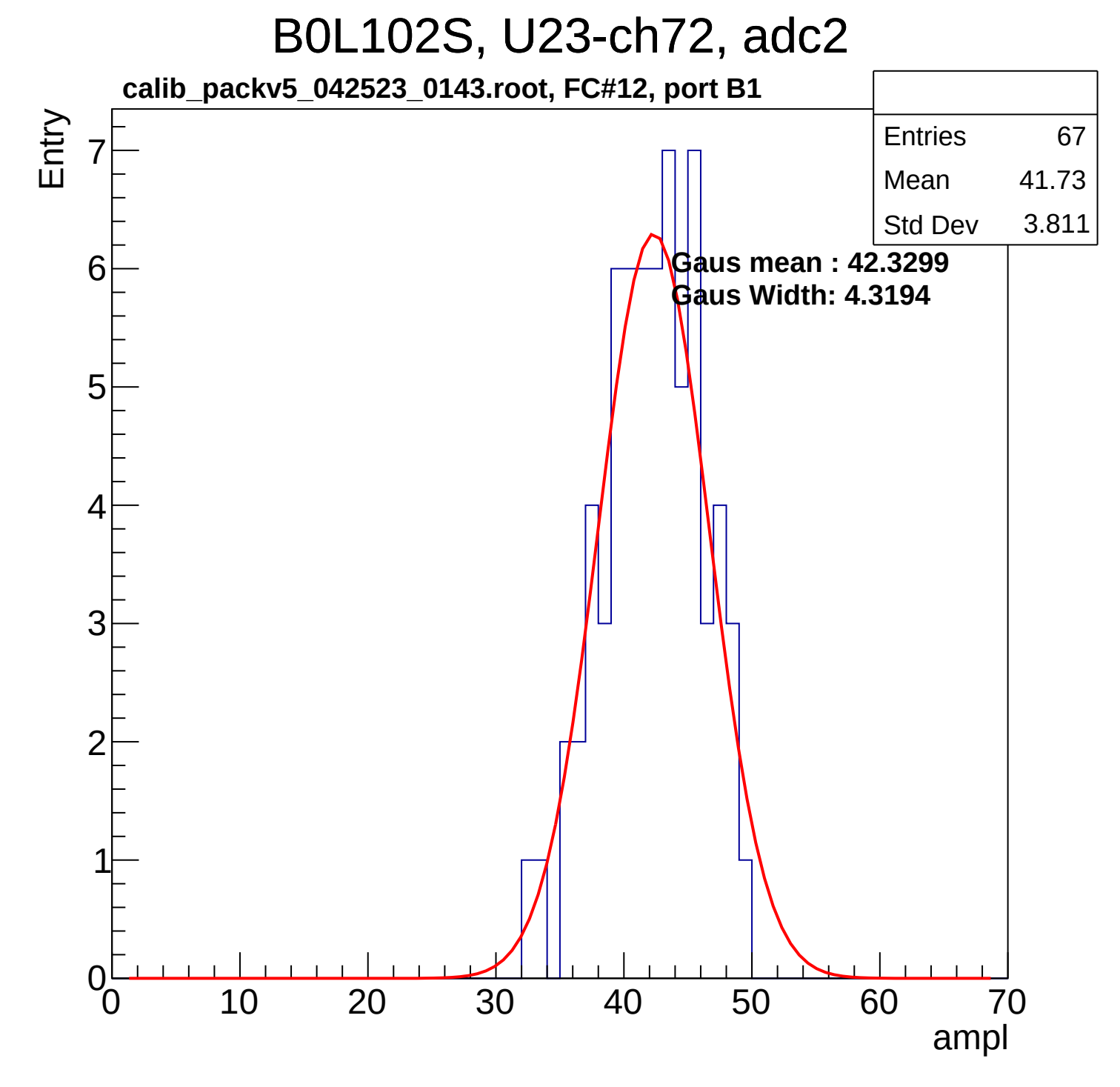
B0L102S, U23-ch72, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 41.73
Std Dev 3.811
Gaus mean : 42.3299
Gaus Width: 4.3194
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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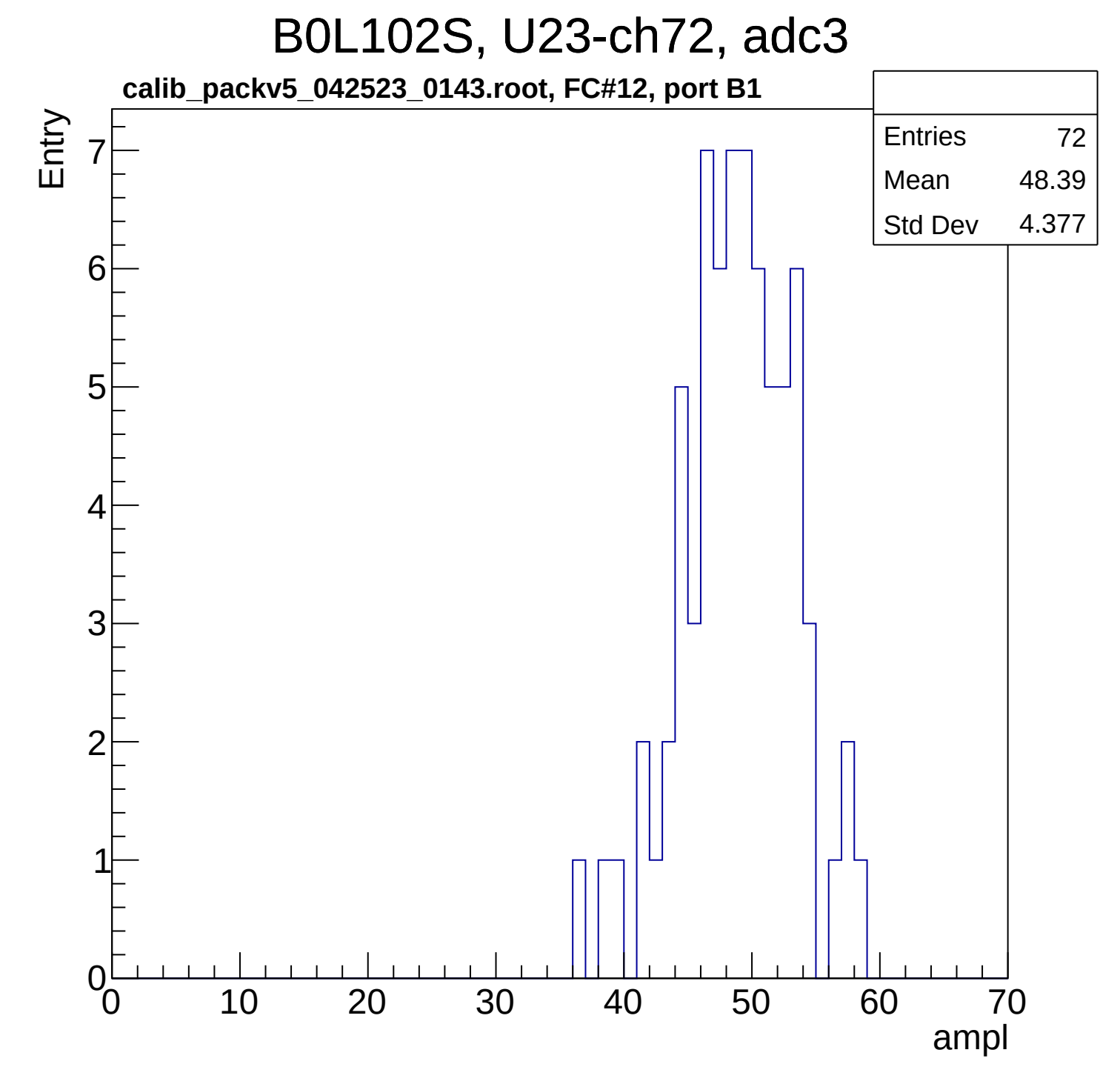
B0L102S, U23-ch72, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 48.39
Std Dev 4.377
Entry
ampl
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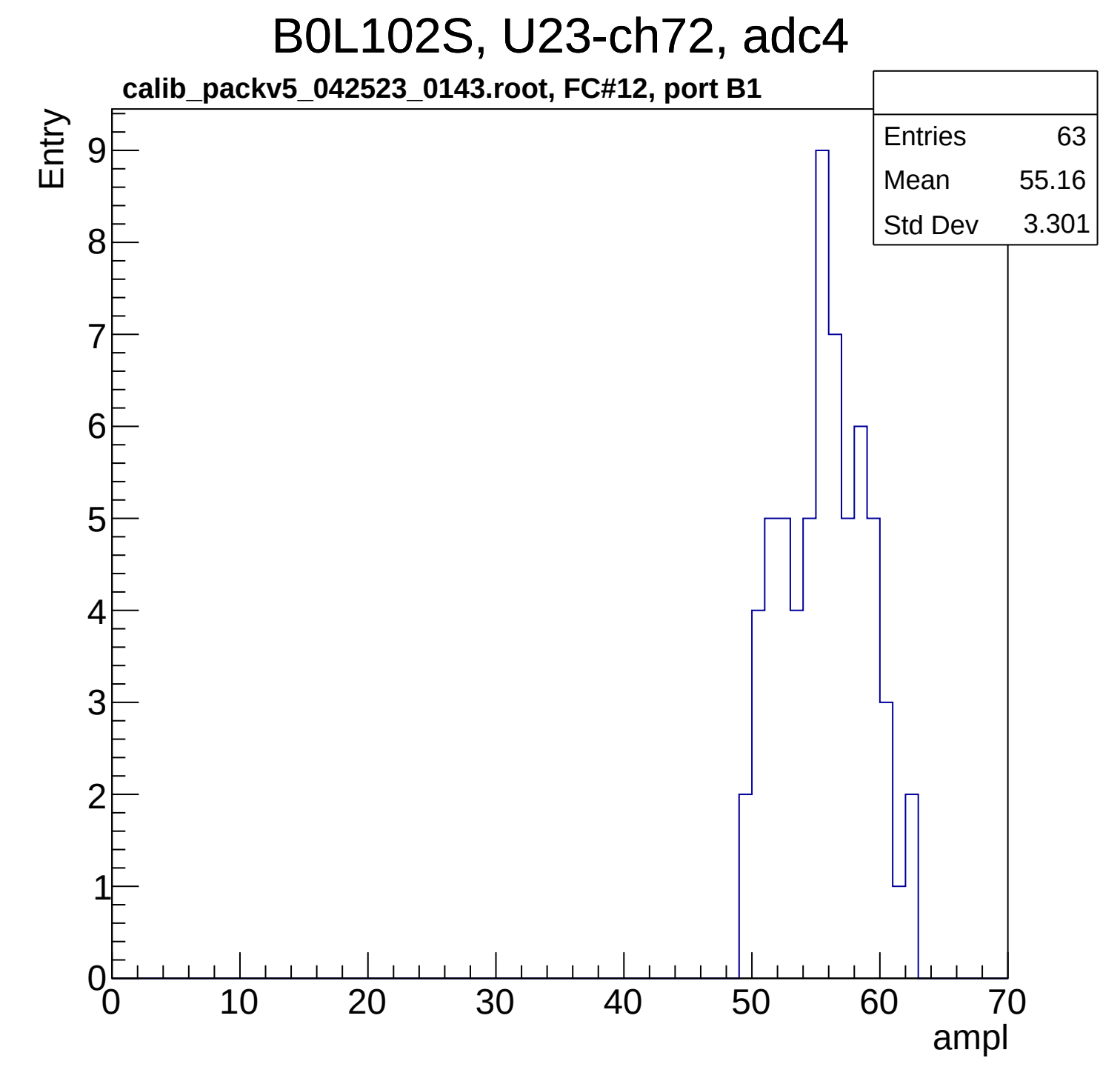
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch72, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 55.16
Std Dev 3.301
Entry
ampl
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2
3
4
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6
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0
10
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40
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60
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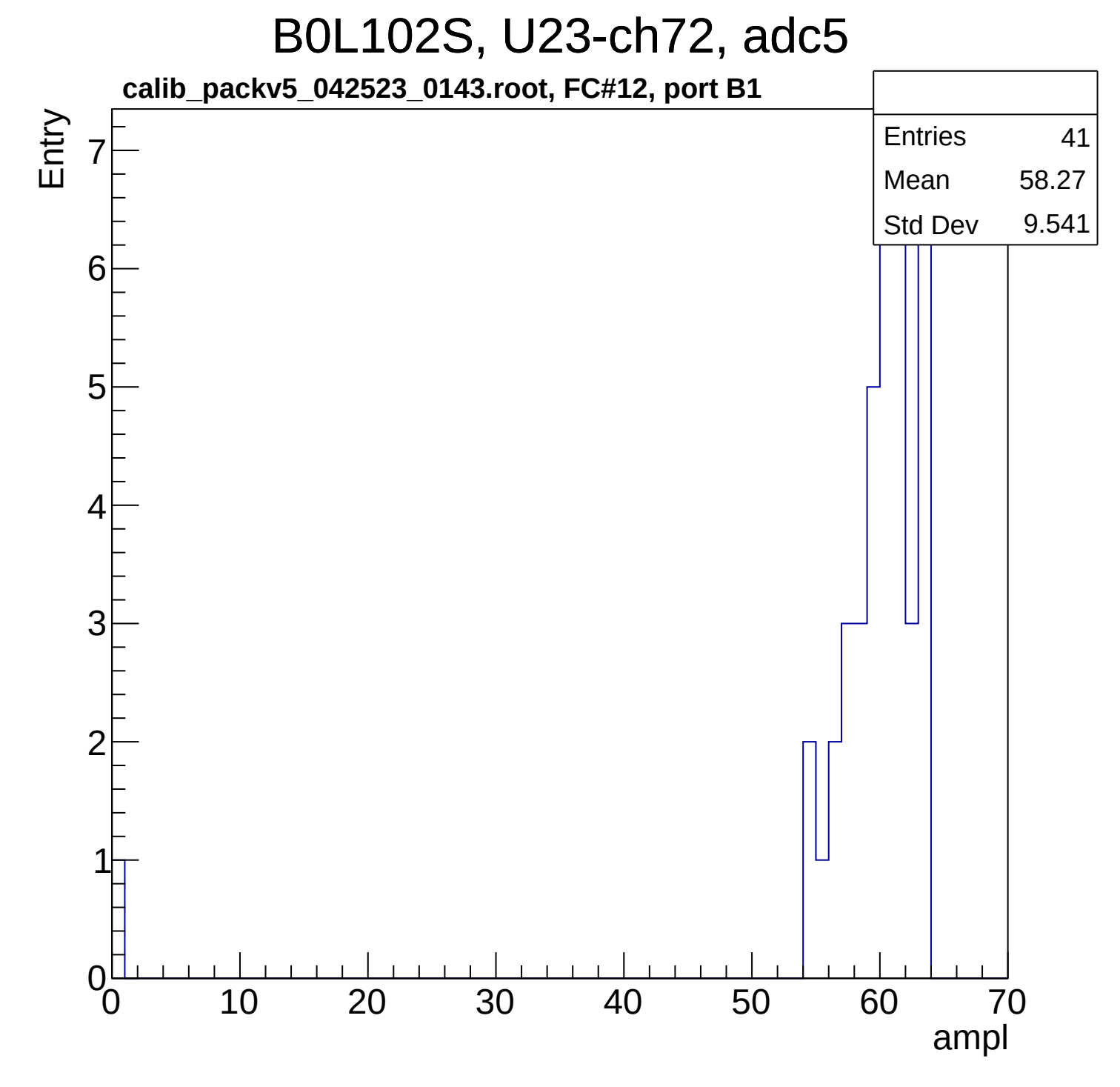
B0L102S, U23-ch72, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.27
Std Dev 9.541
Entry
ampl
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2
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6
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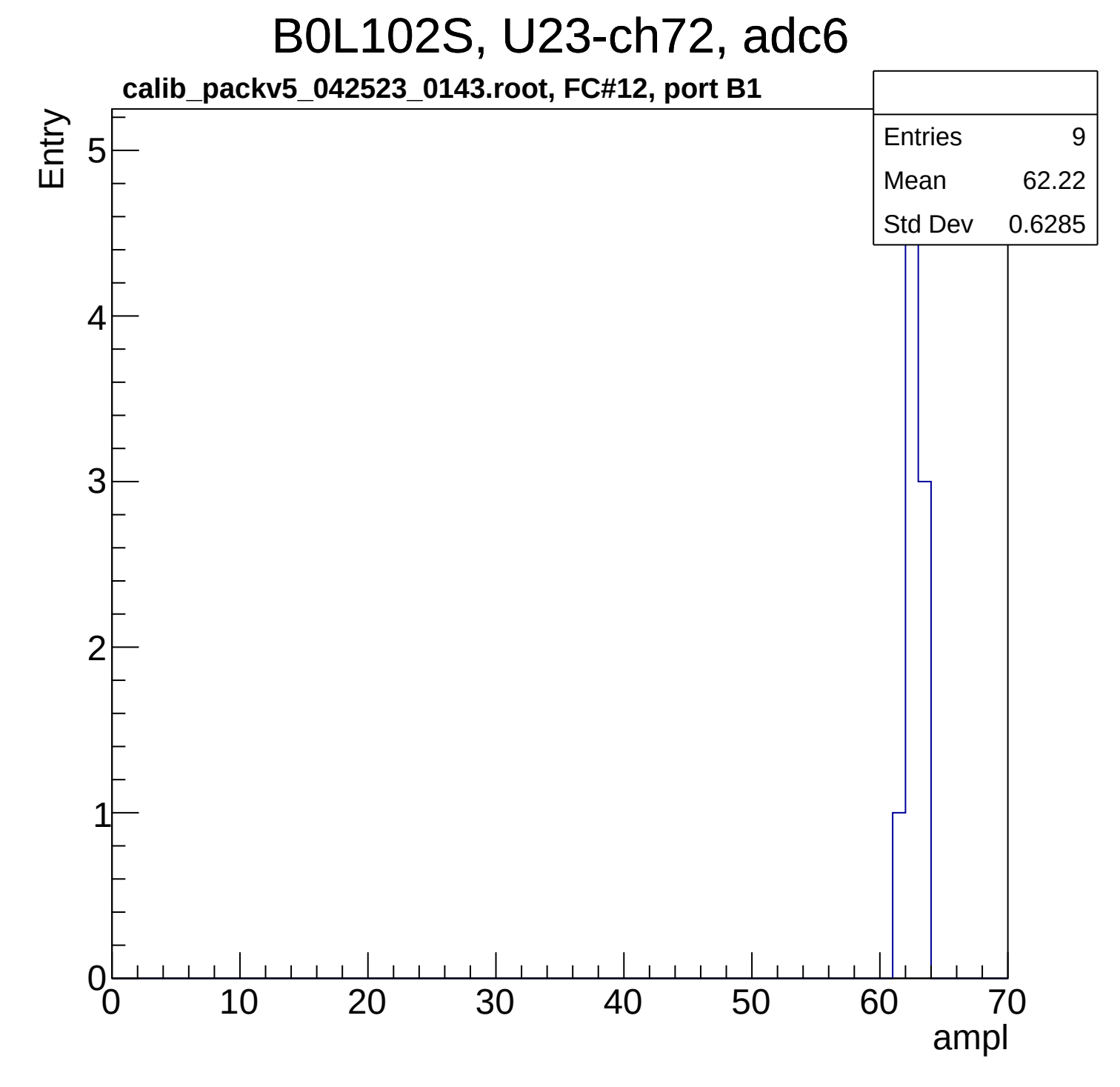
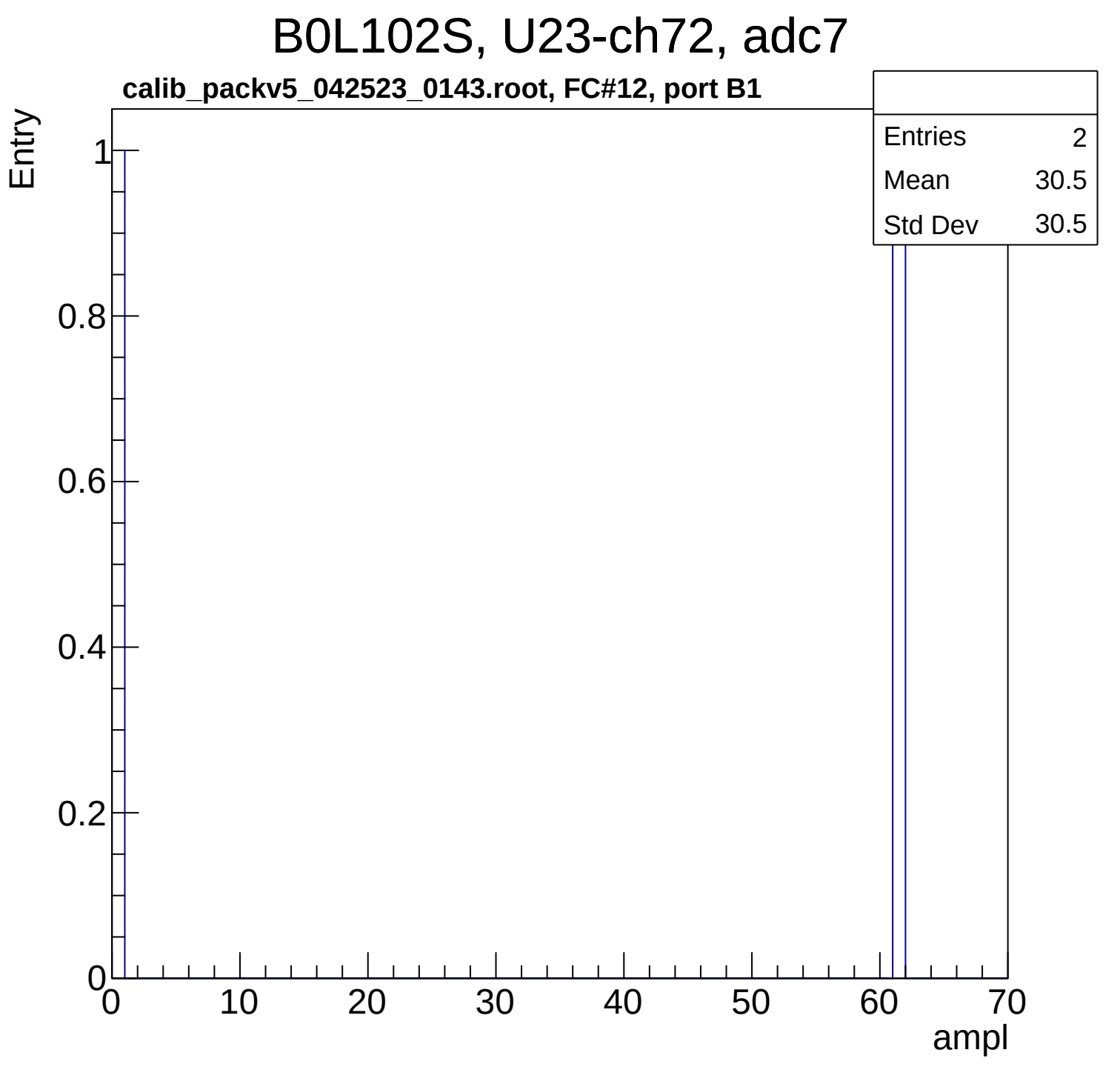
0
10
20
30
40
50
60
70


B0L102S, U23-ch72, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 62.22
Std Dev 0.6285
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L102S, U23-ch72, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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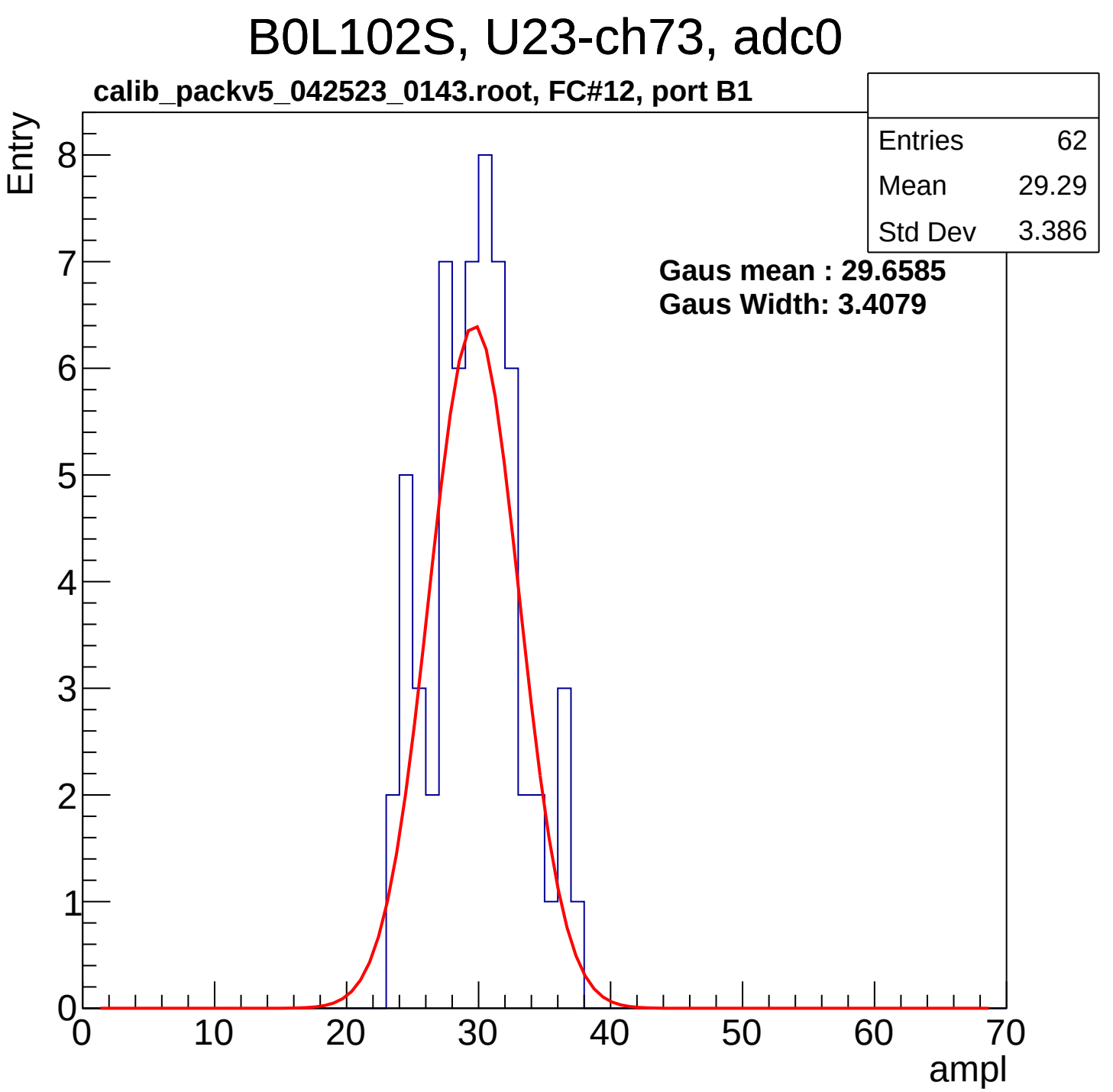
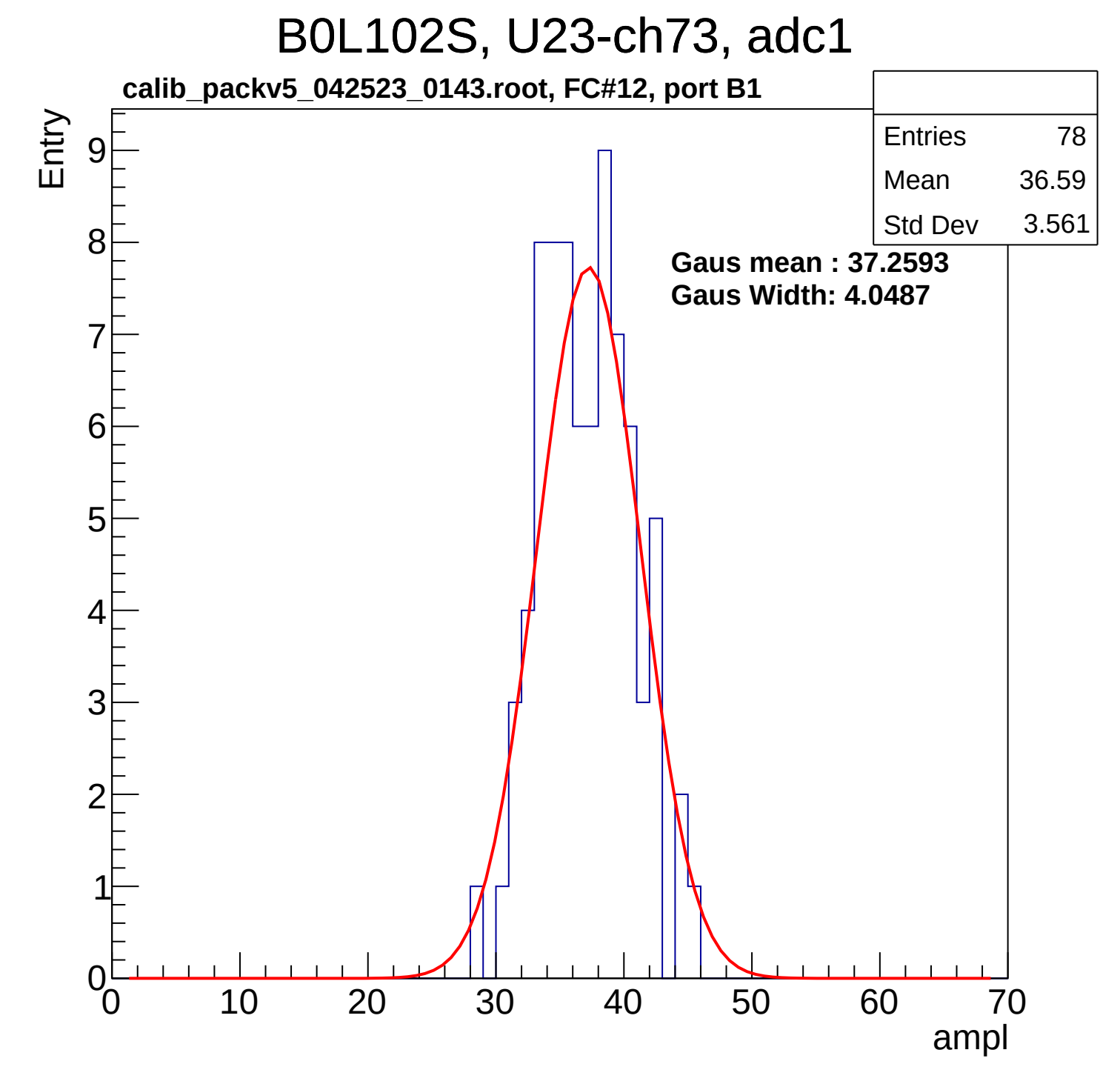
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch73, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 29.29
Std Dev 3.386
Gaus mean : 29.6585
Gaus Width: 3.4079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch73, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 36.59
Std Dev 3.561
Gaus mean : 37.2593
Gaus Width: 4.0487
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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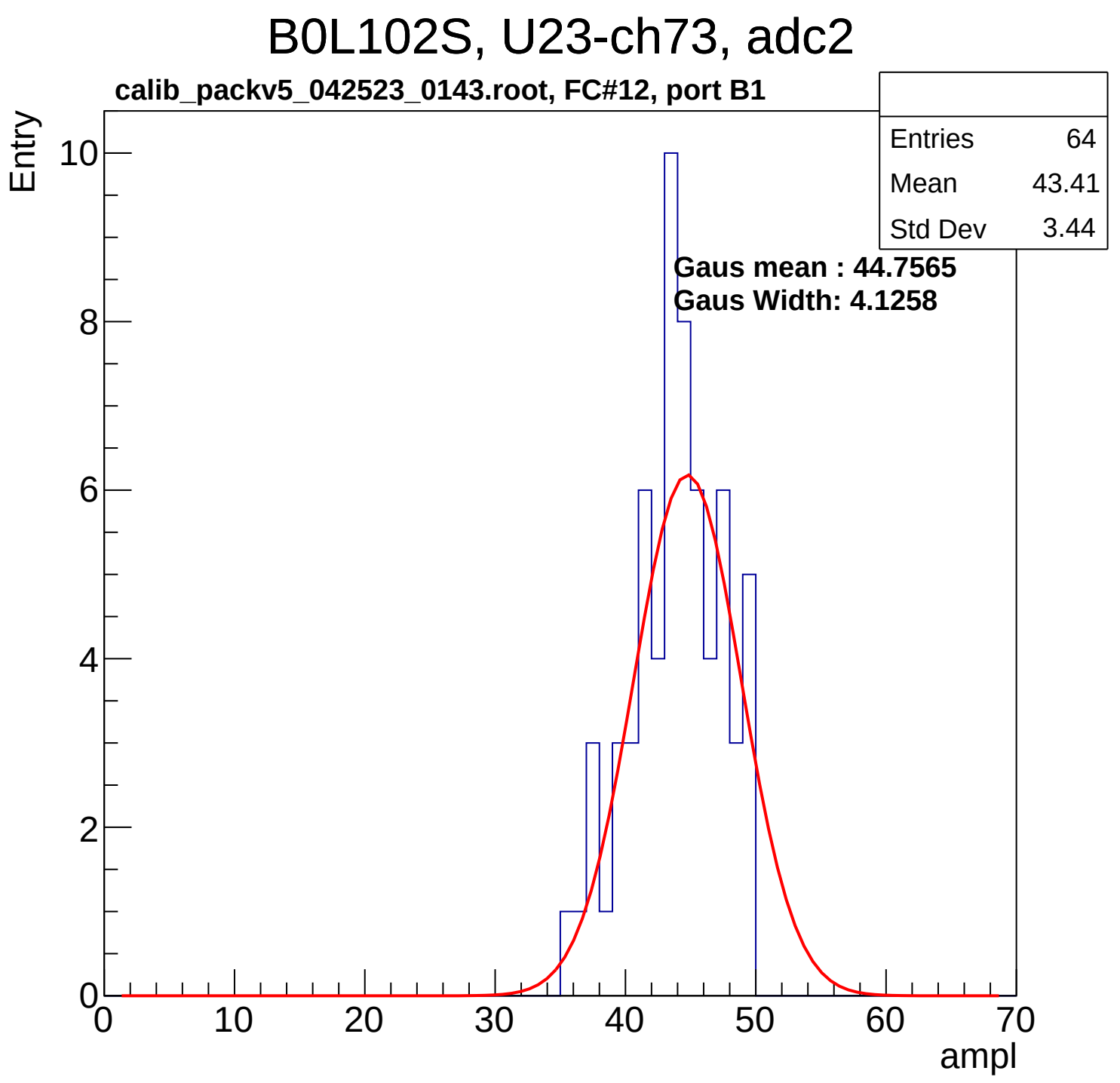
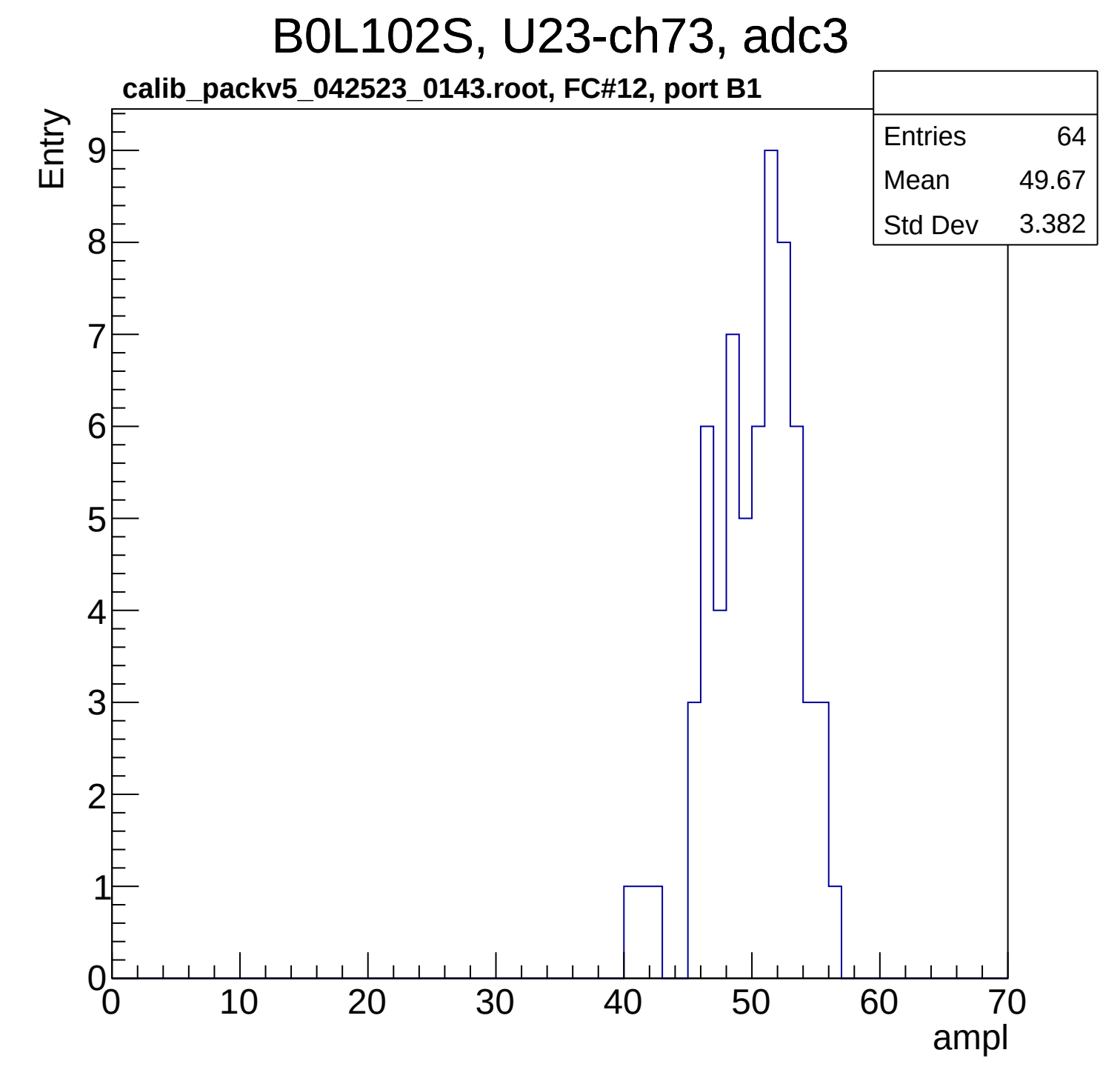

B0L102S, U23-ch73, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.41
Std Dev 3.44
Gaus mean : 44.7565
Gaus Width: 4.1258
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch73, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 49.67
Std Dev 3.382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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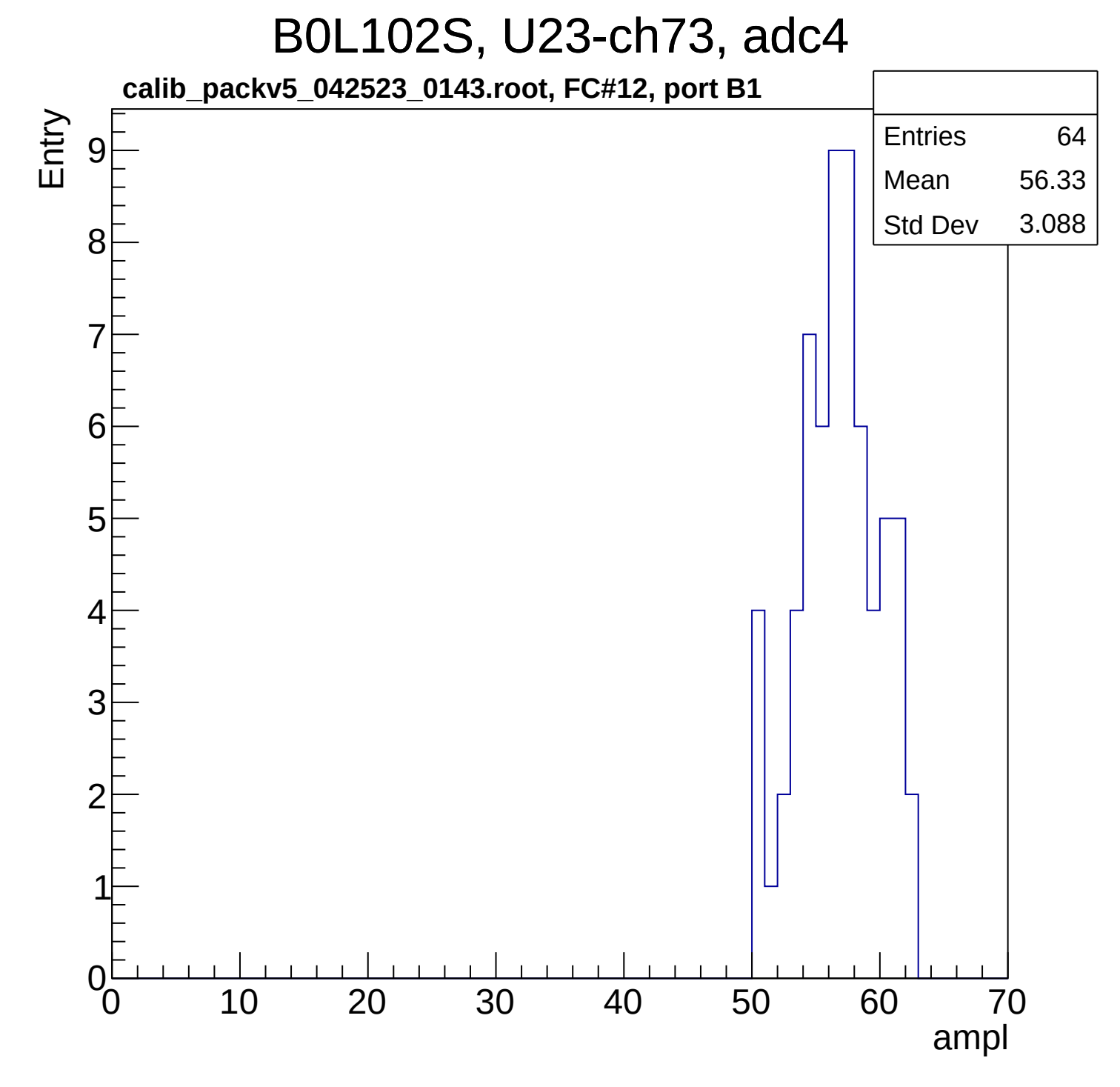
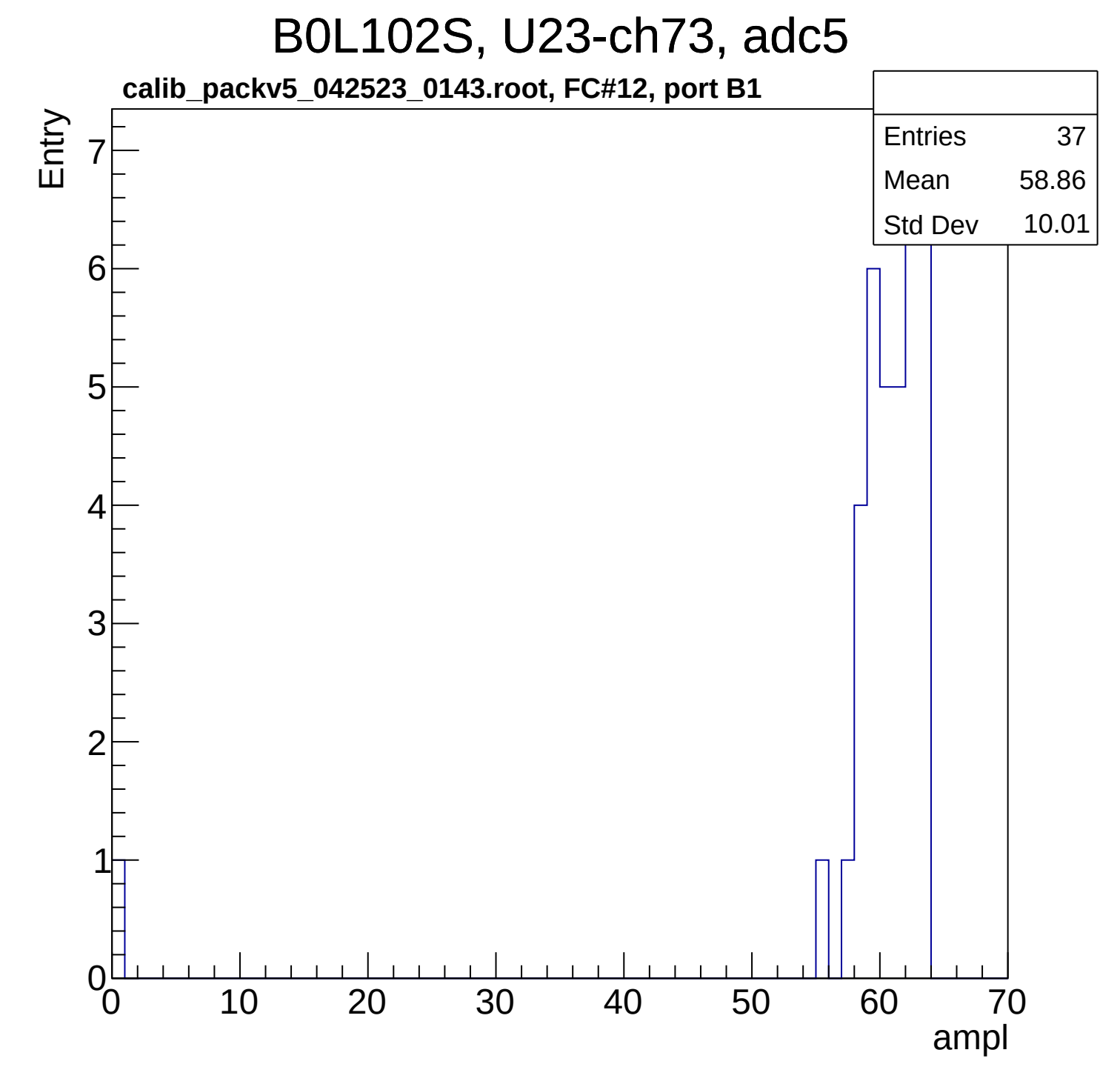
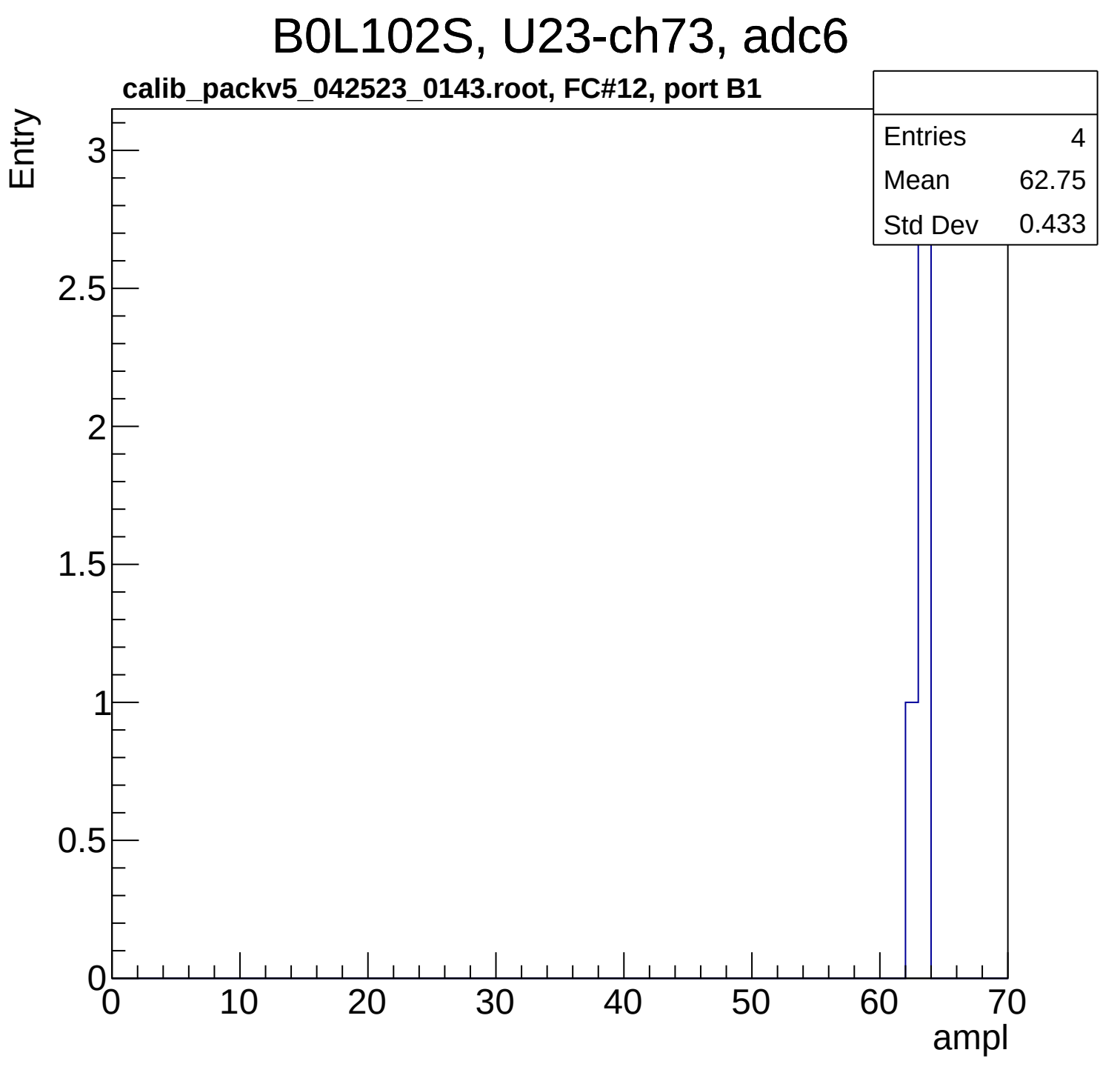
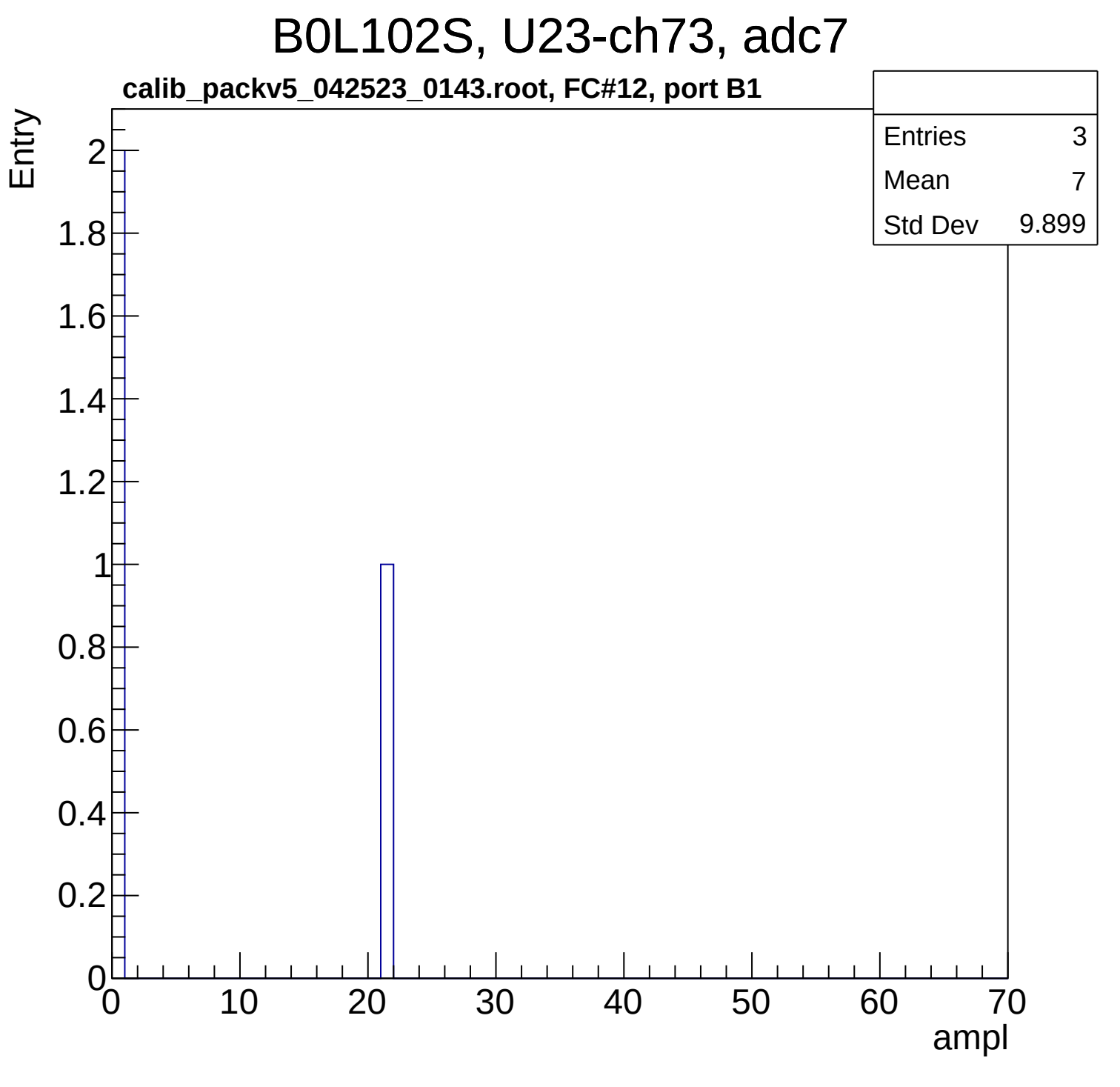
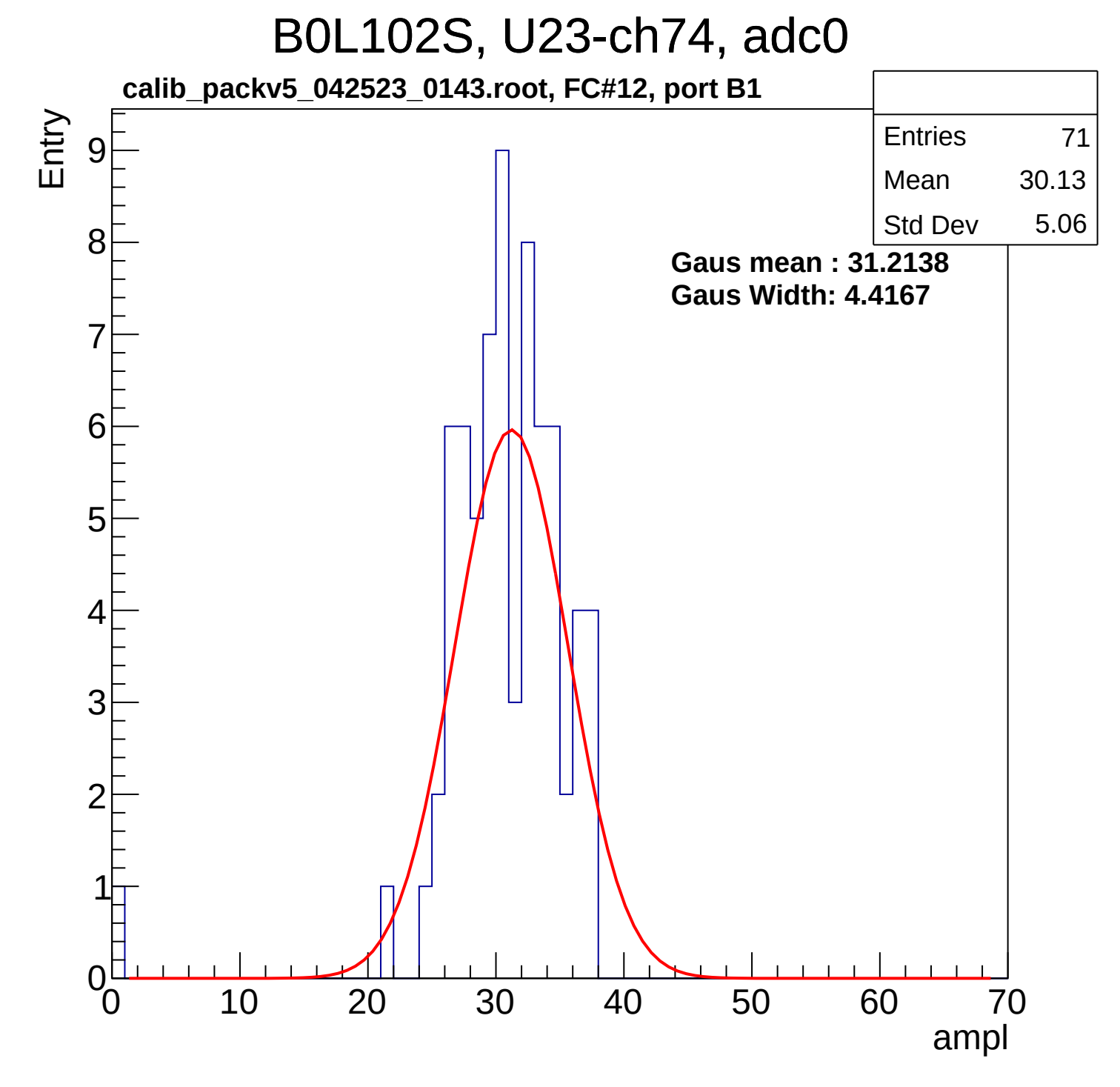
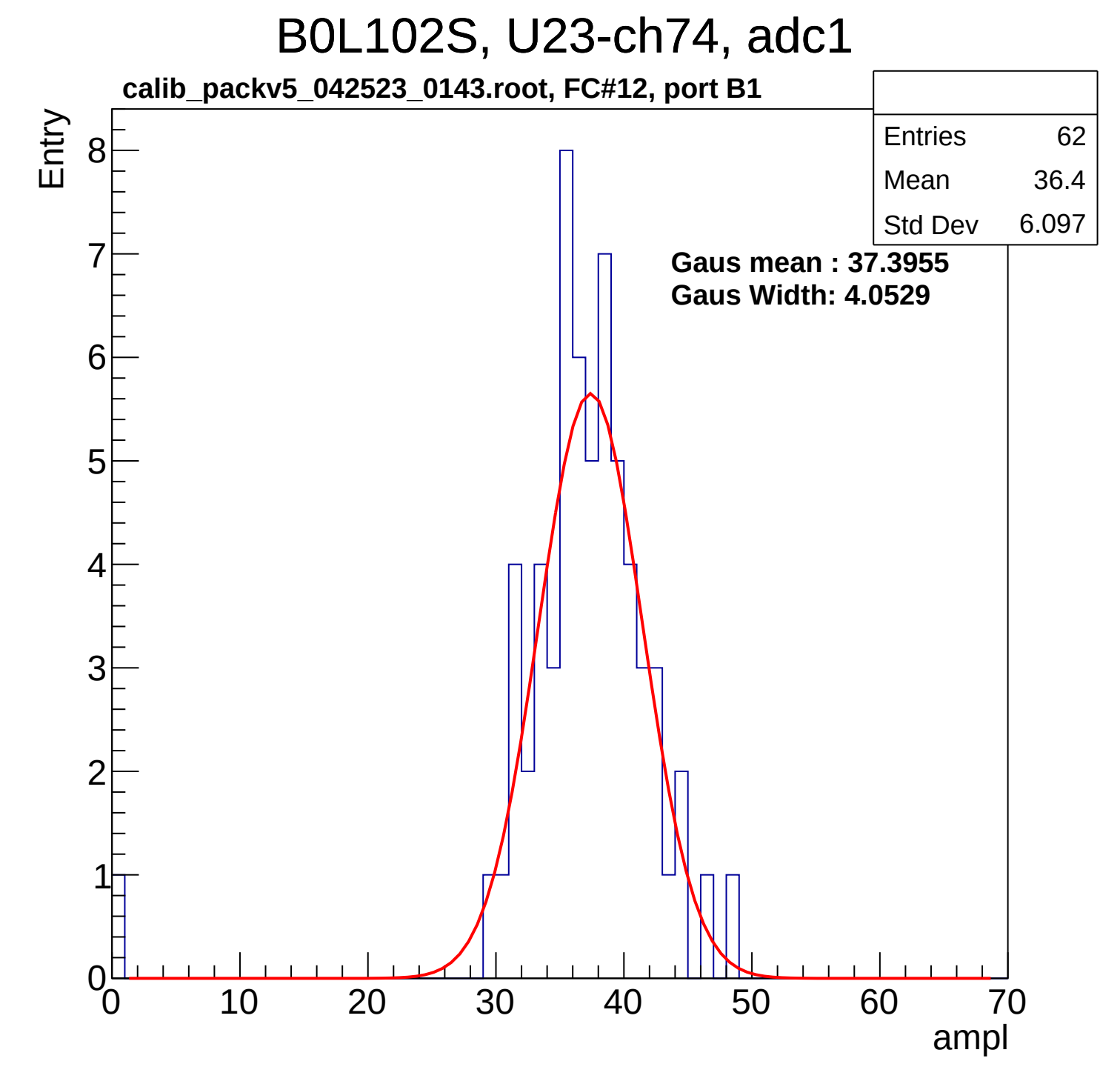

B0L102S, U23-ch73, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.33
Std Dev 3.088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

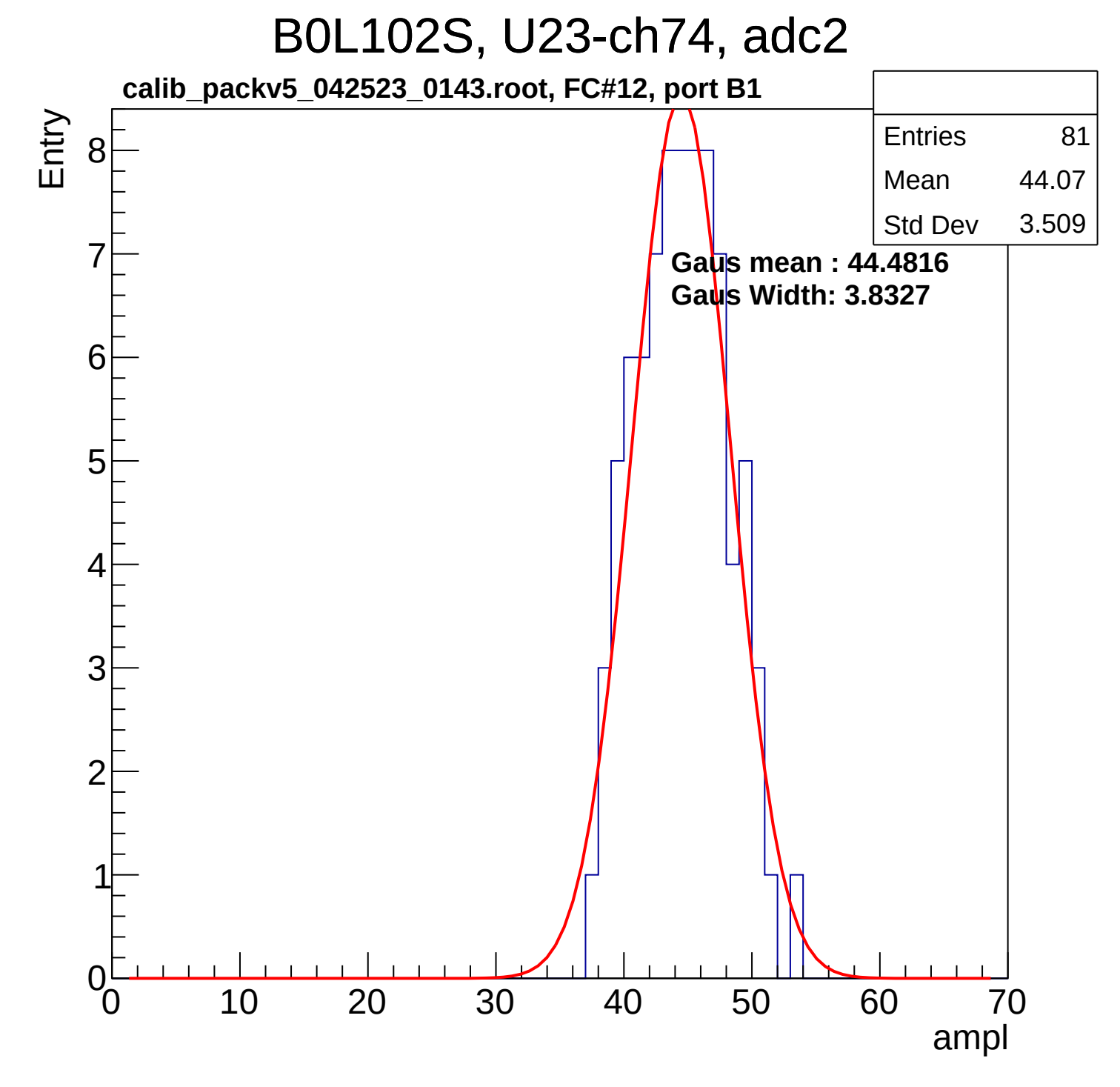

B0L102S, U23-ch73, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 58.86
Std Dev 10.01
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

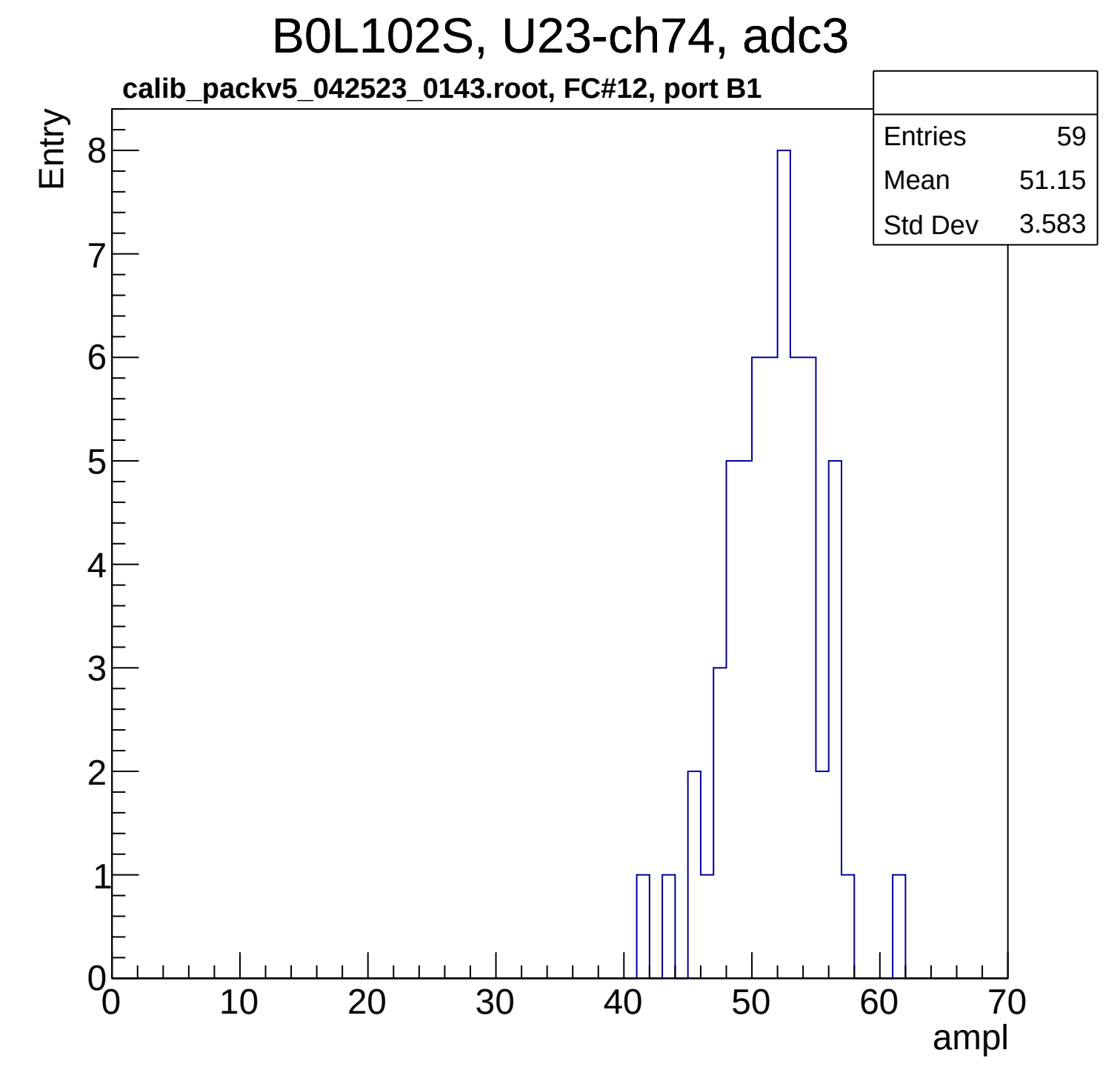

B0L102S, U23-ch73, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

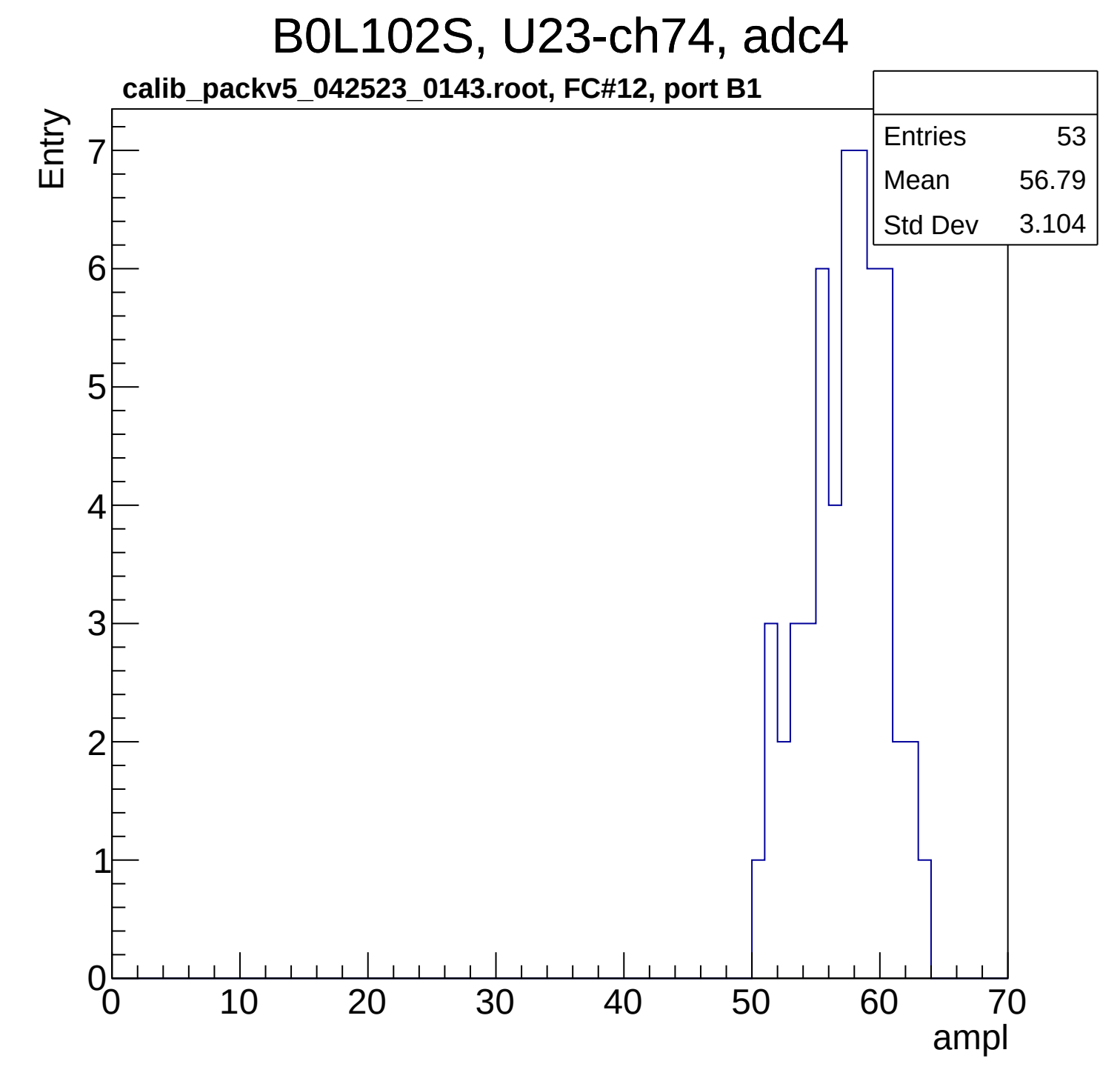

B0L102S, U23-ch73, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

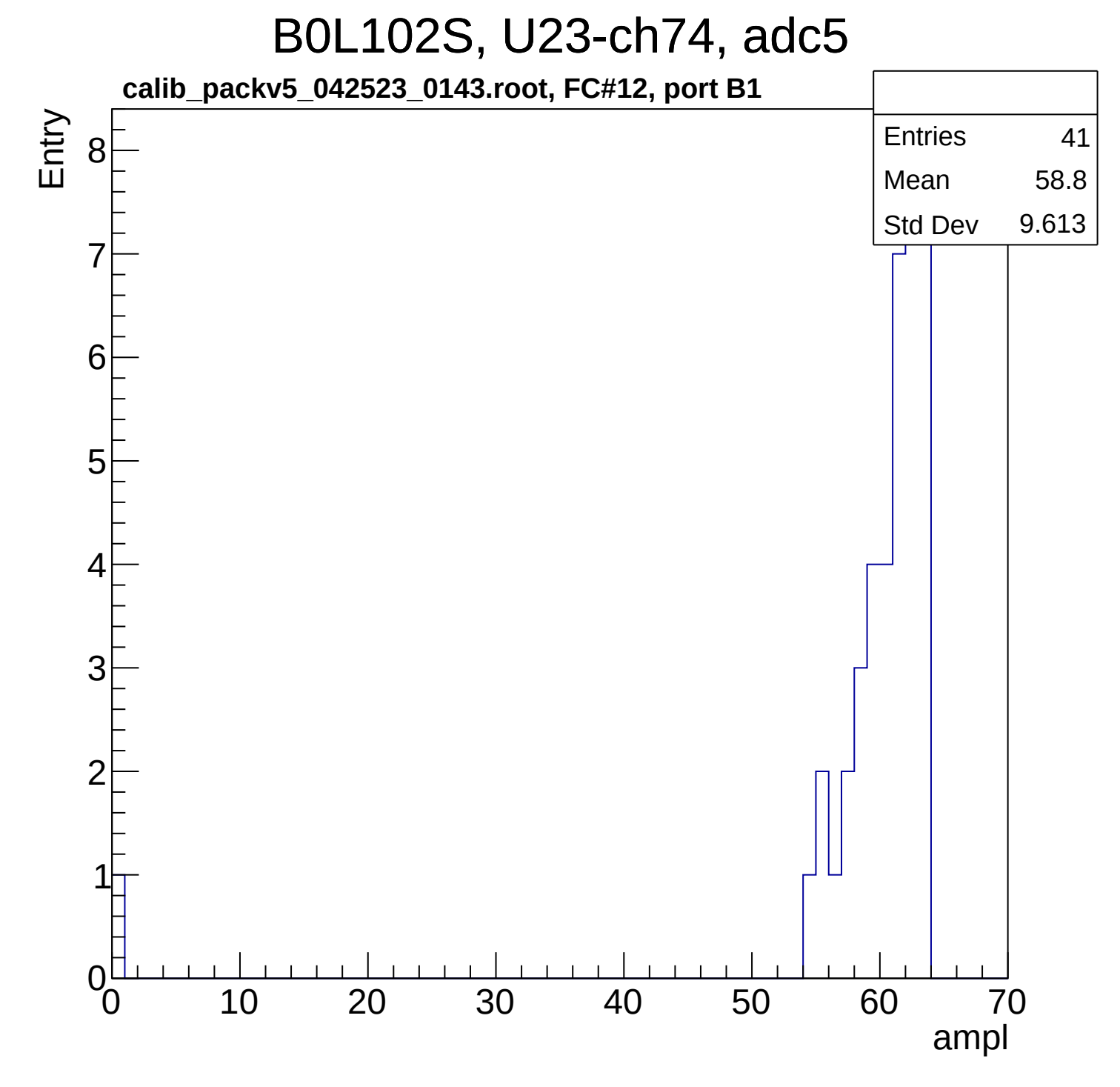

B0L102S, U23-ch74, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 30.13
Std Dev 5.06
Gaus mean : 31.2138
Gaus Width: 4.4167
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

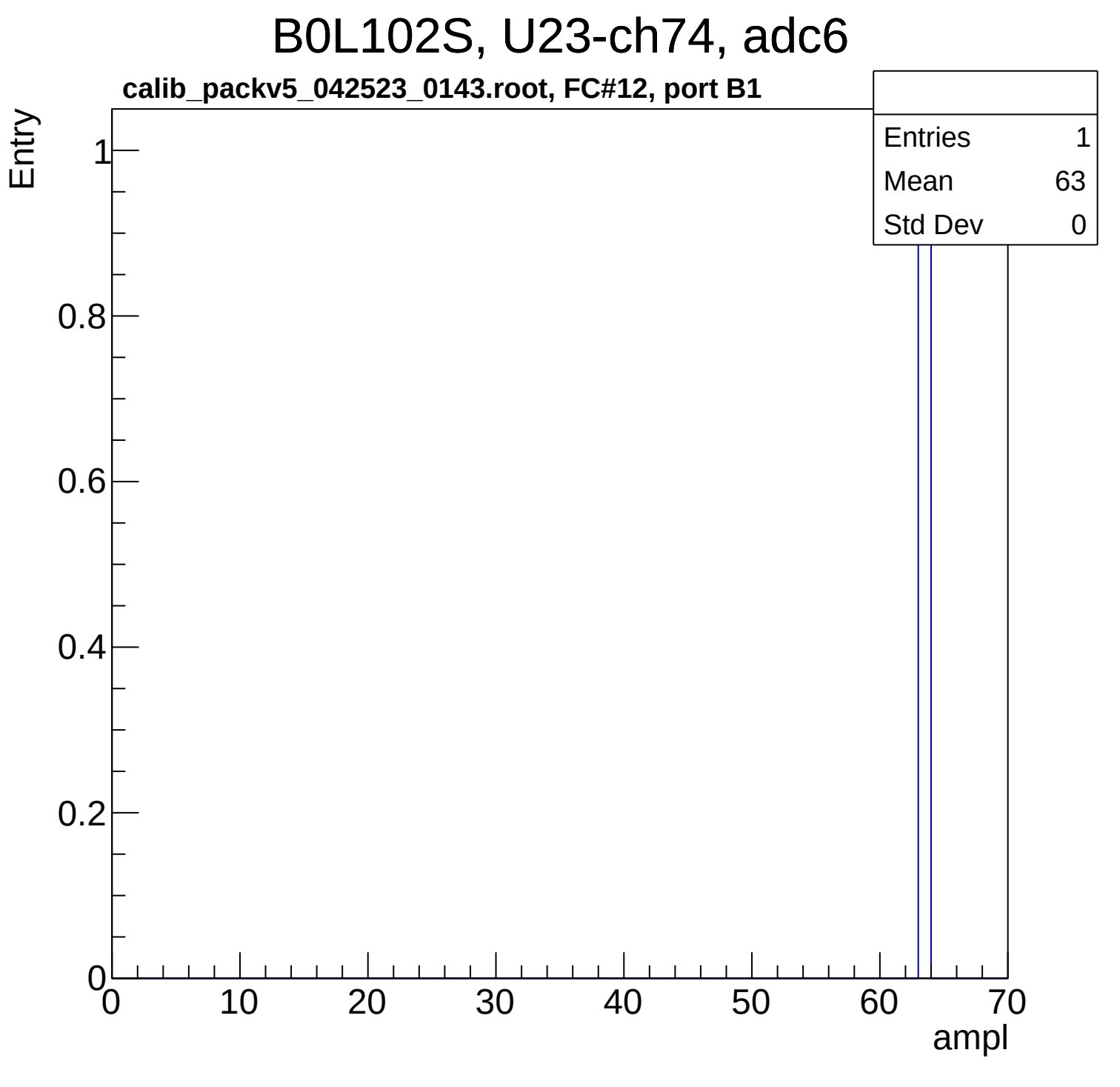

B0L102S, U23-ch74, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 36.4
Std Dev 6.097
Gaus mean : 37.3955
Gaus Width: 4.0529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

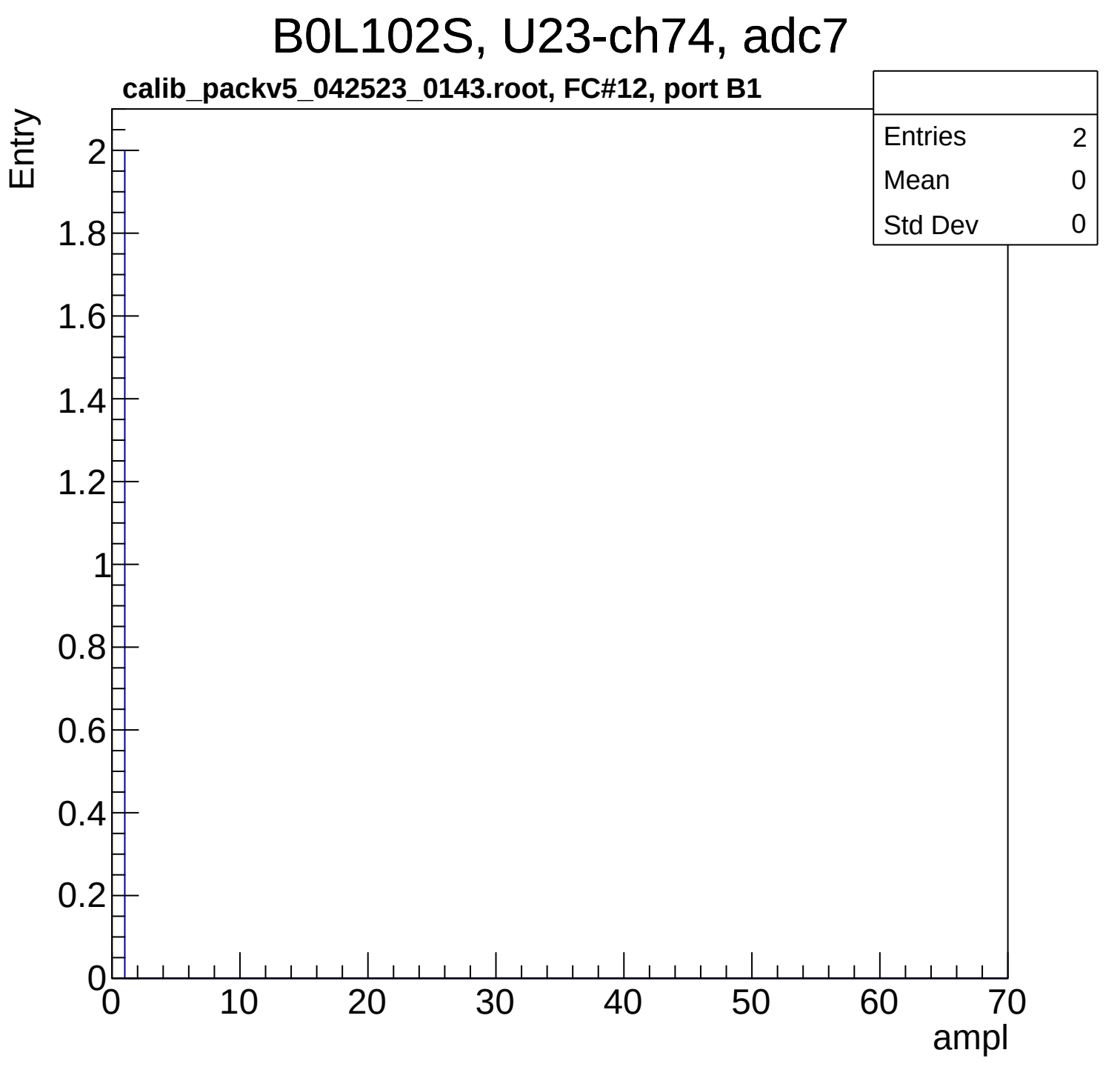

B0L102S, U23-ch74, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 44.07
Std Dev 3.509
Gaus mean : 44.4816
Gaus Width: 3.8327
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

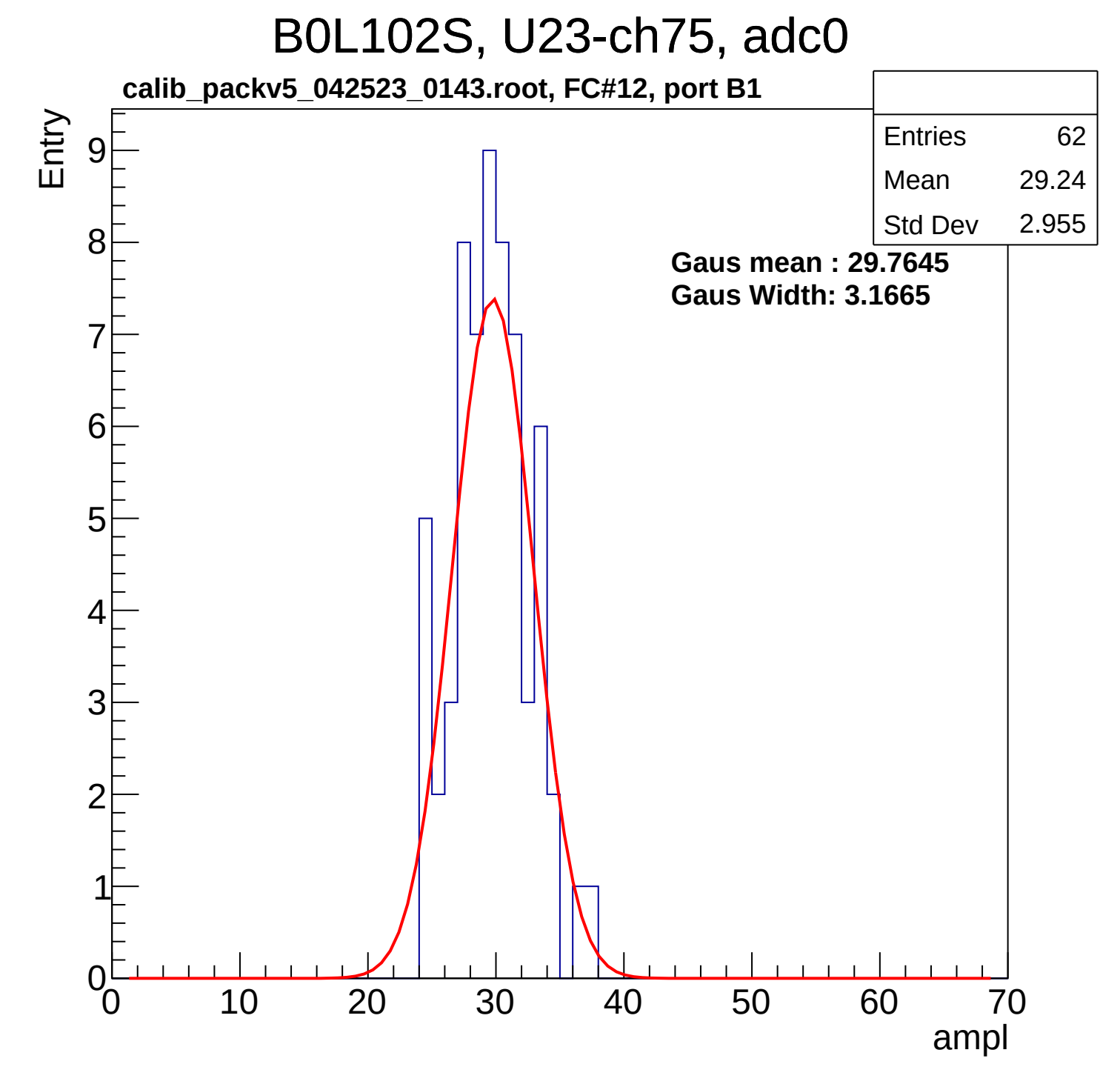

B0L102S, U23-ch74, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 51.15
Std Dev 3.583
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

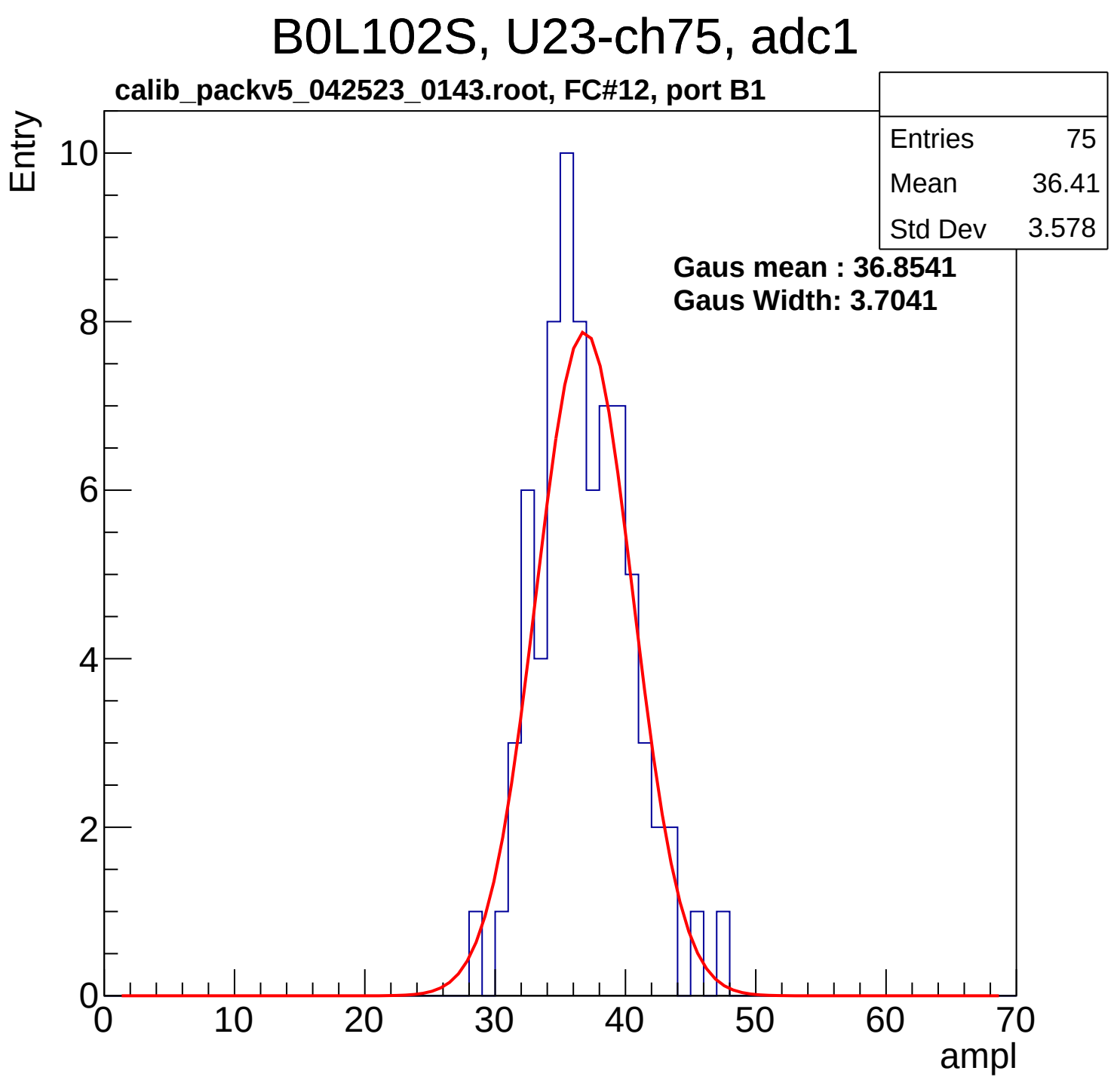

B0L102S, U23-ch74, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 56.79
Std Dev 3.104
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

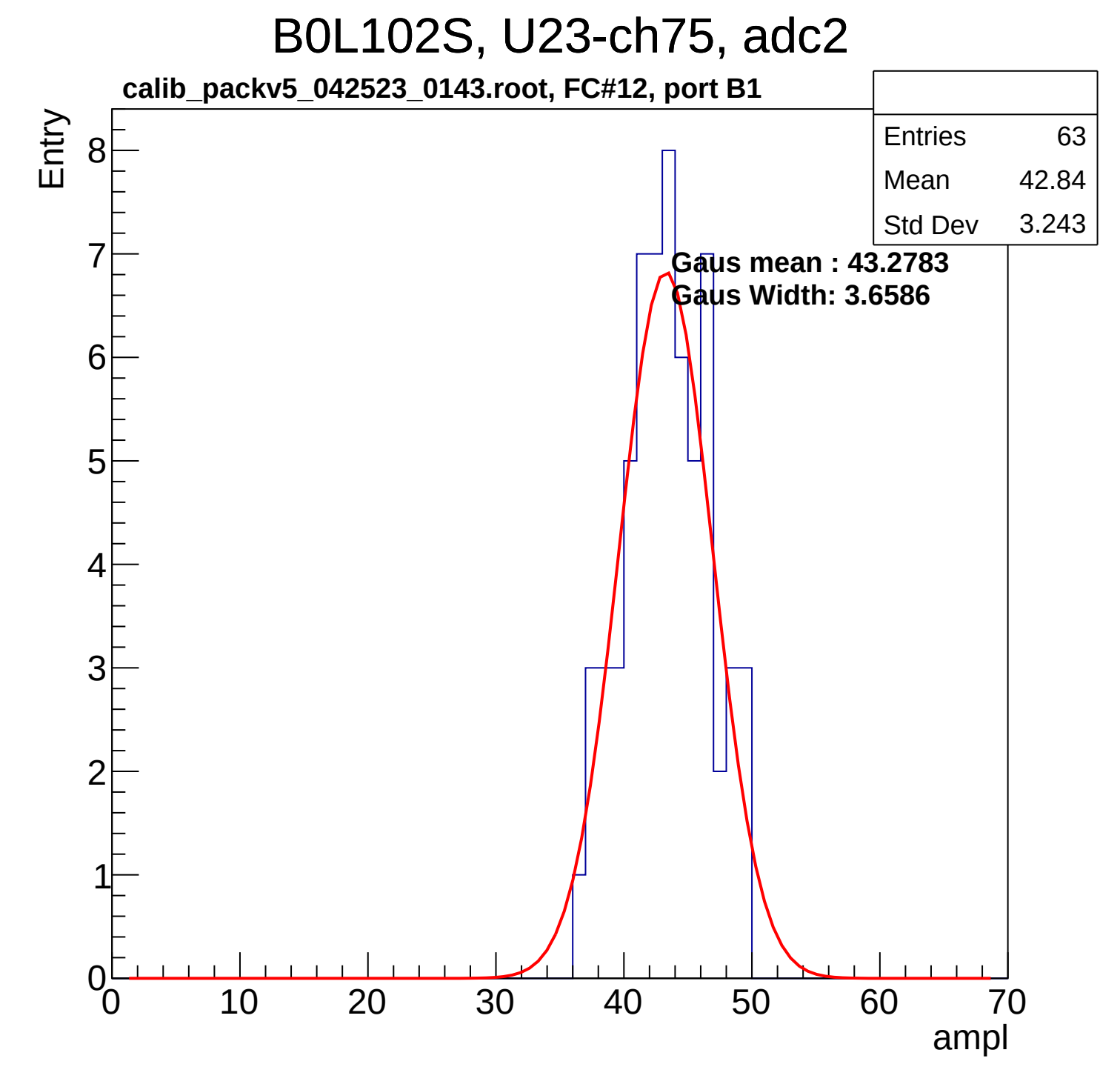

B0L102S, U23-ch74, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.8
Std Dev 9.613
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

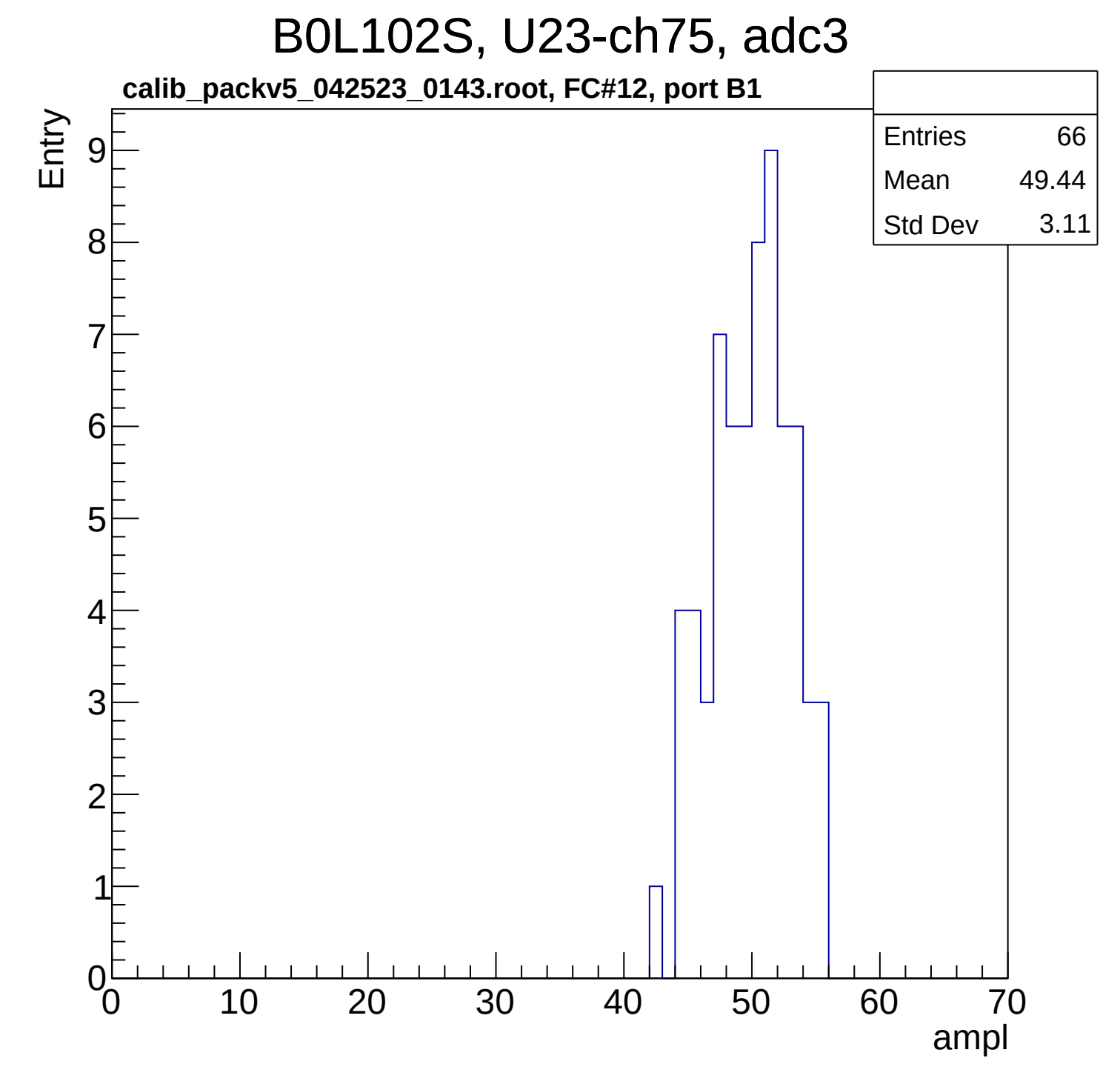

B0L102S, U23-ch74, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

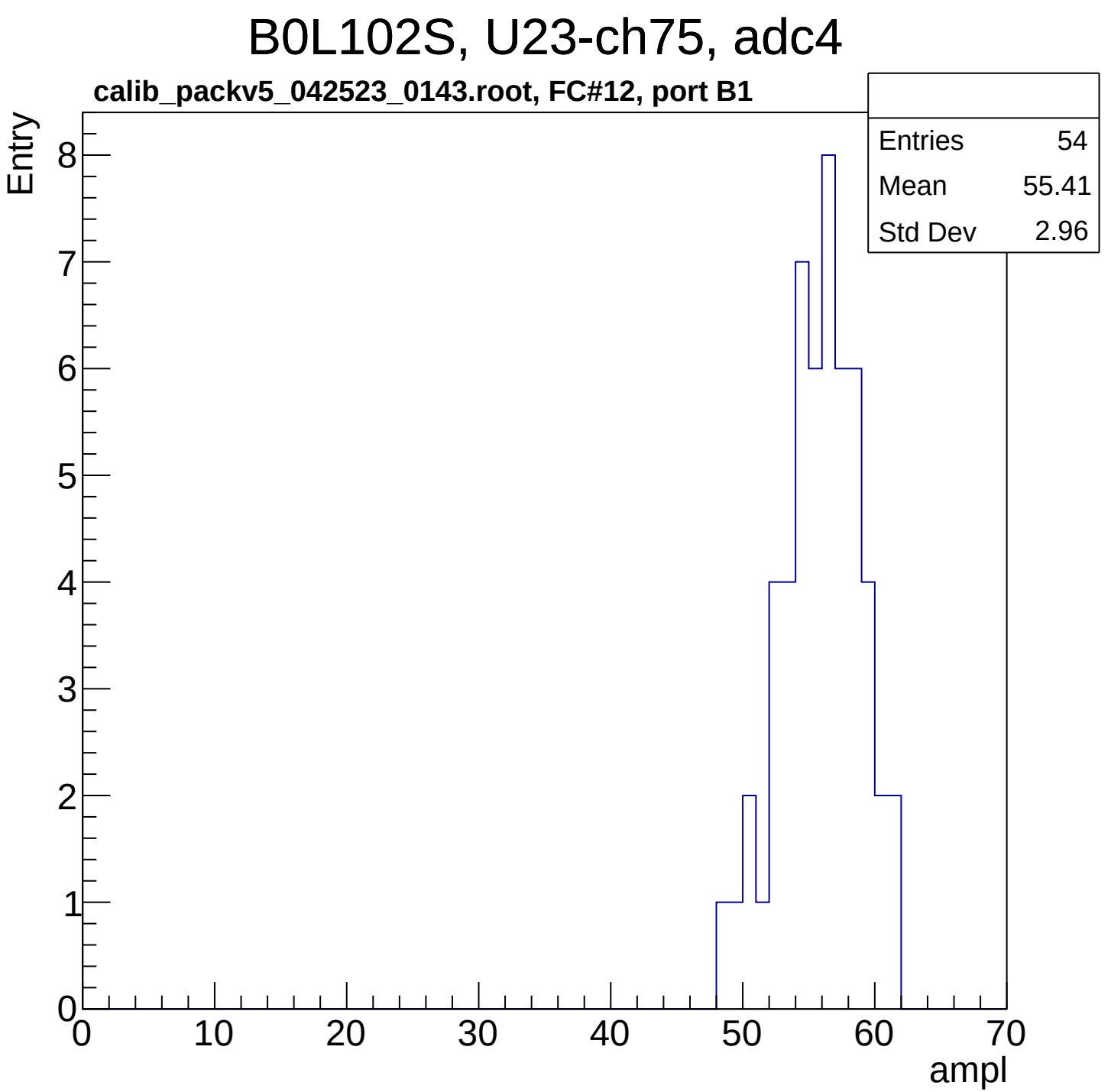

B0L102S, U23-ch74, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

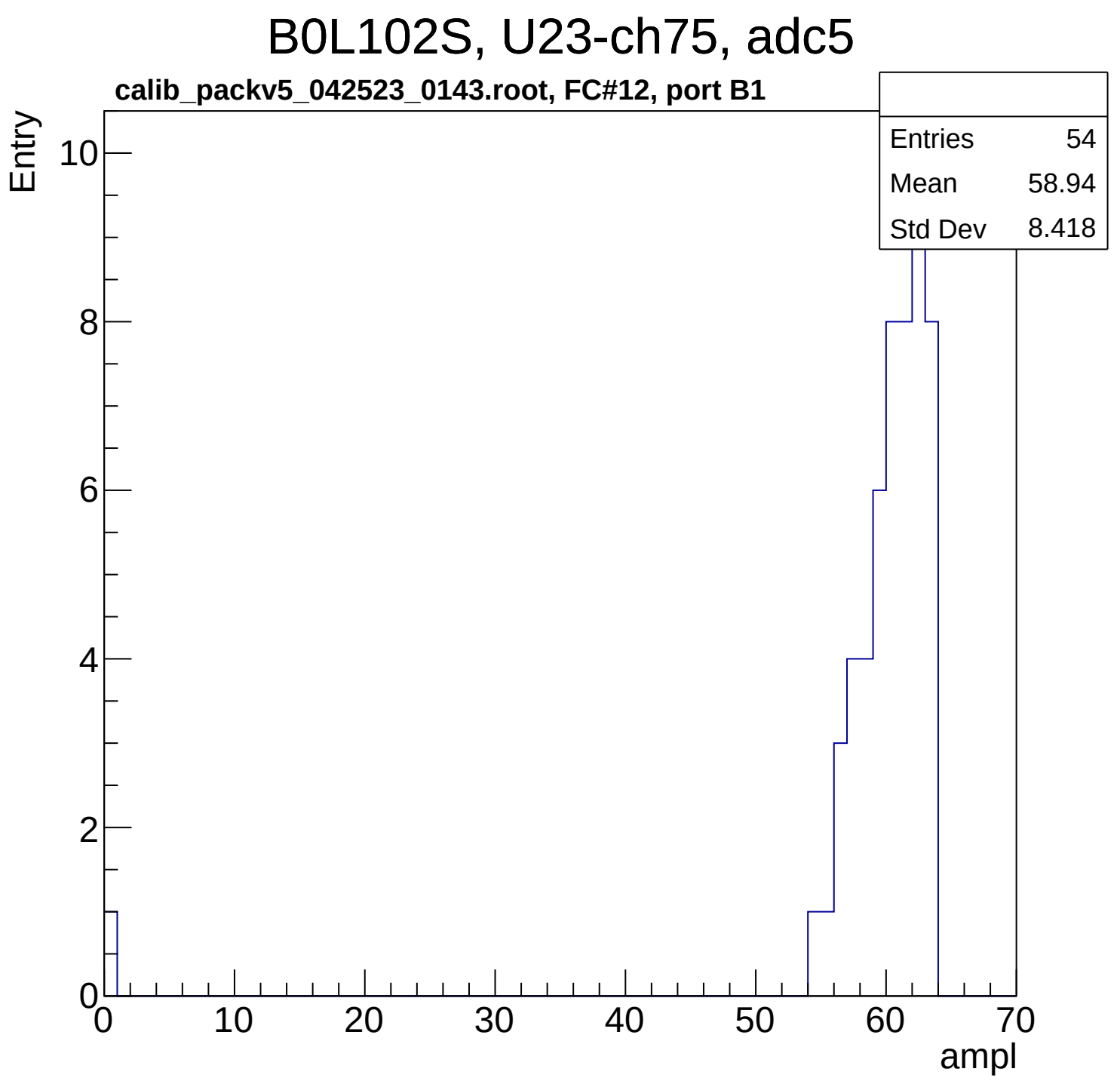

B0L102S, U23-ch75, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 29.24
Std Dev 2.955
Gaus mean : 29.7645
Gaus Width: 3.1665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch75, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.41
Std Dev 3.578
Gaus mean : 36.8541
Gaus Width: 3.7041
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch75, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 42.84
Std Dev 3.243
Gaus mean : 43.2783
Gaus Width: 3.6586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch75, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.44
Std Dev 3.11
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch75, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 55.41
Std Dev 2.96
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch75, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 58.94
Std Dev 8.418
Entry
0
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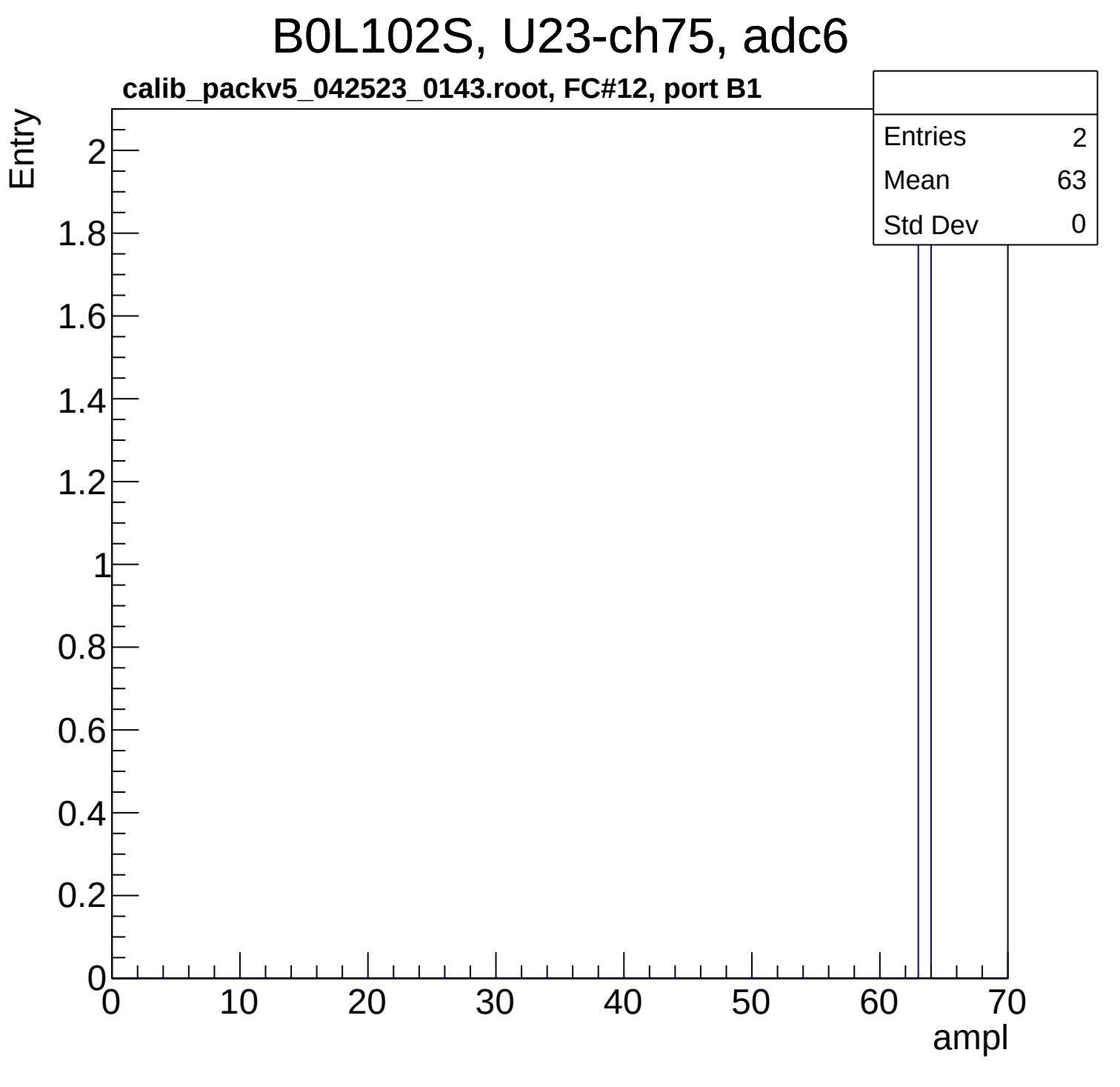
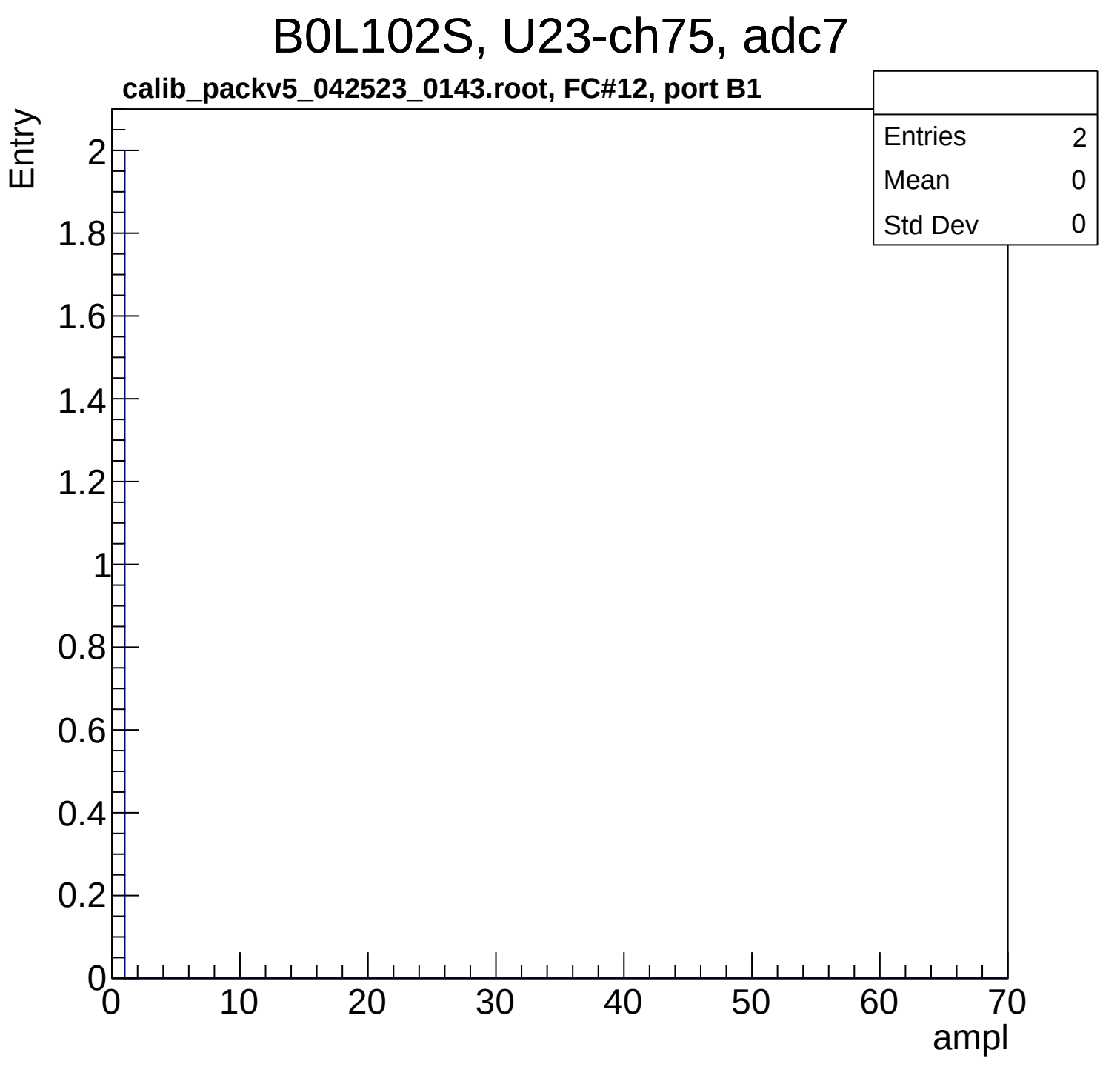
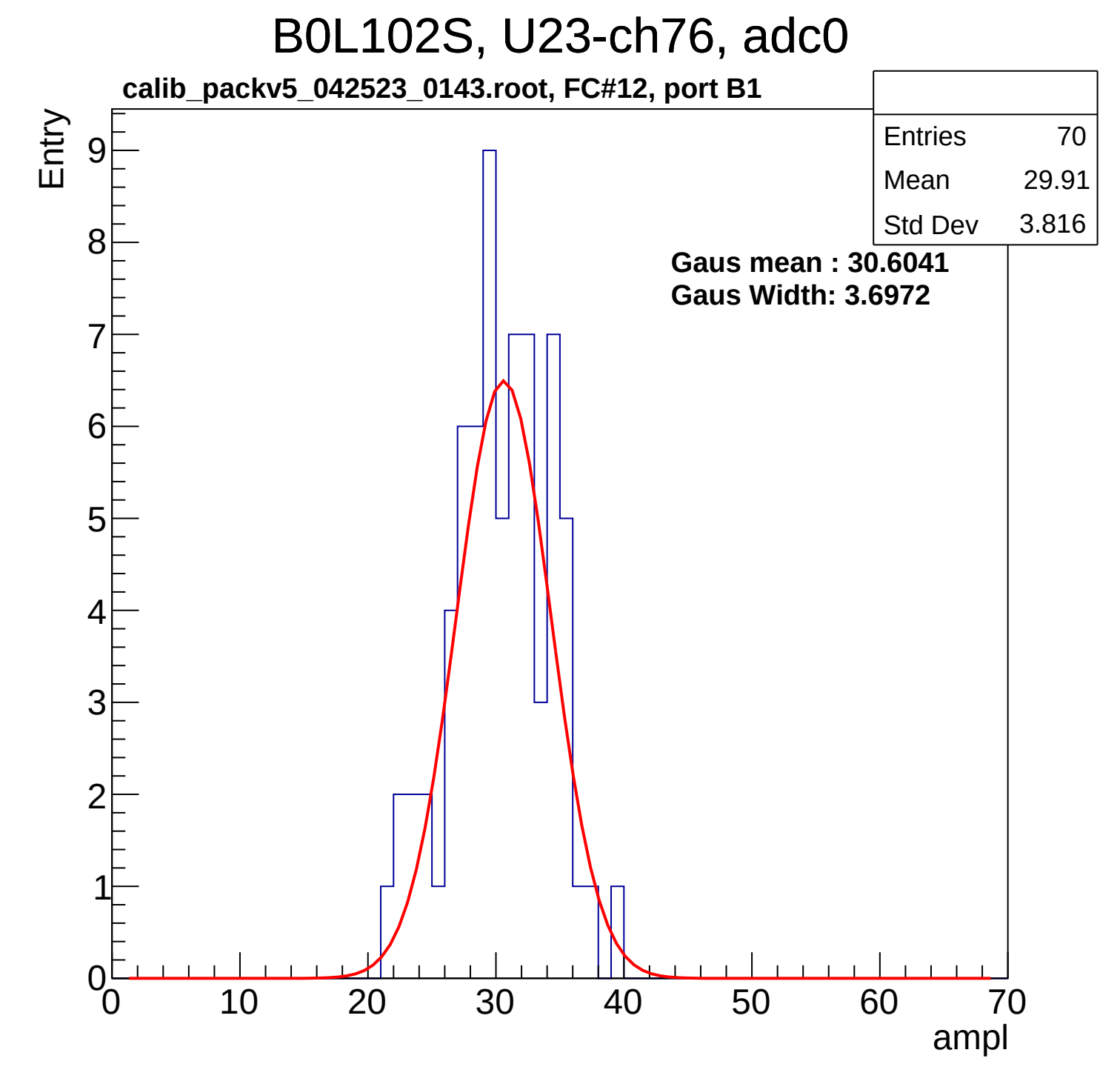
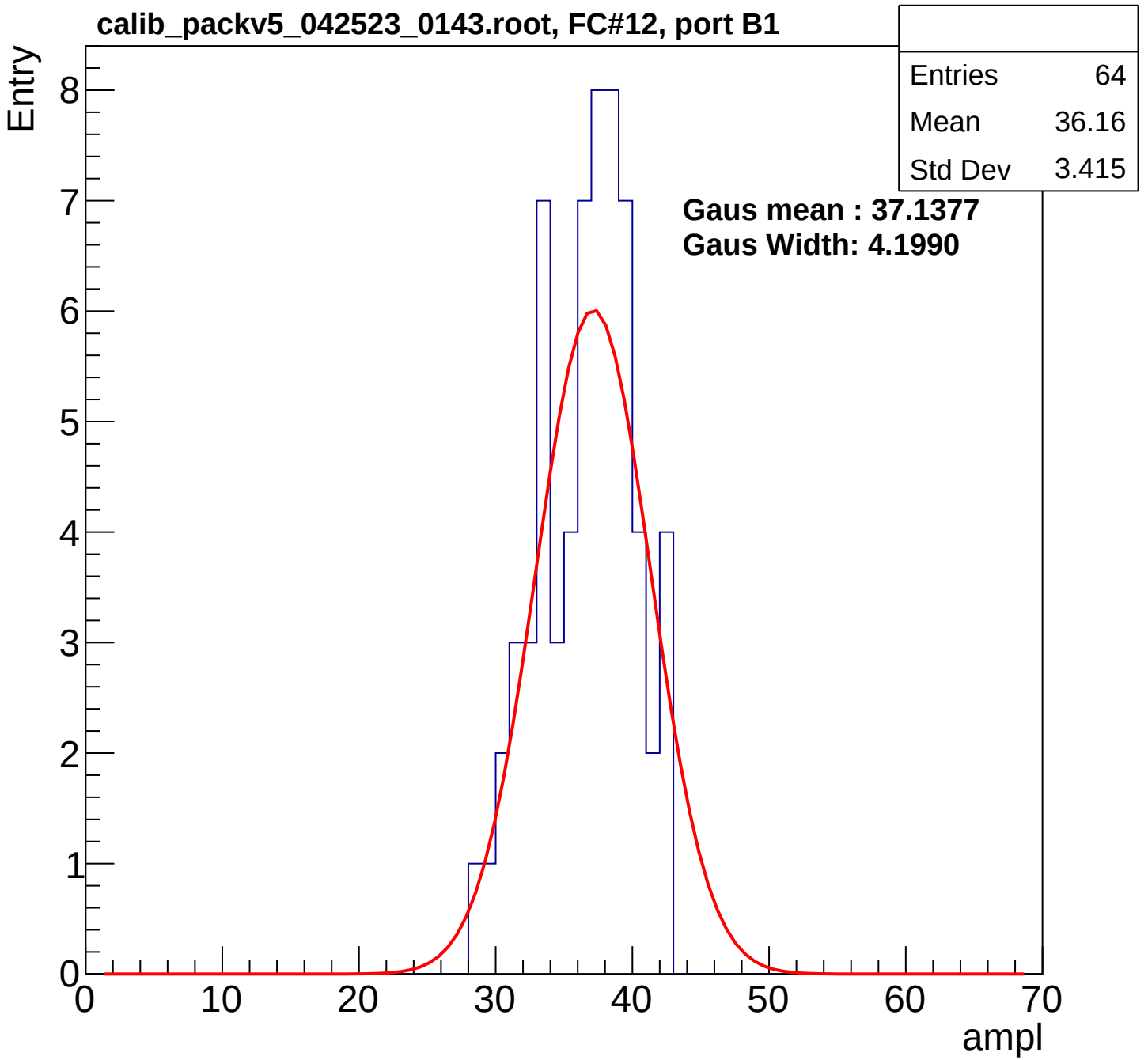
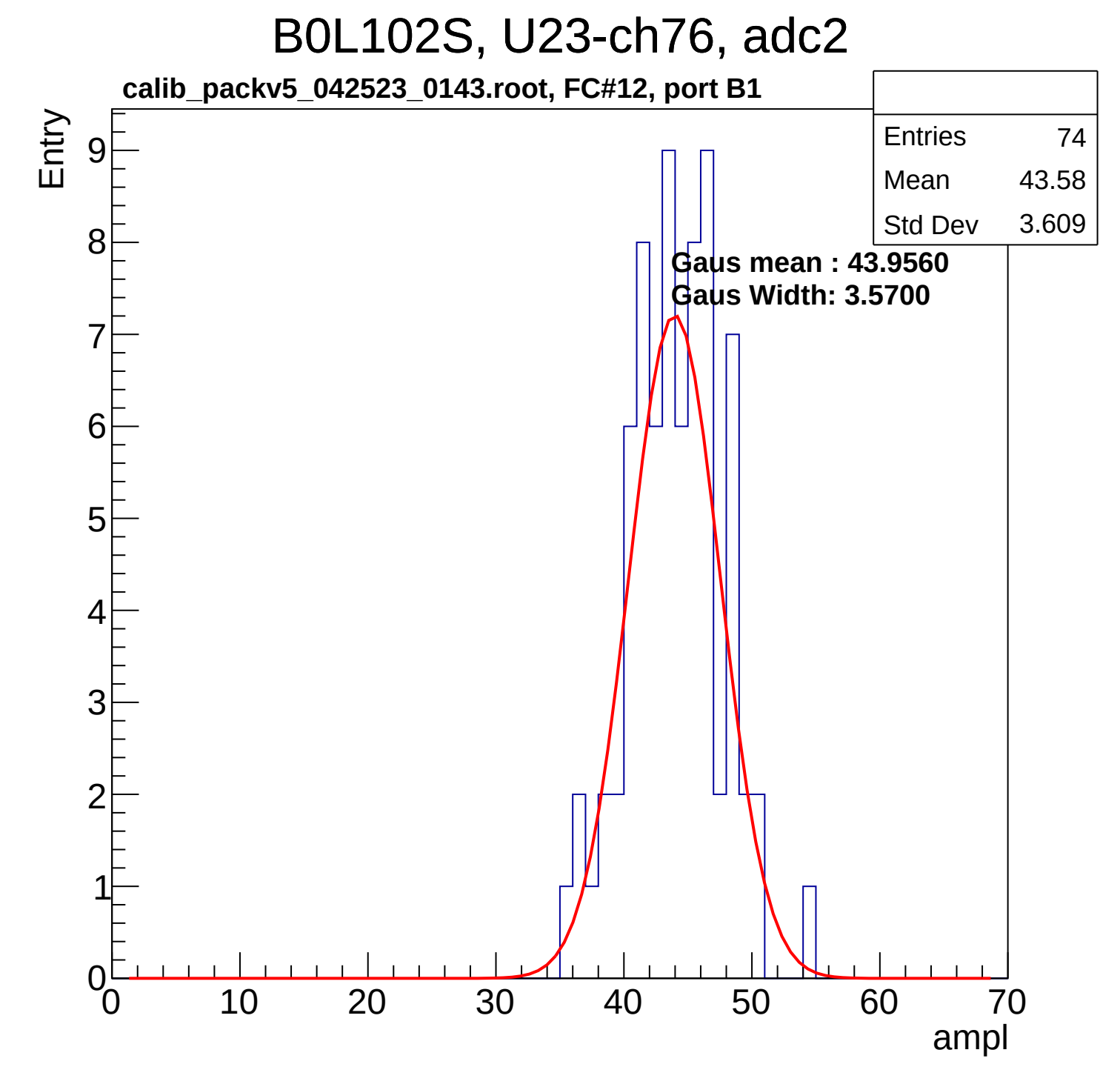
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch75, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch75, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch76, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 29.91
Std Dev 3.816
Gaus mean : 30.6041
Gaus Width: 3.6972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch76, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.16
Std Dev 3.415
Gaus mean : 37.1377
Gaus Width: 4.1990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch76, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 43.58
Std Dev 3.609
Gaus mean : 43.9560
Gaus Width: 3.5700
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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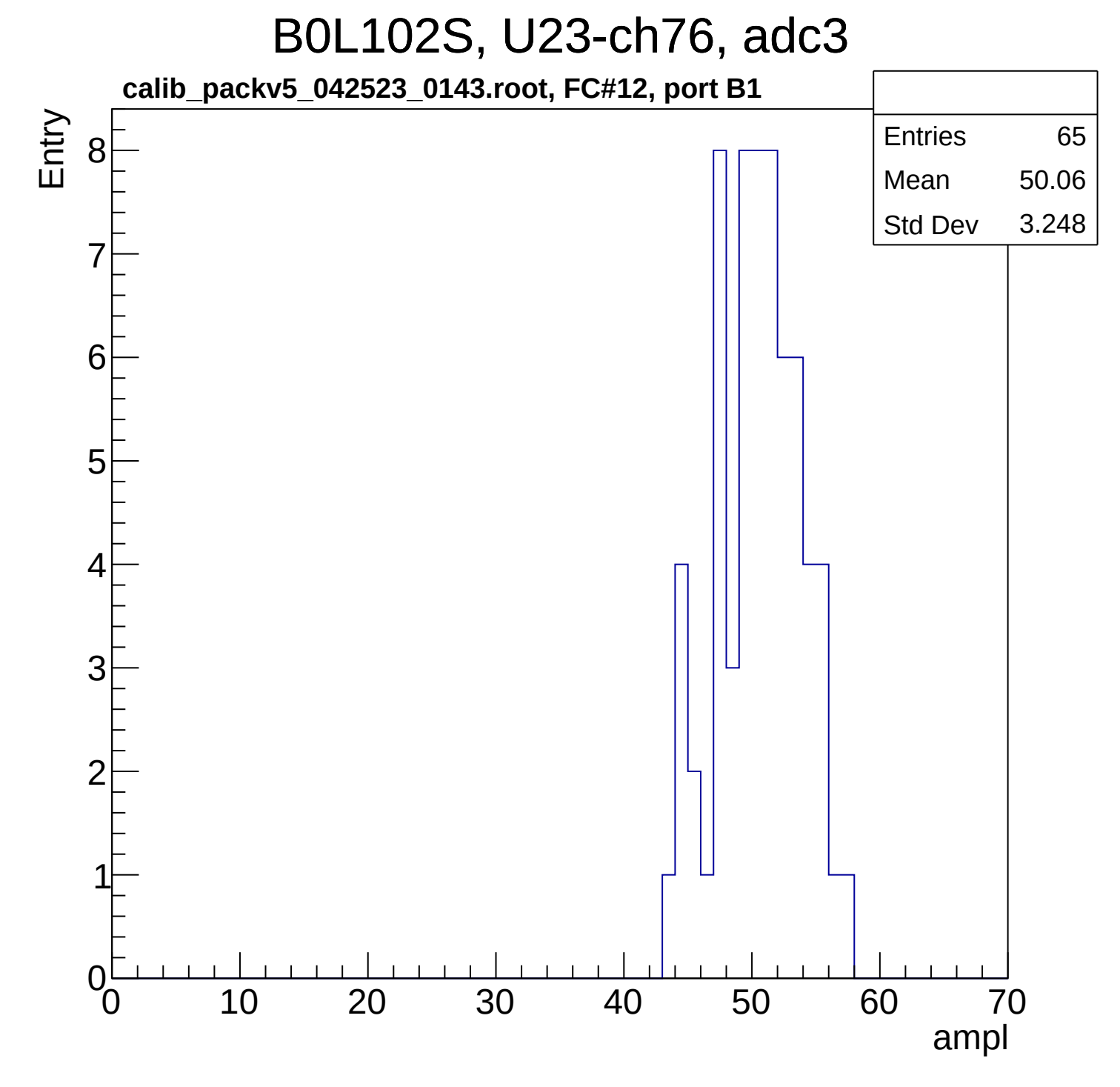
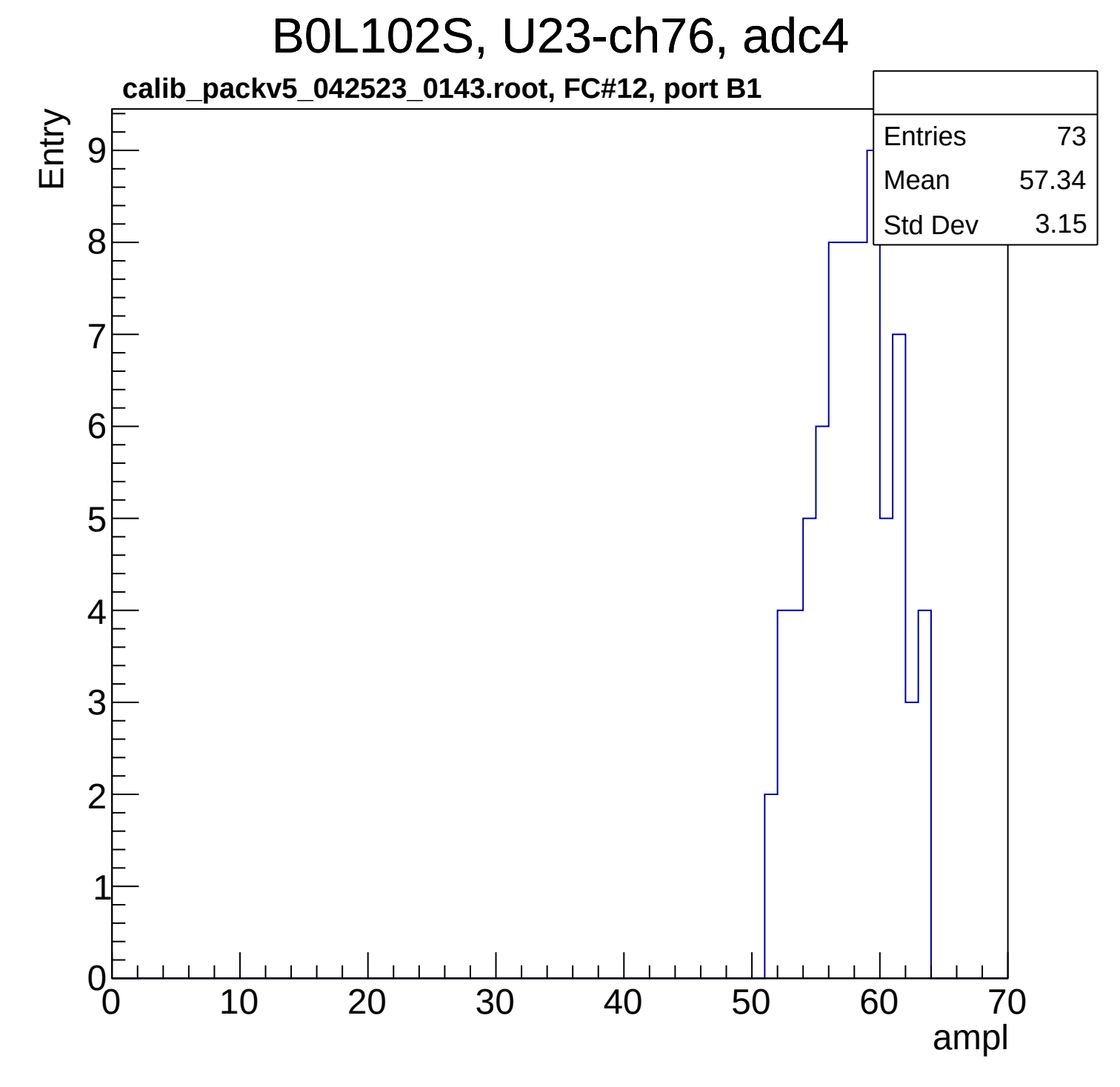
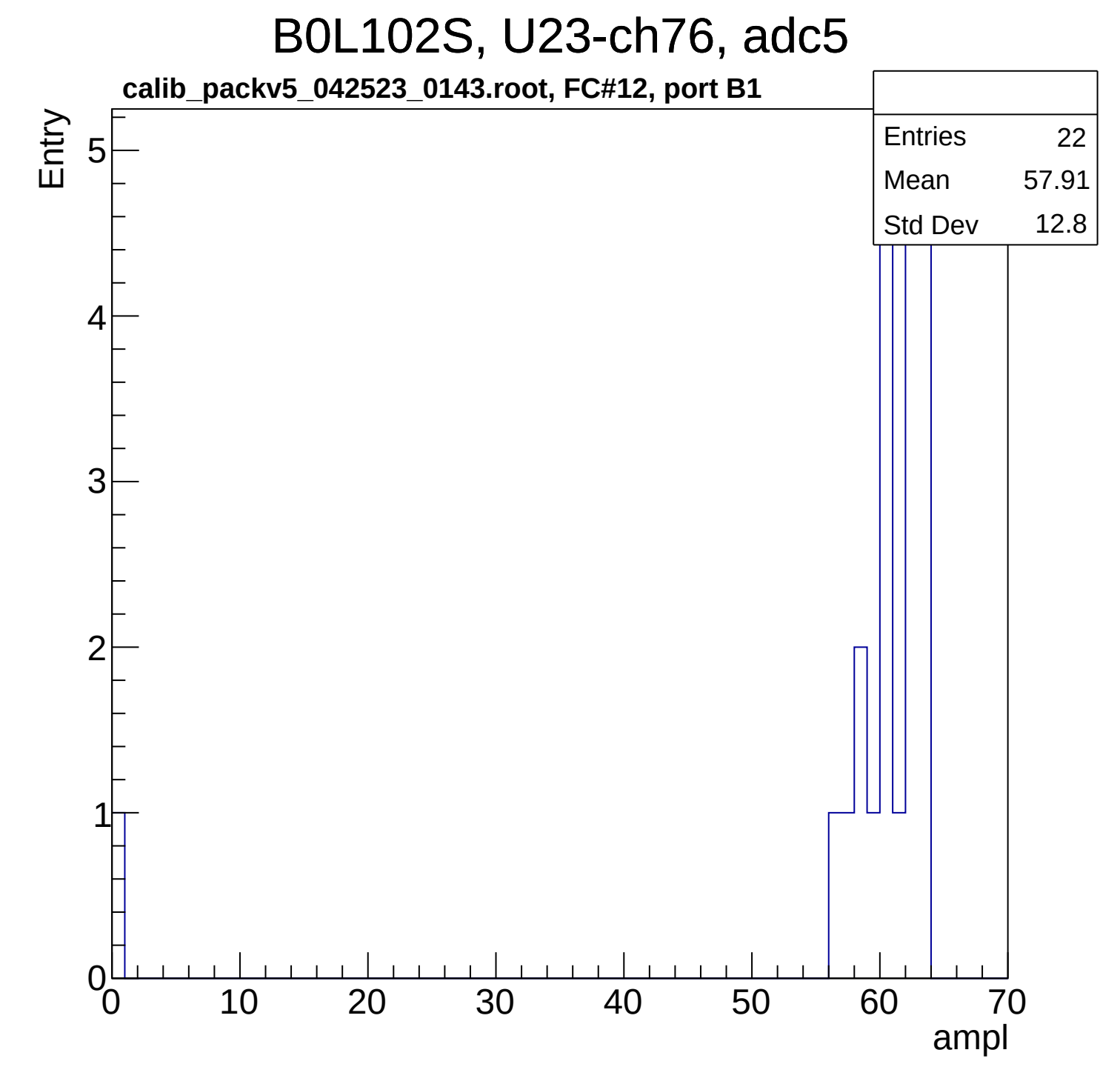
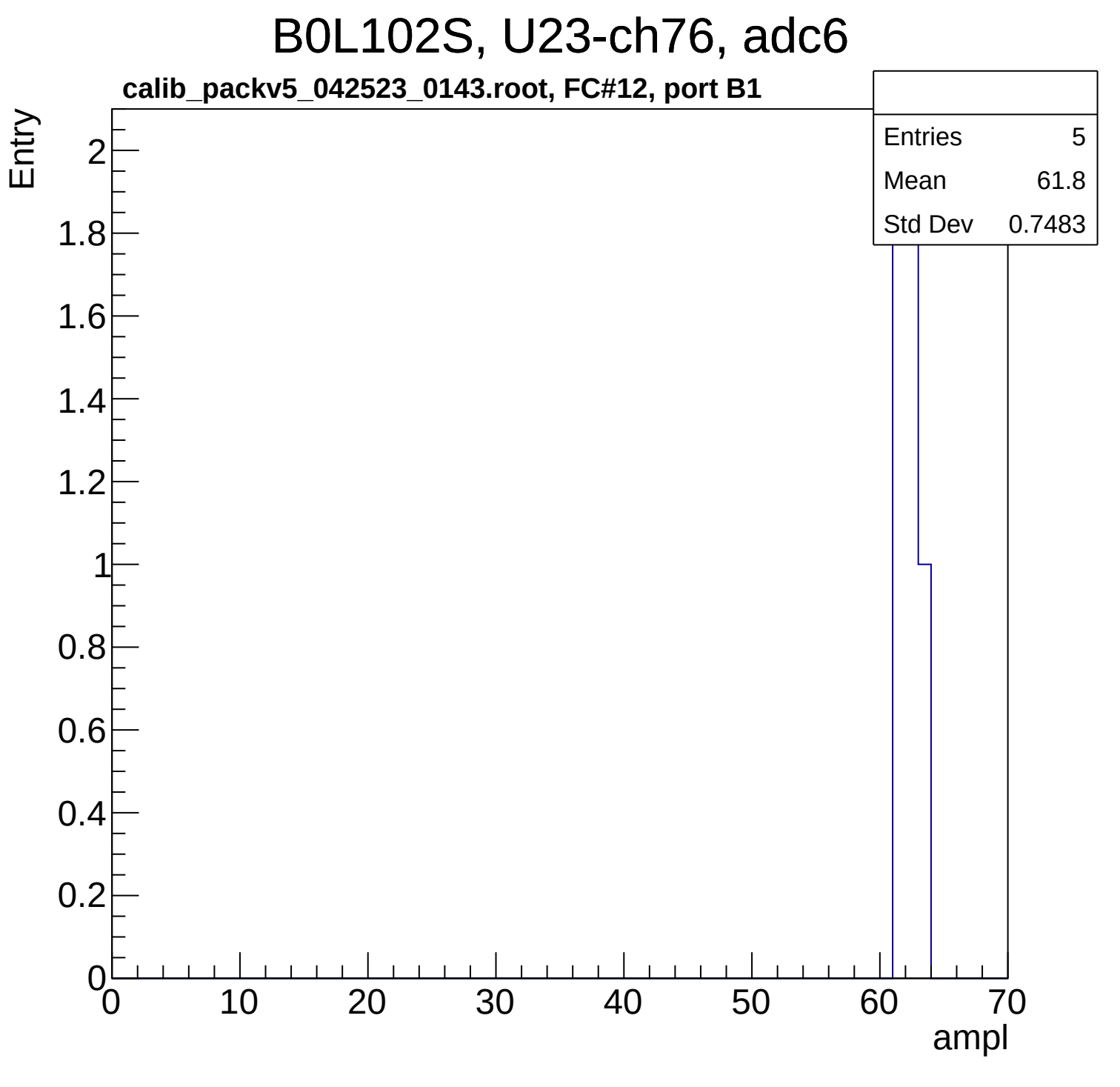
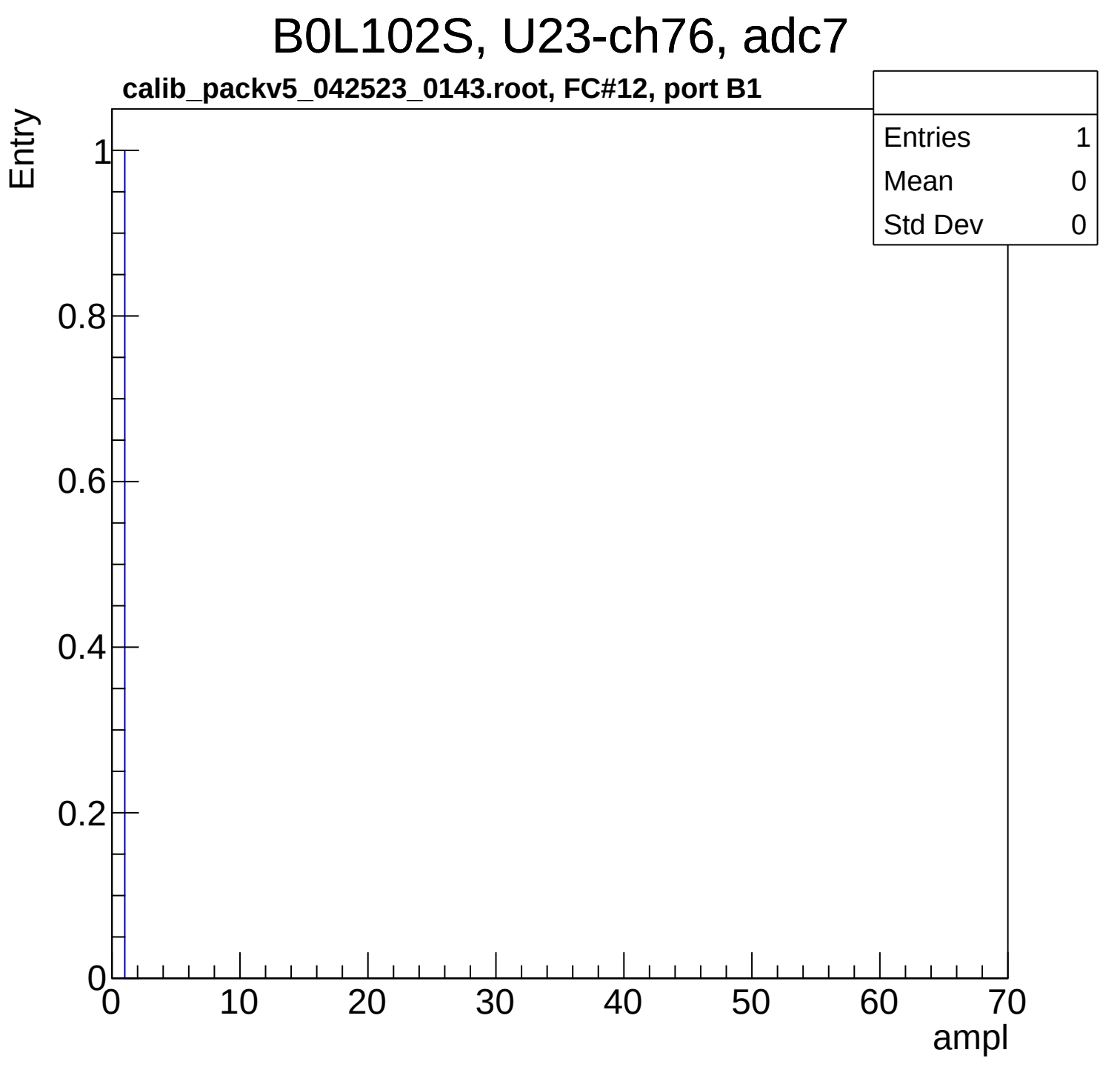
40
50
60
70

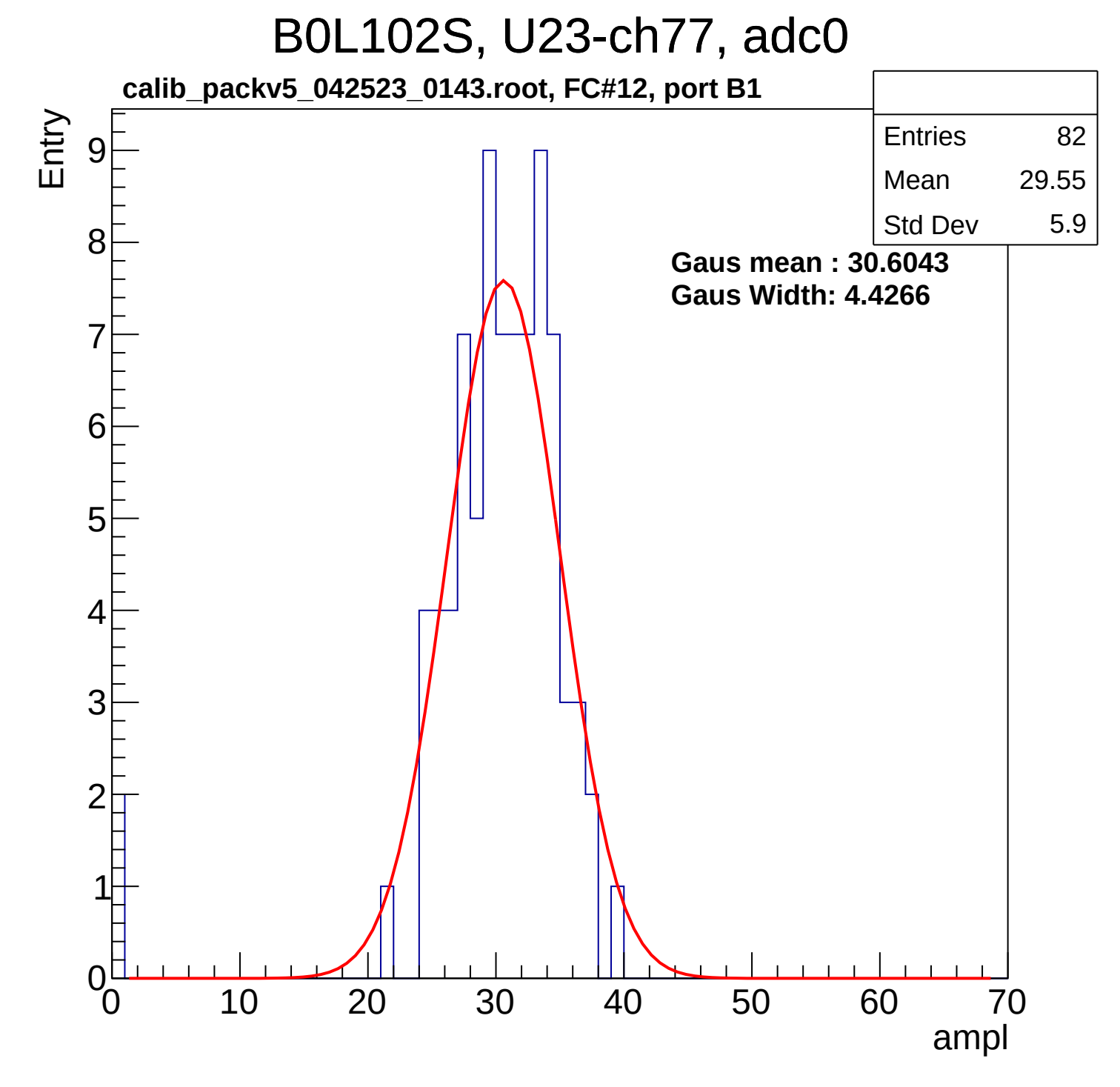

B0L102S, U23-ch76, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 50.06
Std Dev 3.248
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

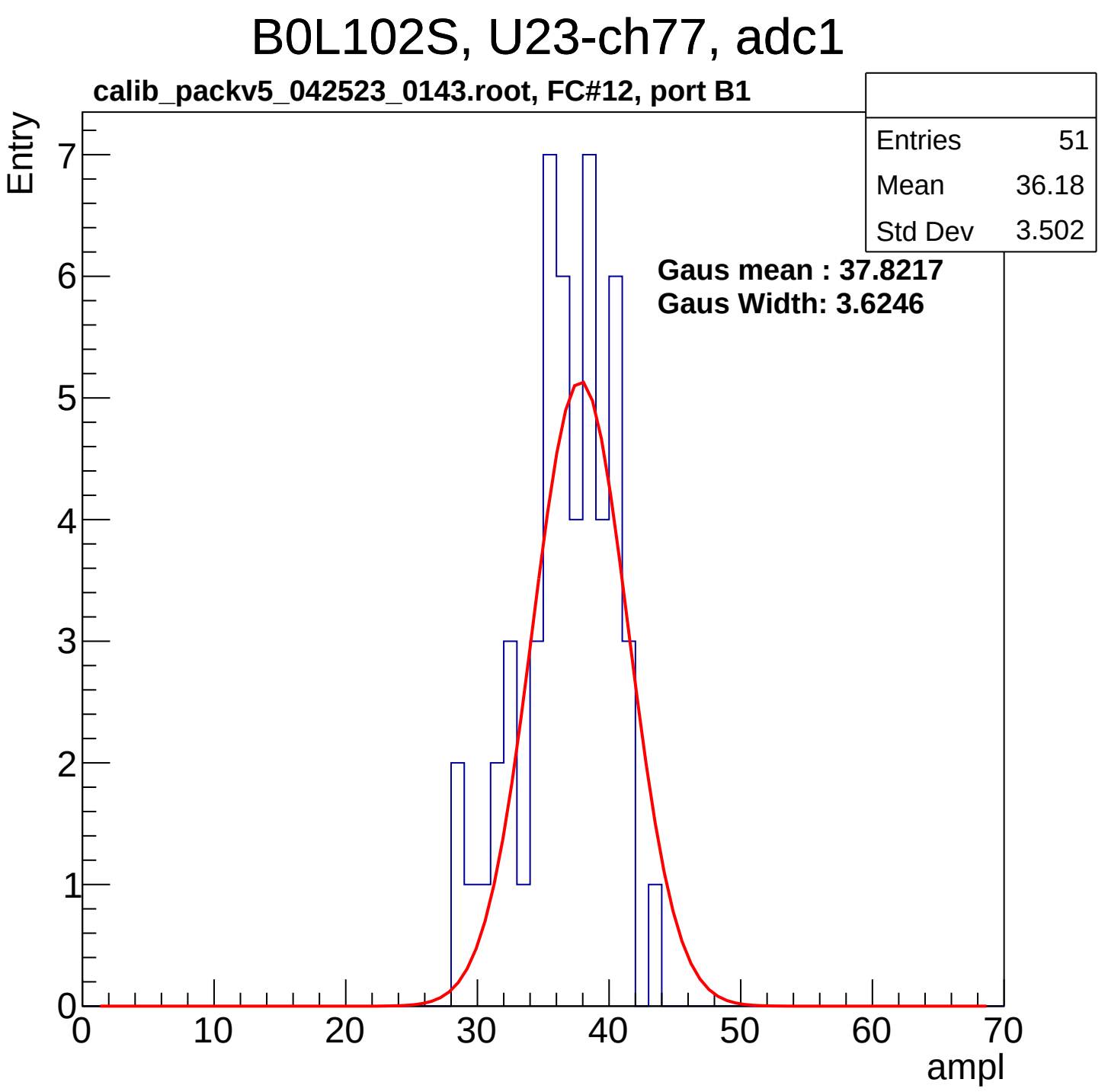

B0L102S, U23-ch76, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 57.34
Std Dev 3.15
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch76, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 22
Mean 57.91
Std Dev 12.8
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch76, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch76, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch77, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 29.55
Std Dev 5.9
Gaus mean : 30.6043
Gaus Width: 4.4266
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch77, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 36.18
Std Dev 3.502
Gaus mean : 37.8217
Gaus Width: 3.6246
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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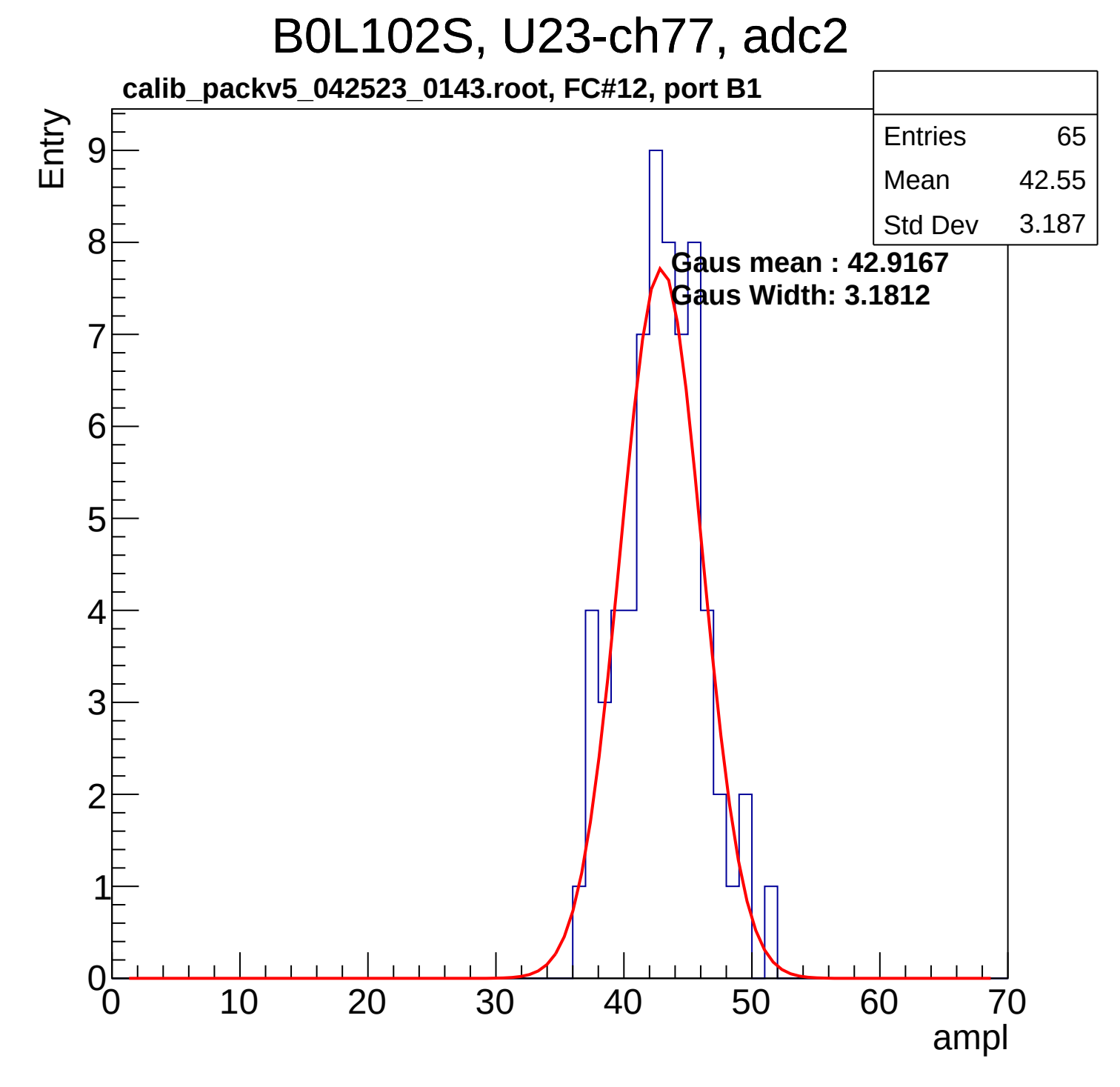
B0L102S, U23-ch77, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 42.55
Std Dev 3.187
Gaus mean : 42.9167
Gaus Width: 3.1812
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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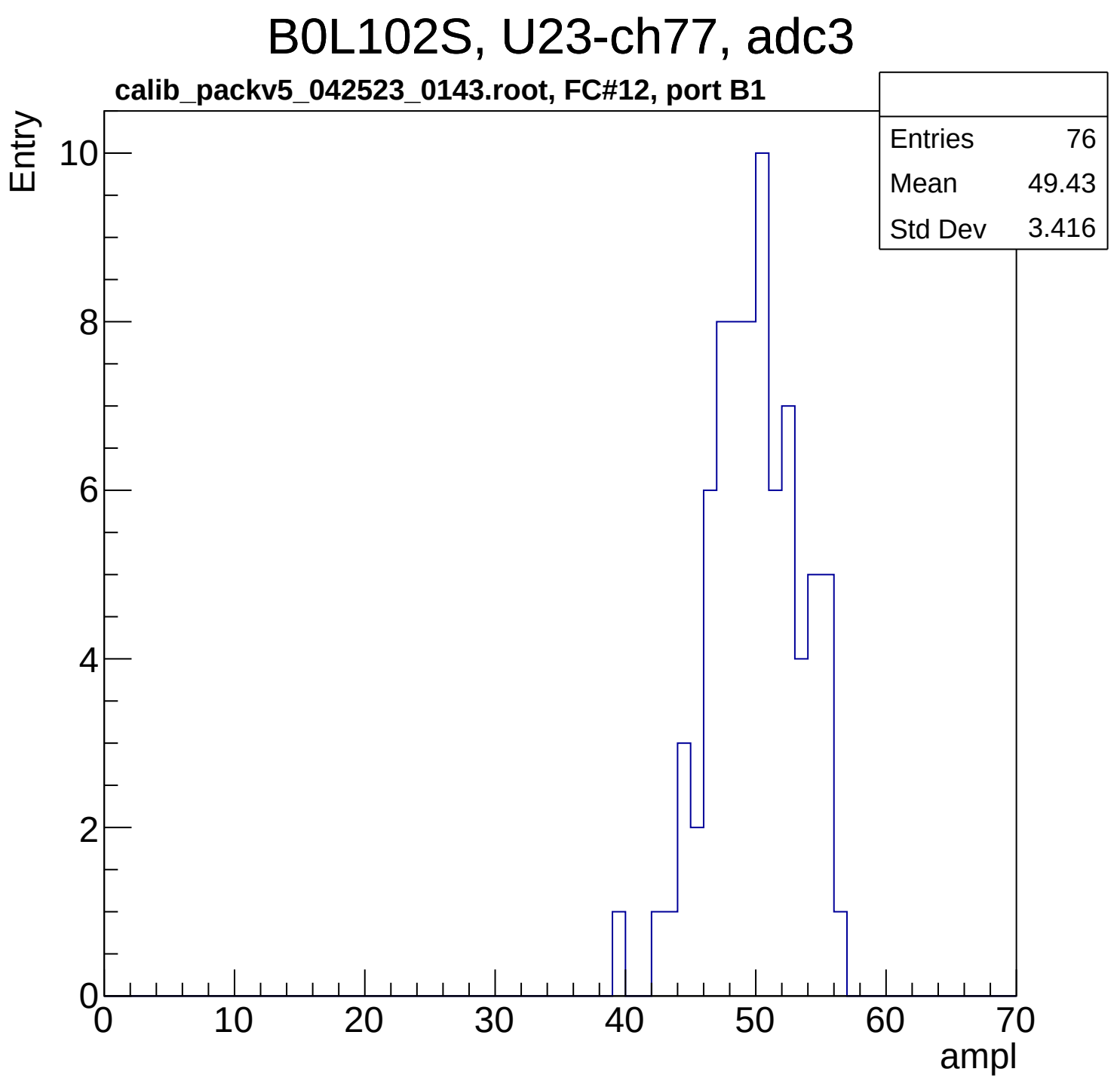
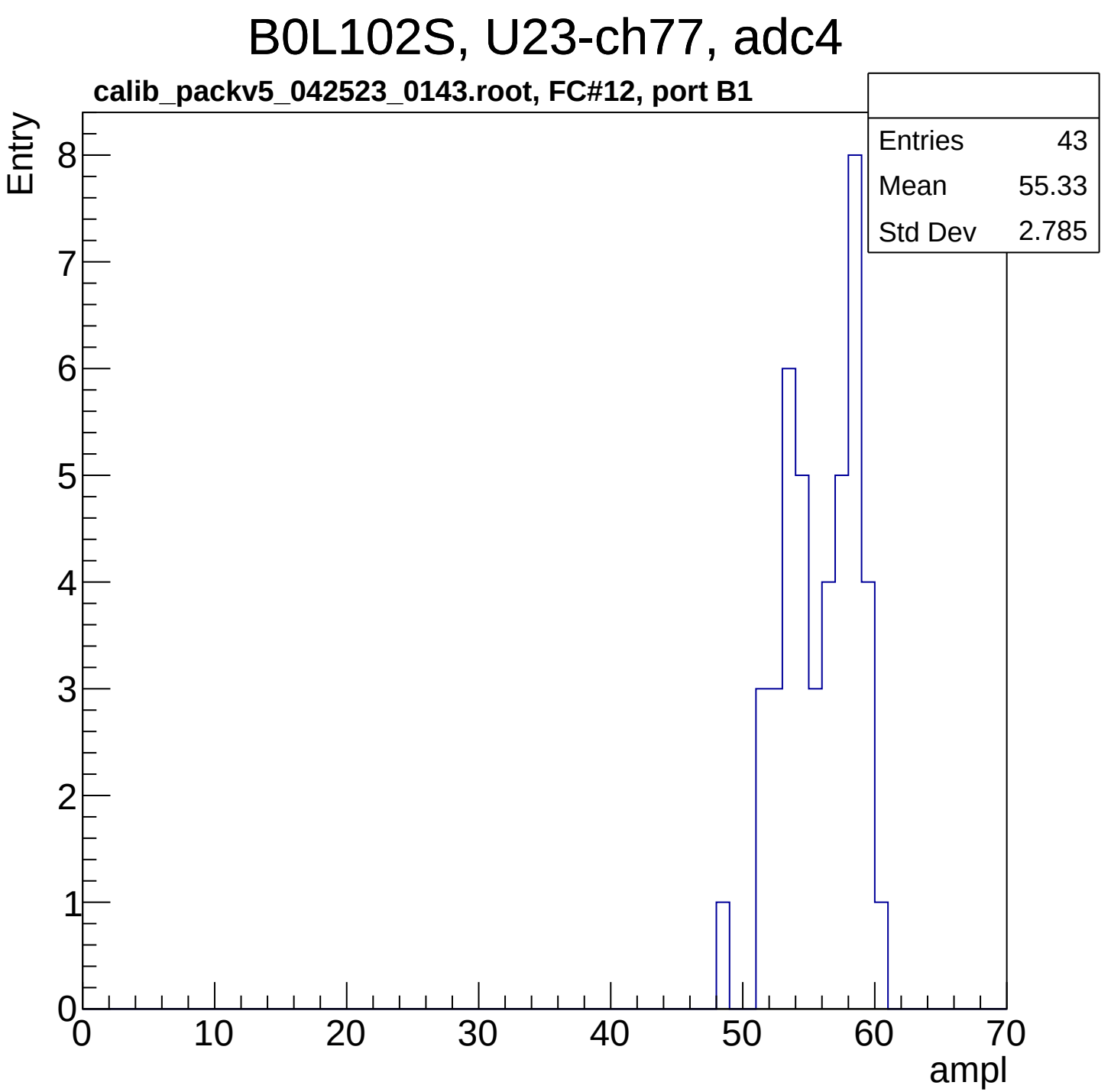

B0L102S, U23-ch77, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 49.43
Std Dev 3.416
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch77, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 55.33
Std Dev 2.785
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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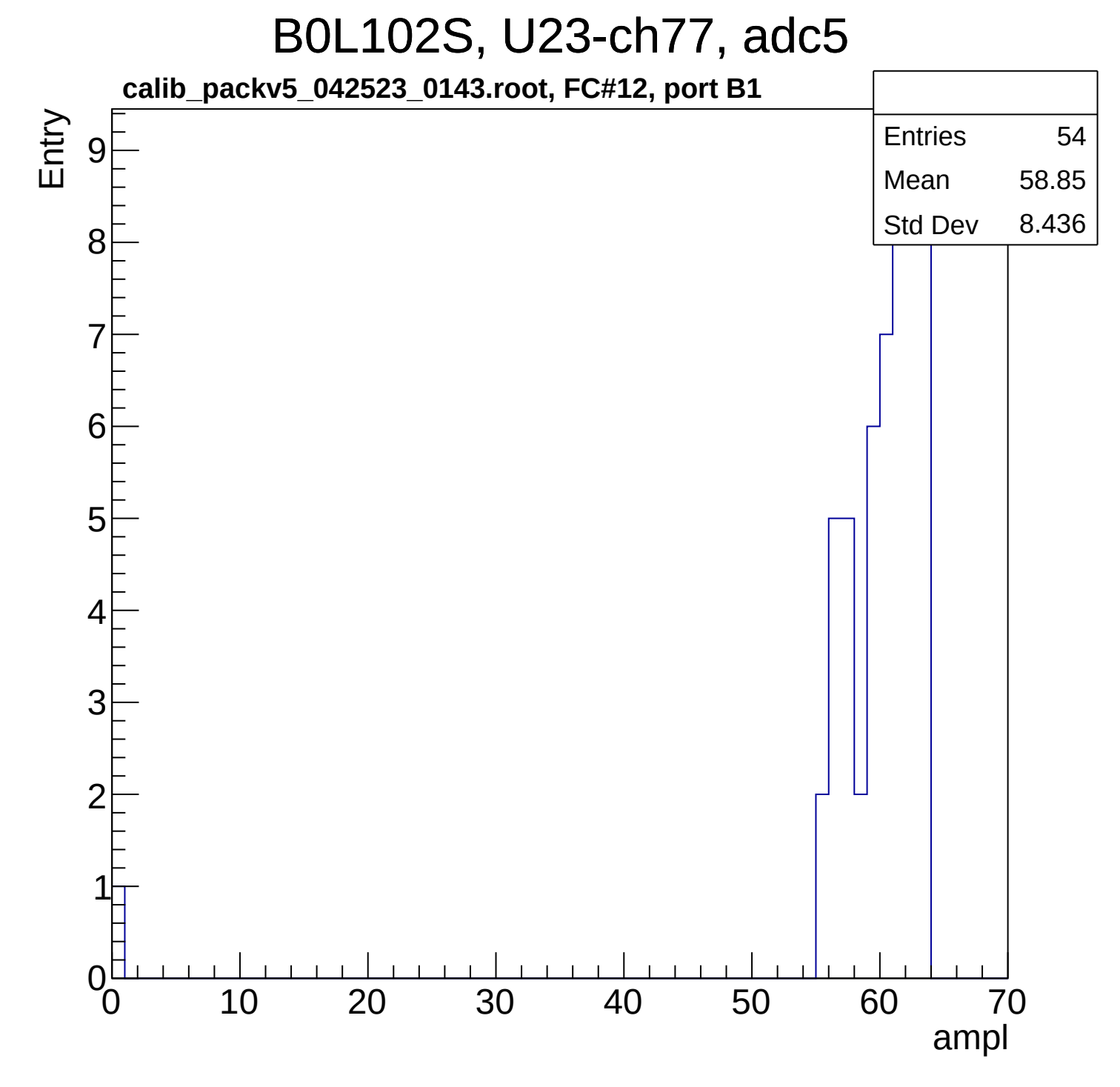
B0L102S, U23-ch77, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 58.85
Std Dev 8.436
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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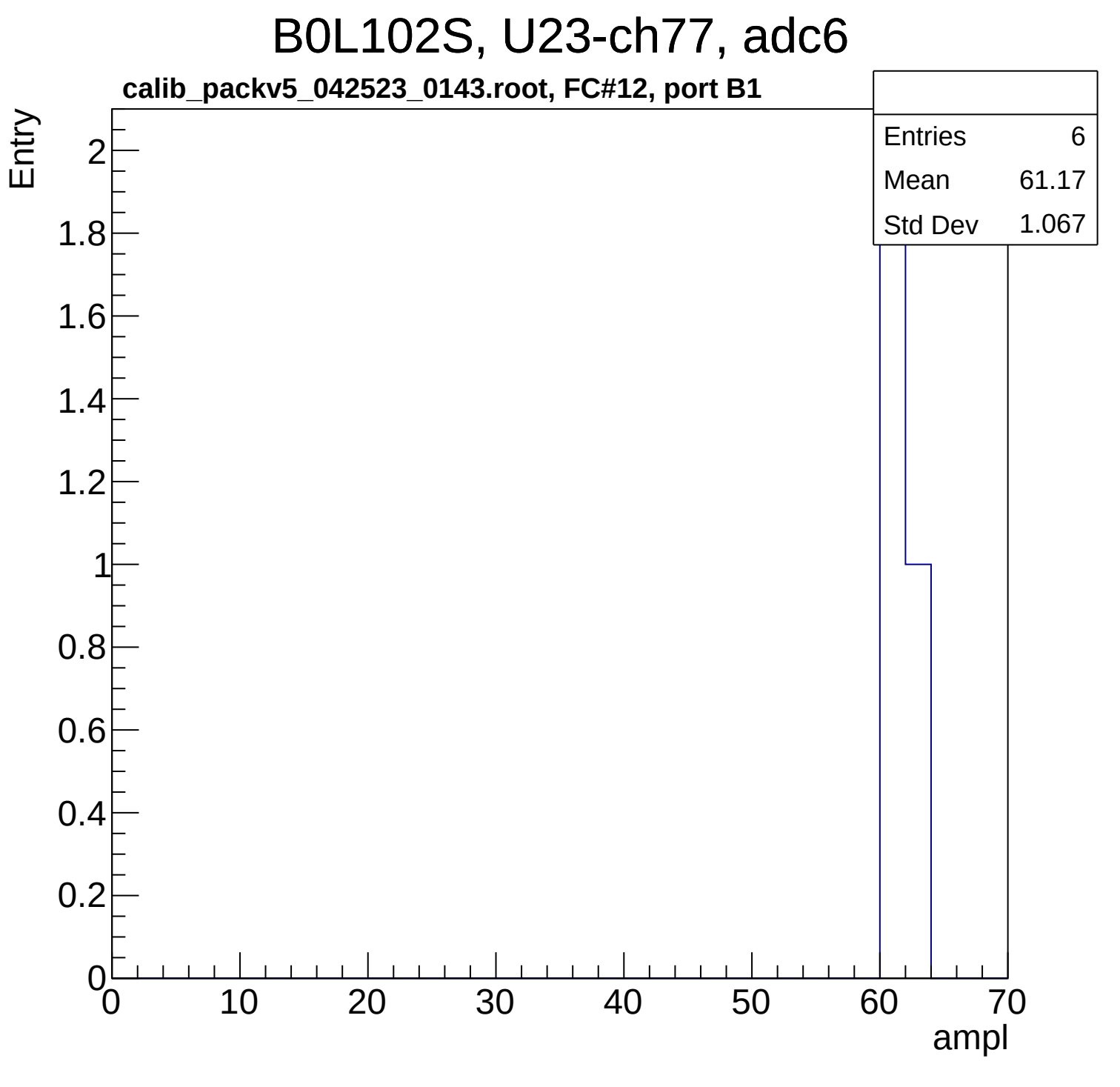
B0L102S, U23-ch77, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.17
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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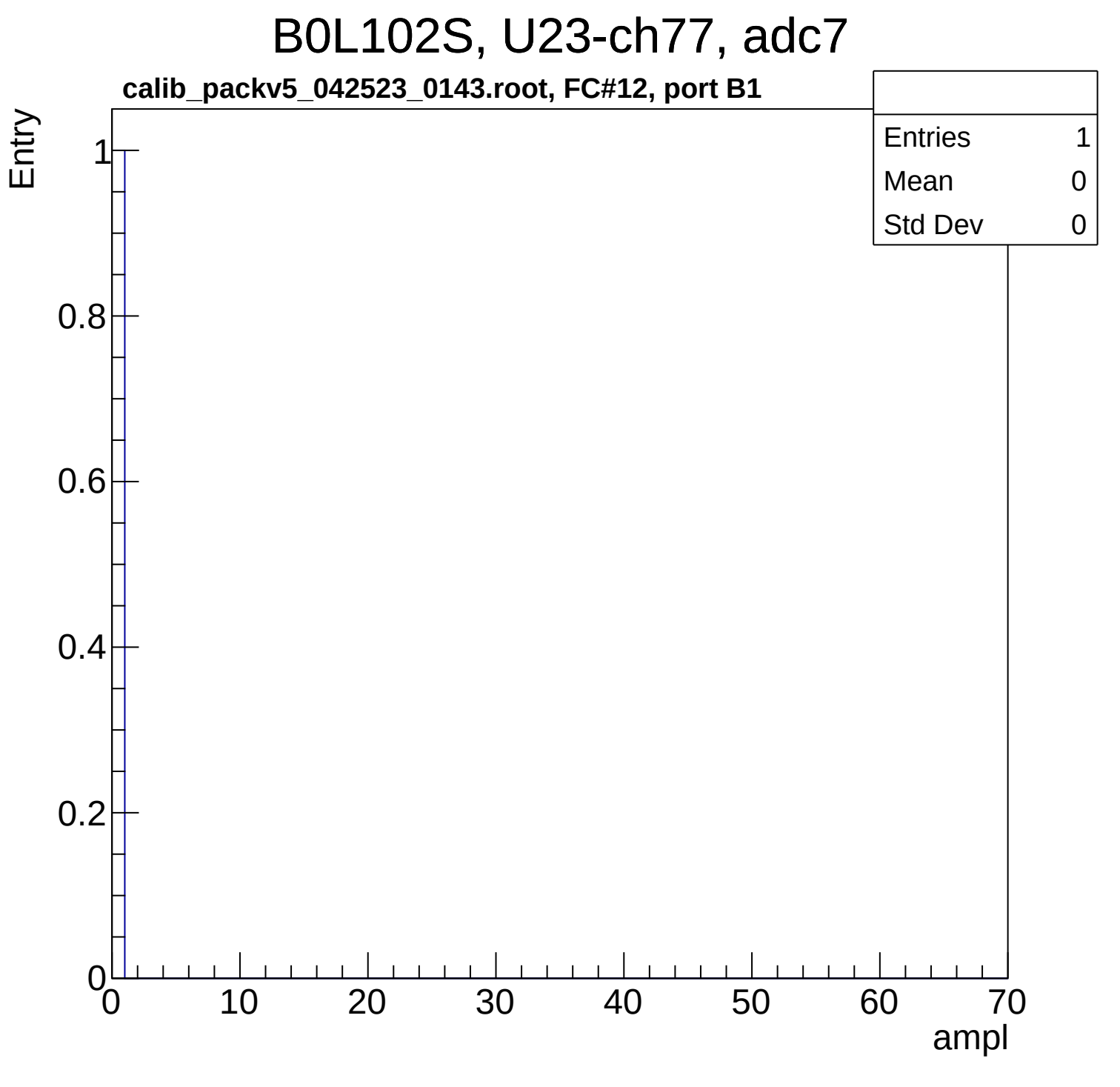
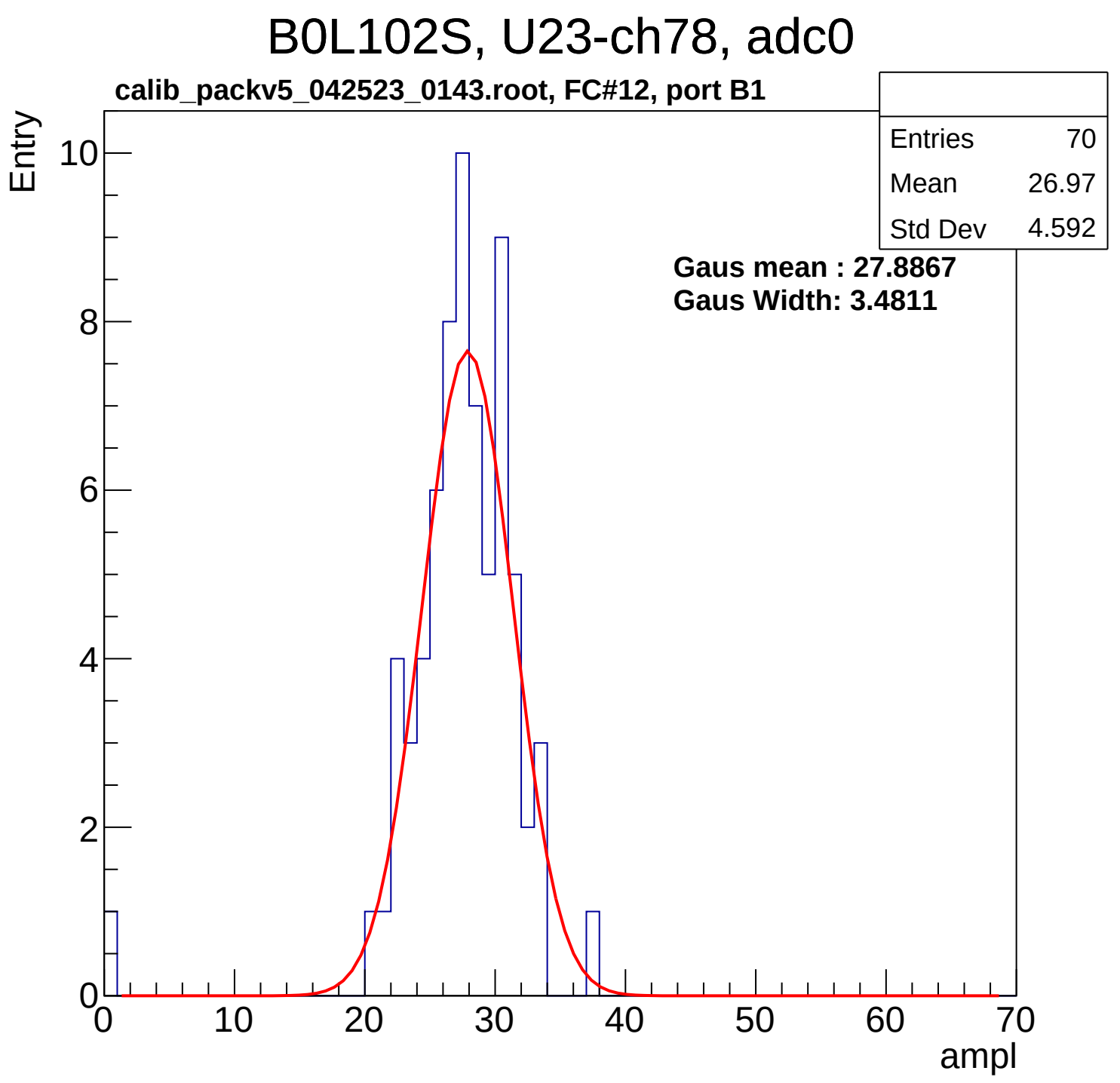
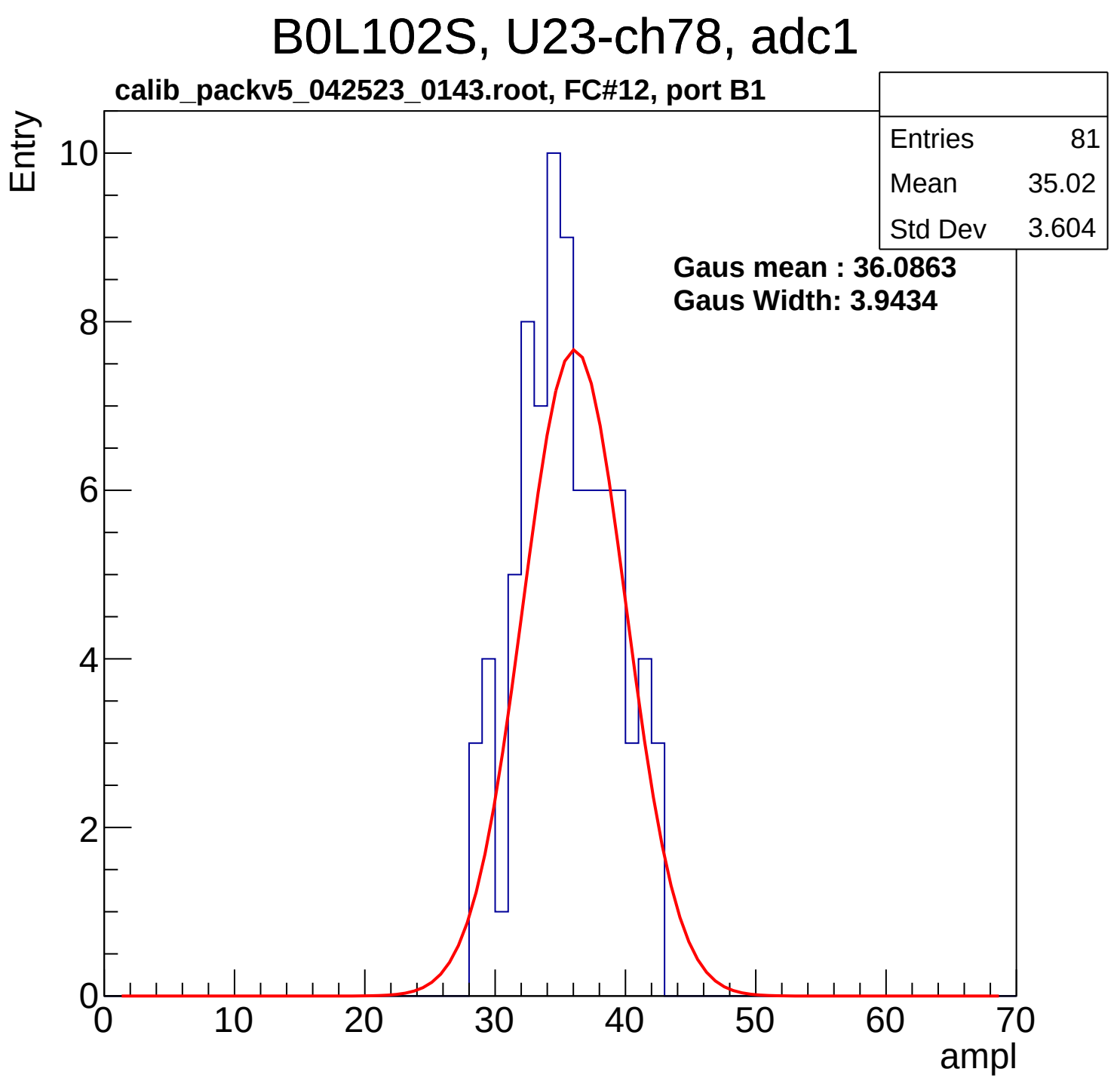
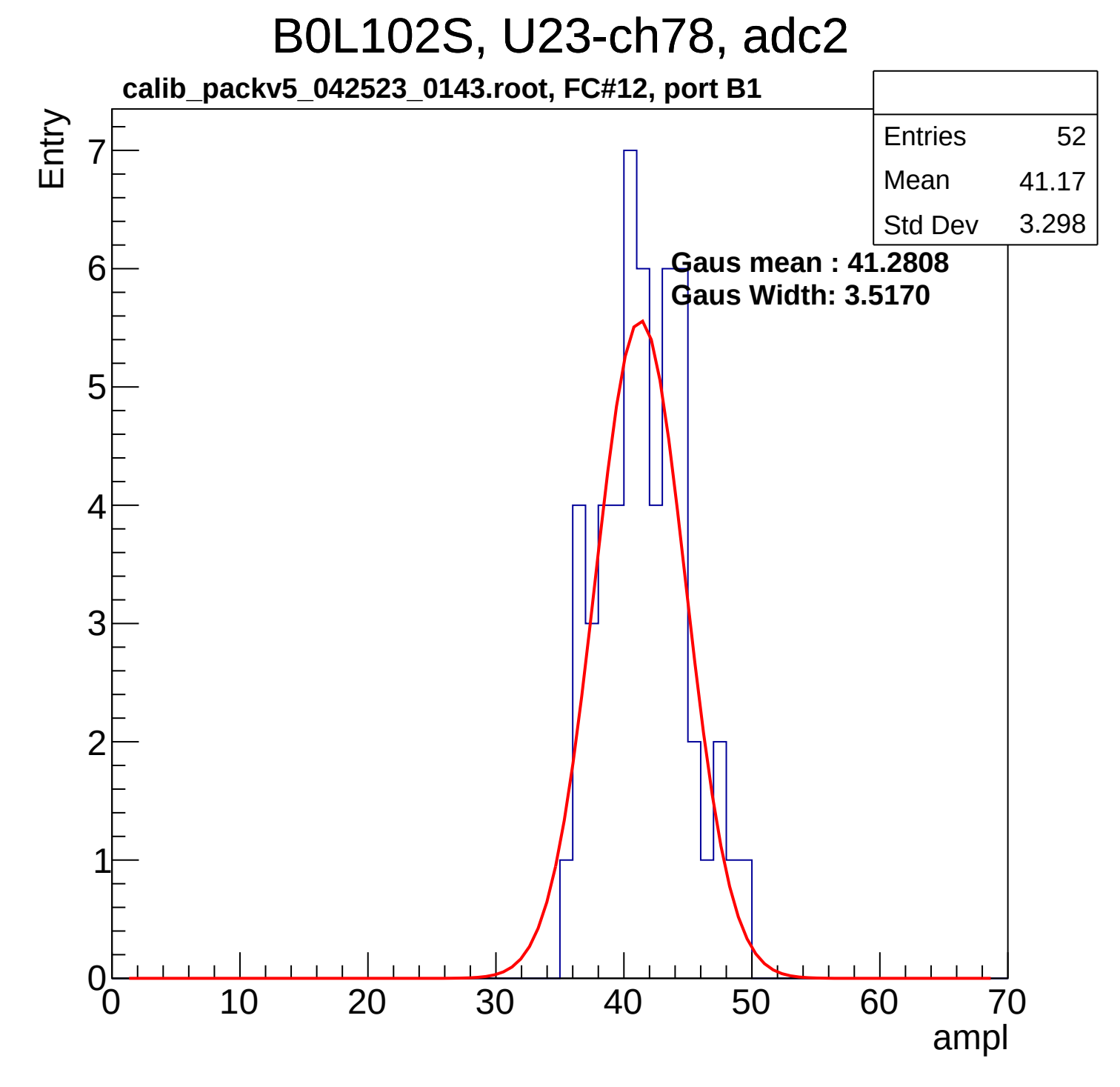
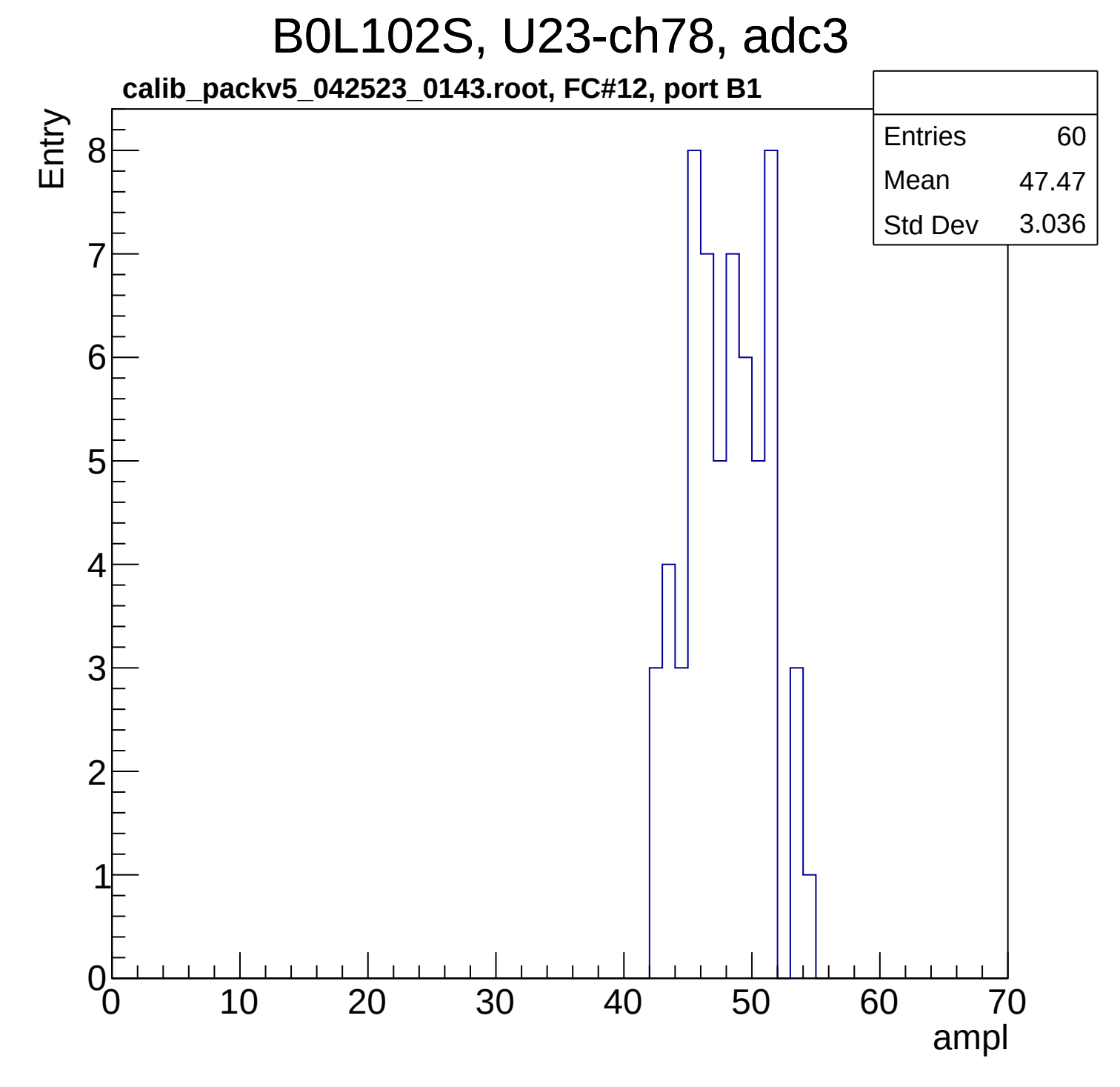
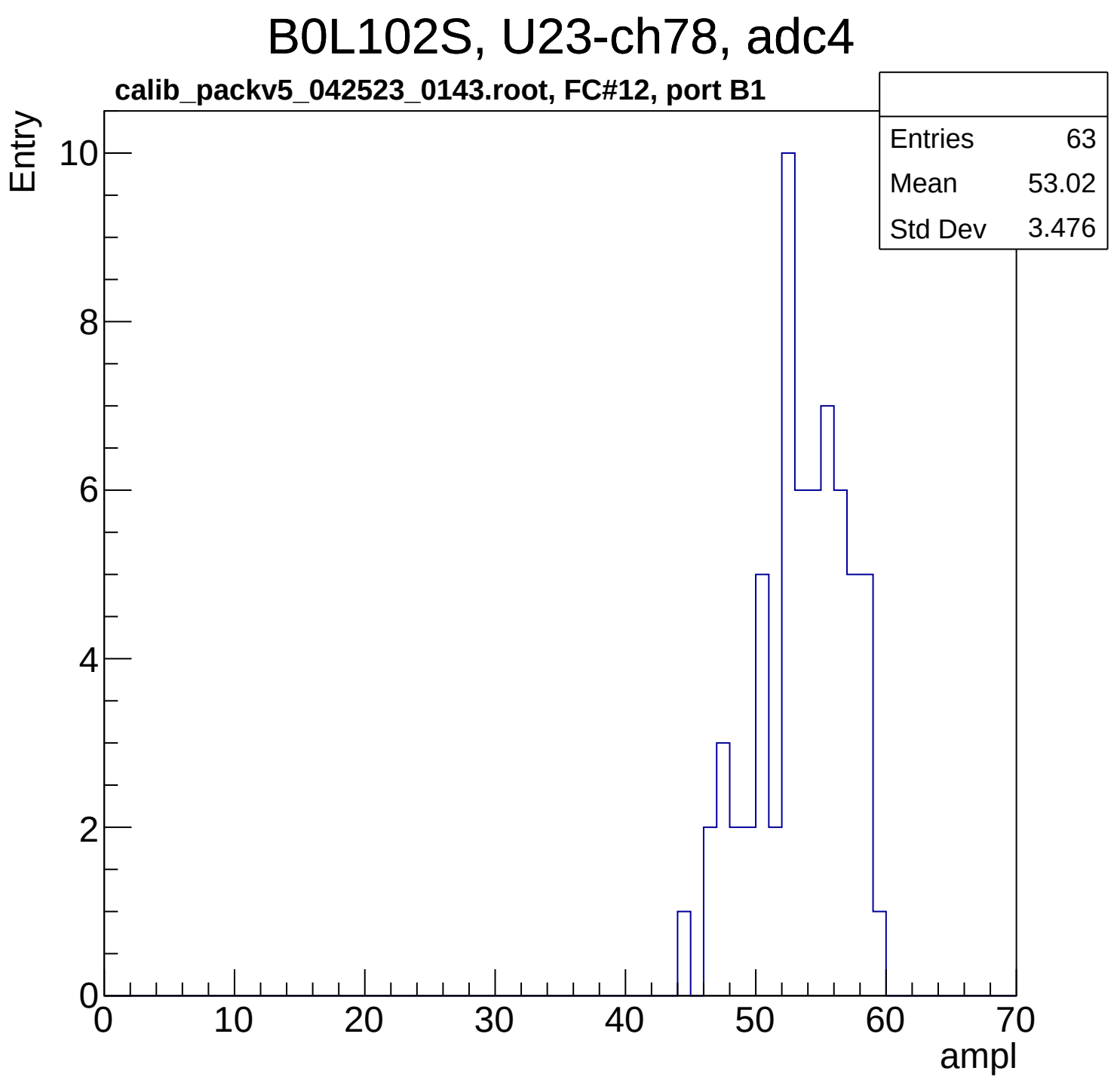

B0L102S, U23-ch77, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

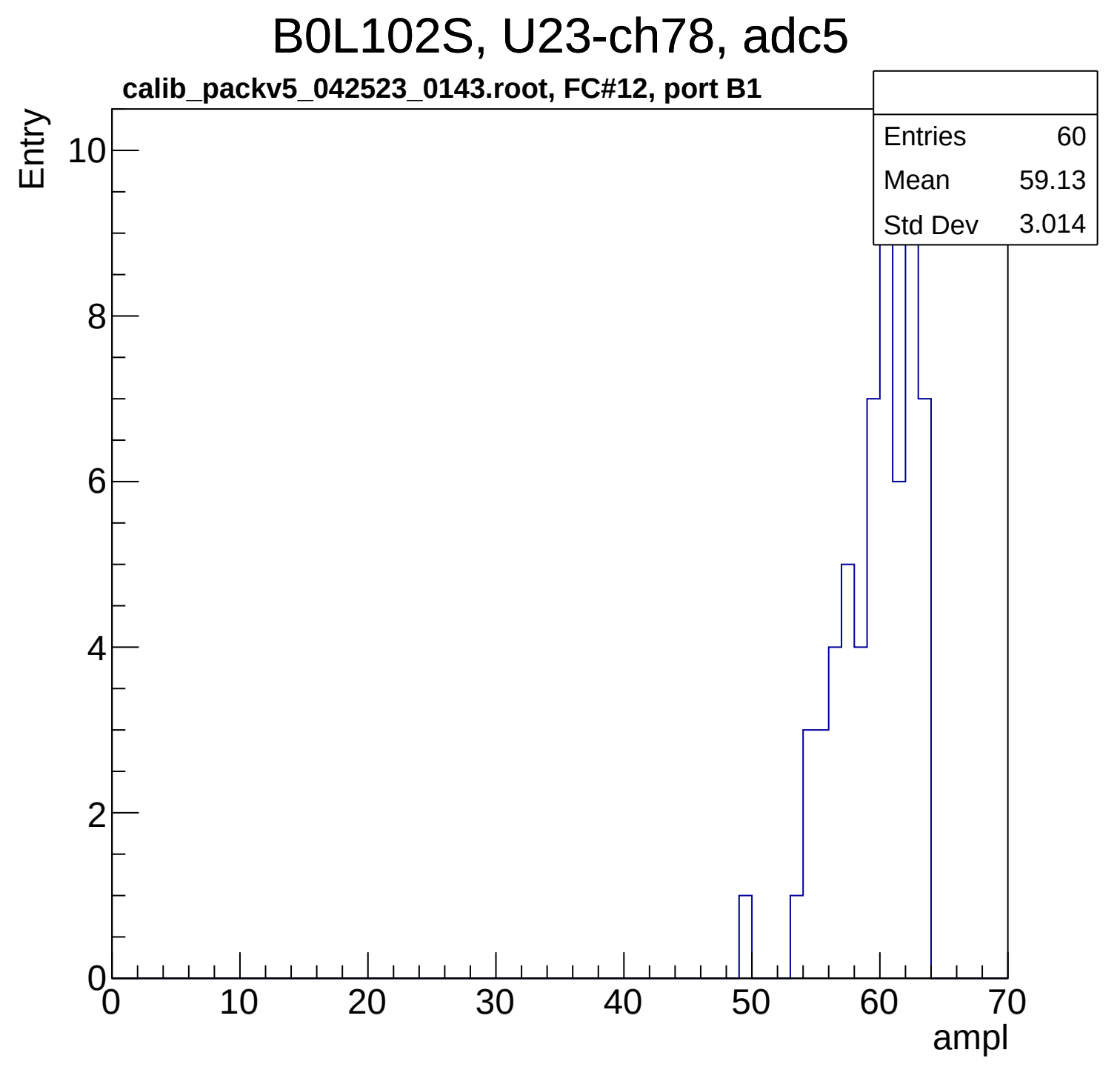

B0L102S, U23-ch78, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 26.97
Std Dev 4.592
Gaus mean : 27.8867
Gaus Width: 3.4811
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

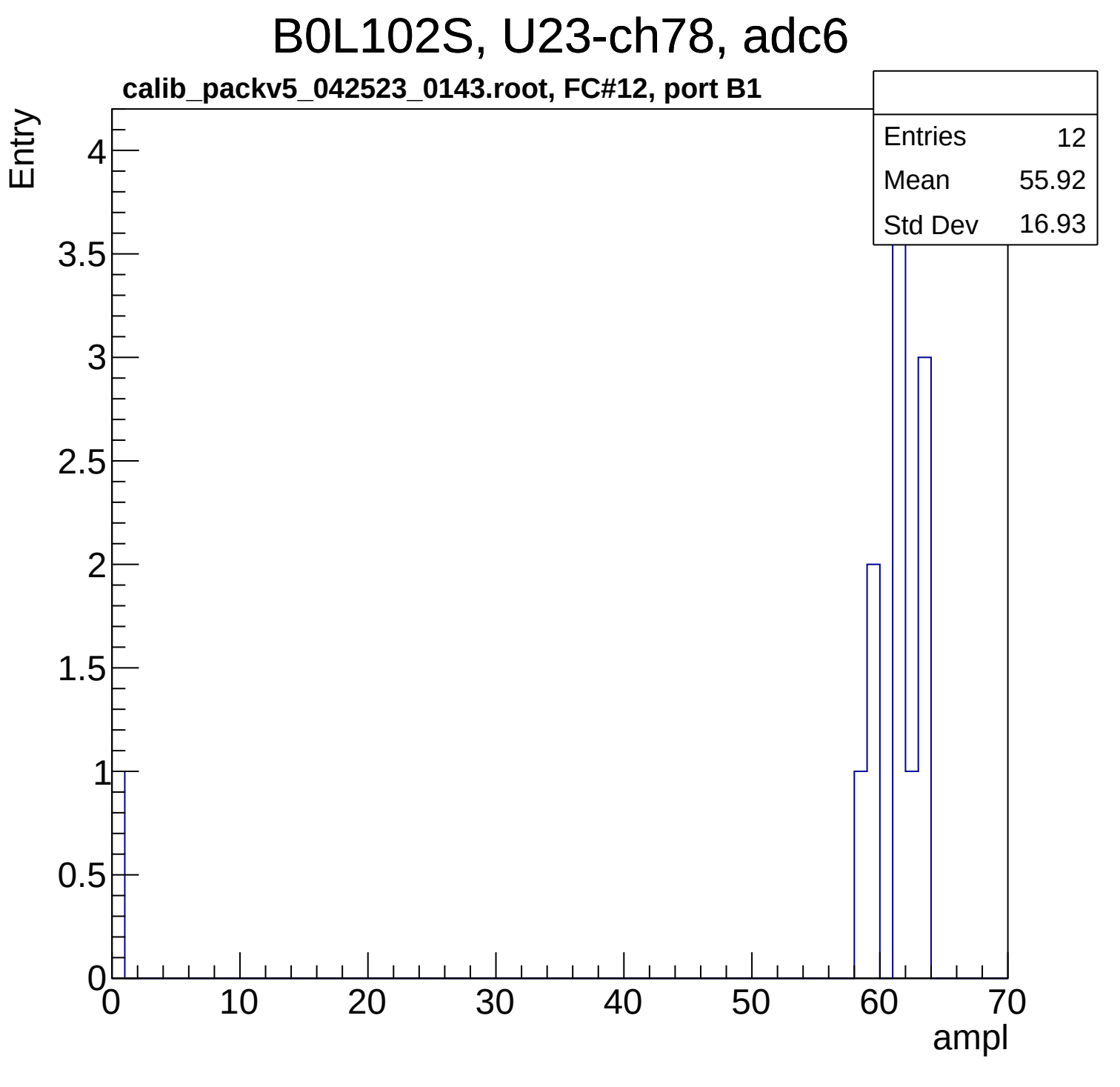

B0L102S, U23-ch78, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 35.02
Std Dev 3.604
Gaus mean : 36.0863
Gaus Width: 3.9434
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch78, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 41.17
Std Dev 3.298
Gaus mean : 41.2808
Gaus Width: 3.5170
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch78, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 47.47
Std Dev 3.036
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch78, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 53.02
Std Dev 3.476
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch78, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 59.13
Std Dev 3.014
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch78, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 55.92
Std Dev 16.93
Entry
ampl
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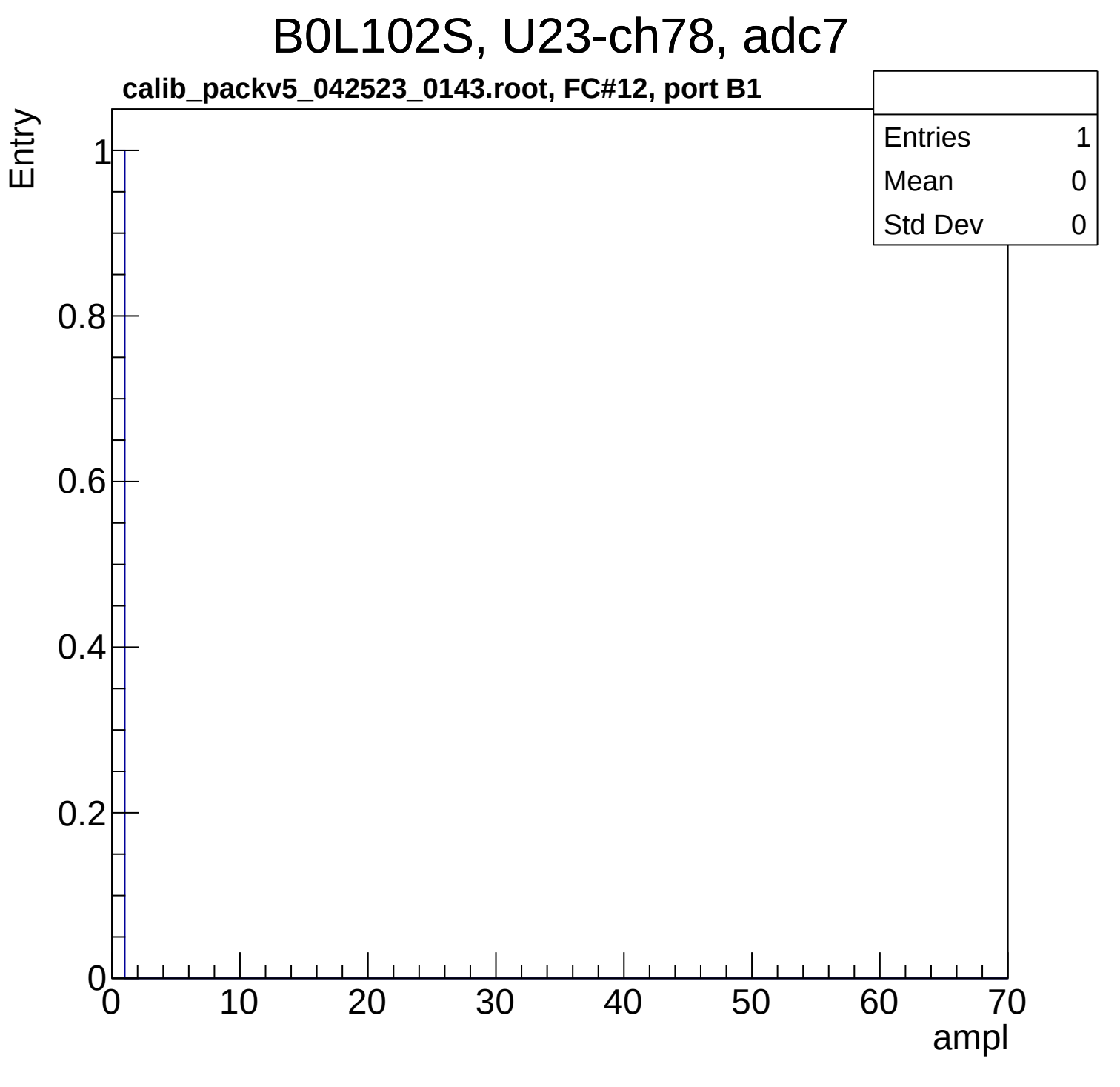
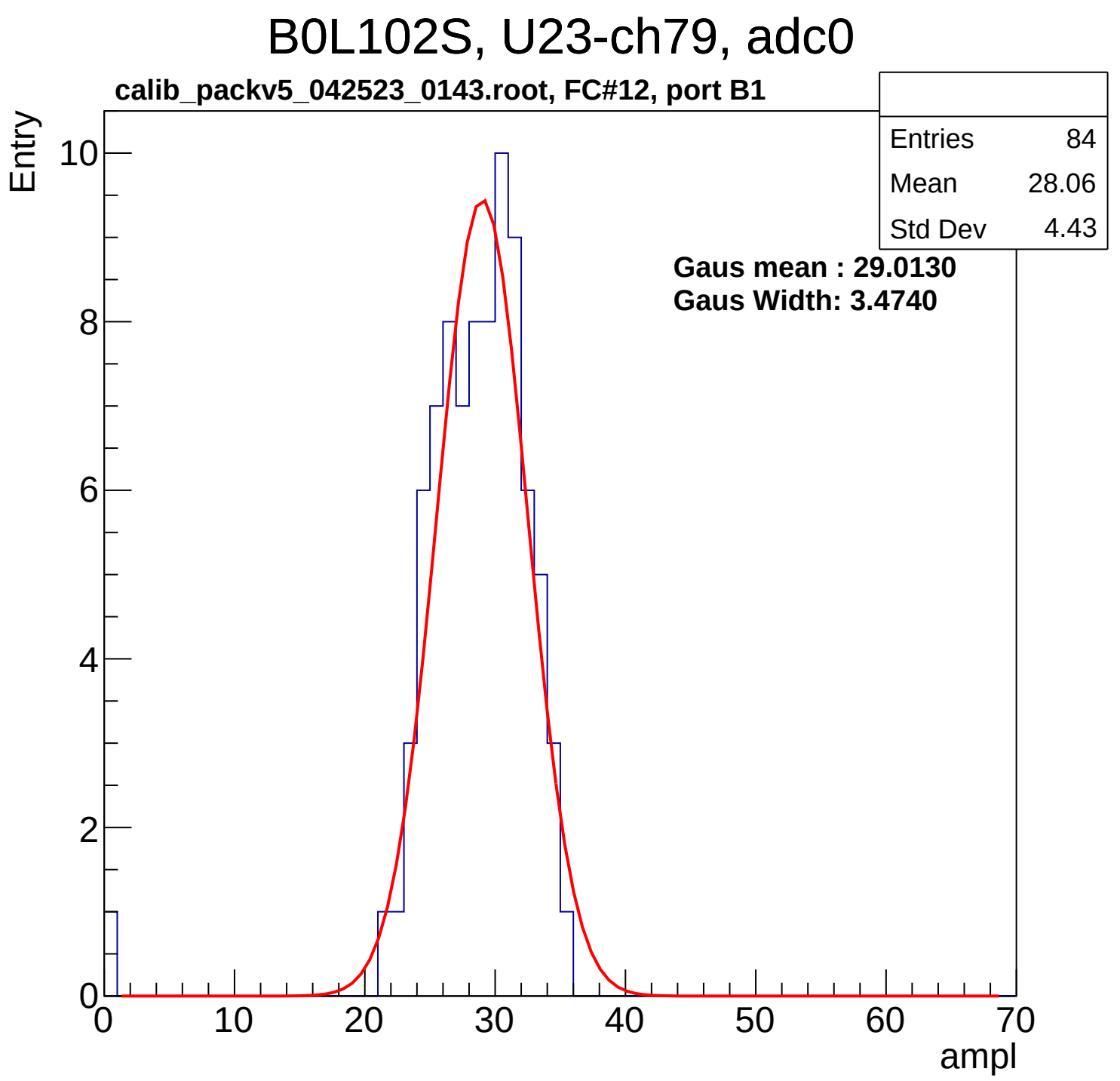
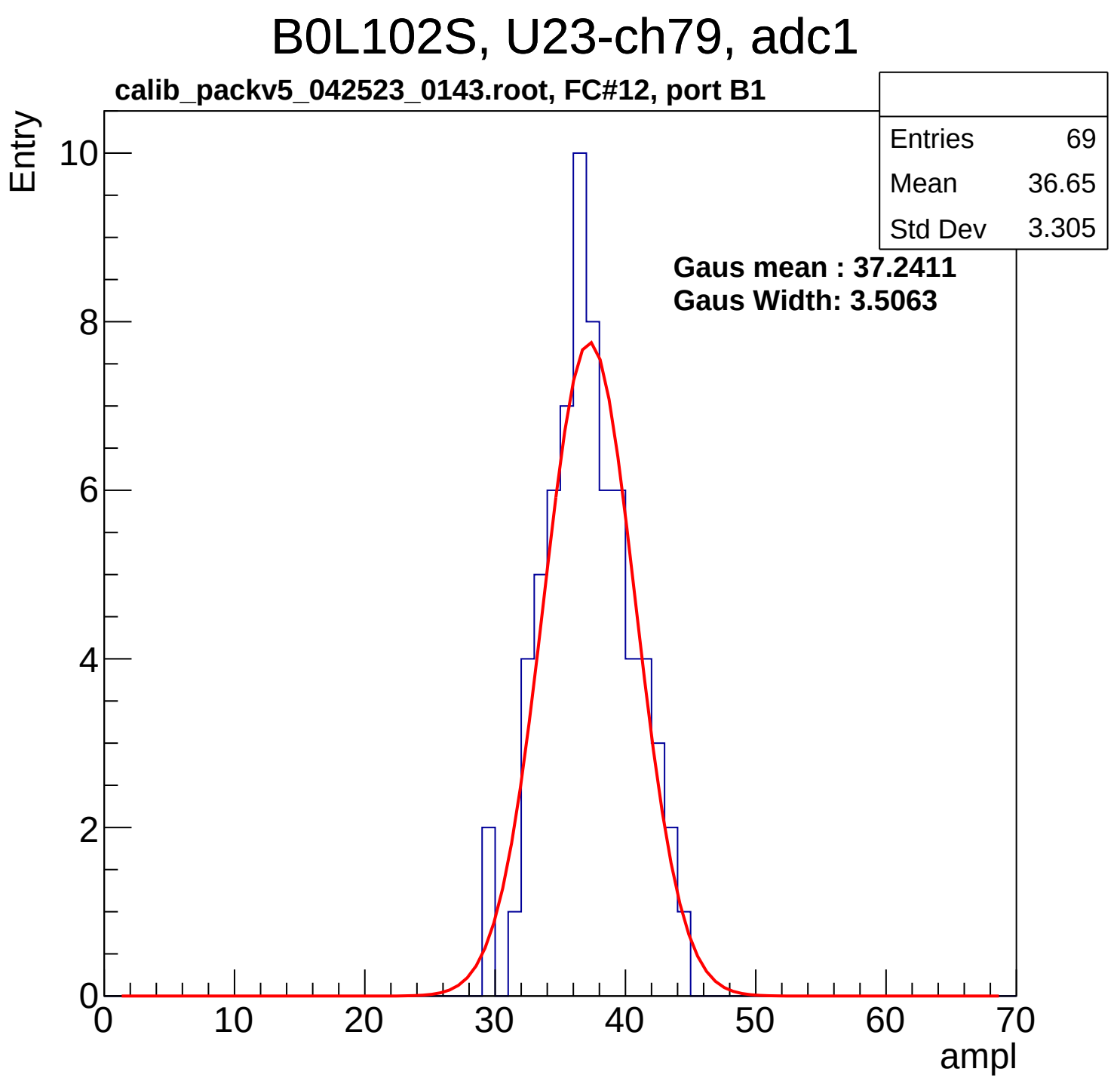
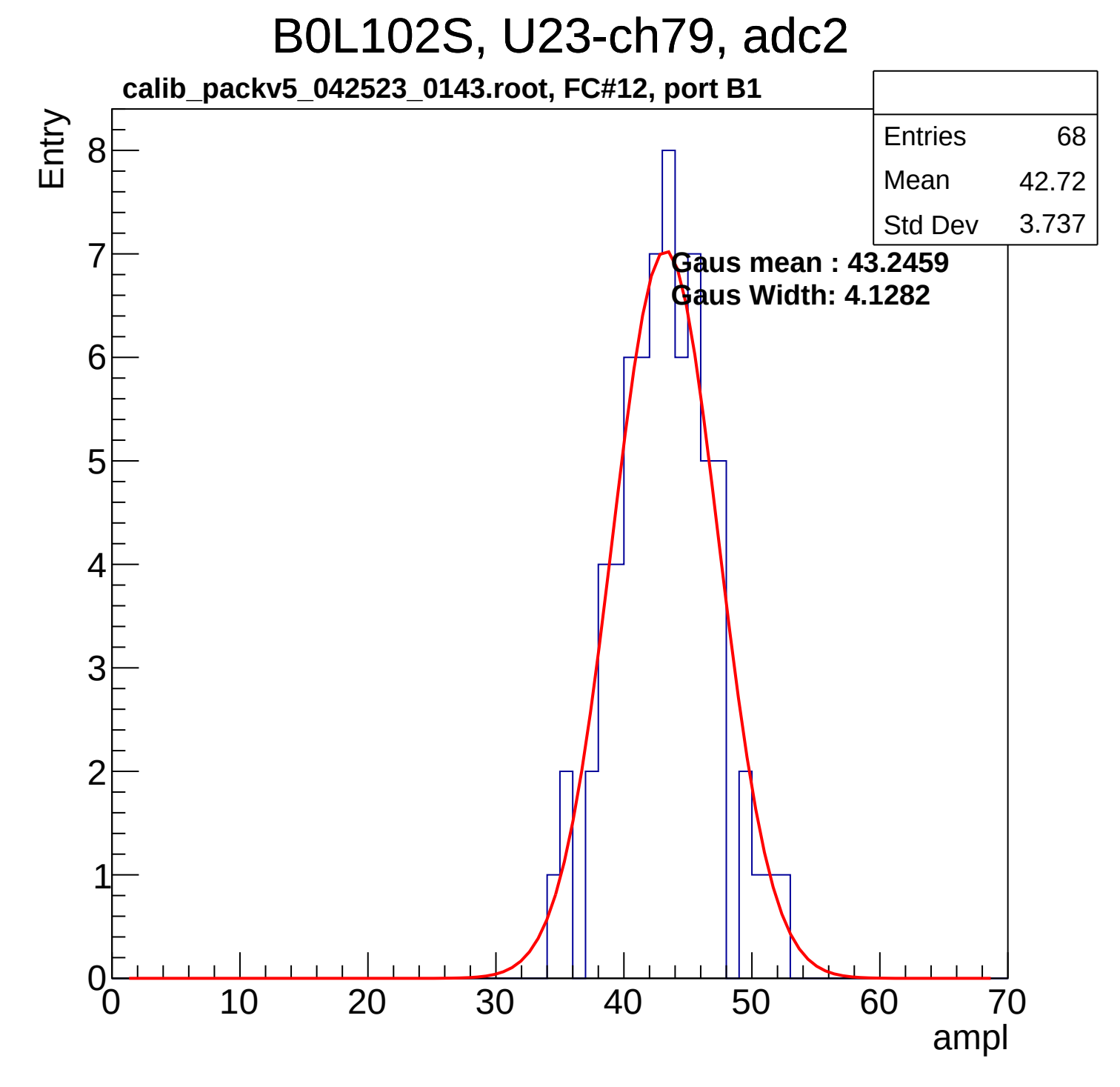
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch78, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch79, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 28.06
Std Dev 4.43
Gaus mean : 29.0130
Gaus Width: 3.4740
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch79, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 36.65
Std Dev 3.305
Gaus mean : 37.2411
Gaus Width: 3.5063
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch79, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 42.72
Std Dev 3.737
Gaus mean : 43.2459
Gaus Width: 4.1282
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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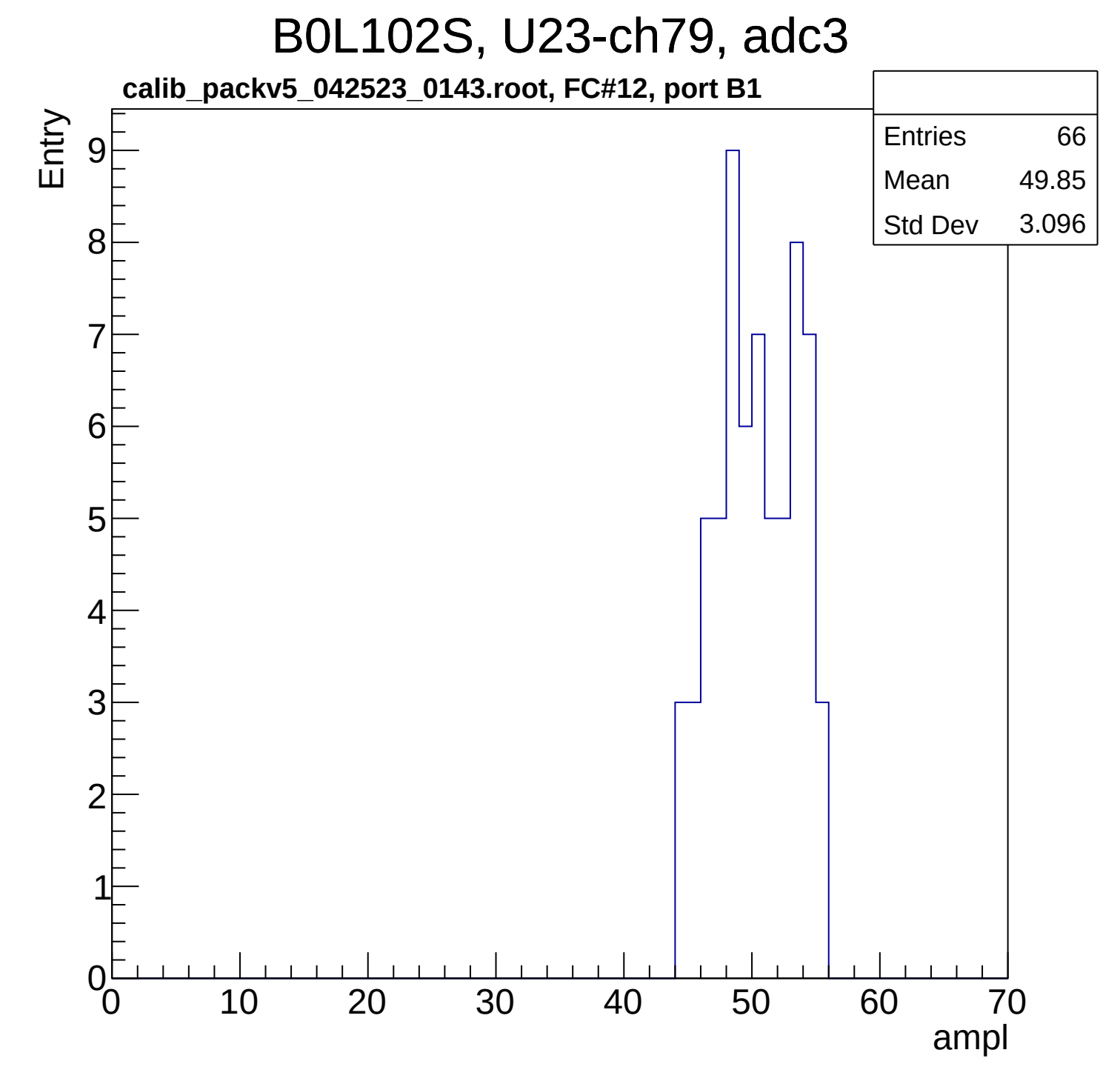
B0L102S, U23-ch79, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 49.85
Std Dev 3.096
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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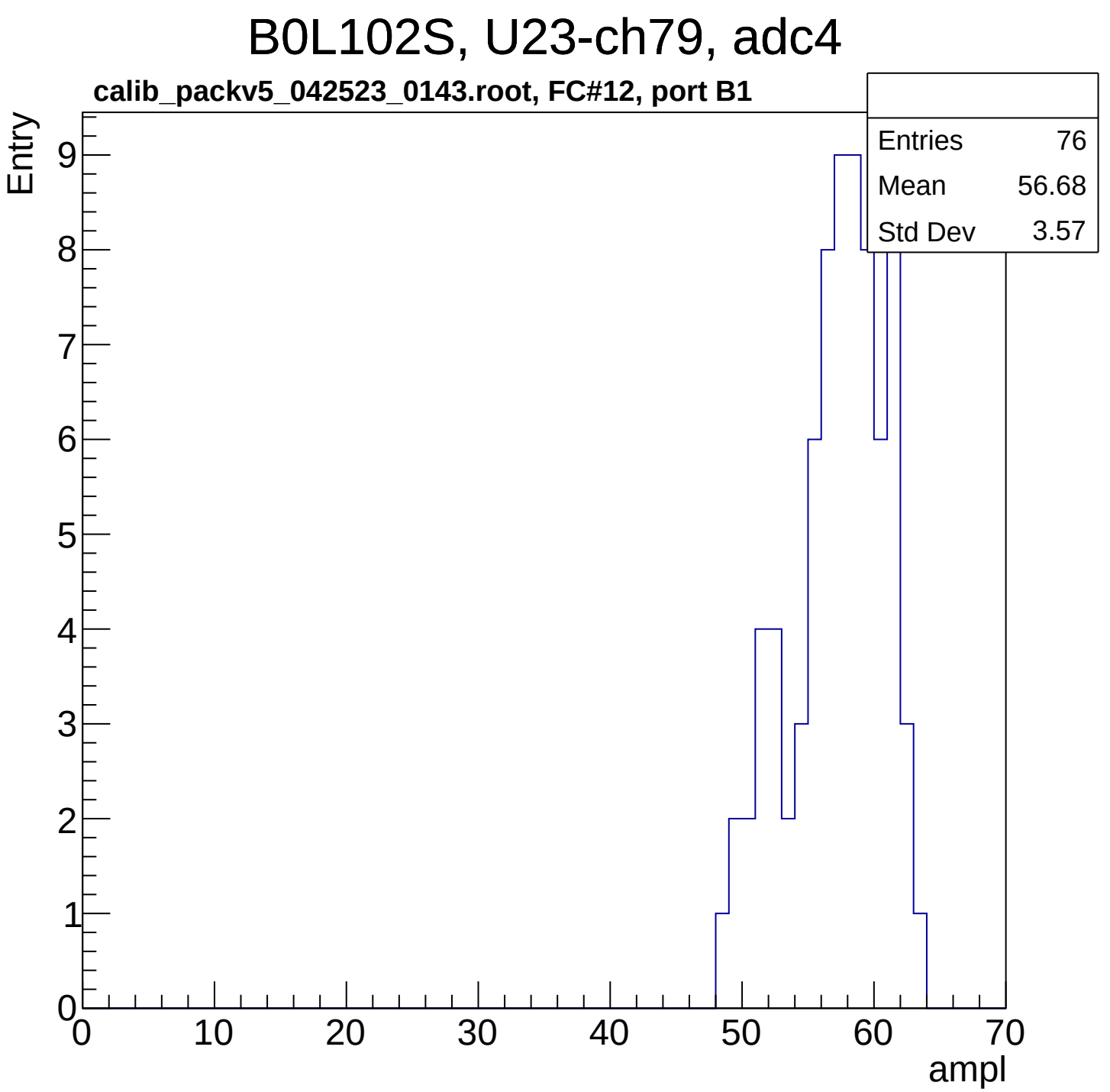
B0L102S, U23-ch79, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 56.68
Std Dev 3.57
Entry
ampl
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3
4
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7
8
9
0
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20
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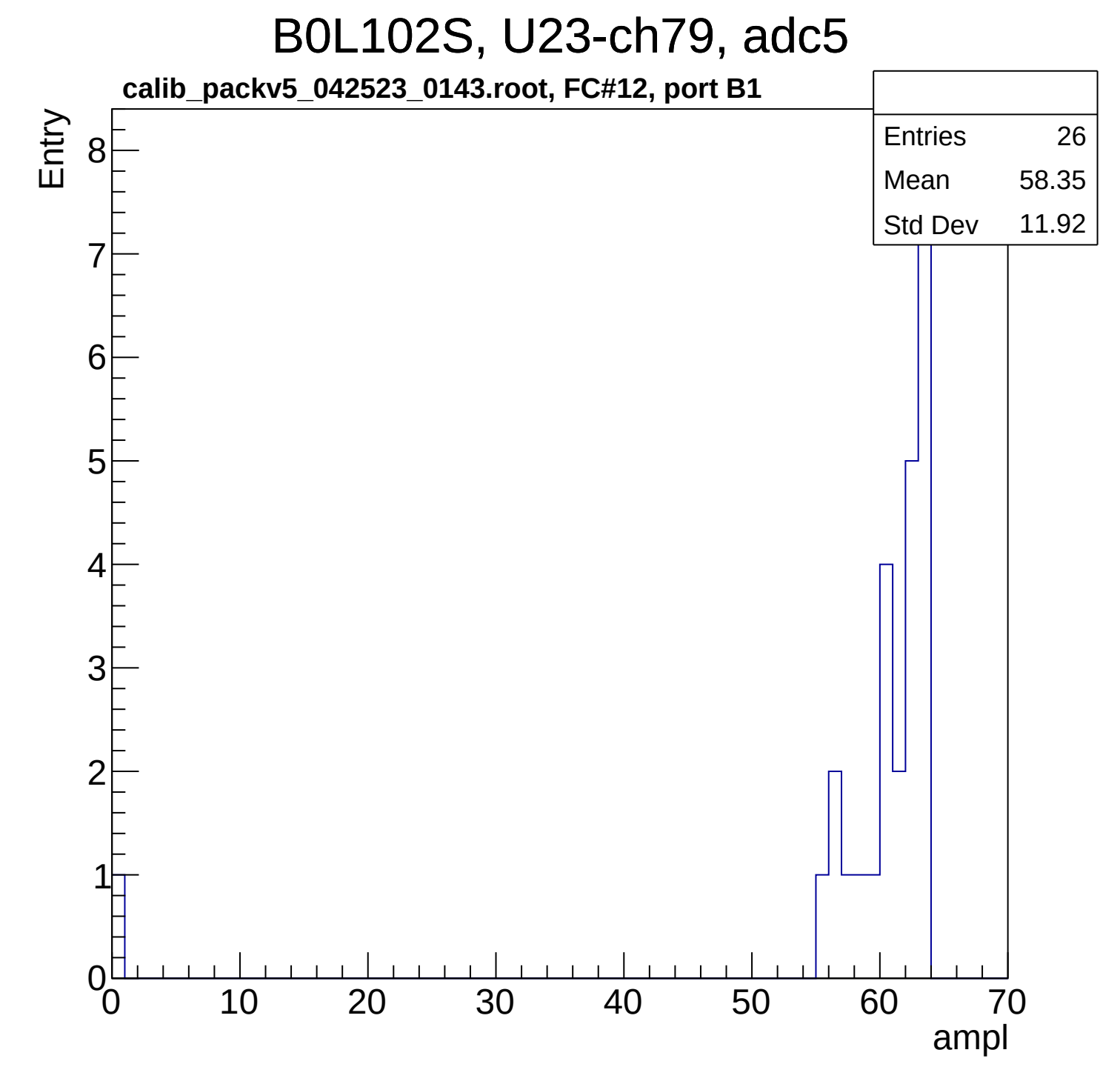
B0L102S, U23-ch79, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 26
Mean 58.35
Std Dev 11.92
Entry
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3
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5
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8
0
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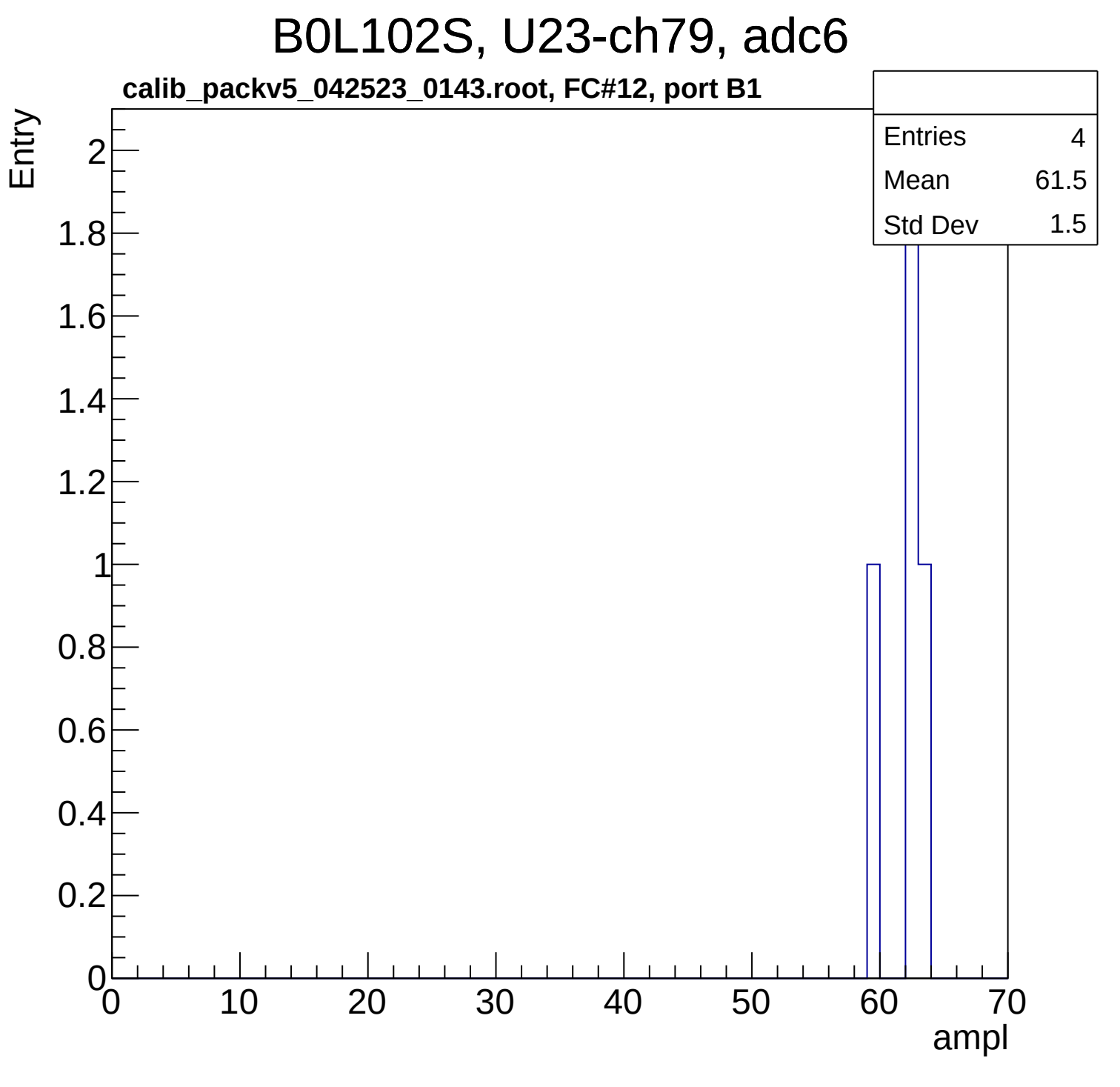
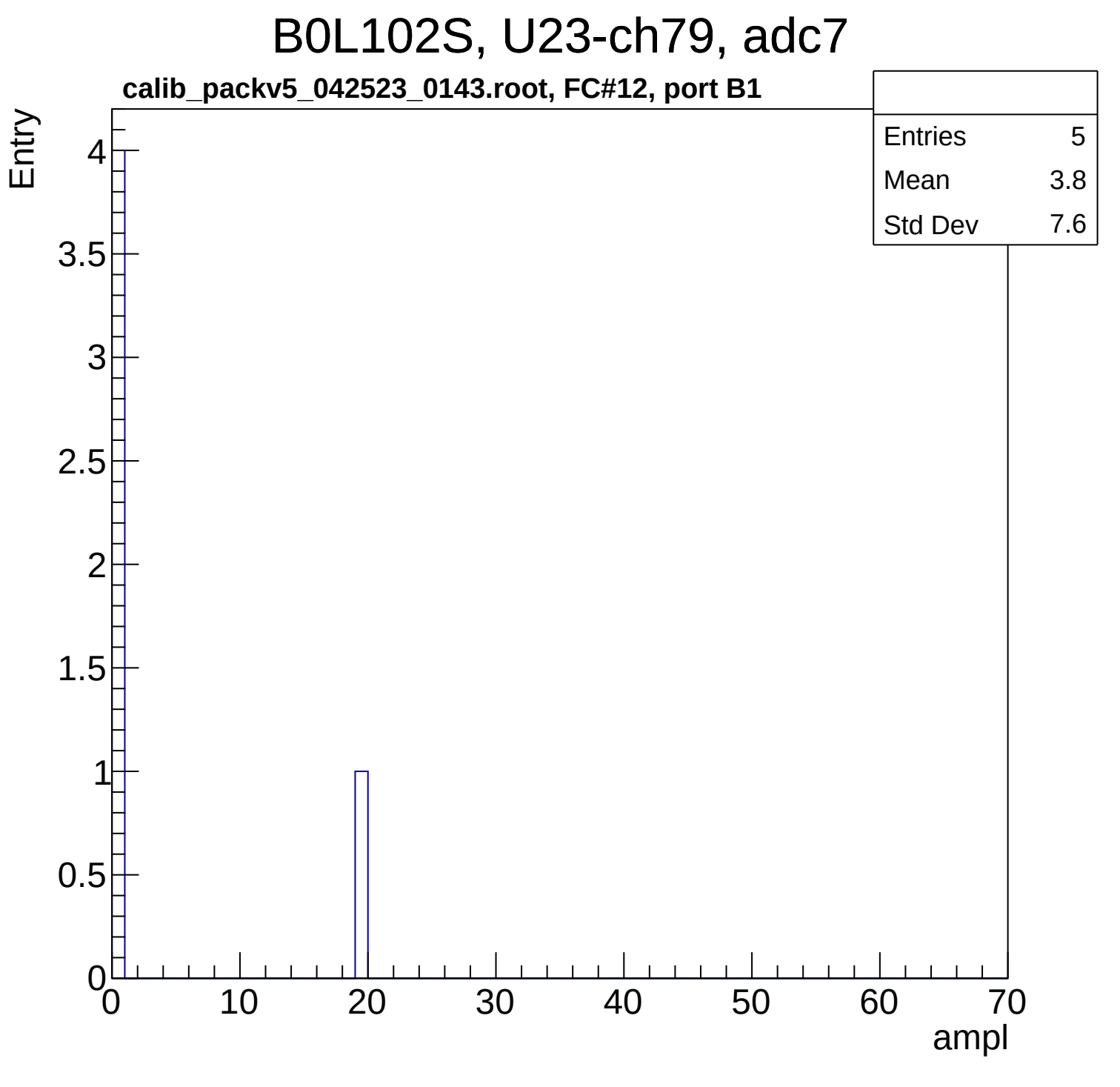
50
60
70
ampl


B0L102S, U23-ch79, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch79, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 3.8
Std Dev 7.6
Entry
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0.5
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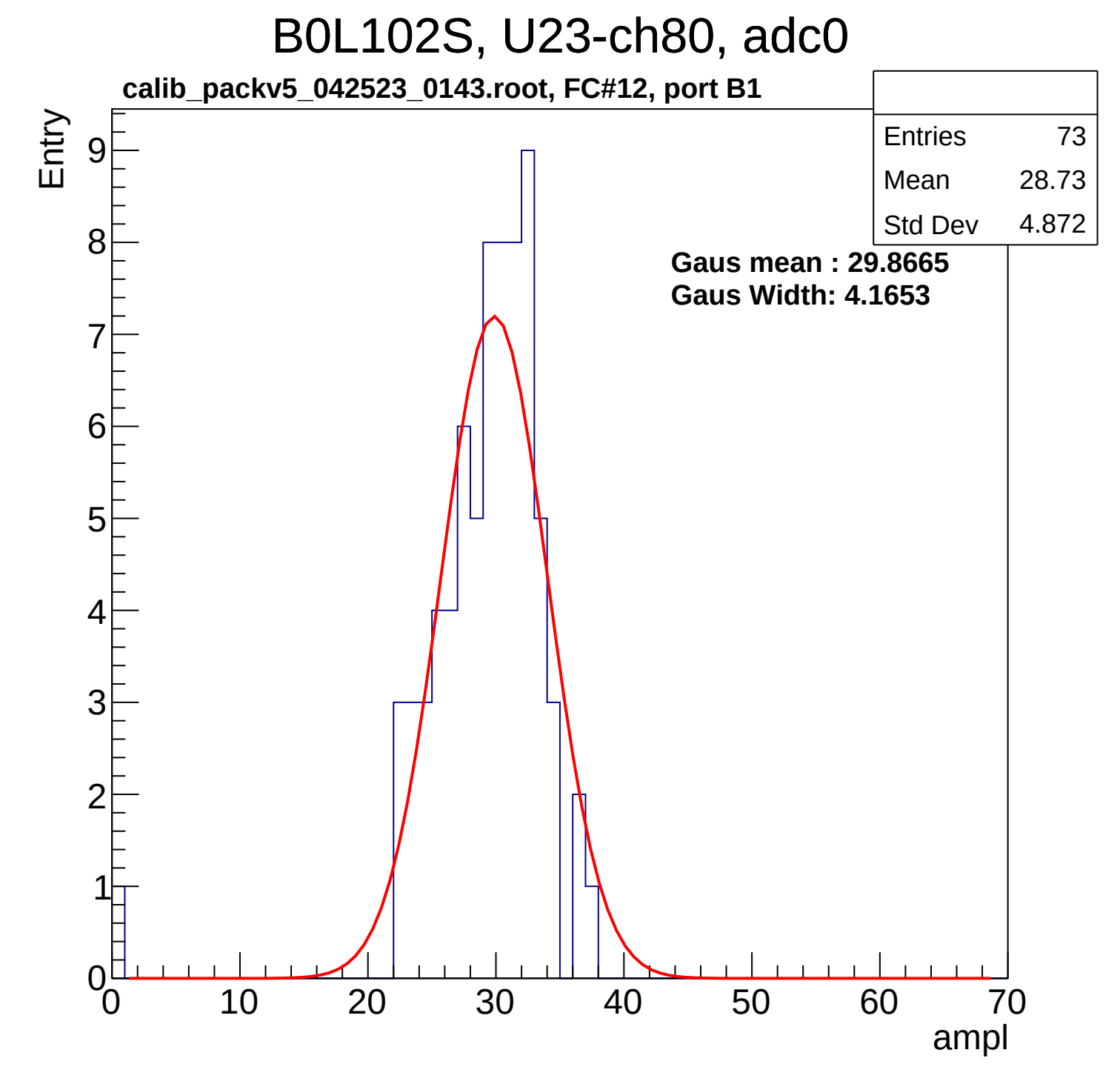
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch80, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 28.73
Std Dev 4.872
Gaus mean : 29.8665
Gaus Width: 4.1653
Entry
ampl
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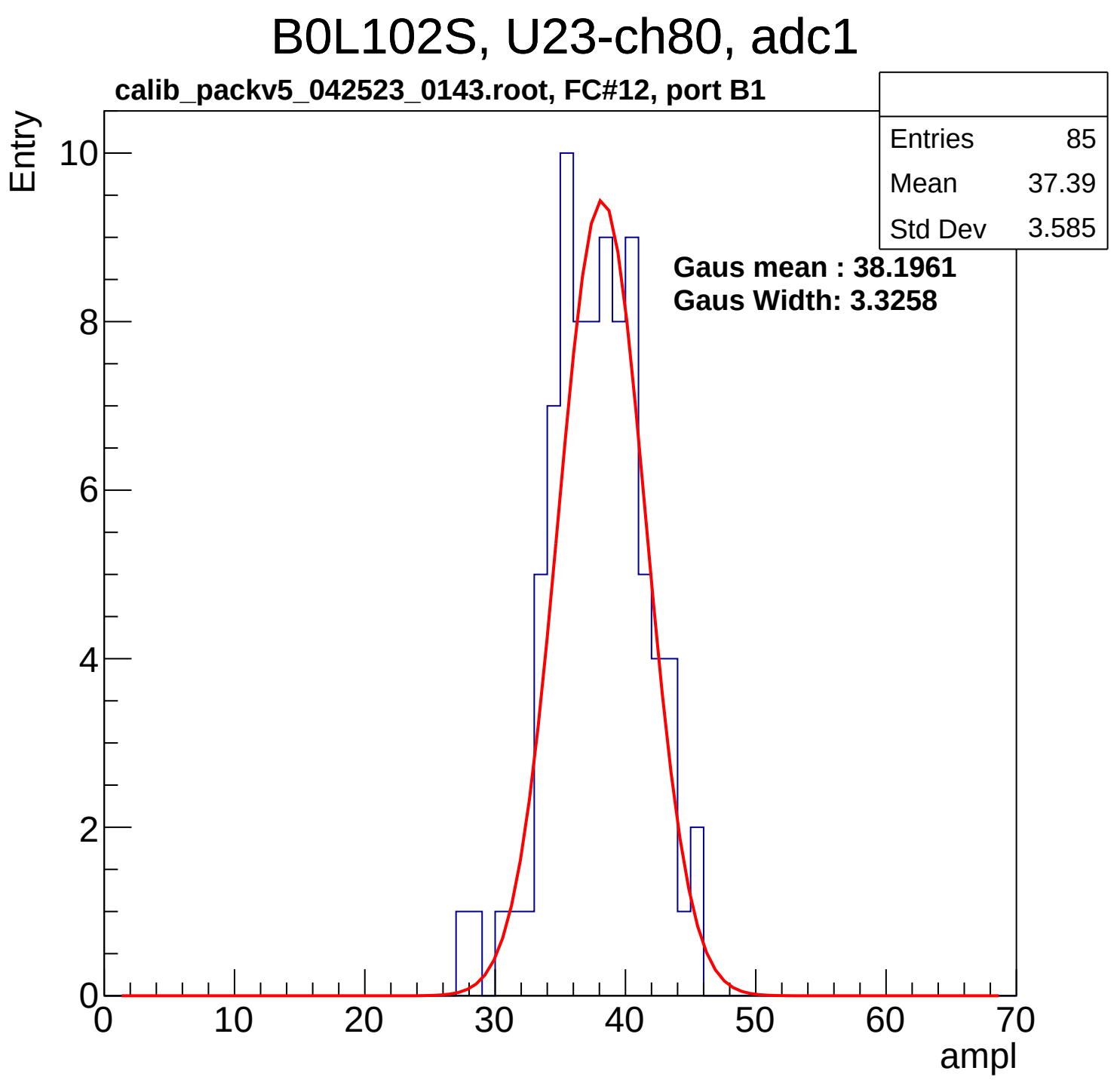
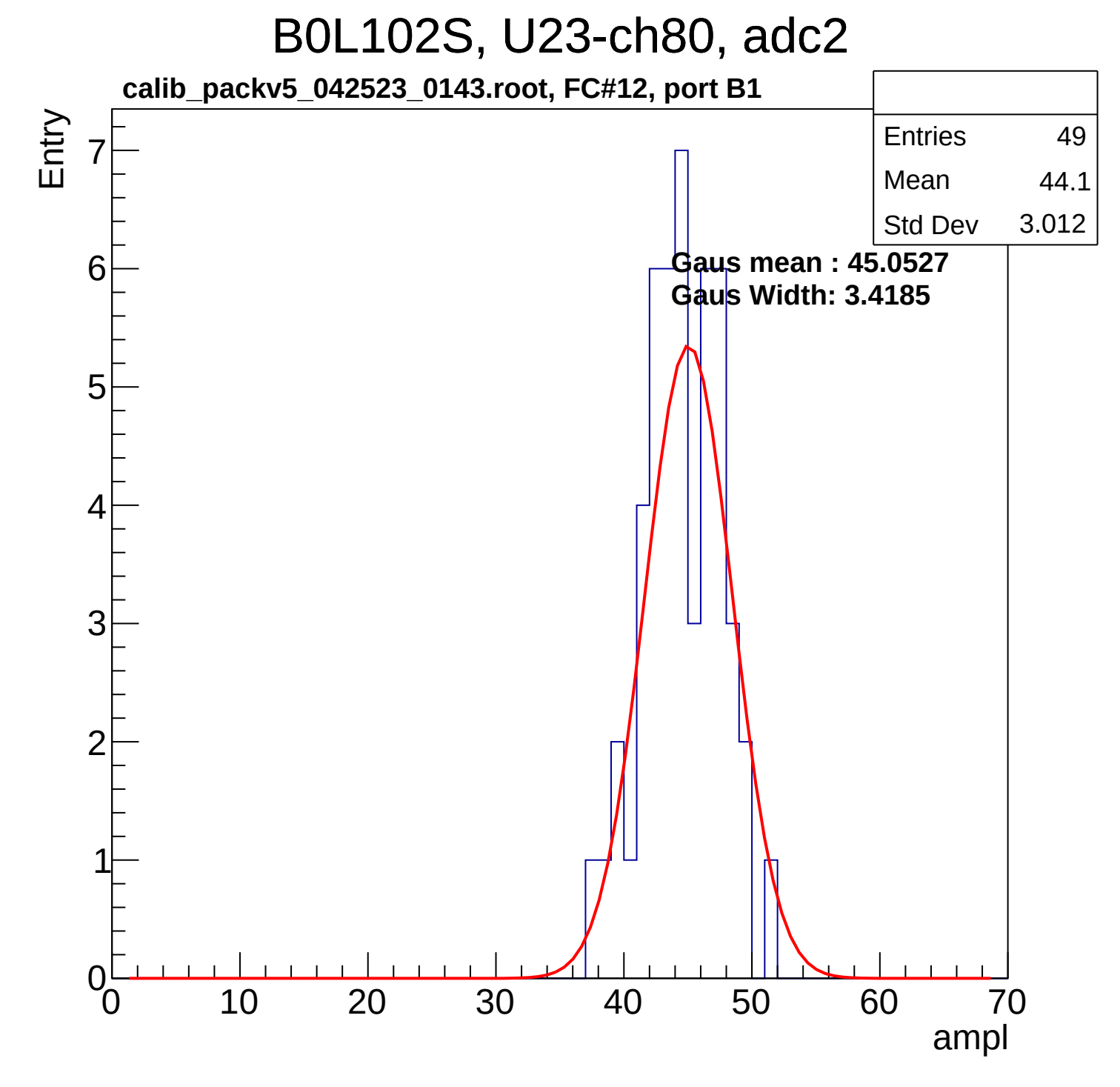
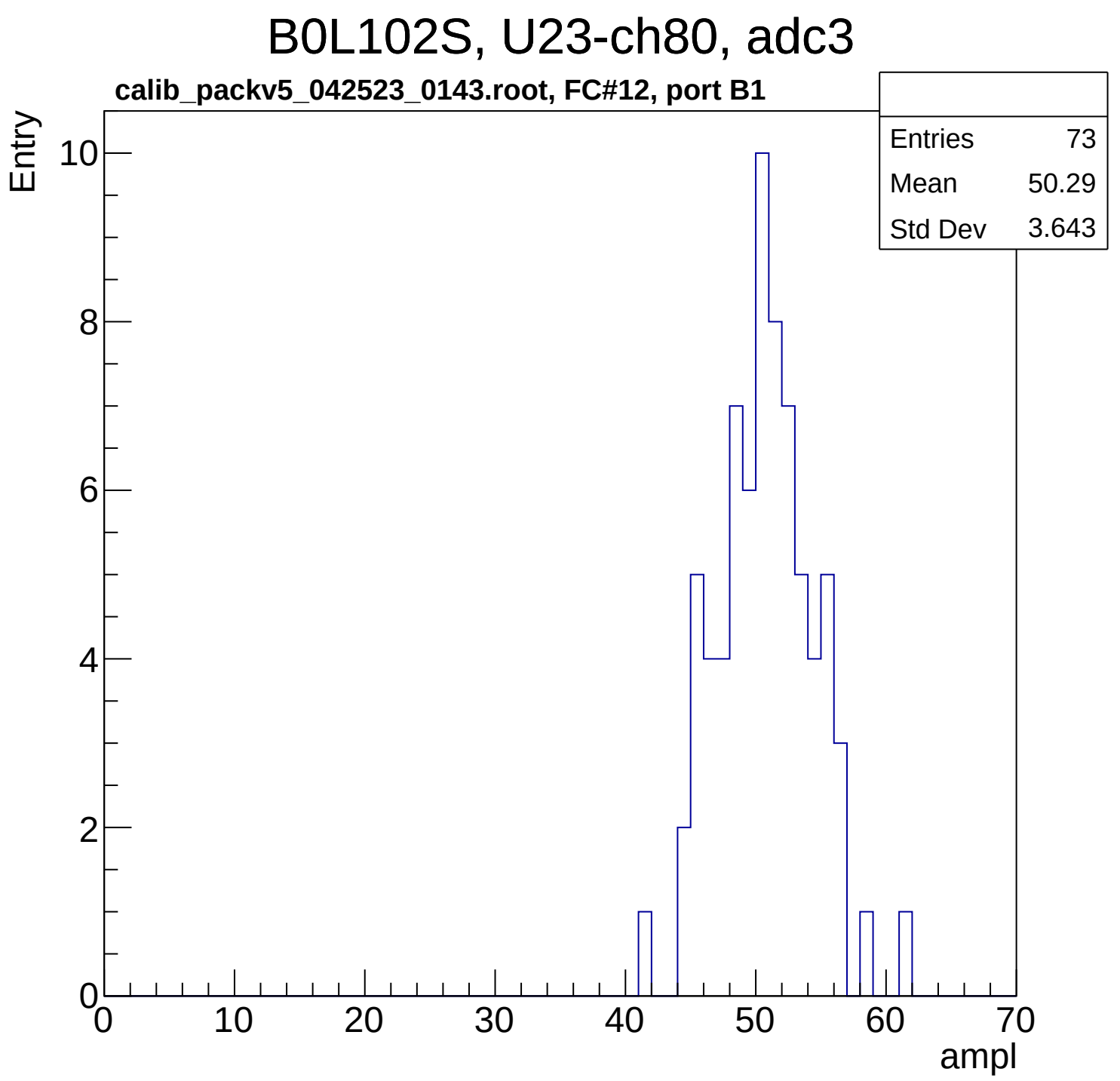
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch80, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 37.39
Std Dev 3.585
Gaus mean : 38.1961
Gaus Width: 3.3258
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch80, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 44.1
Std Dev 3.012
Gaus mean : 45.0527
Gaus Width: 3.4185
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch80, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 50.29
Std Dev 3.643
Entry
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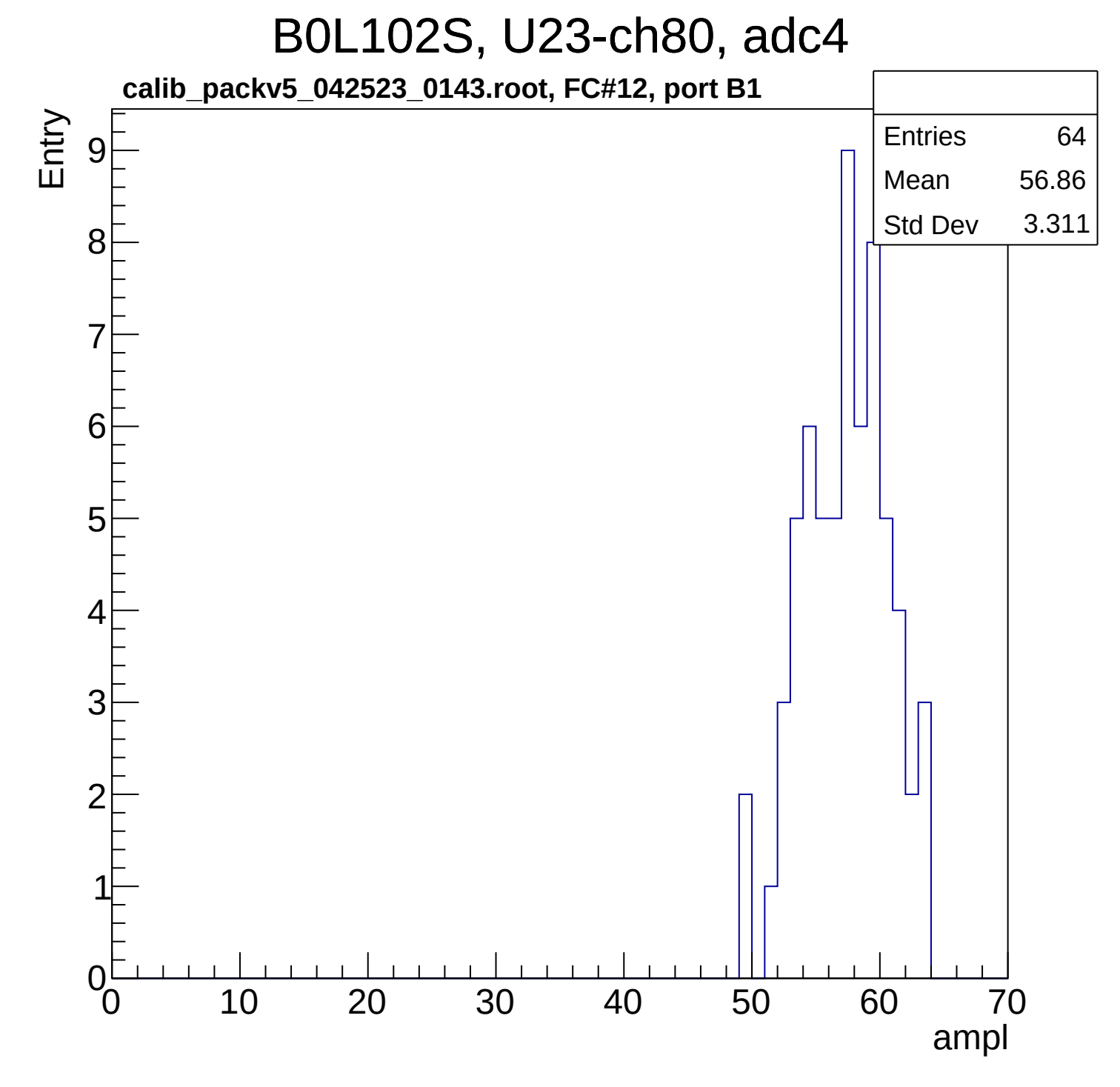
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch80, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.86
Std Dev 3.311
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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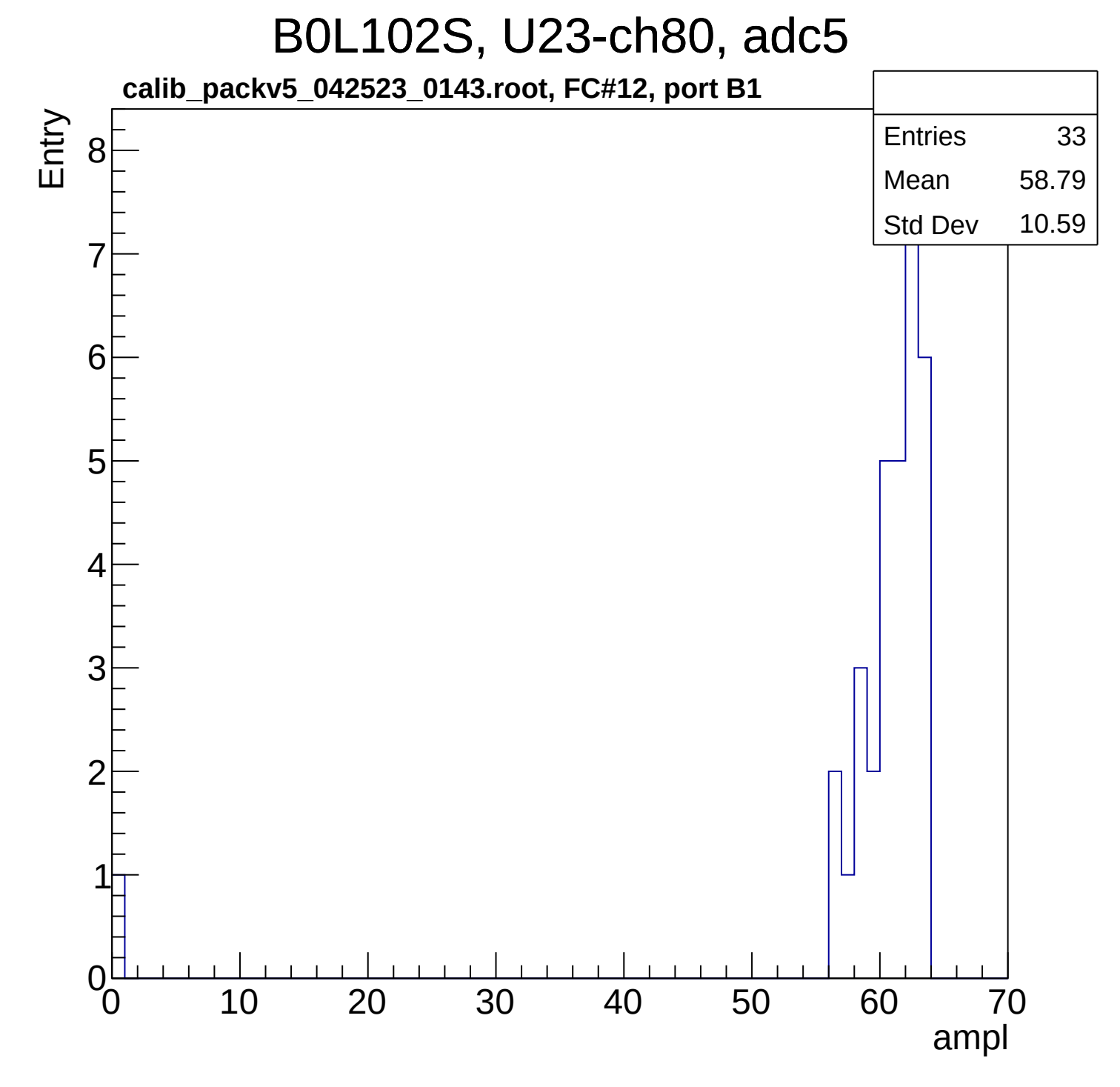
B0L102S, U23-ch80, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 33
Mean 58.79
Std Dev 10.59
Entry
ampl
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1
2
3
4
5
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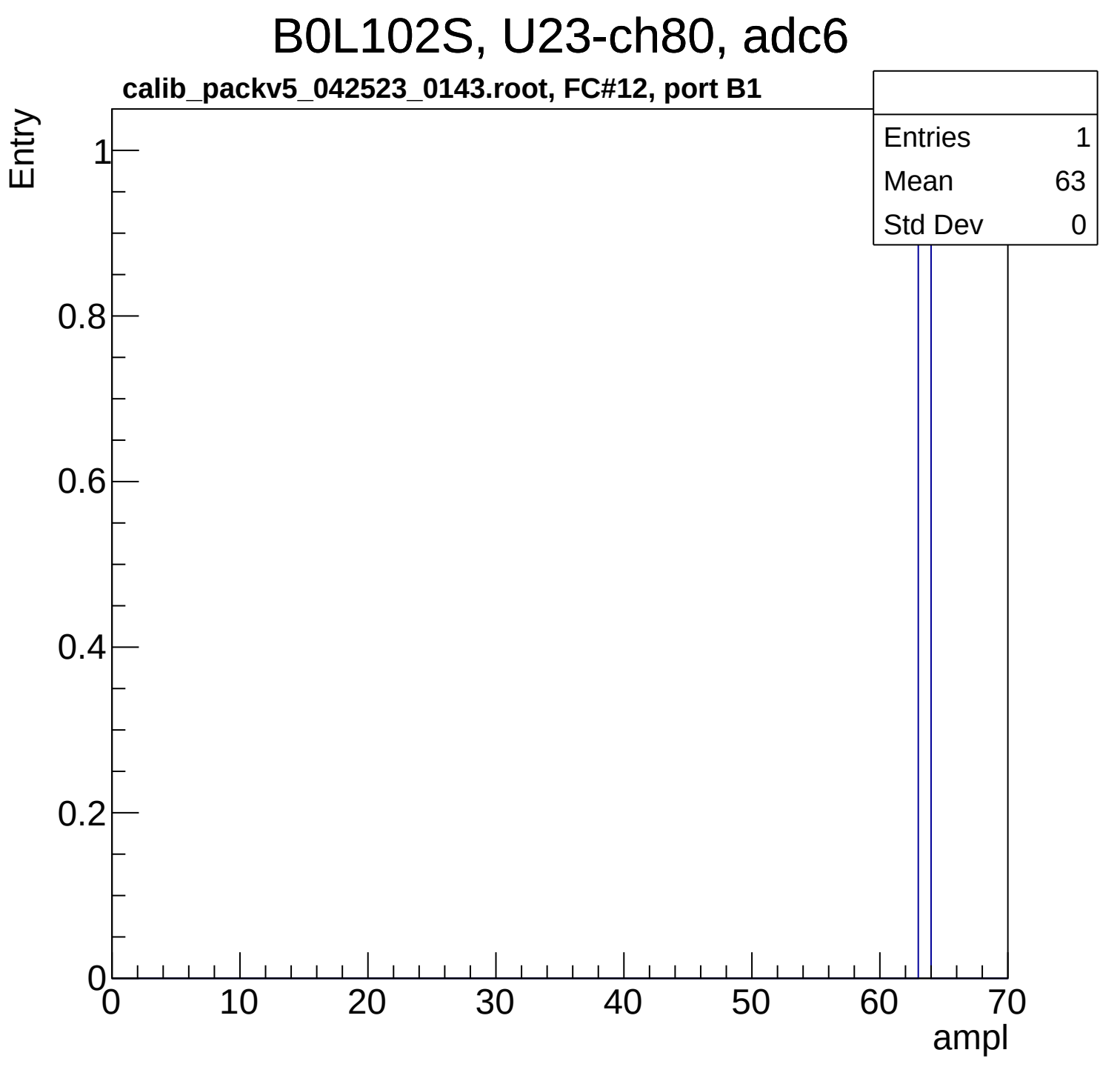
B0L102S, U23-ch80, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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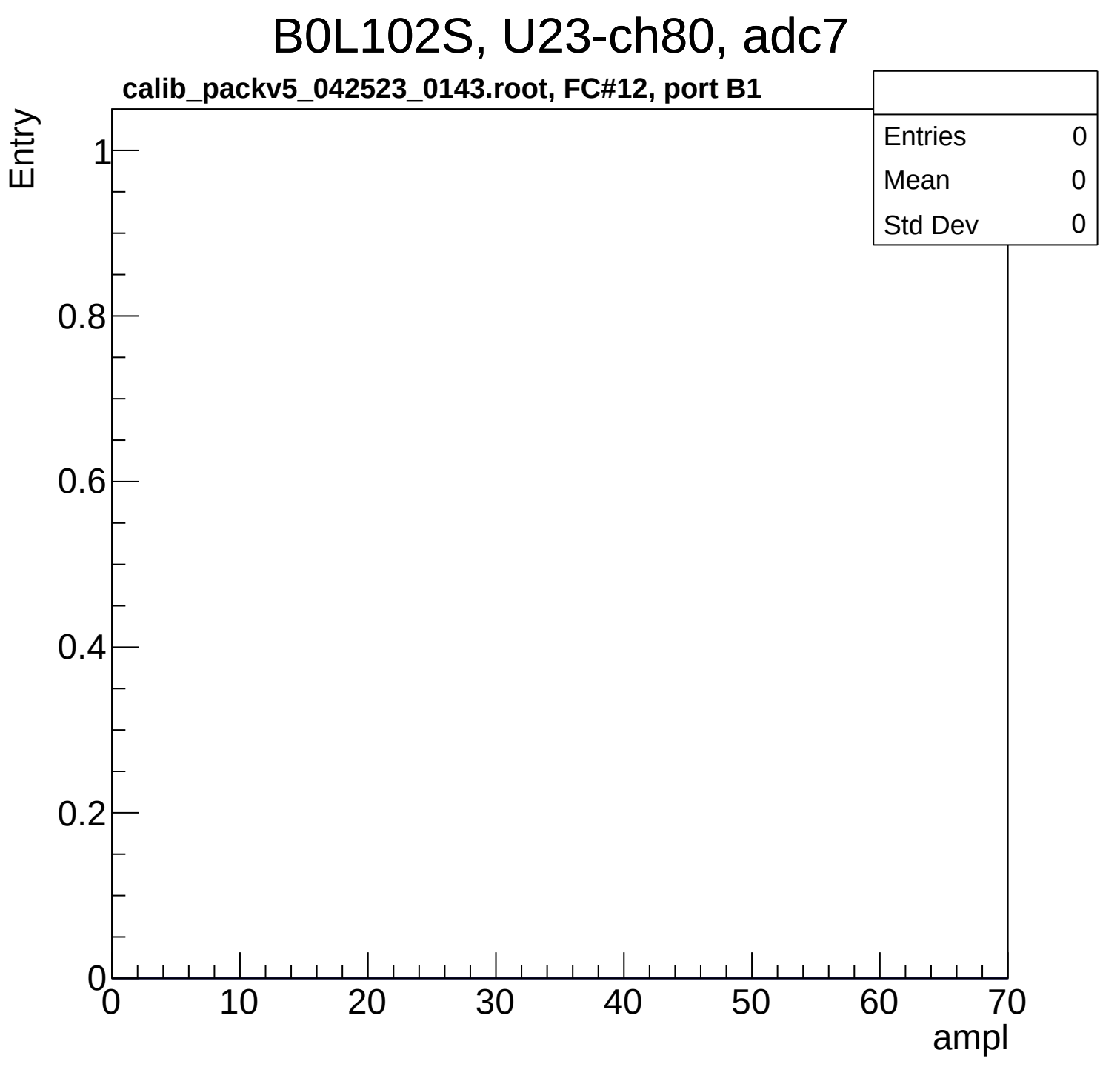
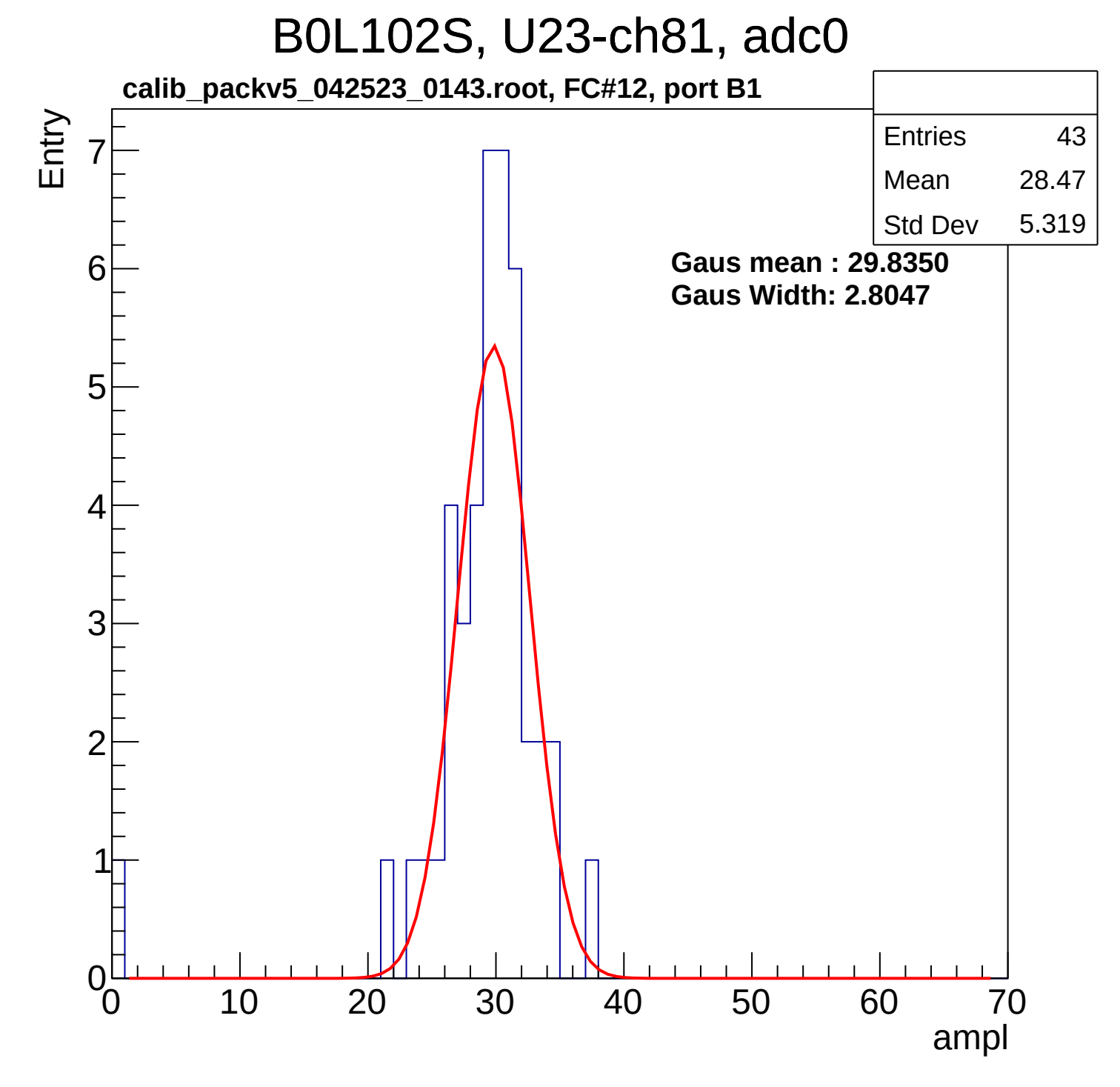
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch80, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch81, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 28.47
Std Dev 5.319
Gaus mean : 29.8350
Gaus Width: 2.8047
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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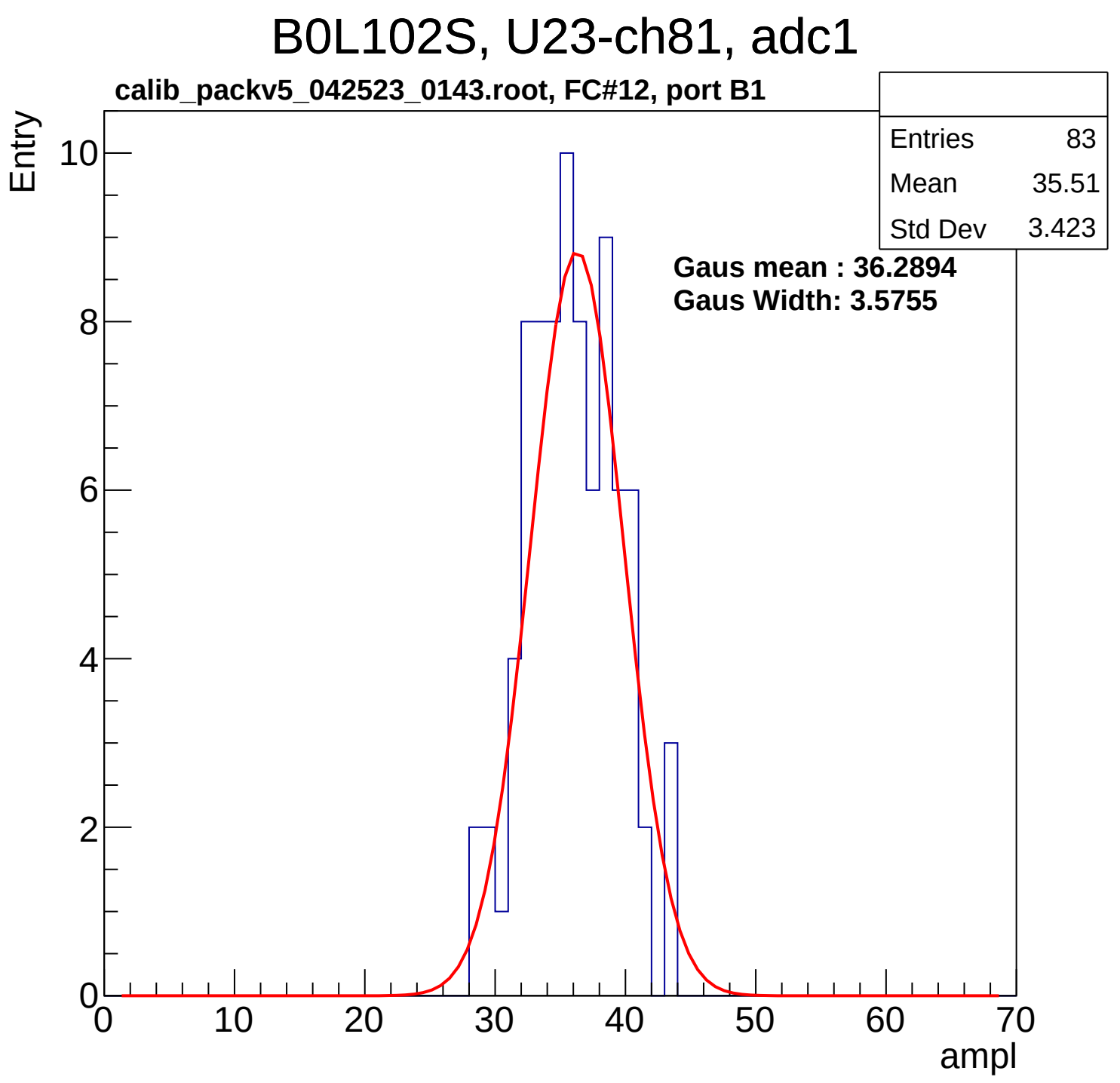
B0L102S, U23-ch81, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 35.51
Std Dev 3.423
Gaus mean : 36.2894
Gaus Width: 3.5755
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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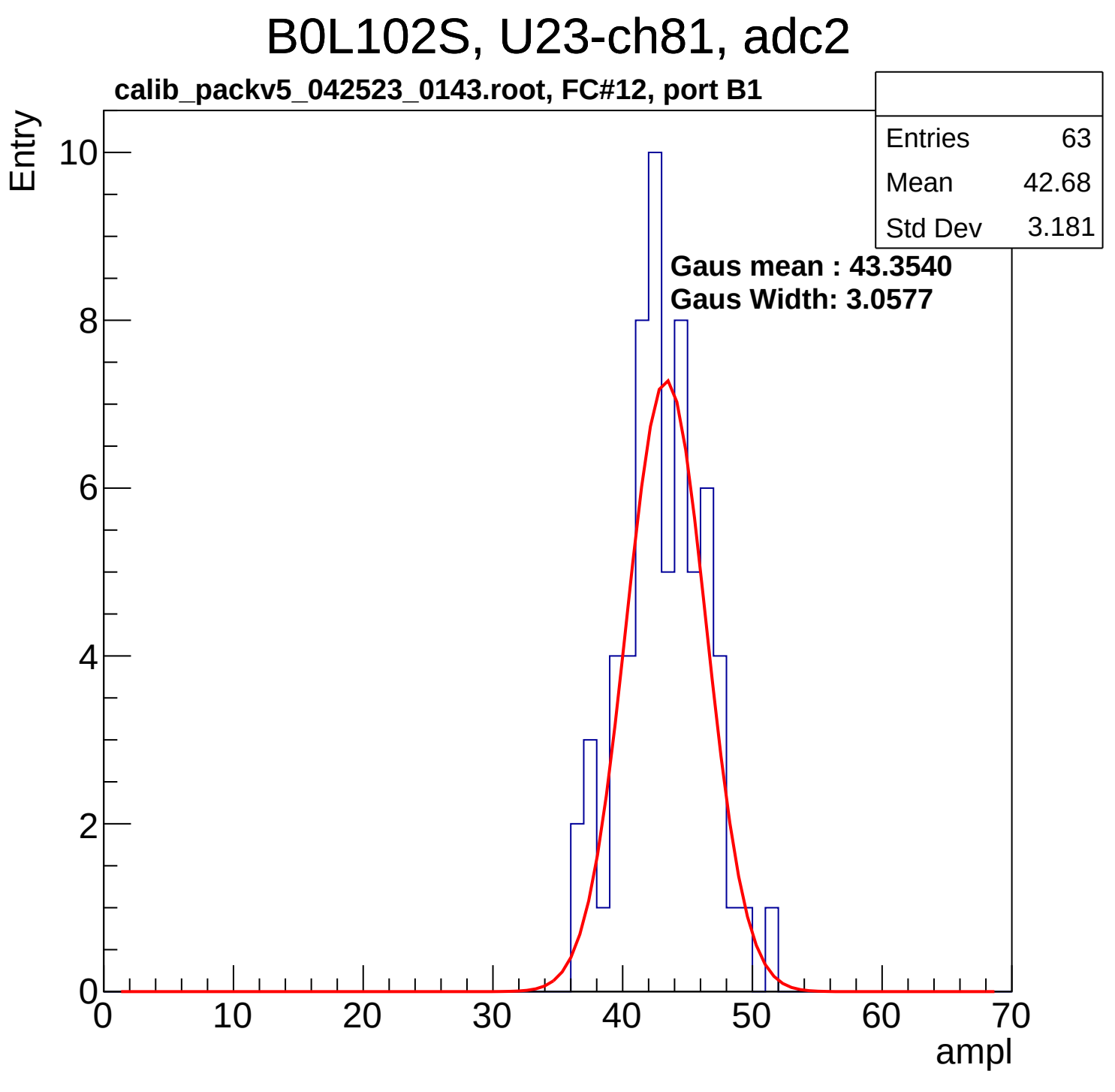
B0L102S, U23-ch81, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 42.68
Std Dev 3.181
Gaus mean : 43.3540
Gaus Width: 3.0577
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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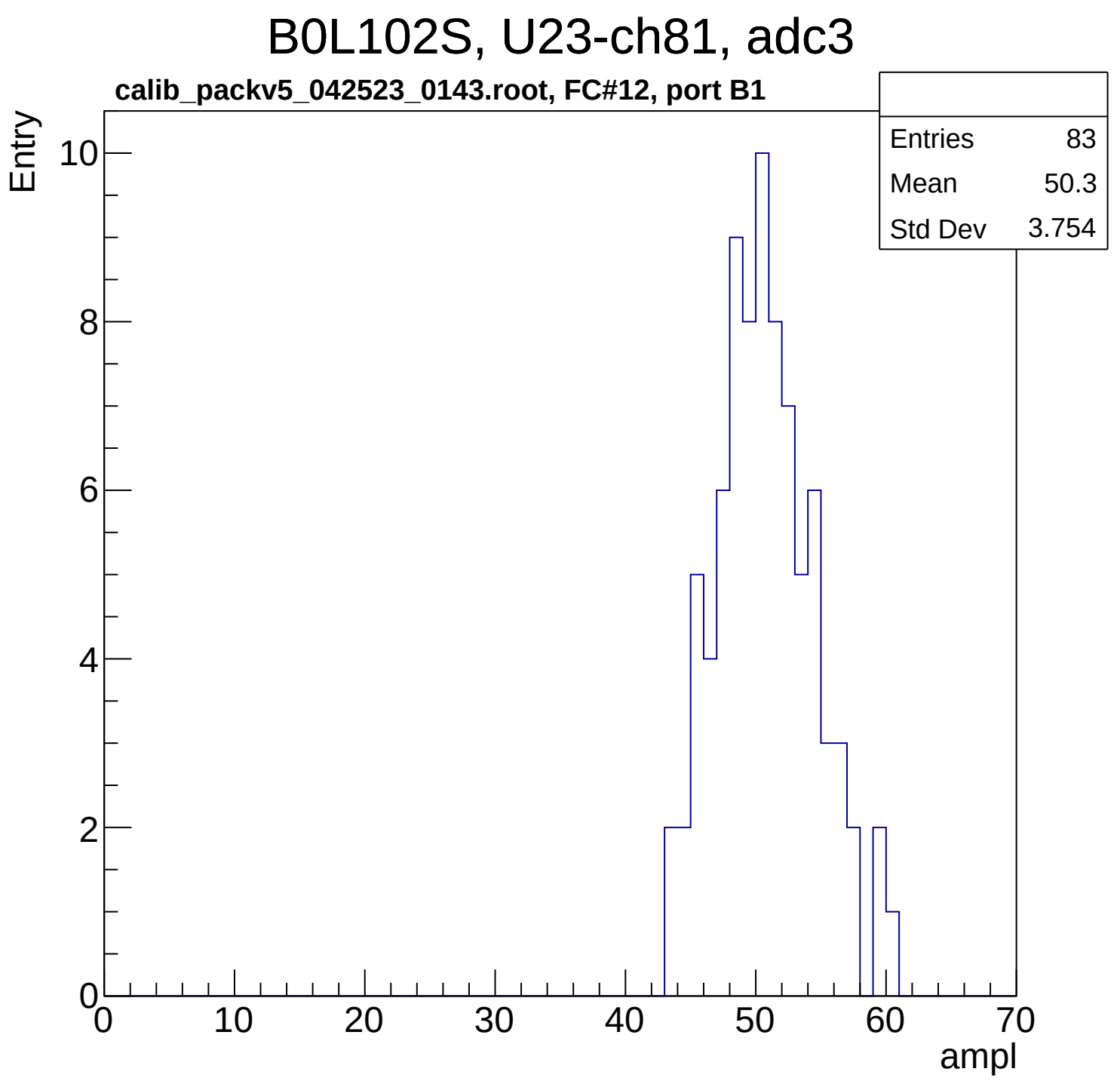
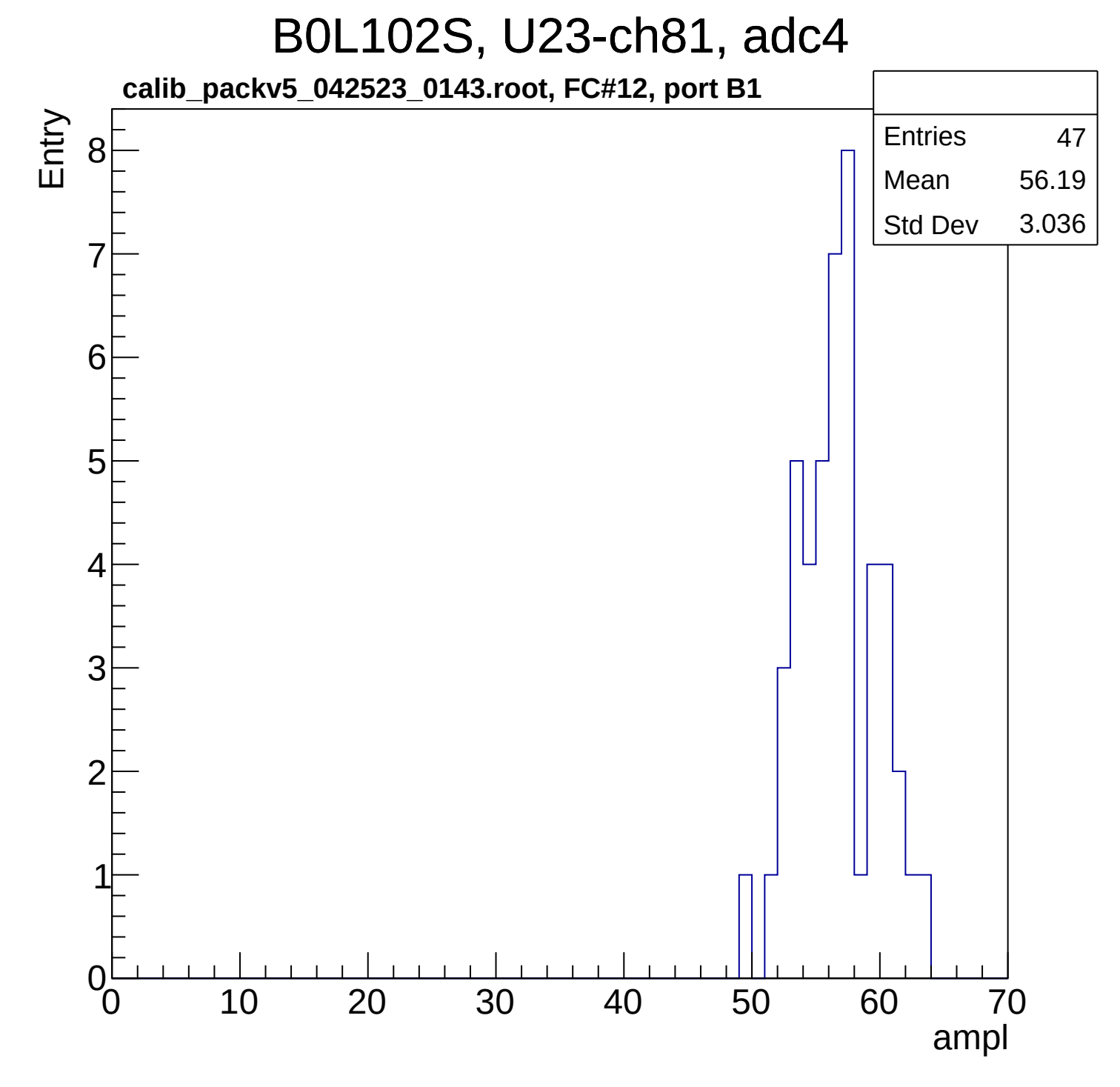
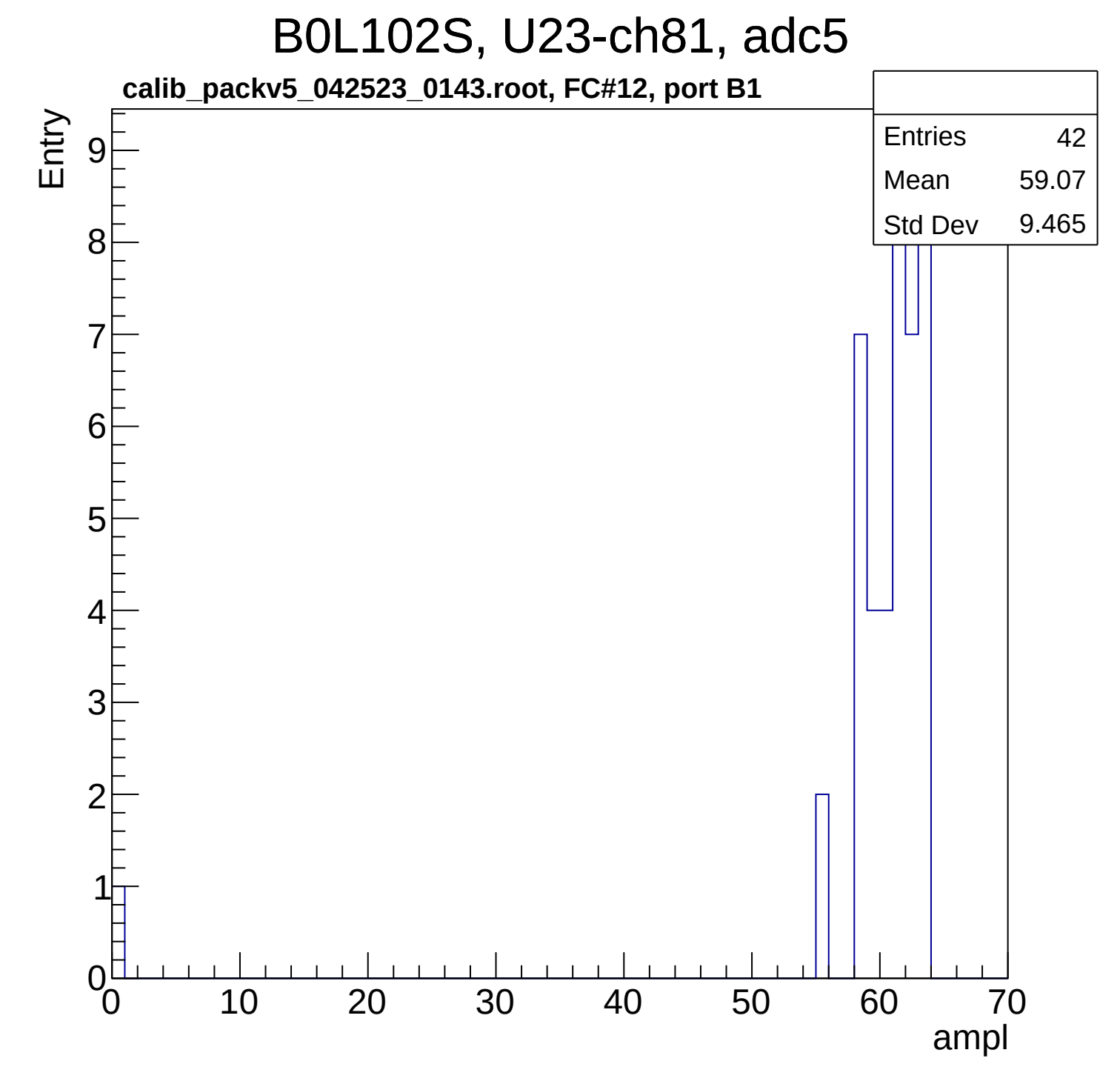

B0L102S, U23-ch81, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 50.3
Std Dev 3.754
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch81, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 56.19
Std Dev 3.036
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch81, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 59.07
Std Dev 9.465
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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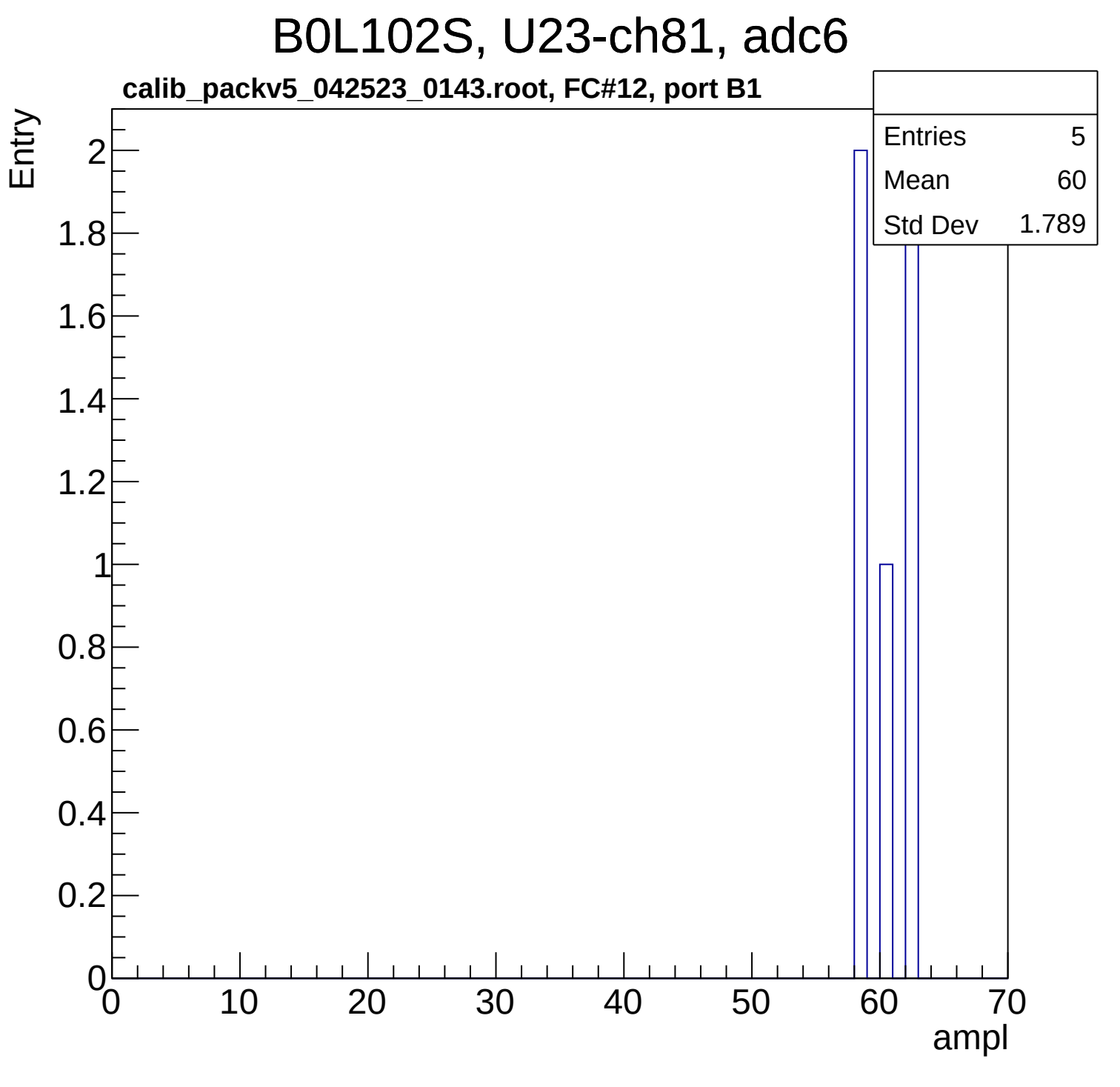
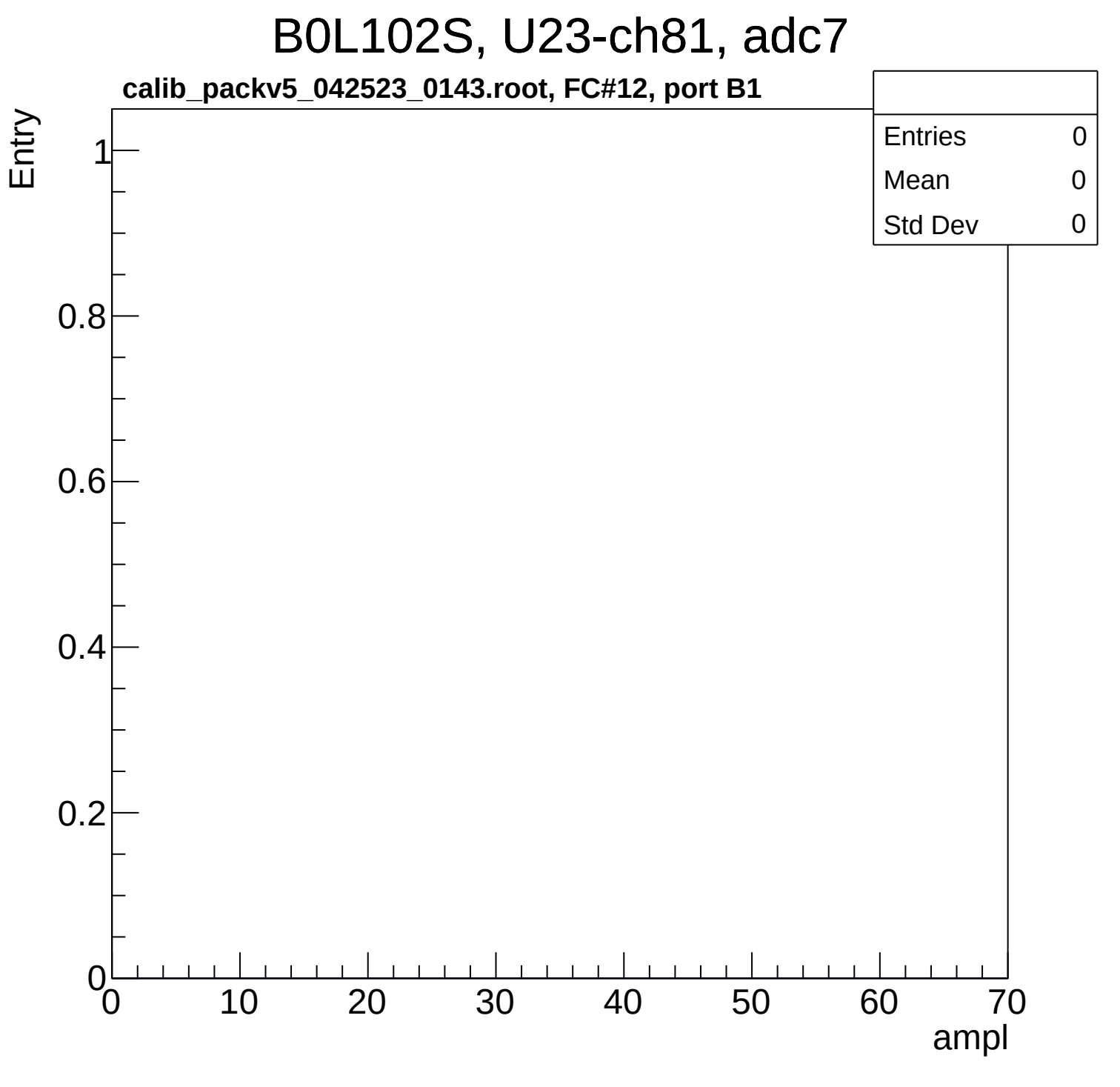
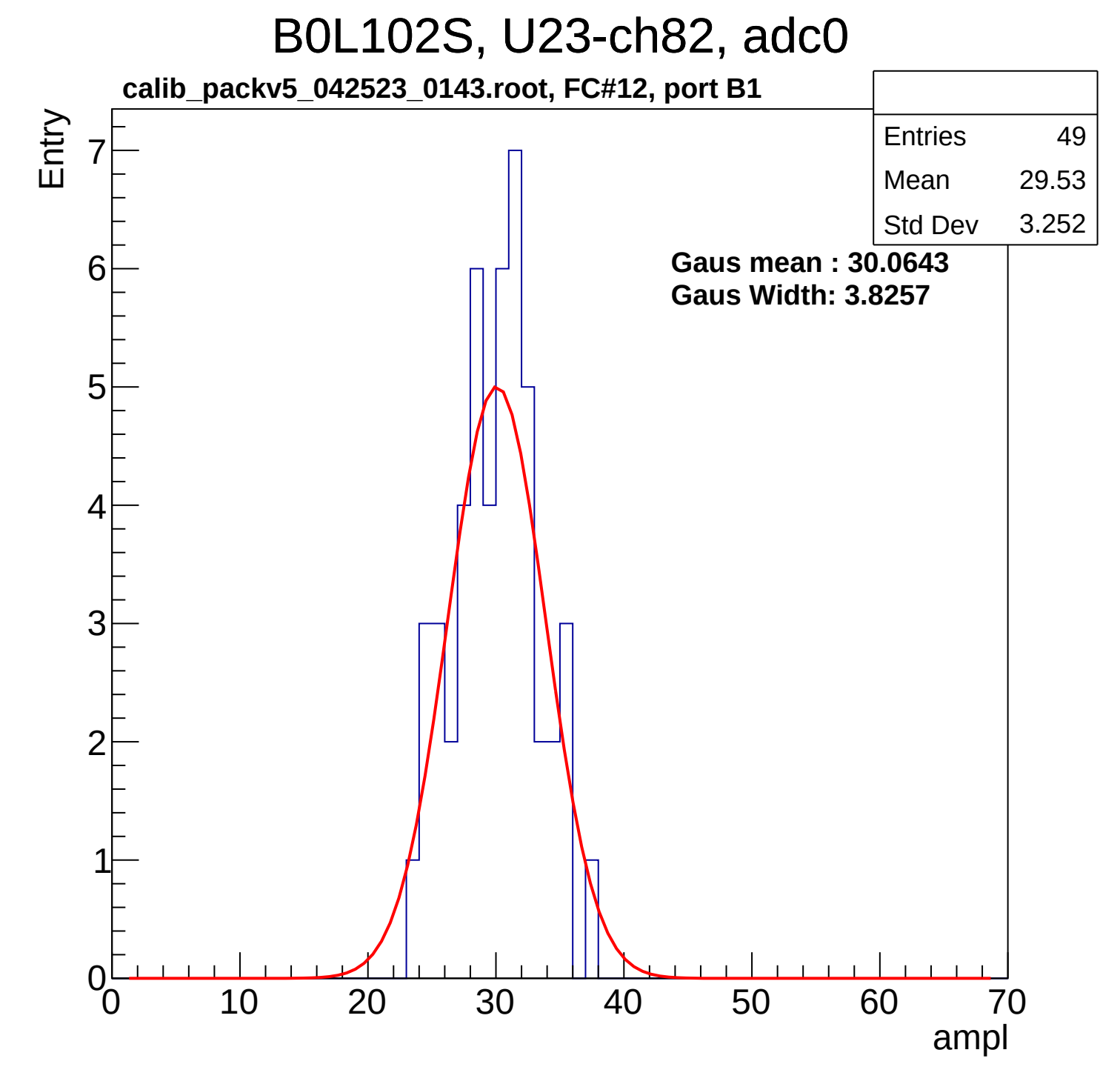
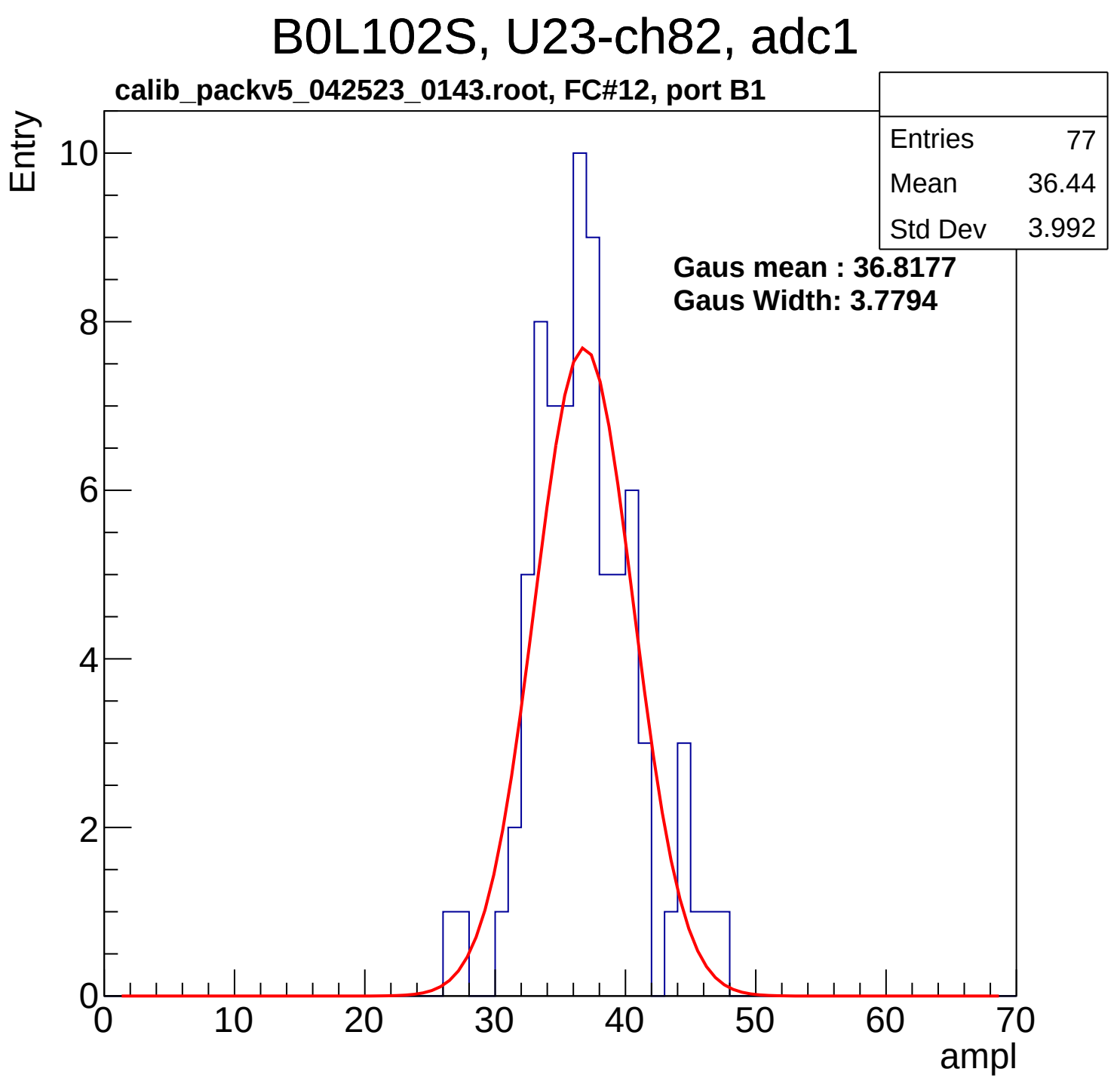
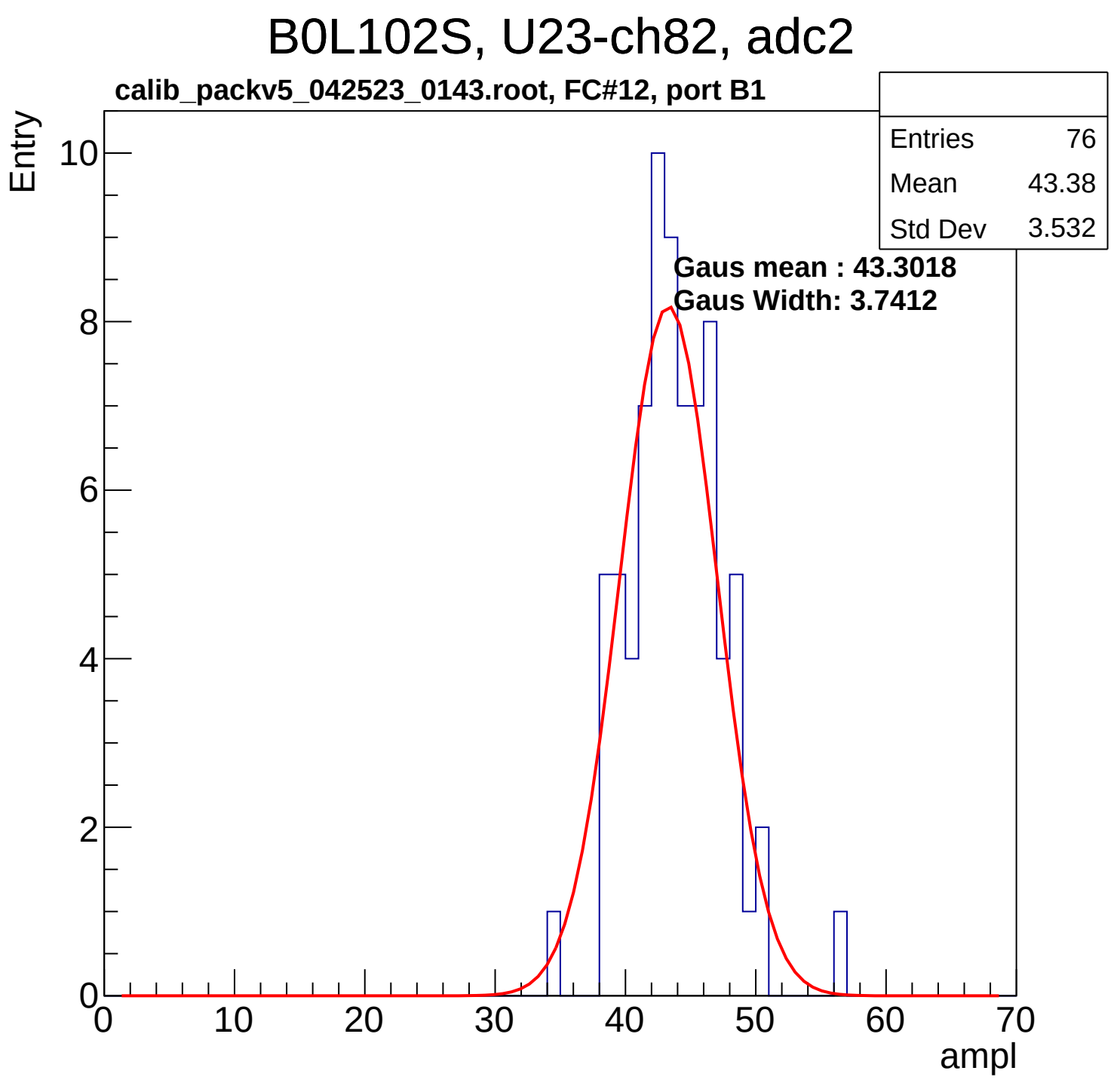
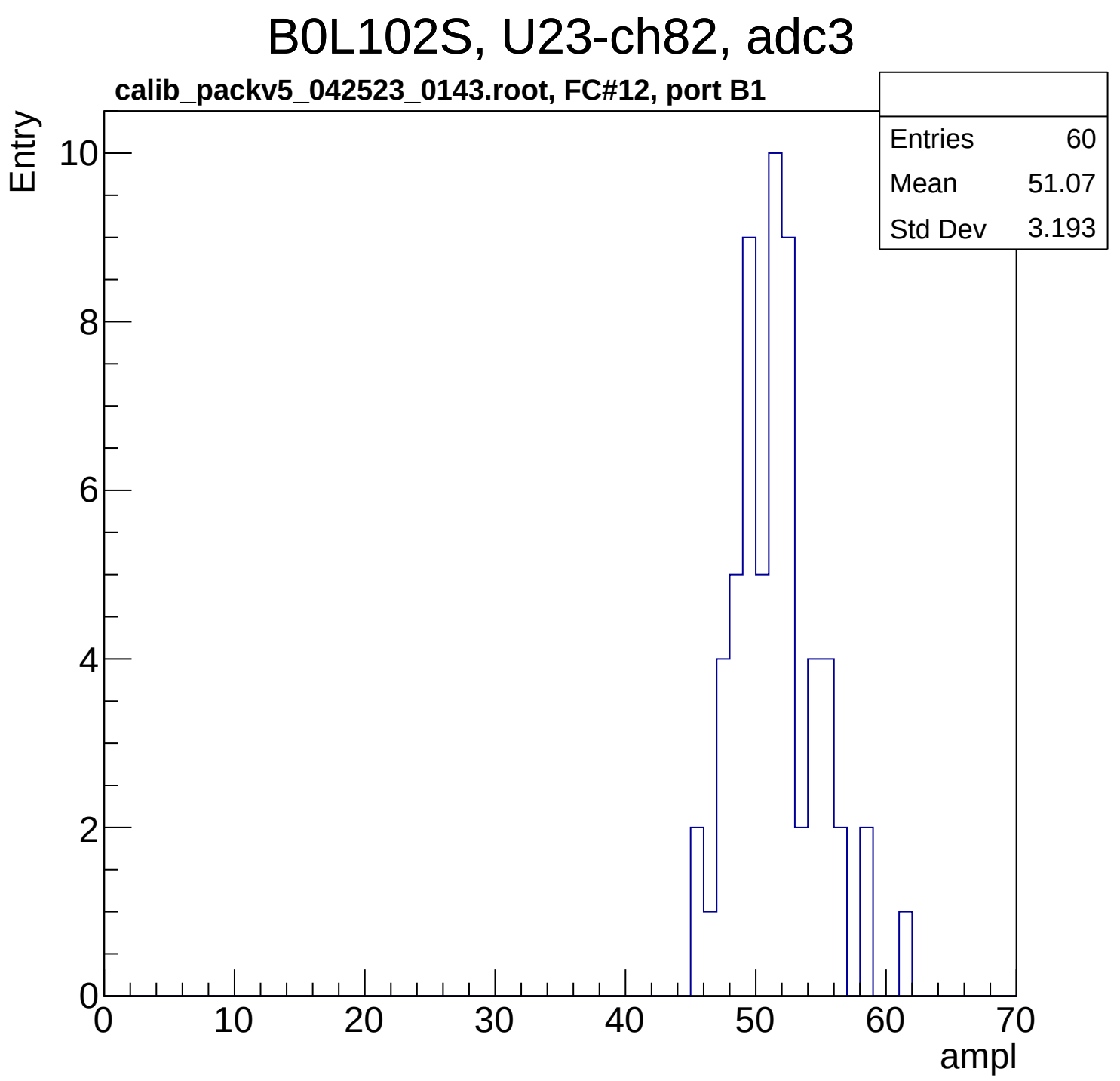

B0L102S, U23-ch81, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 60
Std Dev 1.789
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

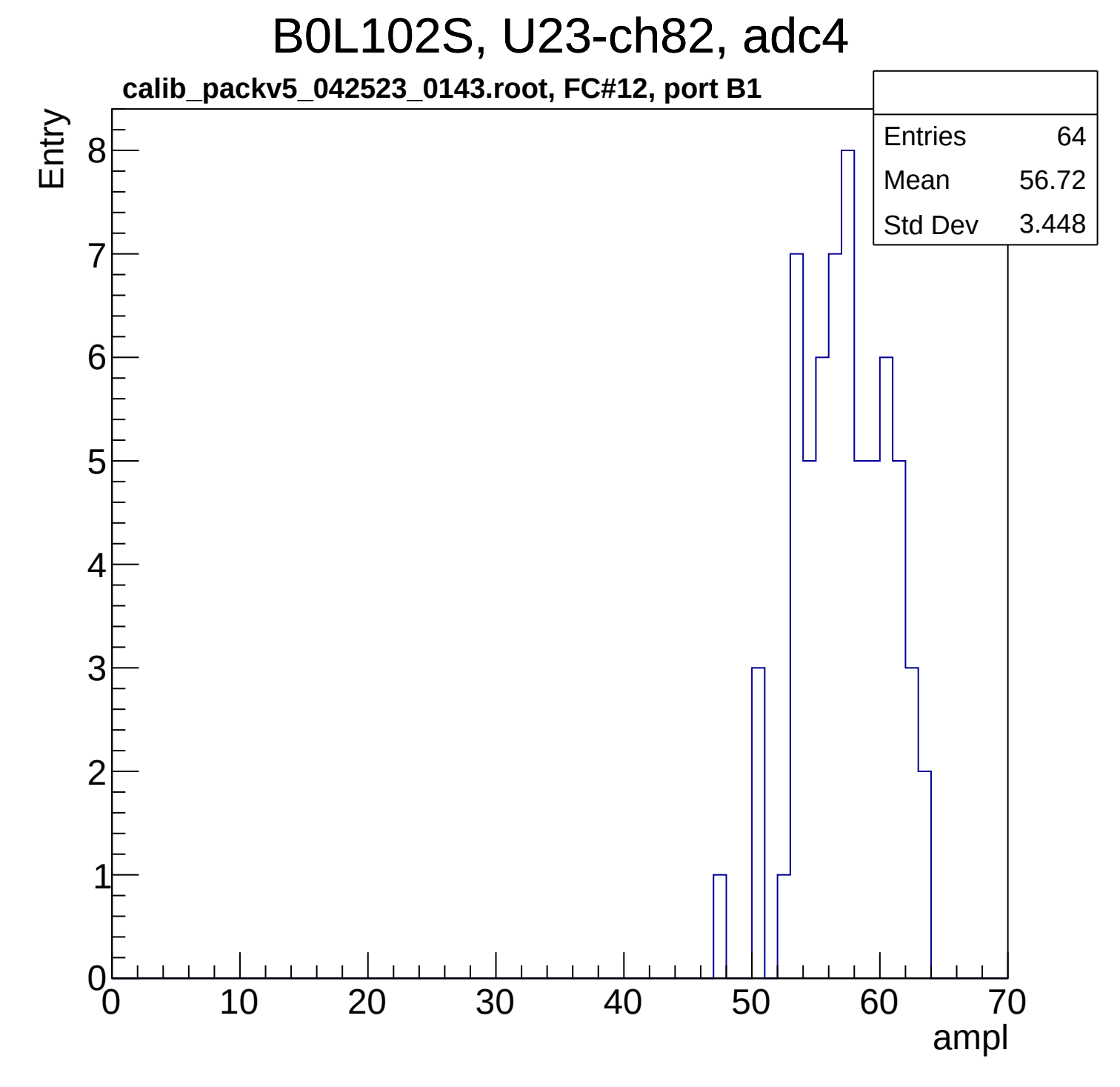

B0L102S, U23-ch81, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch82, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 29.53
Std Dev 3.252
Gaus mean : 30.0643
Gaus Width: 3.8257
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch82, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 36.44
Std Dev 3.992
Gaus mean : 36.8177
Gaus Width: 3.7794
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch82, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 43.38
Std Dev 3.532
Gaus mean : 43.3018
Gaus Width: 3.7412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch82, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 51.07
Std Dev 3.193
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch82, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 56.72
Std Dev 3.448
Entry
ampl
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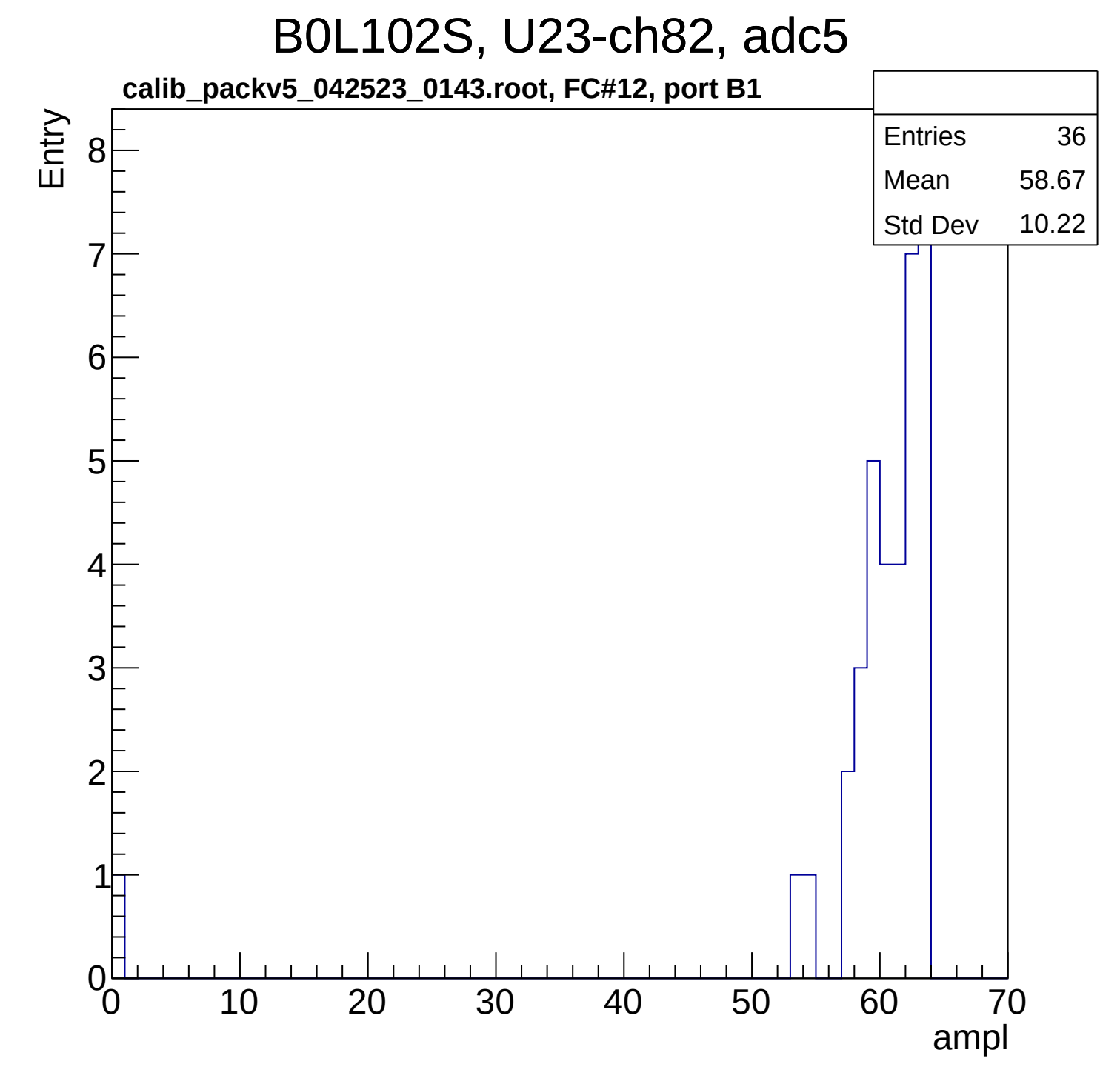
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch82, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.67
Std Dev 10.22
Entry
ampl
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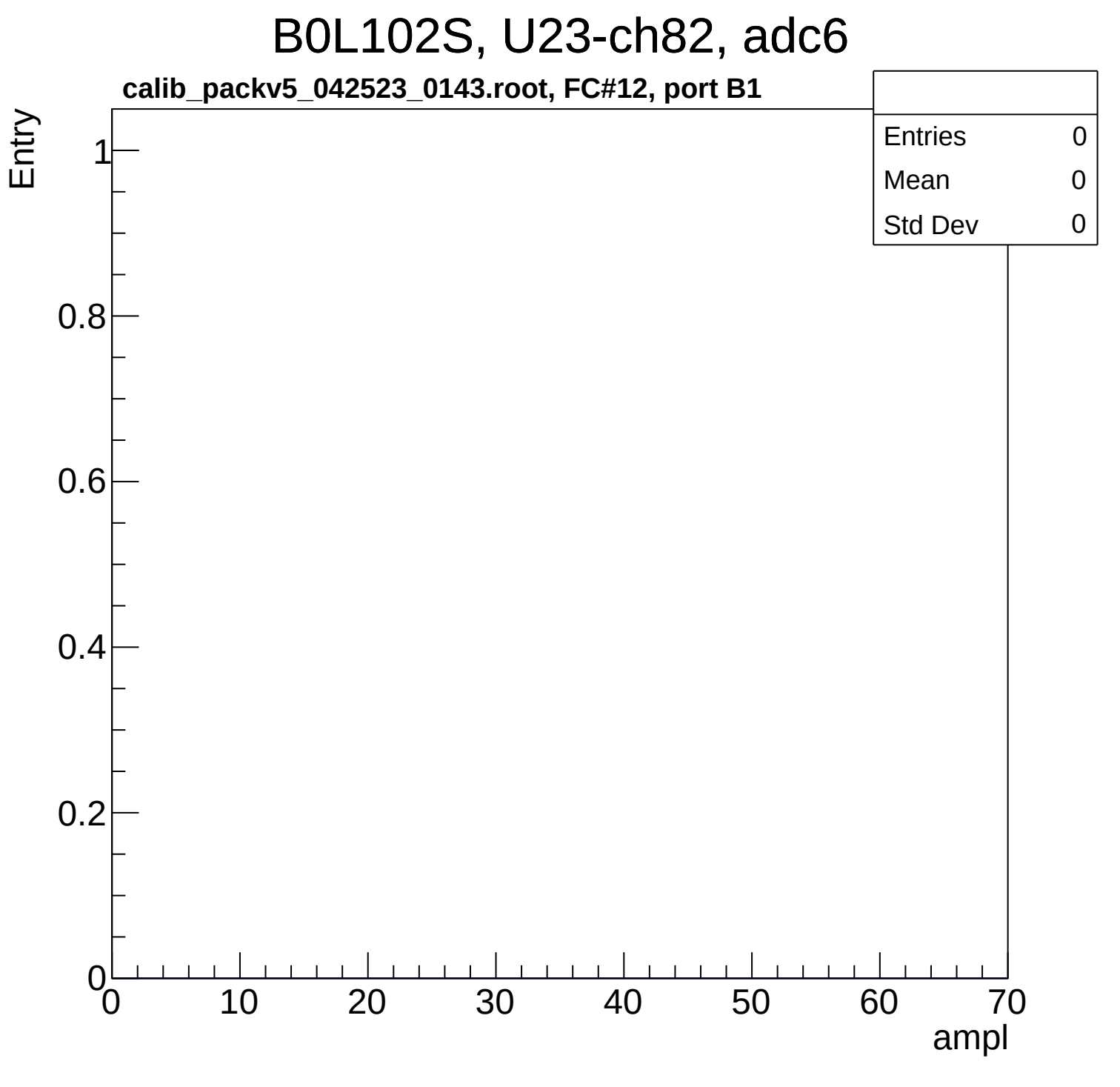
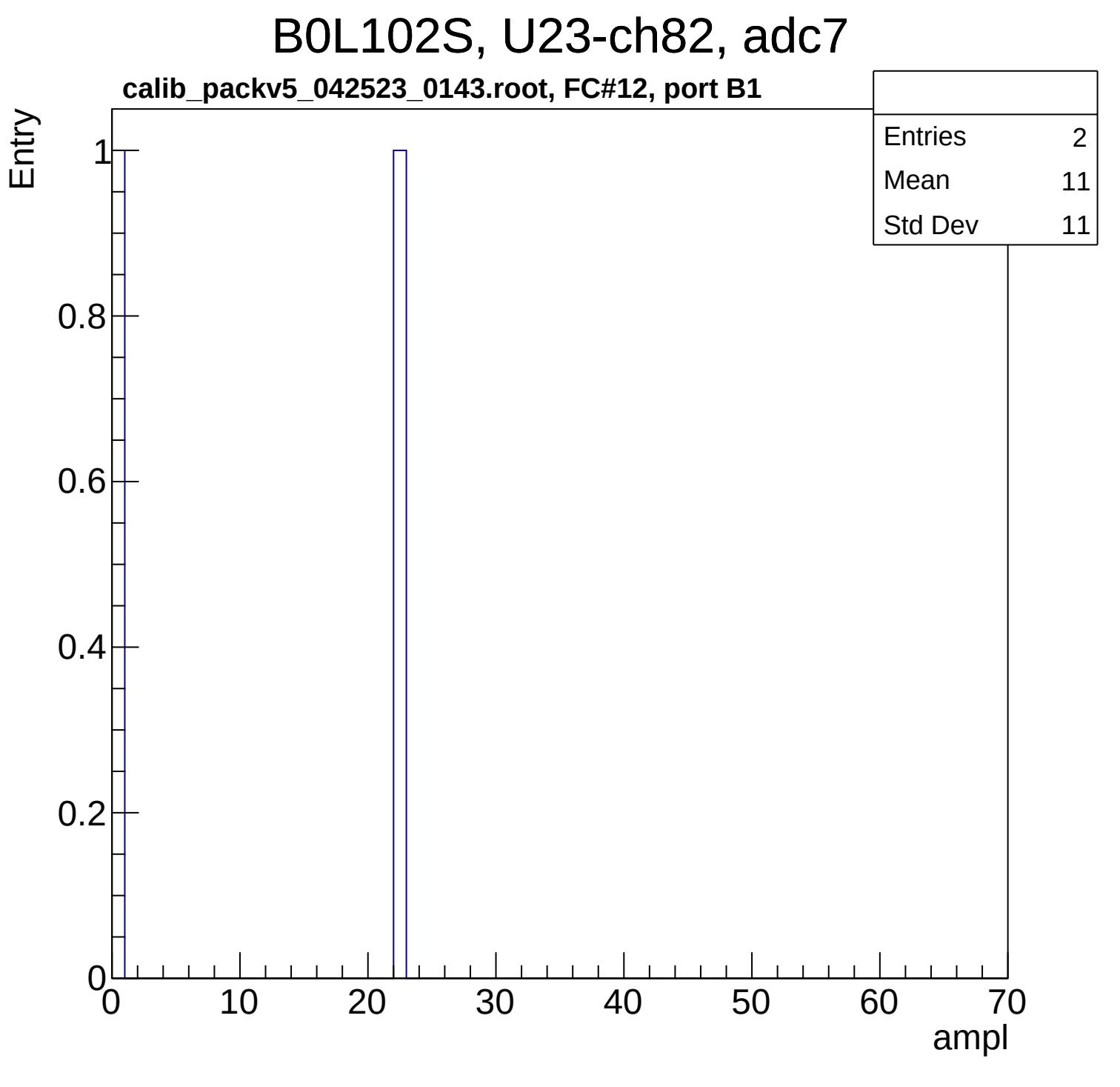
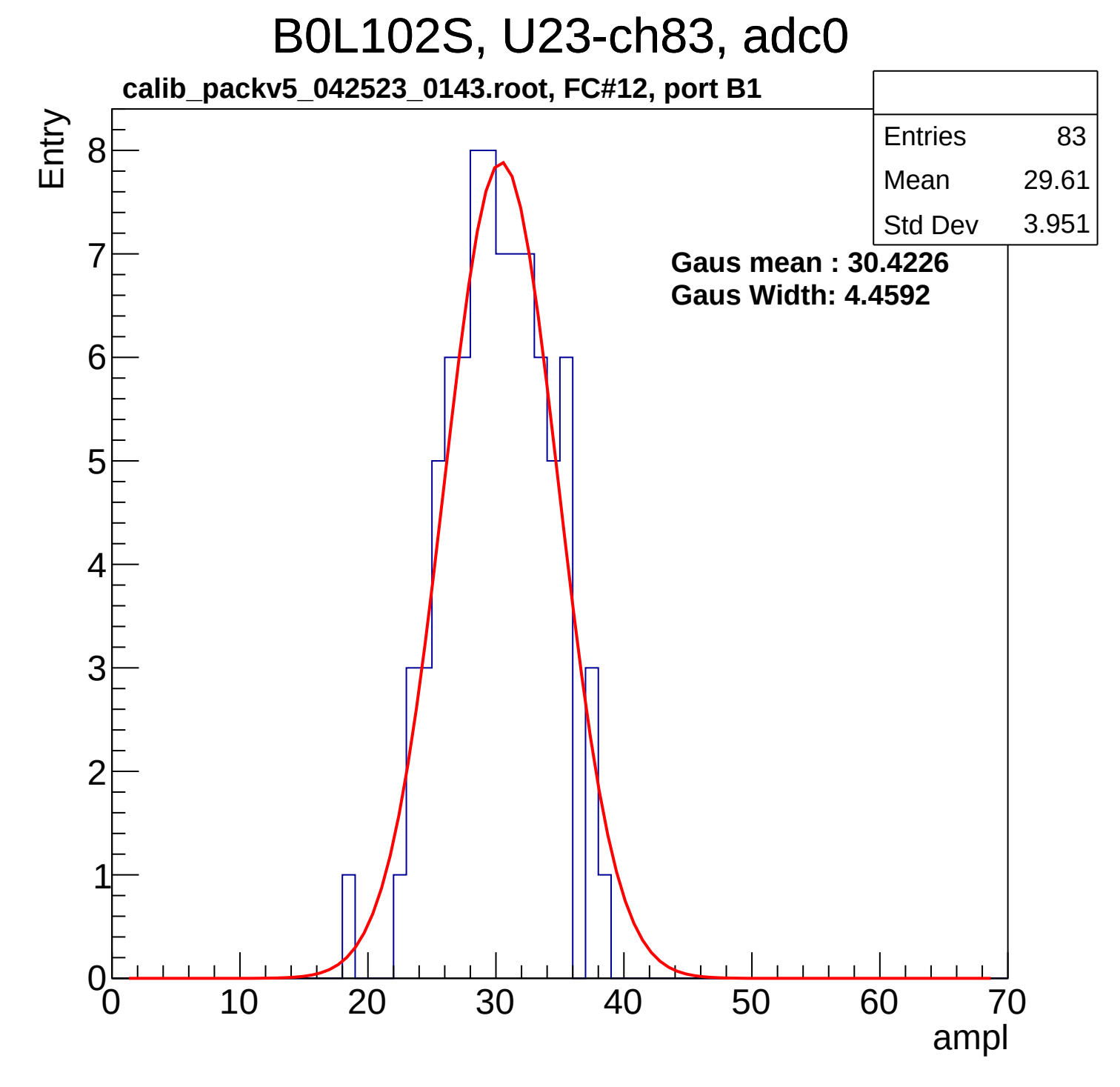
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch82, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch82, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch83, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 29.61
Std Dev 3.951
Gaus mean : 30.4226
Gaus Width: 4.4592
Entry
ampl
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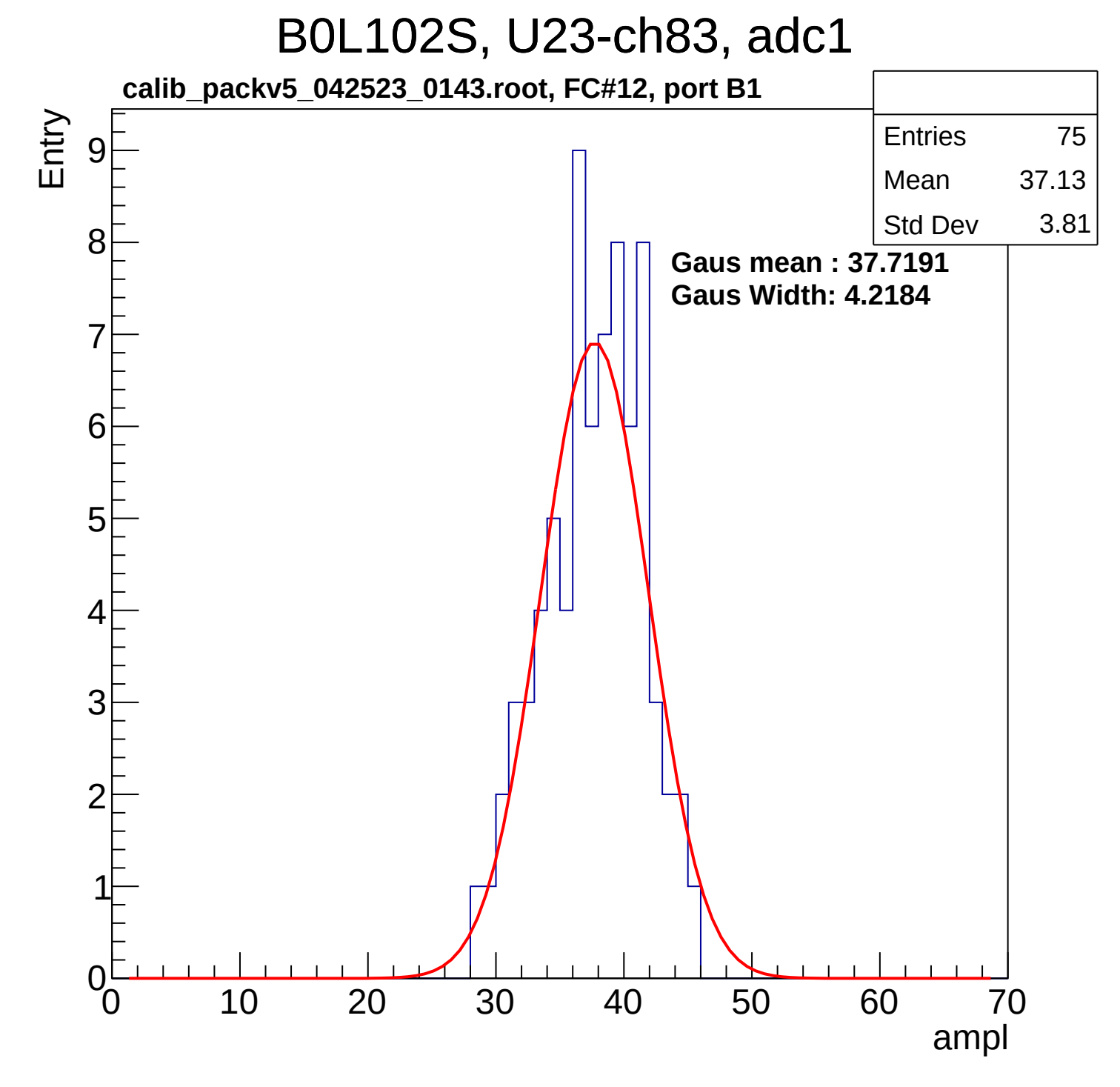
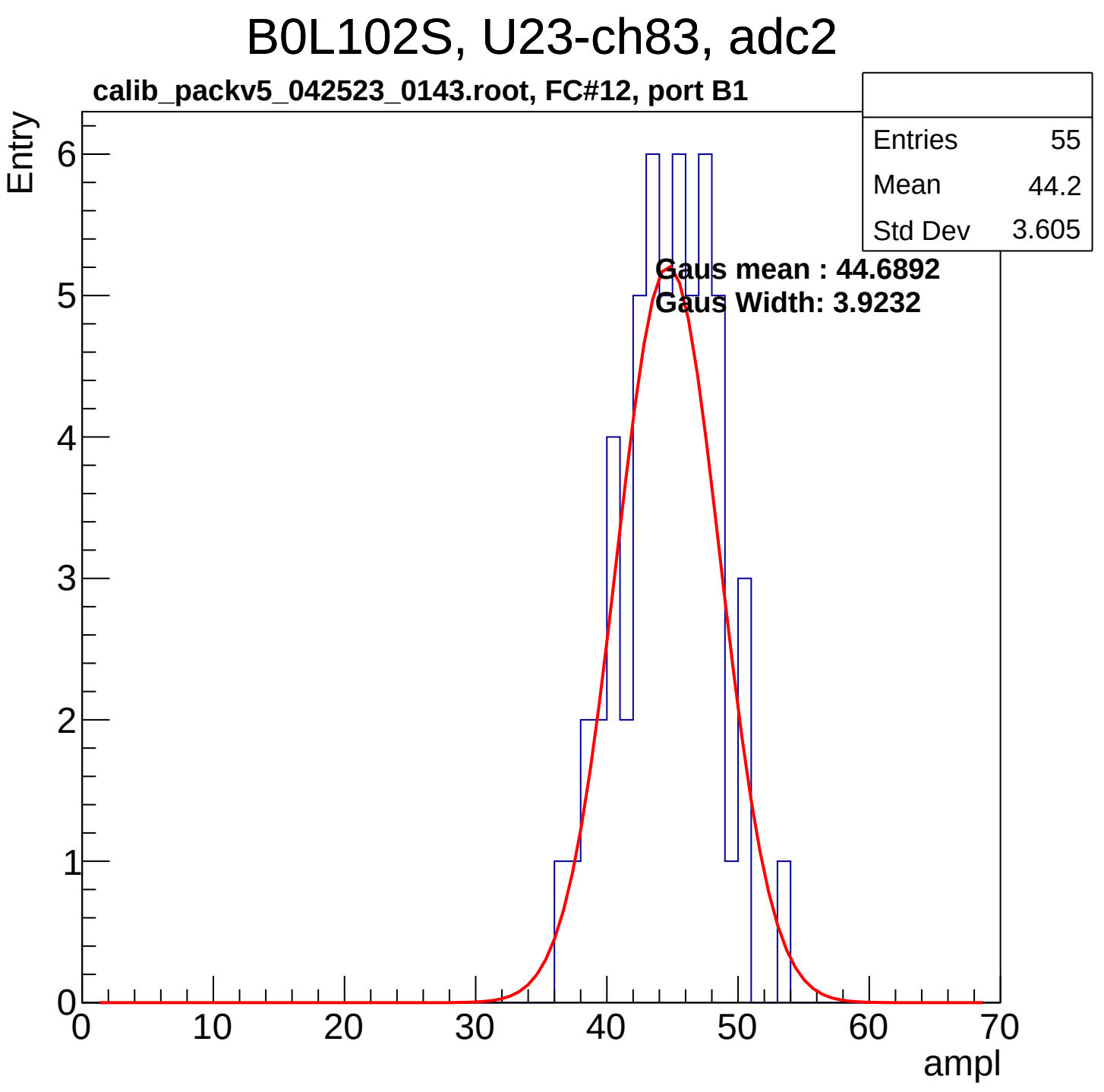
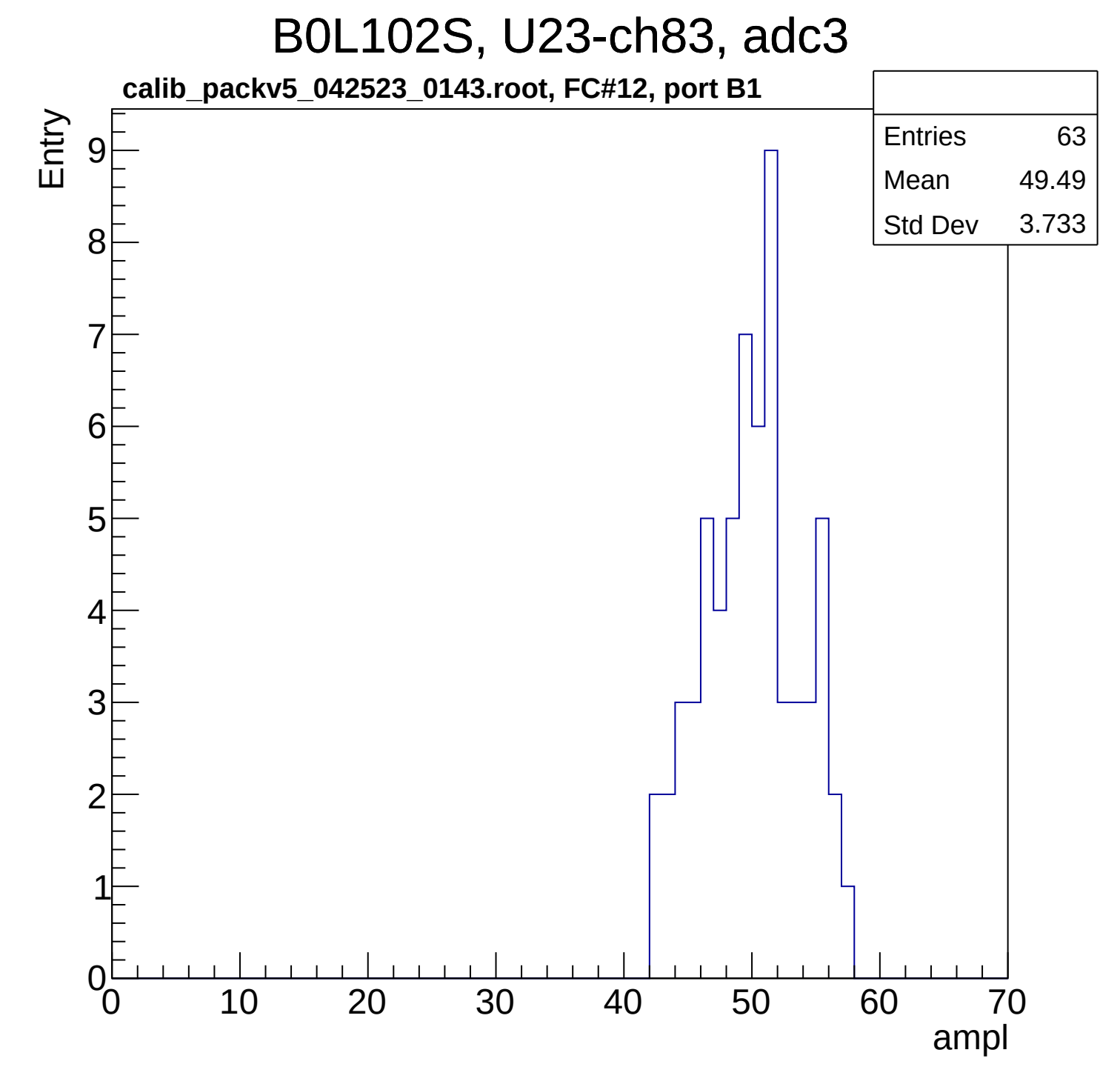
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch83, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.13
Std Dev 3.81
Gaus mean : 37.7191
Gaus Width: 4.2184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch83, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 44.2
Std Dev 3.605
Gaus mean : 44.6892
Gaus Width: 3.9232
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U23-ch83, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 49.49
Std Dev 3.733
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
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60
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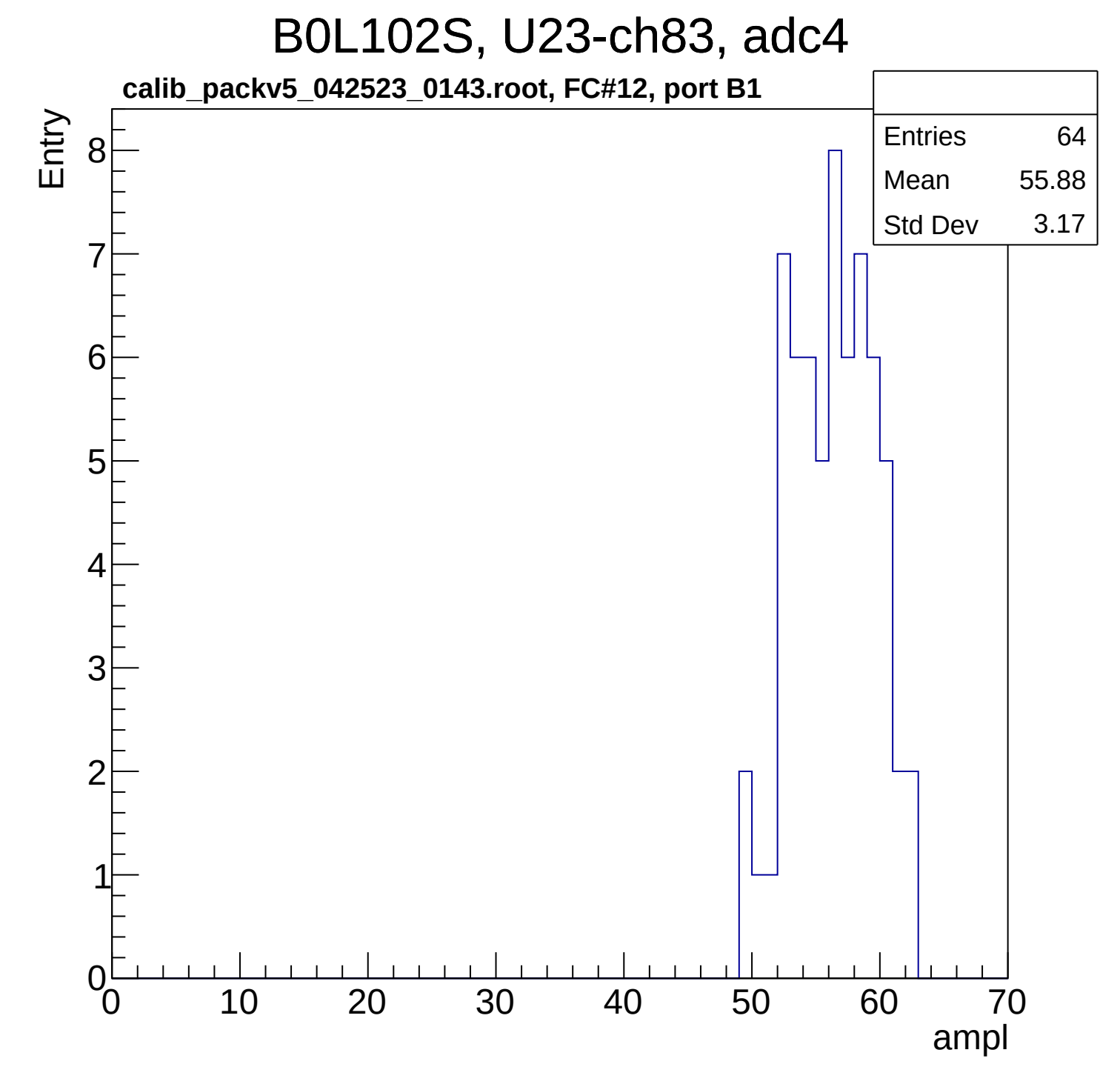
B0L102S, U23-ch83, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 55.88
Std Dev 3.17
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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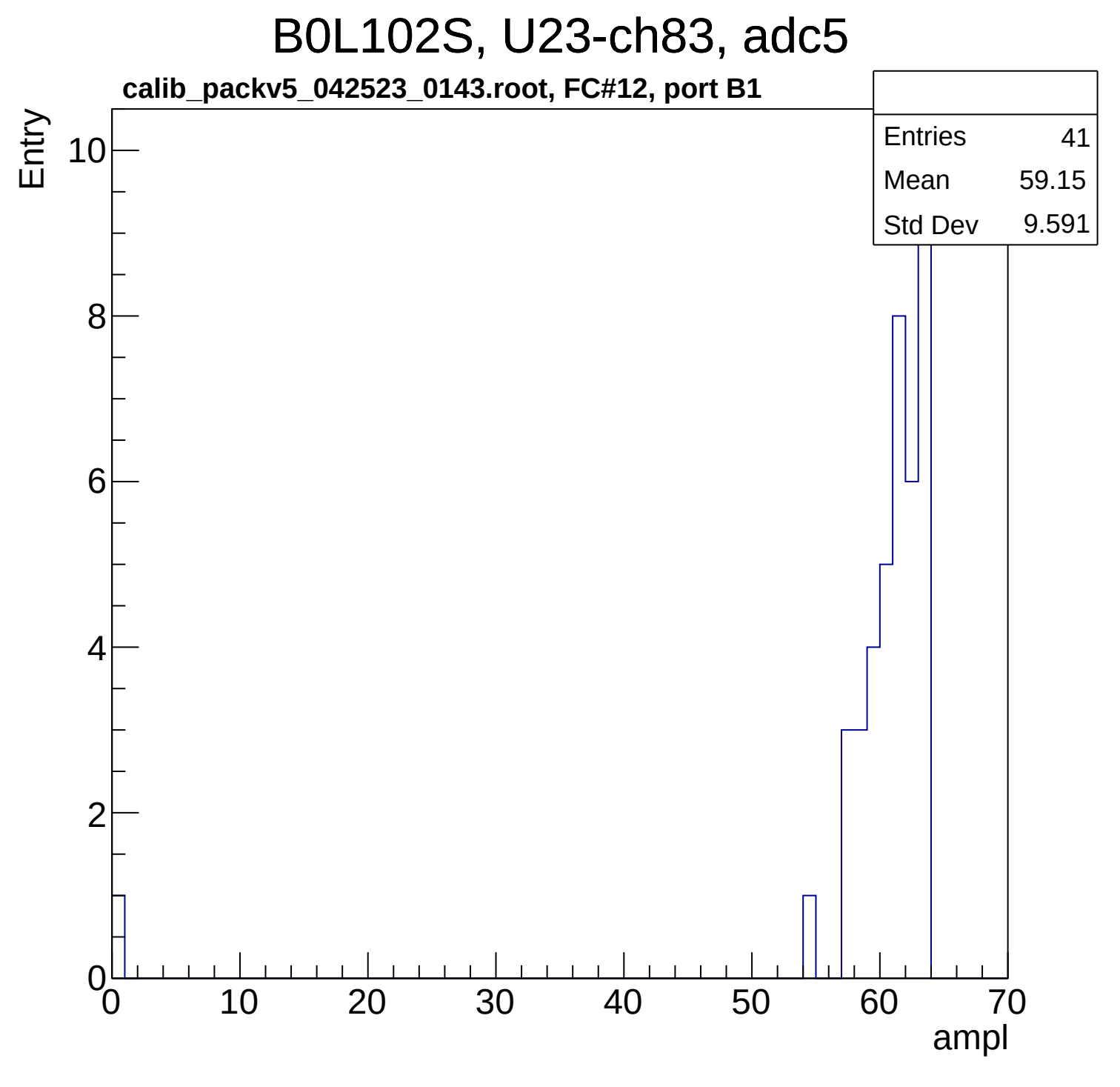
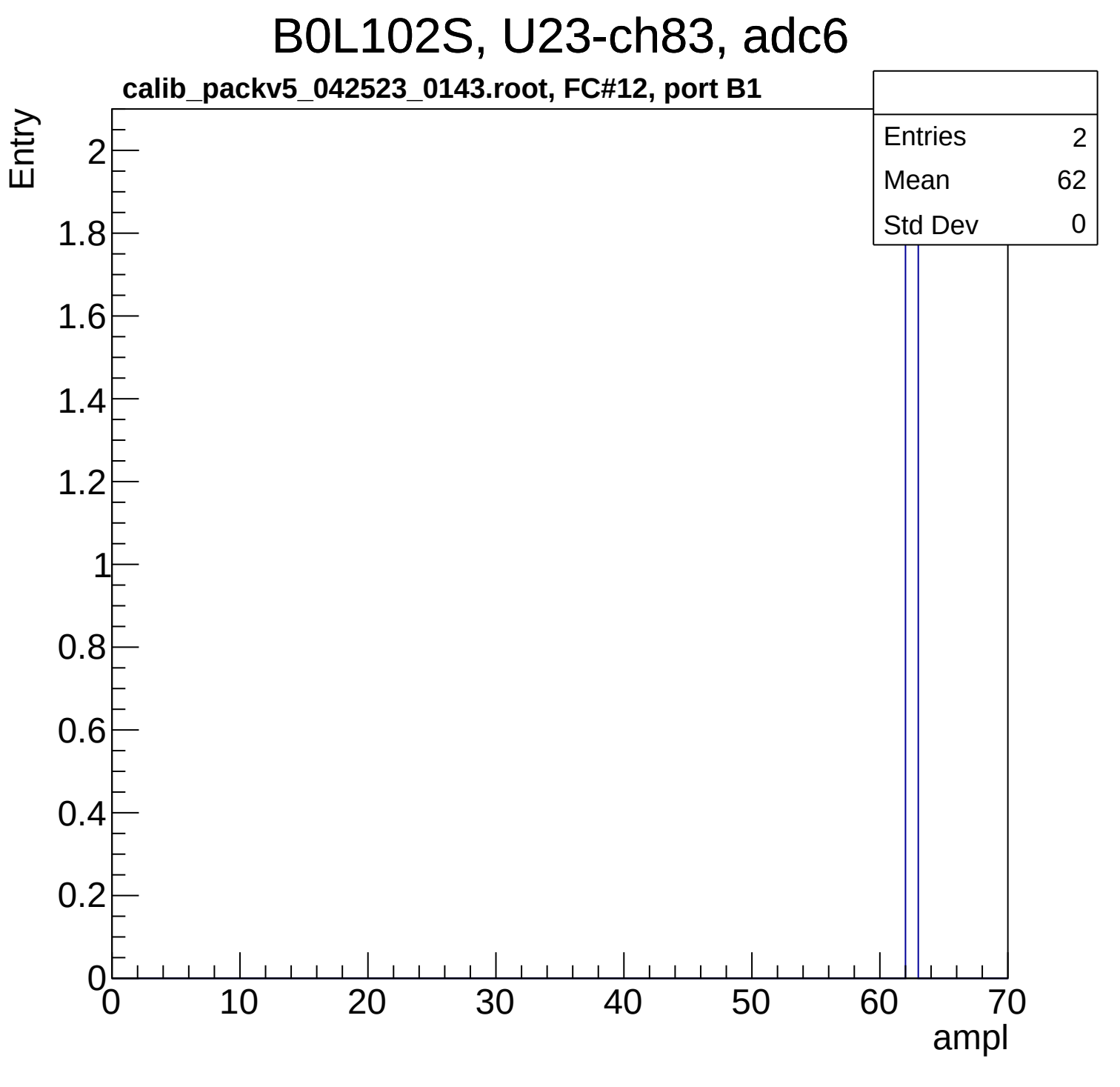
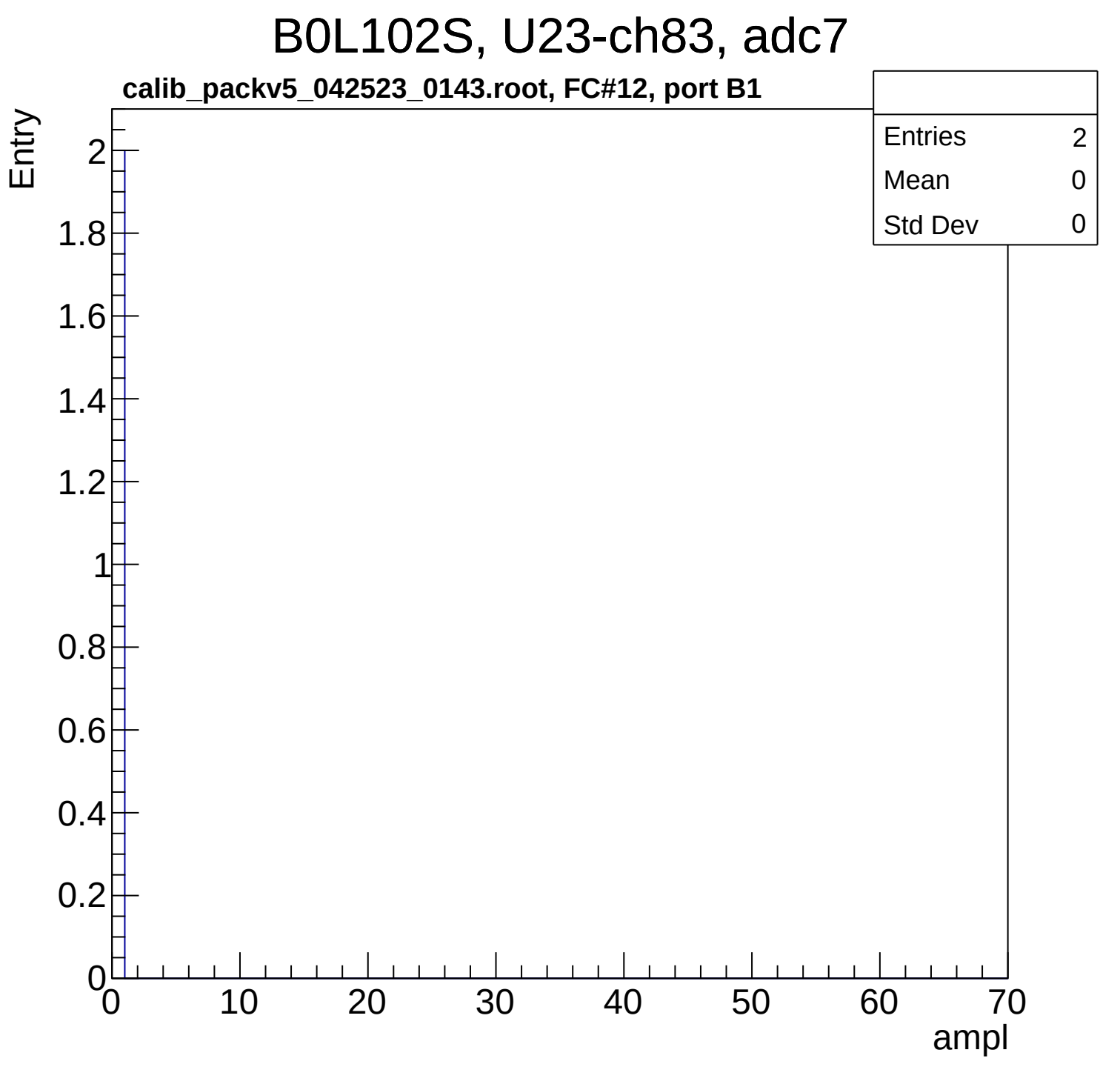
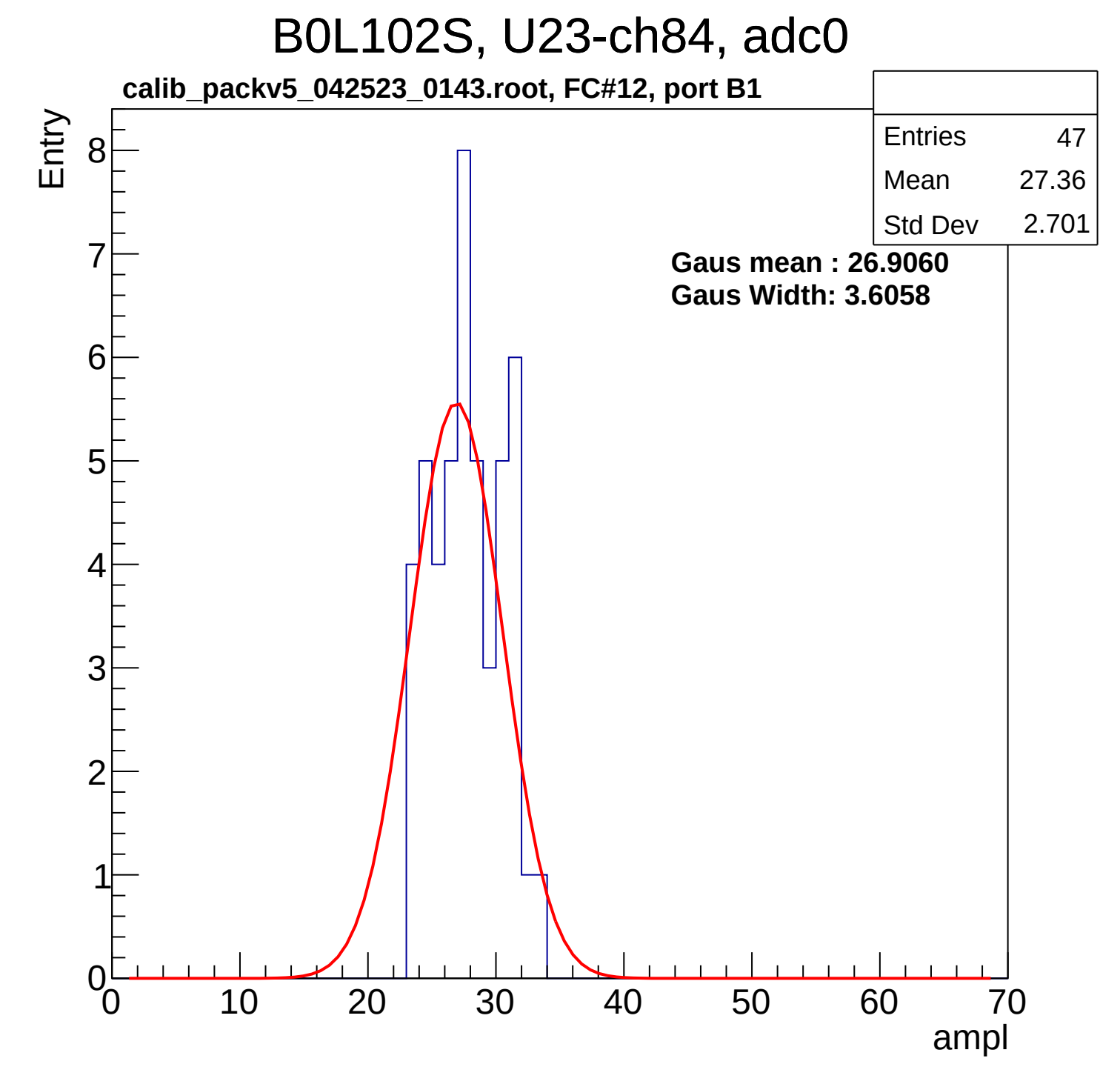
60
70


B0L102S, U23-ch83, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 59.15
Std Dev 9.591
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch83, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch83, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch84, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 27.36
Std Dev 2.701
Gaus mean : 26.9060
Gaus Width: 3.6058
Entry
ampl
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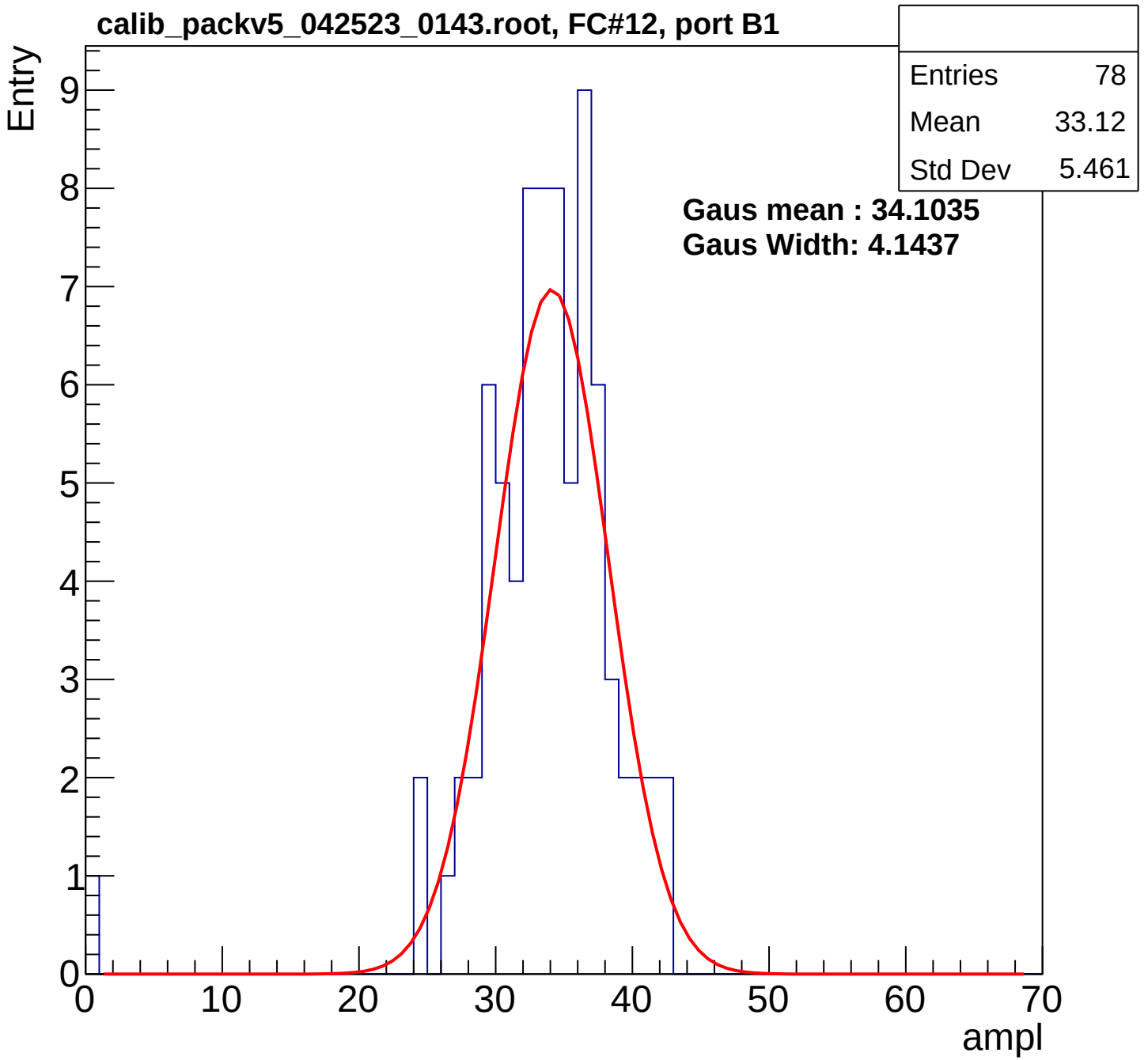
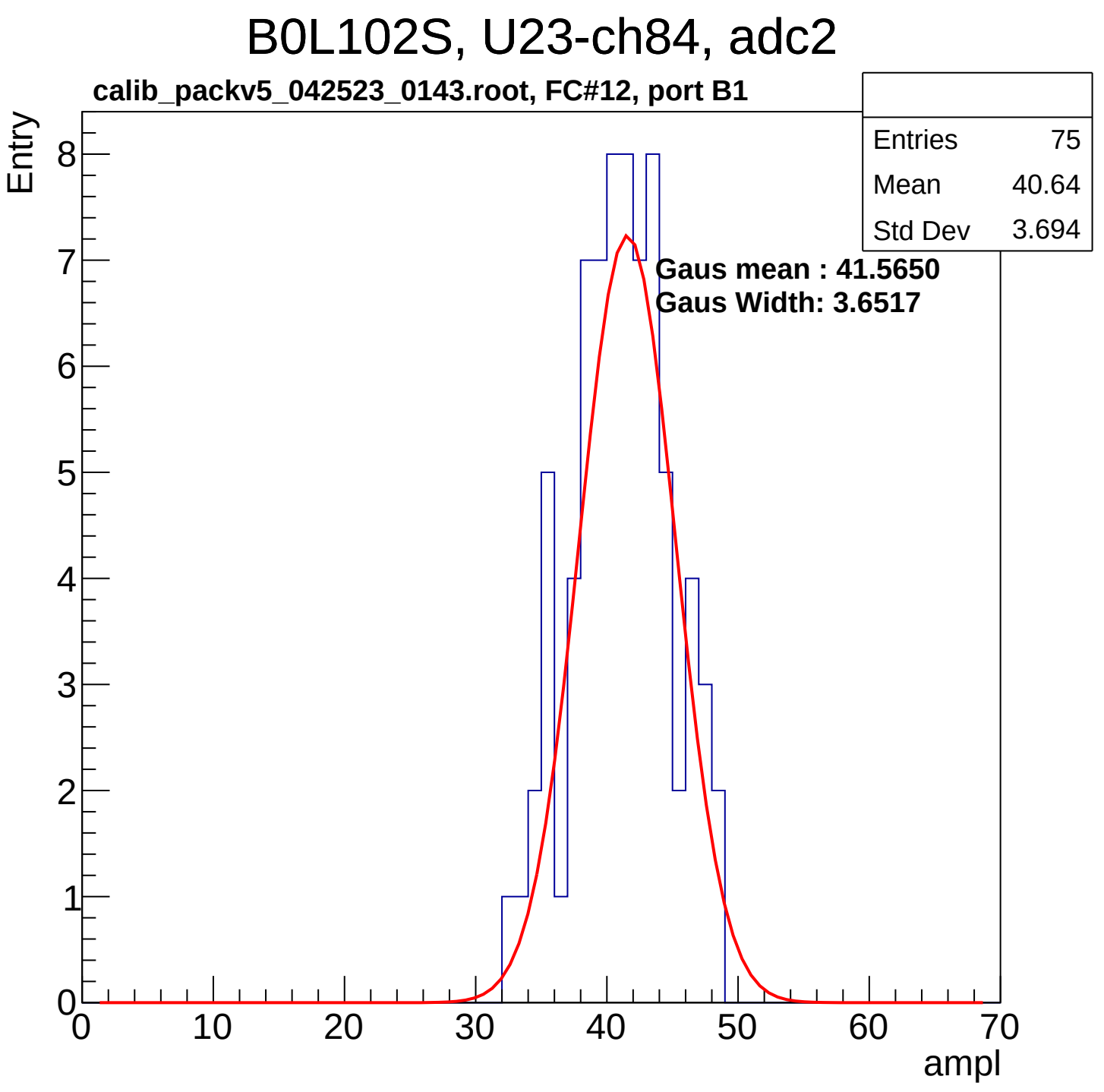
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch84, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 33.12
Std Dev 5.461
Gaus mean : 34.1035
Gaus Width: 4.1437
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch84, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 40.64
Std Dev 3.694
Gaus mean : 41.5650
Gaus Width: 3.6517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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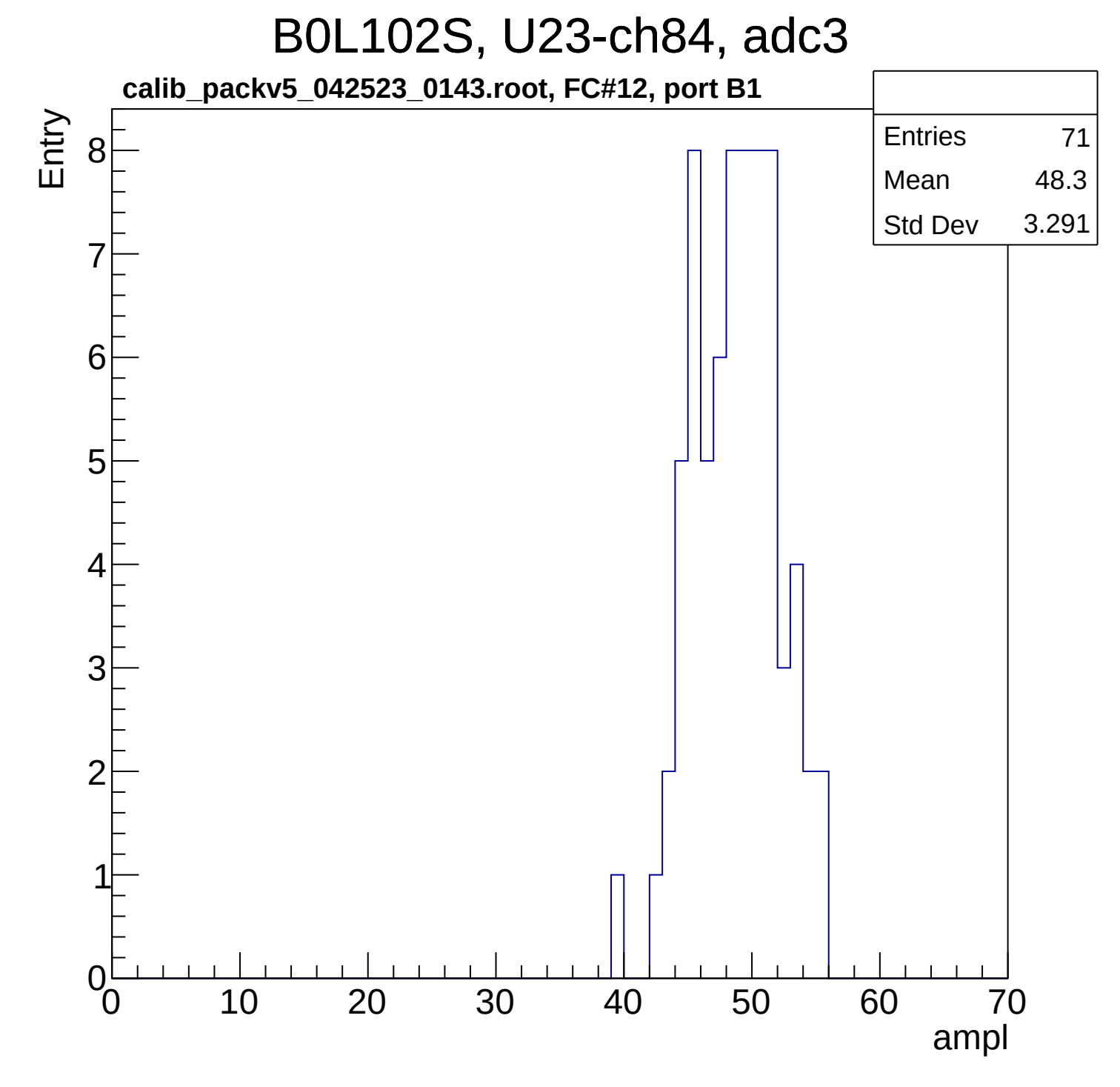
B0L102S, U23-ch84, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 48.3
Std Dev 3.291
Entry
ampl
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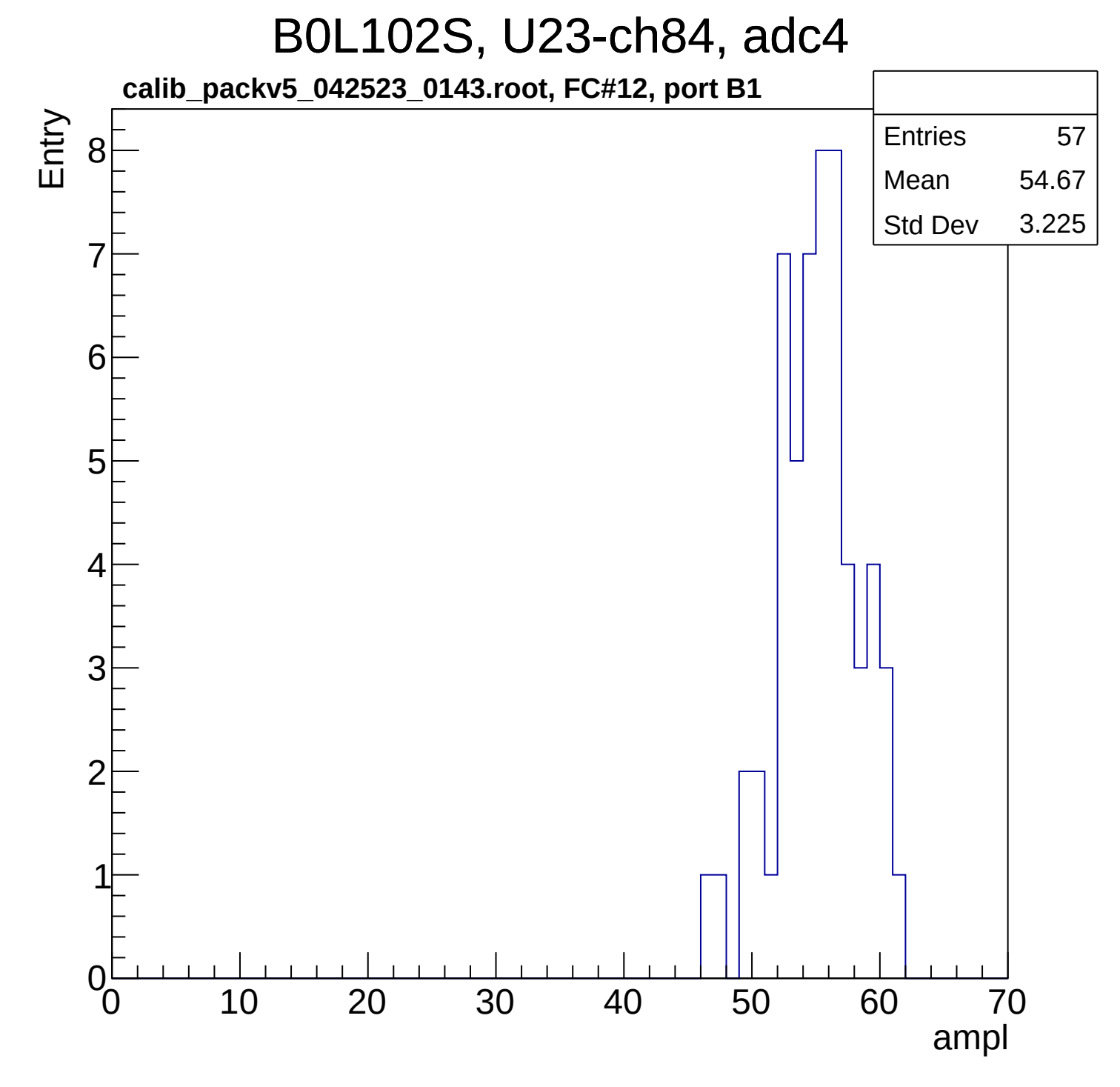
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch84, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 54.67
Std Dev 3.225
Entry
ampl
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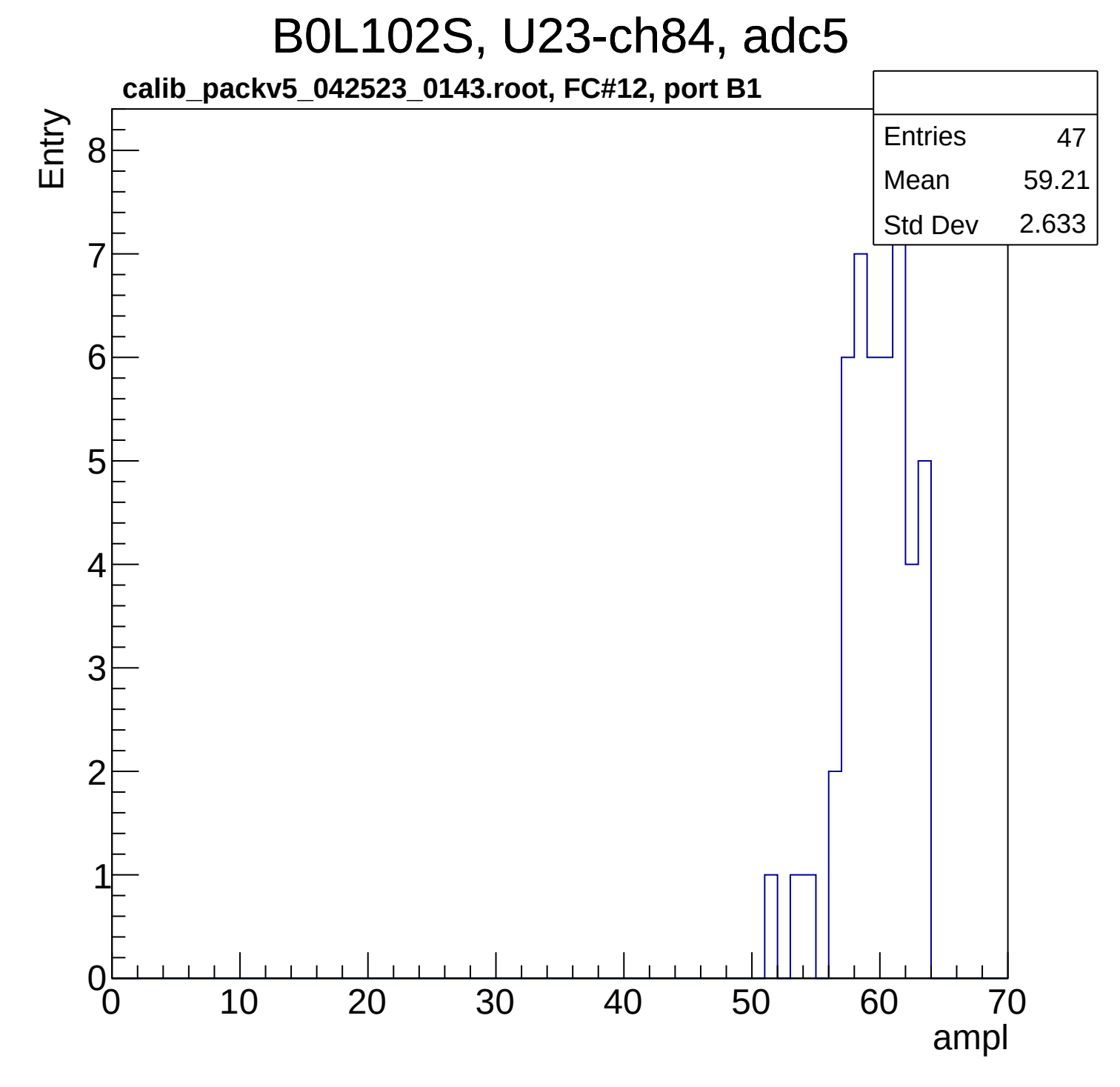
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch84, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 59.21
Std Dev 2.633
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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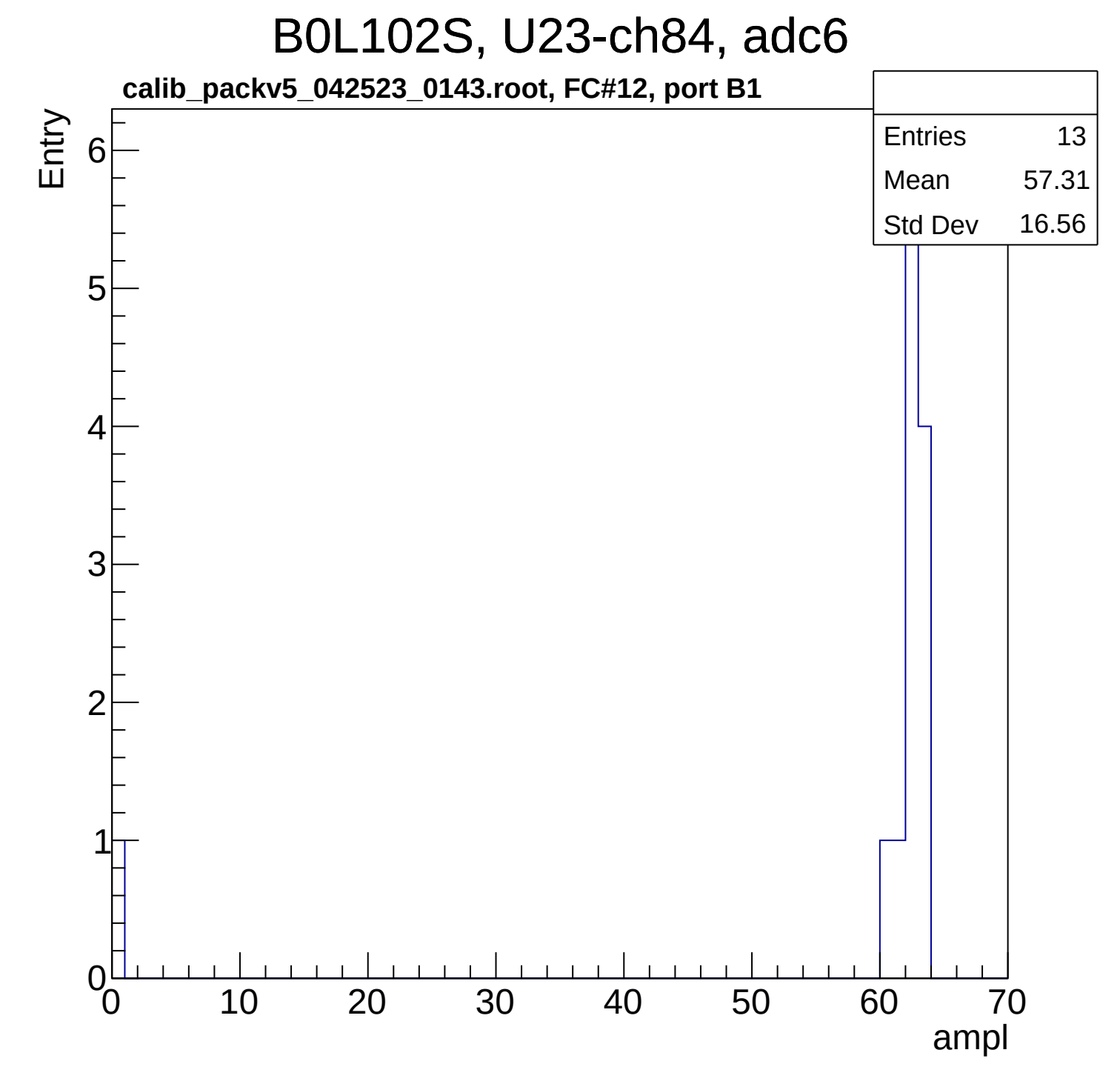
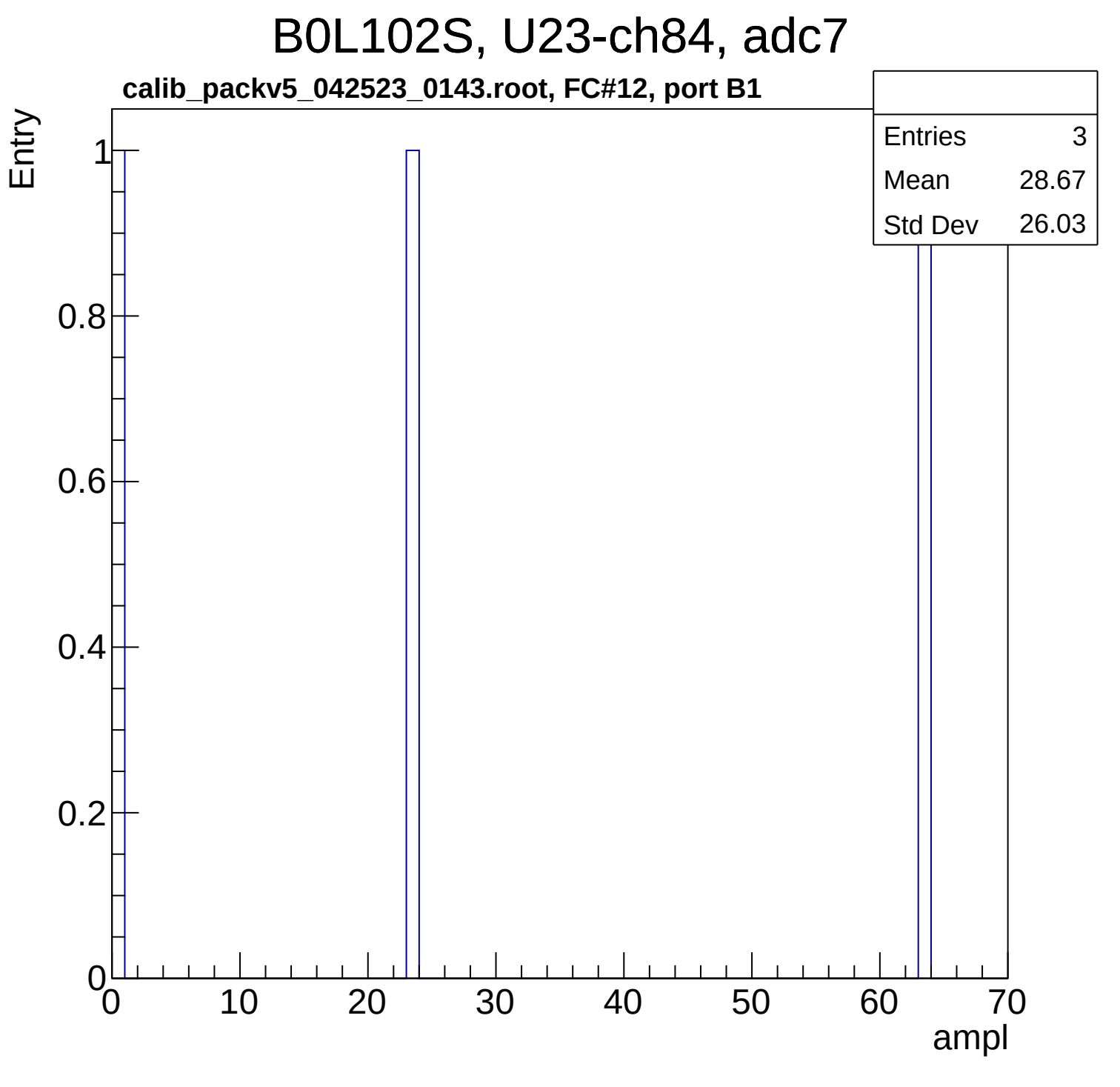

B0L102S, U23-ch84, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 57.31
Std Dev 16.56
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch84, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 28.67
Std Dev 26.03
Entry
1
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0.6
0.4
0.2
0
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50
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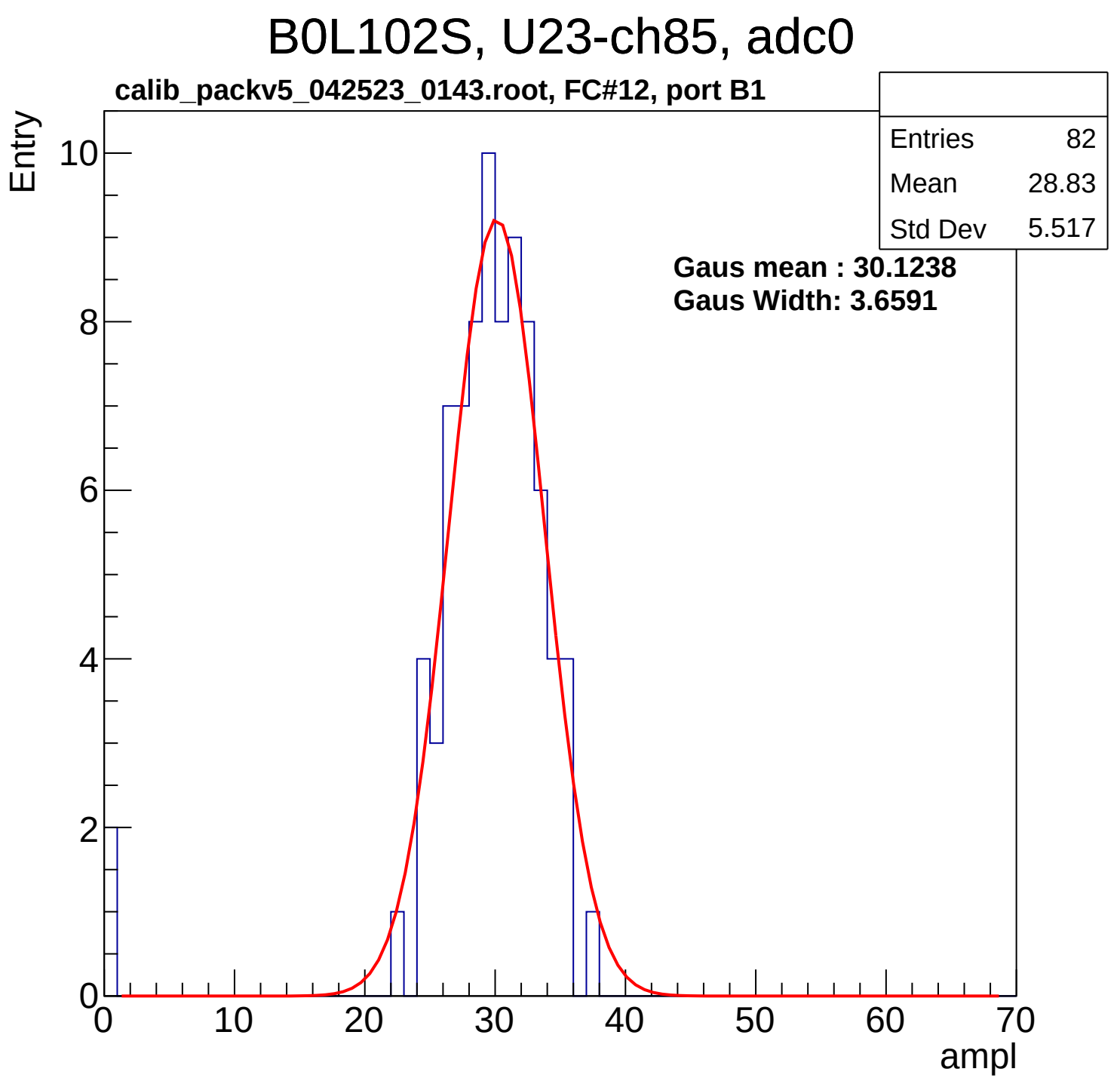
ampl


B0L102S, U23-ch85, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 28.83
Std Dev 5.517
Gaus mean : 30.1238
Gaus Width: 3.6591
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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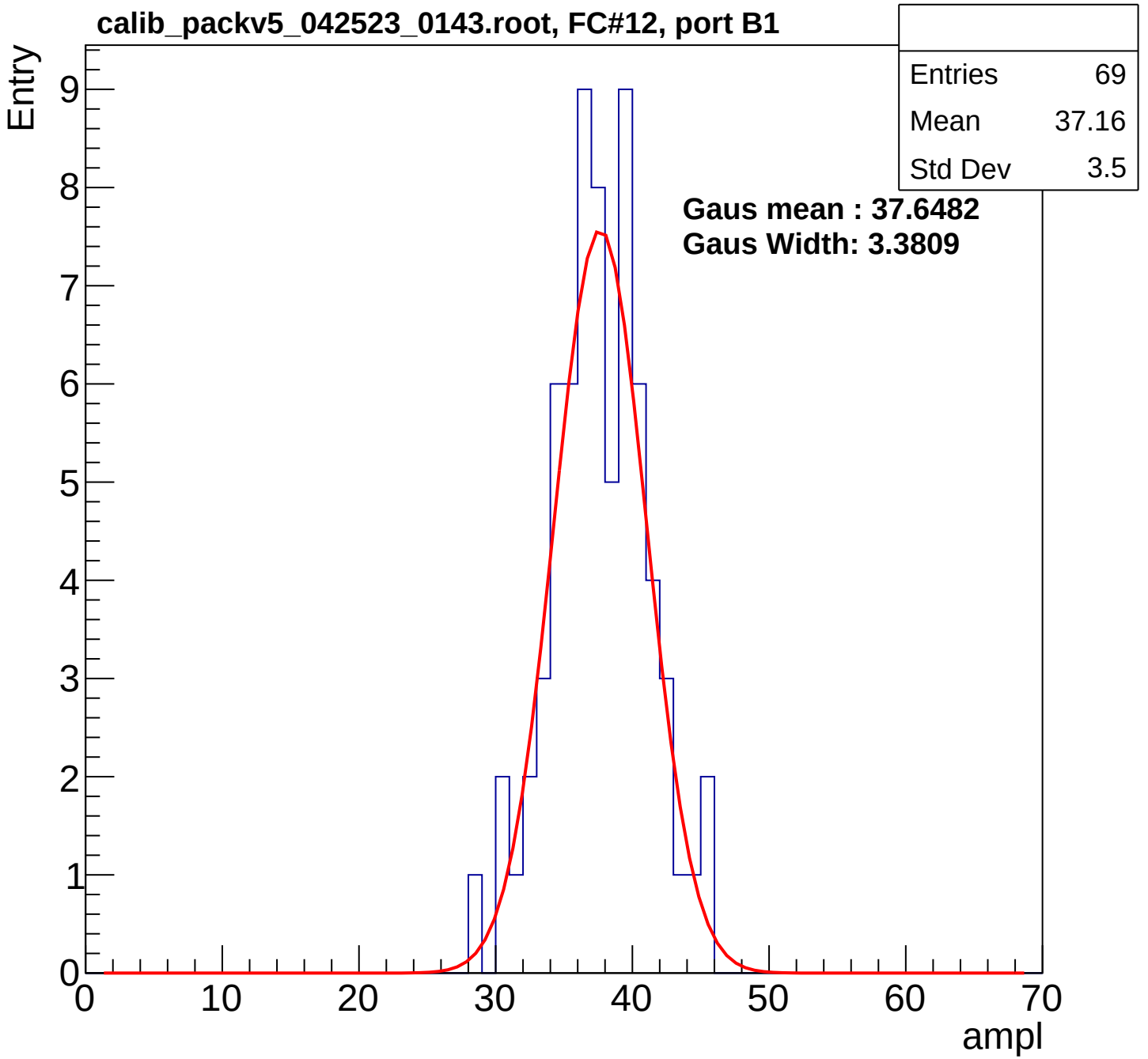
B0L102S, U23-ch85, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 37.16
Std Dev 3.5
Gaus mean : 37.6482
Gaus Width: 3.3809
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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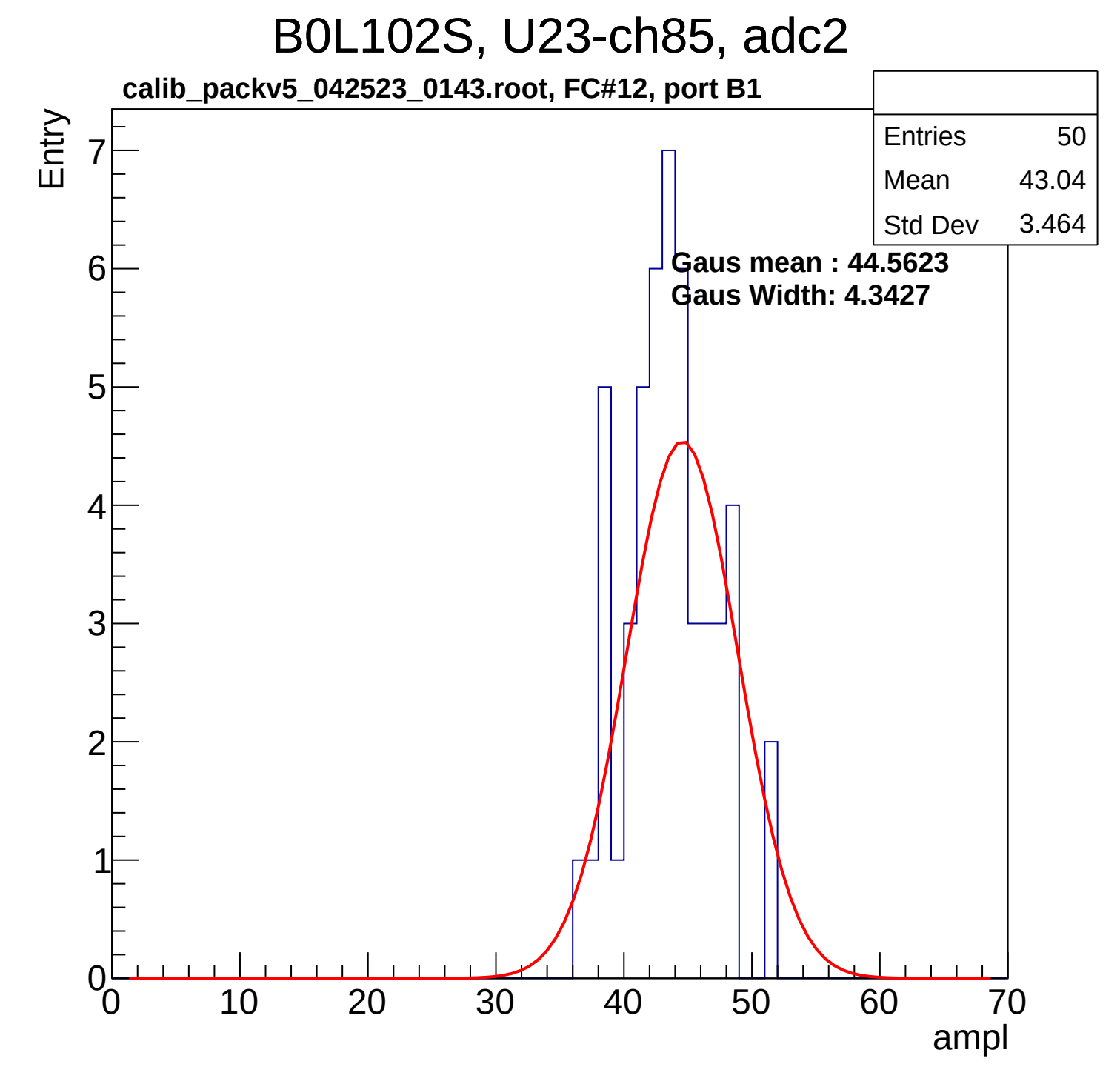
B0L102S, U23-ch85, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 43.04
Std Dev 3.464
Gaus mean : 44.5623
Gaus Width: 4.3427
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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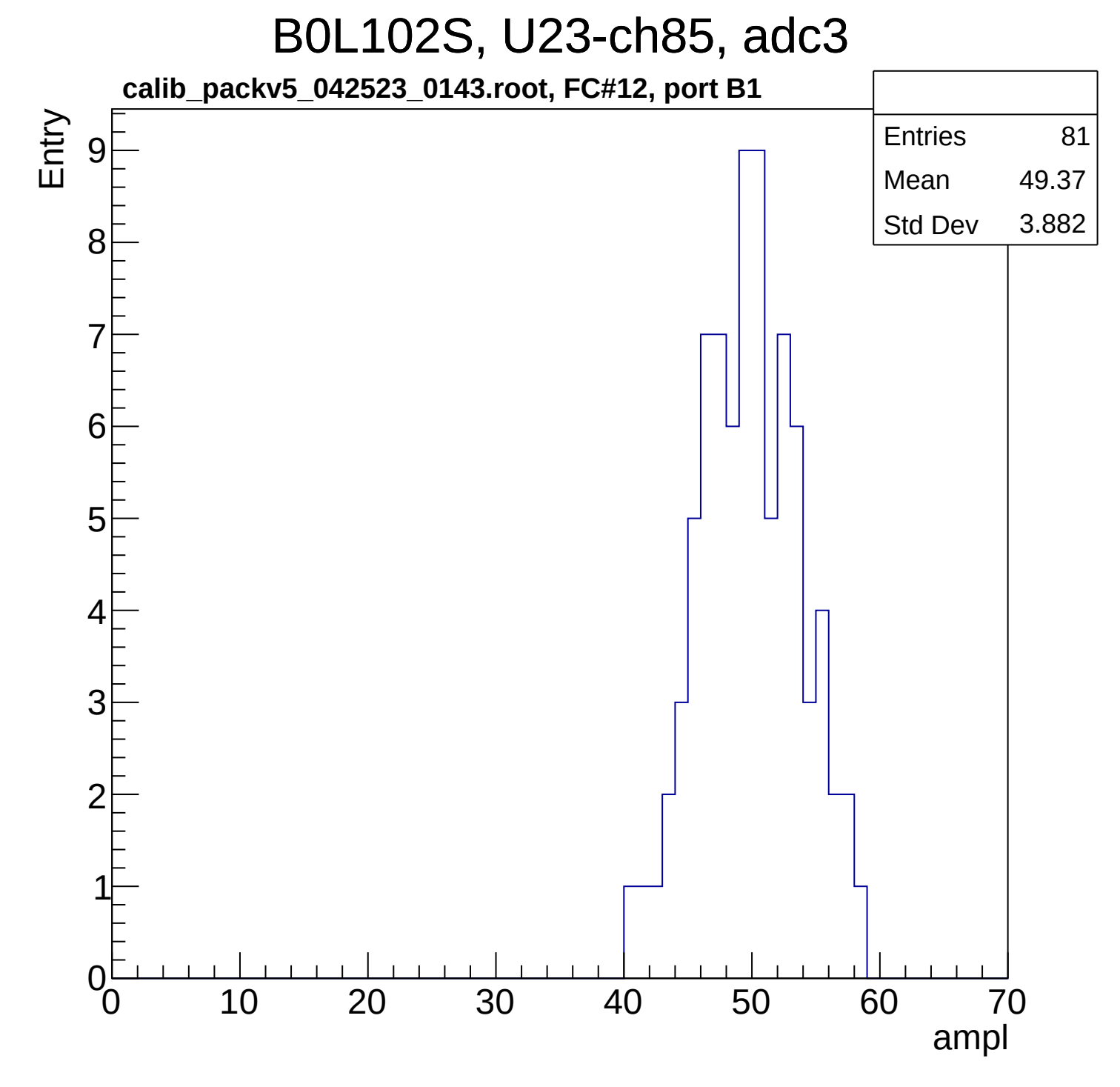
B0L102S, U23-ch85, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 49.37
Std Dev 3.882
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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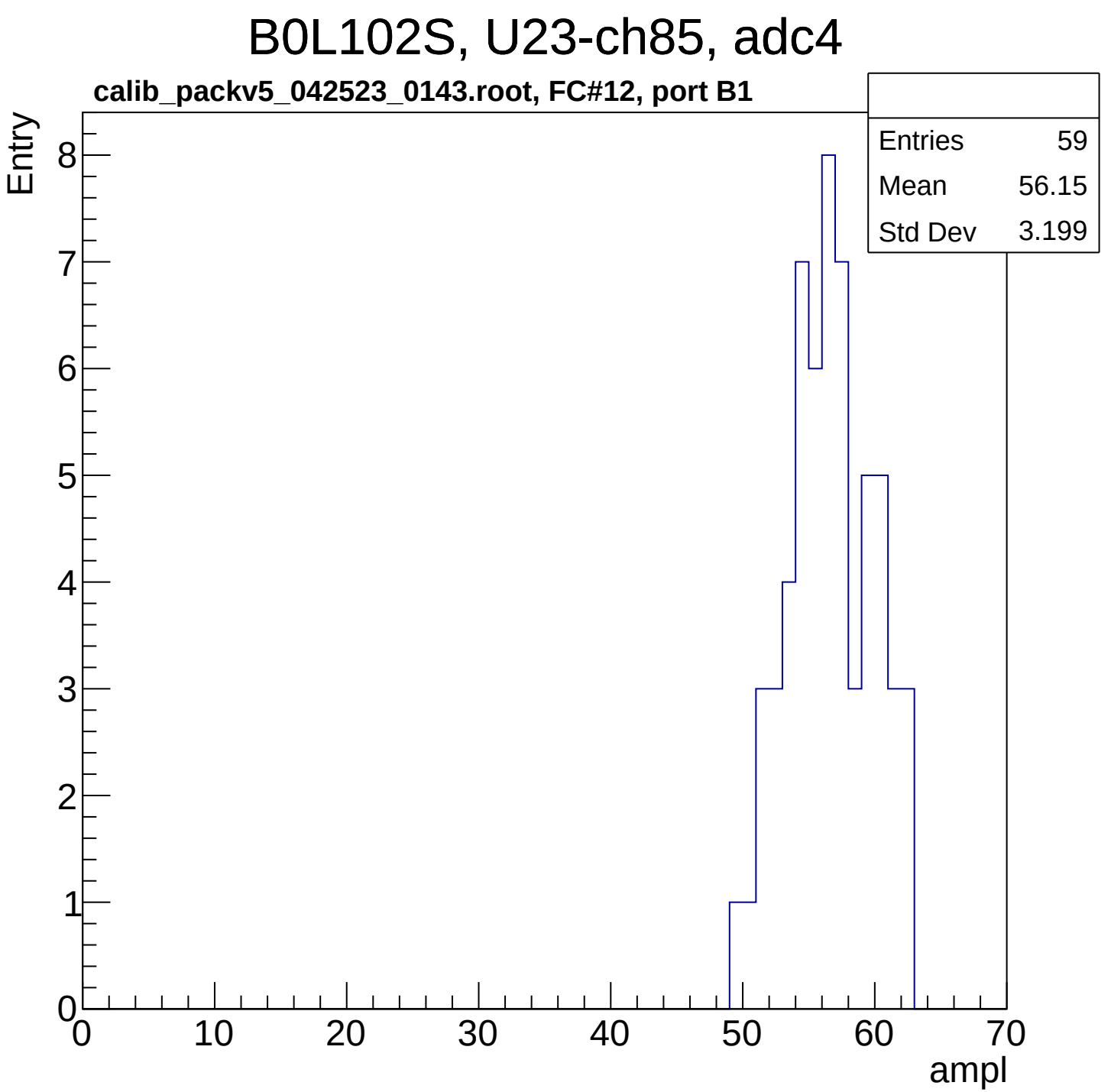
B0L102S, U23-ch85, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 56.15
Std Dev 3.199
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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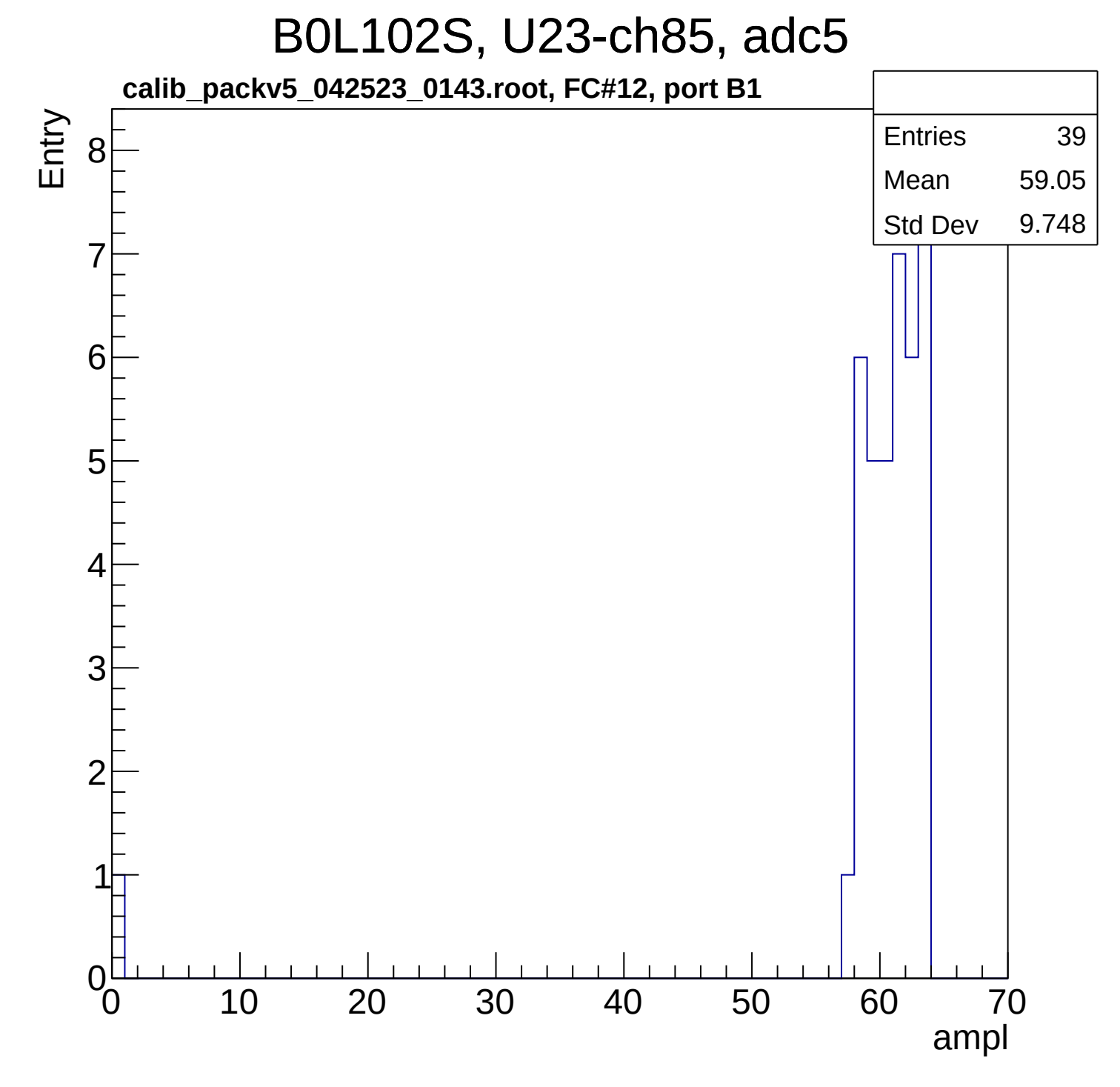
B0L102S, U23-ch85, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 59.05
Std Dev 9.748
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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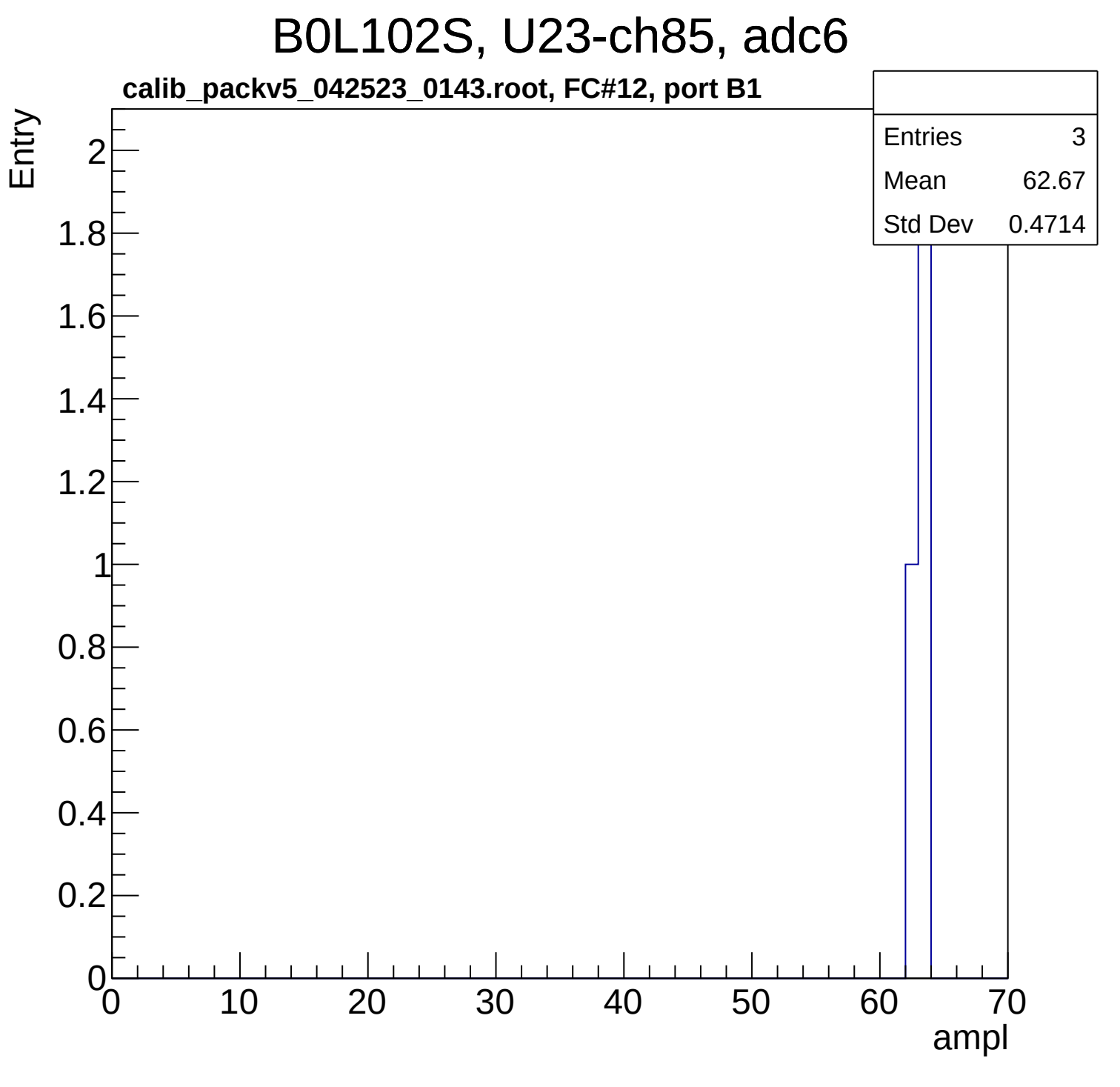
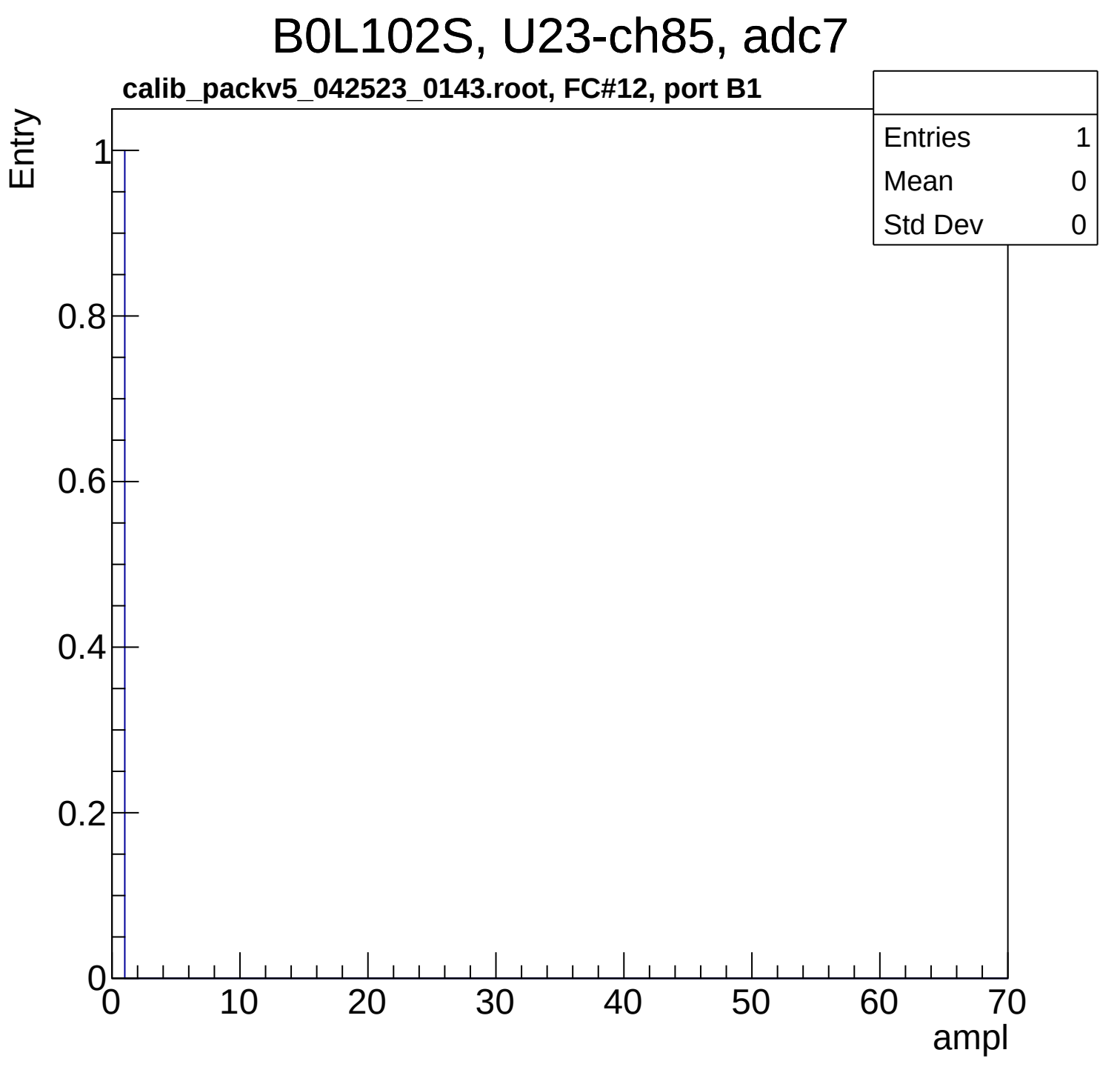
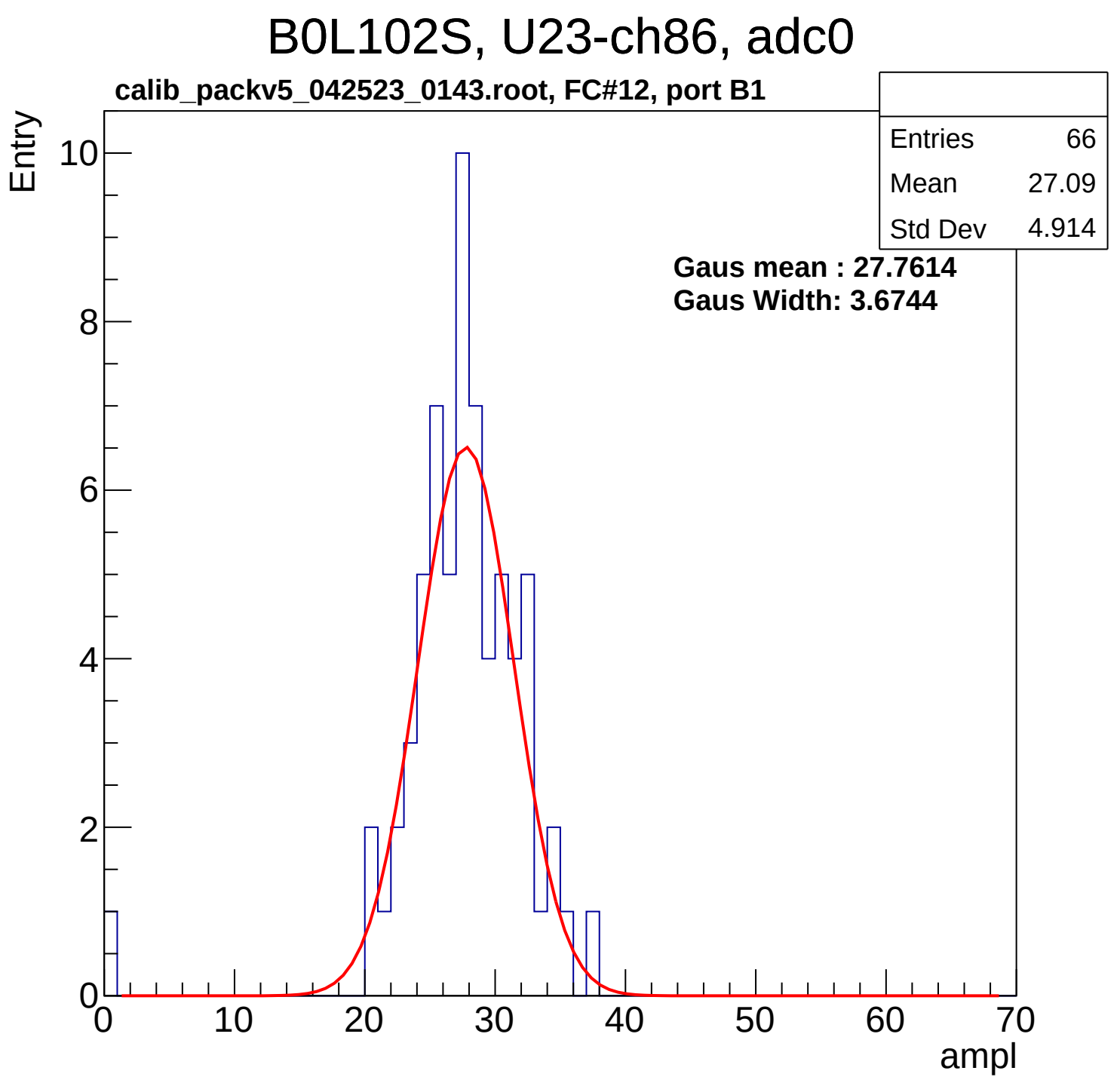
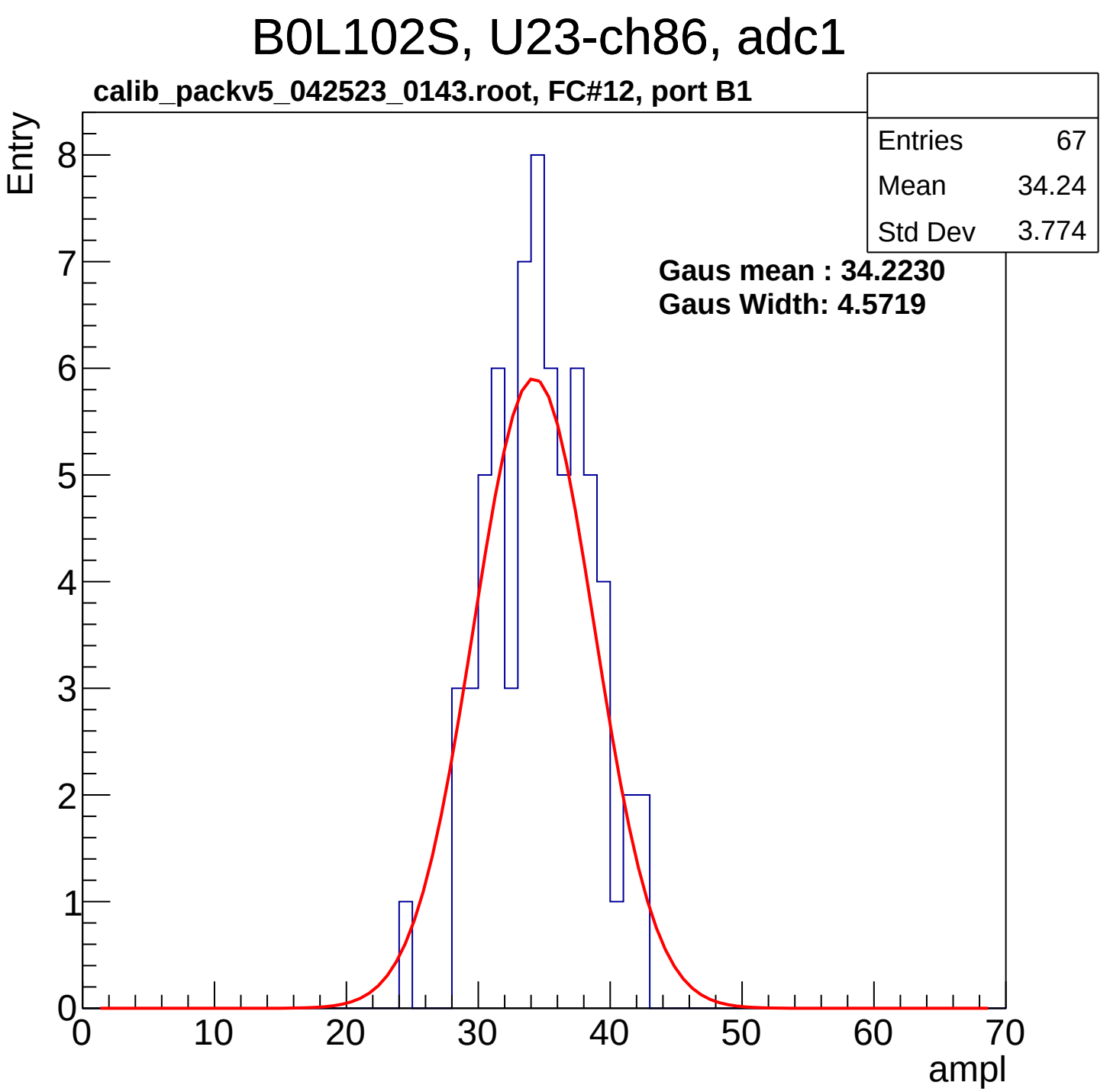

B0L102S, U23-ch85, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch85, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch86, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 27.09
Std Dev 4.914
Gaus mean : 27.7614
Gaus Width: 3.6744
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch86, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 34.24
Std Dev 3.774
Gaus mean : 34.2230
Gaus Width: 4.5719
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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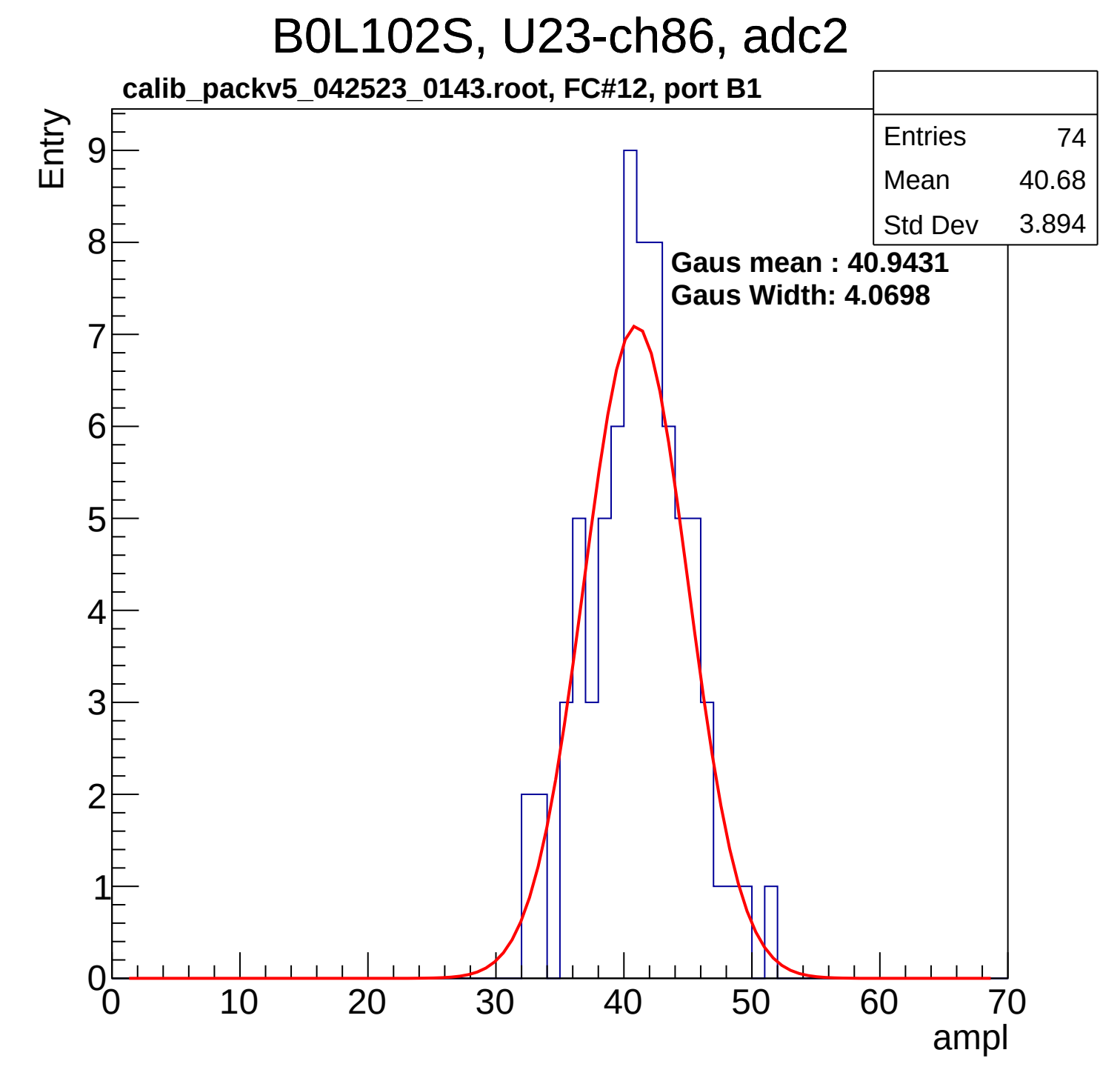
B0L102S, U23-ch86, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 40.68
Std Dev 3.894
Gaus mean : 40.9431
Gaus Width: 4.0698
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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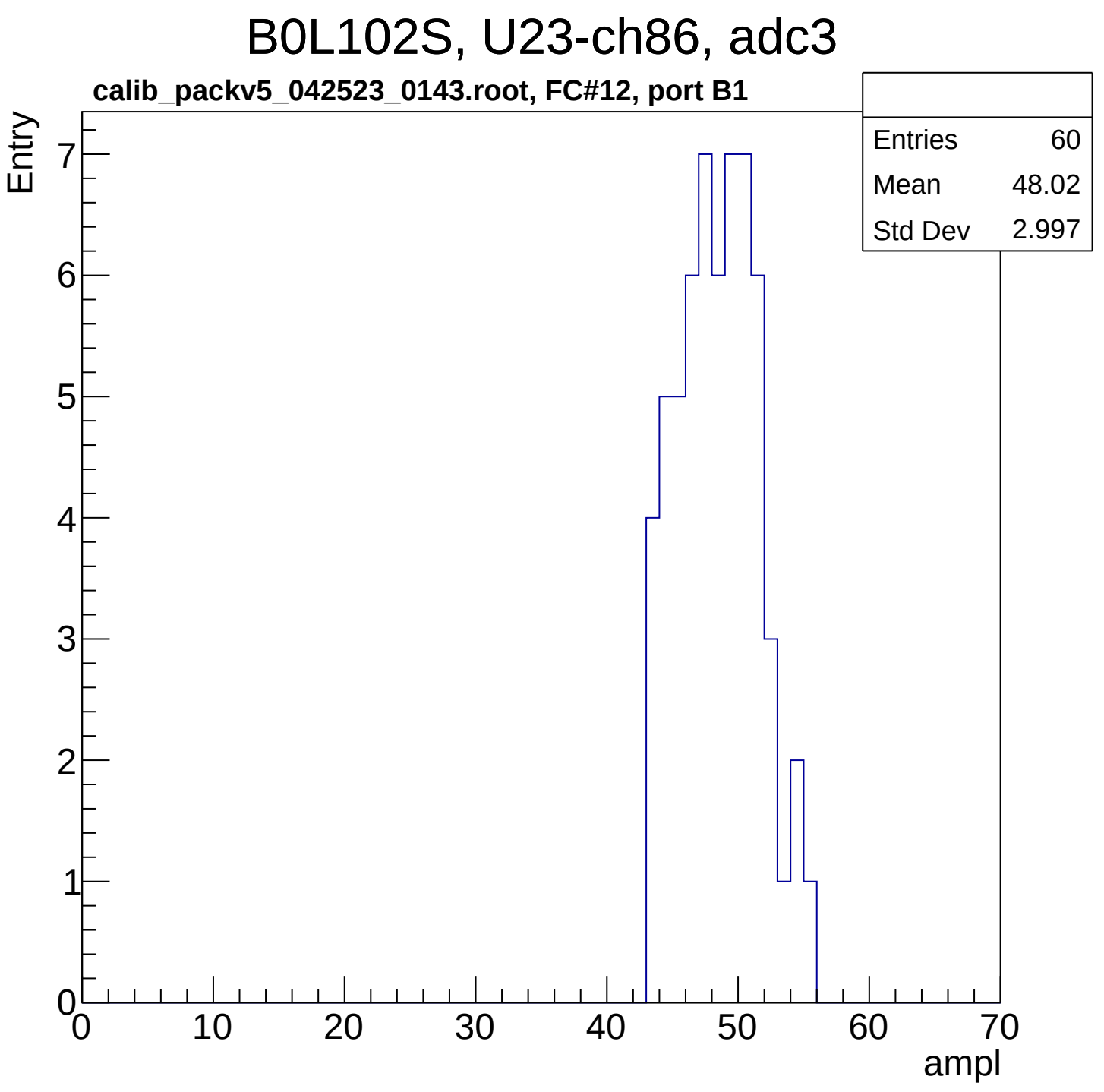
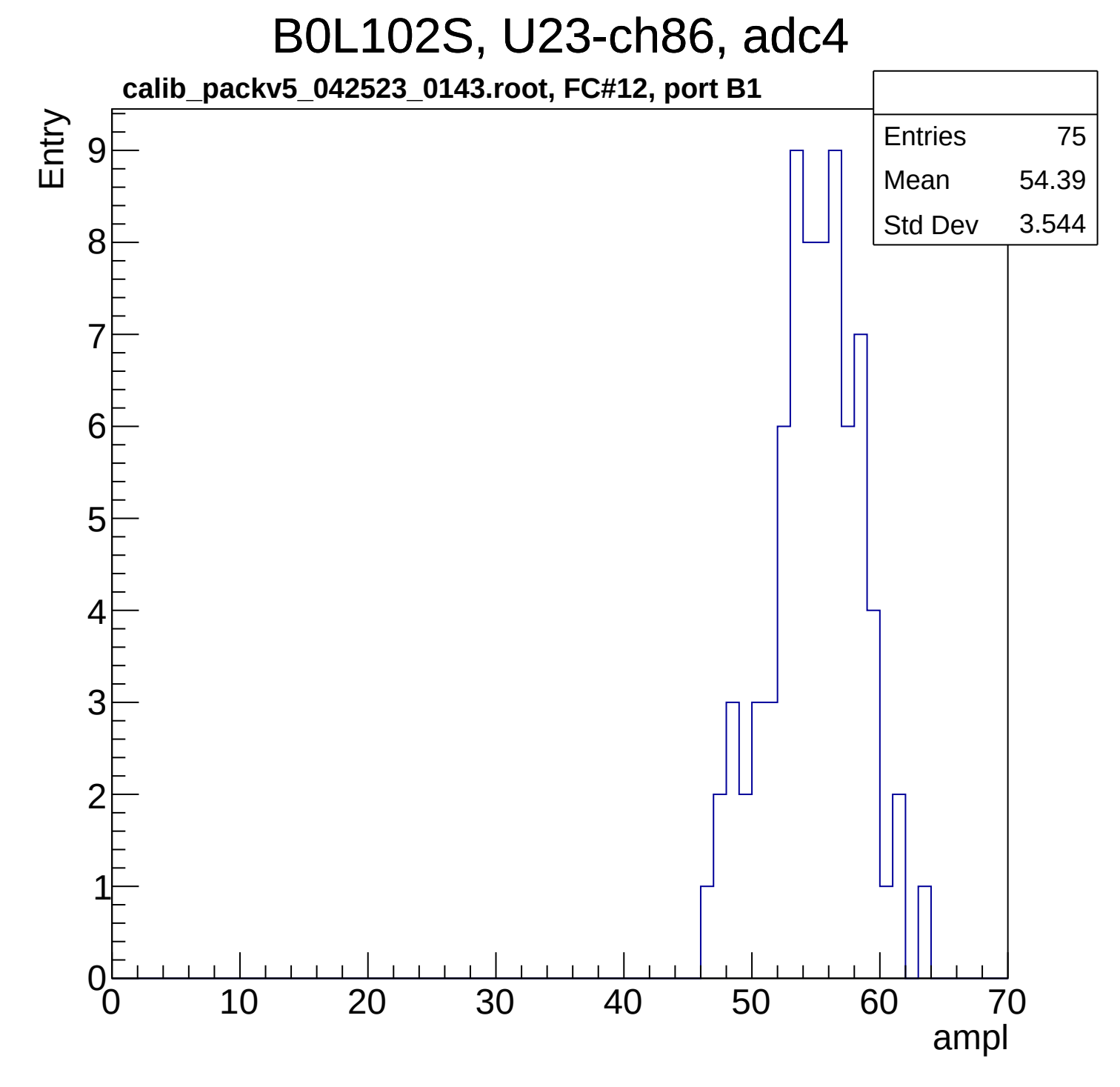

B0L102S, U23-ch86, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 48.02
Std Dev 2.997
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch86, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 54.39
Std Dev 3.544
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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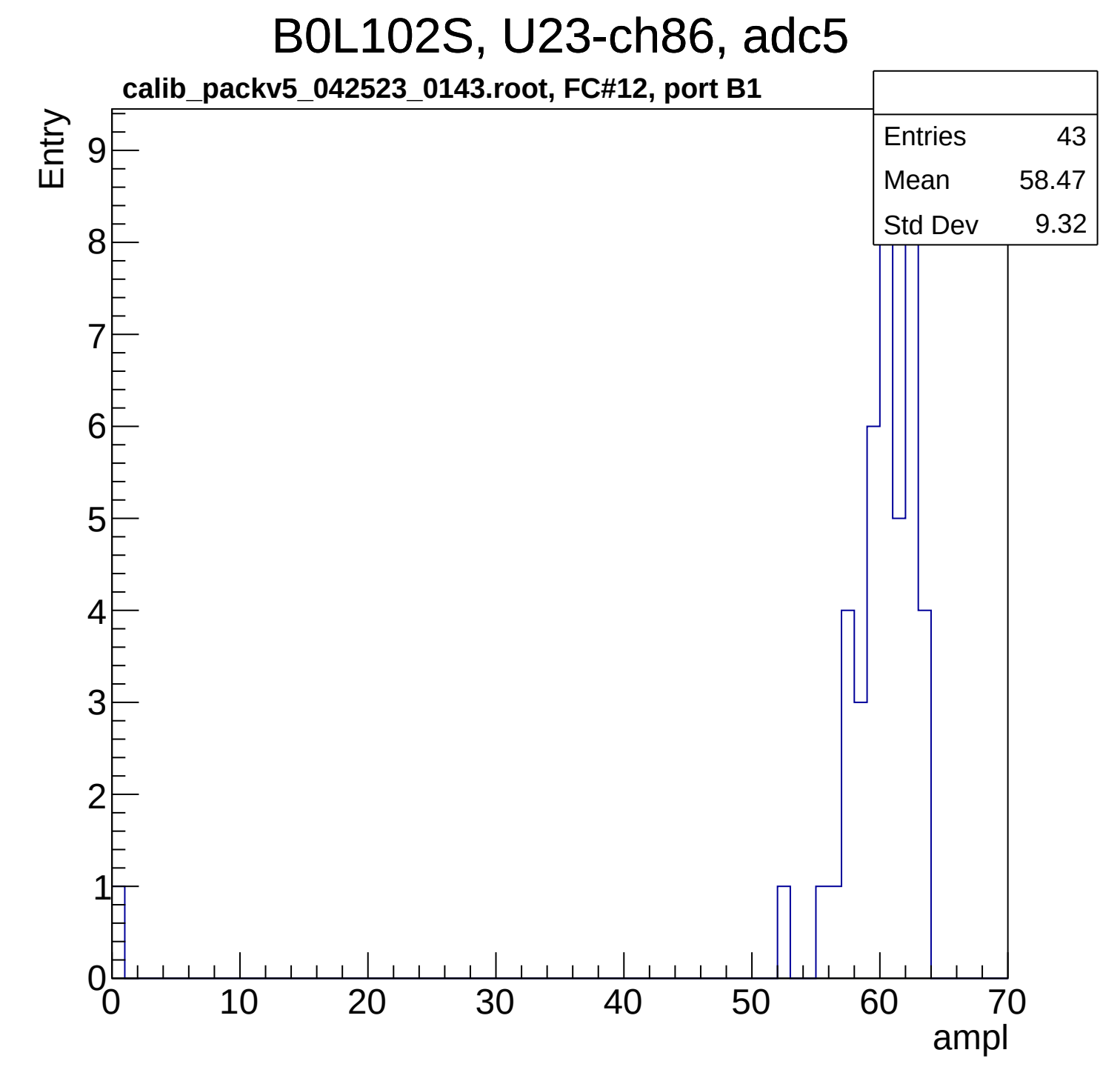
B0L102S, U23-ch86, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 58.47
Std Dev 9.32
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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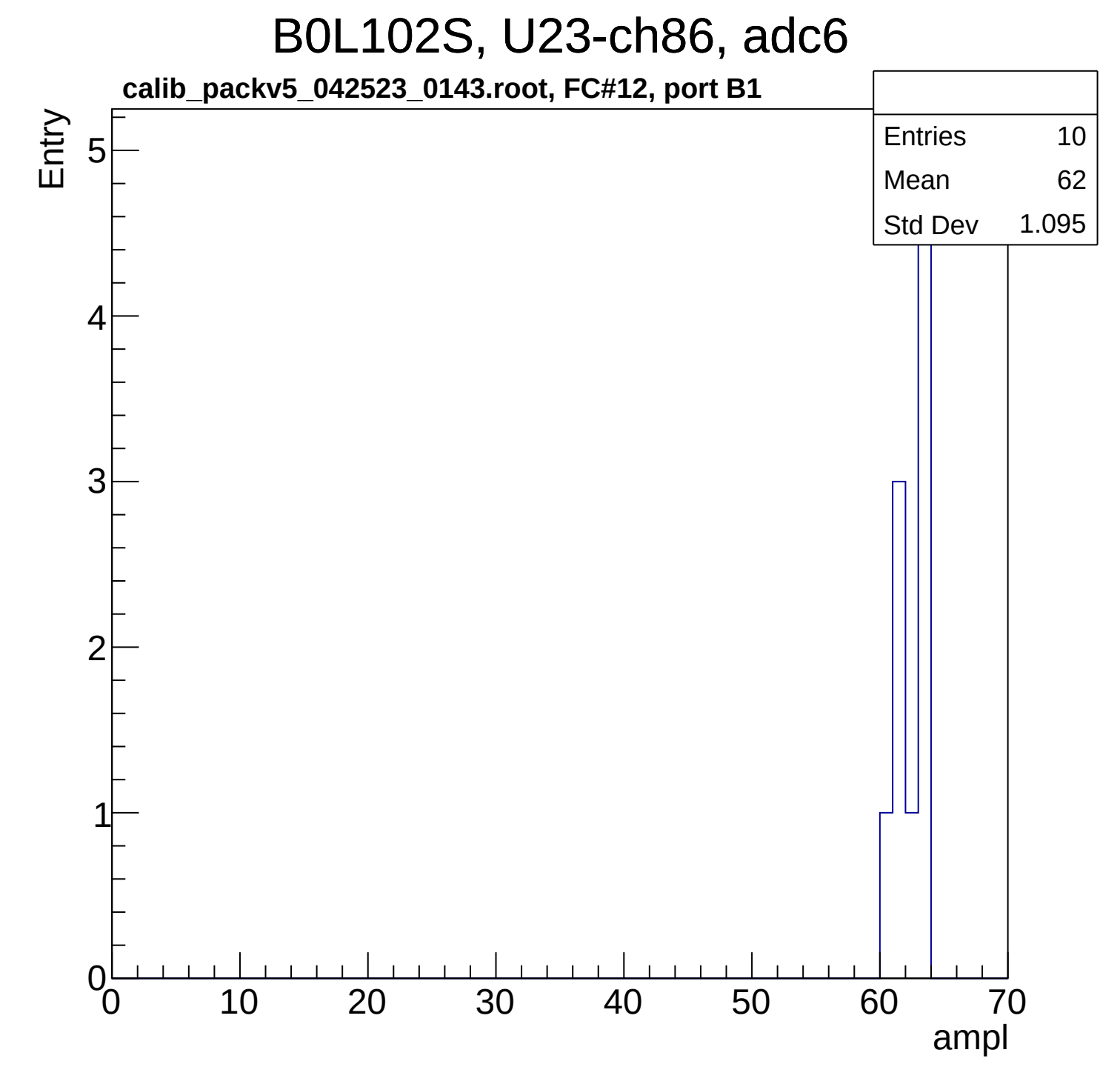
B0L102S, U23-ch86, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 62
Std Dev 1.095
Entry
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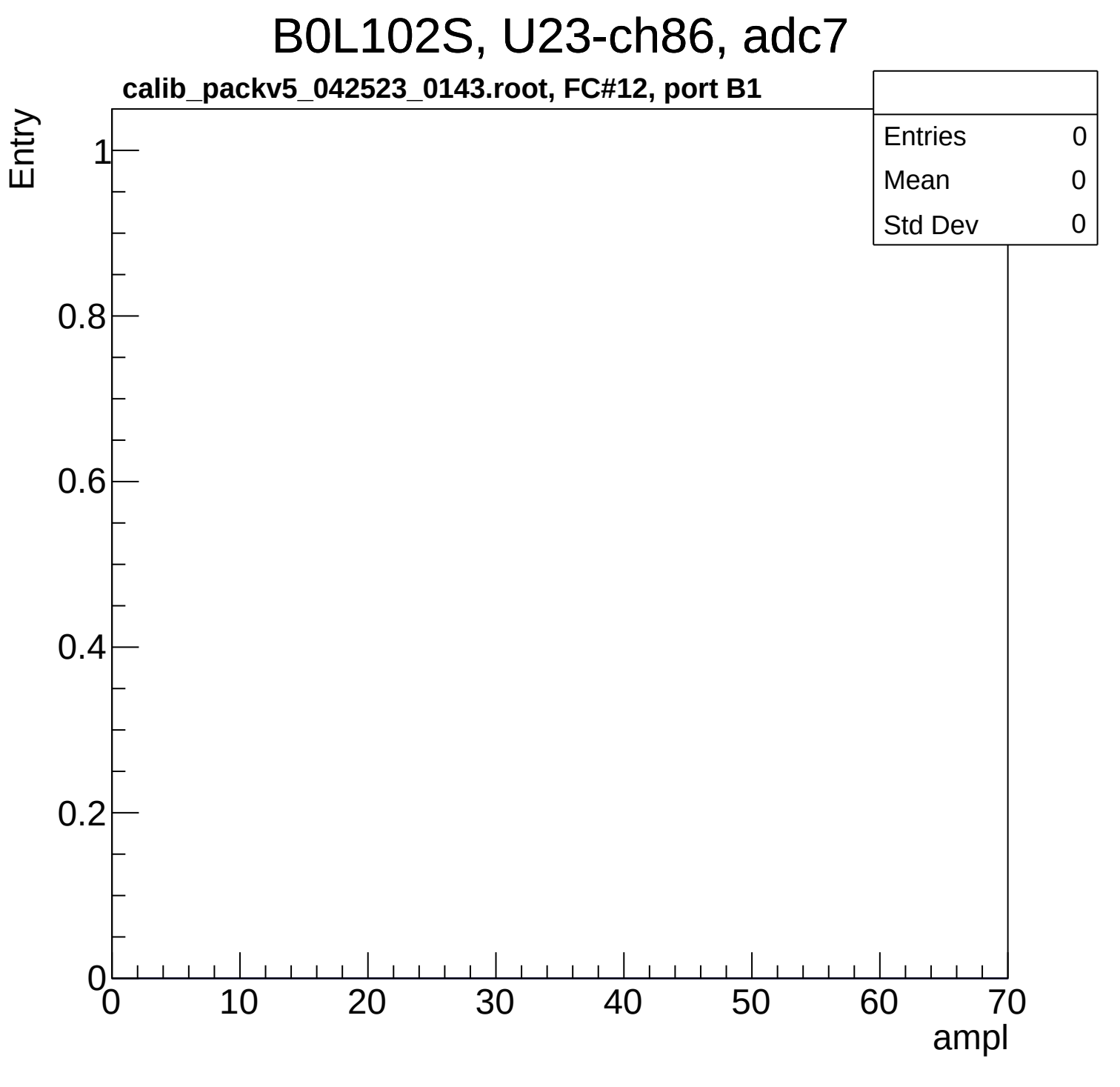
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch86, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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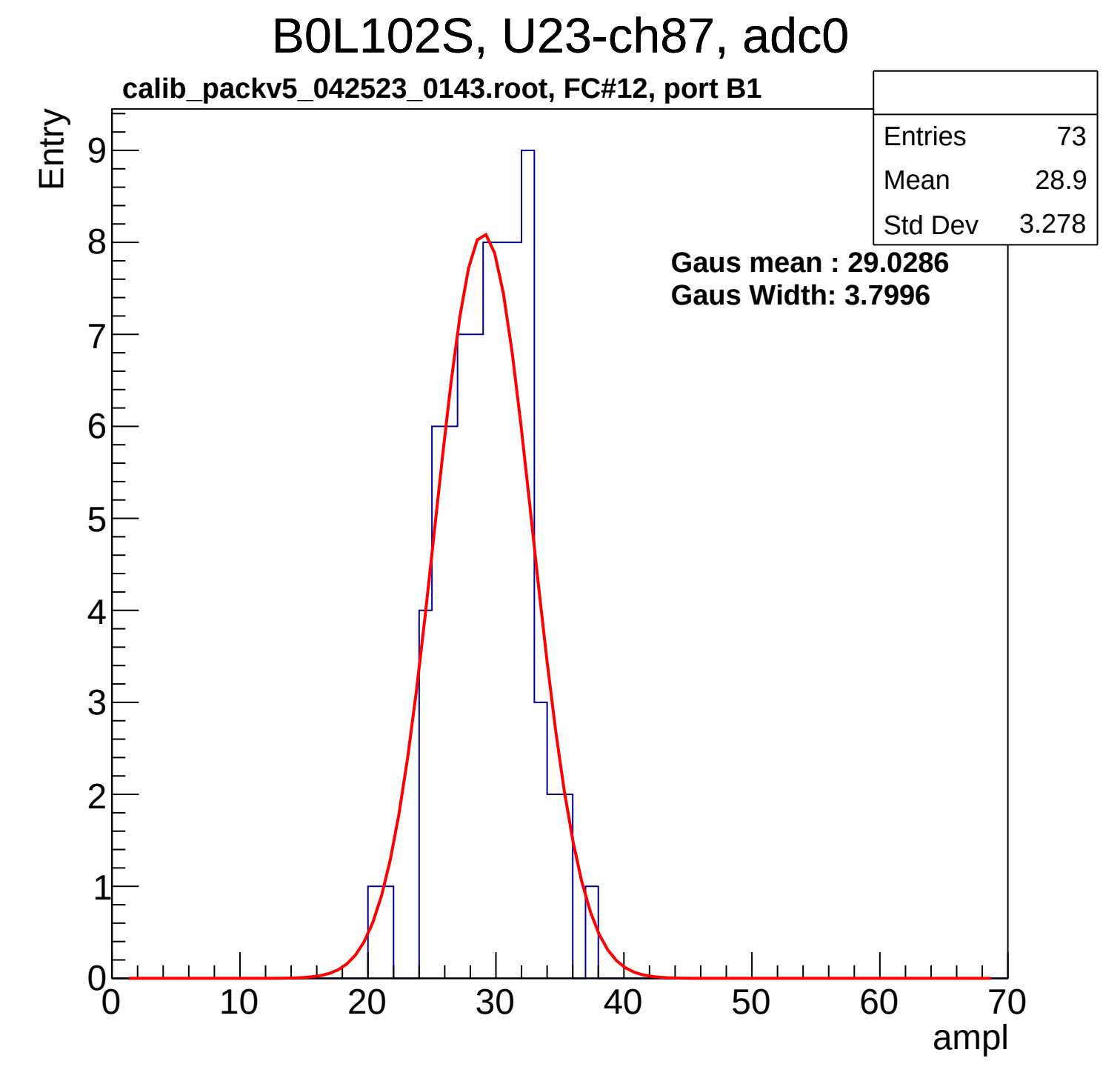
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch87, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 28.9
Std Dev 3.278
Gaus mean : 29.0286
Gaus Width: 3.7996
Entry
ampl
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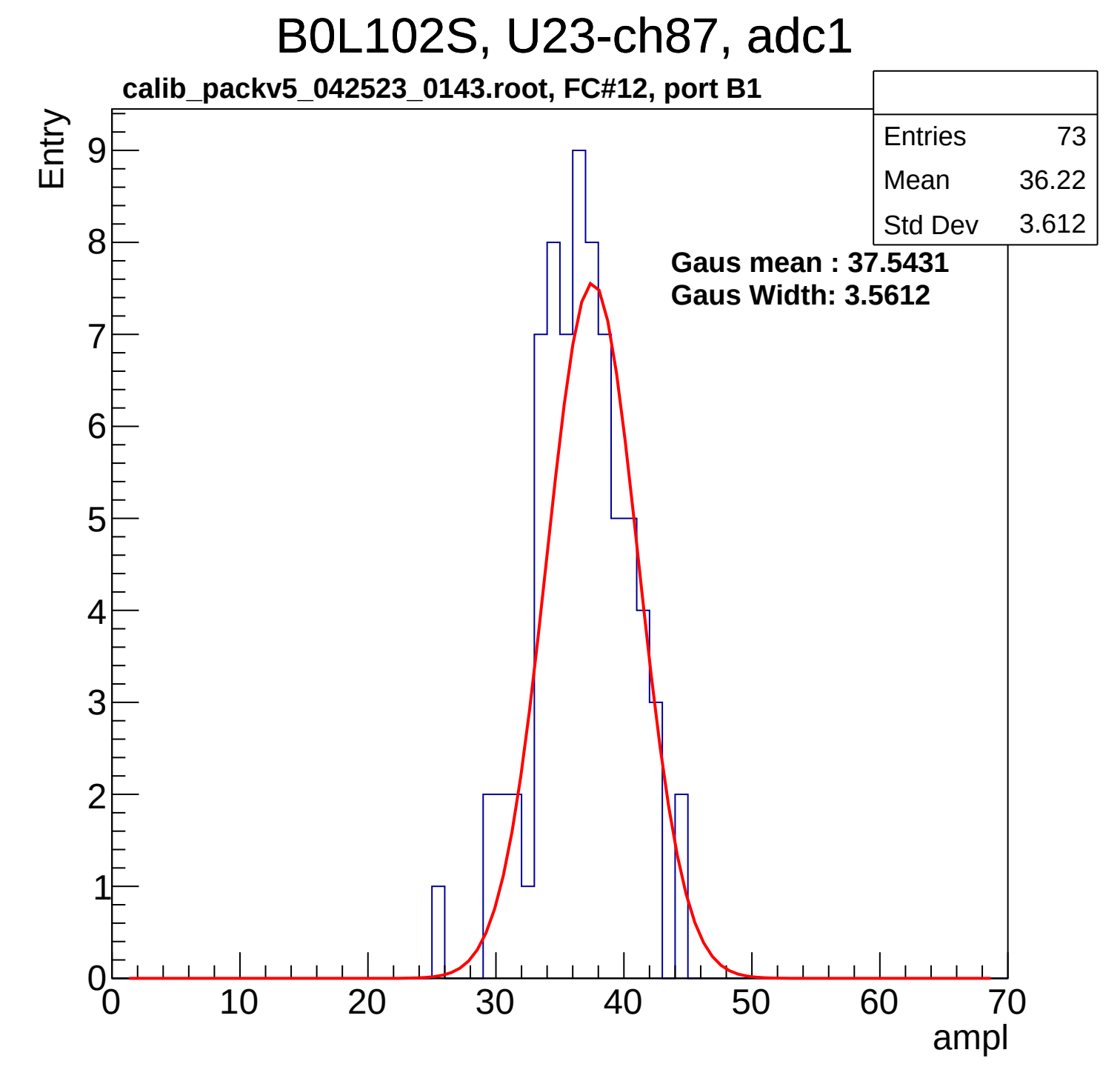
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch87, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 36.22
Std Dev 3.612
Gaus mean : 37.5431
Gaus Width: 3.5612
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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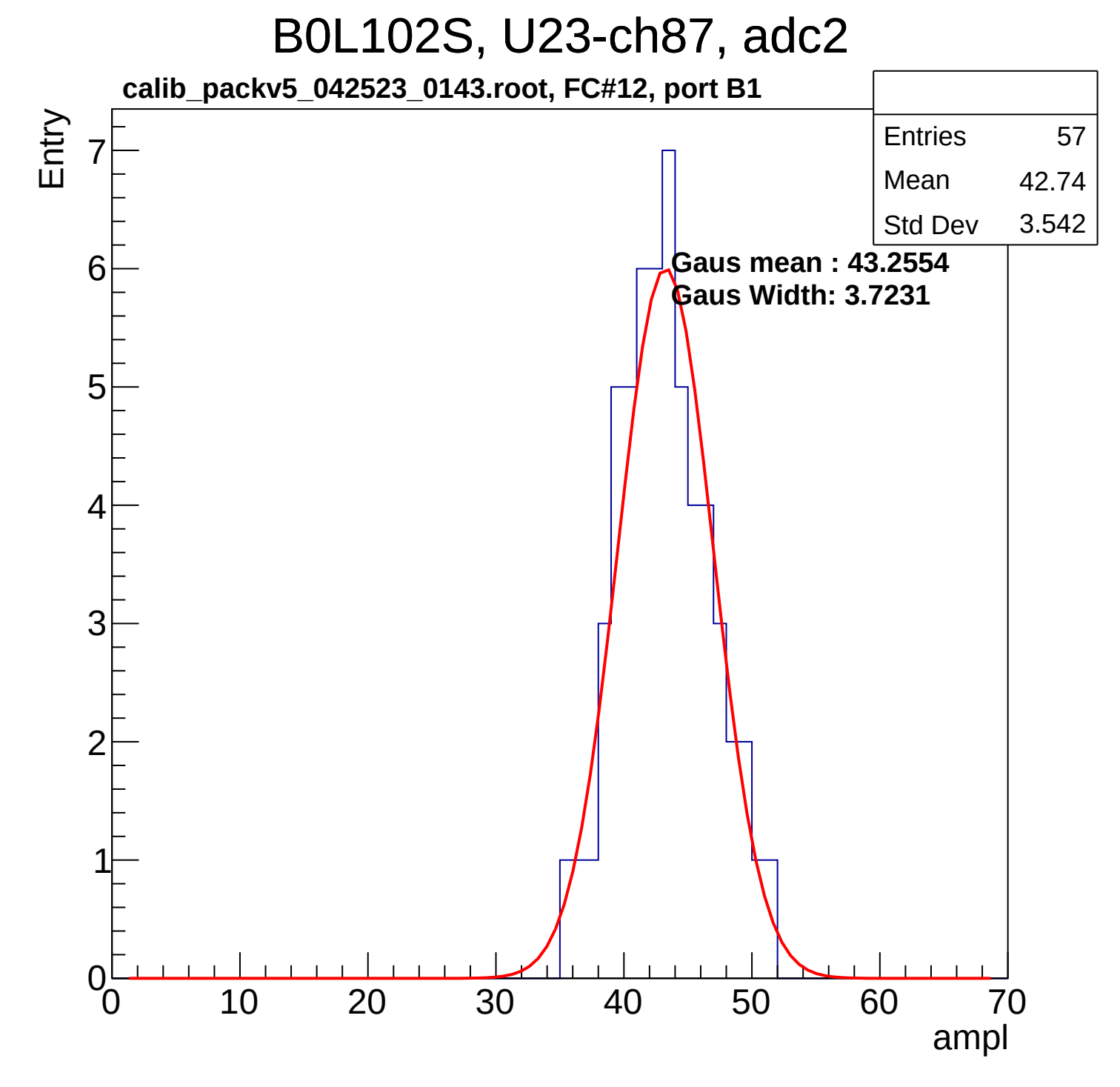
B0L102S, U23-ch87, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 42.74
Std Dev 3.542
Gaus mean : 43.2554
Gaus Width: 3.7231
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
50
60
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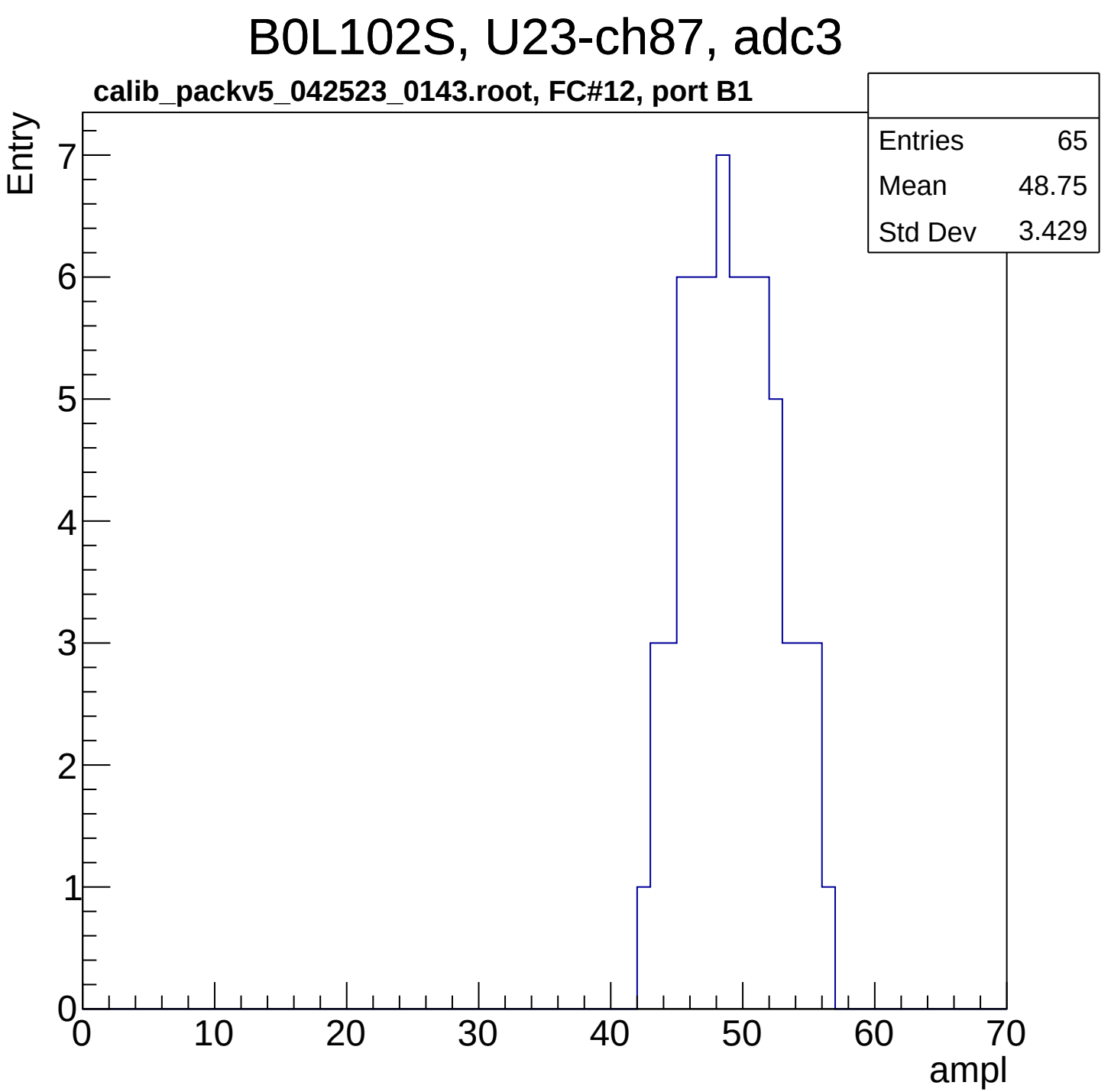
B0L102S, U23-ch87, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 48.75
Std Dev 3.429
Entry
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3
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7
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10
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50
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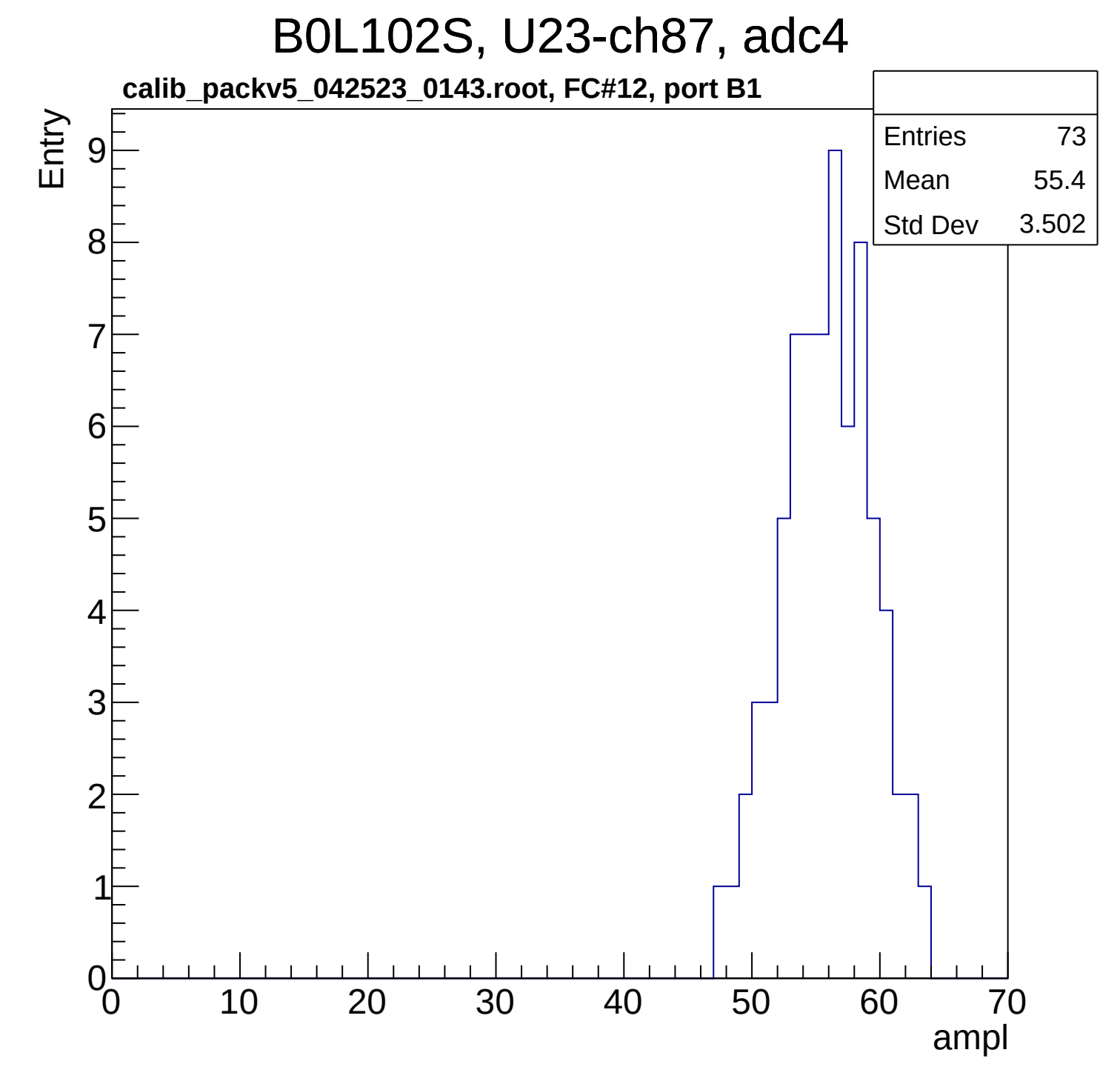
70
ampl


B0L102S, U23-ch87, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 55.4
Std Dev 3.502
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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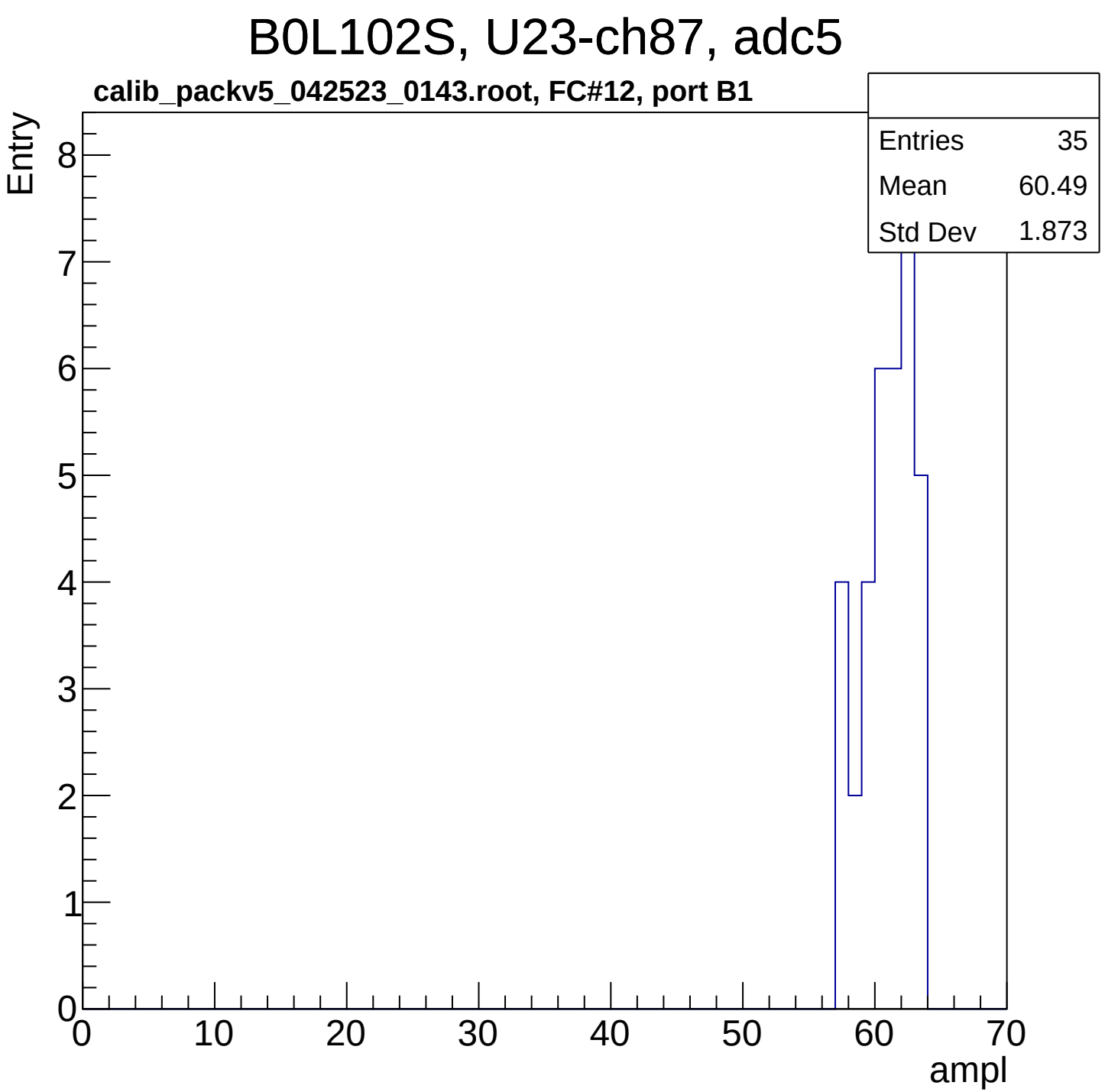
B0L102S, U23-ch87, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 60.49
Std Dev 1.873
Entry
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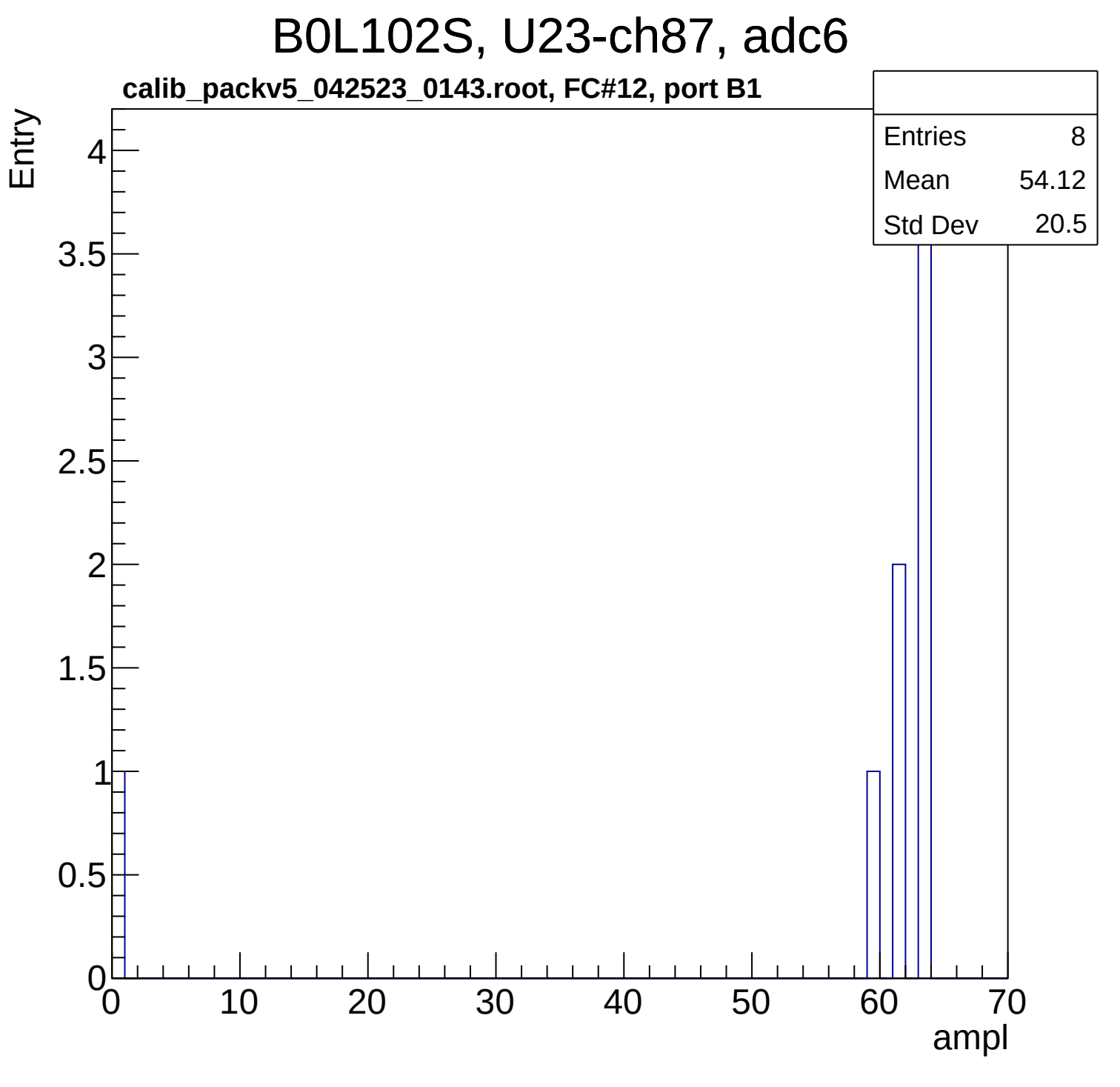
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch87, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 54.12
Std Dev 20.5
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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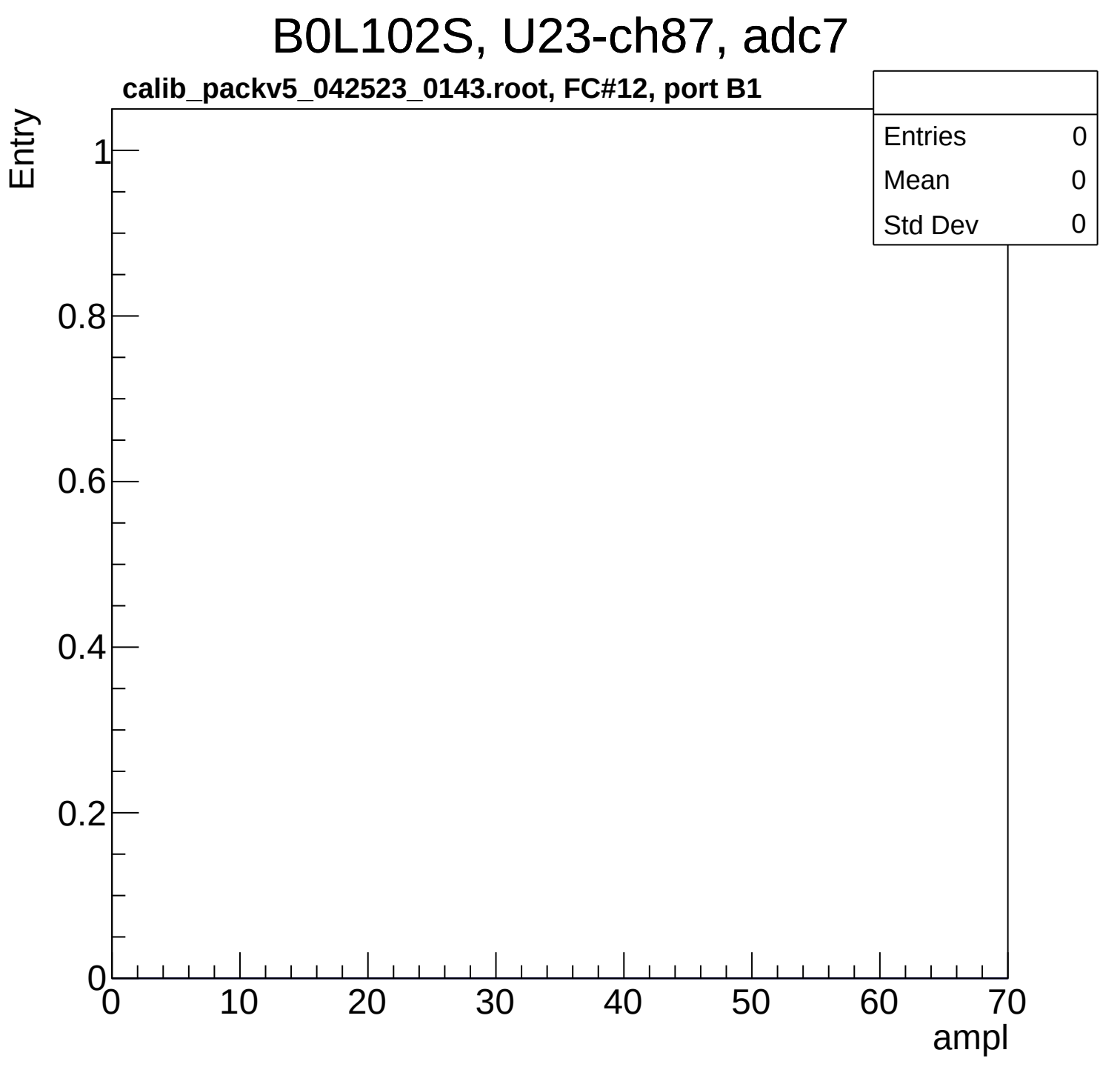
50
60
70


B0L102S, U23-ch87, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
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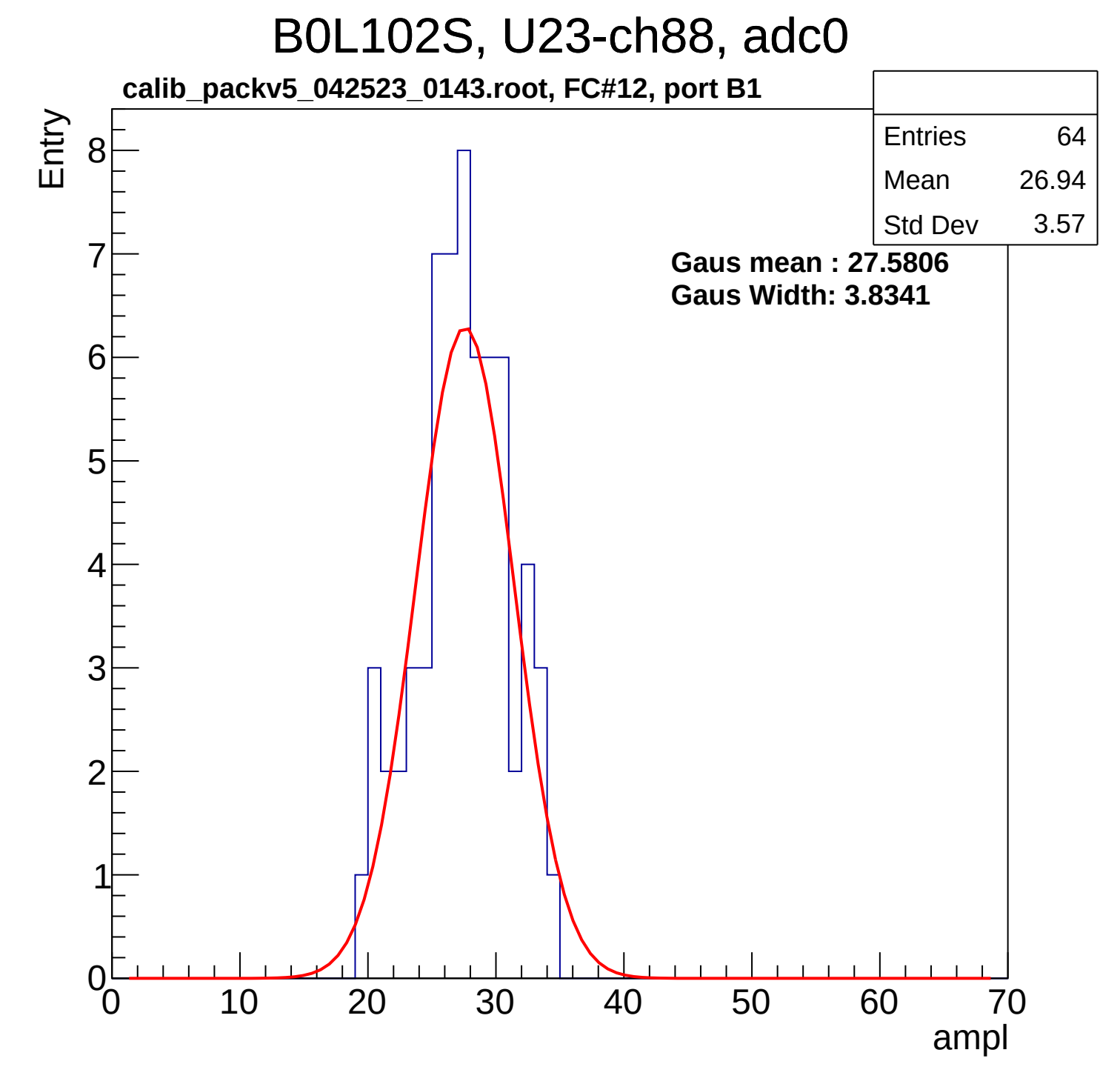
40
50
60
70
ampl


B0L102S, U23-ch88, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 26.94
Std Dev 3.57
Gaus mean : 27.5806
Gaus Width: 3.8341
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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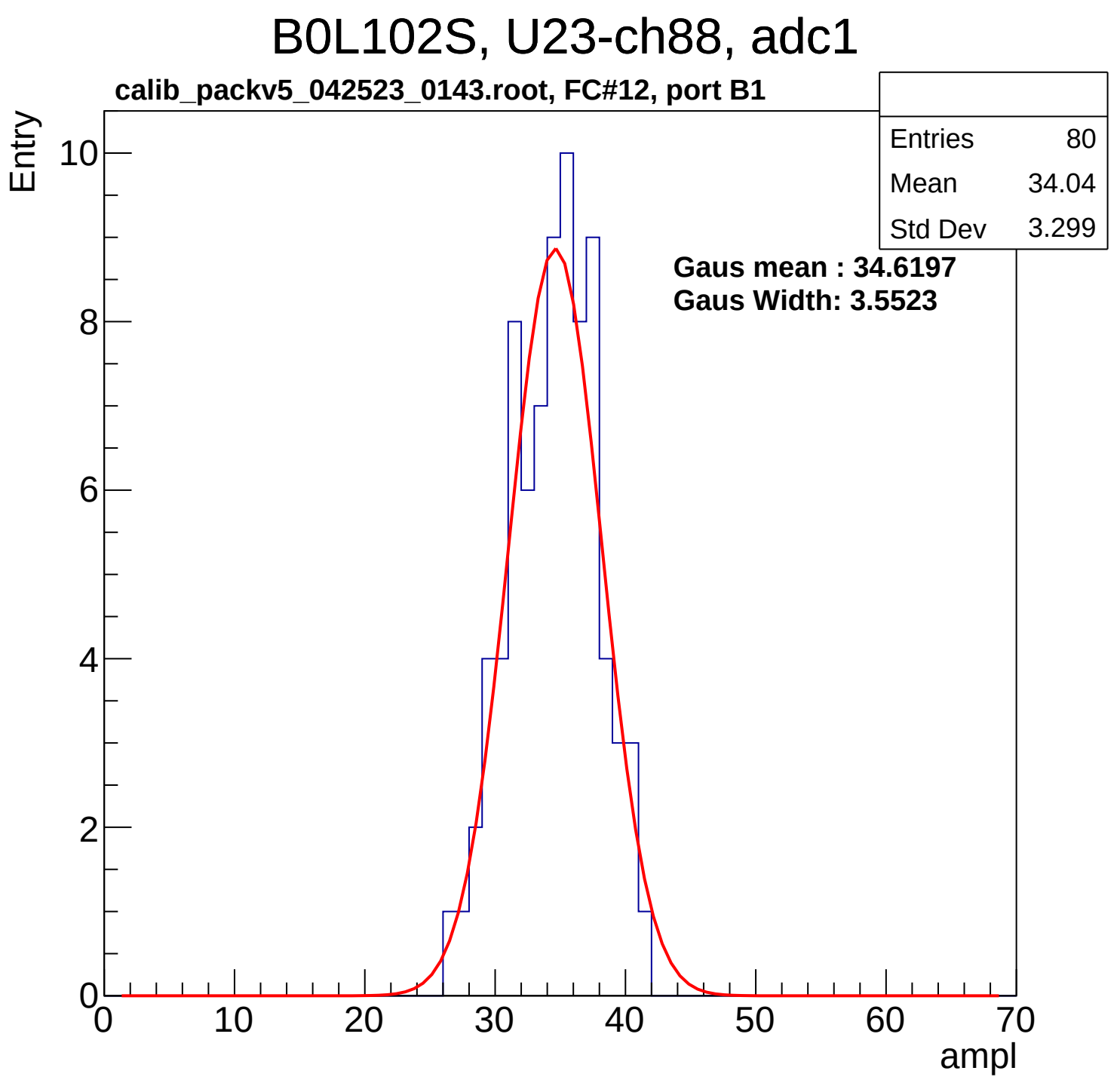
B0L102S, U23-ch88, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 34.04
Std Dev 3.299
Gaus mean : 34.6197
Gaus Width: 3.5523
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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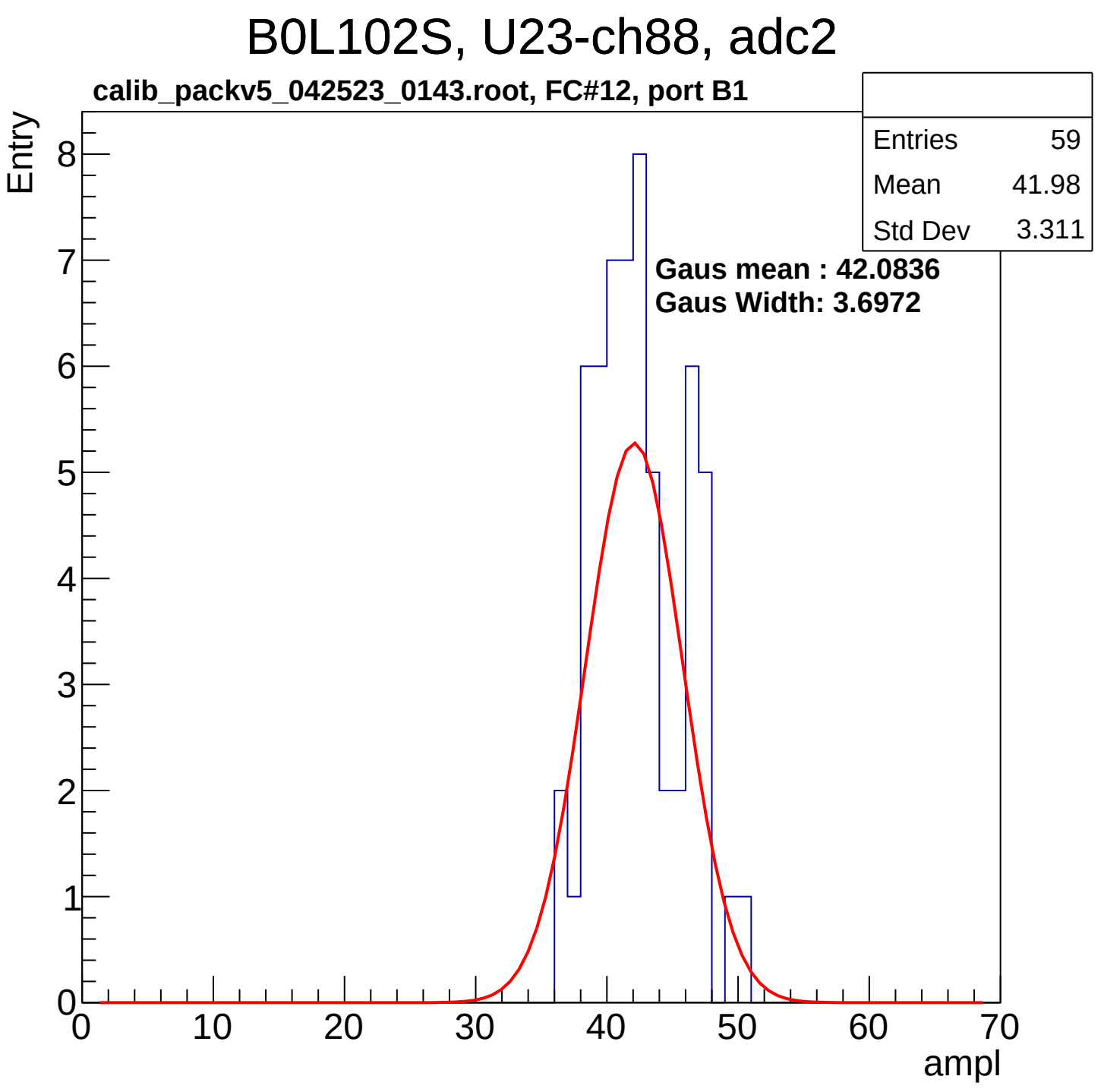
B0L102S, U23-ch88, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 41.98
Std Dev 3.311
Gaus mean : 42.0836
Gaus Width: 3.6972
Entry
ampl
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1
2
3
4
5
6
7
8
0
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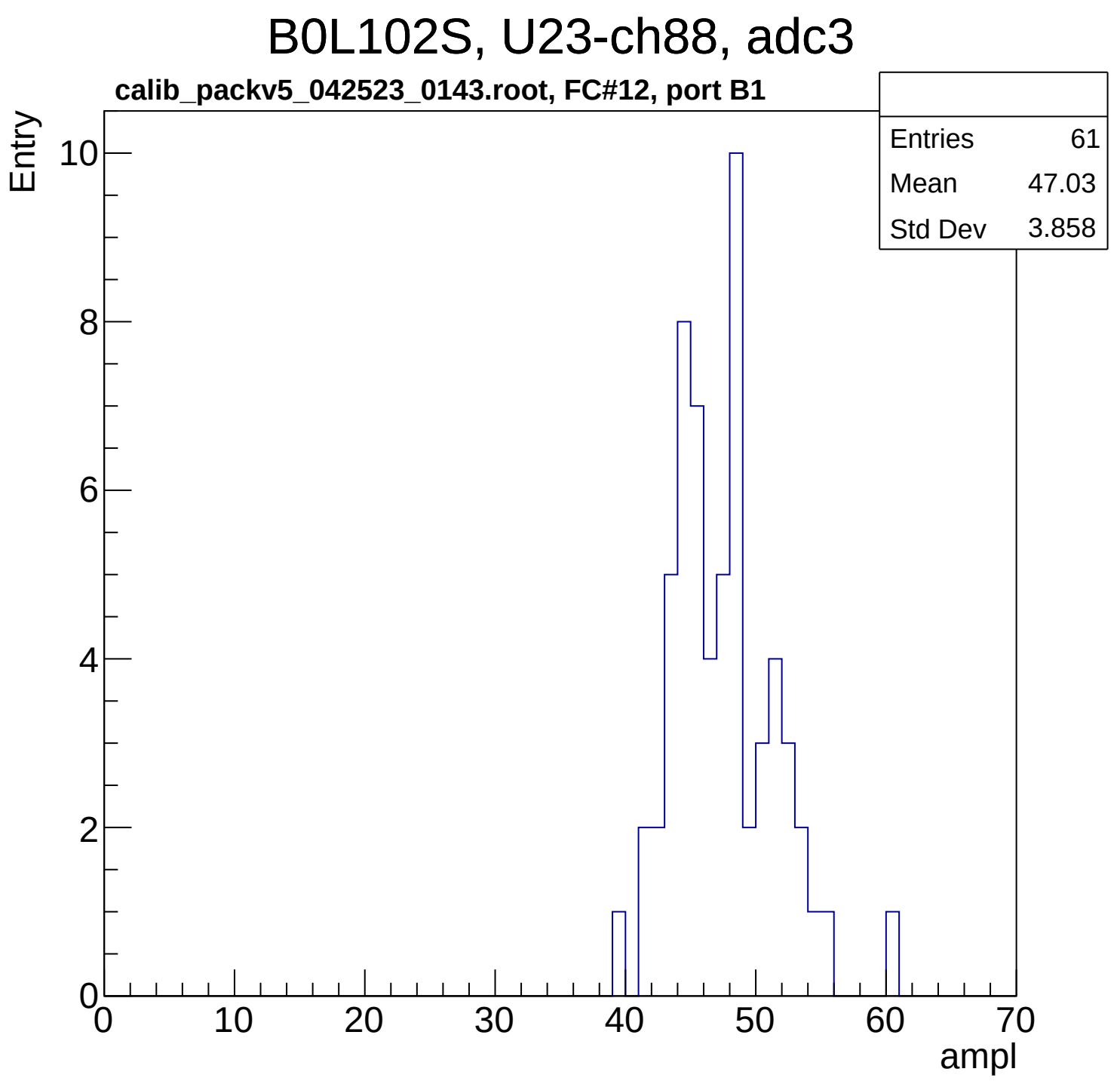
B0L102S, U23-ch88, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 47.03
Std Dev 3.858
Entry
ampl
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2
4
6
8
10
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40
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60
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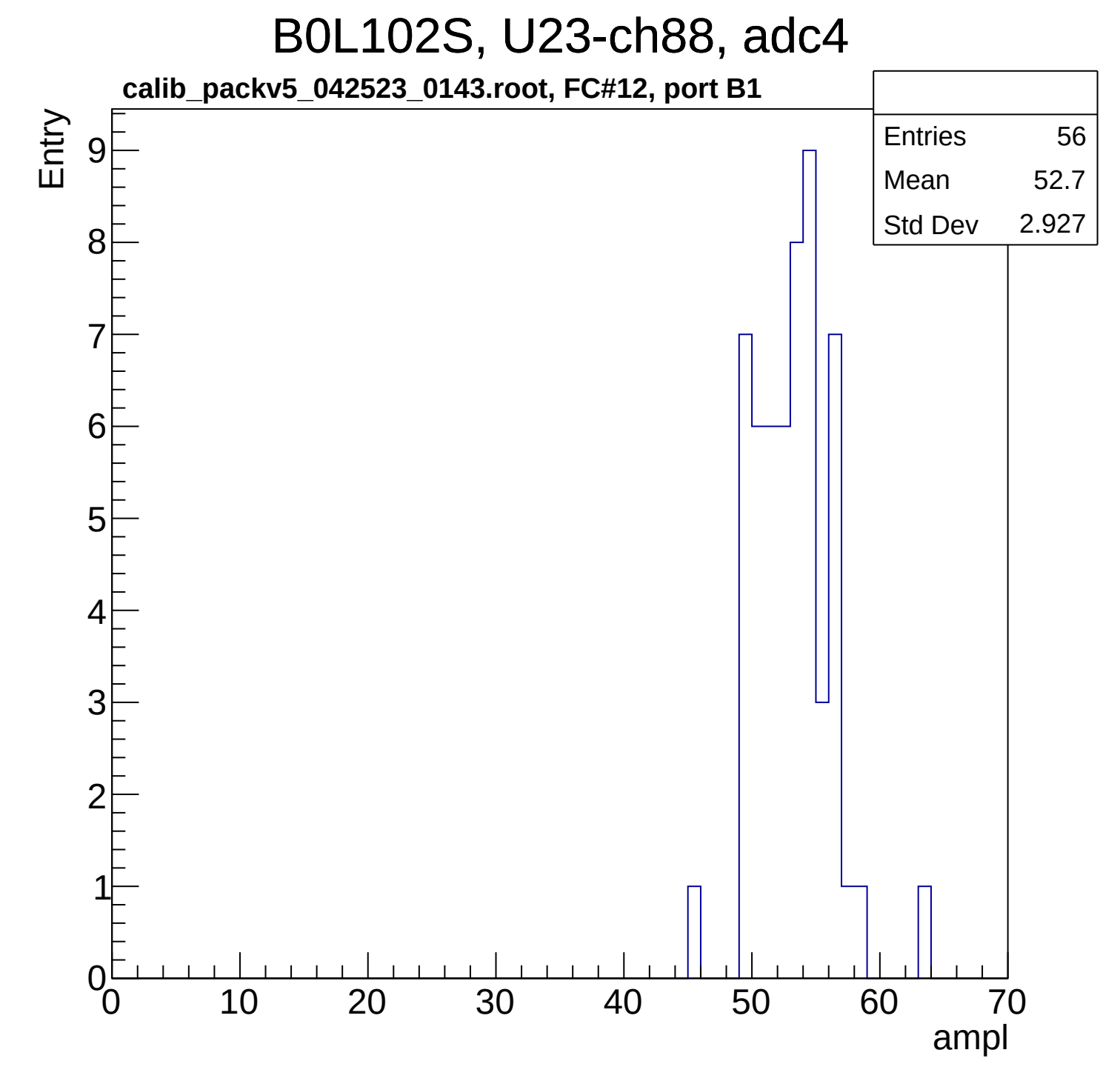
B0L102S, U23-ch88, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 52.7
Std Dev 2.927
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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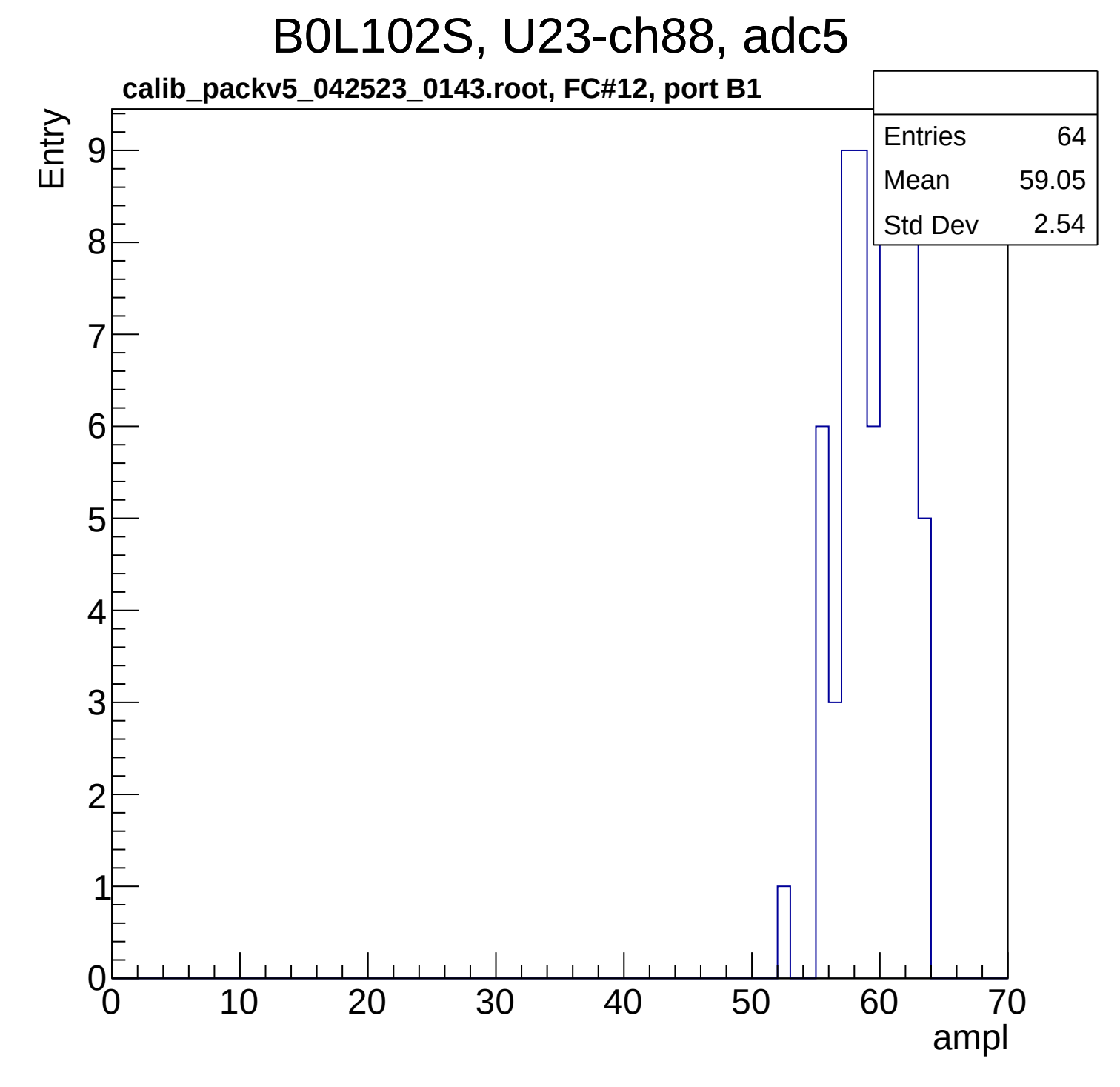
B0L102S, U23-ch88, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 59.05
Std Dev 2.54
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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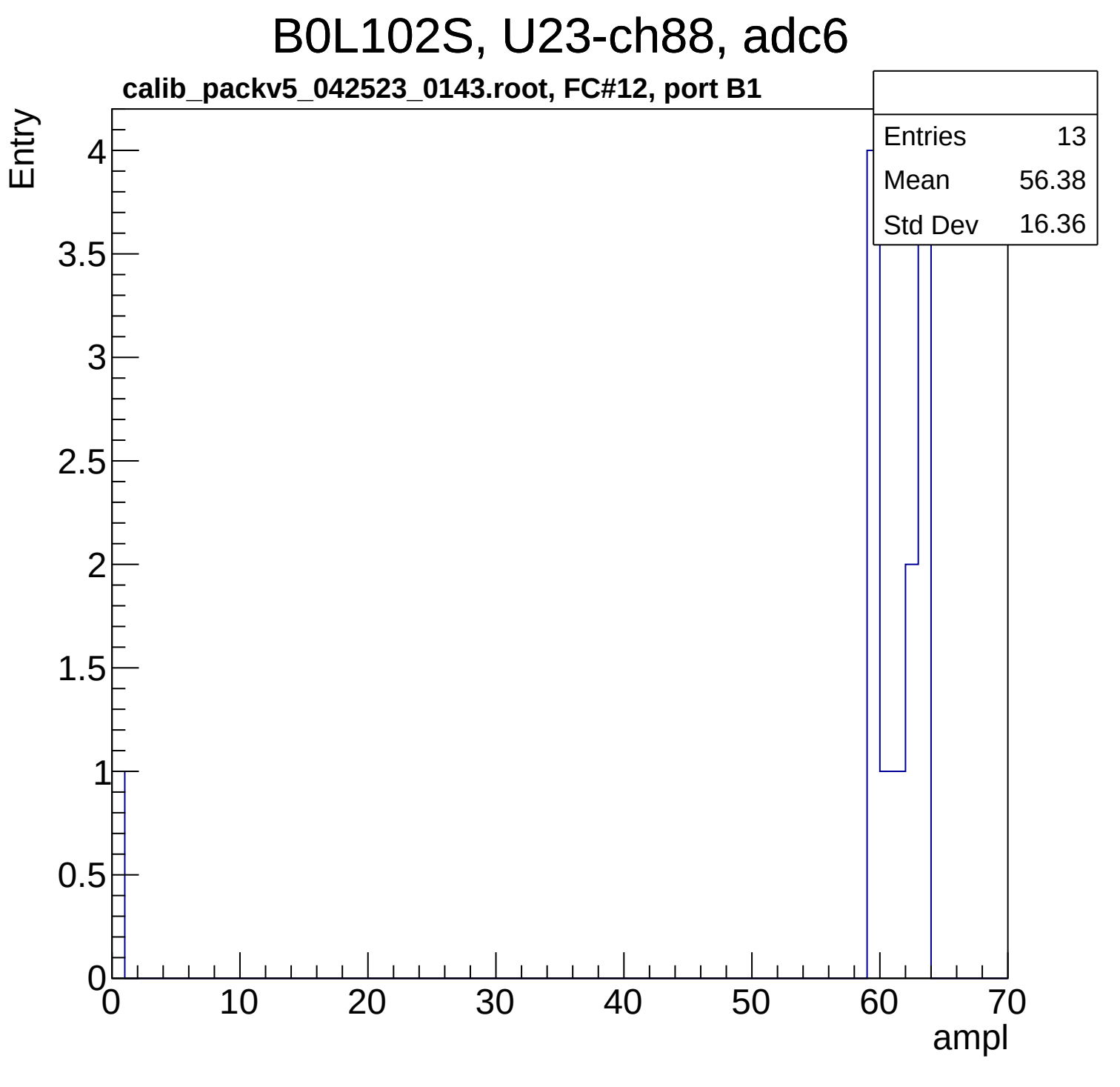
60
70


B0L102S, U23-ch88, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.38
Std Dev 16.36
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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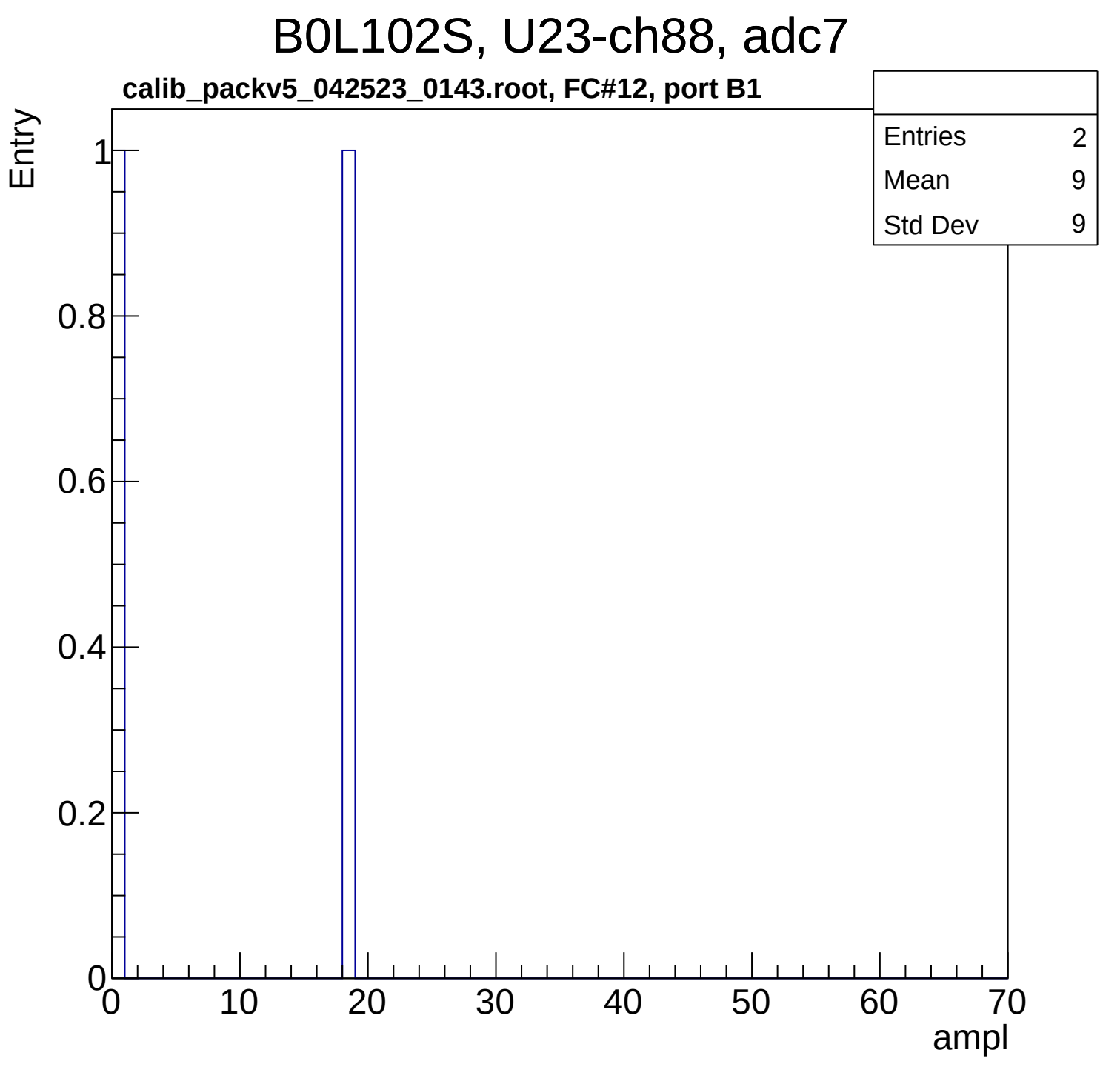
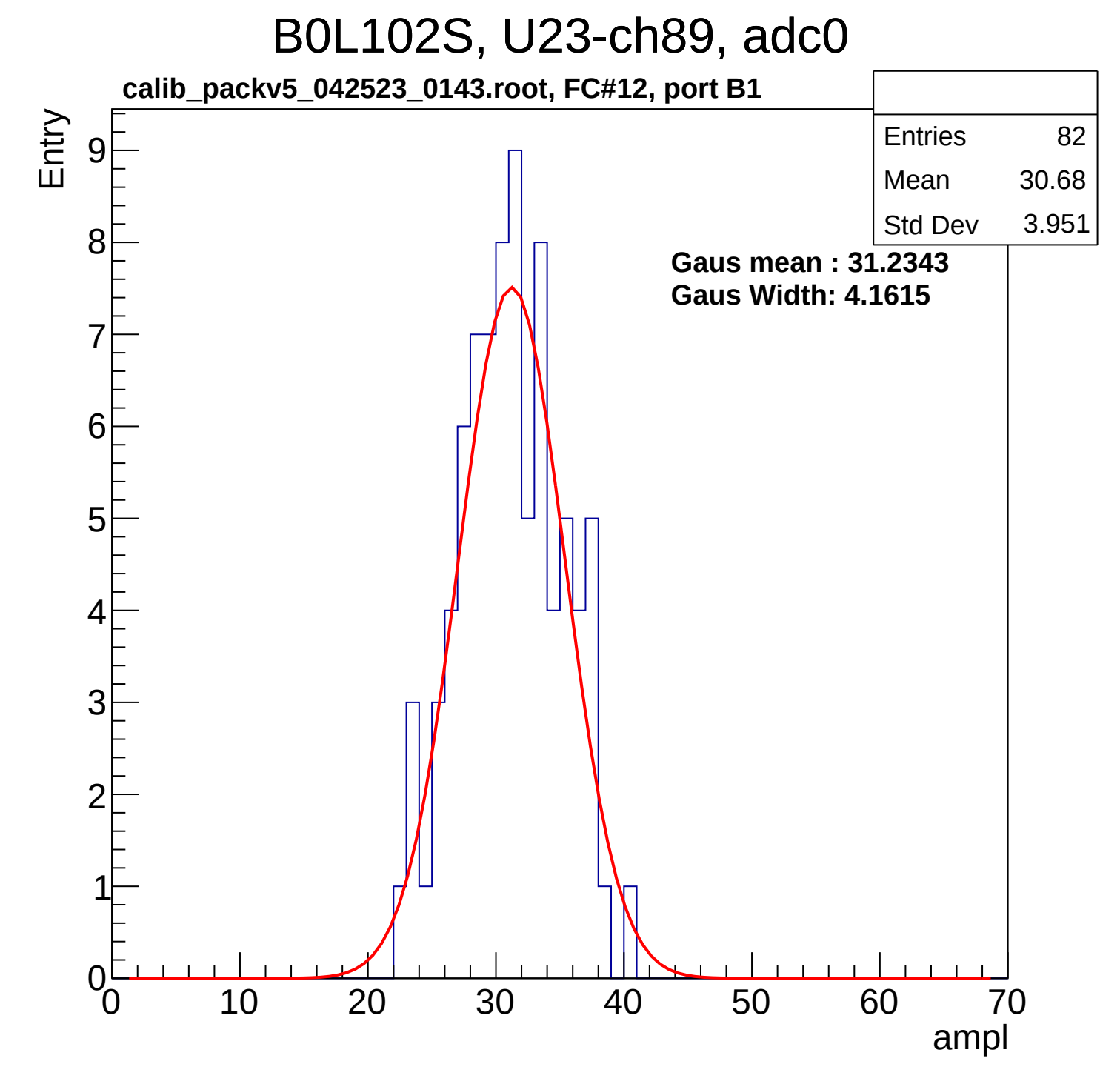

B0L102S, U23-ch88, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 9
Std Dev 9
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch89, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 30.68
Std Dev 3.951
Gaus mean : 31.2343
Gaus Width: 4.1615
Entry
ampl
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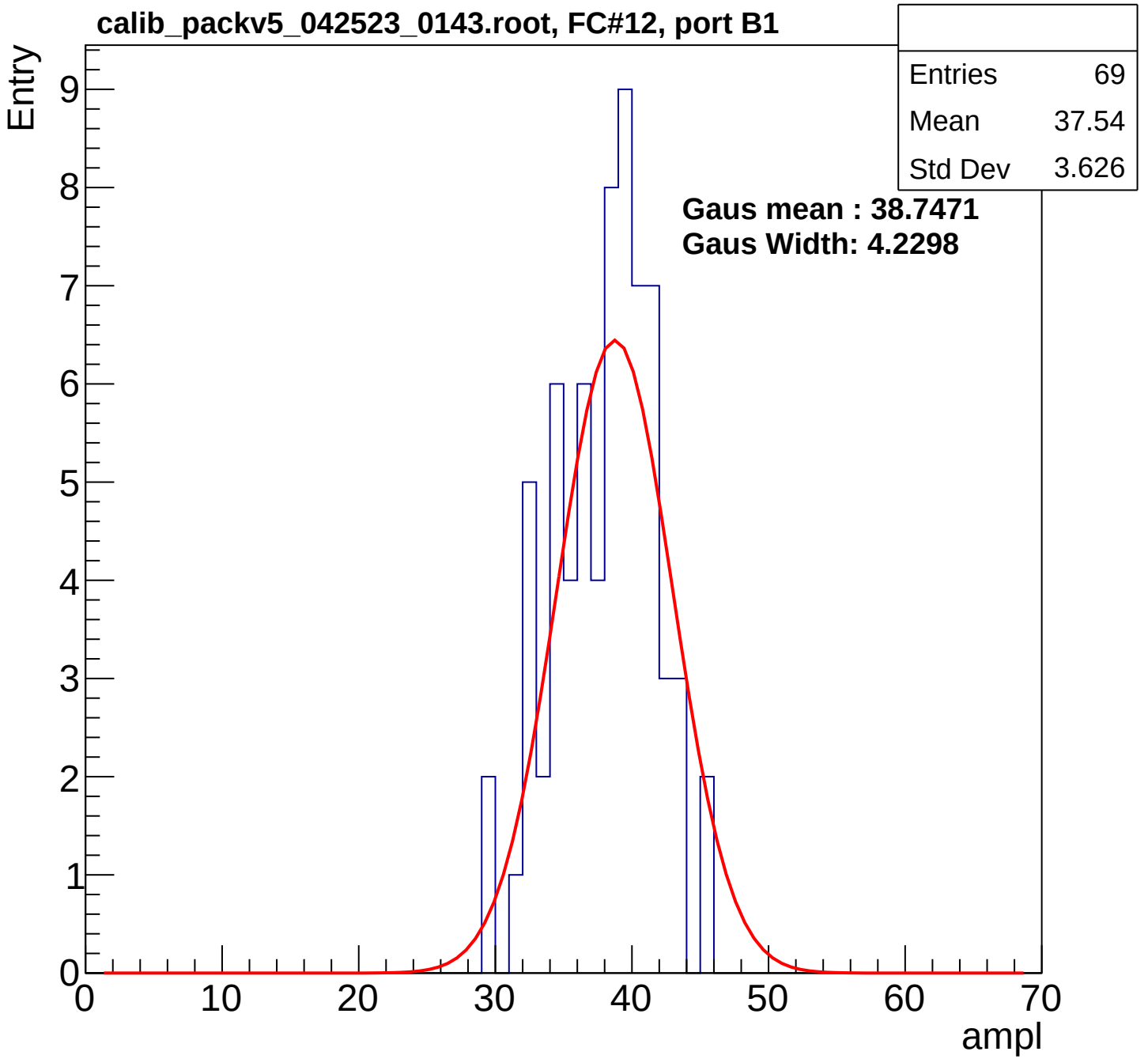
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch89, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 37.54
Std Dev 3.626
Gaus mean : 38.7471
Gaus Width: 4.2298
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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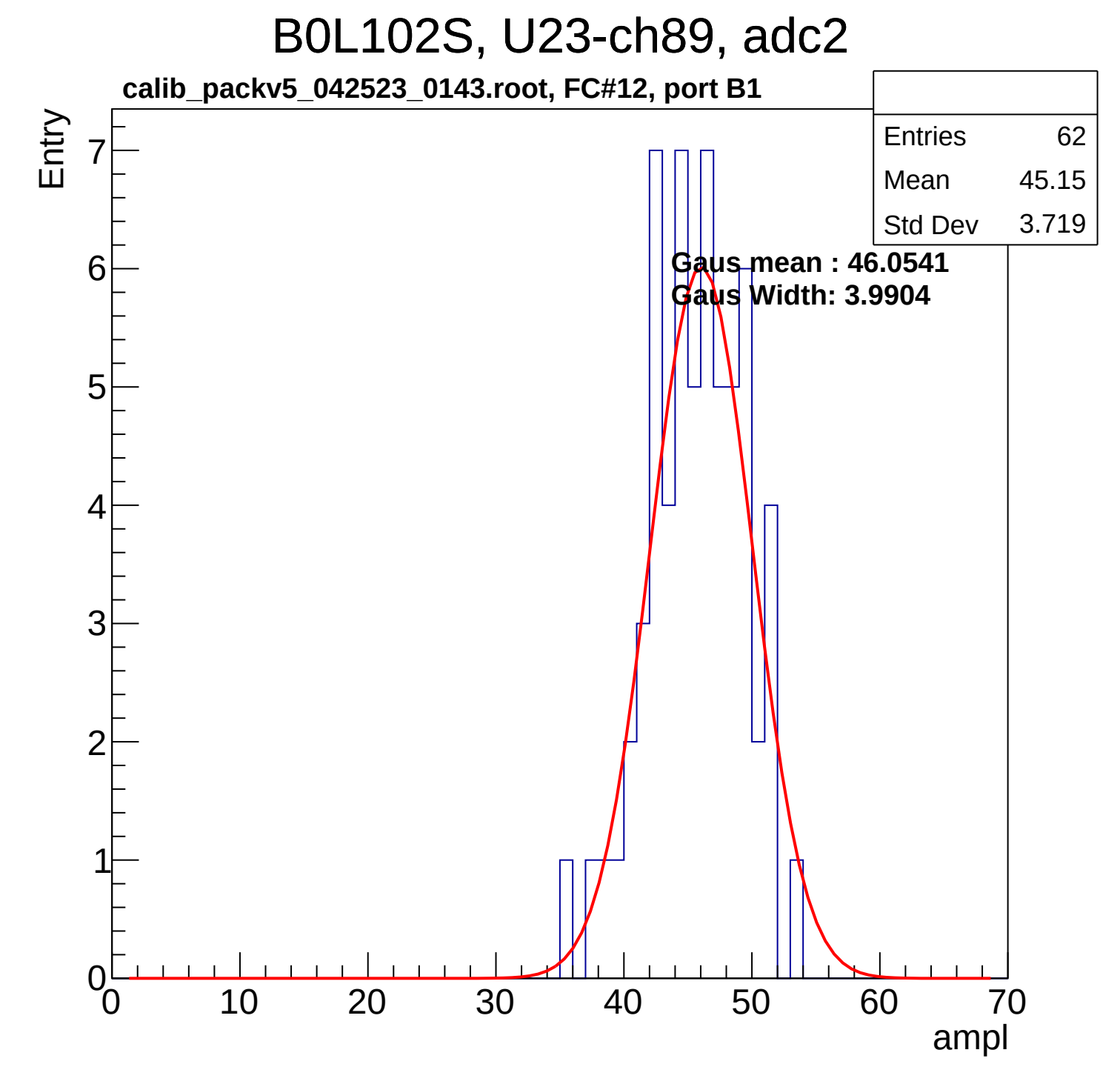
B0L102S, U23-ch89, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 45.15
Std Dev 3.719
Gaus mean : 46.0541
Gaus Width: 3.9904
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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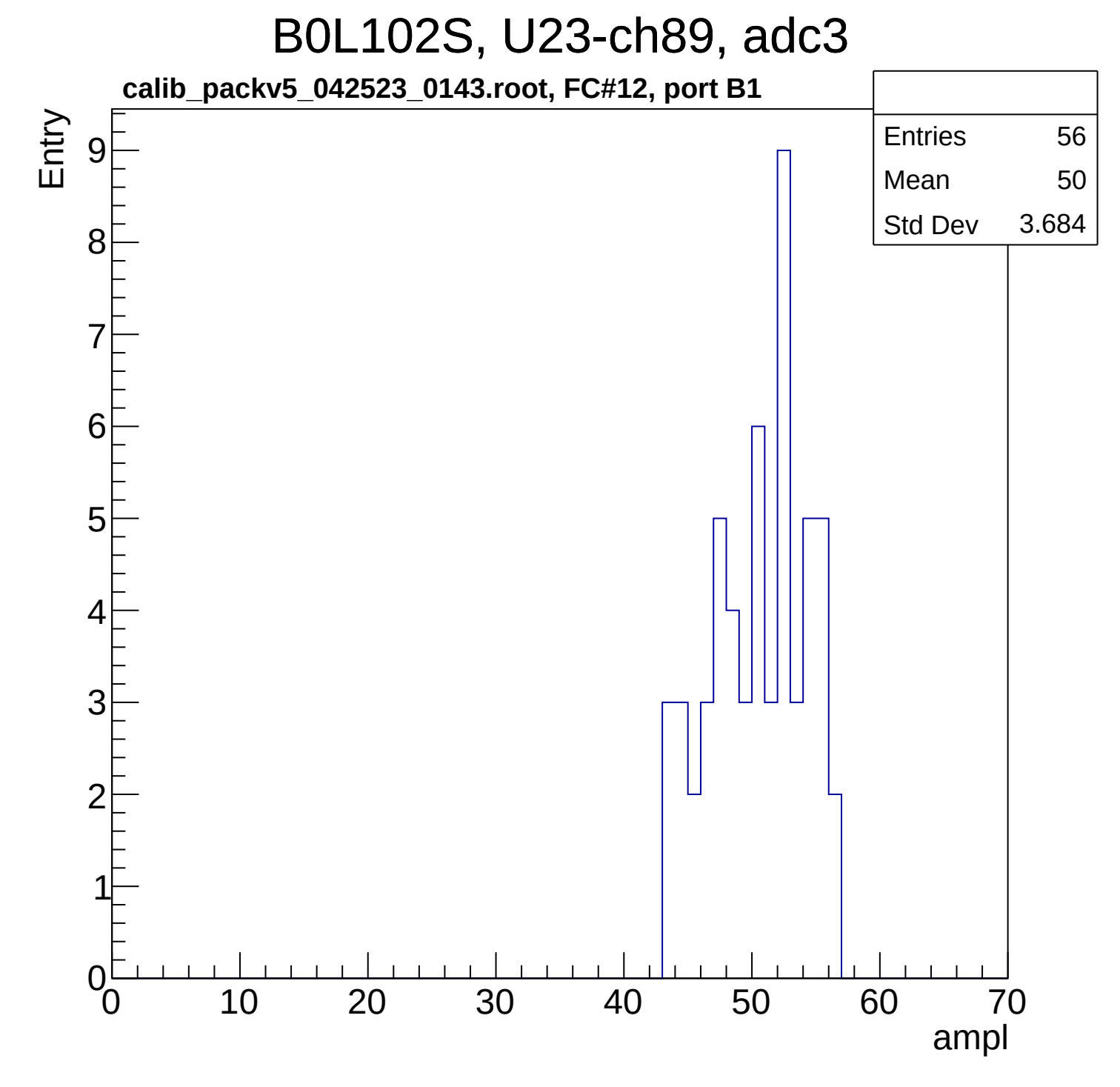
B0L102S, U23-ch89, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 50
Std Dev 3.684
Entry
ampl
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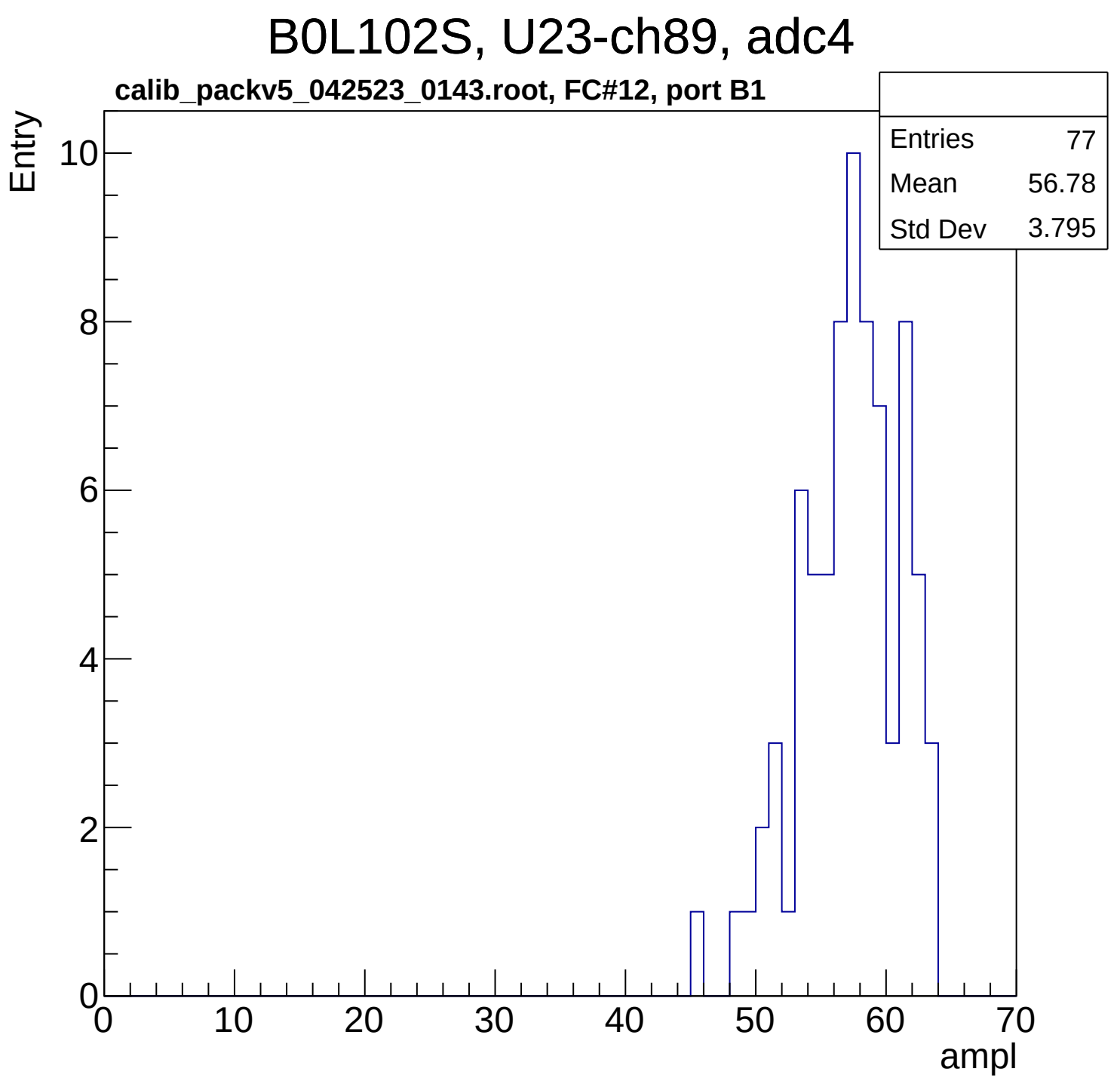
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch89, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 56.78
Std Dev 3.795
Entry
0
2
4
6
8
10
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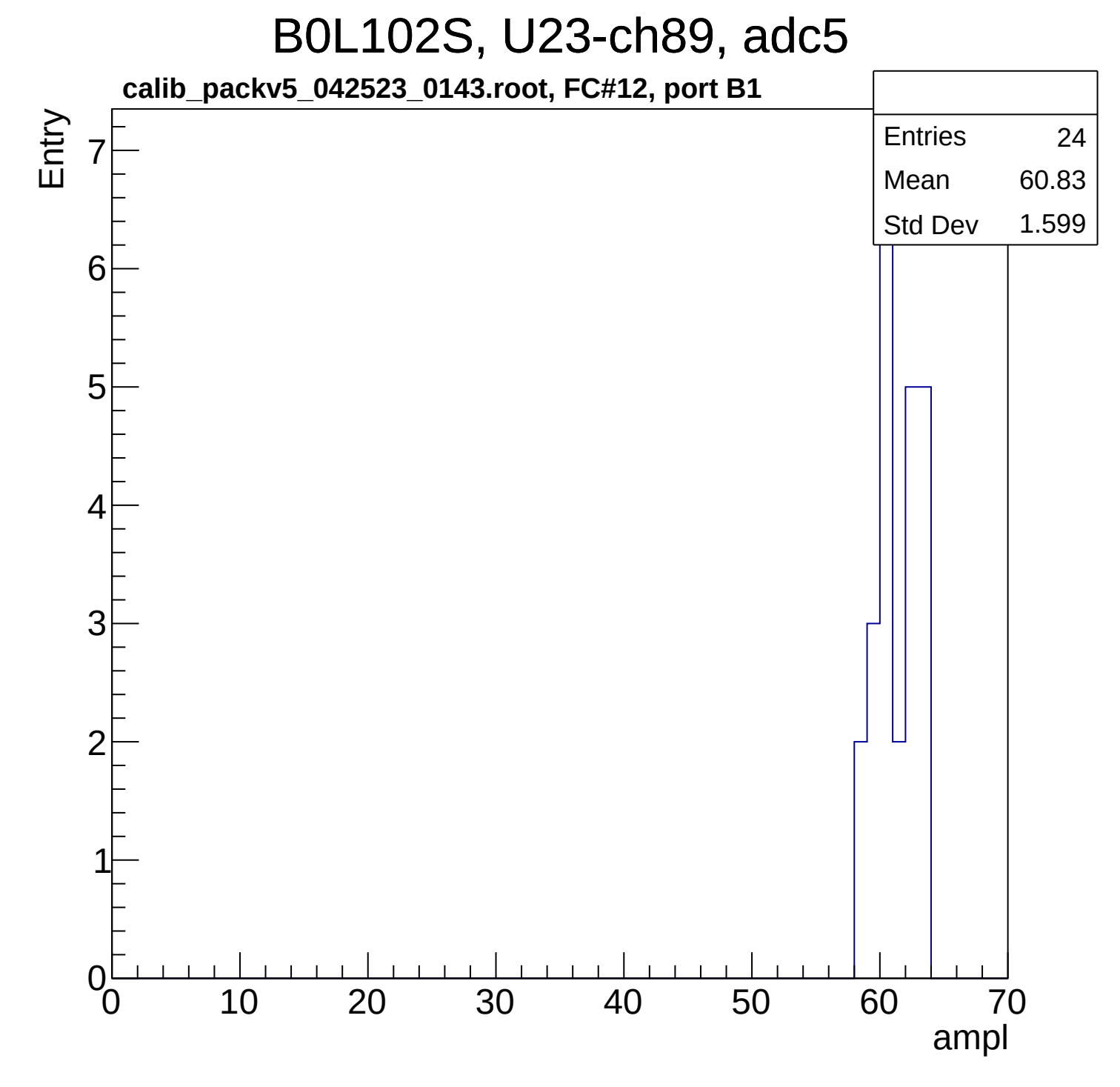
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch89, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 24
Mean 60.83
Std Dev 1.599
Entry
ampl
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1
2
3
4
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7
0
10
20
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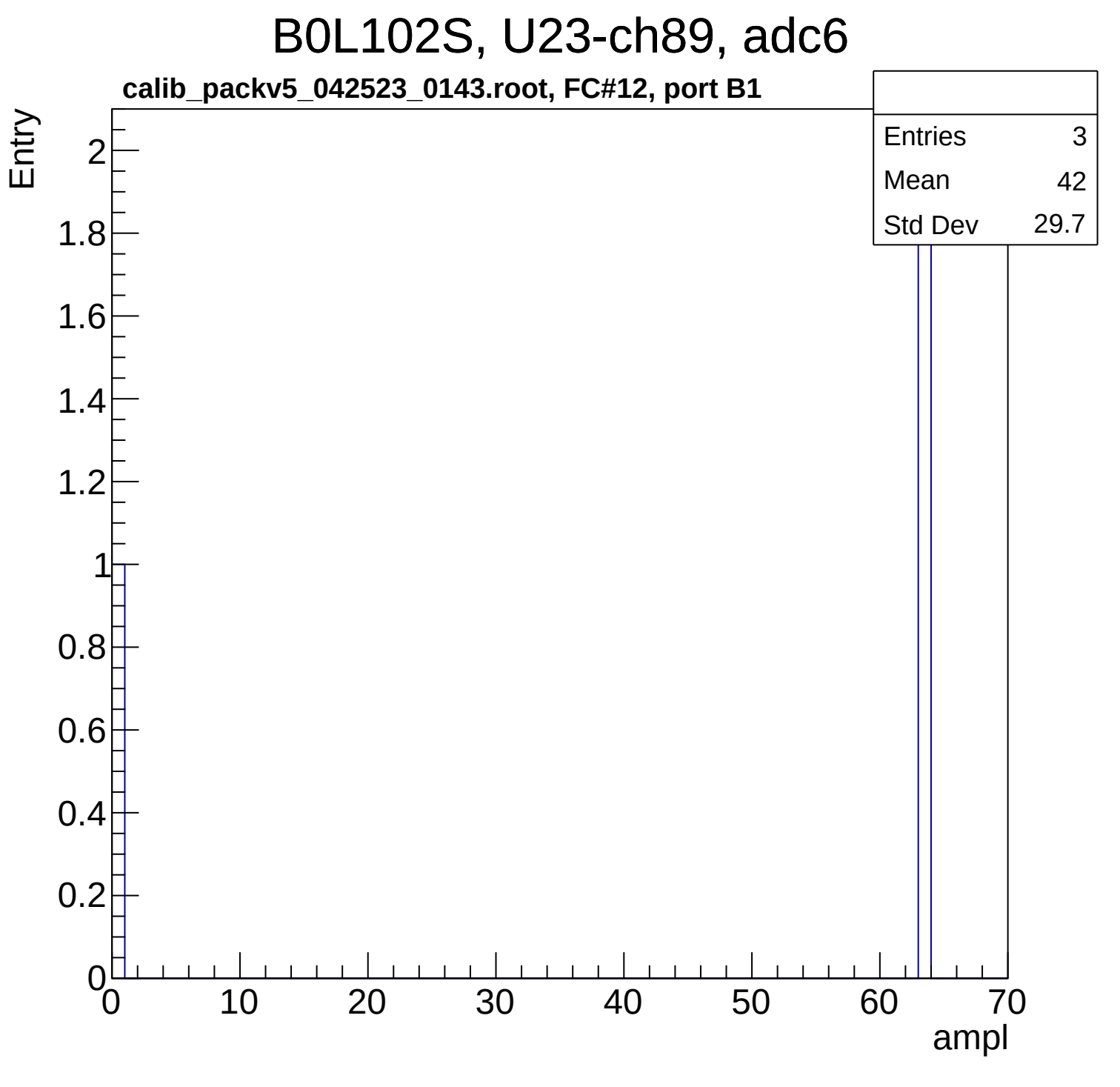
40
50
60
70


B0L102S, U23-ch89, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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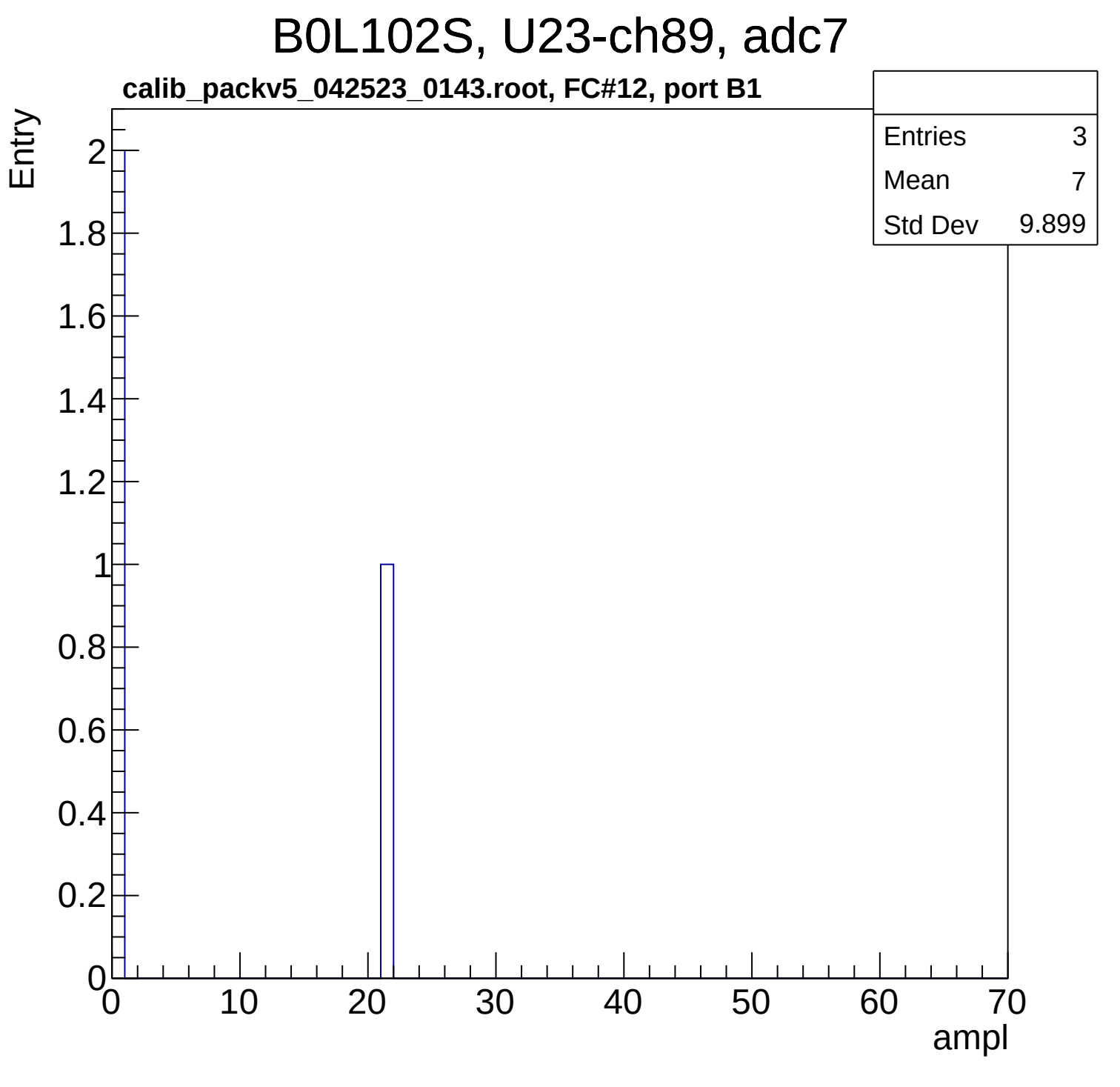
B0L102S, U23-ch89, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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40
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60
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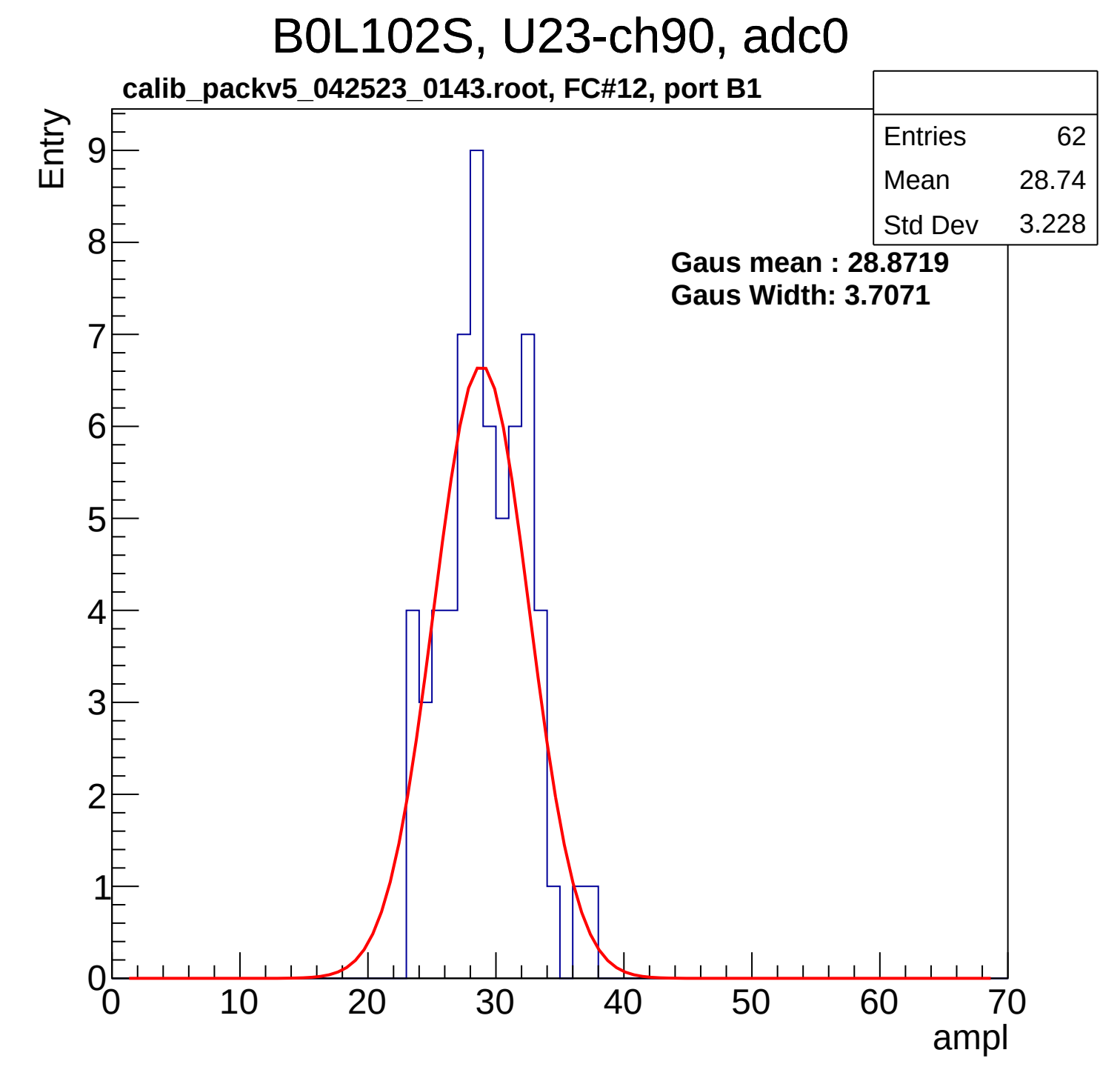
B0L102S, U23-ch90, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 28.74
Std Dev 3.228
Gaus mean : 28.8719
Gaus Width: 3.7071
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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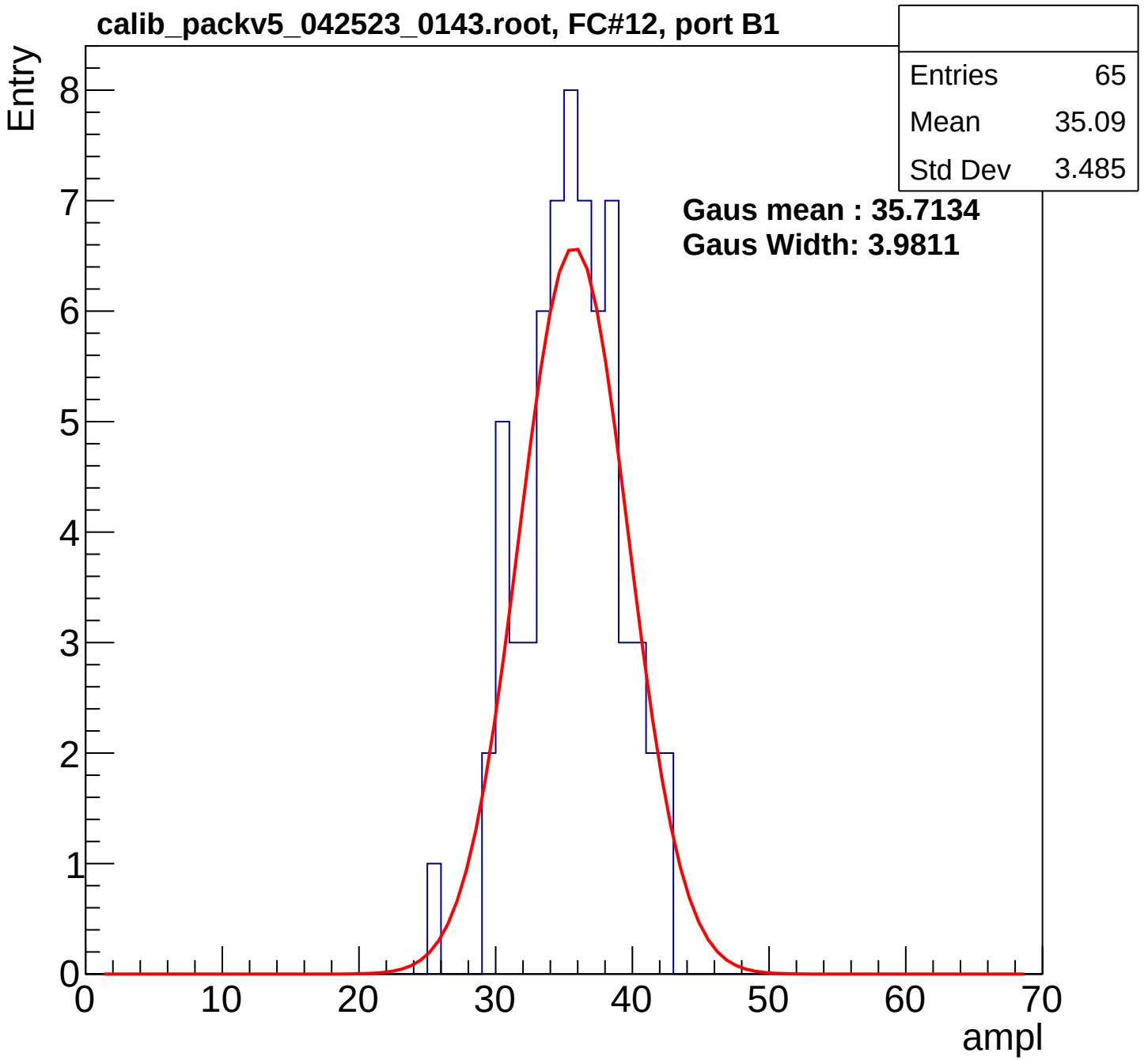
B0L102S, U23-ch90, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 35.09
Std Dev 3.485
Gaus mean : 35.7134
Gaus Width: 3.9811
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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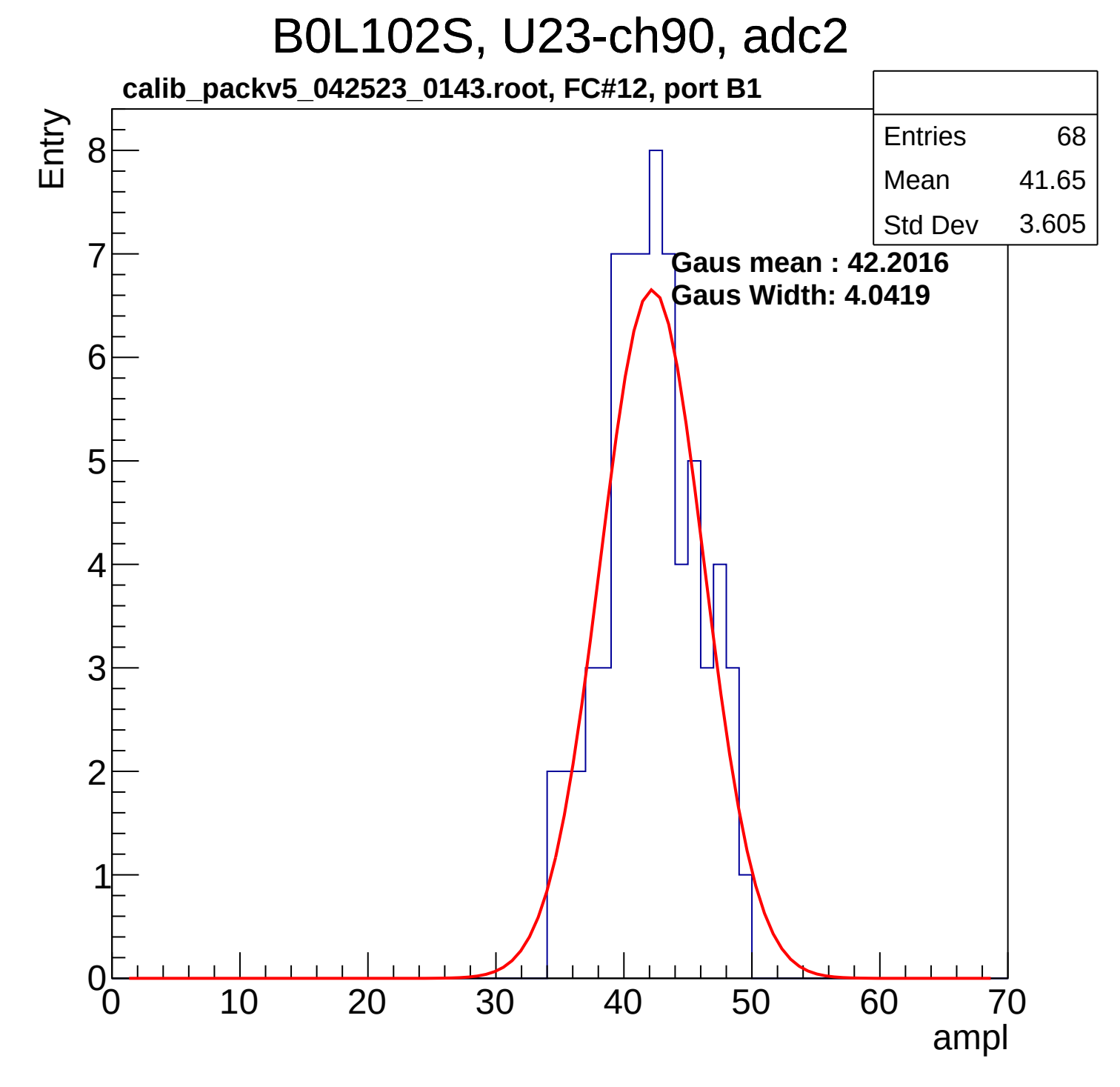
B0L102S, U23-ch90, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.65
Std Dev 3.605
Gaus mean : 42.2016
Gaus Width: 4.0419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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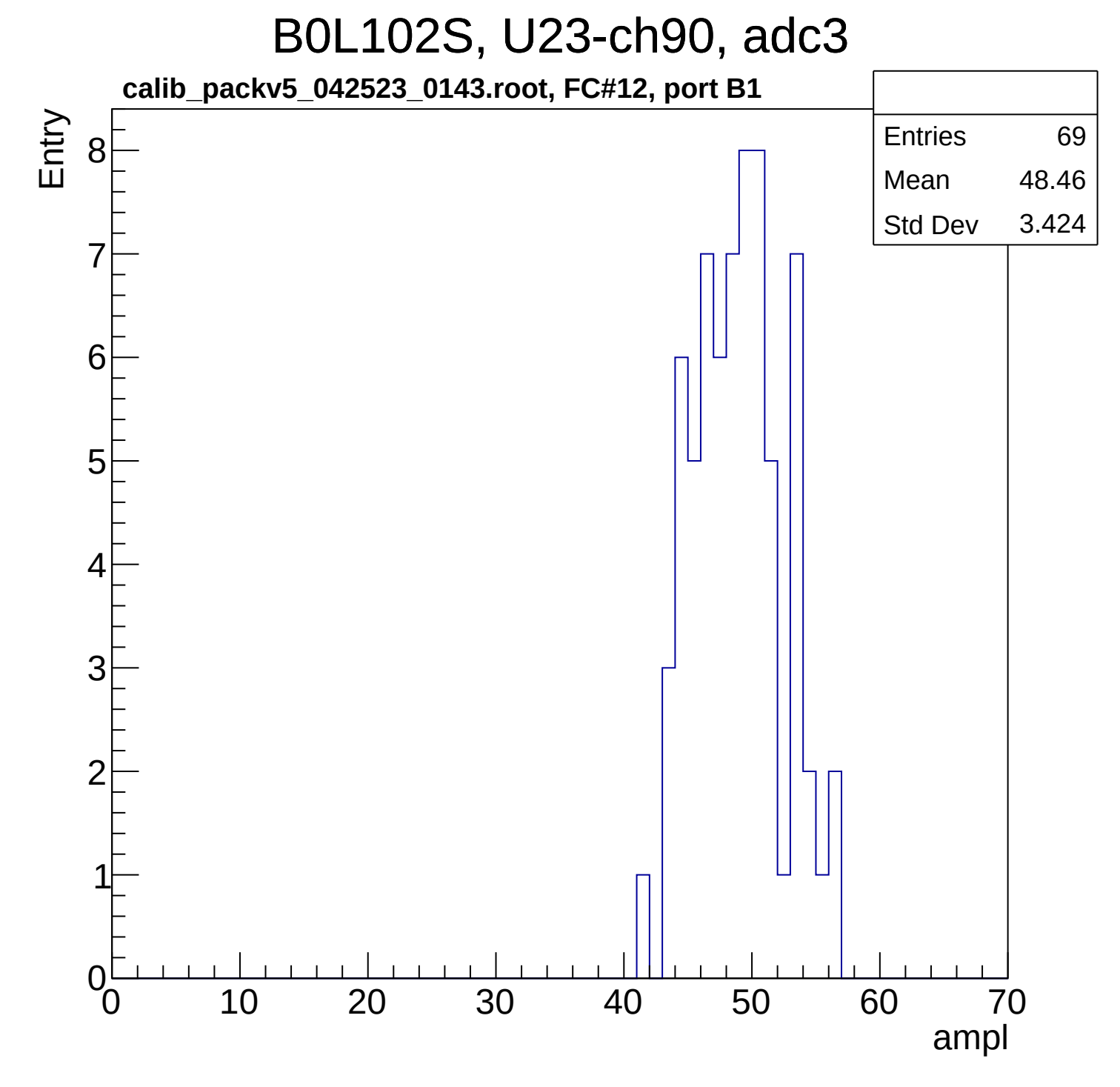
B0L102S, U23-ch90, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 48.46
Std Dev 3.424
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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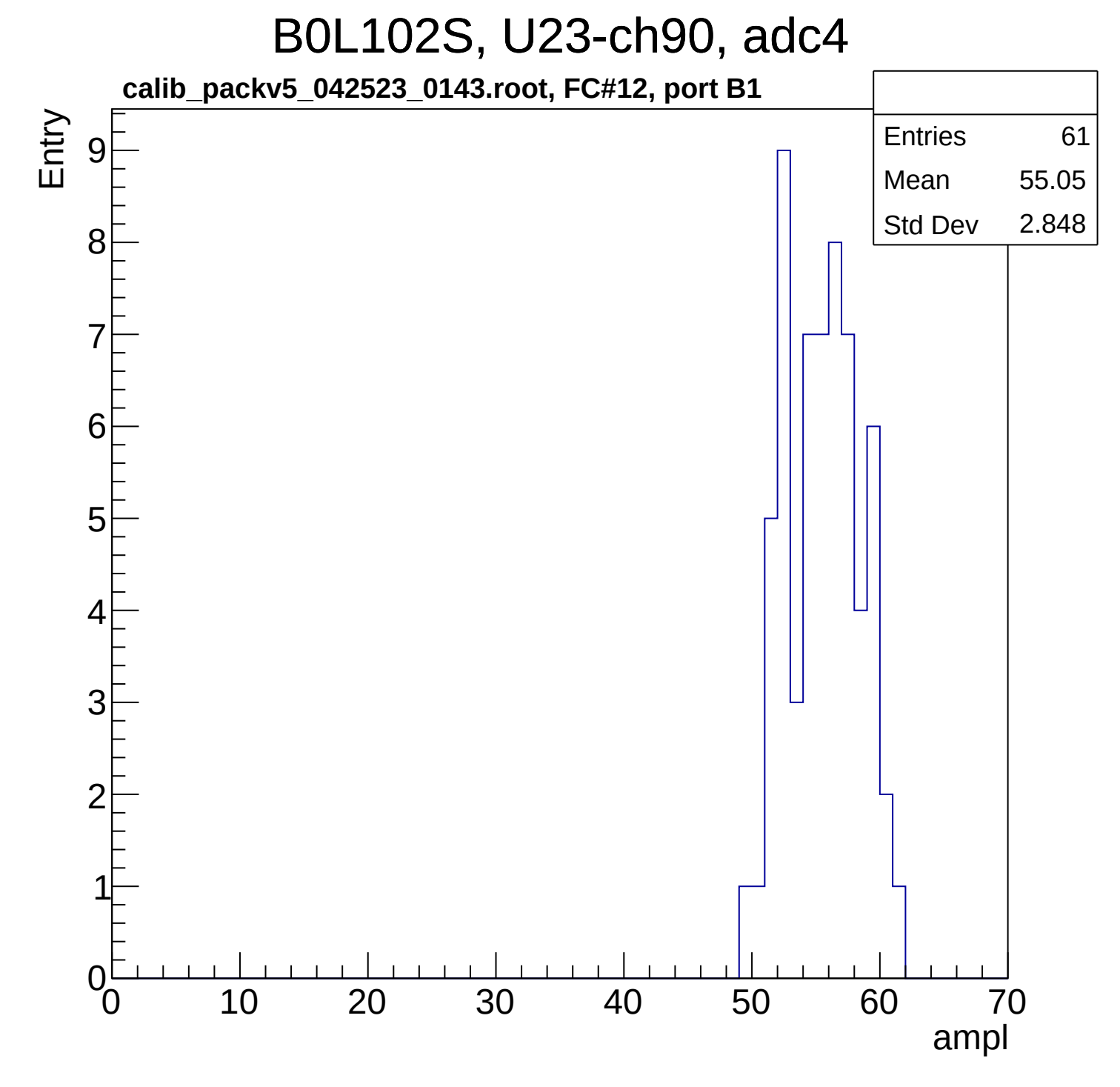
B0L102S, U23-ch90, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 55.05
Std Dev 2.848
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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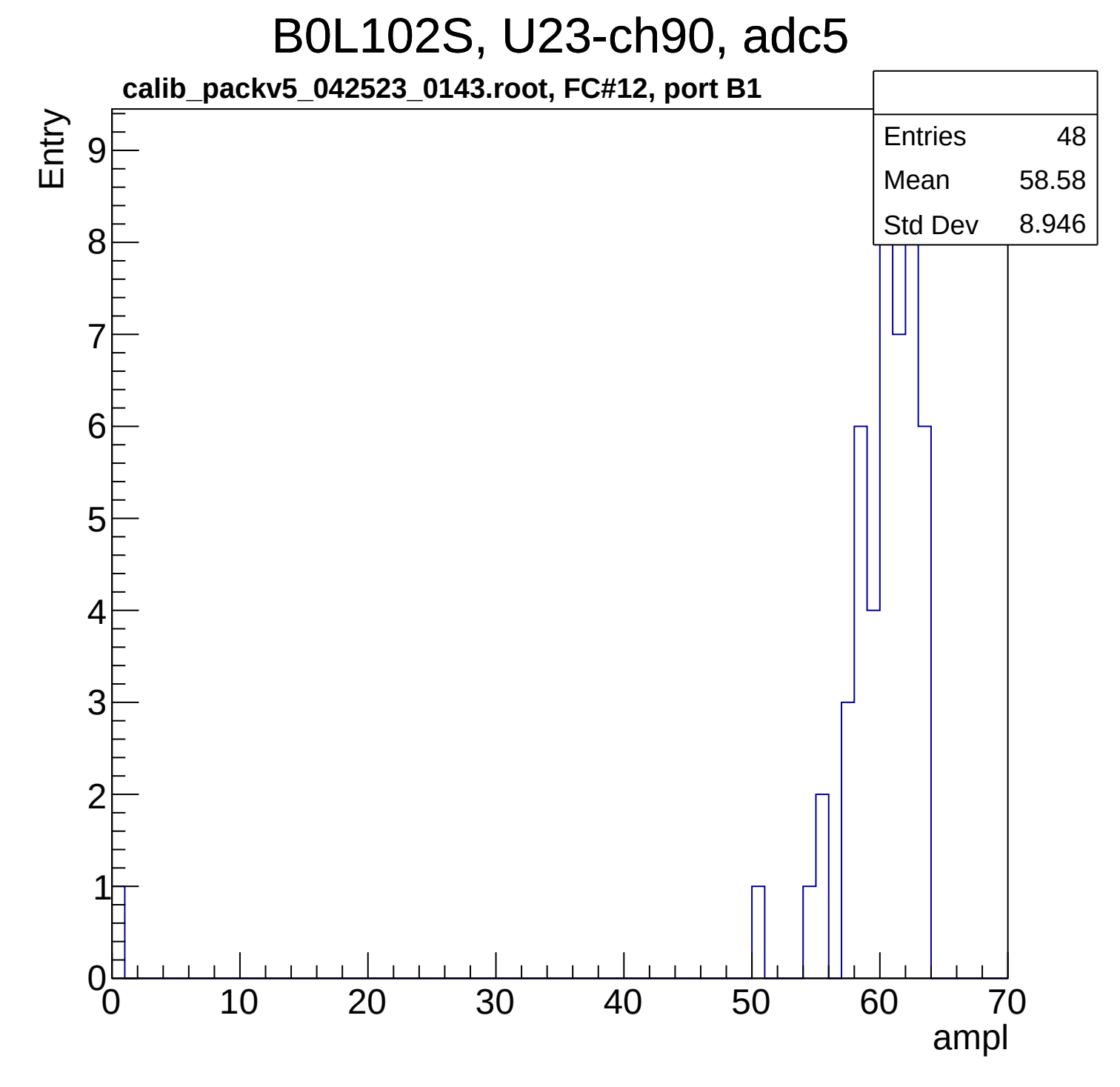
B0L102S, U23-ch90, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.58
Std Dev 8.946
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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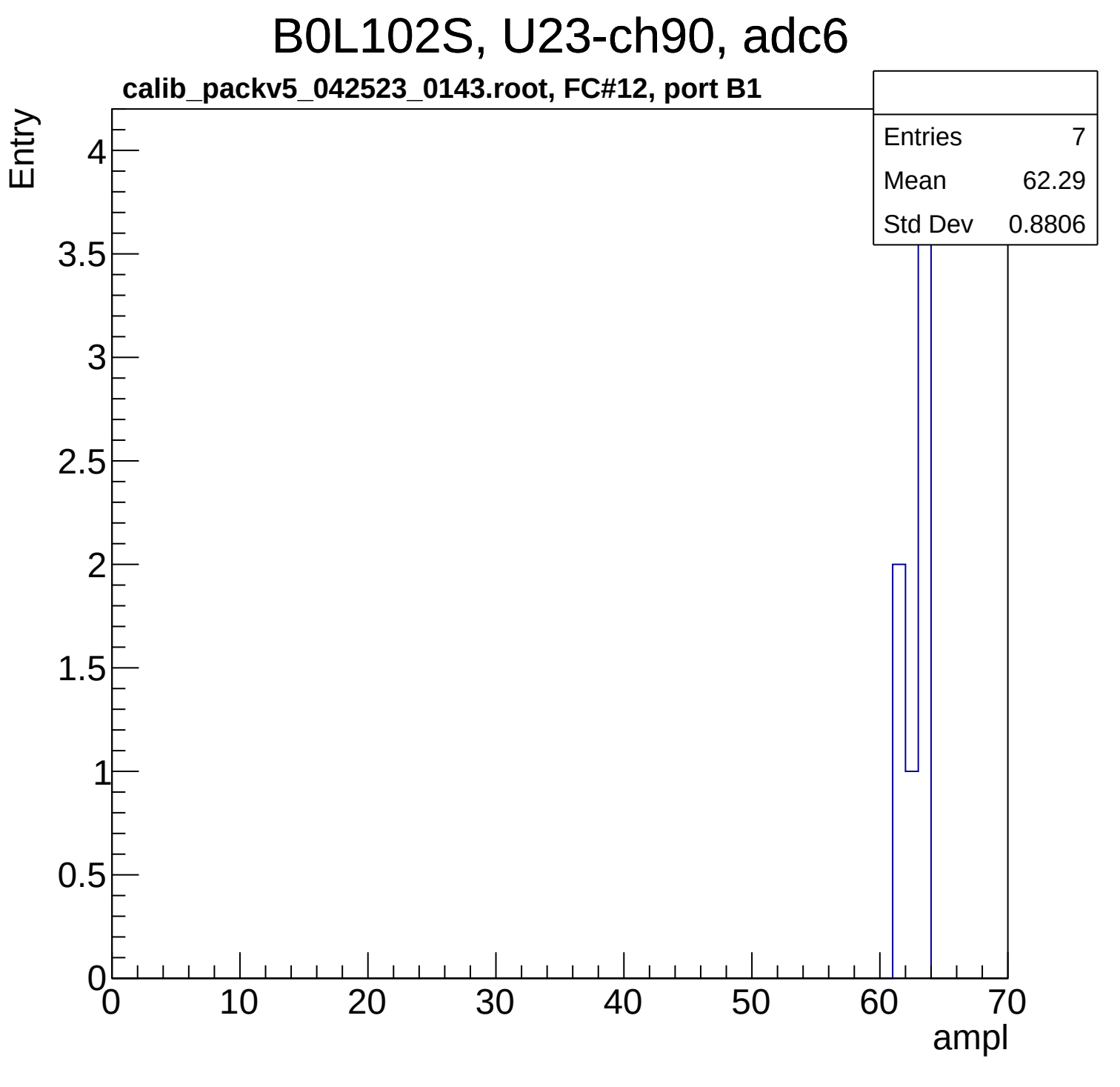
B0L102S, U23-ch90, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62.29
Std Dev 0.8806
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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60
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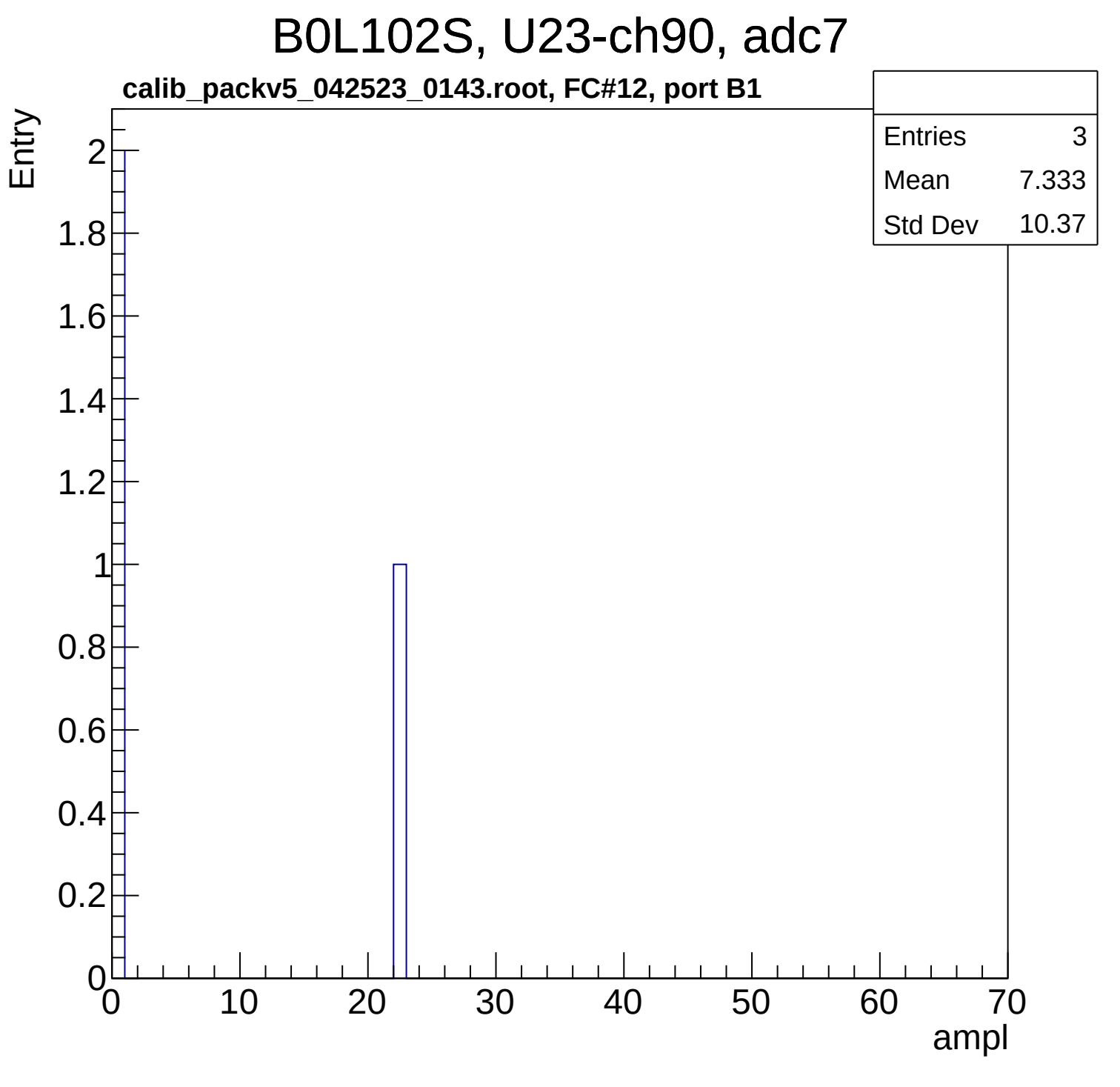
B0L102S, U23-ch90, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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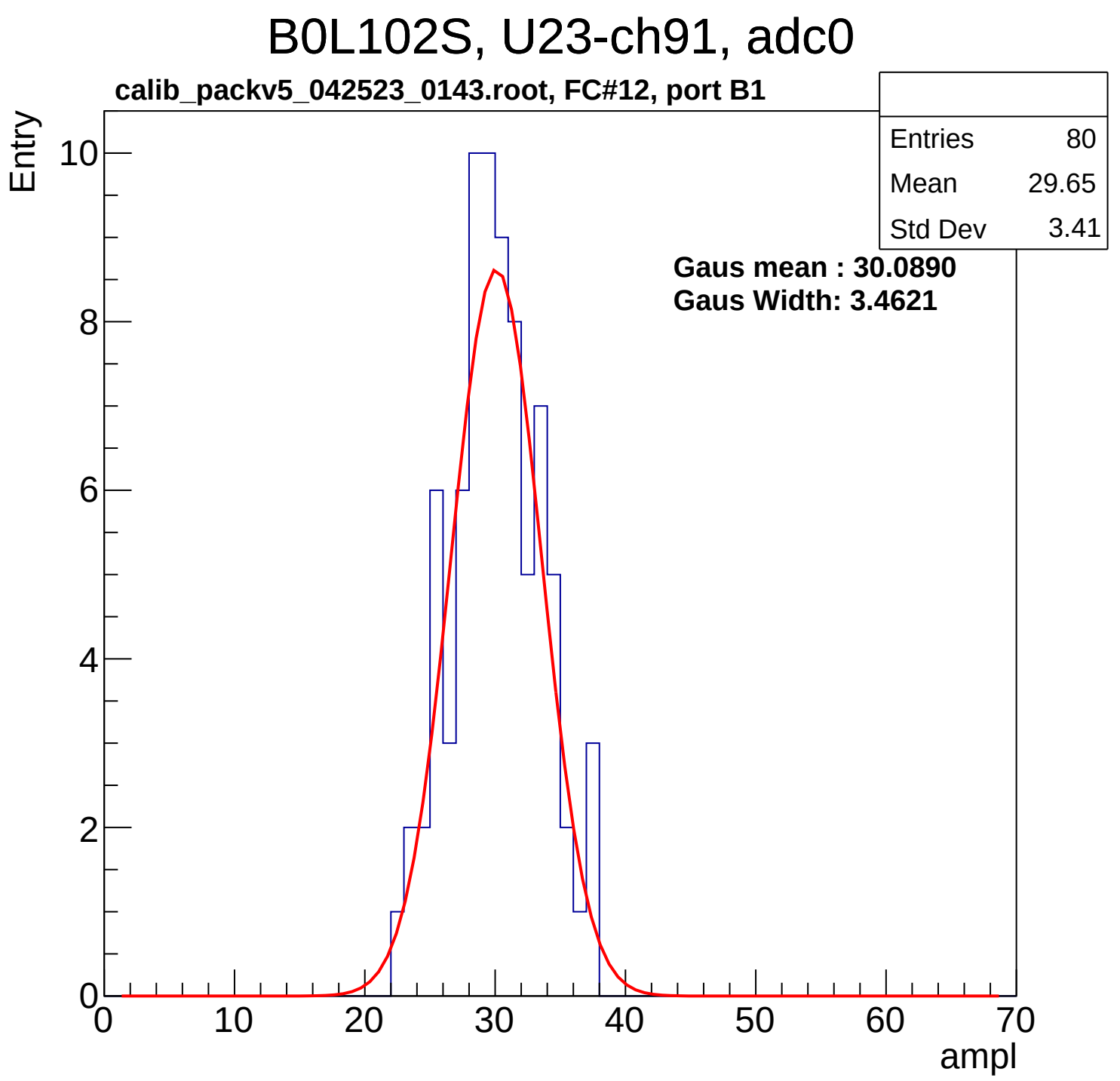
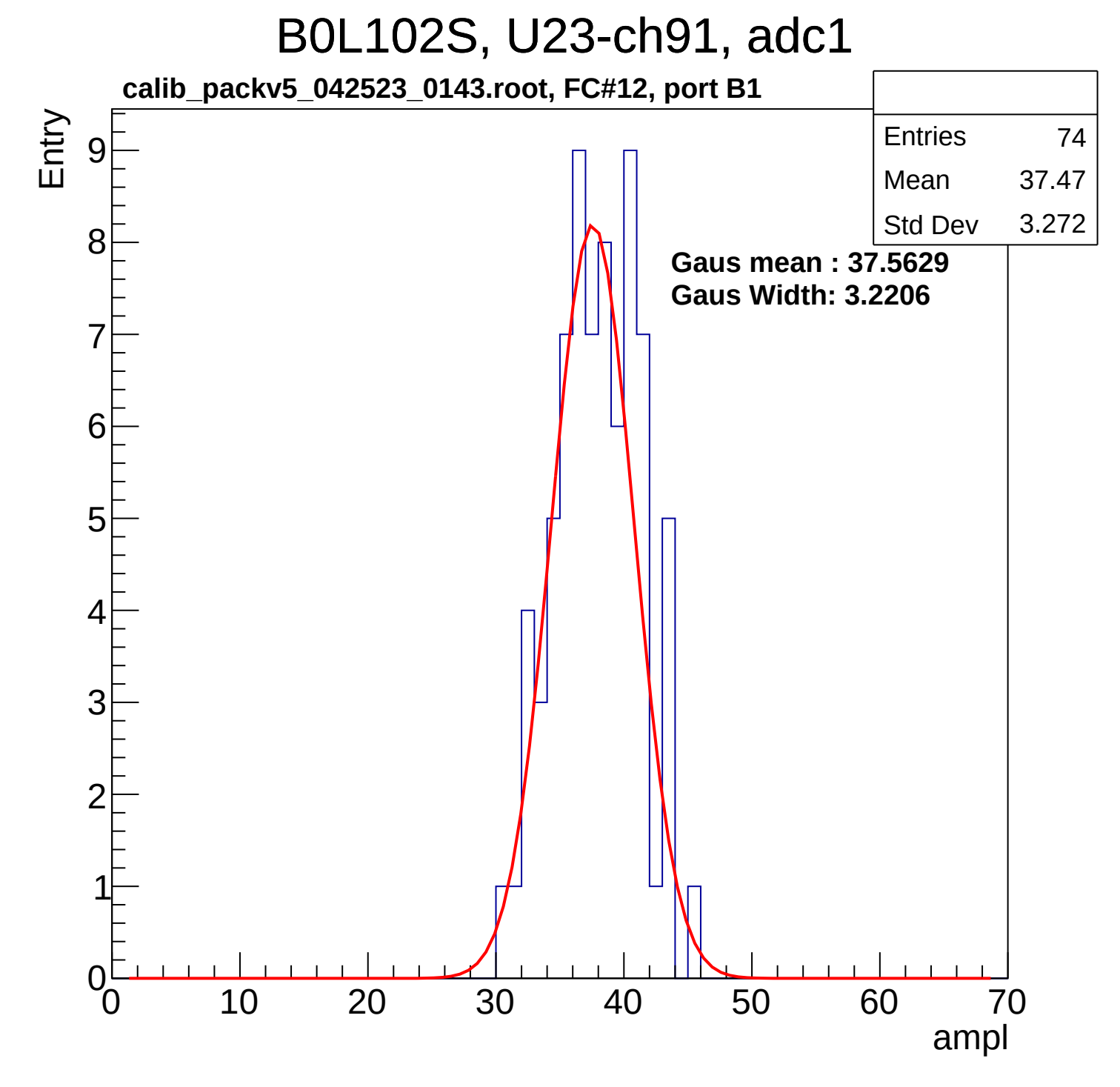

B0L102S, U23-ch91, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 29.65
Std Dev 3.41
Gaus mean : 30.0890
Gaus Width: 3.4621
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch91, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 37.47
Std Dev 3.272
Gaus mean : 37.5629
Gaus Width: 3.2206
Entry
ampl
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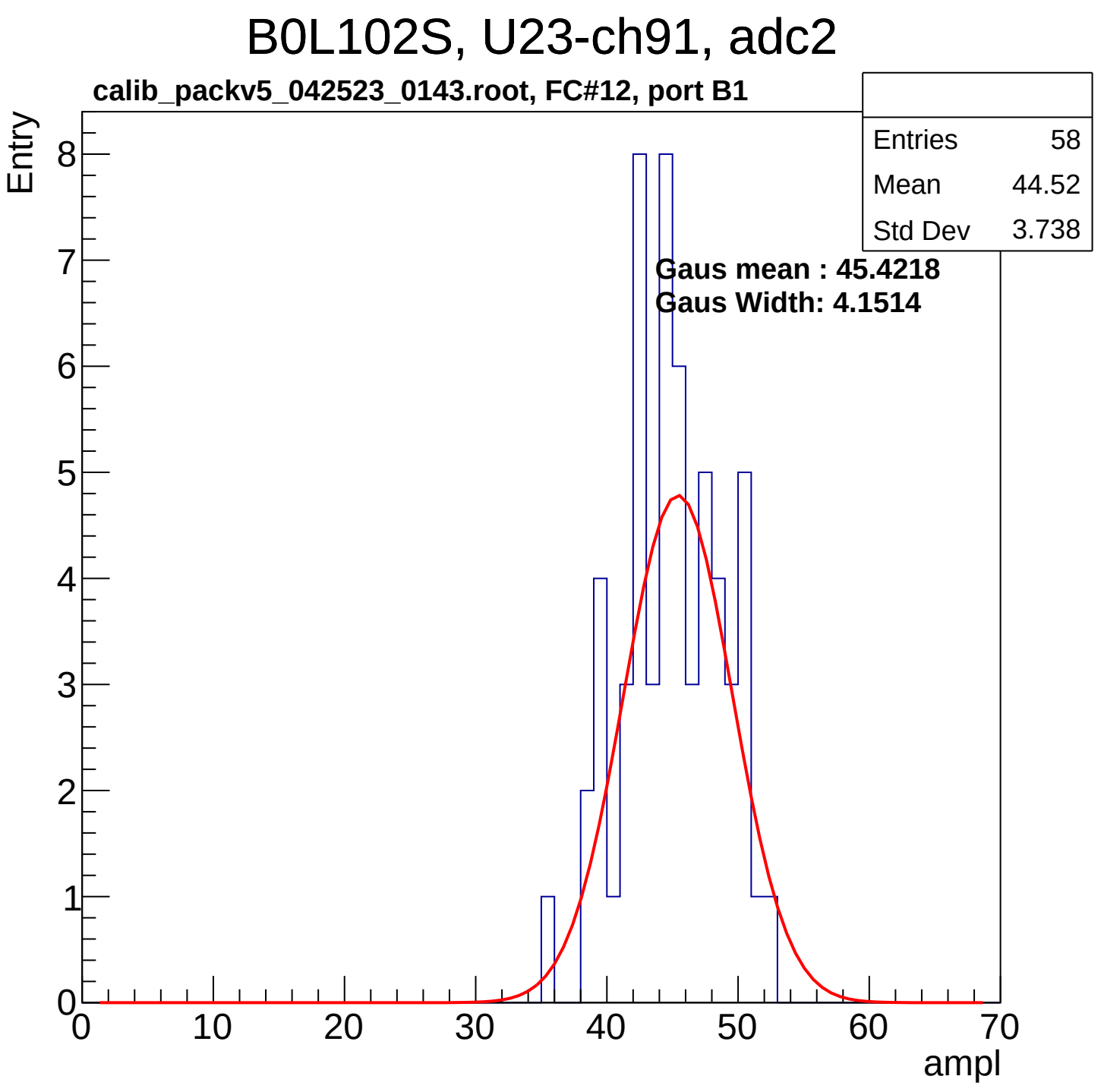
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch91, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 44.52
Std Dev 3.738
Gaus mean : 45.4218
Gaus Width: 4.1514
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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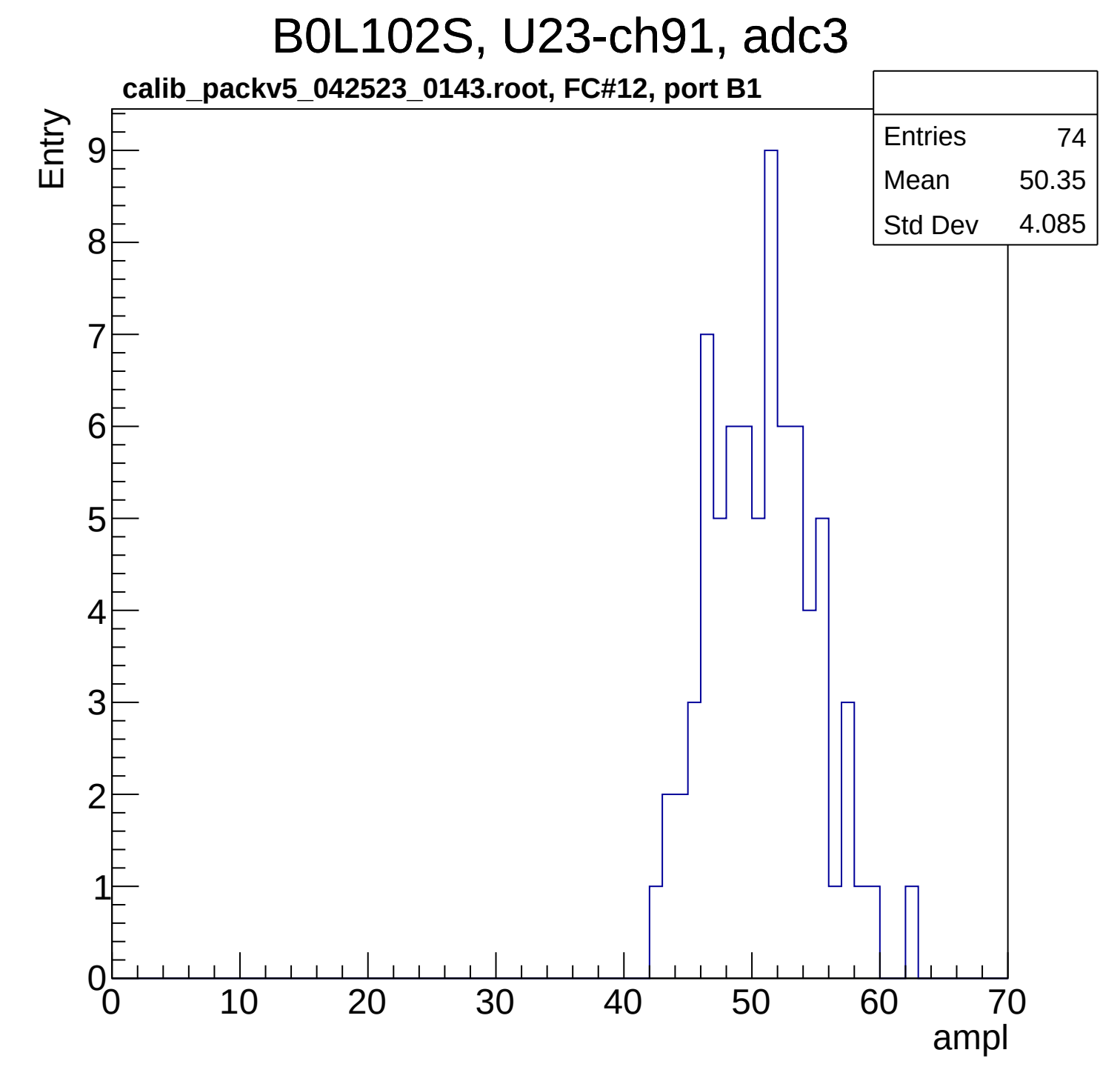
B0L102S, U23-ch91, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 50.35
Std Dev 4.085
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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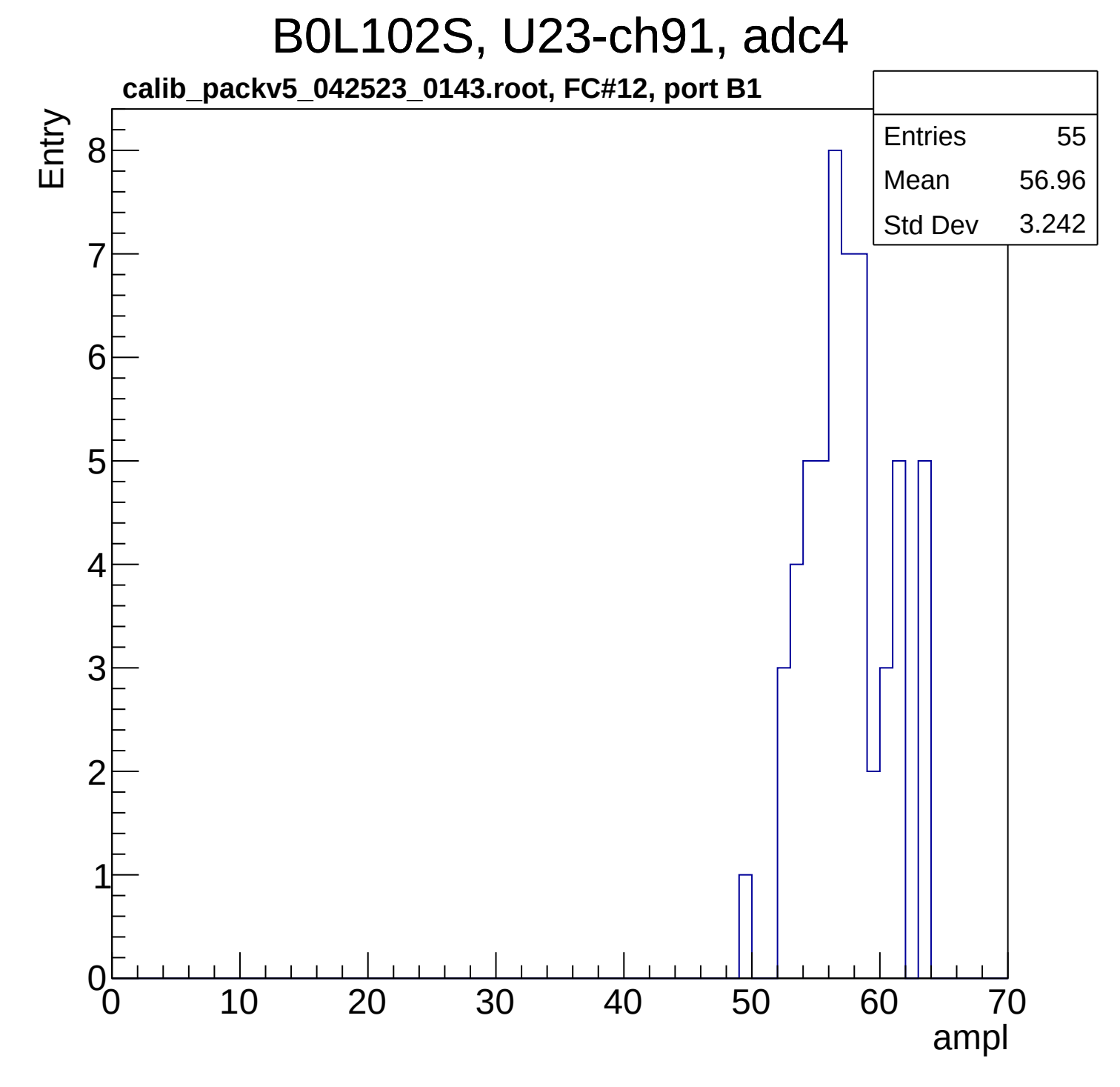
B0L102S, U23-ch91, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.96
Std Dev 3.242
Entry
ampl
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0
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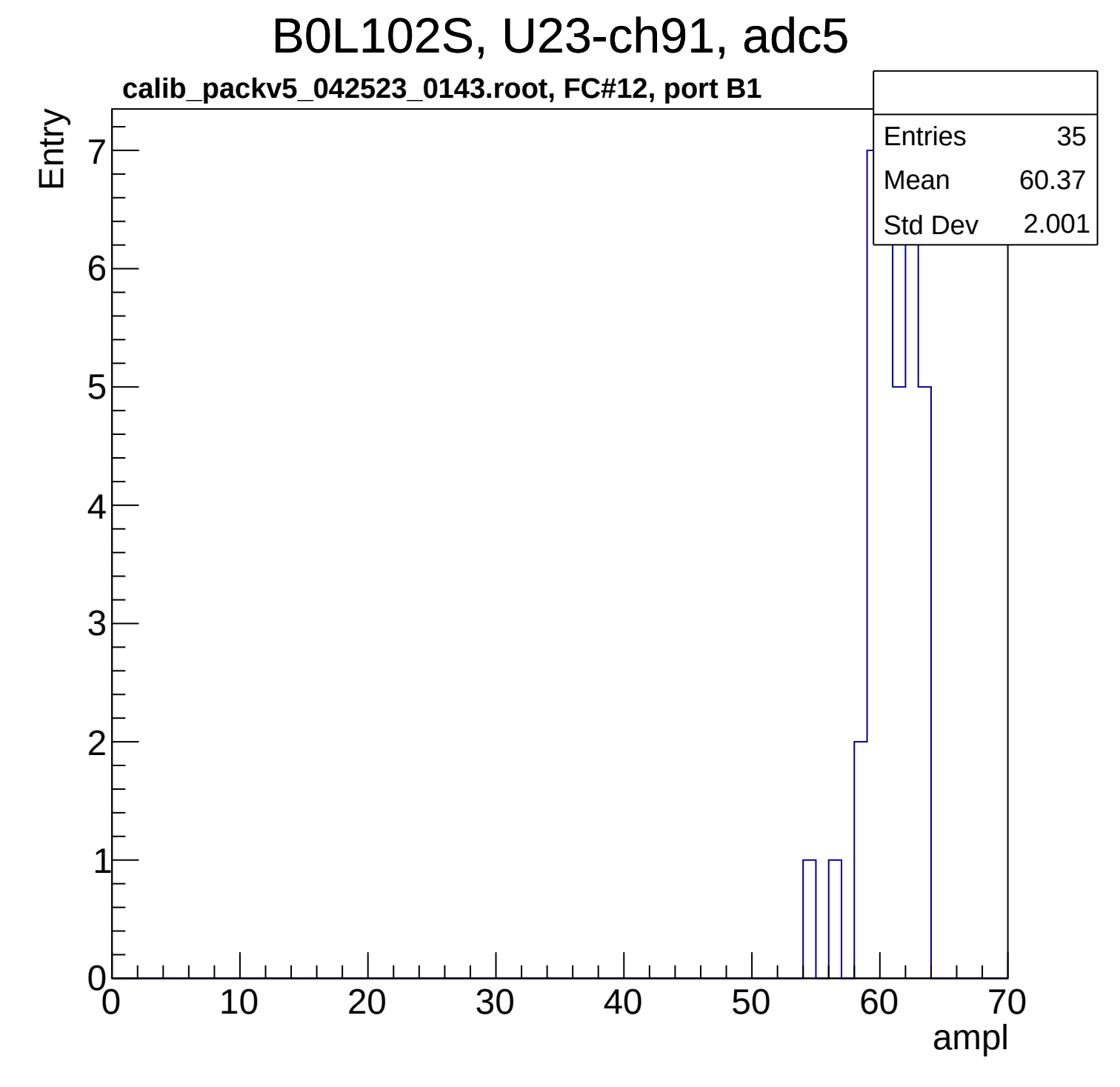
B0L102S, U23-ch91, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 35
Mean 60.37
Std Dev 2.001
Entry
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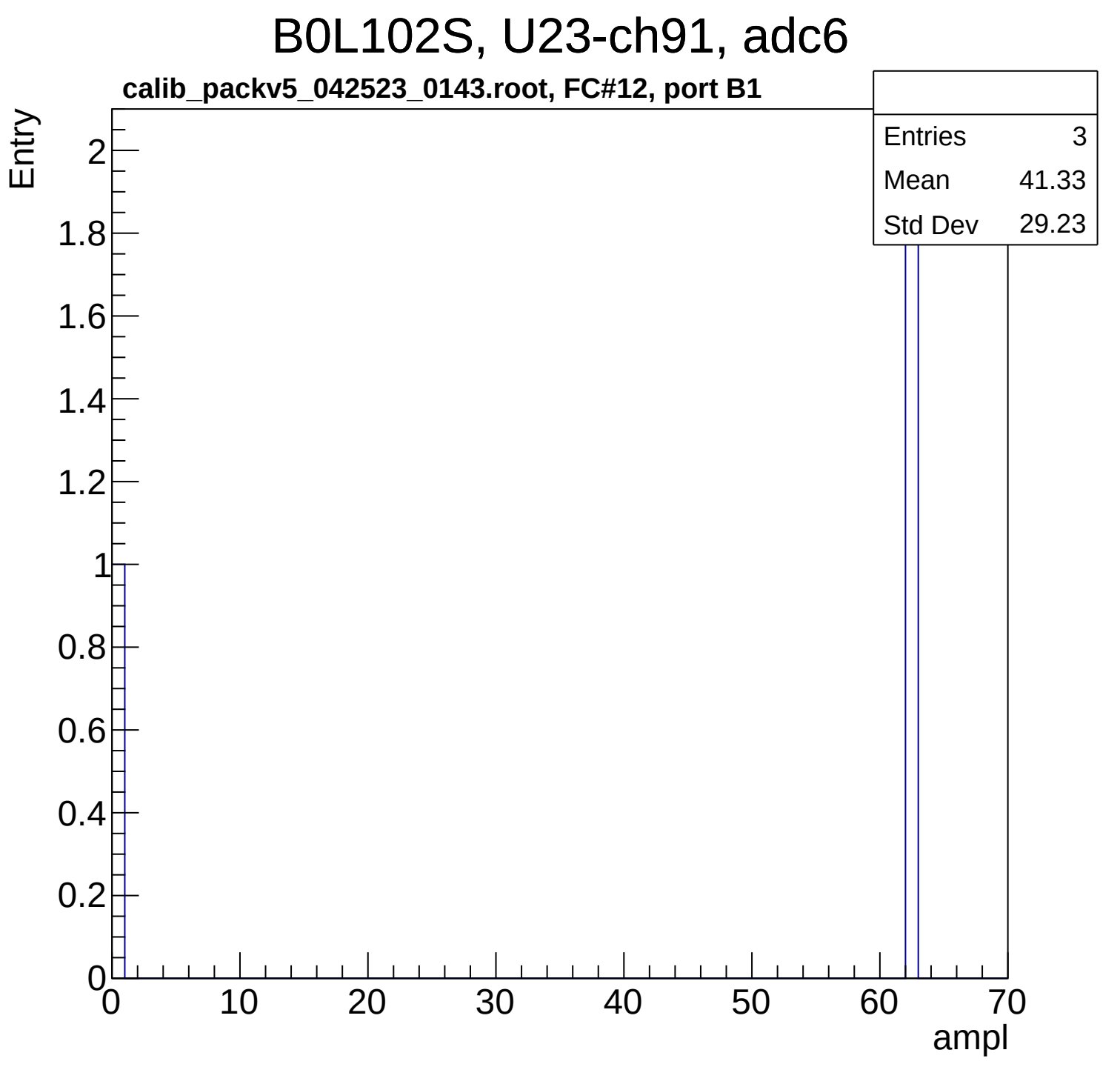
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch91, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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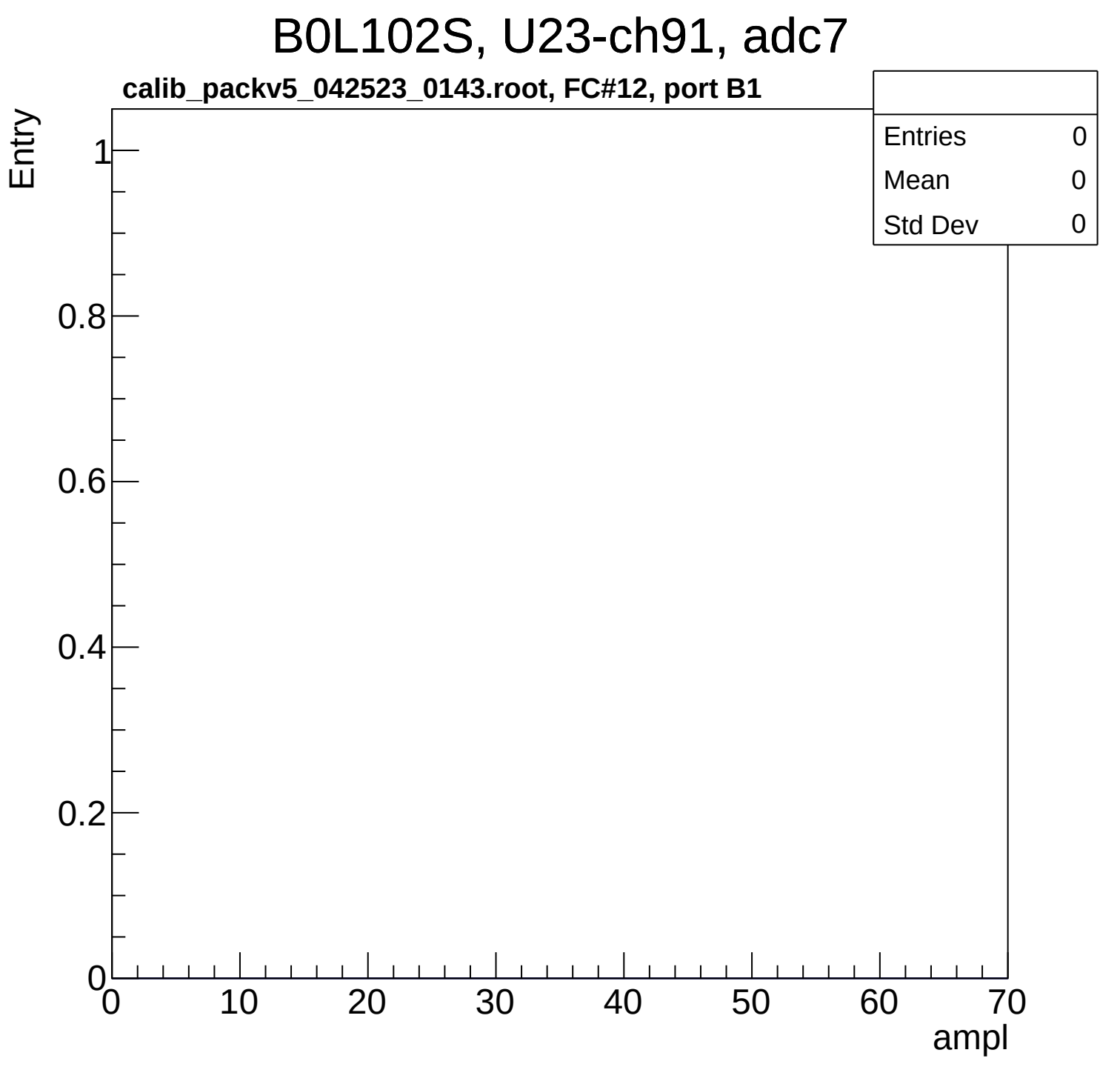
B0L102S, U23-ch91, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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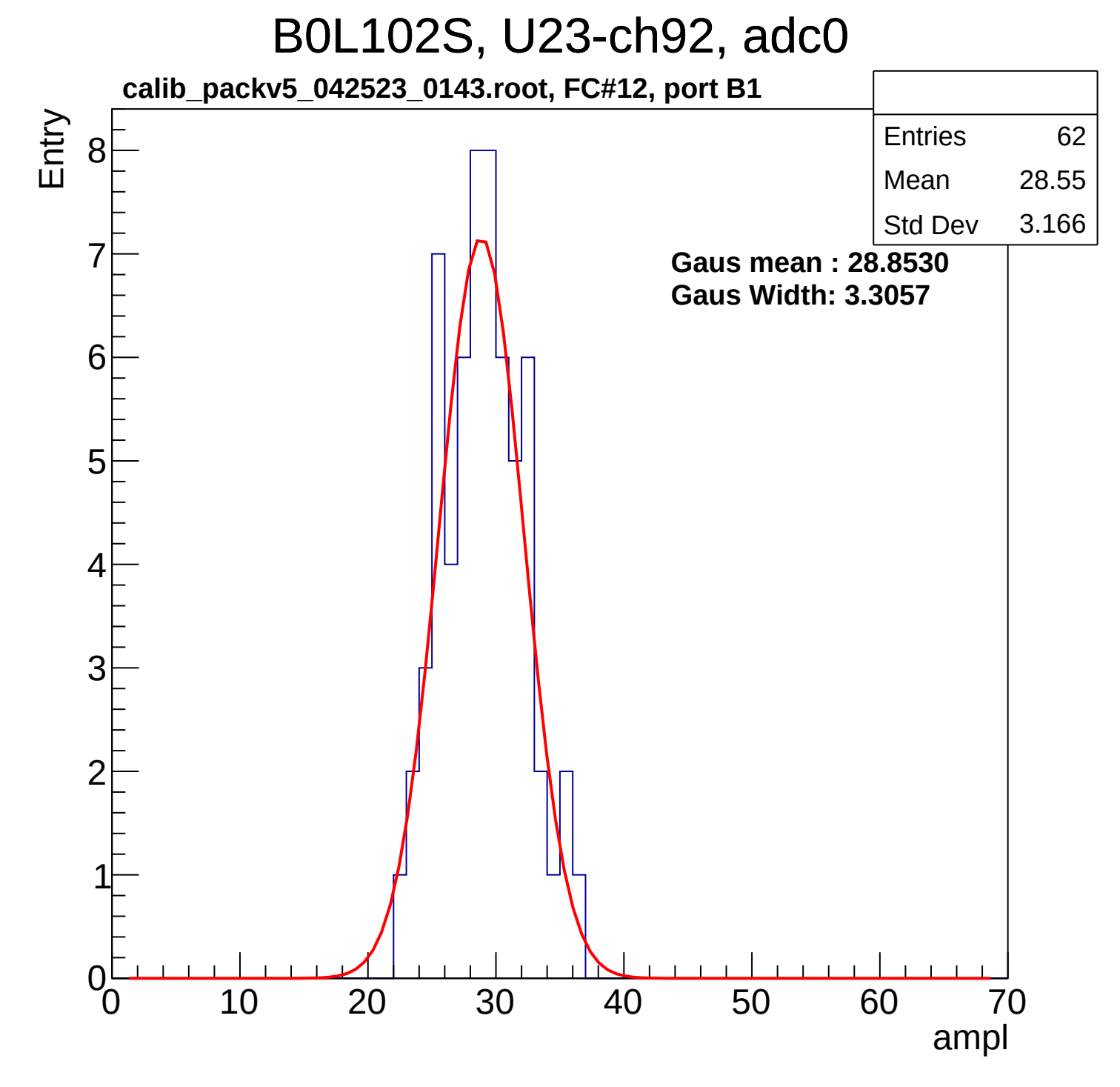
50
60
70
ampl


B0L102S, U23-ch92, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 28.55
Std Dev 3.166
Gaus mean : 28.8530
Gaus Width: 3.3057
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
50
60
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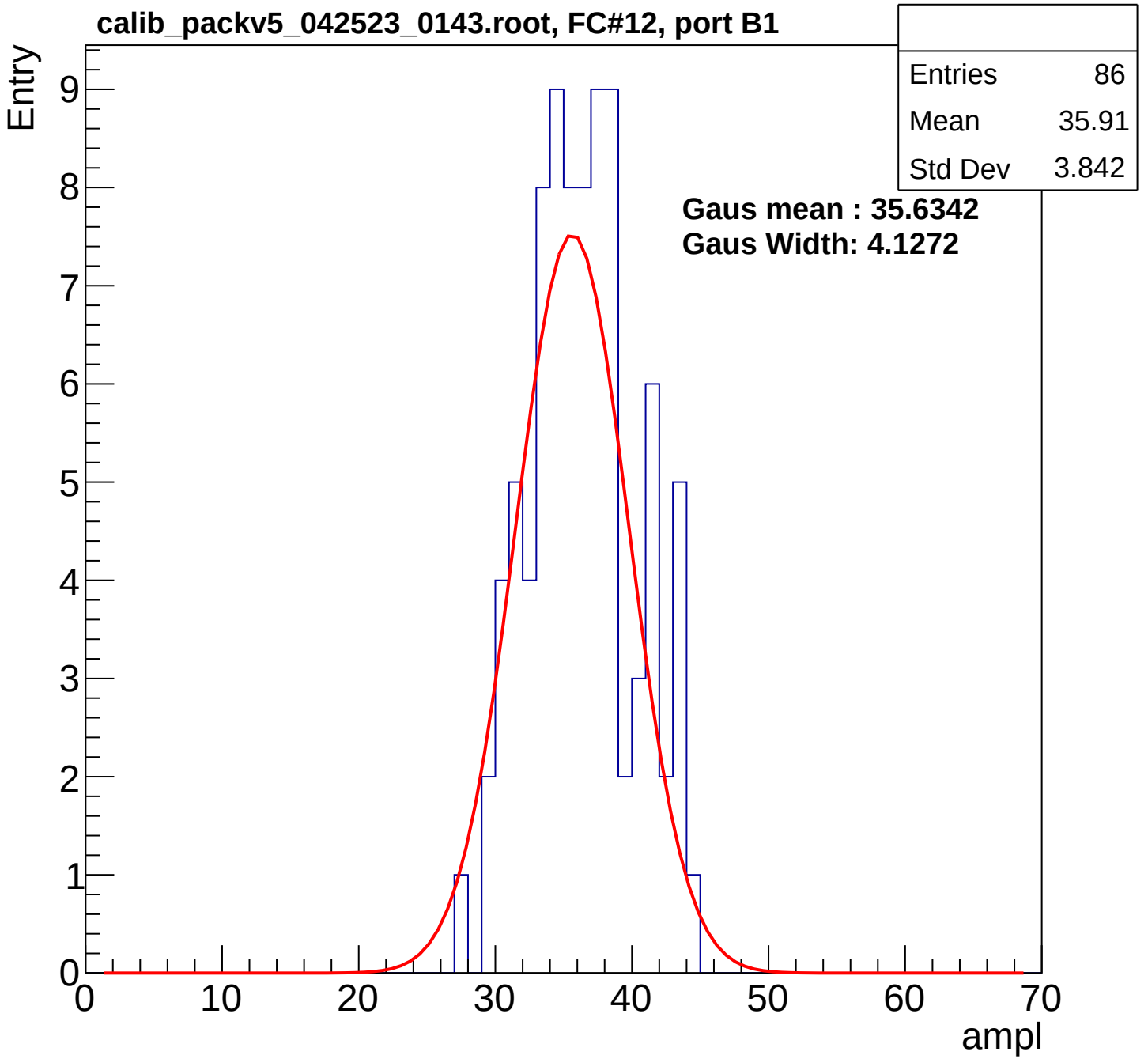
B0L102S, U23-ch92, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 35.91
Std Dev 3.842
Gaus mean : 35.6342
Gaus Width: 4.1272
Entry
ampl
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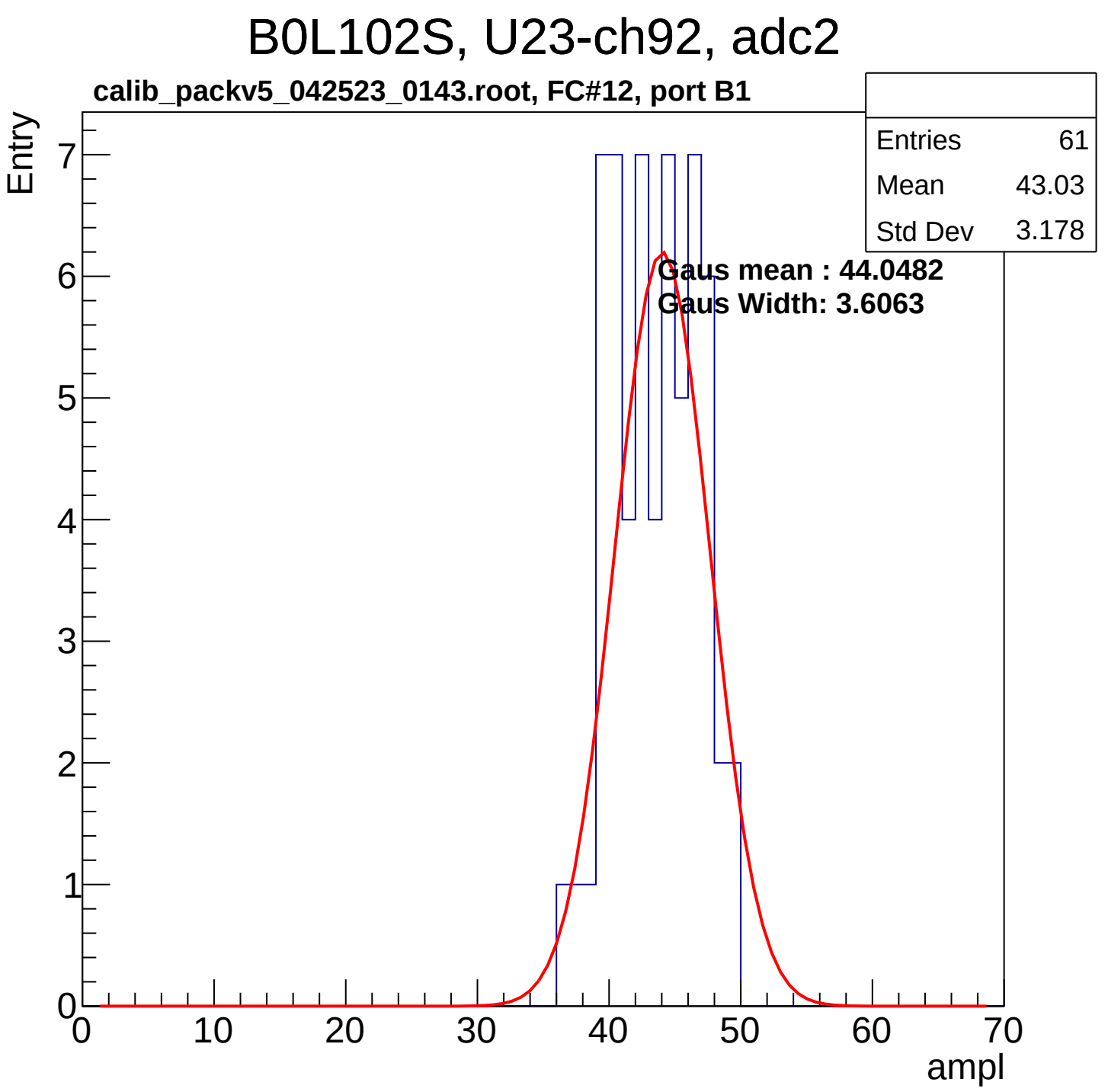
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch92, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 43.03
Std Dev 3.178
Gaus mean : 44.0482
Gaus Width: 3.6063
Entry
ampl
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3
4
5
6
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0
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40
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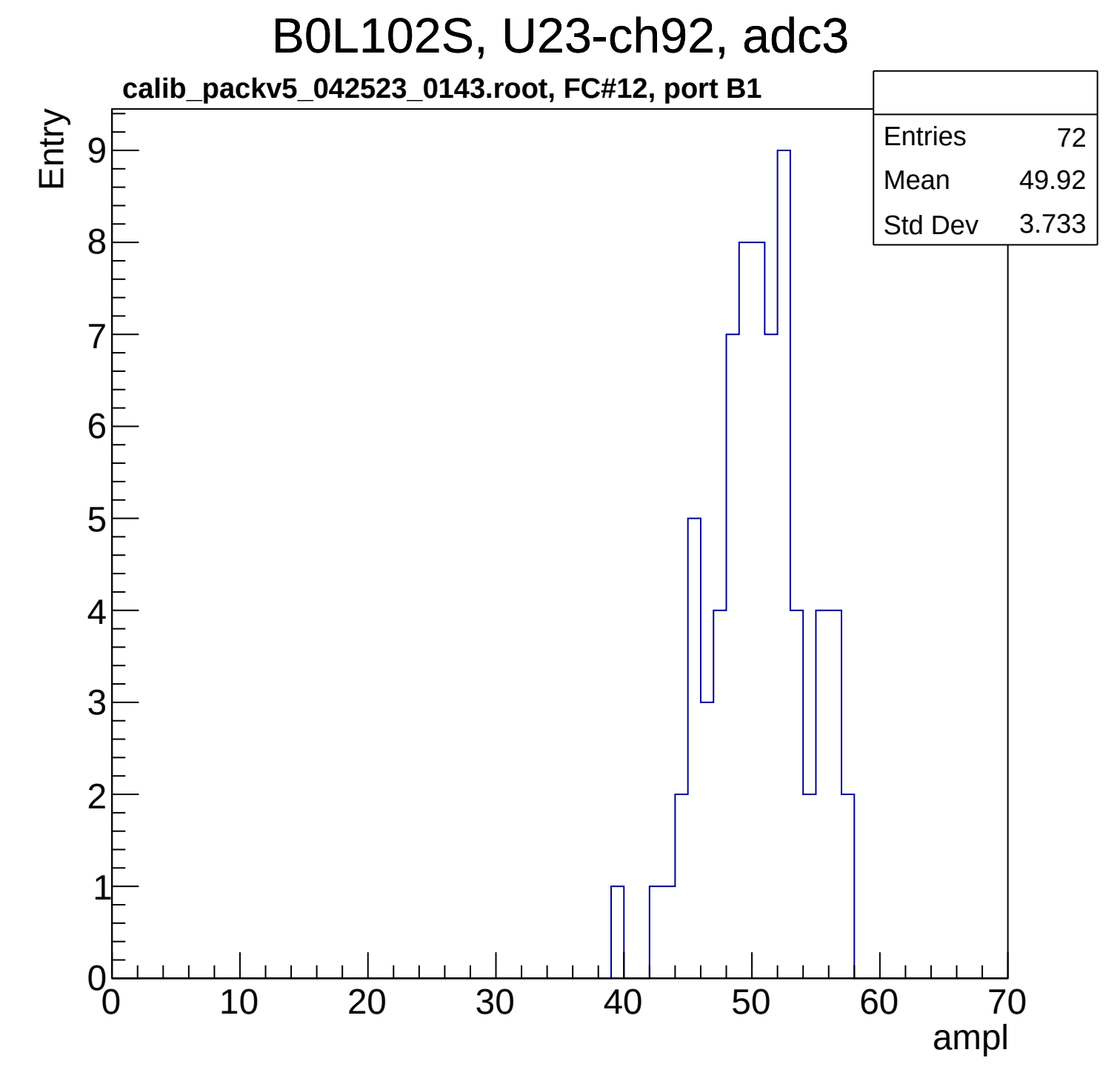
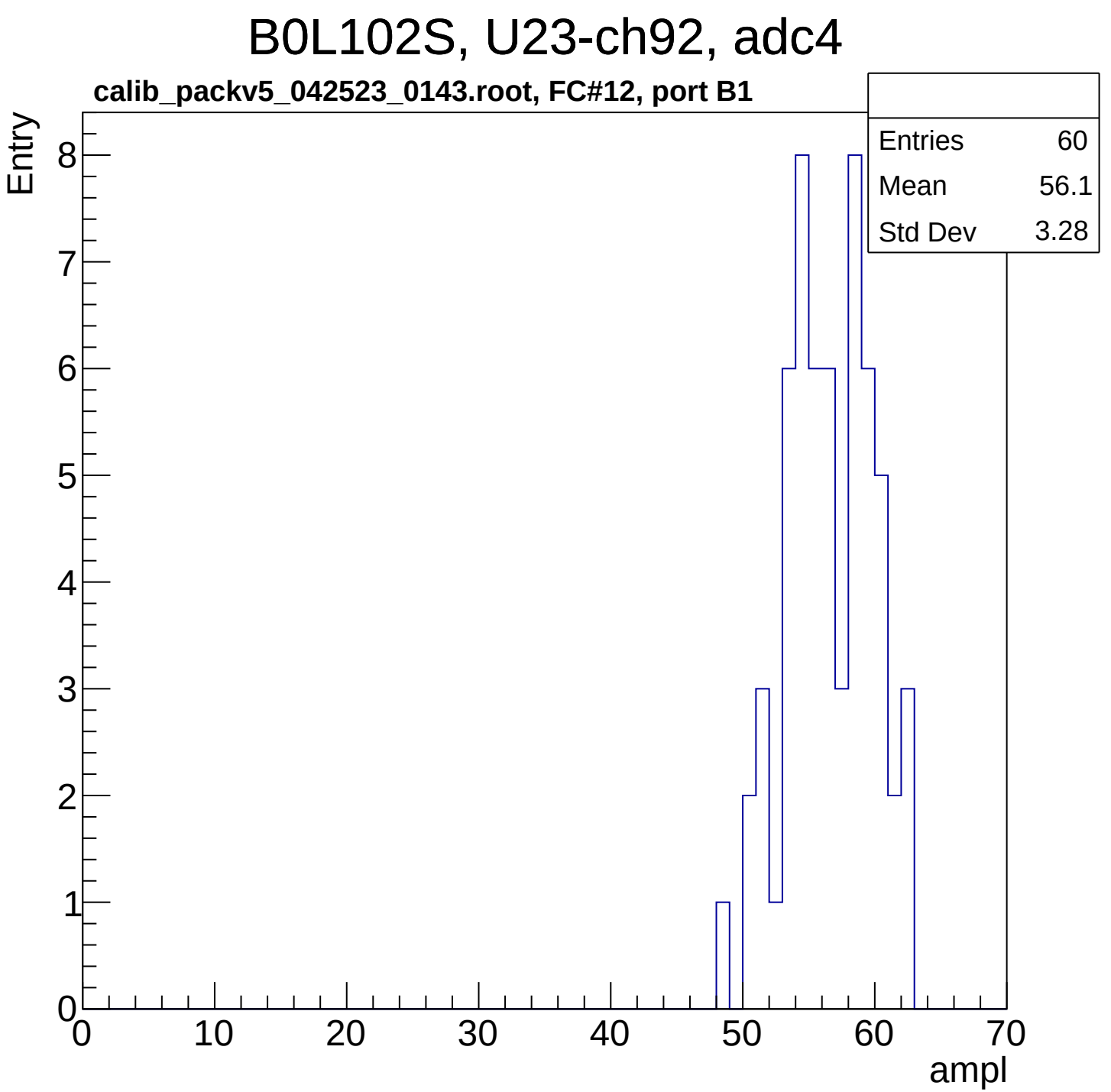

B0L102S, U23-ch92, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 49.92
Std Dev 3.733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch92, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.1
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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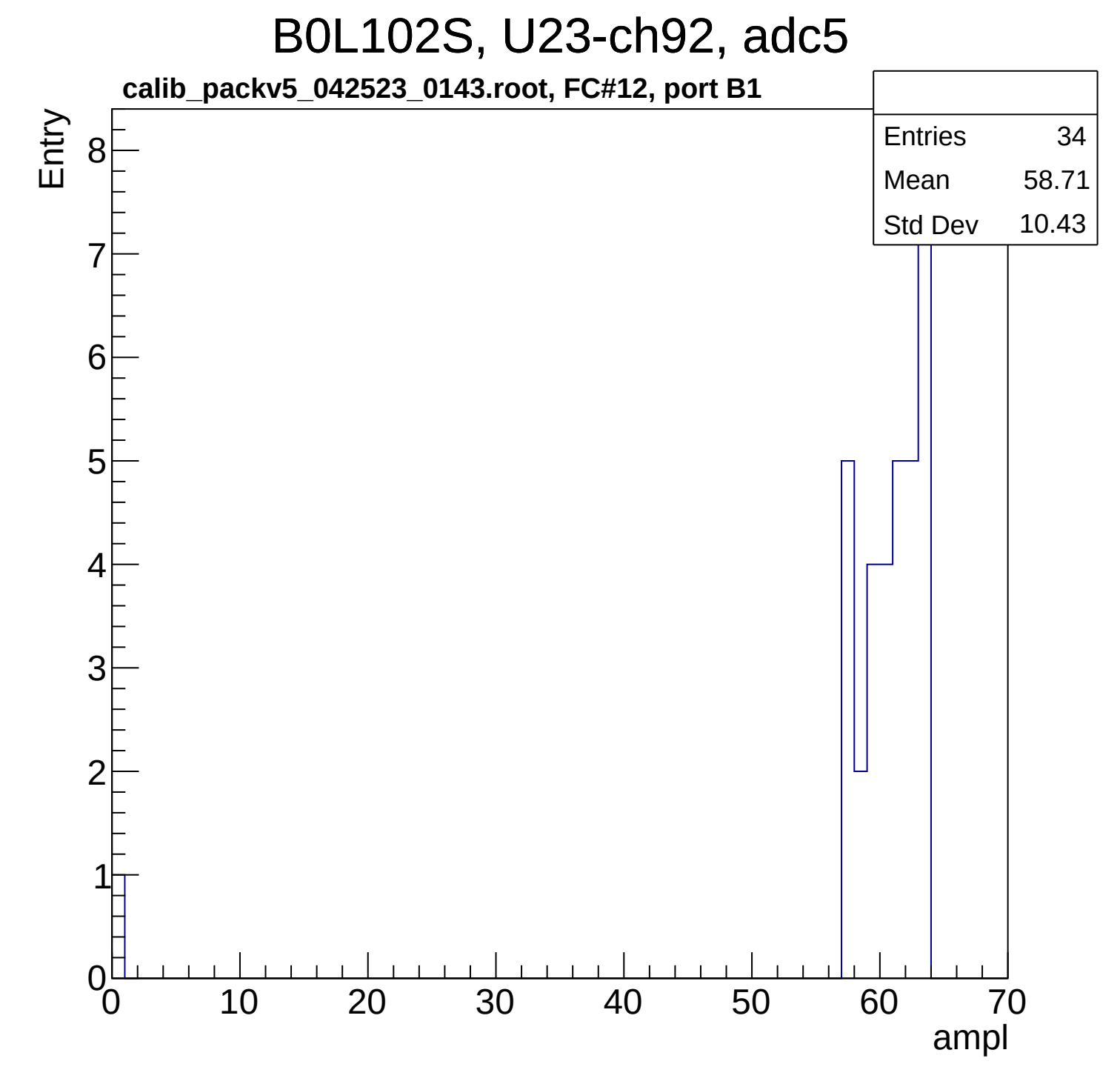
B0L102S, U23-ch92, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 58.71
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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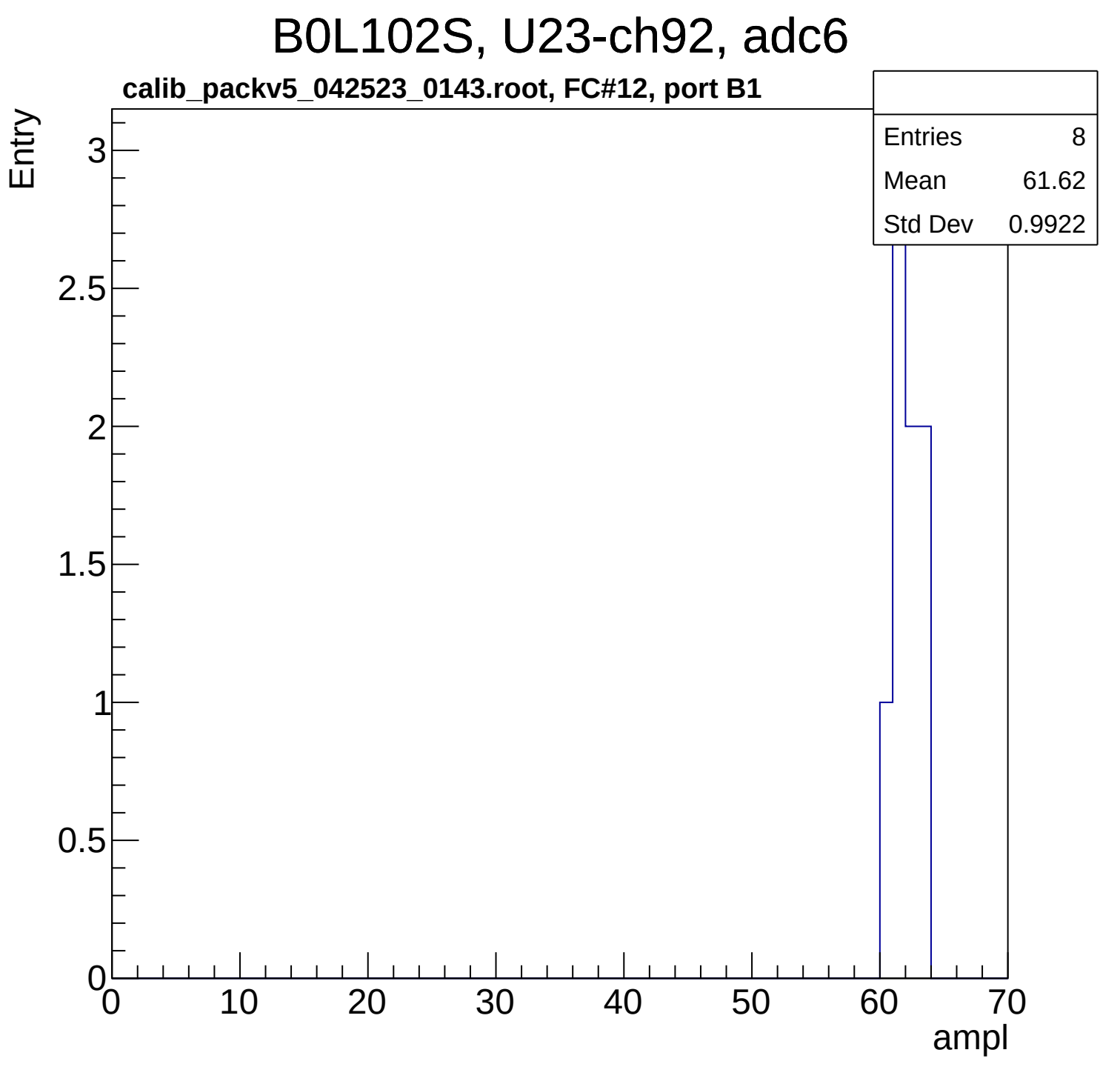
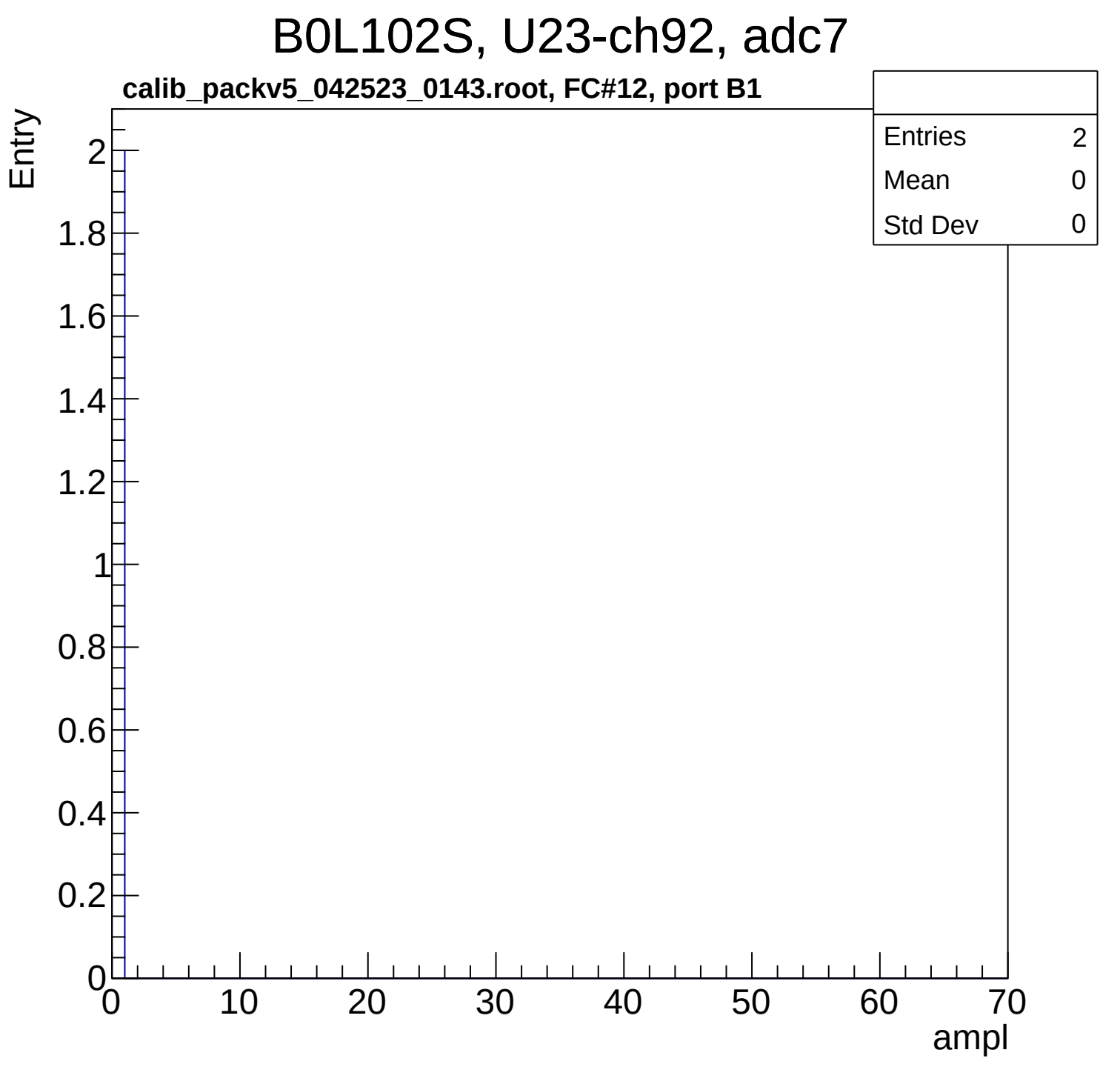
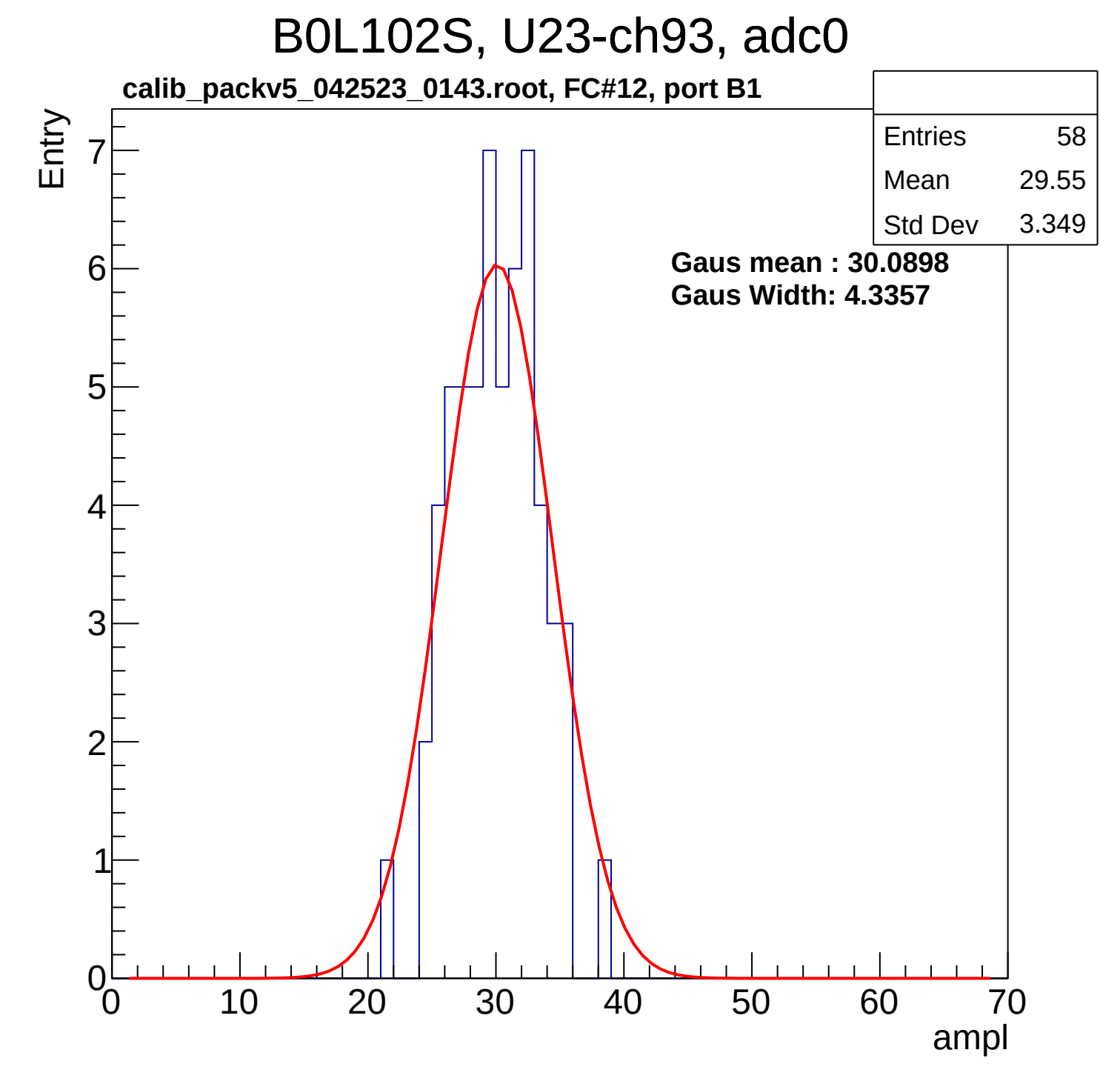

B0L102S, U23-ch92, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.62
Std Dev 0.9922
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch92, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch93, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 29.55
Std Dev 3.349
Gaus mean : 30.0898
Gaus Width: 4.3357
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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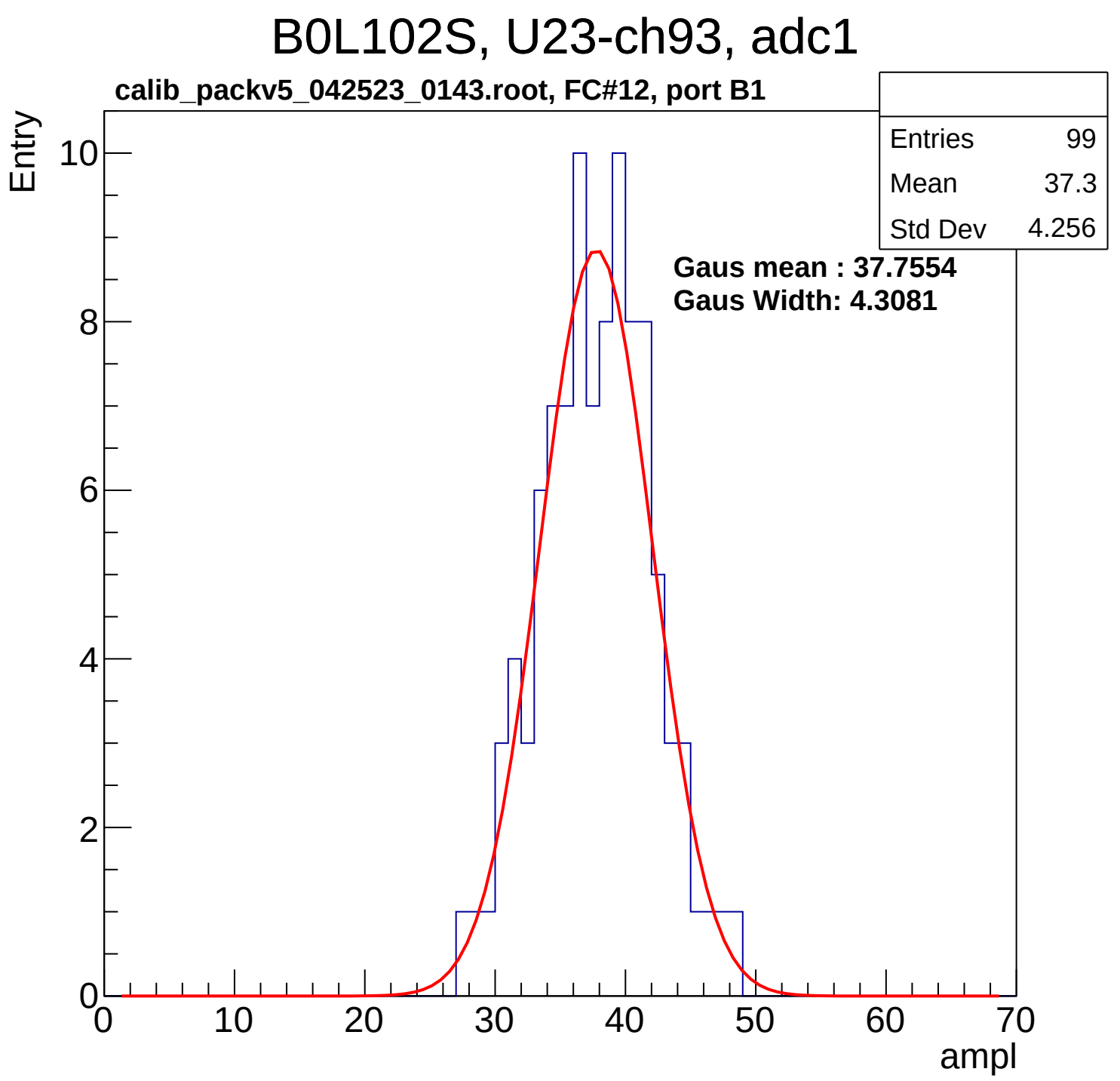
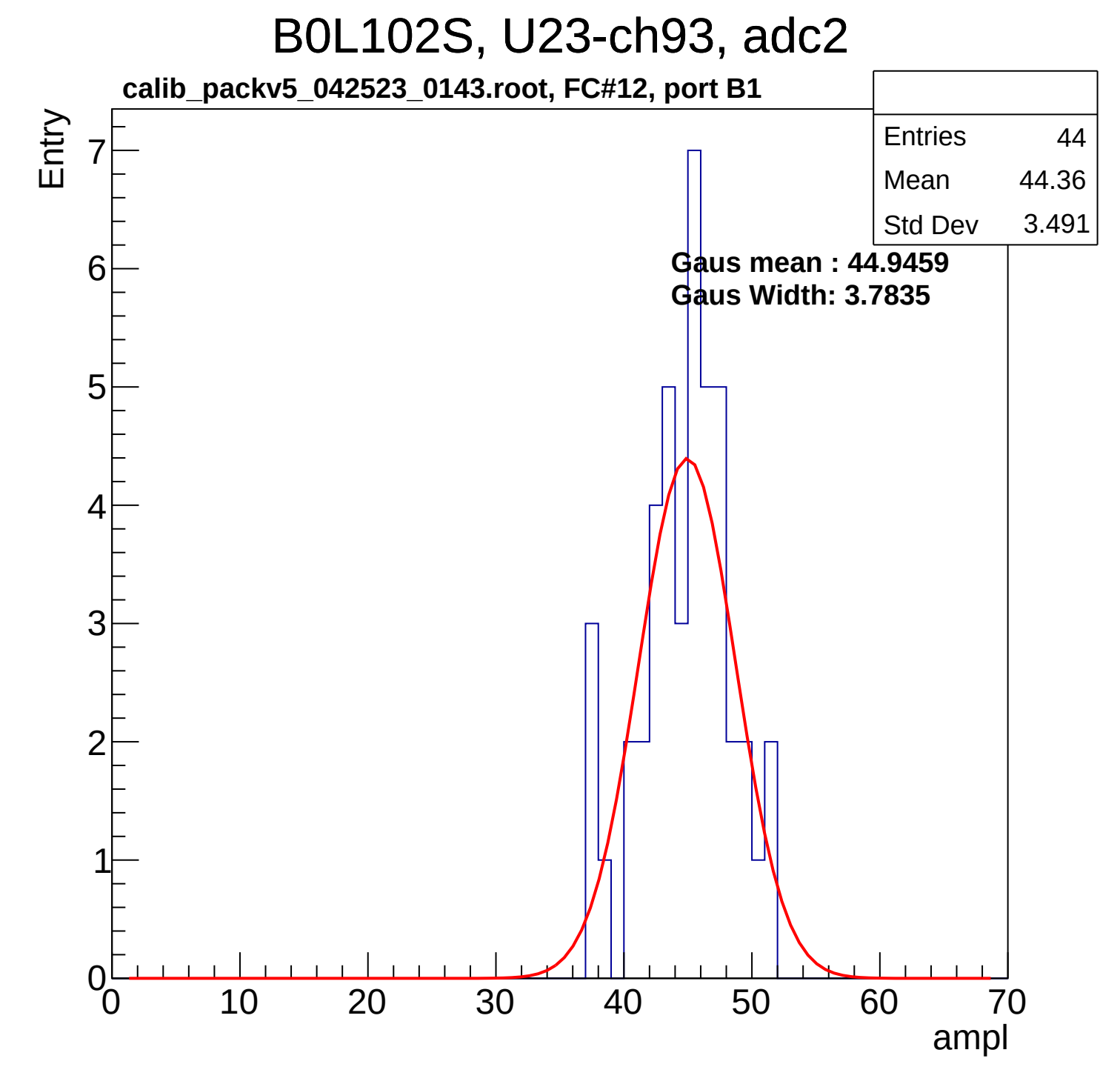

B0L102S, U23-ch93, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 99
Mean 37.3
Std Dev 4.256
Gaus mean : 37.7554
Gaus Width: 4.3081
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch93, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 44.36
Std Dev 3.491
Gaus mean : 44.9459
Gaus Width: 3.7835
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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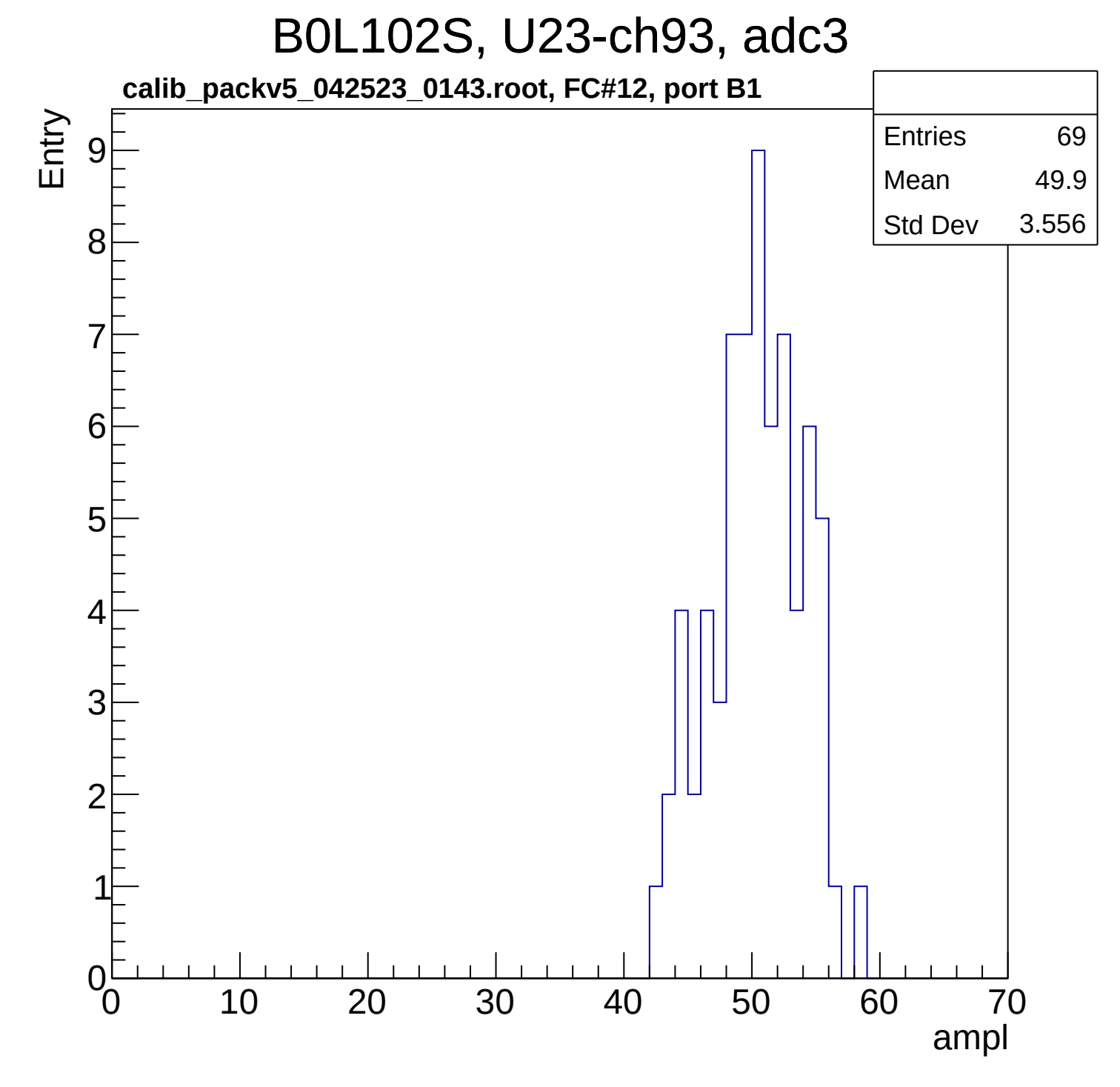
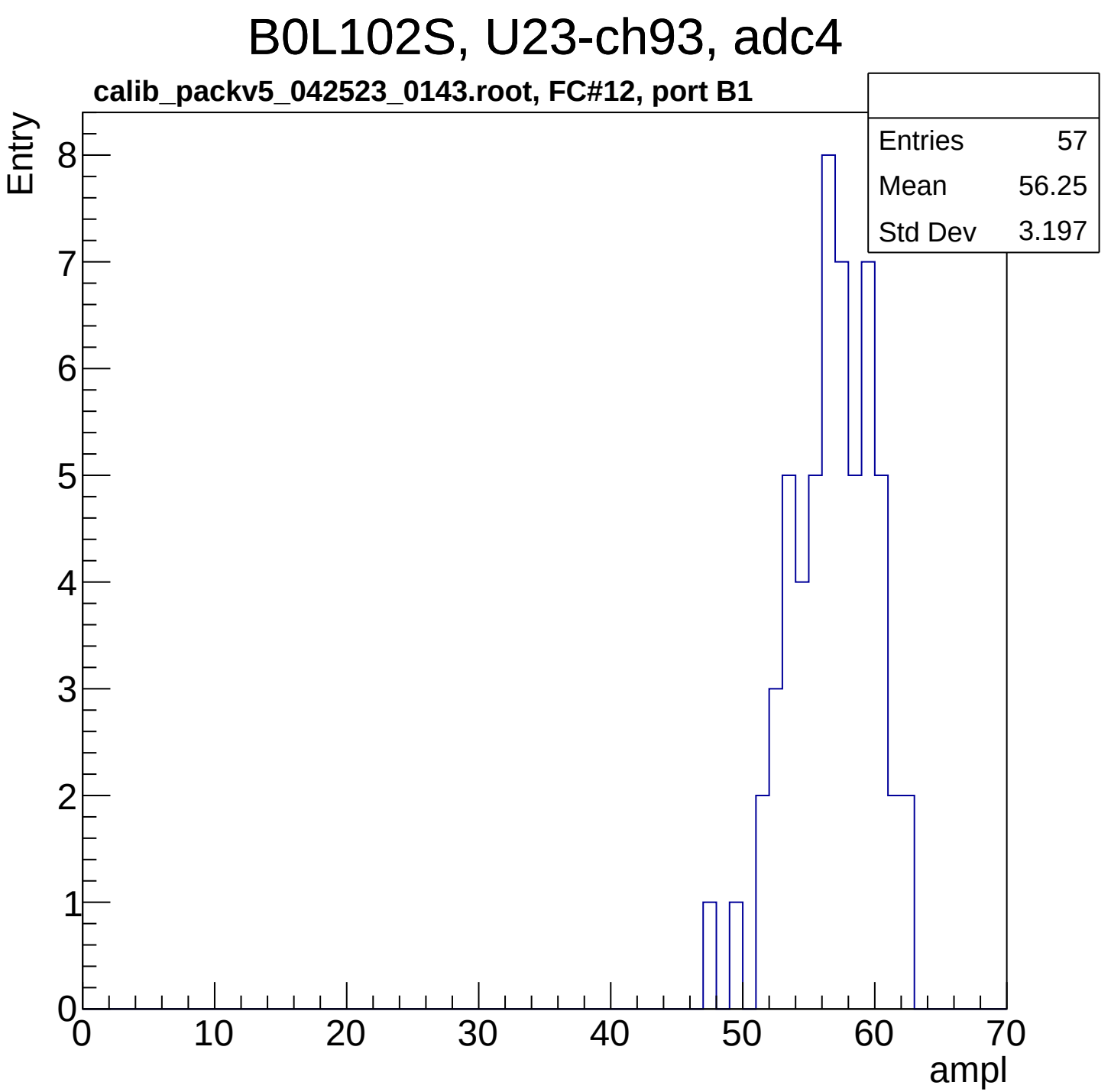
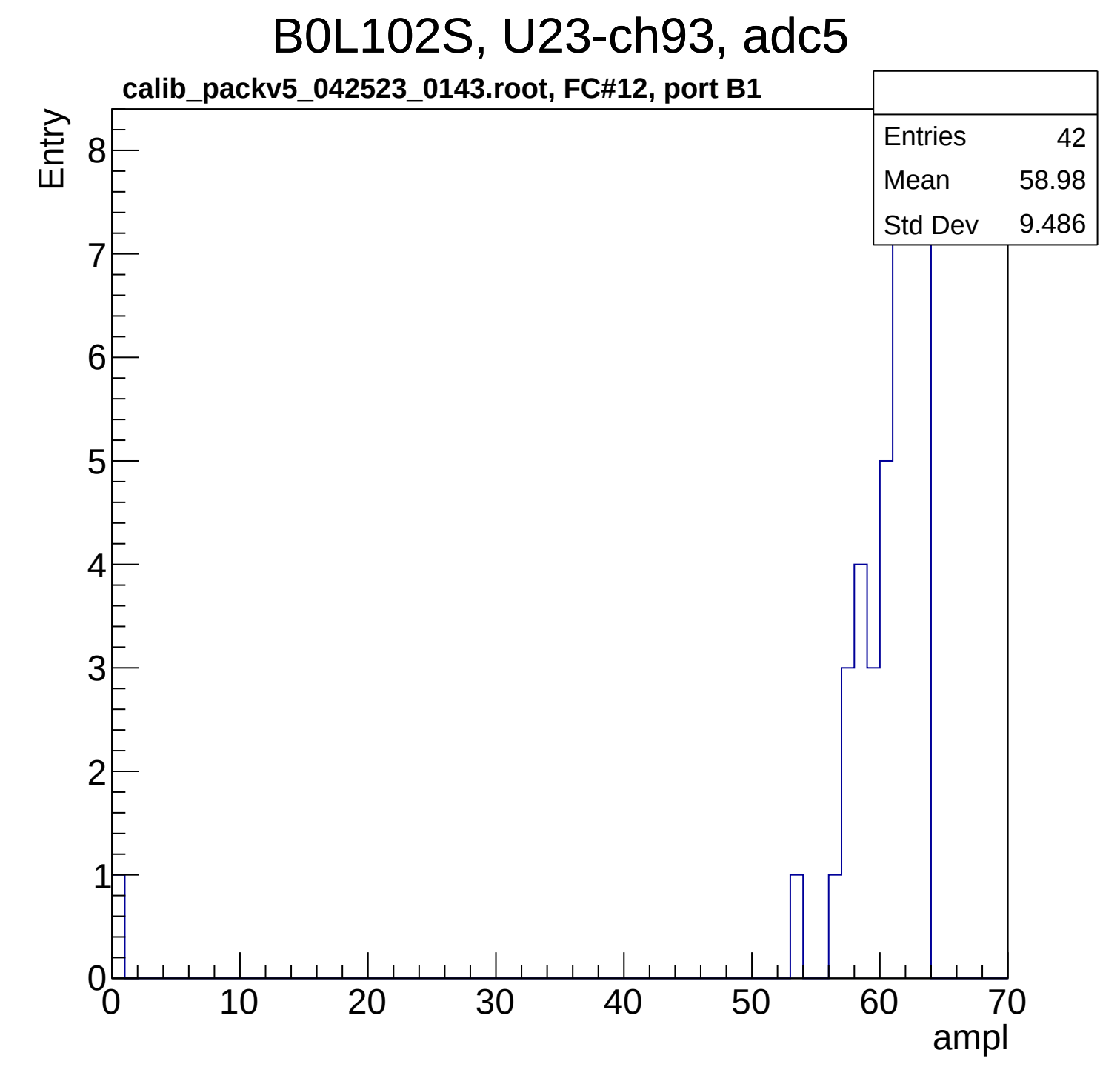
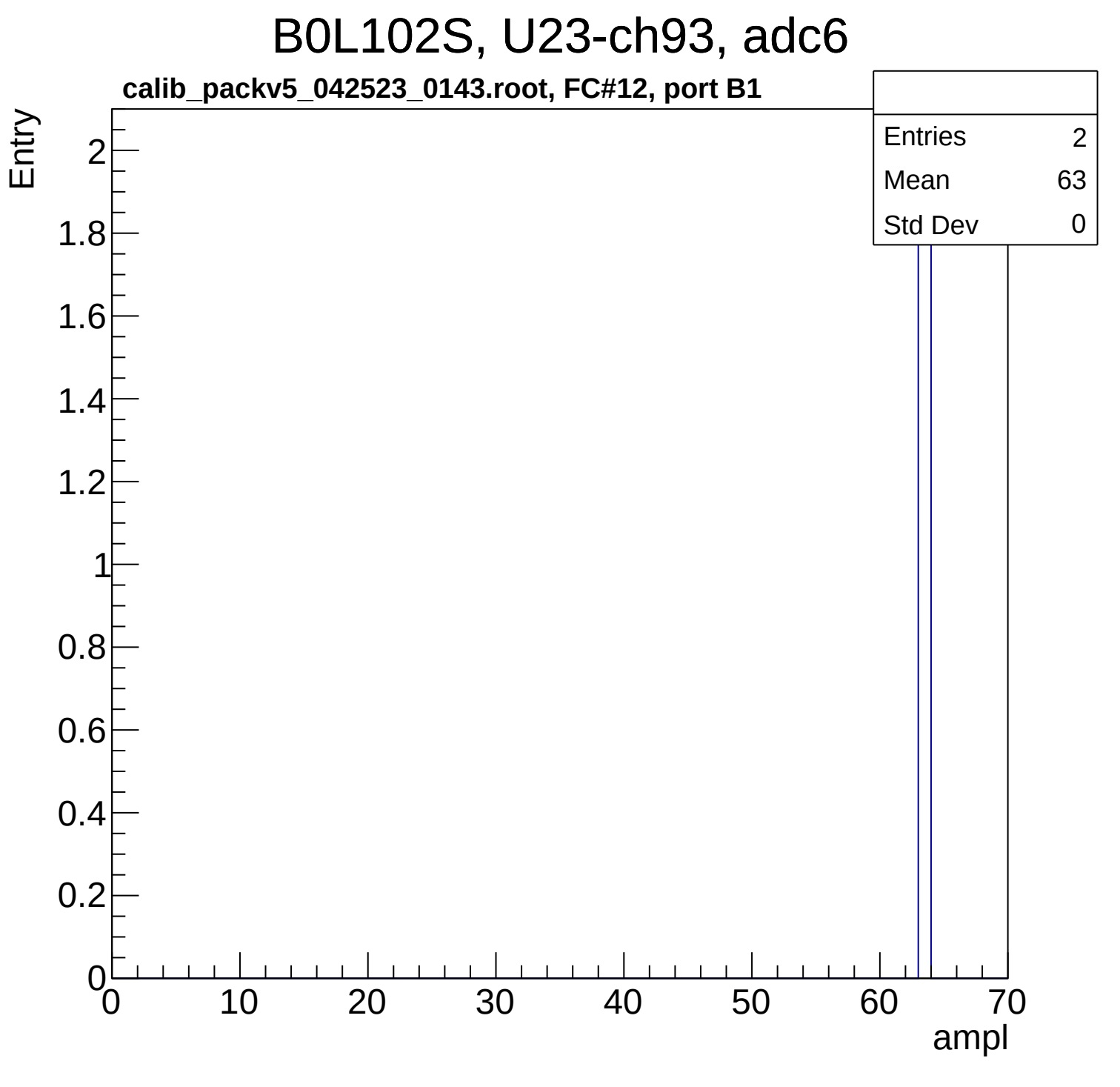
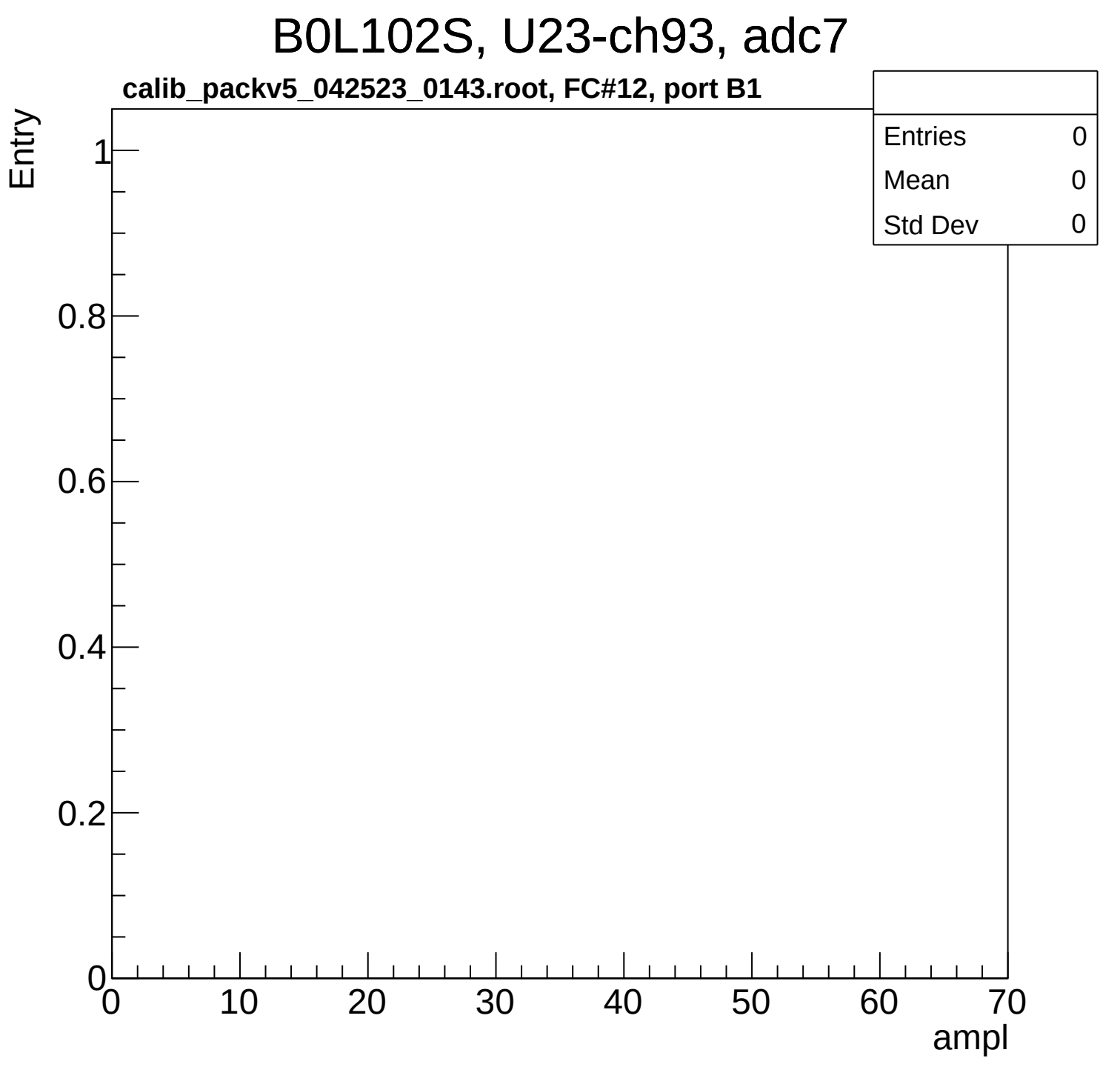
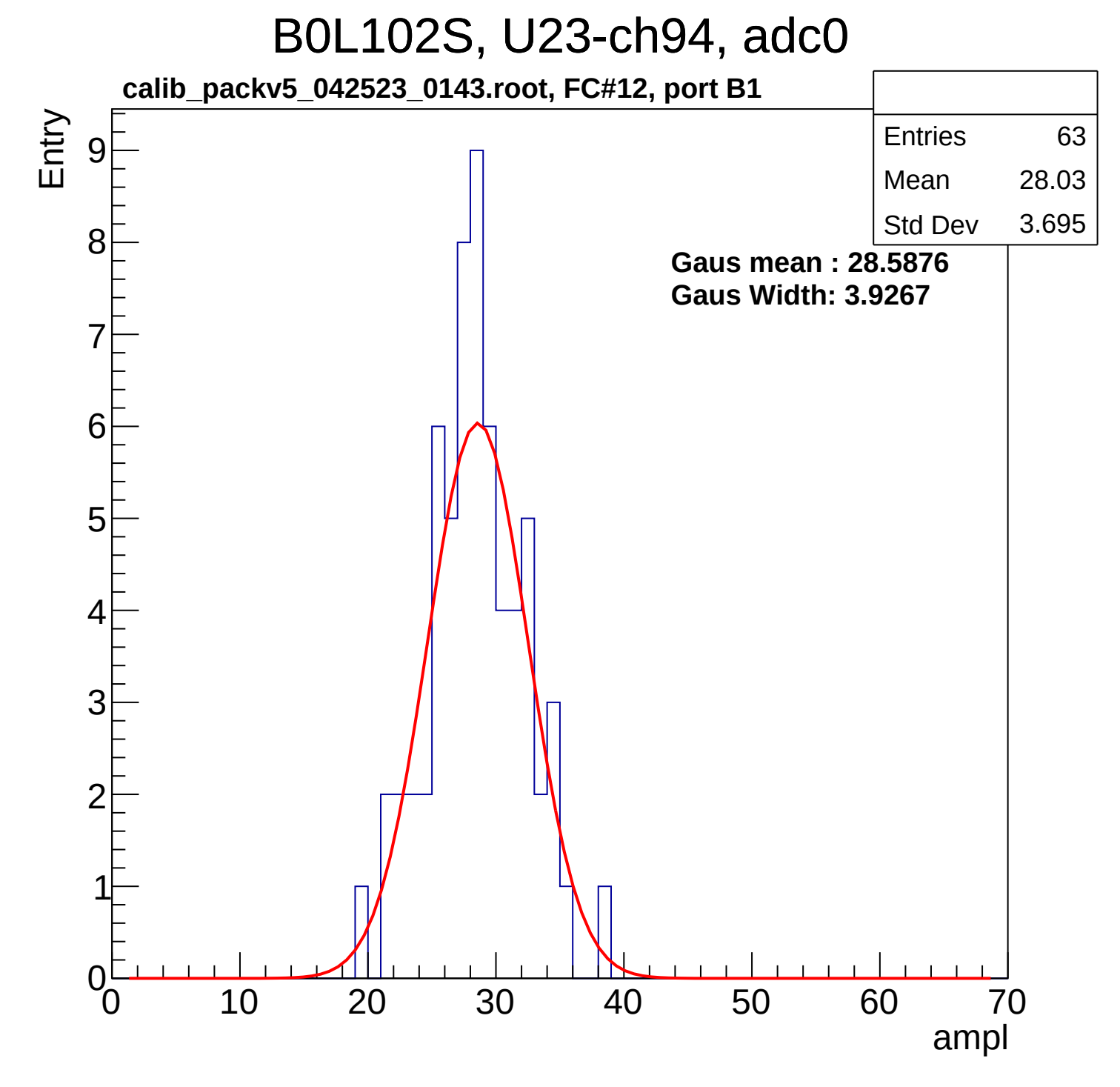

B0L102S, U23-ch93, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 49.9
Std Dev 3.556
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

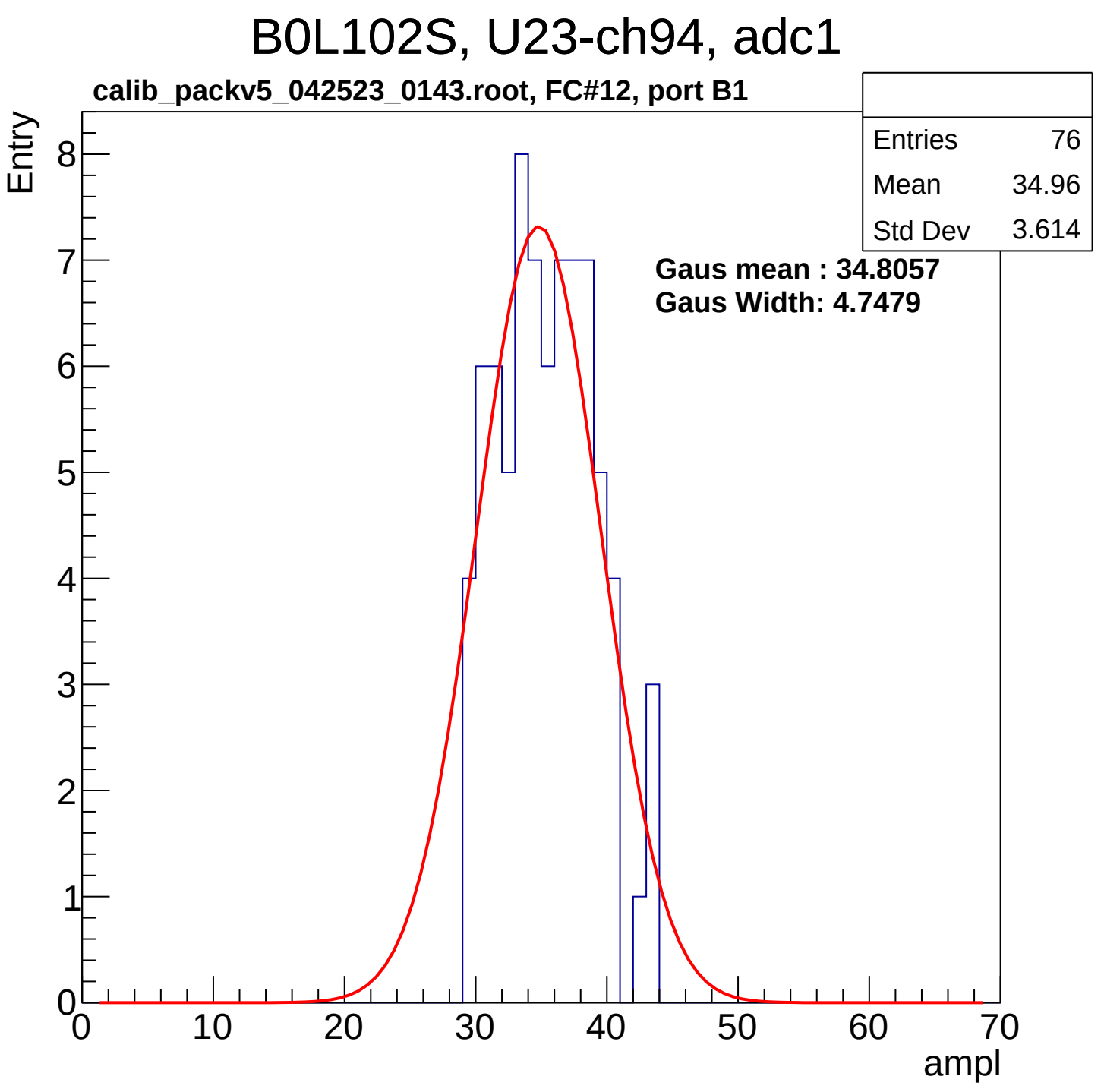

B0L102S, U23-ch93, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 56.25
Std Dev 3.197
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

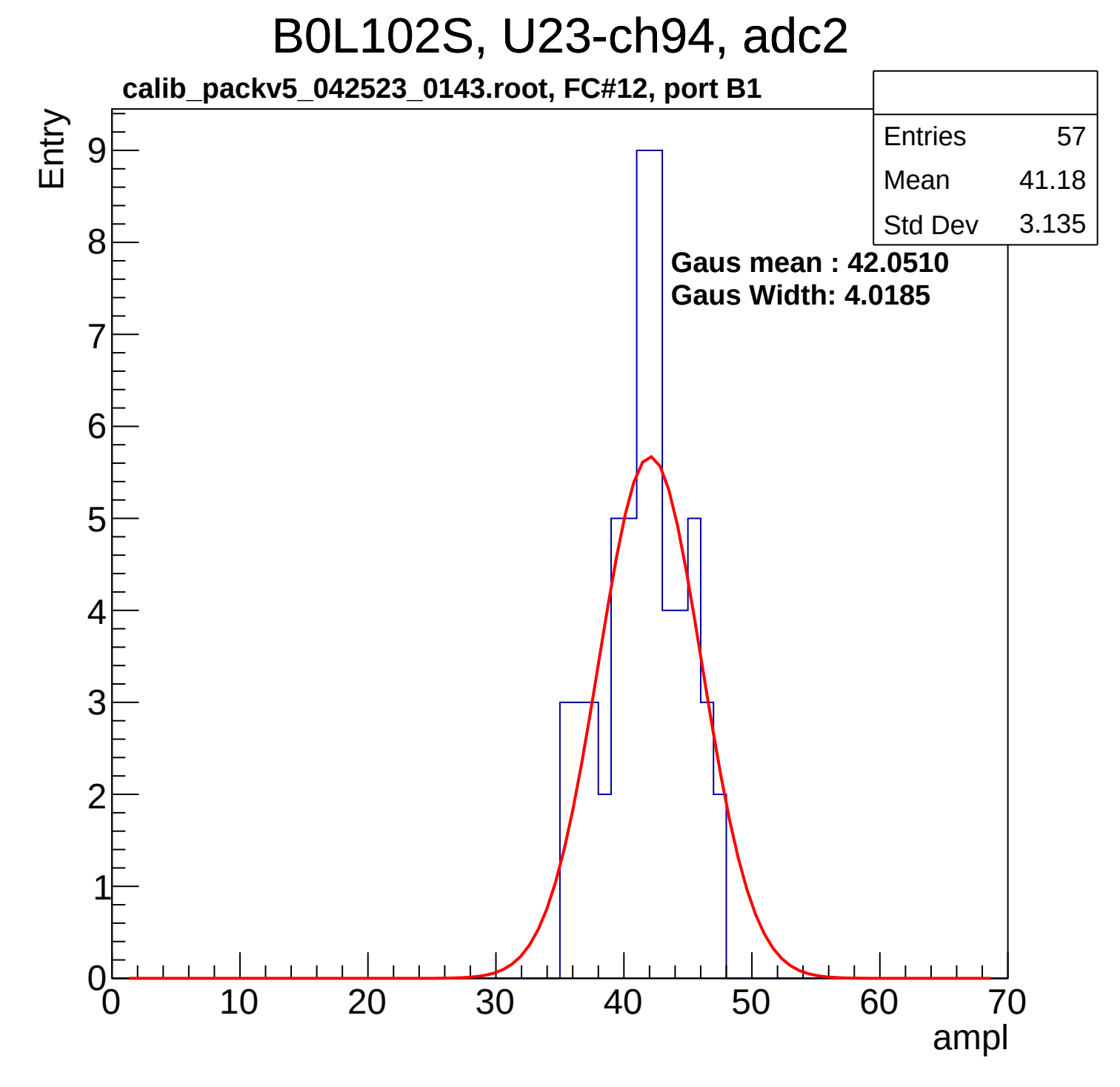

B0L102S, U23-ch93, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 58.98
Std Dev 9.486
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch93, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch93, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch94, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 28.03
Std Dev 3.695
Gaus mean : 28.5876
Gaus Width: 3.9267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch94, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 34.96
Std Dev 3.614
Gaus mean : 34.8057
Gaus Width: 4.7479
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch94, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 41.18
Std Dev 3.135
Gaus mean : 42.0510
Gaus Width: 4.0185
Entry
ampl
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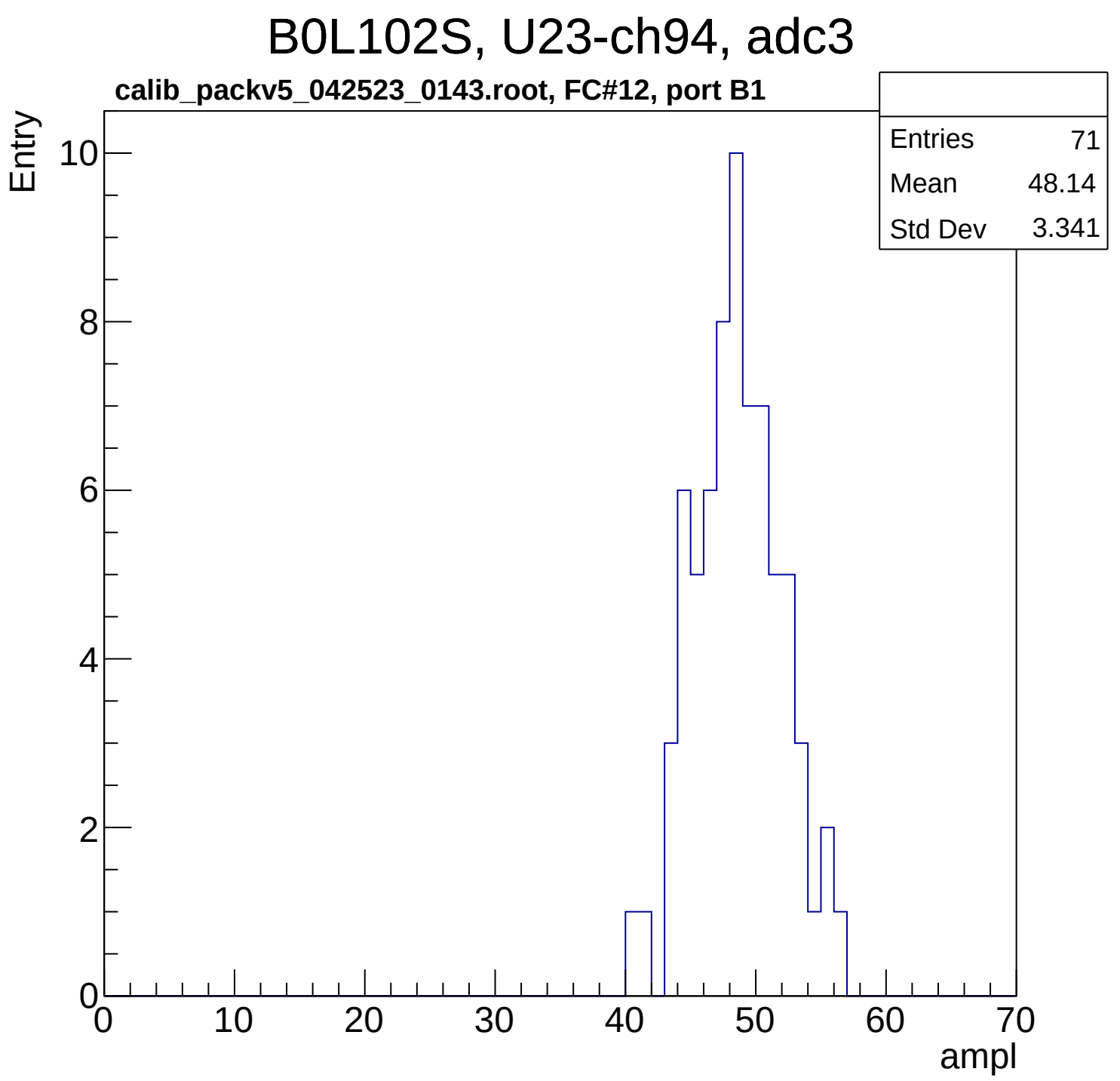
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch94, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 48.14
Std Dev 3.341
Entry
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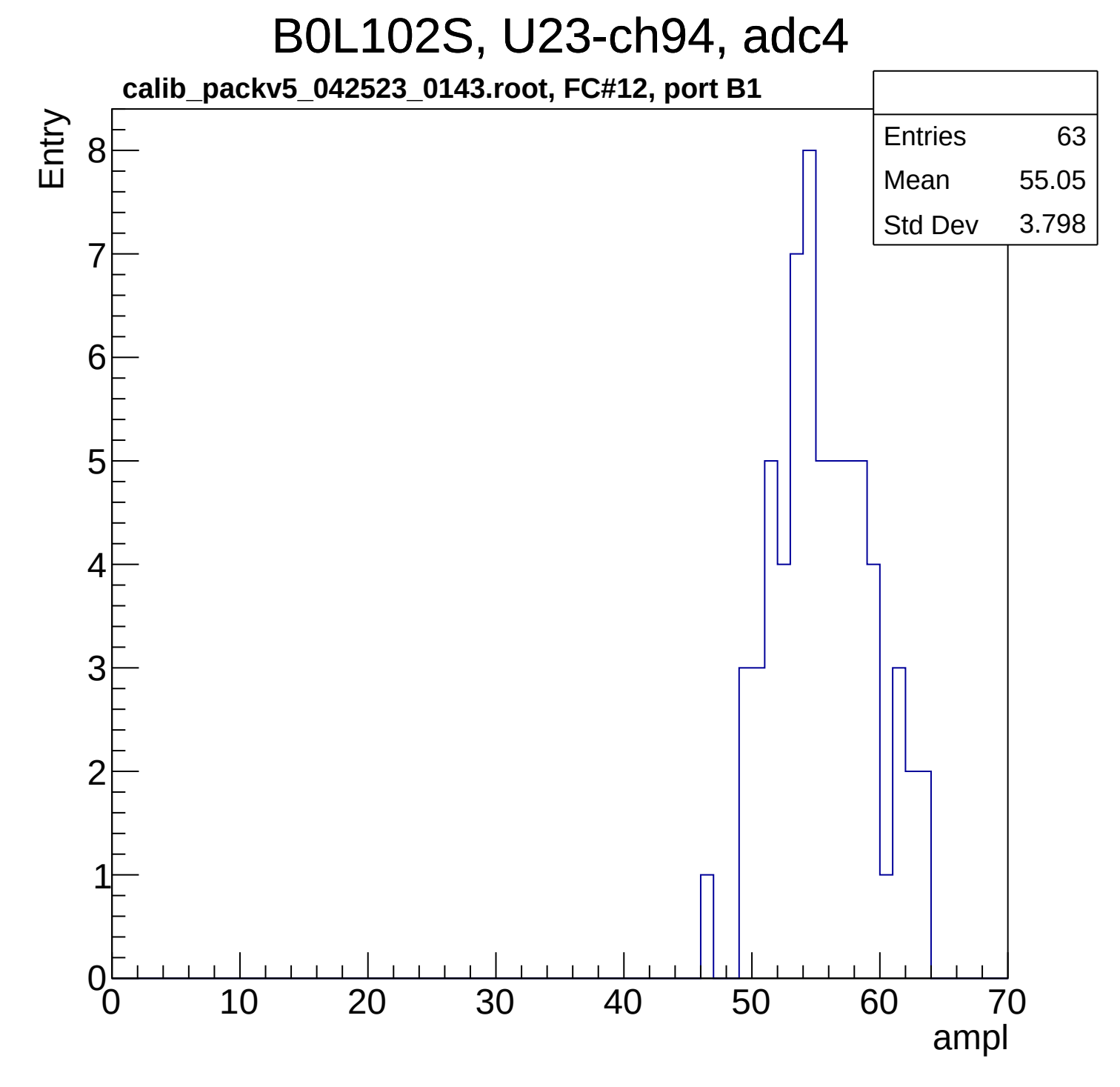
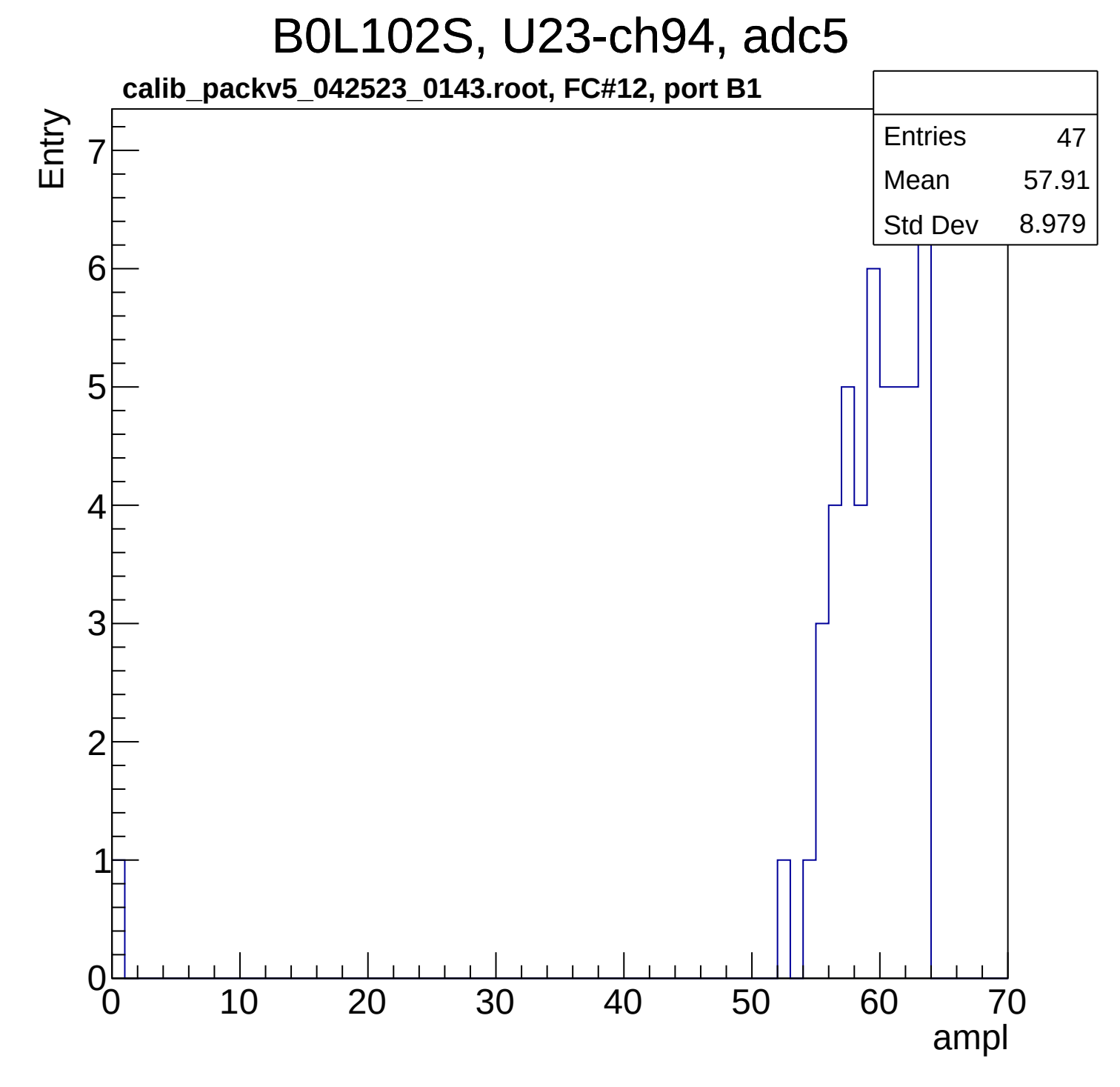
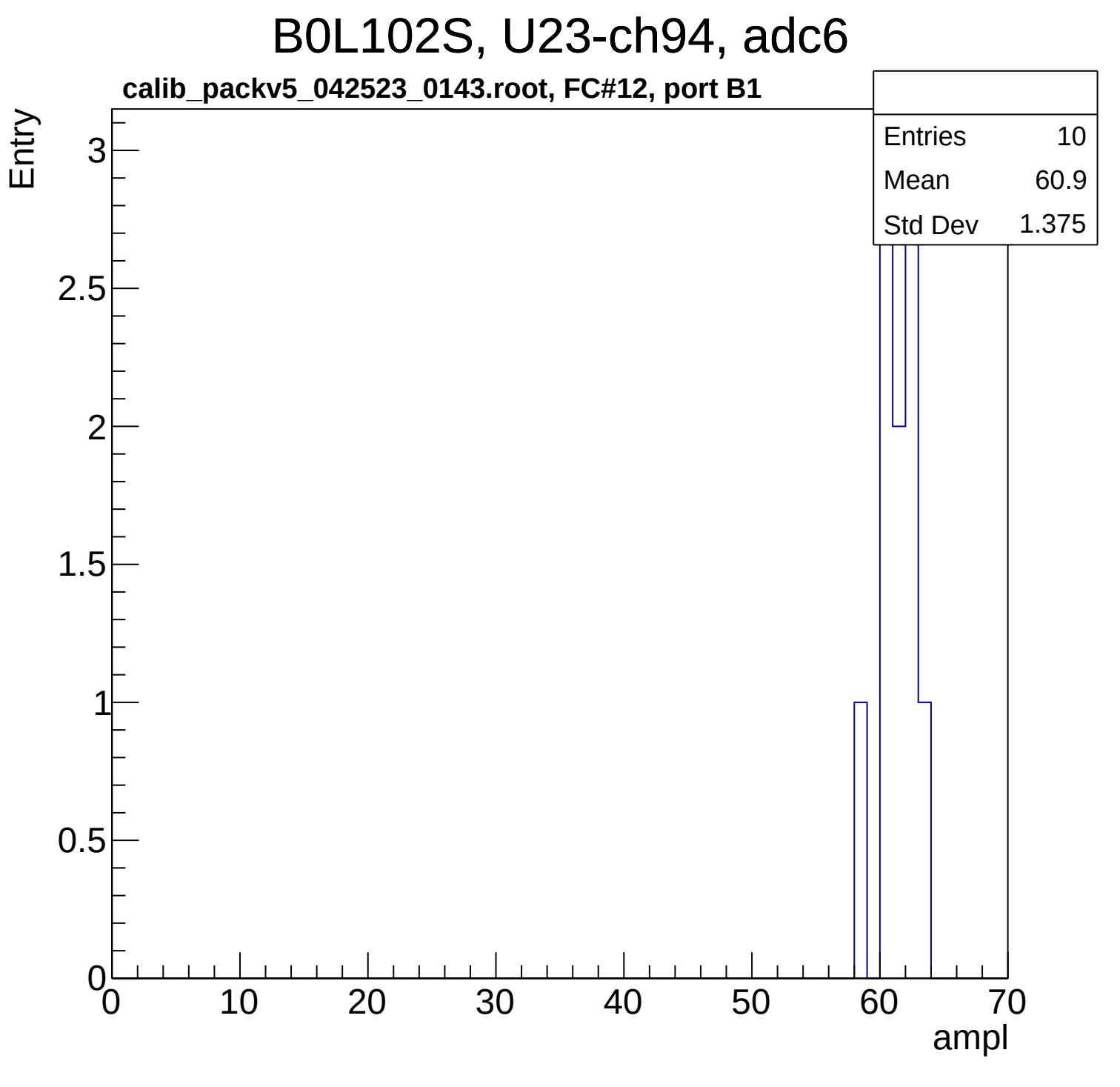
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch94, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 55.05
Std Dev 3.798
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch94, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 57.91
Std Dev 8.979
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch94, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 60.9
Std Dev 1.375
Entry
ampl
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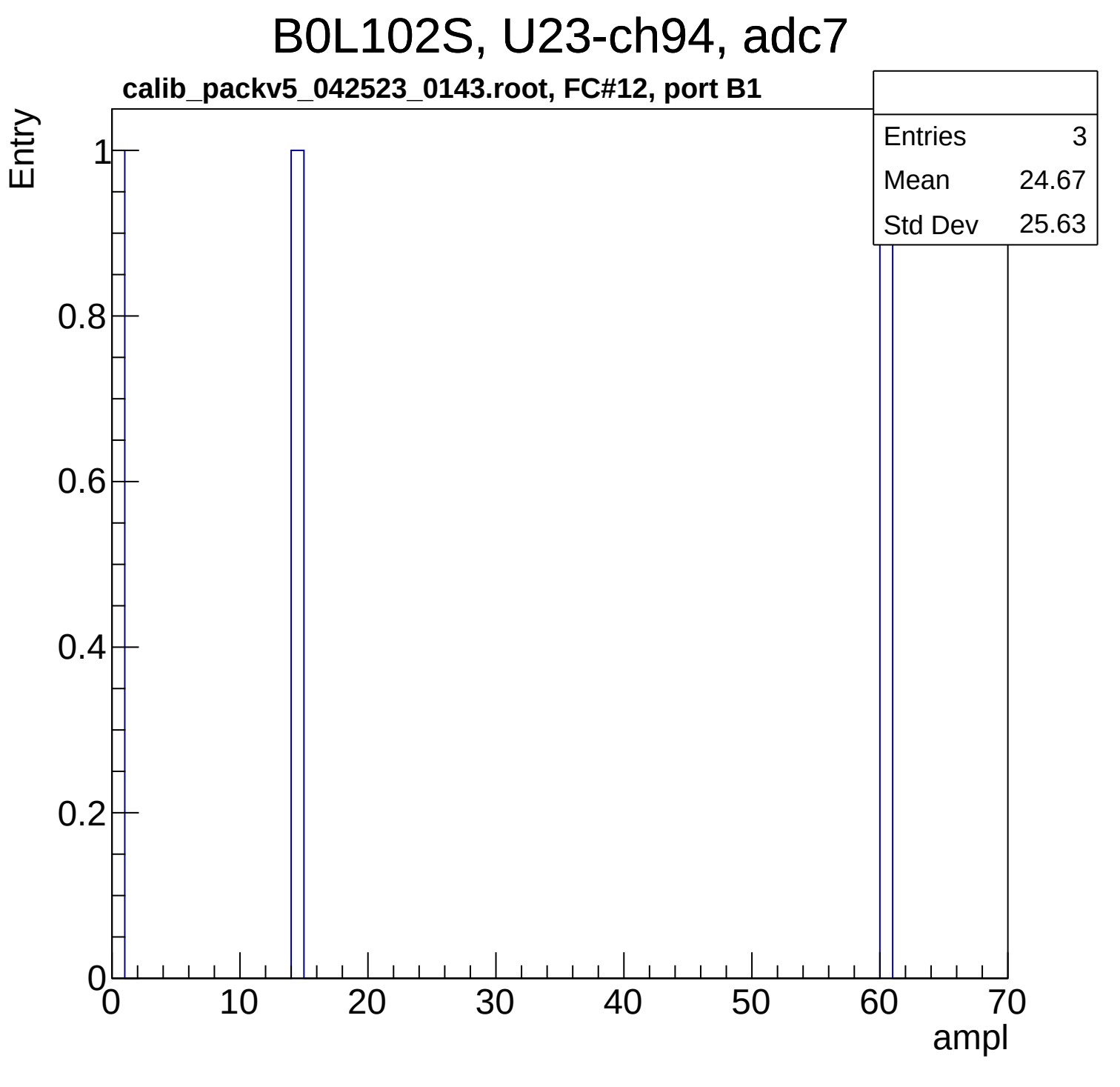
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch94, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 24.67
Std Dev 25.63
Entry
ampl
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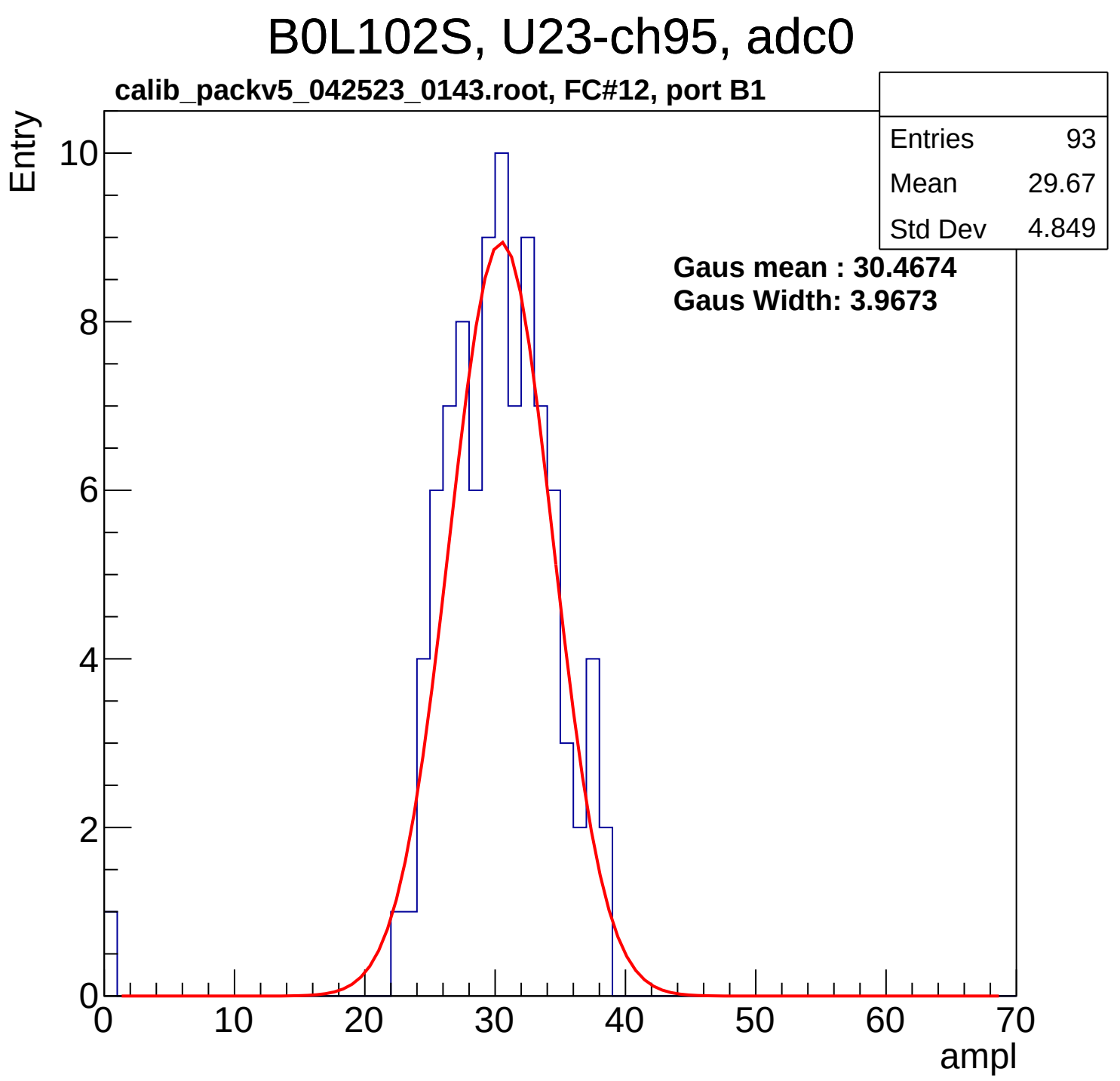
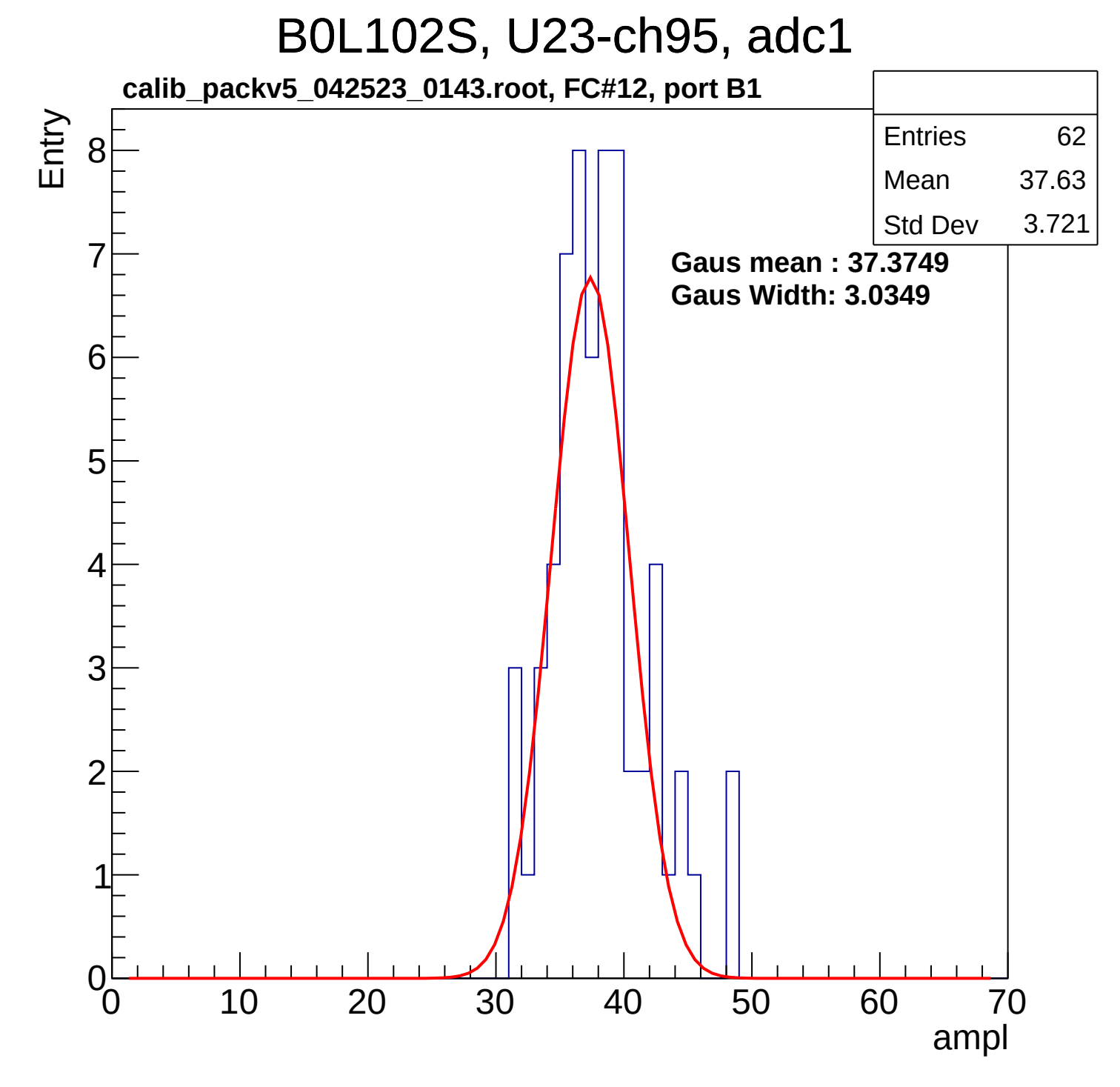
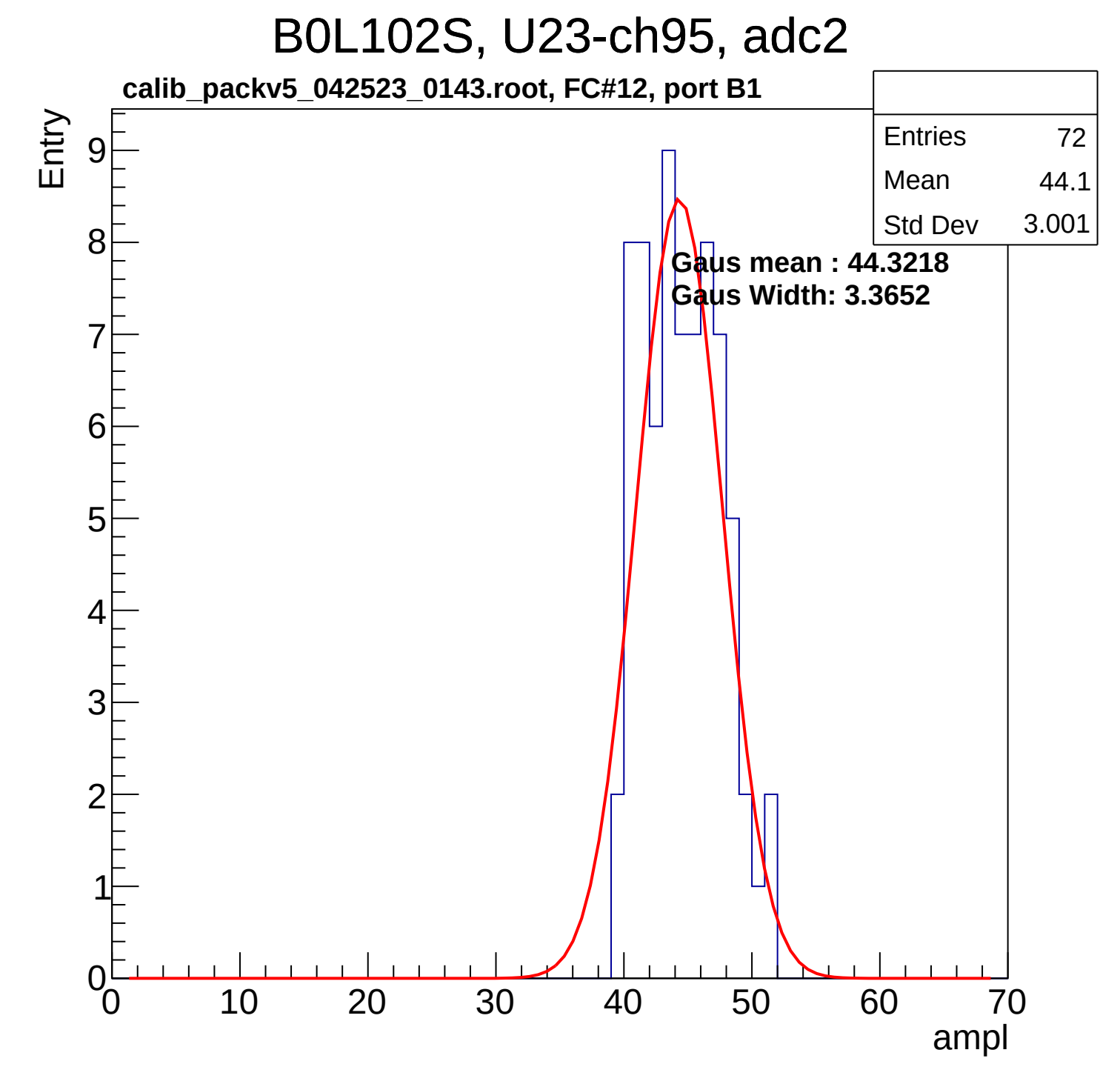
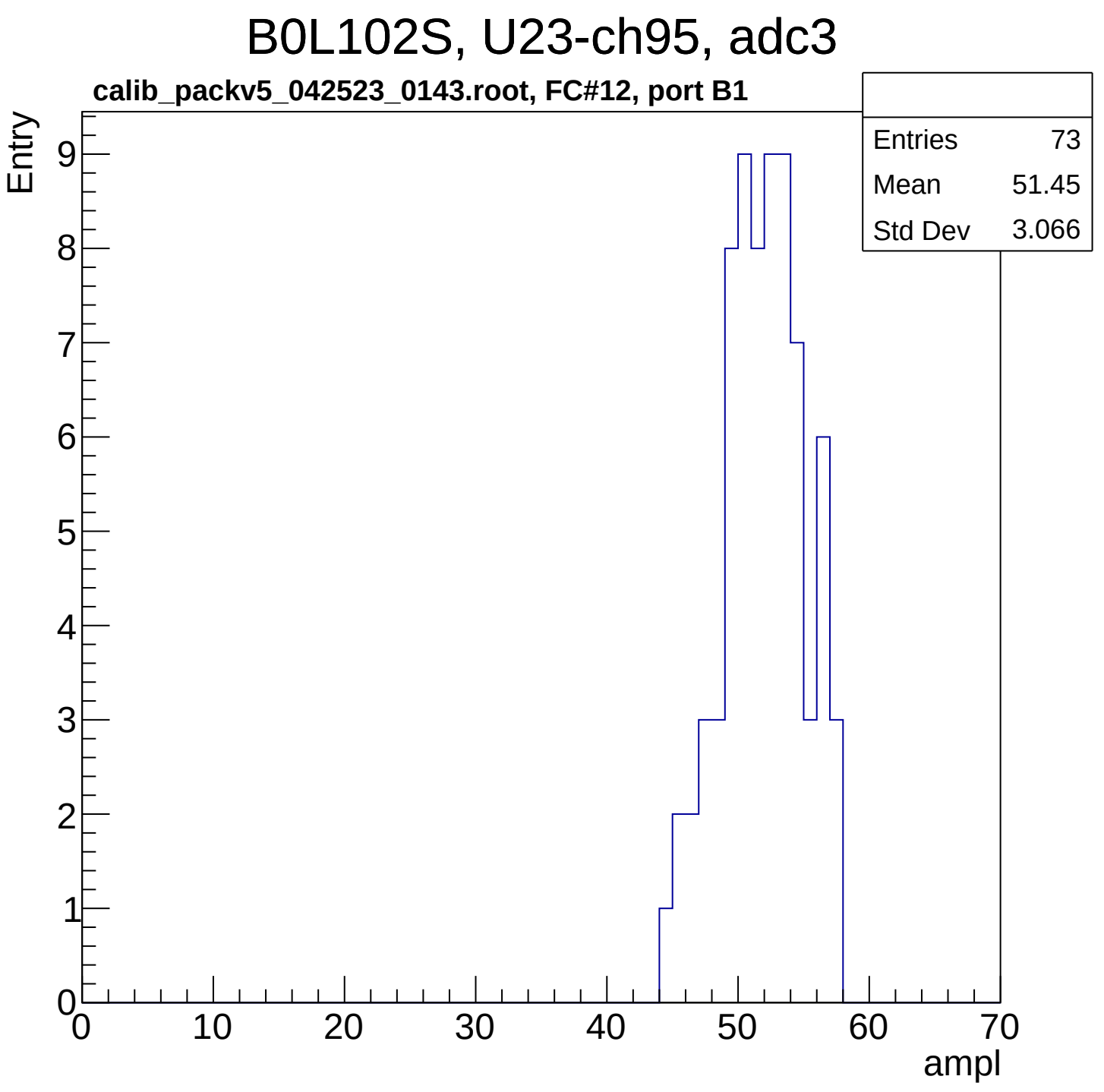
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U23-ch95, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 93
Mean 29.67
Std Dev 4.849
Gaus mean : 30.4674
Gaus Width: 3.9673
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch95, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 37.63
Std Dev 3.721
Gaus mean : 37.3749
Gaus Width: 3.0349
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch95, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 44.1
Std Dev 3.001
Gaus mean : 44.3218
Gaus Width: 3.3652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch95, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 51.45
Std Dev 3.066
Entry
ampl
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3
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5
6
7
8
9
0
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40
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60
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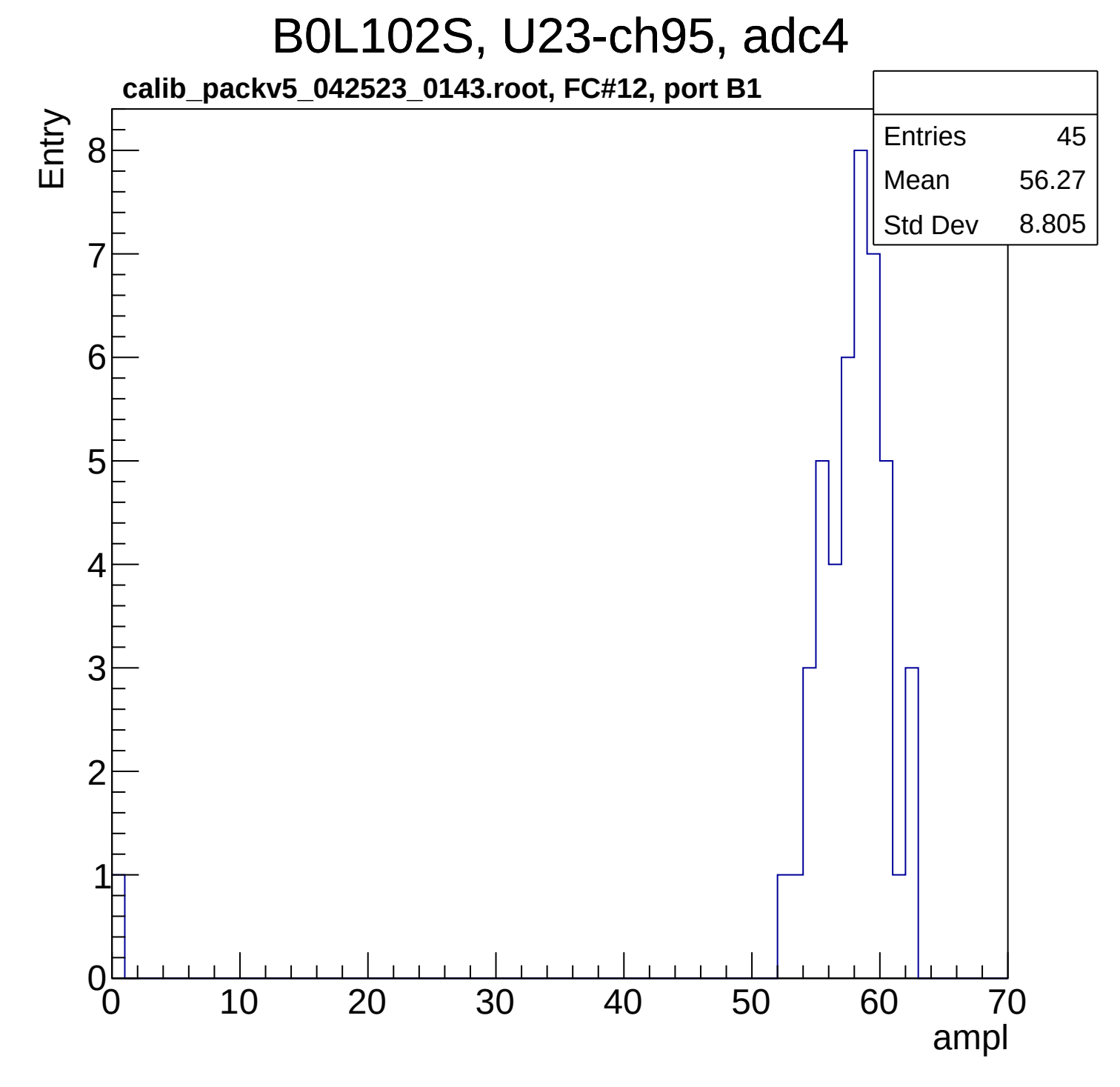
B0L102S, U23-ch95, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 56.27
Std Dev 8.805
Entry
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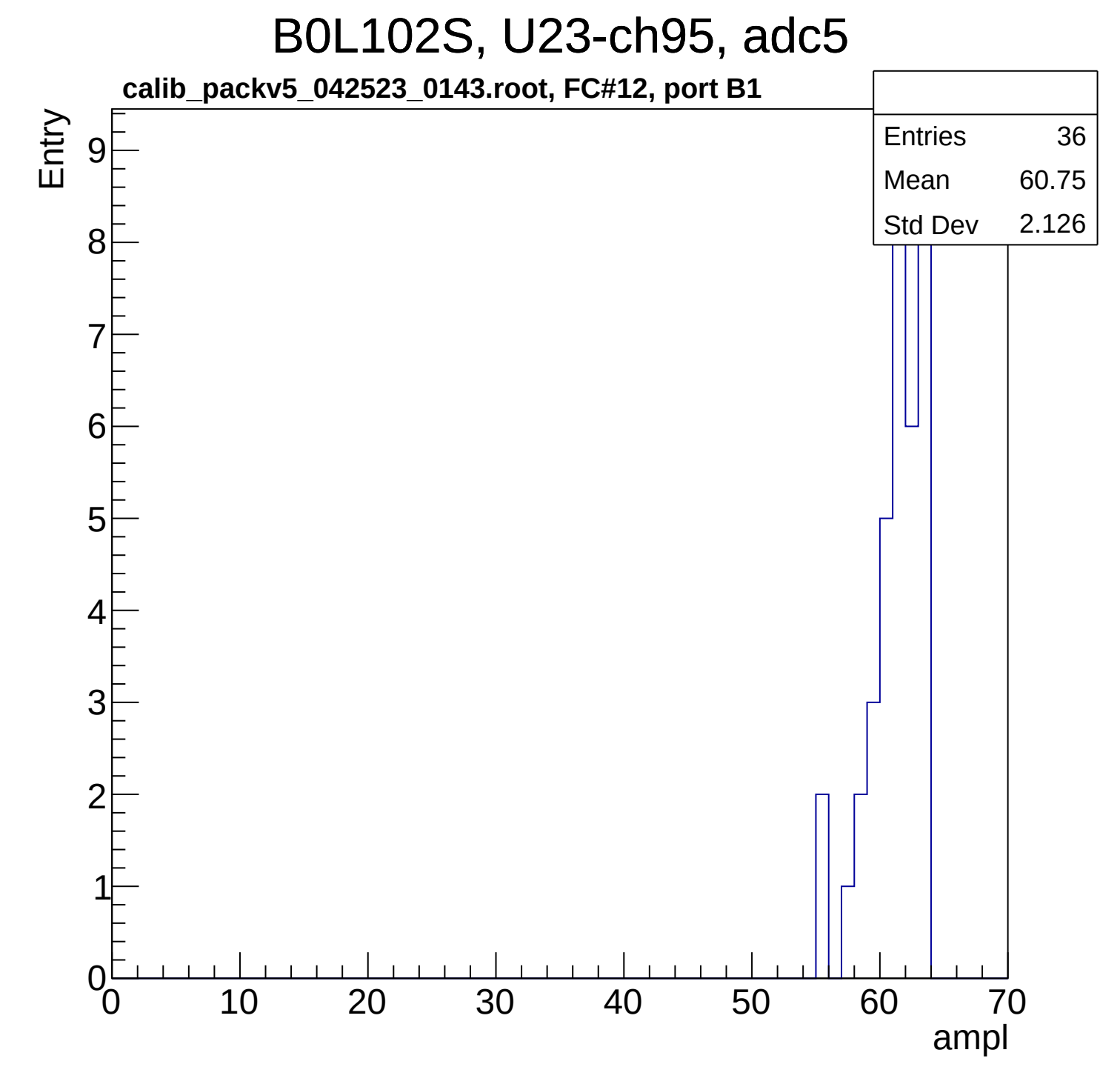
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch95, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 60.75
Std Dev 2.126
Entry
ampl
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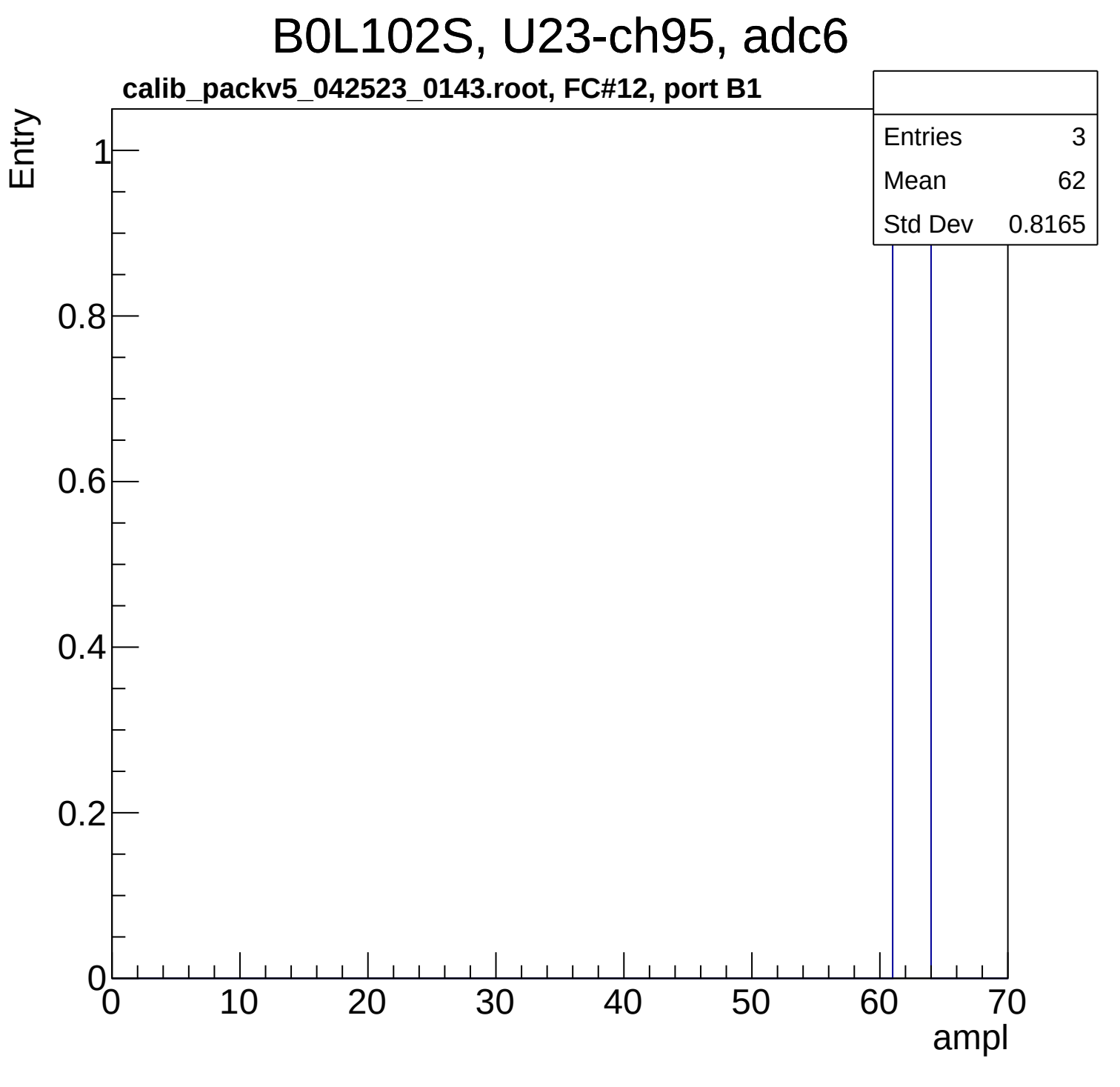
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch95, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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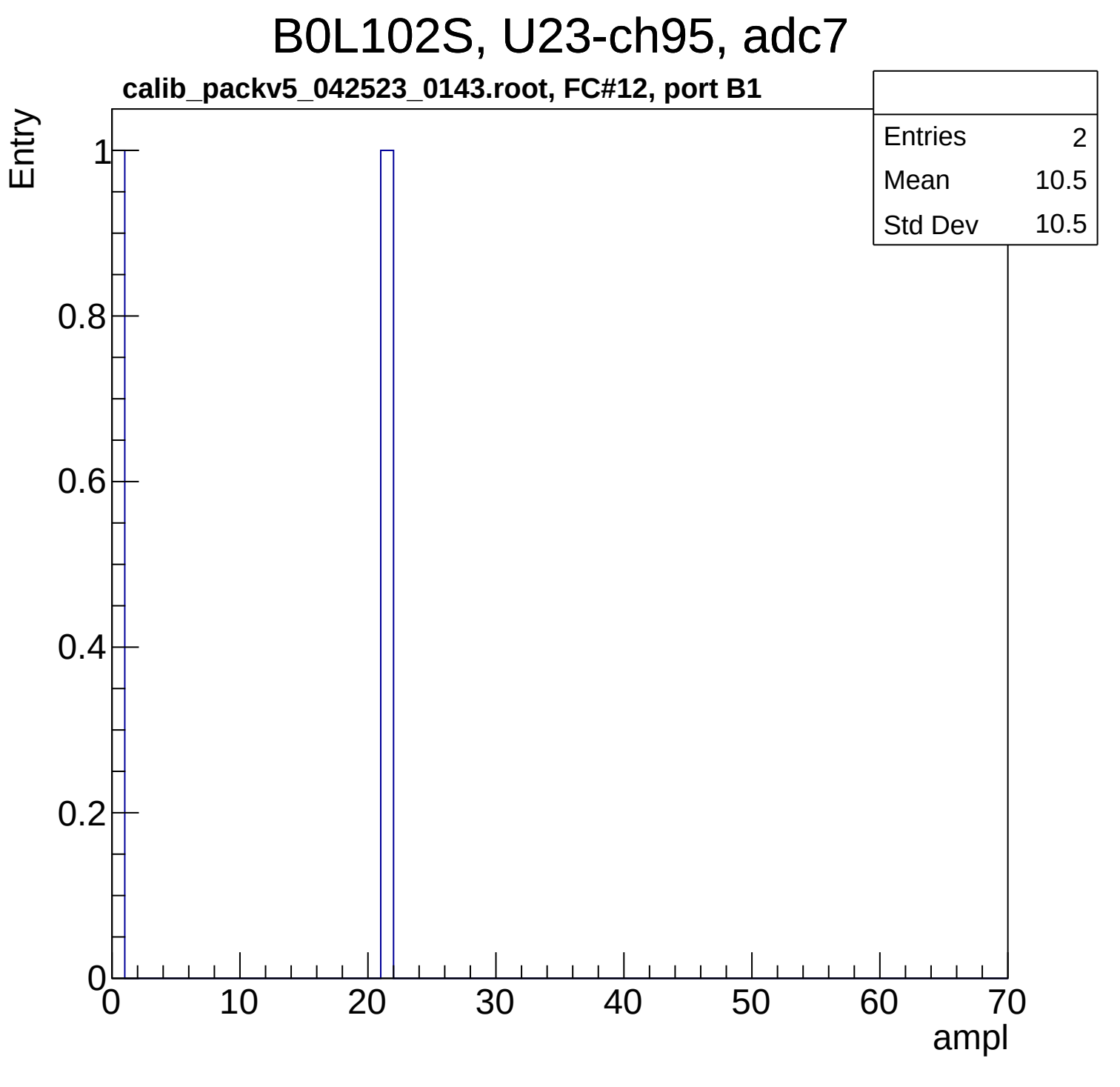
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch95, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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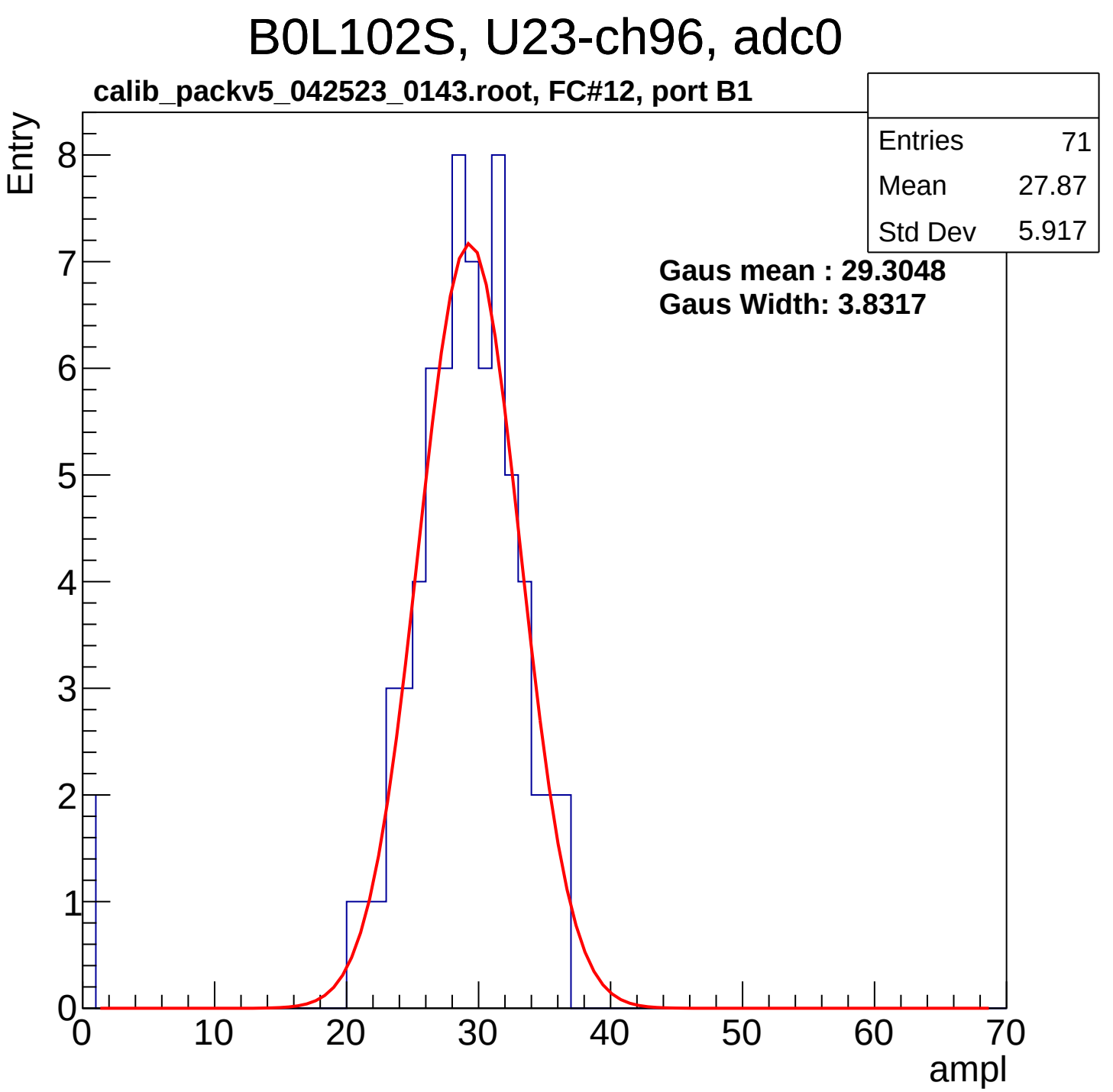
50
60
70
ampl


B0L102S, U23-ch96, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 27.87
Std Dev 5.917
Gaus mean : 29.3048
Gaus Width: 3.8317
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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50
60
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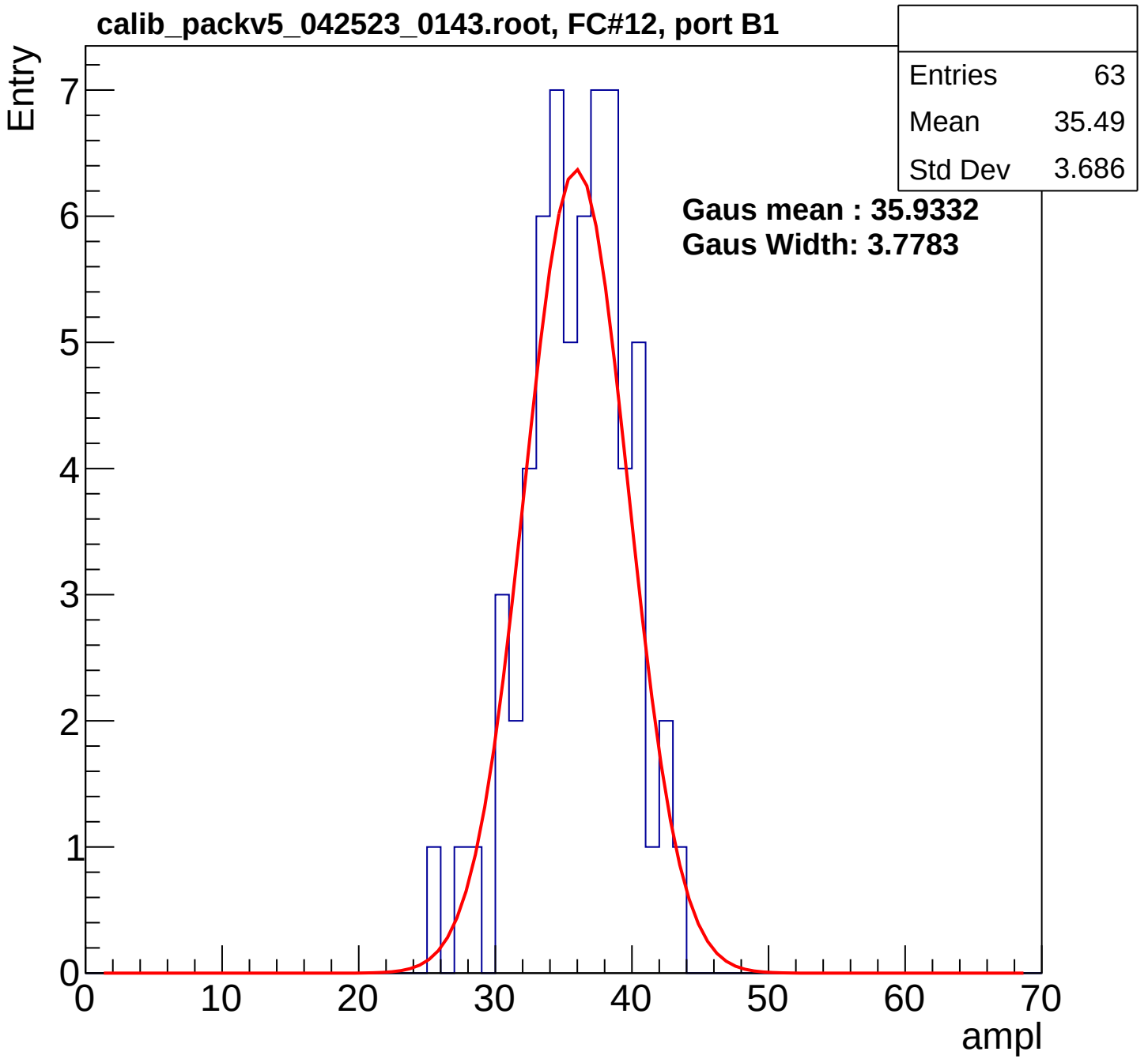
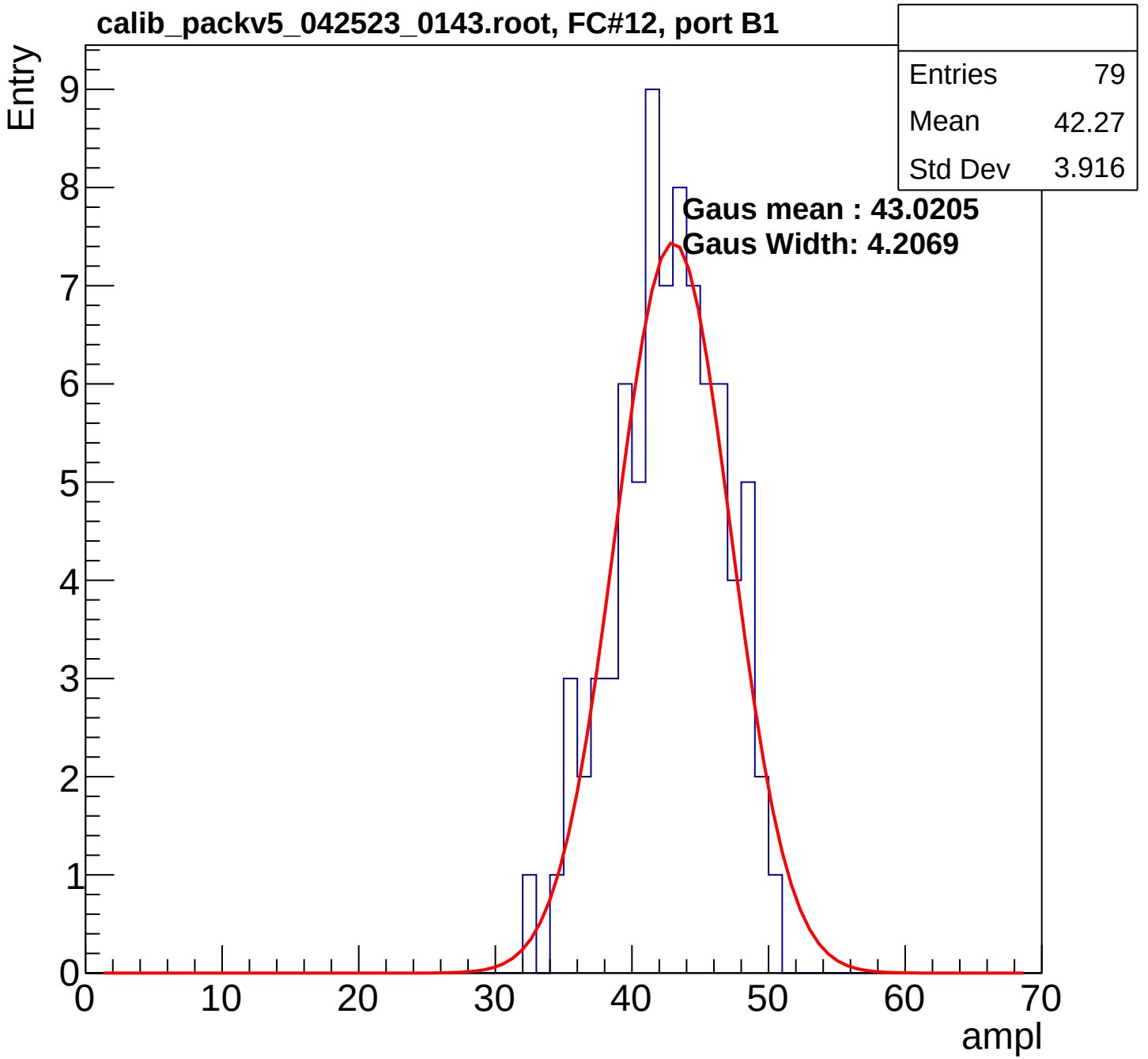

B0L102S, U23-ch96, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 35.49
Std Dev 3.686
Gaus mean : 35.9332
Gaus Width: 3.7783
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch96, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 42.27
Std Dev 3.916
Gaus mean : 43.0205
Gaus Width: 4.2069
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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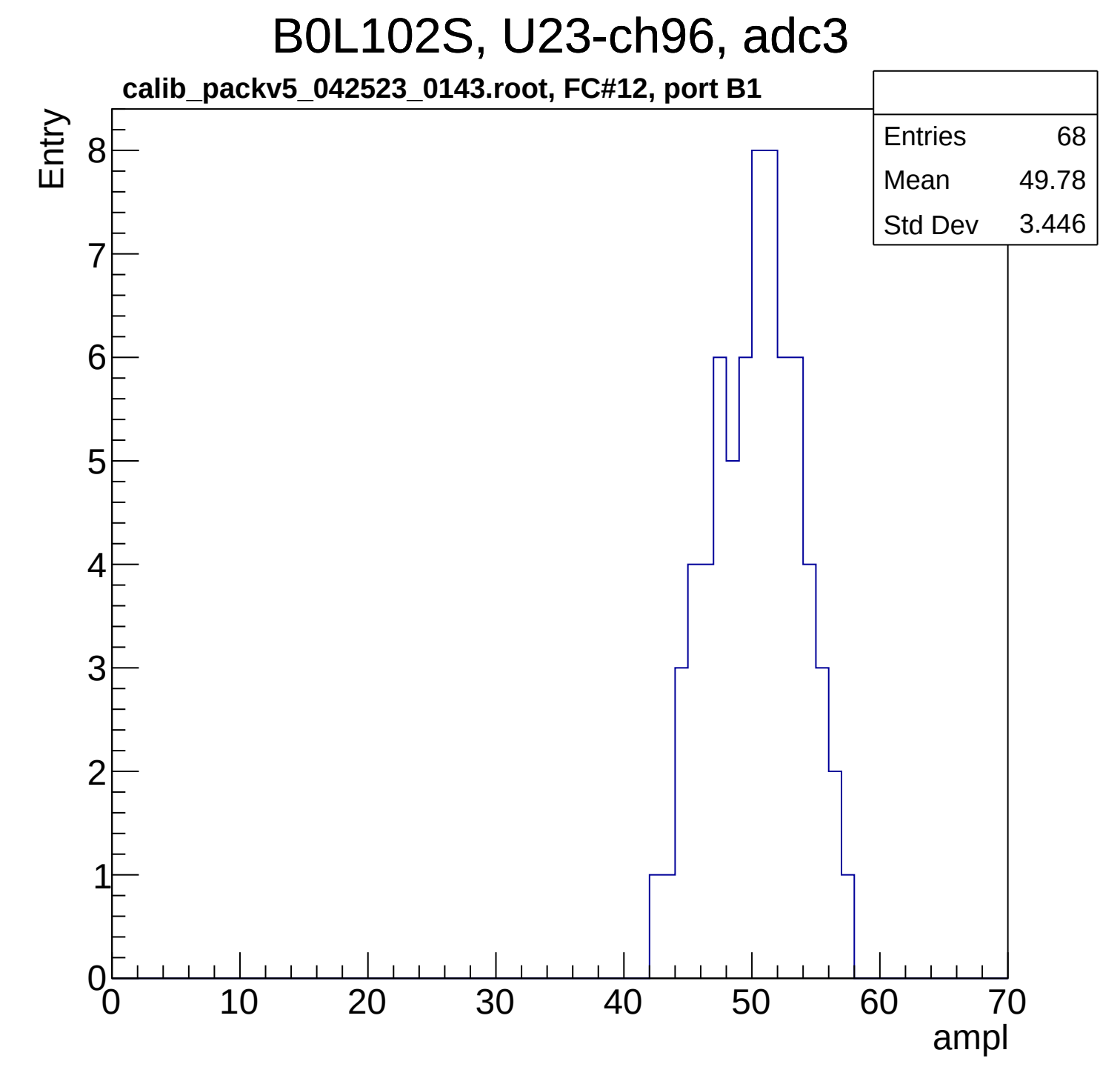
B0L102S, U23-ch96, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 49.78
Std Dev 3.446
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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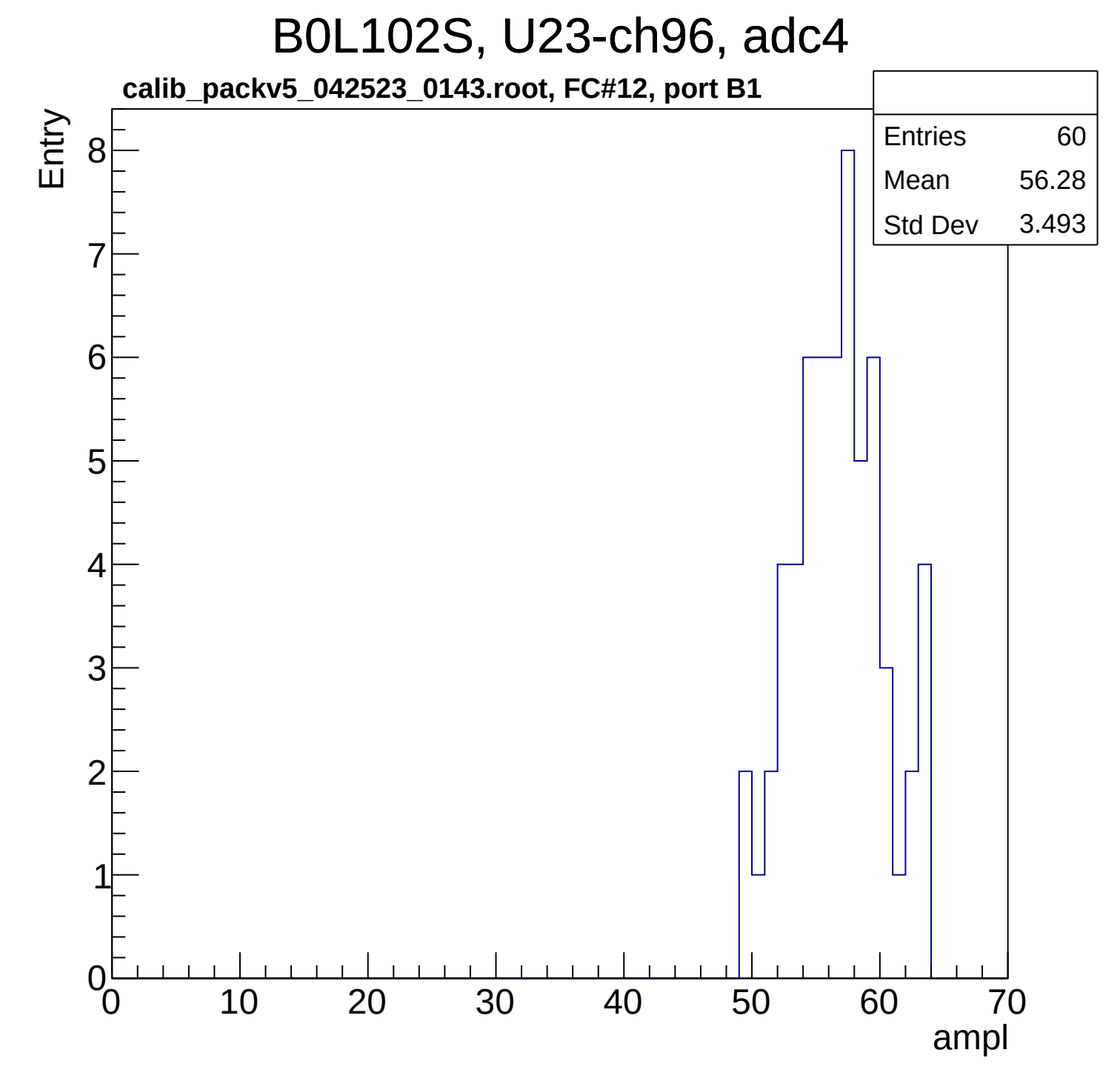
B0L102S, U23-ch96, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 56.28
Std Dev 3.493
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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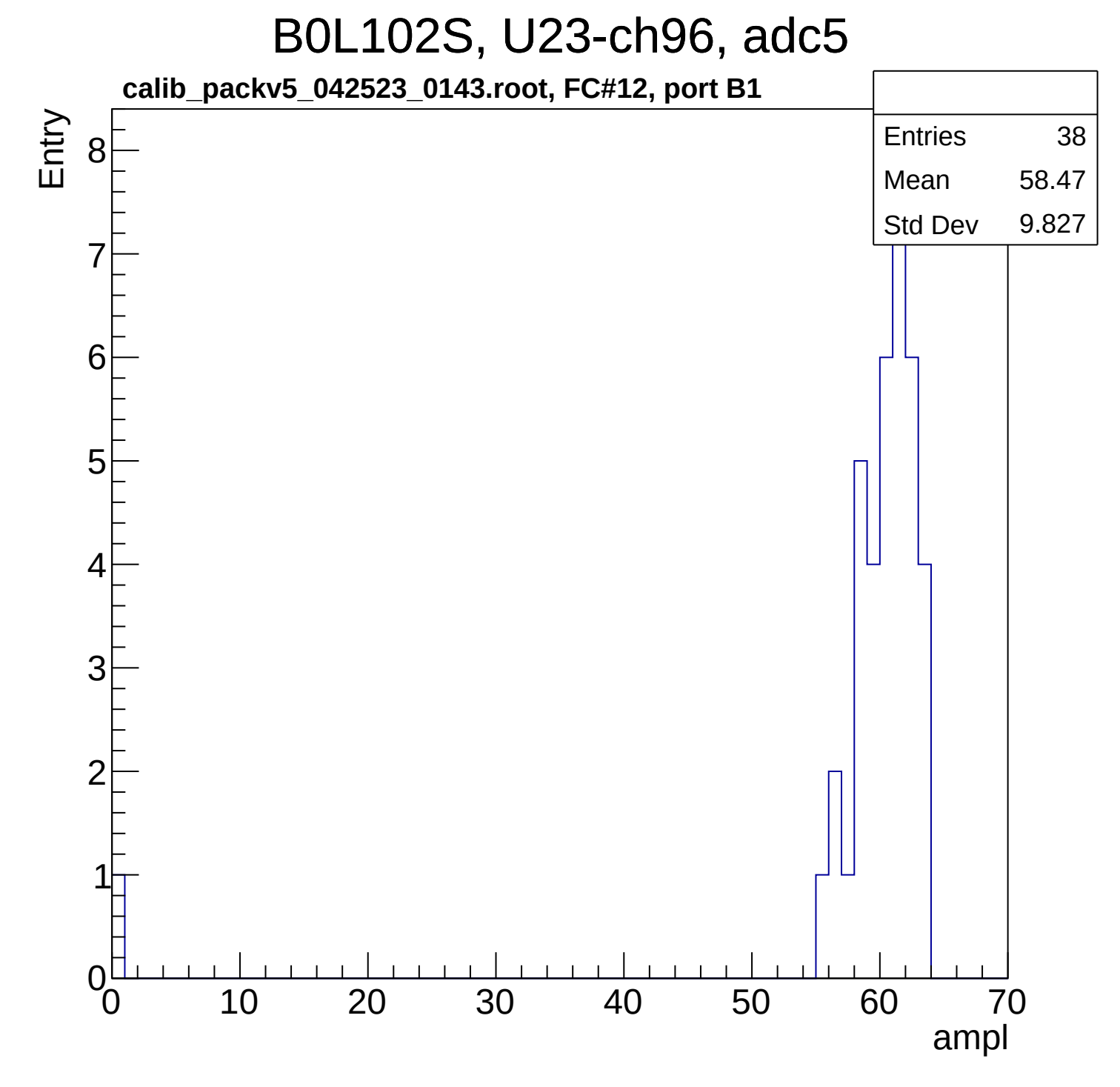
B0L102S, U23-ch96, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.47
Std Dev 9.827
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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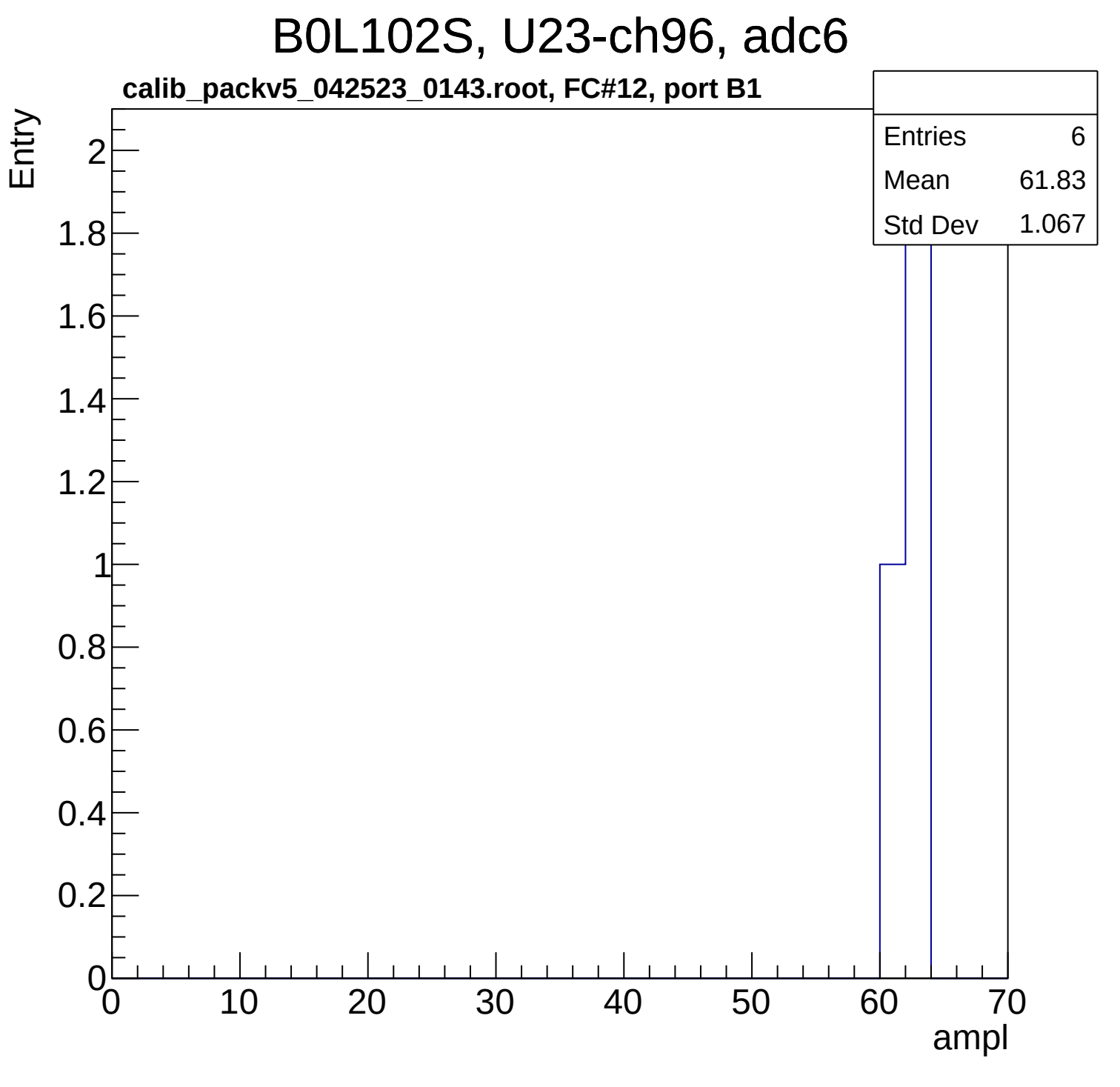
B0L102S, U23-ch96, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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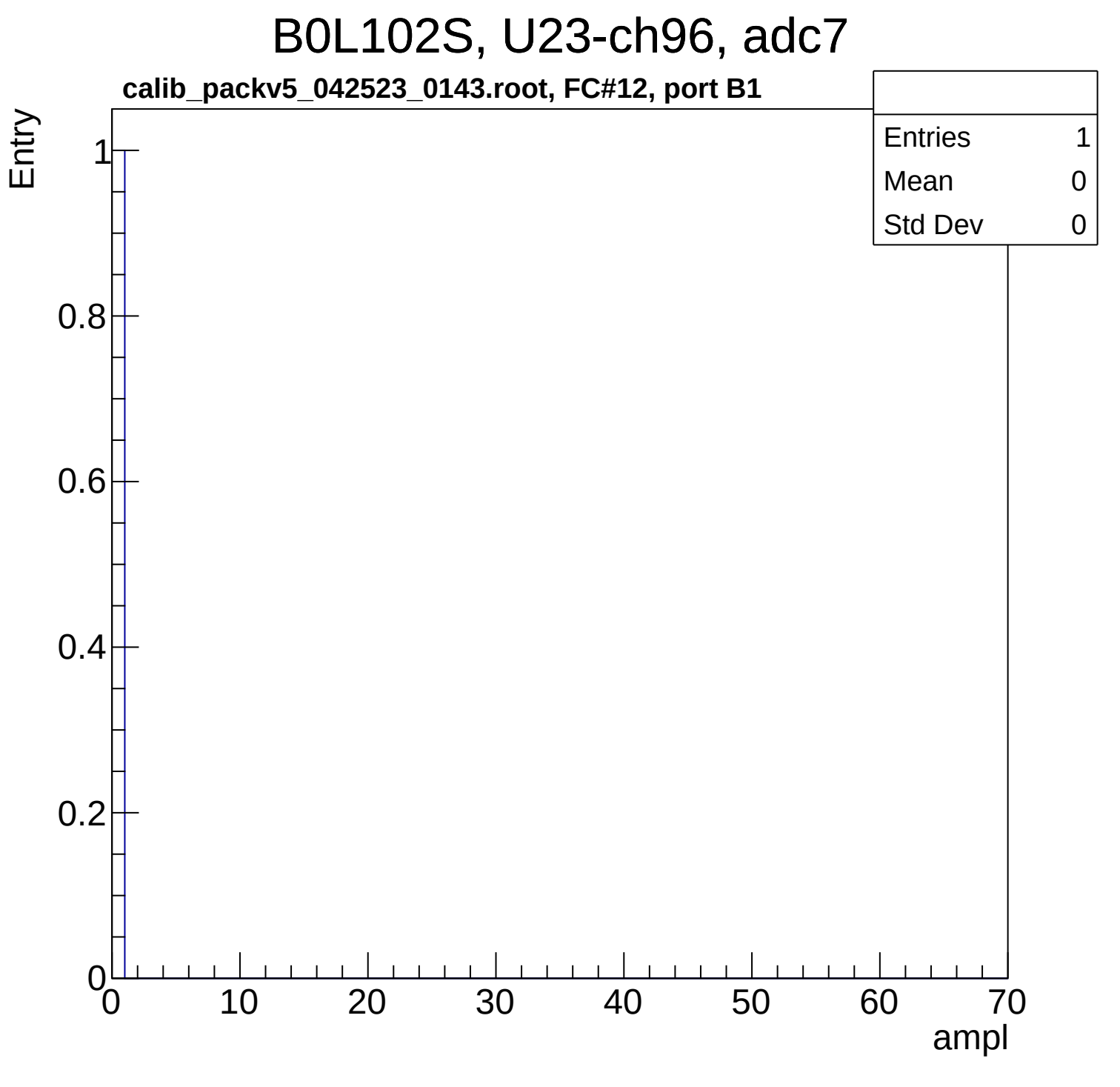
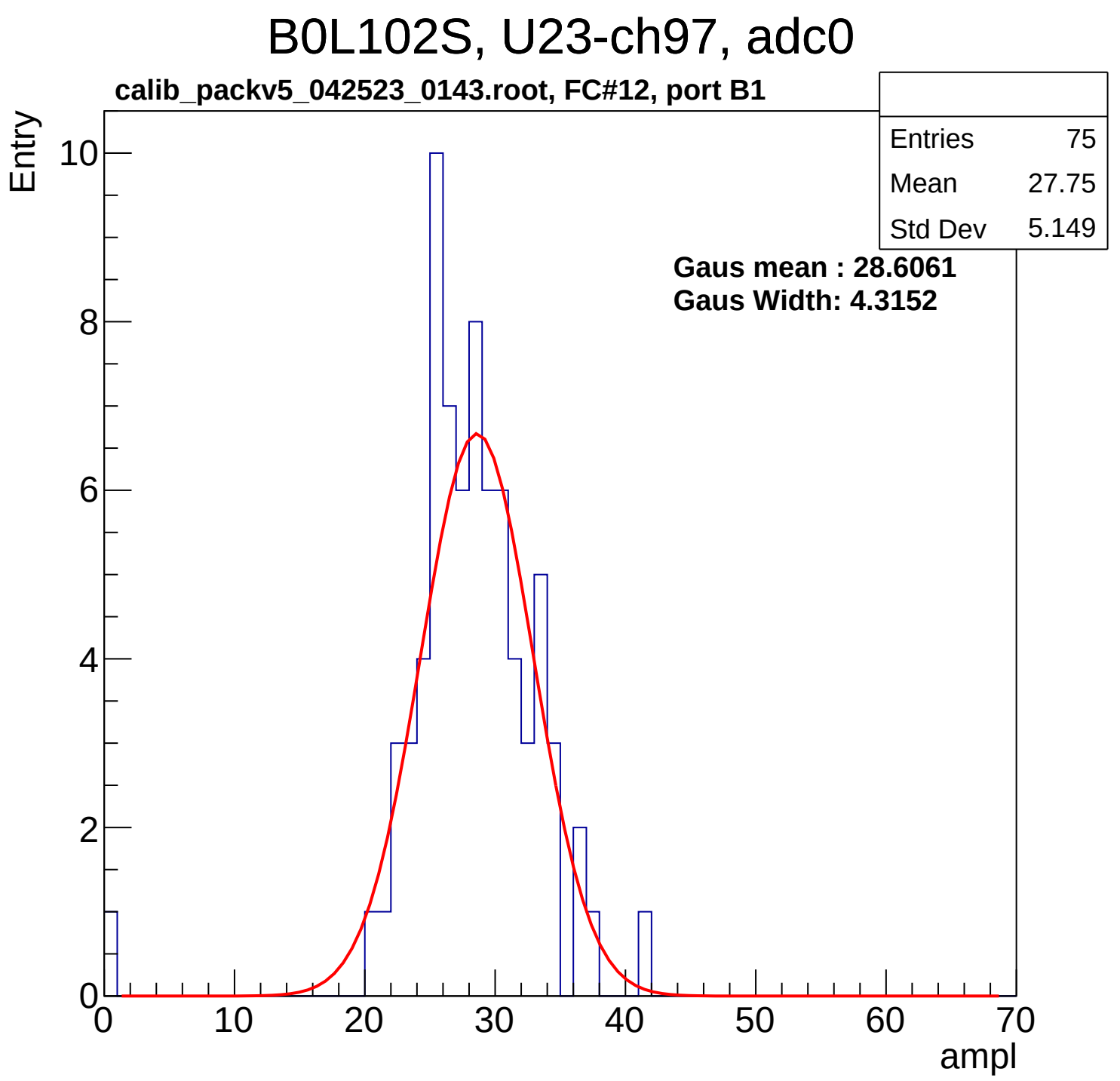
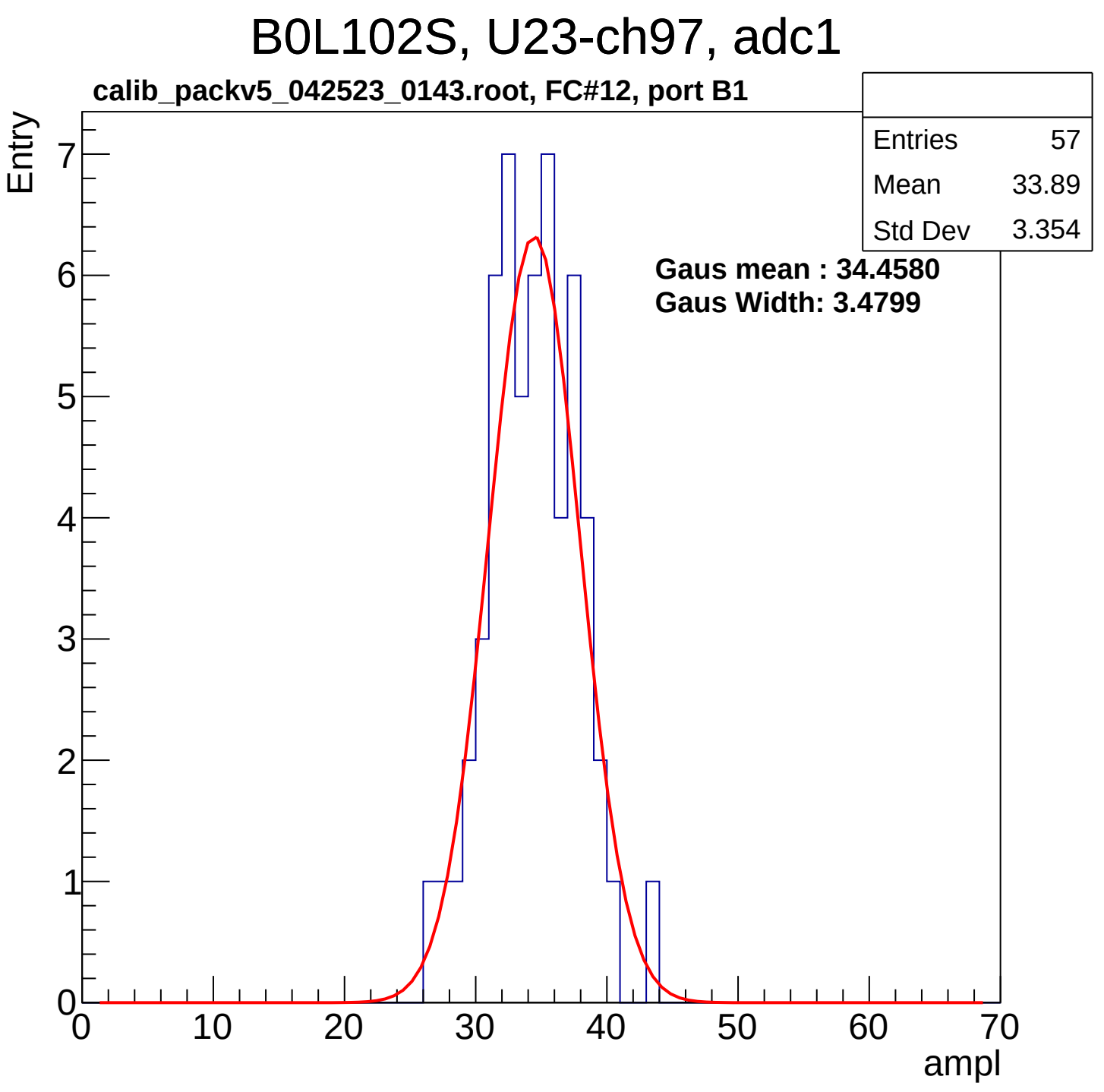
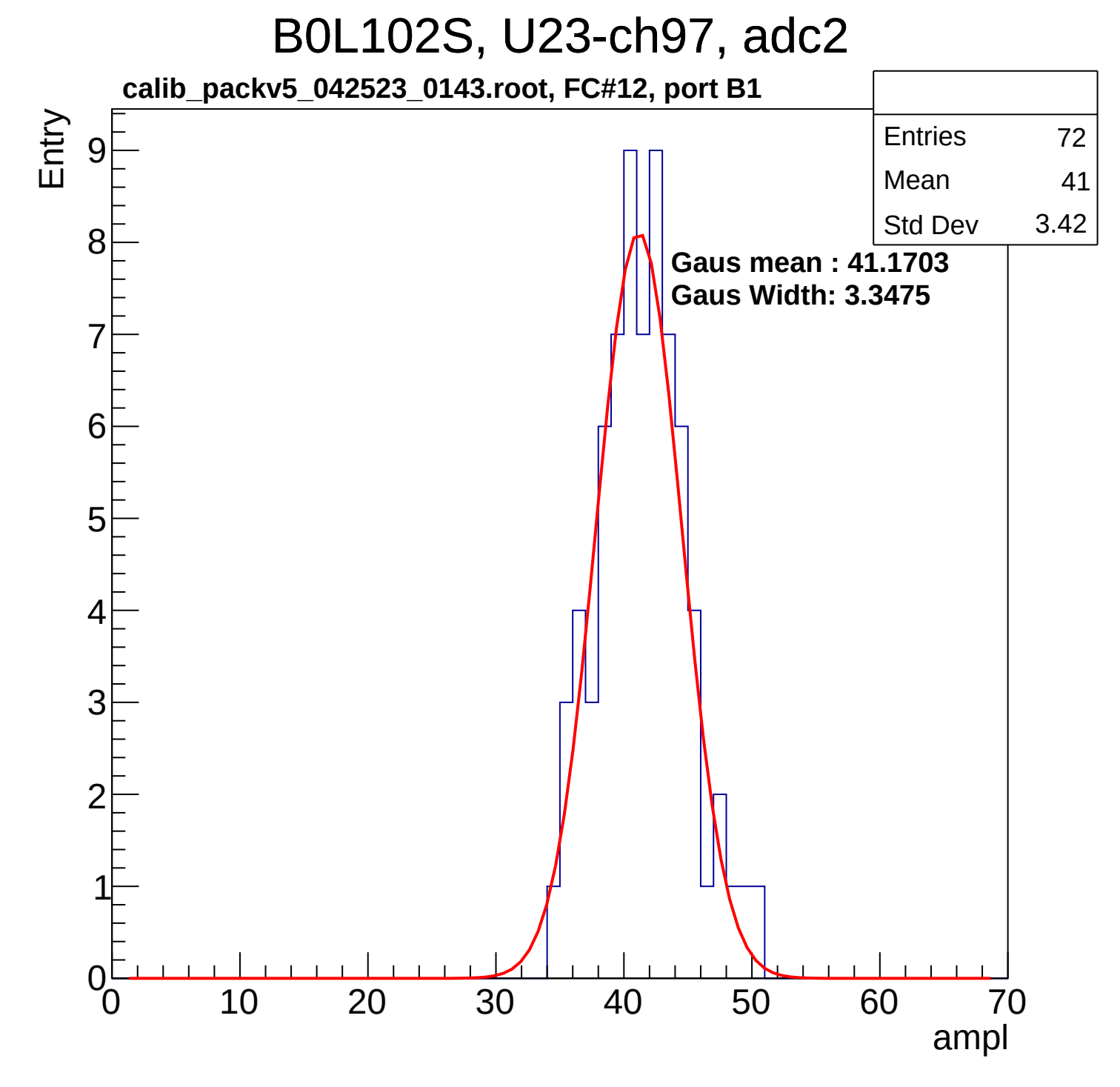

B0L102S, U23-ch96, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch97, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 27.75
Std Dev 5.149
Gaus mean : 28.6061
Gaus Width: 4.3152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch97, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 33.89
Std Dev 3.354
Gaus mean : 34.4580
Gaus Width: 3.4799
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch97, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 41
Std Dev 3.42
Gaus mean : 41.1703
Gaus Width: 3.3475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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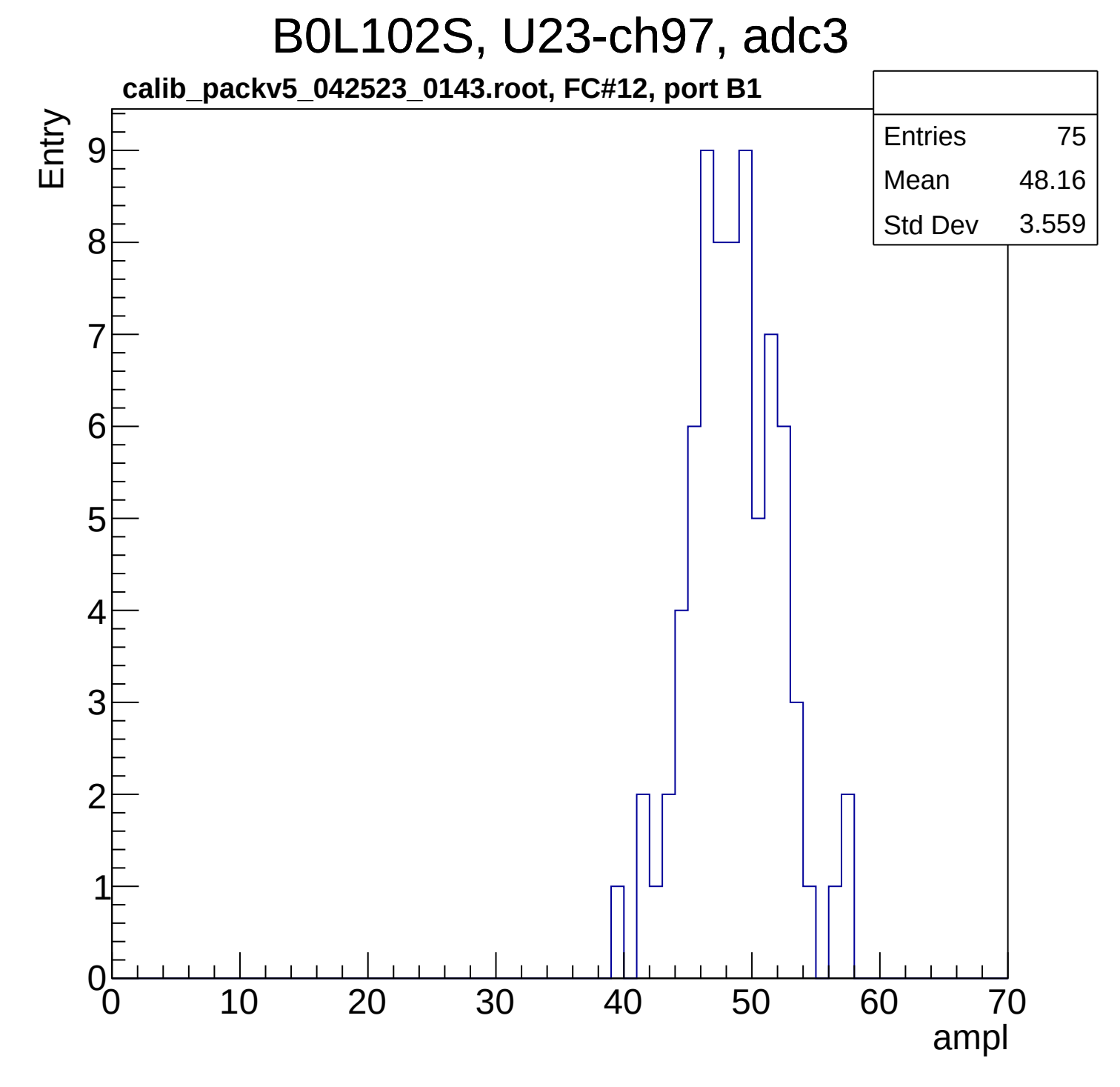
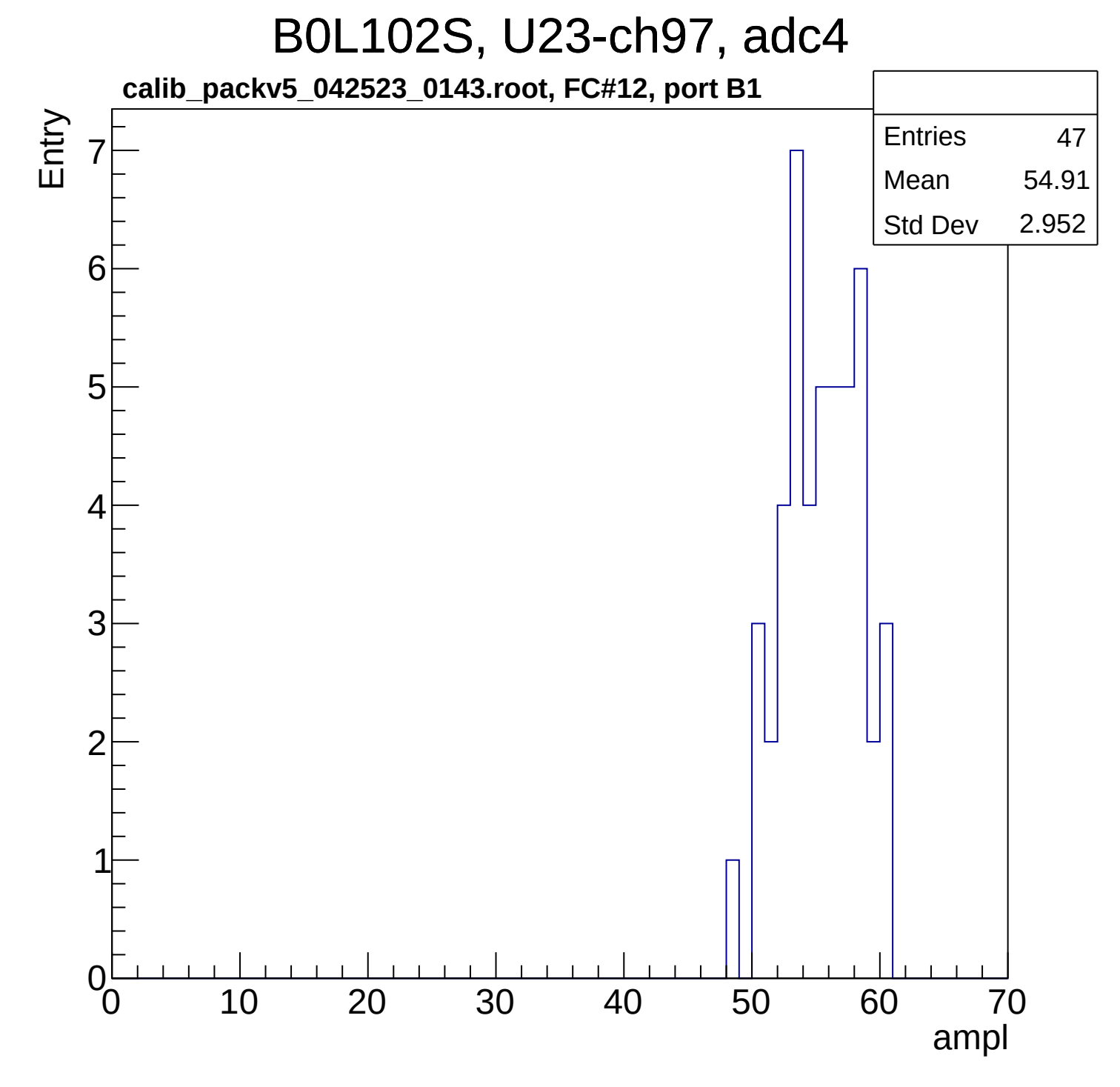
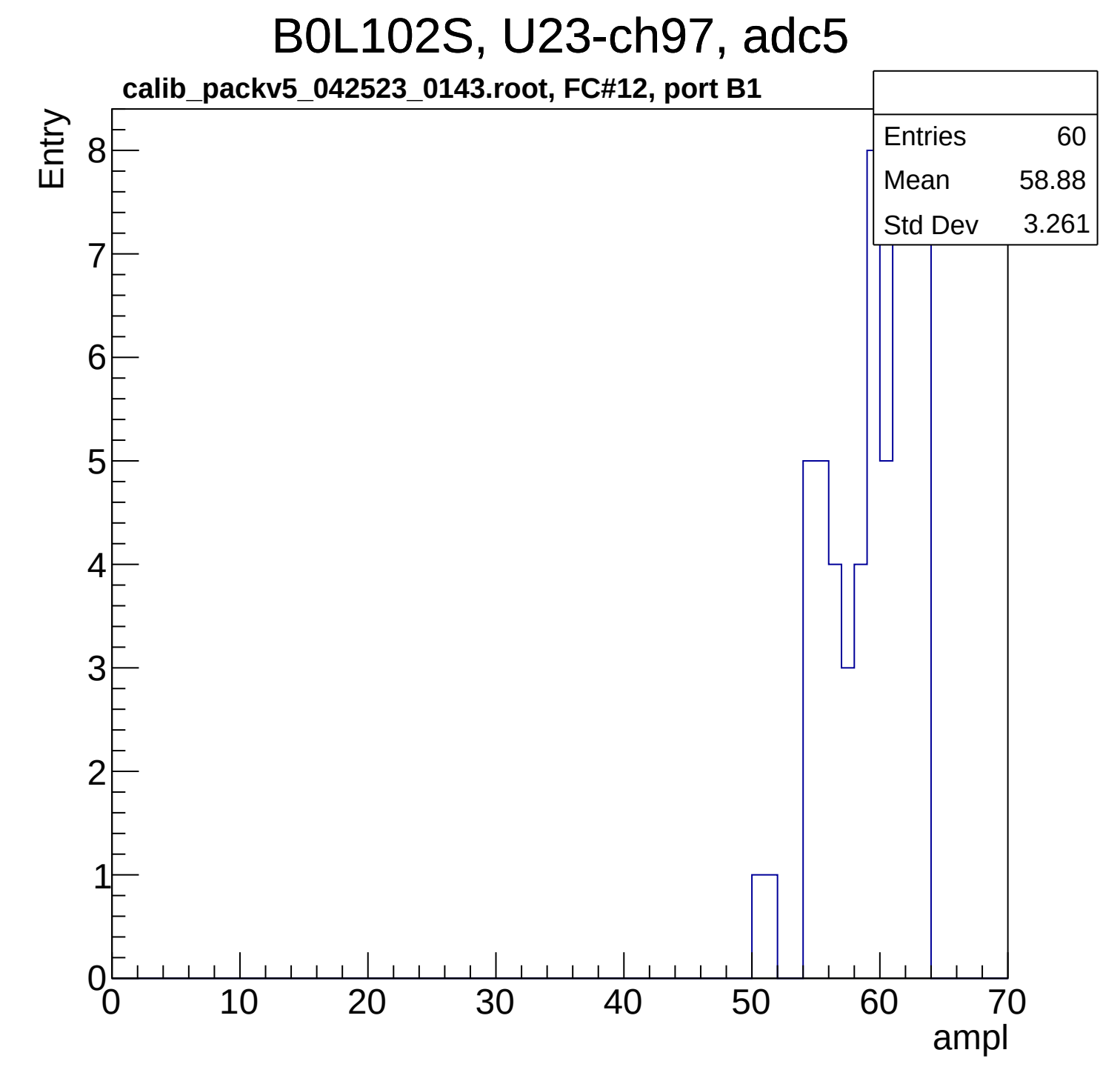
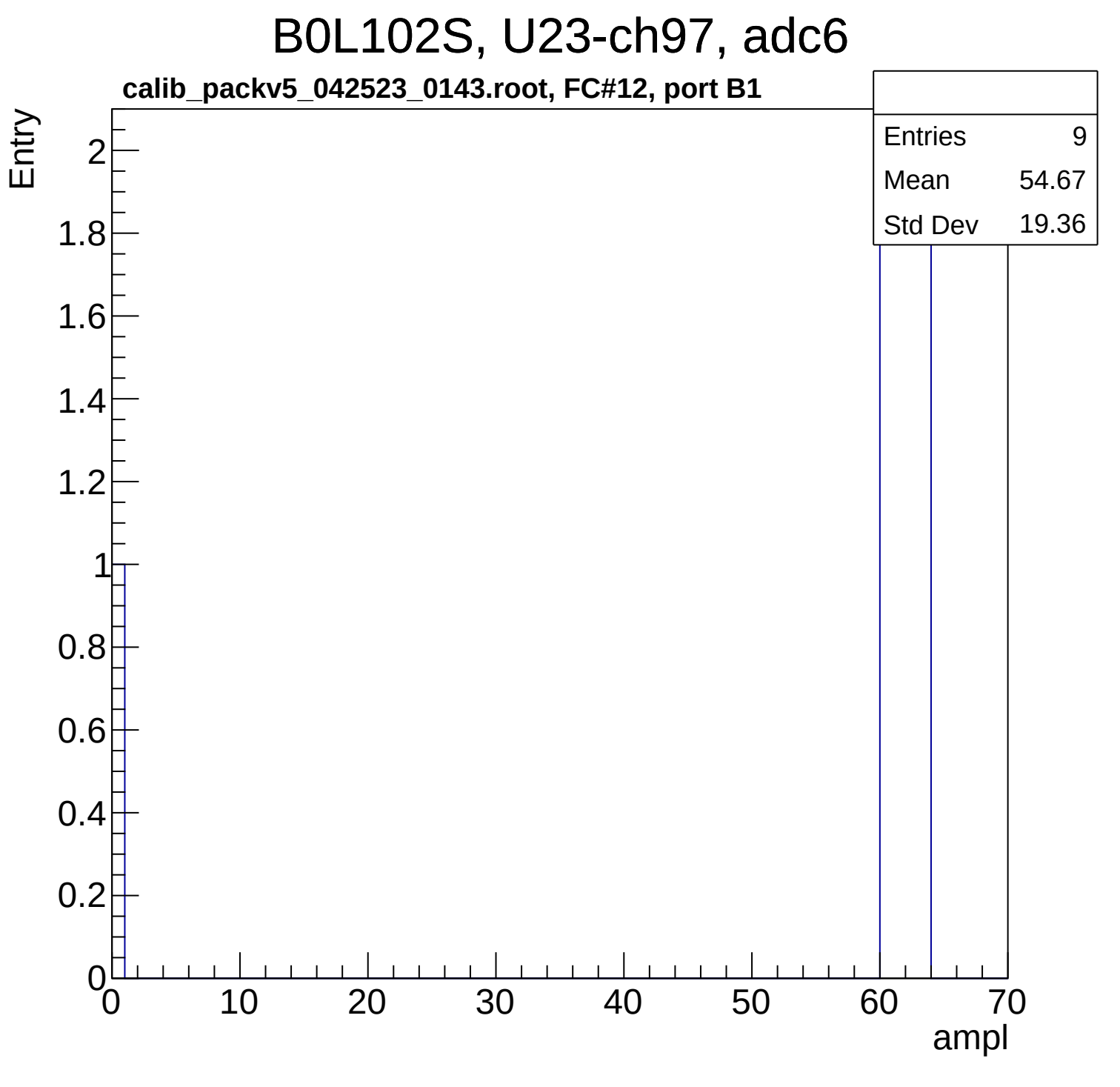
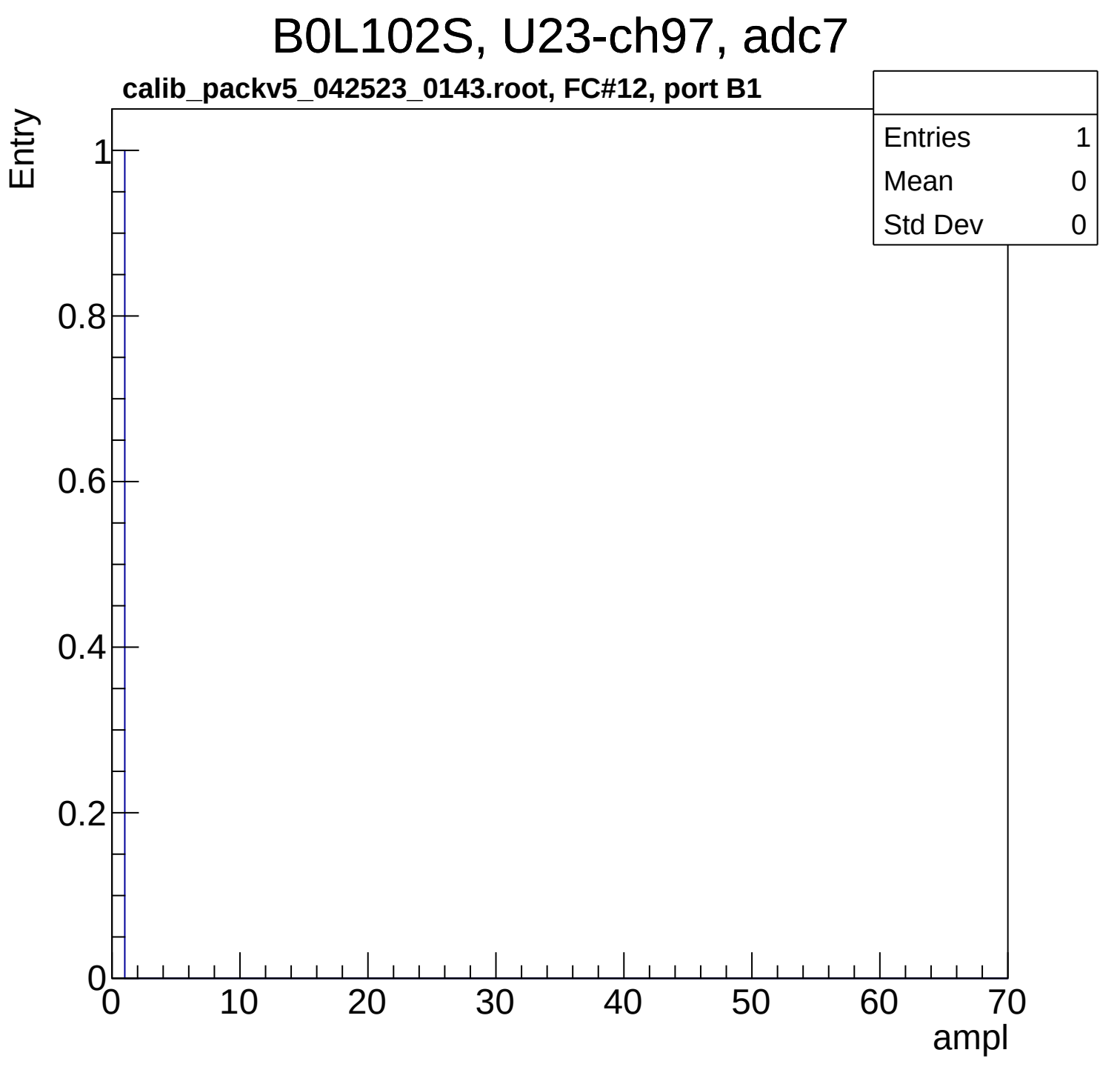
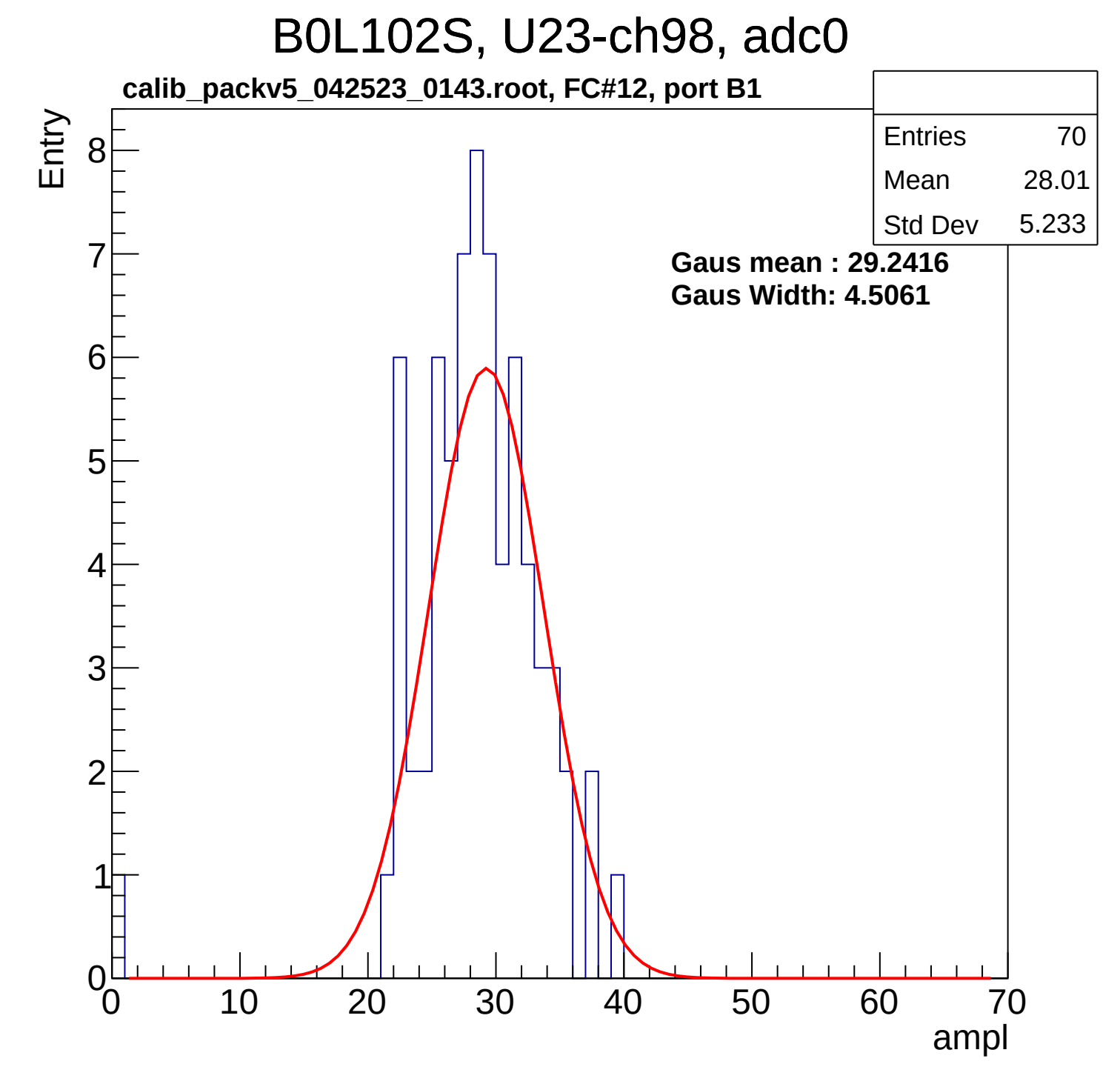

B0L102S, U23-ch97, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 48.16
Std Dev 3.559
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

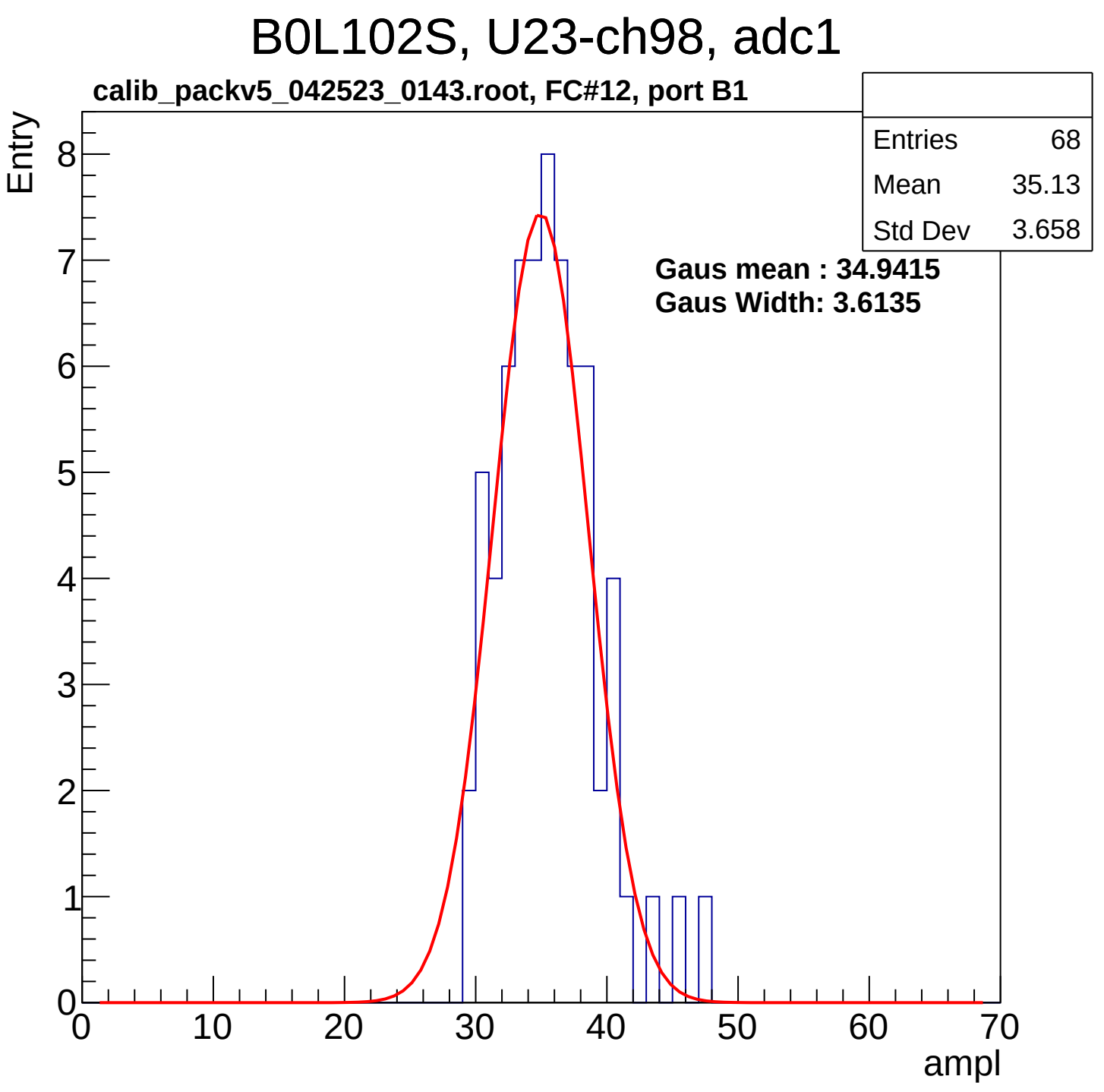

B0L102S, U23-ch97, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 54.91
Std Dev 2.952
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

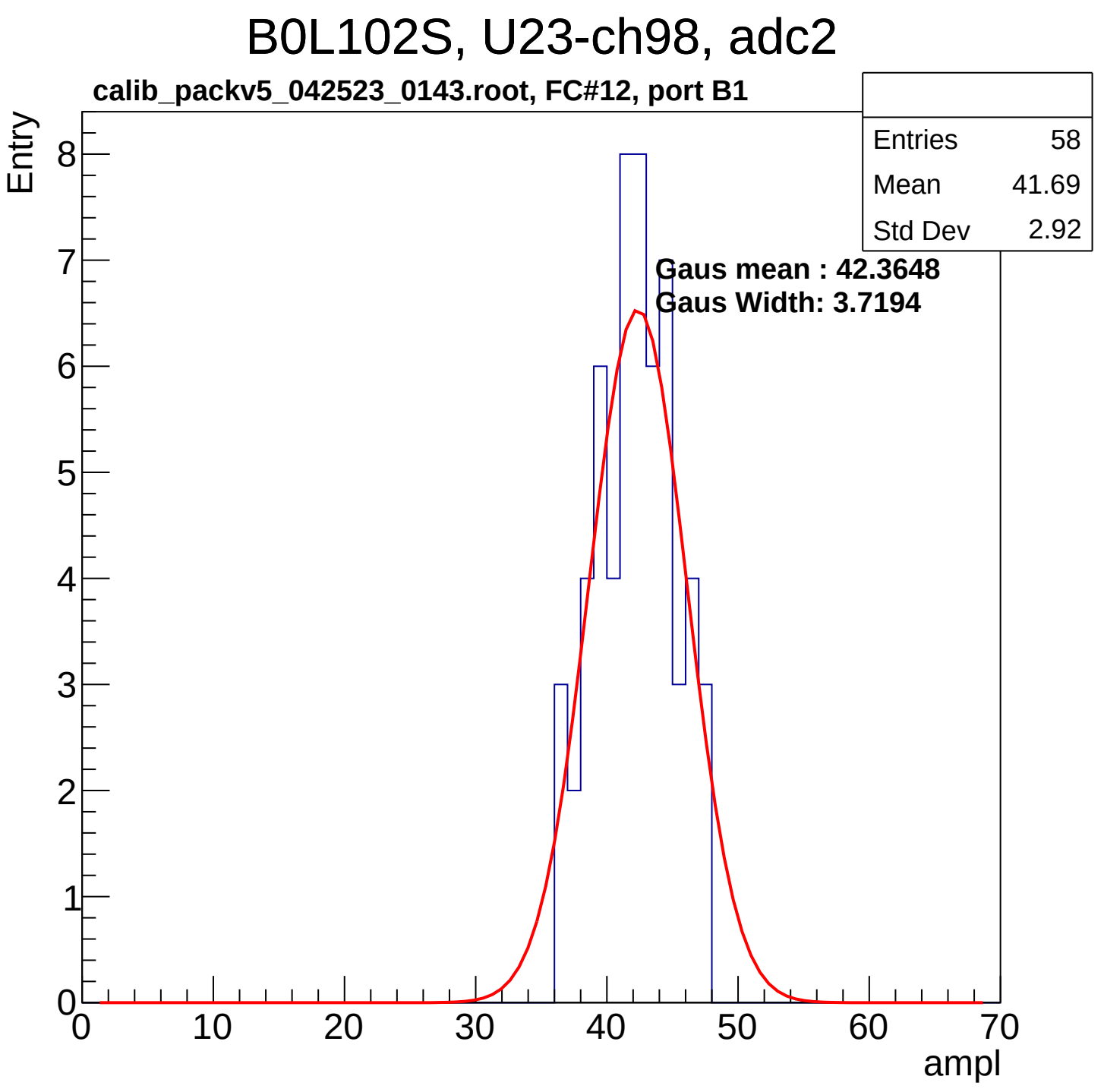

B0L102S, U23-ch97, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 58.88
Std Dev 3.261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch97, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 9
Mean 54.67
Std Dev 19.36
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch97, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch98, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 28.01
Std Dev 5.233
Gaus mean : 29.2416
Gaus Width: 4.5061
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch98, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 35.13
Std Dev 3.658
Gaus mean : 34.9415
Gaus Width: 3.6135
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch98, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 41.69
Std Dev 2.92
Gaus mean : 42.3648
Gaus Width: 3.7194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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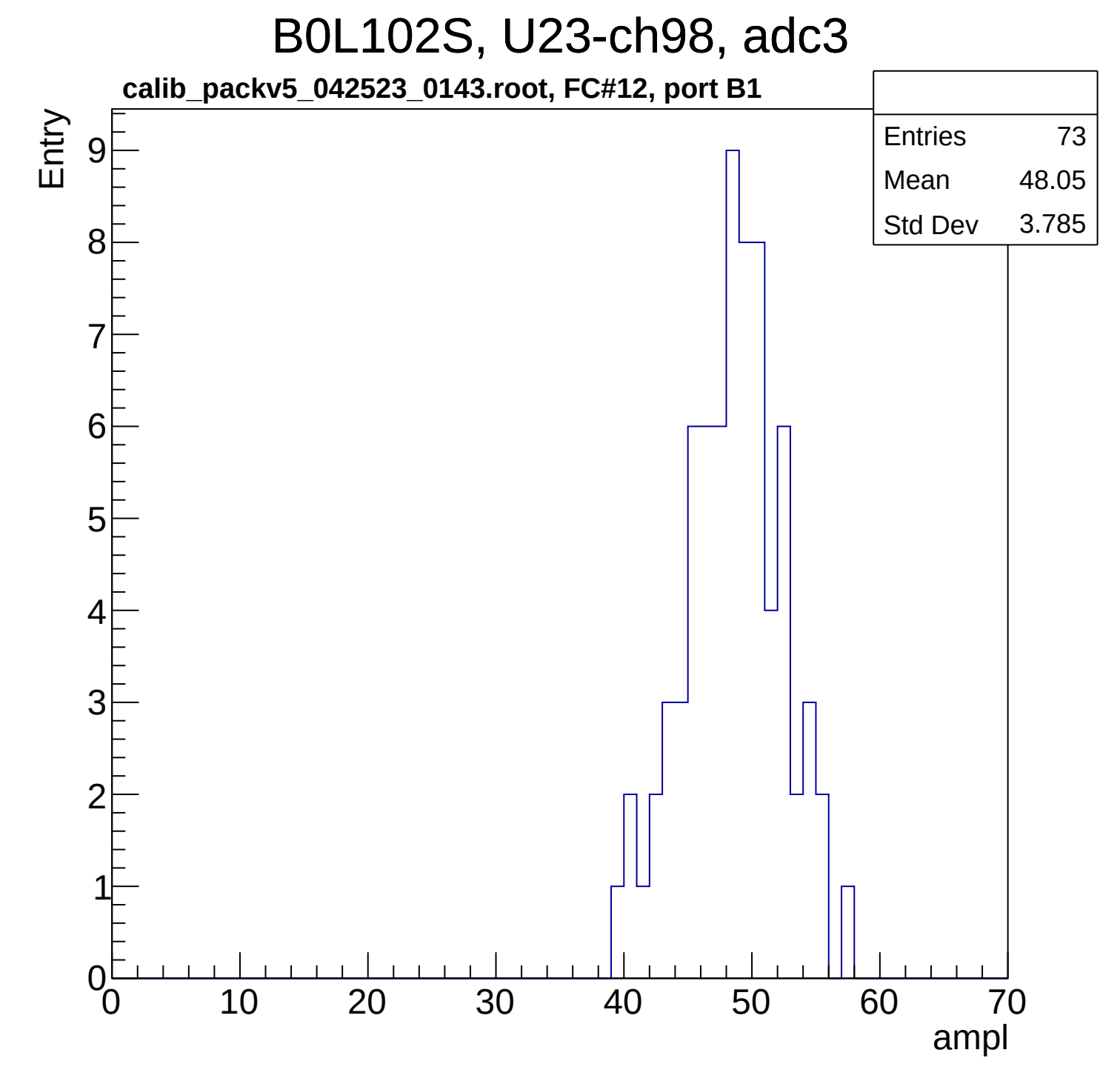
B0L102S, U23-ch98, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 48.05
Std Dev 3.785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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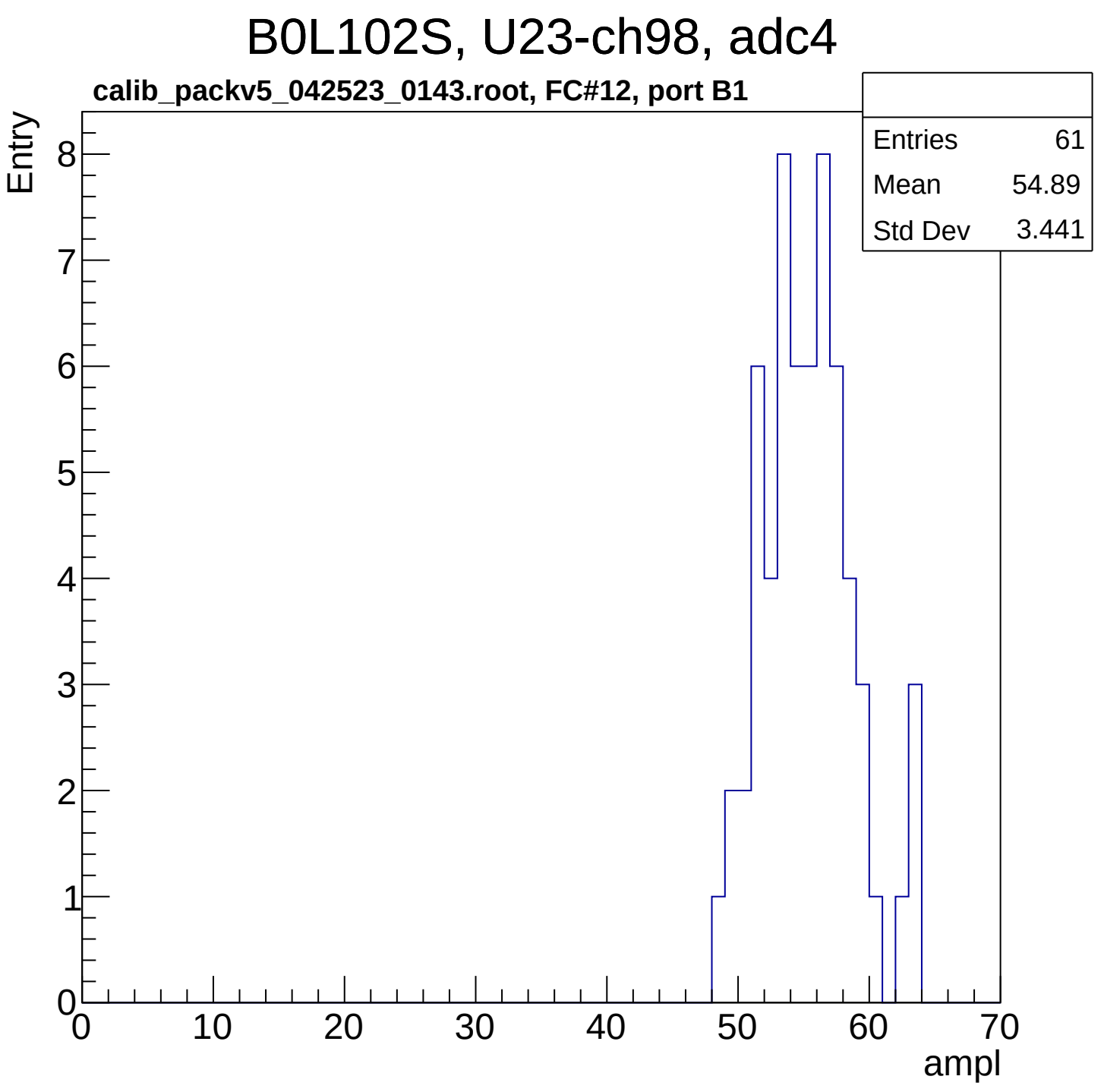
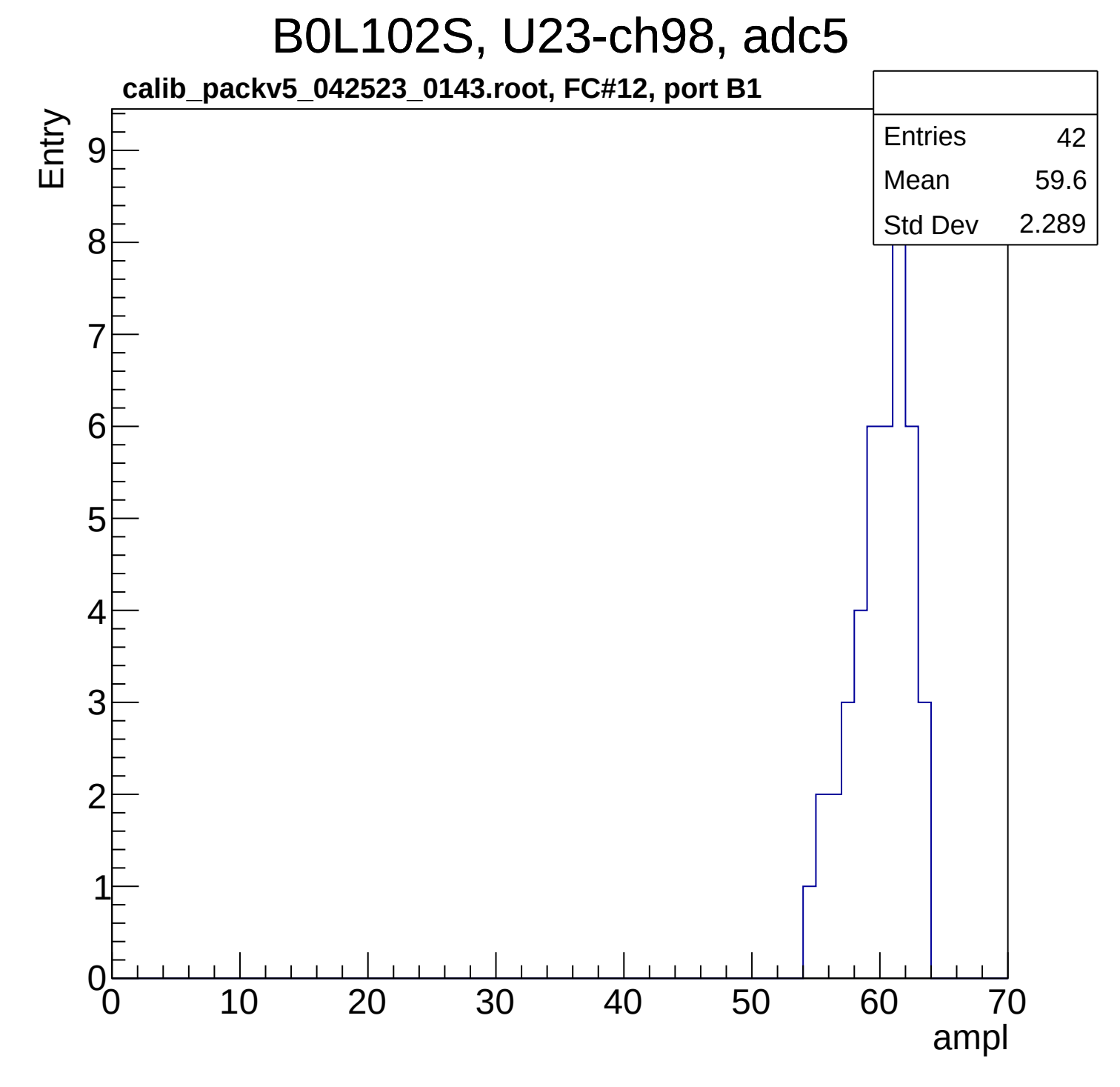
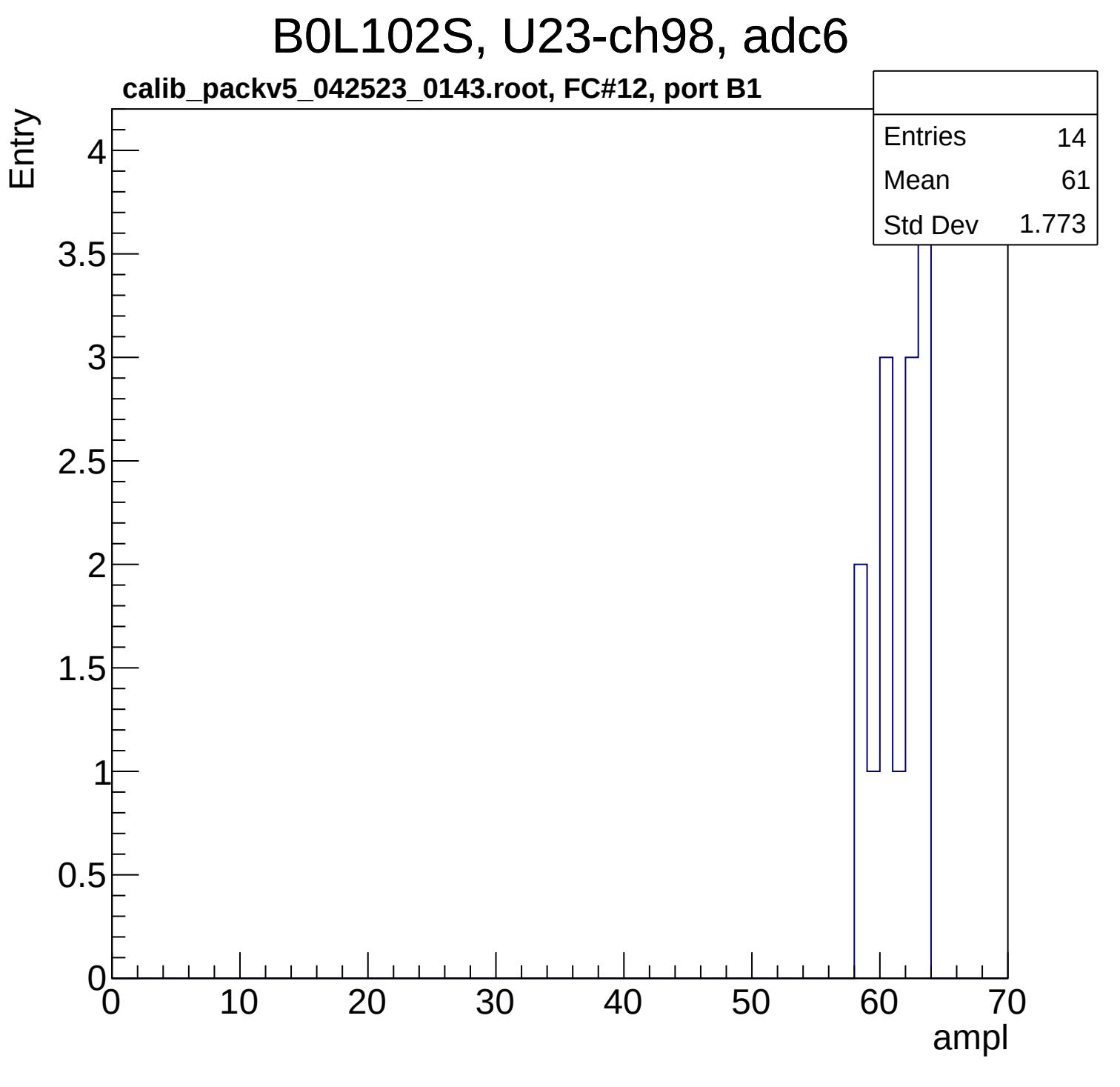
40
50
60
70


B0L102S, U23-ch98, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 54.89
Std Dev 3.441
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch98, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 42
Mean 59.6
Std Dev 2.289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch98, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 14
Mean 61
Std Dev 1.773
Entry
ampl
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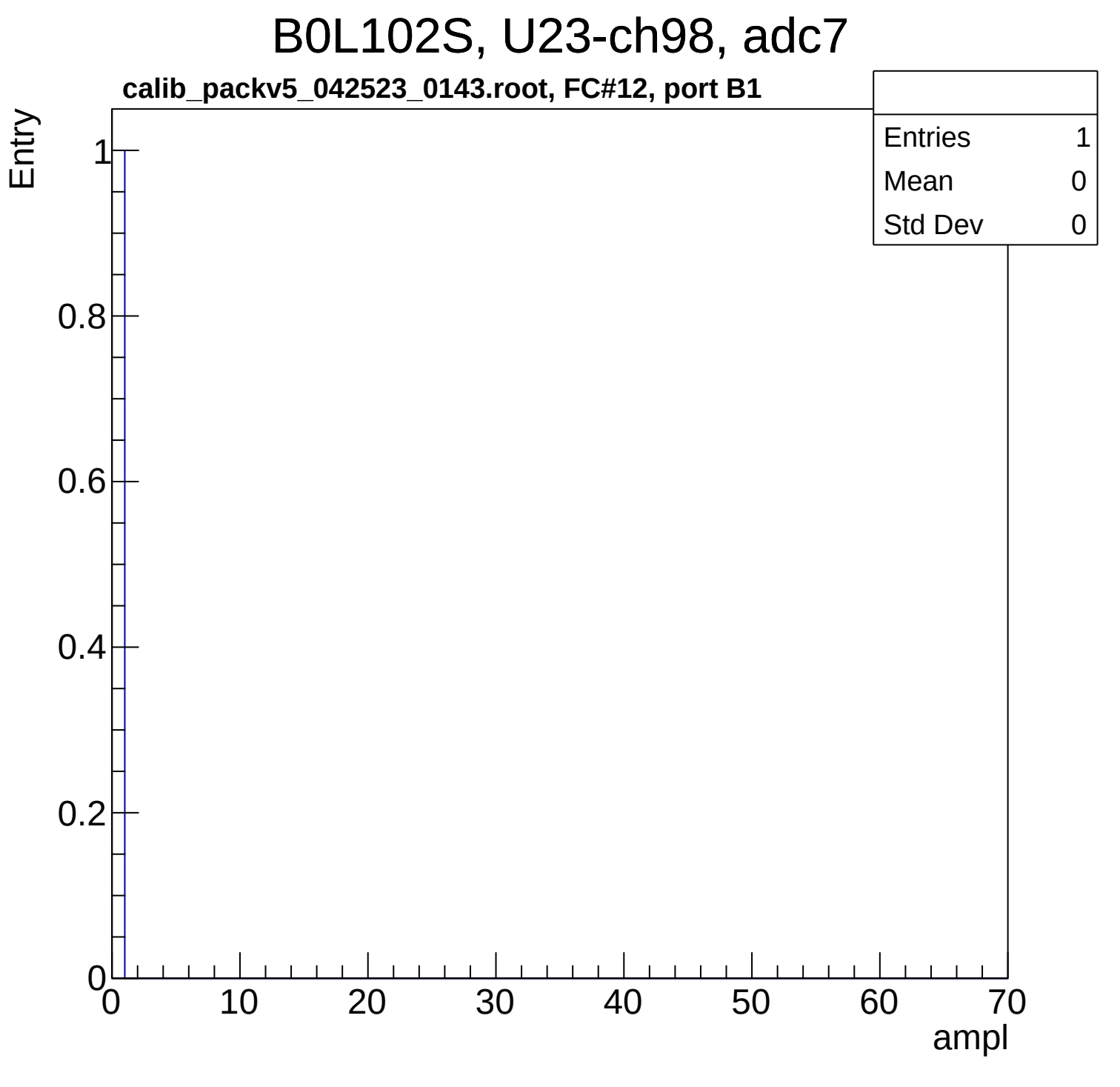
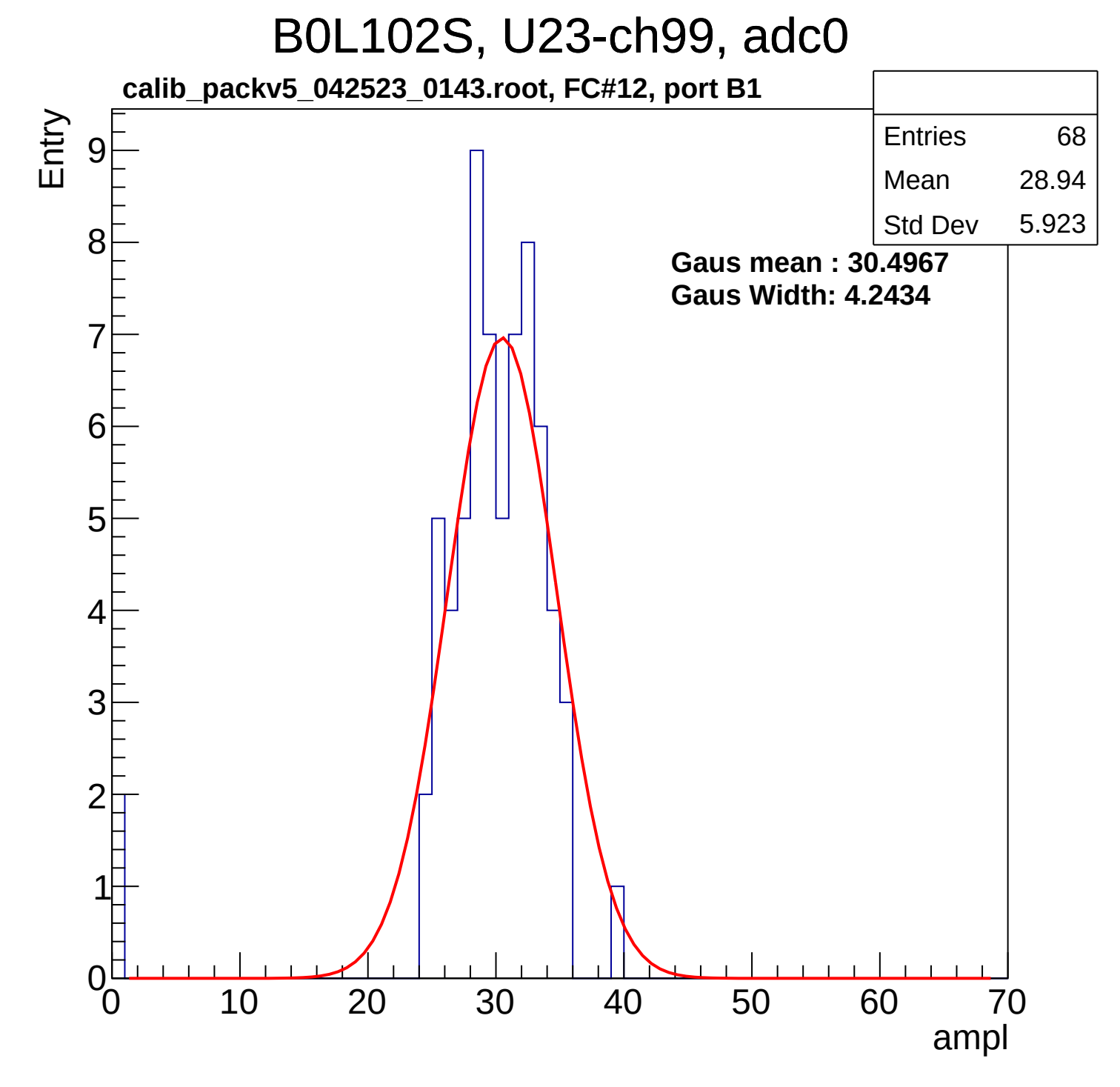
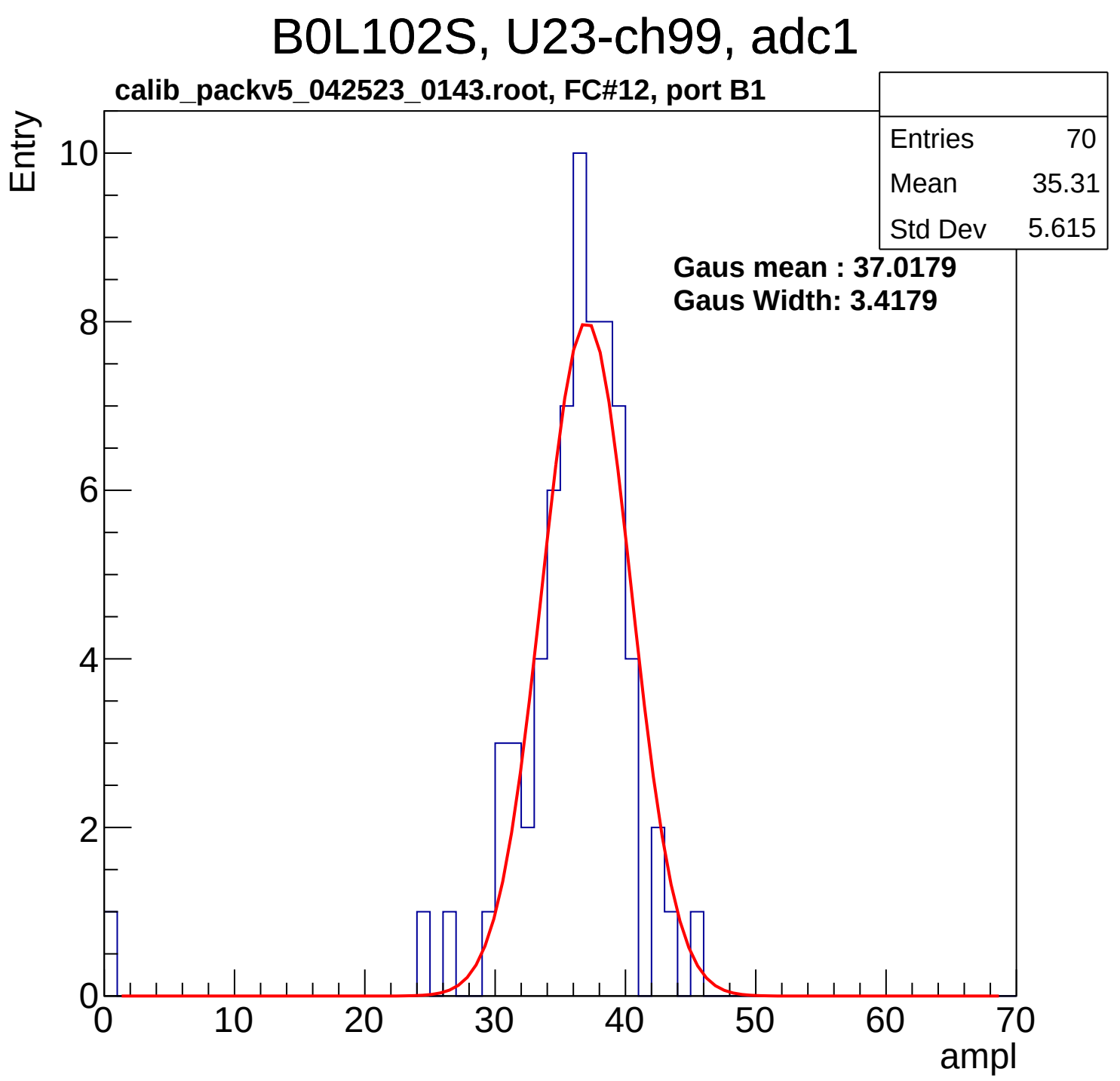
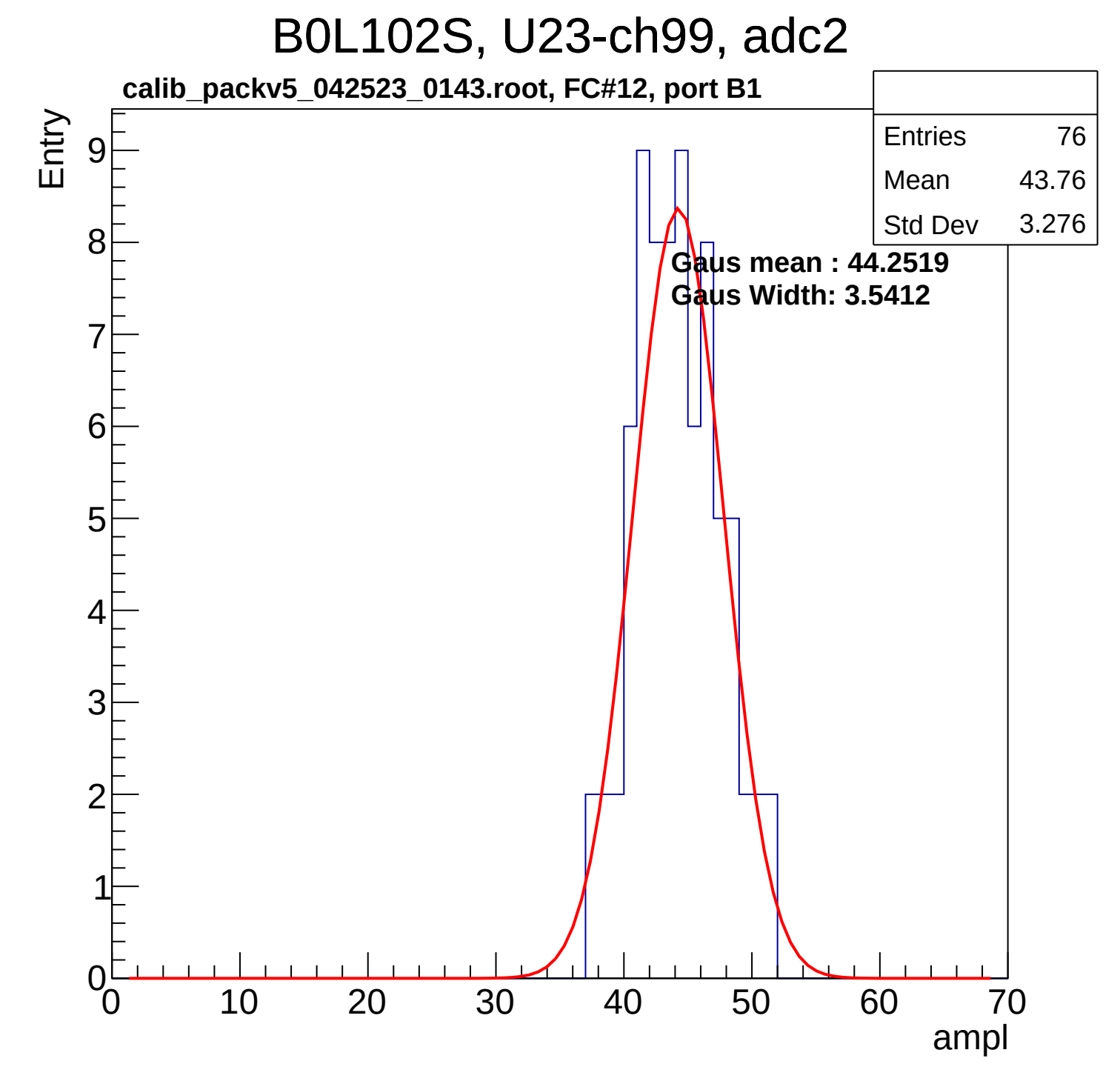
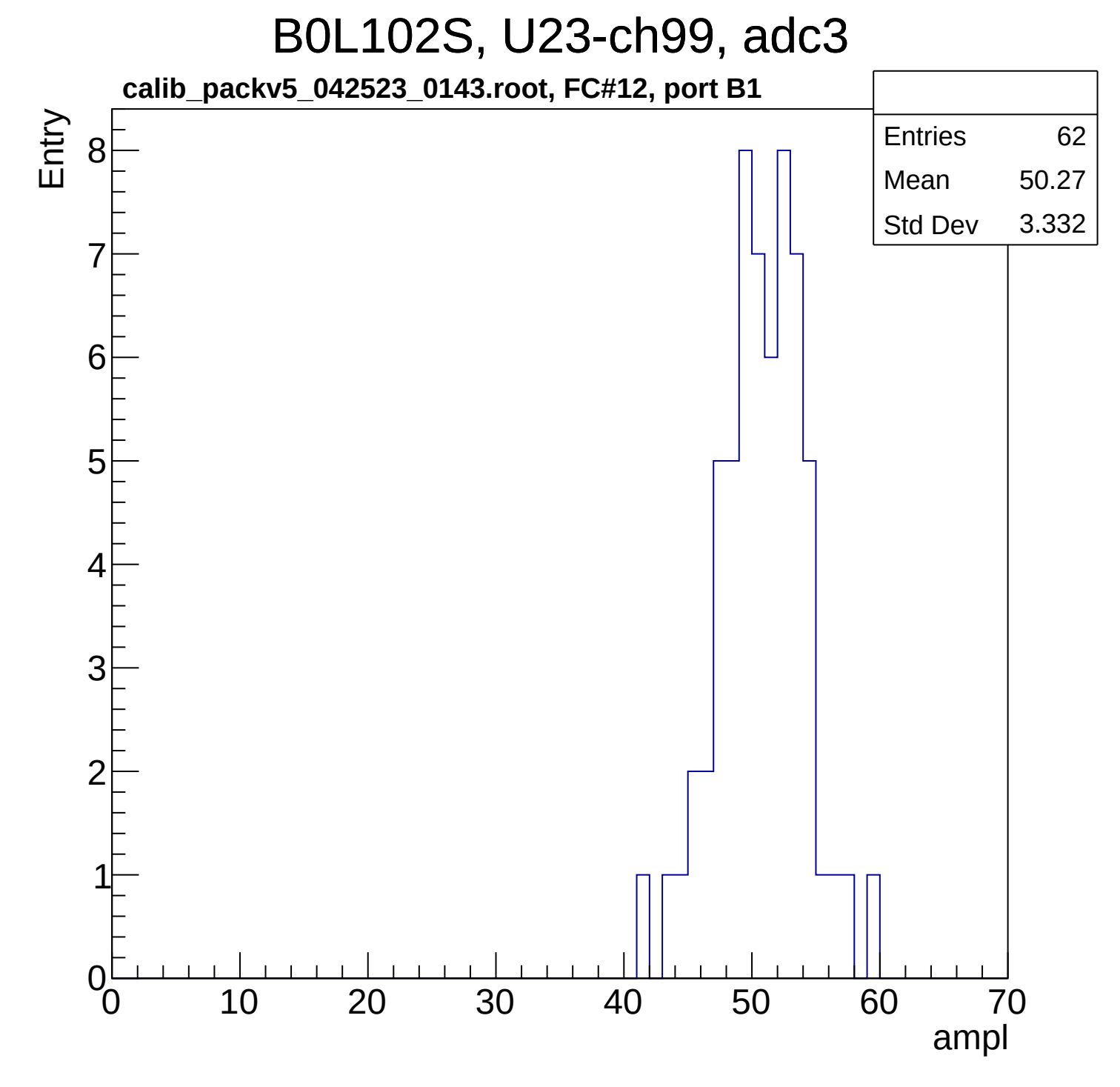
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L102S, U23-ch98, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch99, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 28.94
Std Dev 5.923
Gaus mean : 30.4967
Gaus Width: 4.2434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch99, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 35.31
Std Dev 5.615
Gaus mean : 37.0179
Gaus Width: 3.4179
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch99, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 43.76
Std Dev 3.276
Gaus mean : 44.2519
Gaus Width: 3.5412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch99, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 50.27
Std Dev 3.332
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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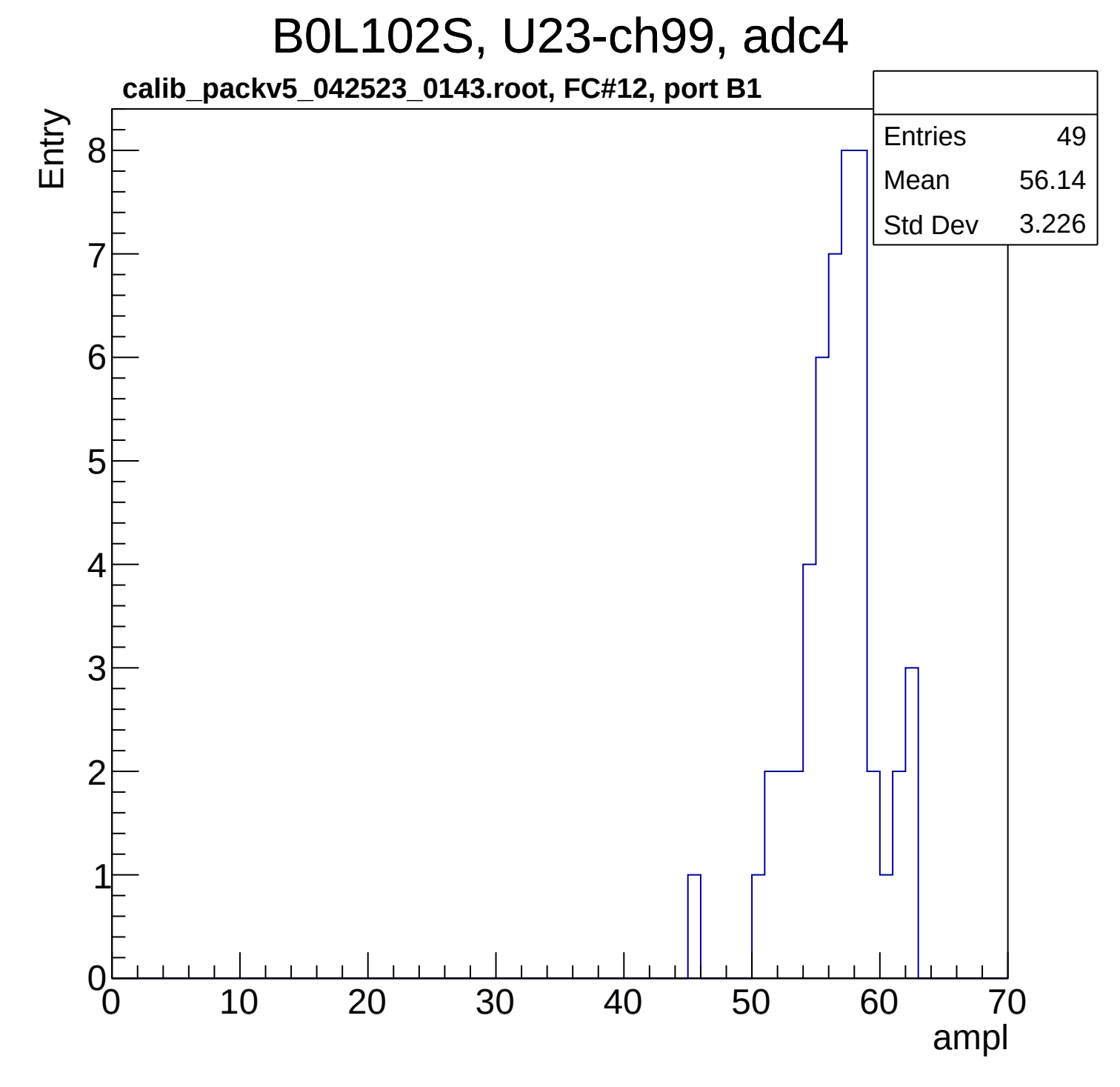
B0L102S, U23-ch99, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 56.14
Std Dev 3.226
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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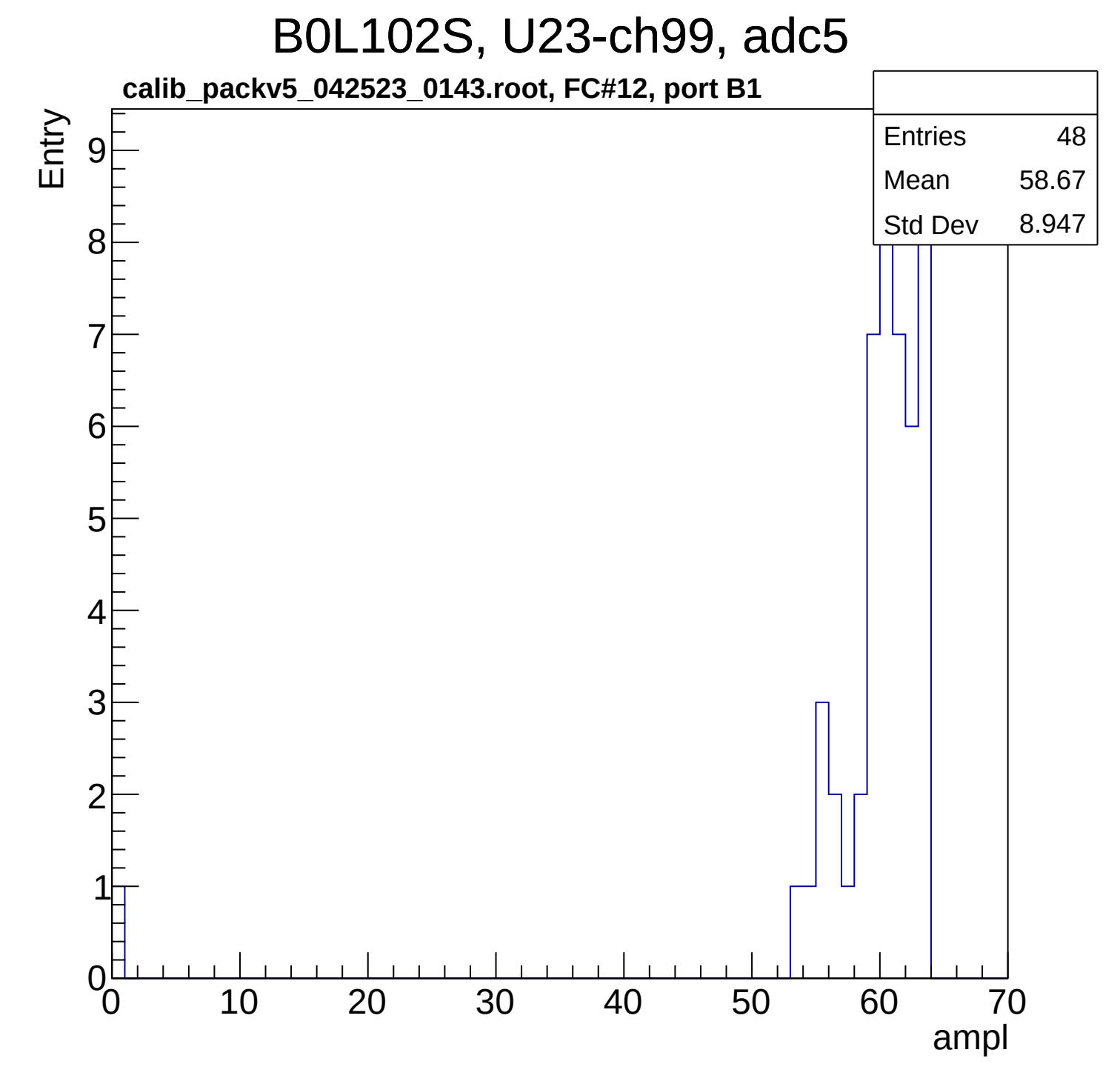
B0L102S, U23-ch99, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 48
Mean 58.67
Std Dev 8.947
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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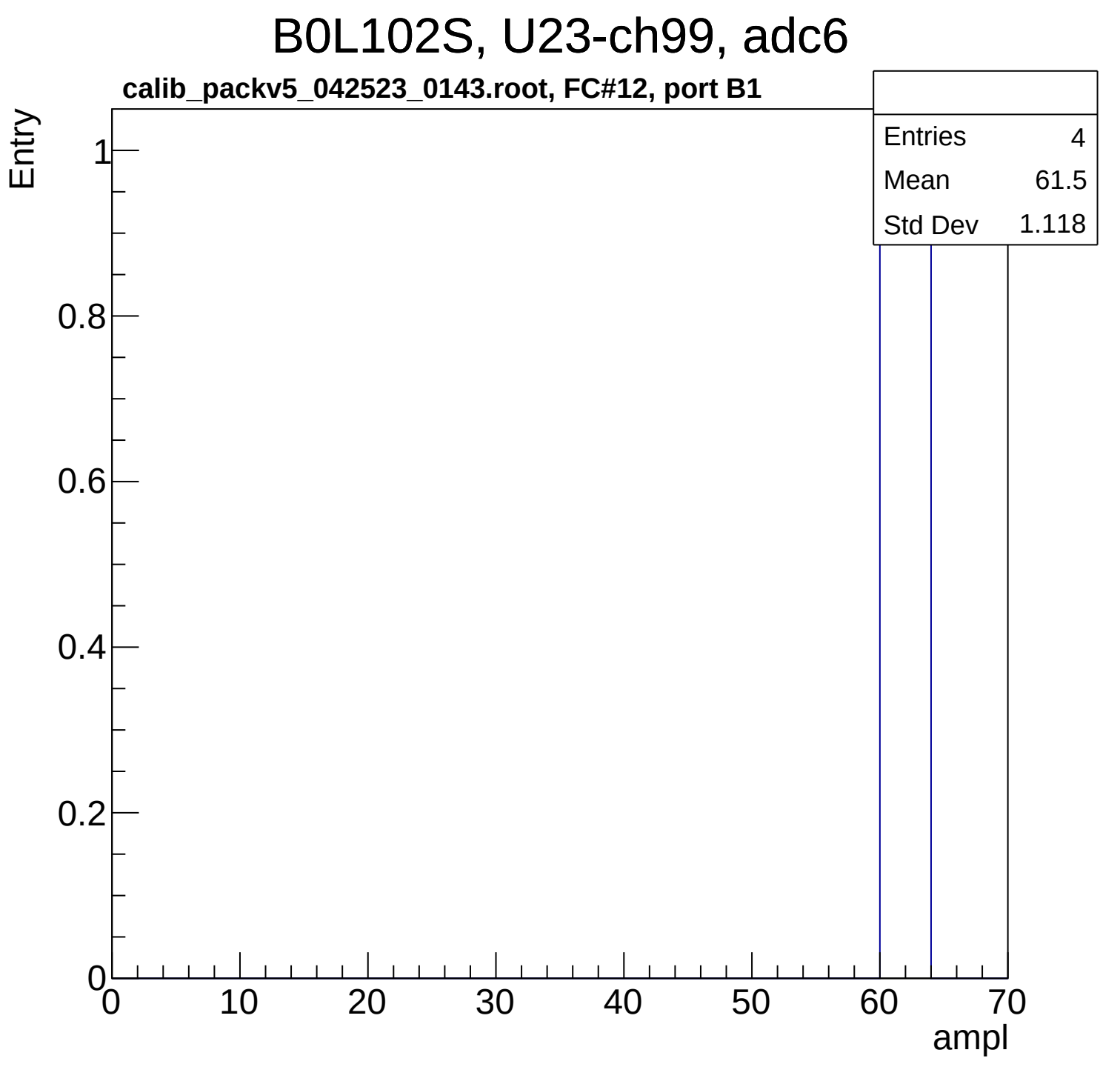
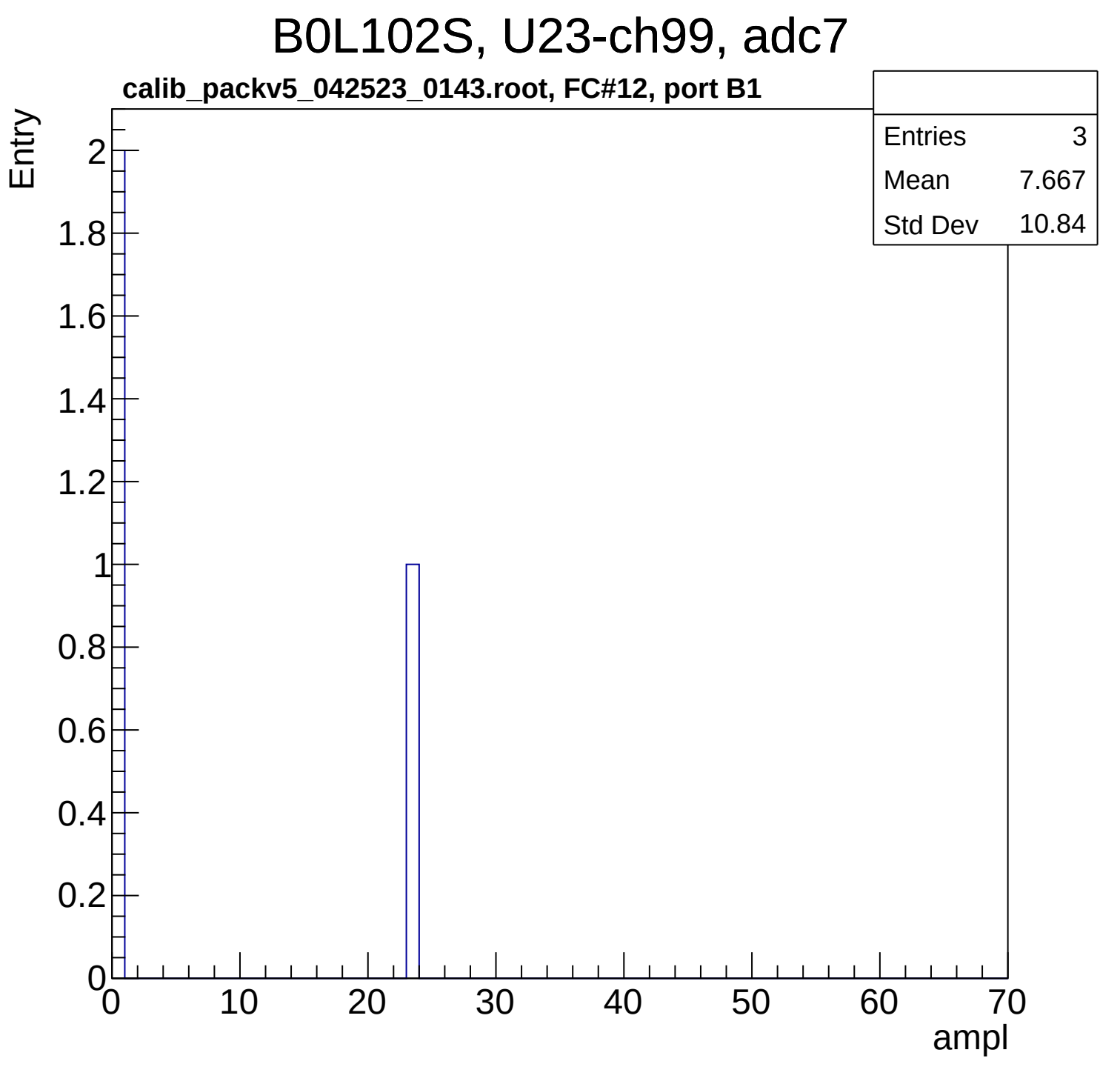
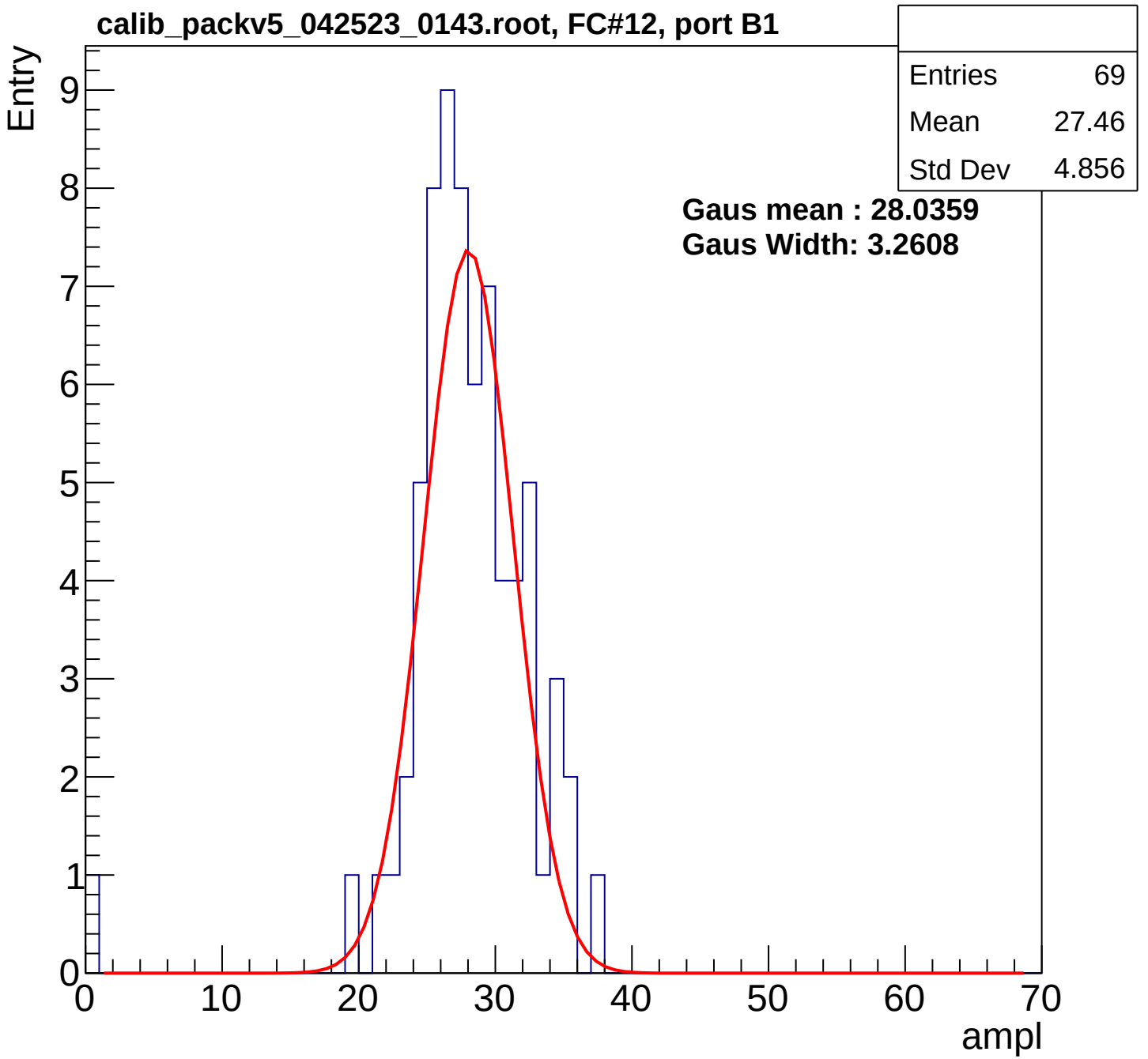
40
50
60
70


B0L102S, U23-ch99, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch99, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch100, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 27.46
Std Dev 4.856
Gaus mean : 28.0359
Gaus Width: 3.2608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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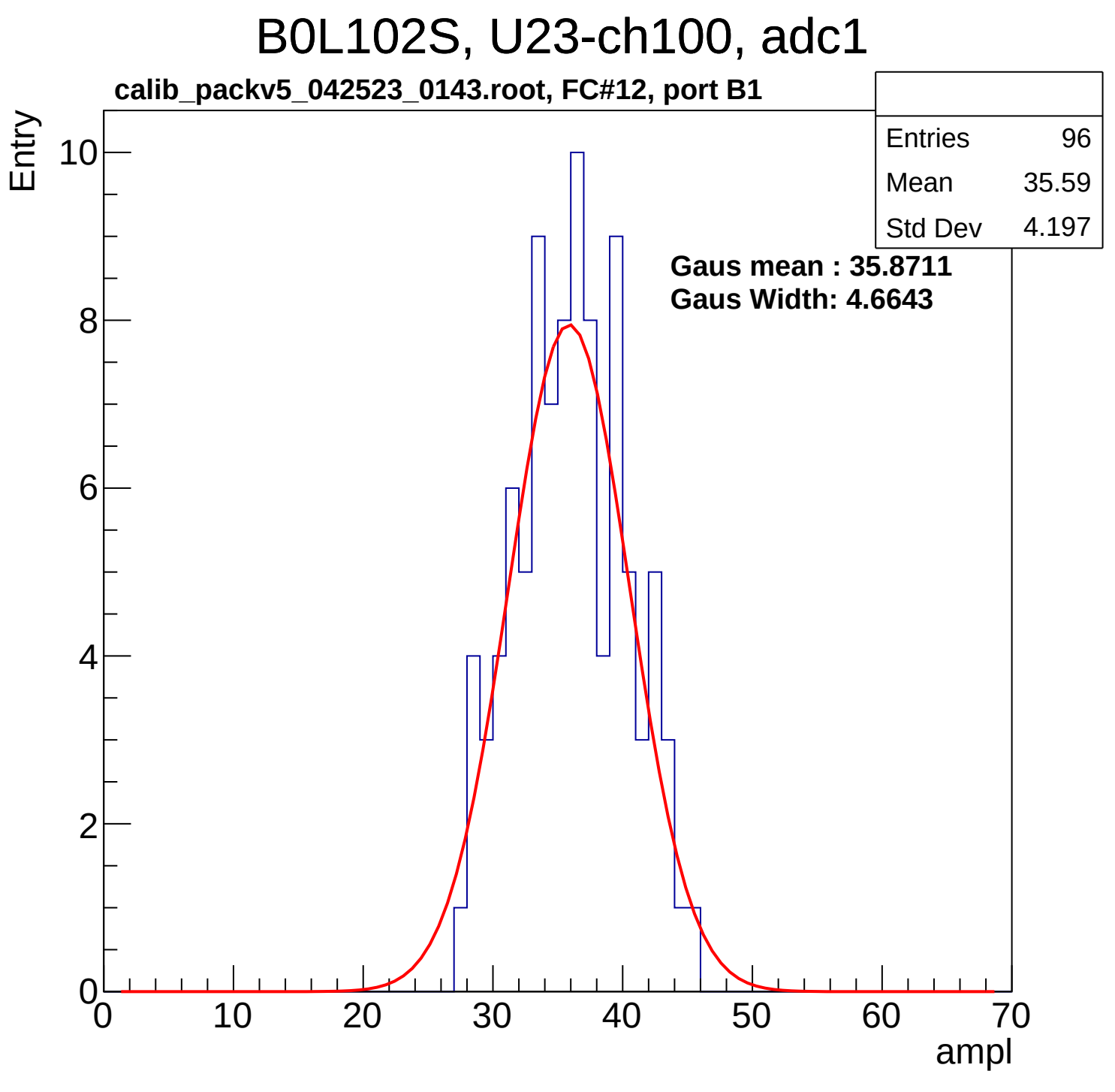
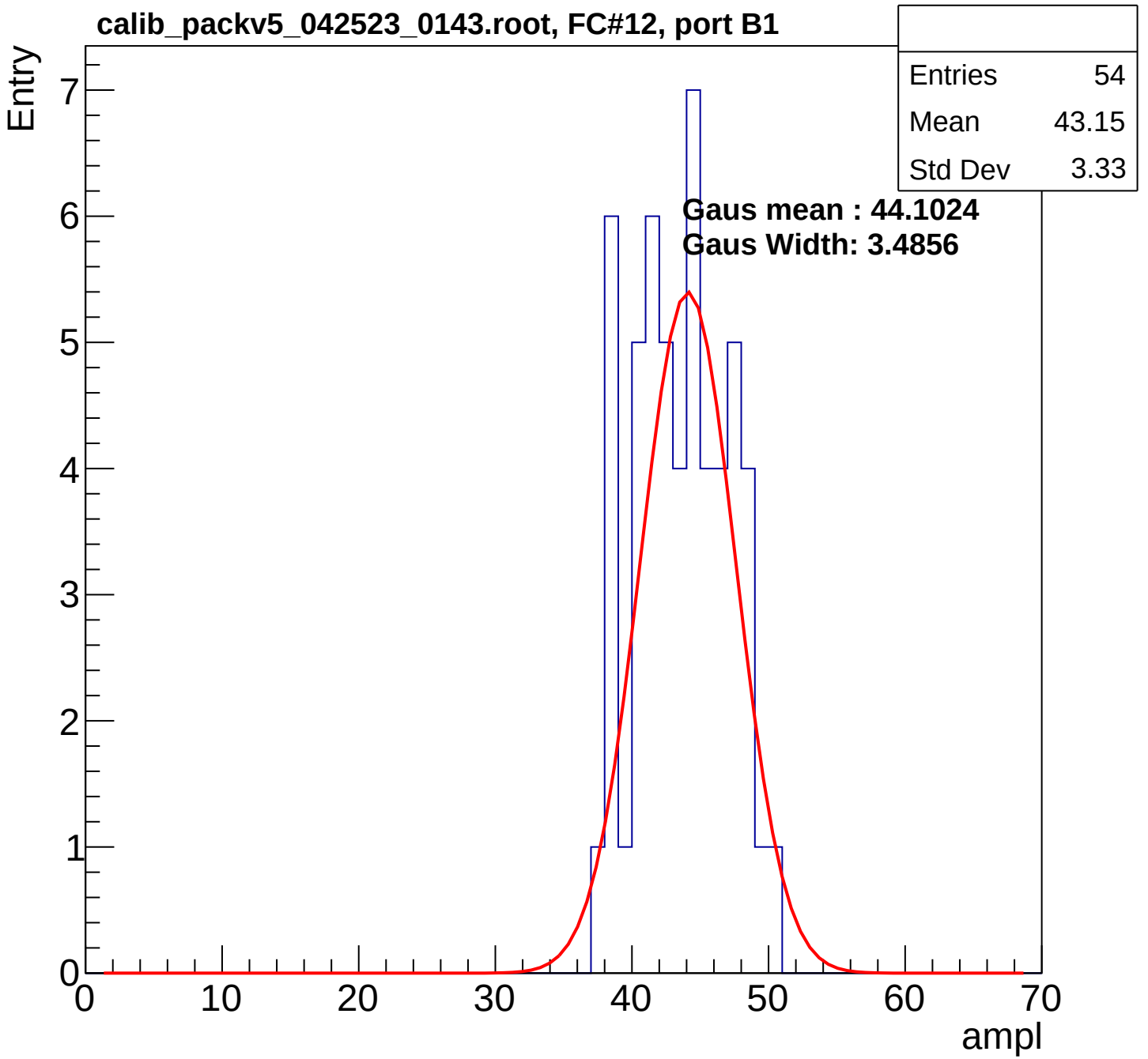
60
70


B0L102S, U23-ch100, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 96
Mean 35.59
Std Dev 4.197
Gaus mean : 35.8711
Gaus Width: 4.6643
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch100, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43.15
Std Dev 3.33
Gaus mean : 44.1024
Gaus Width: 3.4856
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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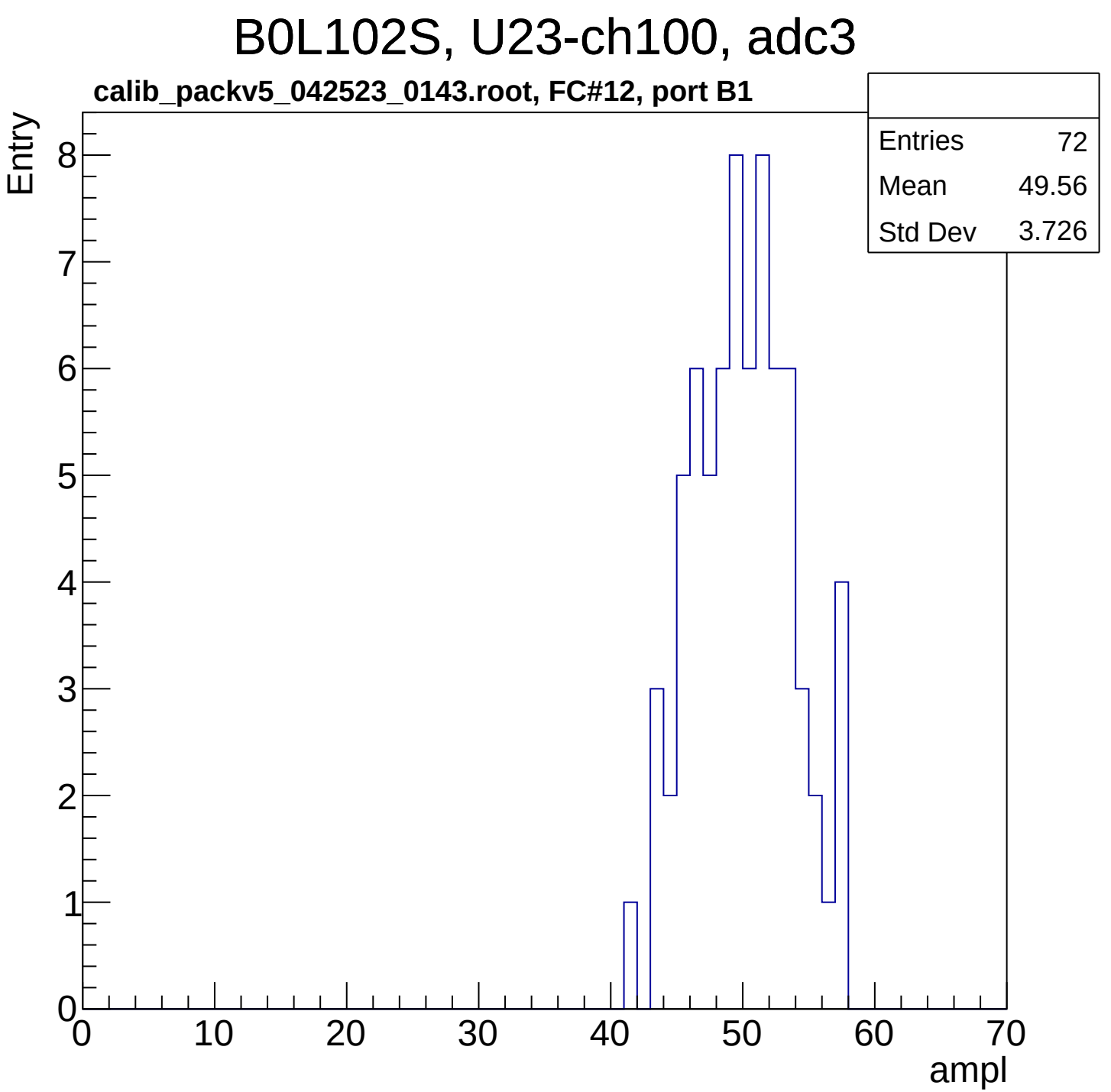
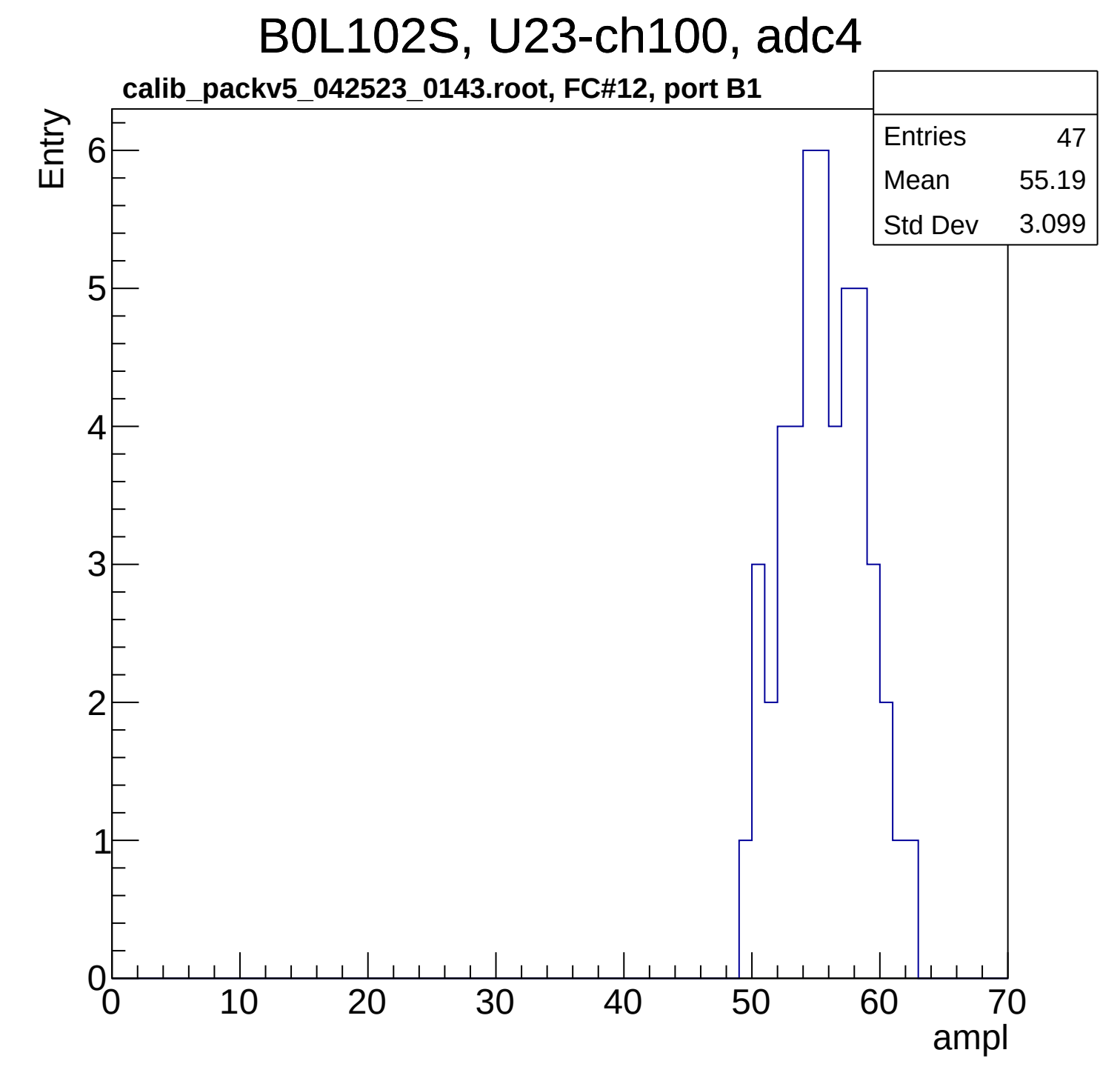
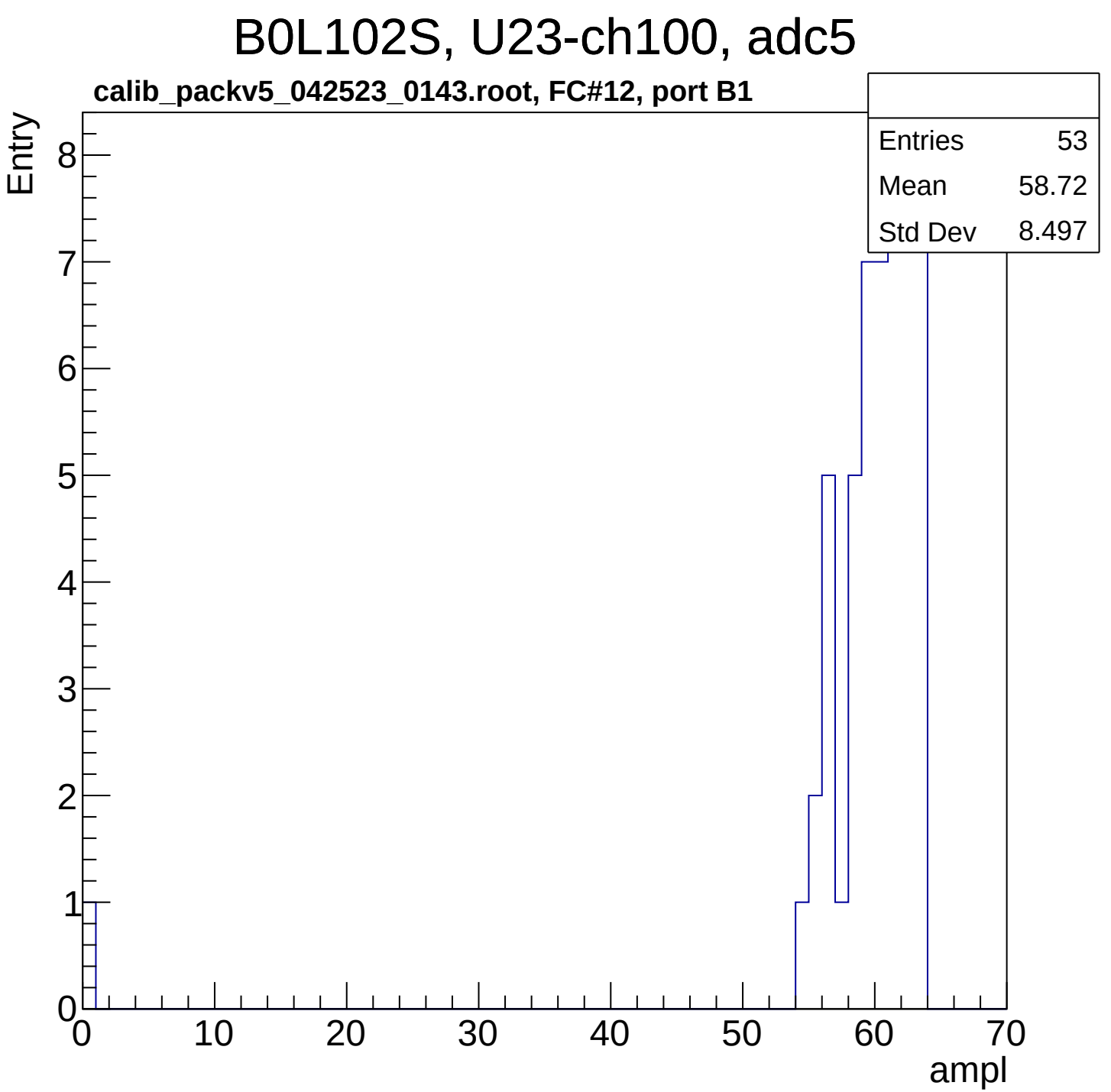

B0L102S, U23-ch100, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 49.56
Std Dev 3.726
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch100, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 47
Mean 55.19
Std Dev 3.099
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch100, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 58.72
Std Dev 8.497
Entry
ampl
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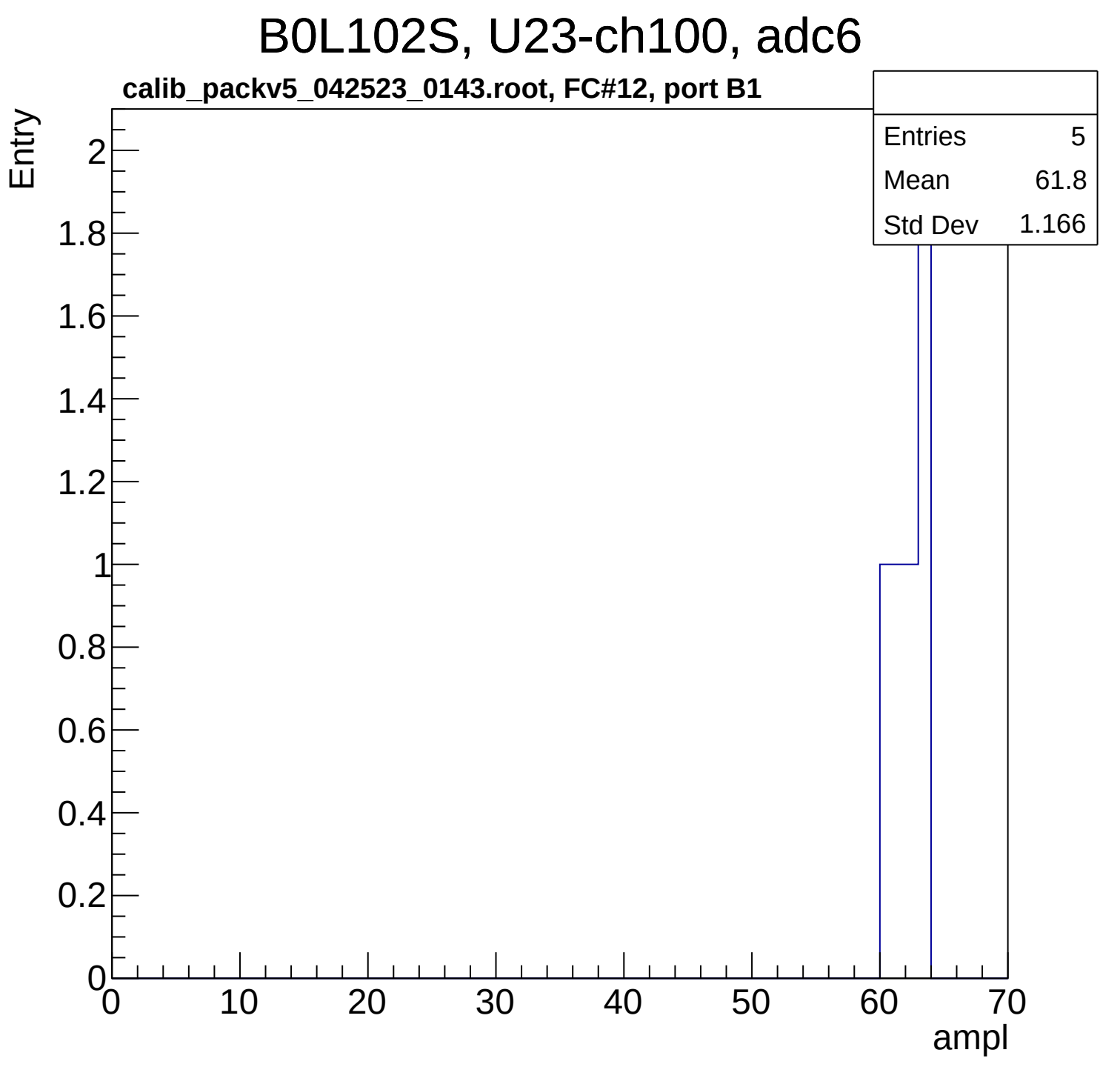
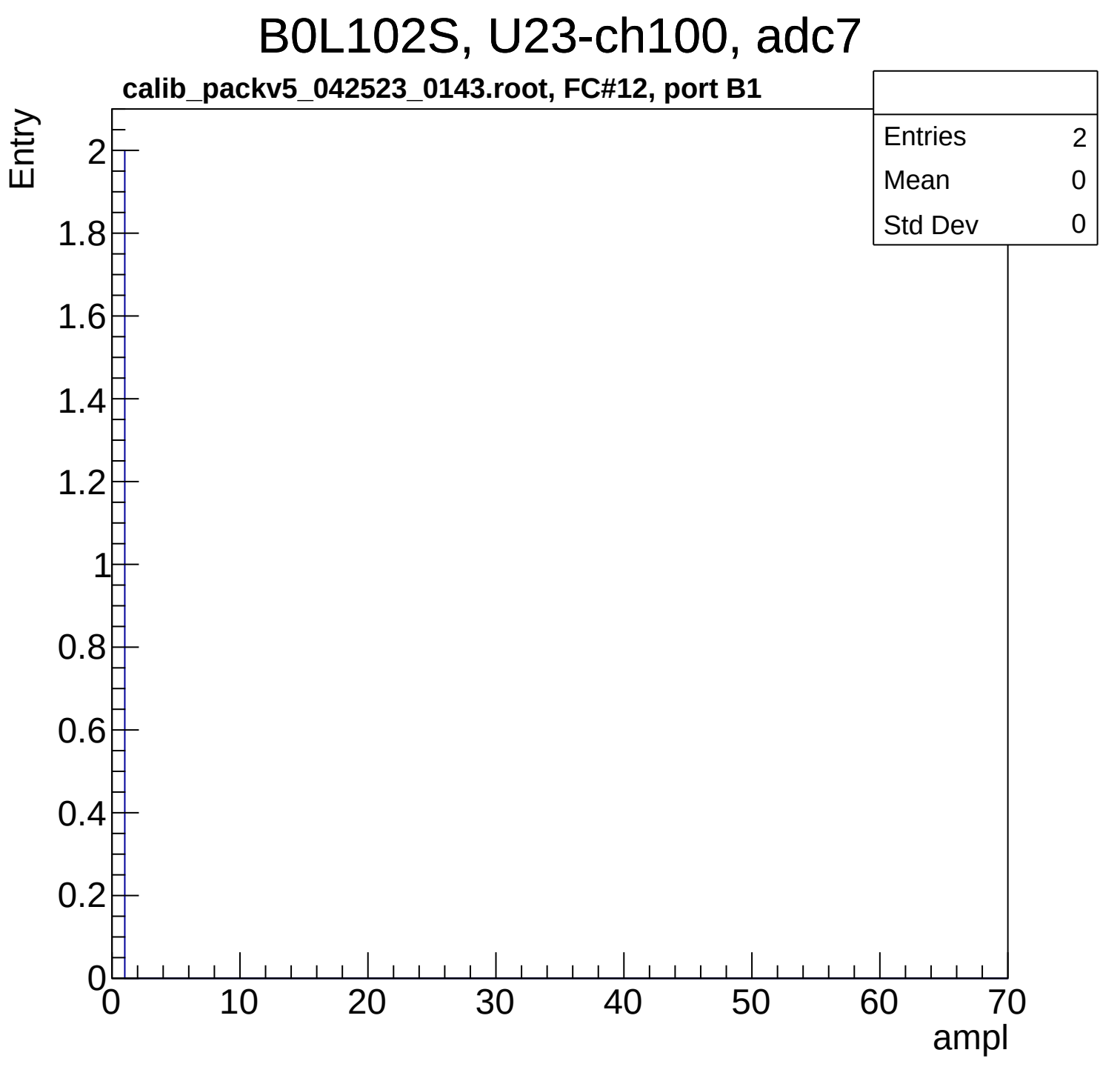
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch100, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch100, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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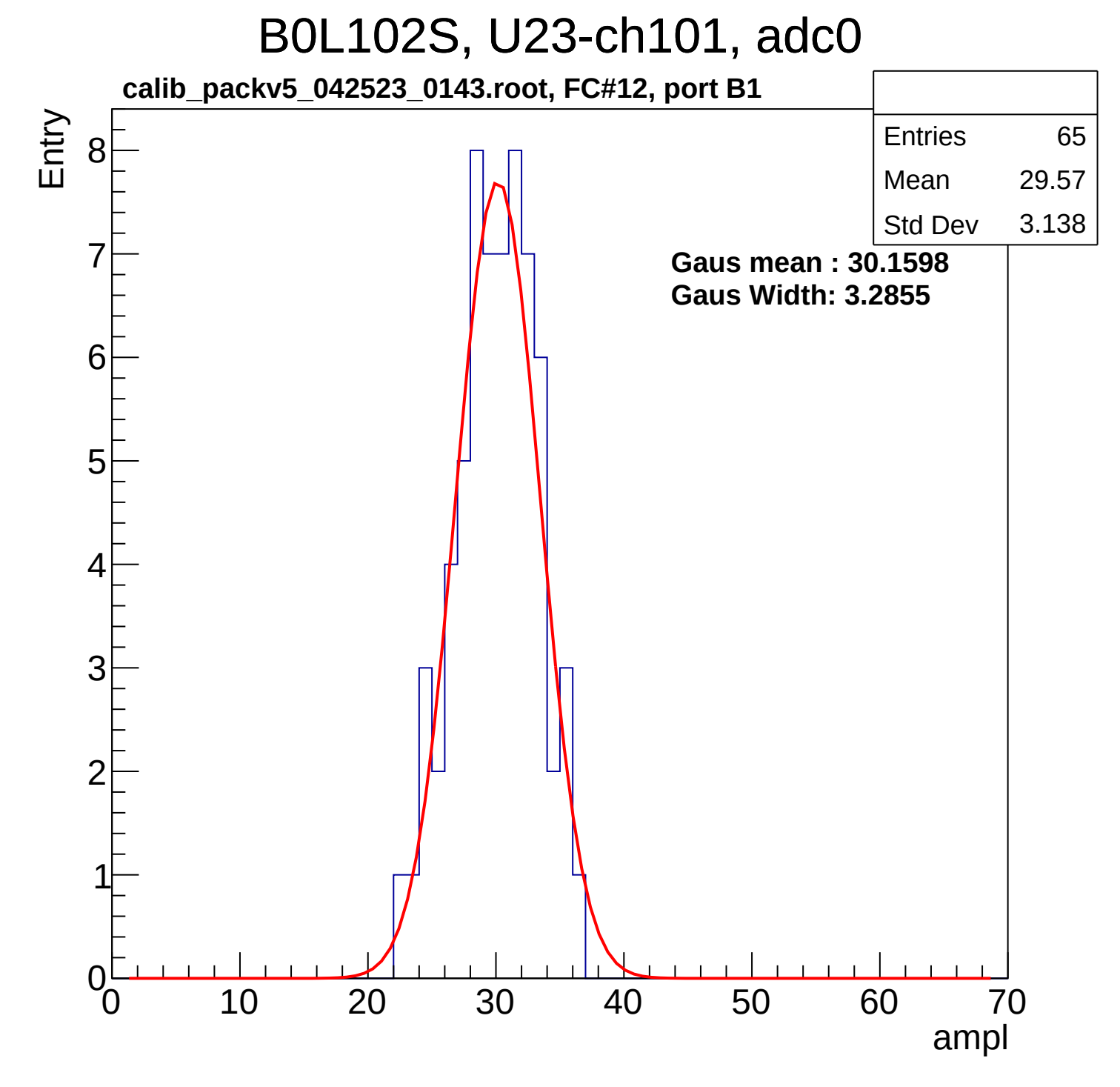
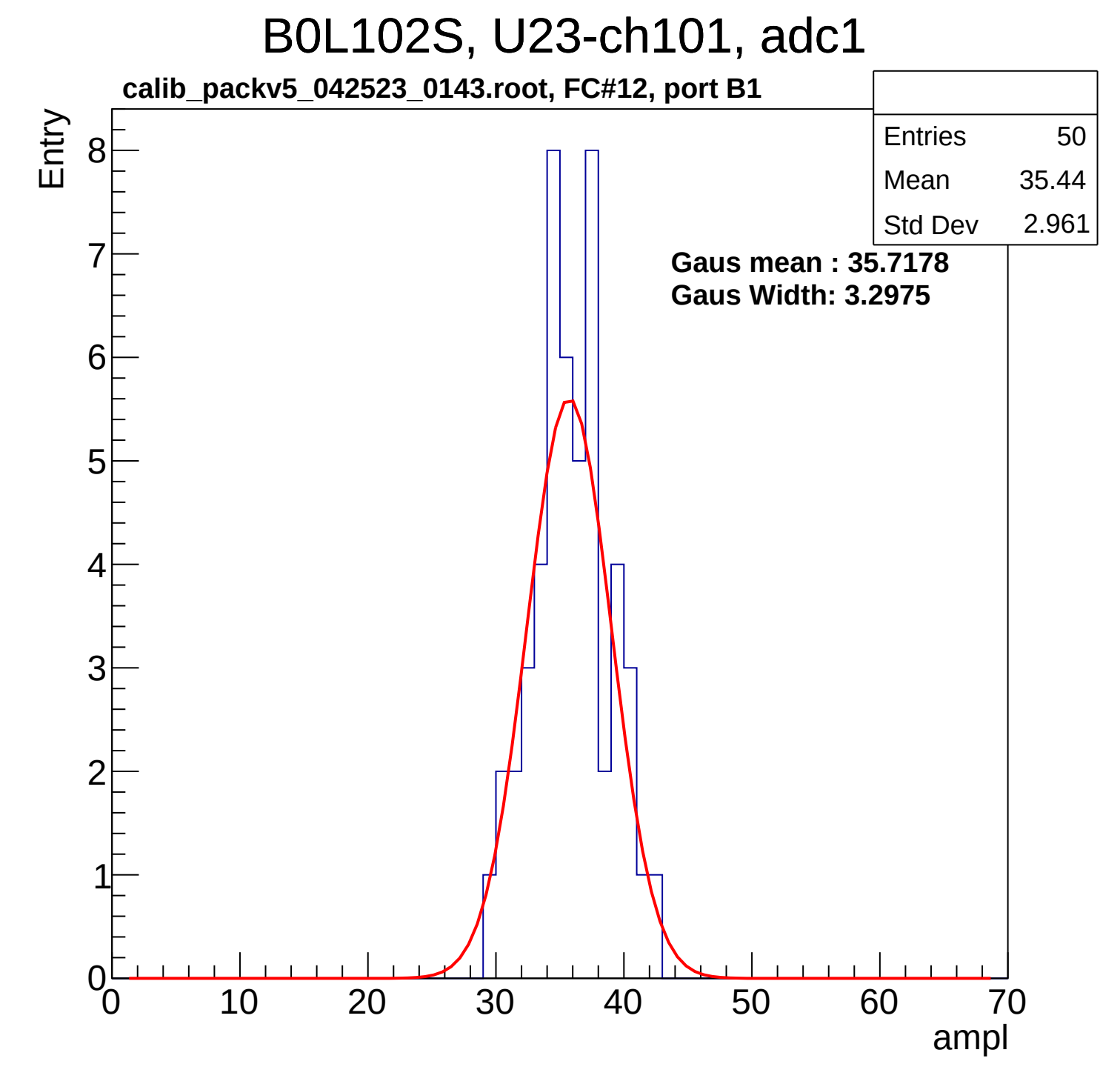
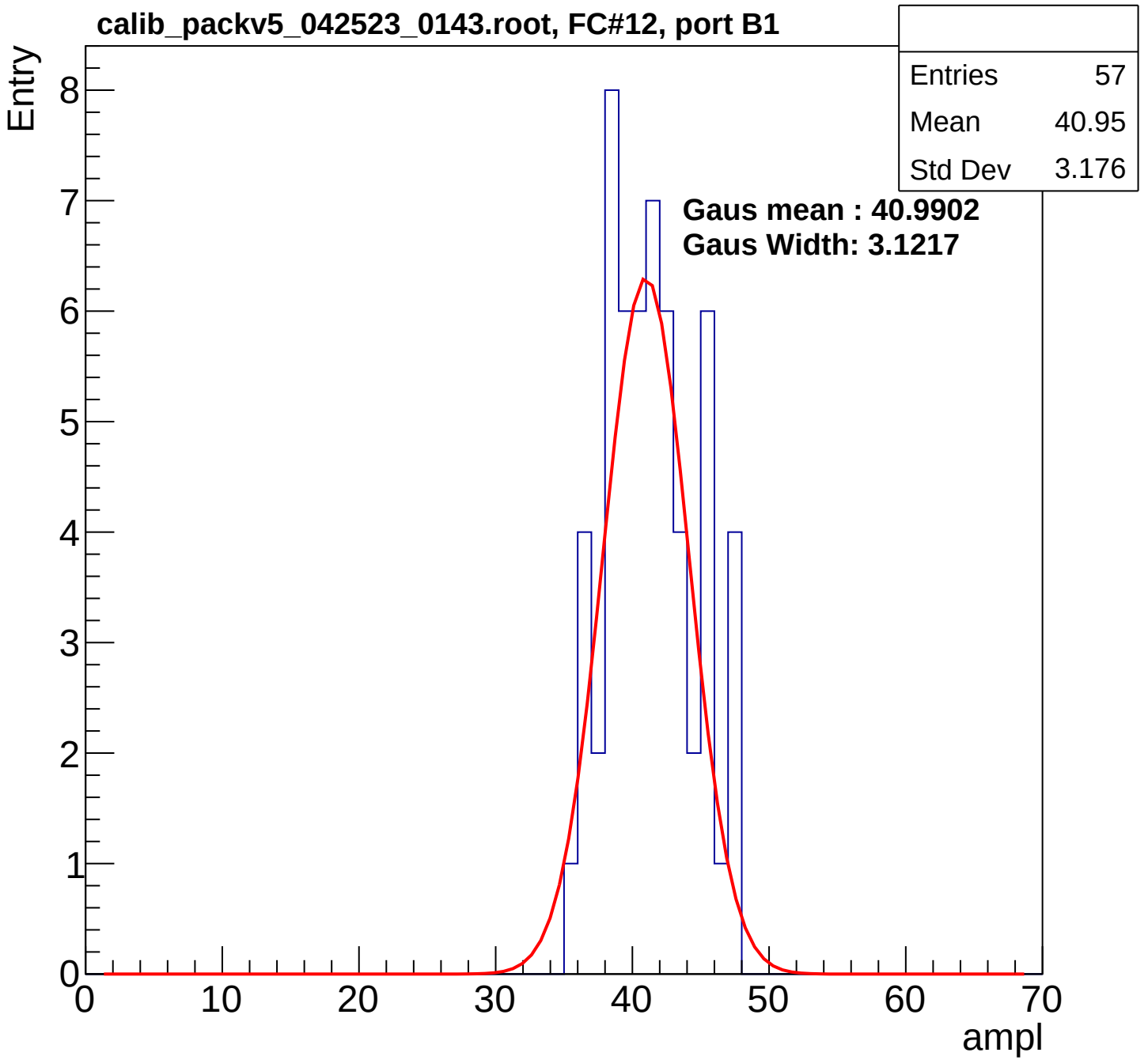
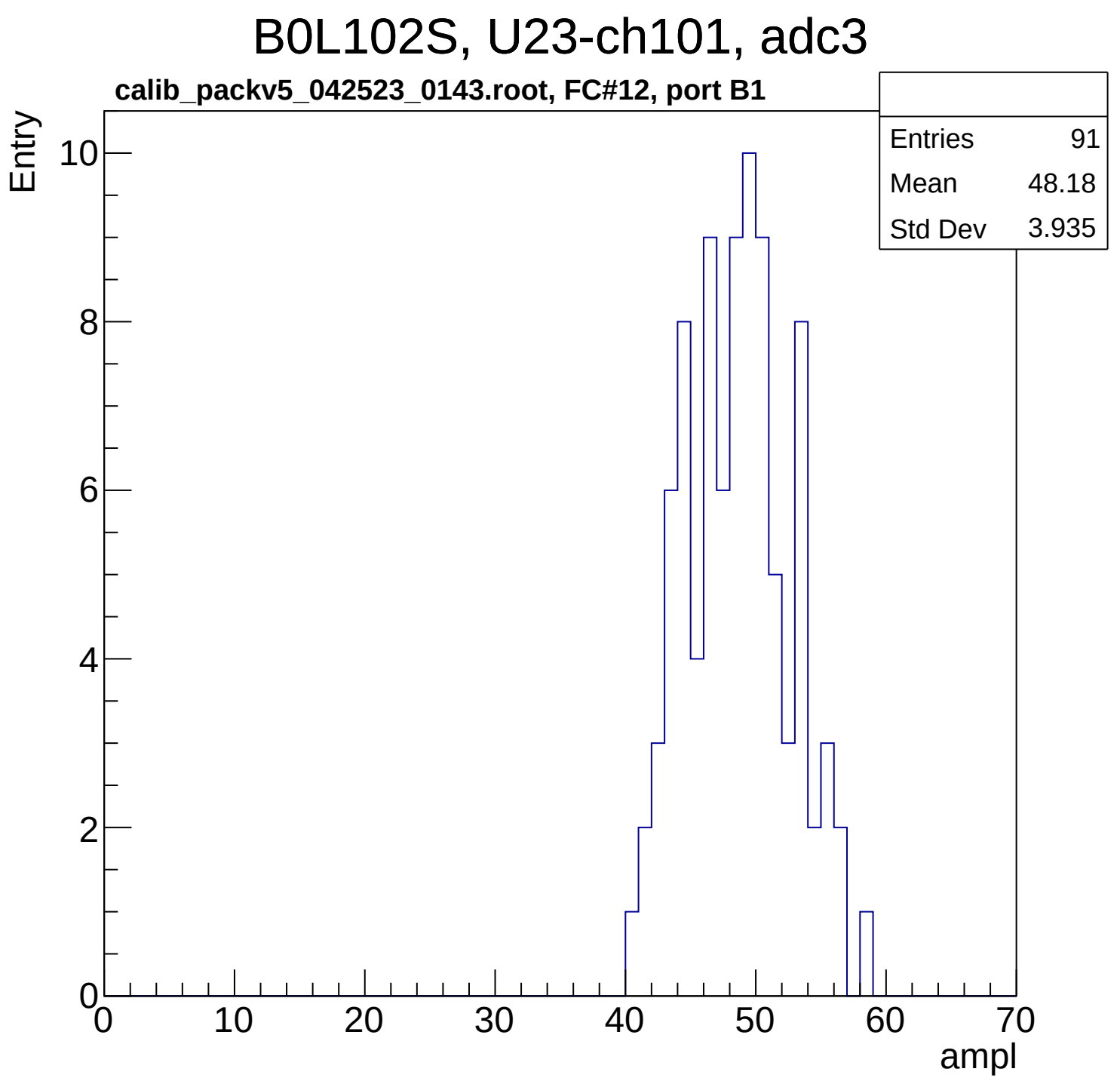
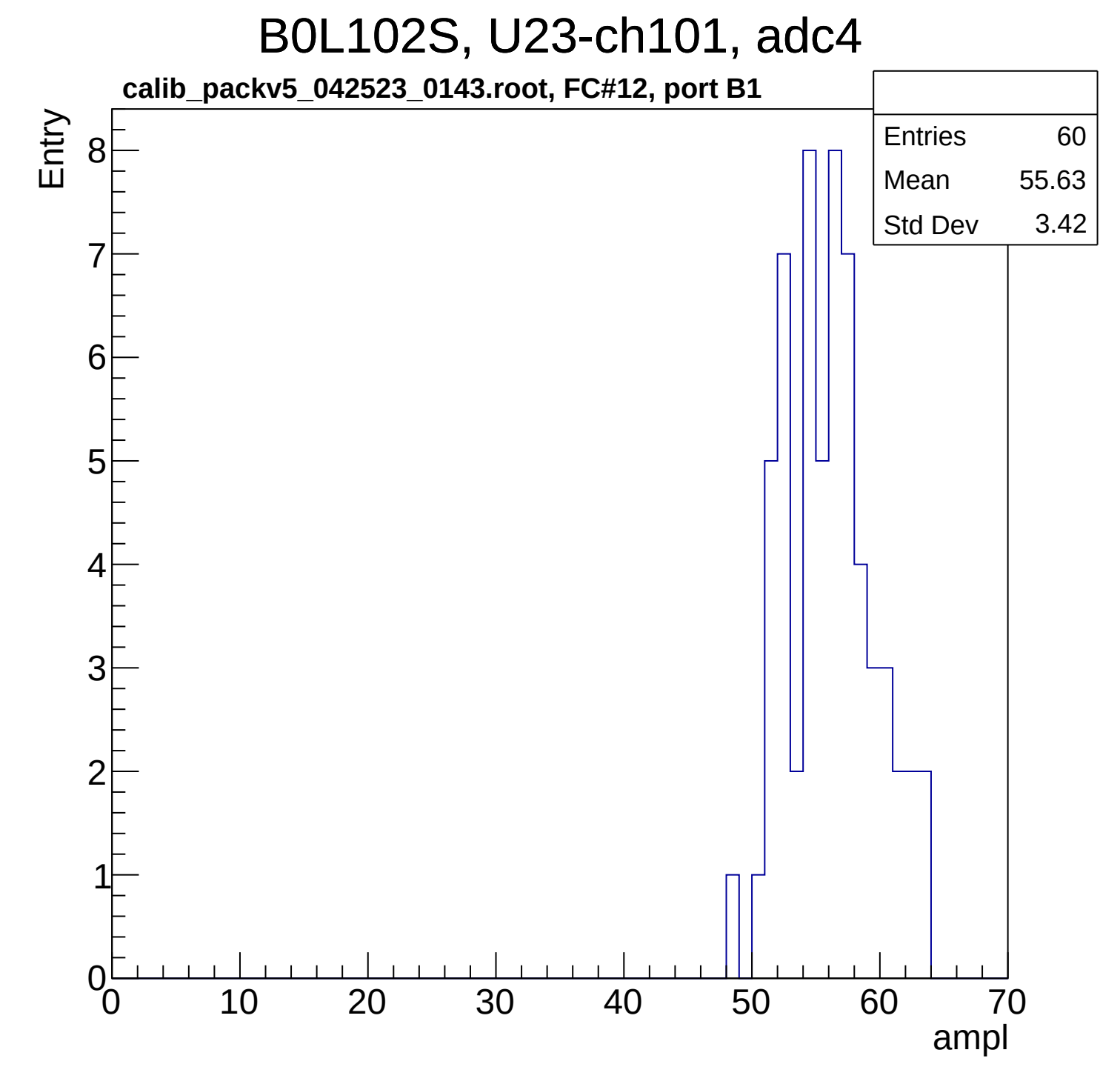
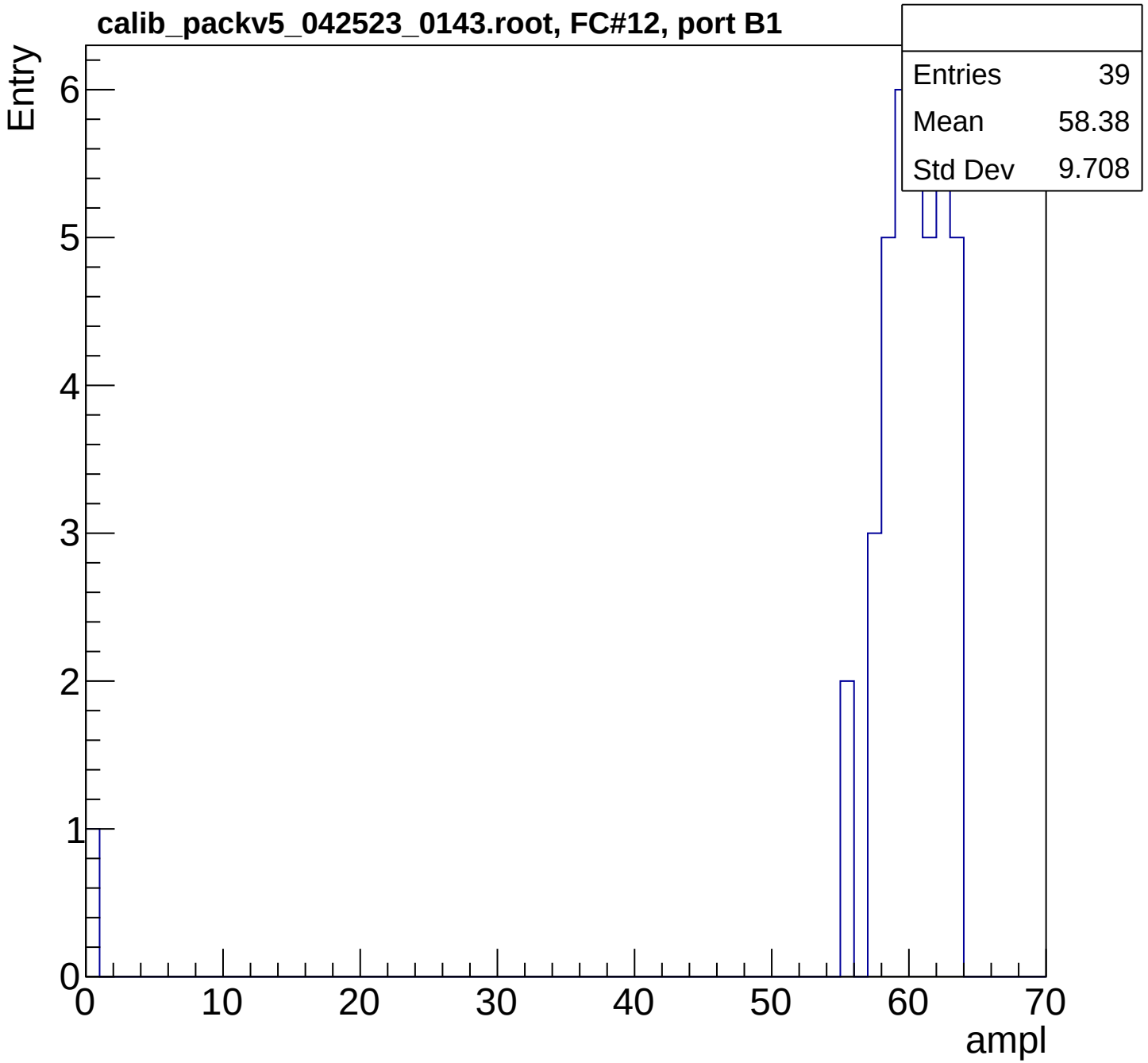

B0L102S, U23-ch101, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 29.57
Std Dev 3.138
Gaus mean : 30.1598
Gaus Width: 3.2855
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

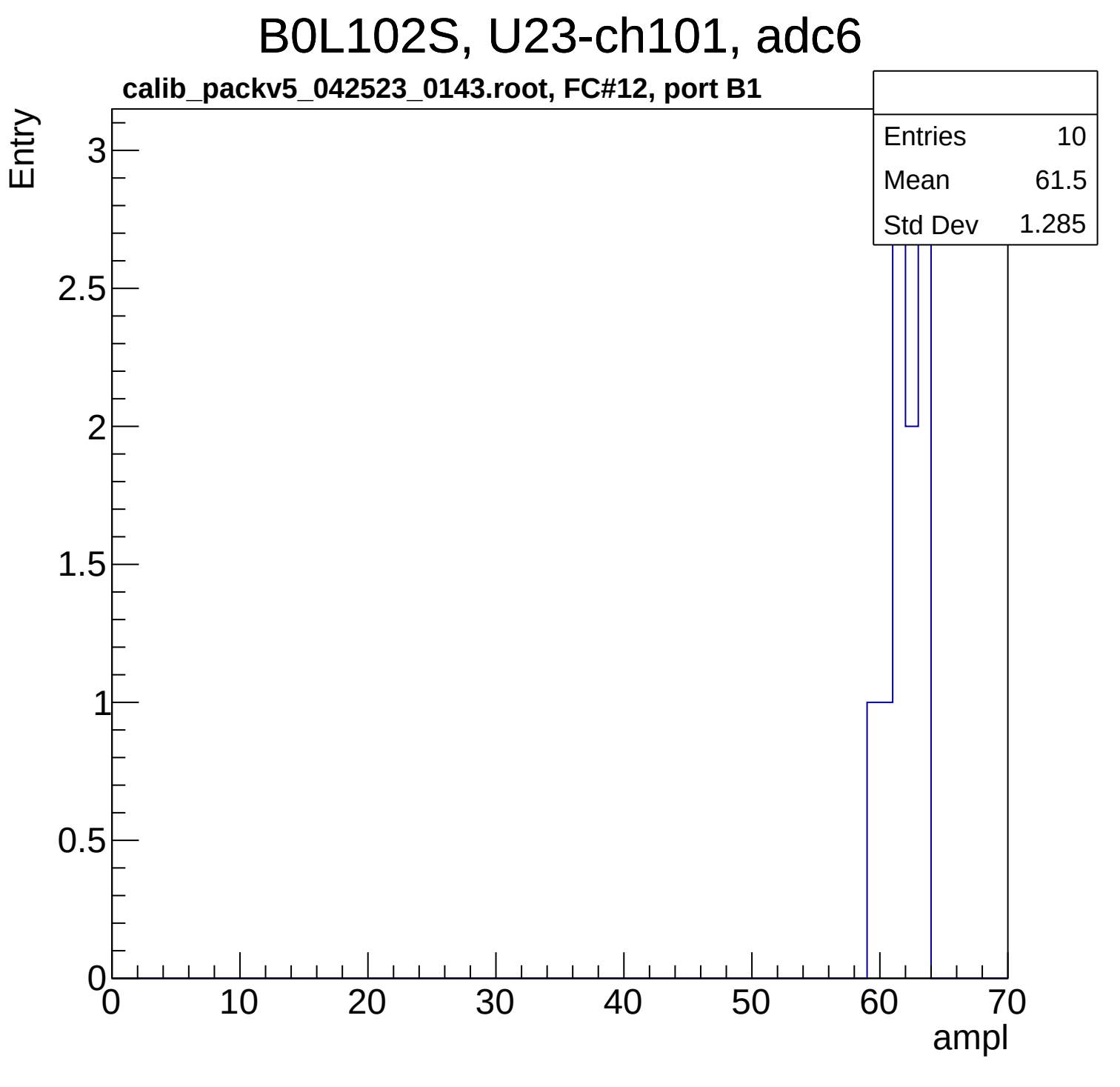

B0L102S, U23-ch101, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 35.44
Std Dev 2.961
Gaus mean : 35.7178
Gaus Width: 3.2975
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

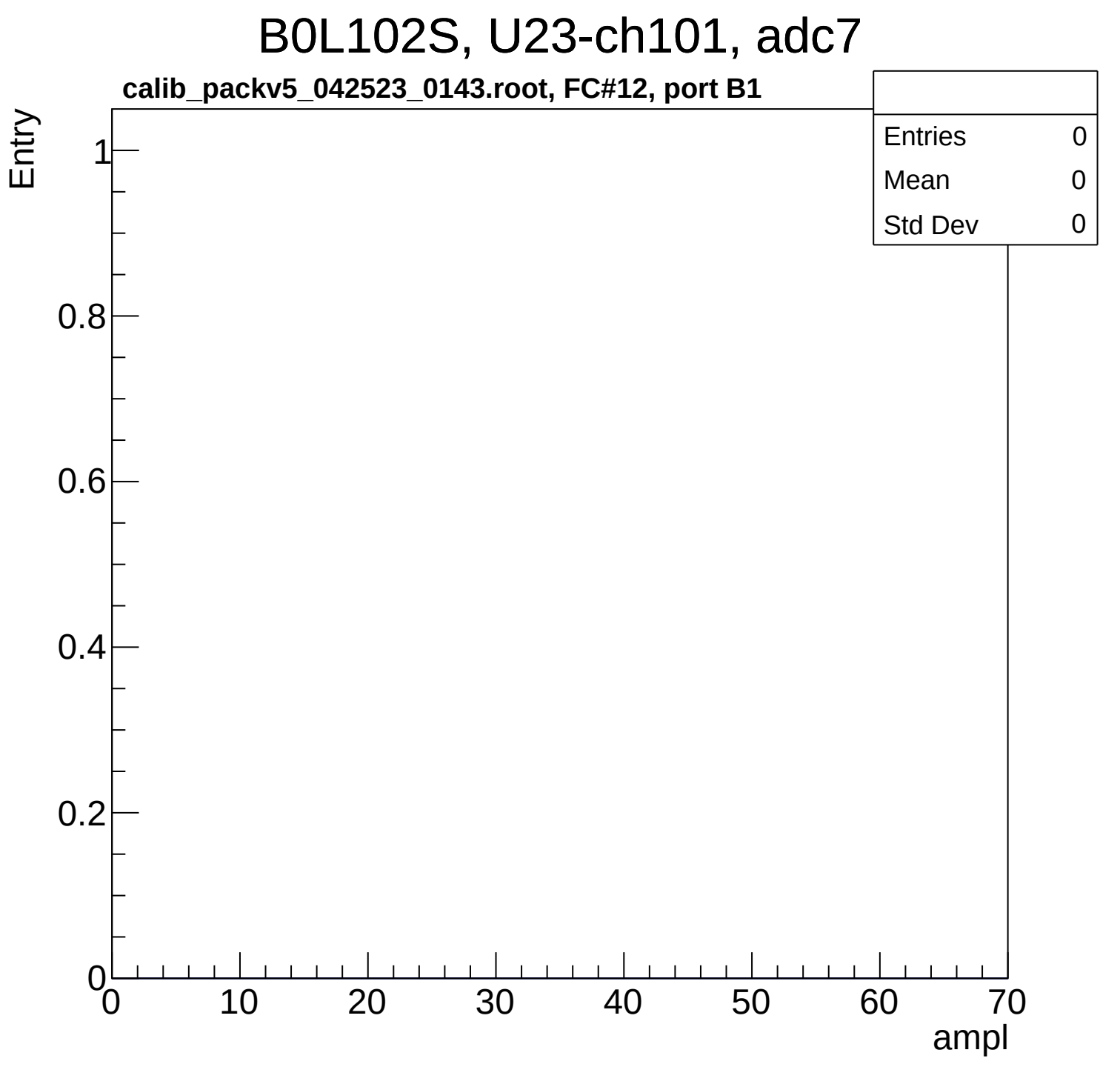

B0L102S, U23-ch101, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 40.95
Std Dev 3.176
Gaus mean : 40.9902
Gaus Width: 3.1217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

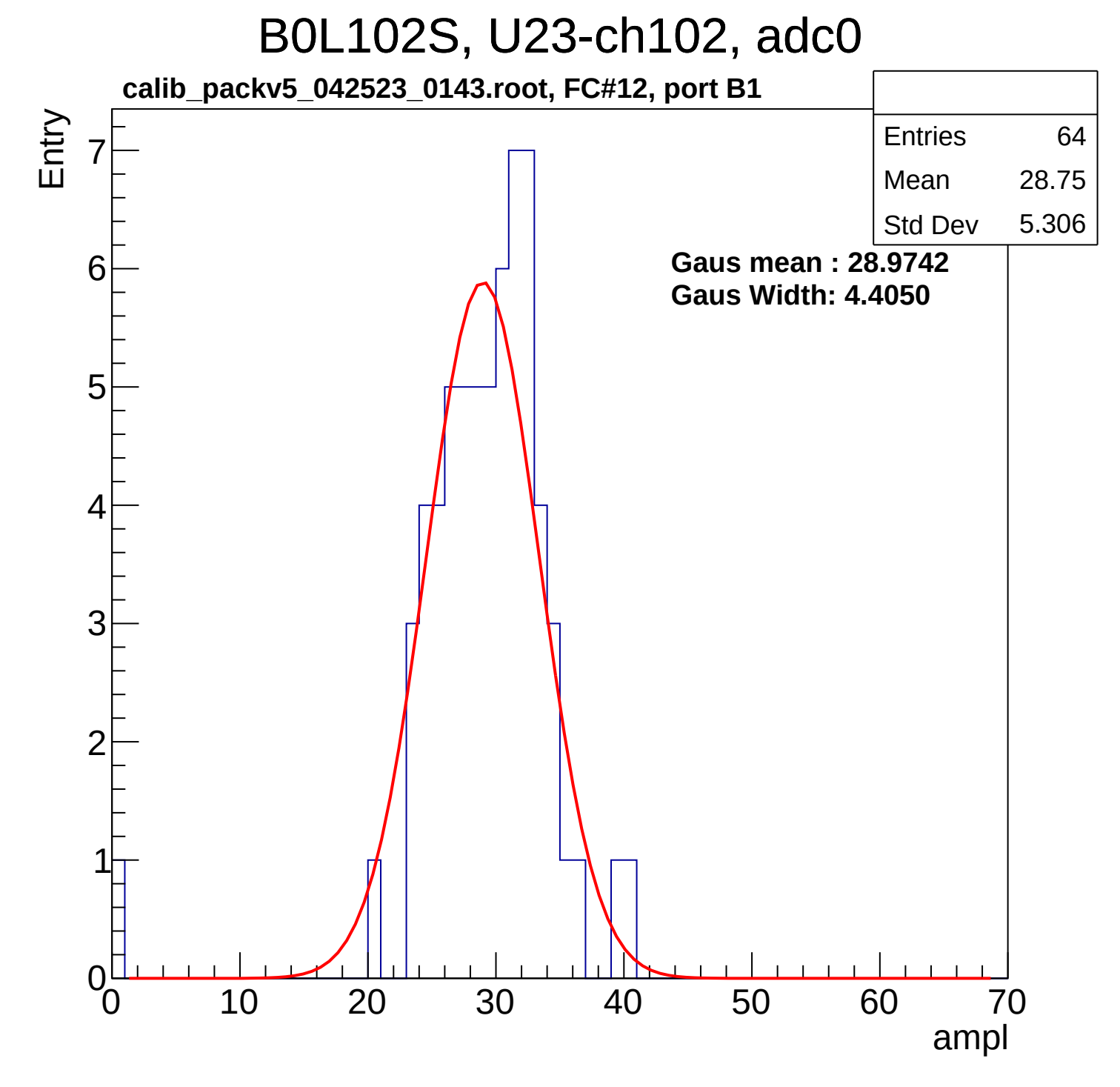

B0L102S, U23-ch101, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 91
Mean 48.18
Std Dev 3.935
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

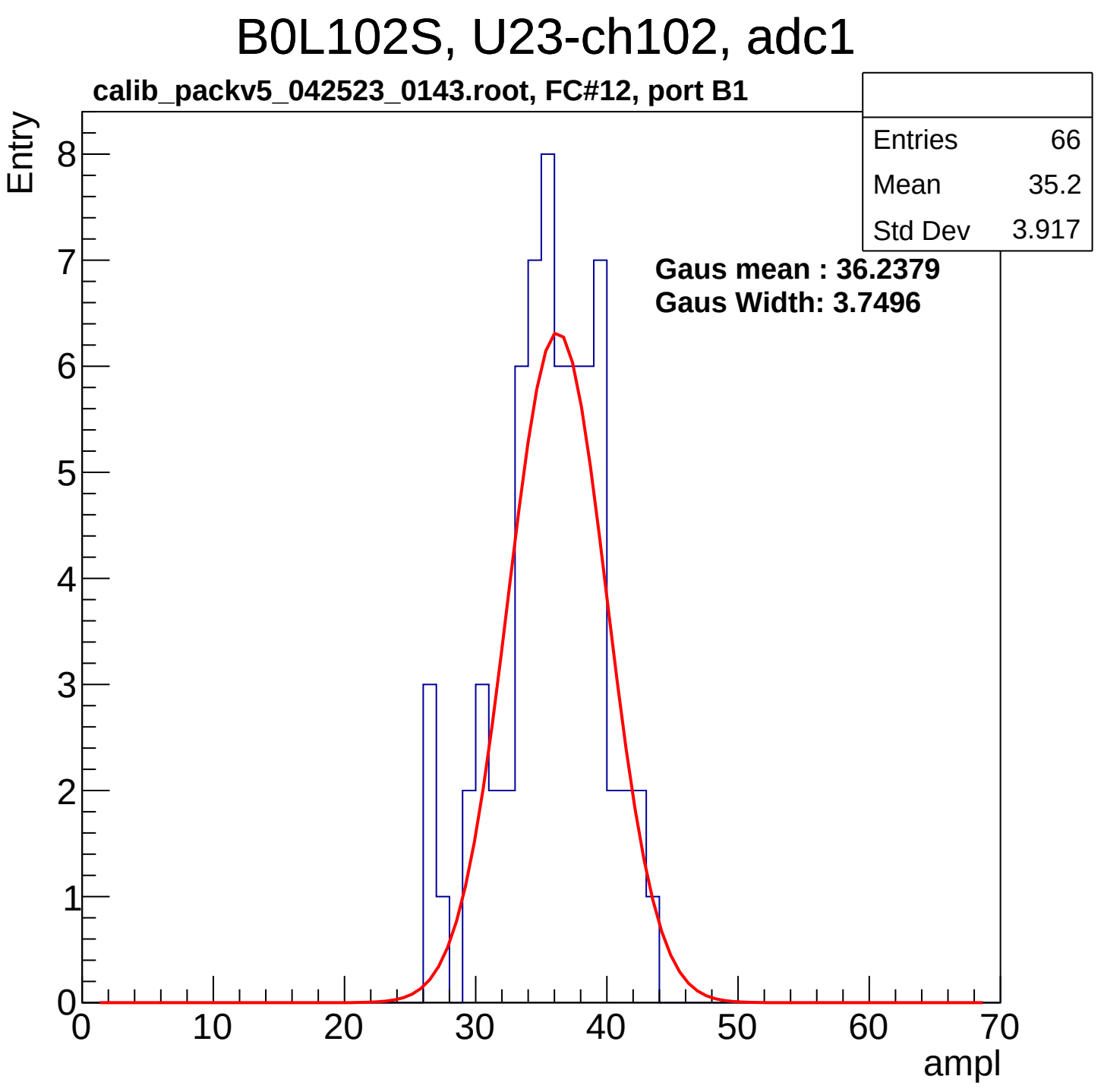

B0L102S, U23-ch101, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 55.63
Std Dev 3.42
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

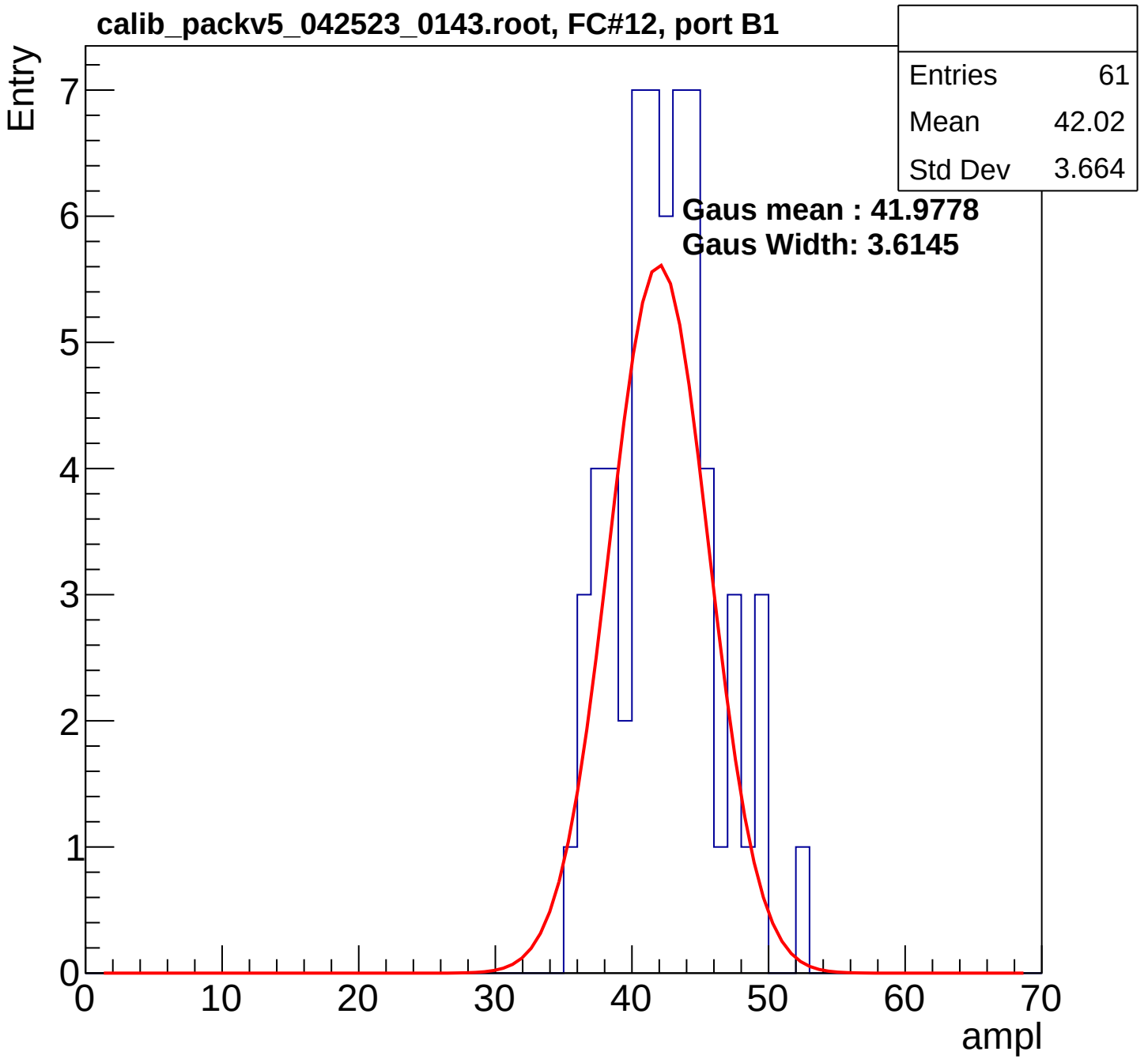

B0L102S, U23-ch101, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 58.38
Std Dev 9.708
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L102S, U23-ch101, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 10
Mean 61.5
Std Dev 1.285
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch101, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch102, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 28.75
Std Dev 5.306
Gaus mean : 28.9742
Gaus Width: 4.4050
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch102, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 35.2
Std Dev 3.917
Gaus mean : 36.2379
Gaus Width: 3.7496
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch102, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 42.02
Std Dev 3.664
Gaus mean : 41.9778
Gaus Width: 3.6145
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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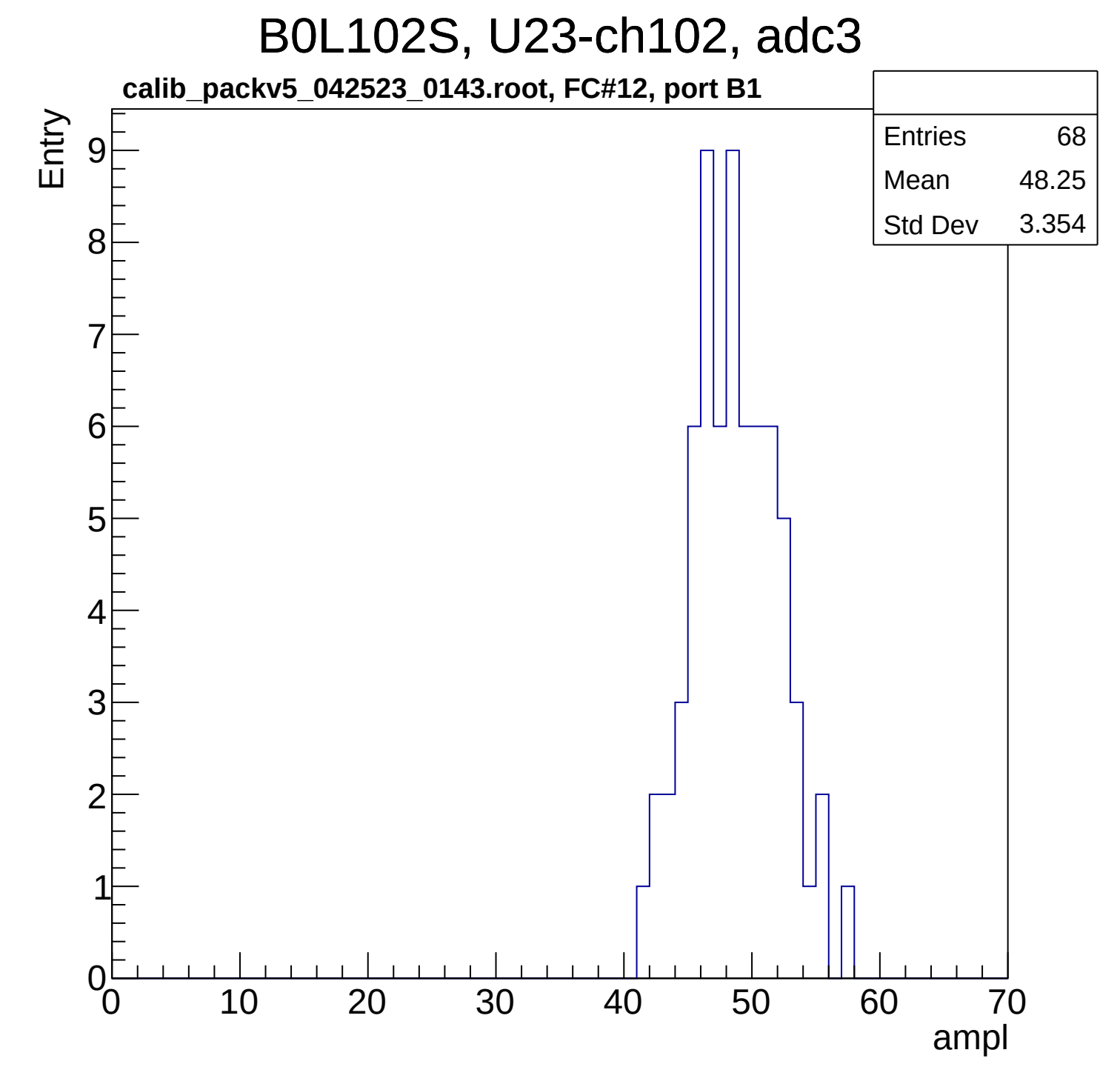
B0L102S, U23-ch102, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.25
Std Dev 3.354
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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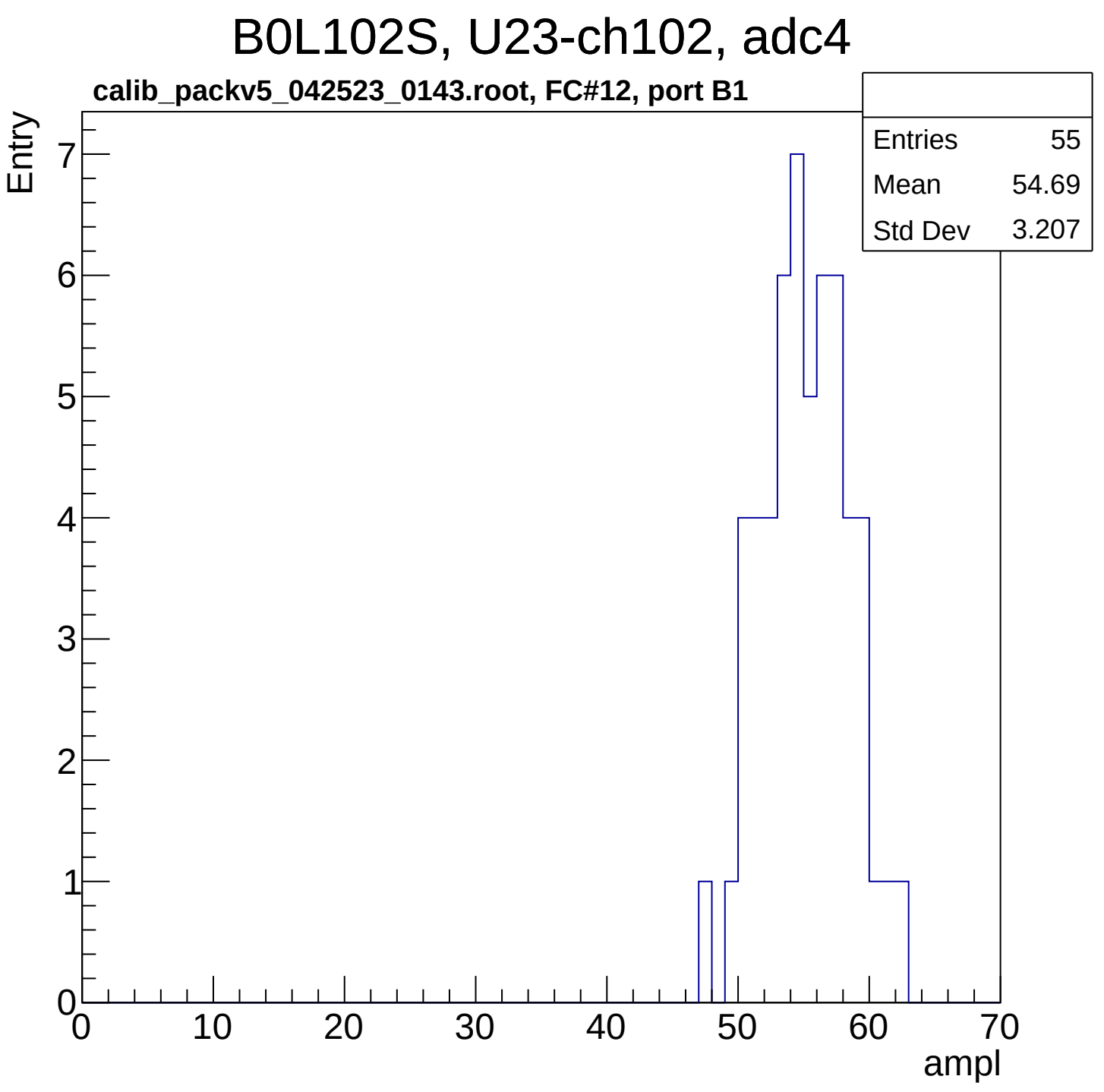
40
50
60
70


B0L102S, U23-ch102, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 54.69
Std Dev 3.207
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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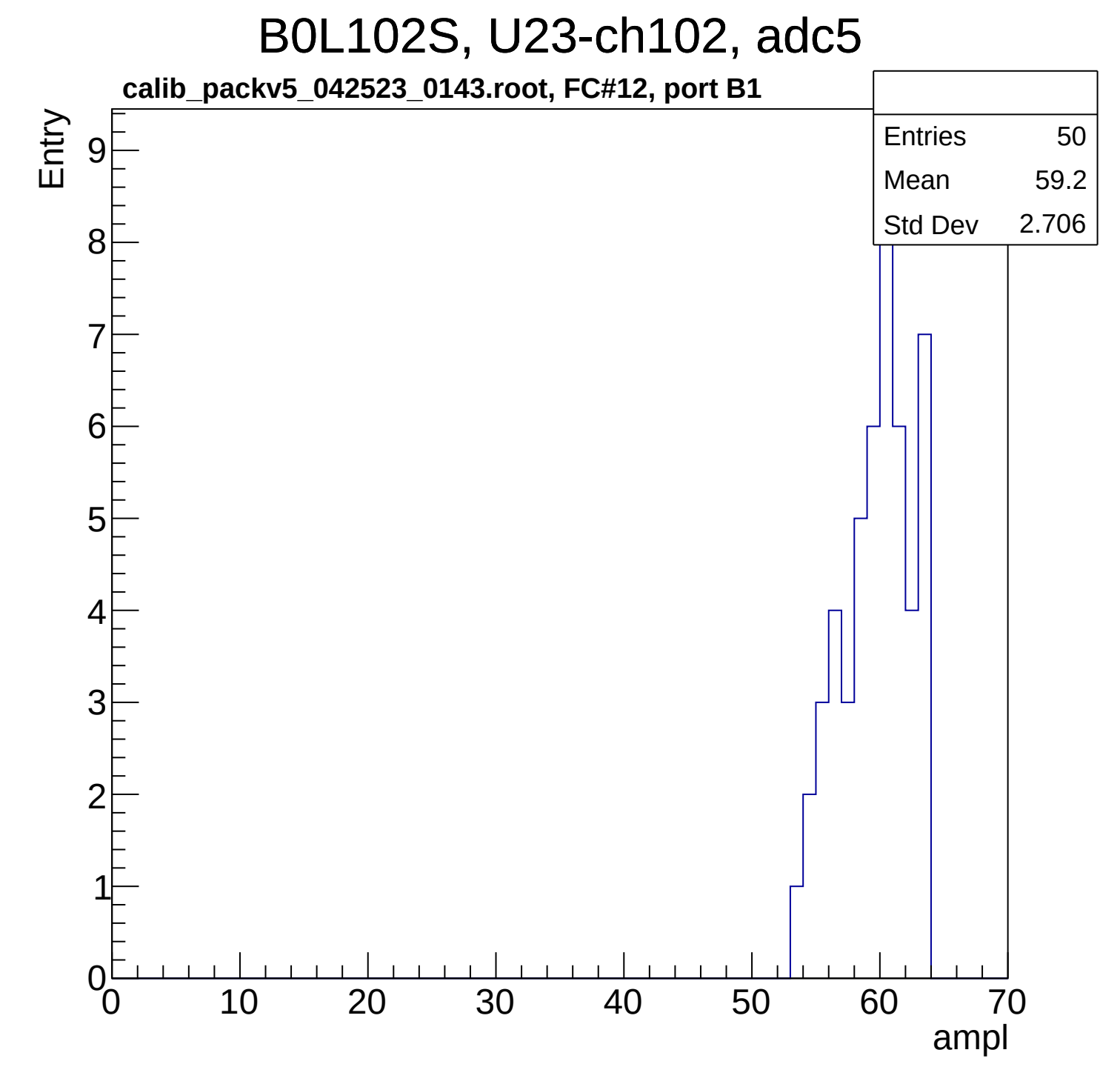
40
50
60
70


B0L102S, U23-ch102, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 59.2
Std Dev 2.706
Entry
ampl
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2
3
4
5
6
7
8
9
0
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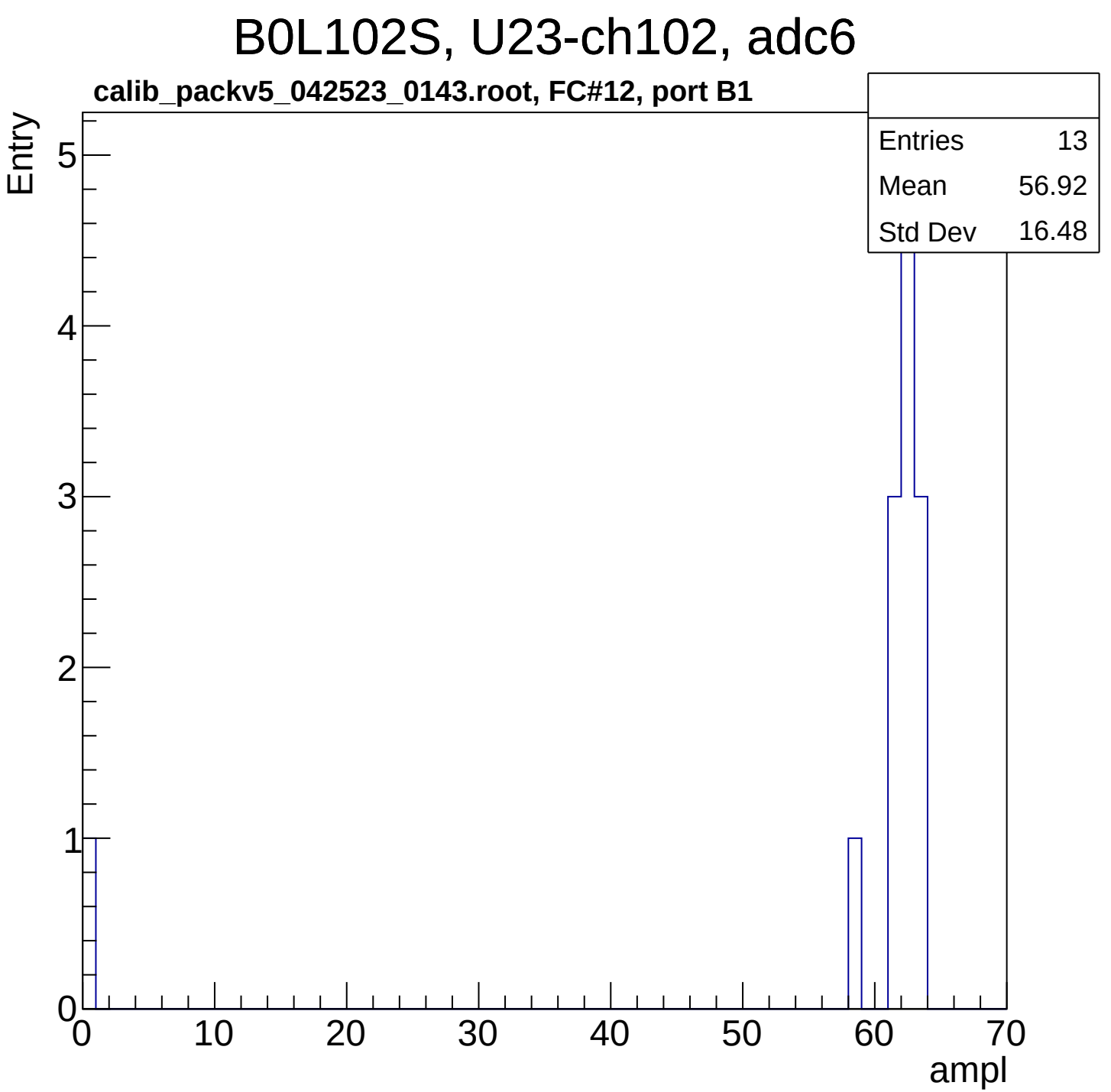
20
30
40
50
60
70


B0L102S, U23-ch102, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.92
Std Dev 16.48
Entry
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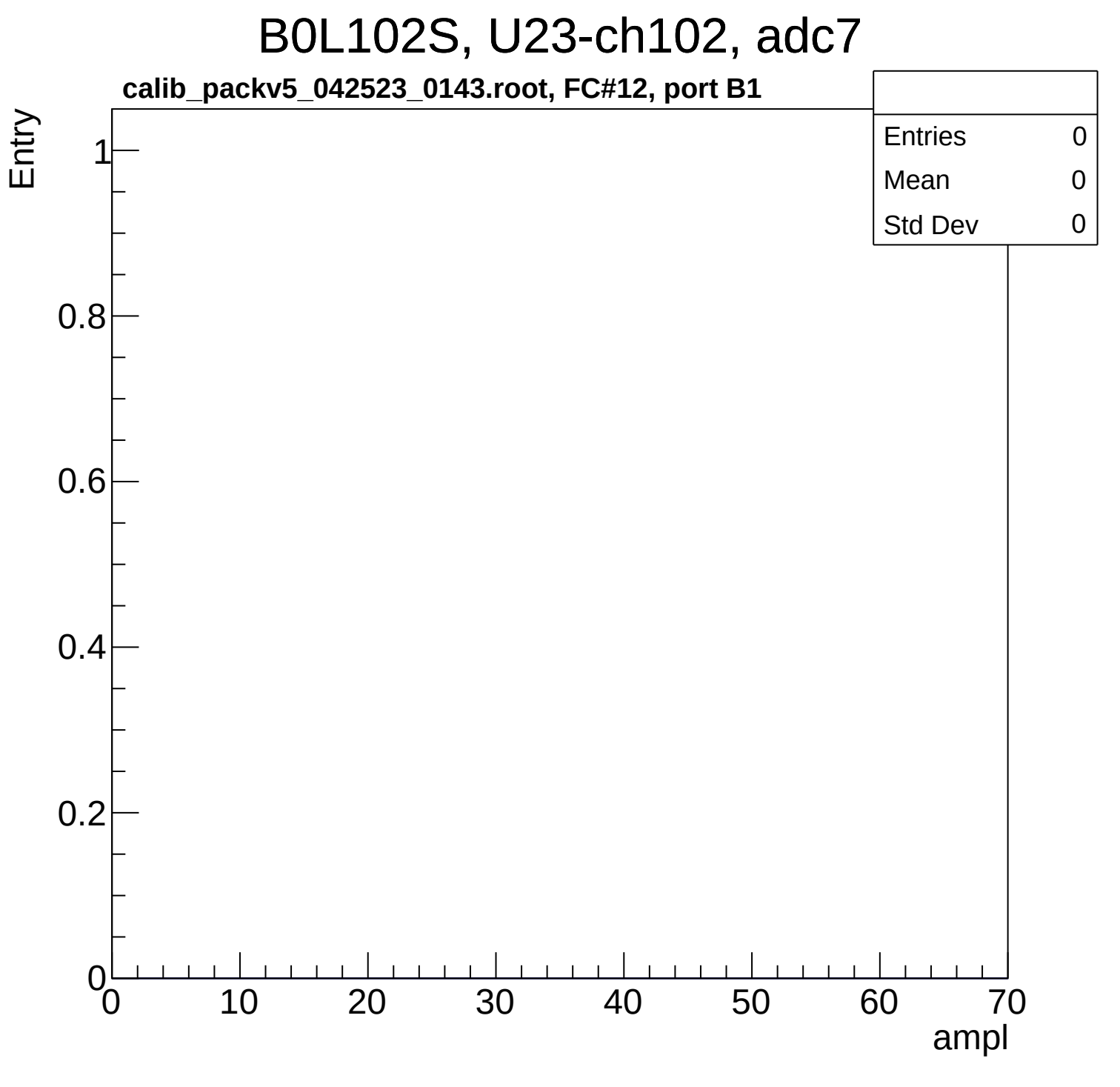
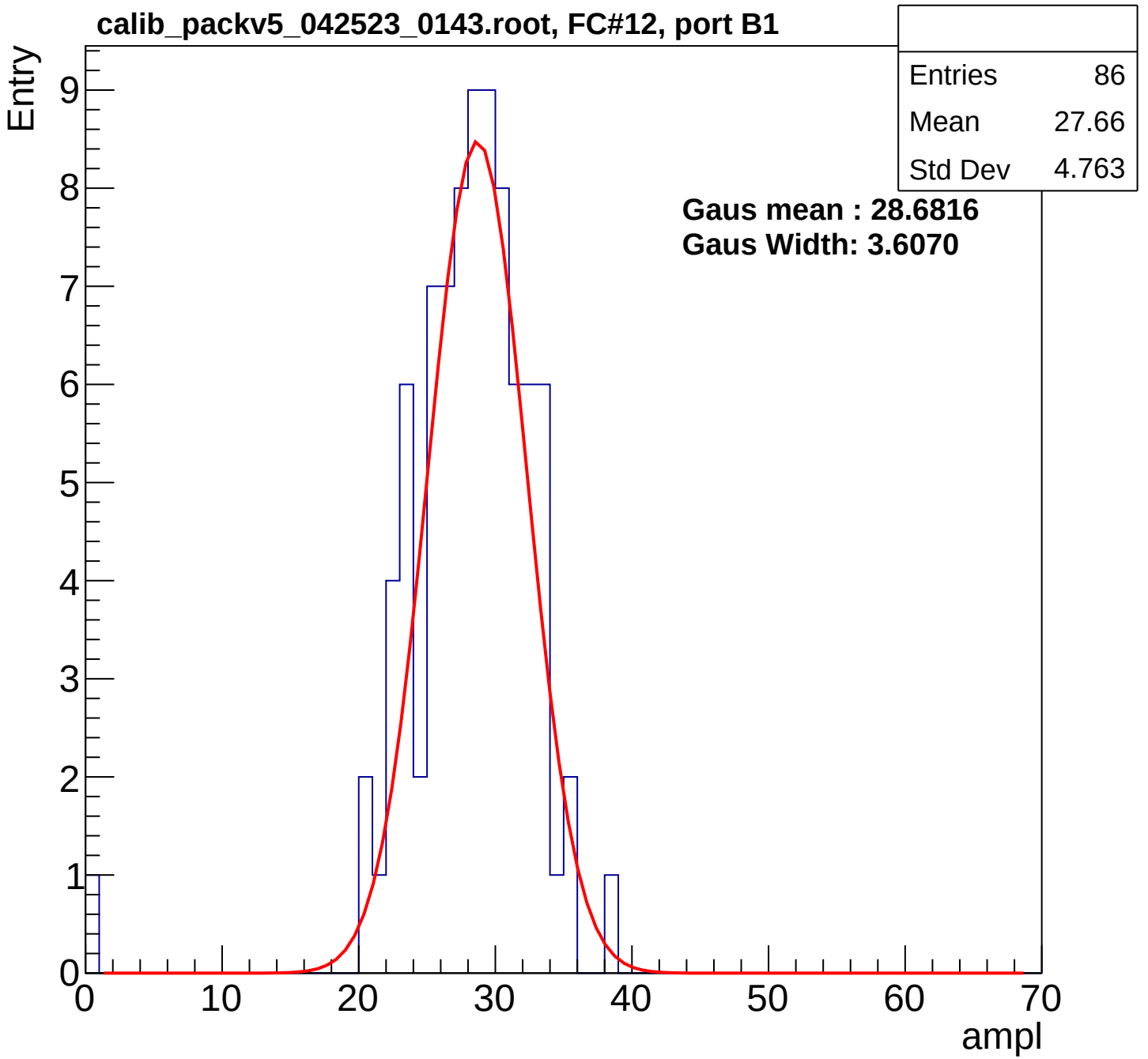
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch102, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch103, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 86
Mean 27.66
Std Dev 4.763
Gaus mean : 28.6816
Gaus Width: 3.6070
Entry
ampl
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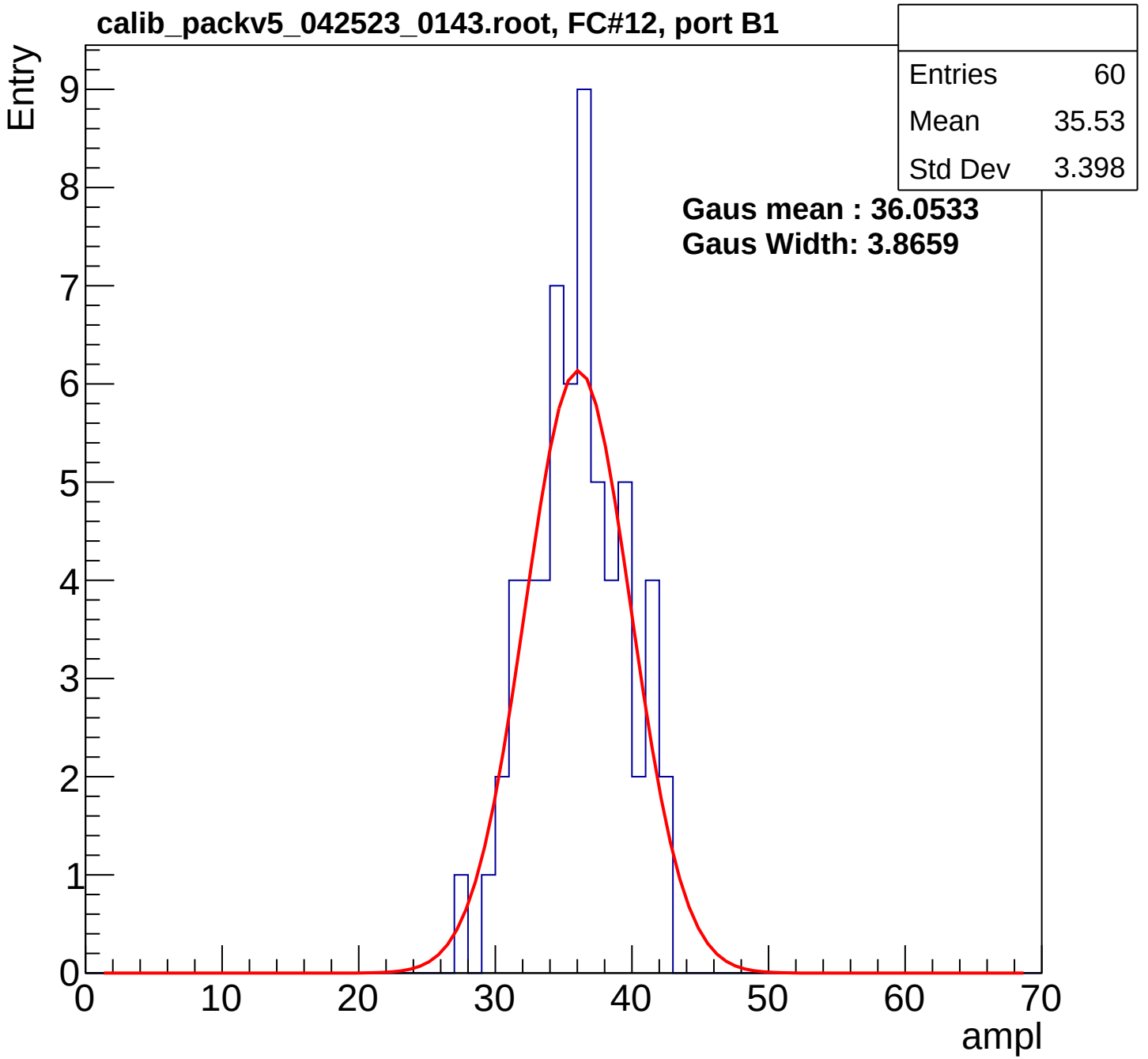
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch103, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 35.53
Std Dev 3.398
Gaus mean : 36.0533
Gaus Width: 3.8659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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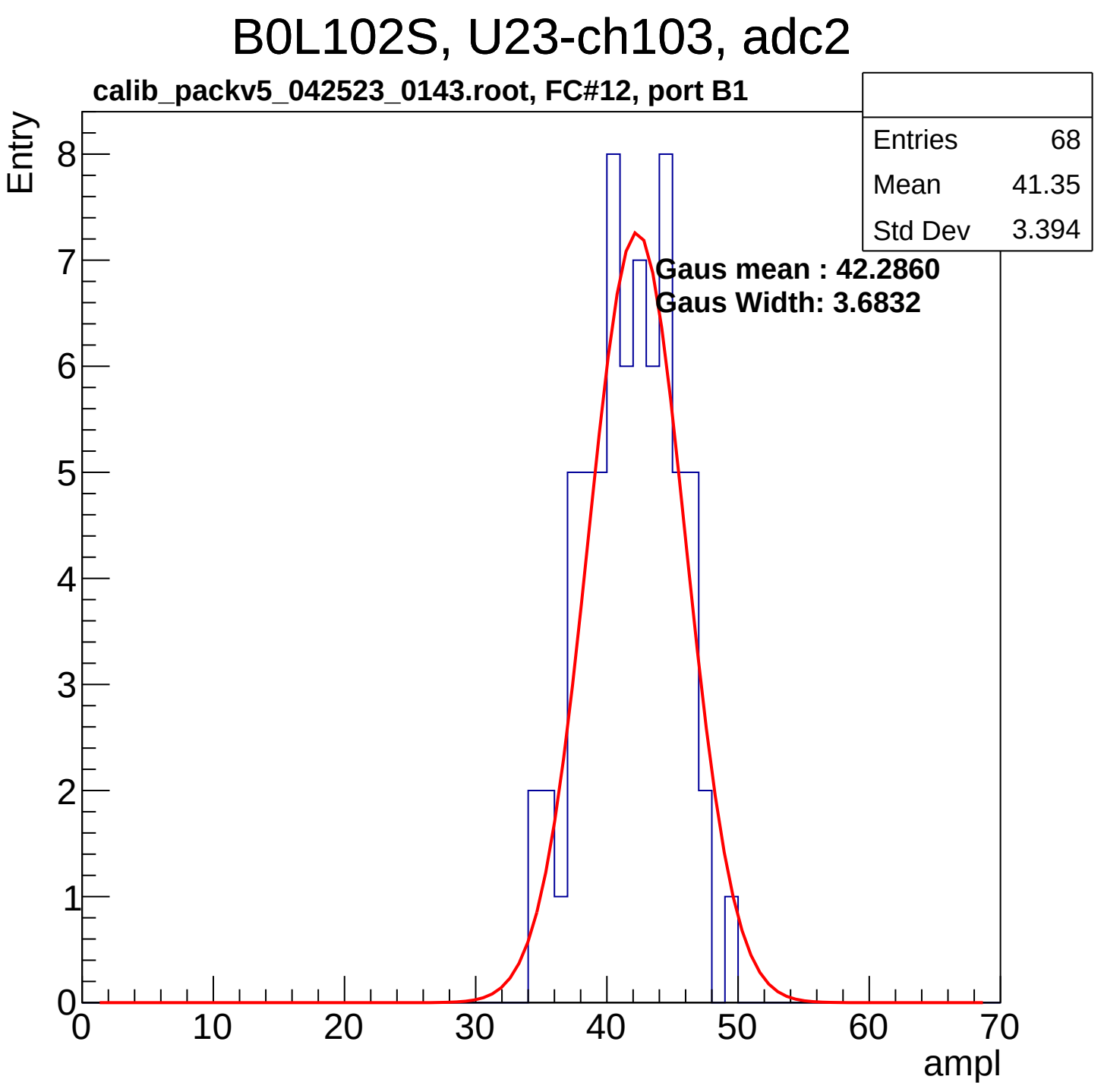
B0L102S, U23-ch103, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 41.35
Std Dev 3.394
Gaus mean : 42.2860
Gaus Width: 3.6832
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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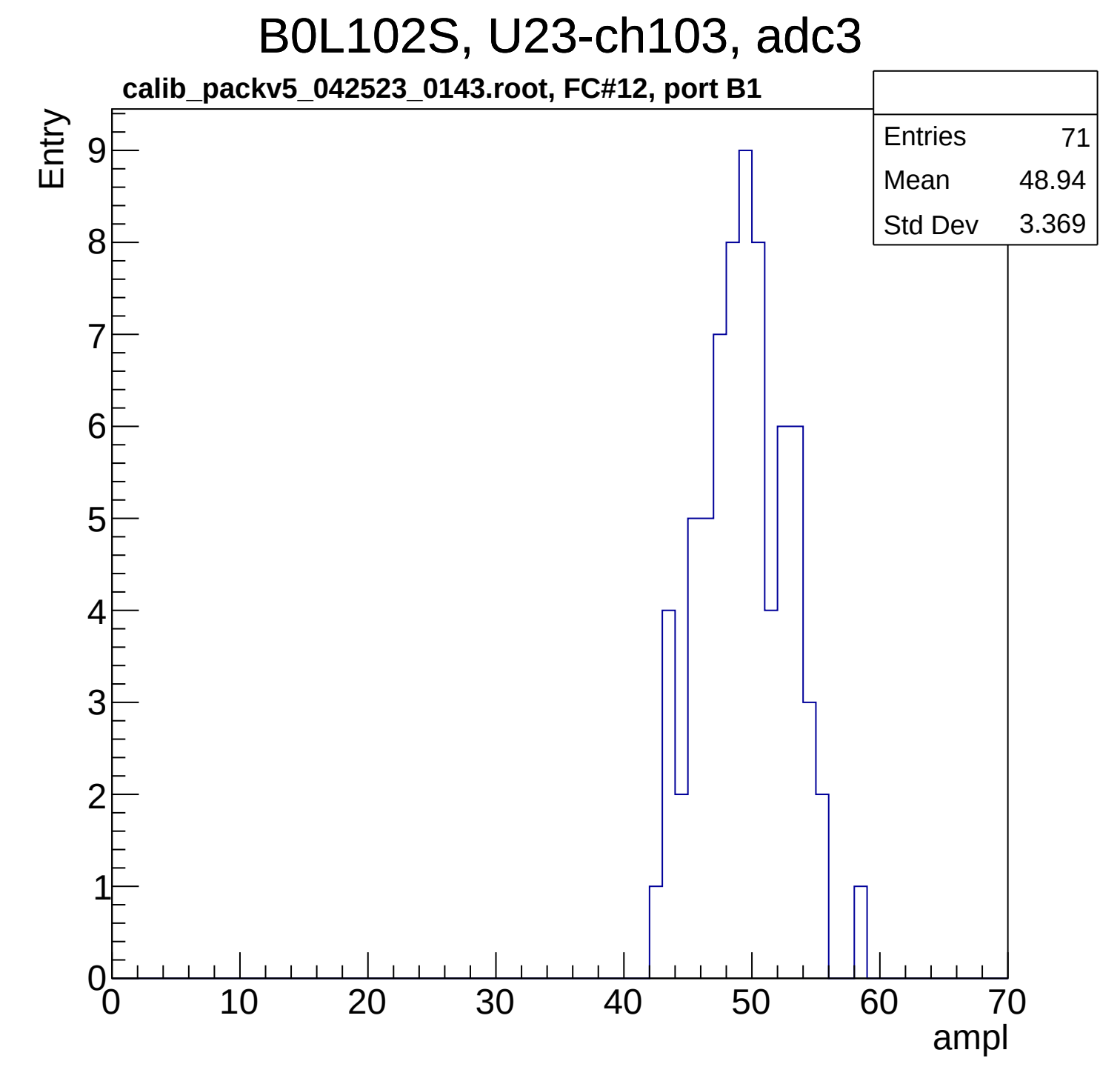
B0L102S, U23-ch103, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 48.94
Std Dev 3.369
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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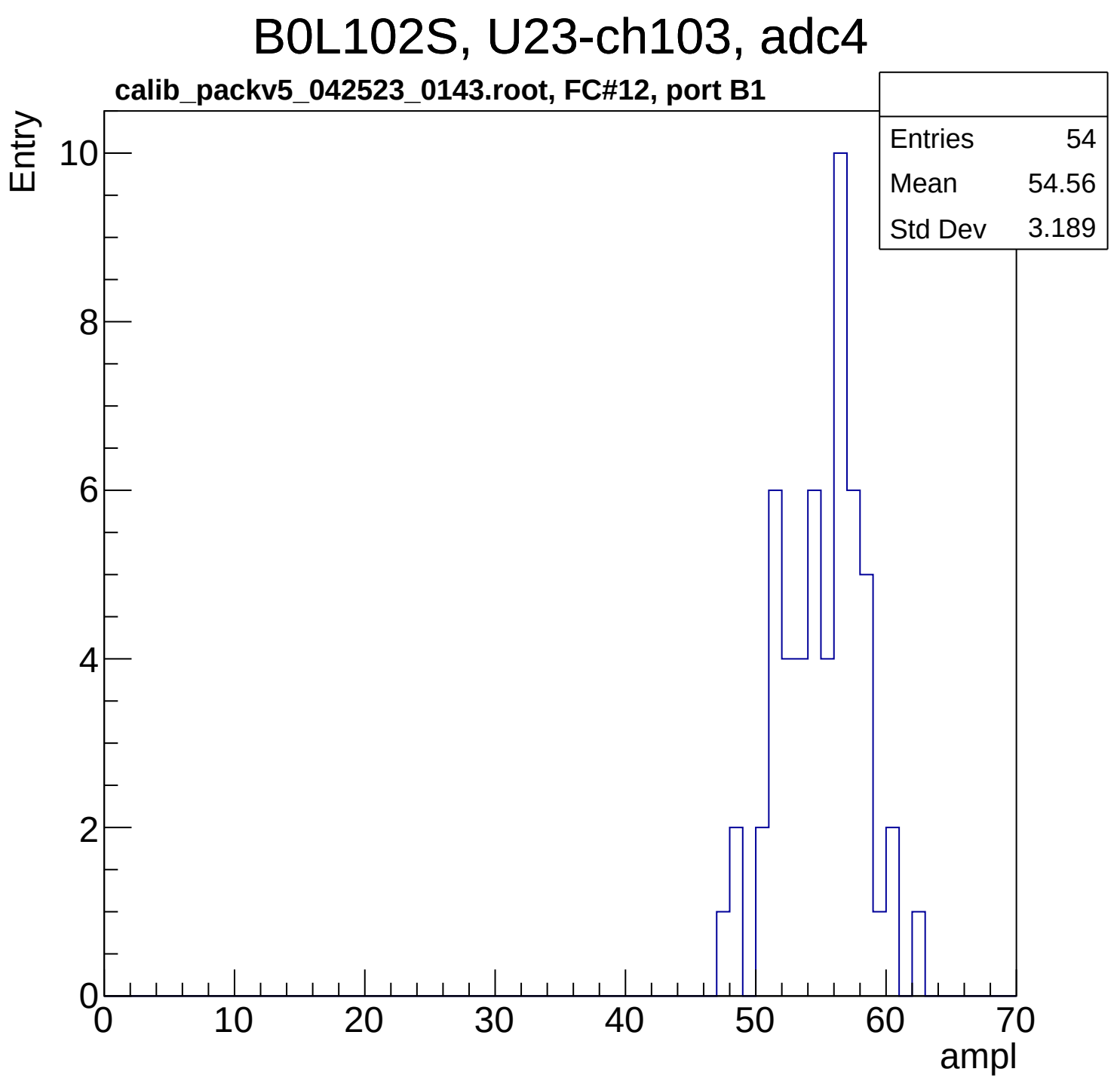
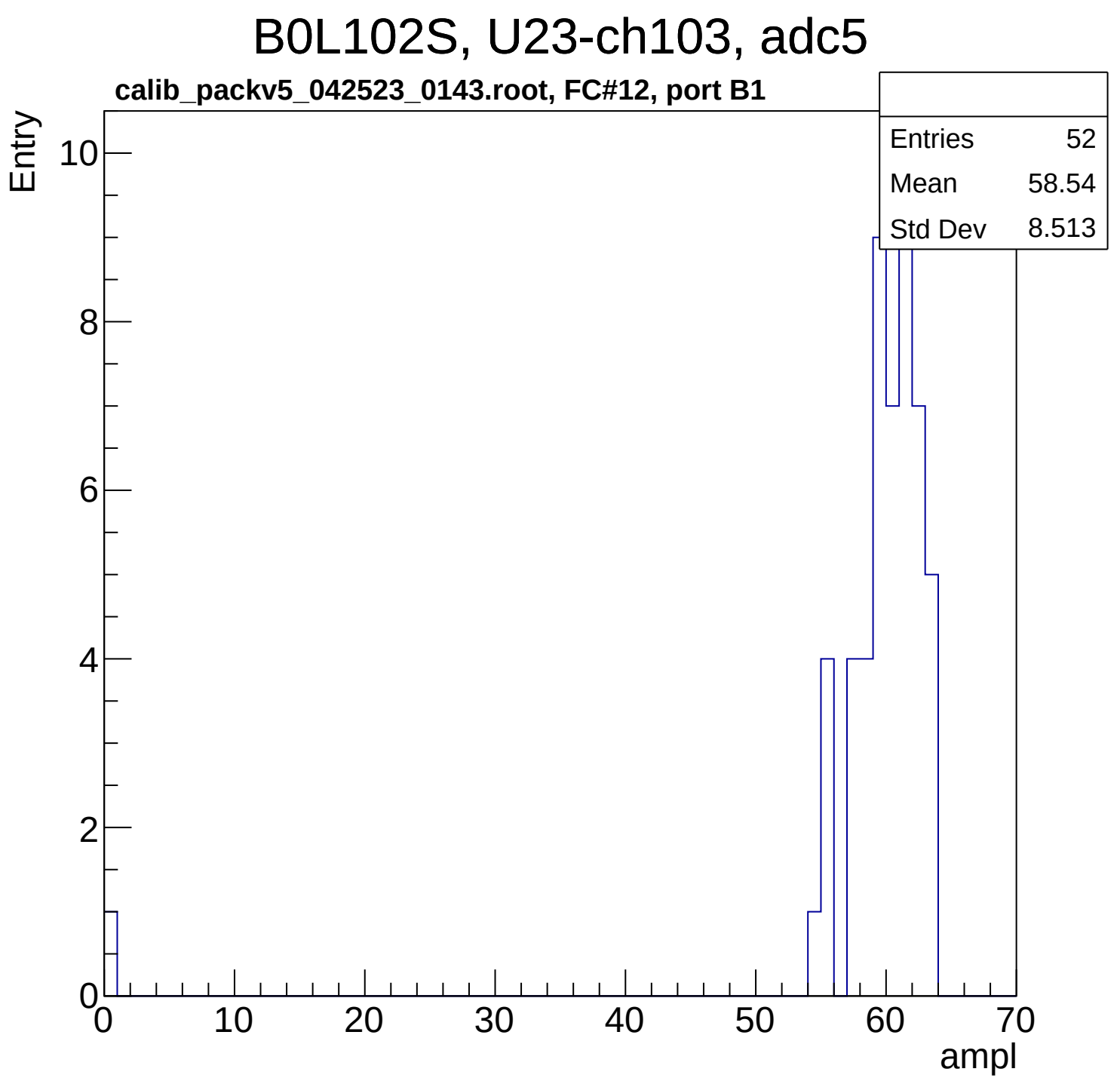

B0L102S, U23-ch103, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 54.56
Std Dev 3.189
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch103, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 58.54
Std Dev 8.513
Entry
ampl
0
2
4
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60
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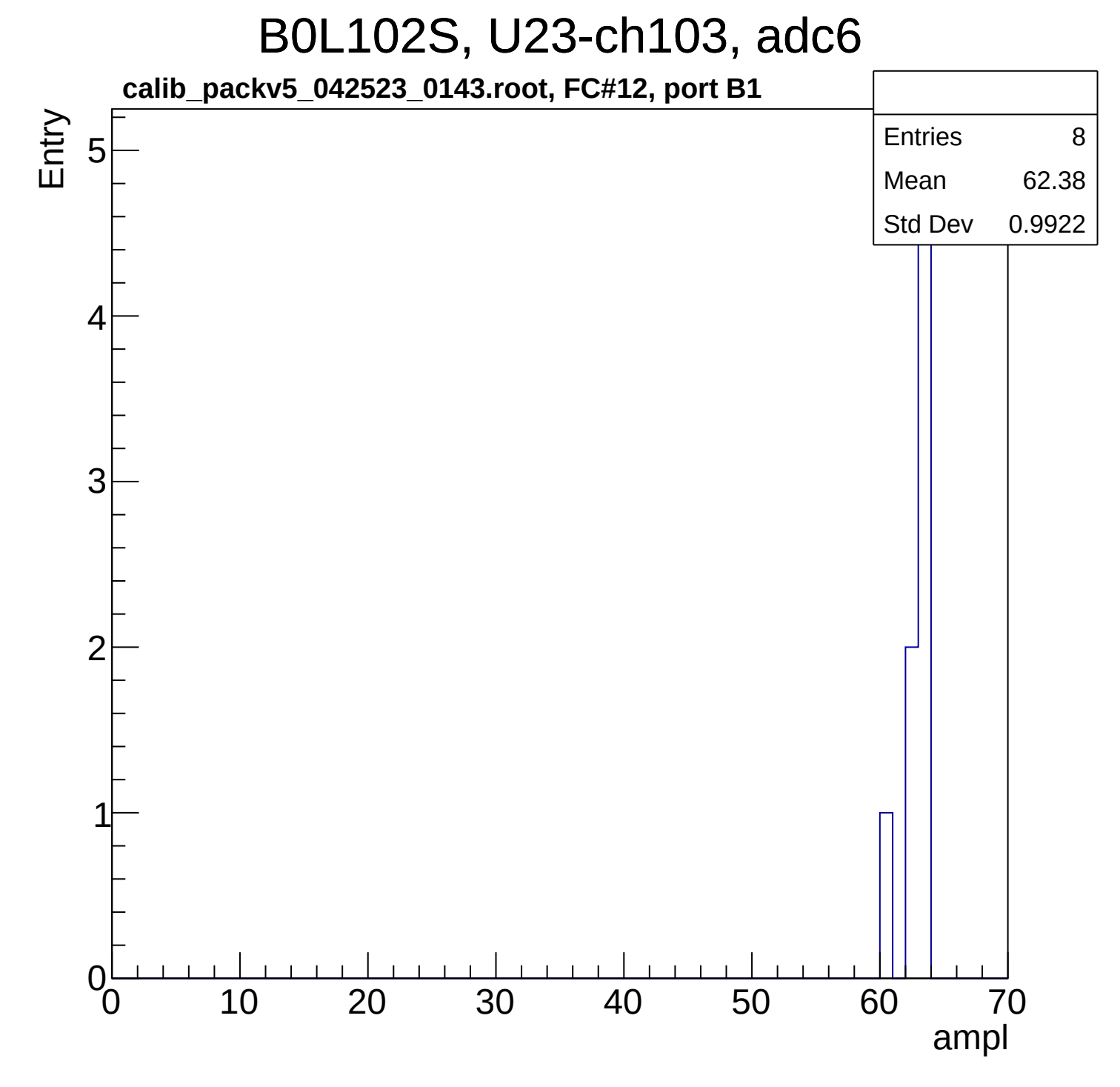
B0L102S, U23-ch103, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 62.38
Std Dev 0.9922
Entry
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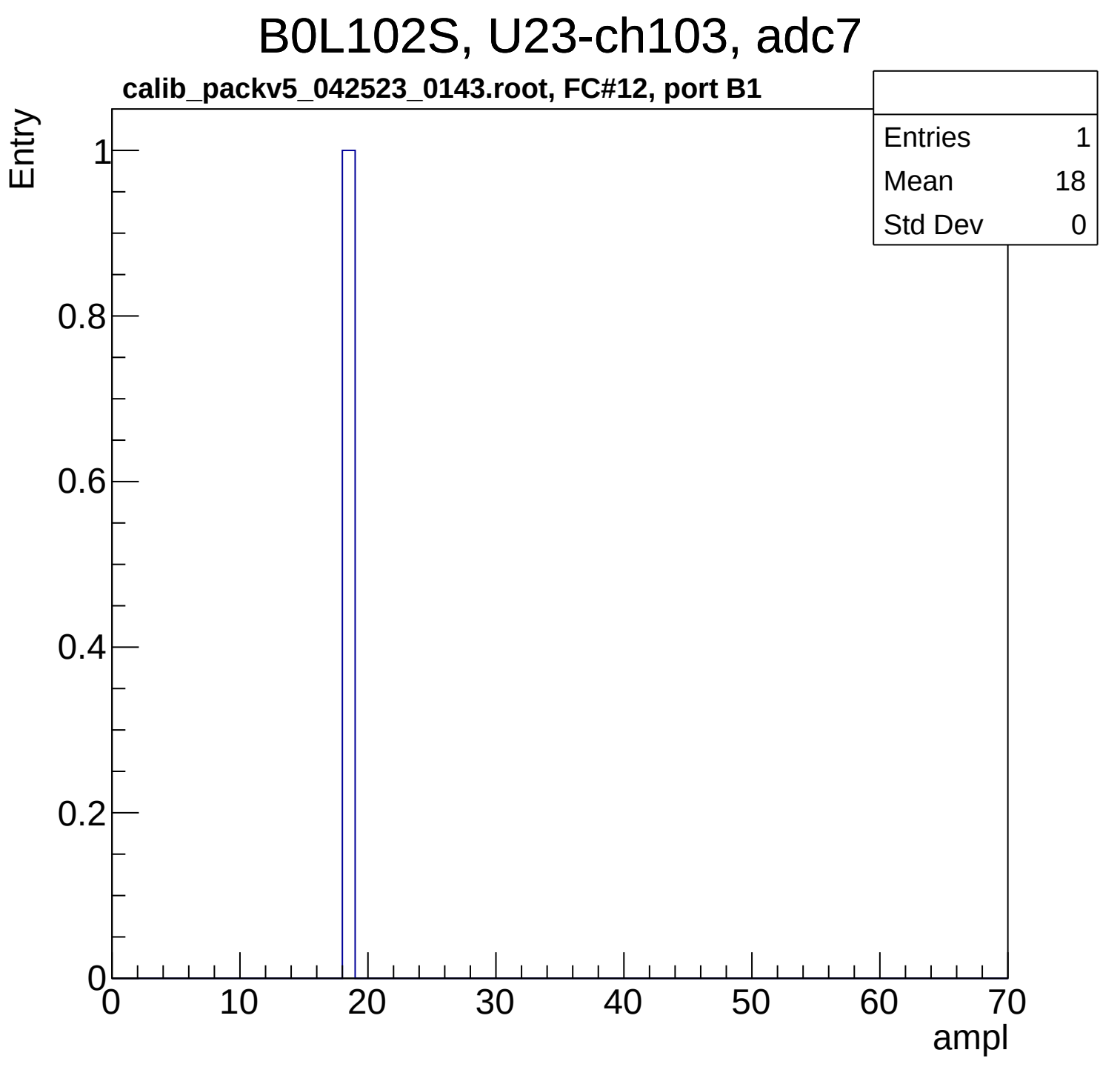
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch103, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 18
Std Dev 0
Entry
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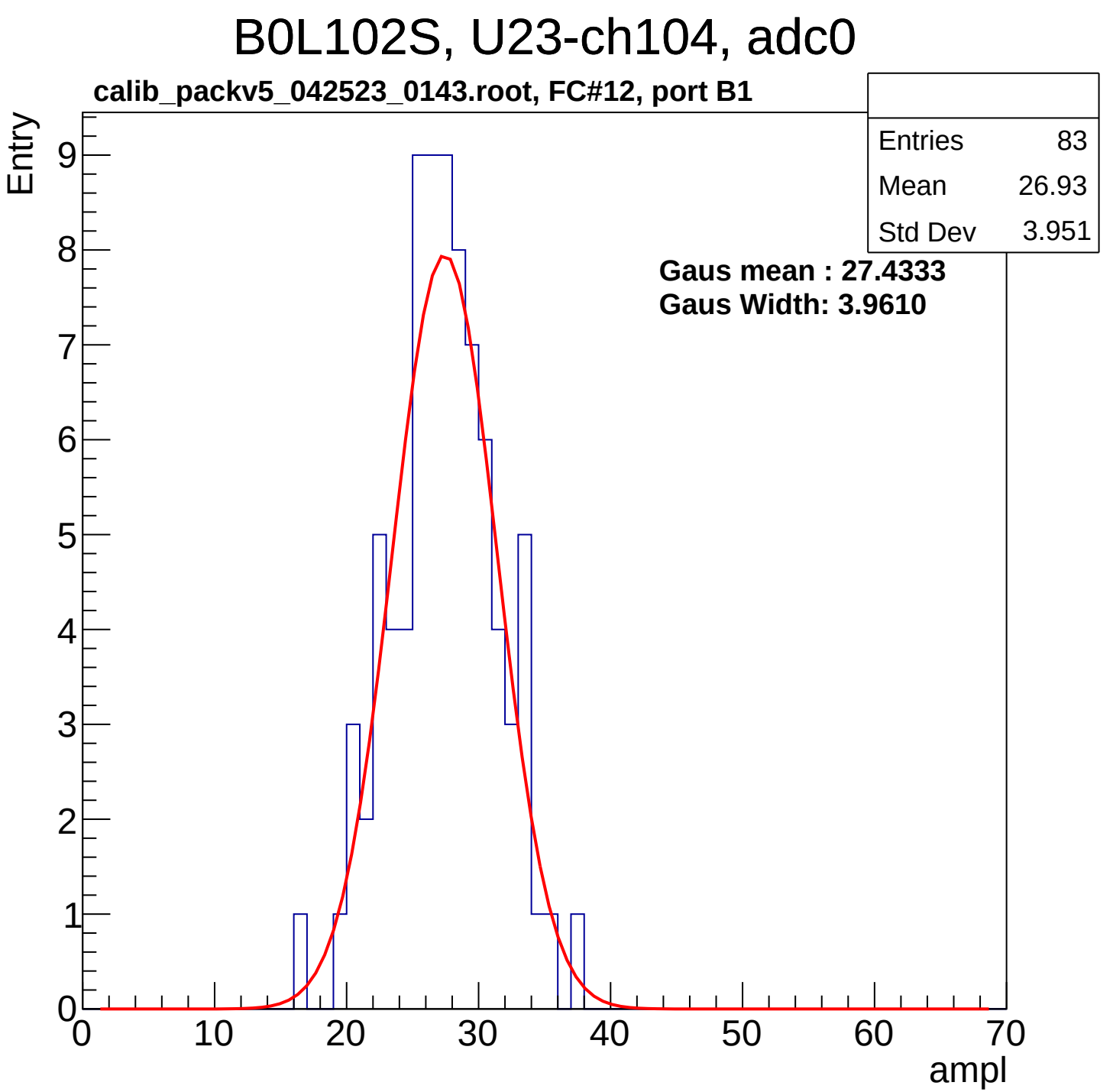
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch104, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 26.93
Std Dev 3.951
Gaus mean : 27.4333
Gaus Width: 3.9610
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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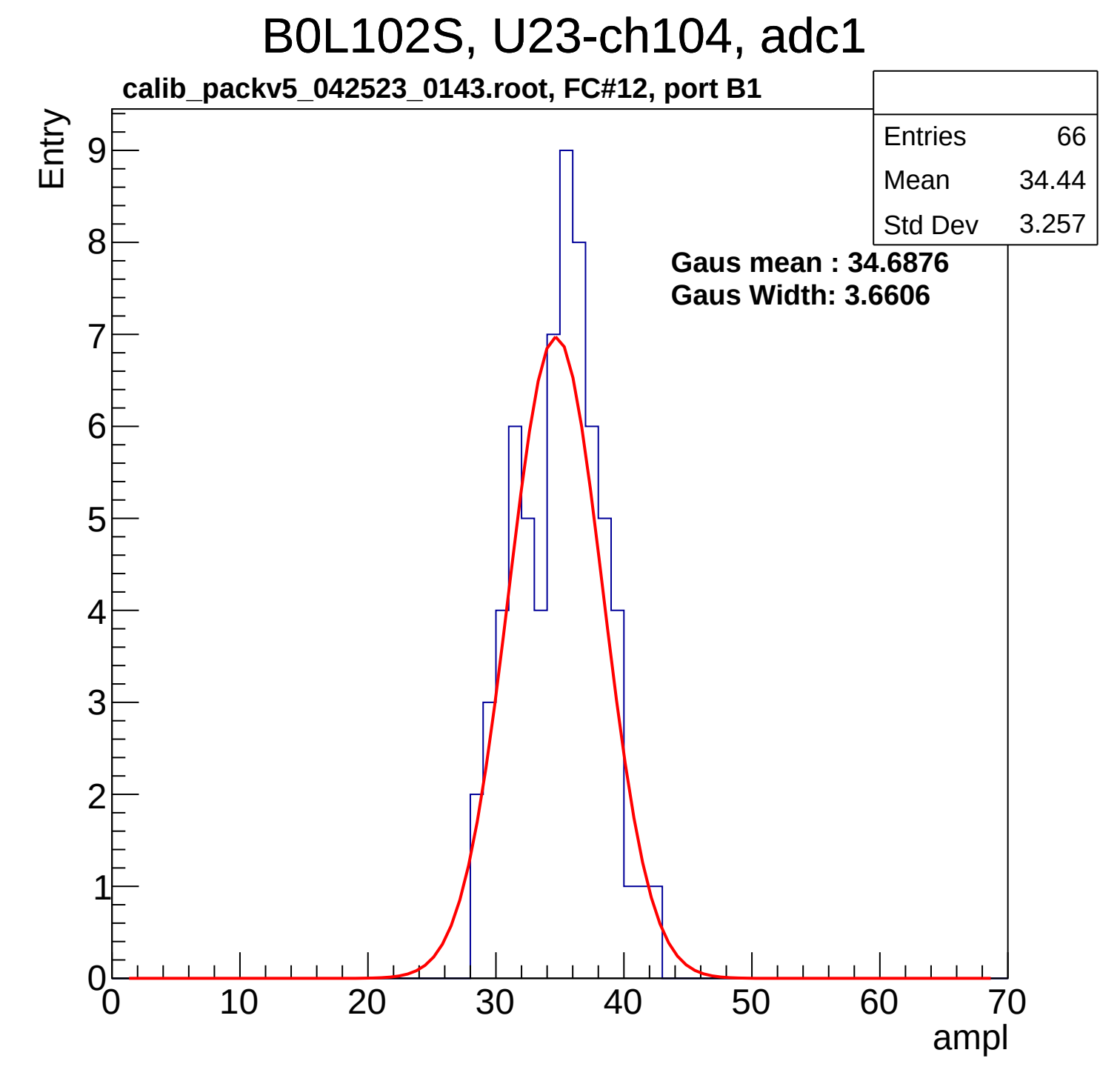
B0L102S, U23-ch104, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 34.44
Std Dev 3.257
Gaus mean : 34.6876
Gaus Width: 3.6606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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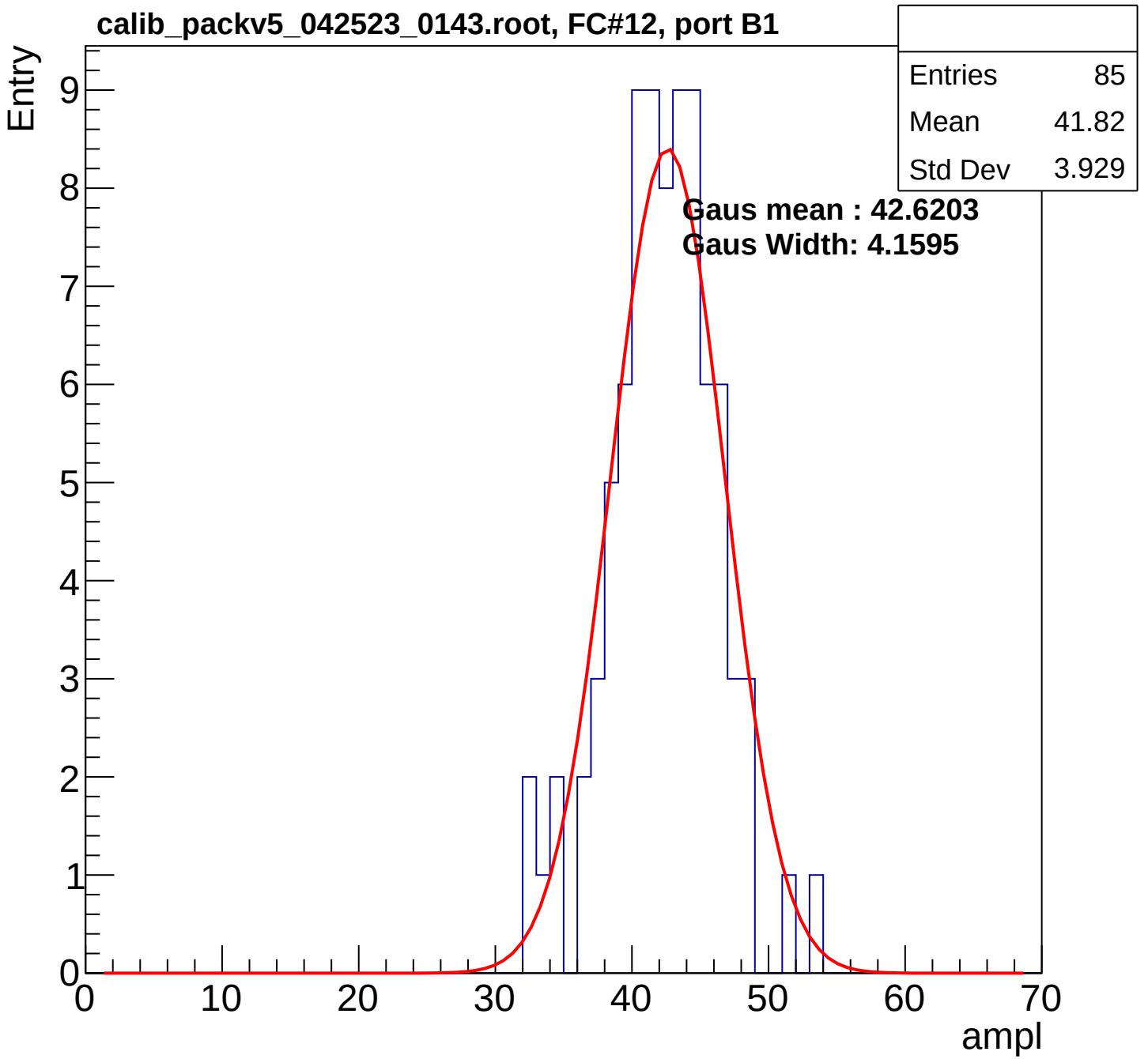
B0L102S, U23-ch104, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 85
Mean 41.82
Std Dev 3.929
Gaus mean : 42.6203
Gaus Width: 4.1595
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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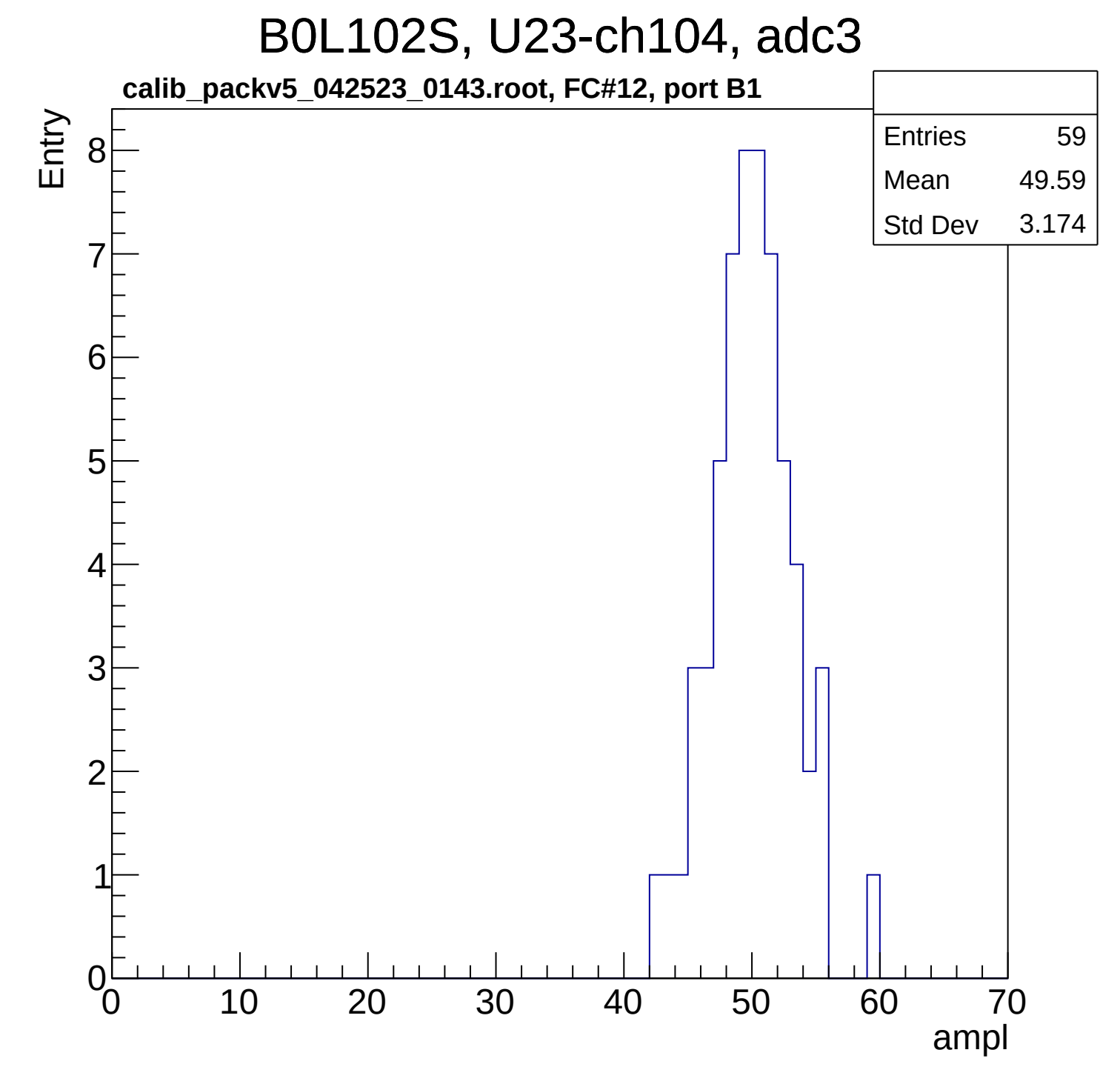
B0L102S, U23-ch104, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 49.59
Std Dev 3.174
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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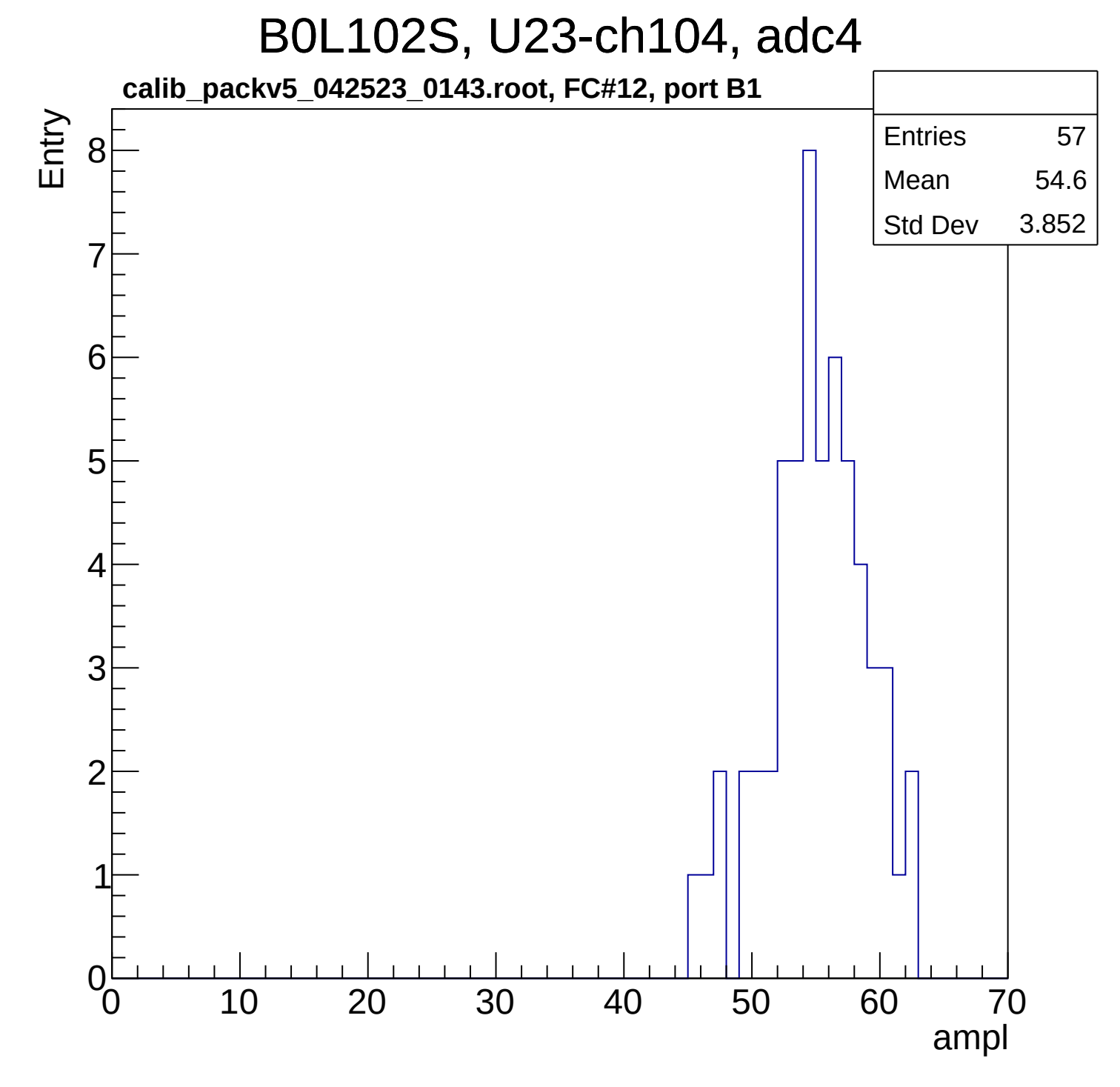
B0L102S, U23-ch104, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 54.6
Std Dev 3.852
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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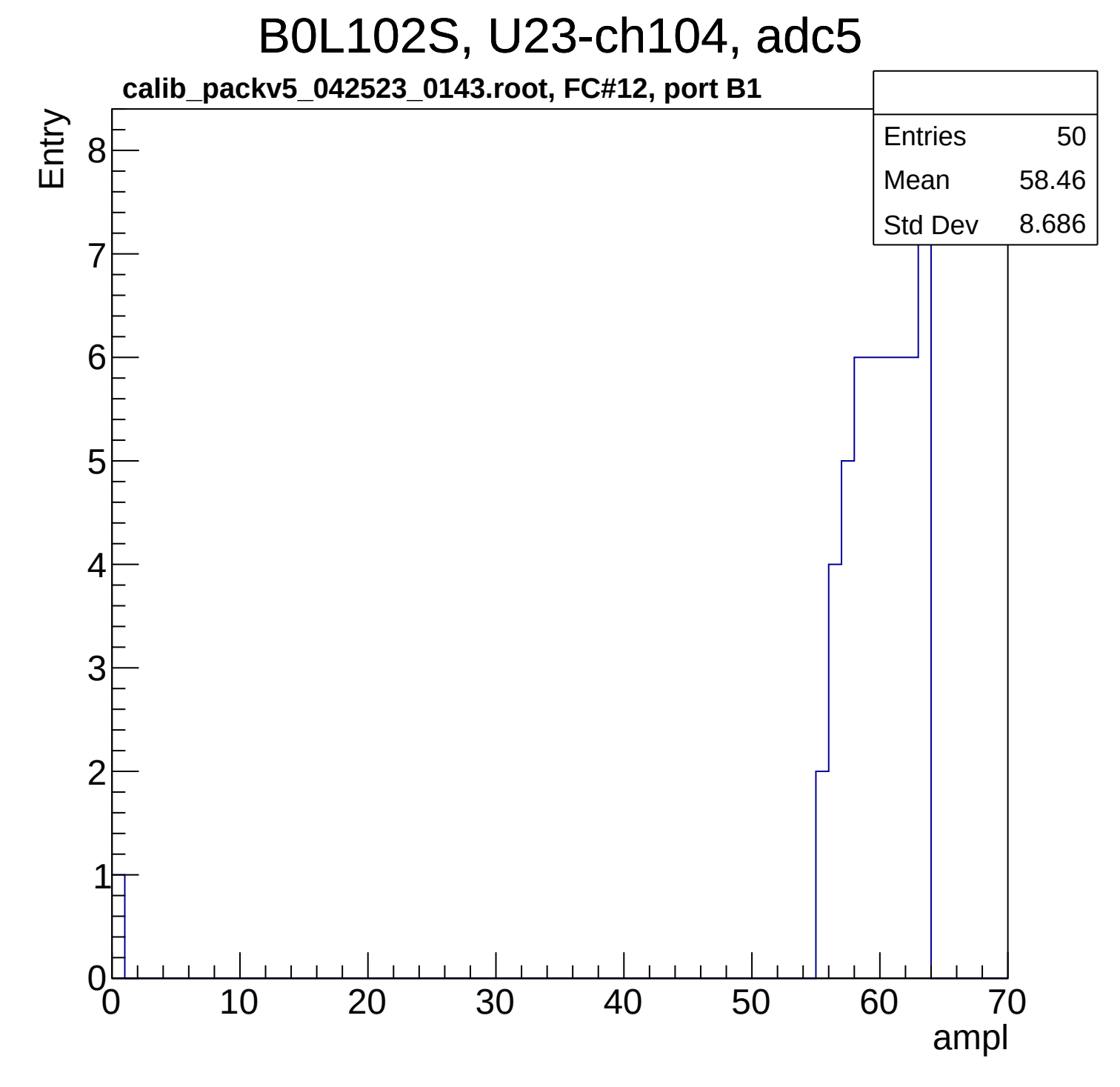
B0L102S, U23-ch104, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.46
Std Dev 8.686
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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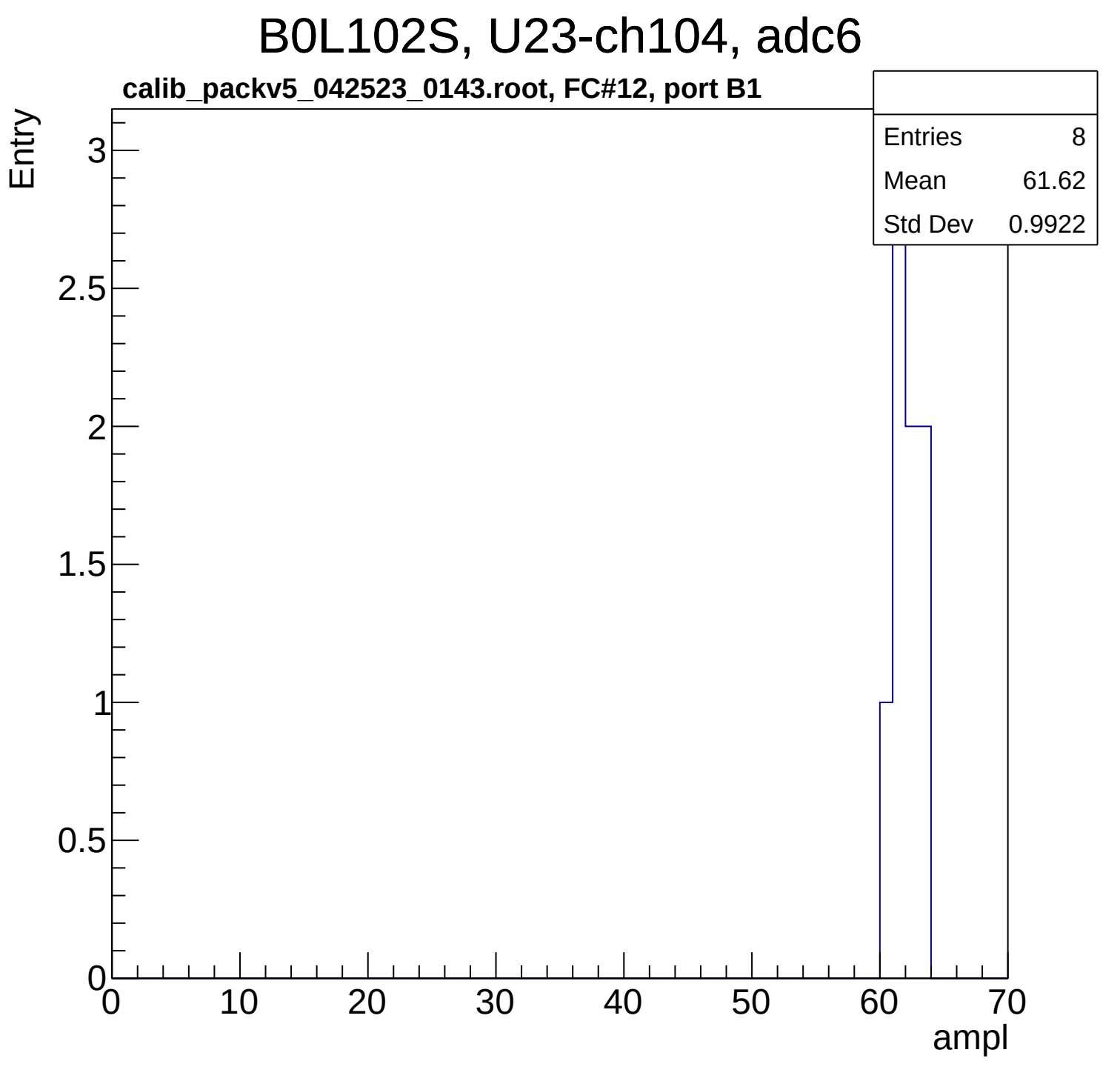
B0L102S, U23-ch104, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.62
Std Dev 0.9922
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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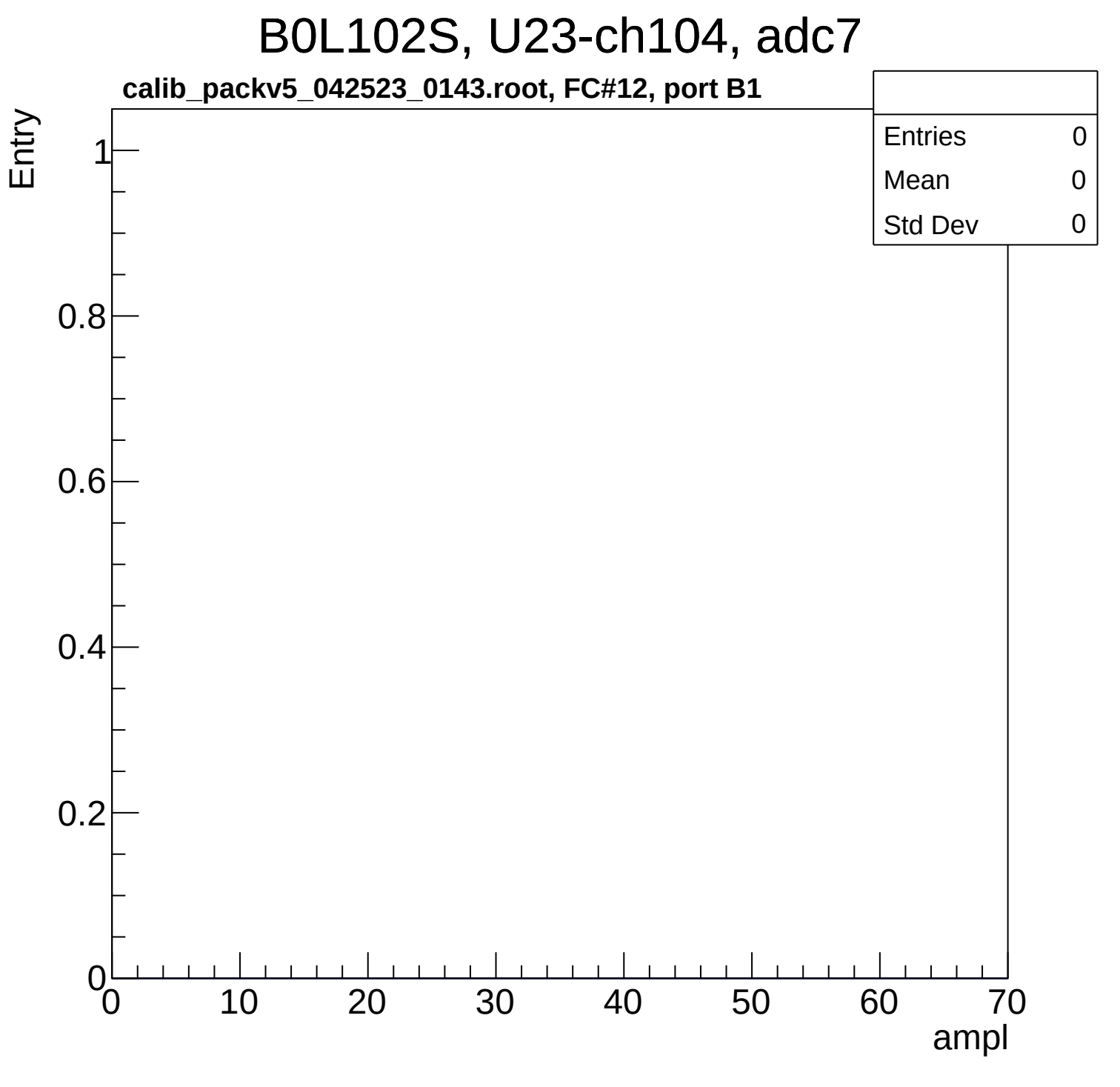
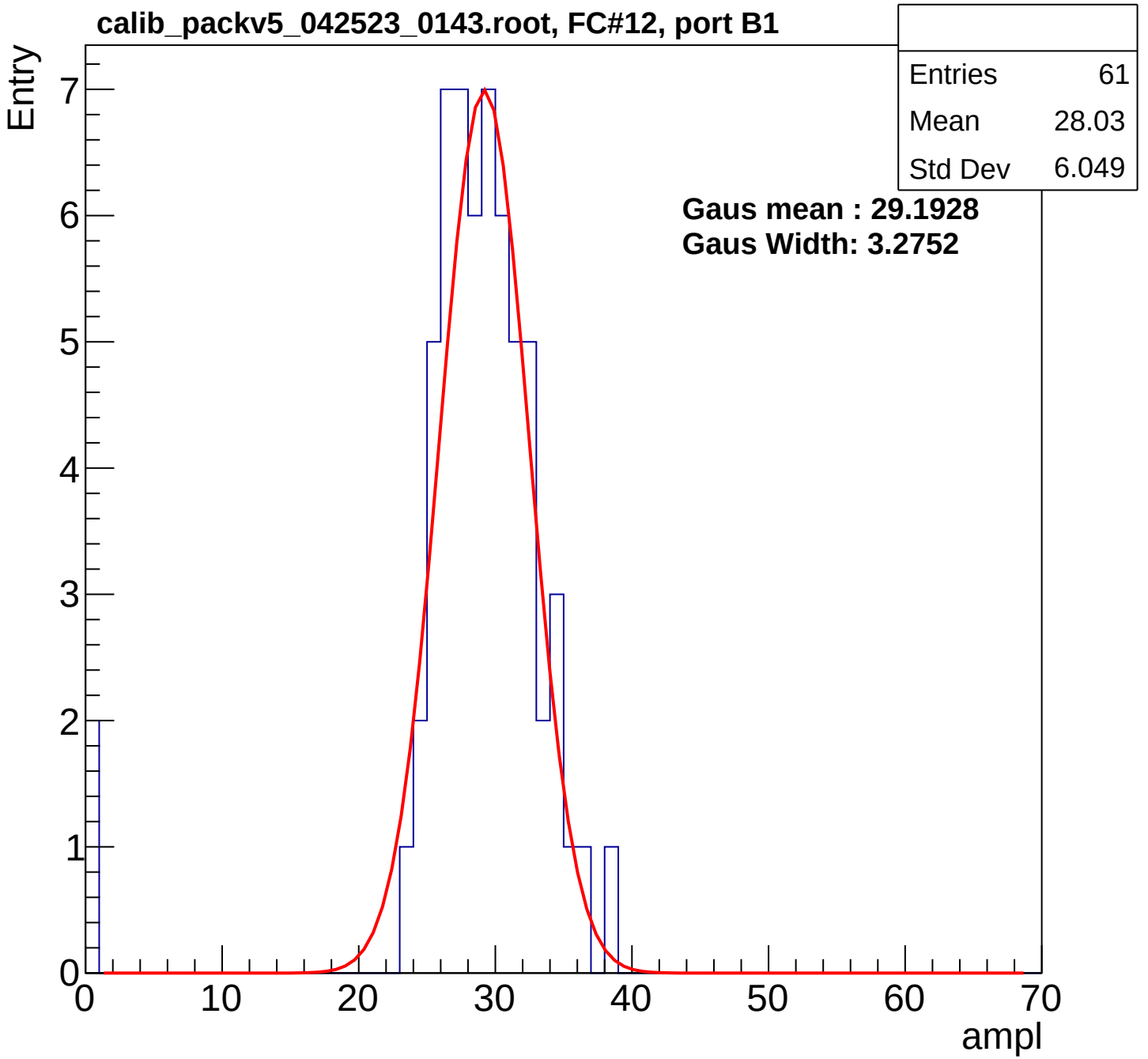
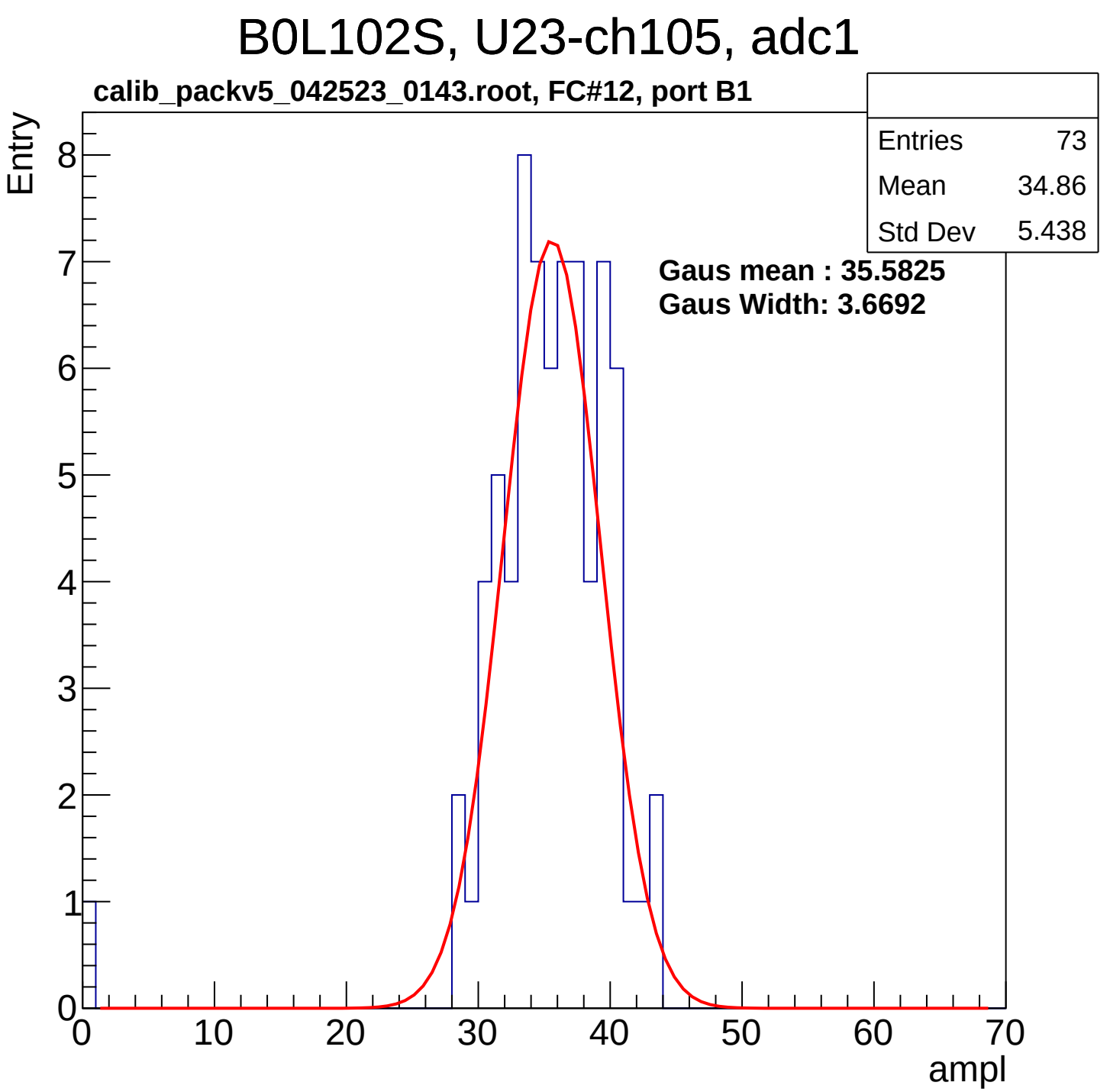
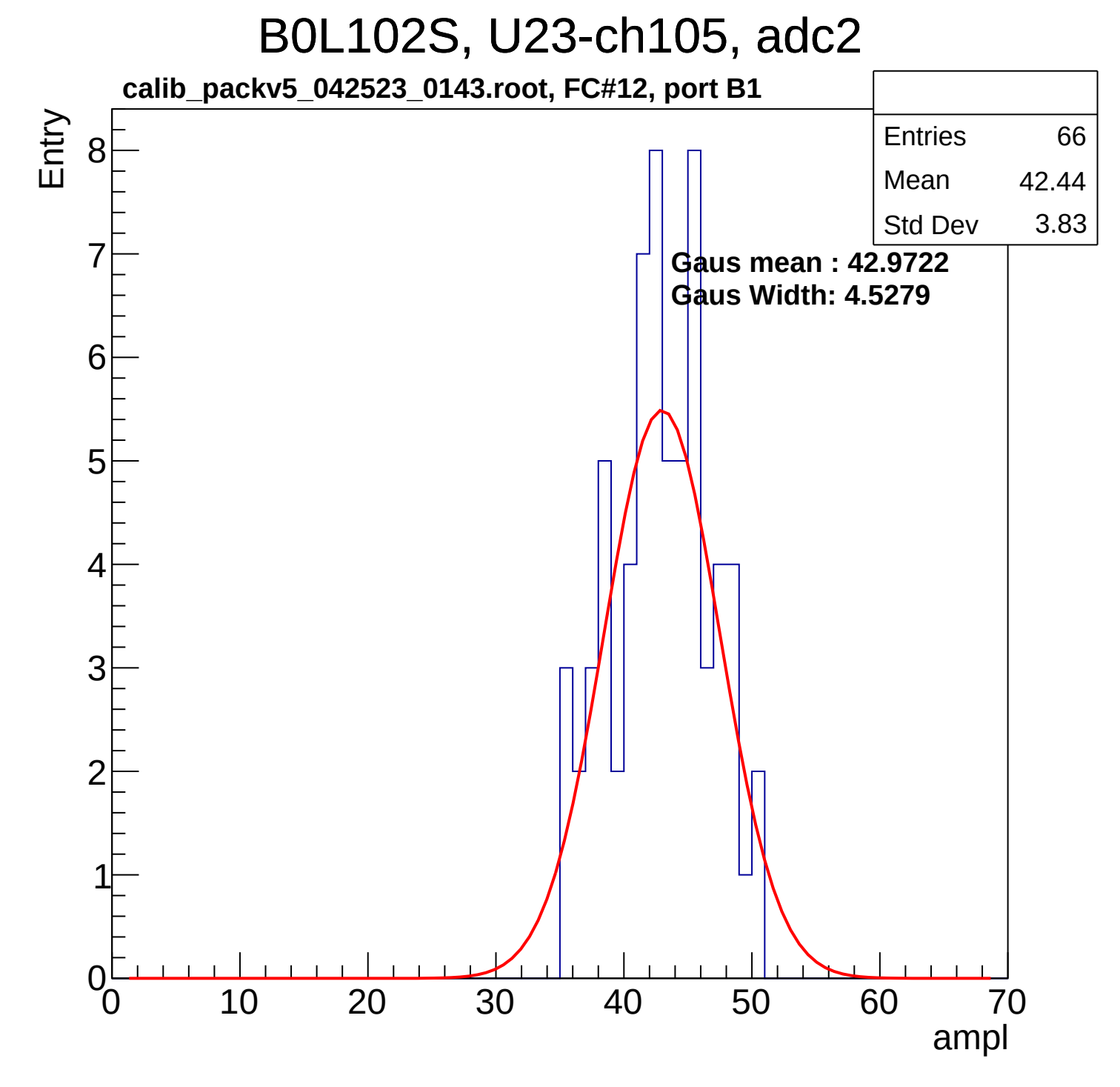

B0L102S, U23-ch104, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch105, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 28.03
Std Dev 6.049
Gaus mean : 29.1928
Gaus Width: 3.2752
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch105, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 34.86
Std Dev 5.438
Gaus mean : 35.5825
Gaus Width: 3.6692
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch105, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 42.44
Std Dev 3.83
Gaus mean : 42.9722
Gaus Width: 4.5279
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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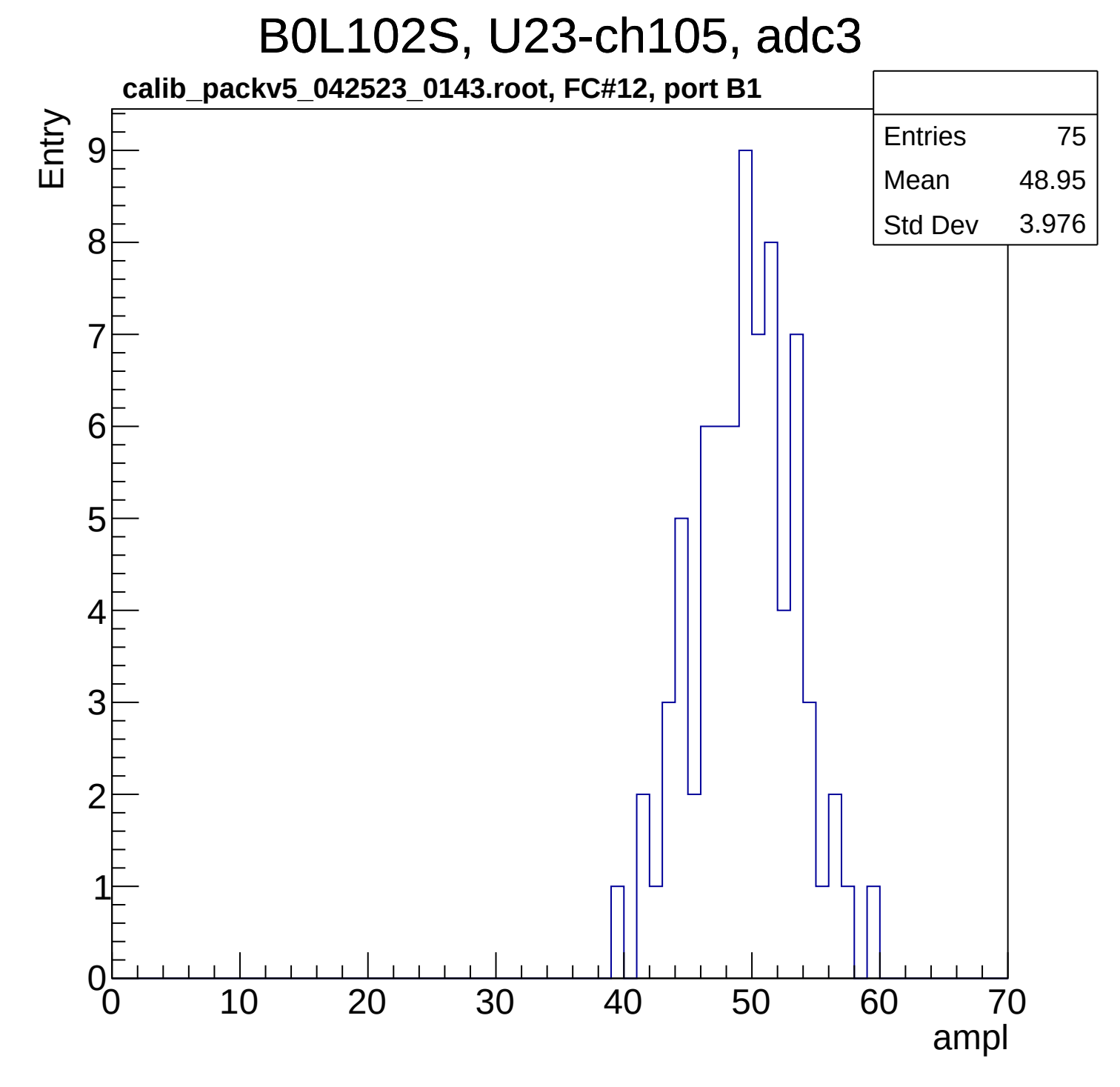
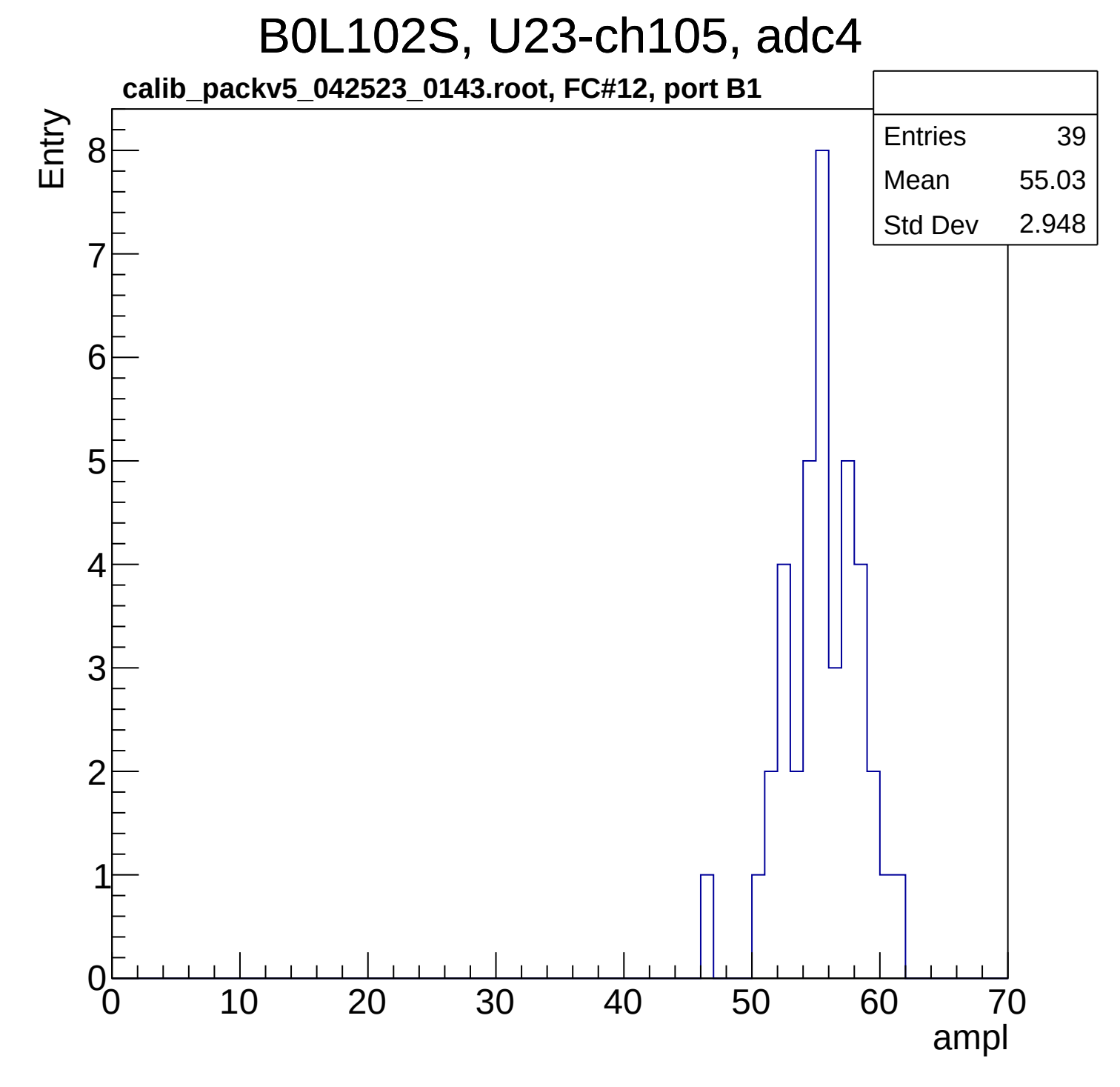
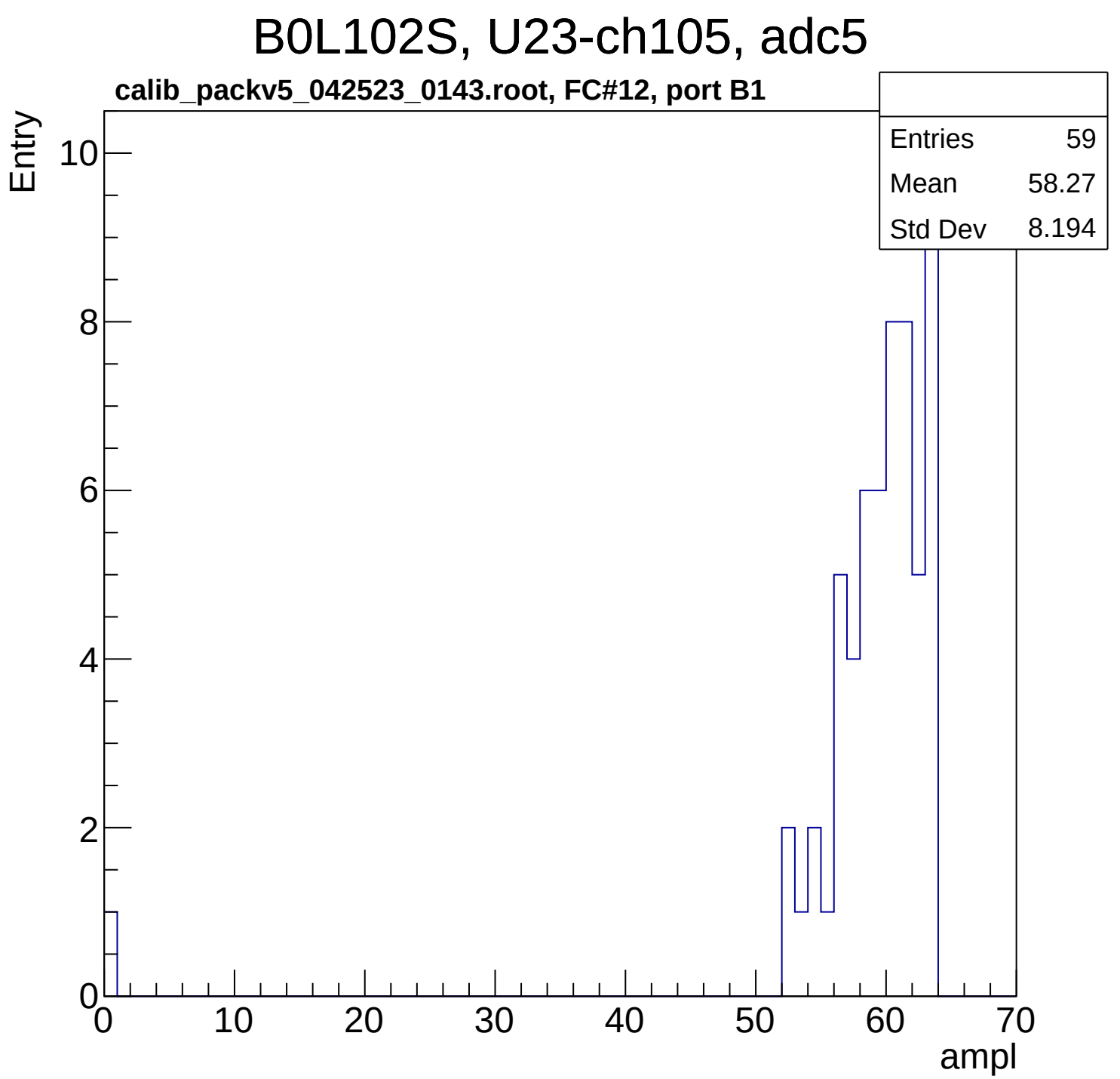
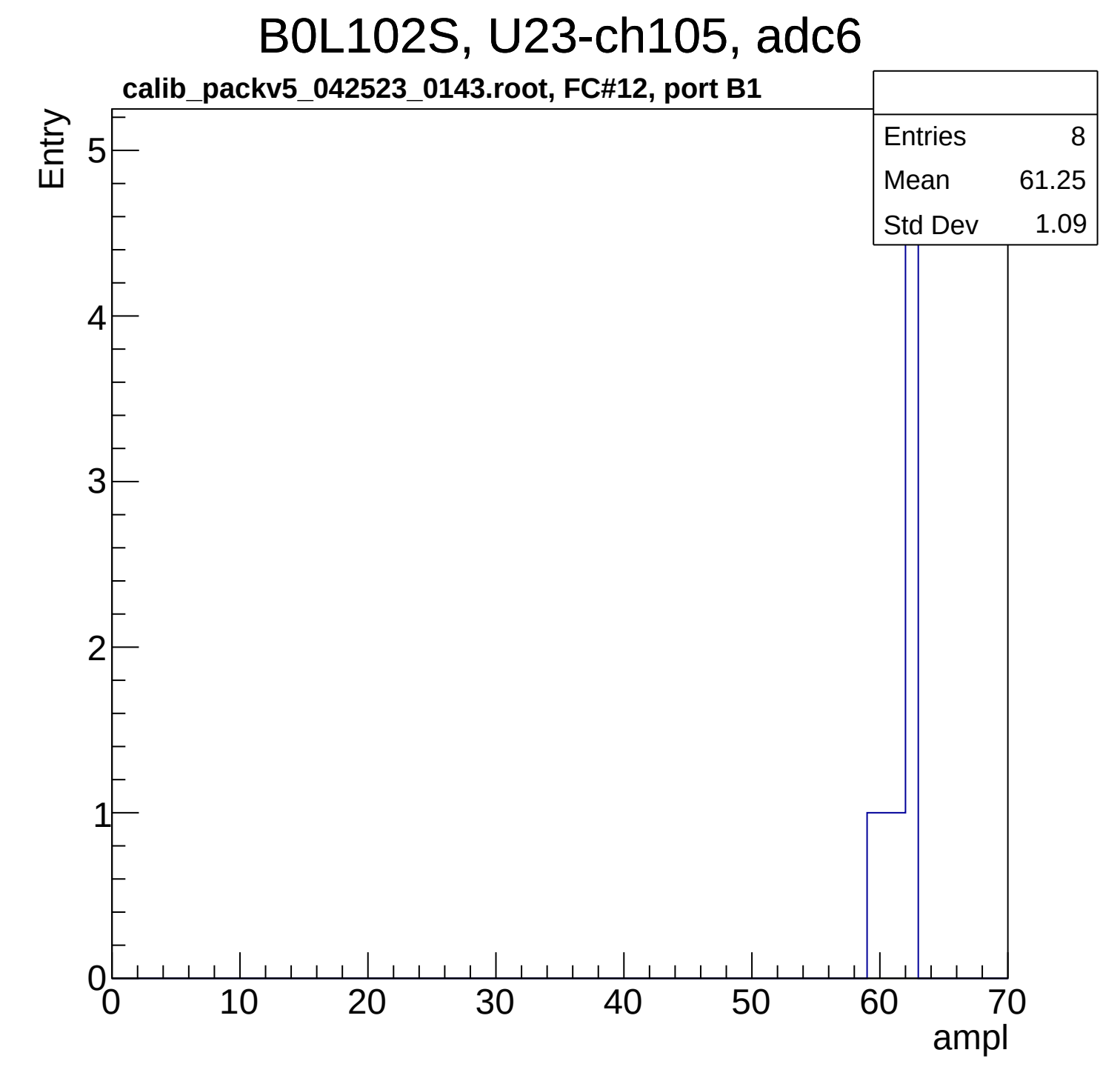
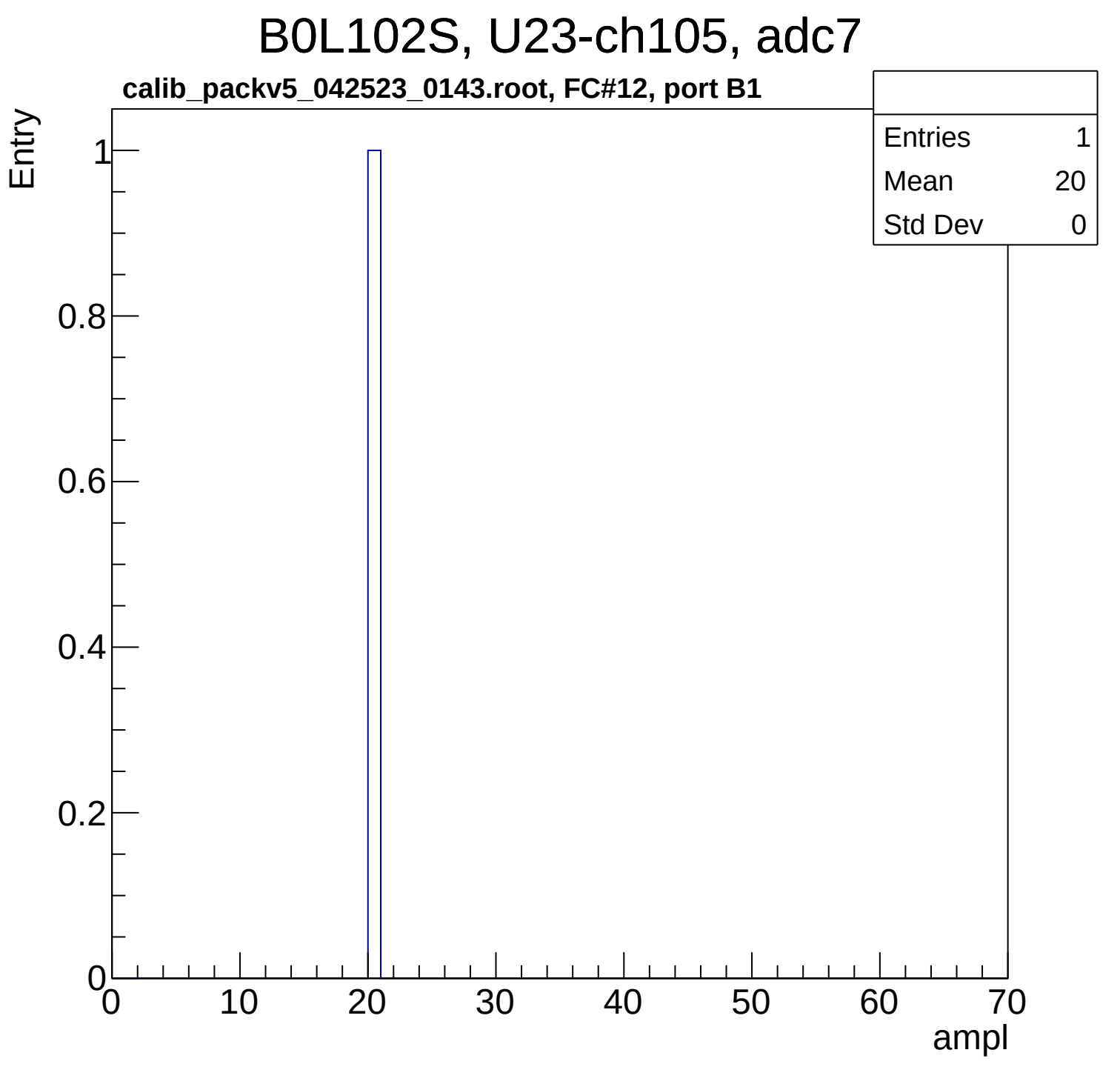
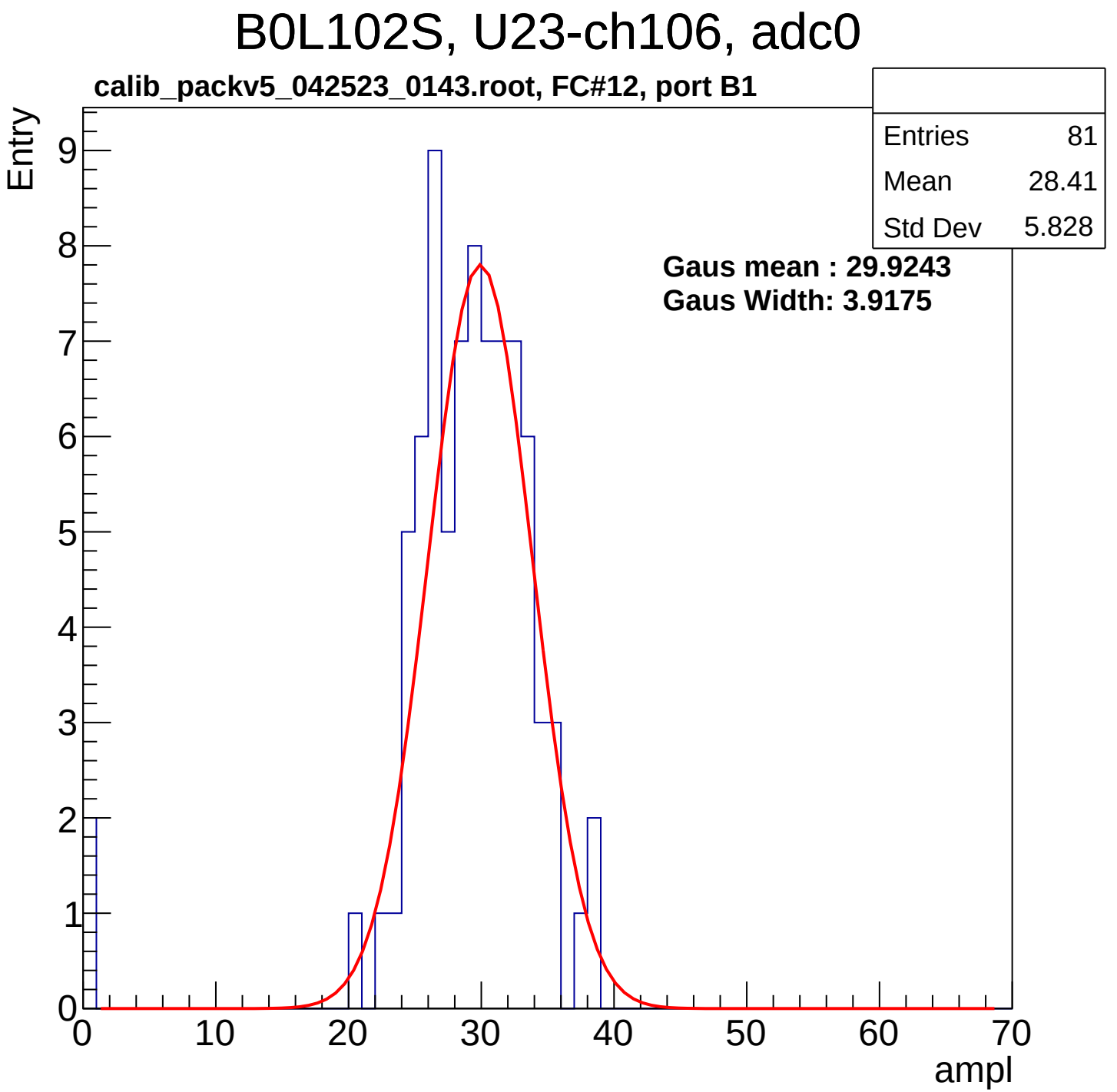

B0L102S, U23-ch105, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 48.95
Std Dev 3.976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

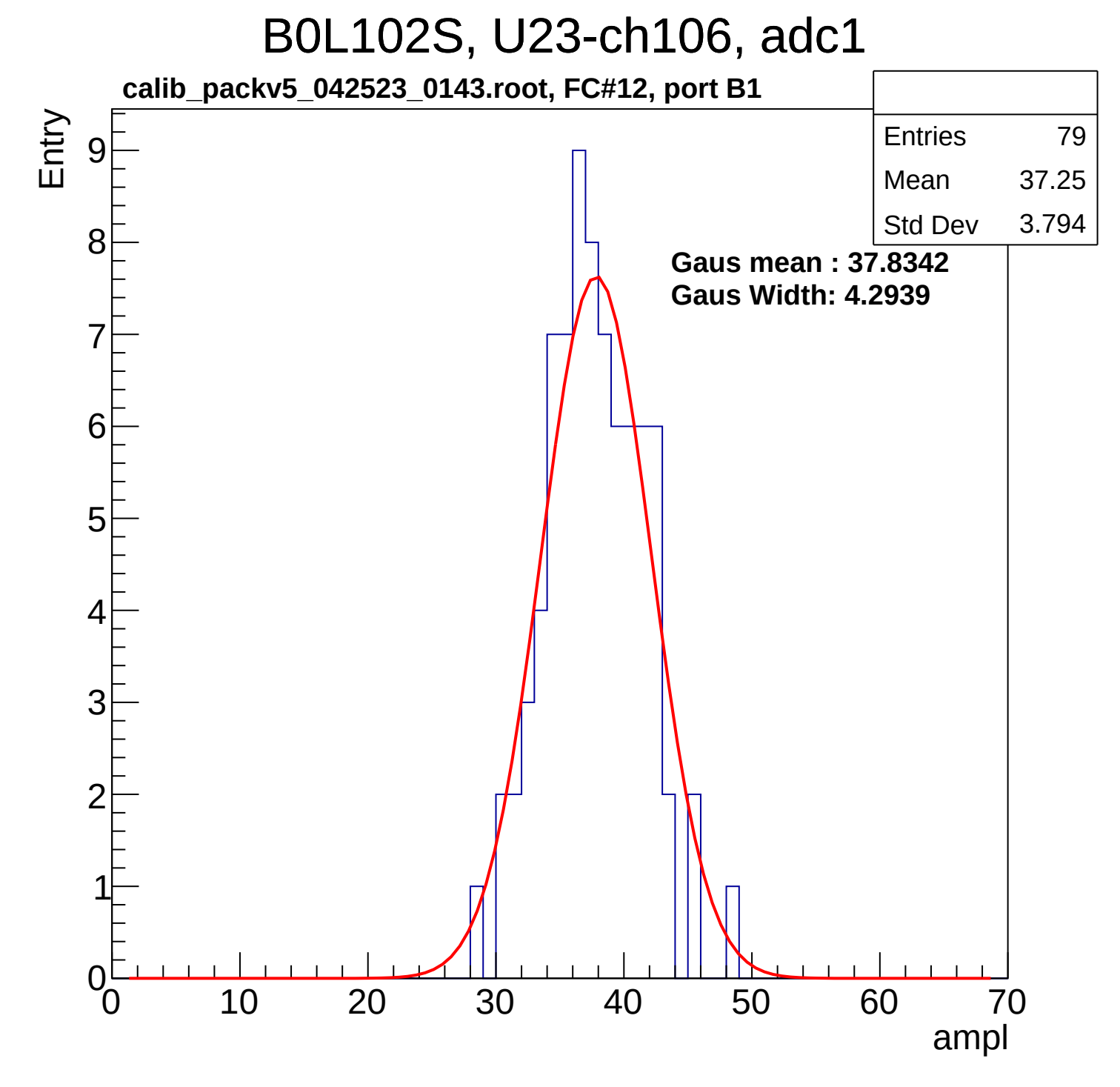

B0L102S, U23-ch105, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 39
Mean 55.03
Std Dev 2.948
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

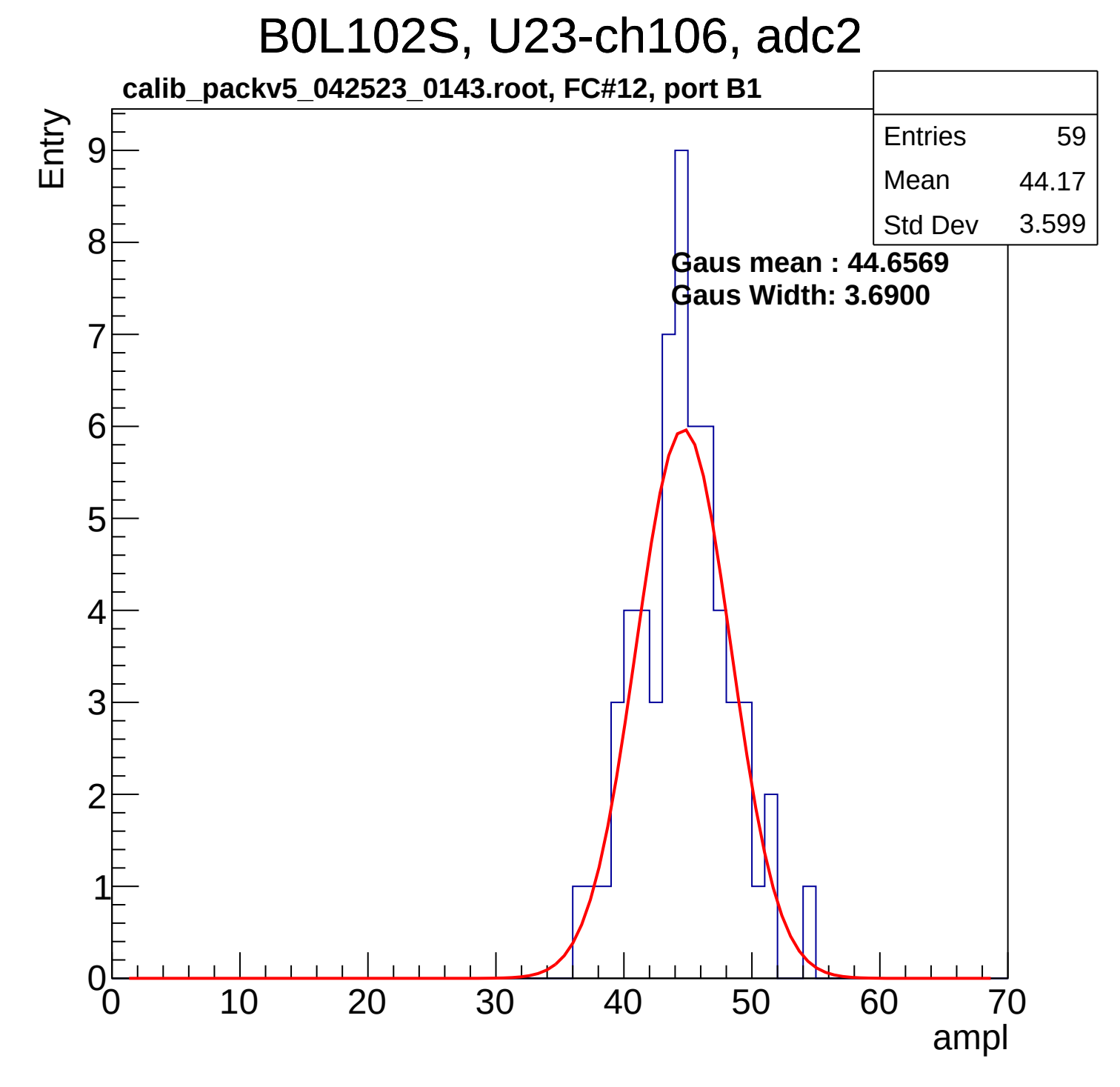

B0L102S, U23-ch105, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 58.27
Std Dev 8.194
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch105, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 8
Mean 61.25
Std Dev 1.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch105, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch106, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 28.41
Std Dev 5.828
Gaus mean : 29.9243
Gaus Width: 3.9175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch106, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 37.25
Std Dev 3.794
Gaus mean : 37.8342
Gaus Width: 4.2939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch106, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 44.17
Std Dev 3.599
Gaus mean : 44.6569
Gaus Width: 3.6900
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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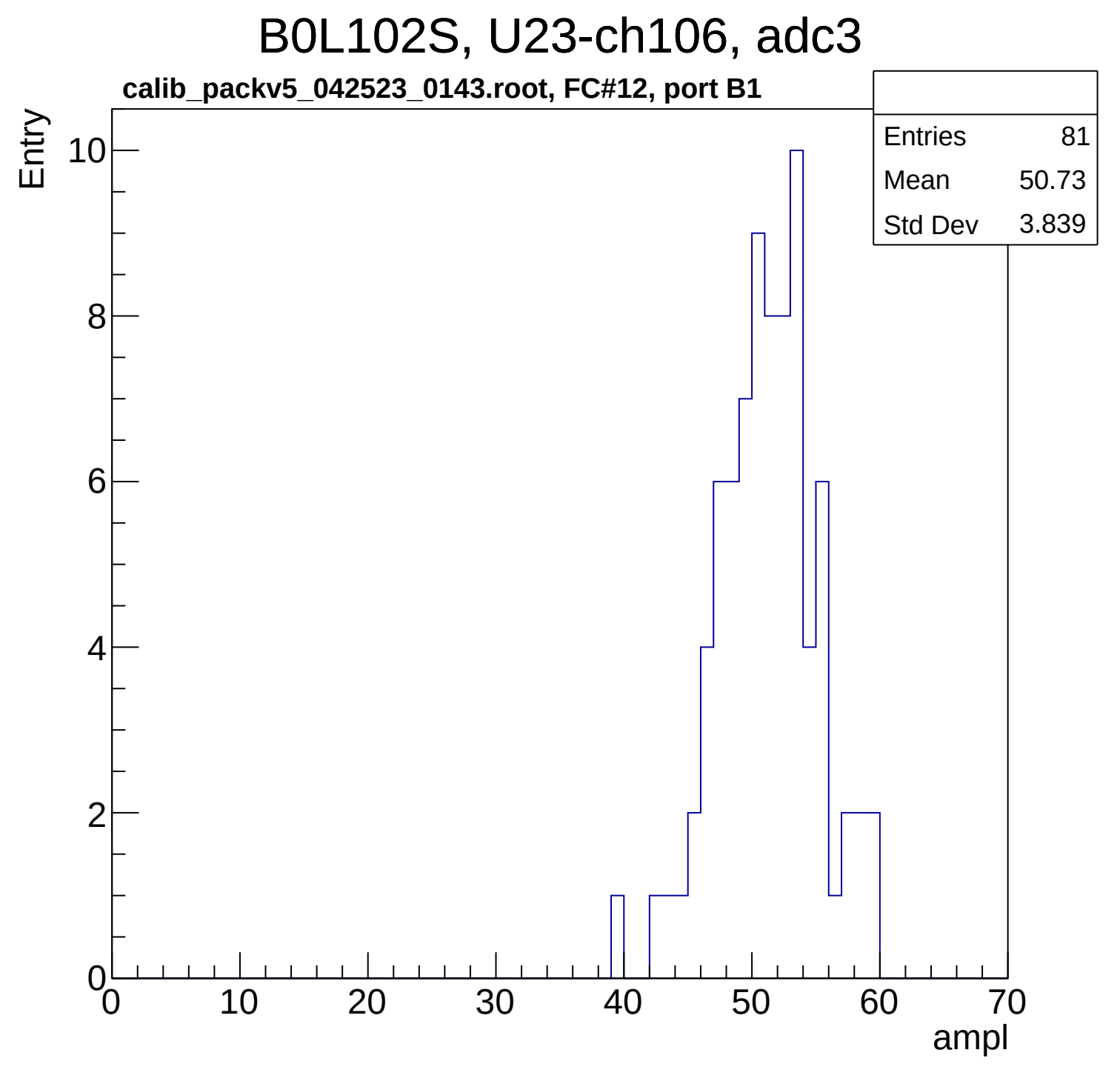
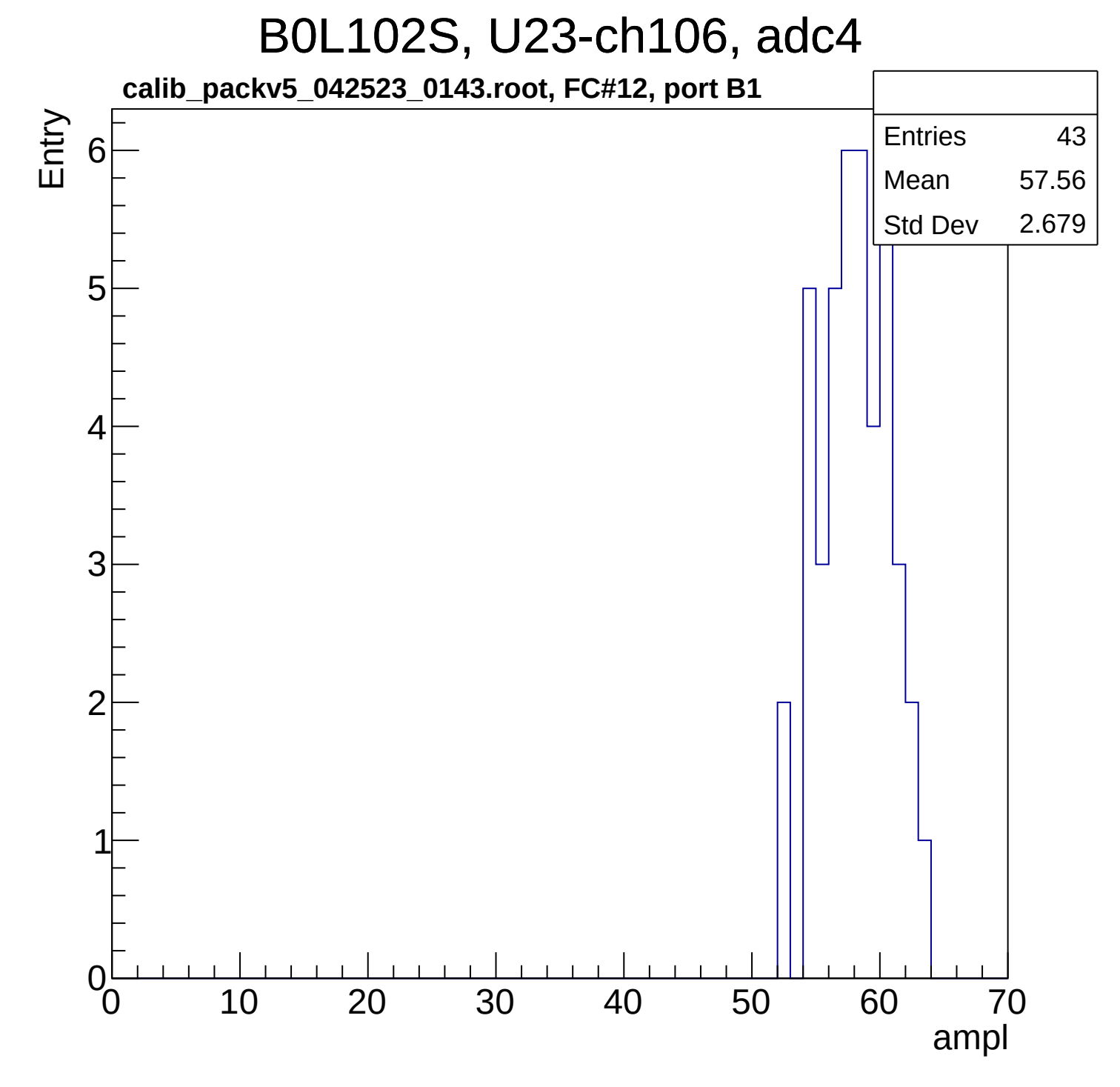

B0L102S, U23-ch106, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 50.73
Std Dev 3.839
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch106, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 43
Mean 57.56
Std Dev 2.679
Entry
ampl
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1
2
3
4
5
6
0
10
20
30
40
50
60
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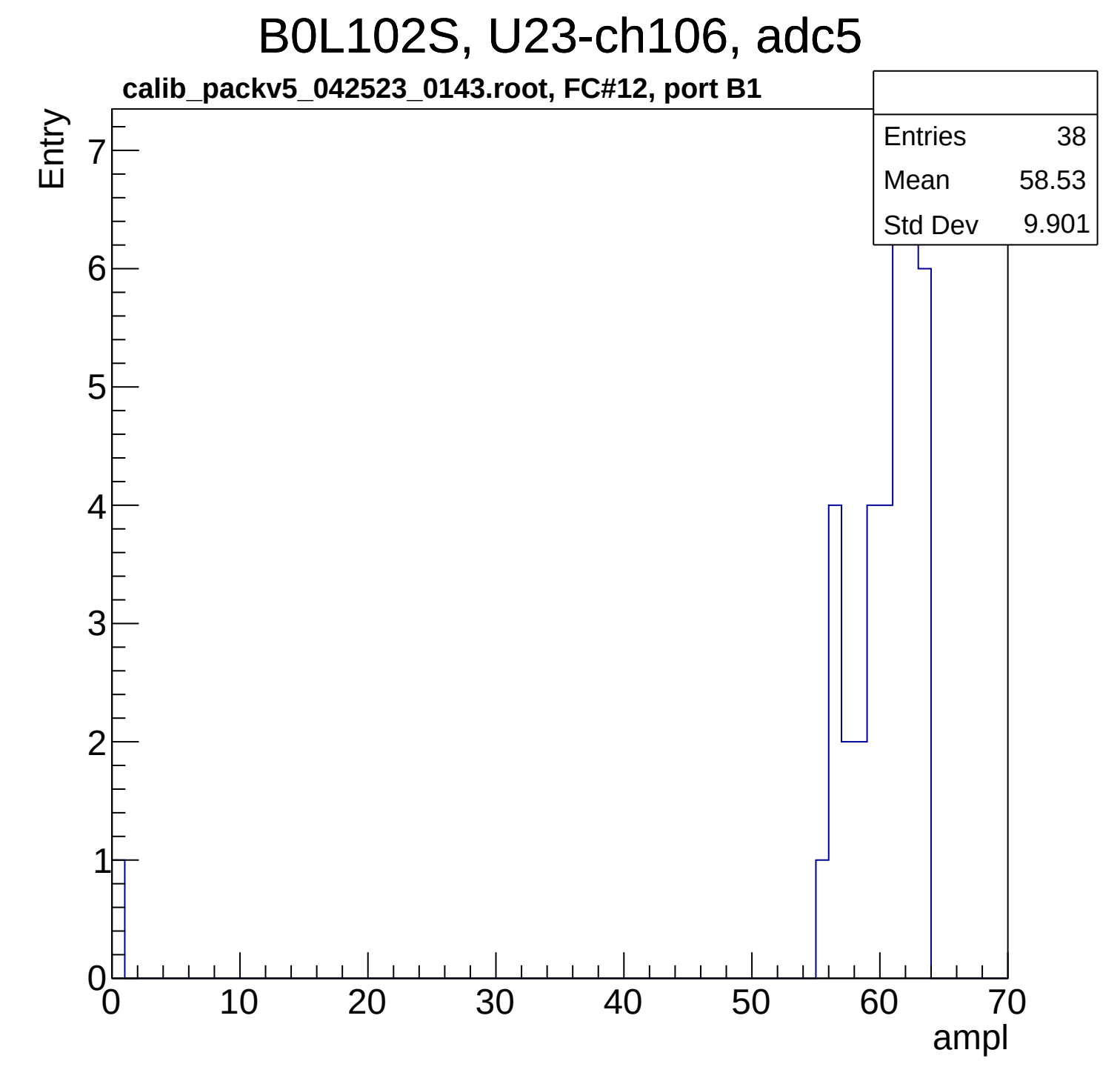
B0L102S, U23-ch106, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 38
Mean 58.53
Std Dev 9.901
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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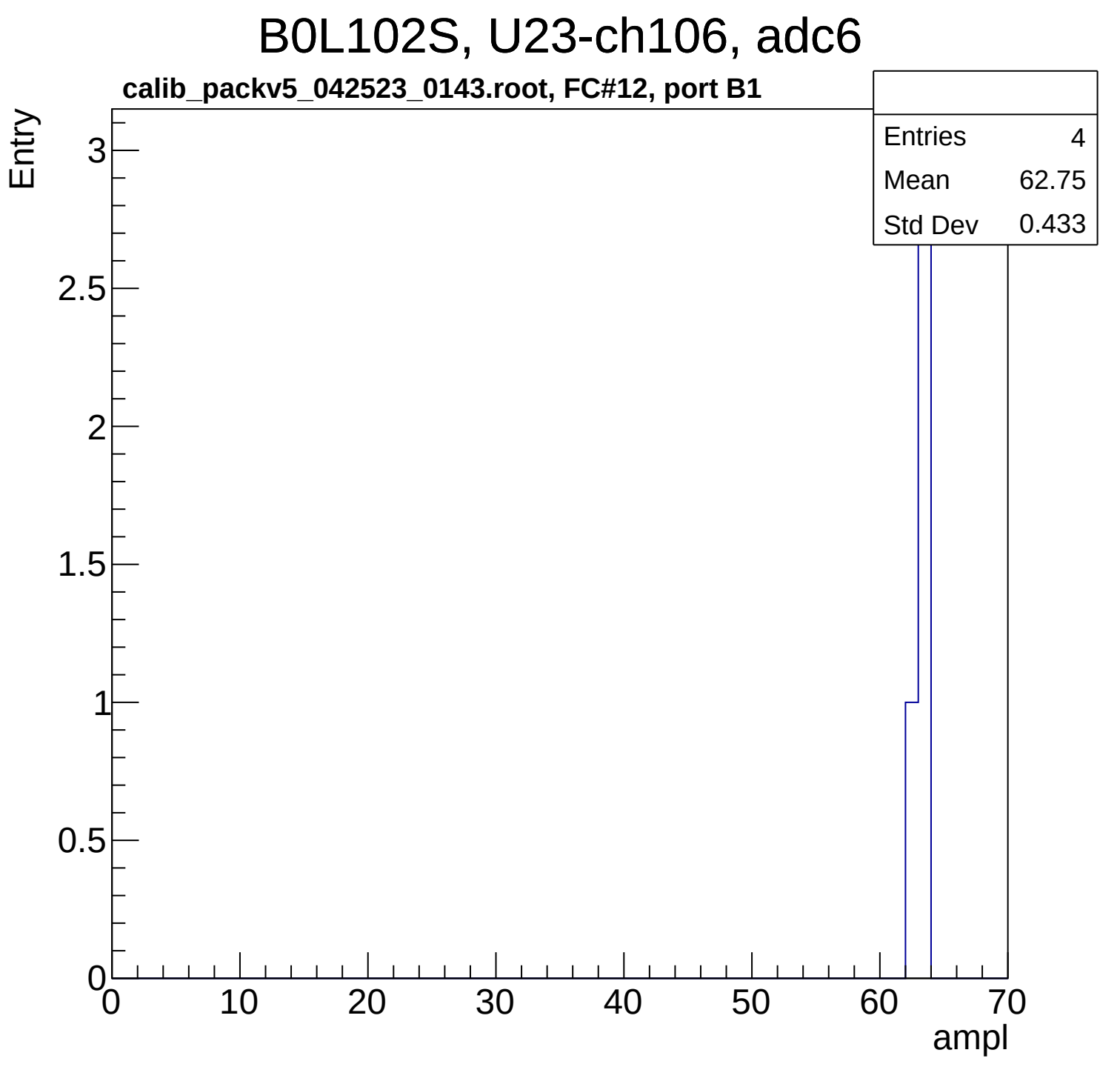
B0L102S, U23-ch106, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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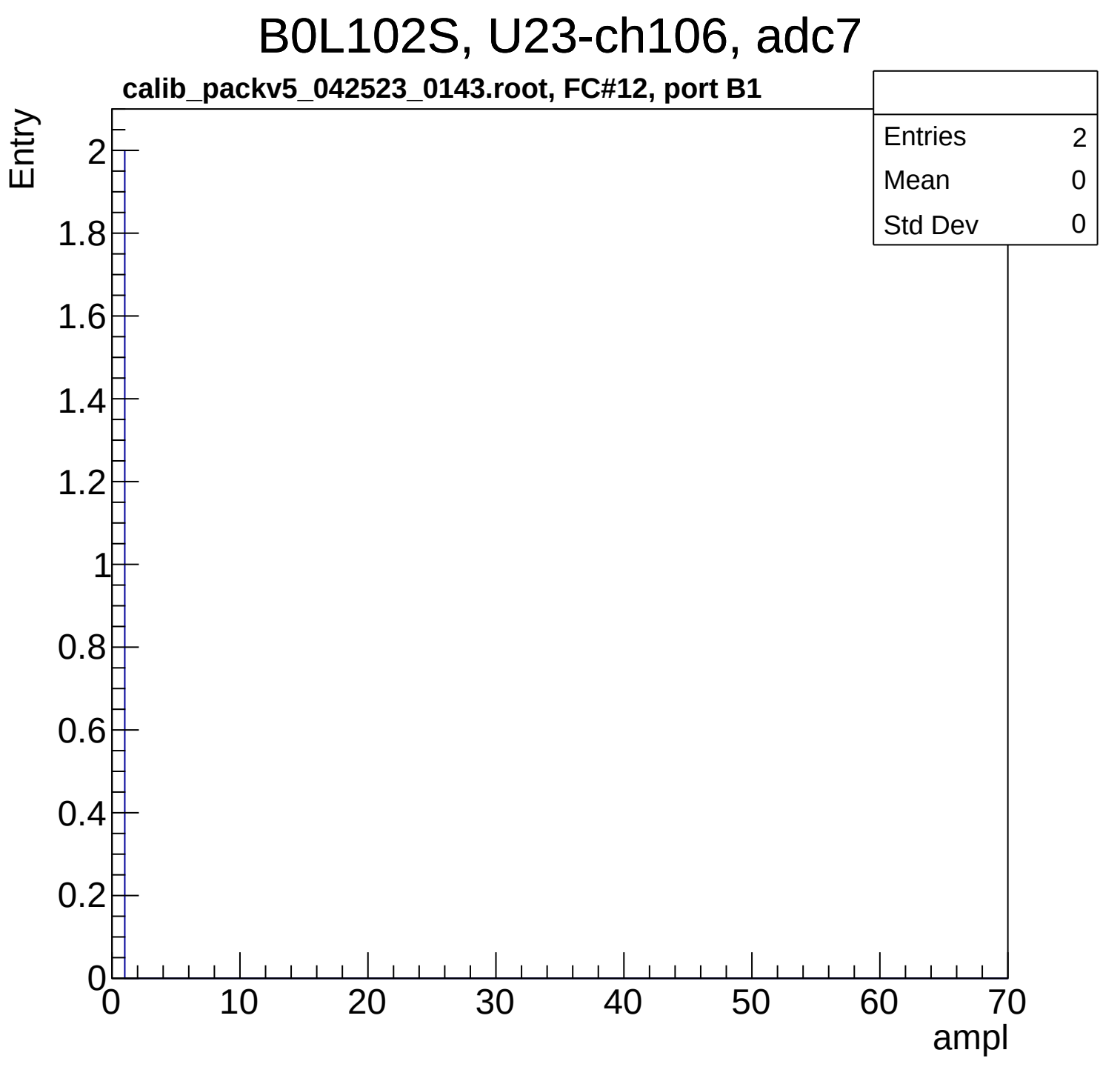
B0L102S, U23-ch106, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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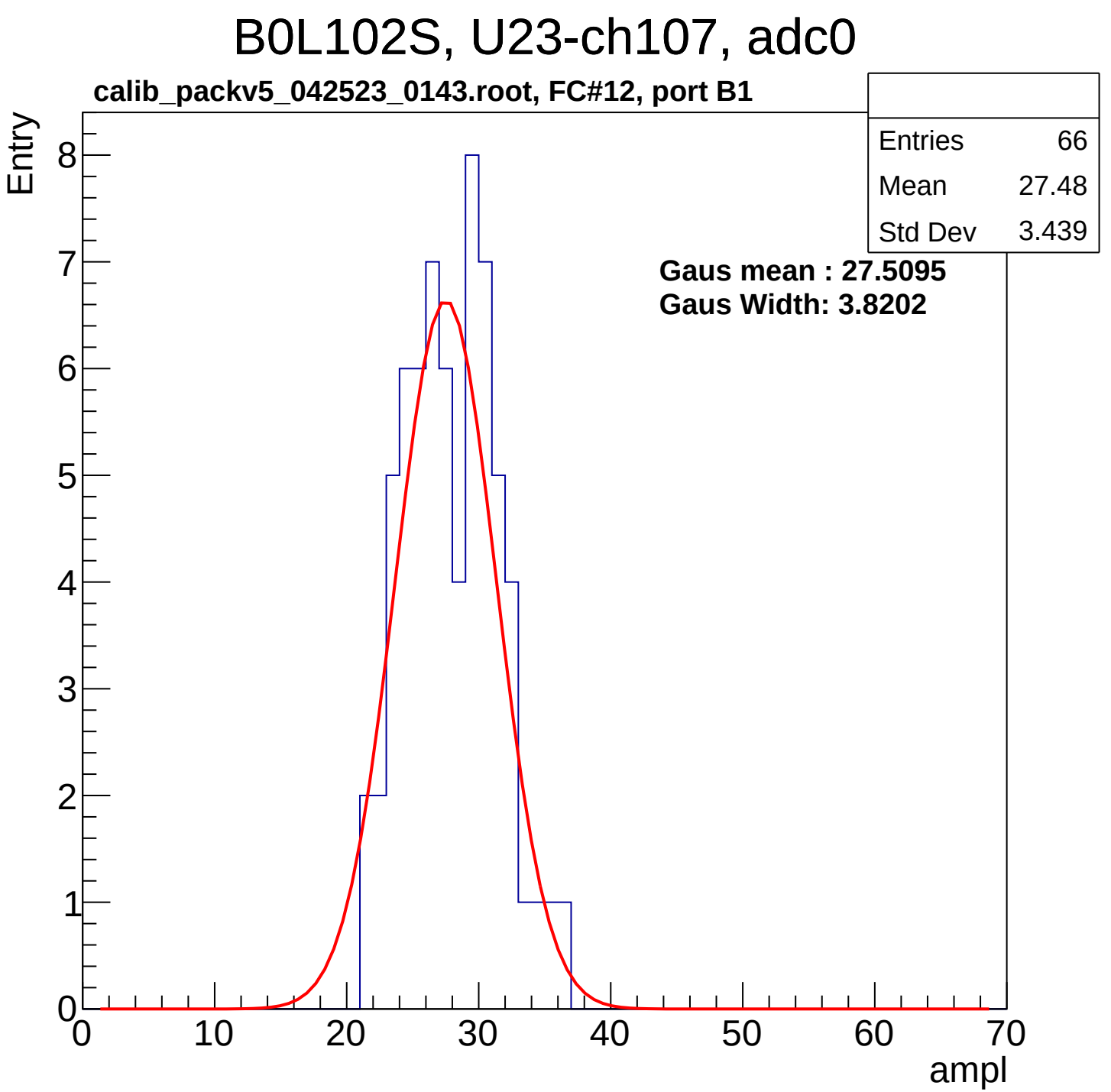
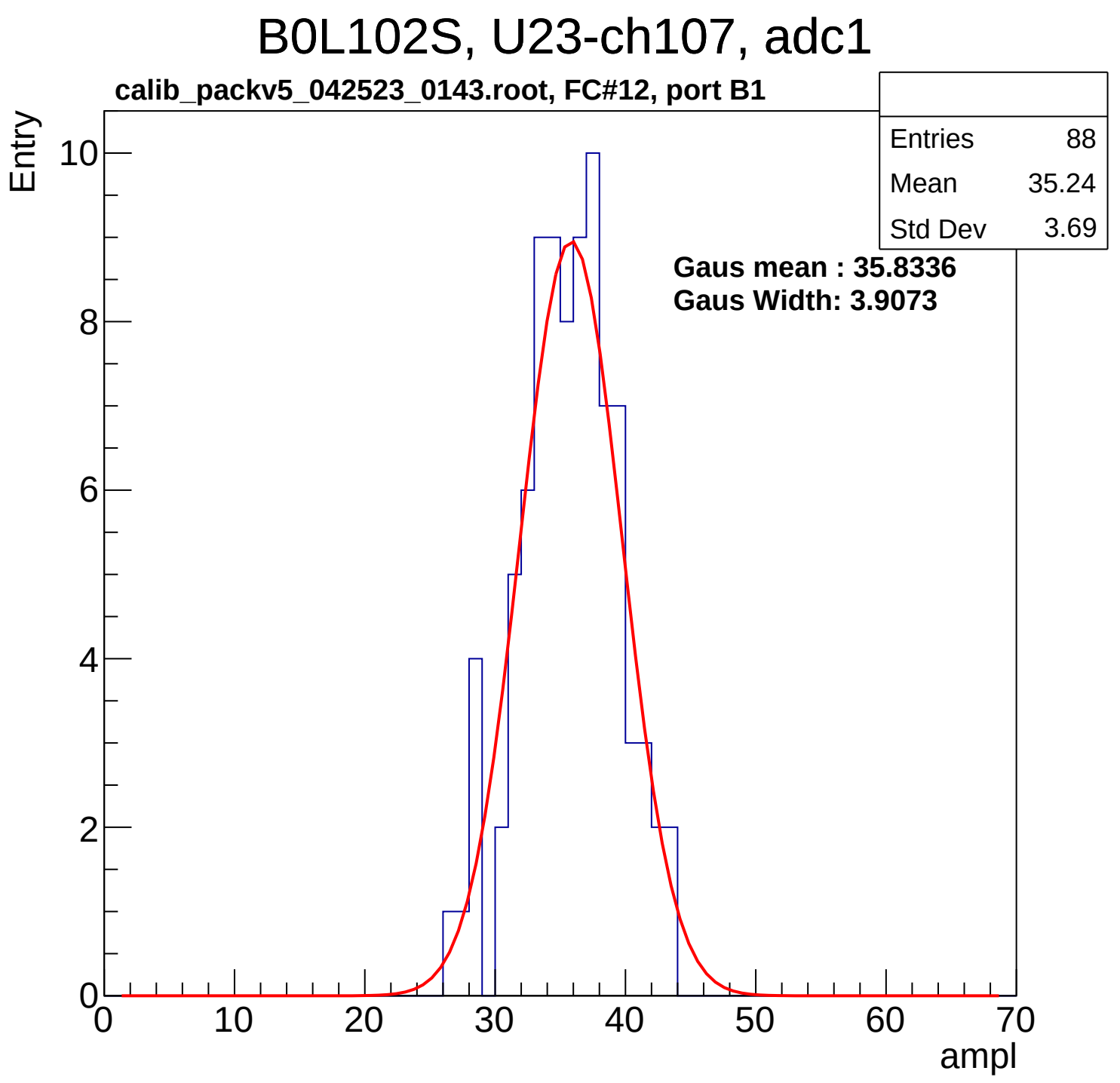
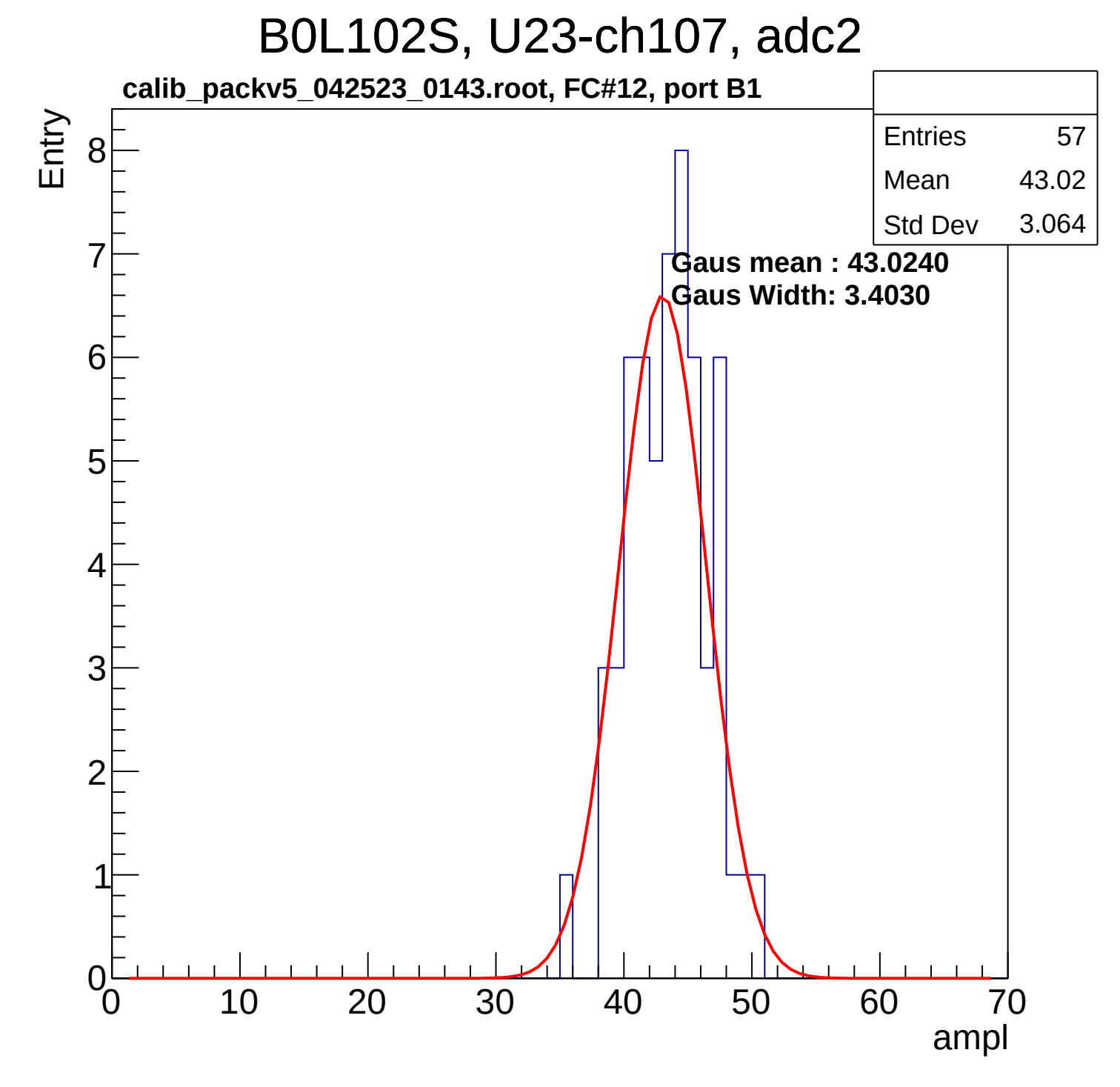

B0L102S, U23-ch107, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 27.48
Std Dev 3.439
Gaus mean : 27.5095
Gaus Width: 3.8202
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch107, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 35.24
Std Dev 3.69
Gaus mean : 35.8336
Gaus Width: 3.9073
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch107, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.02
Std Dev 3.064
Gaus mean : 43.0240
Gaus Width: 3.4030
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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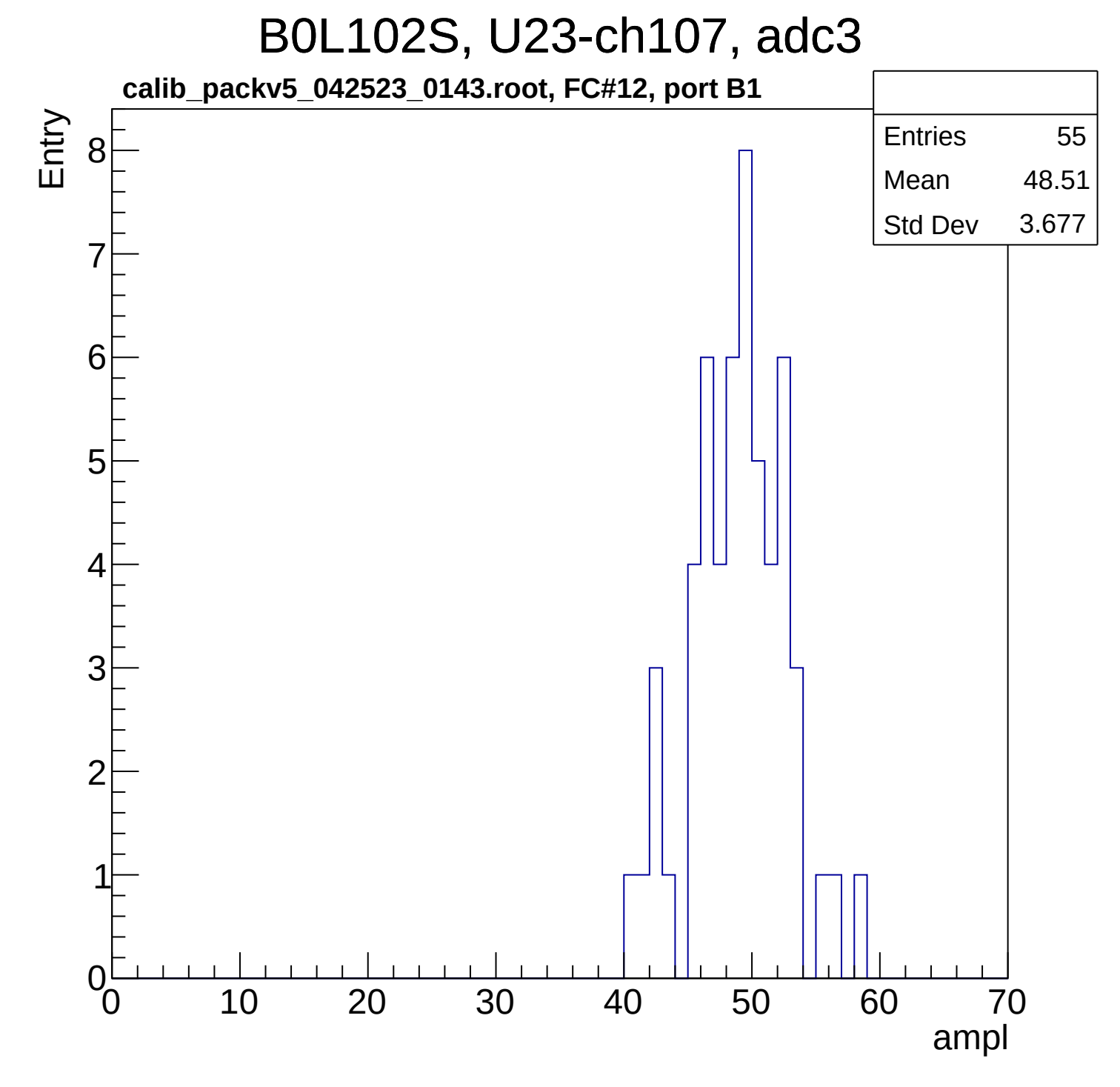
40
50
60
70


B0L102S, U23-ch107, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 48.51
Std Dev 3.677
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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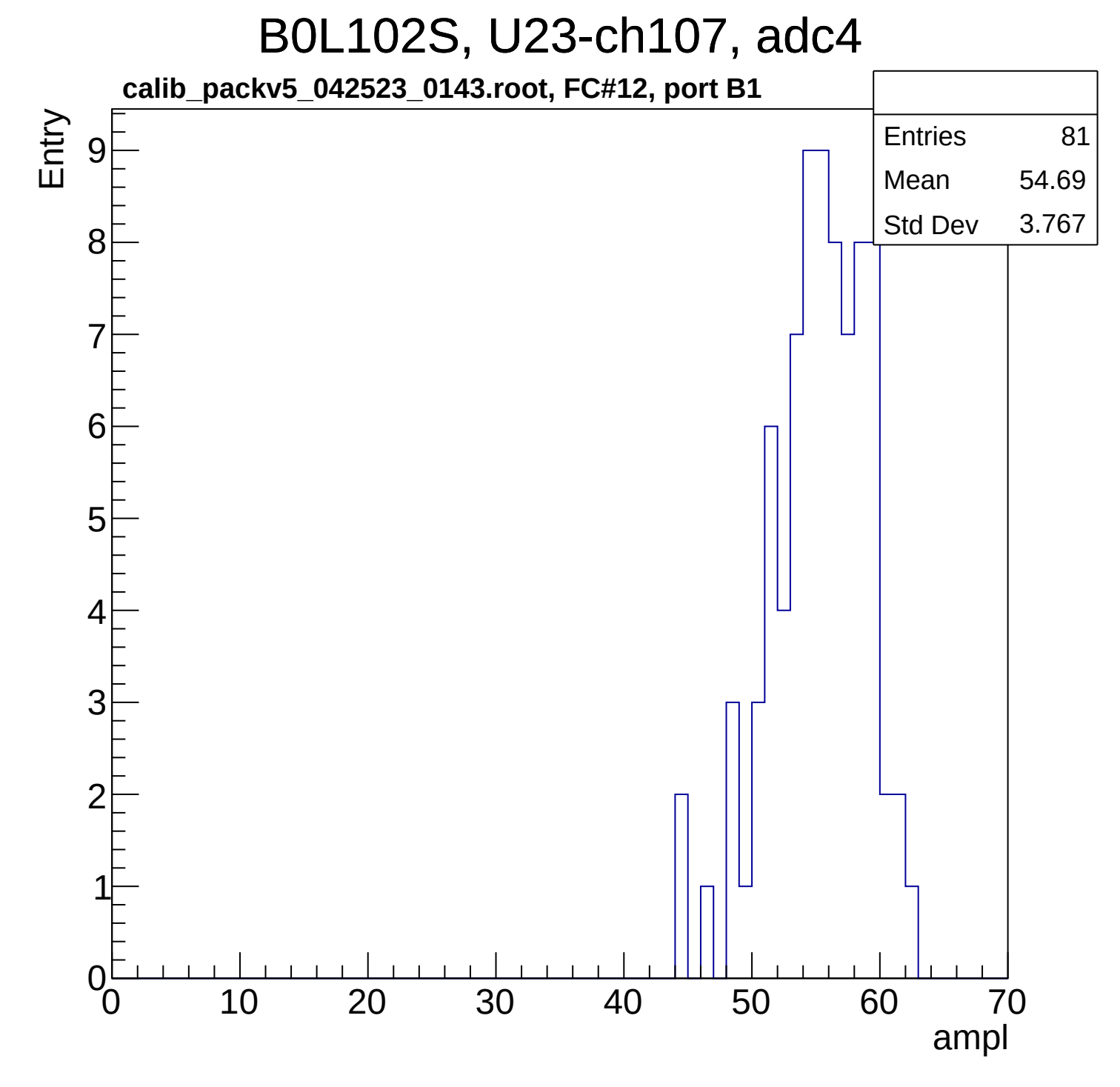
40
50
60
70


B0L102S, U23-ch107, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 54.69
Std Dev 3.767
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
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60
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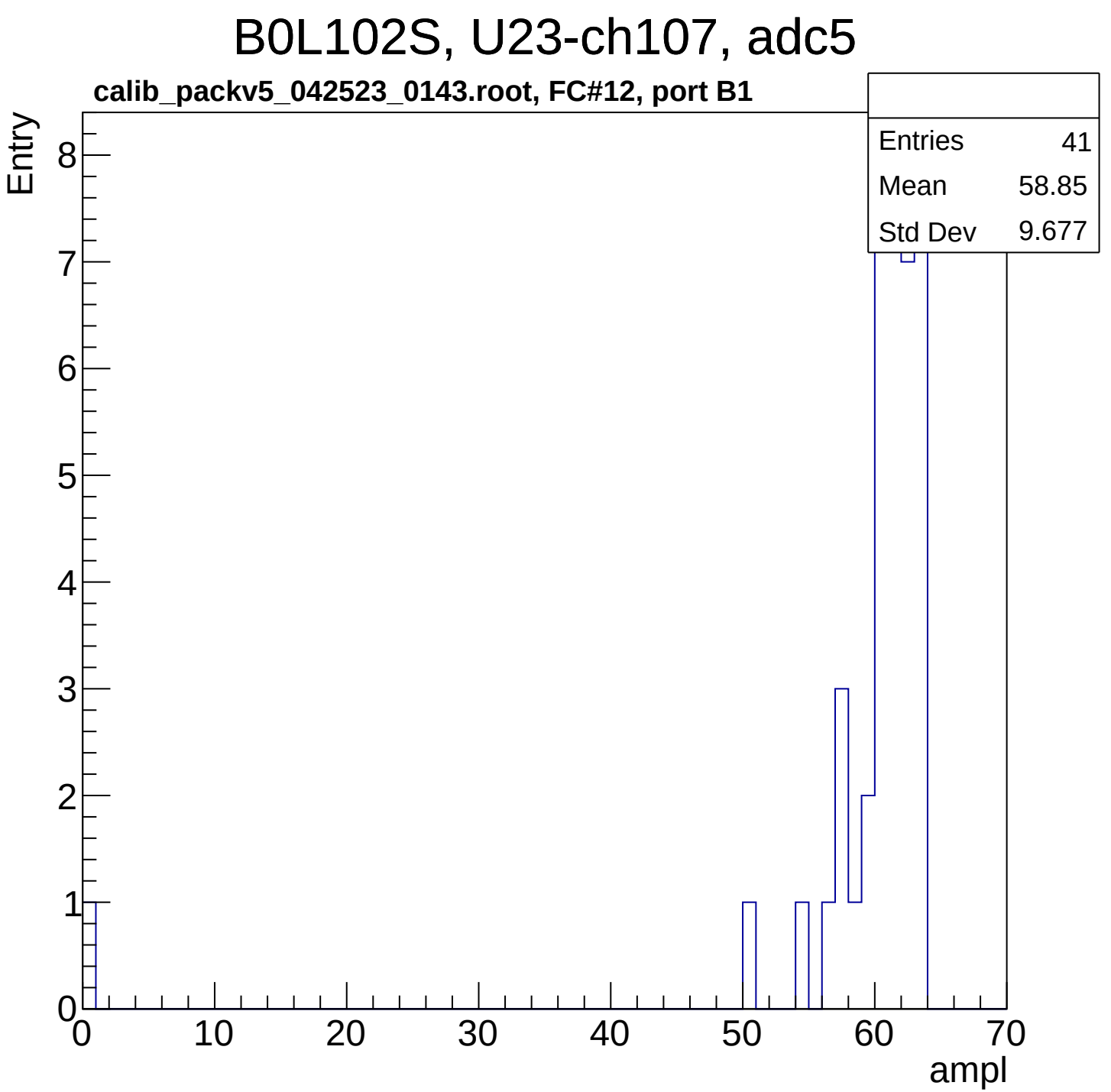
B0L102S, U23-ch107, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.85
Std Dev 9.677
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
50
60
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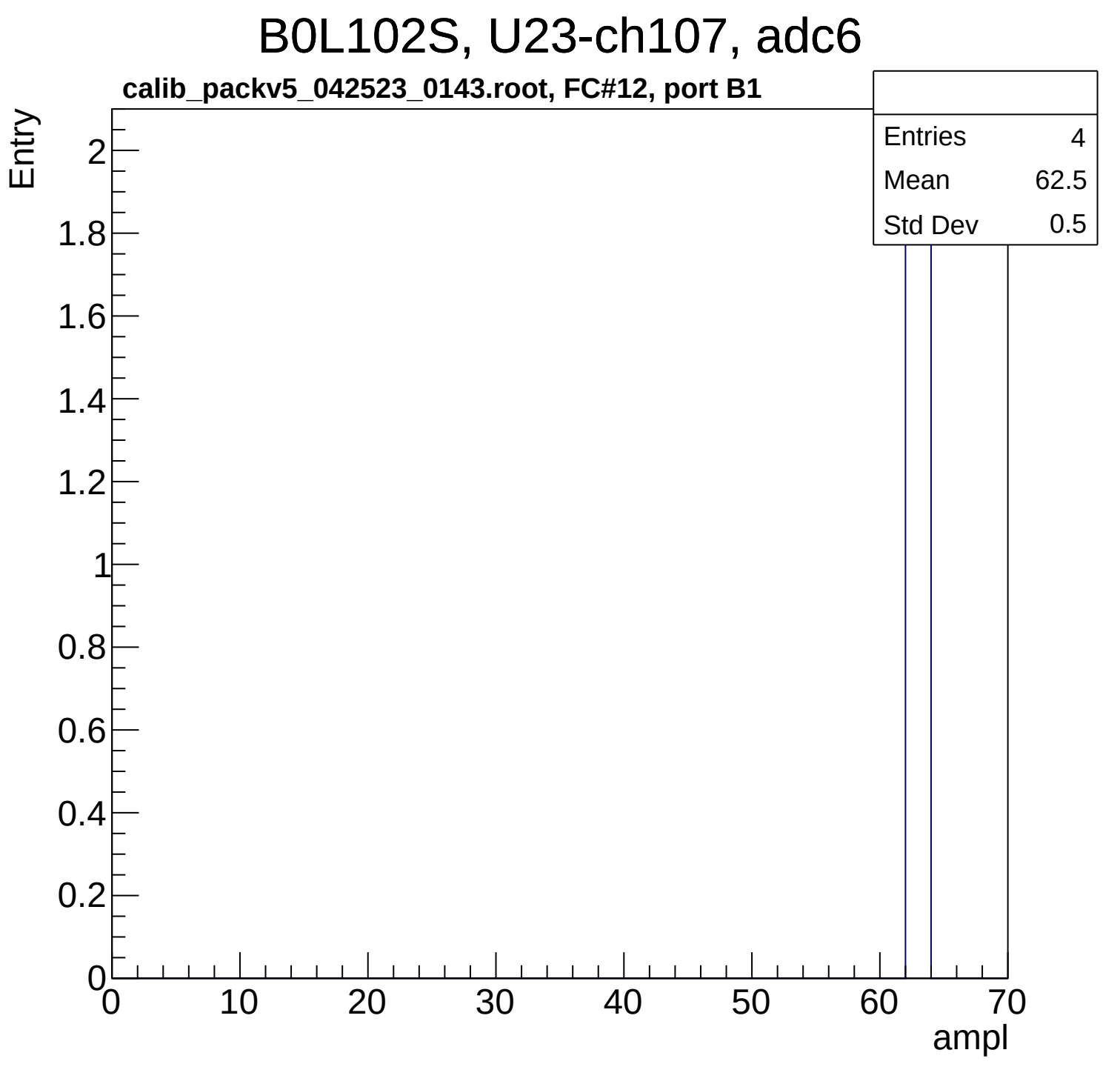
B0L102S, U23-ch107, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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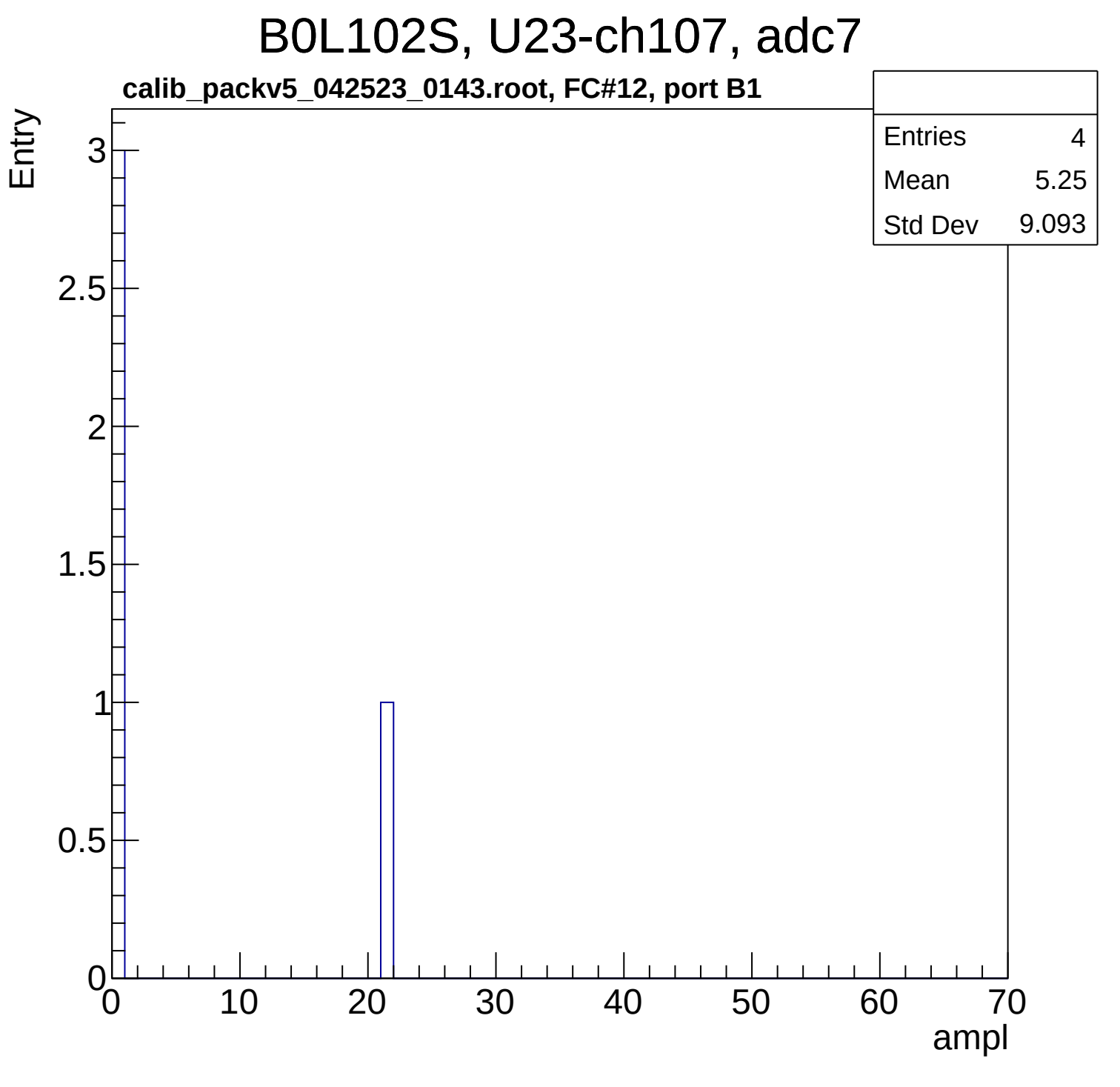
B0L102S, U23-ch107, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 5.25
Std Dev 9.093
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
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60
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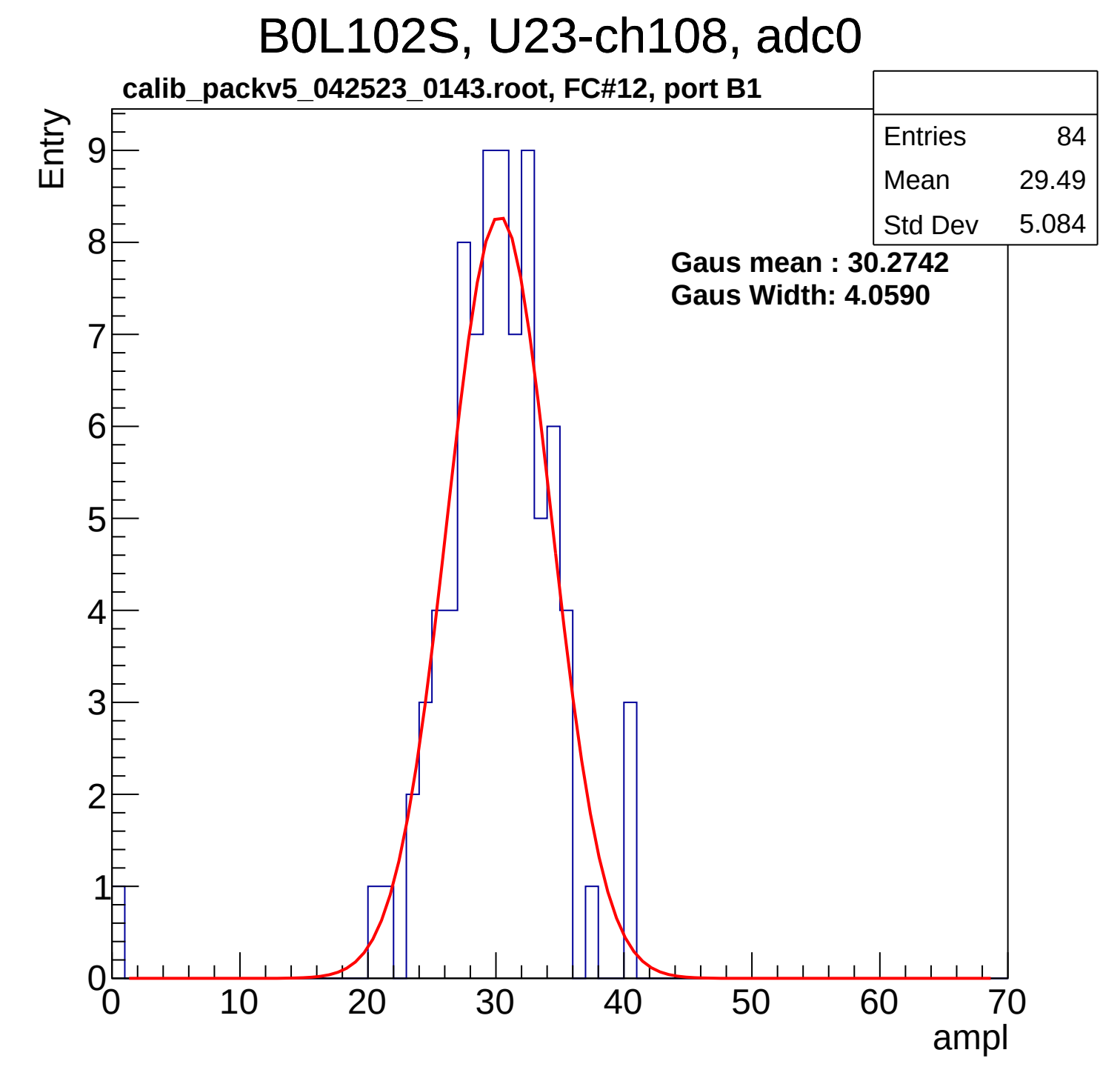
B0L102S, U23-ch108, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 84
Mean 29.49
Std Dev 5.084
Gaus mean : 30.2742
Gaus Width: 4.0590
Entry
ampl
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10
20
30
40
50
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1
2
3
4
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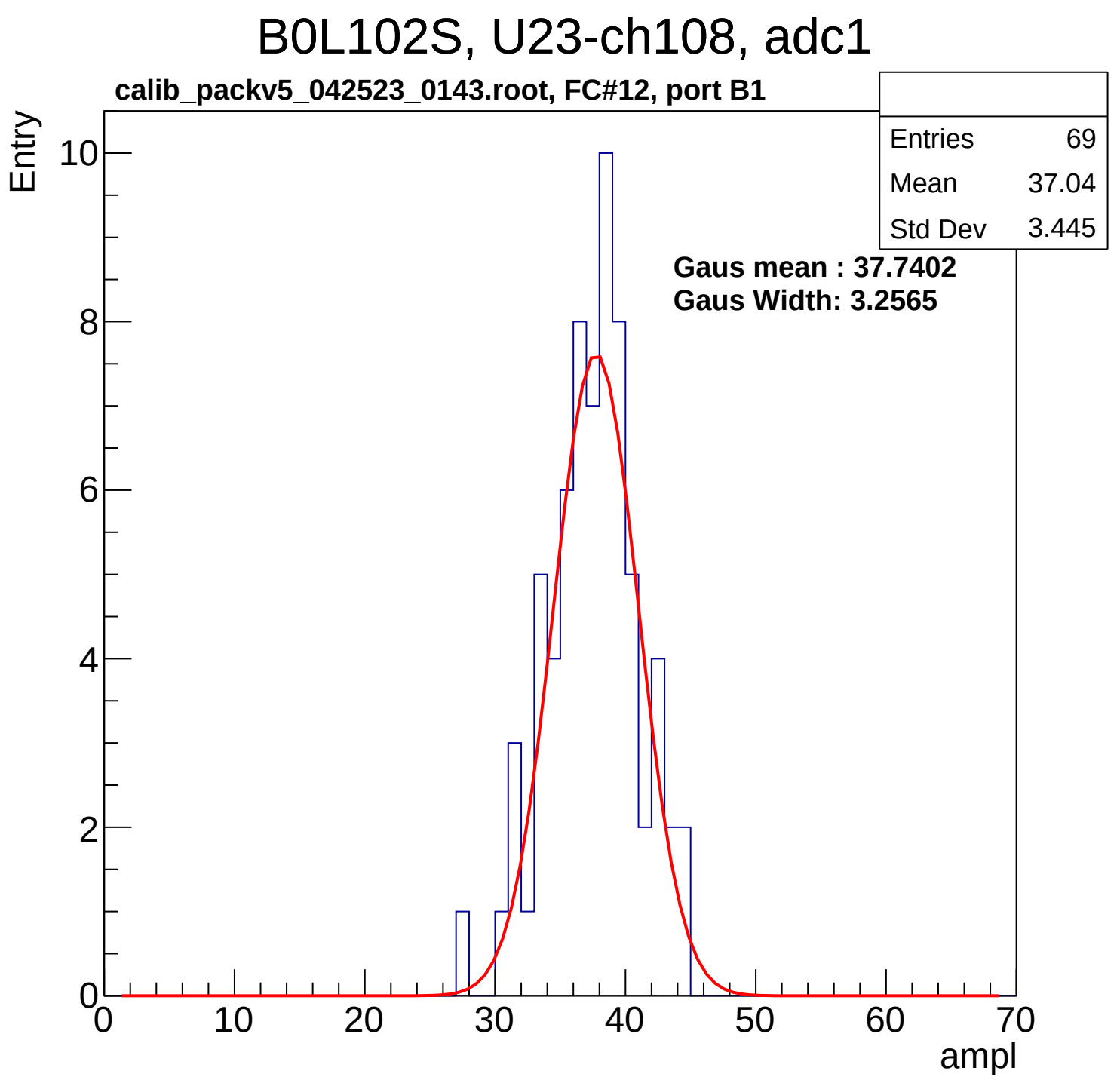
6
7
8
9


B0L102S, U23-ch108, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 37.04
Std Dev 3.445
Gaus mean : 37.7402
Gaus Width: 3.2565
Entry
ampl
0
2
4
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8
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0
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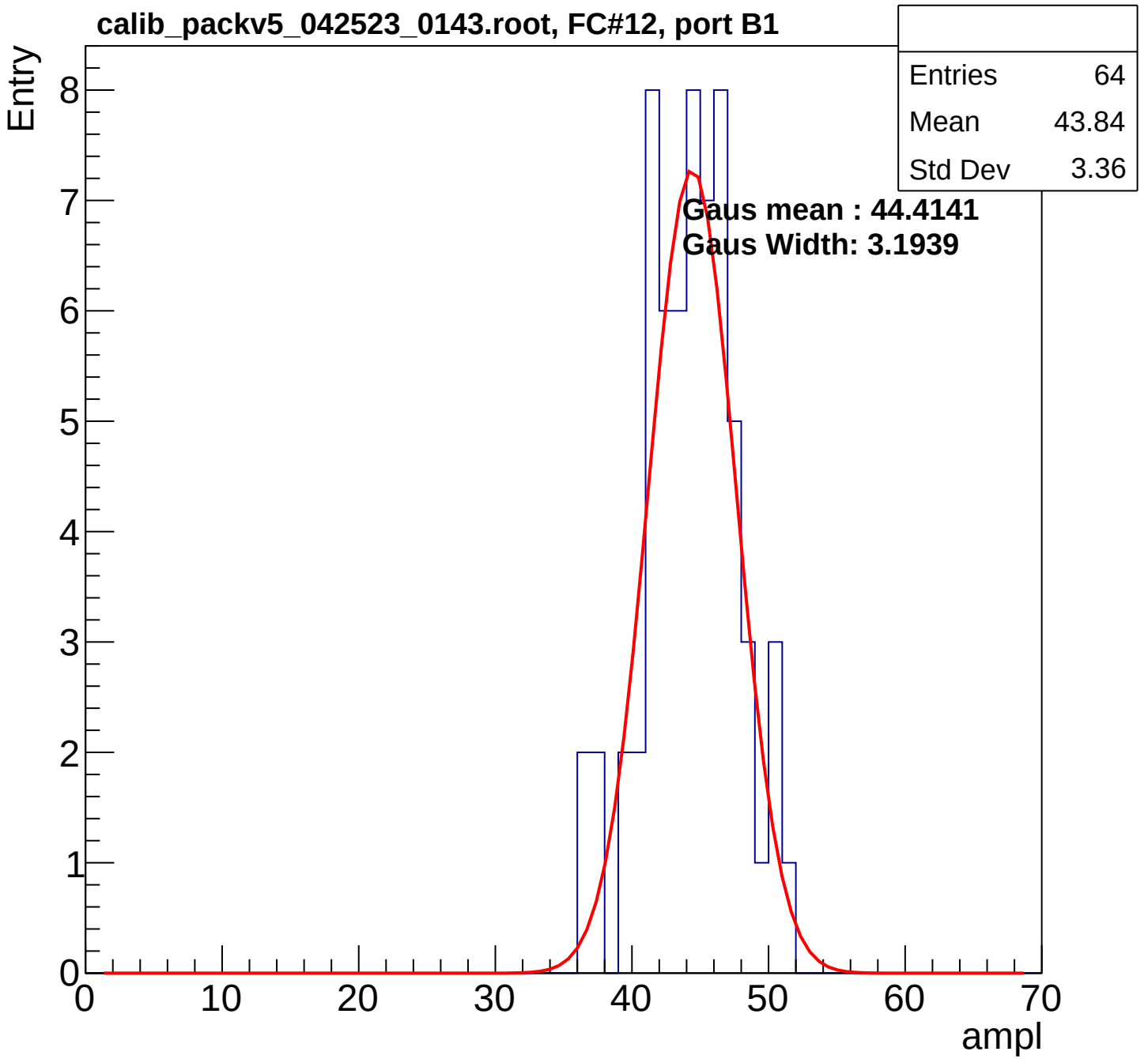
B0L102S, U23-ch108, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 43.84
Std Dev 3.36
Gaus mean : 44.4141
Gaus Width: 3.1939
Entry
ampl
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1
2
3
4
5
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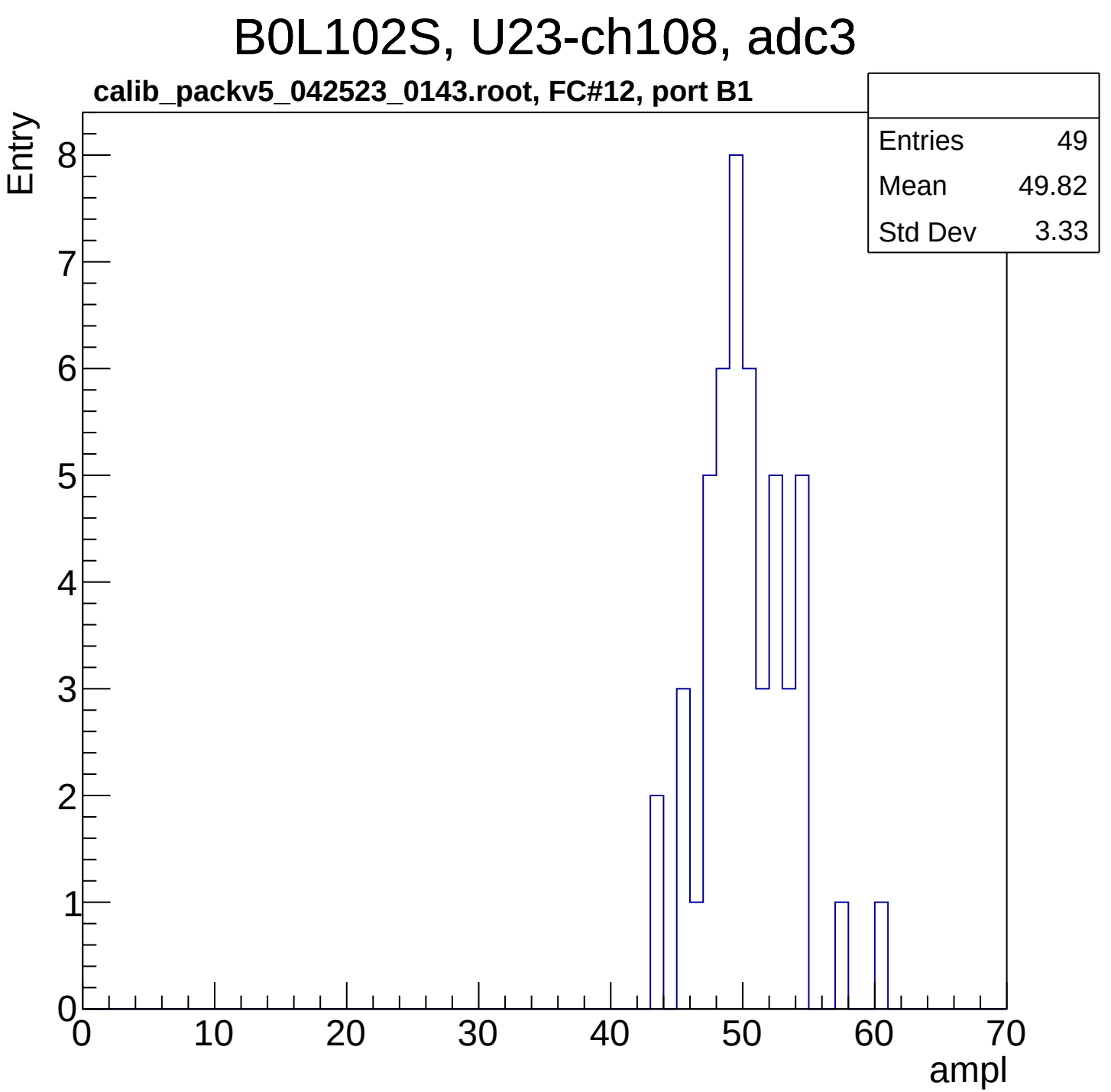
B0L102S, U23-ch108, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 49.82
Std Dev 3.33
Entry
ampl
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1
2
3
4
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0
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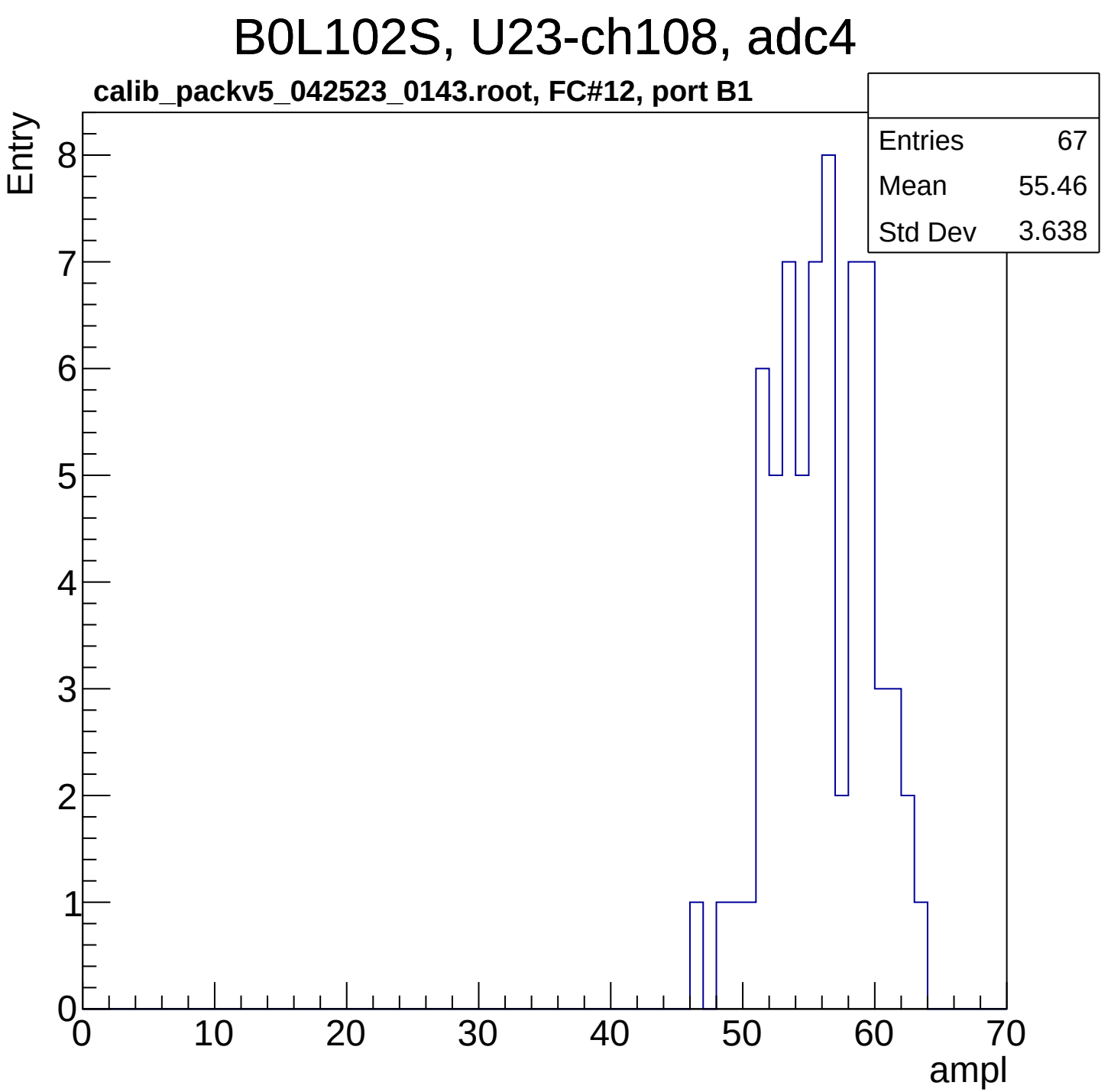
B0L102S, U23-ch108, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 55.46
Std Dev 3.638
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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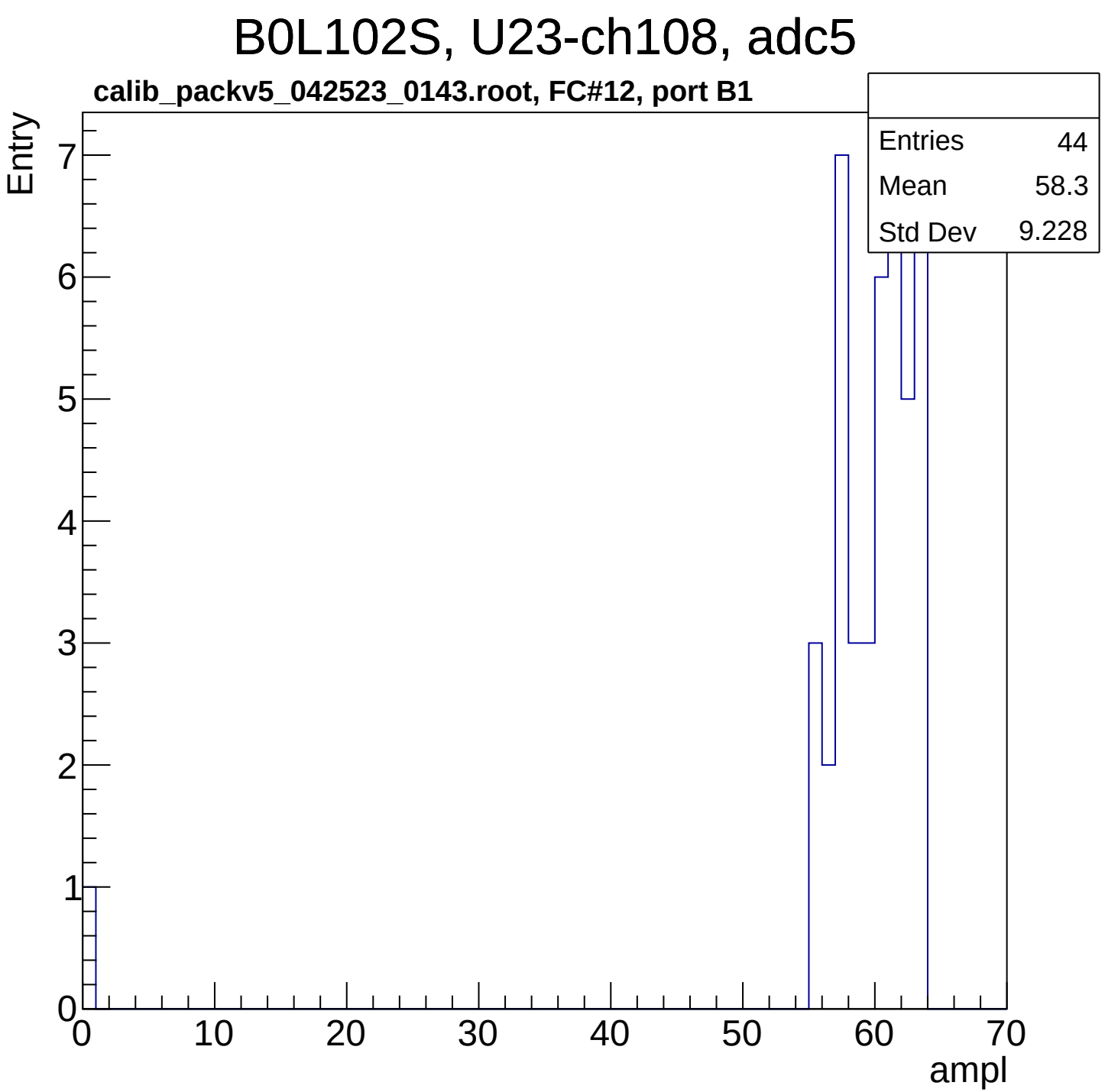
B0L102S, U23-ch108, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.3
Std Dev 9.228
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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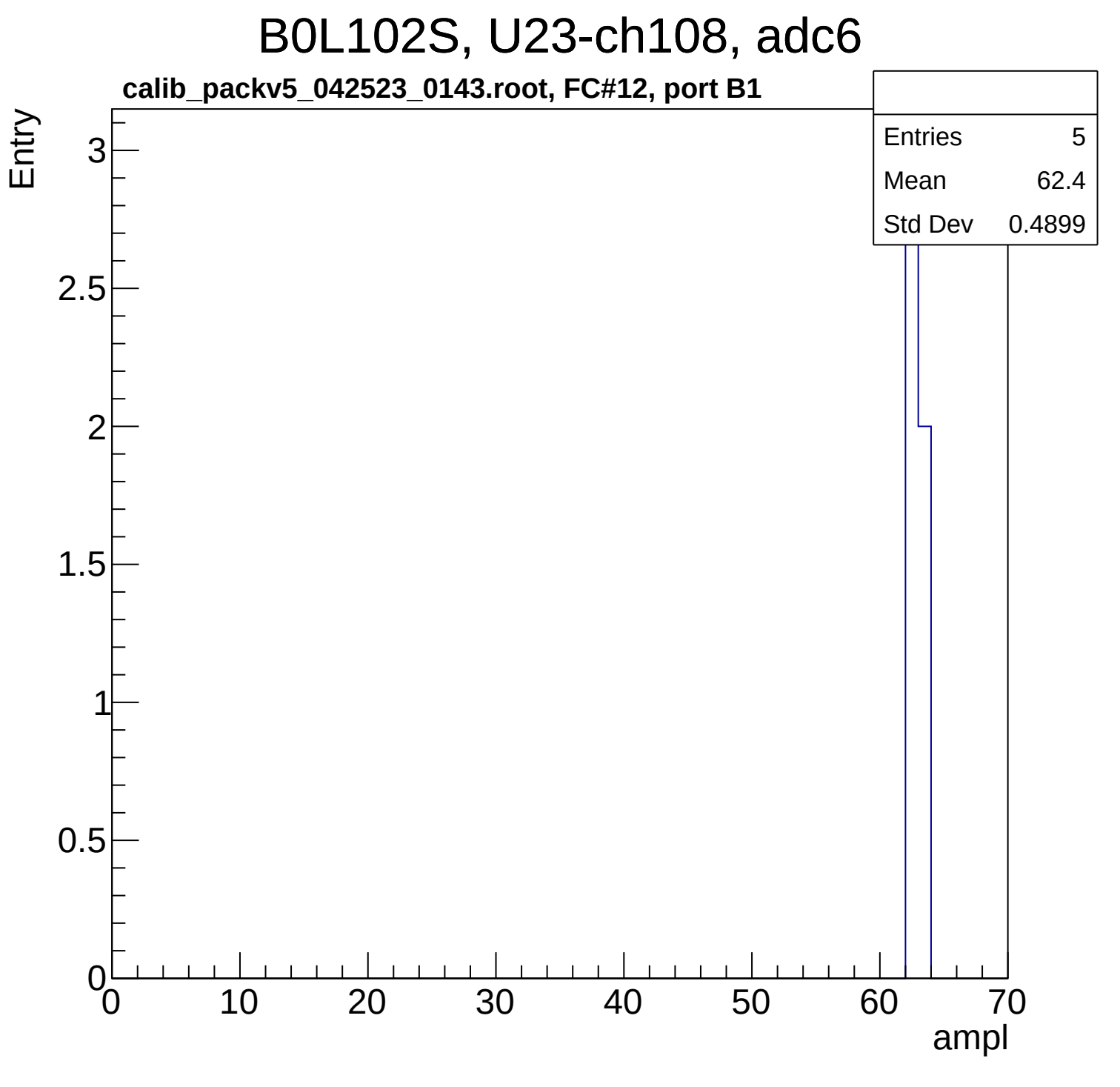
B0L102S, U23-ch108, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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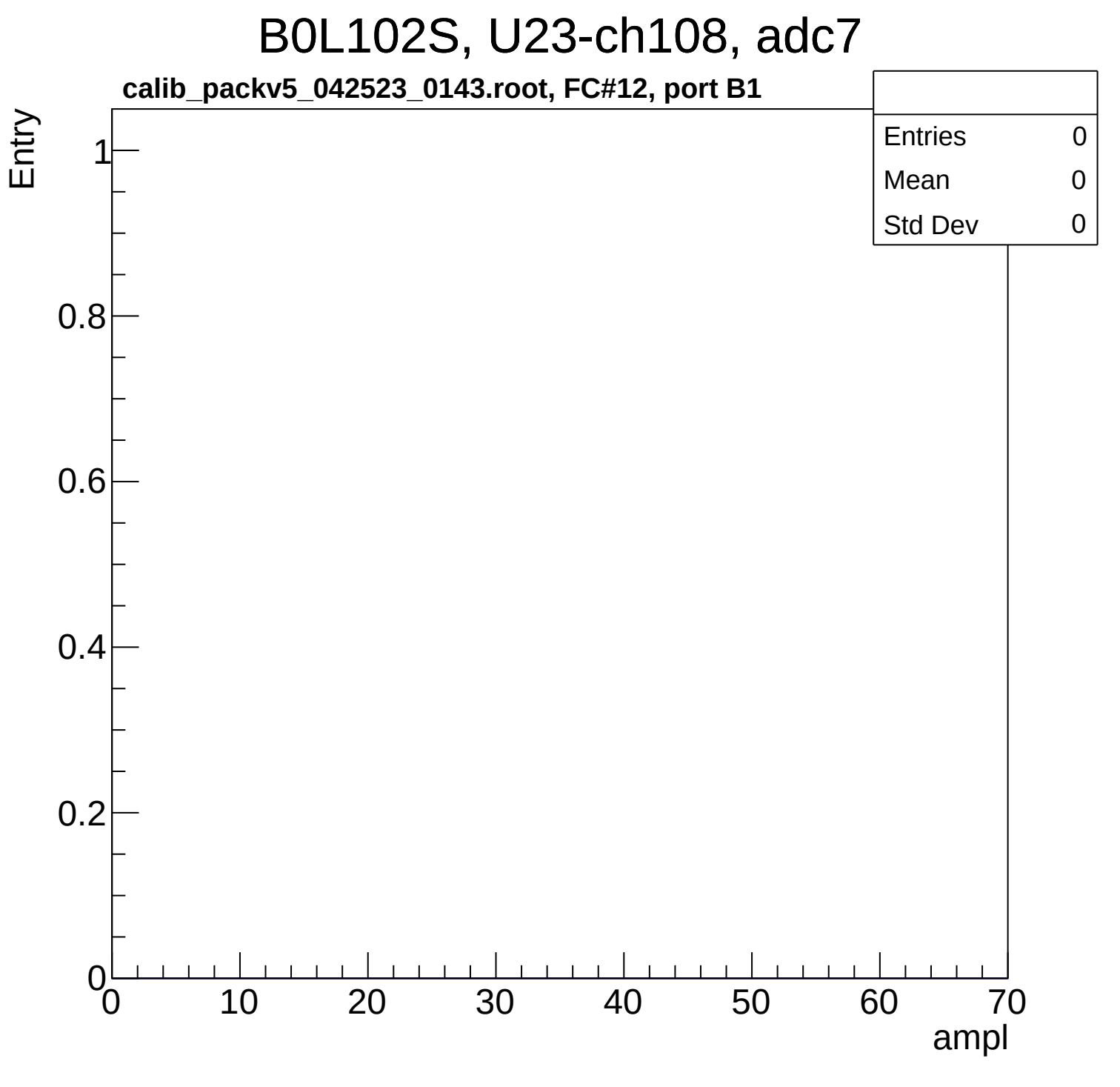
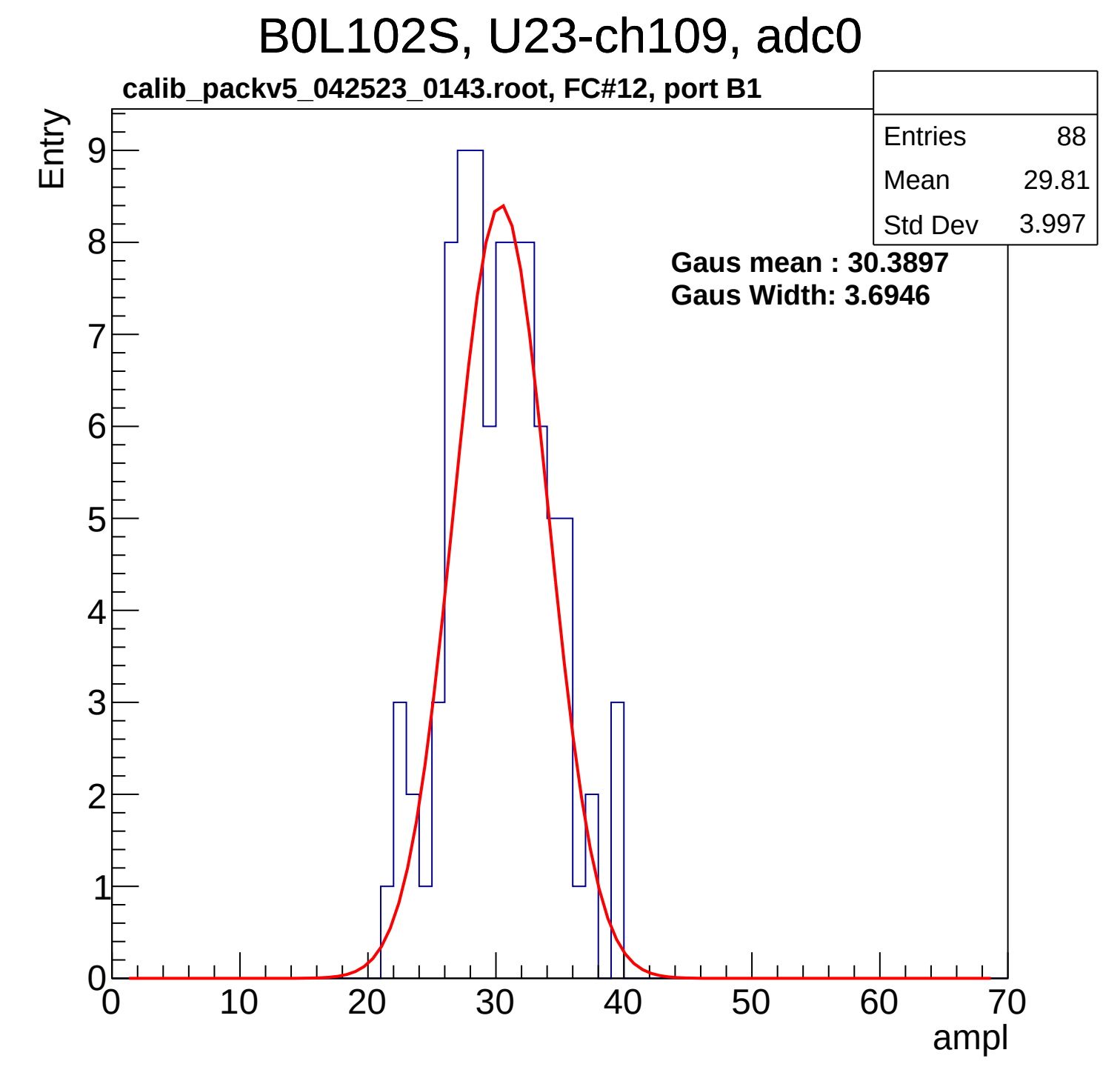
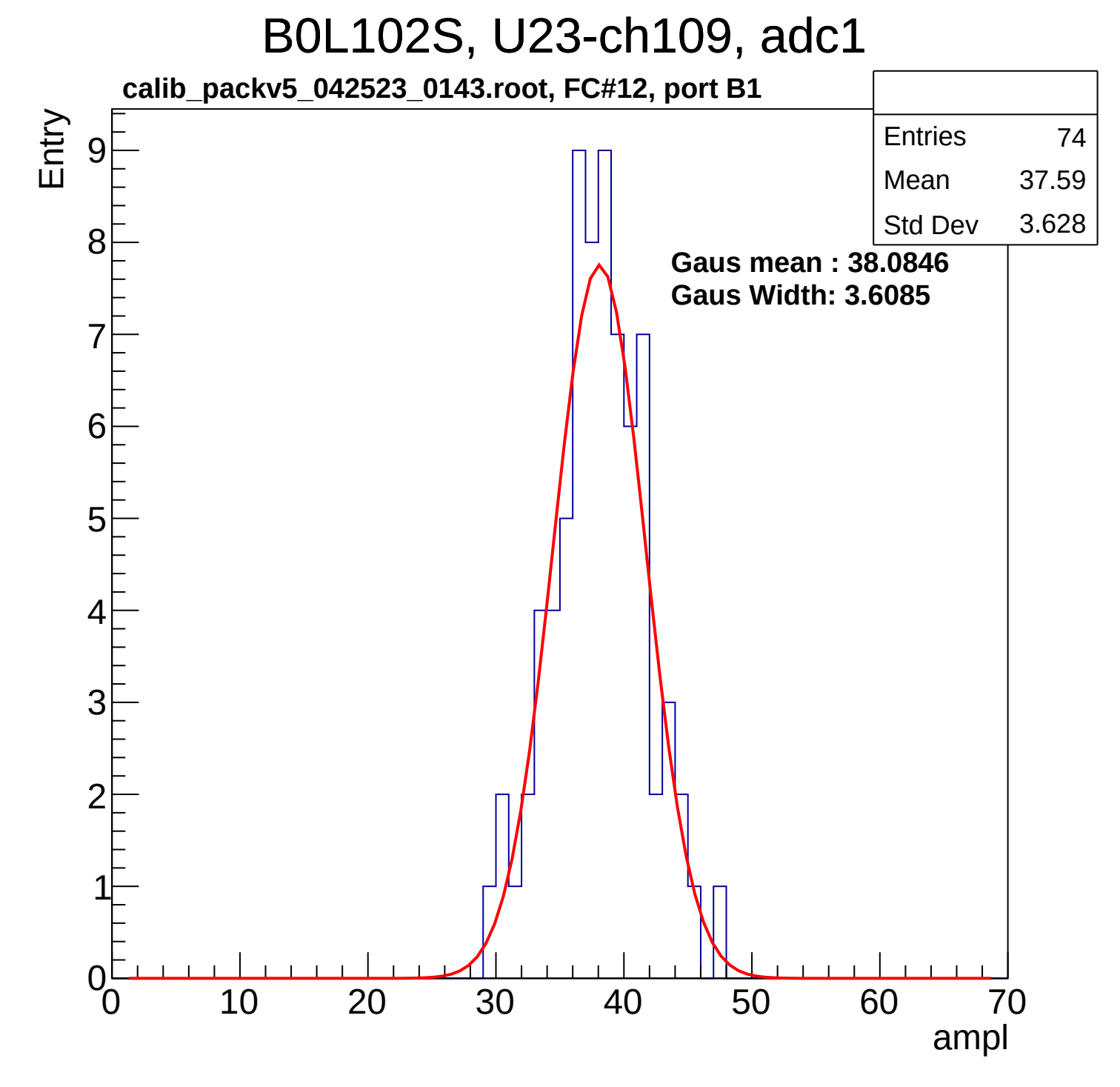

B0L102S, U23-ch108, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch109, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 88
Mean 29.81
Std Dev 3.997
Gaus mean : 30.3897
Gaus Width: 3.6946
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch109, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 37.59
Std Dev 3.628
Gaus mean : 38.0846
Gaus Width: 3.6085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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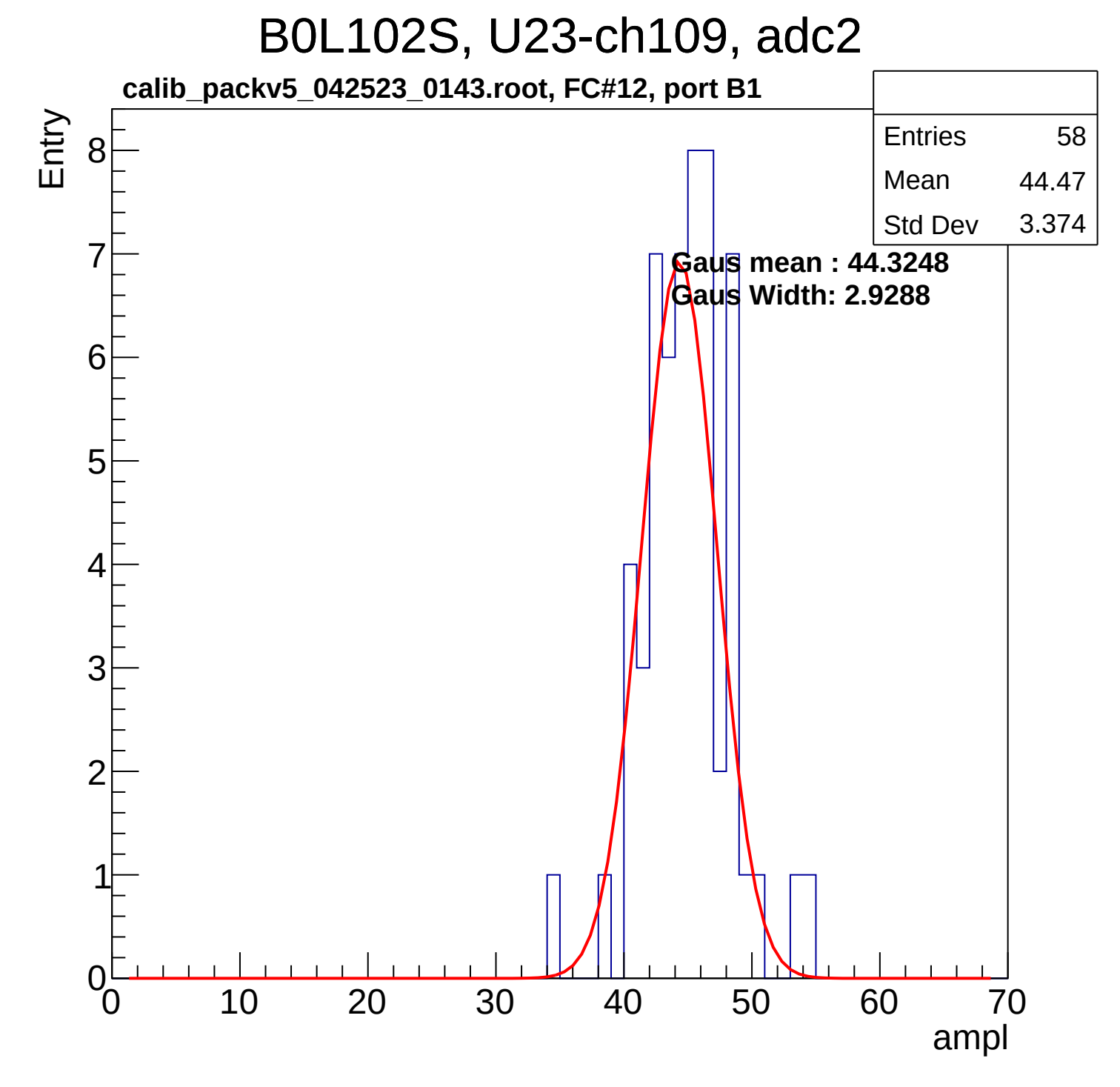
B0L102S, U23-ch109, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 44.47
Std Dev 3.374
Gaus mean : 44.3248
Gaus Width: 2.9288
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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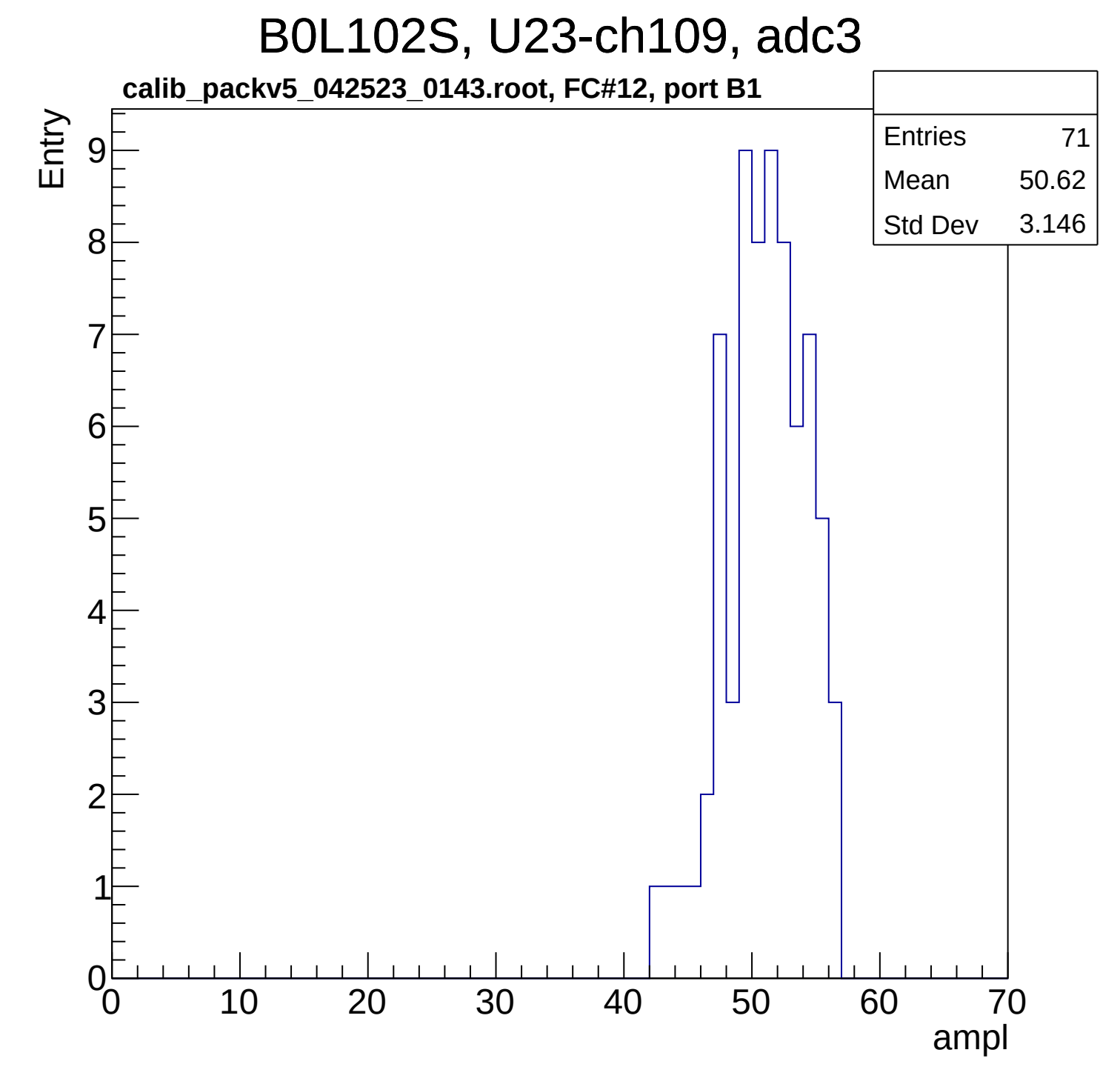
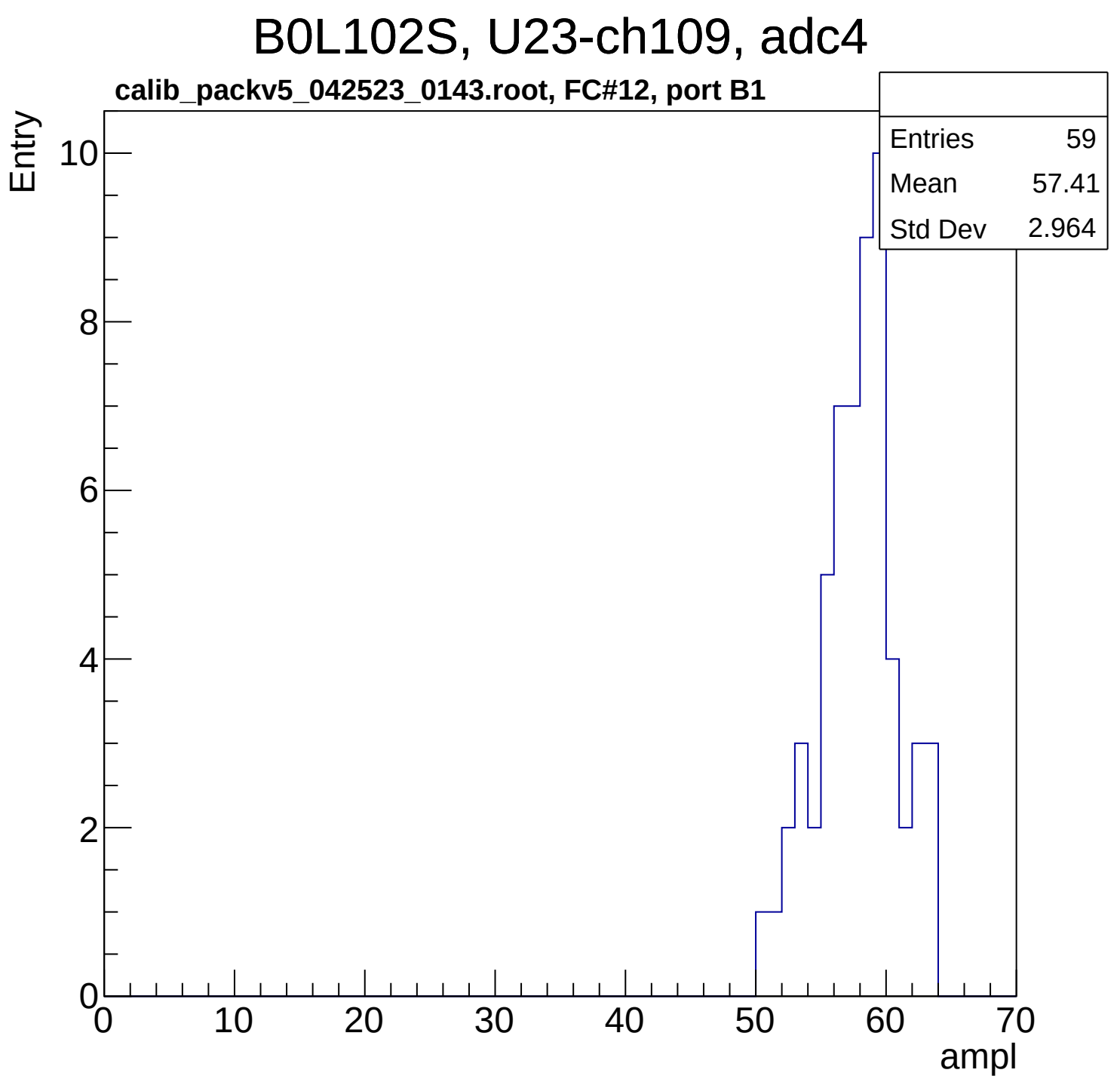
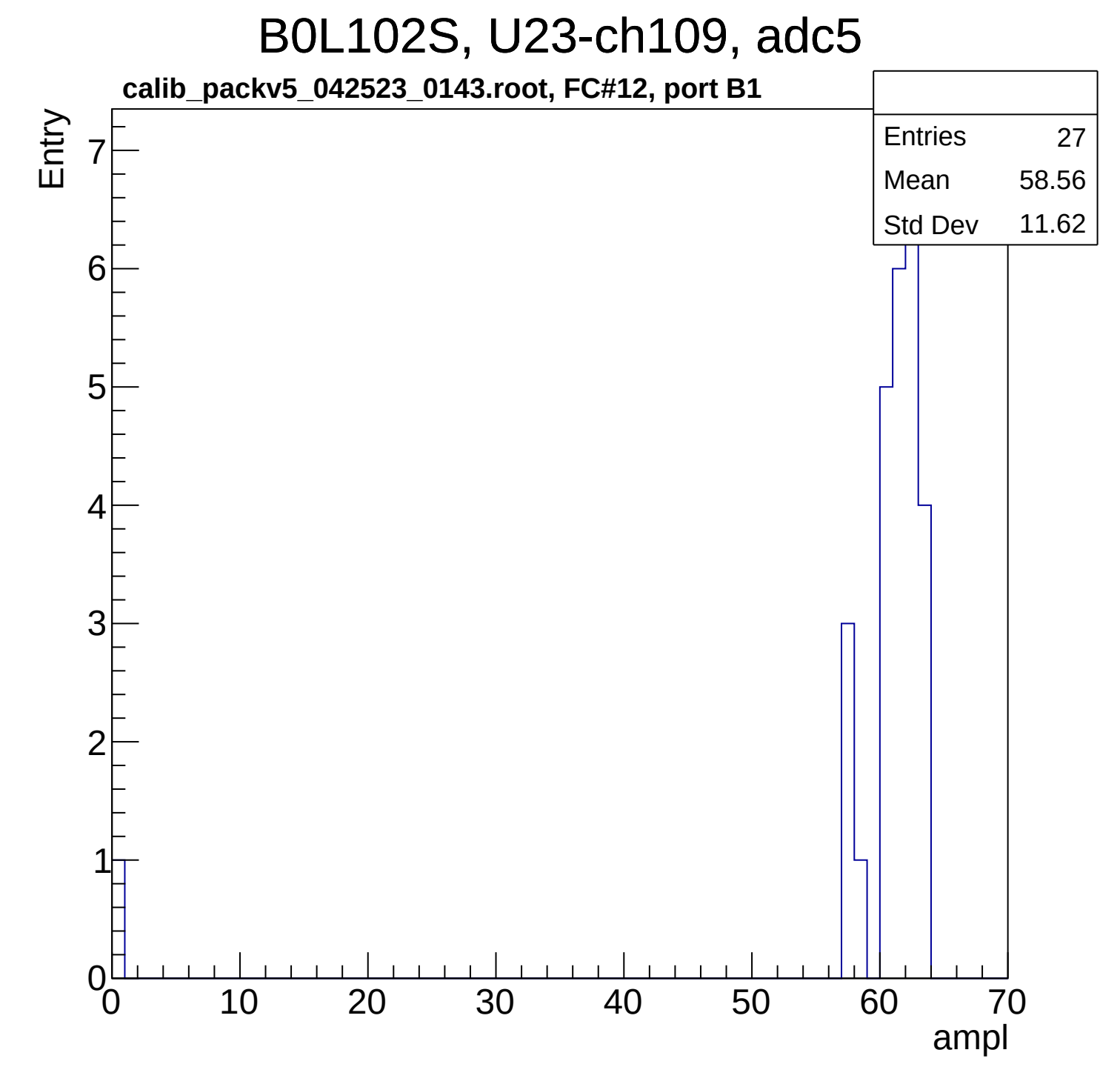
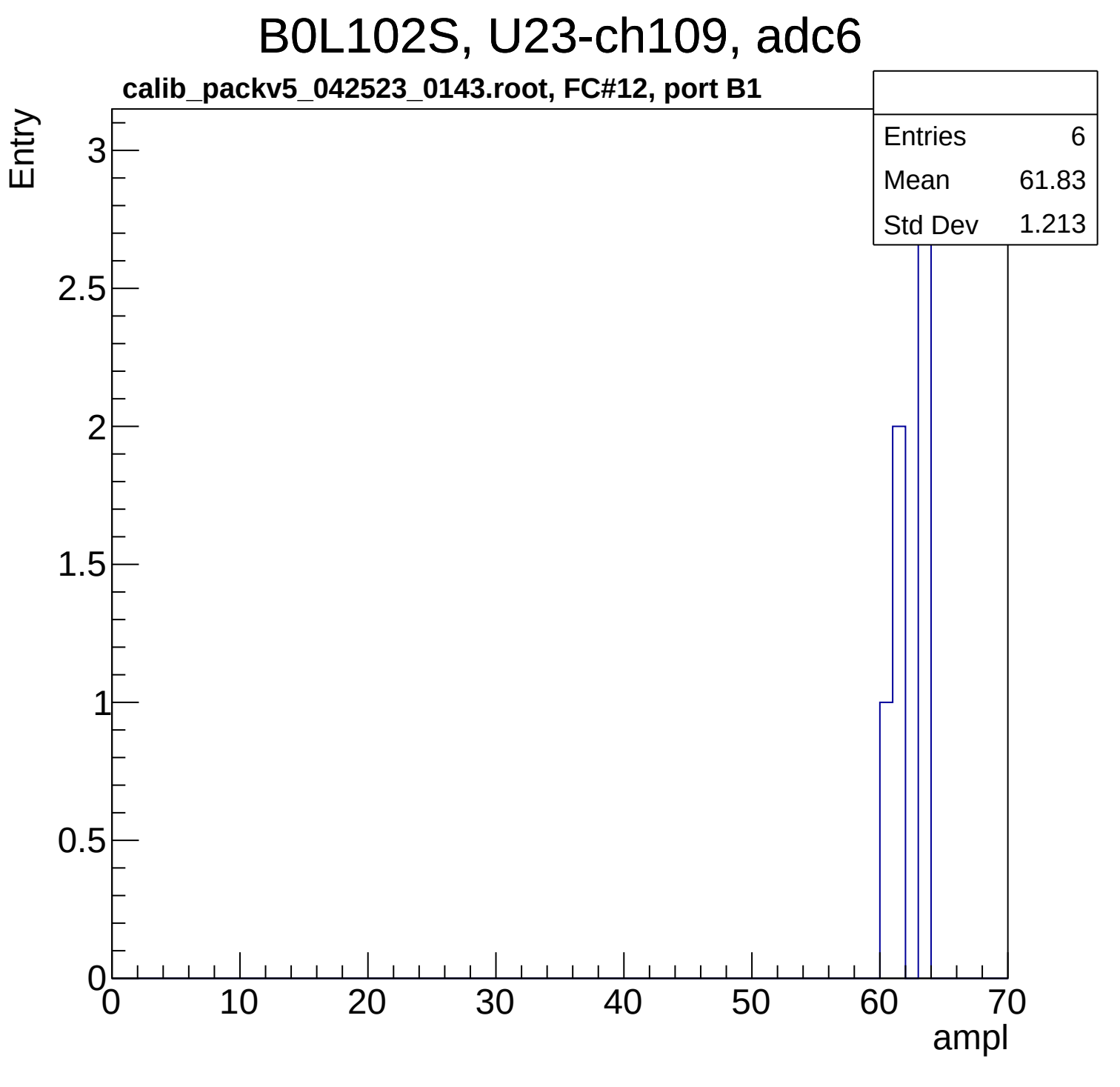

B0L102S, U23-ch109, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 50.62
Std Dev 3.146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch109, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 57.41
Std Dev 2.964
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch109, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 27
Mean 58.56
Std Dev 11.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch109, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 61.83
Std Dev 1.213
Entry
ampl
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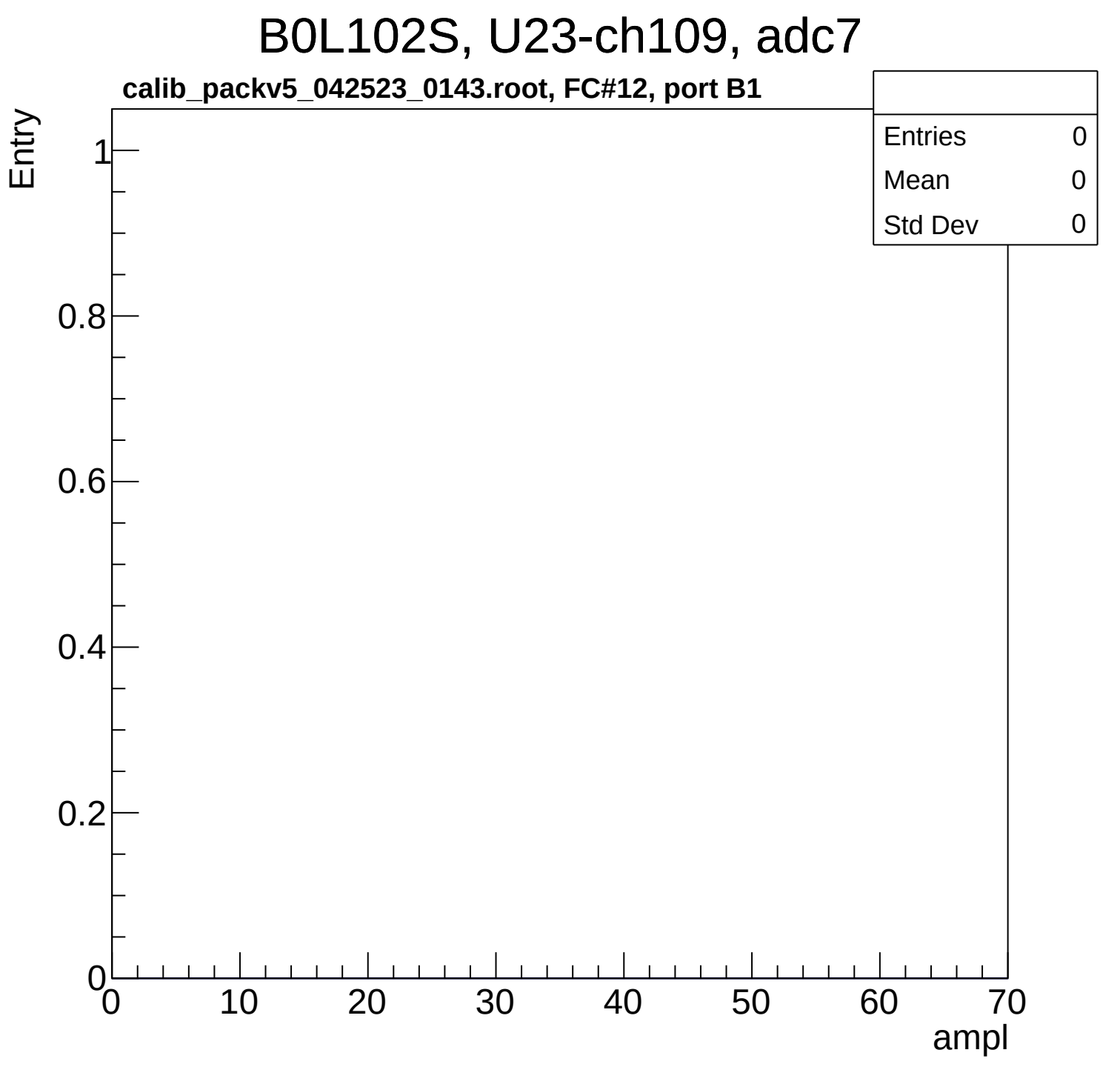
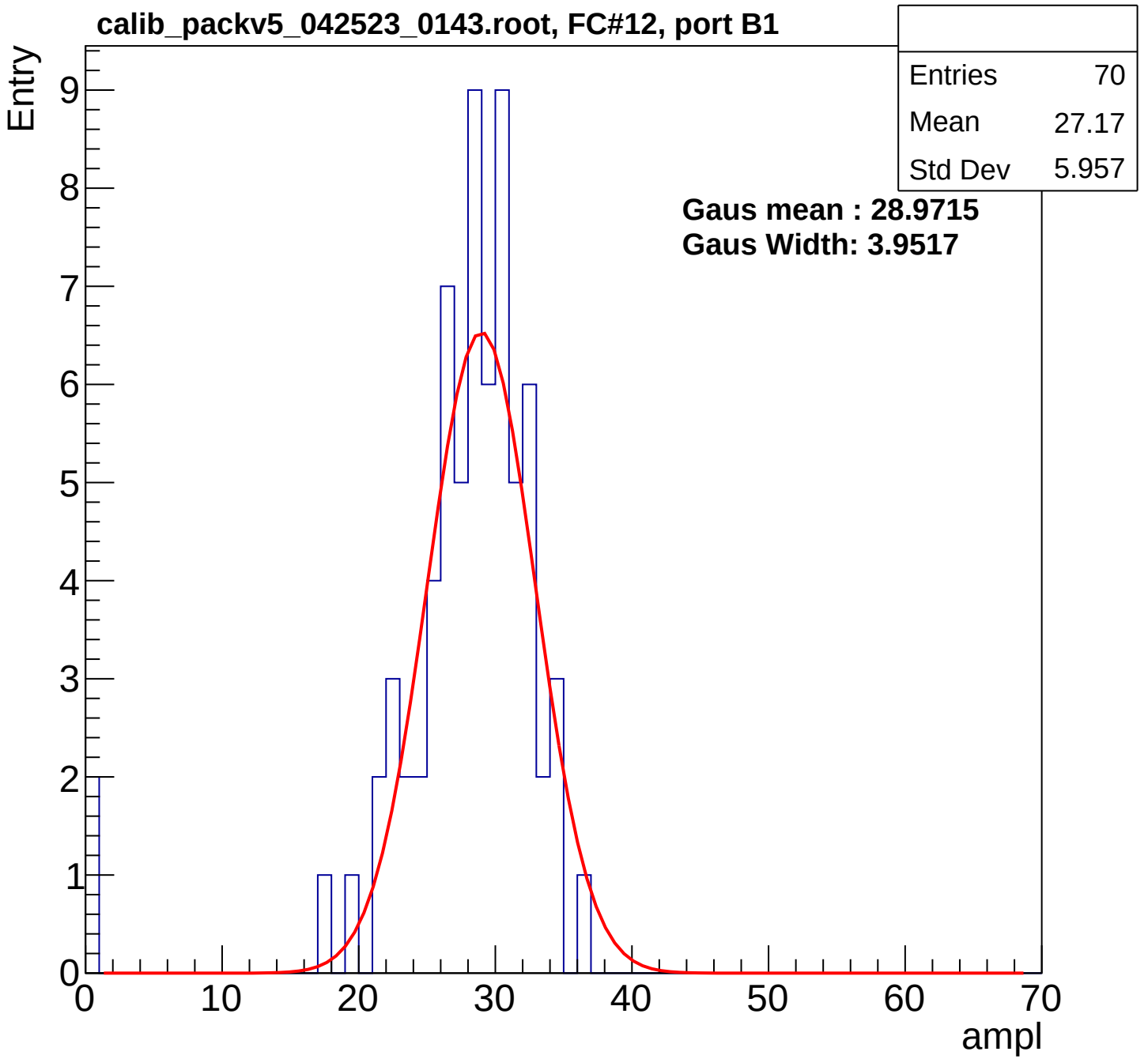
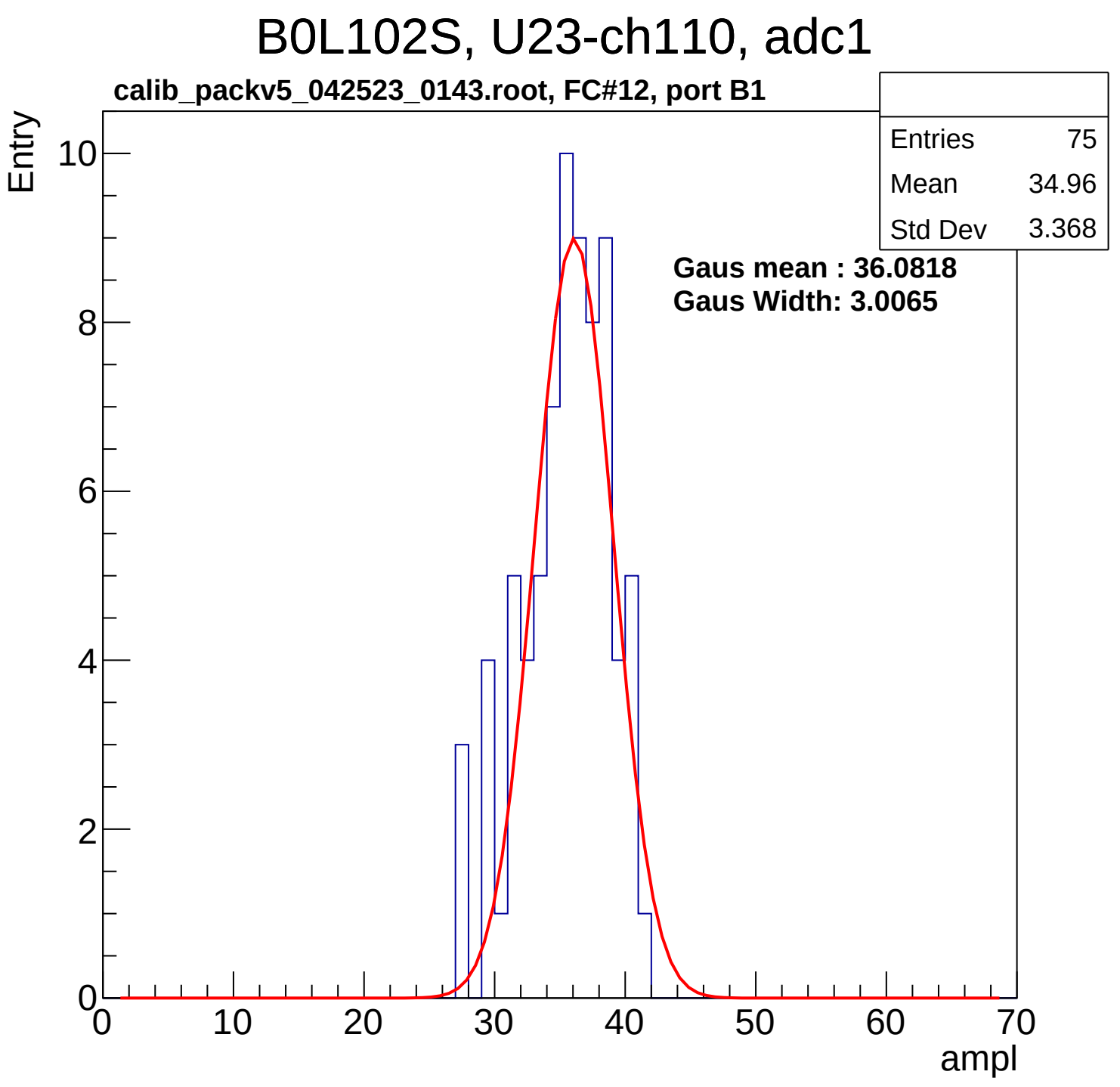
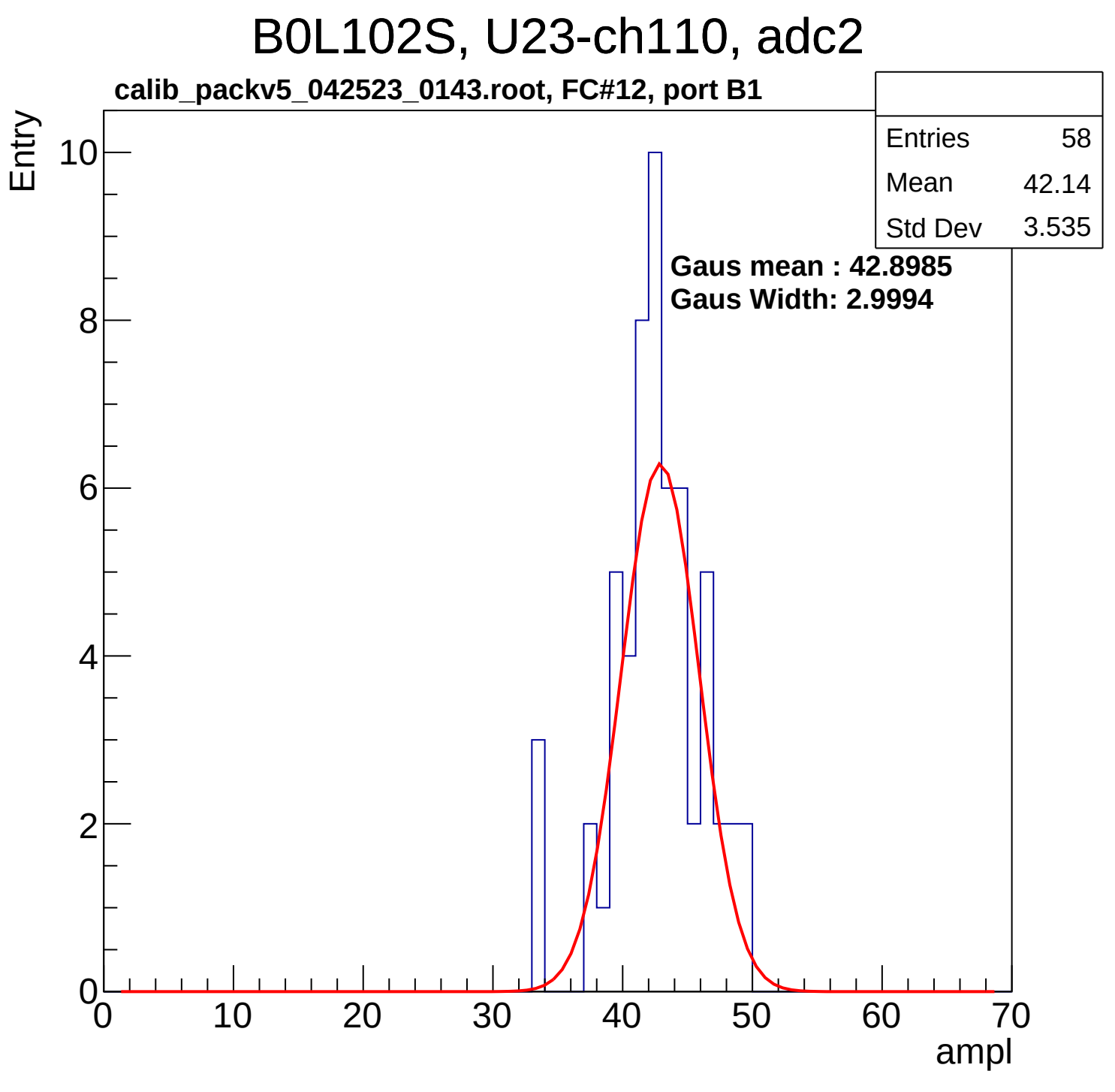
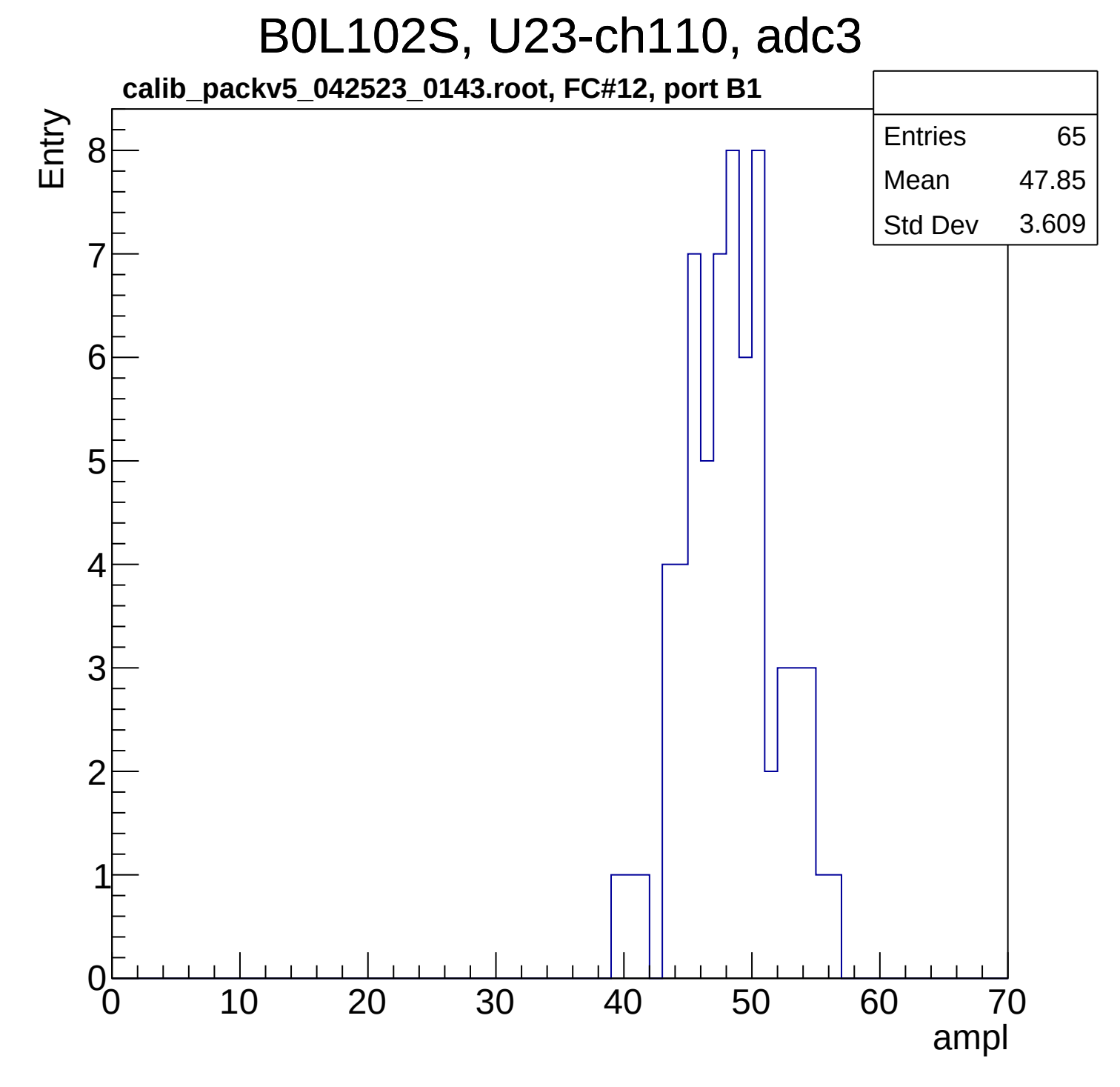
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch109, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch110, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 27.17
Std Dev 5.957
Gaus mean : 28.9715
Gaus Width: 3.9517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch110, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 34.96
Std Dev 3.368
Gaus mean : 36.0818
Gaus Width: 3.0065
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch110, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 42.14
Std Dev 3.535
Gaus mean : 42.8985
Gaus Width: 2.9994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch110, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 47.85
Std Dev 3.609
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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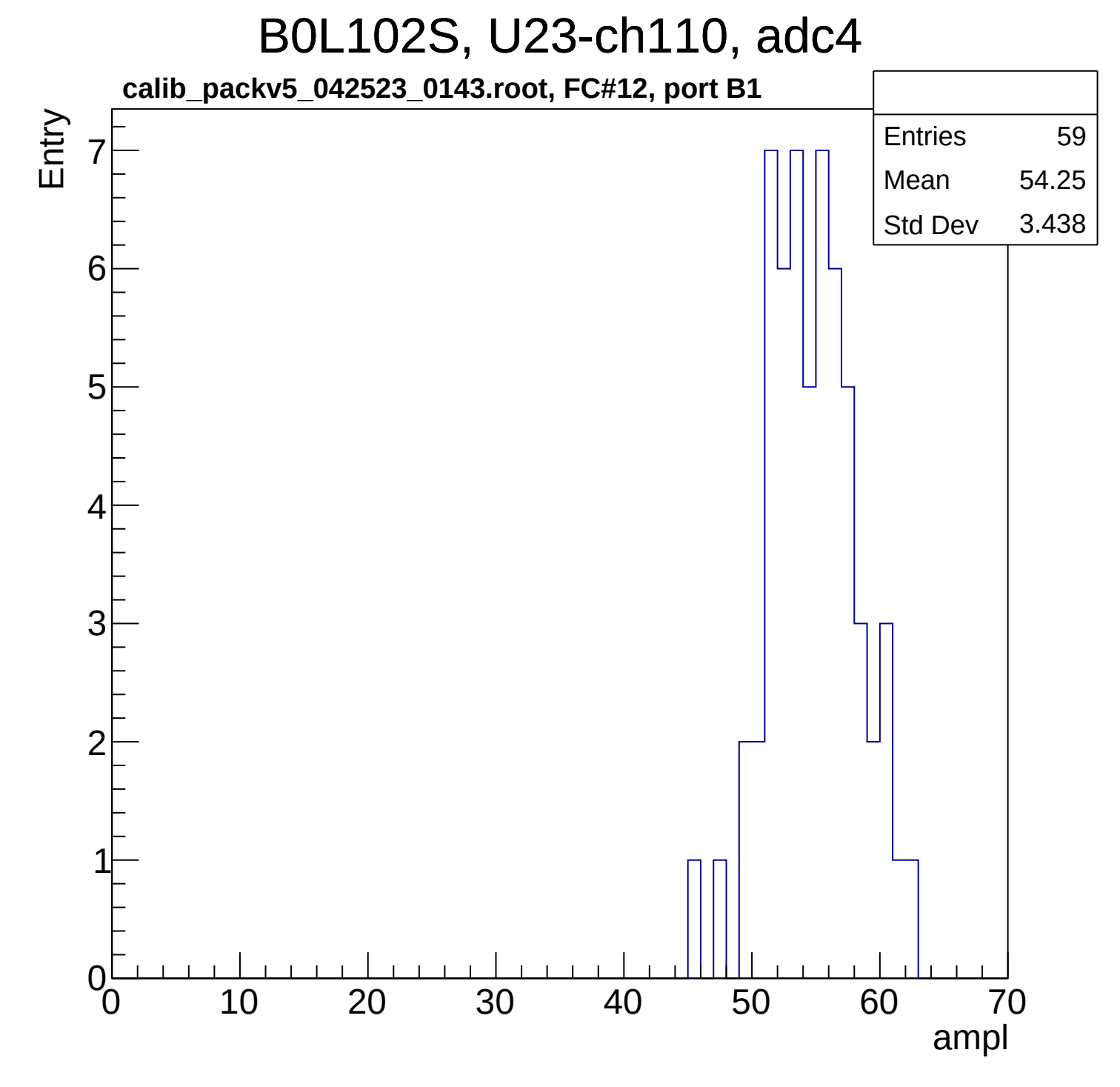
40
50
60
70


B0L102S, U23-ch110, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 54.25
Std Dev 3.438
Entry
ampl
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3
4
5
6
7
0
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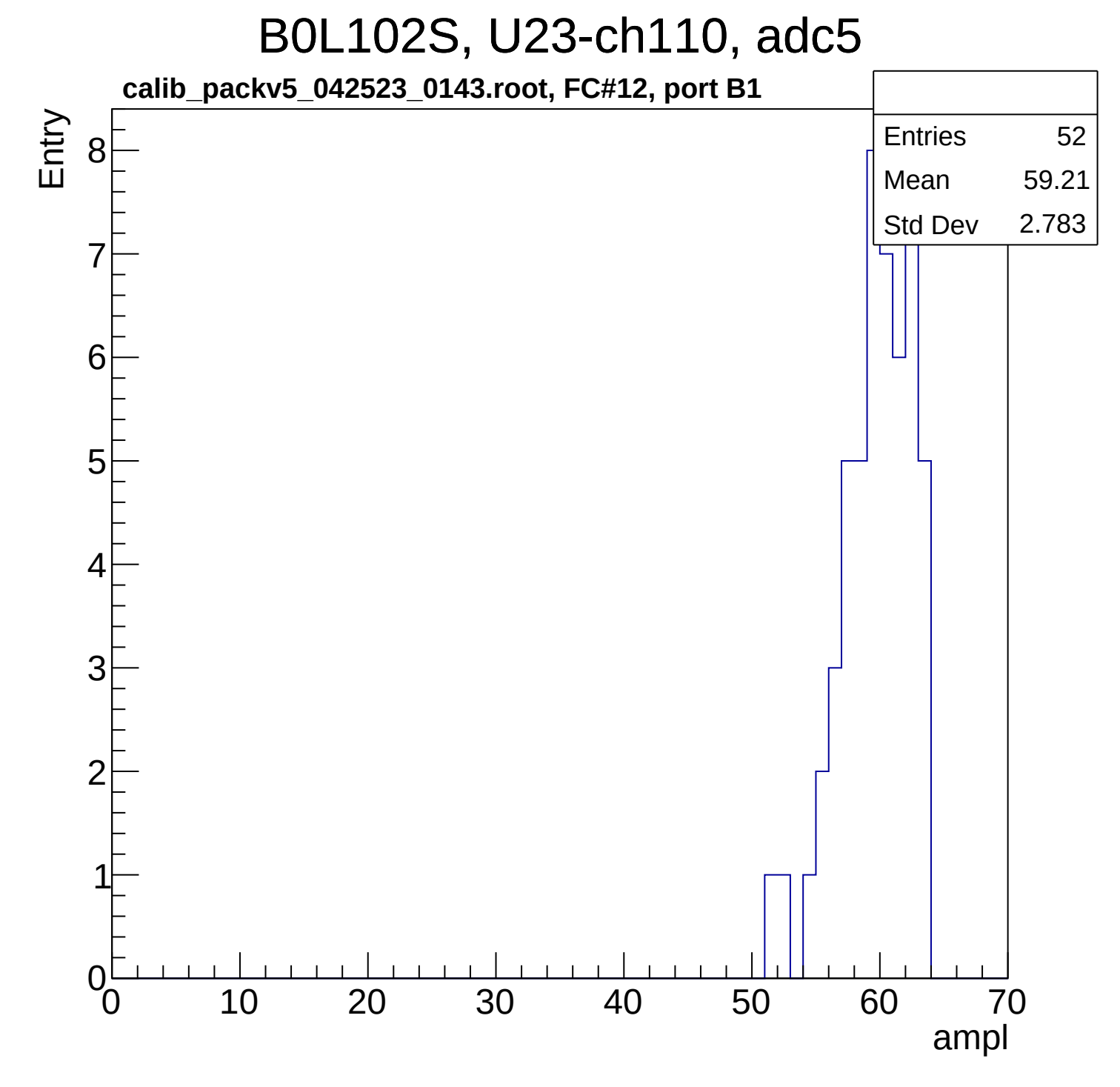
20
30
40
50
60
70


B0L102S, U23-ch110, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 59.21
Std Dev 2.783
Entry
ampl
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2
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4
5
6
7
8
0
10
20
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40
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60
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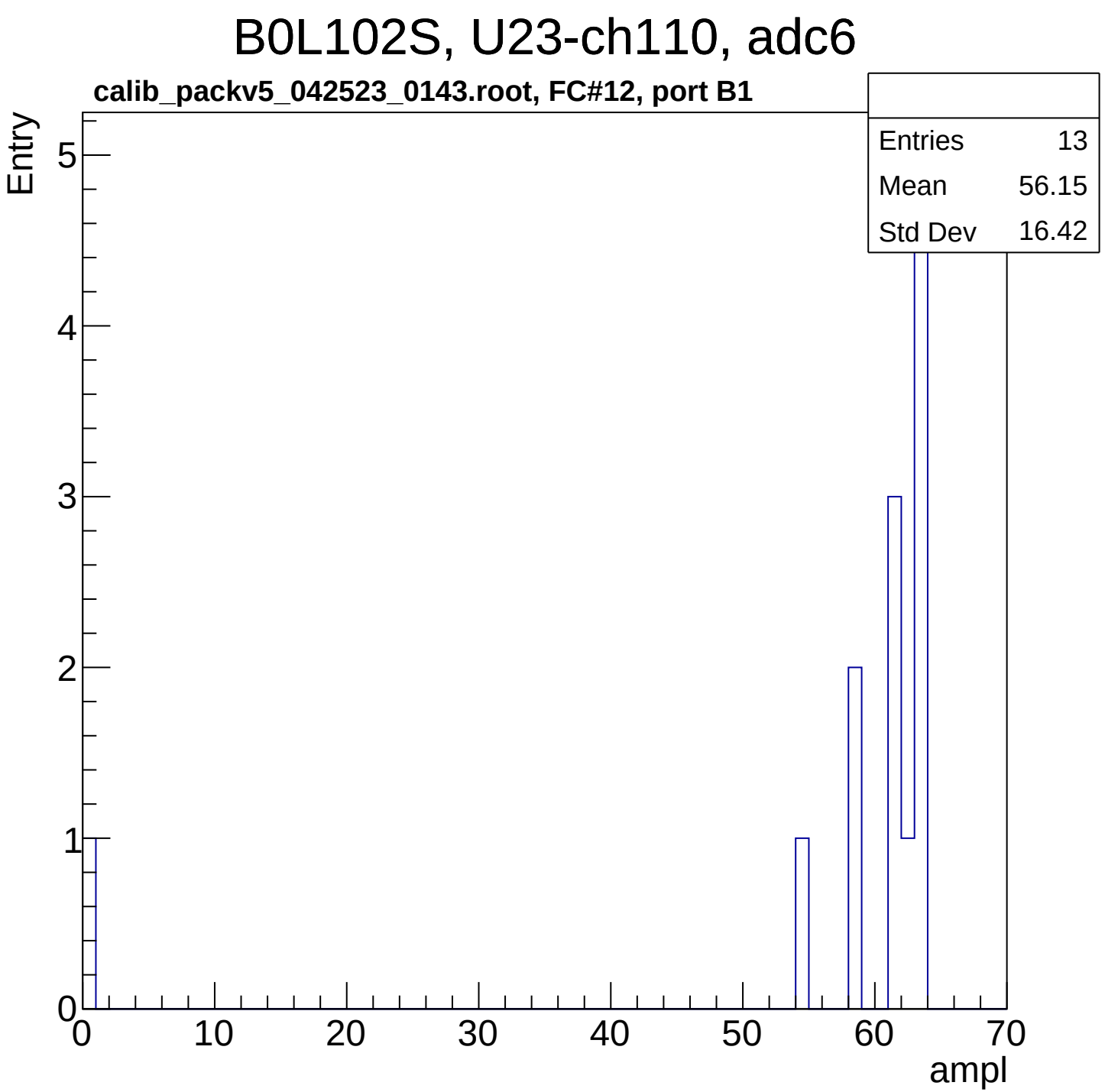
B0L102S, U23-ch110, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 13
Mean 56.15
Std Dev 16.42
Entry
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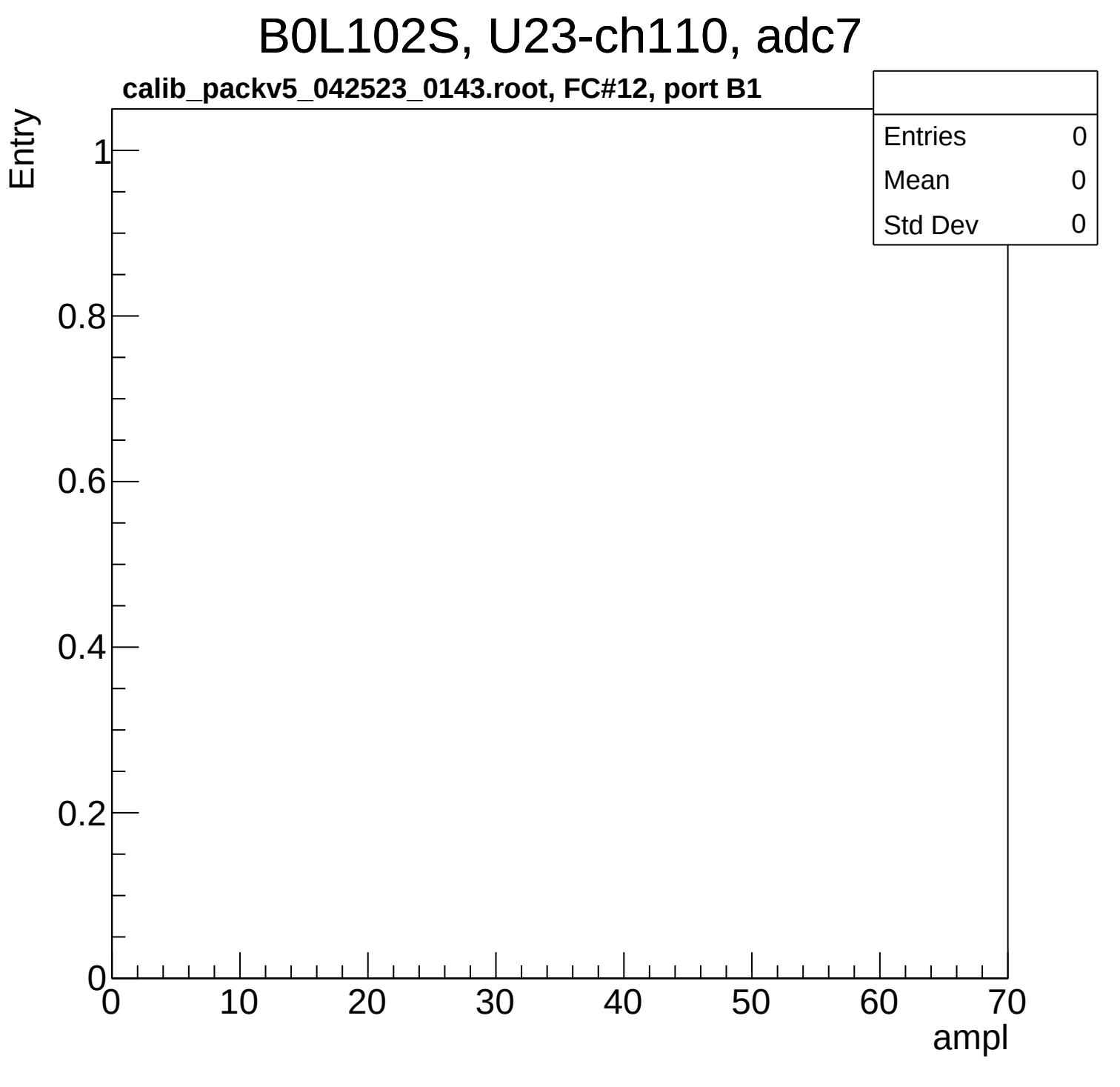
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch110, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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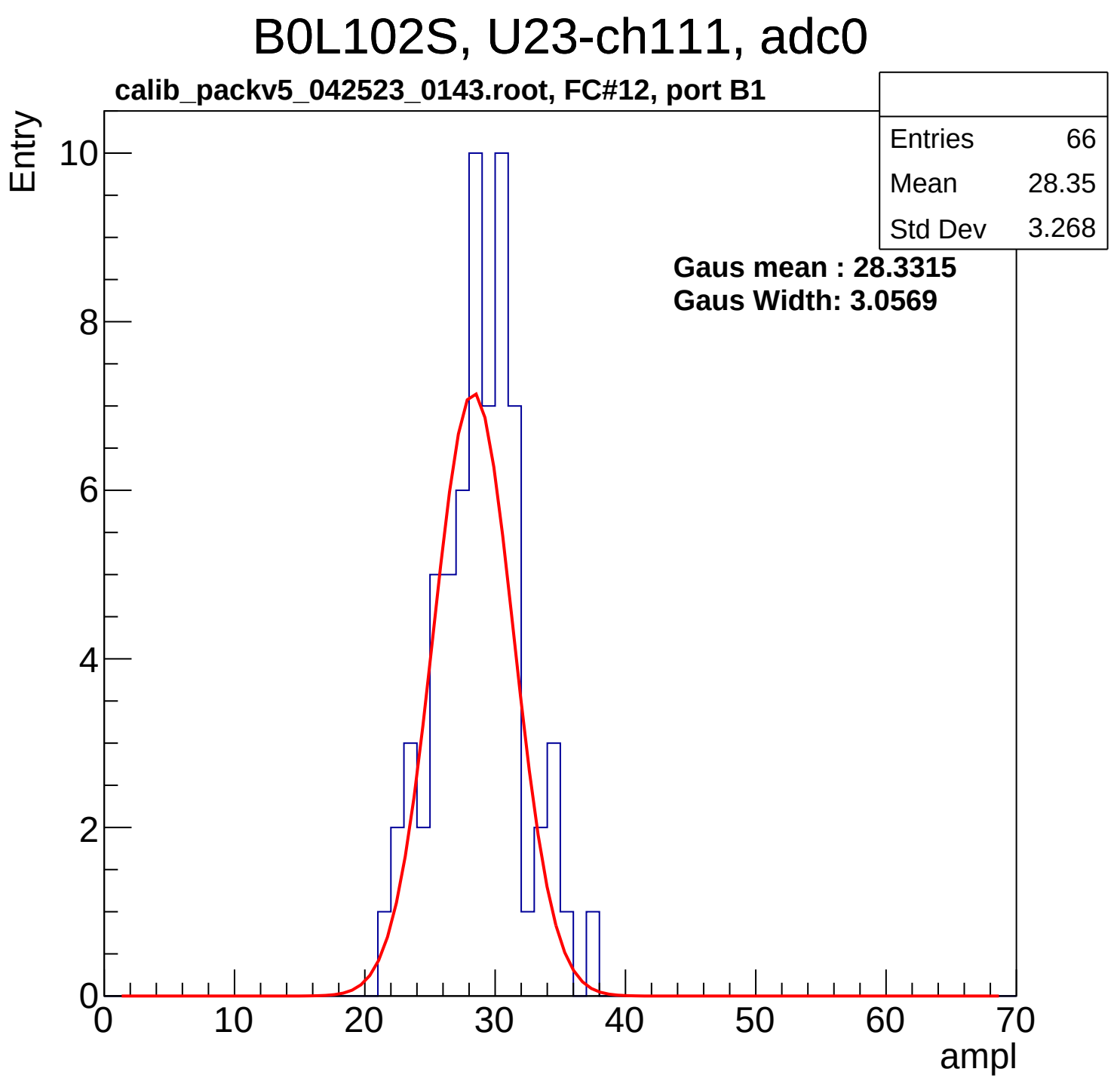
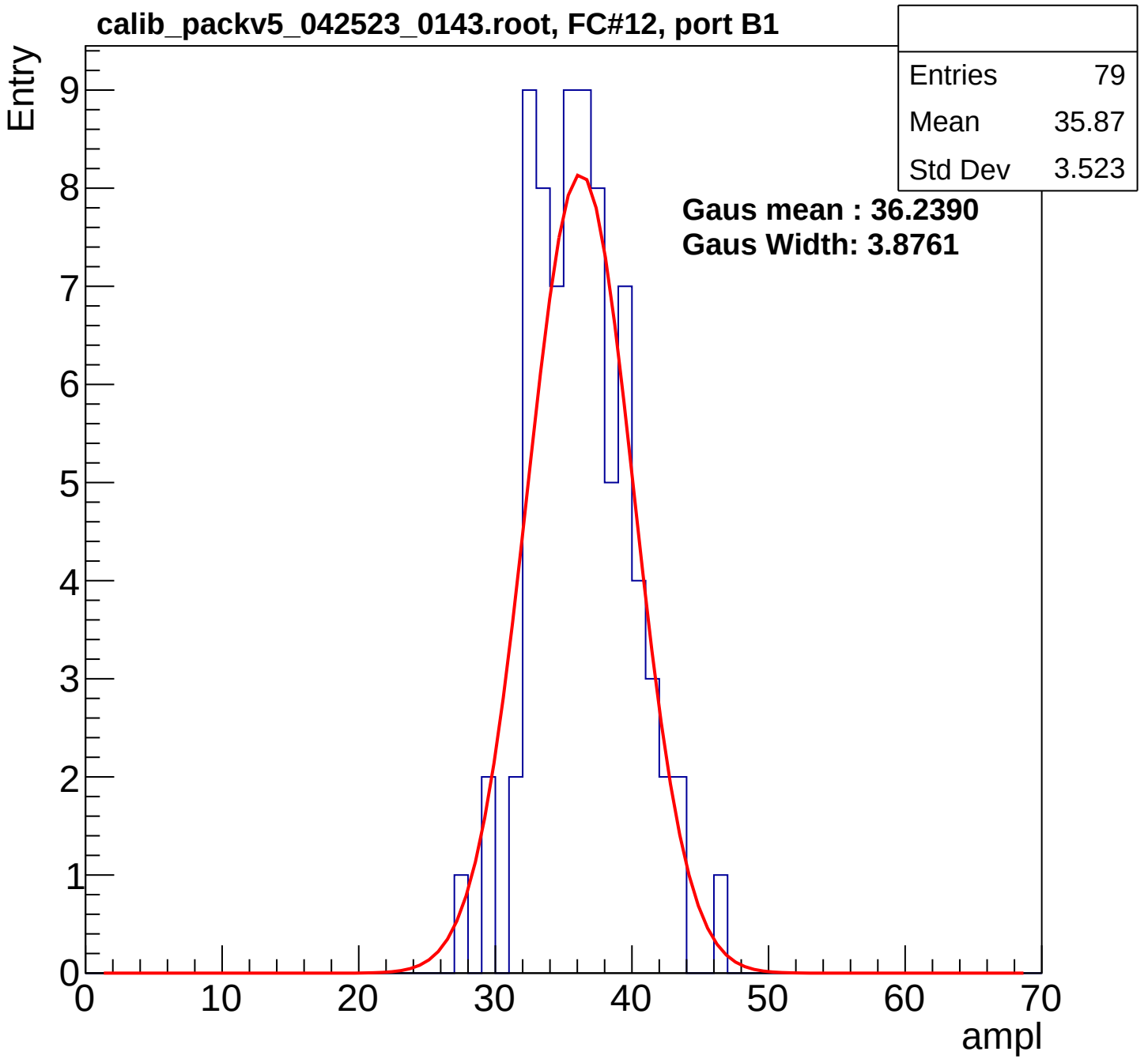
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch111, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 28.35
Std Dev 3.268
Gaus mean : 28.3315
Gaus Width: 3.0569
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch111, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 35.87
Std Dev 3.523
Gaus mean : 36.2390
Gaus Width: 3.8761
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
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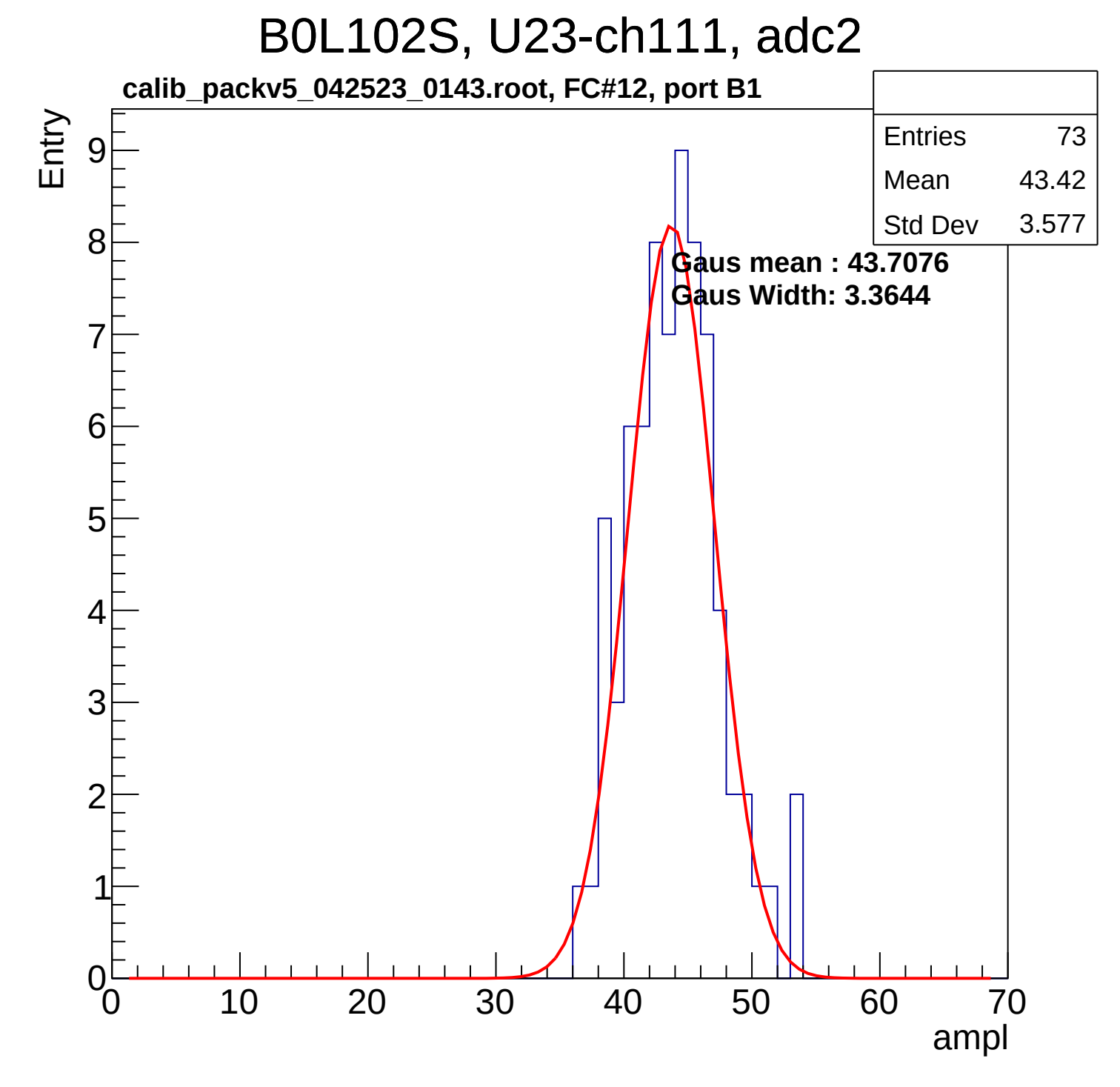
60
70


B0L102S, U23-ch111, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 43.42
Std Dev 3.577
Gaus mean : 43.7076
Gaus Width: 3.3644
Entry
ampl
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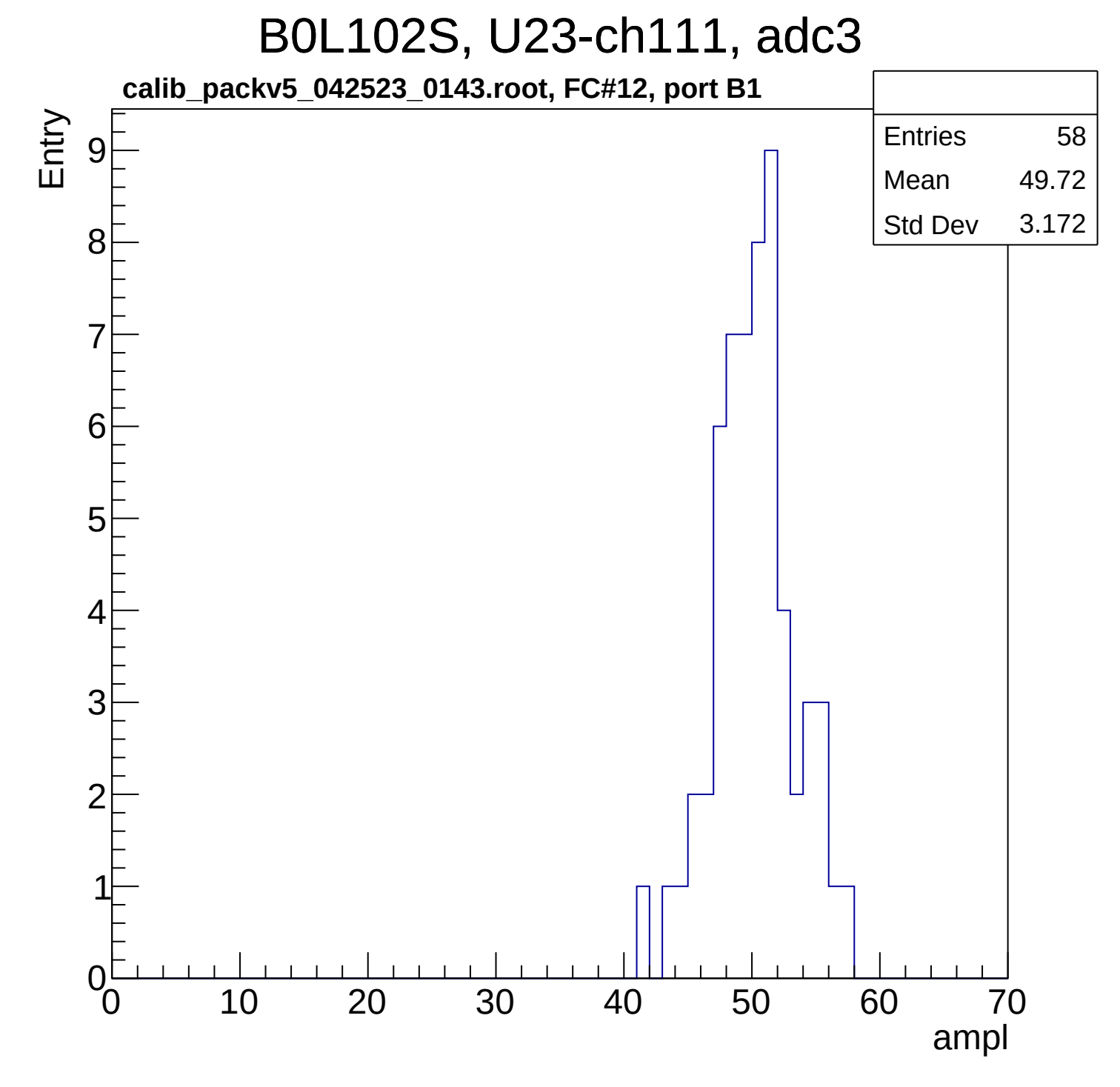
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch111, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 58
Mean 49.72
Std Dev 3.172
Entry
ampl
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3
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5
6
7
8
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0
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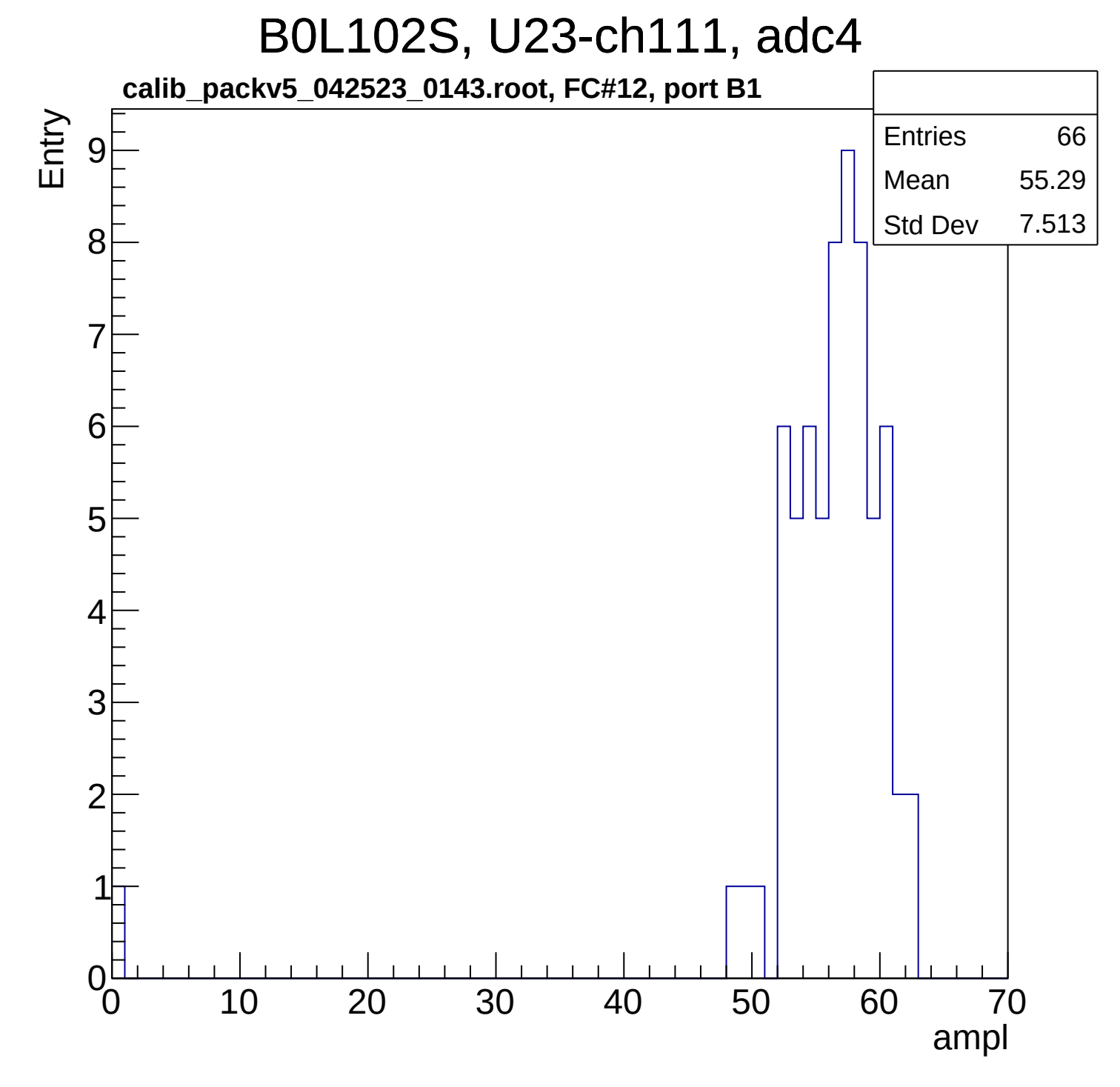
B0L102S, U23-ch111, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 55.29
Std Dev 7.513
Entry
ampl
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3
4
5
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0
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60
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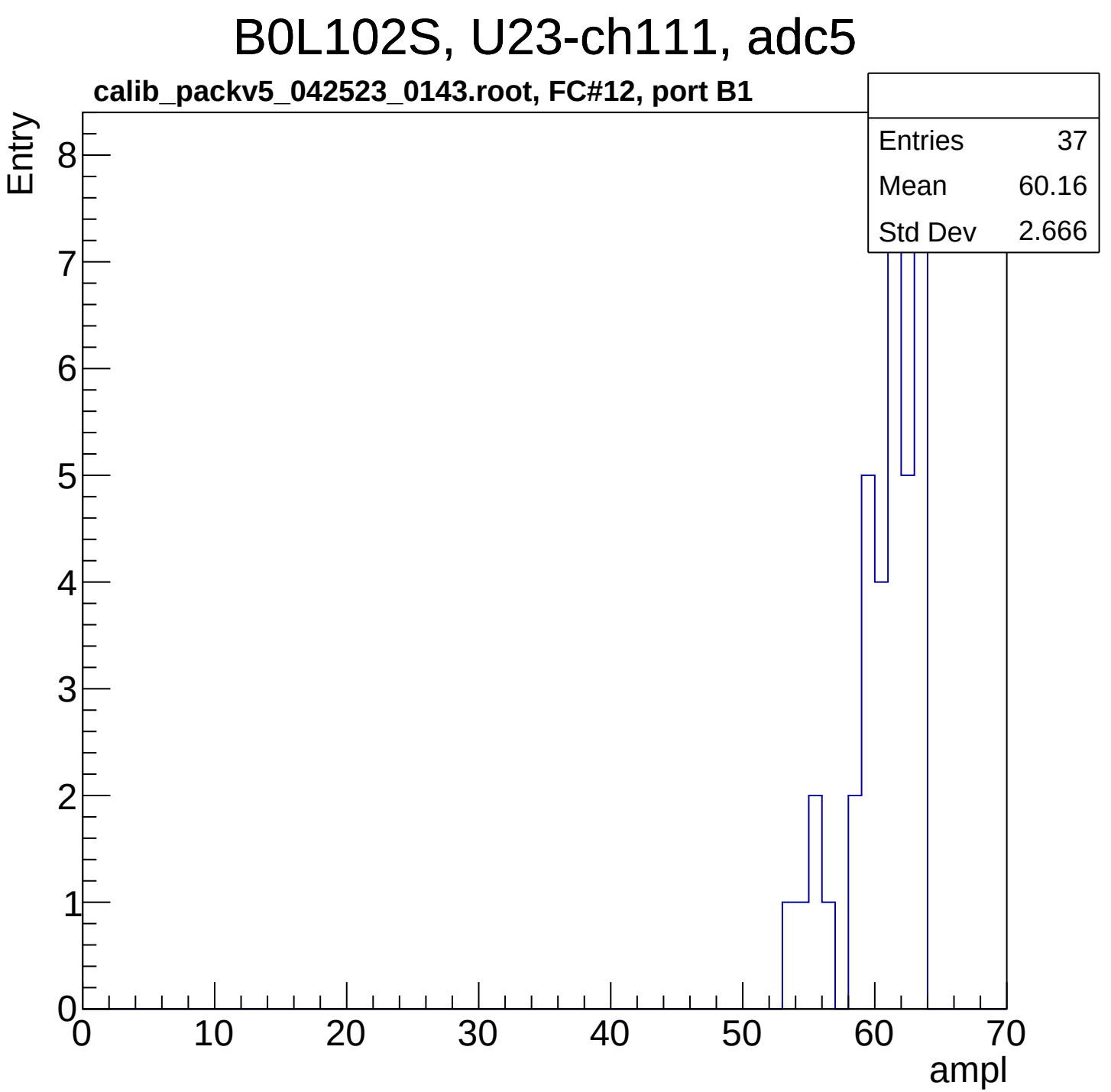
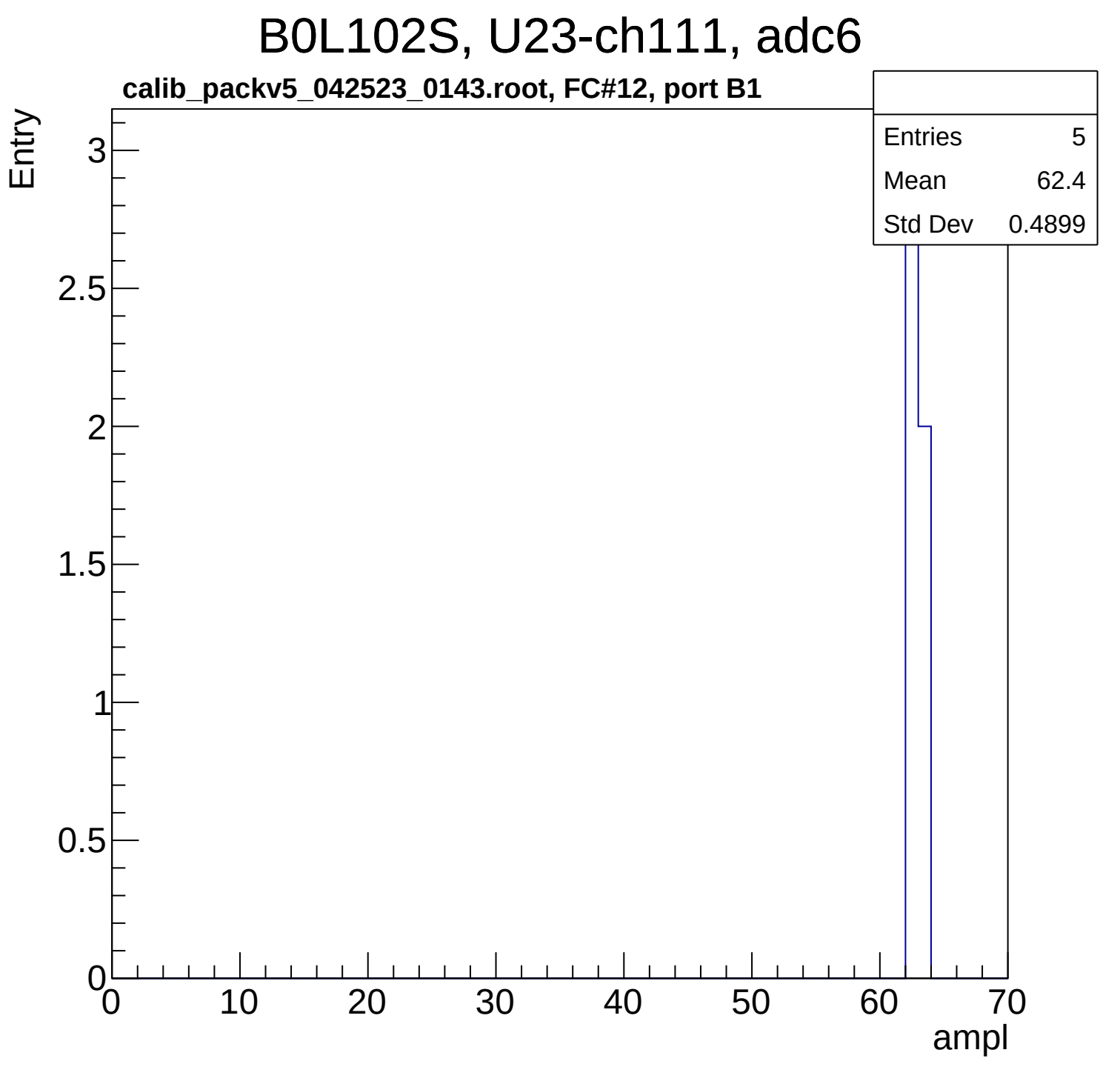

B0L102S, U23-ch111, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 60.16
Std Dev 2.666
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch111, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
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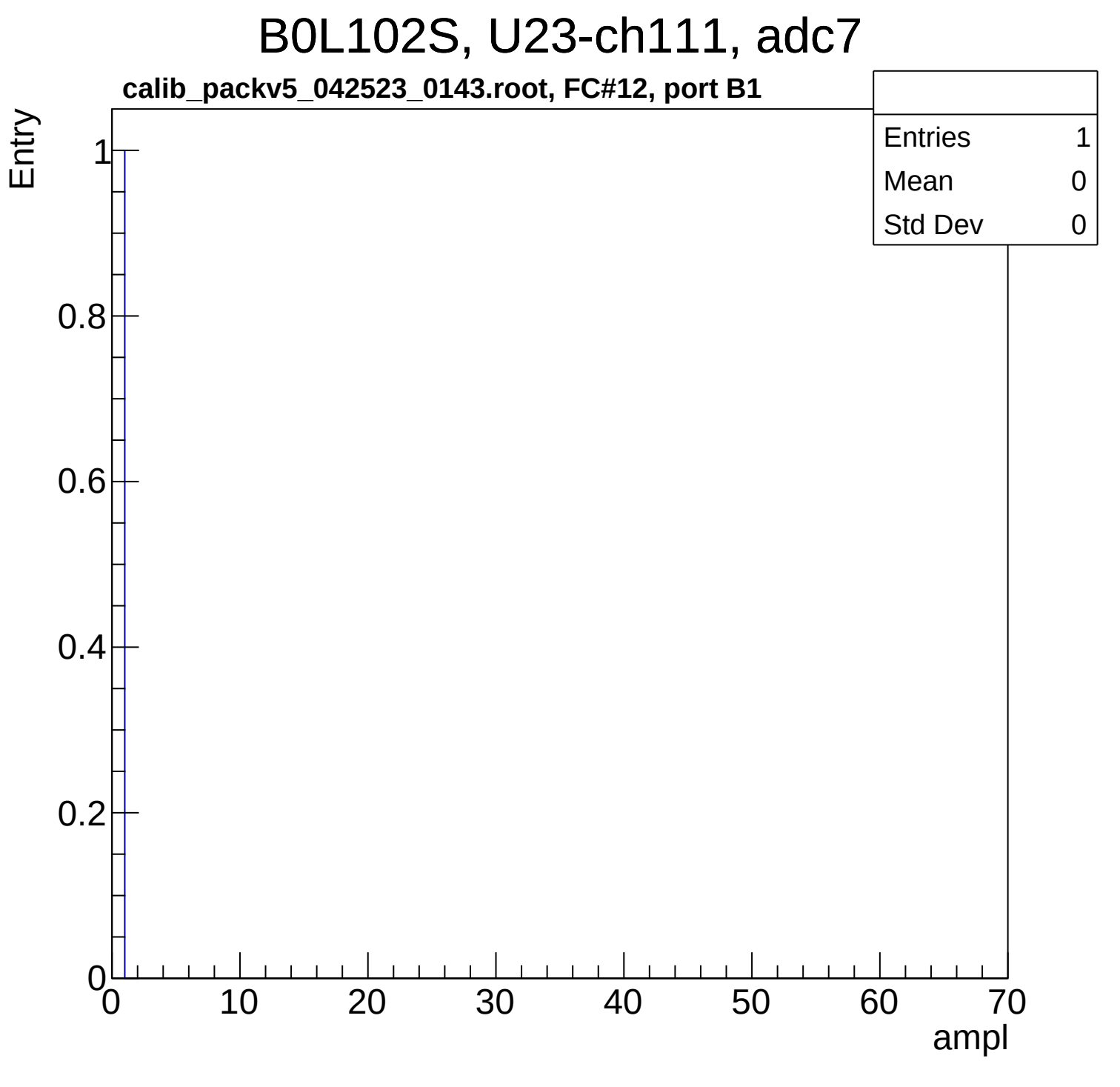
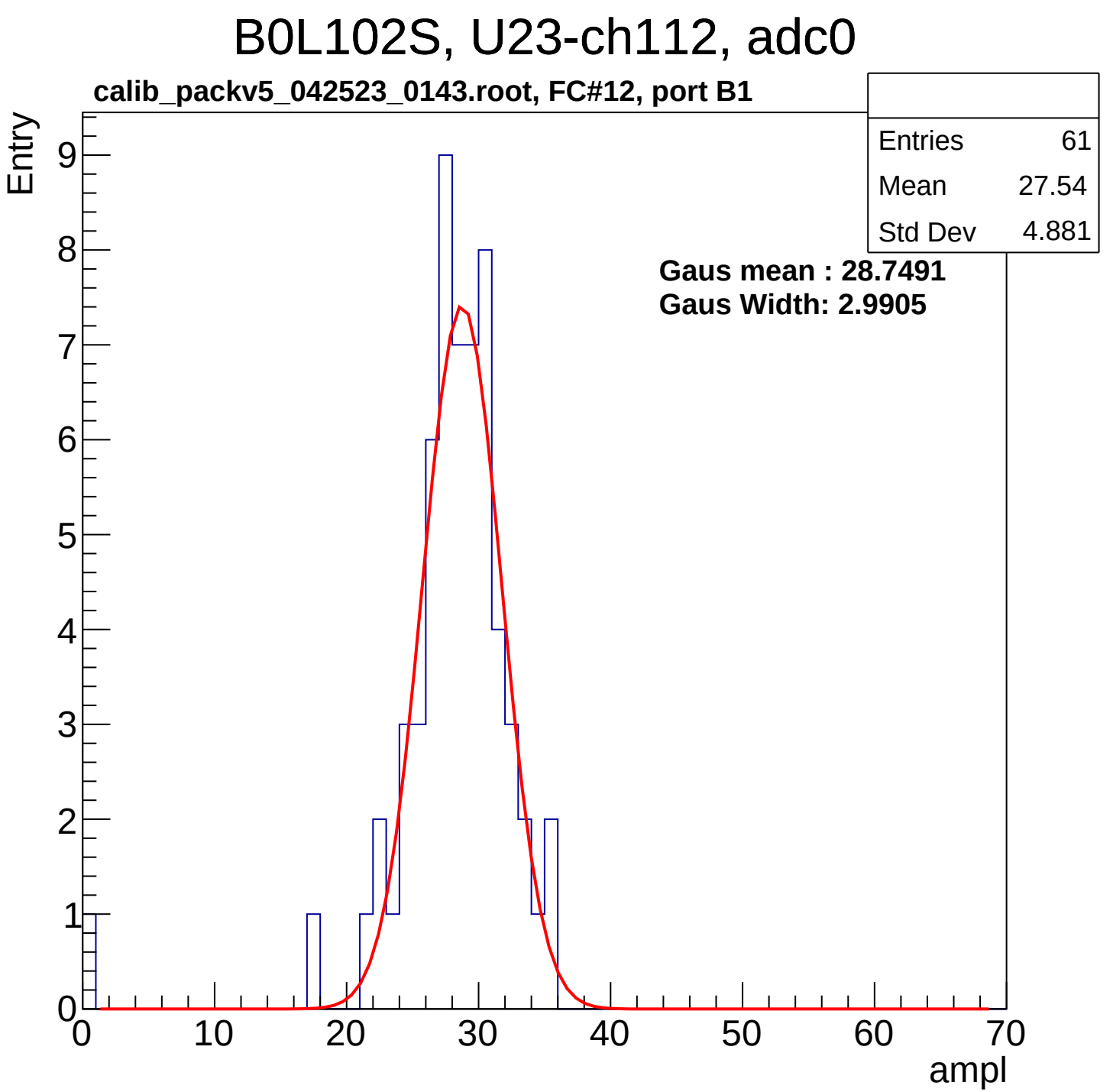
40
50
60
70


B0L102S, U23-ch111, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch112, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 27.54
Std Dev 4.881
Gaus mean : 28.7491
Gaus Width: 2.9905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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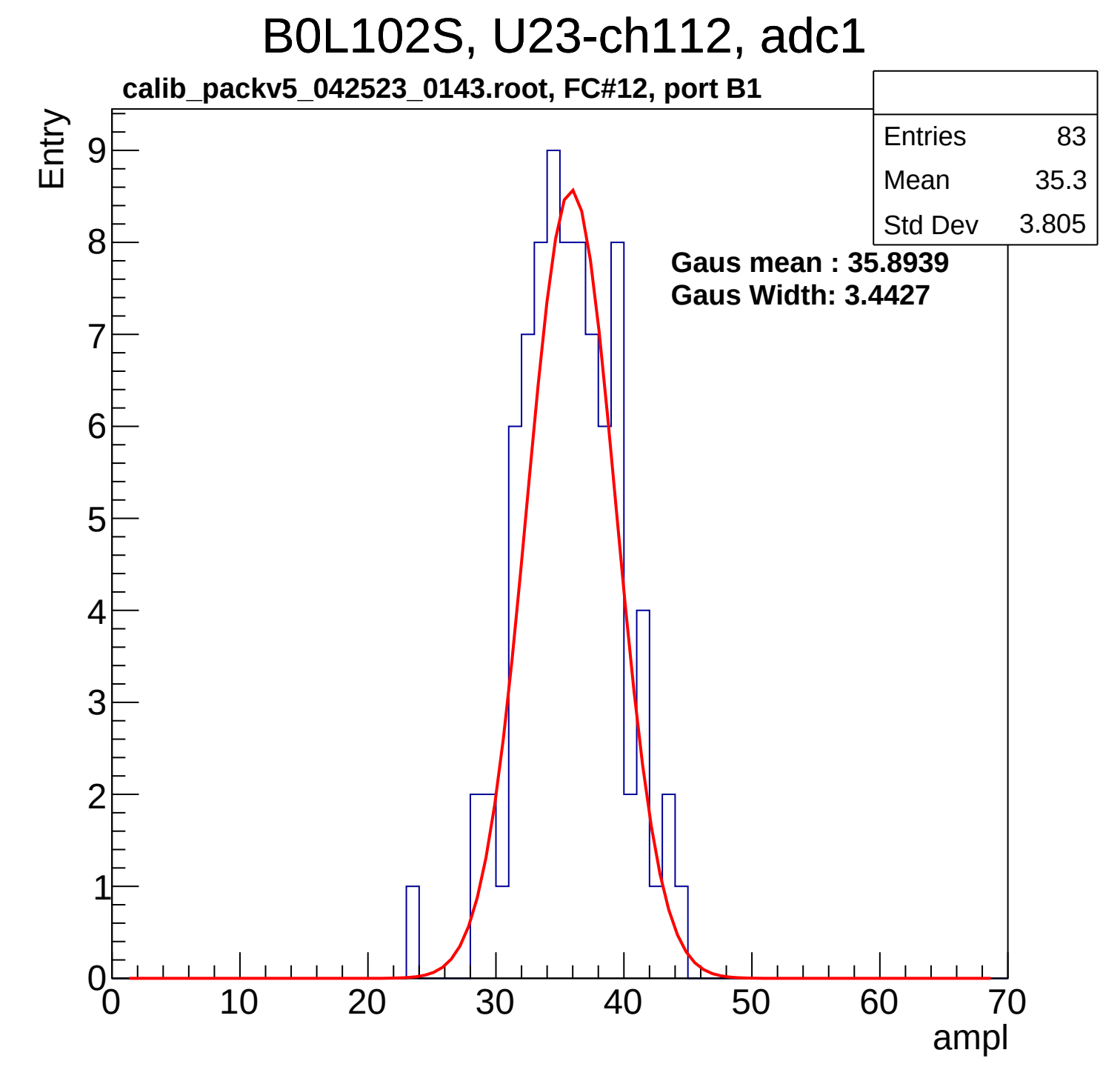
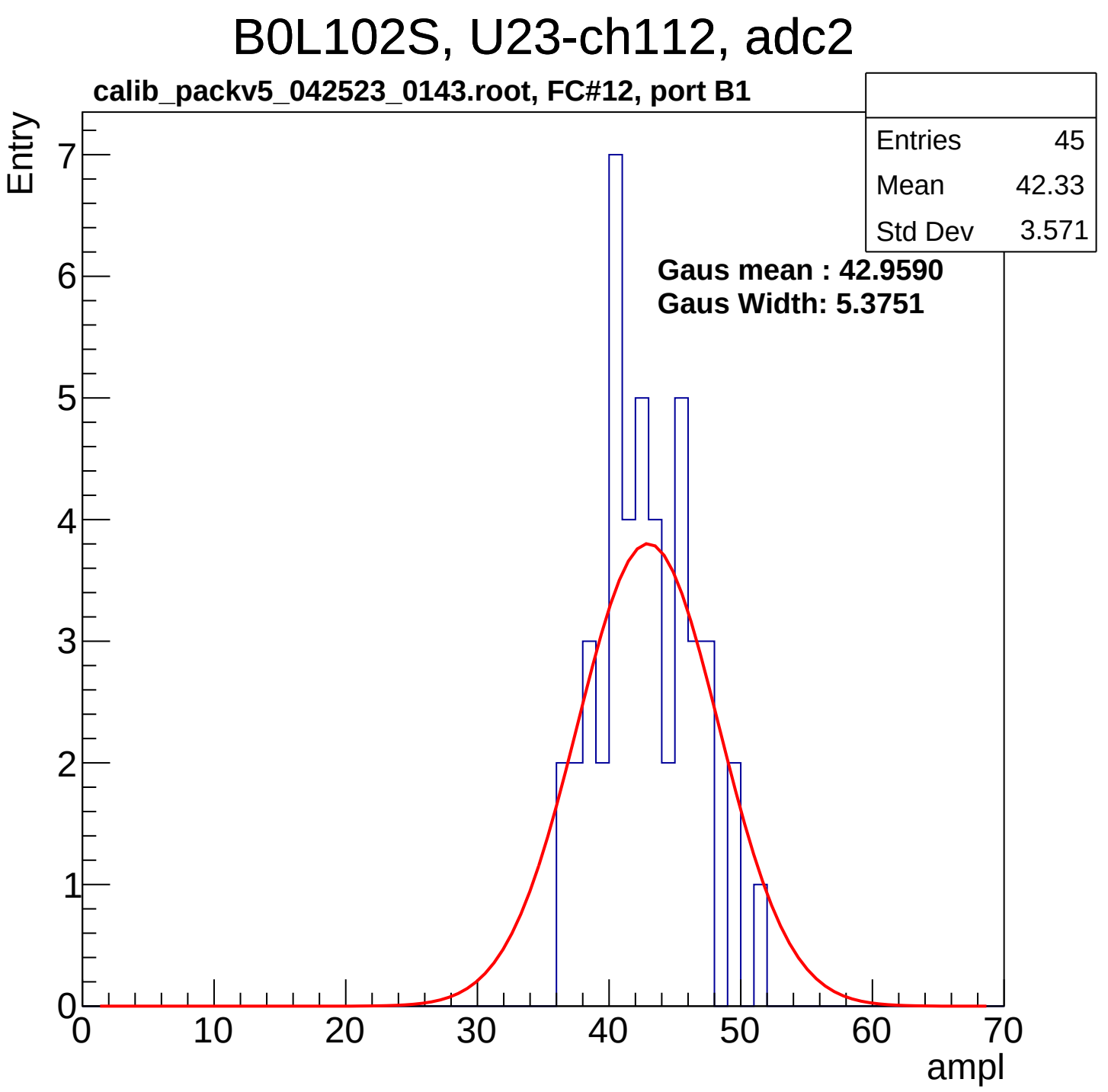
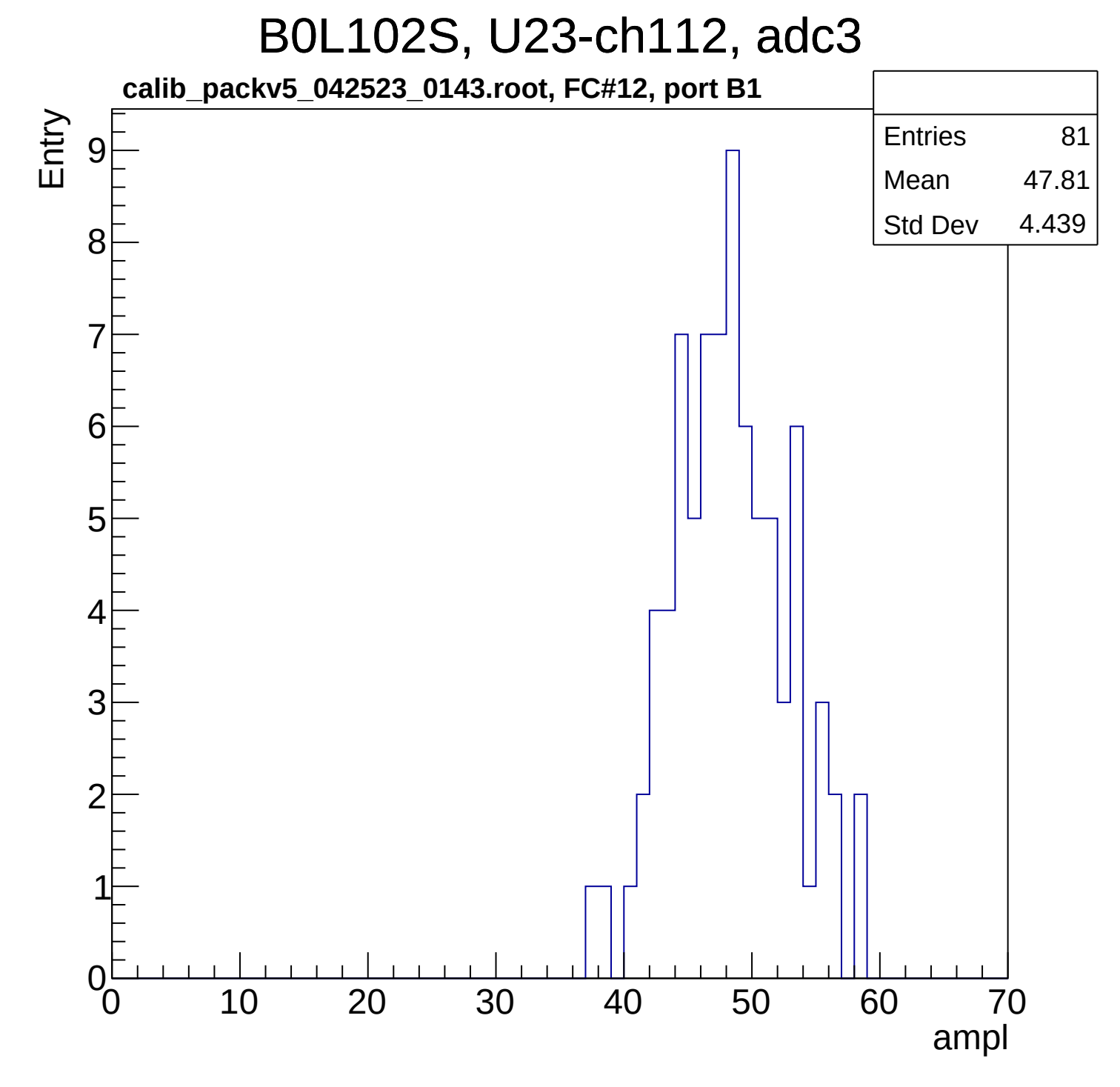

B0L102S, U23-ch112, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 83
Mean 35.3
Std Dev 3.805
Gaus mean : 35.8939
Gaus Width: 3.4427
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch112, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 42.33
Std Dev 3.571
Gaus mean : 42.9590
Gaus Width: 5.3751
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch112, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 47.81
Std Dev 4.439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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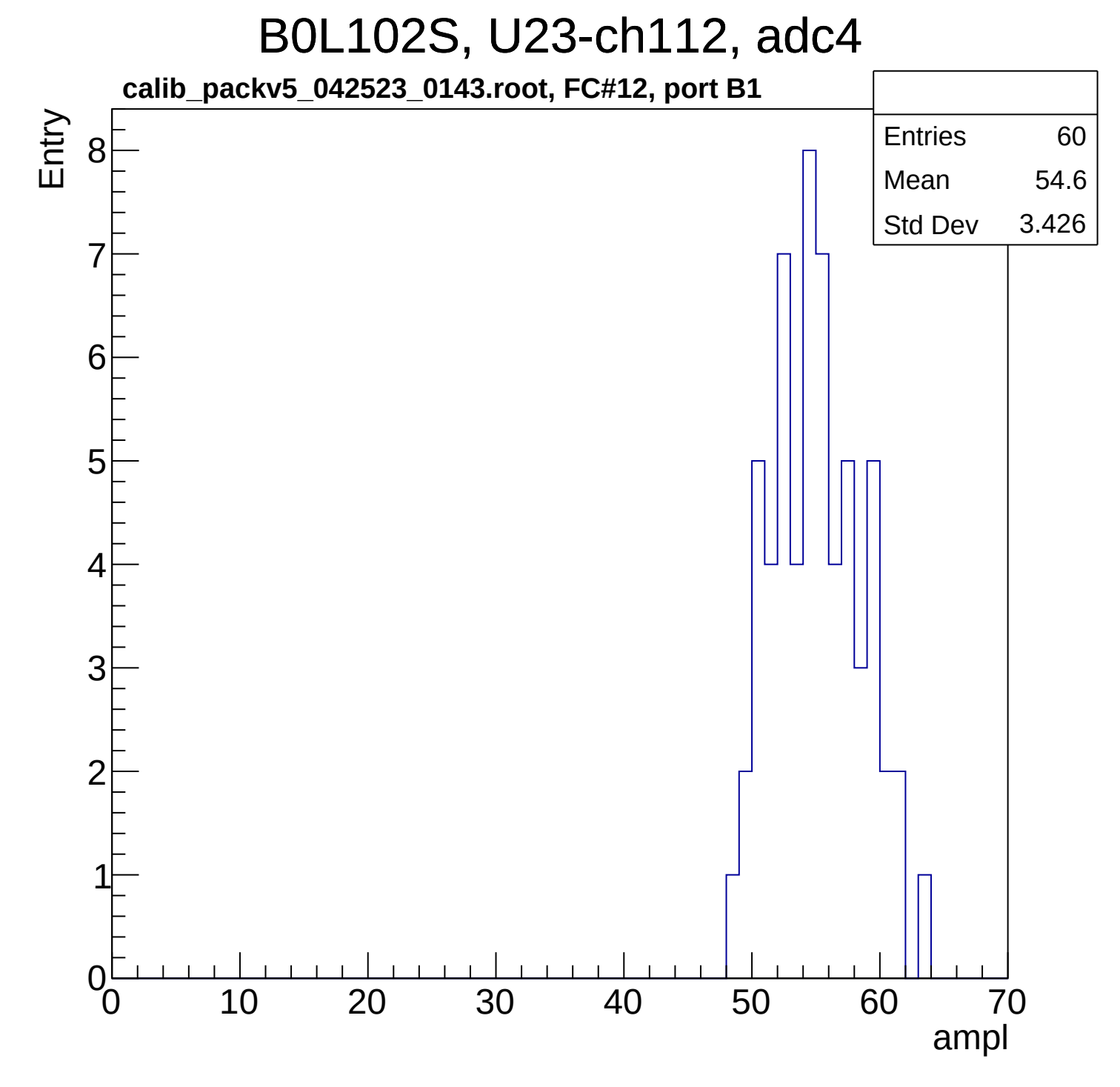
B0L102S, U23-ch112, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 54.6
Std Dev 3.426
Entry
ampl
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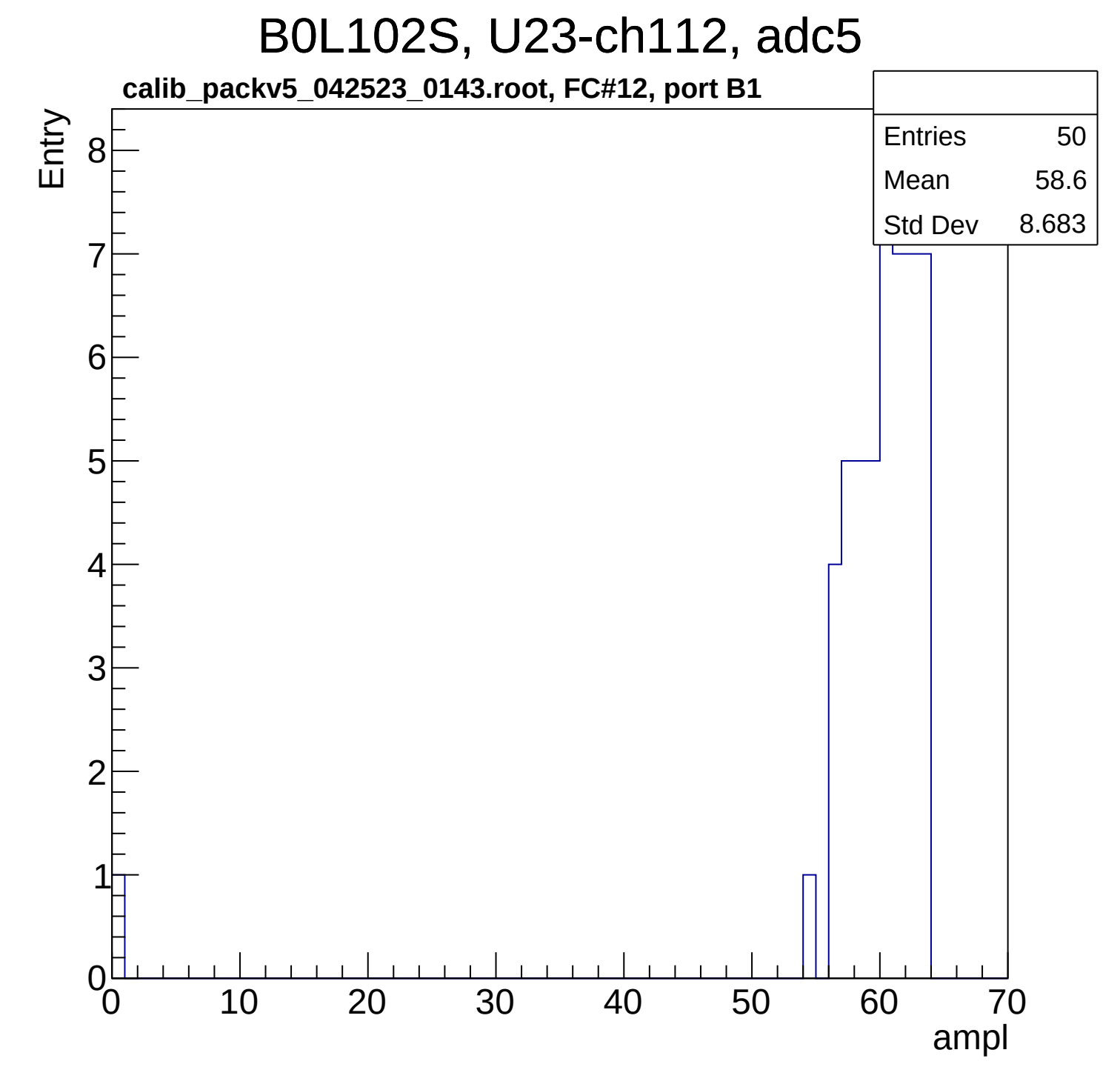
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch112, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 58.6
Std Dev 8.683
Entry
ampl
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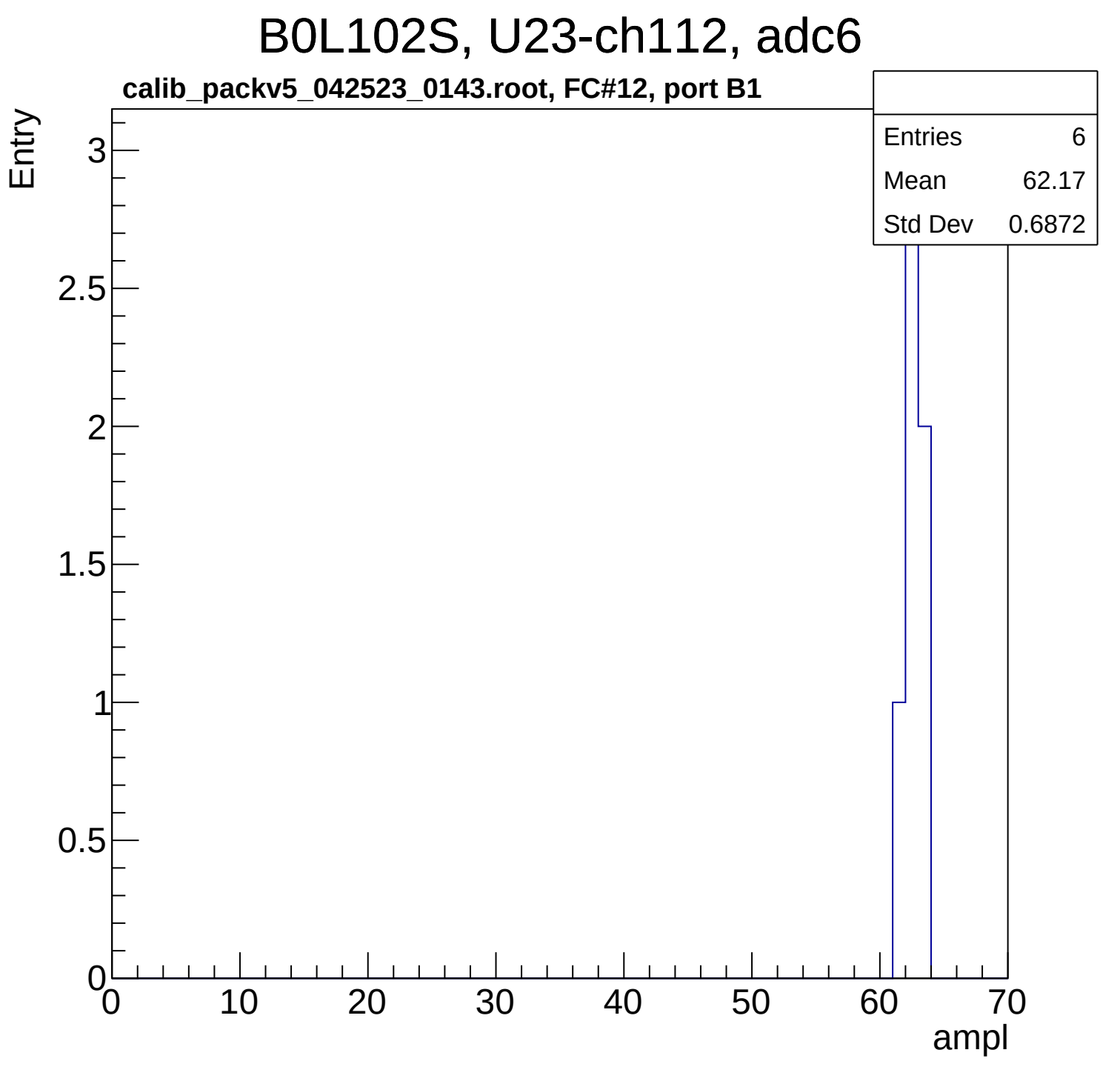
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch112, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
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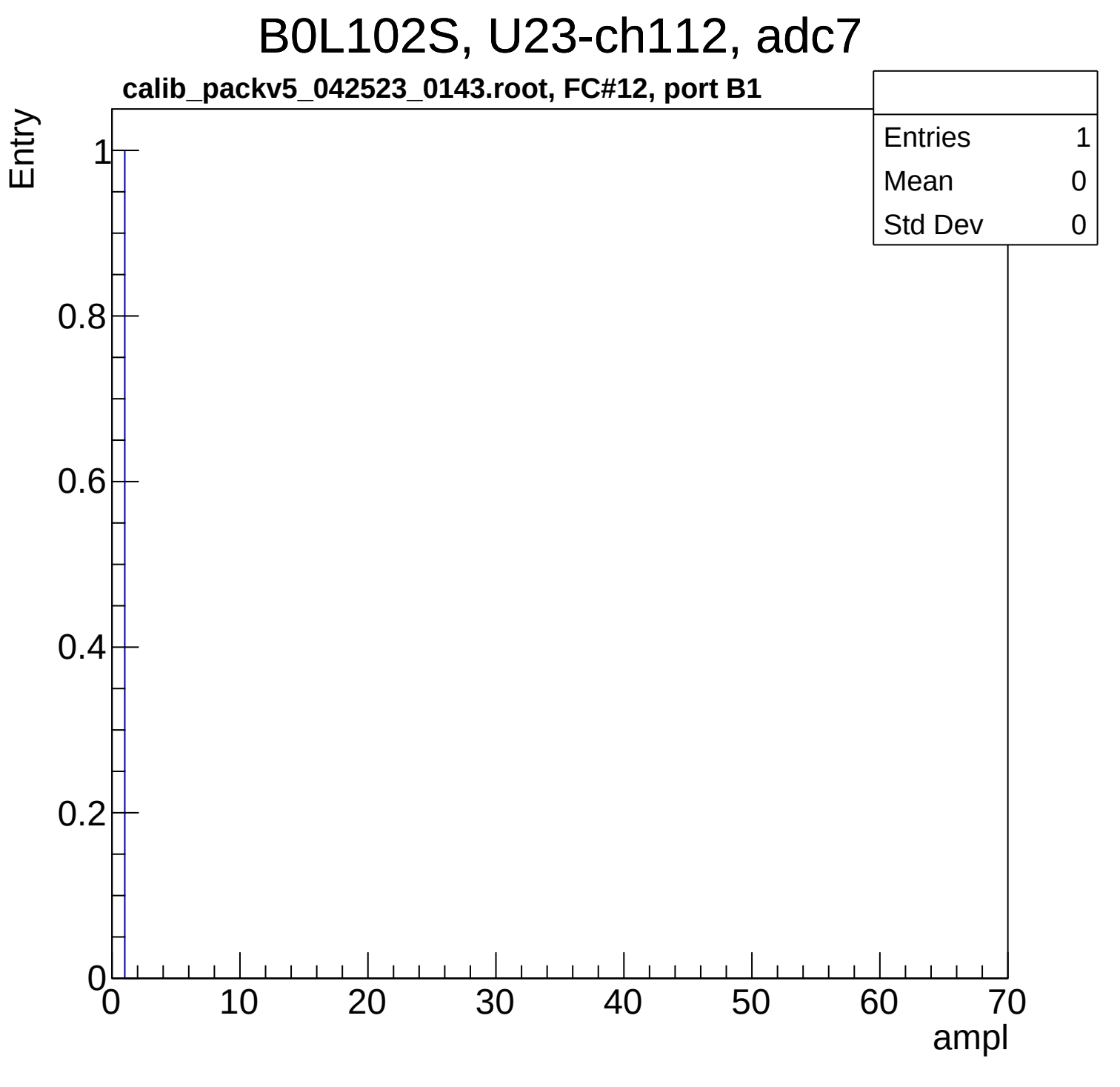
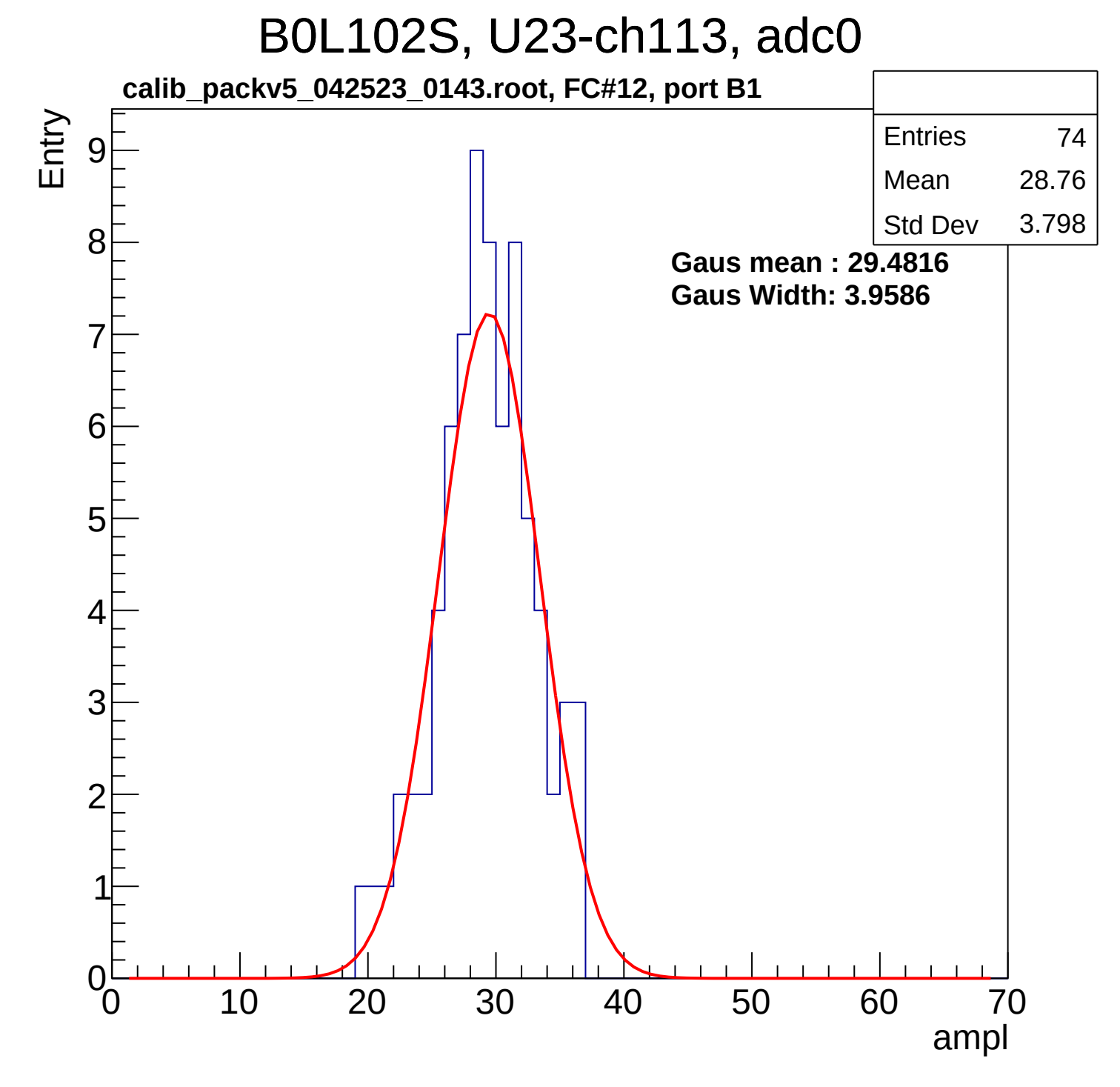
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L102S, U23-ch112, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch113, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 74
Mean 28.76
Std Dev 3.798
Gaus mean : 29.4816
Gaus Width: 3.9586
Entry
ampl
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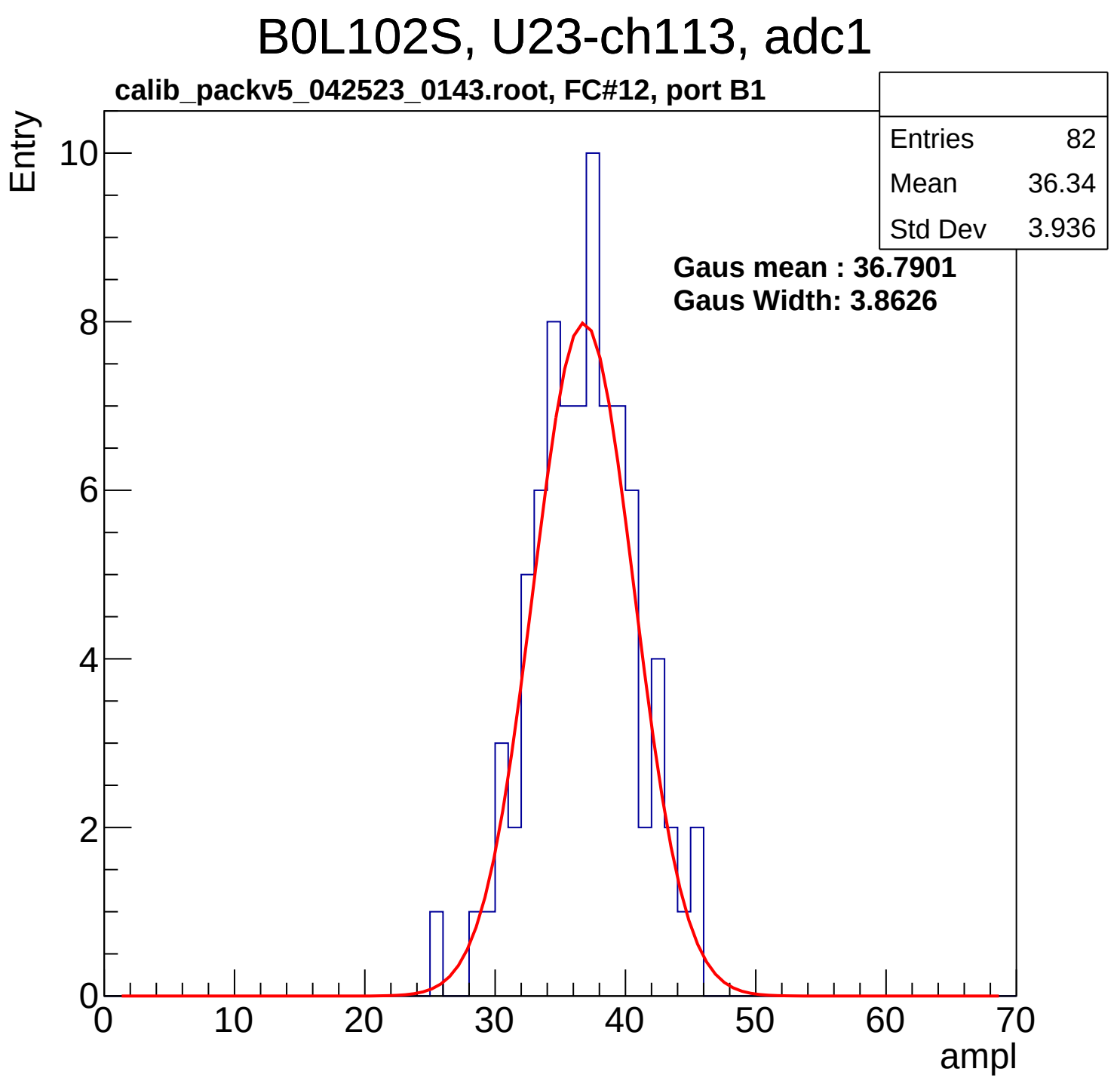
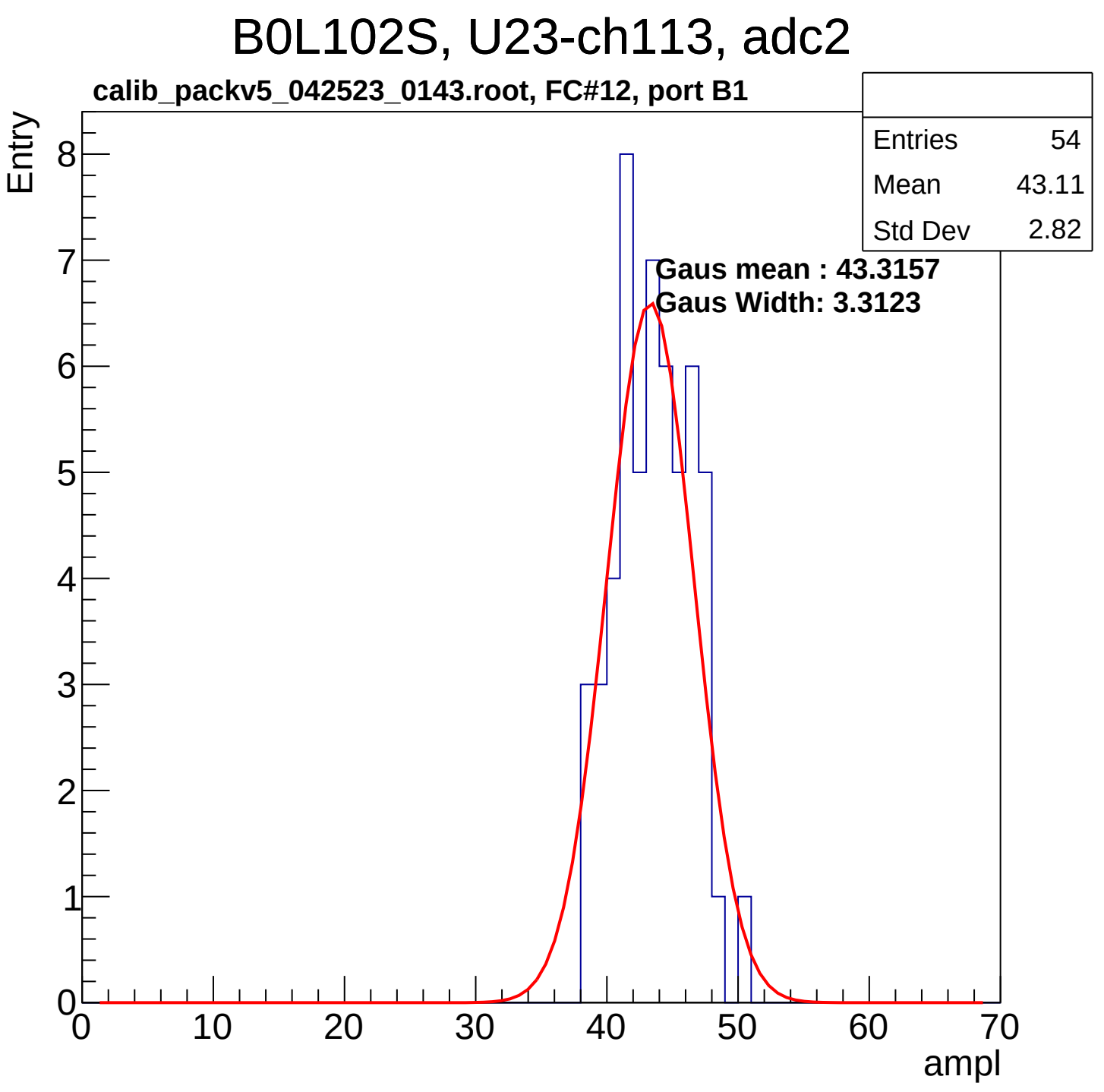
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L102S, U23-ch113, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 82
Mean 36.34
Std Dev 3.936
Gaus mean : 36.7901
Gaus Width: 3.8626
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch113, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 43.11
Std Dev 2.82
Gaus mean : 43.3157
Gaus Width: 3.3123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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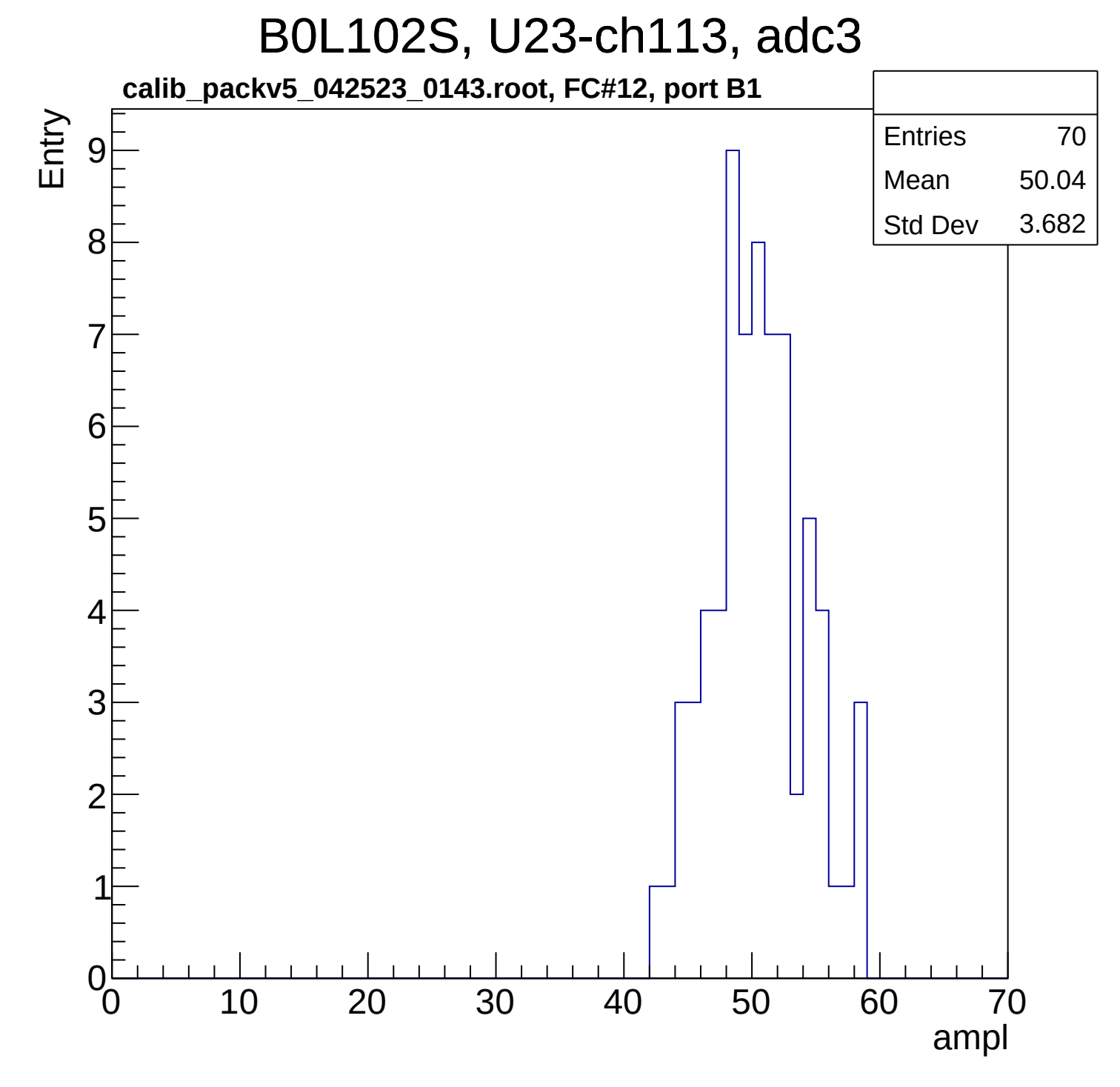
B0L102S, U23-ch113, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 50.04
Std Dev 3.682
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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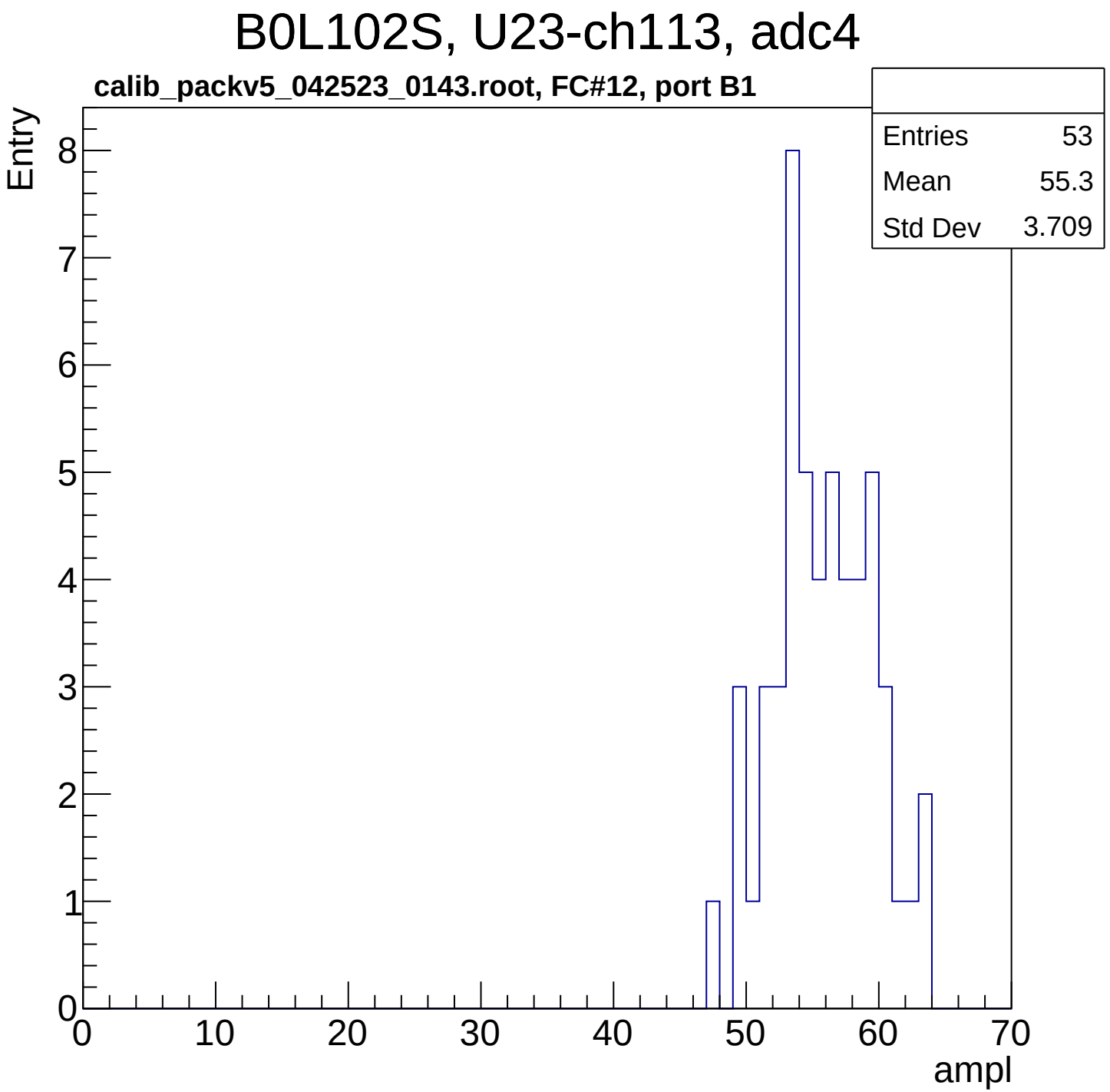
B0L102S, U23-ch113, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 55.3
Std Dev 3.709
Entry
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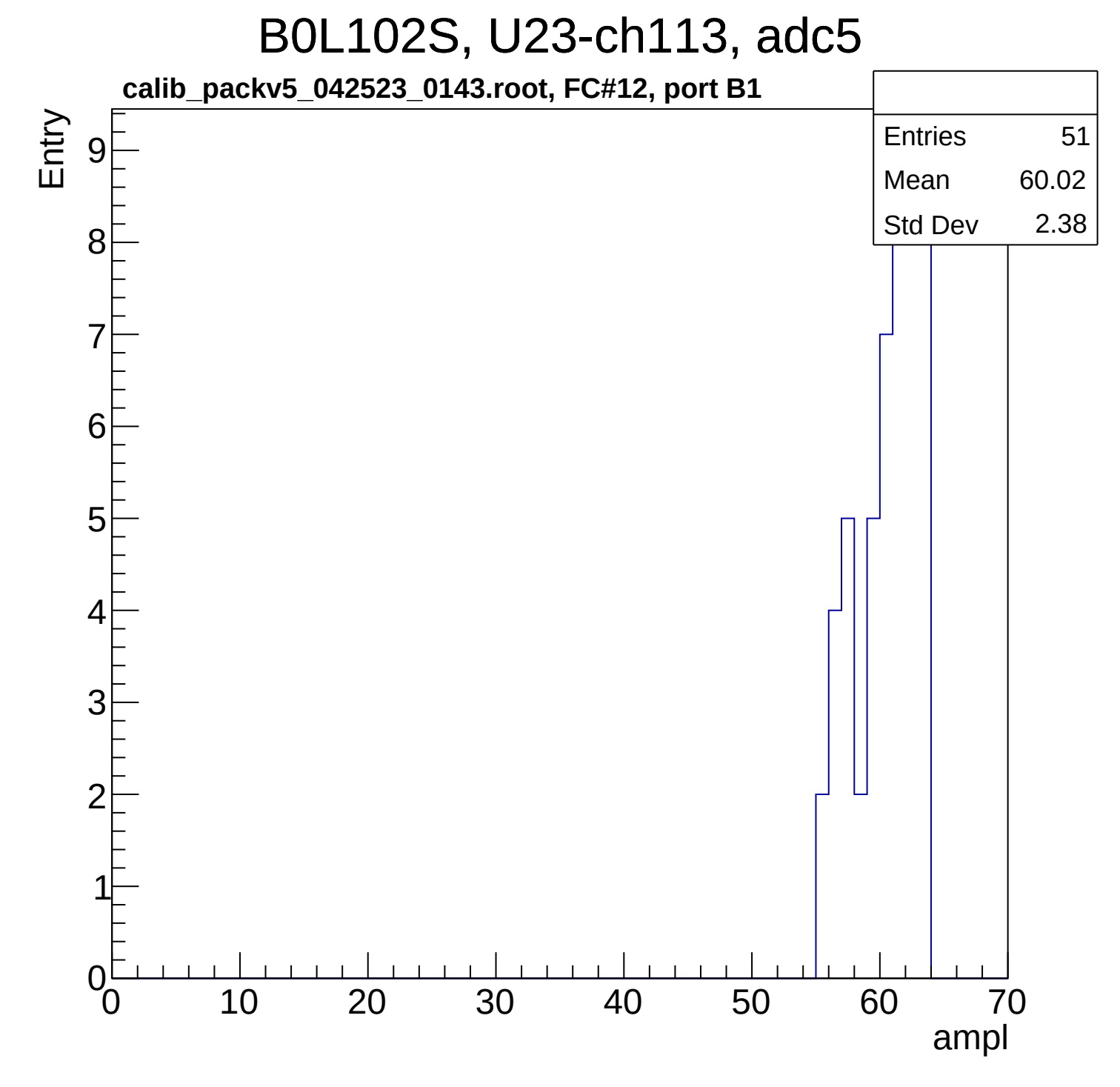
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch113, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 51
Mean 60.02
Std Dev 2.38
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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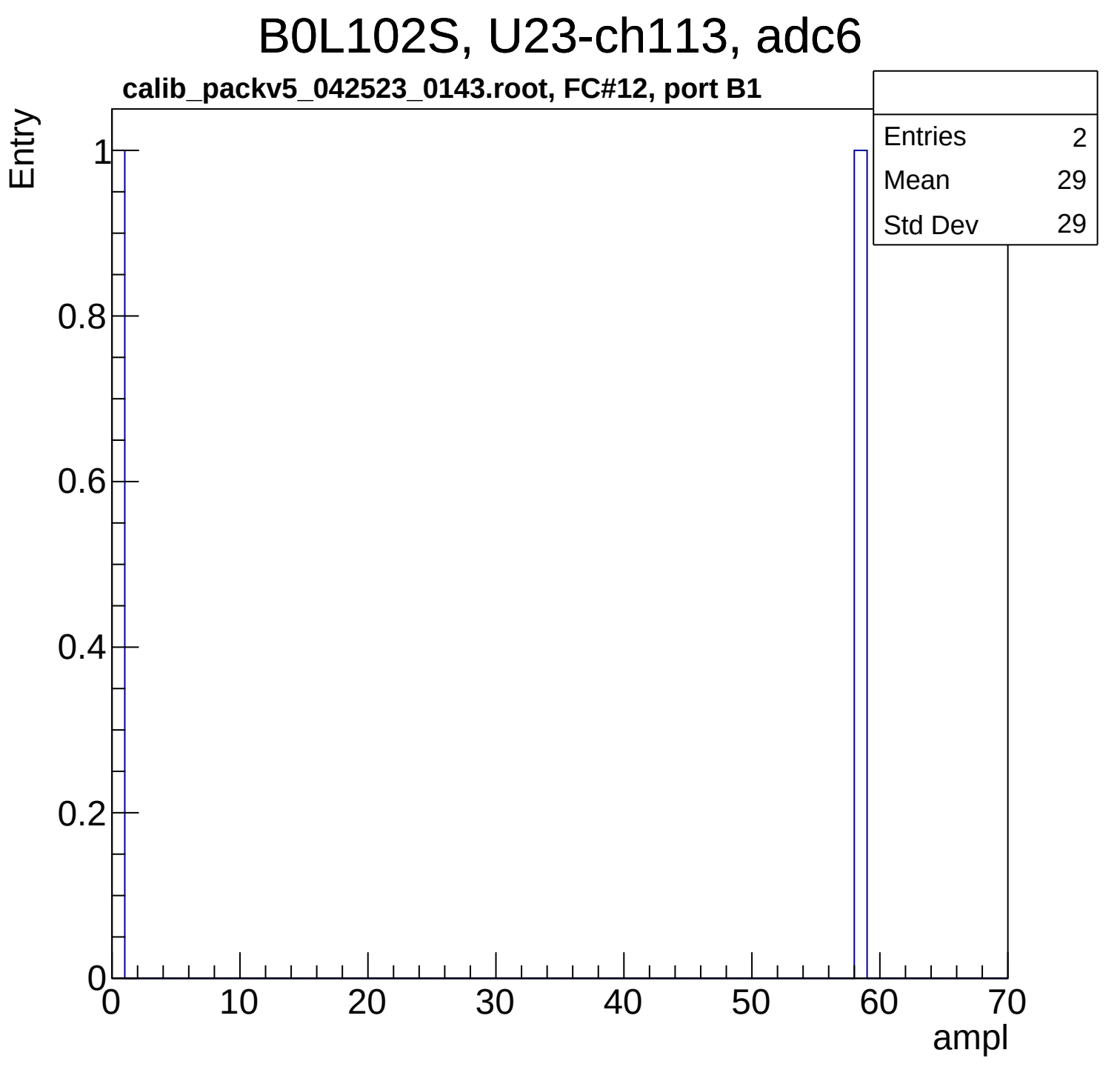
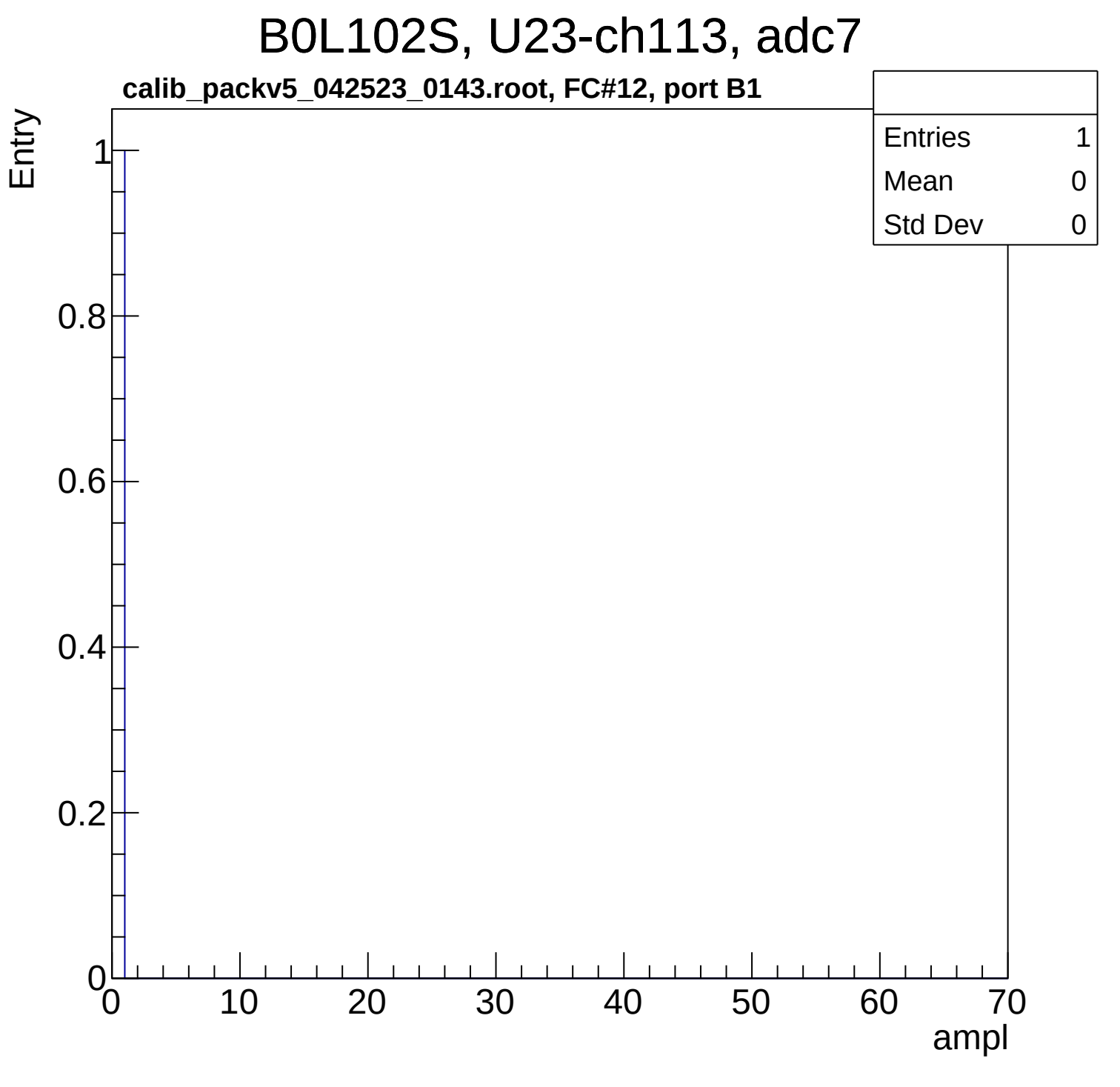
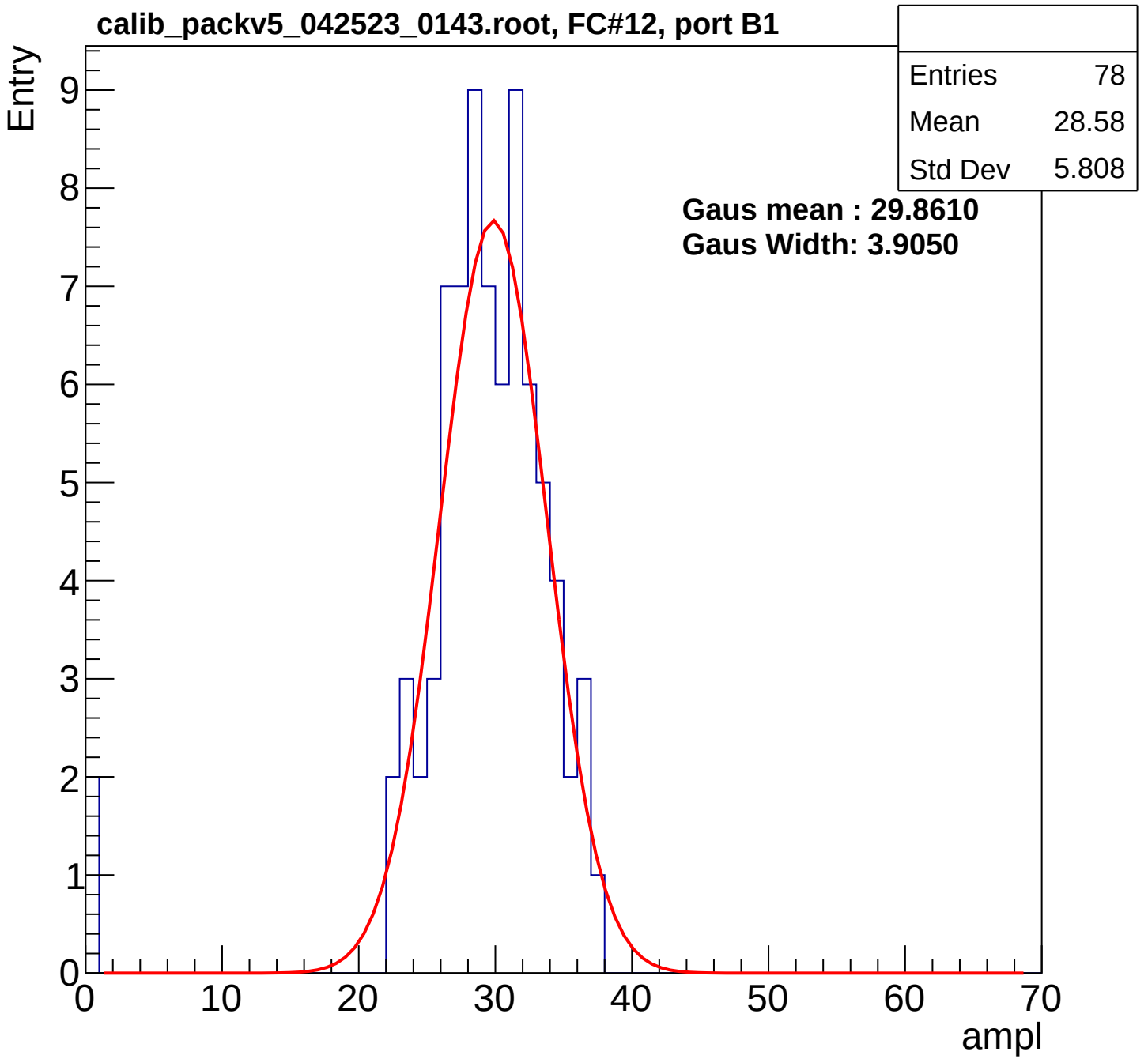
40
50
60
70


B0L102S, U23-ch113, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 29
Std Dev 29
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch113, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch114, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 78
Mean 28.58
Std Dev 5.808
Gaus mean : 29.8610
Gaus Width: 3.9050
Entry
ampl
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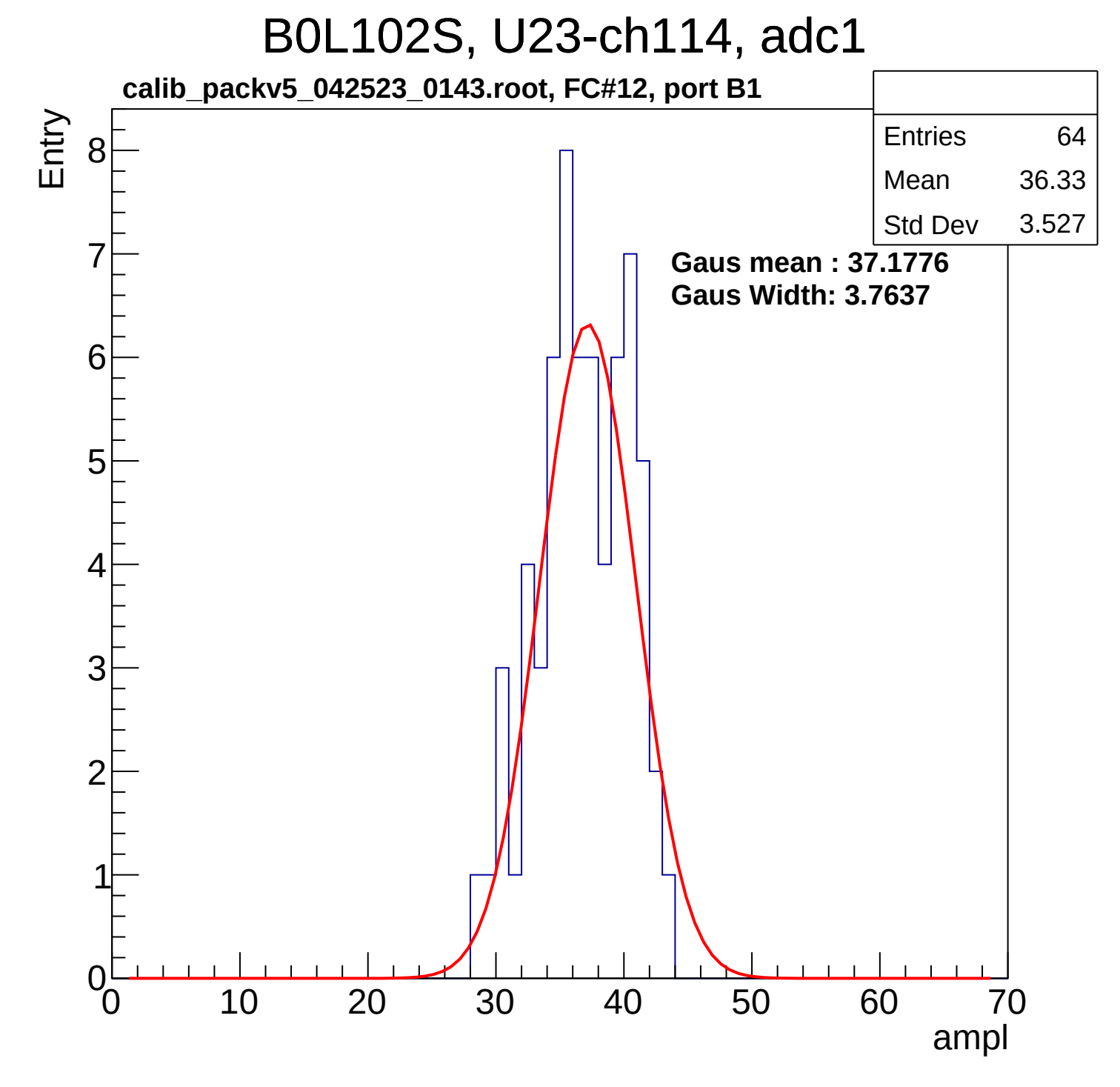
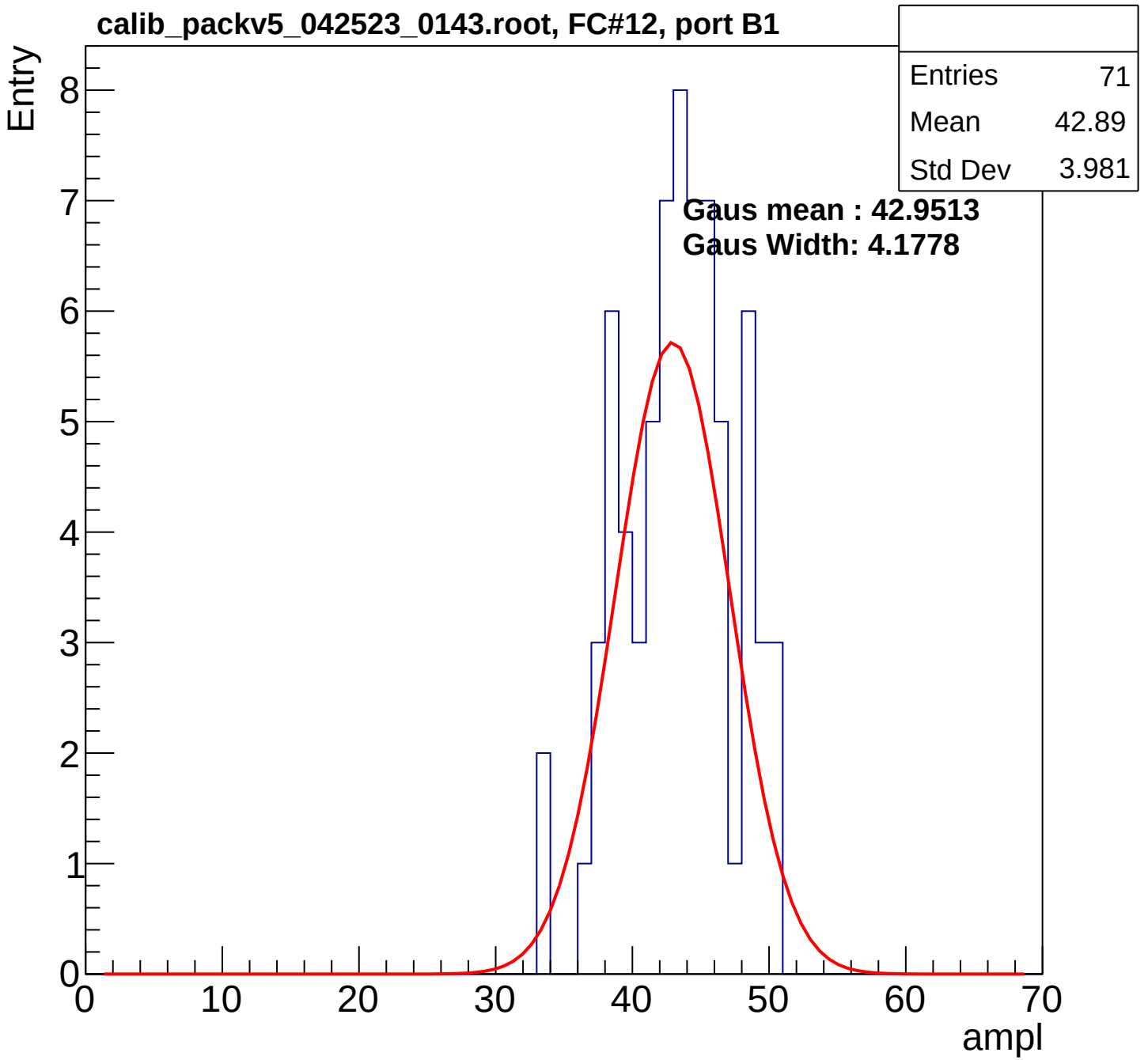
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch114, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 36.33
Std Dev 3.527
Gaus mean : 37.1776
Gaus Width: 3.7637
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch114, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 42.89
Std Dev 3.981
Gaus mean : 42.9513
Gaus Width: 4.1778
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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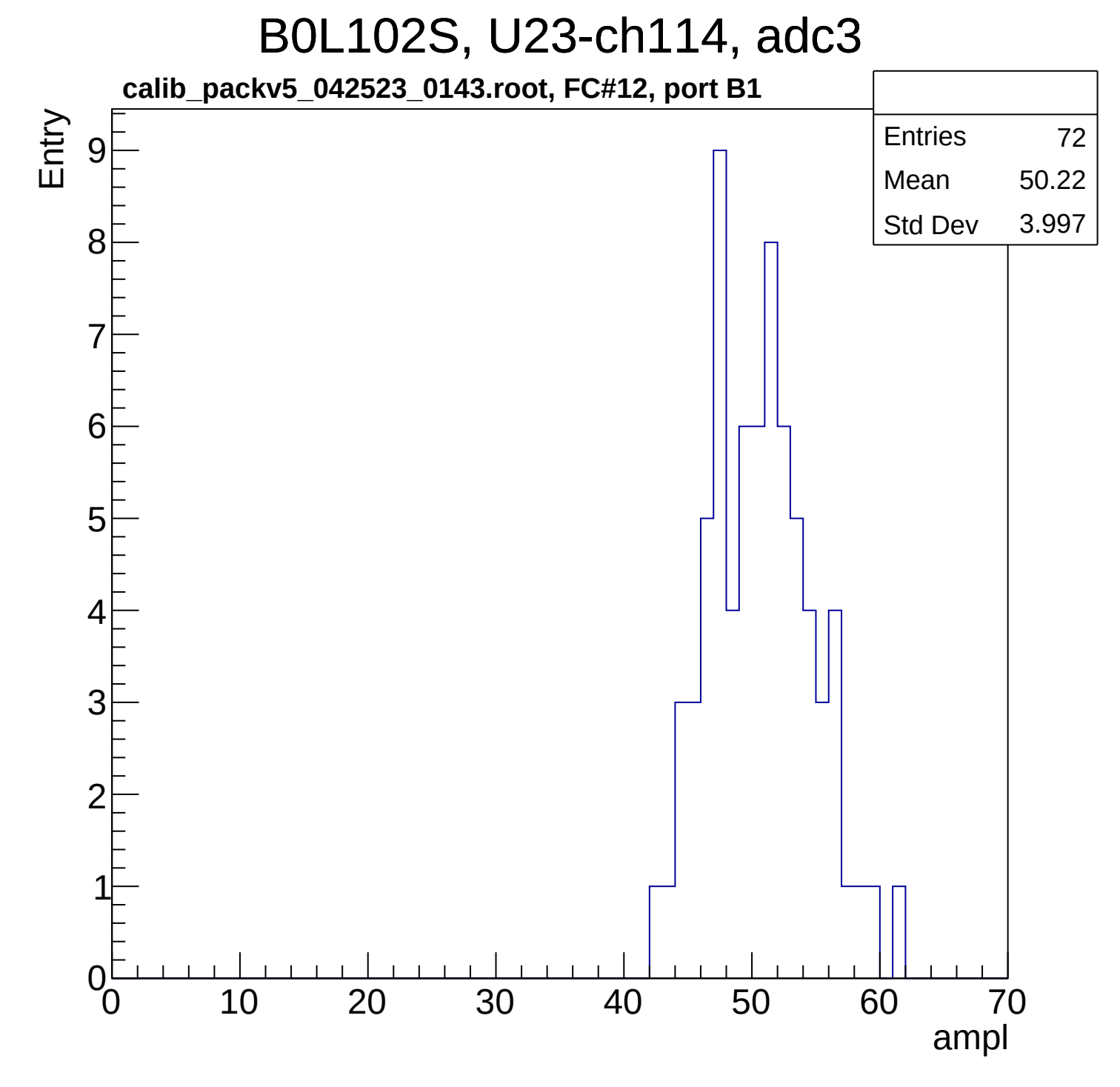
40
50
60
70


B0L102S, U23-ch114, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 50.22
Std Dev 3.997
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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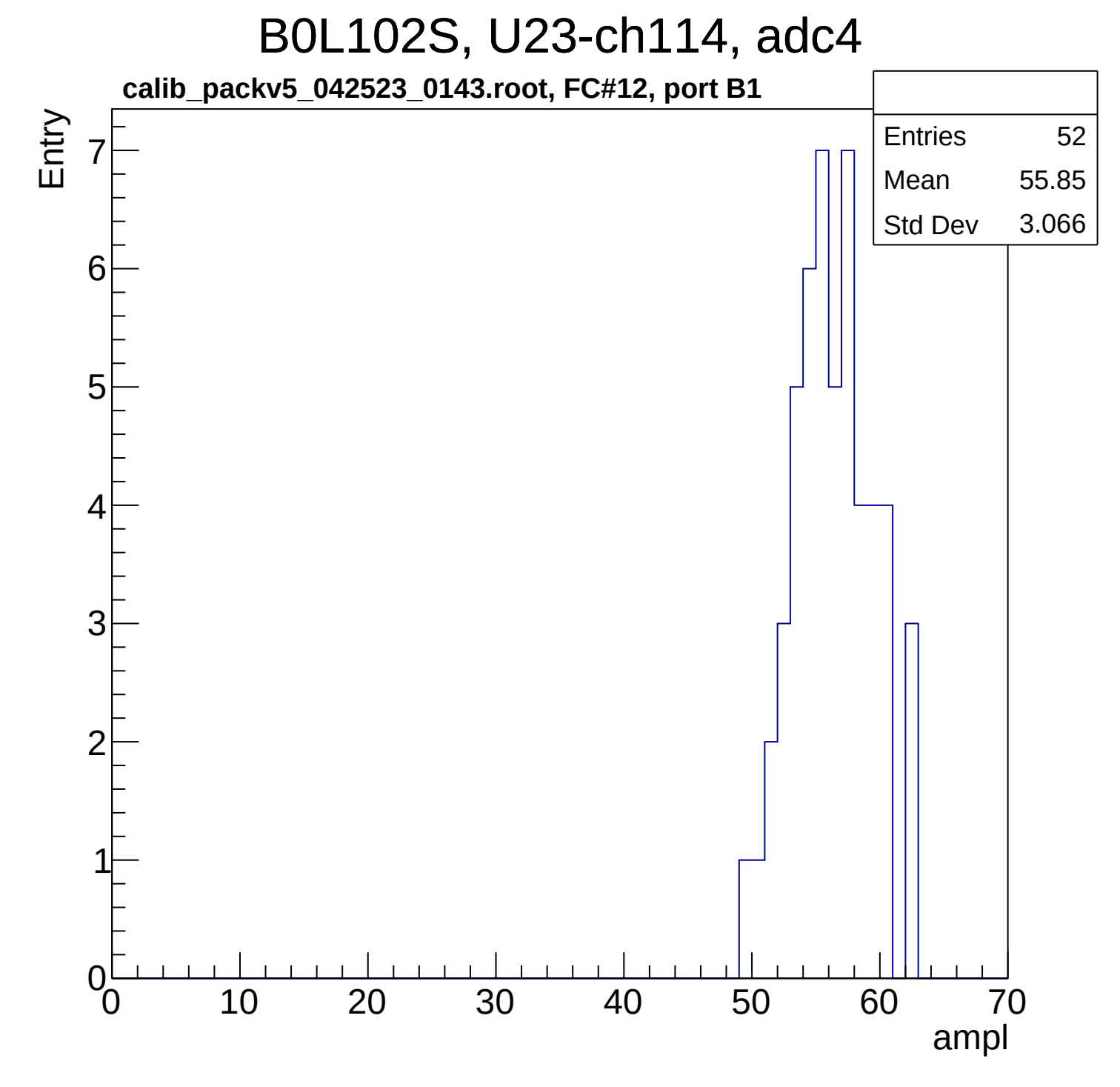
B0L102S, U23-ch114, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 55.85
Std Dev 3.066
Entry
ampl
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0
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60
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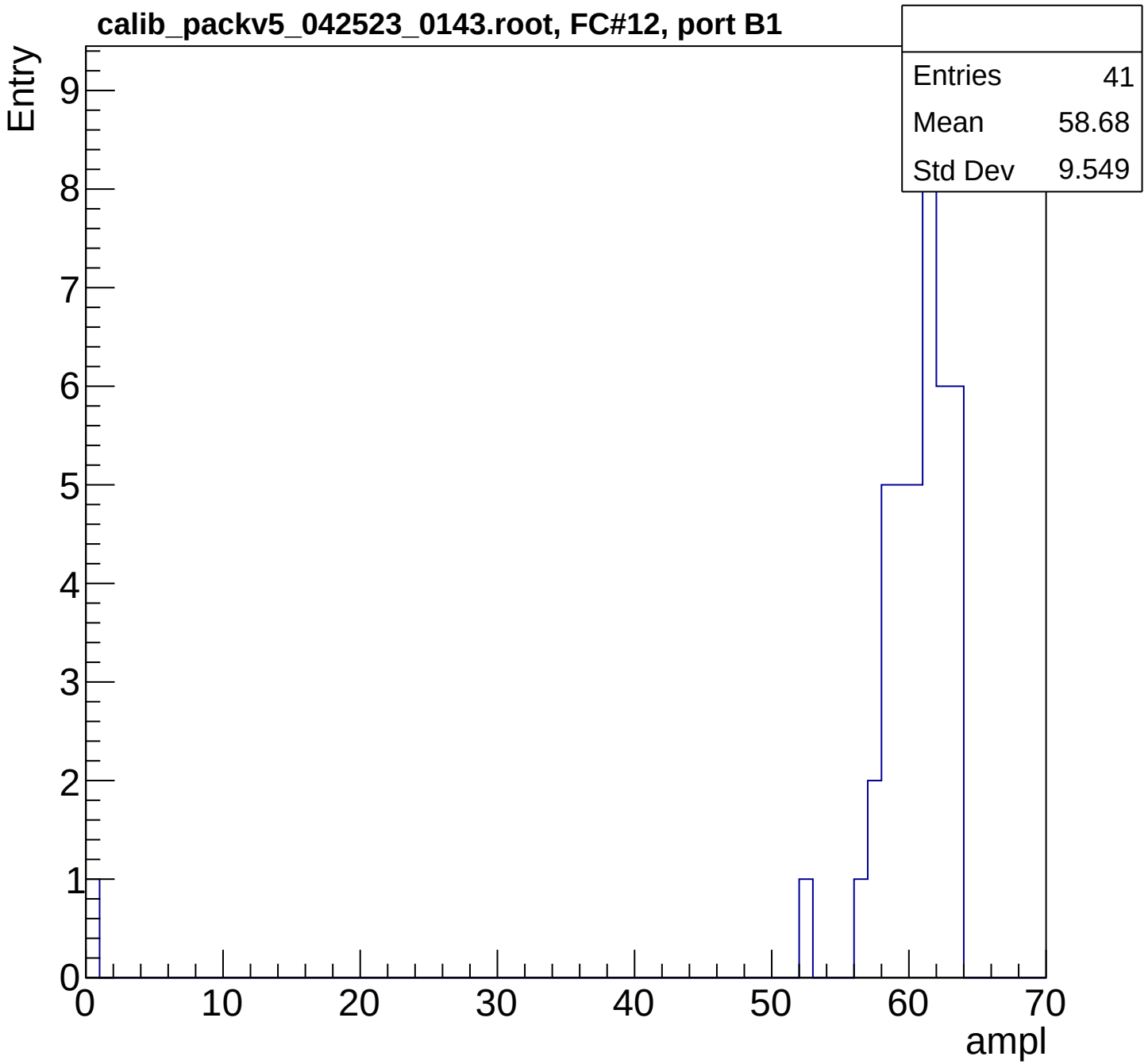
B0L102S, U23-ch114, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.68
Std Dev 9.549
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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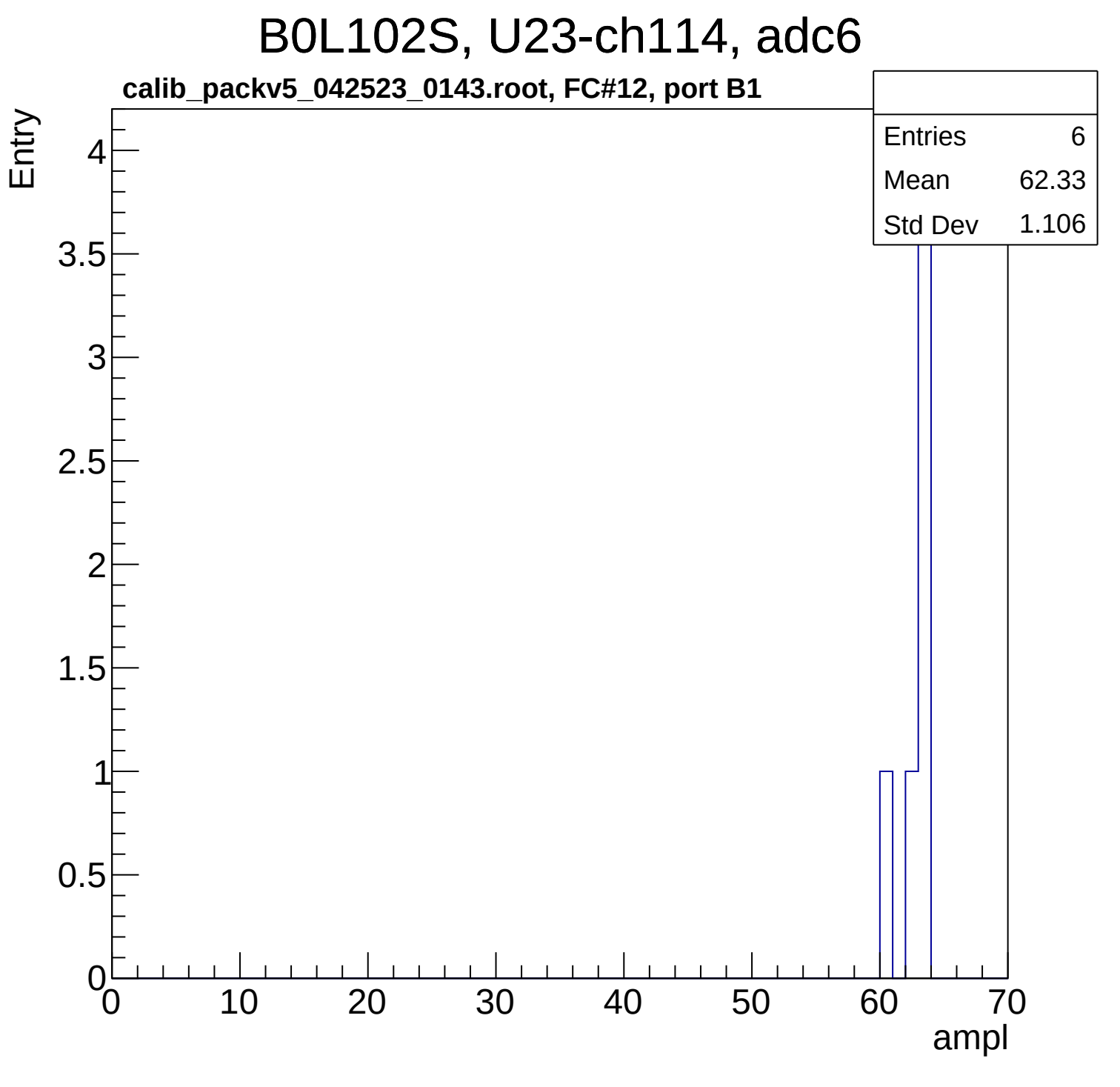
B0L102S, U23-ch114, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 6
Mean 62.33
Std Dev 1.106
Entry
ampl
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1.5
2
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3.5
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0
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60
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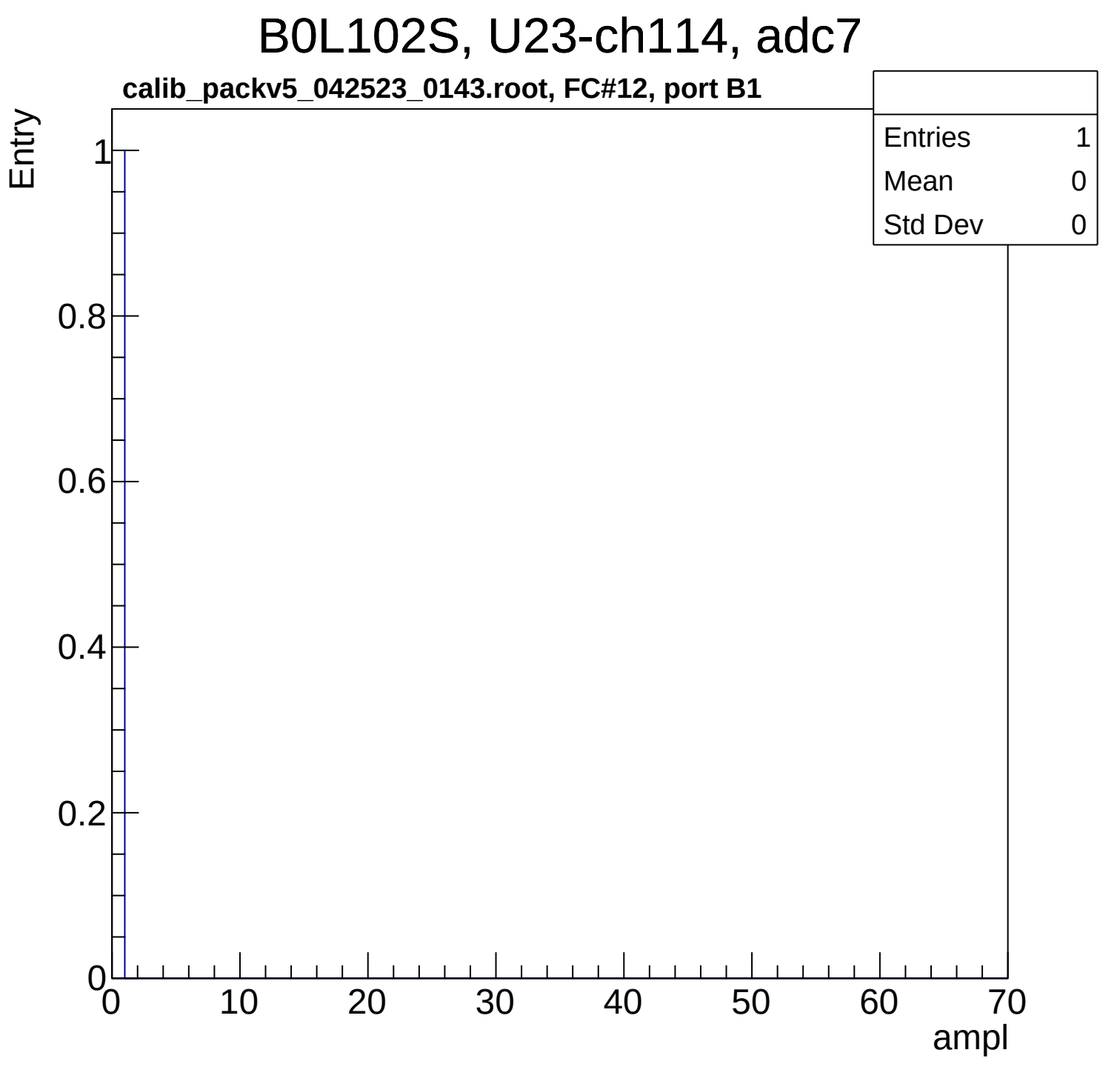
B0L102S, U23-ch114, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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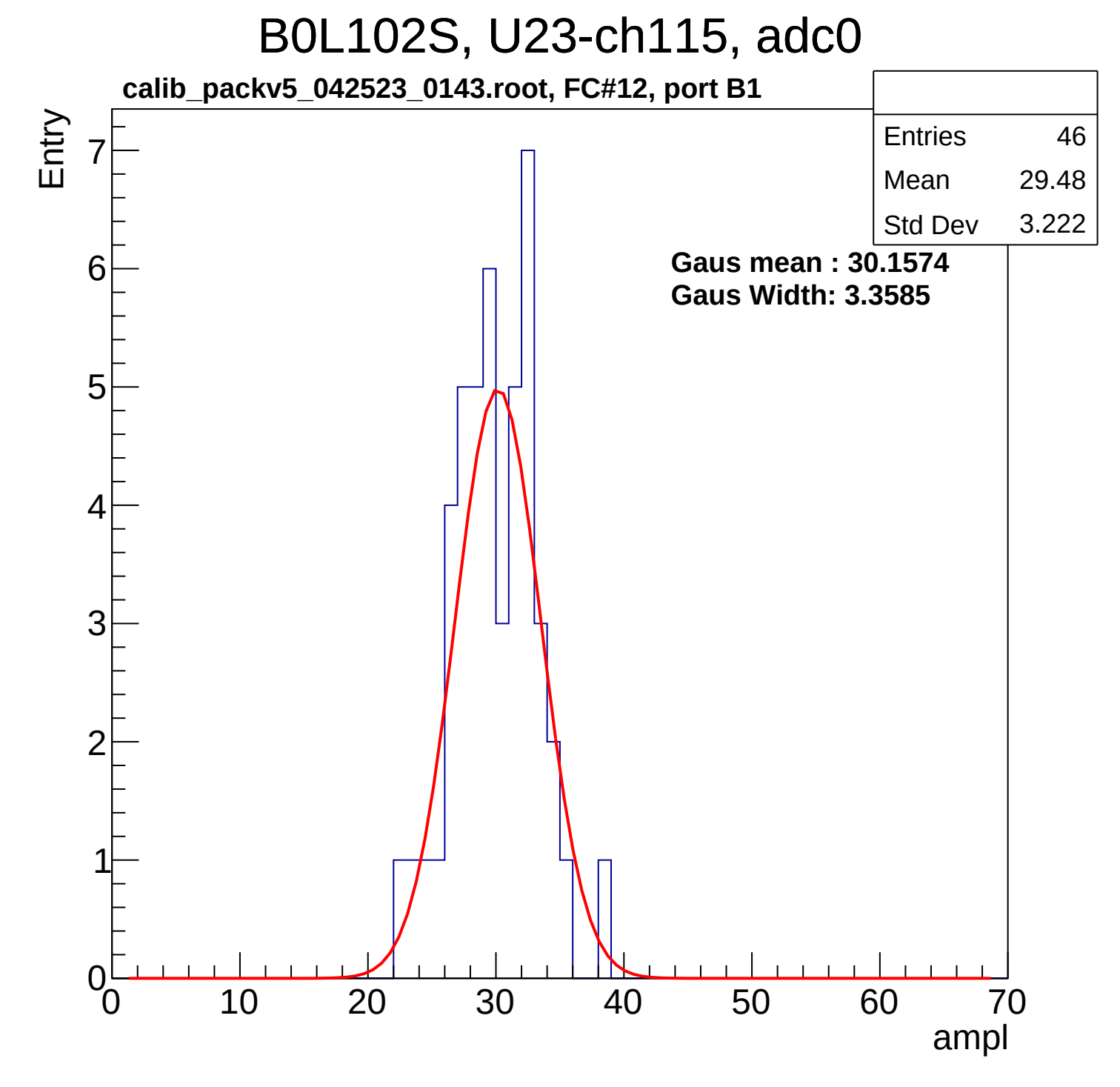
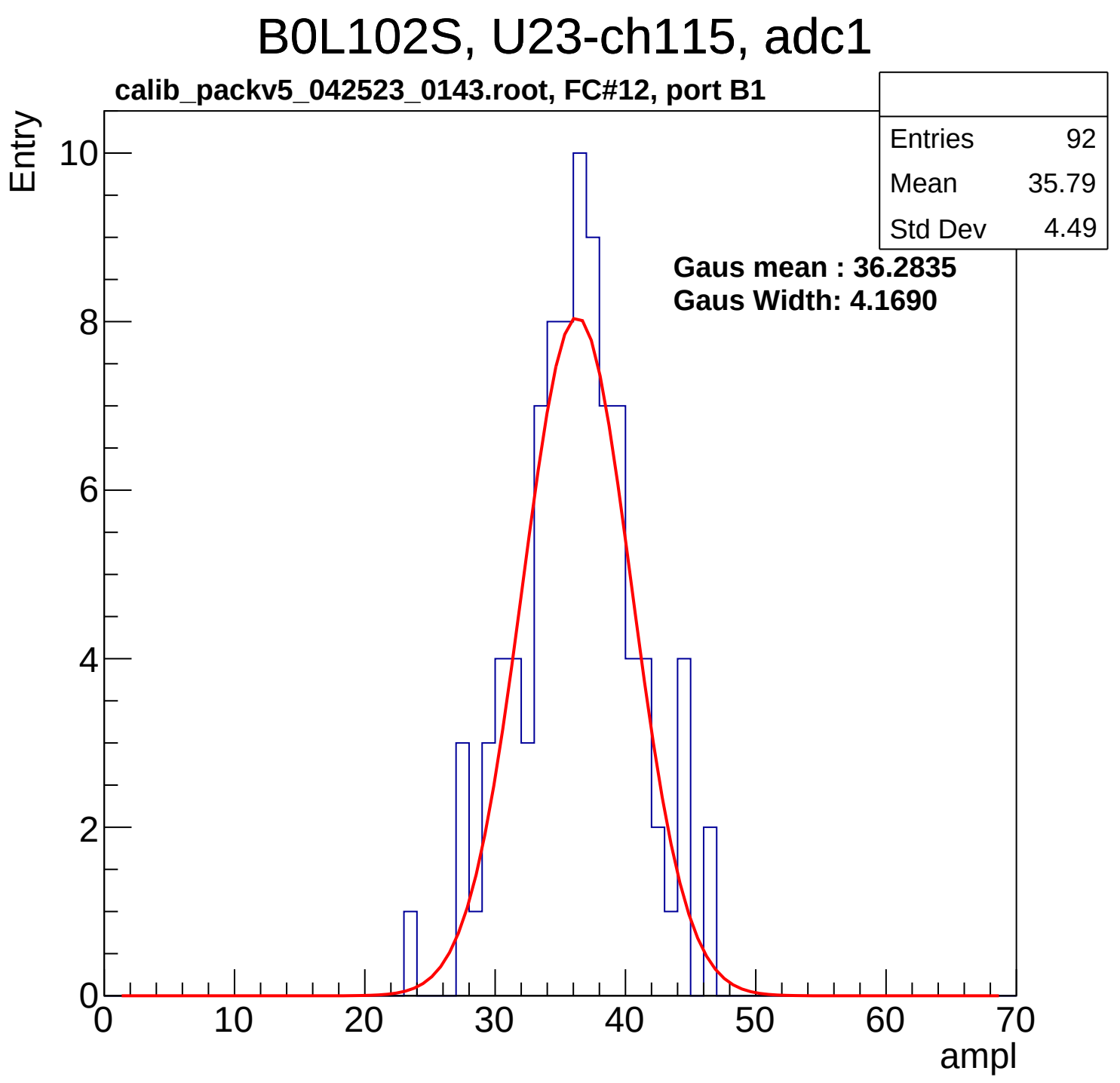
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch115, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 29.48
Std Dev 3.222
Gaus mean : 30.1574
Gaus Width: 3.3585
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch115, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 92
Mean 35.79
Std Dev 4.49
Gaus mean : 36.2835
Gaus Width: 4.1690
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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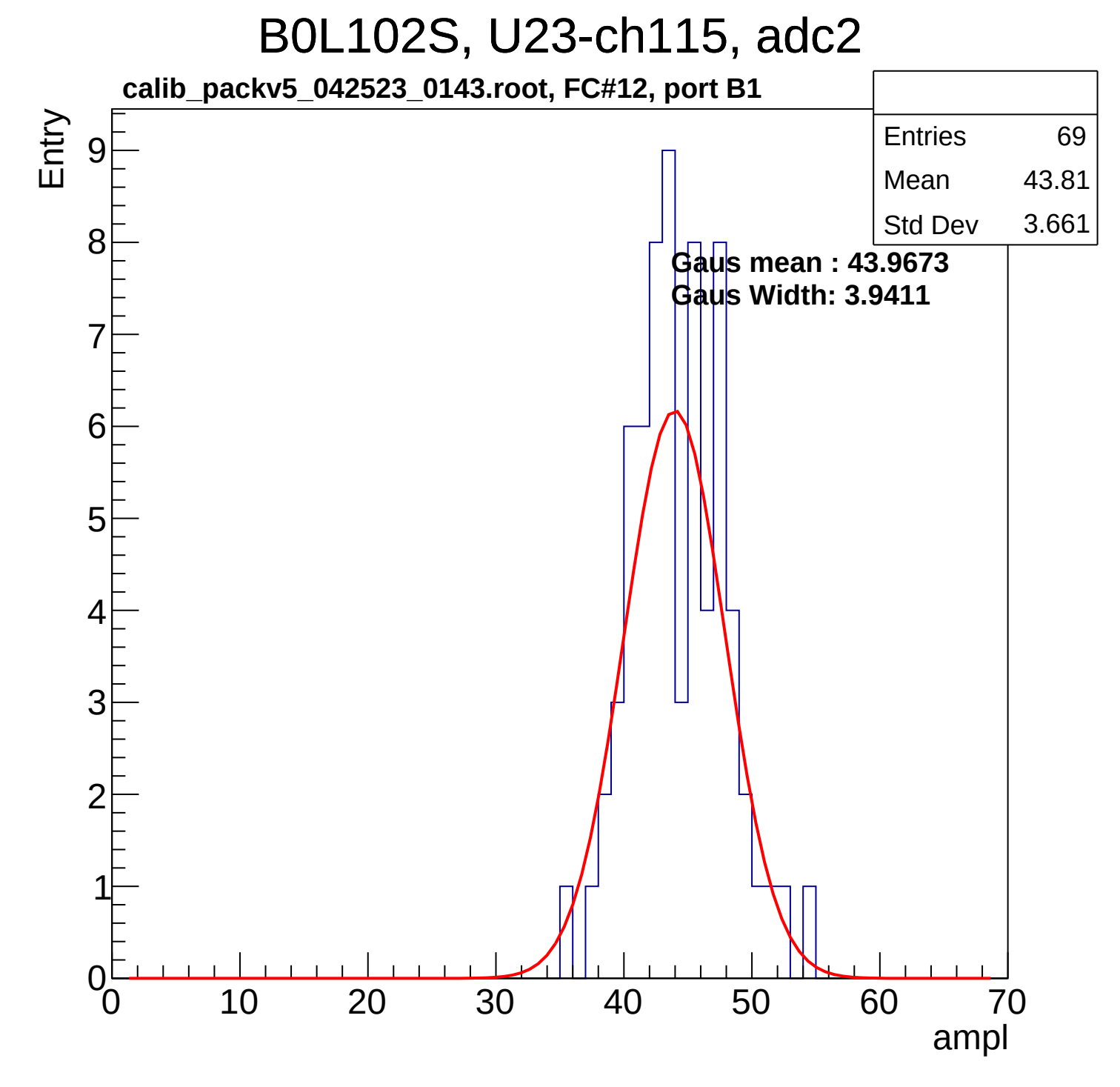
B0L102S, U23-ch115, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 43.81
Std Dev 3.661
Gaus mean : 43.9673
Gaus Width: 3.9411
Entry
ampl
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2
3
4
5
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7
8
9
0
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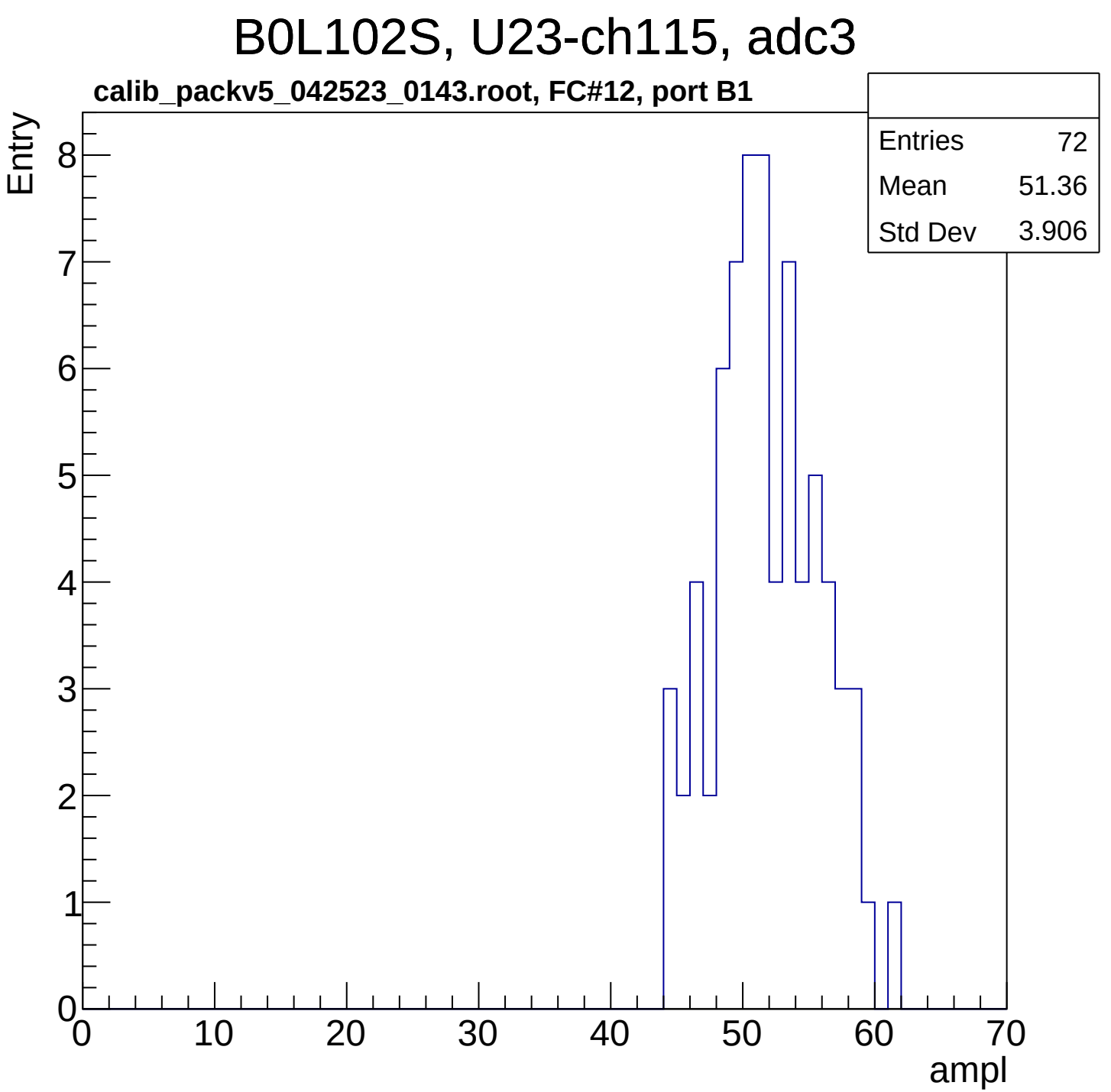
B0L102S, U23-ch115, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 51.36
Std Dev 3.906
Entry
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2
3
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10
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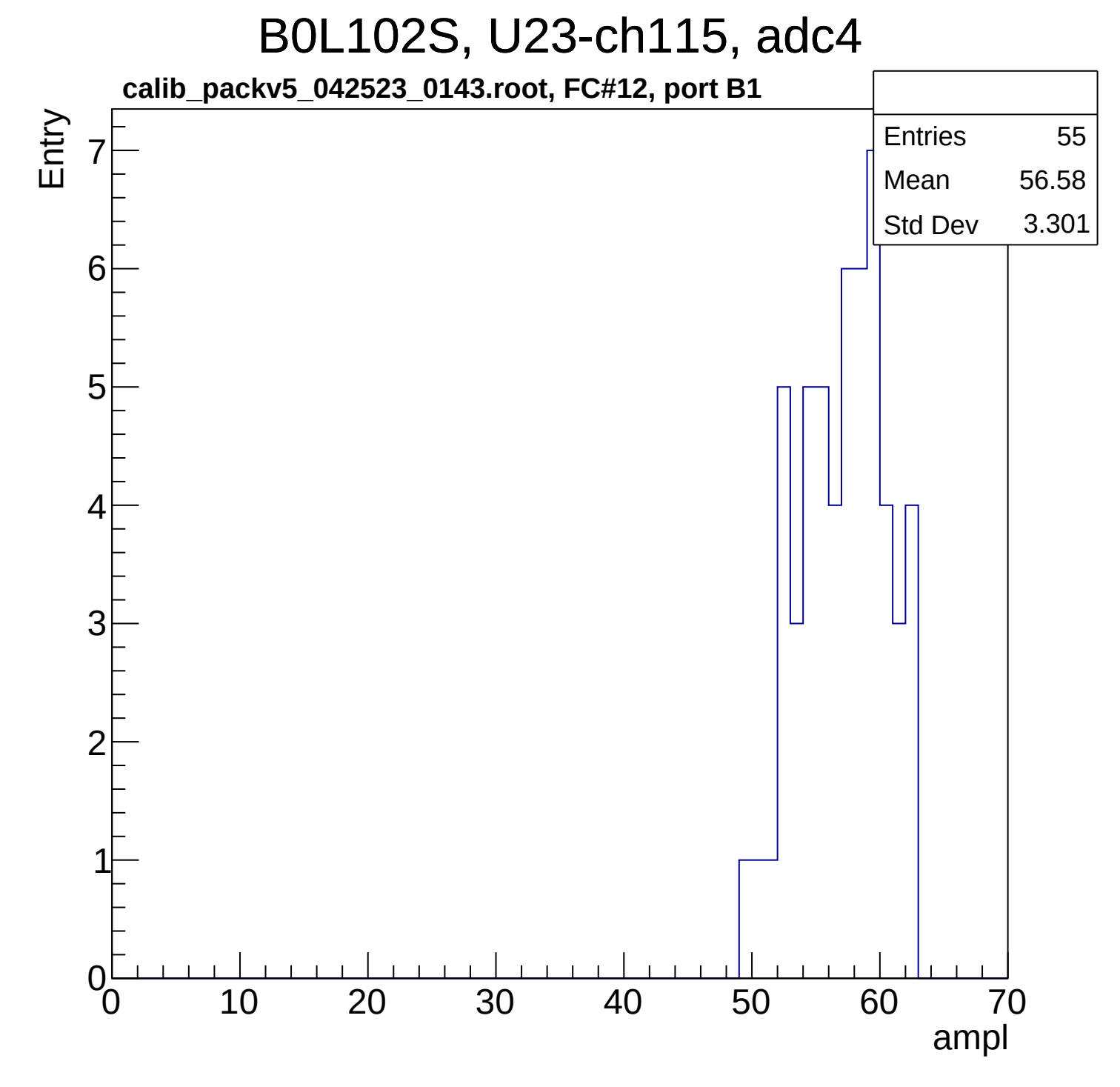
50
60
70
ampl


B0L102S, U23-ch115, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 56.58
Std Dev 3.301
Entry
ampl
0
1
2
3
4
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0
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60
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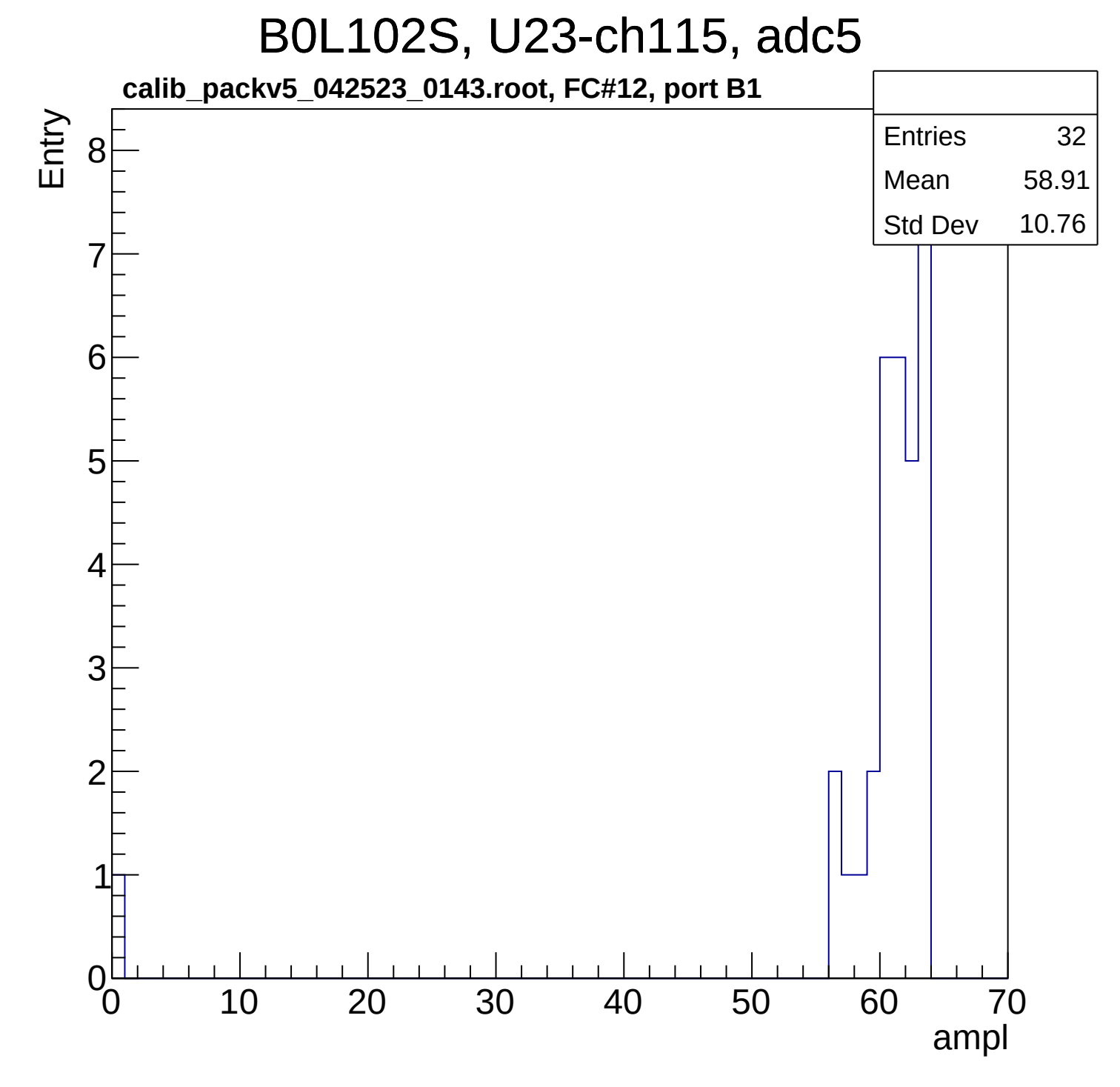
B0L102S, U23-ch115, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 58.91
Std Dev 10.76
Entry
ampl
0
1
2
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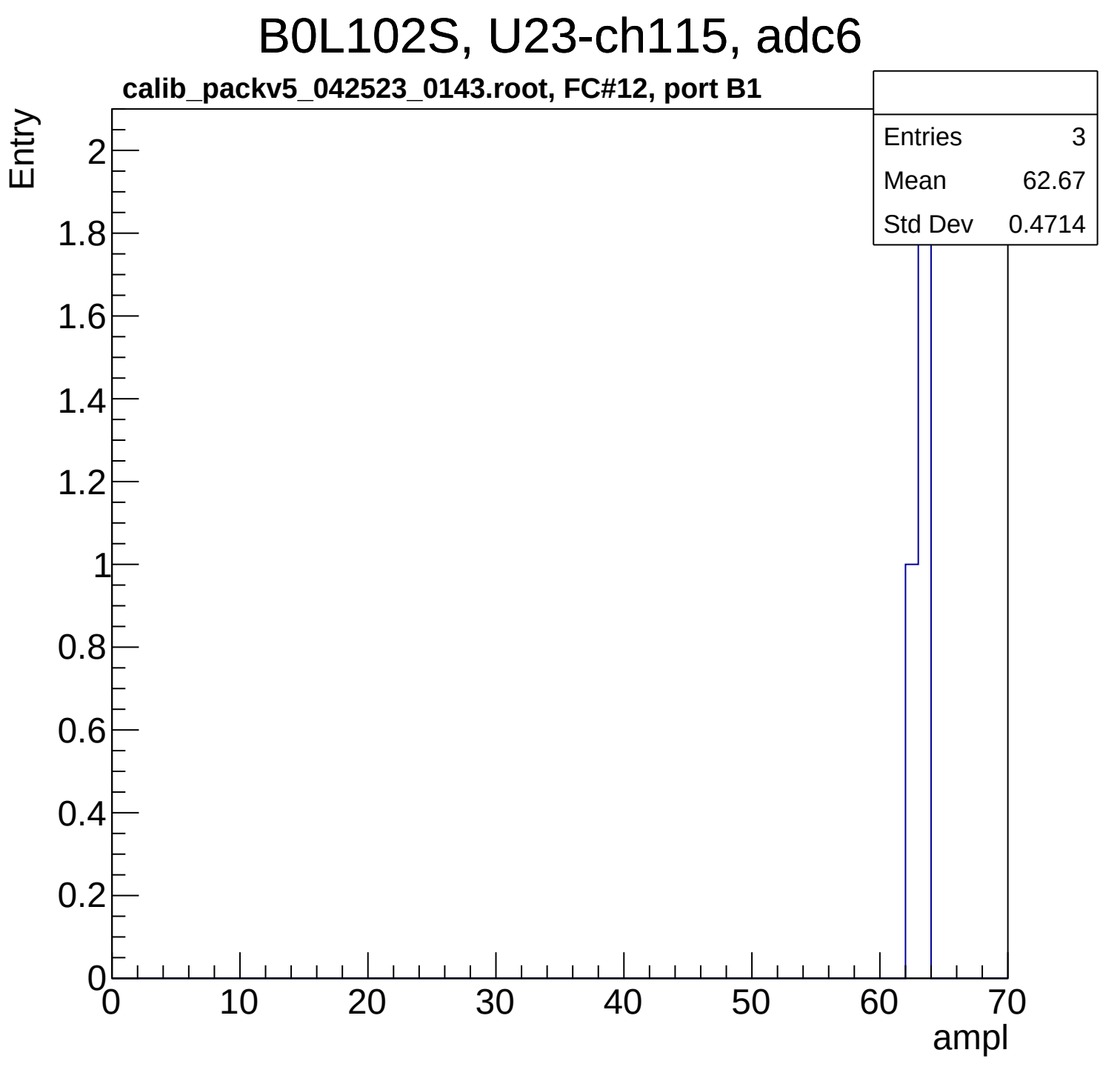
B0L102S, U23-ch115, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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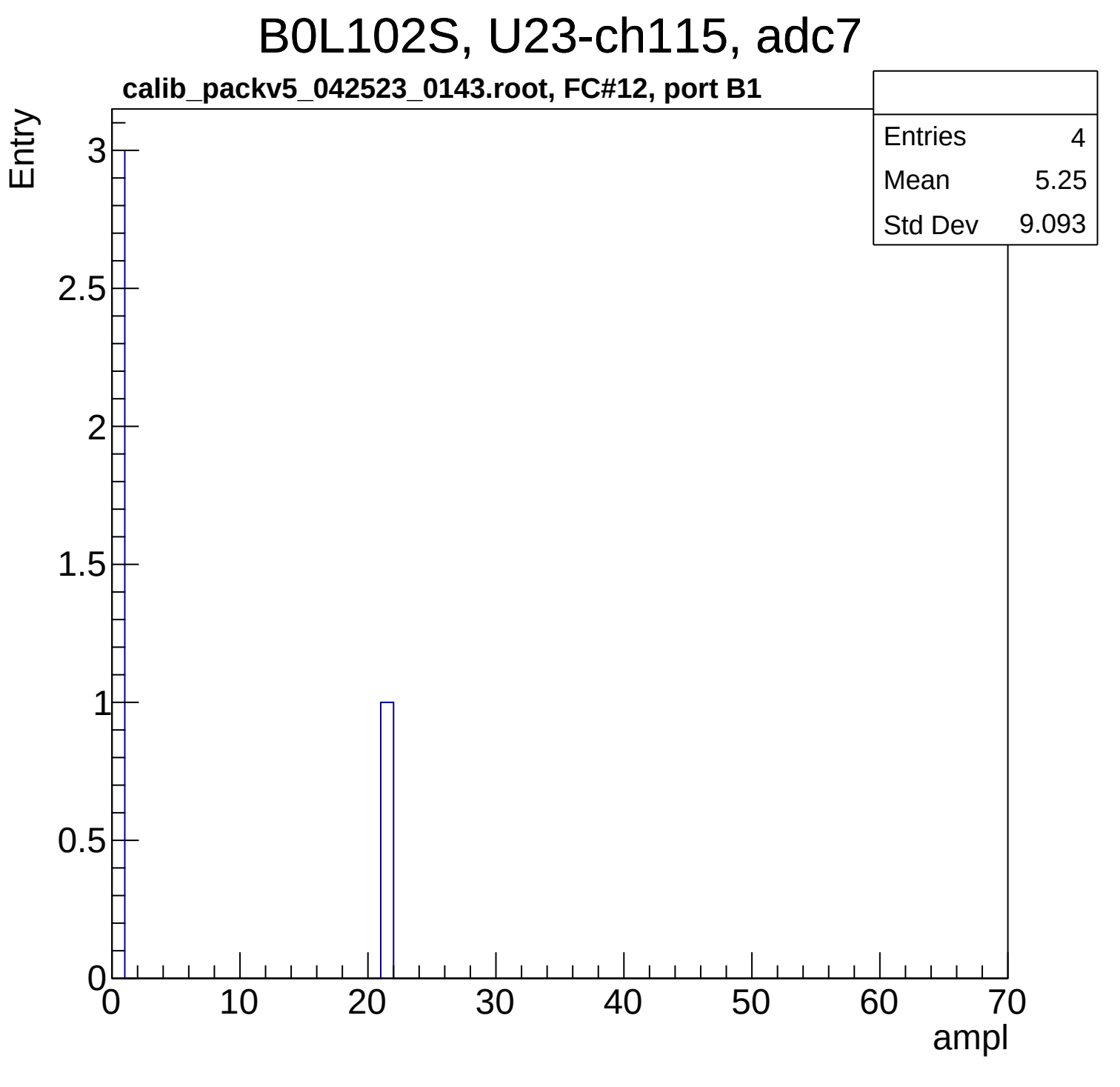
B0L102S, U23-ch115, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 5.25
Std Dev 9.093
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
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40
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60
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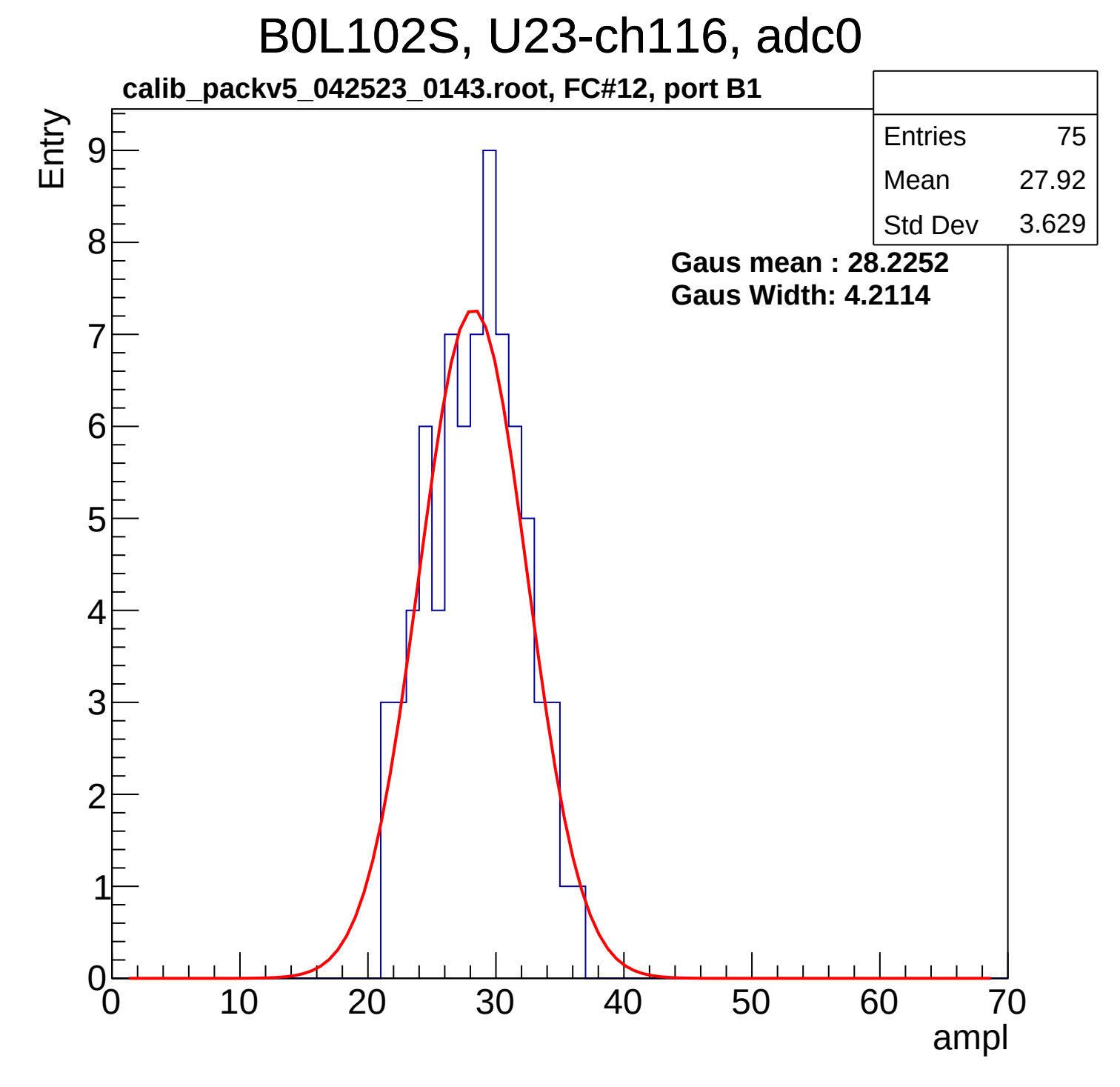
B0L102S, U23-ch116, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 27.92
Std Dev 3.629
Gaus mean : 28.2252
Gaus Width: 4.2114
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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30
40
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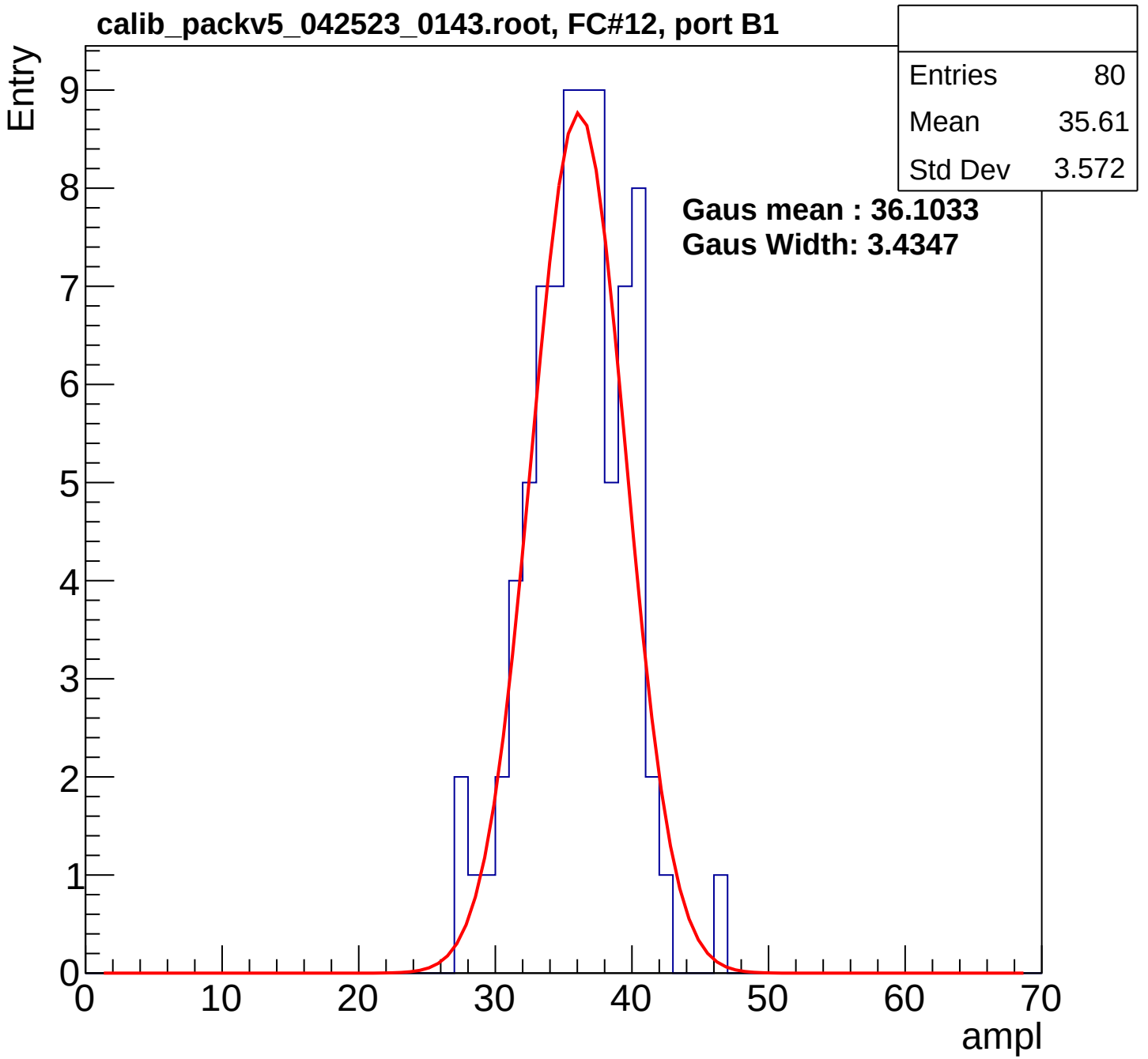
B0L102S, U23-ch116, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 80
Mean 35.61
Std Dev 3.572
Gaus mean : 36.1033
Gaus Width: 3.4347
Entry
ampl
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2
3
4
5
6
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8
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0
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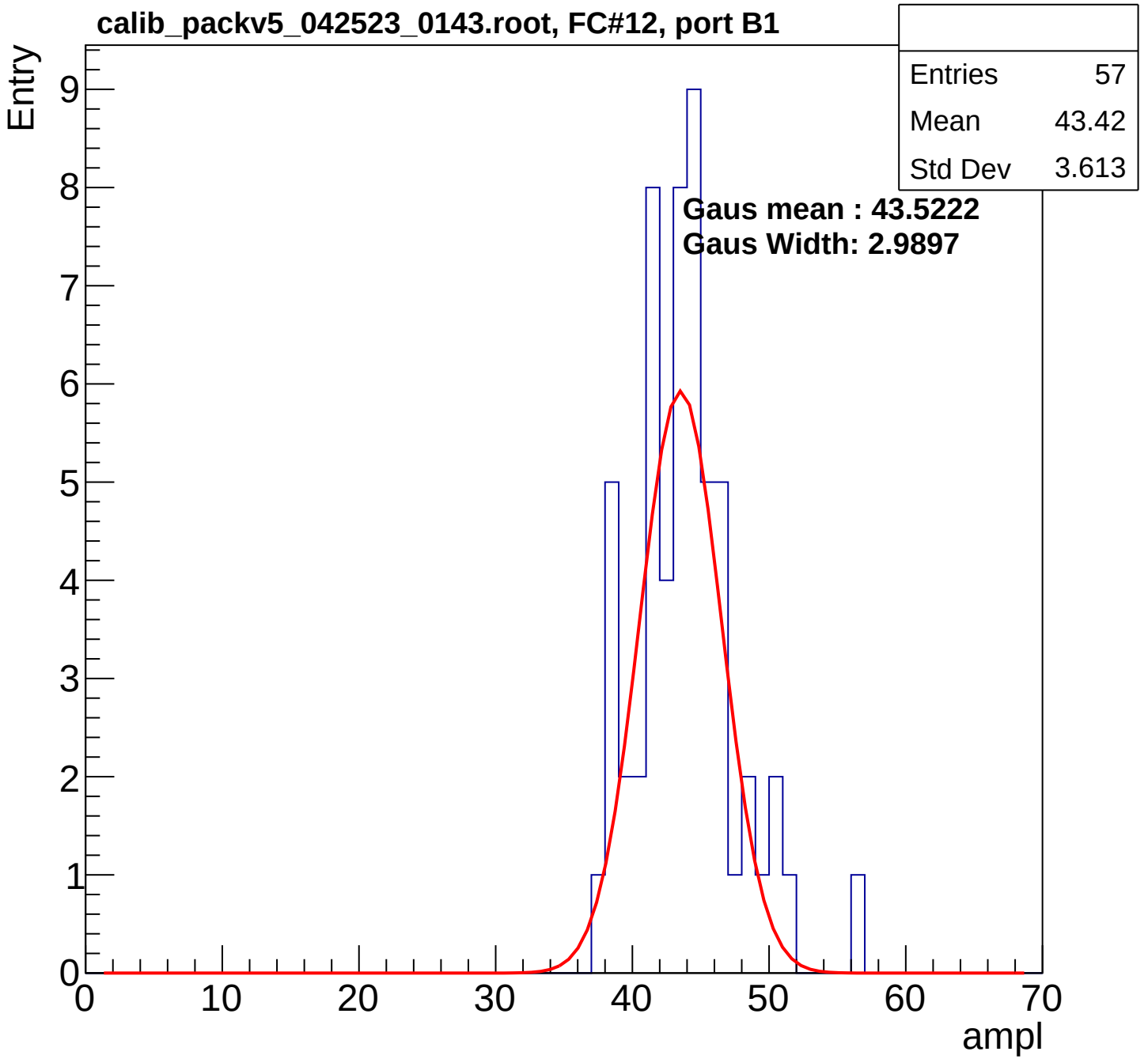
B0L102S, U23-ch116, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.42
Std Dev 3.613
Gaus mean : 43.5222
Gaus Width: 2.9897
Entry
ampl
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2
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0
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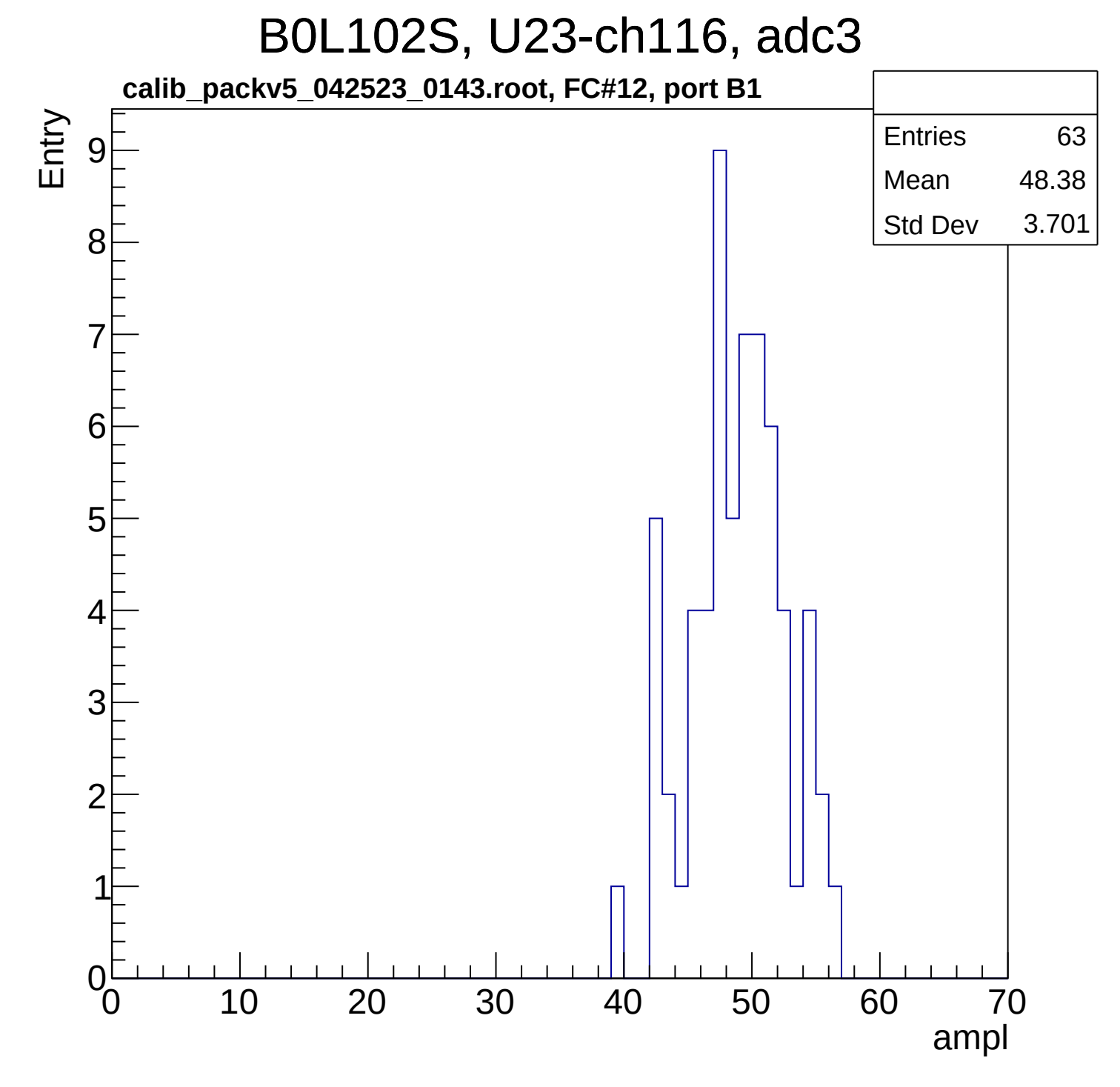
B0L102S, U23-ch116, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 48.38
Std Dev 3.701
Entry
ampl
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2
3
4
5
6
7
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0
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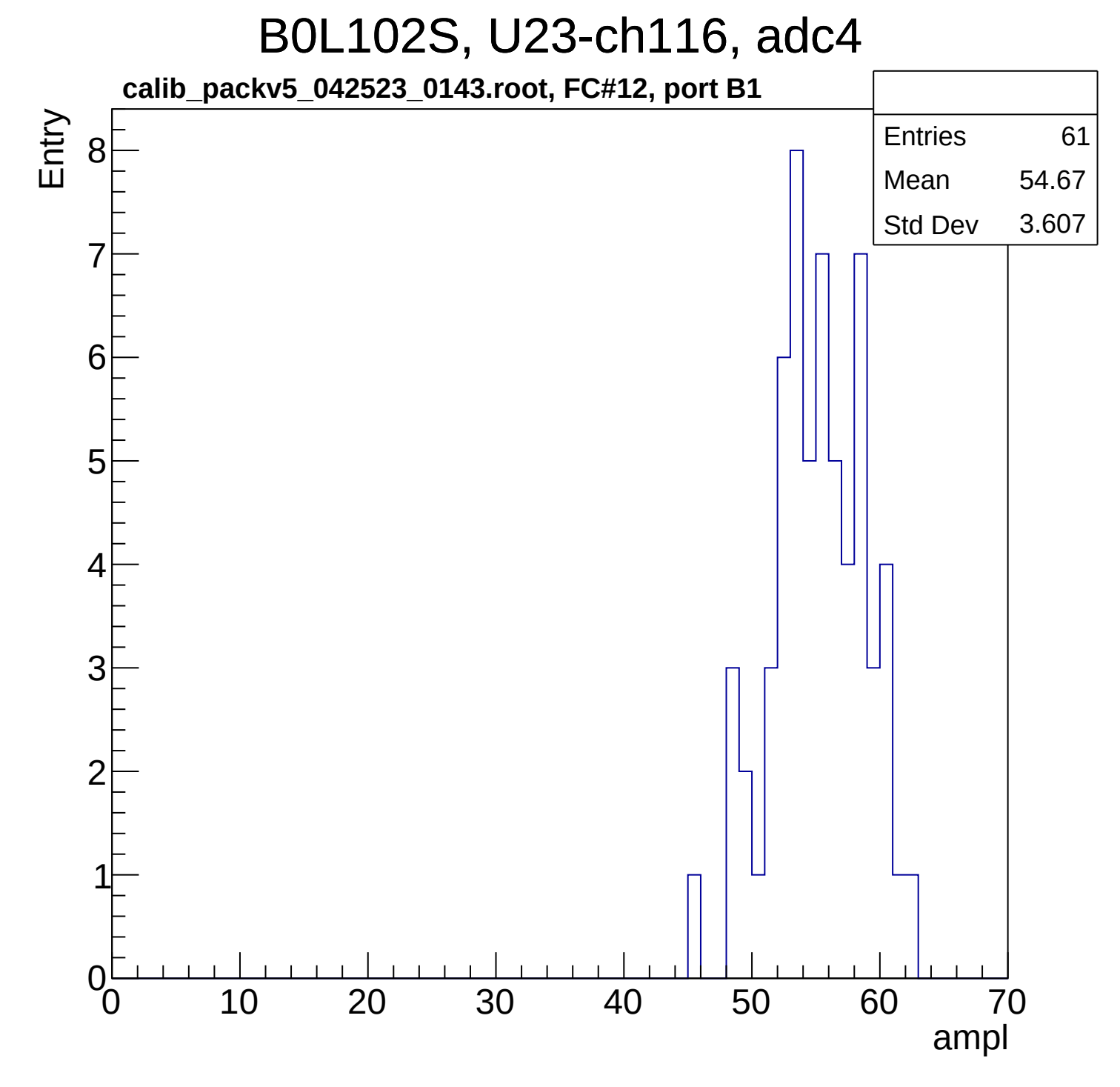
B0L102S, U23-ch116, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 54.67
Std Dev 3.607
Entry
ampl
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2
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4
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0
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40
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60
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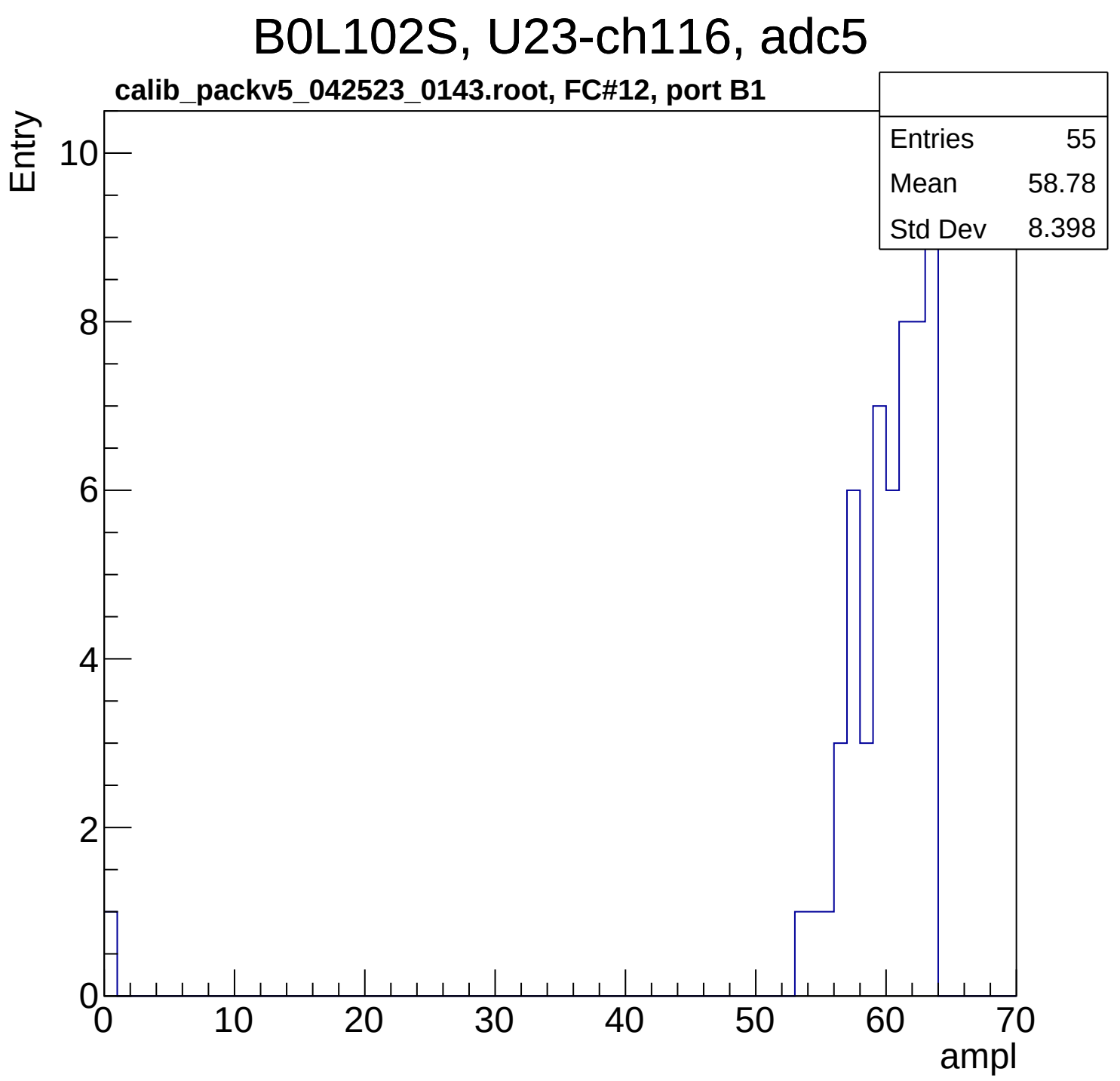
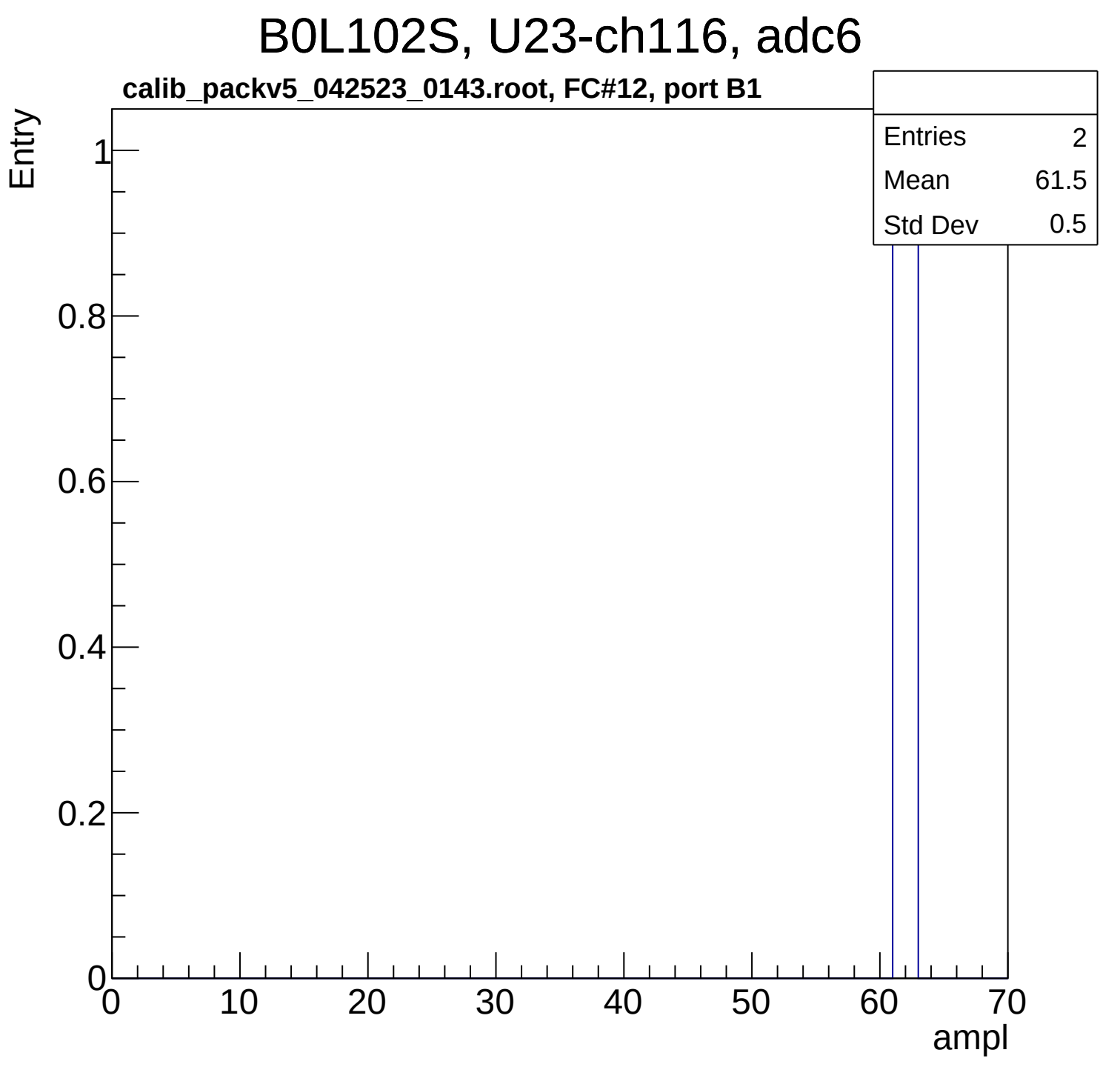
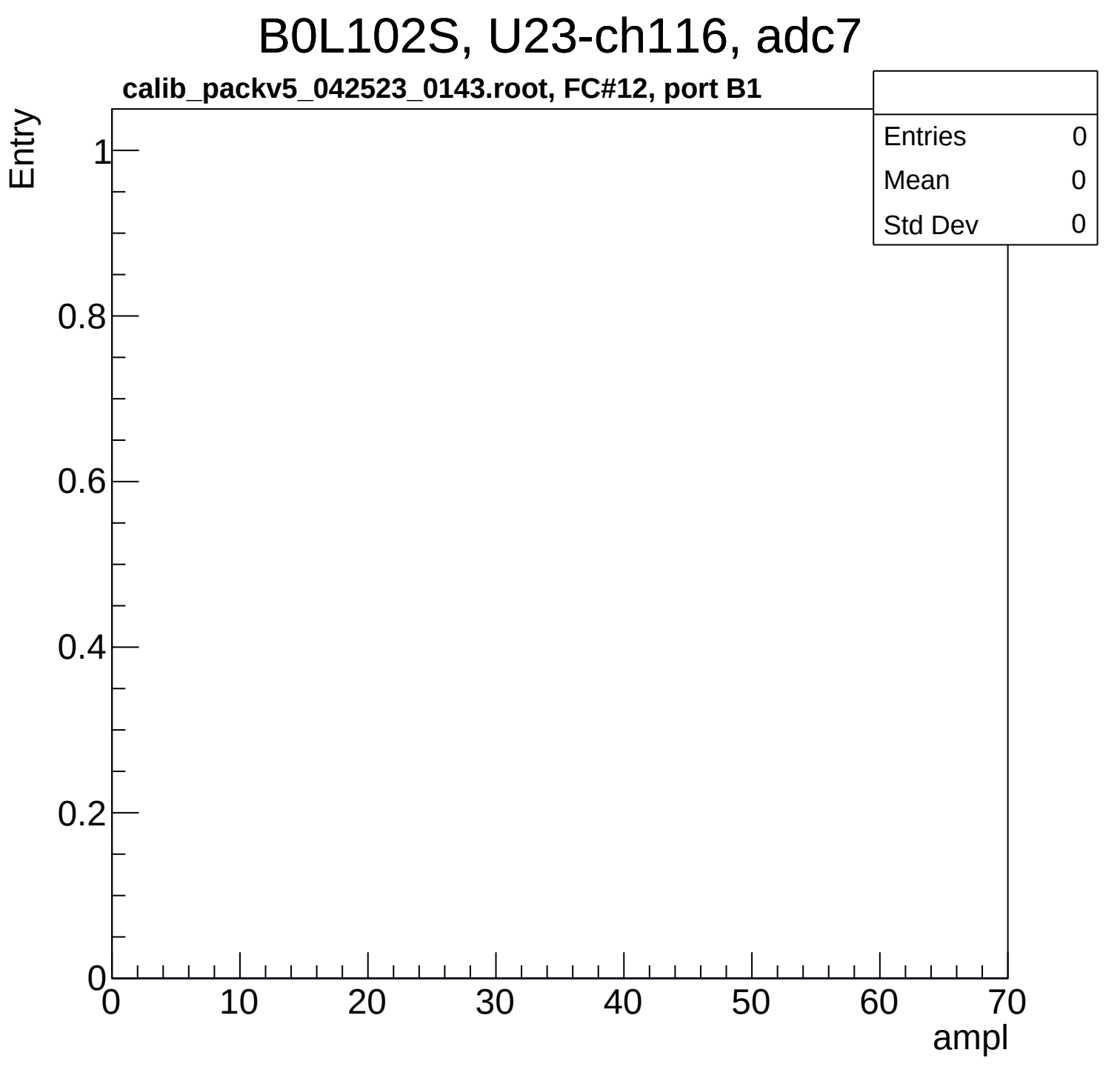
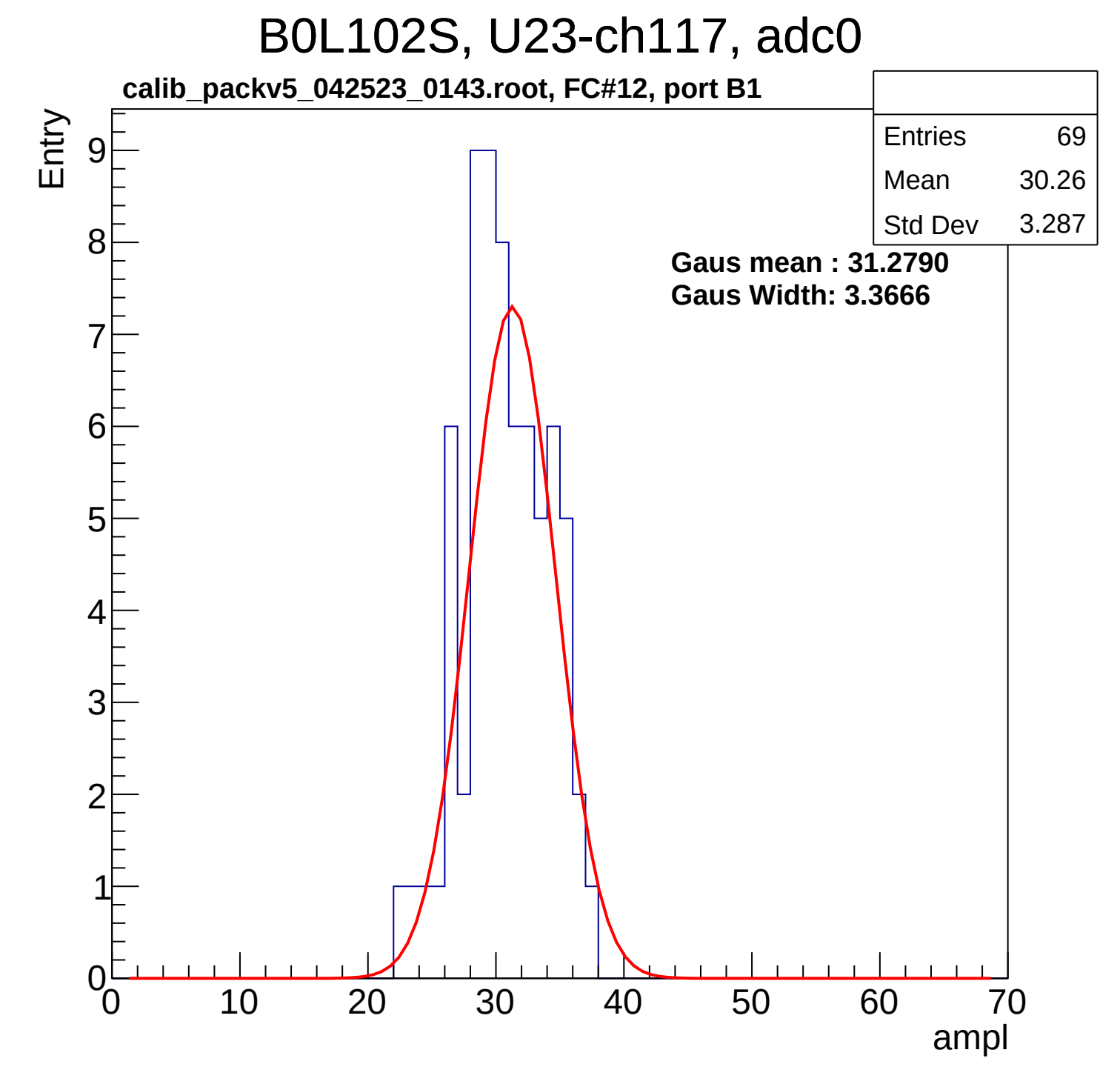

B0L102S, U23-ch116, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 58.78
Std Dev 8.398
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch116, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch116, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch117, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 30.26
Std Dev 3.287
Gaus mean : 31.2790
Gaus Width: 3.3666
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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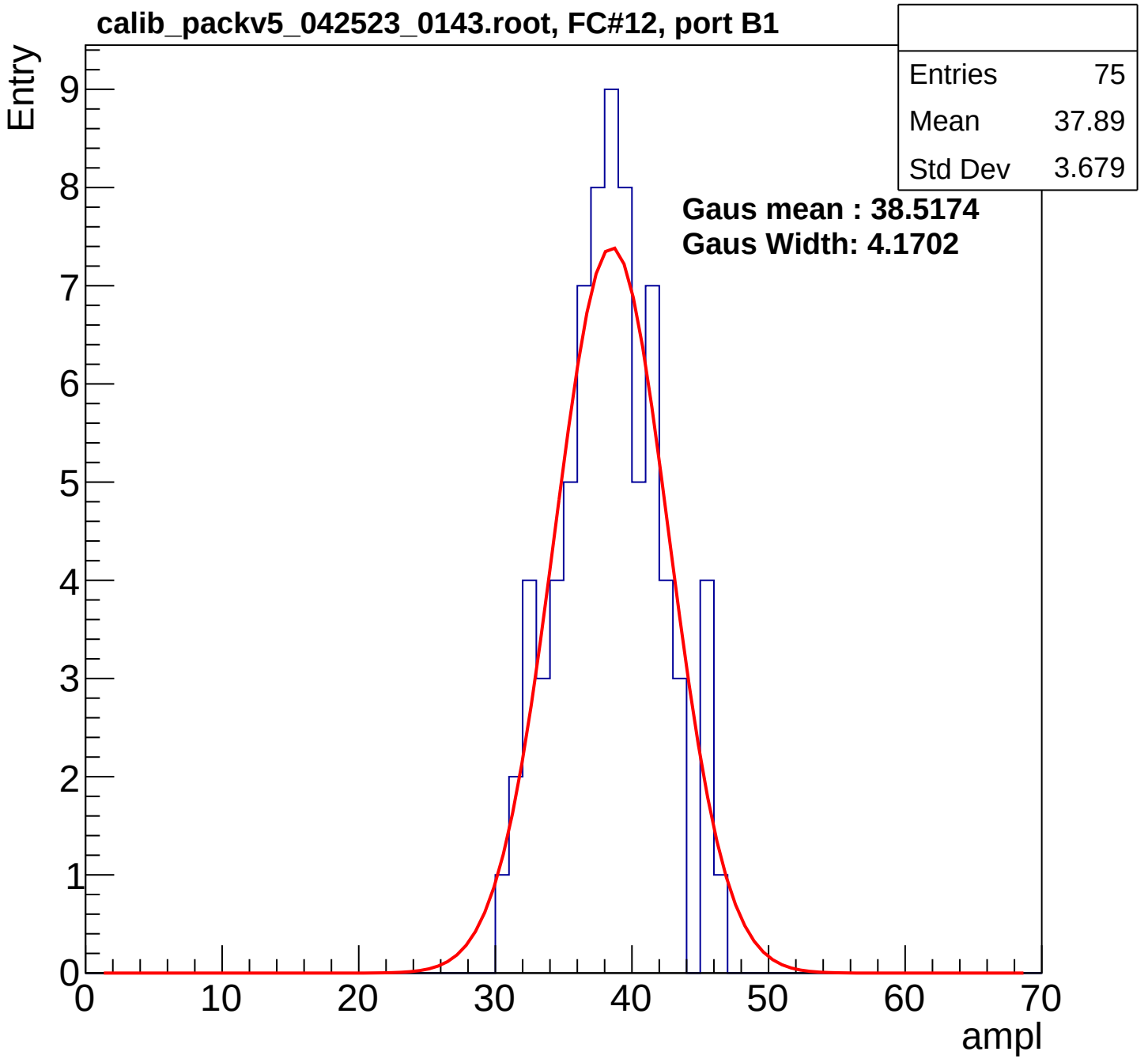
B0L102S, U23-ch117, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 37.89
Std Dev 3.679
Gaus mean : 38.5174
Gaus Width: 4.1702
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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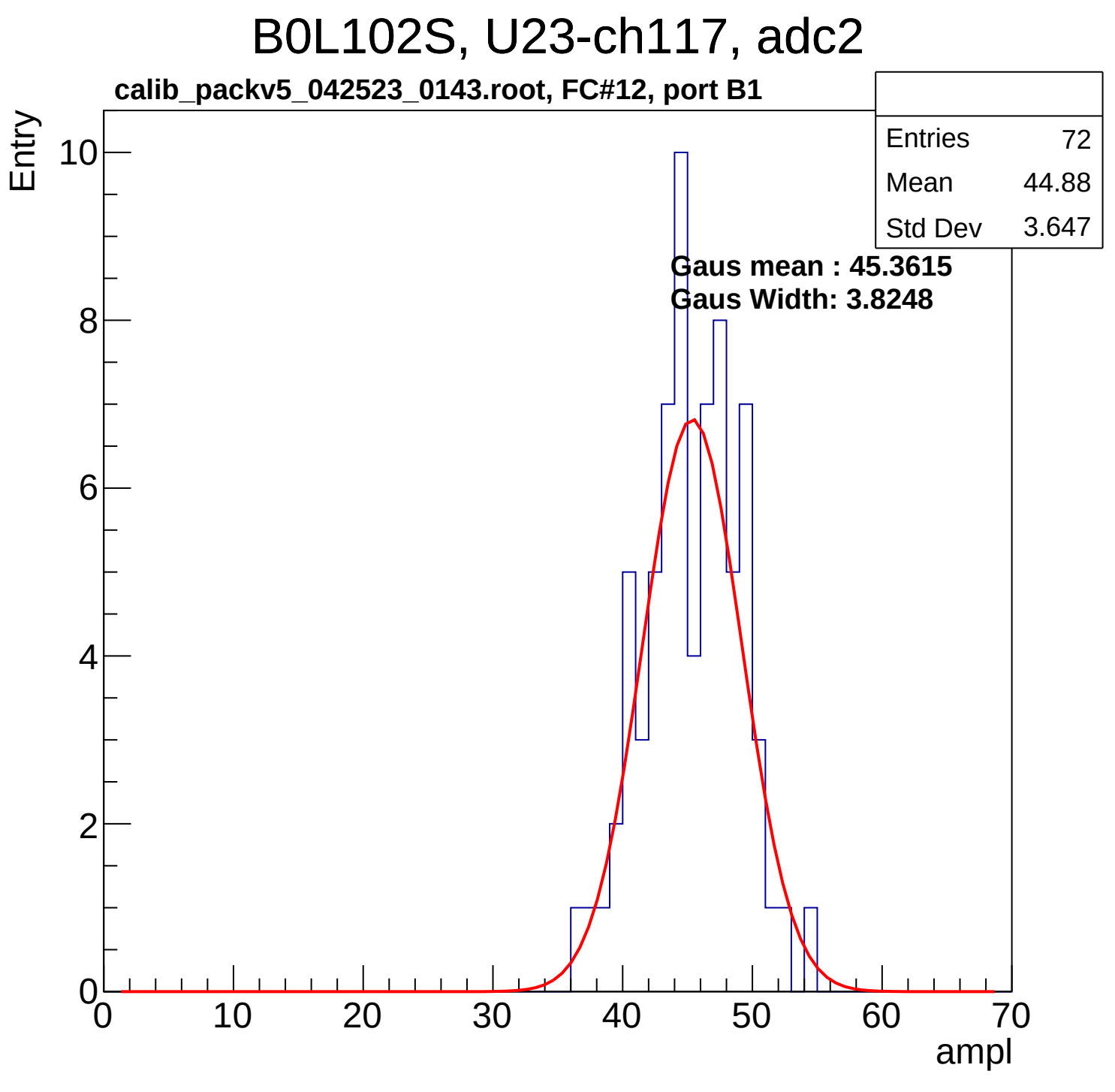
B0L102S, U23-ch117, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 44.88
Std Dev 3.647
Gaus mean : 45.3615
Gaus Width: 3.8248
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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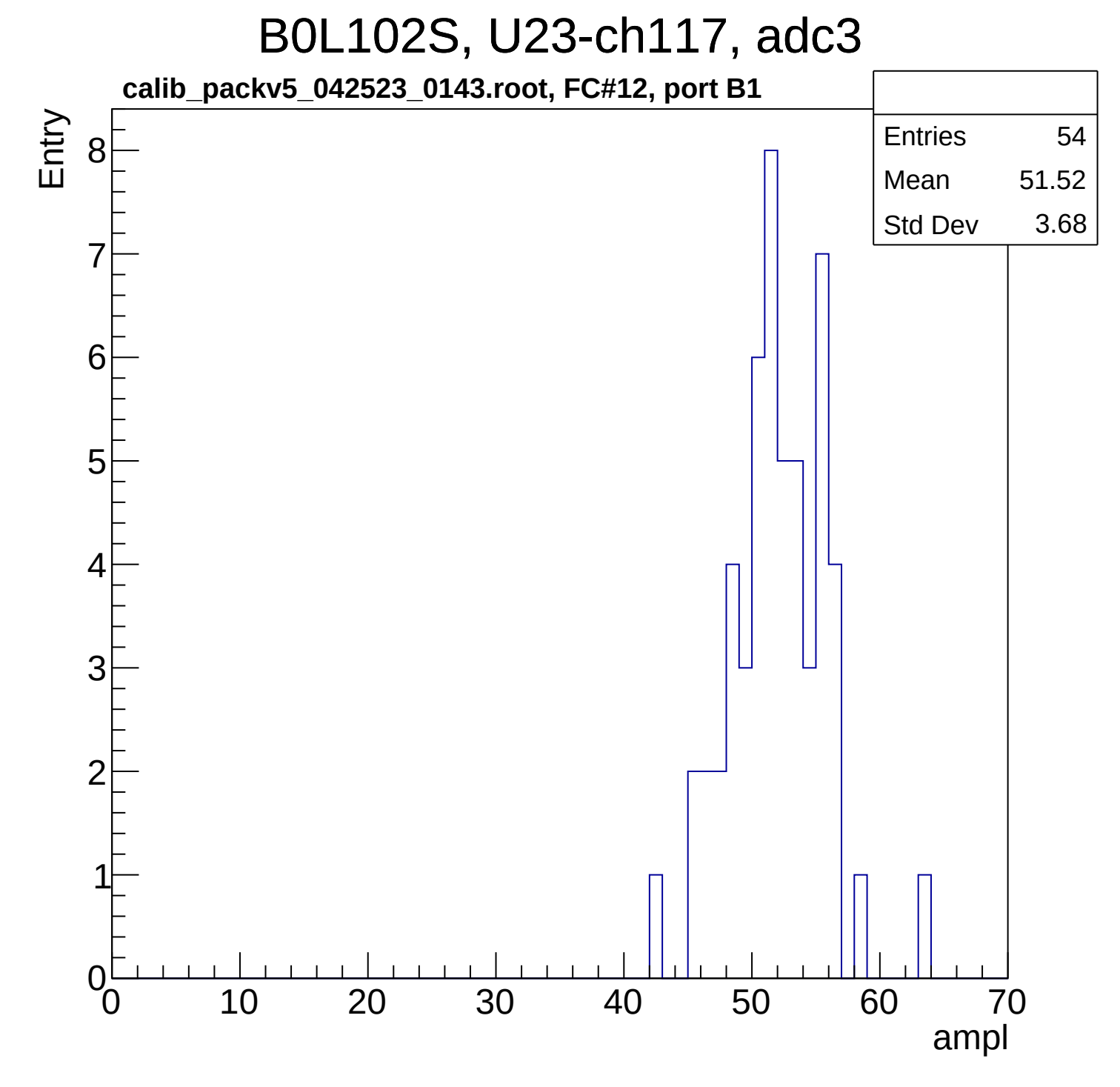
B0L102S, U23-ch117, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 51.52
Std Dev 3.68
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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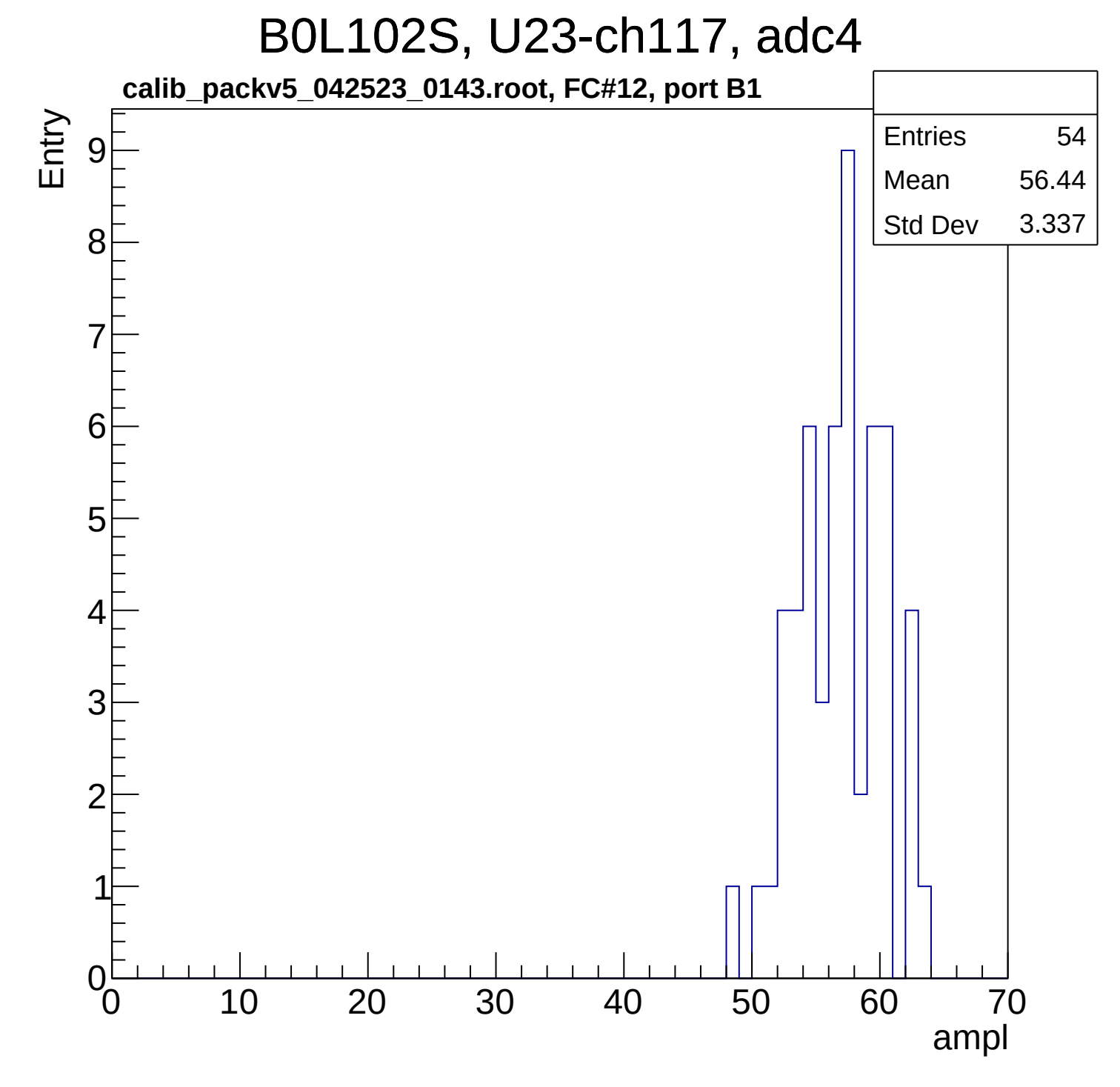
B0L102S, U23-ch117, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 56.44
Std Dev 3.337
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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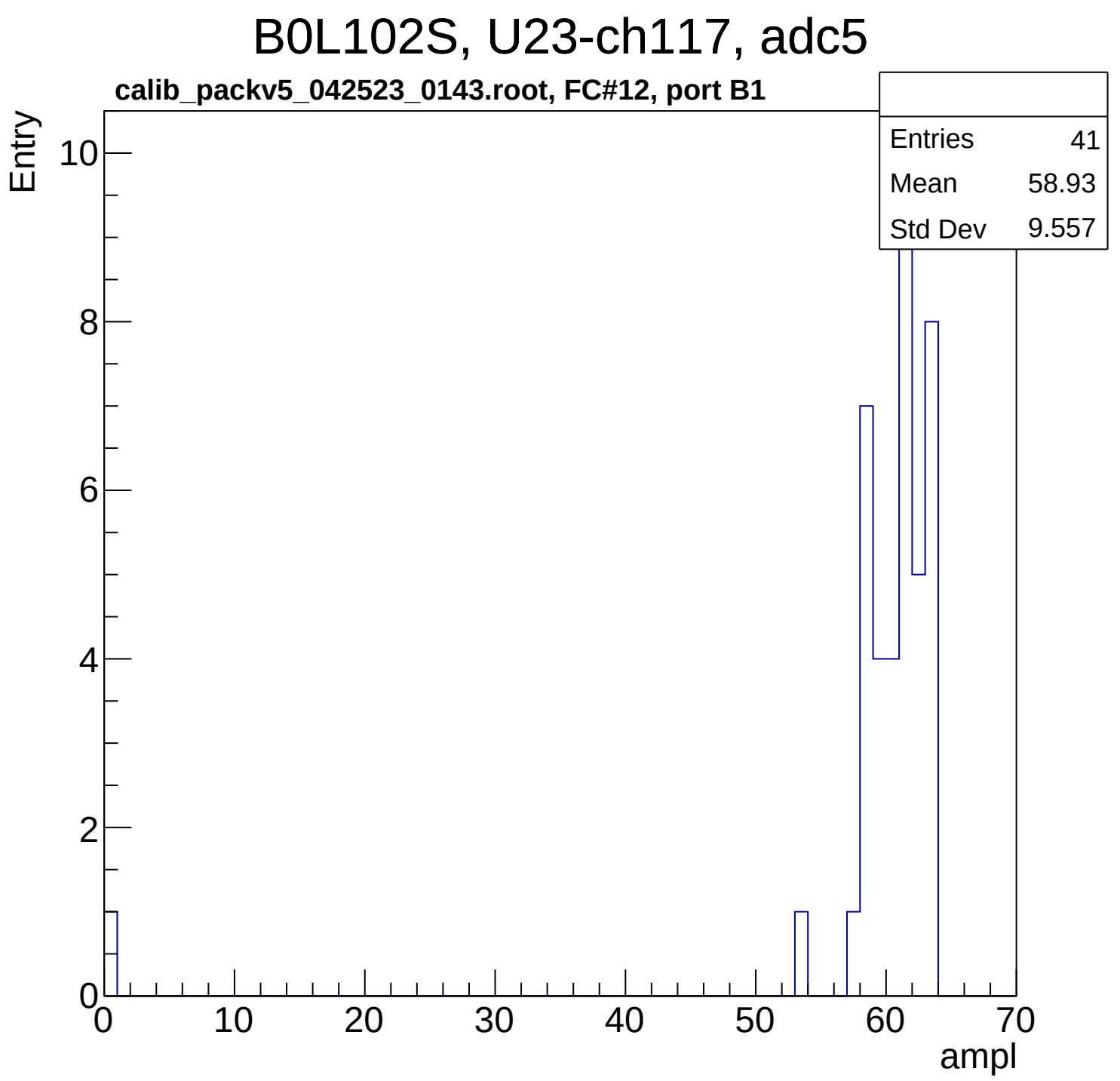
B0L102S, U23-ch117, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.93
Std Dev 9.557
Entry
ampl
0
2
4
6
8
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0
10
20
30
40
50
60
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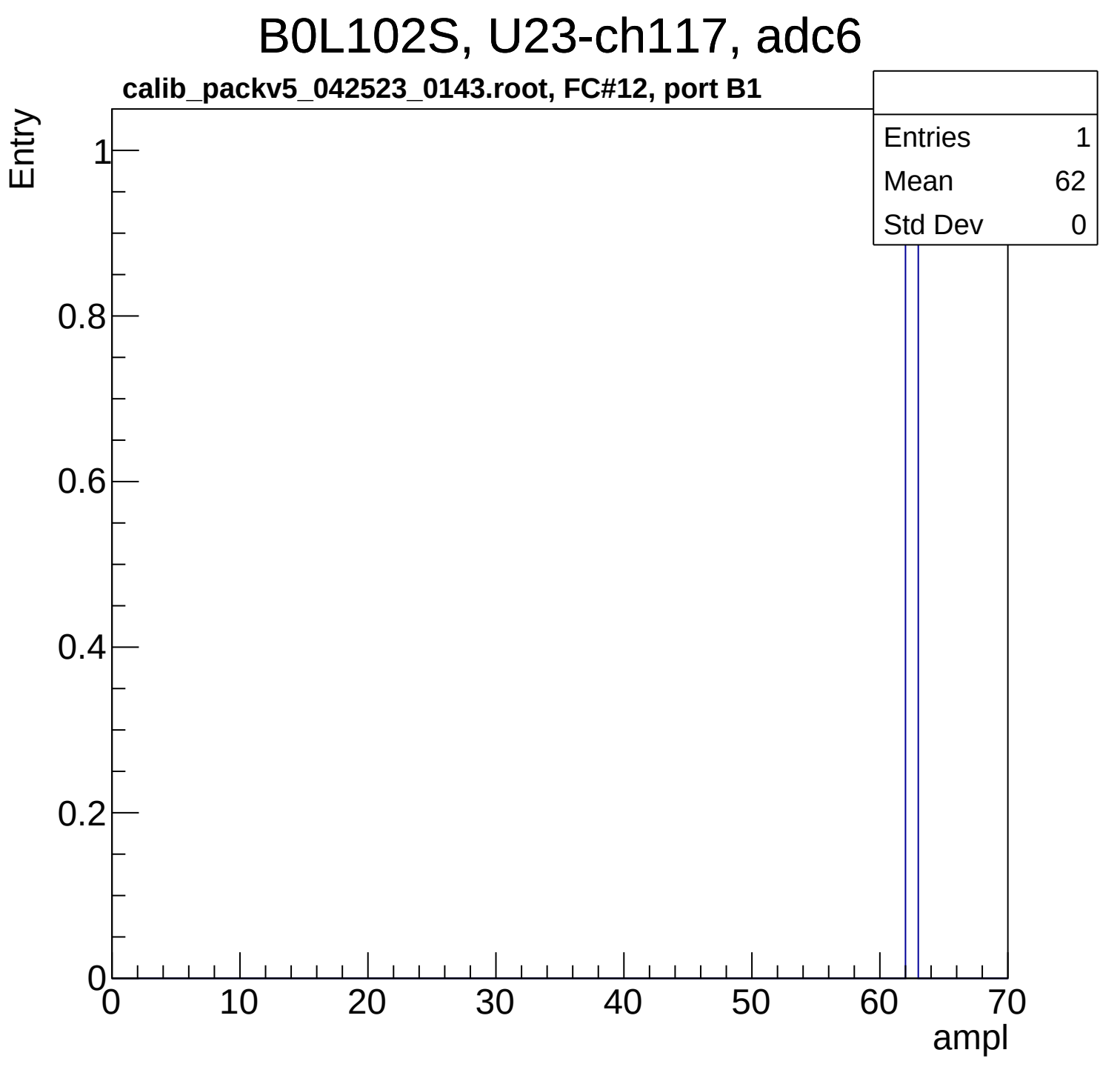
B0L102S, U23-ch117, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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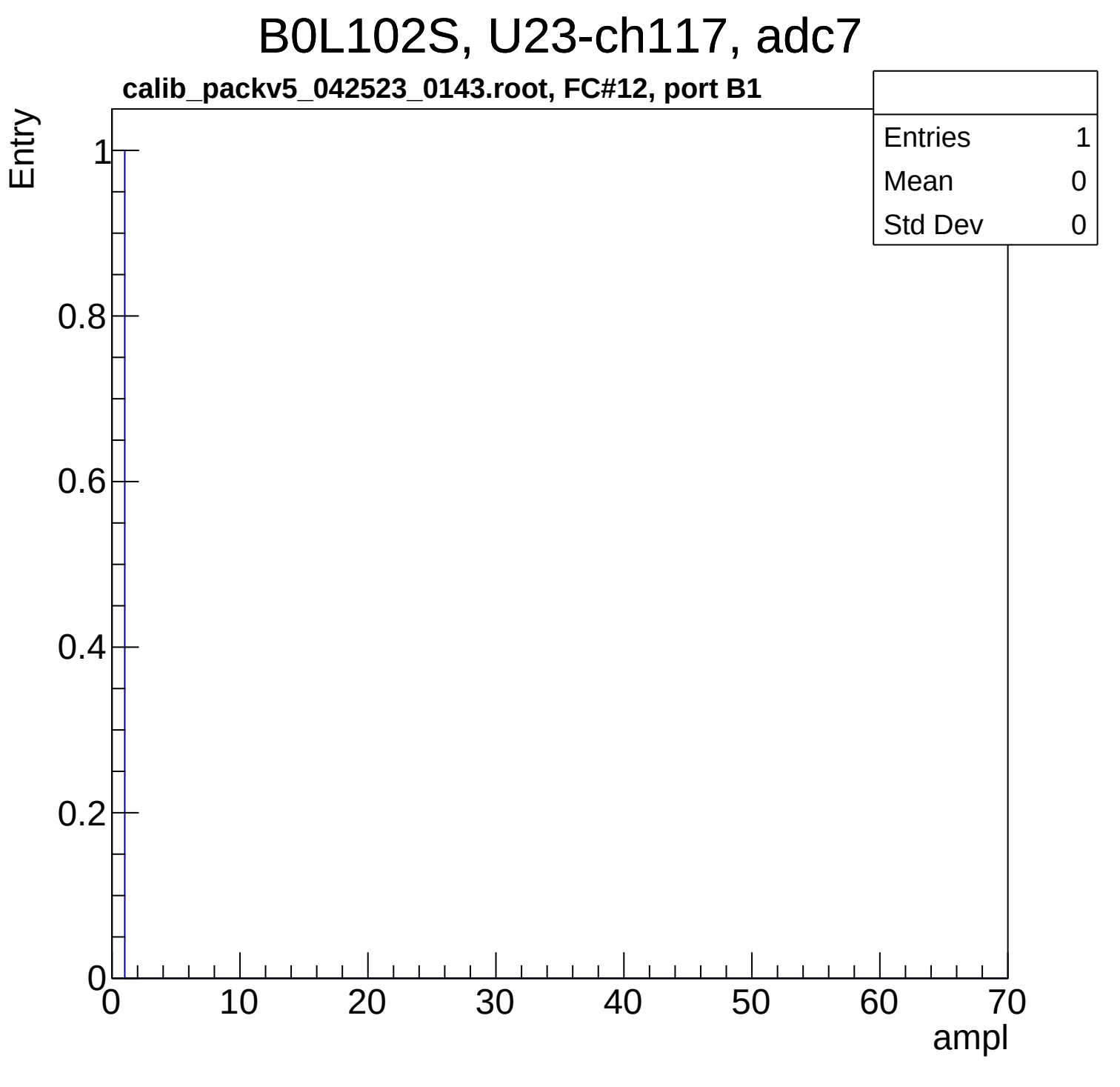
30
40
50
60
70
ampl


B0L102S, U23-ch117, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
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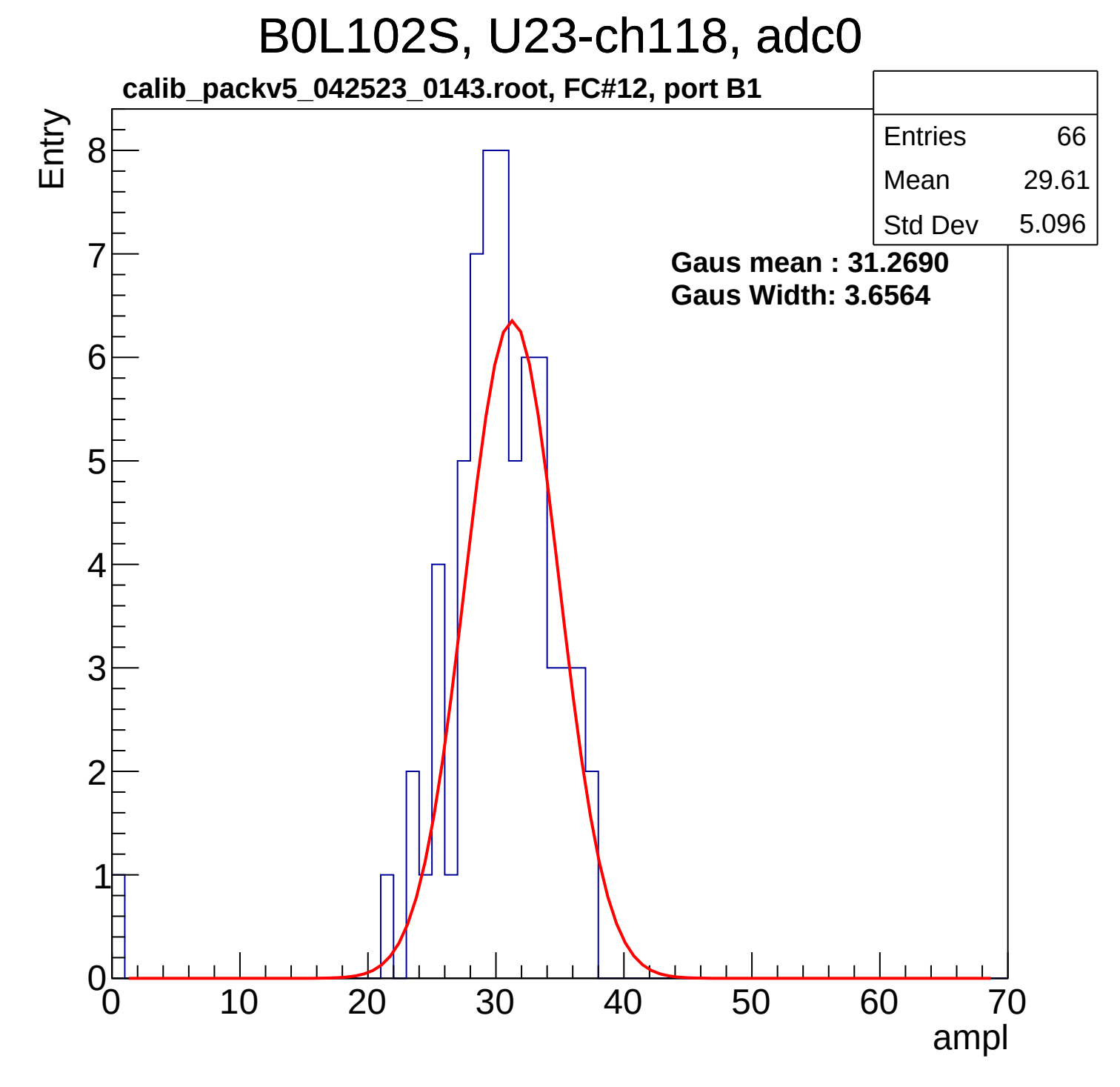
50
60
70
ampl


B0L102S, U23-ch118, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 66
Mean 29.61
Std Dev 5.096
Gaus mean : 31.2690
Gaus Width: 3.6564
Entry
ampl
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3
4
5
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7
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0
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20
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40
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60
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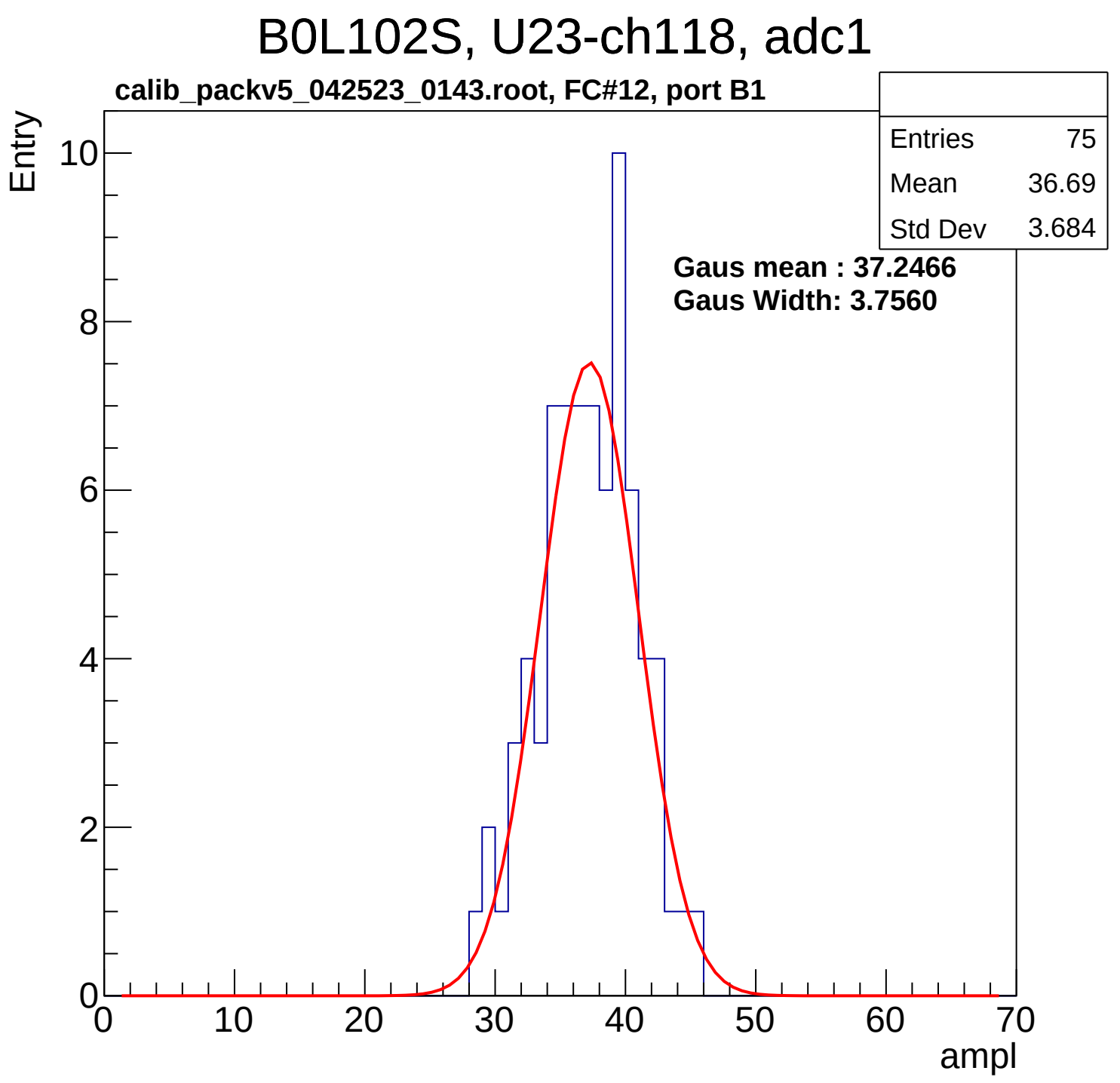
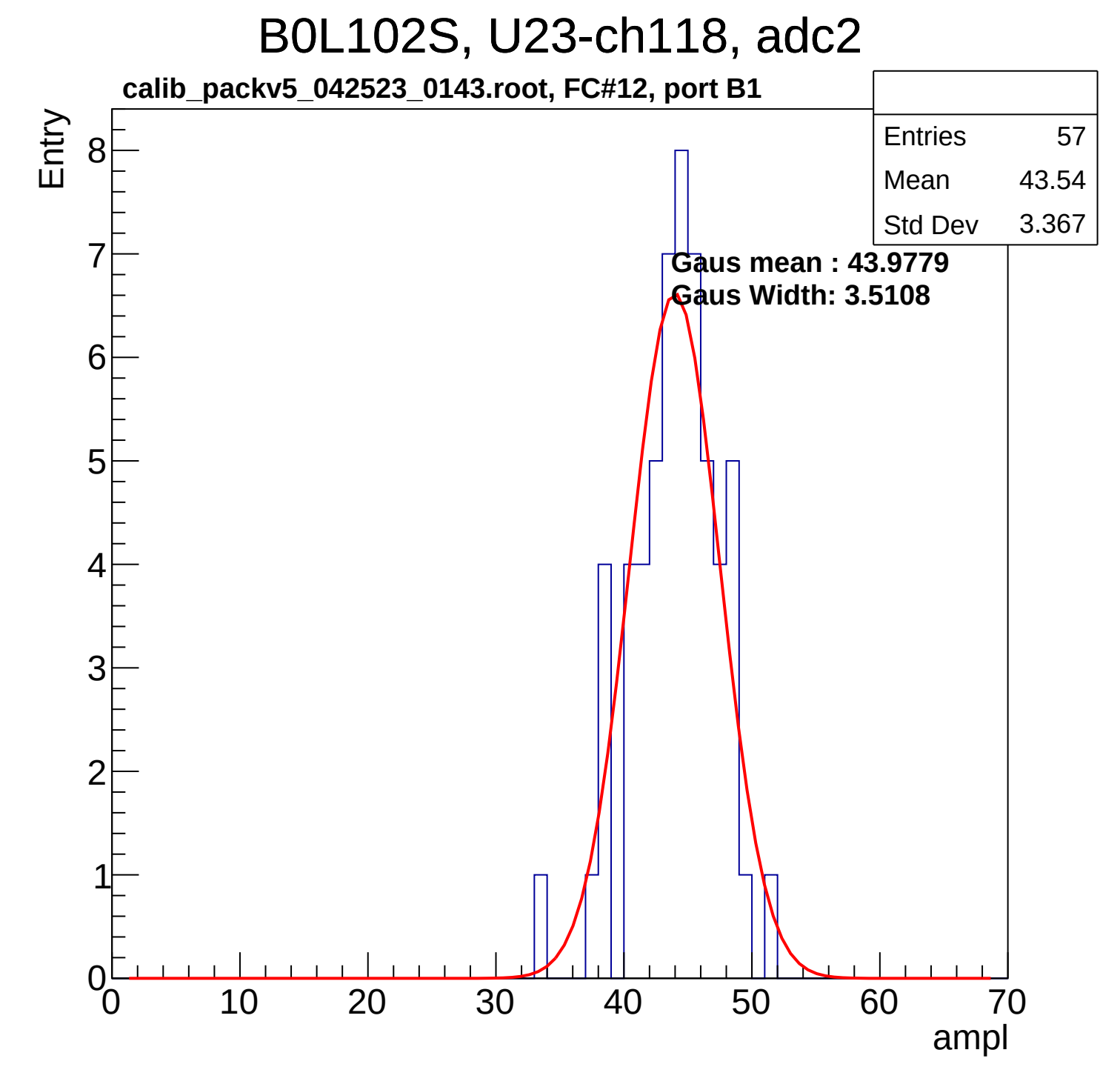

B0L102S, U23-ch118, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 36.69
Std Dev 3.684
Gaus mean : 37.2466
Gaus Width: 3.7560
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch118, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 43.54
Std Dev 3.367
Gaus mean : 43.9779
Gaus Width: 3.5108
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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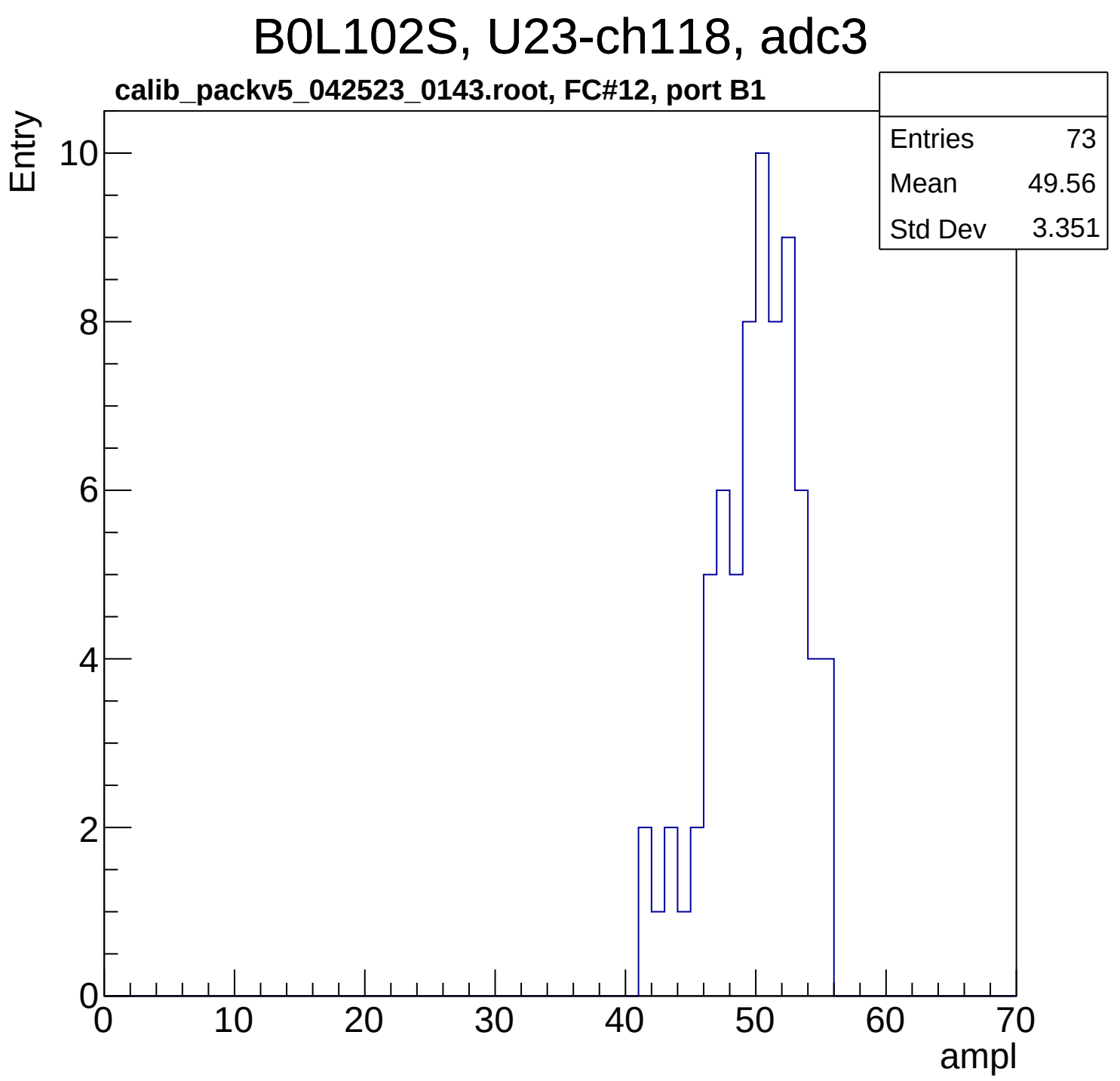
B0L102S, U23-ch118, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 49.56
Std Dev 3.351
Entry
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2
4
6
8
10
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10
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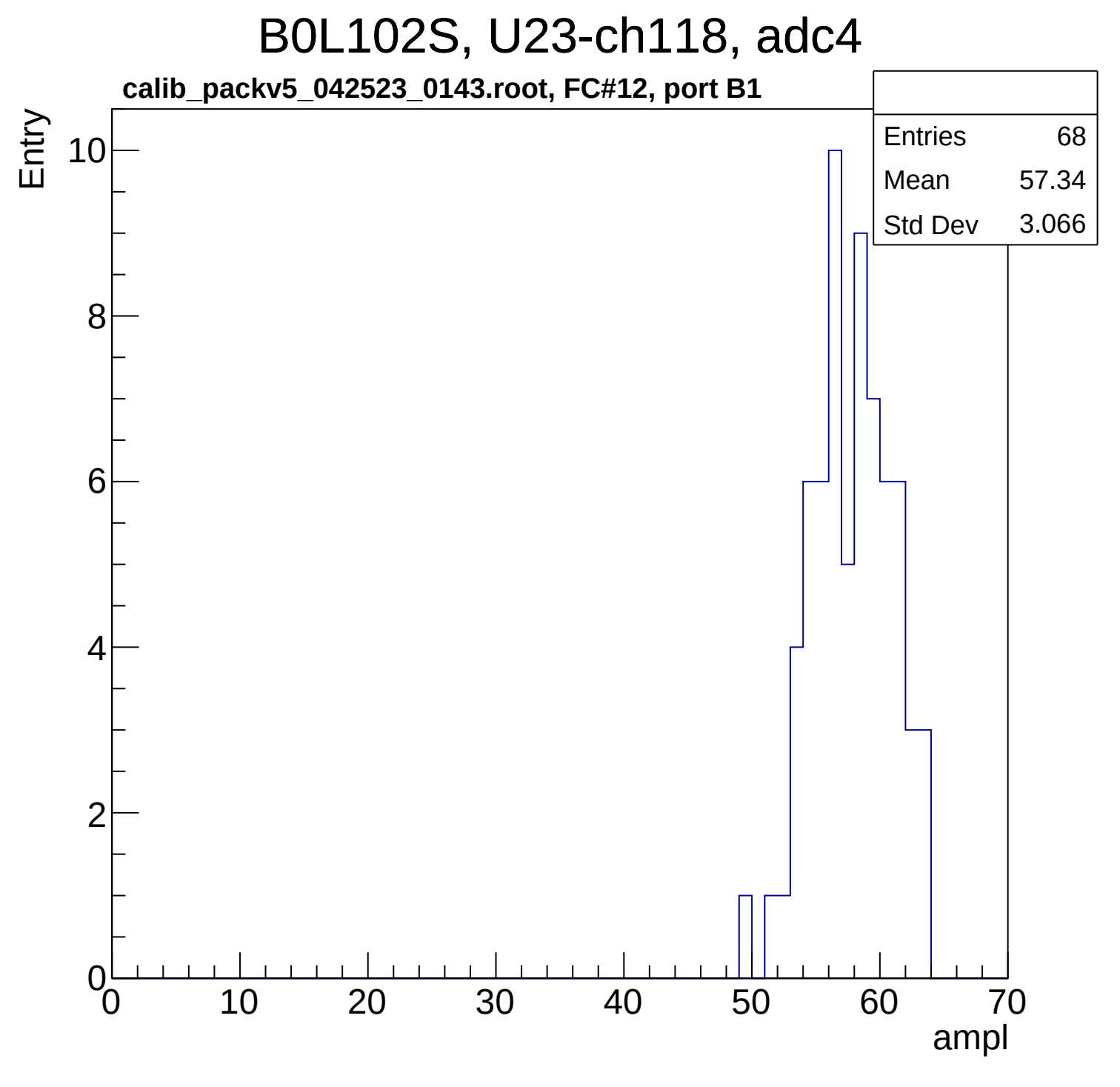
30
40
50
60
70
ampl


B0L102S, U23-ch118, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 57.34
Std Dev 3.066
Entry
ampl
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0
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40
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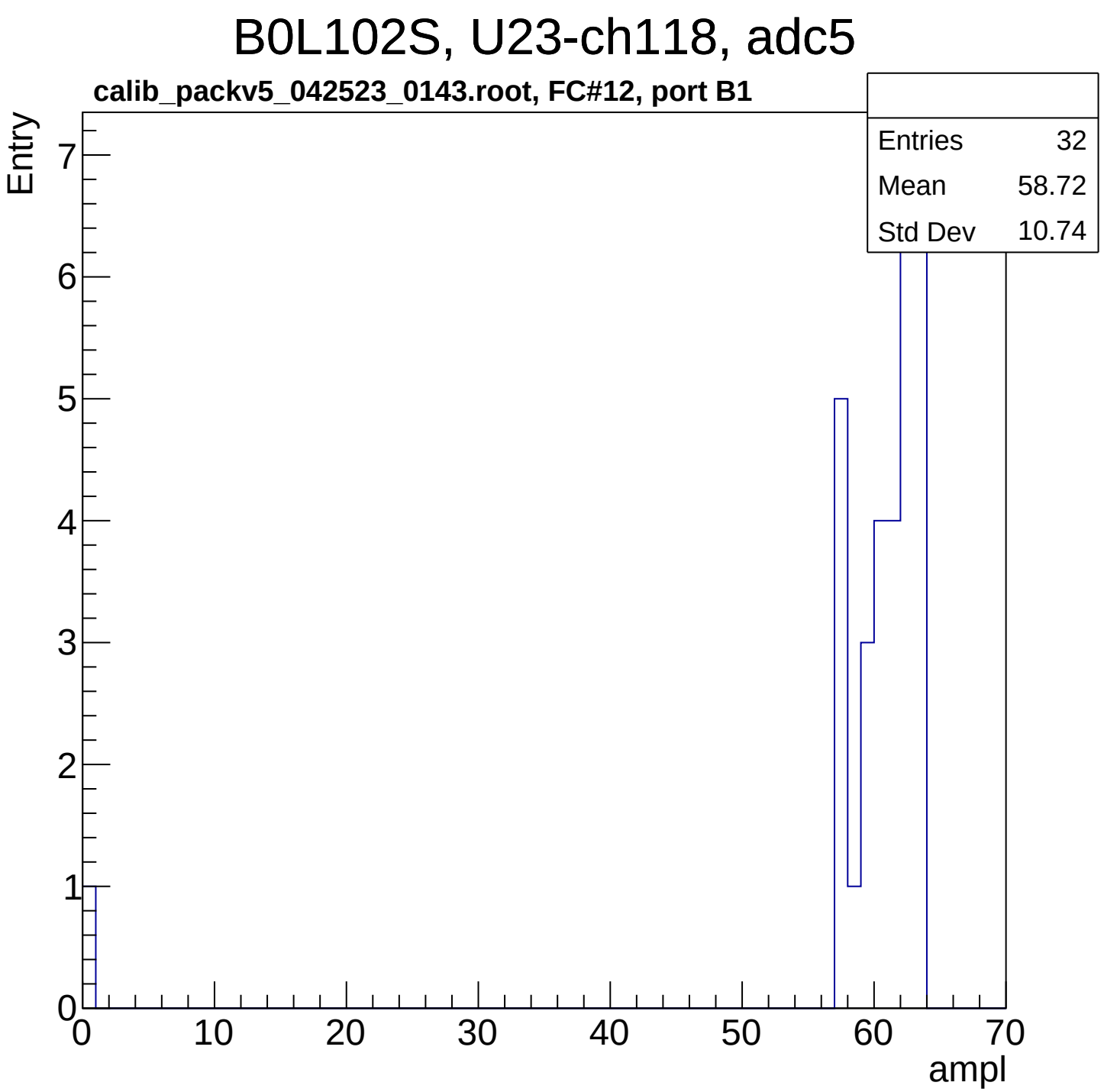
60
70


B0L102S, U23-ch118, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 32
Mean 58.72
Std Dev 10.74
Entry
ampl
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3
4
5
6
7
0
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20
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40
50
60
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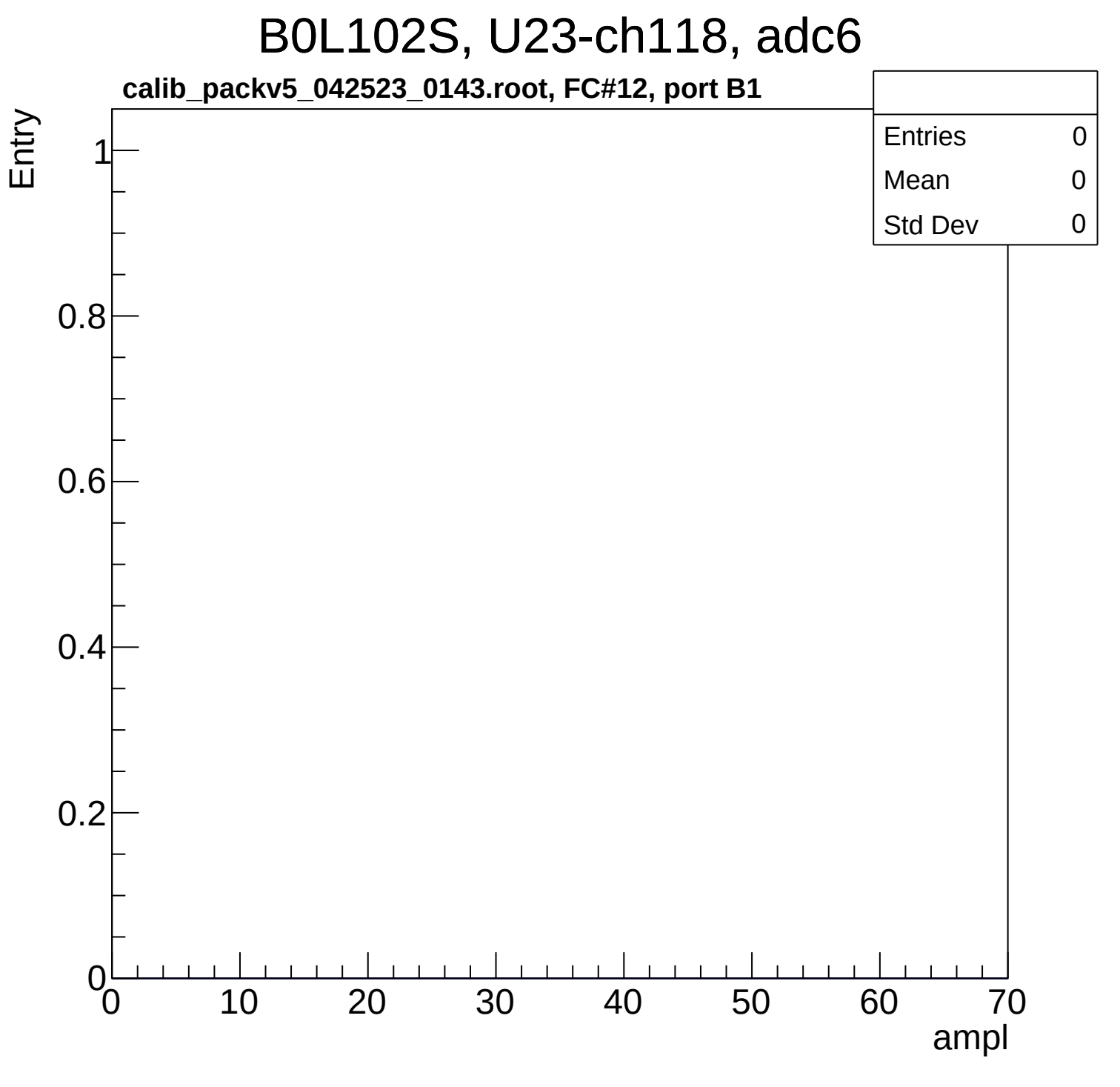
B0L102S, U23-ch118, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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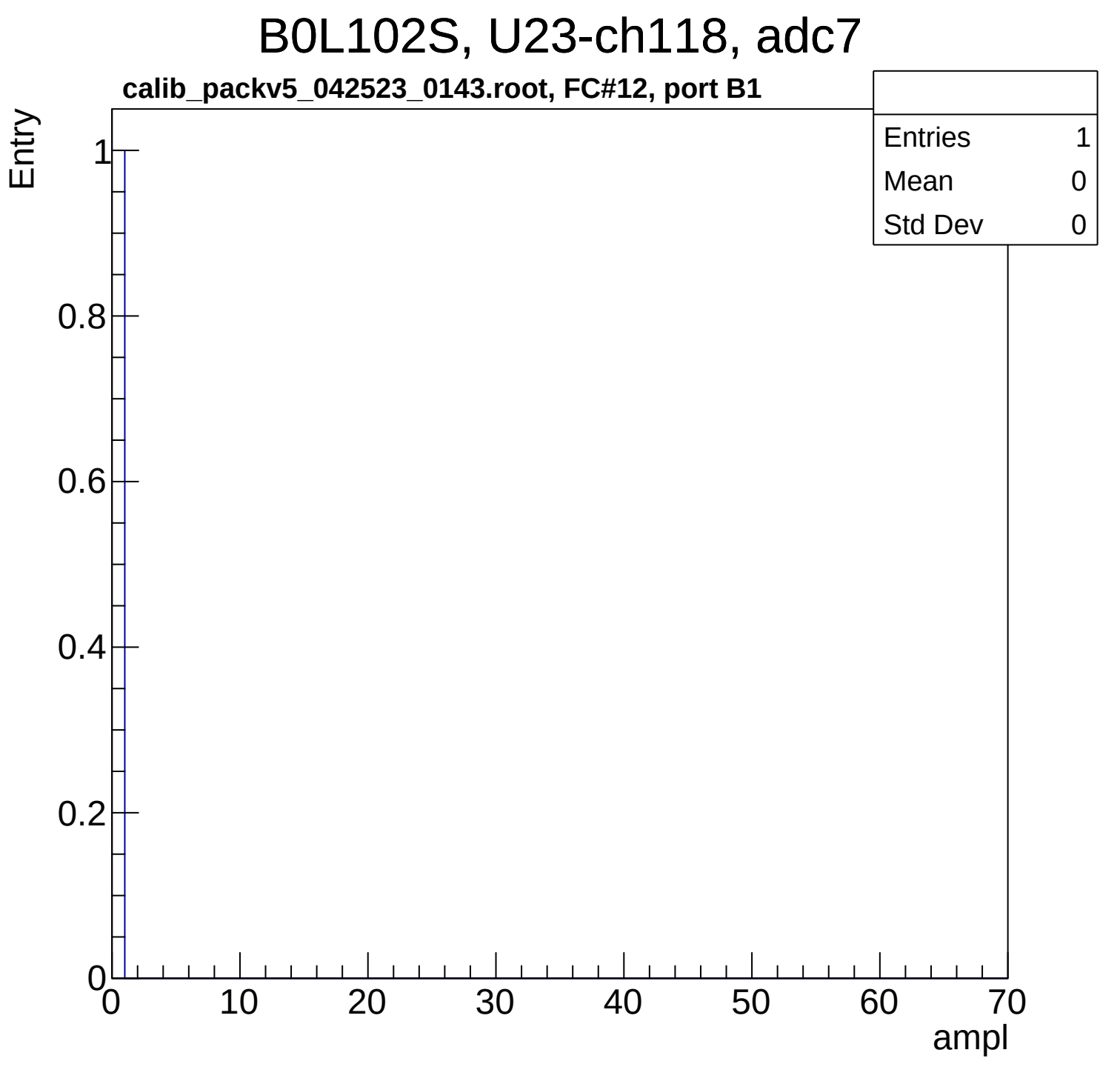
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch118, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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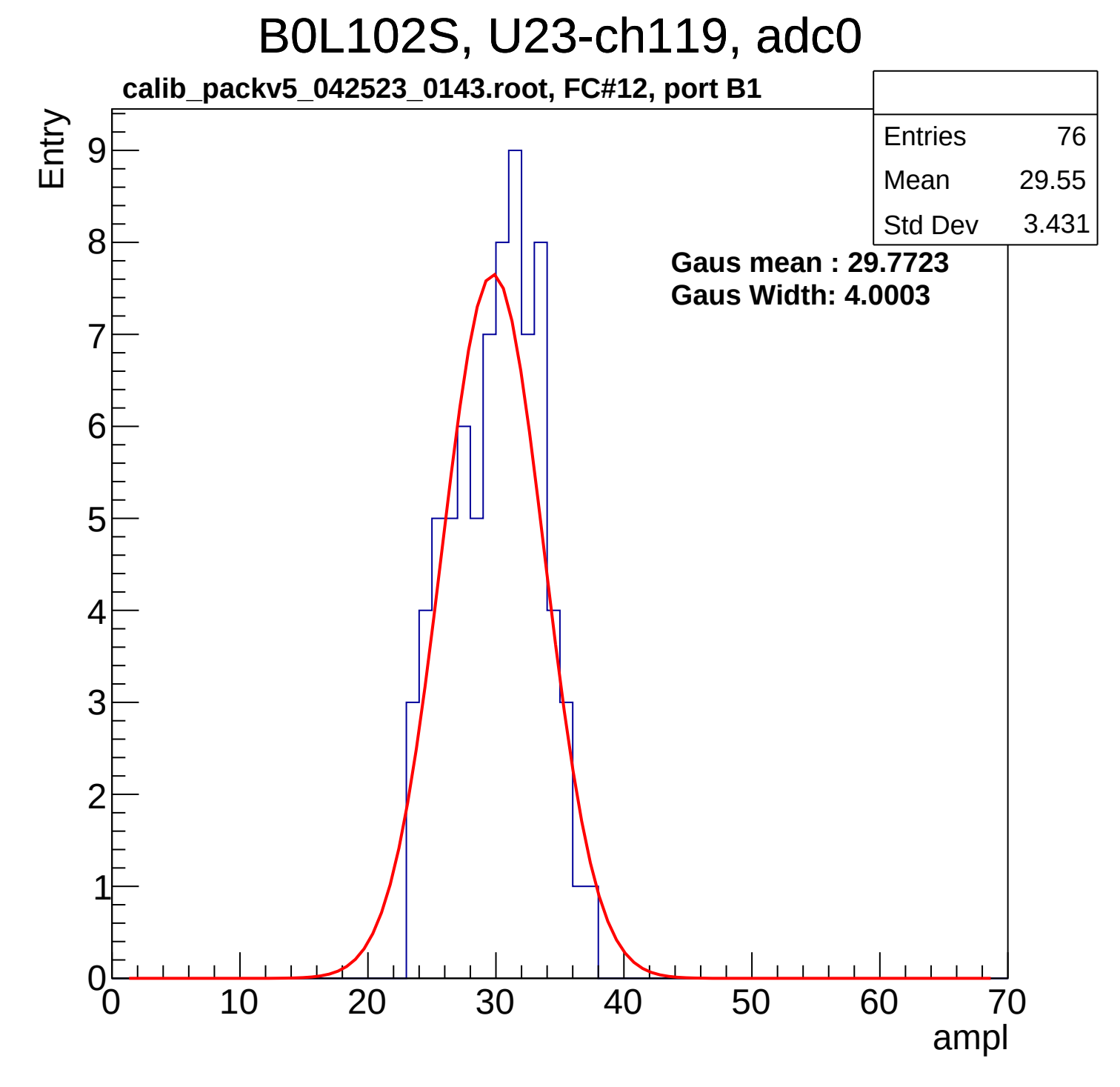
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch119, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 29.55
Std Dev 3.431
Gaus mean : 29.7723
Gaus Width: 4.0003
Entry
ampl
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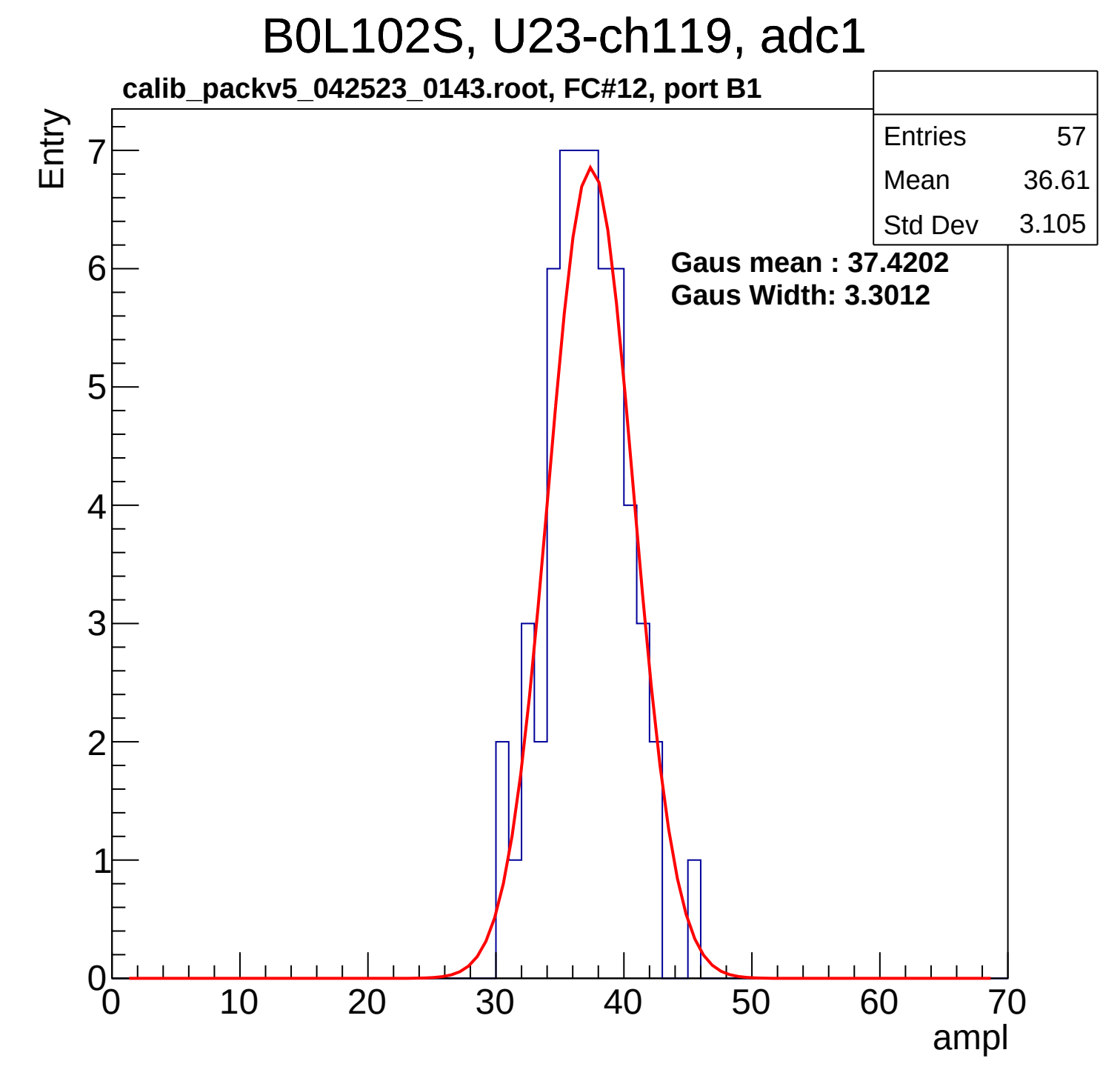
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch119, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 57
Mean 36.61
Std Dev 3.105
Gaus mean : 37.4202
Gaus Width: 3.3012
Entry
ampl
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2
3
4
5
6
7
0
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60
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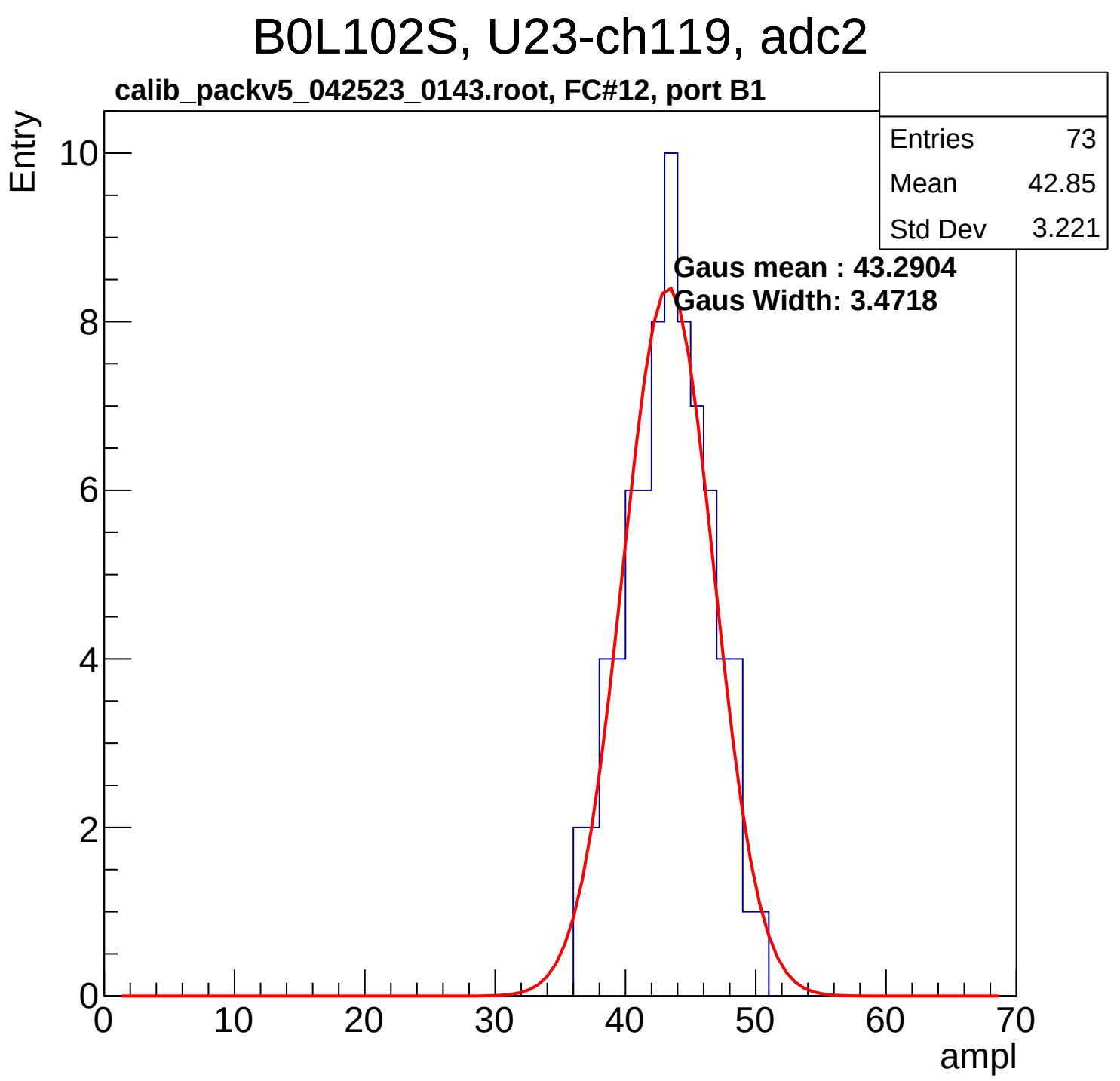
B0L102S, U23-ch119, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 73
Mean 42.85
Std Dev 3.221
Gaus mean : 43.2904
Gaus Width: 3.4718
Entry
ampl
0
2
4
6
8
10
0
10
20
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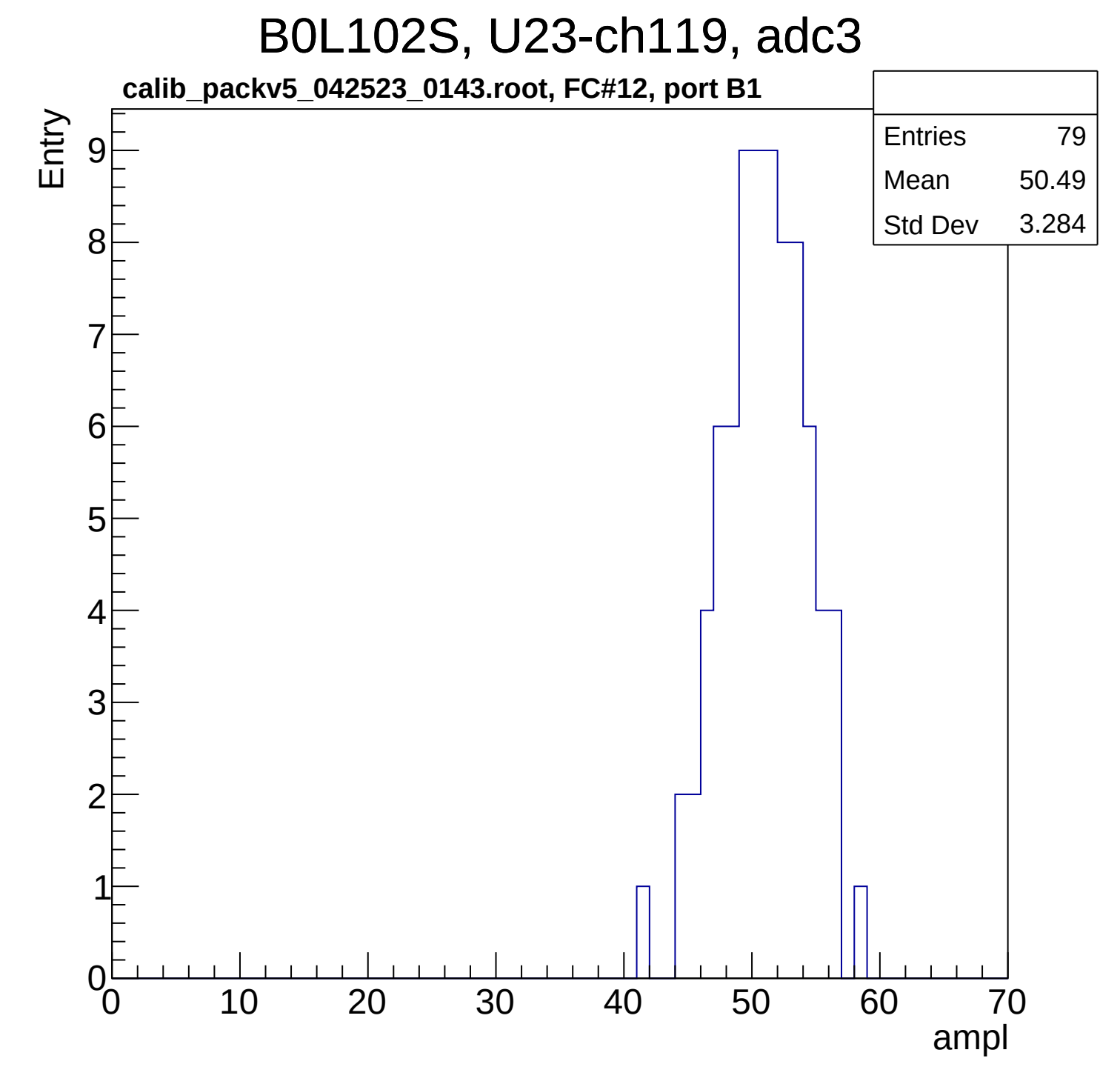
40
50
60
70


B0L102S, U23-ch119, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 50.49
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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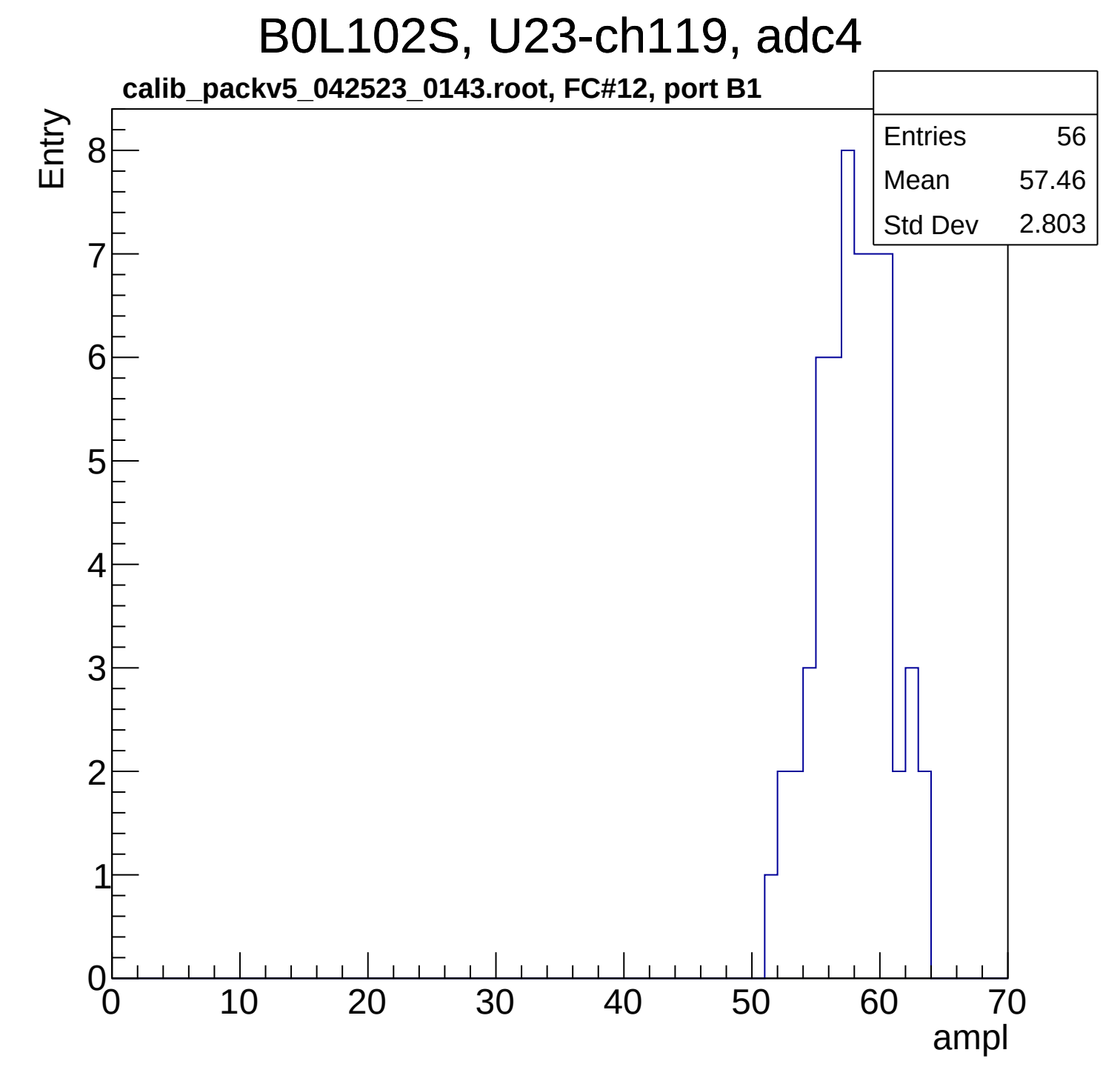
40
50
60
70


B0L102S, U23-ch119, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 56
Mean 57.46
Std Dev 2.803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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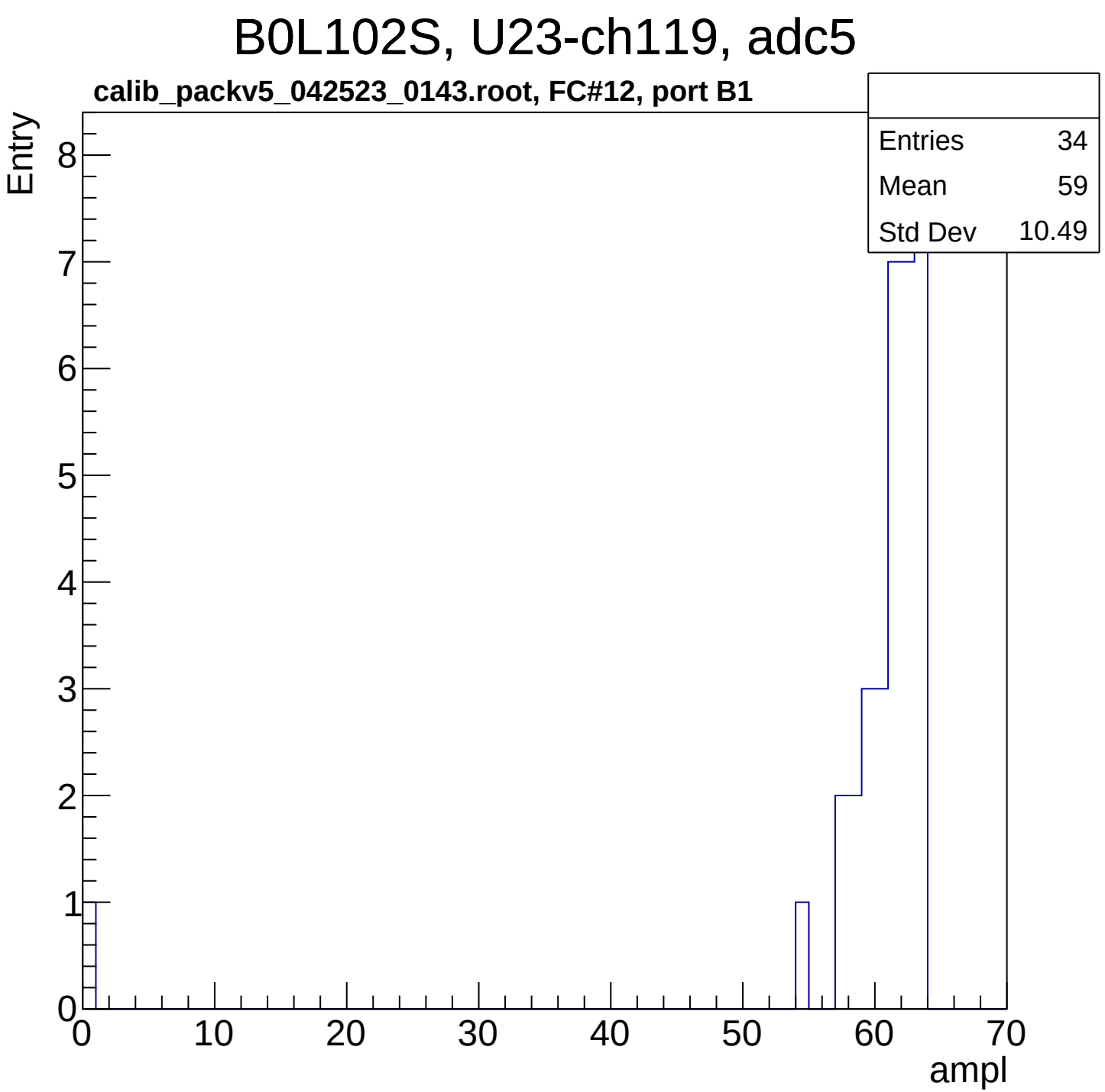
B0L102S, U23-ch119, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 59
Std Dev 10.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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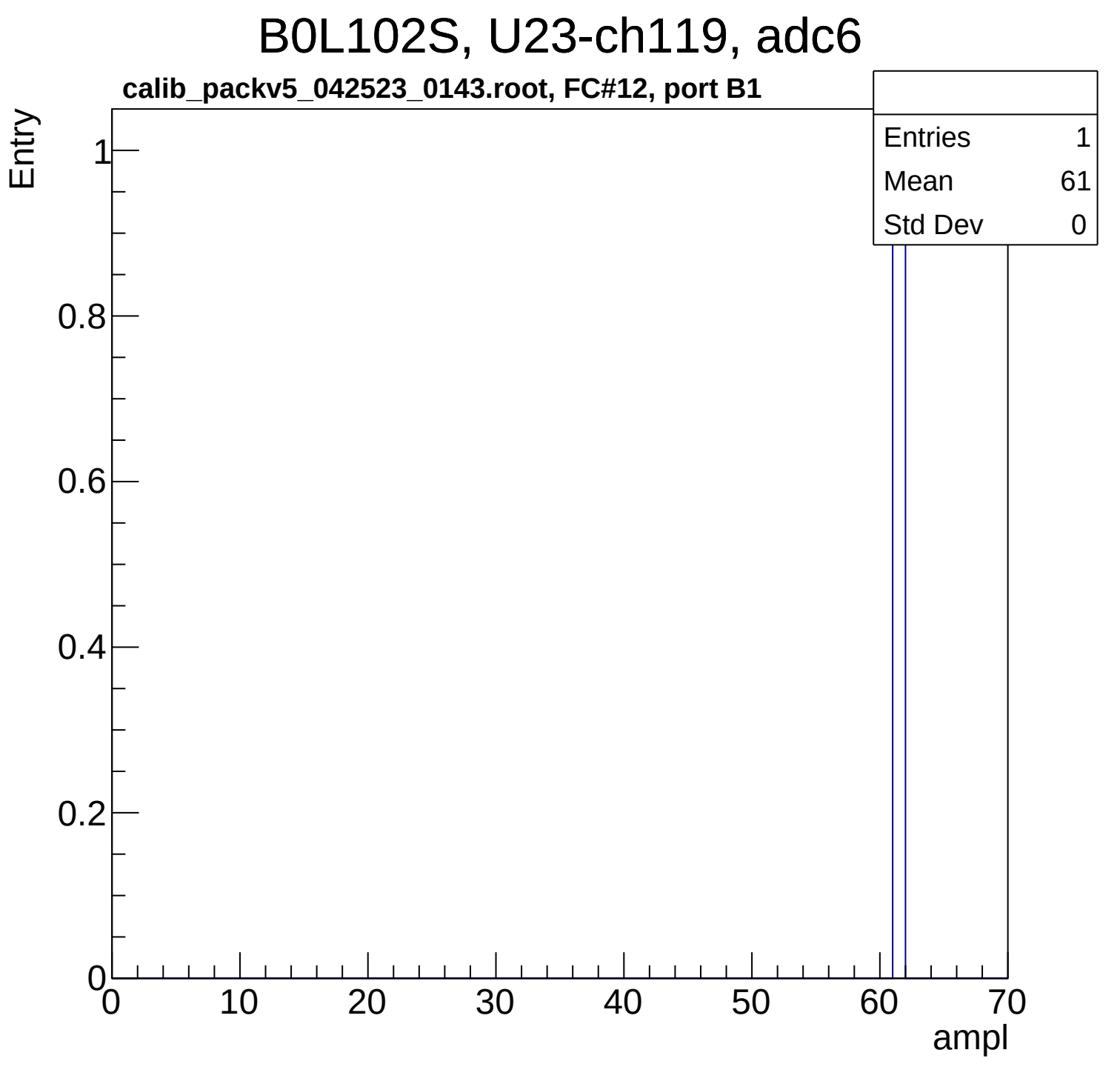
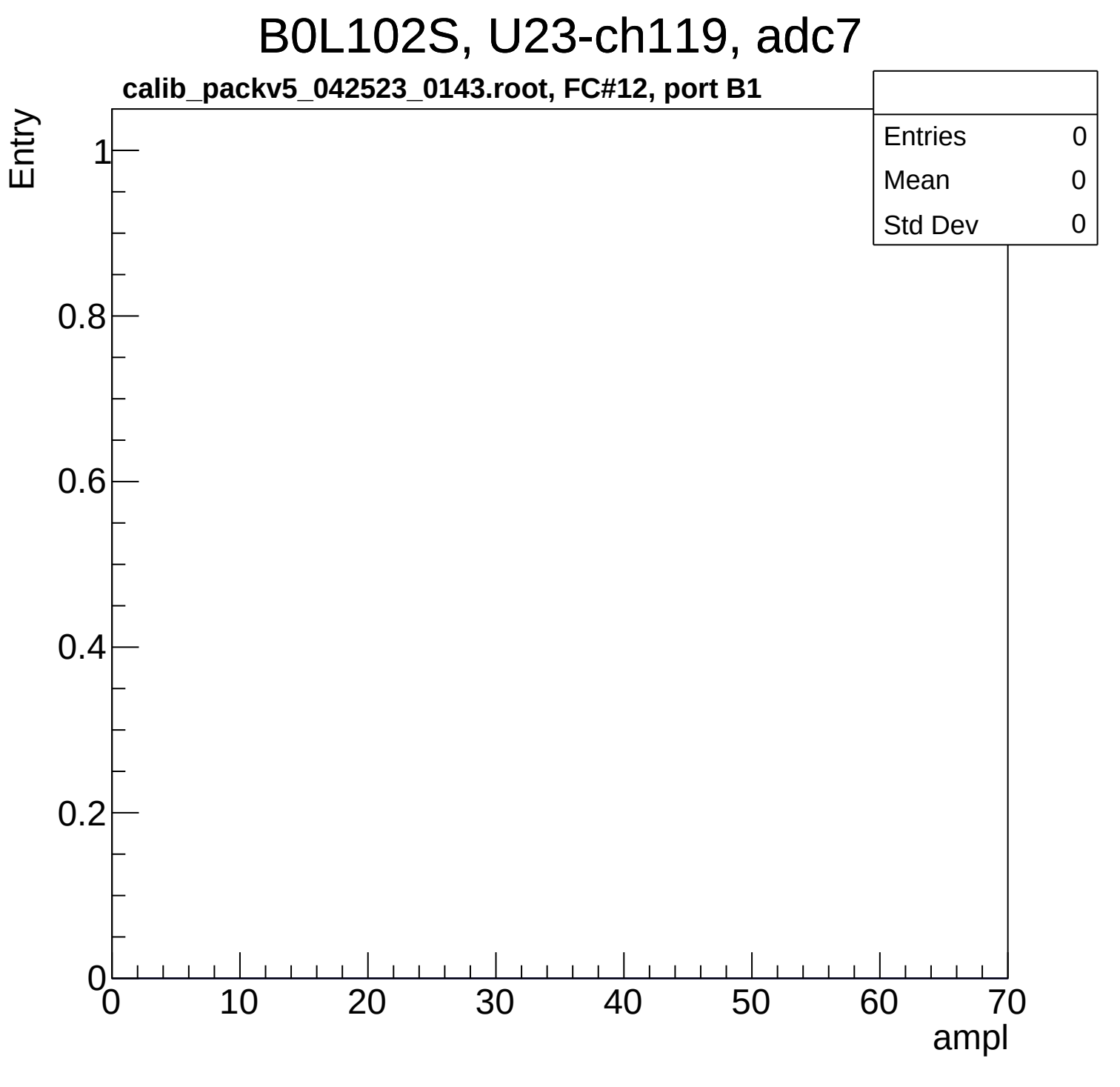
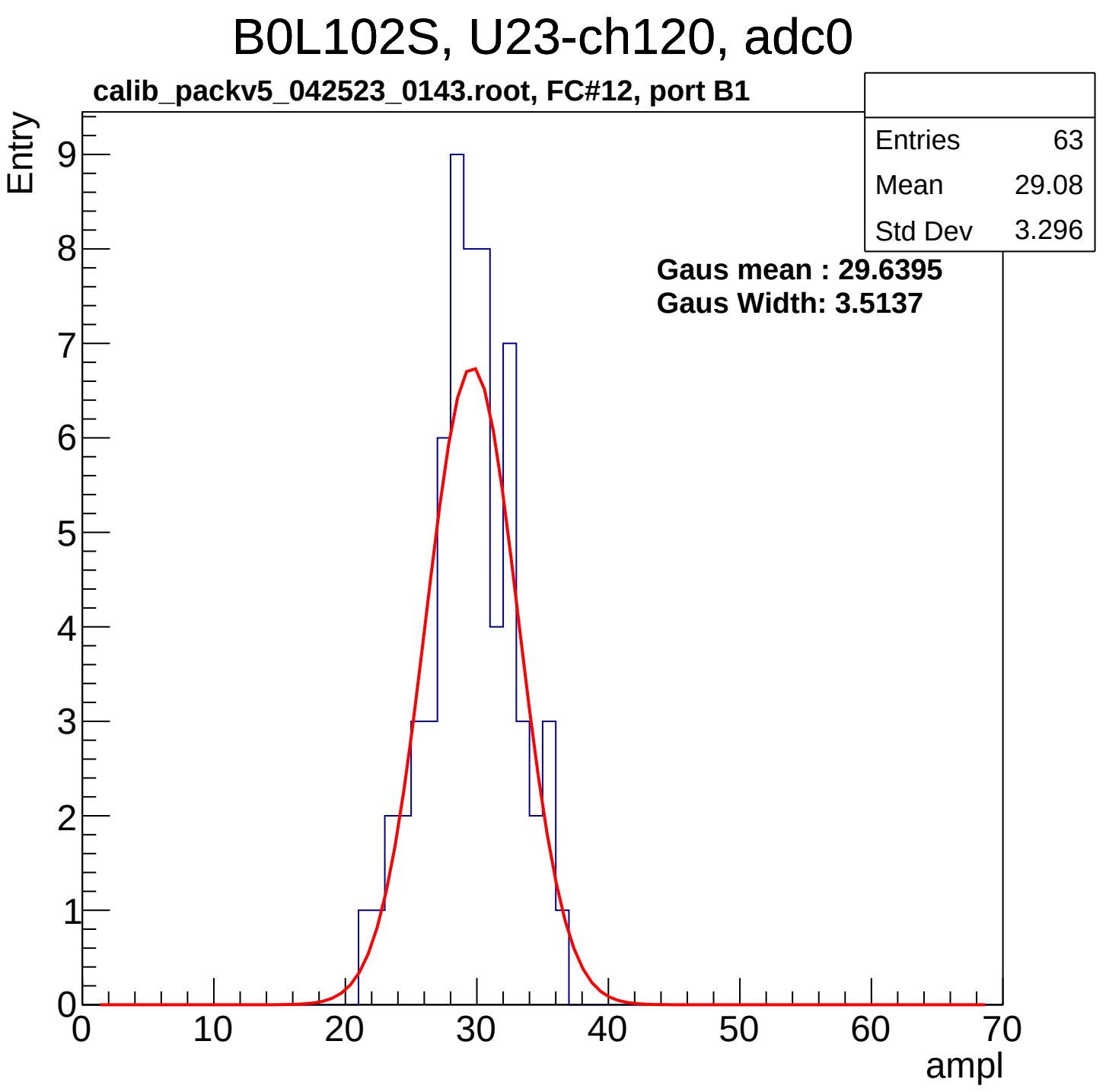
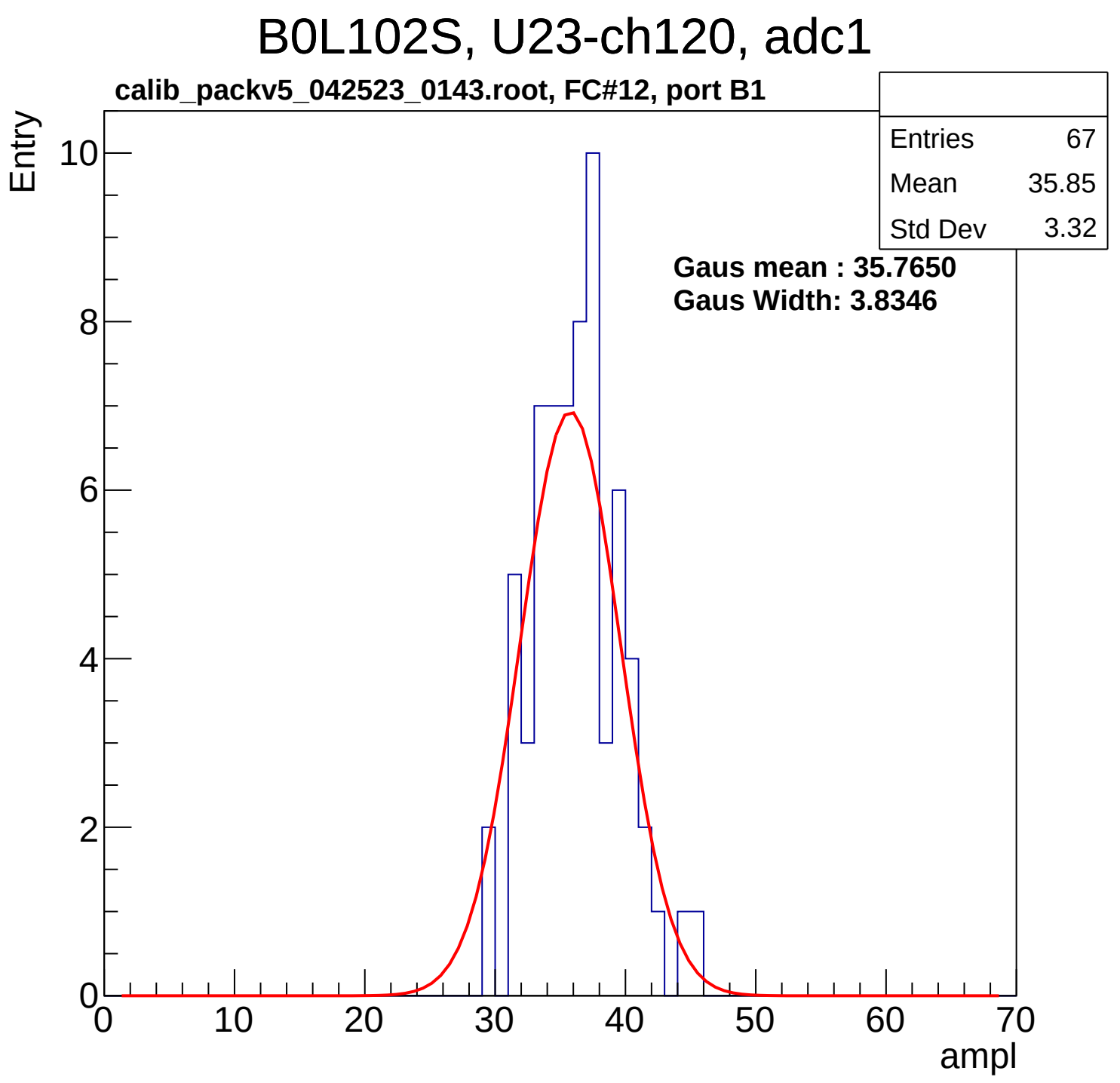
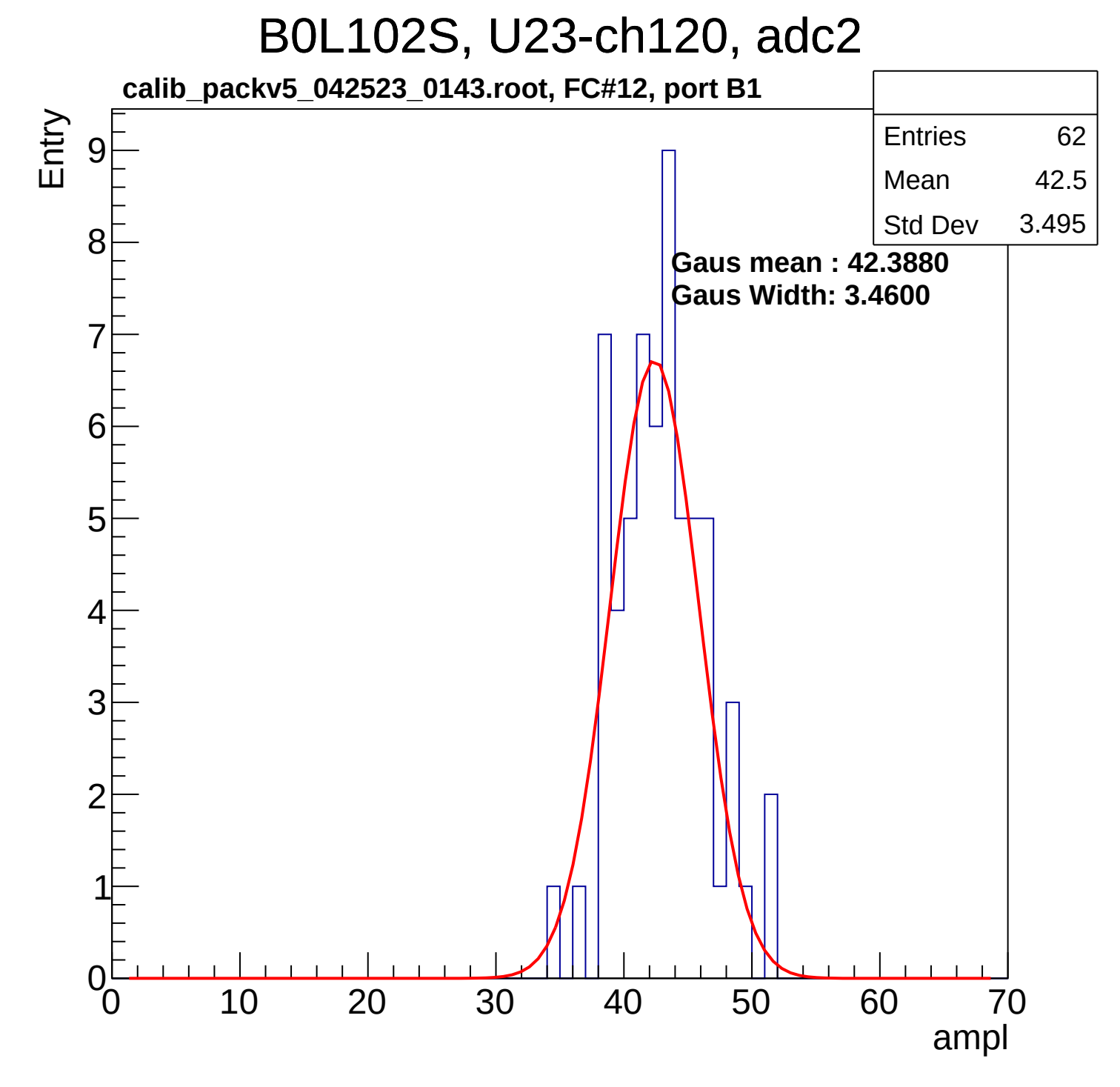
20
30
40
50
60
70

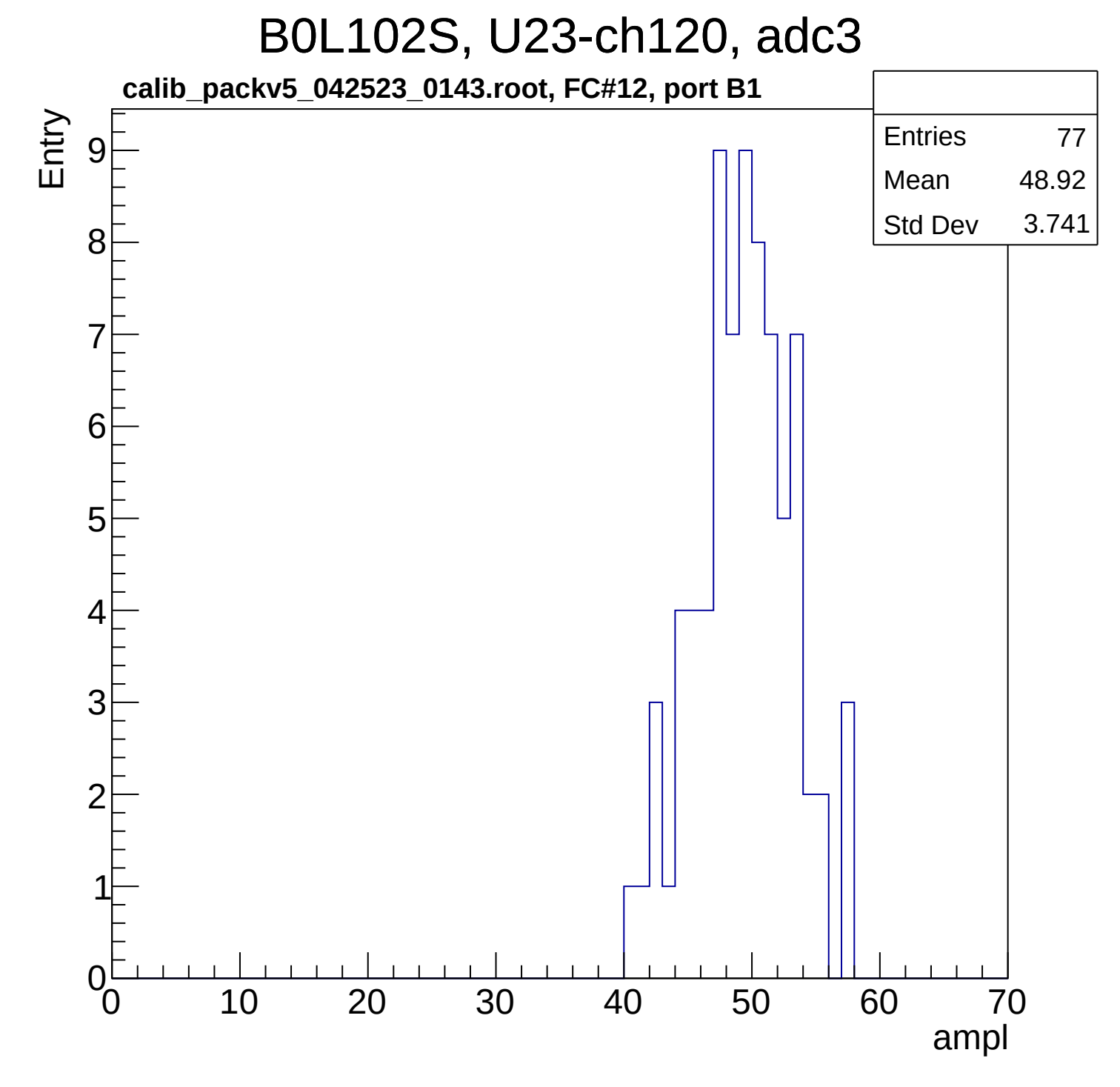

B0L102S, U23-ch119, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

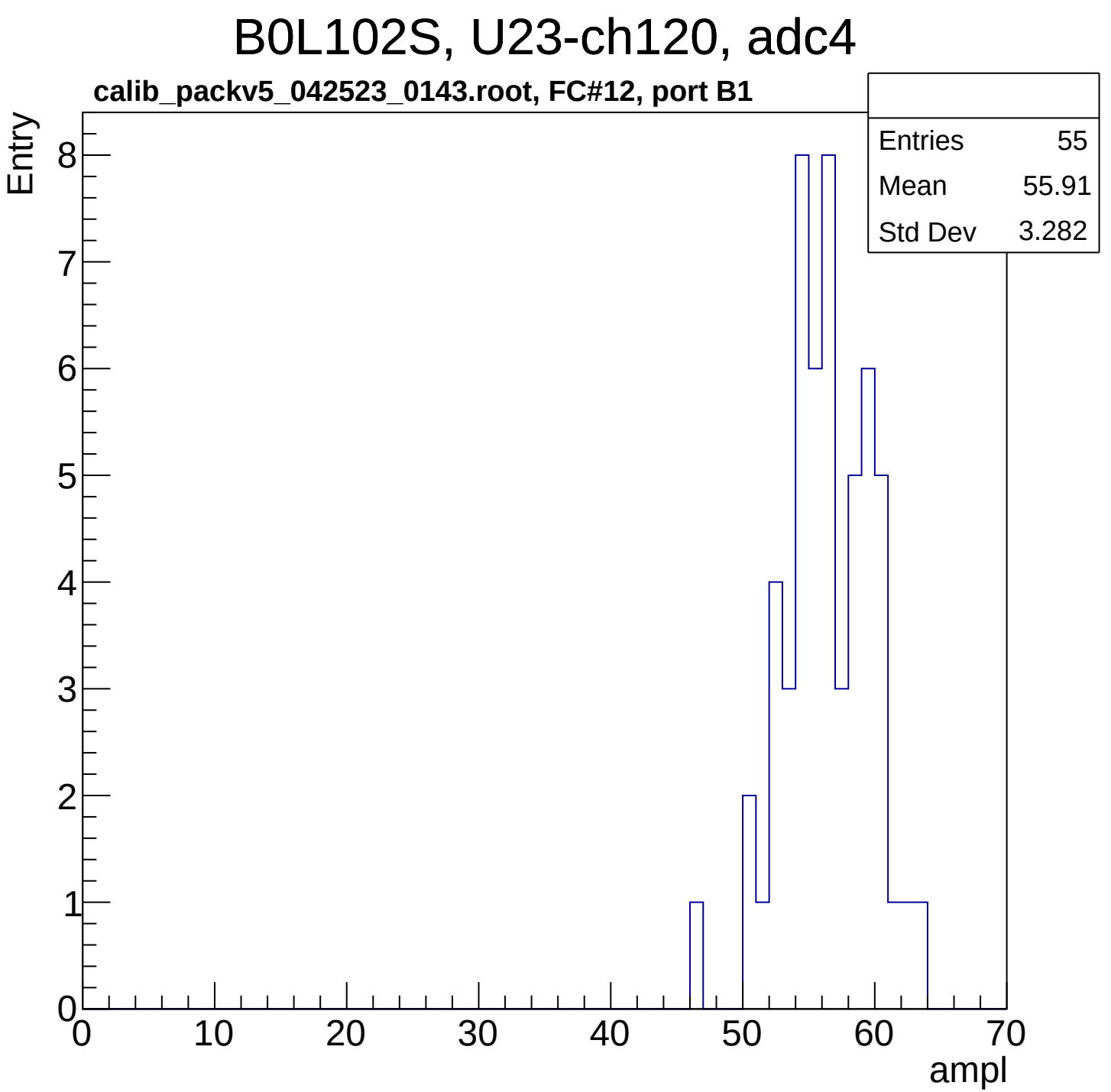

B0L102S, U23-ch119, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

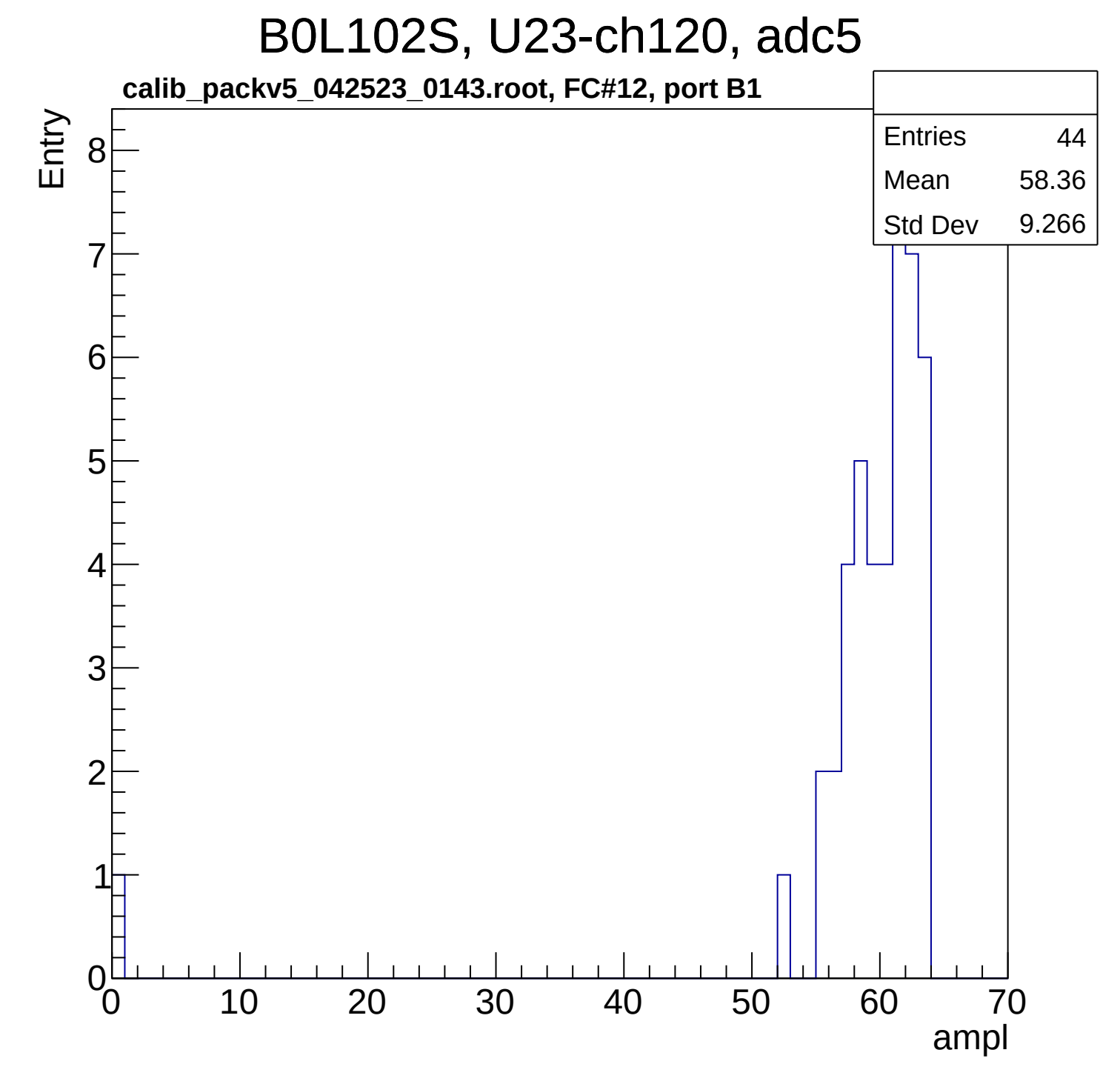

B0L102S, U23-ch120, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 29.08
Std Dev 3.296
Gaus mean : 29.6395
Gaus Width: 3.5137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

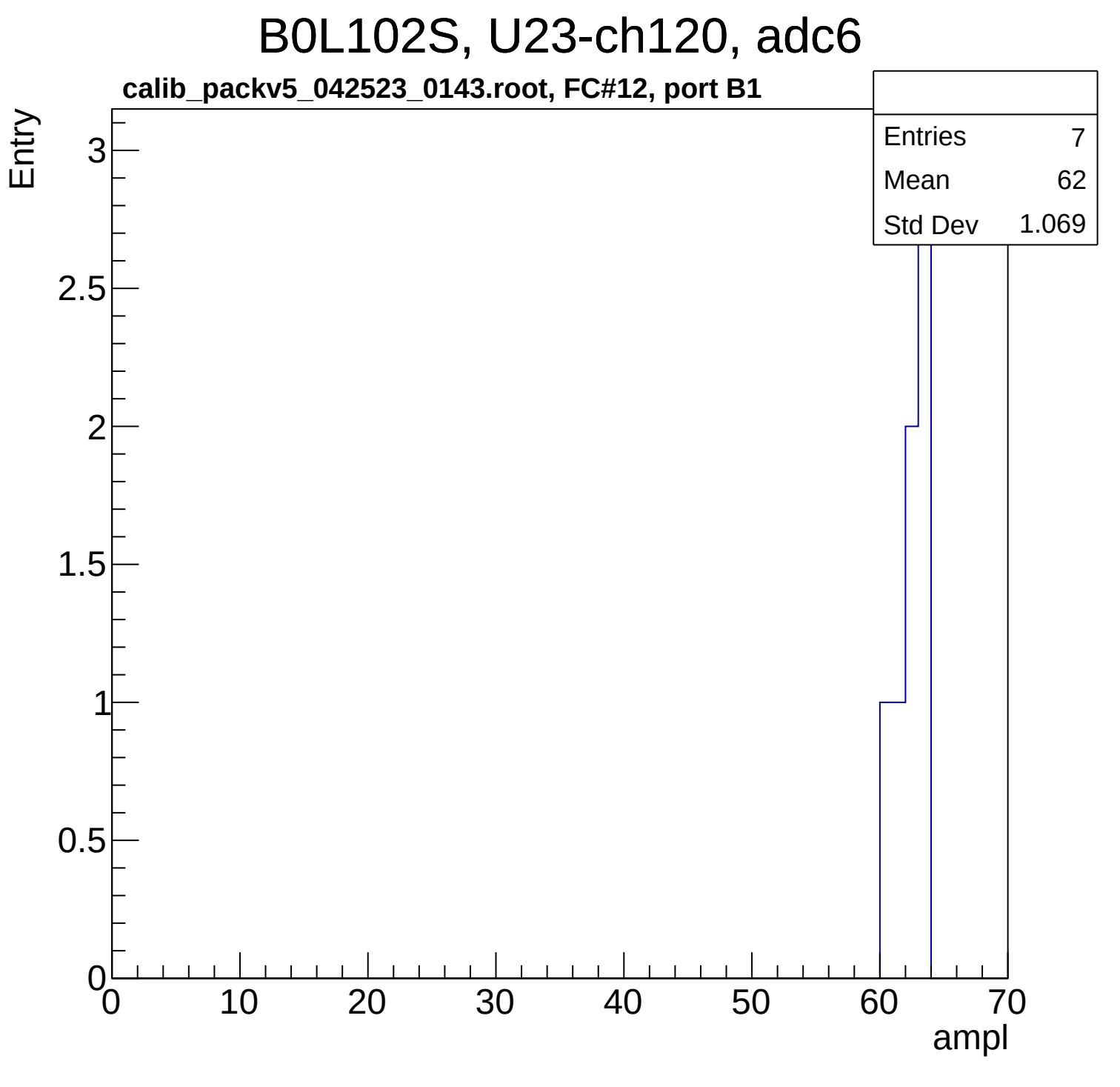

B0L102S, U23-ch120, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 67
Mean 35.85
Std Dev 3.32
Gaus mean : 35.7650
Gaus Width: 3.8346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

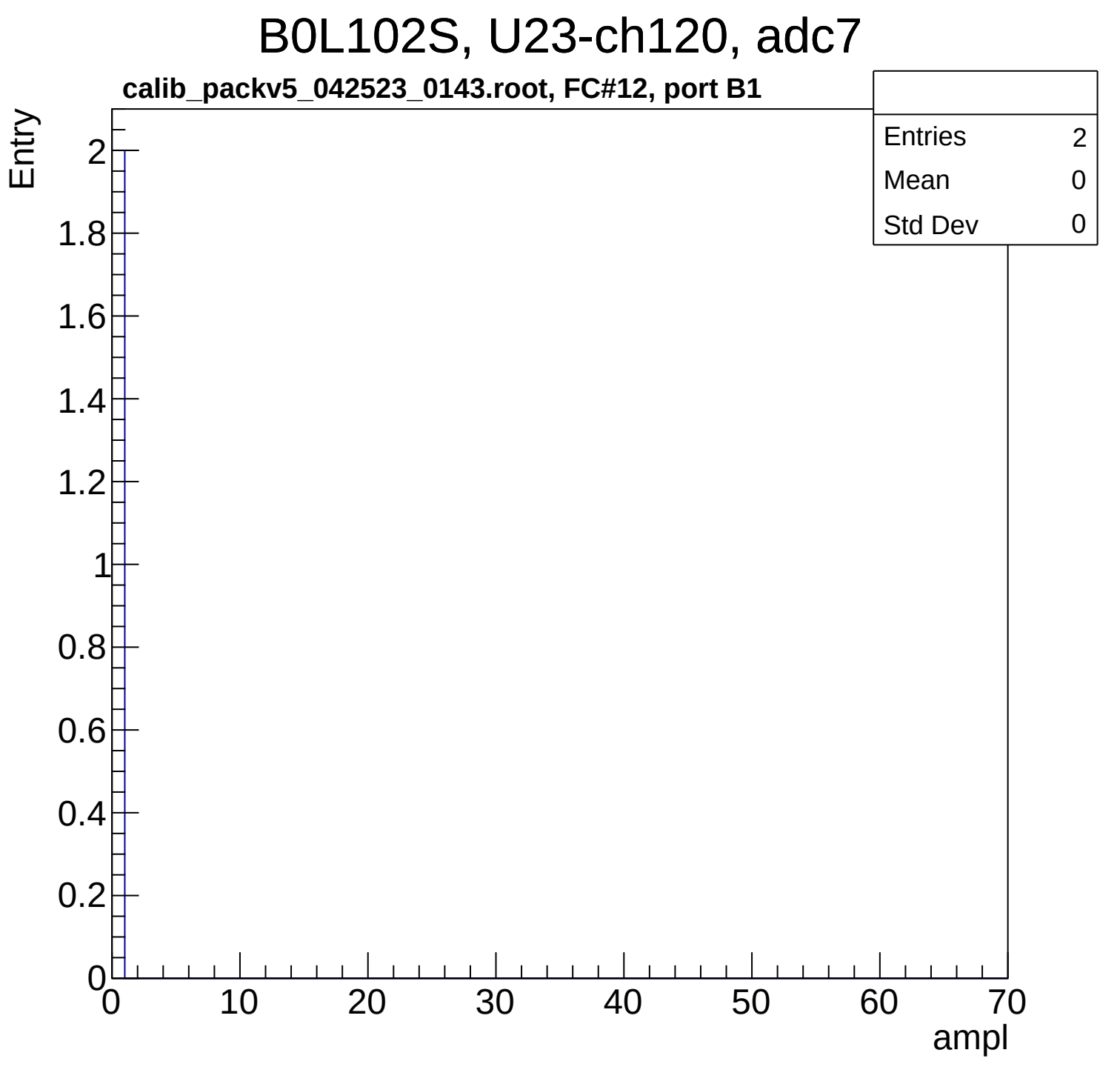

B0L102S, U23-ch120, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 42.5
Std Dev 3.495
Gaus mean : 42.3880
Gaus Width: 3.4600
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

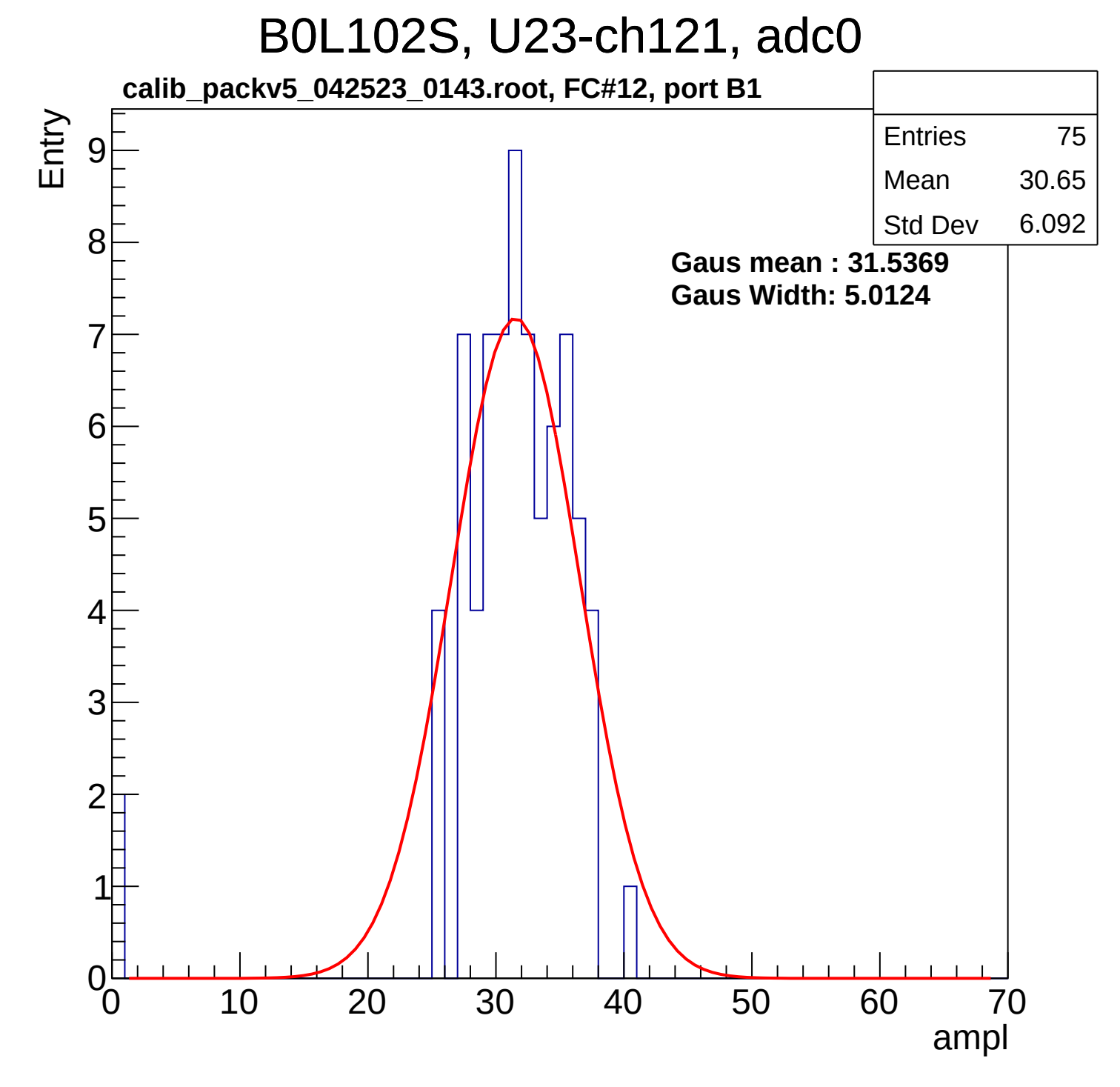

B0L102S, U23-ch120, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 48.92
Std Dev 3.741
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

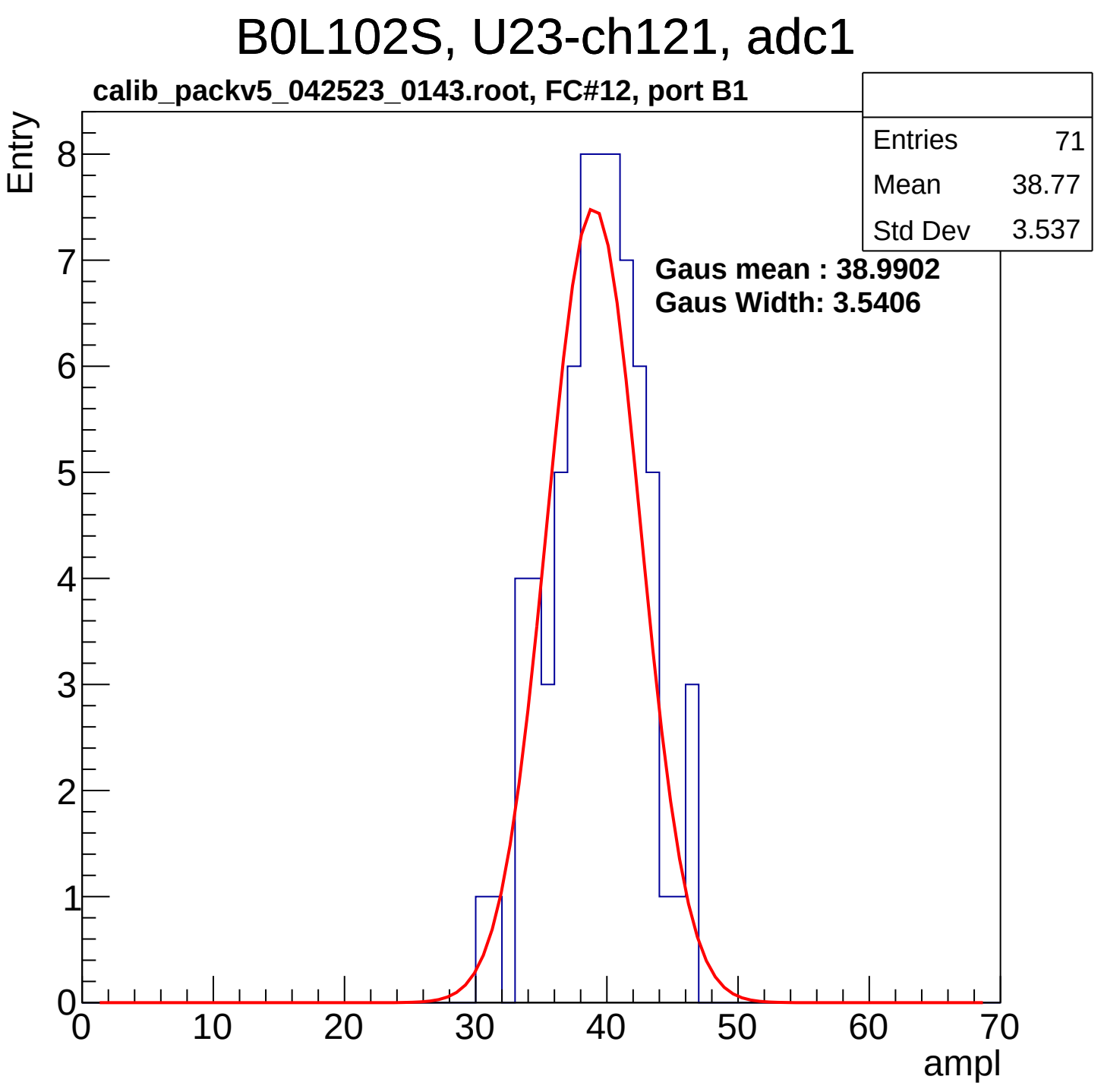

B0L102S, U23-ch120, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 55
Mean 55.91
Std Dev 3.282
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

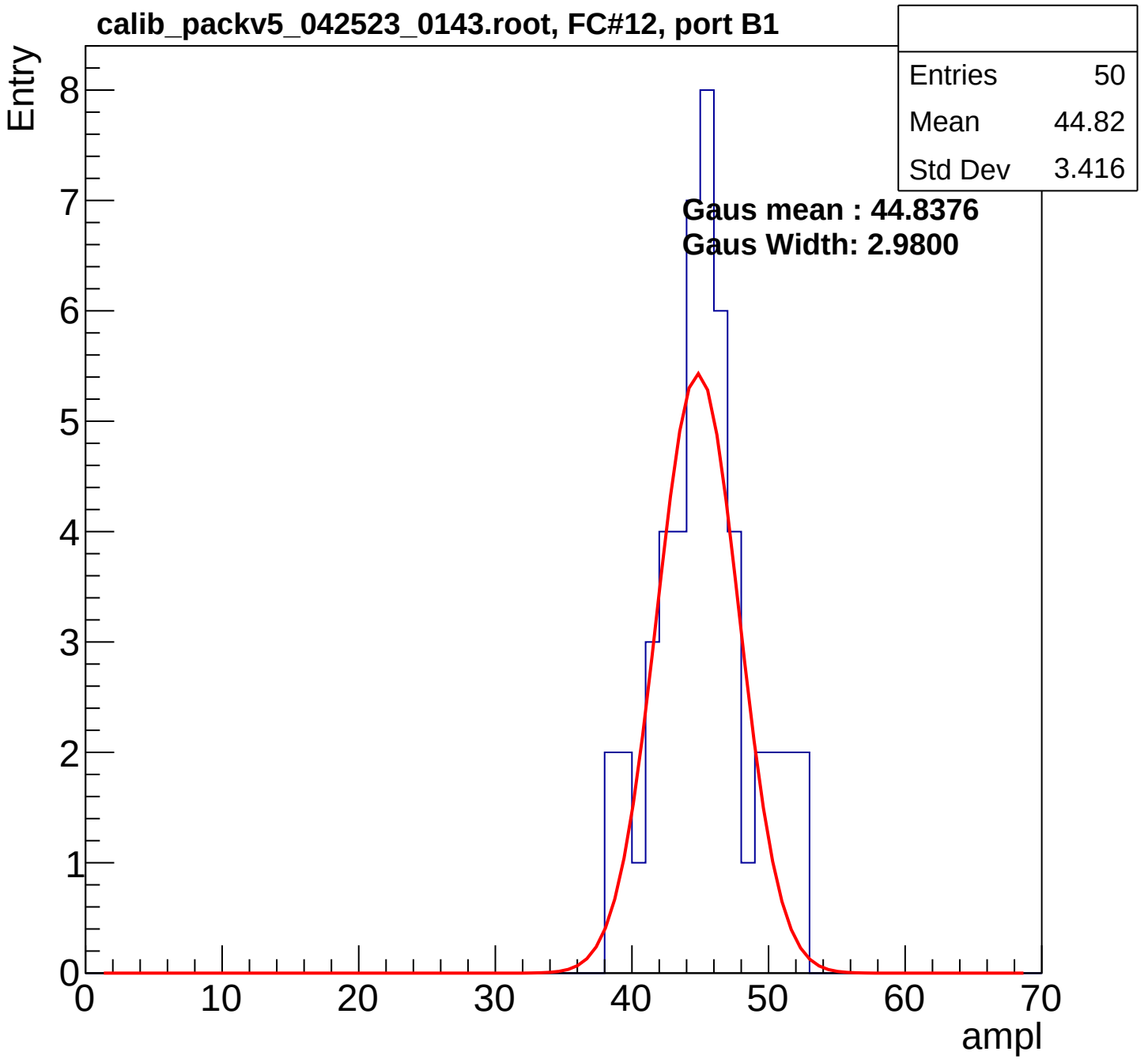

B0L102S, U23-ch120, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 58.36
Std Dev 9.266
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

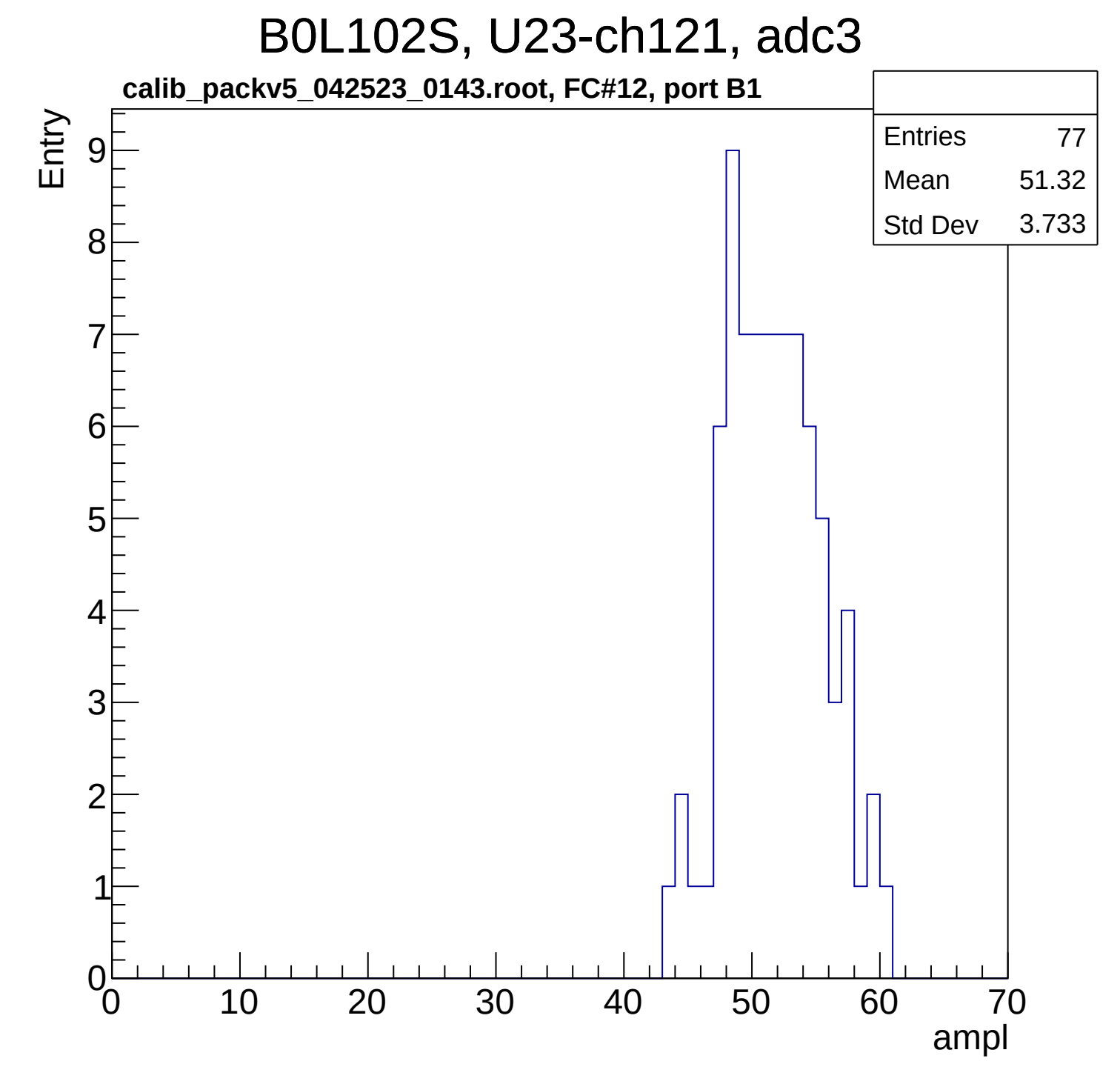

B0L102S, U23-ch120, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 7
Mean 62
Std Dev 1.069
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

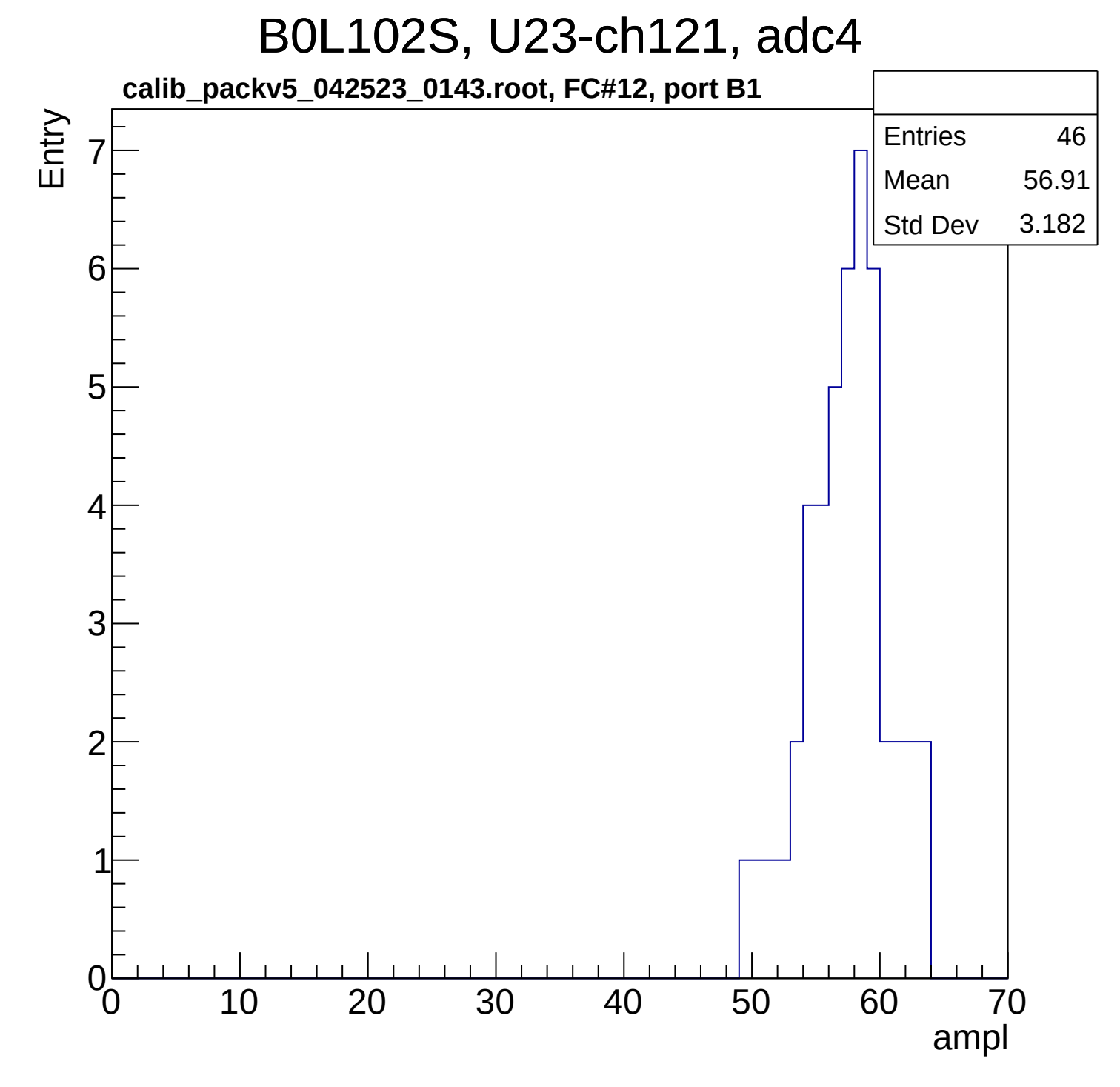

B0L102S, U23-ch120, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch121, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 30.65
Std Dev 6.092
Gaus mean : 31.5369
Gaus Width: 5.0124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch121, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 38.77
Std Dev 3.537
Gaus mean : 38.9902
Gaus Width: 3.5406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch121, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 44.82
Std Dev 3.416
Gaus mean : 44.8376
Gaus Width: 2.9800
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch121, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 77
Mean 51.32
Std Dev 3.733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch121, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 46
Mean 56.91
Std Dev 3.182
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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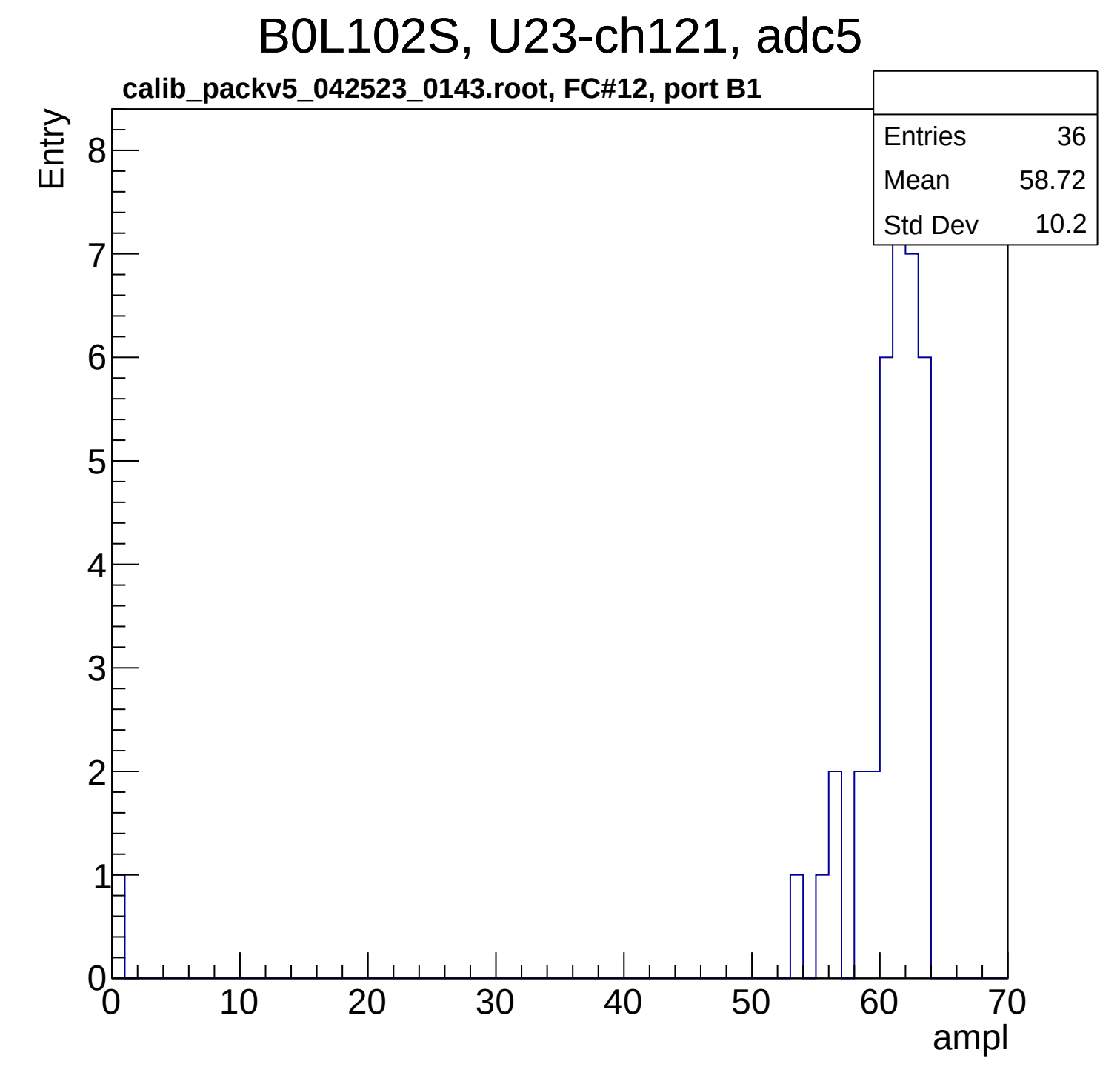
B0L102S, U23-ch121, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 36
Mean 58.72
Std Dev 10.2
Entry
ampl
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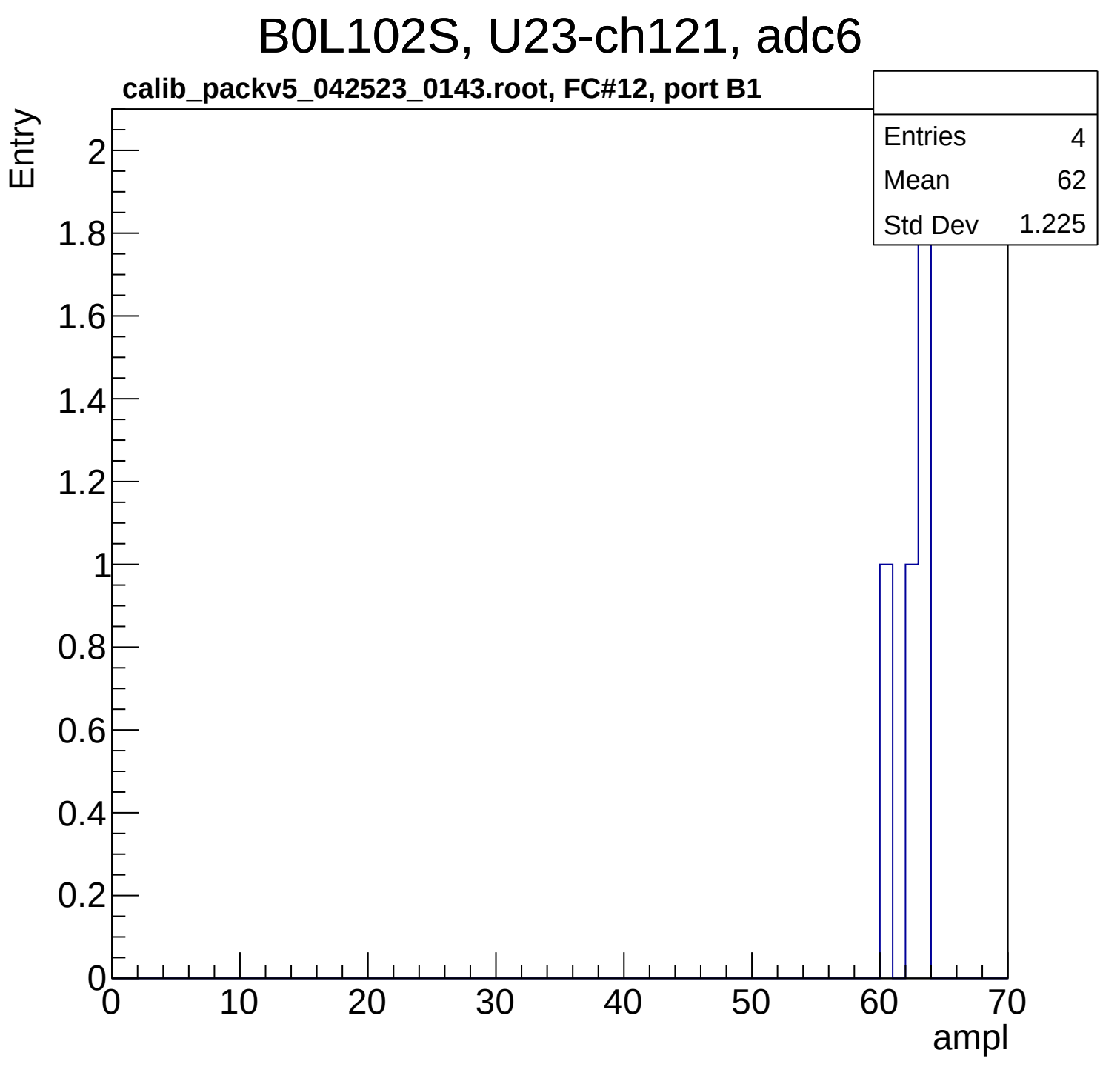
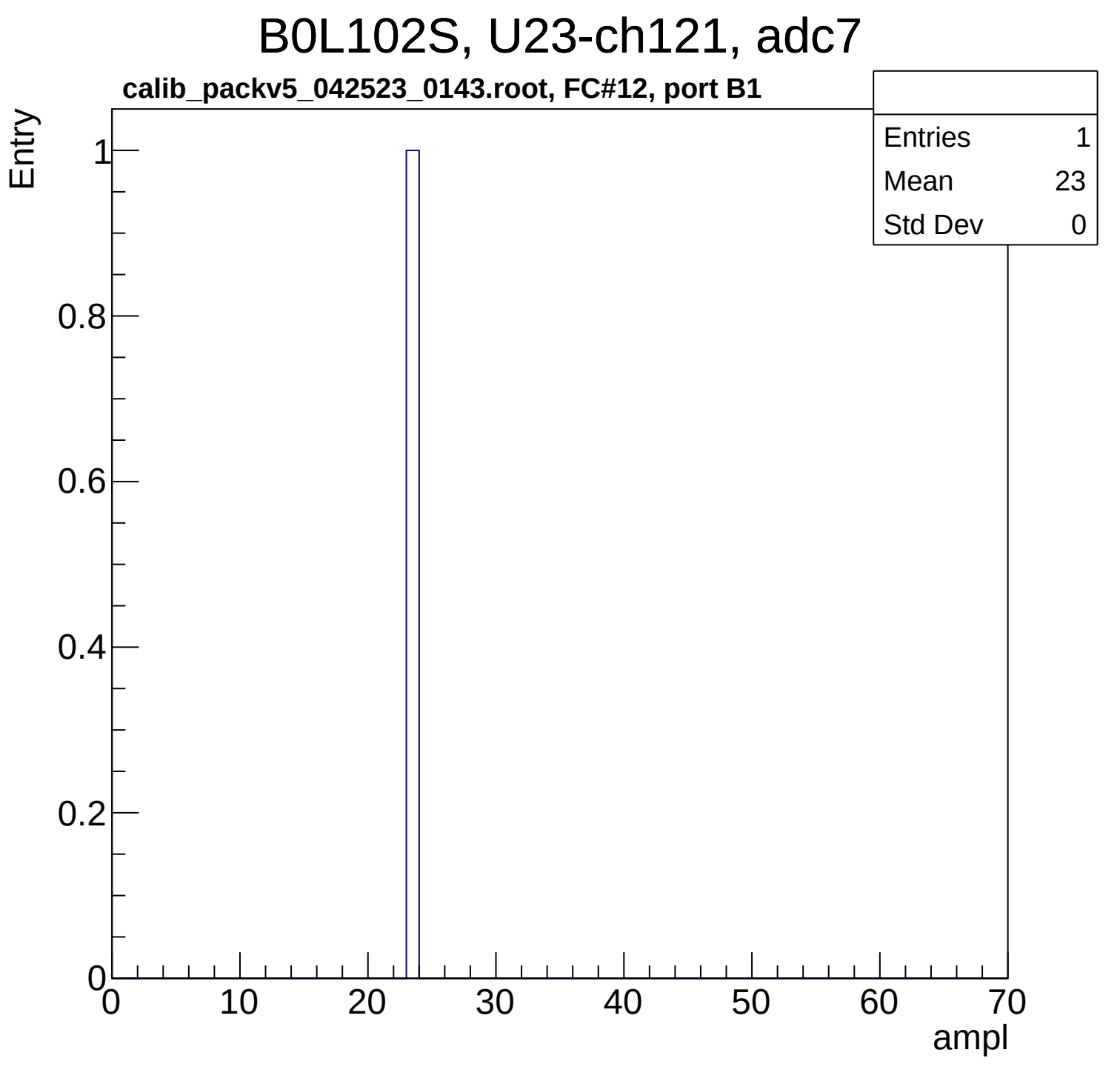
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch121, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch121, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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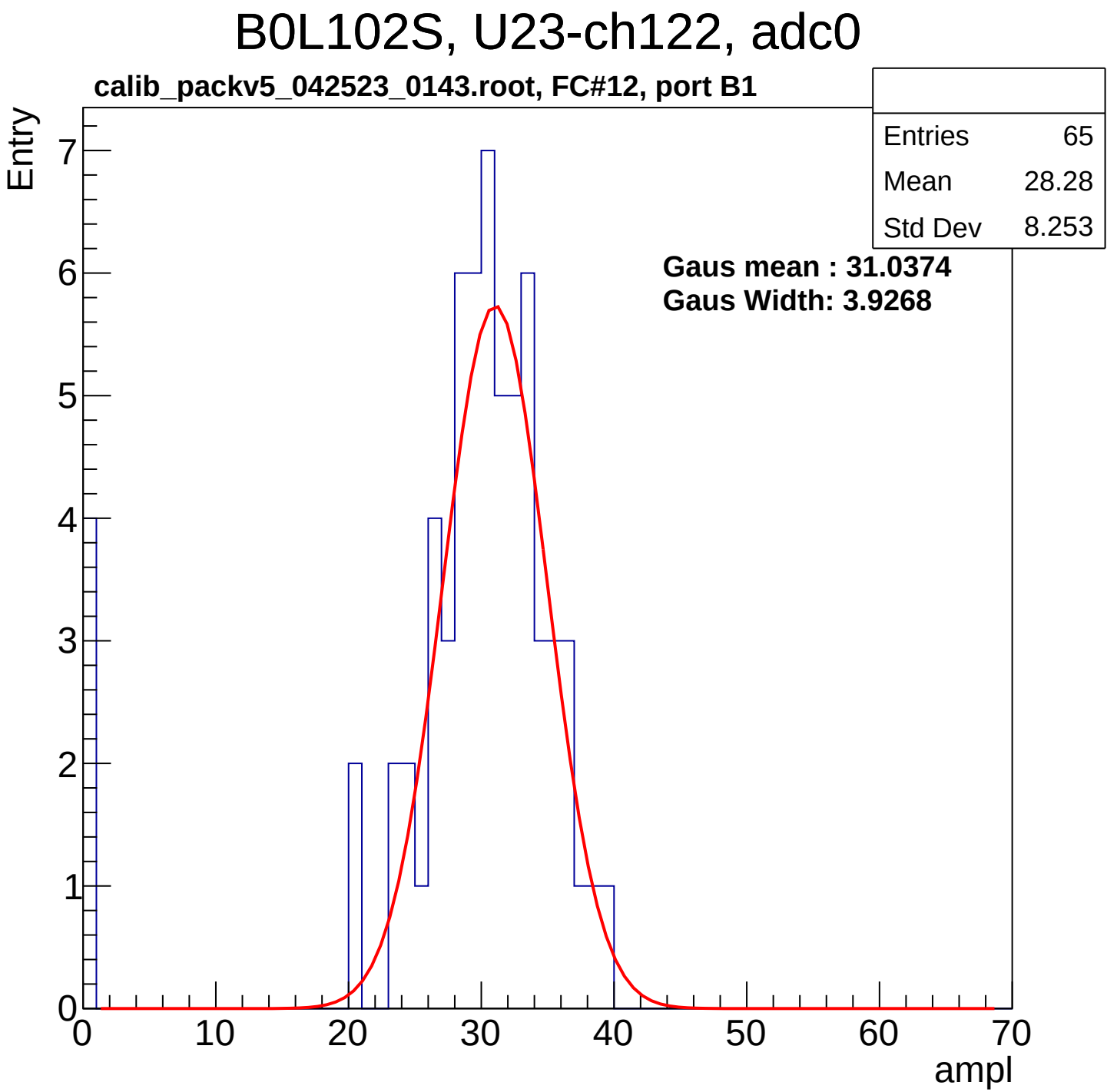
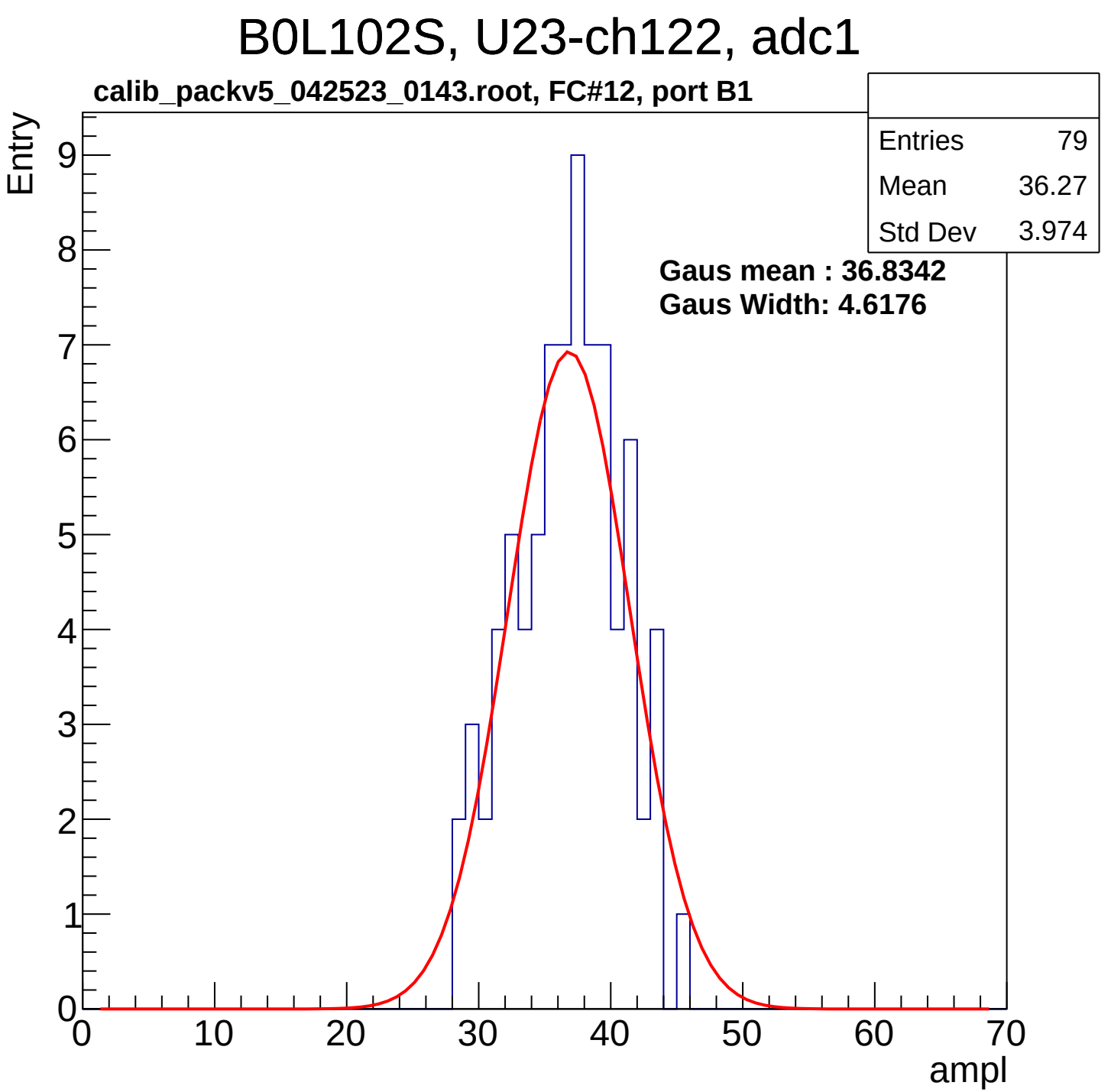
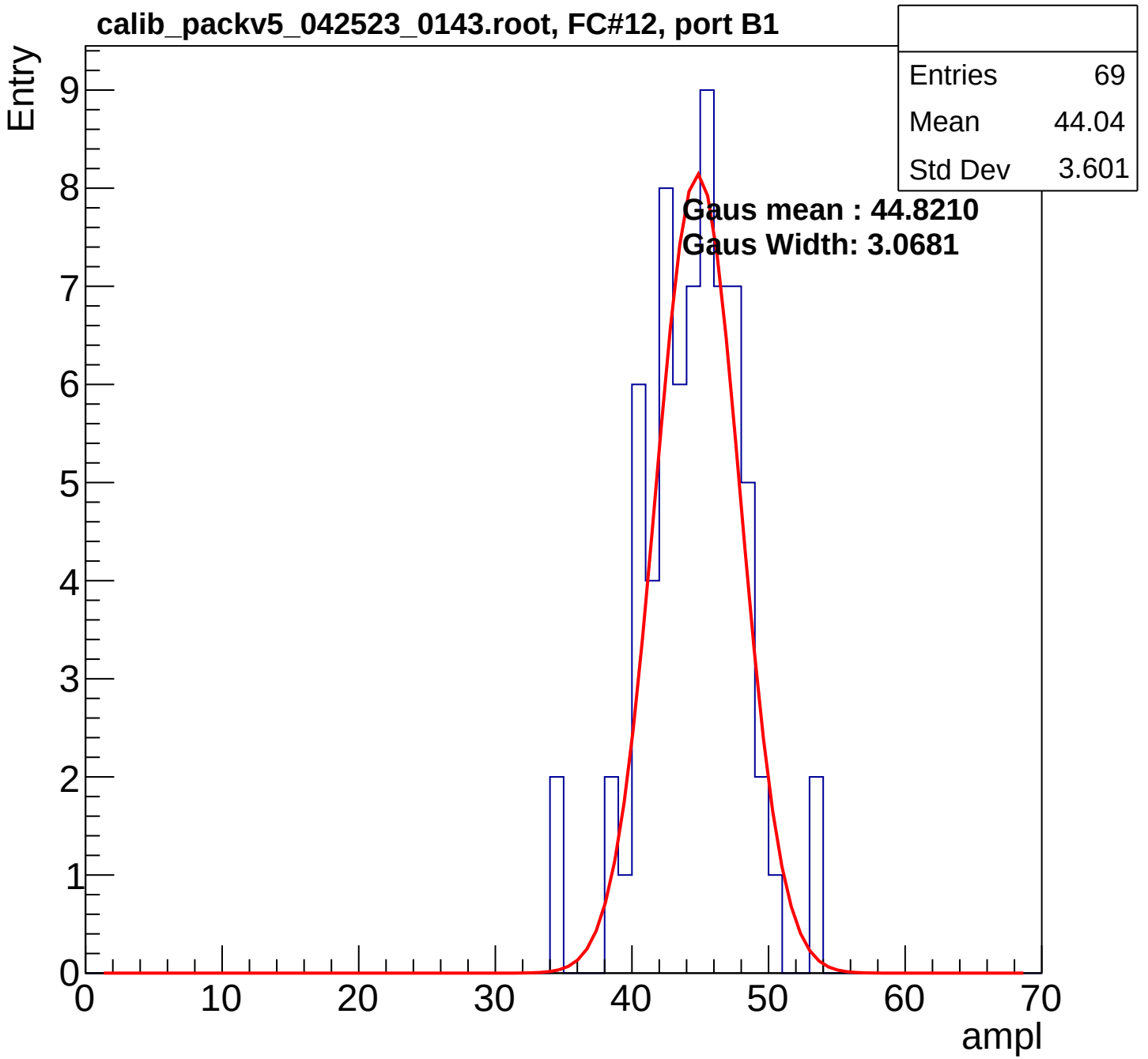
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch122, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 28.28
Std Dev 8.253
Gaus mean : 31.0374
Gaus Width: 3.9268
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch122, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 79
Mean 36.27
Std Dev 3.974
Gaus mean : 36.8342
Gaus Width: 4.6176
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch122, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 44.04
Std Dev 3.601
Gaus mean : 44.8210
Gaus Width: 3.0681
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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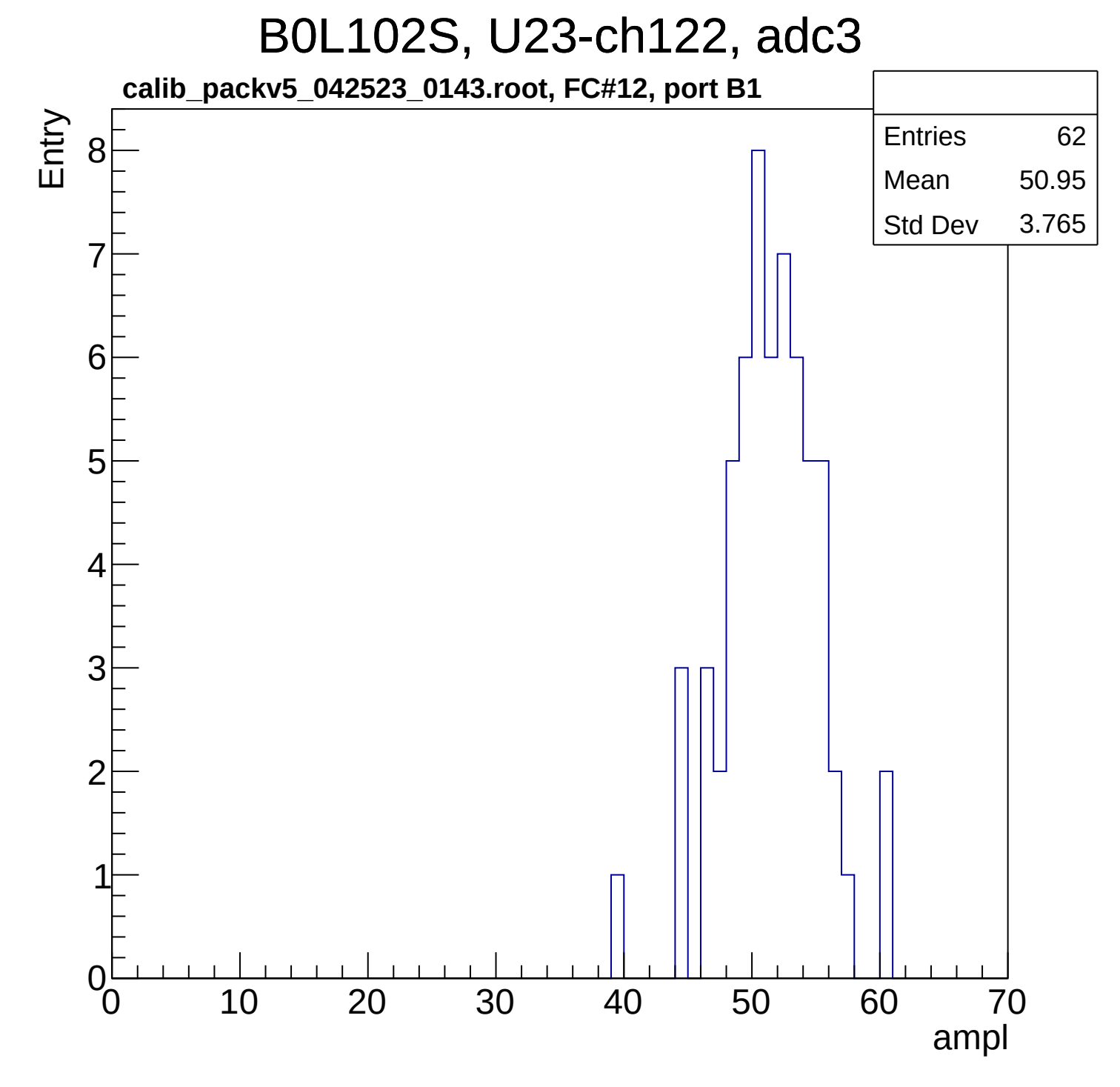
B0L102S, U23-ch122, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 50.95
Std Dev 3.765
Entry
ampl
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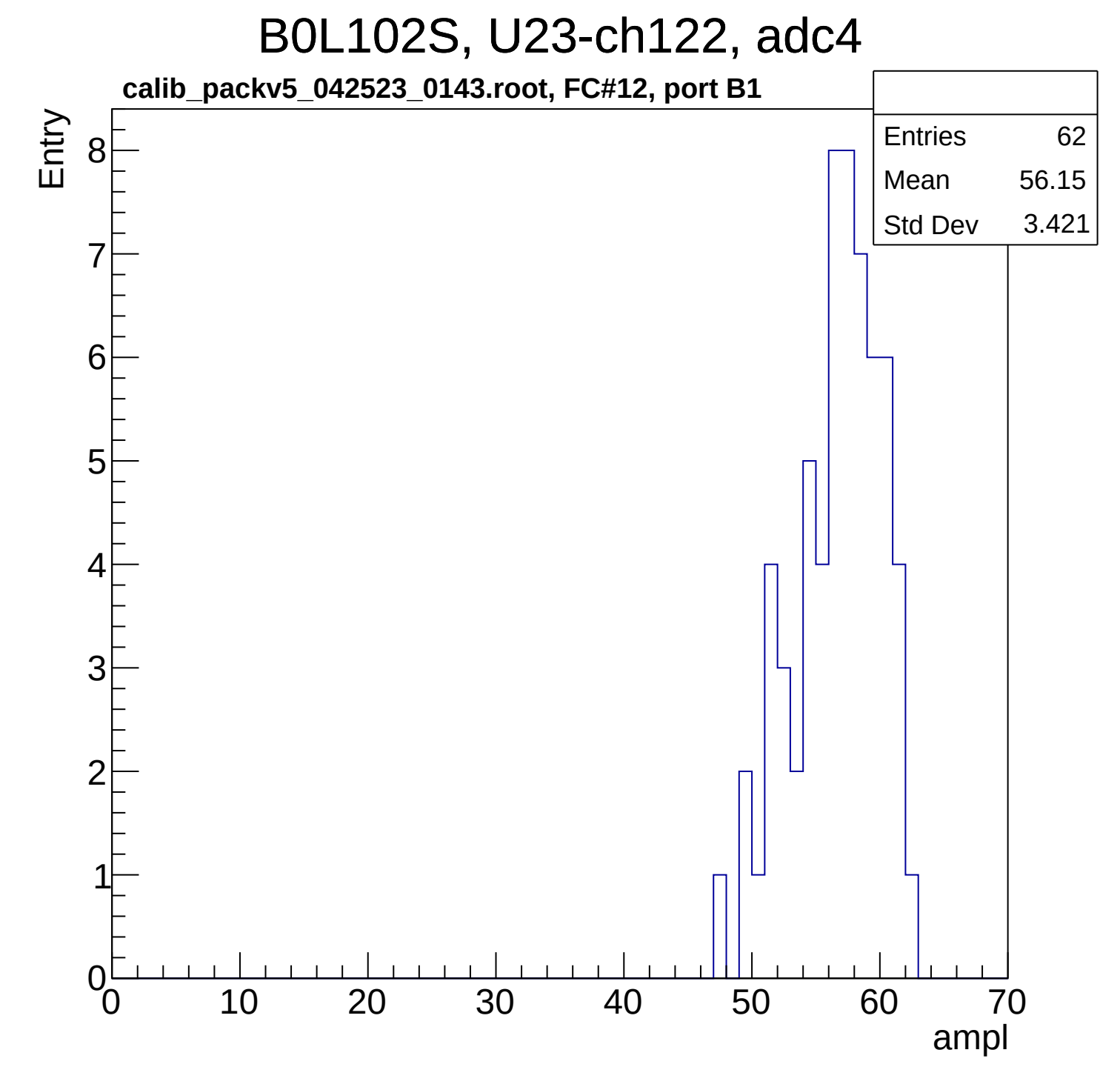
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch122, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 62
Mean 56.15
Std Dev 3.421
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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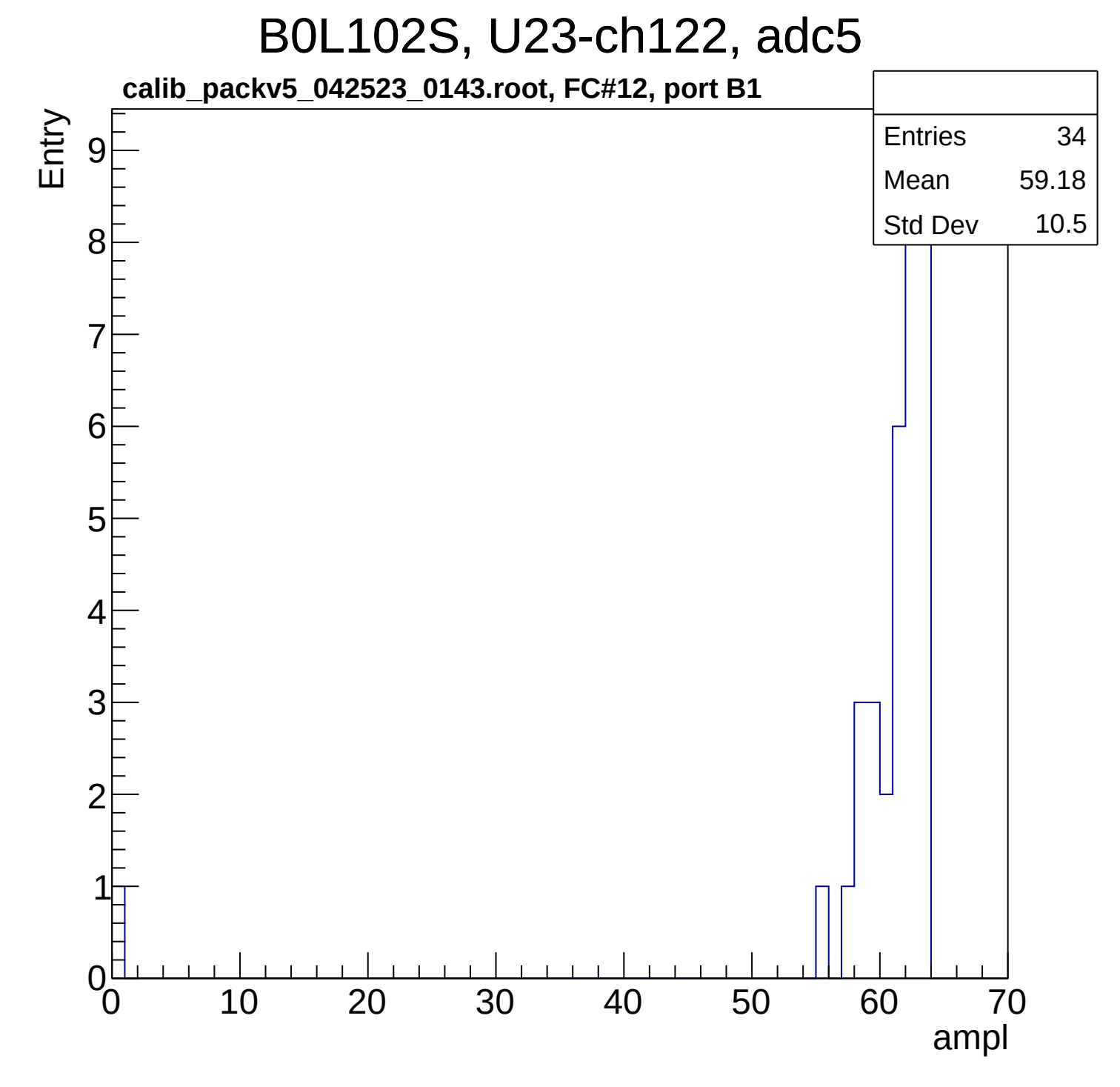
B0L102S, U23-ch122, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 34
Mean 59.18
Std Dev 10.5
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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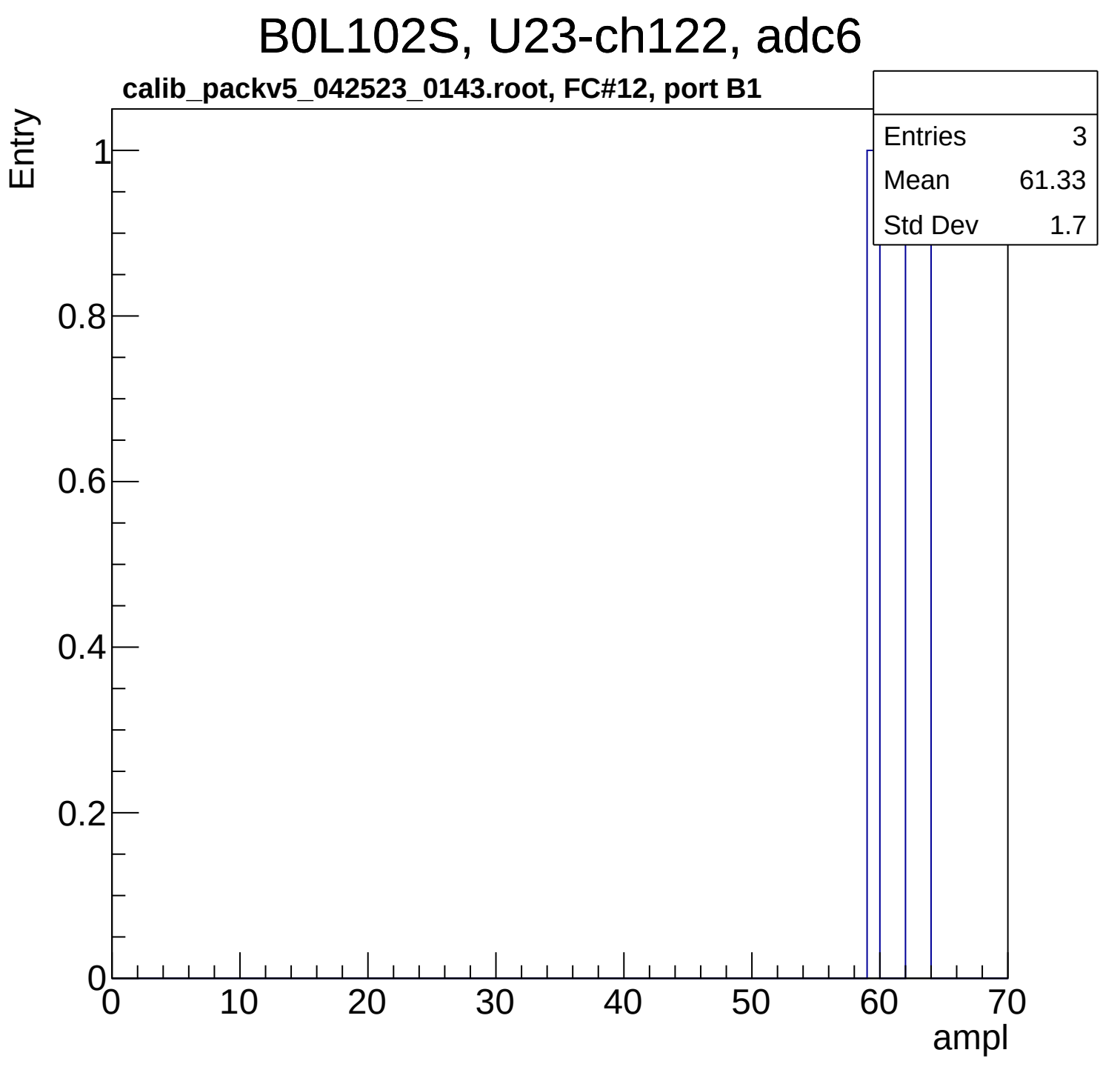
60
70


B0L102S, U23-ch122, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 61.33
Std Dev 1.7
Entry
ampl
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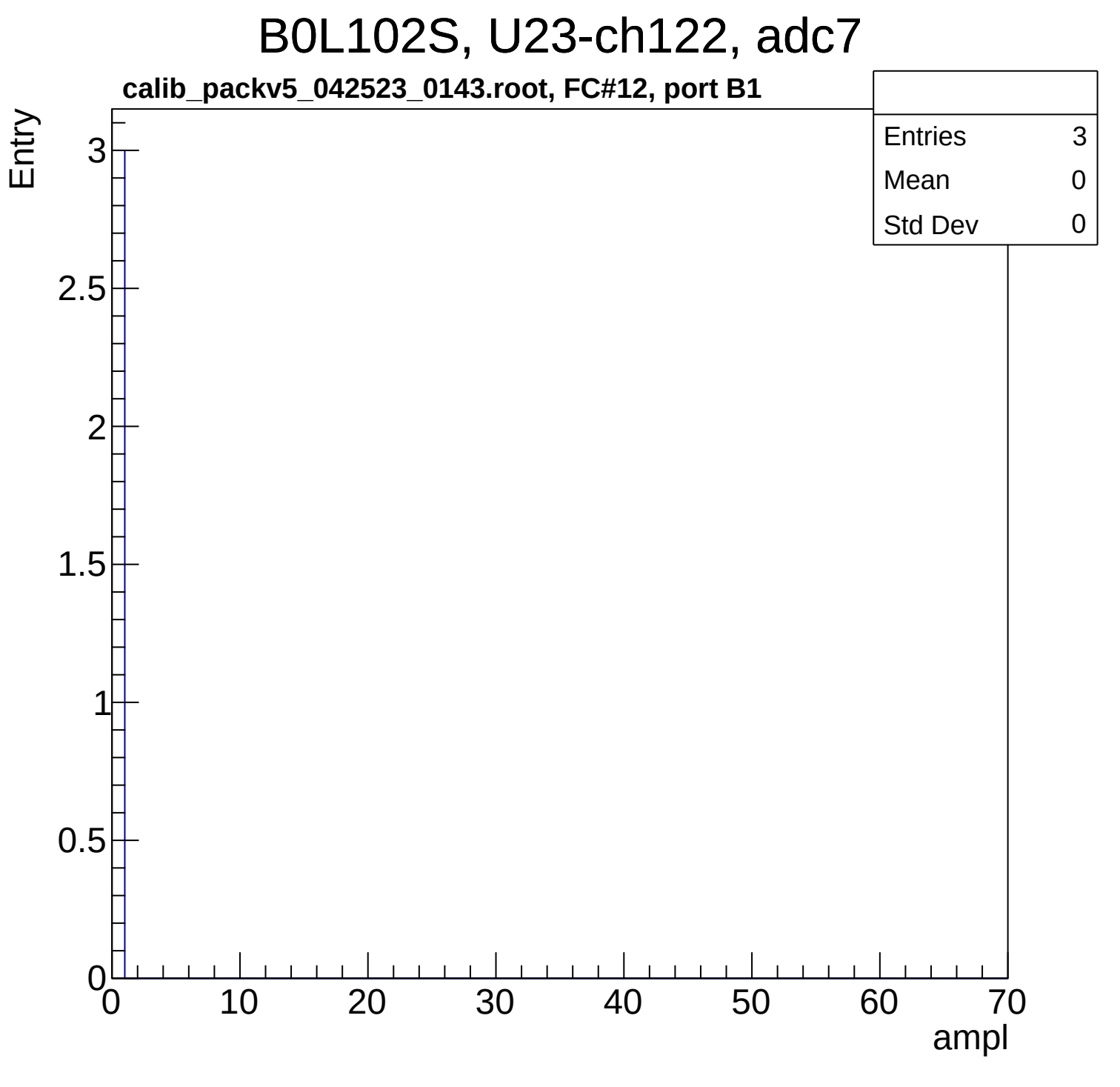
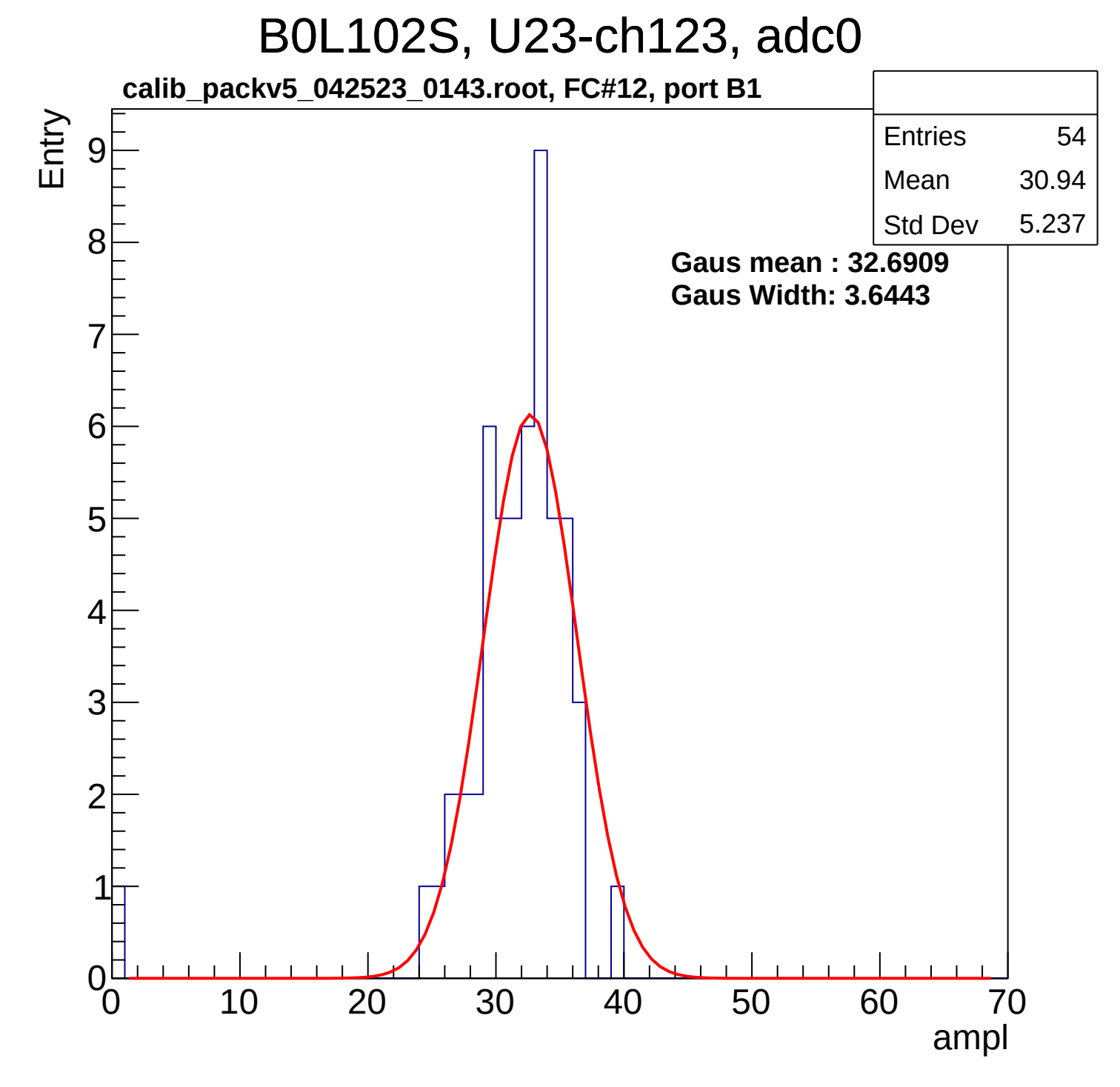
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L102S, U23-ch122, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch123, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 54
Mean 30.94
Std Dev 5.237
Gaus mean : 32.6909
Gaus Width: 3.6443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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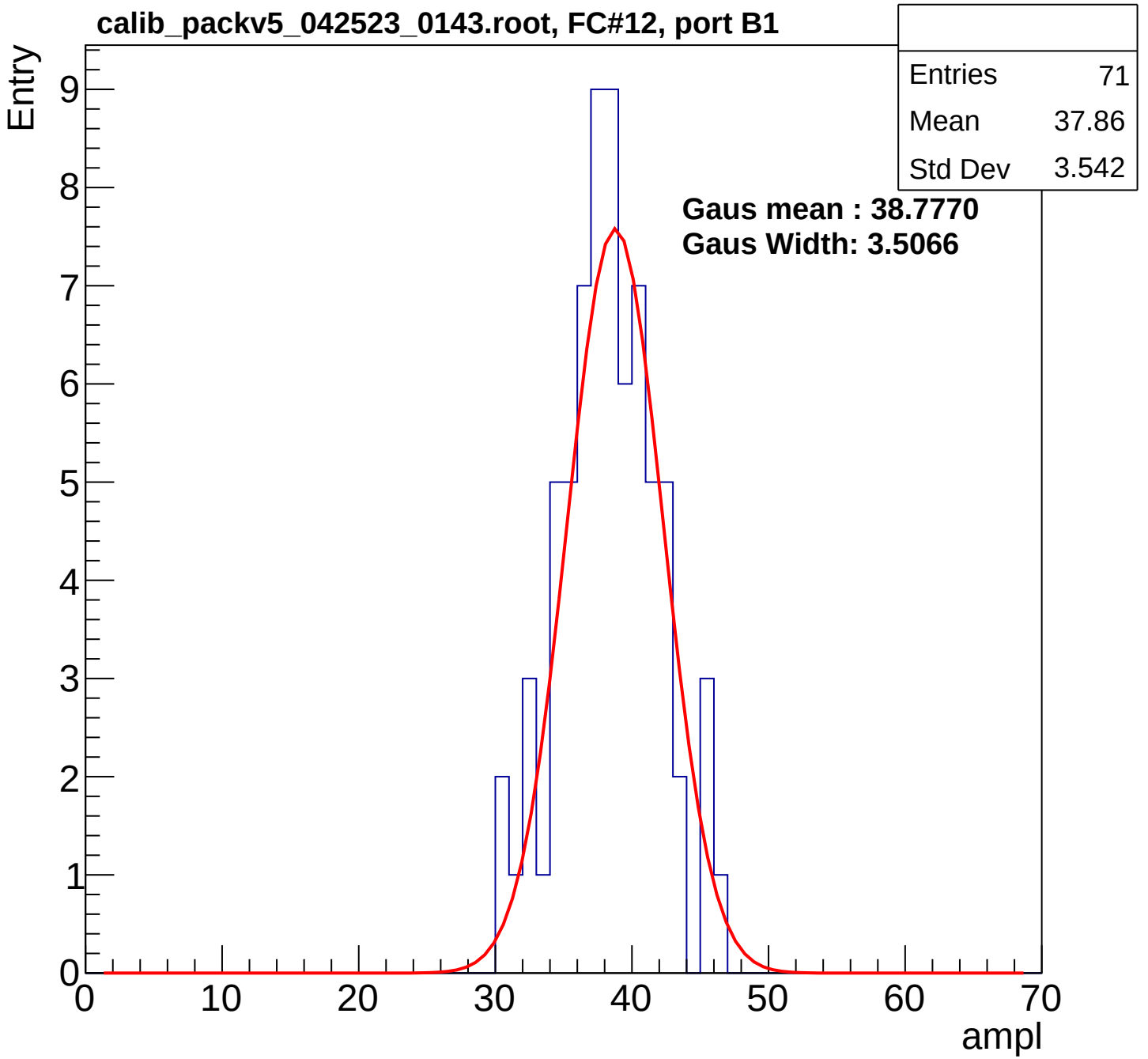
B0L102S, U23-ch123, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 37.86
Std Dev 3.542
Gaus mean : 38.7770
Gaus Width: 3.5066
Entry
ampl
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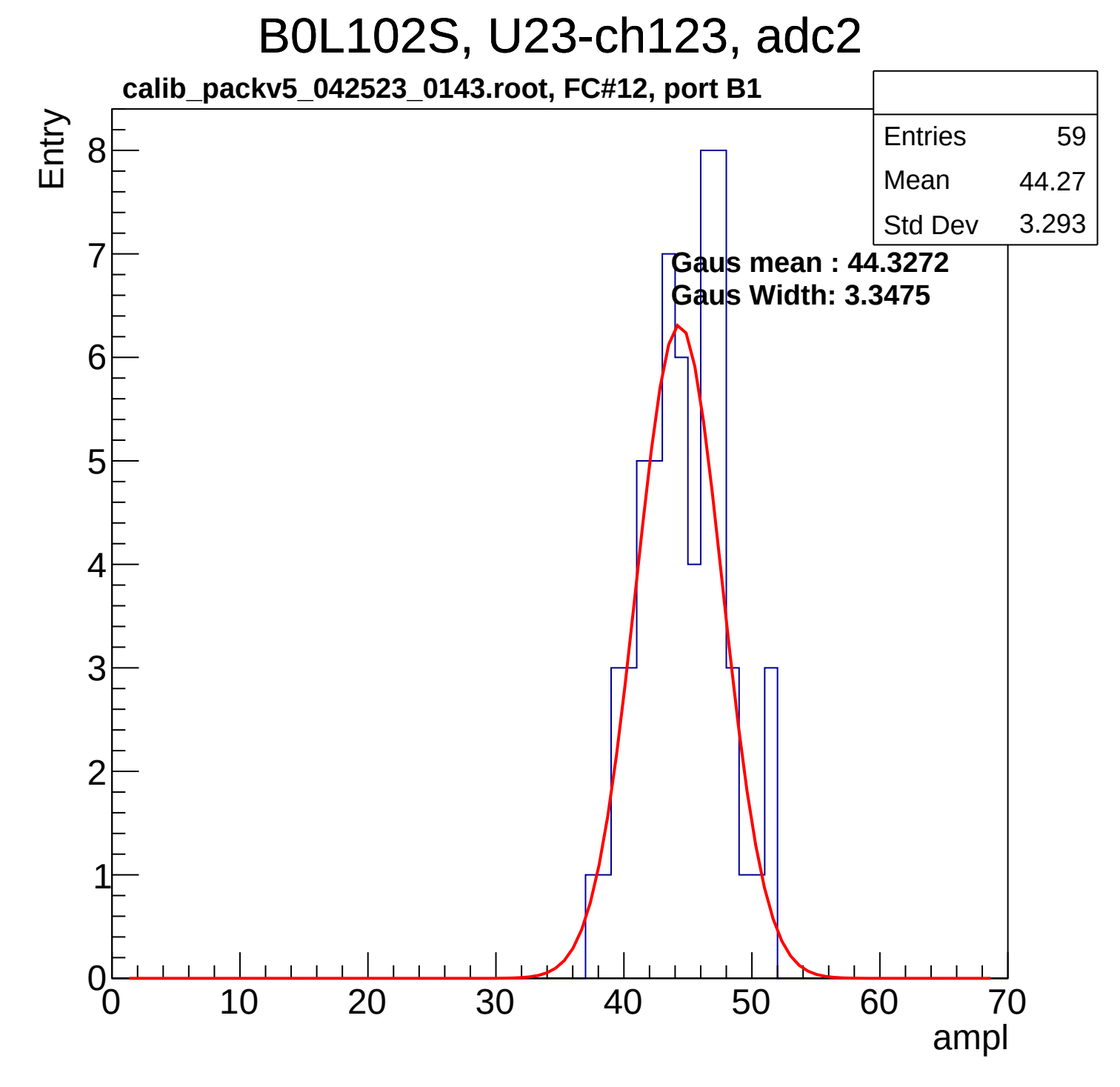
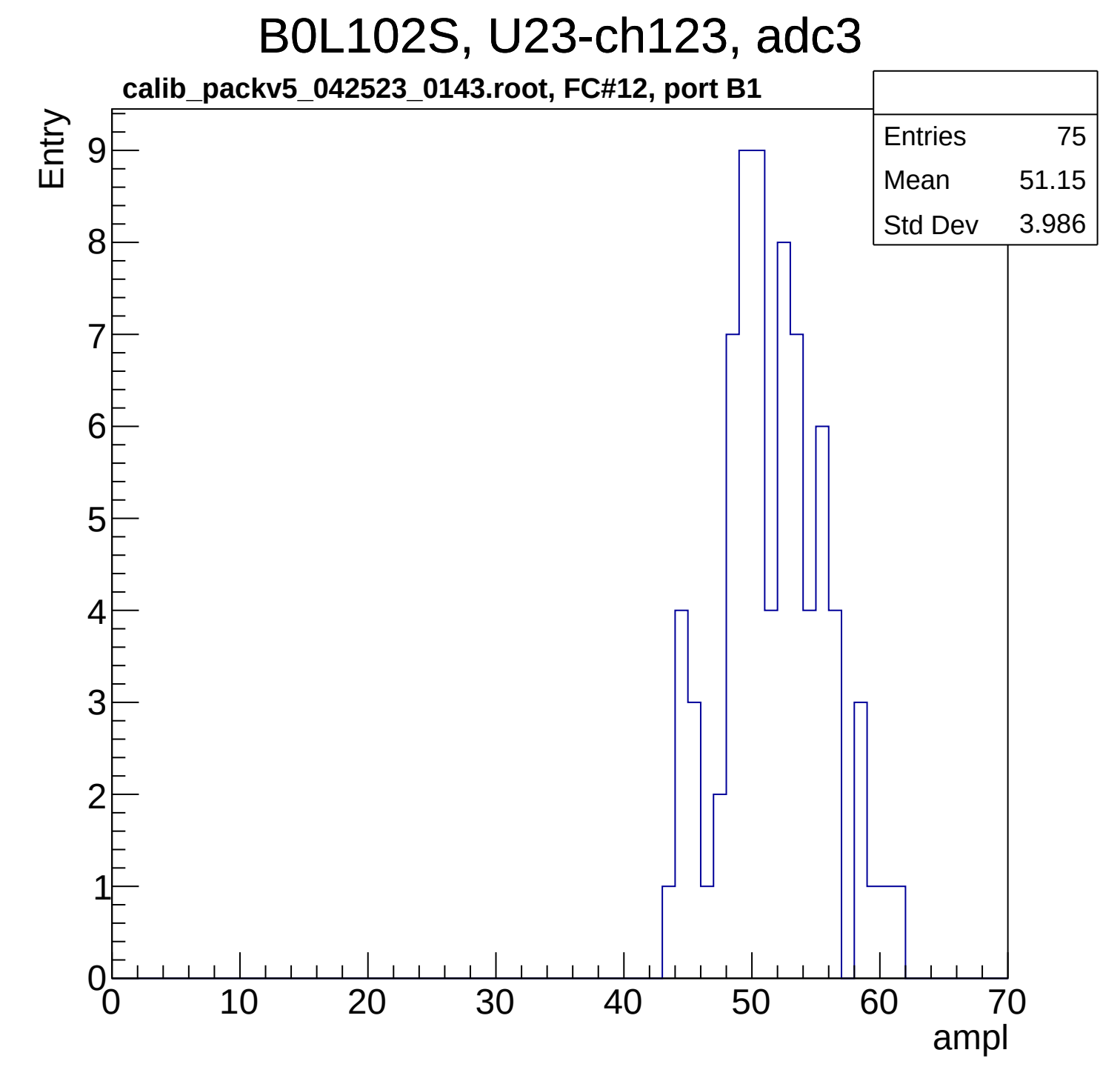
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch123, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 59
Mean 44.27
Std Dev 3.293
Gaus mean : 44.3272
Gaus Width: 3.3475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch123, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 75
Mean 51.15
Std Dev 3.986
Entry
0
1
2
3
4
5
6
7
8
9
0
10
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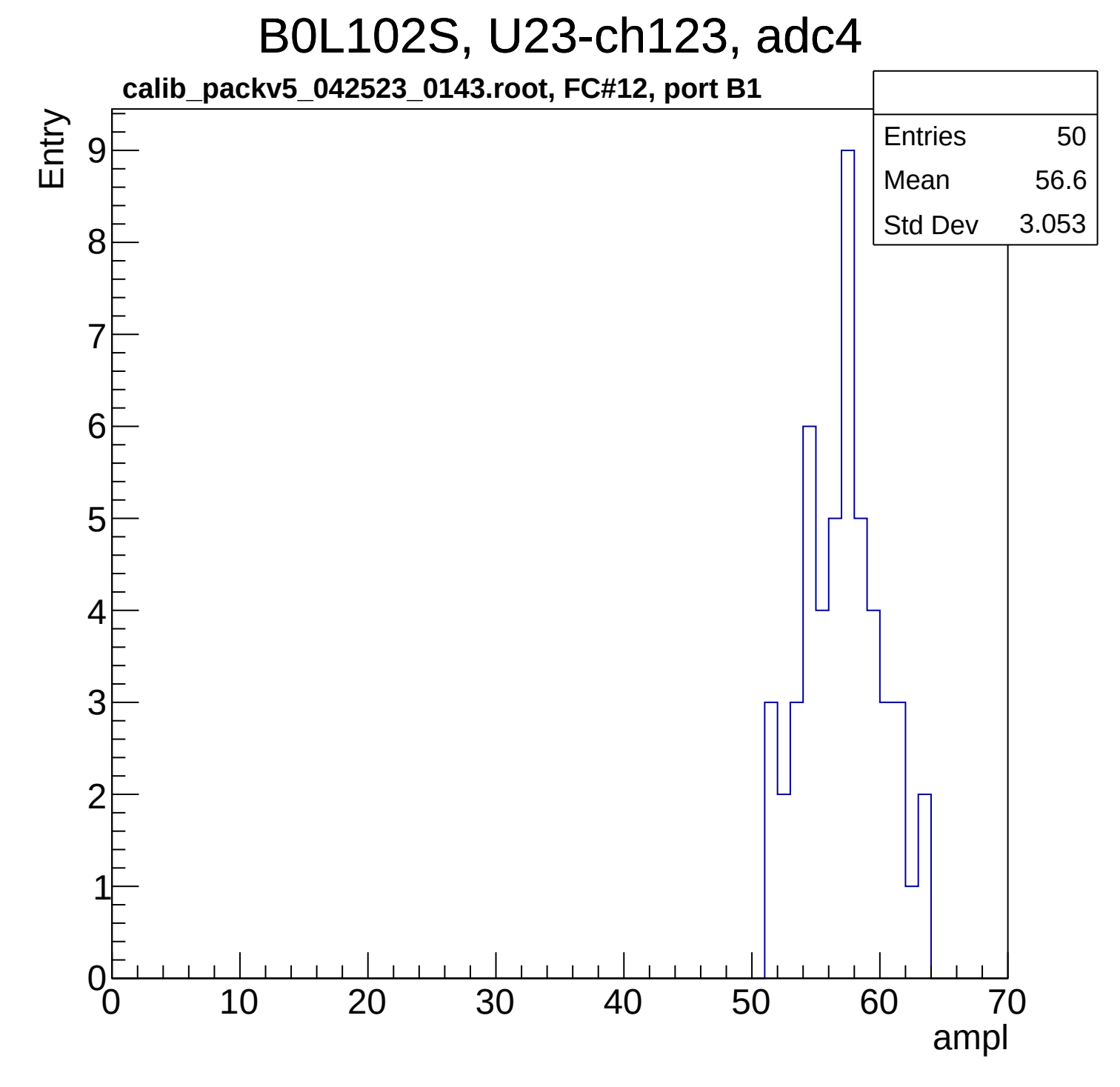
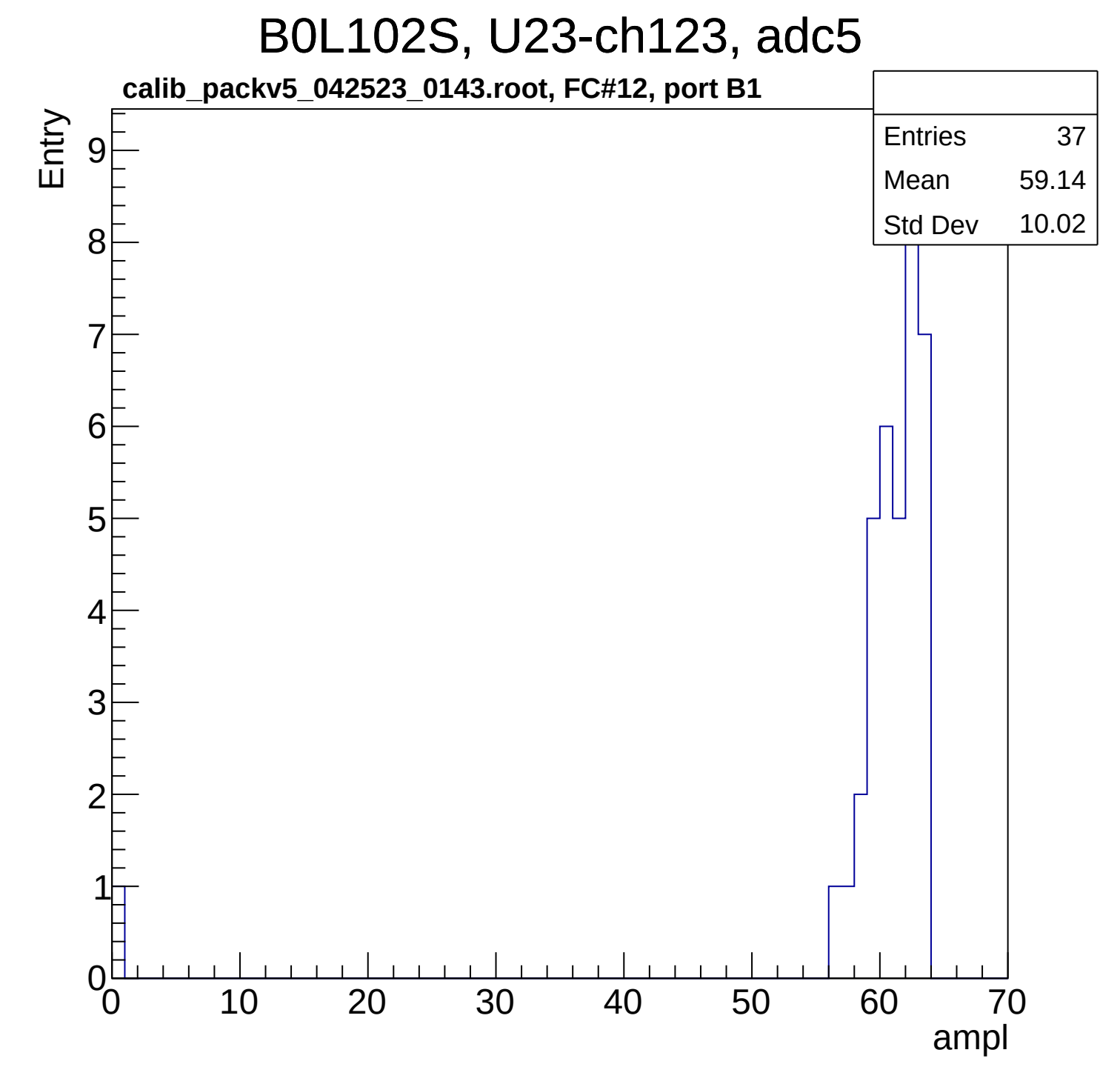
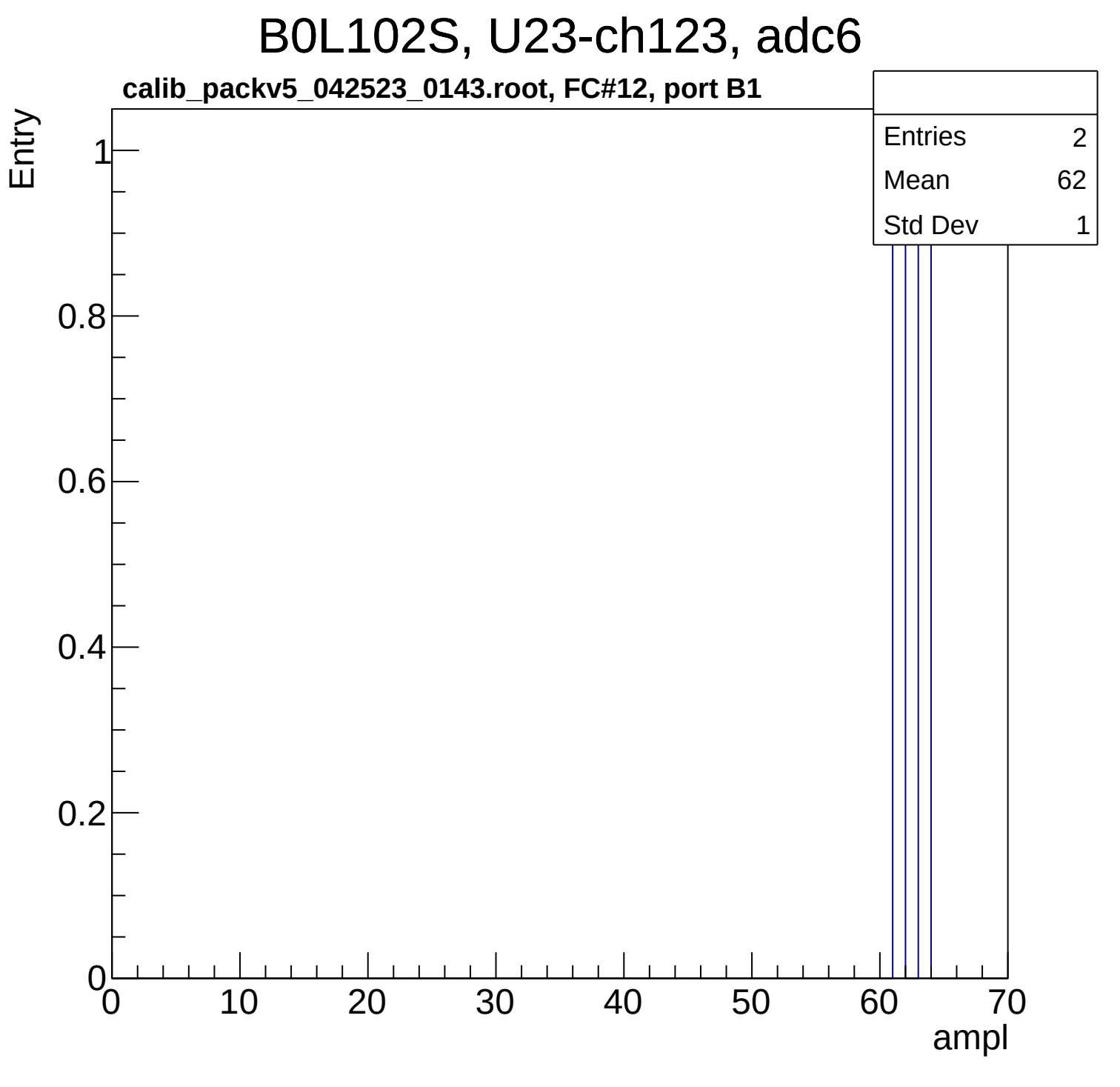
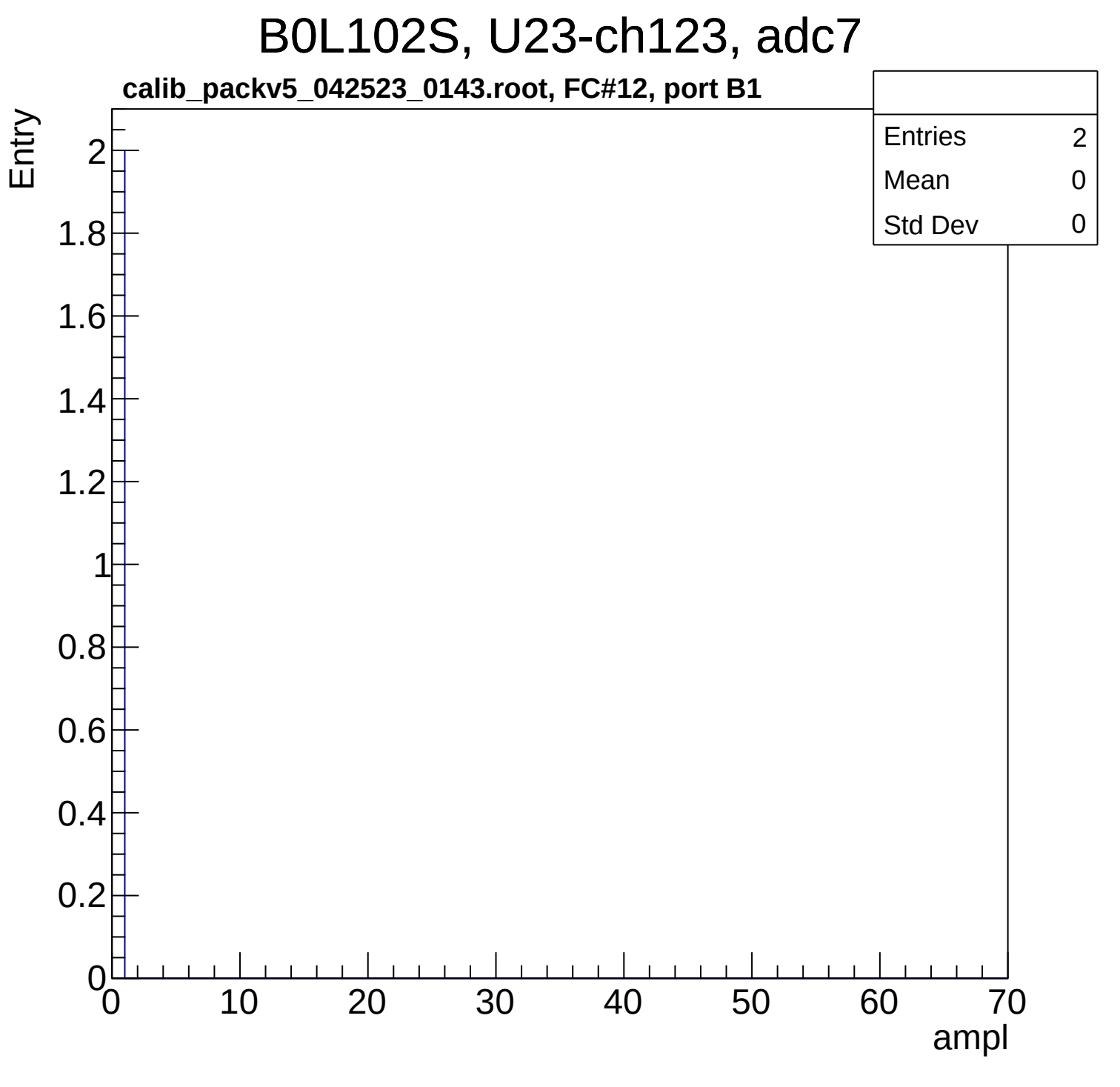
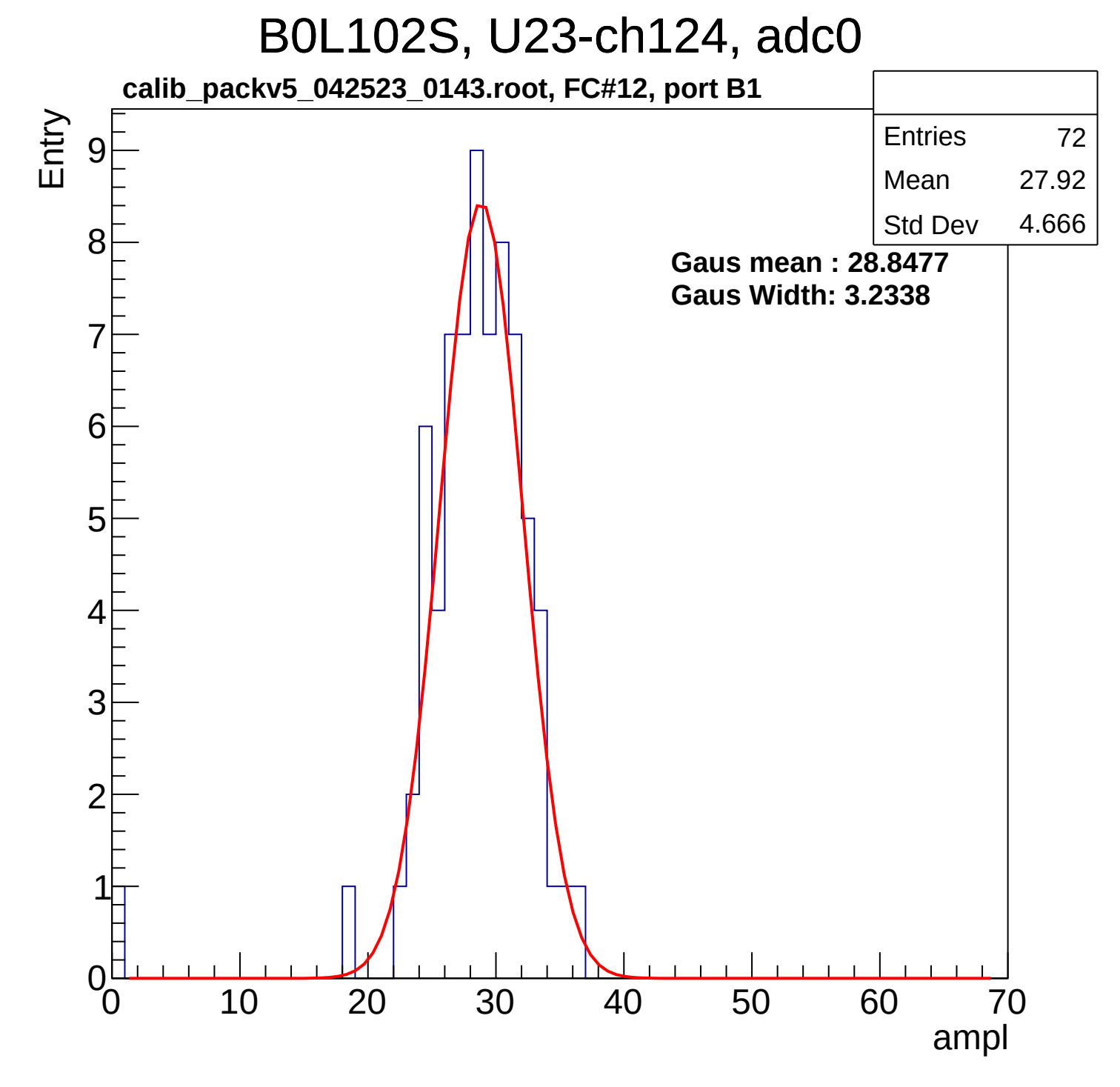
40
50
60
70
ampl

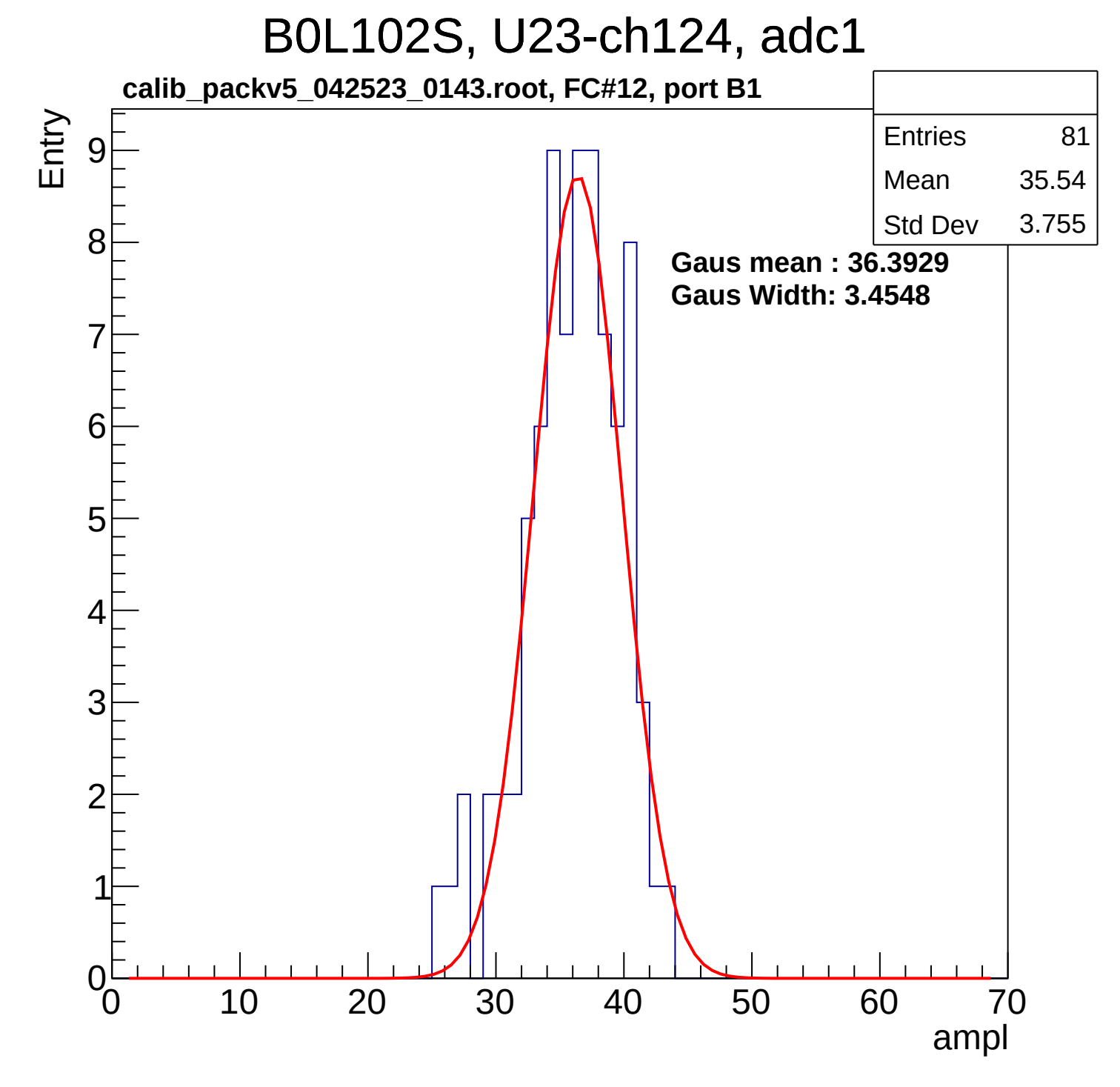

B0L102S, U23-ch123, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 50
Mean 56.6
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch123, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 37
Mean 59.14
Std Dev 10.02
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch123, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch123, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L102S, U23-ch124, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 27.92
Std Dev 4.666
Gaus mean : 28.8477
Gaus Width: 3.2338
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch124, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 81
Mean 35.54
Std Dev 3.755
Gaus mean : 36.3929
Gaus Width: 3.4548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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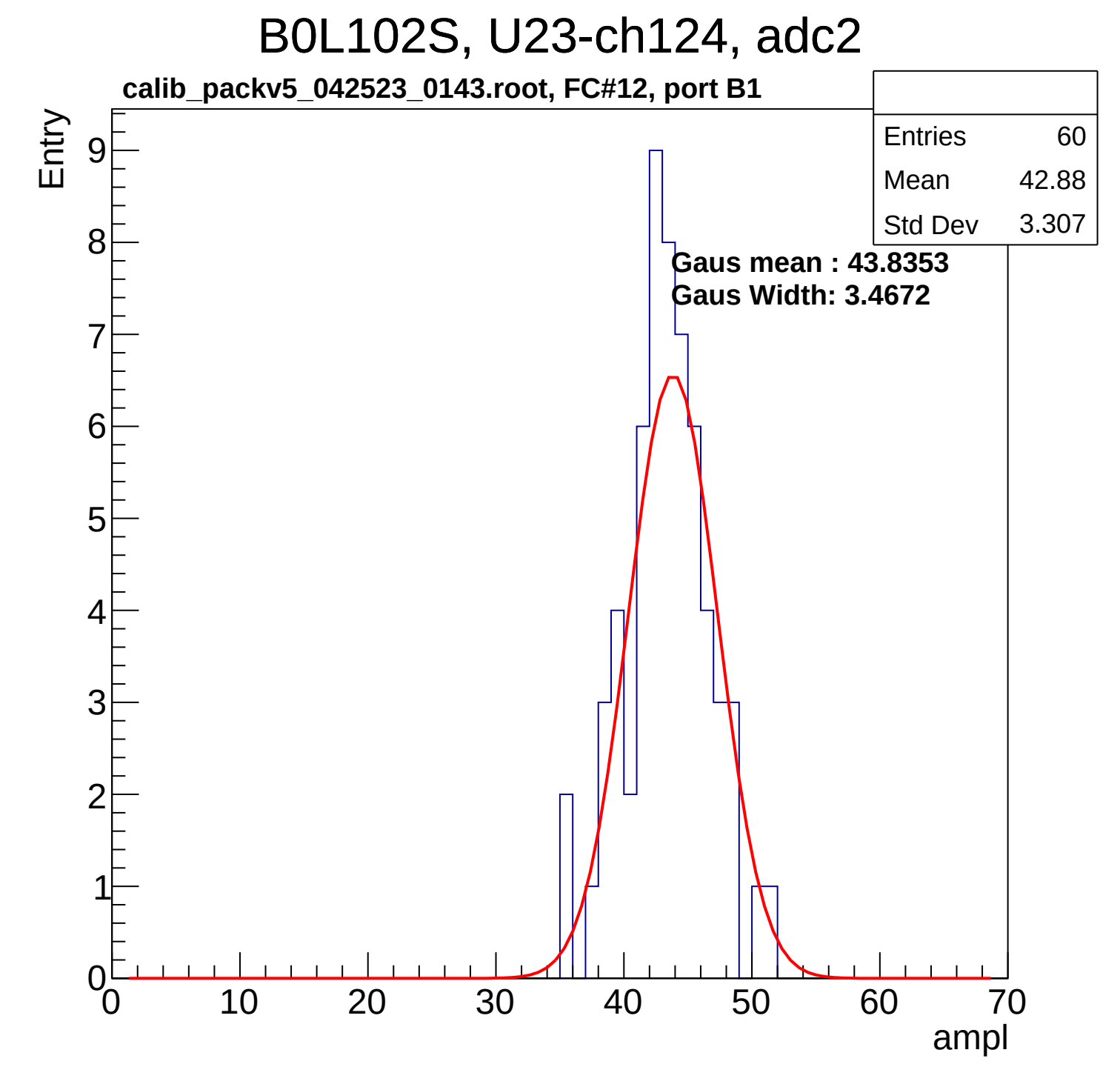
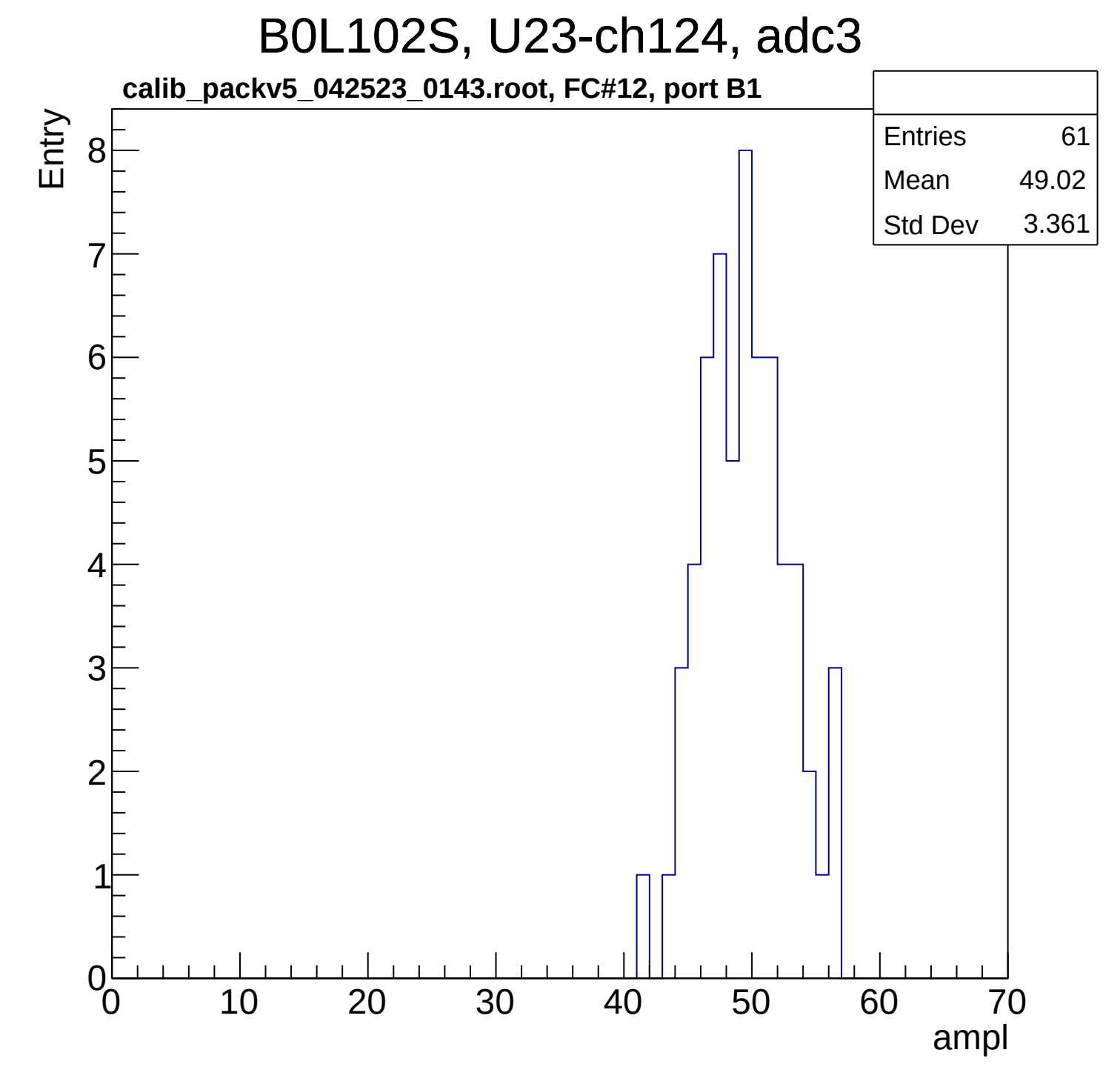
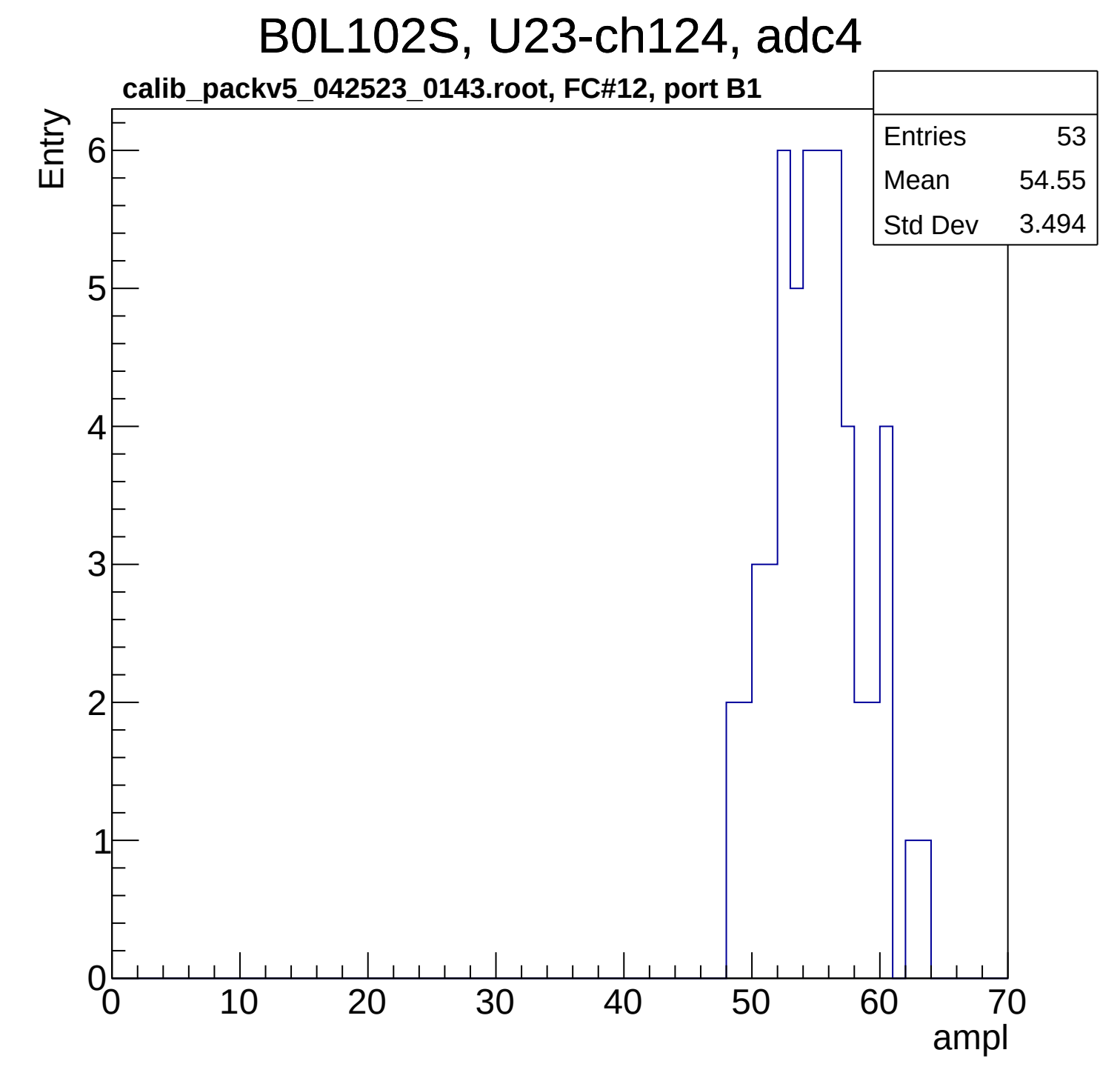
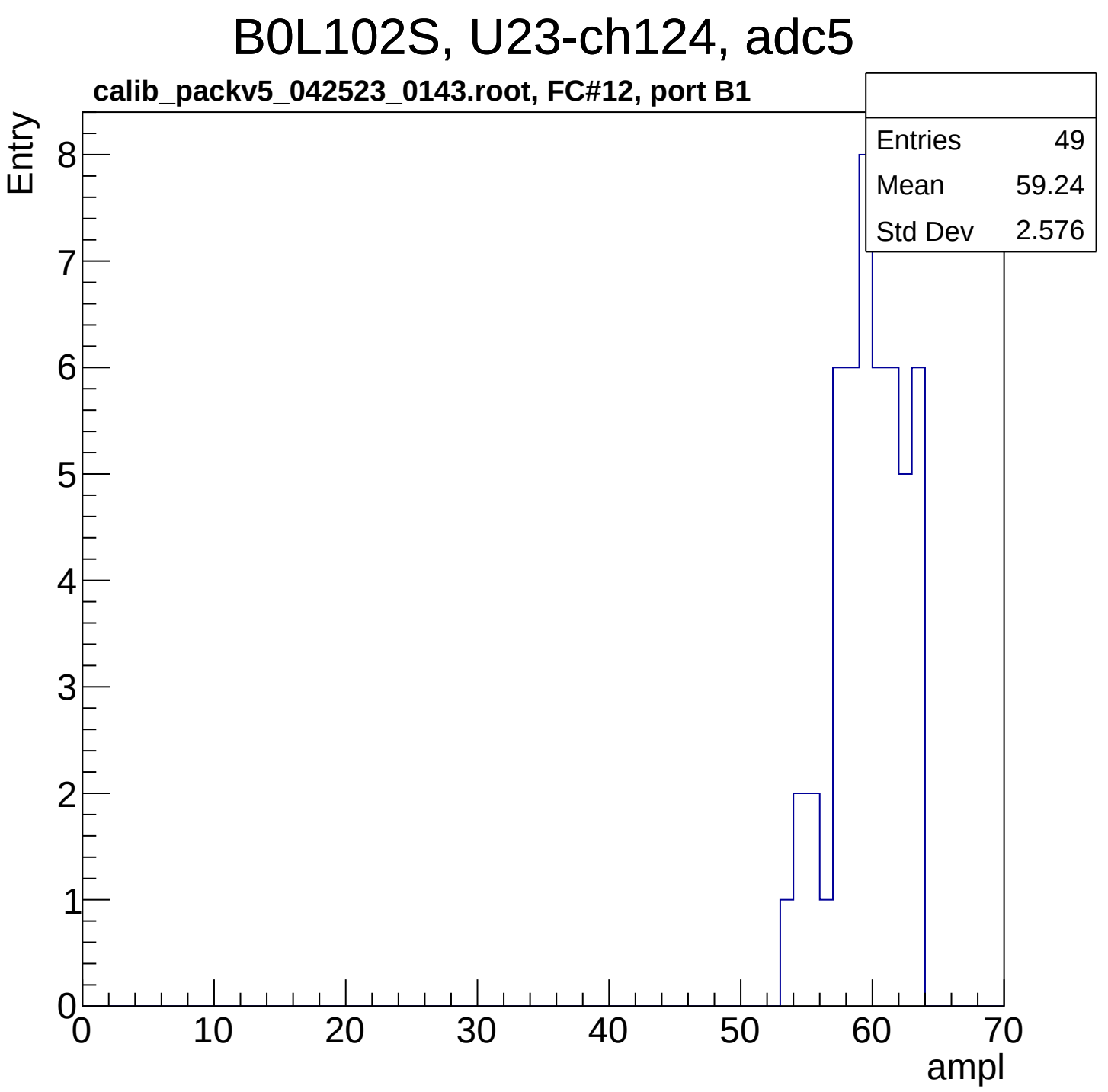
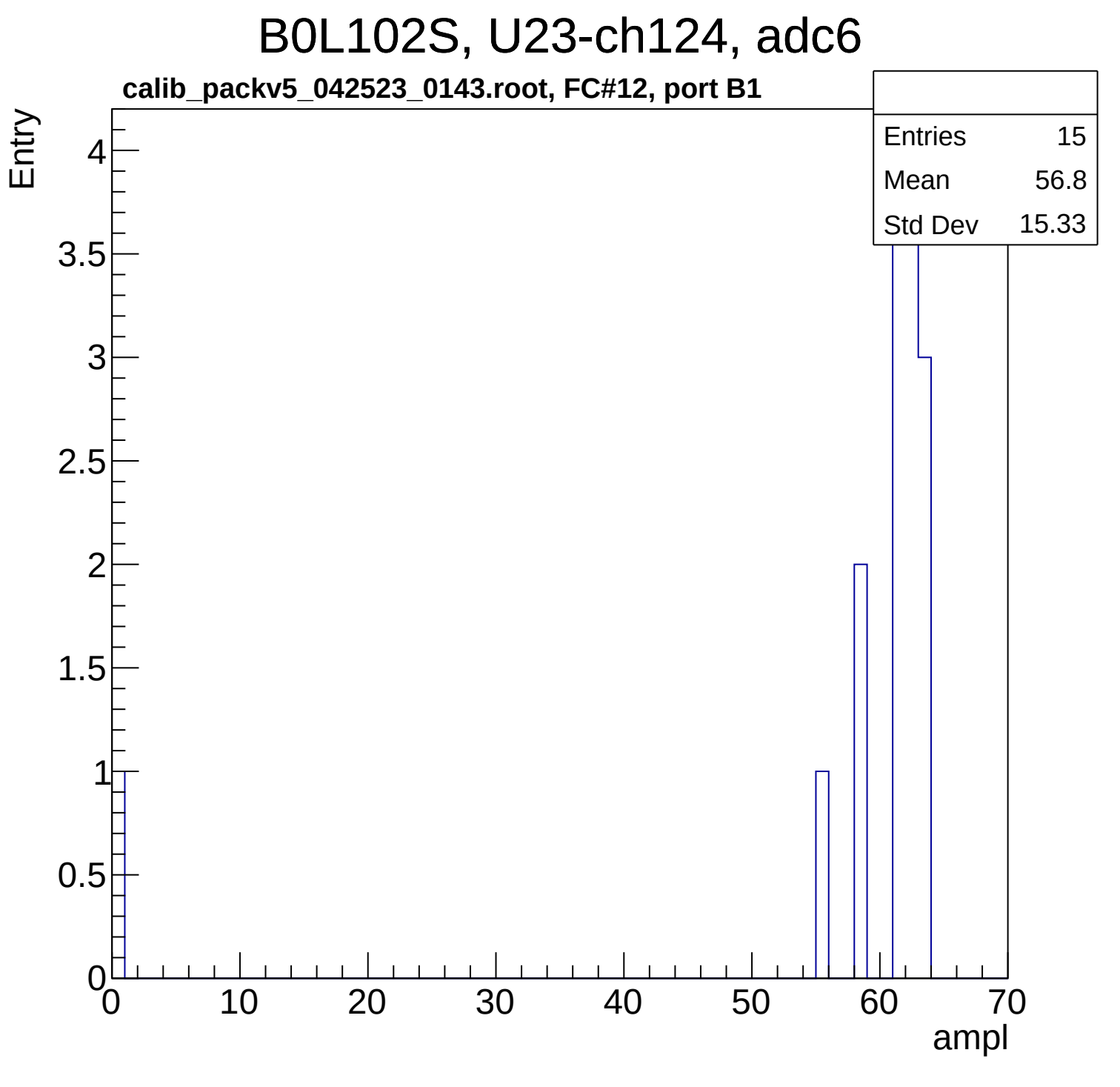
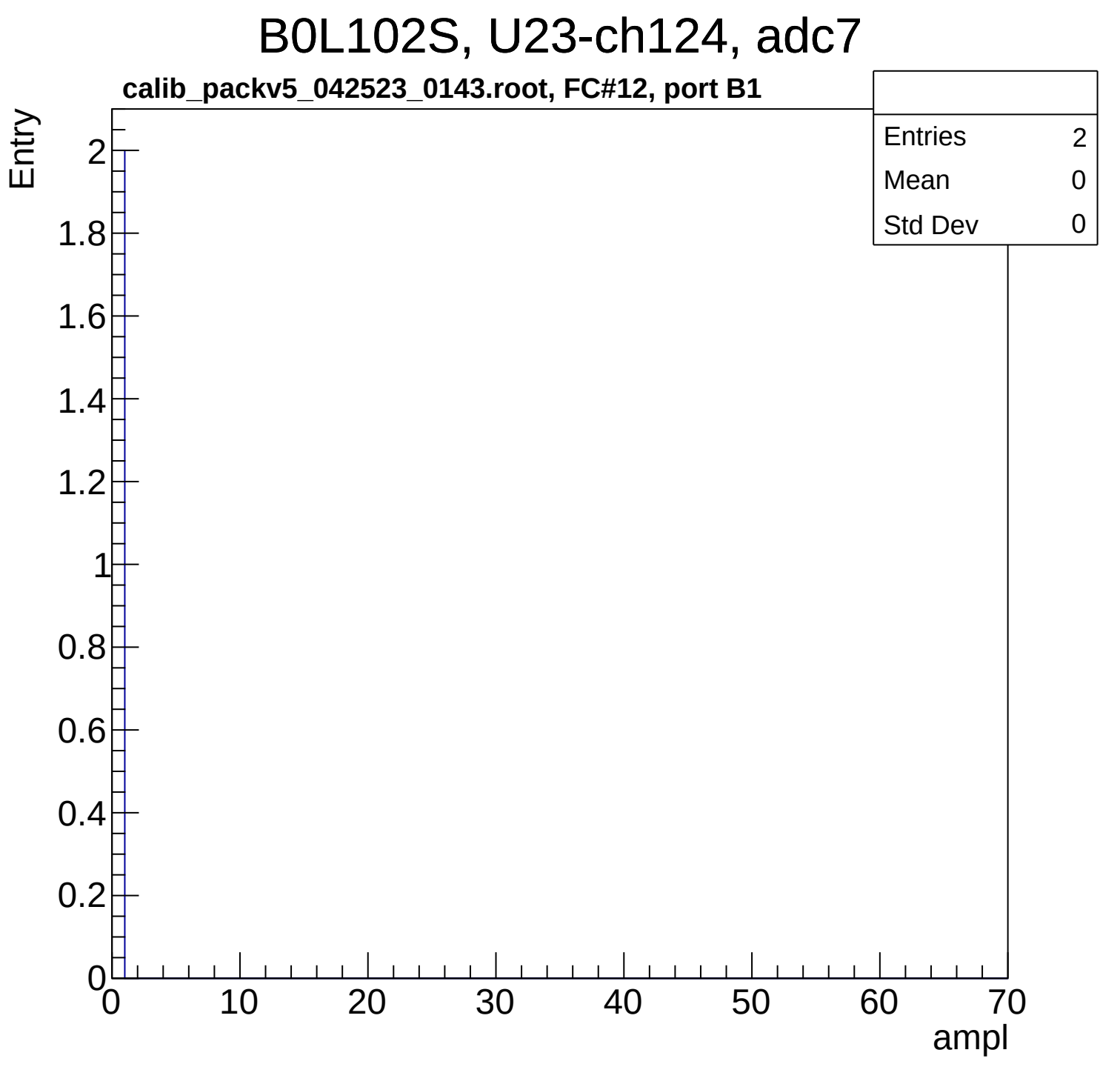

B0L102S, U23-ch124, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 60
Mean 42.88
Std Dev 3.307
Gaus mean : 43.8353
Gaus Width: 3.4672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

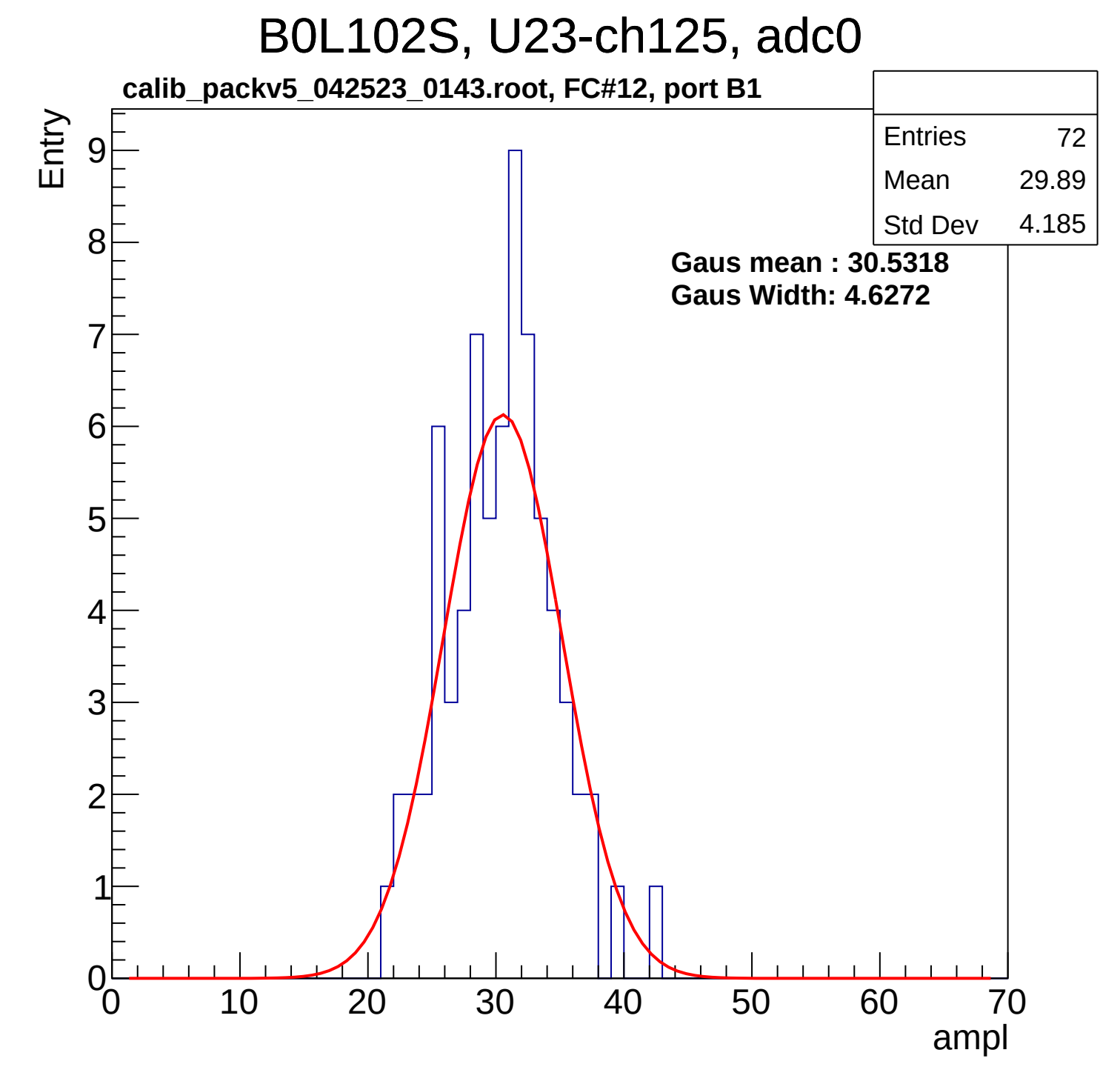

B0L102S, U23-ch124, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 49.02
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

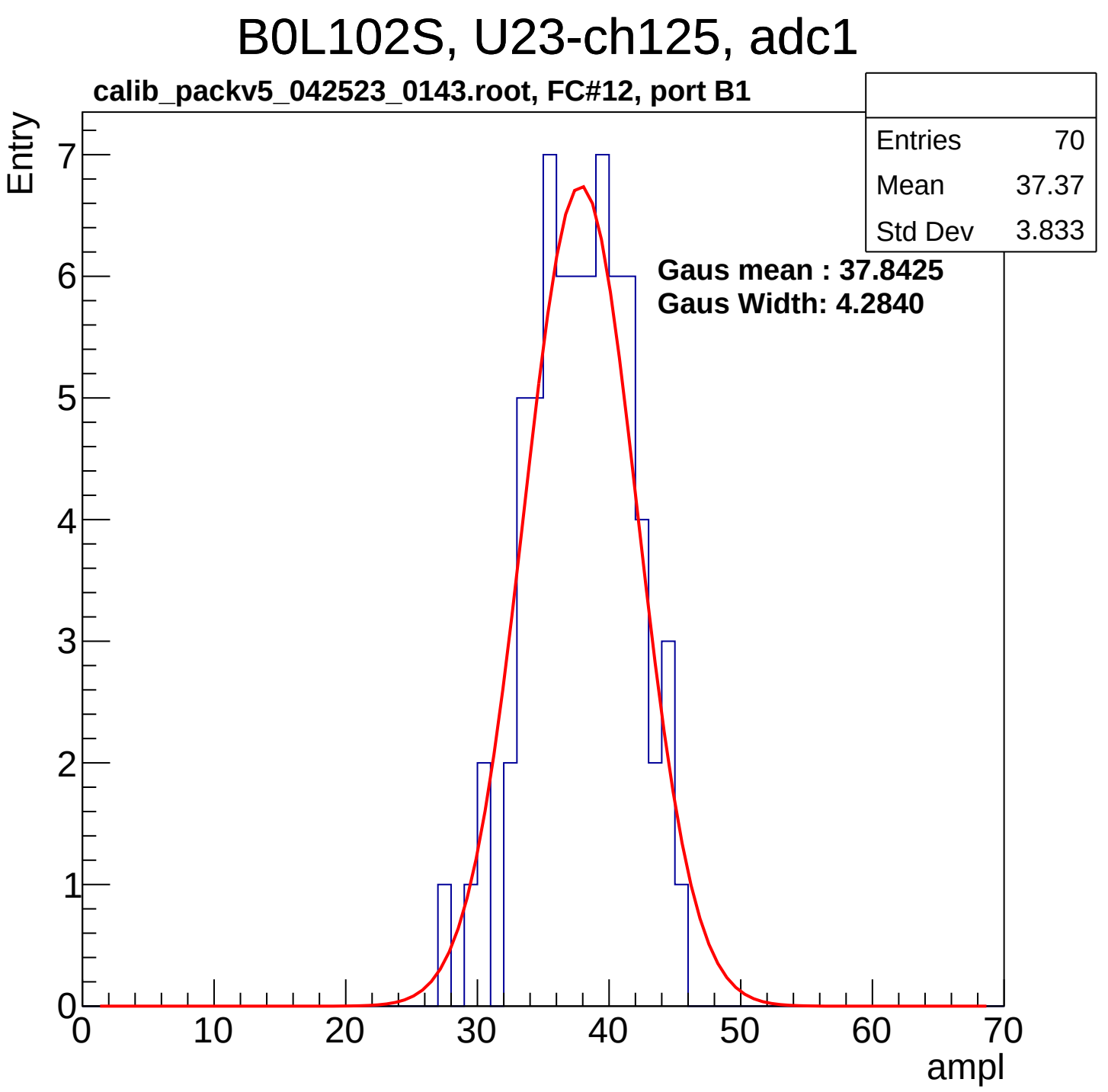

B0L102S, U23-ch124, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 53
Mean 54.55
Std Dev 3.494
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

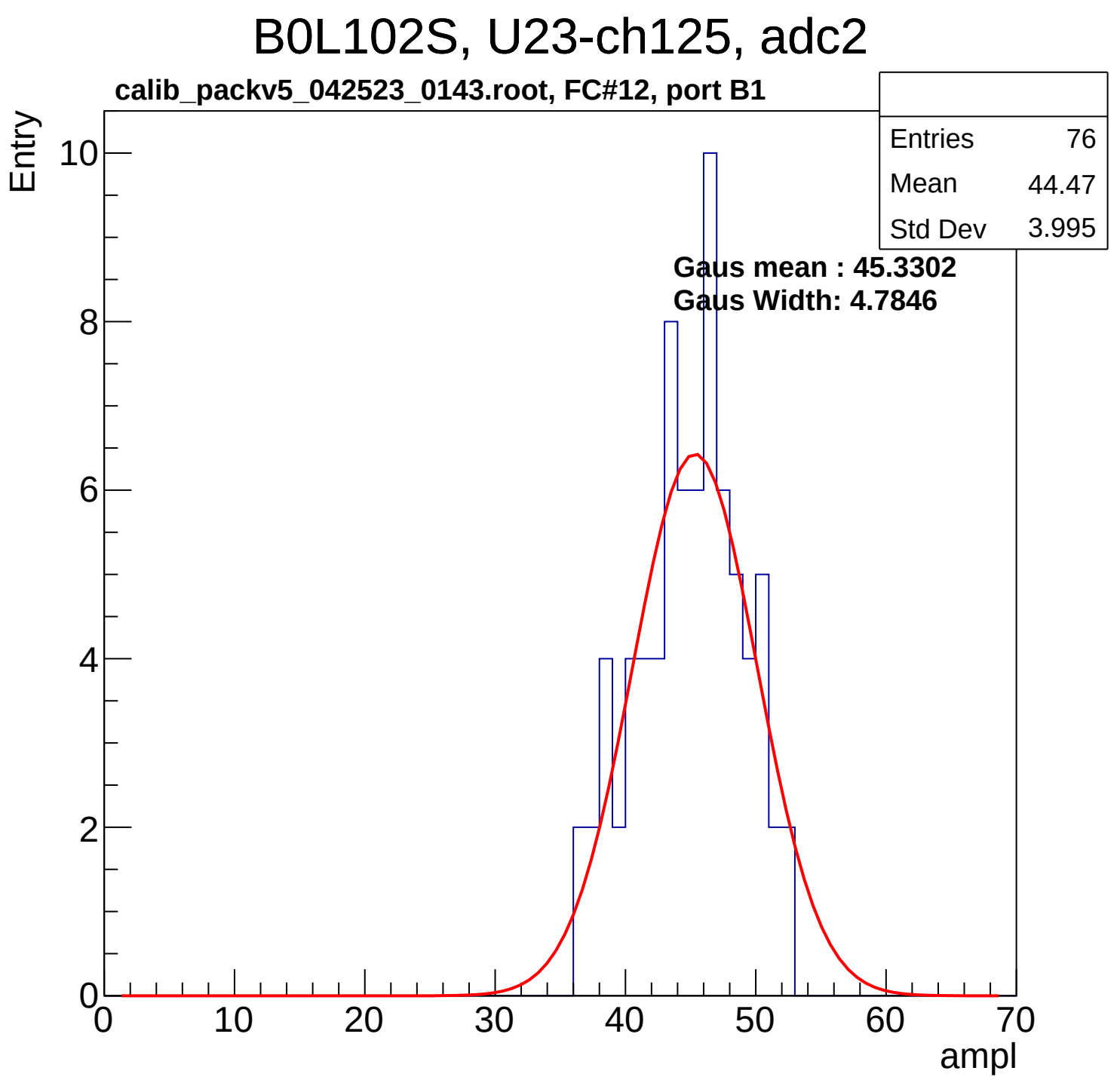

B0L102S, U23-ch124, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 59.24
Std Dev 2.576
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

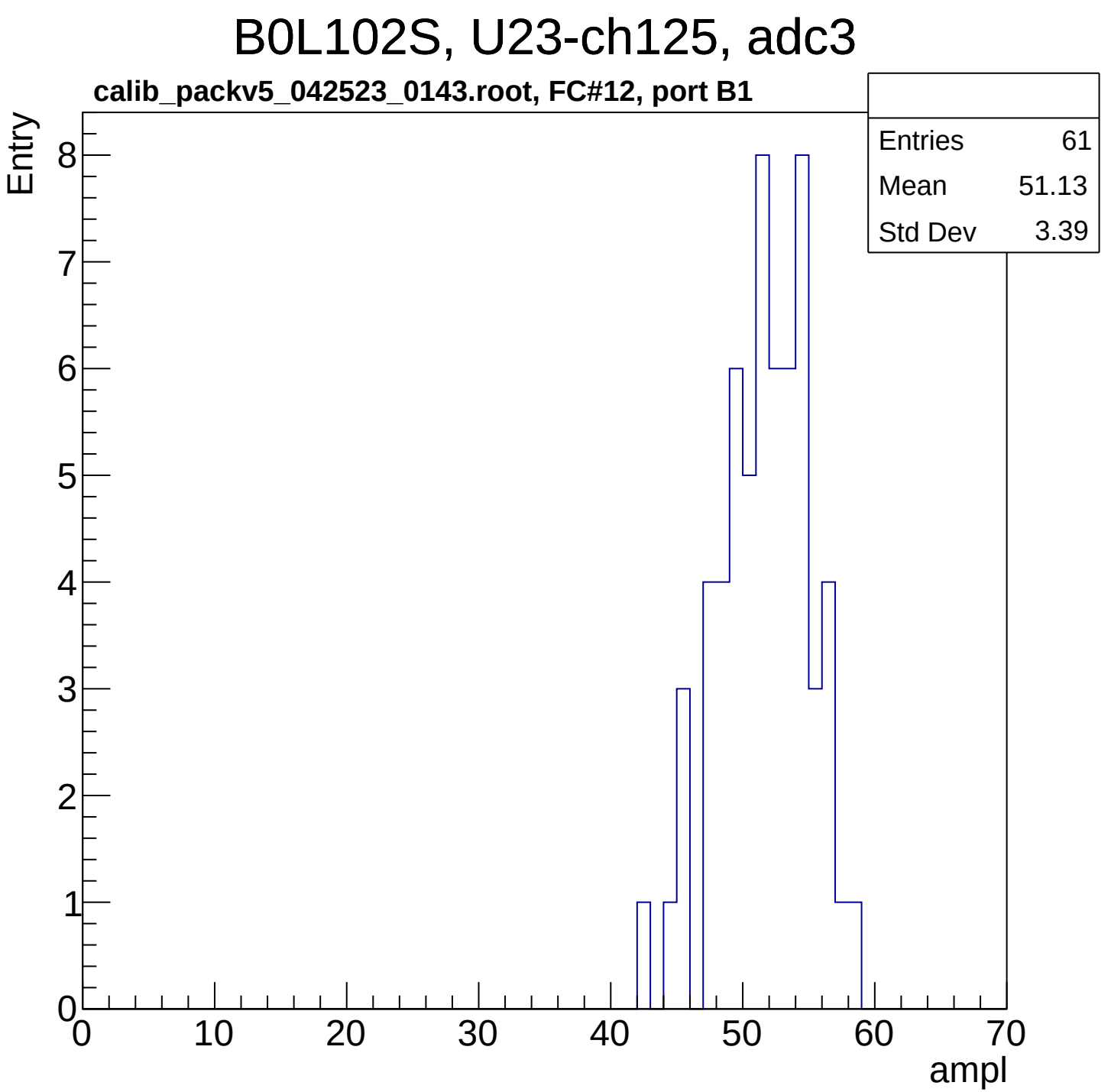

B0L102S, U23-ch124, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 15
Mean 56.8
Std Dev 15.33
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

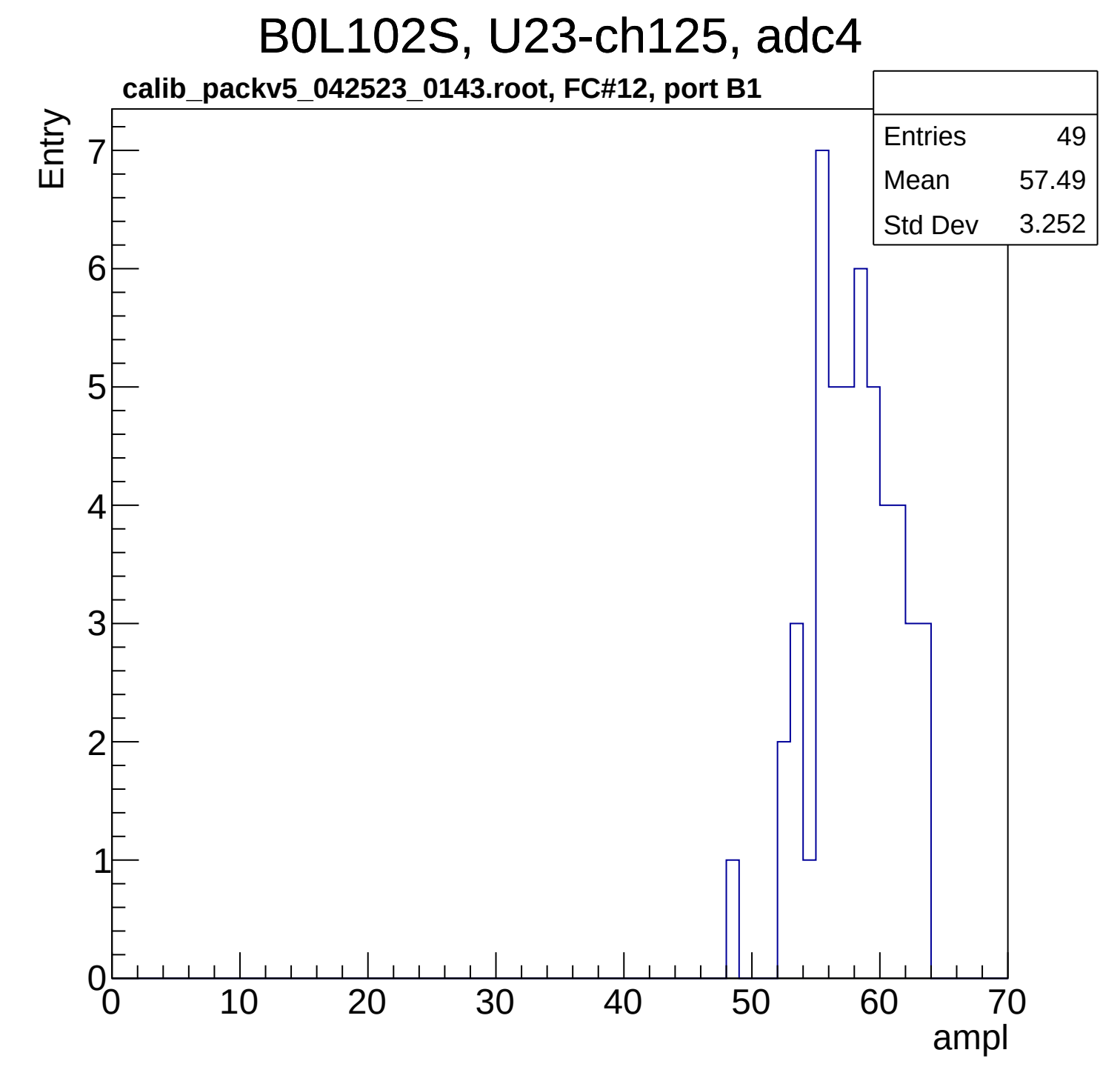

B0L102S, U23-ch124, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

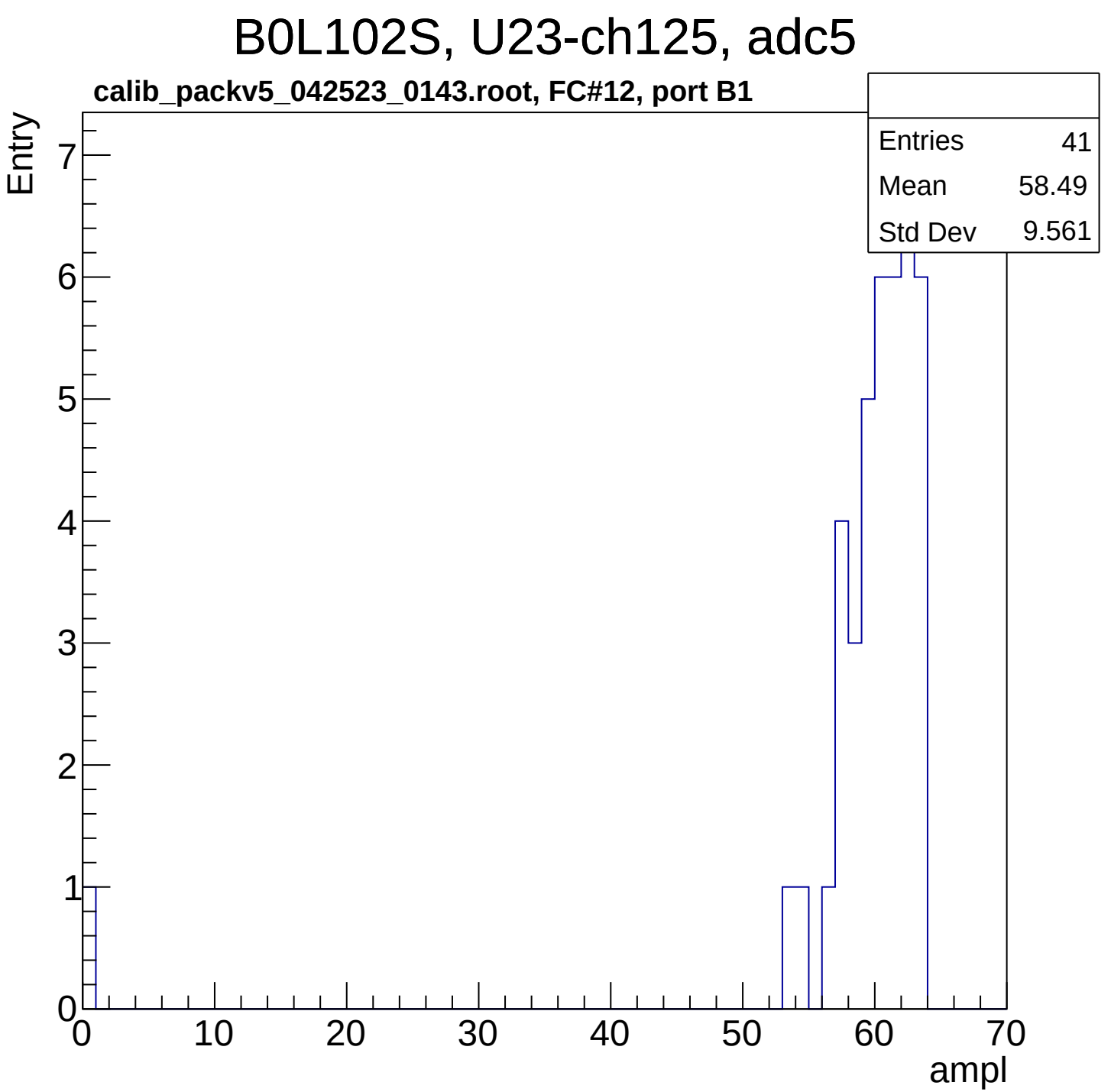

B0L102S, U23-ch125, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 29.89
Std Dev 4.185
Gaus mean : 30.5318
Gaus Width: 4.6272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L102S, U23-ch125, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 70
Mean 37.37
Std Dev 3.833
Gaus mean : 37.8425
Gaus Width: 4.2840
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch125, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 44.47
Std Dev 3.995
Gaus mean : 45.3302
Gaus Width: 4.7846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L102S, U23-ch125, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 61
Mean 51.13
Std Dev 3.39
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch125, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 49
Mean 57.49
Std Dev 3.252
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch125, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 41
Mean 58.49
Std Dev 9.561
Entry
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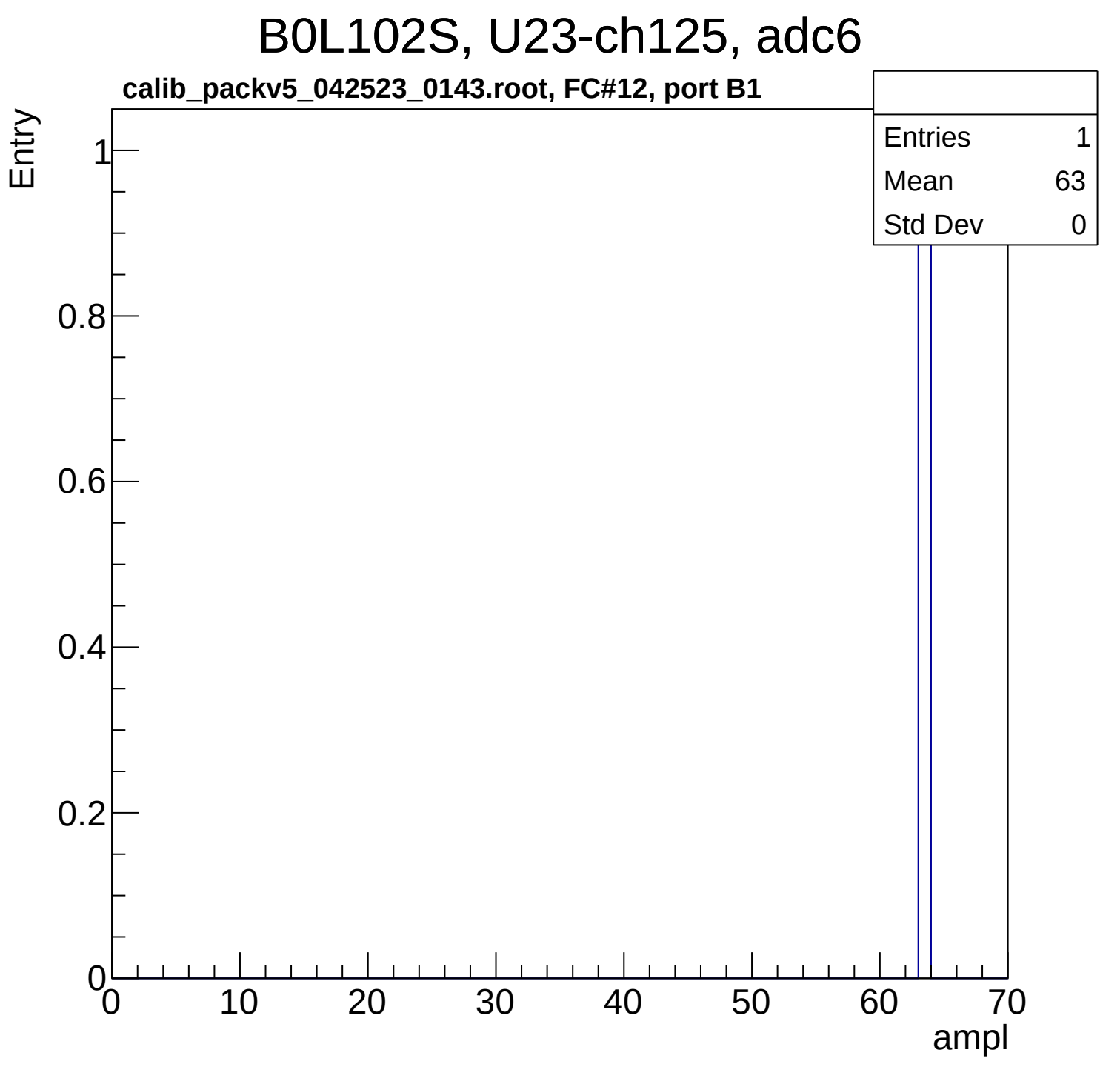
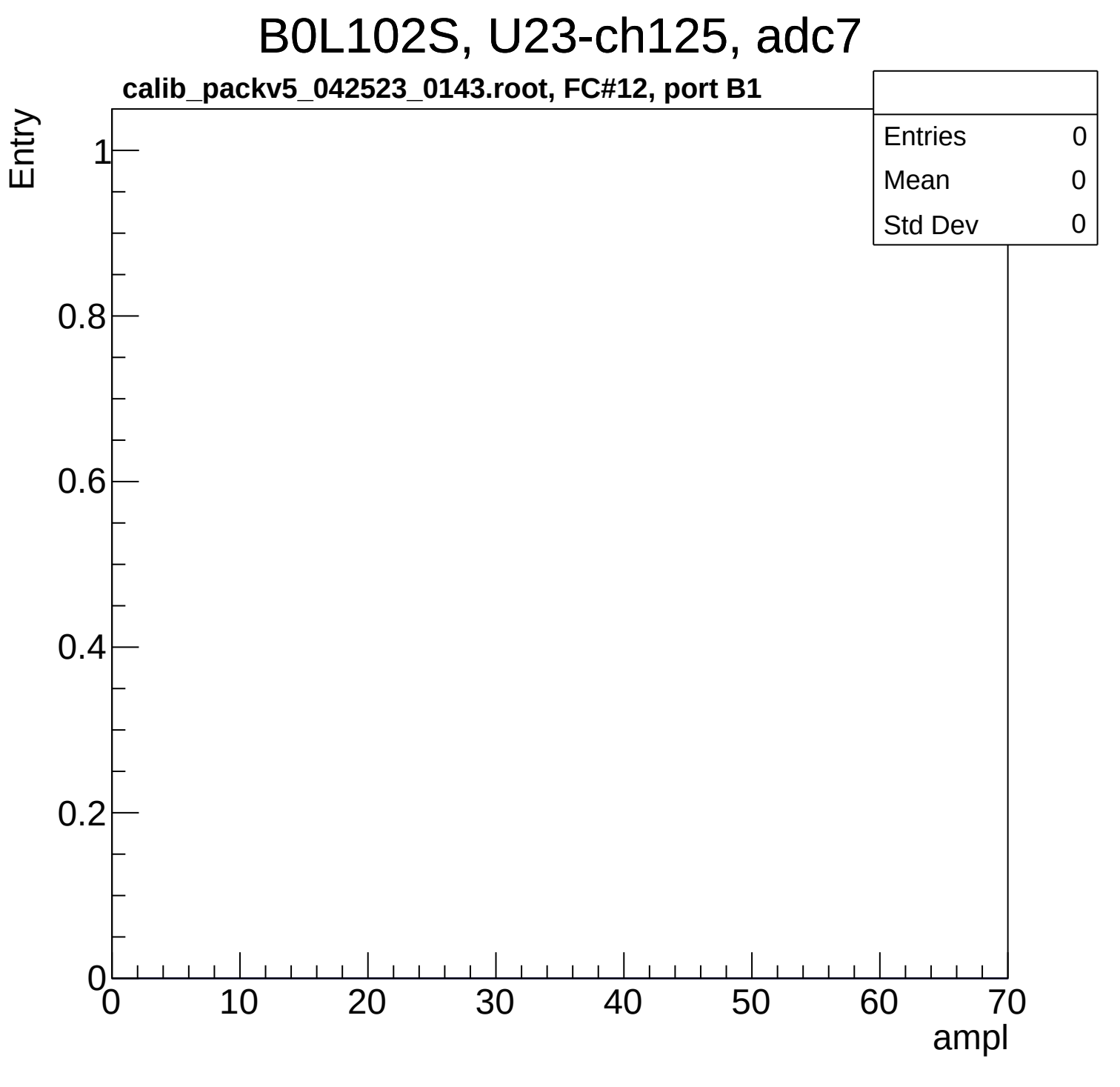
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch125, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch125, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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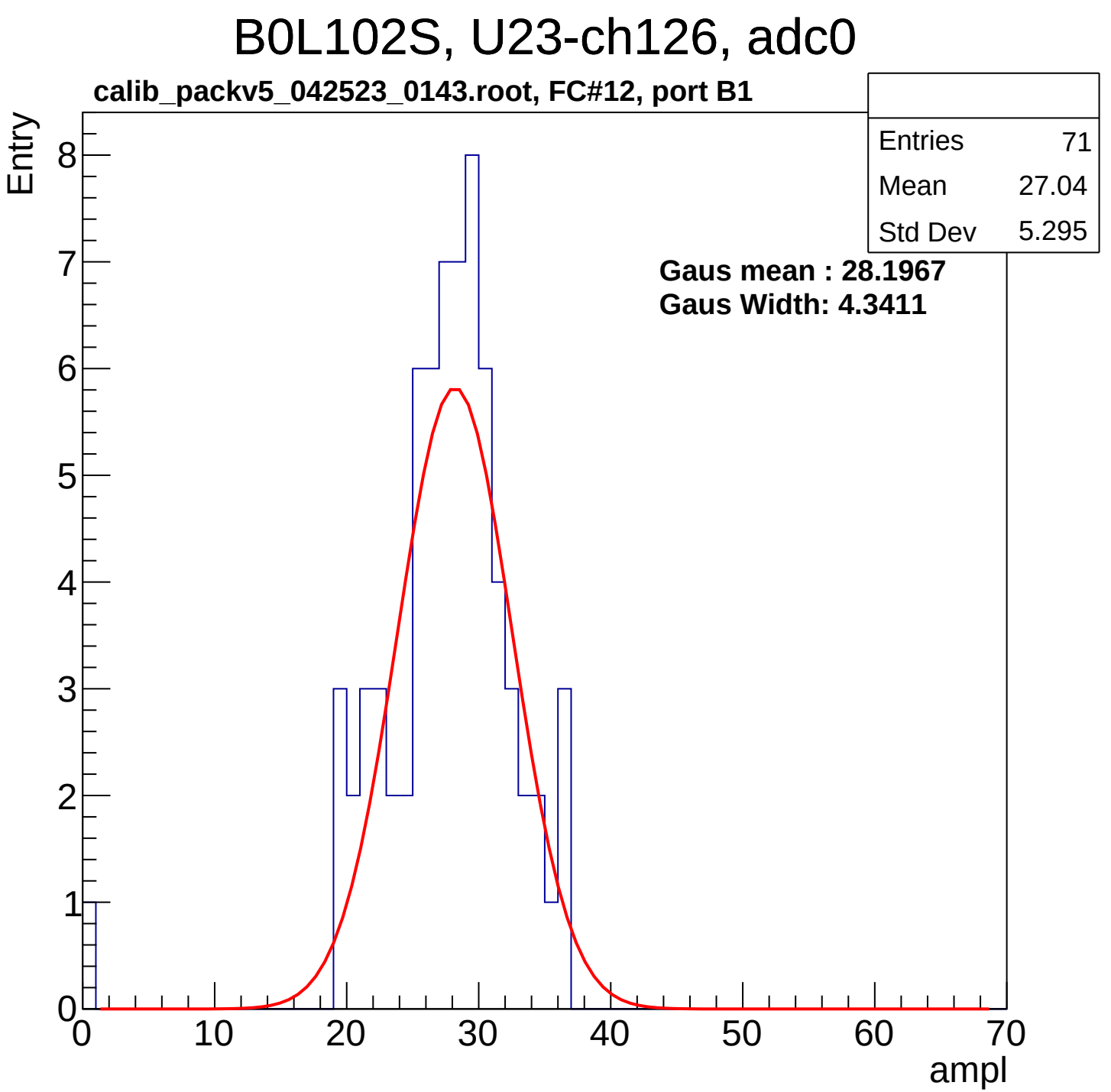
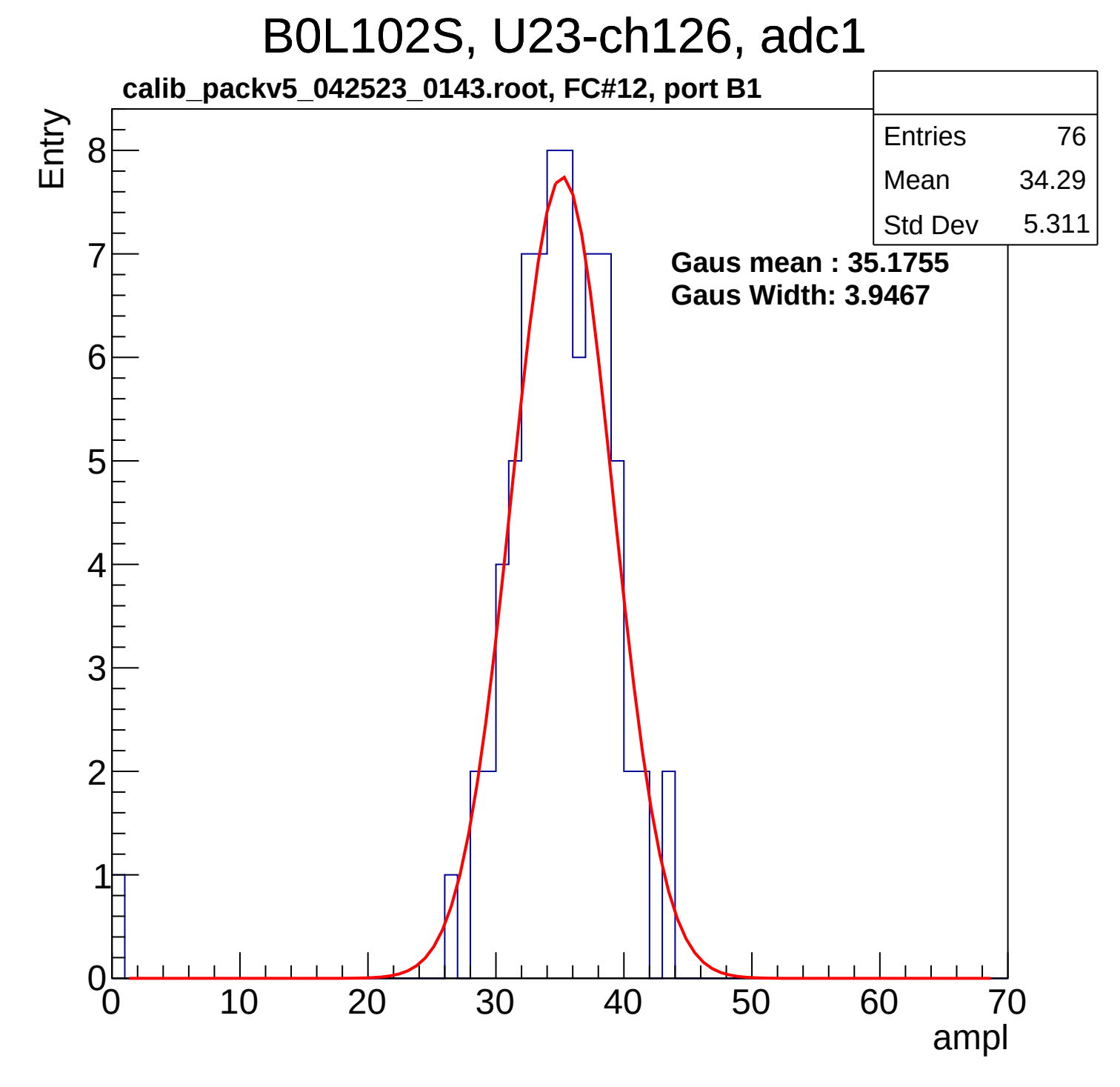
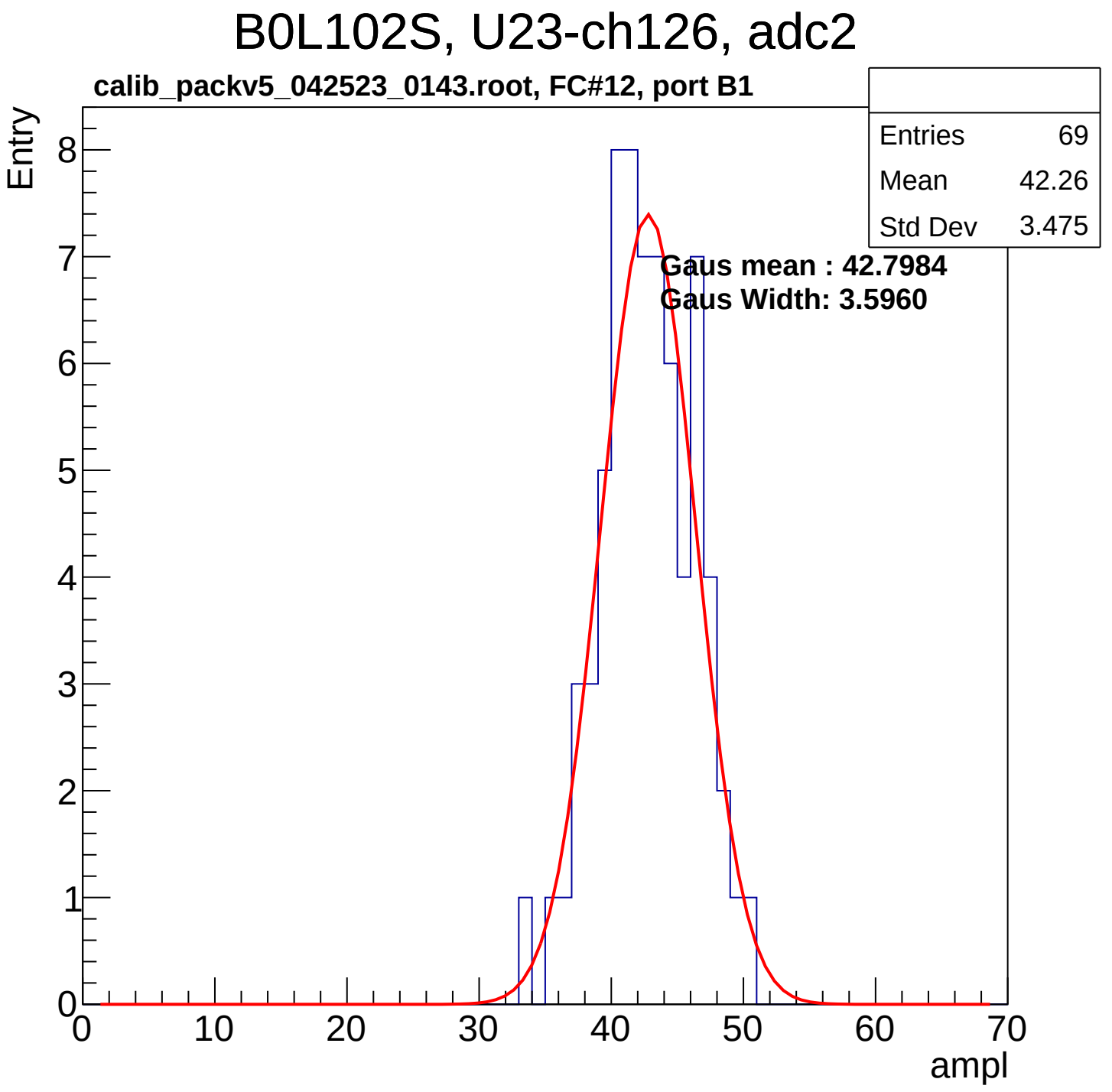
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch126, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 71
Mean 27.04
Std Dev 5.295
Gaus mean : 28.1967
Gaus Width: 4.3411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch126, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 76
Mean 34.29
Std Dev 5.311
Gaus mean : 35.1755
Gaus Width: 3.9467
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch126, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 69
Mean 42.26
Std Dev 3.475
Gaus mean : 42.7984
Gaus Width: 3.5960
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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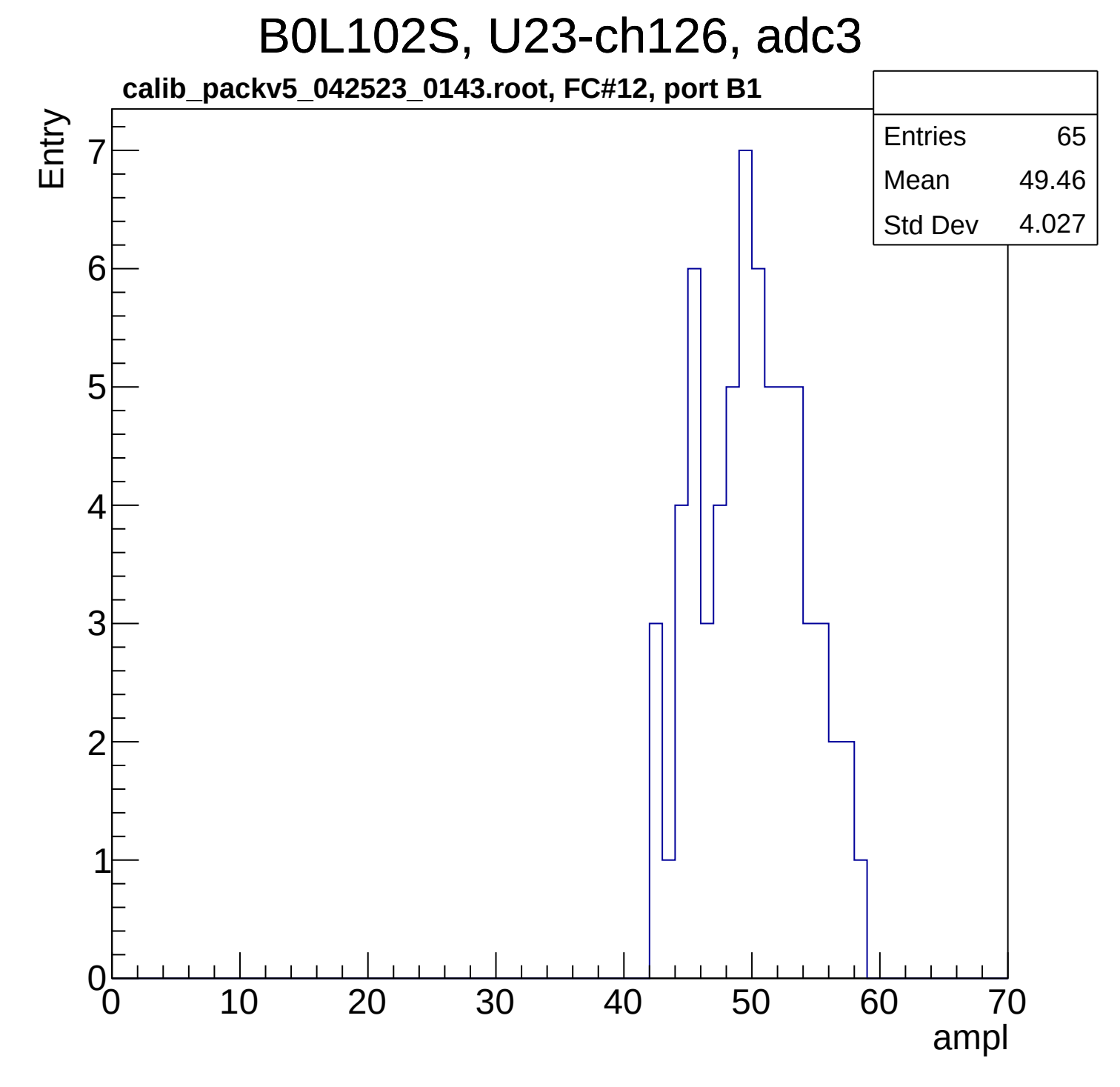
B0L102S, U23-ch126, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 65
Mean 49.46
Std Dev 4.027
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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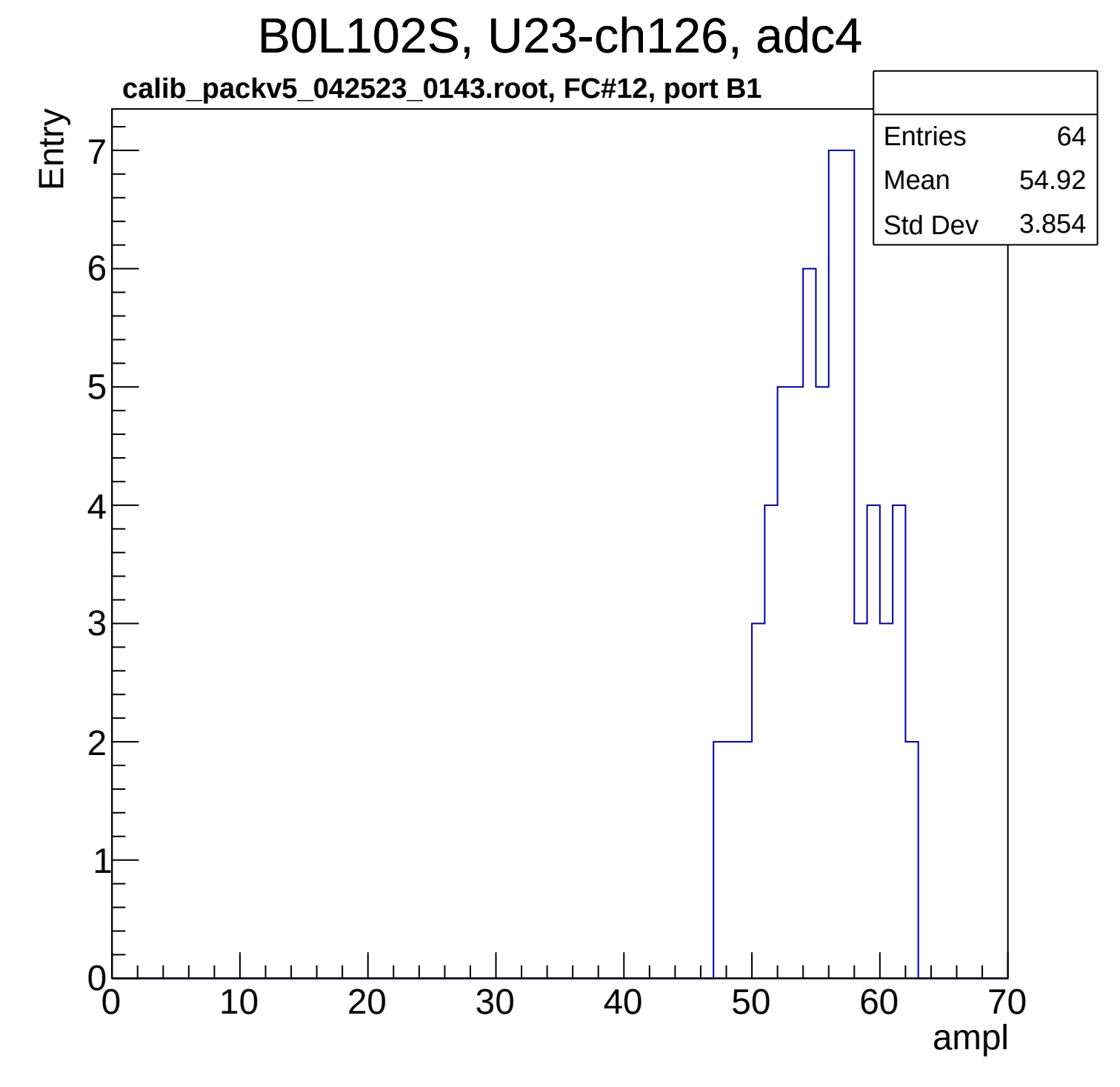
B0L102S, U23-ch126, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 64
Mean 54.92
Std Dev 3.854
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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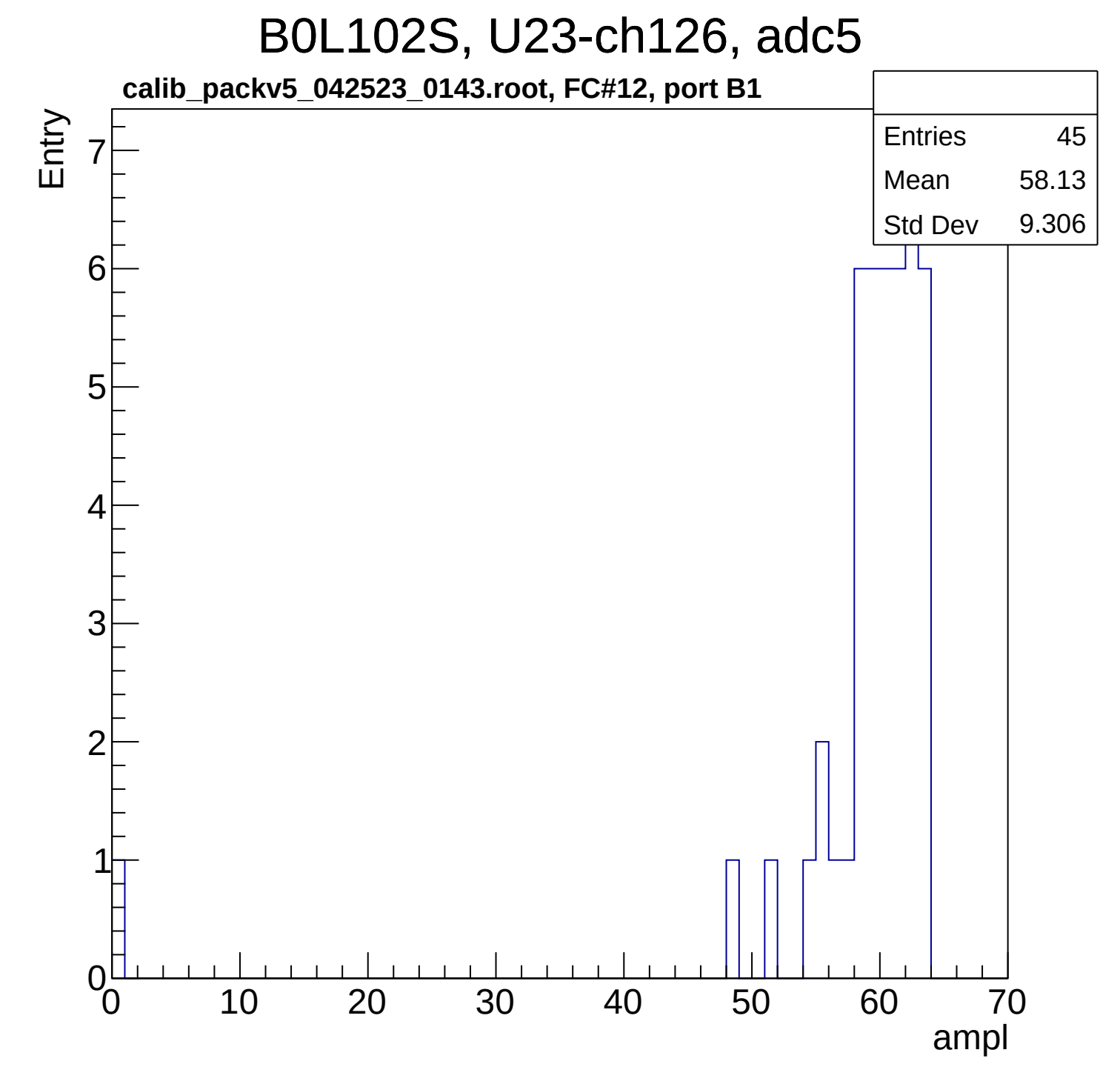
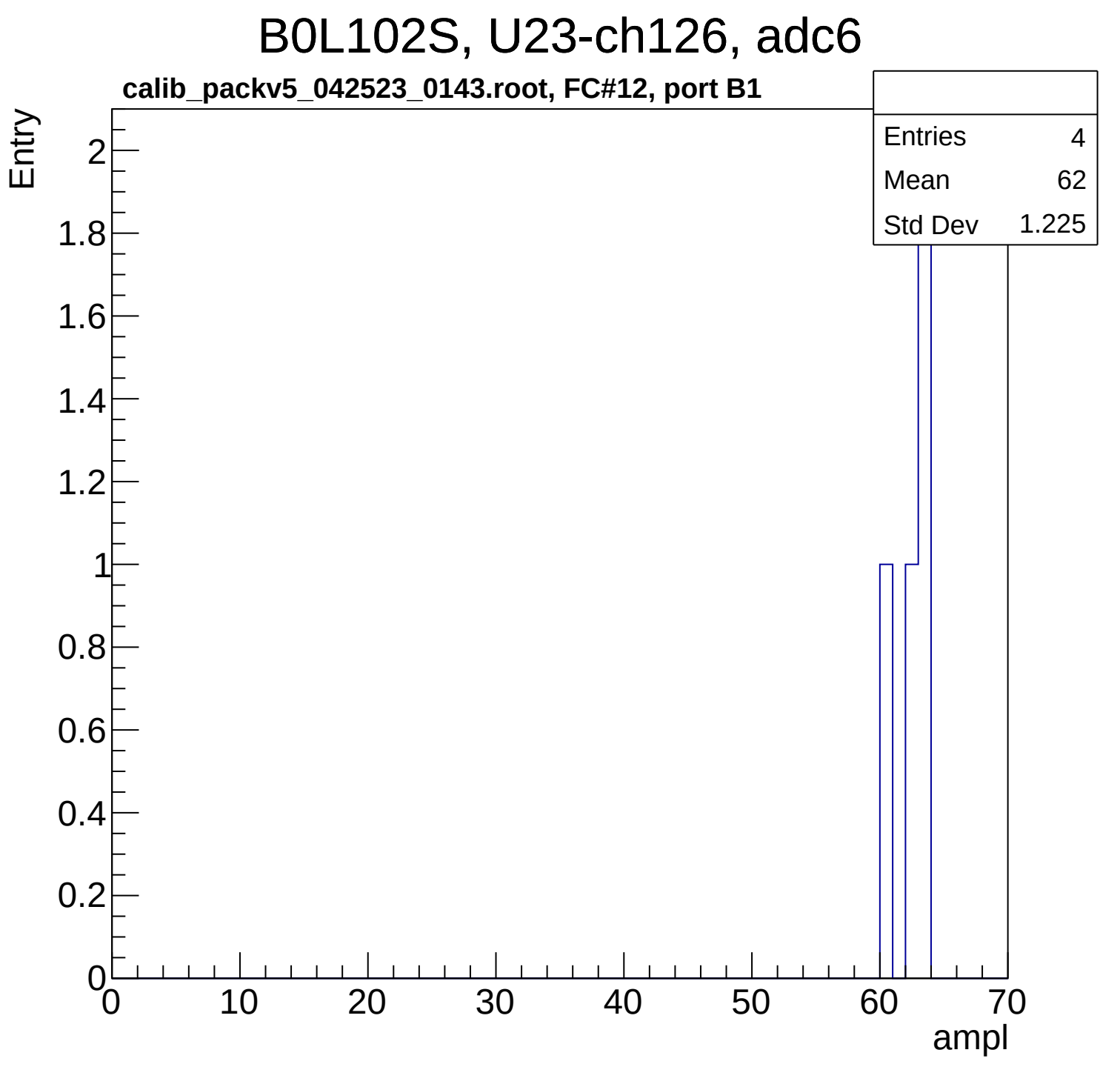

B0L102S, U23-ch126, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 45
Mean 58.13
Std Dev 9.306
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch126, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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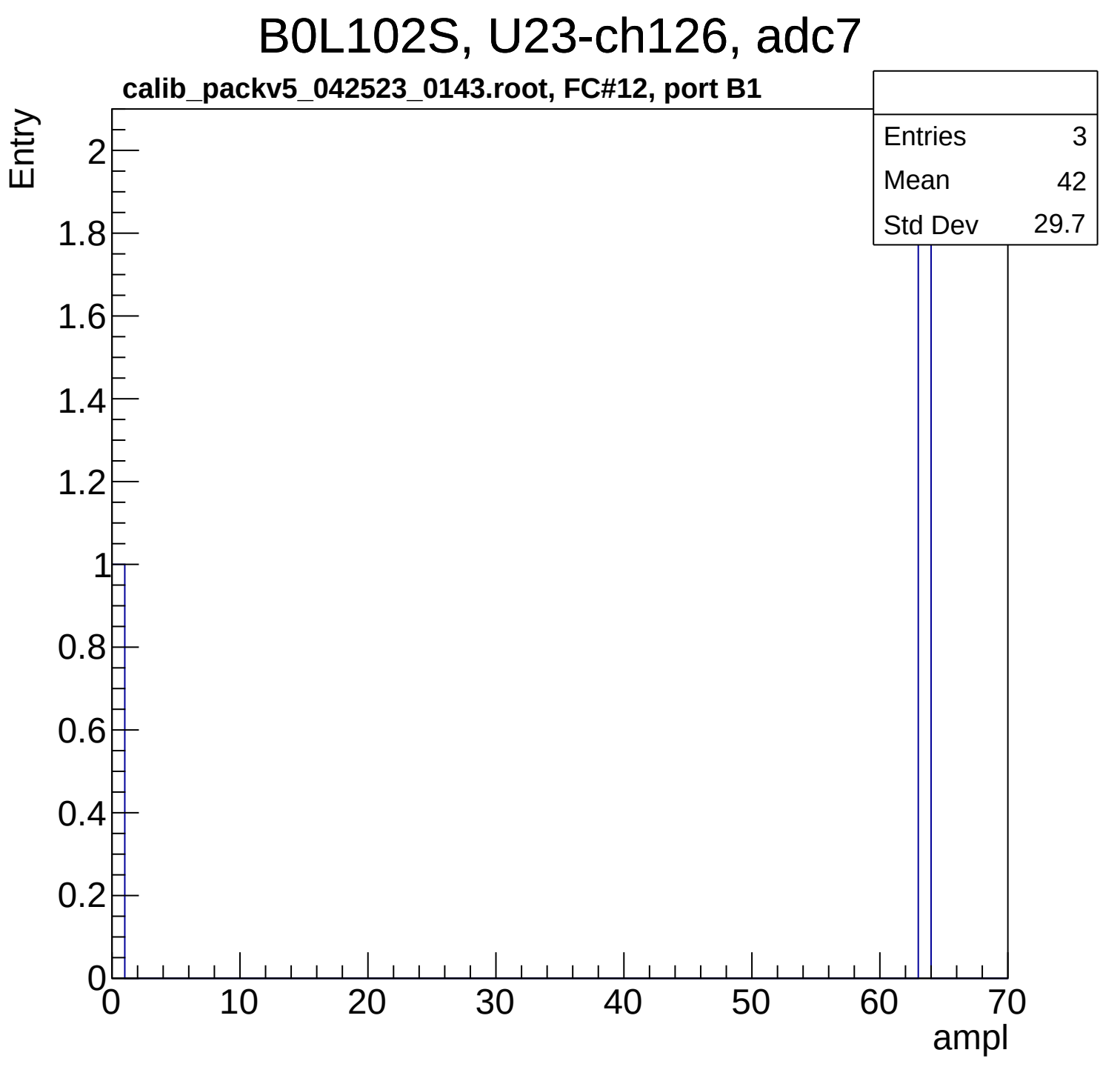
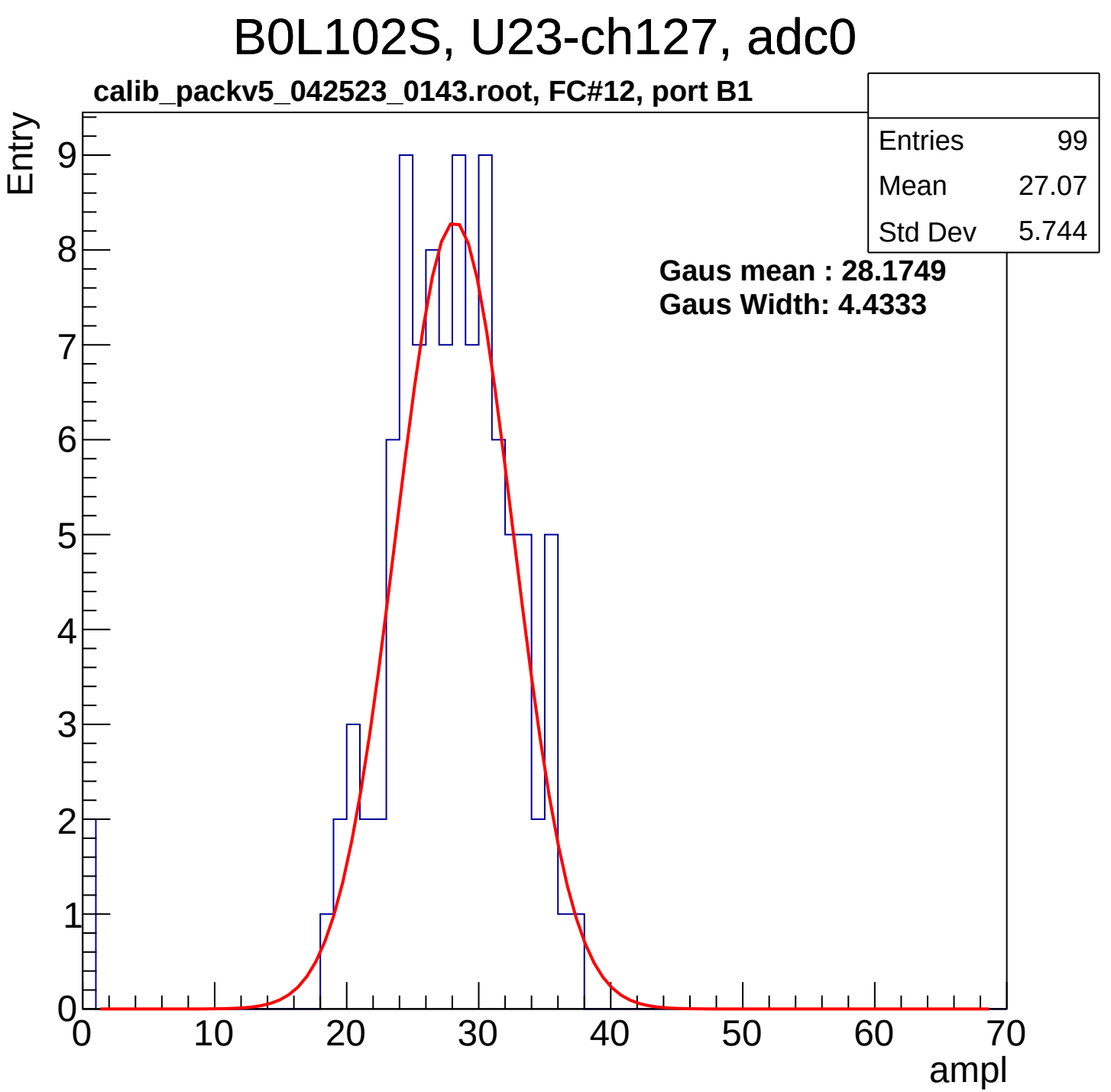
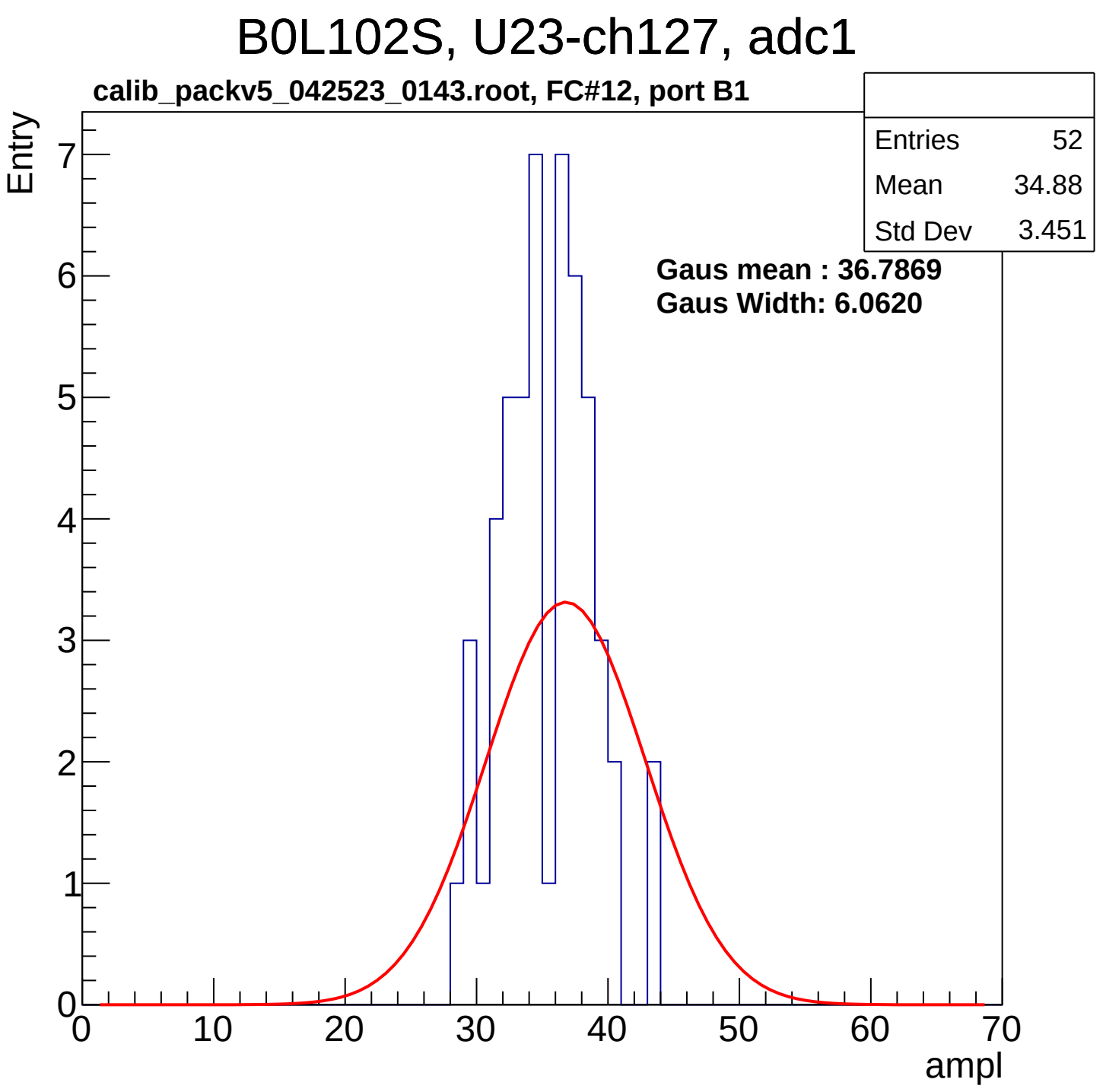
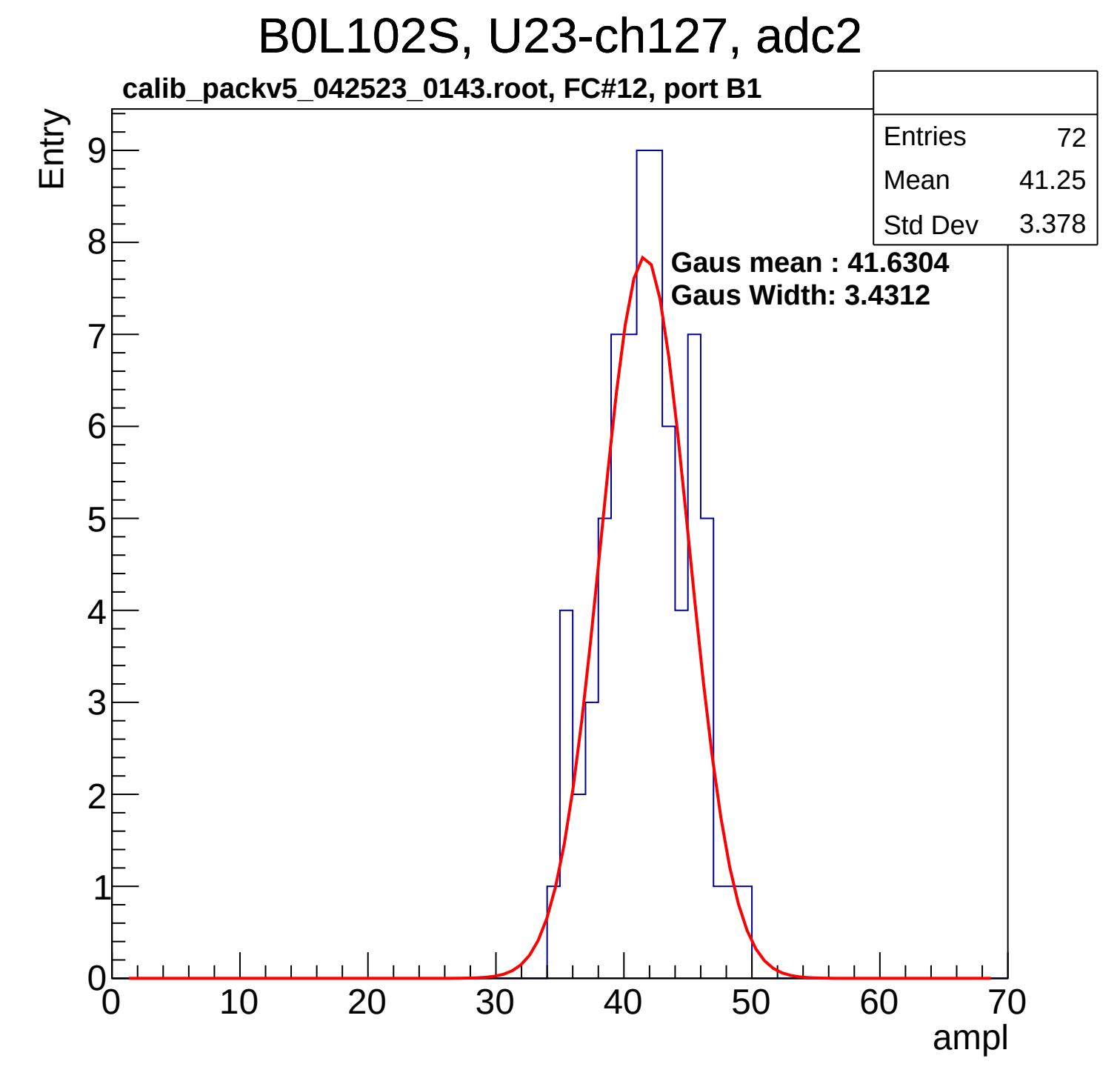
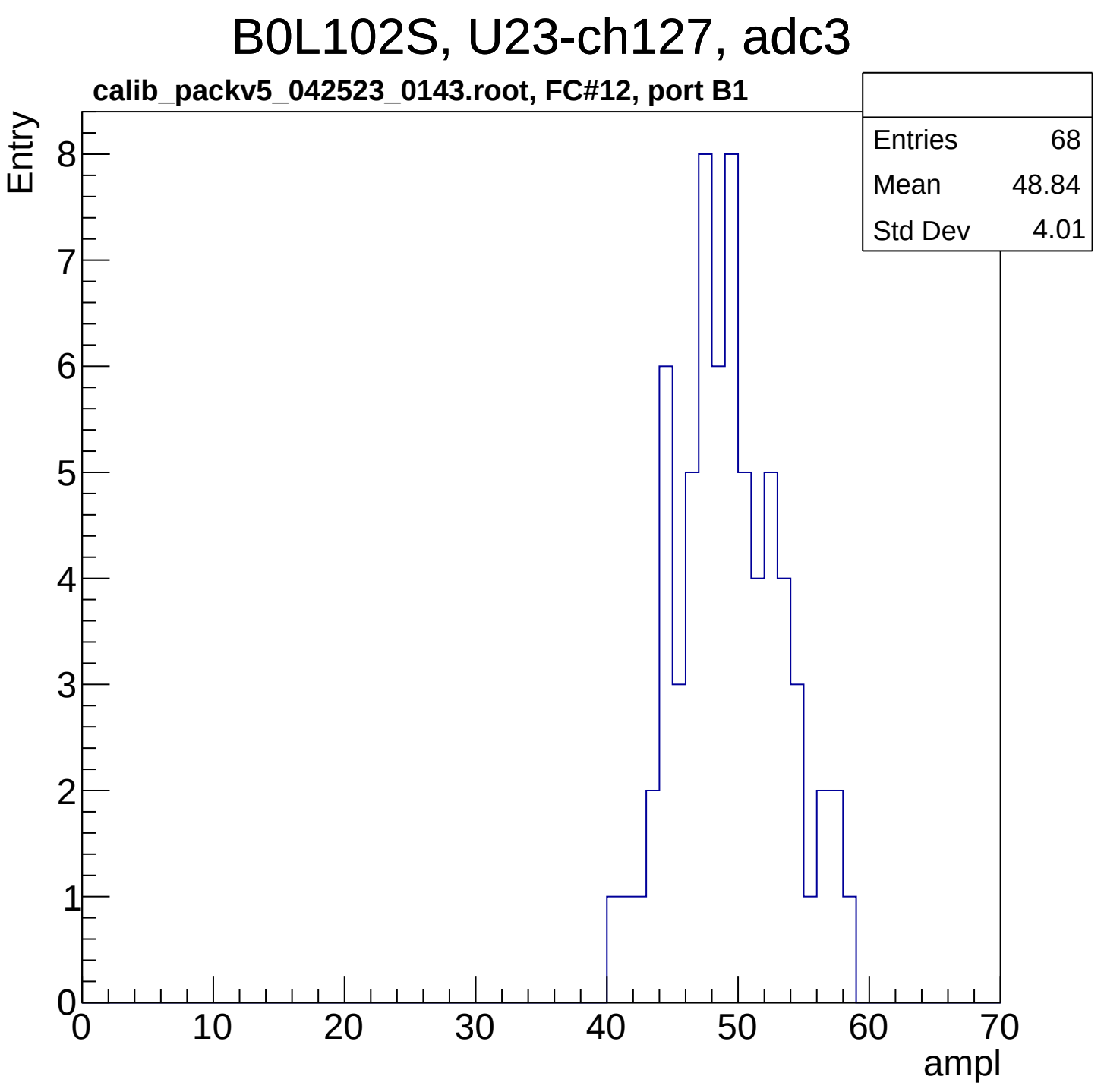
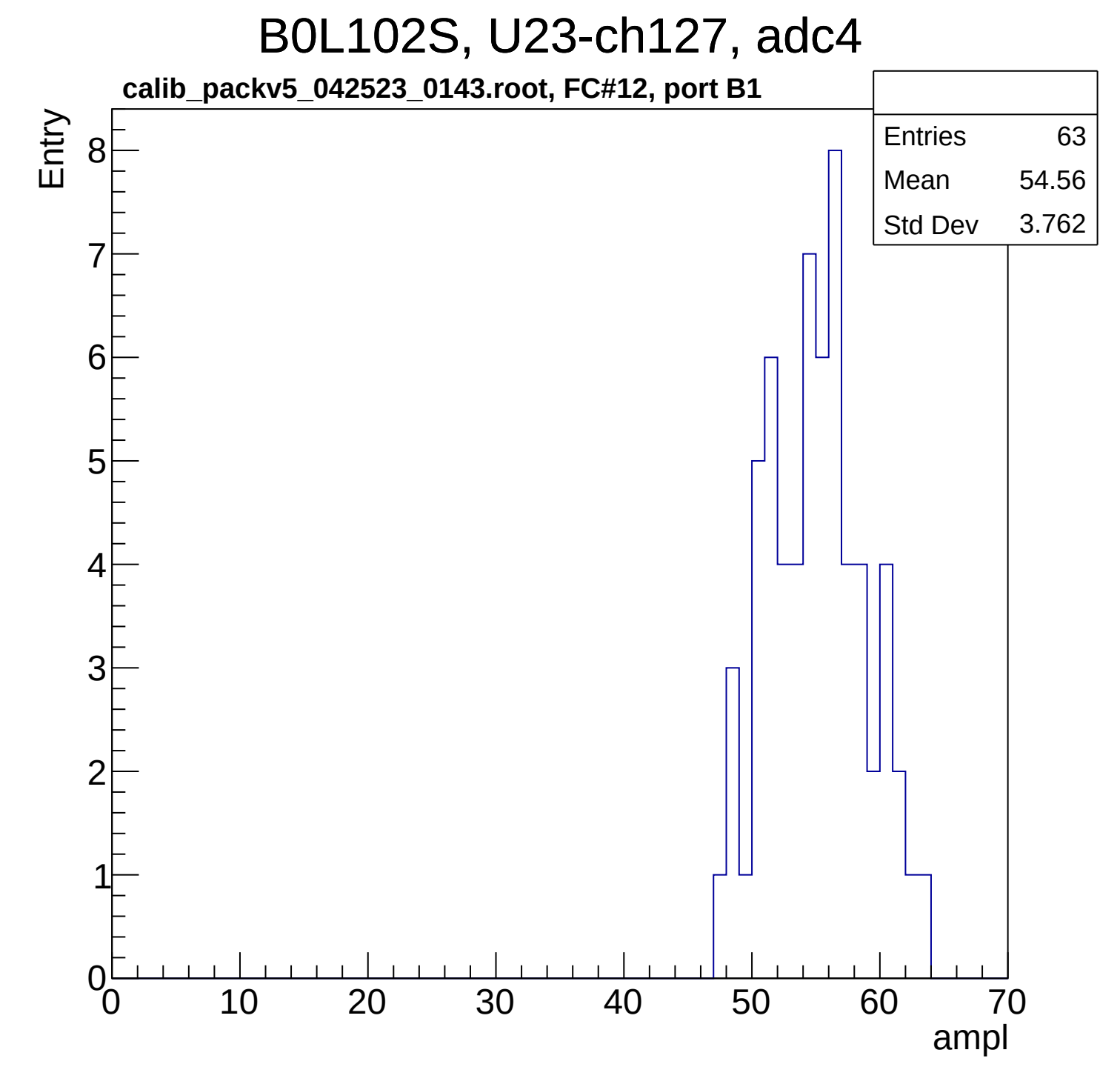

B0L102S, U23-ch126, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

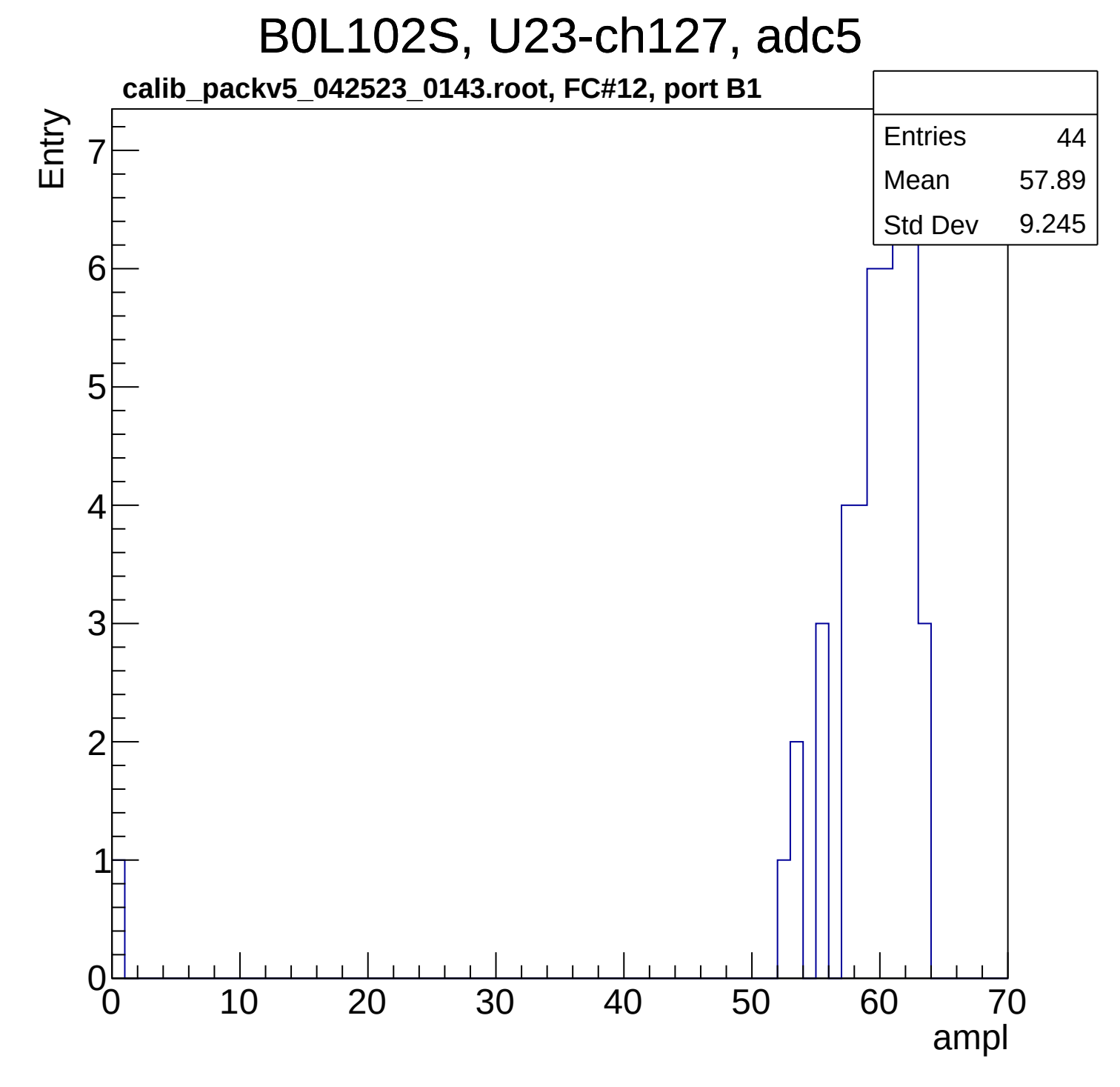

B0L102S, U23-ch127, adc0
calib_packv5_042523_0143.root, FC#12, port B1
Entries 99
Mean 27.07
Std Dev 5.744
Gaus mean : 28.1749
Gaus Width: 4.4333
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

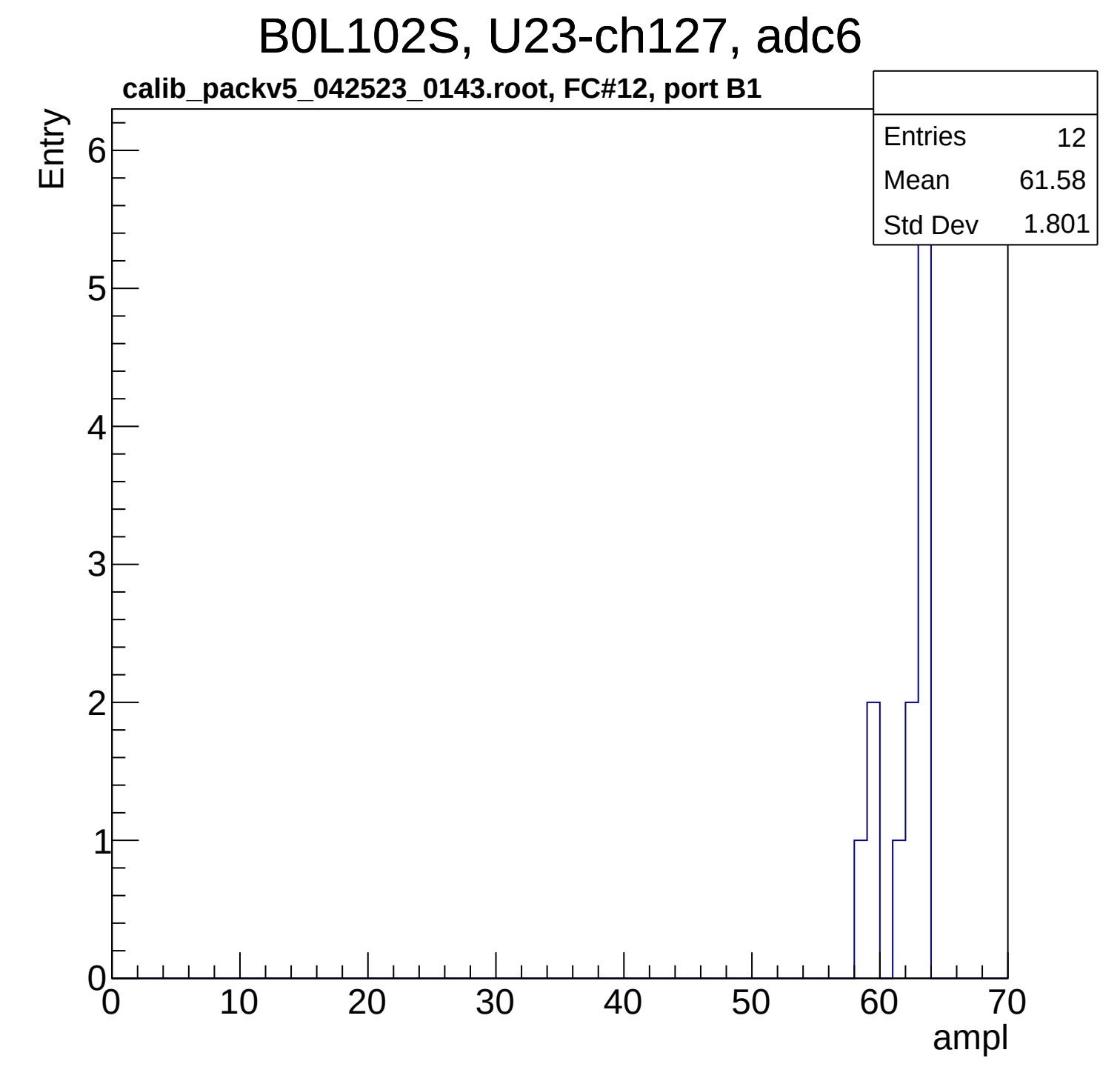

B0L102S, U23-ch127, adc1
calib_packv5_042523_0143.root, FC#12, port B1
Entries 52
Mean 34.88
Std Dev 3.451
Gaus mean : 36.7869
Gaus Width: 6.0620
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

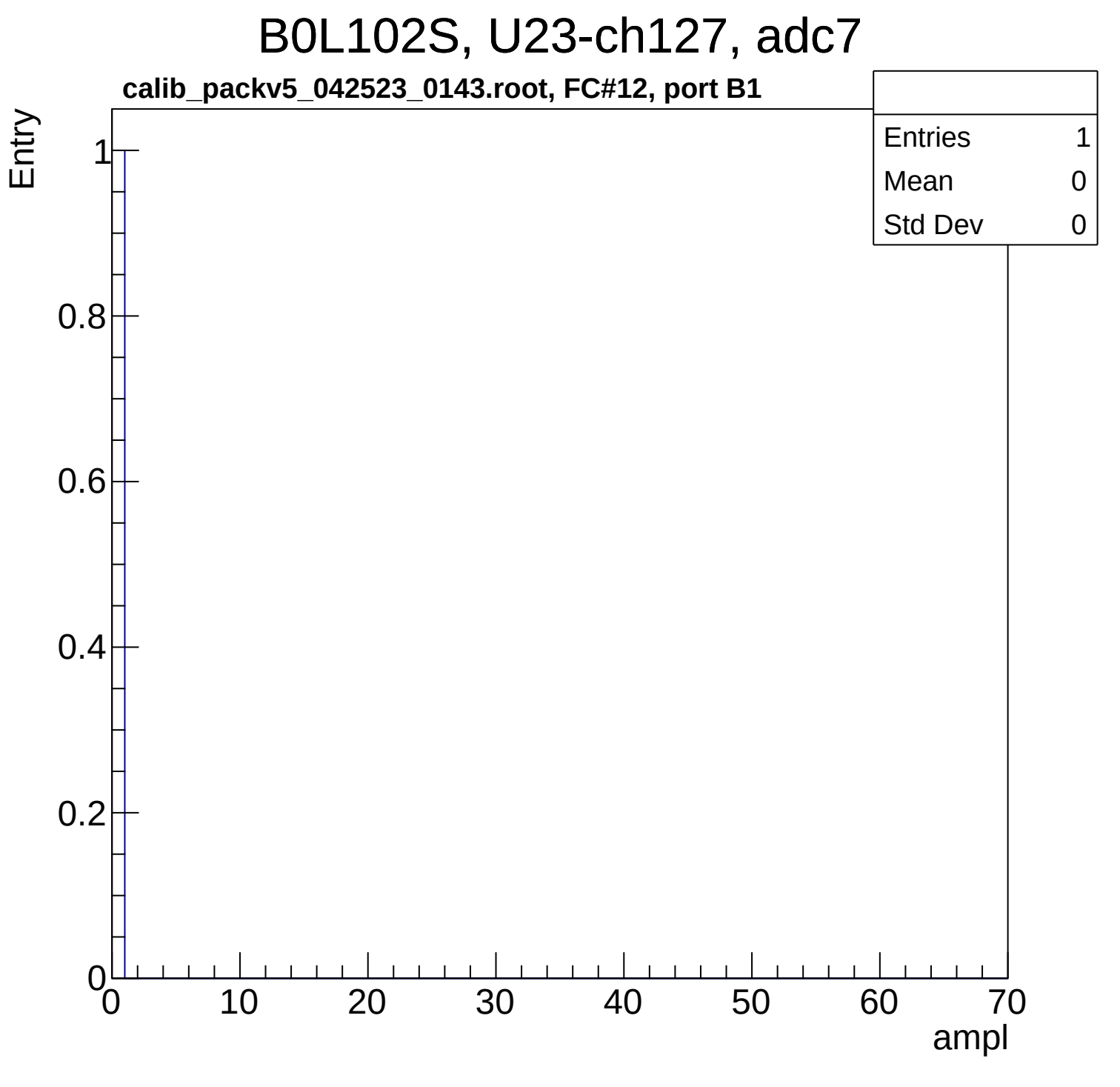

B0L102S, U23-ch127, adc2
calib_packv5_042523_0143.root, FC#12, port B1
Entries 72
Mean 41.25
Std Dev 3.378
Gaus mean : 41.6304
Gaus Width: 3.4312
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

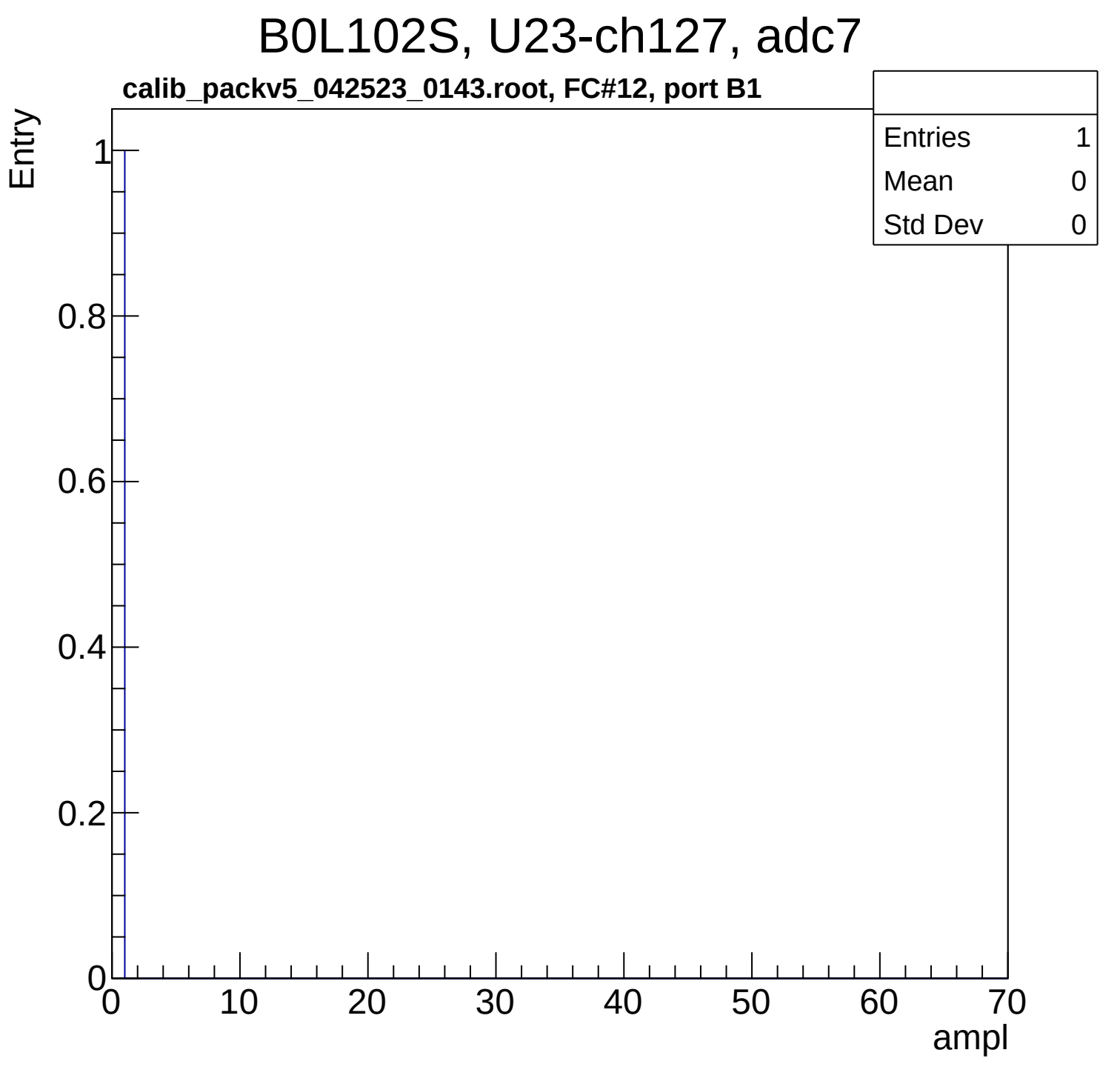

B0L102S, U23-ch127, adc3
calib_packv5_042523_0143.root, FC#12, port B1
Entries 68
Mean 48.84
Std Dev 4.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch127, adc4
calib_packv5_042523_0143.root, FC#12, port B1
Entries 63
Mean 54.56
Std Dev 3.762
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L102S, U23-ch127, adc5
calib_packv5_042523_0143.root, FC#12, port B1
Entries 44
Mean 57.89
Std Dev 9.245
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L102S, U23-ch127, adc6
calib_packv5_042523_0143.root, FC#12, port B1
Entries 12
Mean 61.58
Std Dev 1.801
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L102S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#12, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl