

B1L001S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#2, port C2

Events

1600

1400

1200

1000

800

600

400

200

0

0

50

100

150

200

250

Channel entry

Entries	3328
Mean	90.01
Std Dev	90.01