

B0L002S, U26-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.75
Std Dev	11.21

Turn on : 27.1957

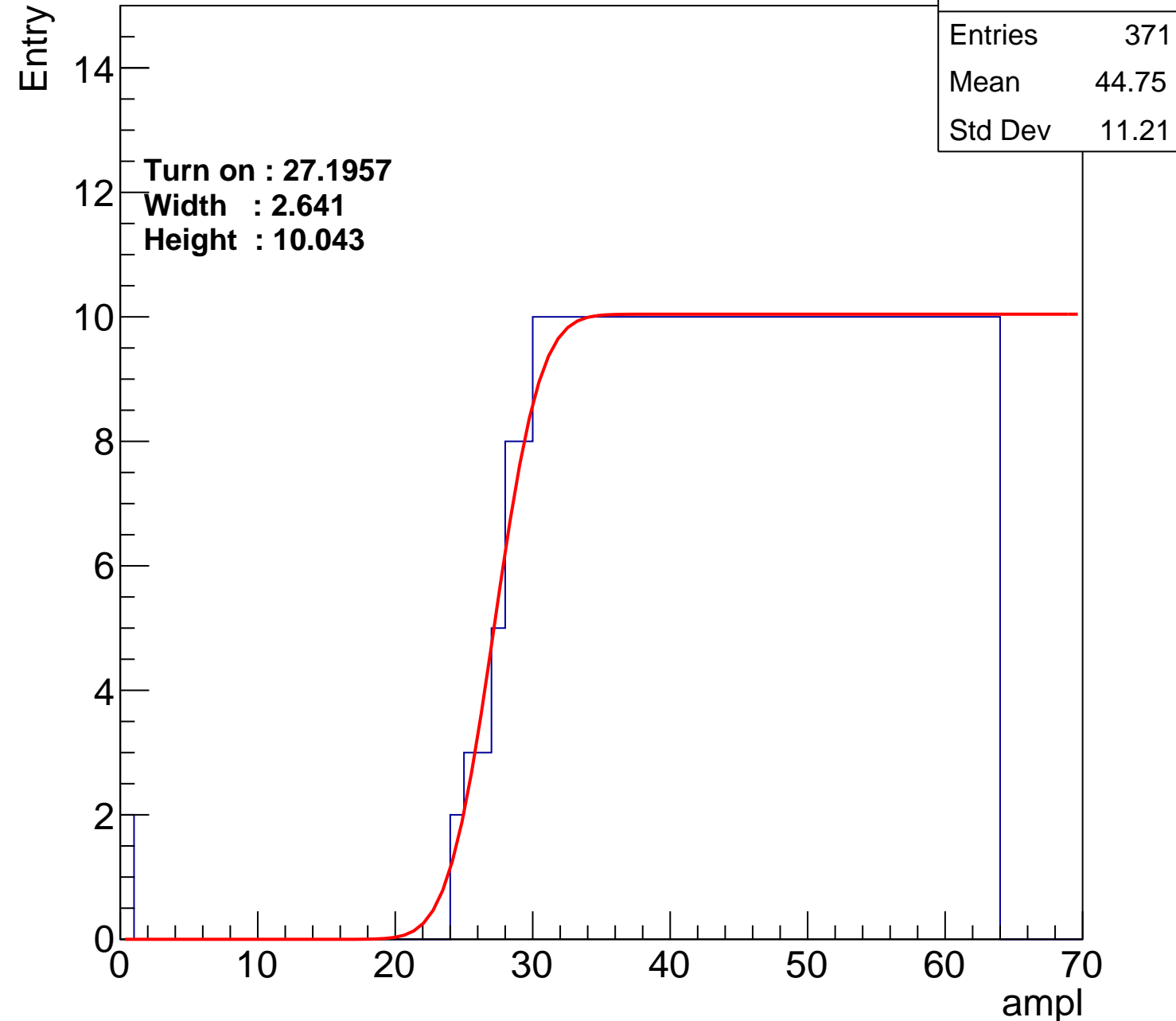
Width : 2.641

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.54
Std Dev	10.85

Turn on : 28.8722

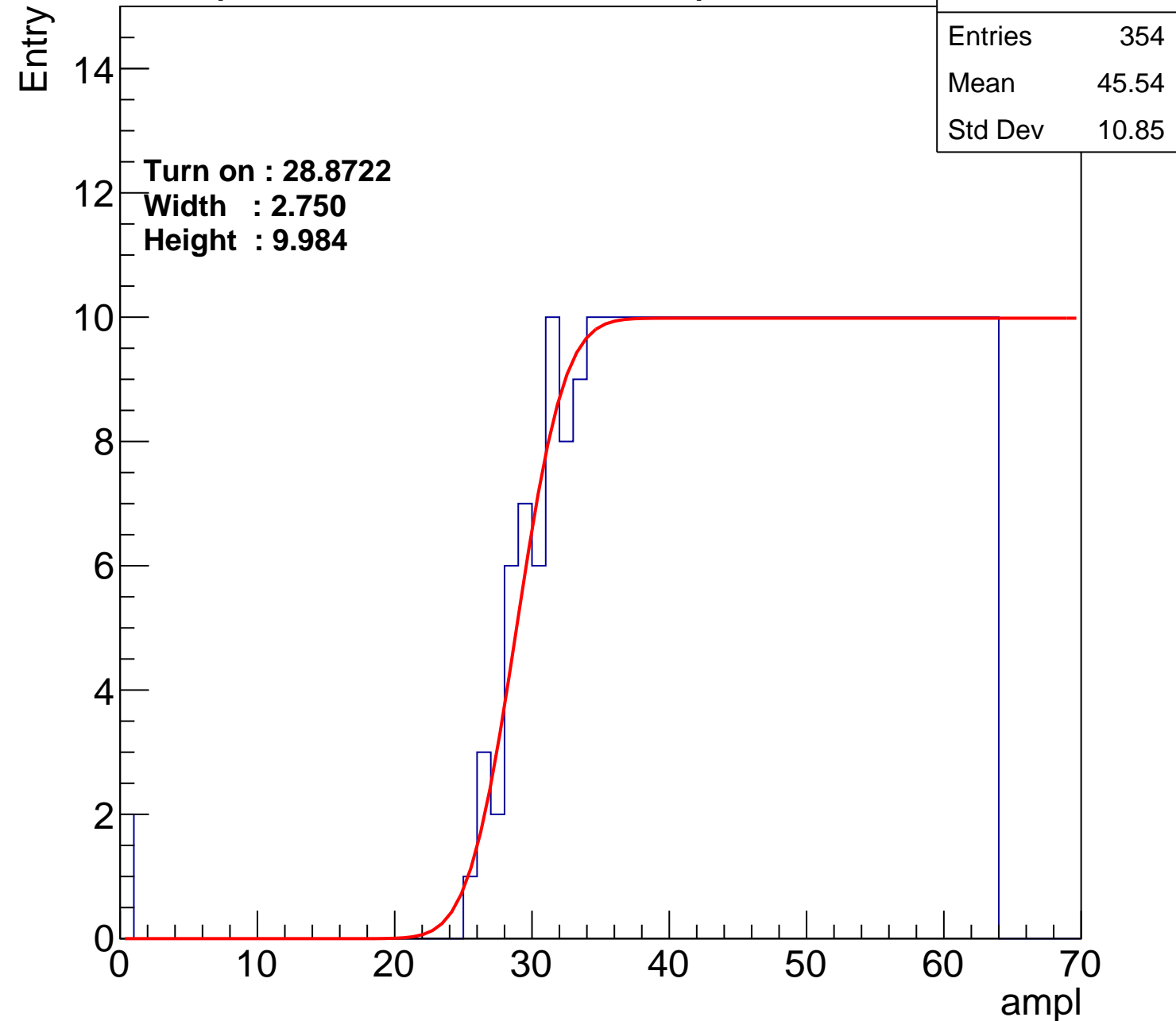
Width : 2.750

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	330
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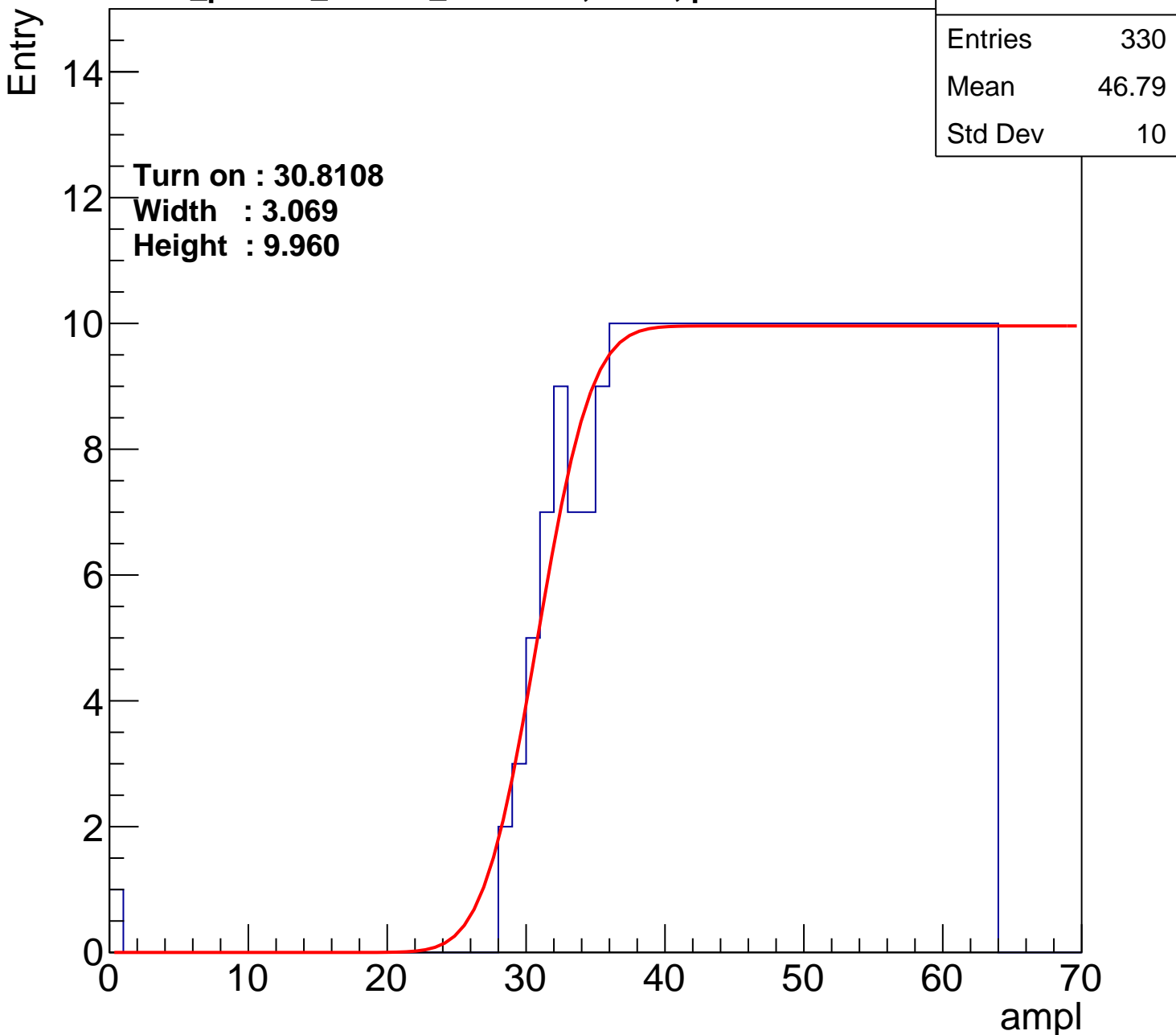
Mean	46.79
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Std Dev	10
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Turn on : 30.8108

Width : 3.069

Height : 9.960



B0L002S, U26-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.58
Std Dev	11.33

Turn on : 26.9418

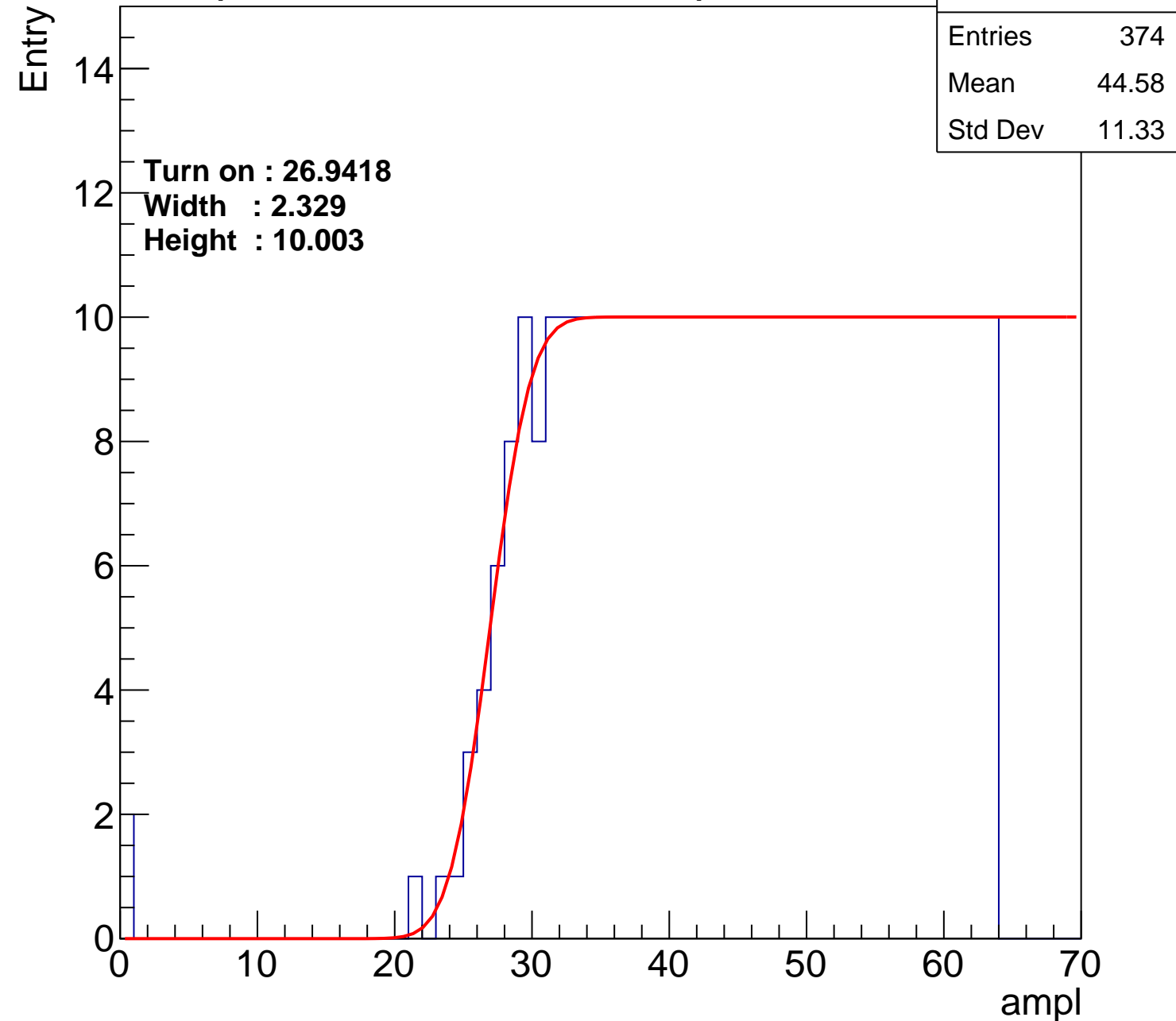
Width : 2.329

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch4

calib_packv5_042523_0143.root, FC#8, port C1

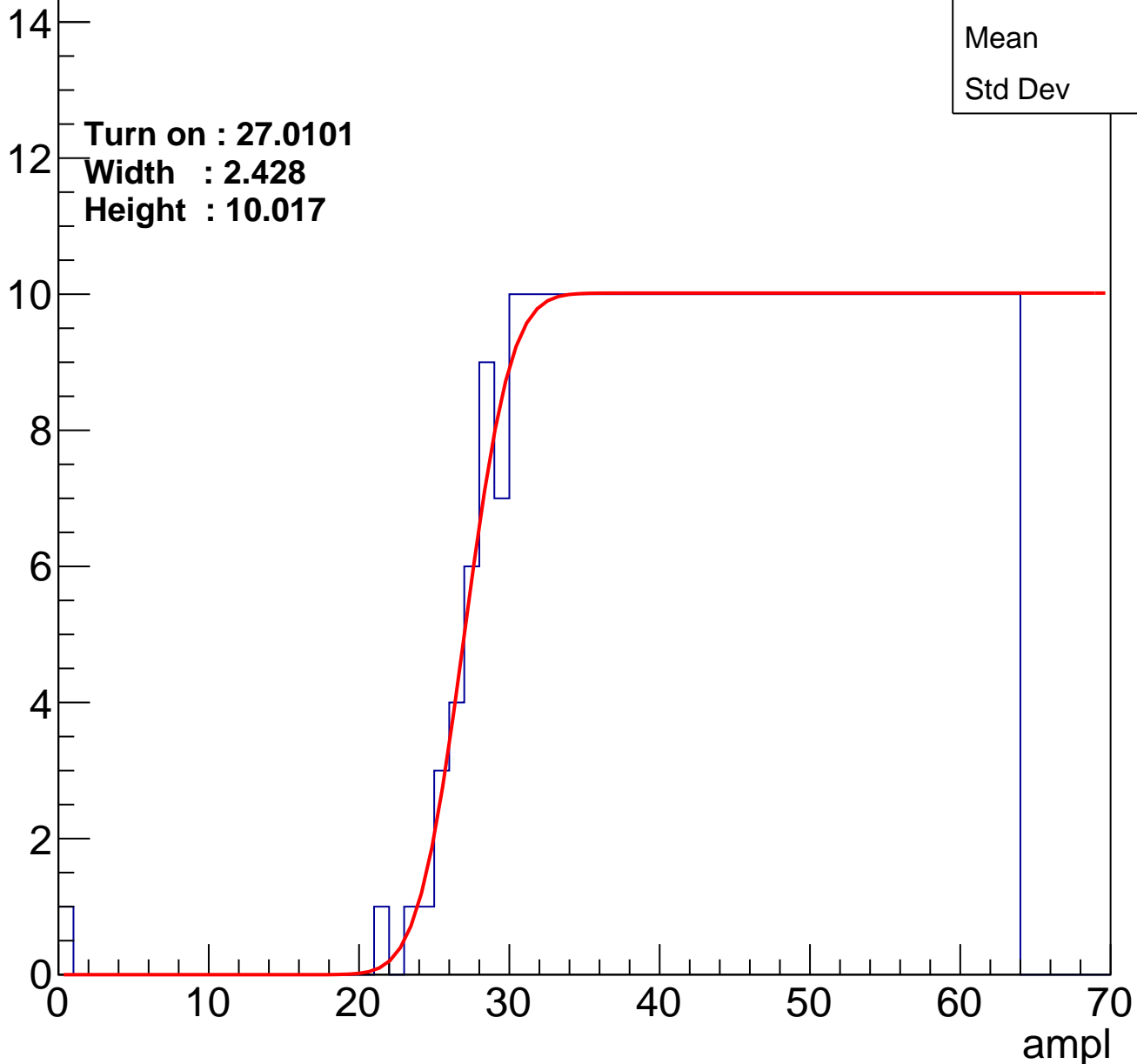
Entry

Entries	373
Mean	44.7
Std Dev	11.1

Turn on : 27.0101

Width : 2.428

Height : 10.017



B0L002S, U26-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.09
Std Dev	11.1

Turn on : 28.1491

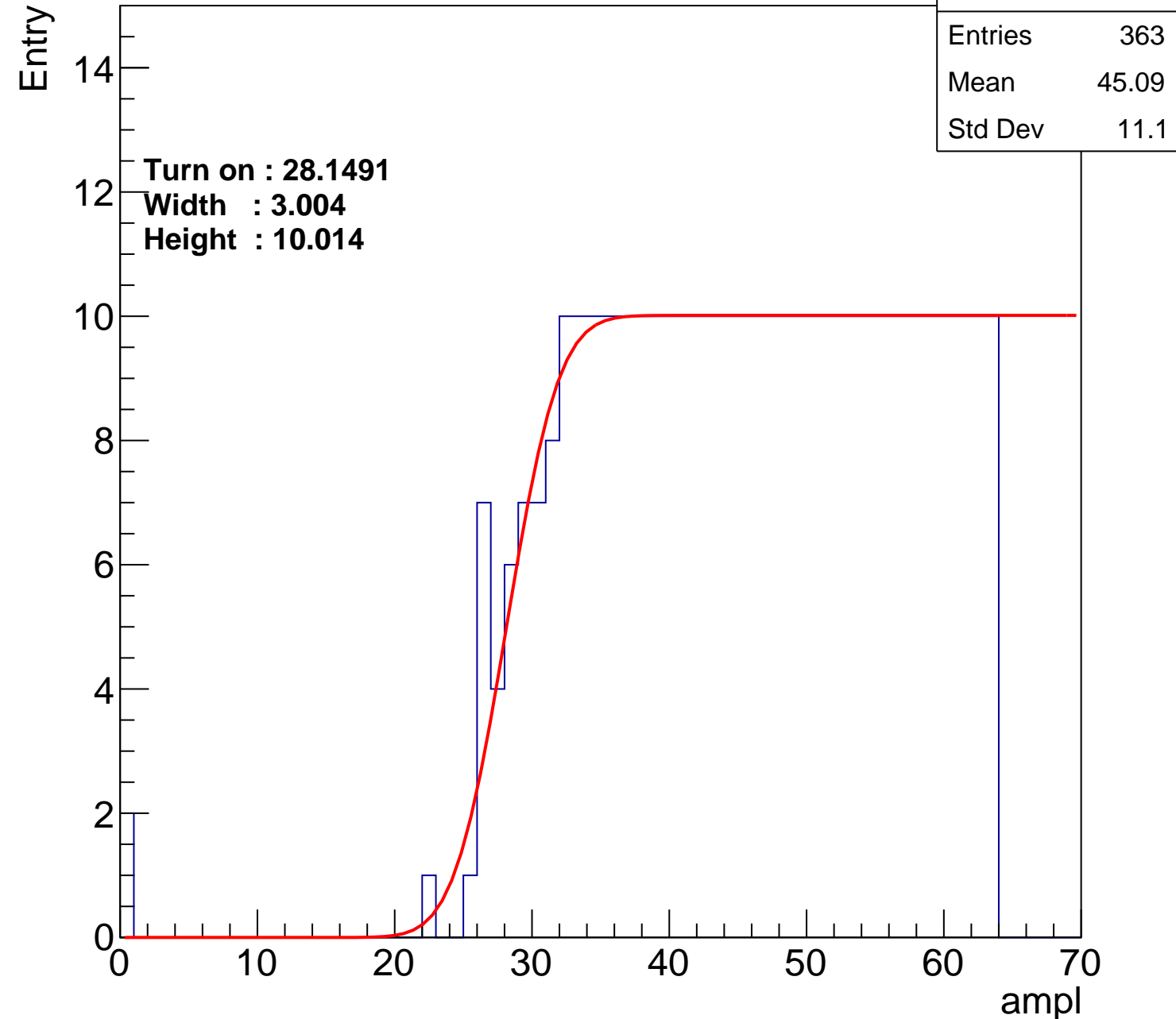
Width : 3.004

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.7018

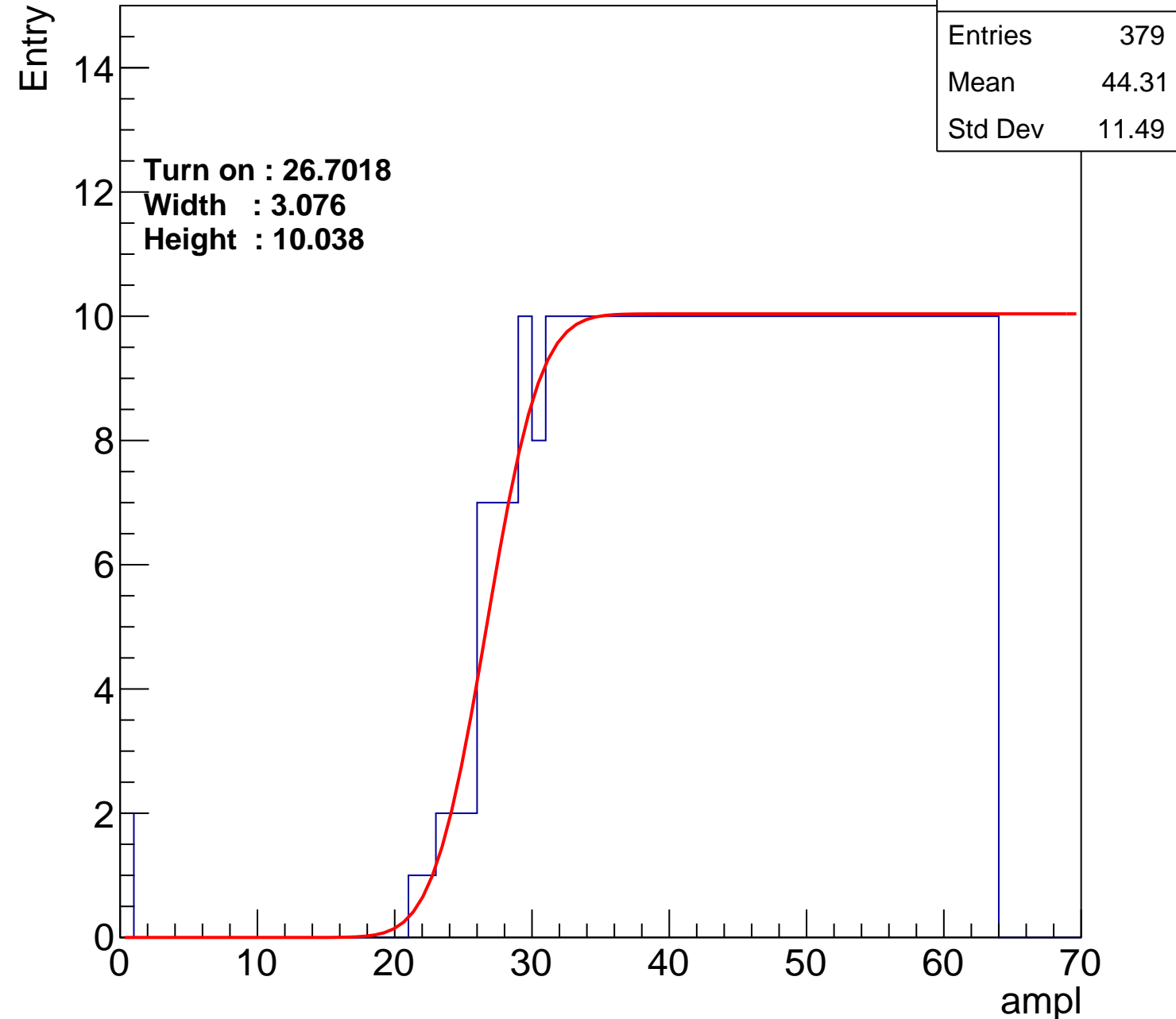
Width : 3.076

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.84
Std Dev	11.55

Turn on : 27.8468

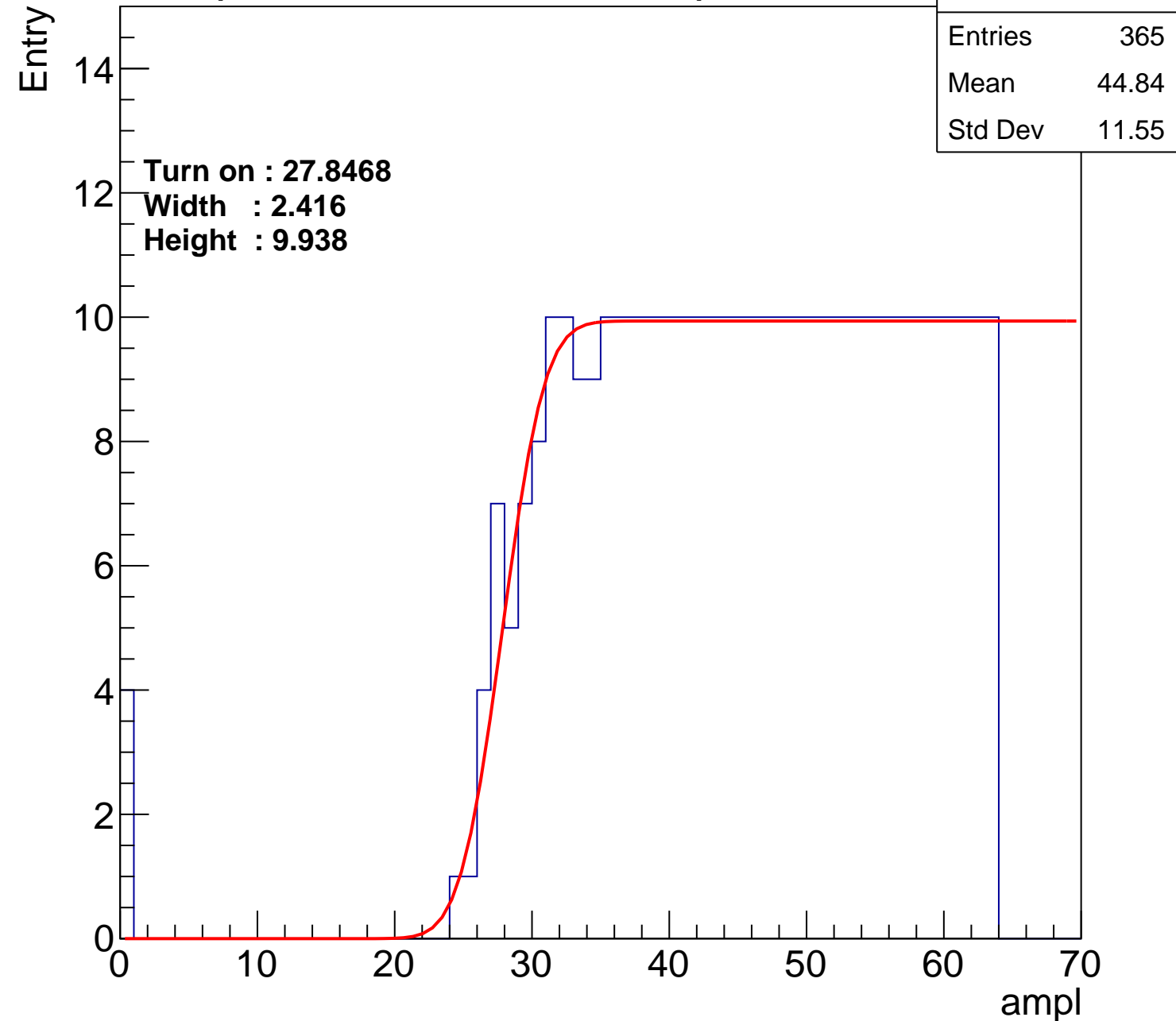
Width : 2.416

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.32
Std Dev	10.74

Turn on : 28.1269

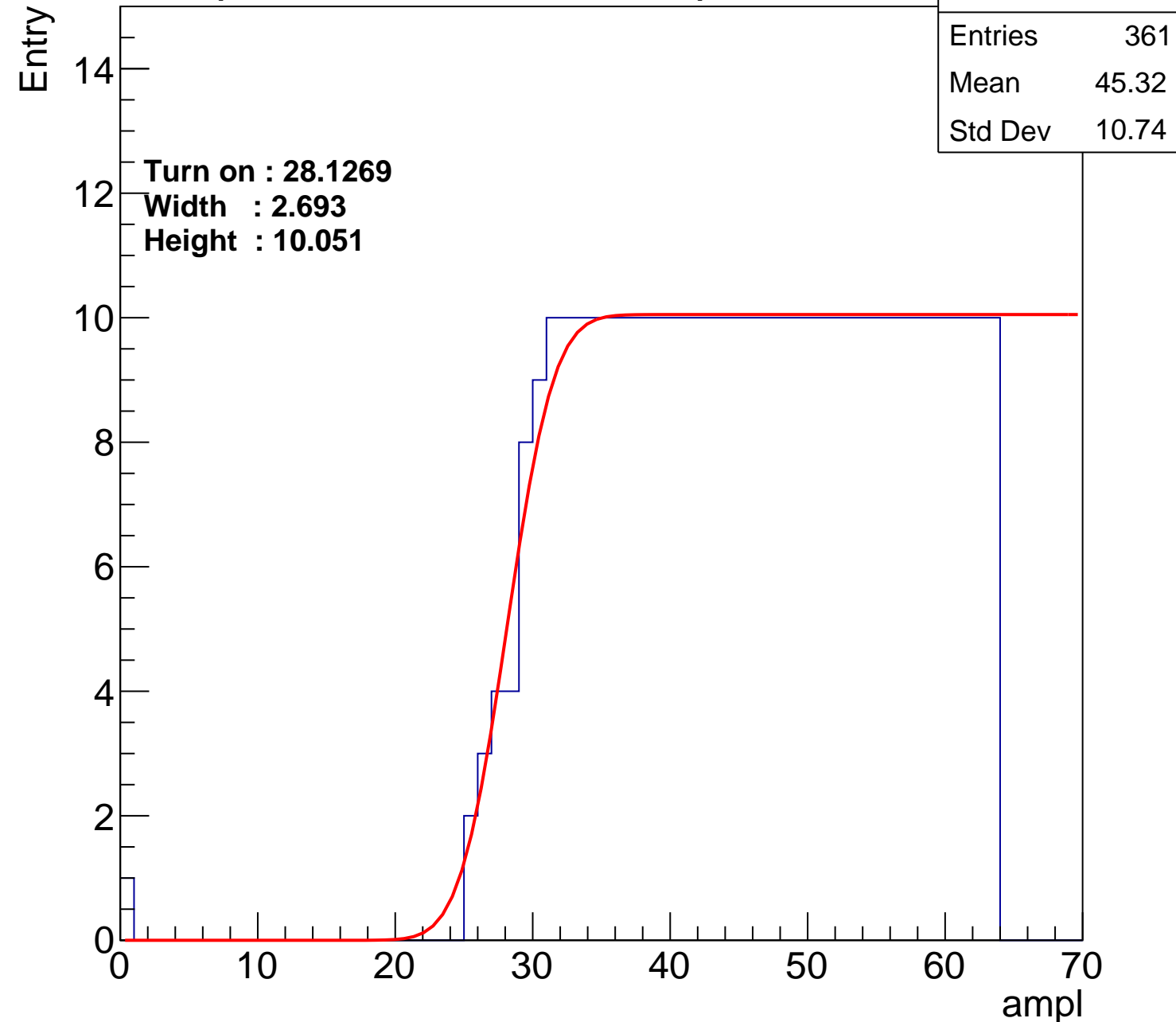
Width : 2.693

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.11
Std Dev	11.06

Turn on : 27.7110

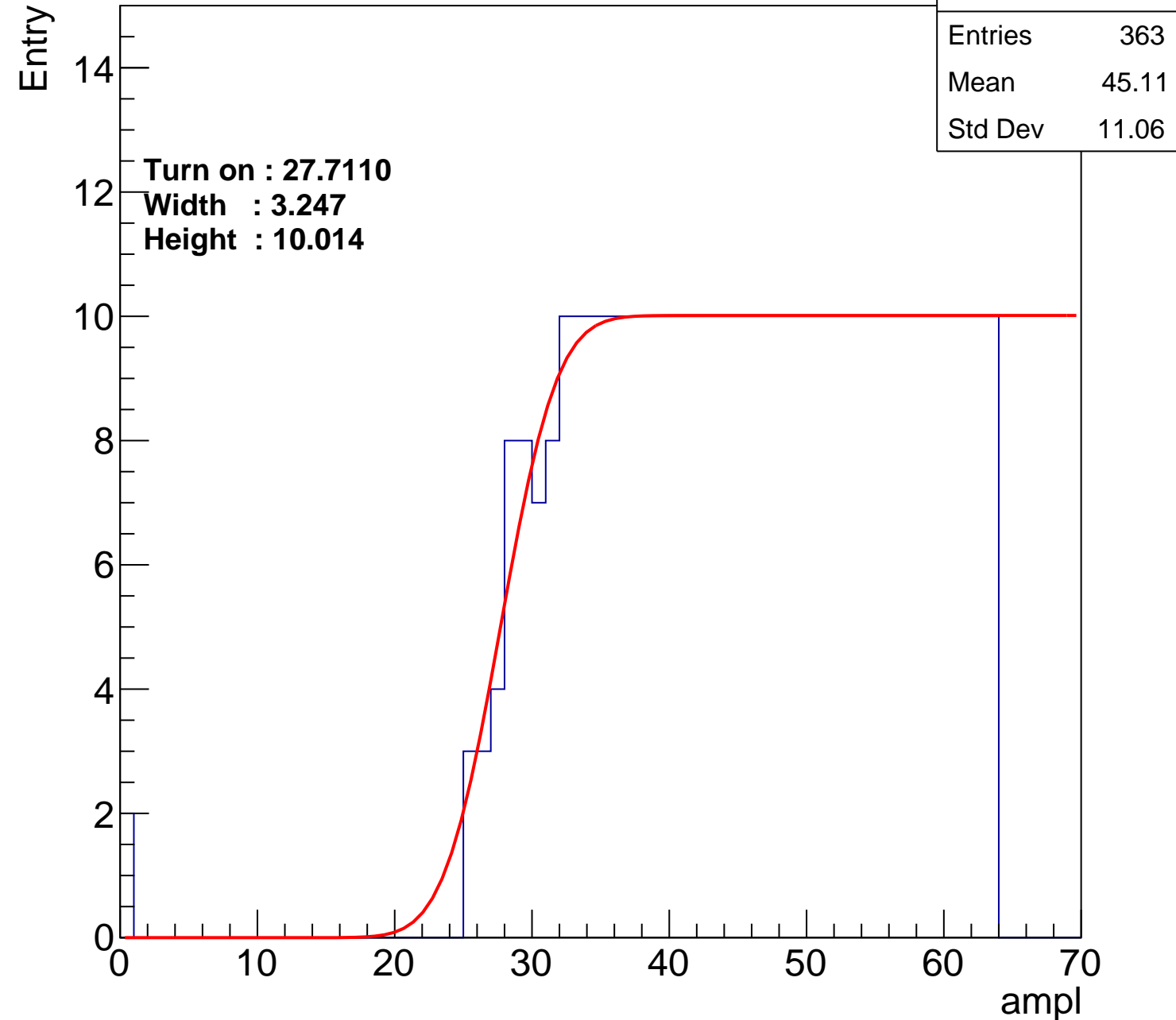
Width : 3.247

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch10

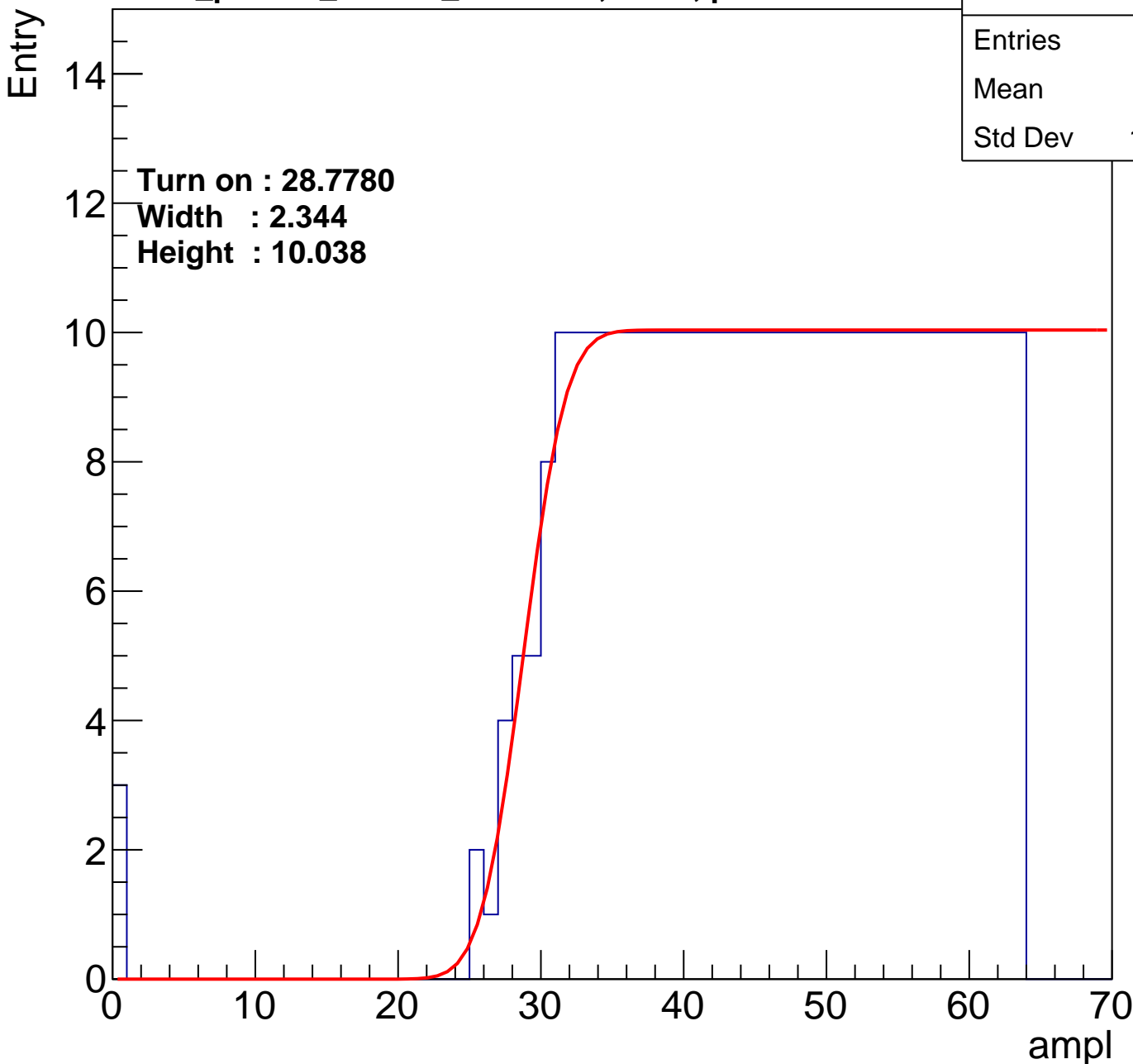
calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.3
Std Dev	11.12

Turn on : 28.7780

Width : 2.344

Height : 10.038



B0L002S, U26-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.16
Std Dev	11.71

Turn on : 26.6493

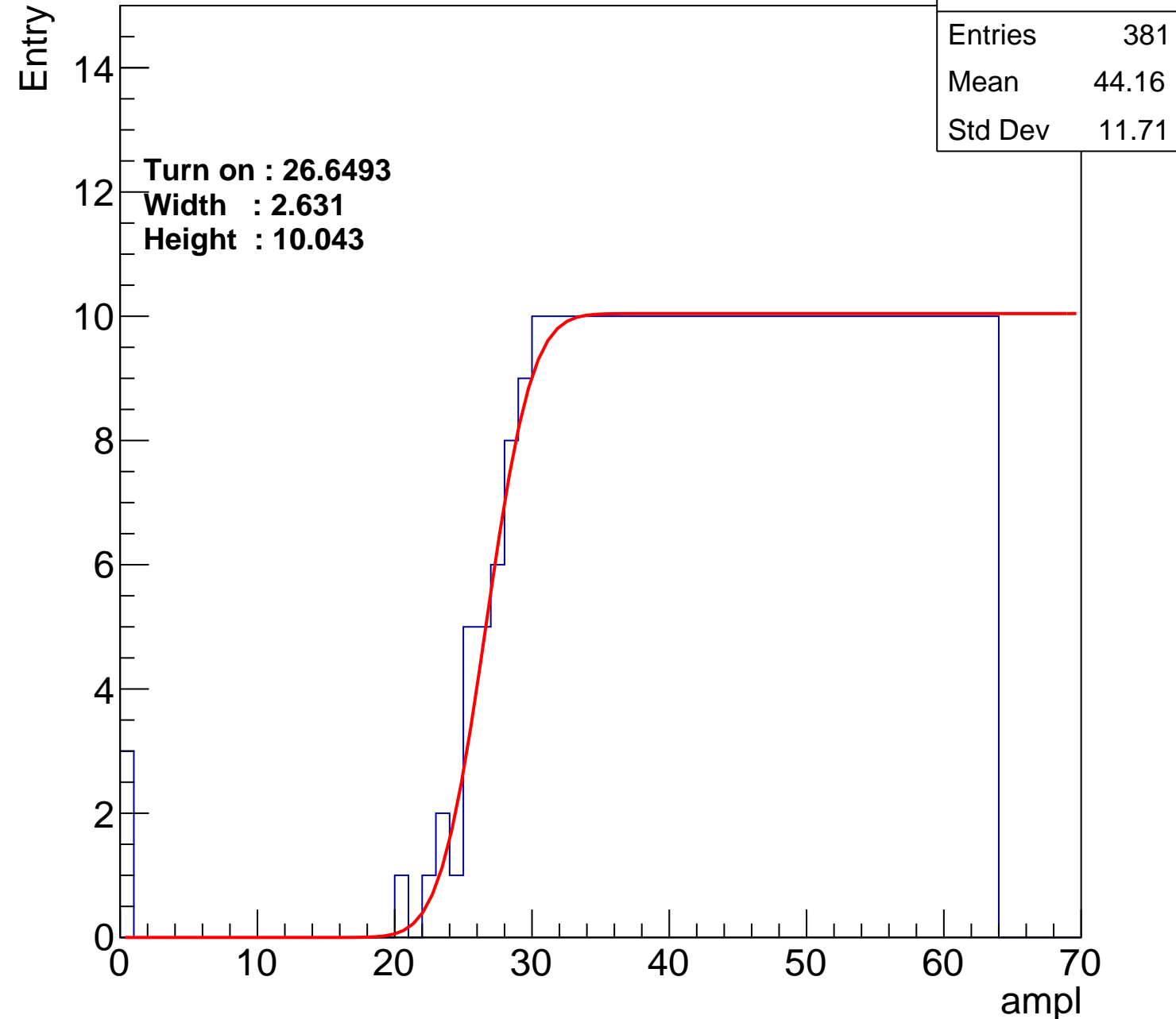
Width : 2.631

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.27
Std Dev	11.02

Turn on : 28.1102

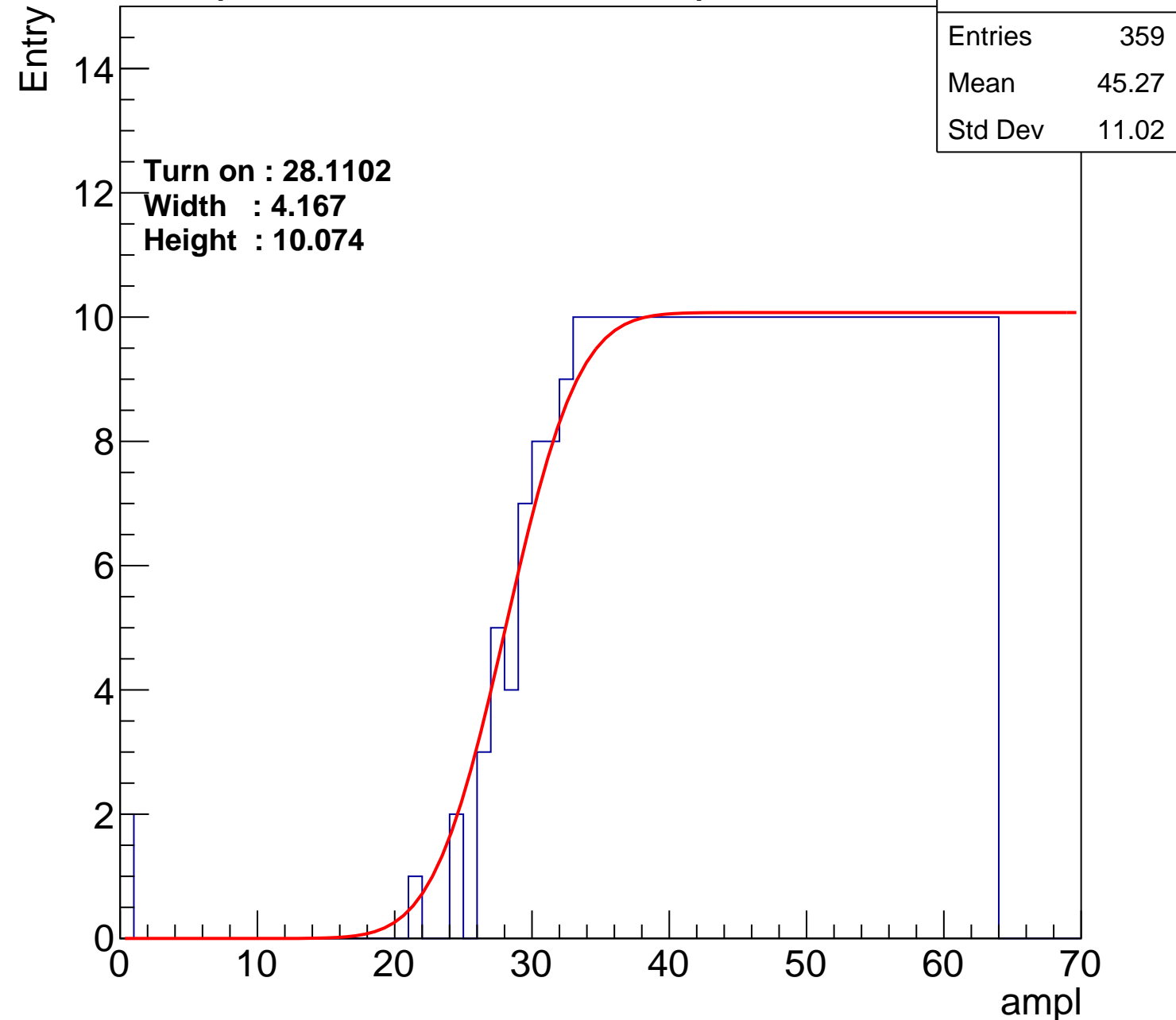
Width : 4.167

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.77
Std Dev	11.43

Turn on : 27.8493

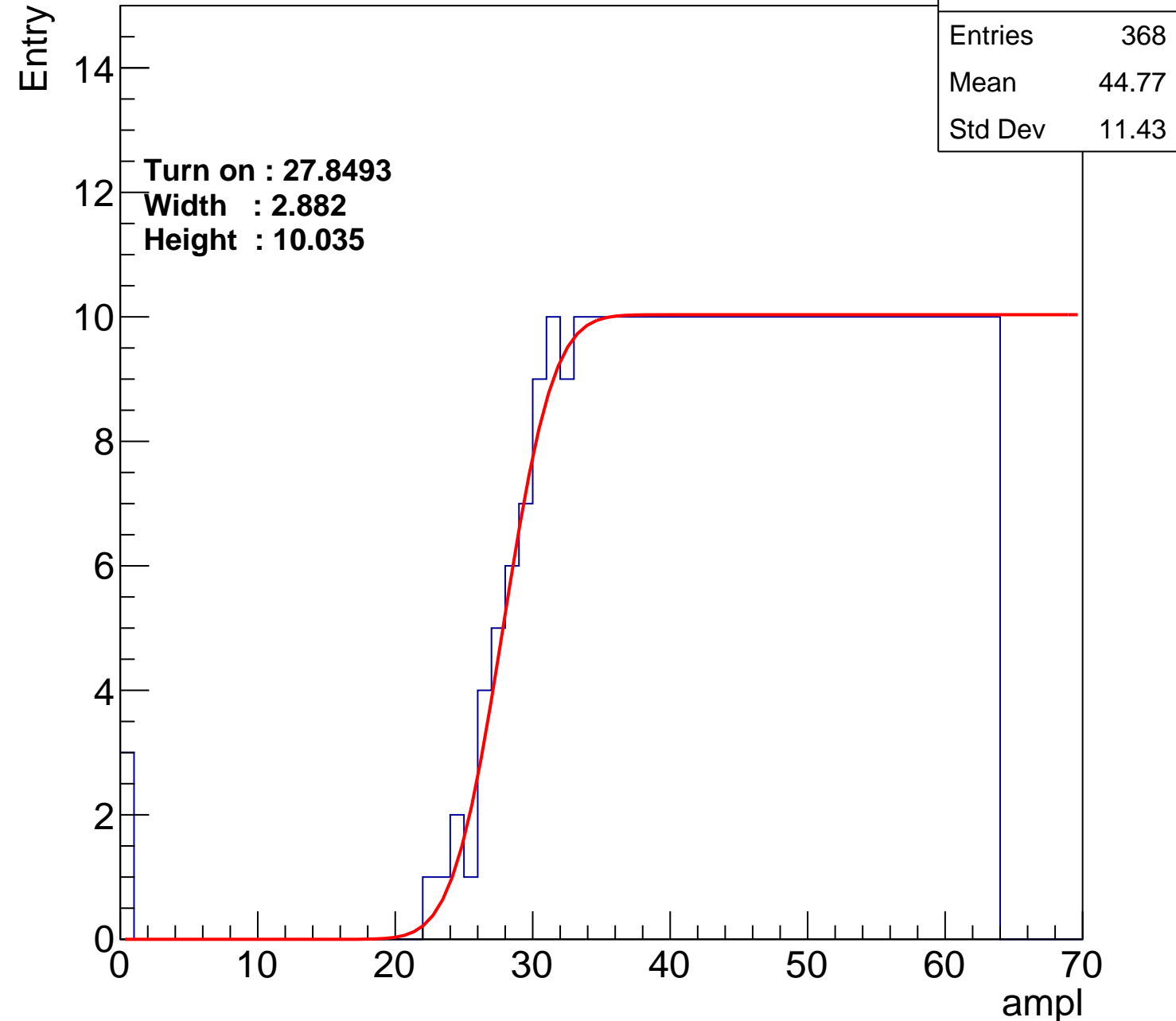
Width : 2.882

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

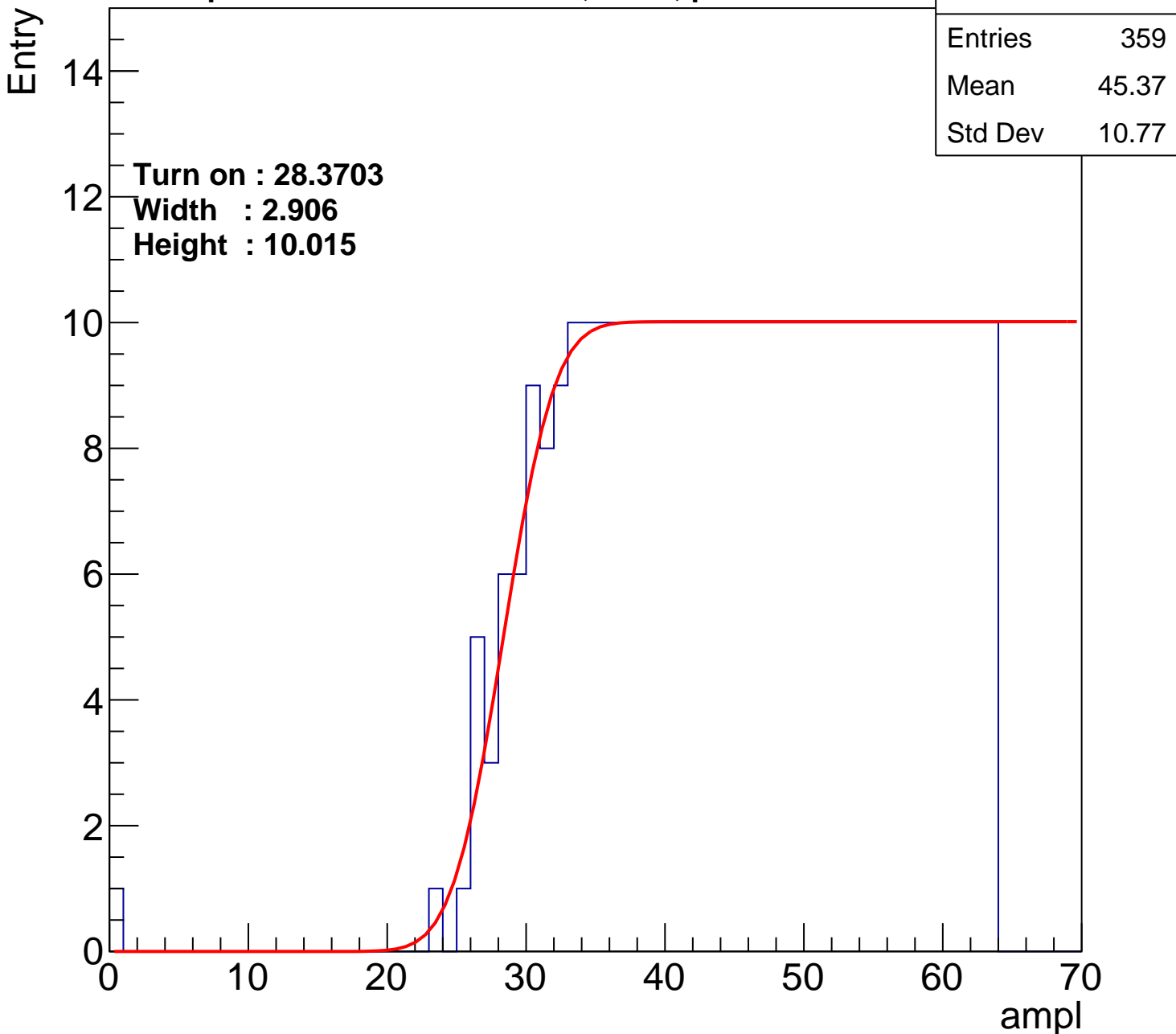
Entries	359
Mean	45.37
Std Dev	10.77

Mean	45.37
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Std Dev	10.77
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Width : 2.906

Height : 10.015



B0L002S, U26-ch15

calib_packv5_042523_0143.root, FC#8, port C1

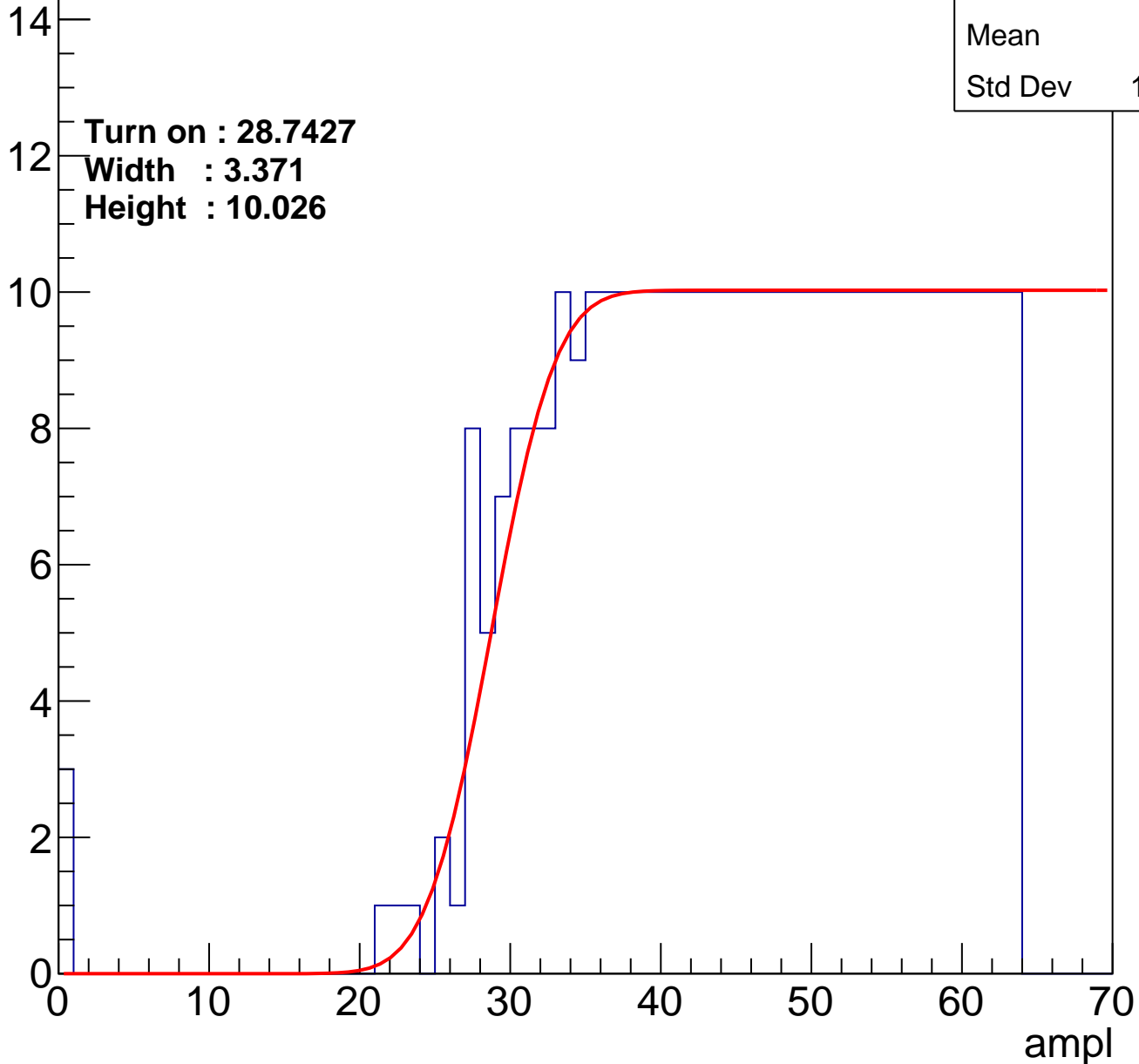
Entries	362
Mean	45
Std Dev	11.38

Turn on : 28.7427

Width : 3.371

Height : 10.026

Entry



B0L002S, U26-ch16

calib_packv5_042523_0143.root, FC#8, port C1

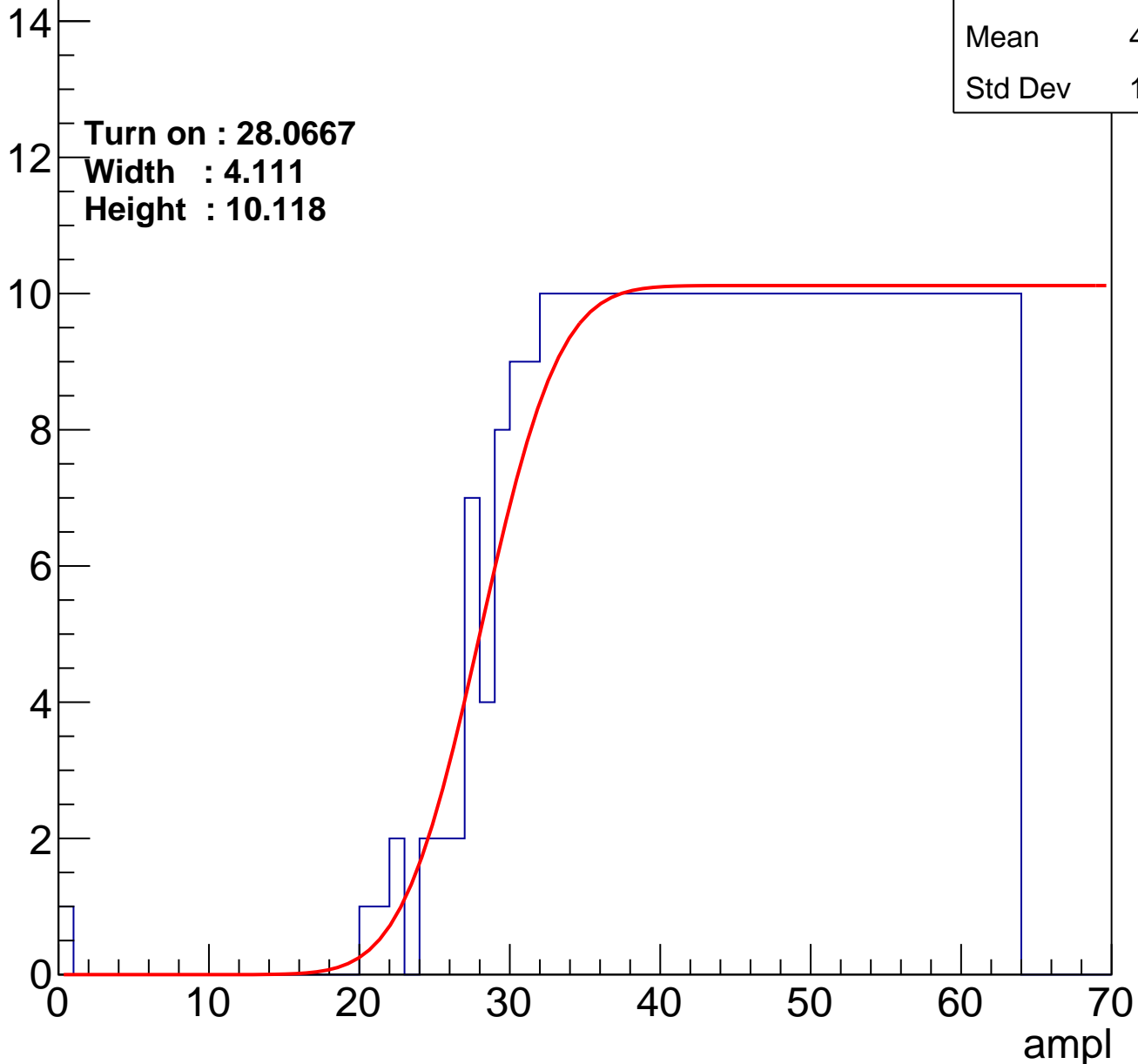
Entries	368
Mean	44.88
Std Dev	11.09

Turn on : 28.0667

Width : 4.111

Height : 10.118

Entry



B0L002S, U26-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 28.2371

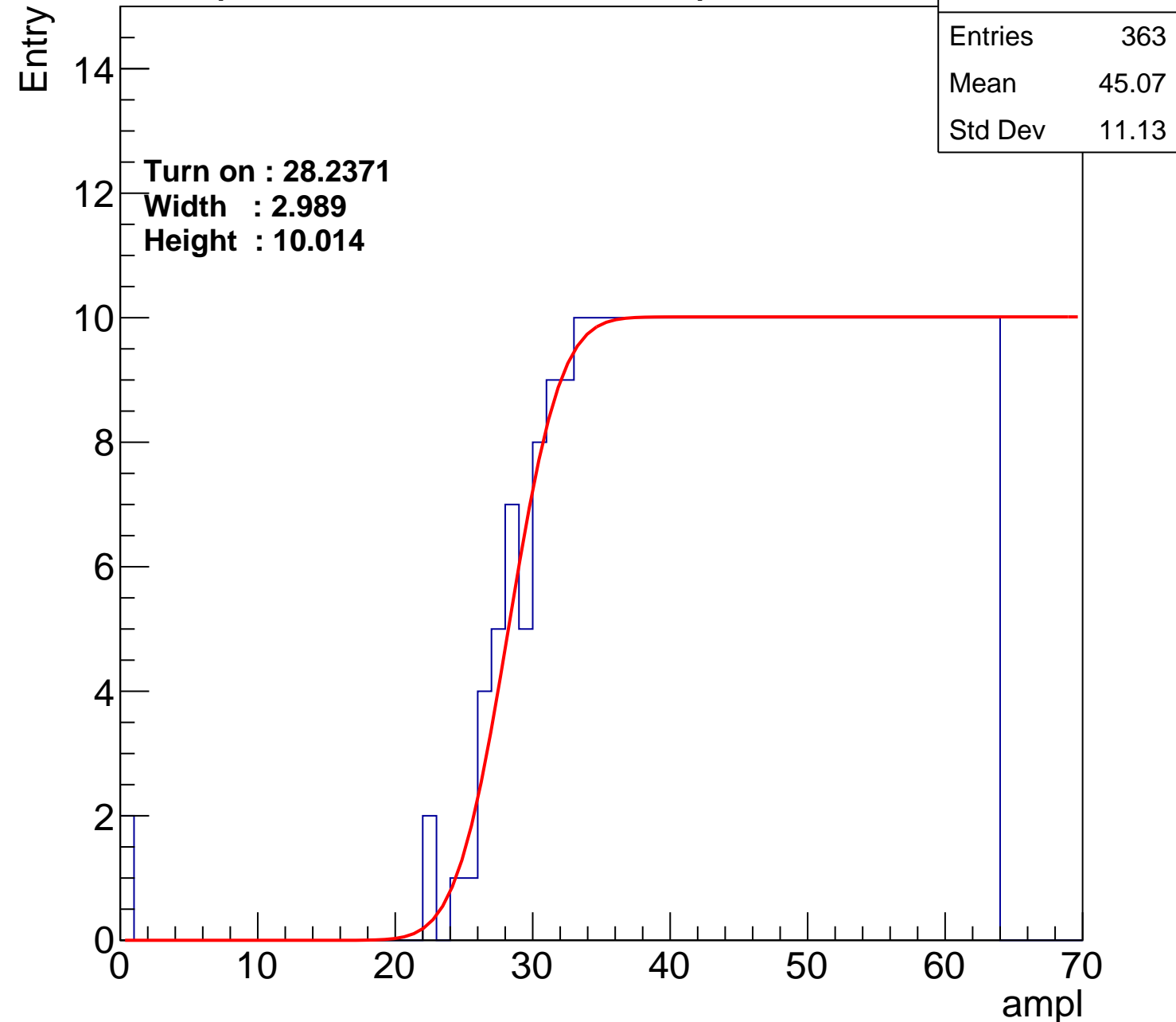
Width : 2.989

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.04
Std Dev	11.32

Turn on : 28.1791

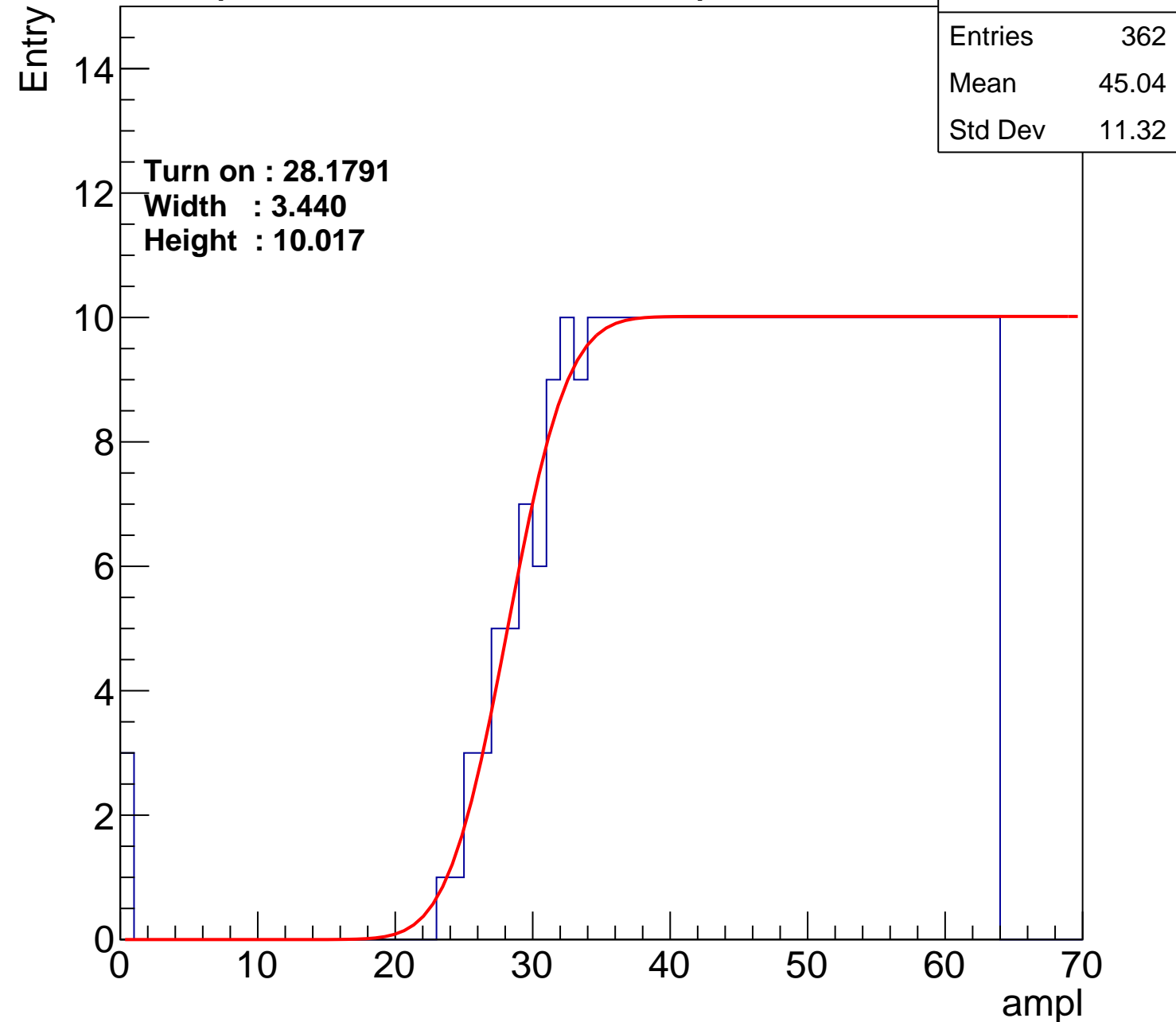
Width : 3.440

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.02
Std Dev	10.96

Turn on : 27.8466

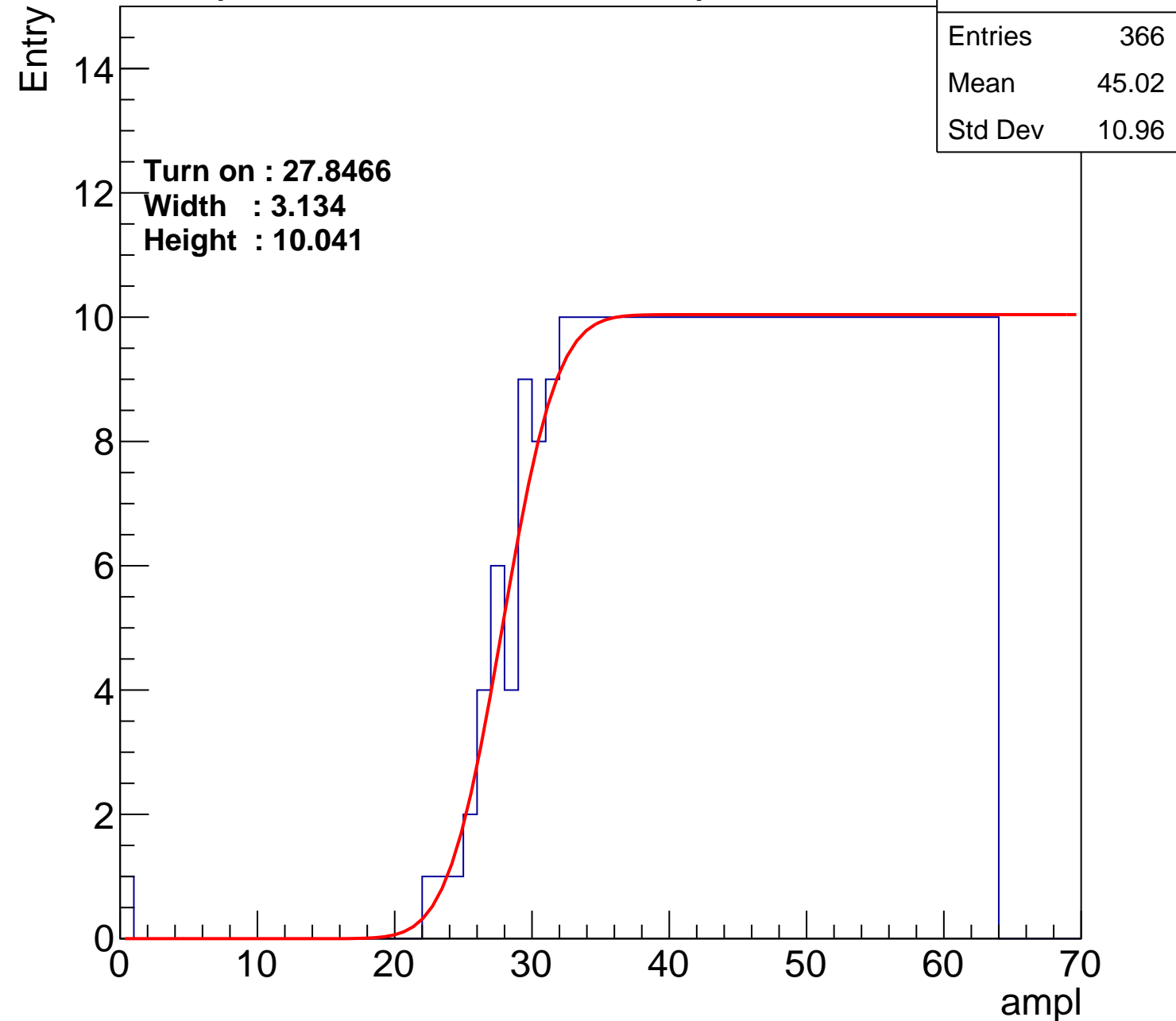
Width : 3.134

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.13
Std Dev	11.57

Turn on : 28.7224

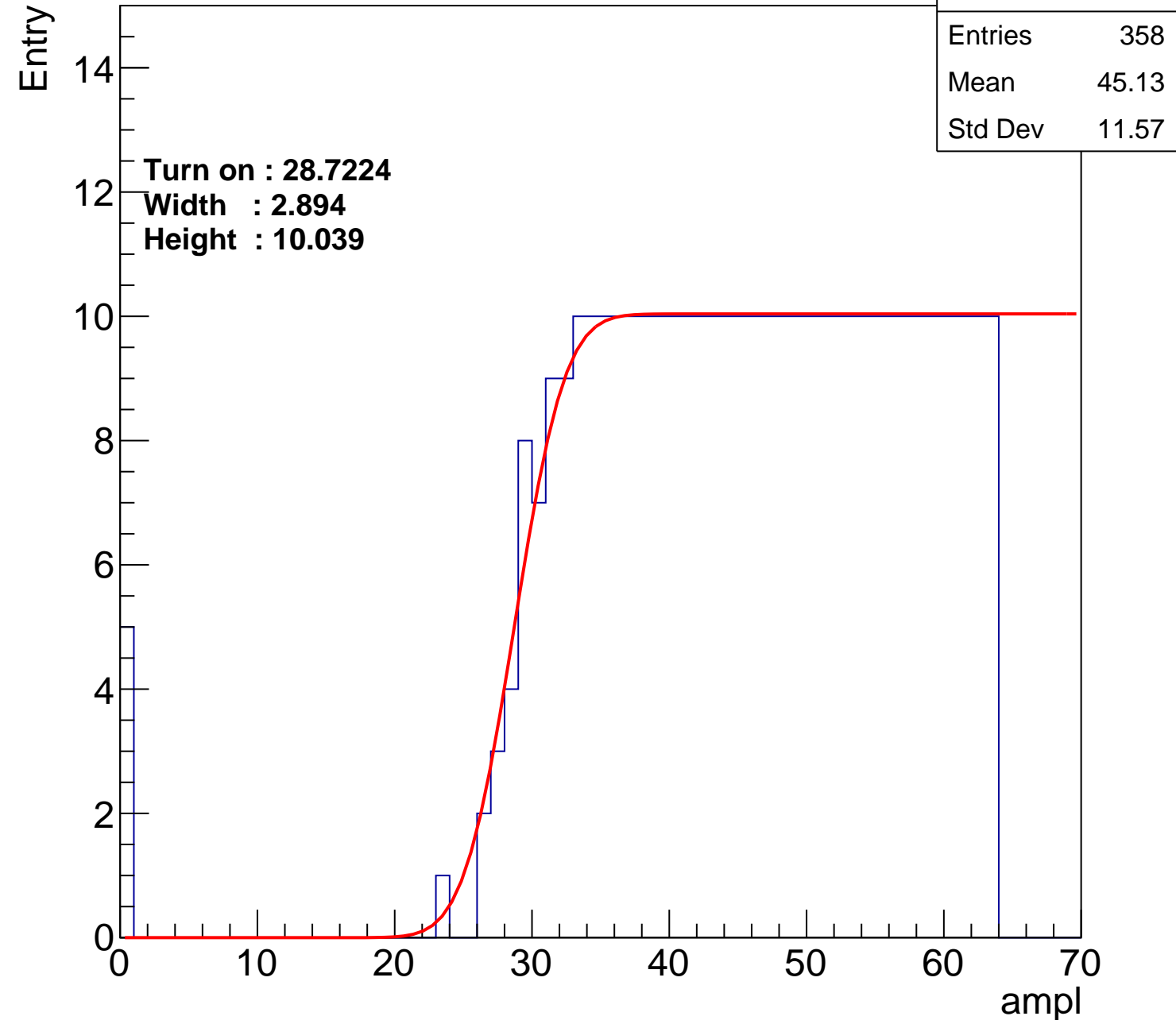
Width : 2.894

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.18
Std Dev	10.86

Turn on : 28.1941

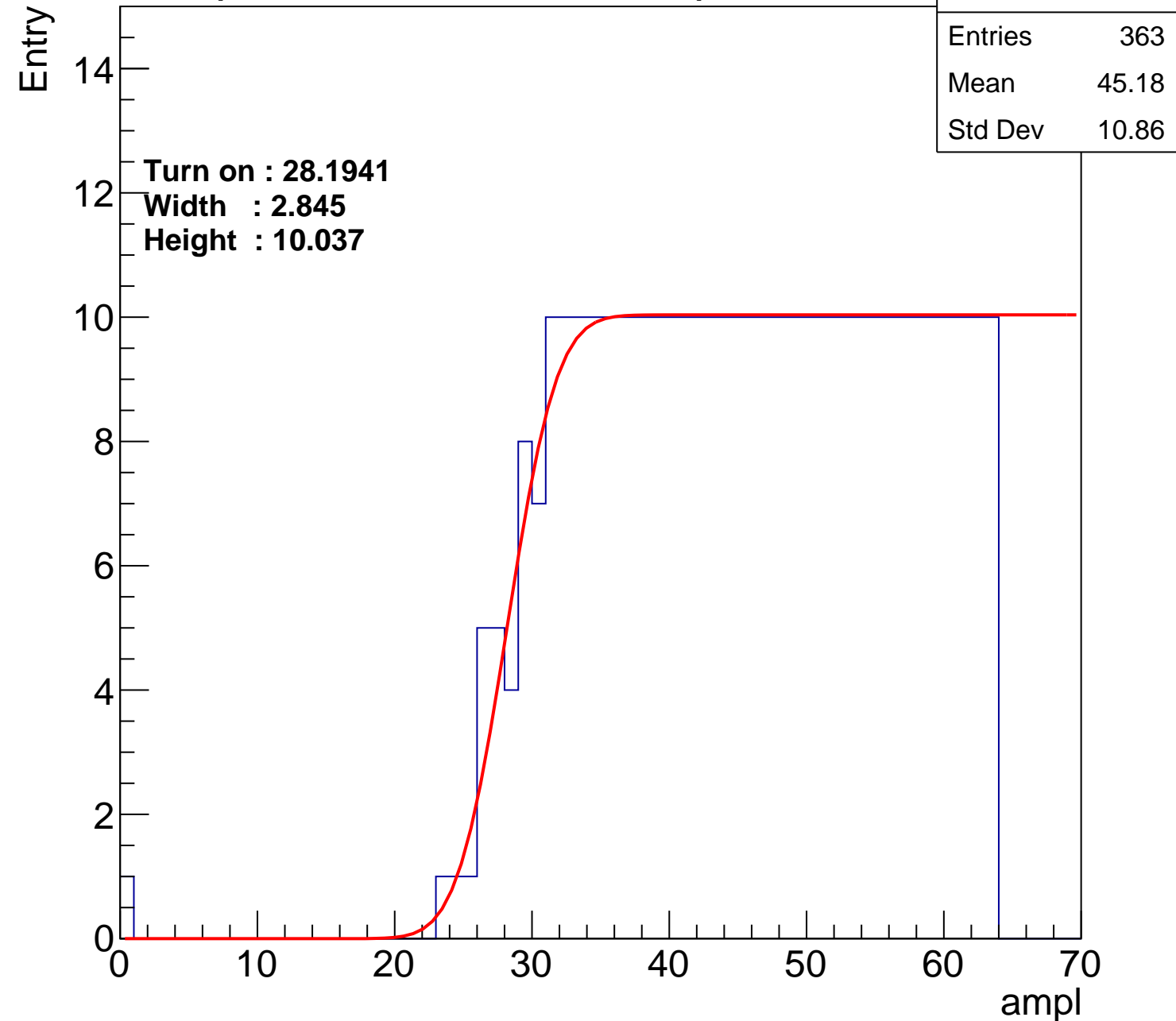
Width : 2.845

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.62
Std Dev	11

Turn on : 29.5052

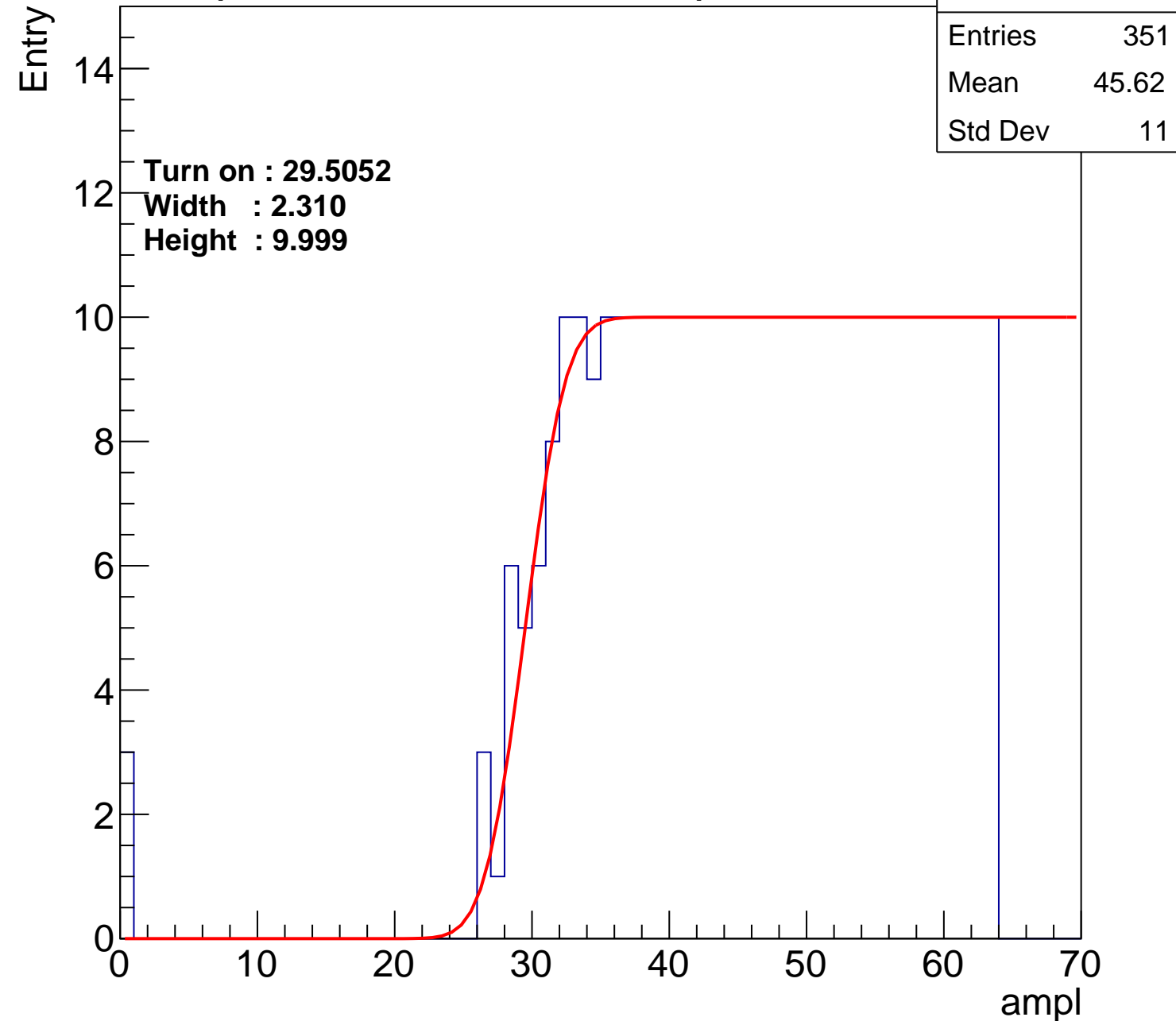
Width : 2.310

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch23

calib_packv5_042523_0143.root, FC#8, port C1

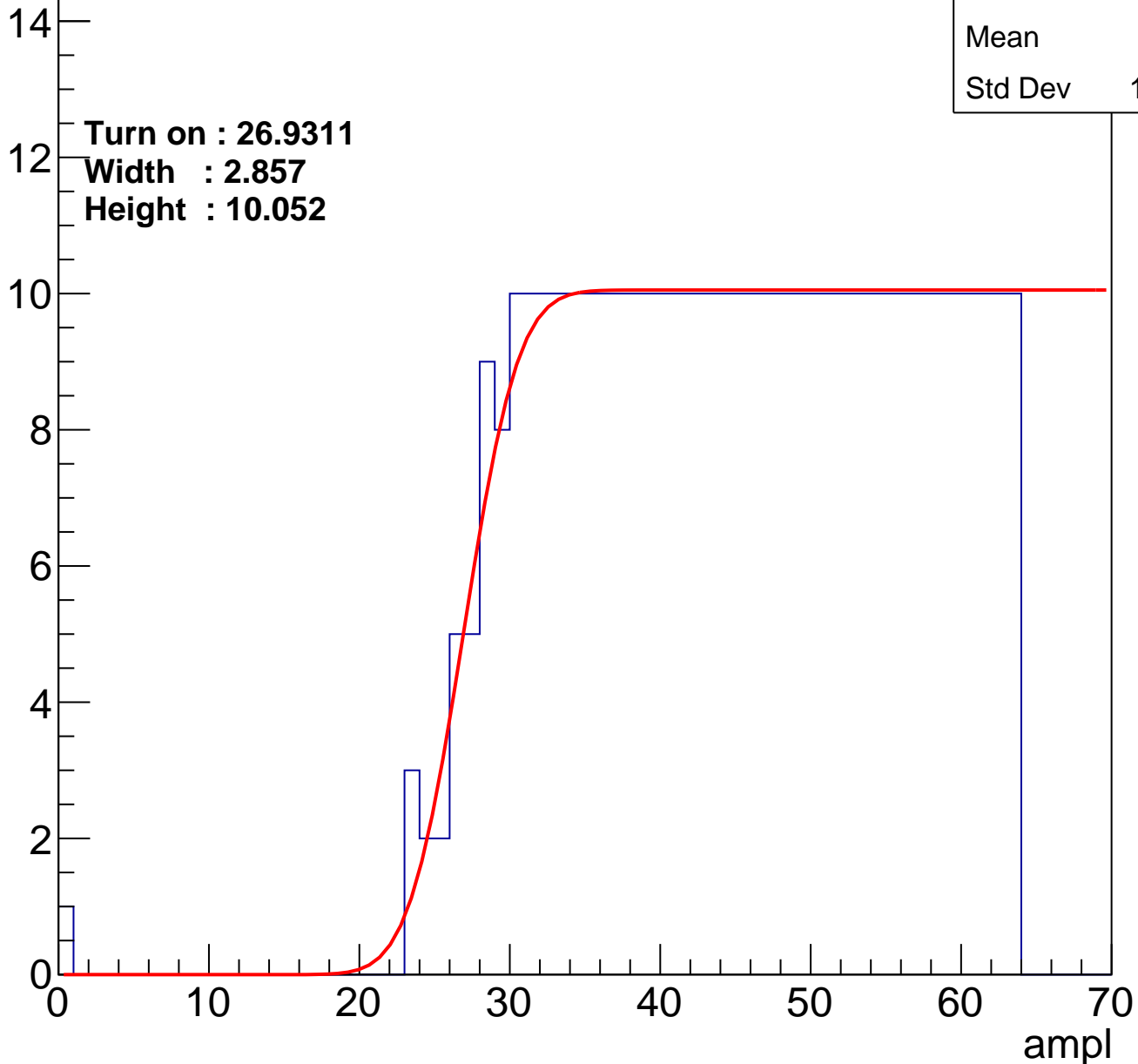
Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.9311

Width : 2.857

Height : 10.052

Entry



B0L002S, U26-ch24

calib_packv5_042523_0143.root, FC#8, port C1

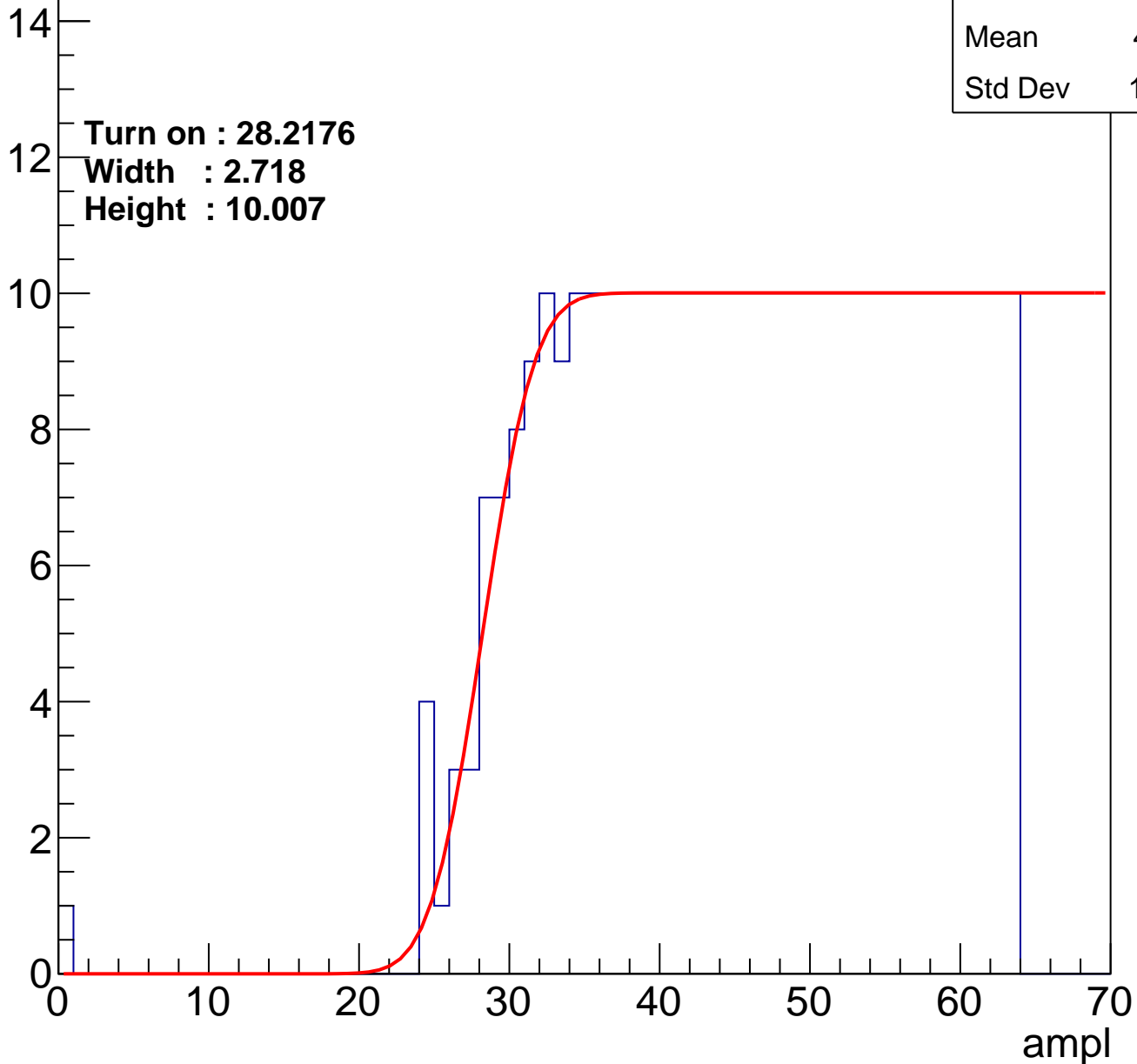
Entries	362
Mean	45.21
Std Dev	10.87

Turn on : 28.2176

Width : 2.718

Height : 10.007

Entry



B0L002S, U26-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.91
Std Dev	11.56

Turn on : 28.5454

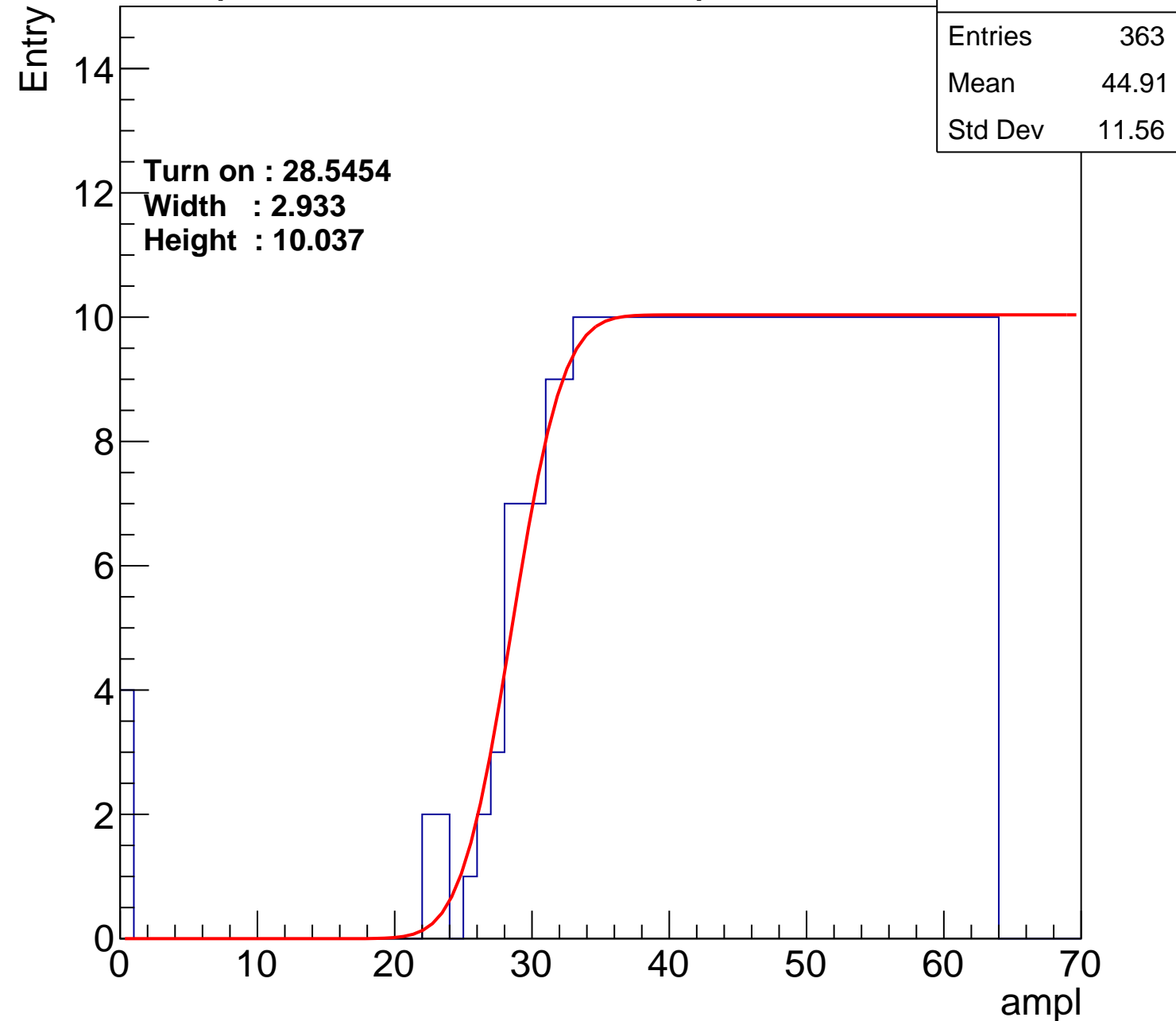
Width : 2.933

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.64
Std Dev	11.35

Turn on : 27.4851

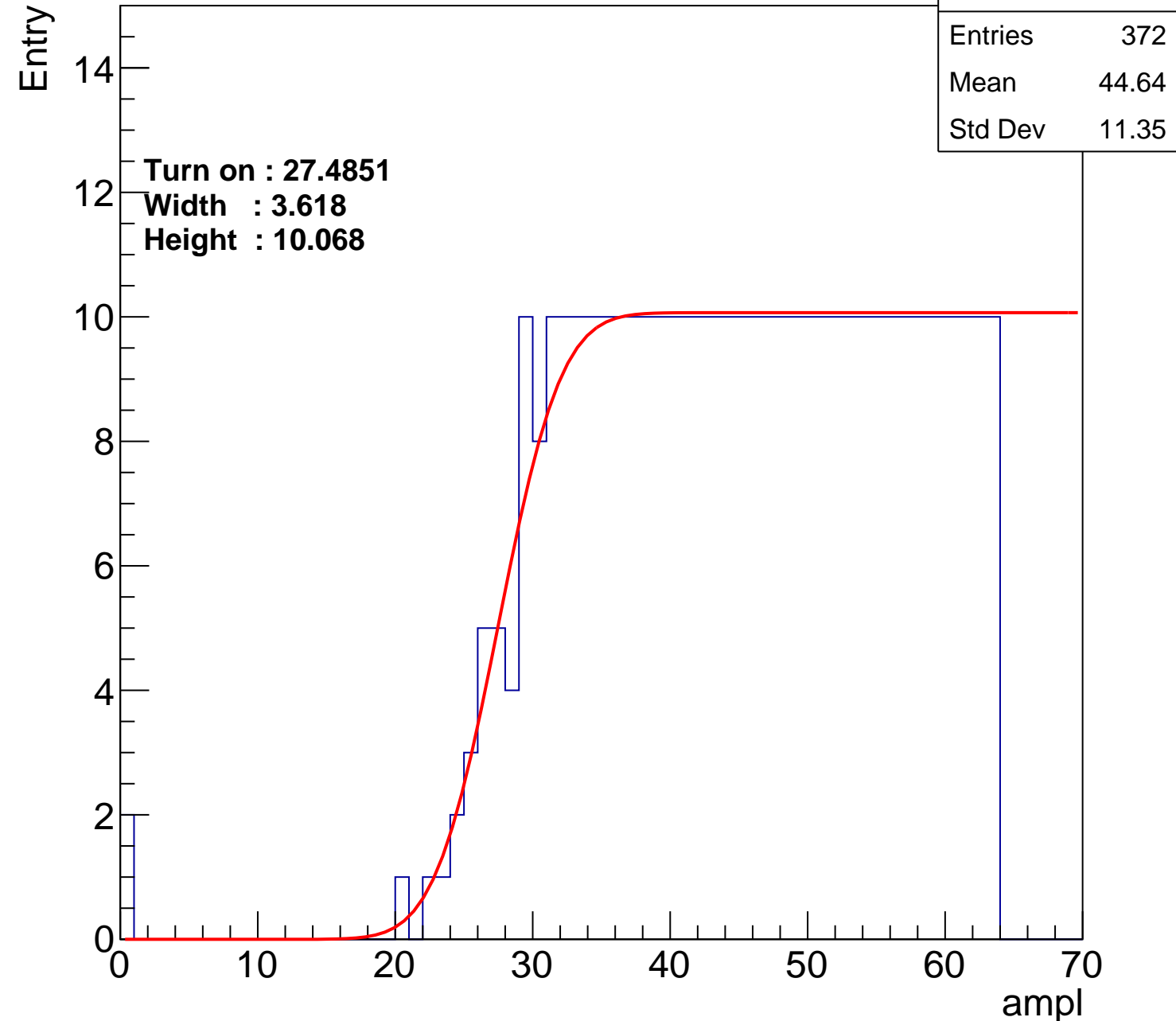
Width : 3.618

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	339
Mean	46.14
Std Dev	10.82

Turn on : 30.6290

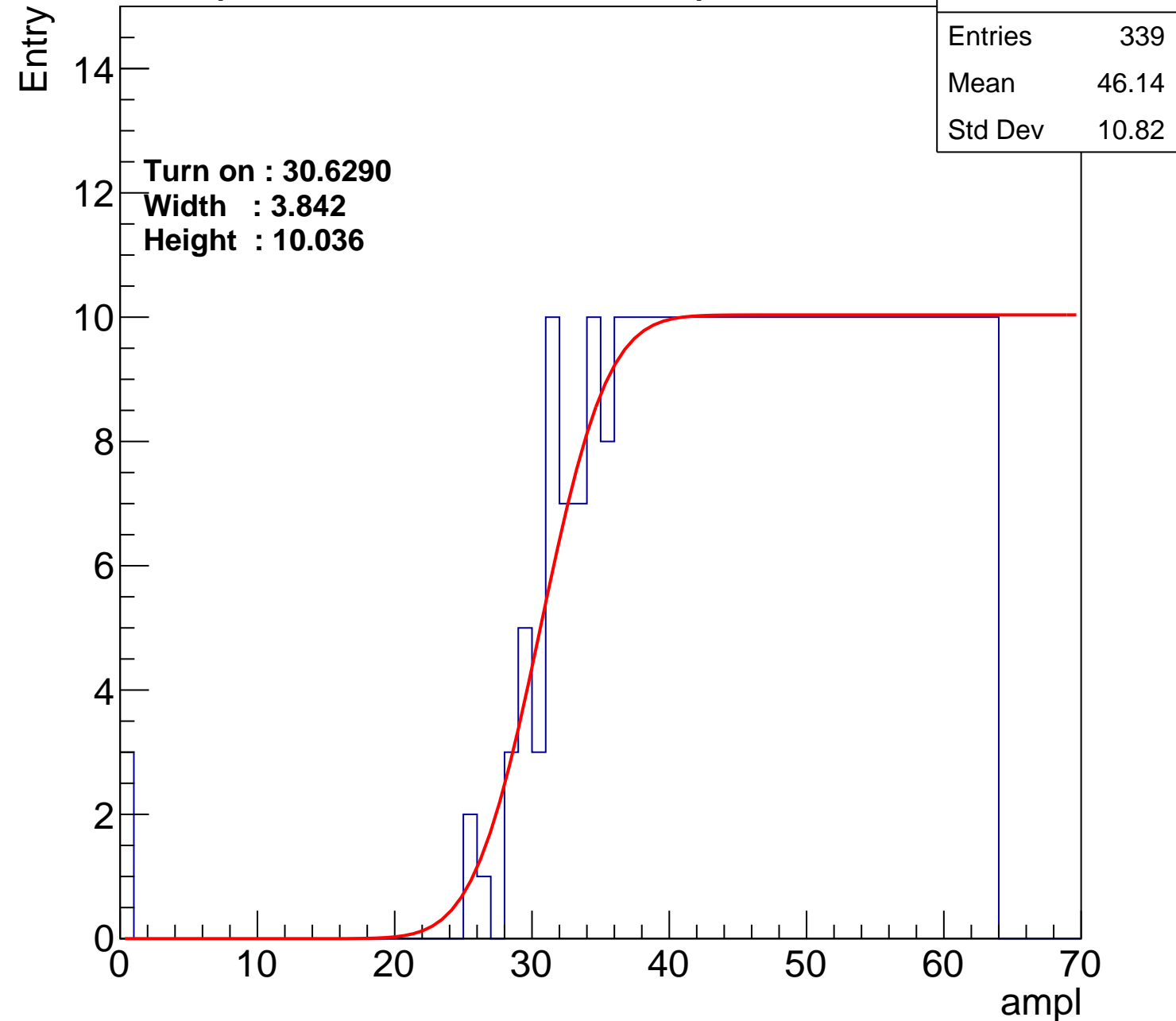
Width : 3.842

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch28

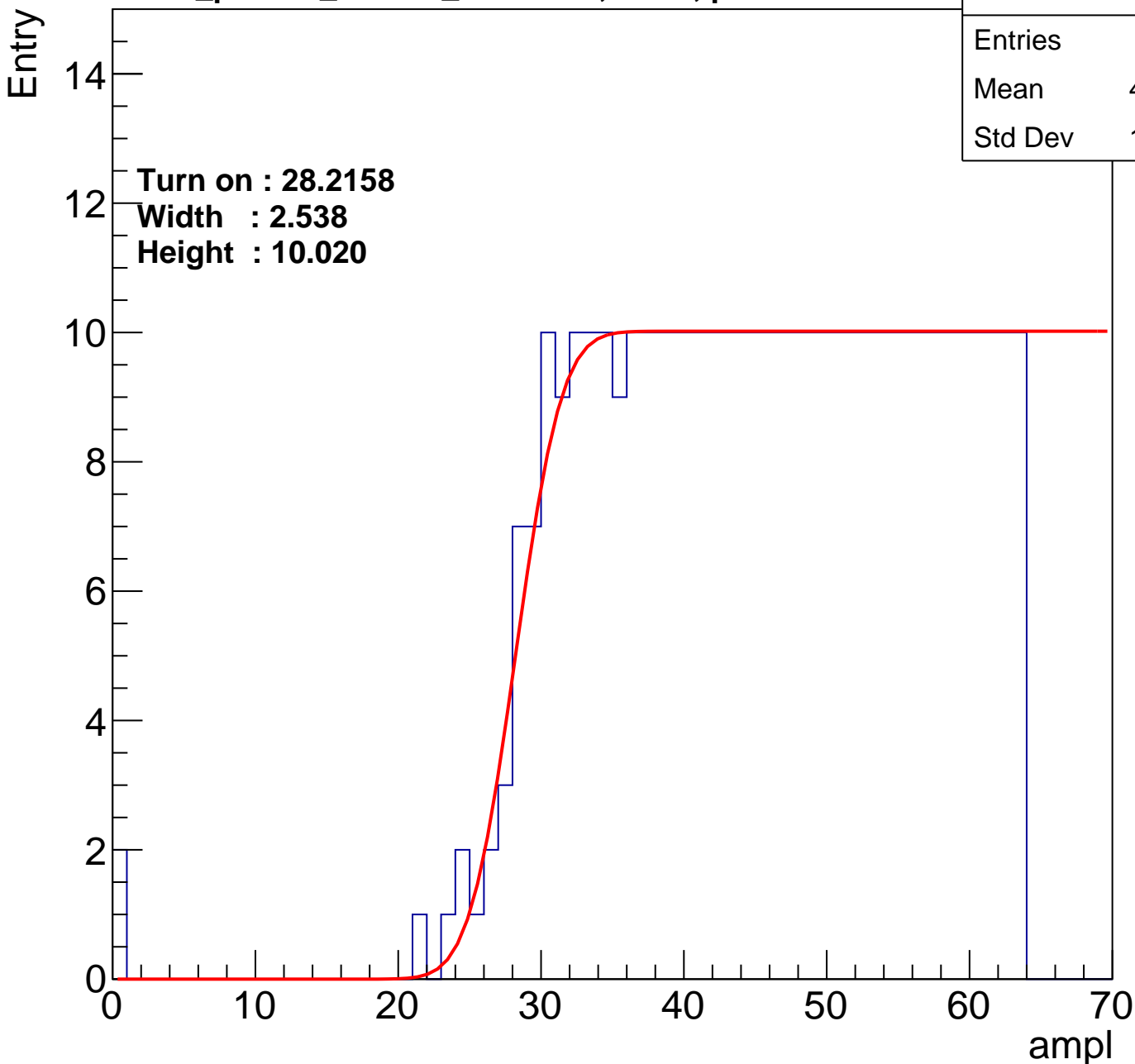
calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.04
Std Dev	11.13

Turn on : 28.2158

Width : 2.538

Height : 10.020



B0L002S, U26-ch29

calib_packv5_042523_0143.root, FC#8, port C1

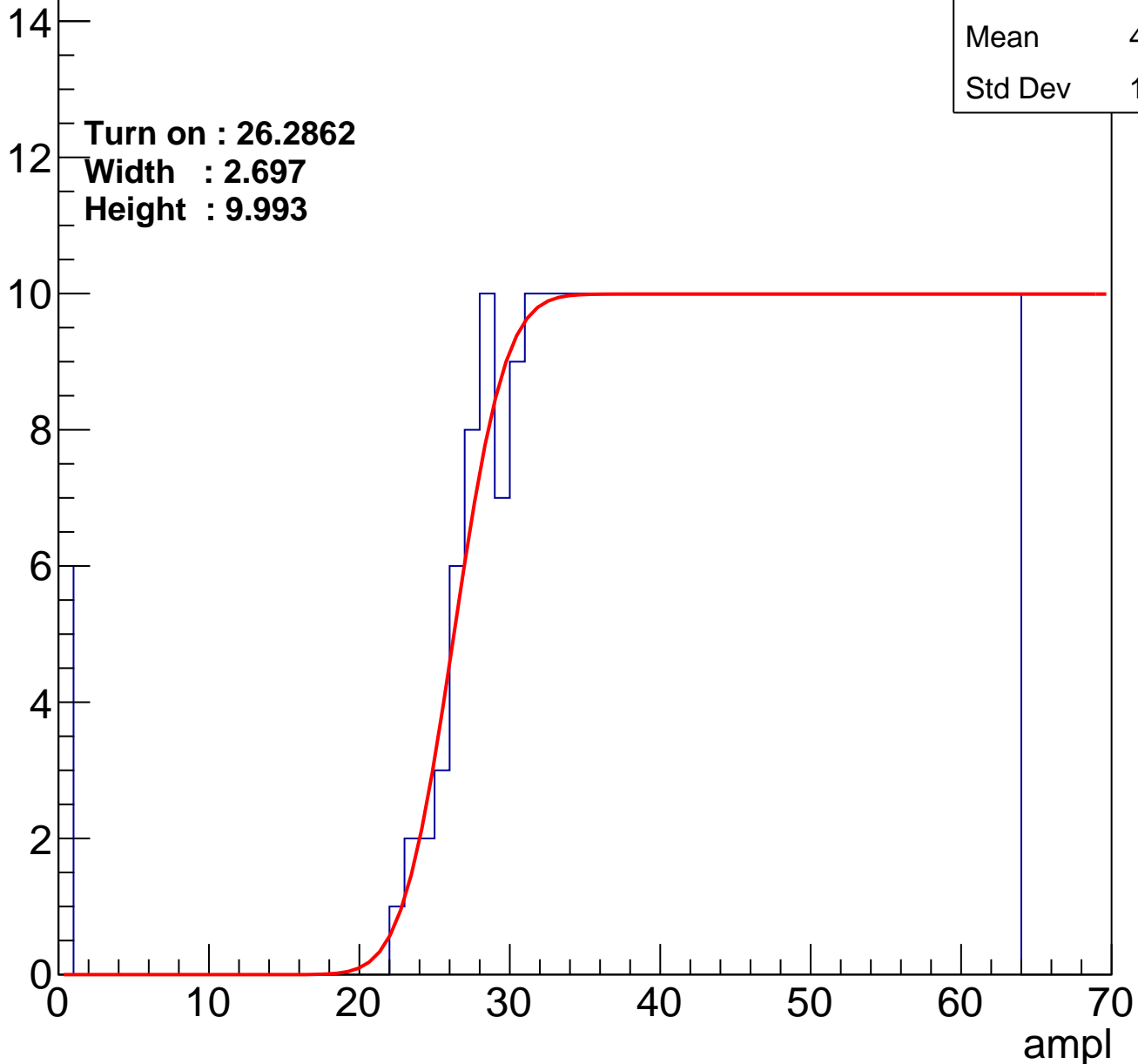
Entries	384
Mean	43.82
Std Dev	12.28

Turn on : 26.2862

Width : 2.697

Height : 9.993

Entry



B0L002S, U26-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.24
Std Dev	10.8

Turn on : 27.7656

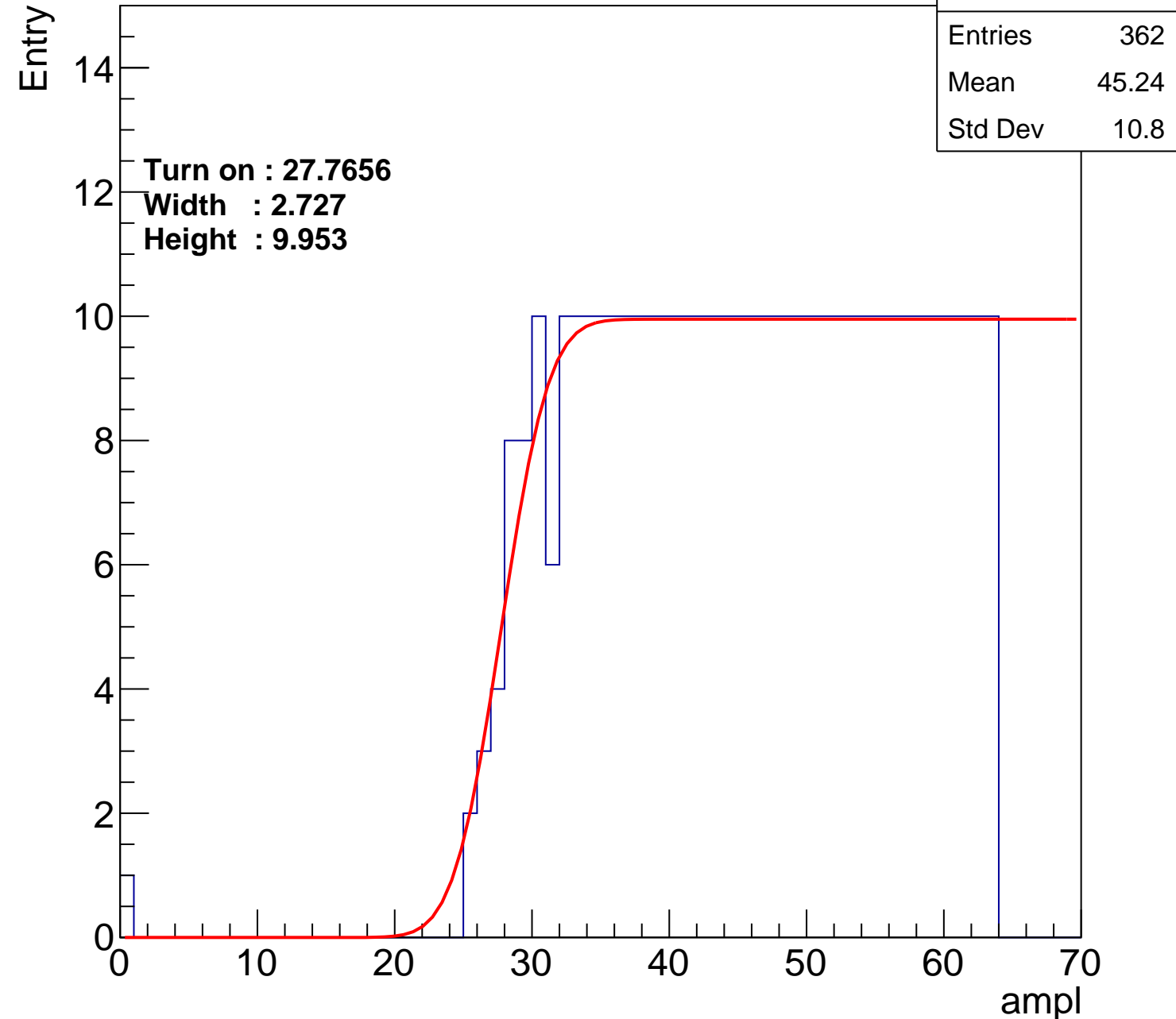
Width : 2.727

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch31

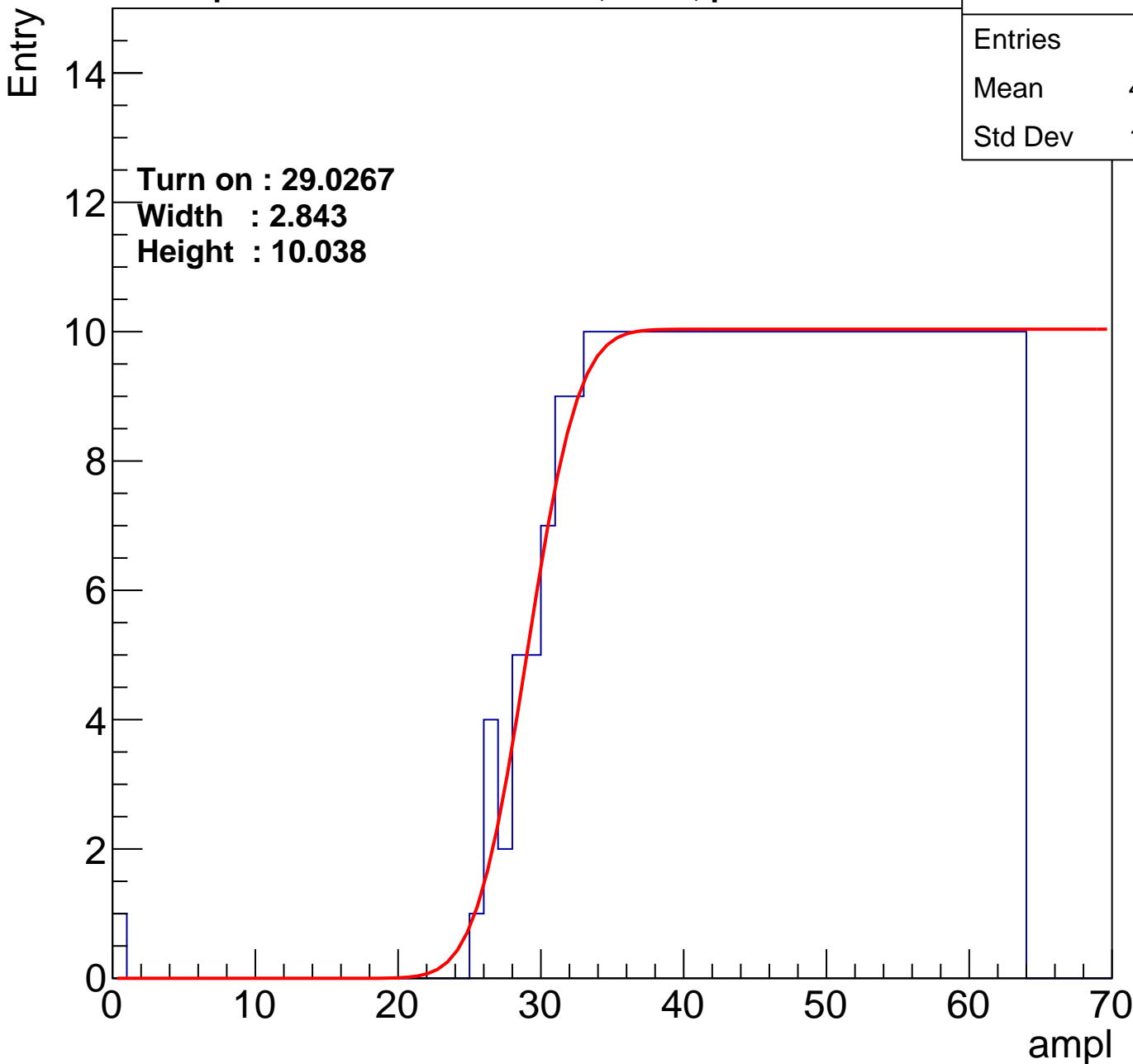
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 29.0267

Width : 2.843

Height : 10.038

Entries	353
Mean	45.68
Std Dev	10.58



B0L002S, U26-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 27.0682

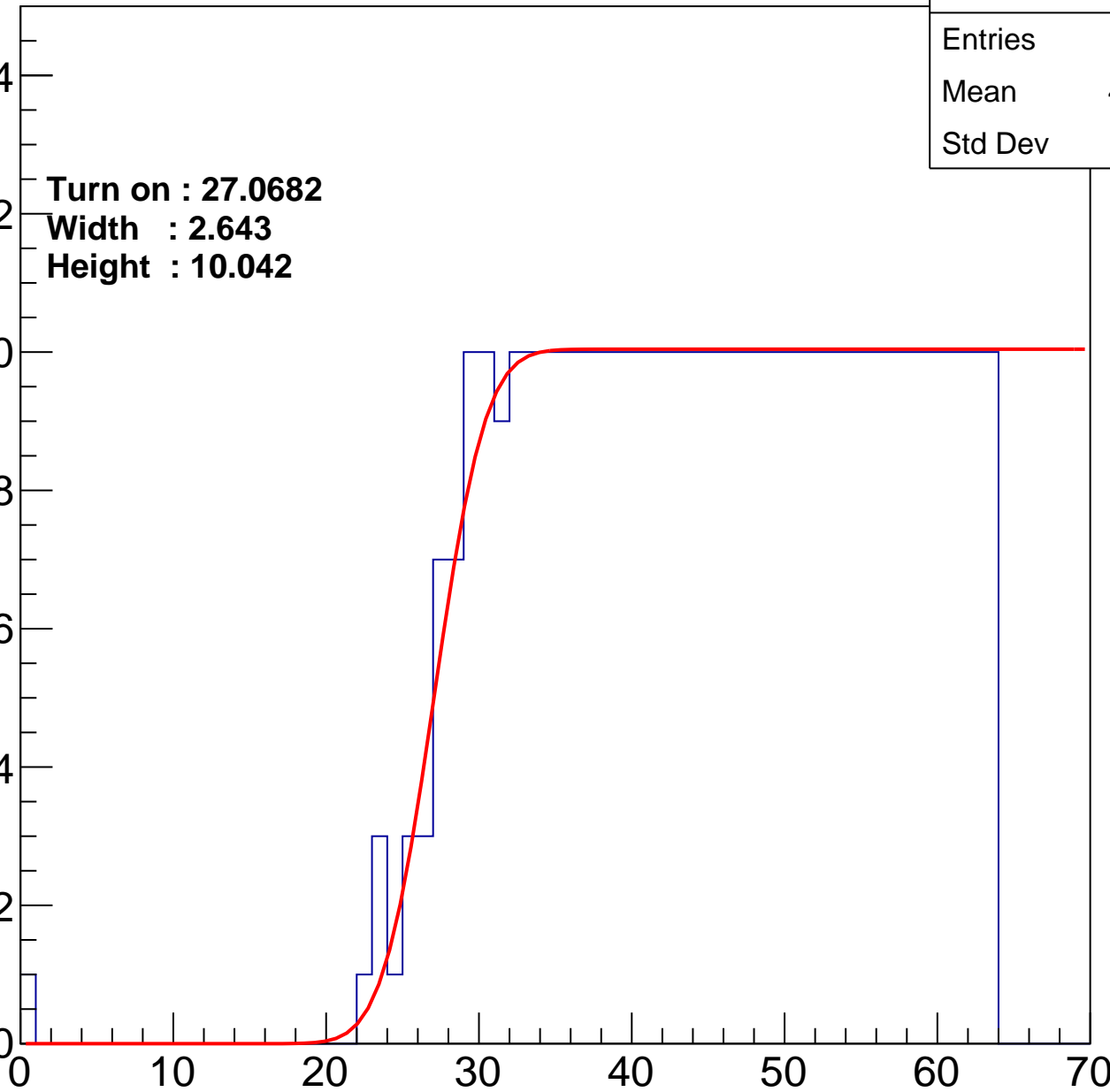
Width : 2.643

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch33

calib_packv5_042523_0143.root, FC#8, port C1

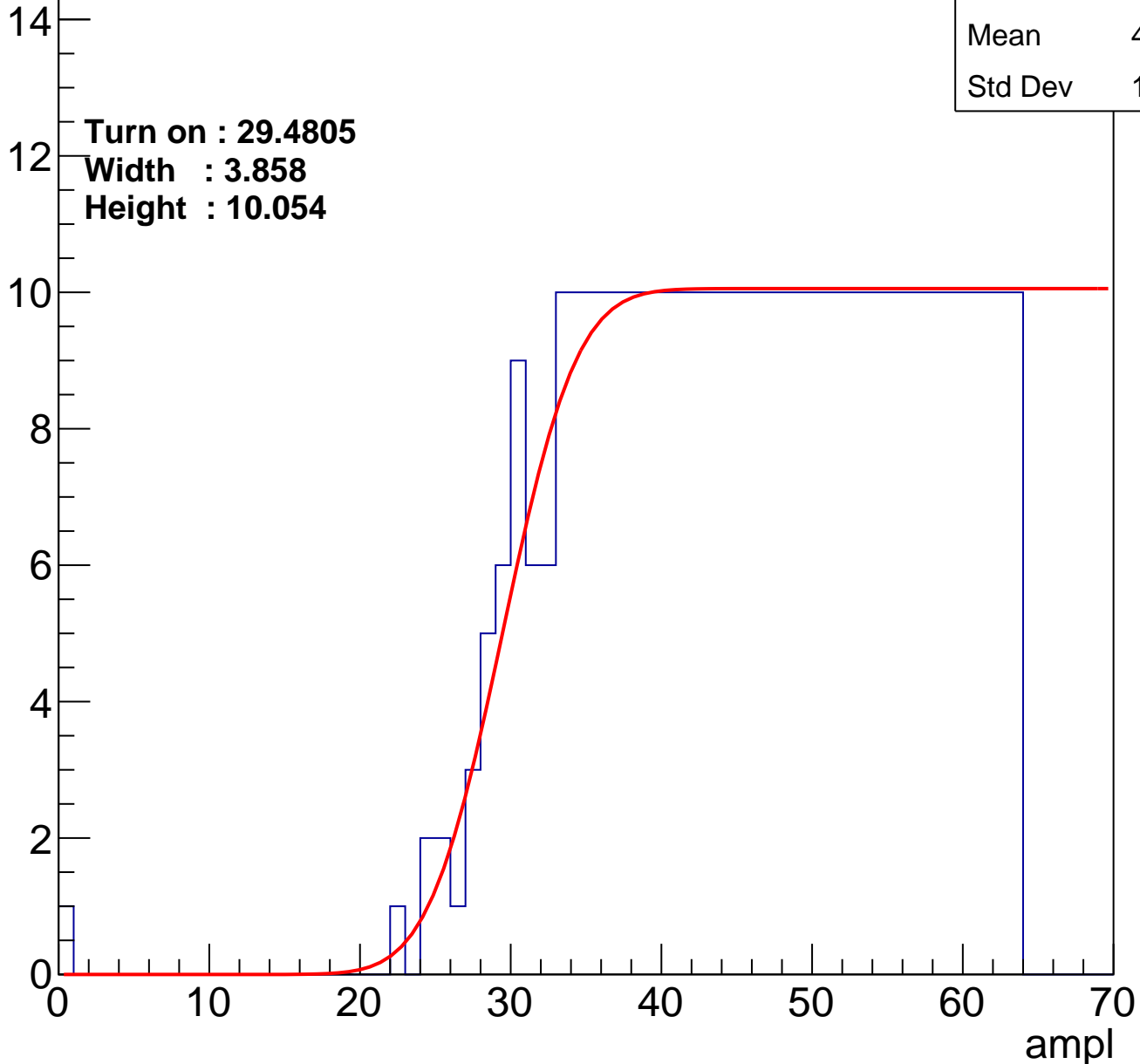
Entries	352
Mean	45.65
Std Dev	10.69

Turn on : 29.4805

Width : 3.858

Height : 10.054

Entry



B0L002S, U26-ch34

calib_packv5_042523_0143.root, FC#8, port C1

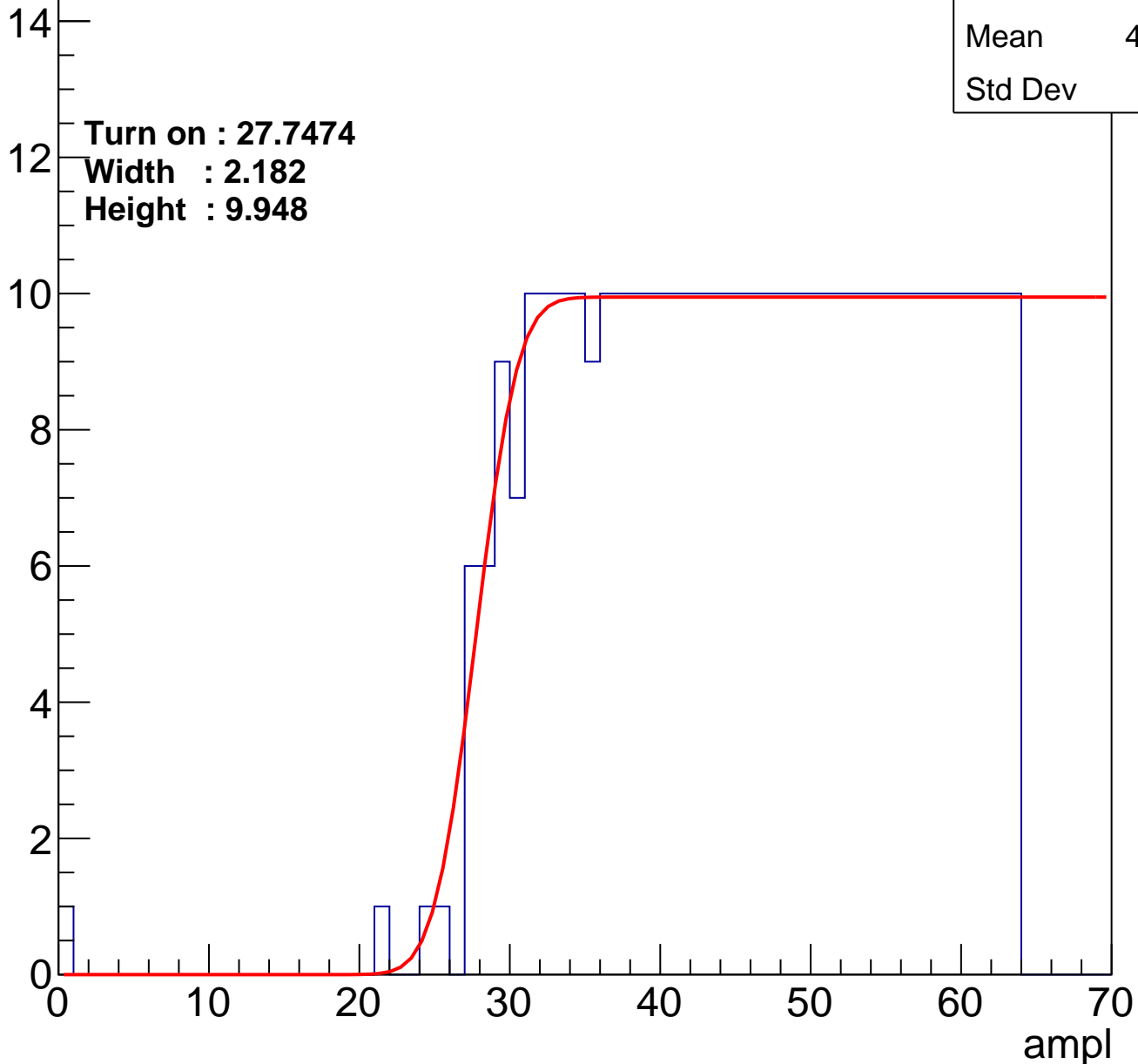
Entries	361
Mean	45.28
Std Dev	10.8

Turn on : 27.7474

Width : 2.182

Height : 9.948

Entry



B0L002S, U26-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.34
Std Dev	10.96

Turn on : 28.4358

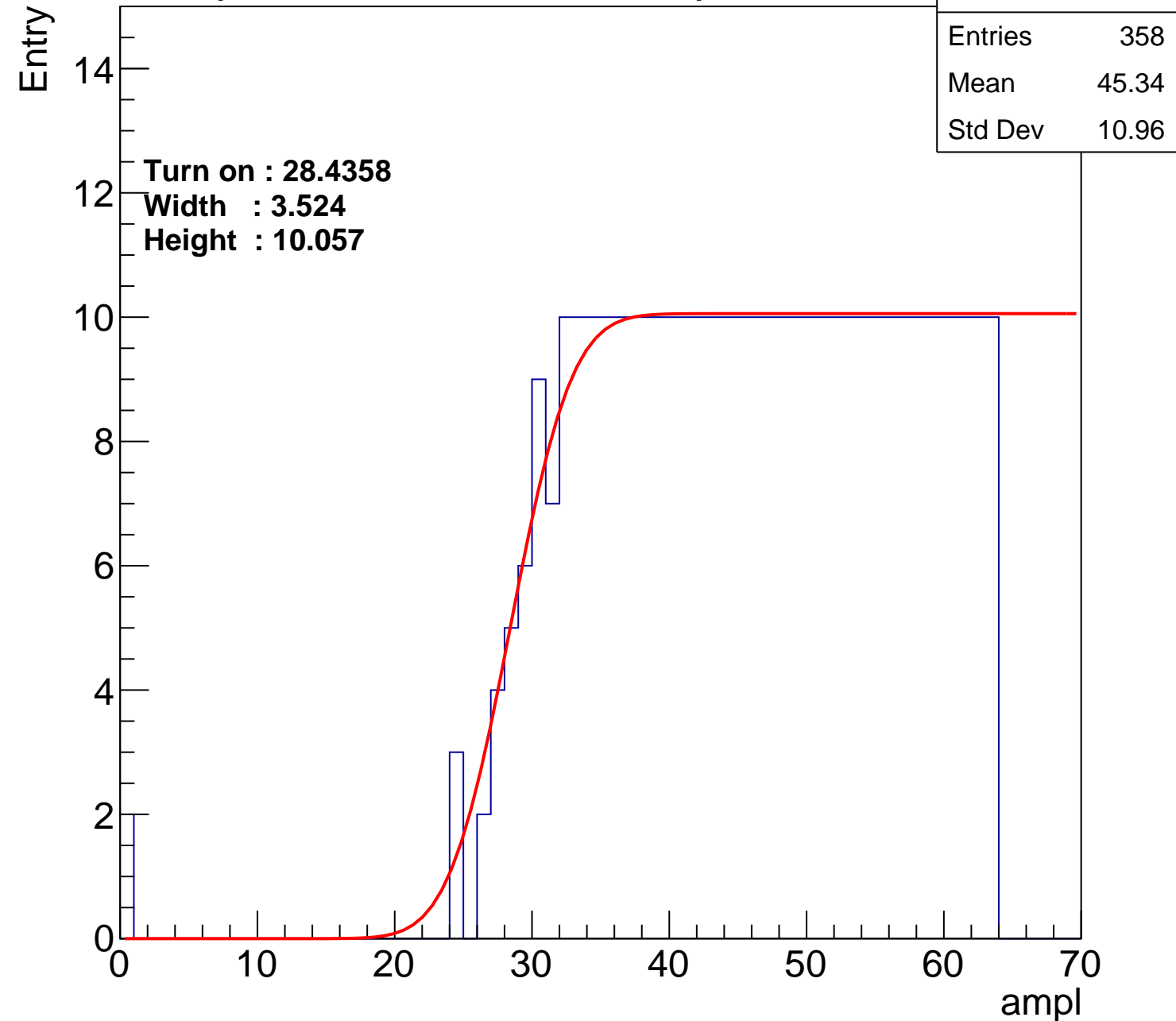
Width : 3.524

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.46
Std Dev	10.72

Turn on : 28.8813

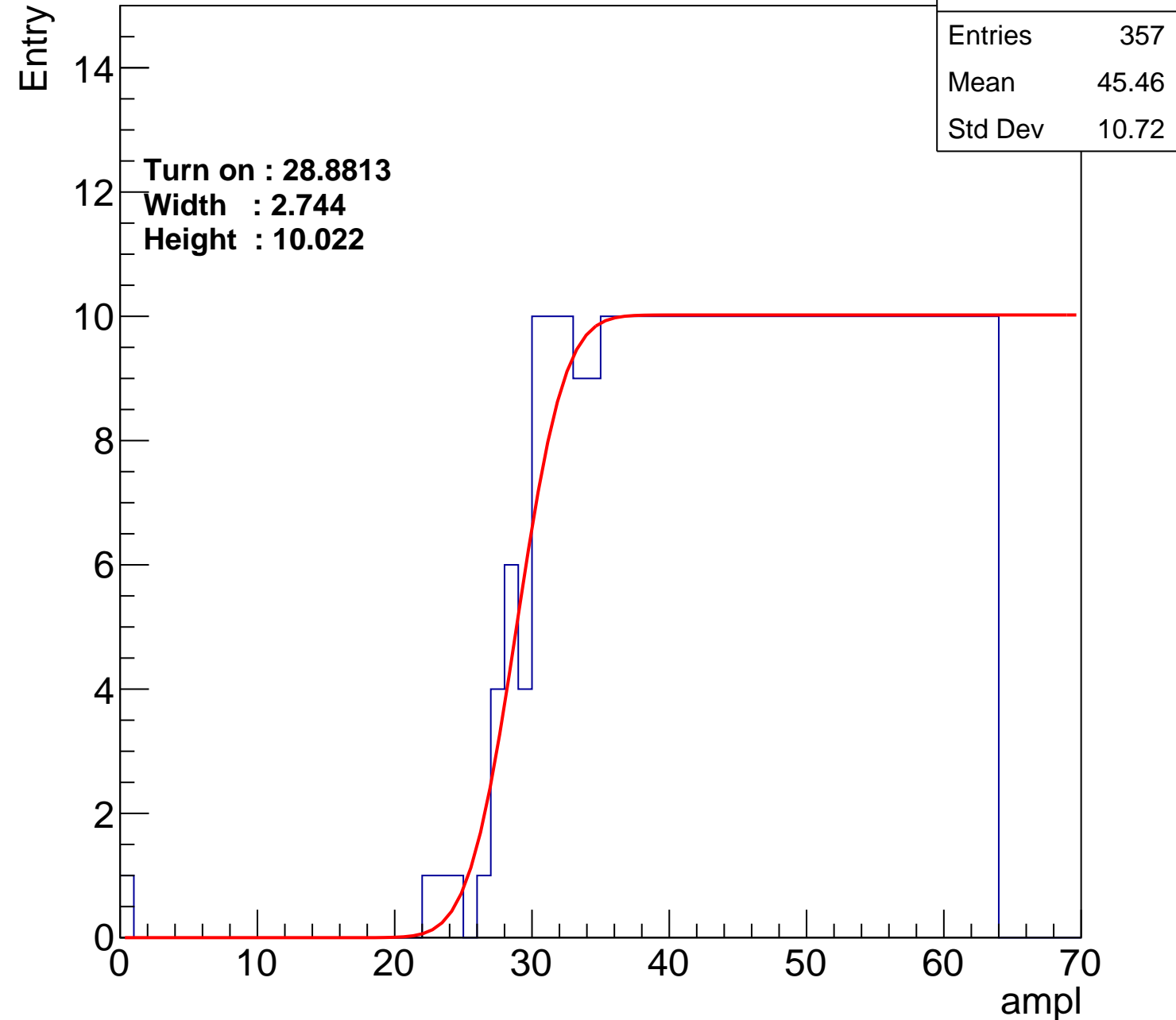
Width : 2.744

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.46
Std Dev	11.77

Turn on : 27.4759

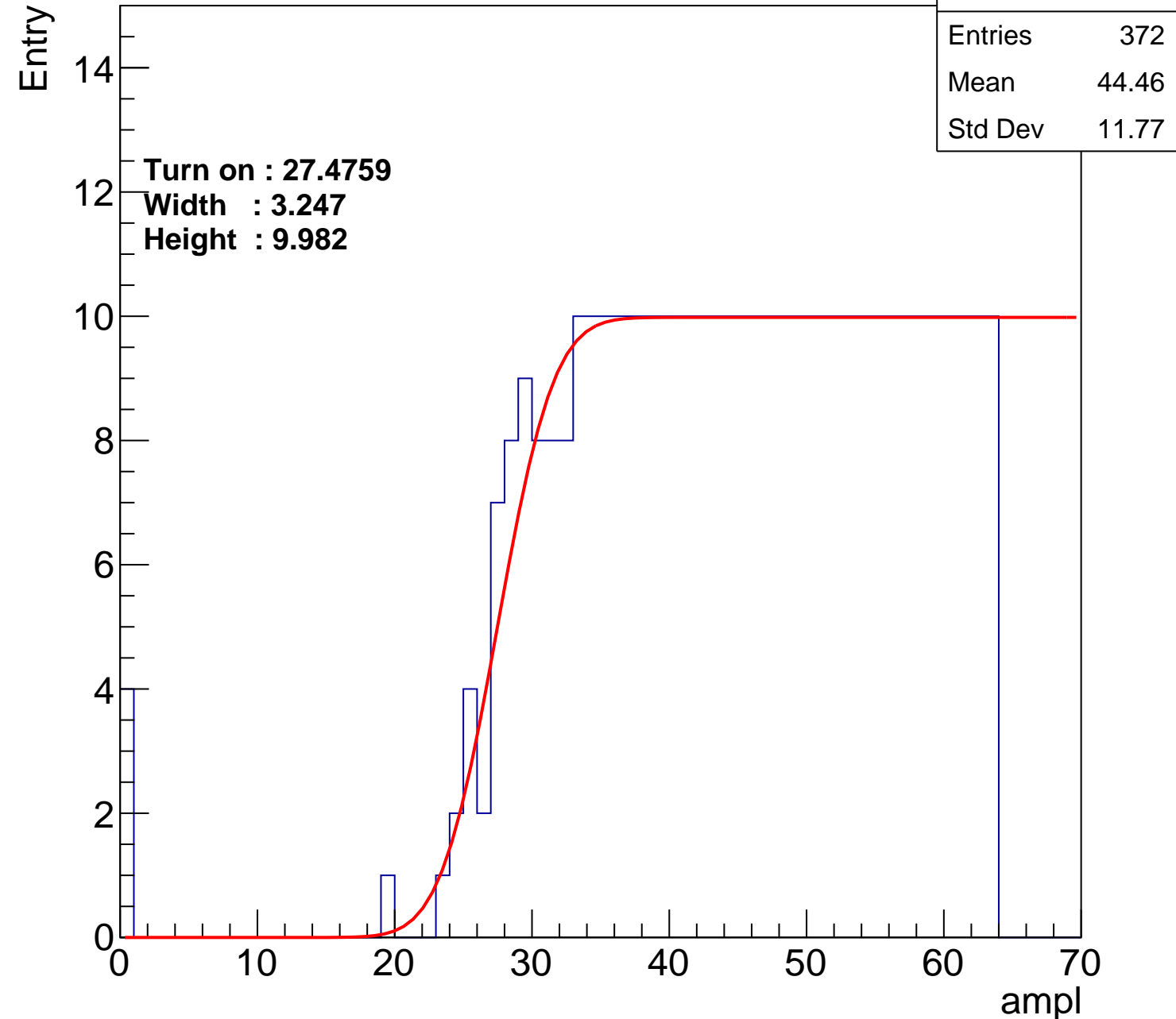
Width : 3.247

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.34
Std Dev	11.89

Turn on : 27.0385

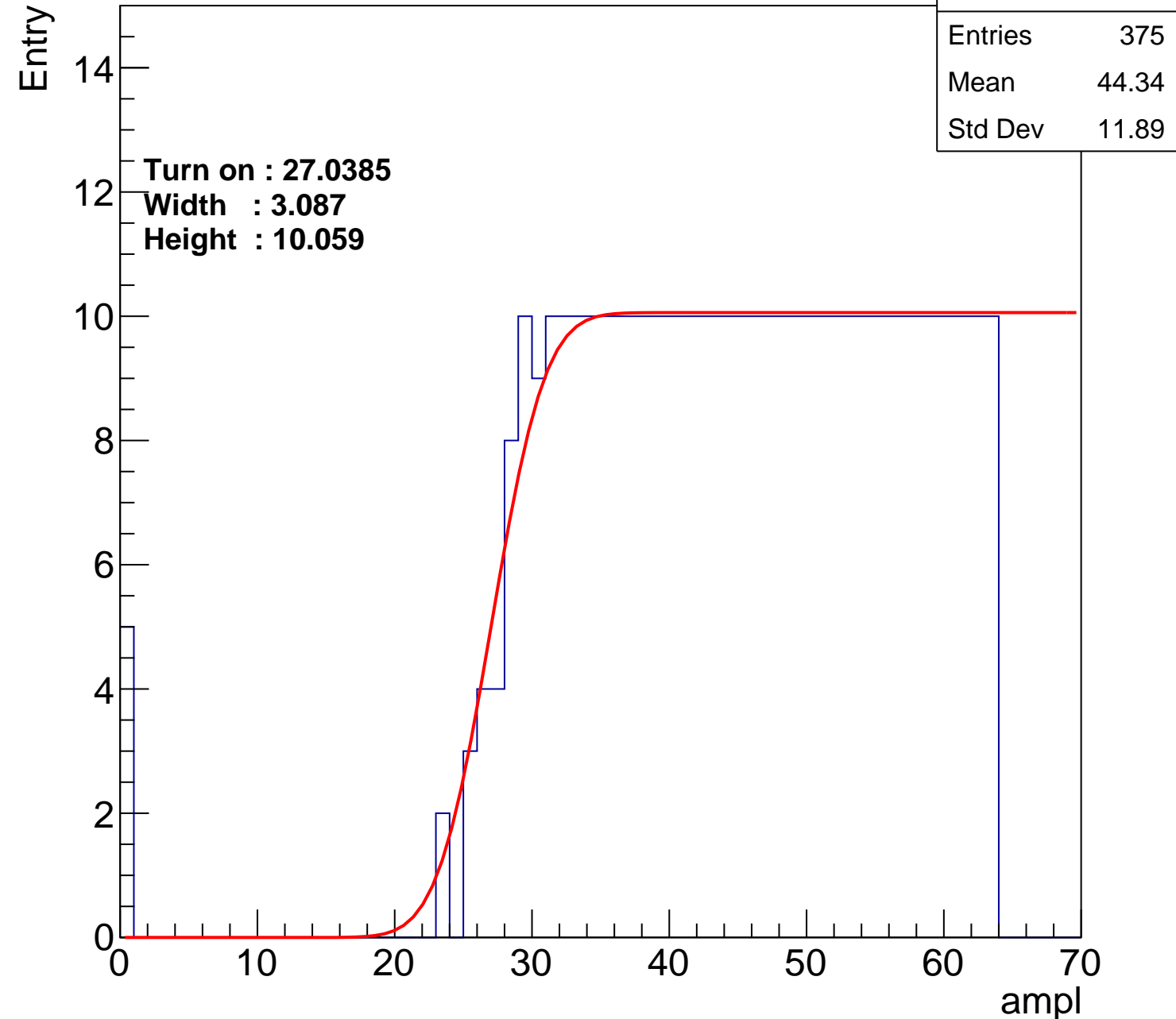
Width : 3.087

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.19
Std Dev	11.04

Turn on : 28.2135

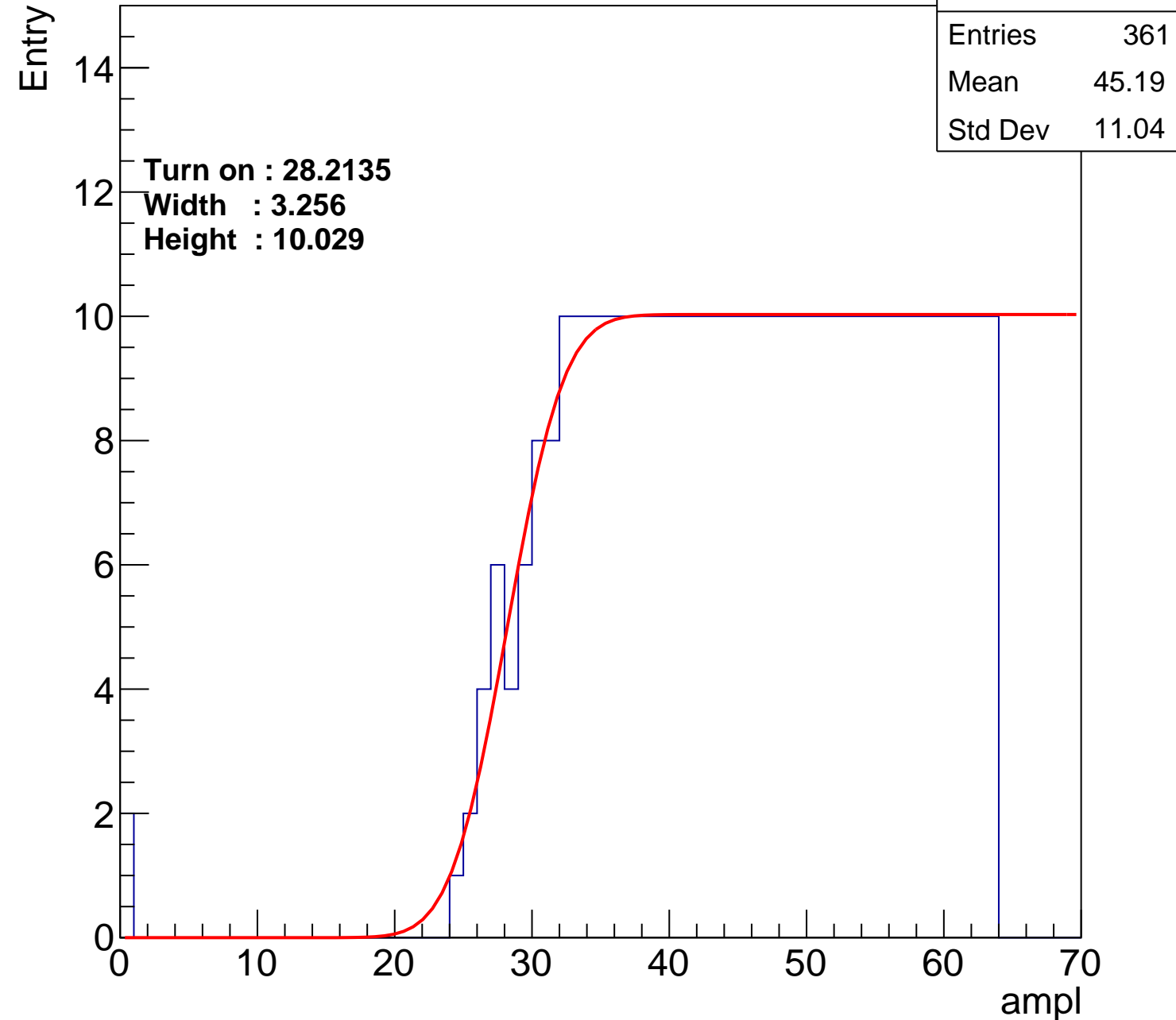
Width : 3.256

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch40

calib_packv5_042523_0143.root, FC#8, port C1

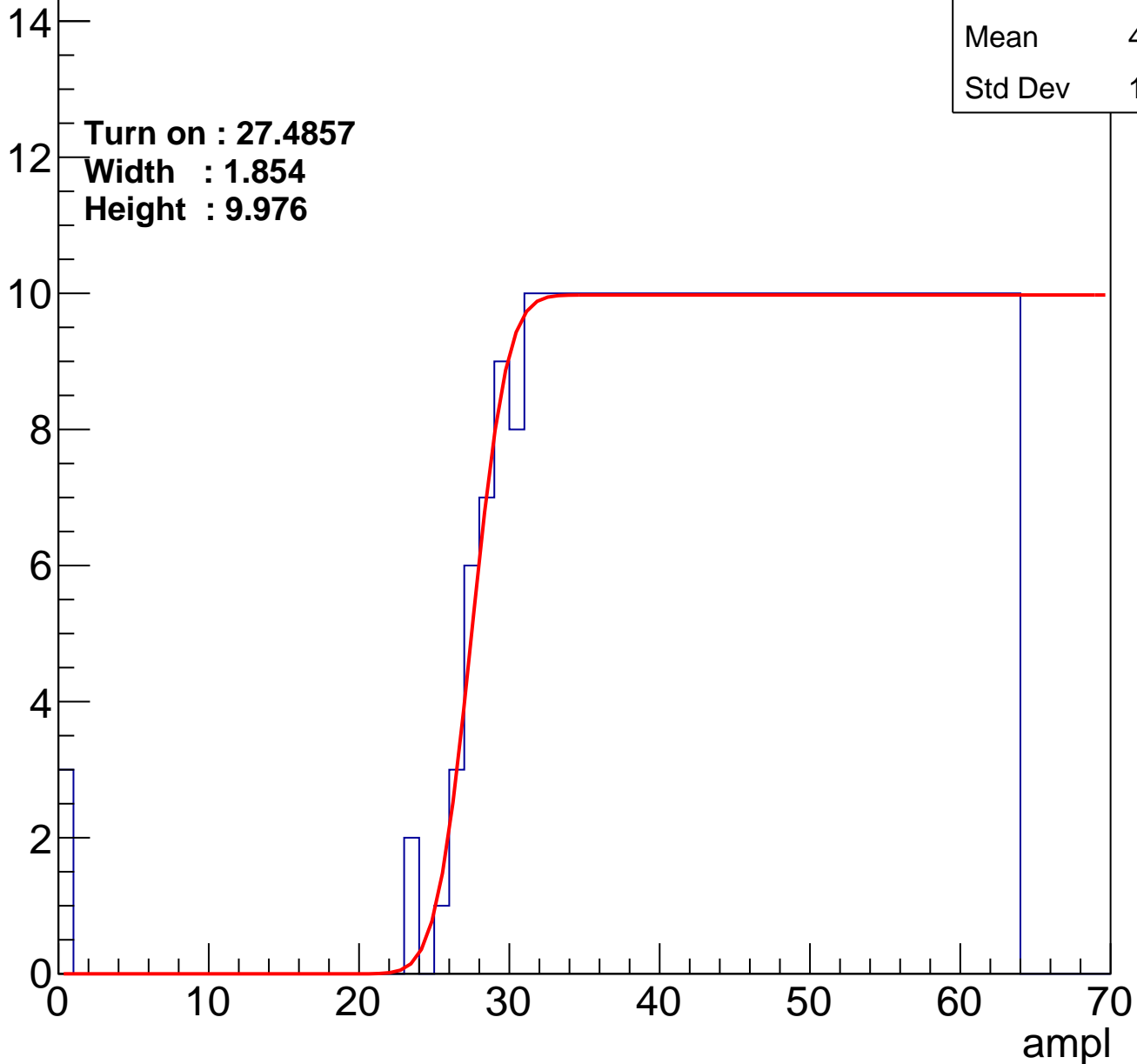
Entries	369
Mean	44.76
Std Dev	11.38

Turn on : 27.4857

Width : 1.854

Height : 9.976

Entry



B0L002S, U26-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.68
Std Dev	10.8

Turn on : 29.5095

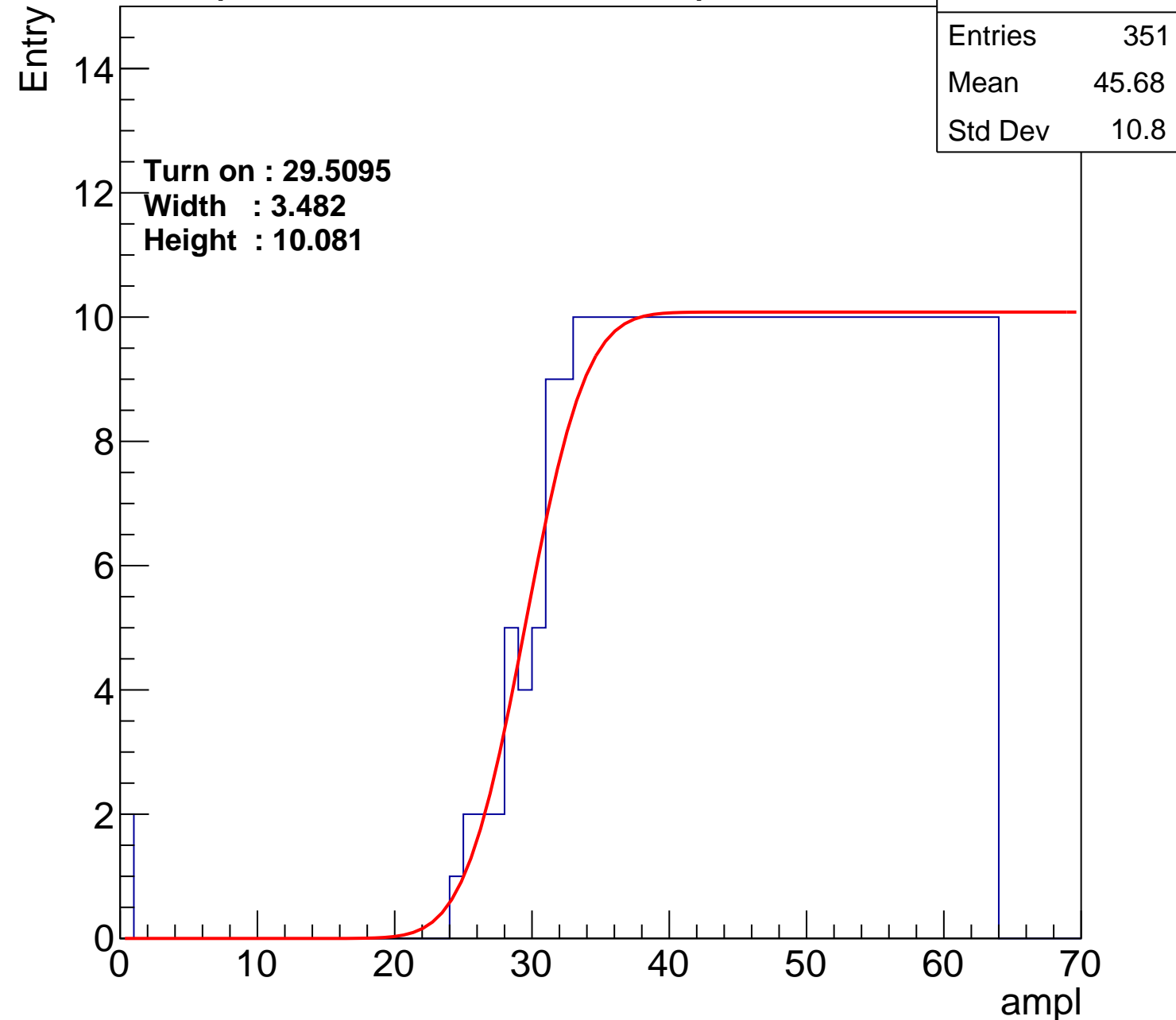
Width : 3.482

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch42

calib_packv5_042523_0143.root, FC#8, port C1

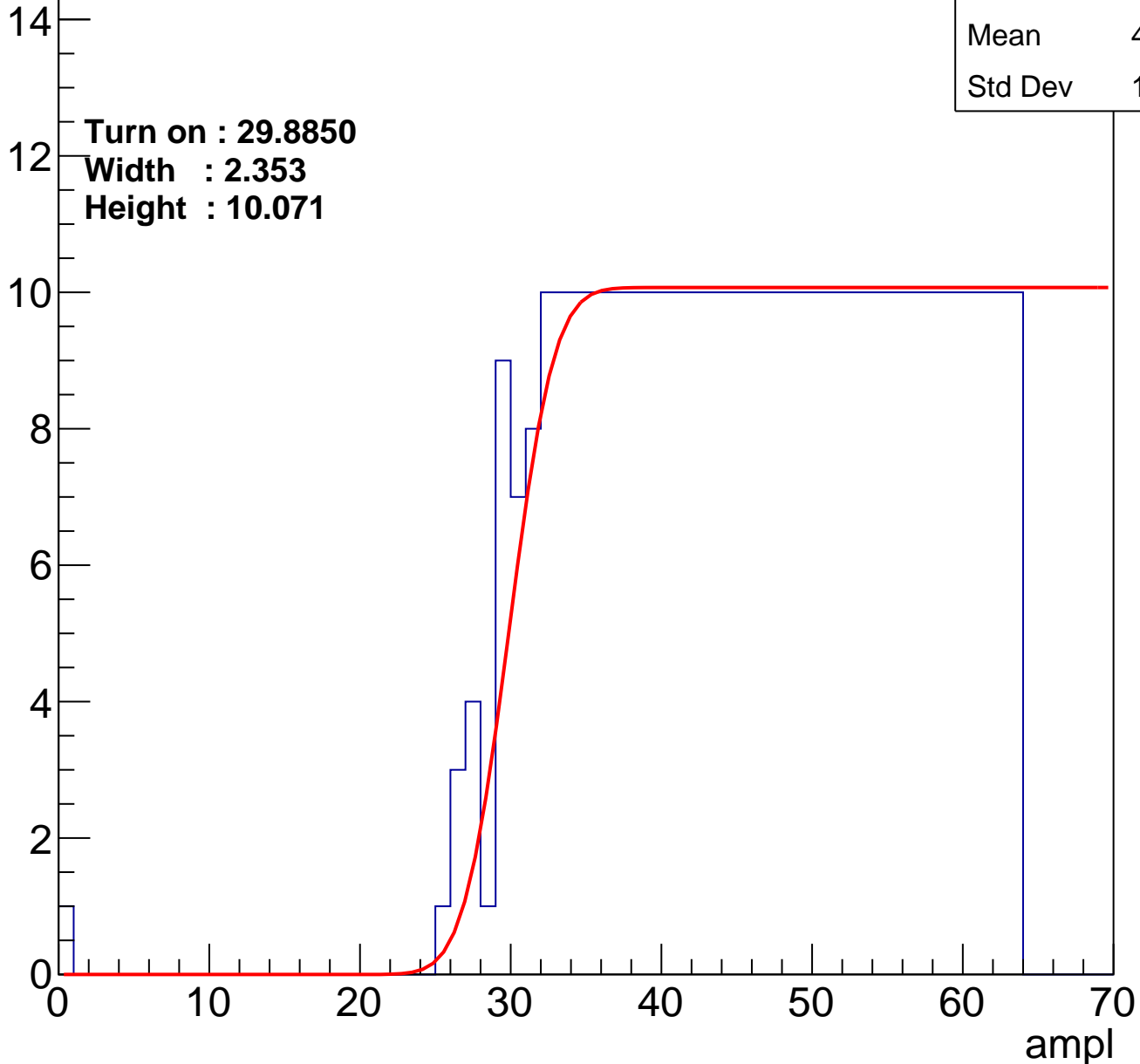
Entries	354
Mean	45.64
Std Dev	10.59

Turn on : 29.8850

Width : 2.353

Height : 10.071

Entry



B0L002S, U26-ch43

calib_packv5_042523_0143.root, FC#8, port C1

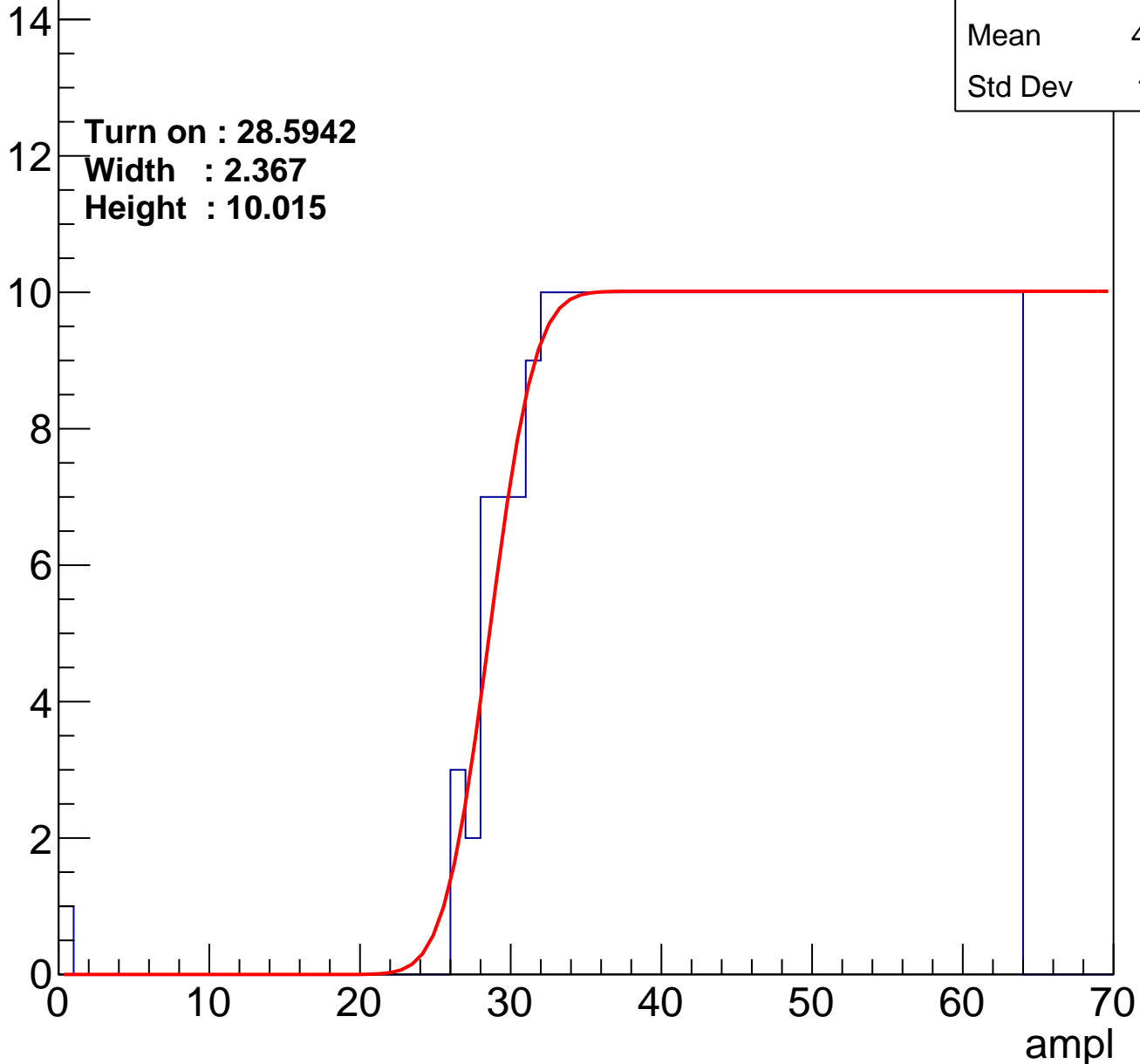
Entries	356
Mean	45.56
Std Dev	10.61

Turn on : 28.5942

Width : 2.367

Height : 10.015

Entry



B0L002S, U26-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.2977

Width : 2.587

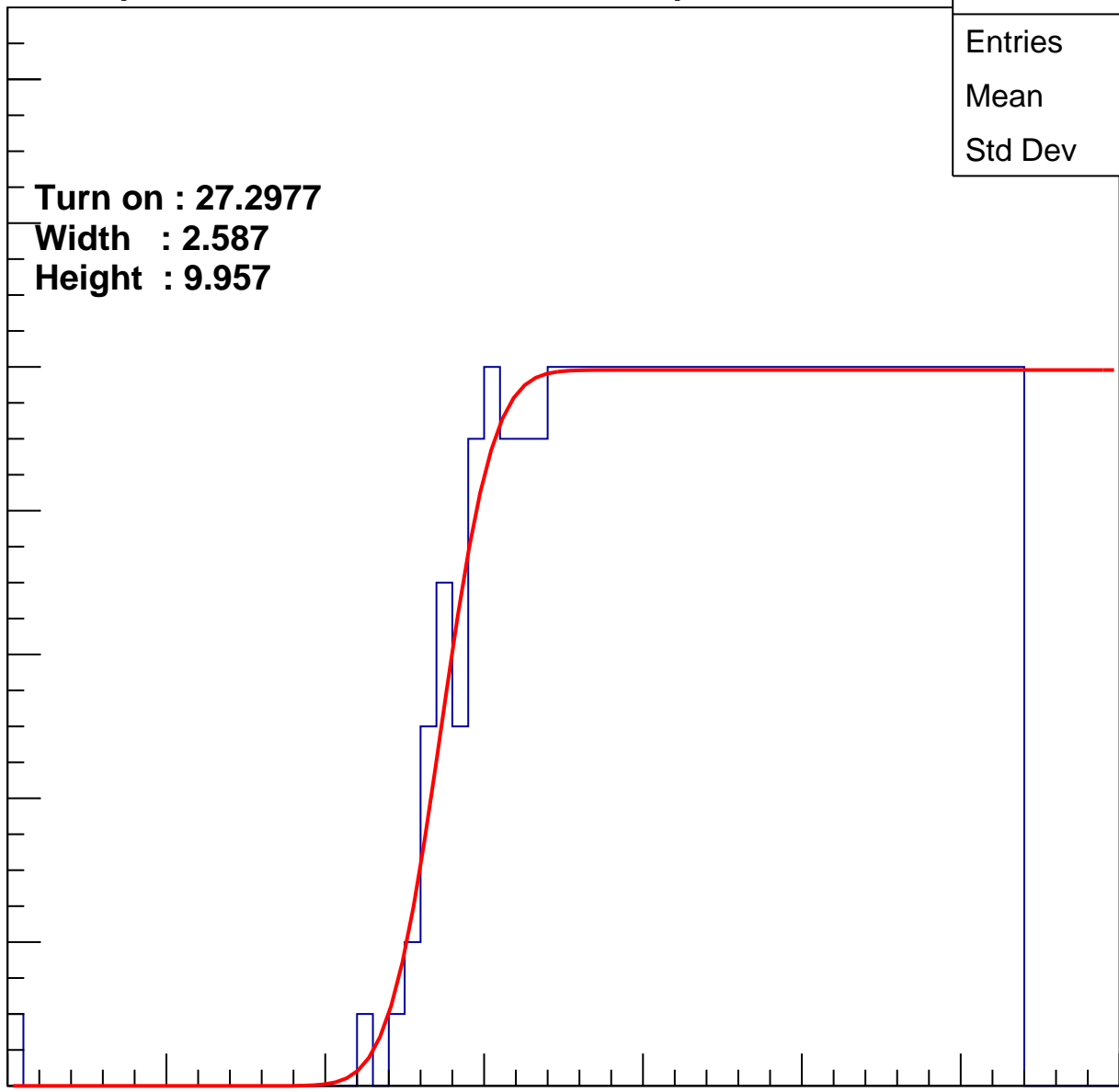
Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U26-ch45

calib_packv5_042523_0143.root, FC#8, port C1

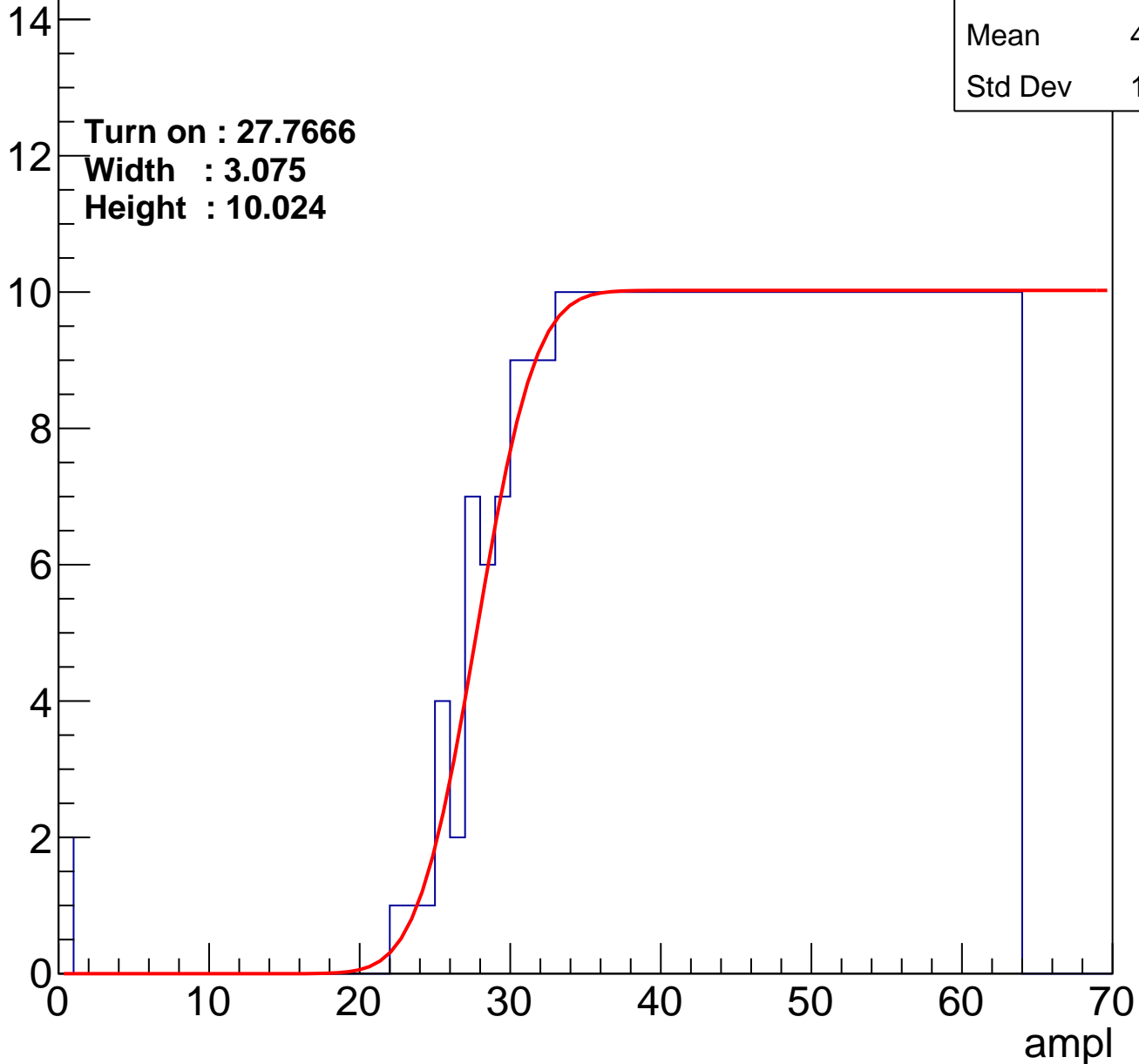
Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.7666

Width : 3.075

Height : 10.024

Entry



B0L002S, U26-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.71
Std Dev	11.42

Turn on : 27.6506

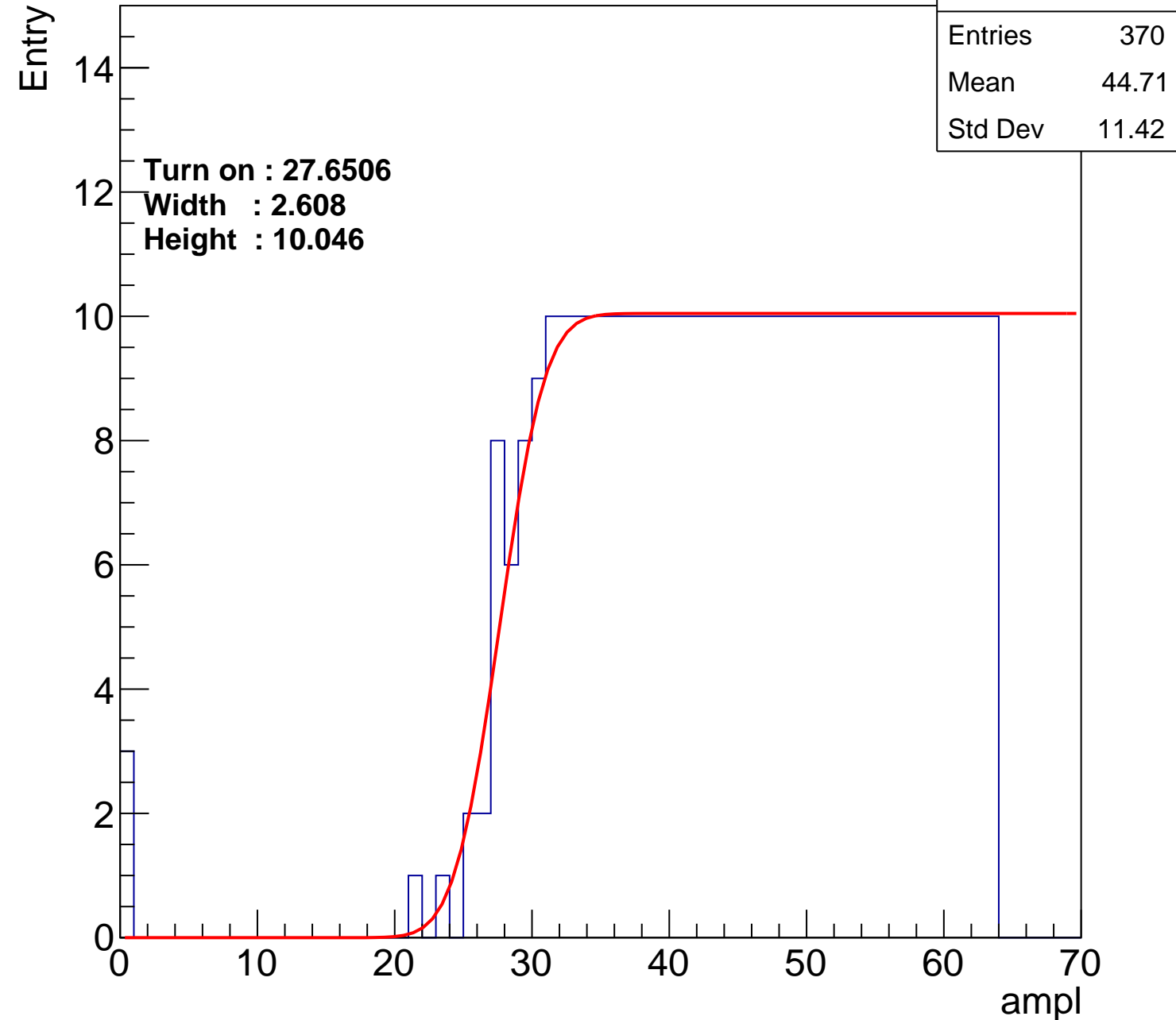
Width : 2.608

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.98
Std Dev	11.36

Turn on : 28.4550

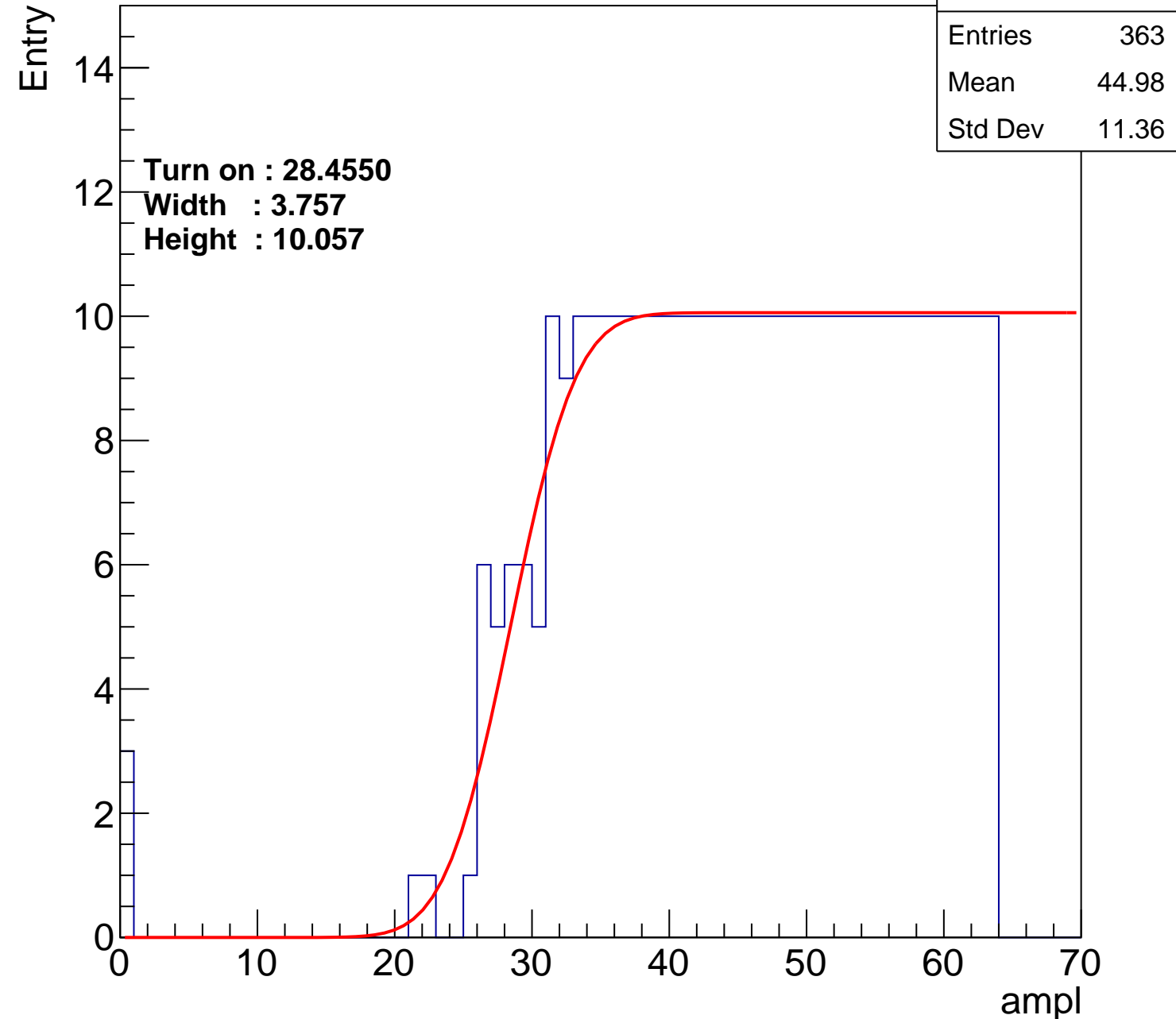
Width : 3.757

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.26
Std Dev	11.13

Turn on : 28.6712

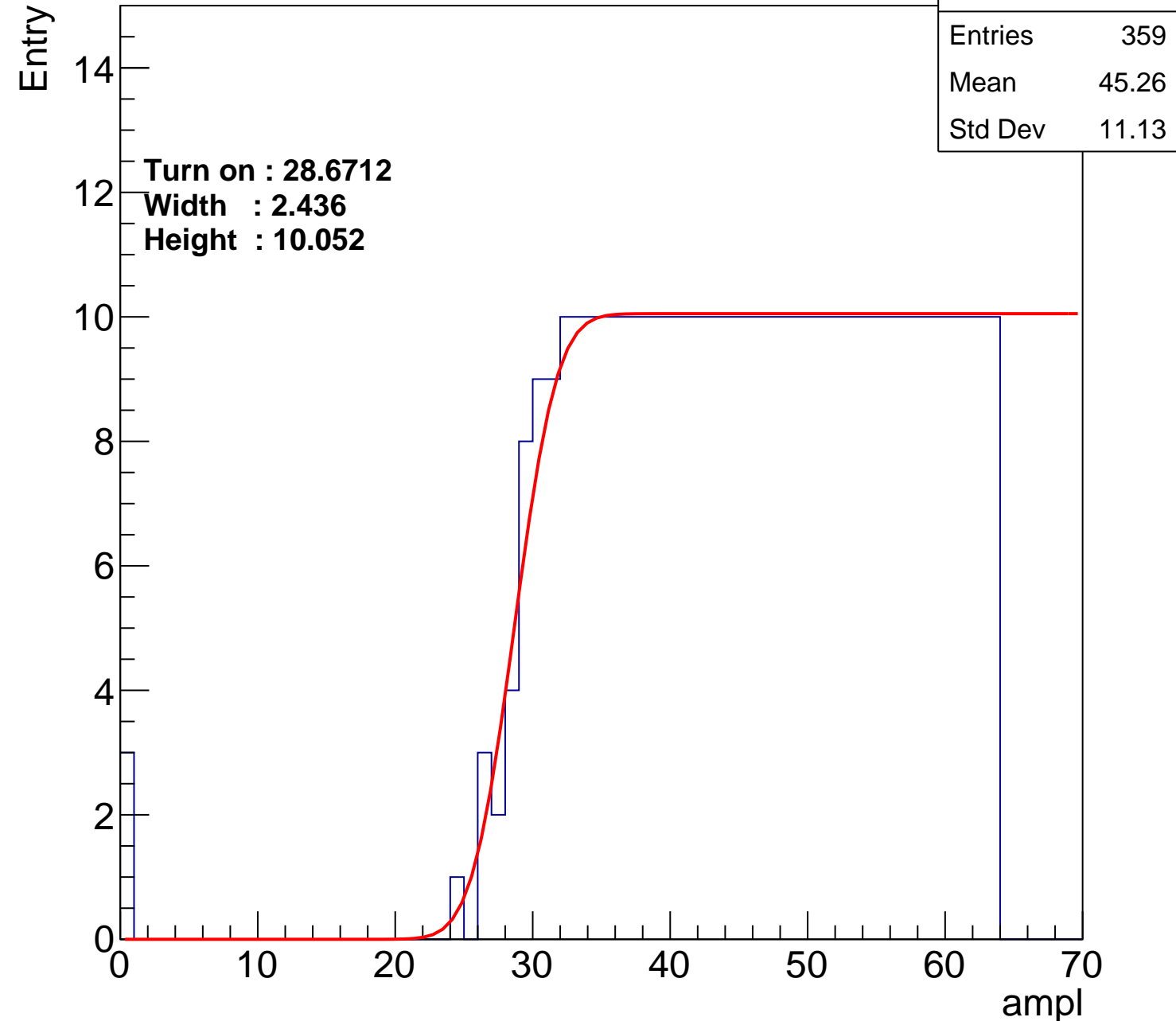
Width : 2.436

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.23
Std Dev	10.83

Turn on : 27.9882

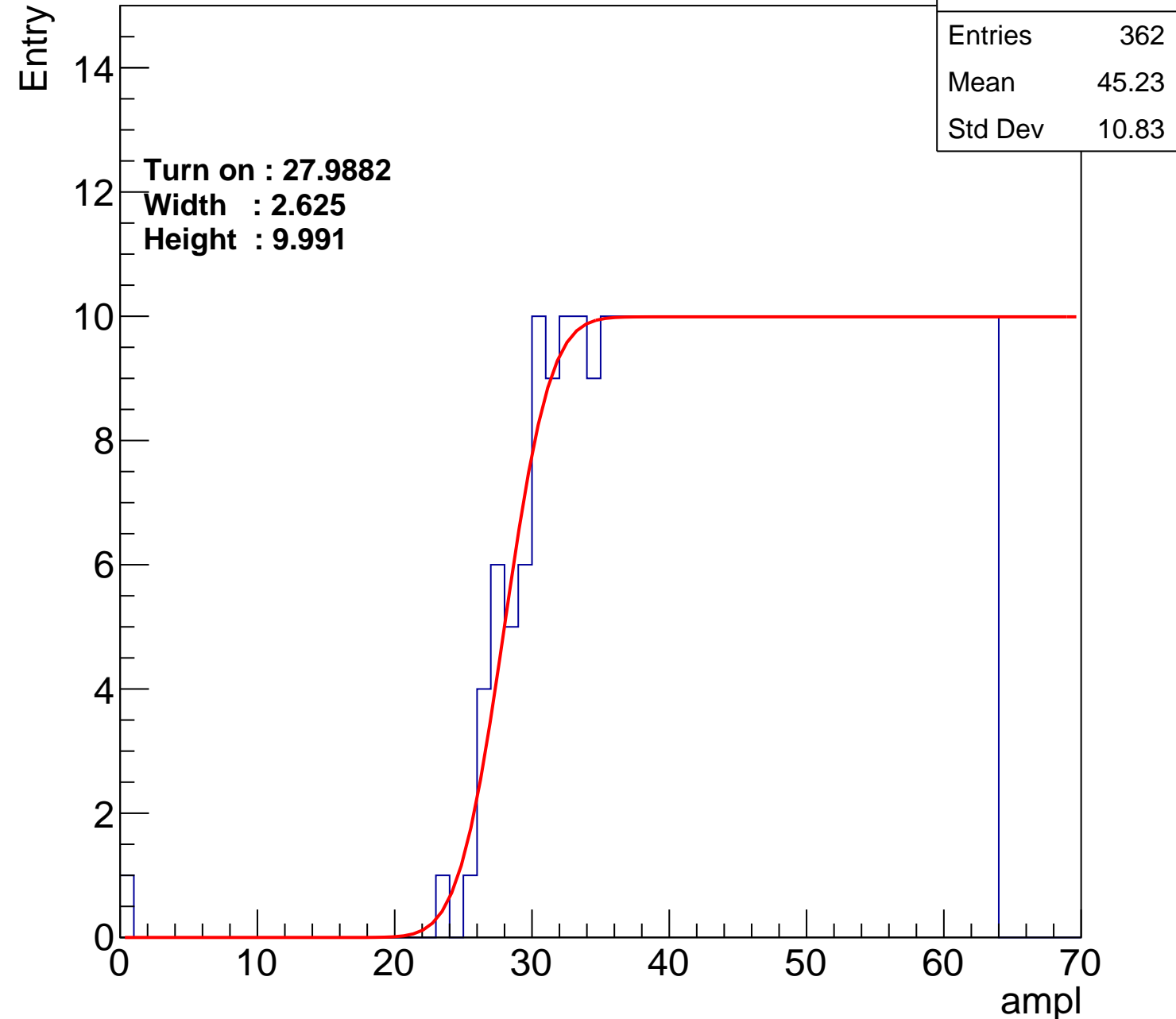
Width : 2.625

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.38
Std Dev	11.74

Turn on : 26.7087

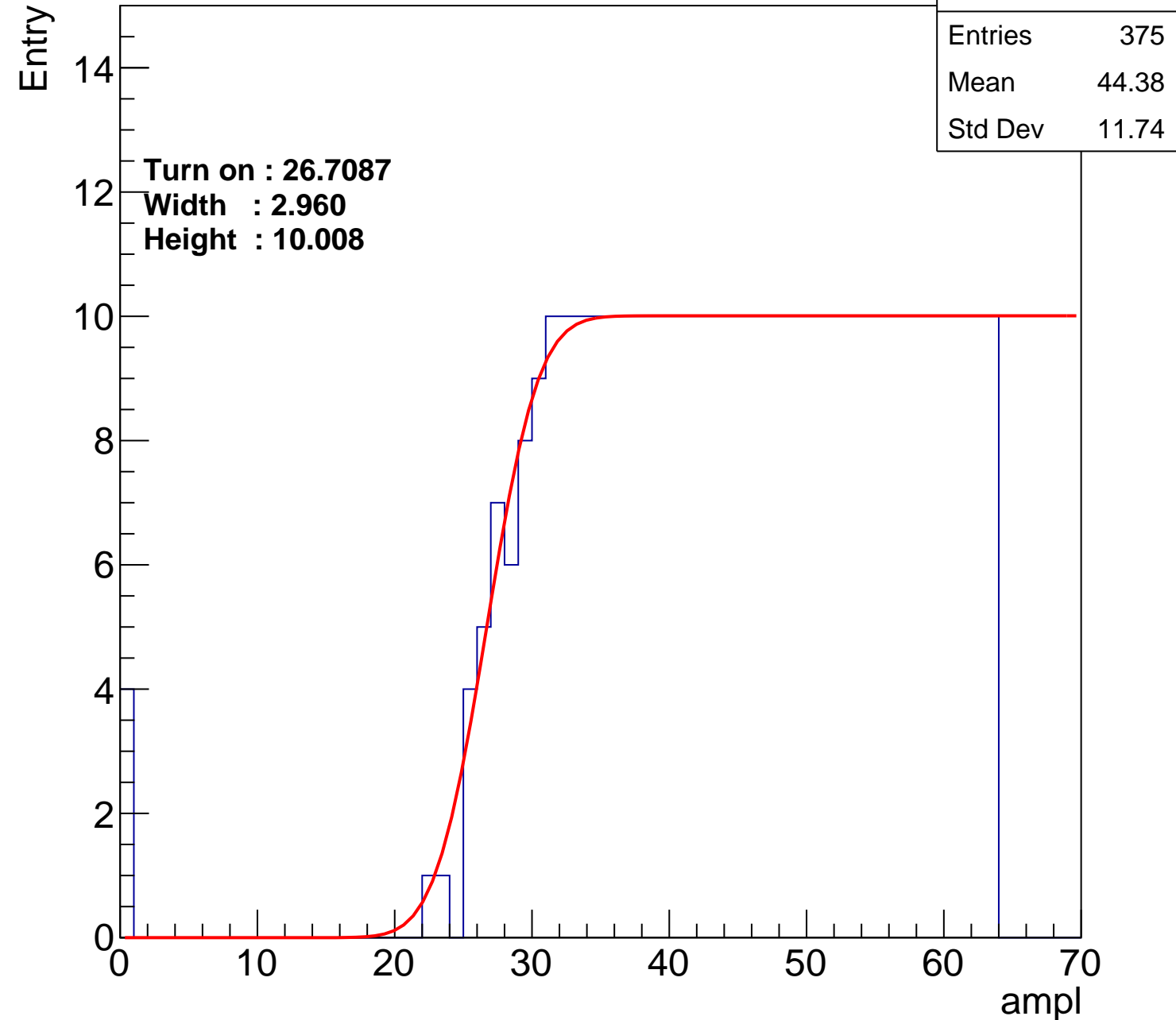
Width : 2.960

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.34
Std Dev	10.97

Turn on : 28.4692

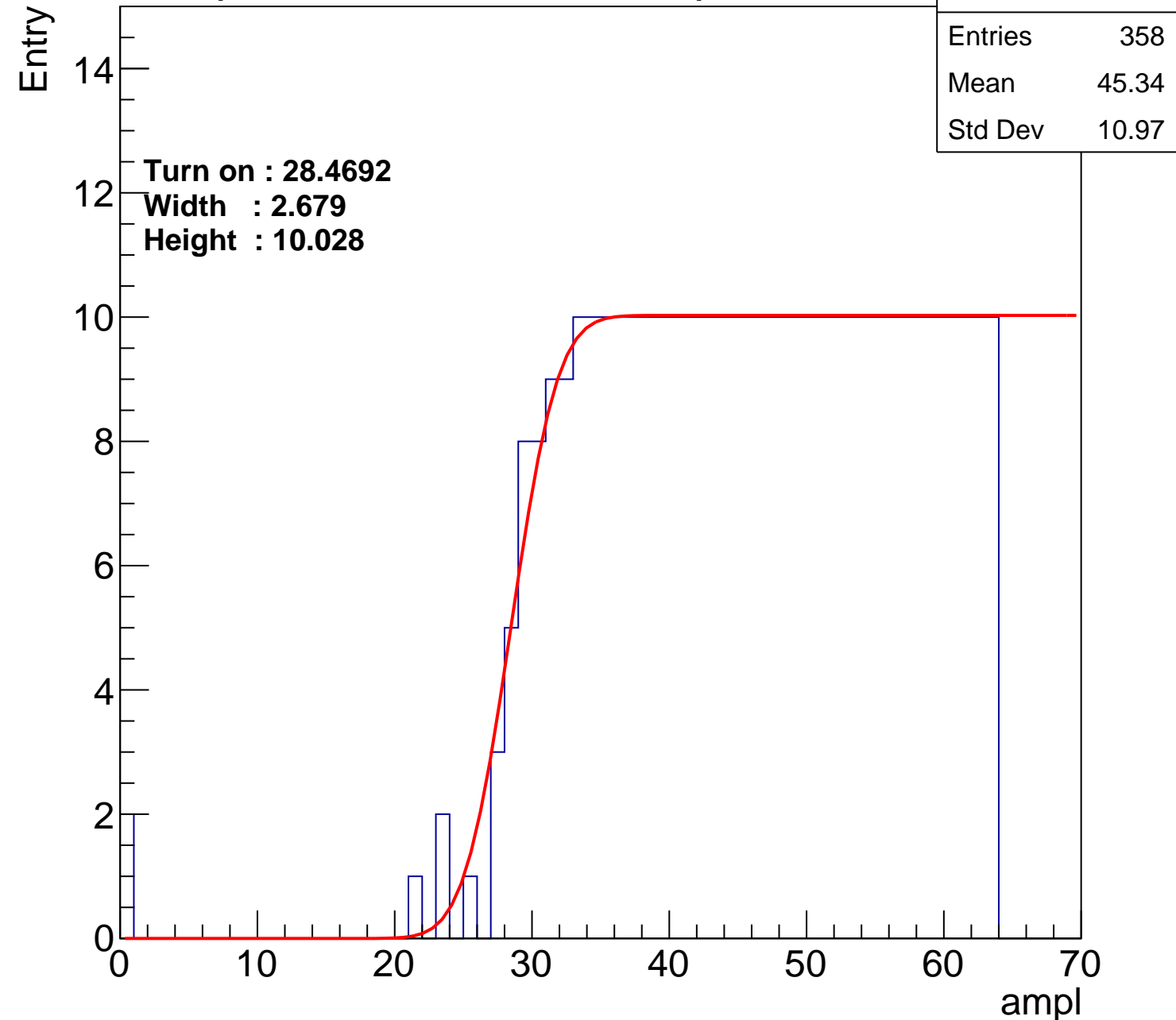
Width : 2.679

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.85
Std Dev	11.07

Turn on : 27.4072

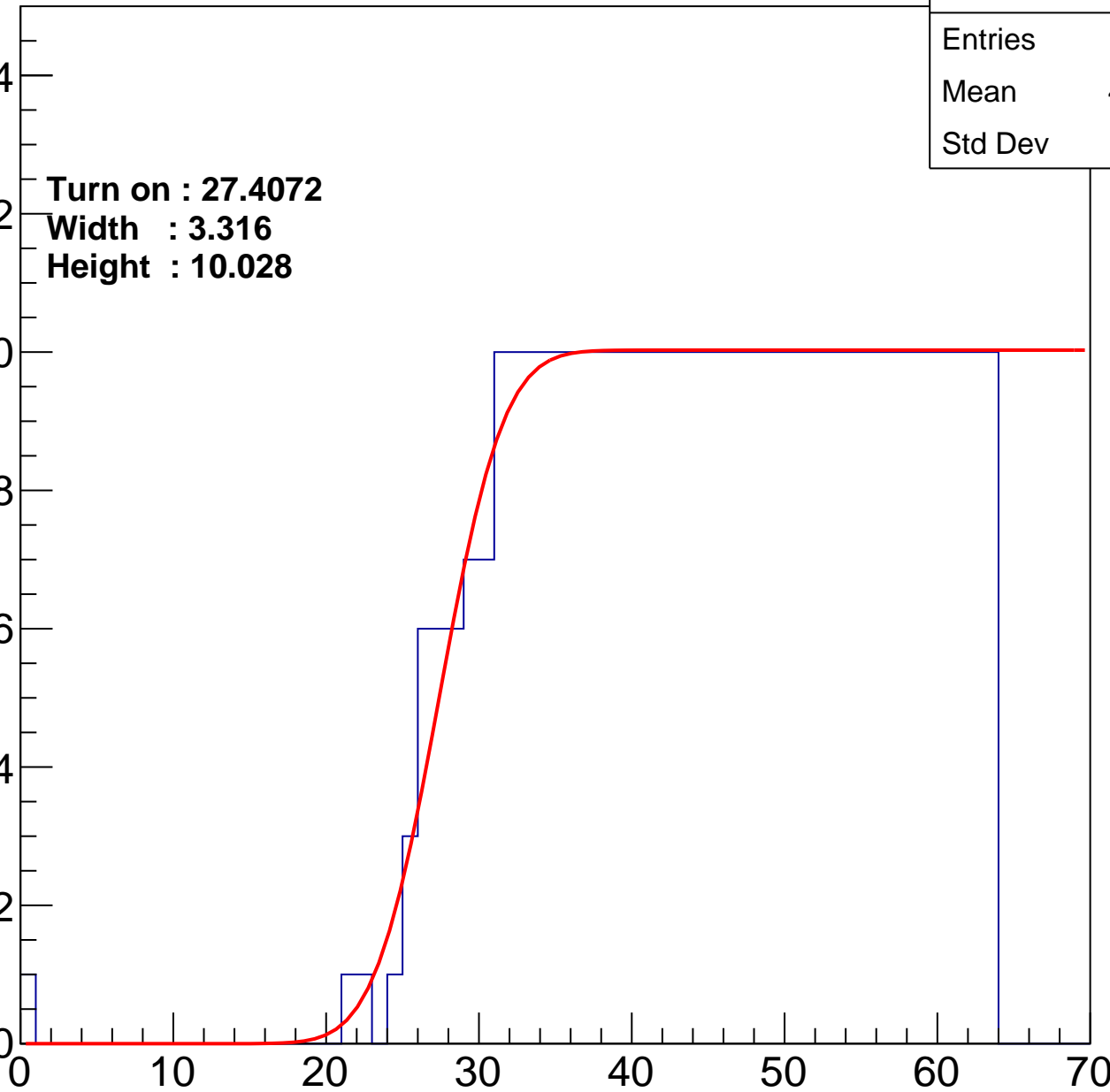
Width : 3.316

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	345
Mean	45.87
Std Dev	10.92

Turn on : 29.4616

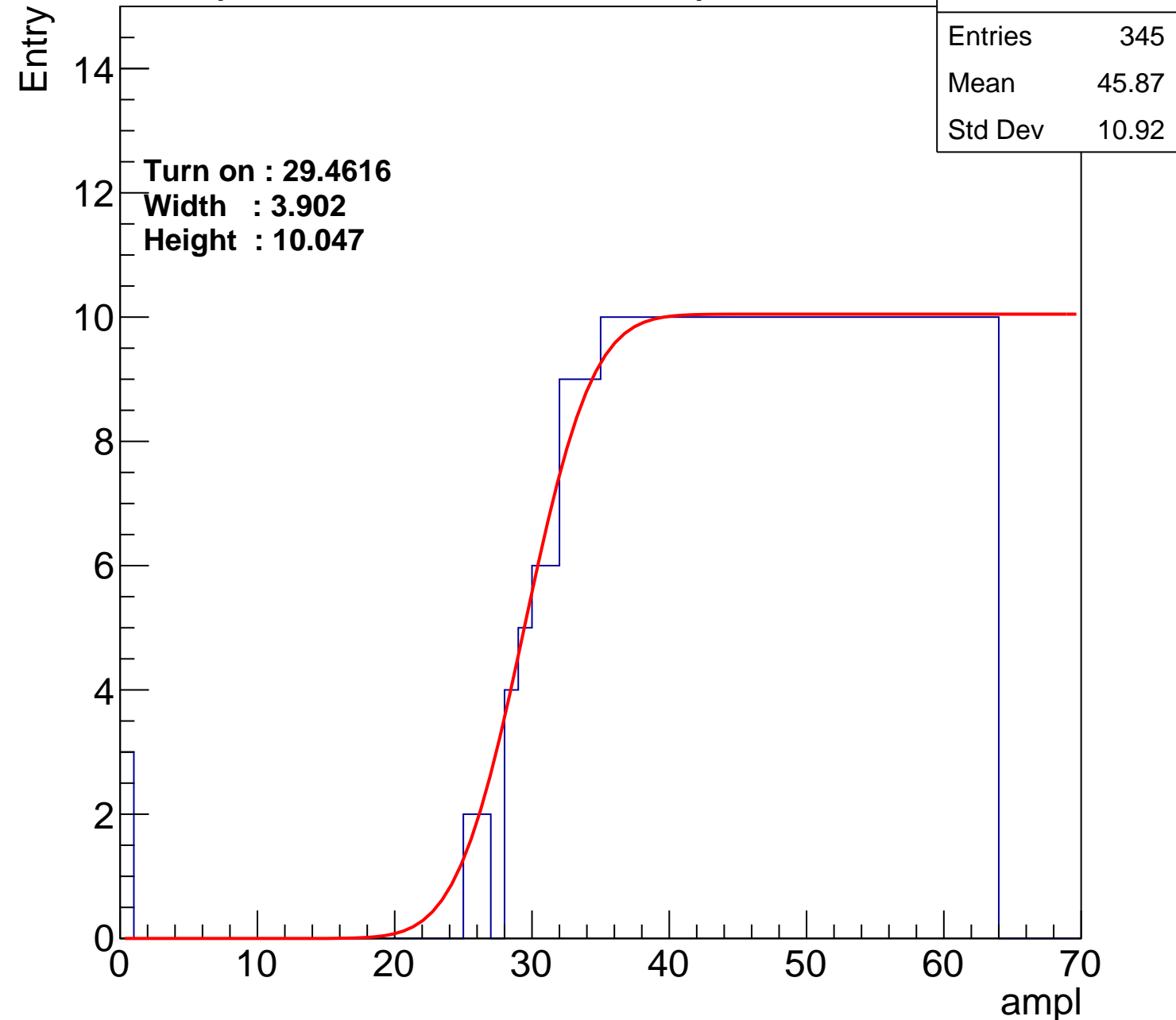
Width : 3.902

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.33
Std Dev	11.59

Turn on : 26.6357

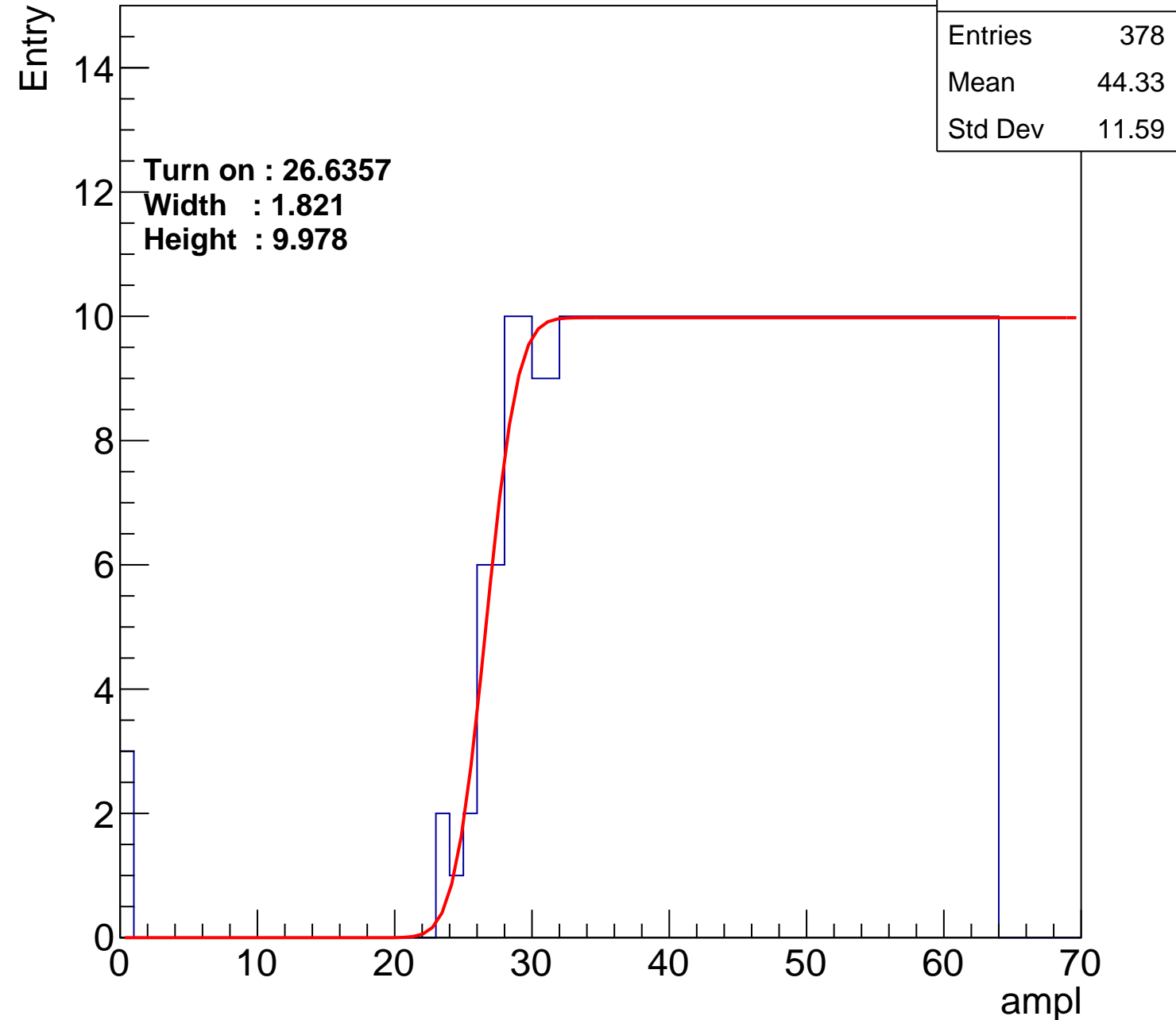
Width : 1.821

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45
Std Dev	11.27

Turn on : 28.1591

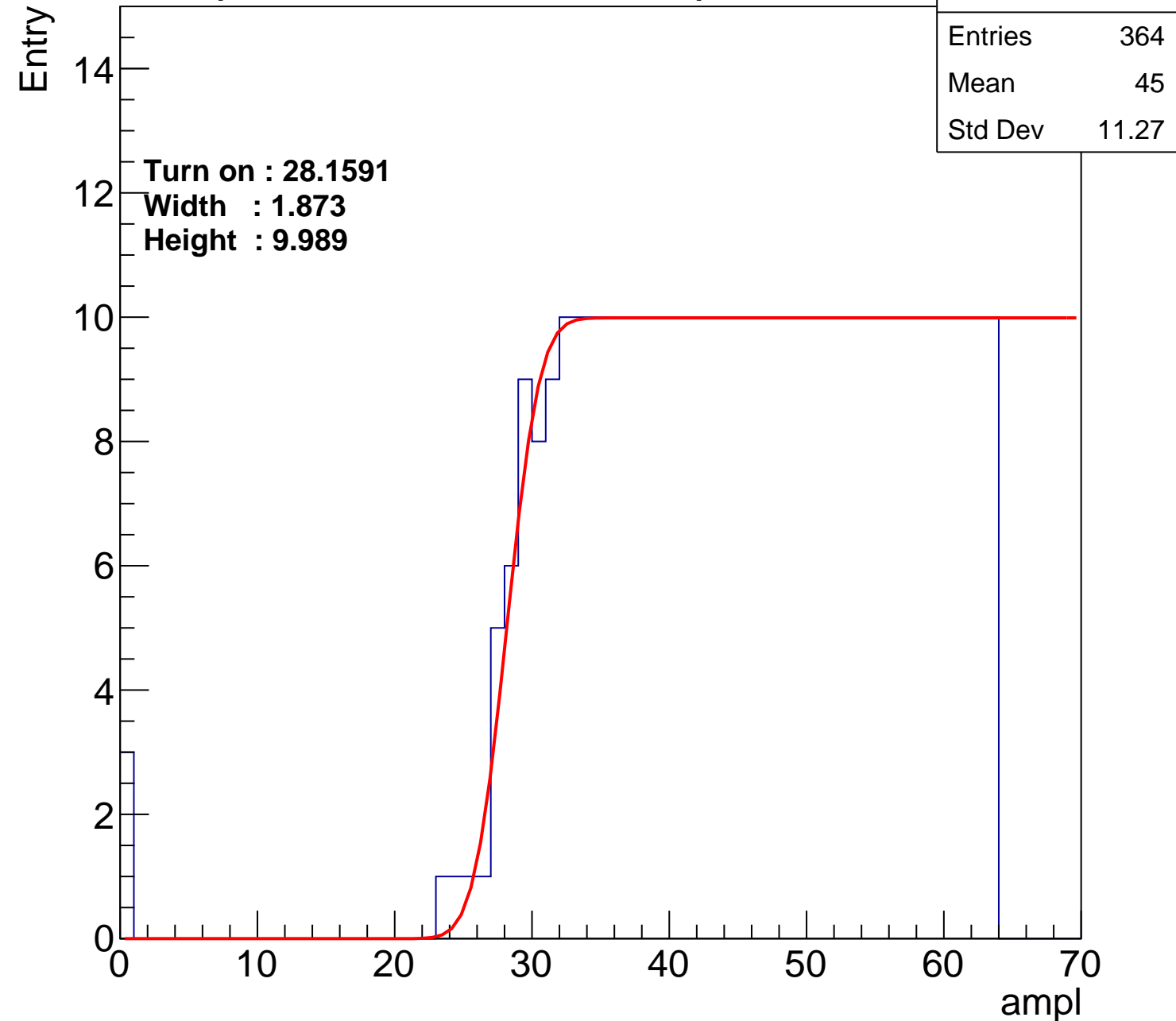
Width : 1.873

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.92
Std Dev	11.34

Turn on : 27.5992

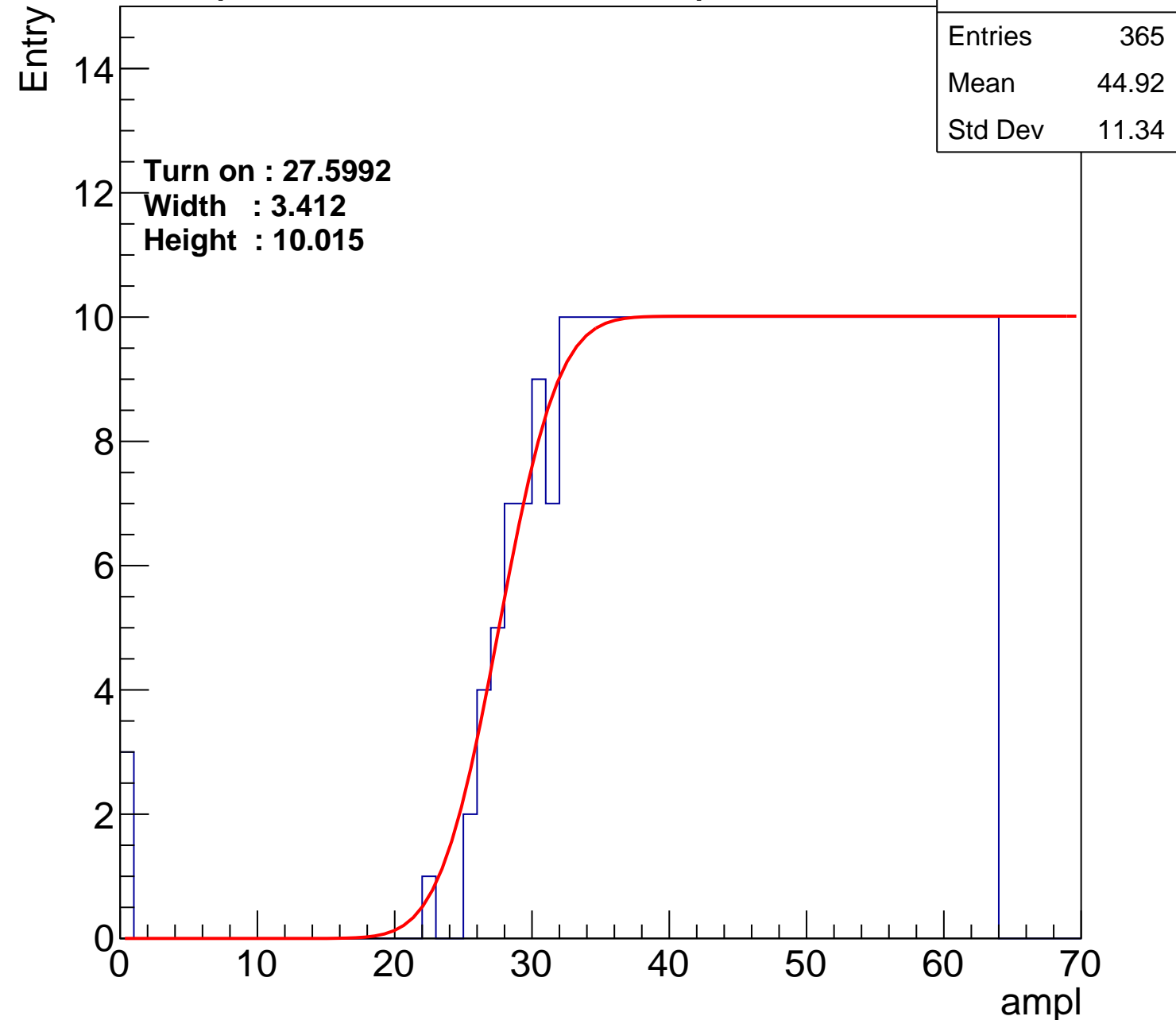
Width : 3.412

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch57

calib_packv5_042523_0143.root, FC#8, port C1

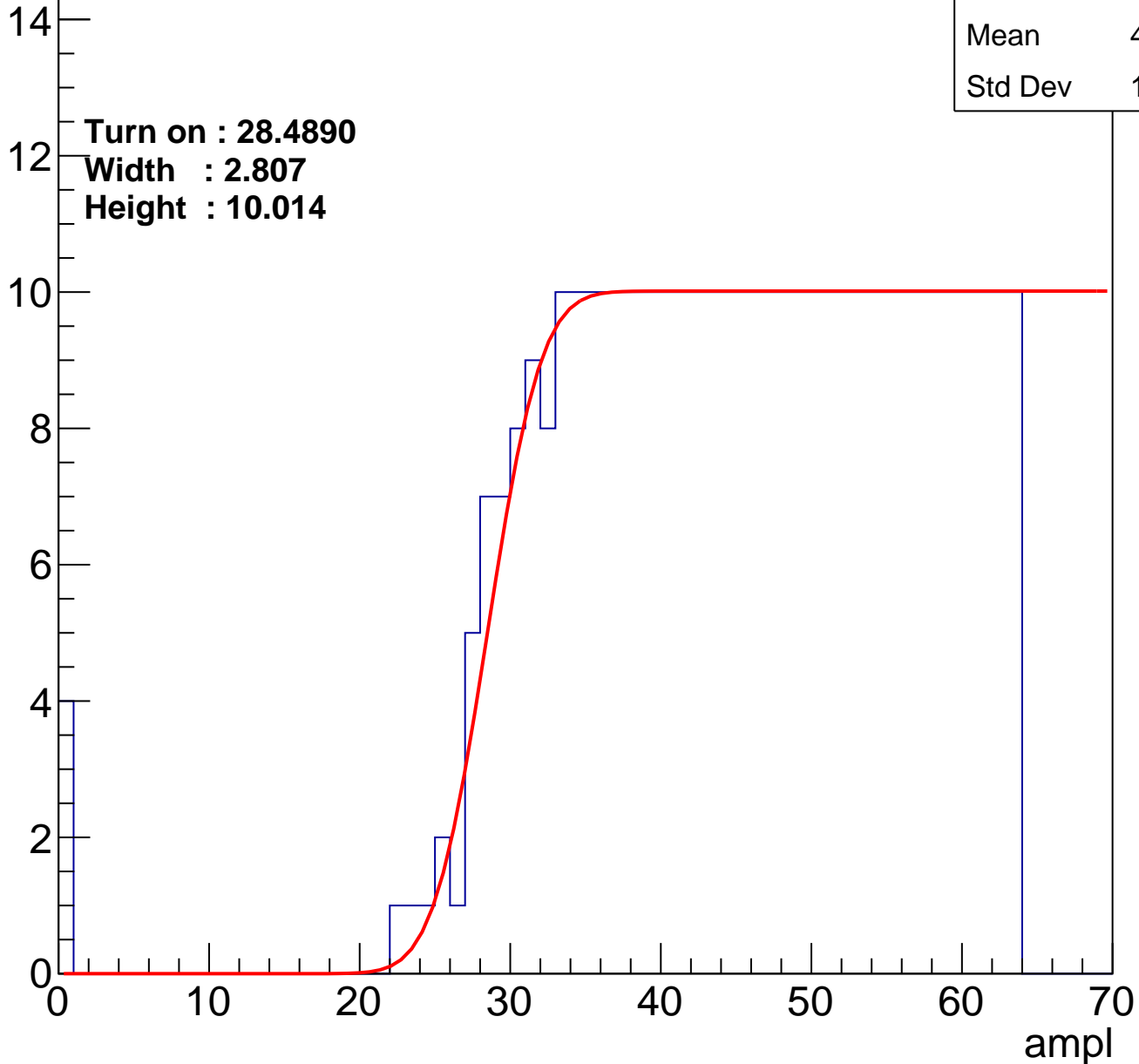
Entries	364
Mean	44.87
Std Dev	11.56

Turn on : 28.4890

Width : 2.807

Height : 10.014

Entry



B0L002S, U26-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.19
Std Dev	11.96

Turn on : 26.4569

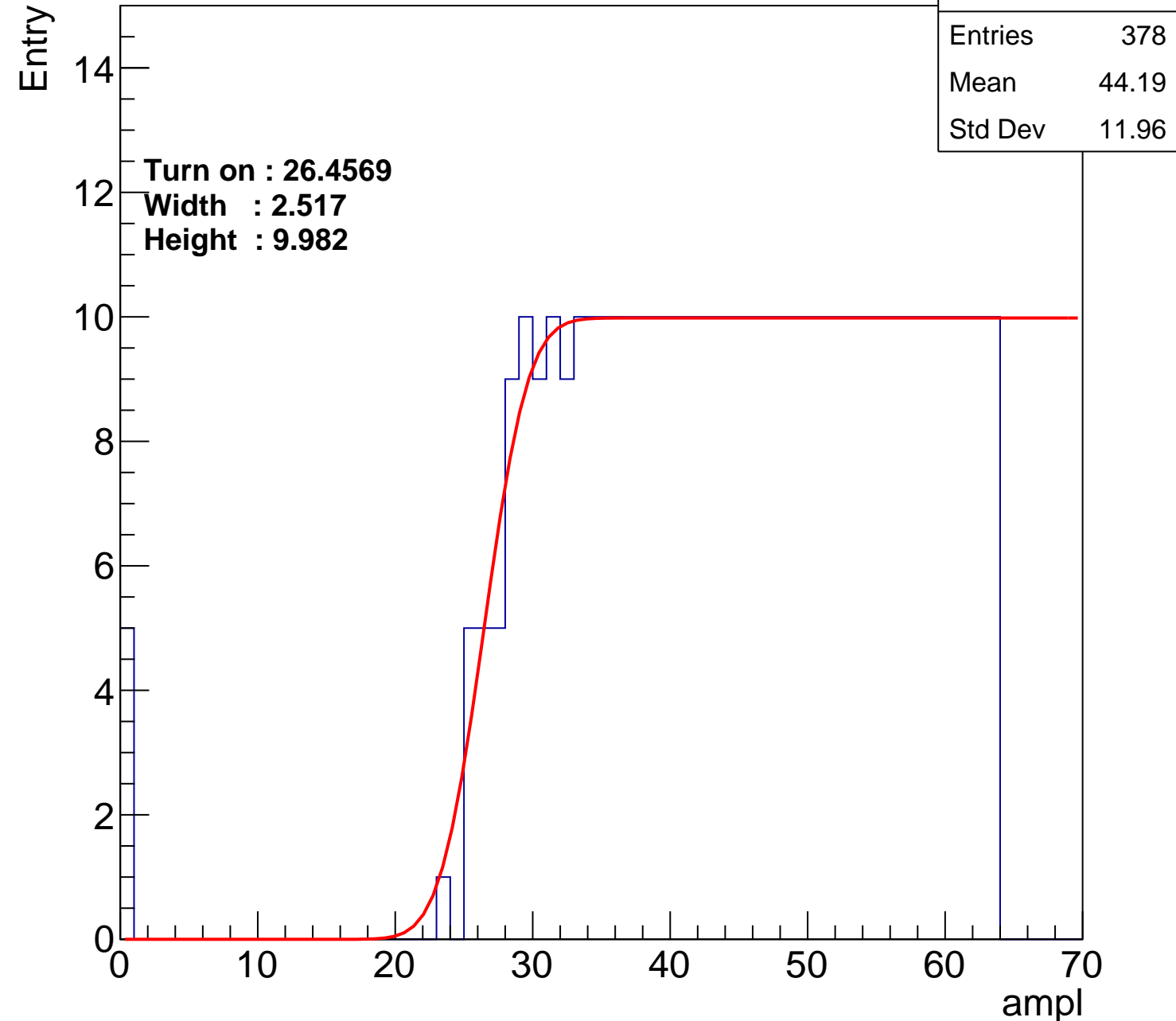
Width : 2.517

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch59

calib_packv5_042523_0143.root, FC#8, port C1

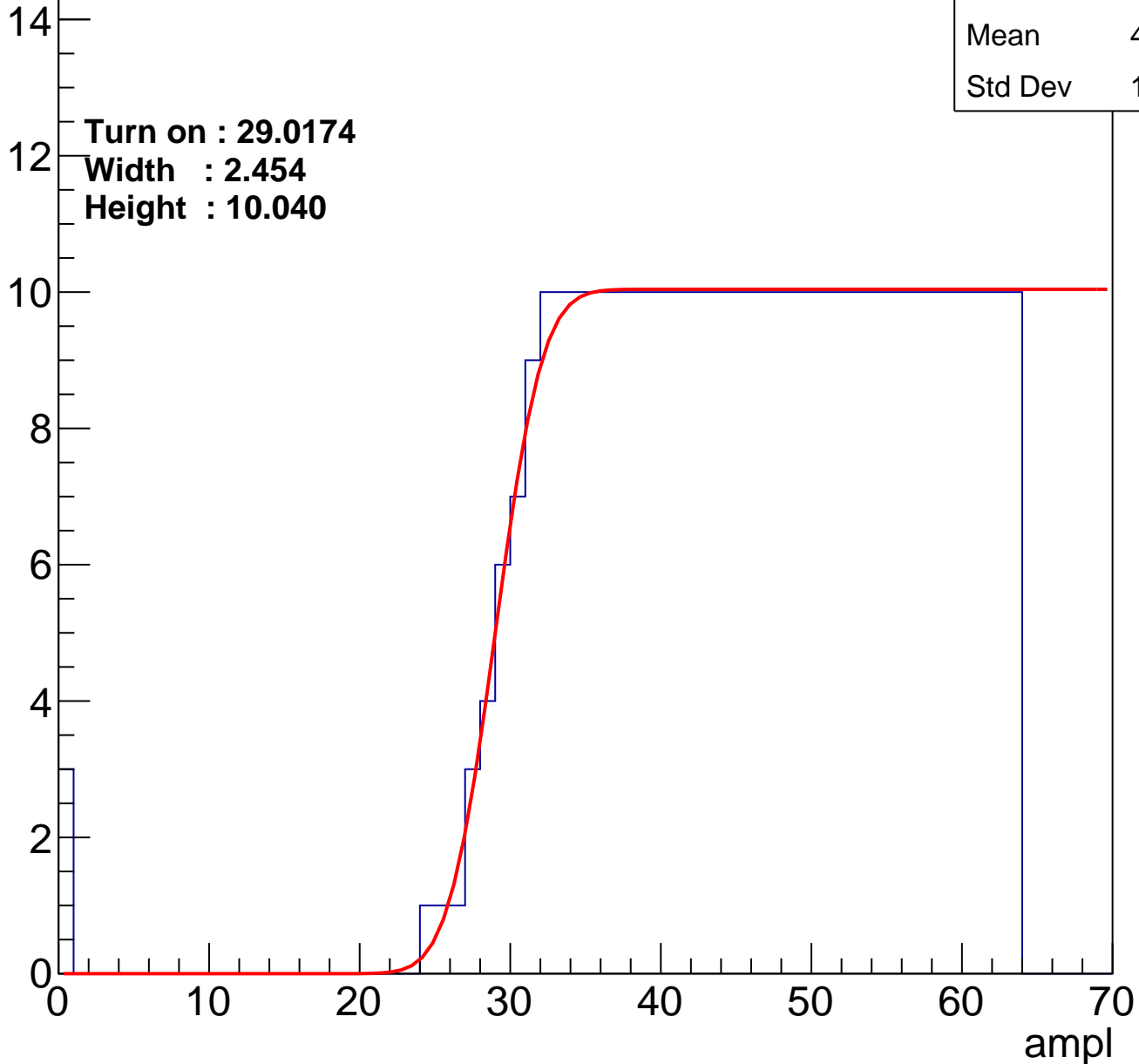
Entries	355
Mean	45.44
Std Dev	11.07

Turn on : 29.0174

Width : 2.454

Height : 10.040

Entry



B0L002S, U26-ch60

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.54
Std Dev	12.06

Turn on : 27.8977

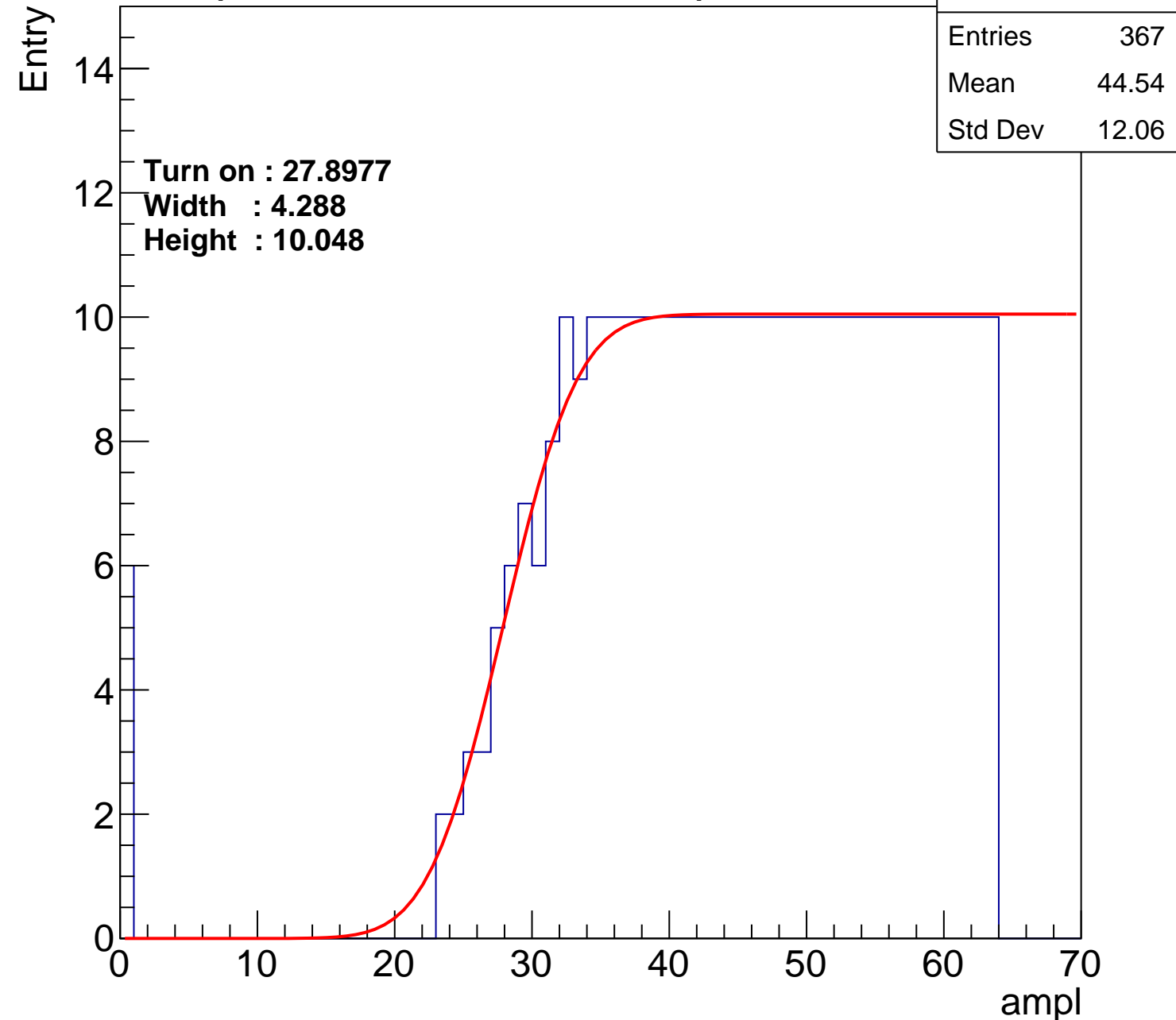
Width : 4.288

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.06
Std Dev	11.04

Turn on : 28.0009

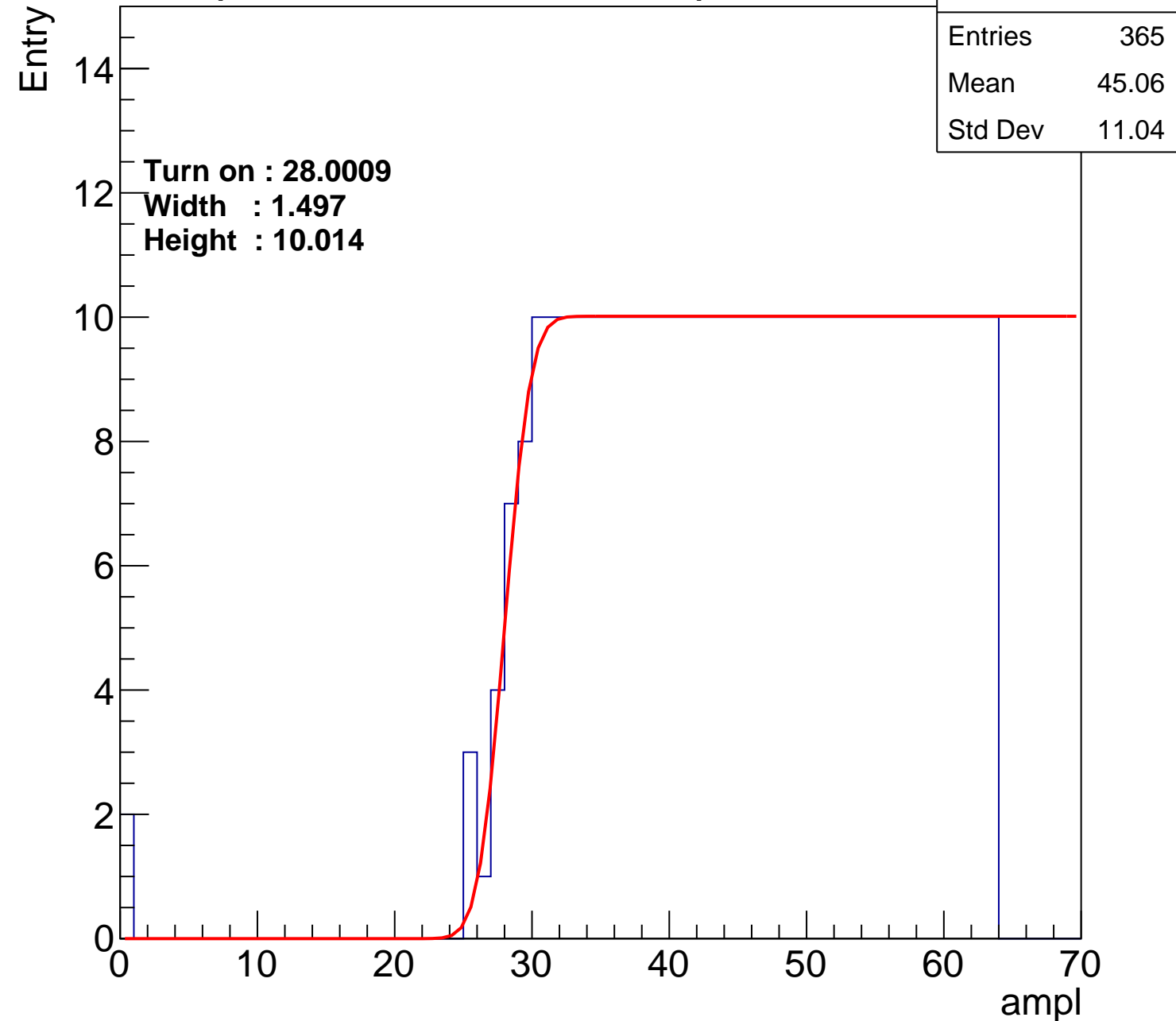
Width : 1.497

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.66
Std Dev	11.63

Turn on : 27.8104

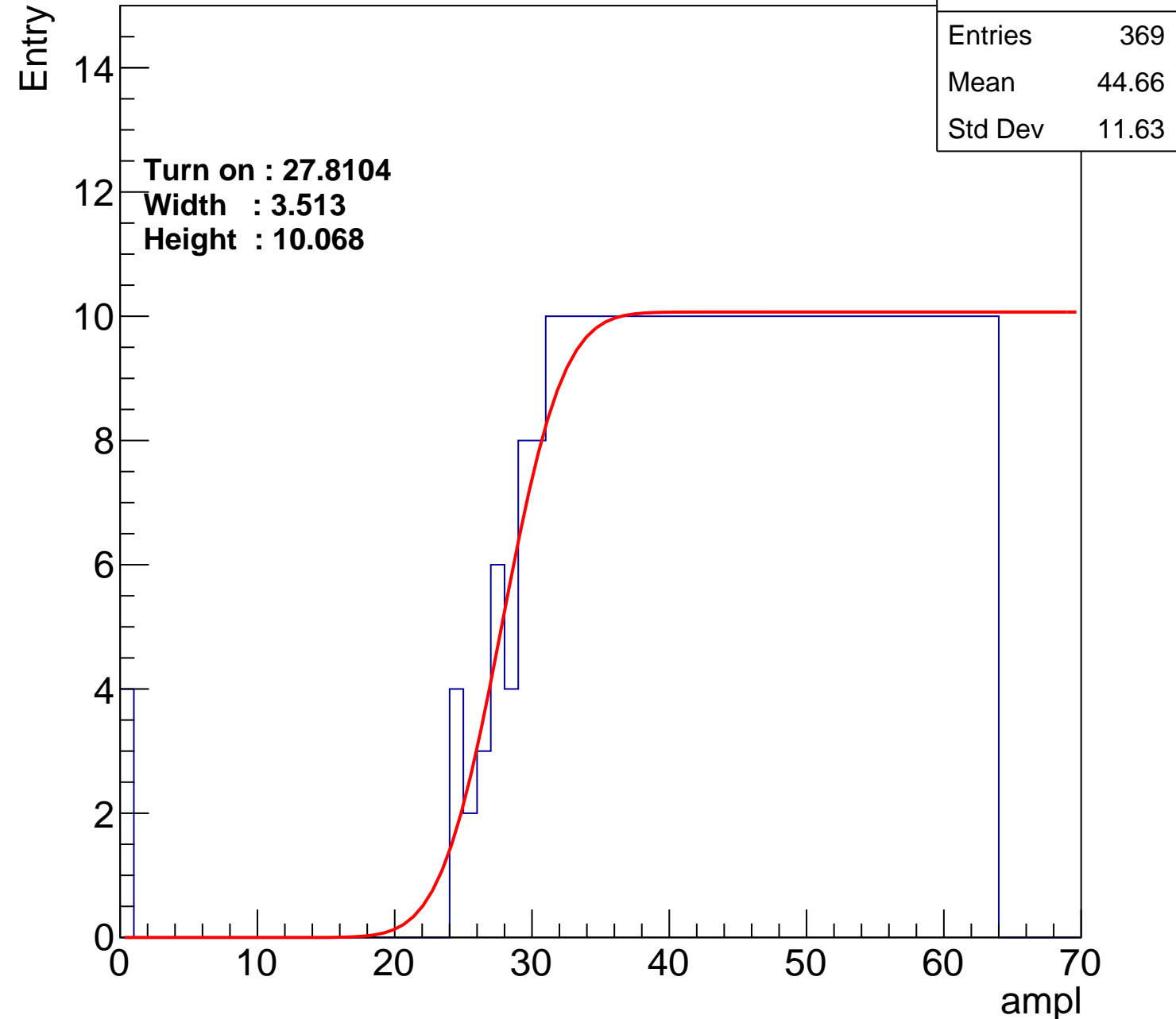
Width : 3.513

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch63

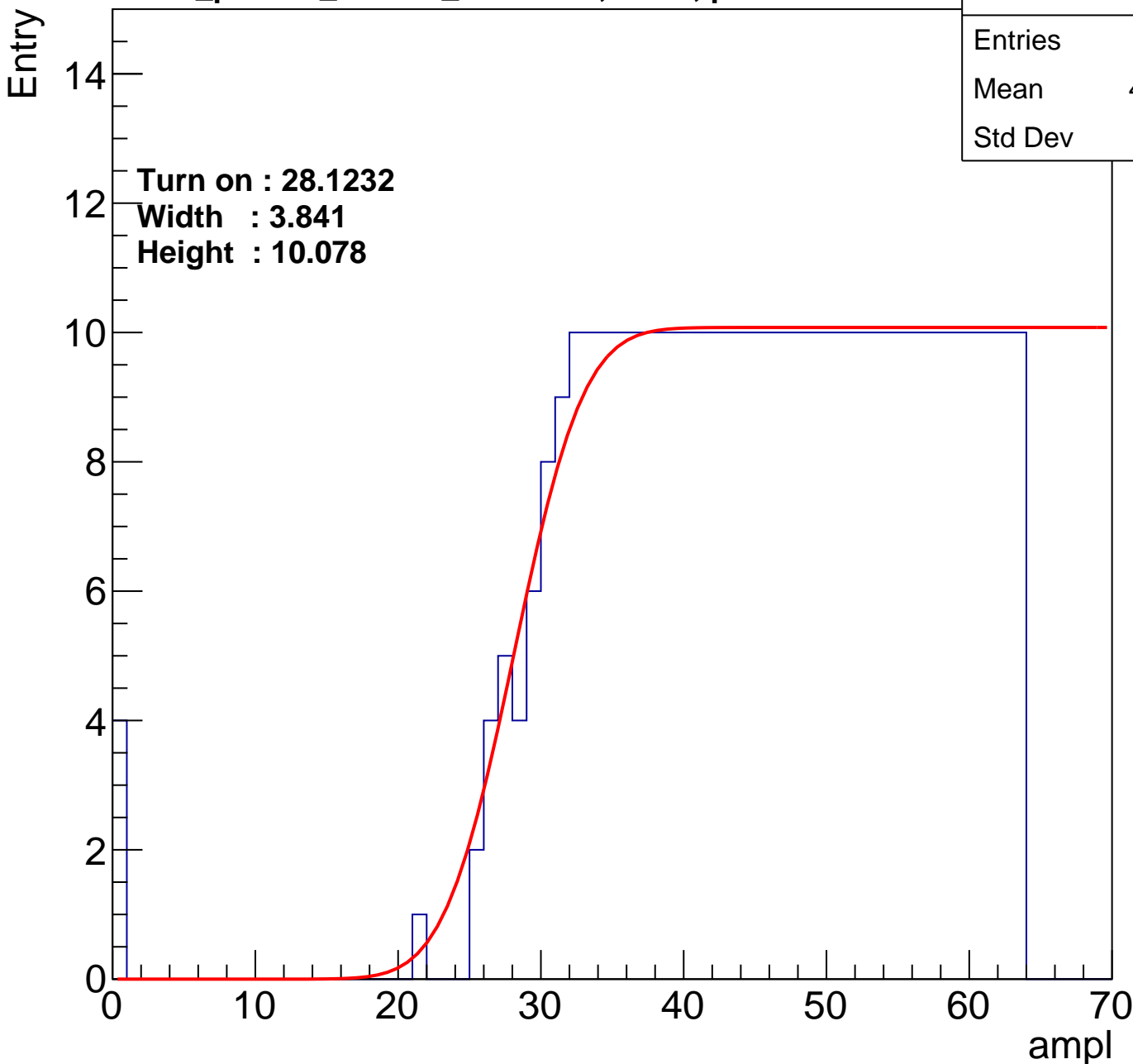
calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.94
Std Dev	11.51

Turn on : 28.1232

Width : 3.841

Height : 10.078



B0L002S, U26-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.3
Std Dev	11.39

Turn on : 27.0768

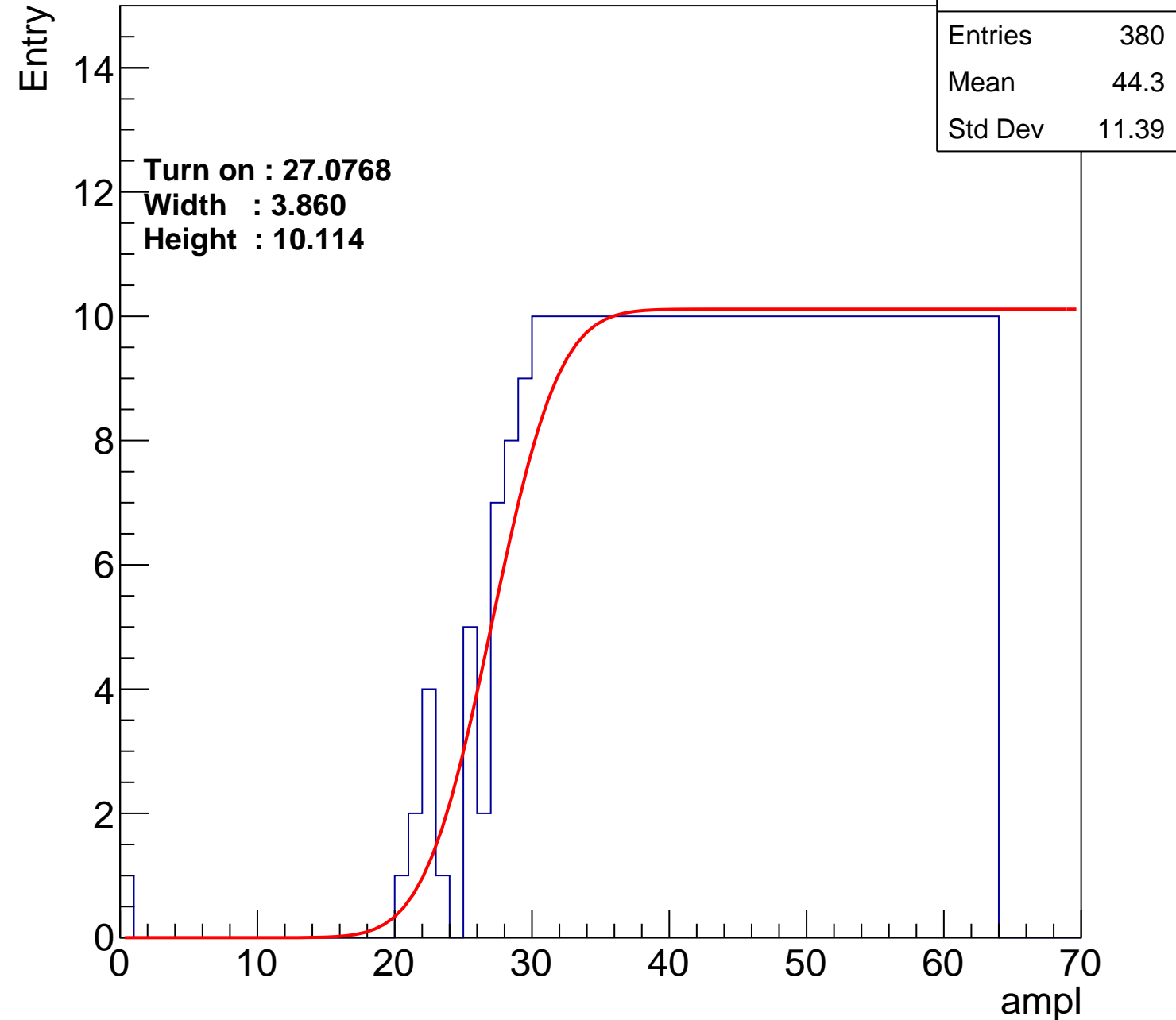
Width : 3.860

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch65

calib_packv5_042523_0143.root, FC#8, port C1

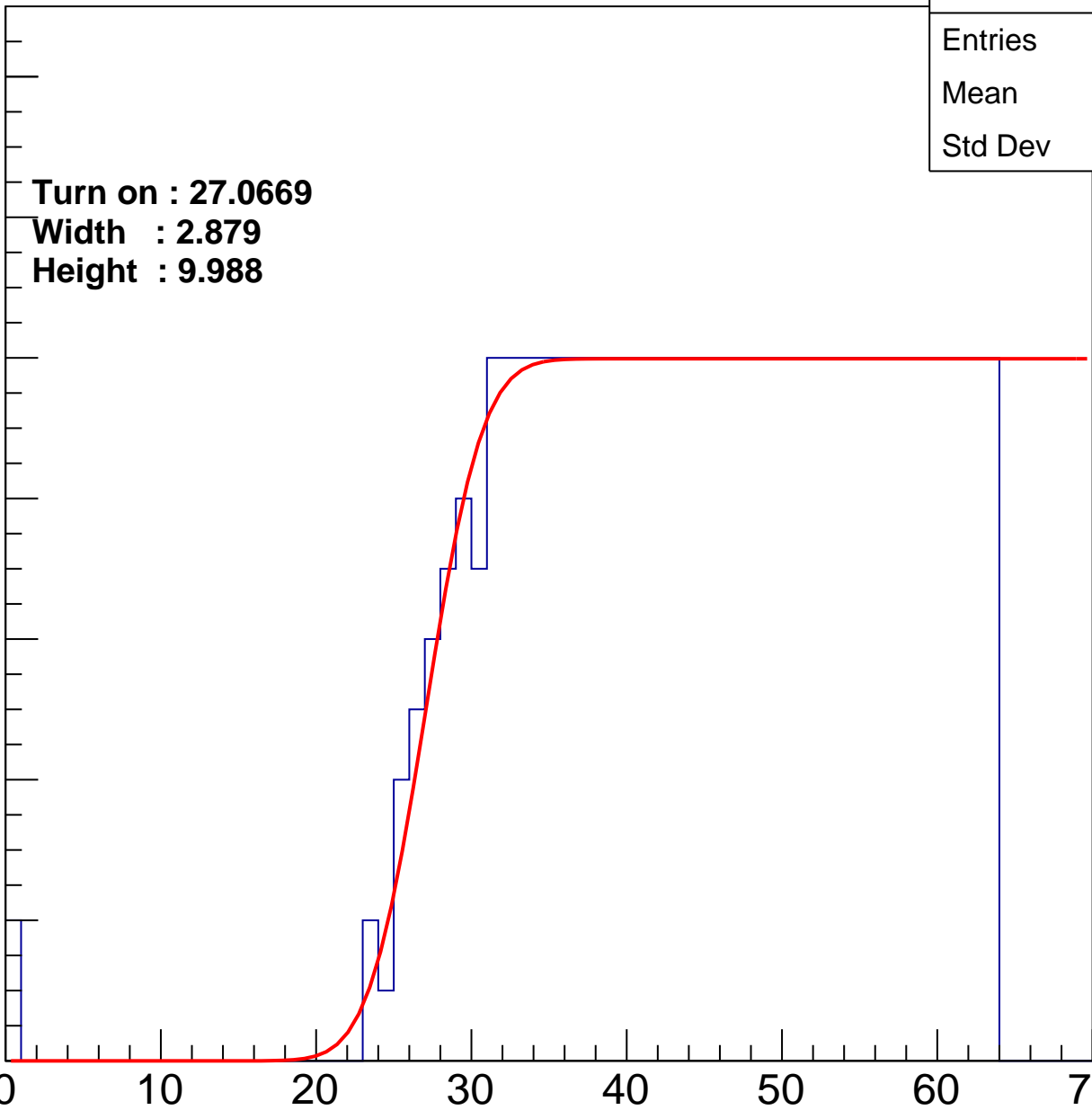
Entry

14
12
10
8
6
4
2
0

Turn on : 27.0669
Width : 2.879
Height : 9.988

Entries	372
Mean	44.65
Std Dev	11.32

ampl



B0L002S, U26-ch66

calib_packv5_042523_0143.root, FC#8, port C1

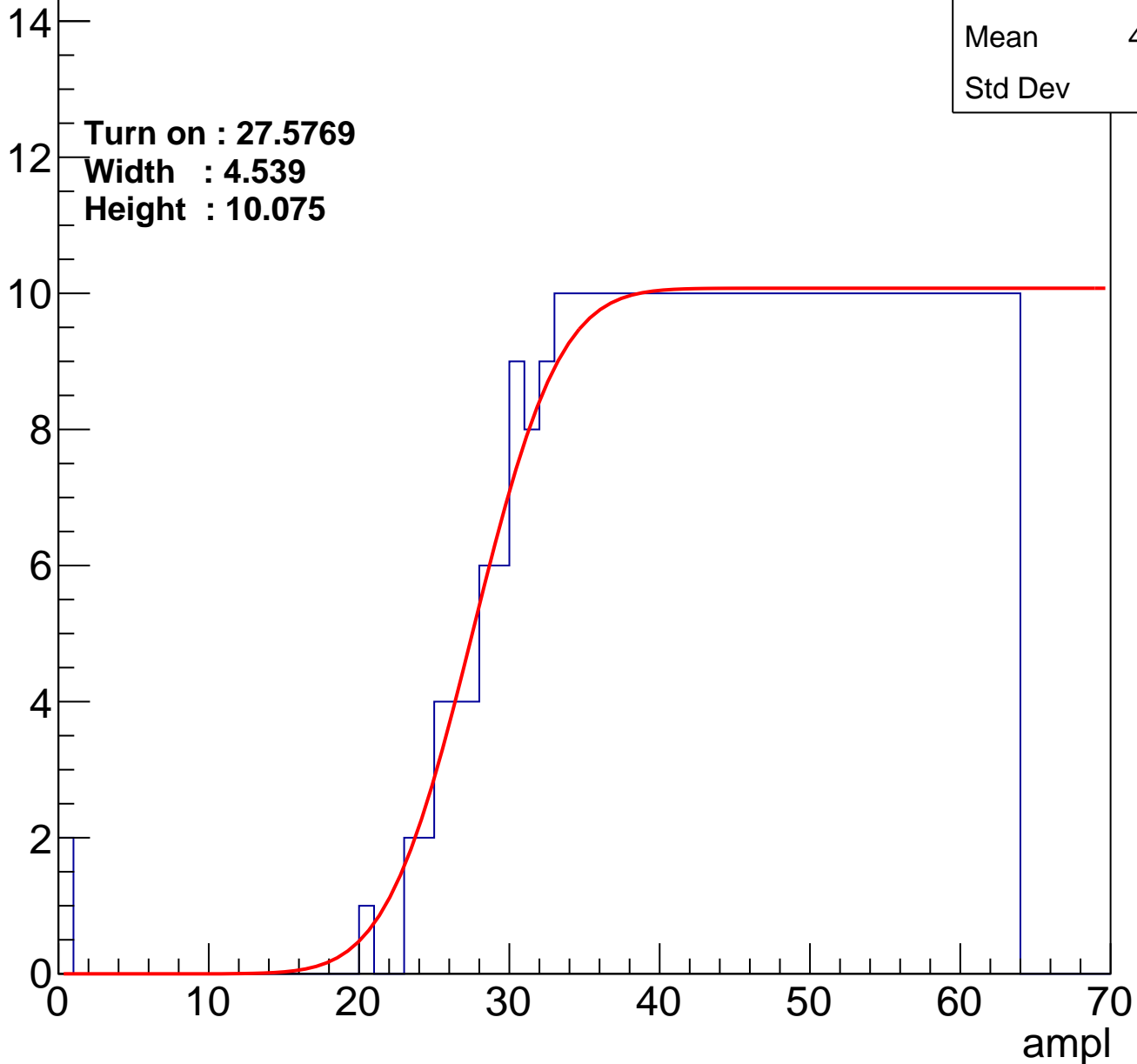
Entries	367
Mean	44.83
Std Dev	11.3

Turn on : 27.5769

Width : 4.539

Height : 10.075

Entry



B0L002S, U26-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.13
Std Dev	11.24

Turn on : 28.8464

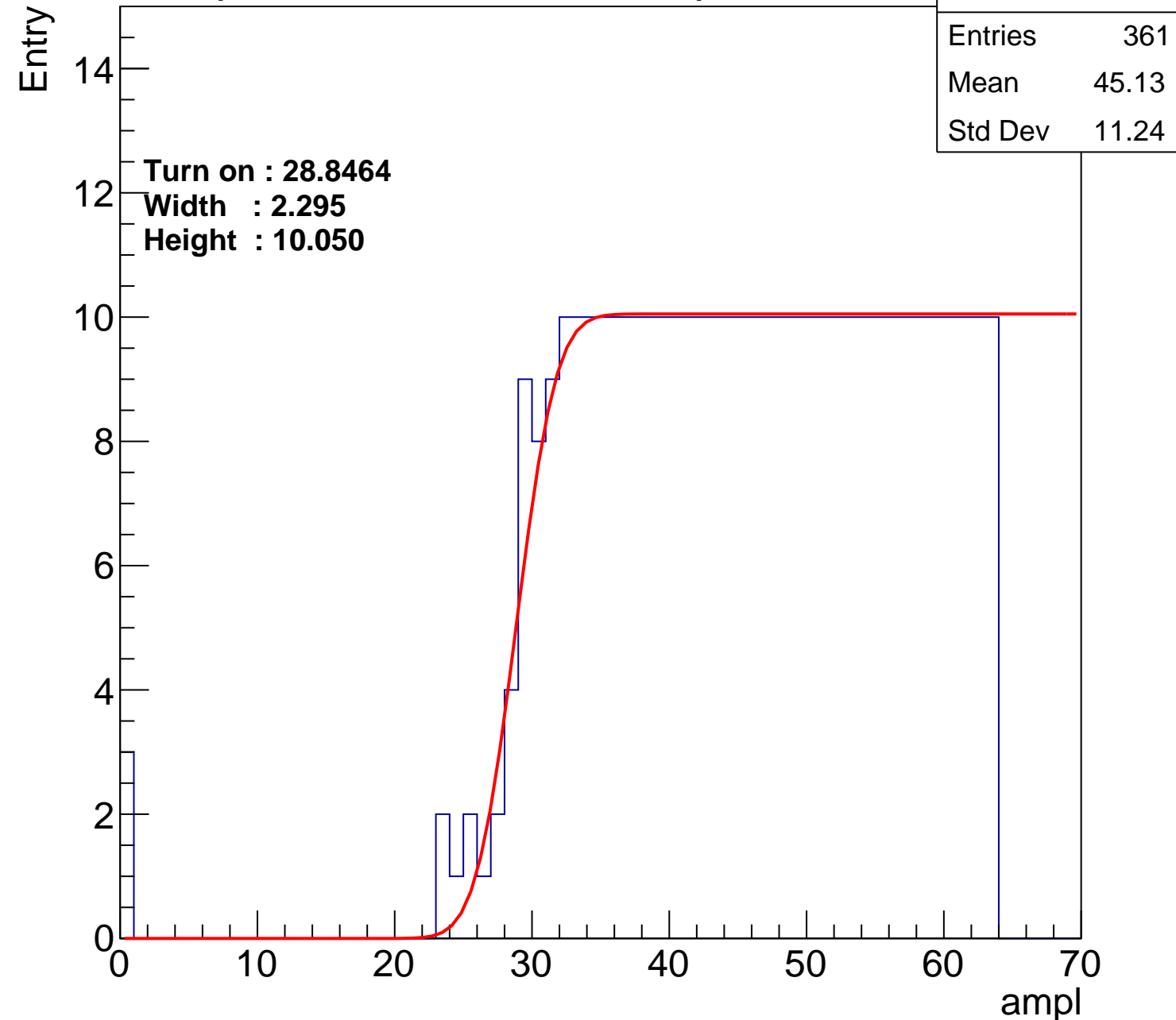
Width : 2.295

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch68

calib_packv5_042523_0143.root, FC#8, port C1

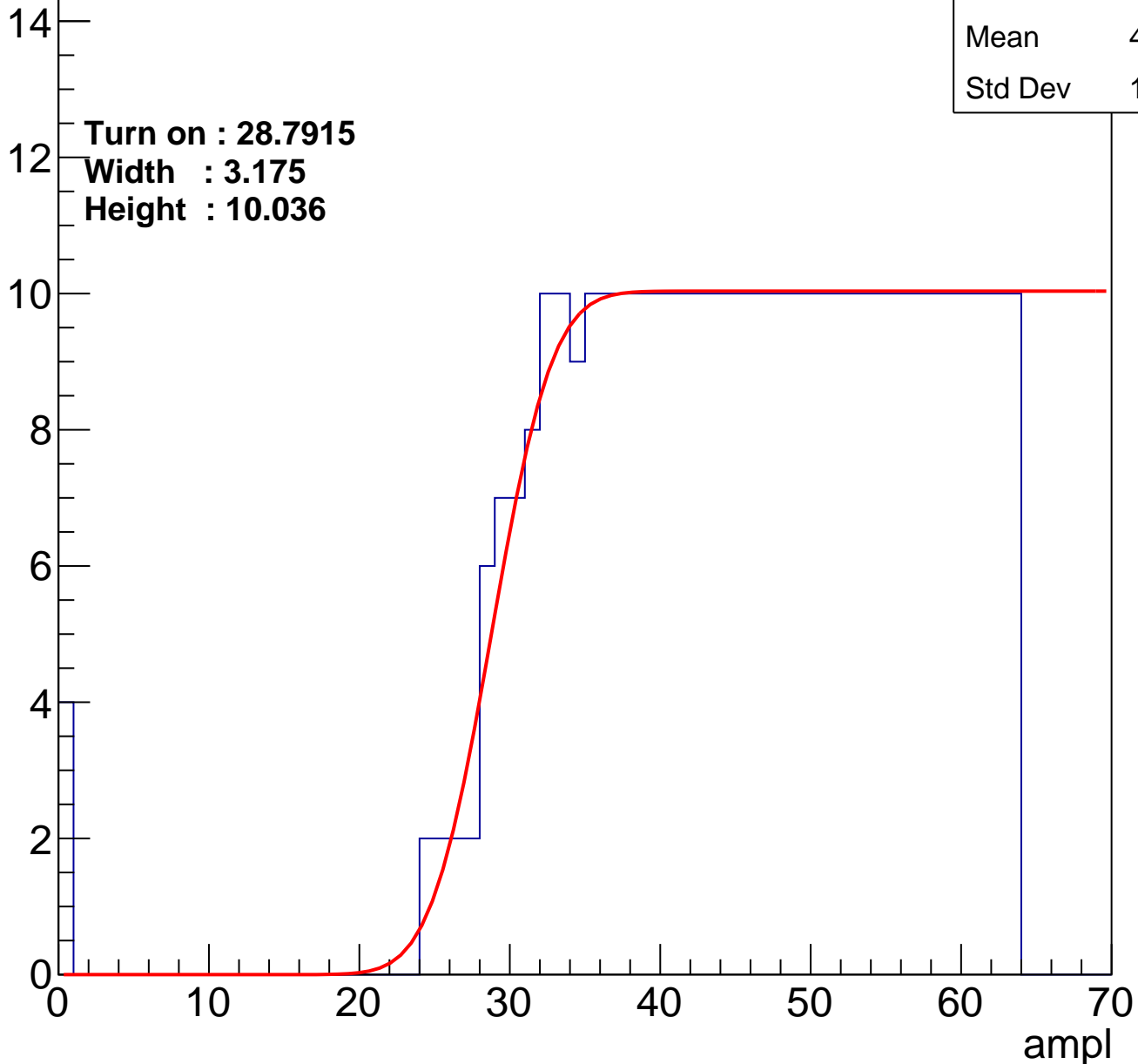
Entries	359
Mean	45.12
Std Dev	11.44

Turn on : 28.7915

Width : 3.175

Height : 10.036

Entry



B0L002S, U26-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.55
Std Dev	10.69

Turn on : 28.9171

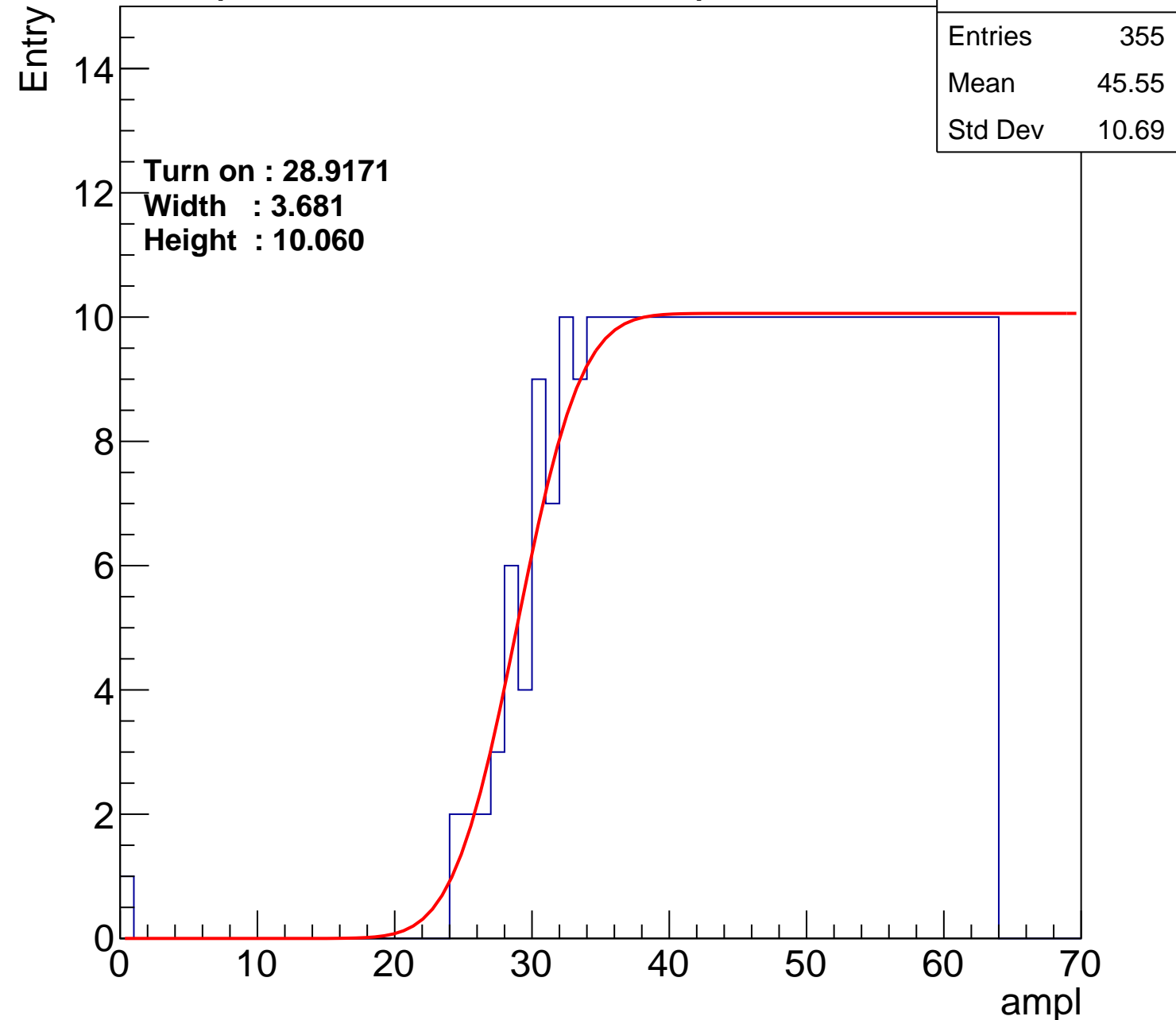
Width : 3.681

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.94
Std Dev	11.13

Turn on : 27.6760

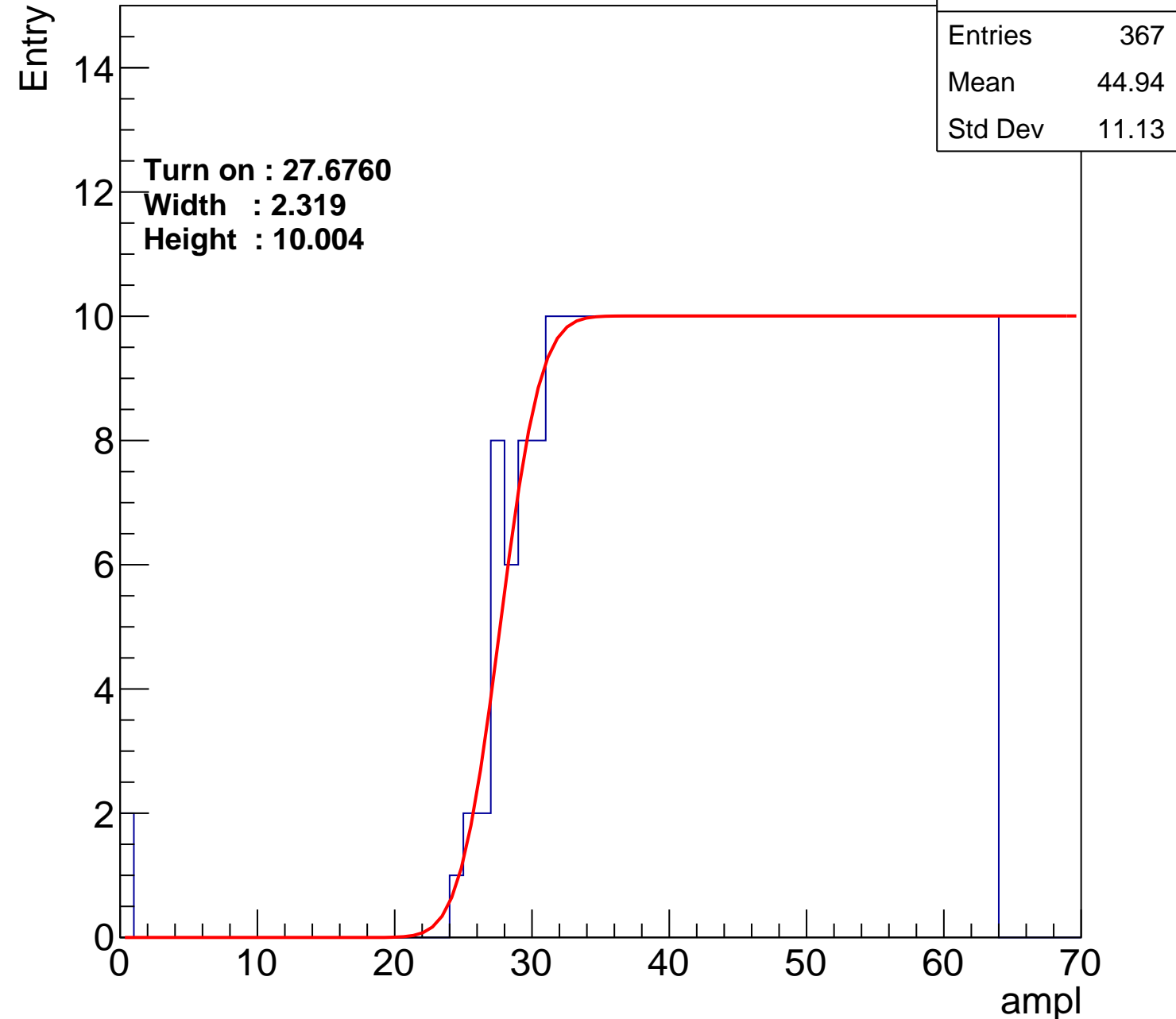
Width : 2.319

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.18
Std Dev	11.63

Turn on : 26.4676

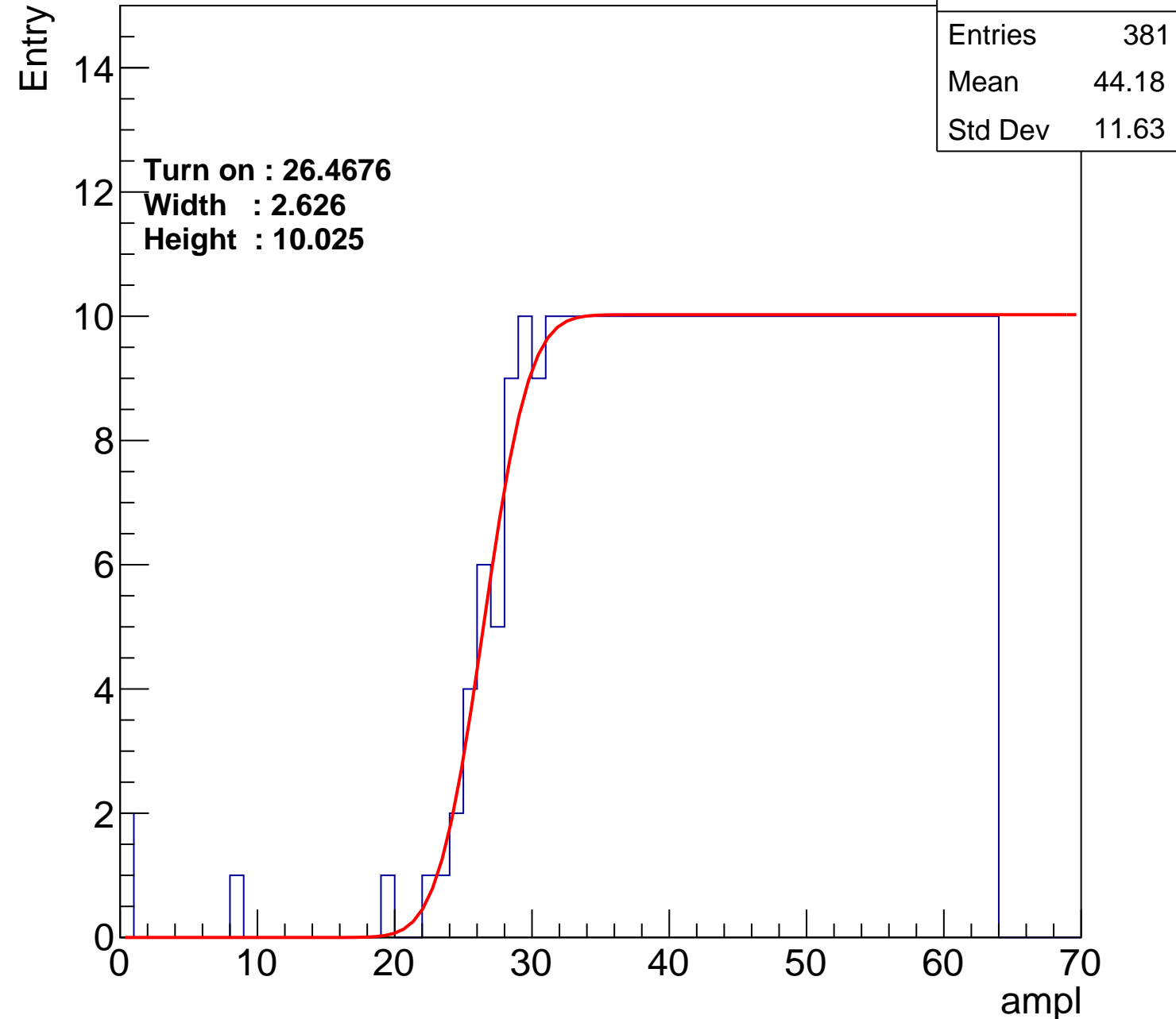
Width : 2.626

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch72

calib_packv5_042523_0143.root, FC#8, port C1

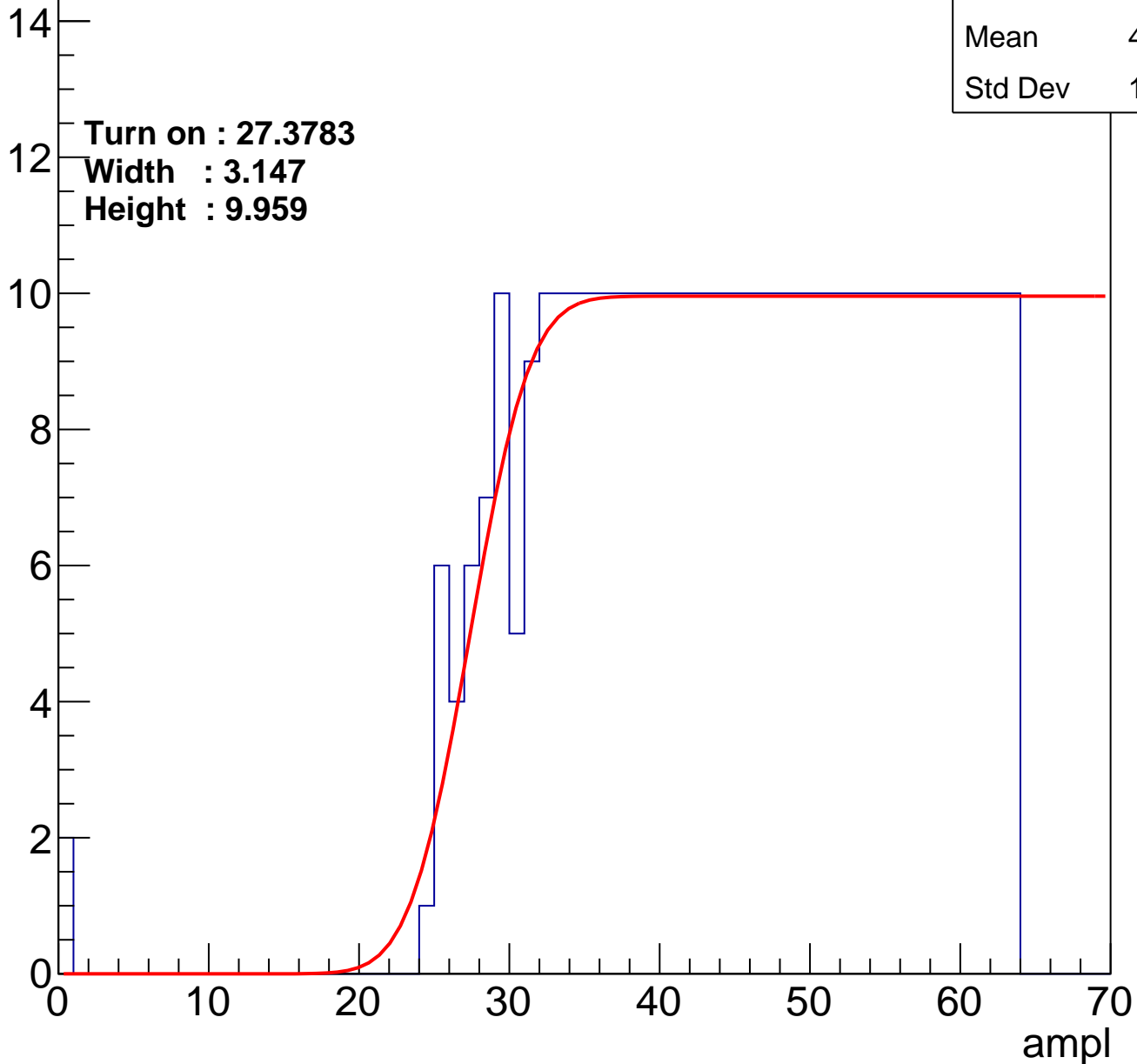
Entries	370
Mean	44.74
Std Dev	11.27

Turn on : 27.3783

Width : 3.147

Height : 9.959

Entry



B0L002S, U26-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.55
Std Dev	11.26

Turn on : 29.6308

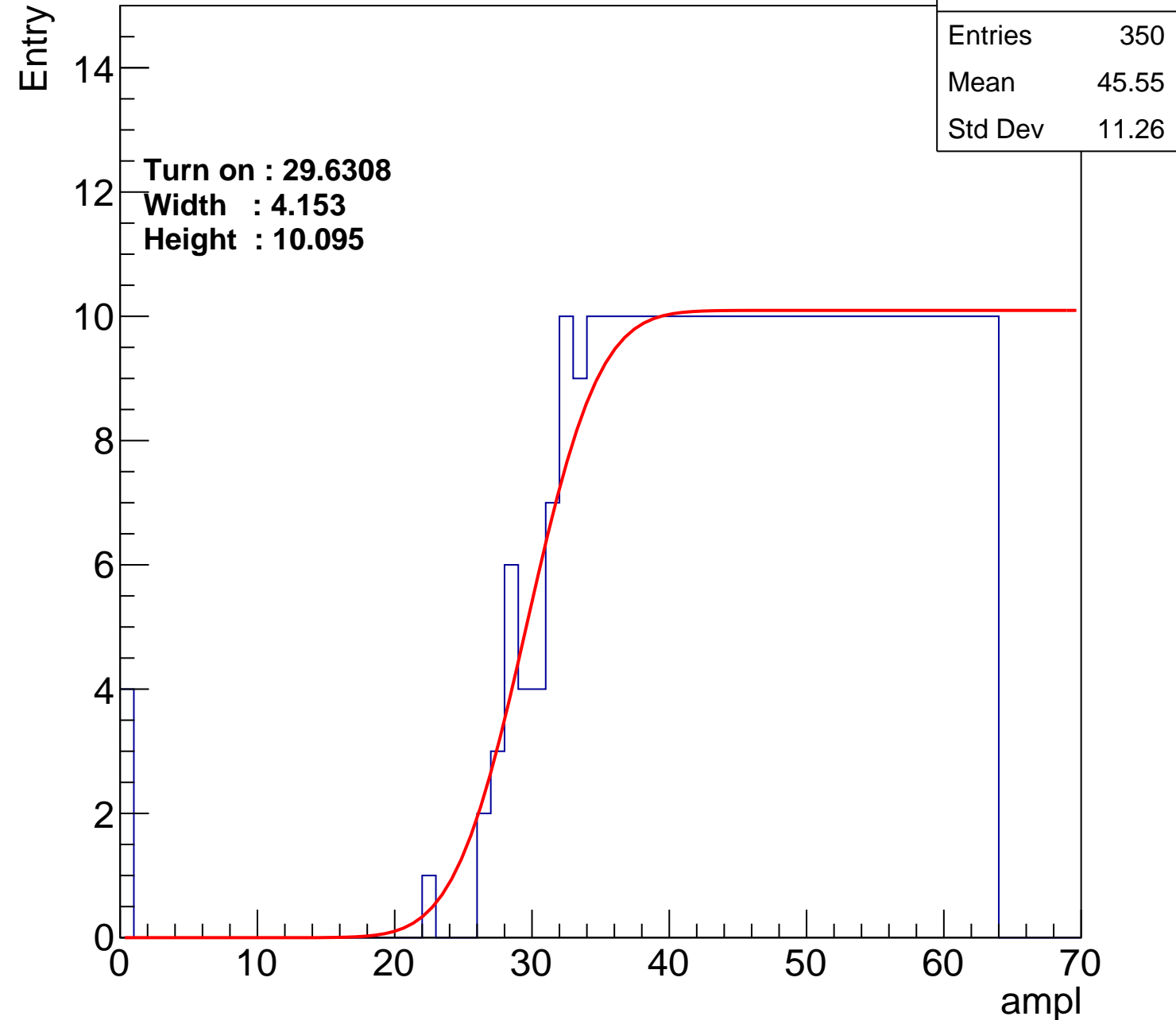
Width : 4.153

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.35
Std Dev	11.74

Turn on : 26.6772

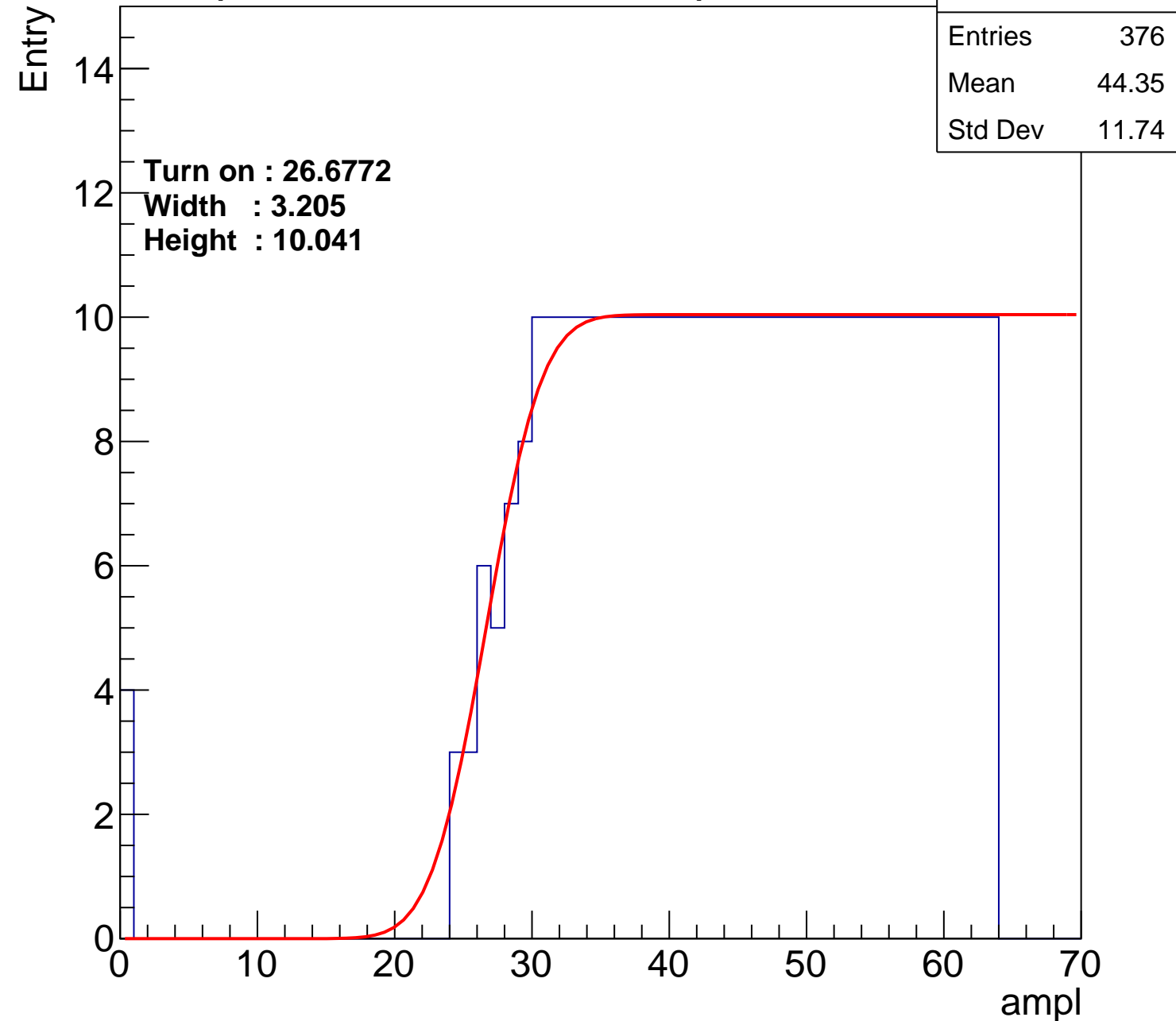
Width : 3.205

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.58
Std Dev	11.53

Turn on : 27.0156

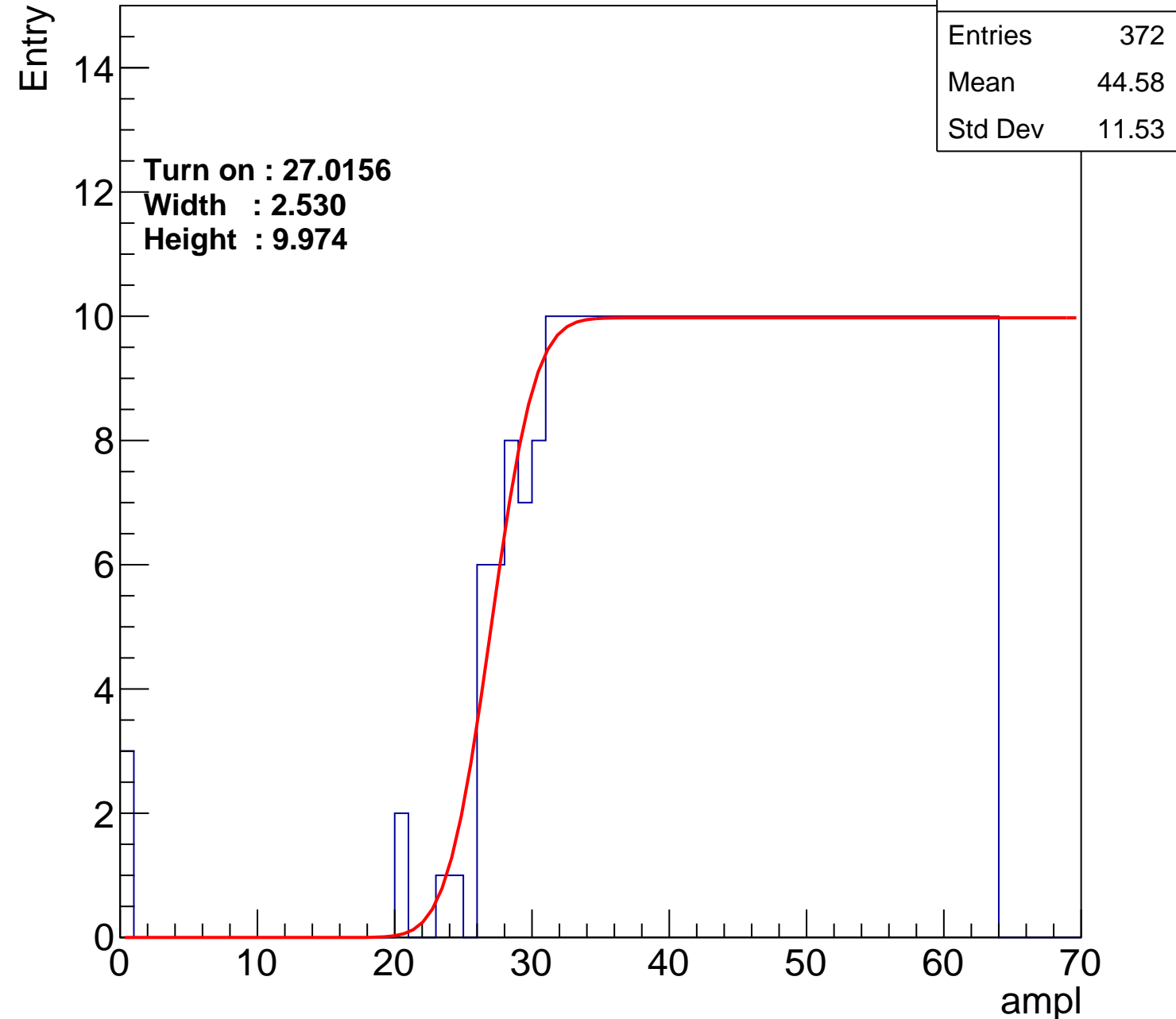
Width : 2.530

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.39
Std Dev	11.91

Turn on : 27.7936

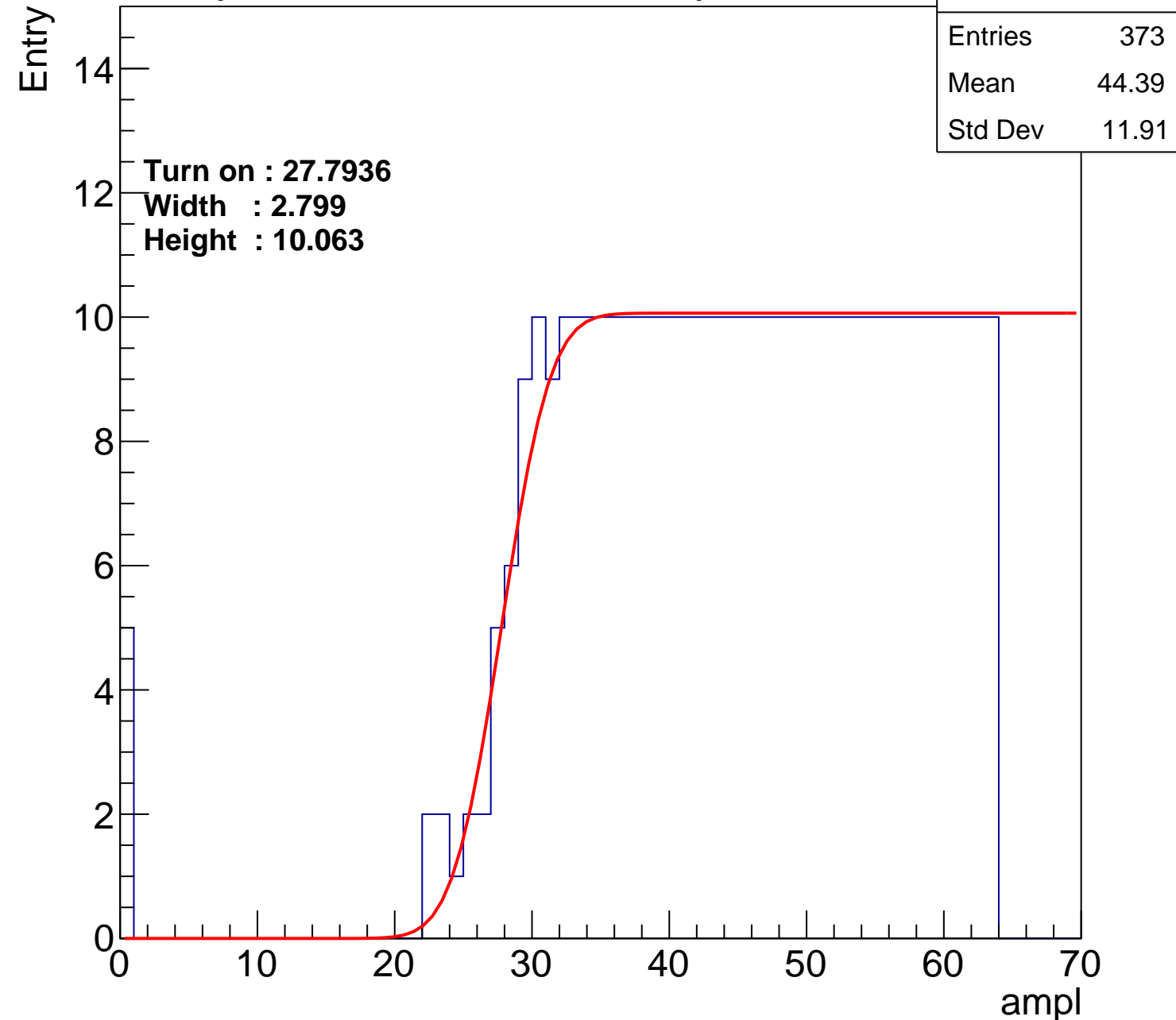
Width : 2.799

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.85
Std Dev	11.98

Turn on : 25.8965

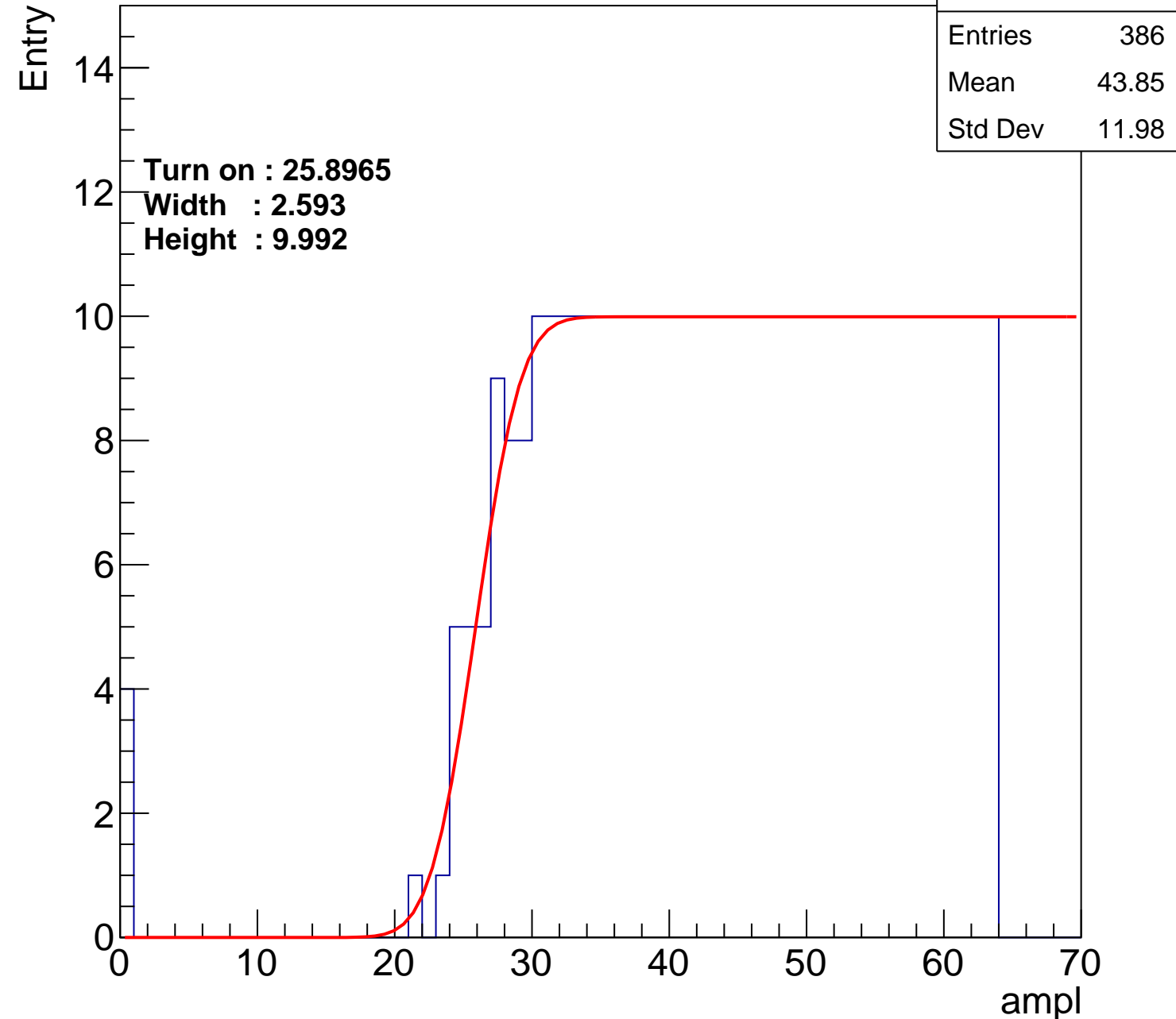
Width : 2.593

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.53
Std Dev	10.67

Turn on : 28.4943

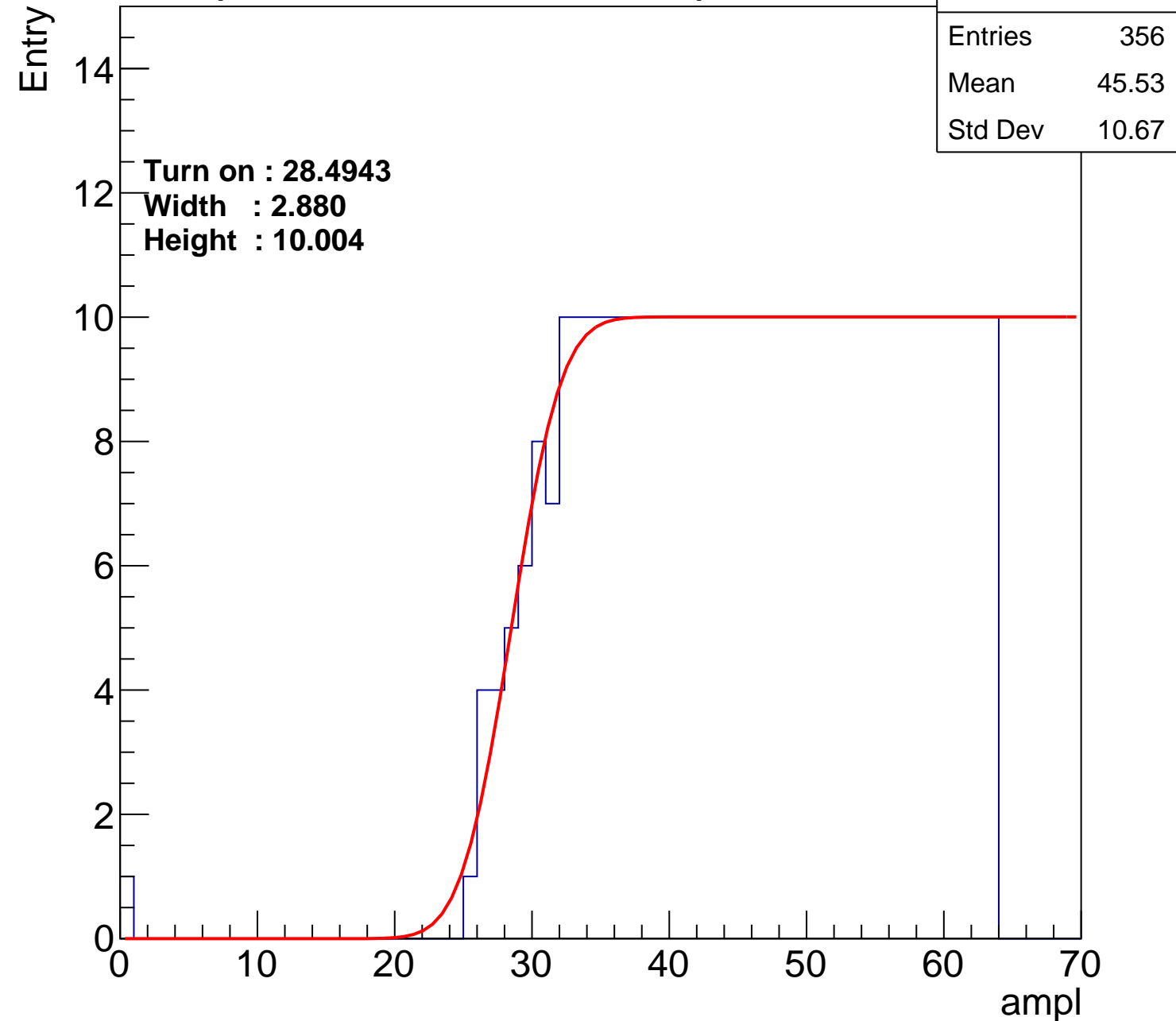
Width : 2.880

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.42
Std Dev	11.31

Turn on : 30.8476

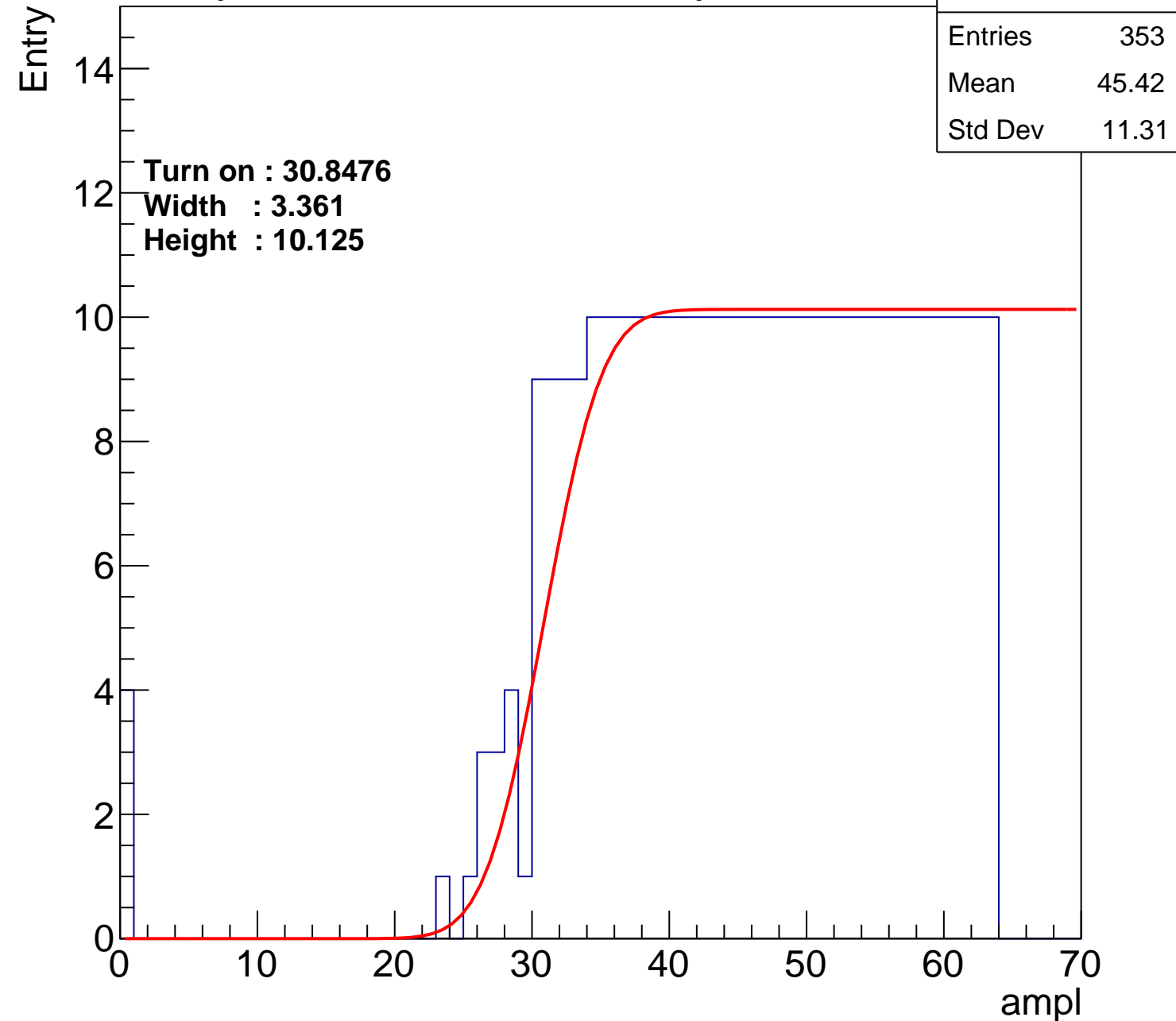
Width : 3.361

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch80

calib_packv5_042523_0143.root, FC#8, port C1

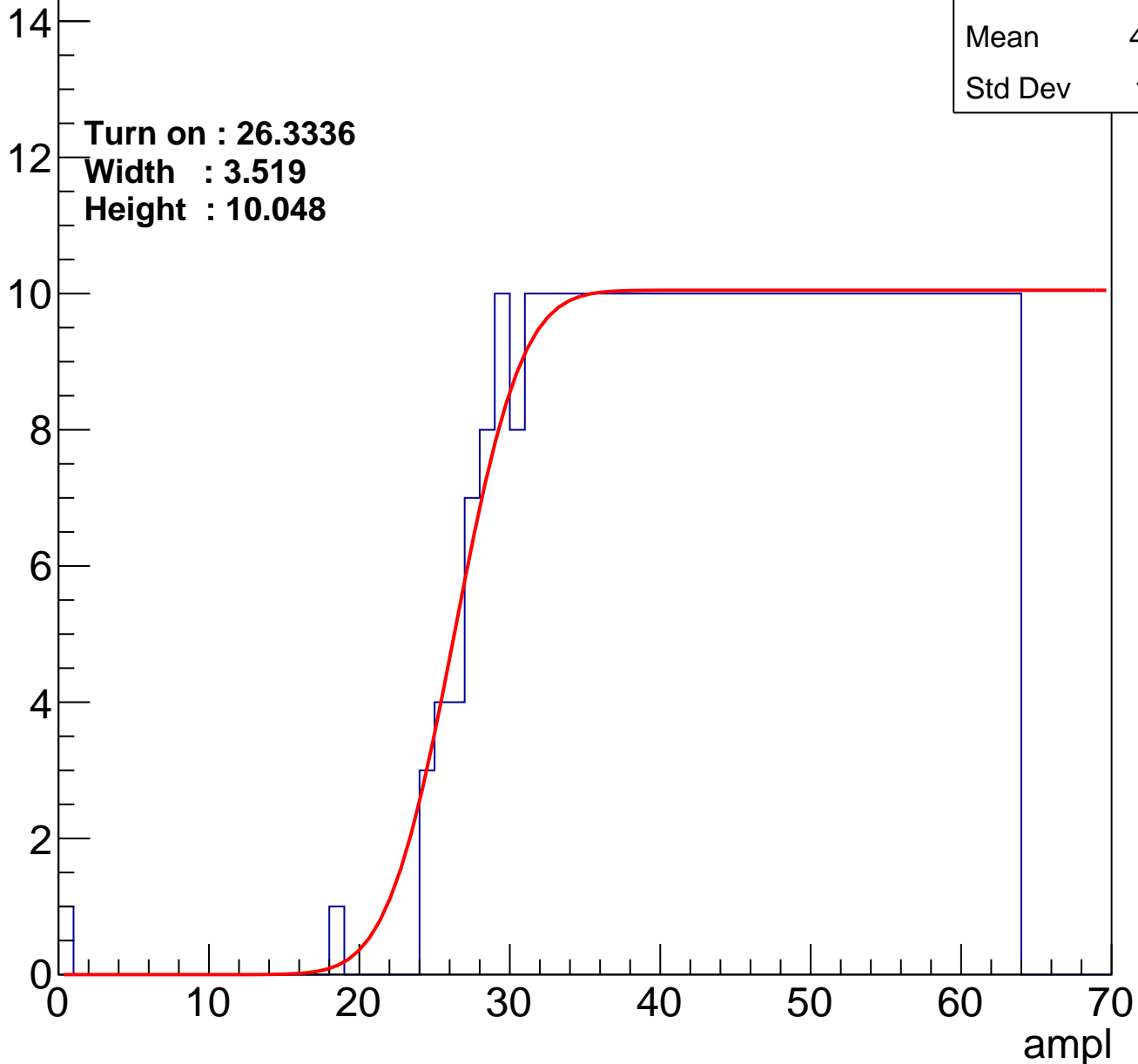
Entries	376
Mean	44.54
Std Dev	11.21

Turn on : 26.3336

Width : 3.519

Height : 10.048

Entry



B0L002S, U26-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.48
Std Dev	10.69

Turn on : 28.6346

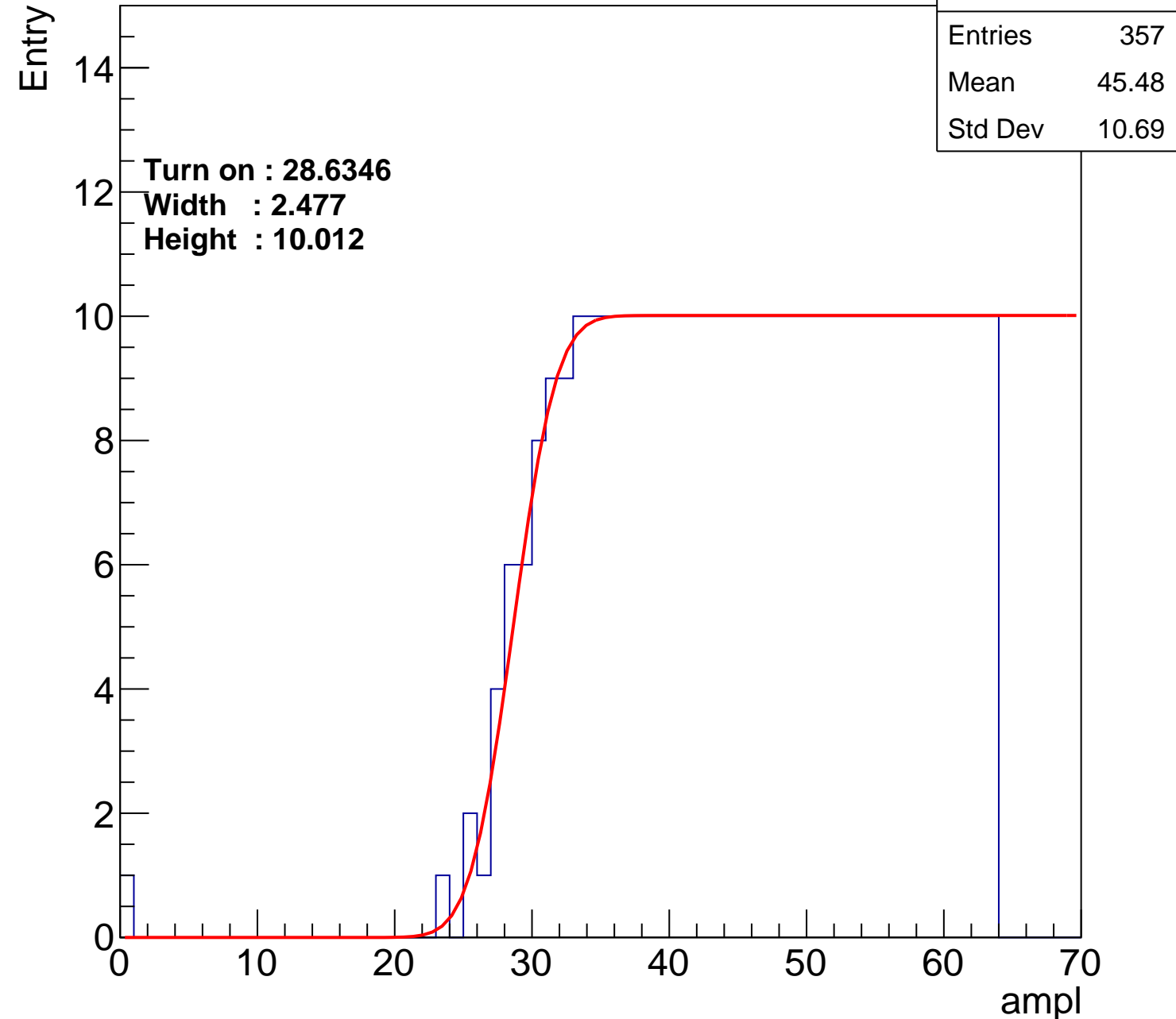
Width : 2.477

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.63
Std Dev	11.49

Turn on : 27.2483

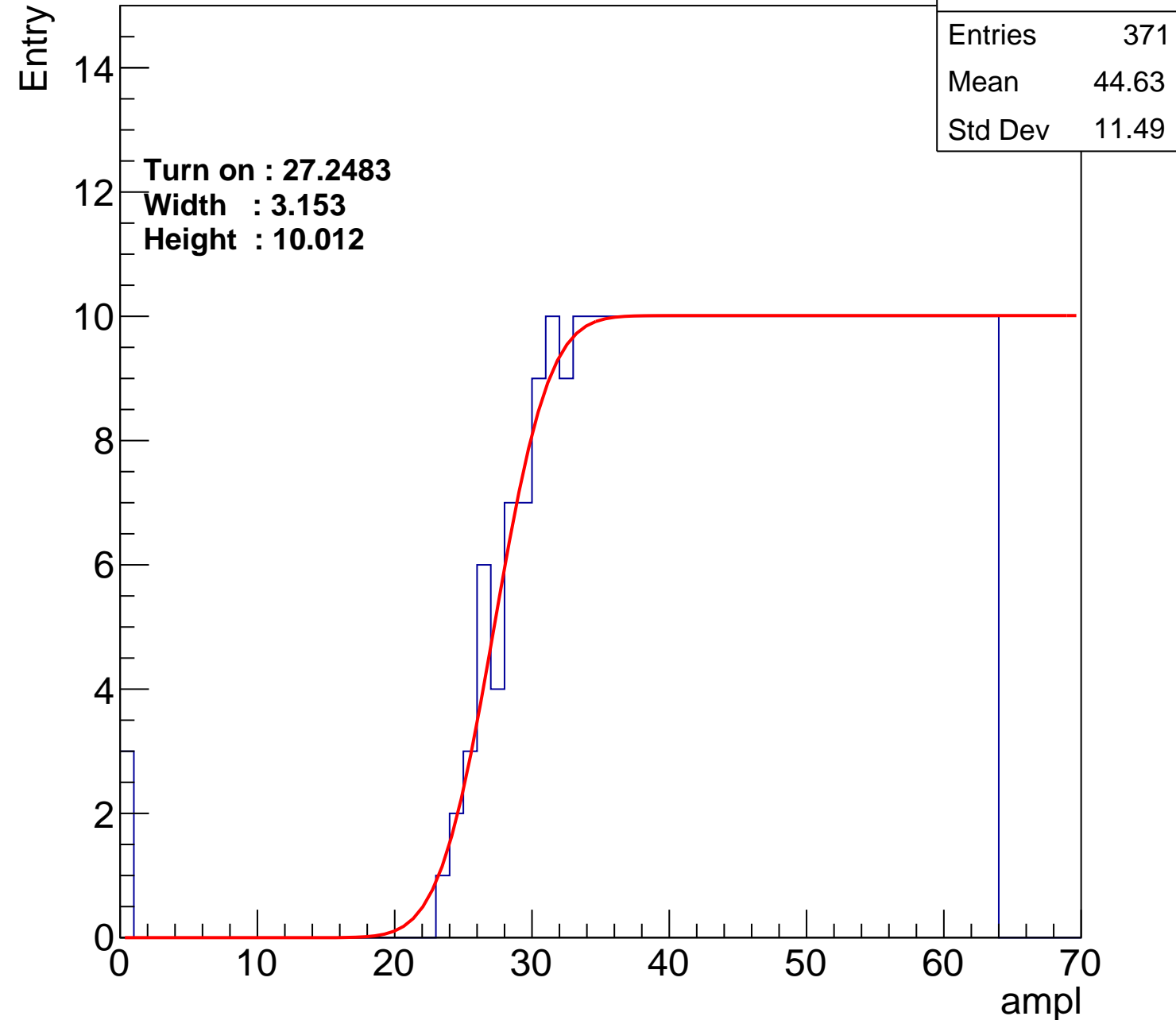
Width : 3.153

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.27
Std Dev	10.27

Turn on : 30.3043

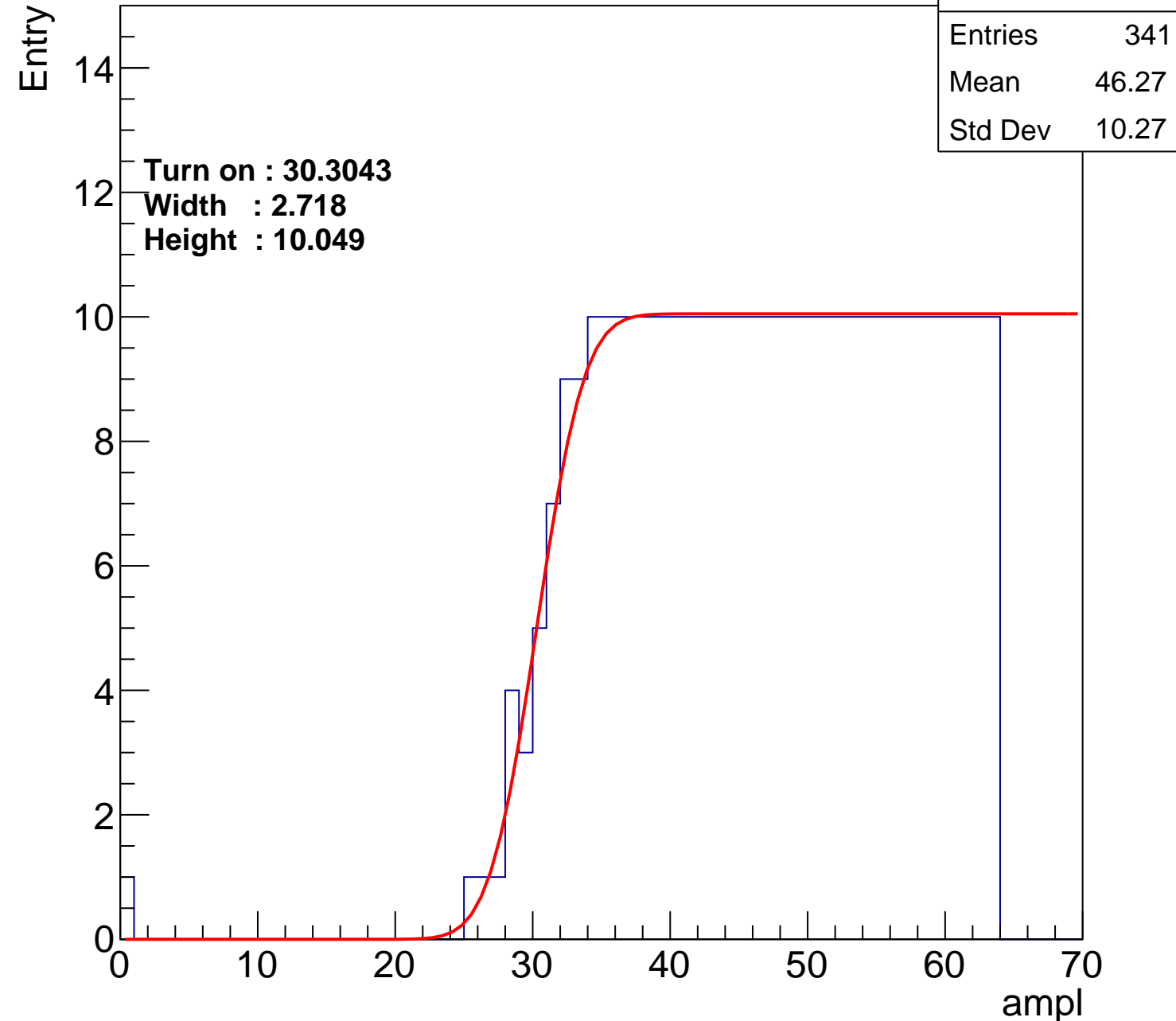
Width : 2.718

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.41
Std Dev	11.07

Turn on : 29.1903

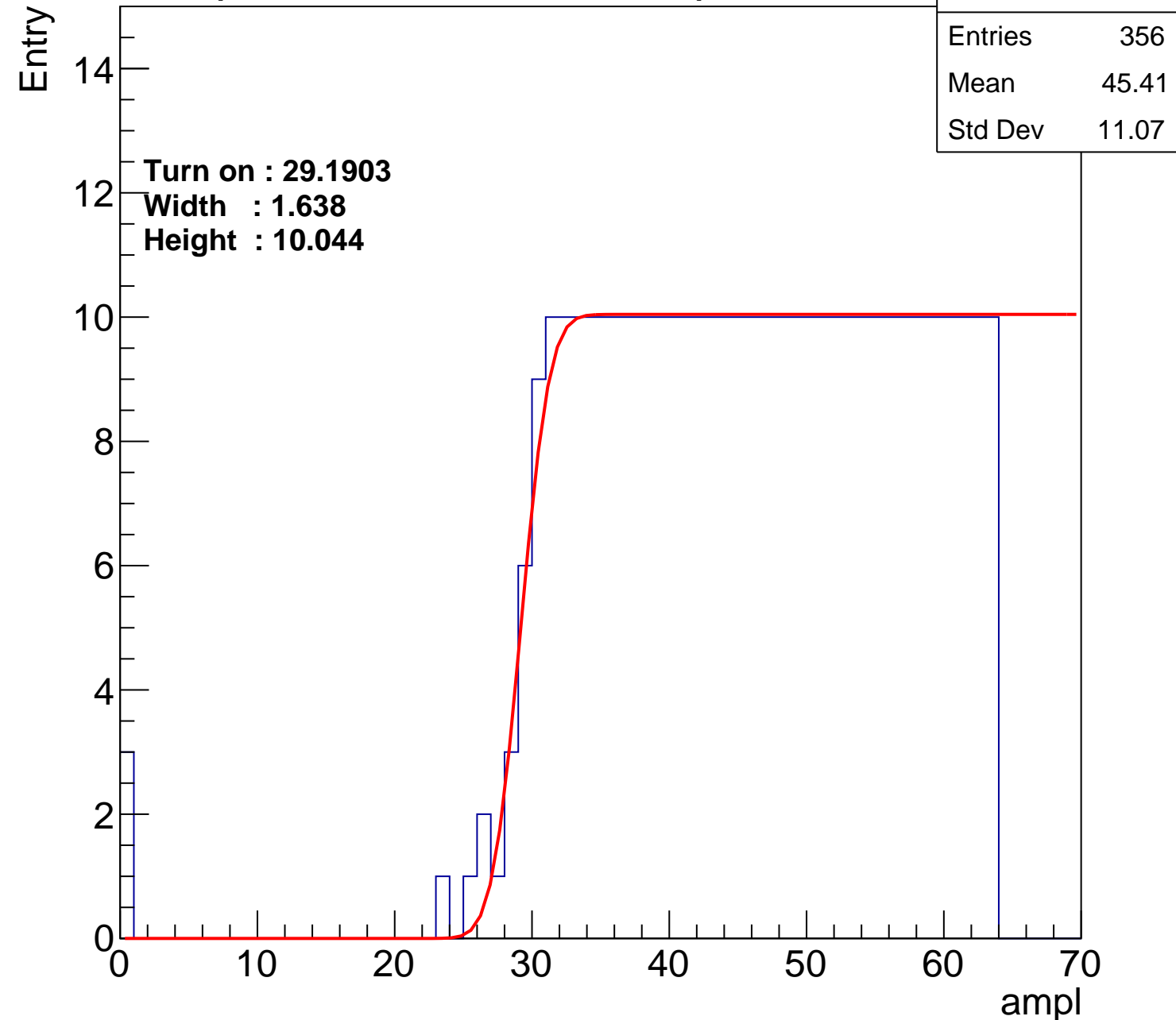
Width : 1.638

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.14
Std Dev	11.41

Turn on : 28.3634

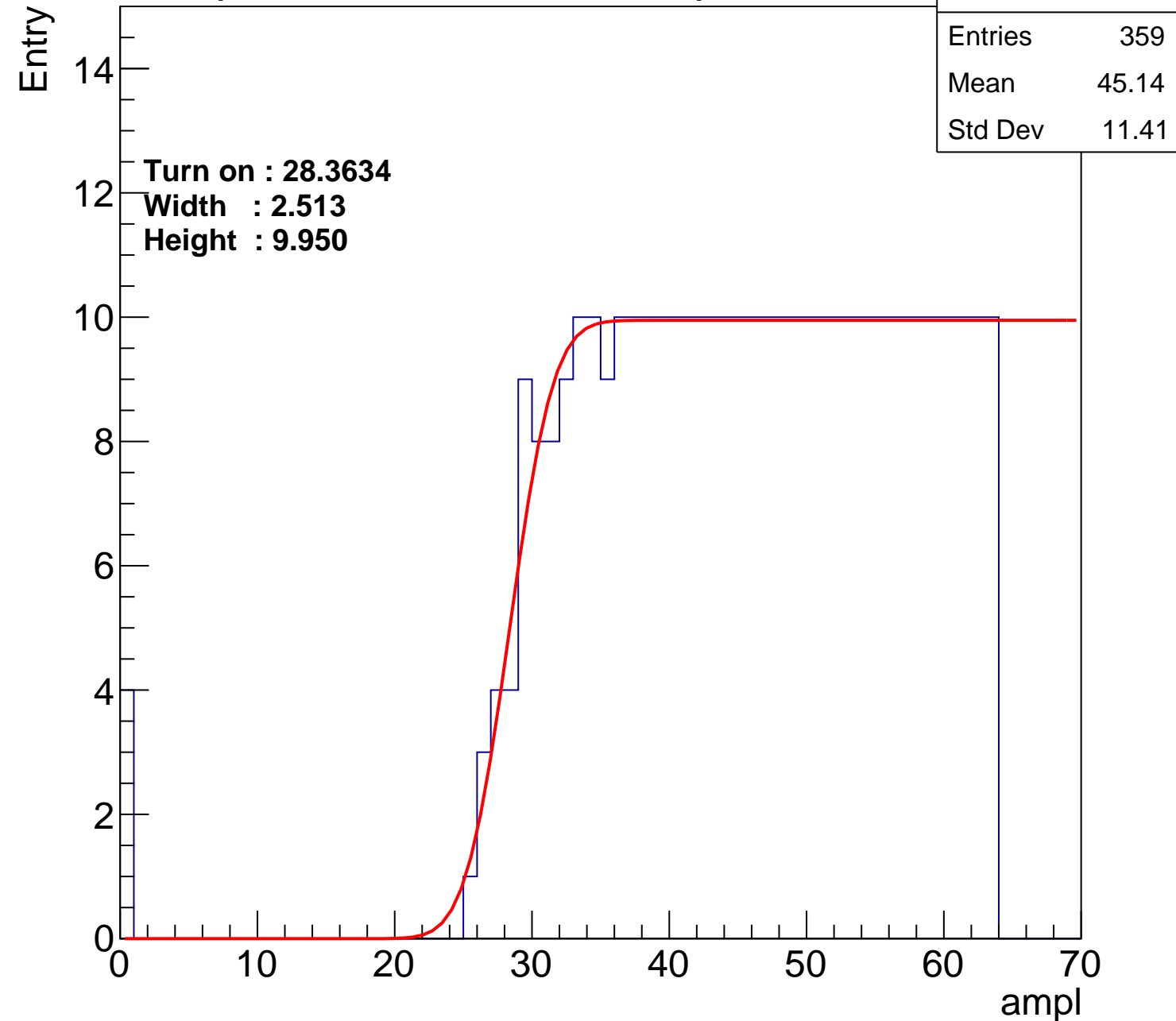
Width : 2.513

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch86

calib_packv5_042523_0143.root, FC#8, port C1

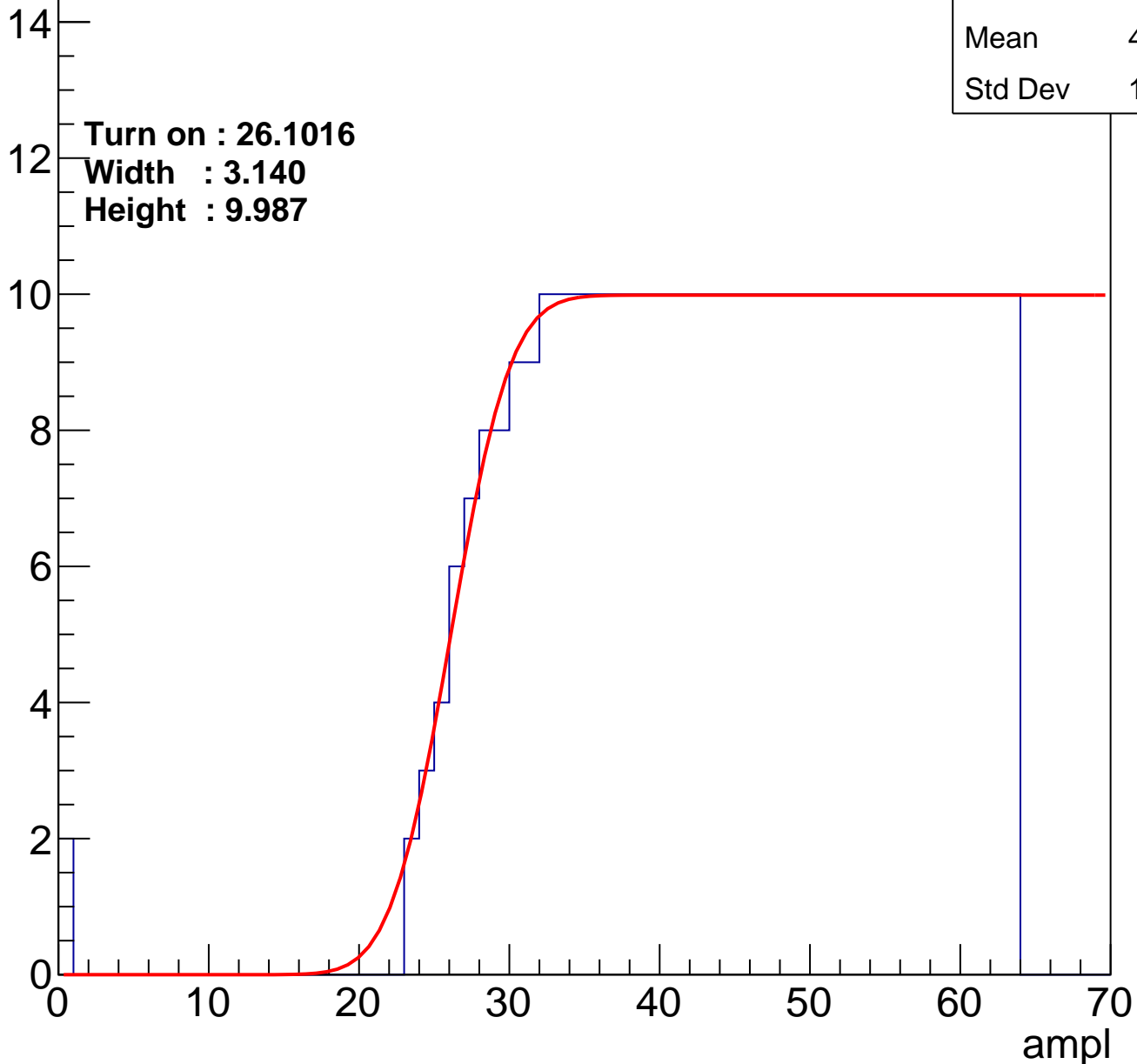
Entries	378
Mean	44.36
Std Dev	11.46

Turn on : 26.1016

Width : 3.140

Height : 9.987

Entry



B0L002S, U26-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.02
Std Dev	11.5

Turn on : 27.9905

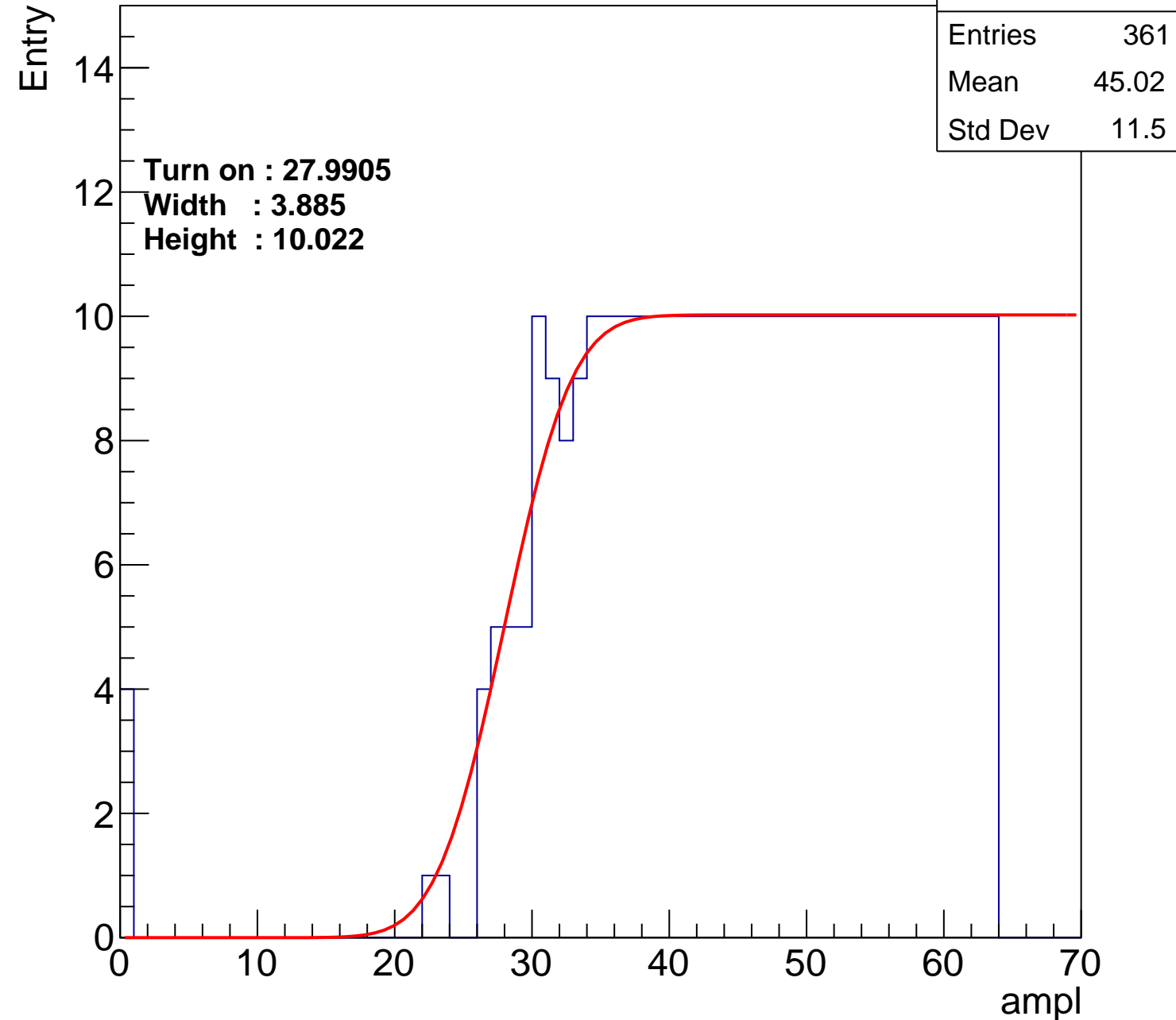
Width : 3.885

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch88

calib_packv5_042523_0143.root, FC#8, port C1

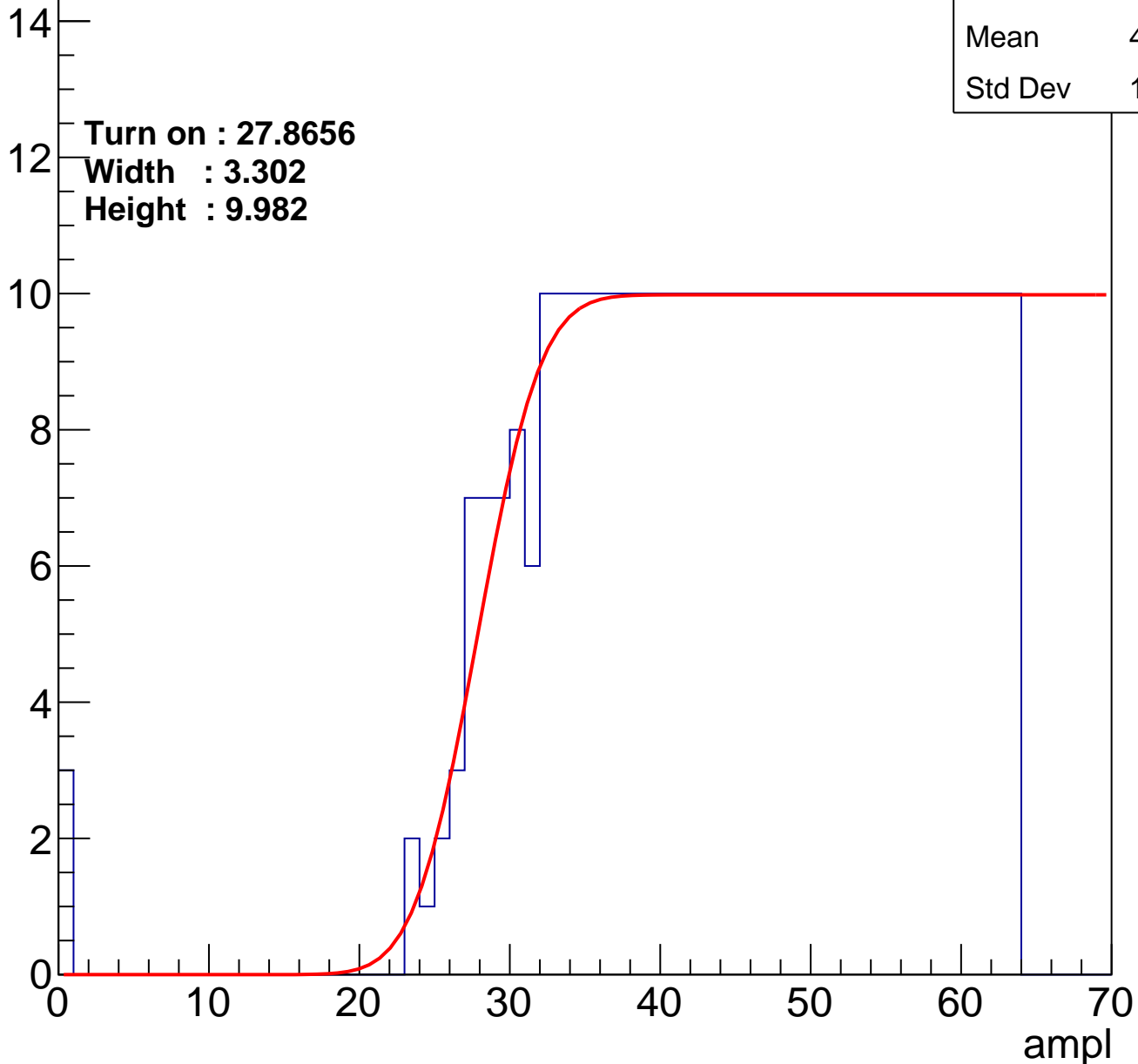
Entries	366
Mean	44.84
Std Dev	11.42

Turn on : 27.8656

Width : 3.302

Height : 9.982

Entry



B0L002S, U26-ch89

calib_packv5_042523_0143.root, FC#8, port C1

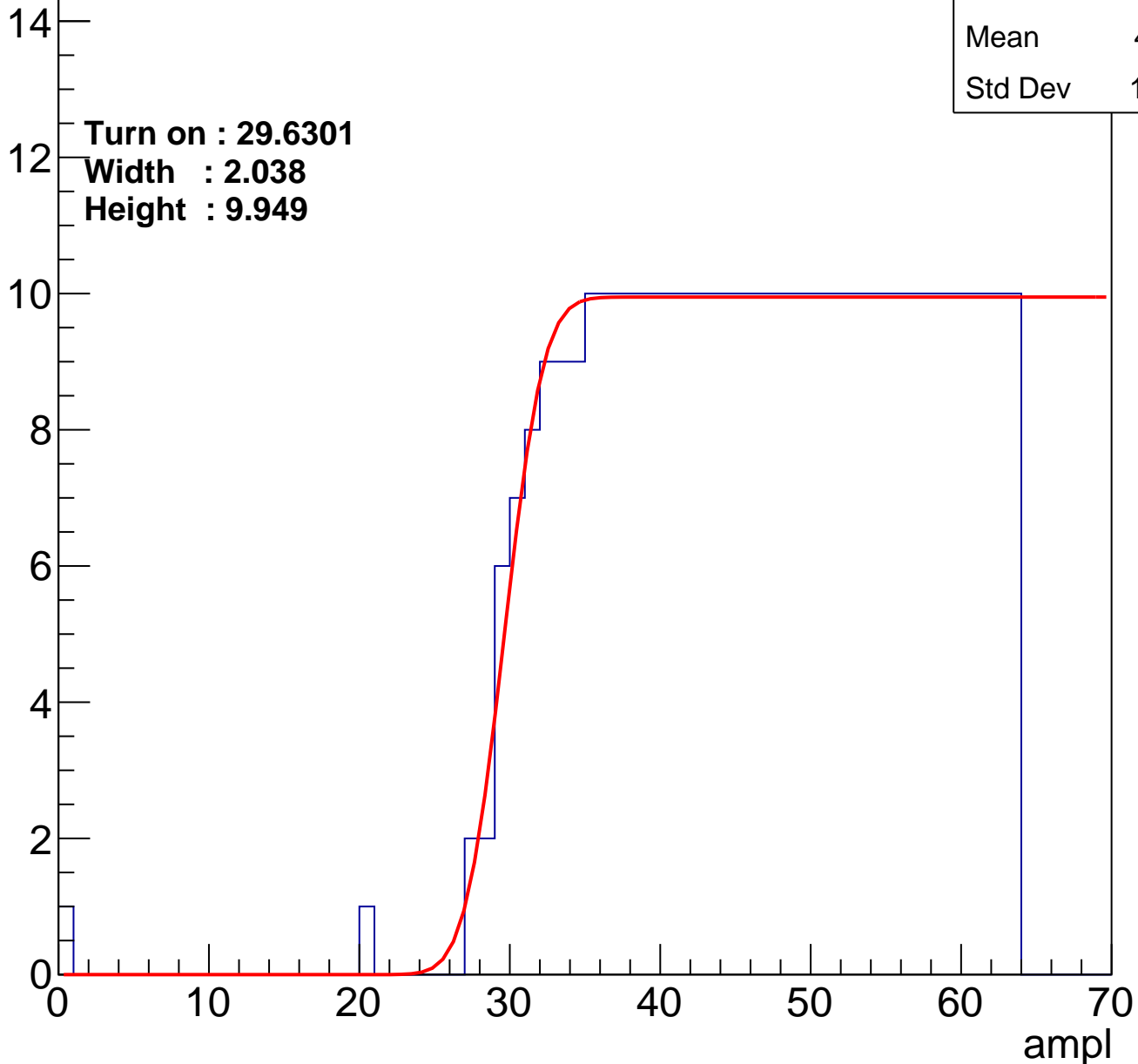
Entries	344
Mean	46.11
Std Dev	10.37

Turn on : 29.6301

Width : 2.038

Height : 9.949

Entry



B0L002S, U26-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.83
Std Dev	11.45

Turn on : 28.3913

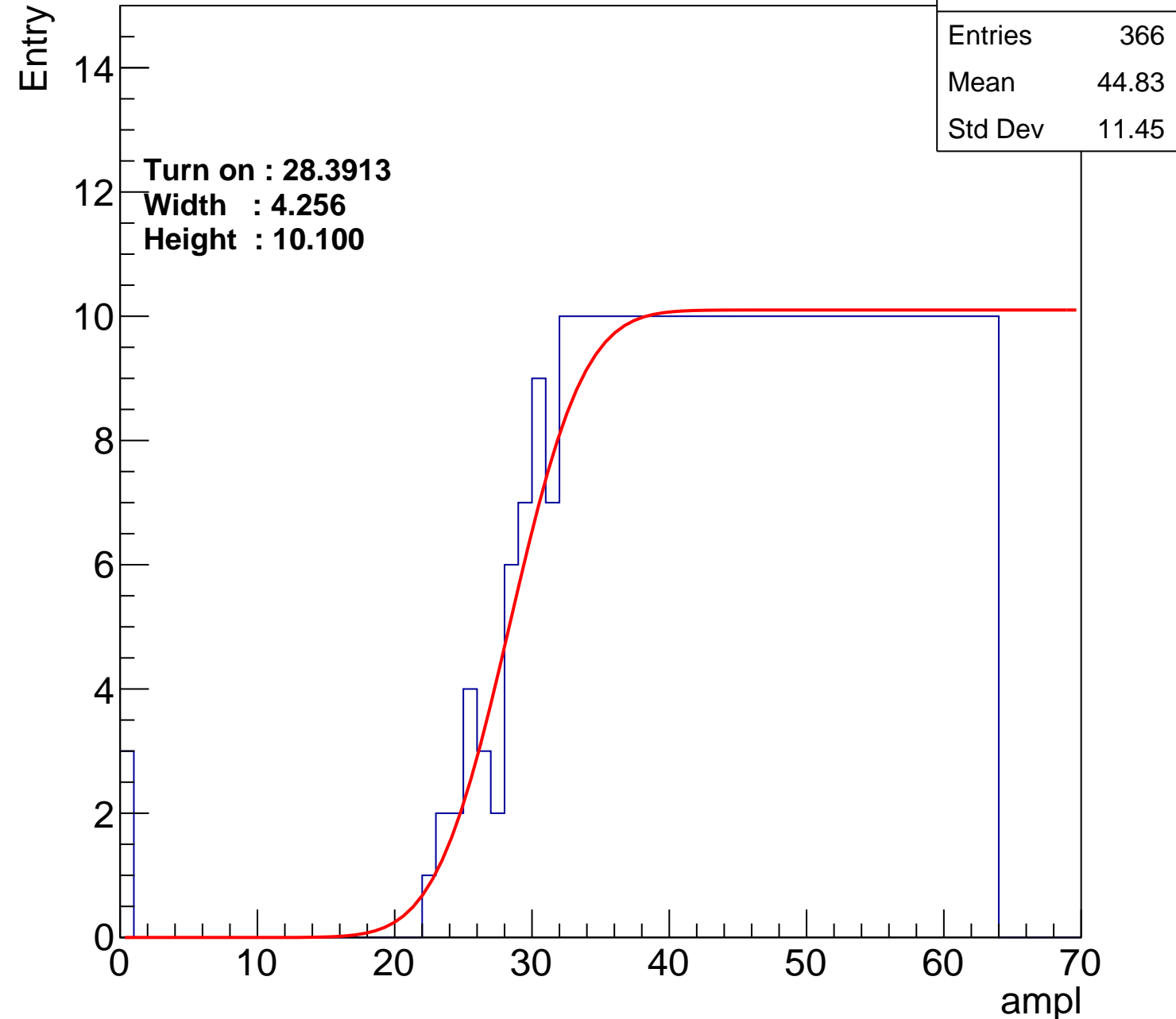
Width : 4.256

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch91

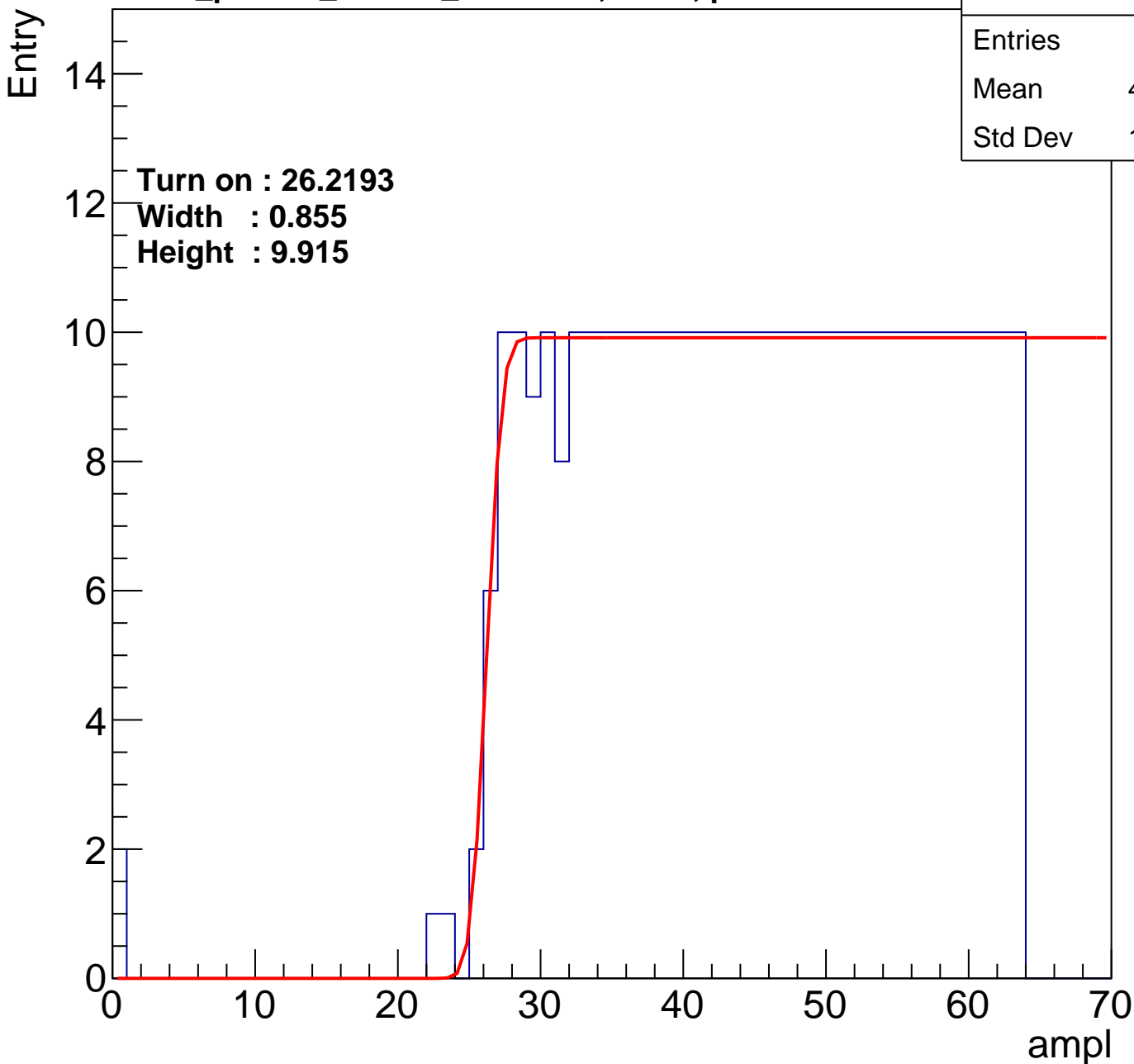
calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.35
Std Dev	11.42

Turn on : 26.2193

Width : 0.855

Height : 9.915



B0L002S, U26-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.7104

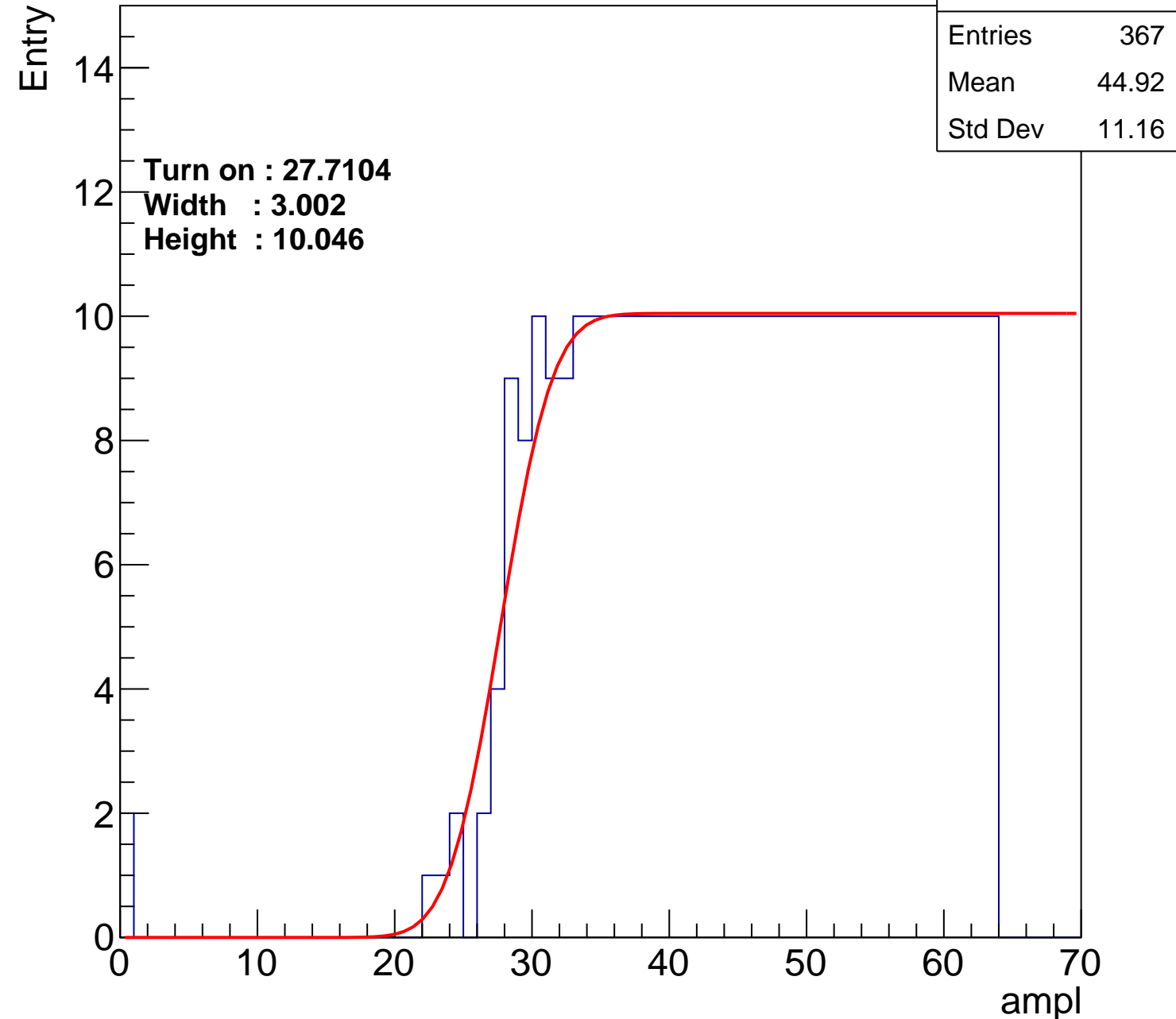
Width : 3.002

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.01
Std Dev	10.98

Turn on : 27.7258

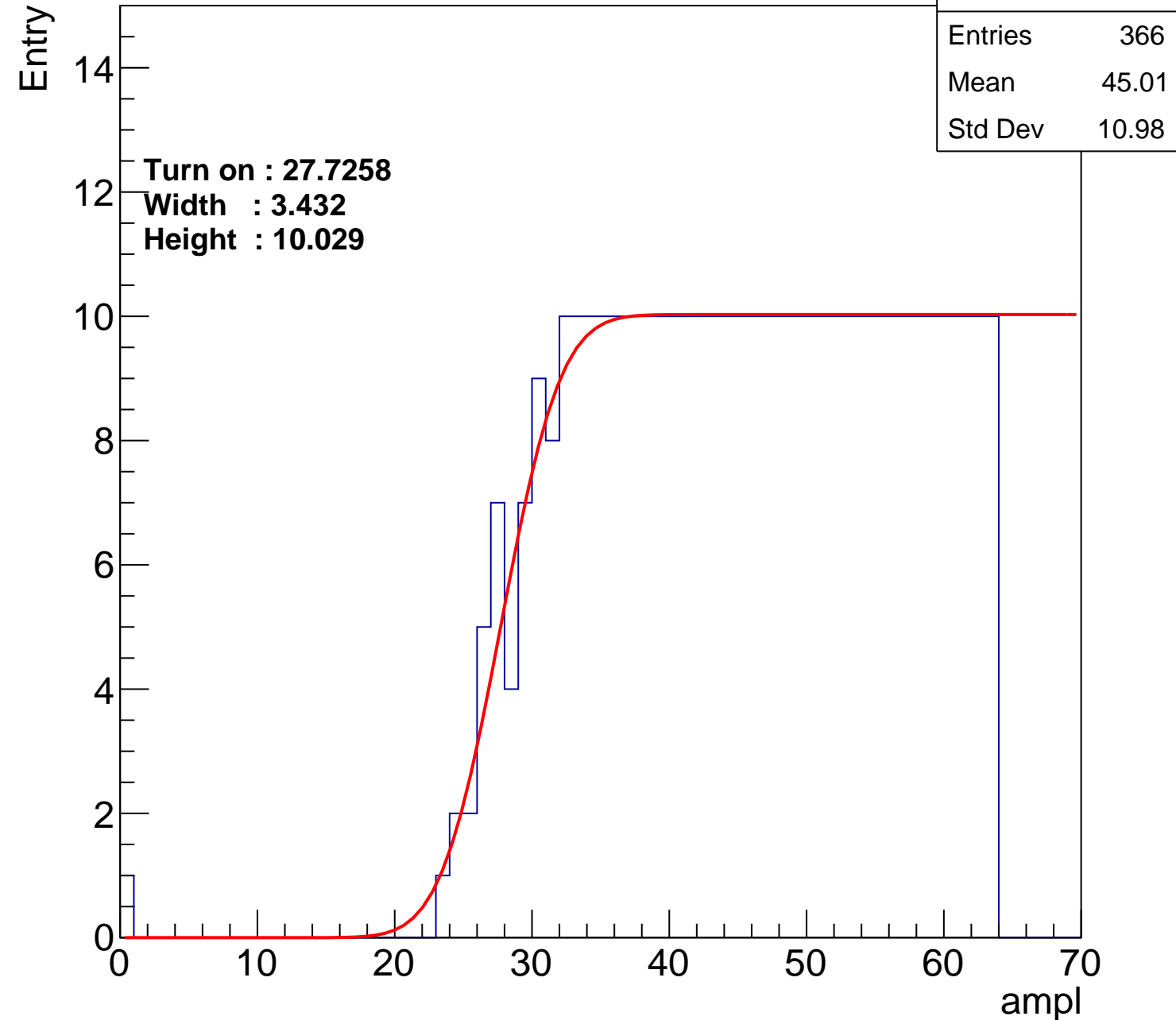
Width : 3.432

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.46
Std Dev	10.76

Turn on : 28.3654

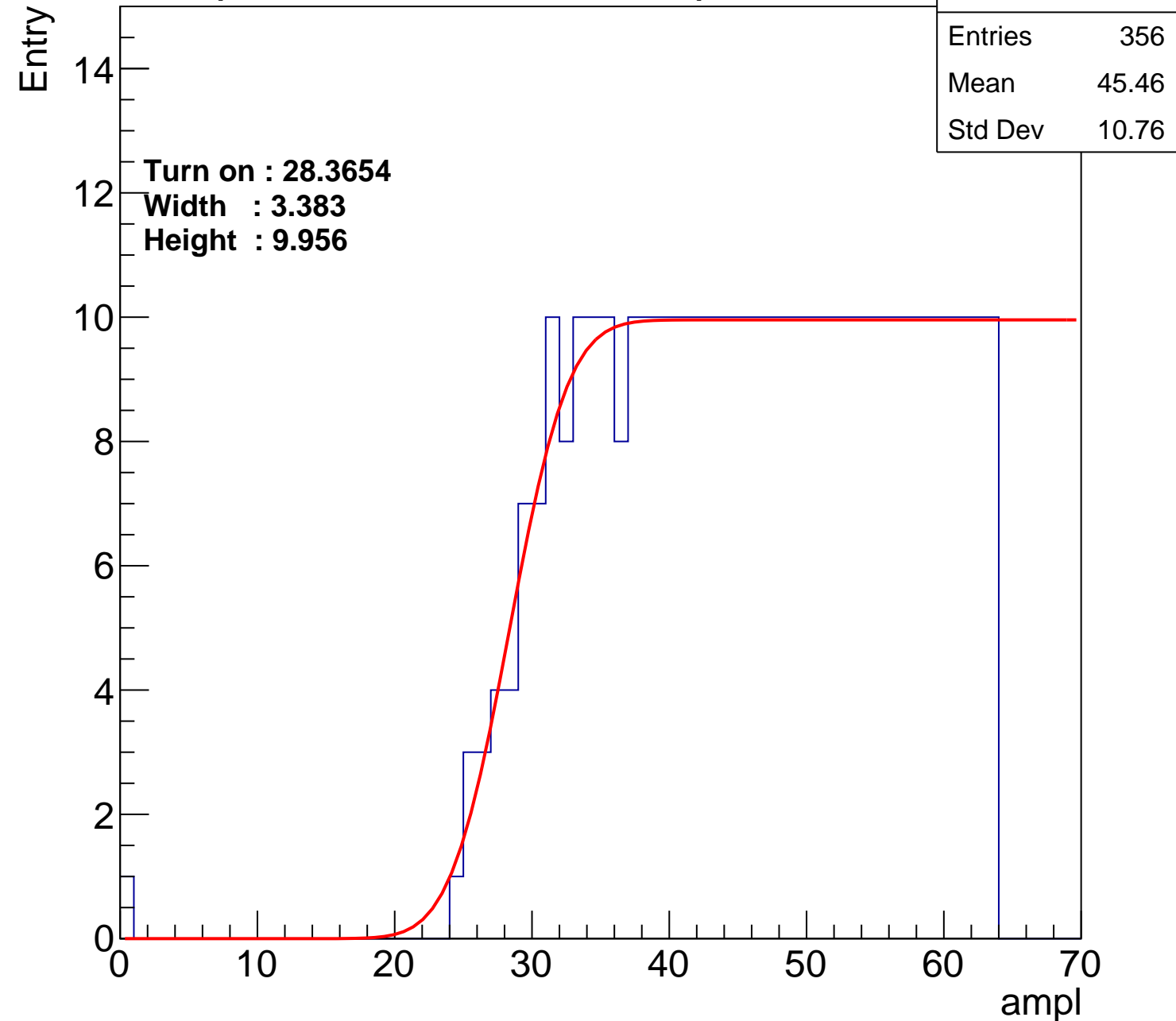
Width : 3.383

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch95

calib_packv5_042523_0143.root, FC#8, port C1

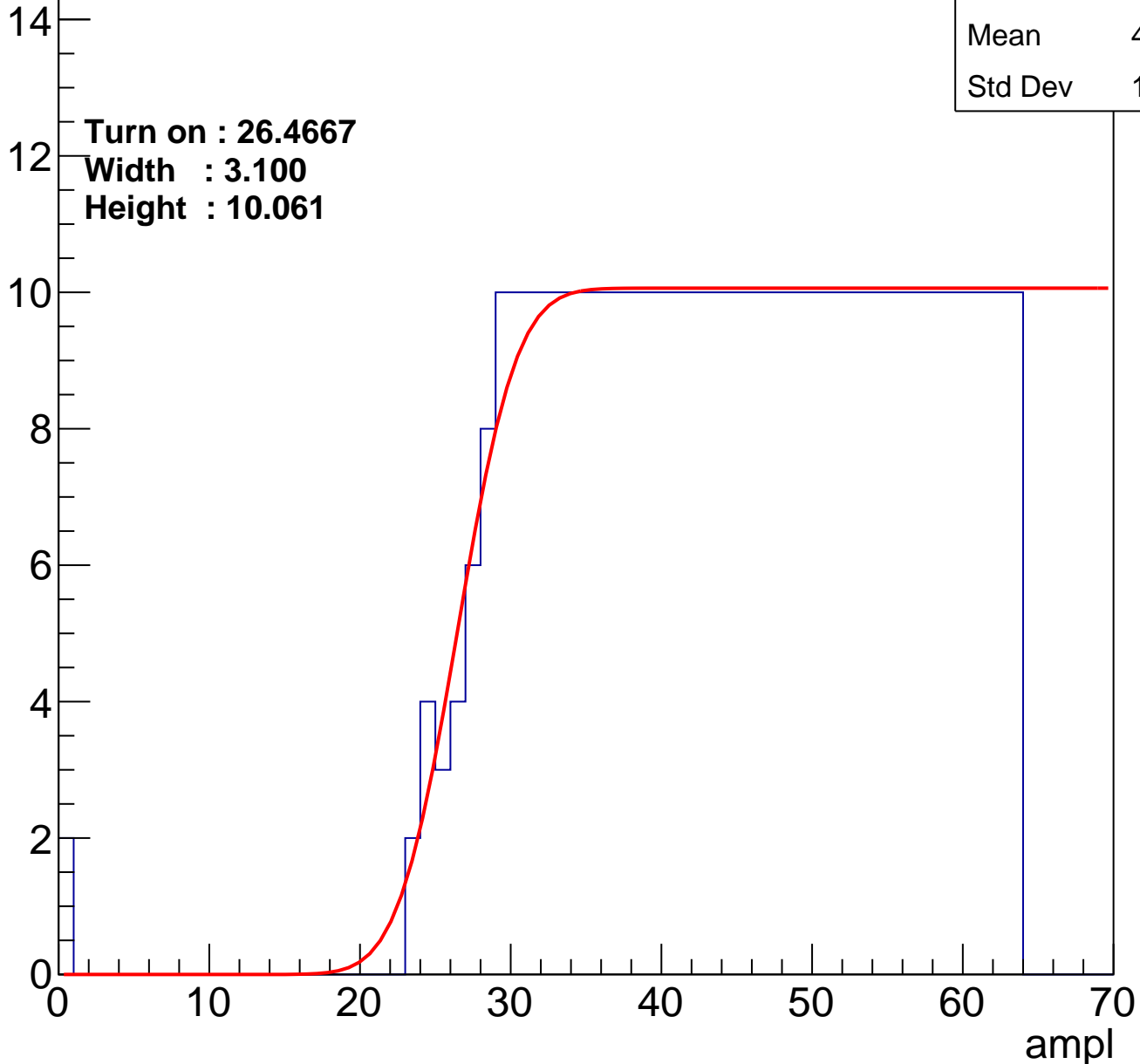
Entries	379
Mean	44.35
Std Dev	11.44

Turn on : 26.4667

Width : 3.100

Height : 10.061

Entry



B0L002S, U26-ch96

calib_packv5_042523_0143.root, FC#8, port C1

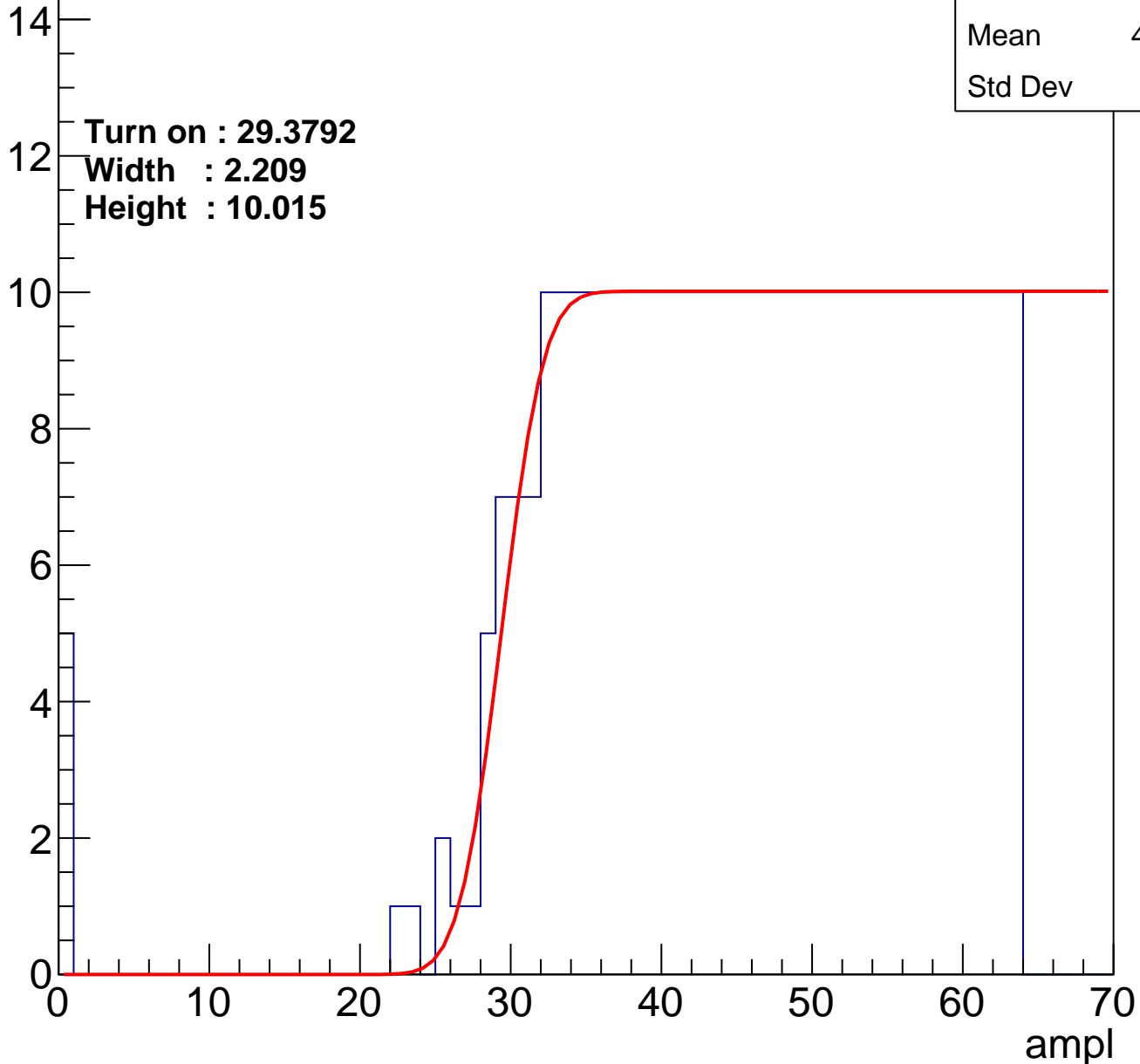
Entries	357
Mean	45.15
Std Dev	11.6

Turn on : 29.3792

Width : 2.209

Height : 10.015

Entry



B0L002S, U26-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.62
Std Dev	10.63

Turn on : 29.0561

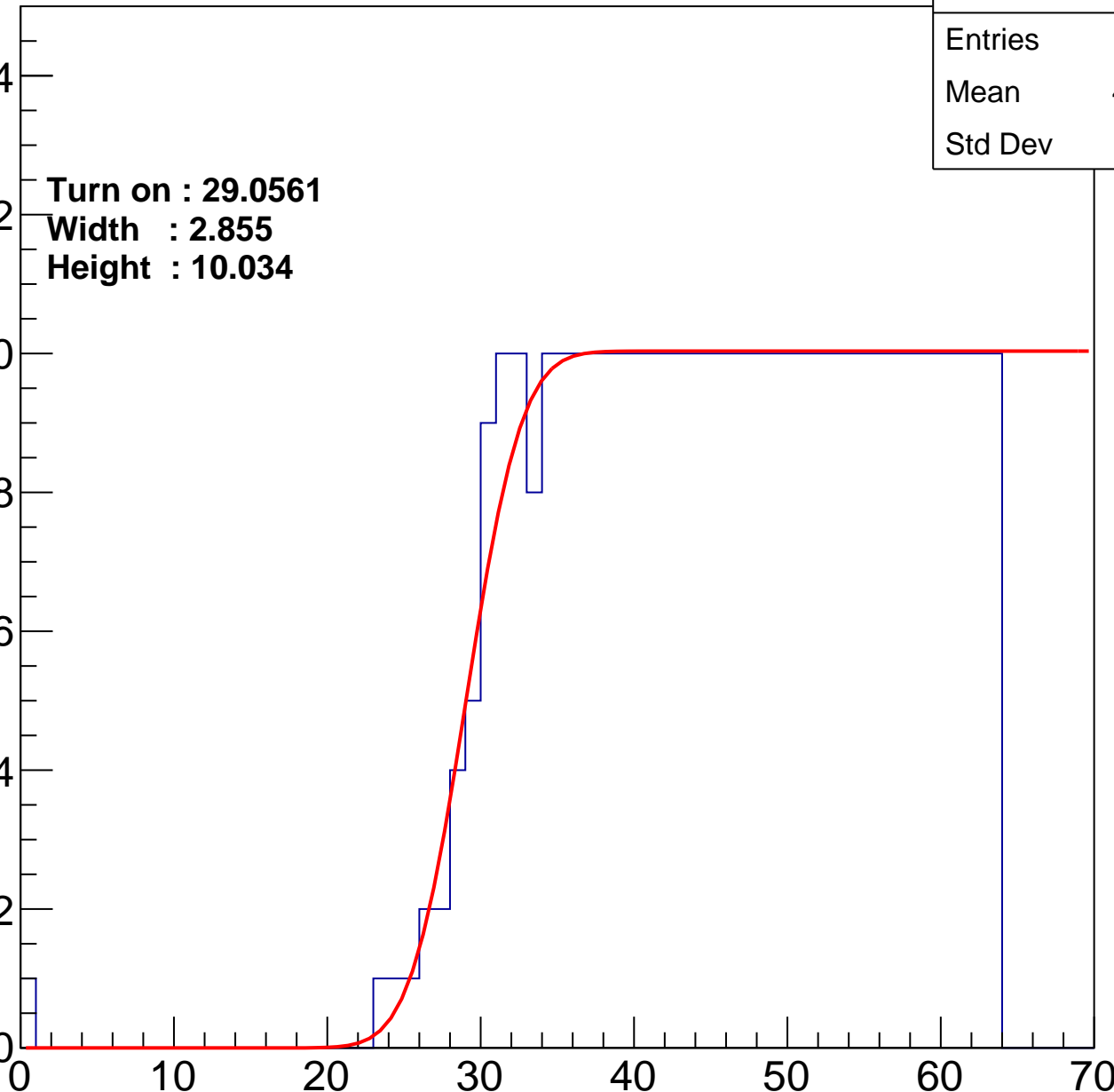
Width : 2.855

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.83
Std Dev	10.92

Turn on : 29.3999

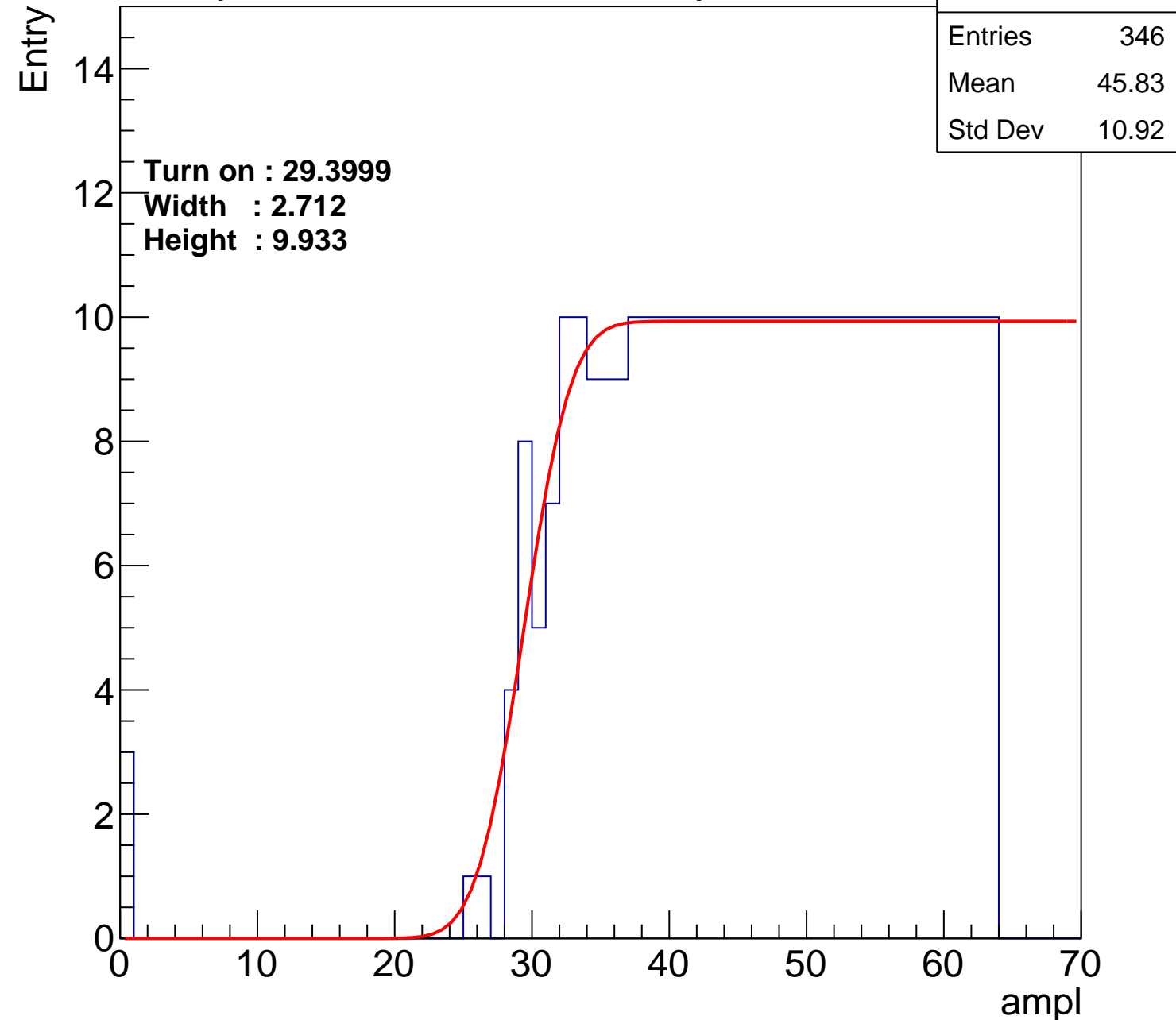
Width : 2.712

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.41
Std Dev	10.76

Turn on : 28.4615

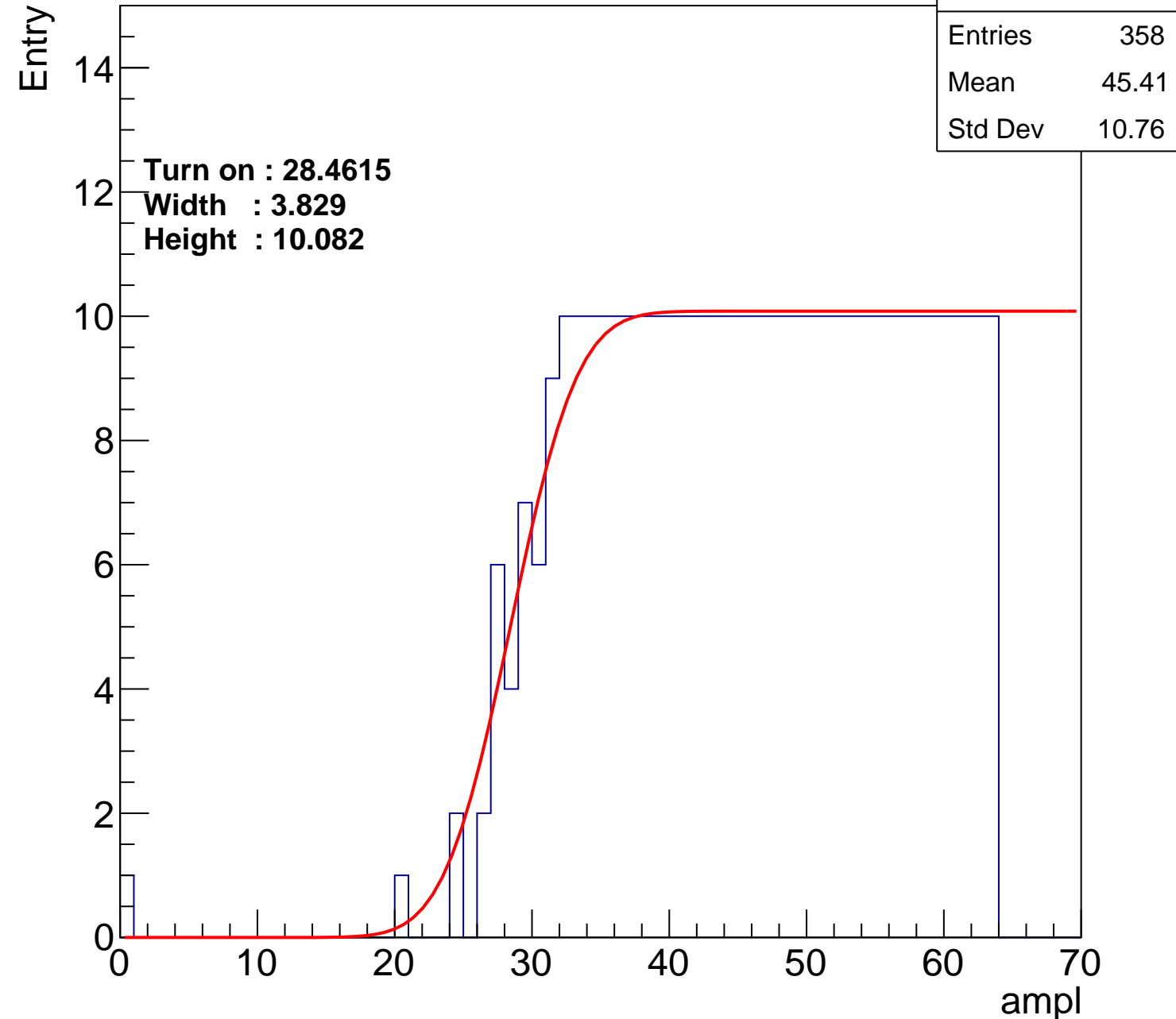
Width : 3.829

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.83
Std Dev	11.91

Turn on : 26.0613

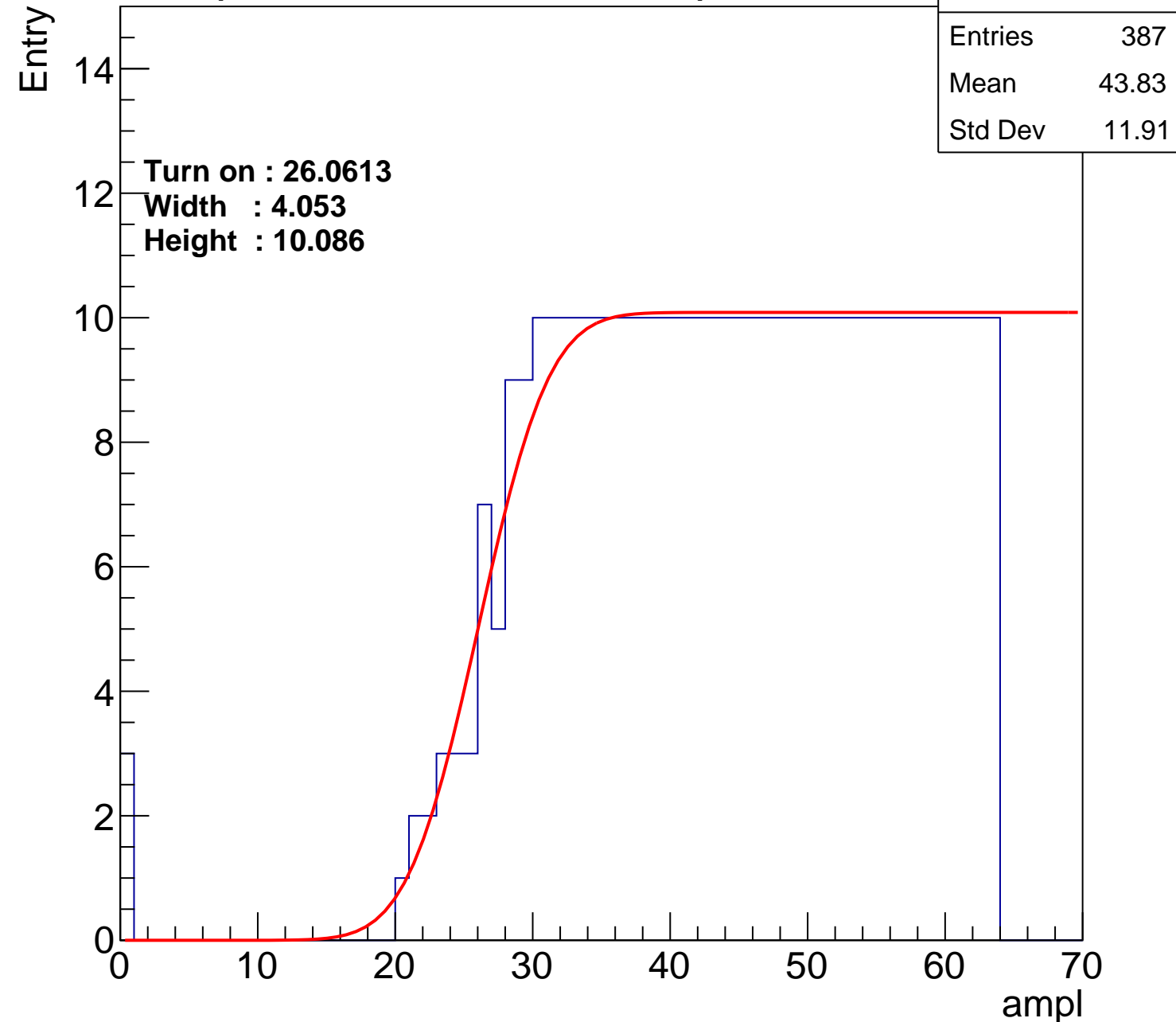
Width : 4.053

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	45.91
Std Dev	11.29

Turn on : 30.3234

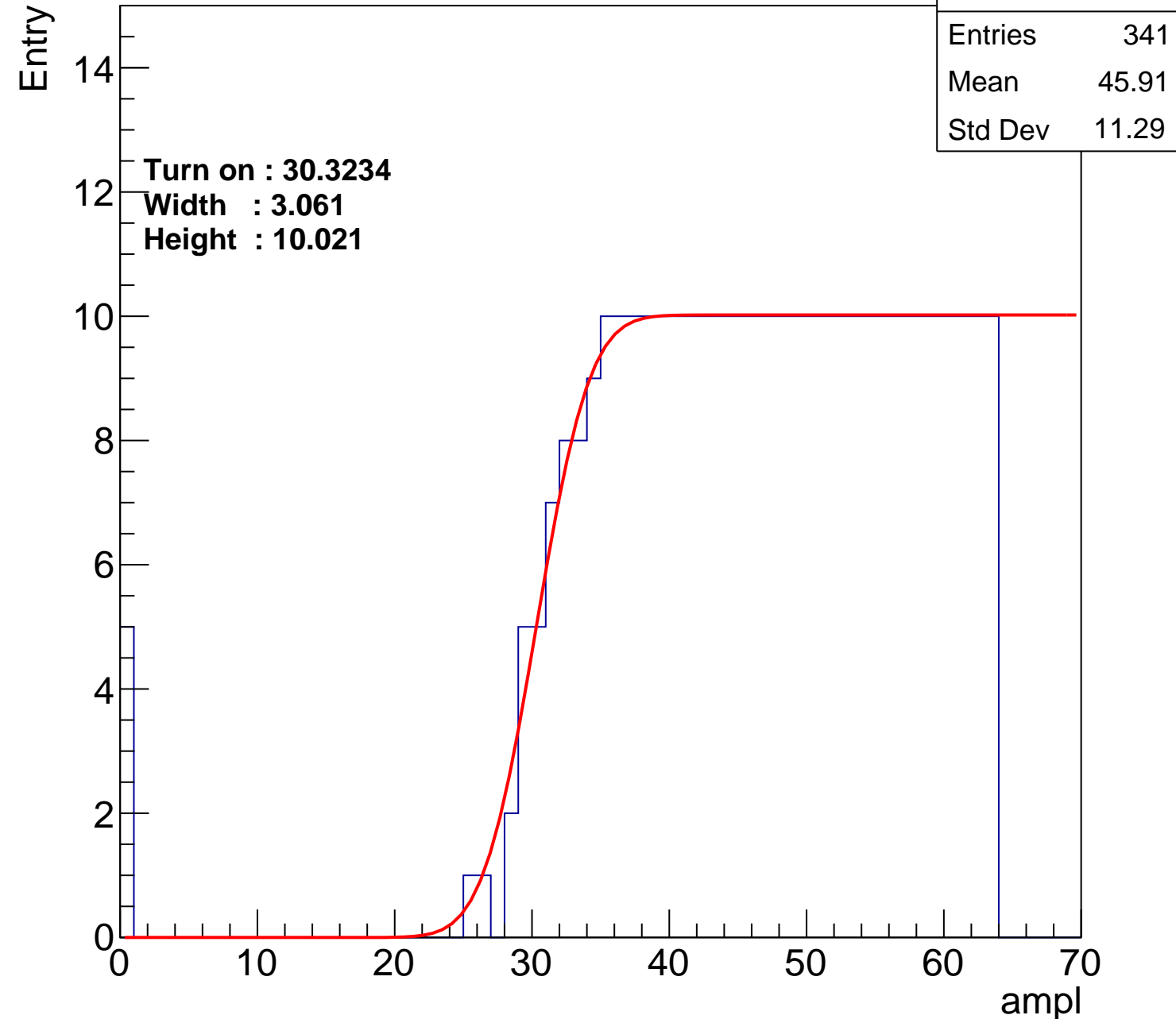
Width : 3.061

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.66
Std Dev	10.62

Turn on : 29.0400

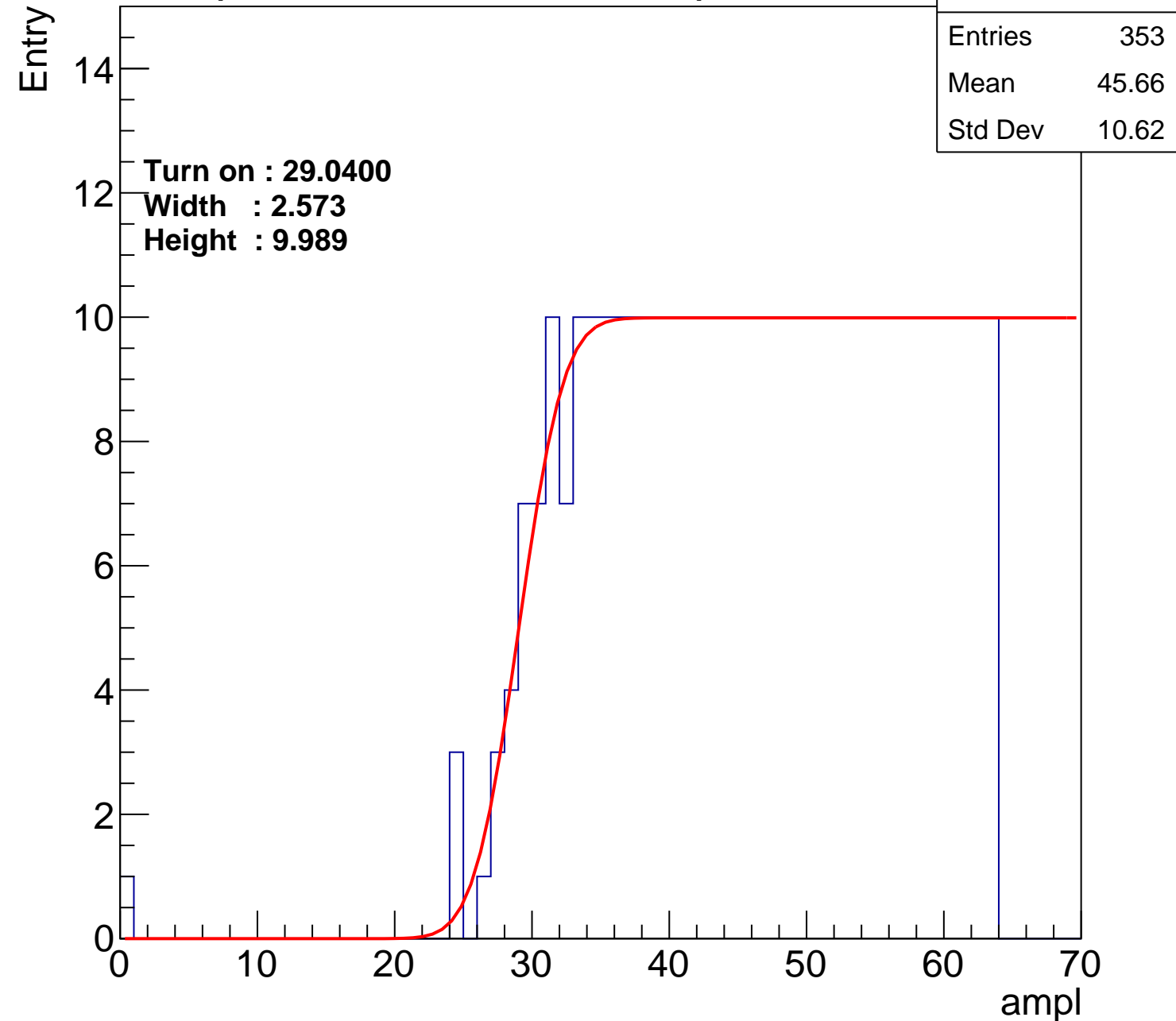
Width : 2.573

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.12
Std Dev	11.19

Turn on : 28.3163

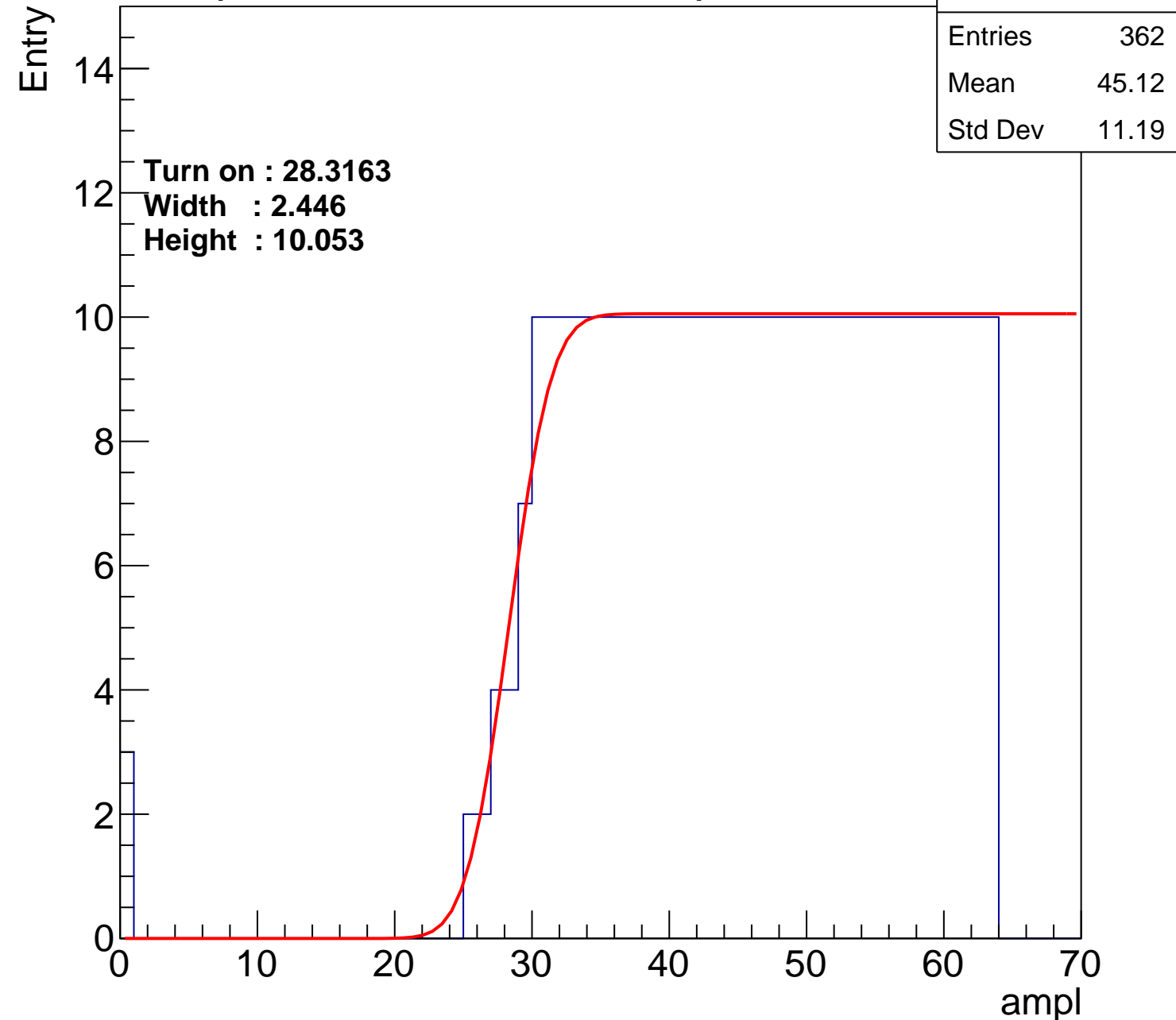
Width : 2.446

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.74
Std Dev	10.75

Turn on : 29.0298

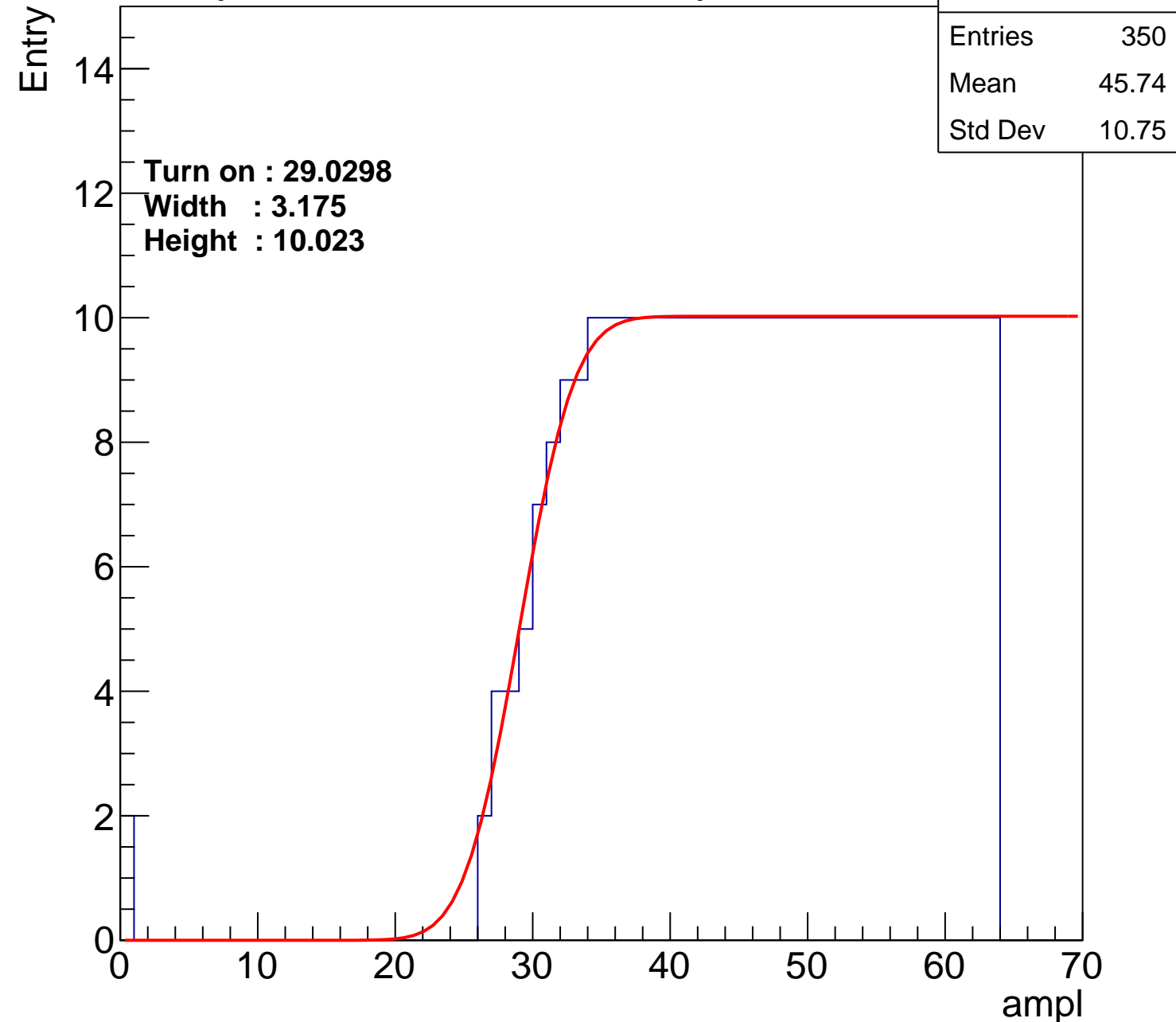
Width : 3.175

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.17
Std Dev	11.66

Turn on : 26.5321

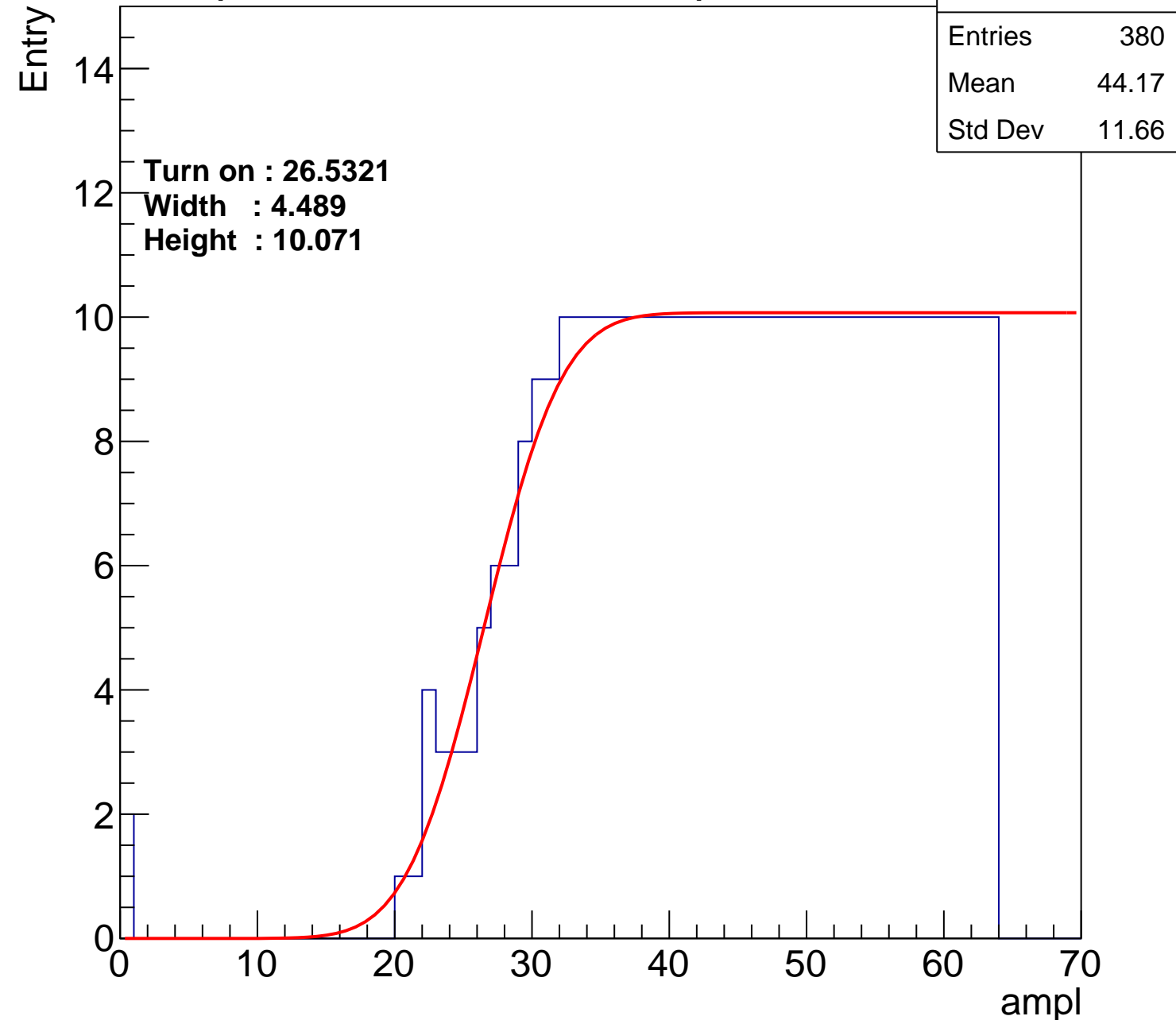
Width : 4.489

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.5
Std Dev	11.26

Turn on : 26.4813

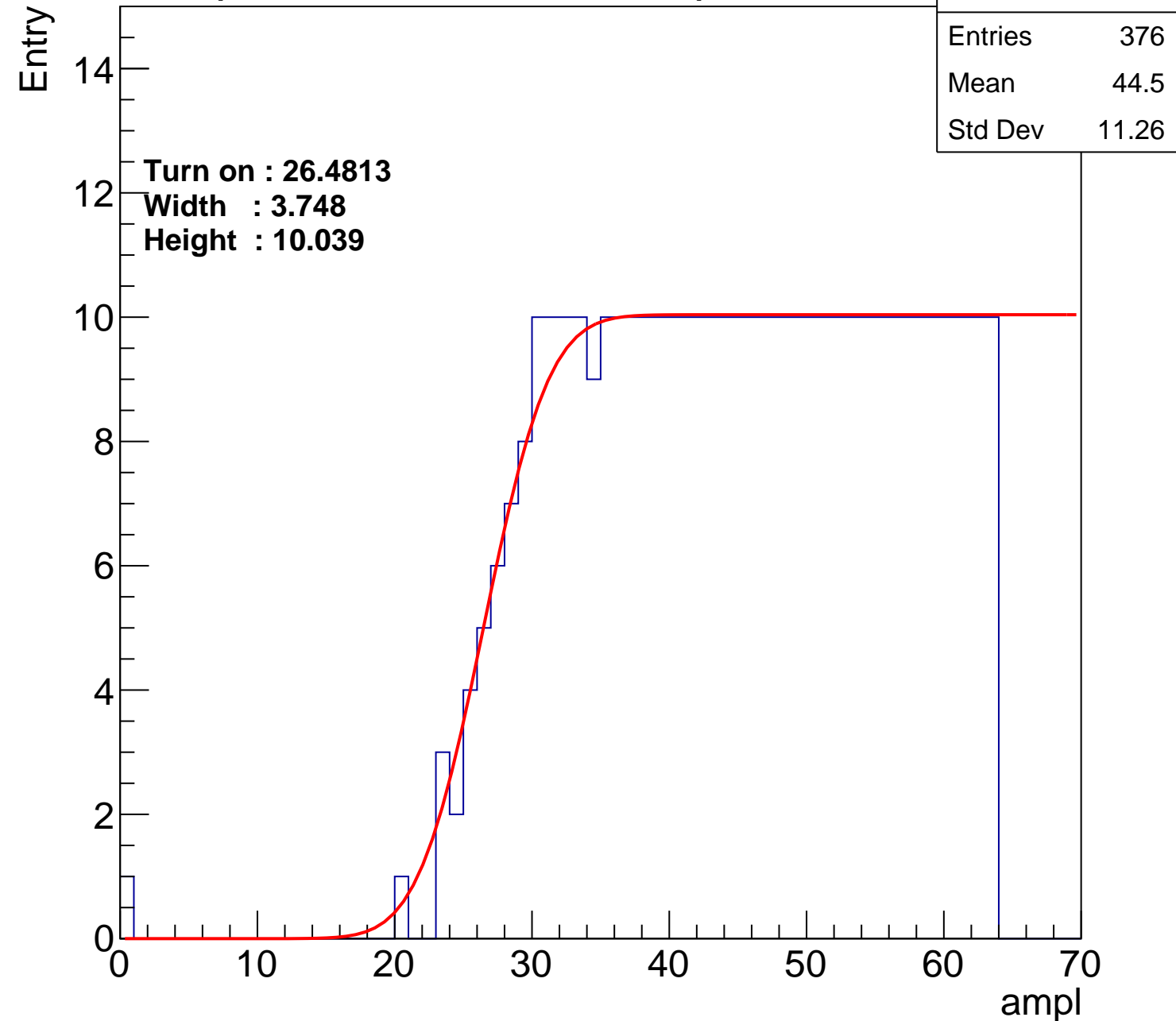
Width : 3.748

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch107

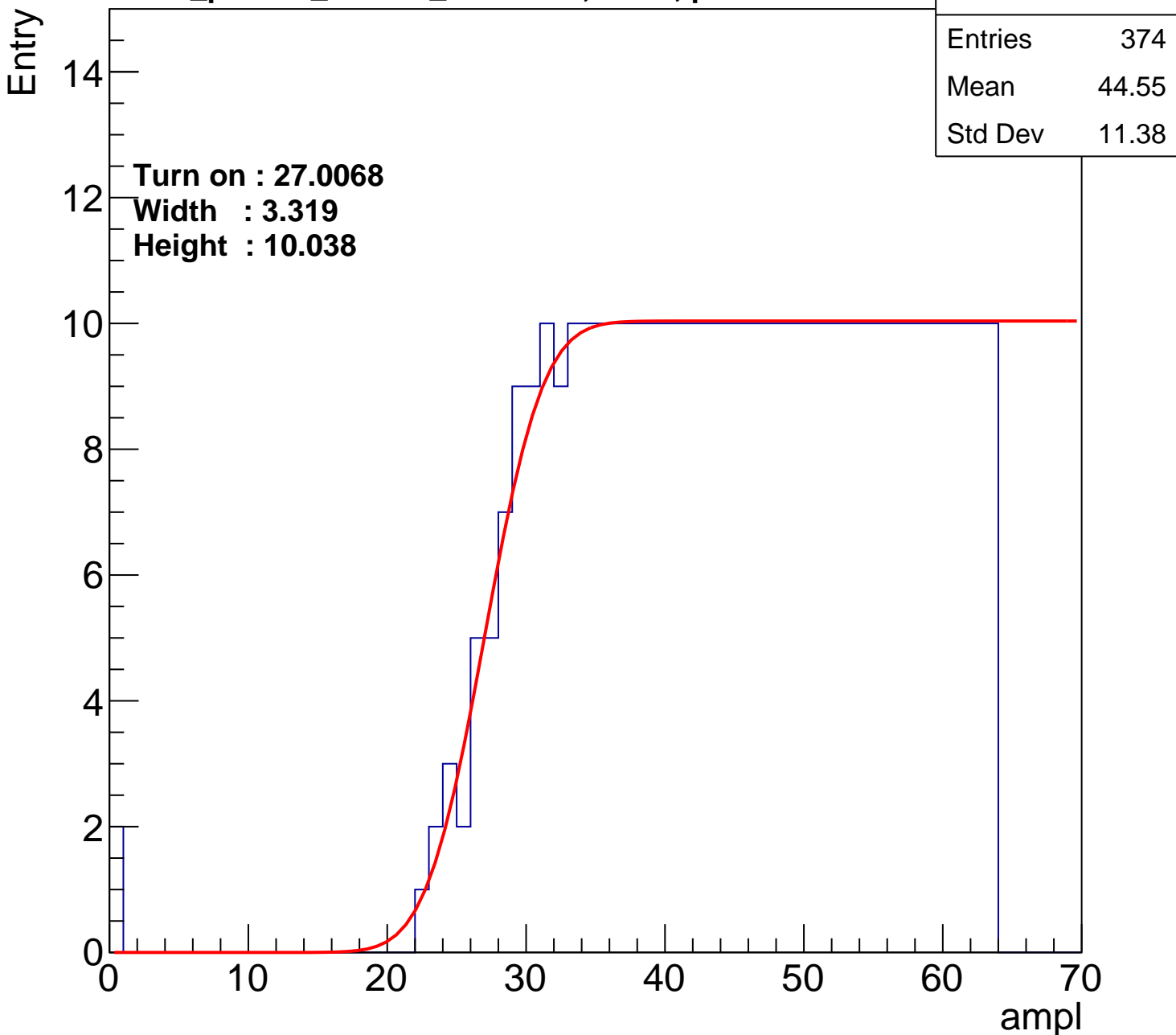
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.0068

Width : 3.319

Height : 10.038

Entries	374
Mean	44.55
Std Dev	11.38



B0L002S, U26-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.61
Std Dev	11.56

Turn on : 27.0539

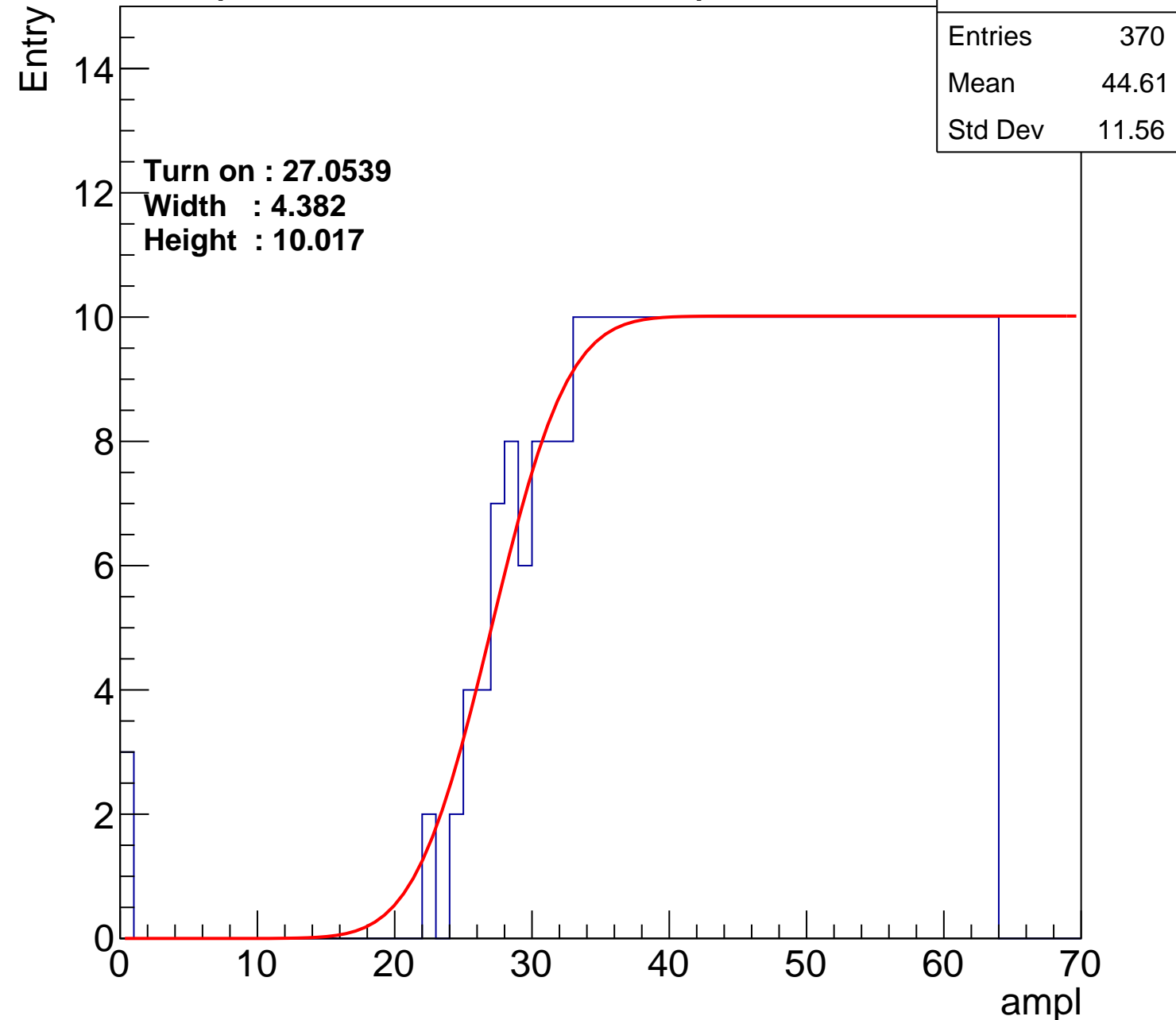
Width : 4.382

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch109

calib_packv5_042523_0143.root, FC#8, port C1

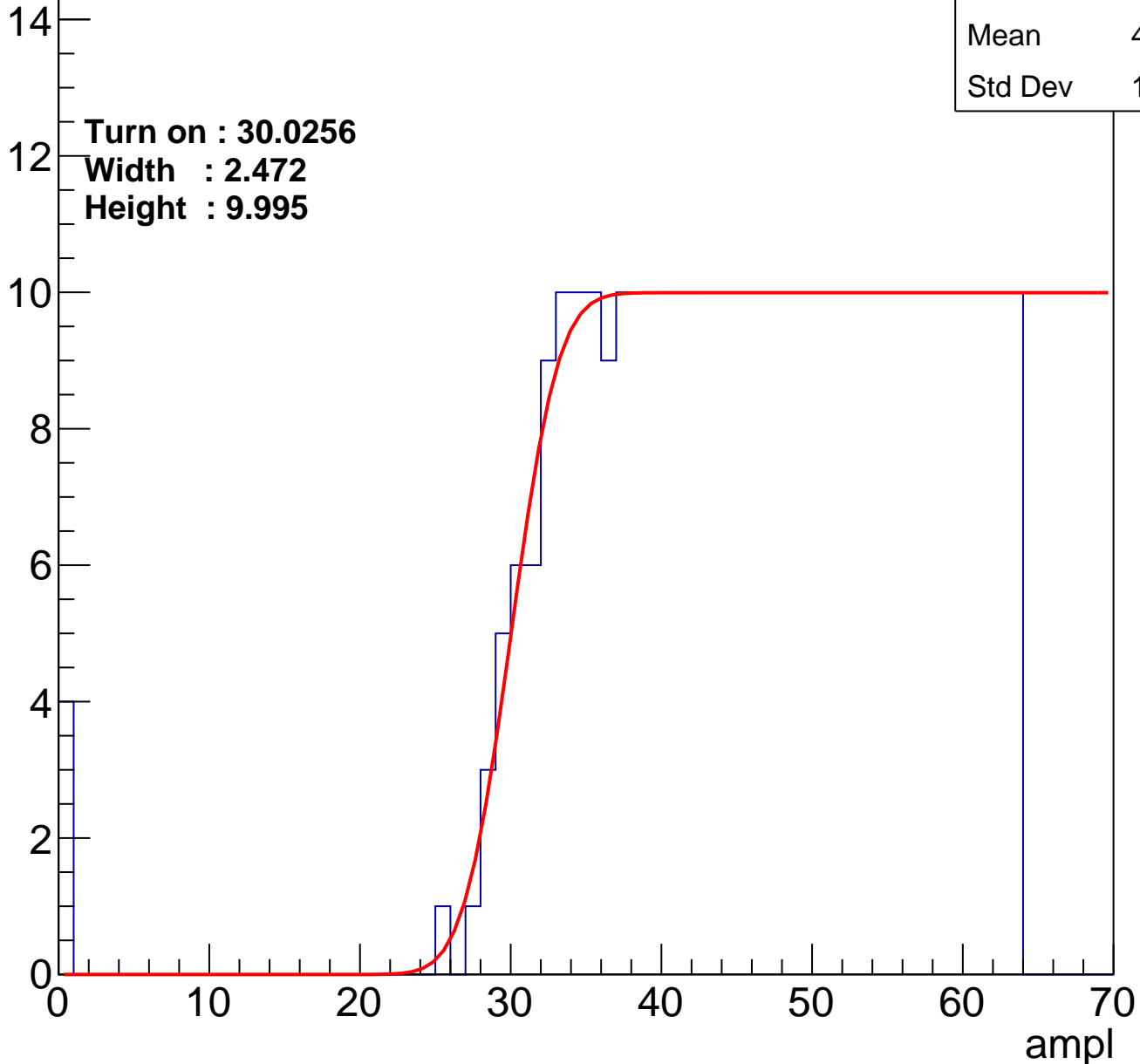
Entries	344
Mean	45.87
Std Dev	11.08

Turn on : 30.0256

Width : 2.472

Height : 9.995

Entry



B0L002S, U26-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.18
Std Dev	11.97

Turn on : 26.7213

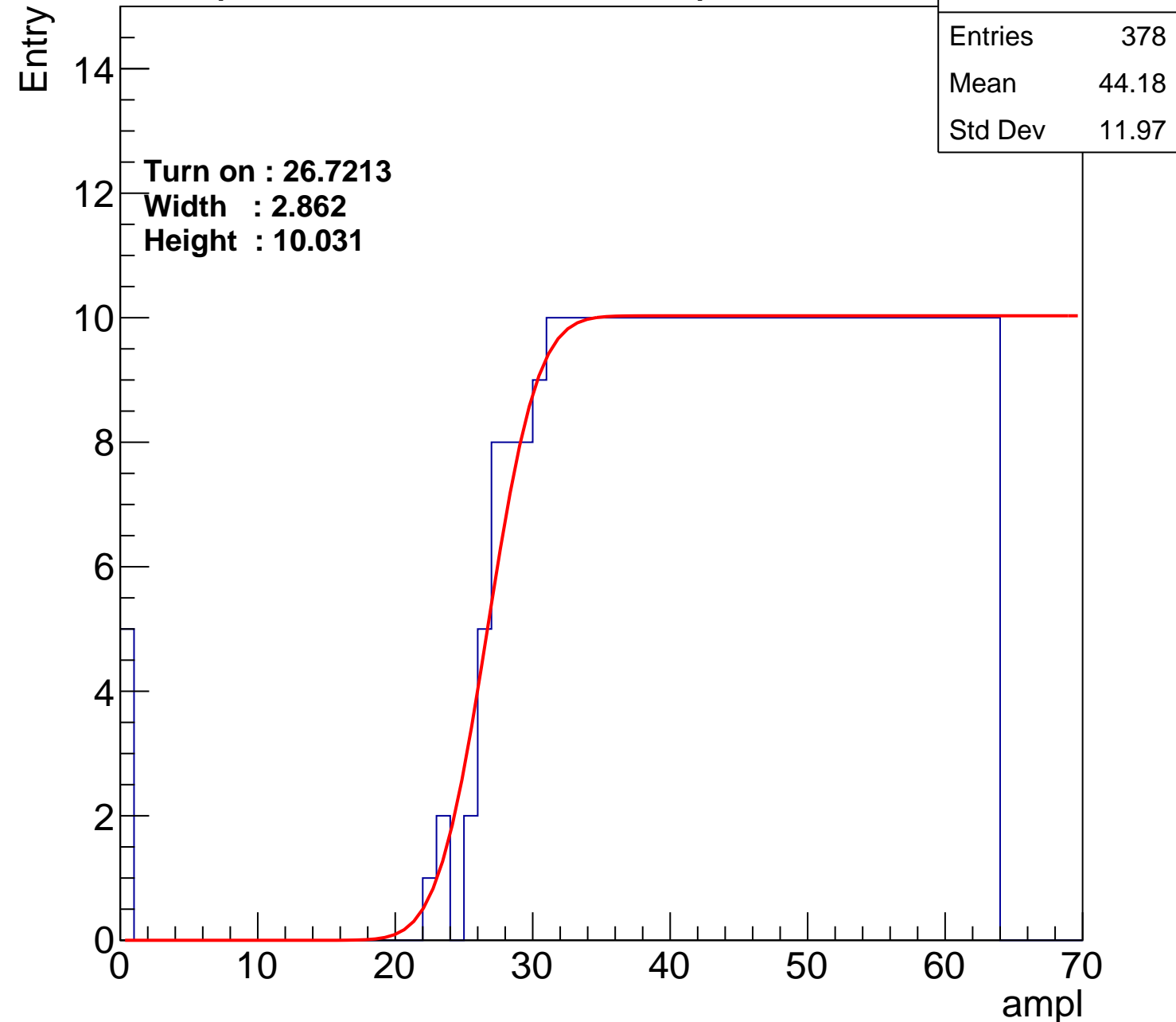
Width : 2.862

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
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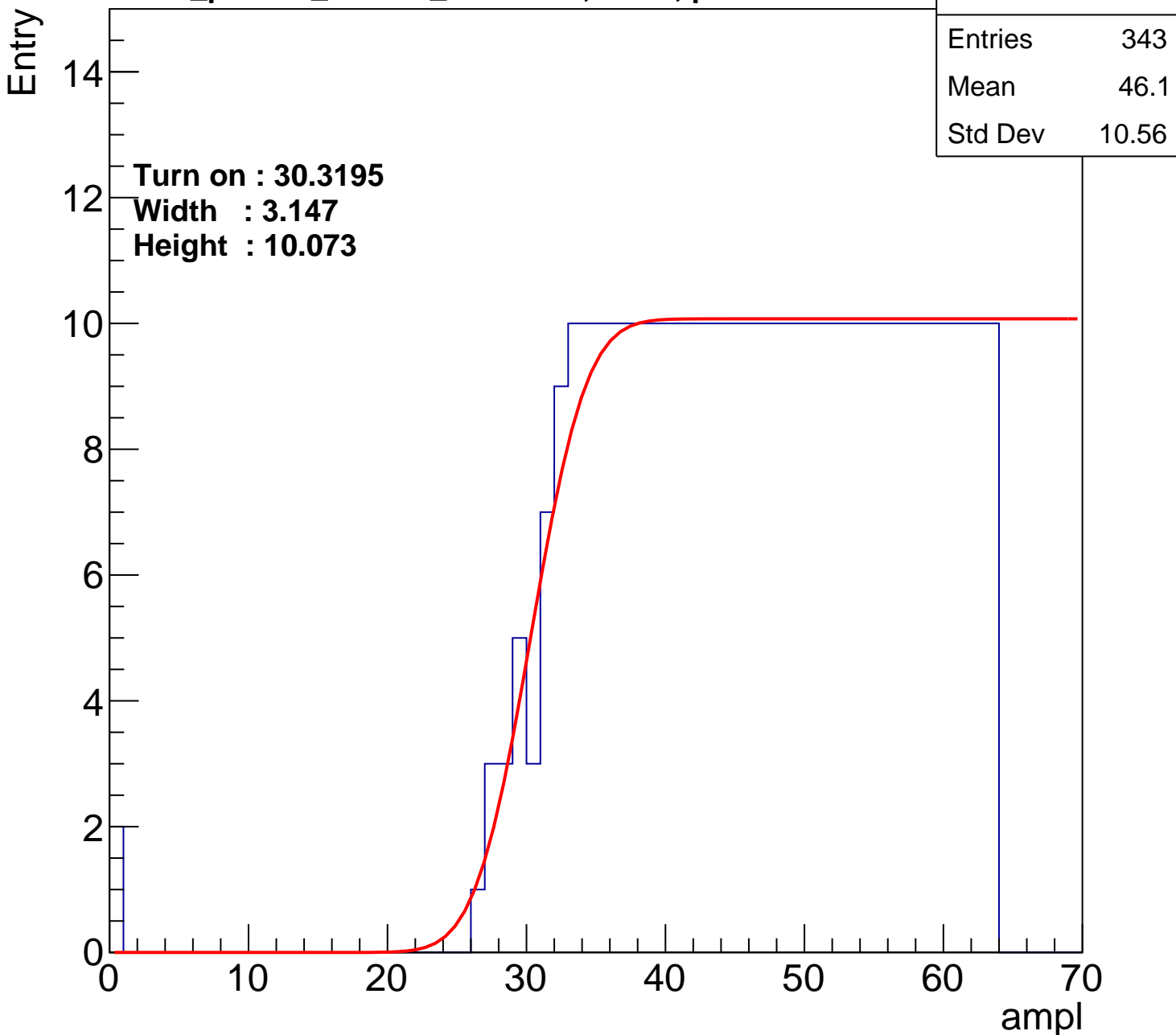
Mean	46.1
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Std Dev	10.56
---------	-------

Turn on : 30.3195

Width : 3.147

Height : 10.073



B0L002S, U26-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.55
Std Dev	11.21

Turn on : 29.2593

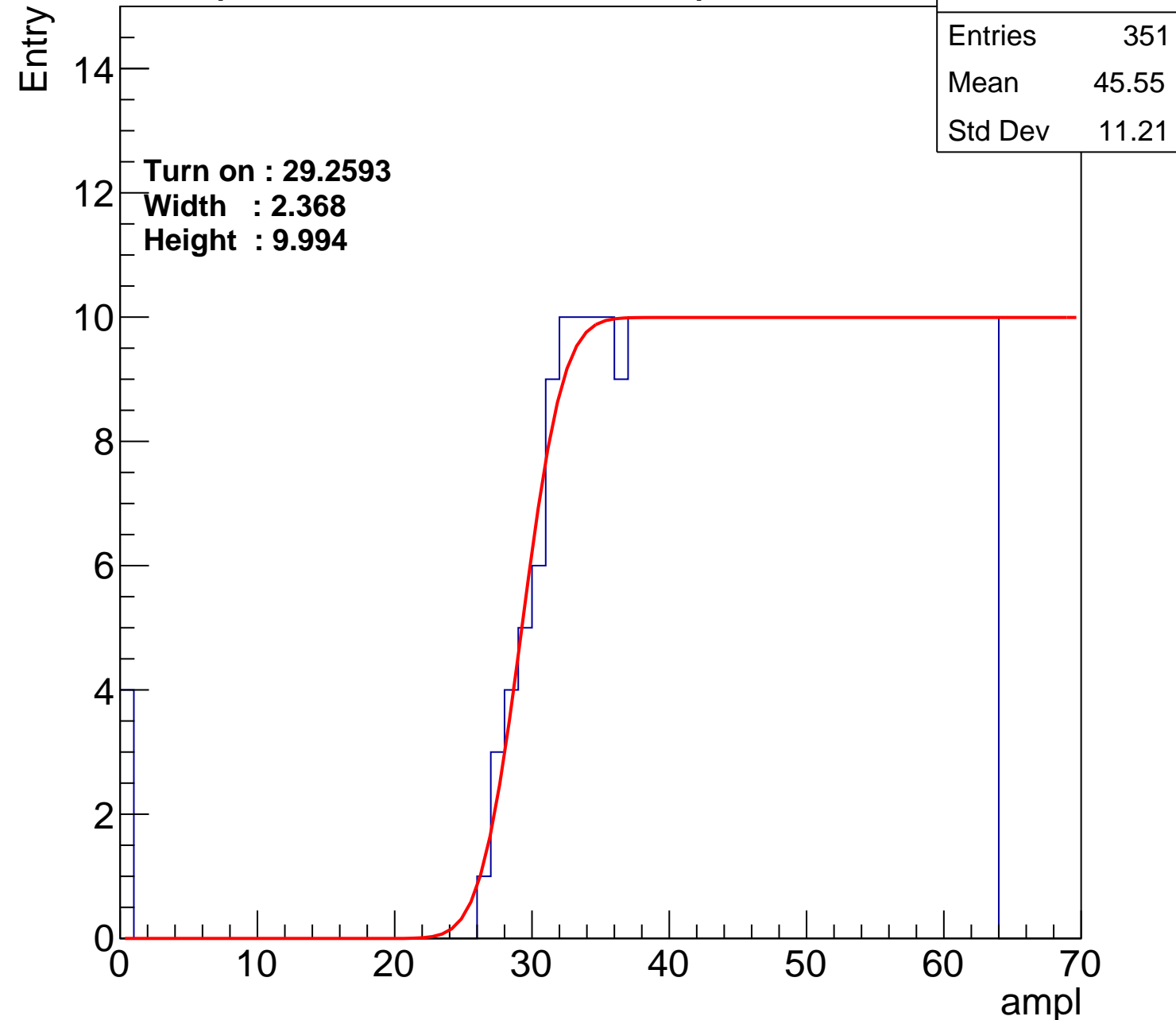
Width : 2.368

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.67
Std Dev	11.53

Turn on : 27.1112

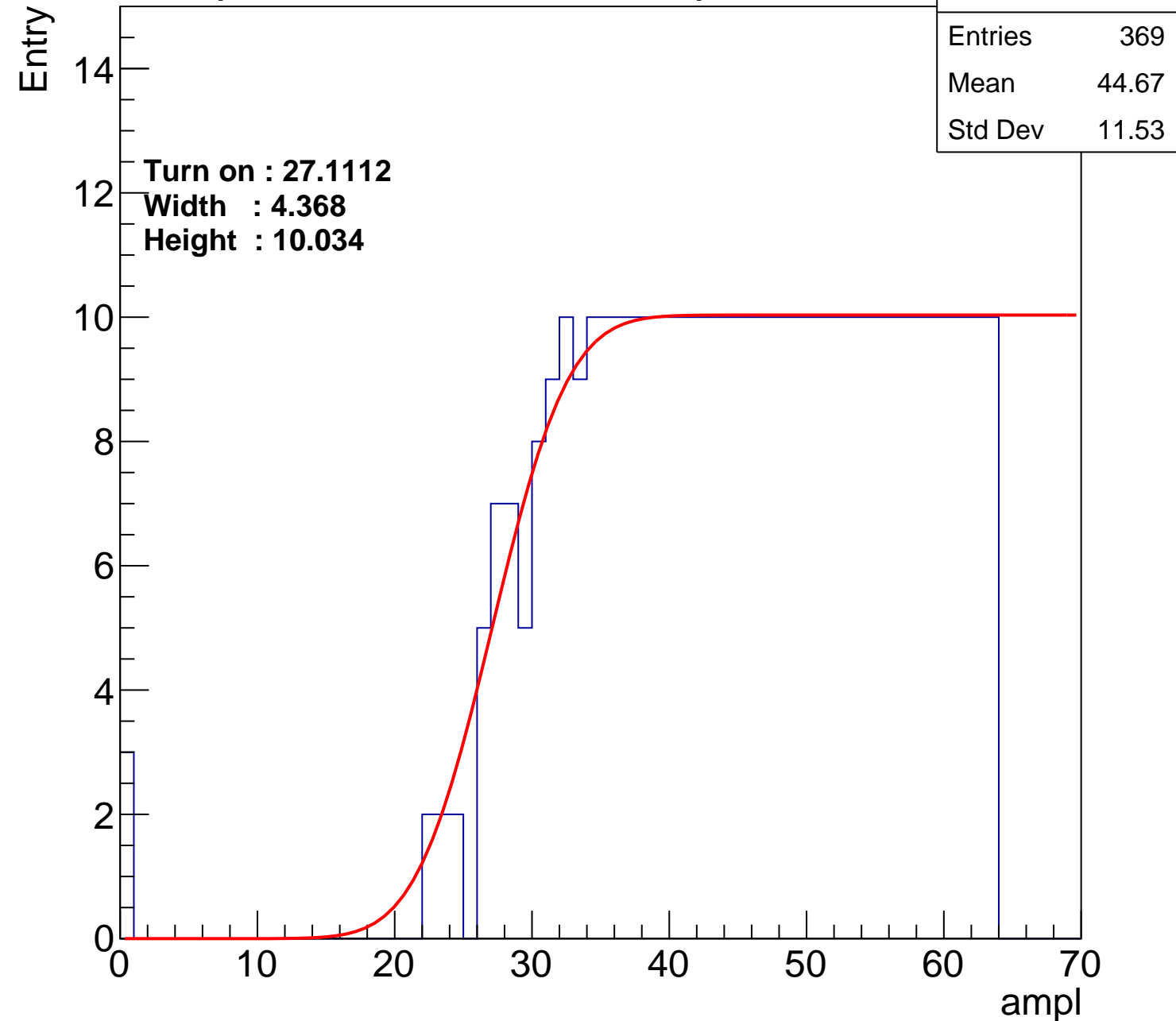
Width : 4.368

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.67
Std Dev	10.86

Turn on : 29.3353

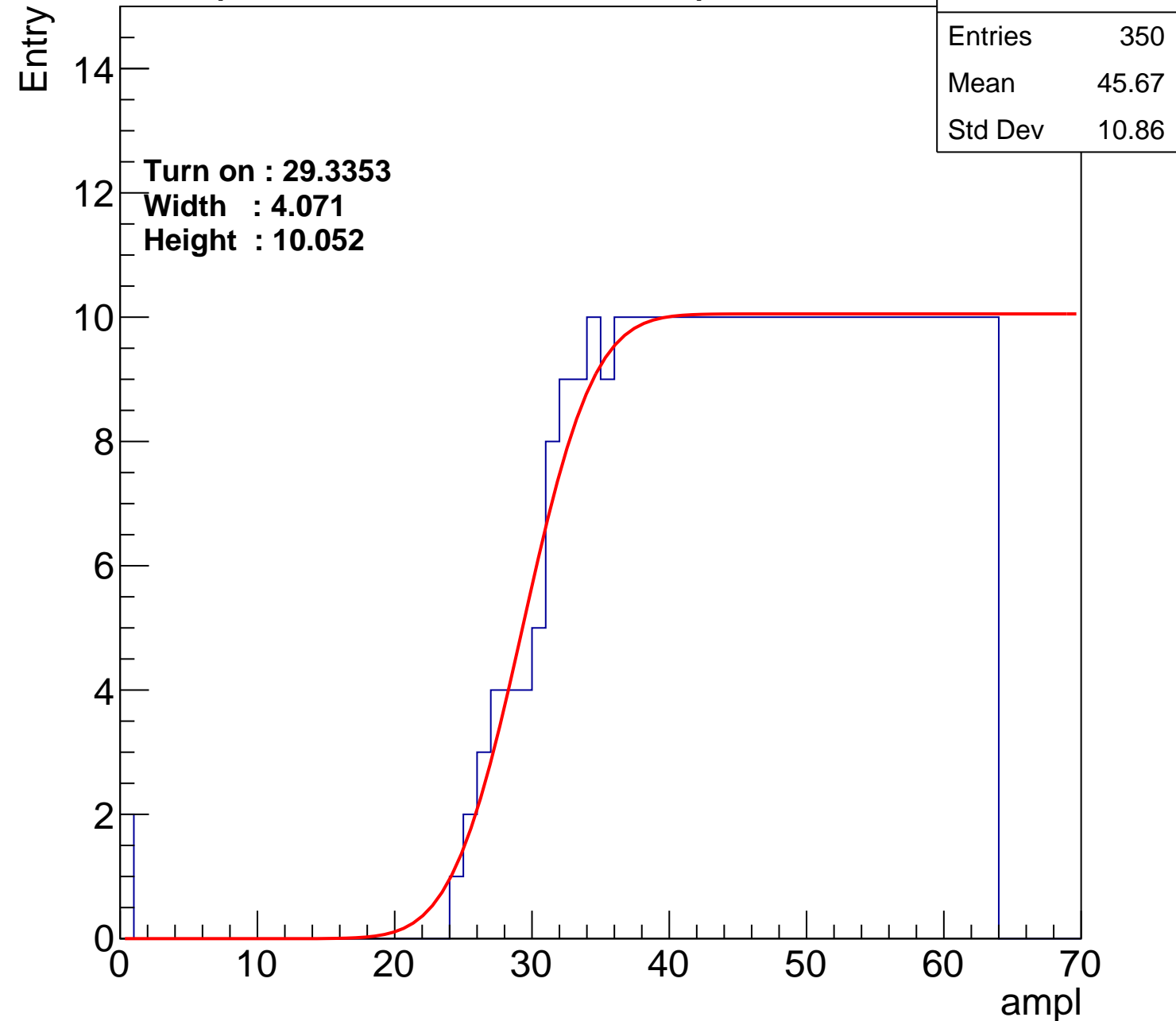
Width : 4.071

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.29
Std Dev	10.78

Turn on : 28.1241

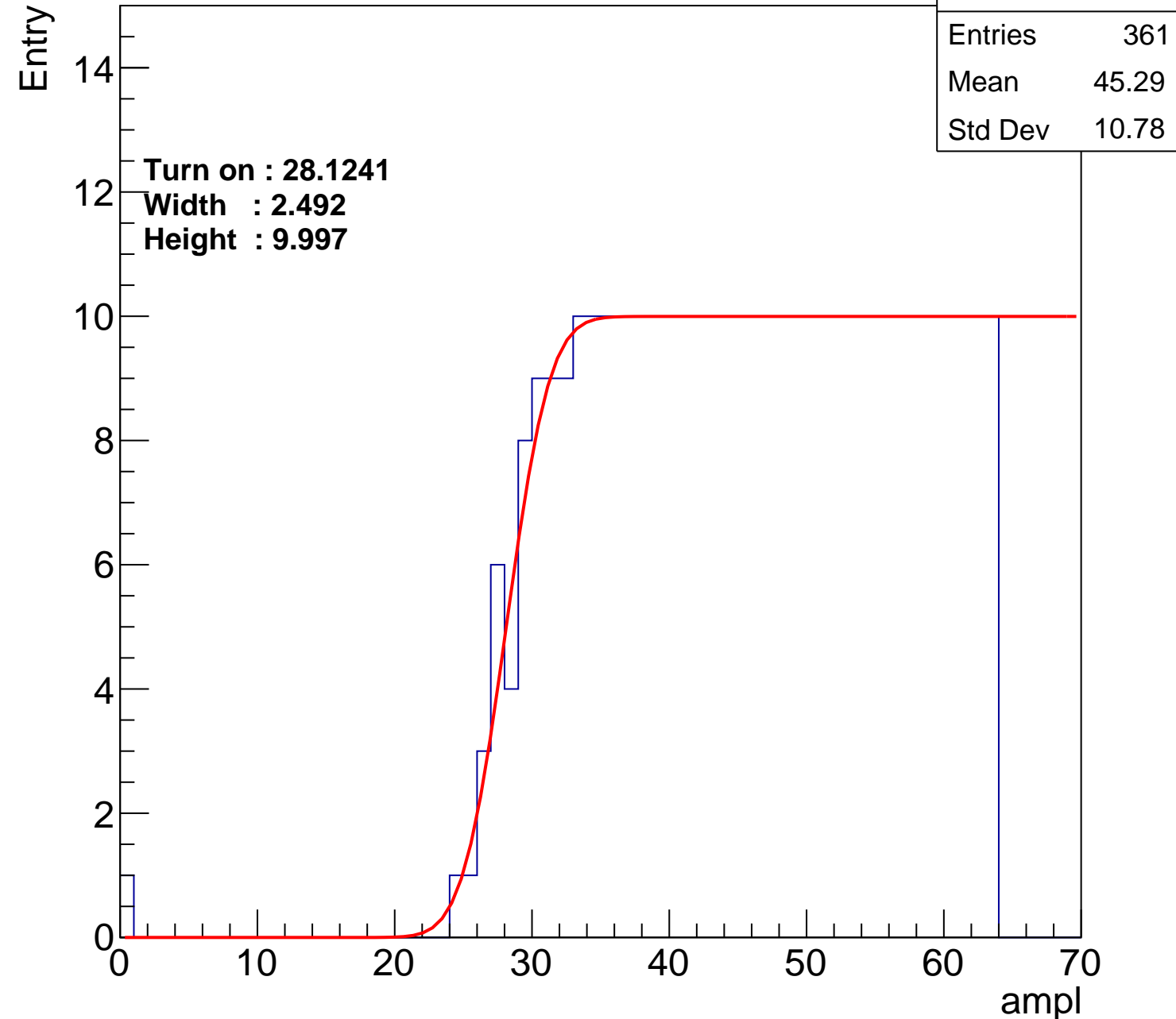
Width : 2.492

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.05
Std Dev	11.26

Turn on : 28.1025

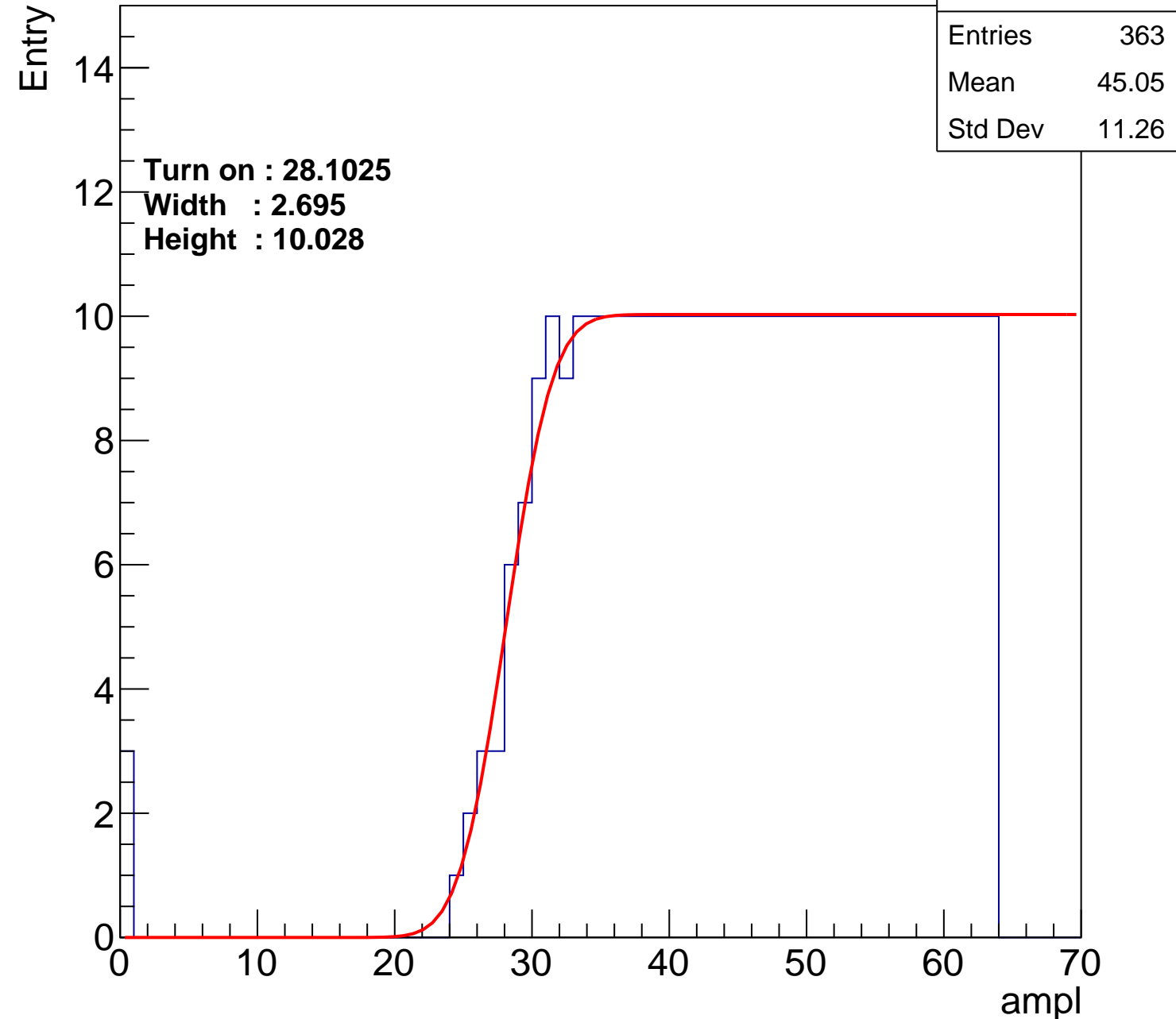
Width : 2.695

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	46.02
Std Dev	10.67

Turn on : 29.7652

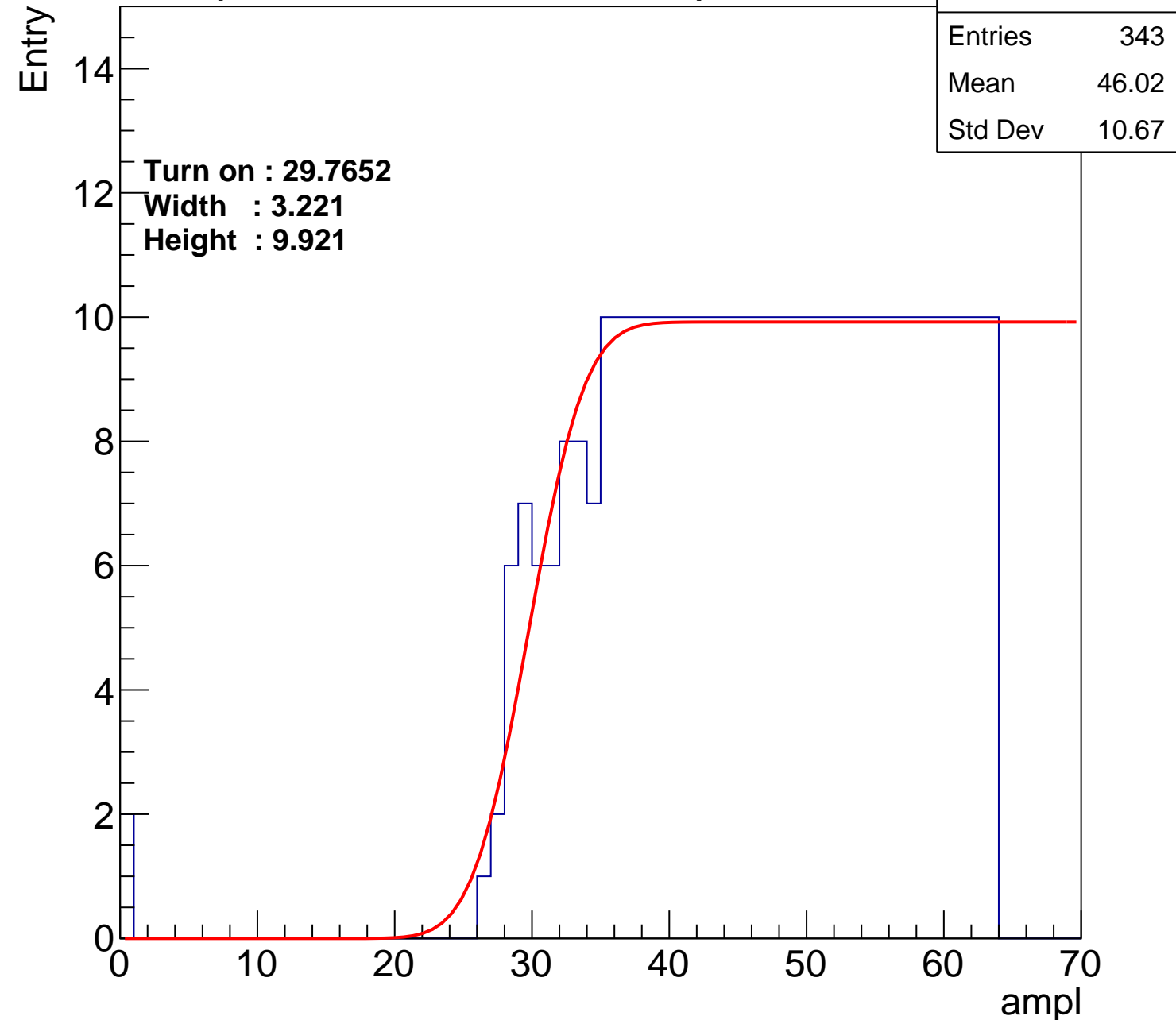
Width : 3.221

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.87
Std Dev	10.71

Turn on : 30.1375

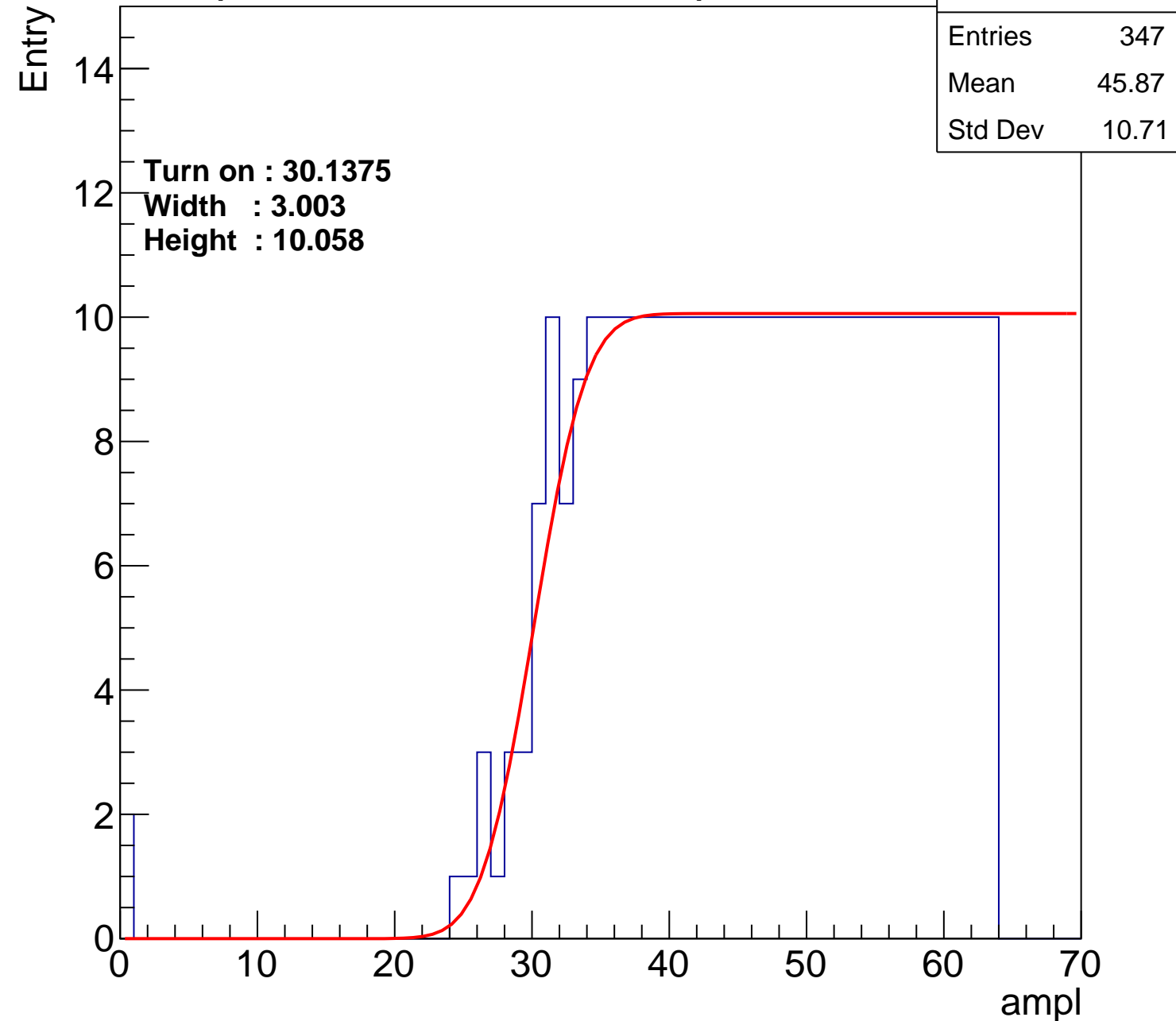
Width : 3.003

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.2467

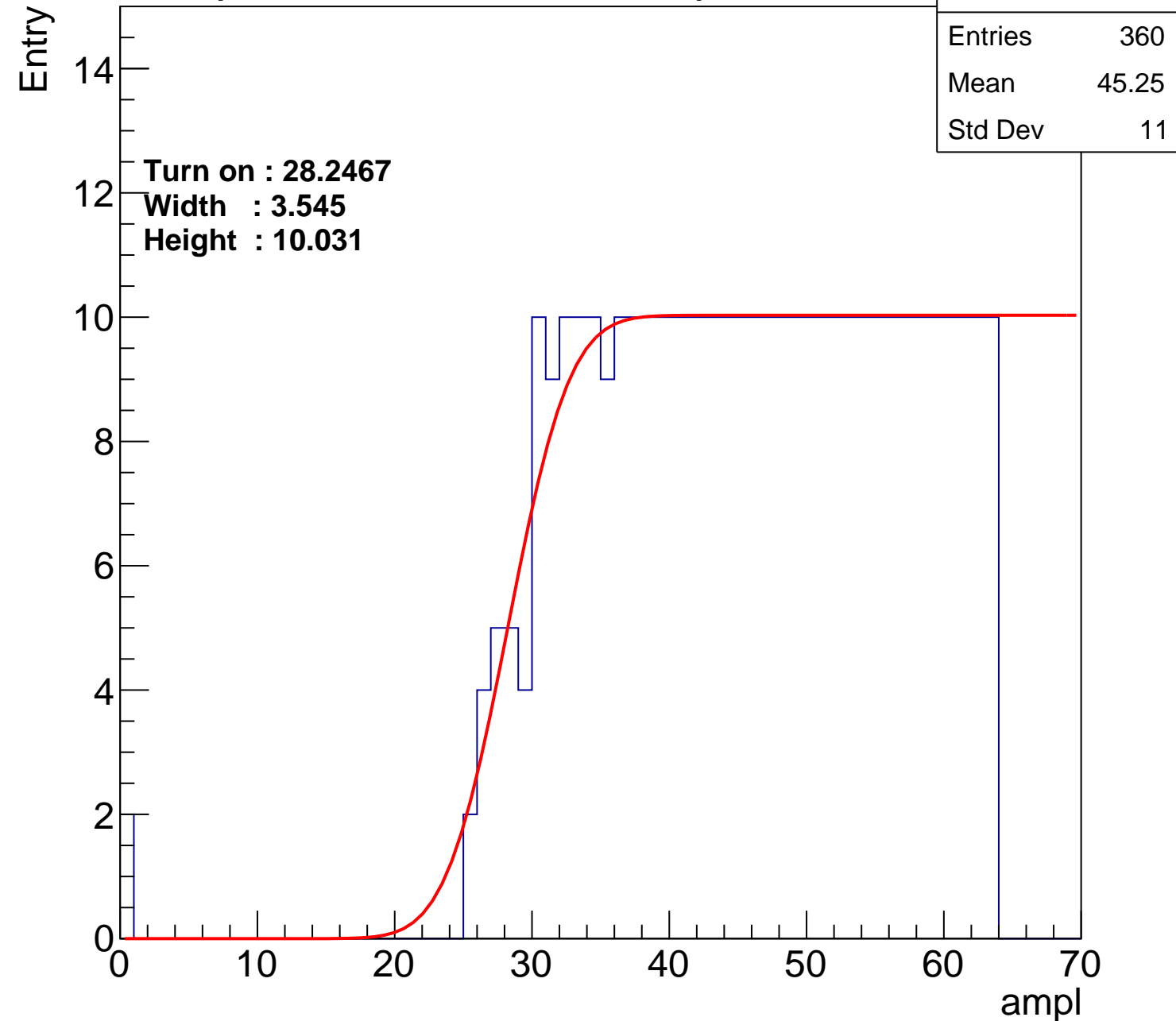
Width : 3.545

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch120

calib_packv5_042523_0143.root, FC#8, port C1

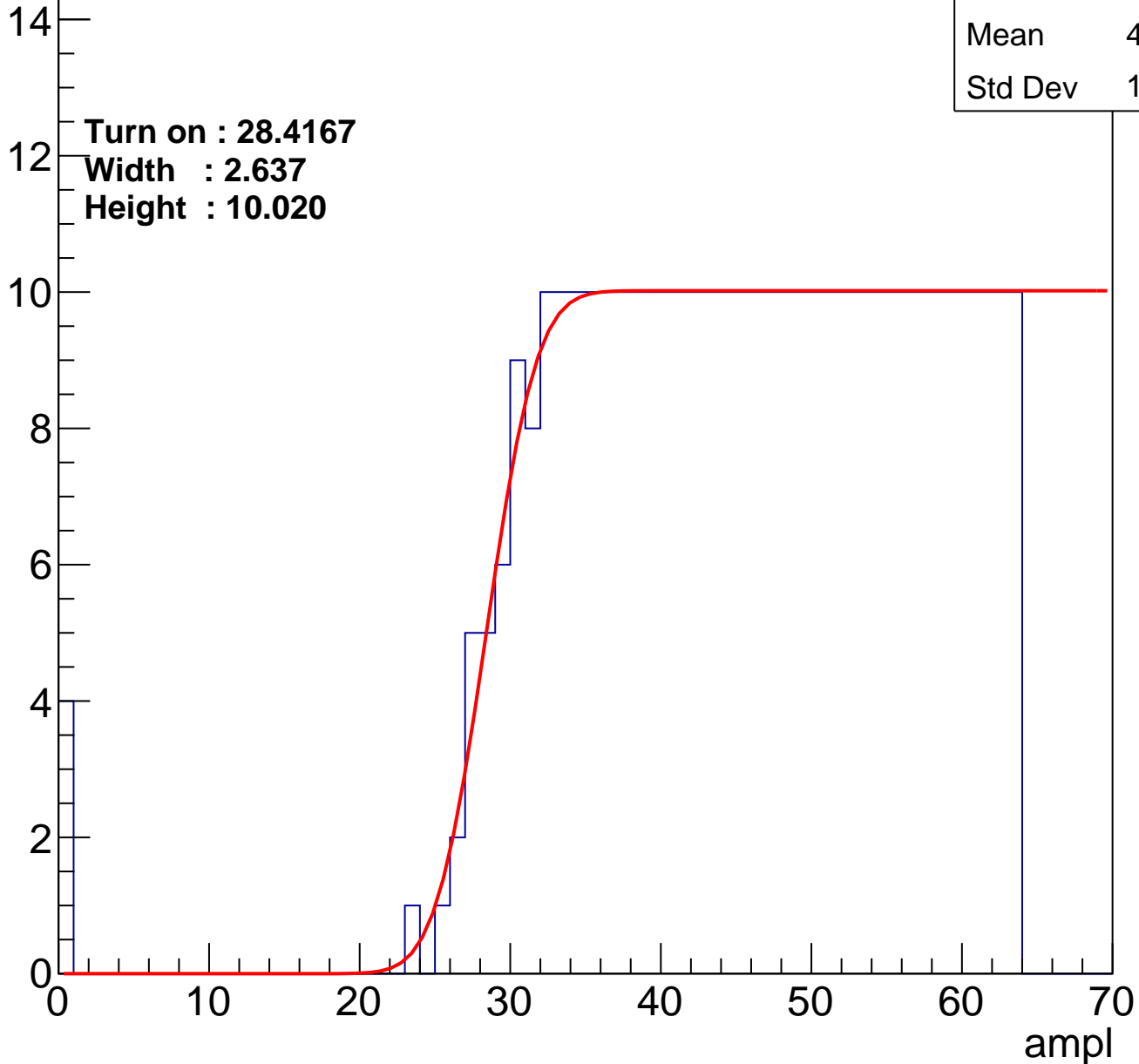
Entries	361
Mean	45.06
Std Dev	11.43

Turn on : 28.4167

Width : 2.637

Height : 10.020

Entry



B0L002S, U26-ch121

calib_packv5_042523_0143.root, FC#8, port C1

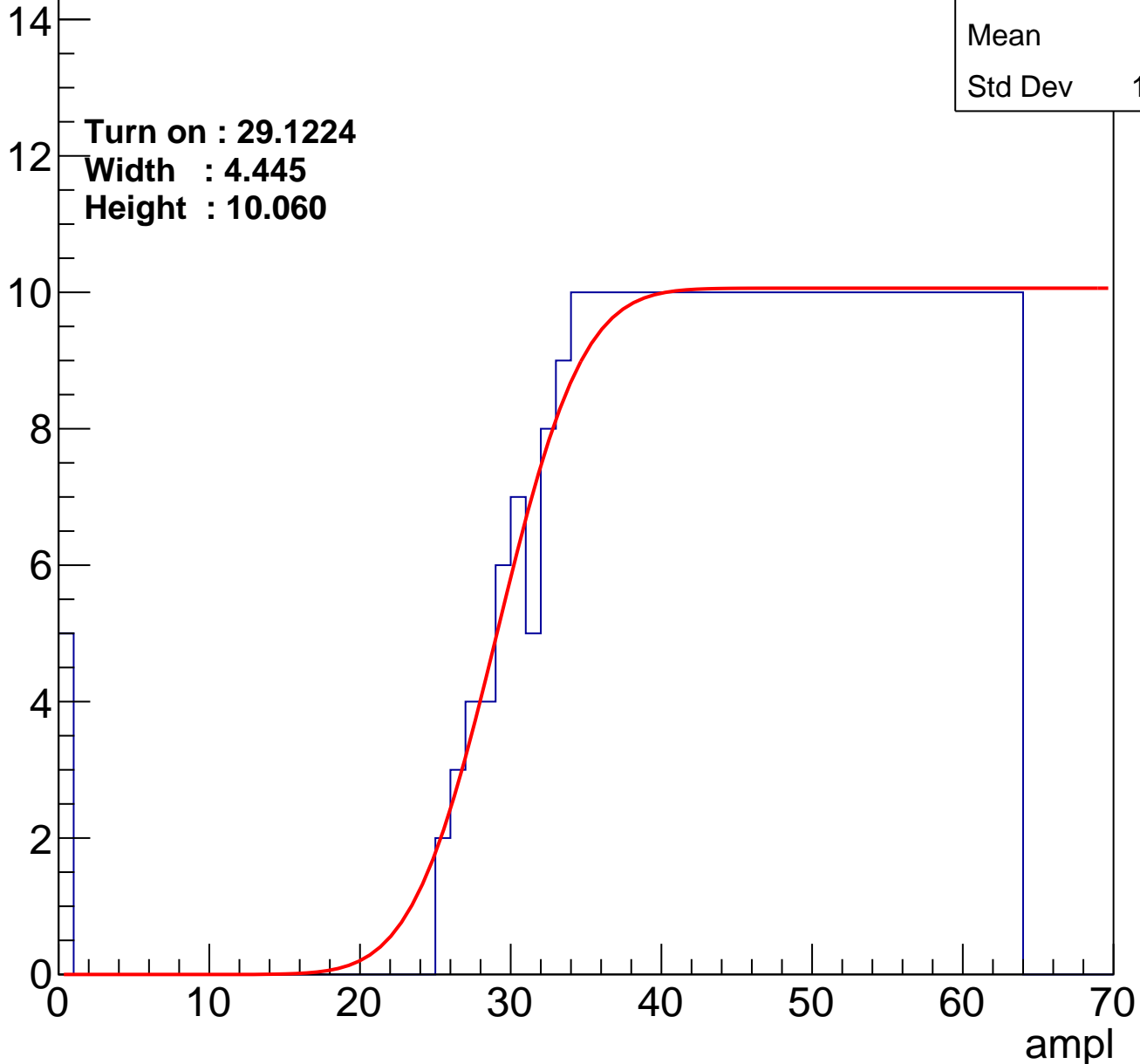
Entries	353
Mean	45.3
Std Dev	11.58

Turn on : 29.1224

Width : 4.445

Height : 10.060

Entry



B0L002S, U26-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.43
Std Dev	10.99

Turn on : 28.6814

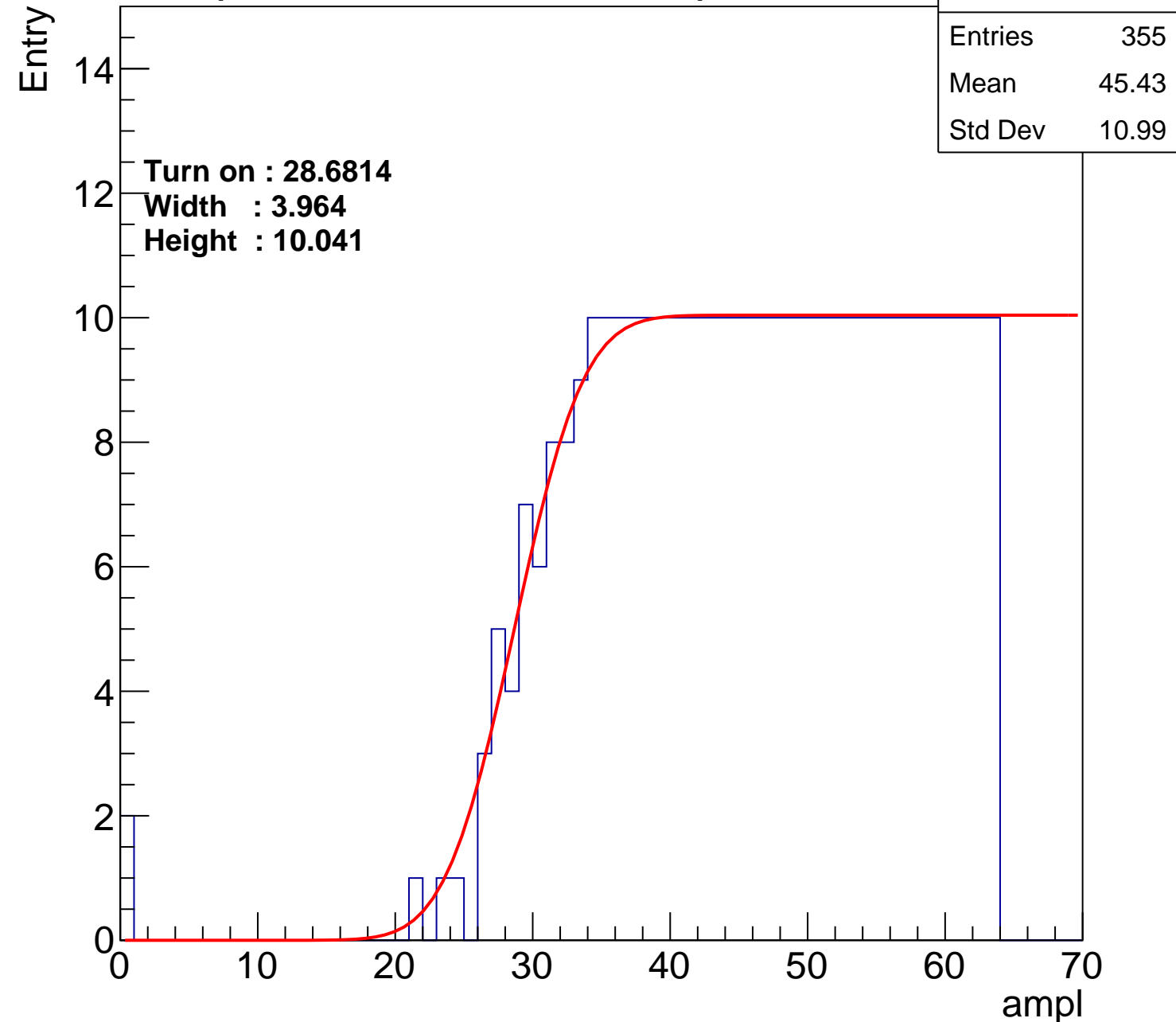
Width : 3.964

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	46.17
Std Dev	10.33

Turn on : 30.7658

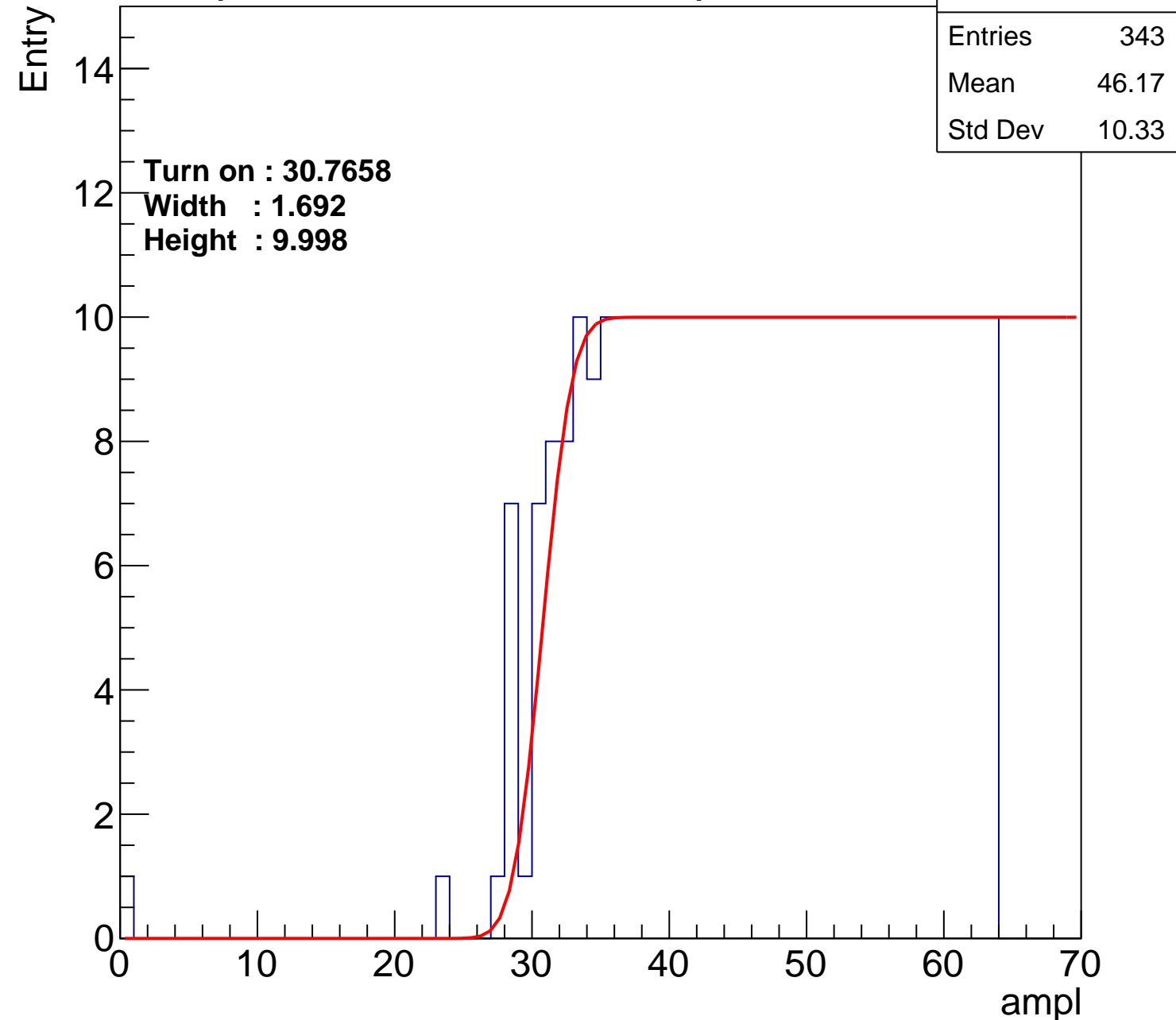
Width : 1.692

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.97
Std Dev	10.44

Turn on : 29.7000

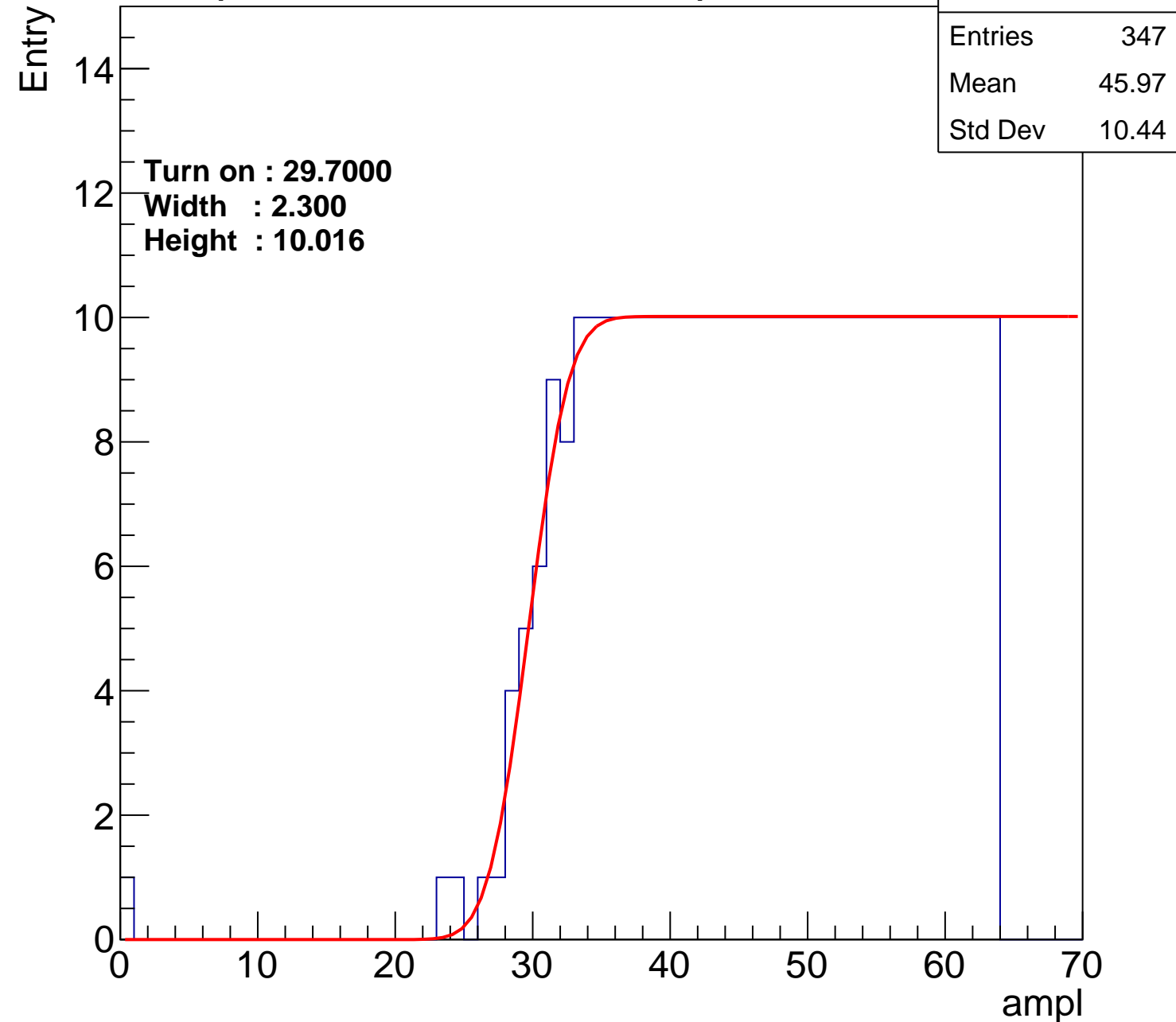
Width : 2.300

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch125

calib_packv5_042523_0143.root, FC#8, port C1

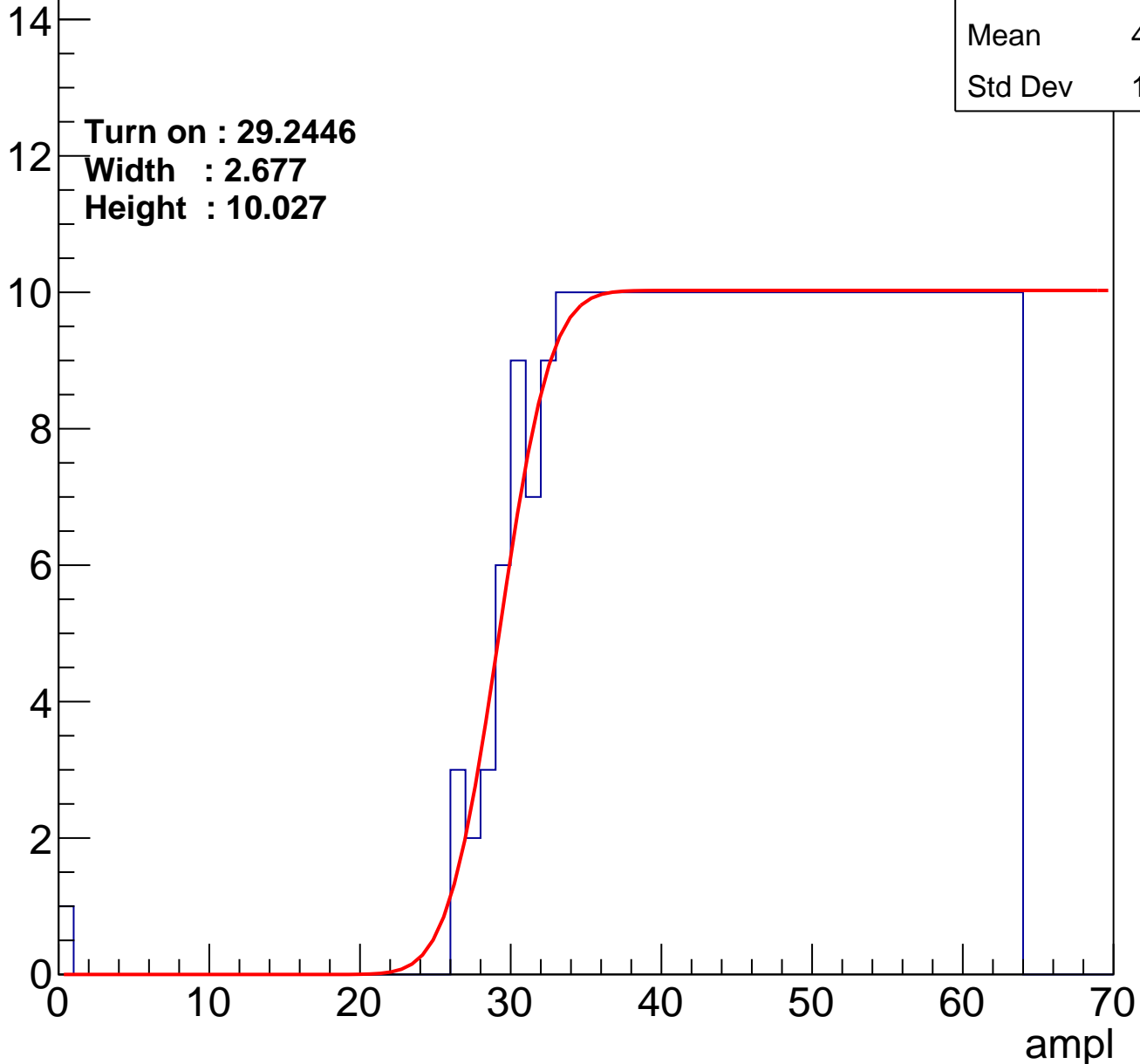
Entries	350
Mean	45.84
Std Dev	10.48

Turn on : 29.2446

Width : 2.677

Height : 10.027

Entry



B0L002S, U26-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 28.1474

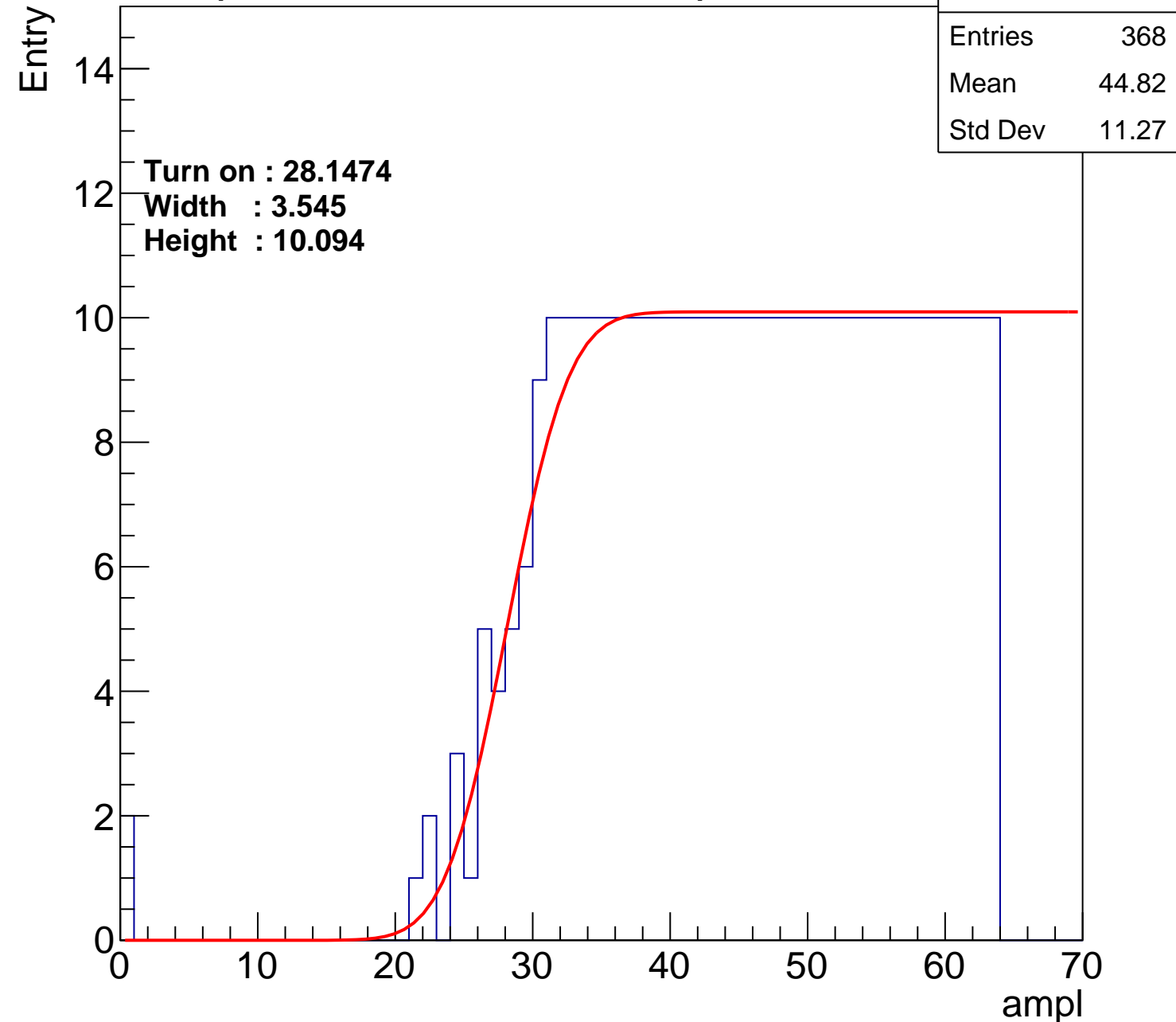
Width : 3.545

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U26-ch127

calib_packv5_042523_0143.root, FC#8, port C1

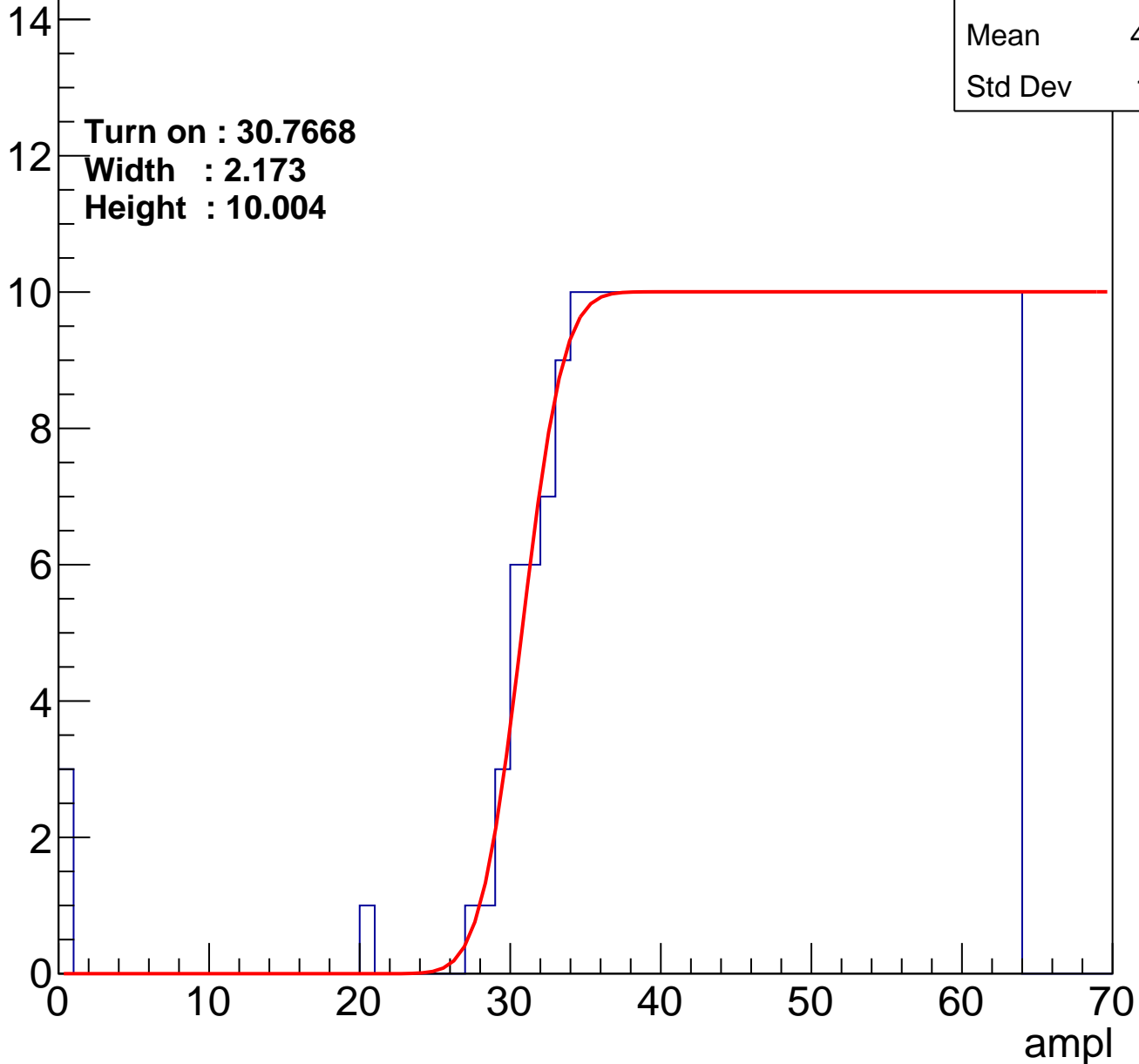
Entries	337
Mean	46.29
Std Dev	10.71

Turn on : 30.7668

Width : 2.173

Height : 10.004

Entry



B0L002S, U26-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	337
Mean	46.29
Std Dev	10.71

Turn on : 30.7668

Width : 2.173

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl

