



# B1L002S, U21-ch0

calib\_packv5\_042523\_0143.root, FC#10, port B3

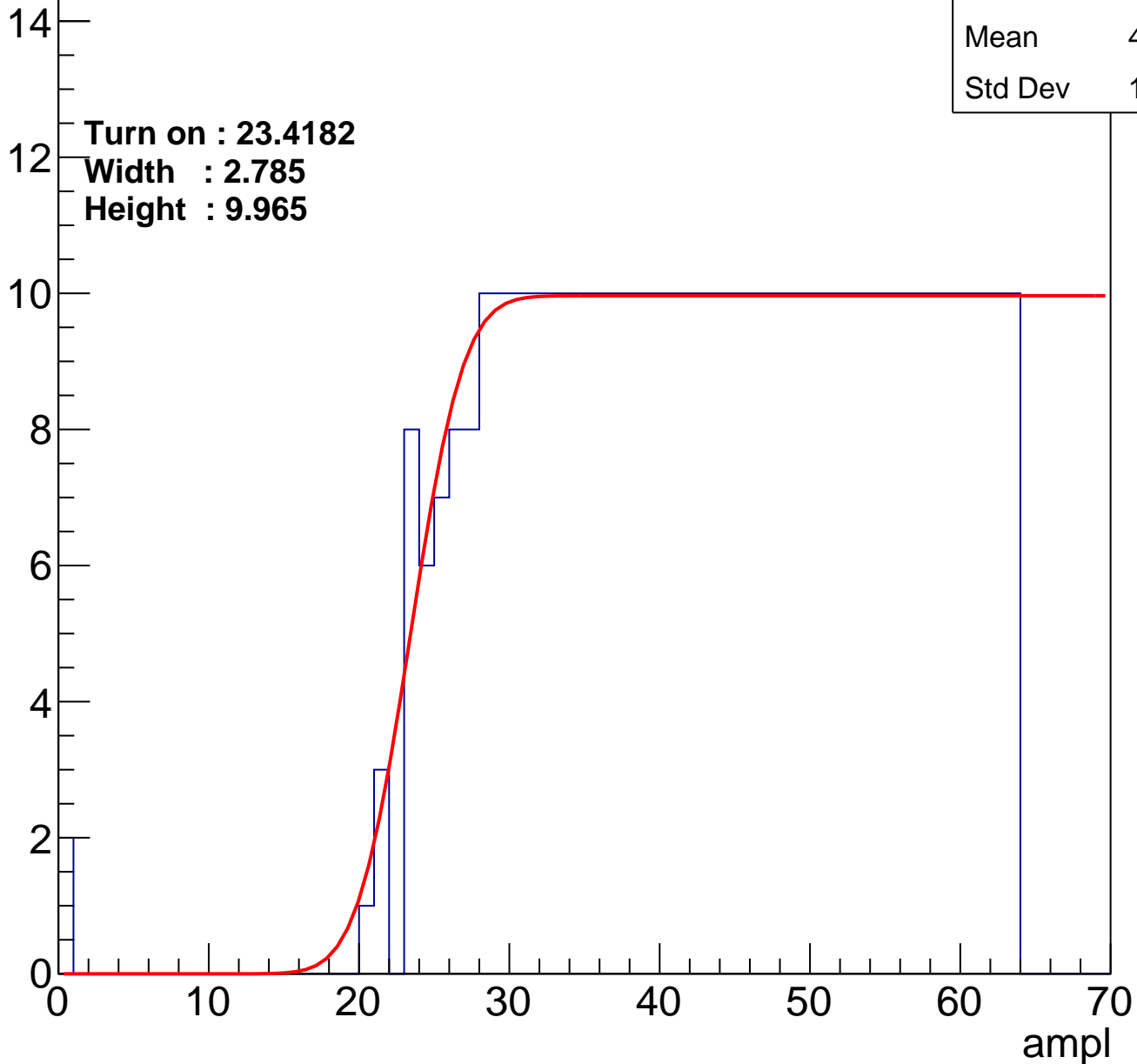
Entries	403
Mean	43.15
Std Dev	12.08

Turn on : 23.4182

Width : 2.785

Height : 9.965

Entry



# B1L002S, U21-ch1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.17
Std Dev	11.51

Turn on : 26.3546

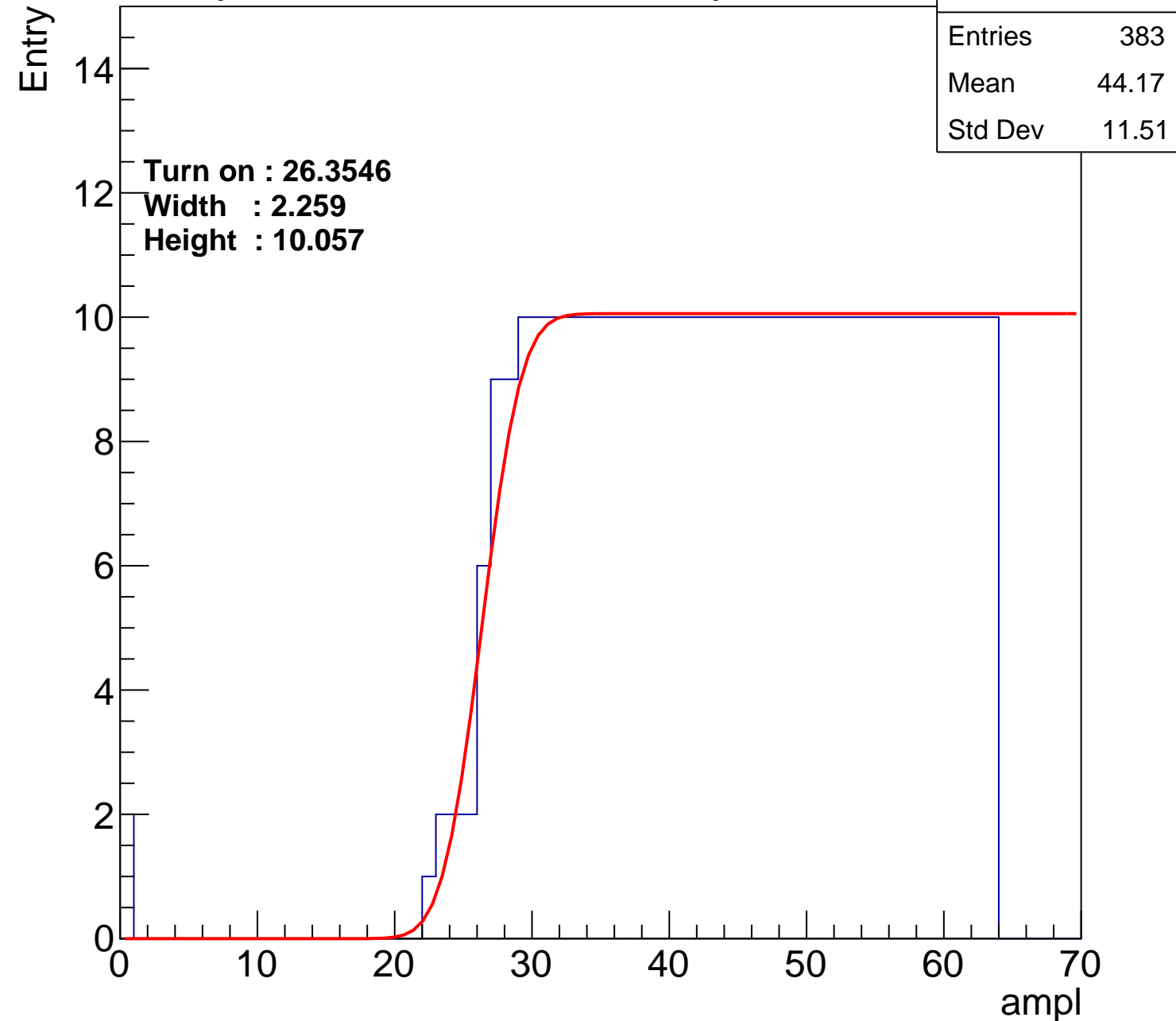
Width : 2.259

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch2

calib\_packv5\_042523\_0143.root, FC#10, port B3

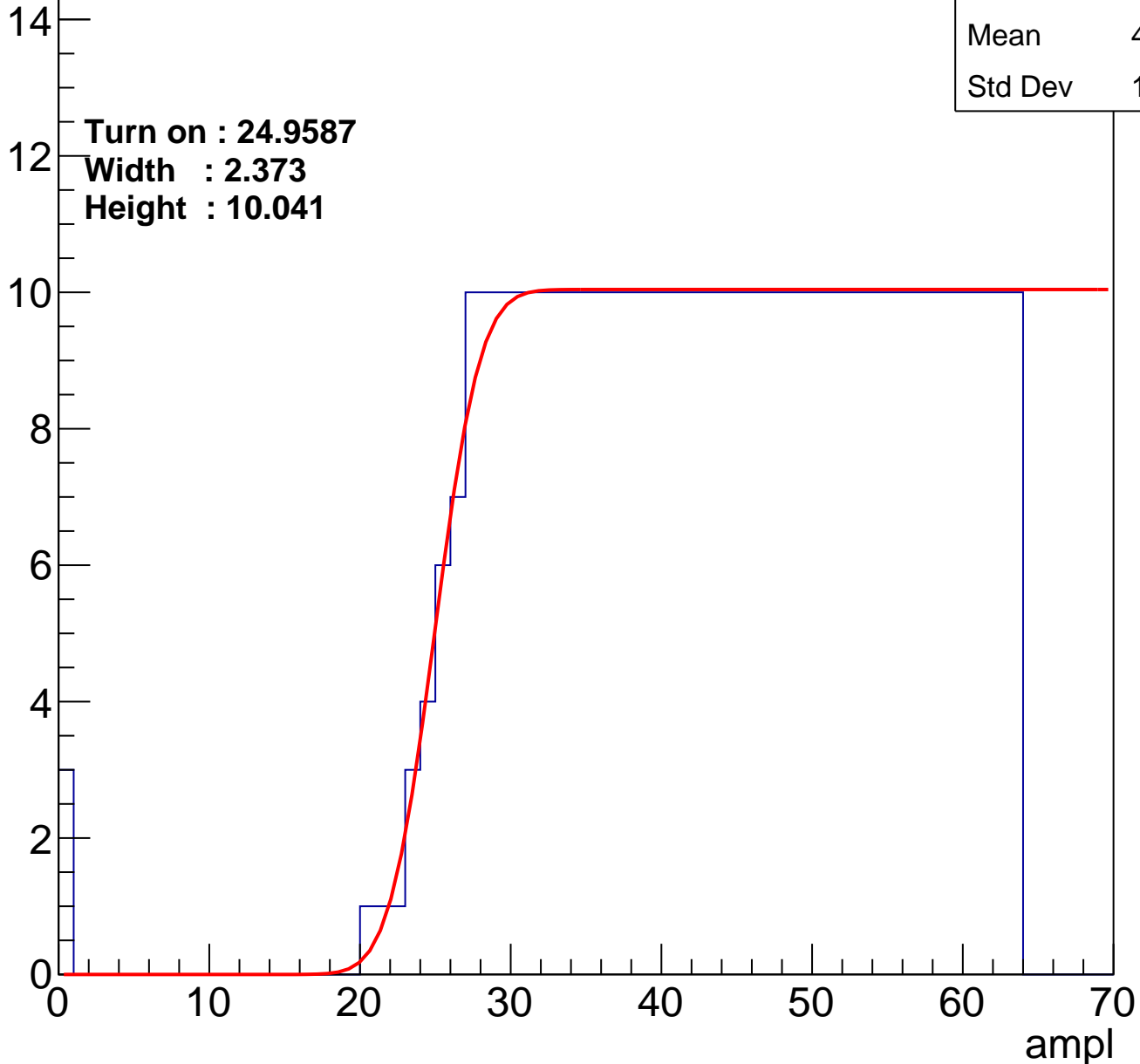
Entries	396
Mean	43.46
Std Dev	12.02

Turn on : 24.9587

Width : 2.373

Height : 10.041

Entry



# B1L002S, U21-ch3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.22
Std Dev	11.52

Turn on : 26.0679

Width : 2.917

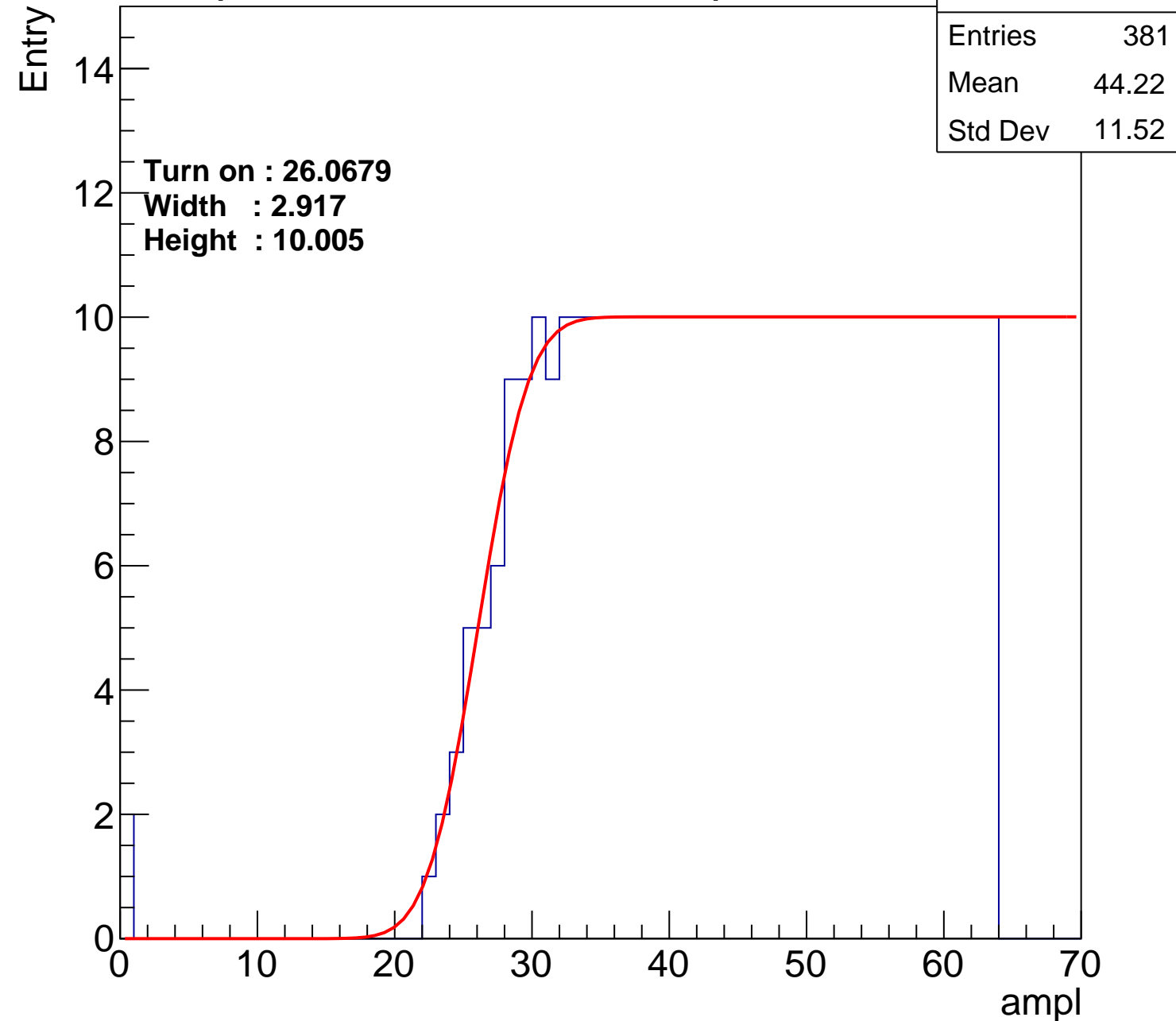
Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U21-ch4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.9668

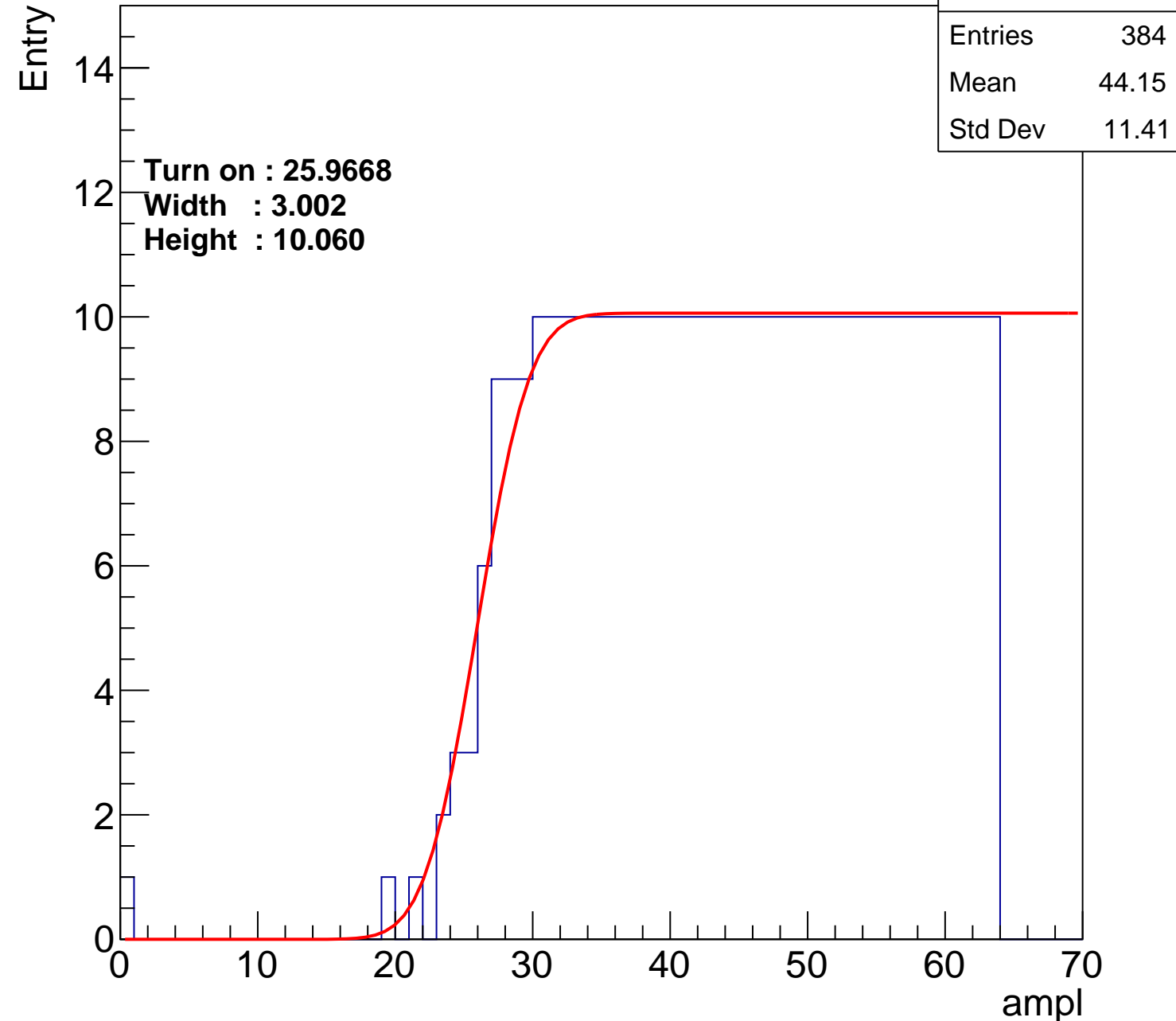
Width : 3.002

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 26.2530

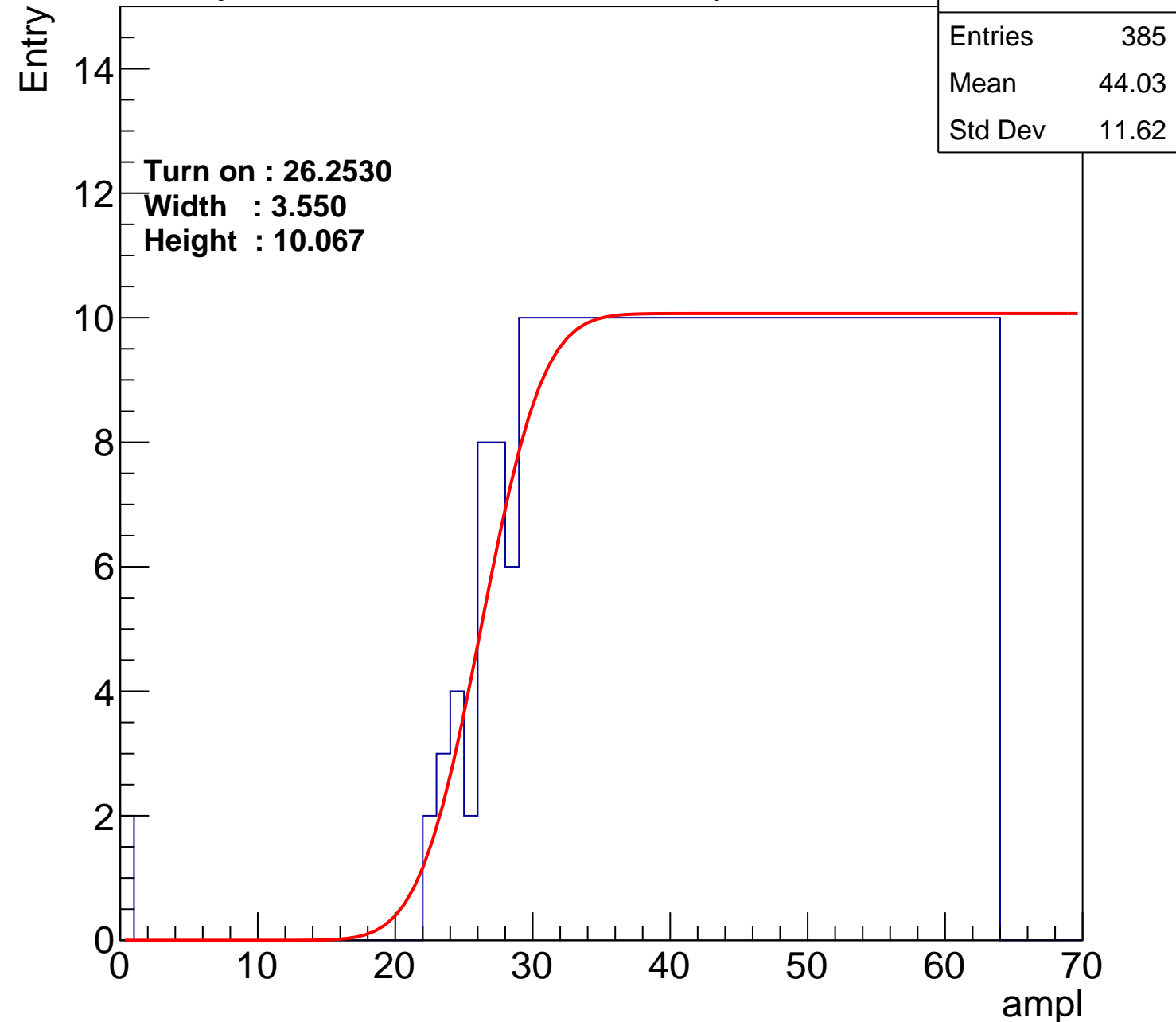
Width : 3.550

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.67

Turn on : 25.6620

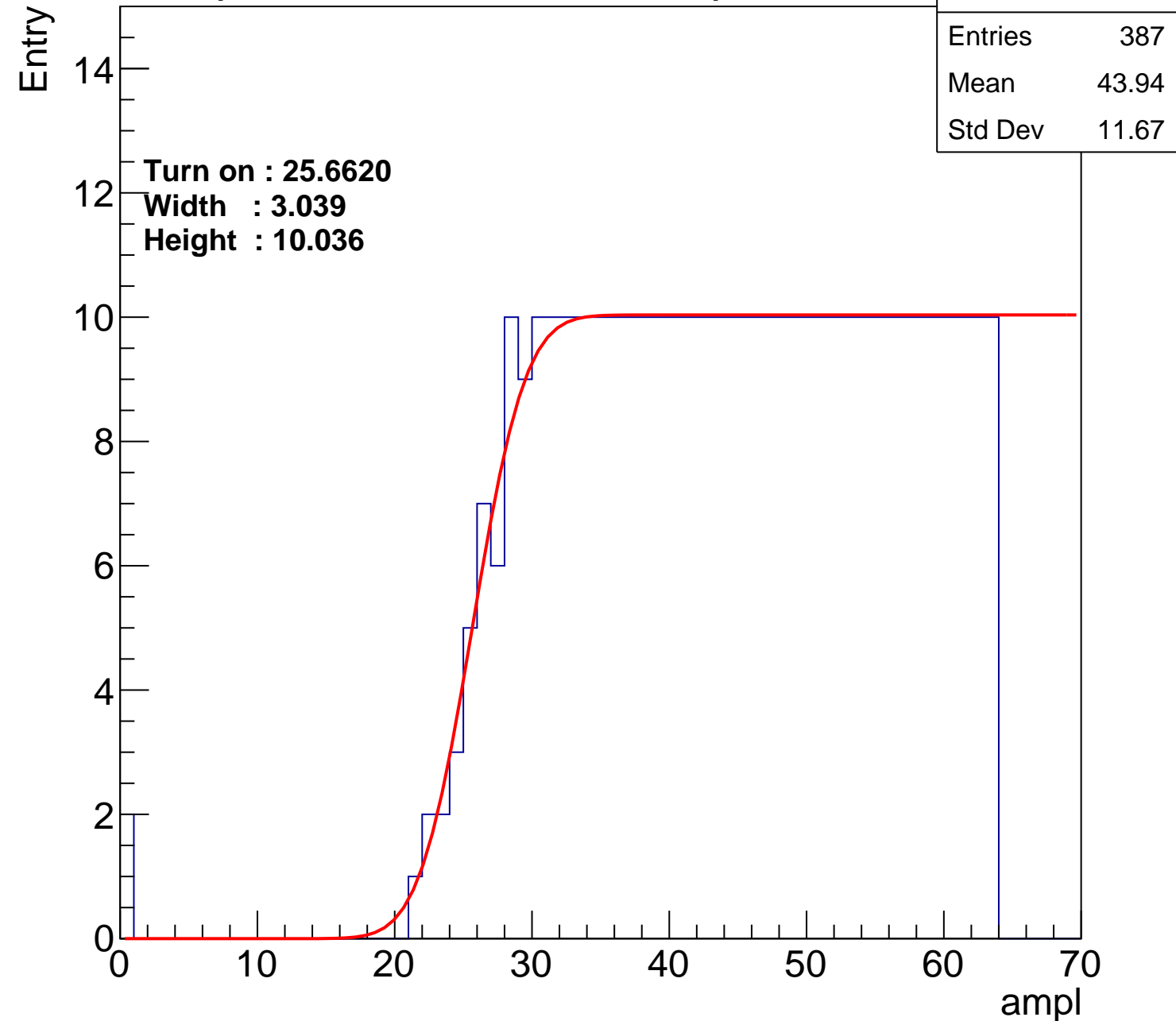
Width : 3.039

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	44.1
Std Dev	11.4

Turn on : 25.8348

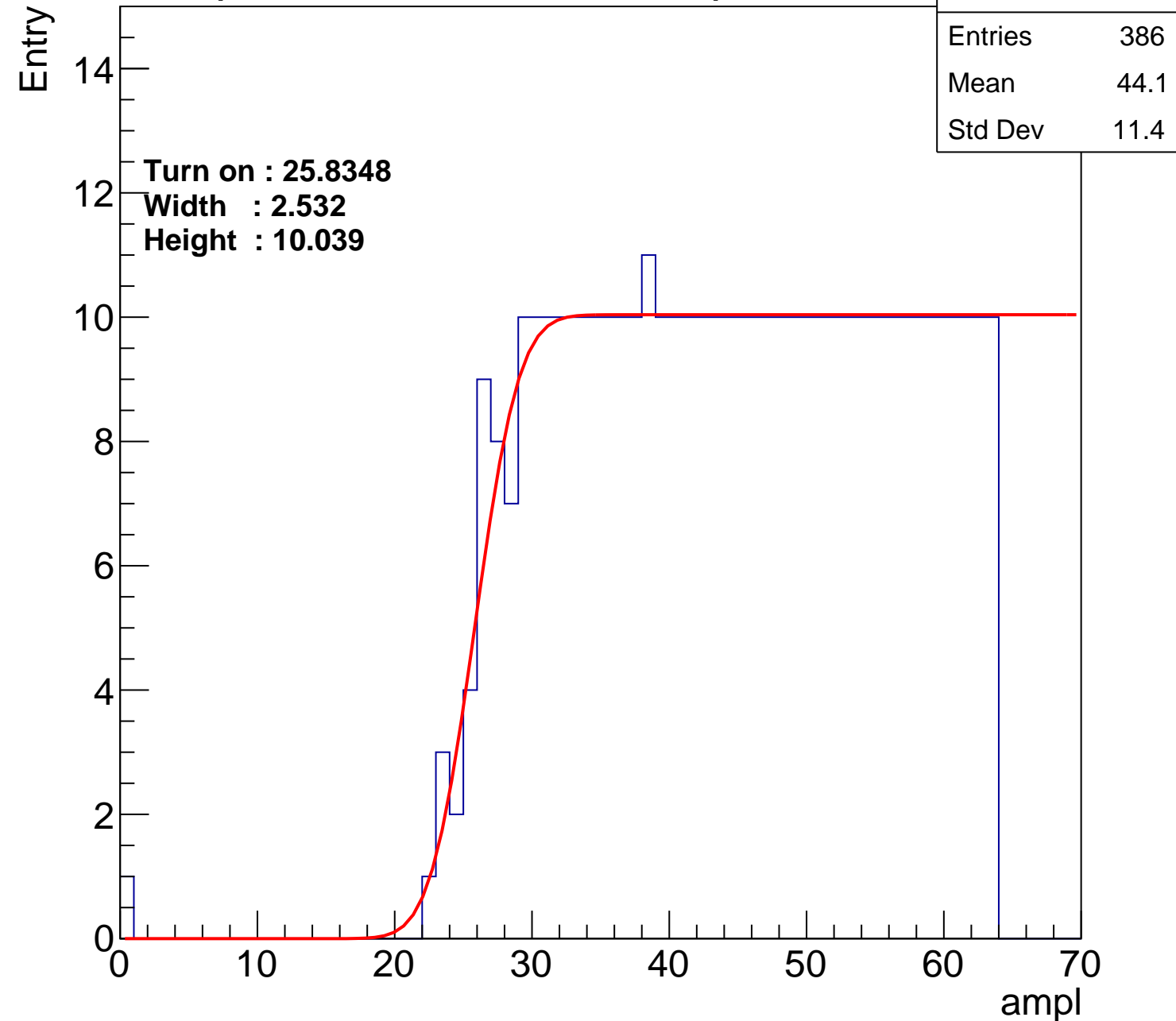
Width : 2.532

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch8

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.27
Std Dev	12.49

Turn on : 25.4158

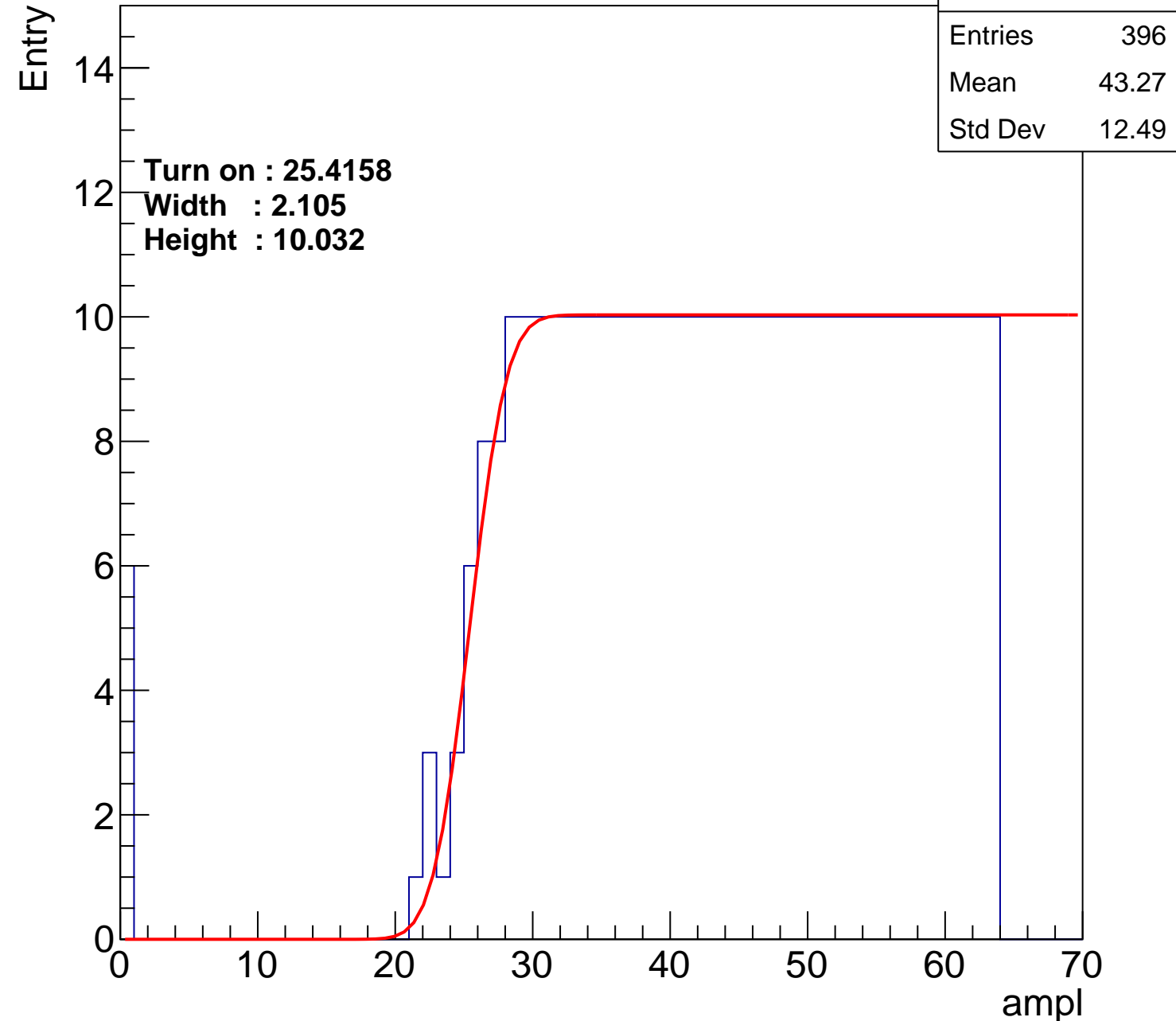
Width : 2.105

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch9

calib\_packv5\_042523\_0143.root, FC#10, port B3

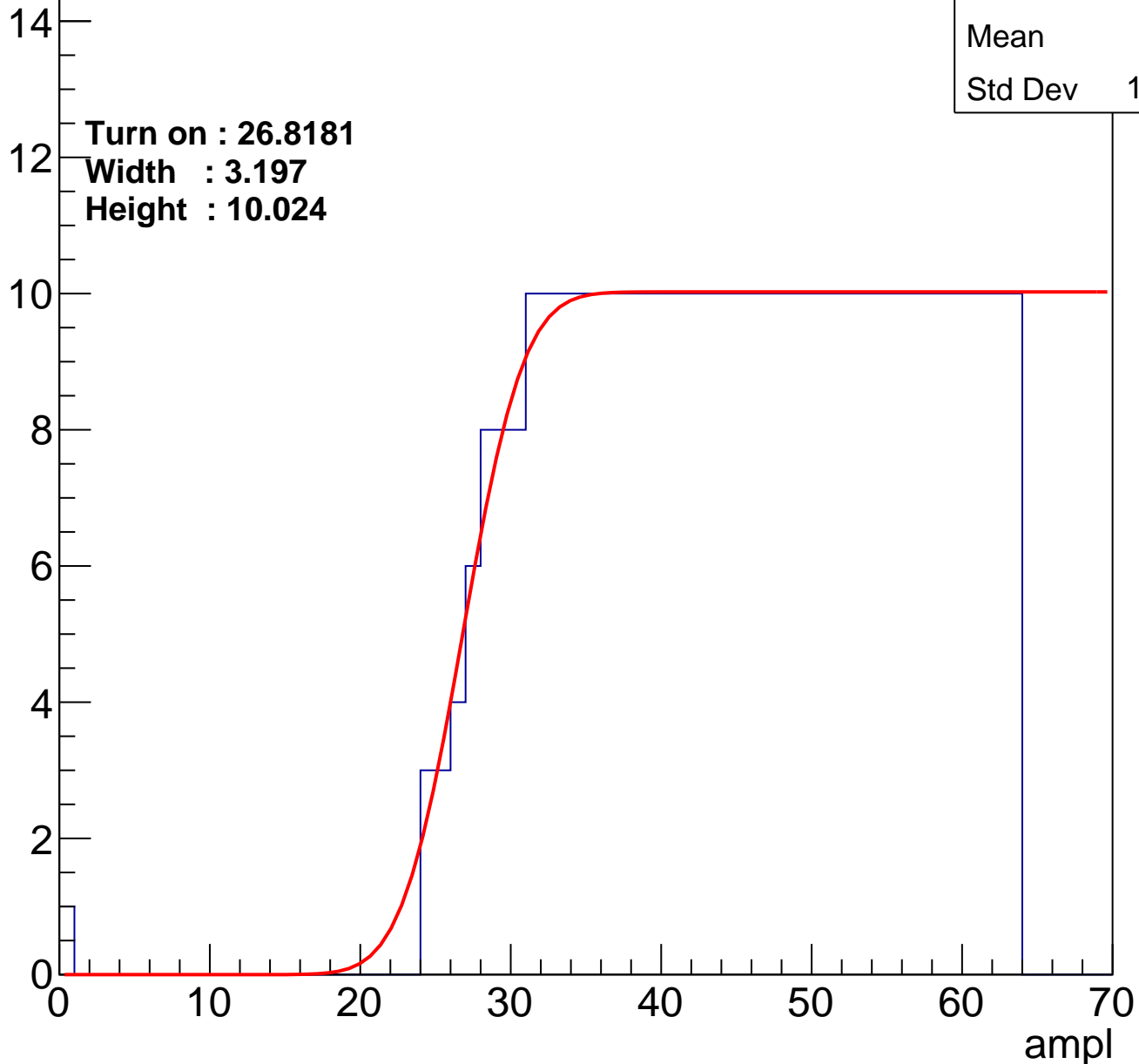
Entries	371
Mean	44.8
Std Dev	11.05

Turn on : 26.8181

Width : 3.197

Height : 10.024

Entry



# B1L002S, U21-ch10

calib\_packv5\_042523\_0143.root, FC#10, port B3

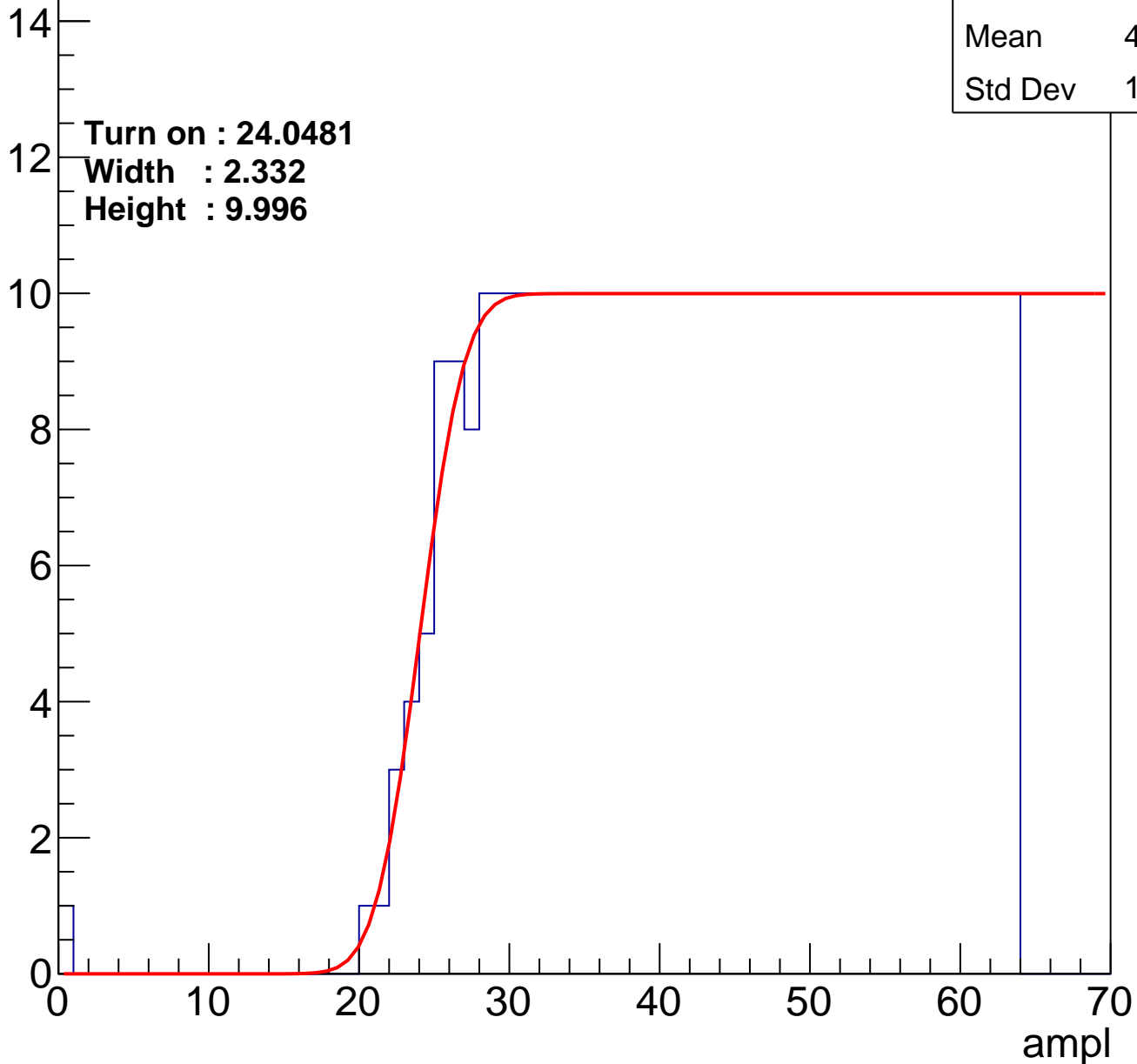
Entries	401
Mean	43.33
Std Dev	11.84

Turn on : 24.0481

Width : 2.332

Height : 9.996

Entry



# B1L002S, U21-ch11

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.21
Std Dev	11.52

Turn on : 26.4270

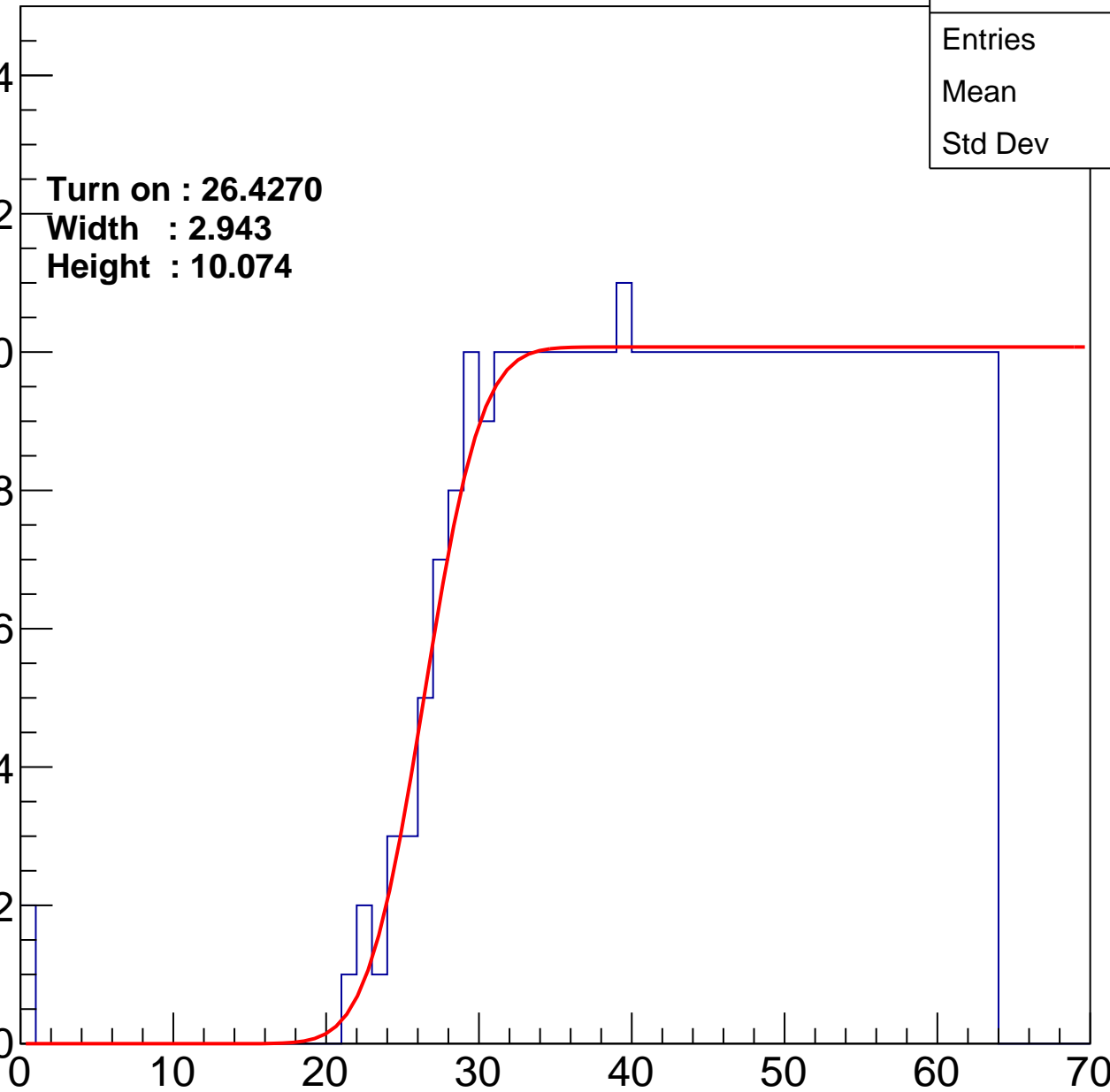
Width : 2.943

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch12

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.1137

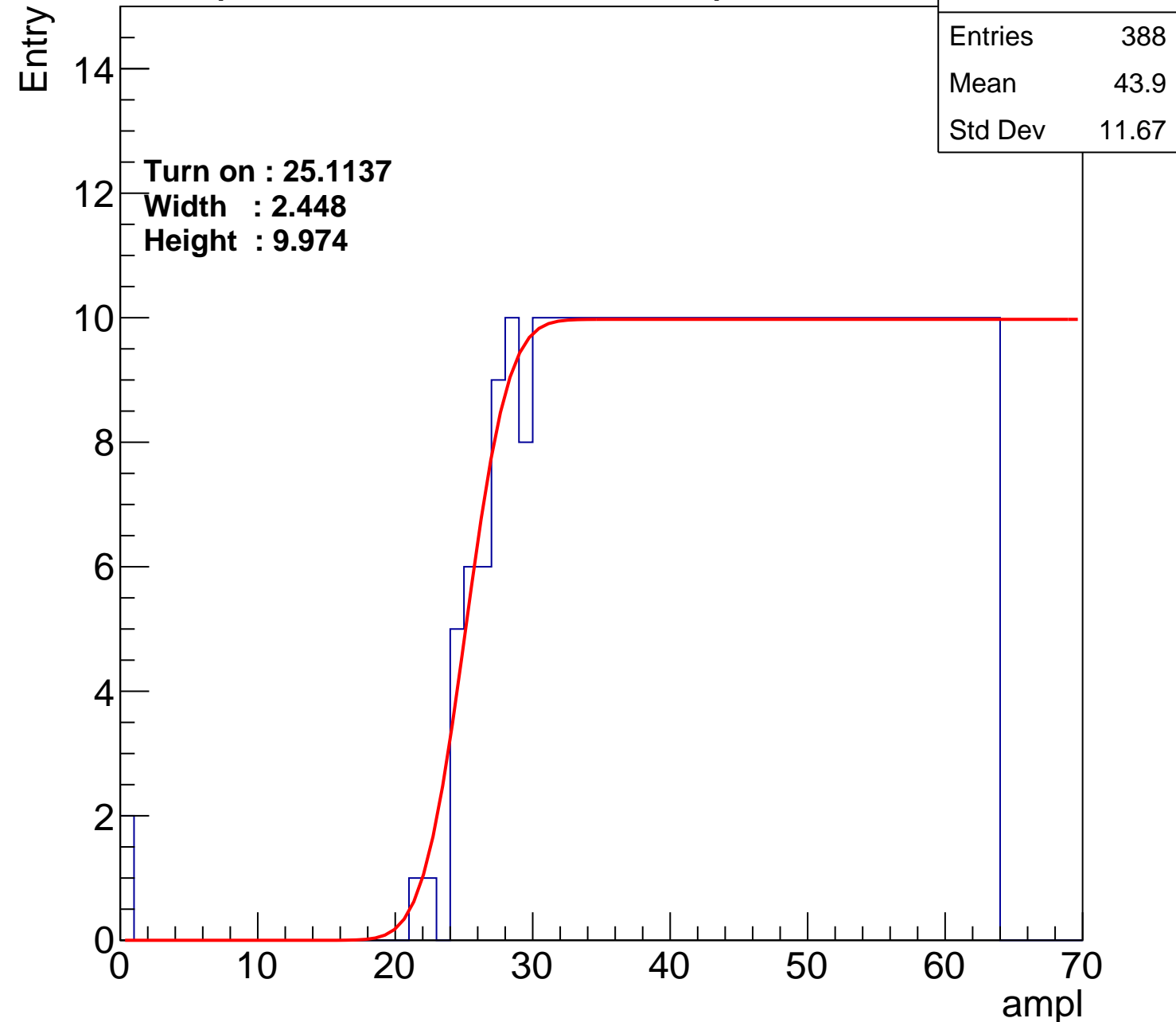
Width : 2.448

Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch13

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.62
Std Dev	11.85

Turn on : 25.0015

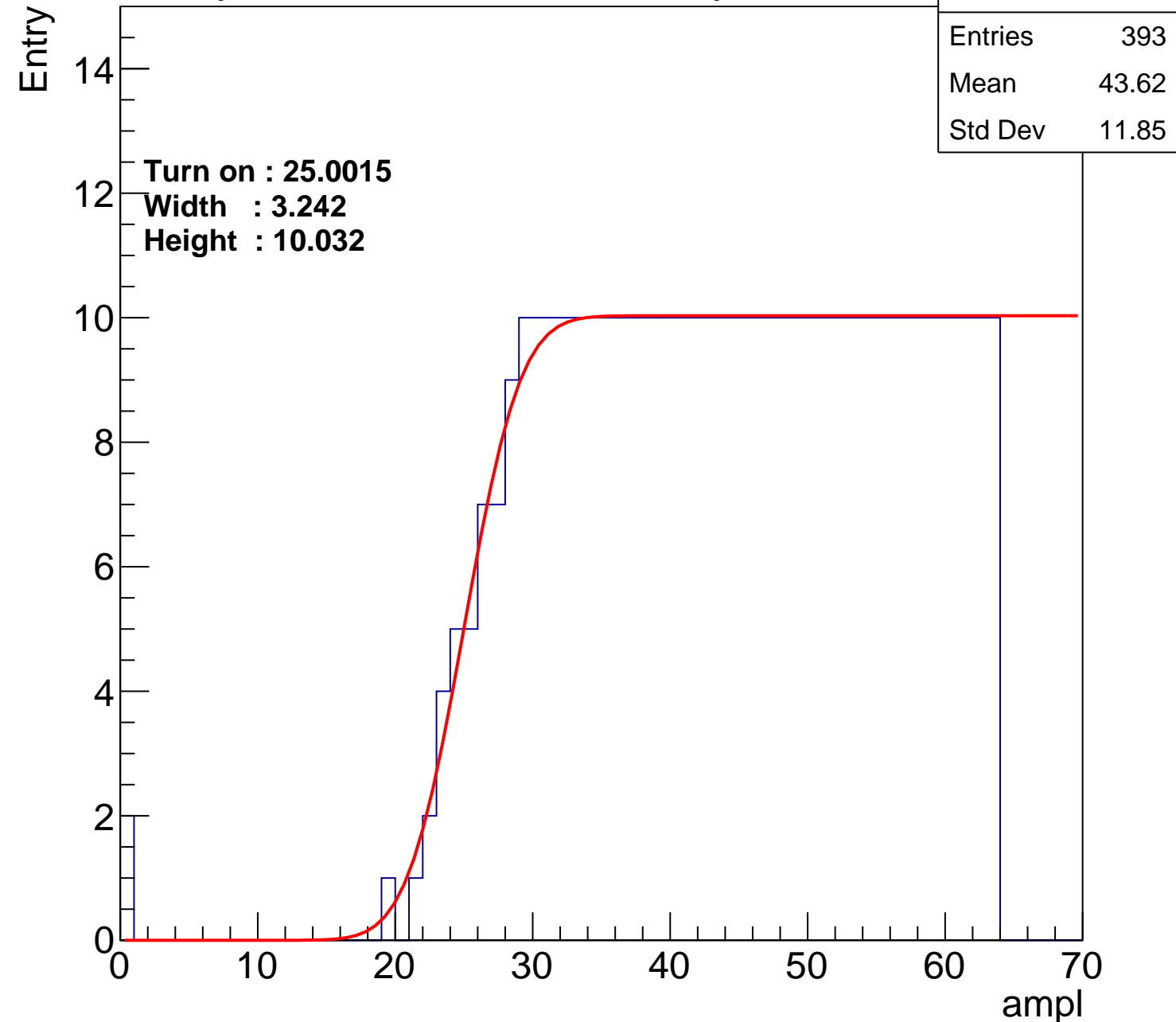
Width : 3.242

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch14

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	400
Mean	43.31
Std Dev	11.98

Turn on : 24.5774

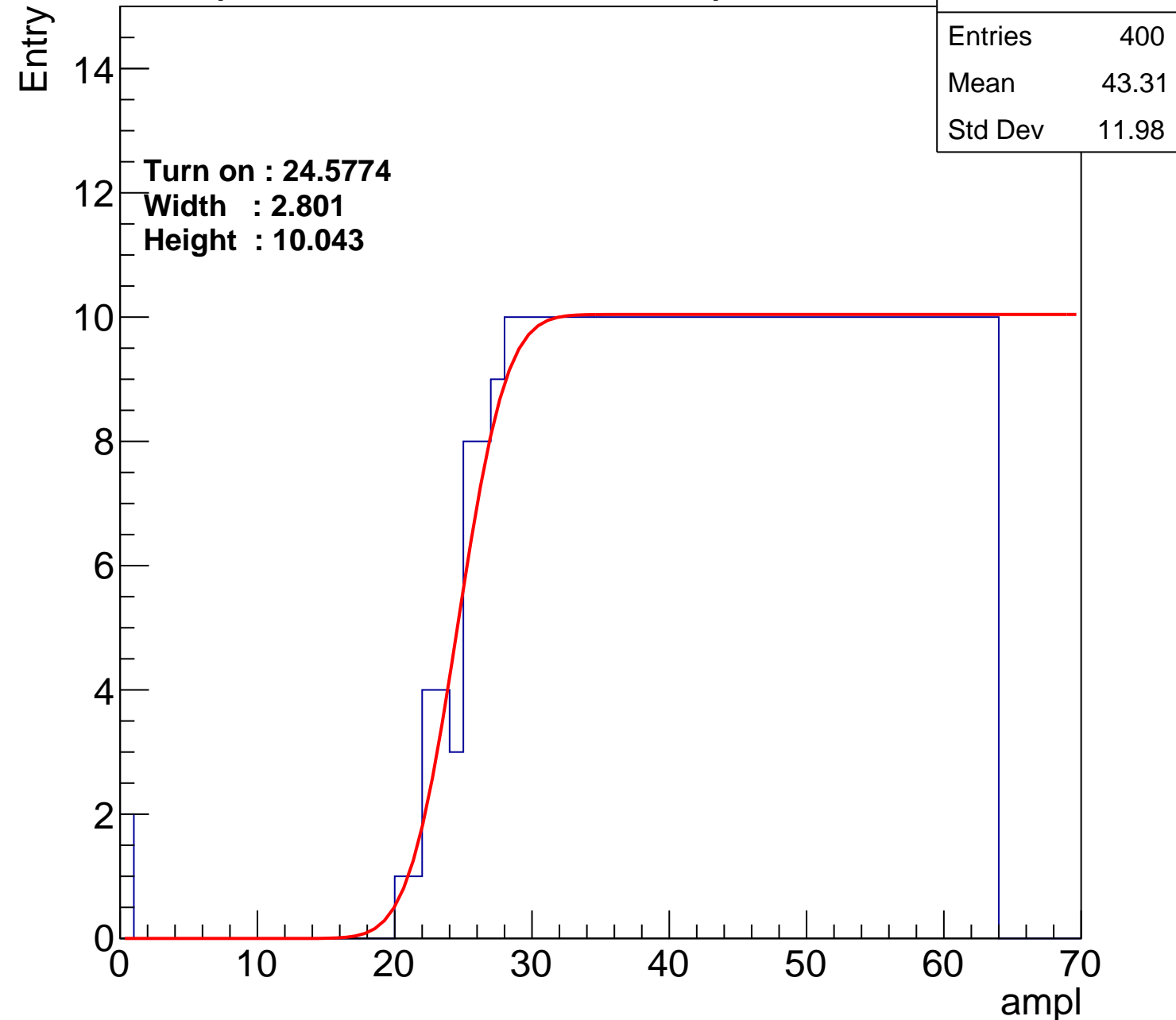
Width : 2.801

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch15

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.4157

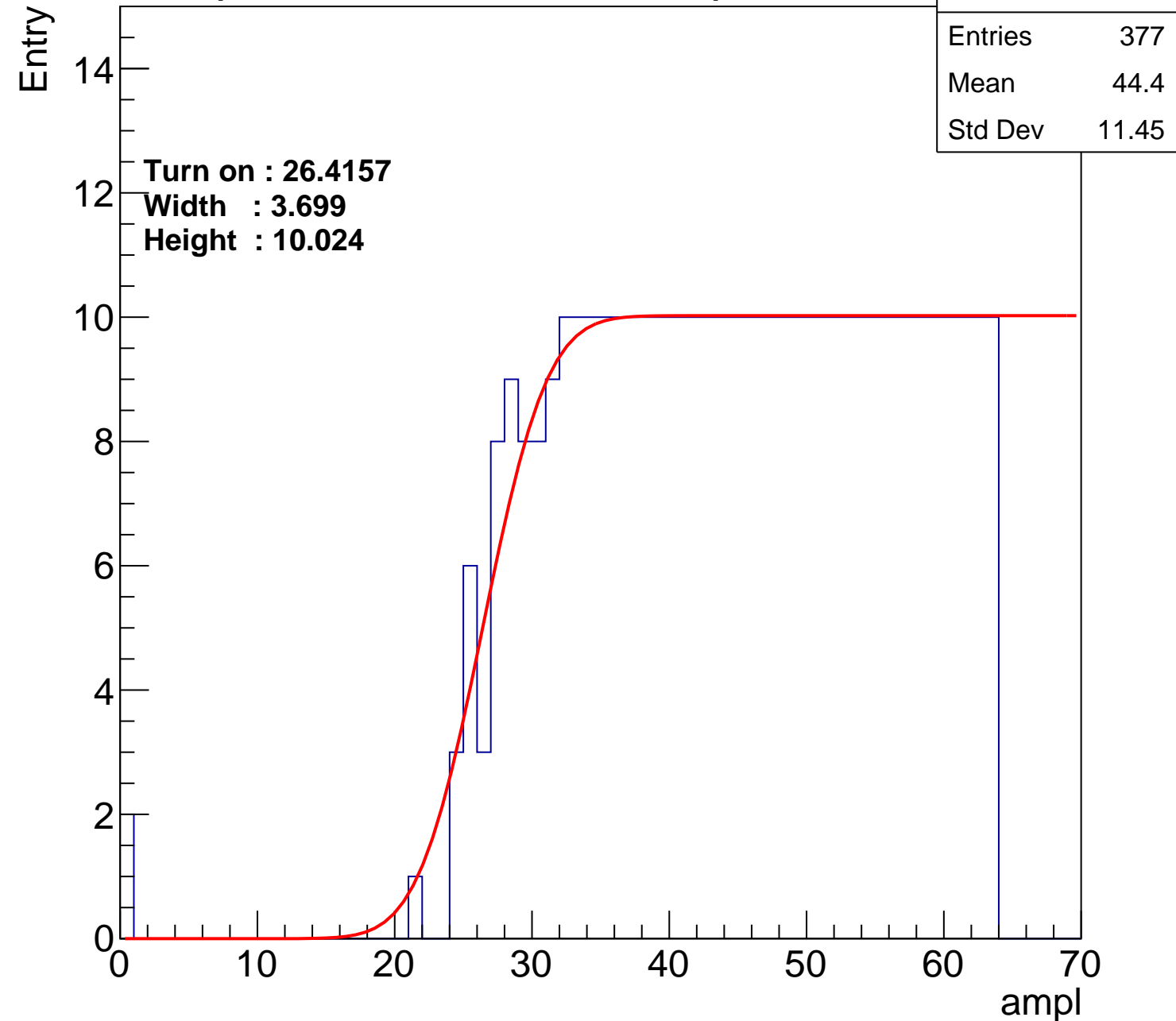
Width : 3.699

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch16

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.04
Std Dev	11.66

Turn on : 25.8670

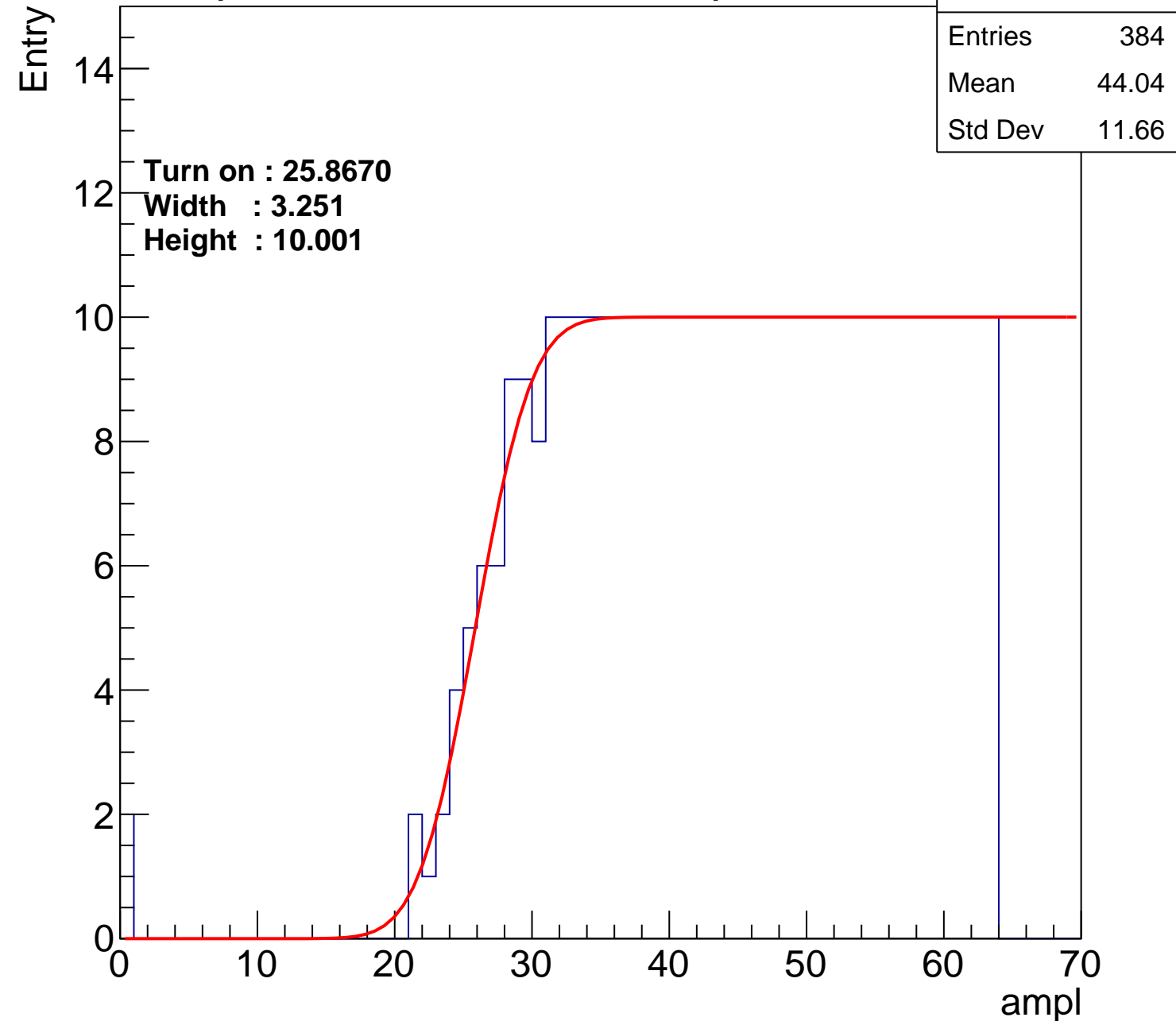
Width : 3.251

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch17

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.57
Std Dev	11.72

Turn on : 24.6159

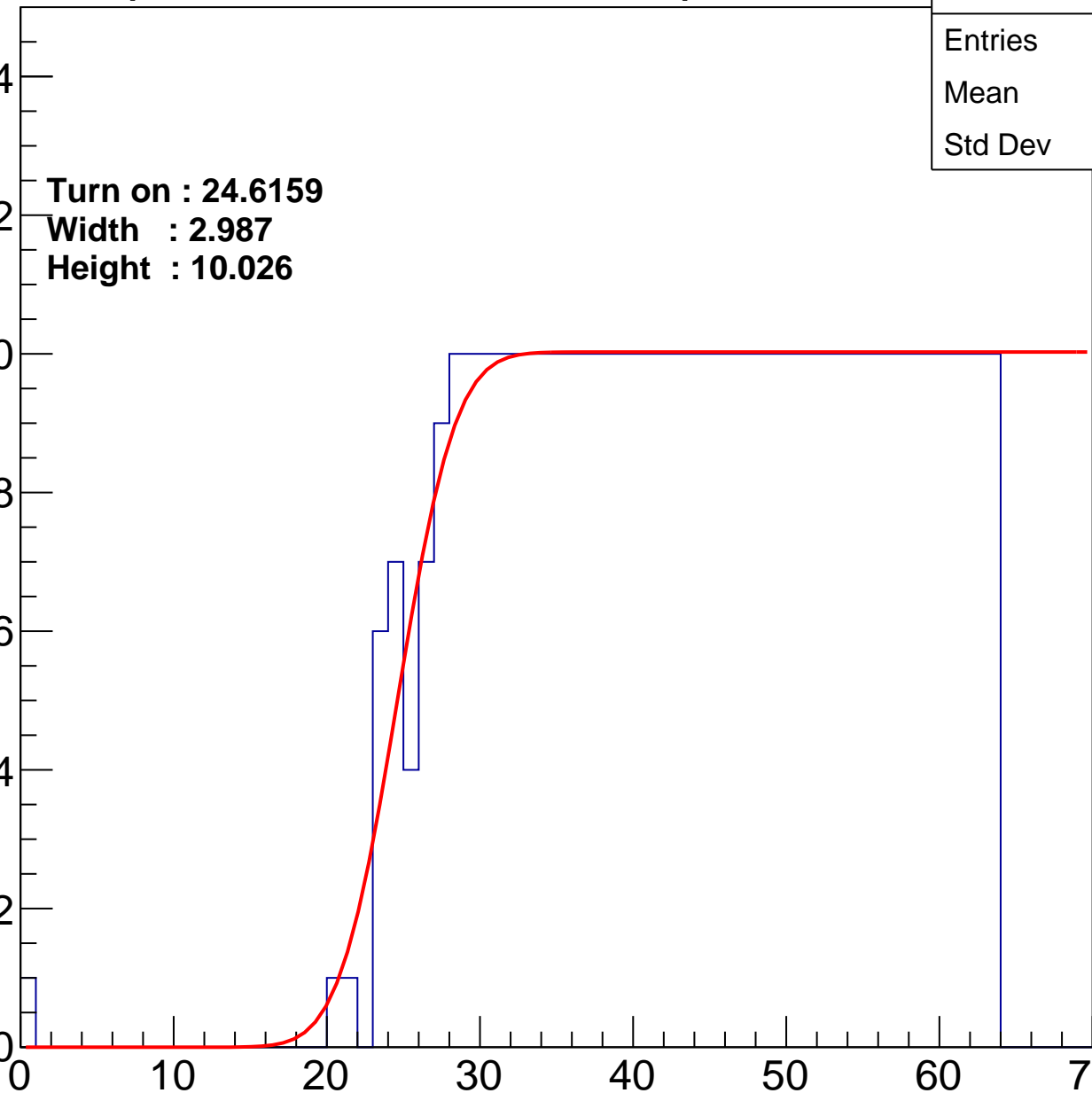
Width : 2.987

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch18

calib\_packv5\_042523\_0143.root, FC#10, port B3

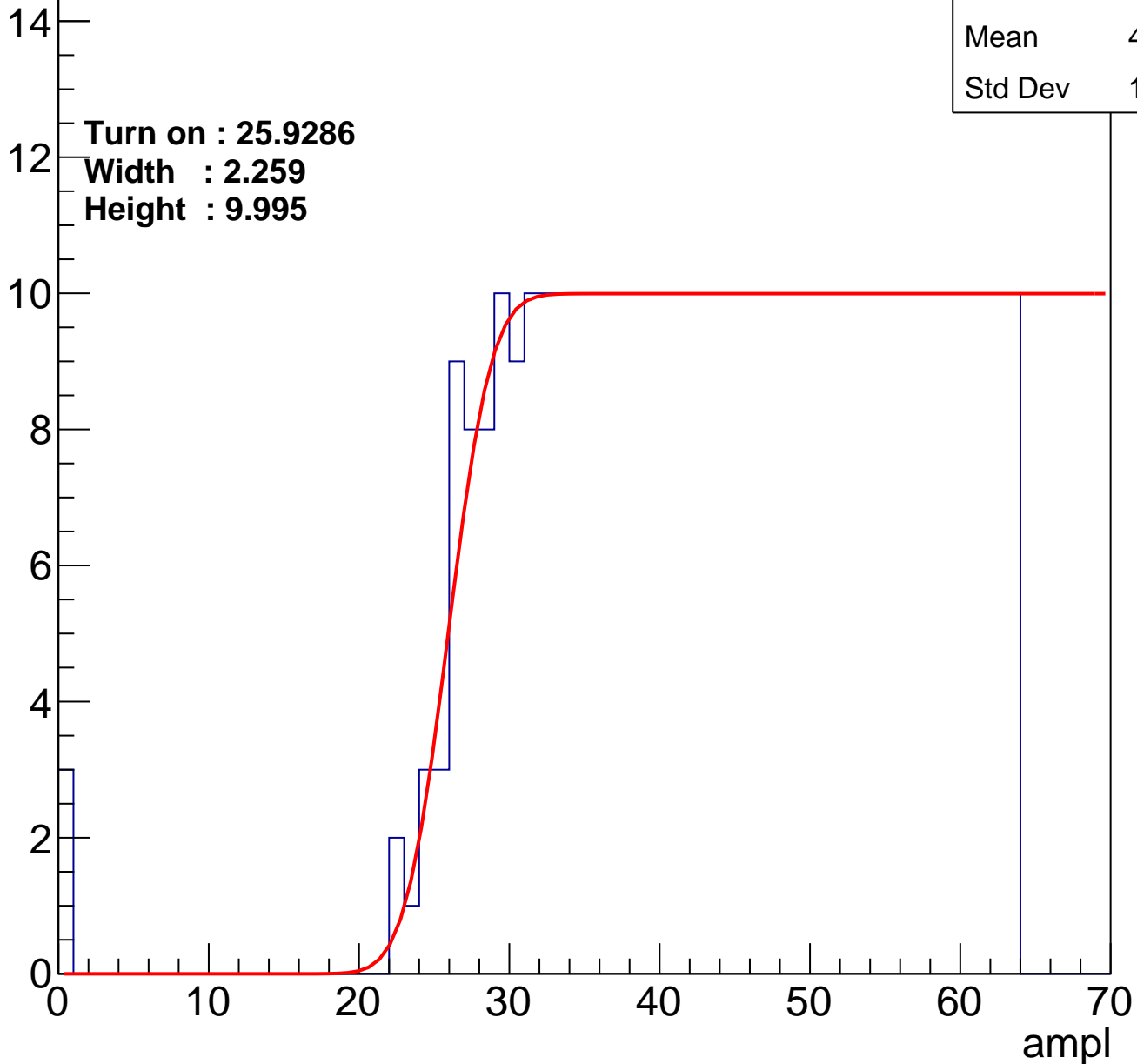
Entries	386
Mean	43.93
Std Dev	11.79

Turn on : 25.9286

Width : 2.259

Height : 9.995

Entry



# B1L002S, U21-ch19

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.35
Std Dev	11.57

Turn on : 27.0569

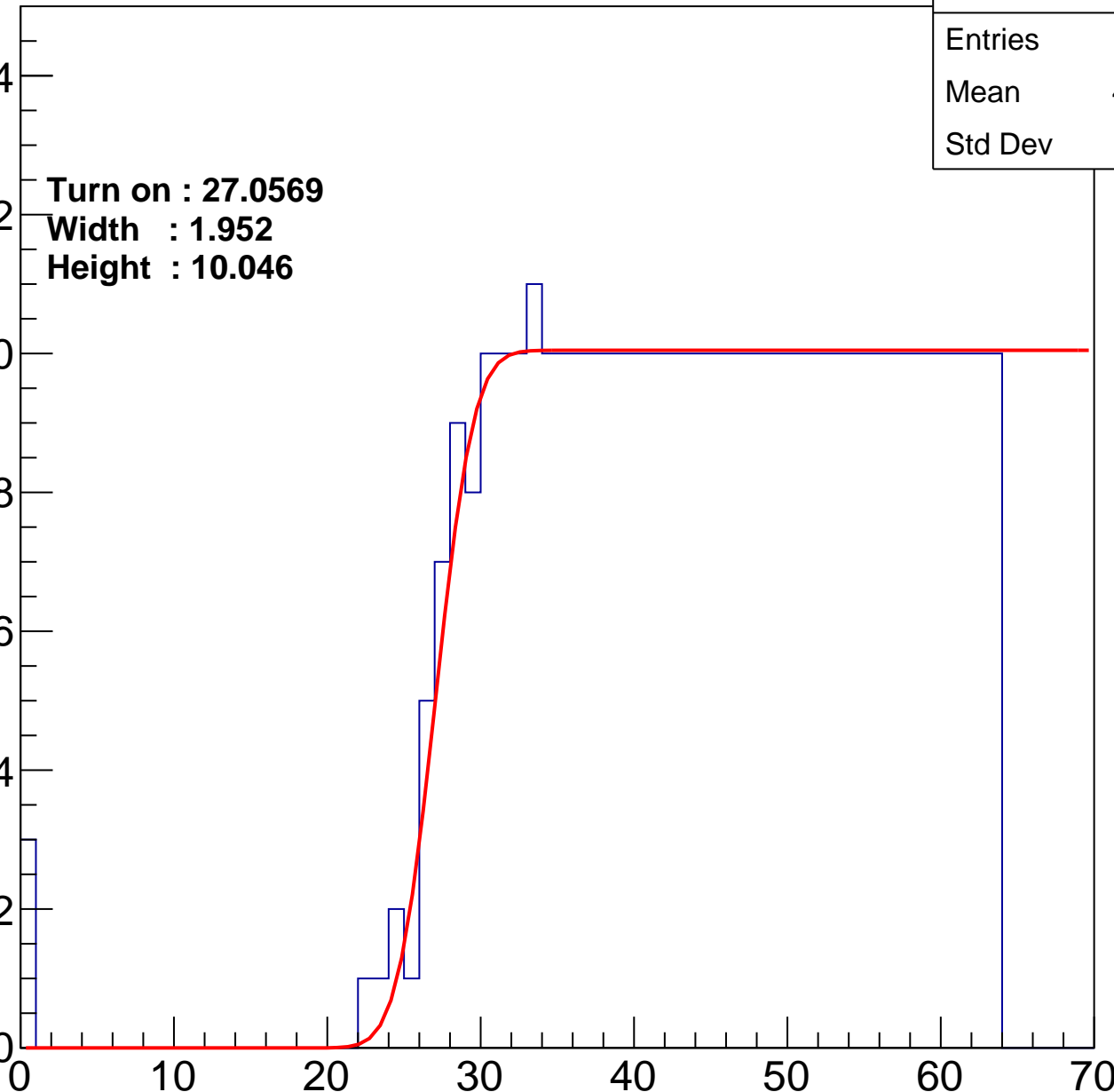
Width : 1.952

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch20

calib\_packv5\_042523\_0143.root, FC#10, port B3

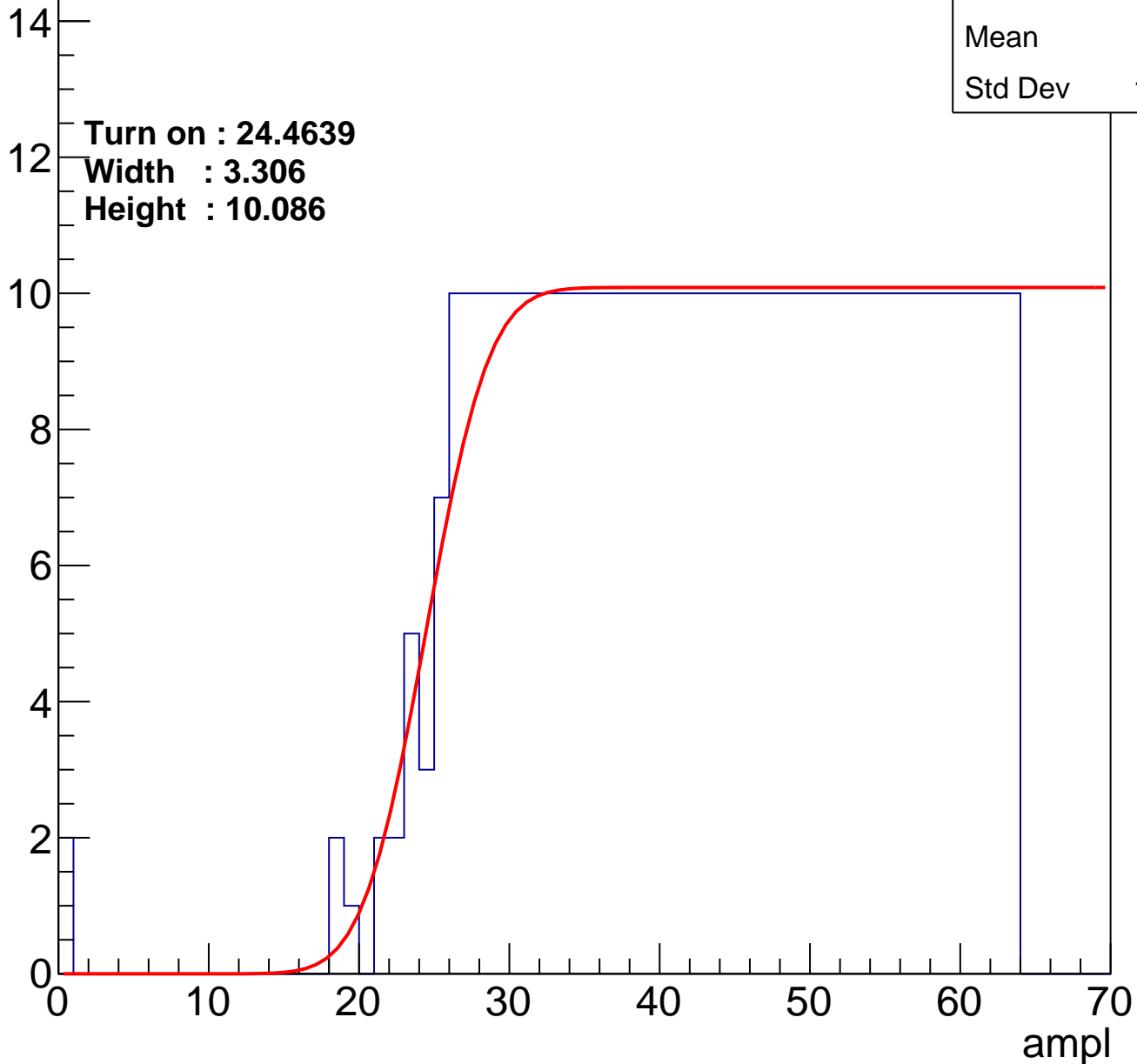
Entries	404
Mean	43.1
Std Dev	12.11

Turn on : 24.4639

Width : 3.306

Height : 10.086

Entry



# B1L002S, U21-ch21

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.93
Std Dev	11.82

Turn on : 25.5494

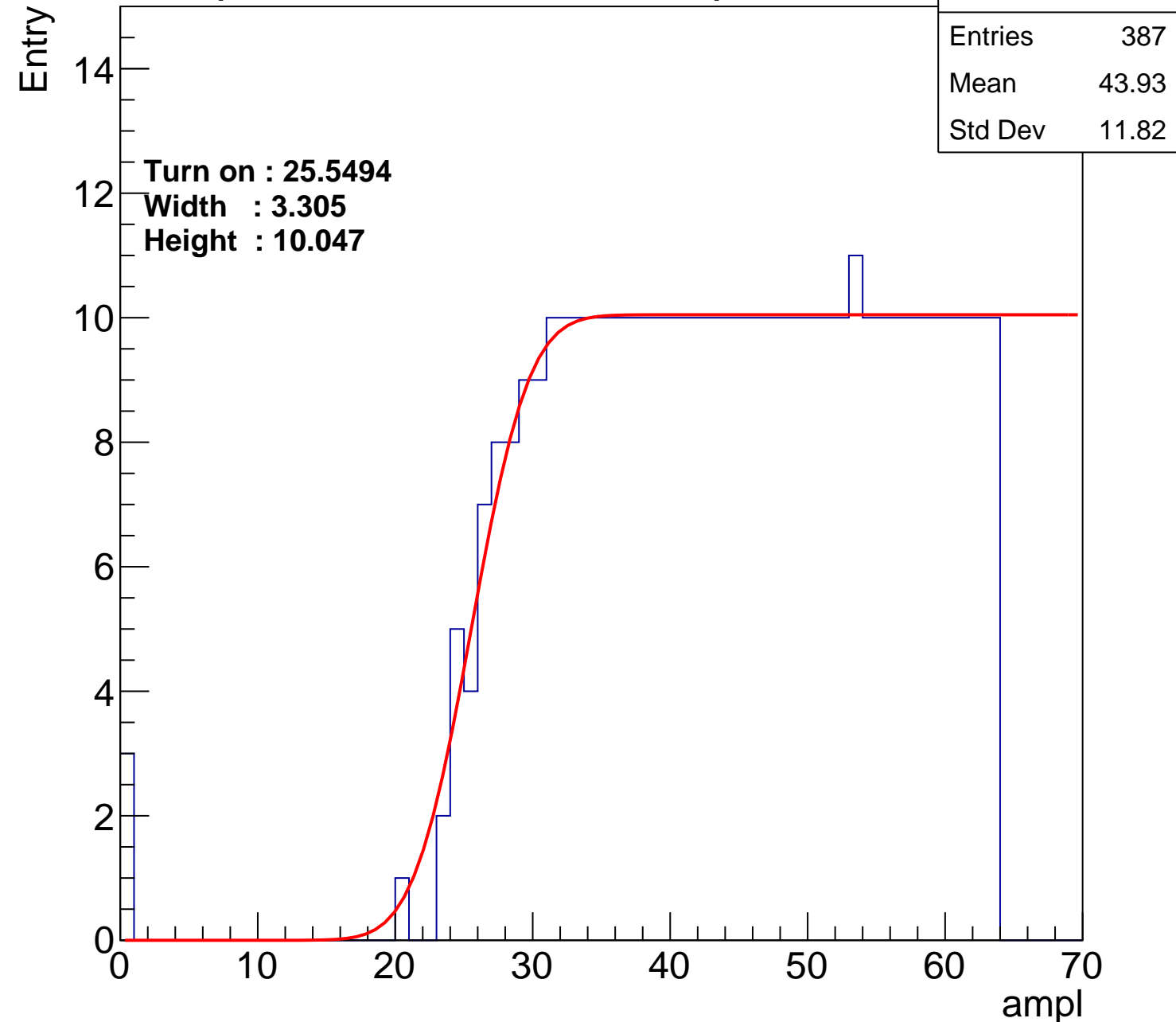
Width : 3.305

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch22

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.2
Std Dev	12.44

Turn on : 25.1701

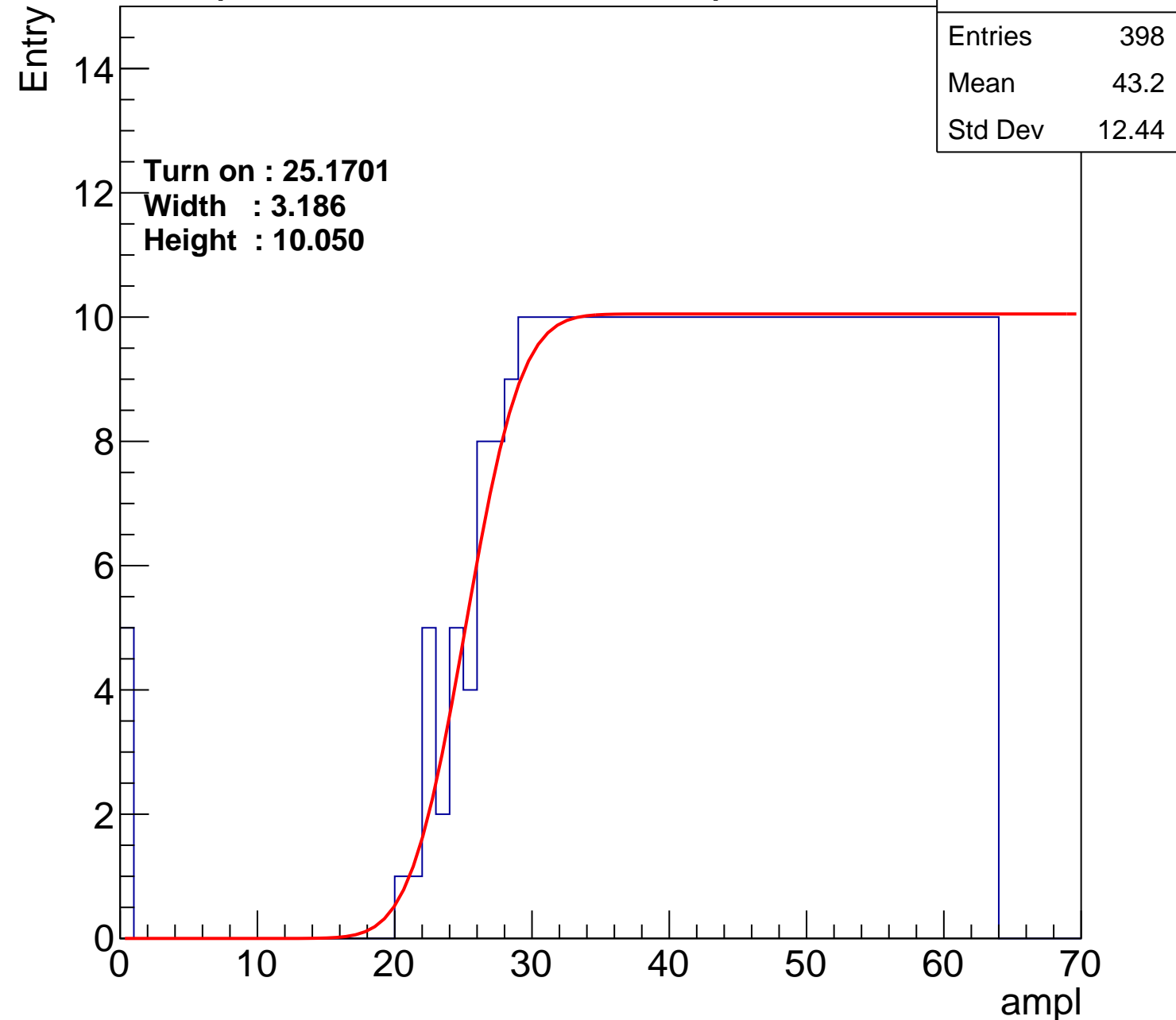
Width : 3.186

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch23

calib\_packv5\_042523\_0143.root, FC#10, port B3

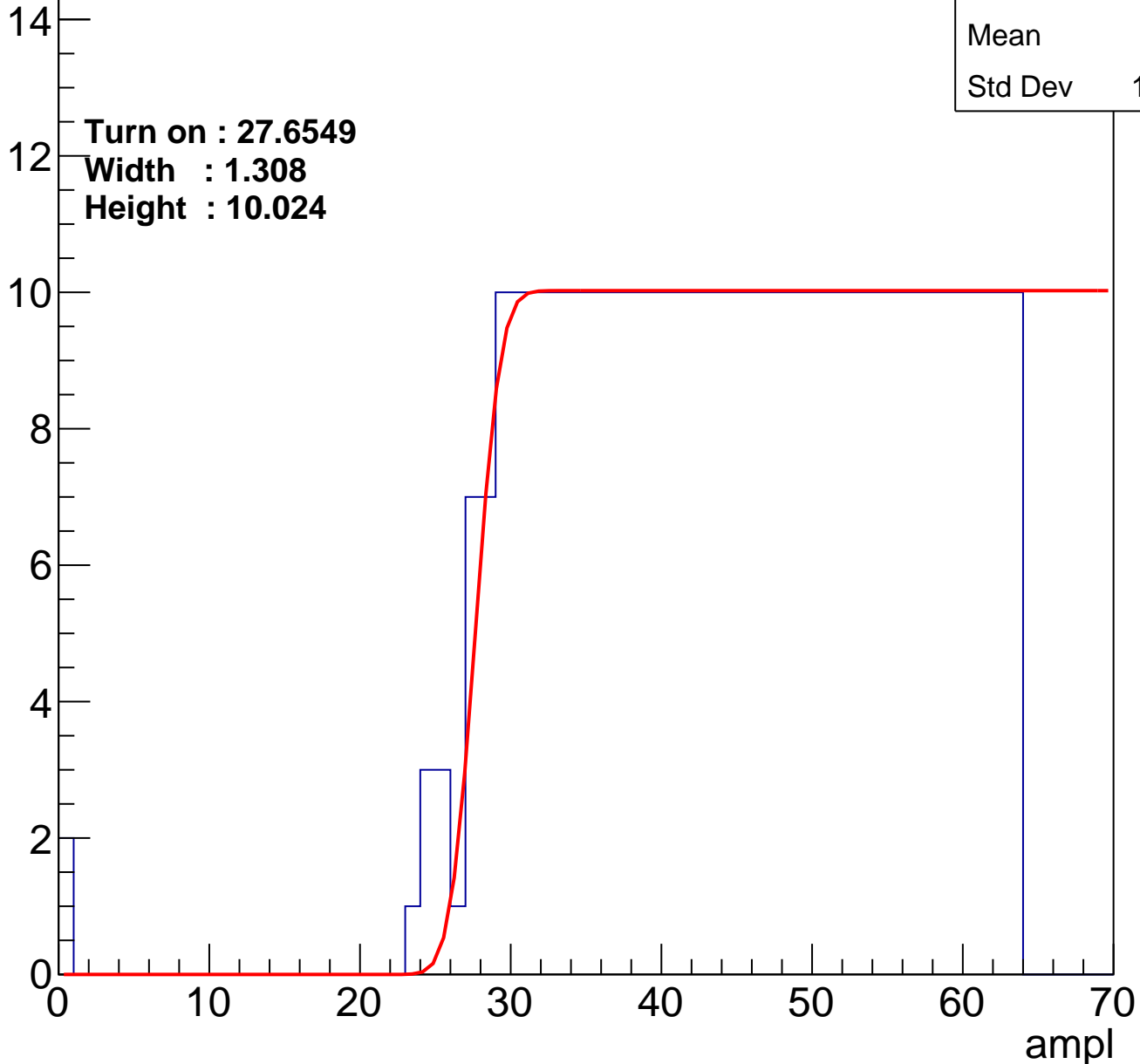
Entries	374
Mean	44.6
Std Dev	11.29

Turn on : 27.6549

Width : 1.308

Height : 10.024

Entry



# B1L002S, U21-ch24

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 25.9721

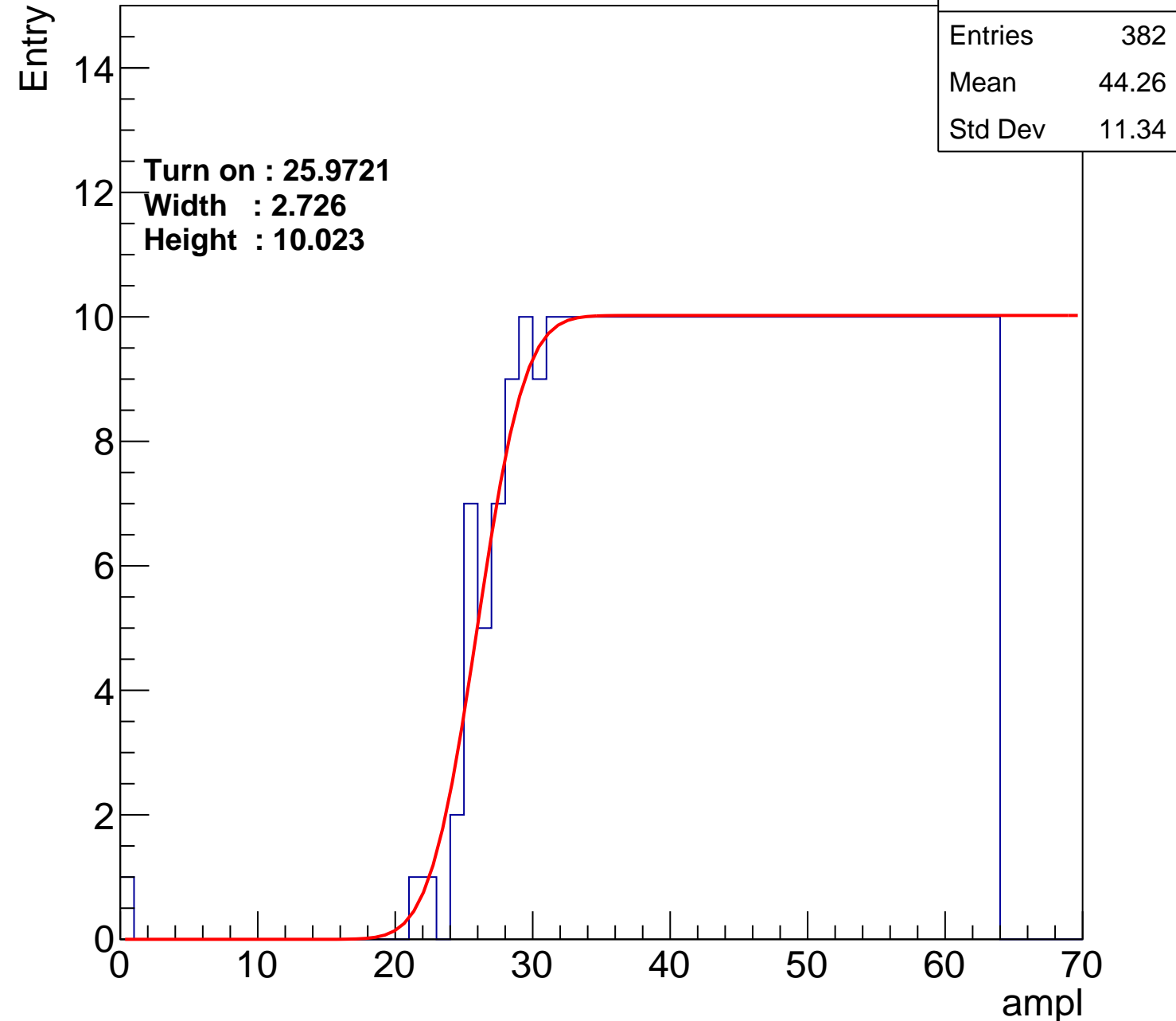
Width : 2.726

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch25

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 26.1842

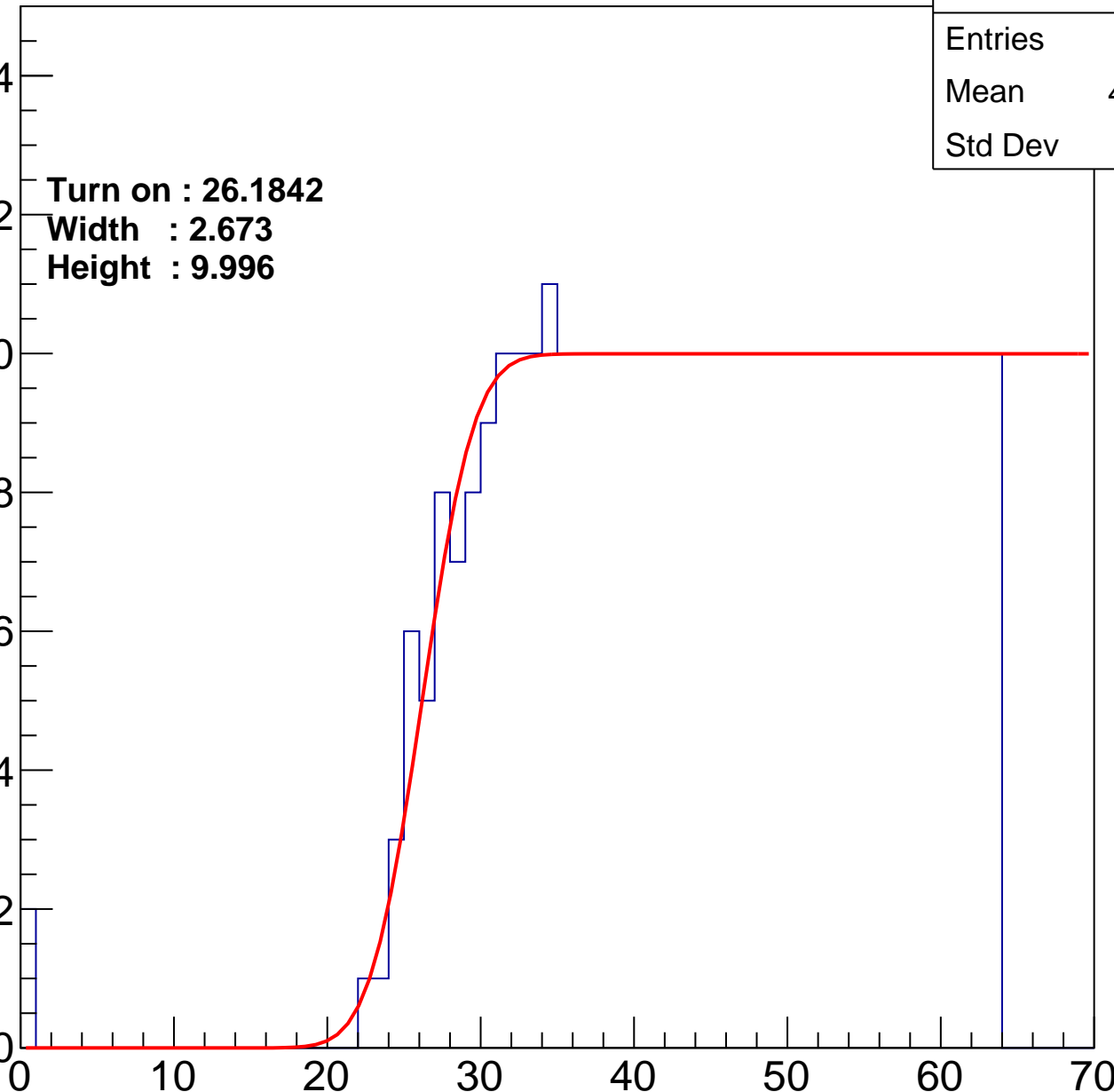
Width : 2.673

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch26

calib\_packv5\_042523\_0143.root, FC#10, port B3

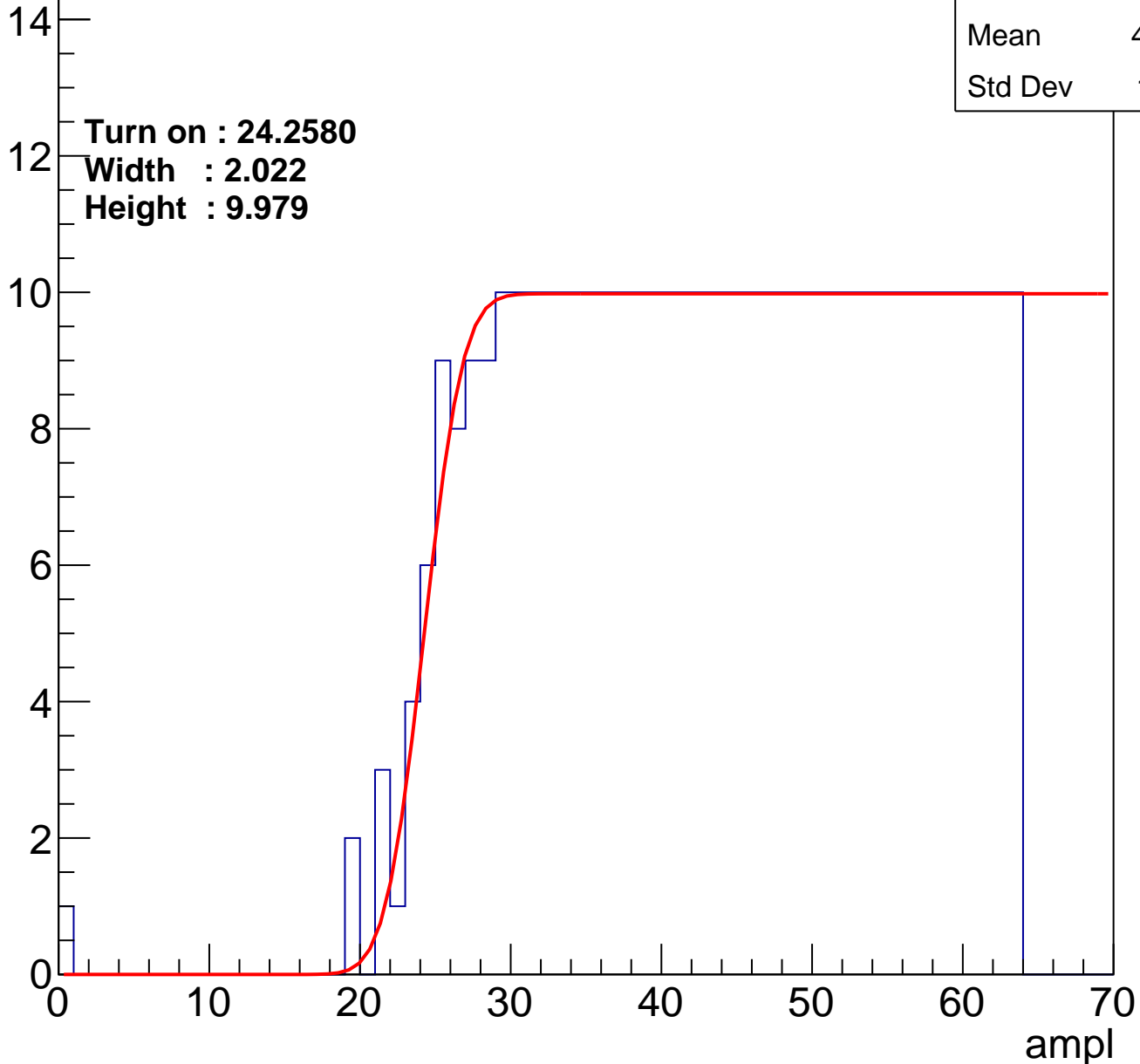
Entries	402
Mean	43.25
Std Dev	11.91

Turn on : 24.2580

Width : 2.022

Height : 9.979

Entry



# B1L002S, U21-ch27

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.4
Std Dev	12.16

Turn on : 24.7299

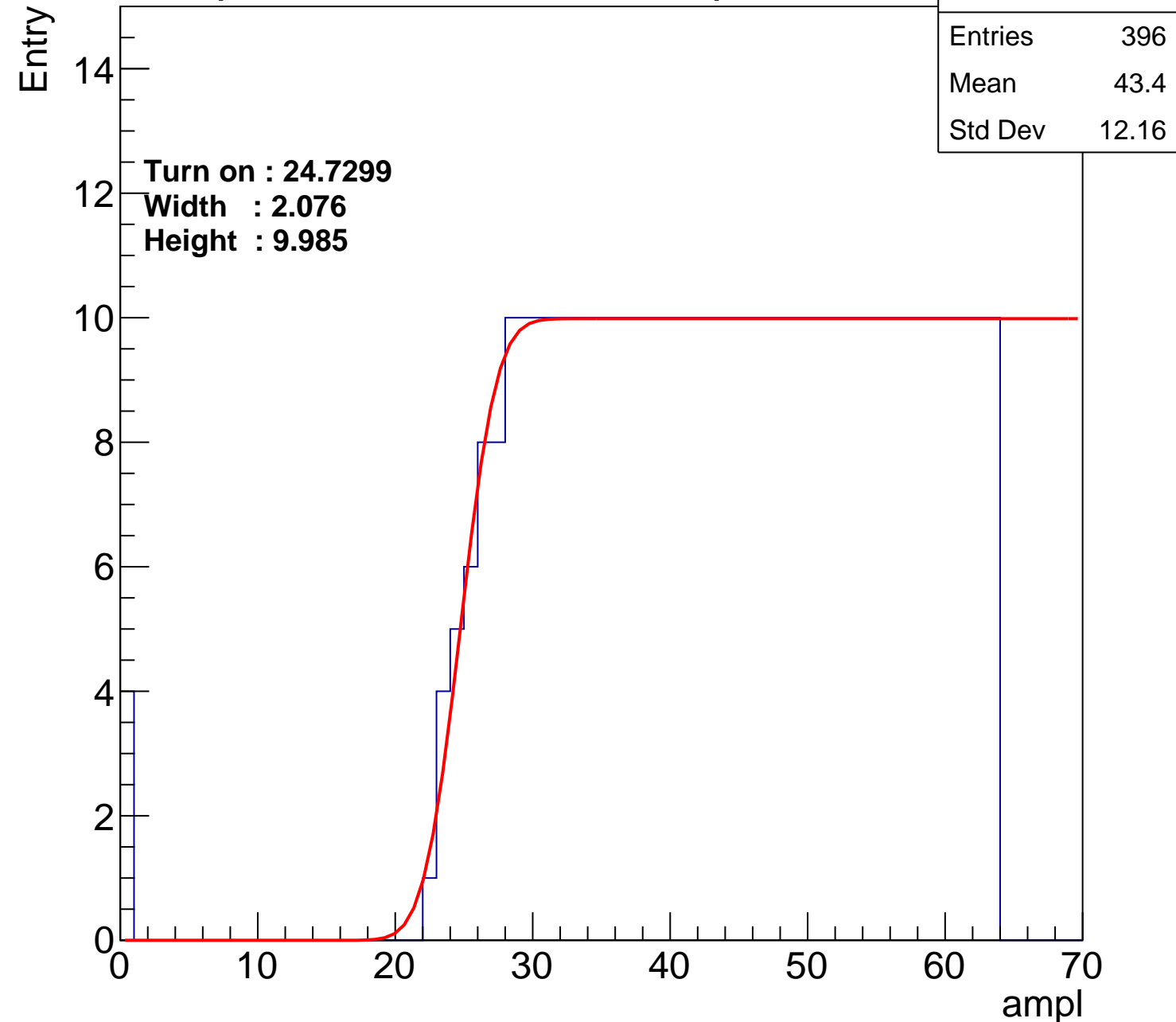
Width : 2.076

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch28

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44.13
Std Dev	11.38

Turn on : 25.4222

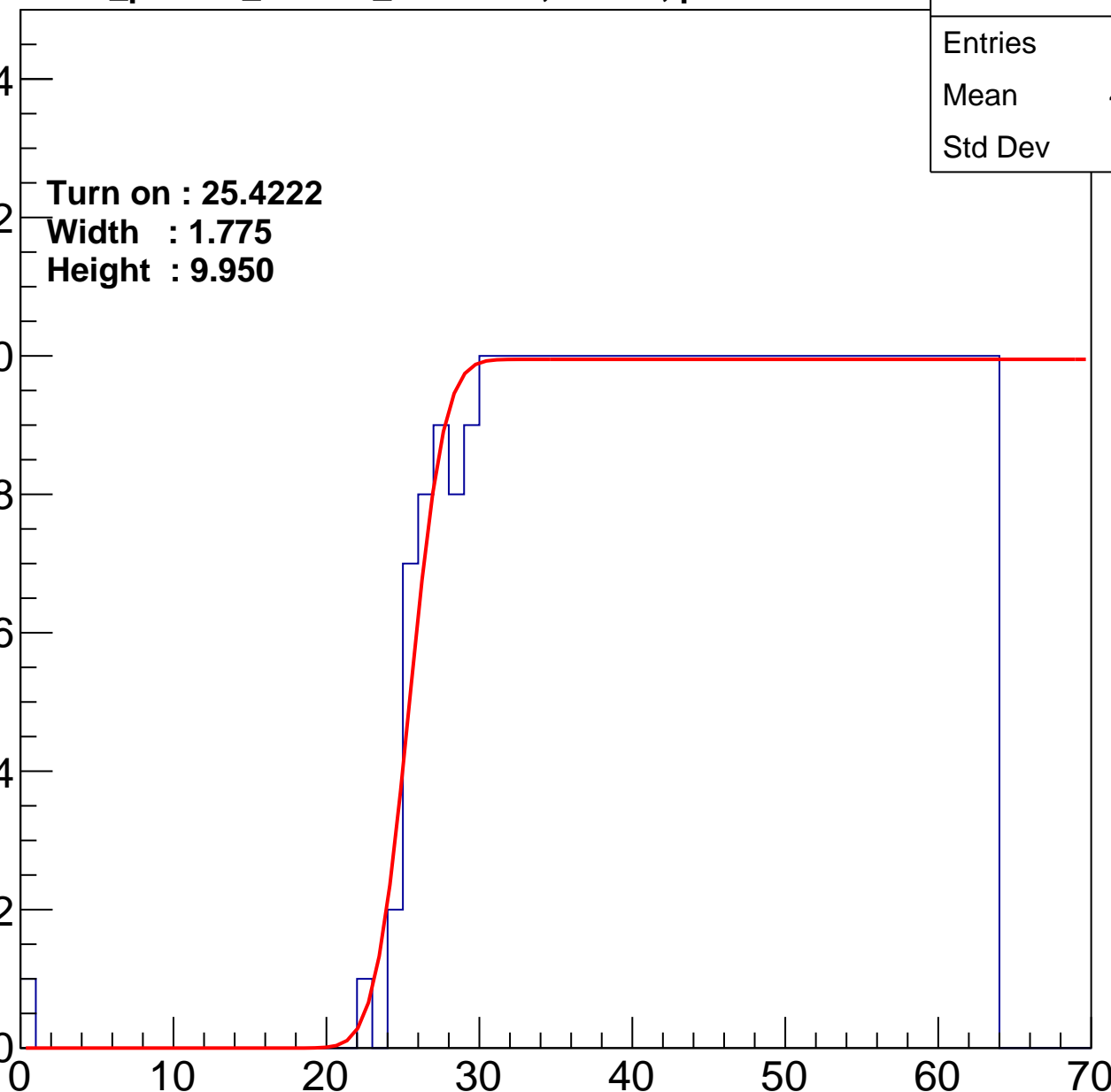
Width : 1.775

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch29

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.6
Std Dev	12.21

Turn on : 25.1503

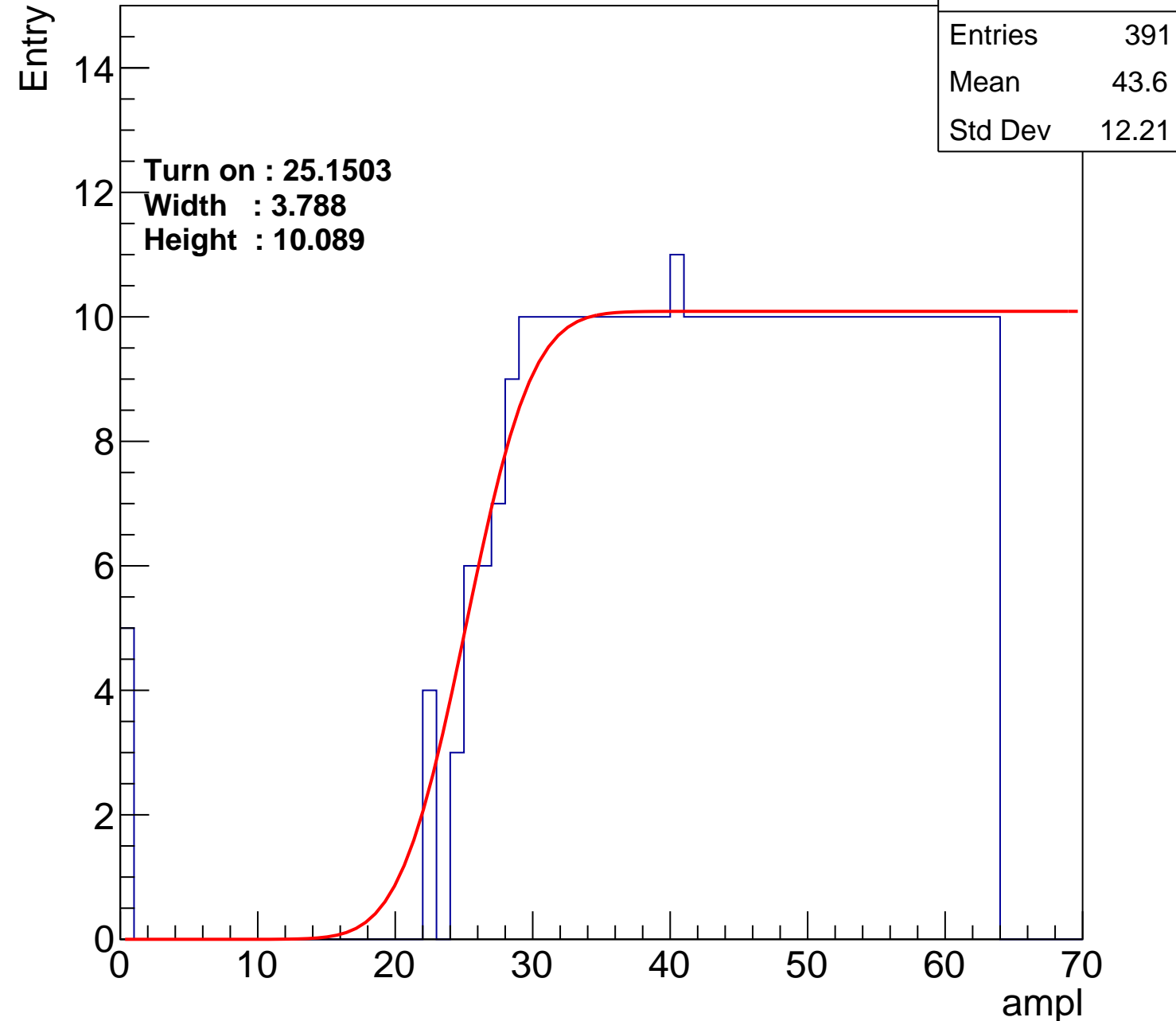
Width : 3.788

Height : 10.089

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch30

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.5141

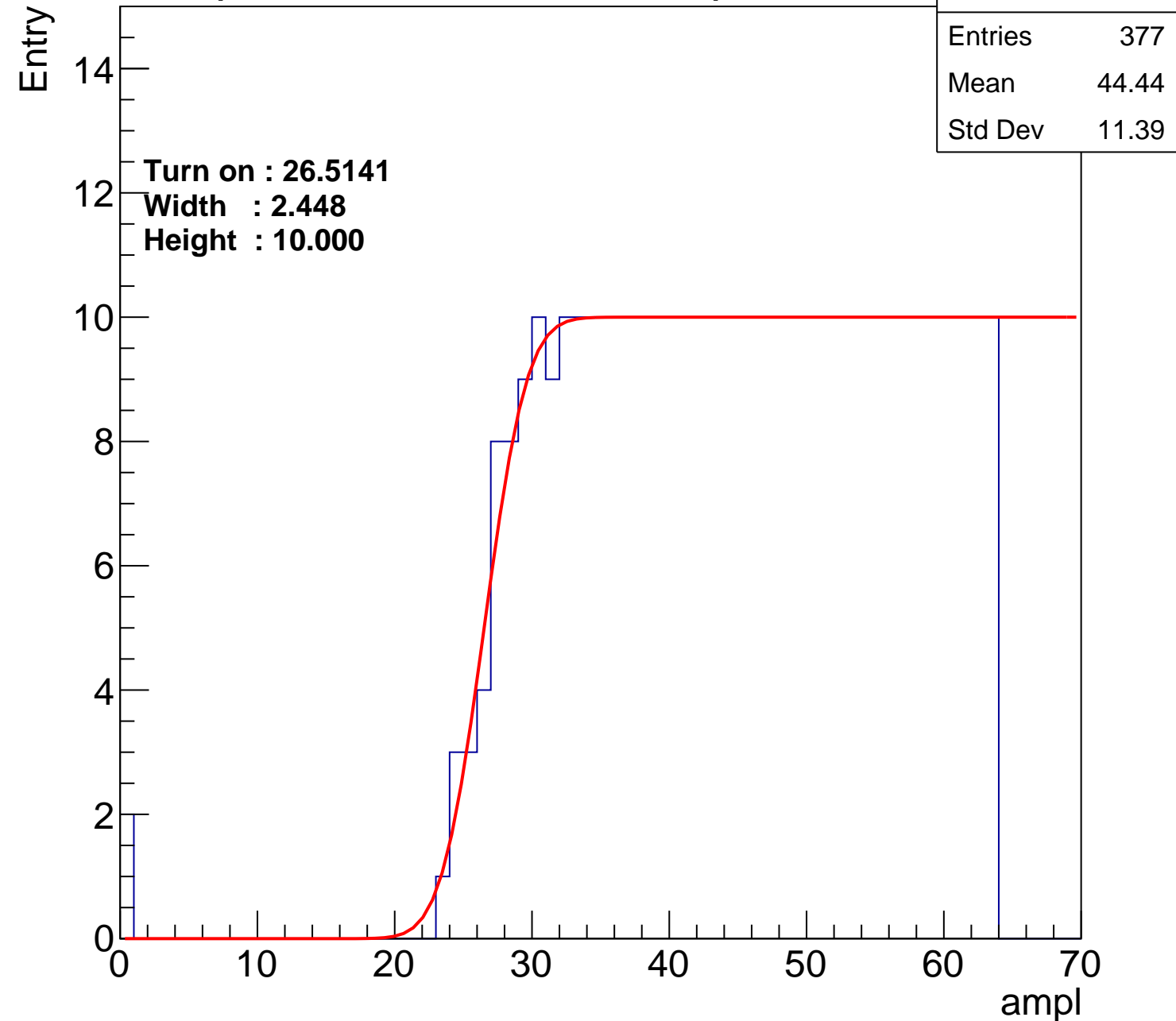
Width : 2.448

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch31

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.58
Std Dev	11.99

Turn on : 25.2794

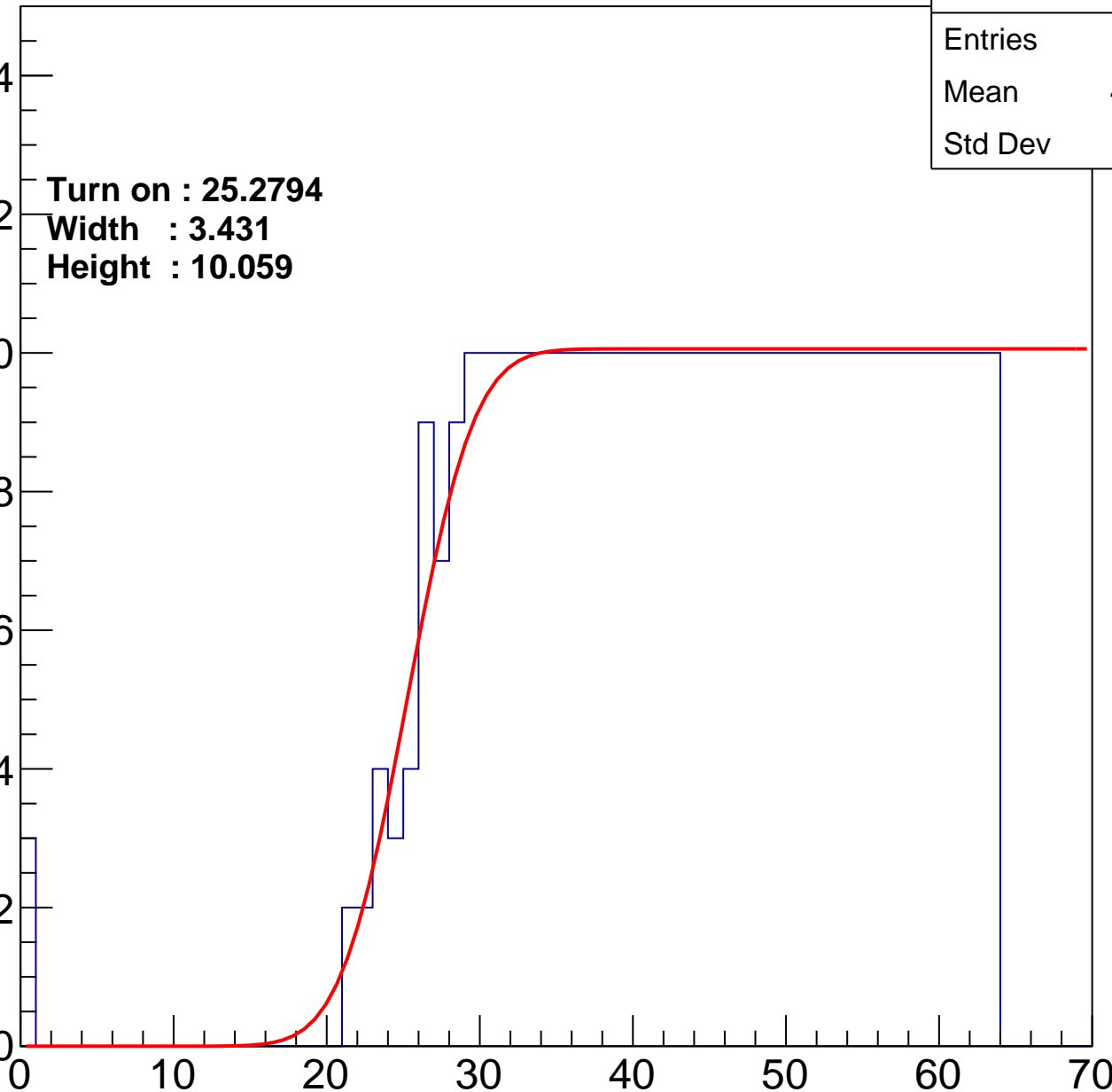
Width : 3.431

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch32

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.55
Std Dev	11.98

Turn on : 25.1809

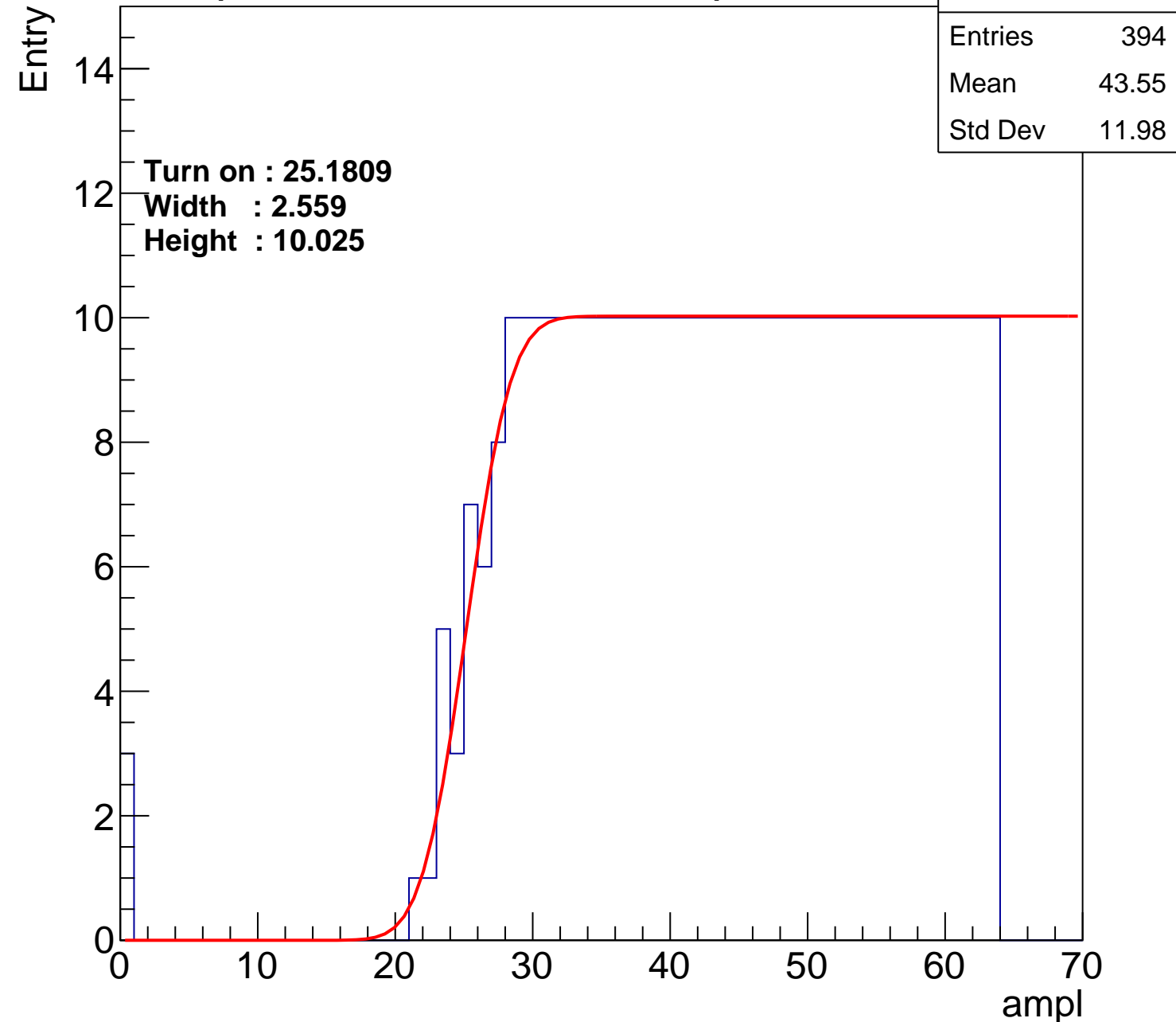
Width : 2.559

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch33

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.49
Std Dev	11.56

Turn on : 27.0285

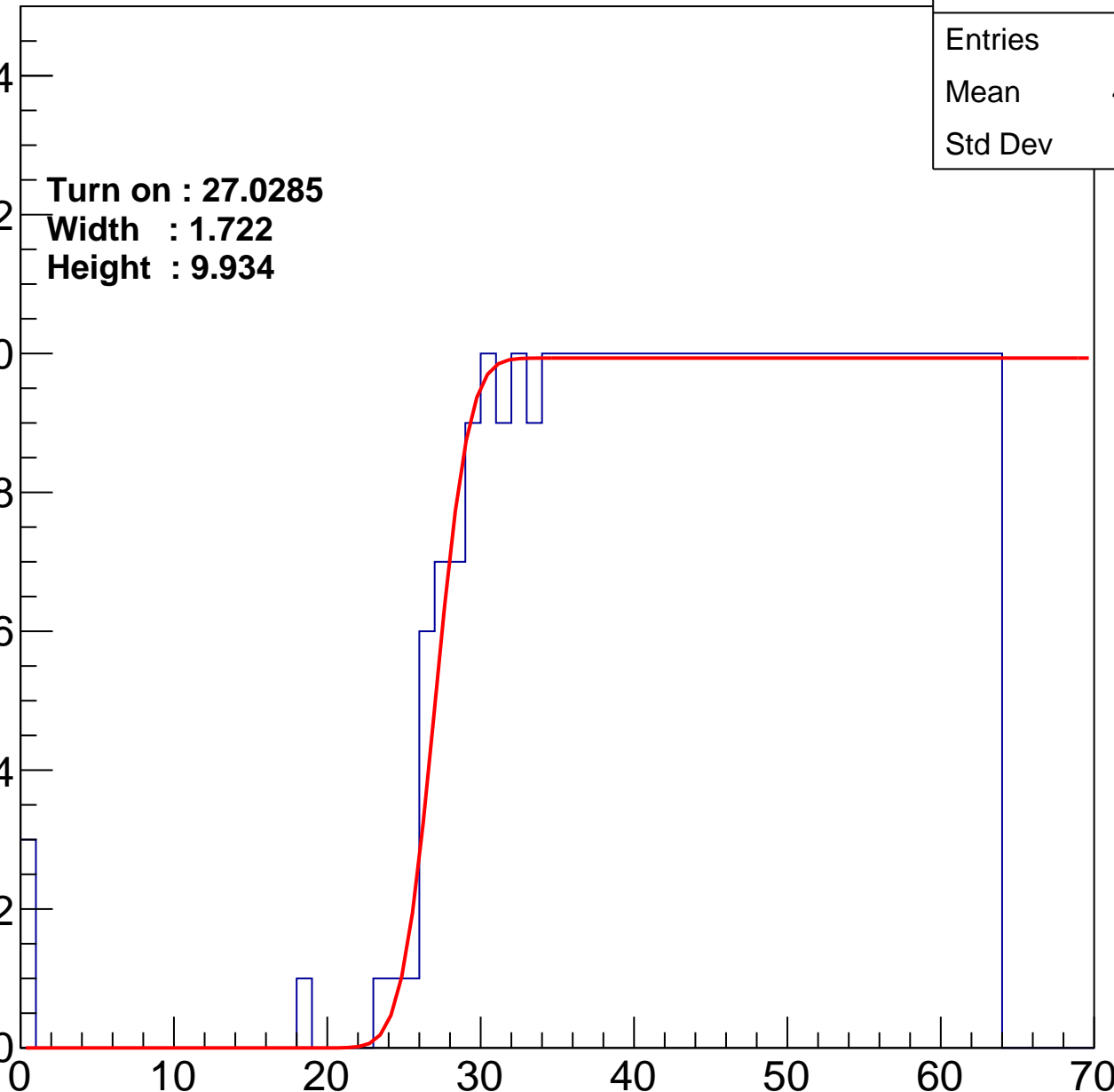
Width : 1.722

Height : 9.934

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch34

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.54
Std Dev	11.99

Turn on : 24.8956

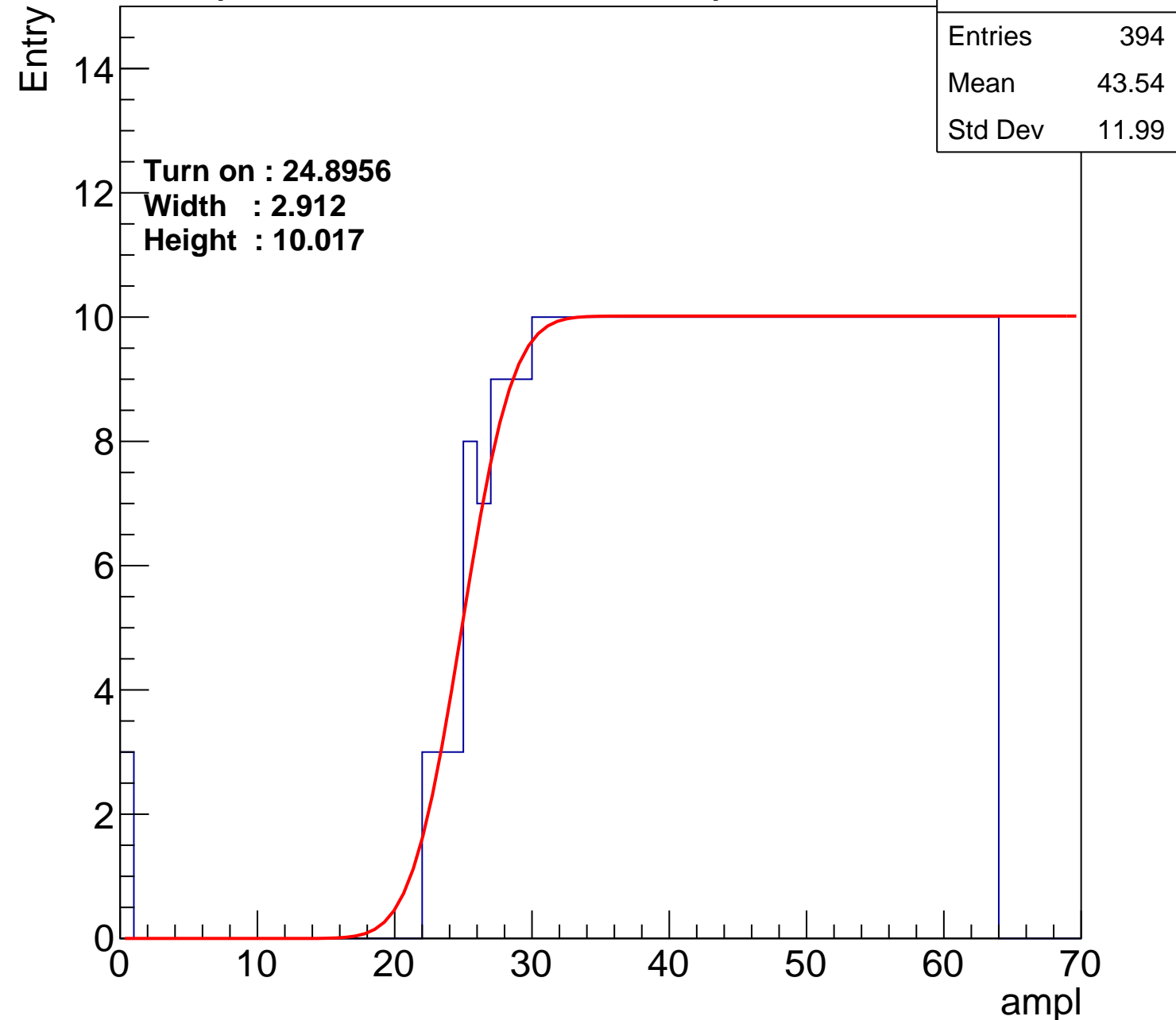
Width : 2.912

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch35

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.64
Std Dev	11.71

Turn on : 24.7469

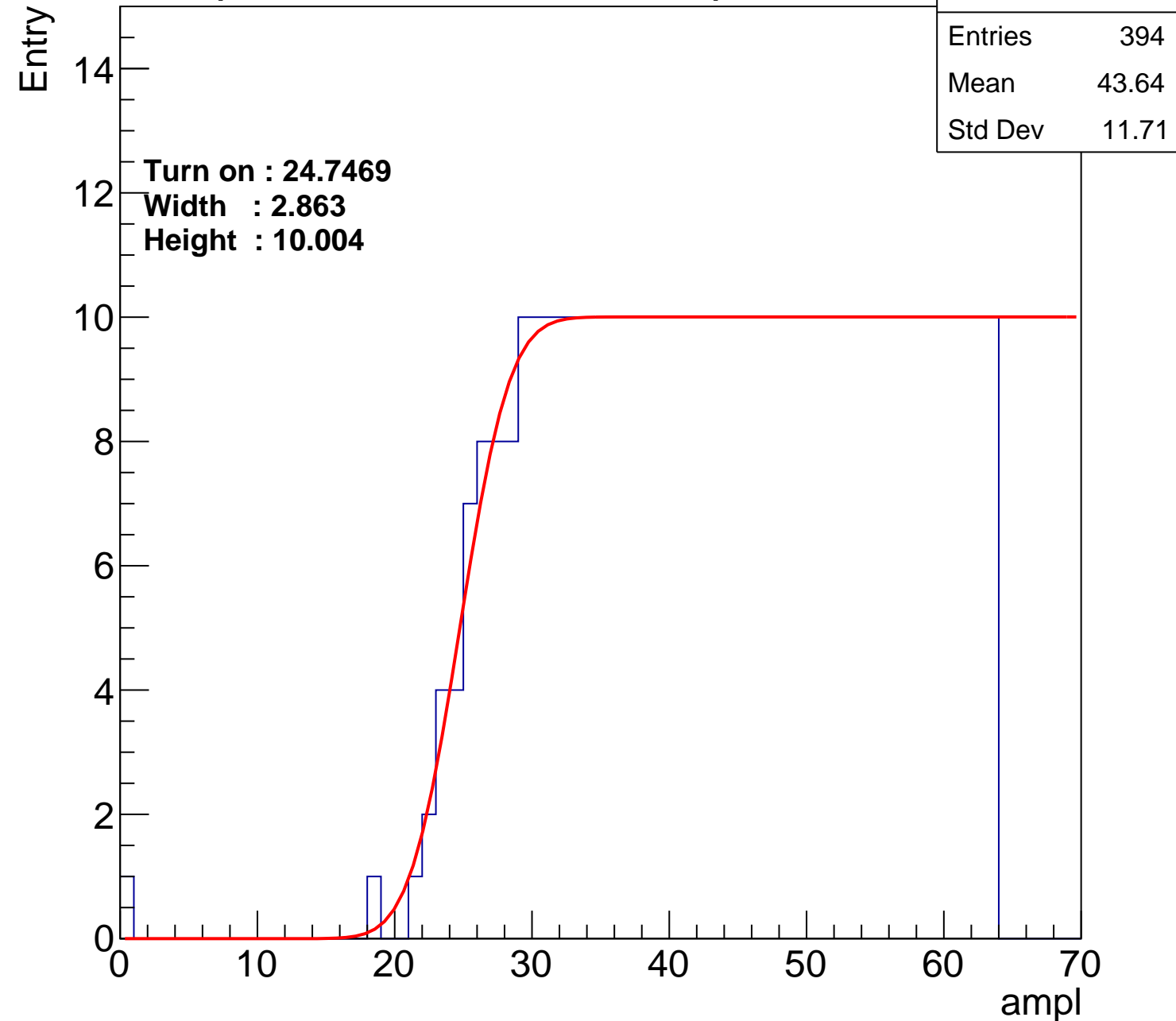
Width : 2.863

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch36

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 26.2035

Width : 2.944

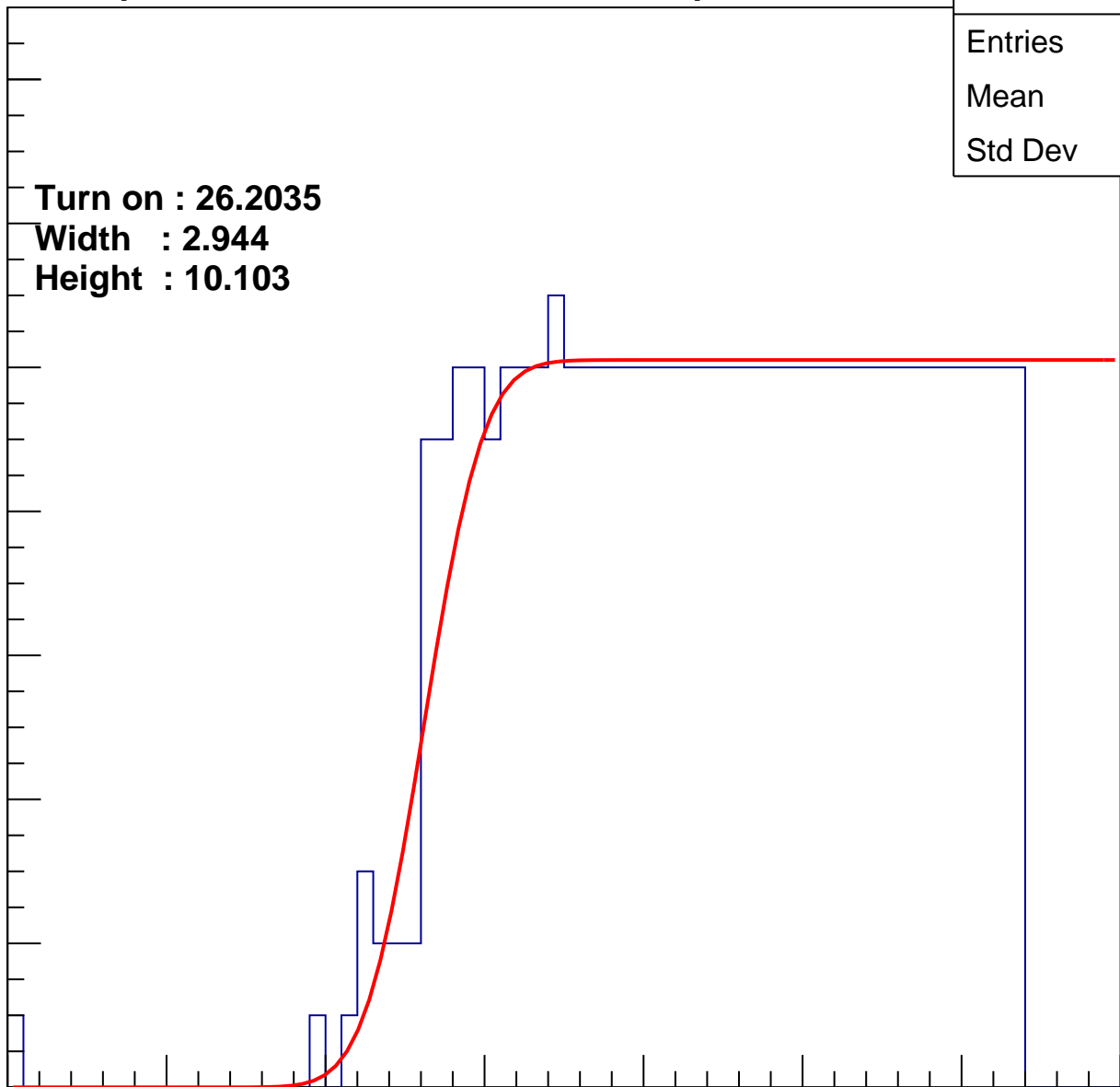
Height : 10.103

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U21-ch37

calib\_packv5\_042523\_0143.root, FC#10, port B3

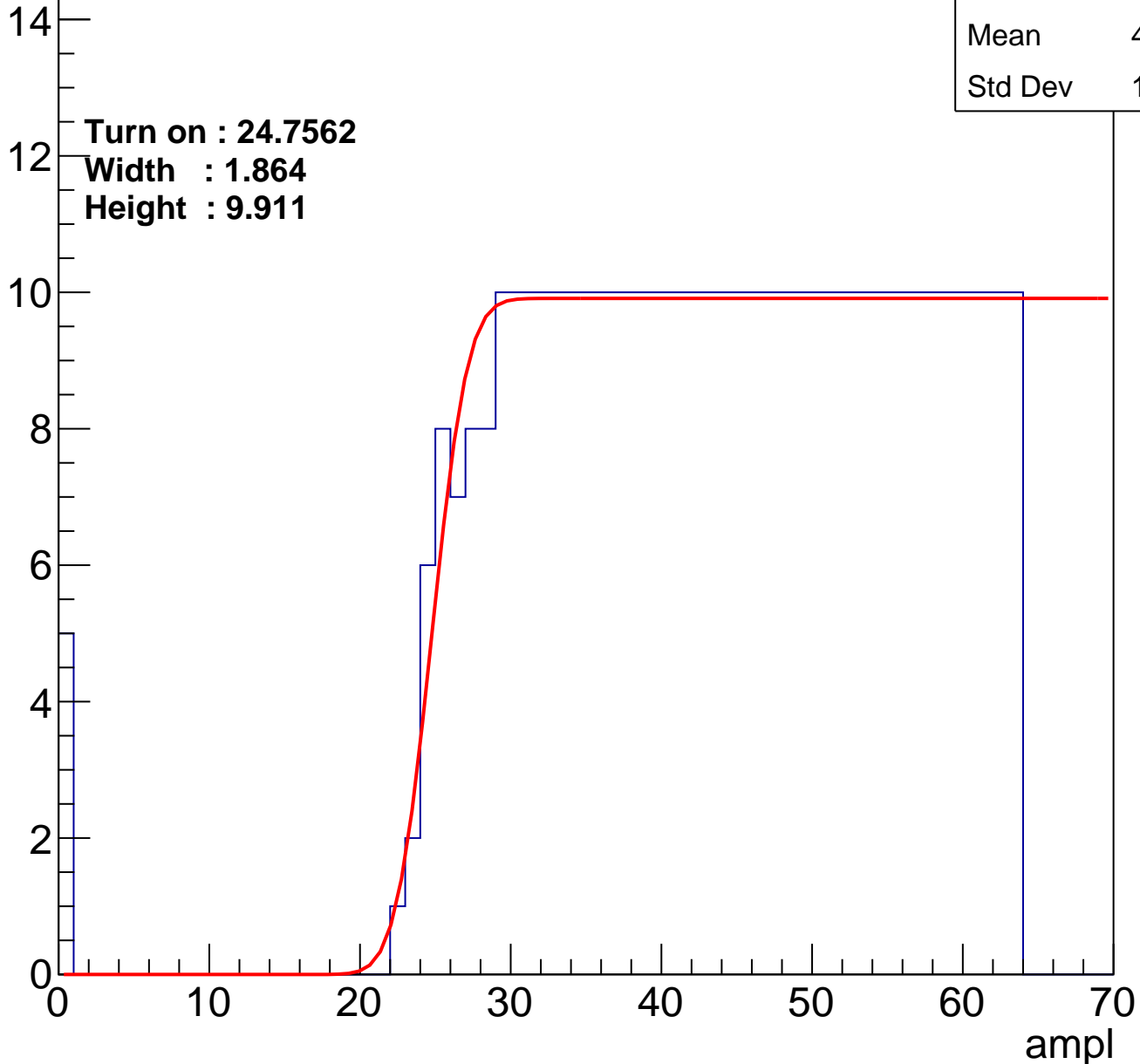
Entries	395
Mean	43.38
Std Dev	12.32

Turn on : 24.7562

Width : 1.864

Height : 9.911

Entry



# B1L002S, U21-ch38

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.59
Std Dev	12.08

Turn on : 25.3341

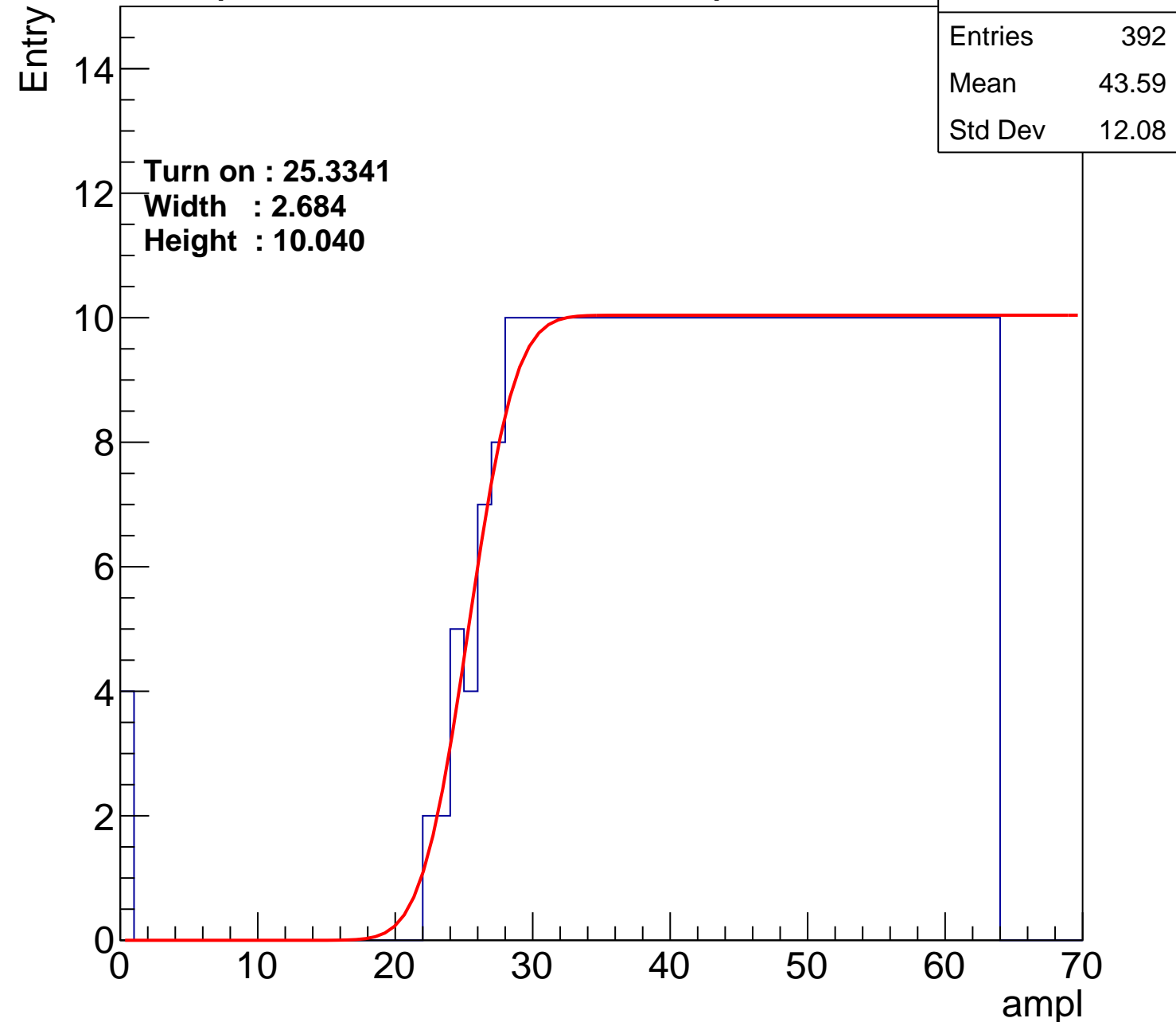
Width : 2.684

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch39

calib\_packv5\_042523\_0143.root, FC#10, port B3

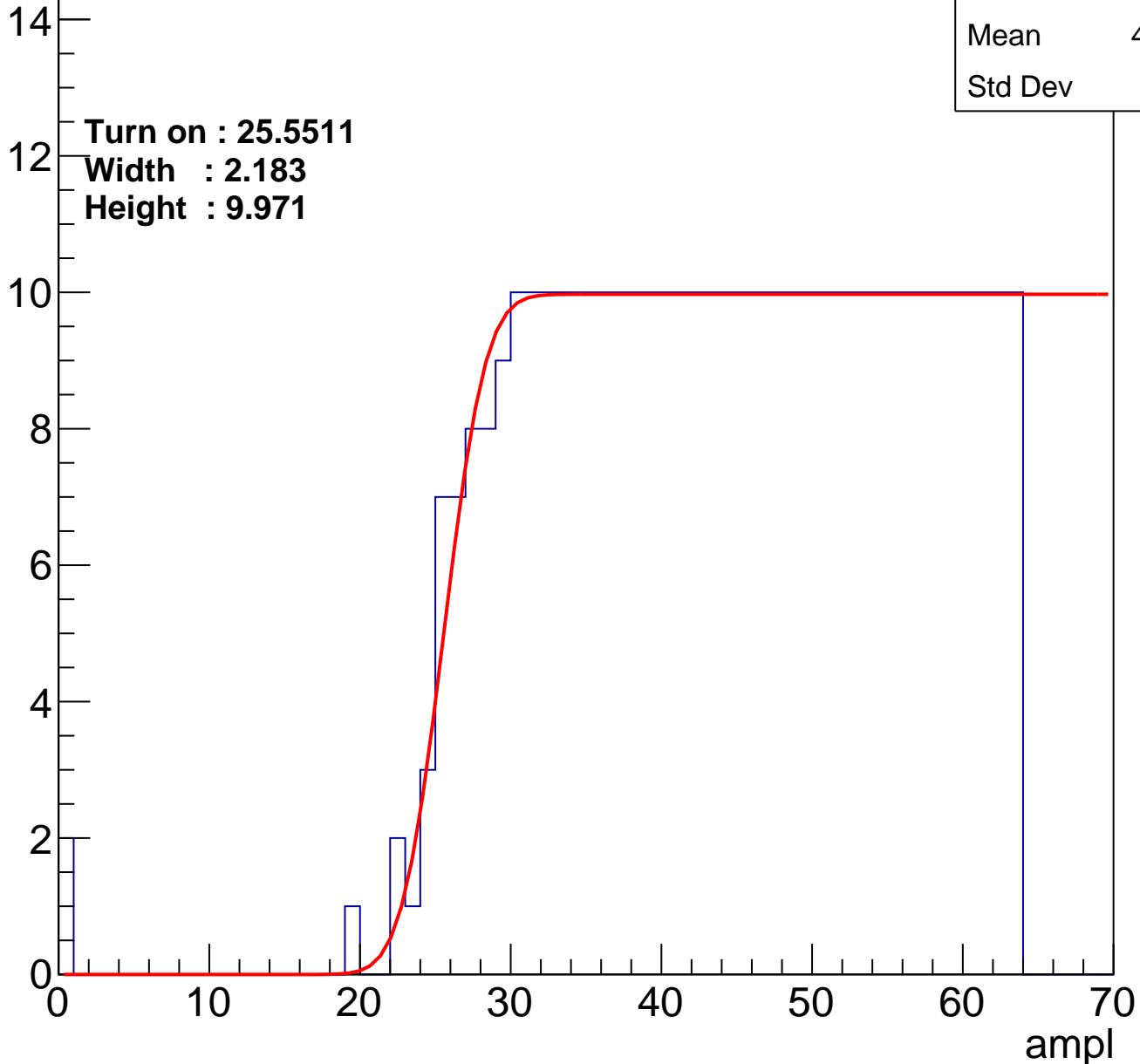
Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.5511

Width : 2.183

Height : 9.971

Entry



# B1L002S, U21-ch40

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.32
Std Dev	11.29

Turn on : 26.2715

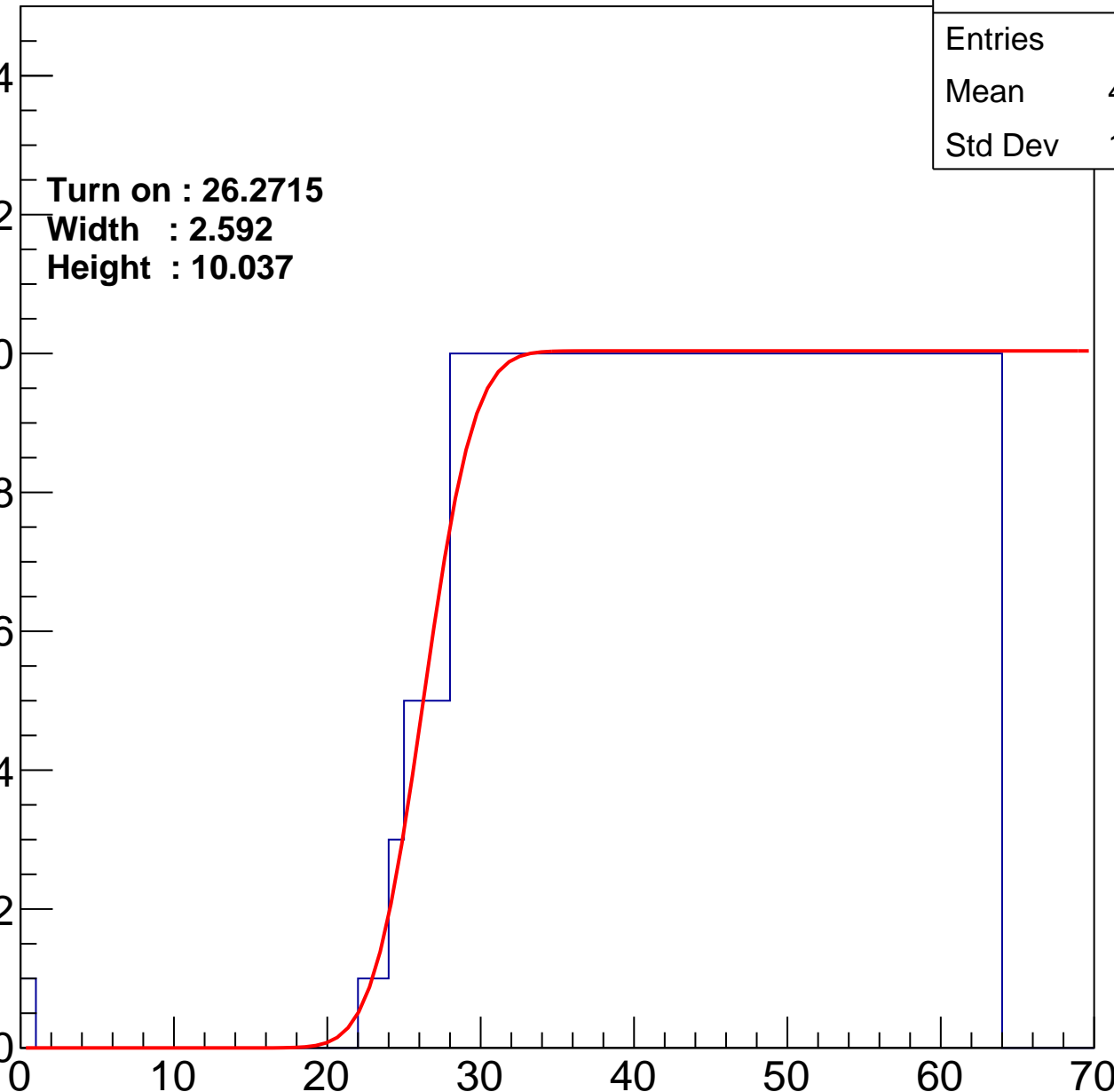
Width : 2.592

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch41

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.56
Std Dev	12.02

Turn on : 25.3810

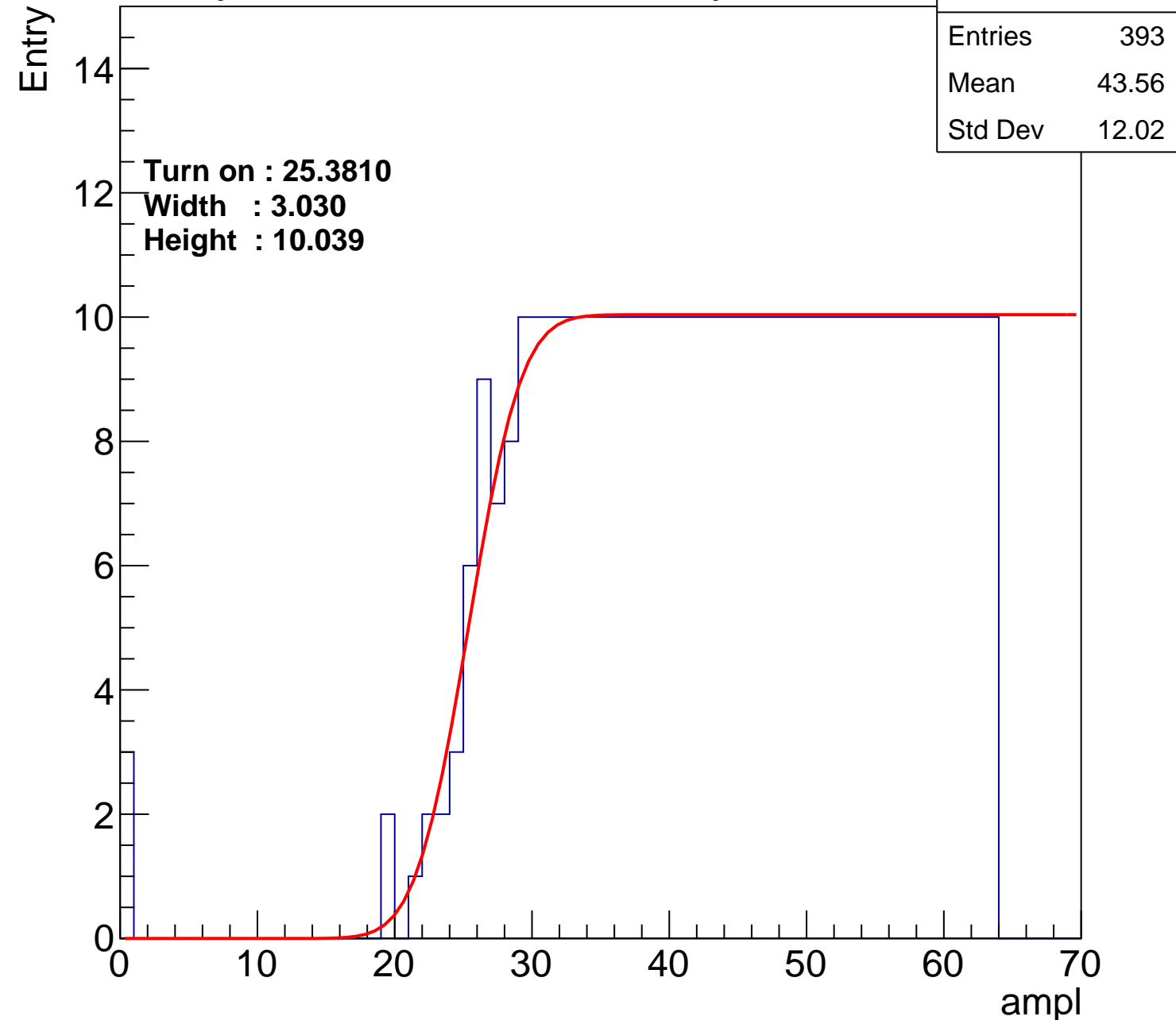
Width : 3.030

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch42

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.61
Std Dev	12.23

Turn on : 26.1391

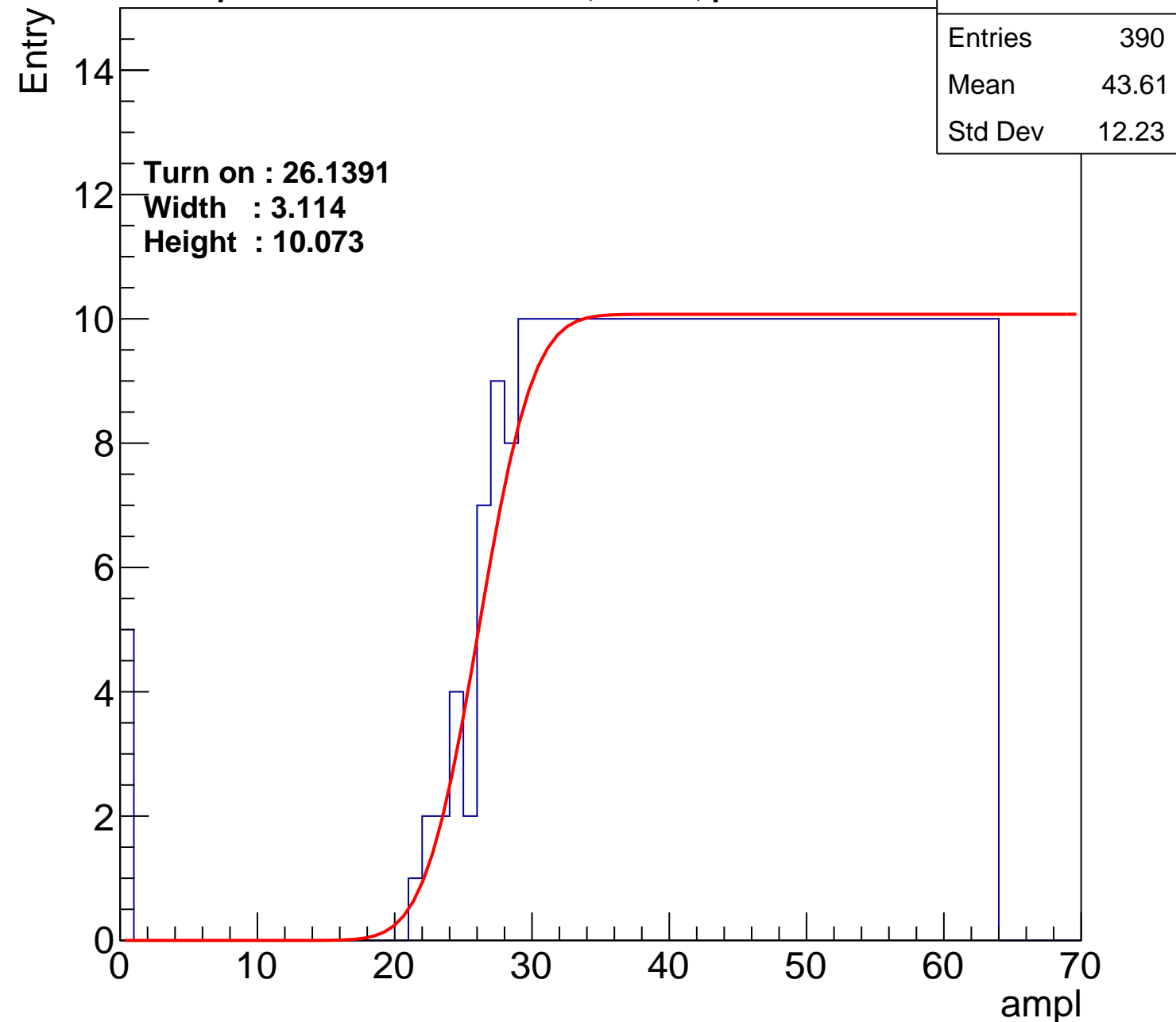
Width : 3.114

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch43

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.87
Std Dev	11.8

Turn on : 25.4341

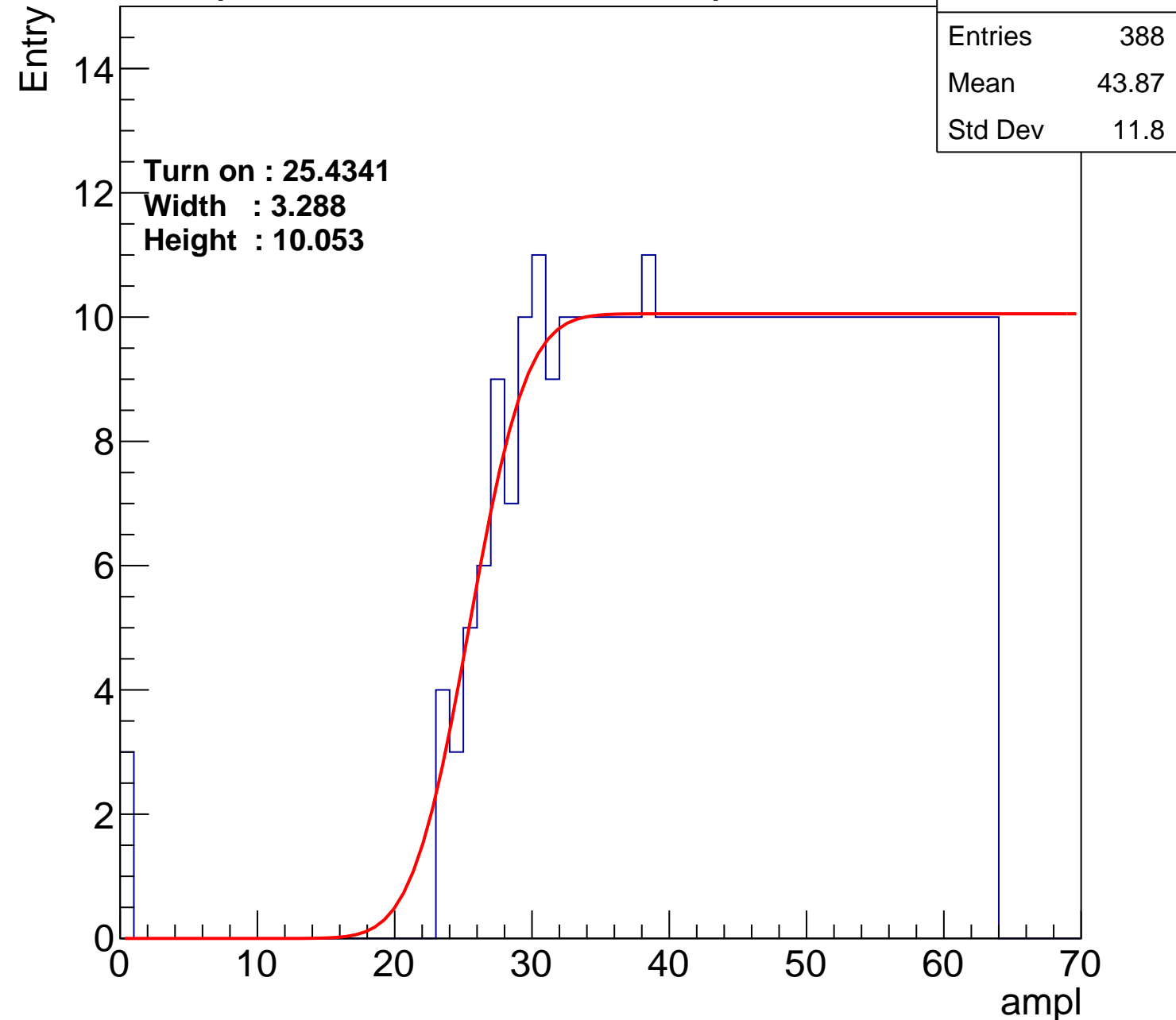
Width : 3.288

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch44

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.46
Std Dev	12.35

Turn on : 25.4714

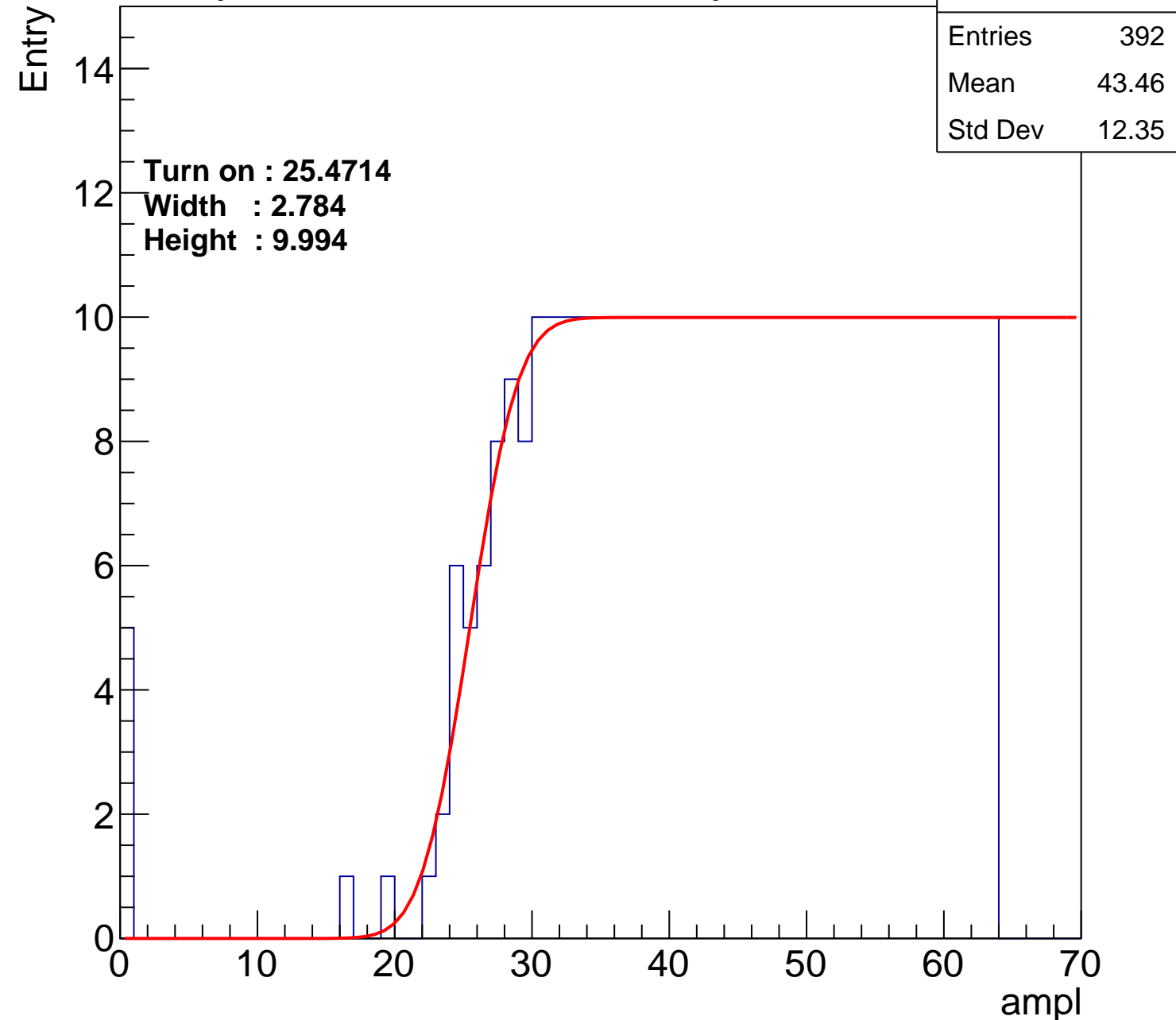
Width : 2.784

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch45

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.68
Std Dev	12.1

Turn on : 25.4268

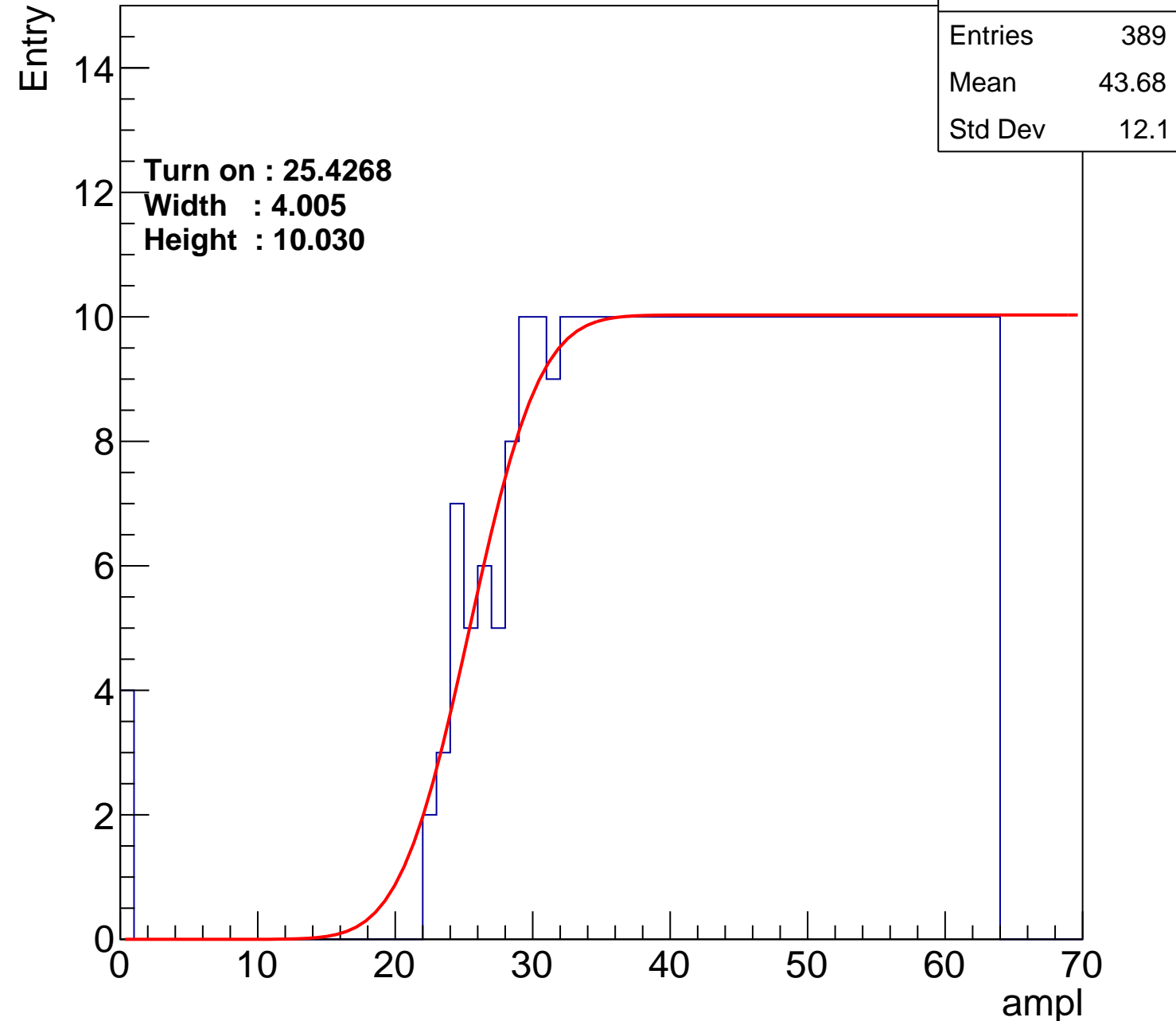
Width : 4.005

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch46

calib\_packv5\_042523\_0143.root, FC#10, port B3

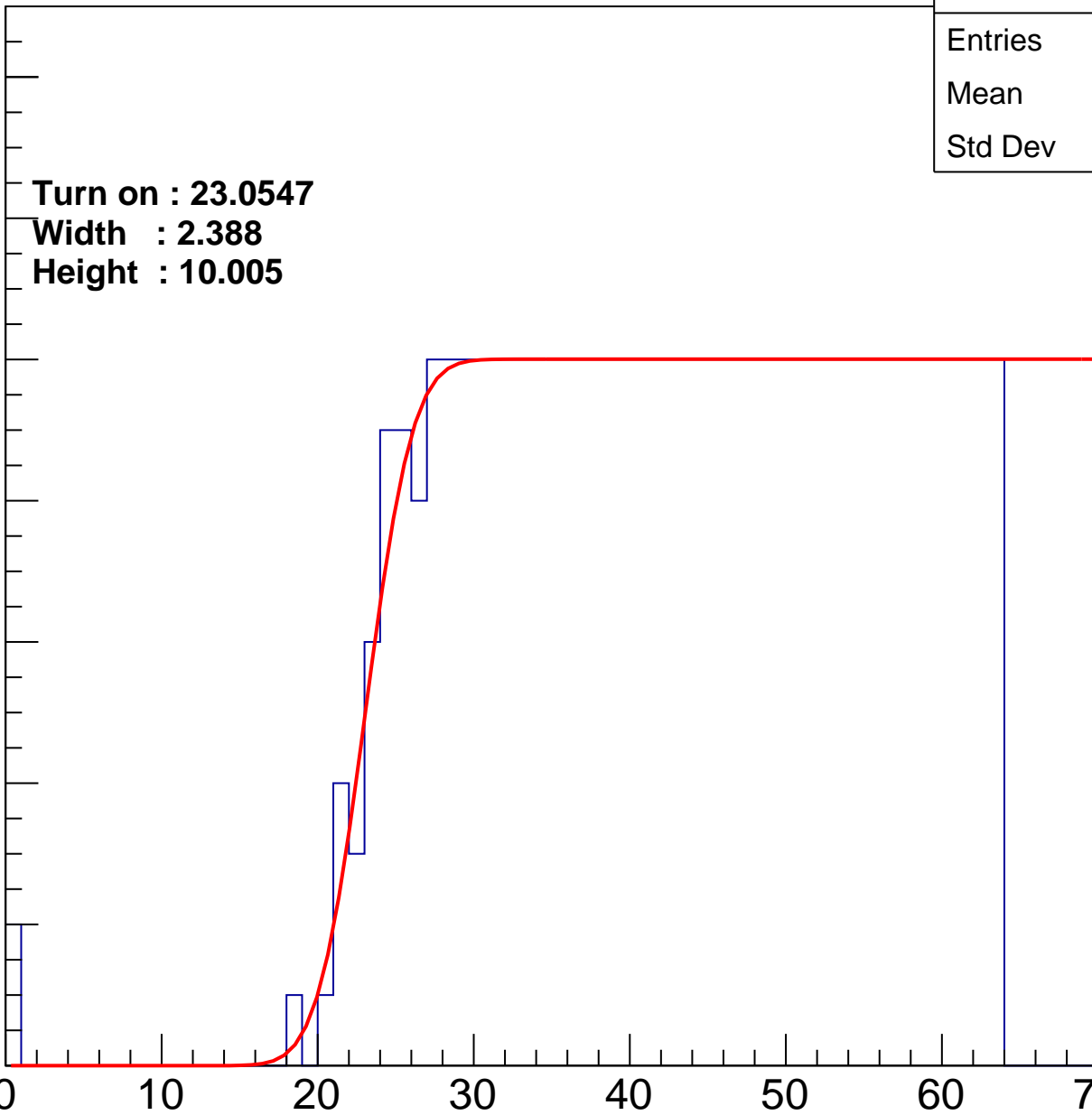
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 23.0547  
Width : 2.388  
Height : 10.005

Entries	413
Mean	42.68
Std Dev	12.31

ampl





# B1L002S, U21-ch47

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 25.1050

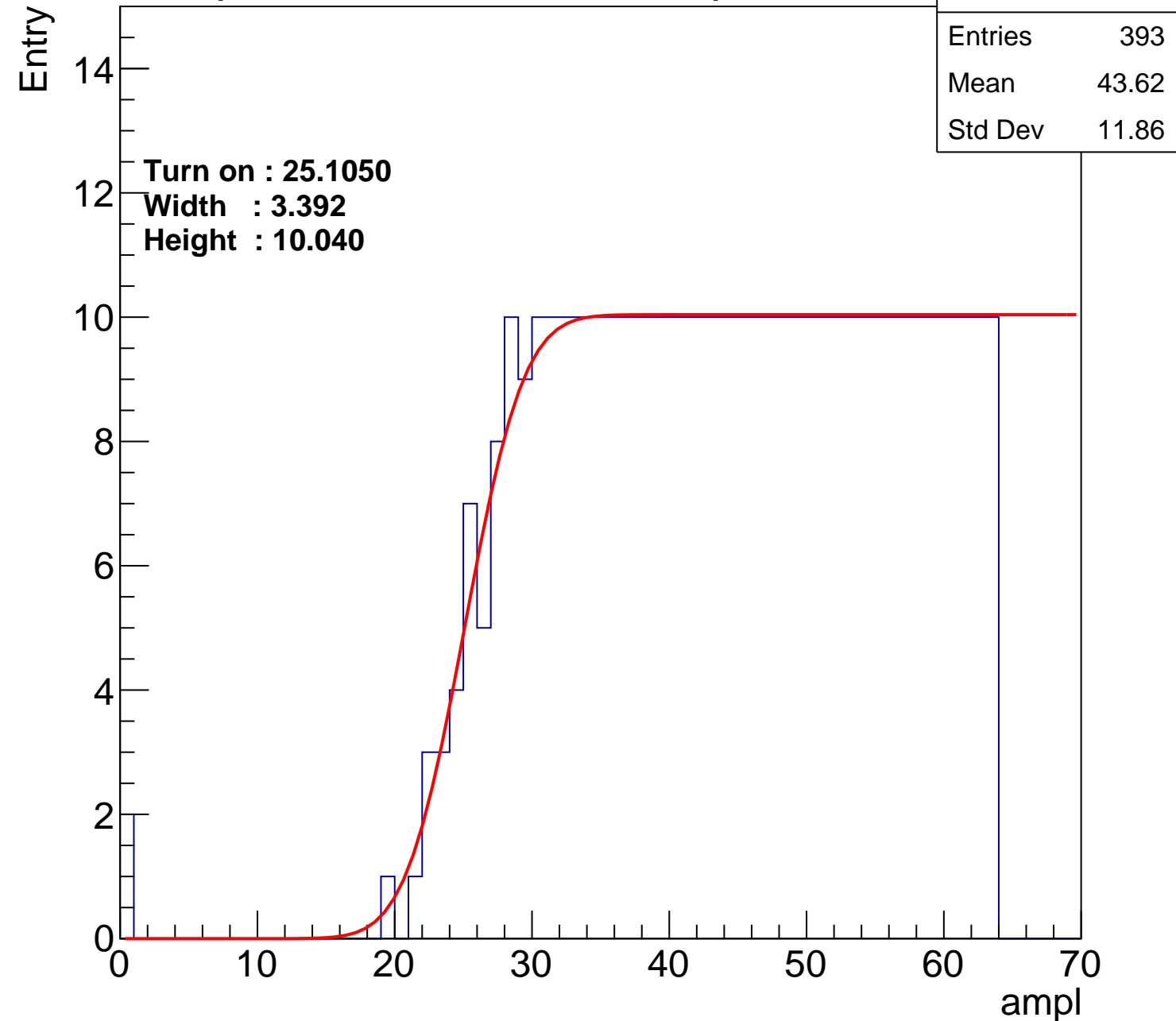
Width : 3.392

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch48

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.67
Std Dev	11.66

Turn on : 24.5604

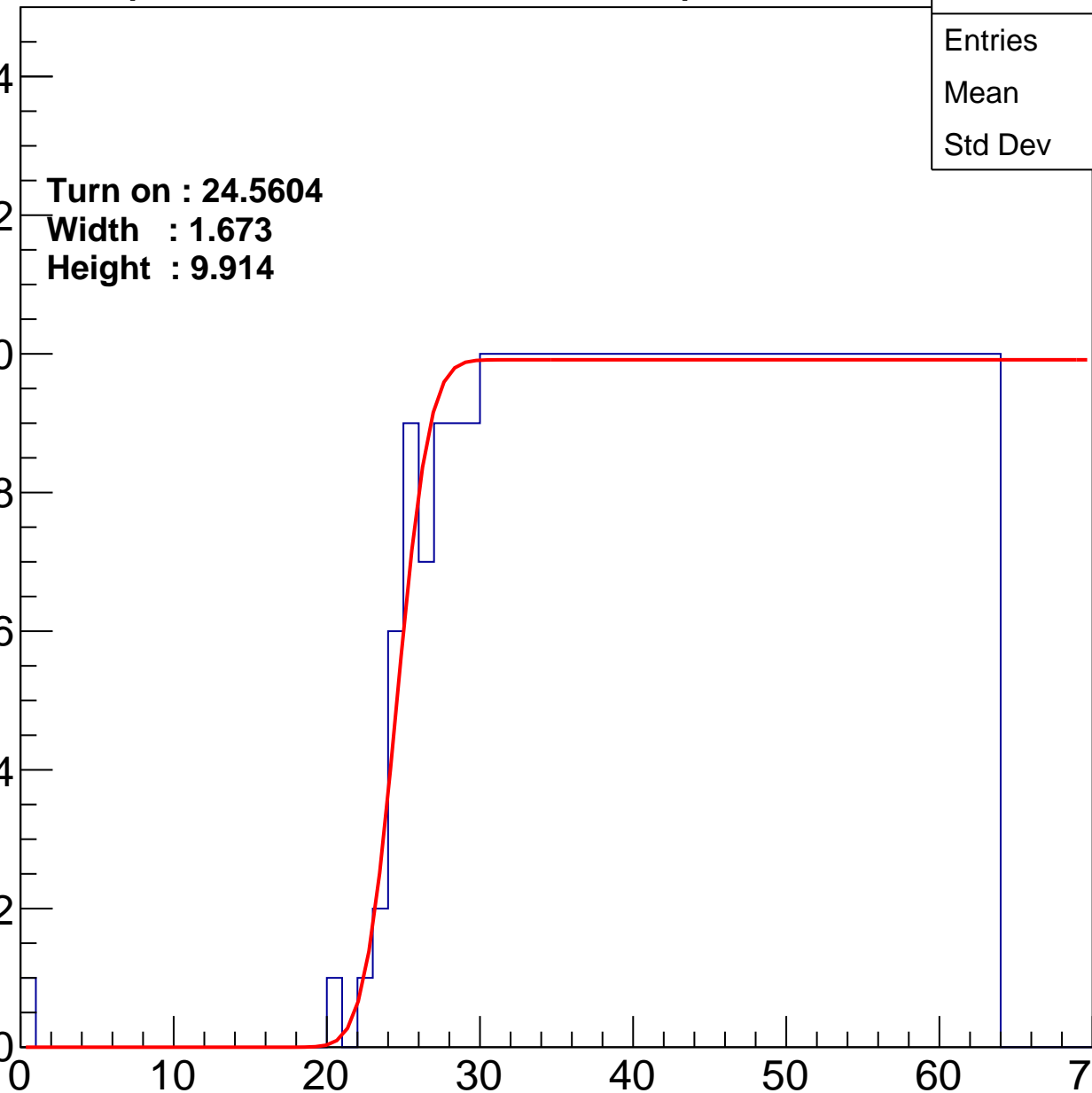
Width : 1.673

Height : 9.914

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch49

calib\_packv5\_042523\_0143.root, FC#10, port B3

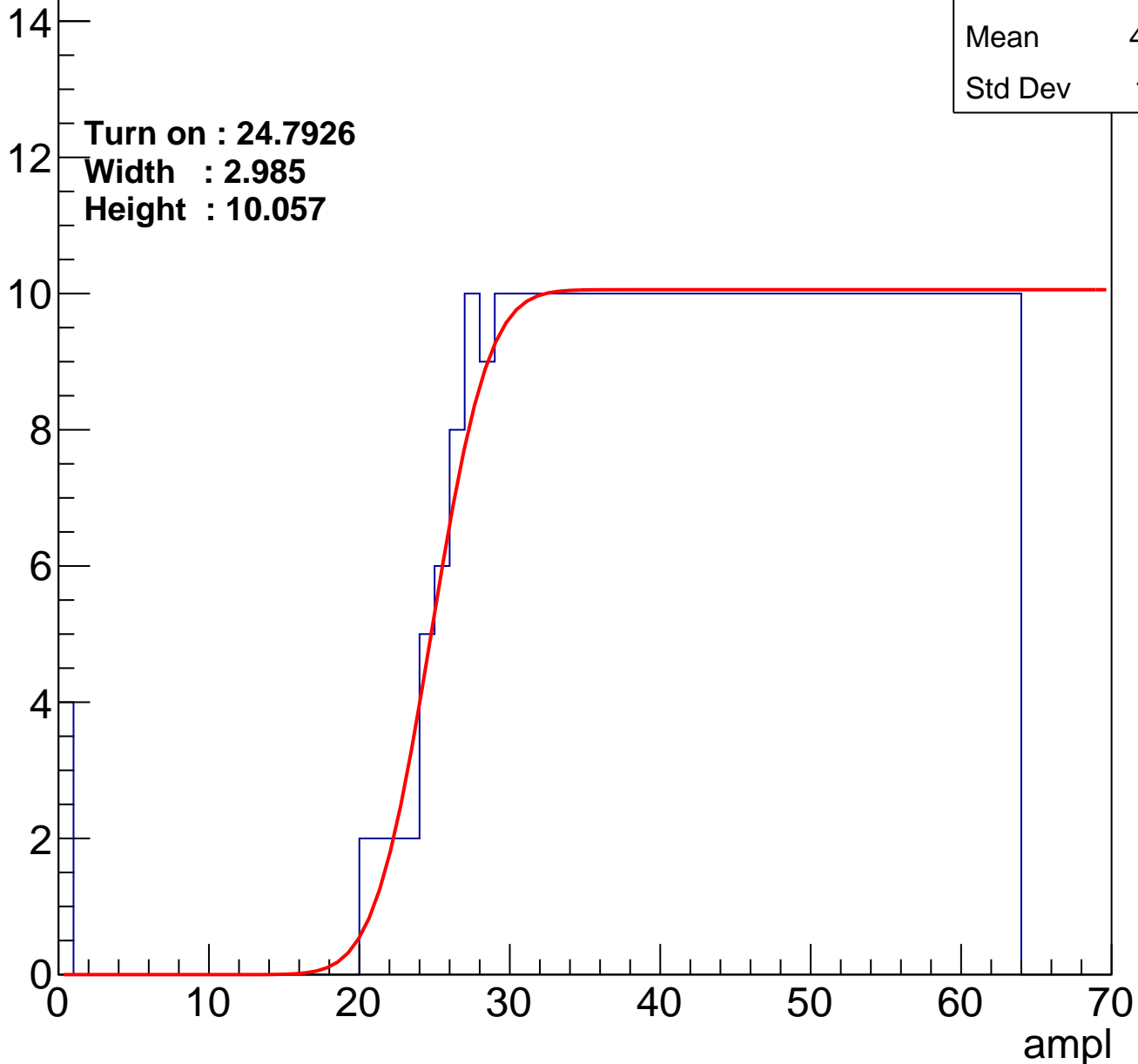
Entries	400
Mean	43.18
Std Dev	12.31

Turn on : 24.7926

Width : 2.985

Height : 10.057

Entry



# B1L002S, U21-ch50

calib\_packv5\_042523\_0143.root, FC#10, port B3

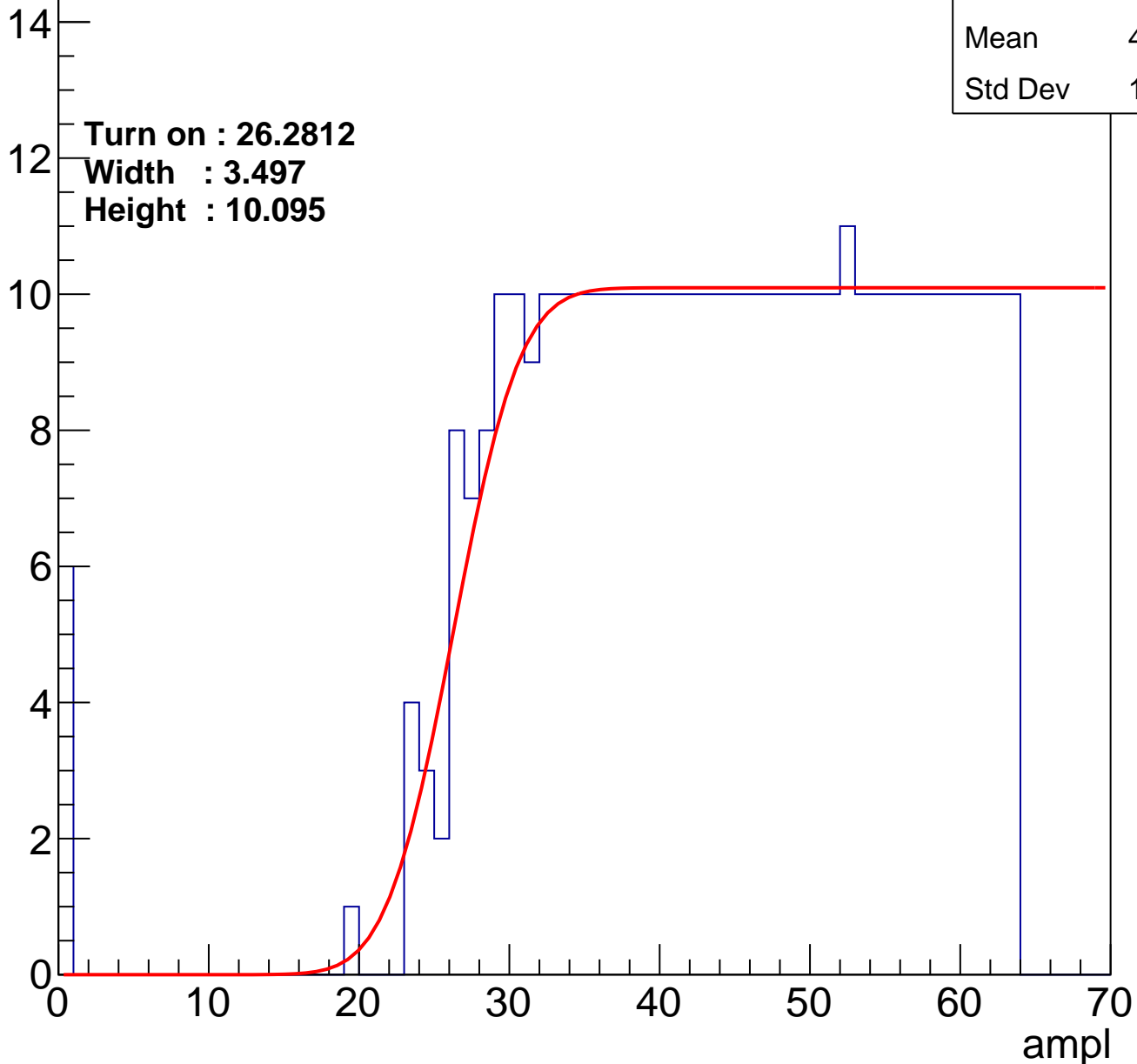
Entries	389
Mean	43.64
Std Dev	12.37

Turn on : 26.2812

Width : 3.497

Height : 10.095

Entry



# B1L002S, U21-ch51

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	407
Mean	42.95
Std Dev	12.26

Turn on : 23.5753

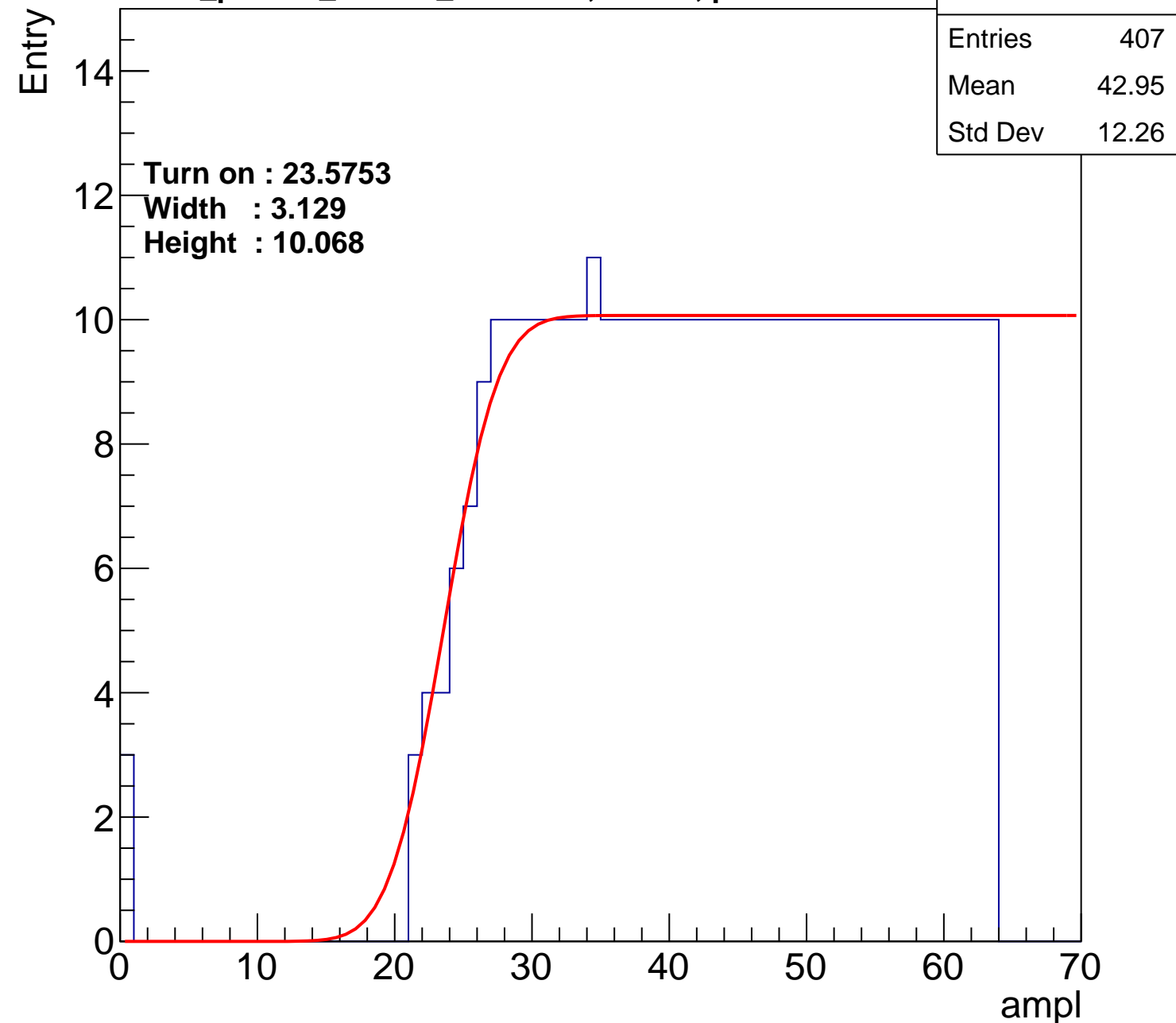
Width : 3.129

Height : 10.068

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch52

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	405
Mean	42.89
Std Dev	12.54

Turn on : 24.0500

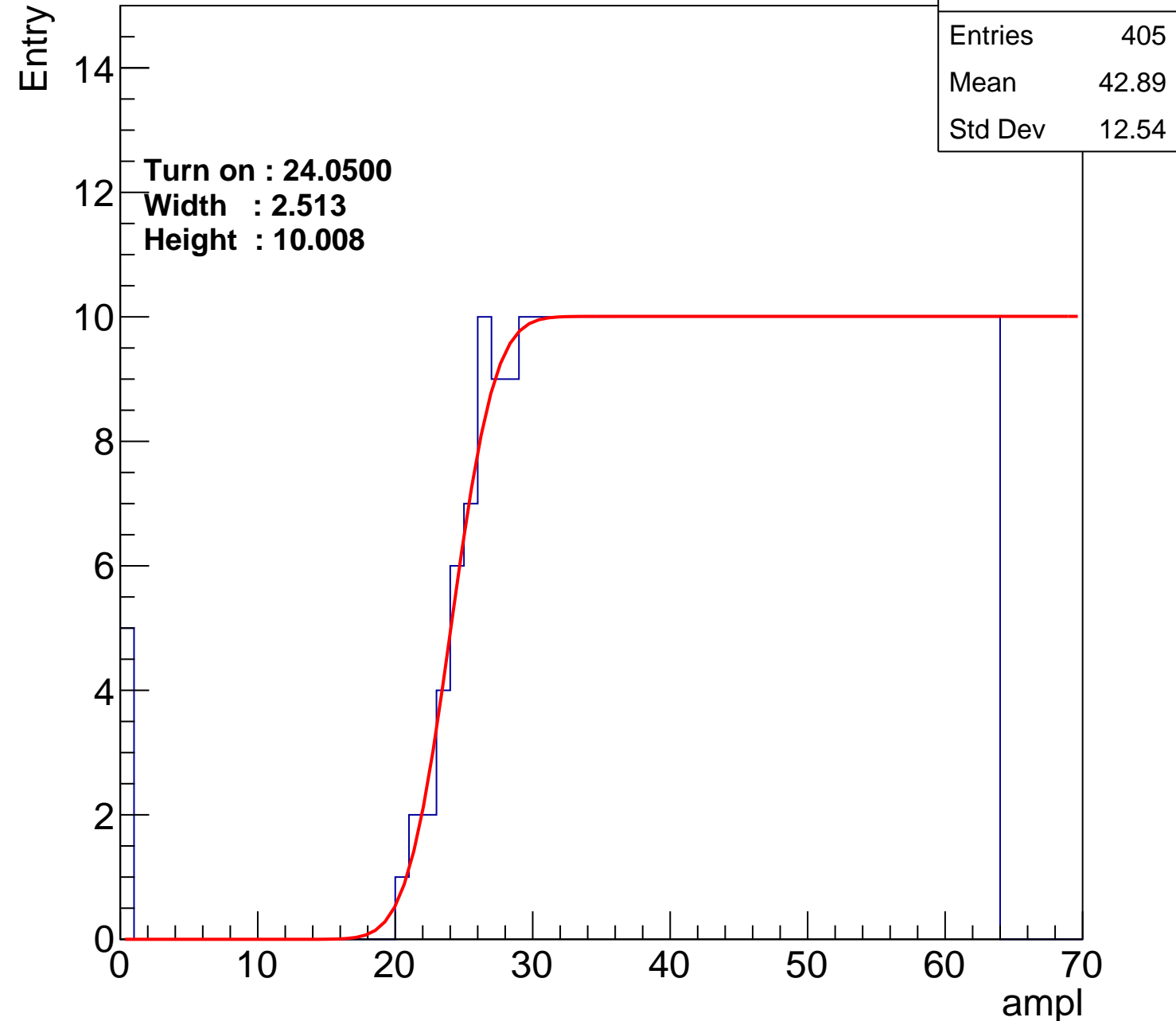
Width : 2.513

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch53

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.79
Std Dev	11.56

Turn on : 25.3479

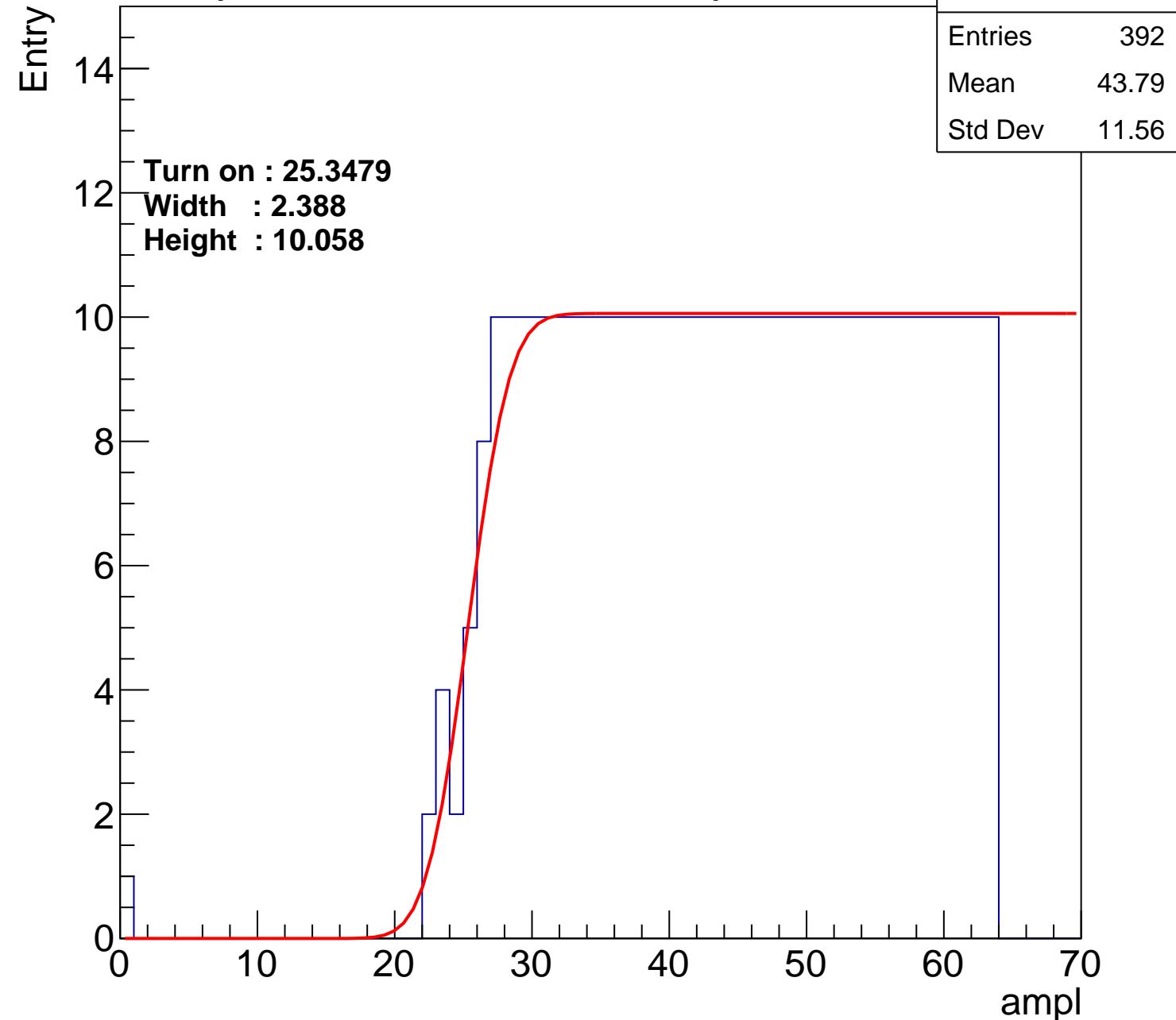
Width : 2.388

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch54

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.73
Std Dev	11.78

Turn on : 25.6022

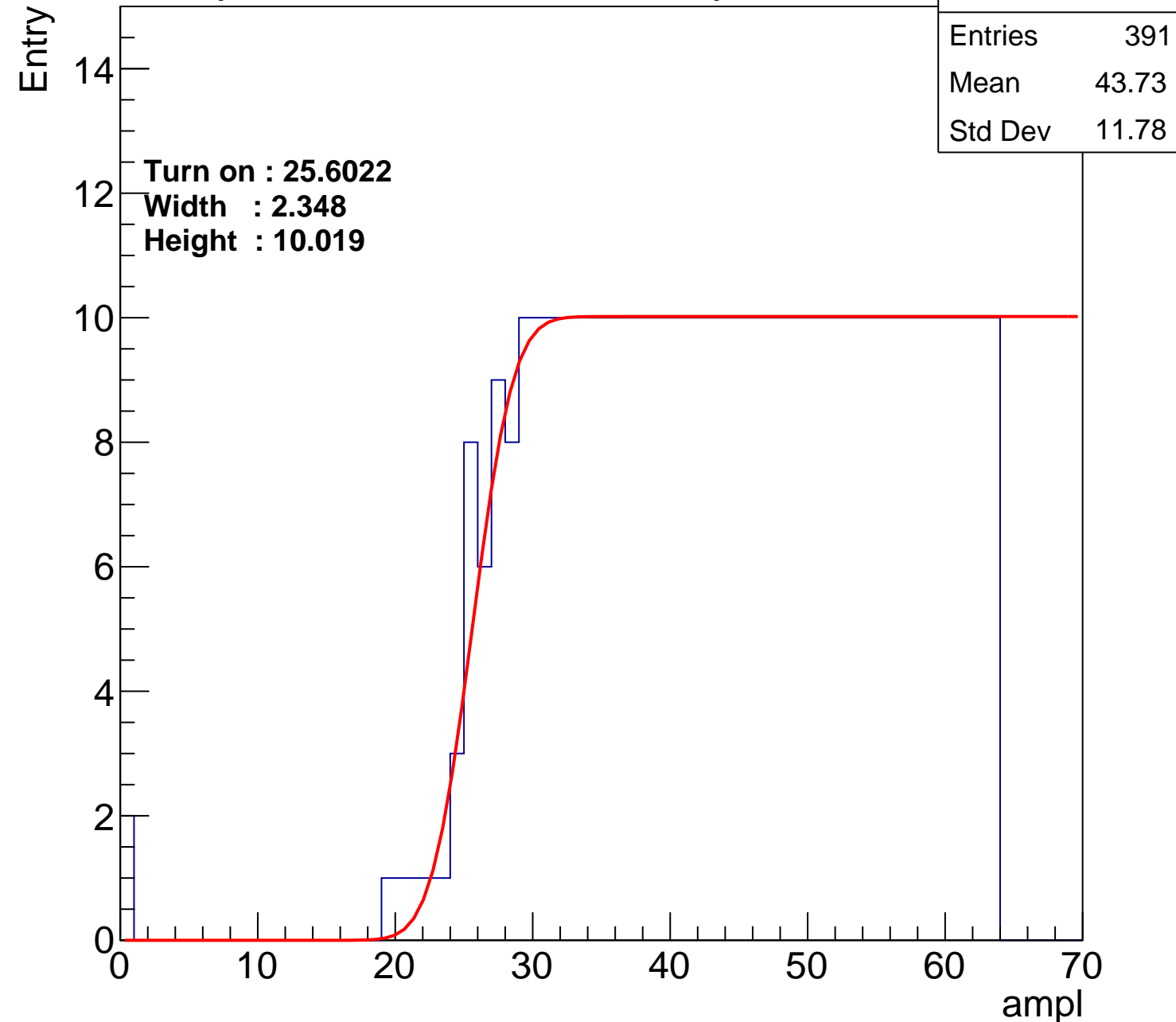
Width : 2.348

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch55

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.23
Std Dev	11.32

Turn on : 25.7012

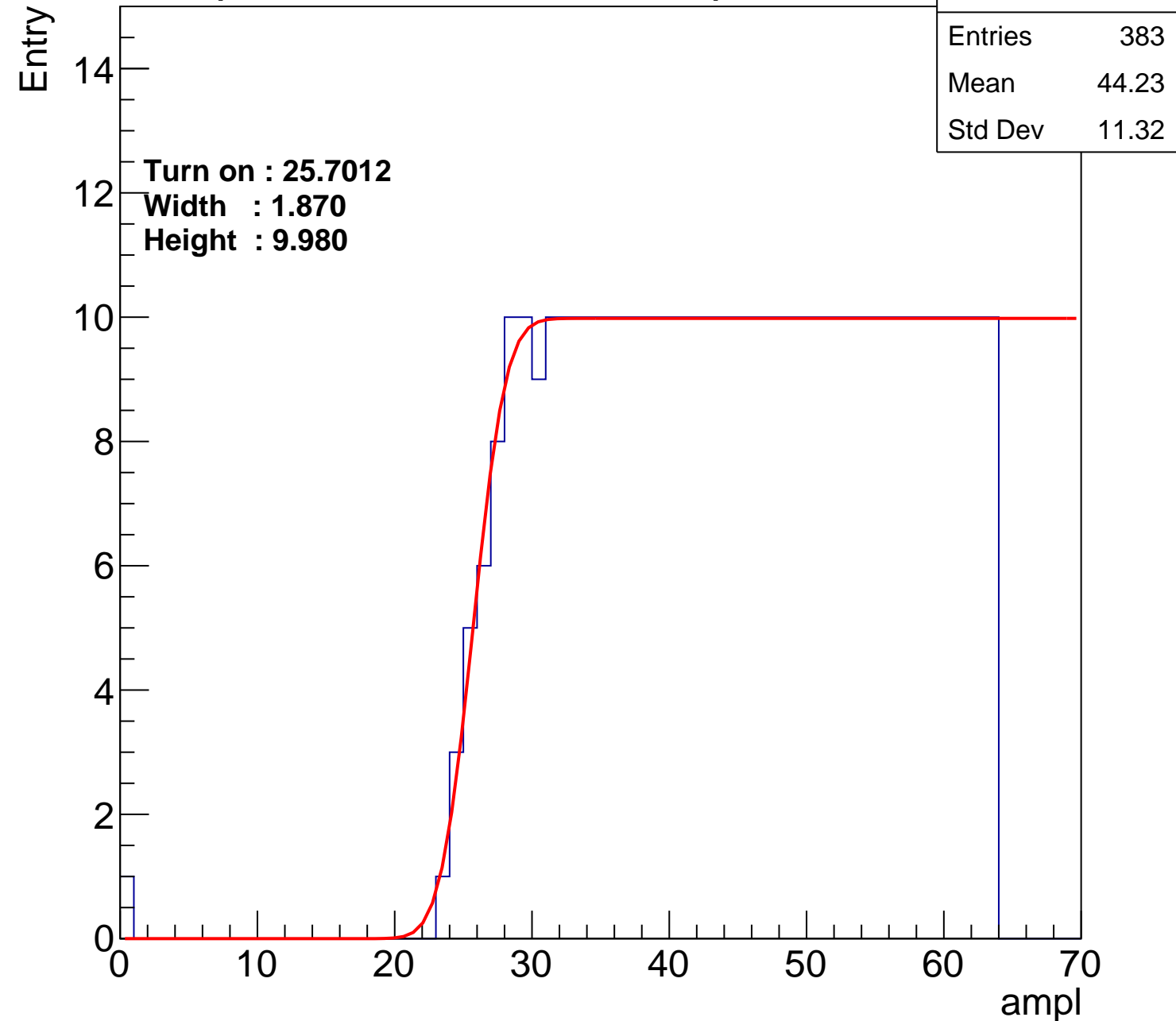
Width : 1.870

Height : 9.980

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch56

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	405
Mean	42.98
Std Dev	12.31

Turn on : 23.7342

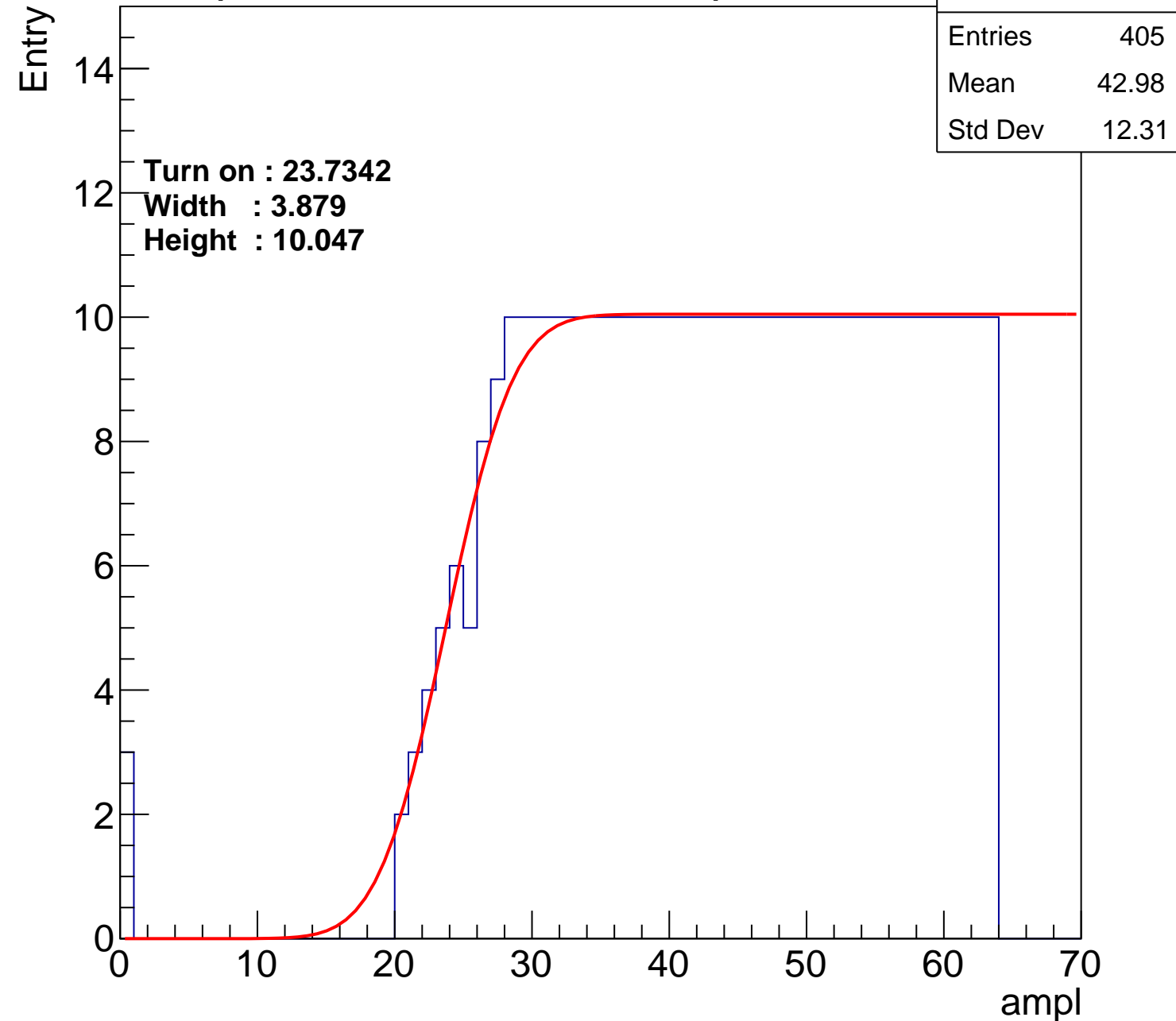
Width : 3.879

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch57

calib\_packv5\_042523\_0143.root, FC#10, port B3

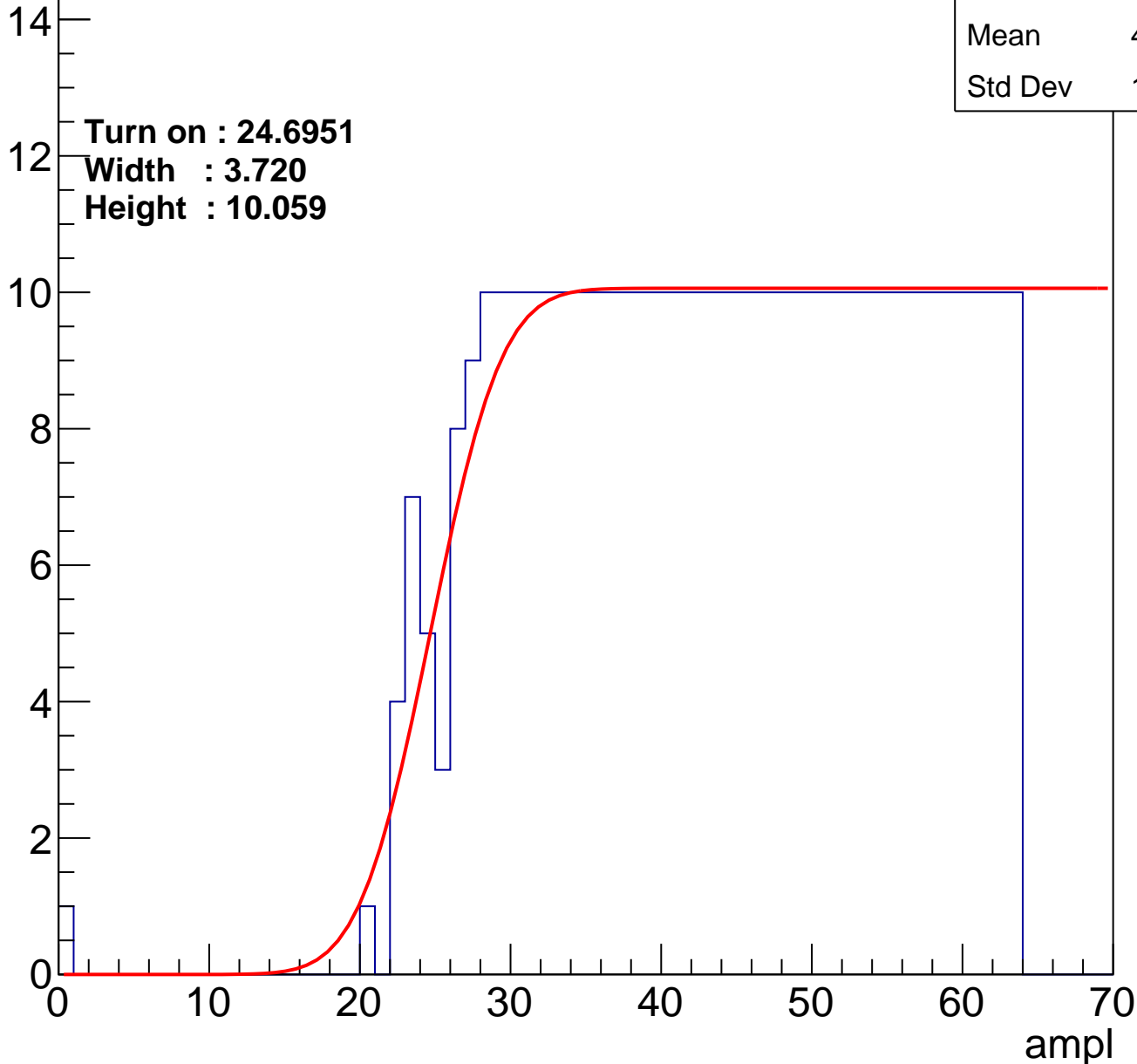
Entries	398
Mean	43.45
Std Dev	11.79

Turn on : 24.6951

Width : 3.720

Height : 10.059

Entry



# B1L002S, U21-ch58

calib\_packv5\_042523\_0143.root, FC#10, port B3

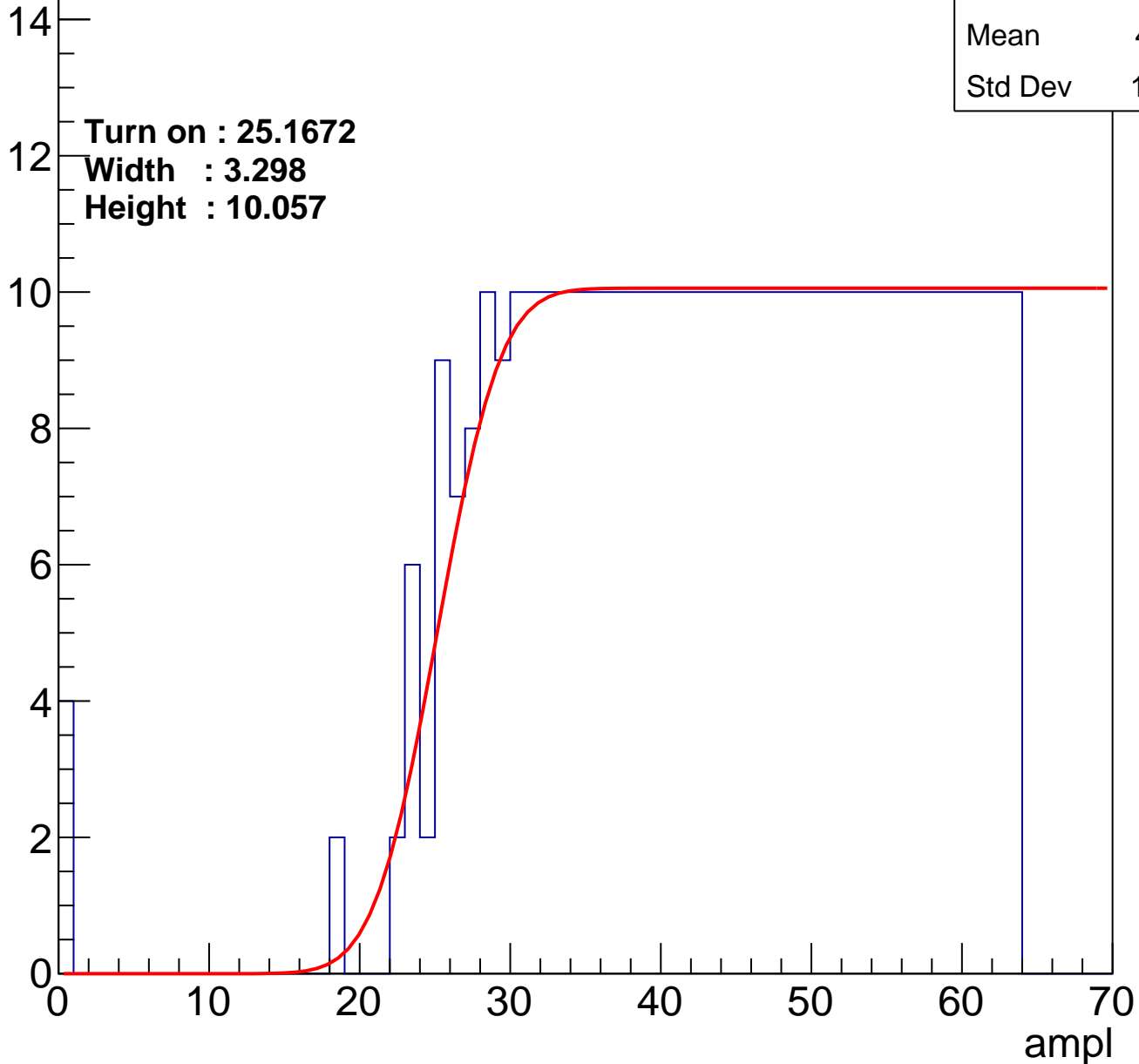
Entries	399
Mean	43.21
Std Dev	12.32

Turn on : 25.1672

Width : 3.298

Height : 10.057

Entry



# B1L002S, U21-ch59

calib\_packv5\_042523\_0143.root, FC#10, port B3

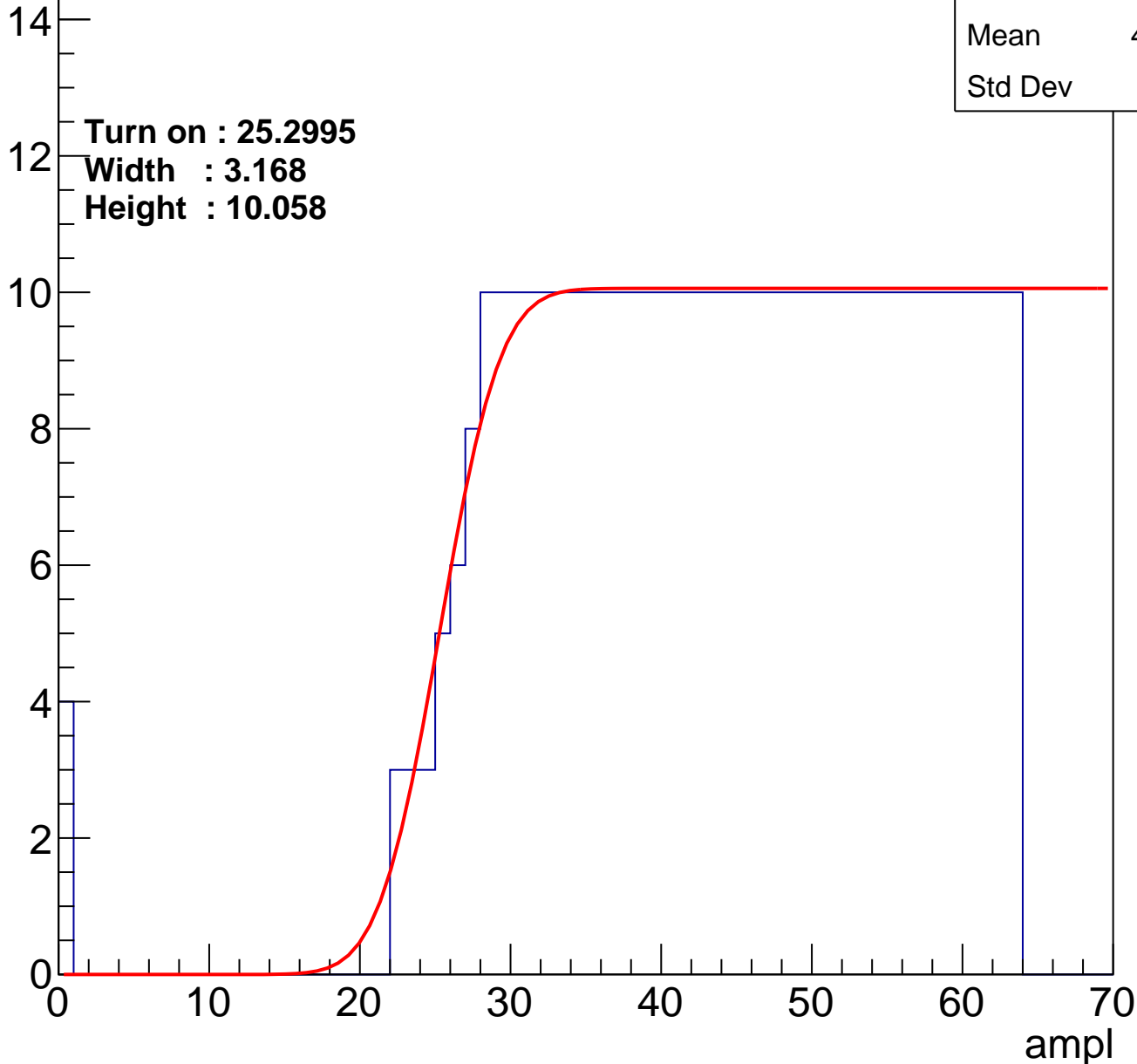
Entries	392
Mean	43.58
Std Dev	12.1

Turn on : 25.2995

Width : 3.168

Height : 10.058

Entry



# B1L002S, U21-ch60

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	409
Mean	42.88
Std Dev	12.19

Turn on : 23.3083

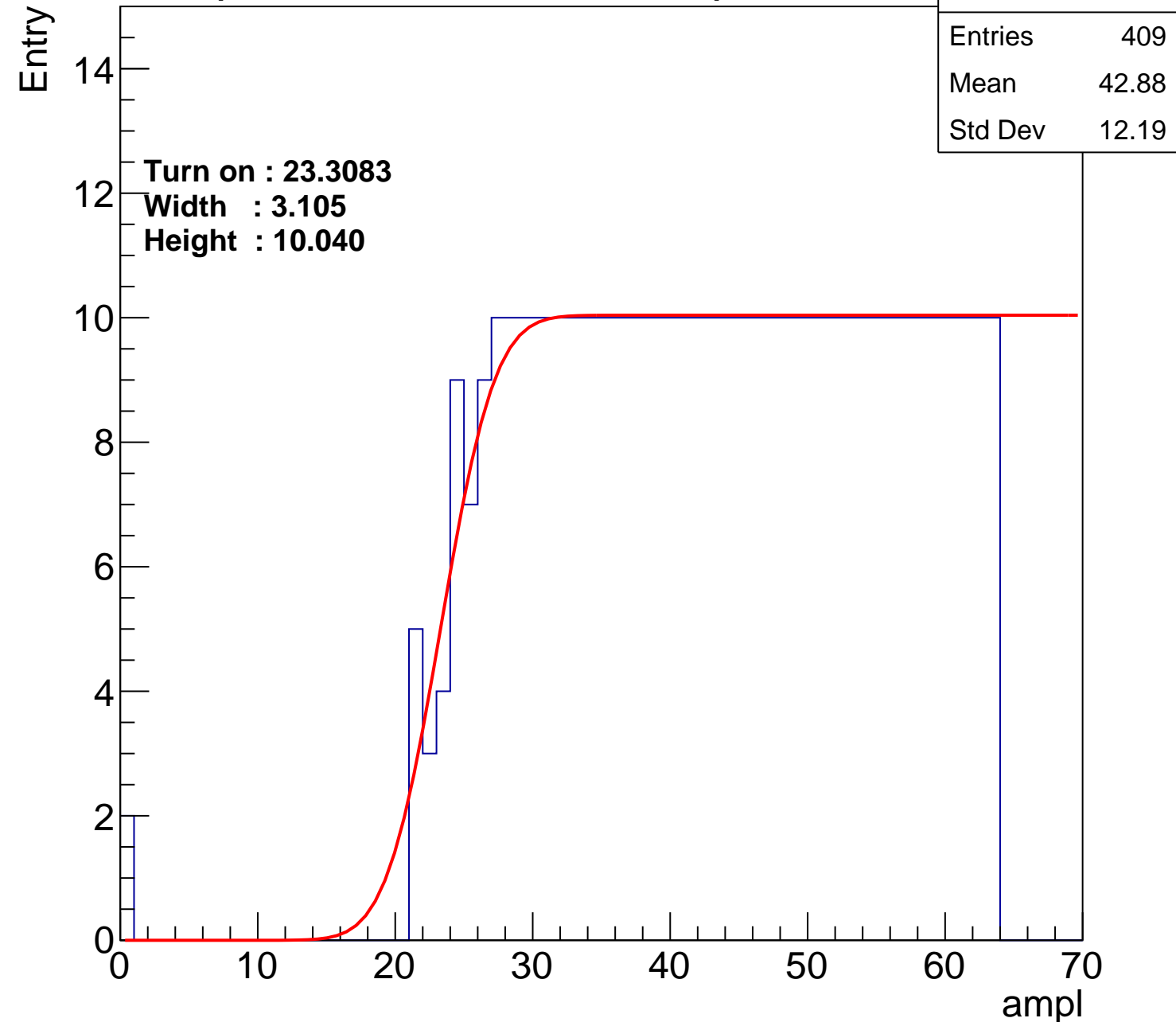
Width : 3.105

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch61

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	395
Mean	43.49
Std Dev	12.03

Turn on : 25.3502

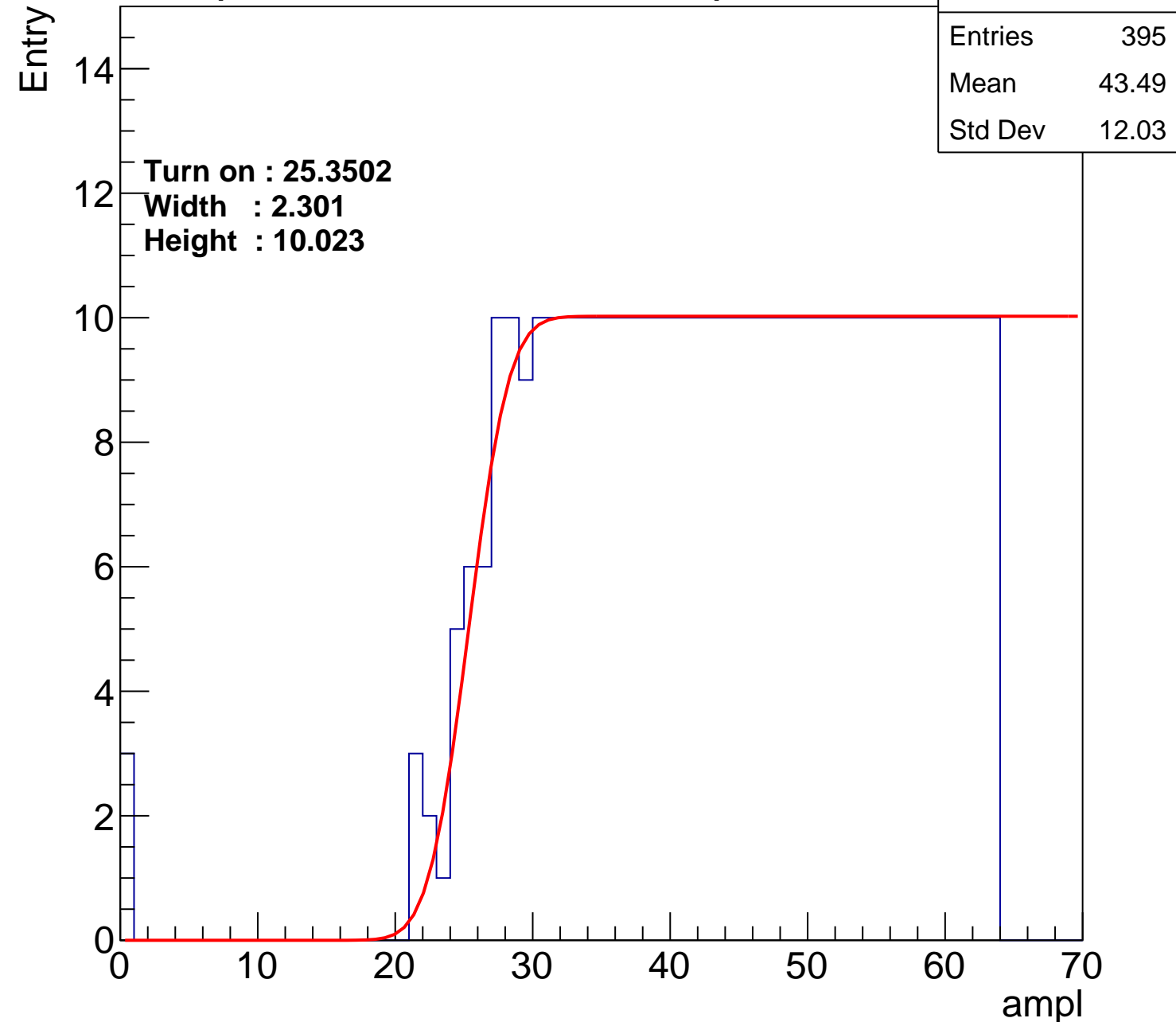
Width : 2.301

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch62

calib\_packv5\_042523\_0143.root, FC#10, port B3

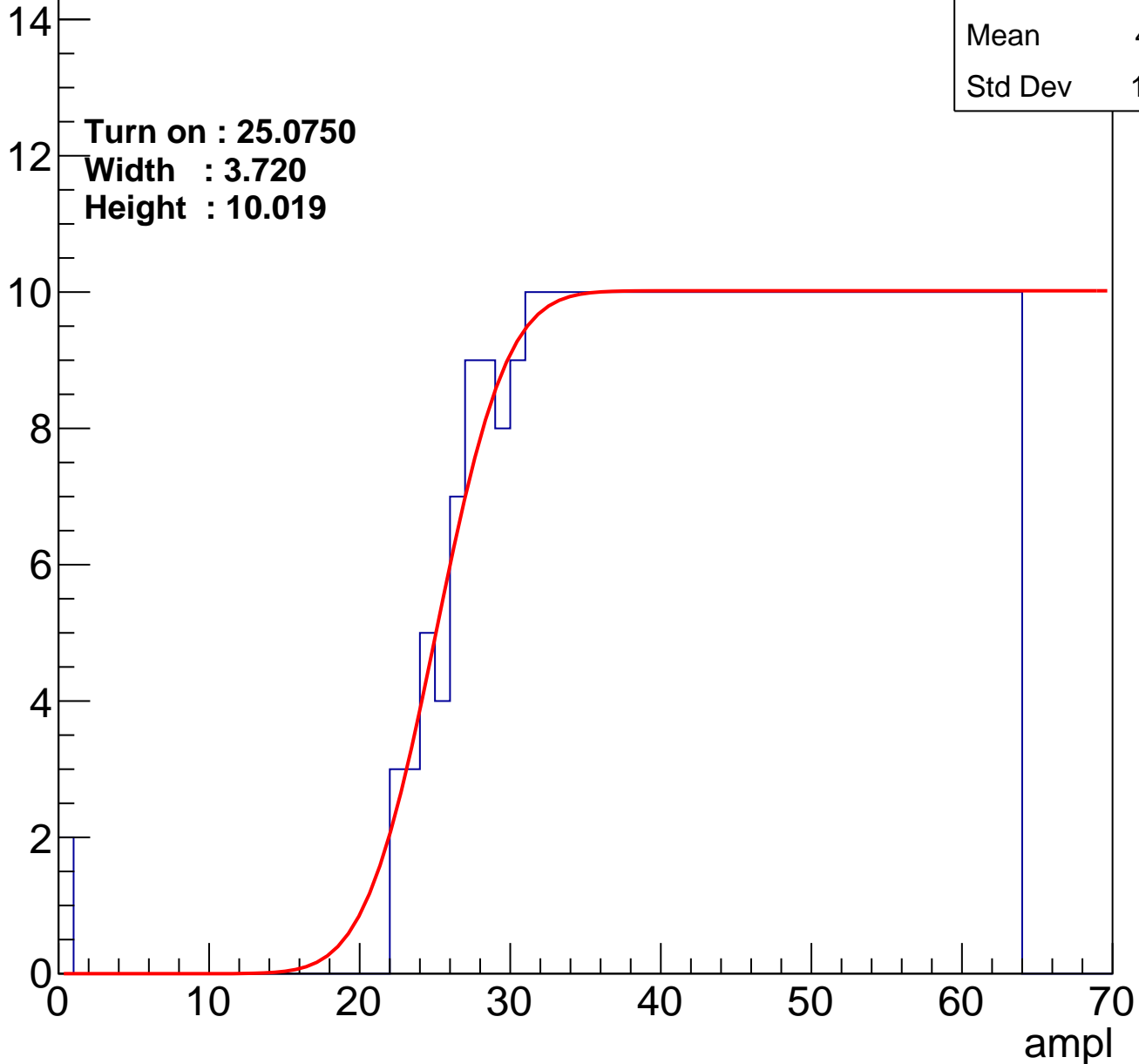
Entries	389
Mean	43.81
Std Dev	11.75

Turn on : 25.0750

Width : 3.720

Height : 10.019

Entry





# B1L002S, U21-ch63

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.68
Std Dev	11.26

Turn on : 27.3509

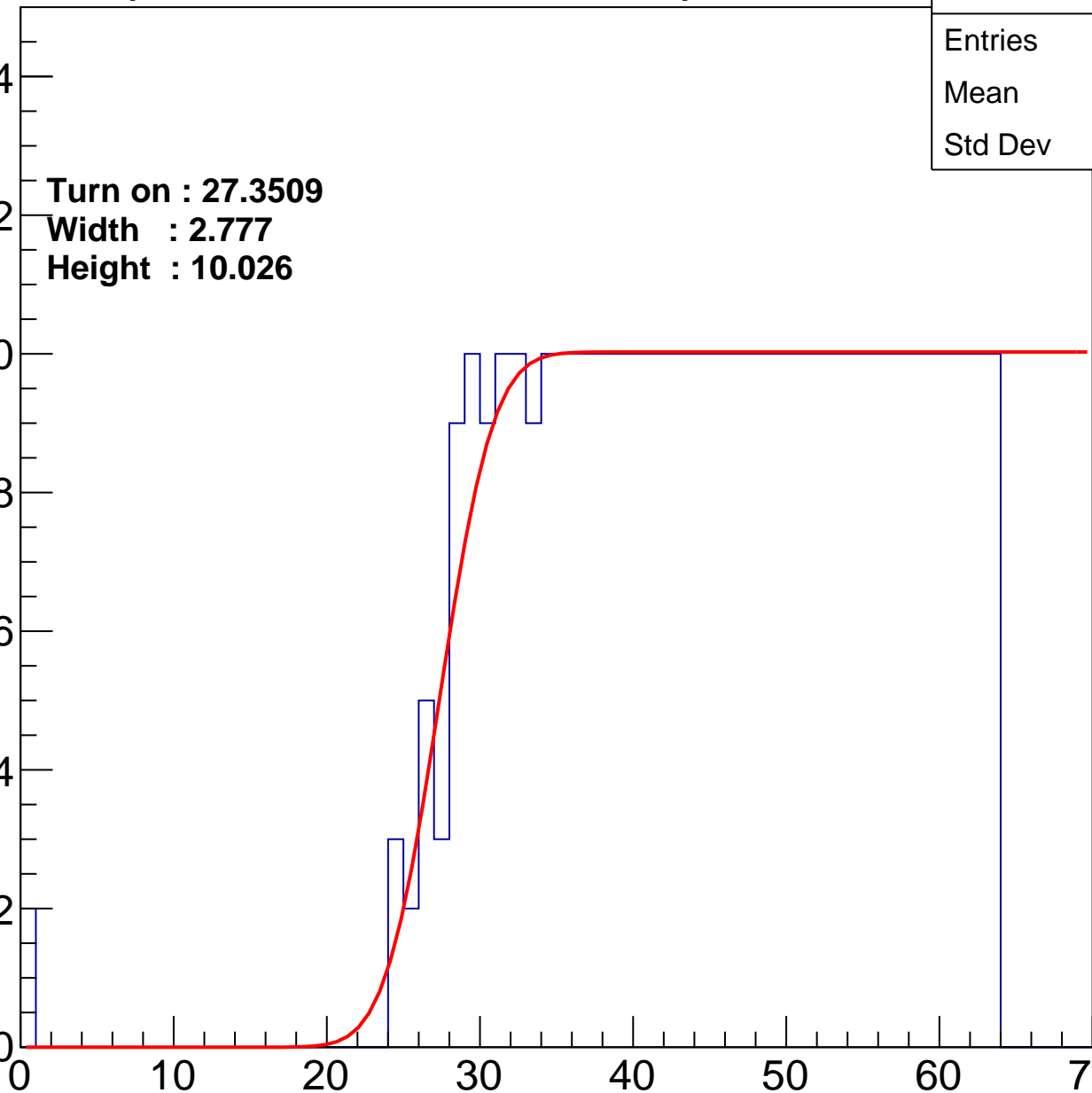
Width : 2.777

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch64

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.77
Std Dev	11.75

Turn on : 25.3775

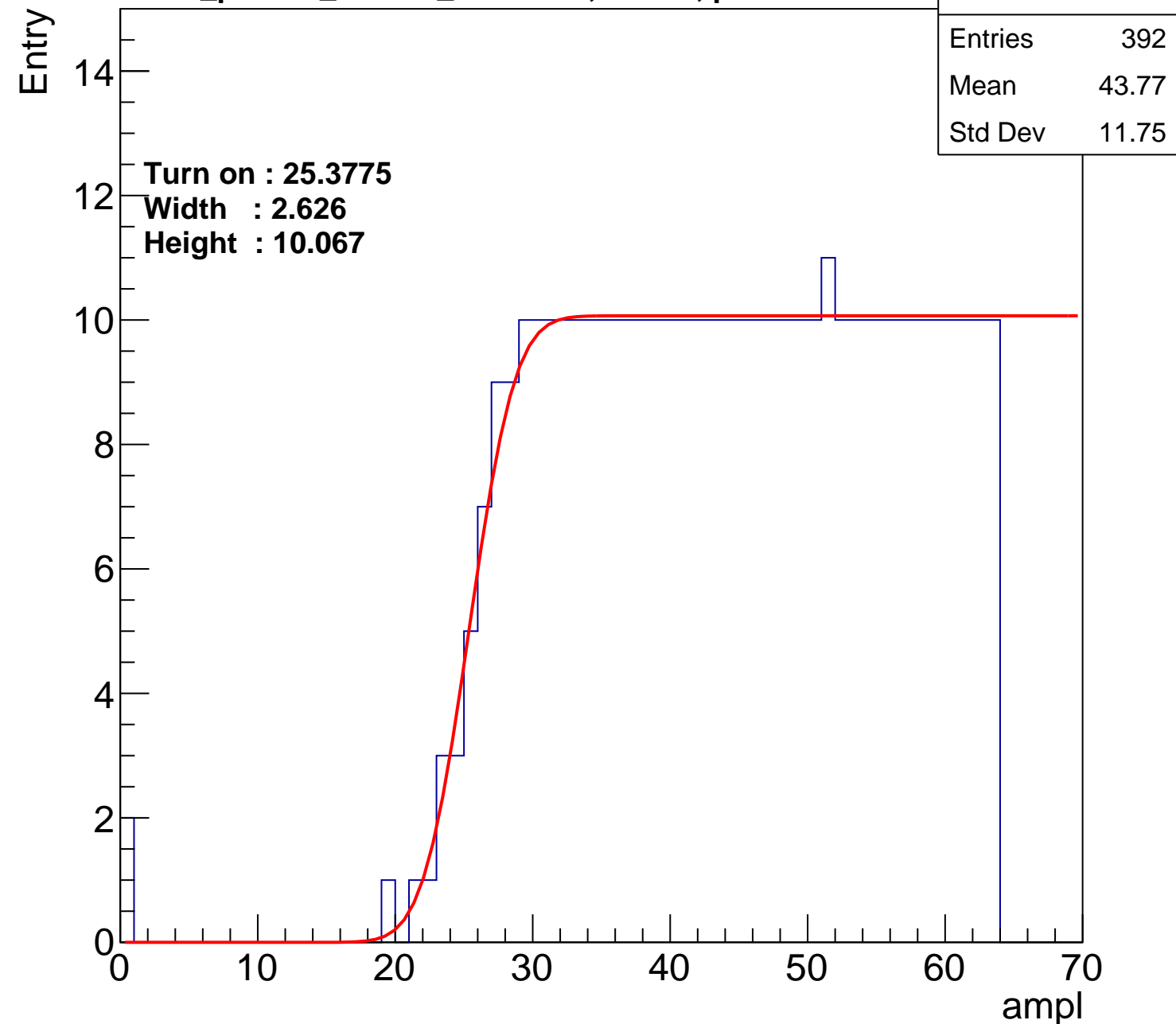
Width : 2.626

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch65

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	402
Mean	43.11
Std Dev	12.25

Turn on : 24.5928

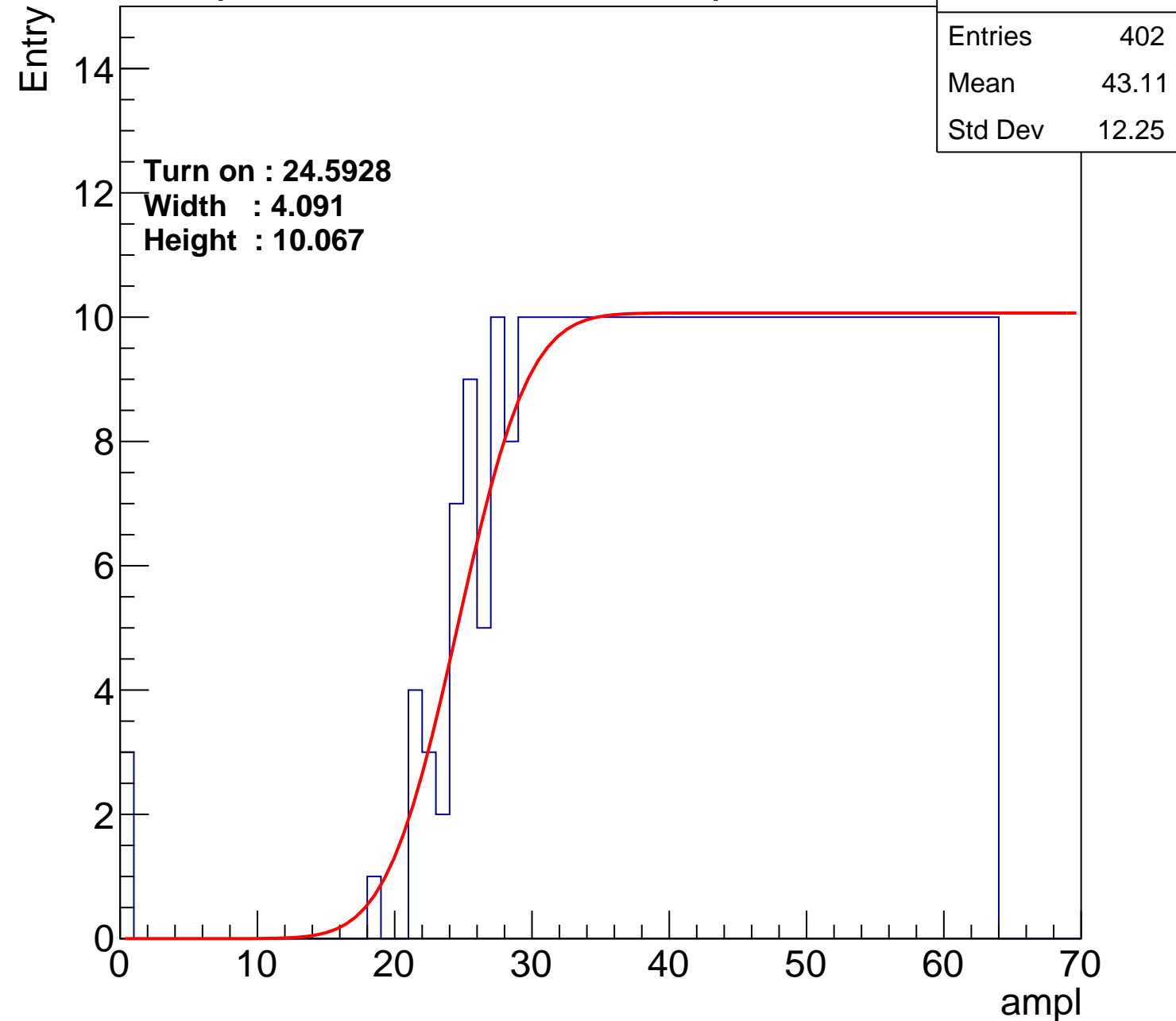
Width : 4.091

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch66

calib\_packv5\_042523\_0143.root, FC#10, port B3

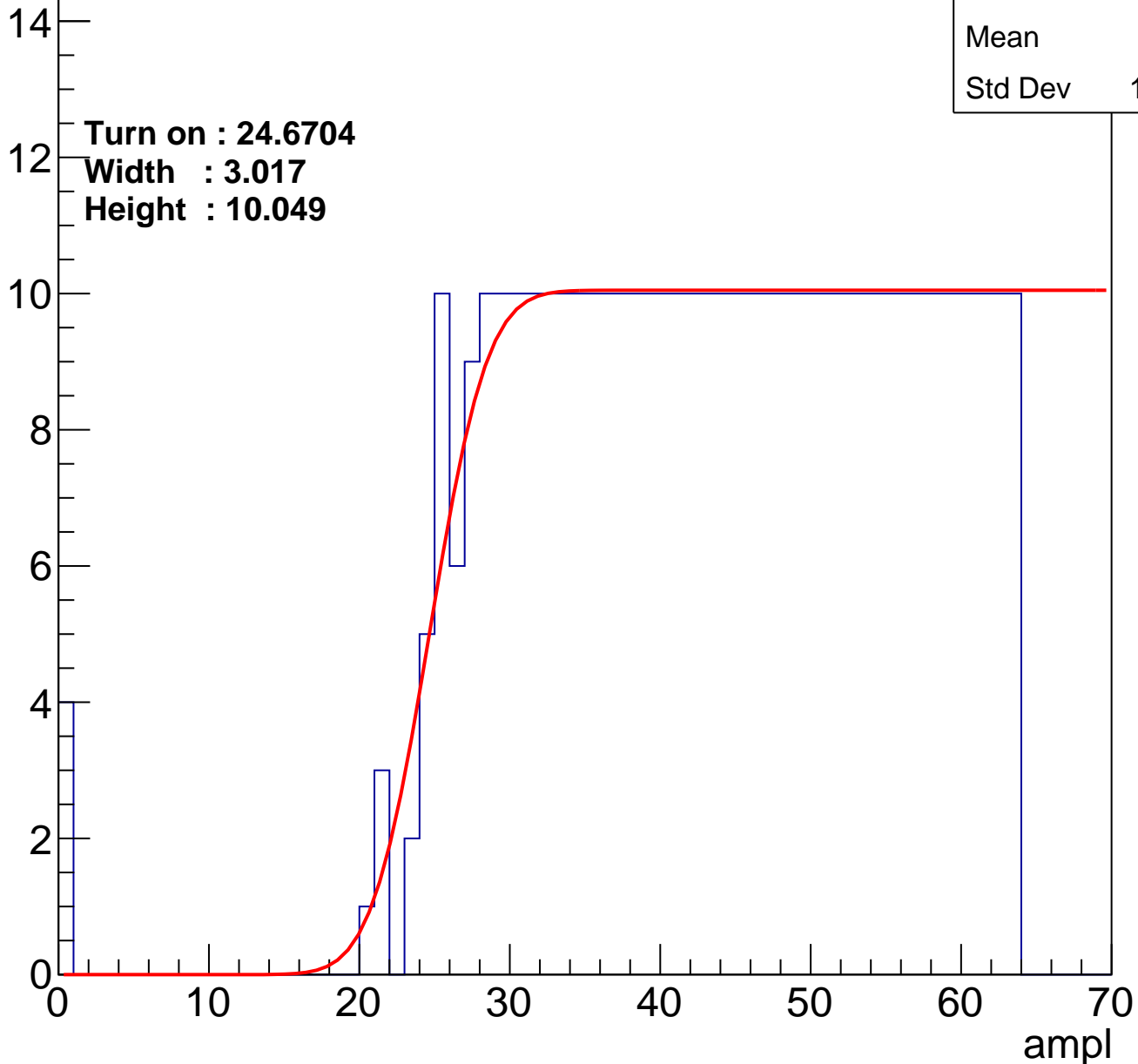
Entries	400
Mean	43.2
Std Dev	12.28

**Turn on : 24.6704**

**Width : 3.017**

**Height : 10.049**

Entry



# B1L002S, U21-ch67

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	407
Mean	42.92
Std Dev	12.29

Turn on : 23.5796

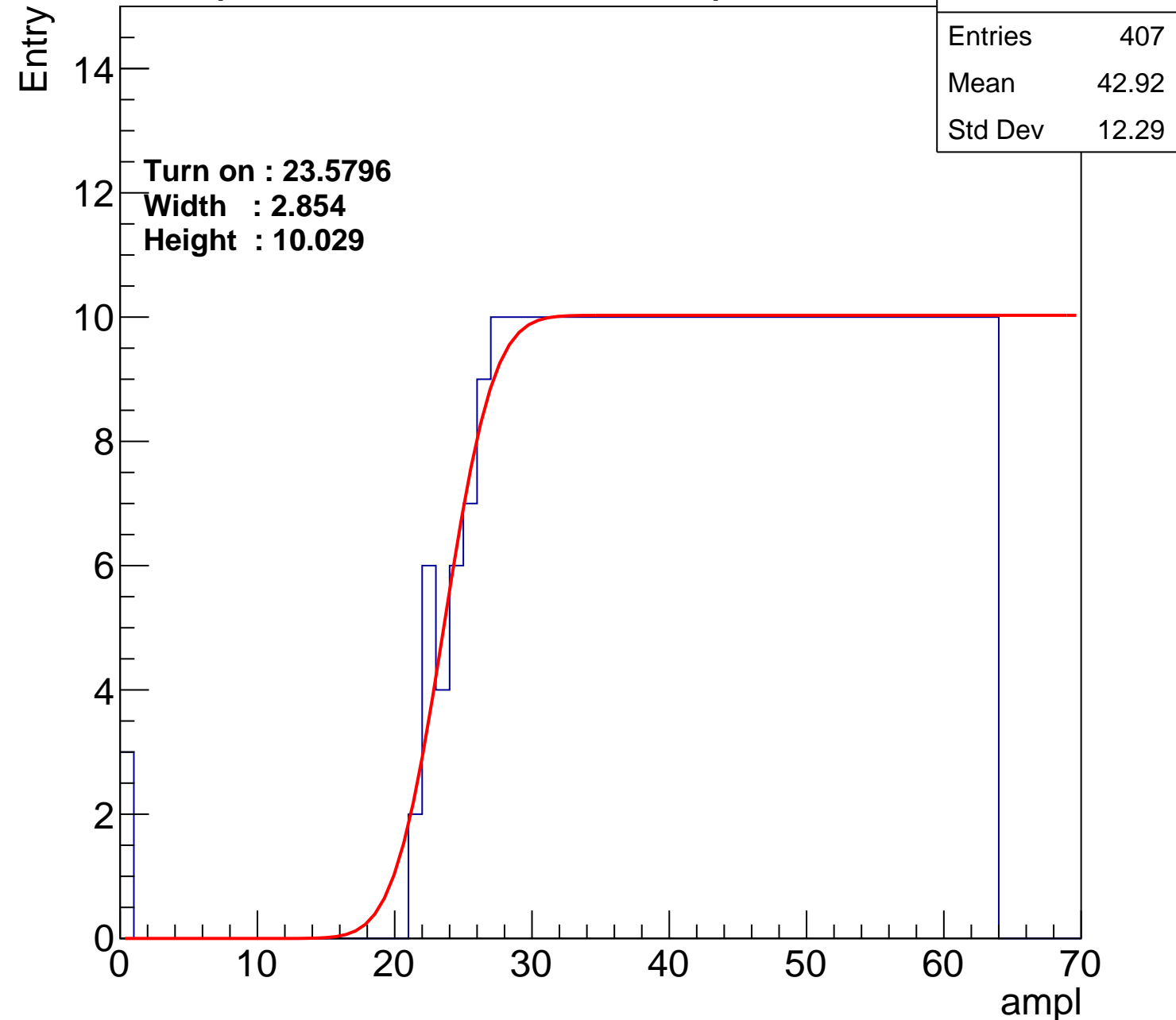
Width : 2.854

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#10, port B3**

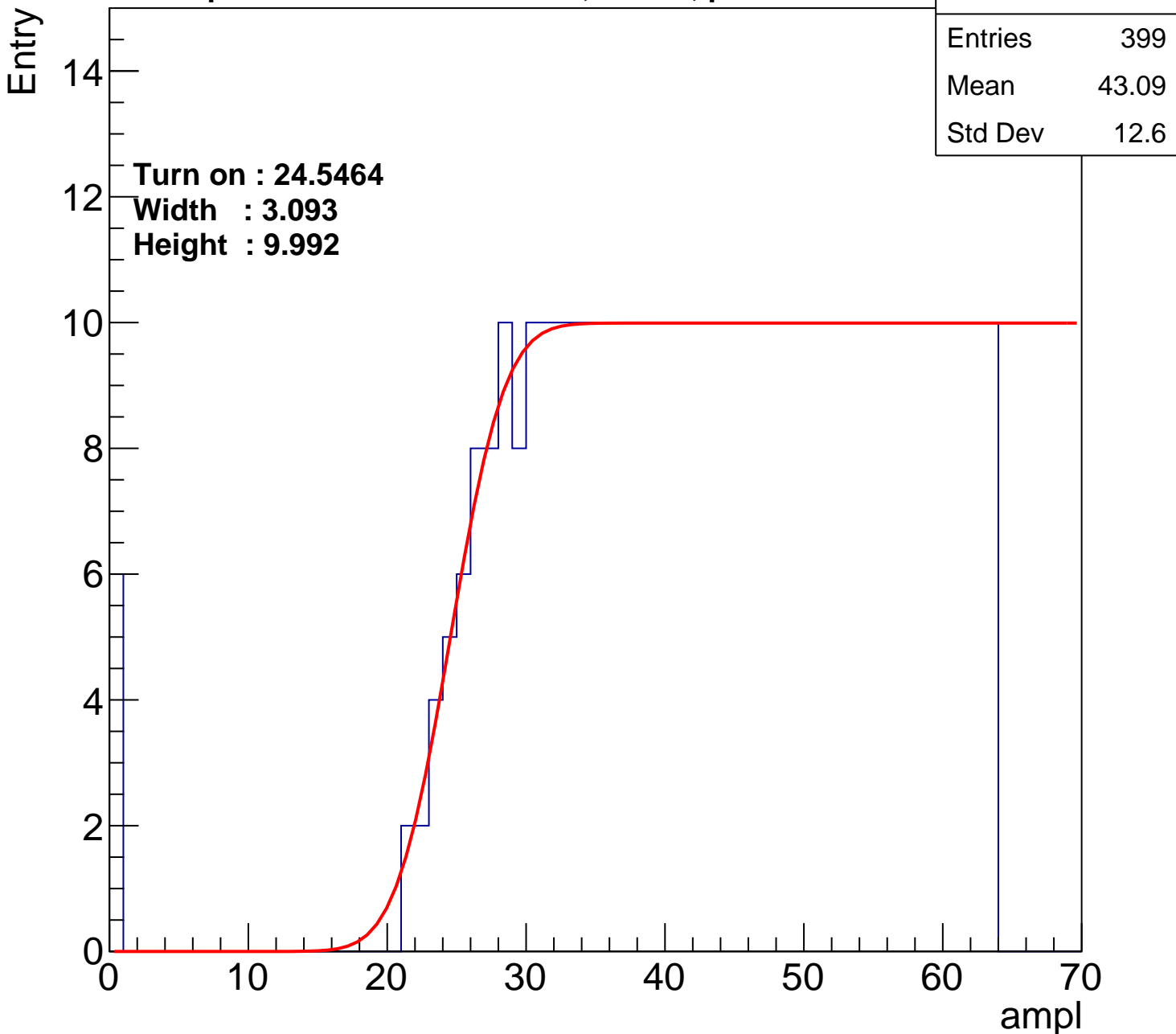
Entries	399
Mean	43.09
Std Dev	12.6

Mean	43.09
------	-------

Std Dev	12.6
---------	------

**Width : 3.093**

**Height : 9.992**



# B1L002S, U21-ch69

calib\_packv5\_042523\_0143.root, FC#10, port B3

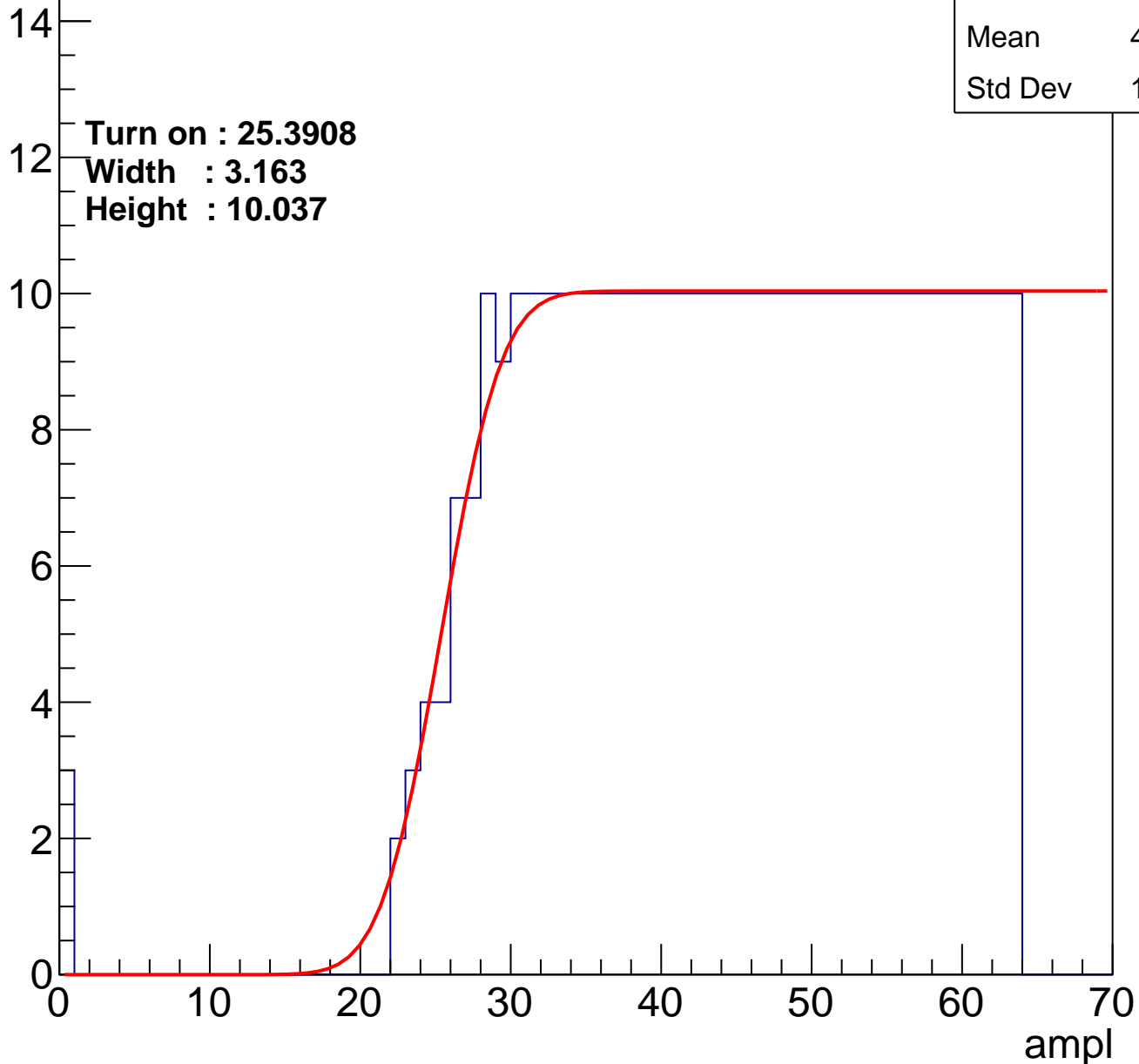
Entries	389
Mean	43.78
Std Dev	11.87

Turn on : 25.3908

Width : 3.163

Height : 10.037

Entry



# B1L002S, U21-ch70

calib\_packv5\_042523\_0143.root, FC#10, port B3

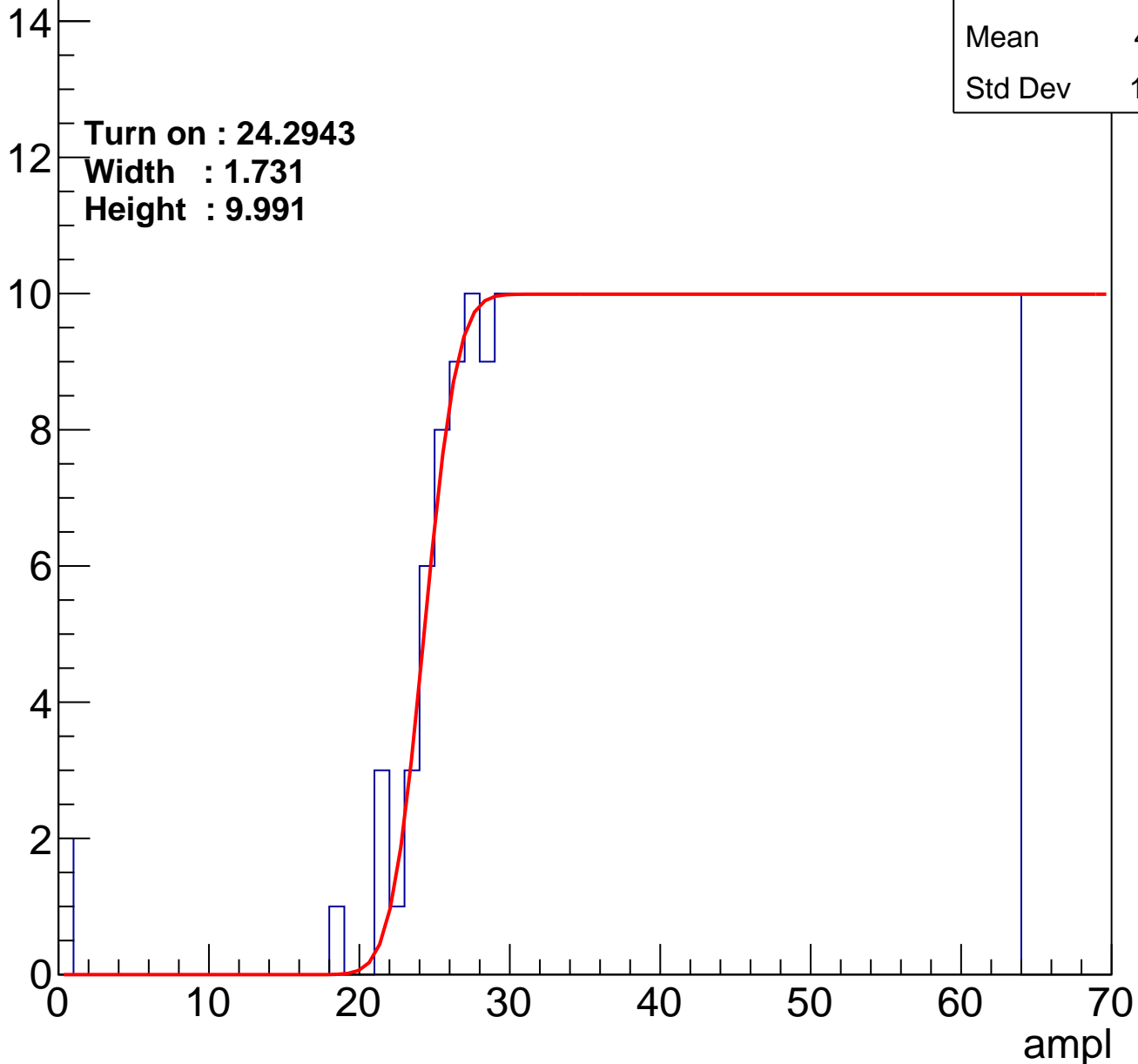
Entries	402
Mean	43.21
Std Dev	12.03

Turn on : 24.2943

Width : 1.731

Height : 9.991

Entry





# B1L002S, U21-ch71

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.74
Std Dev	12.01

Turn on : 25.8651

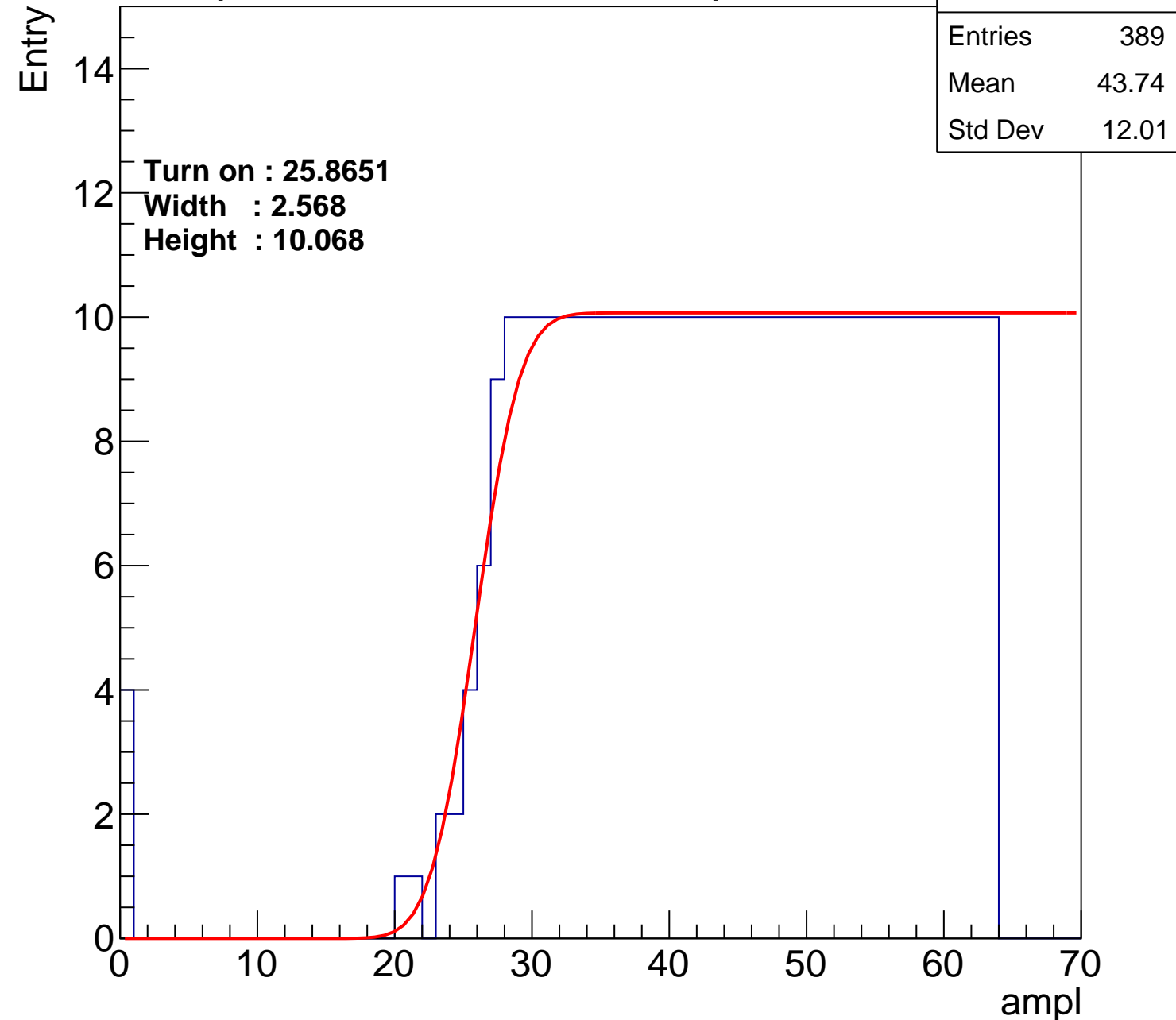
Width : 2.568

Height : 10.068

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch72

calib\_packv5\_042523\_0143.root, FC#10, port B3

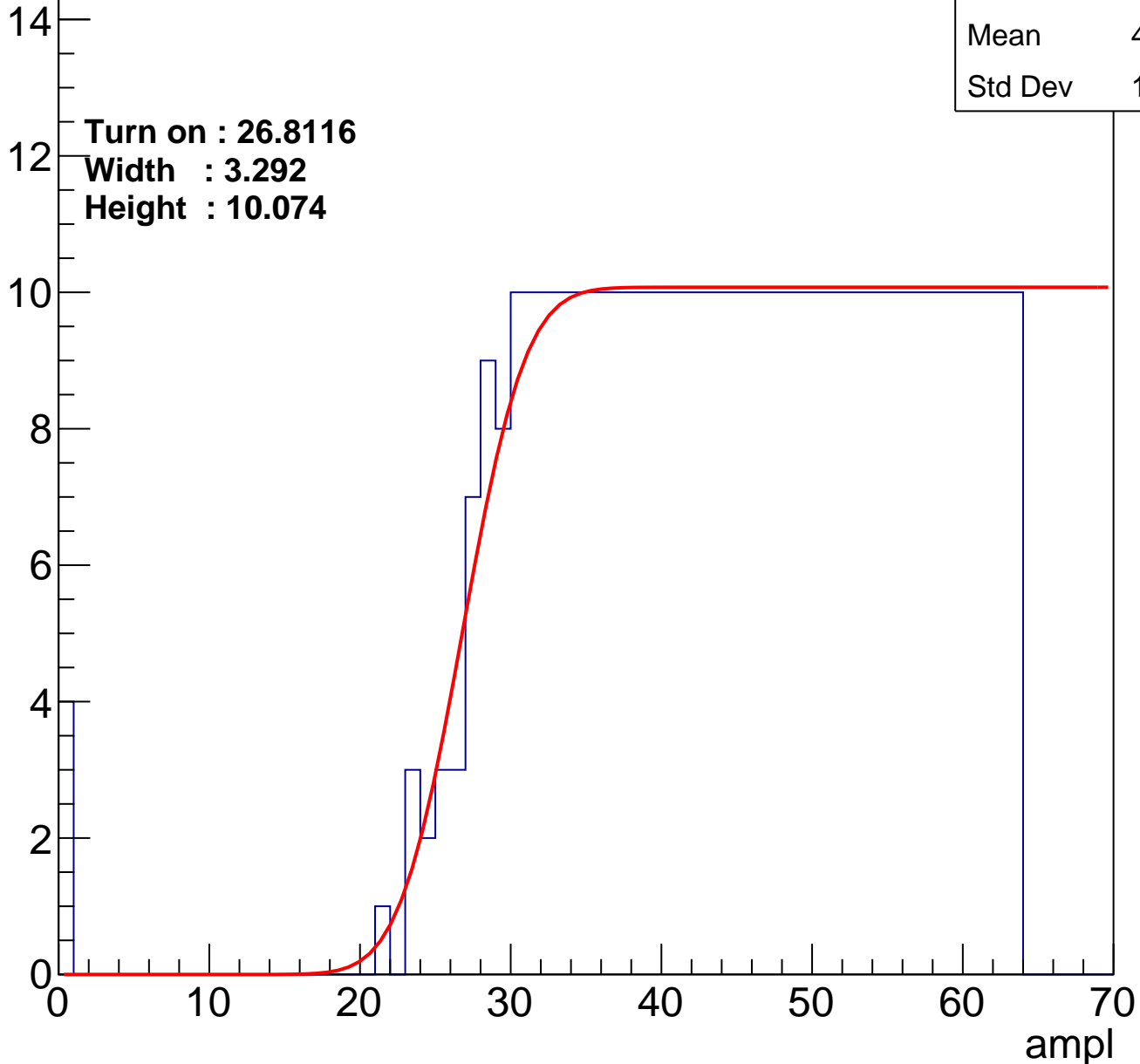
Entries	380
Mean	44.14
Std Dev	11.86

Turn on : 26.8116

Width : 3.292

Height : 10.074

Entry



# B1L002S, U21-ch73

calib\_packv5\_042523\_0143.root, FC#10, port B3

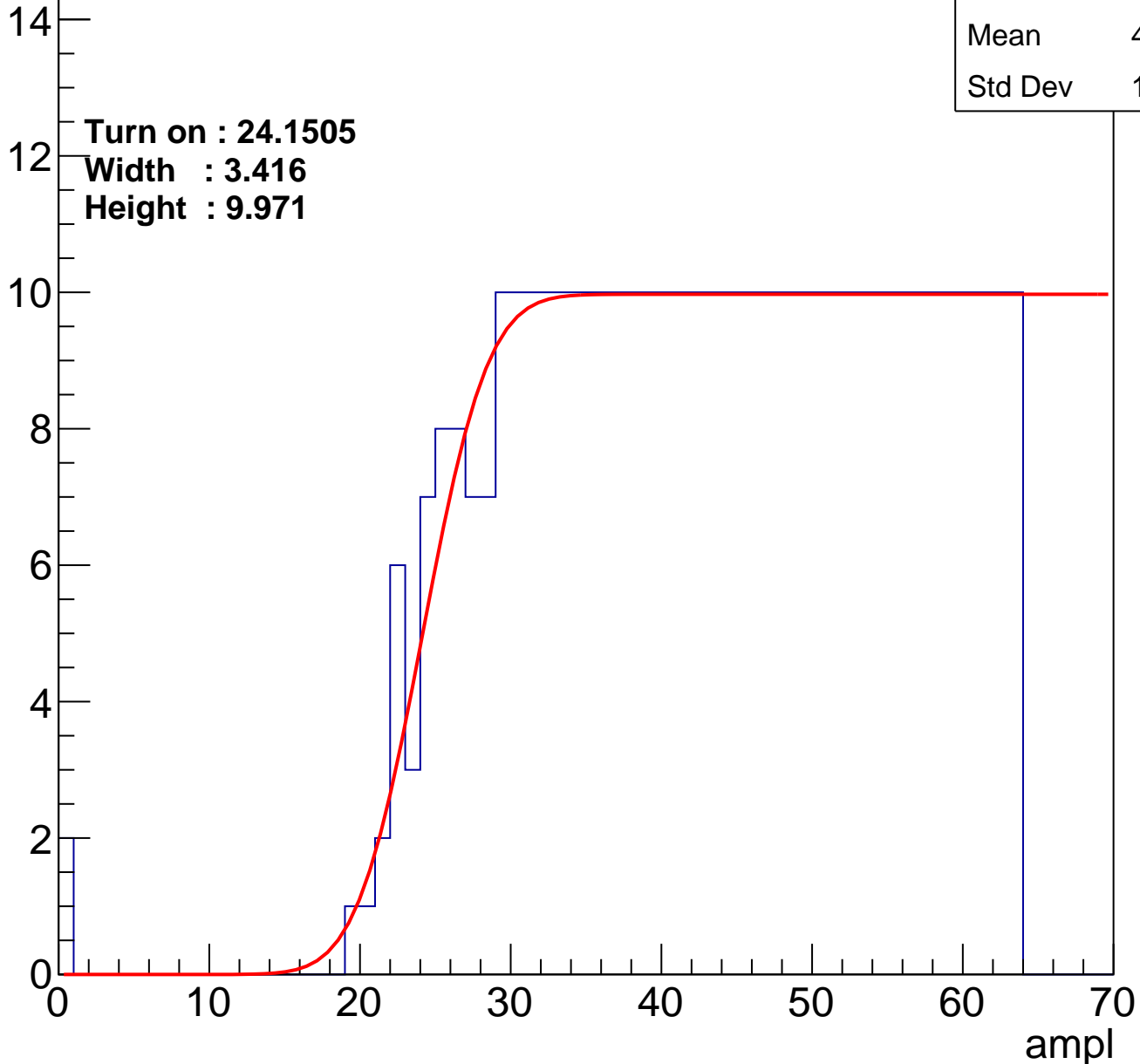
Entries	402
Mean	43.14
Std Dev	12.14

Turn on : 24.1505

Width : 3.416

Height : 9.971

Entry



# B1L002S, U21-ch74

calib\_packv5\_042523\_0143.root, FC#10, port B3

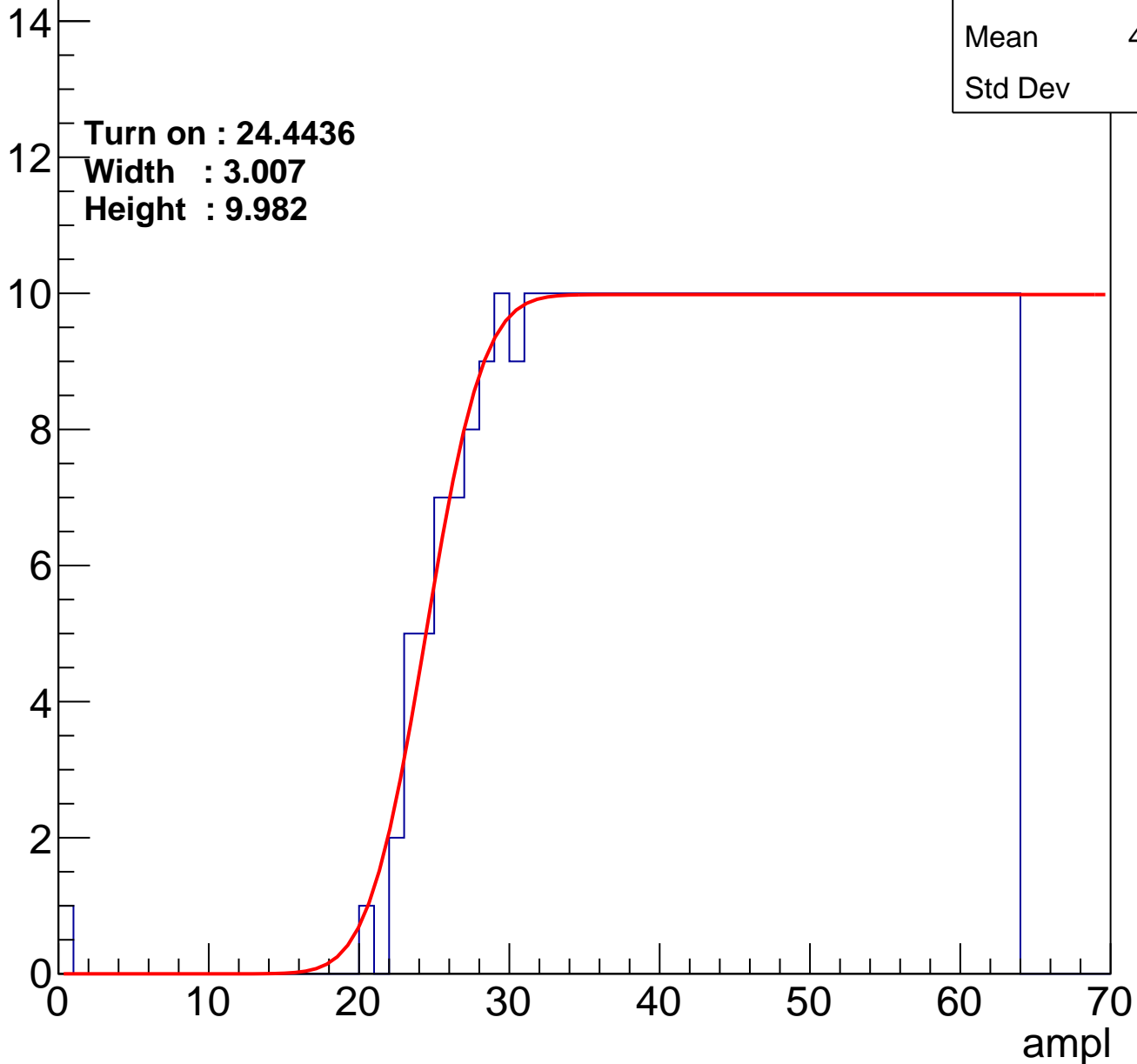
Entries	394
Mean	43.64
Std Dev	11.7

Turn on : 24.4436

Width : 3.007

Height : 9.982

Entry



# B1L002S, U21-ch75

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	403
Mean	43.14
Std Dev	12.09

Turn on : 24.4151

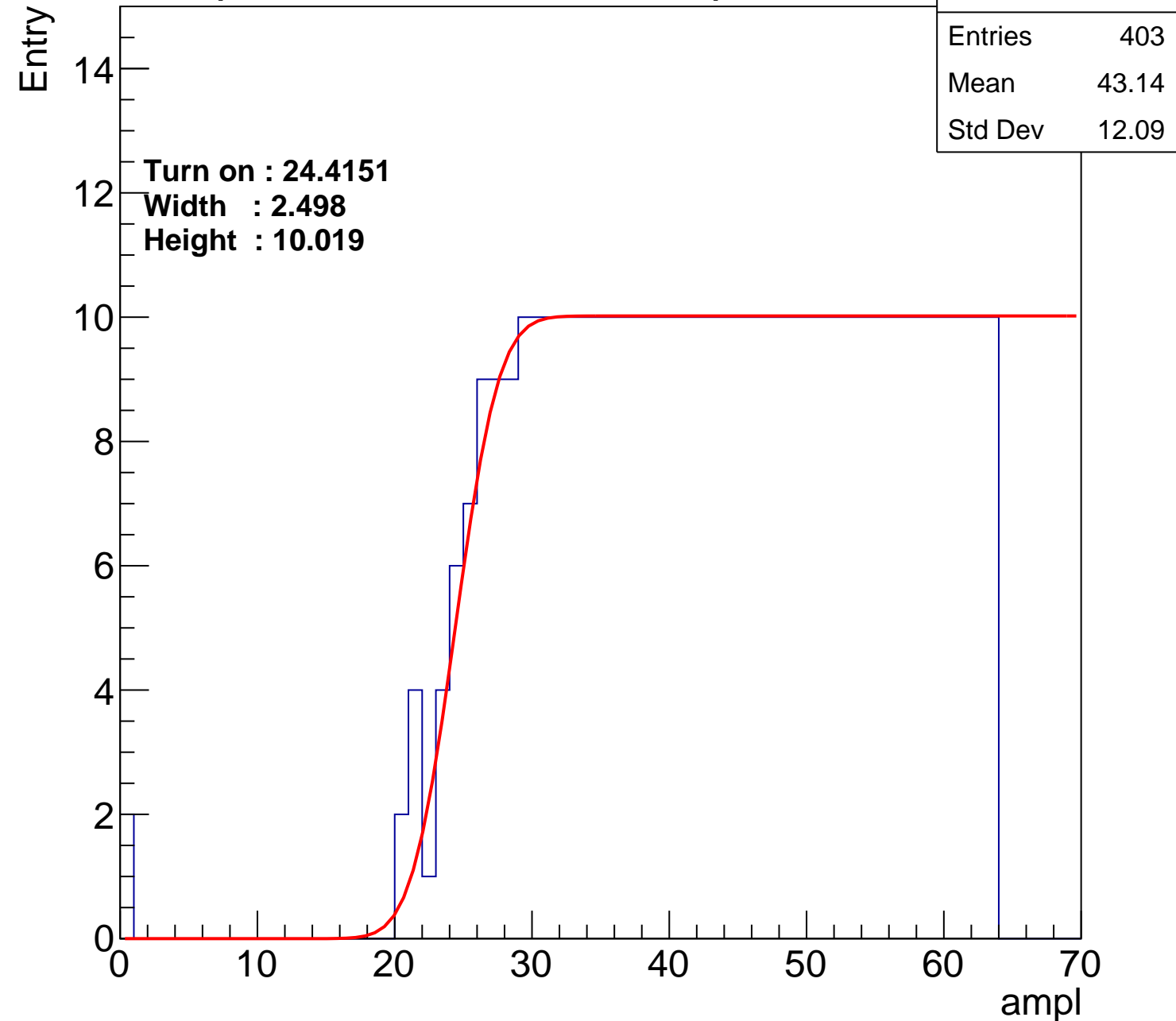
Width : 2.498

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch76

calib\_packv5\_042523\_0143.root, FC#10, port B3

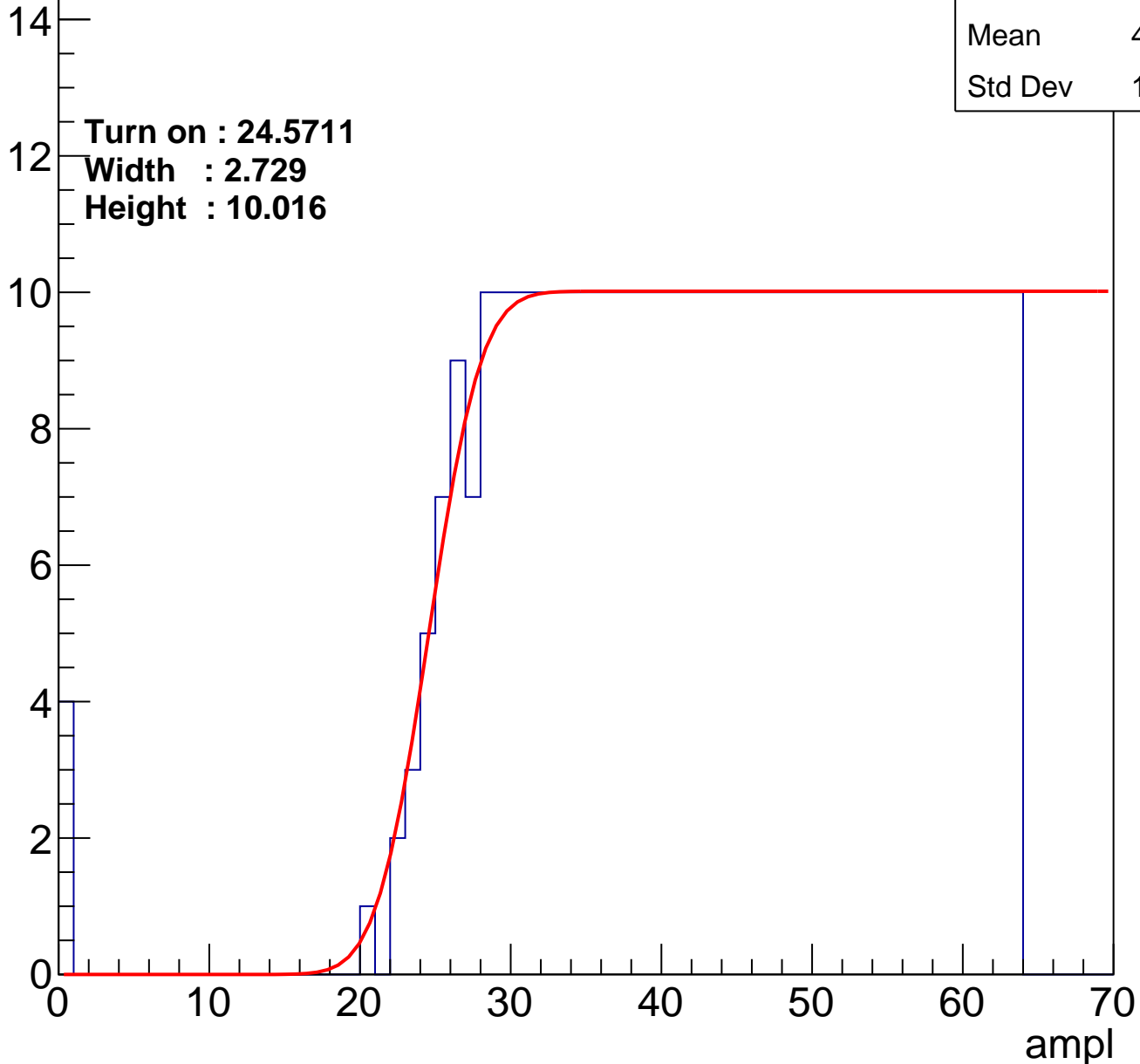
Entries	398
Mean	43.29
Std Dev	12.23

Turn on : 24.5711

Width : 2.729

Height : 10.016

Entry



# B1L002S, U21-ch77

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	403
Mean	43.23
Std Dev	11.89

Turn on : 24.2785

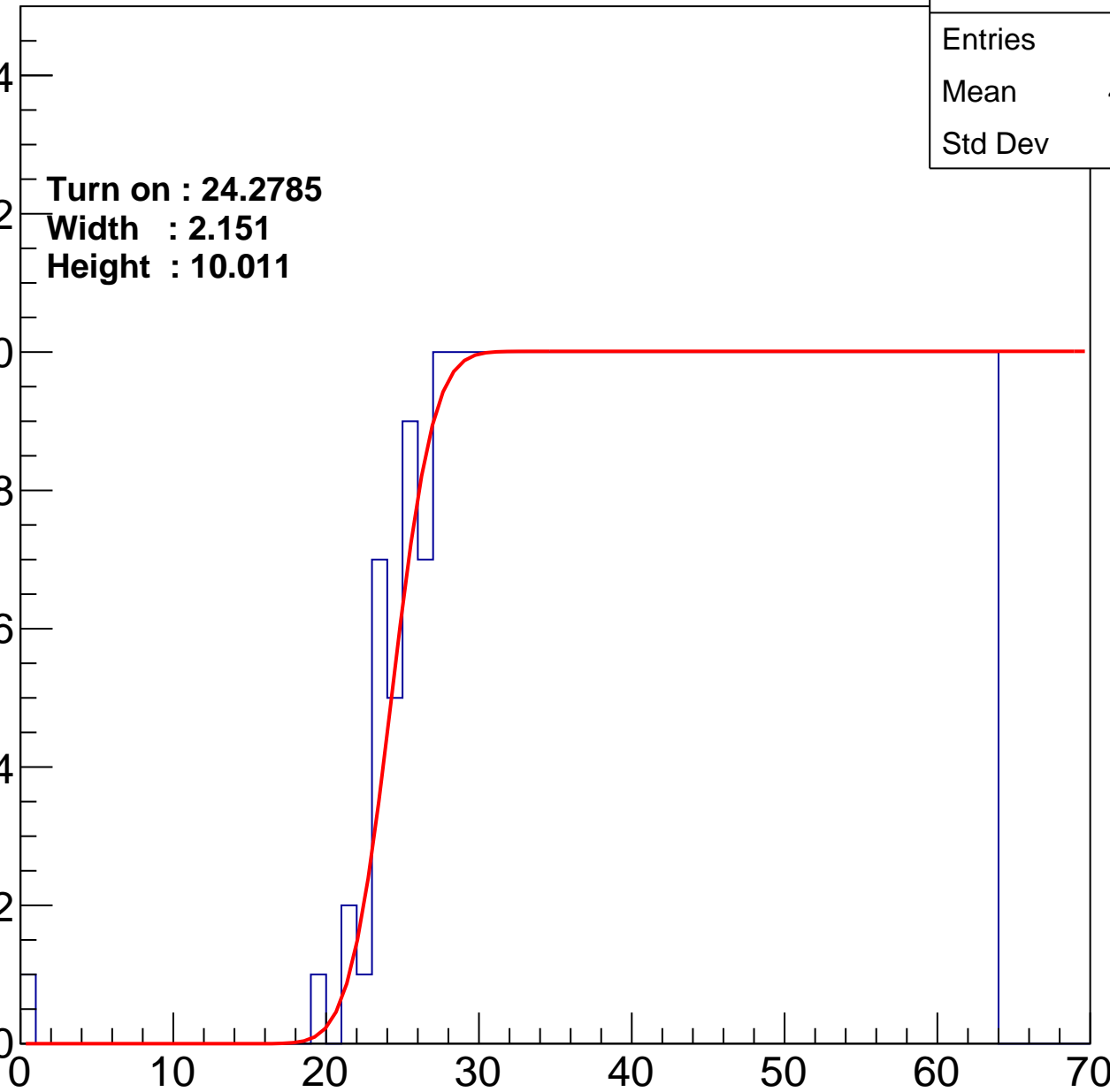
Width : 2.151

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch78

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.82
Std Dev	11.7

Turn on : 25.5911

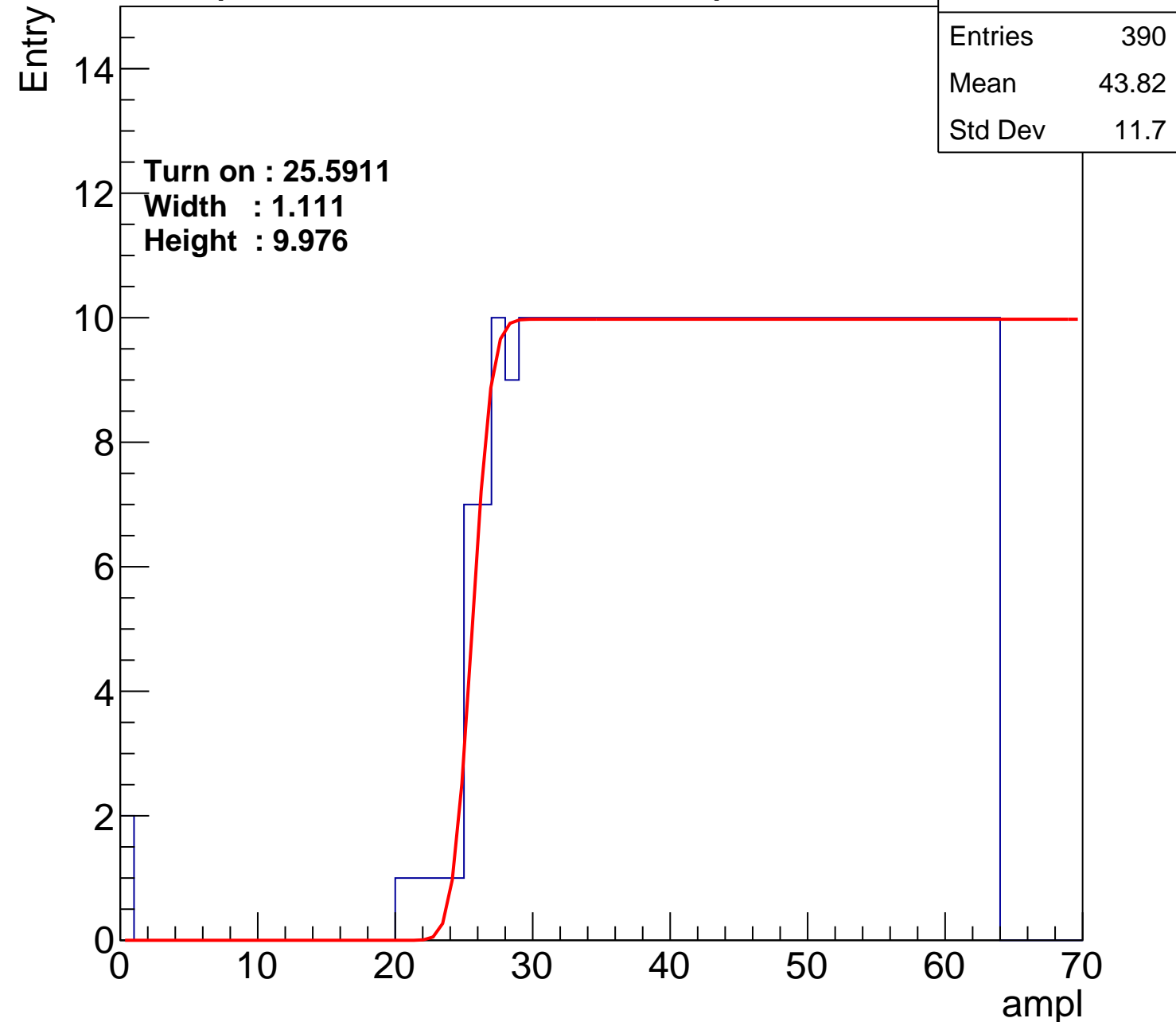
Width : 1.111

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch79

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.8
Std Dev	11.84

Turn on : 25.6465

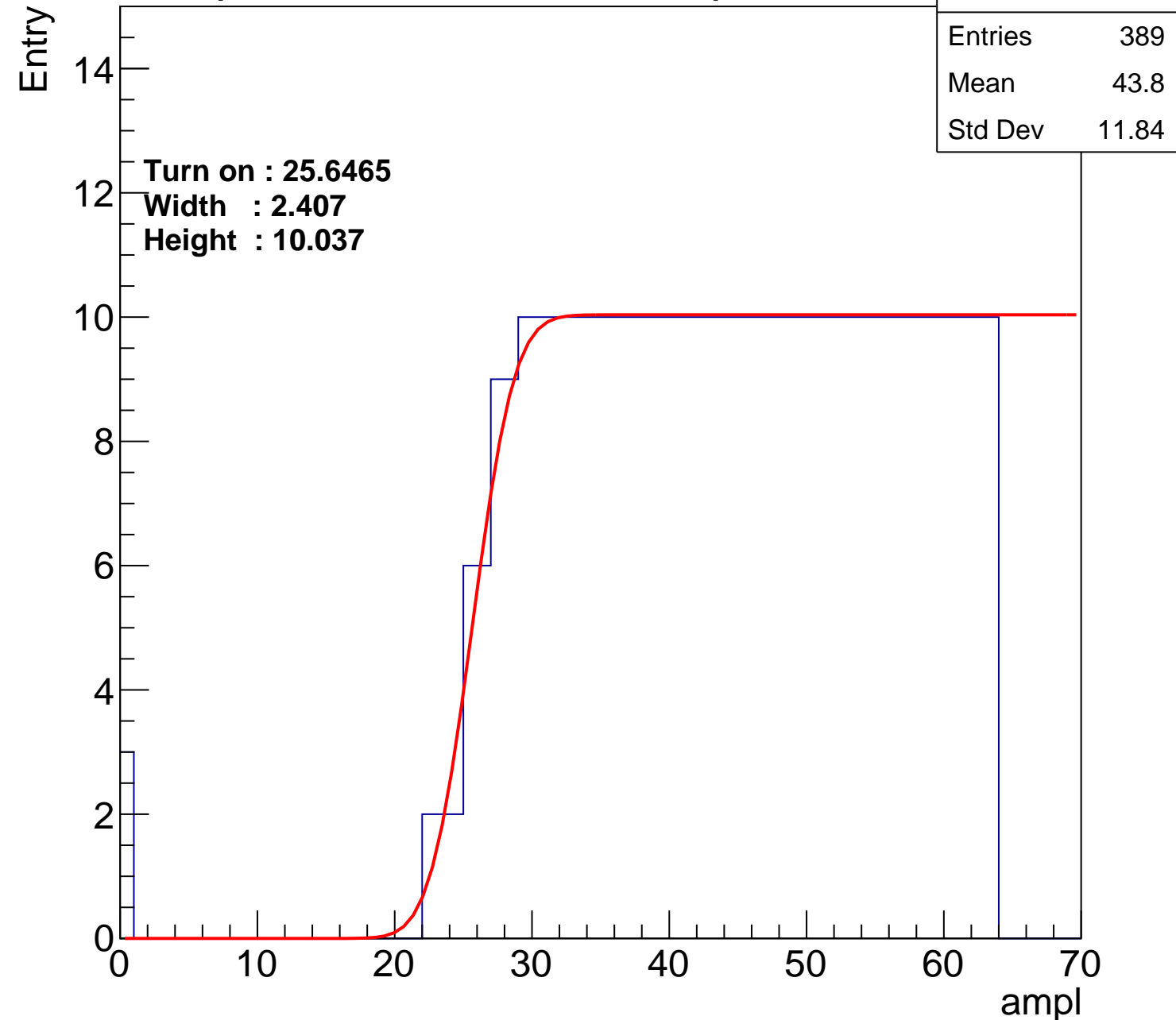
Width : 2.407

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch80

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	412
Mean	42.75
Std Dev	12.19

Turn on : 22.7865

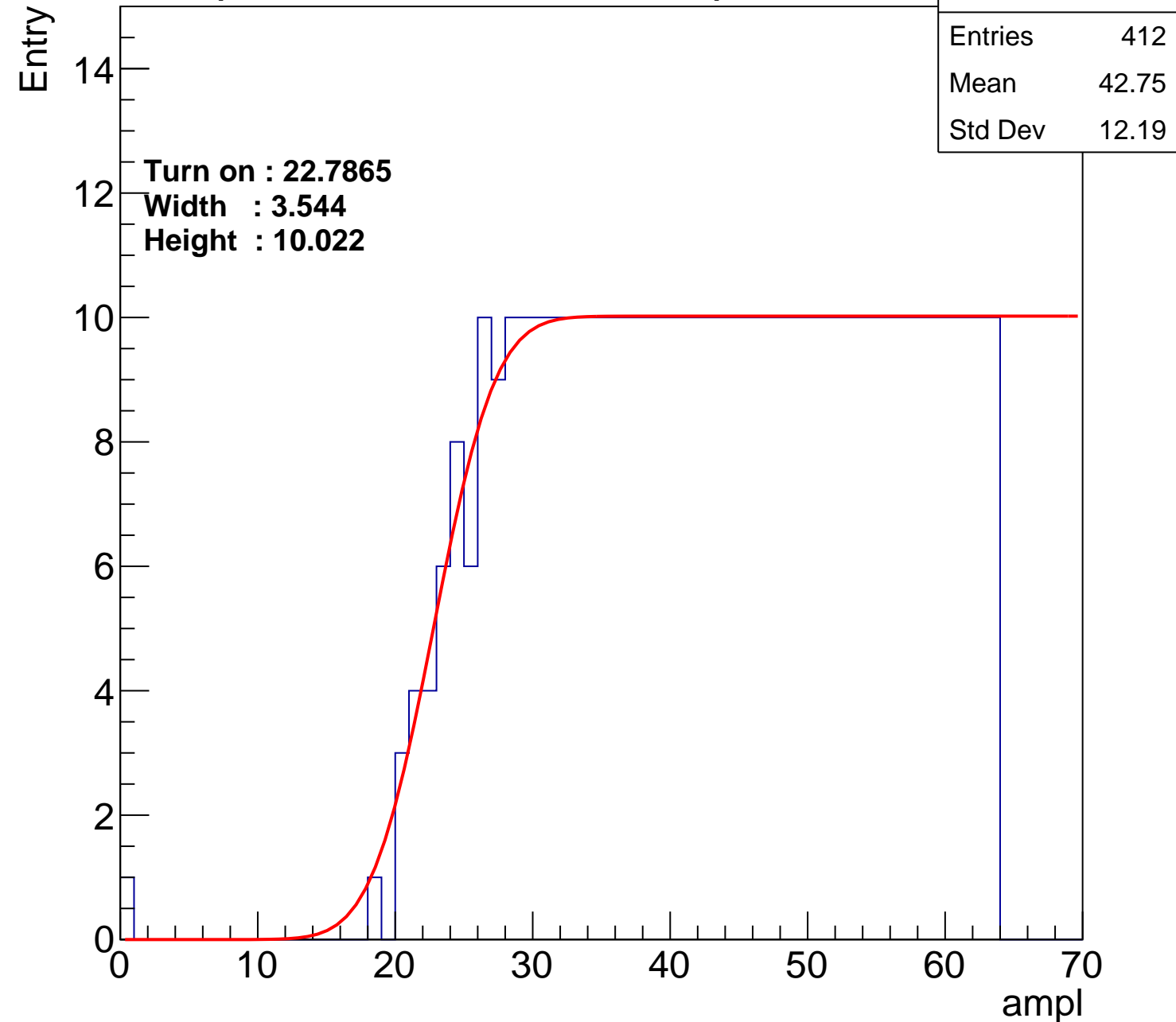
Width : 3.544

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch81

calib\_packv5\_042523\_0143.root, FC#10, port B3

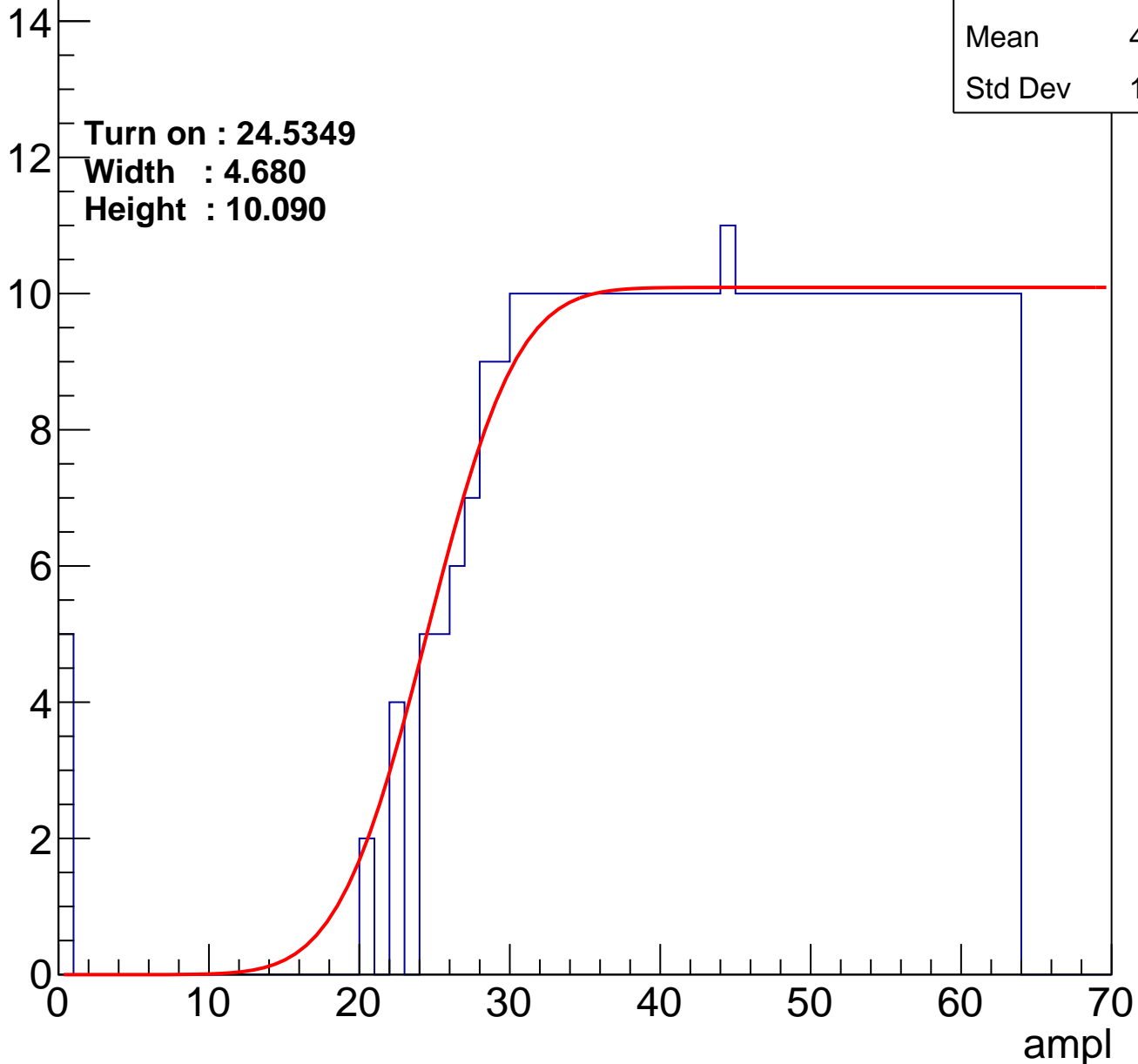
Entries	393
Mean	43.47
Std Dev	12.32

Turn on : 24.5349

Width : 4.680

Height : 10.090

Entry



# B1L002S, U21-ch82

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	395
Mean	43.44
Std Dev	12.17

Turn on : 25.4271

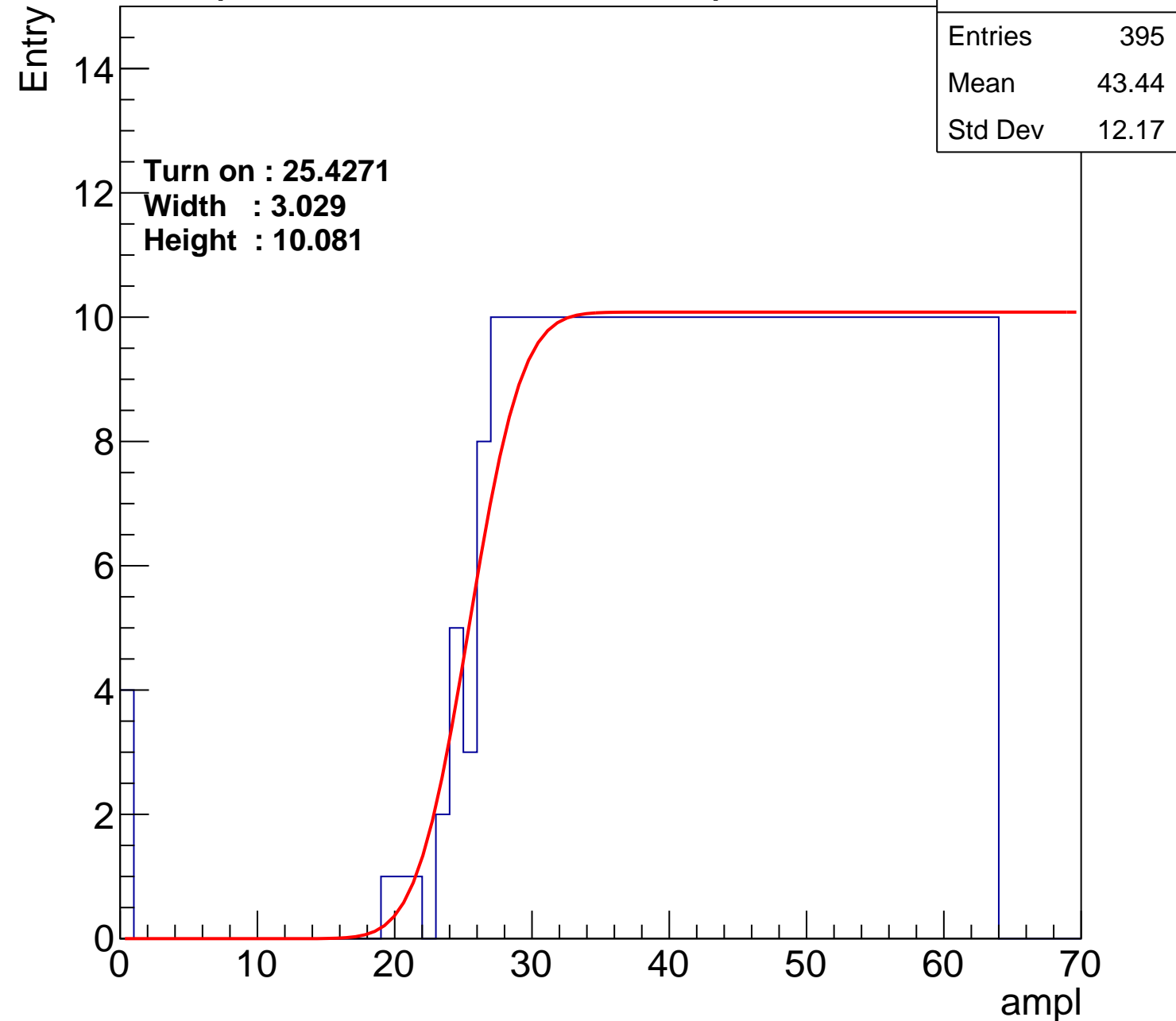
Width : 3.029

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch83

calib\_packv5\_042523\_0143.root, FC#10, port B3

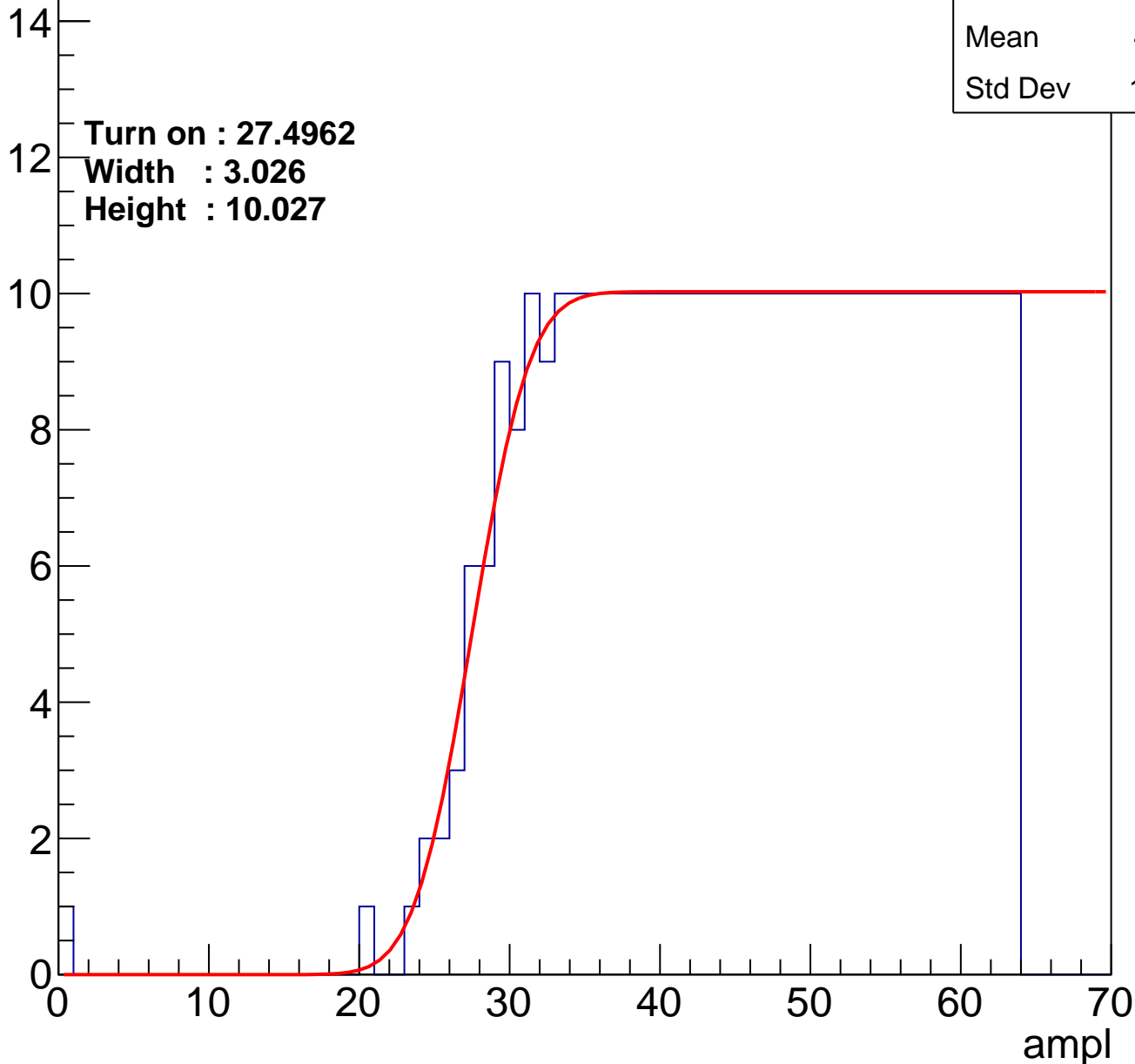
Entry

Entries	368
Mean	44.91
Std Dev	11.03

Turn on : 27.4962

Width : 3.026

Height : 10.027



# B1L002S, U21-ch84

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	405
Mean	43.1
Std Dev	12

Turn on : 23.6445

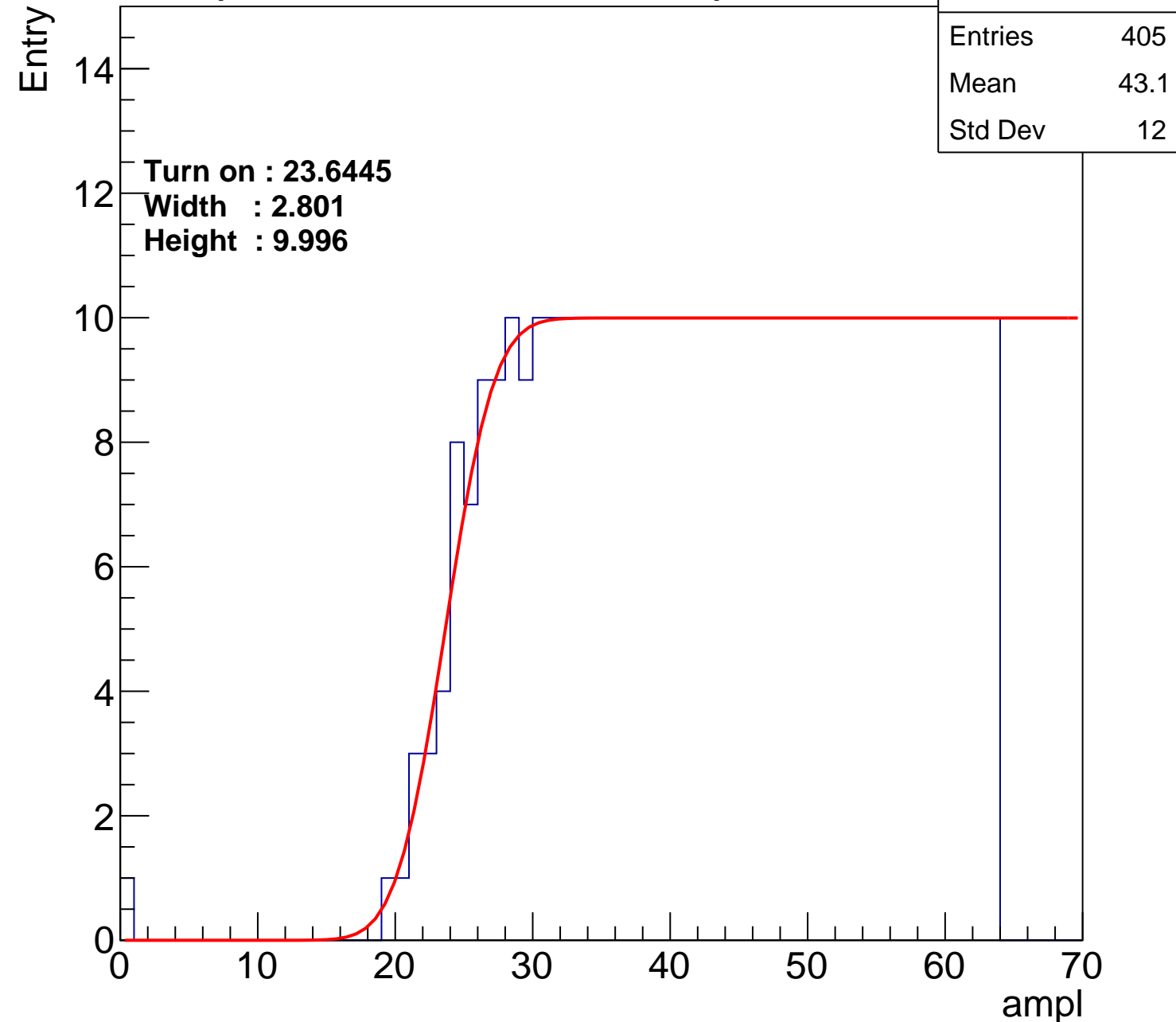
Width : 2.801

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch85

calib\_packv5\_042523\_0143.root, FC#10, port B3

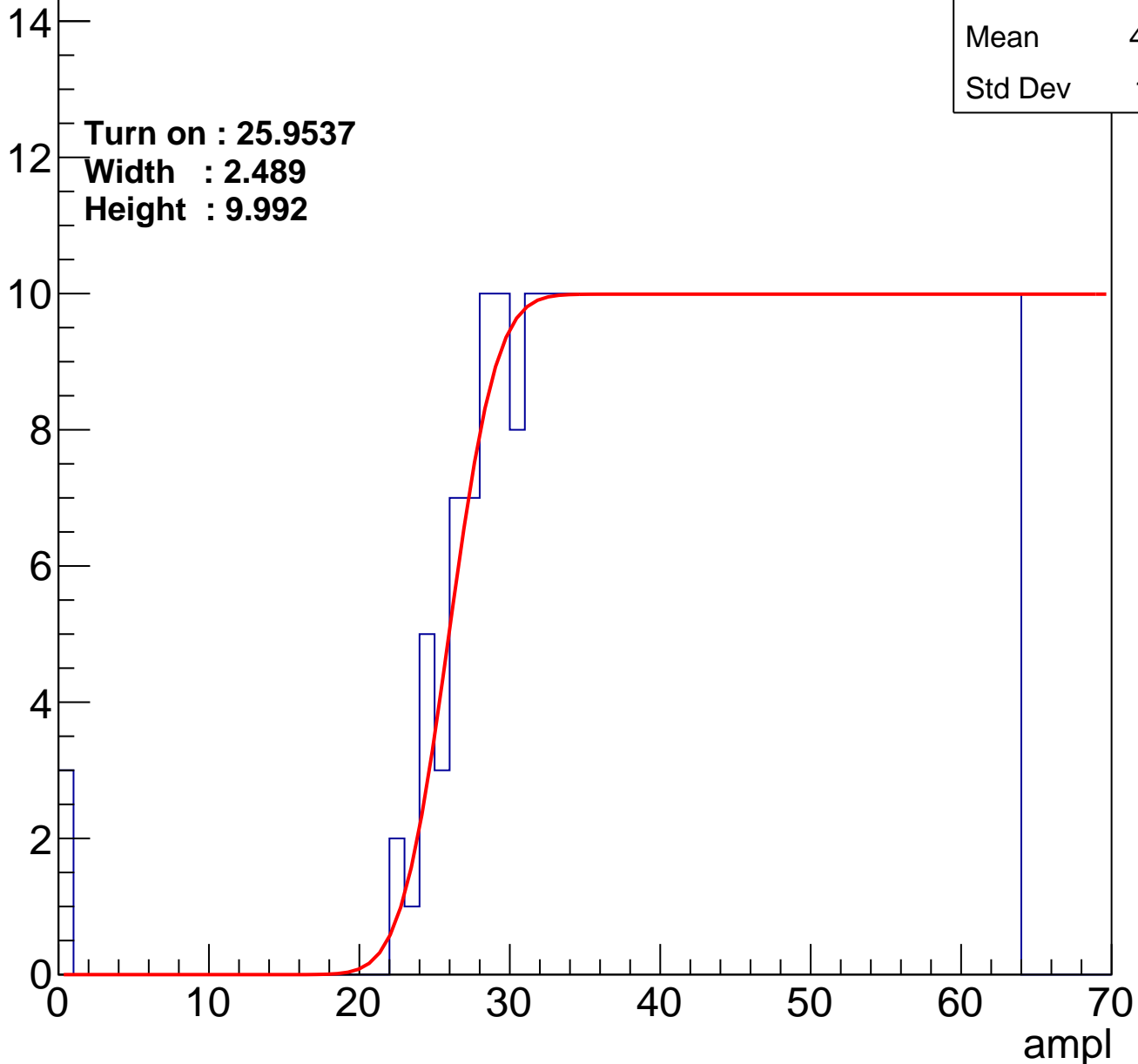
Entries	386
Mean	43.92
Std Dev	11.81

Turn on : 25.9537

Width : 2.489

Height : 9.992

Entry



# B1L002S, U21-ch86

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.42
Std Dev	11.23

Turn on : 26.5781

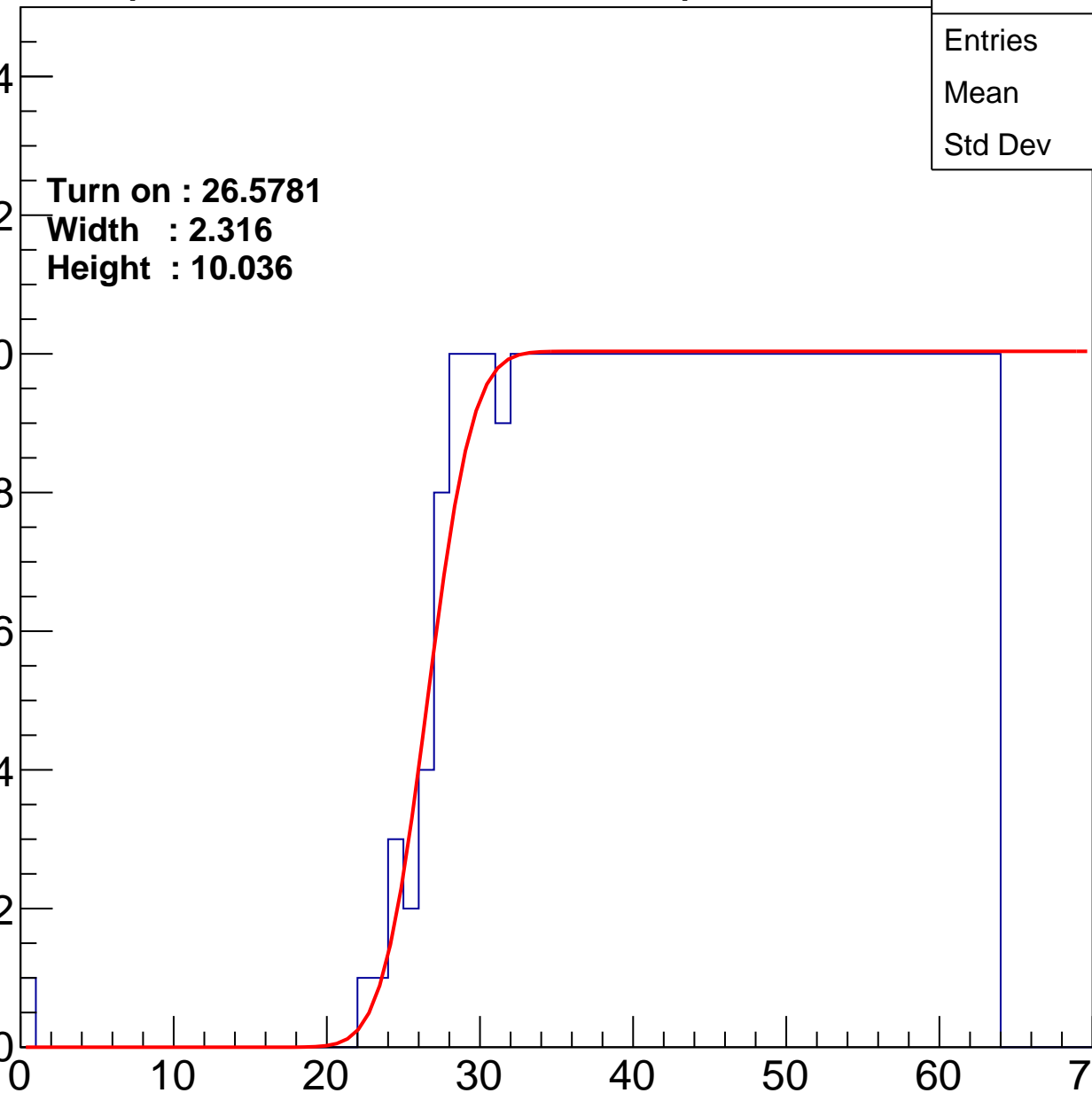
Width : 2.316

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





**calib\_packv5\_042523\_0143.root, FC#10, port B3**

**calib\_packv5\_042523\_0143.root, FC#10, port B3**

Turn on : 26.2539  
Width : 3.068  
Height : 10.078



# B1L002S, U21-ch88

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 26.8866

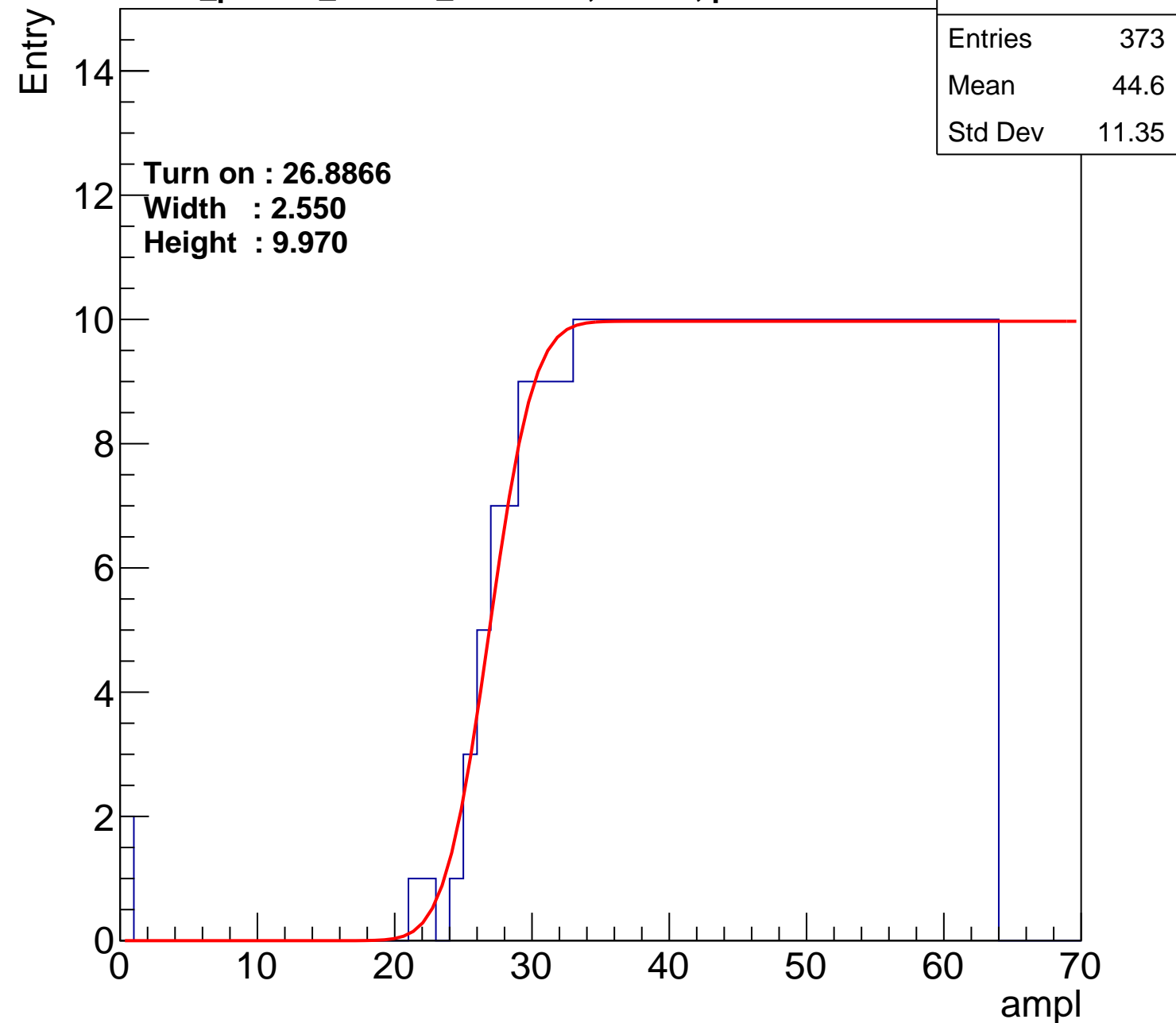
Width : 2.550

Height : 9.970

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch89

calib\_packv5\_042523\_0143.root, FC#10, port B3

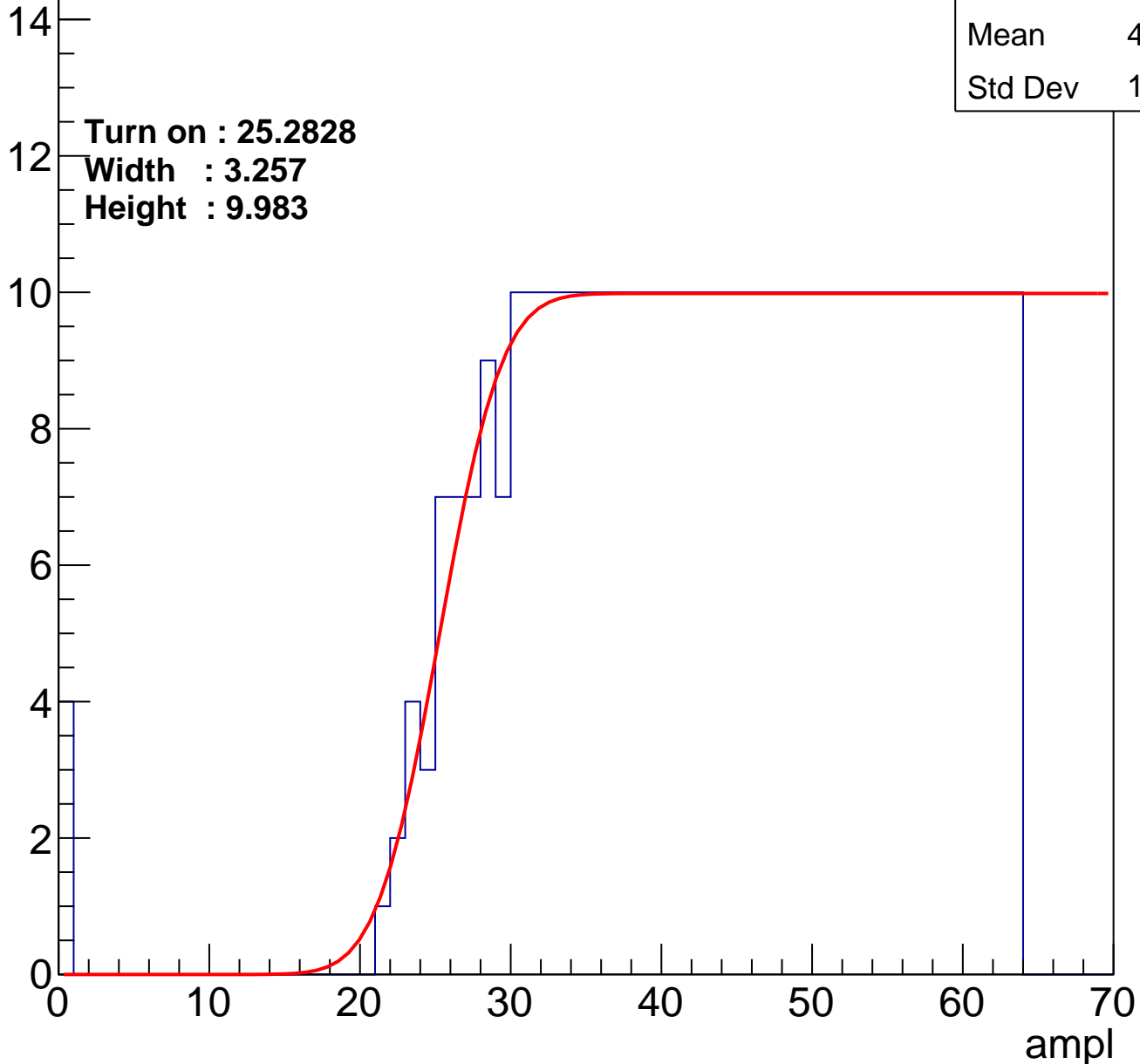
Entries	391
Mean	43.58
Std Dev	12.15

Turn on : 25.2828

Width : 3.257

Height : 9.983

Entry



# B1L002S, U21-ch90

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.59

Turn on : 25.5317

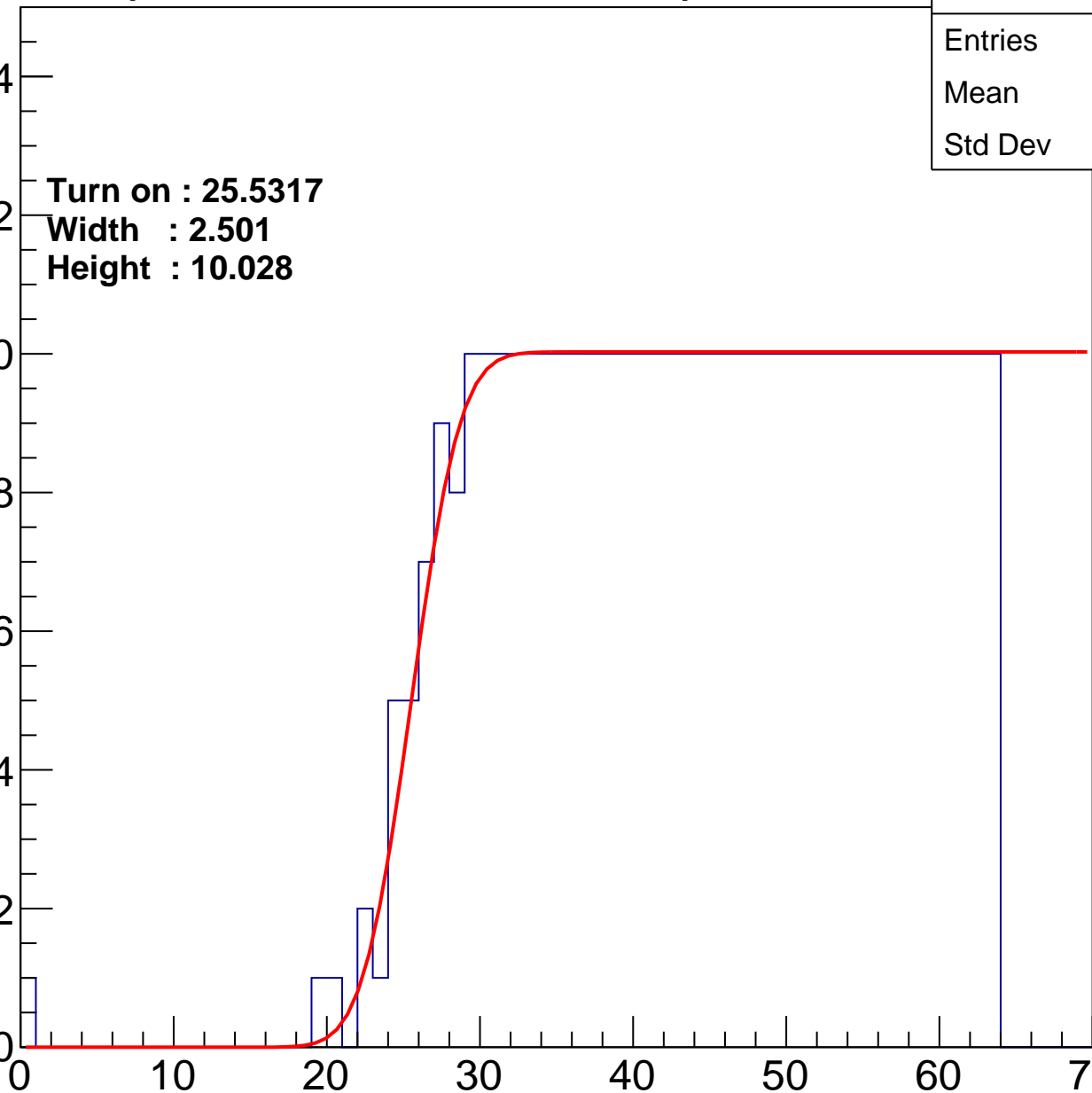
Width : 2.501

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch91

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.6
Std Dev	11.49

Turn on : 27.4434

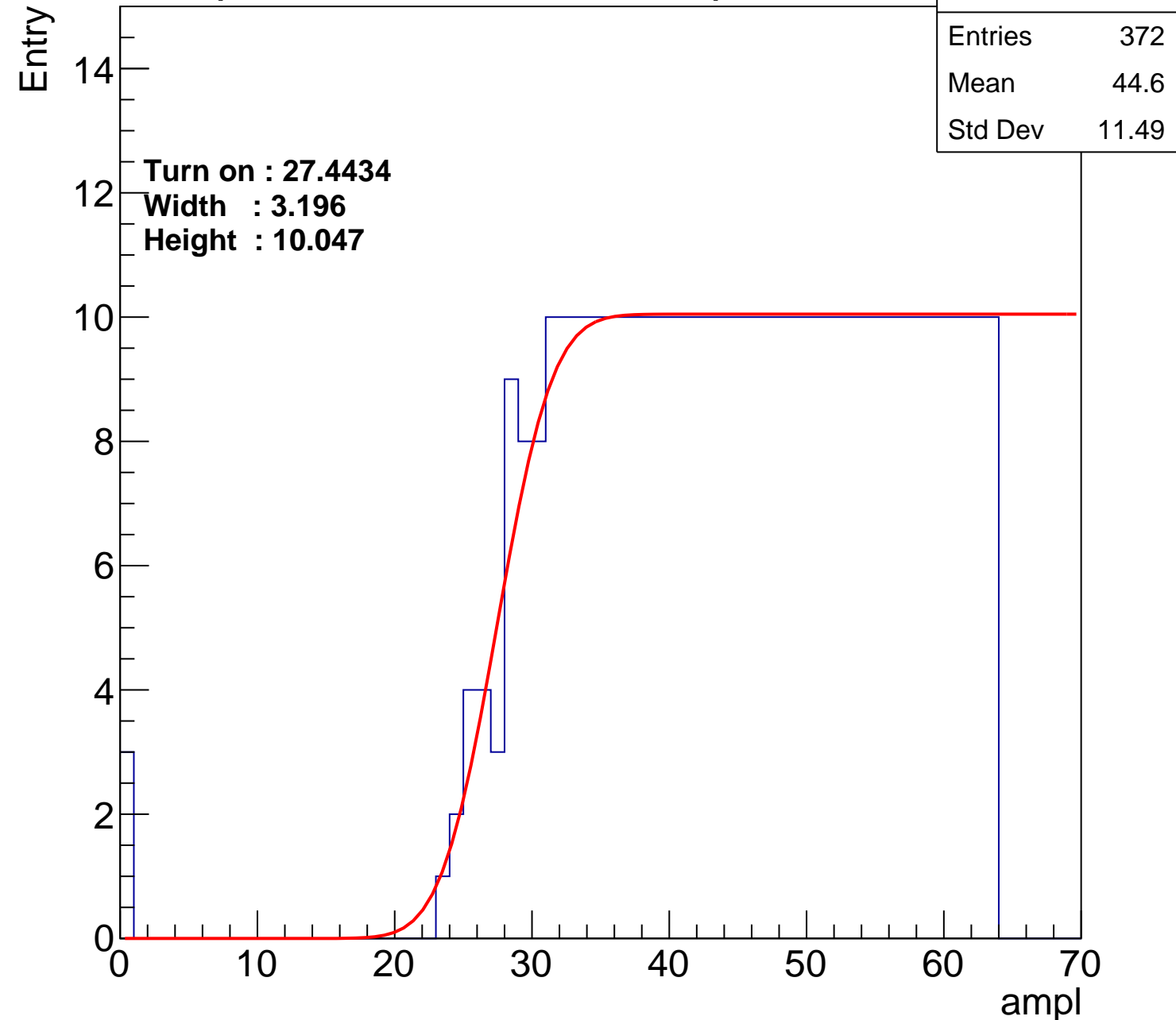
Width : 3.196

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch92

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.85
Std Dev	11.75

Turn on : 25.3224

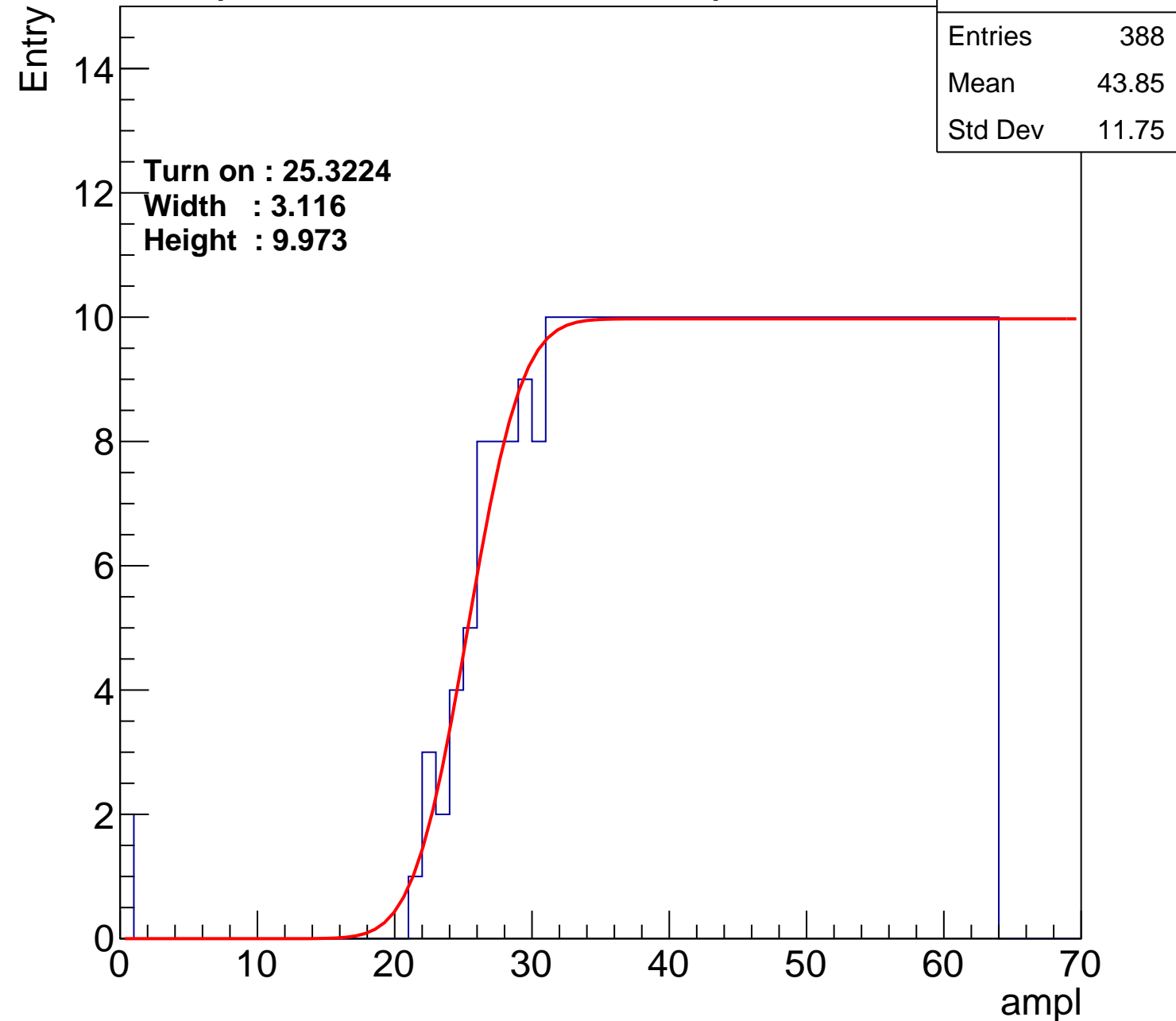
Width : 3.116

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch93

calib\_packv5\_042523\_0143.root, FC#10, port B3

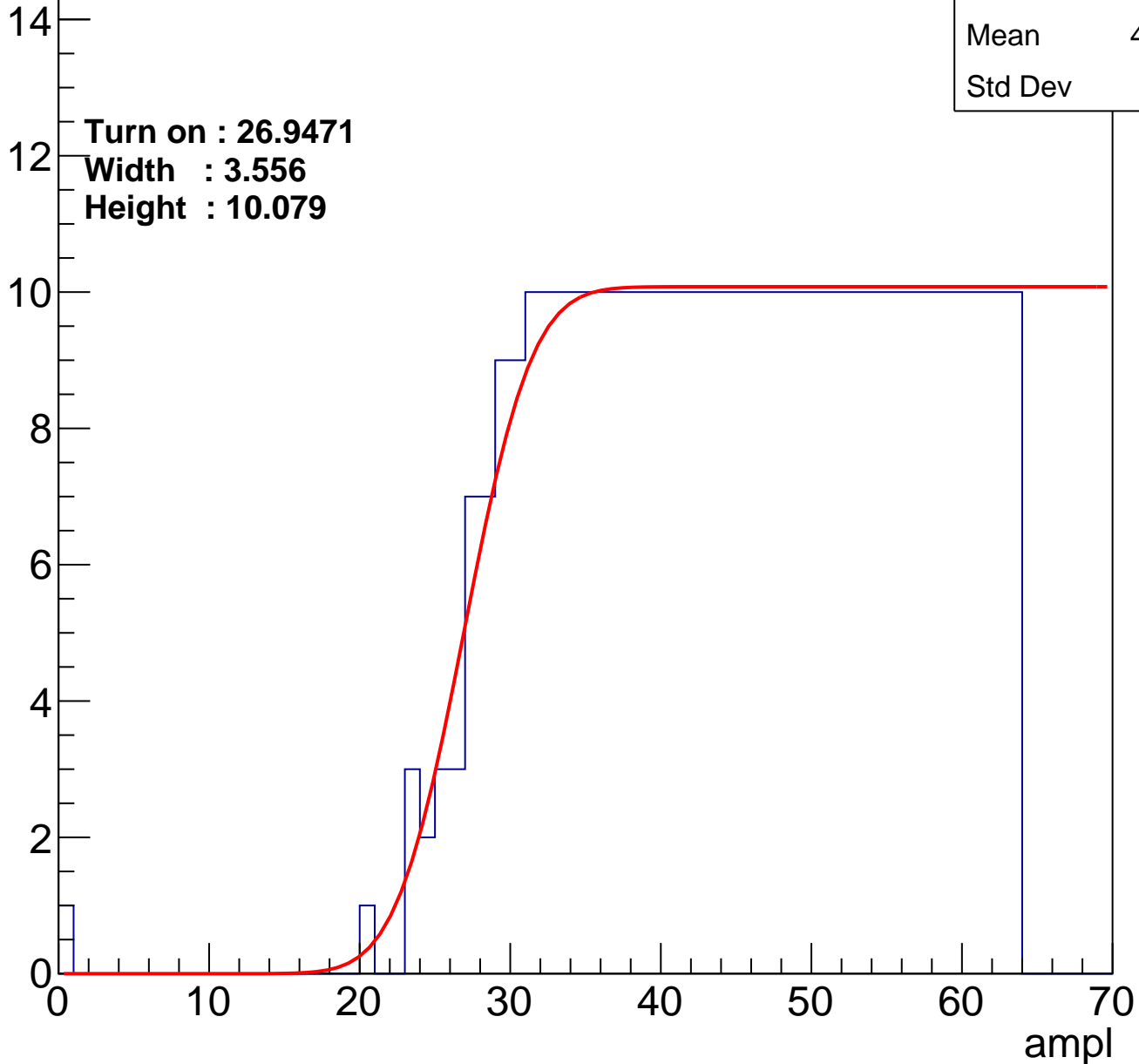
Entries	375
Mean	44.58
Std Dev	11.2

Turn on : 26.9471

Width : 3.556

Height : 10.079

Entry



# B1L002S, U21-ch94

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	413
Mean	42.74
Std Dev	12.32

Turn on : 23.0163

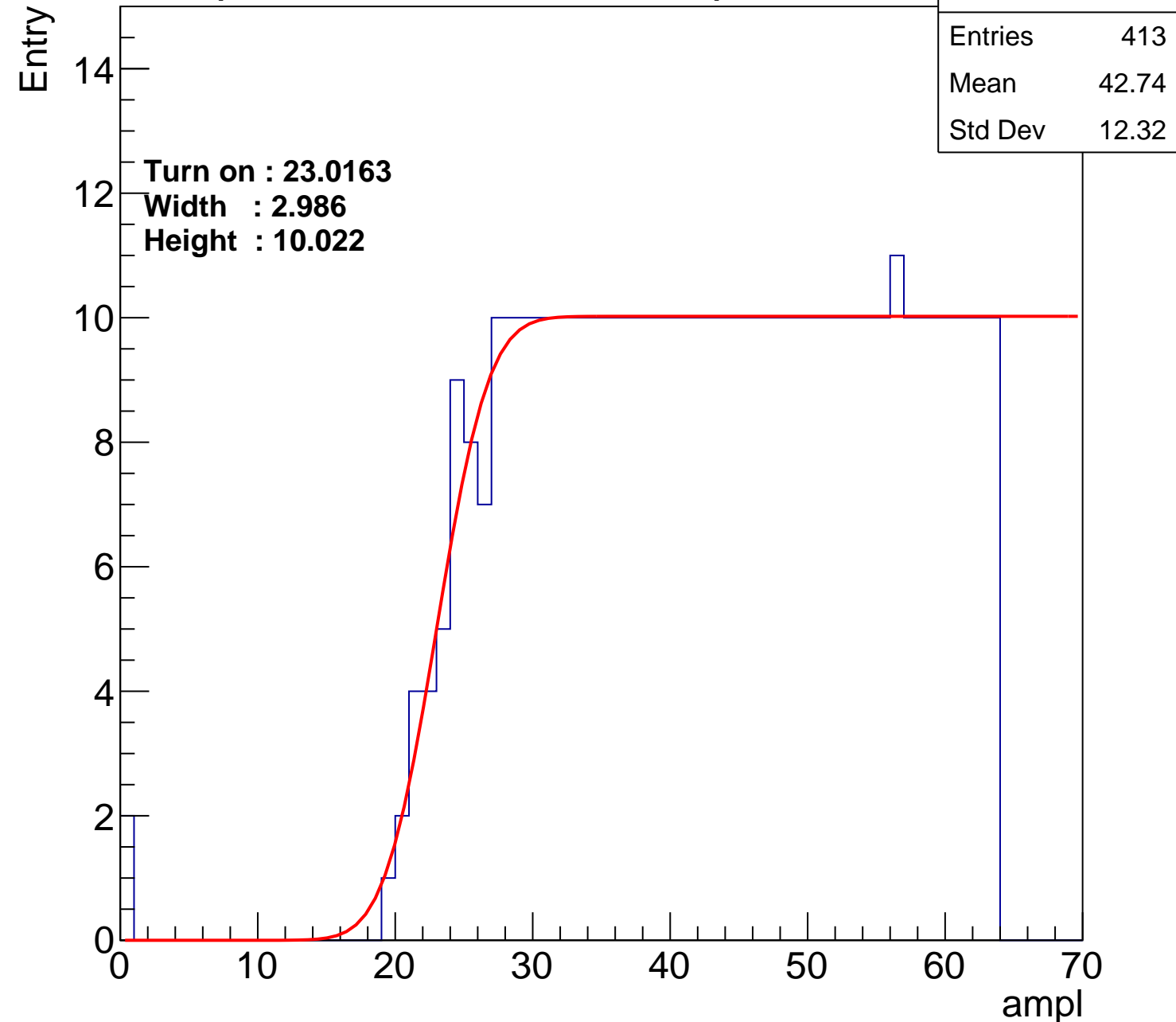
Width : 2.986

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch95

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.47
Std Dev	11.23

Turn on : 26.7197

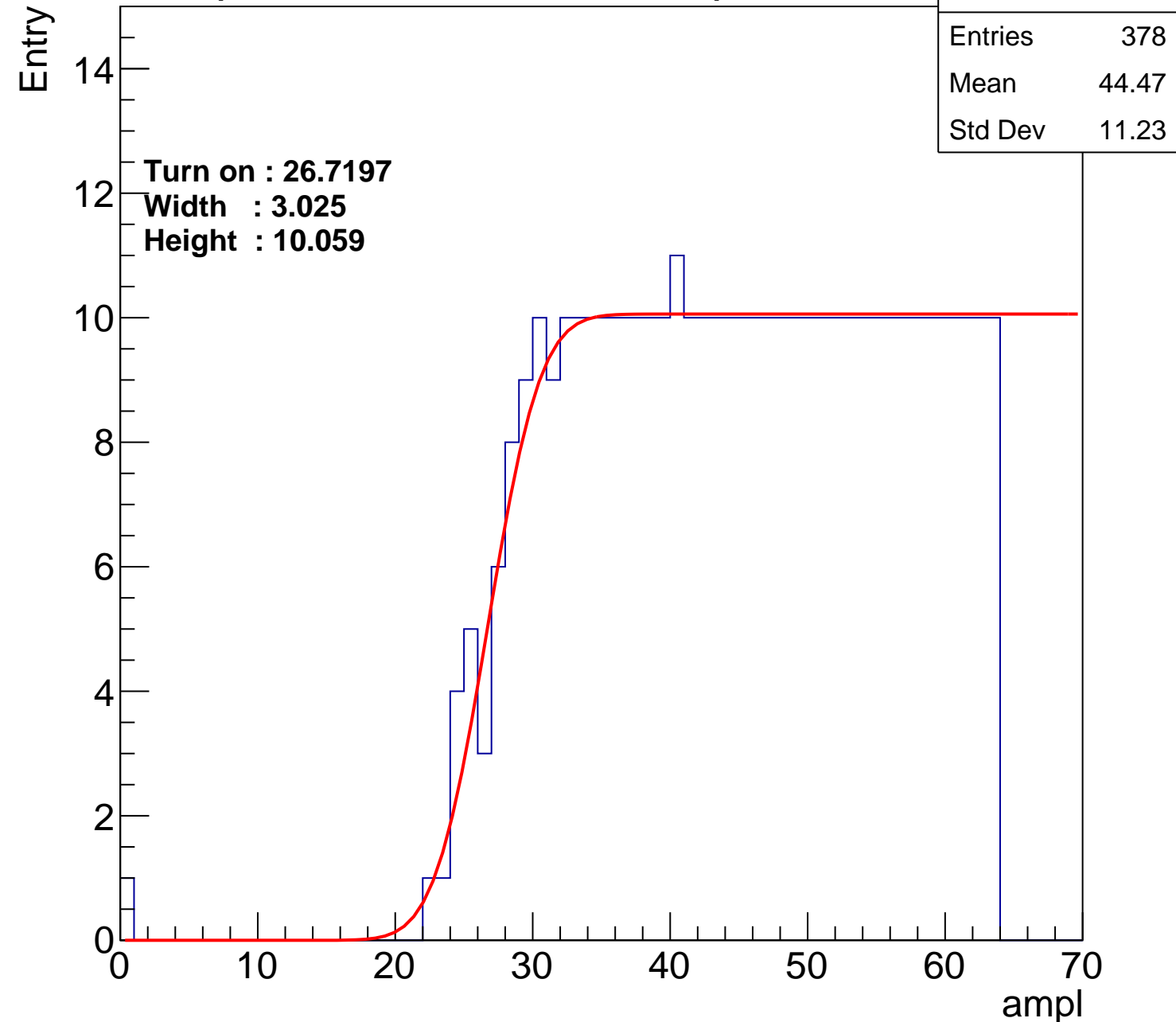
Width : 3.025

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch96

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.84
Std Dev	12

Turn on : 26.0979

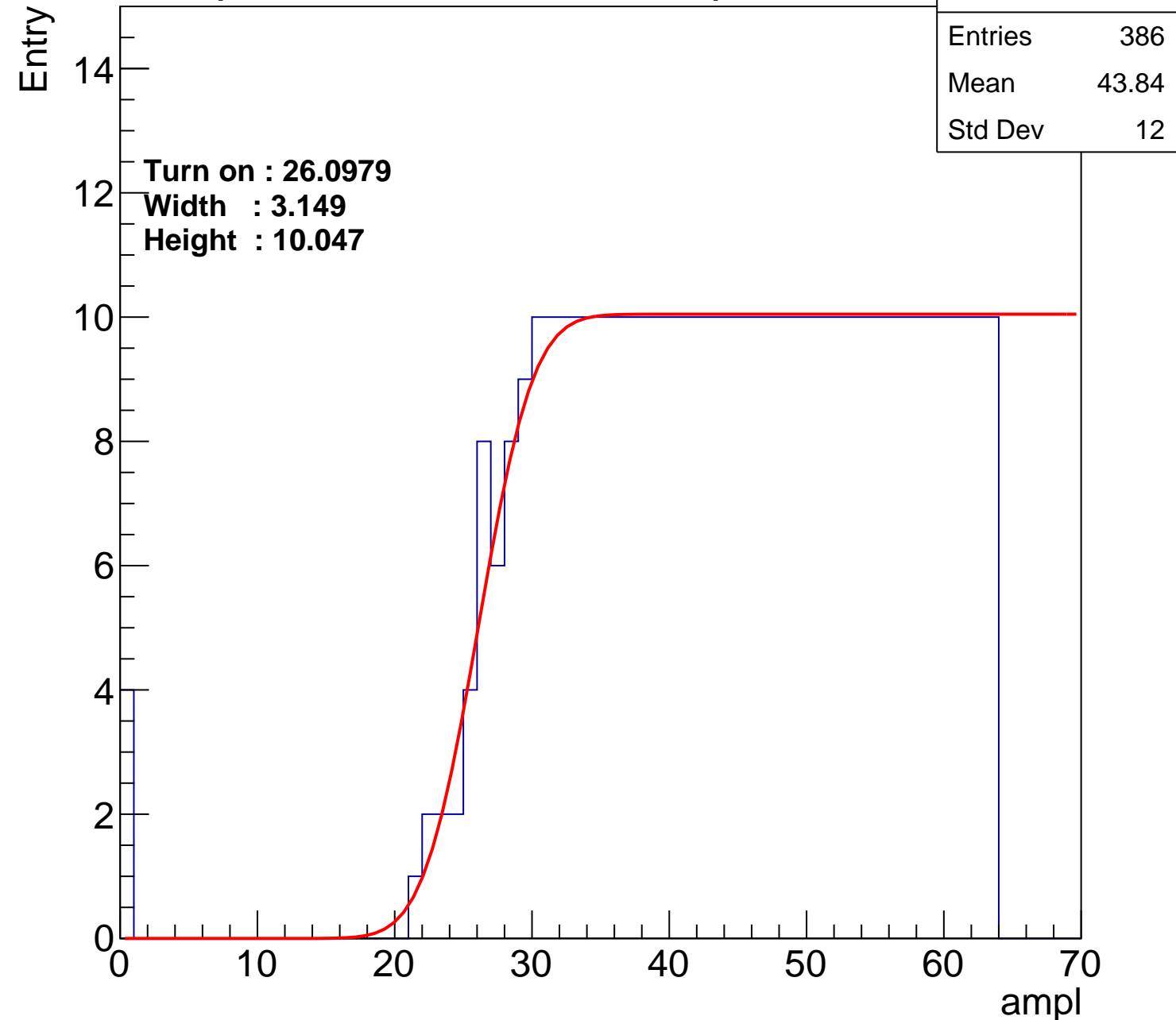
Width : 3.149

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch97

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.59
Std Dev	12.25

Turn on : 25.5829

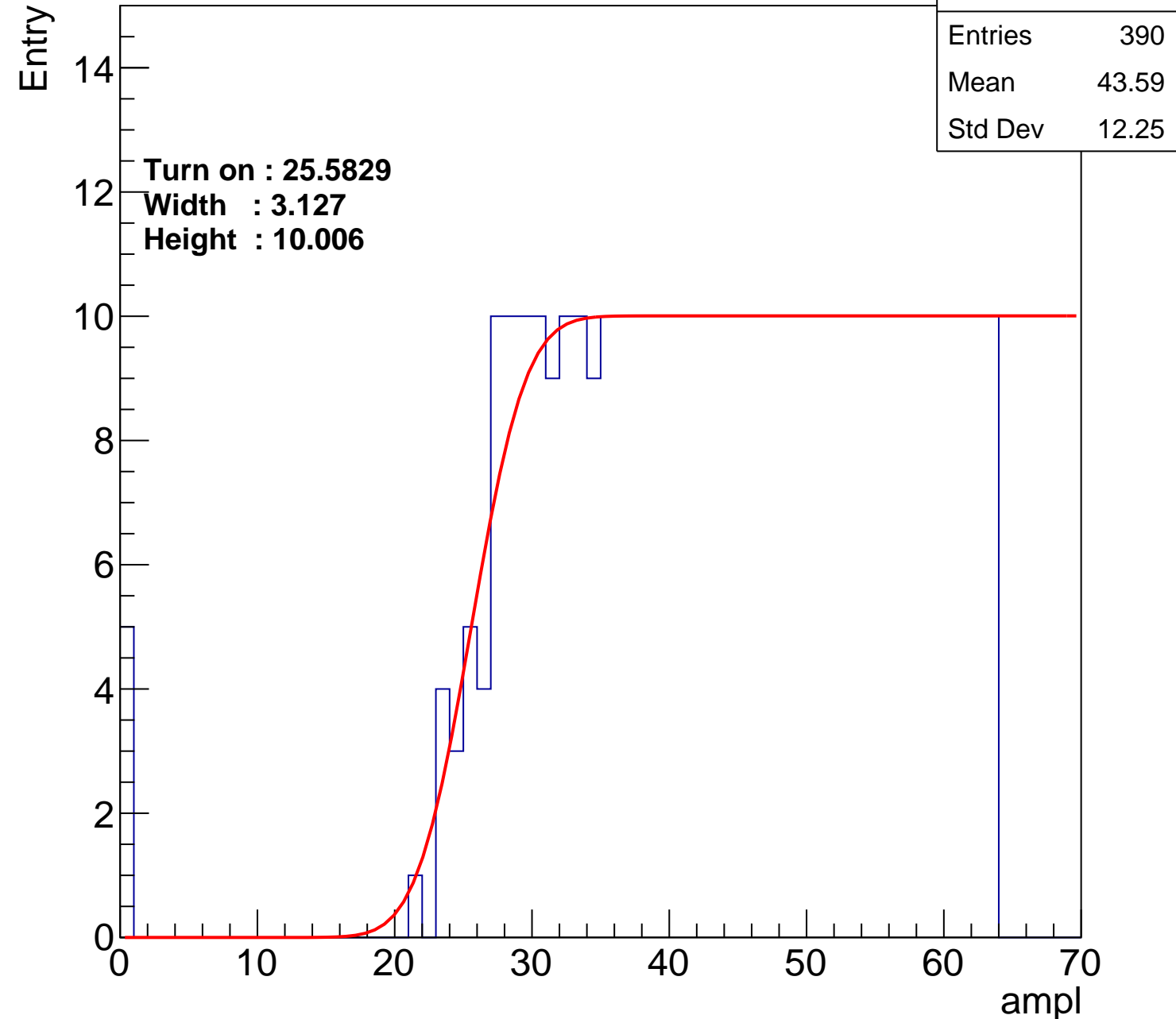
Width : 3.127

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch98

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.62
Std Dev	11.85

Turn on : 24.6816

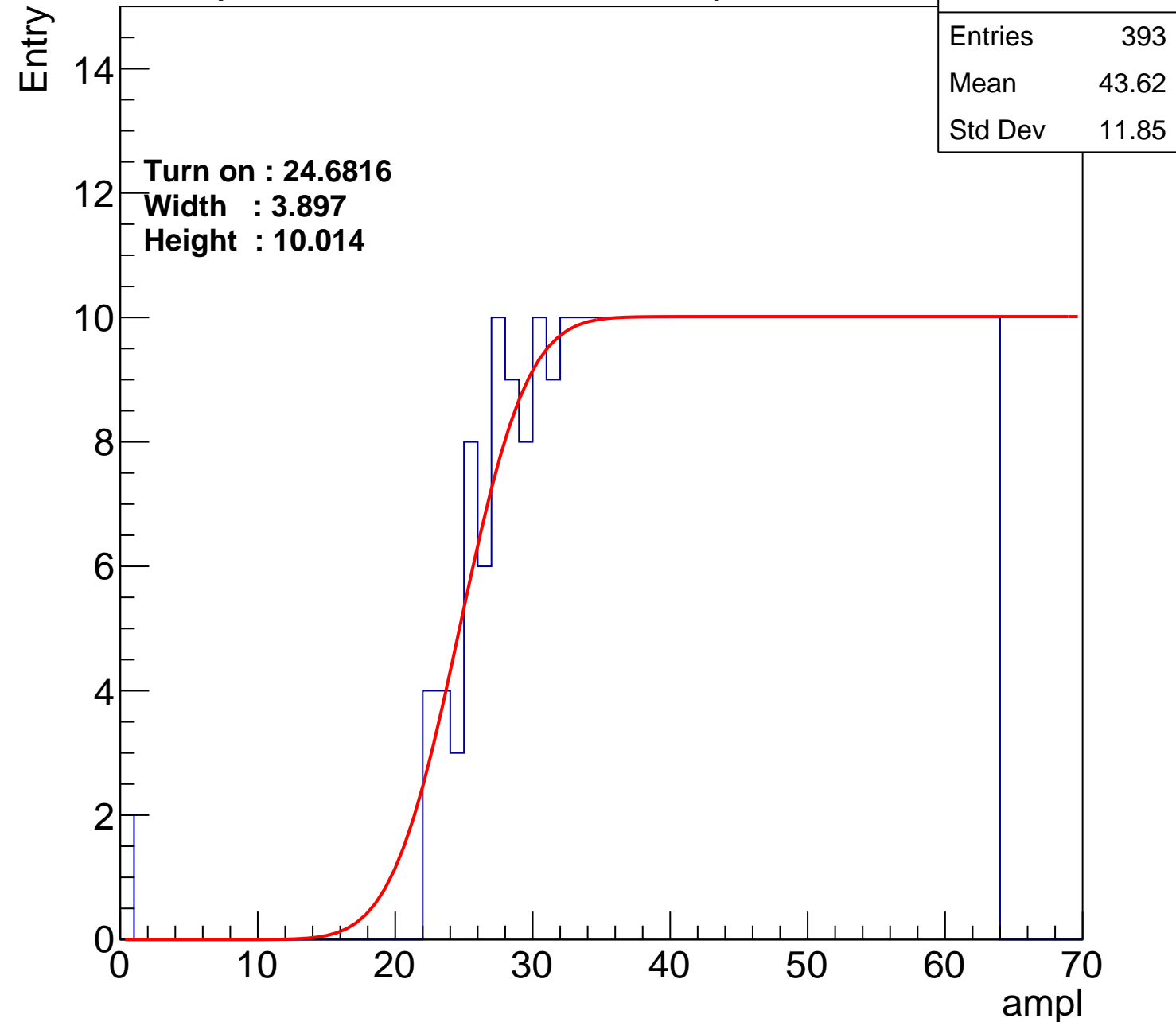
Width : 3.897

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch99

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.78
Std Dev	11.68

Turn on : 24.7664

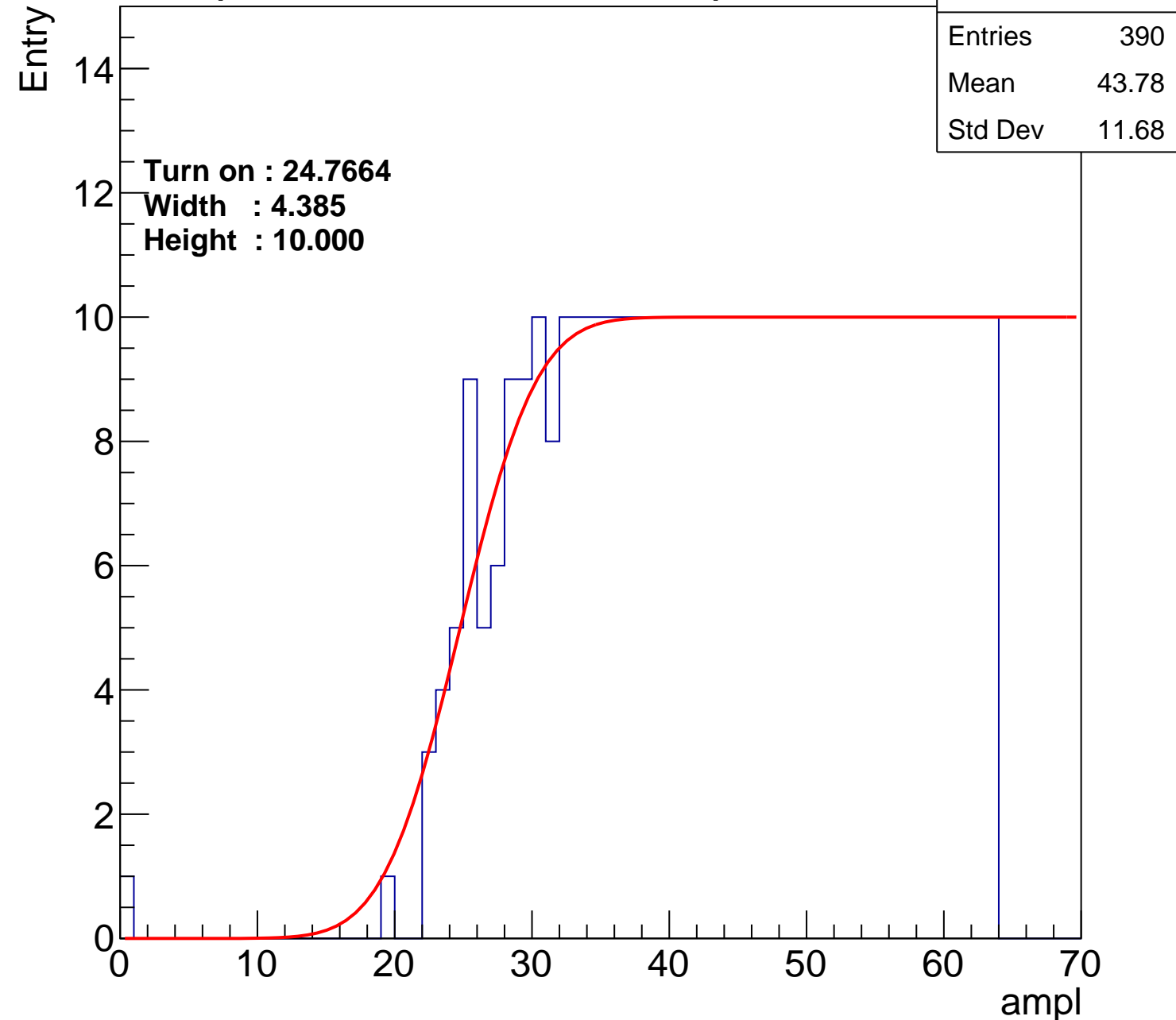
Width : 4.385

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch100

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	397
Mean	43.38
Std Dev	12.1

Turn on : 25.2816

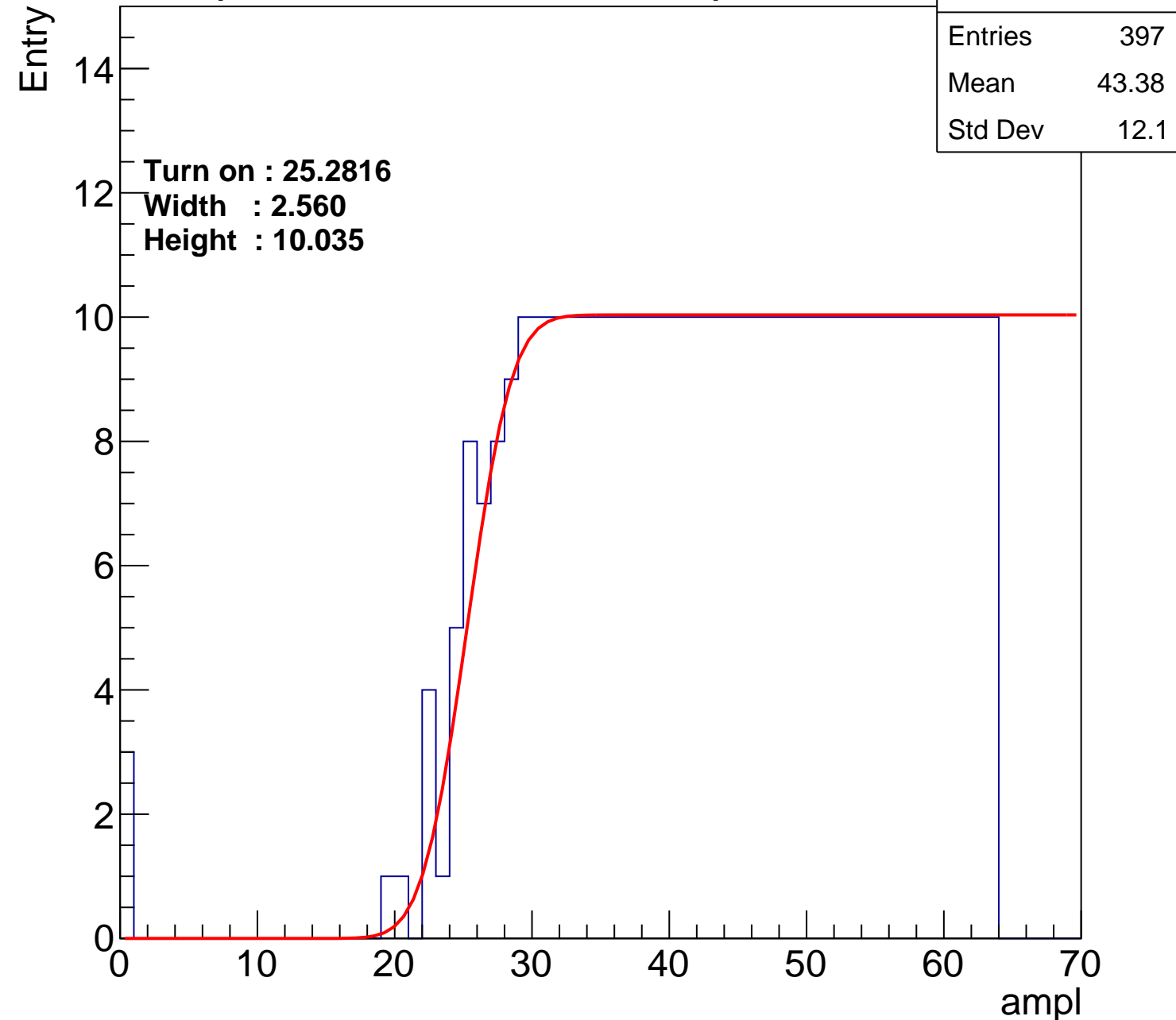
Width : 2.560

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch101

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.6
Std Dev	12.01

Turn on : 24.9754

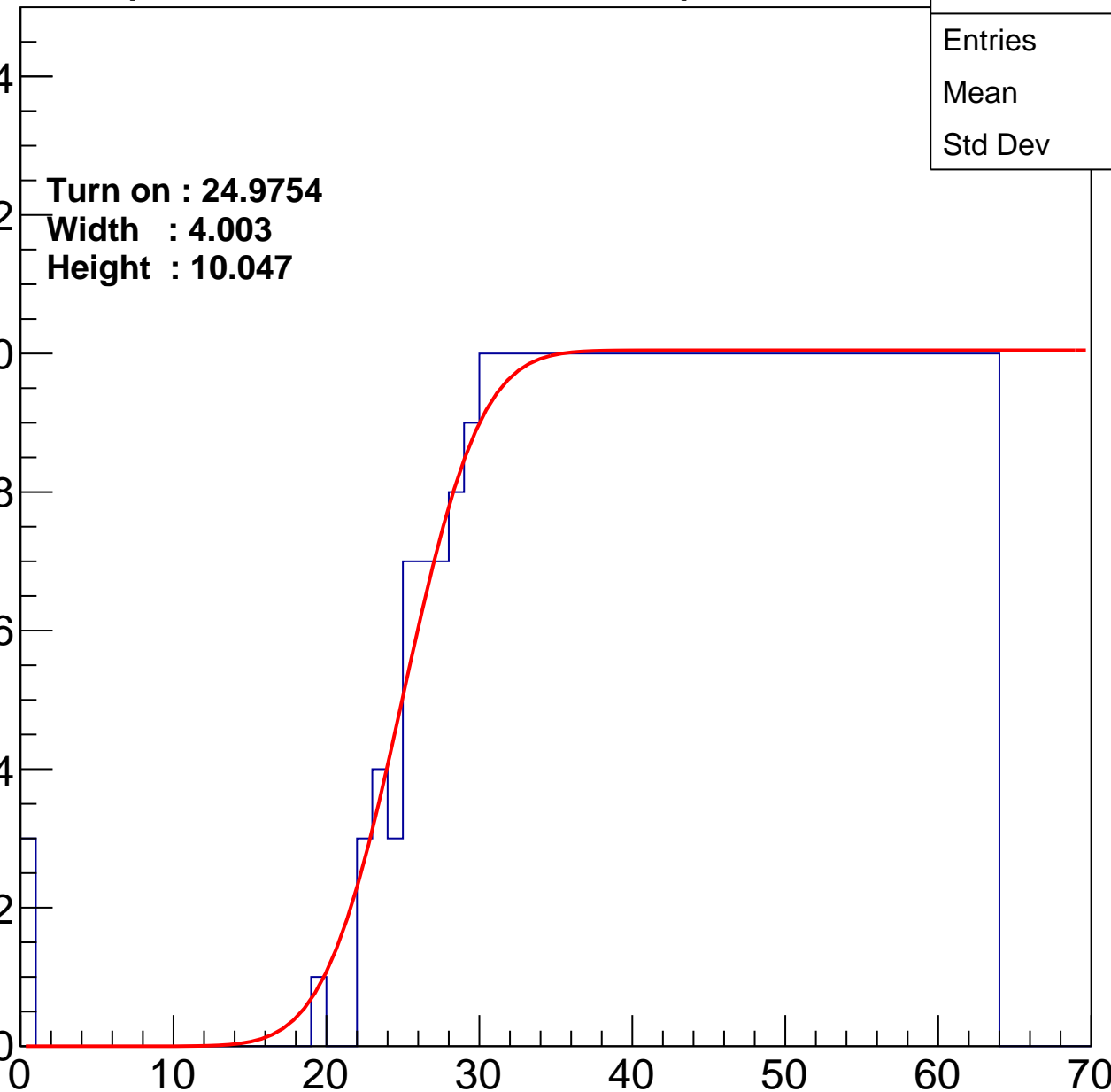
Width : 4.003

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch102

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	401
Mean	43.25
Std Dev	12.02

Turn on : 24.0887

Width : 2.899

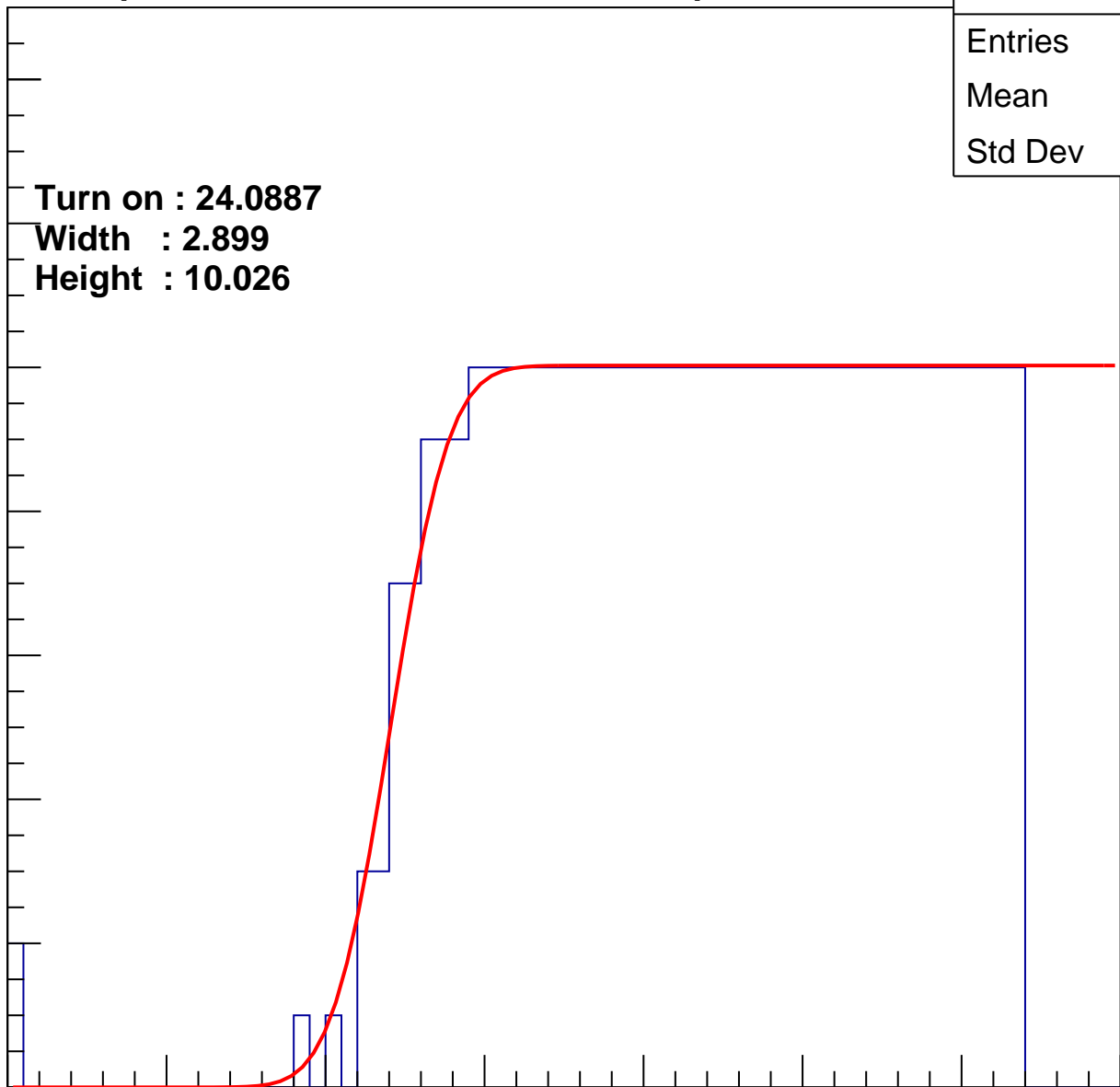
Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B1L002S, U21-ch103

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.01
Std Dev	11.95

Turn on : 26.1726

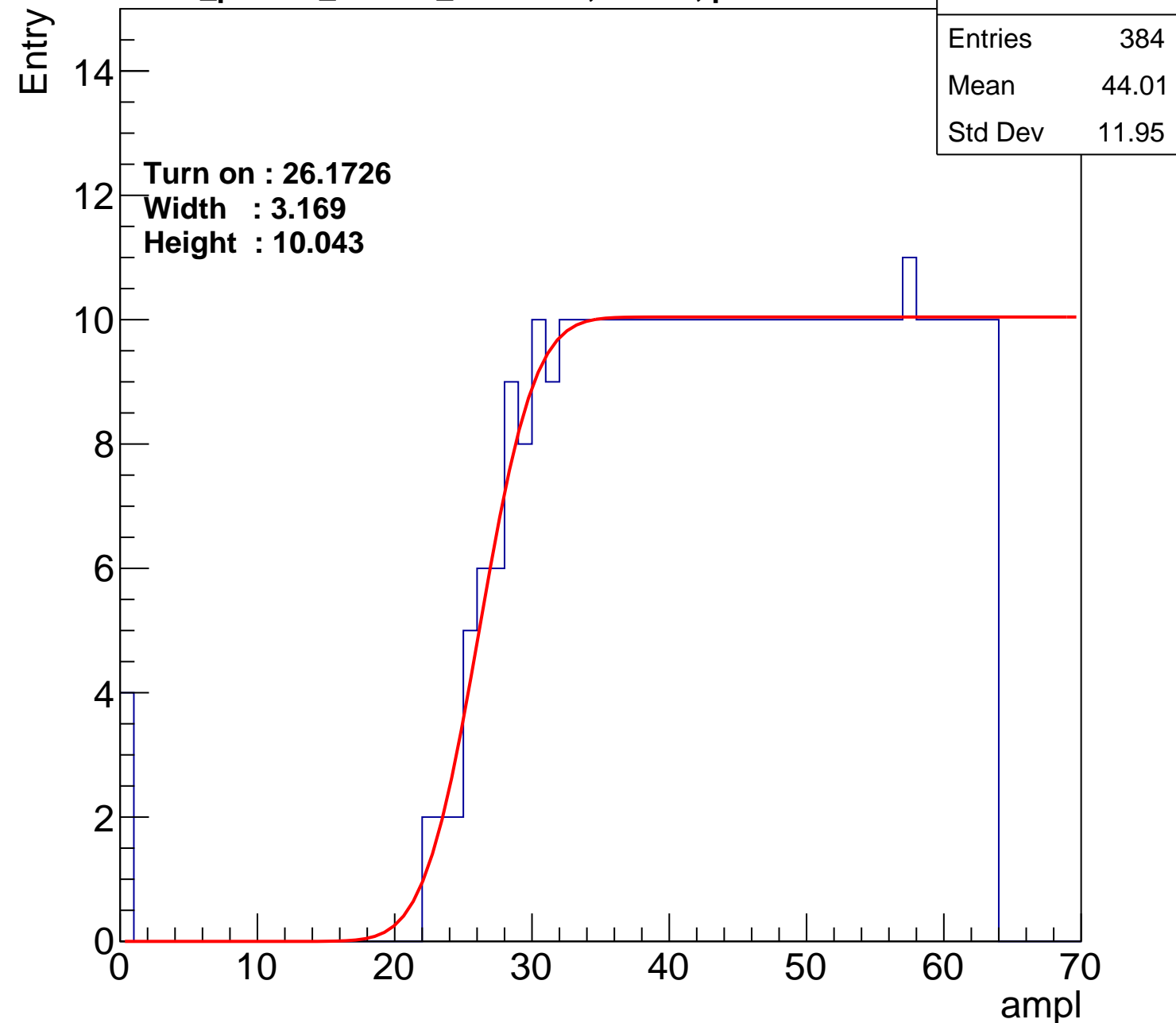
Width : 3.169

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch104

calib\_packv5\_042523\_0143.root, FC#10, port B3

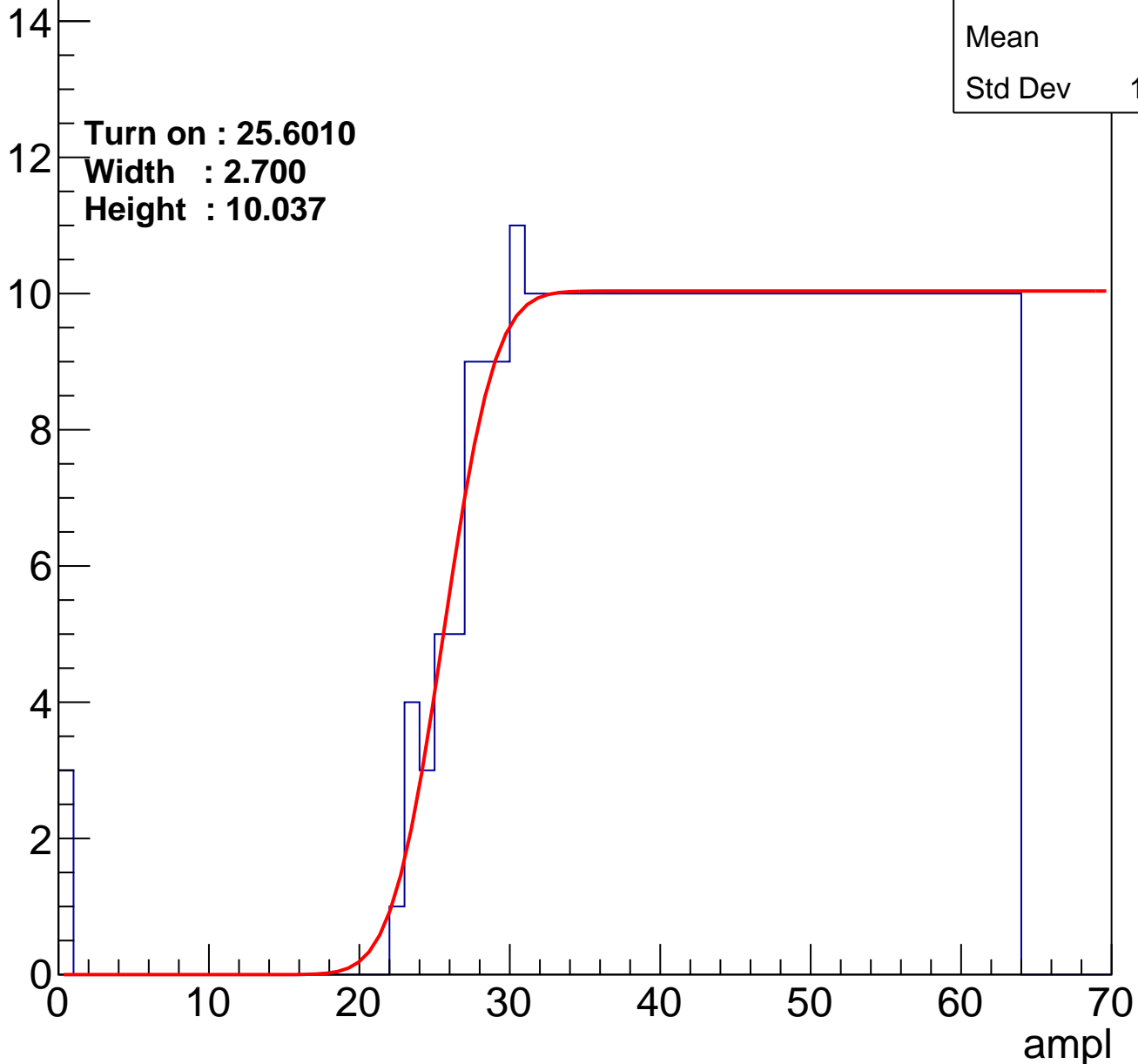
Entries	389
Mean	43.8
Std Dev	11.85

Turn on : 25.6010

Width : 2.700

Height : 10.037

Entry



# B1L002S, U21-ch105

calib\_packv5\_042523\_0143.root, FC#10, port B3

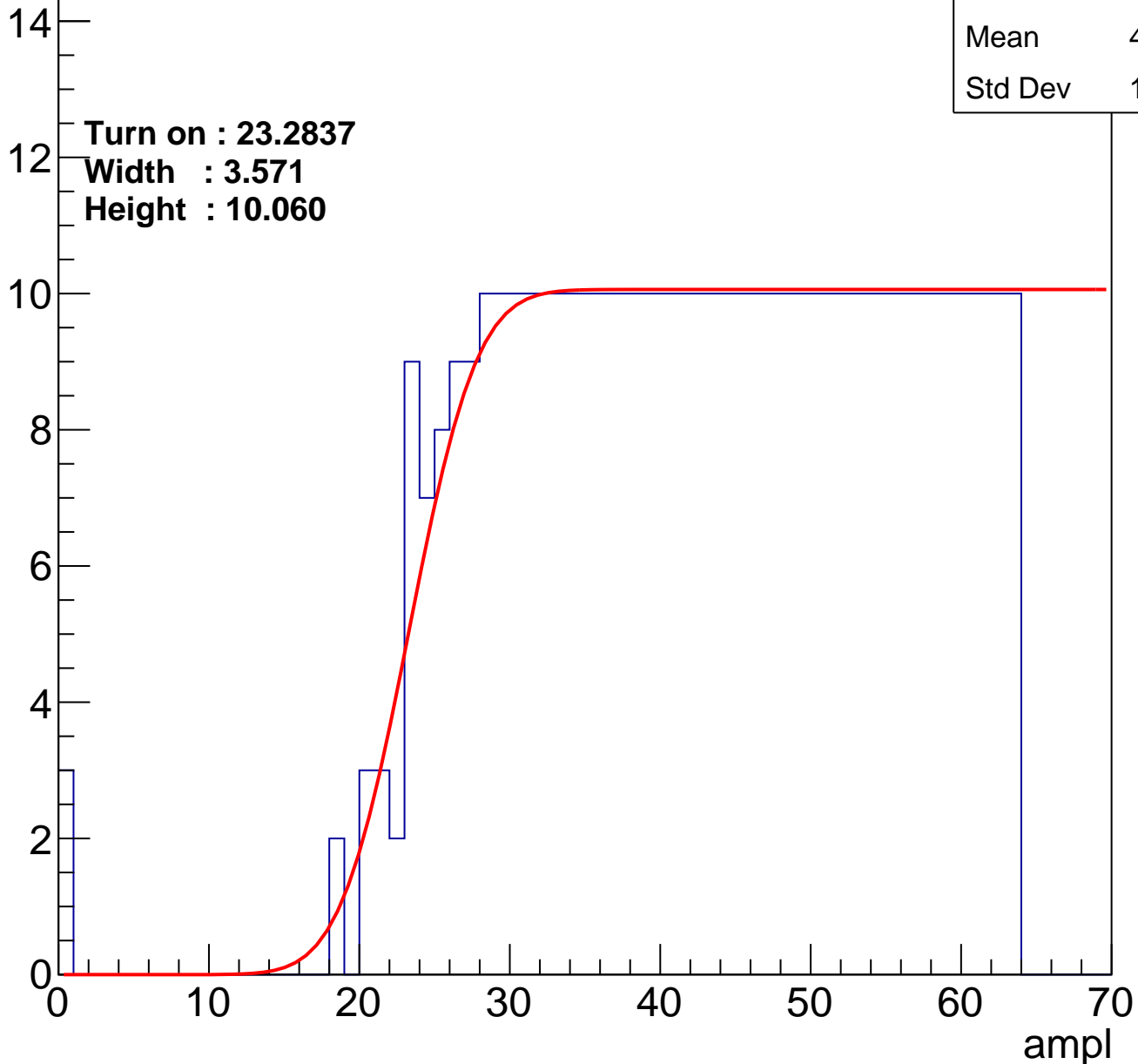
Entries	415
Mean	42.49
Std Dev	12.55

Turn on : 23.2837

Width : 3.571

Height : 10.060

Entry



# B1L002S, U21-ch106

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	405
Mean	43.01
Std Dev	12.26

Turn on : 23.6354

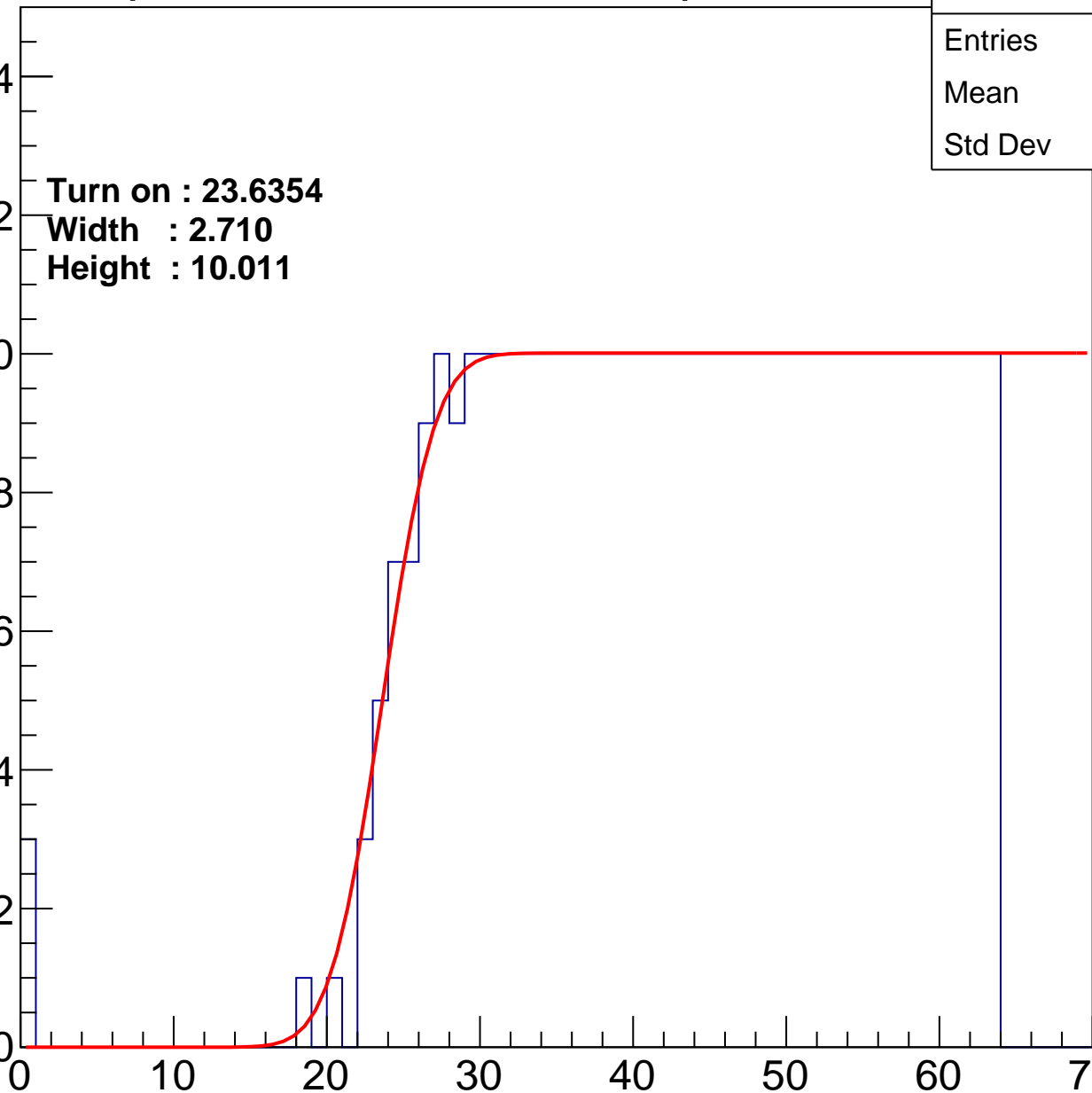
Width : 2.710

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch107

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.4051

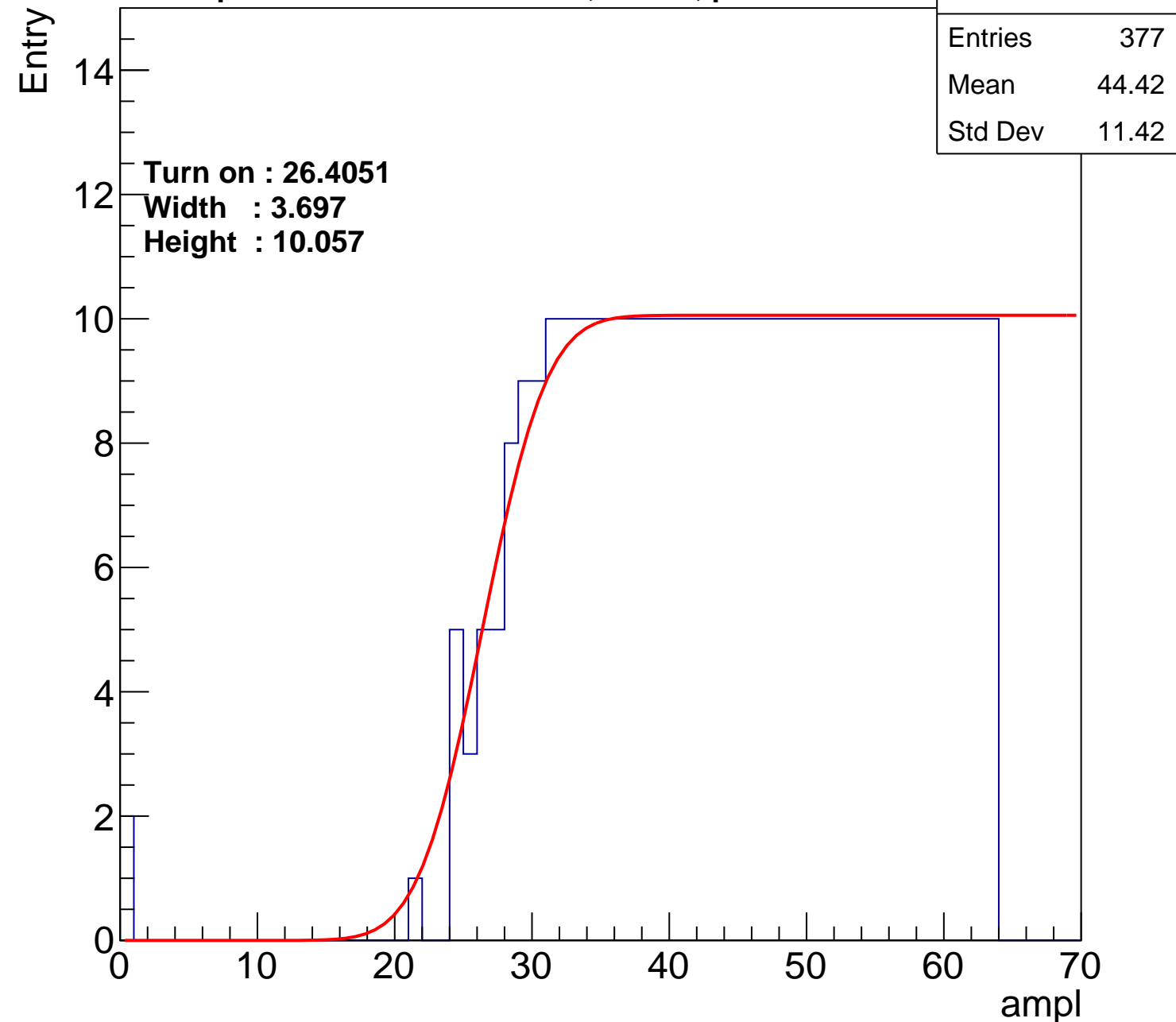
Width : 3.697

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch108

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.93
Std Dev	11.8

Turn on : 25.2566

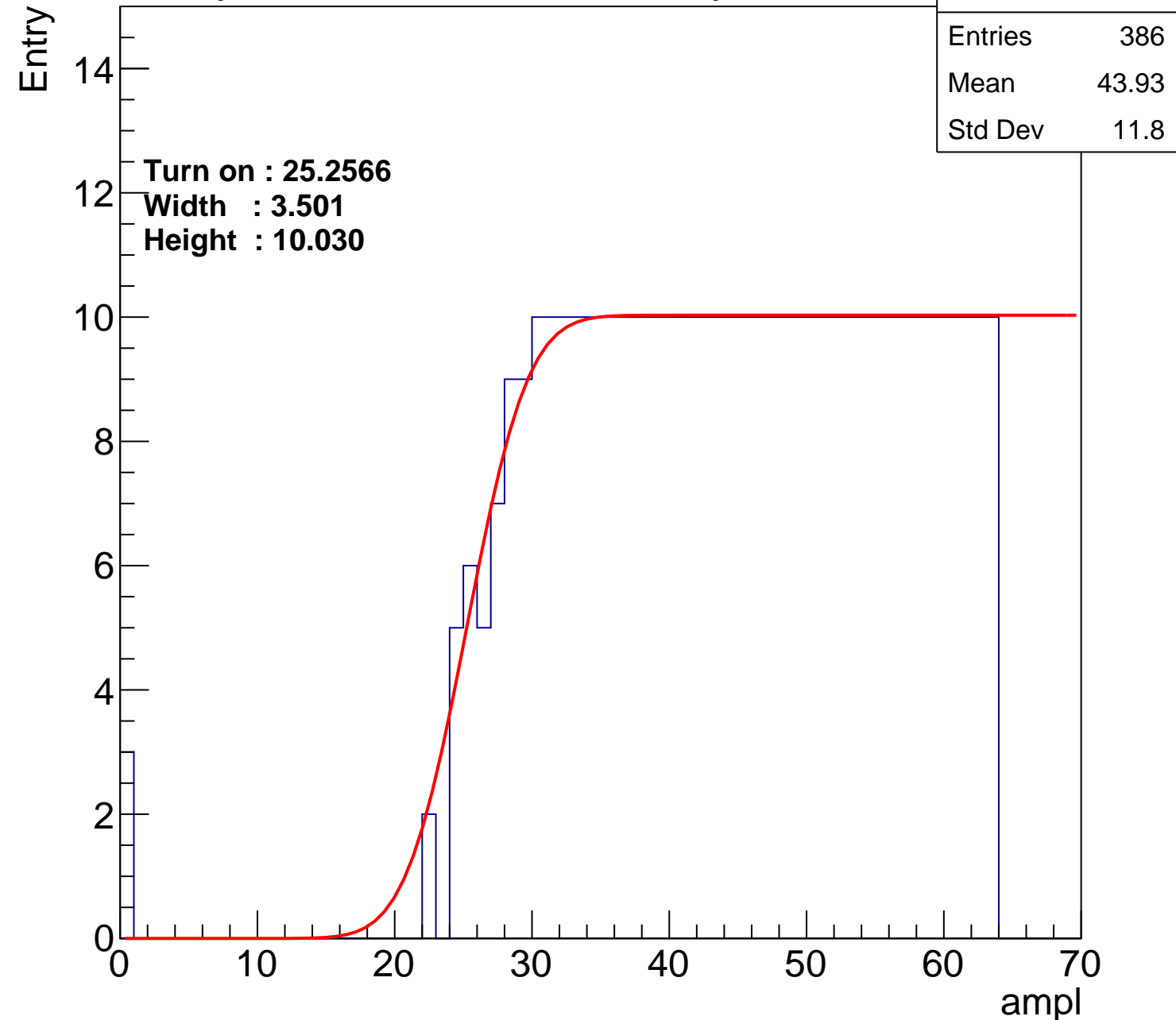
Width : 3.501

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch109

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	411
Mean	42.55
Std Dev	12.77

Turn on : 23.5447

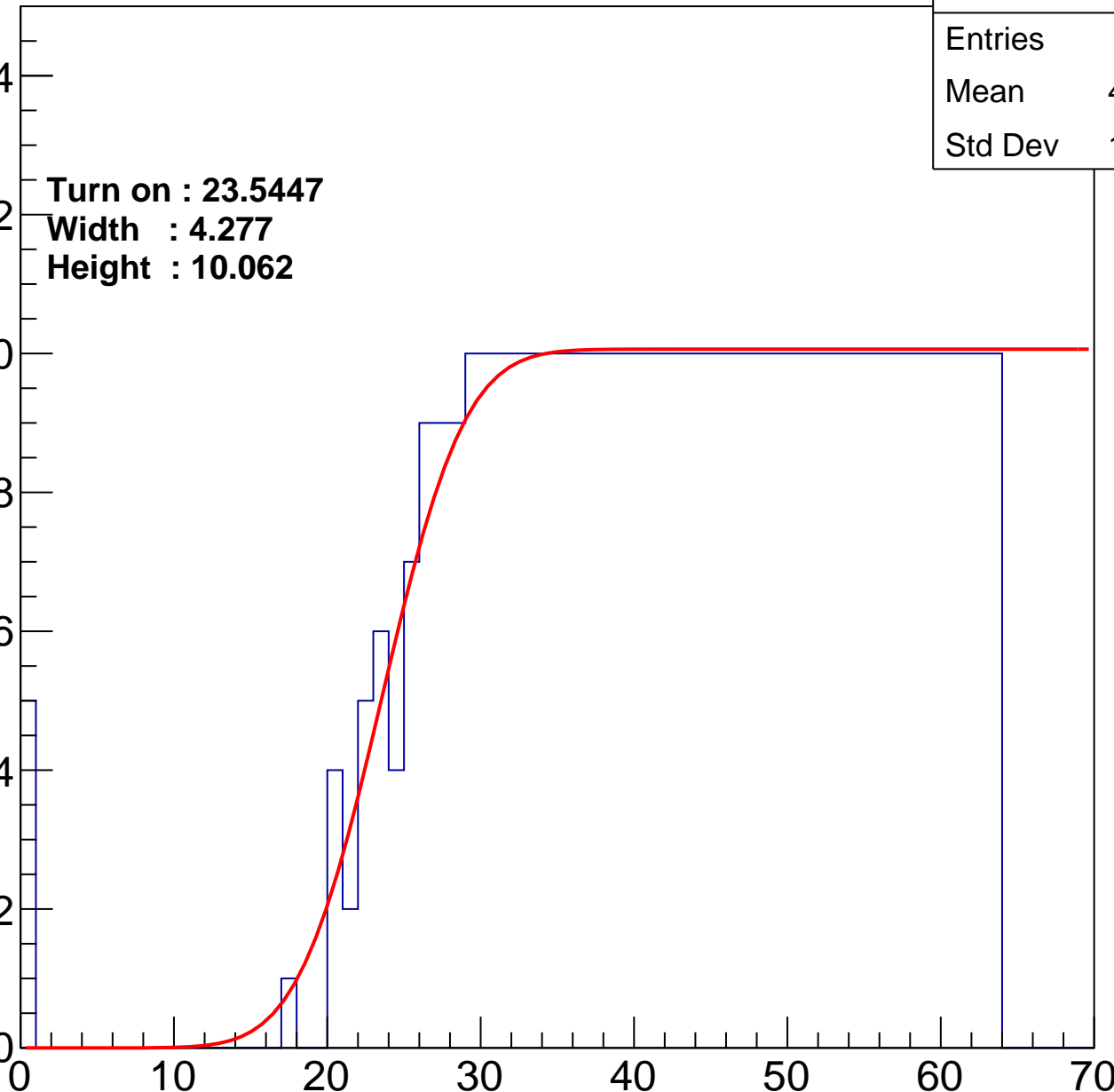
Width : 4.277

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch110

calib\_packv5\_042523\_0143.root, FC#10, port B3

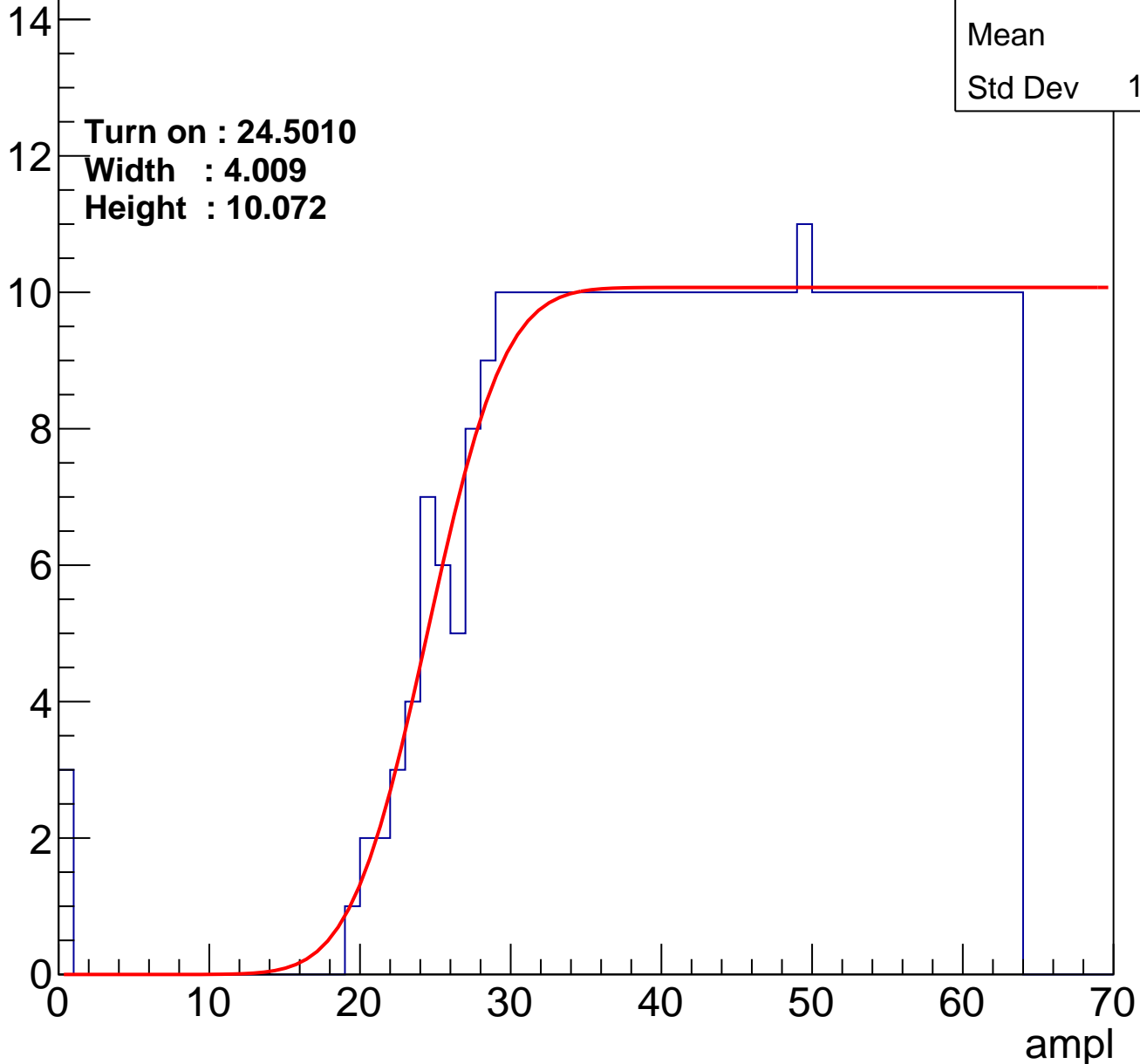
Entries	401
Mean	43.2
Std Dev	12.23

Turn on : 24.5010

Width : 4.009

Height : 10.072

Entry





# B1L002S, U21-ch111

calib\_packv5\_042523\_0143.root, FC#10, port B3

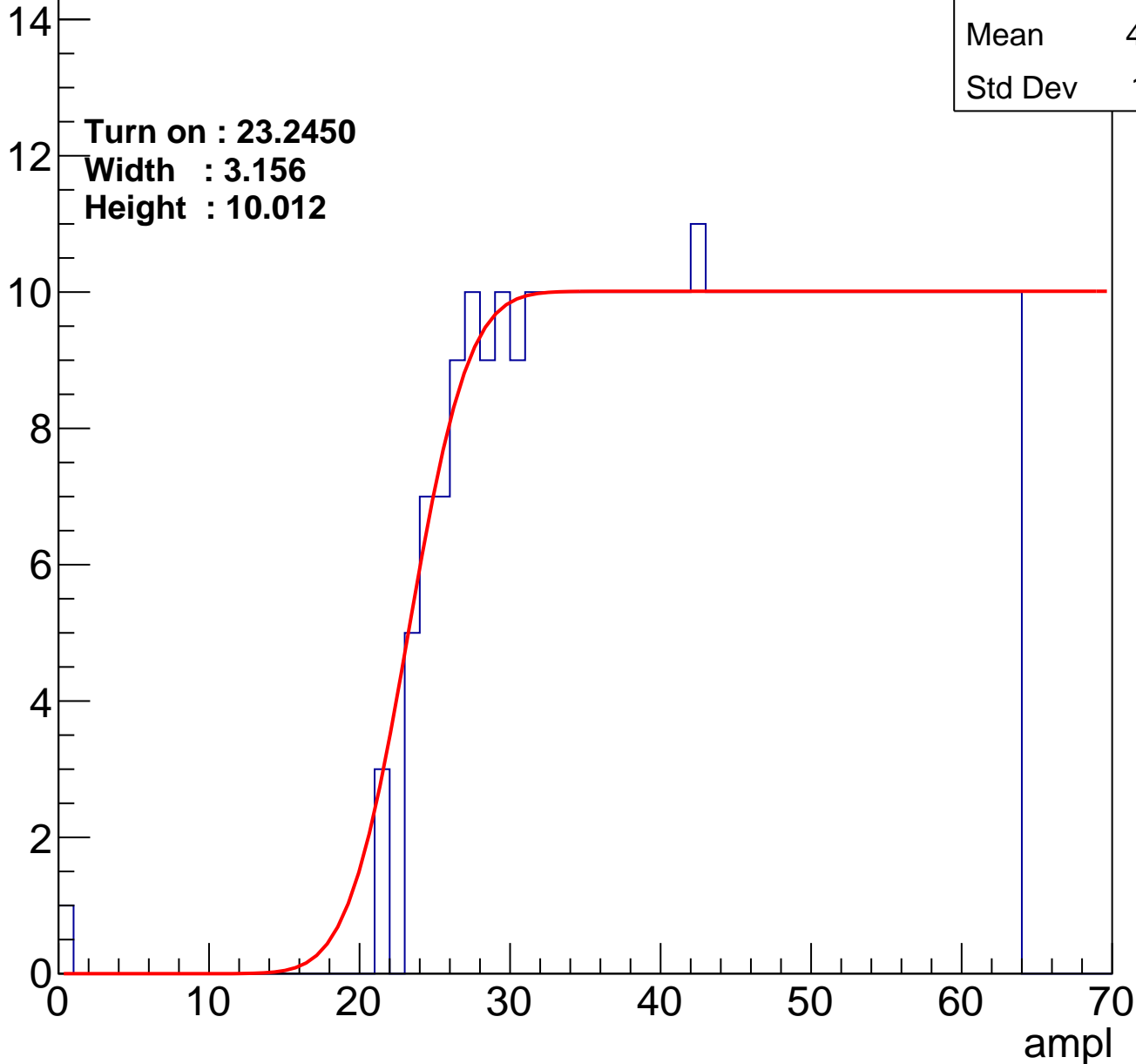
Entries	401
Mean	43.36
Std Dev	11.81

Turn on : 23.2450

Width : 3.156

Height : 10.012

Entry



# B1L002S, U21-ch112

calib\_packv5\_042523\_0143.root, FC#10, port B3

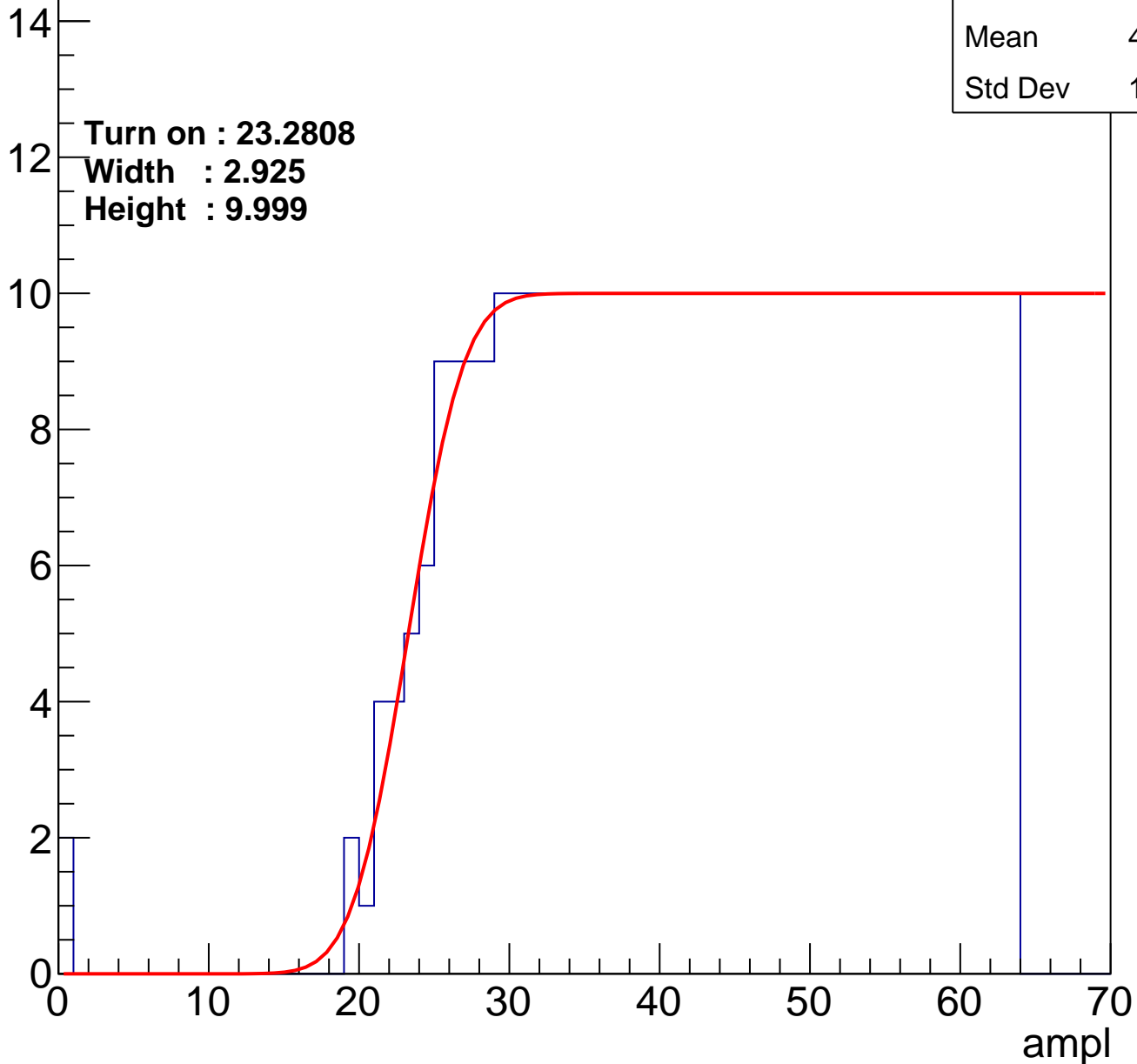
Entries	410
Mean	42.79
Std Dev	12.29

Turn on : 23.2808

Width : 2.925

Height : 9.999

Entry



# B1L002S, U21-ch113

calib\_packv5\_042523\_0143.root, FC#10, port B3

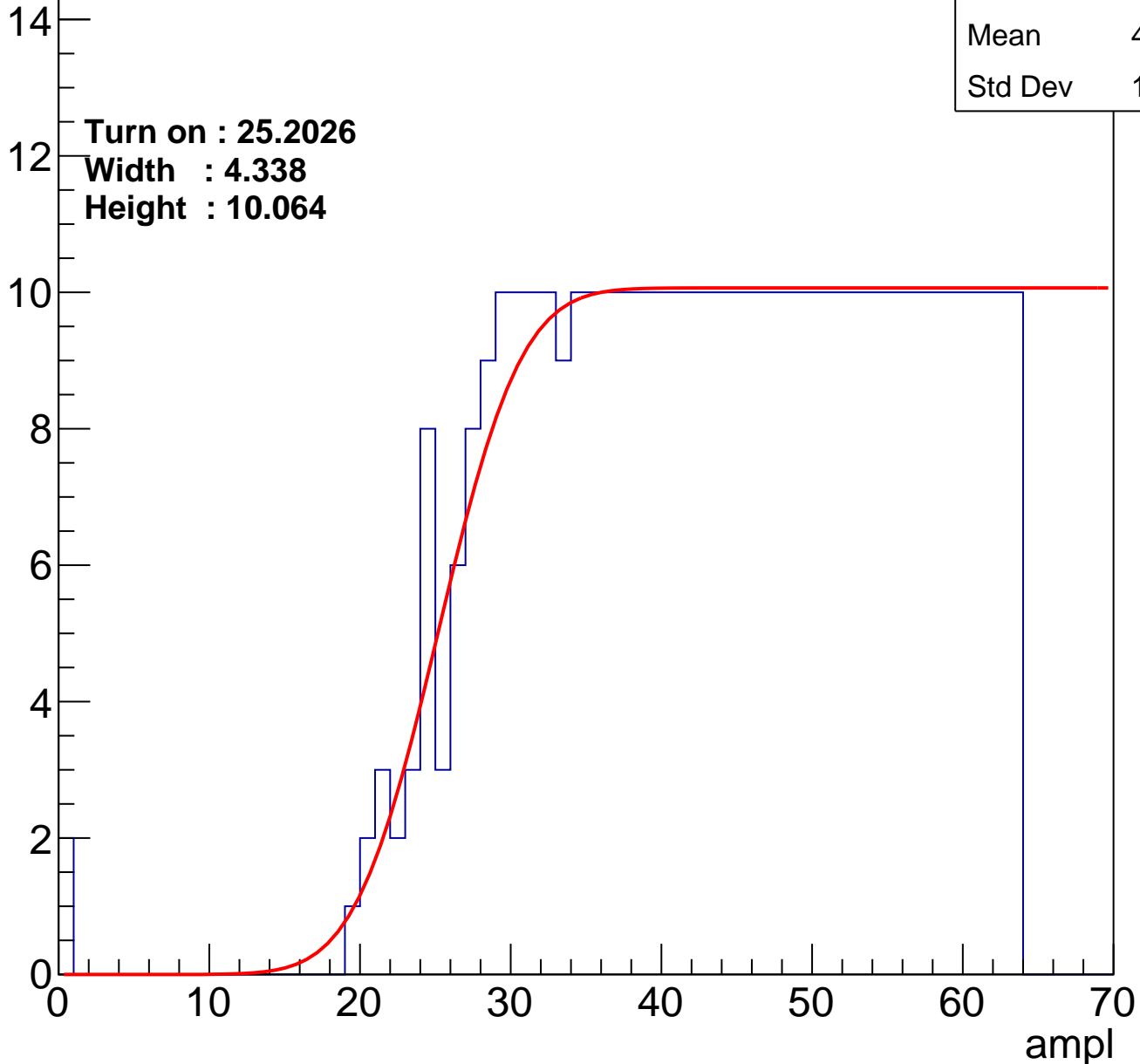
Entries	396
Mean	43.42
Std Dev	12.02

Turn on : 25.2026

Width : 4.338

Height : 10.064

Entry



# B1L002S, U21-ch114

calib\_packv5\_042523\_0143.root, FC#10, port B3

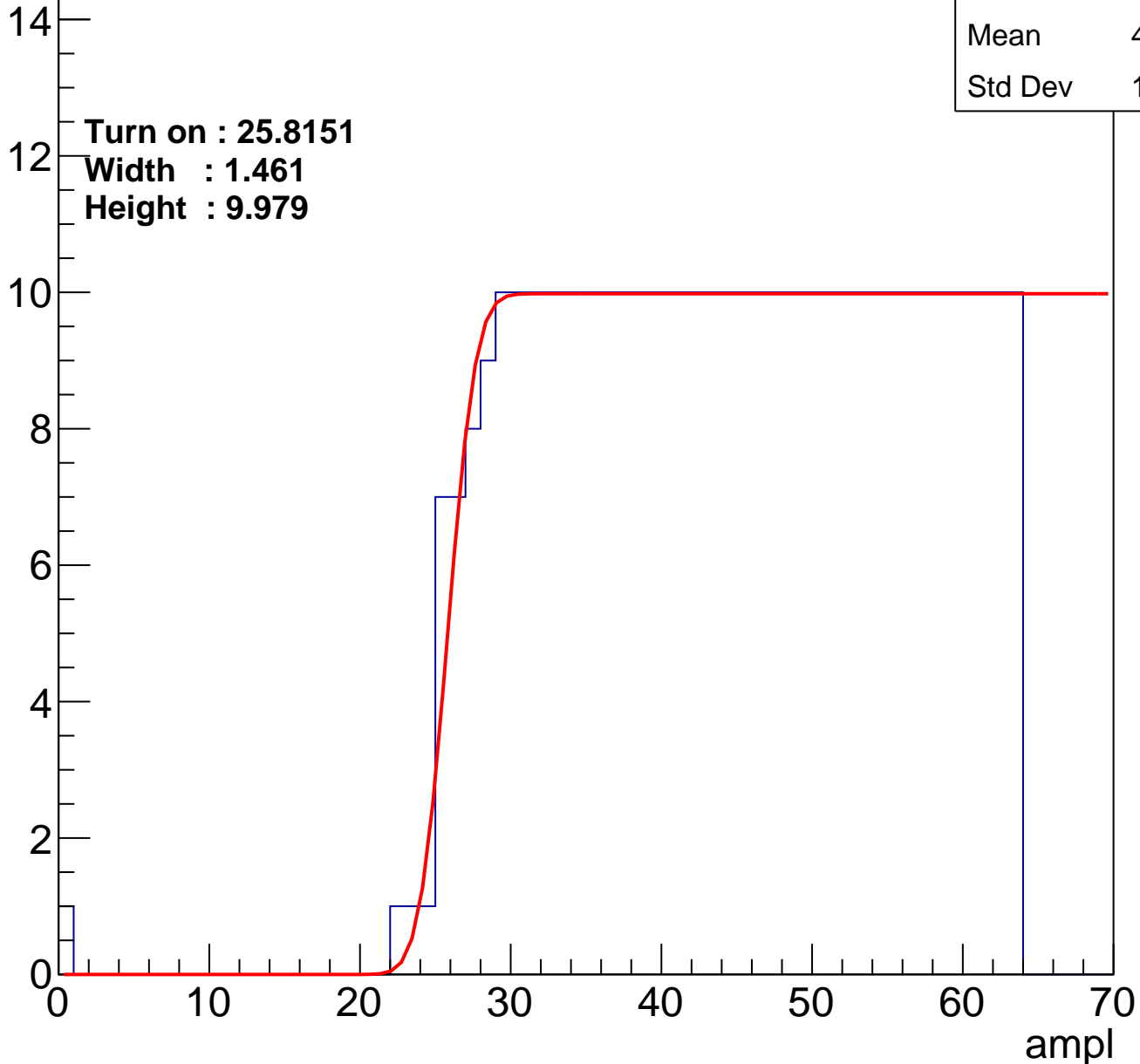
Entries	385
Mean	44.14
Std Dev	11.37

Turn on : 25.8151

Width : 1.461

Height : 9.979

Entry



# B1L002S, U21-ch115

calib\_packv5\_042523\_0143.root, FC#10, port B3

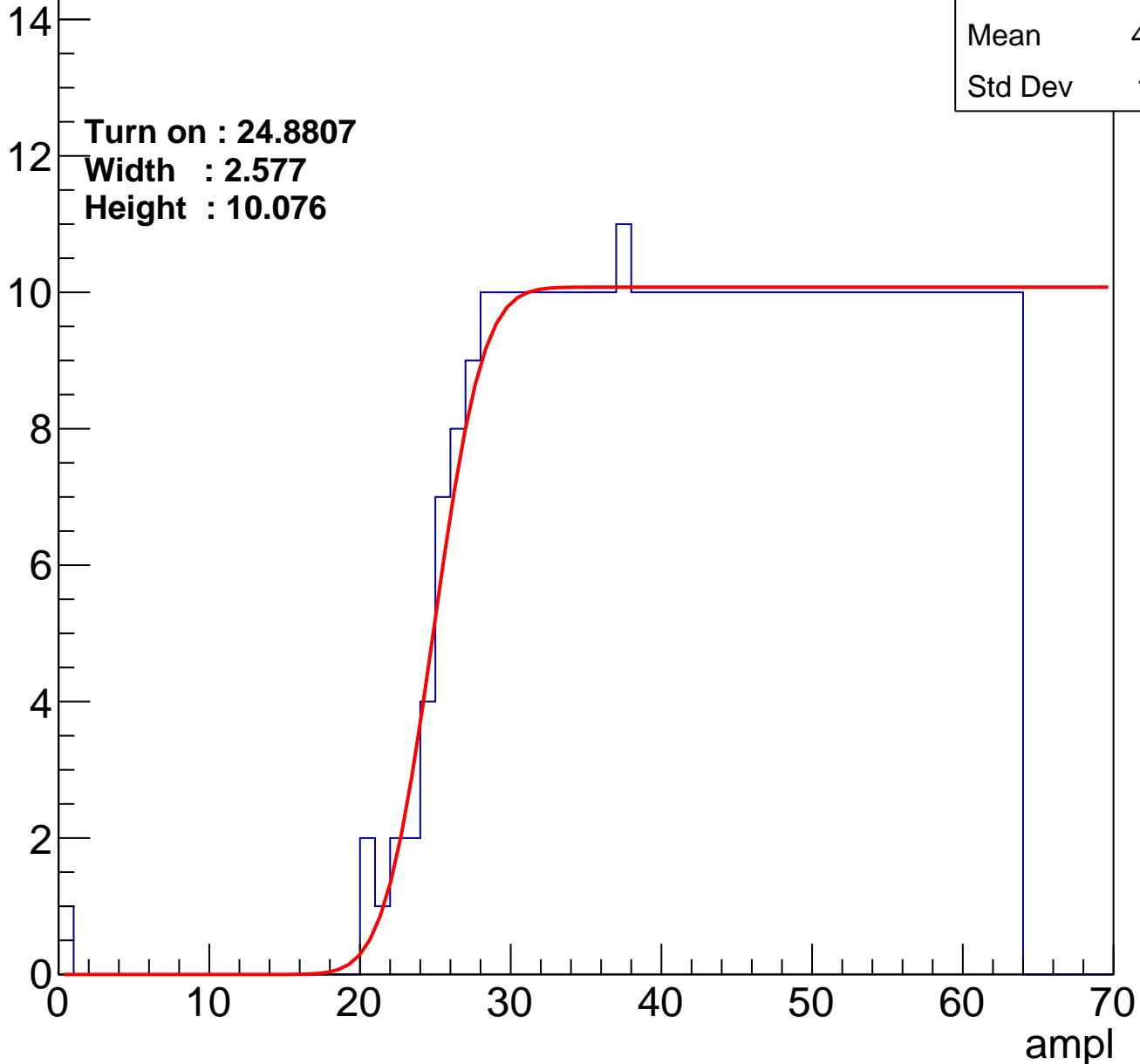
Entries	397
Mean	43.55
Std Dev	11.71

Turn on : 24.8807

Width : 2.577

Height : 10.076

Entry



# B1L002S, U21-ch116

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 25.0246

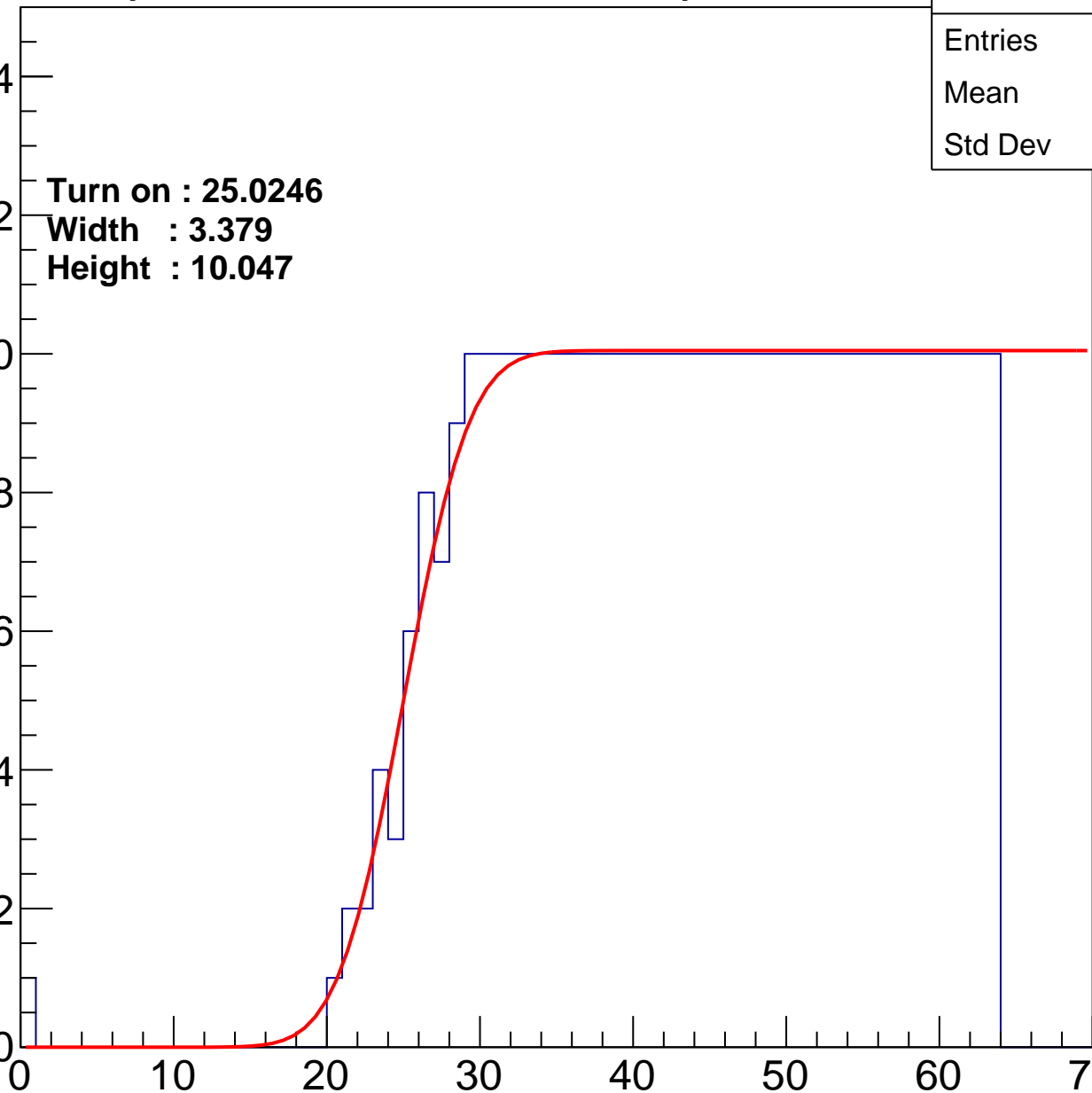
Width : 3.379

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch117

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	367
Mean	44.83
Std Dev	11.38

Turn on : 27.8921

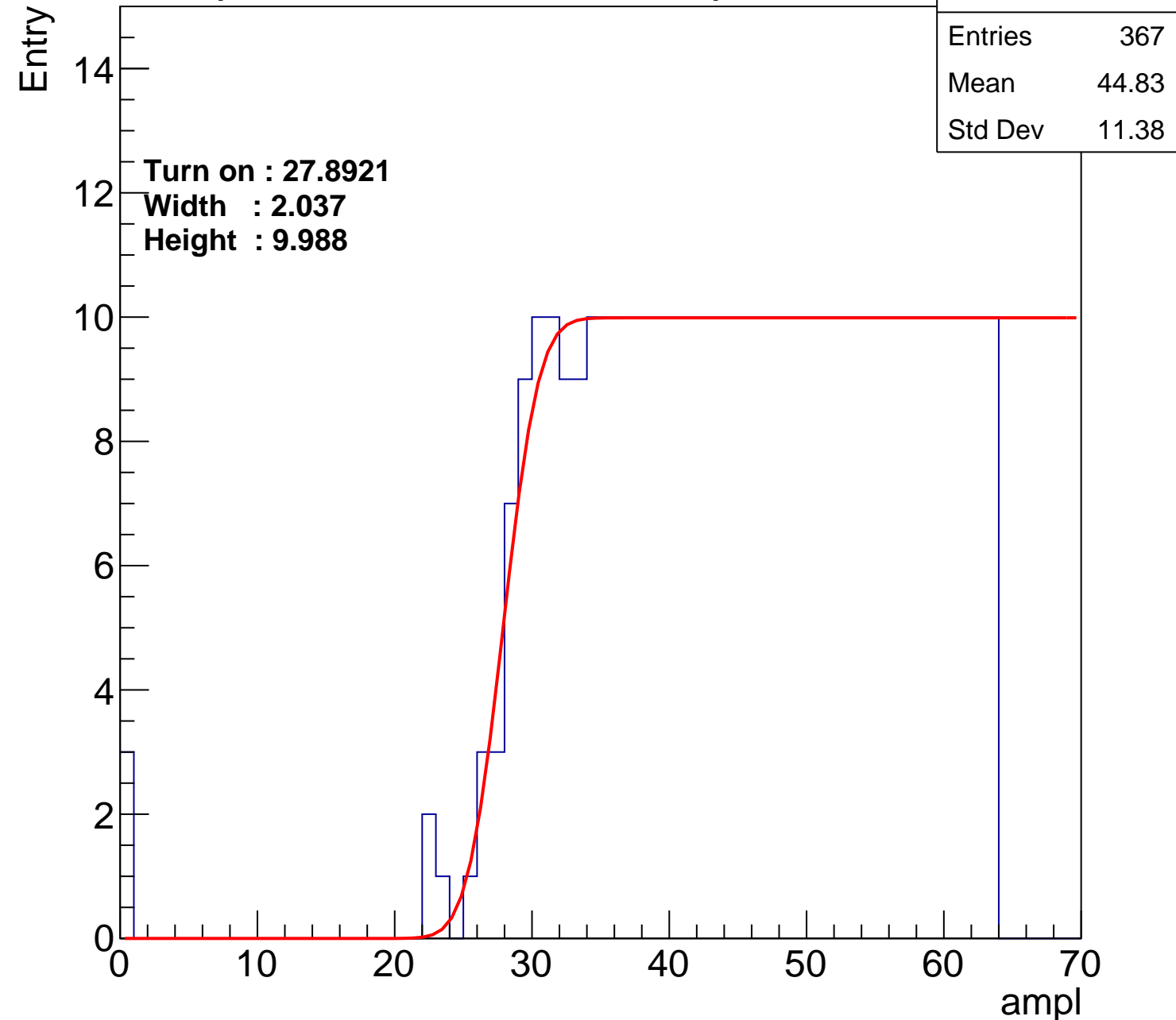
Width : 2.037

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch118

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.66
Std Dev	11.67

Turn on : 24.4994

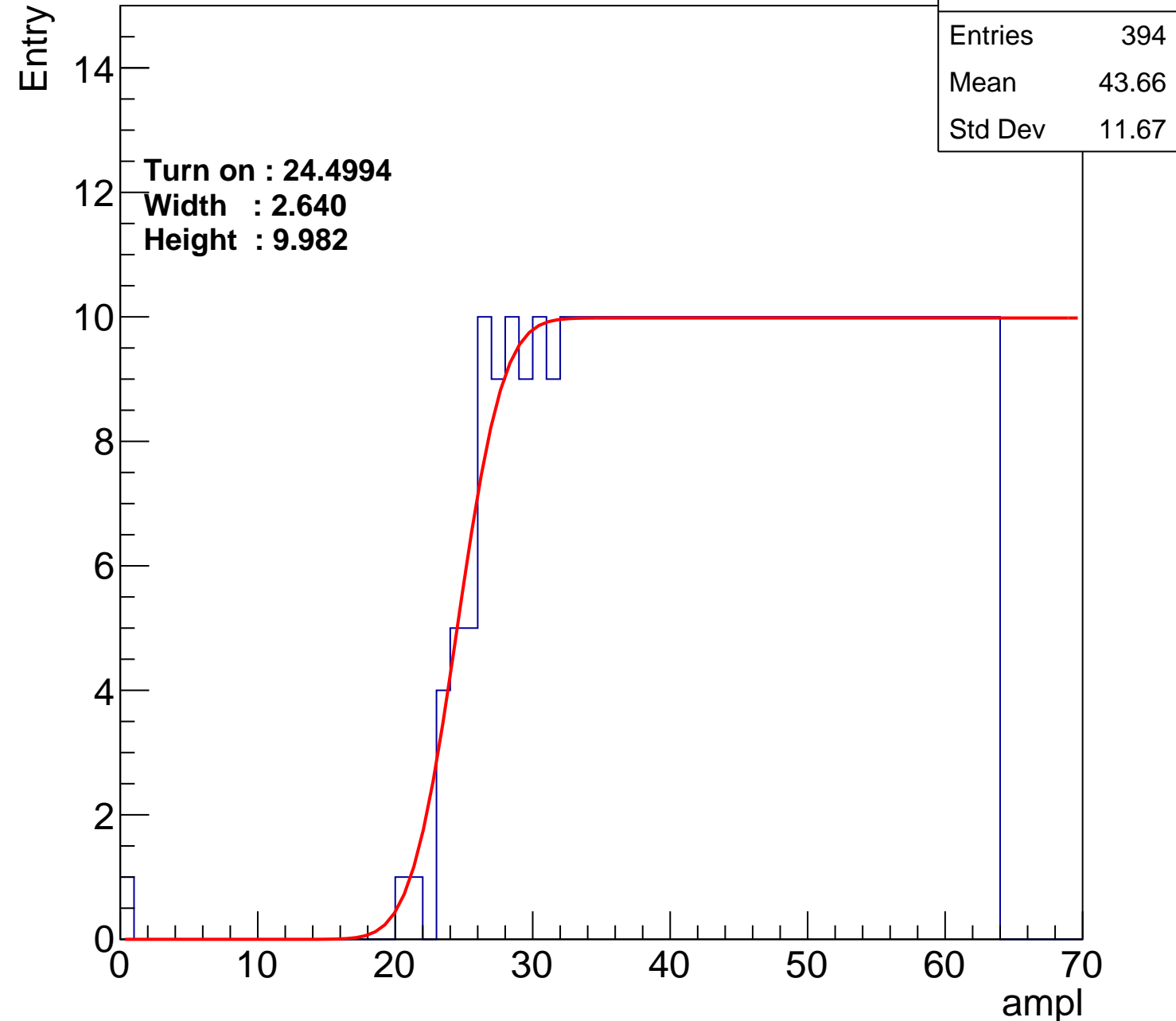
Width : 2.640

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch119

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	406
Mean	42.97
Std Dev	12.27

Turn on : 23.9957

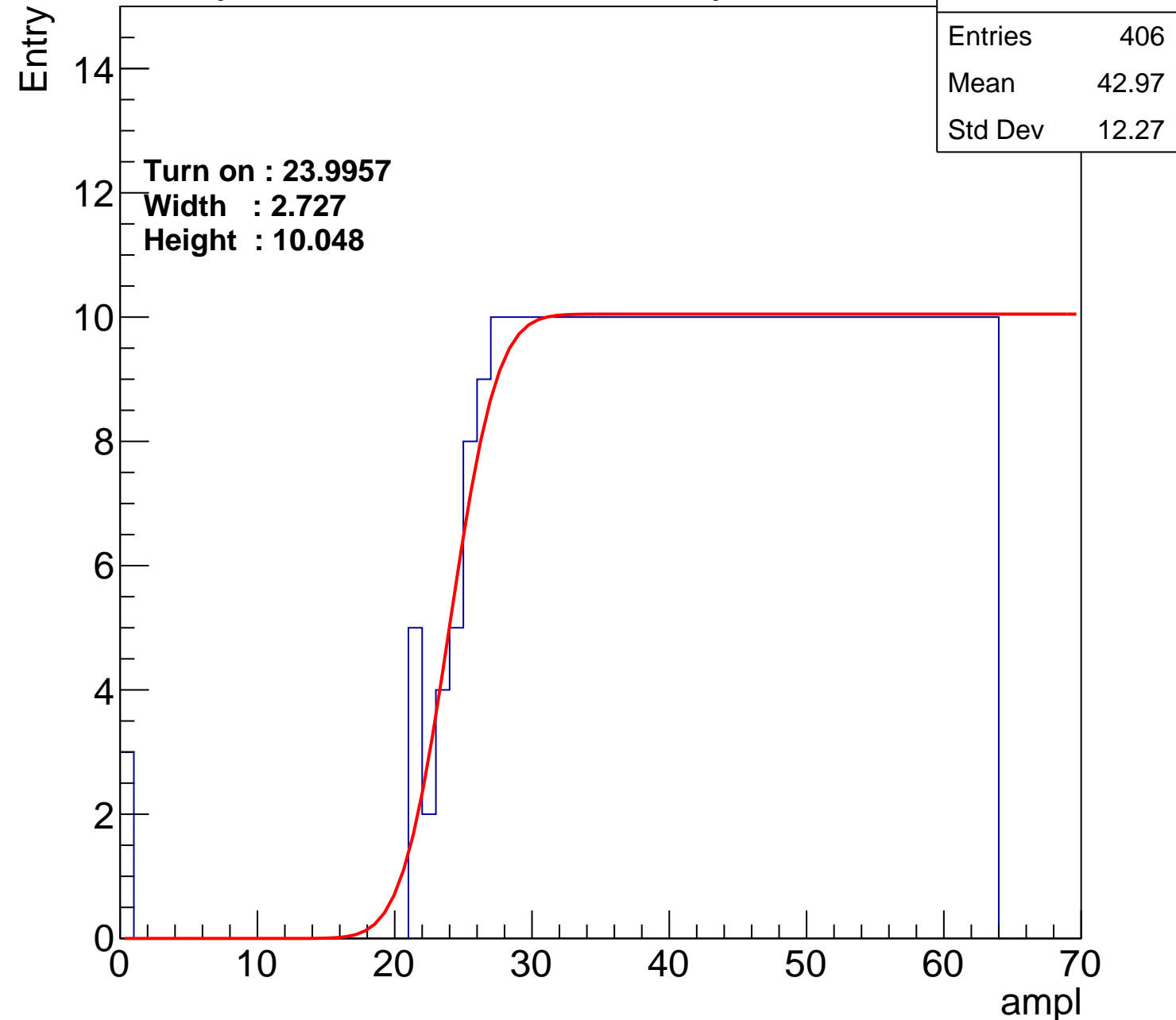
Width : 2.727

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch120

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.4695

Width : 2.496

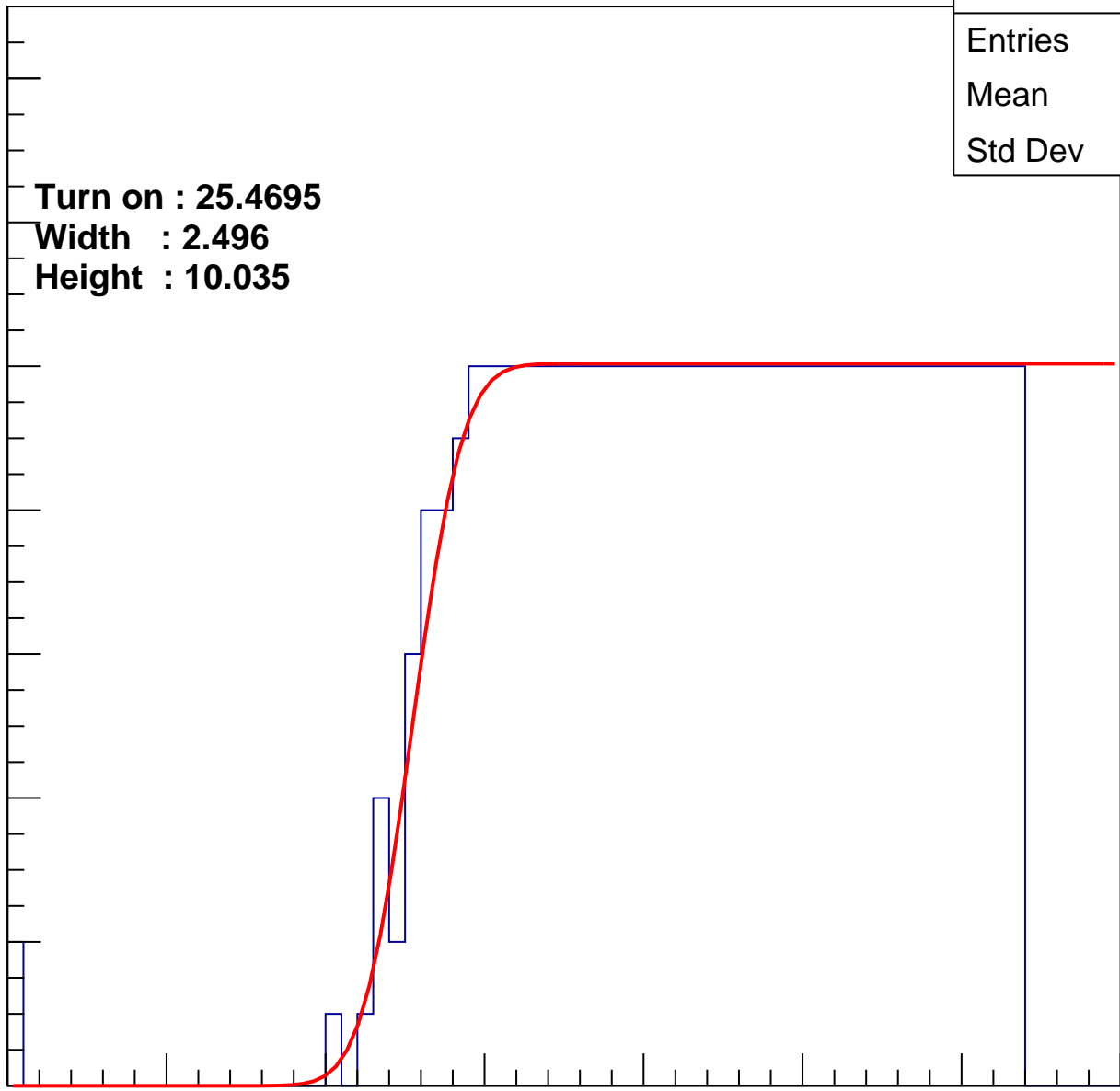
Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U21-ch121

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	401
Mean	43.23
Std Dev	12.07

Turn on : 24.6329

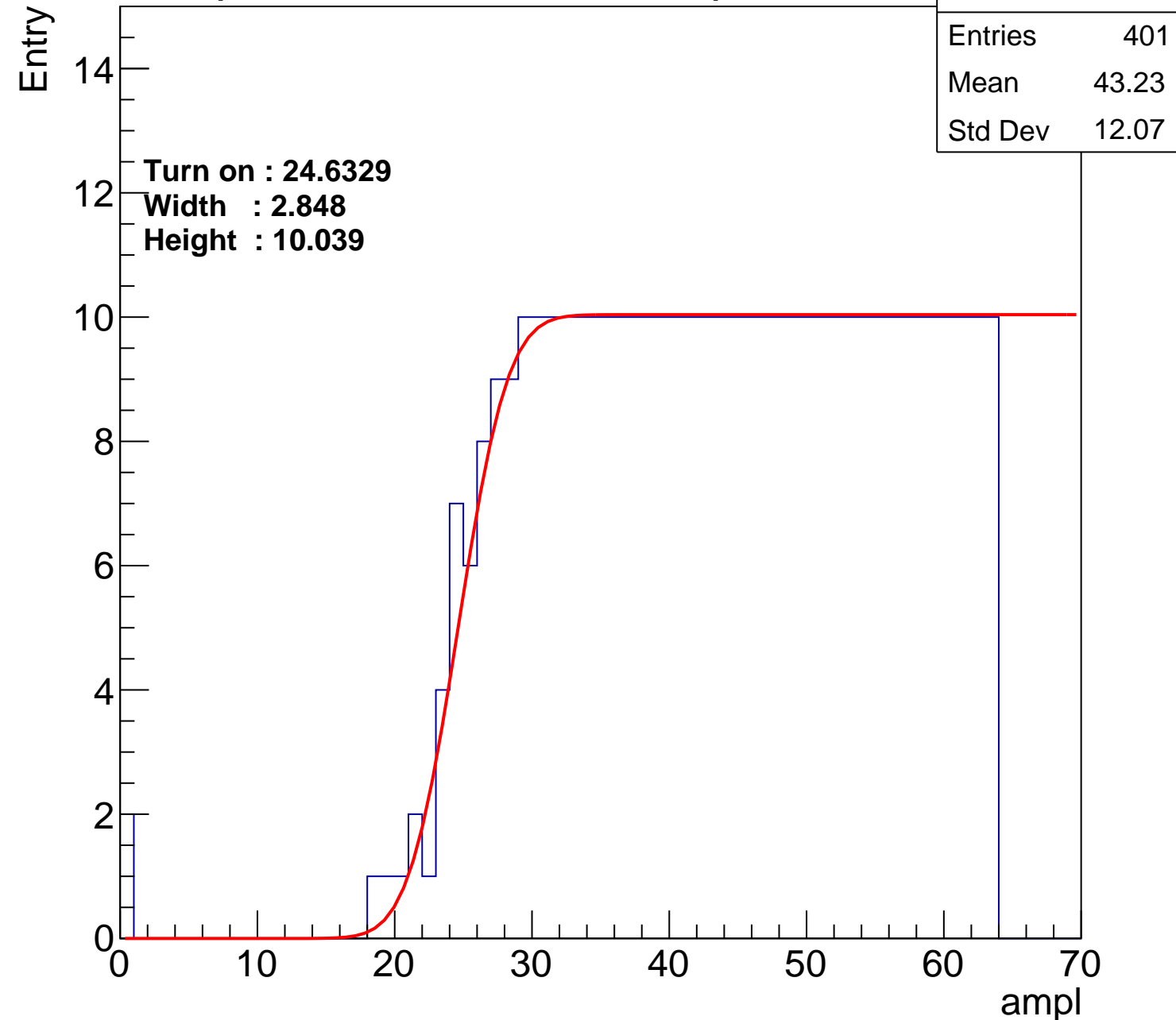
Width : 2.848

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch122

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.24
Std Dev	11.62

Turn on : 26.2508

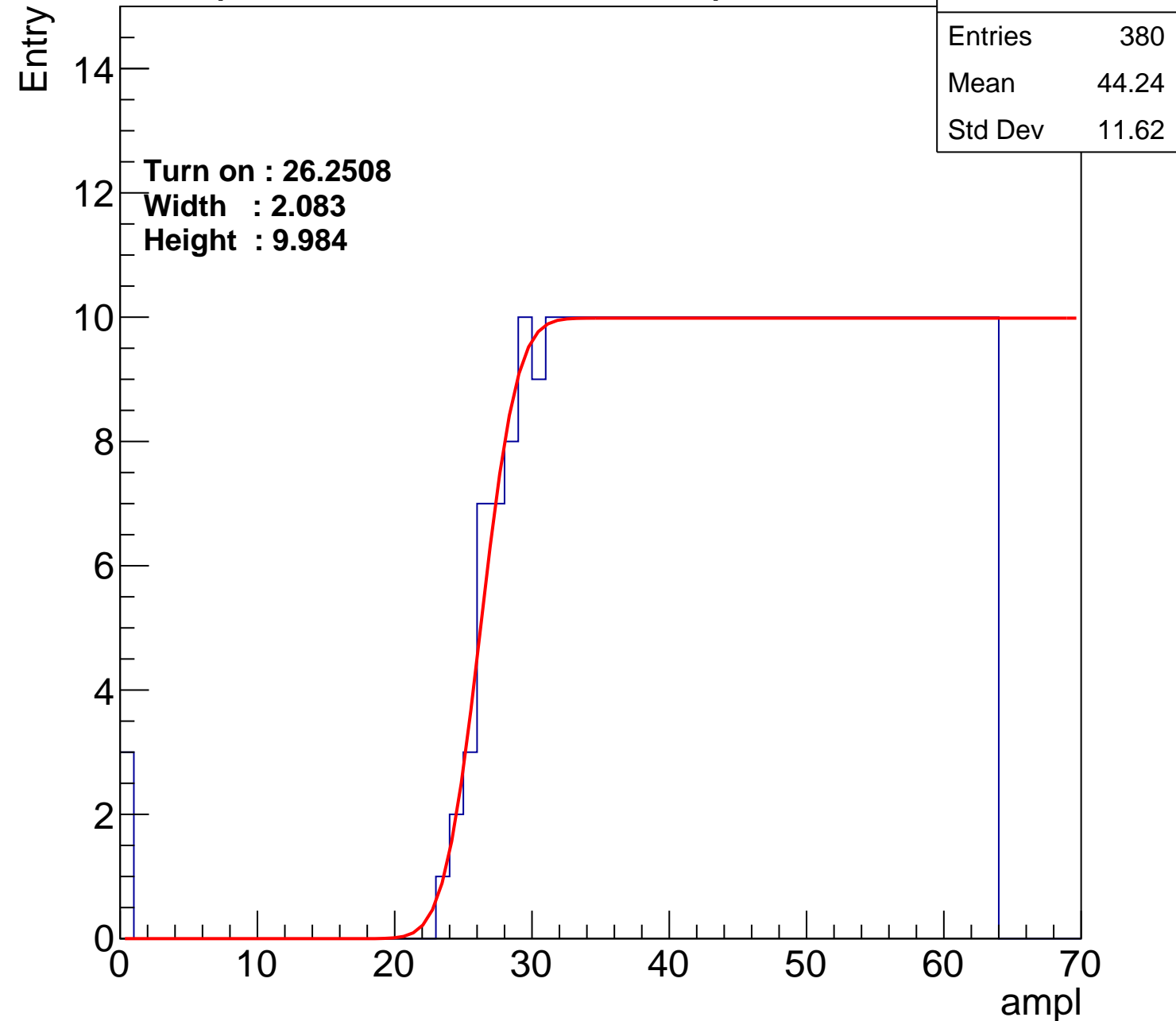
Width : 2.083

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch123

calib\_packv5\_042523\_0143.root, FC#10, port B3

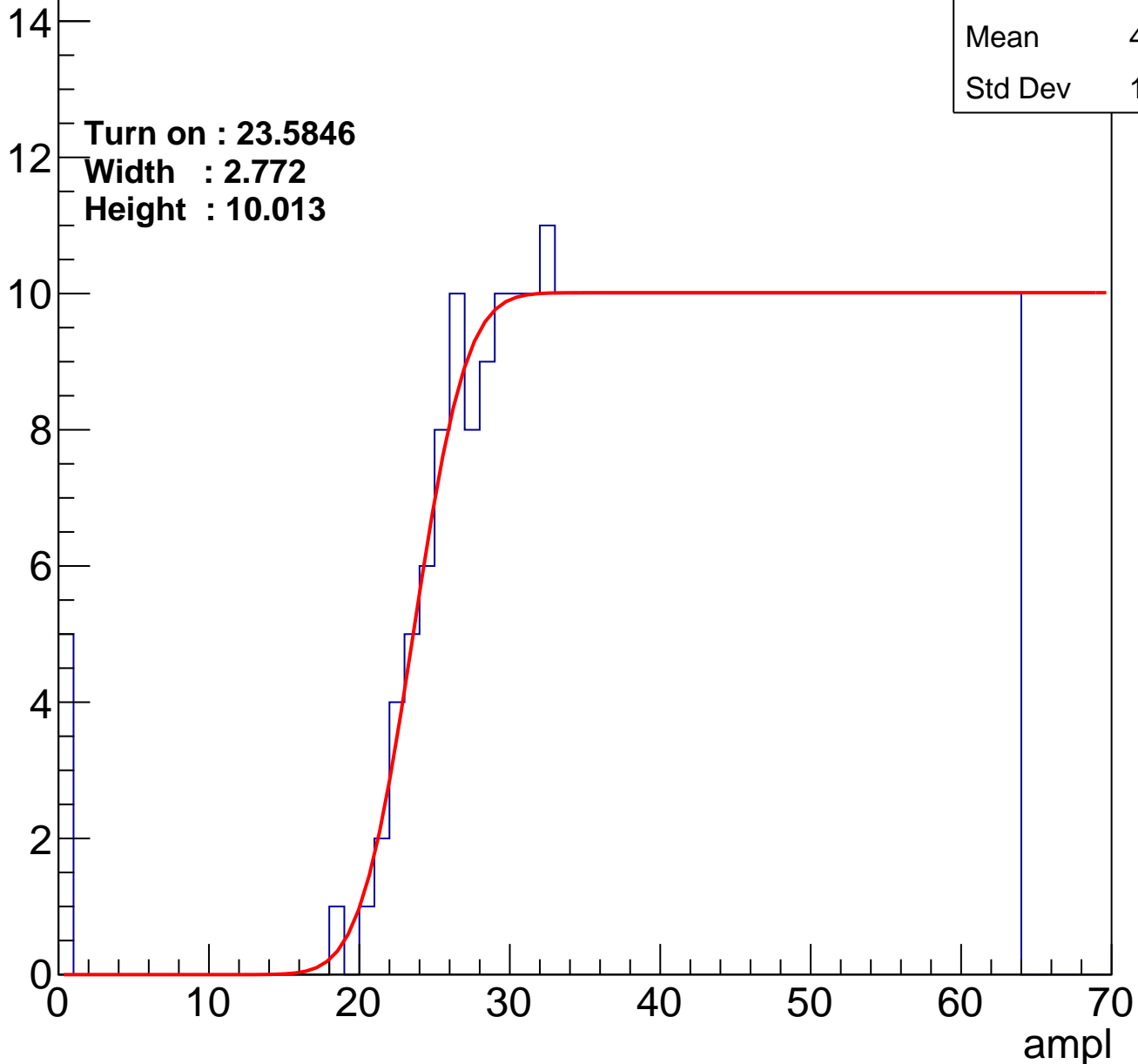
Entries	410
Mean	42.65
Std Dev	12.67

Turn on : 23.5846

Width : 2.772

Height : 10.013

Entry



# B1L002S, U21-ch124

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.19
Std Dev	11.52

**Turn on : 26.2947**

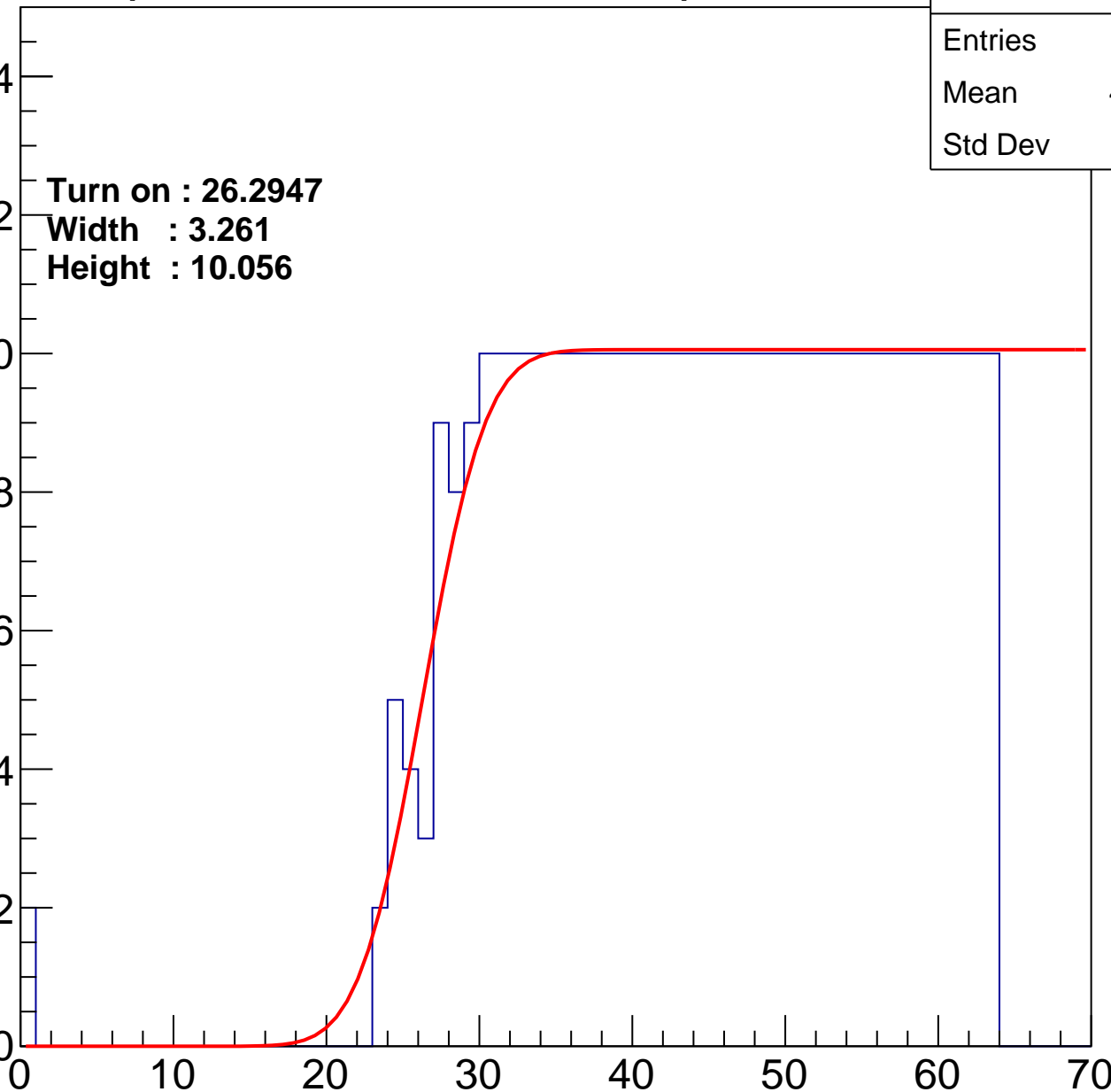
**Width : 3.261**

**Height : 10.056**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch125

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.68
Std Dev	11.14

Turn on : 27.0281

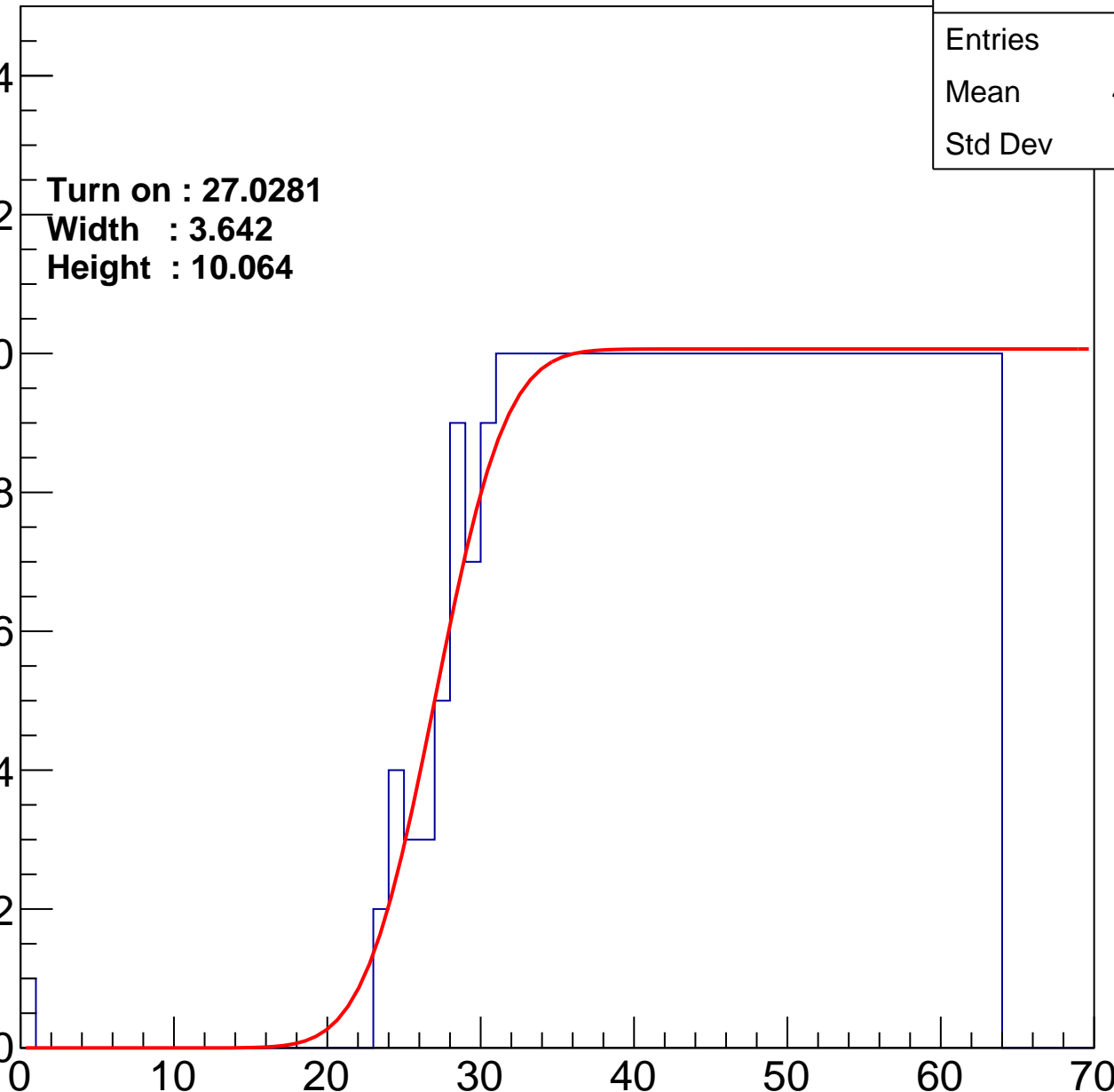
Width : 3.642

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U21-ch126

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.48
Std Dev	11.25

Turn on : 26.8119

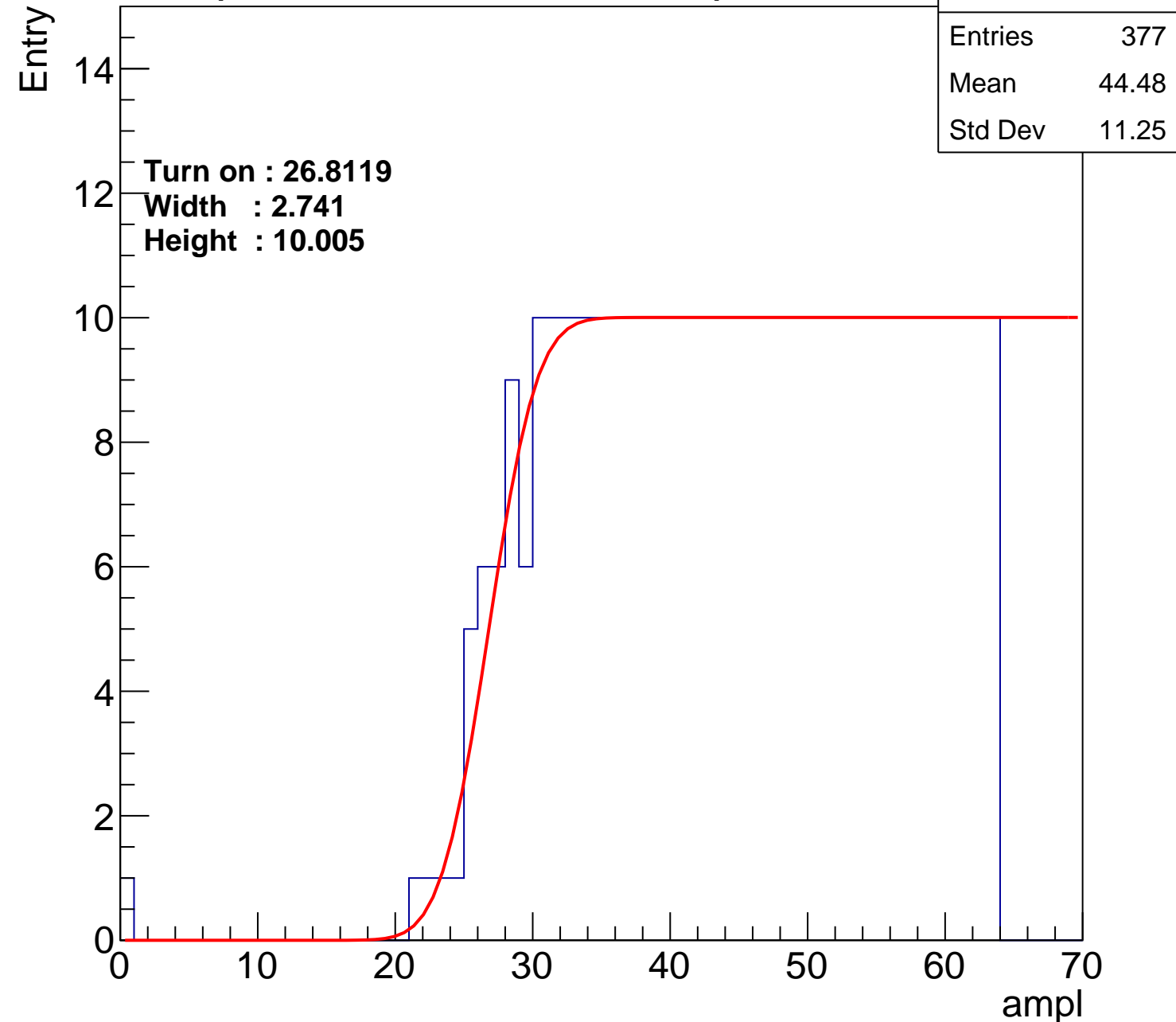
Width : 2.741

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U21-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

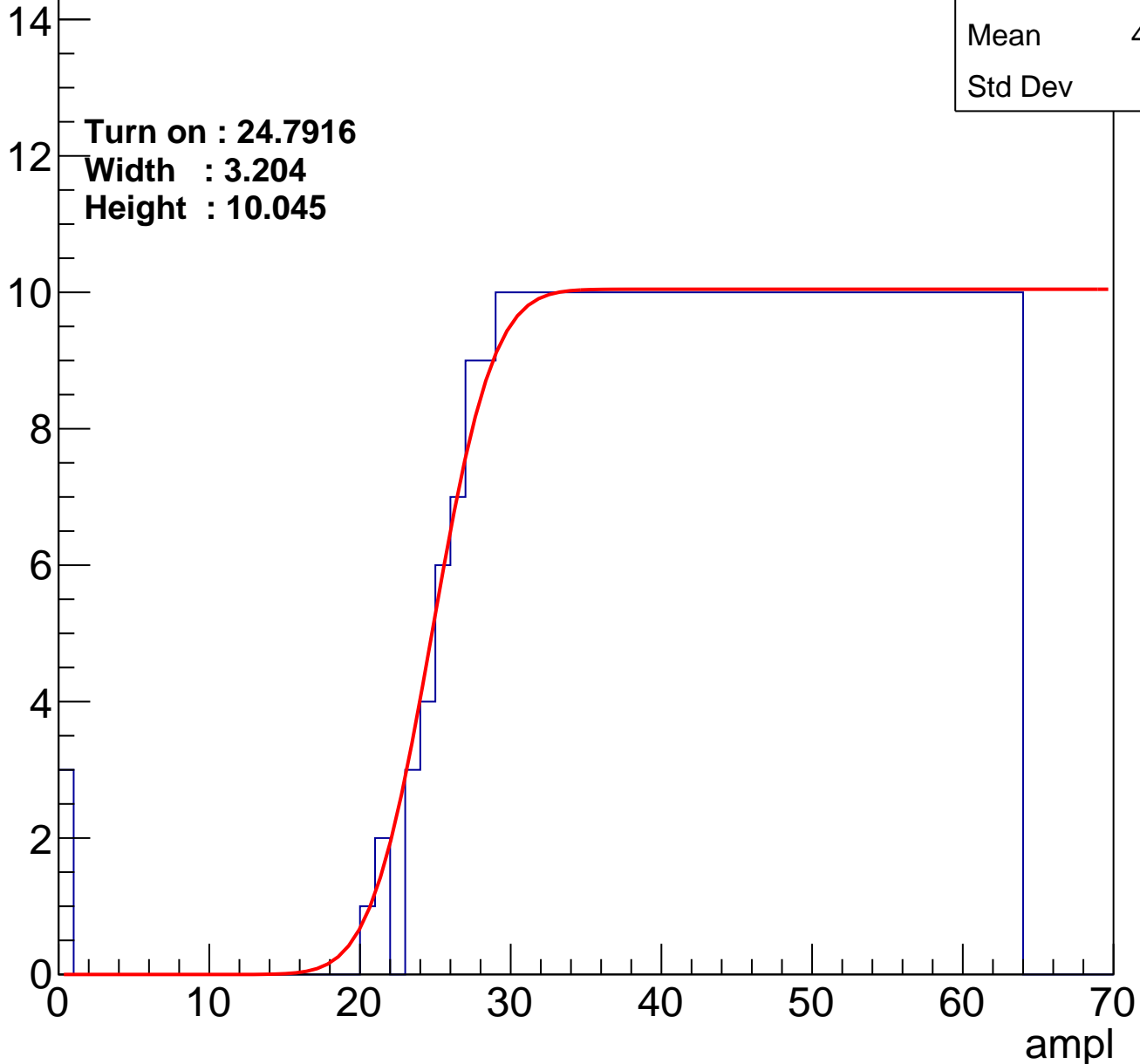
Entries	394
Mean	43.54
Std Dev	12

Turn on : 24.7916

Width : 3.204

Height : 10.045

Entry



# B1L002S, U21-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.54
Std Dev	12

Turn on : 24.7916

Width : 3.204

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

