

B0L000S, U2-ch0, adc0

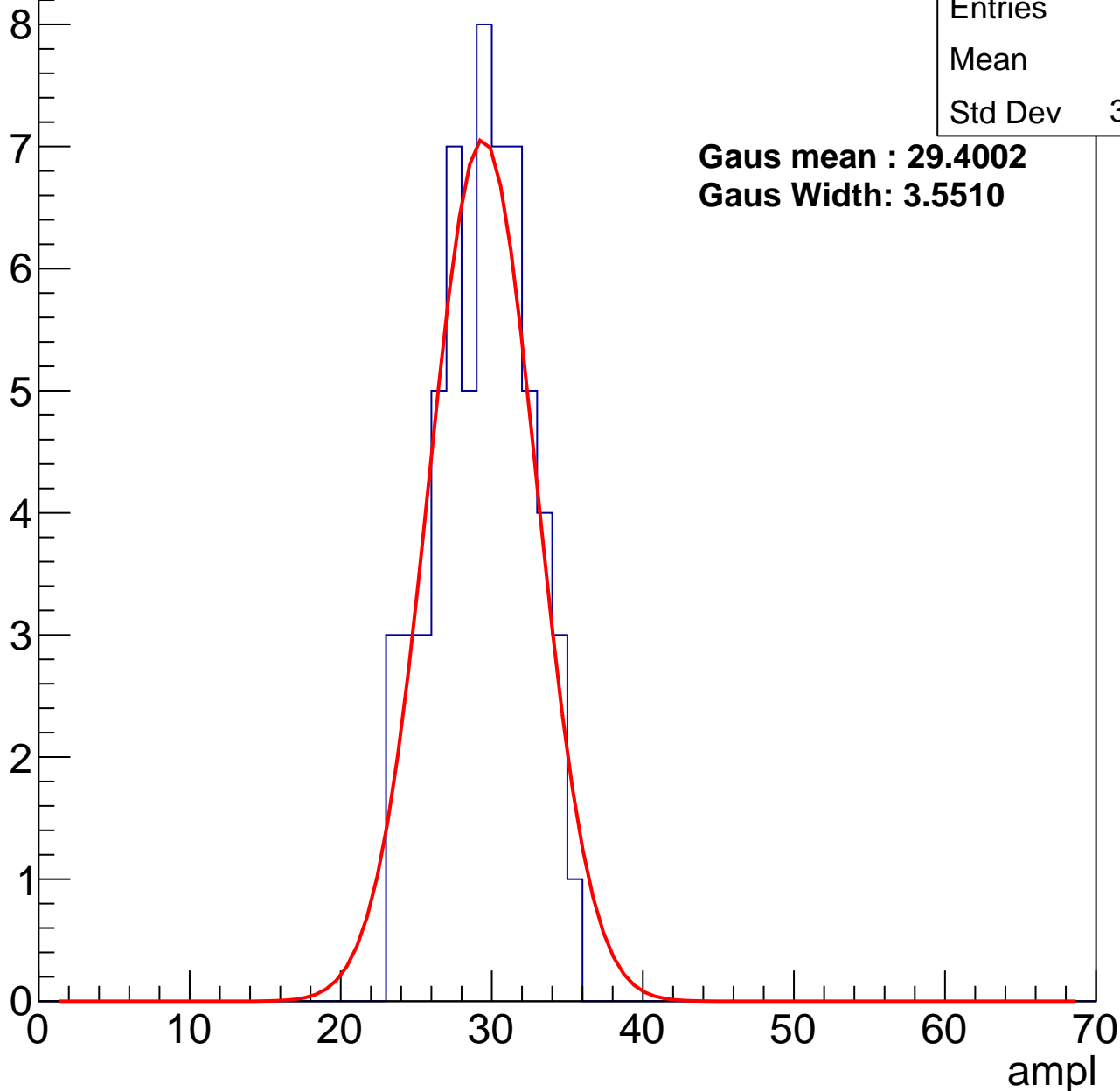
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	28.9
Std Dev	3.039

Gaus mean : 29.4002

Gaus Width: 3.5510



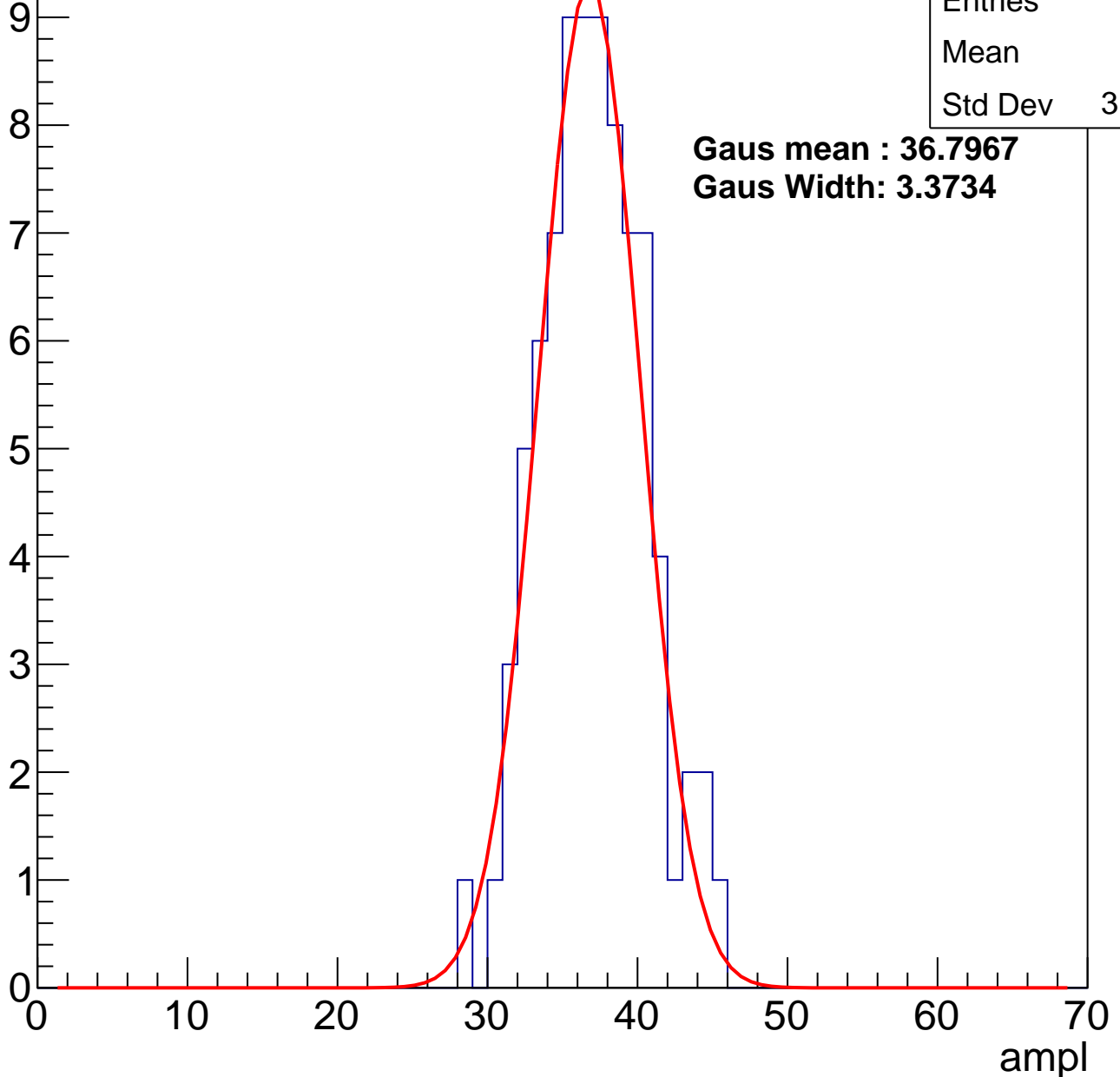
B0L000S, U2-ch0, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	36.6
Std Dev	3.449

Gaus mean : 36.7967
Gaus Width: 3.3734

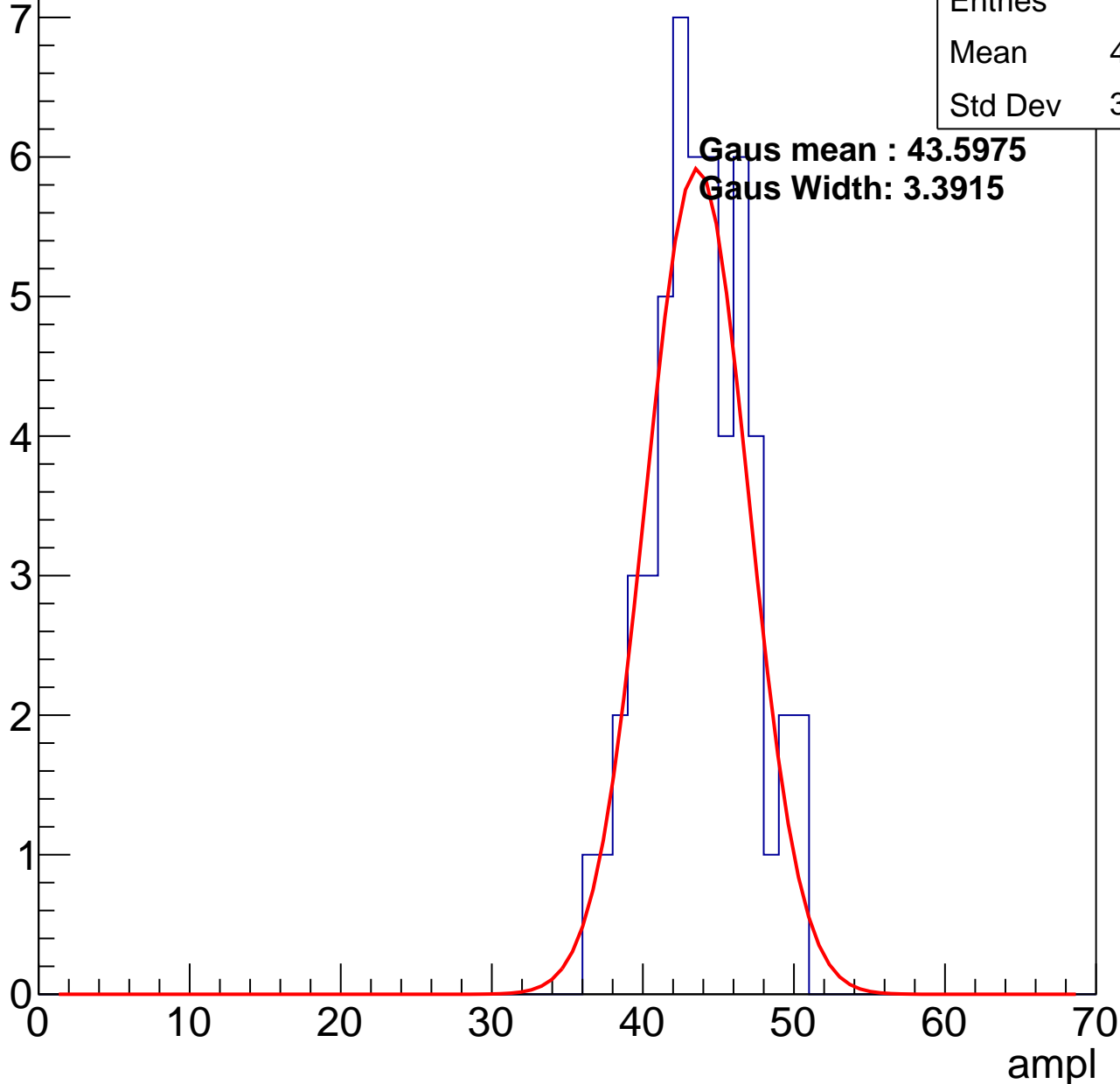


B0L000S, U2-ch0, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.34
Std Dev	3.268

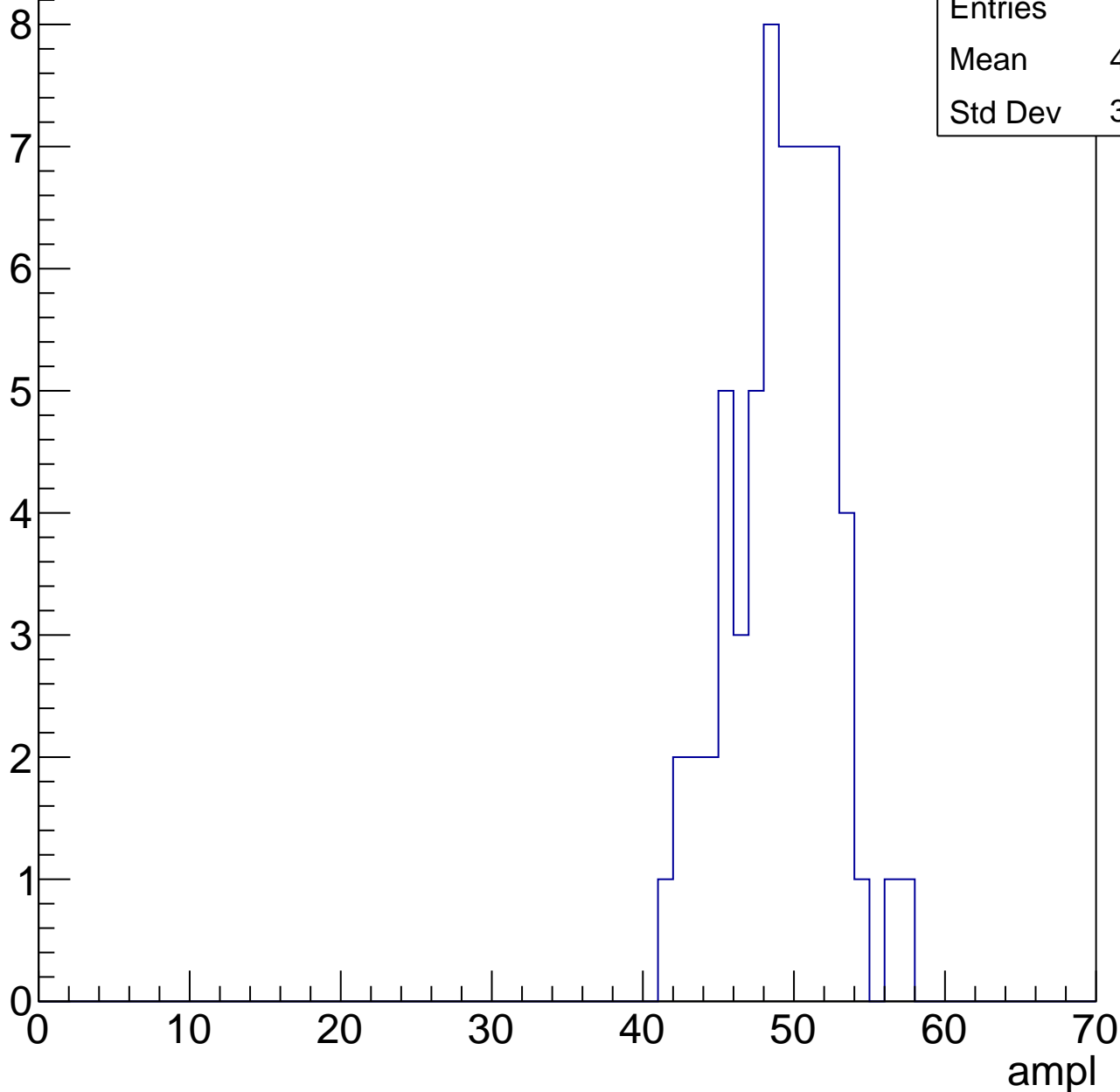


B0L000S, U2-ch0, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

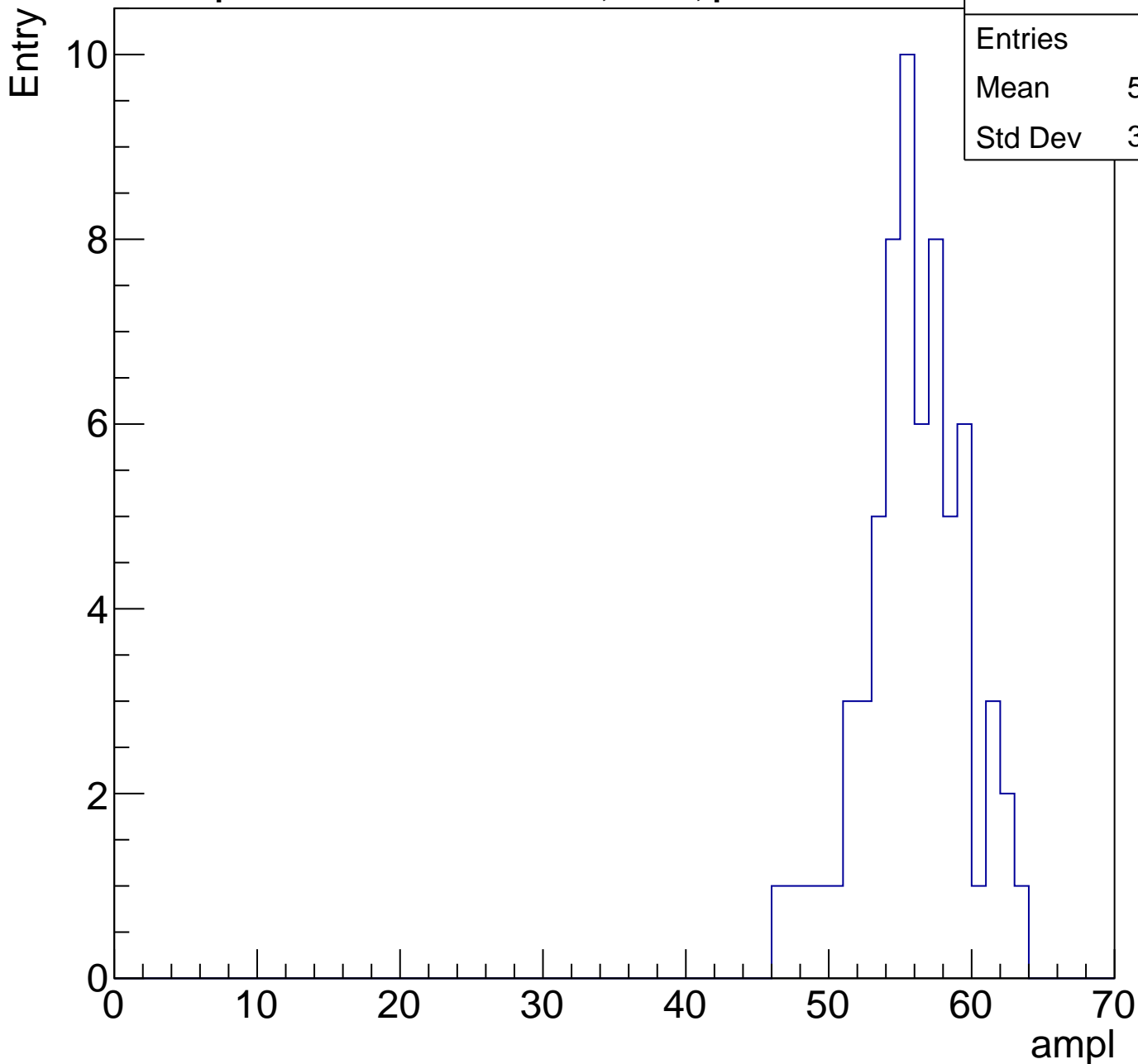
Entries	63
Mean	48.79
Std Dev	3.372



B0L000S, U2-ch0, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	66
Mean	55.48
Std Dev	3.517



B0L000S, U2-ch0, adc5

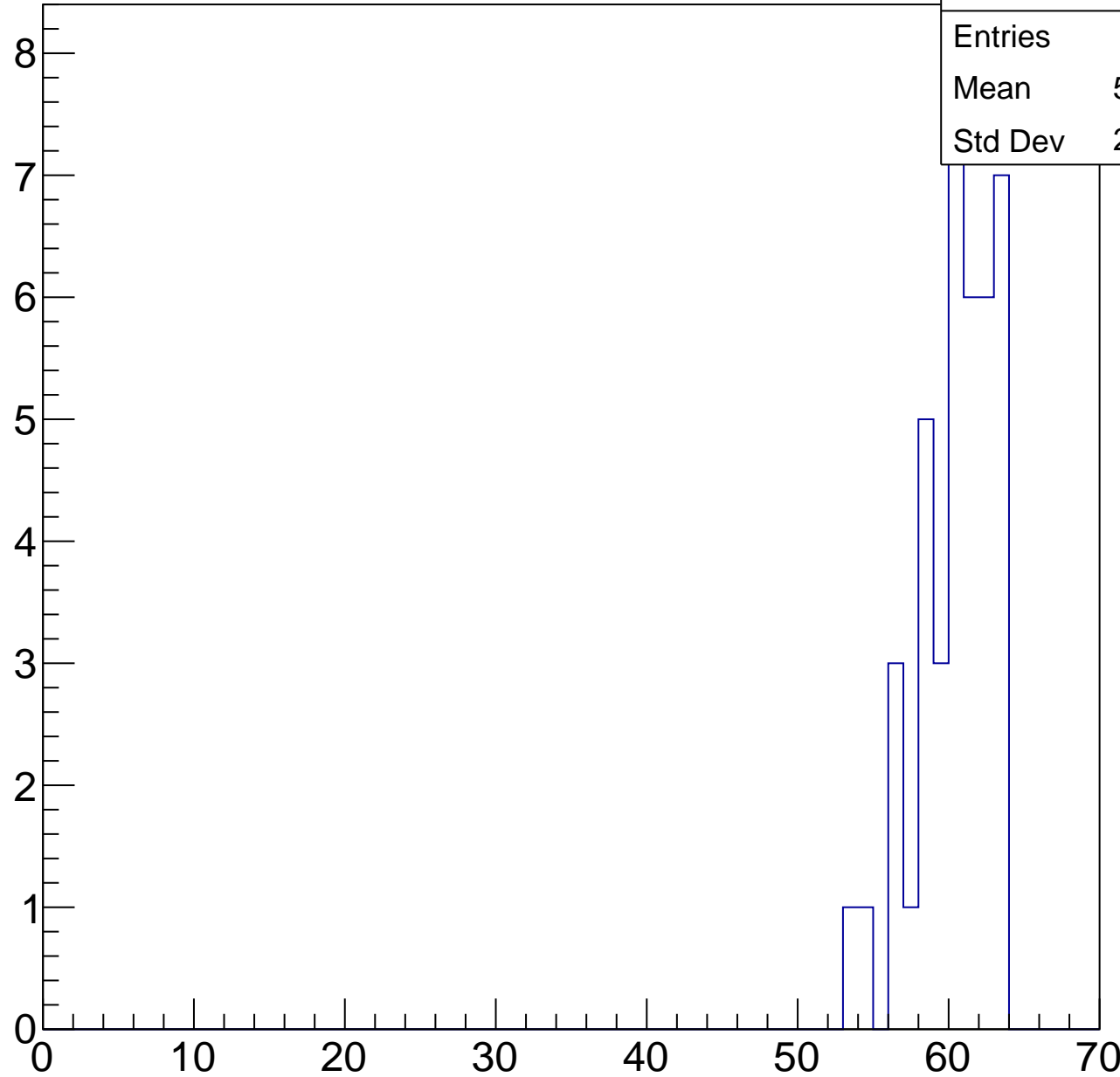
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.95
Std Dev	2.508

ampl



B0L000S, U2-ch0, adc6

calib_packv5_042523_0143.root, FC#5, port B1

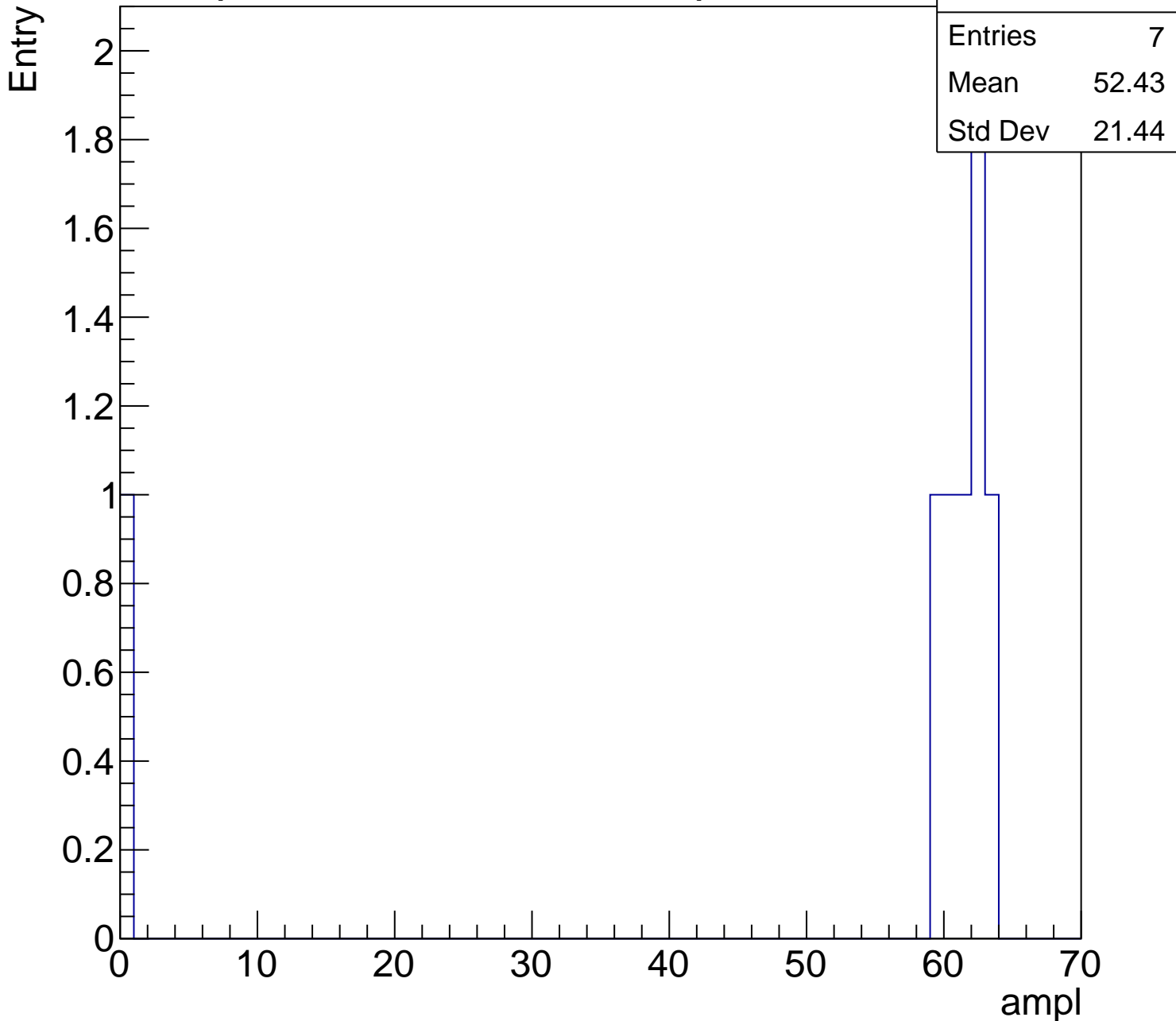
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.43
Std Dev	21.44

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch0, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch1, adc0

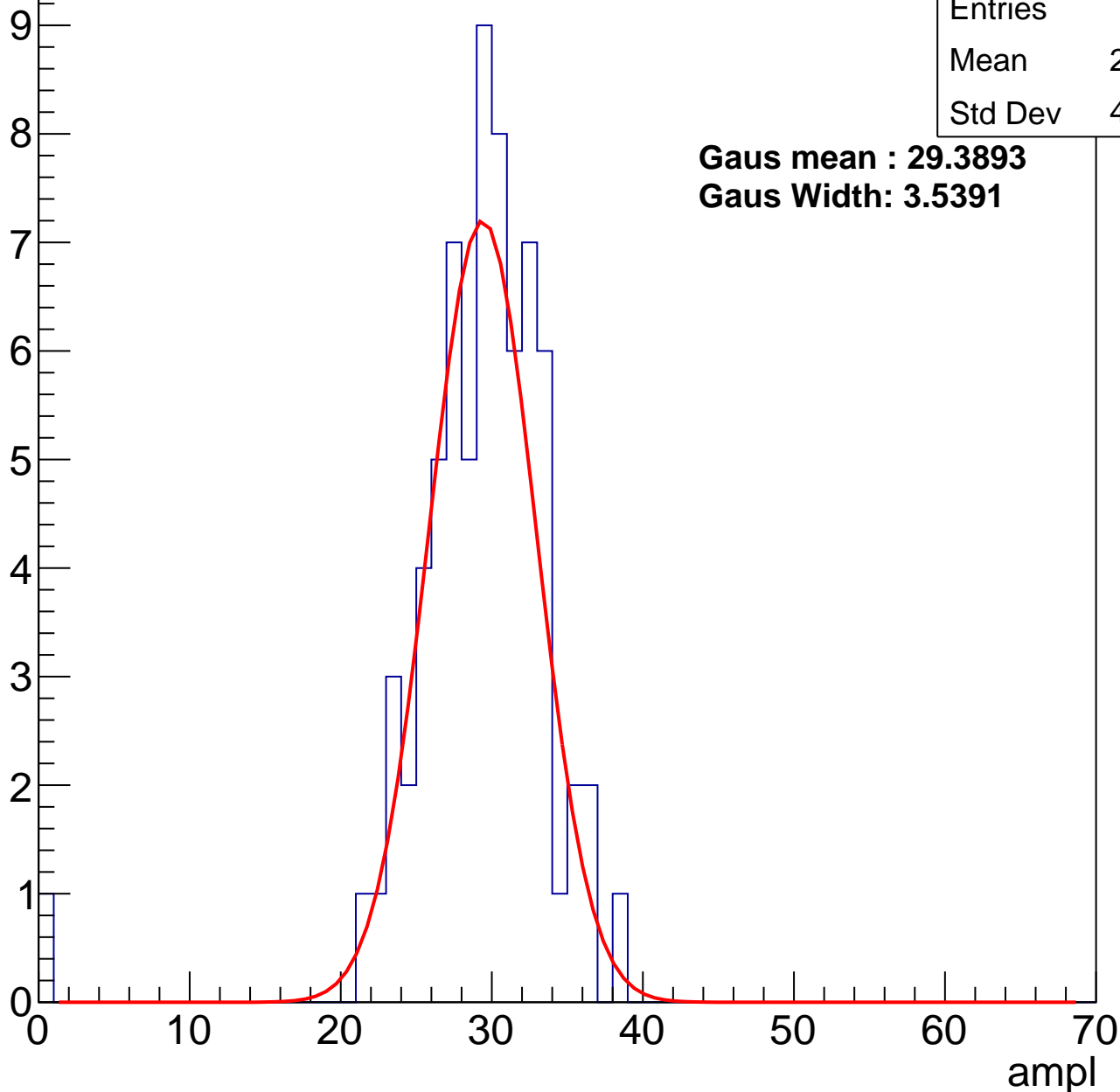
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	28.76
Std Dev	4.918

Gaus mean : 29.3893

Gaus Width: 3.5391



B0L000S, U2-ch1, adc1

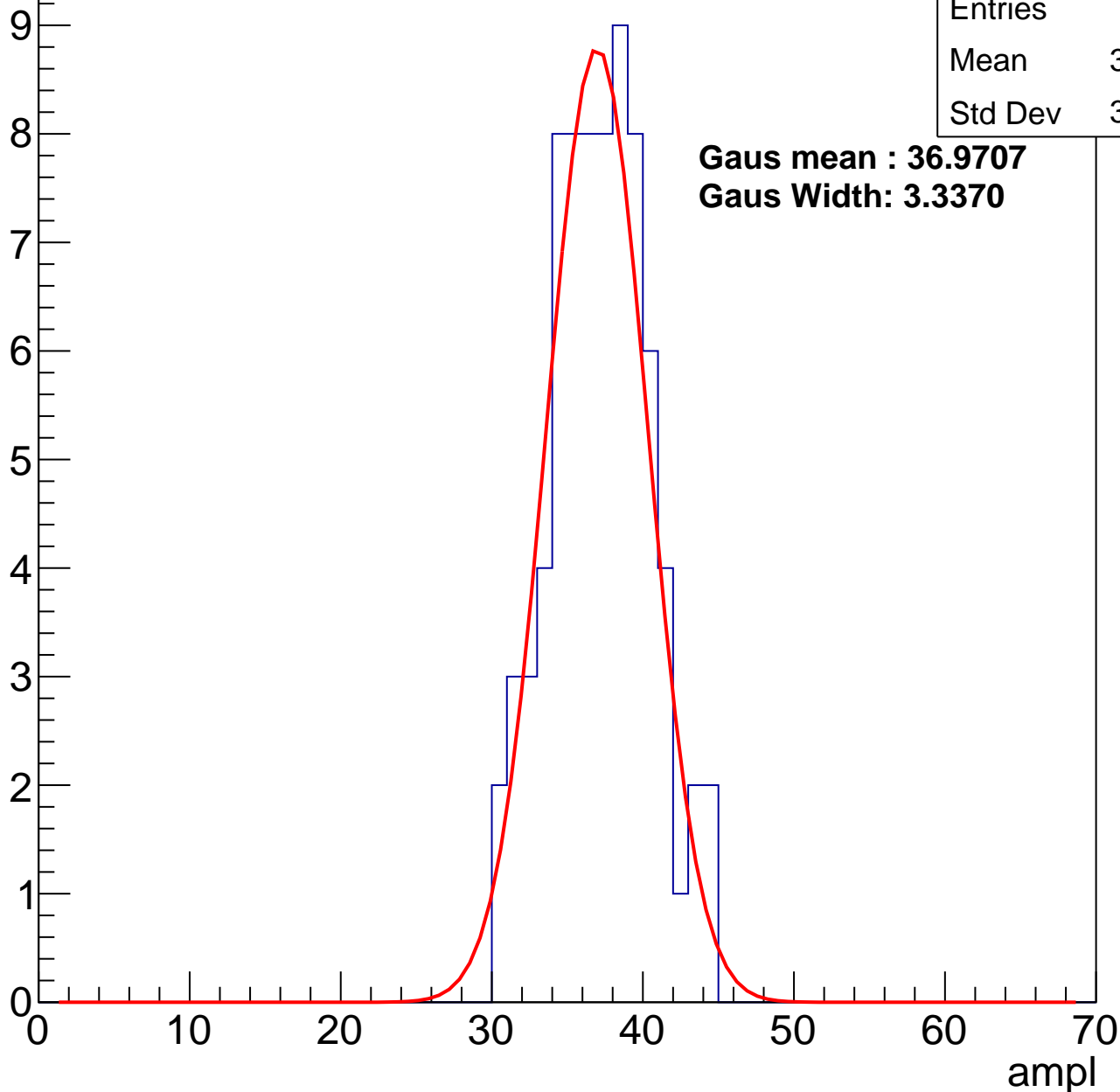
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	36.72
Std Dev	3.255

Gaus mean : 36.9707

Gaus Width: 3.3370



B0L000S, U2-ch1, adc2

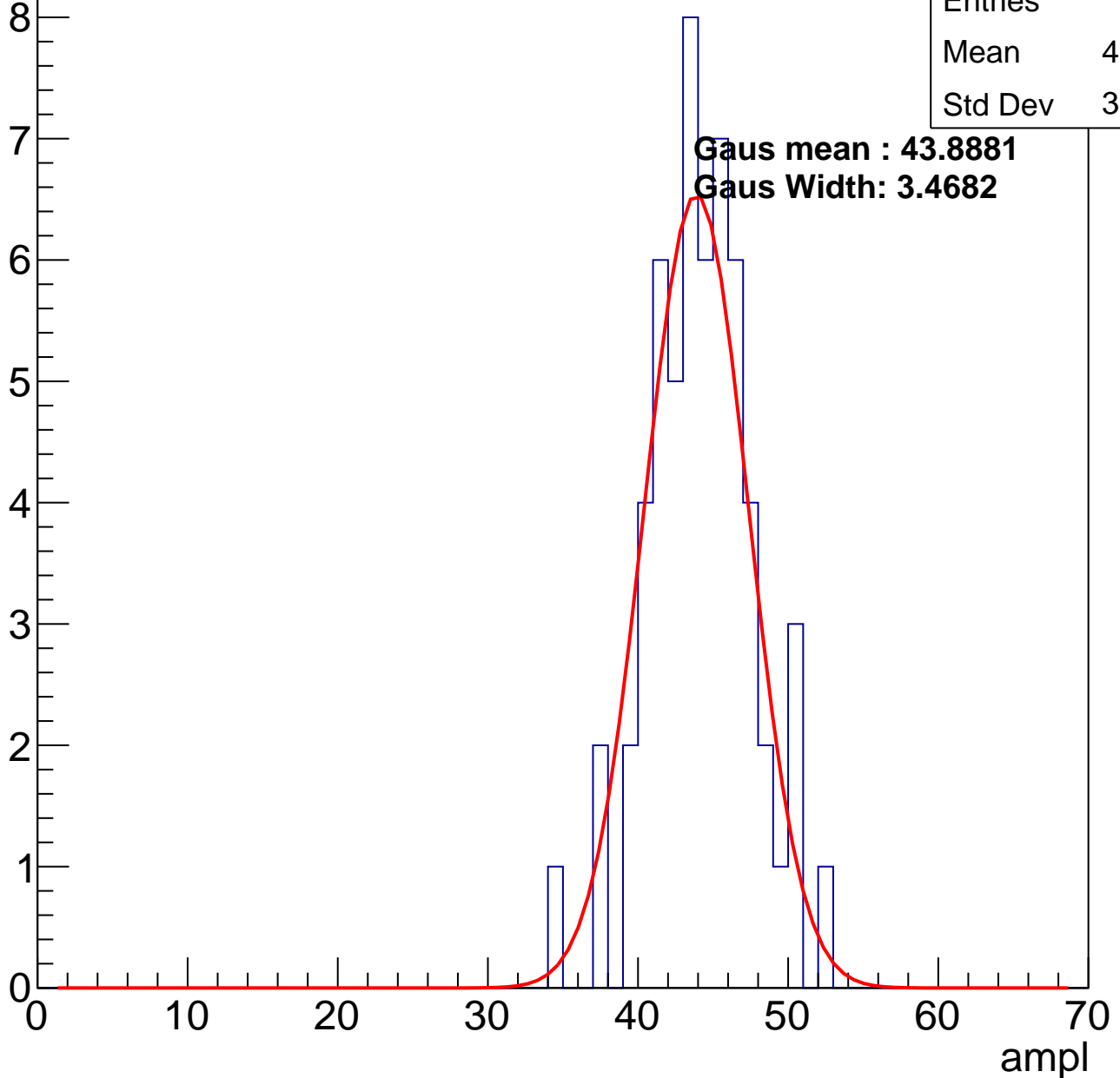
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.72
Std Dev	3.438

Gaus mean : 43.8881

Gaus Width: 3.4682

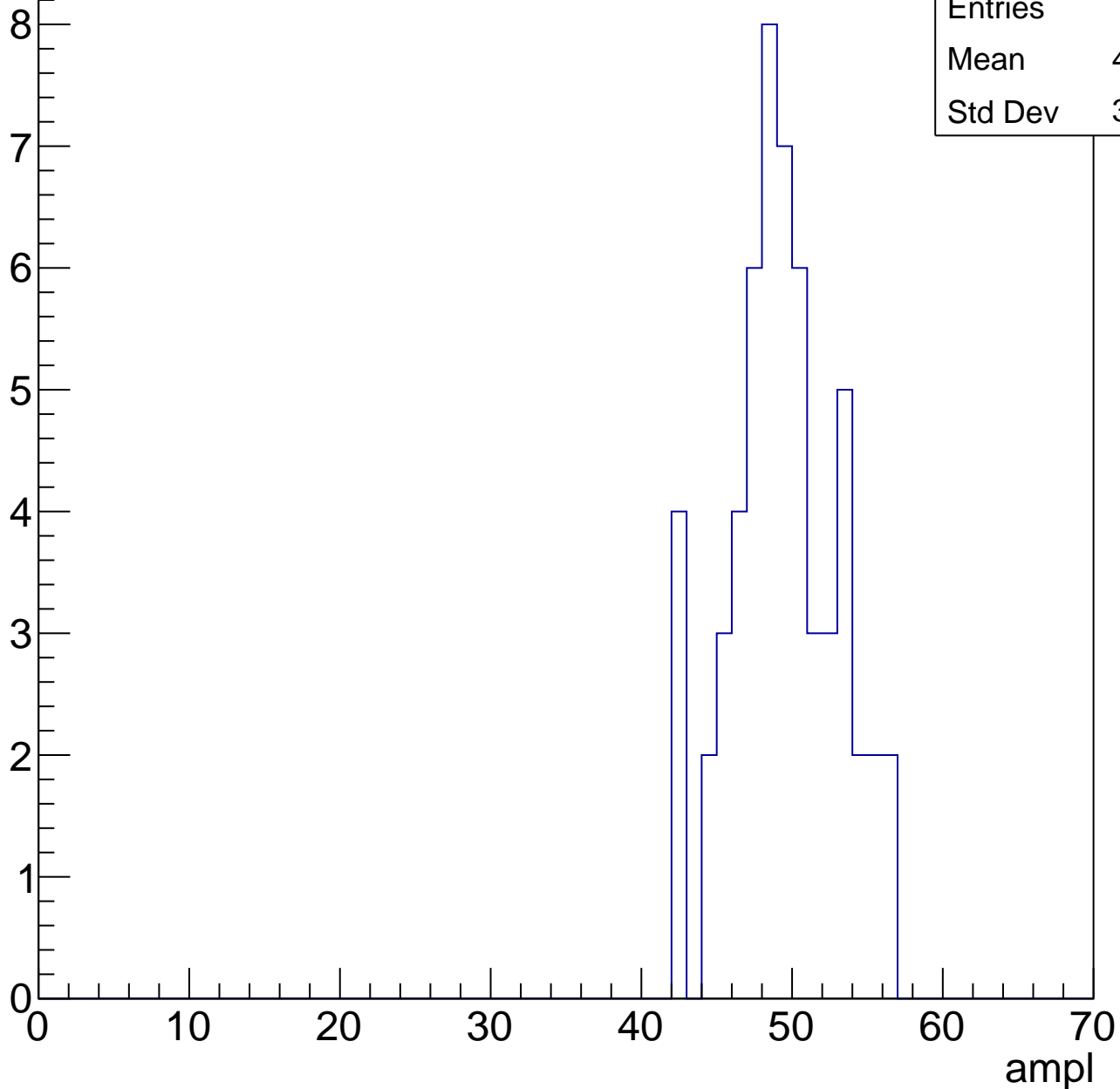


B0L000S, U2-ch1, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

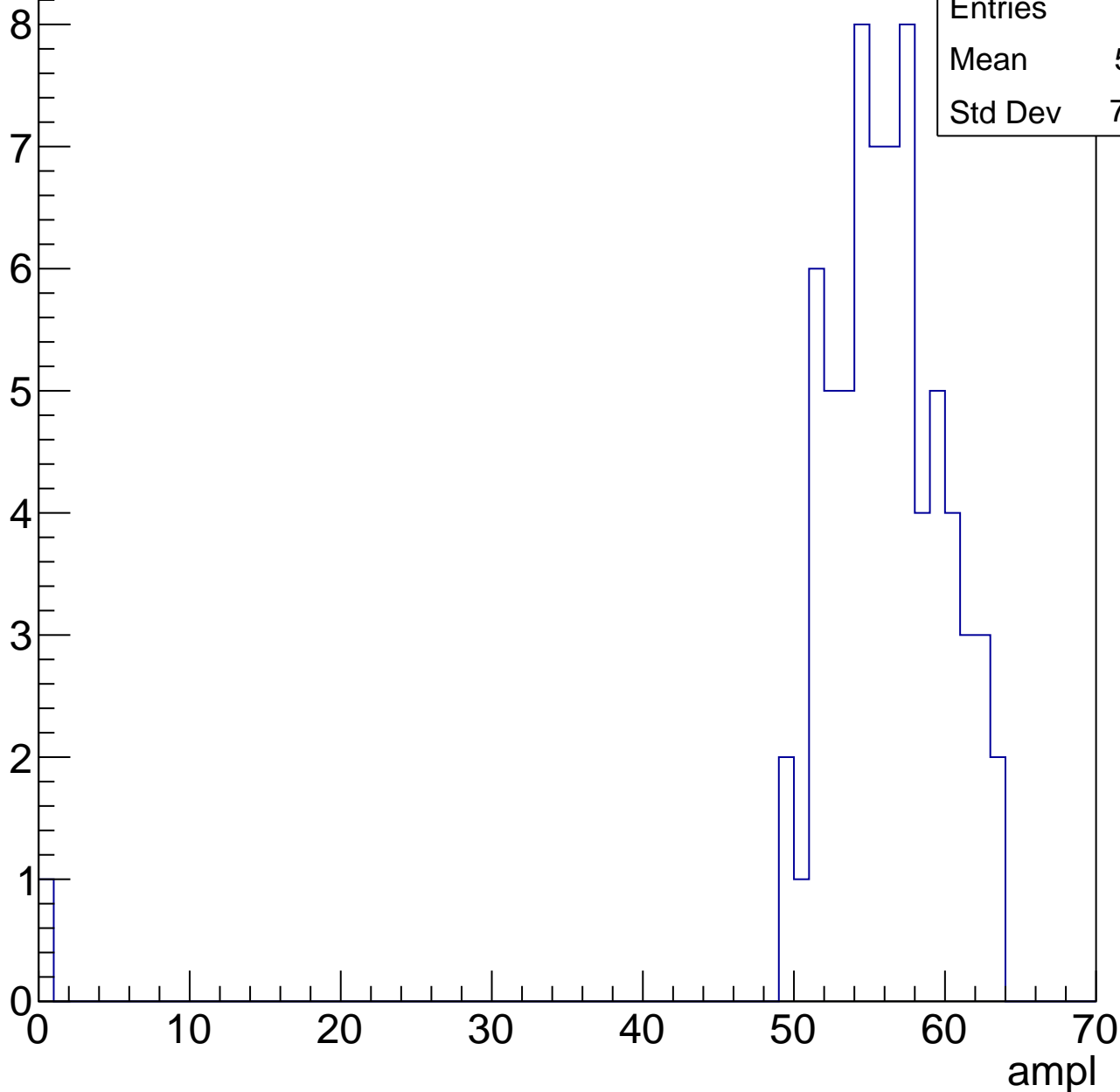
Entries	57
Mean	48.91
Std Dev	3.521



B0L000S, U2-ch1, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

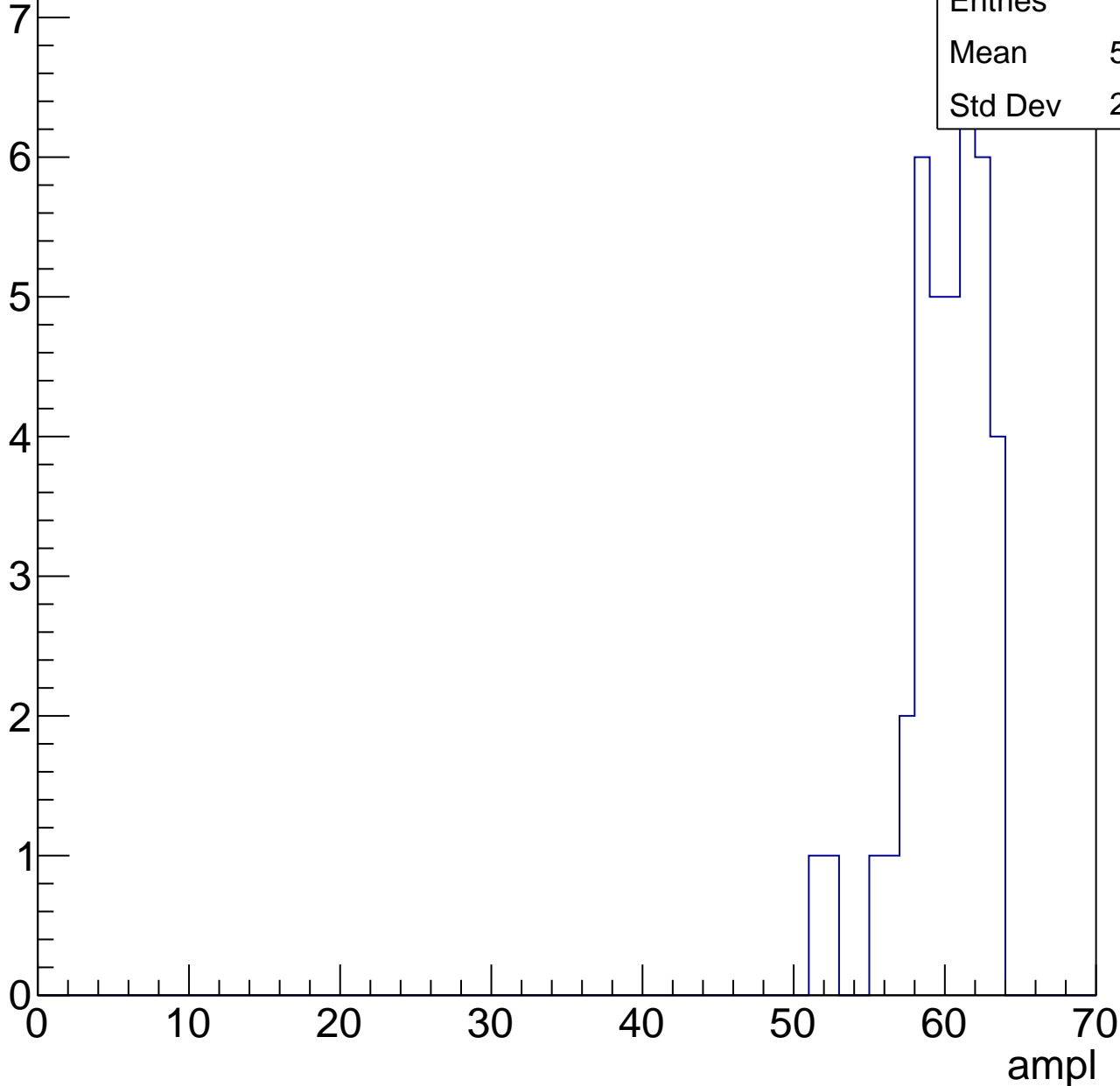


B0L000S, U2-ch1, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

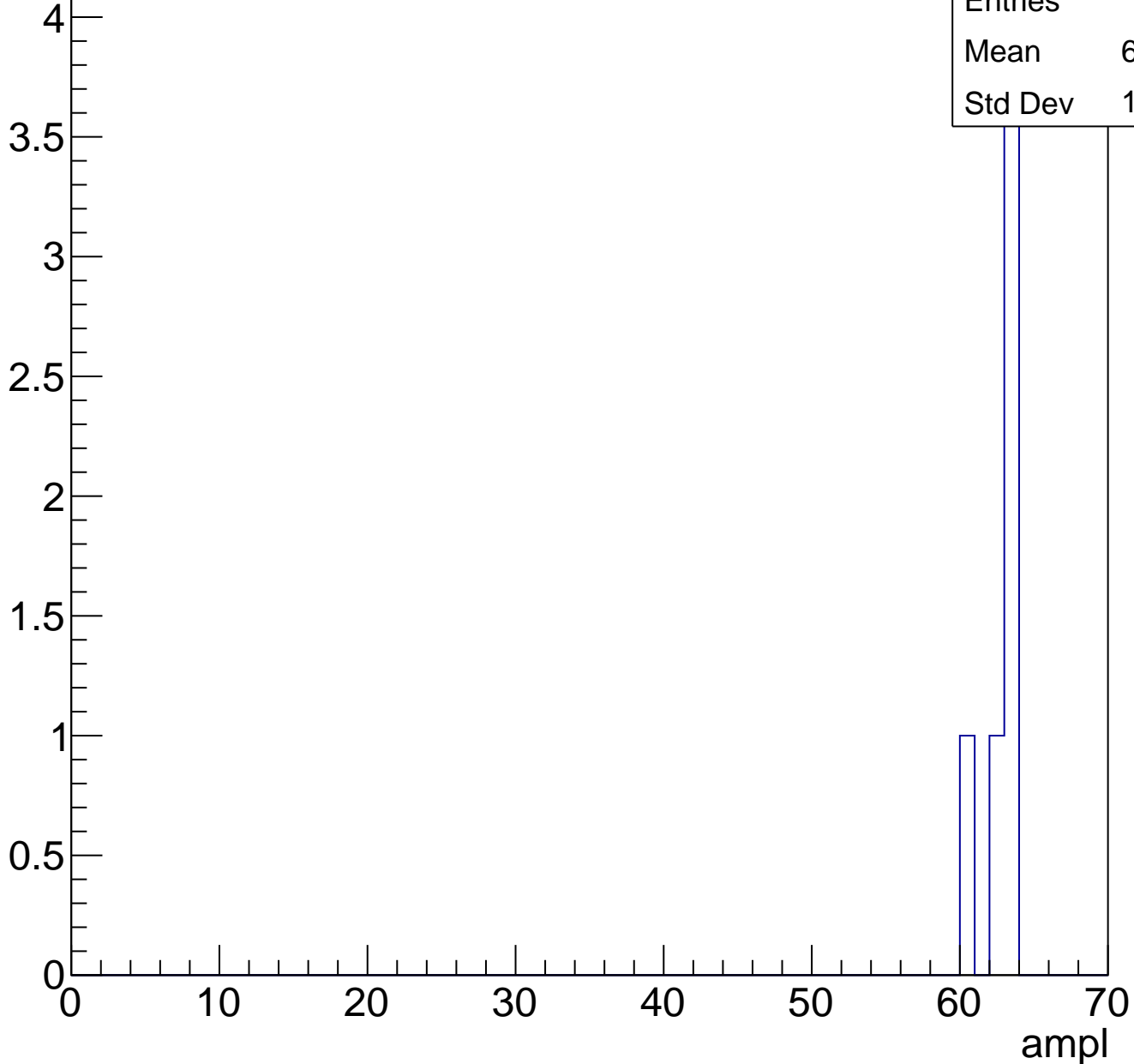
Entries	39
Mean	59.54
Std Dev	2.735



B0L000S, U2-ch1, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch1, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch2, adc0

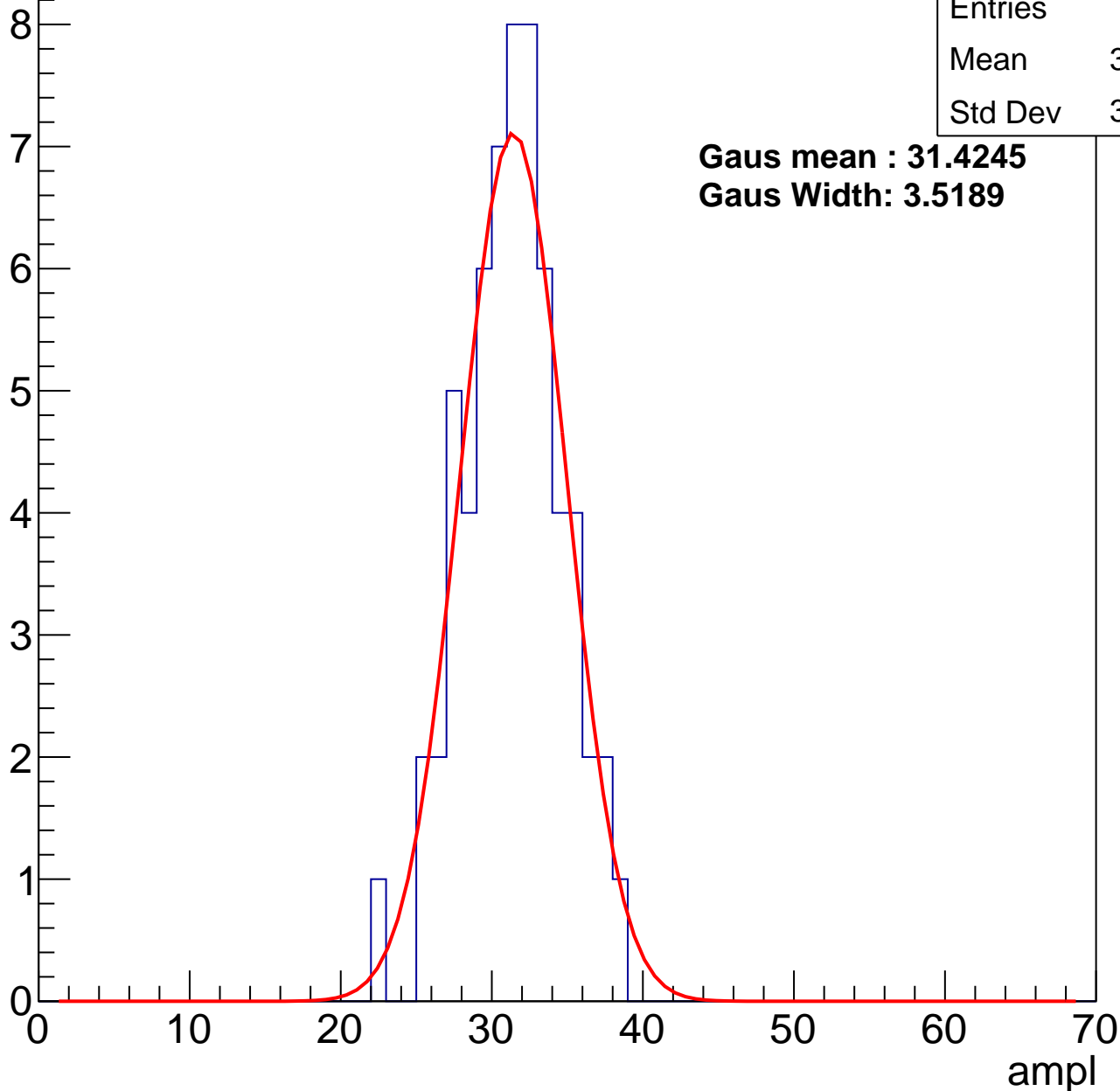
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.92
Std Dev	3.244

Gaus mean : 31.4245

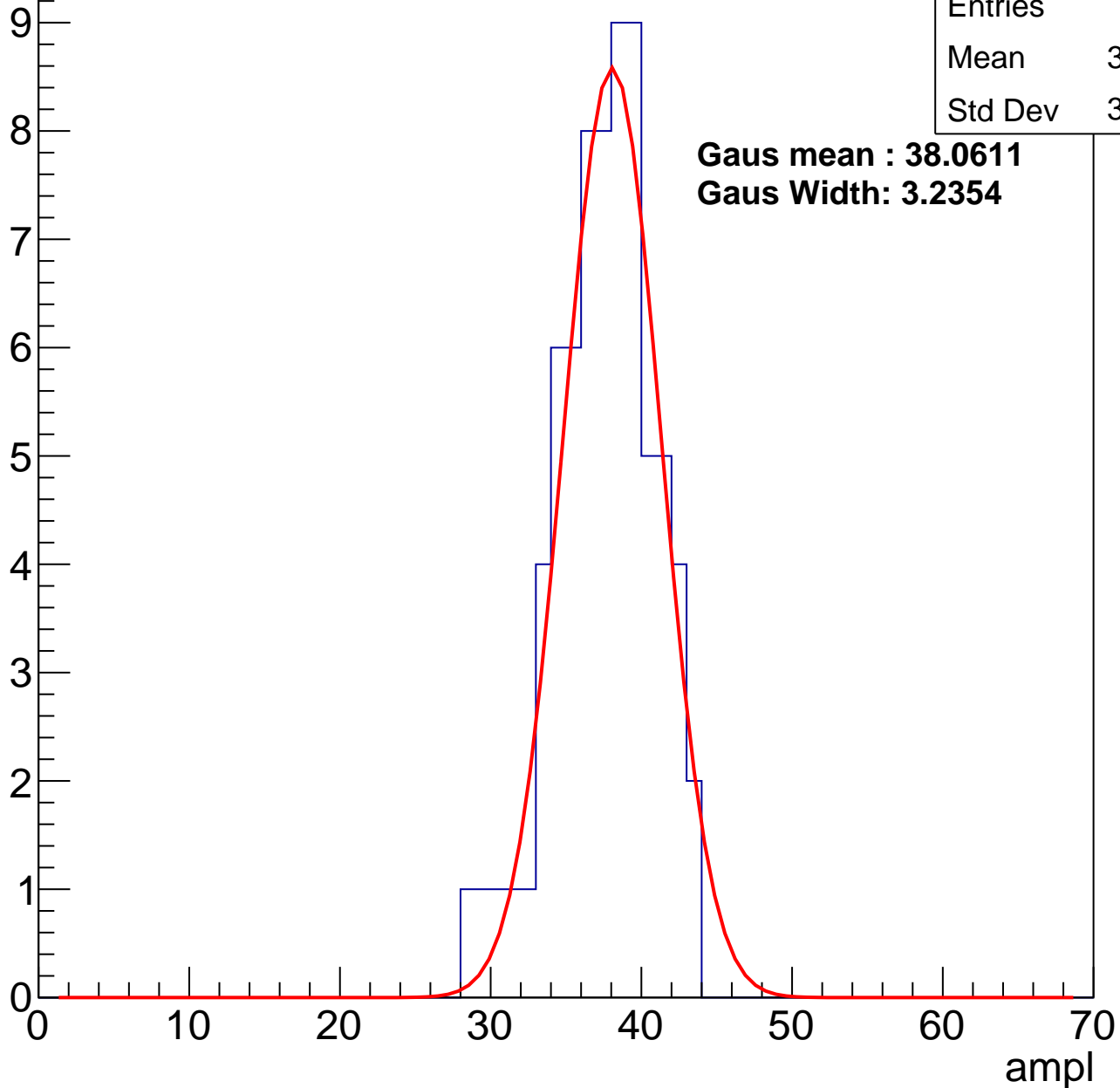
Gaus Width: 3.5189



B0L000S, U2-ch2, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch2, adc2

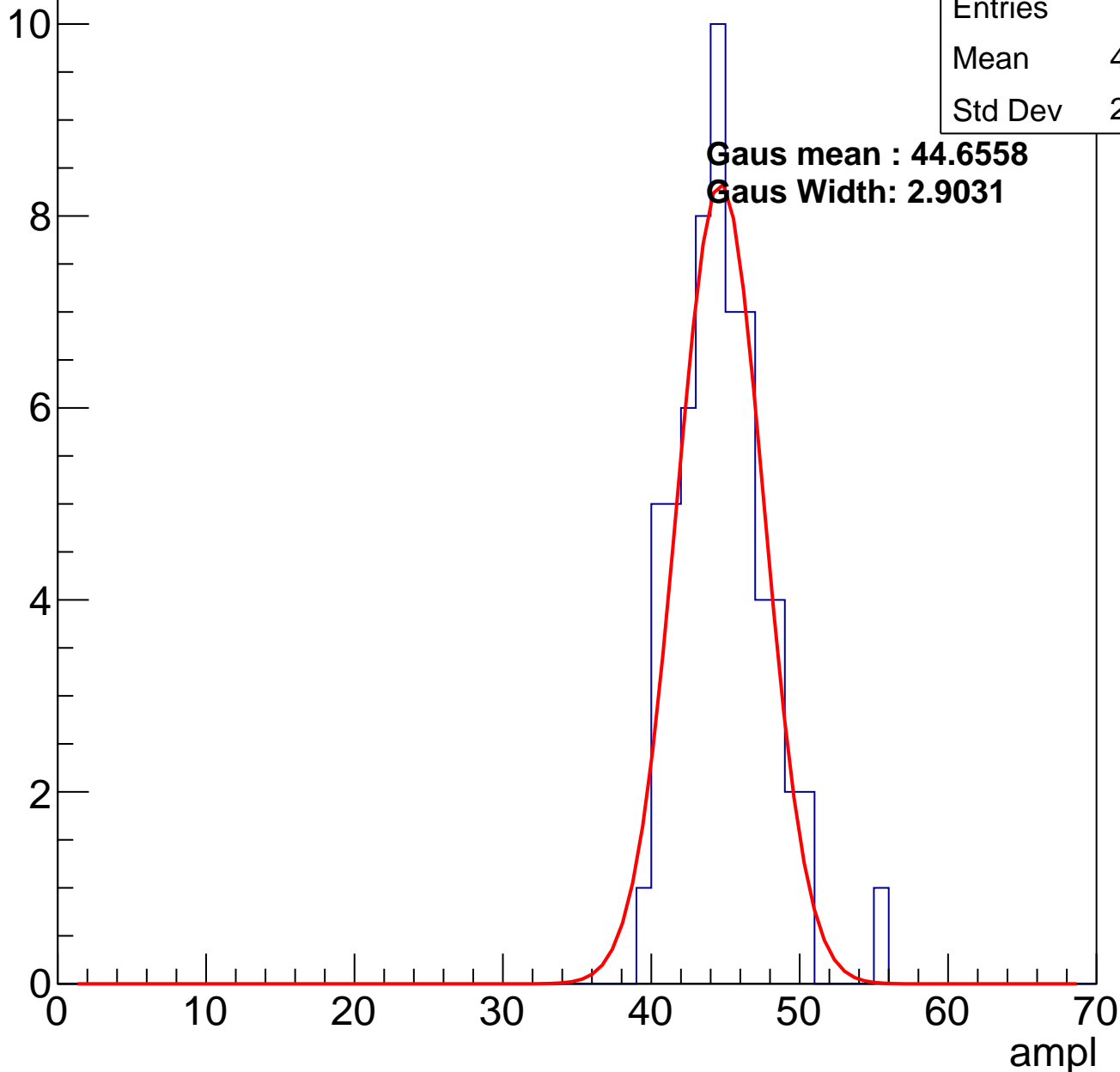
calib_packv5_042523_0143.root, FC#5, port B1

Entries	62
Mean	44.35
Std Dev	2.984

Gaus mean : 44.6558

Gaus Width: 2.9031

Entry

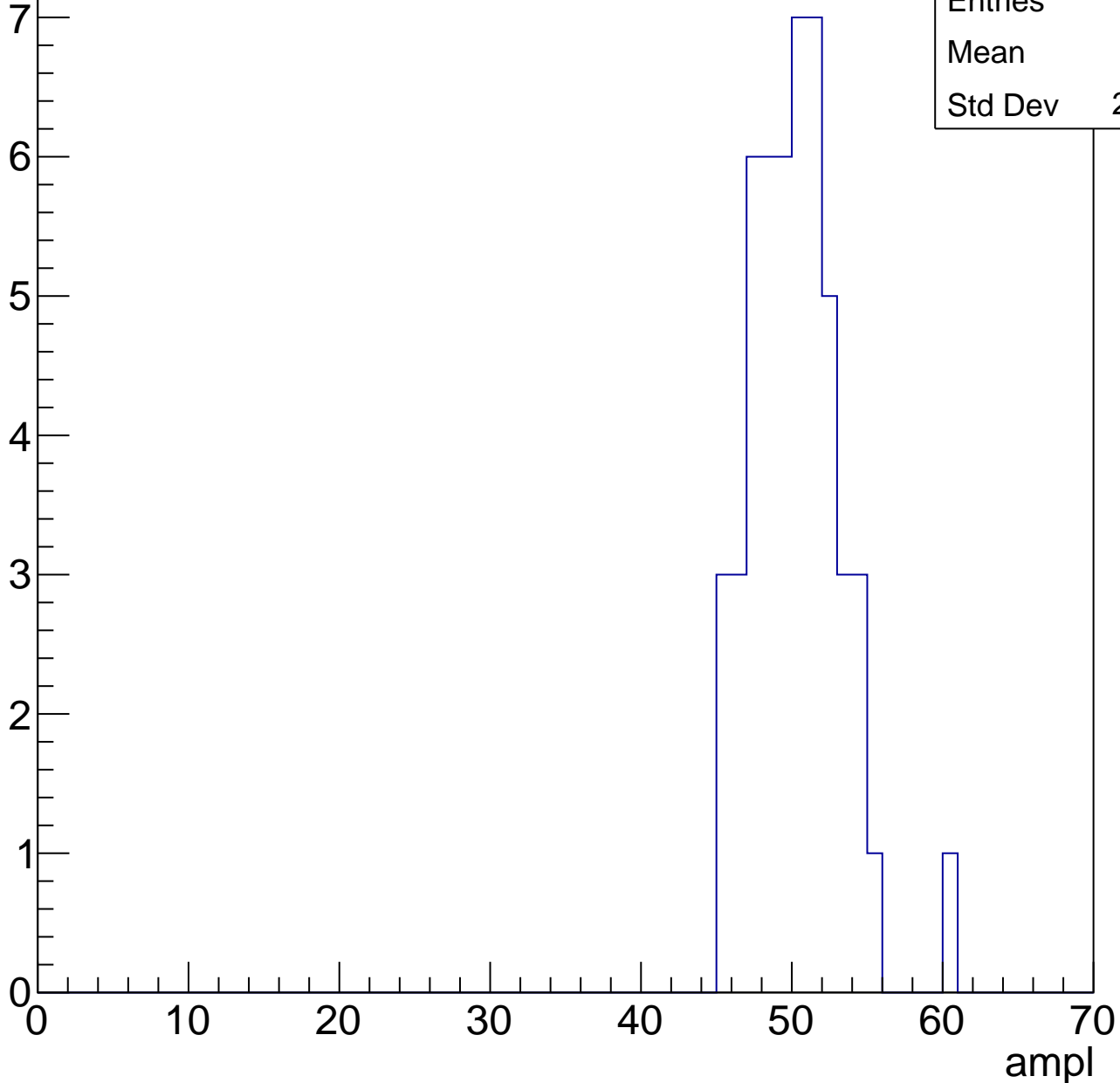


B0L000S, U2-ch2, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	49.8
Std Dev	2.911

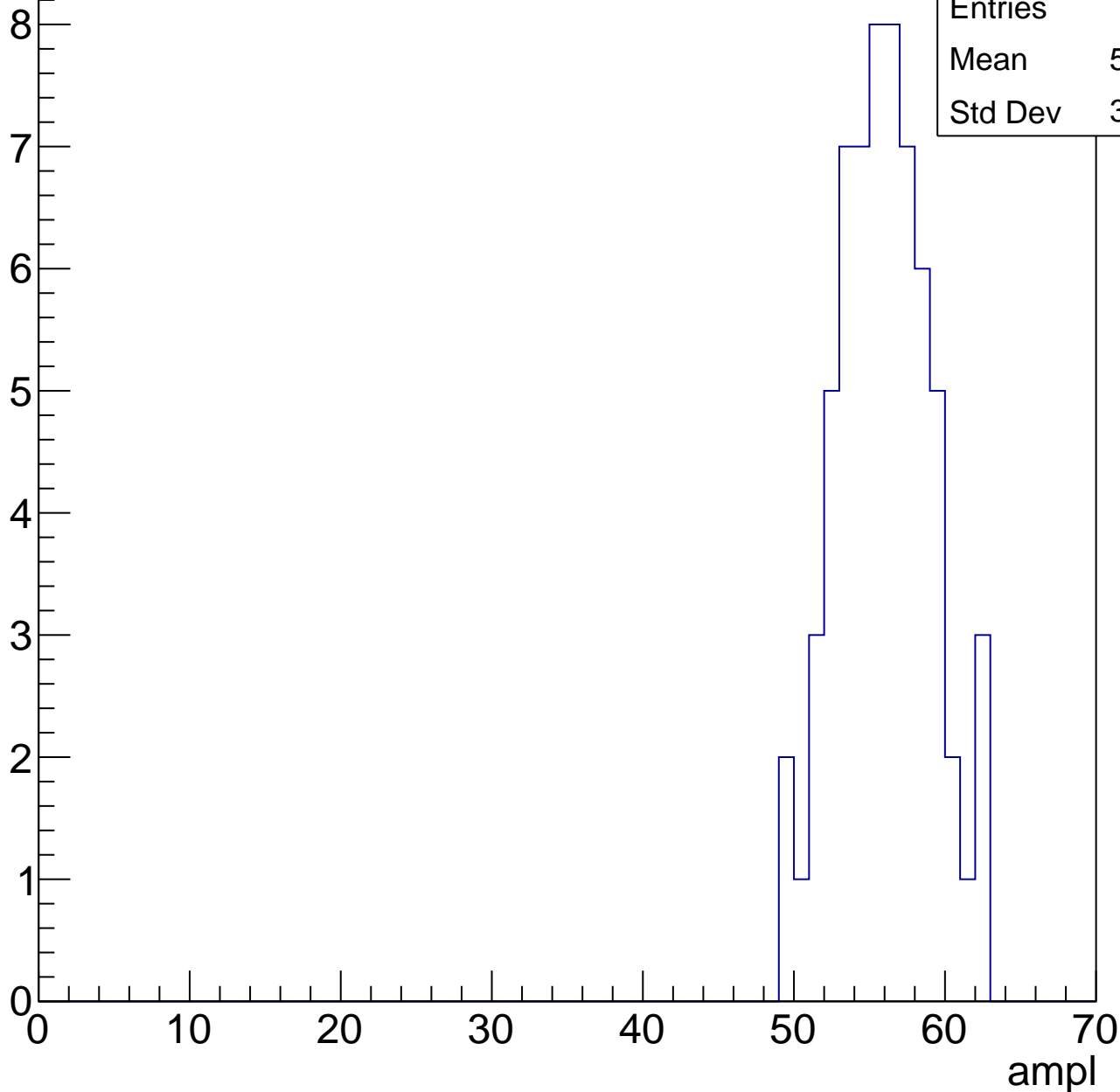


B0L000S, U2-ch2, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

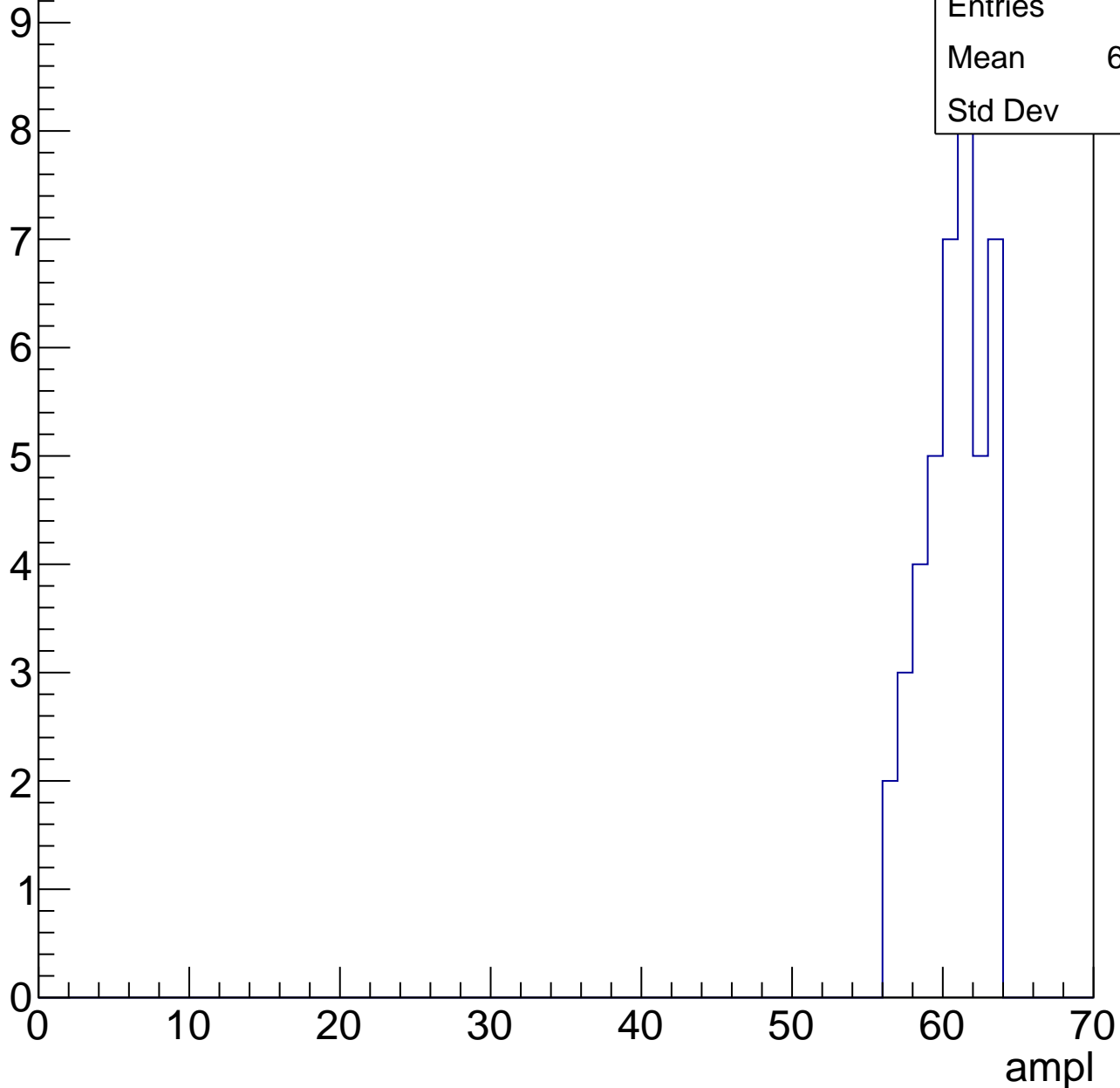
Entries	65
Mean	55.49
Std Dev	3.069



B0L000S, U2-ch2, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch2, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch2, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch3, adc0

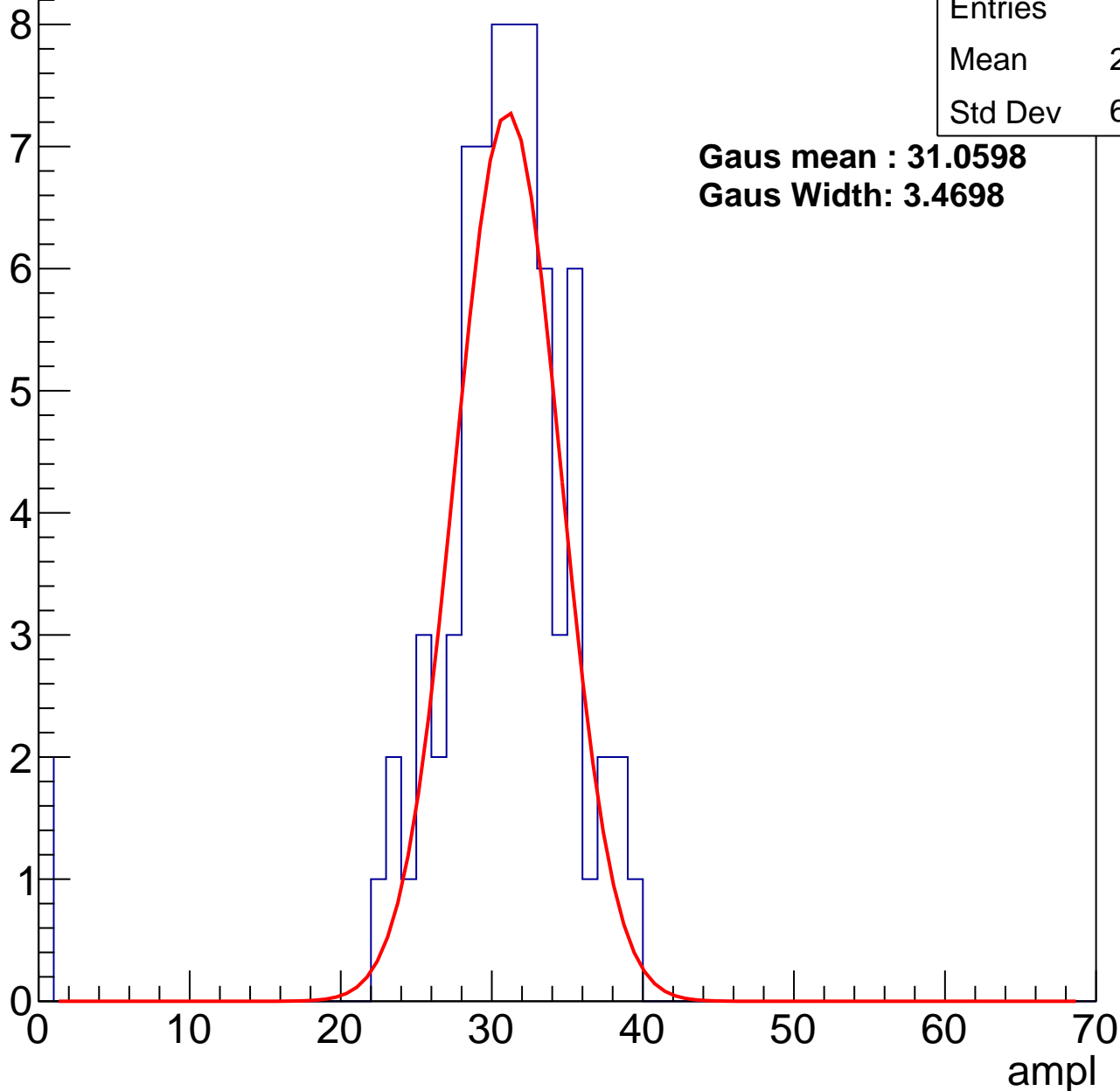
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.84
Std Dev	6.193

Gaus mean : 31.0598

Gaus Width: 3.4698

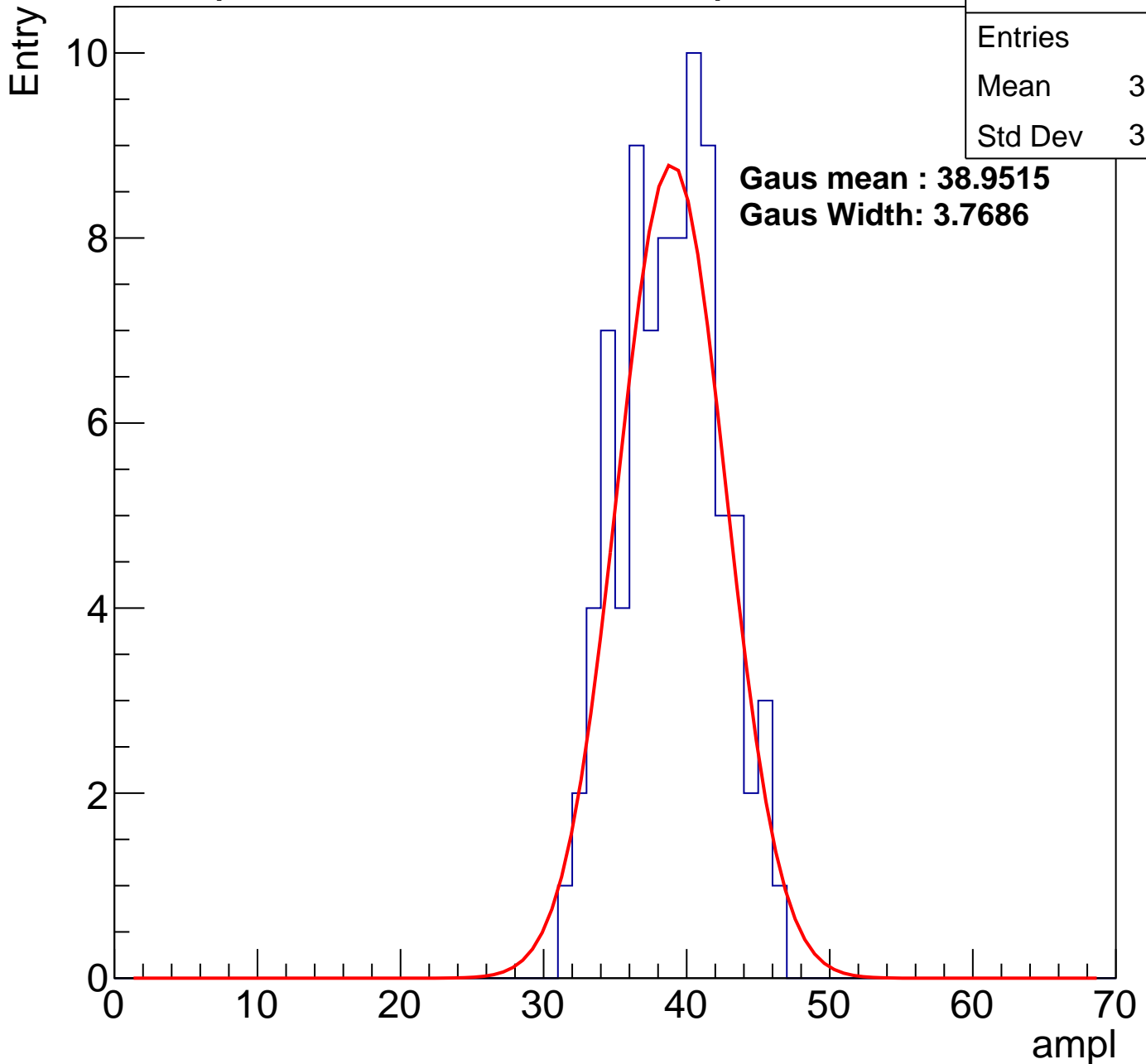


B0L000S, U2-ch3, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	85
Mean	38.44
Std Dev	3.452

Gaus mean : 38.9515
Gaus Width: 3.7686

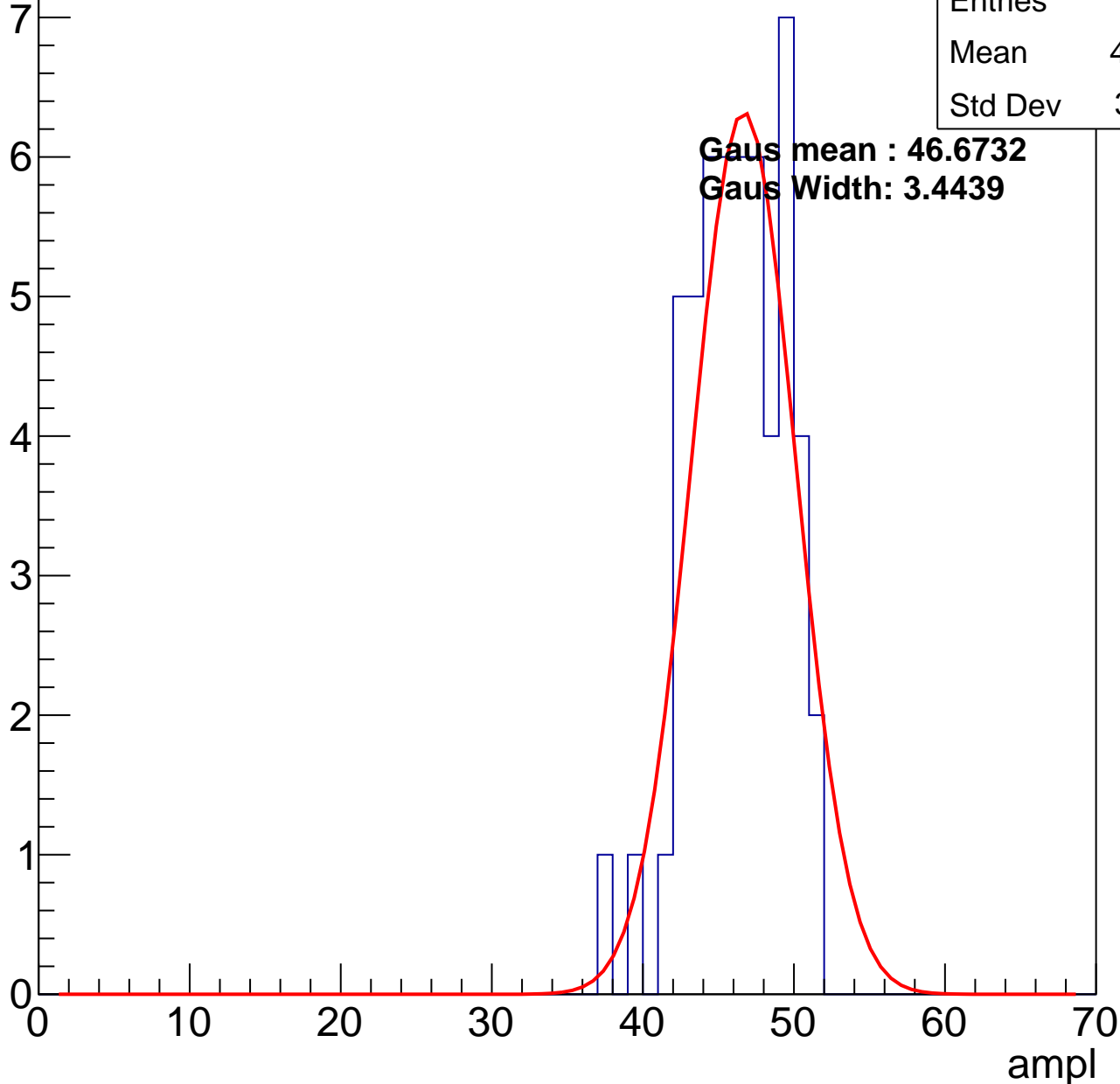


B0L000S, U2-ch3, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	45.76
Std Dev	3.061

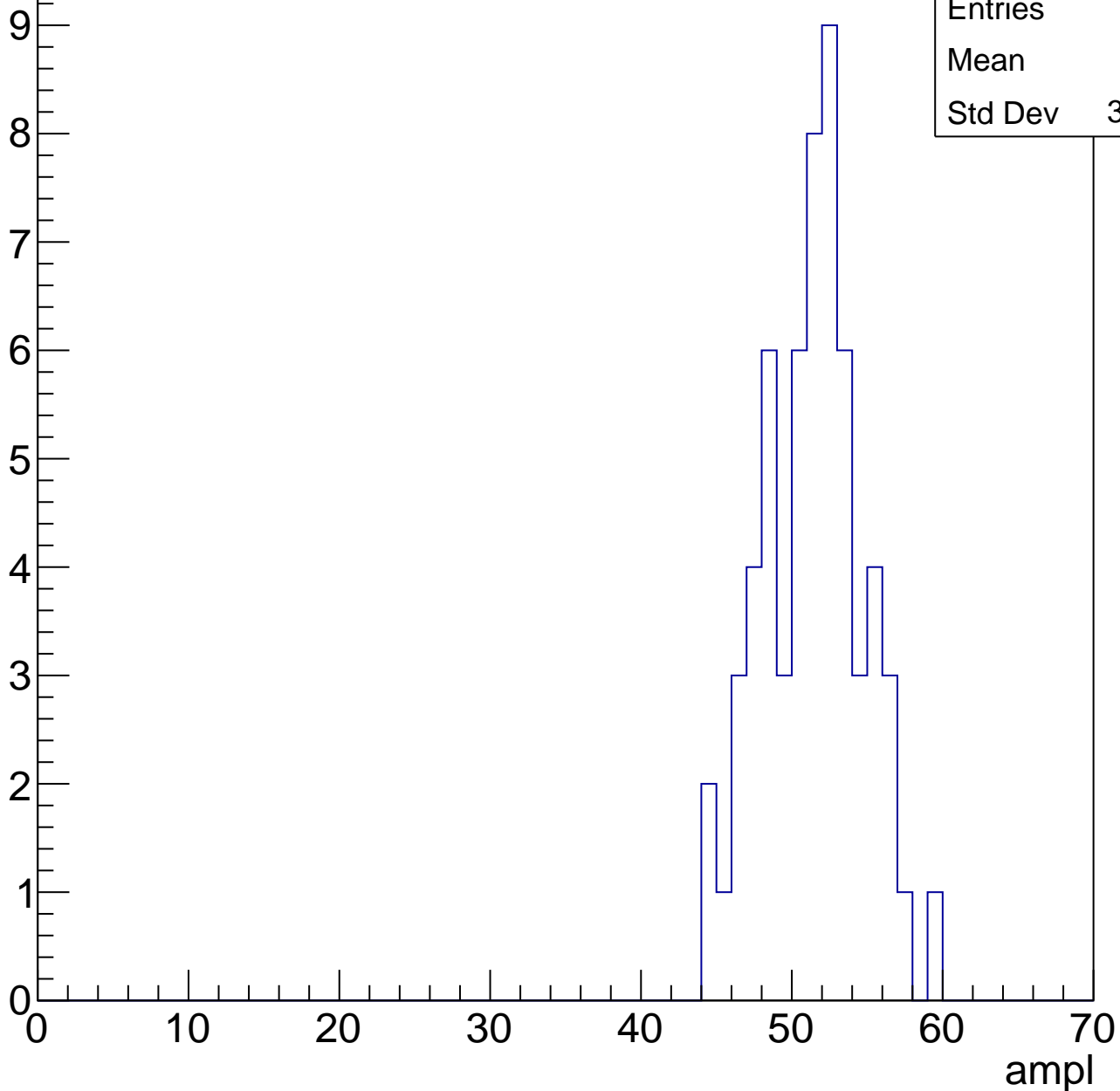


B0L000S, U2-ch3, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.9
Std Dev	3.275

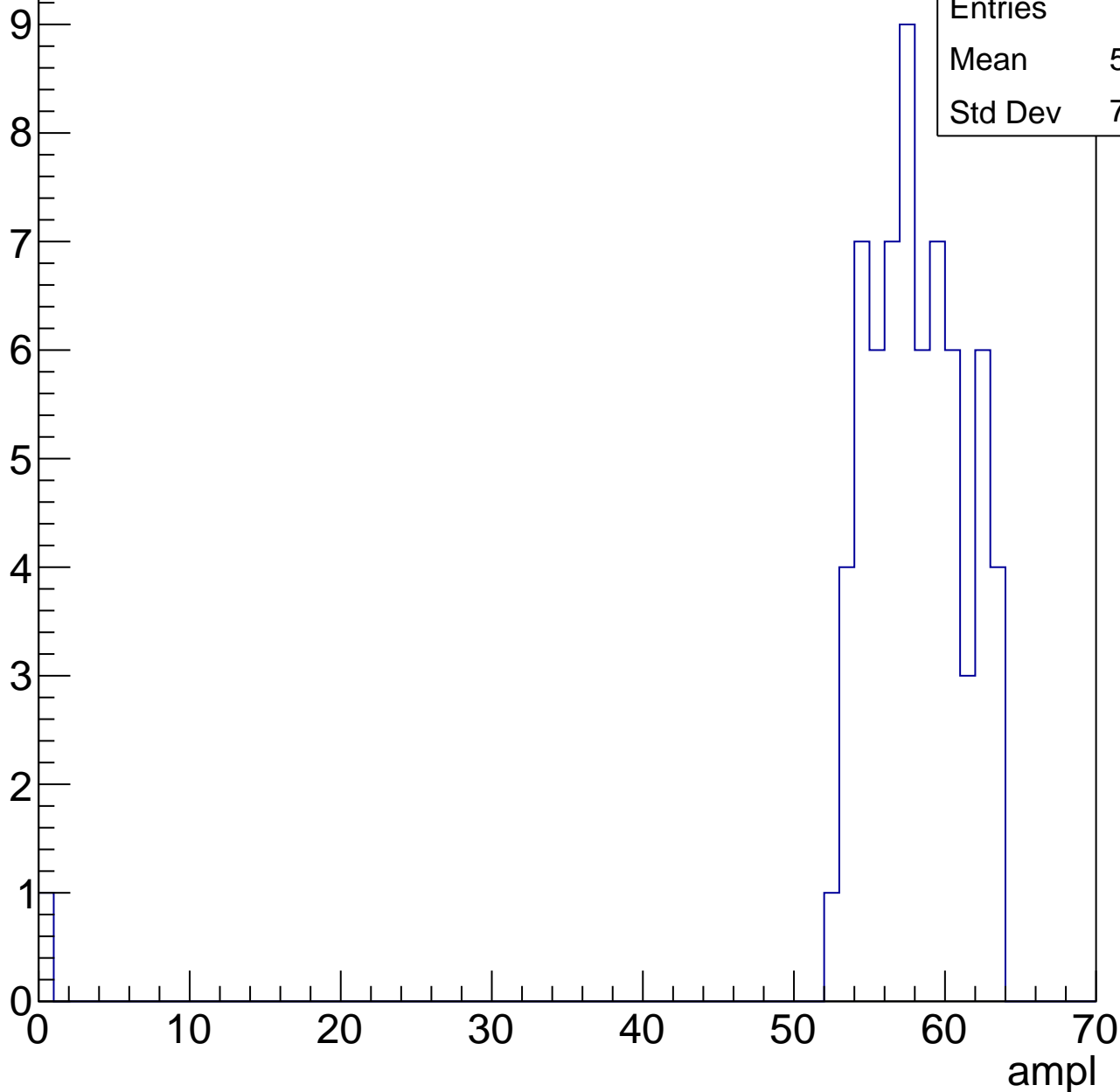


B0L000S, U2-ch3, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	56.79
Std Dev	7.589

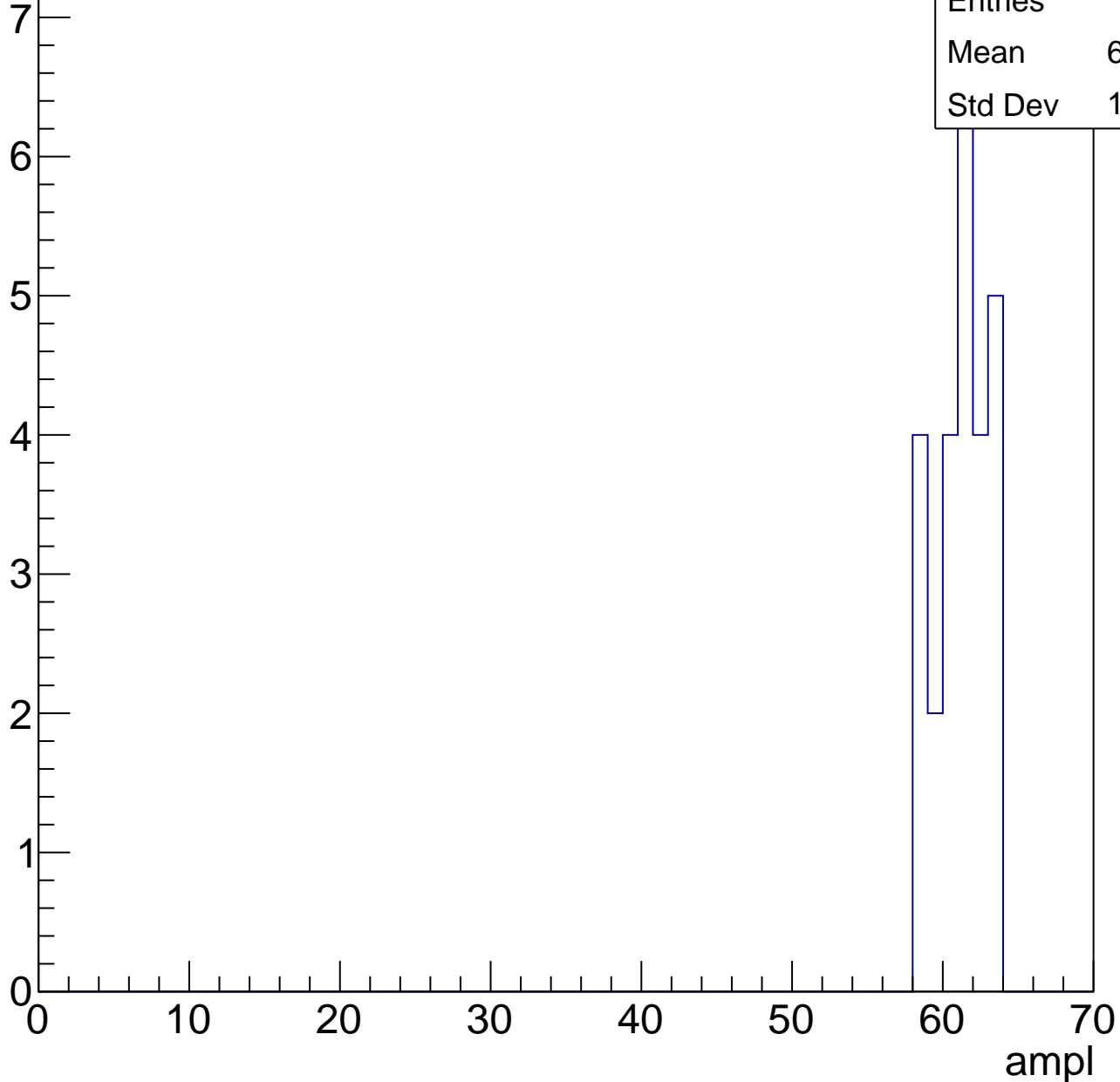


B0L000S, U2-ch3, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	26
Mean	60.77
Std Dev	1.648



B0L000S, U2-ch3, adc6

calib_packv5_042523_0143.root, FC#5, port B1

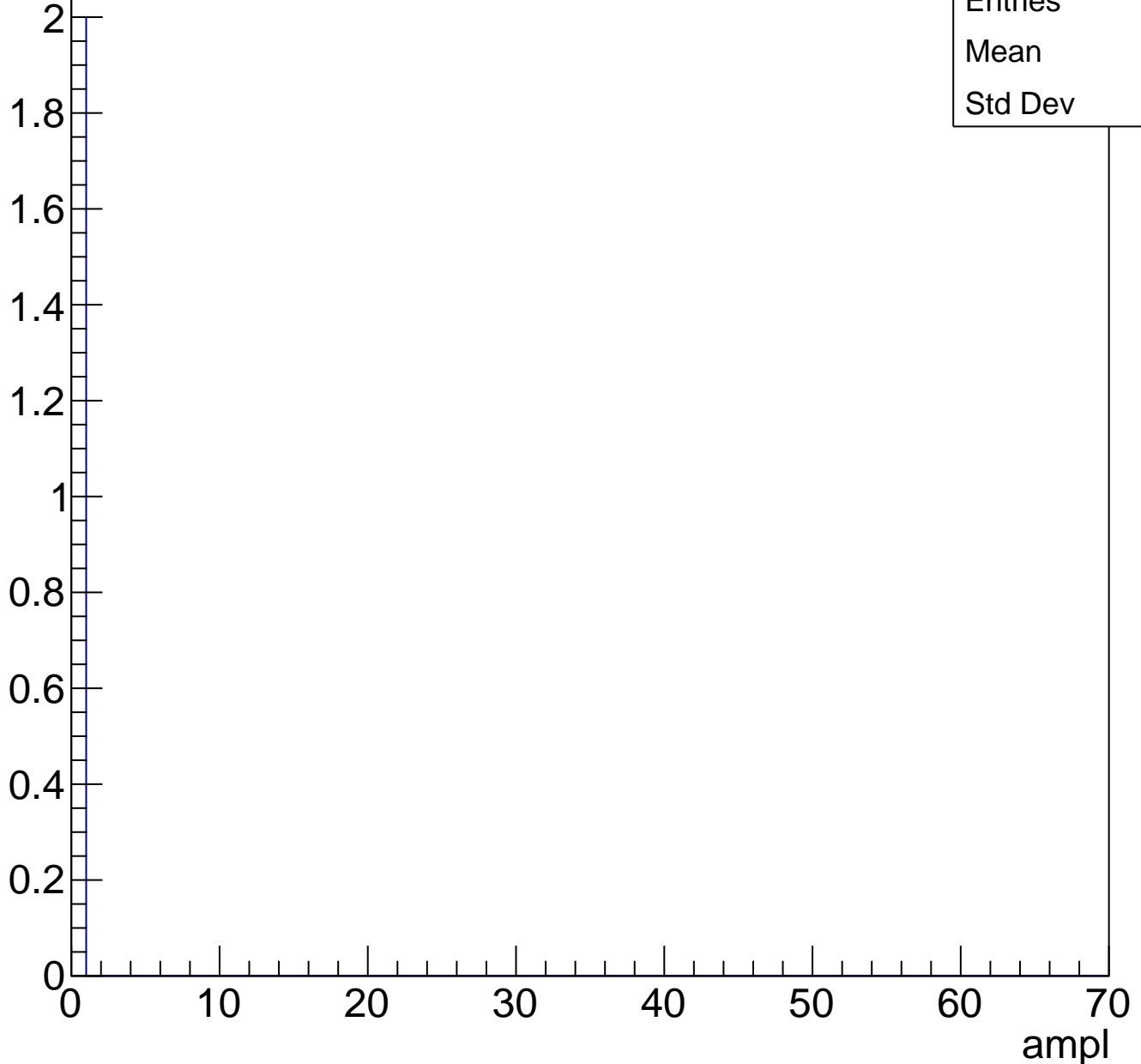
Entry



B0L000S, U2-ch3, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch4, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	77
Mean	30.14
Std Dev	3.226

Gaus mean : 30.5291

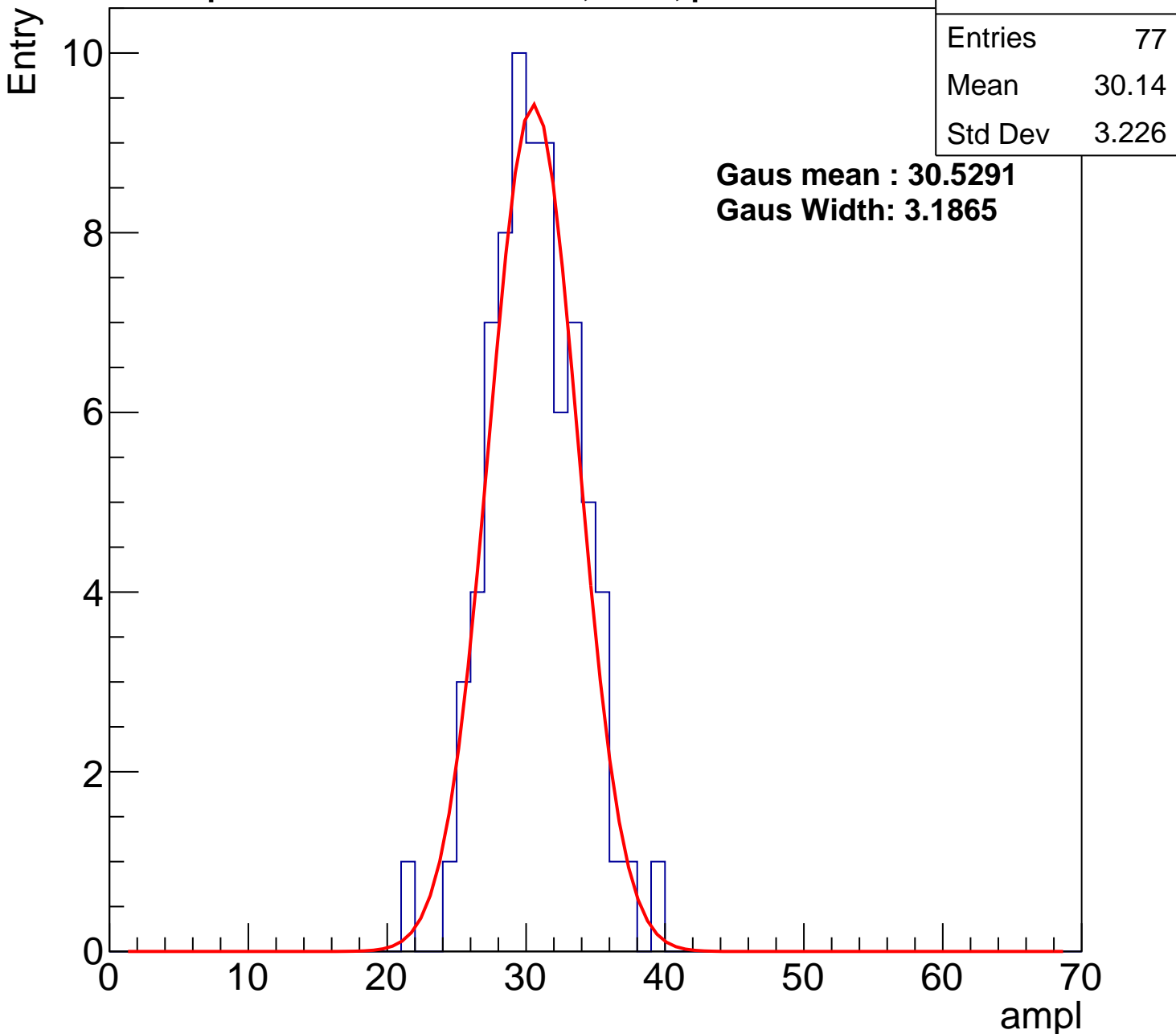
Gaus Width: 3.1865

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch4, adc1

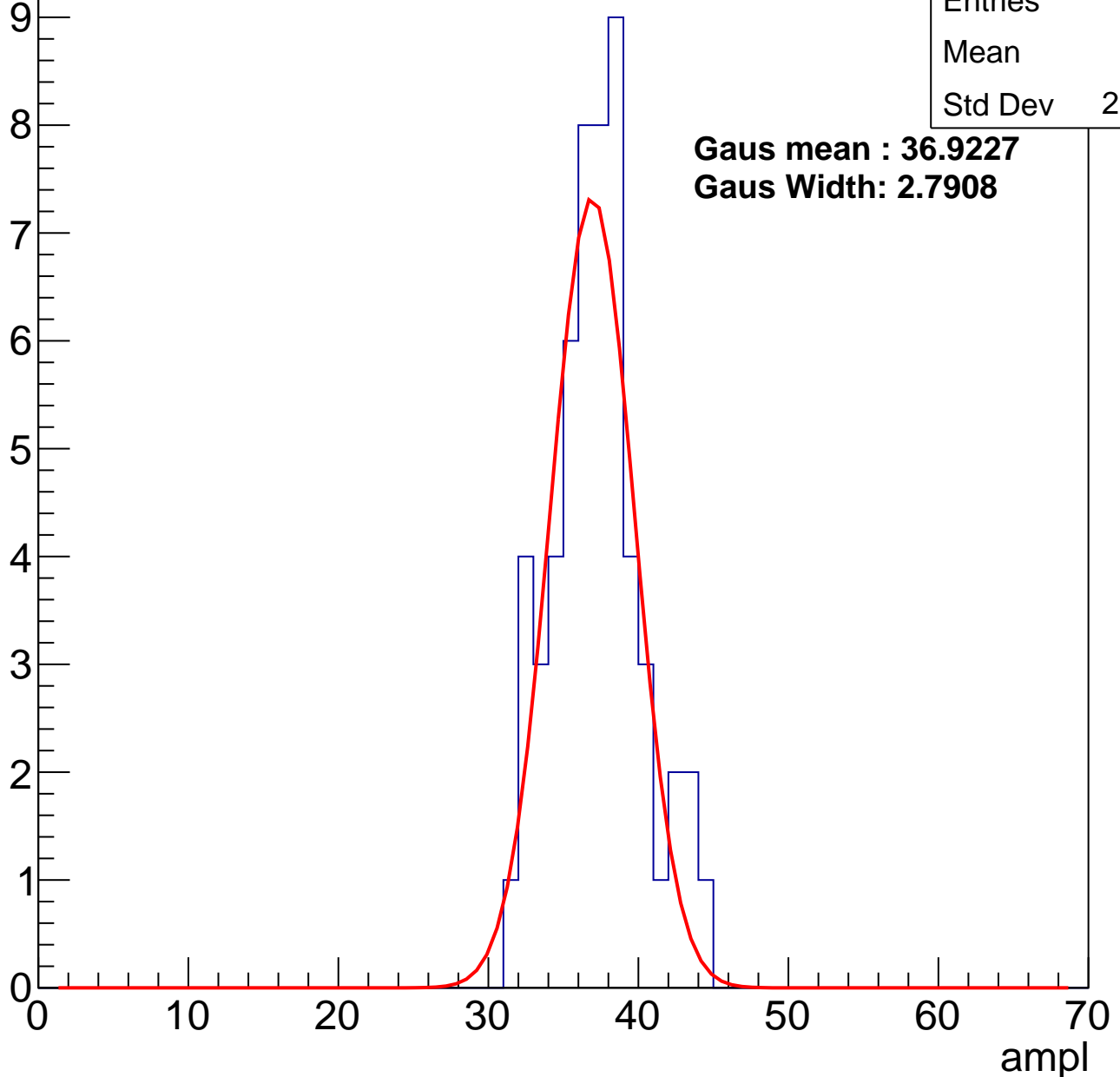
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.8
Std Dev	2.955

Gaus mean : 36.9227

Gaus Width: 2.7908



B0L000S, U2-ch4, adc2

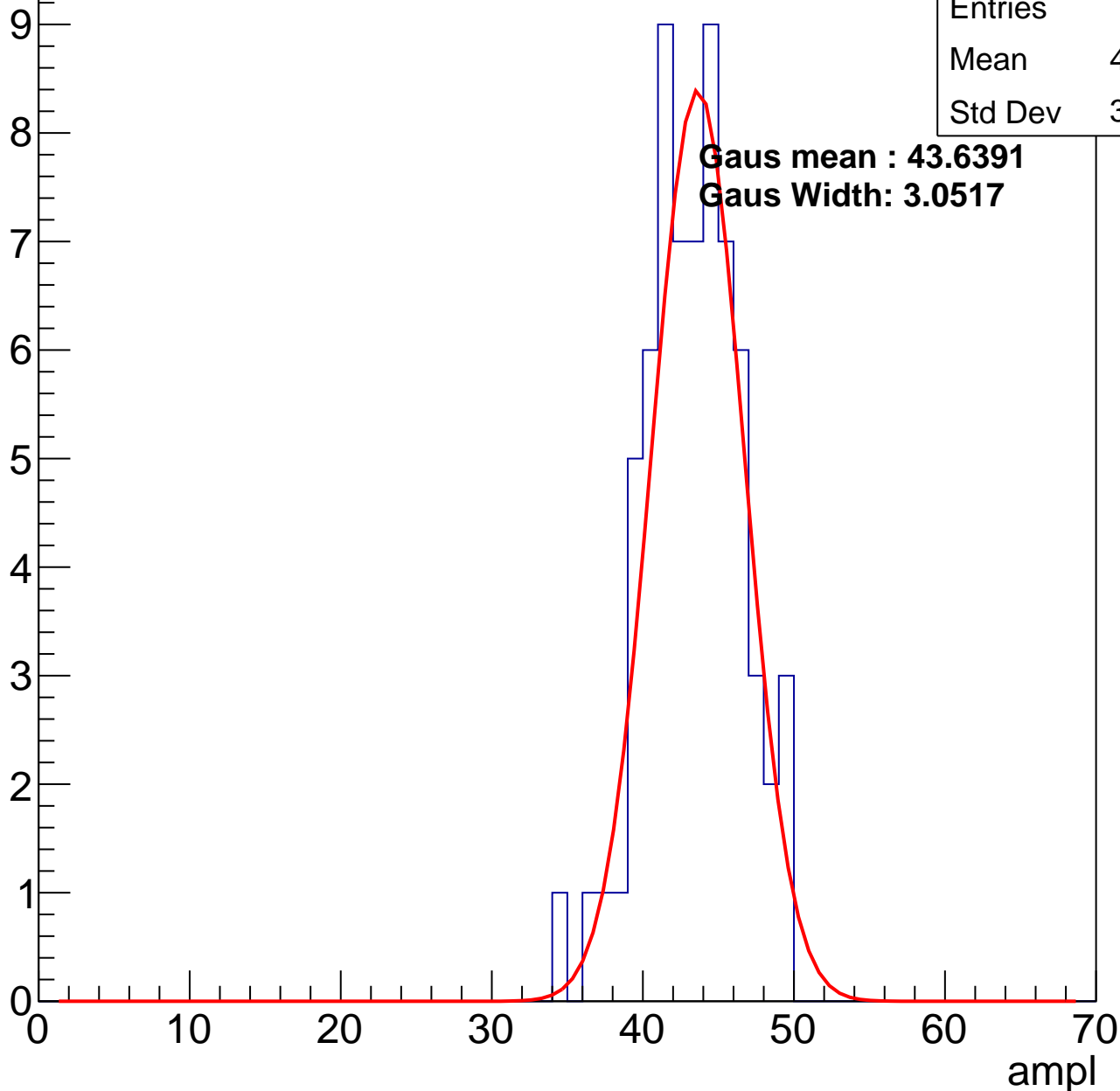
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.87
Std Dev	3.124

Gaus mean : 43.6391

Gaus Width: 3.0517

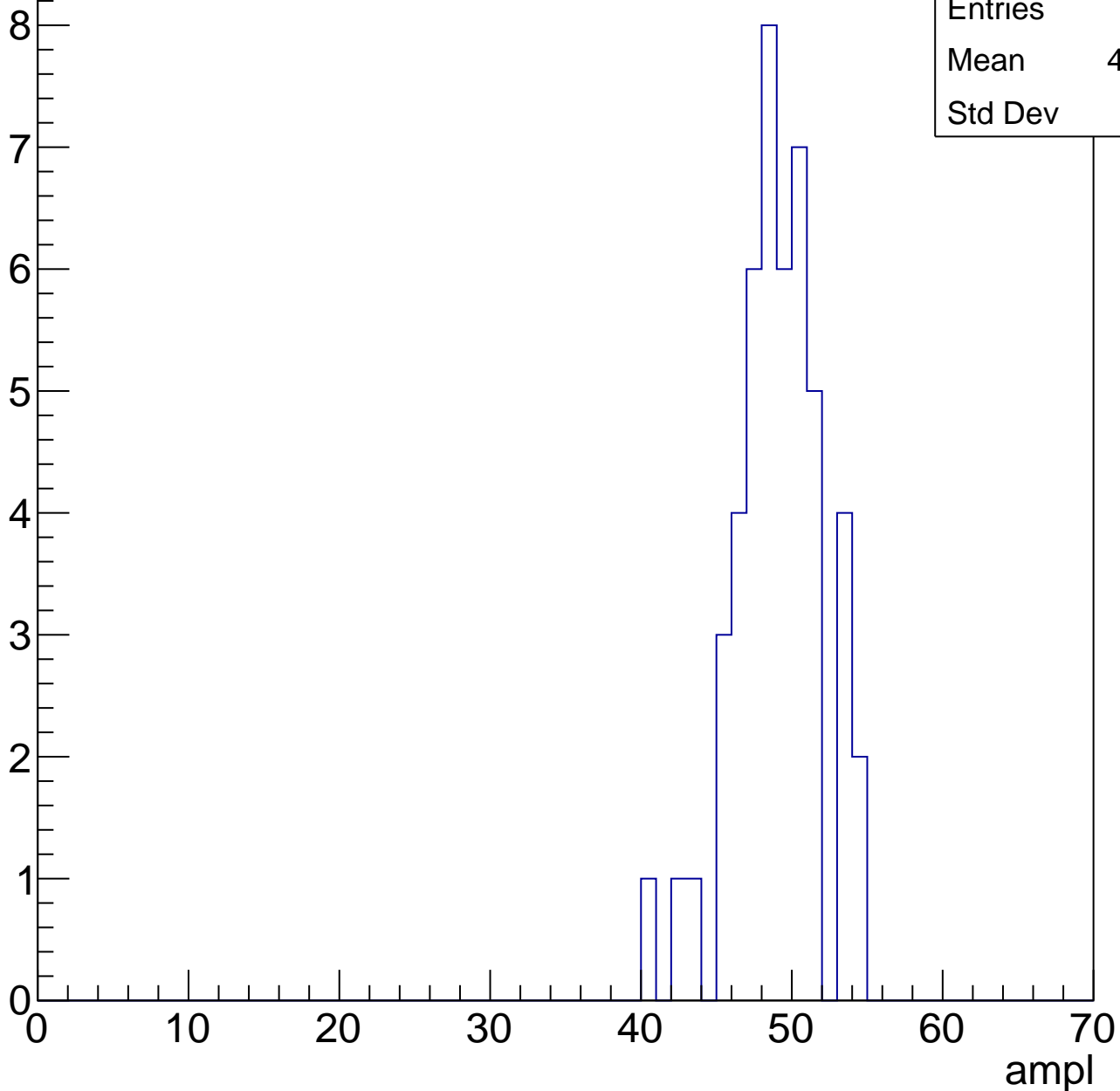


B0L000S, U2-ch4, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

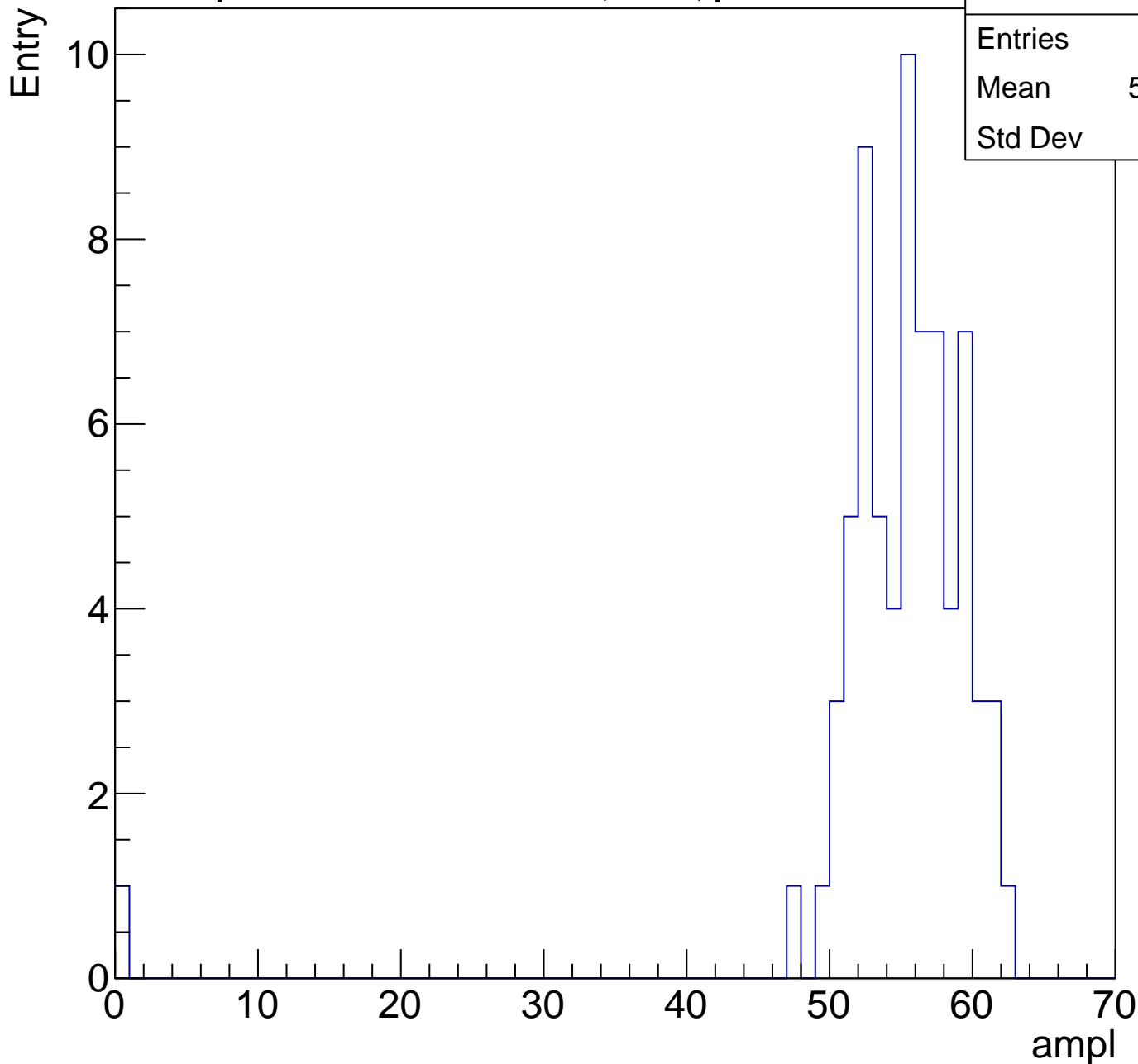
Entries	48
Mean	48.52
Std Dev	2.93



B0L000S, U2-ch4, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	54.38
Std Dev	7.29



B0L000S, U2-ch4, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	59.26
Std Dev	3.014

ampl

0

10

20

30

40

50

60

70

B0L000S, U2-ch4, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch4, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch5, adc0

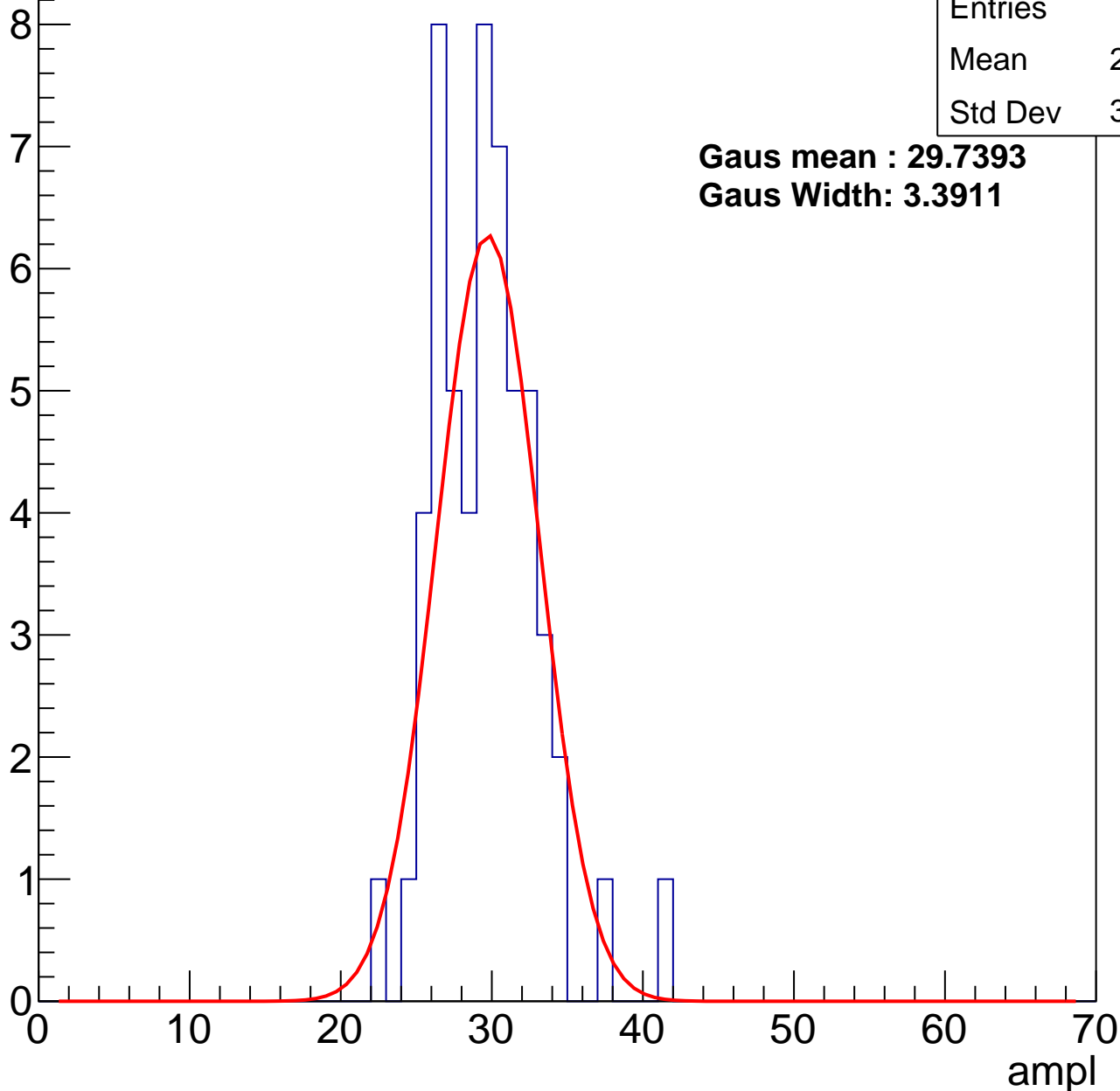
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	29.15
Std Dev	3.333

Gaus mean : 29.7393

Gaus Width: 3.3911



B0L000S, U2-ch5, adc1

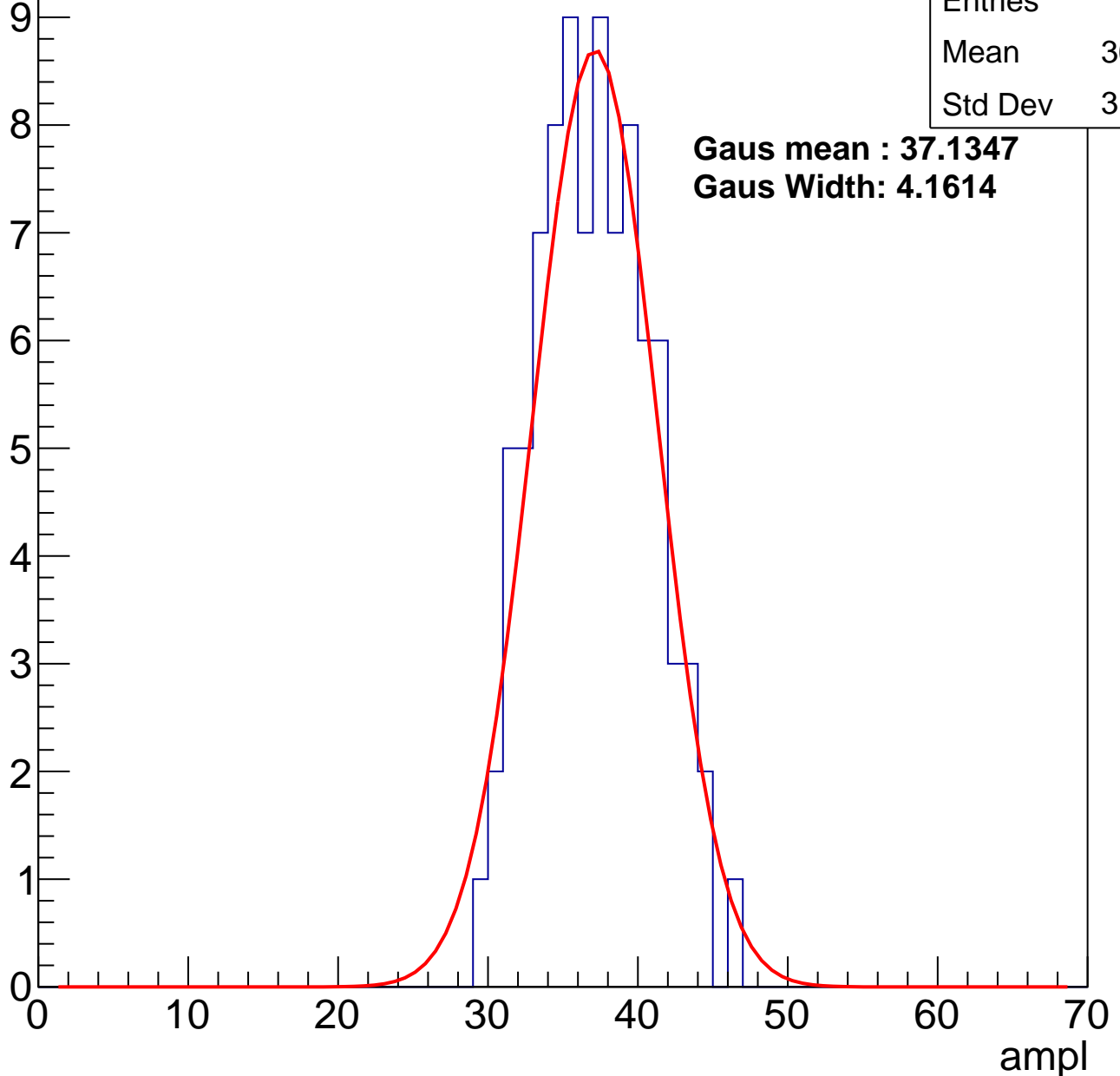
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	89
Mean	36.63
Std Dev	3.722

Gaus mean : 37.1347

Gaus Width: 4.1614



B0L000S, U2-ch5, adc2

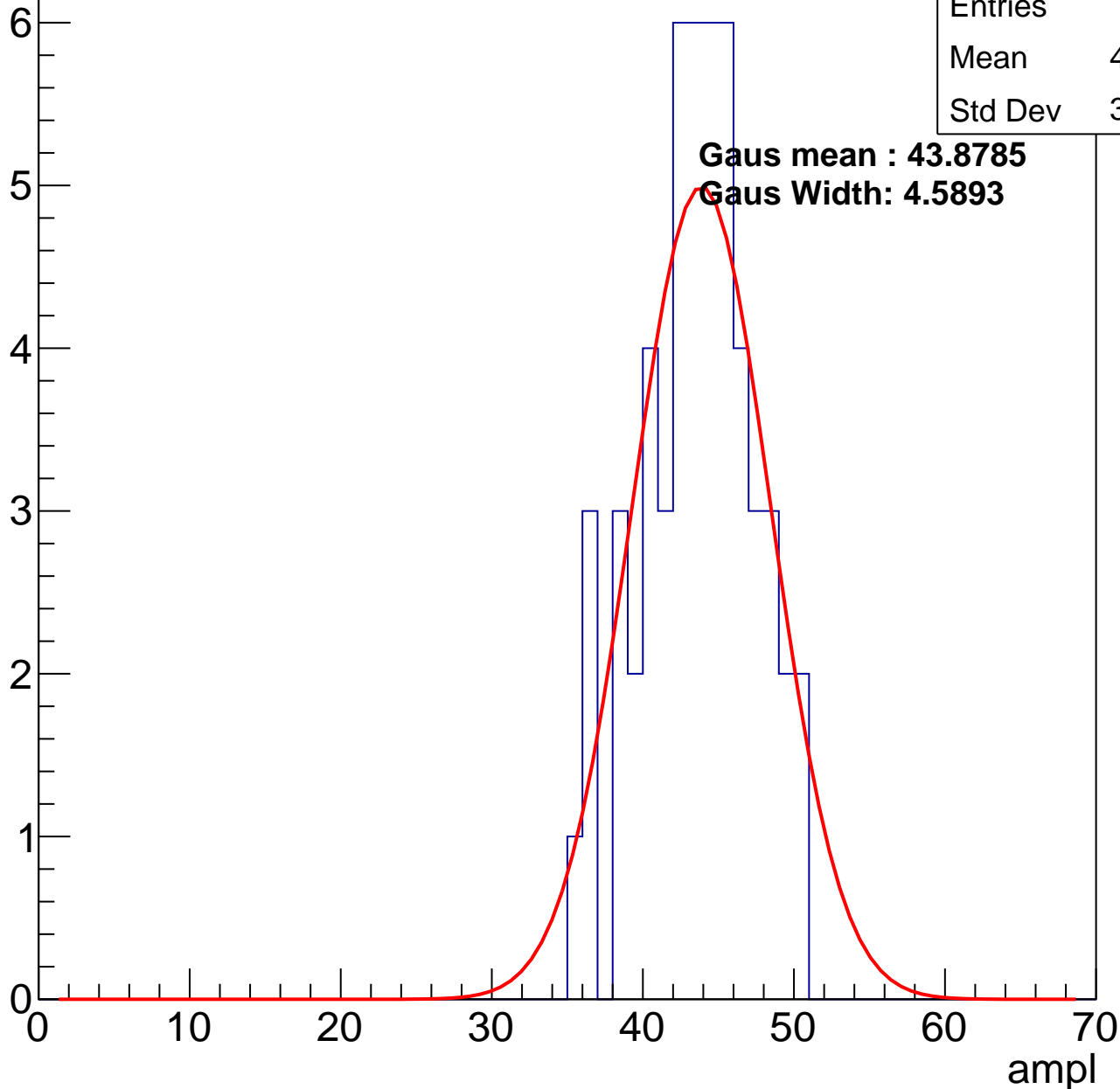
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	43.13
Std Dev	3.677

Gaus mean : 43.8785

Gaus Width: 4.5893

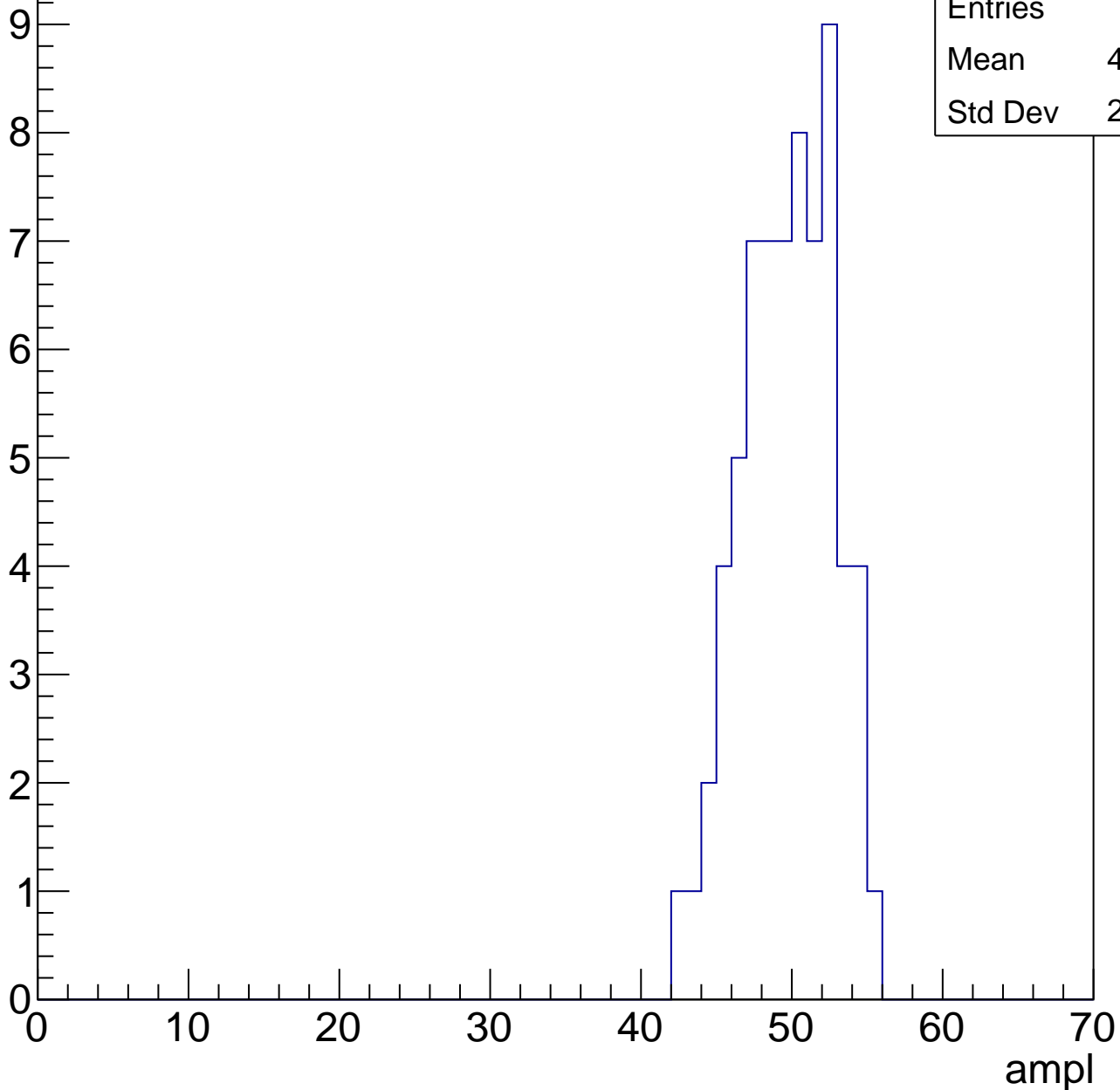


B0L000S, U2-ch5, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.24
Std Dev	2.973

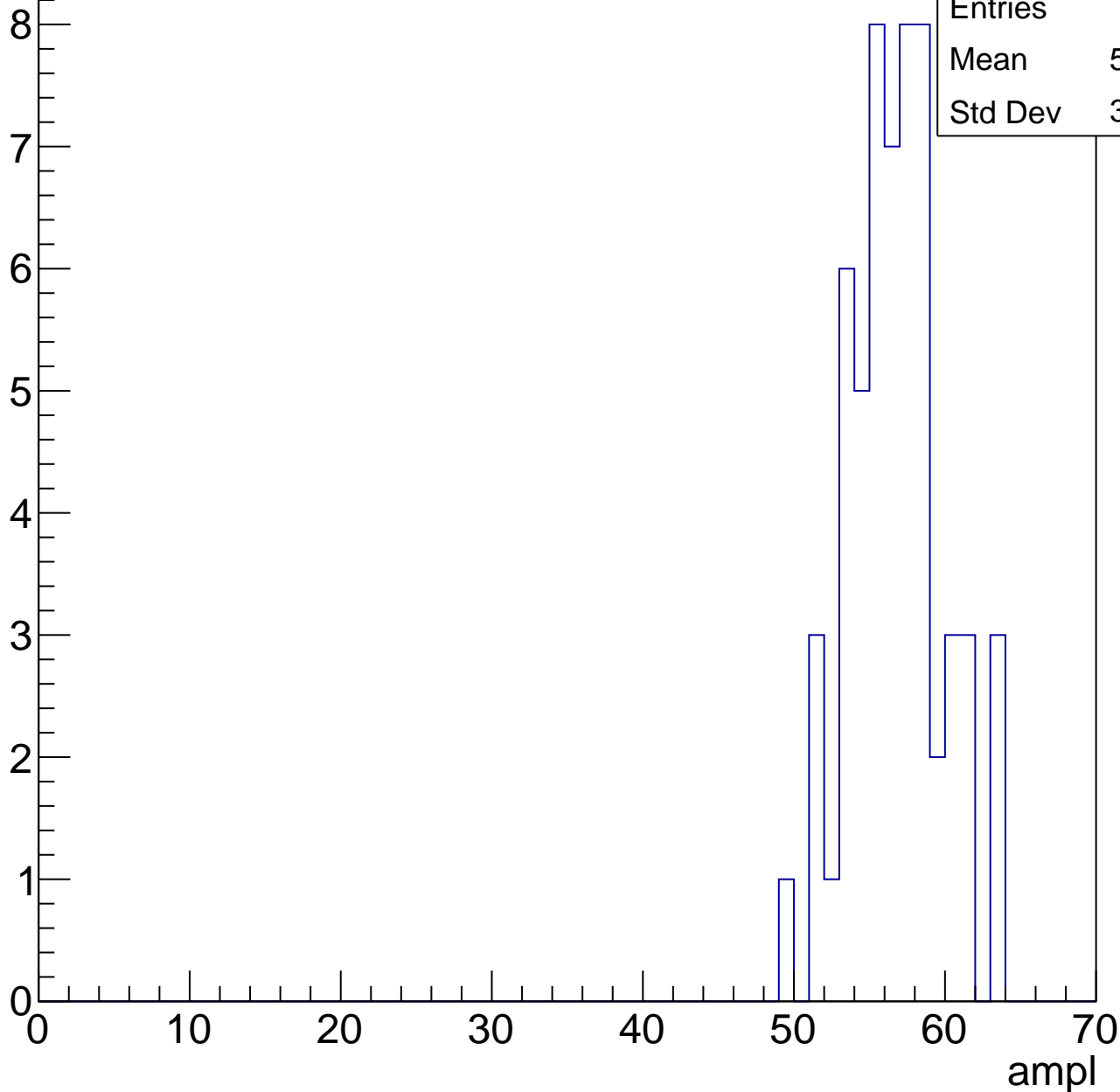


B0L000S, U2-ch5, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	56.28
Std Dev	3.067



B0L000S, U2-ch5, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	45
Mean	58.8
Std Dev	9.171

Entry

10

8

6

4

2

0

0

10

20

30

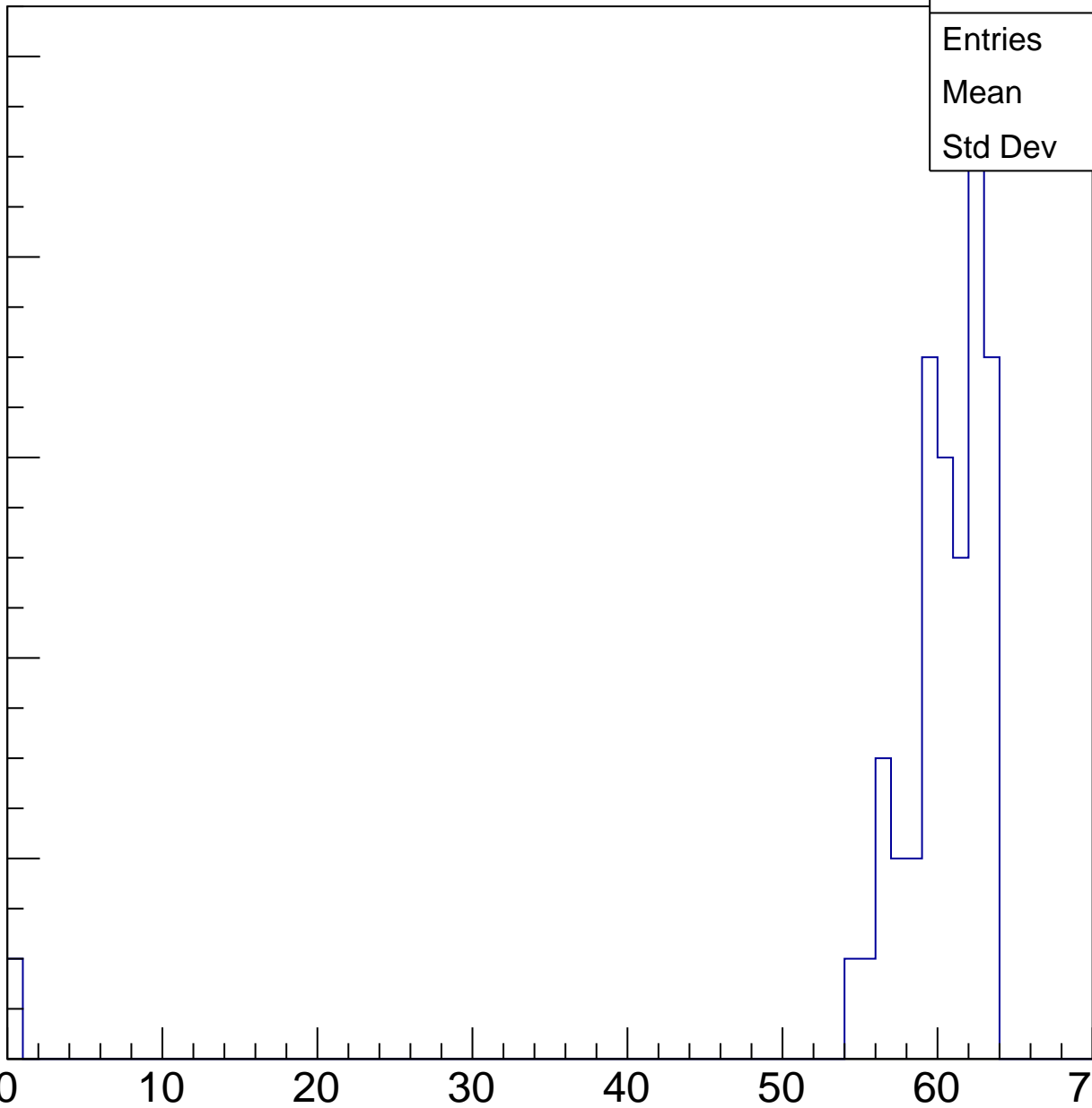
40

50

60

70

ampl



B0L000S, U2-ch5, adc6

calib_packv5_042523_0143.root, FC#5, port B1

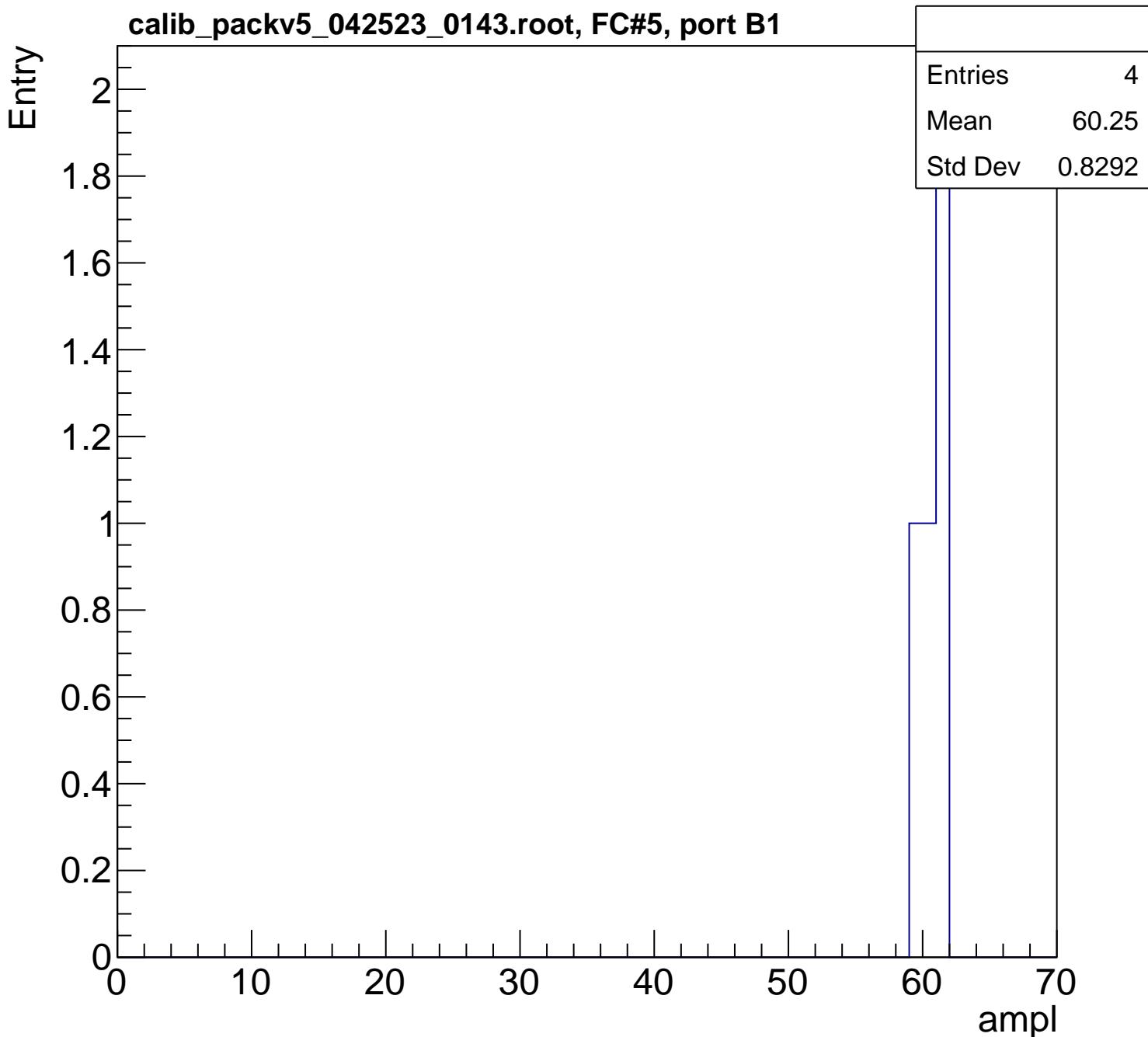
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	60.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch5, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch6, adc0

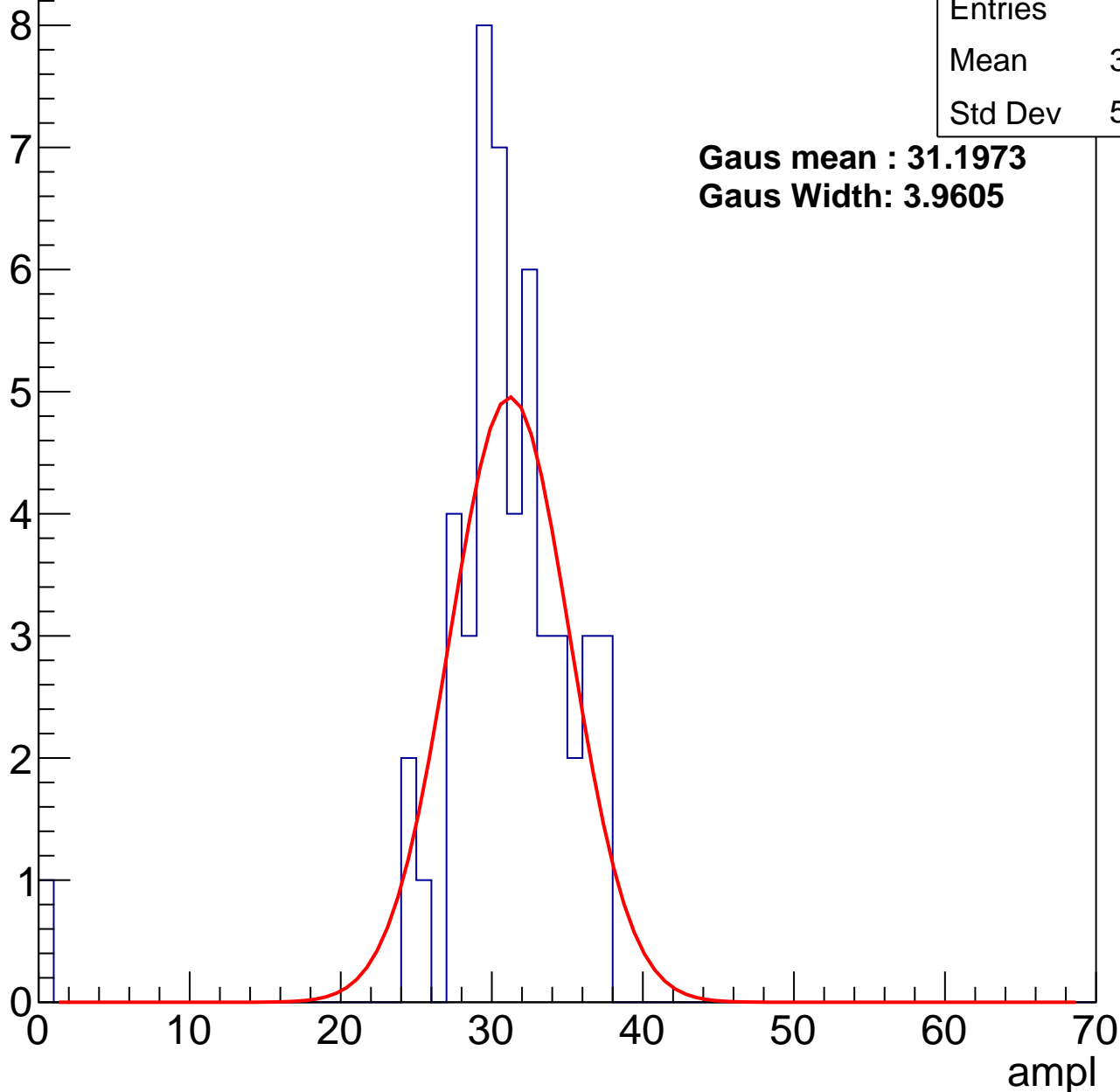
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	30.26
Std Dev	5.399

Gaus mean : 31.1973

Gaus Width: 3.9605



B0L000S, U2-ch6, adc1

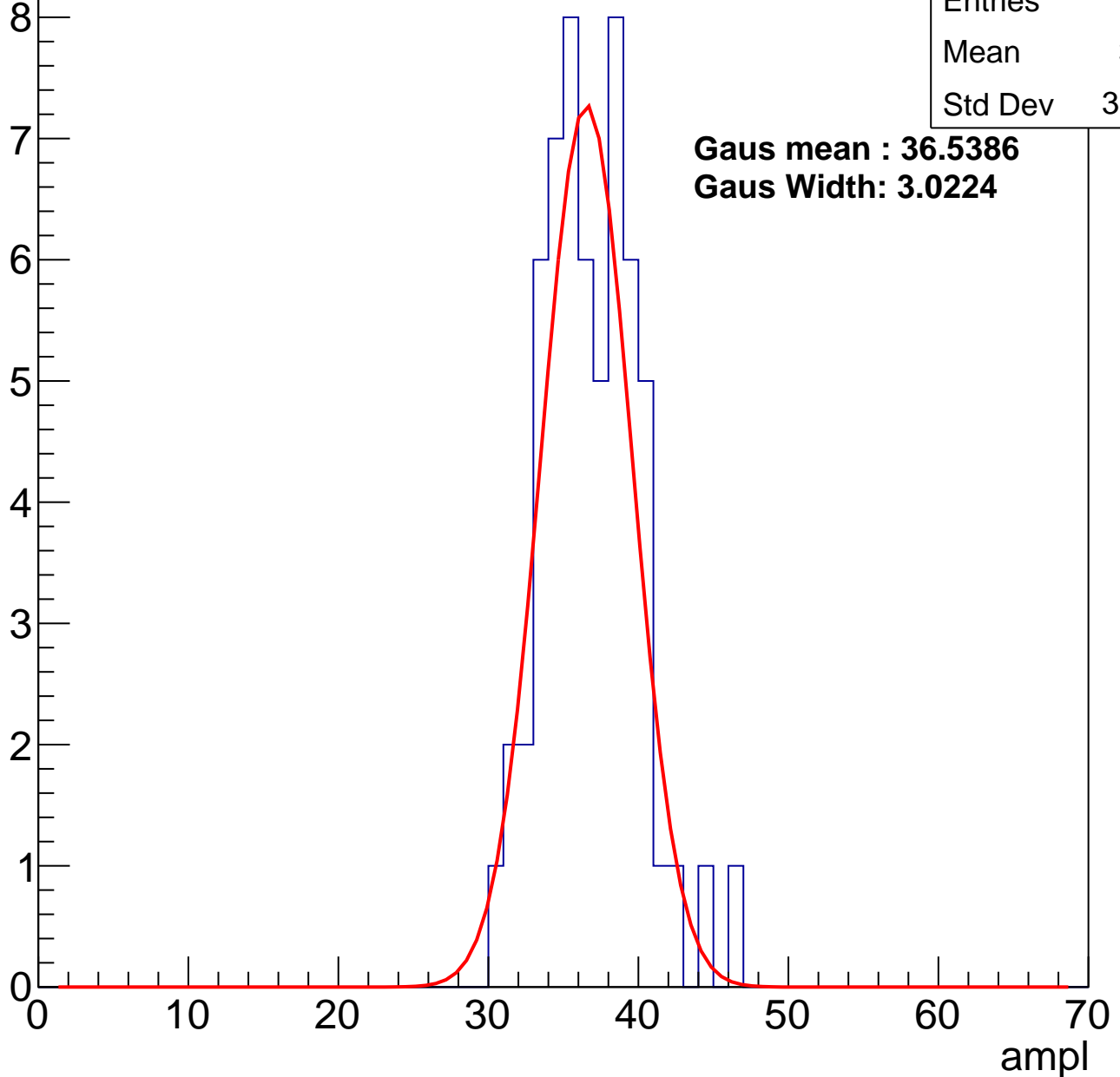
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.4
Std Dev	3.147

Gaus mean : 36.5386

Gaus Width: 3.0224



B0L000S, U2-ch6, adc2

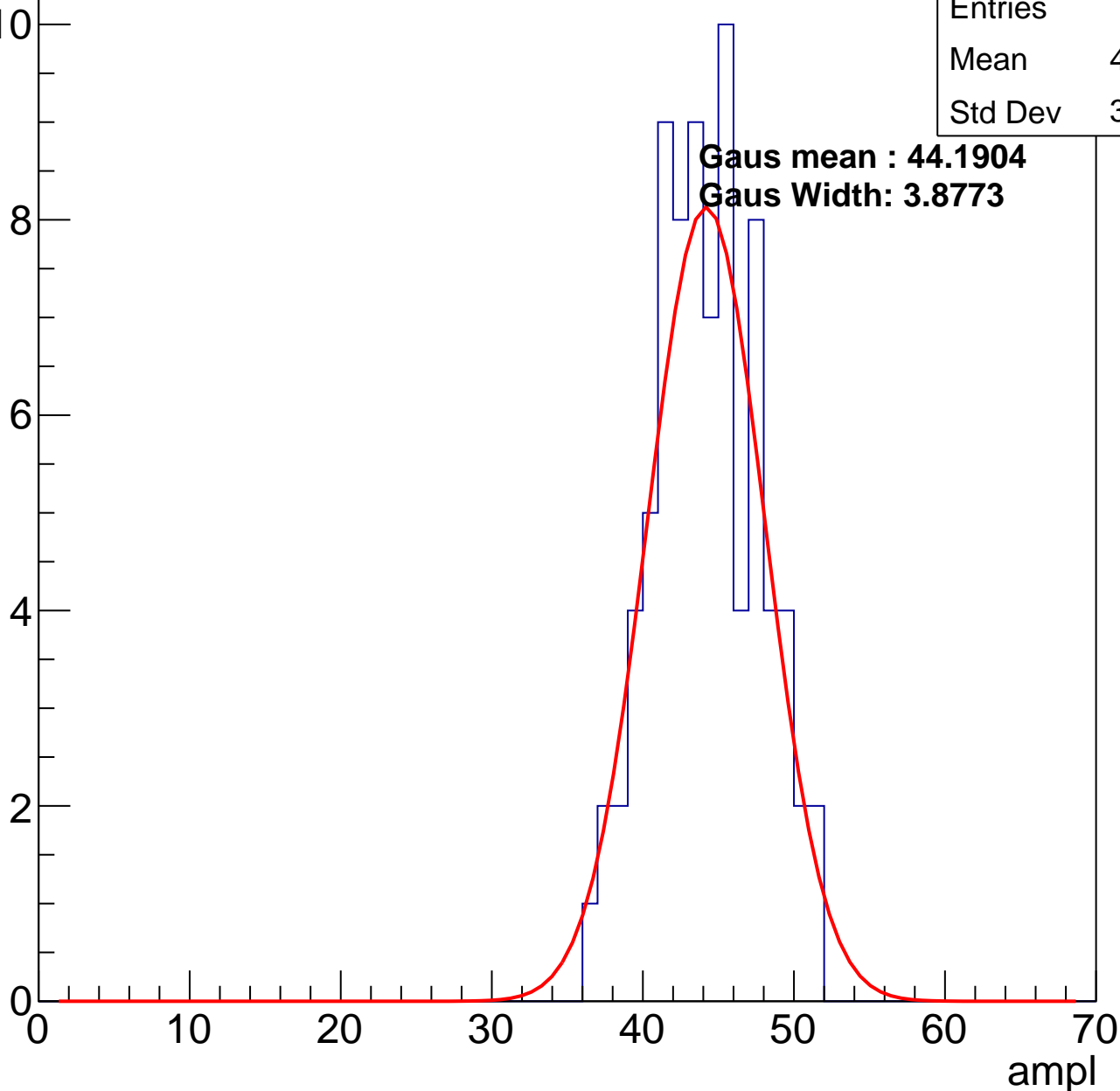
calib_packv5_042523_0143.root, FC#5, port B1

Entries	81
Mean	43.73
Std Dev	3.446

Gaus mean : 44.1904

Gaus Width: 3.8773

Entry

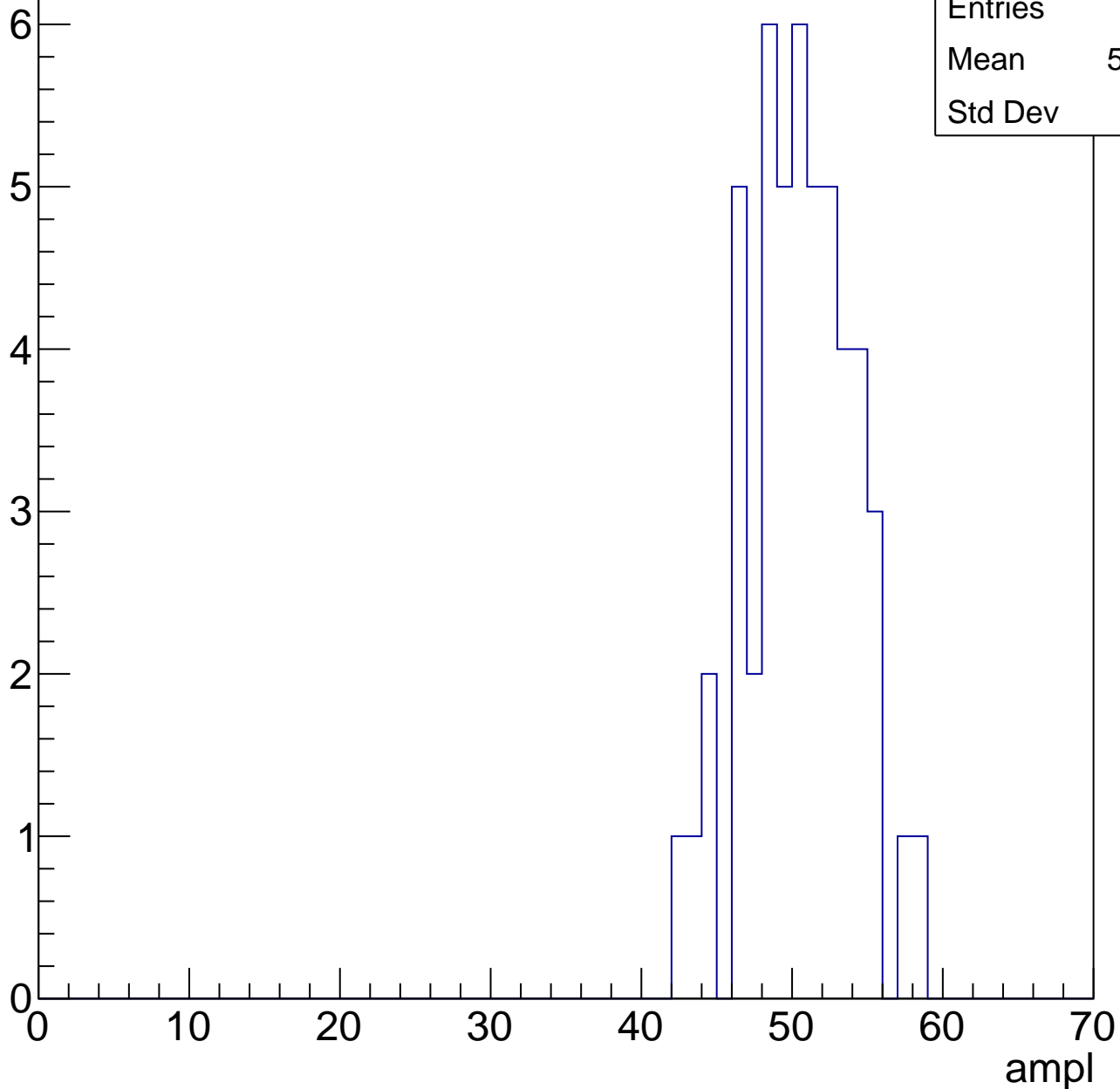


B0L000S, U2-ch6, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	50.06
Std Dev	3.5



B0L000S, U2-ch6, adc4

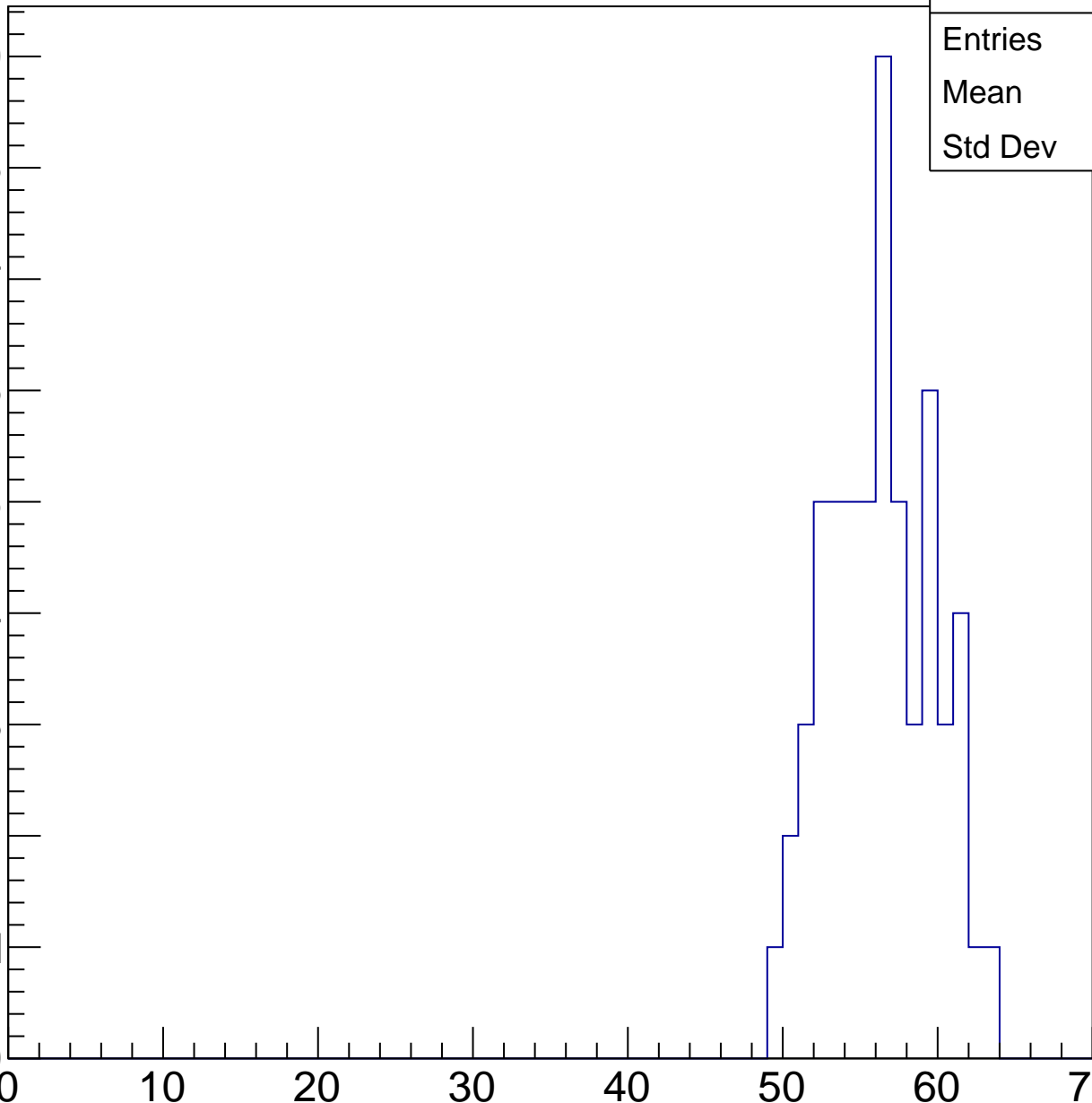
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	55.83
Std Dev	3.343

ampl



B0L000S, U2-ch6, adc5

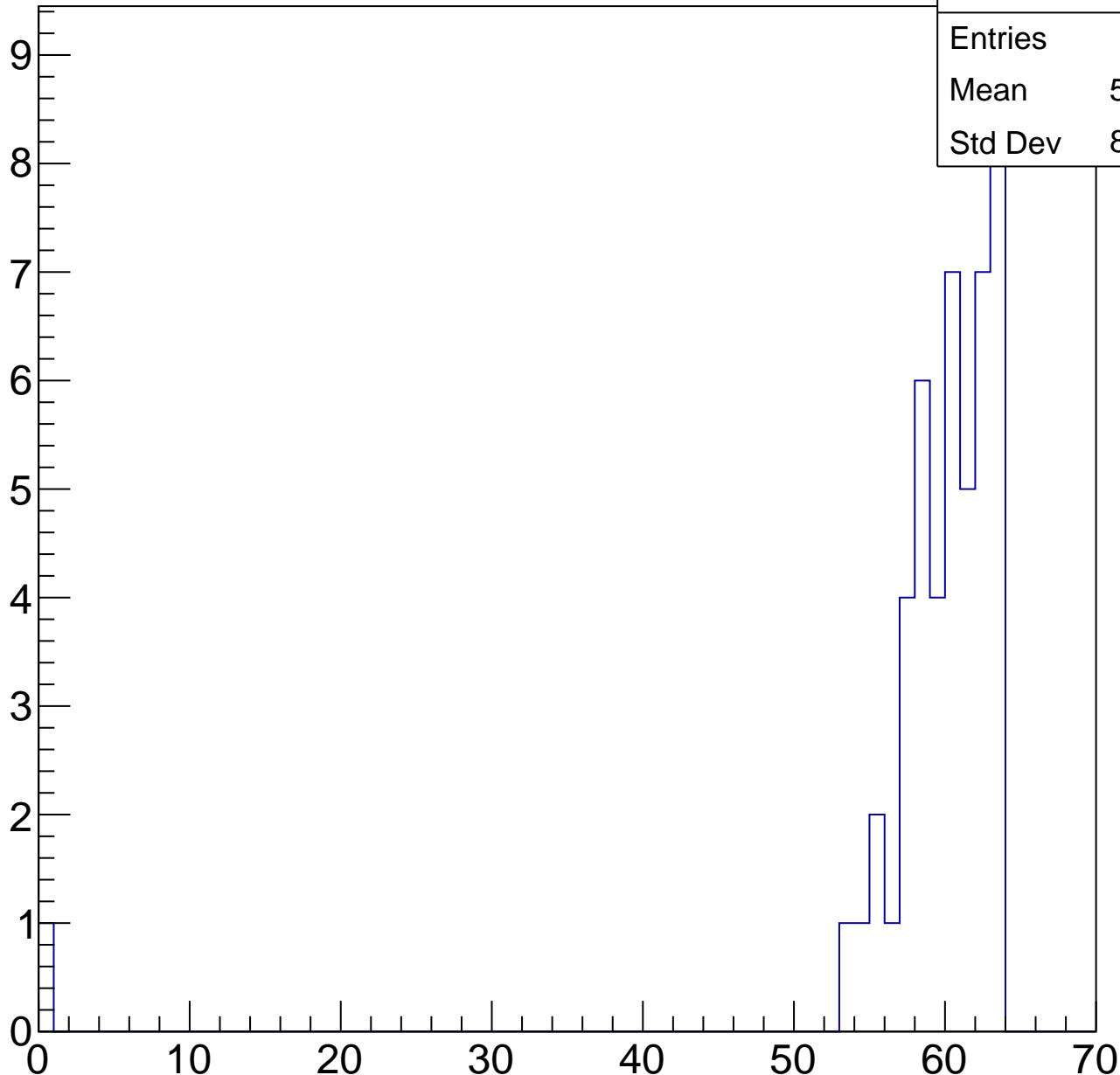
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.56
Std Dev	8.932

ampl



B0L000S, U2-ch6, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L000S, U2-ch6, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch7, adc0

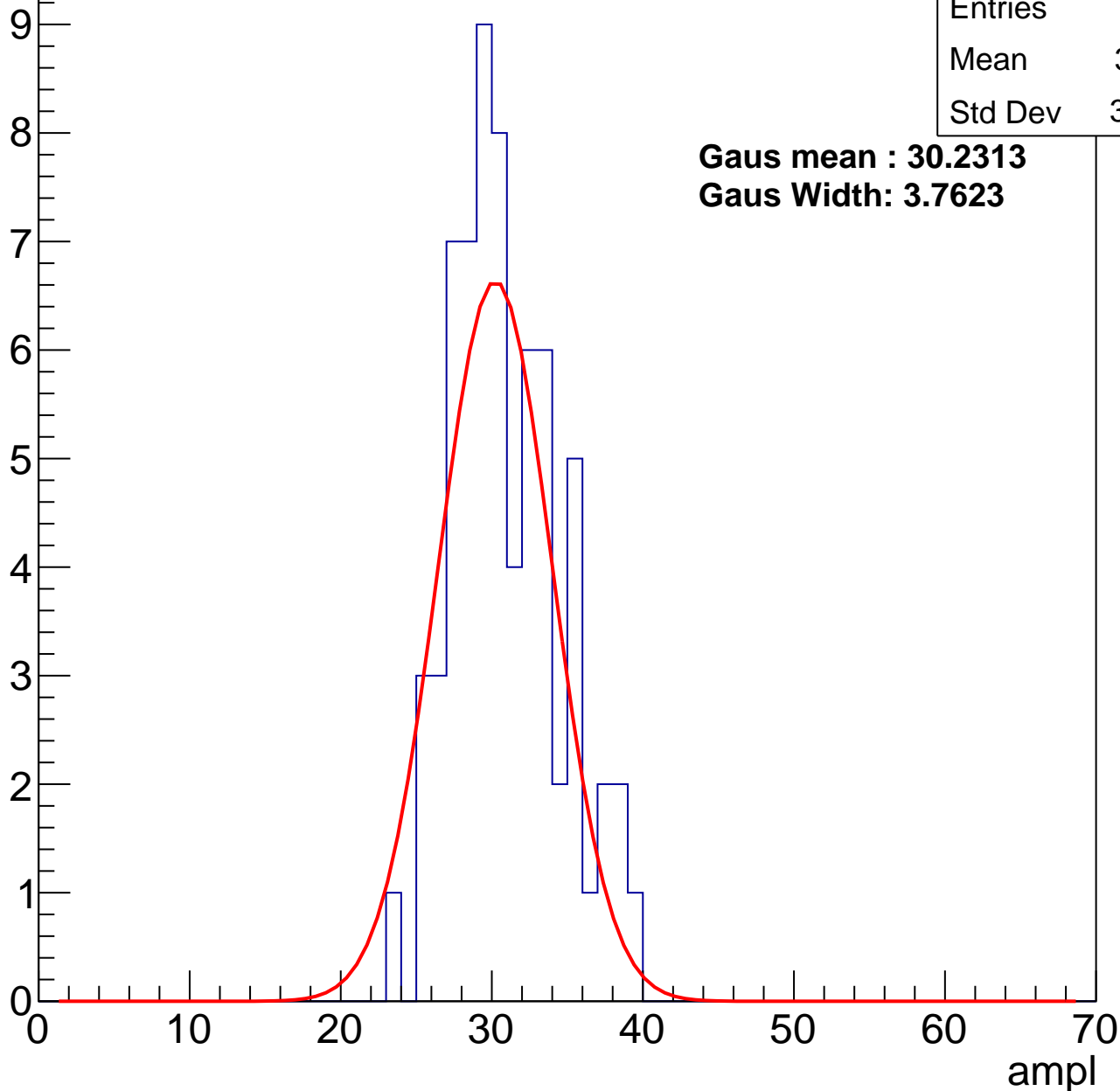
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.51
Std Dev	3.534

Gaus mean : 30.2313

Gaus Width: 3.7623



B0L000S, U2-ch7, adc1

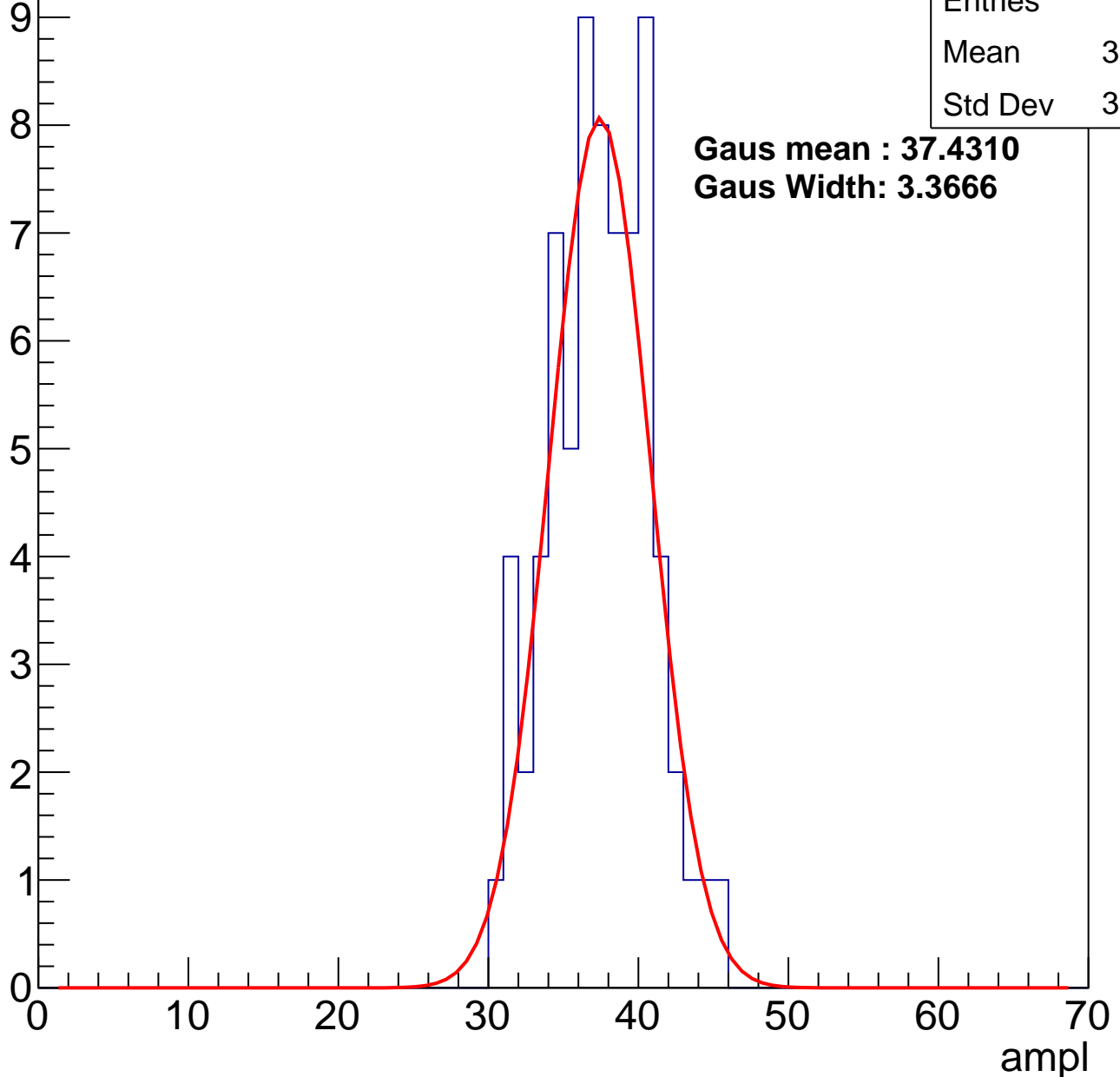
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.97
Std Dev	3.287

Gaus mean : 37.4310

Gaus Width: 3.3666



B0L000S, U2-ch7, adc2

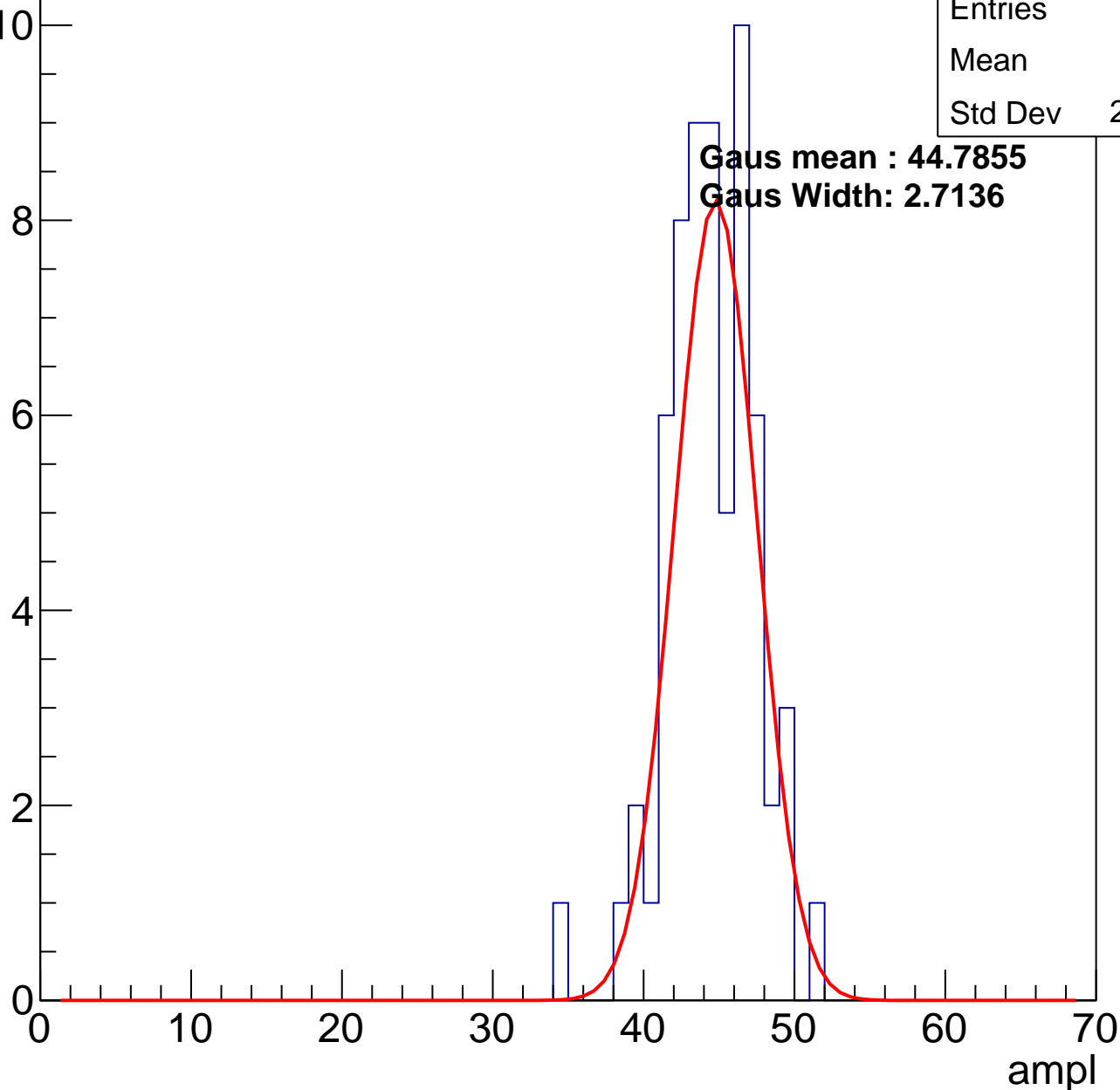
calib_packv5_042523_0143.root, FC#5, port B1

Entries	64
Mean	44
Std Dev	2.937

Entry

Gaus mean : 44.7855

Gaus Width: 2.7136

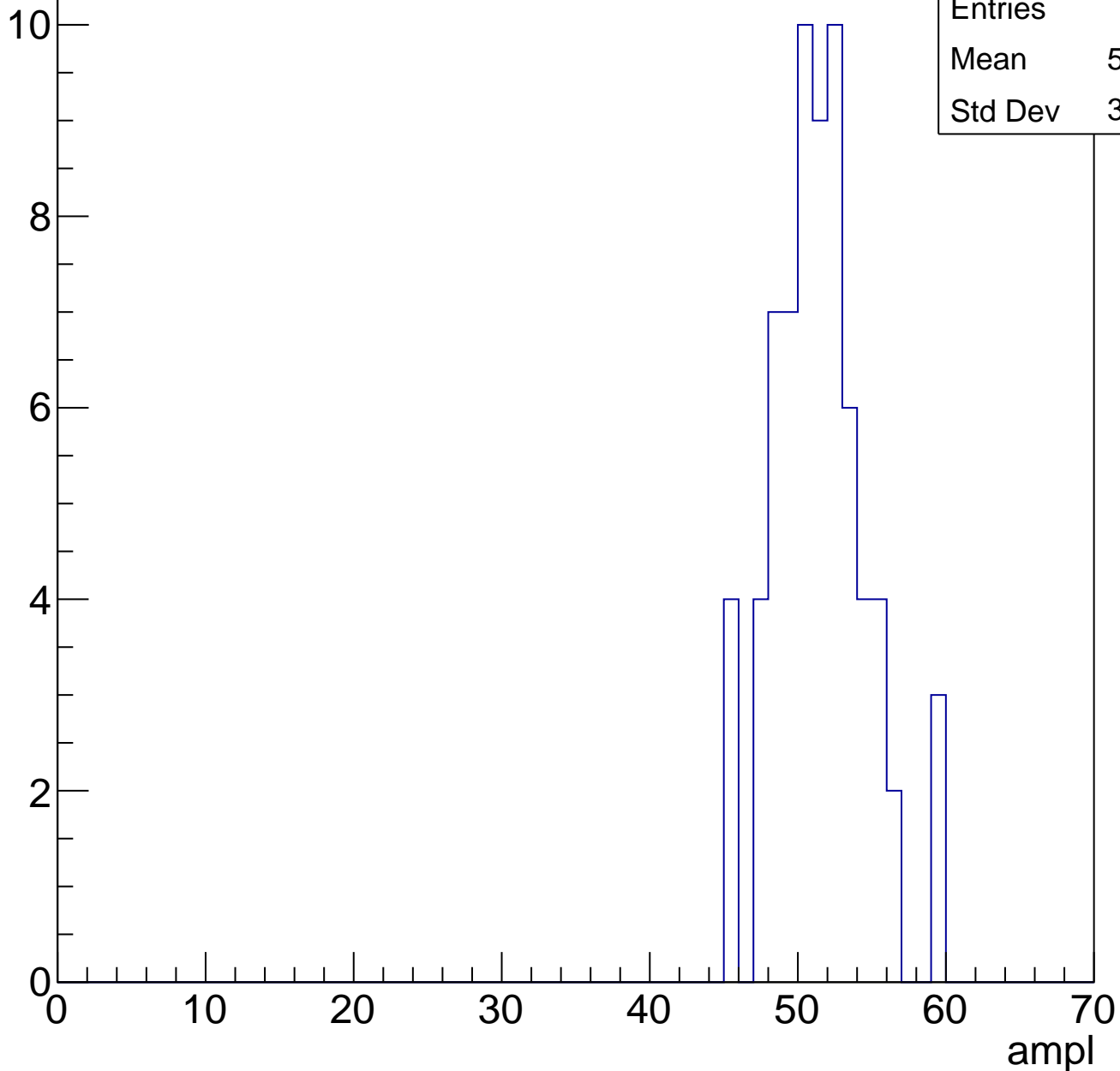


B0L000S, U2-ch7, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	70
Mean	50.99
Std Dev	3.128

Entry

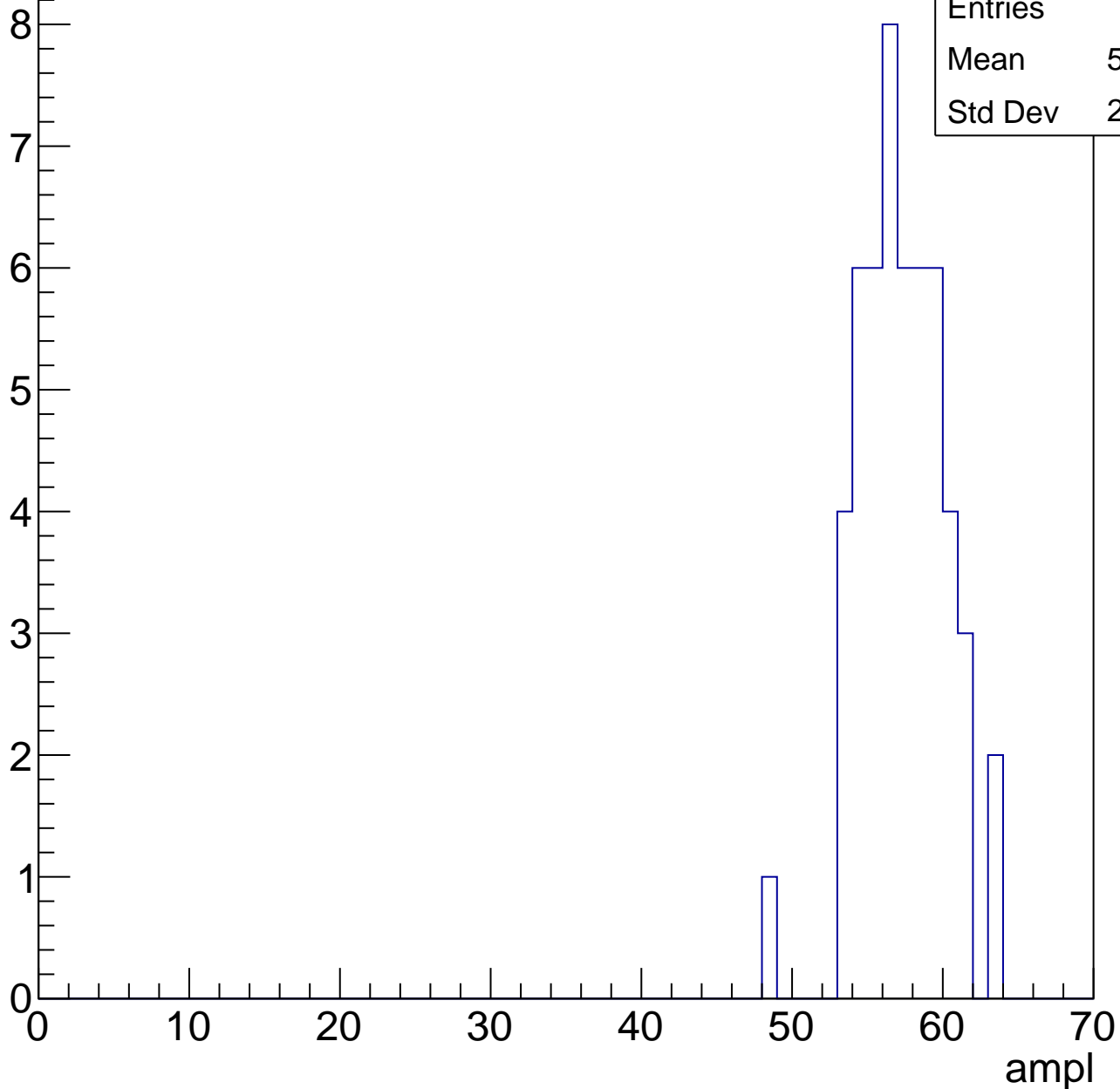


B0L000S, U2-ch7, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	56.83
Std Dev	2.827

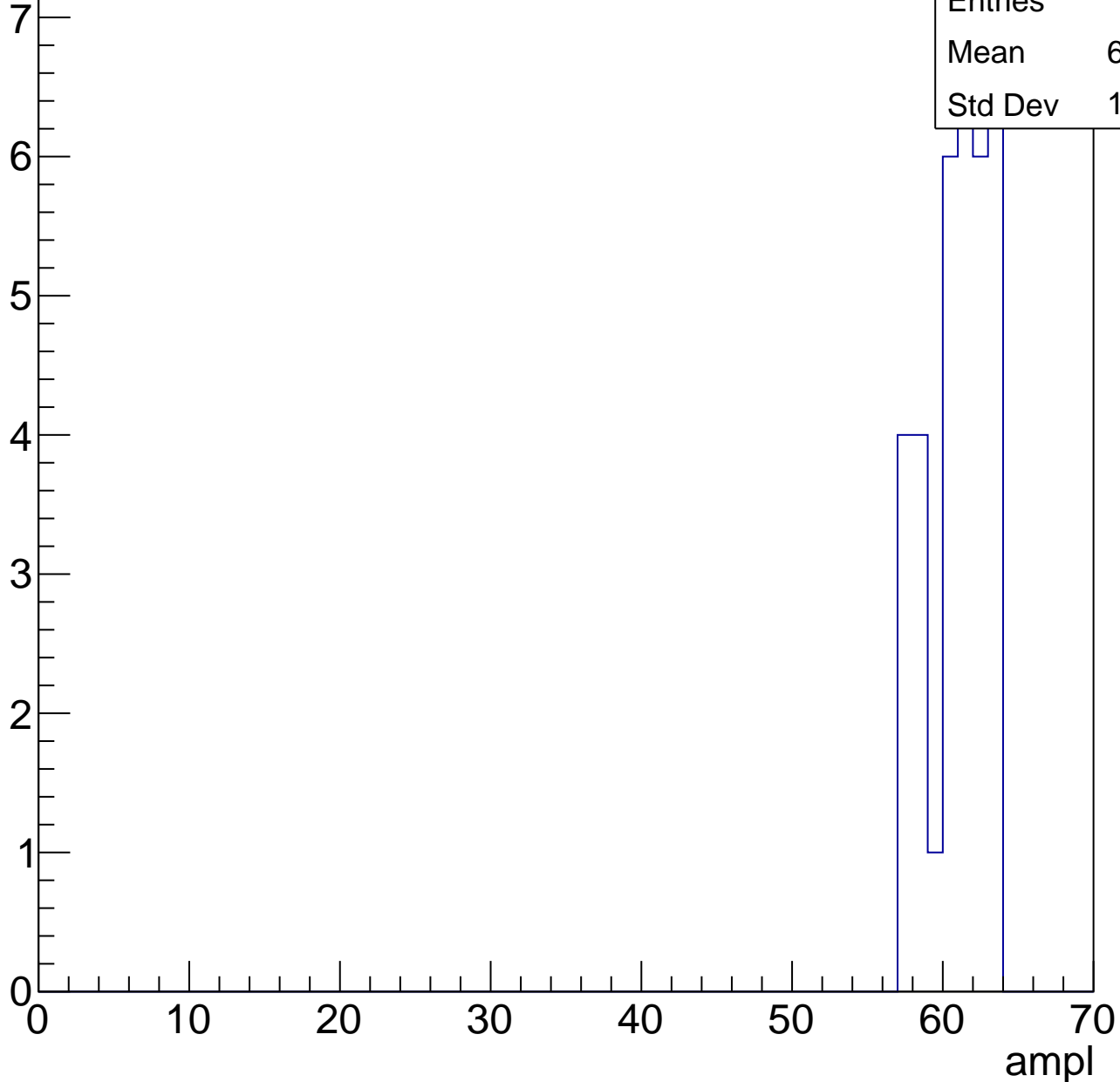


B0L000S, U2-ch7, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

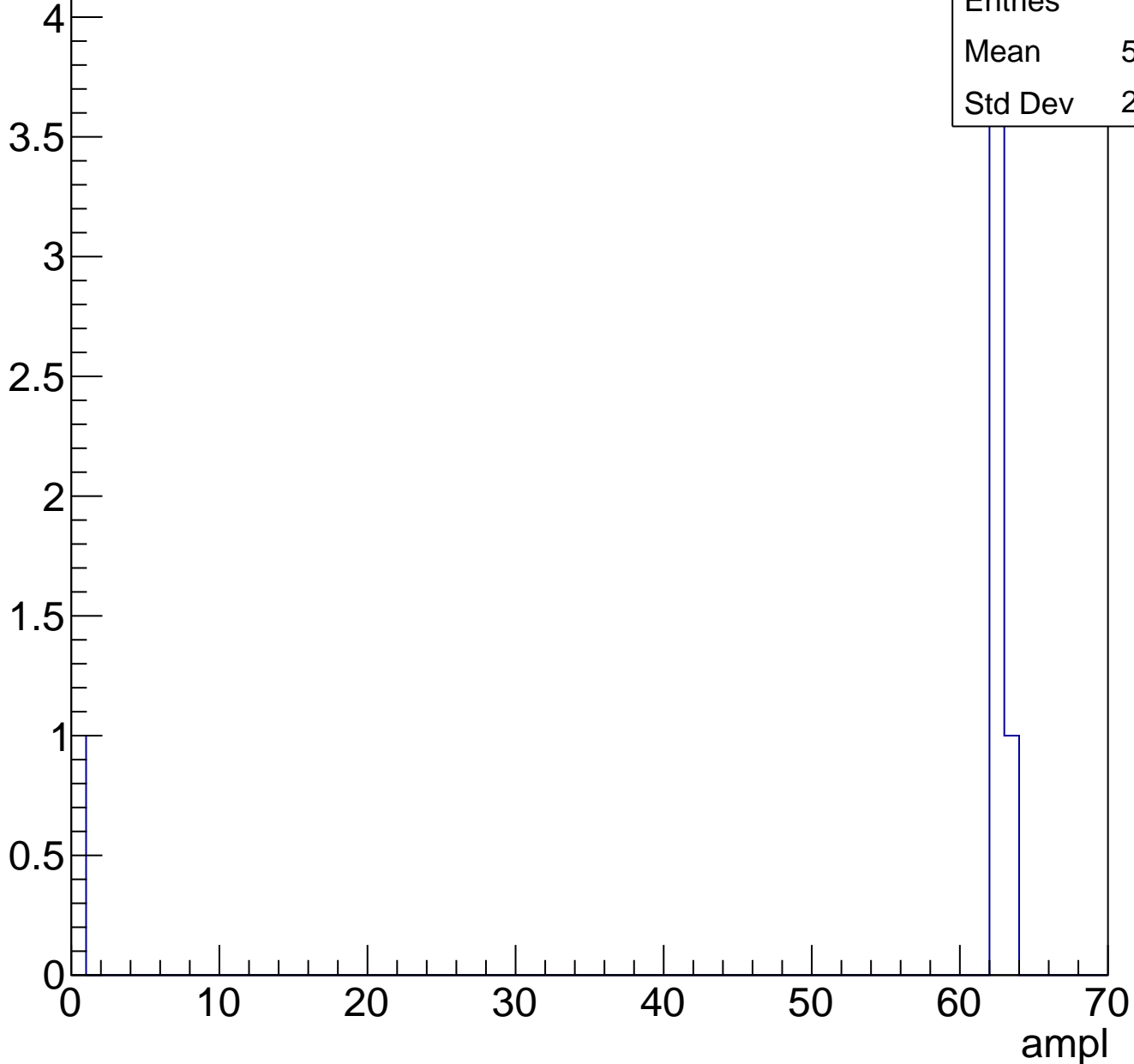
Entries	35
Mean	60.54
Std Dev	1.976



B0L000S, U2-ch7, adc6

calib_packv5_042523_0143.root, FC#5, port B1

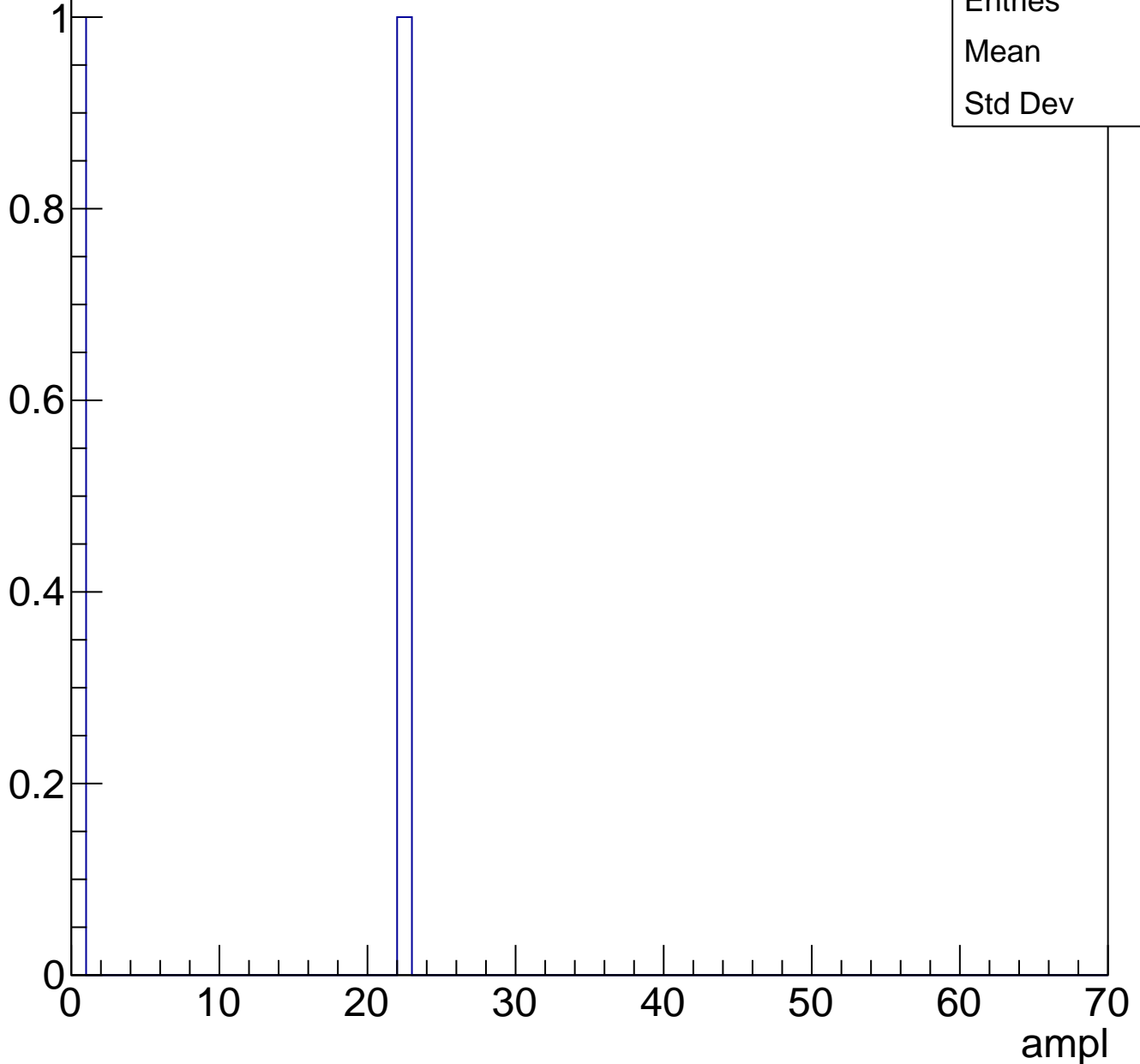
Entry



B0L000S, U2-ch7, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch8, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	73
Mean	31.03
Std Dev	3.63

Gaus mean : 31.5011

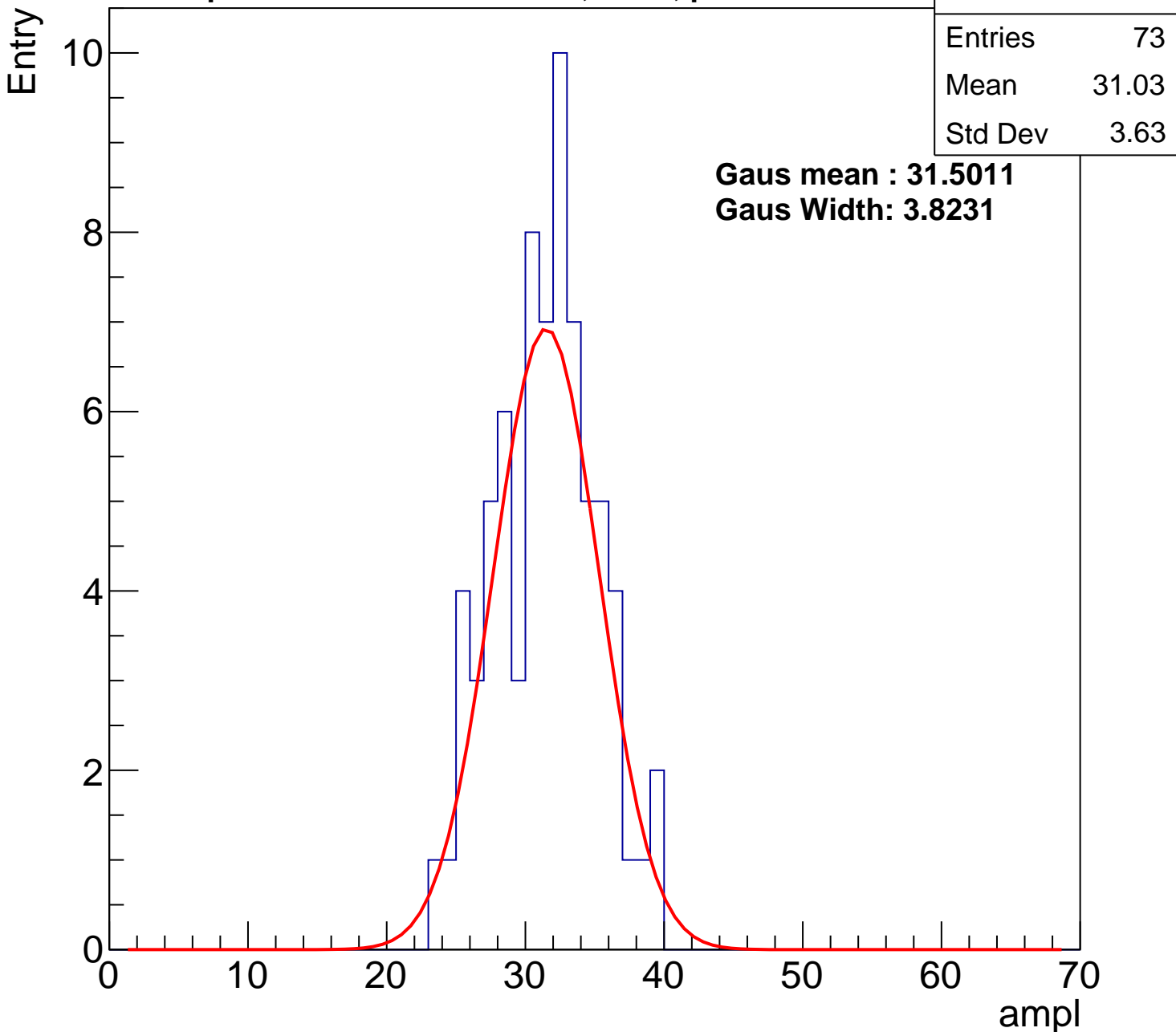
Gaus Width: 3.8231

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch8, adc1

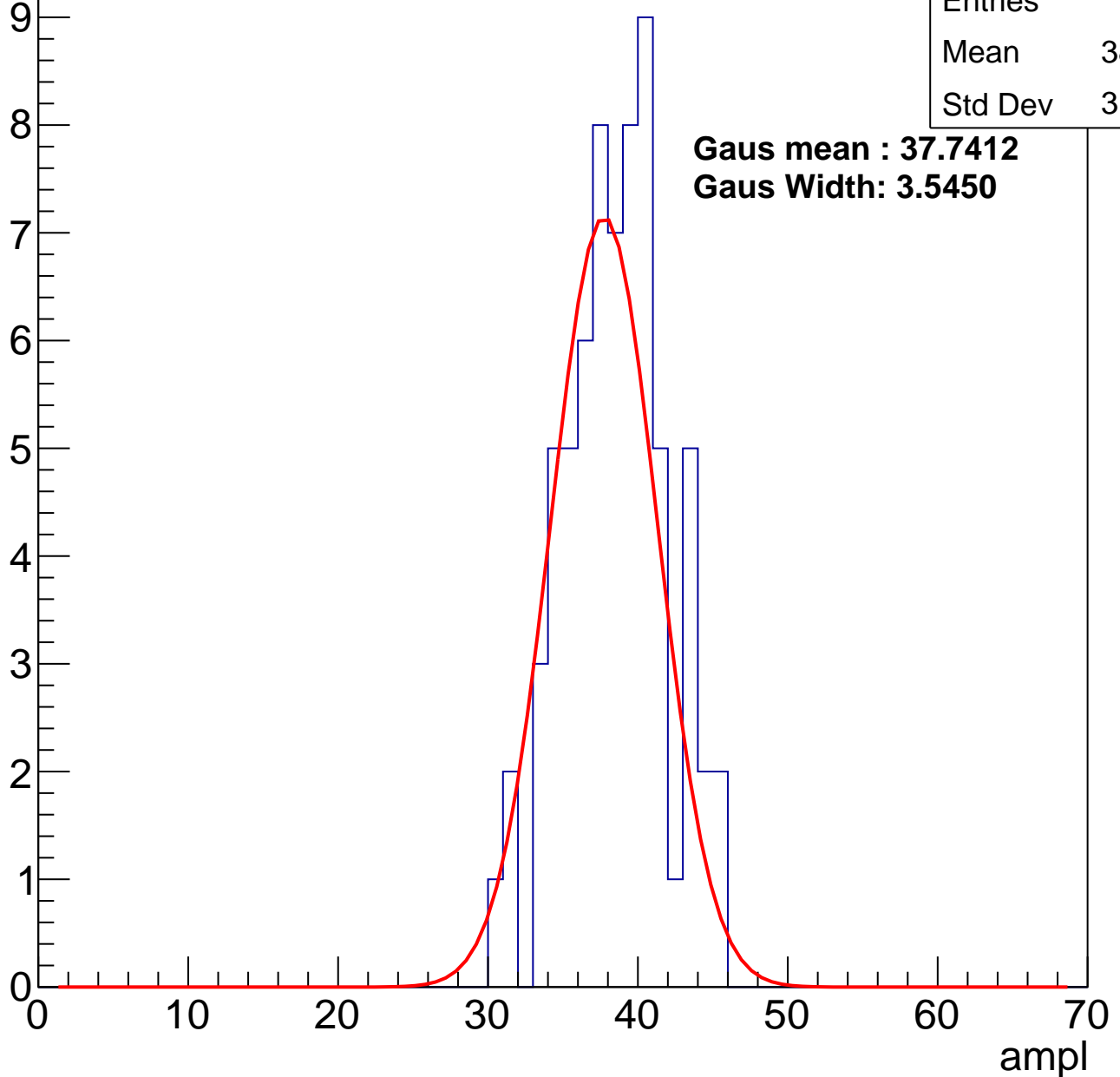
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	38.06
Std Dev	3.392

Gaus mean : 37.7412

Gaus Width: 3.5450



B0L000S, U2-ch8, adc2

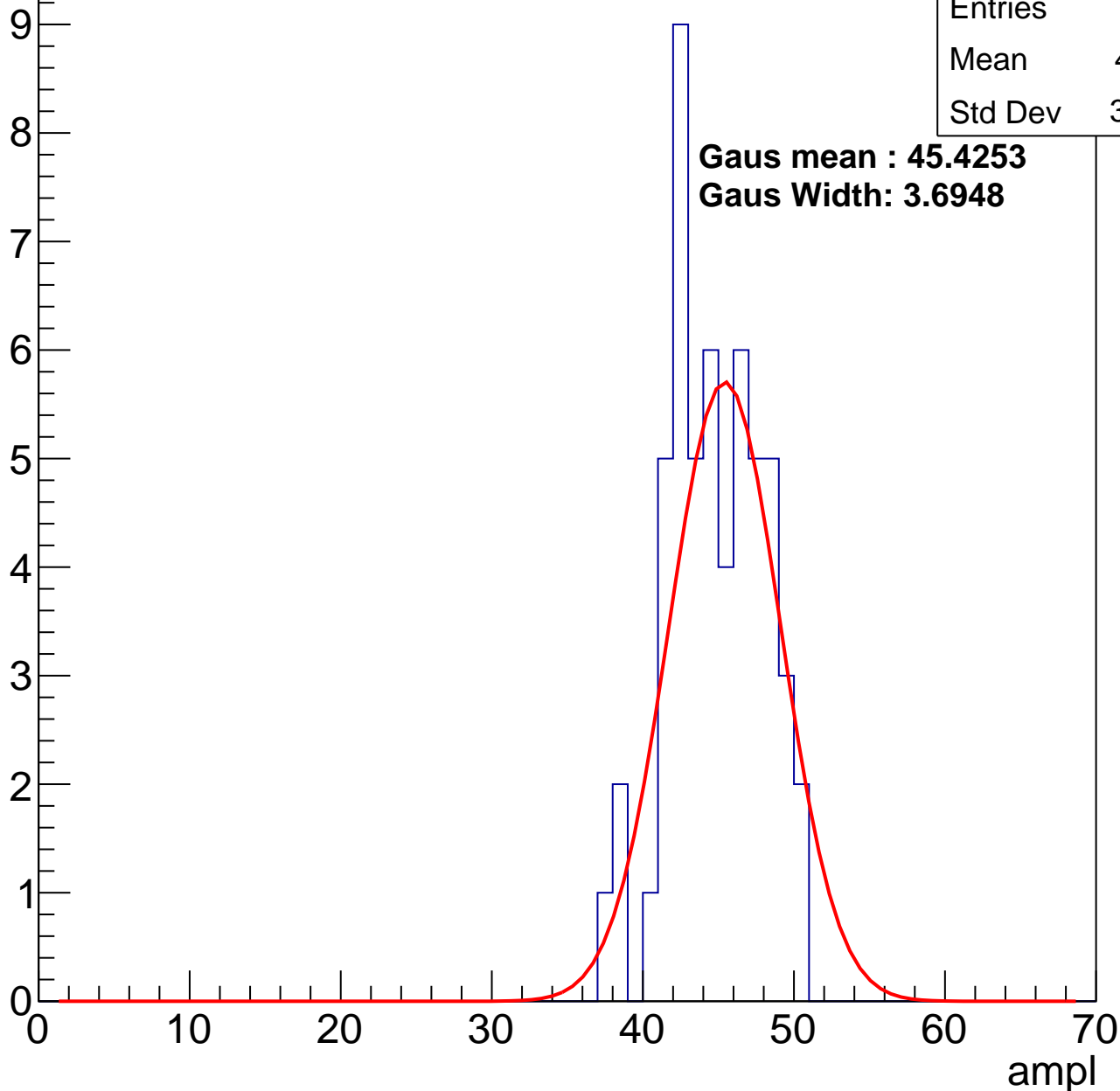
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	44.31
Std Dev	3.096

Gaus mean : 45.4253

Gaus Width: 3.6948

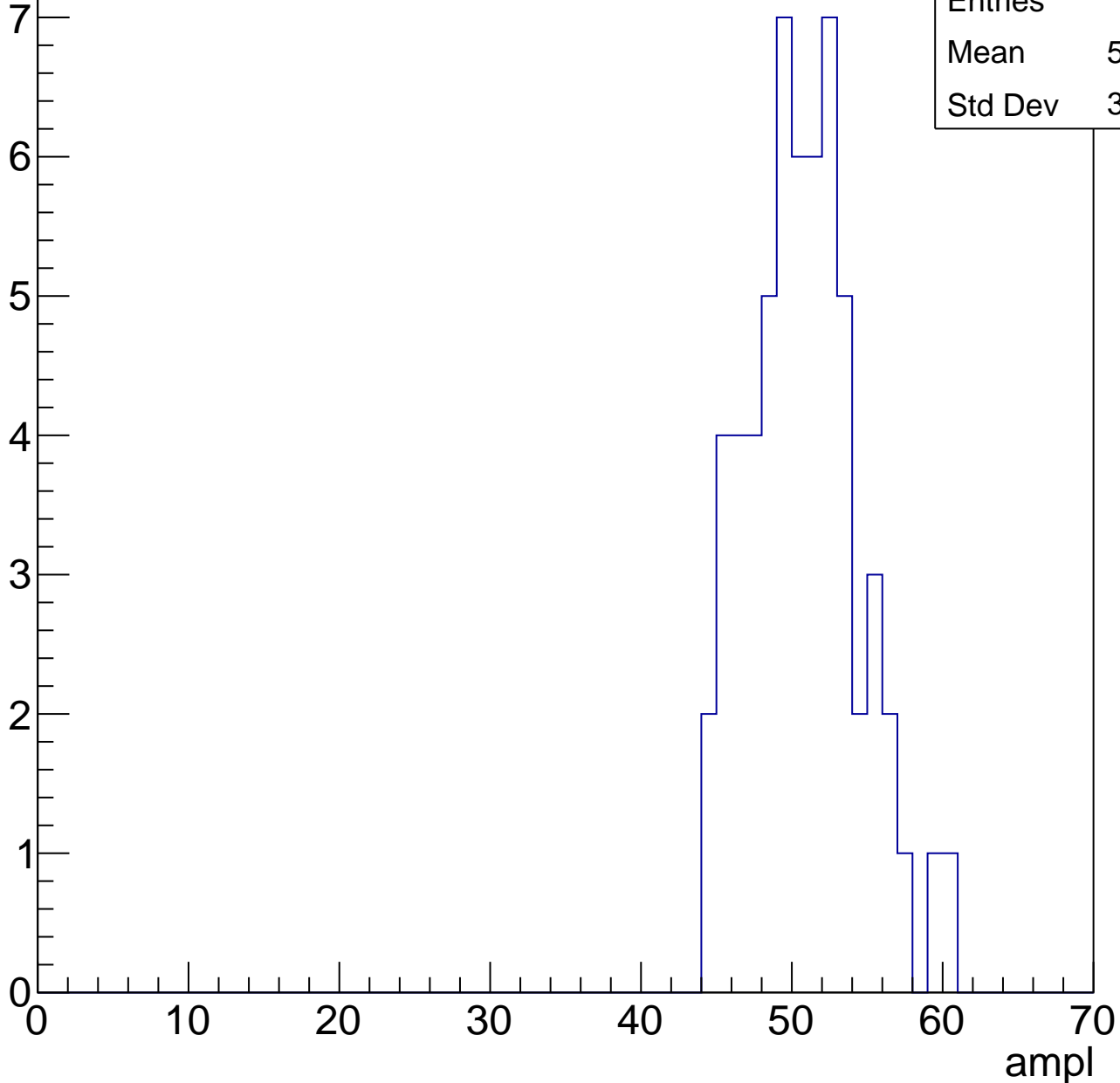


B0L000S, U2-ch8, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.32
Std Dev	3.608

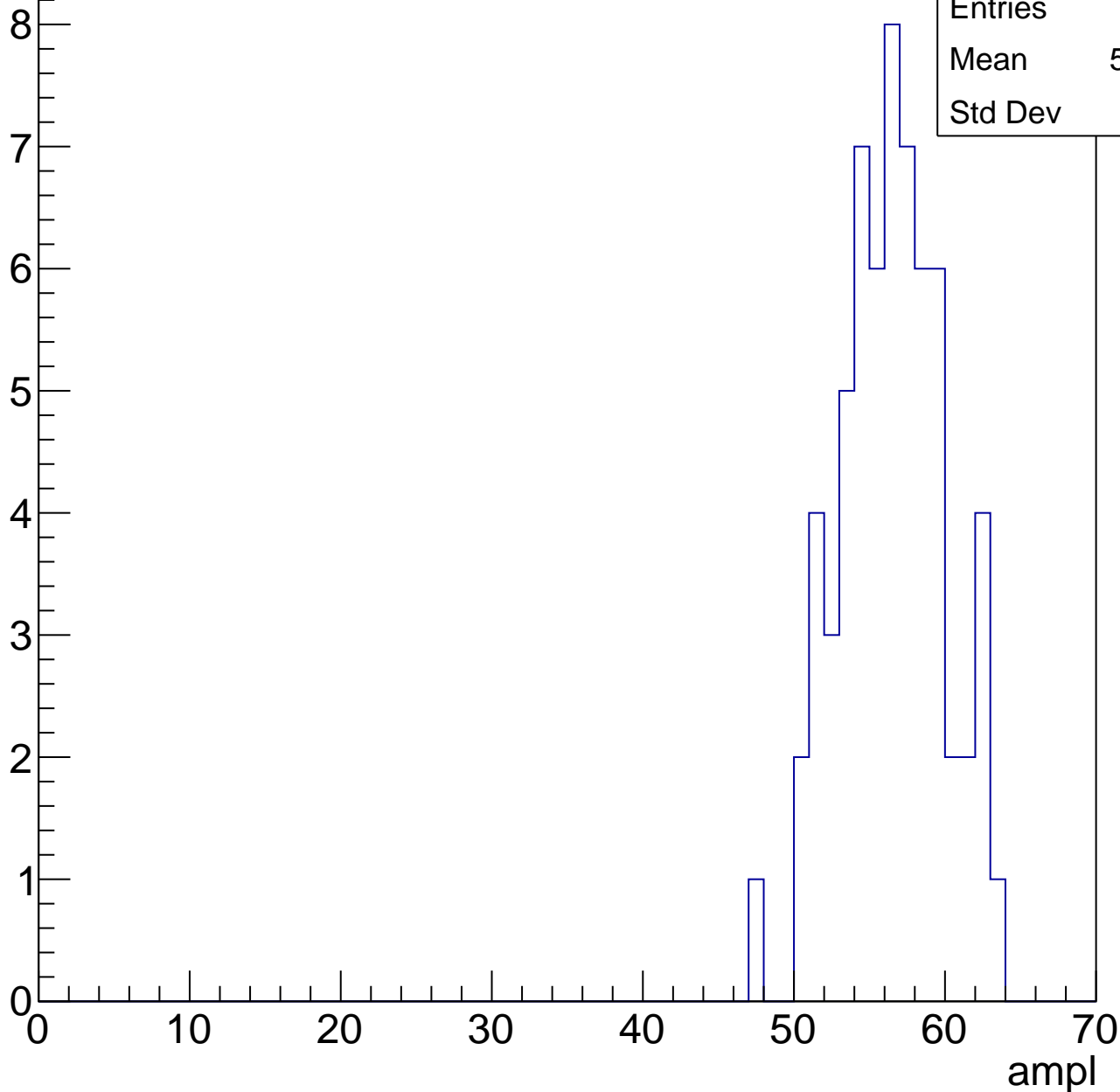


B0L000S, U2-ch8, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	55.97
Std Dev	3.4

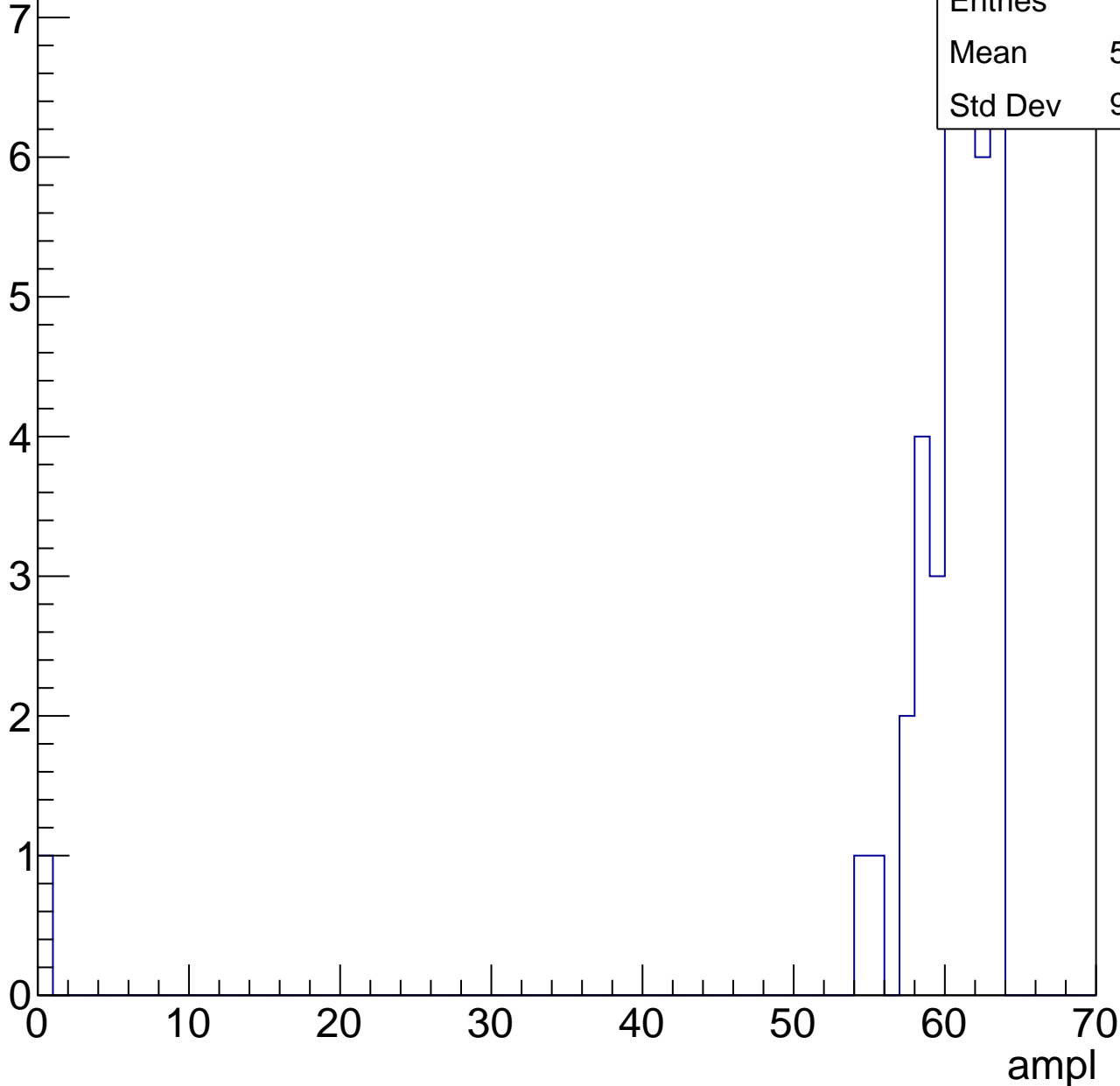


B0L000S, U2-ch8, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.77
Std Dev	9.783



B0L000S, U2-ch8, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch8, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch9, adc0

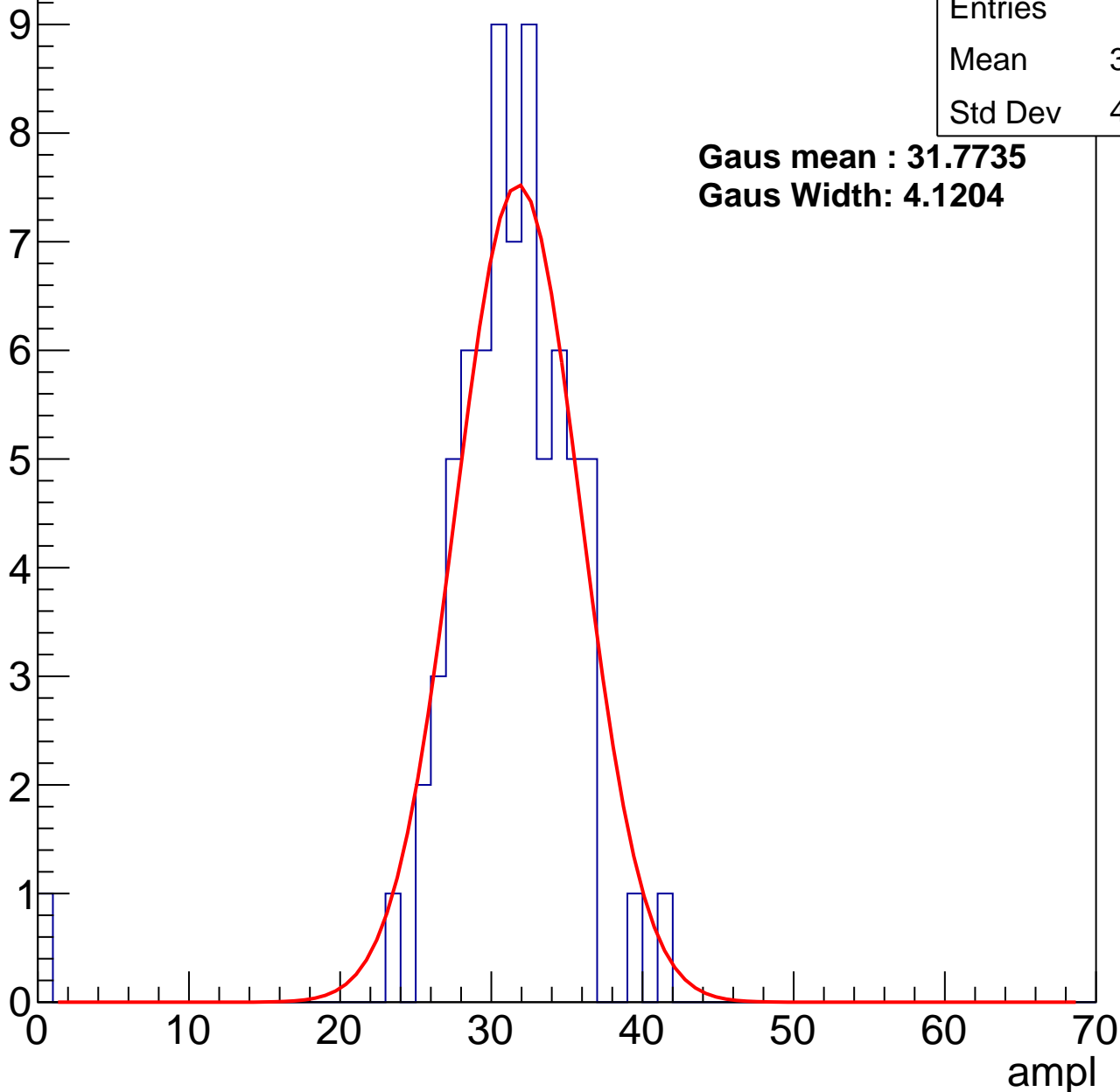
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.65
Std Dev	4.975

Gaus mean : 31.7735

Gaus Width: 4.1204



B0L000S, U2-ch9, adc1

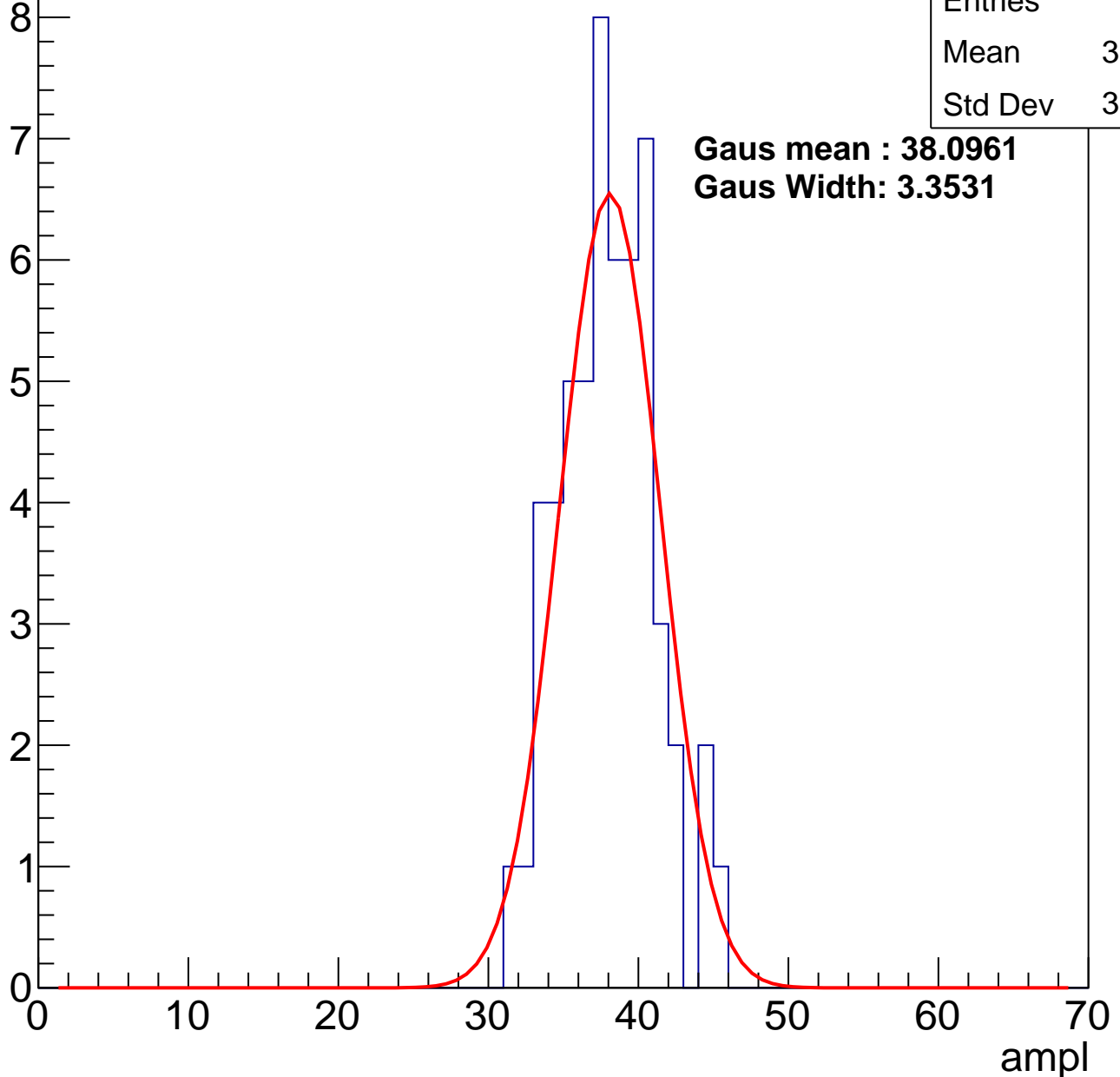
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	37.53
Std Dev	3.086

Gaus mean : 38.0961

Gaus Width: 3.3531



B0L000S, U2-ch9, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	75
Mean	44.01
Std Dev	3.478

Gaus mean : 44.6427

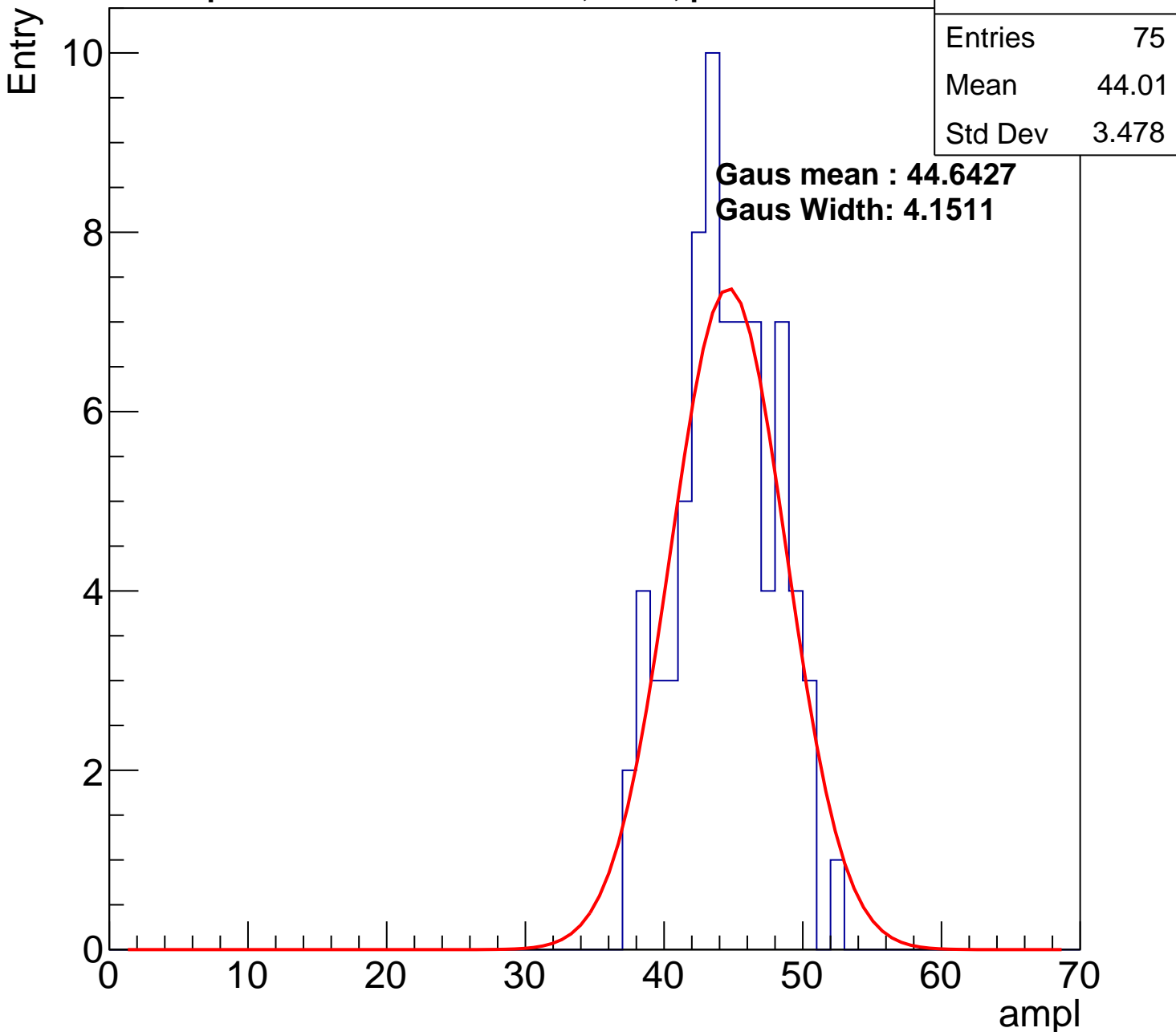
Gaus Width: 4.1511

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

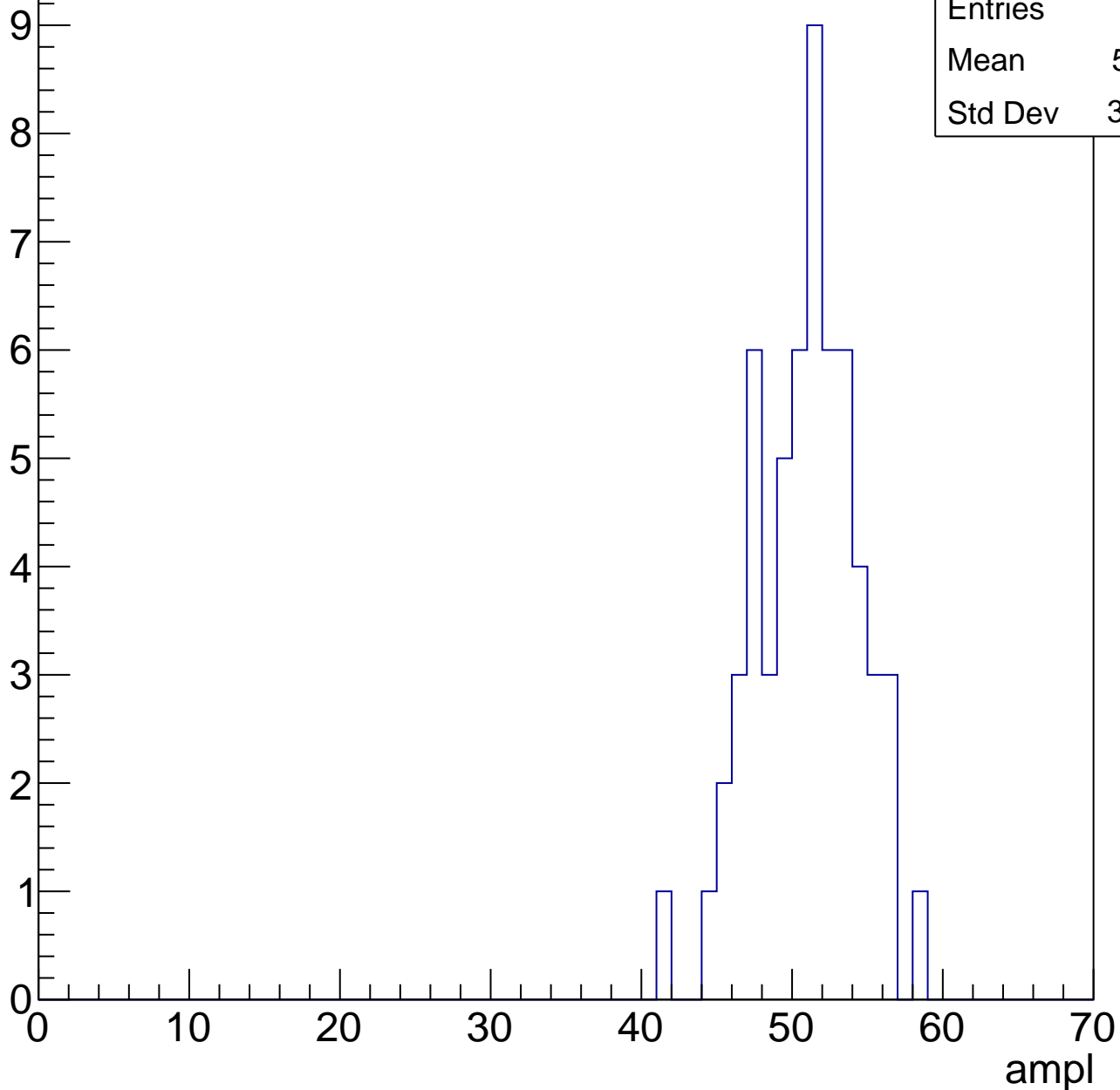


B0L000S, U2-ch9, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	50.51
Std Dev	3.367

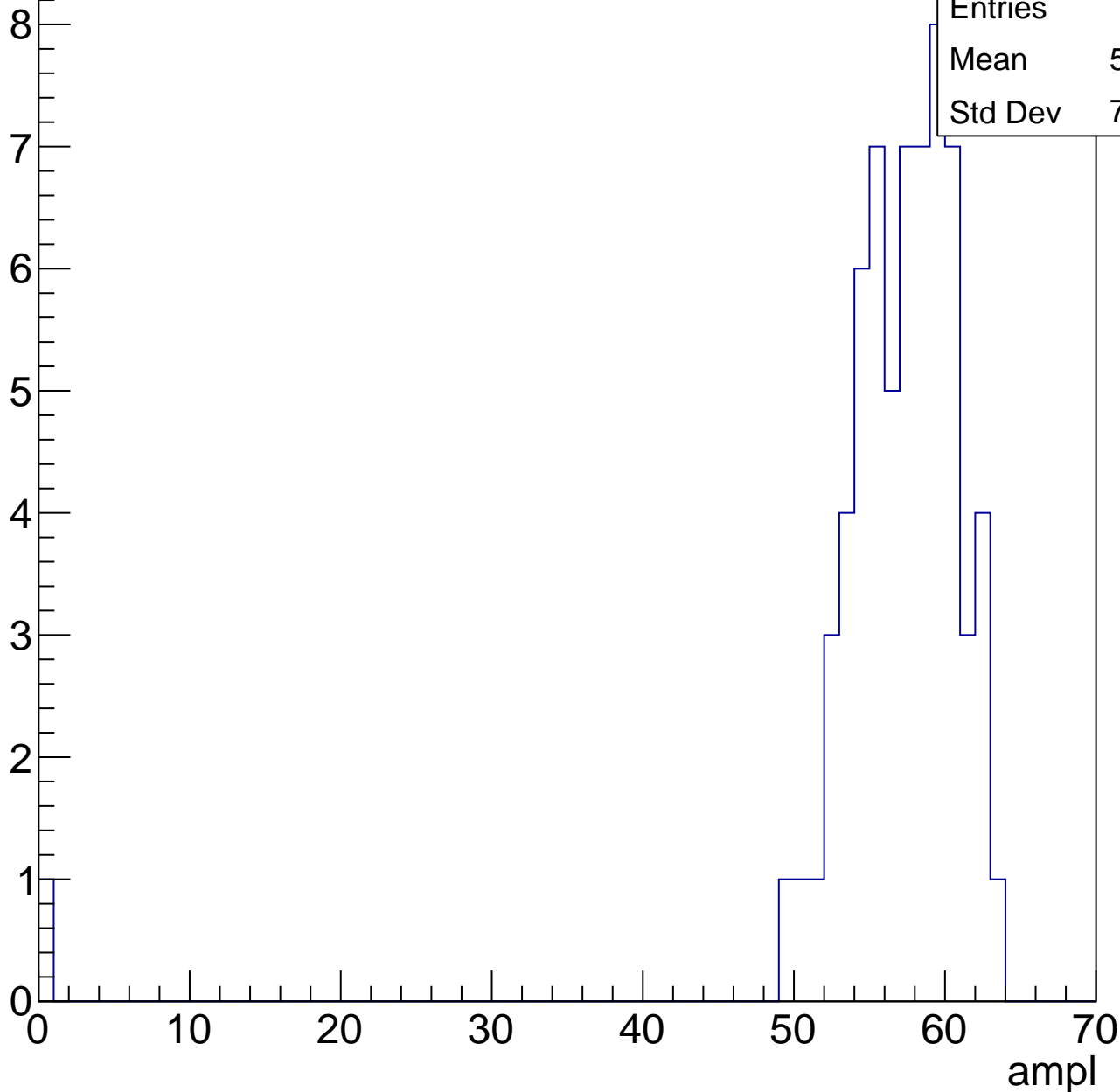


B0L000S, U2-ch9, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	56.03
Std Dev	7.636

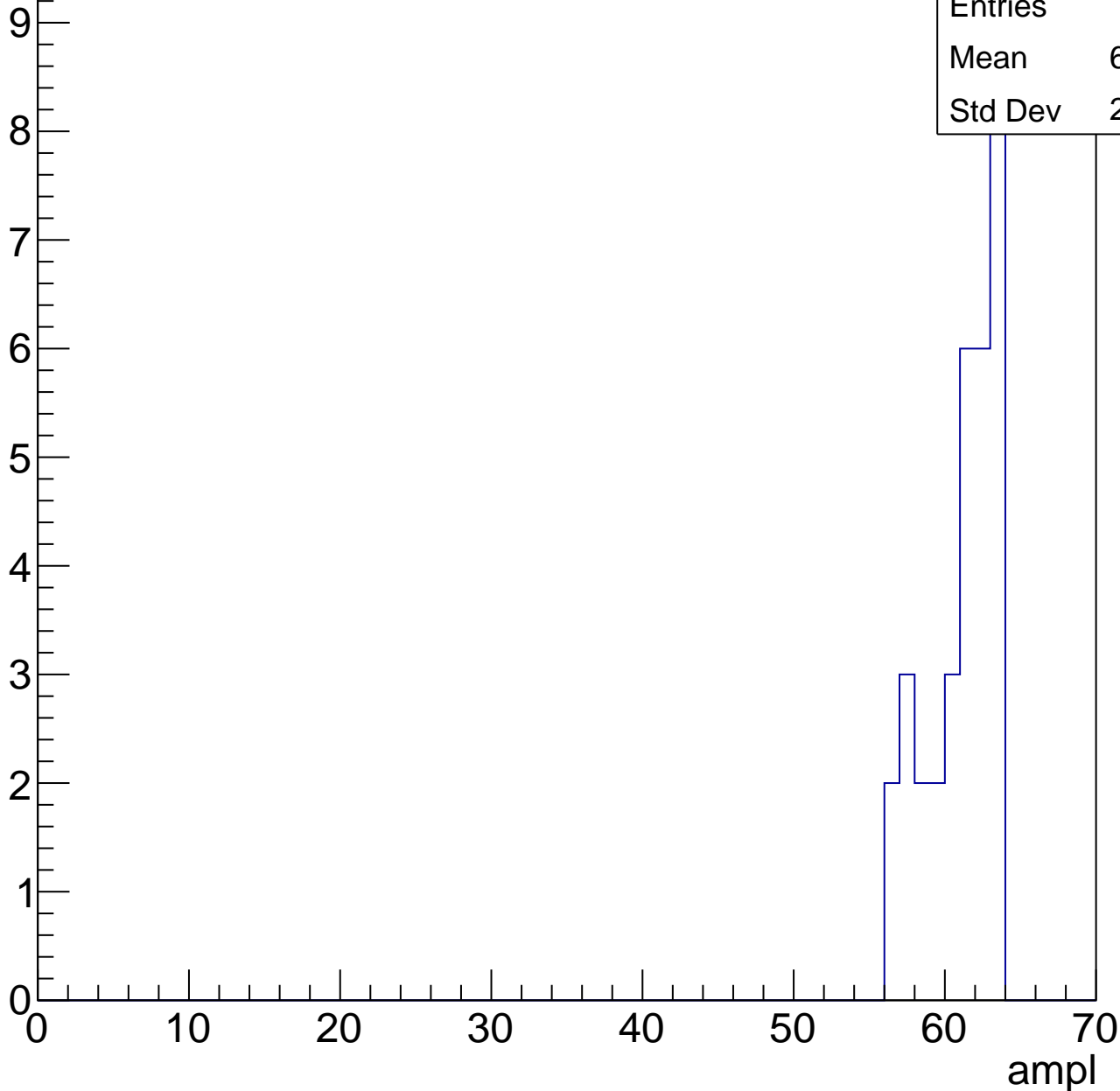


B0L000S, U2-ch9, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	33
Mean	60.67
Std Dev	2.238



B0L000S, U2-ch9, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch9, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch10, adc0

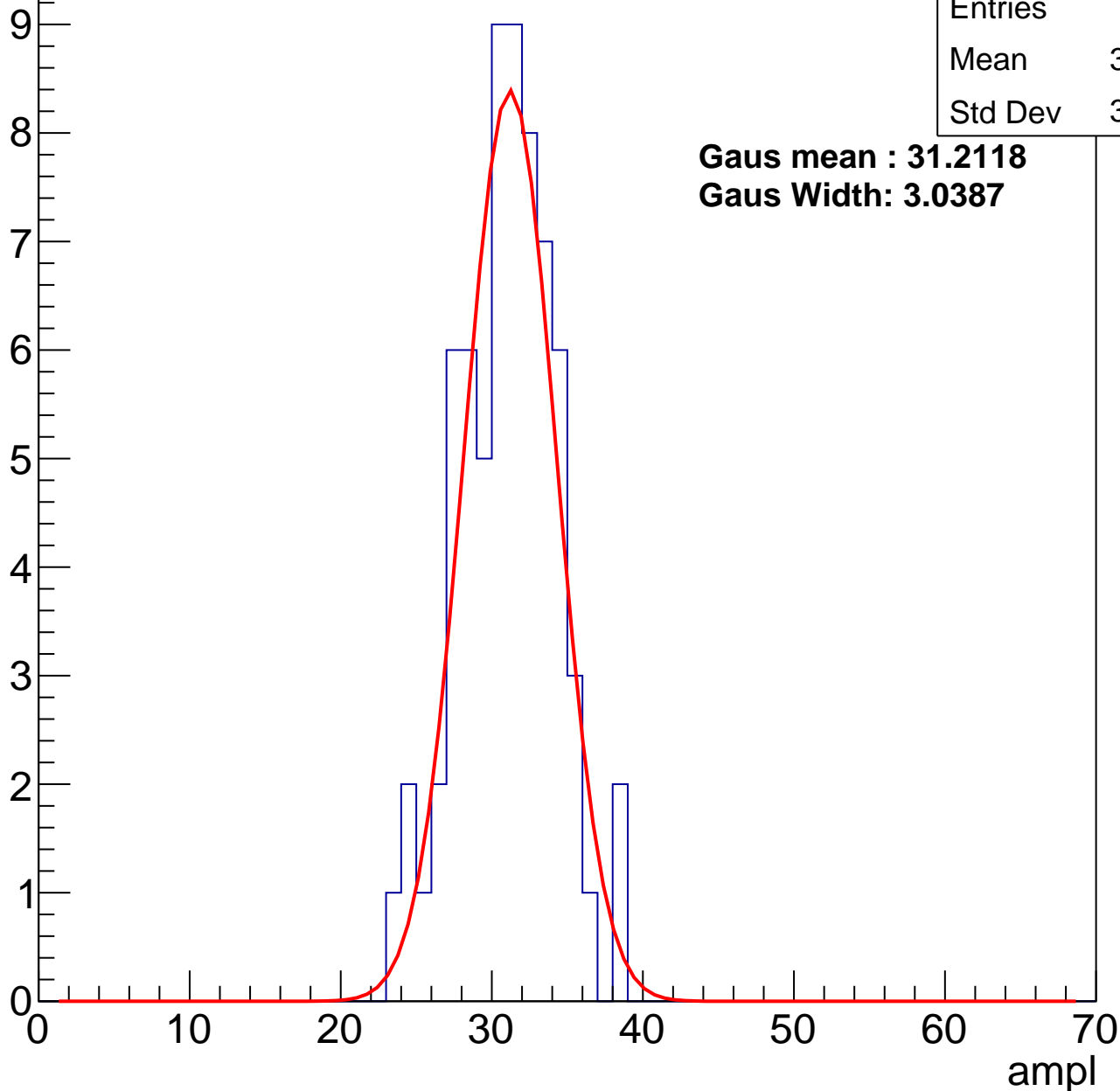
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.59
Std Dev	3.154

Gaus mean : 31.2118

Gaus Width: 3.0387



B0L000S, U2-ch10, adc1

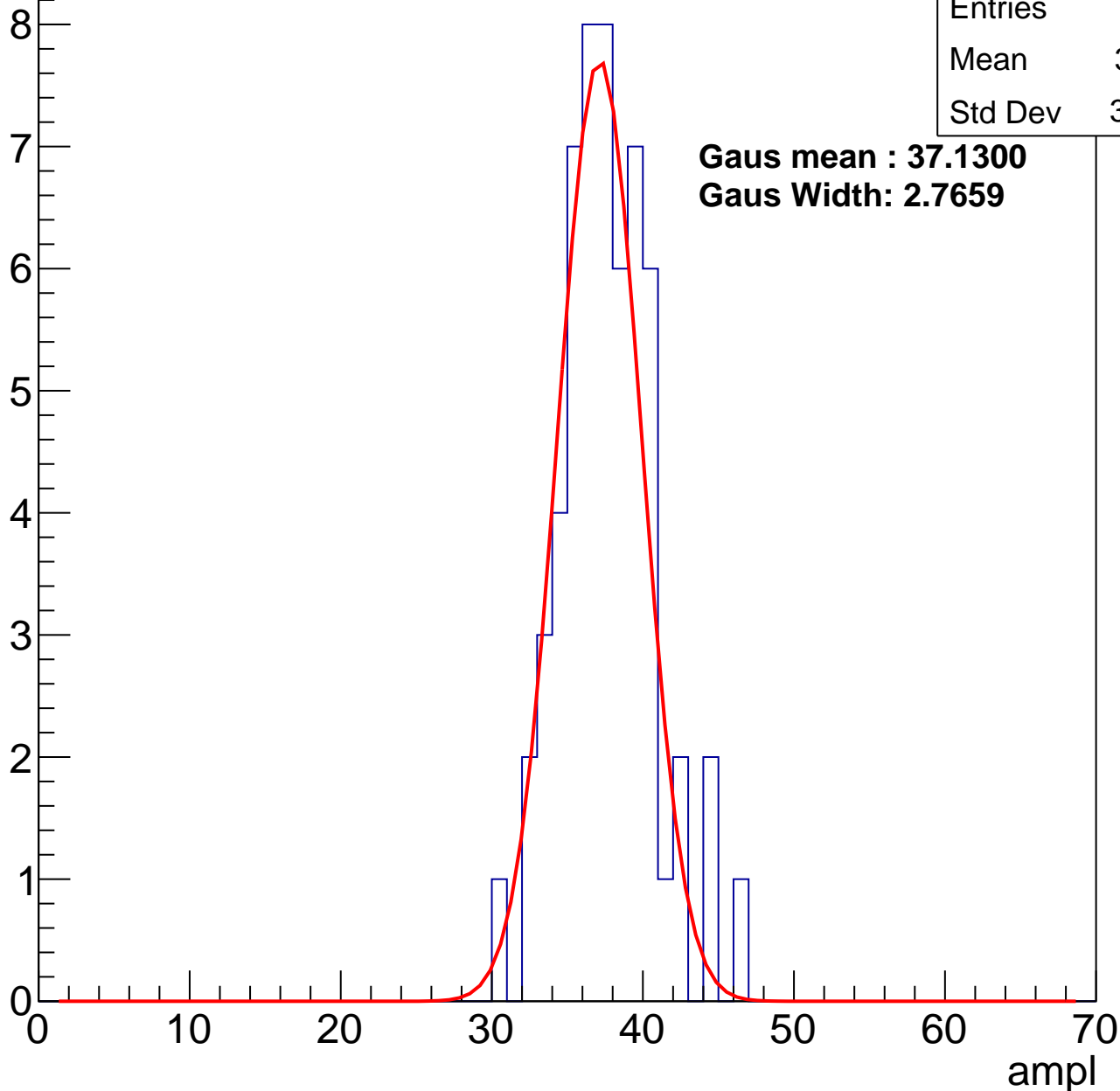
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	37.21
Std Dev	3.078

Gaus mean : 37.1300

Gaus Width: 2.7659



B0L000S, U2-ch10, adc2

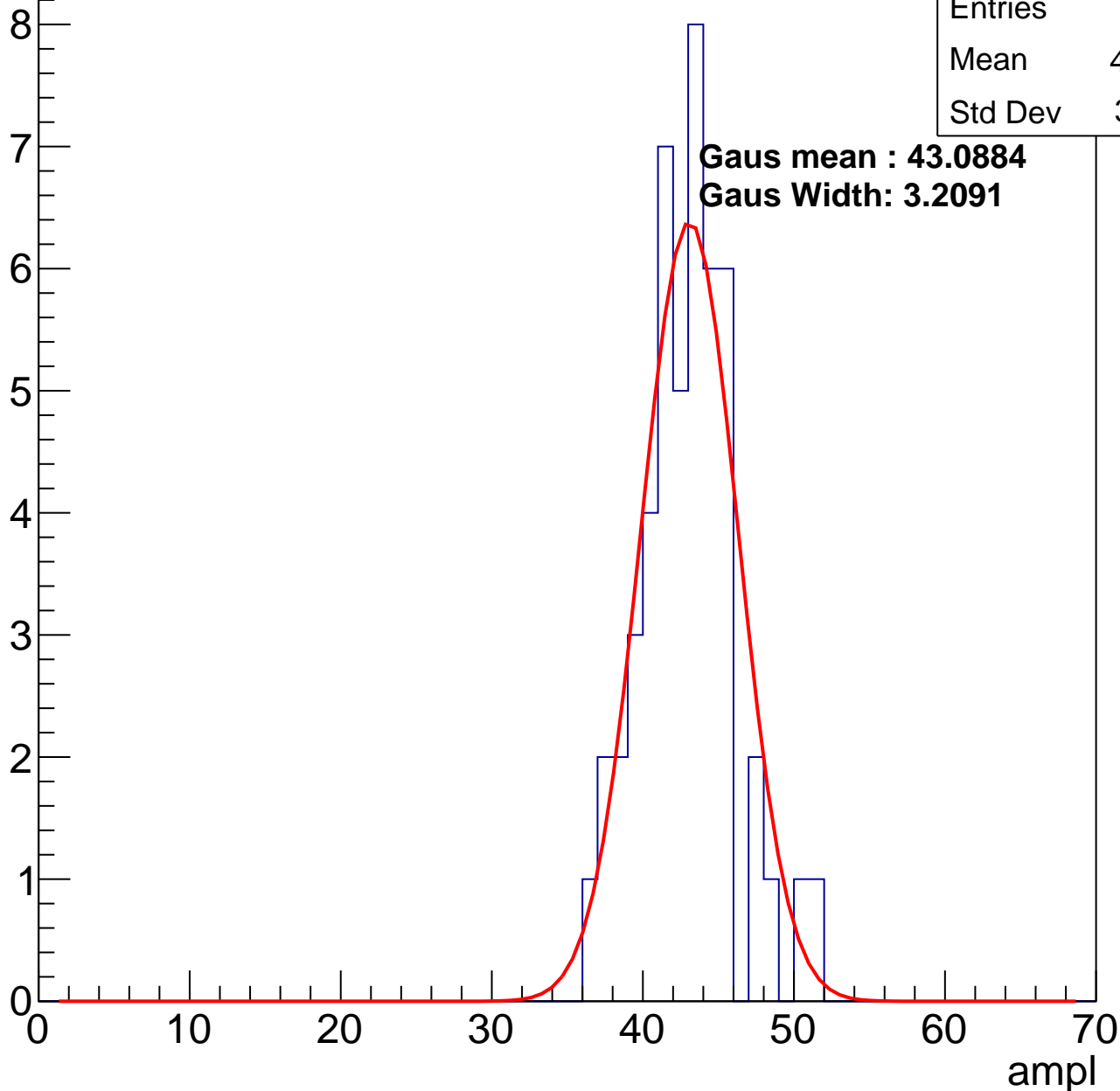
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	42.47
Std Dev	3.111

Gaus mean : 43.0884

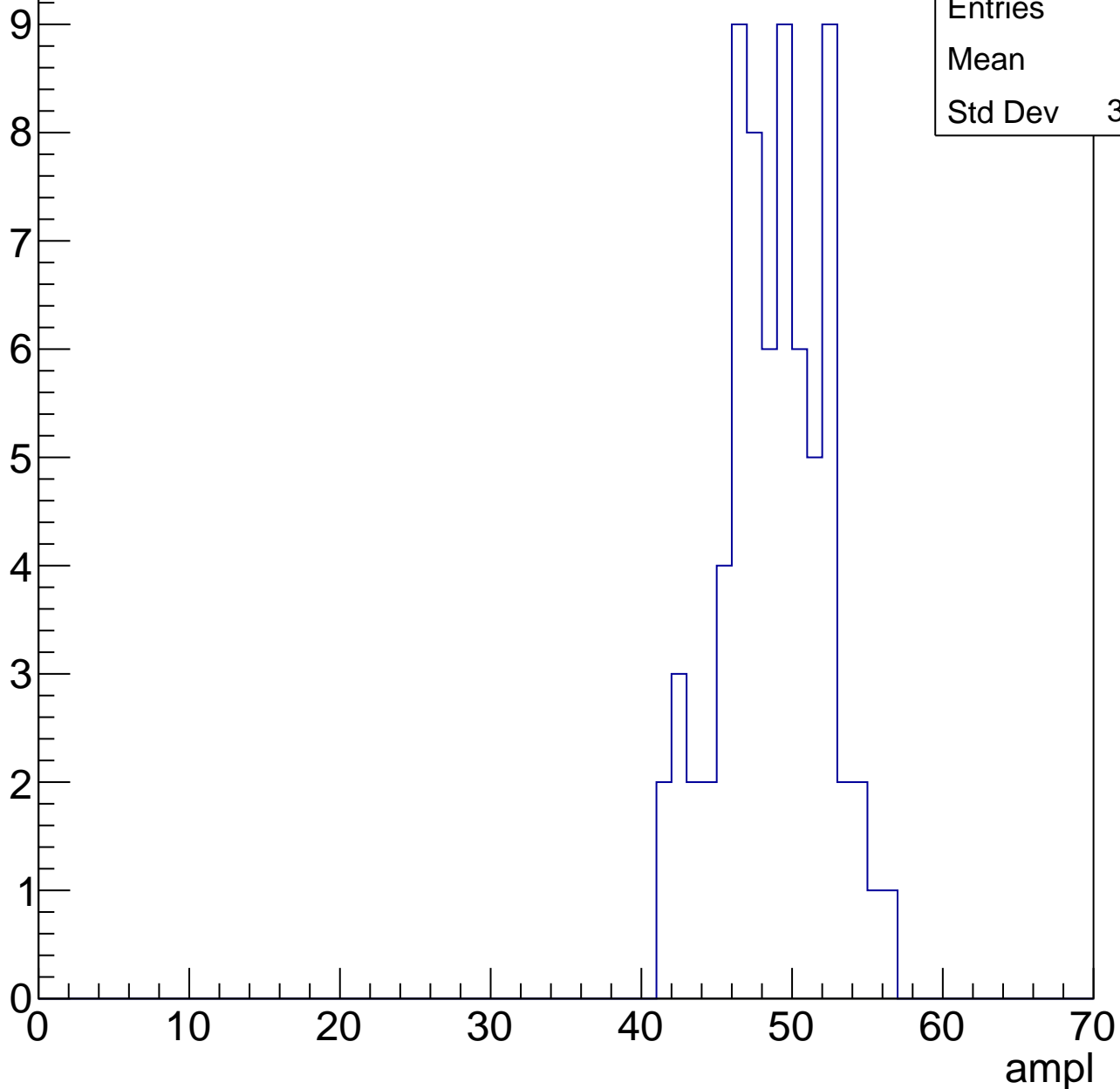
Gaus Width: 3.2091



B0L000S, U2-ch10, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

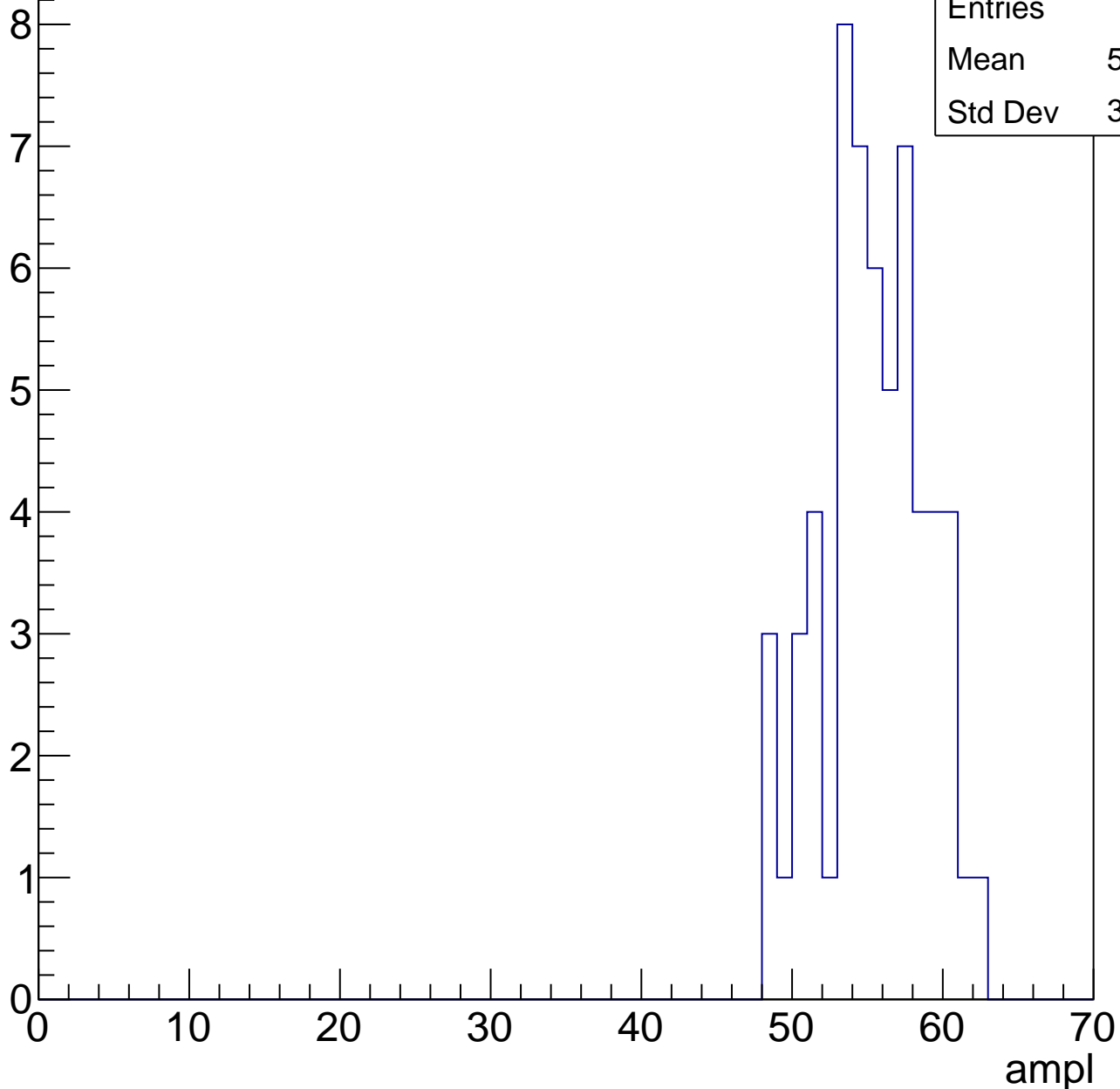


B0L000S, U2-ch10, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54.93
Std Dev	3.414

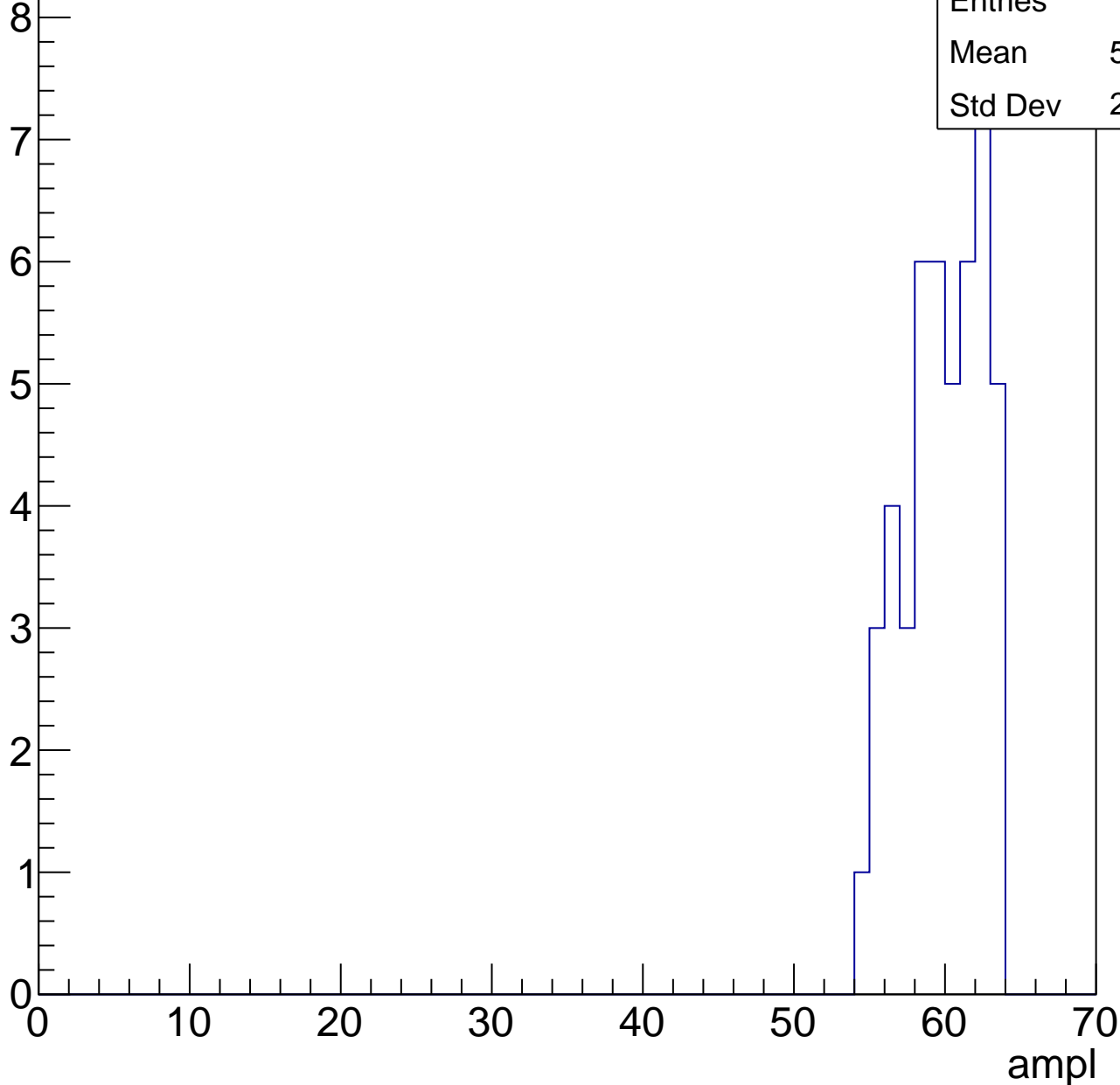


B0L000S, U2-ch10, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

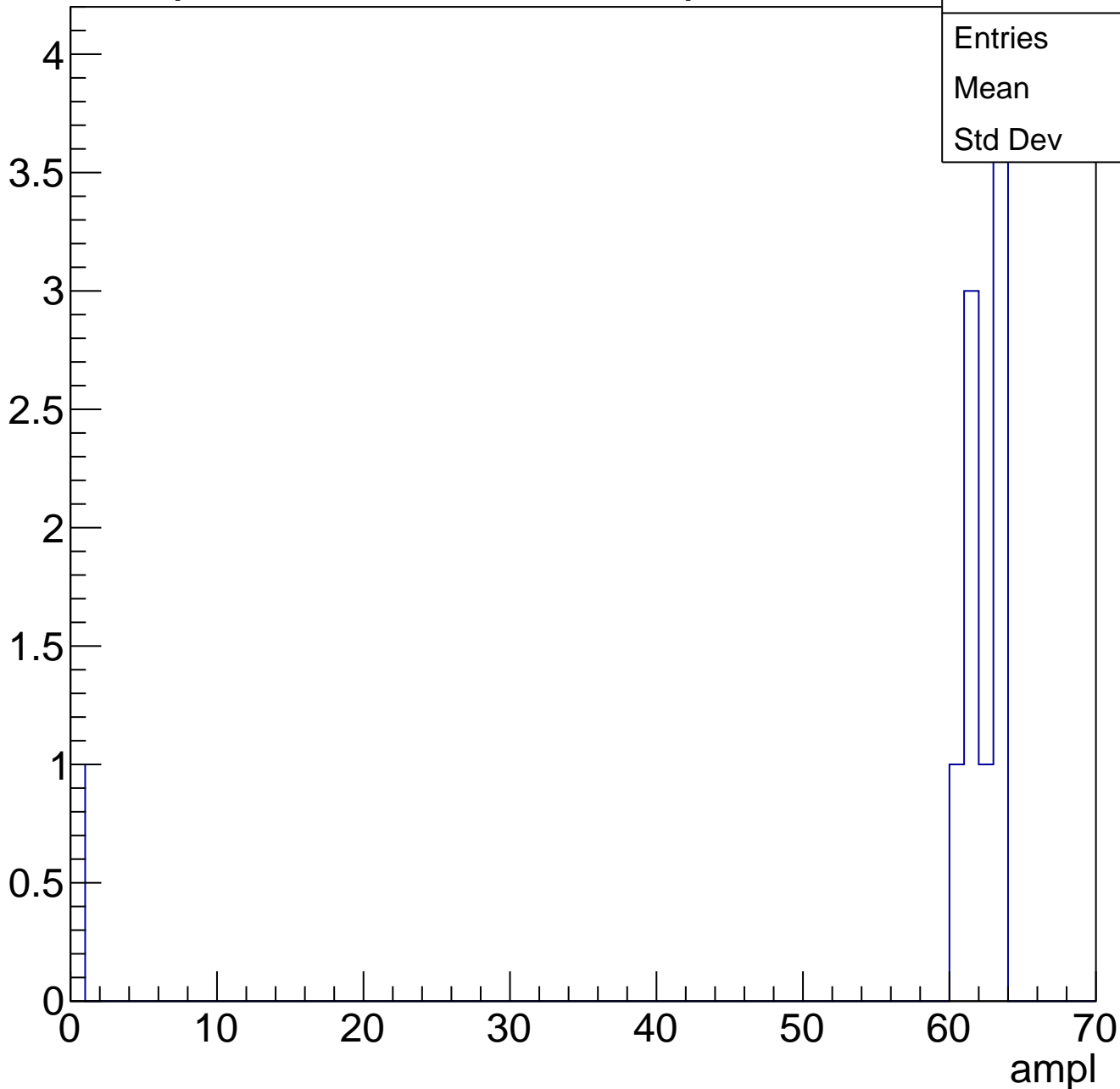
Entries	47
Mean	59.43
Std Dev	2.524



B0L000S, U2-ch10, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch10, adc7

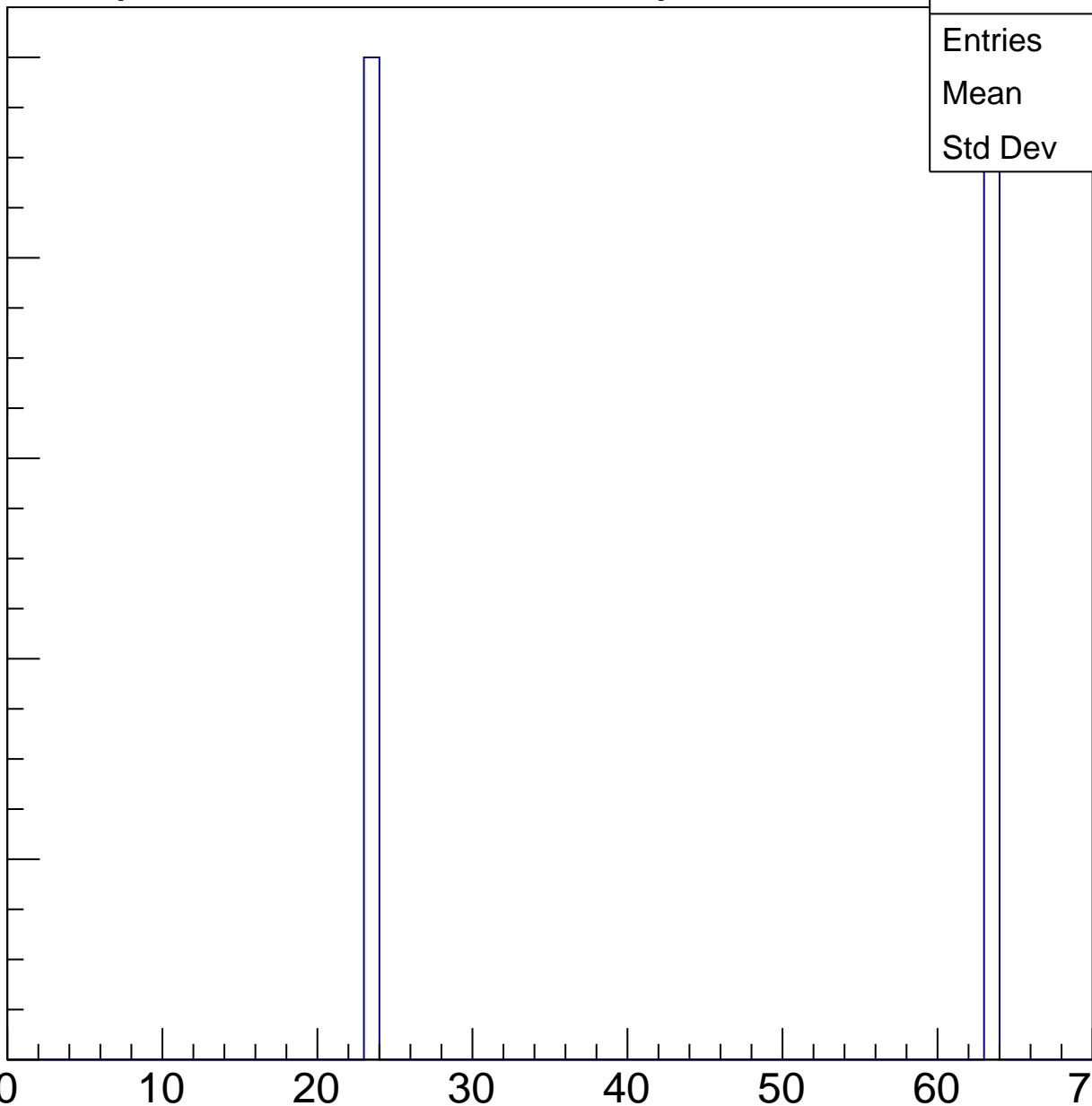
calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl



B0L000S, U2-ch11, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	91
Mean	27.55
Std Dev	5.565

Gaus mean : 29.0059

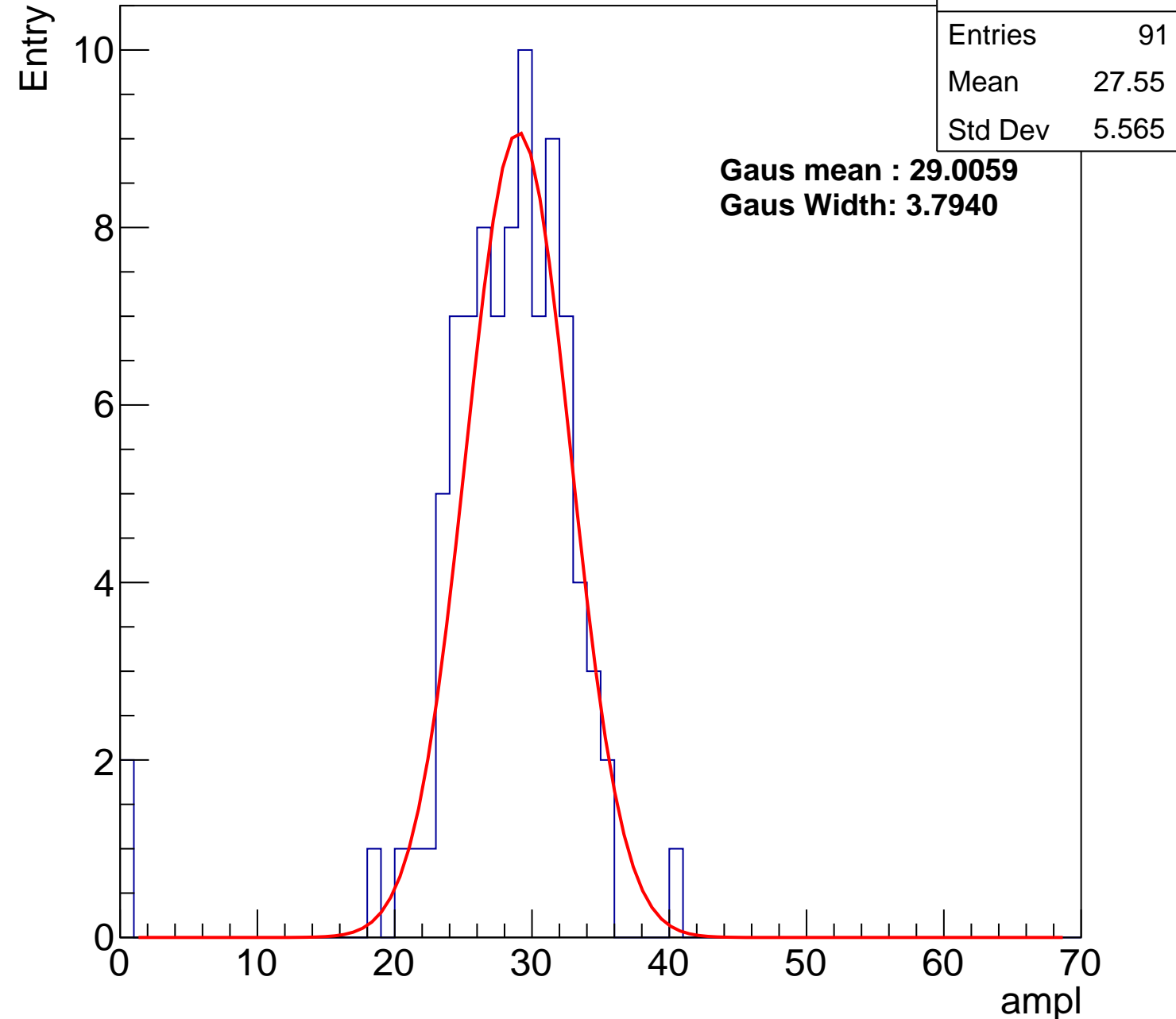
Gaus Width: 3.7940

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch11, adc1

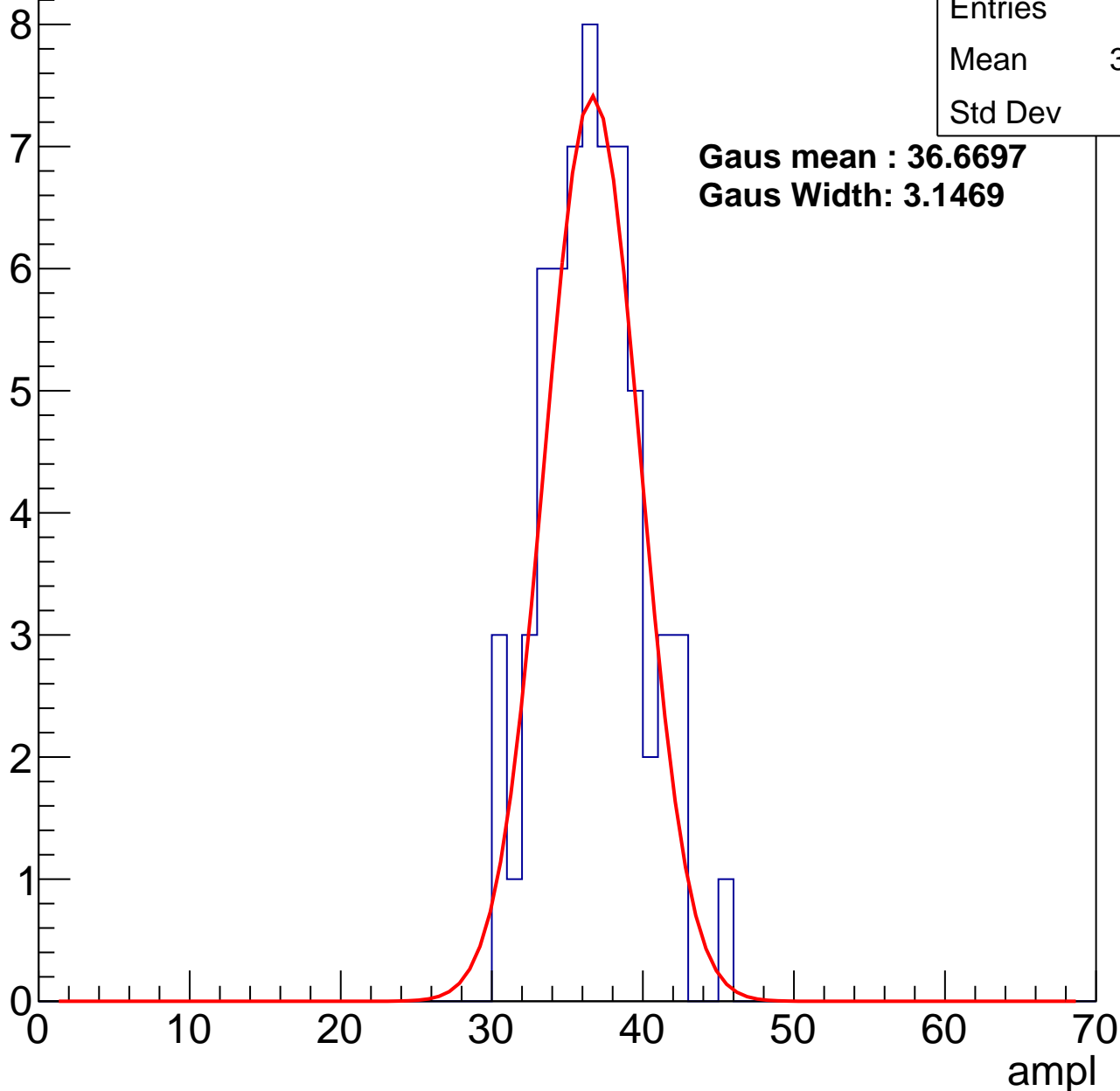
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.23
Std Dev	3.21

Gaus mean : 36.6697

Gaus Width: 3.1469



B0L000S, U2-ch11, adc2

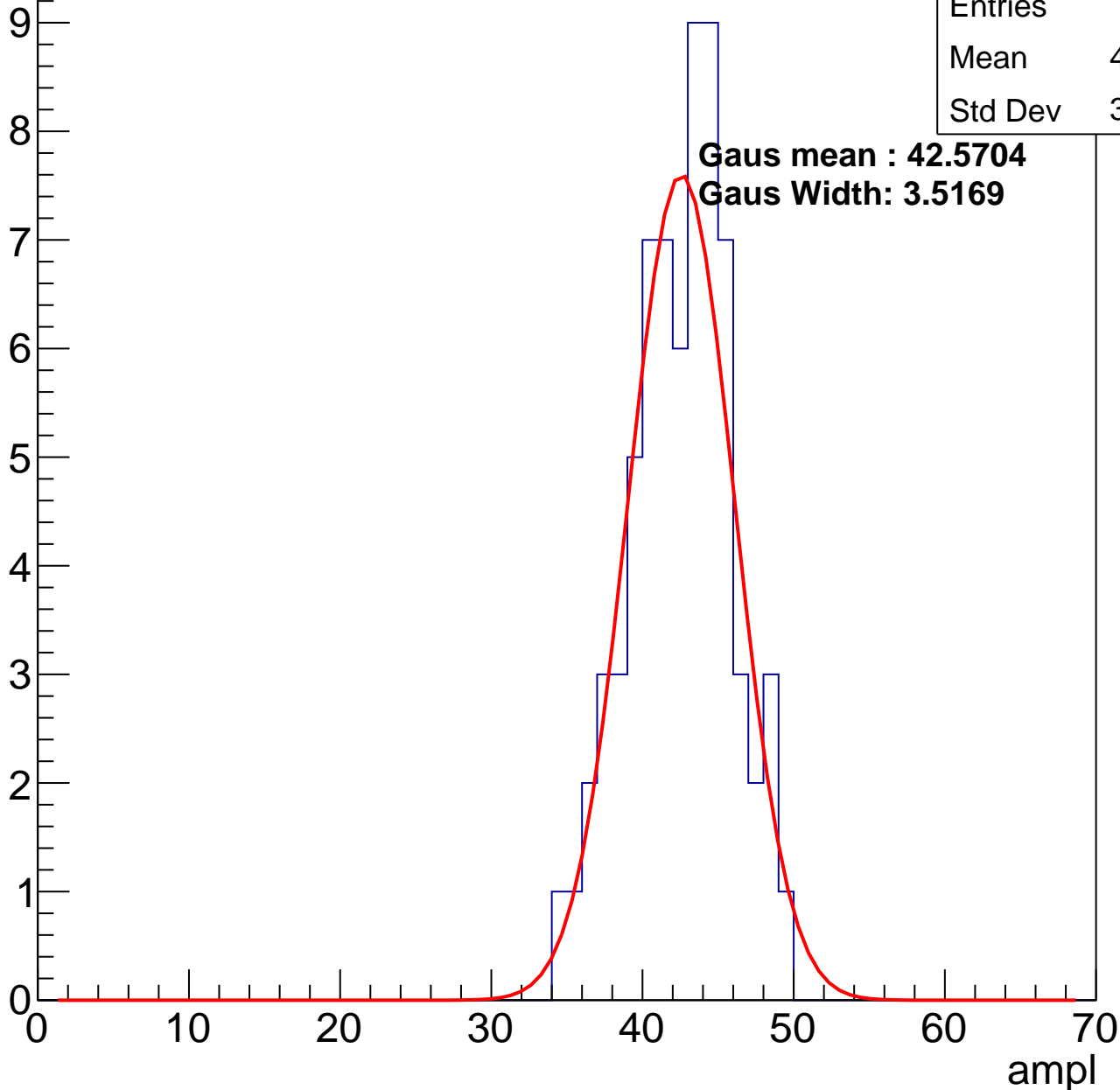
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.07
Std Dev	3.298

Gaus mean : 42.5704

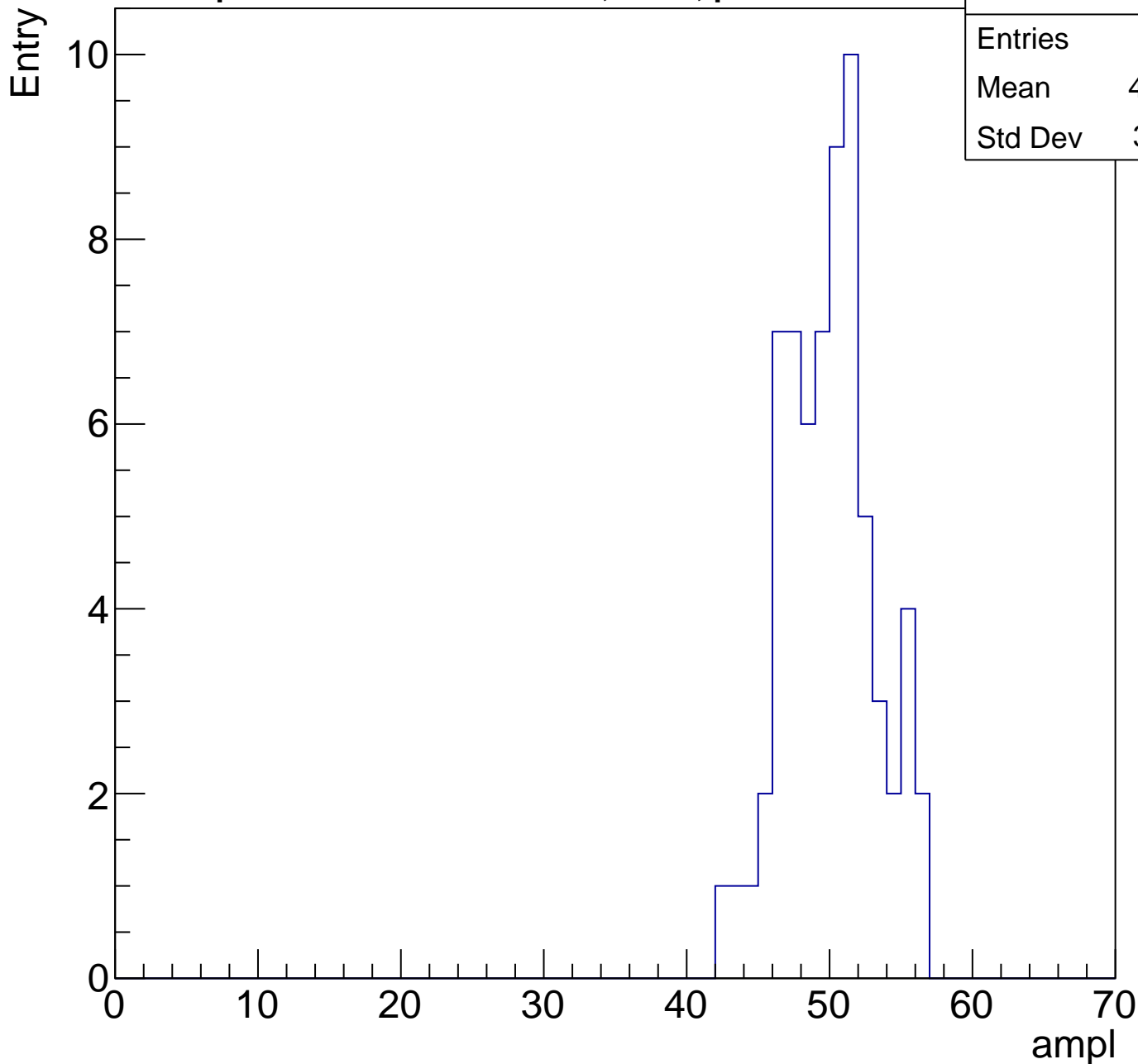
Gaus Width: 3.5169



B0L000S, U2-ch11, adc3

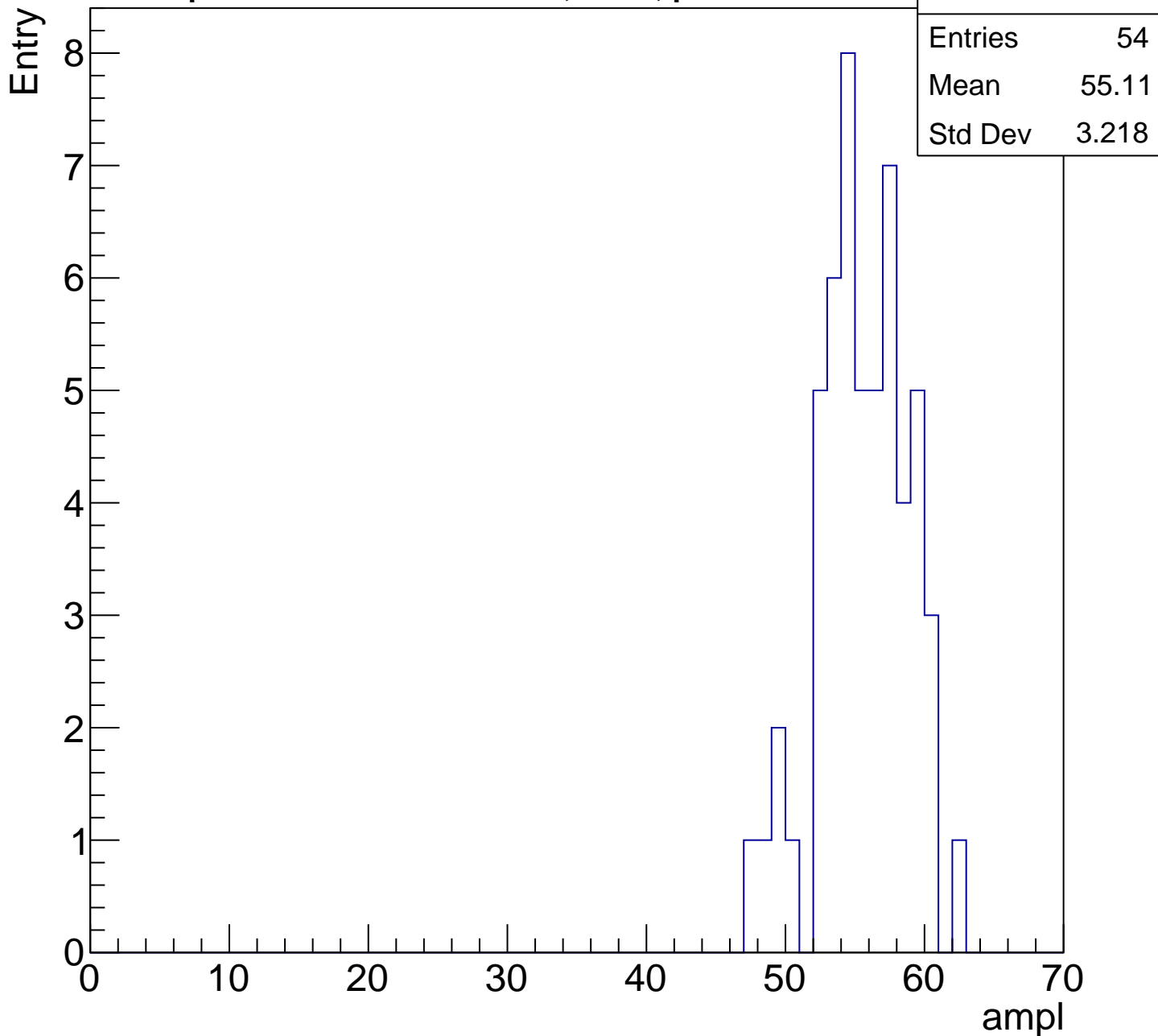
calib_packv5_042523_0143.root, FC#5, port B1

Entries	67
Mean	49.55
Std Dev	3.121



B0L000S, U2-ch11, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch11, adc5

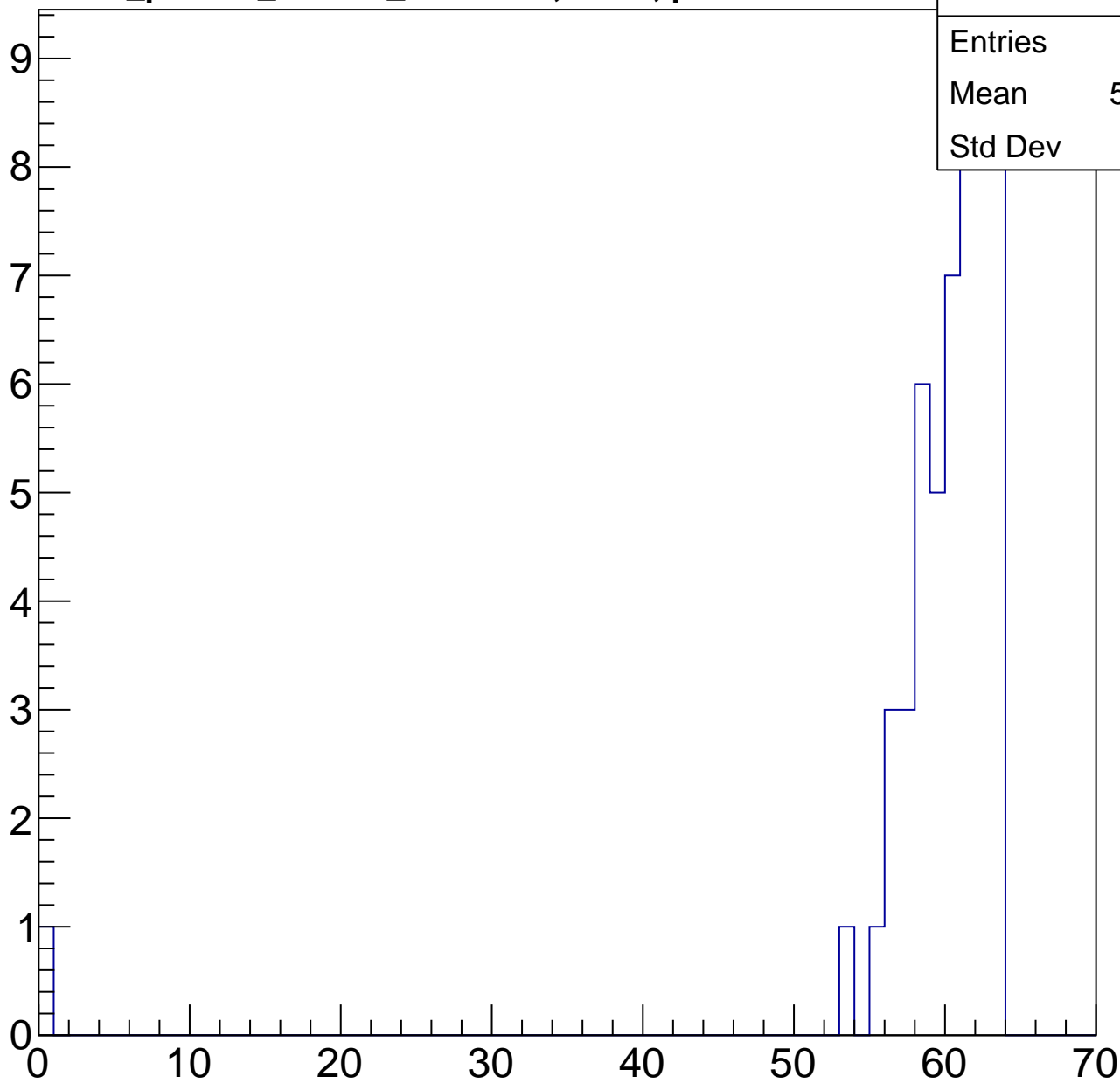
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.89
Std Dev	8.5

ampl



B0L000S, U2-ch11, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch11, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch12, adc0

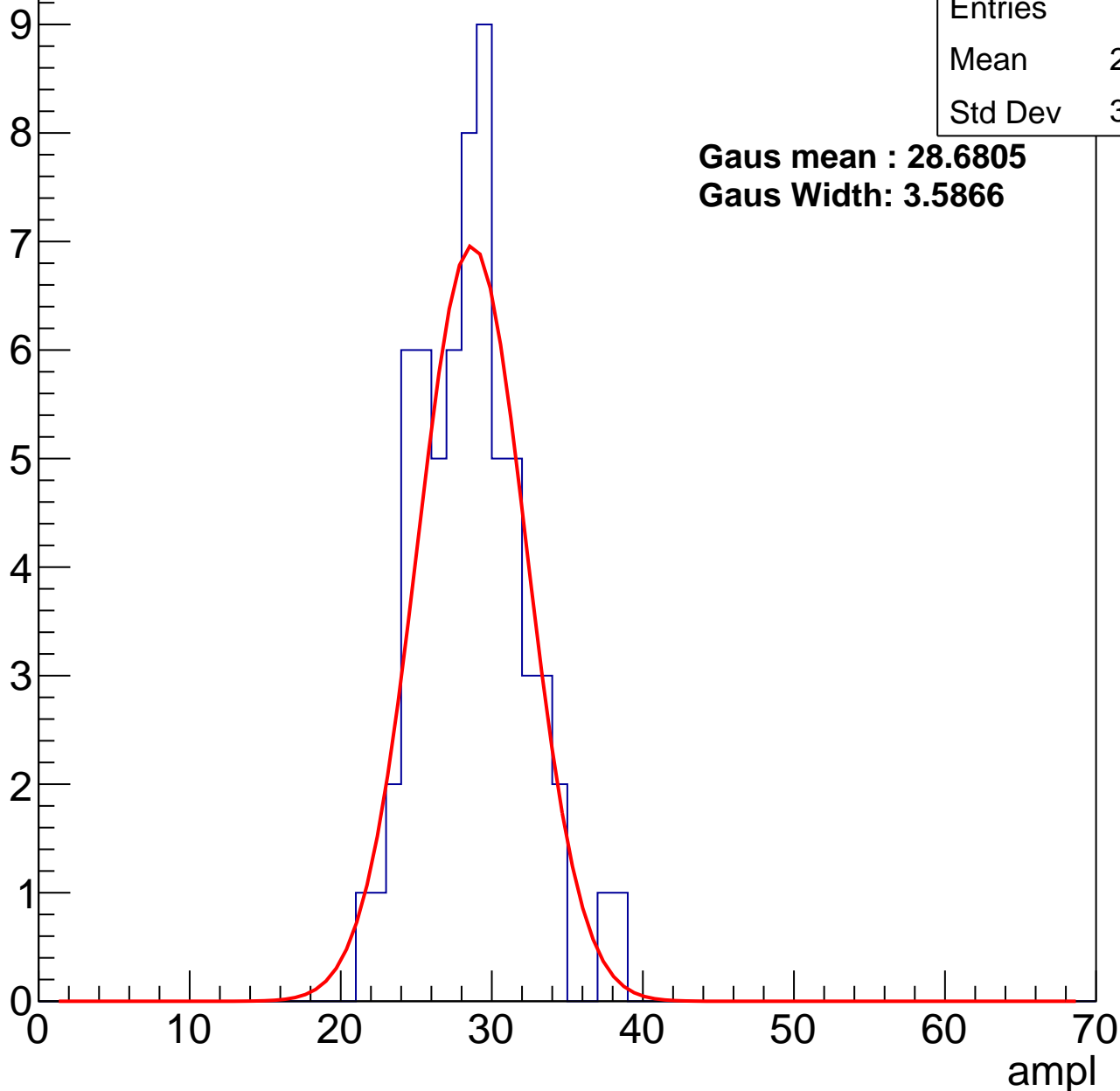
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	28.17
Std Dev	3.444

Gaus mean : 28.6805

Gaus Width: 3.5866



B0L000S, U2-ch12, adc1

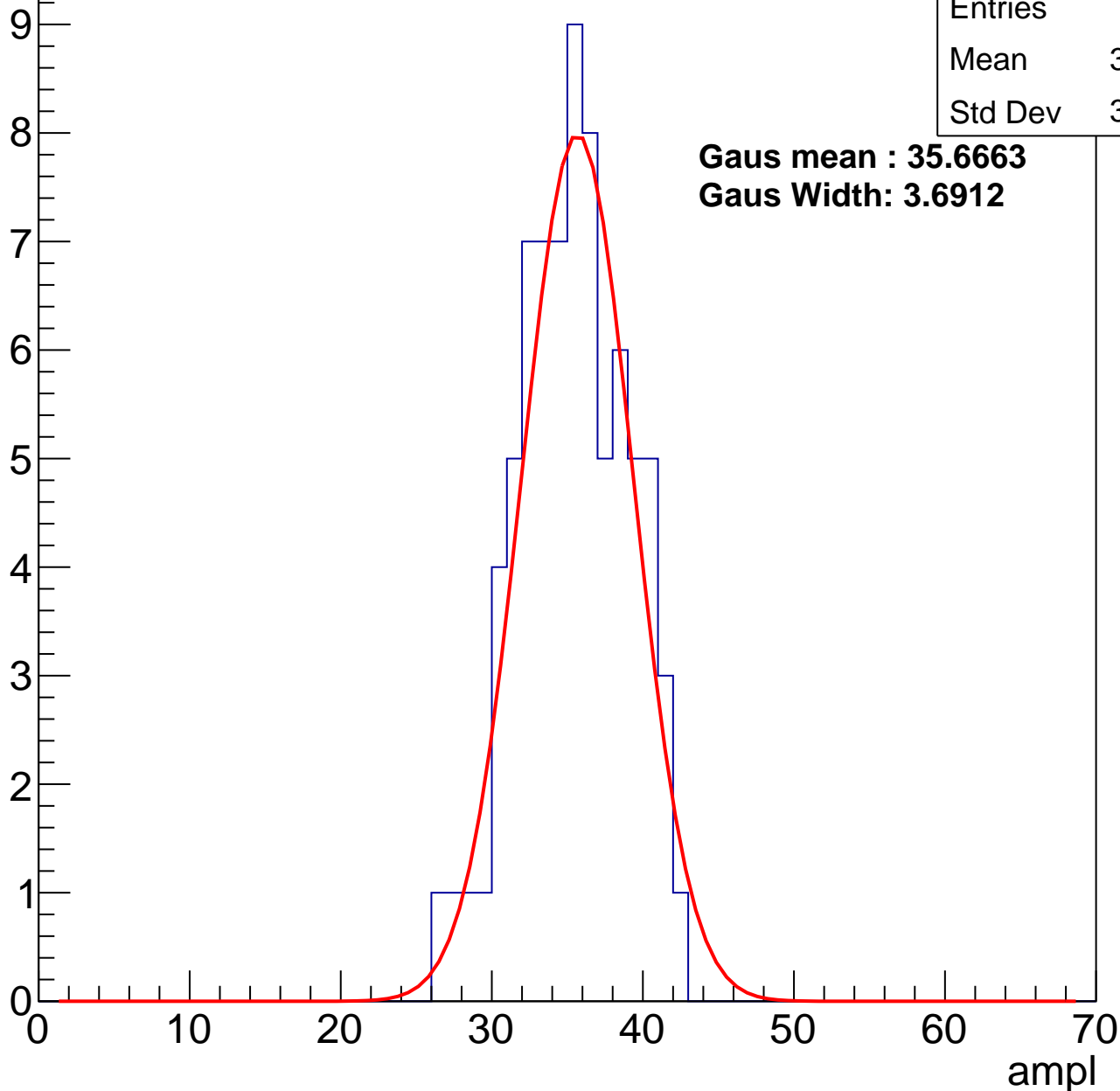
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	34.92
Std Dev	3.538

Gaus mean : 35.6663

Gaus Width: 3.6912



B0L000S, U2-ch12, adc2

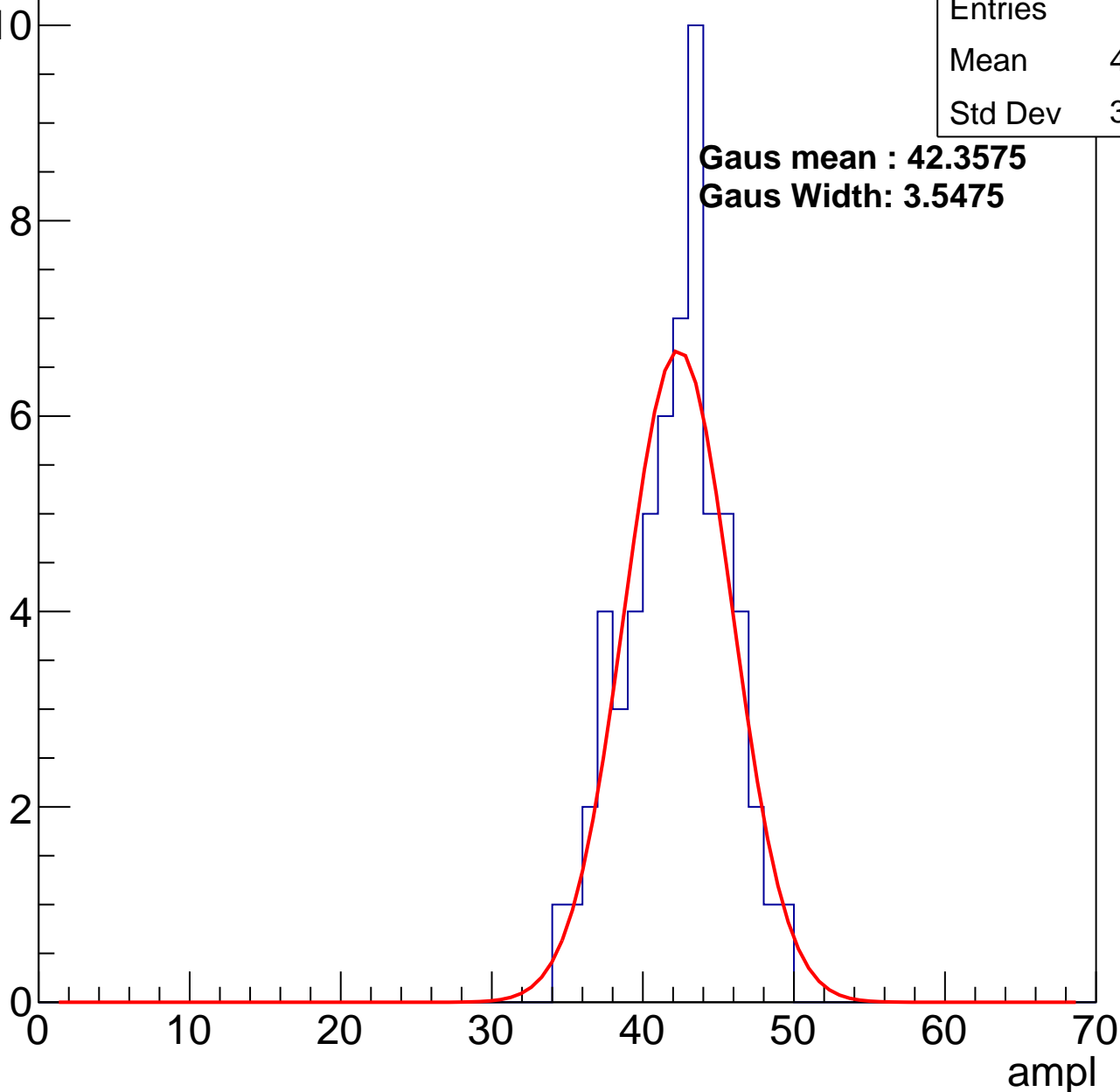
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	41.79
Std Dev	3.305

Gaus mean : 42.3575

Gaus Width: 3.5475

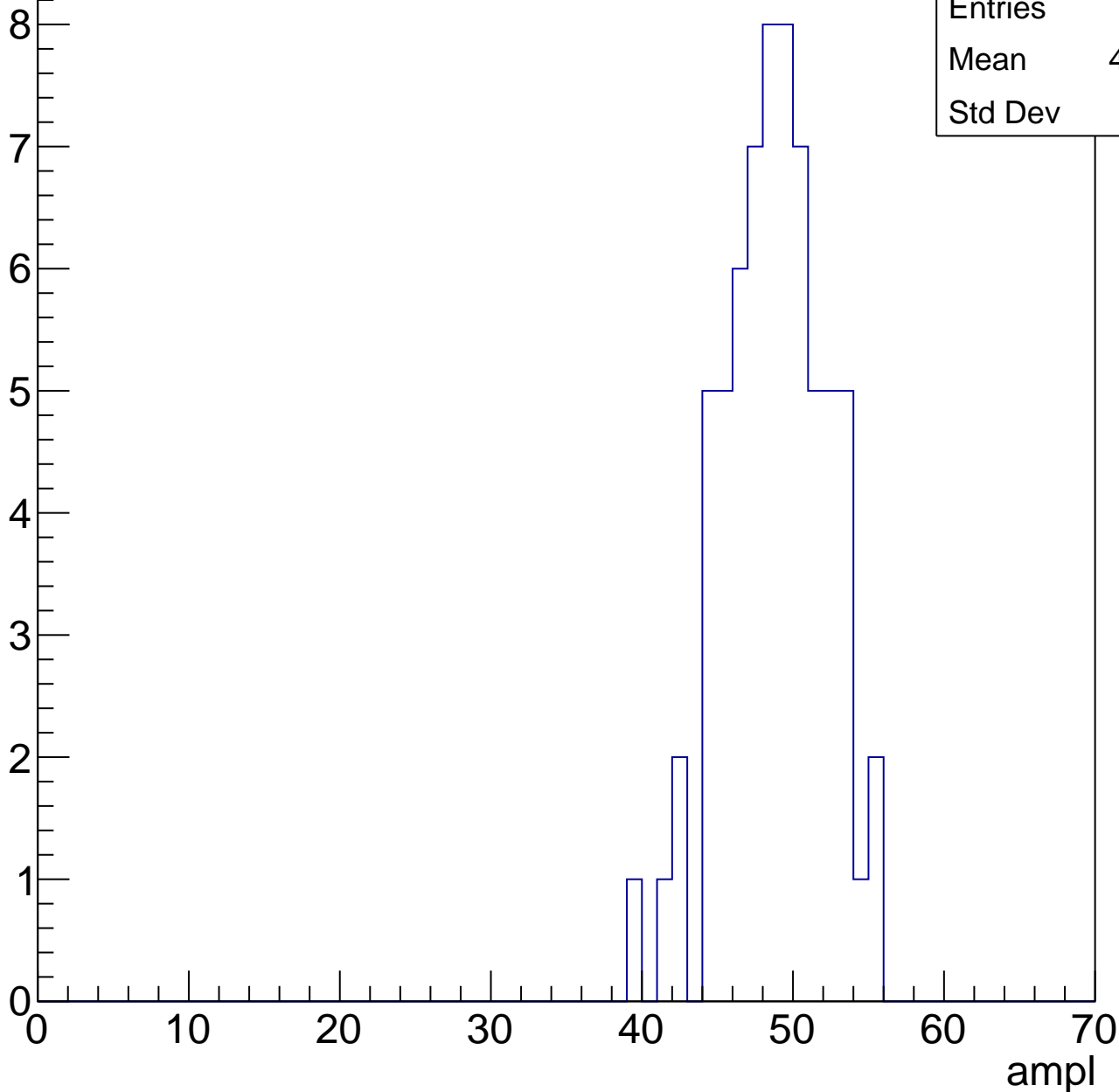


B0L000S, U2-ch12, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

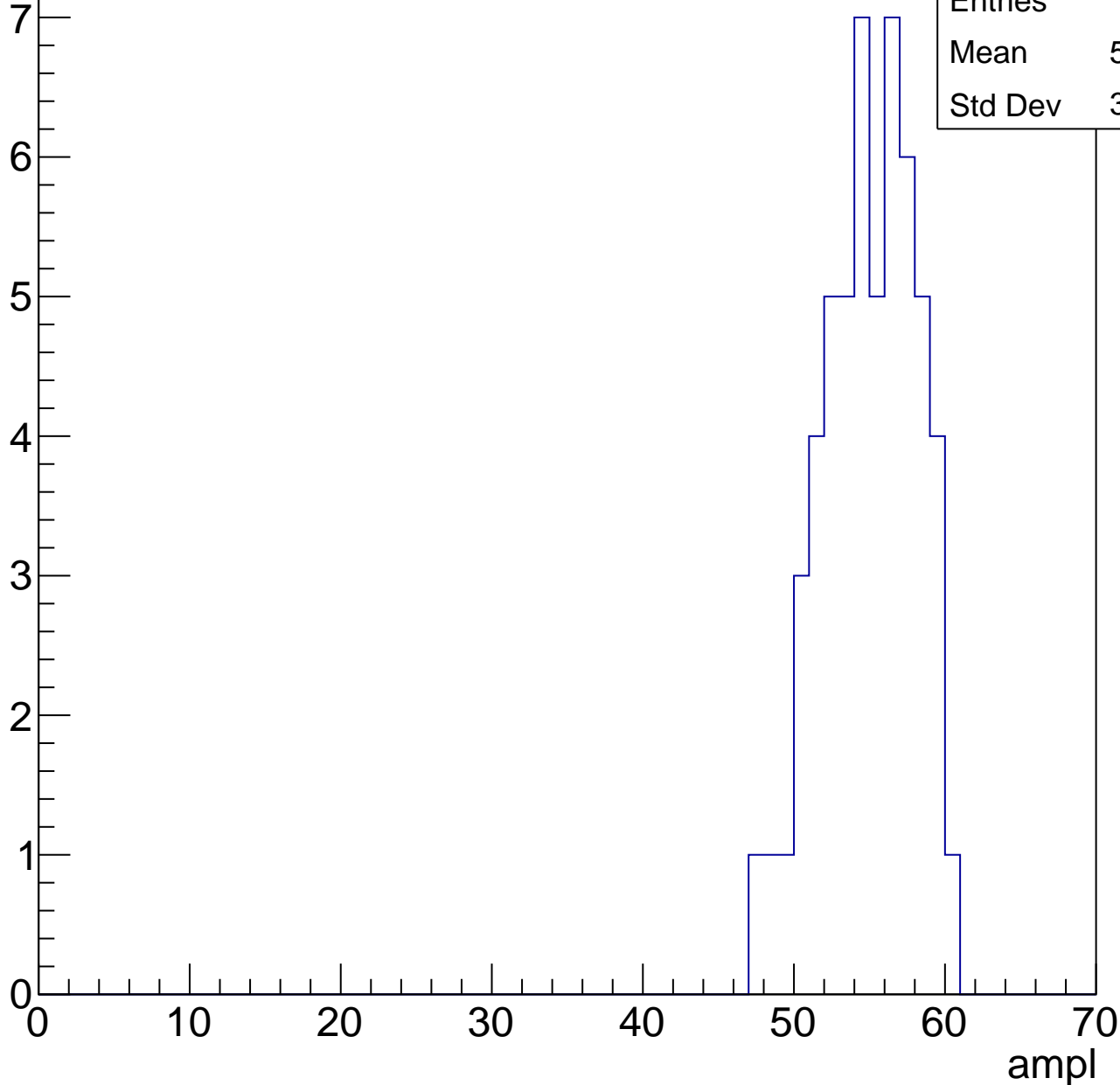
Entries	68
Mean	48.29
Std Dev	3.37



B0L000S, U2-ch12, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	55
Mean	54.47
Std Dev	3.044

B0L000S, U2-ch12, adc5

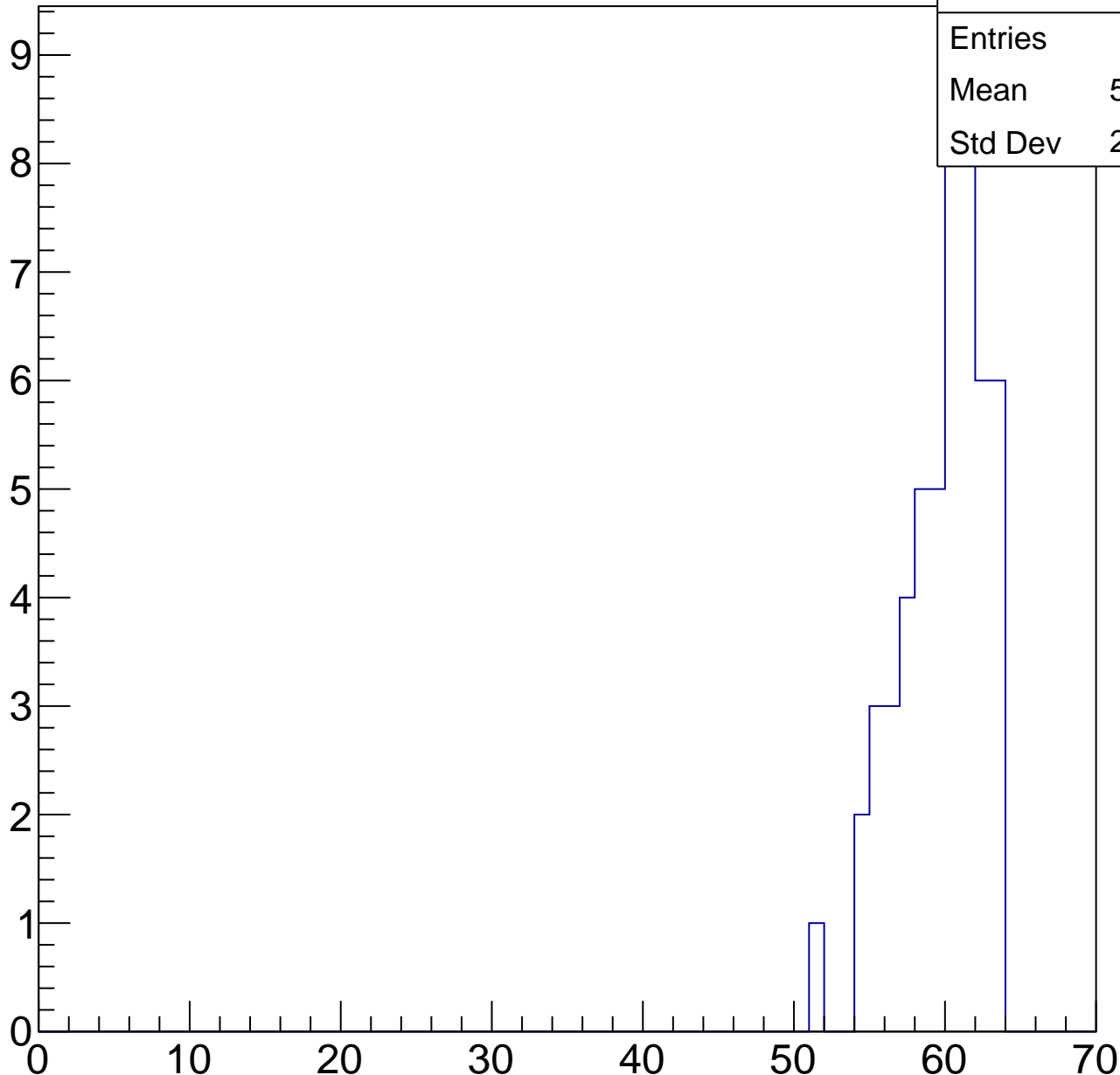
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.32
Std Dev	2.746

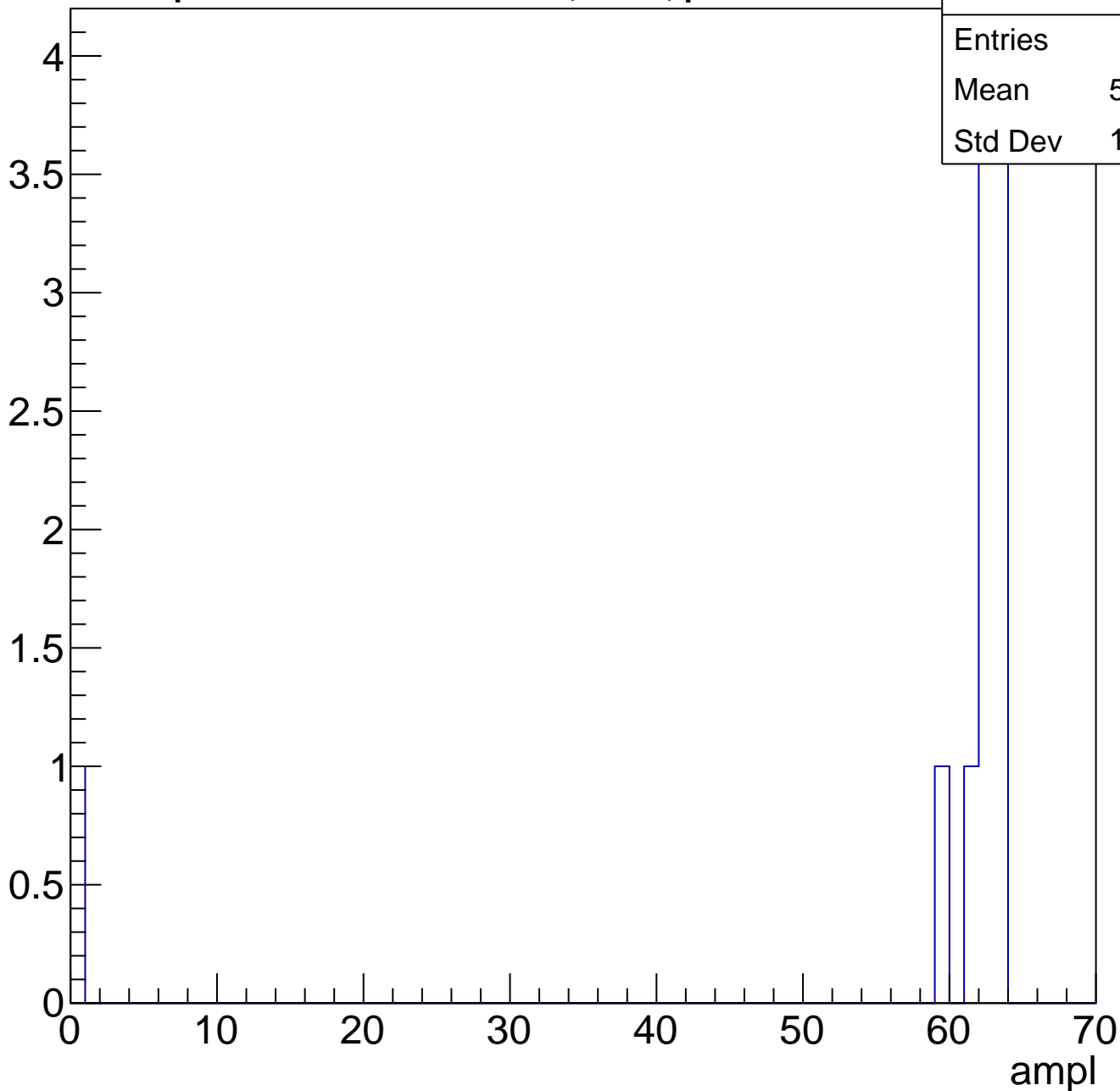
ampl



B0L000S, U2-ch12, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch12, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch13, adc0

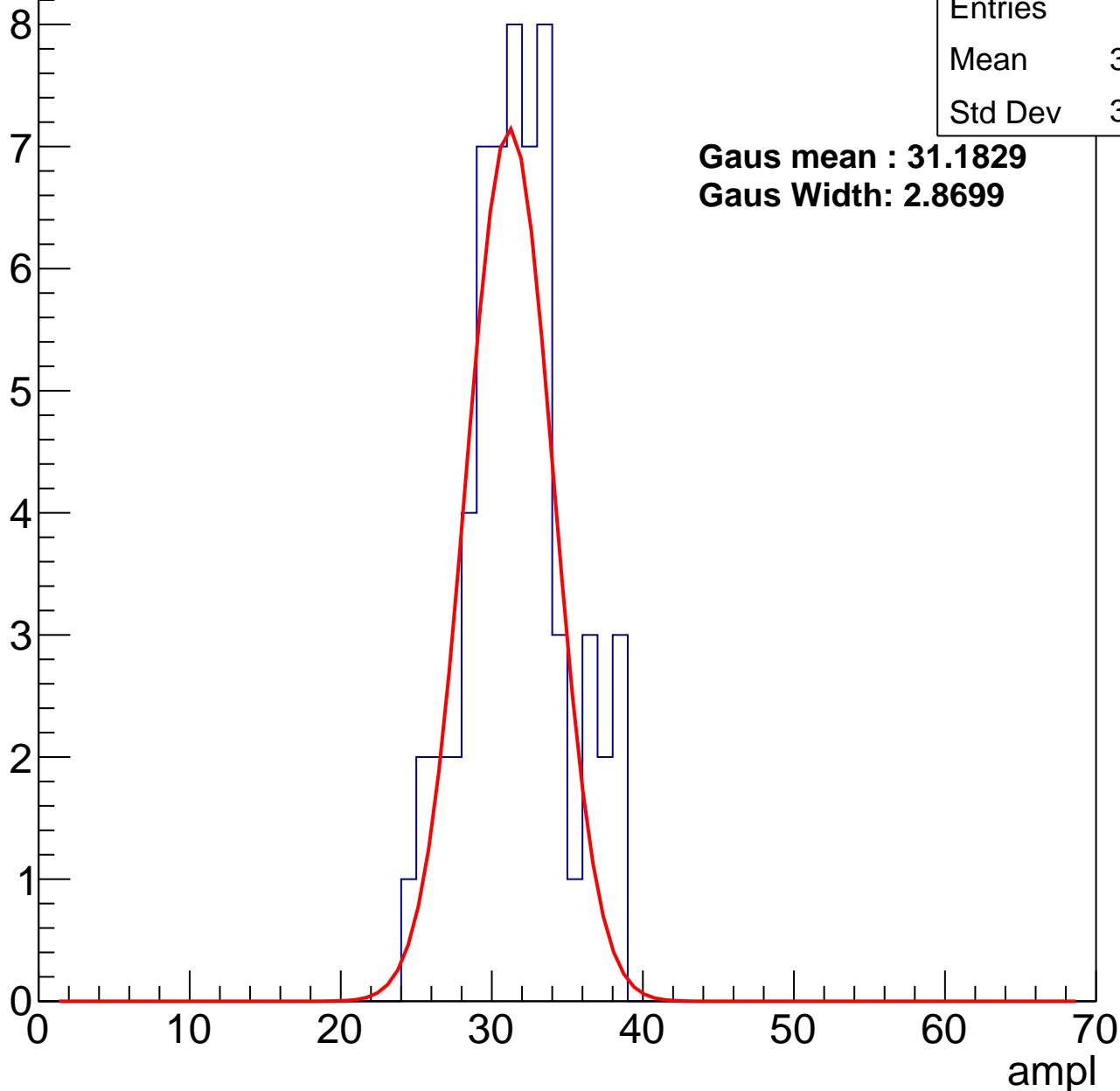
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	31.23
Std Dev	3.283

Gaus mean : 31.1829

Gaus Width: 2.8699



B0L000S, U2-ch13, adc1

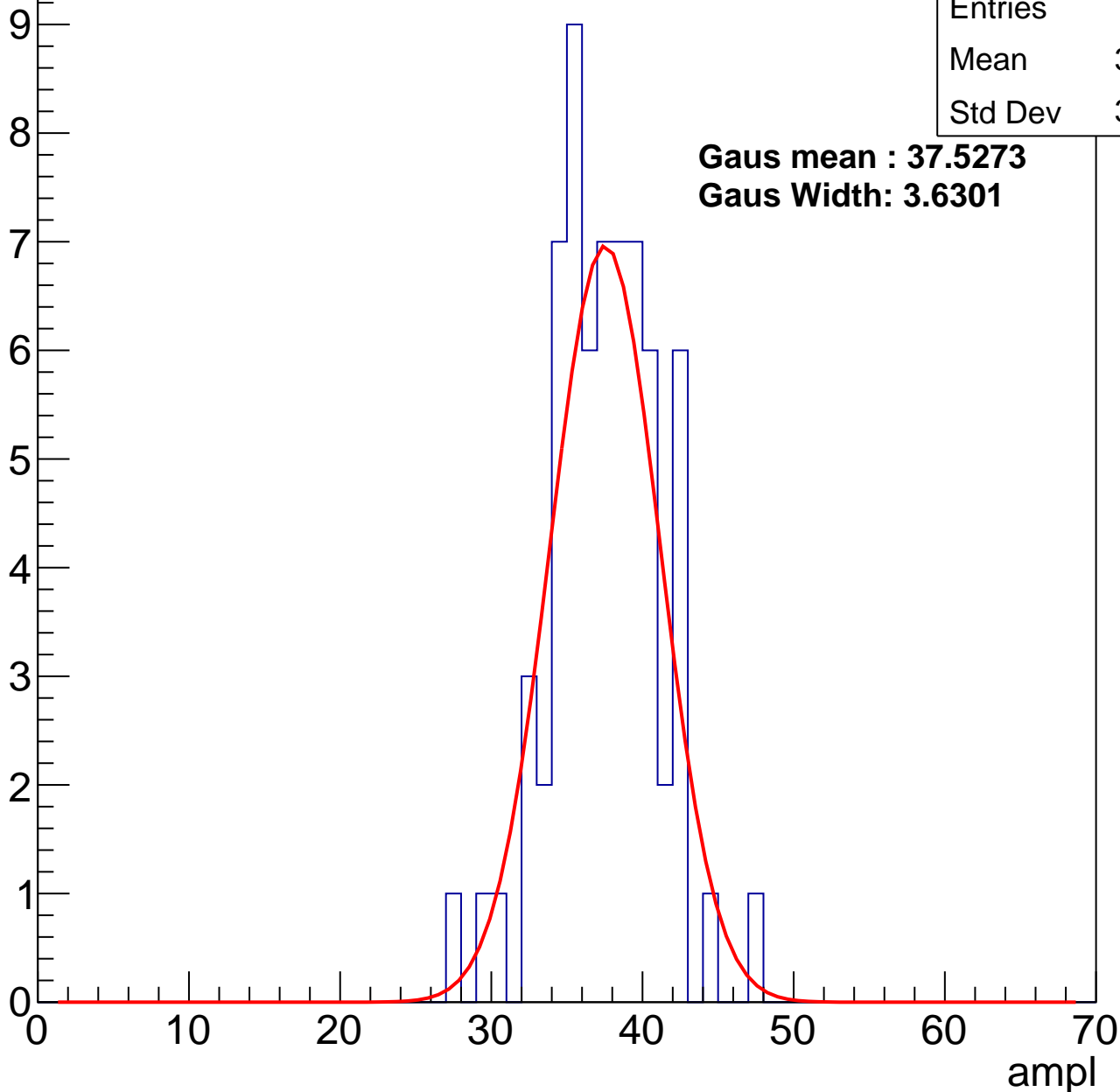
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.01
Std Dev	3.551

Gaus mean : 37.5273

Gaus Width: 3.6301



B0L000S, U2-ch13, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.83
Std Dev	3.102

Gaus mean : 44.0673

Gaus Width: 3.3267

10

8

6

4

2

0

ampl

0

10

20

30

40

50

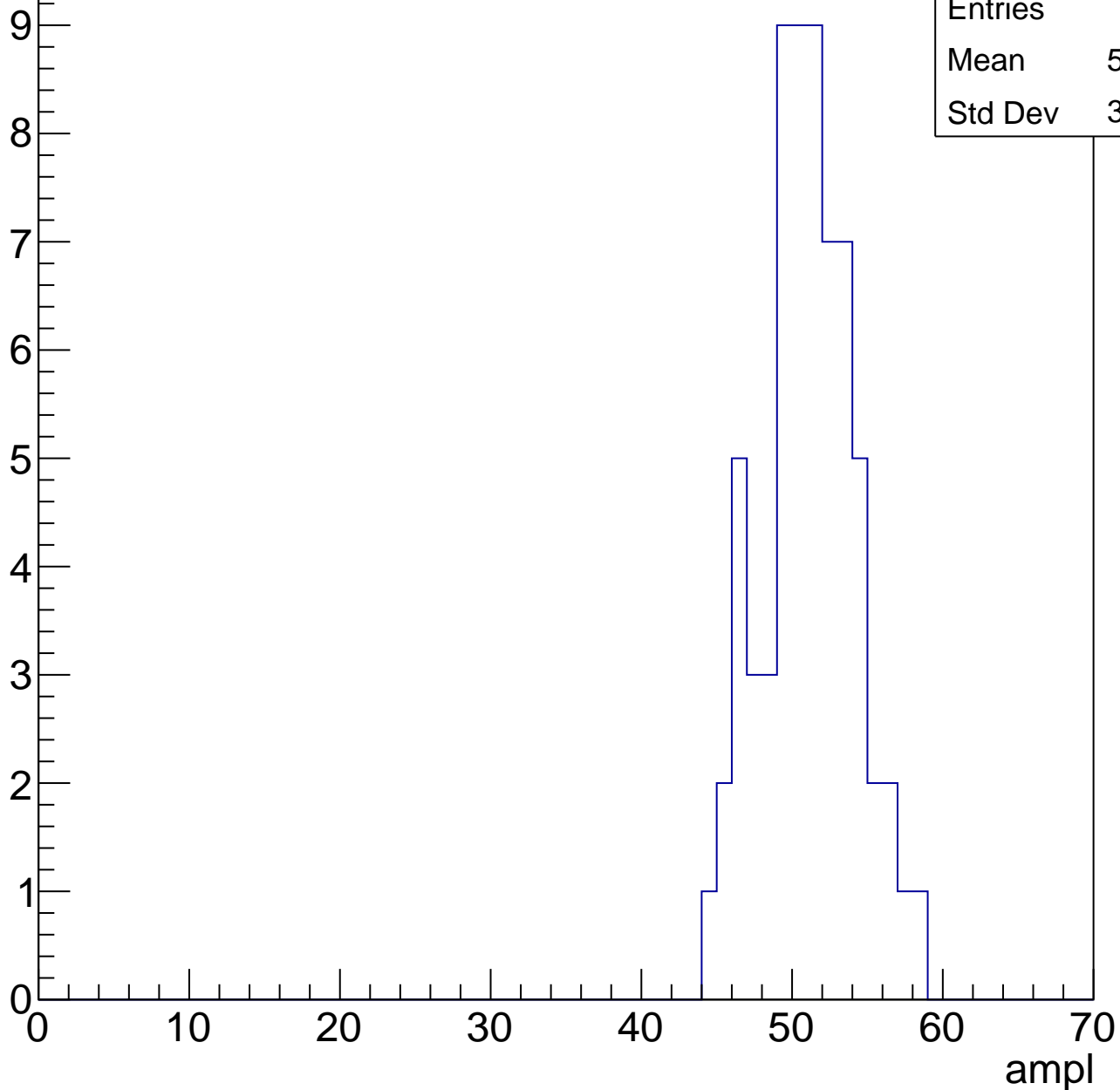
60

70

B0L000S, U2-ch13, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

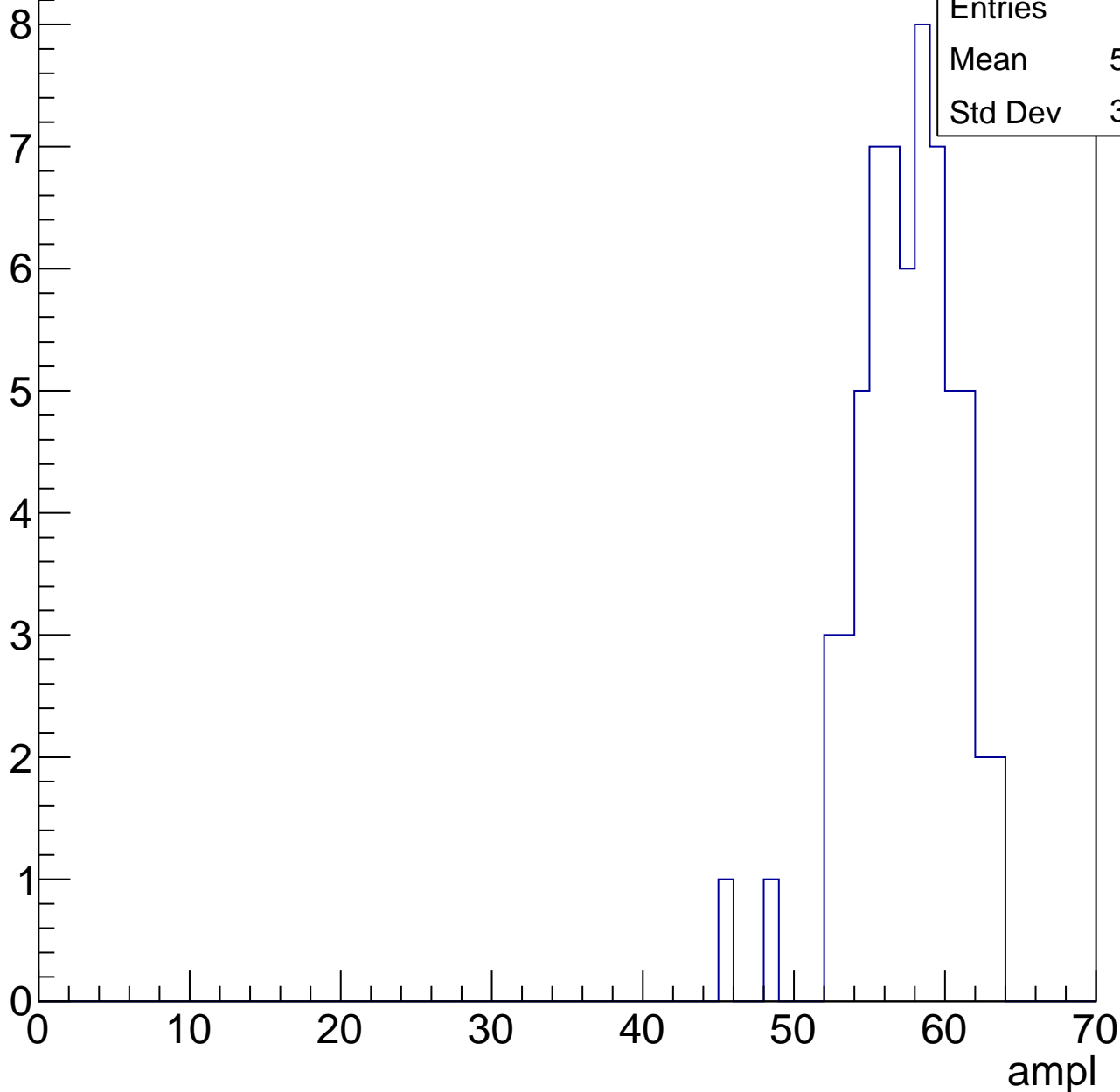


B0L000S, U2-ch13, adc4

calib_packv5_042523_0143.root, FC#5, port B1

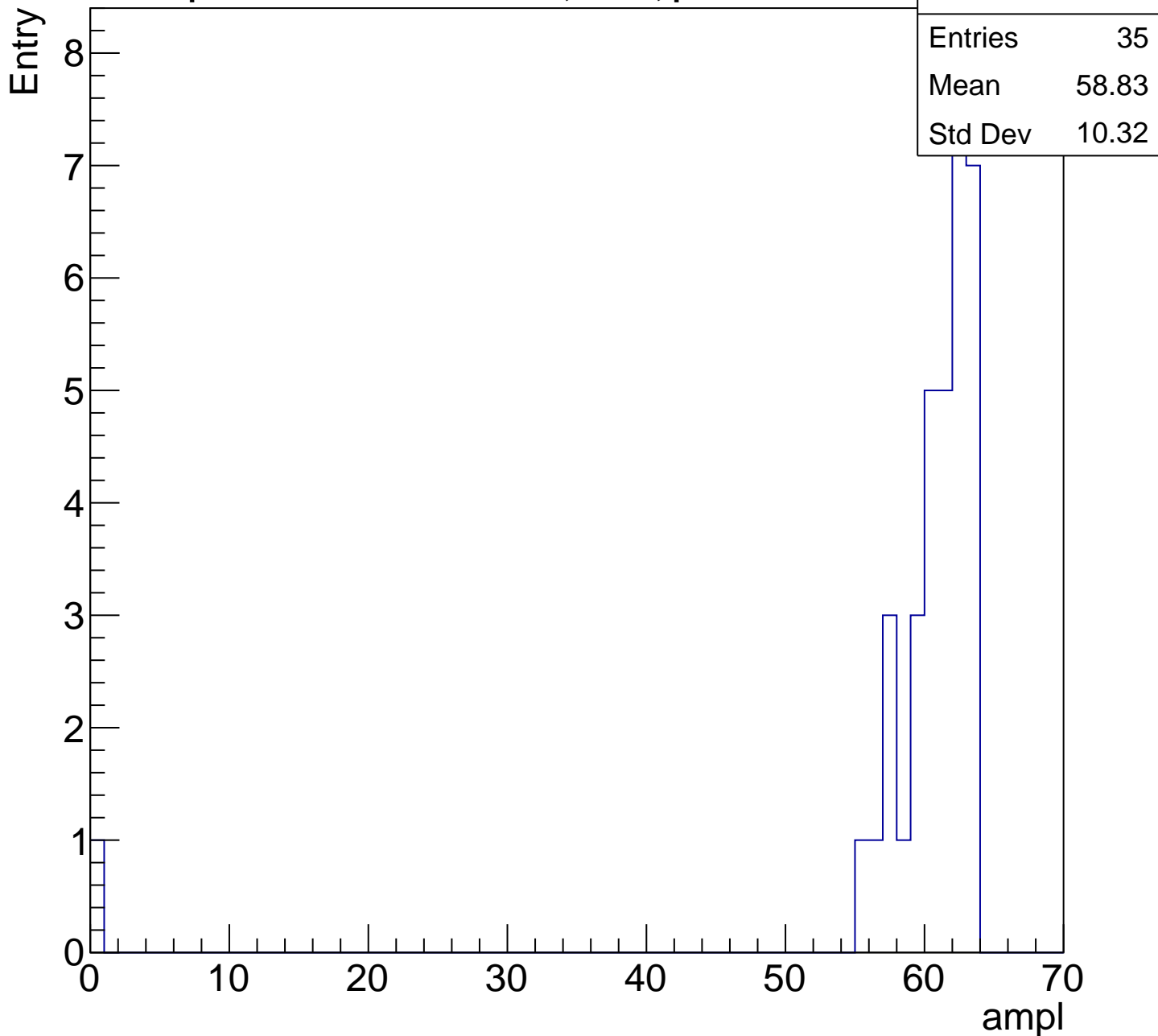
Entry

Entries	62
Mean	56.92
Std Dev	3.385



B0L000S, U2-ch13, adc5

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch13, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B0L000S, U2-ch13, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch14, adc0

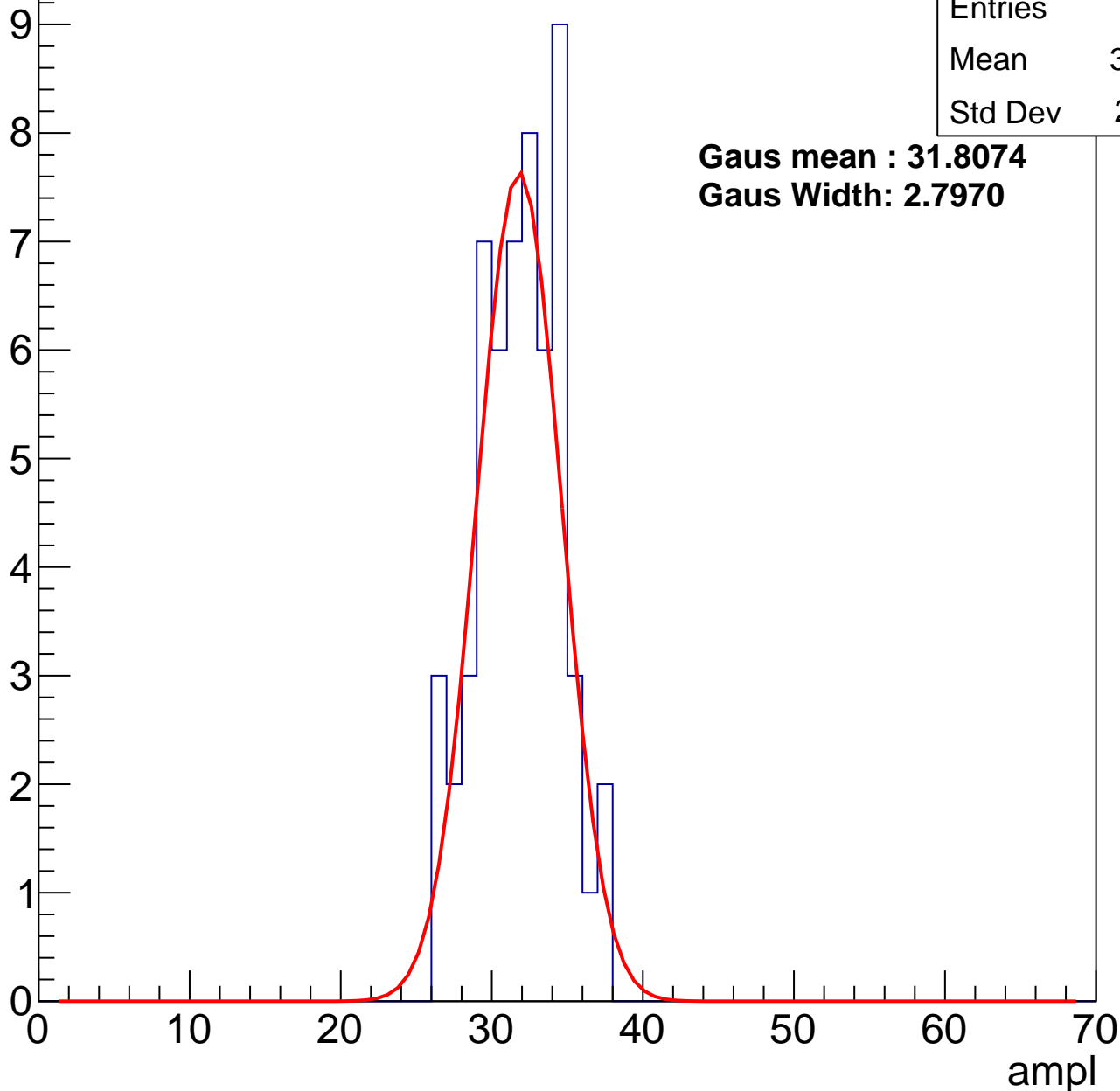
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	31.42
Std Dev	2.701

Gaus mean : 31.8074

Gaus Width: 2.7970



B0L000S, U2-ch14, adc1

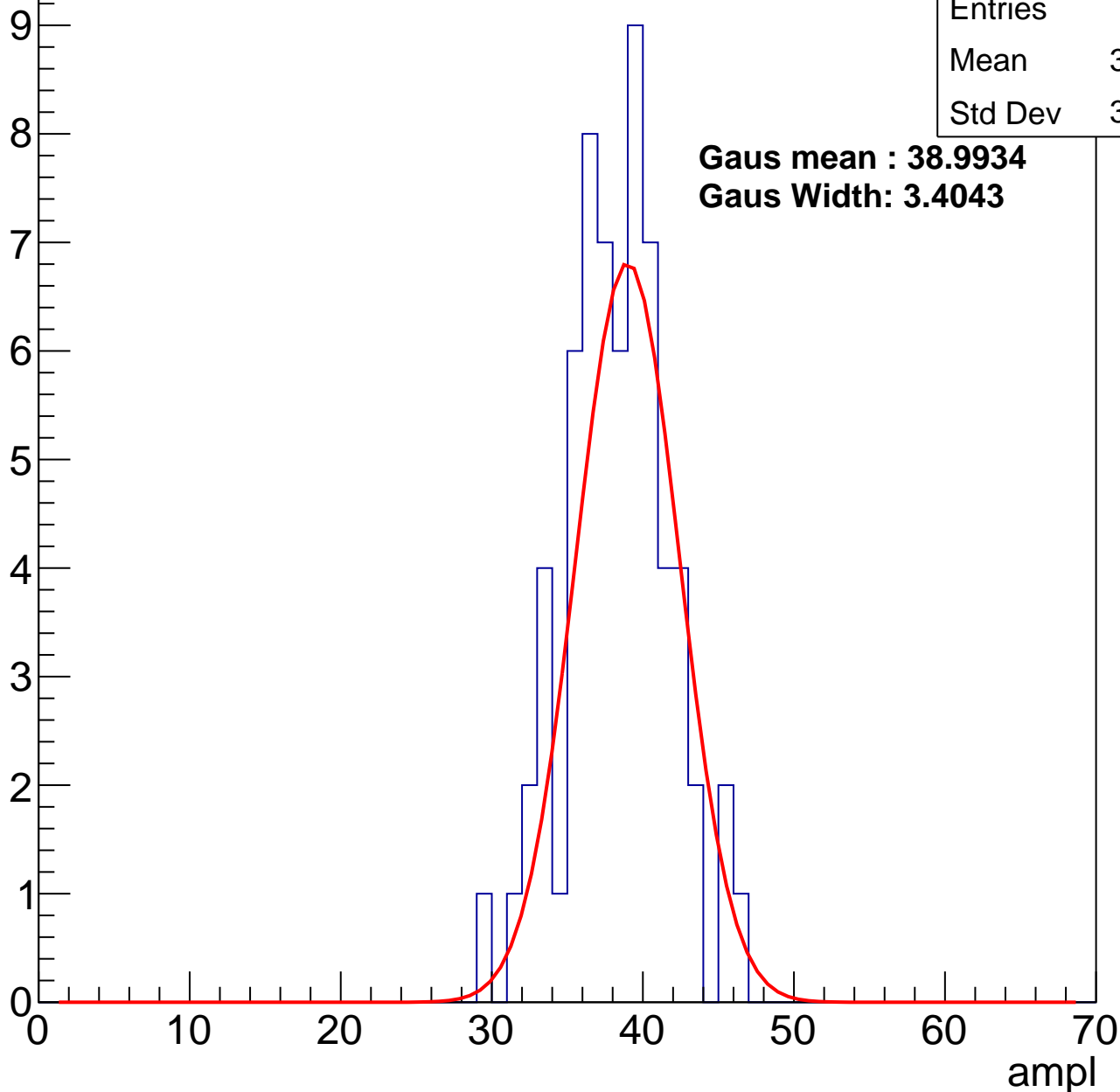
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.85
Std Dev	3.416

Gaus mean : 38.9934

Gaus Width: 3.4043



B0L000S, U2-ch14, adc2

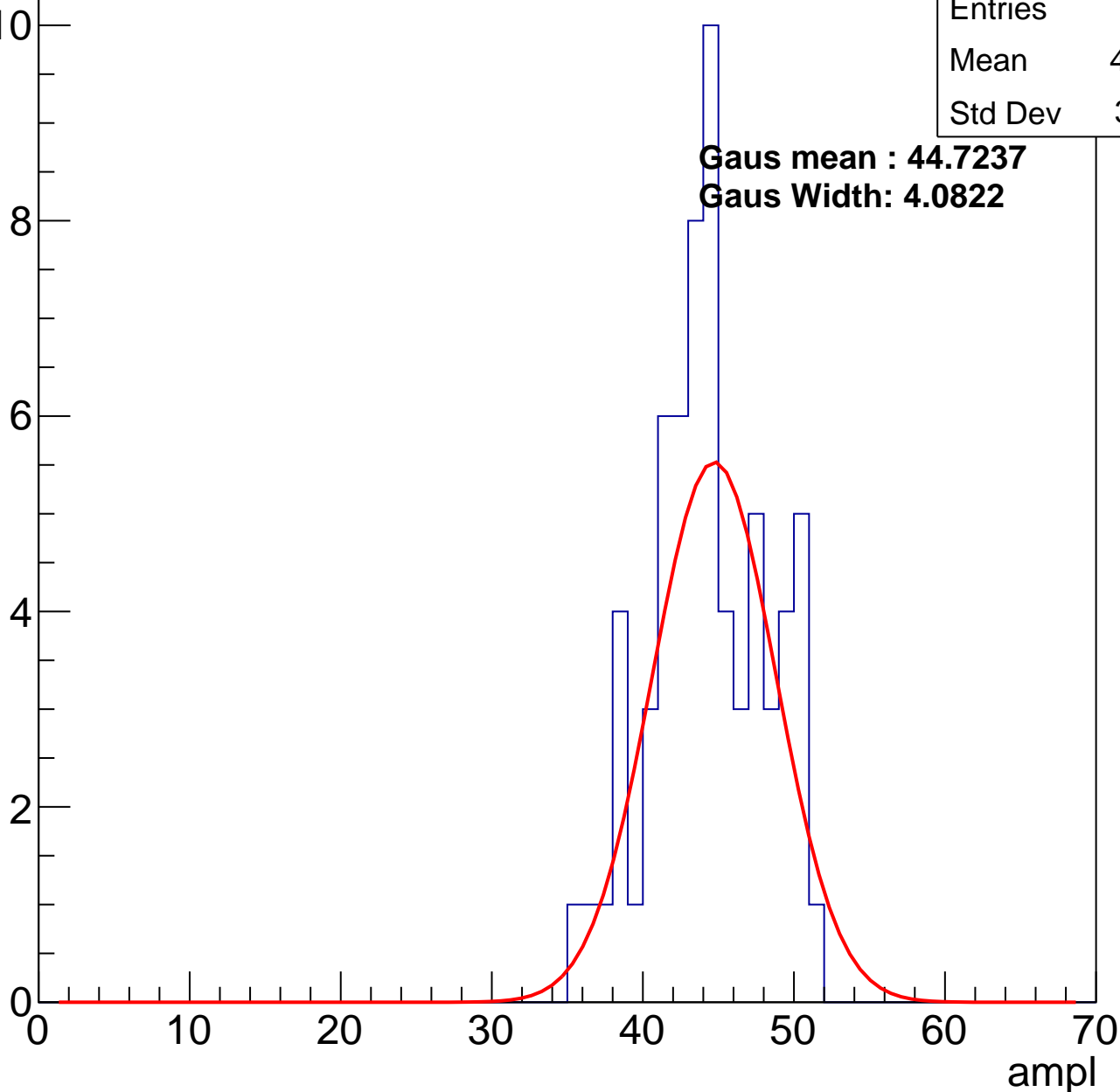
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.86
Std Dev	3.761

Gaus mean : 44.7237

Gaus Width: 4.0822

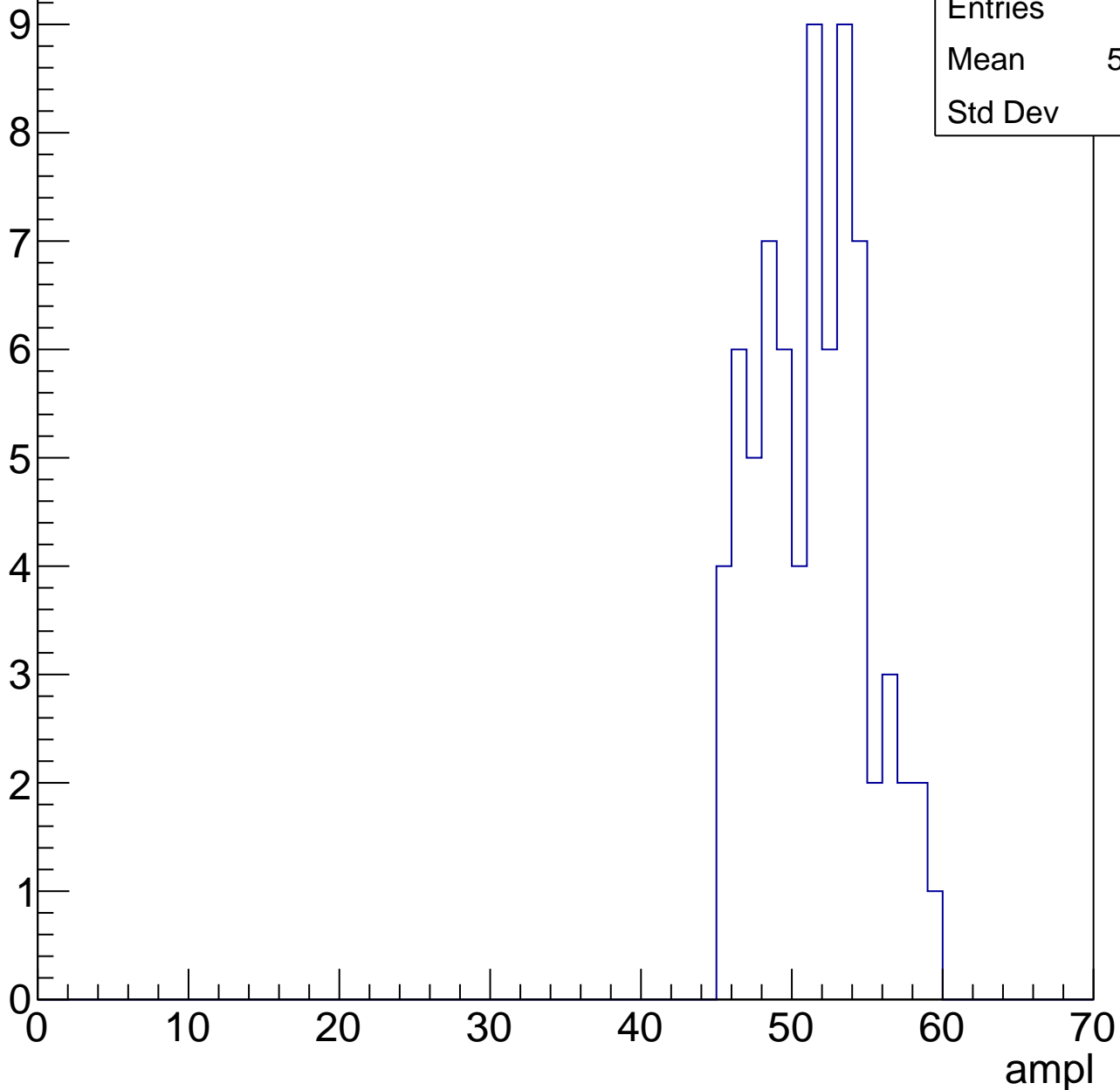


B0L000S, U2-ch14, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	50.88
Std Dev	3.53

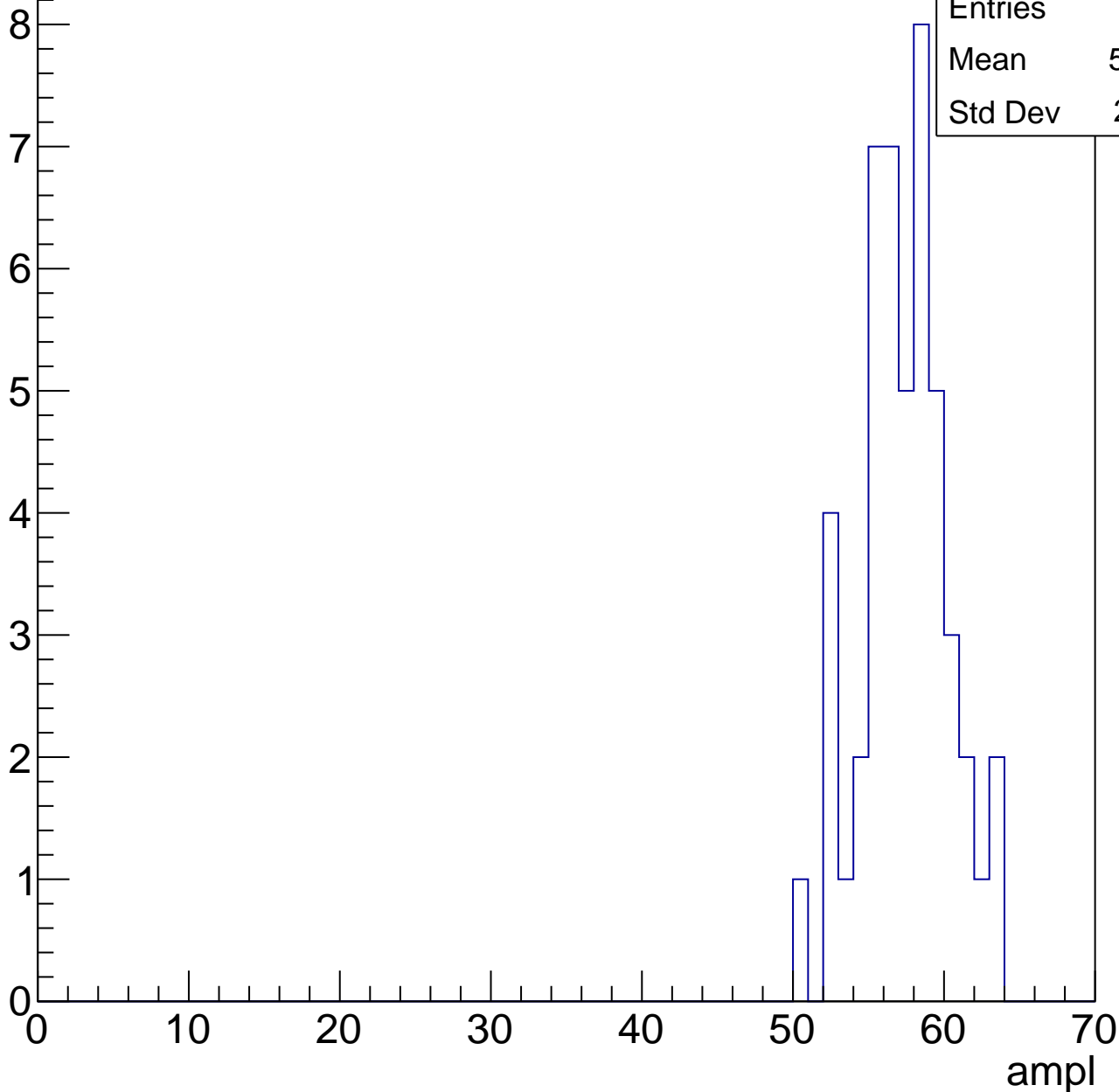


B0L000S, U2-ch14, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	56.88
Std Dev	2.891

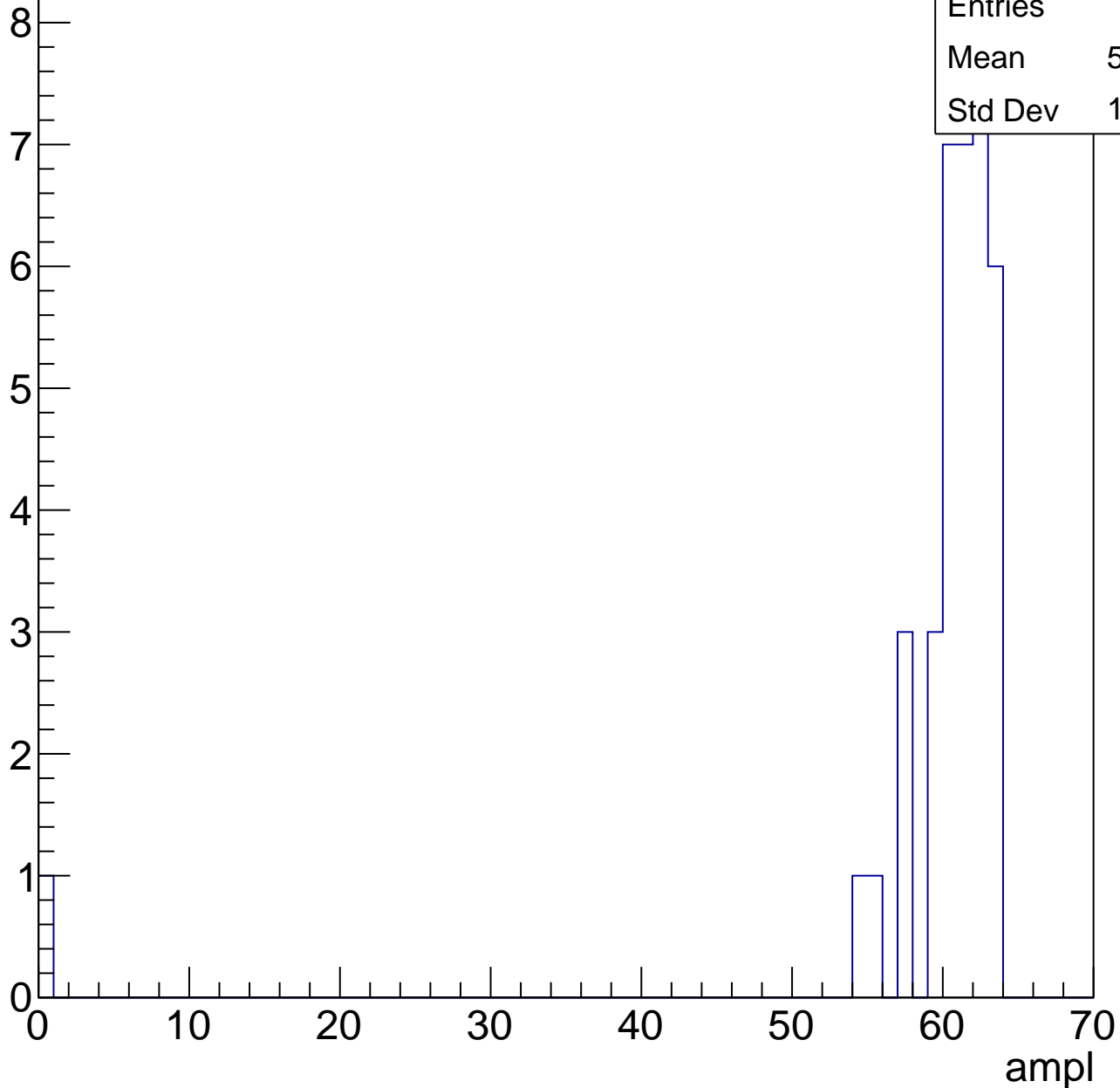


B0L000S, U2-ch14, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

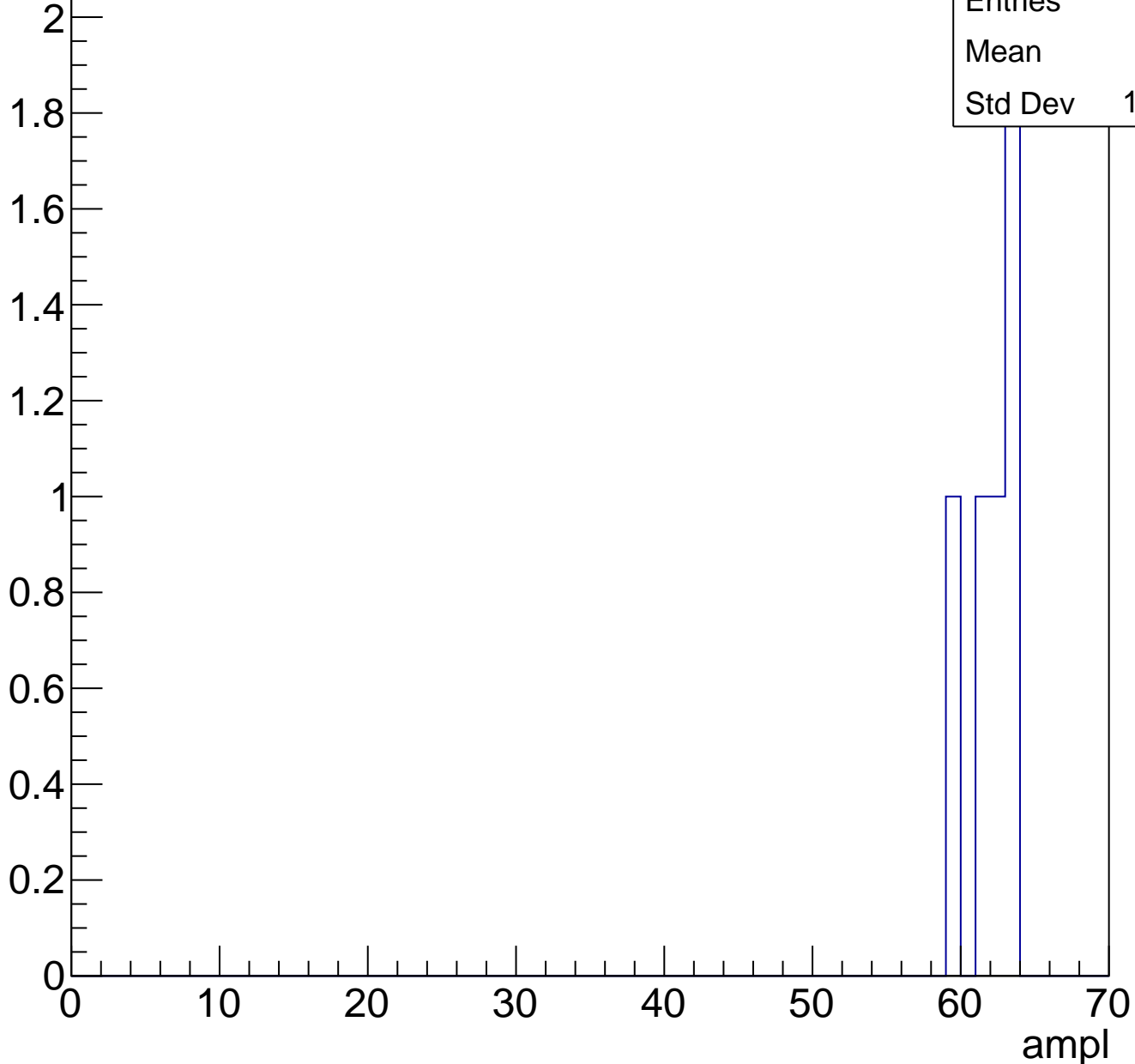
Entries	37
Mean	58.86
Std Dev	10.05



B0L000S, U2-ch14, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	5
Mean	61.6
Std Dev	1.497

B0L000S, U2-ch14, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch15, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	29.65
Std Dev	3.595

Gaus mean : 30.3504

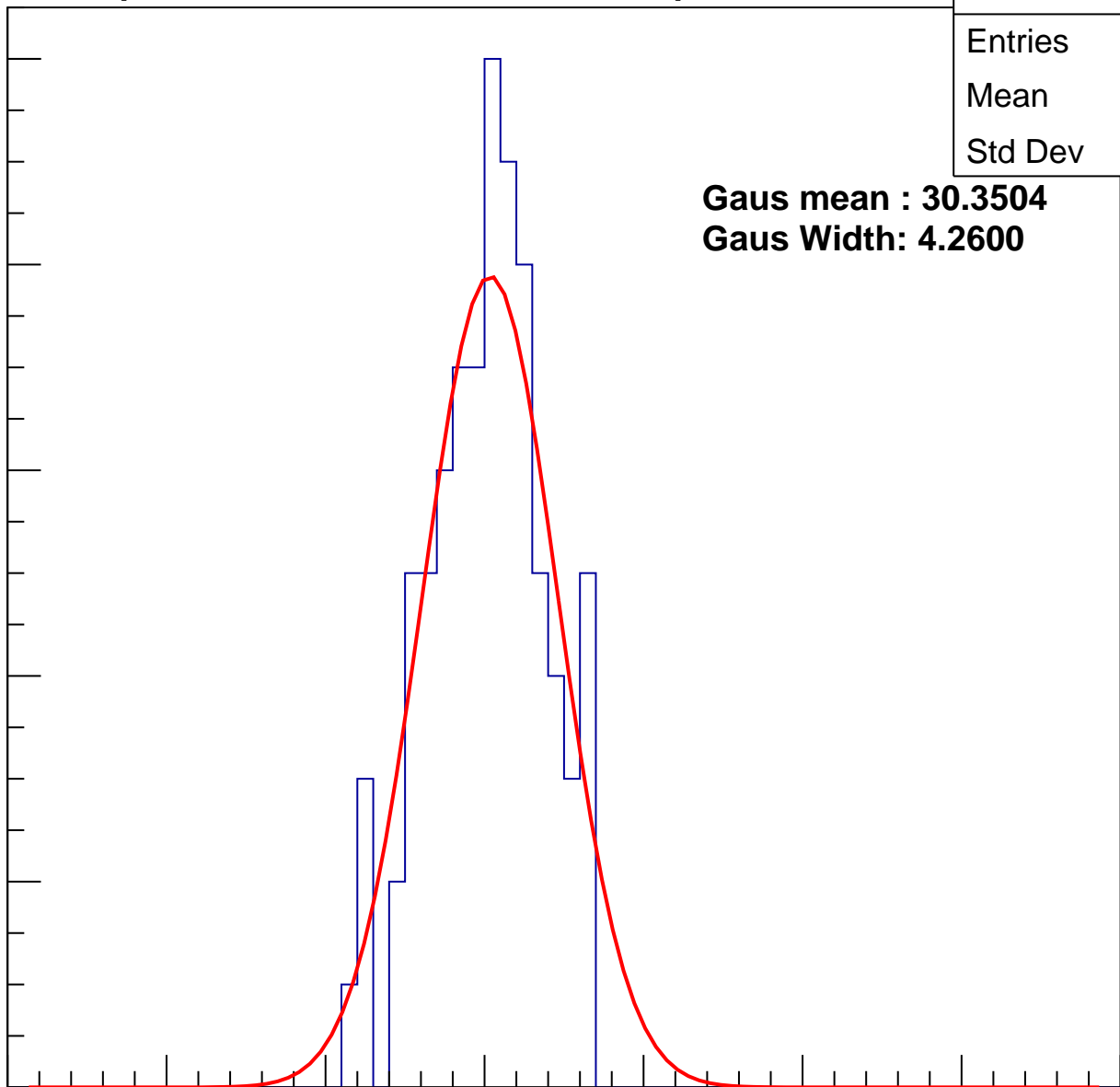
Gaus Width: 4.2600

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch15, adc1

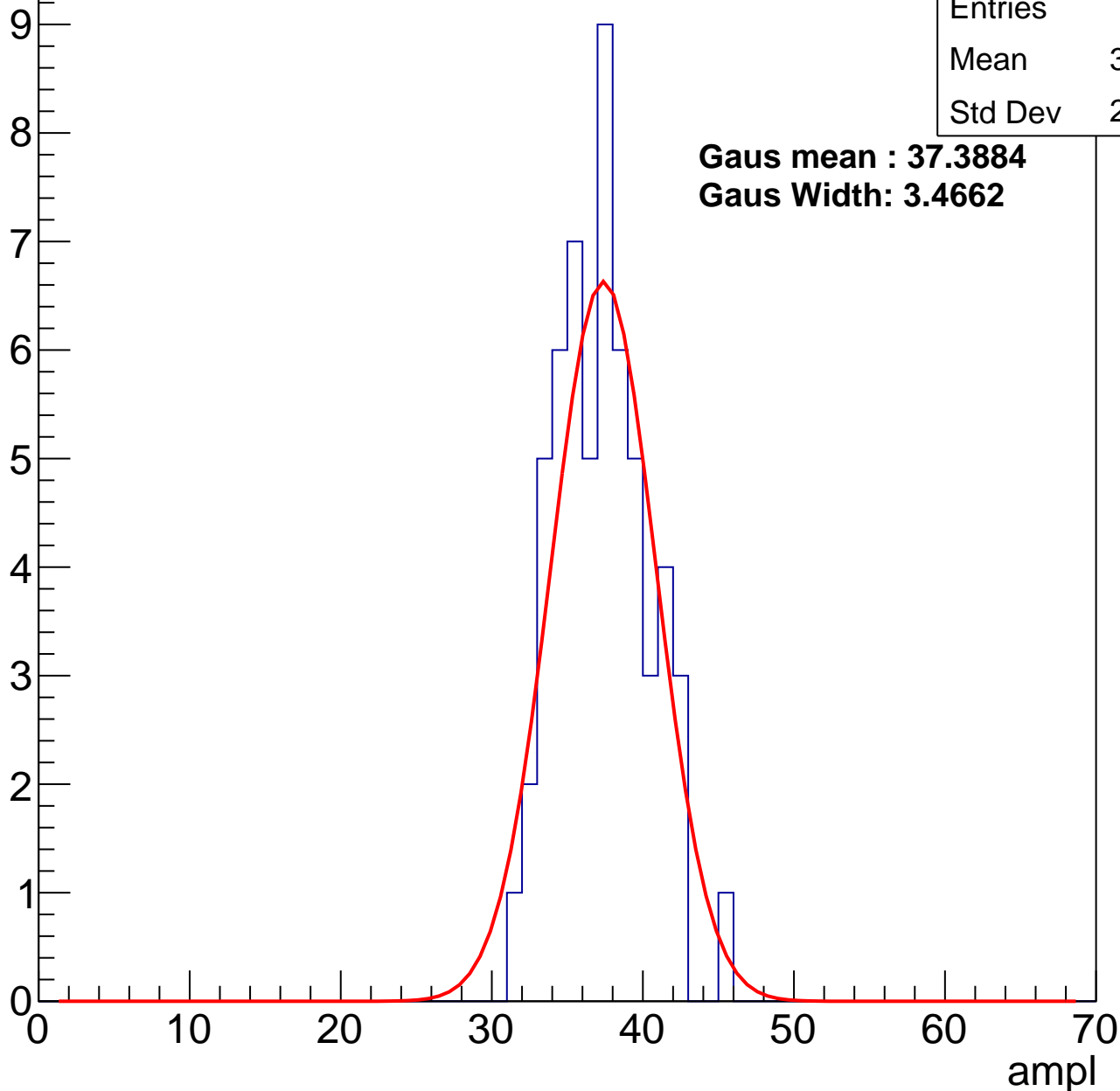
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	36.84
Std Dev	2.984

Gaus mean : 37.3884

Gaus Width: 3.4662



B0L000S, U2-ch15, adc2

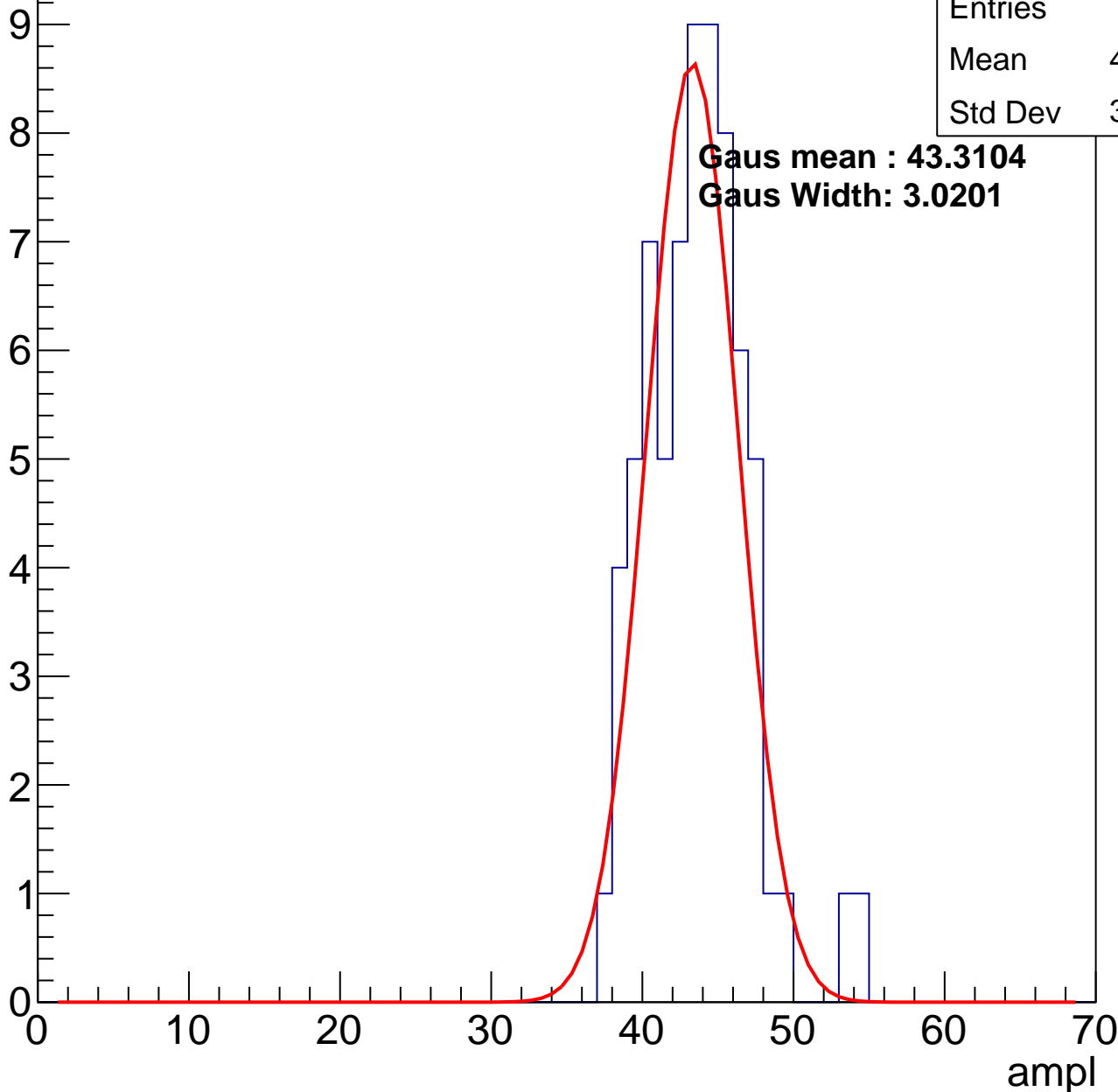
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.16
Std Dev	3.298

Gaus mean : 43.3104

Gaus Width: 3.0201

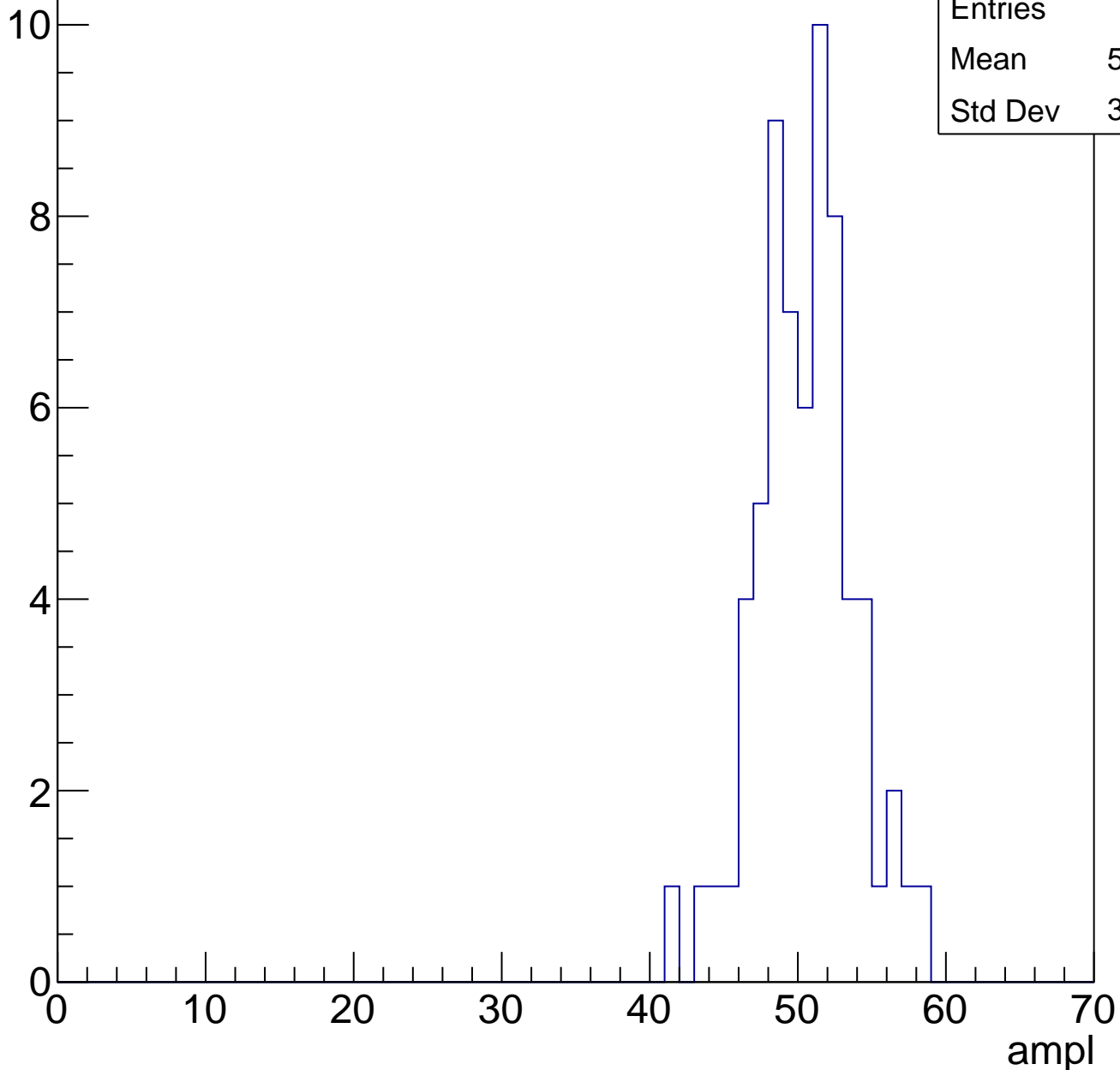


B0L000S, U2-ch15, adc3

calib_packv5_042523_0143.root, FC#5, port B1

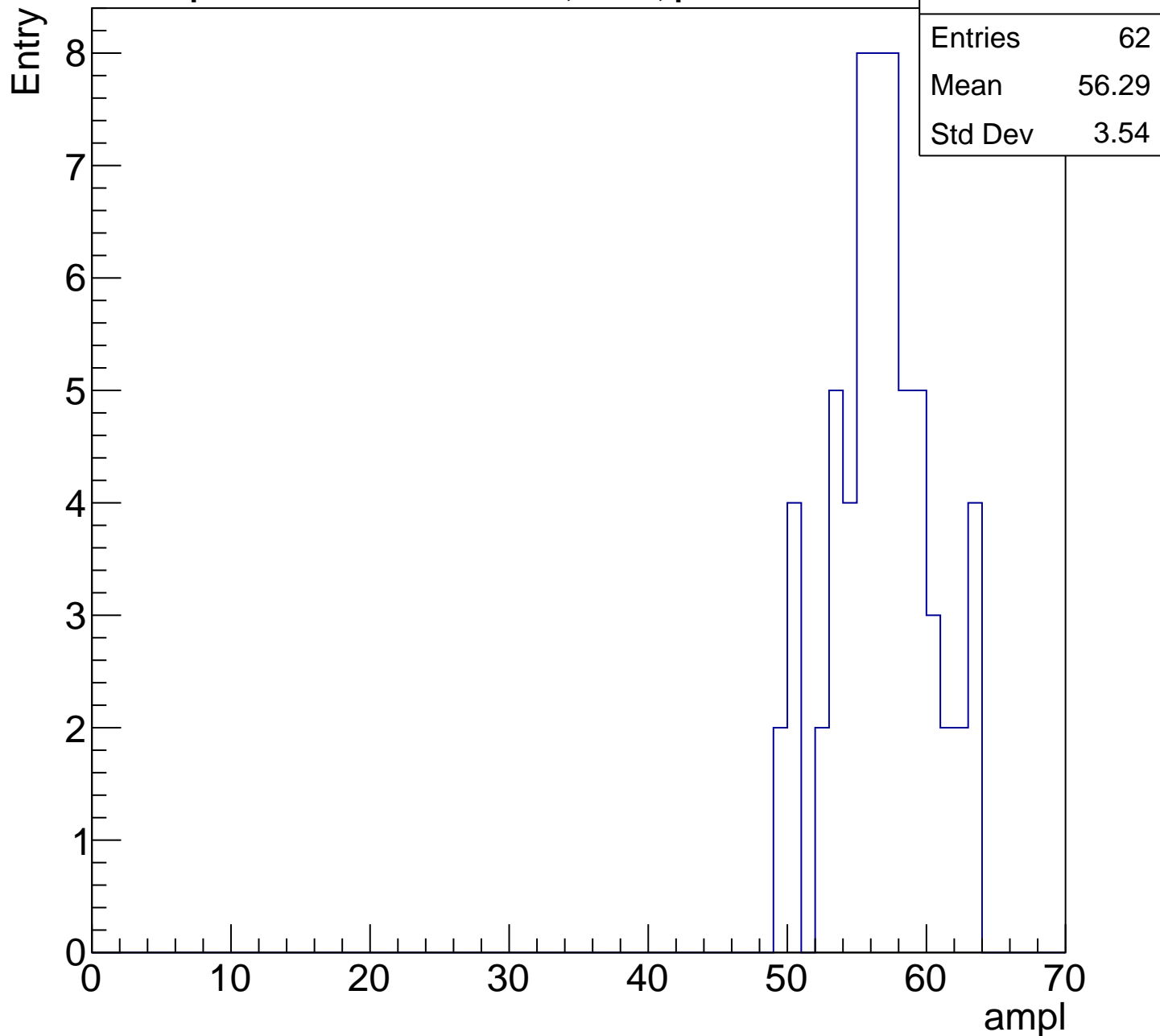
Entries	66
Mean	50.05
Std Dev	3.245

Entry



B0L000S, U2-ch15, adc4

calib_packv5_042523_0143.root, FC#5, port B1

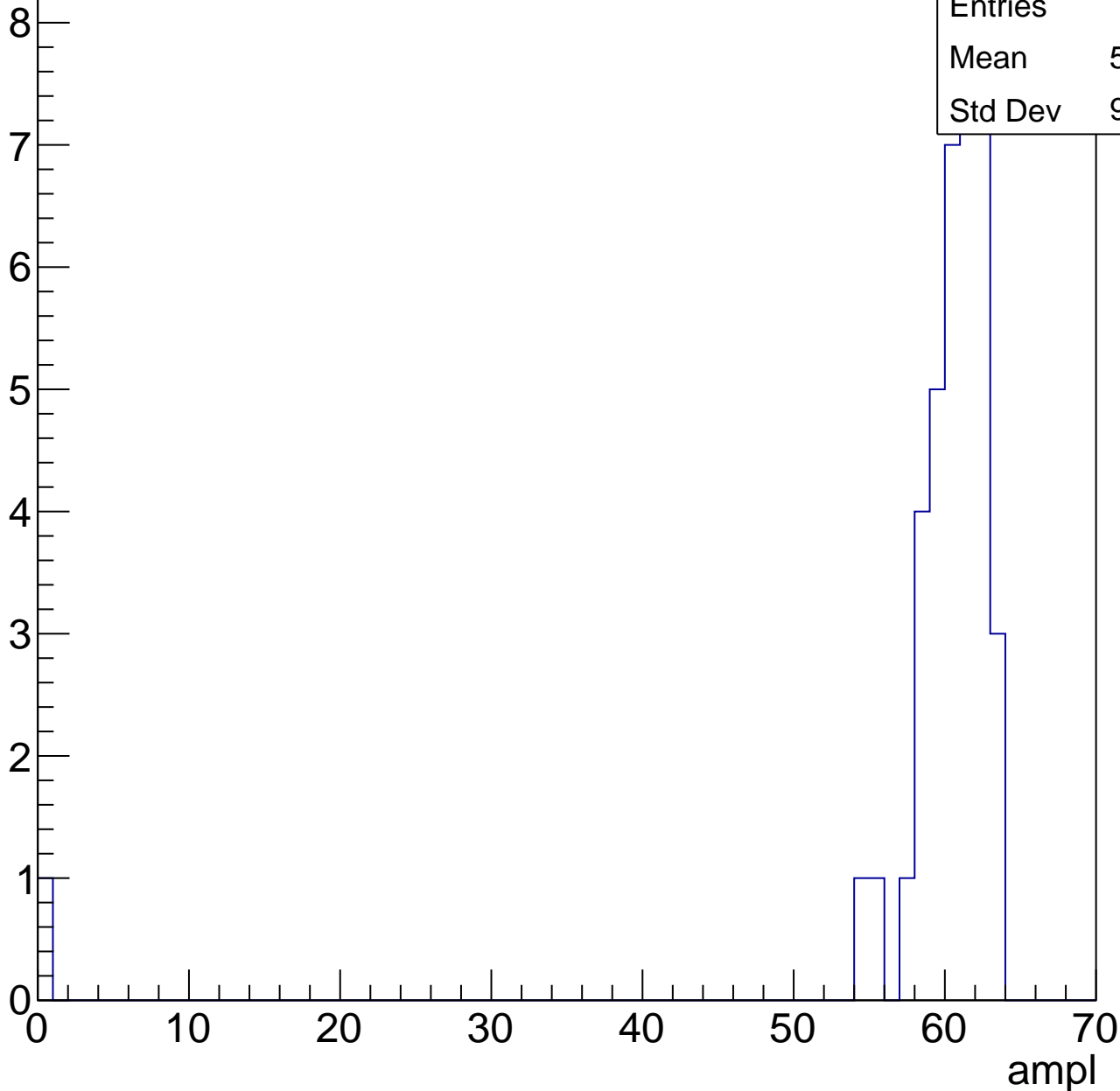


B0L000S, U2-ch15, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.62
Std Dev	9.718



B0L000S, U2-ch15, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch15, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L000S, U2-ch16, adc0

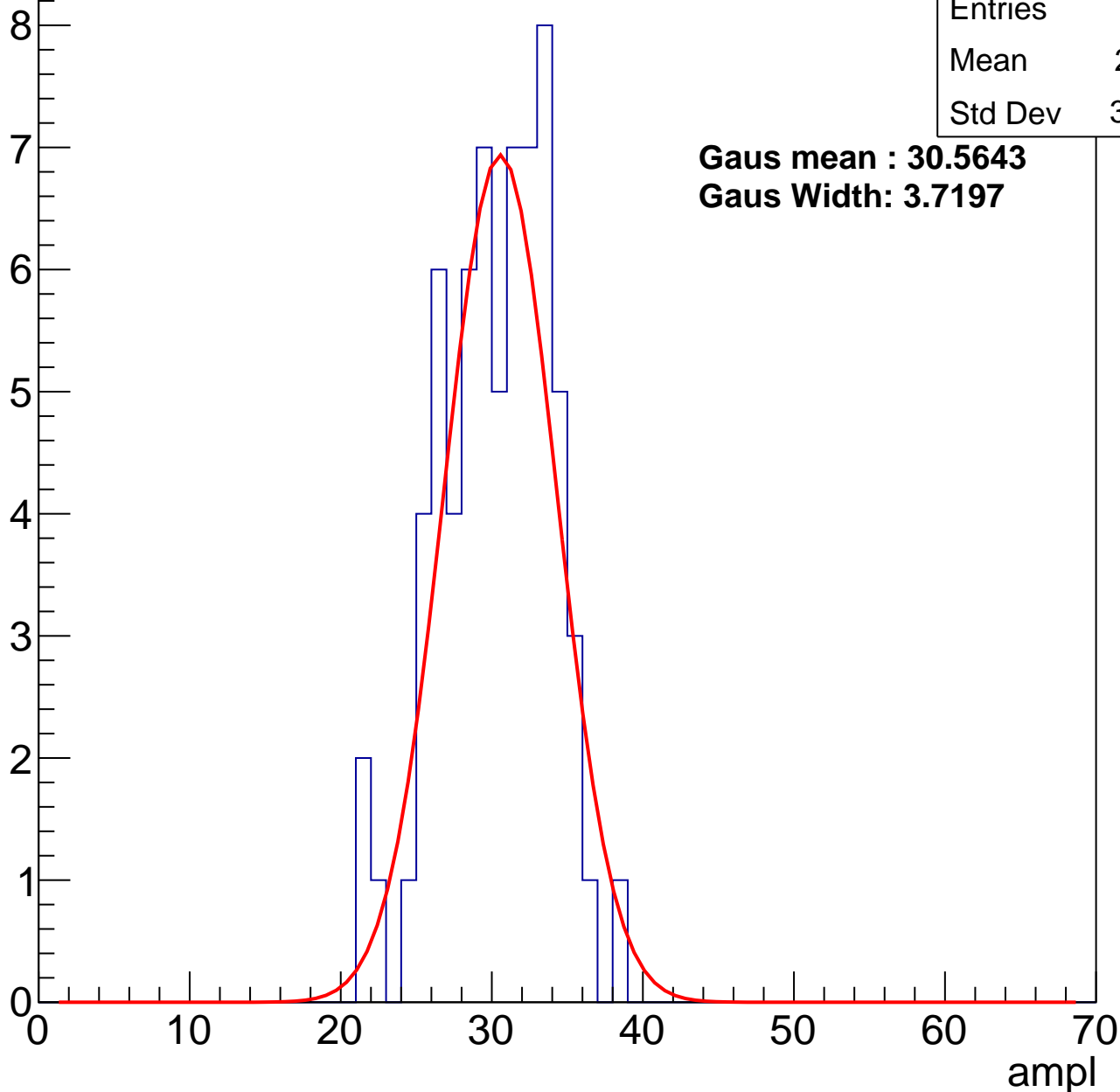
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	29.81
Std Dev	3.607

Gaus mean : 30.5643

Gaus Width: 3.7197



B0L000S, U2-ch16, adc1

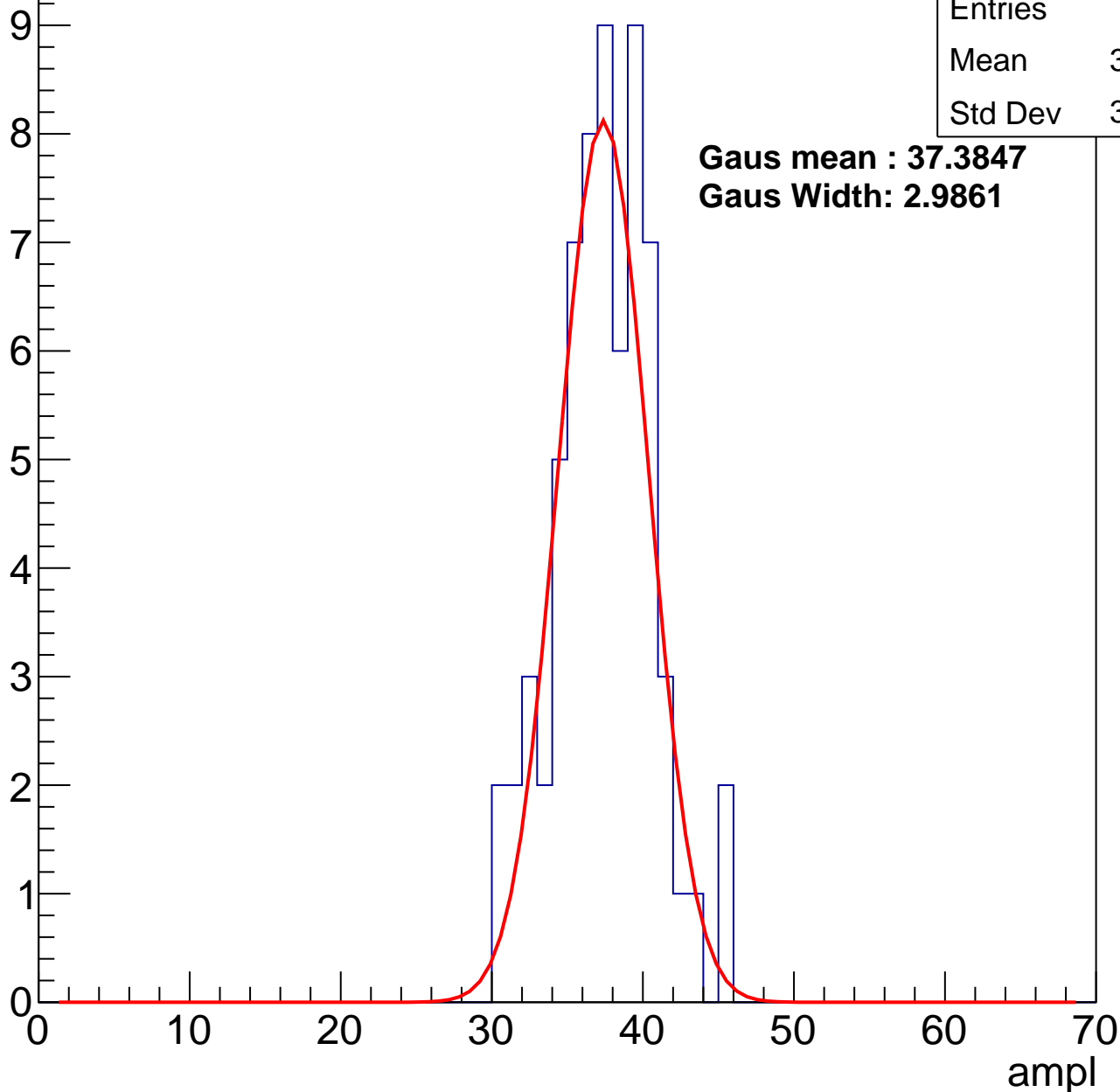
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.97
Std Dev	3.232

Gaus mean : 37.3847

Gaus Width: 2.9861



B0L000S, U2-ch16, adc2

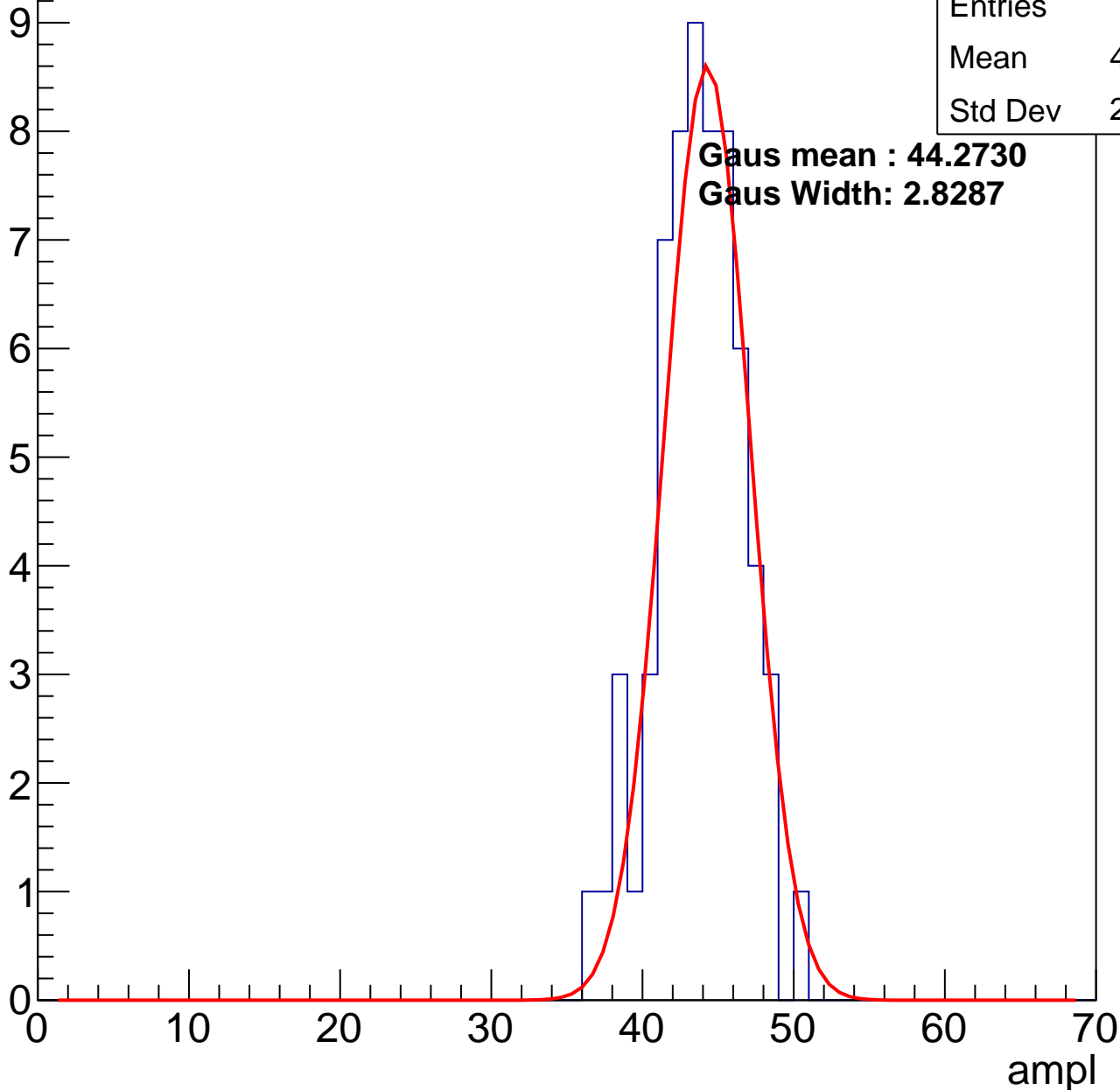
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	43.27
Std Dev	2.863

Gaus mean : 44.2730

Gaus Width: 2.8287

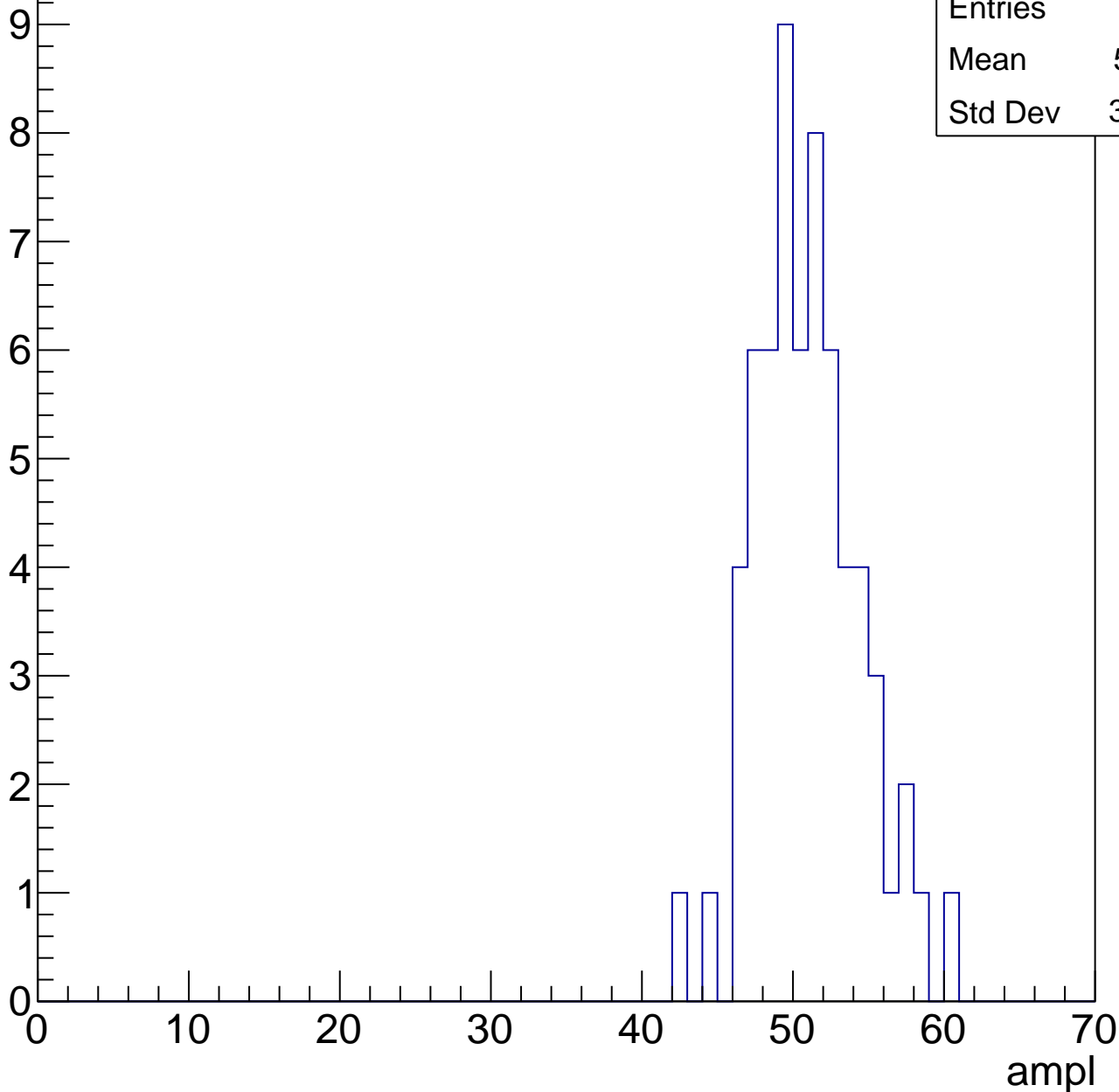


B0L000S, U2-ch16, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	50.51
Std Dev	3.427

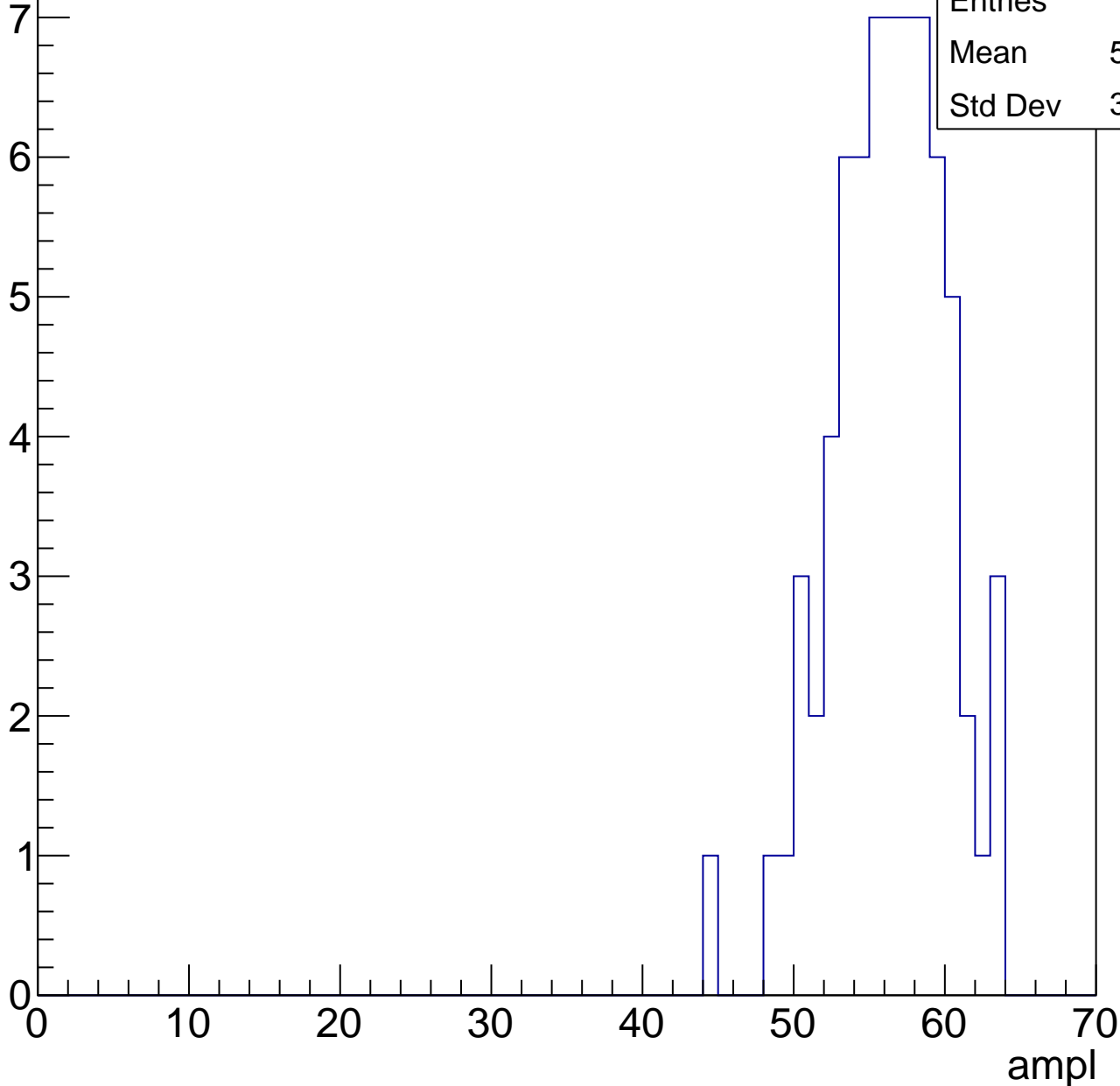


B0L000S, U2-ch16, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	55.83
Std Dev	3.738

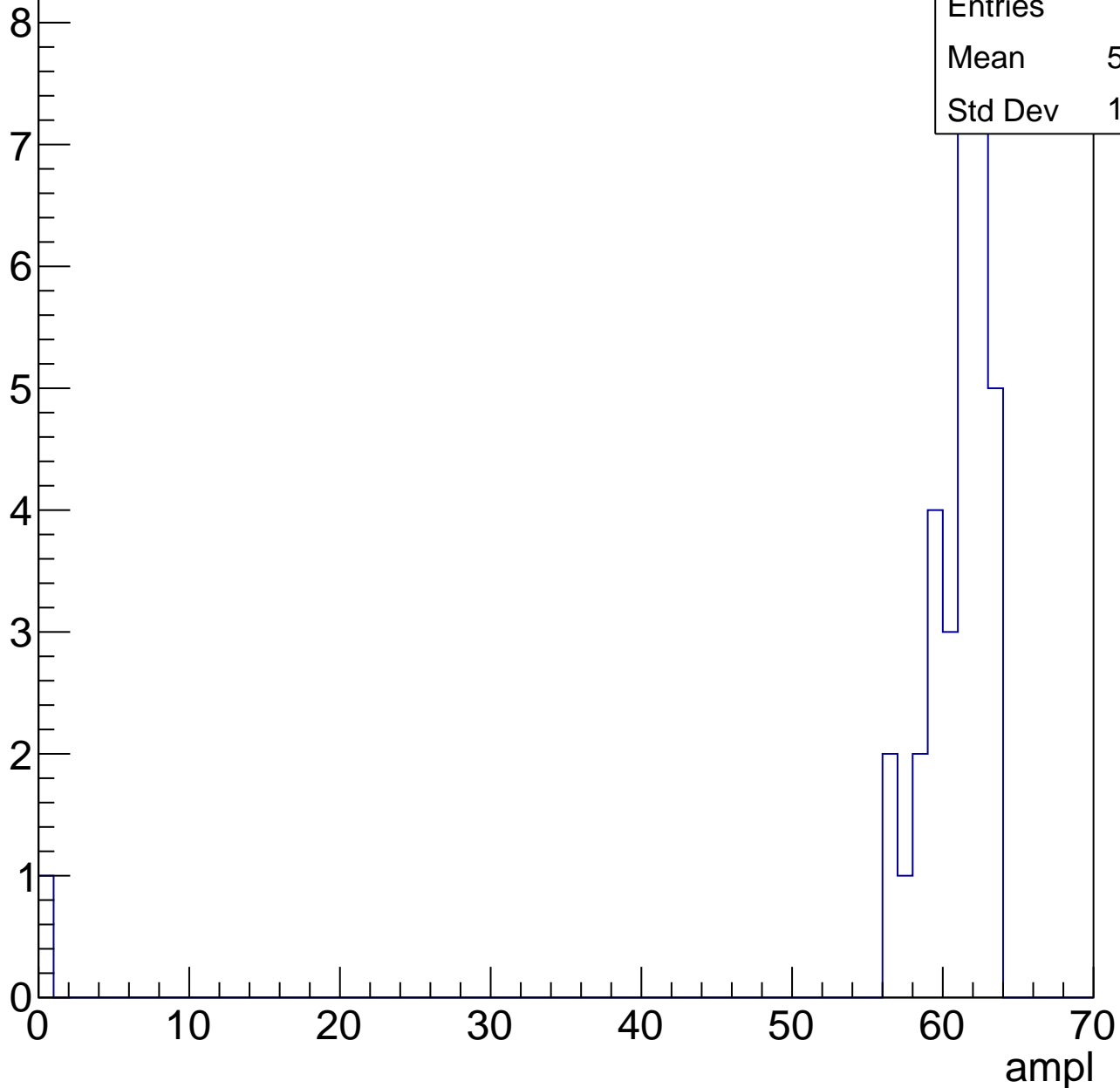


B0L000S, U2-ch16, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.82
Std Dev	10.42



B0L000S, U2-ch16, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	4
Mean	62
Std Dev	1.225

B0L000S, U2-ch16, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch17, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	90
Mean	29.28
Std Dev	3.833

Gaus mean : 29.5101

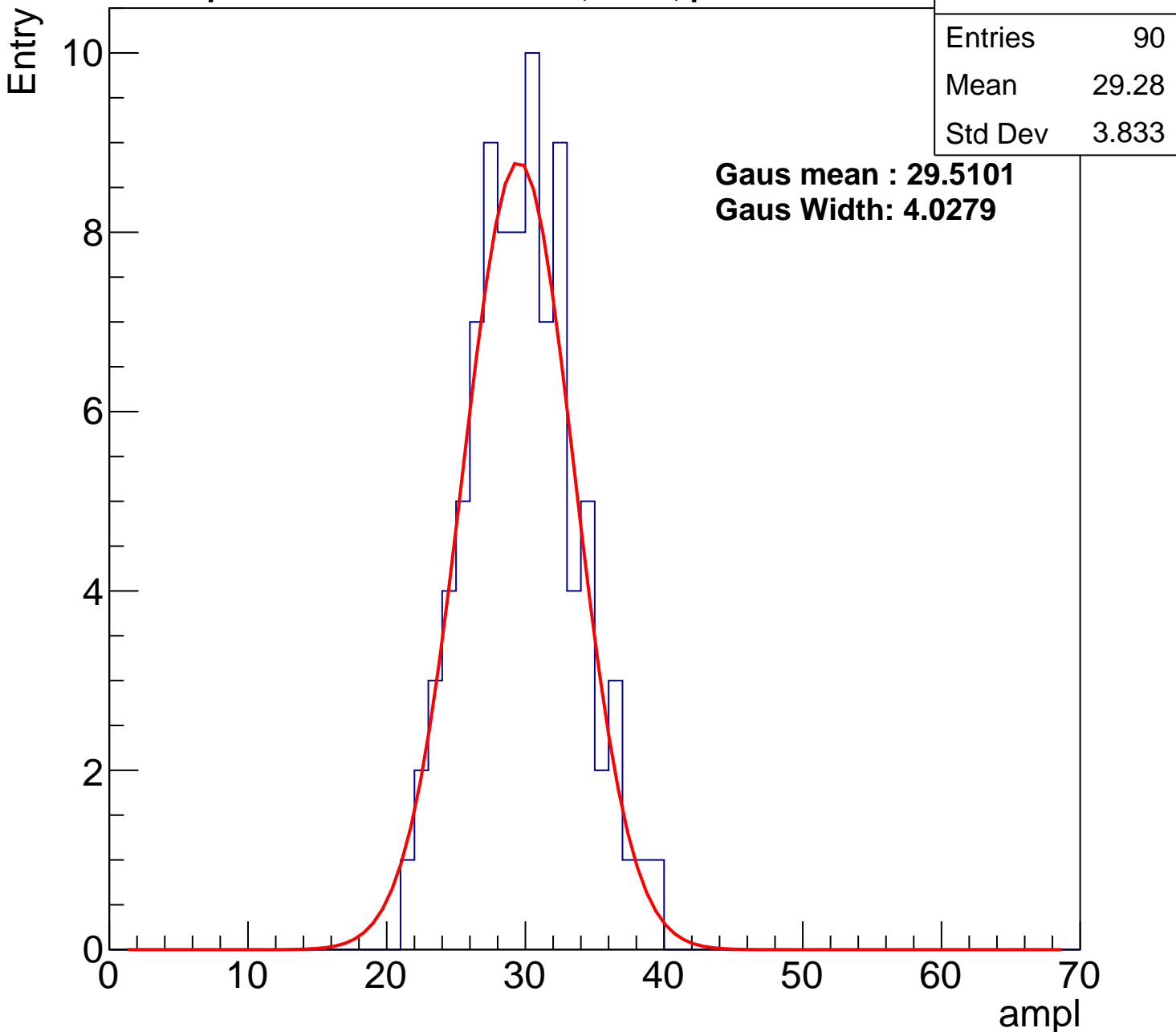
Gaus Width: 4.0279

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch17, adc1

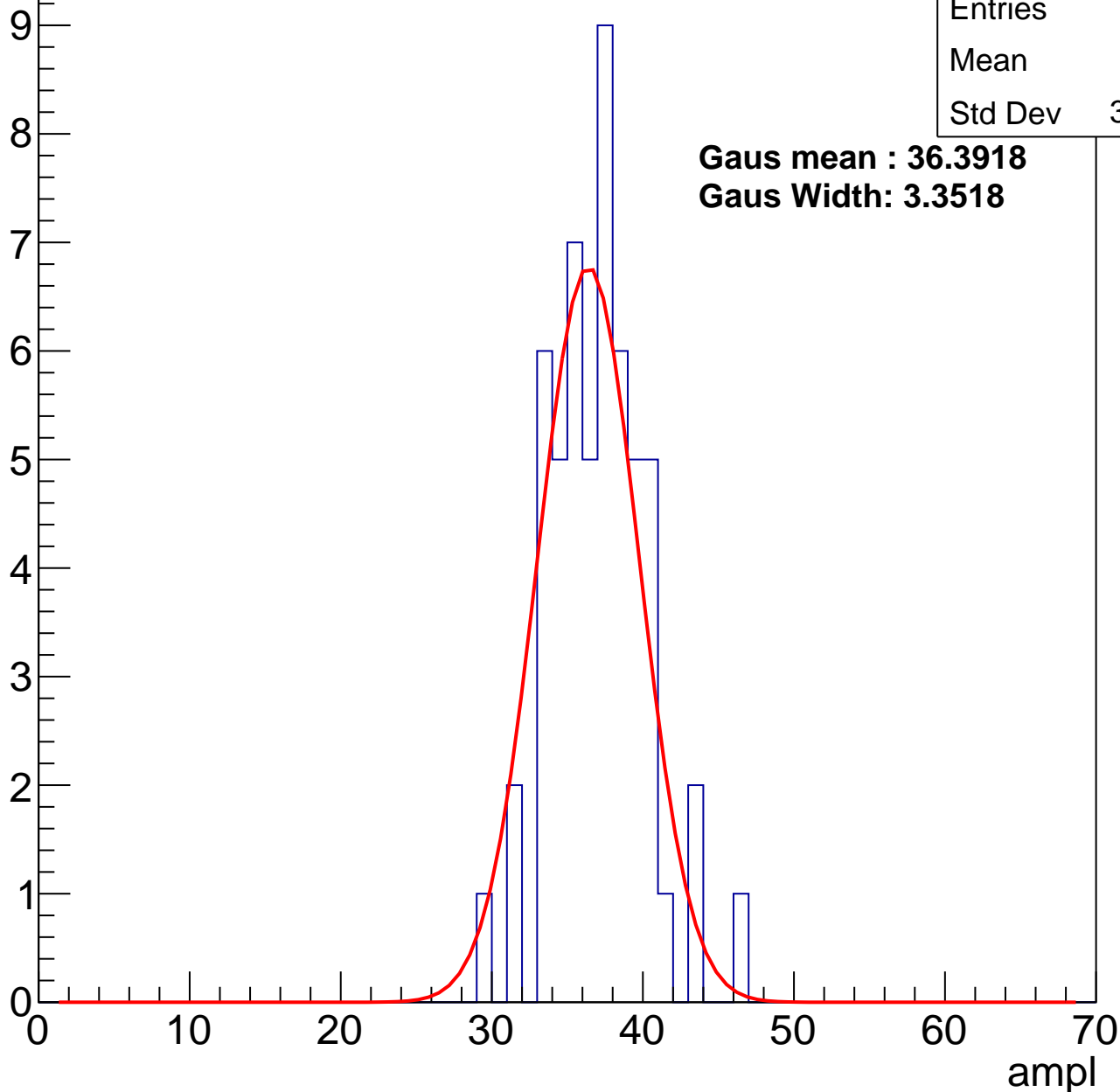
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	36.6
Std Dev	3.137

Gaus mean : 36.3918

Gaus Width: 3.3518



B0L000S, U2-ch17, adc2

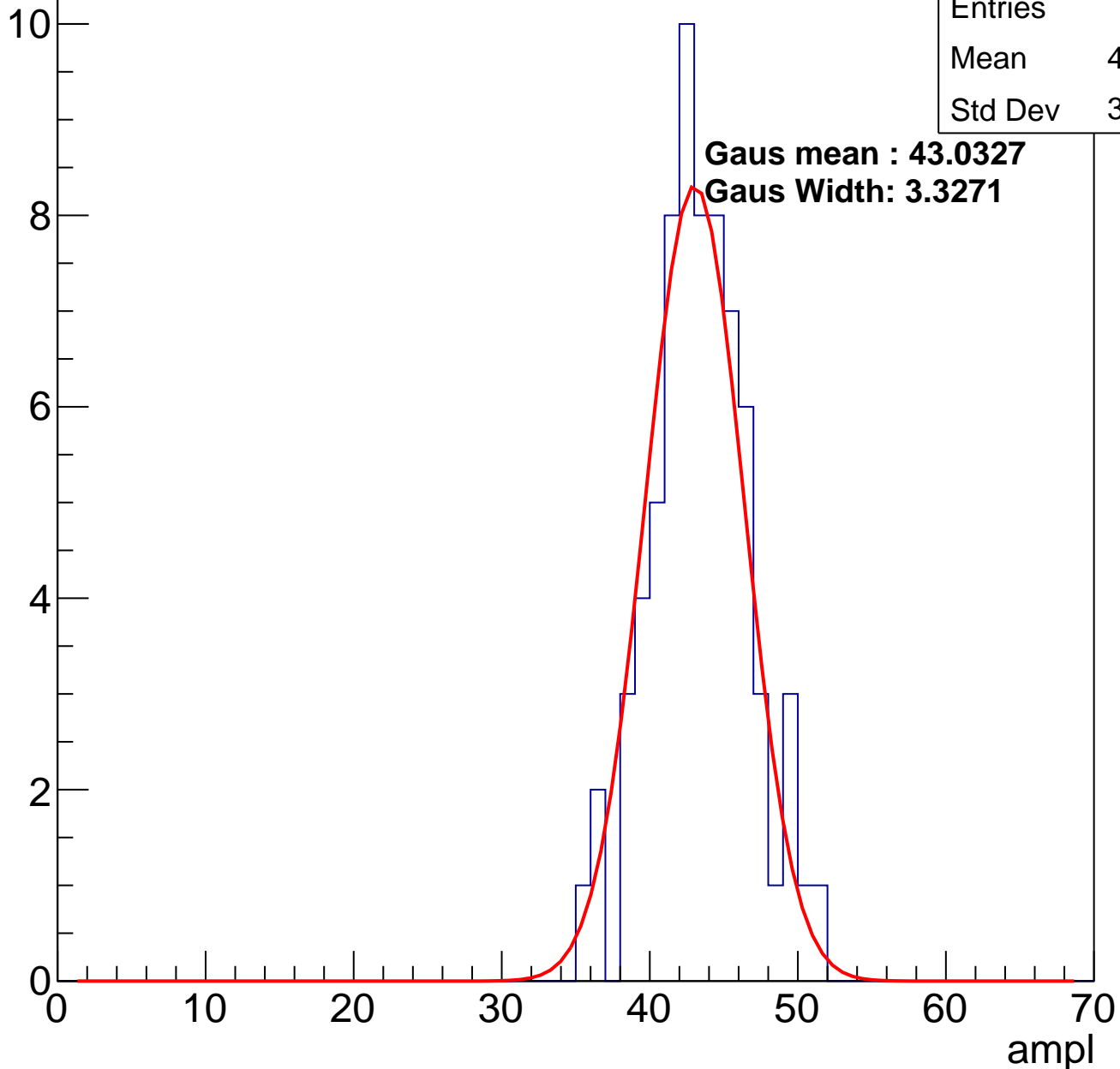
calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	42.94
Std Dev	3.297

Gaus mean : 43.0327

Gaus Width: 3.3271

Entry

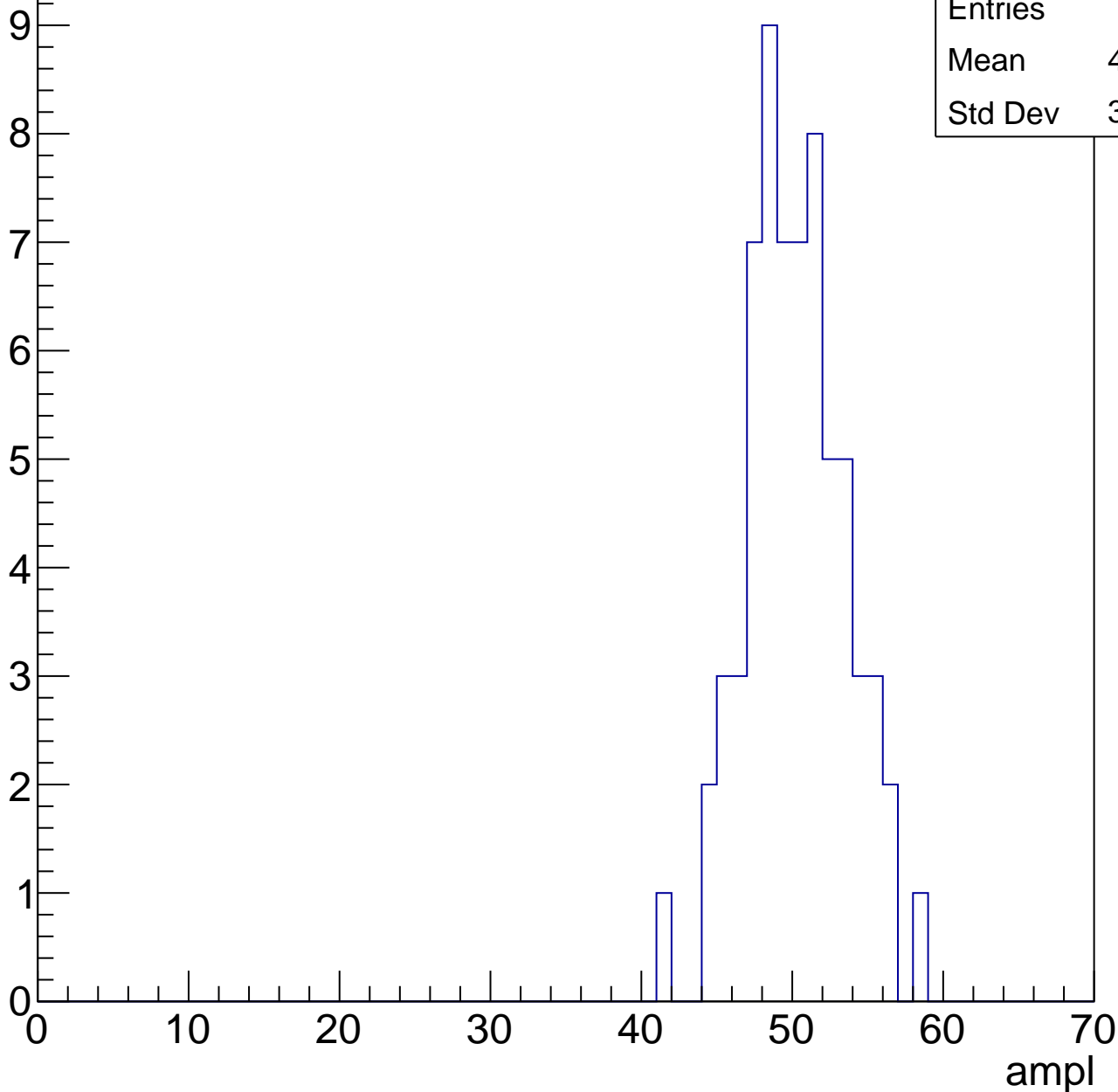


B0L000S, U2-ch17, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	49.79
Std Dev	3.282

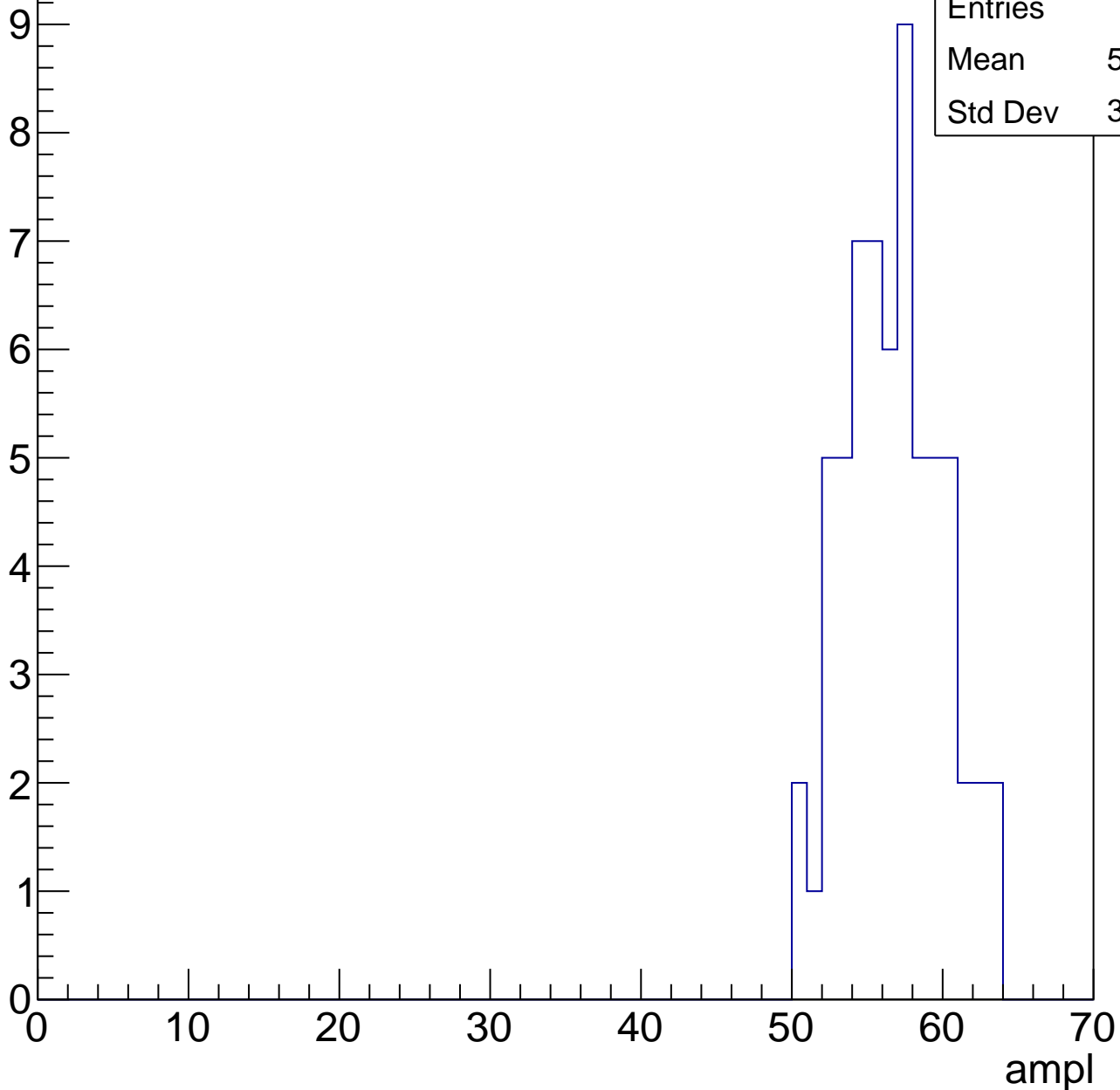


B0L000S, U2-ch17, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

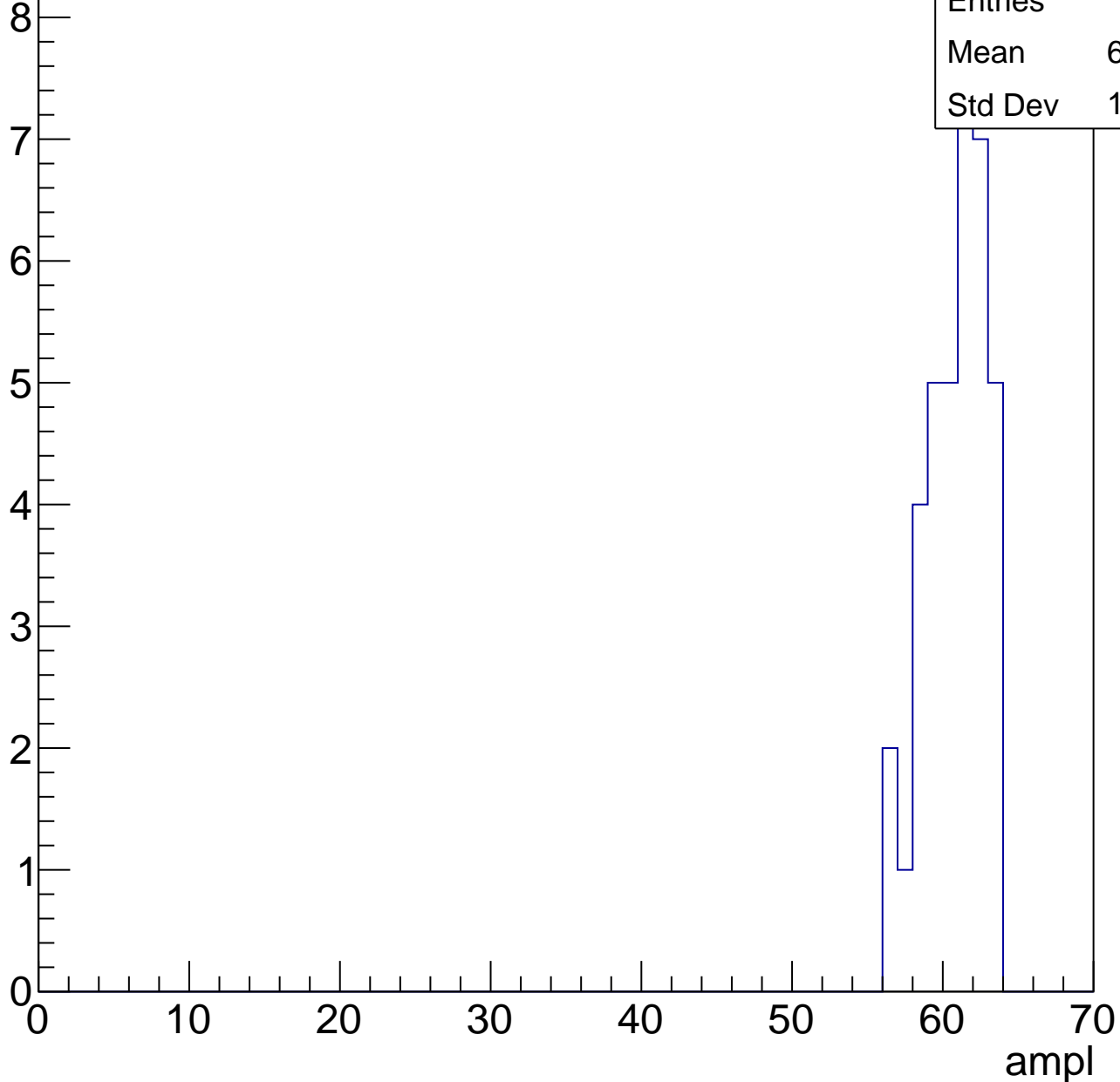
Entries	63
Mean	56.27
Std Dev	3.153



B0L000S, U2-ch17, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

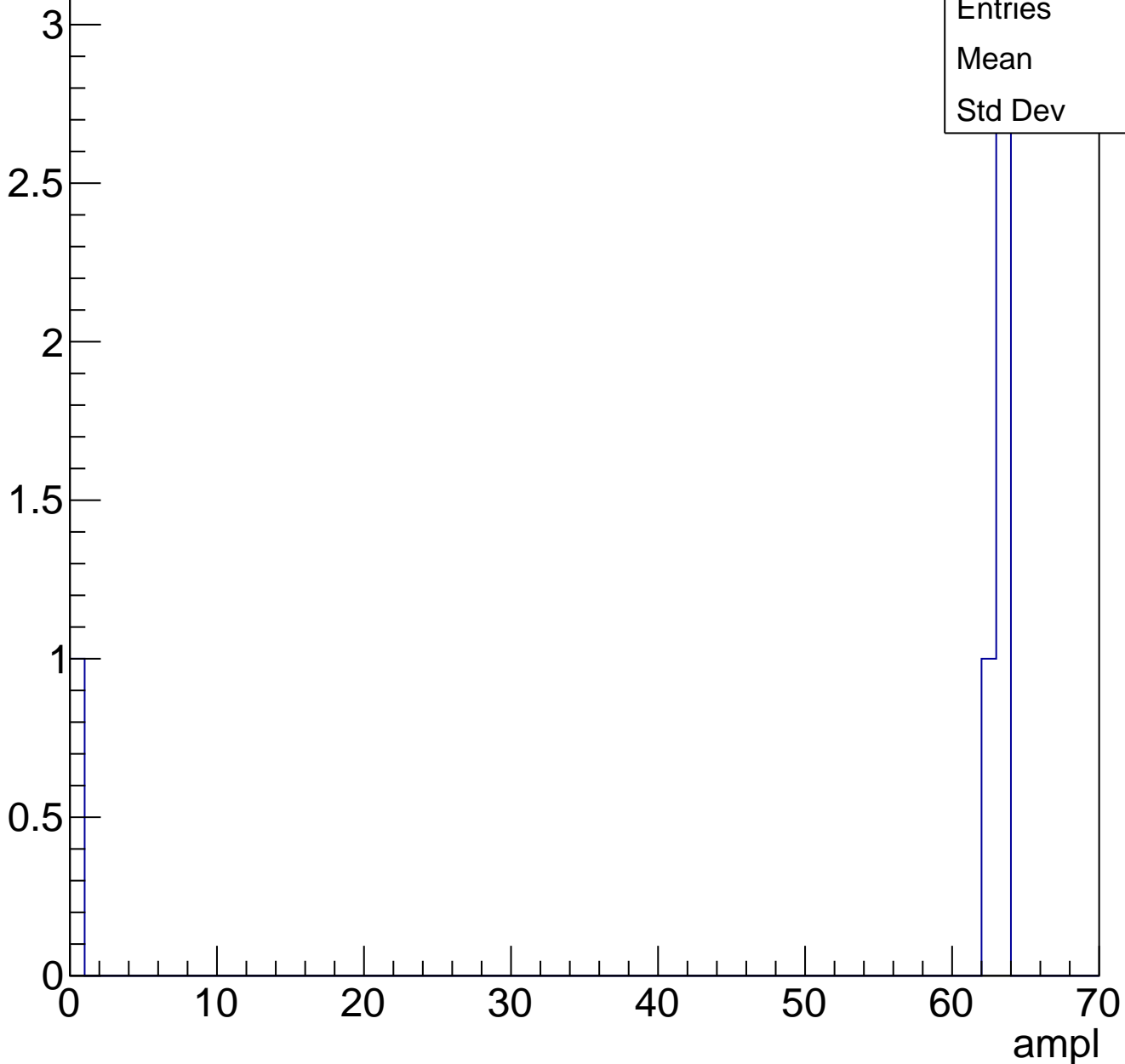


Entries	37
Mean	60.35
Std Dev	1.934

B0L000S, U2-ch17, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch17, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L000S, U2-ch18, adc0

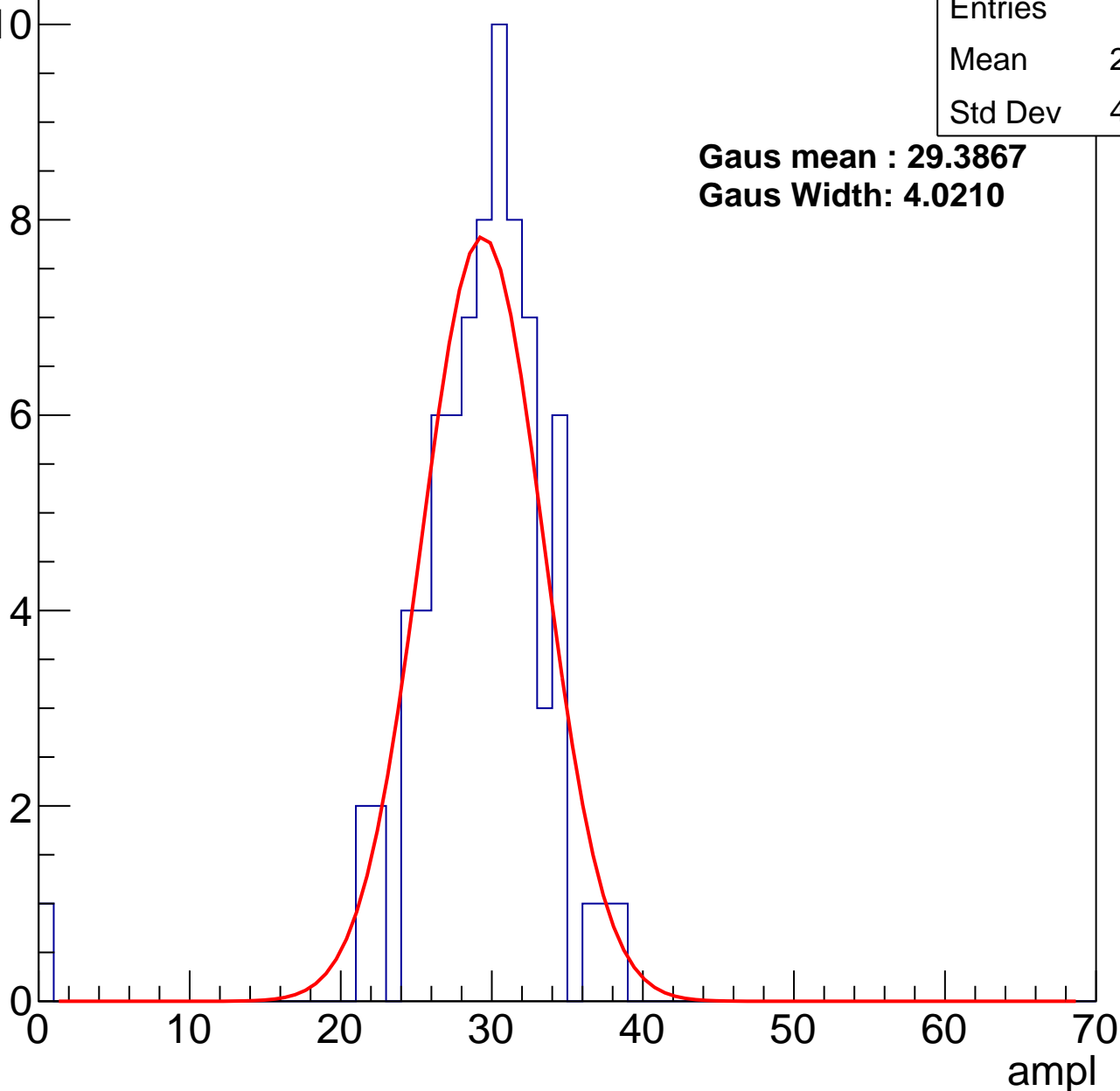
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	28.75
Std Dev	4.846

Gaus mean : 29.3867

Gaus Width: 4.0210



B0L000S, U2-ch18, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	75
Mean	36.97
Std Dev	3.709

Gaus mean : 37.2395

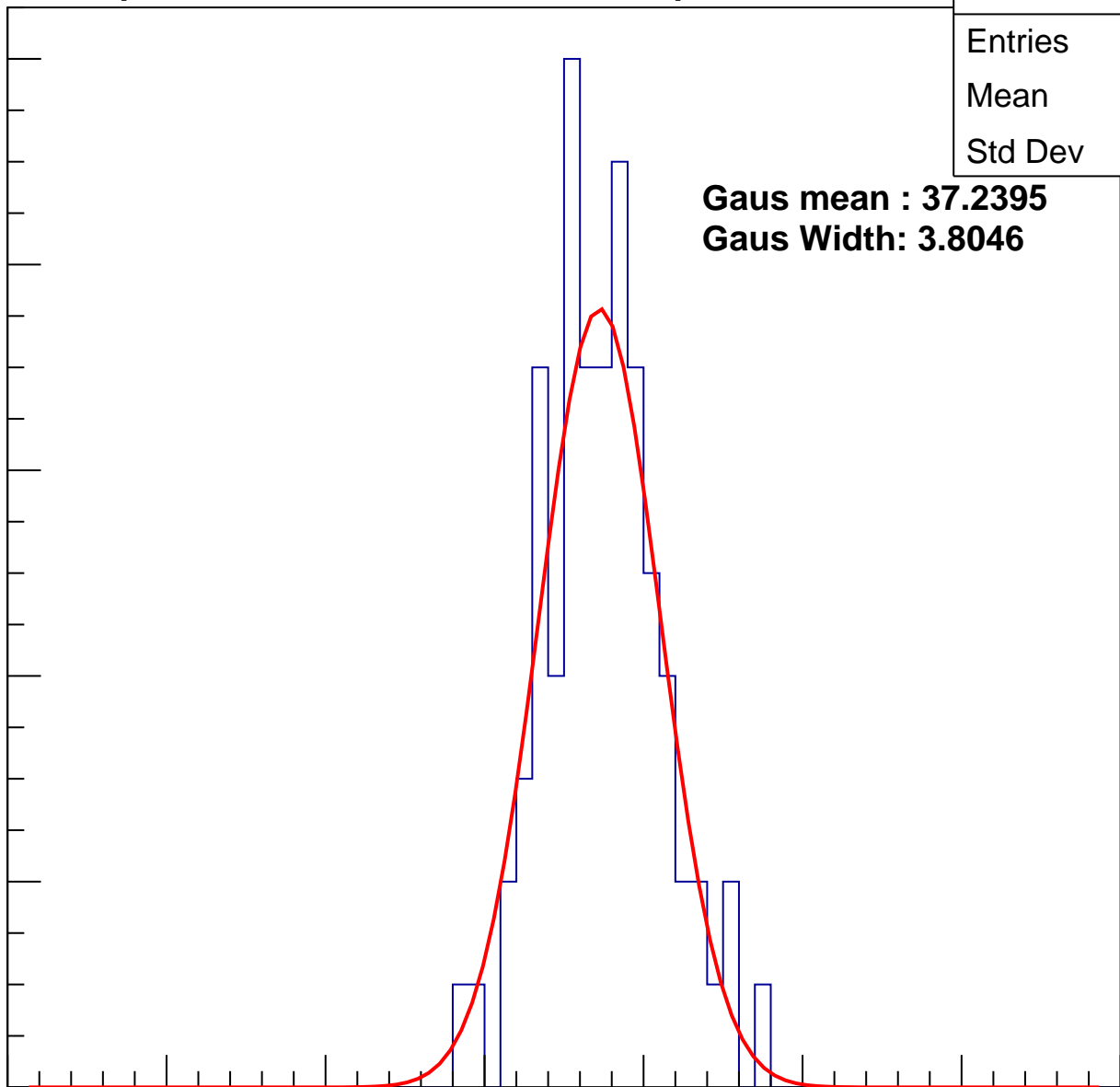
Gaus Width: 3.8046

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch18, adc2

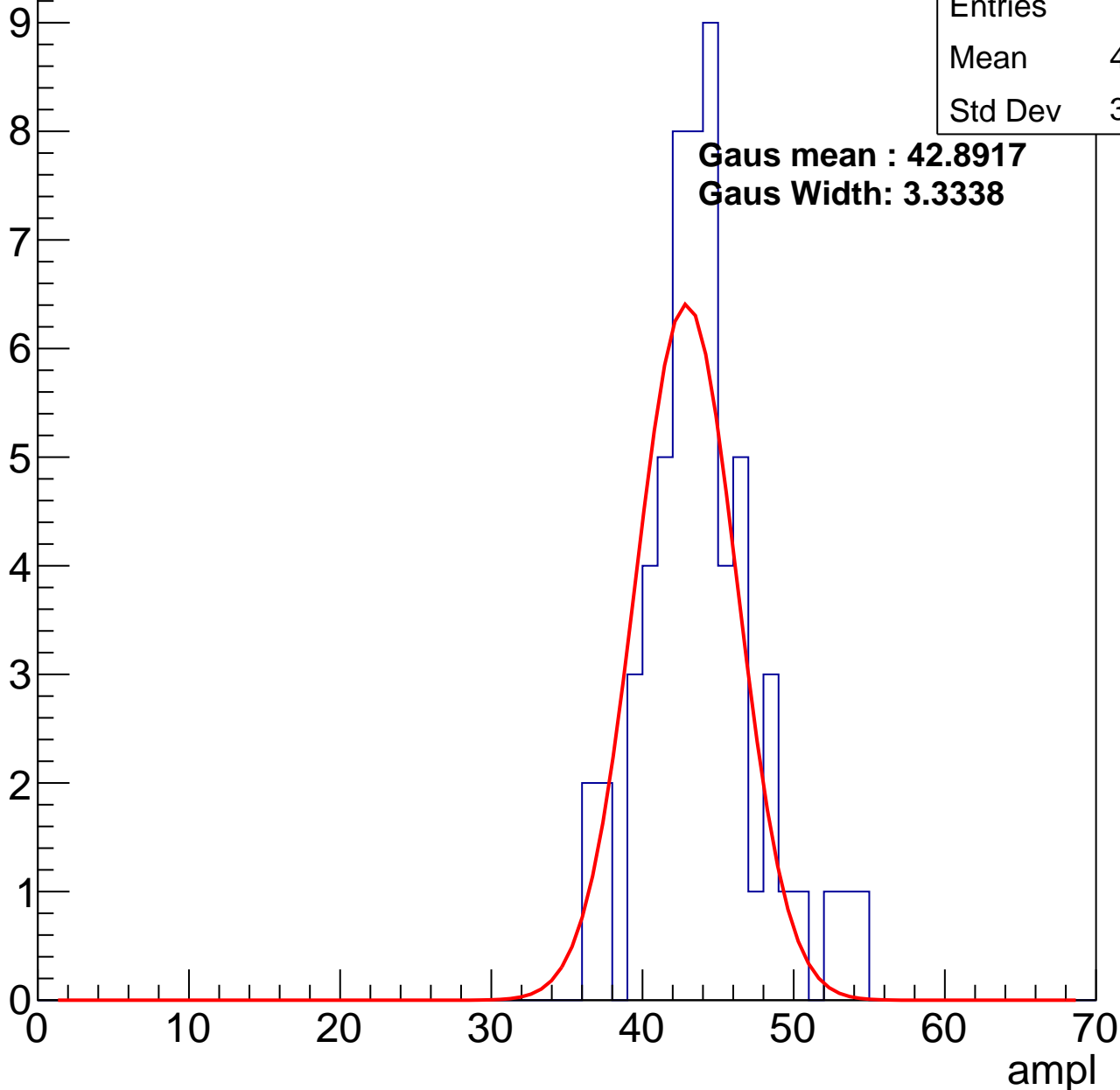
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	43.44
Std Dev	3.725

Gaus mean : 42.8917

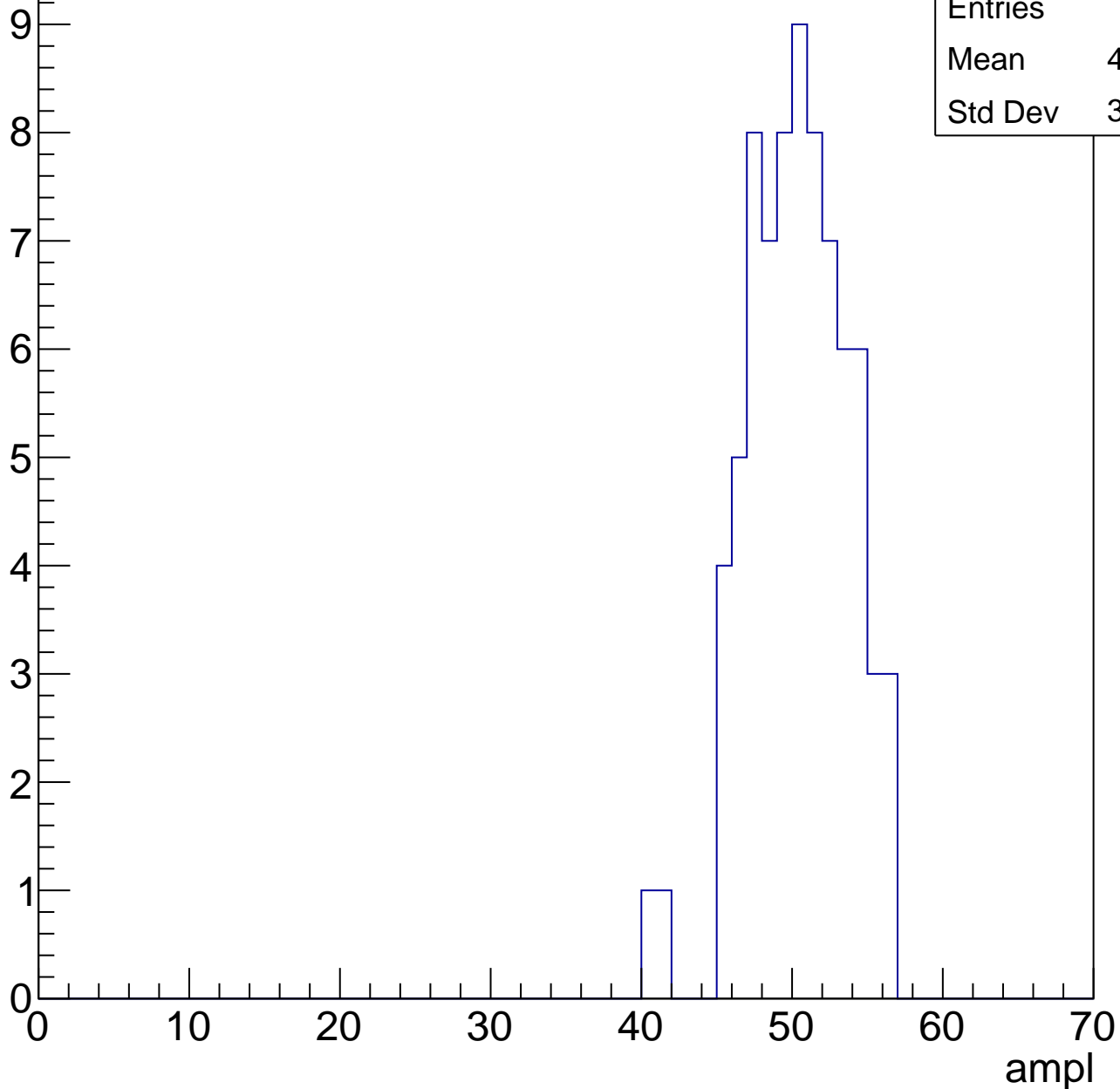
Gaus Width: 3.3338



B0L000S, U2-ch18, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

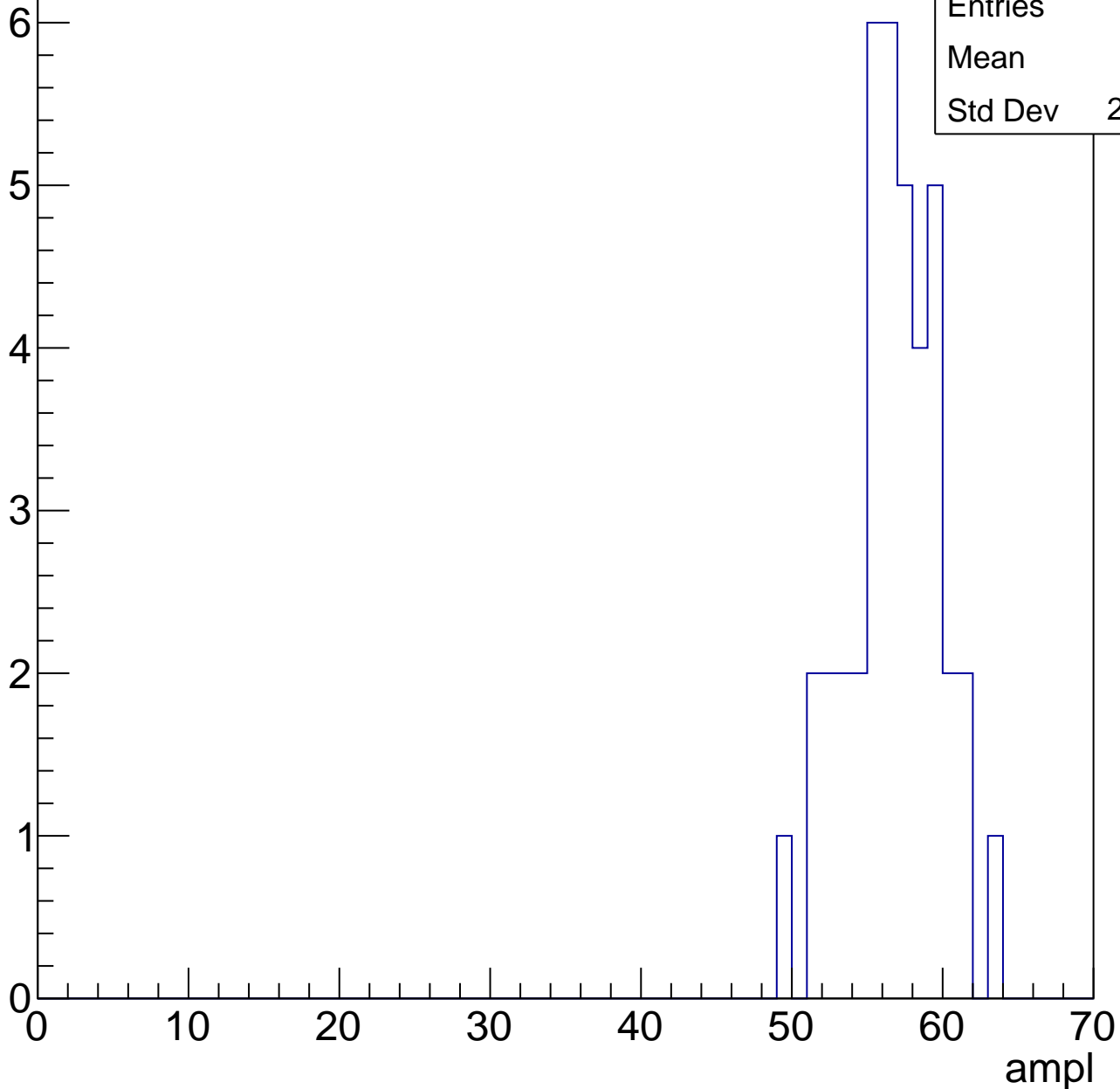


B0L000S, U2-ch18, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	40
Mean	56.3
Std Dev	2.985

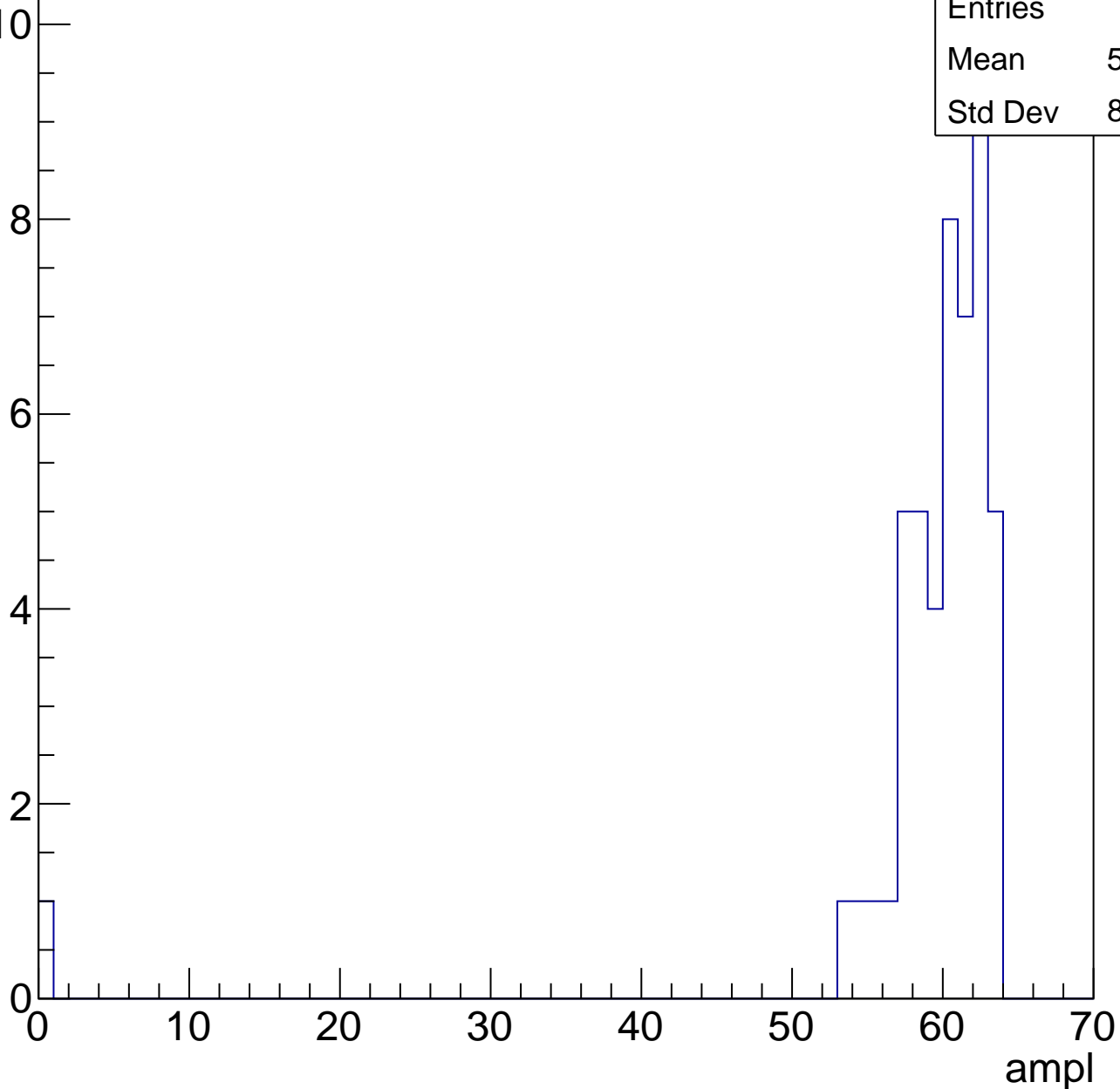


B0L000S, U2-ch18, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

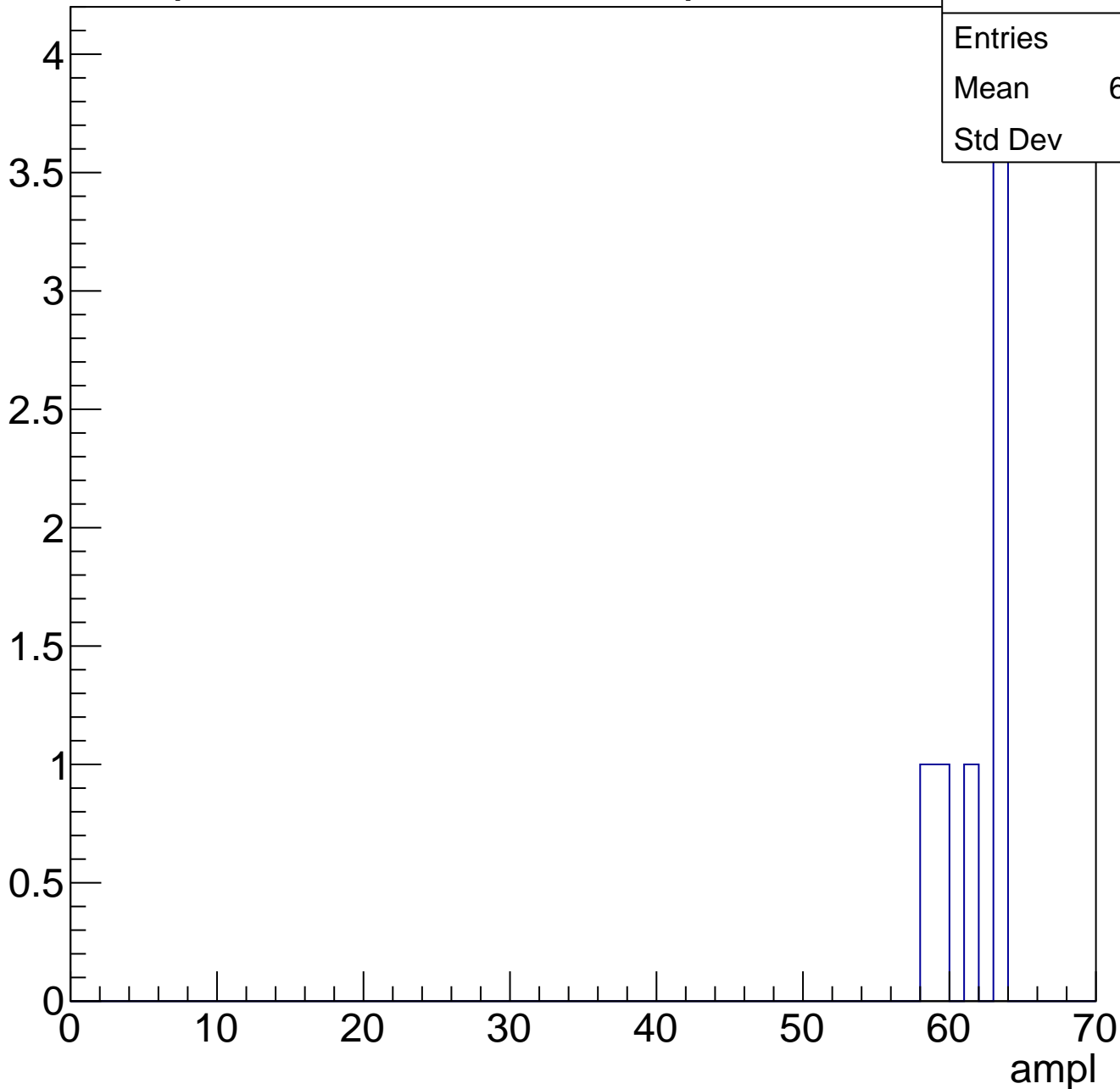
Entries	49
Mean	58.59
Std Dev	8.795



B0L000S, U2-ch18, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch18, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch19, adc0

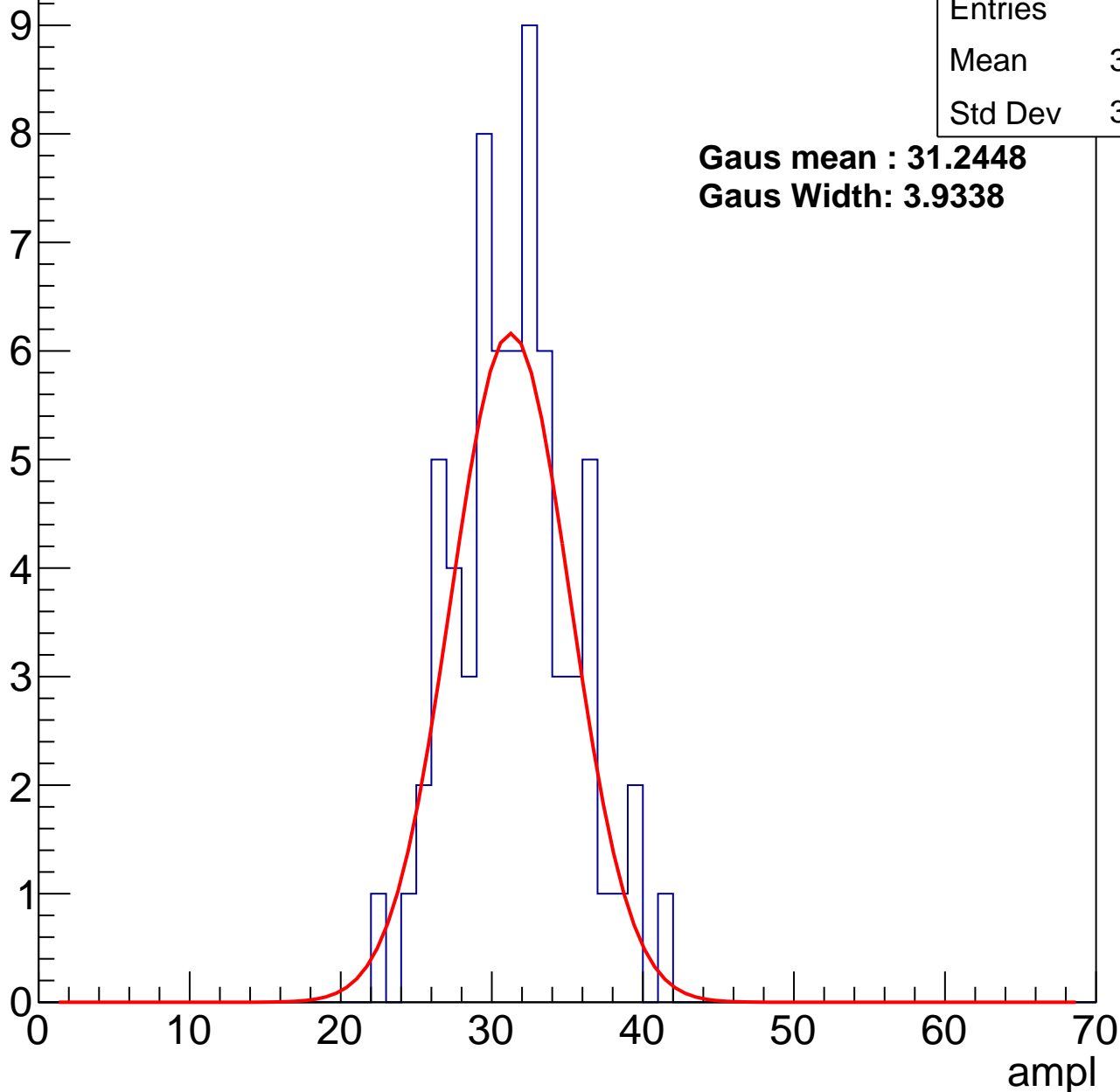
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	31.09
Std Dev	3.866

Gaus mean : 31.2448

Gaus Width: 3.9338



B0L000S, U2-ch19, adc1

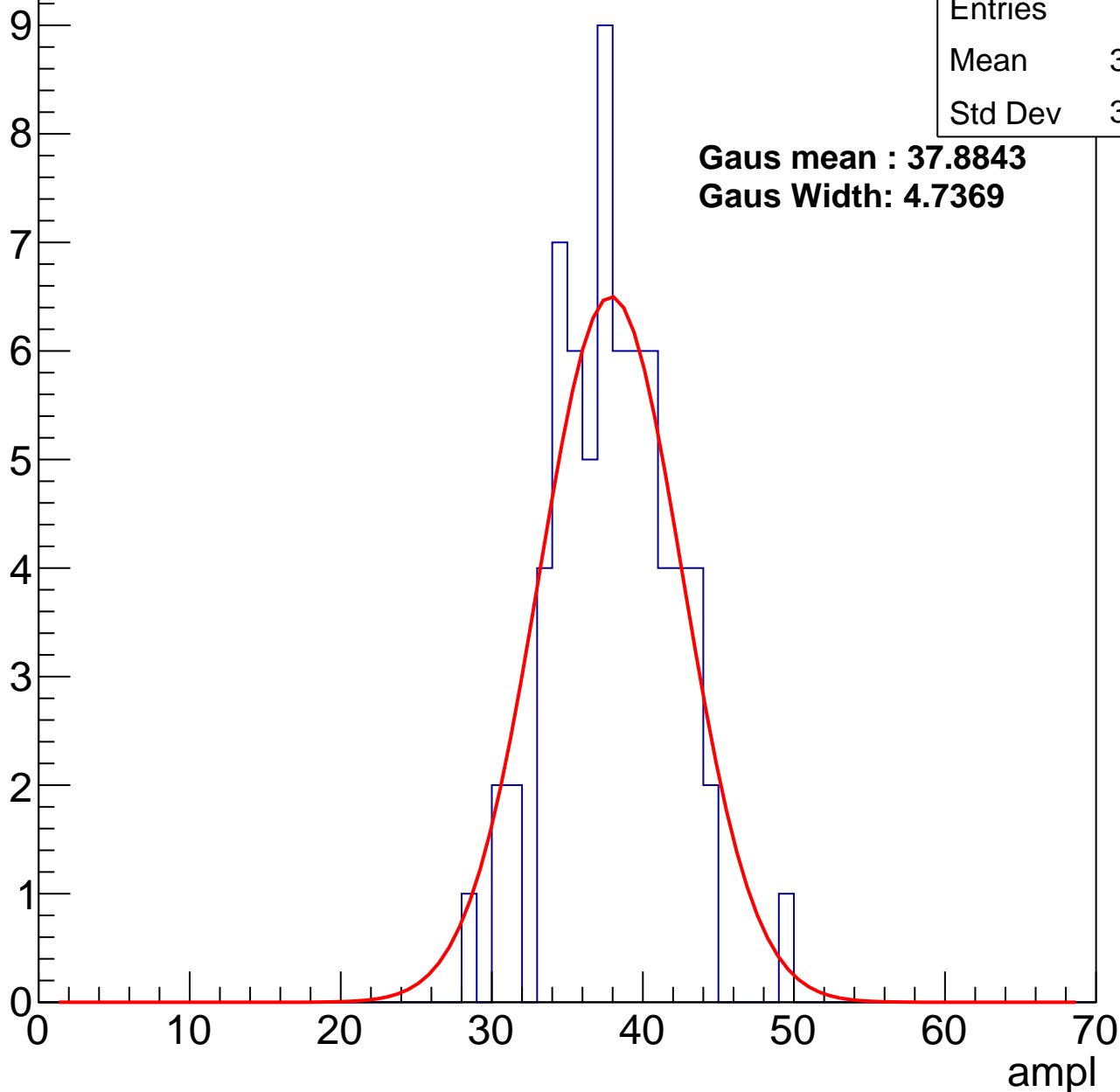
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	37.48
Std Dev	3.855

Gaus mean : 37.8843

Gaus Width: 4.7369



B0L000S, U2-ch19, adc2

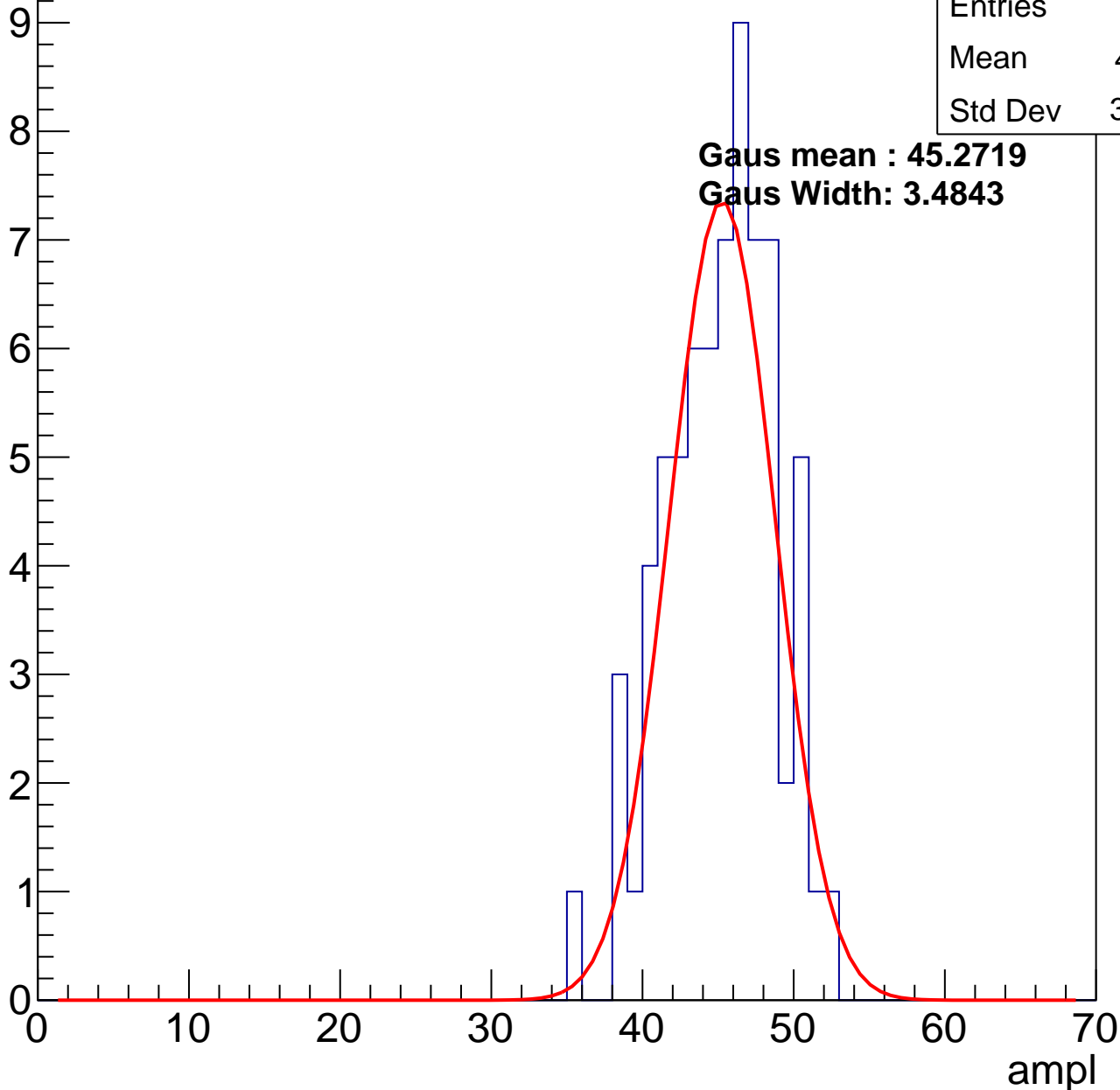
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	44.71
Std Dev	3.534

Gaus mean : 45.2719

Gaus Width: 3.4843

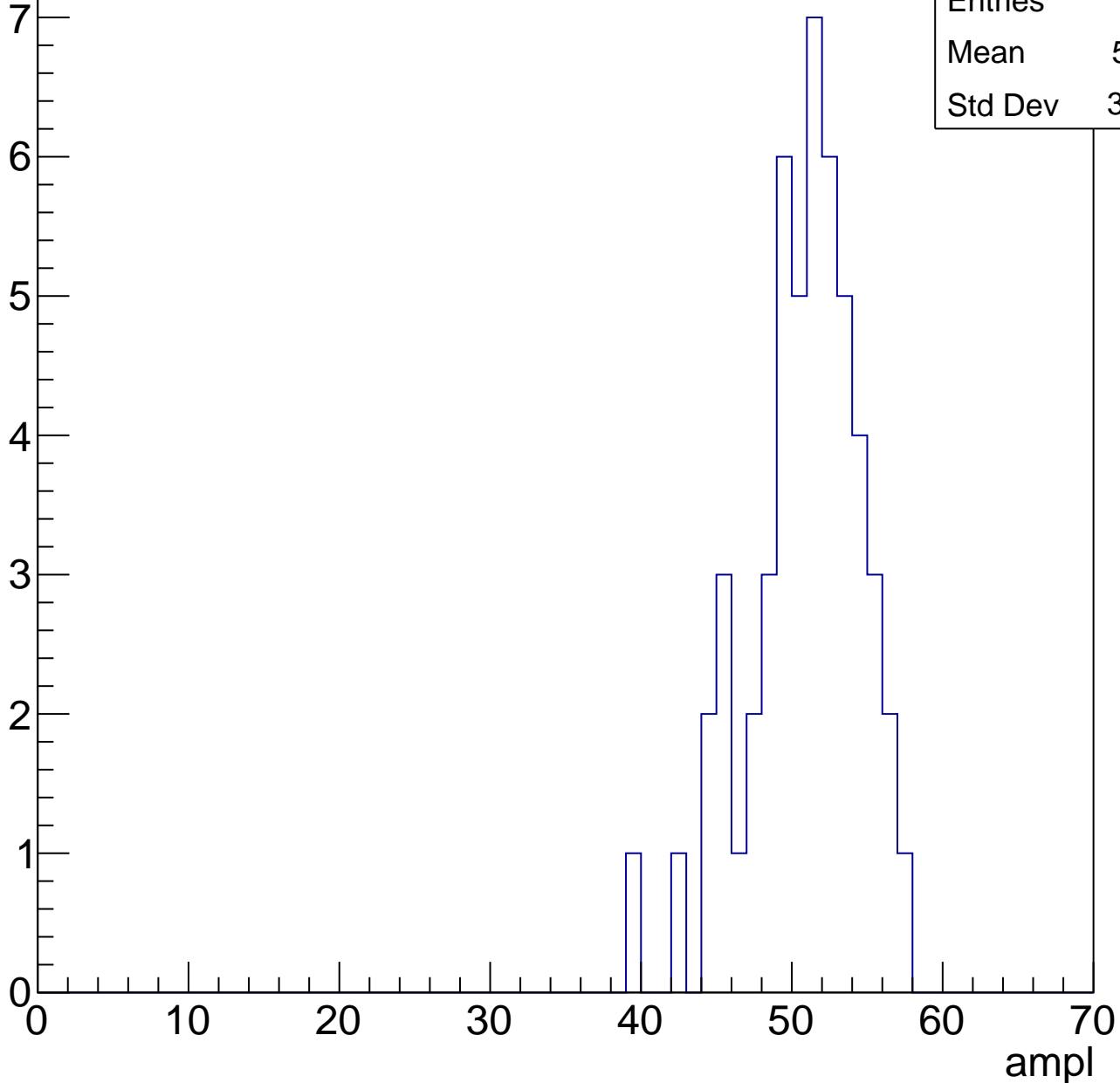


B0L000S, U2-ch19, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	50.31
Std Dev	3.708

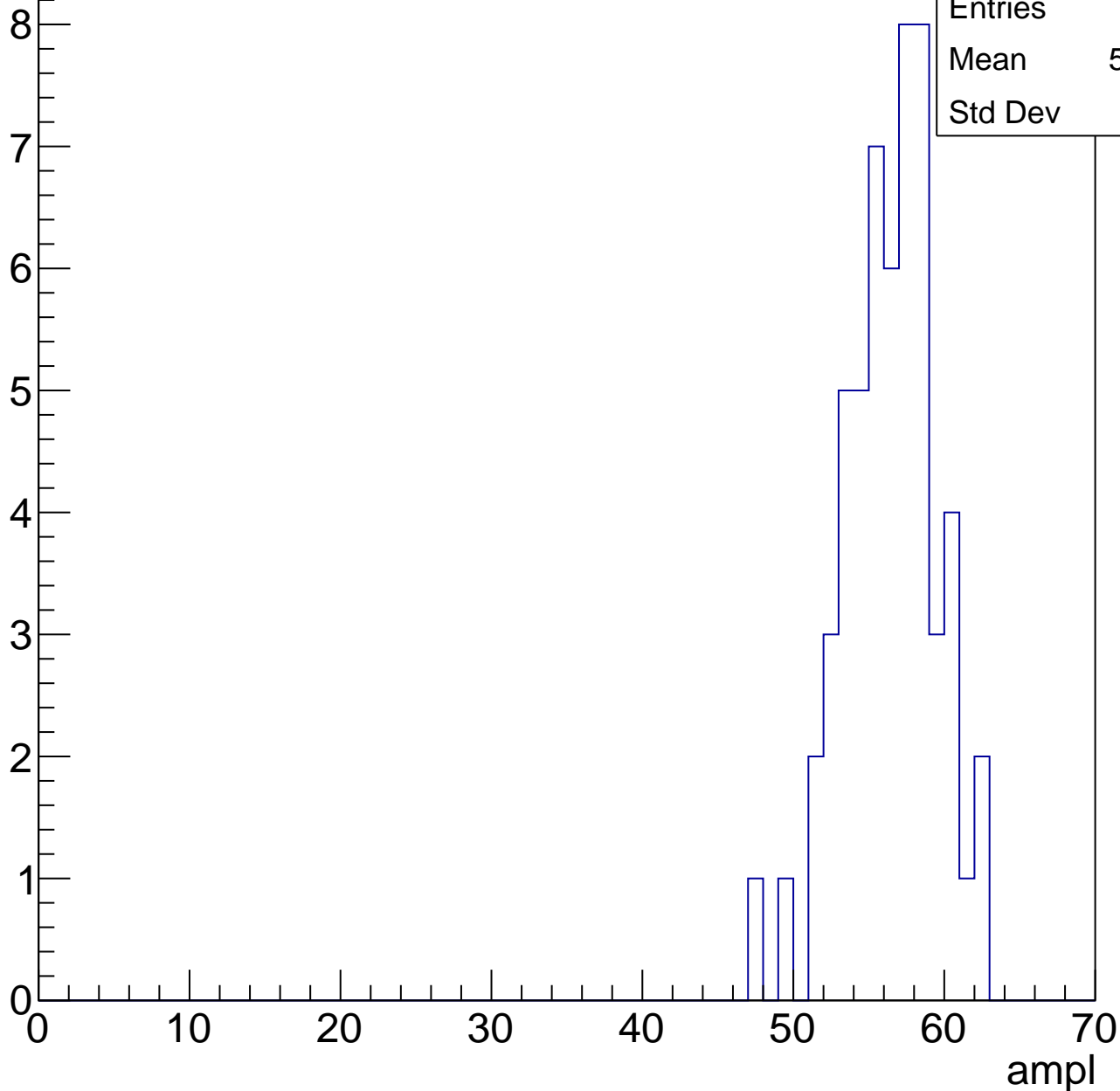


B0L000S, U2-ch19, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.93
Std Dev	3.07

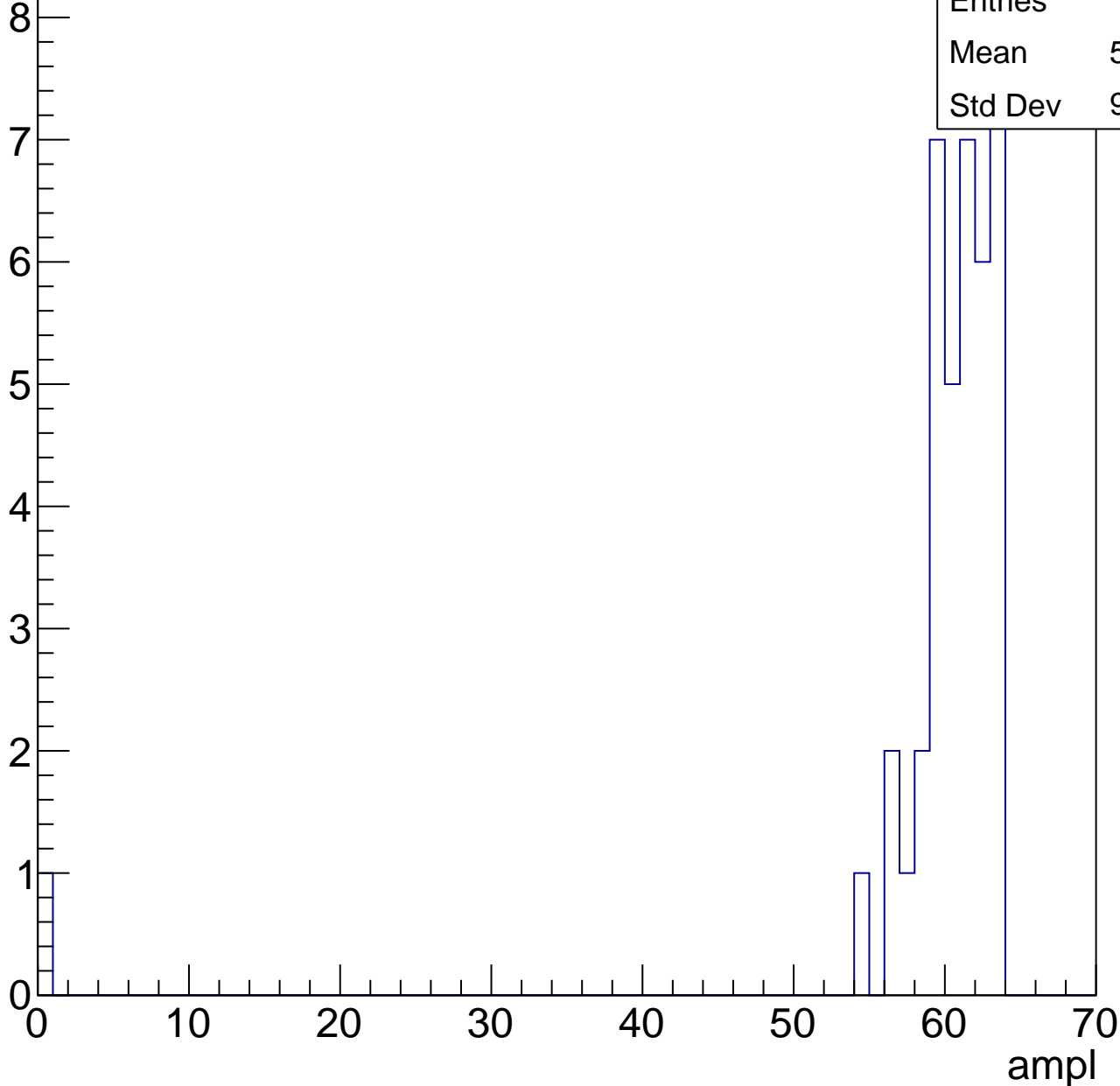


B0L000S, U2-ch19, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

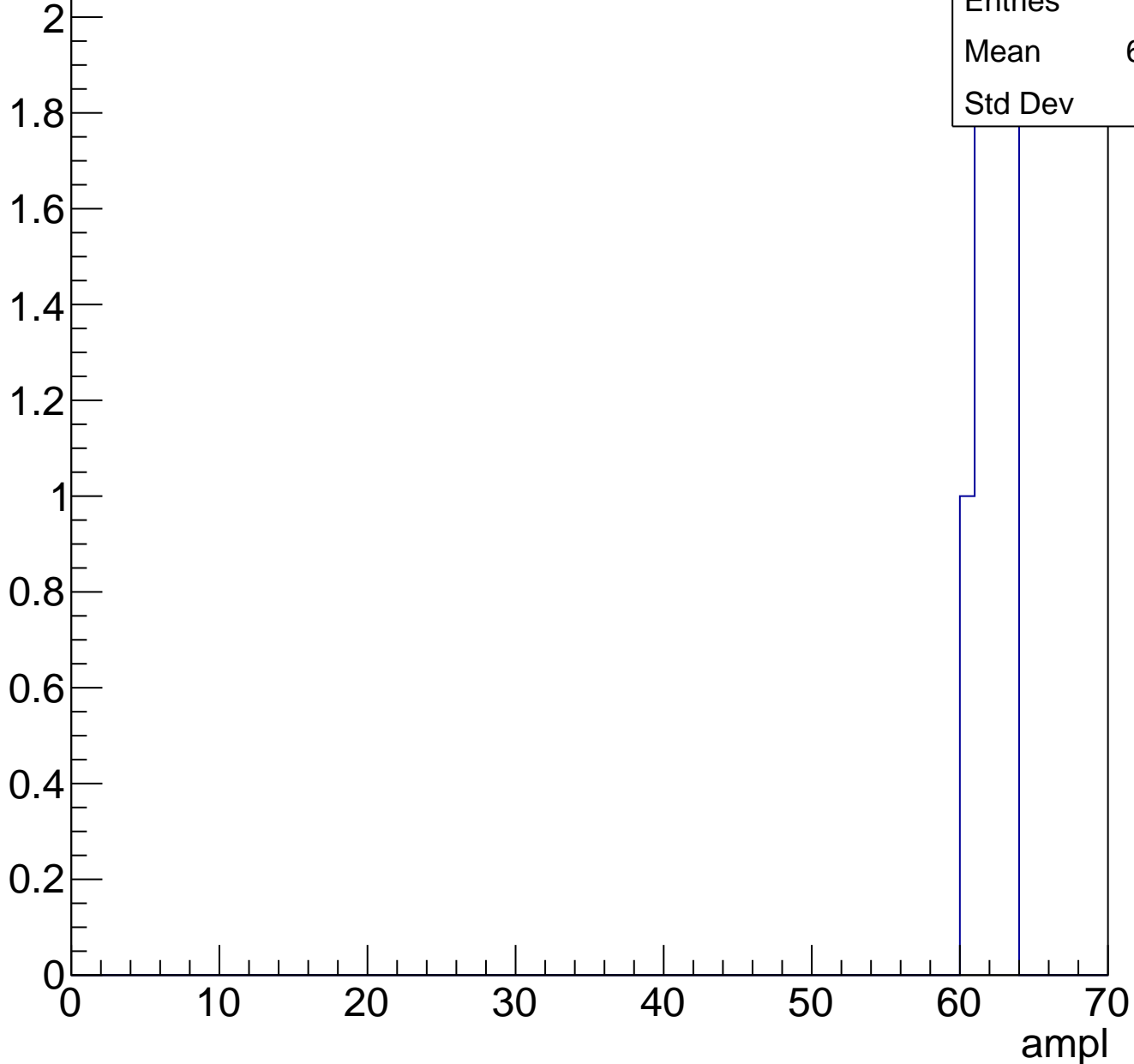
Entries	40
Mean	58.88
Std Dev	9.675



B0L000S, U2-ch19, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch19, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch20, adc0

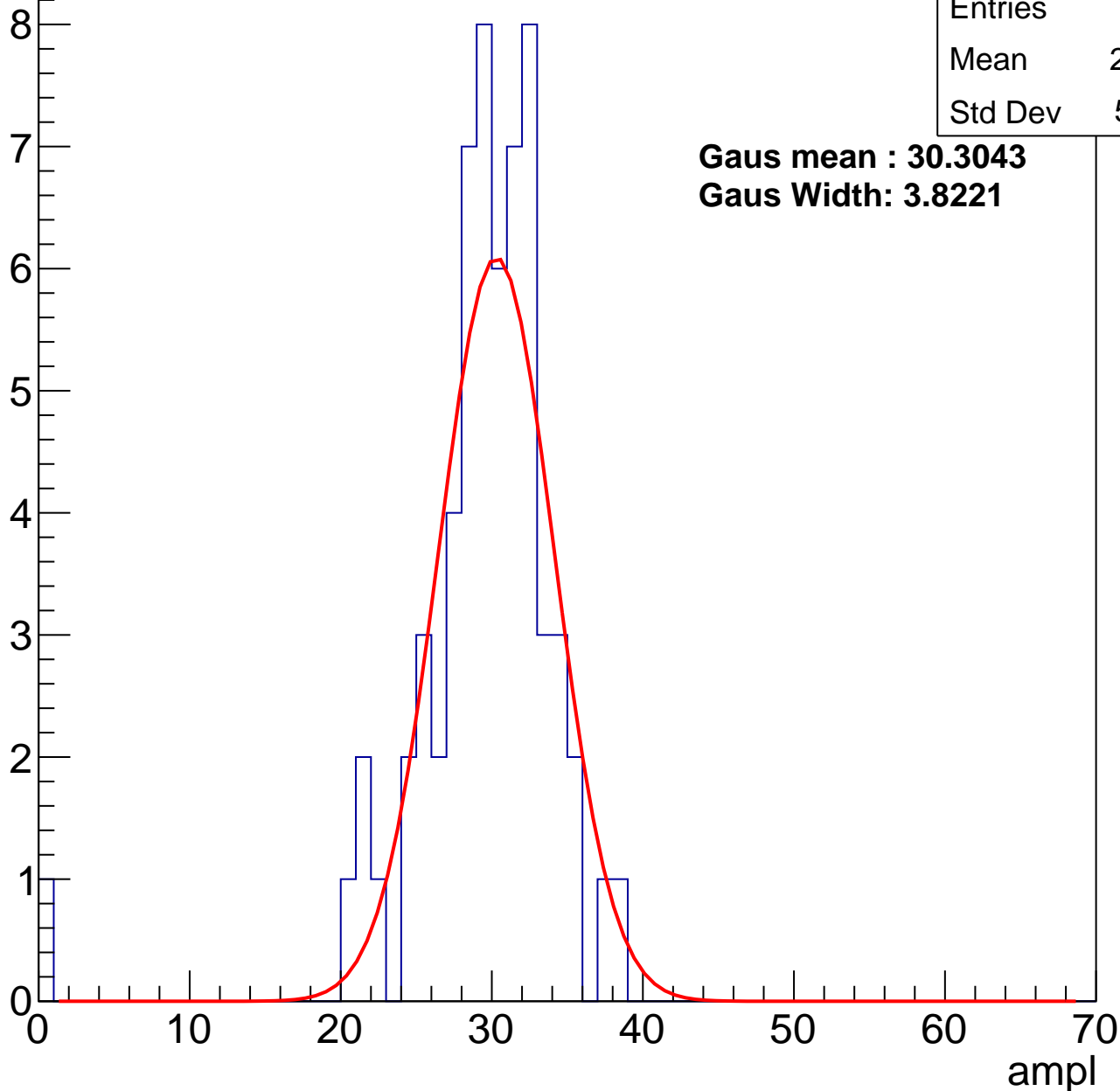
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	28.94
Std Dev	5.211

Gaus mean : 30.3043

Gaus Width: 3.8221



B0L000S, U2-ch20, adc1

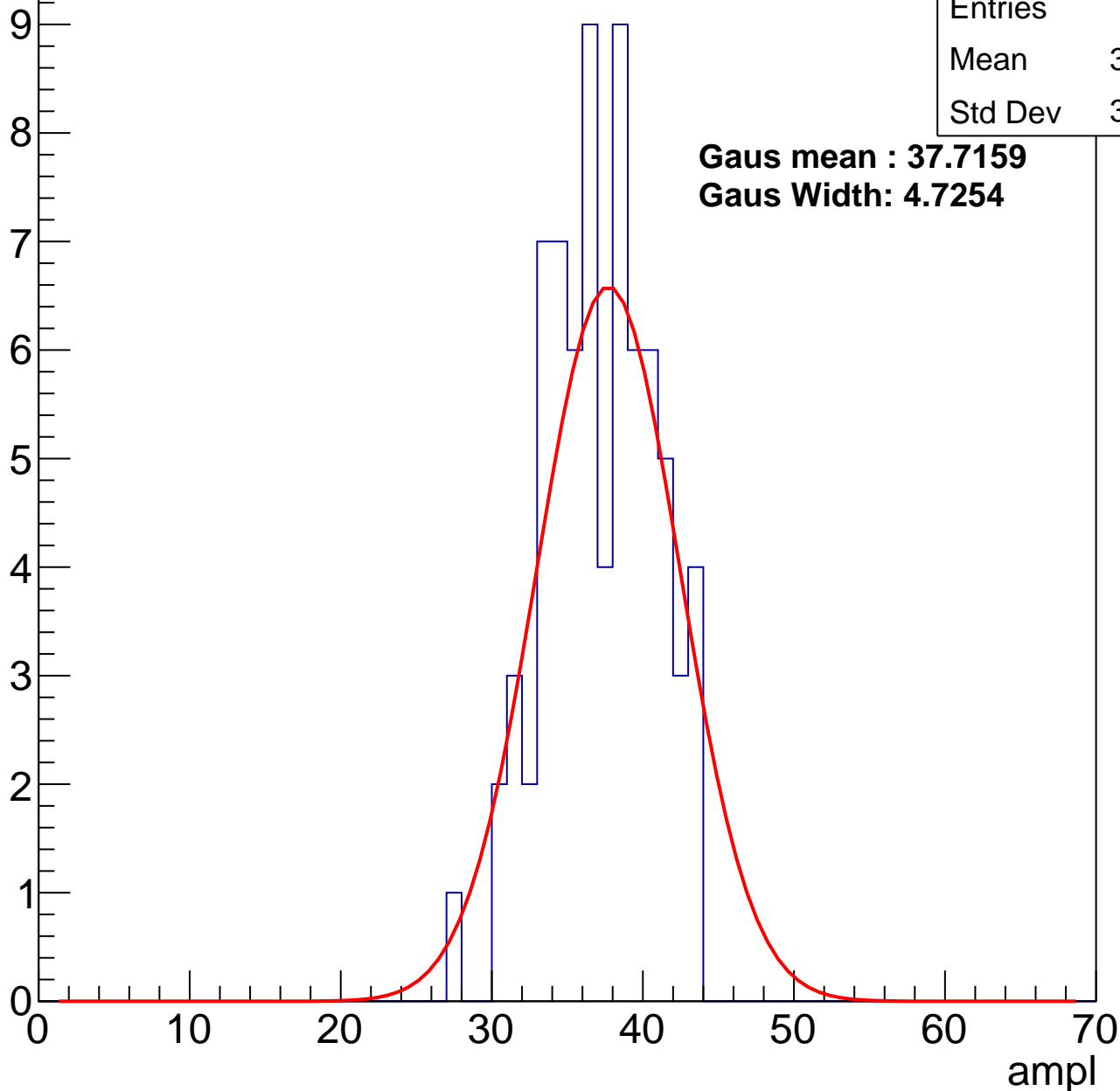
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.68
Std Dev	3.568

Gaus mean : 37.7159

Gaus Width: 4.7254



B0L000S, U2-ch20, adc2

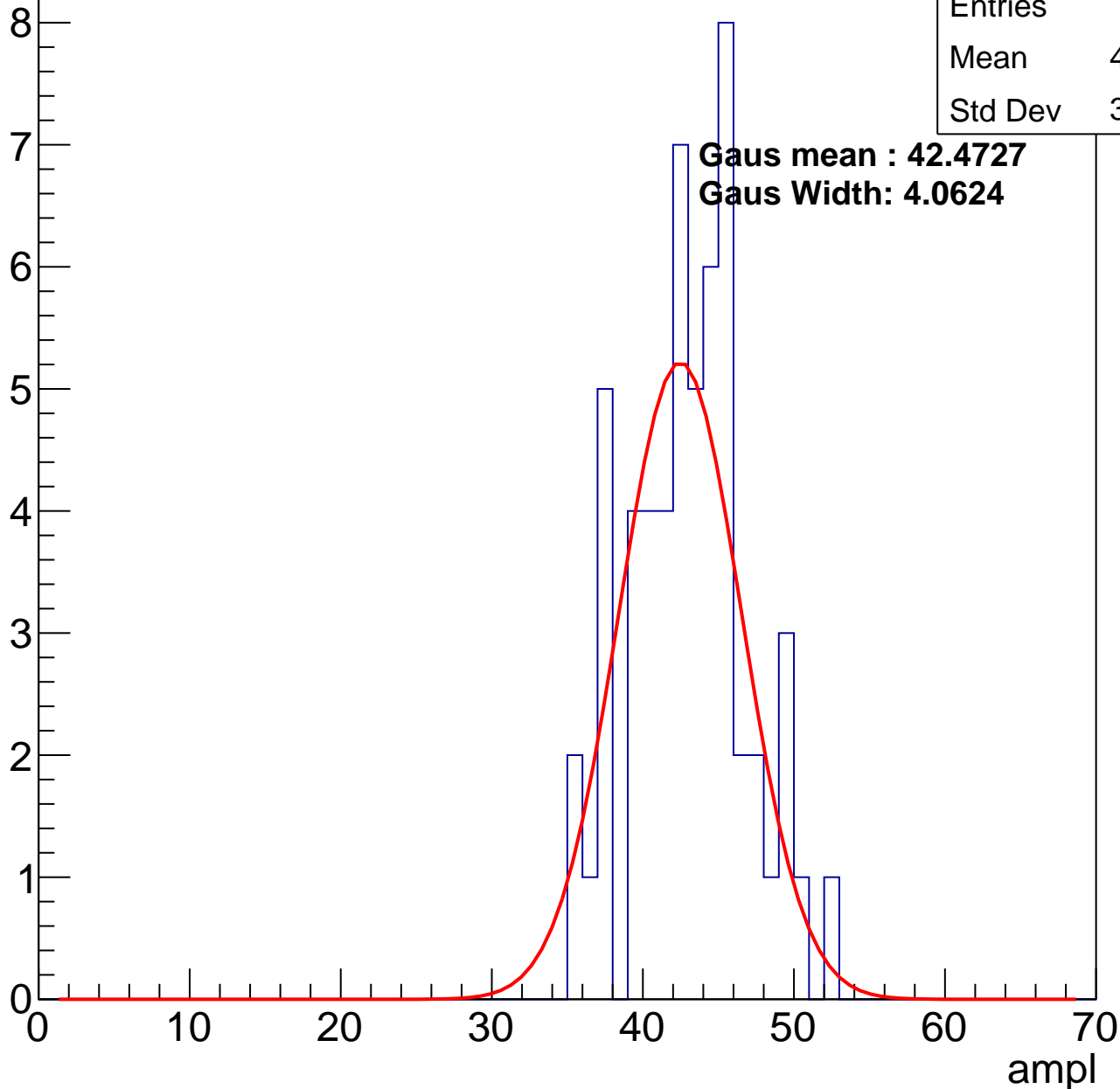
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.62
Std Dev	3.834

Gaus mean : 42.4727

Gaus Width: 4.0624

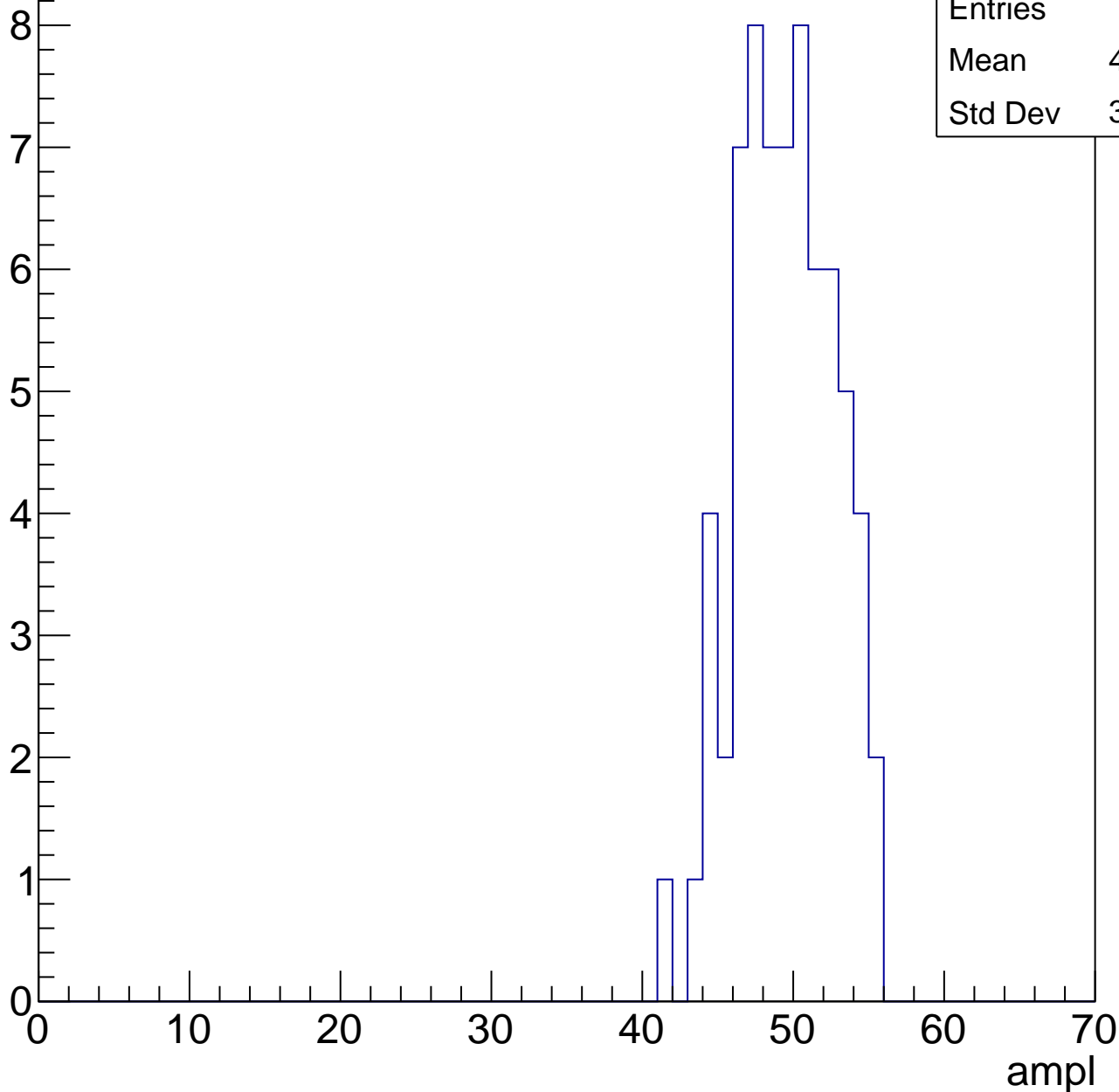


B0L000S, U2-ch20, adc3

calib_packv5_042523_0143.root, FC#5, port B1

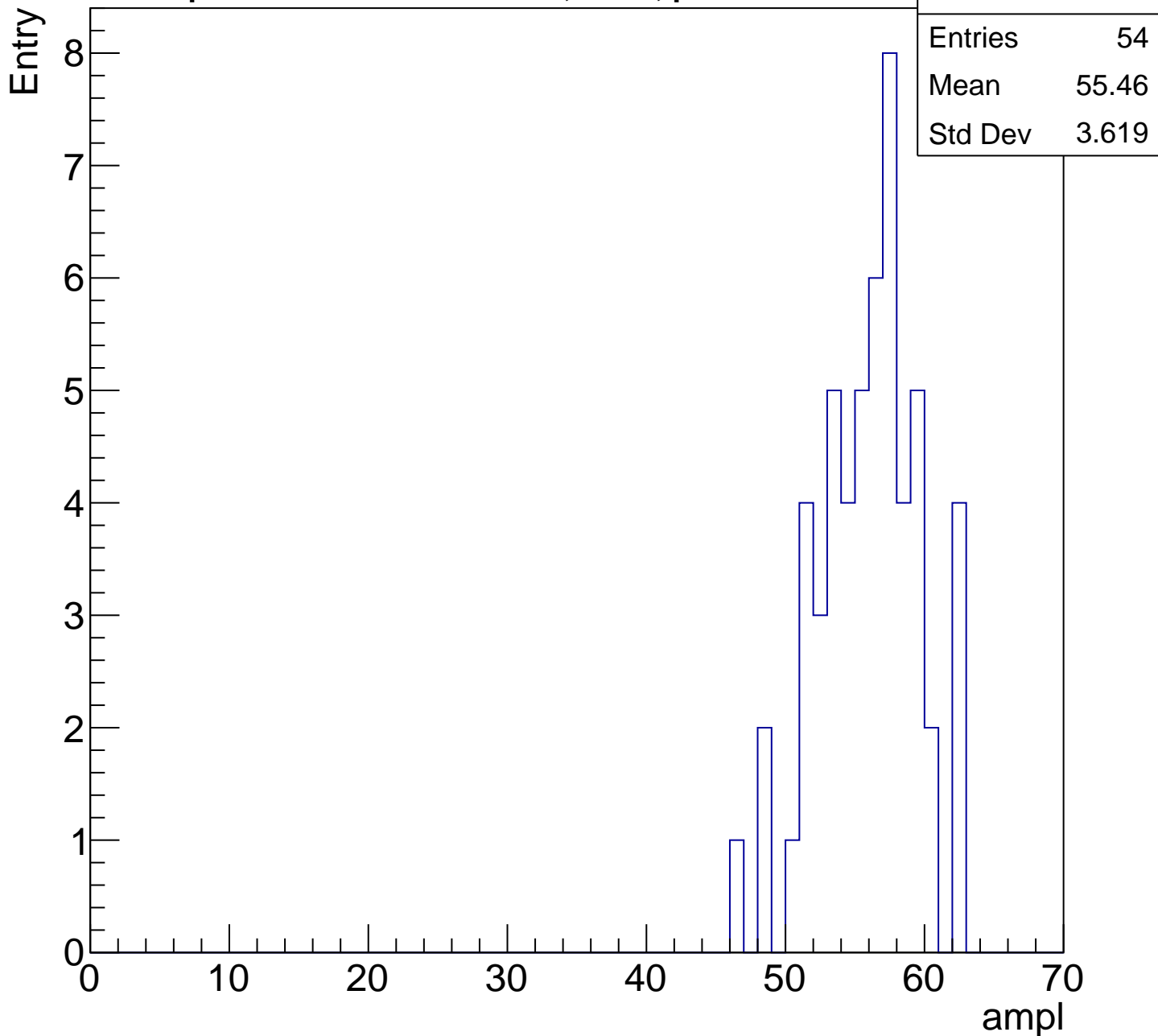
Entry

Entries	68
Mean	49.06
Std Dev	3.143



B0L000S, U2-ch20, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch20, adc5

calib_packv5_042523_0143.root, FC#5, port B1

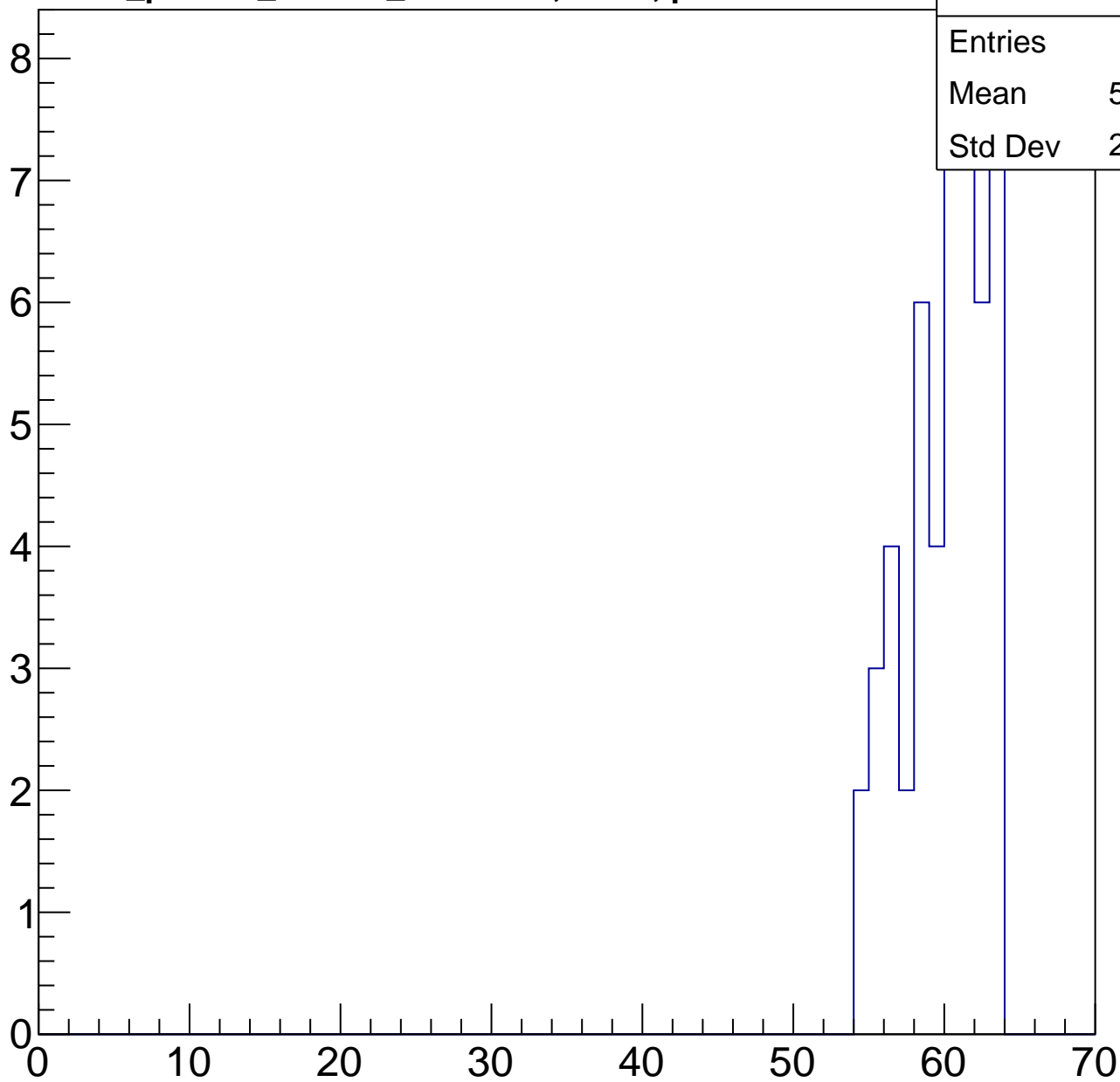
Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.59
Std Dev	2.628

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch20, adc6

calib_packv5_042523_0143.root, FC#5, port B1

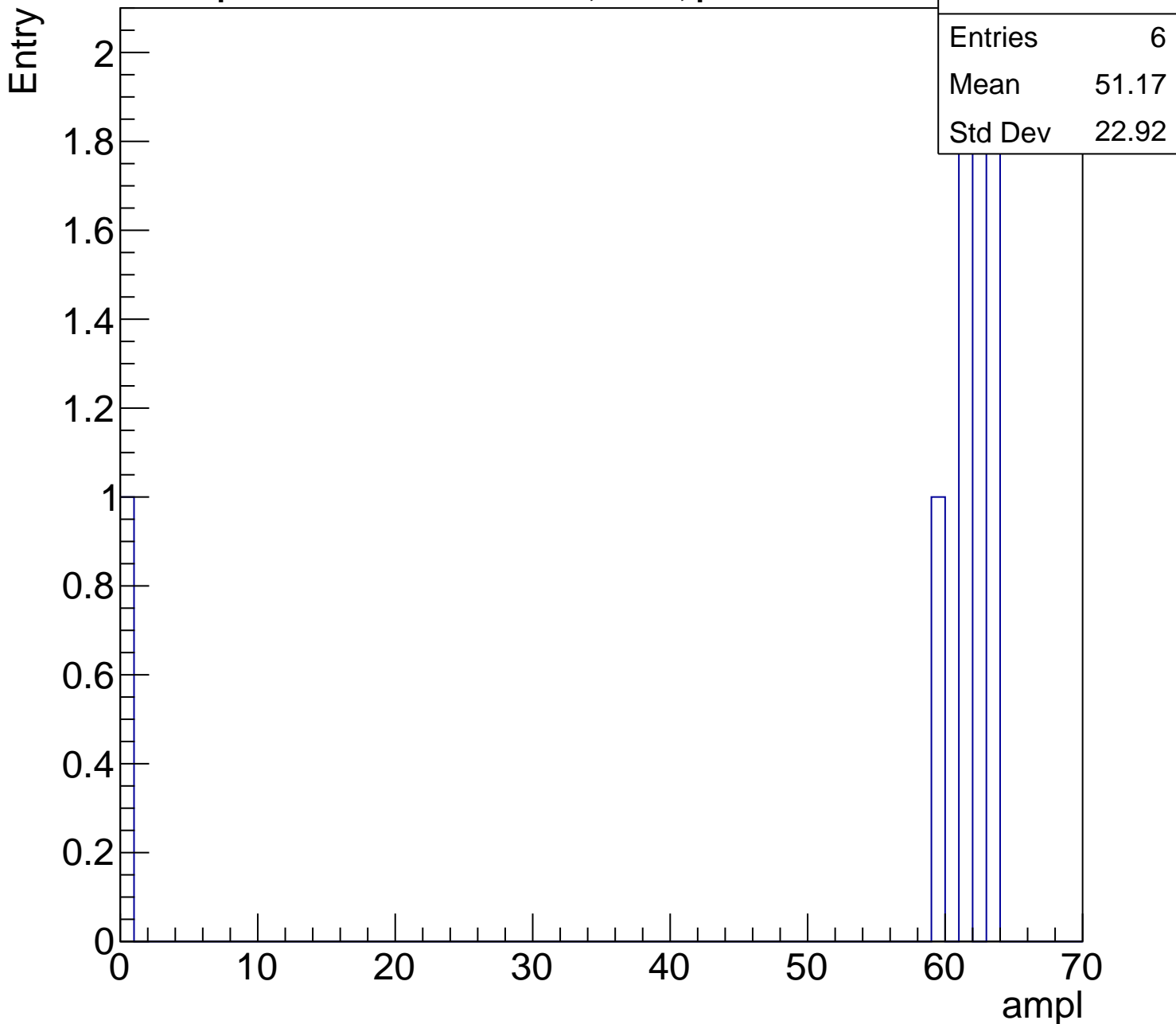
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.92

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch20, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch21, adc0

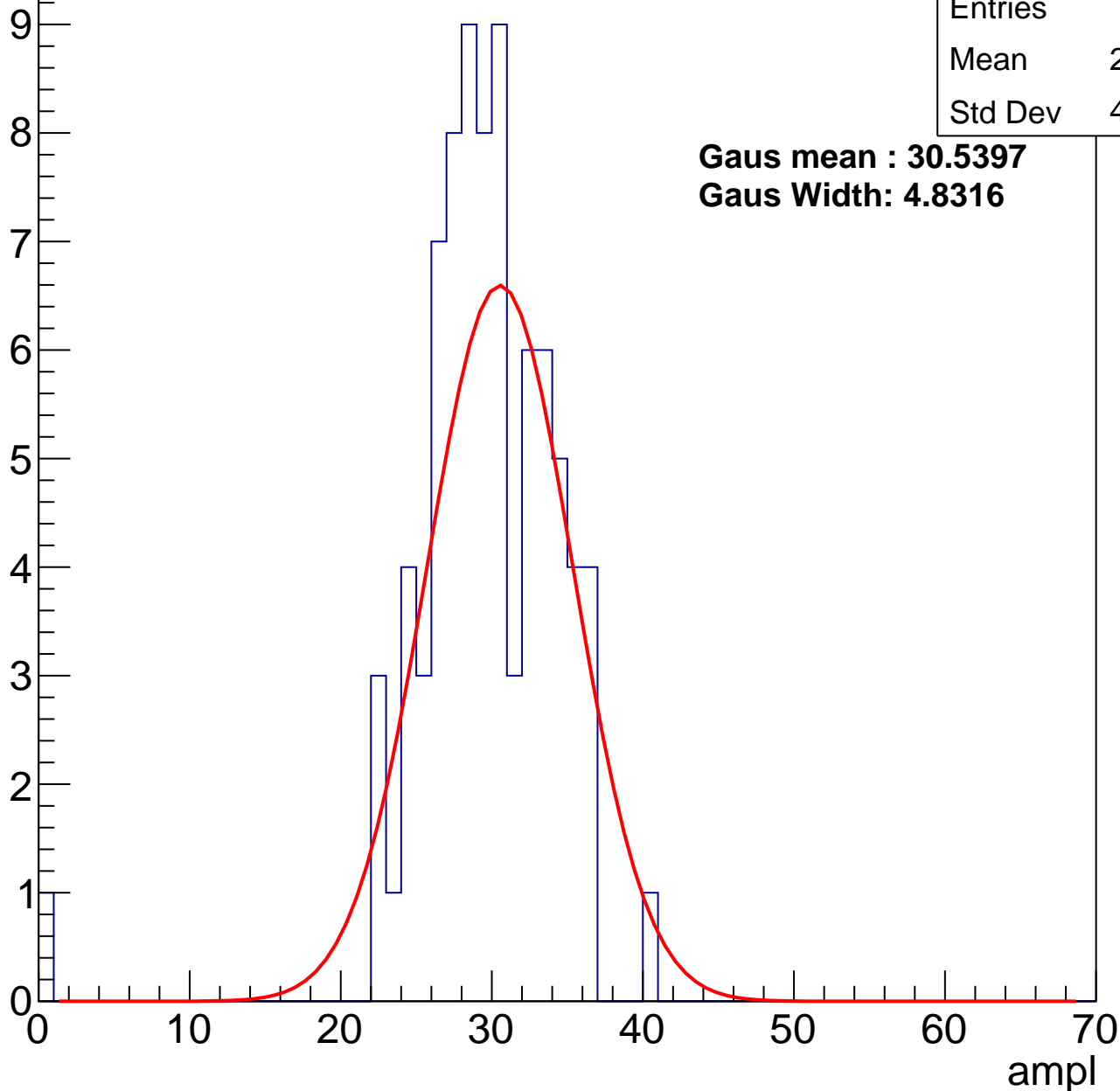
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	29.13
Std Dev	4.977

Gaus mean : 30.5397

Gaus Width: 4.8316



B0L000S, U2-ch21, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	36.83
Std Dev	3.965

Gaus mean : 37.6473

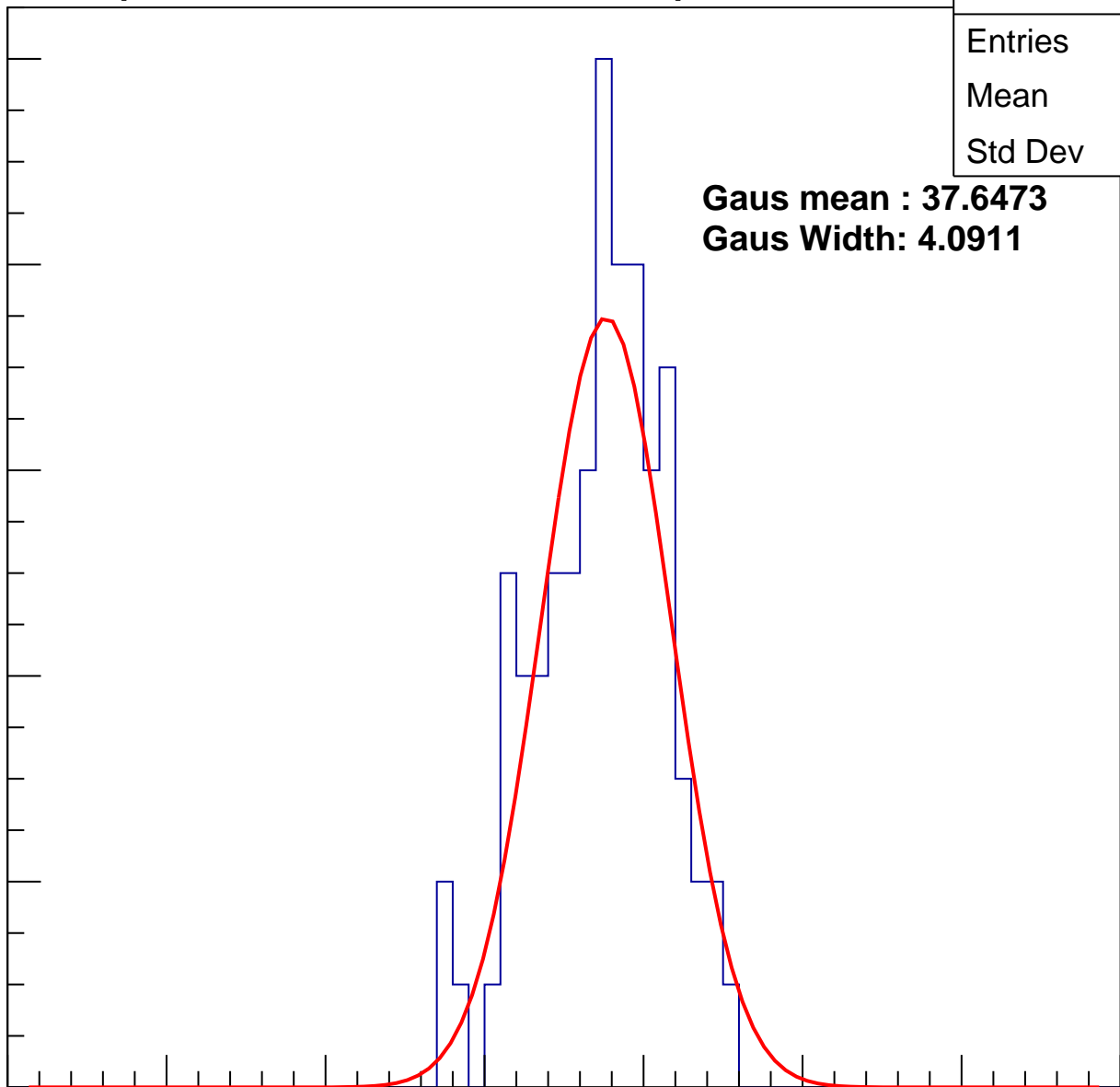
Gaus Width: 4.0911

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch21, adc2

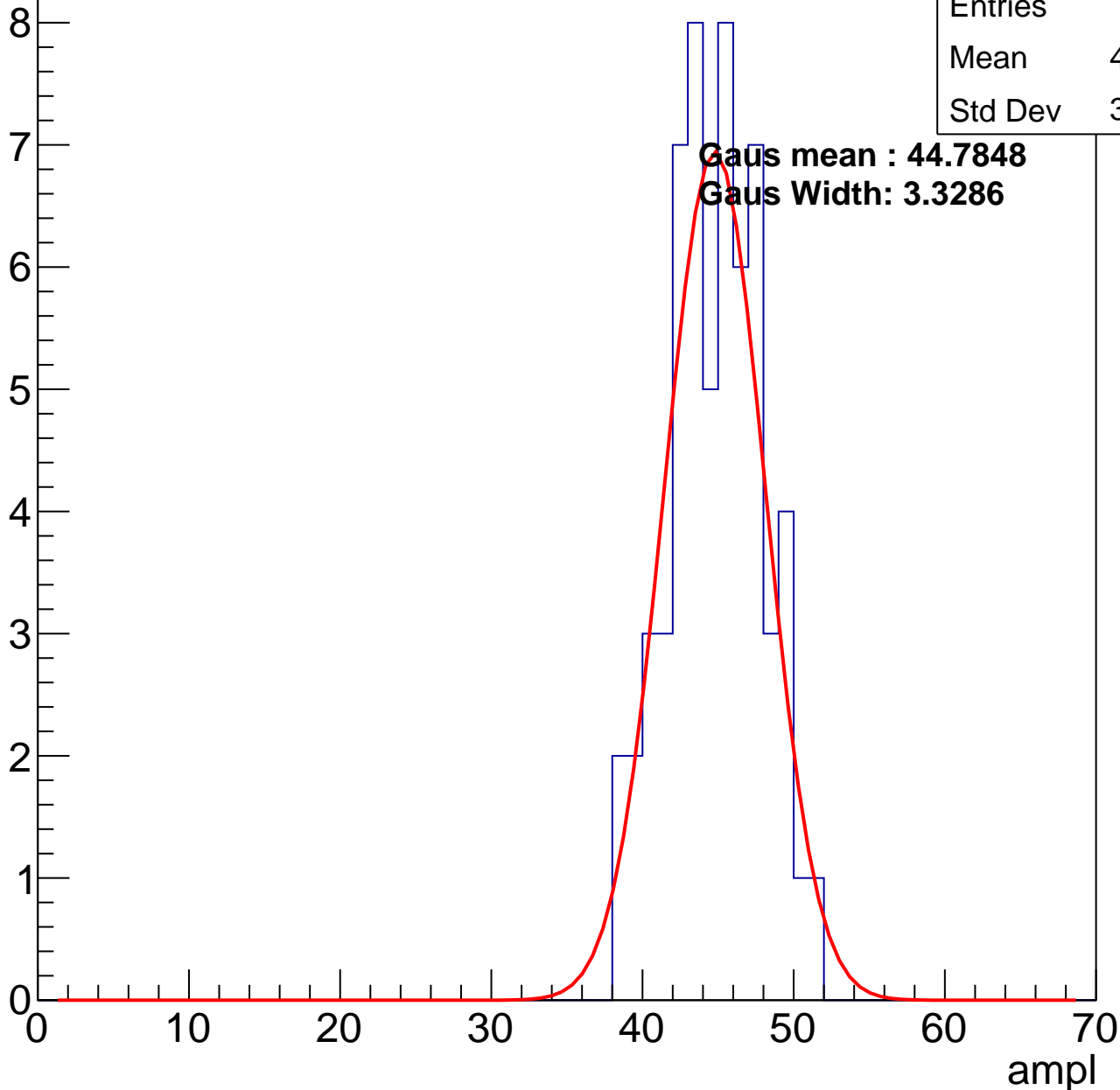
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.35
Std Dev	3.038

Gaus mean : 44.7848

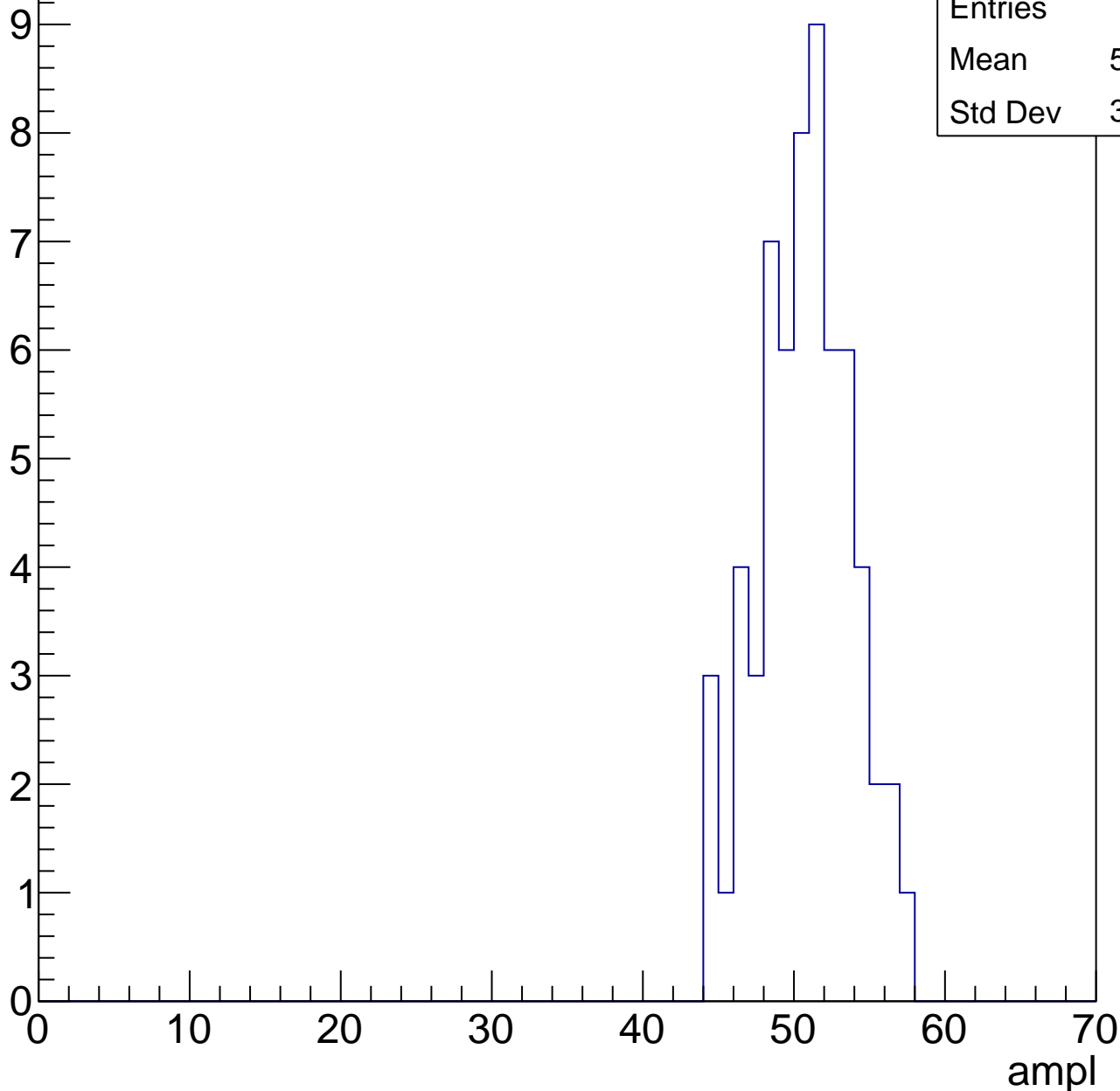
Gaus Width: 3.3286



B0L000S, U2-ch21, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



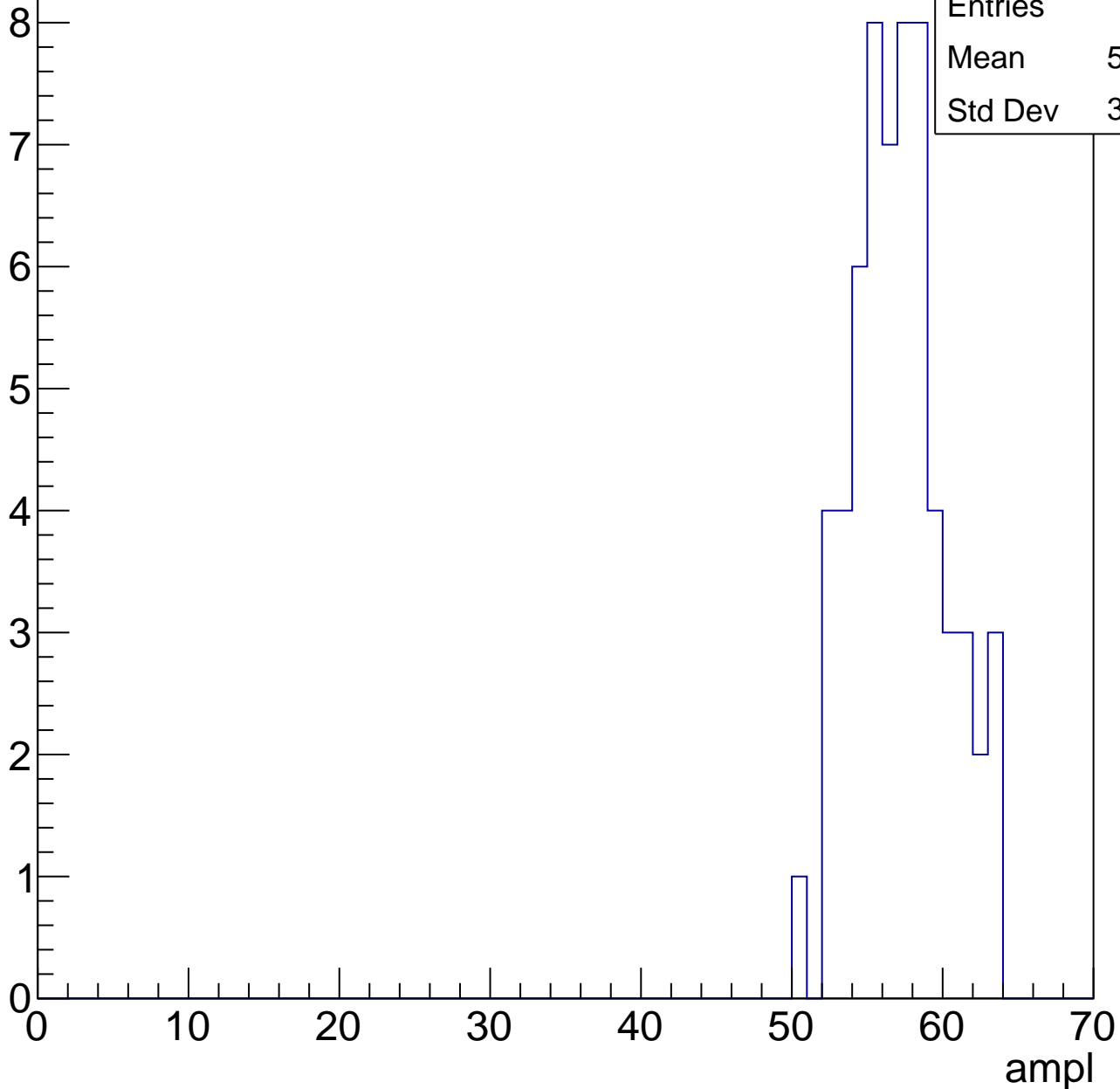
Entries	62
Mean	50.26
Std Dev	3.048

B0L000S, U2-ch21, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56.69
Std Dev	3.022



B0L000S, U2-ch21, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	59.1
Std Dev	9.748

ampl

0

10

20

30

40

50

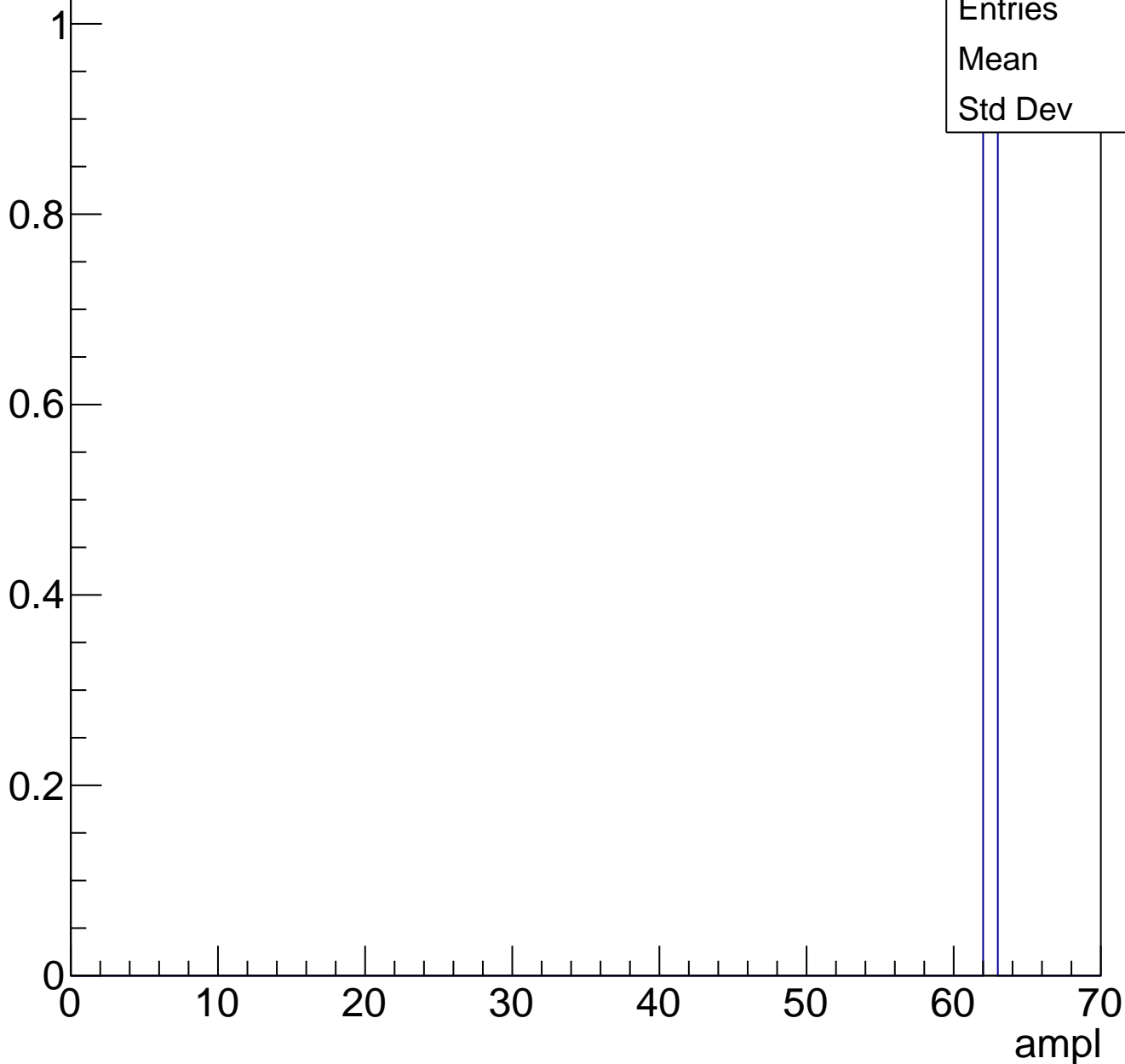
60

70

B0L000S, U2-ch21, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch21, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch22, adc0

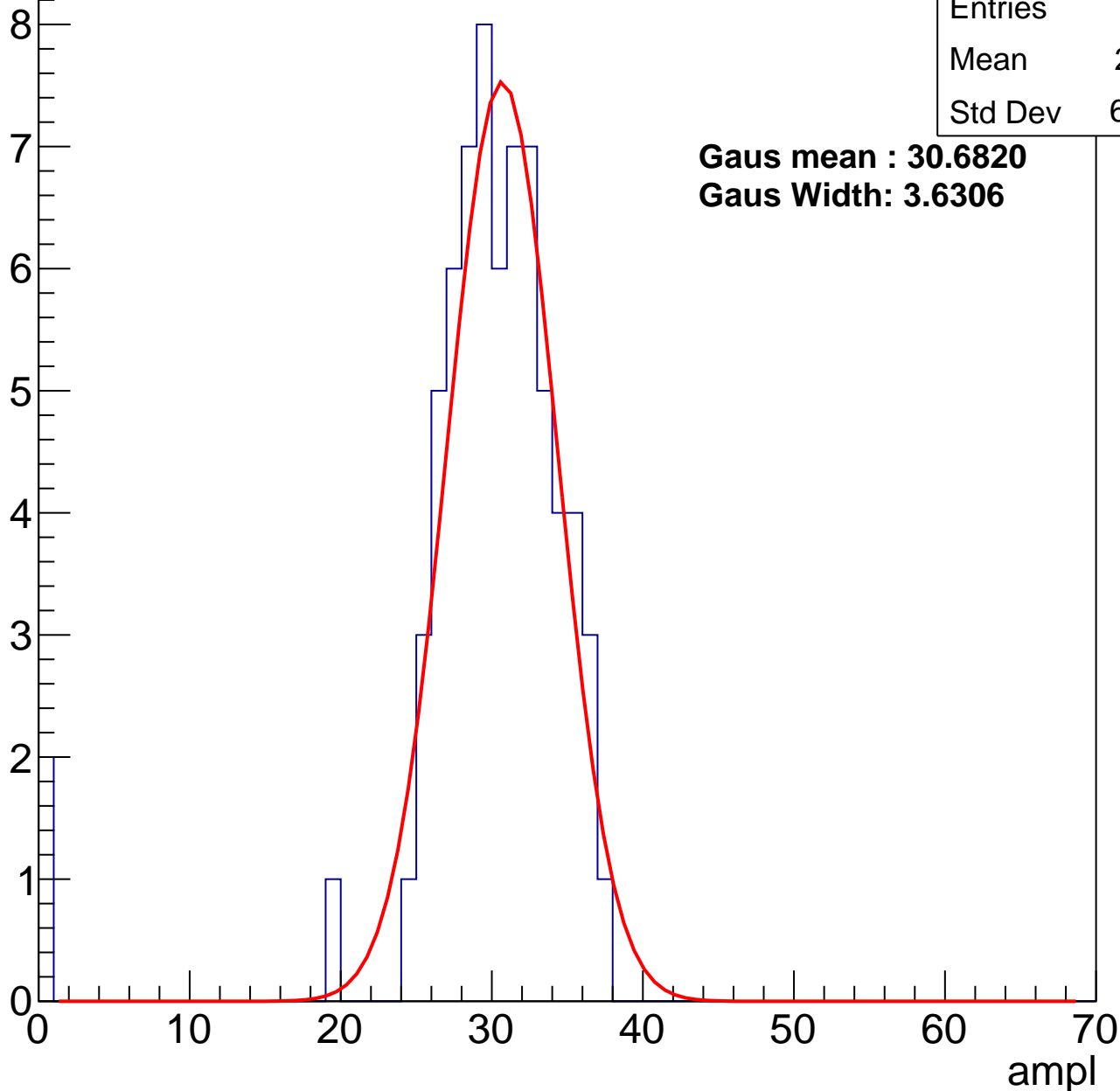
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.21
Std Dev	6.047

Gaus mean : 30.6820

Gaus Width: 3.6306



B0L000S, U2-ch22, adc1

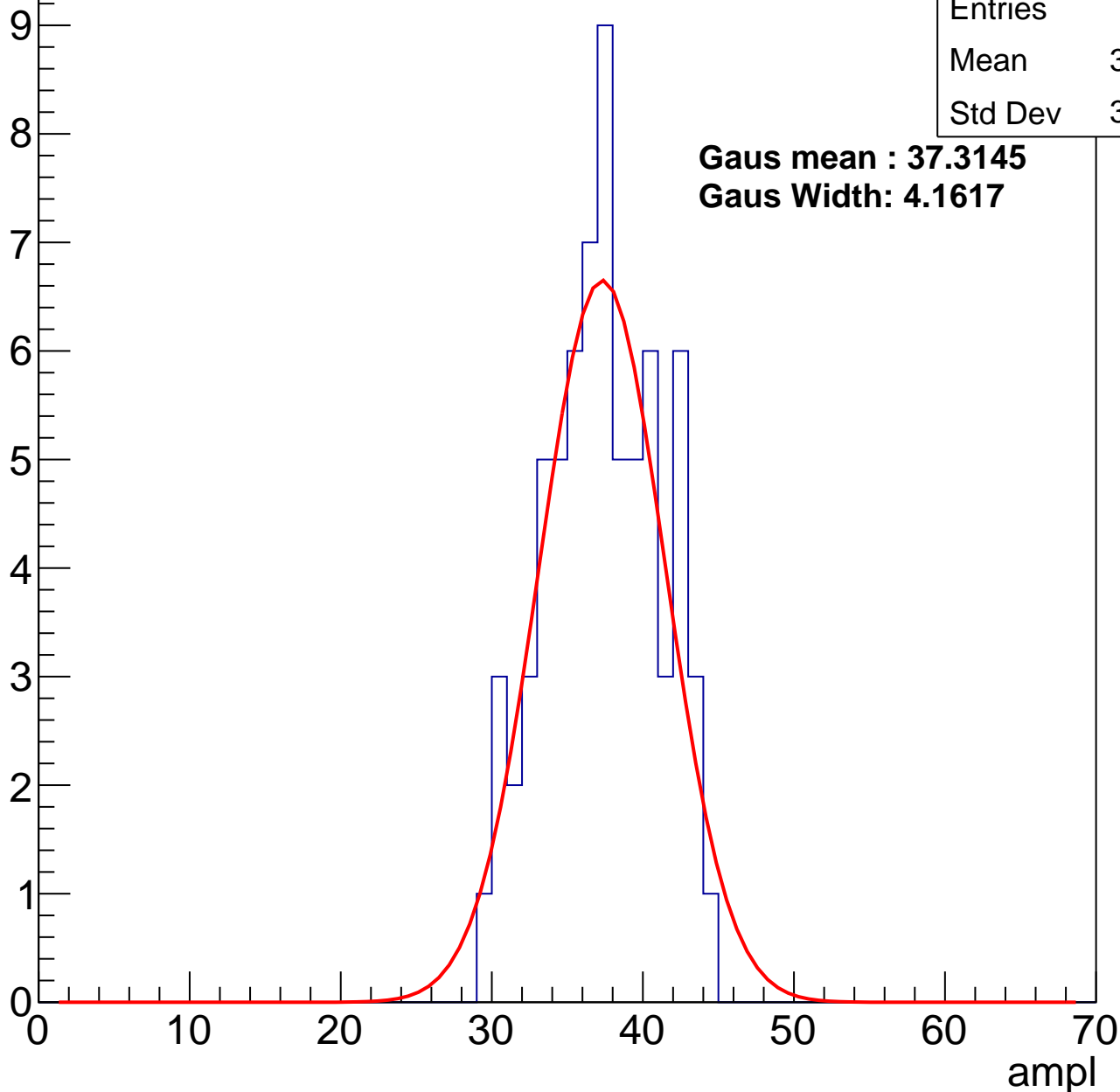
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.86
Std Dev	3.677

Gaus mean : 37.3145

Gaus Width: 4.1617



B0L000S, U2-ch22, adc2

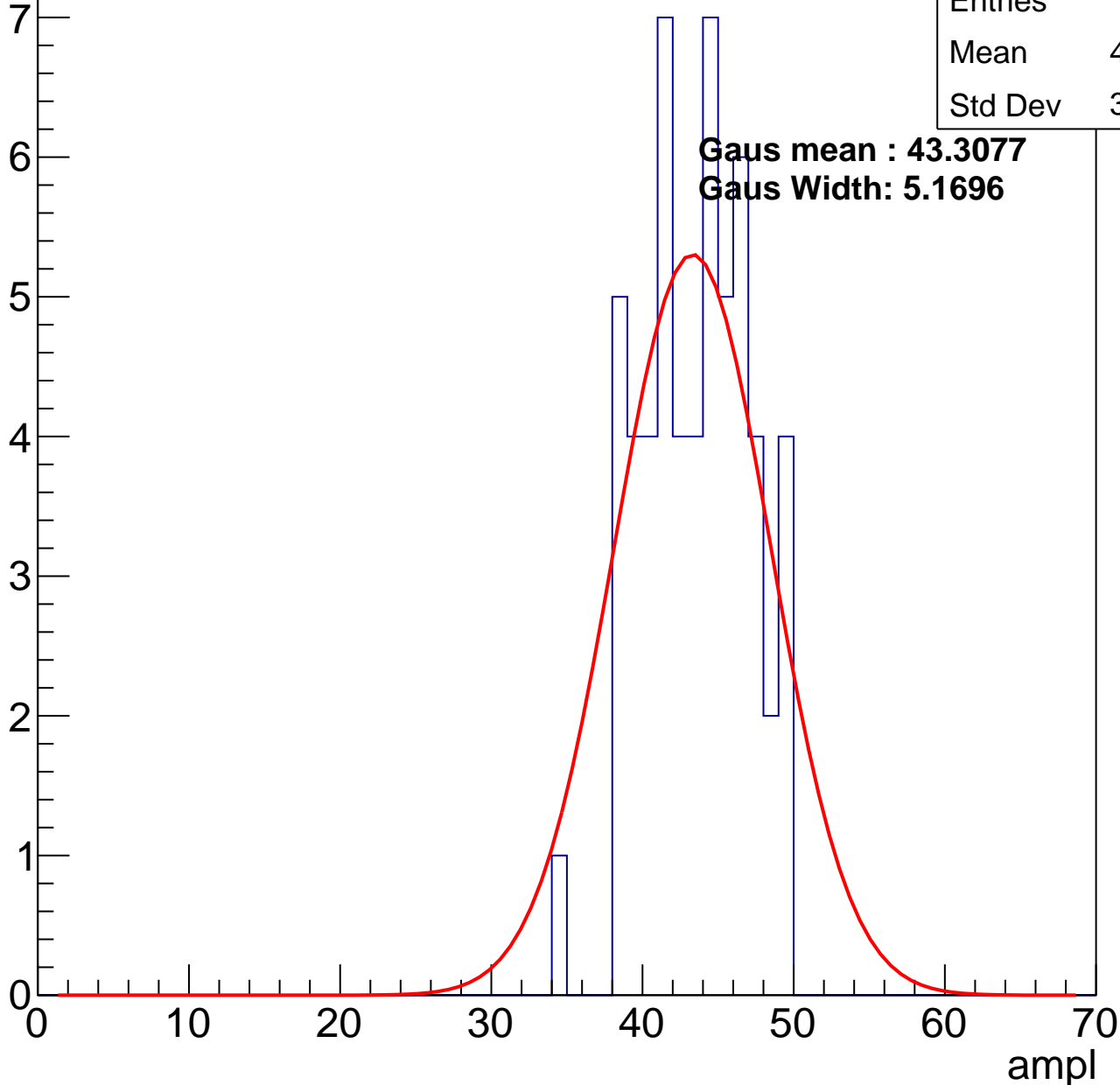
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.09
Std Dev	3.445

Gaus mean : 43.3077

Gaus Width: 5.1696

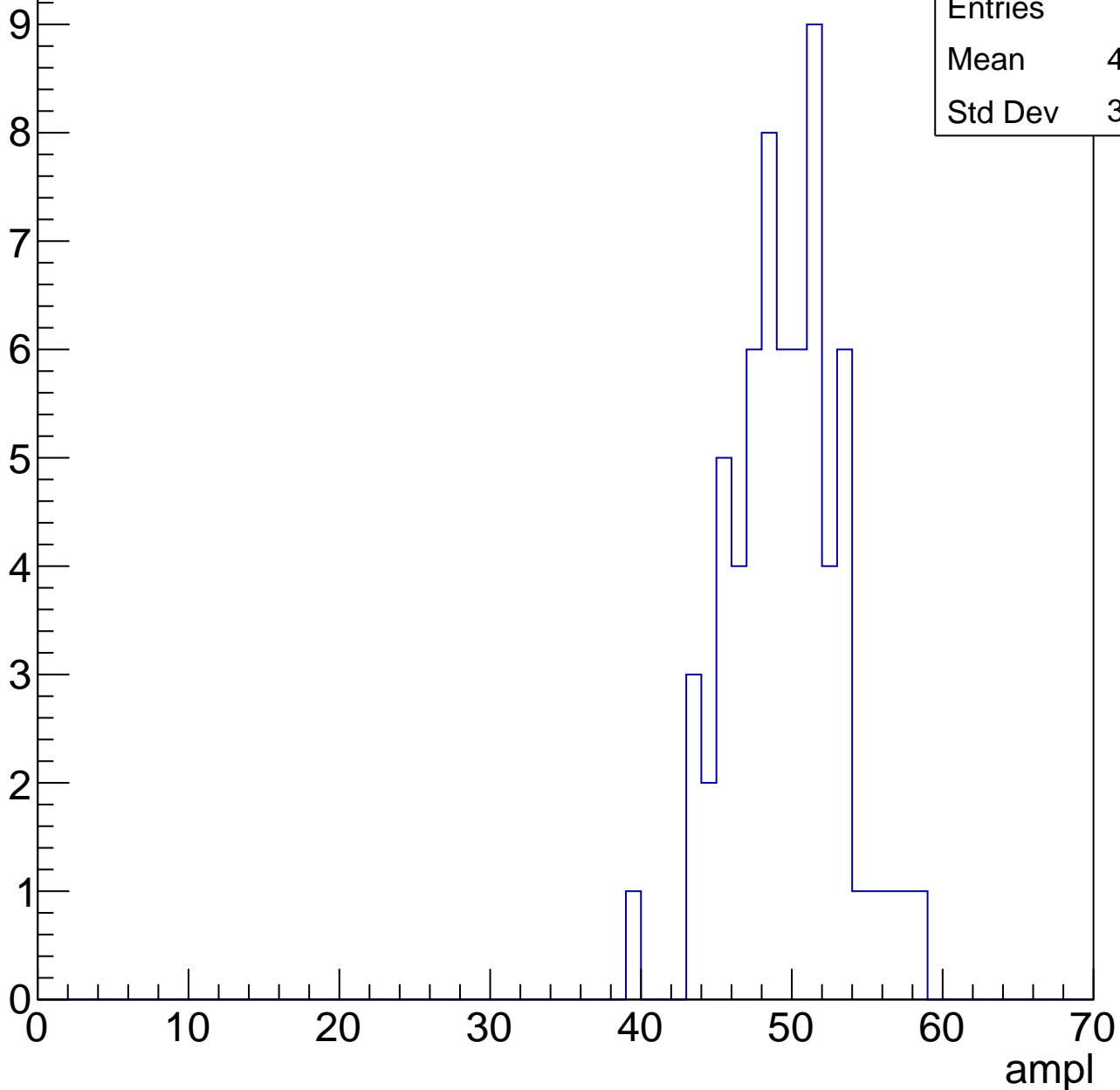


B0L000S, U2-ch22, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	49.08
Std Dev	3.583

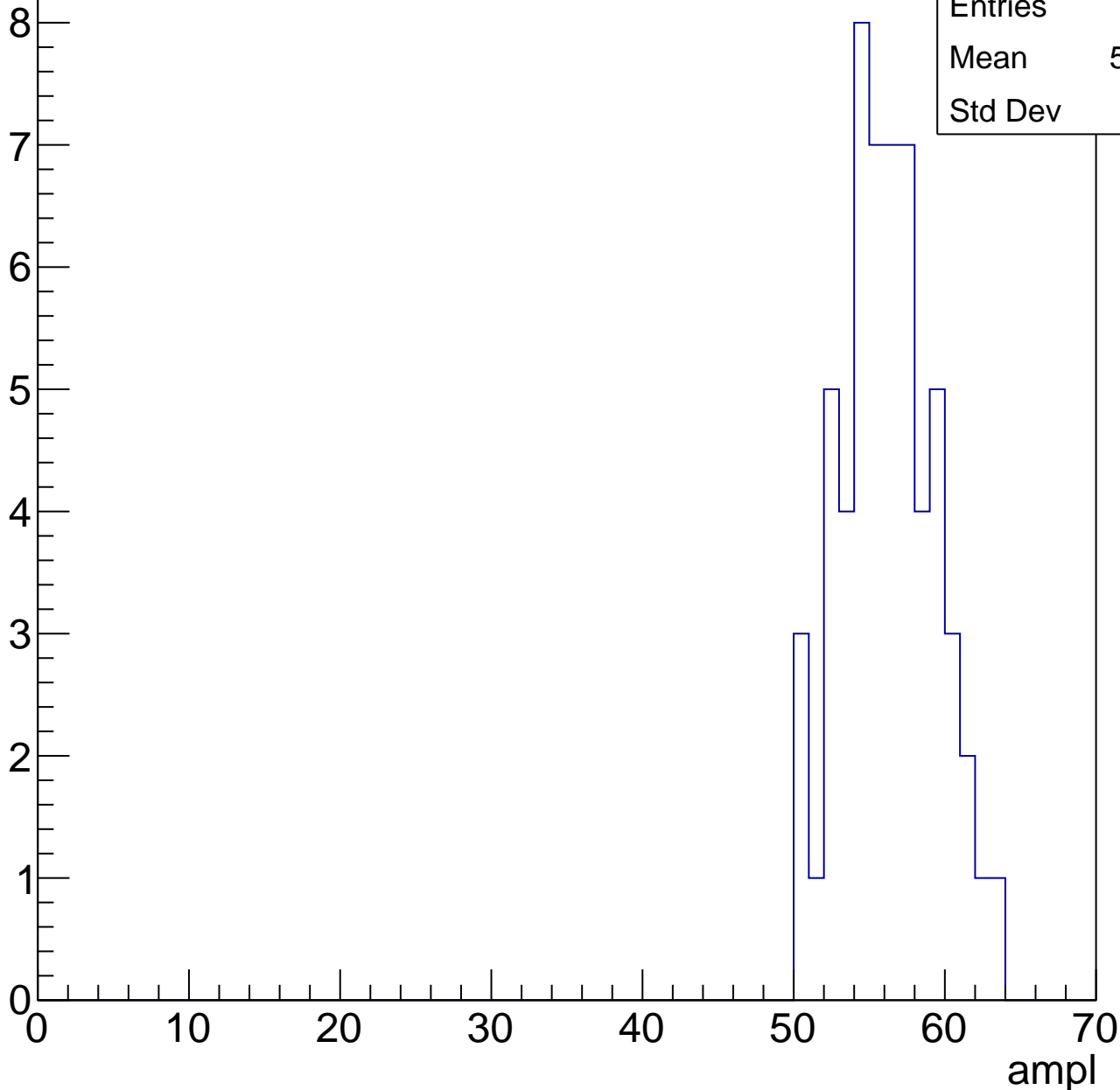


B0L000S, U2-ch22, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.78
Std Dev	3.04

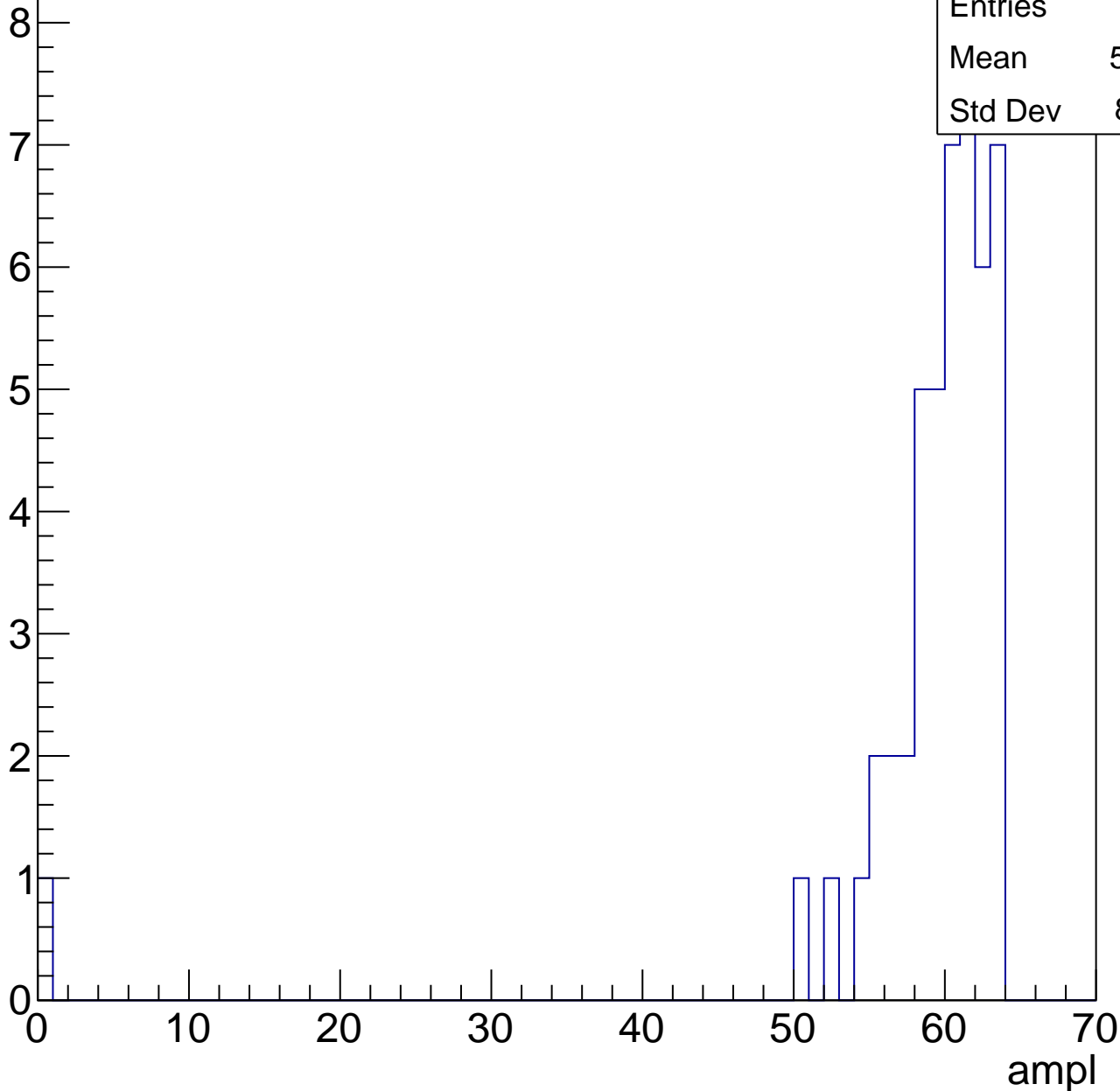


B0L000S, U2-ch22, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	58.29
Std Dev	8.991



B0L000S, U2-ch22, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch22, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch23, adc0

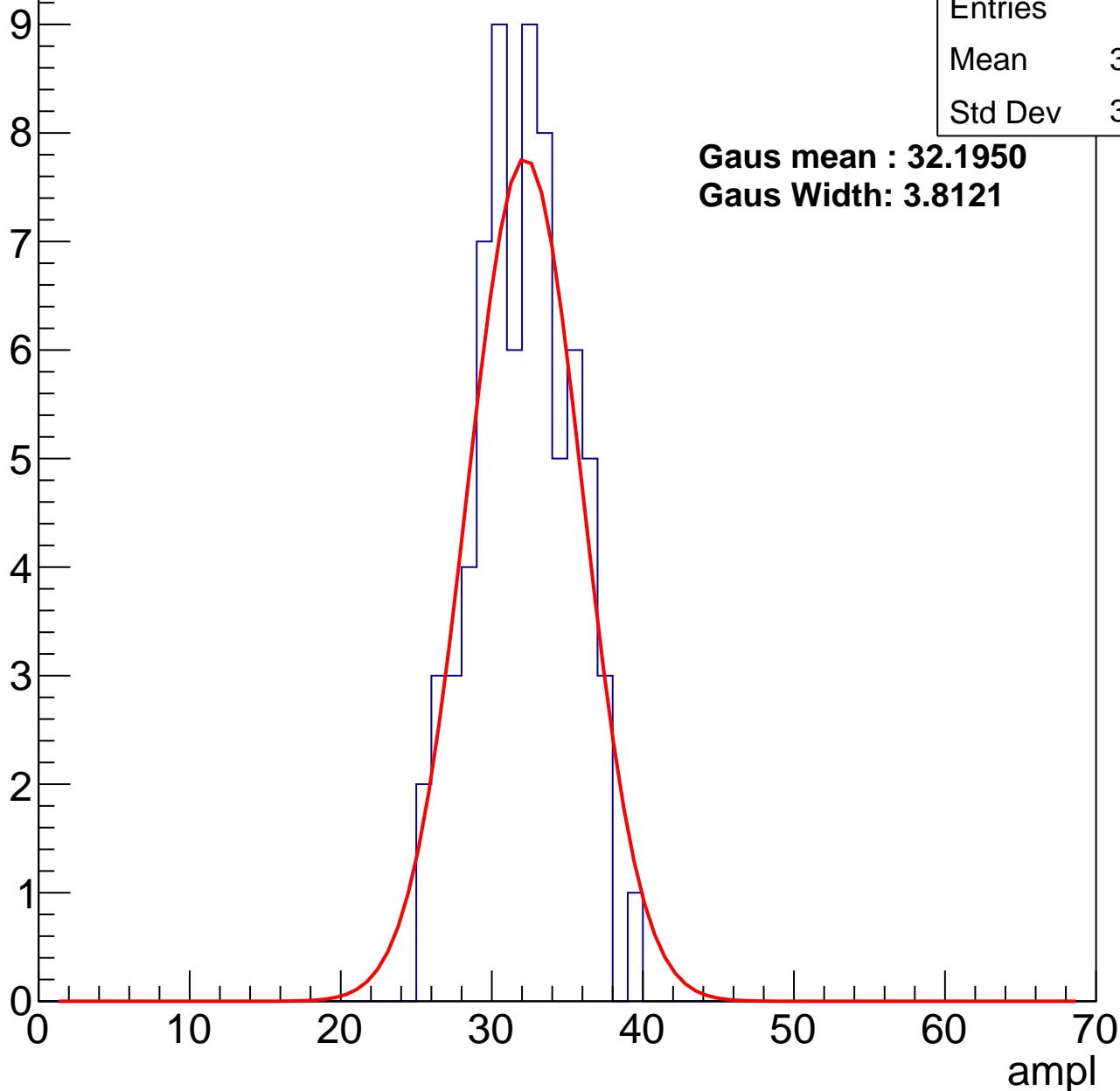
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	31.58
Std Dev	3.192

Gaus mean : 32.1950

Gaus Width: 3.8121



B0L000S, U2-ch23, adc1

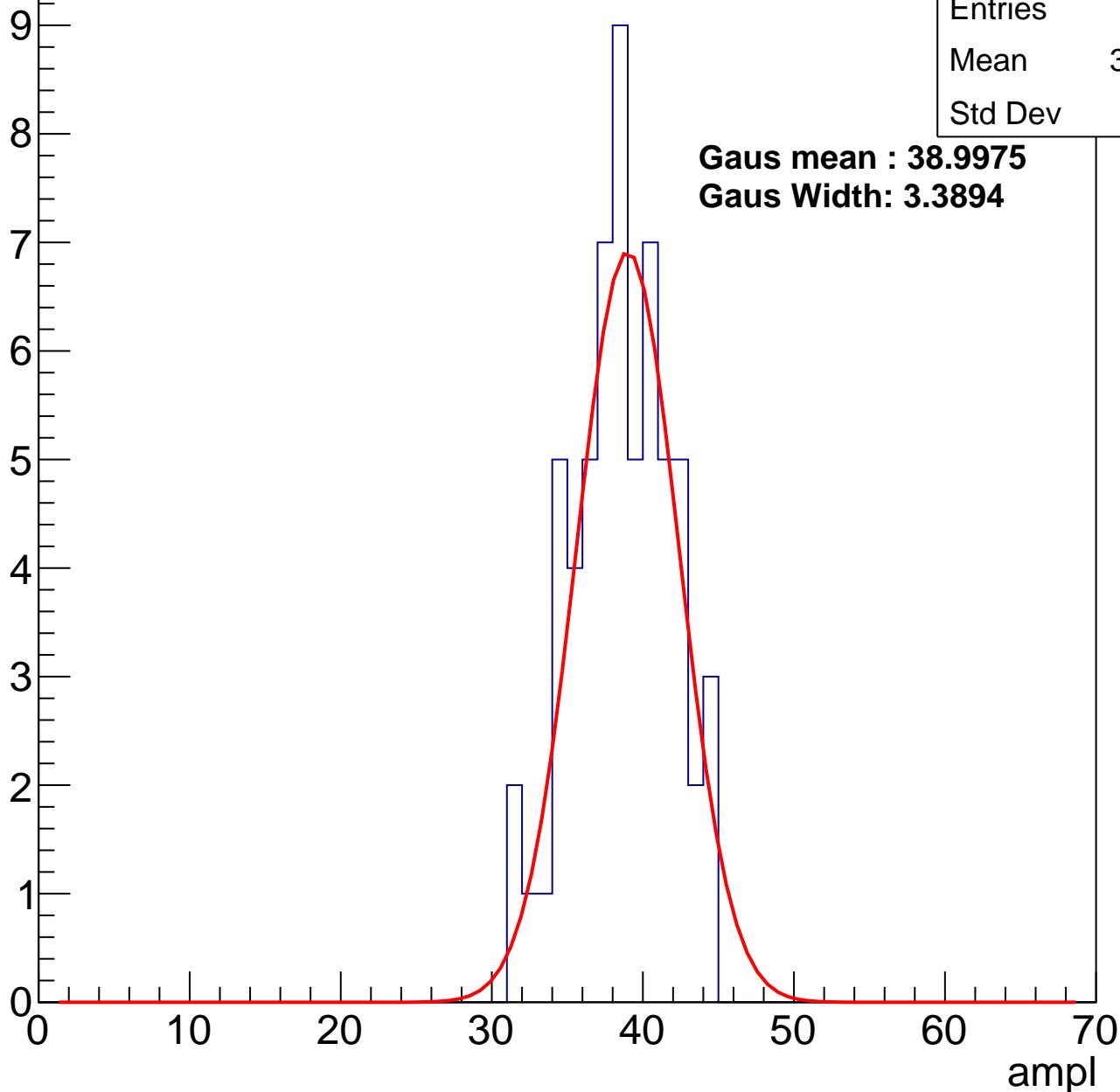
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	38.13
Std Dev	3.18

Gaus mean : 38.9975

Gaus Width: 3.3894



B0L000S, U2-ch23, adc2

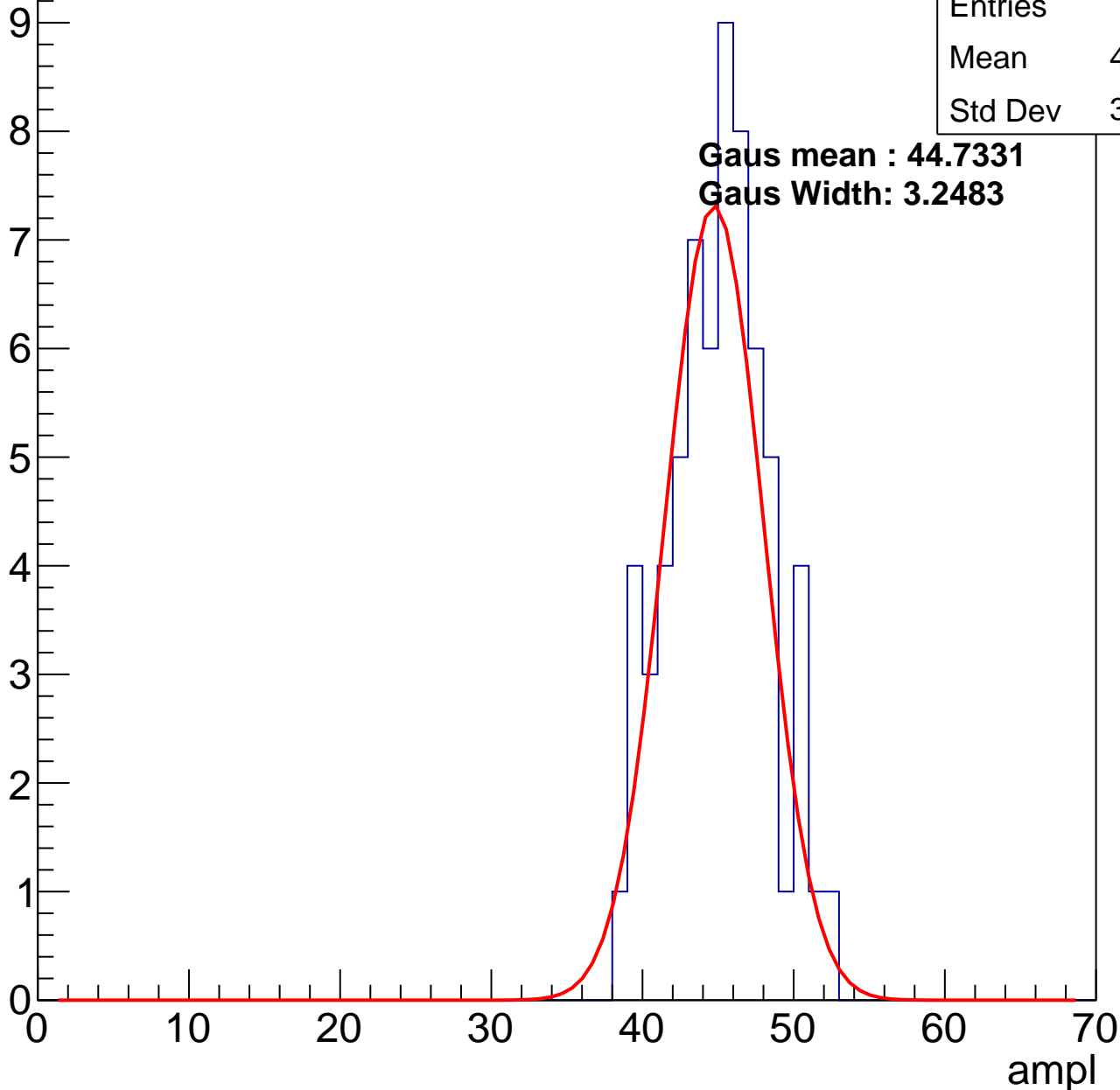
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.62
Std Dev	3.233

Gaus mean : 44.7331

Gaus Width: 3.2483

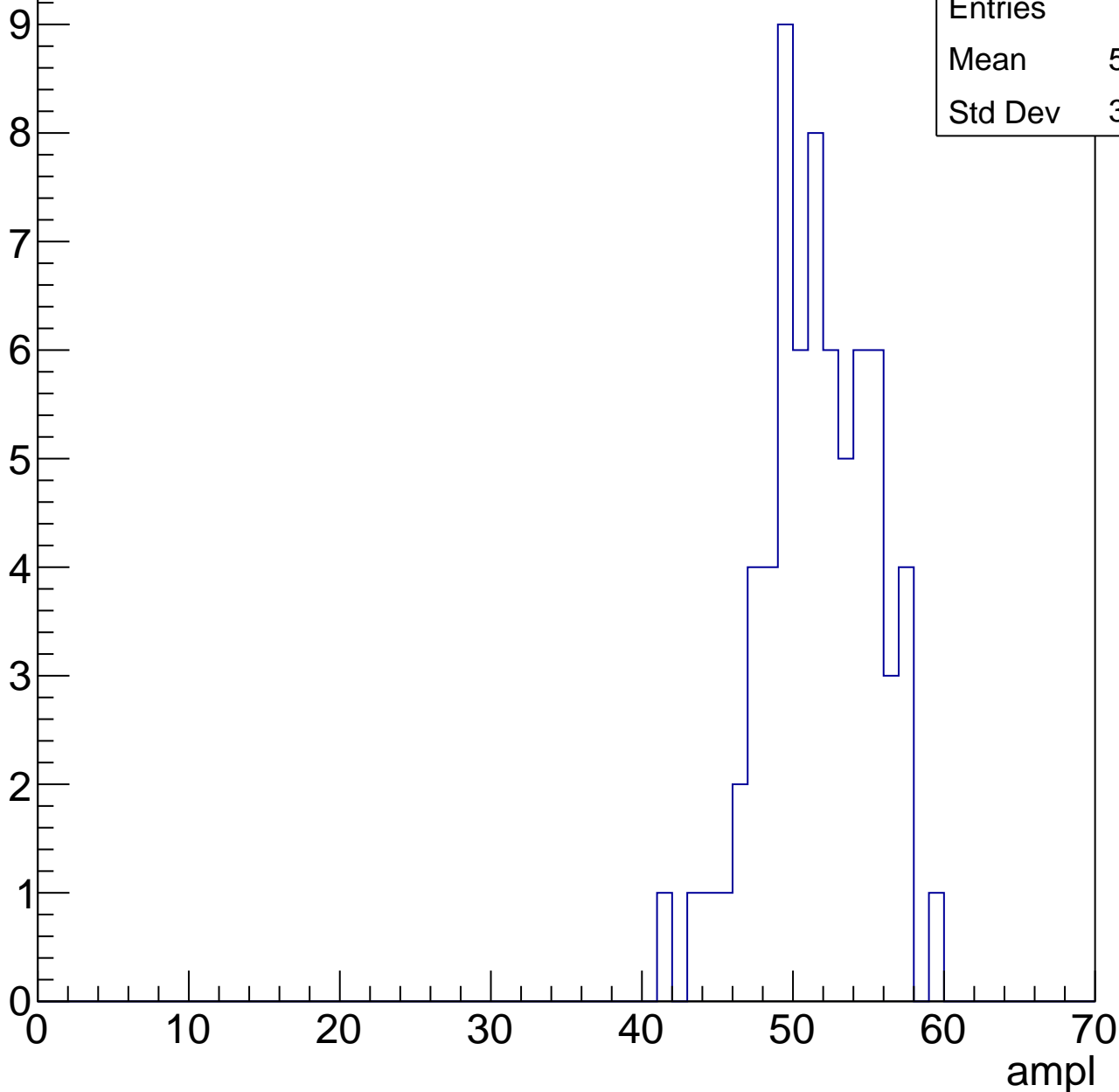


B0L000S, U2-ch23, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	51.18
Std Dev	3.634

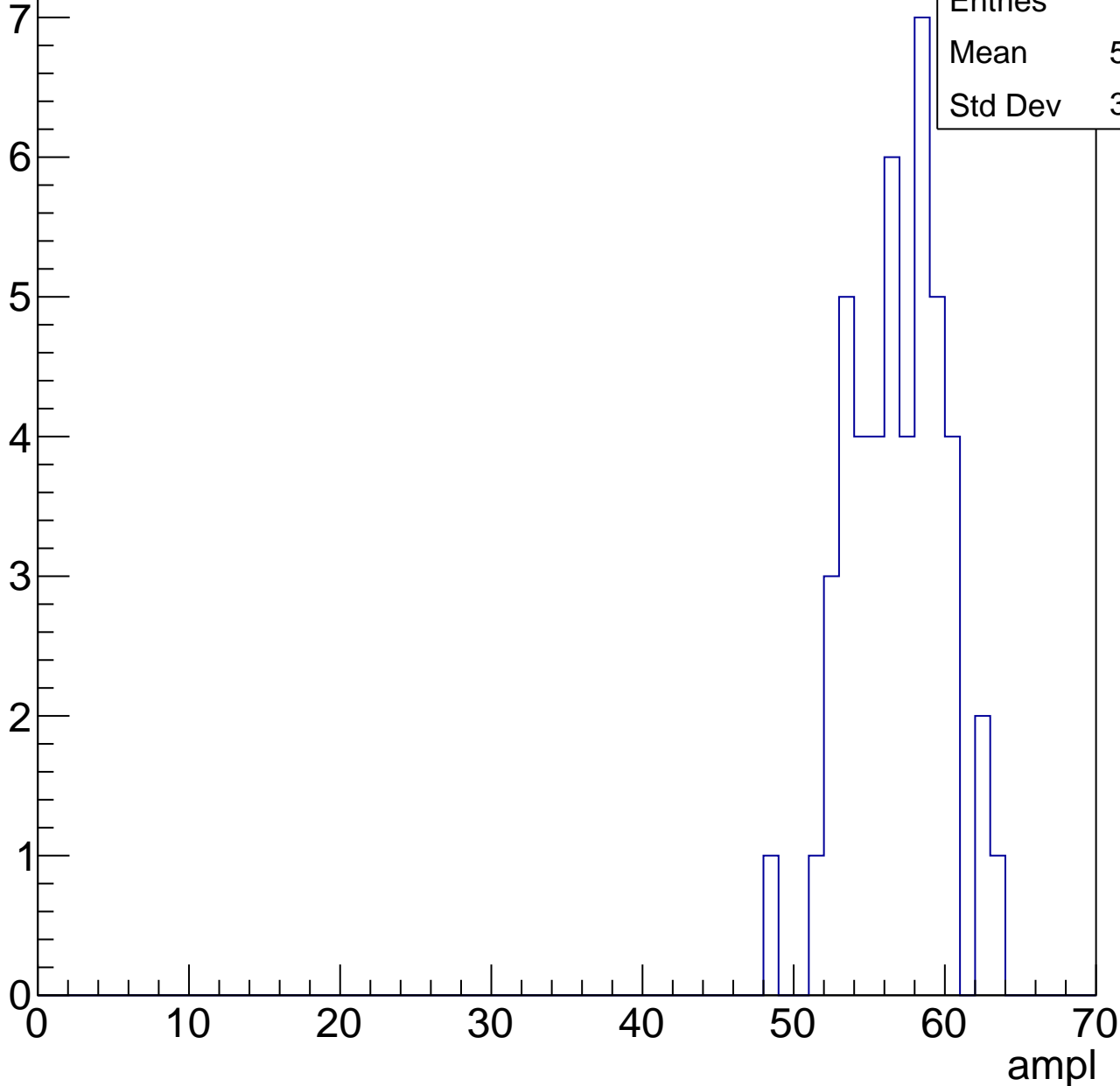


B0L000S, U2-ch23, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	56.34
Std Dev	3.124

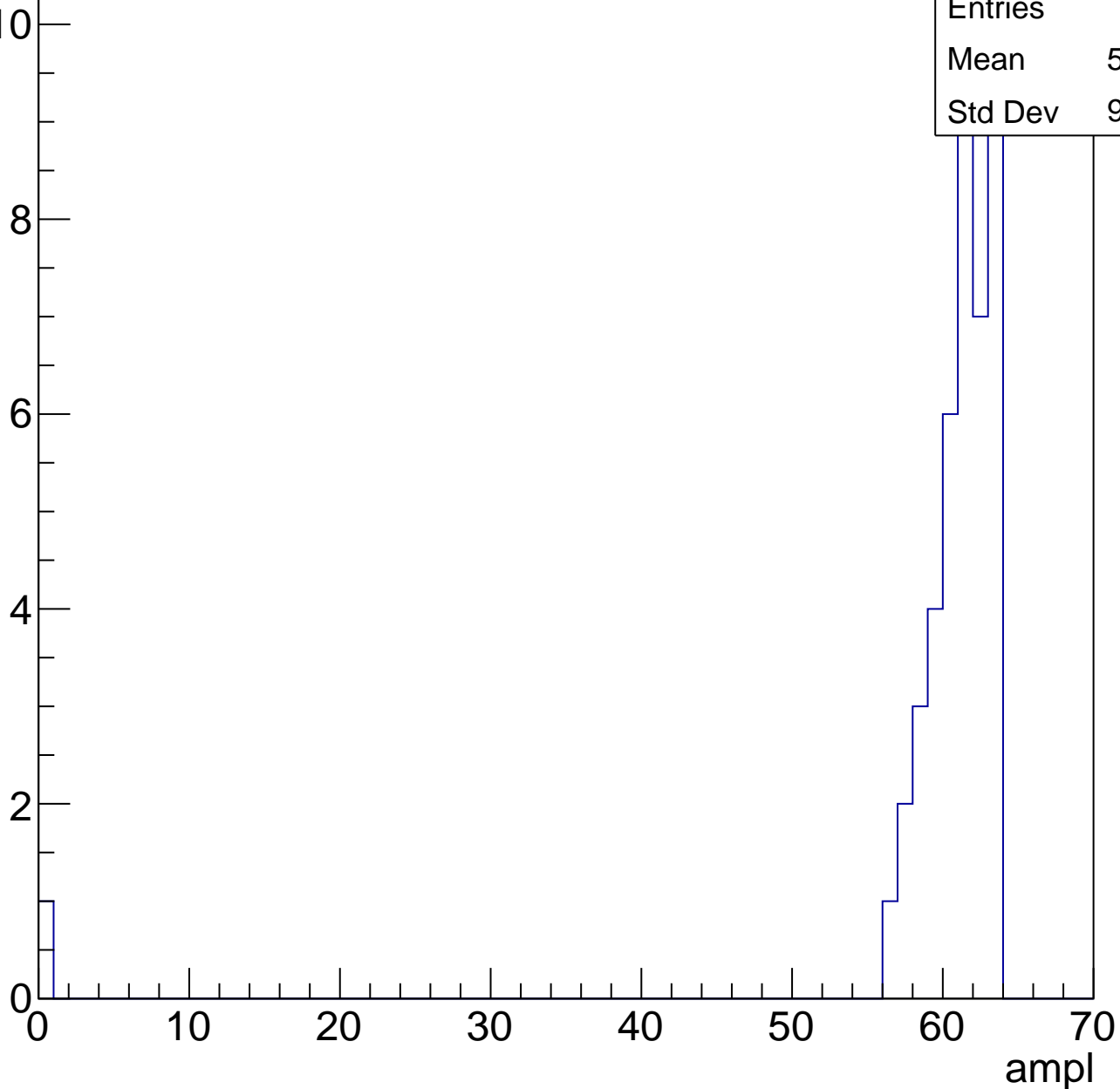


B0L000S, U2-ch23, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

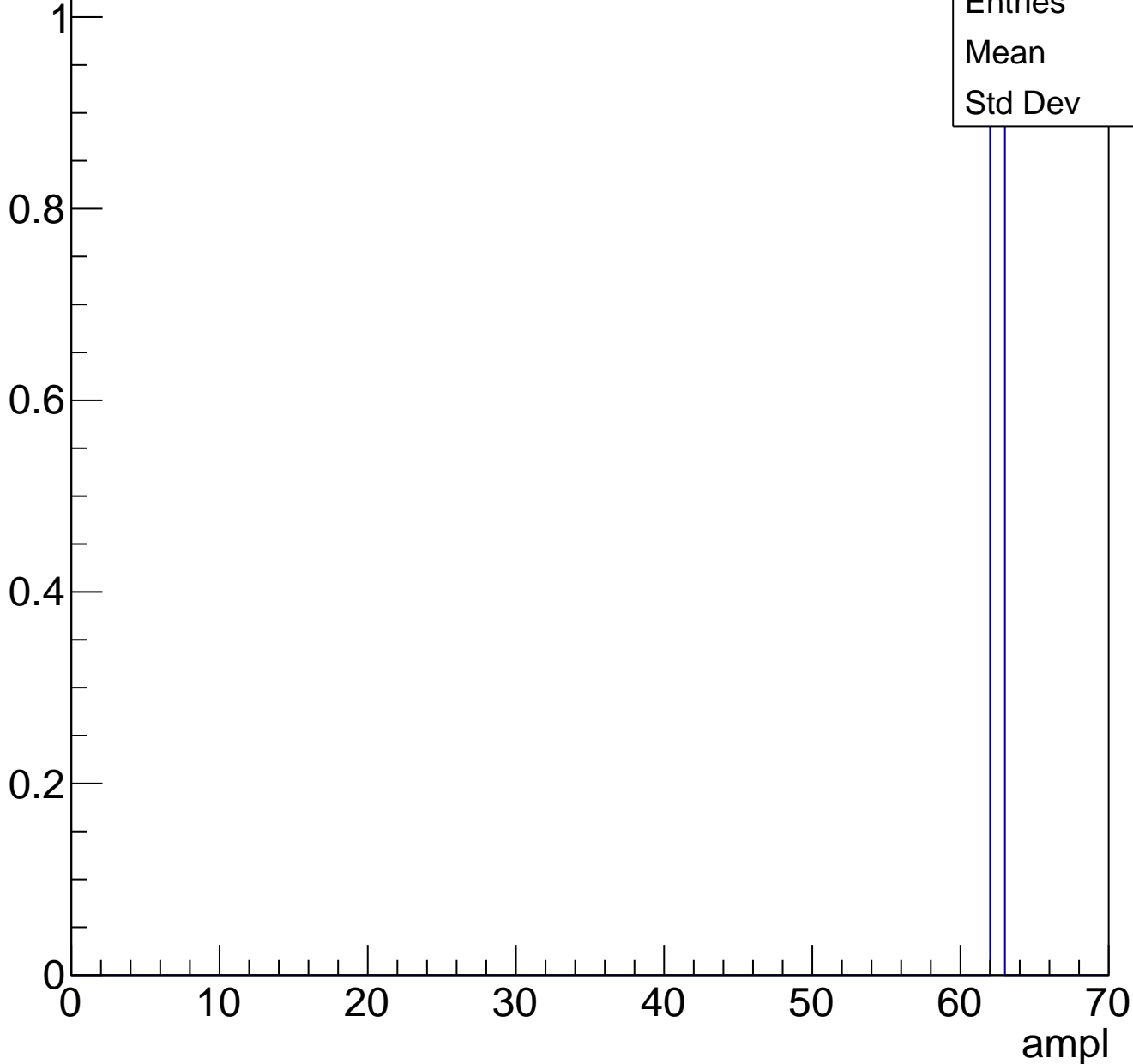
Entries	43
Mean	59.33
Std Dev	9.338



B0L000S, U2-ch23, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch23, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch24, adc0

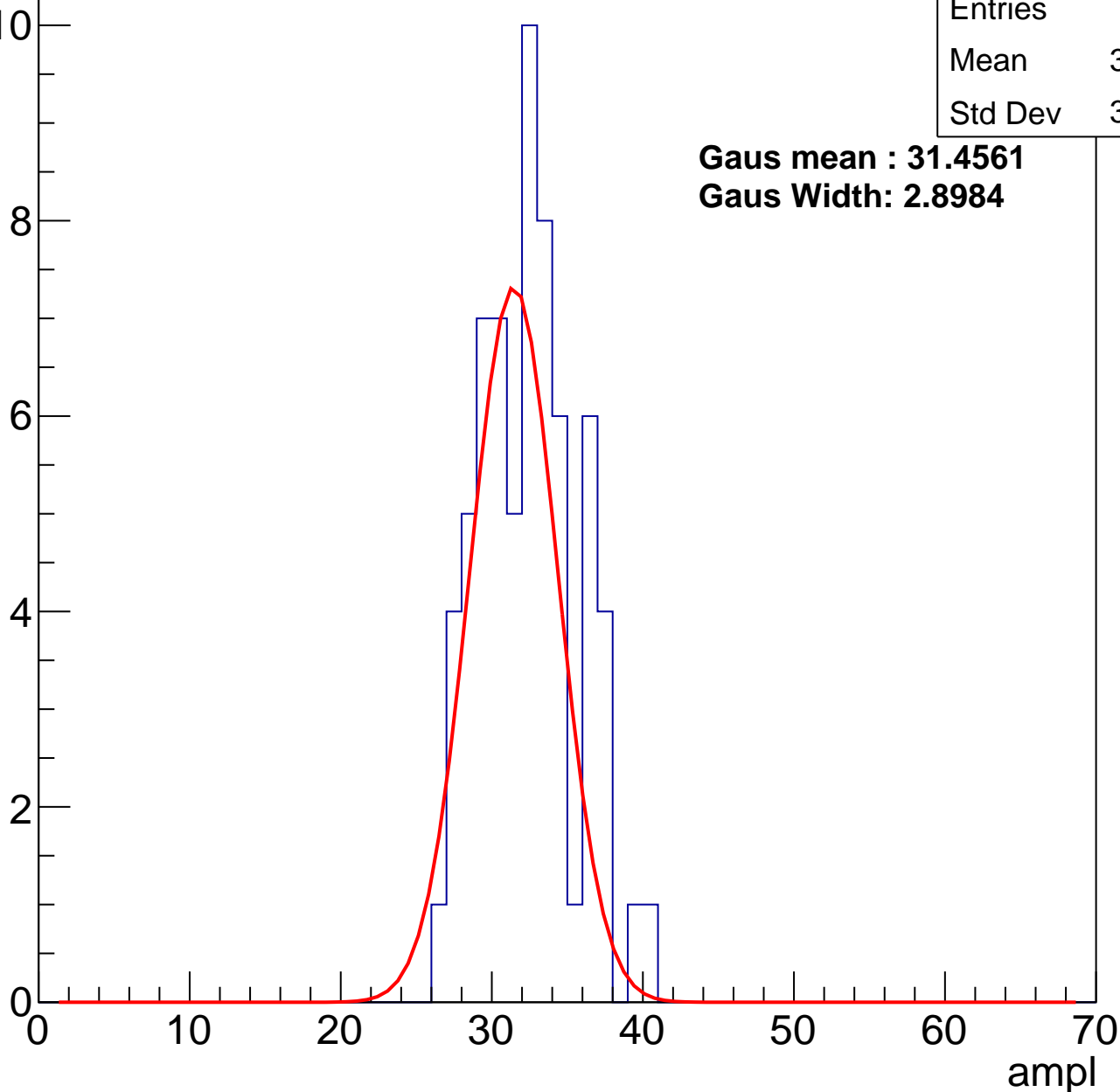
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	31.94
Std Dev	3.166

Gaus mean : 31.4561

Gaus Width: 2.8984



B0L000S, U2-ch24, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	82
Mean	38.85
Std Dev	3.606

Gaus mean : 39.3929

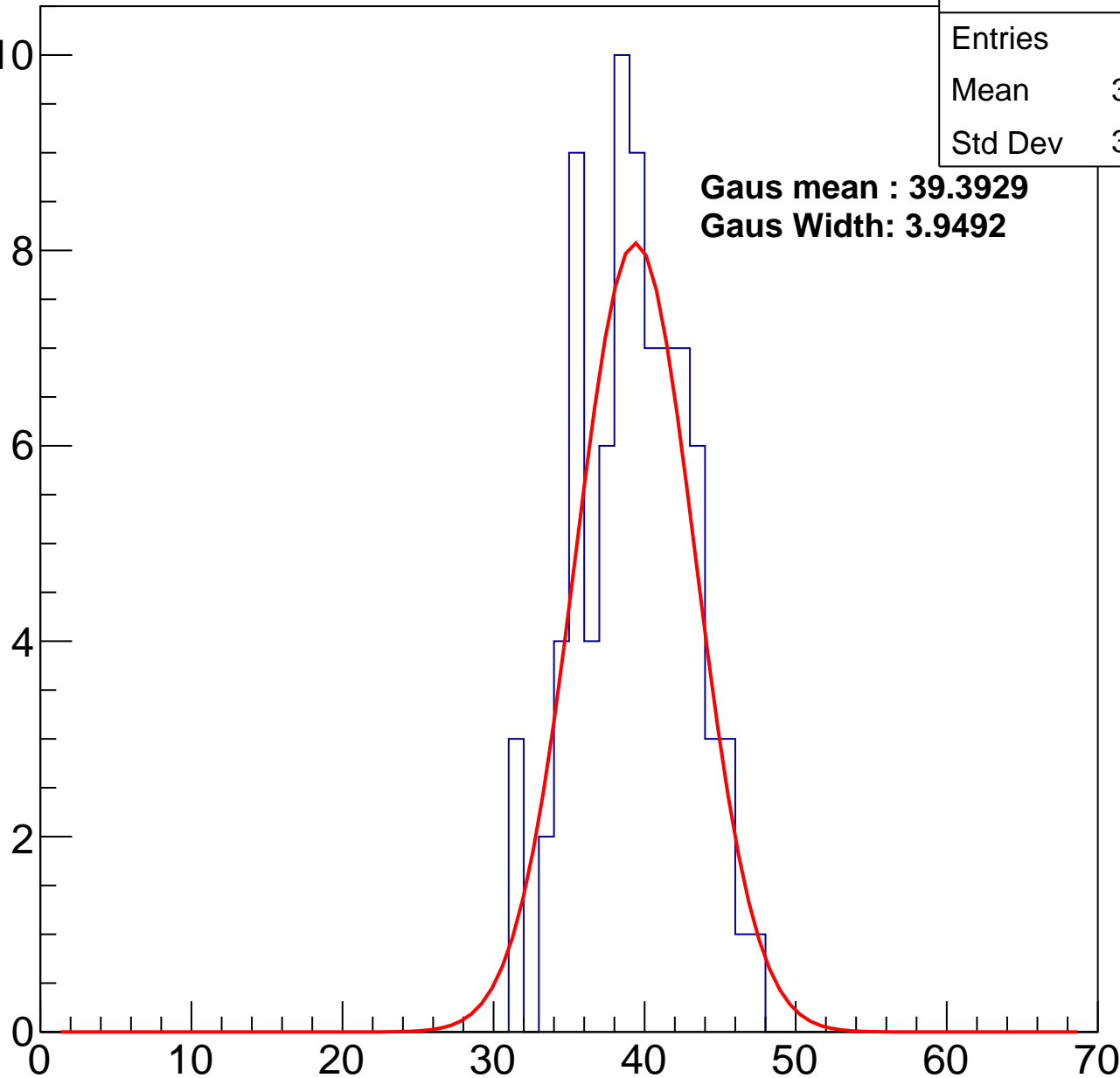
Gaus Width: 3.9492

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch24, adc2

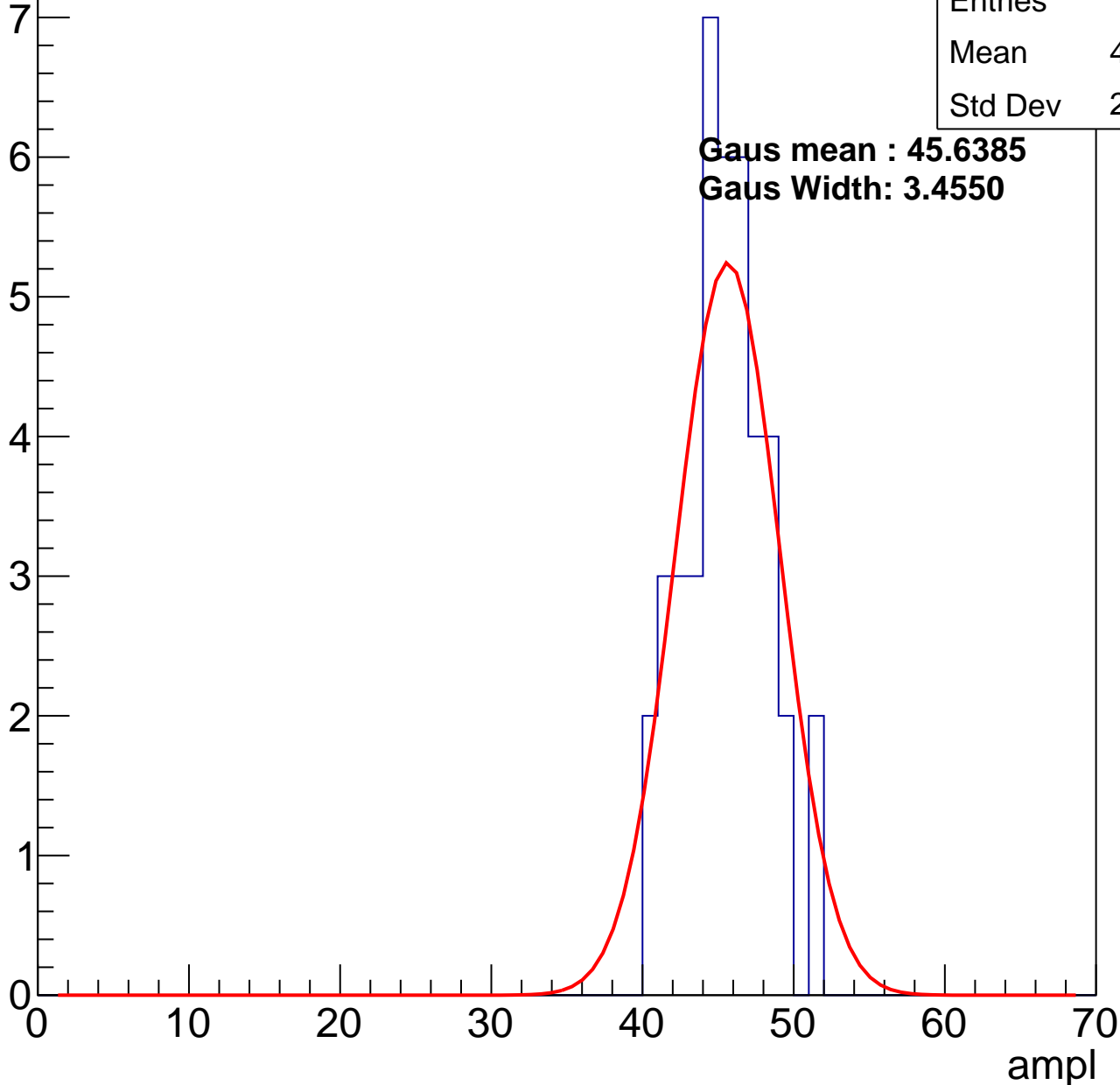
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	42
Mean	45.05
Std Dev	2.699

Gaus mean : 45.6385

Gaus Width: 3.4550

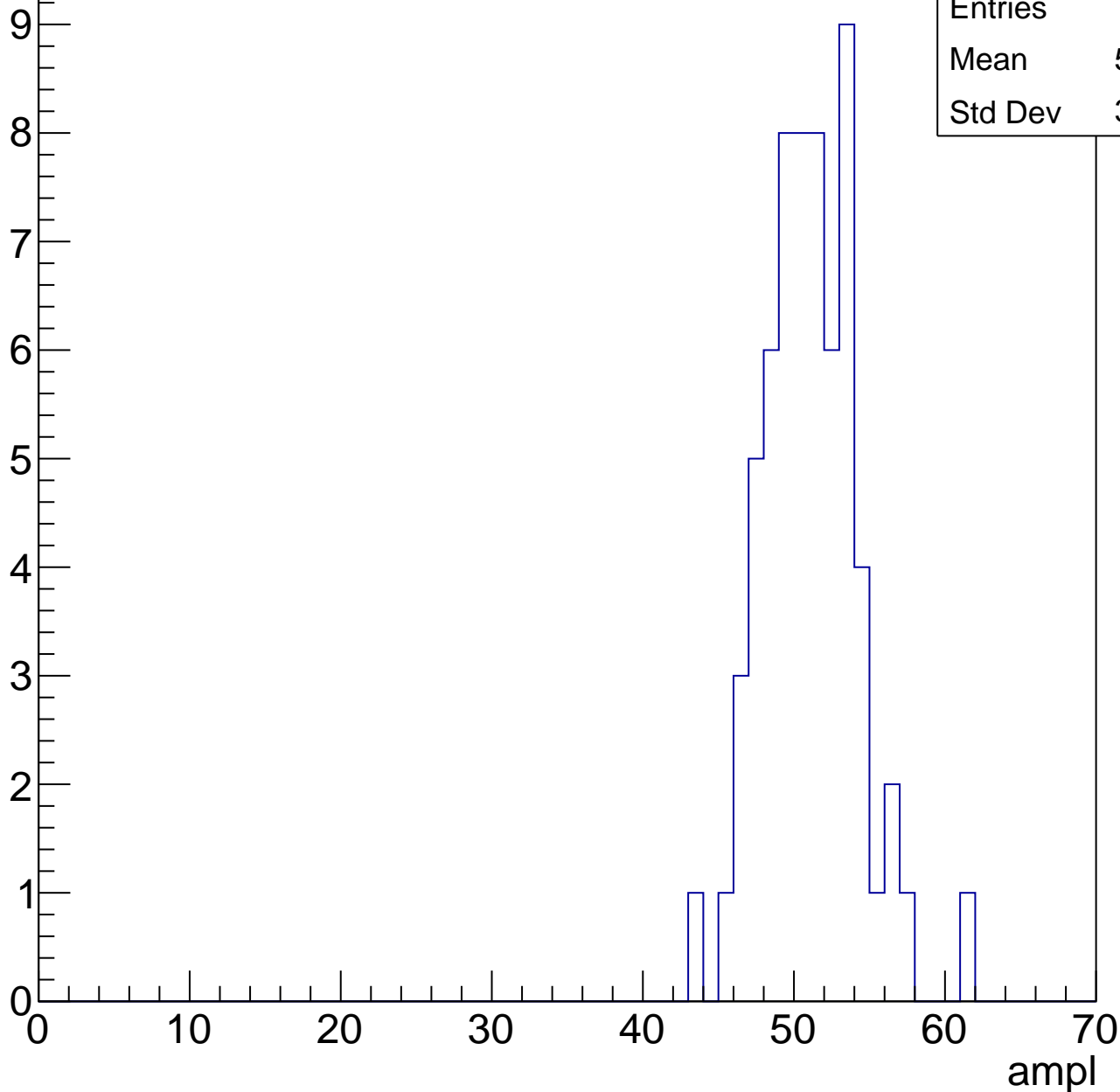


B0L000S, U2-ch24, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	50.61
Std Dev	3.111

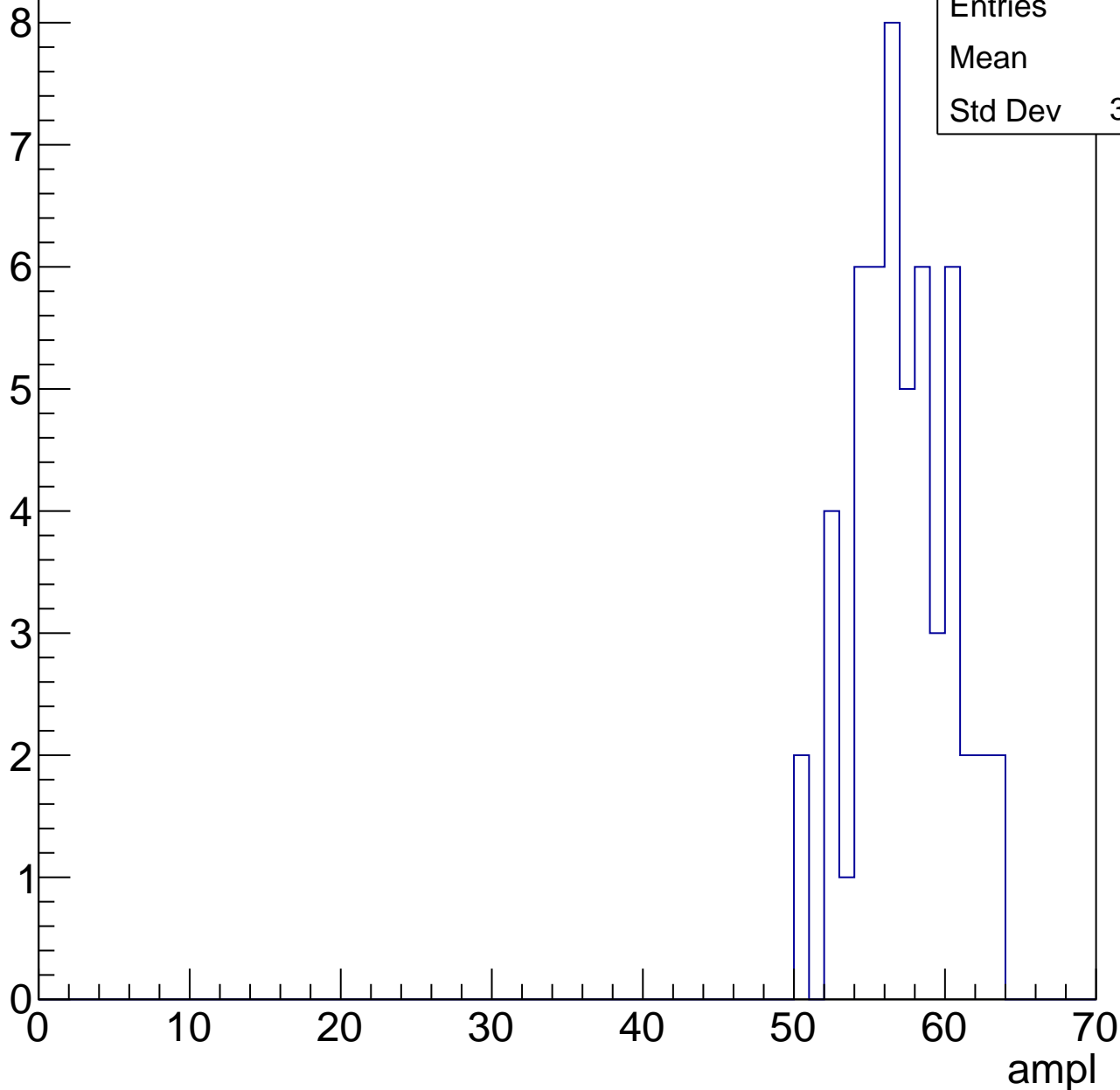


B0L000S, U2-ch24, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.7
Std Dev	3.136



B0L000S, U2-ch24, adc5

calib_packv5_042523_0143.root, FC#5, port B1

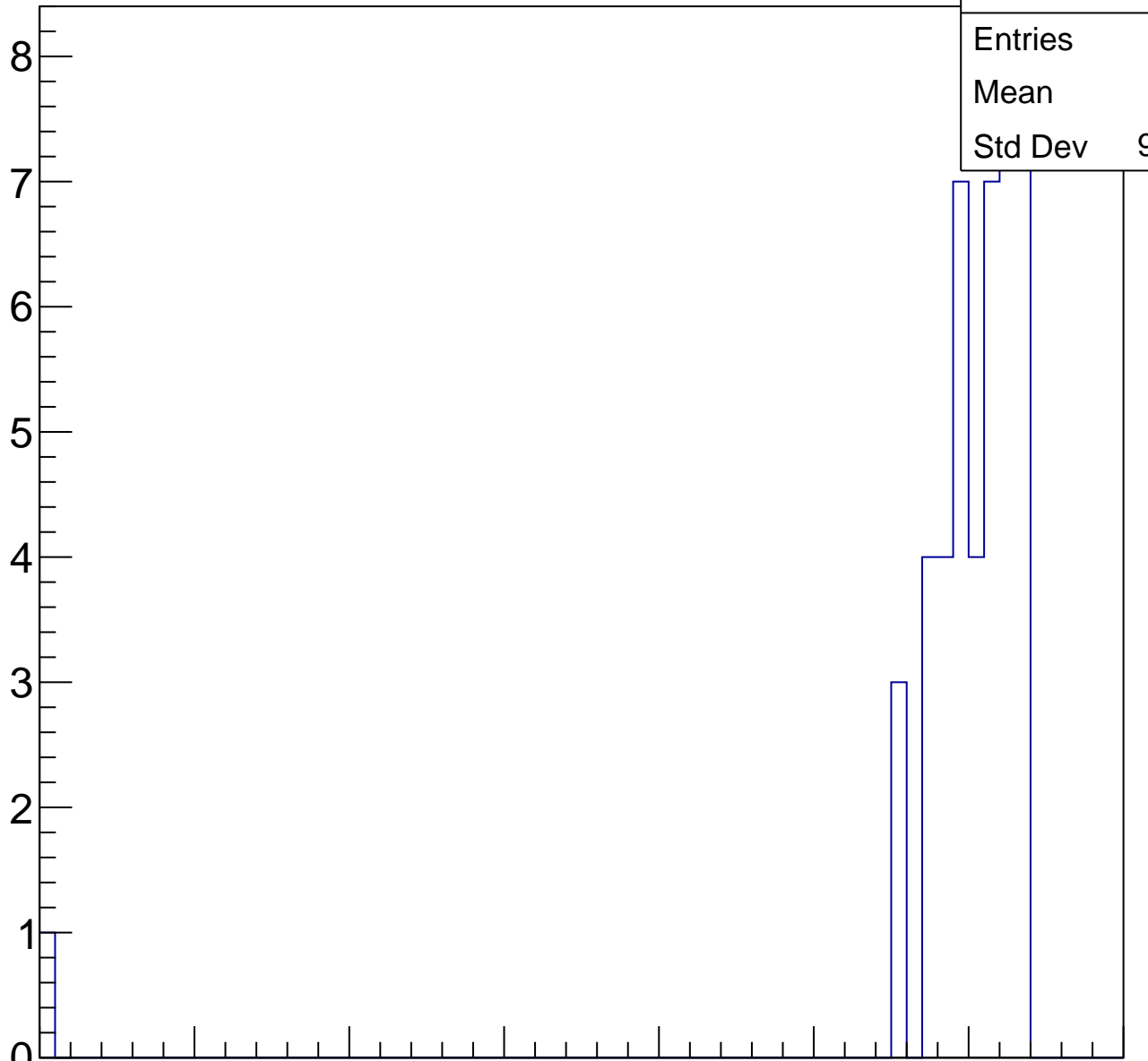
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.8
Std Dev	9.064

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch24, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

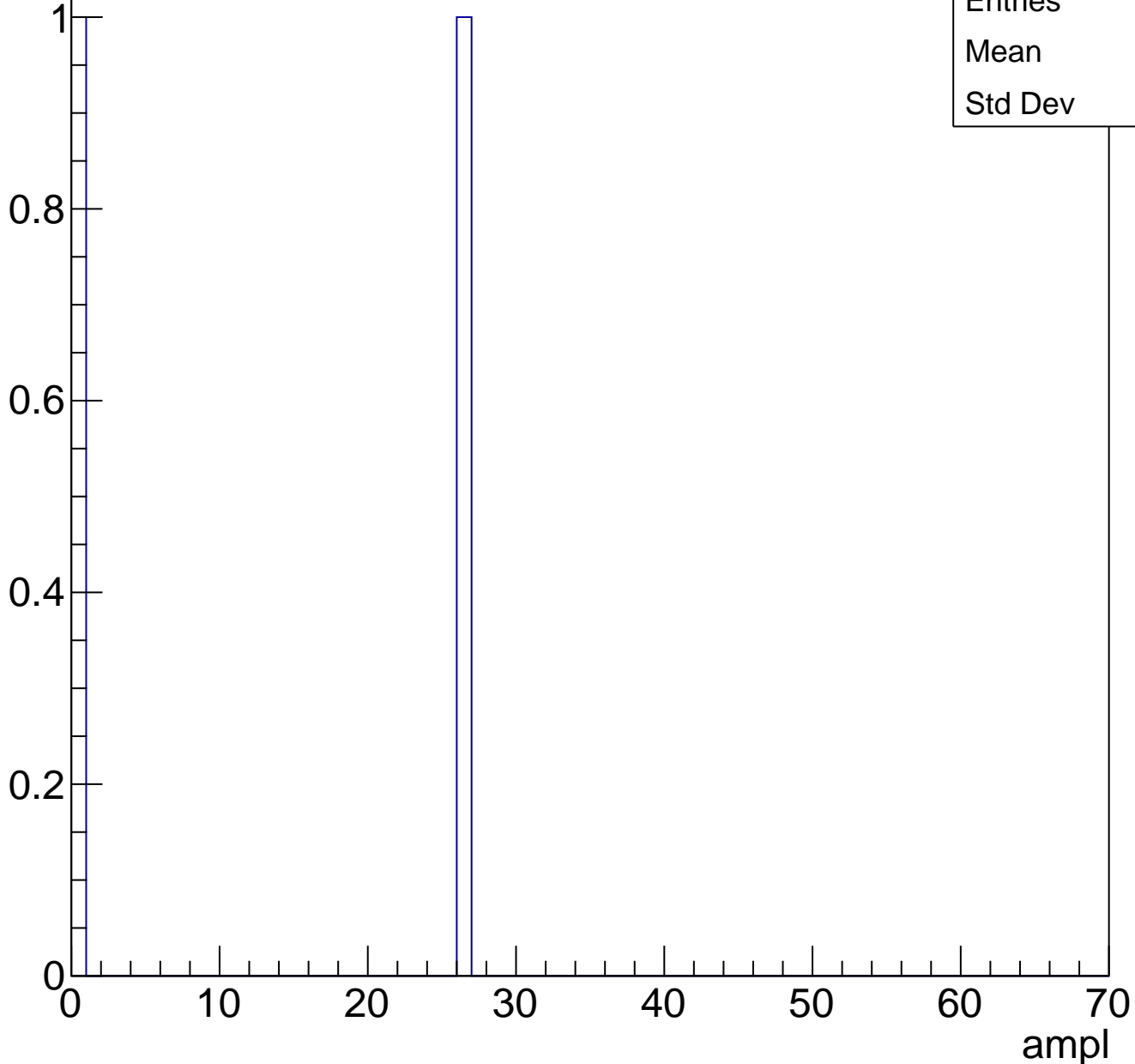


Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch24, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	13
Std Dev	13

B0L000S, U2-ch25, adc0

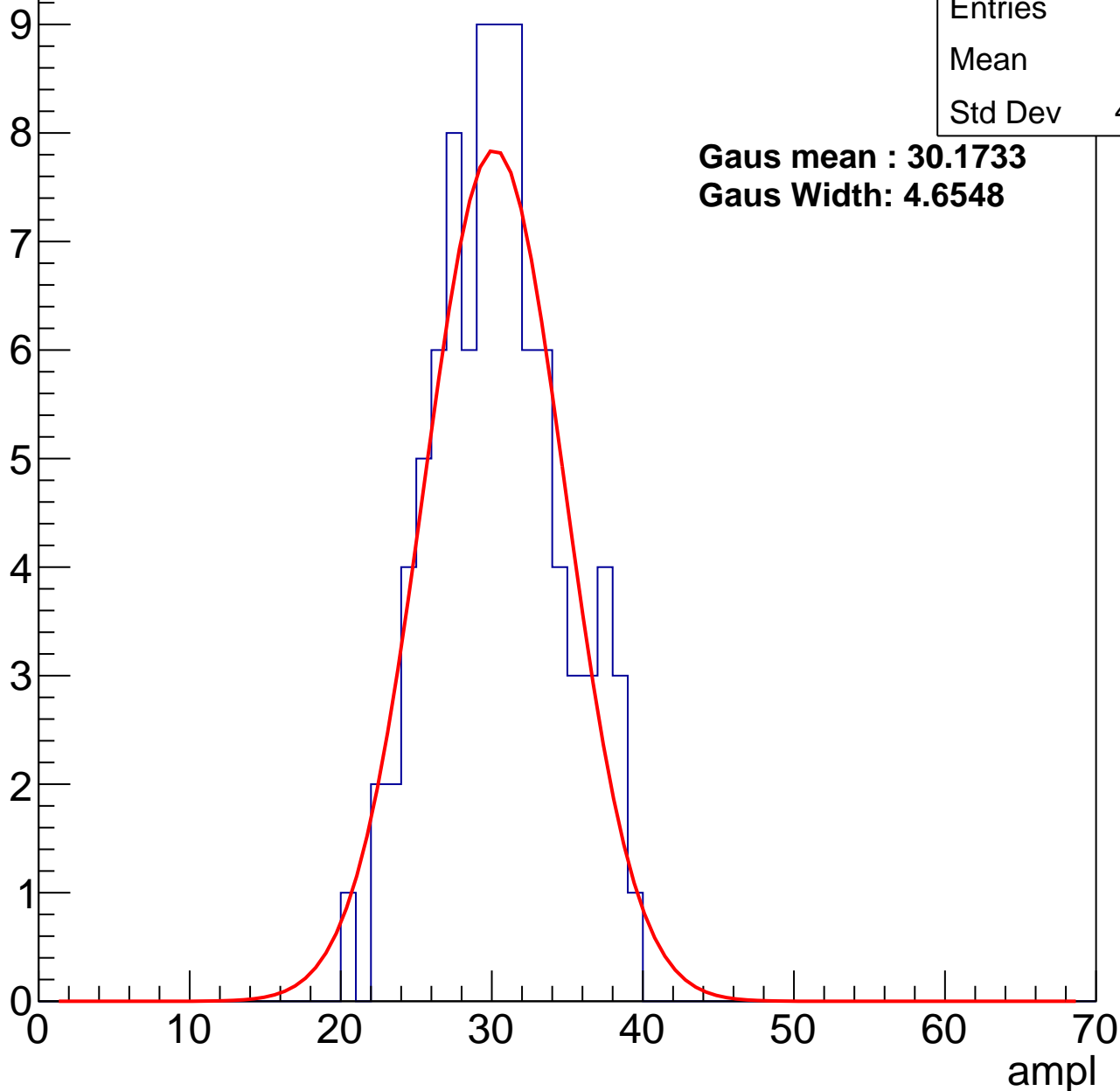
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	91
Mean	29.9
Std Dev	4.191

Gaus mean : 30.1733

Gaus Width: 4.6548



B0L000S, U2-ch25, adc1

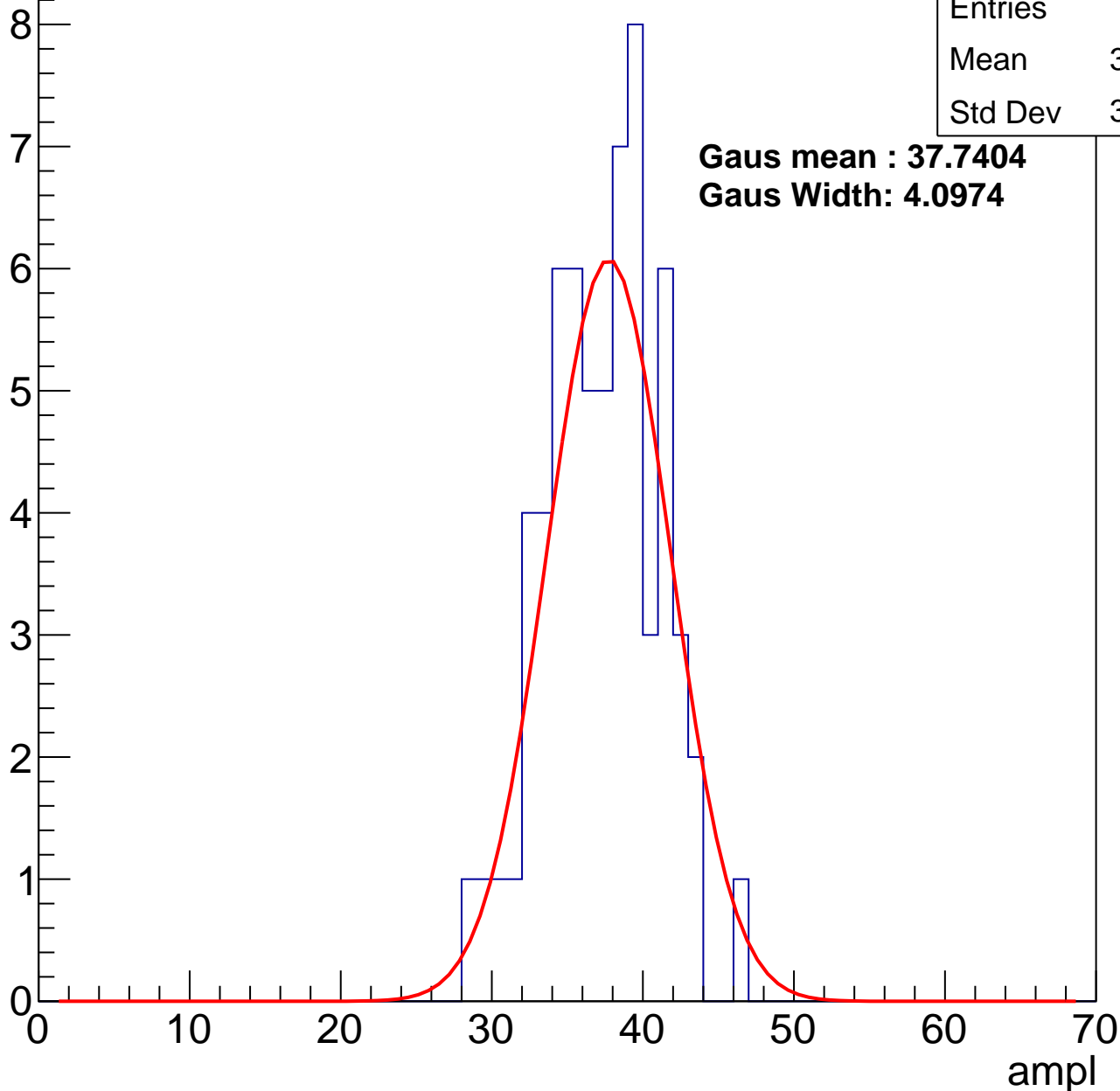
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.86
Std Dev	3.678

Gaus mean : 37.7404

Gaus Width: 4.0974



B0L000S, U2-ch25, adc2

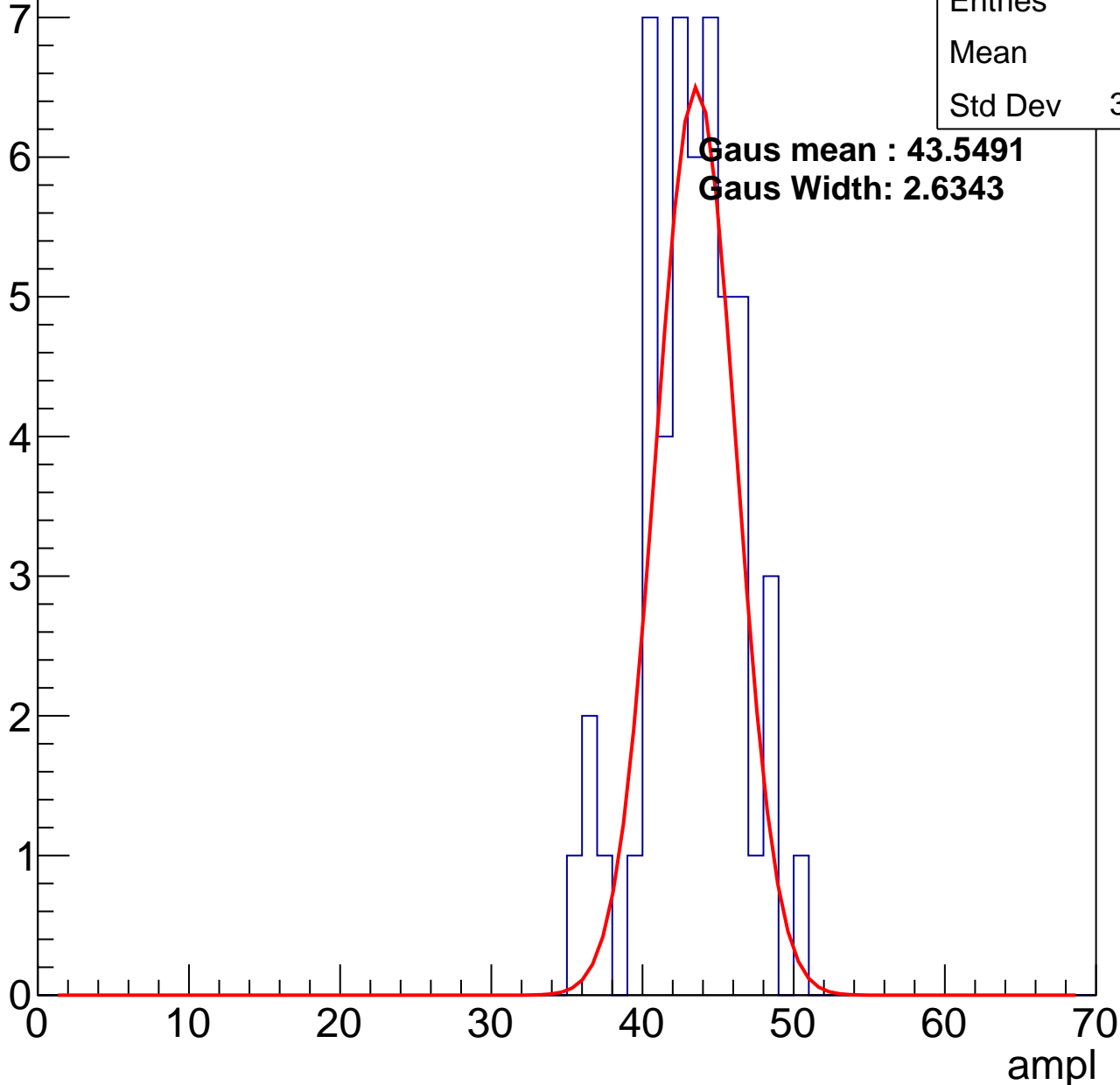
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	42.8
Std Dev	3.162

Gaus mean : 43.5491

Gaus Width: 2.6343

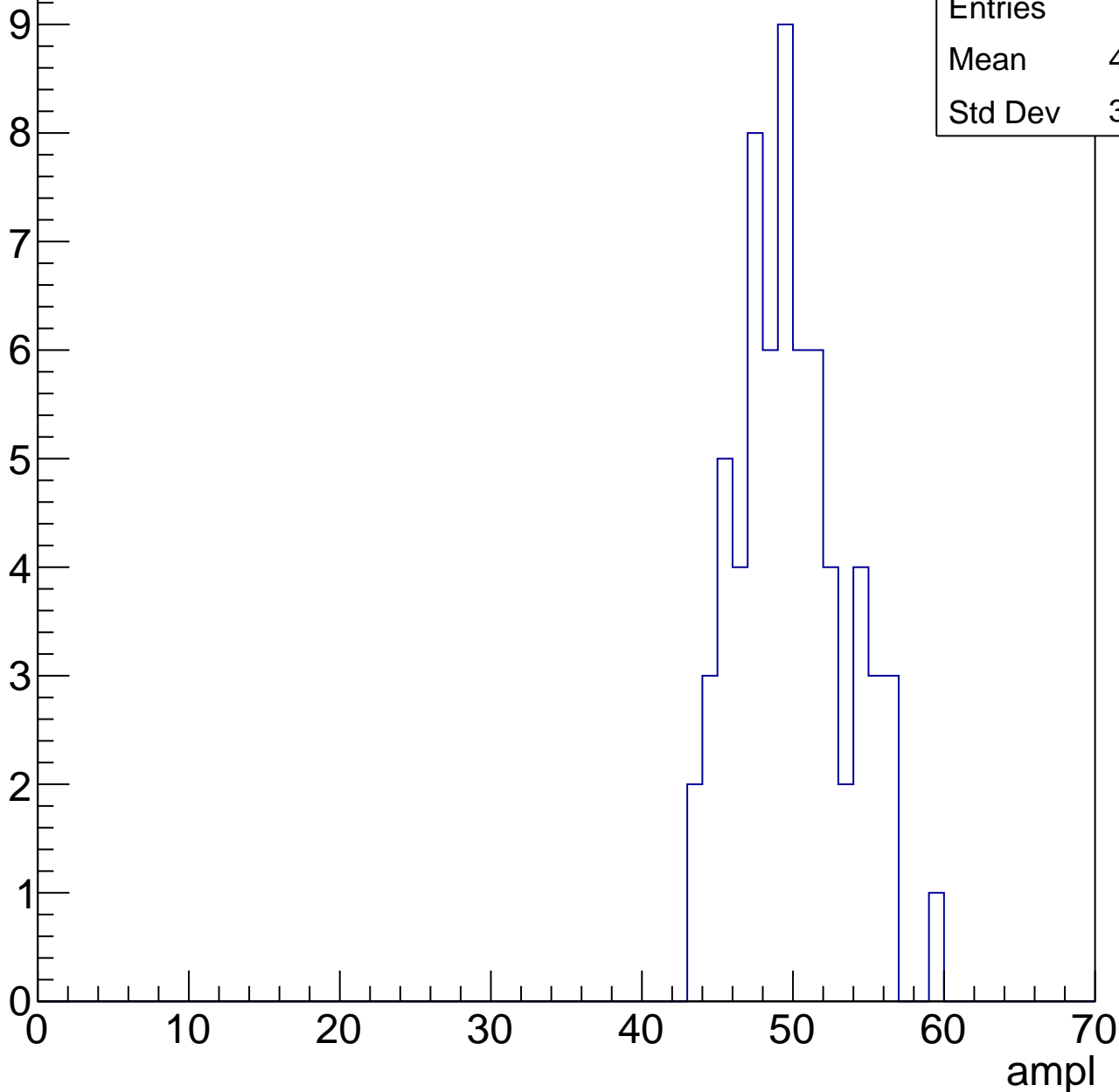


B0L000S, U2-ch25, adc3

calib_packv5_042523_0143.root, FC#5, port B1

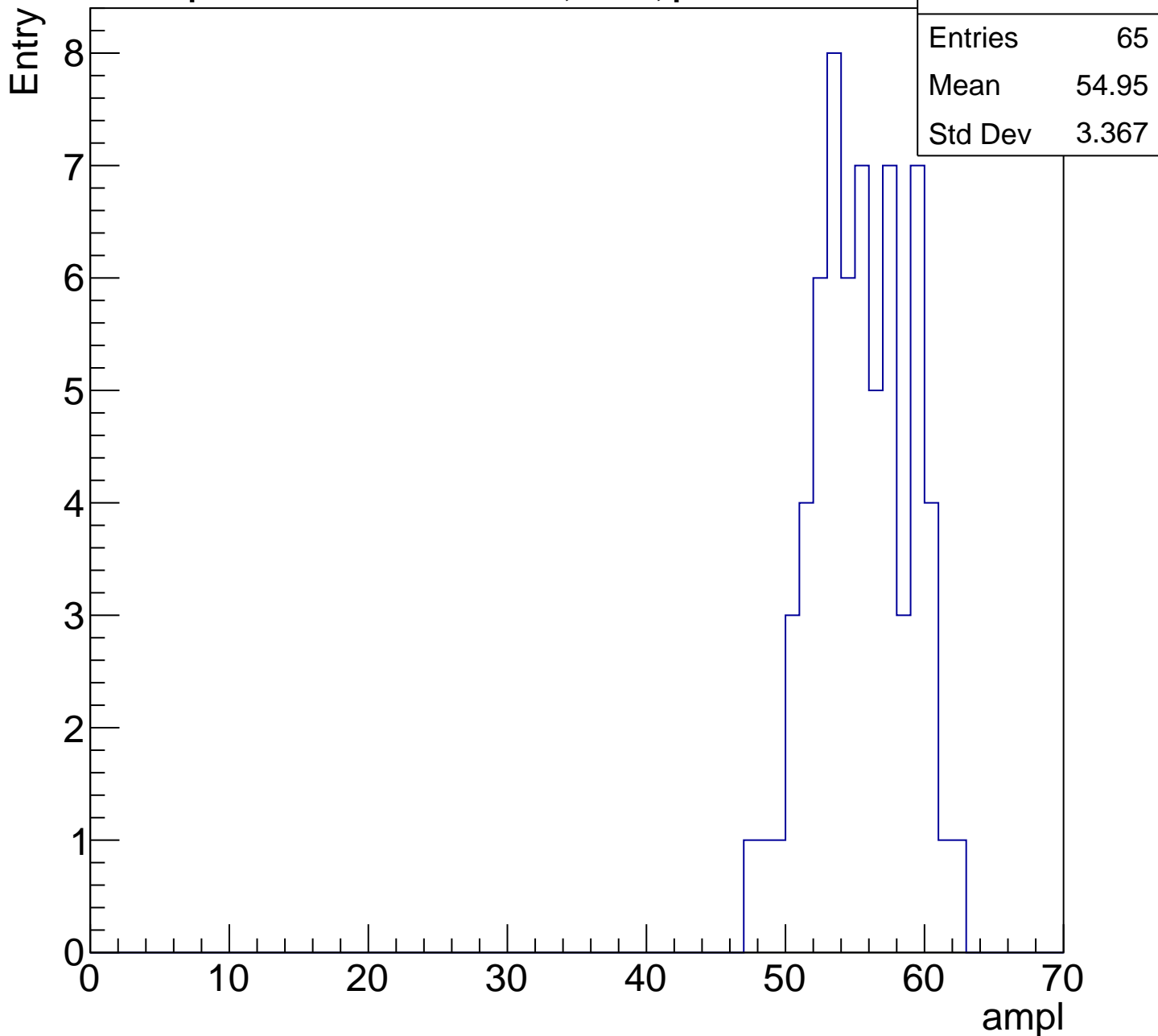
Entry

Entries	66
Mean	49.39
Std Dev	3.576



B0L000S, U2-ch25, adc4

calib_packv5_042523_0143.root, FC#5, port B1

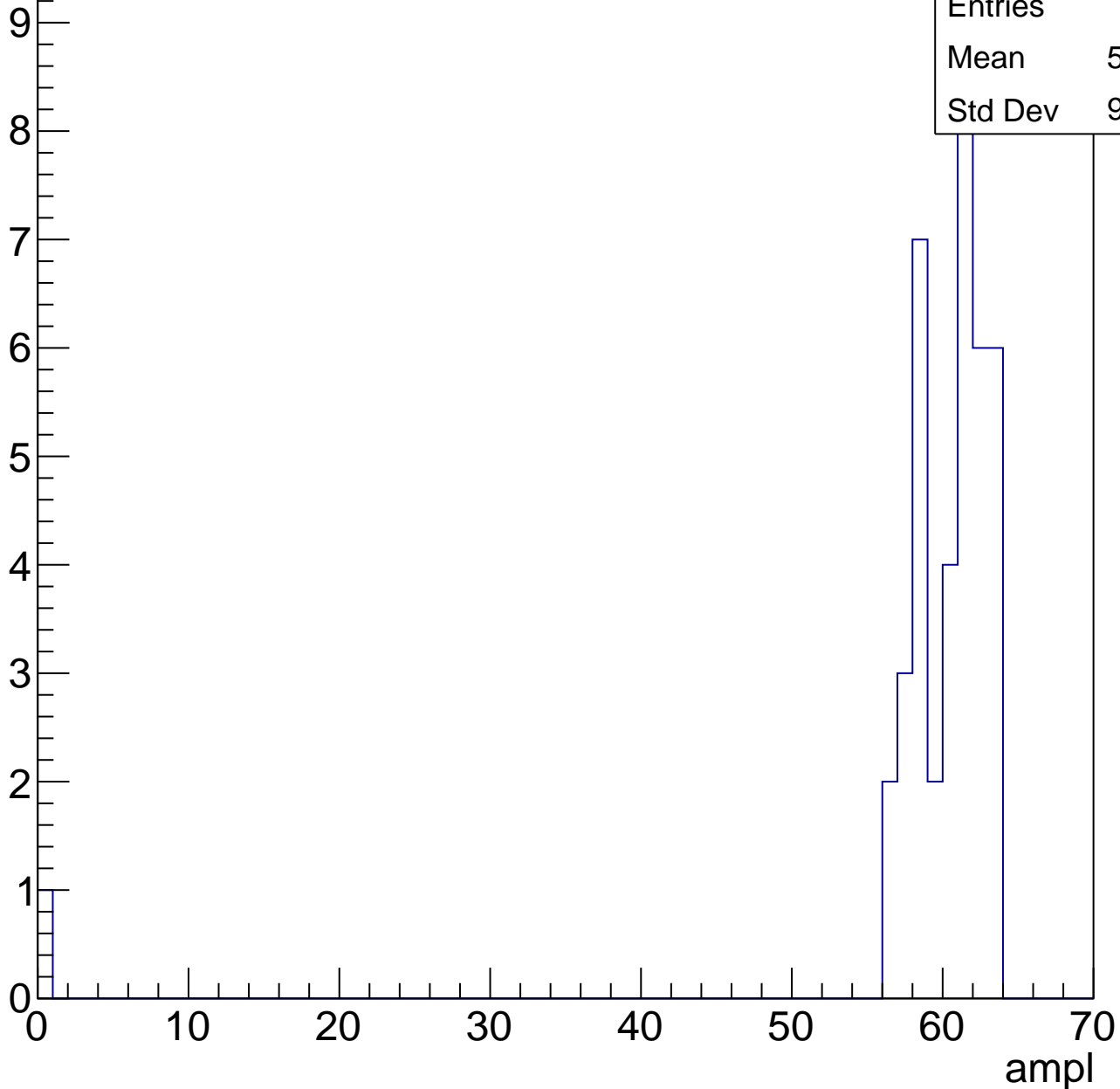


B0L000S, U2-ch25, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

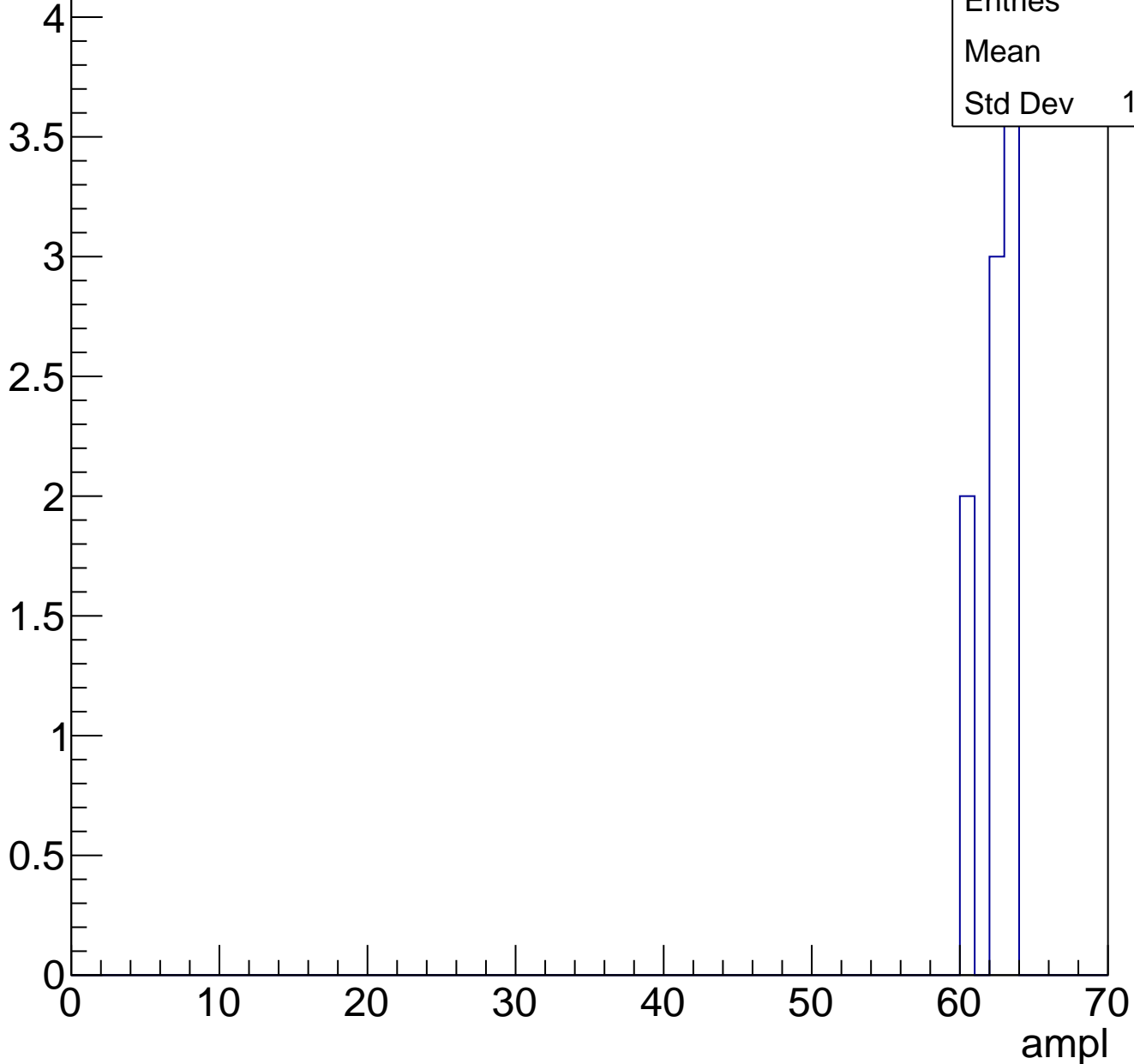
Entries	40
Mean	58.65
Std Dev	9.622



B0L000S, U2-ch25, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch25, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch26, adc0

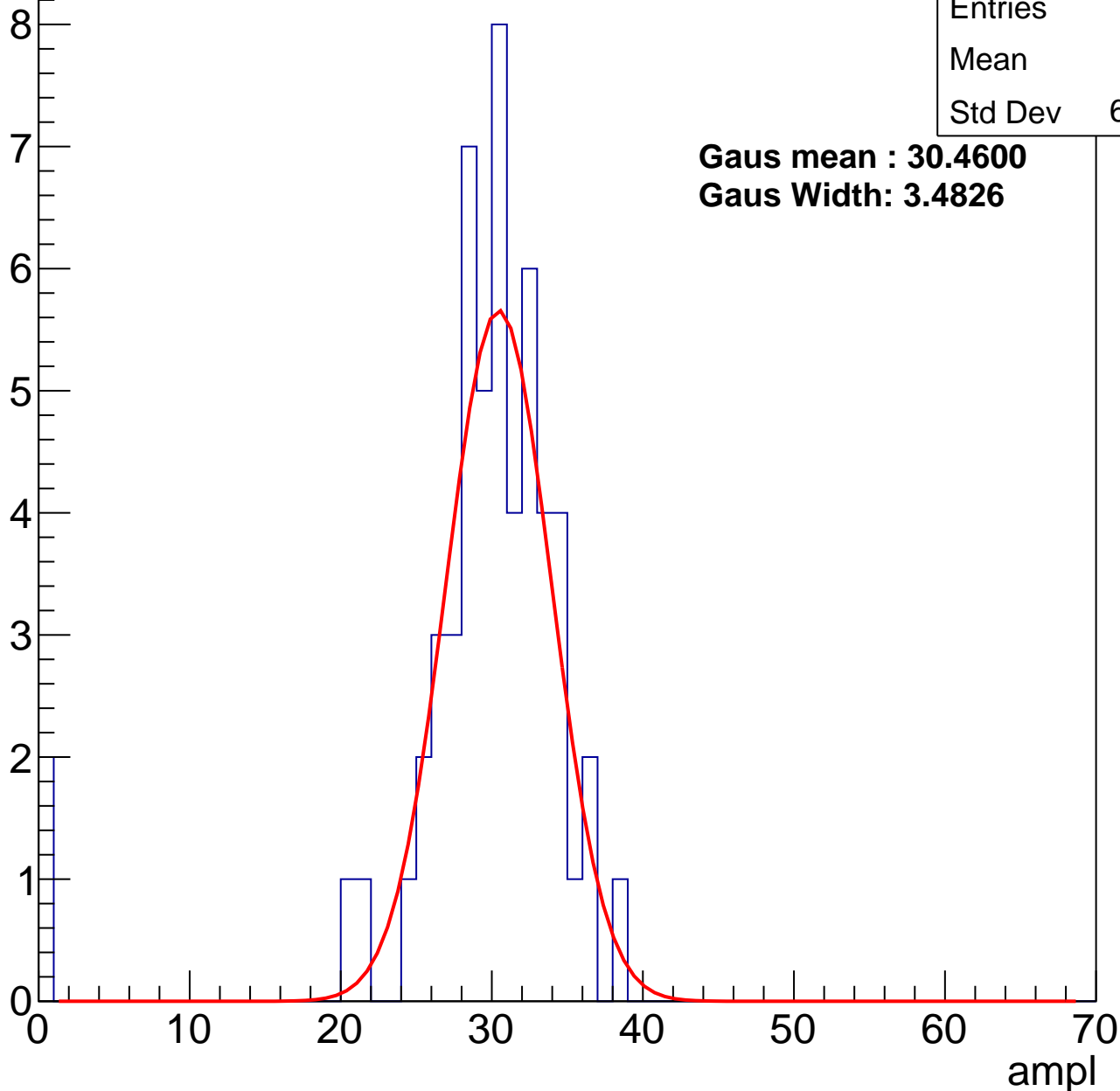
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	28.8
Std Dev	6.585

Gaus mean : 30.4600

Gaus Width: 3.4826



B0L000S, U2-ch26, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	78
Mean	36.47
Std Dev	3.696

Gaus mean : 37.0577

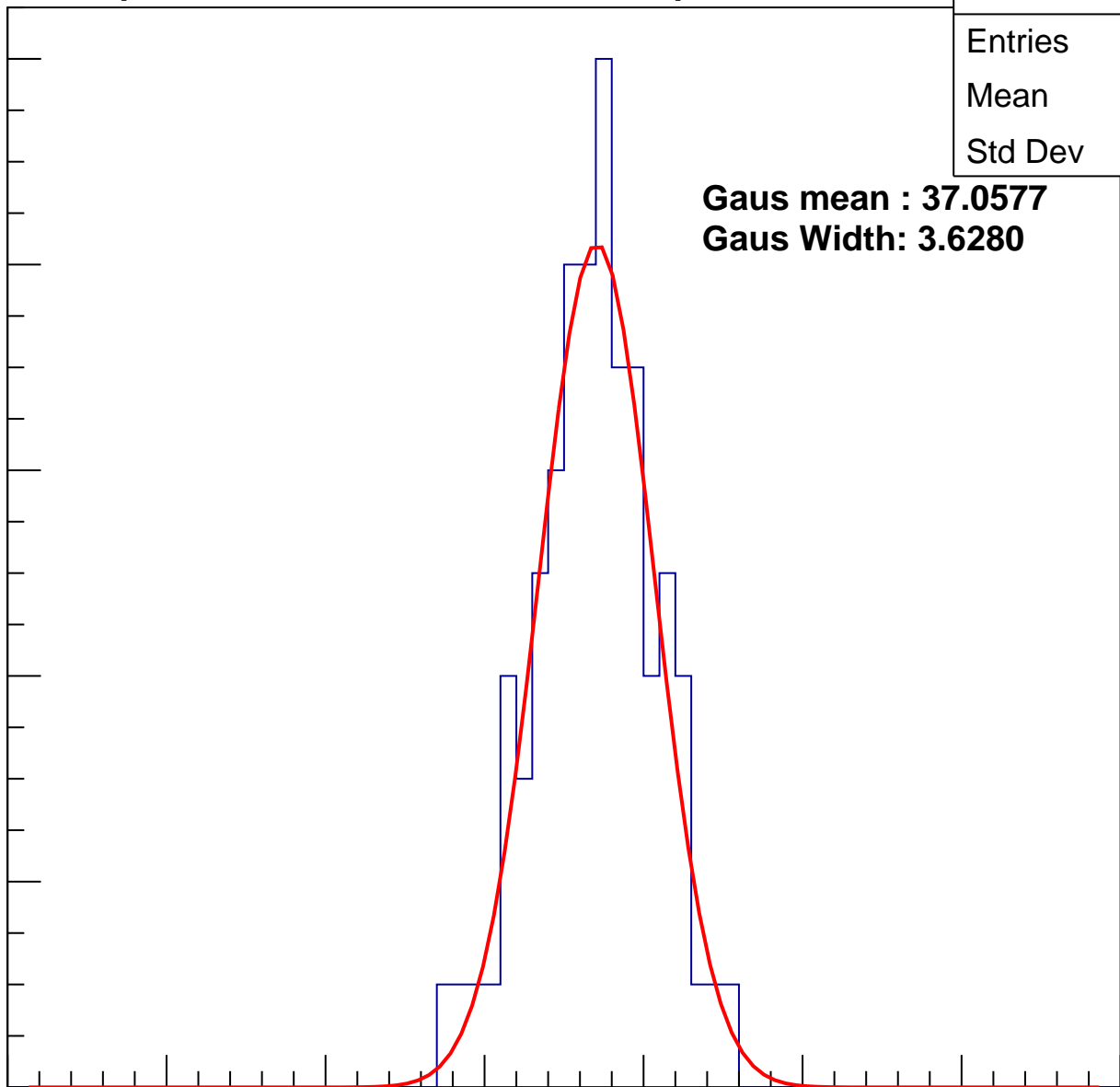
Gaus Width: 3.6280

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch26, adc2

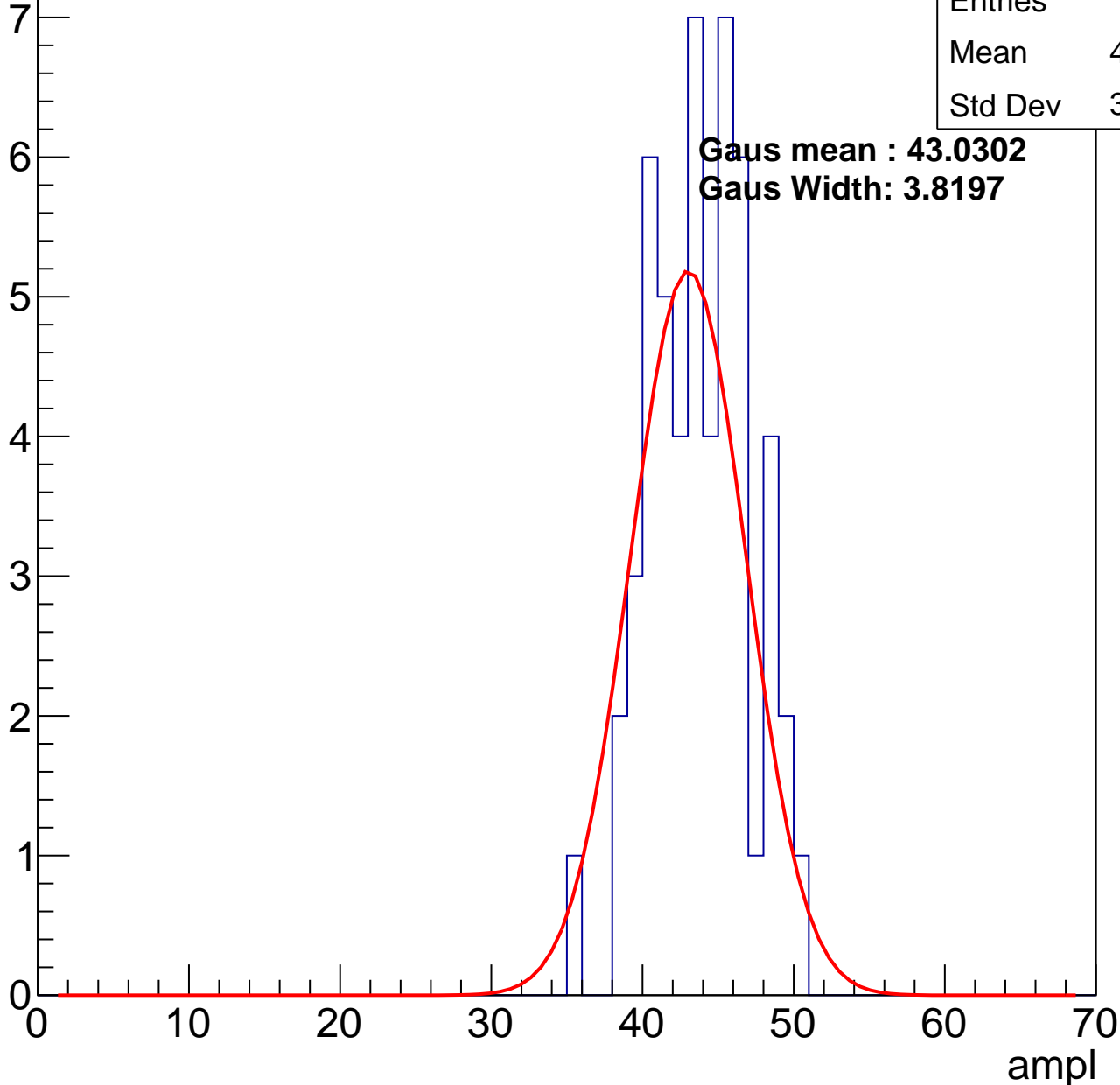
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.32
Std Dev	3.243

Gaus mean : 43.0302

Gaus Width: 3.8197

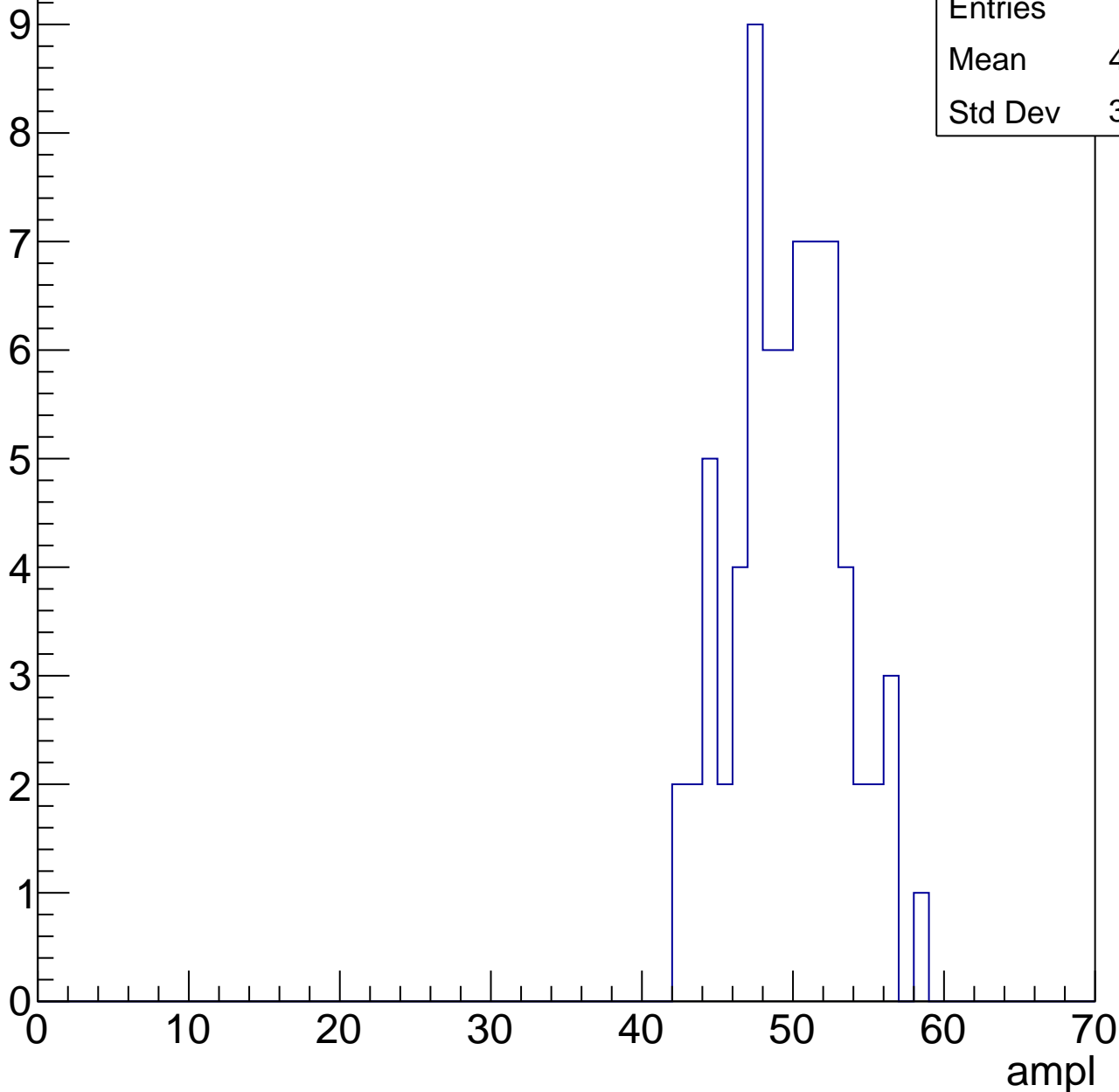


B0L000S, U2-ch26, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.22
Std Dev	3.639

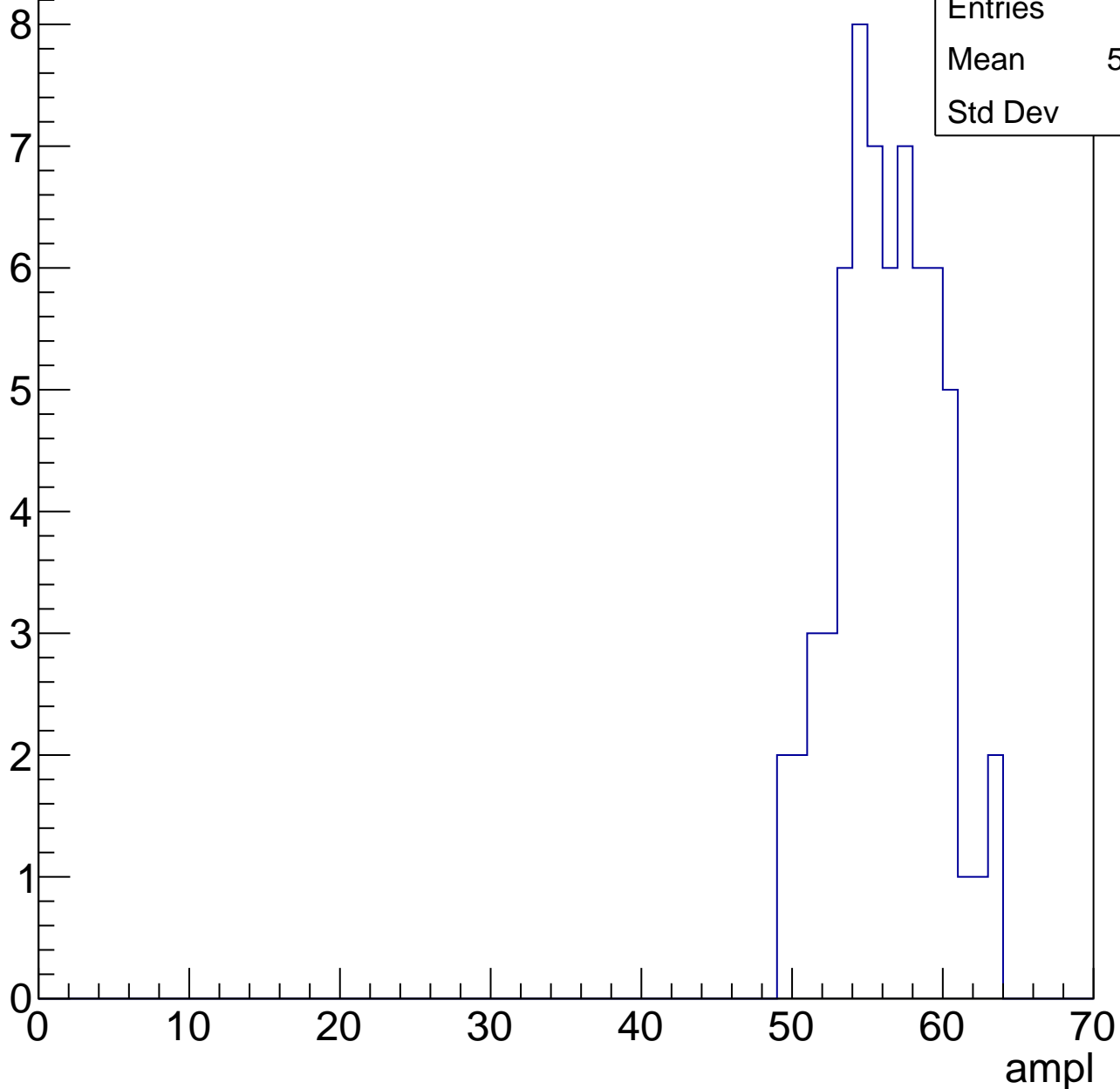


B0L000S, U2-ch26, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	55.82
Std Dev	3.3



B0L000S, U2-ch26, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	58.77
Std Dev	9.76

ampl

0

10

20

30

40

50

60

70

B0L000S, U2-ch26, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B0L000S, U2-ch26, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch27, adc0

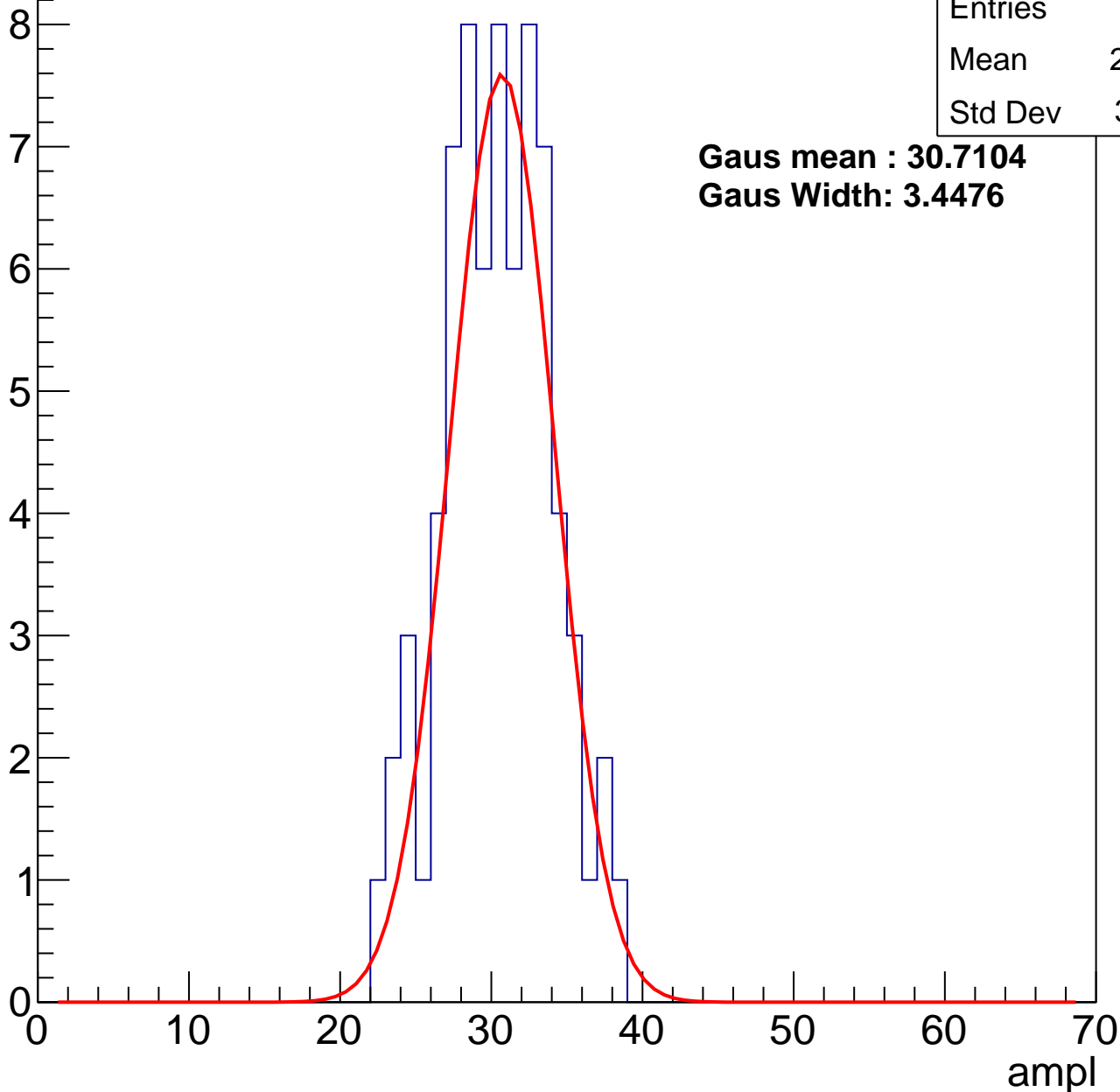
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	29.97
Std Dev	3.531

Gaus mean : 30.7104

Gaus Width: 3.4476



B0L000S, U2-ch27, adc1

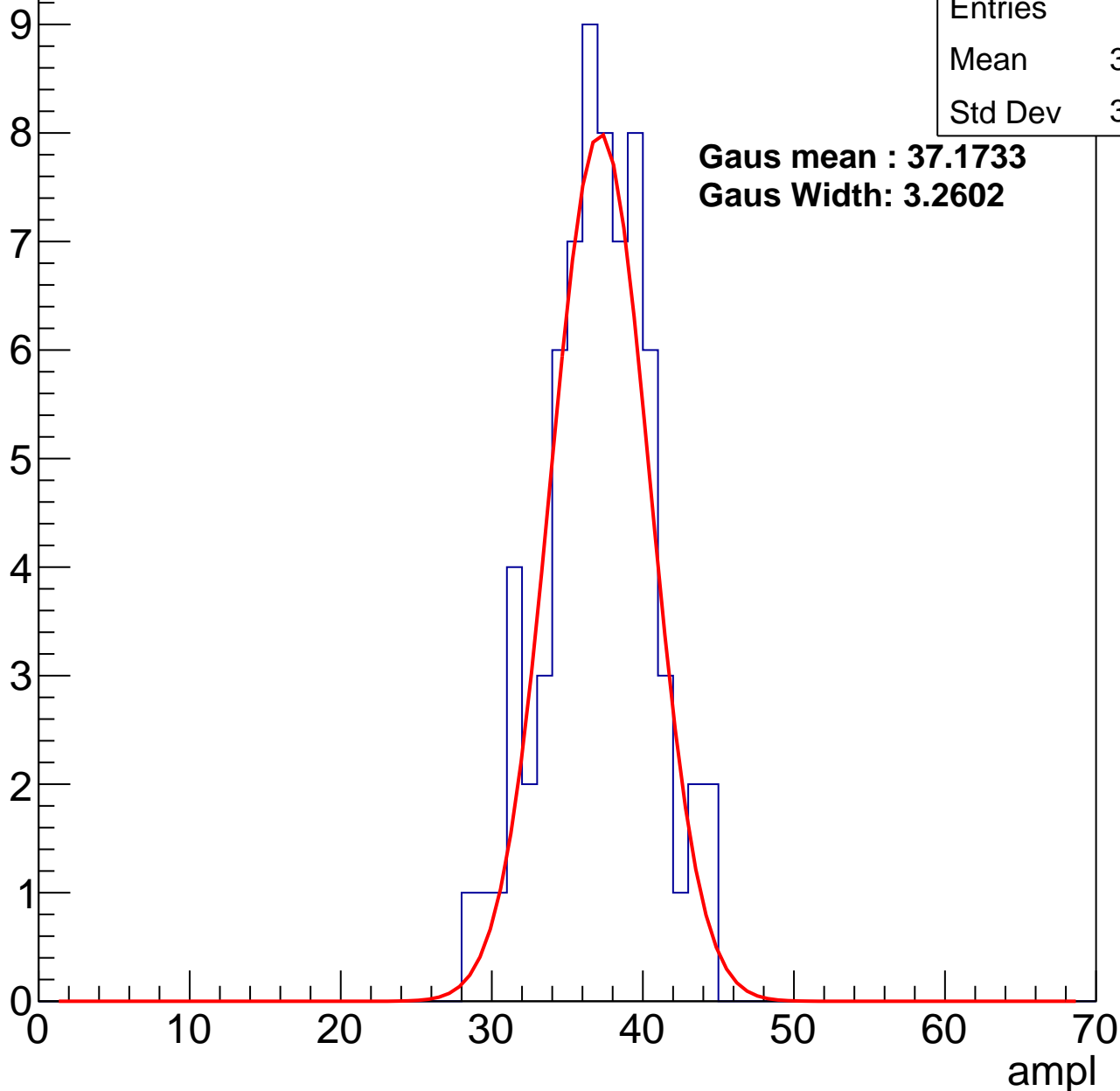
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.62
Std Dev	3.466

Gaus mean : 37.1733

Gaus Width: 3.2602



B0L000S, U2-ch27, adc2

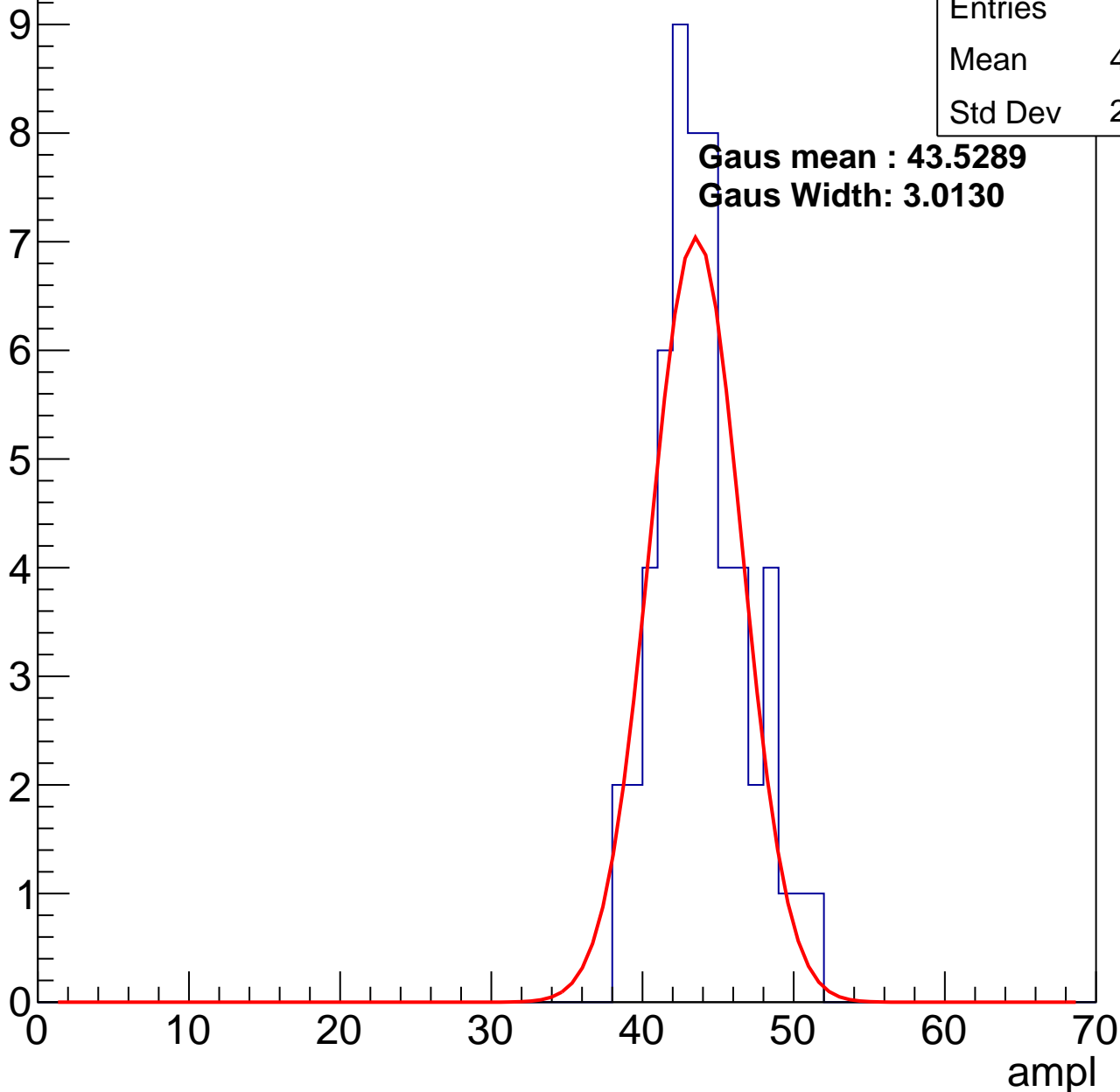
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43.46
Std Dev	2.927

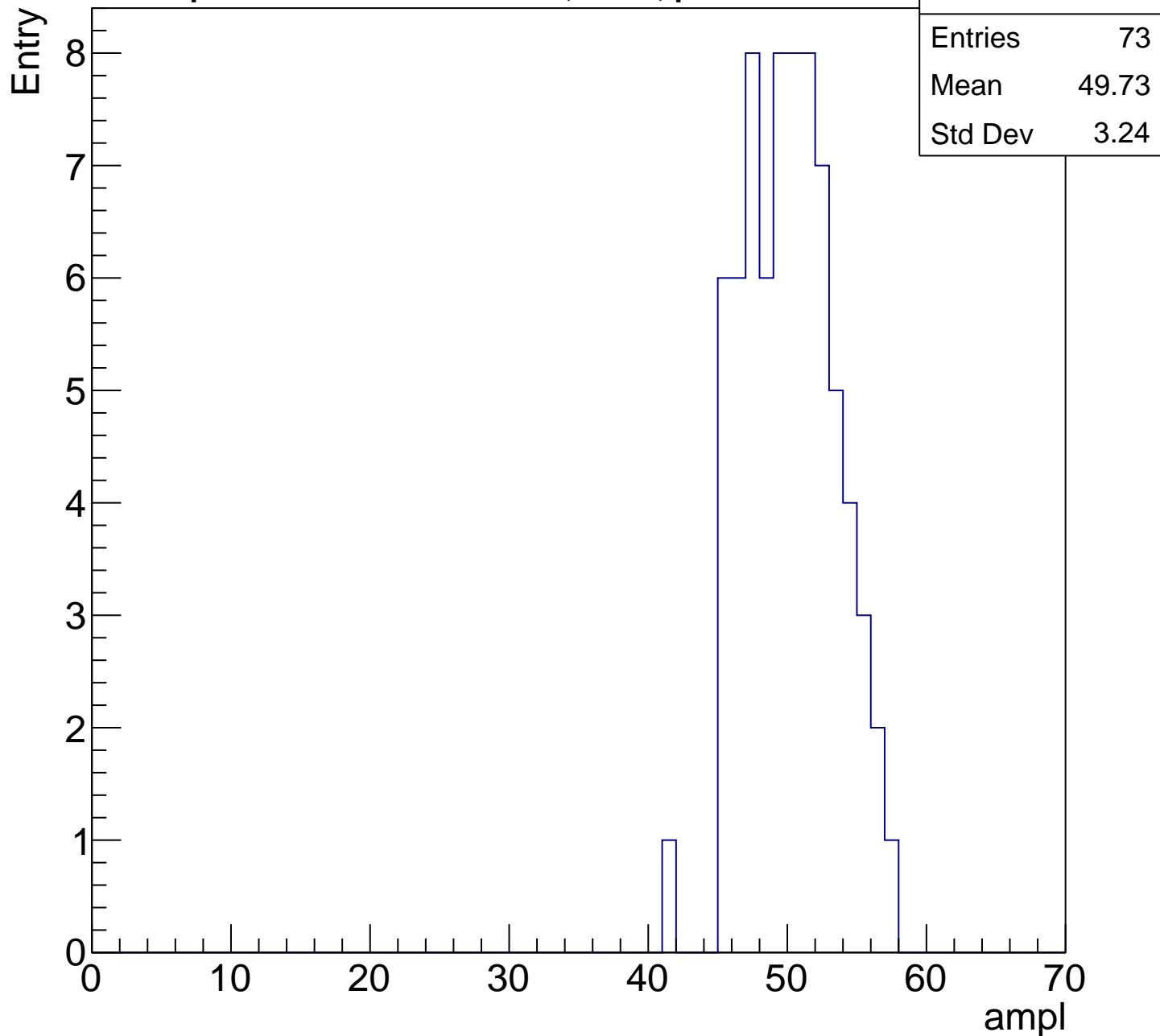
Gaus mean : 43.5289

Gaus Width: 3.0130



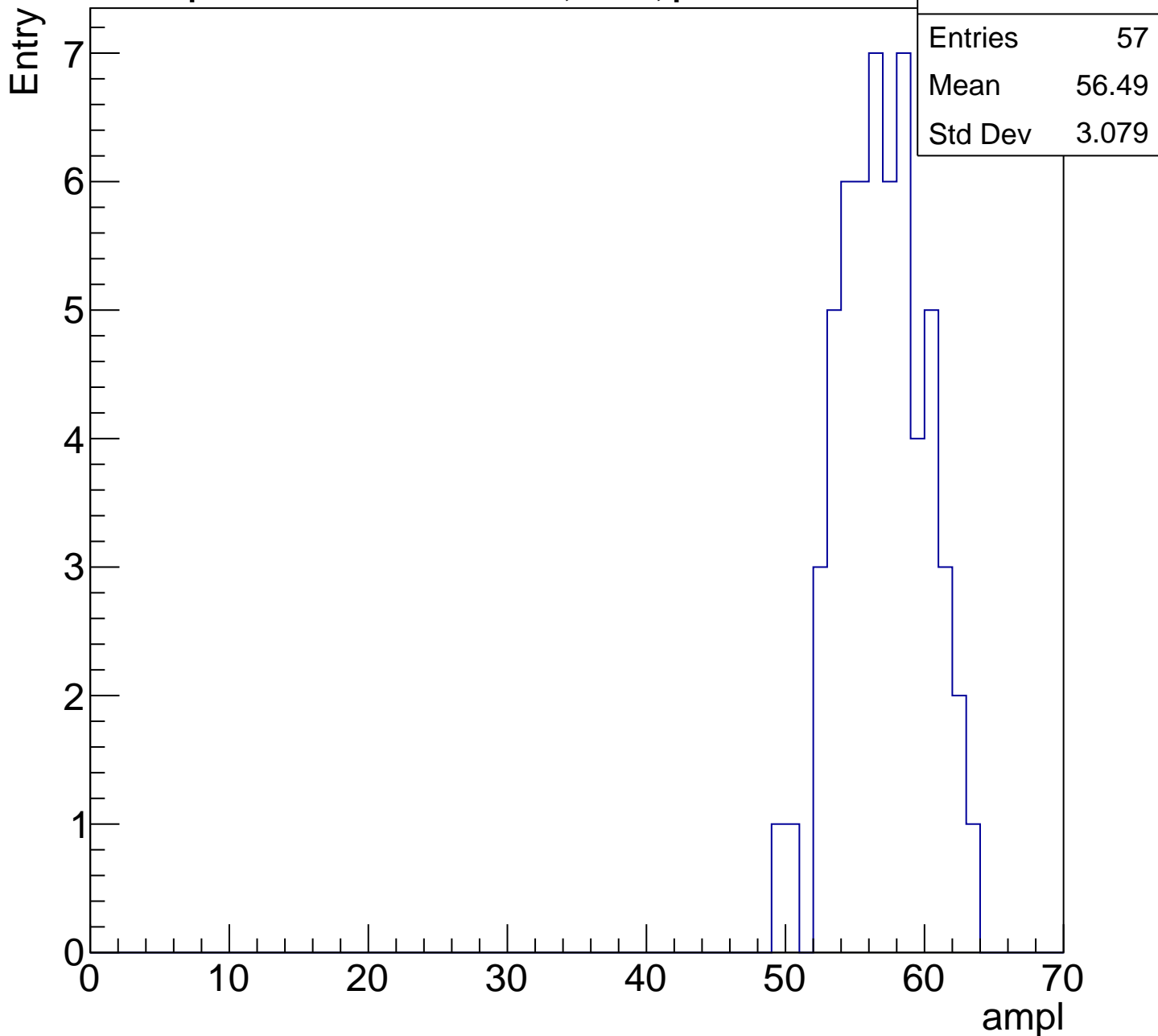
B0L000S, U2-ch27, adc3

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch27, adc4

calib_packv5_042523_0143.root, FC#5, port B1

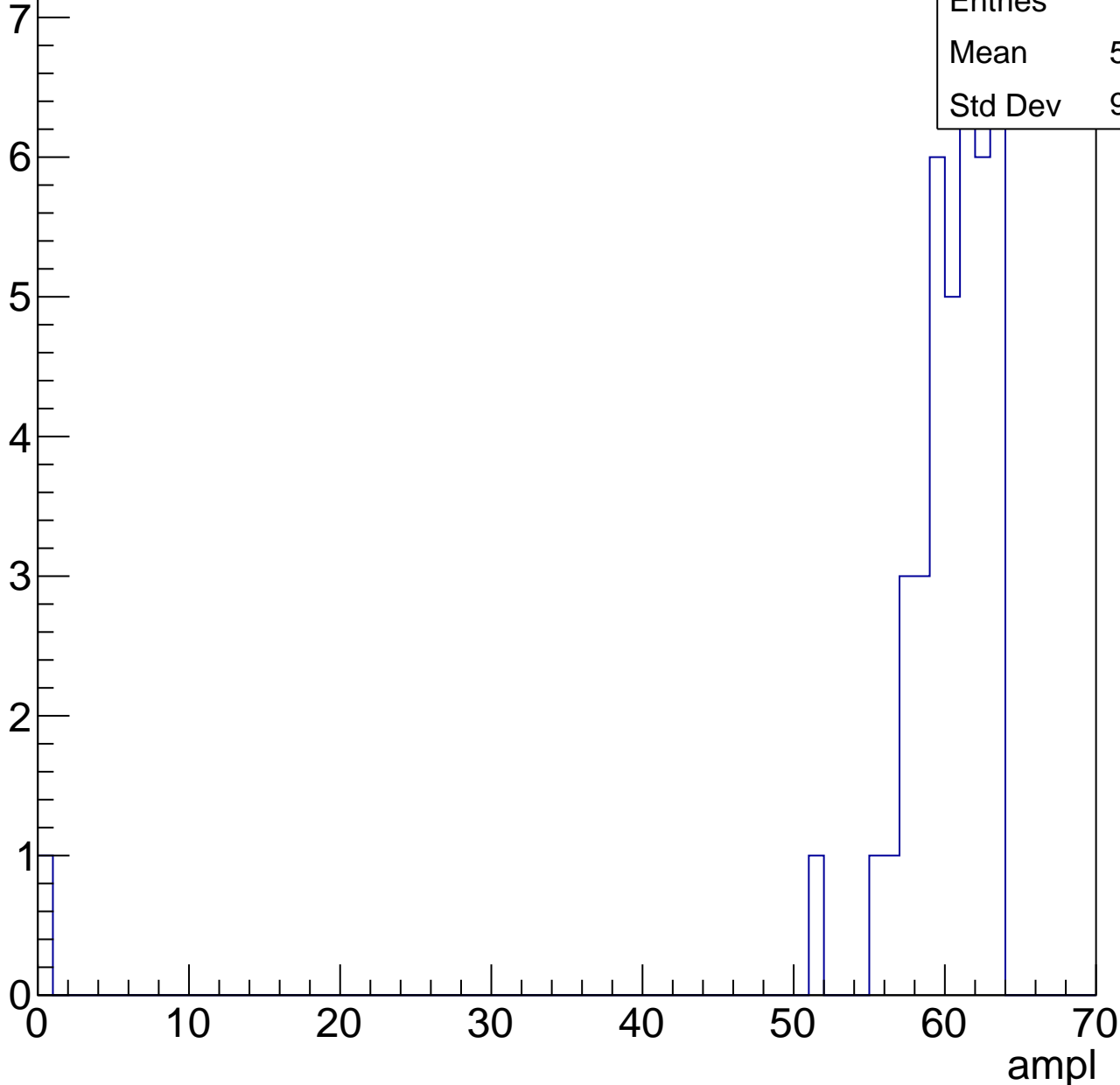


B0L000S, U2-ch27, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.56
Std Dev	9.597



B0L000S, U2-ch27, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch27, adc7

calib_packv5_042523_0143.root, FC#5, port B1

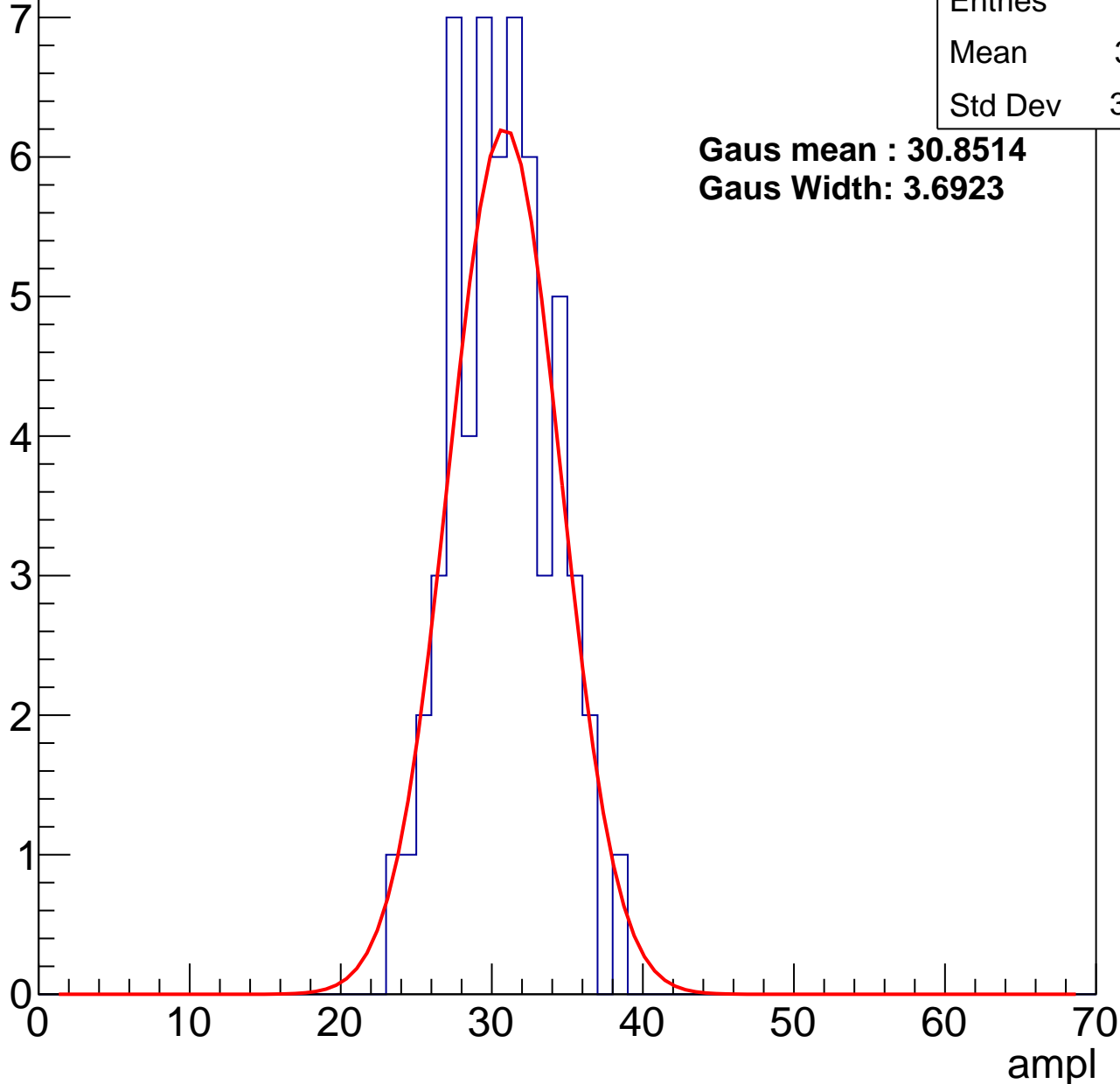
Entry



B0L000S, U2-ch28, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch28, adc1

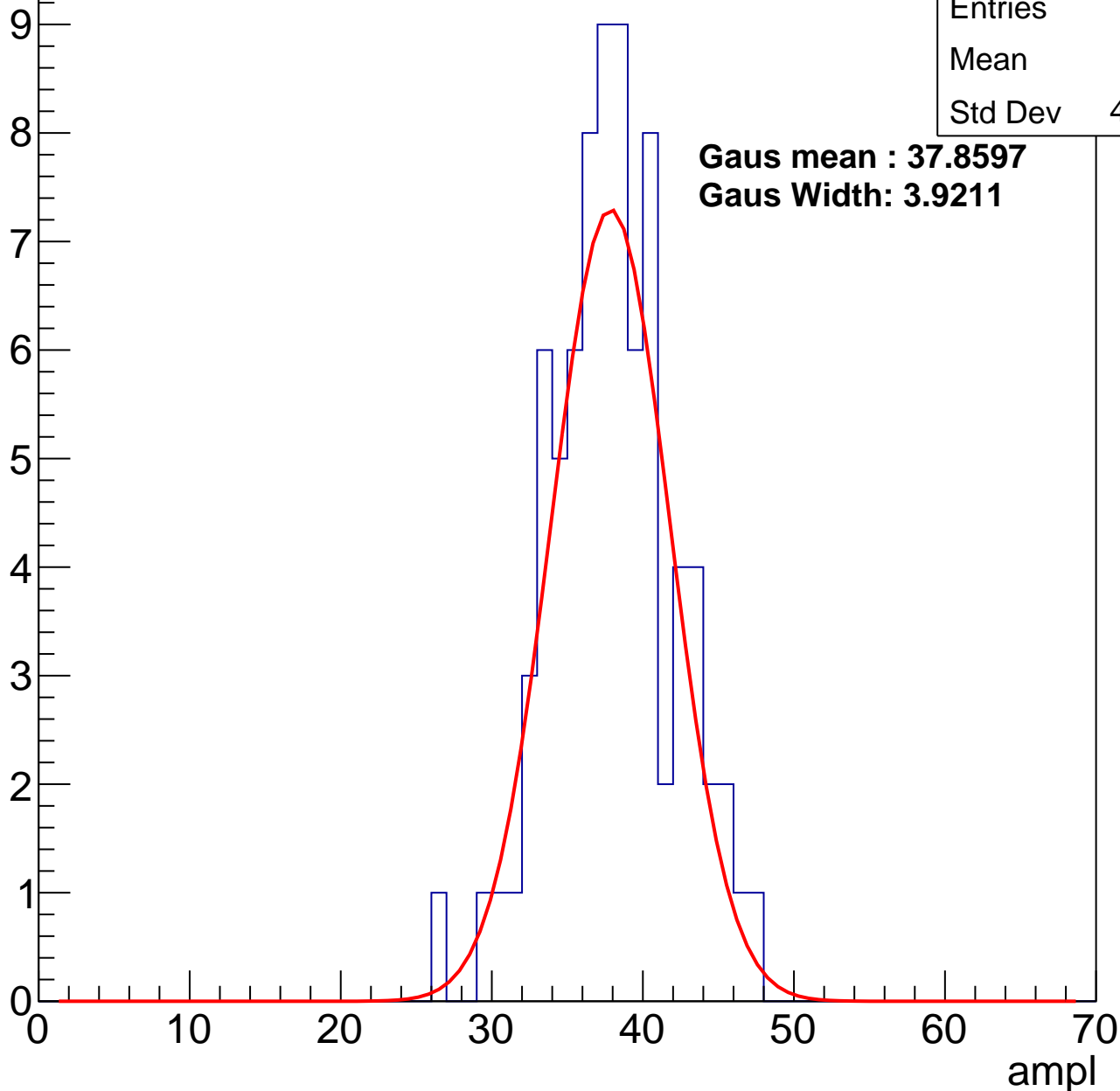
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	37.5
Std Dev	4.009

Gaus mean : 37.8597

Gaus Width: 3.9211



B0L000S, U2-ch28, adc2

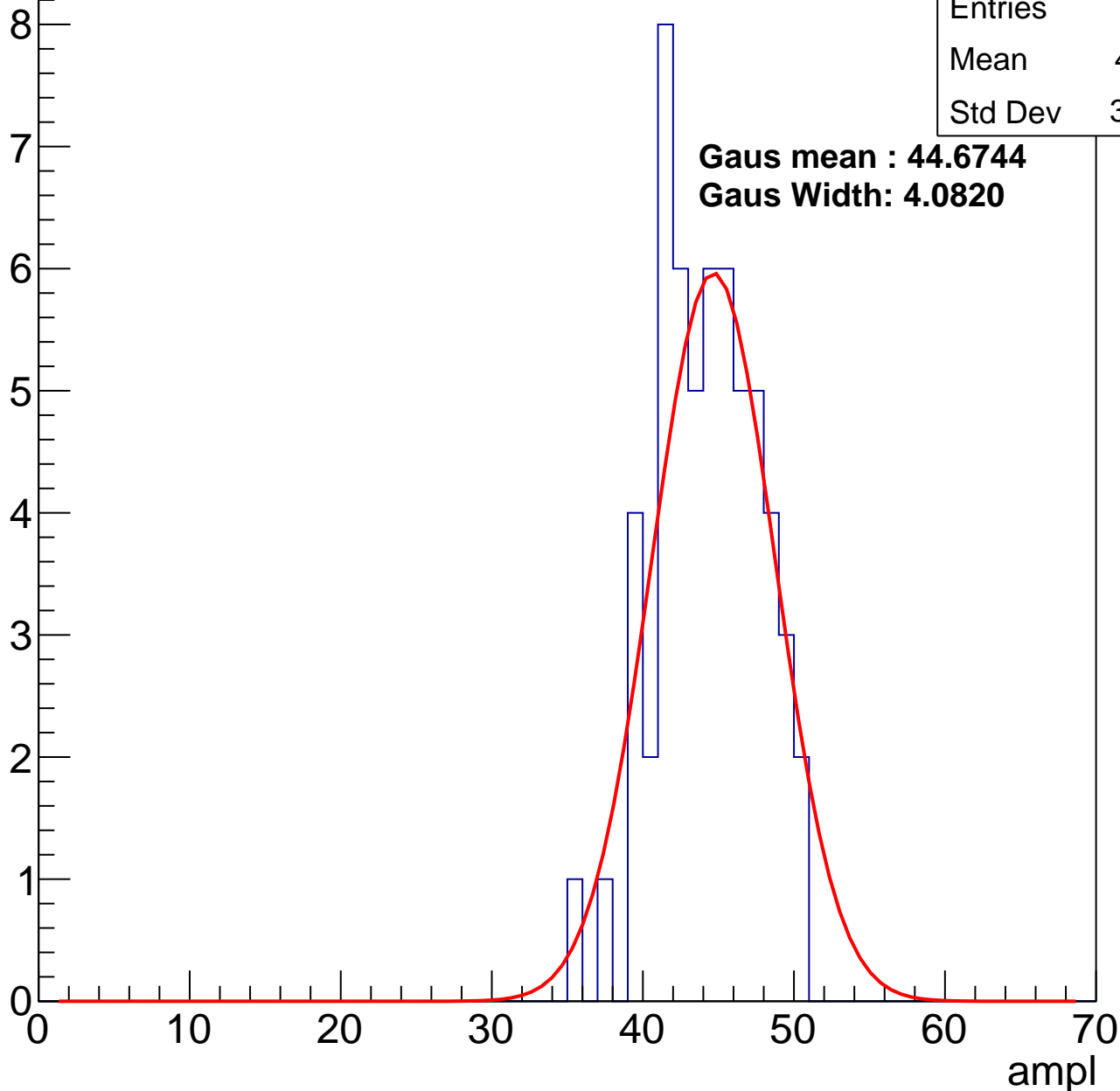
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.81
Std Dev	3.329

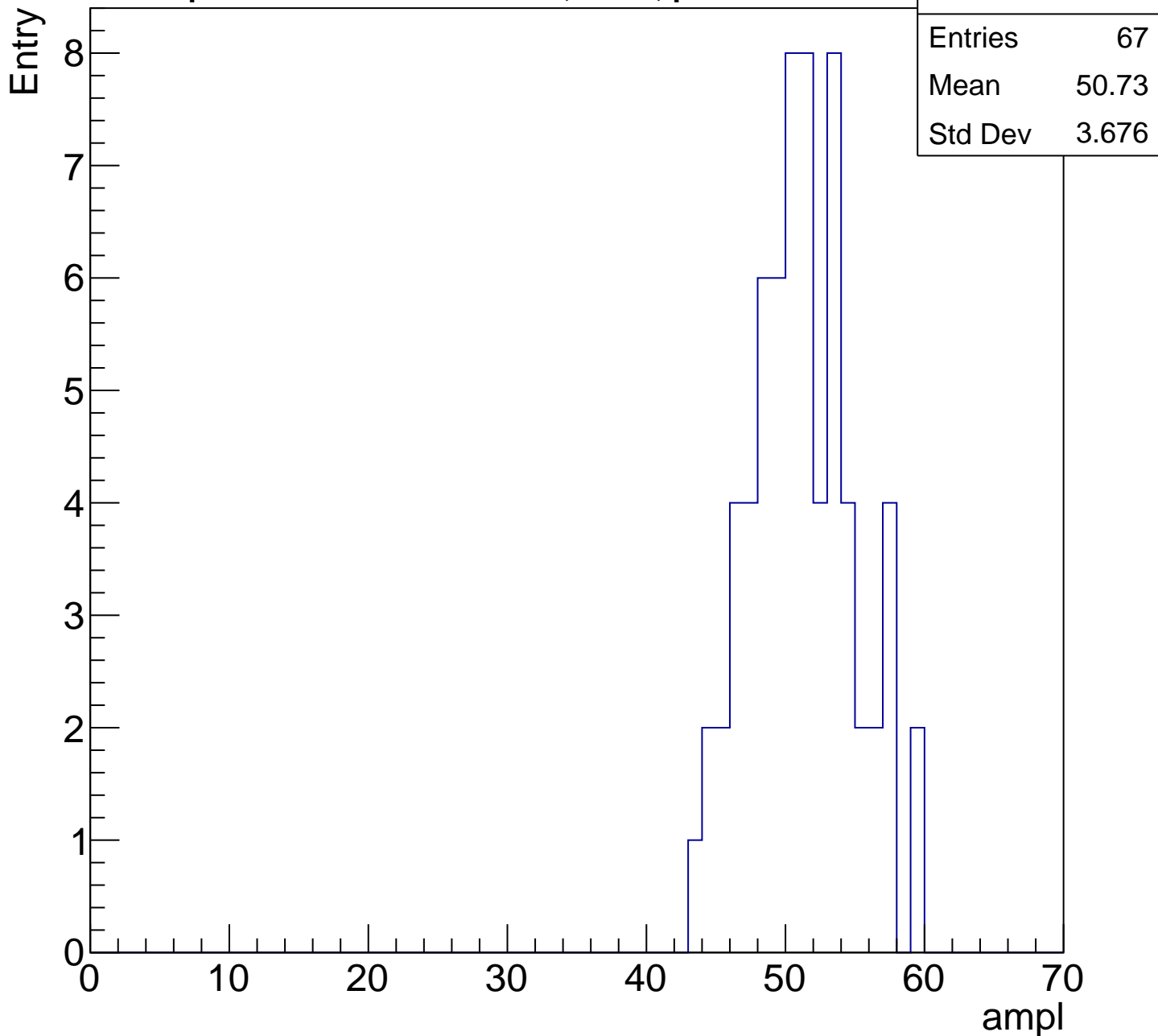
Gaus mean : 44.6744

Gaus Width: 4.0820



B0L000S, U2-ch28, adc3

calib_packv5_042523_0143.root, FC#5, port B1

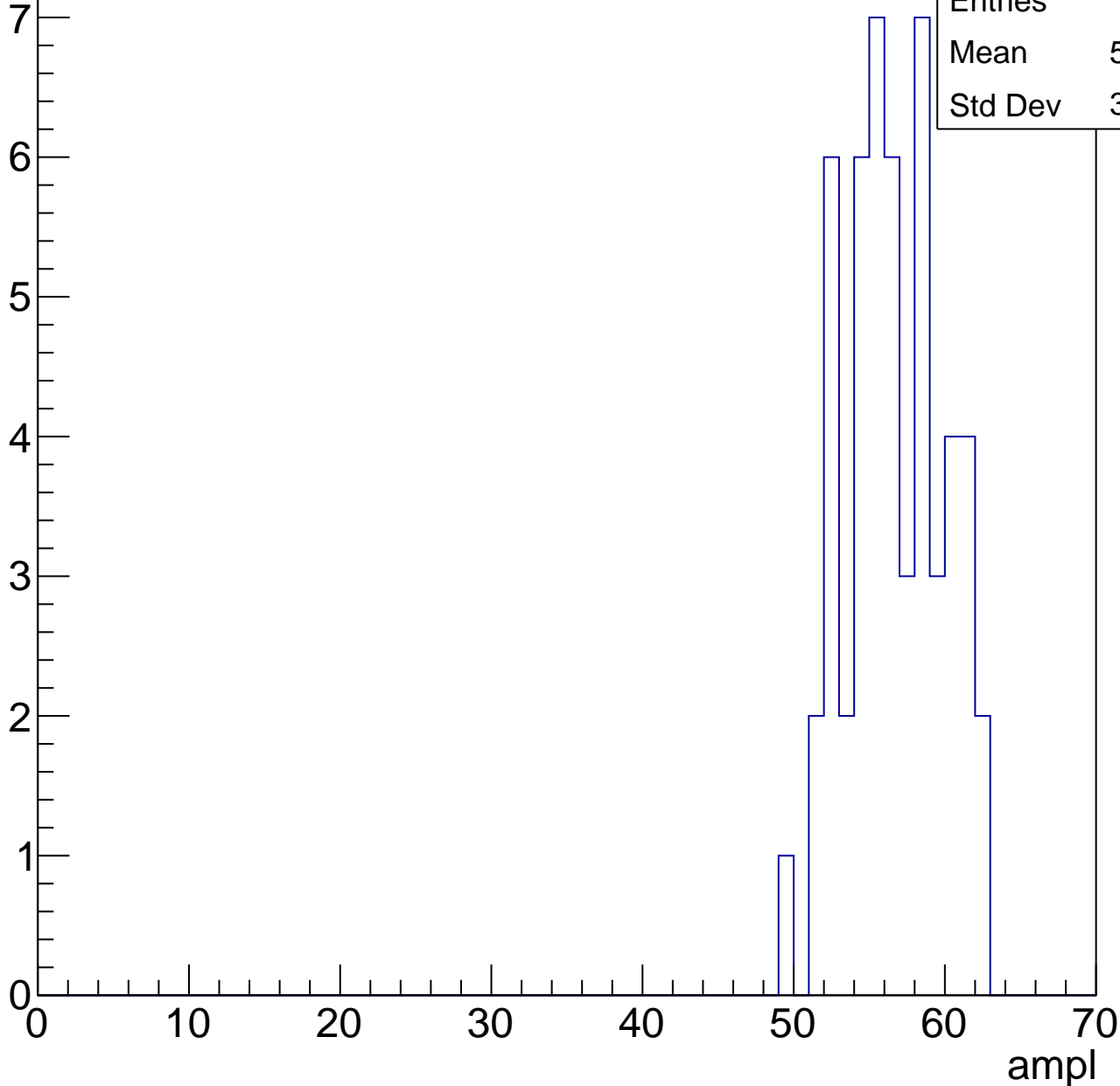


B0L000S, U2-ch28, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.15
Std Dev	3.182

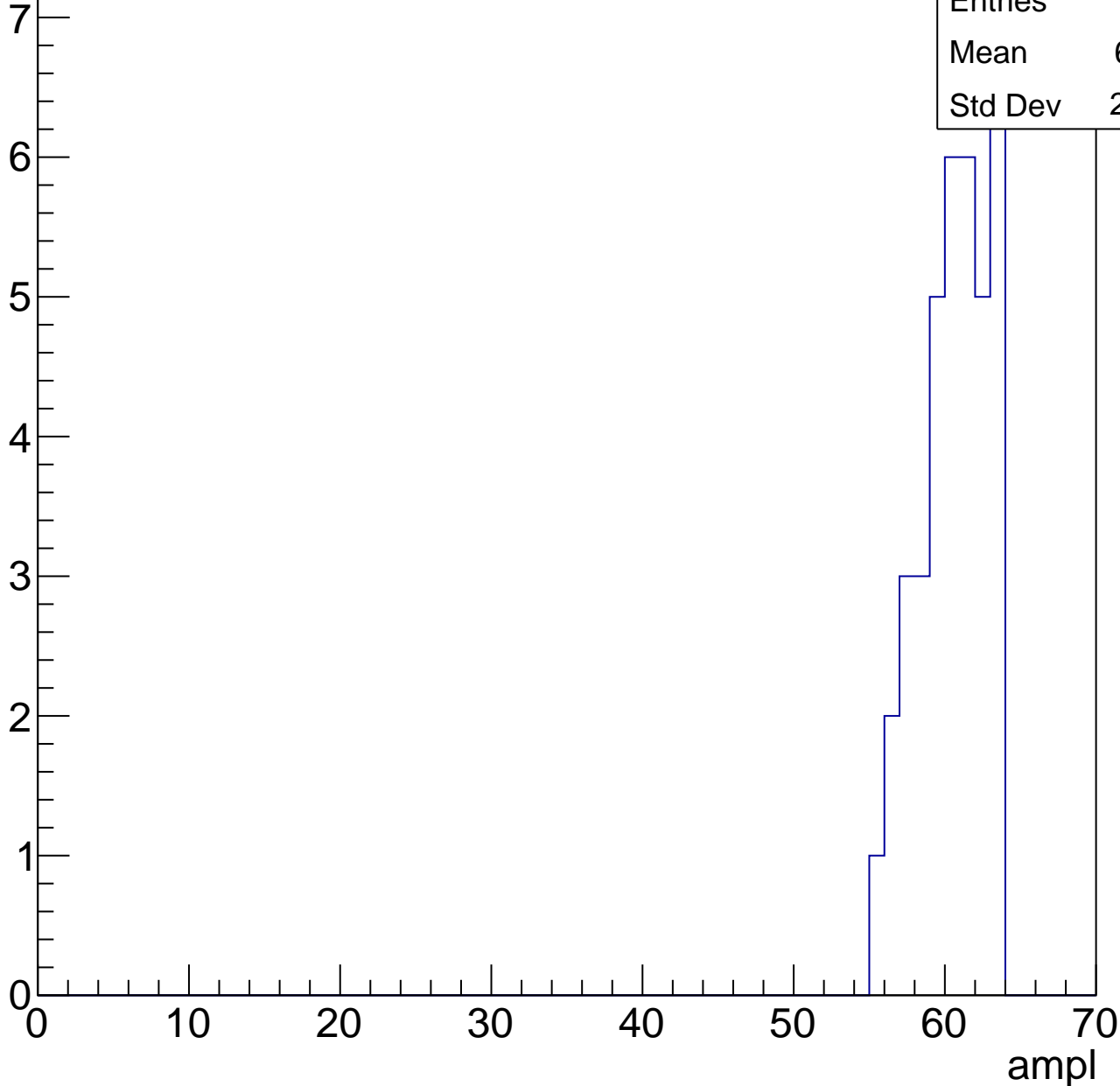


B0L000S, U2-ch28, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

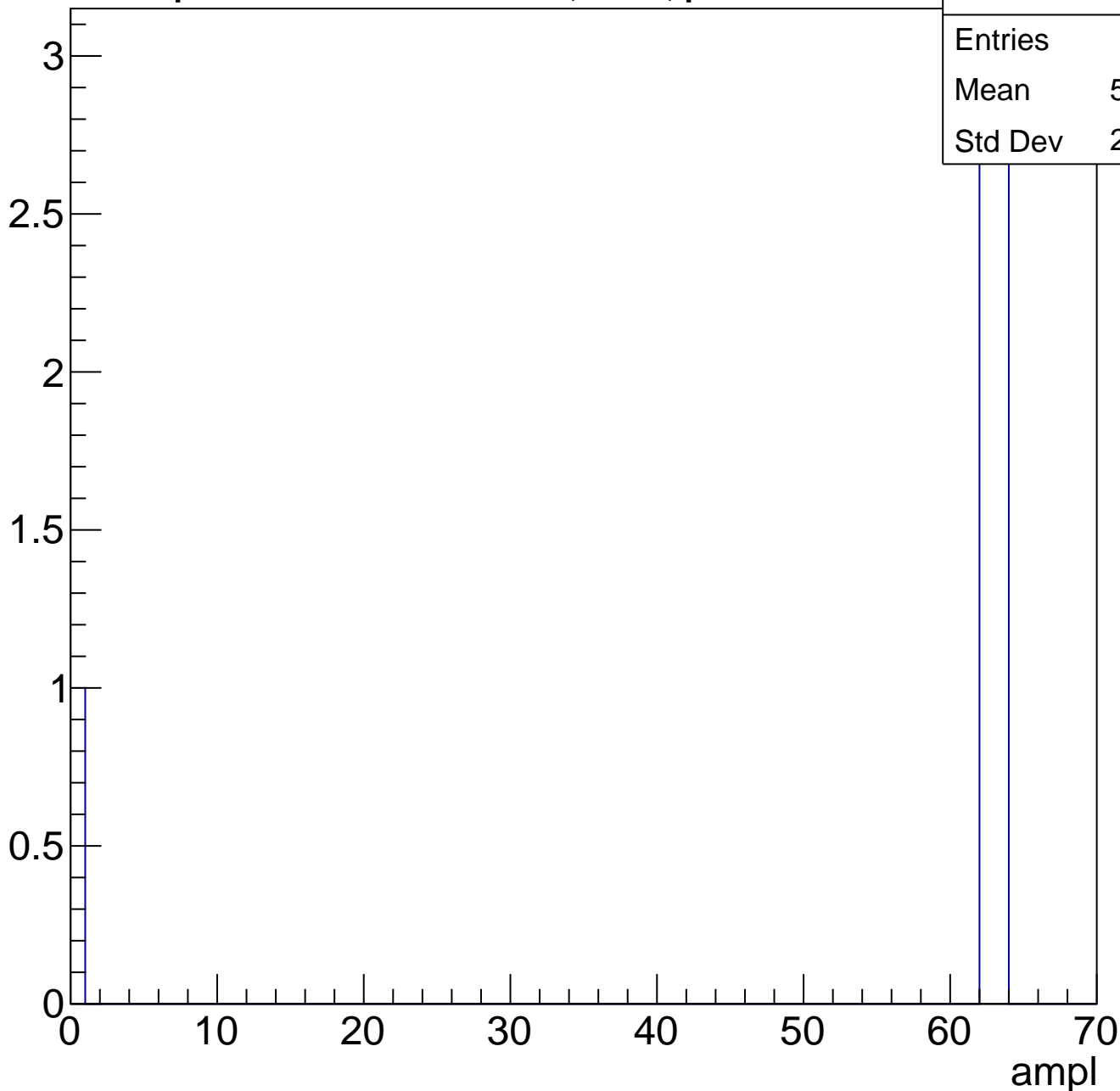
Entries	38
Mean	60.11
Std Dev	2.234



B0L000S, U2-ch28, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch28, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch29, adc0

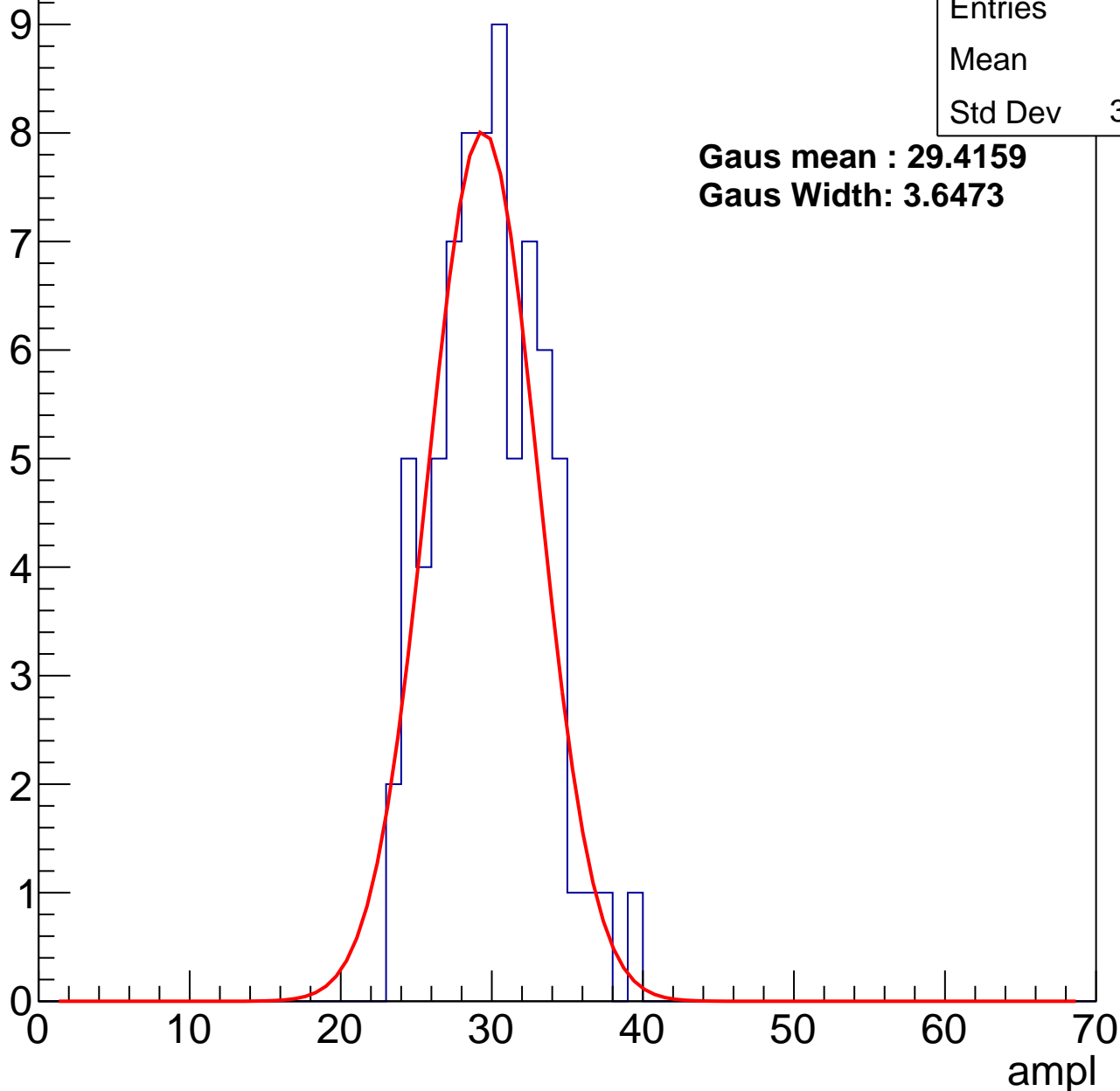
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.4
Std Dev	3.449

Gaus mean : 29.4159

Gaus Width: 3.6473



B0L000S, U2-ch29, adc1

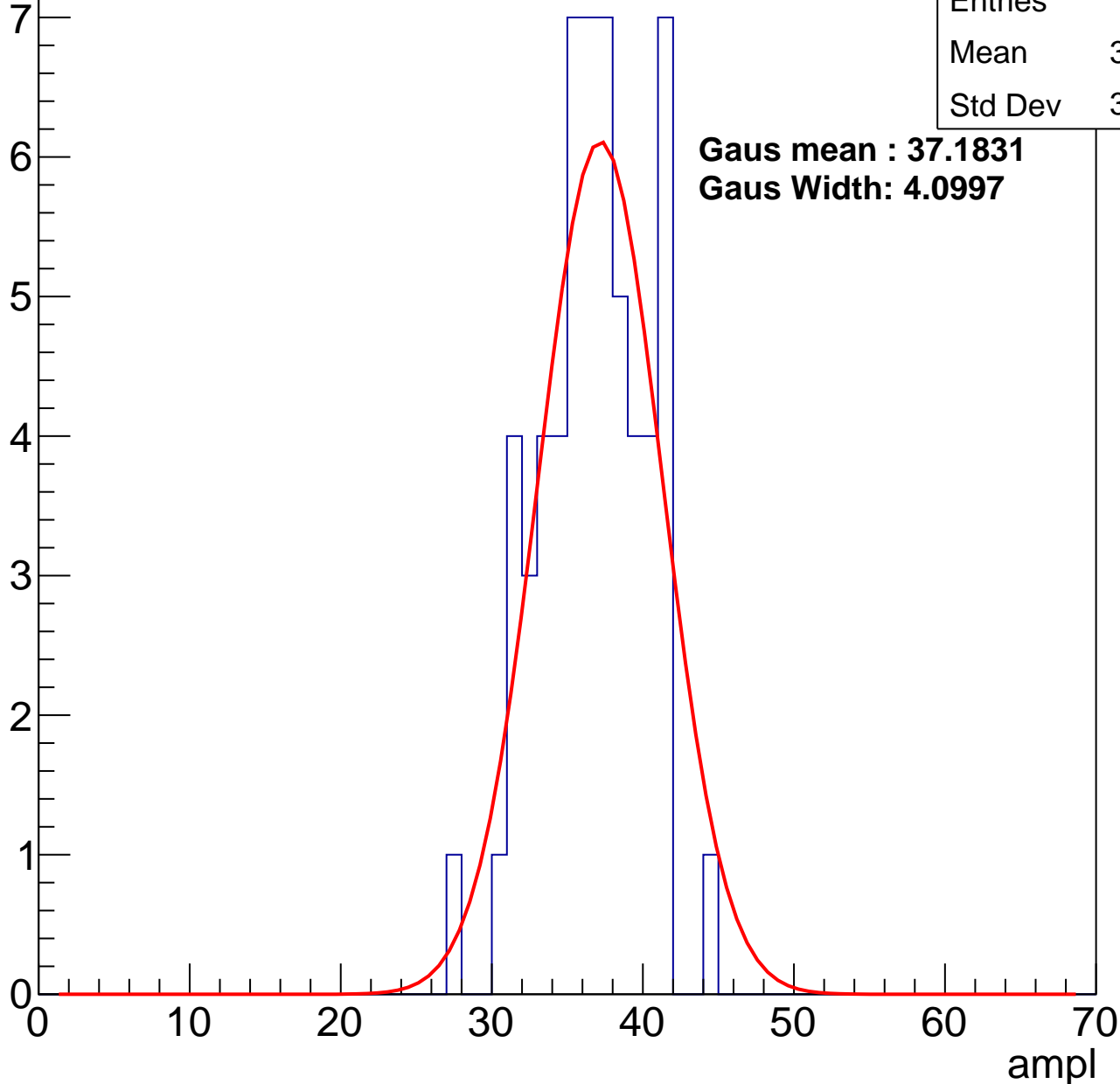
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.24
Std Dev	3.412

Gaus mean : 37.1831

Gaus Width: 4.0997



B0L000S, U2-ch29, adc2

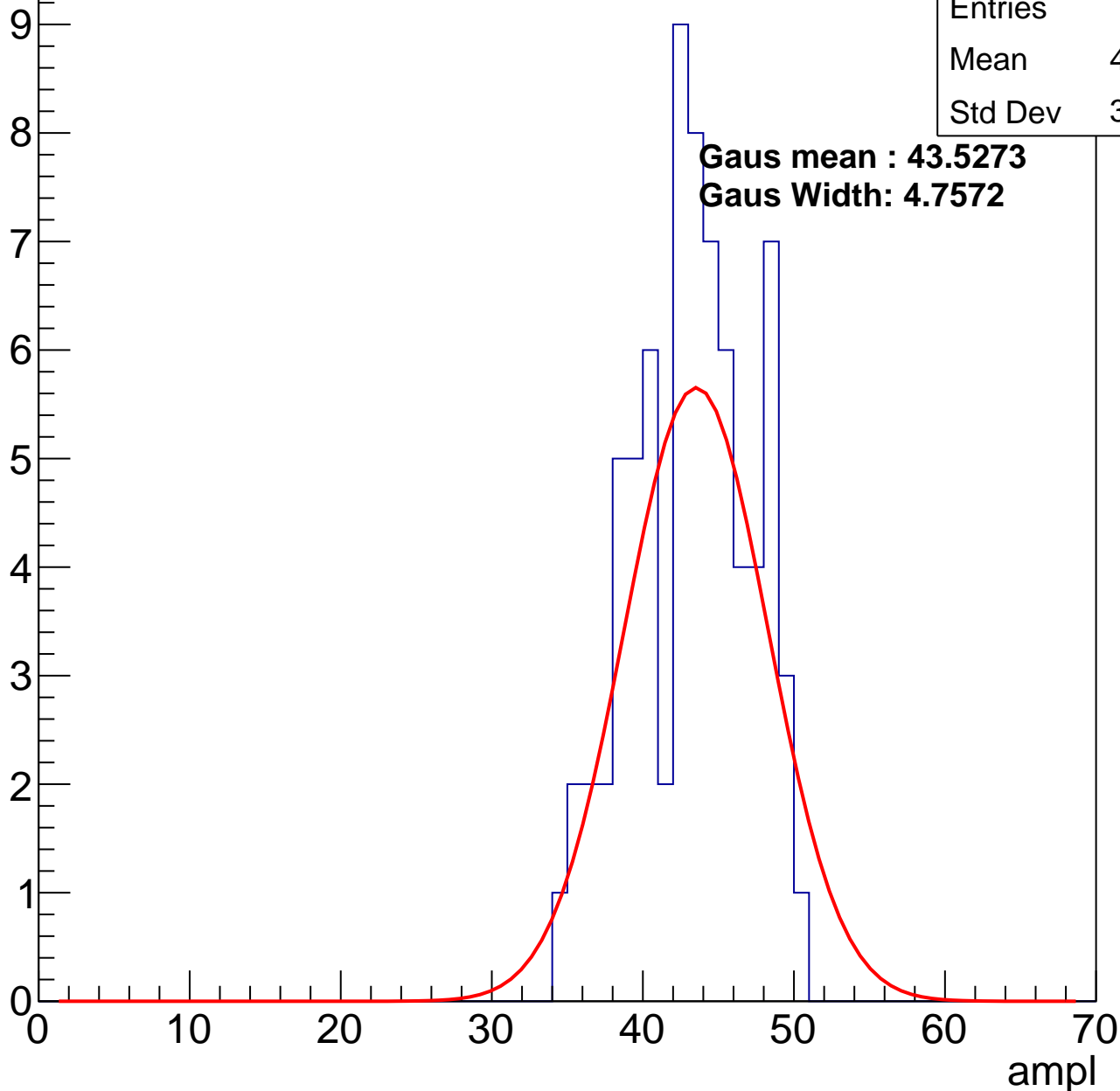
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.73
Std Dev	3.878

Gaus mean : 43.5273

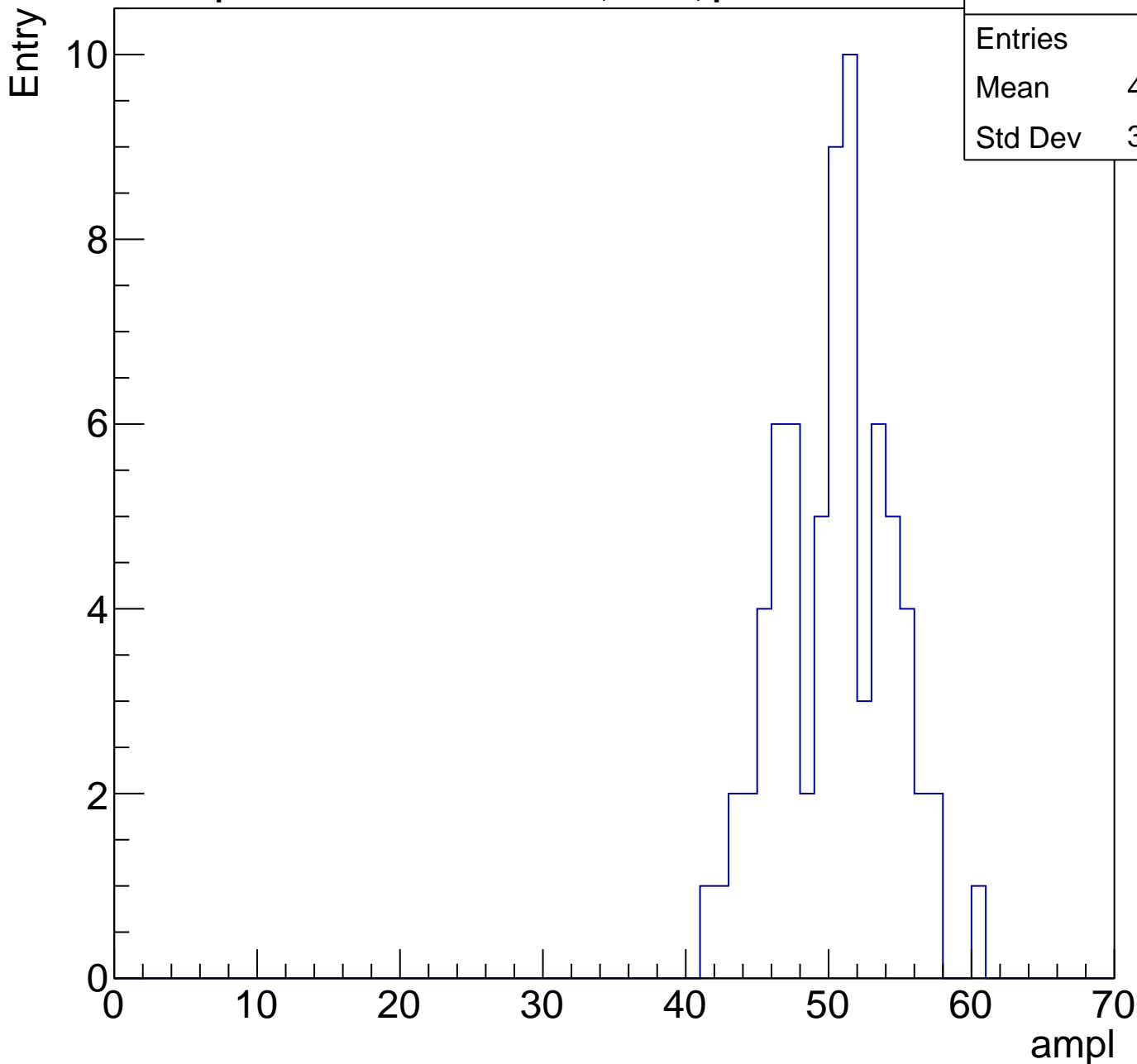
Gaus Width: 4.7572



B0L000S, U2-ch29, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	49.94
Std Dev	3.943

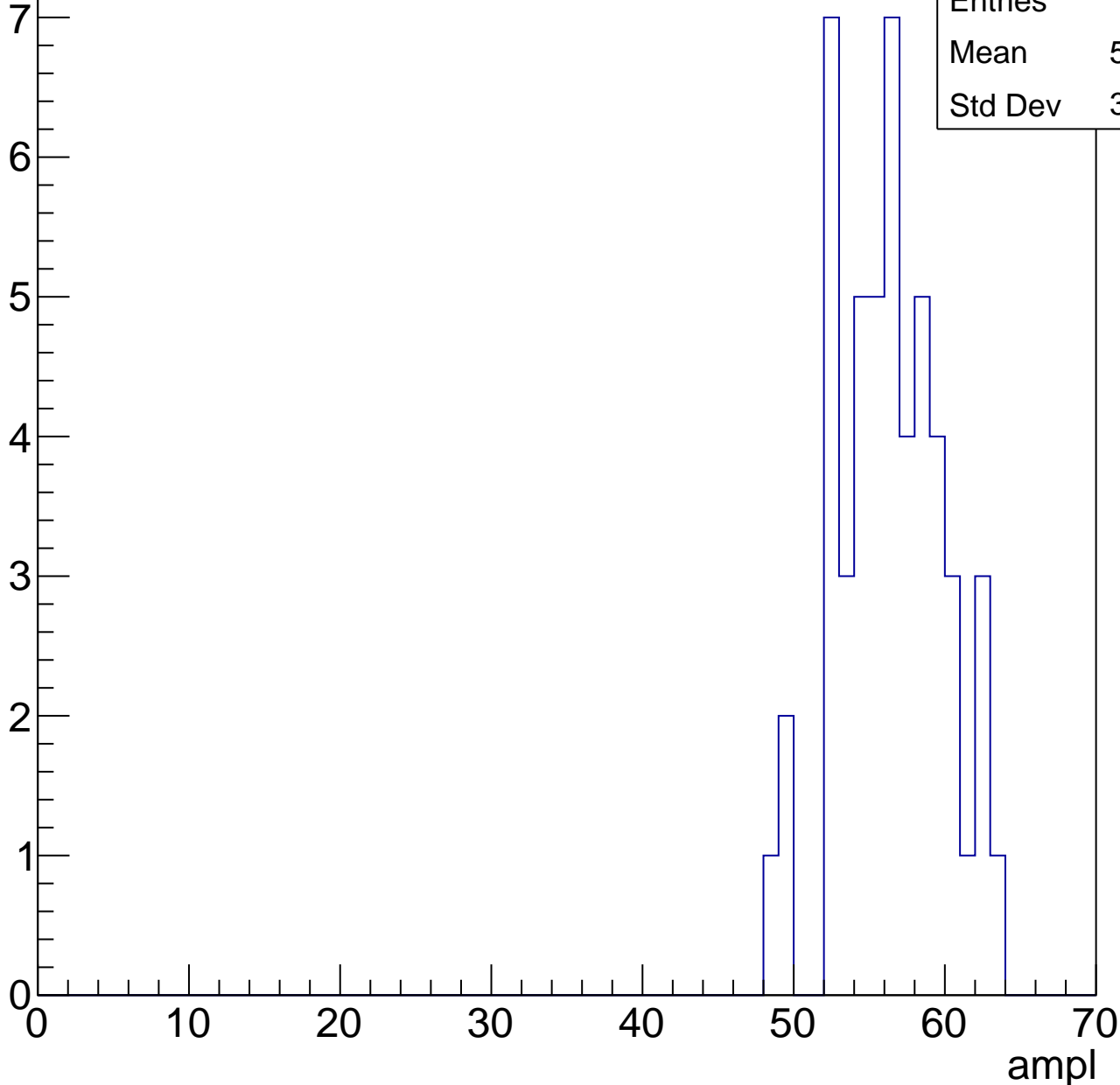


B0L000S, U2-ch29, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	55.88
Std Dev	3.473

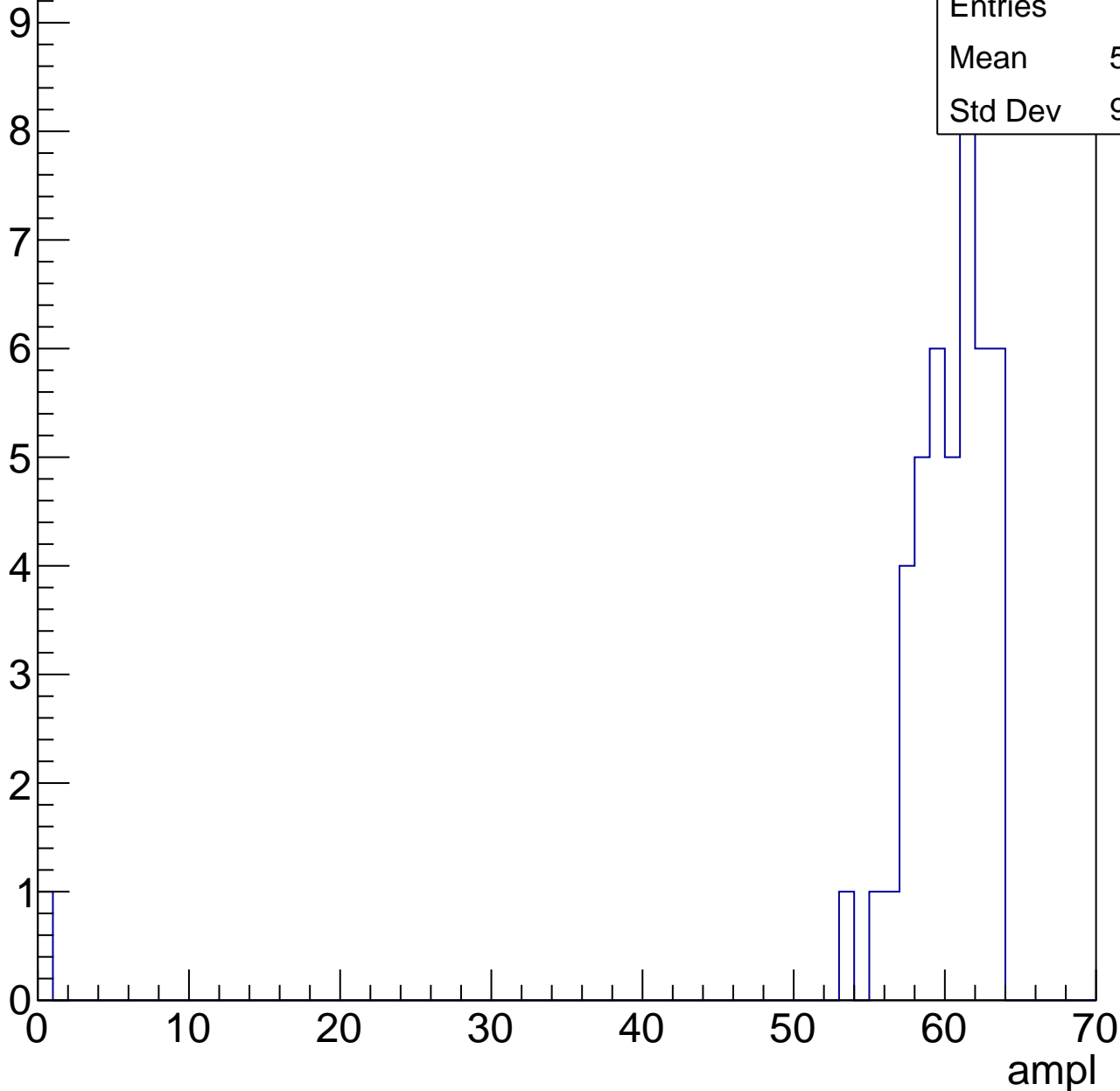


B0L000S, U2-ch29, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.56
Std Dev	9.123



B0L000S, U2-ch29, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch29, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch30, adc0

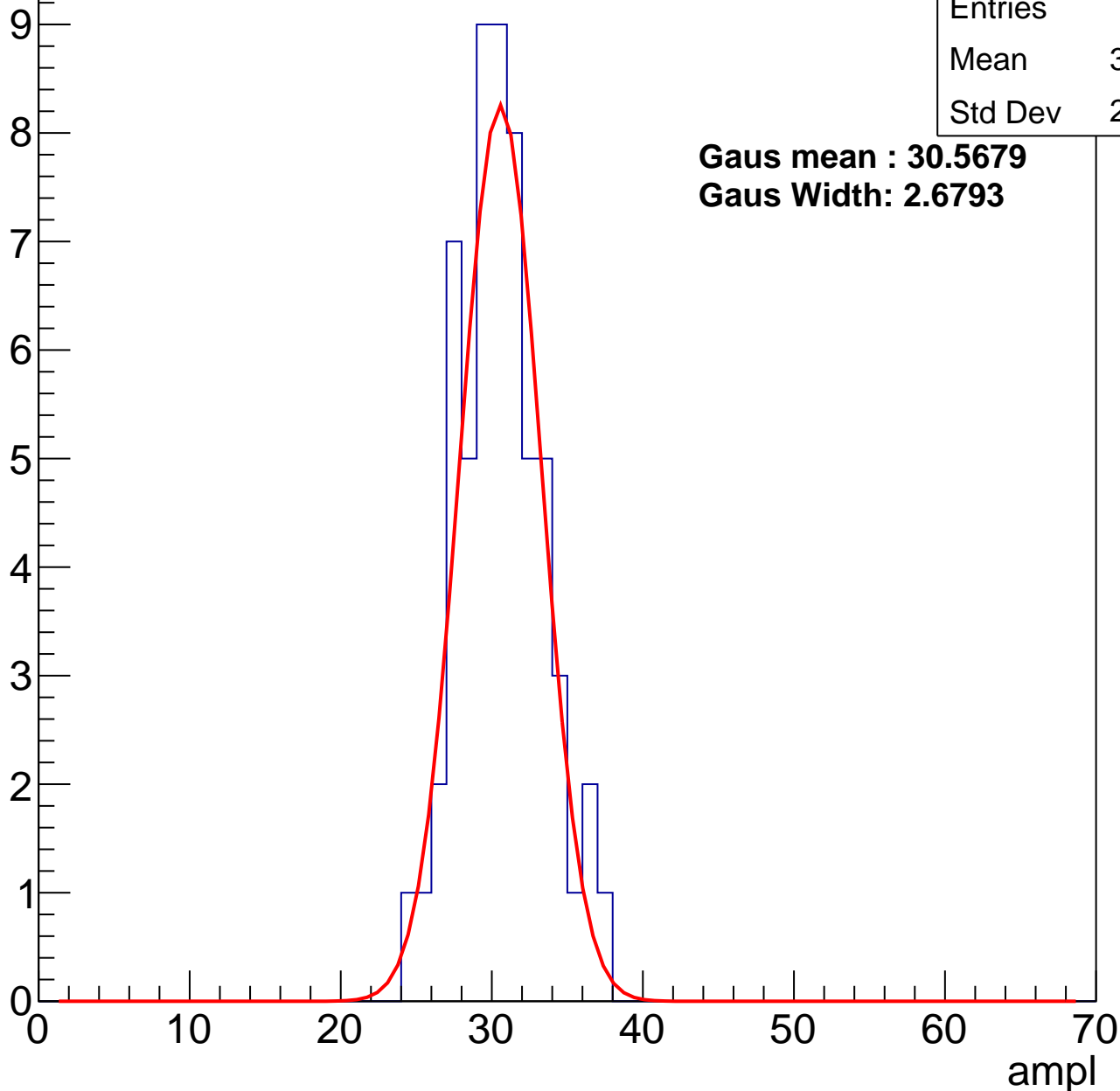
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	30.17
Std Dev	2.763

Gaus mean : 30.5679

Gaus Width: 2.6793



B0L000S, U2-ch30, adc1

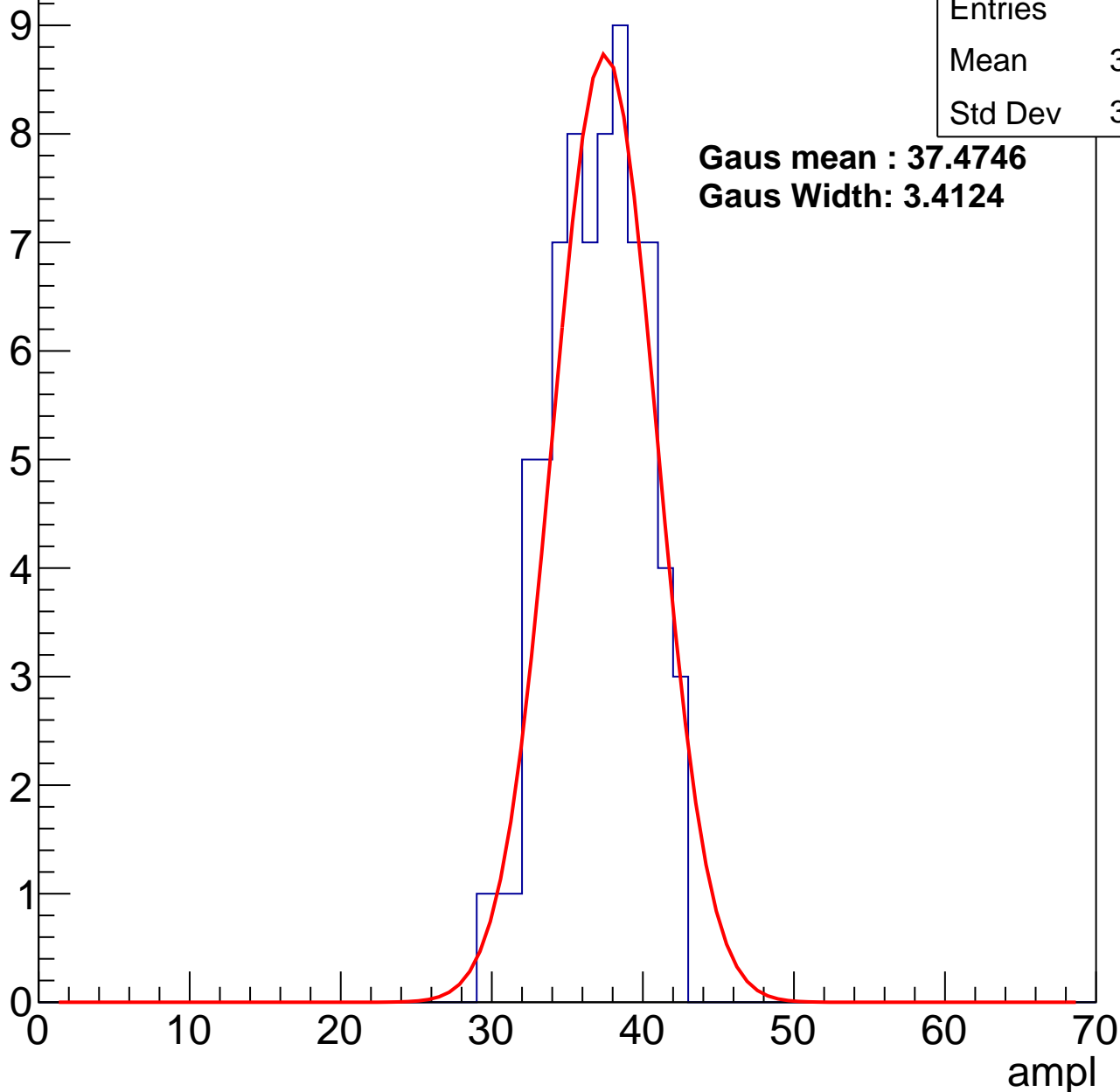
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	36.52
Std Dev	3.048

Gaus mean : 37.4746

Gaus Width: 3.4124



B0L000S, U2-ch30, adc2

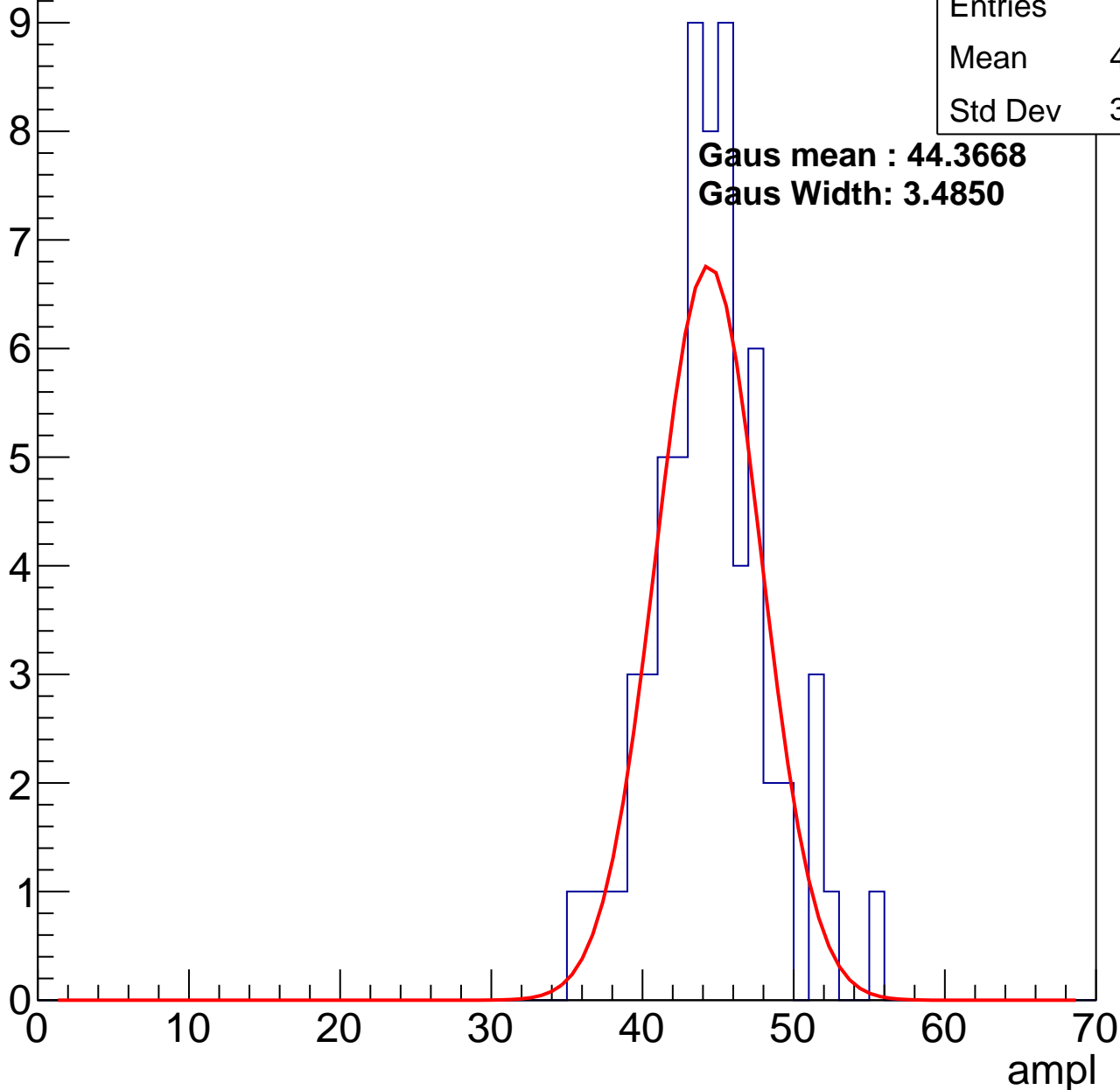
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.03
Std Dev	3.762

Gaus mean : 44.3668

Gaus Width: 3.4850

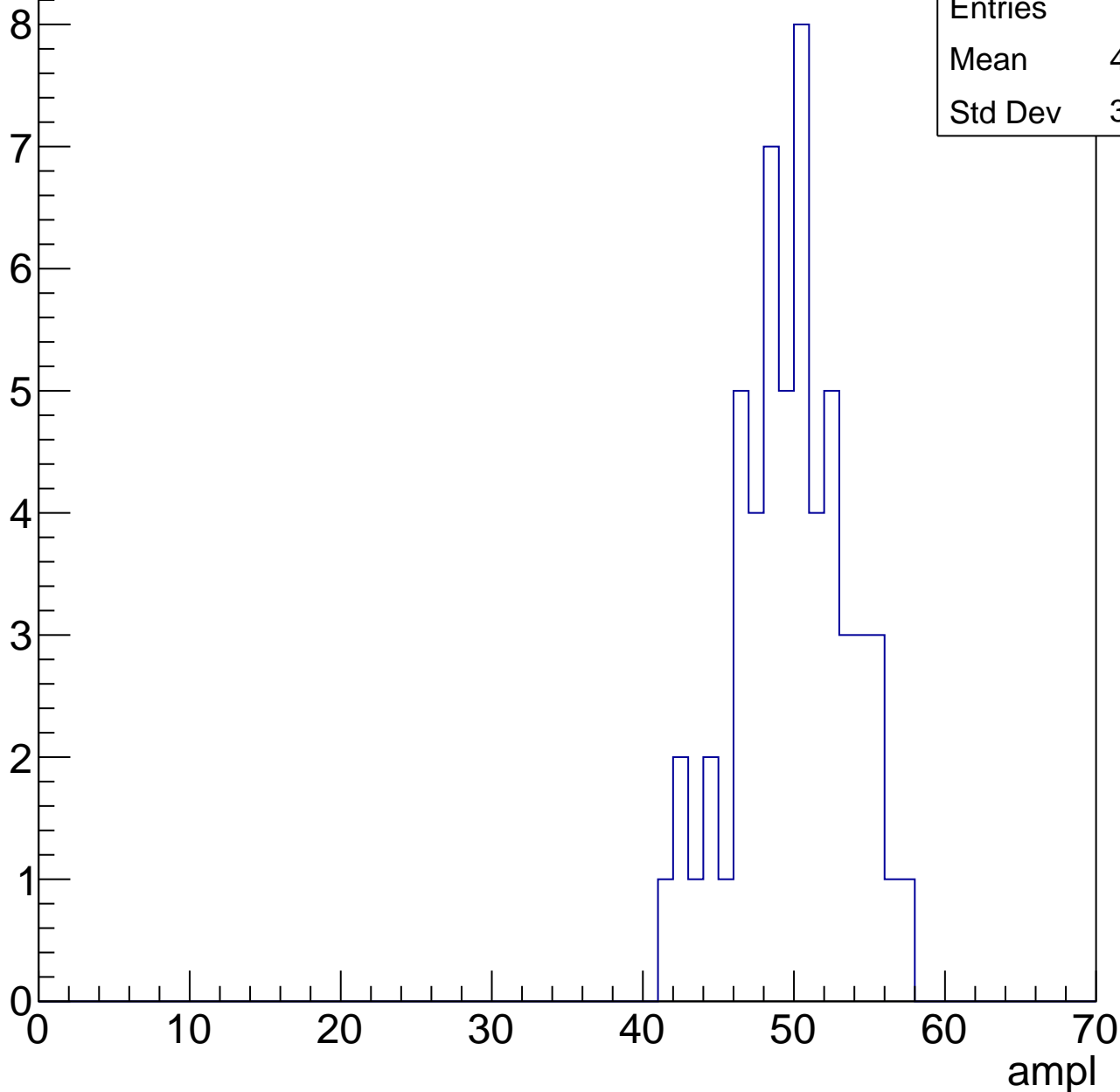


B0L000S, U2-ch30, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	49.34
Std Dev	3.632

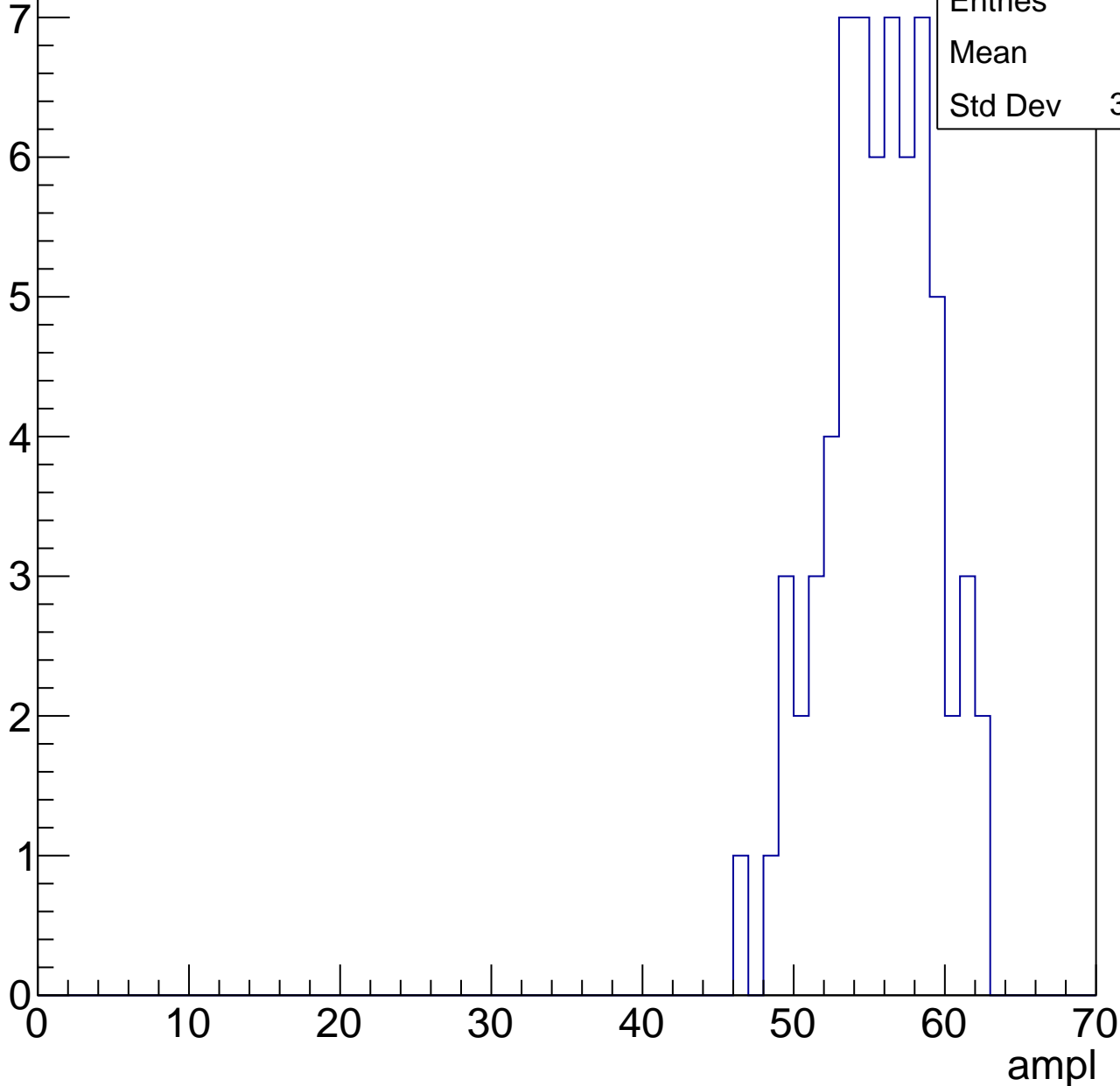


B0L000S, U2-ch30, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	55.2
Std Dev	3.556



B0L000S, U2-ch30, adc5

calib_packv5_042523_0143.root, FC#5, port B1

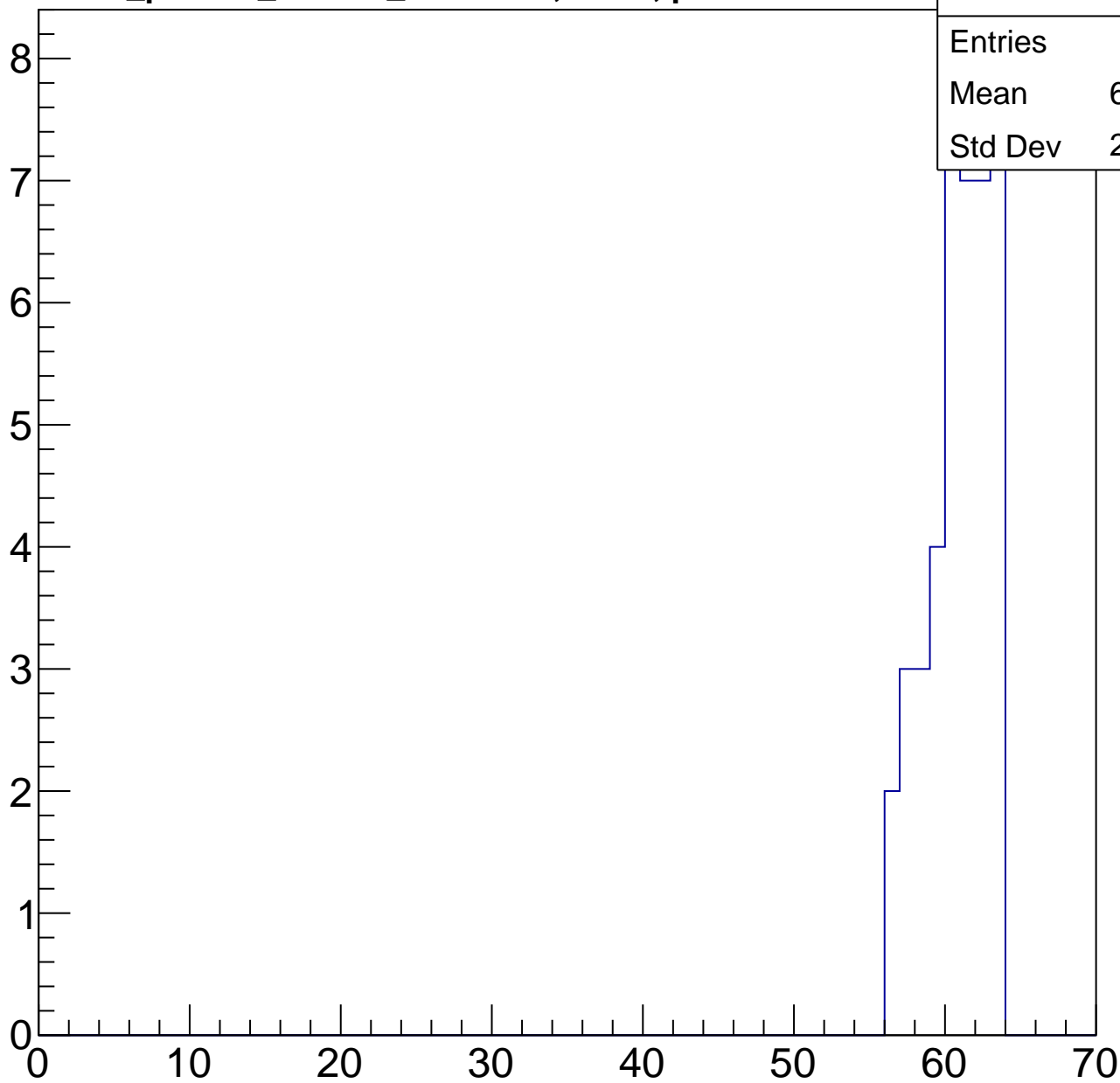
Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.43
Std Dev	2.037

ampl

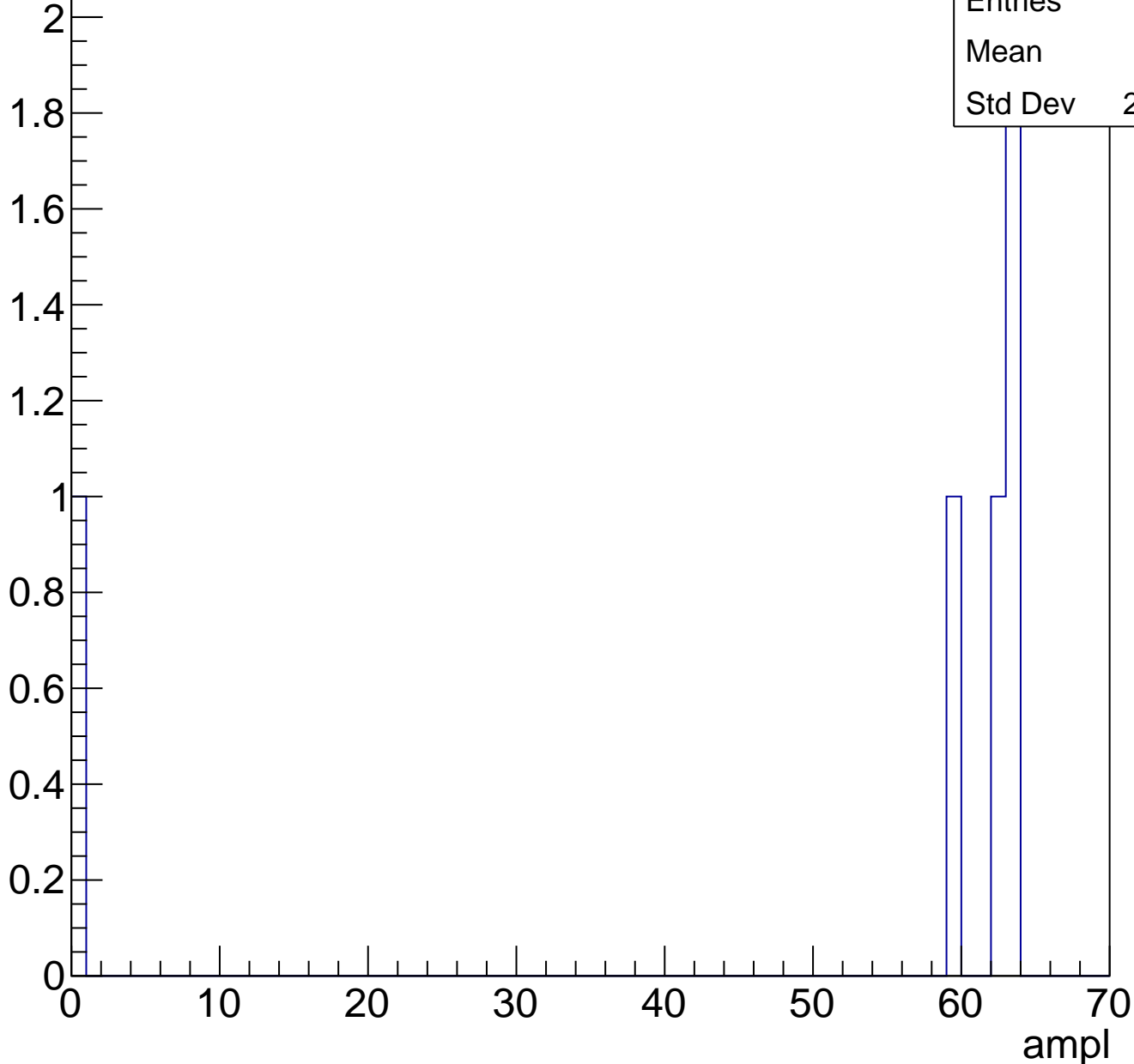
0 10 20 30 40 50 60 70



B0L000S, U2-ch30, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch30, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch31, adc0

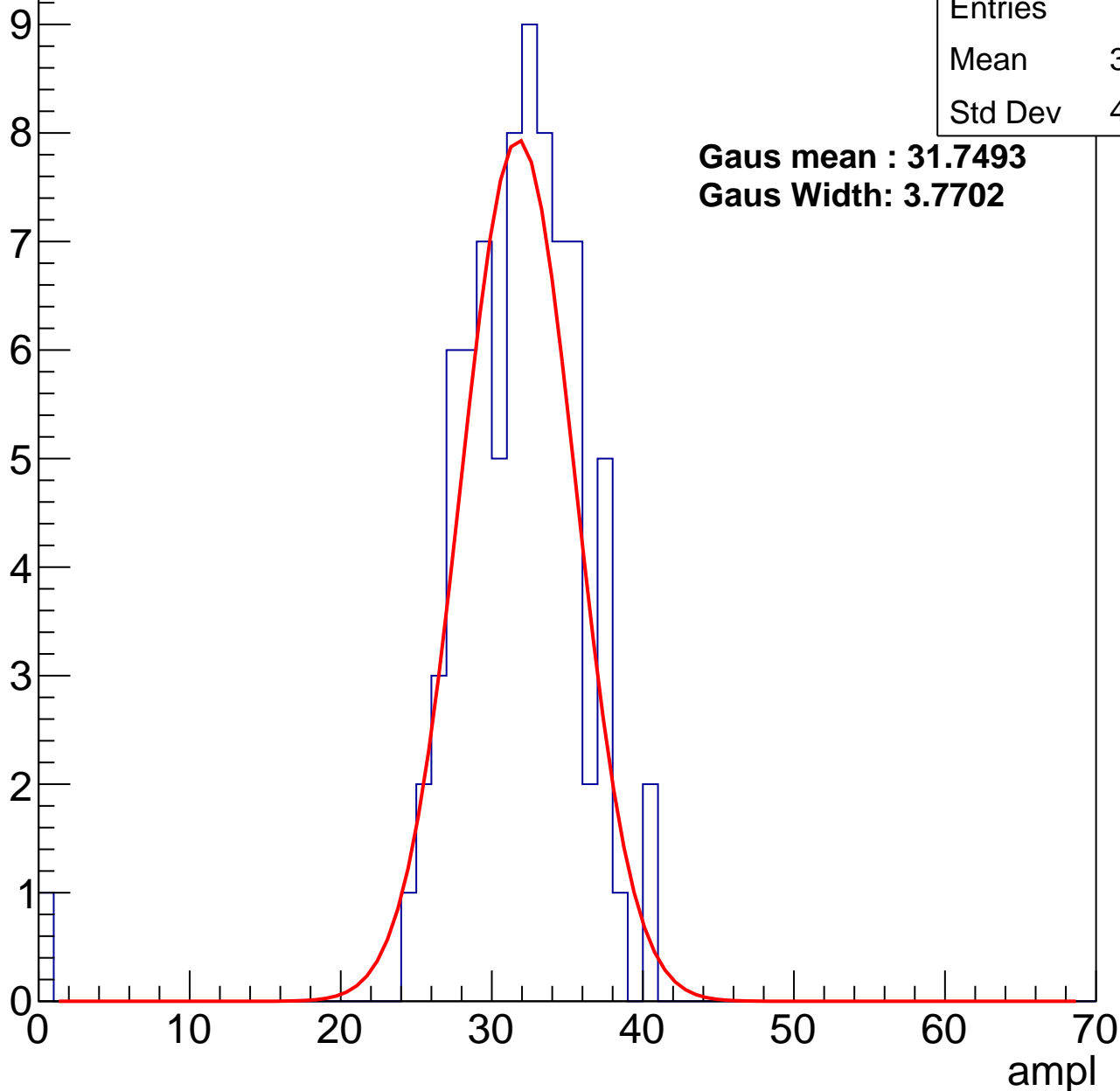
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	31.16
Std Dev	4.994

Gaus mean : 31.7493

Gaus Width: 3.7702



B0L000S, U2-ch31, adc1

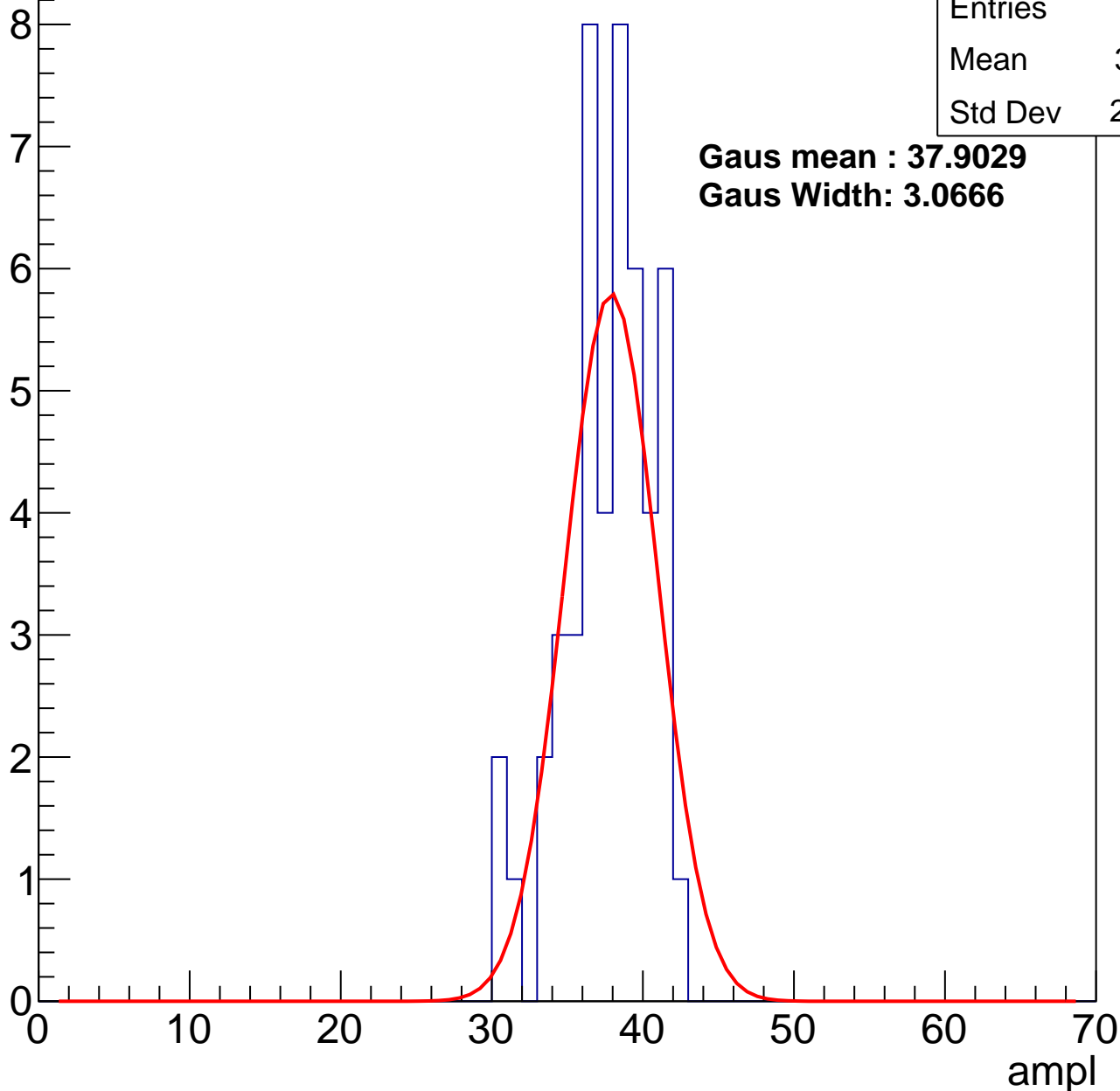
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	37.21
Std Dev	2.886

Gaus mean : 37.9029

Gaus Width: 3.0666



B0L000S, U2-ch31, adc2

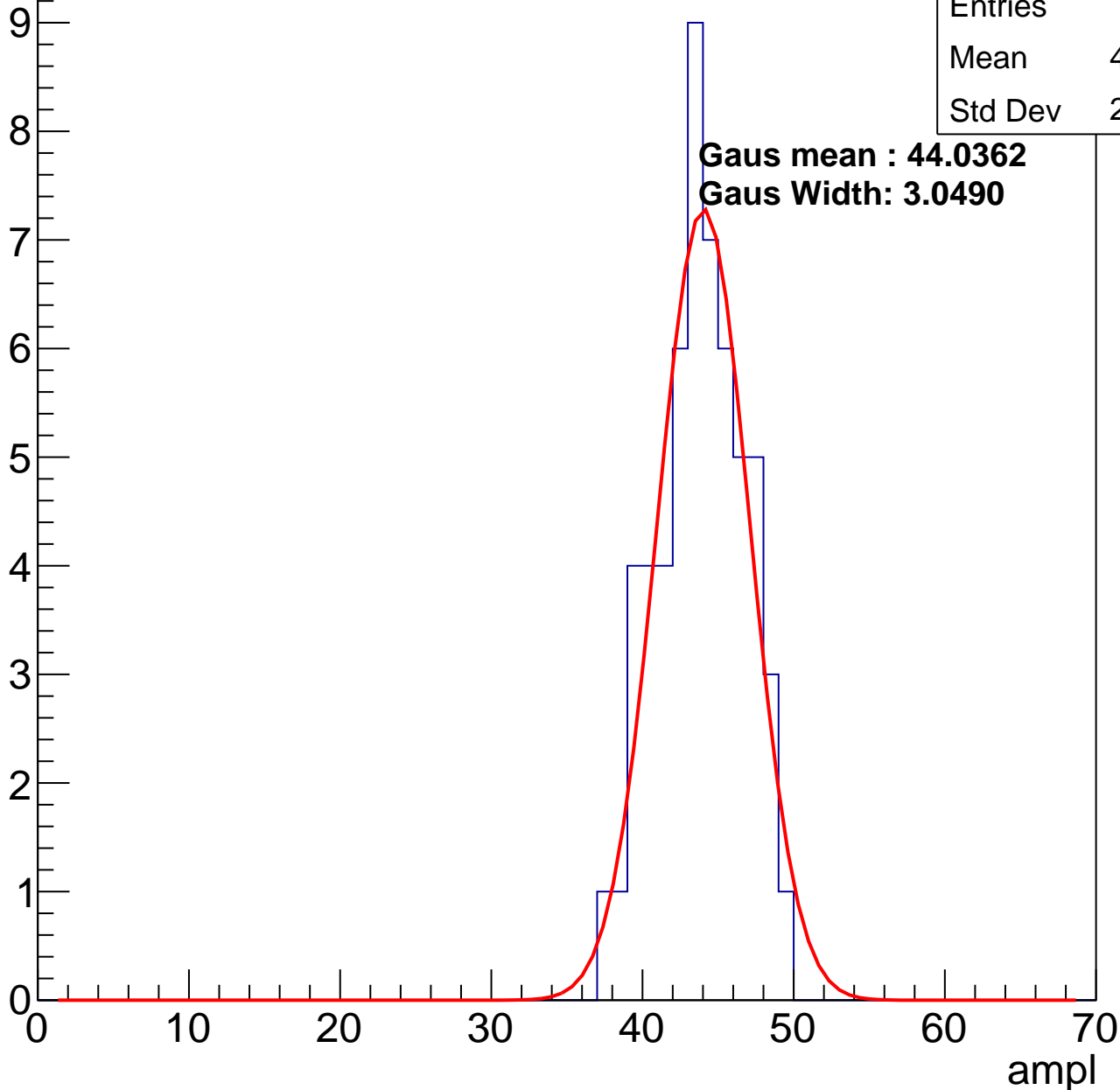
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43.39
Std Dev	2.807

Gaus mean : 44.0362

Gaus Width: 3.0490

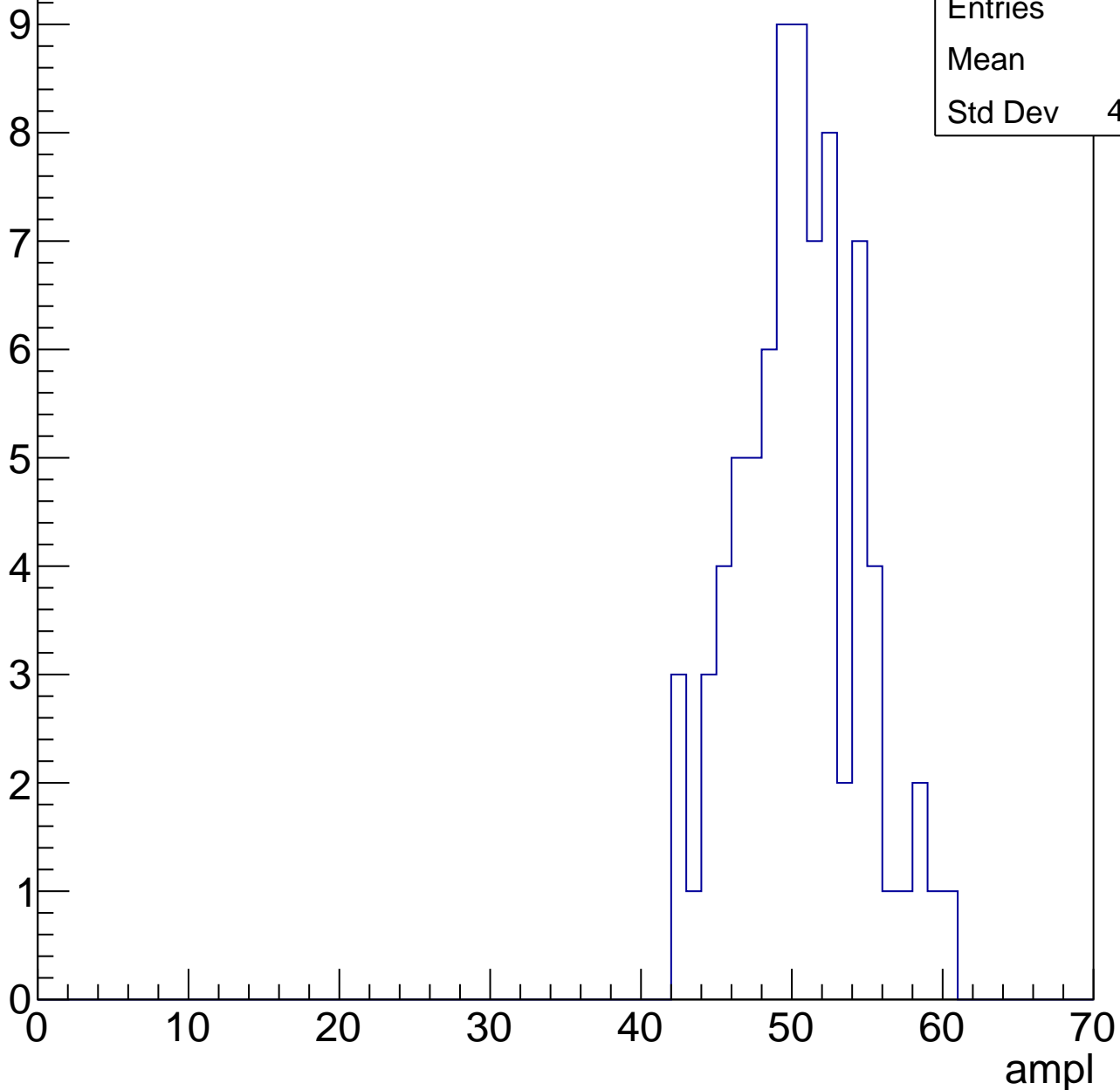


B0L000S, U2-ch31, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	79
Mean	50
Std Dev	4.009

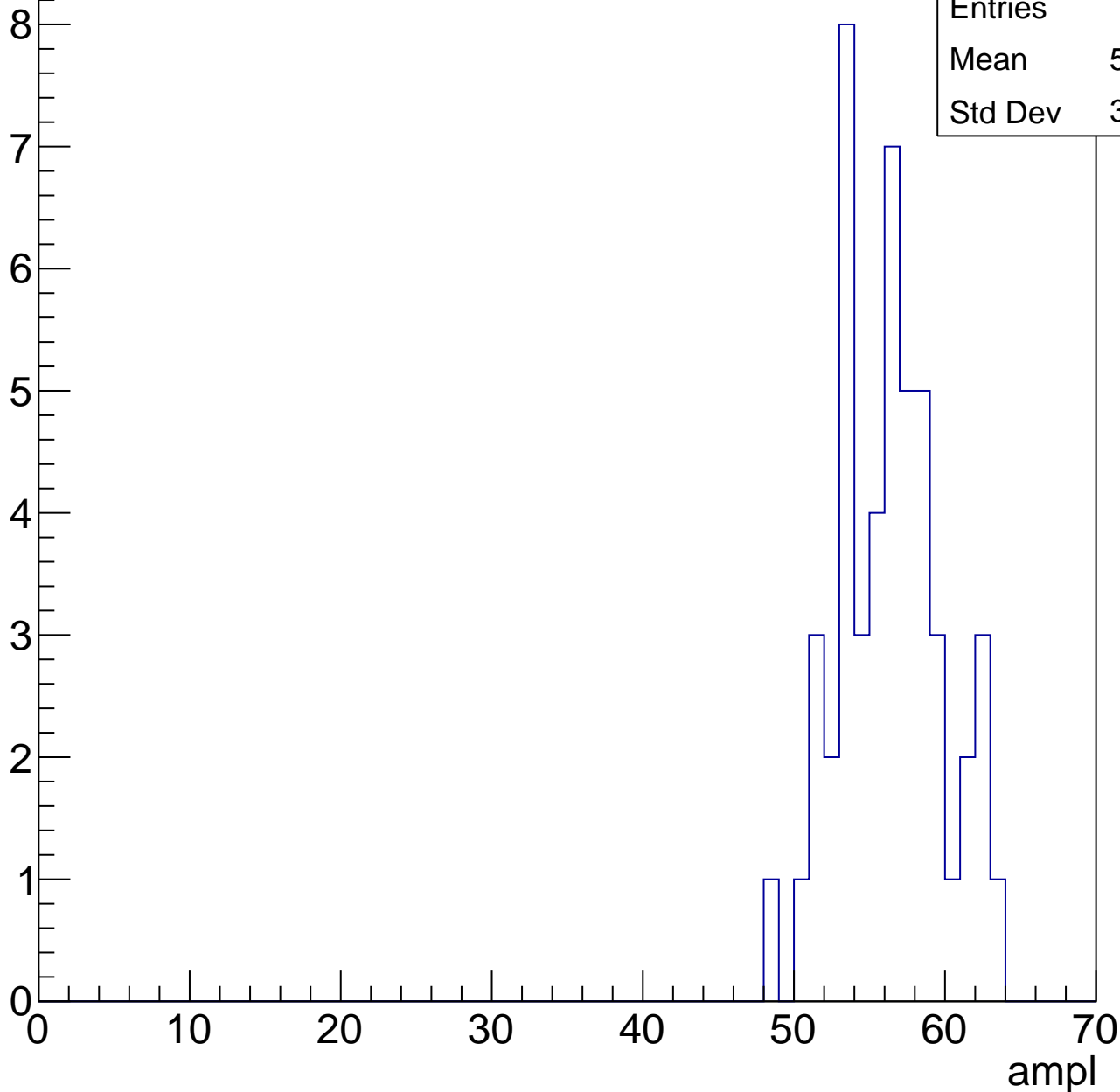


B0L000S, U2-ch31, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.84
Std Dev	3.407

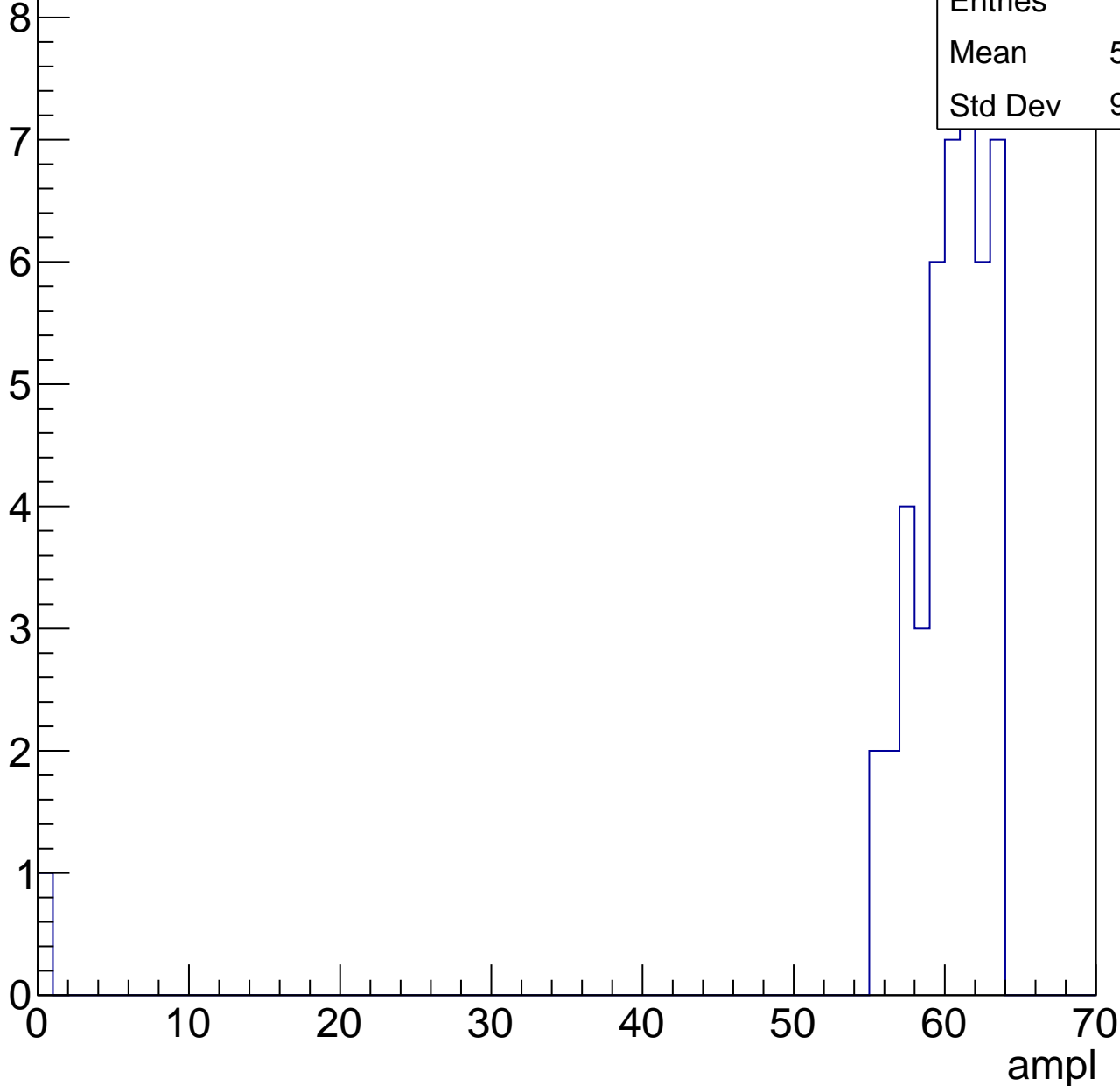


B0L000S, U2-ch31, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.67
Std Dev	9.029



B0L000S, U2-ch31, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch31, adc7

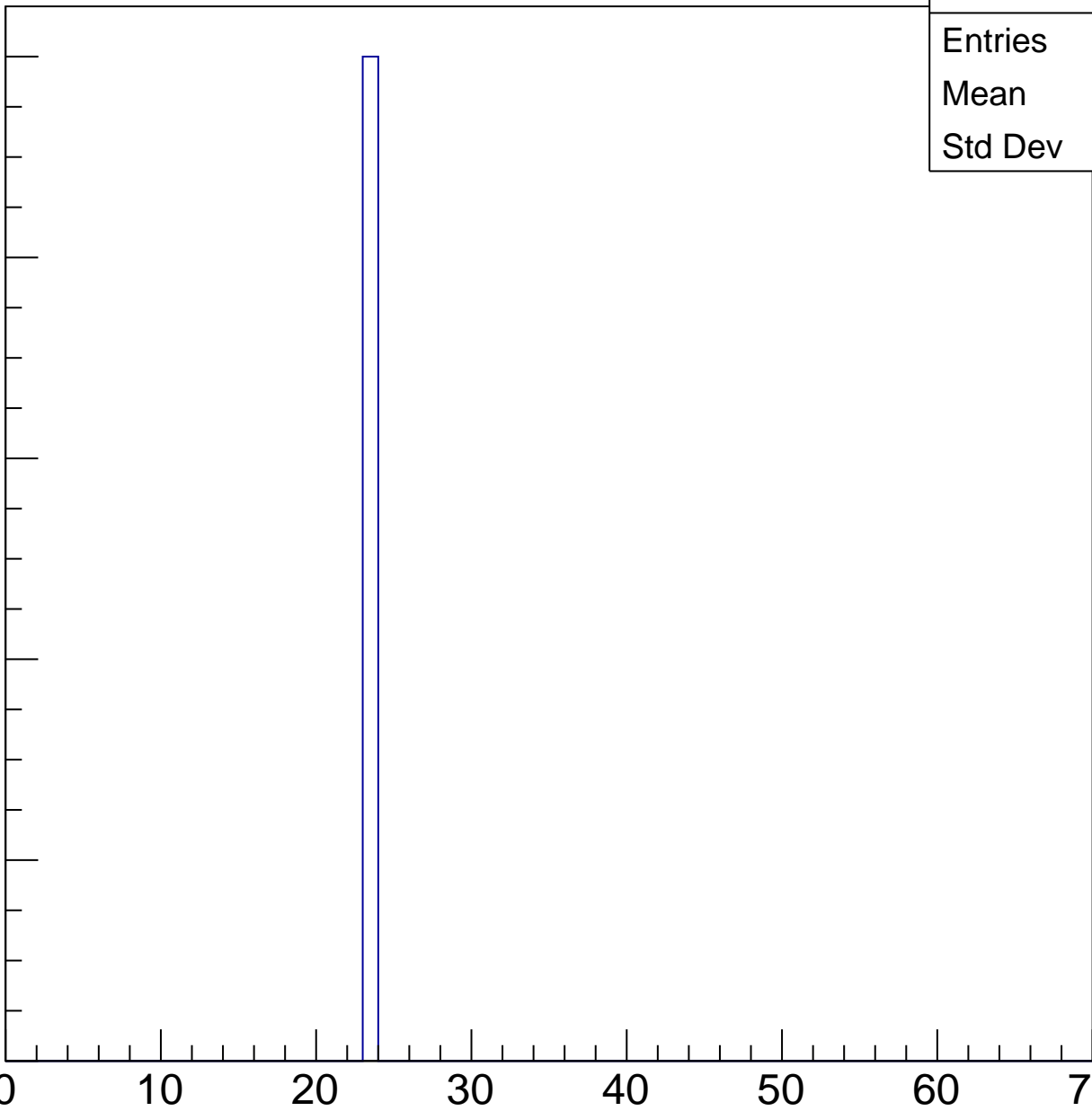
calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B0L000S, U2-ch32, adc0

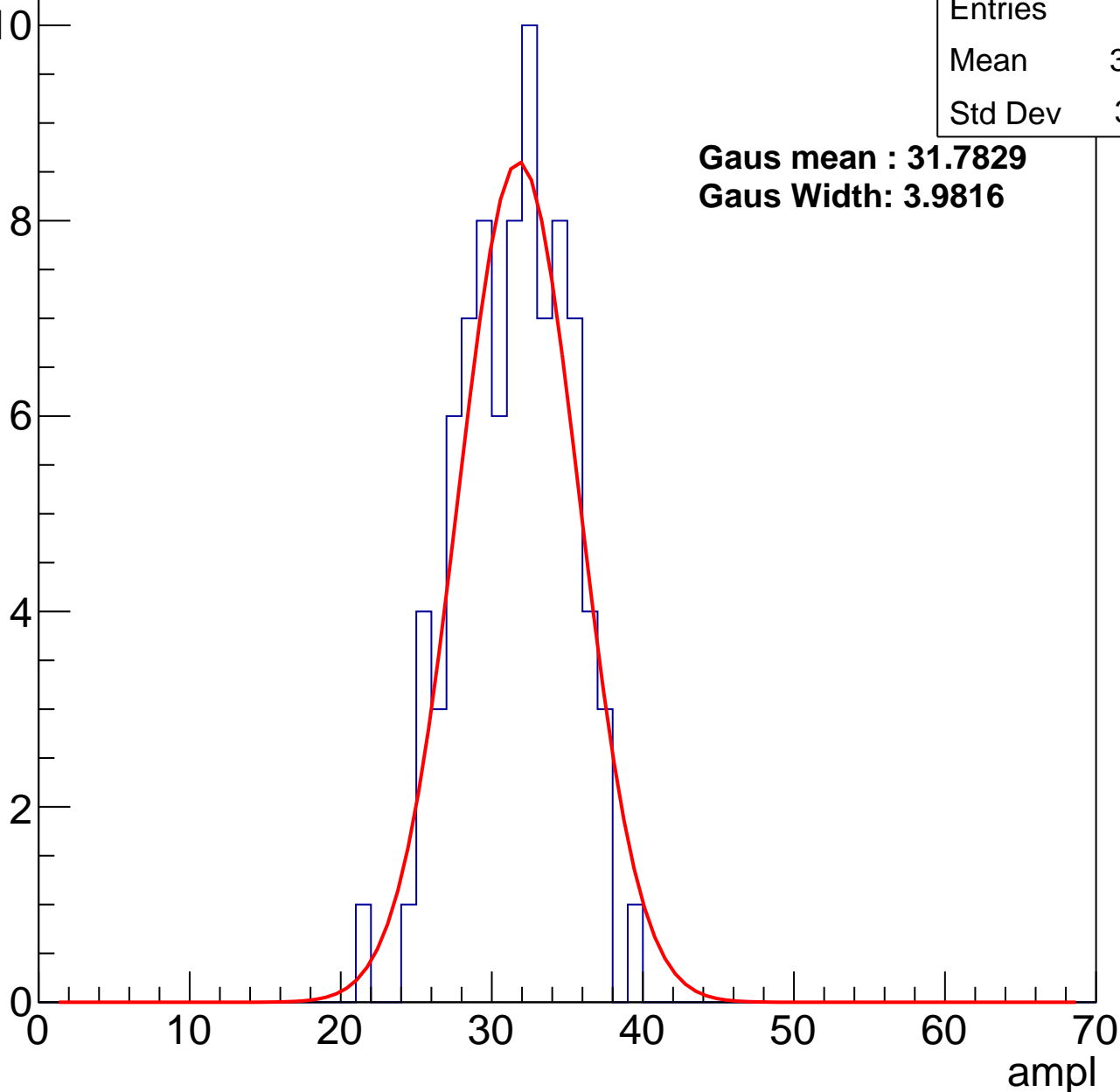
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	84
Mean	30.99
Std Dev	3.551

Gaus mean : 31.7829

Gaus Width: 3.9816



B0L000S, U2-ch32, adc1

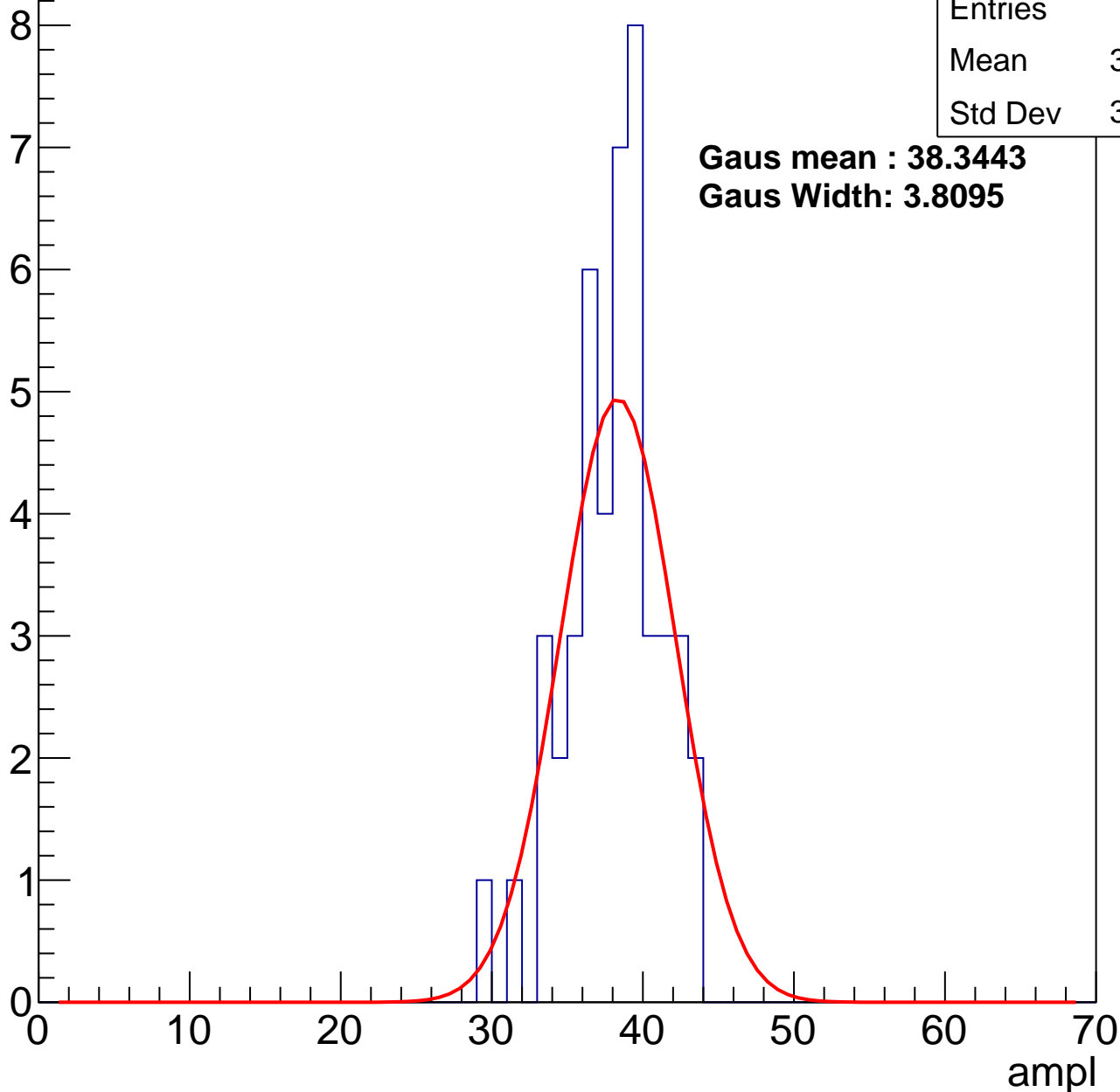
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	37.59
Std Dev	3.054

Gaus mean : 38.3443

Gaus Width: 3.8095



B0L000S, U2-ch32, adc2

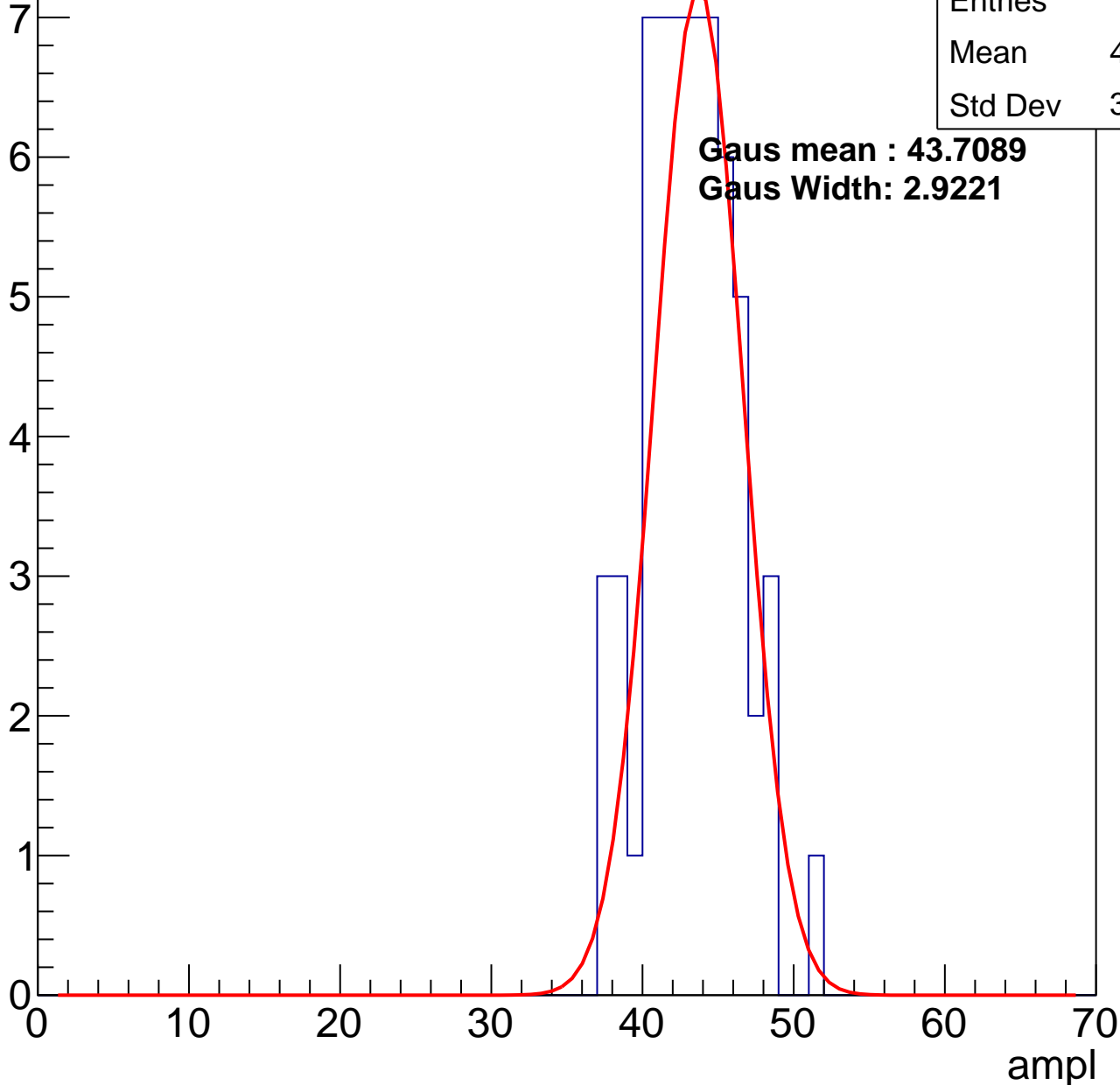
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.76
Std Dev	3.027

Gaus mean : 43.7089

Gaus Width: 2.9221



B0L000S, U2-ch32, adc3

calib_packv5_042523_0143.root, FC#5, port B1

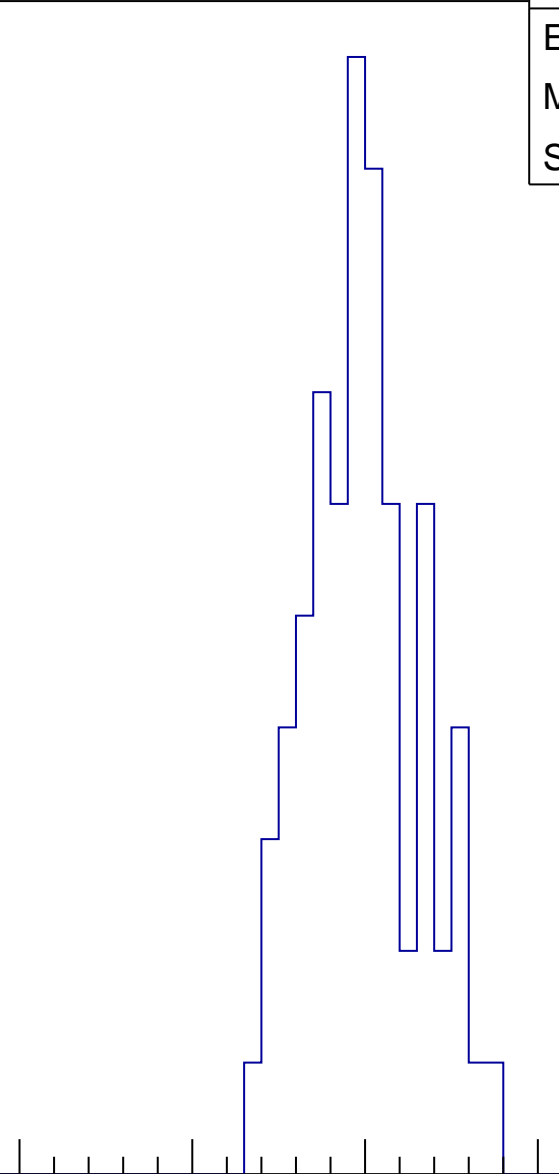
Entries	67
Mean	49.42
Std Dev	3.224

Entry

10
8
6
4
2
0

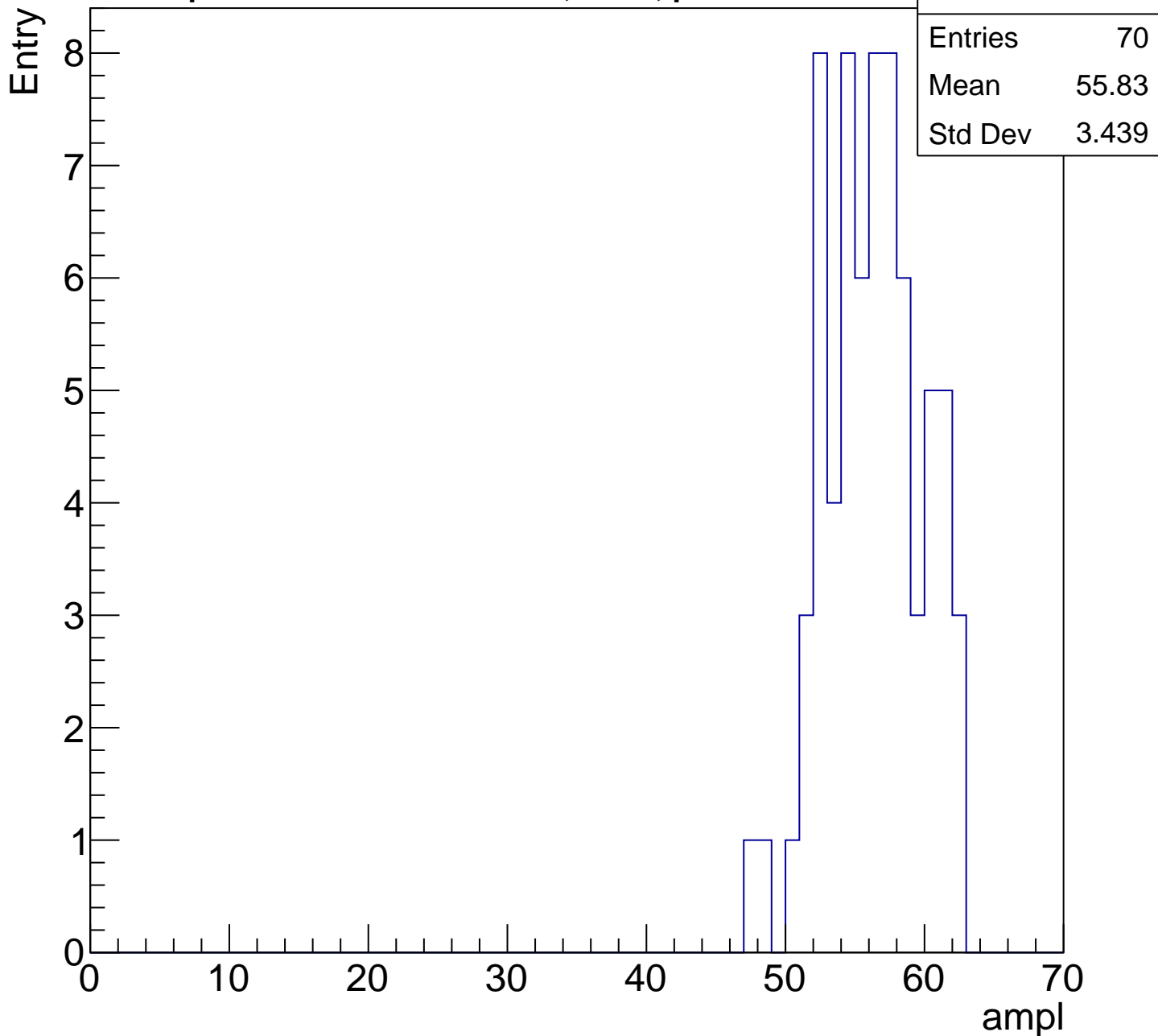
0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch32, adc4

calib_packv5_042523_0143.root, FC#5, port B1

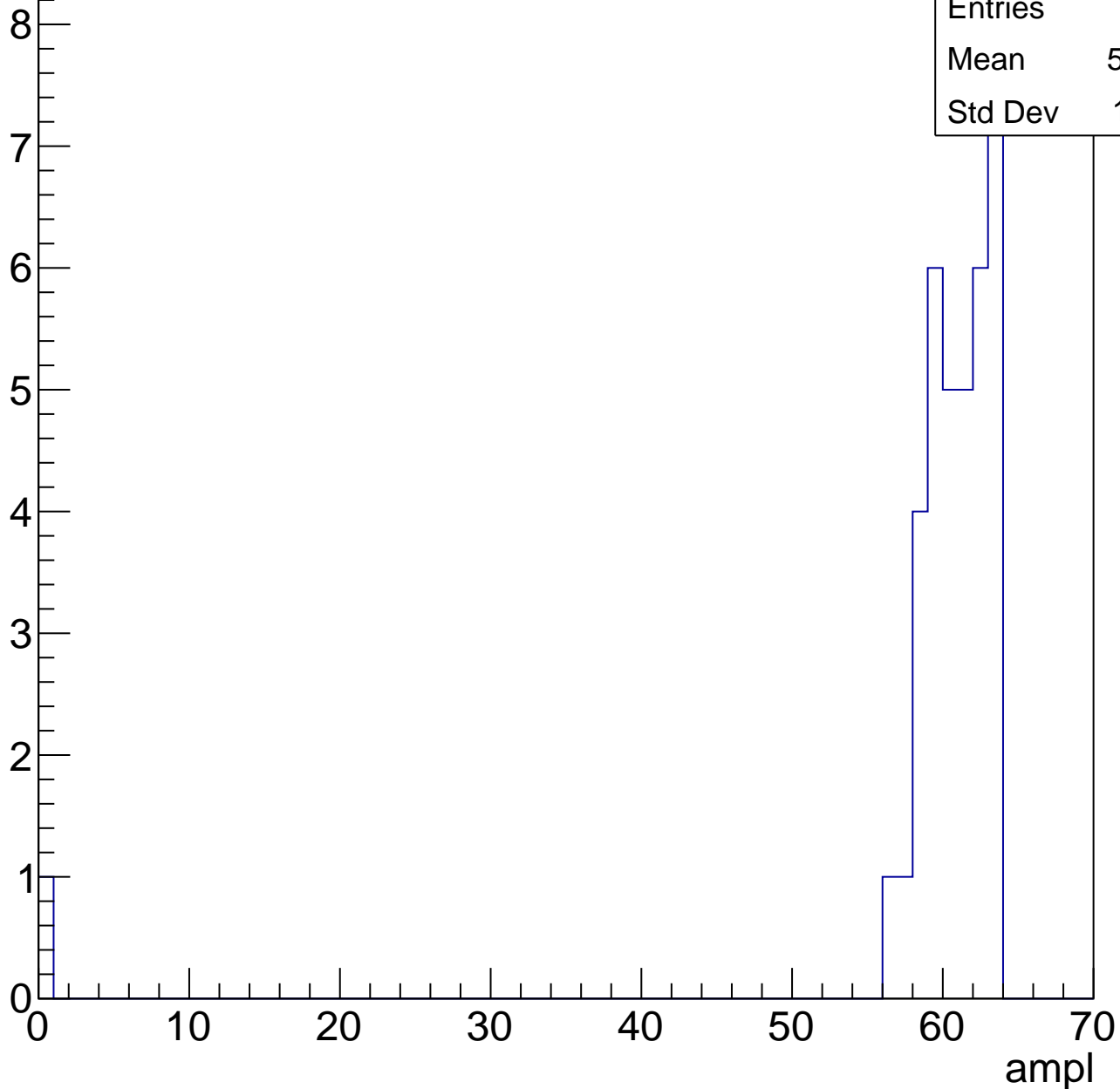


B0L000S, U2-ch32, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

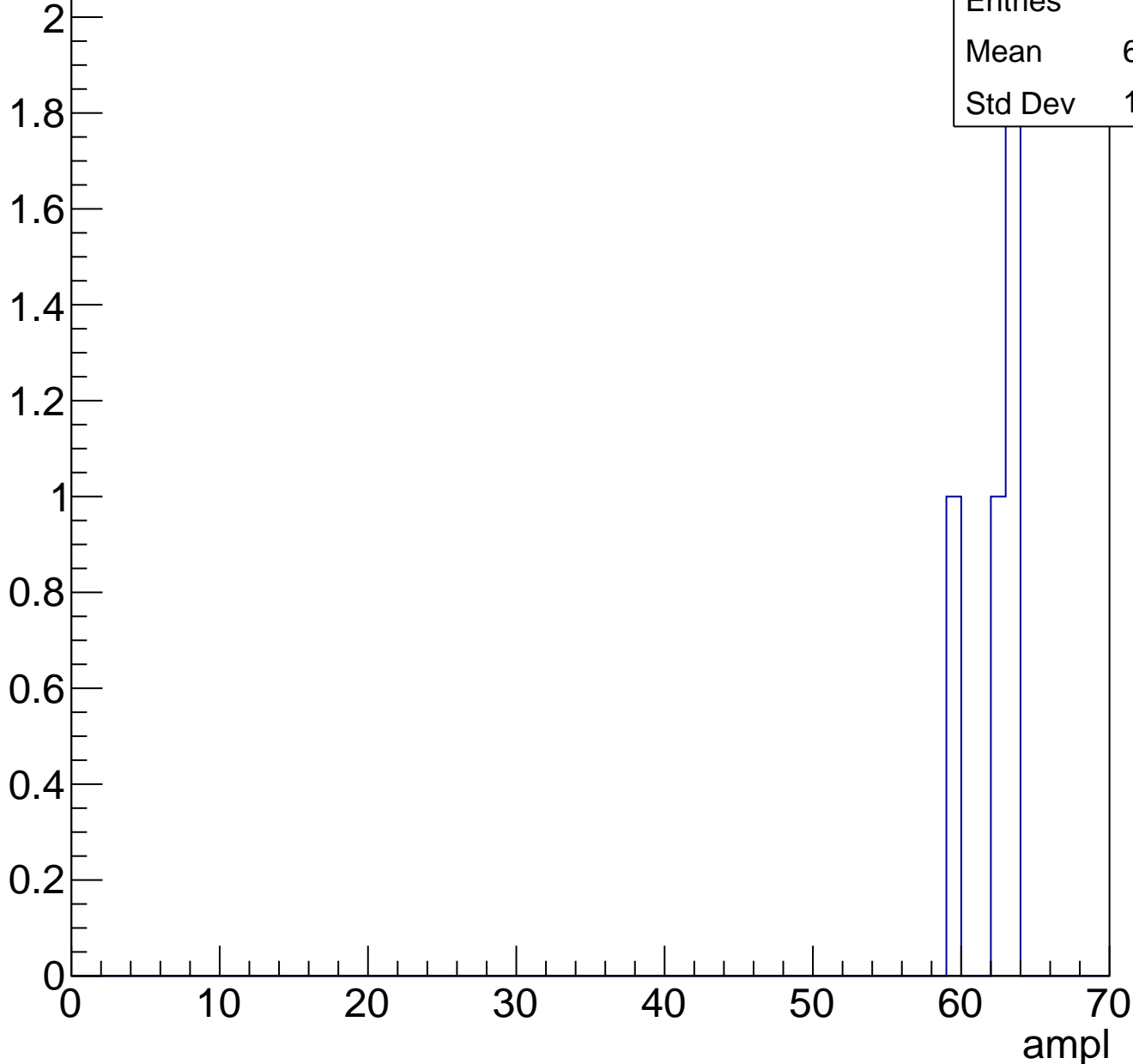
Entries	37
Mean	58.92
Std Dev	10.01



B0L000S, U2-ch32, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch32, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch33, adc0

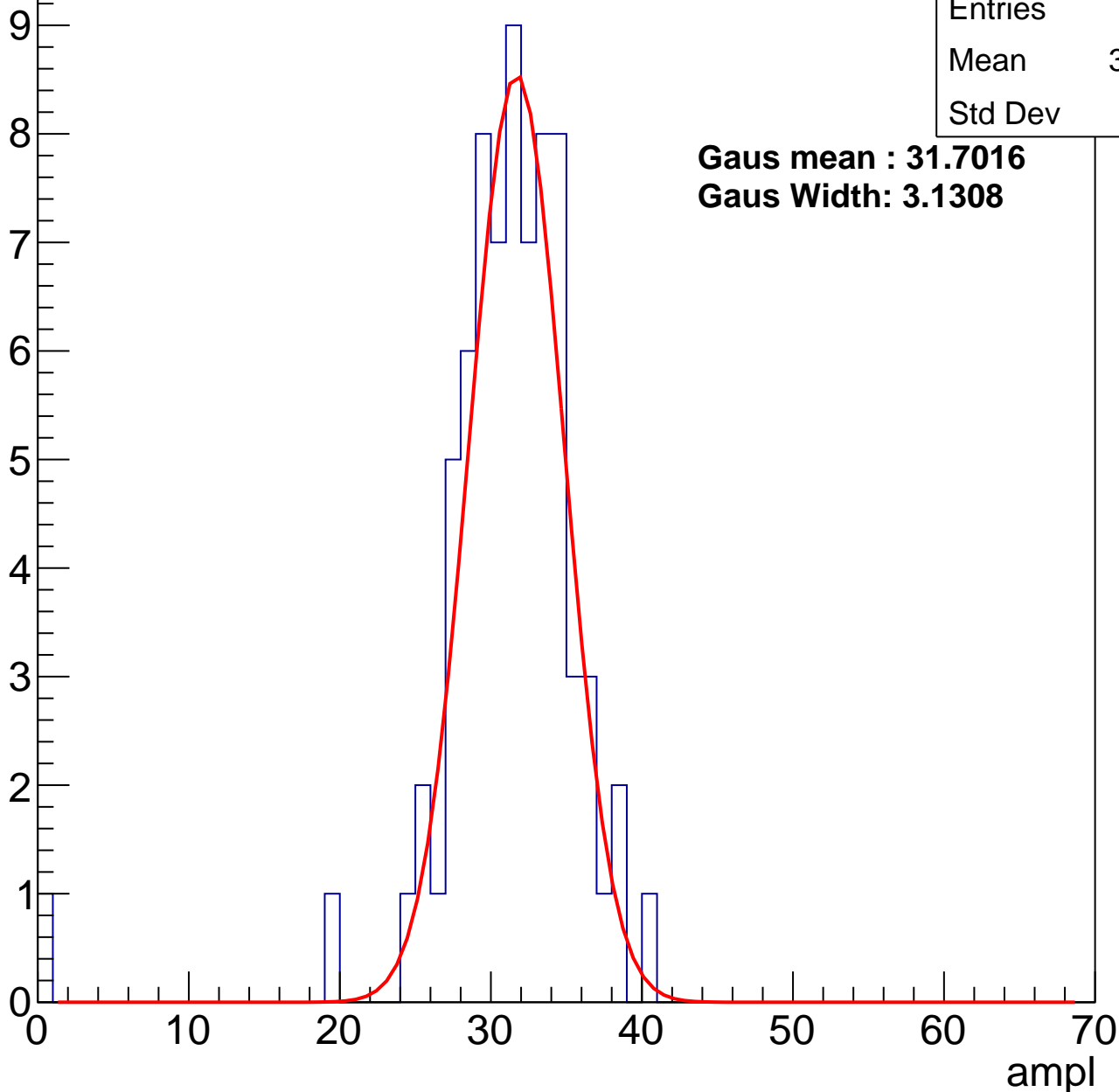
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	30.66
Std Dev	5.02

Gaus mean : 31.7016

Gaus Width: 3.1308



B0L000S, U2-ch33, adc1

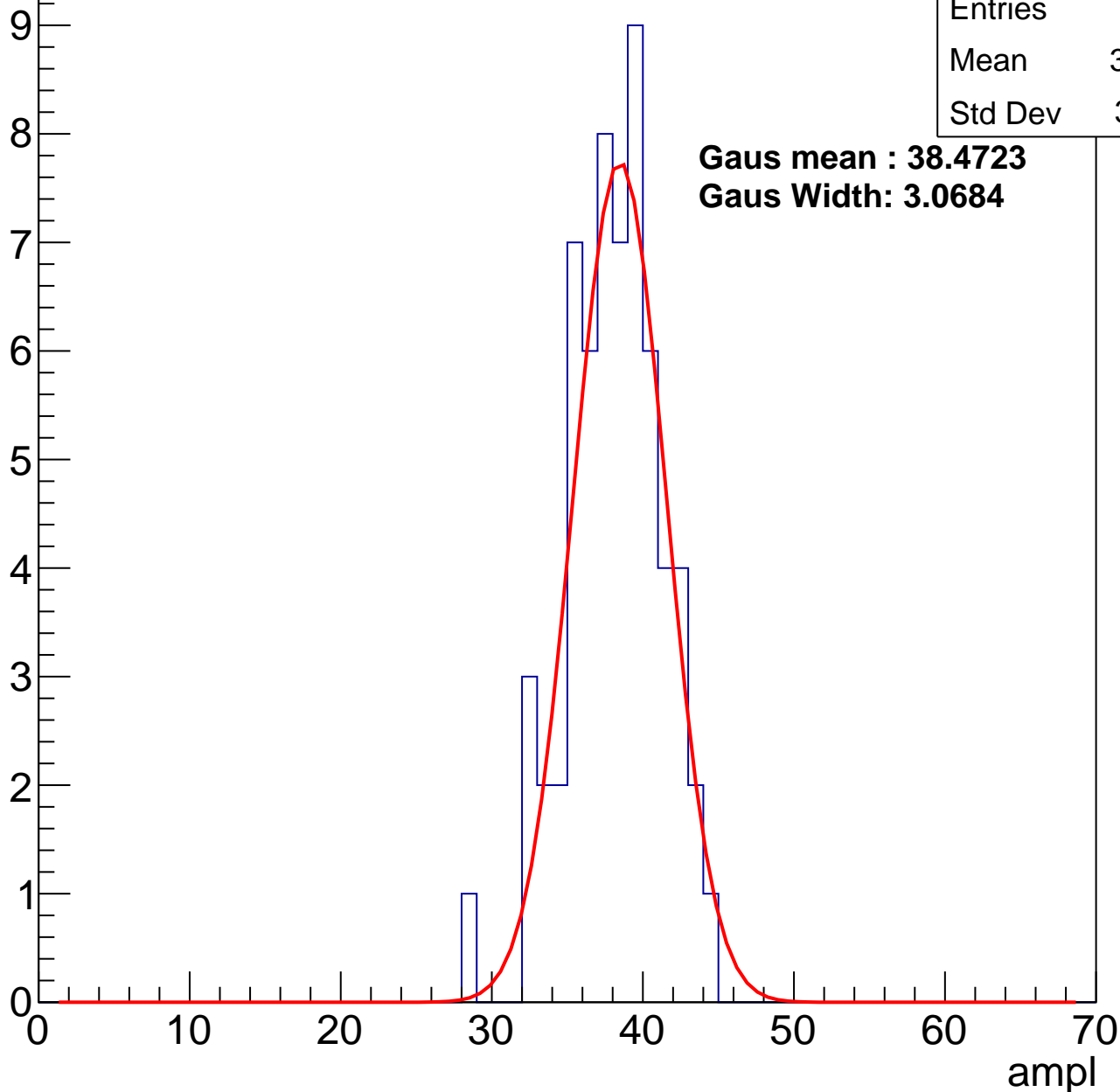
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.65
Std Dev	3.101

Gaus mean : 38.4723

Gaus Width: 3.0684



B0L000S, U2-ch33, adc2

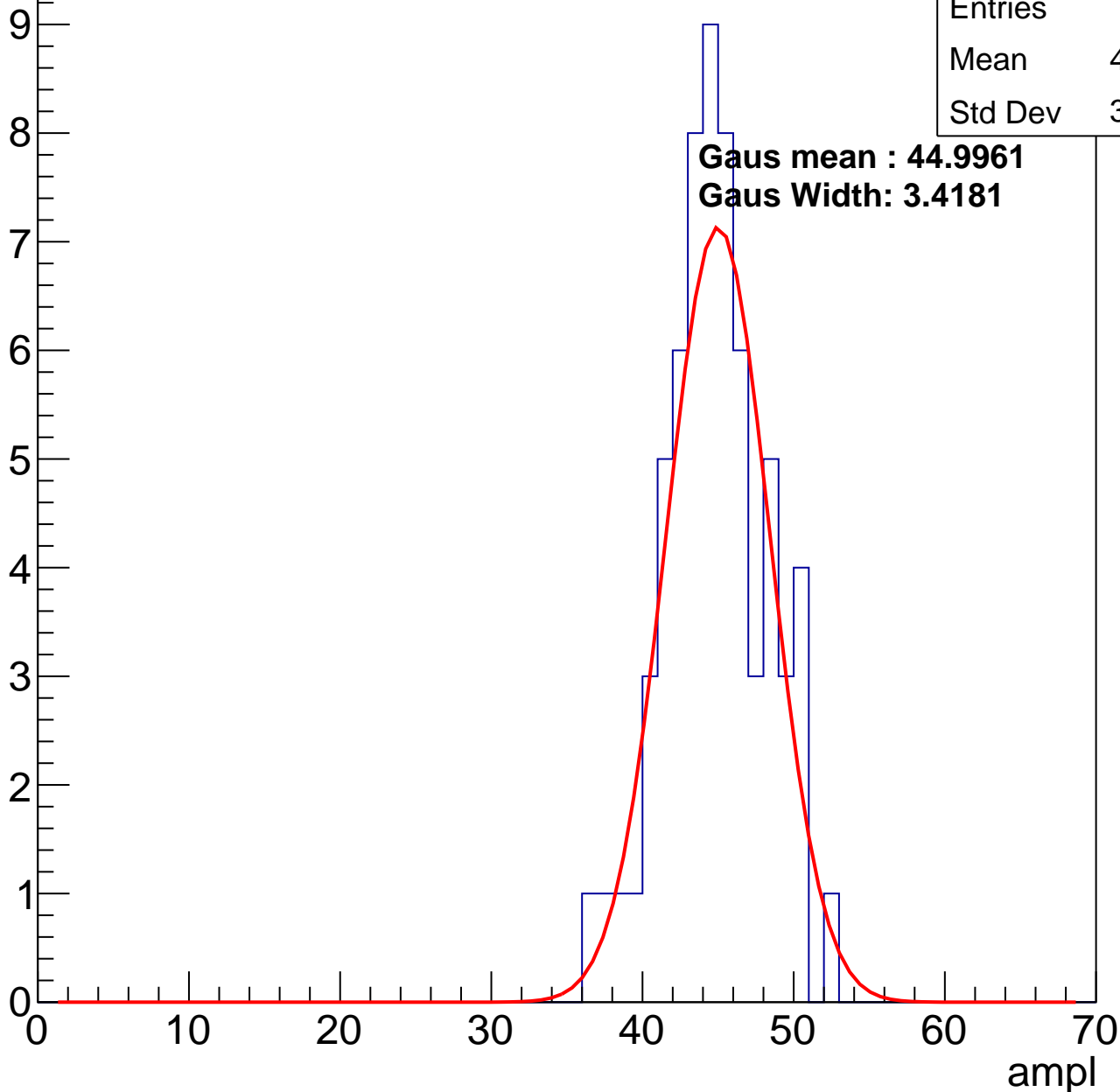
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.35
Std Dev	3.312

Gaus mean : 44.9961

Gaus Width: 3.4181

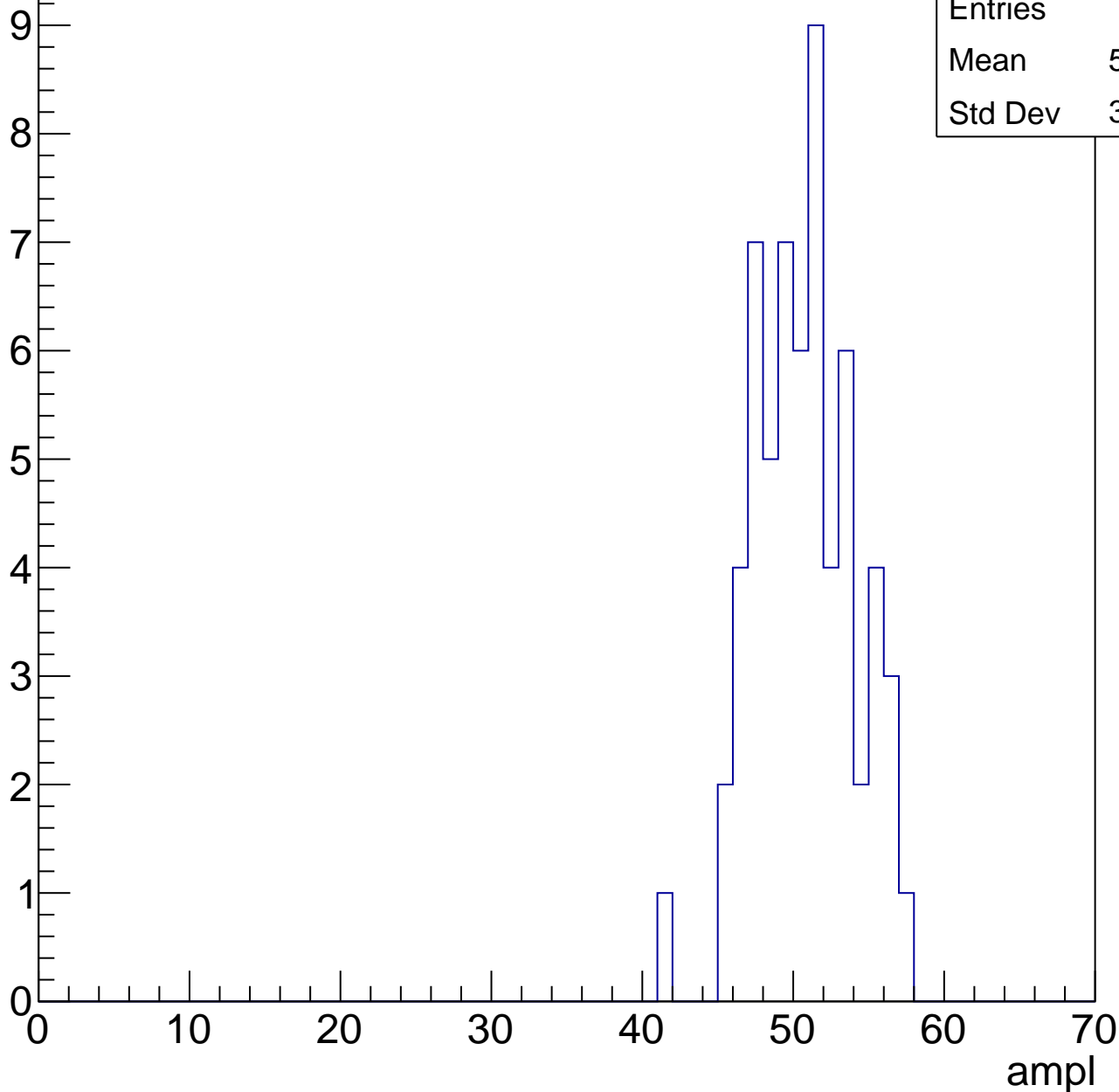


B0L000S, U2-ch33, adc3

calib_packv5_042523_0143.root, FC#5, port B1

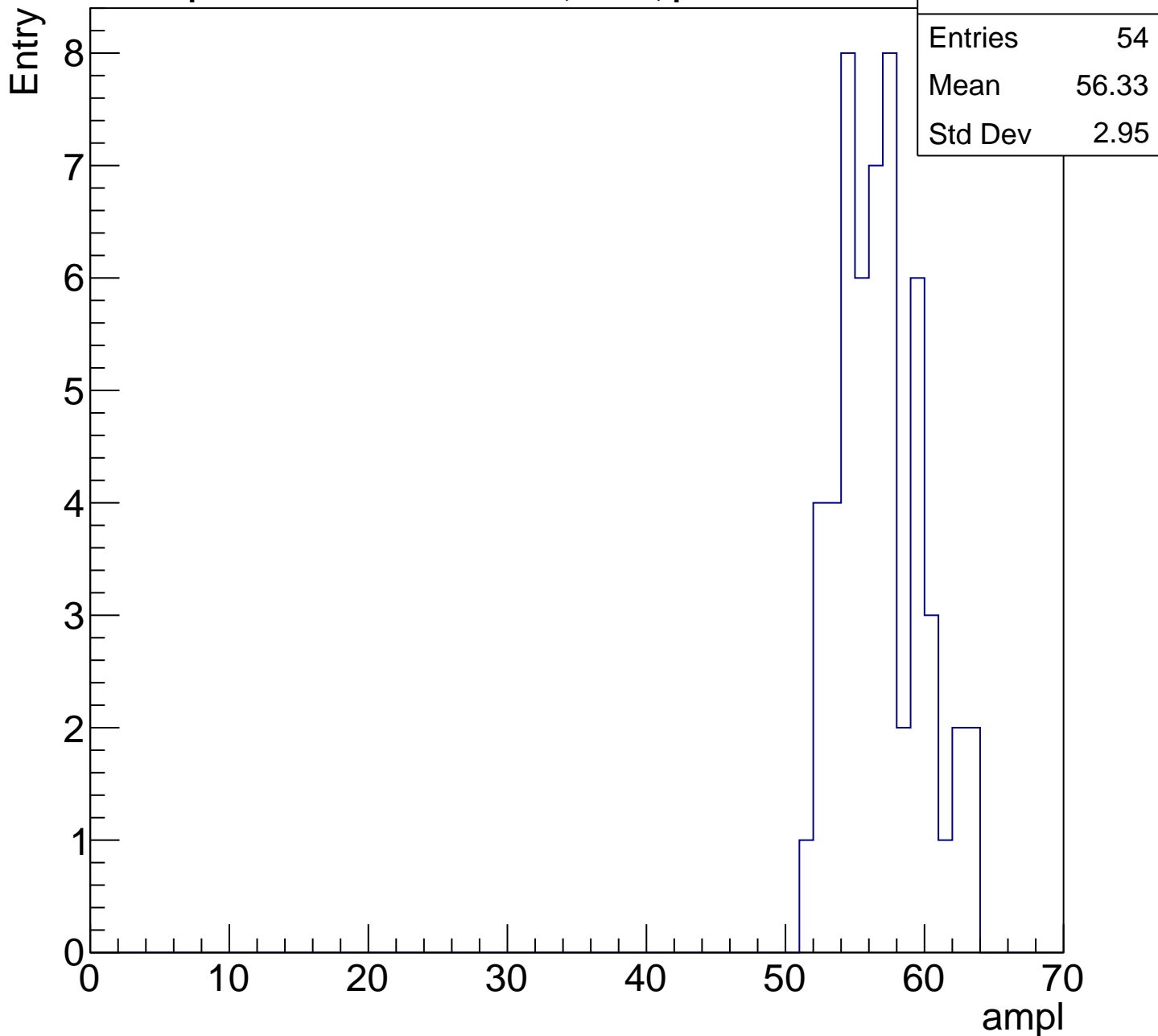
Entry

Entries	61
Mean	50.25
Std Dev	3.263



B0L000S, U2-ch33, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch33, adc5

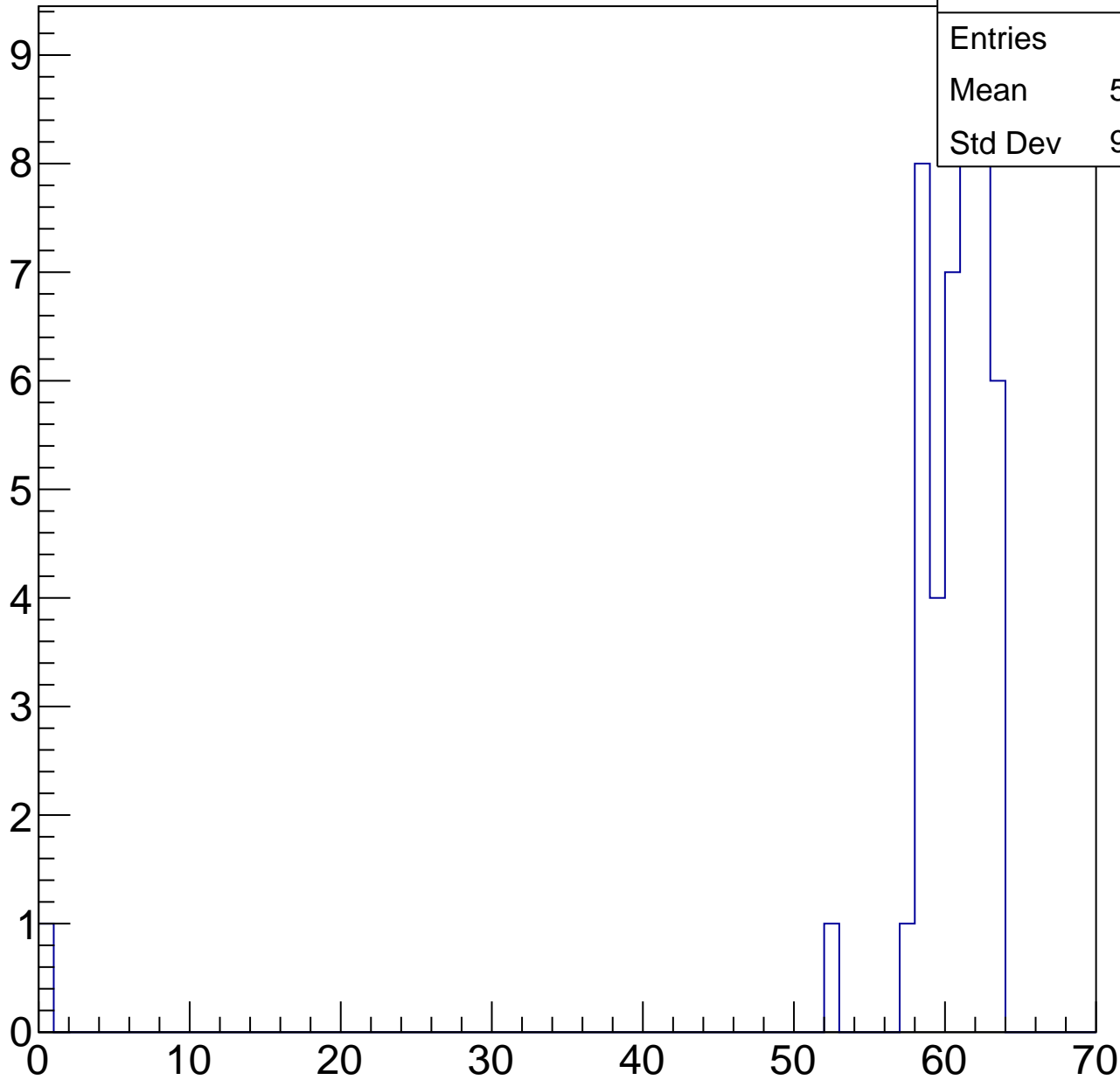
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.93
Std Dev	9.132

ampl



B0L000S, U2-ch33, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch33, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch34, adc0

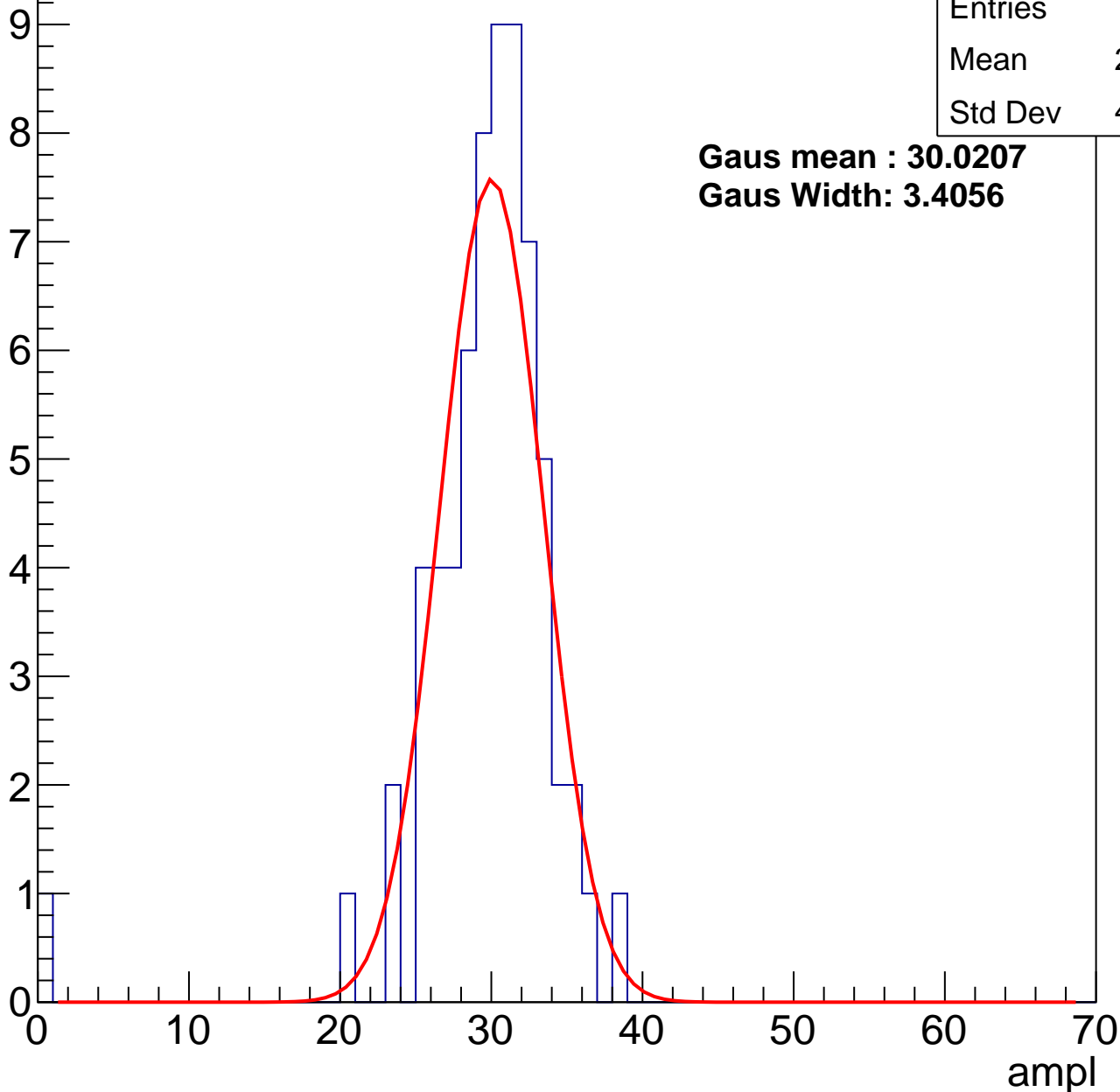
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.21
Std Dev	4.851

Gaus mean : 30.0207

Gaus Width: 3.4056



B0L000S, U2-ch34, adc1

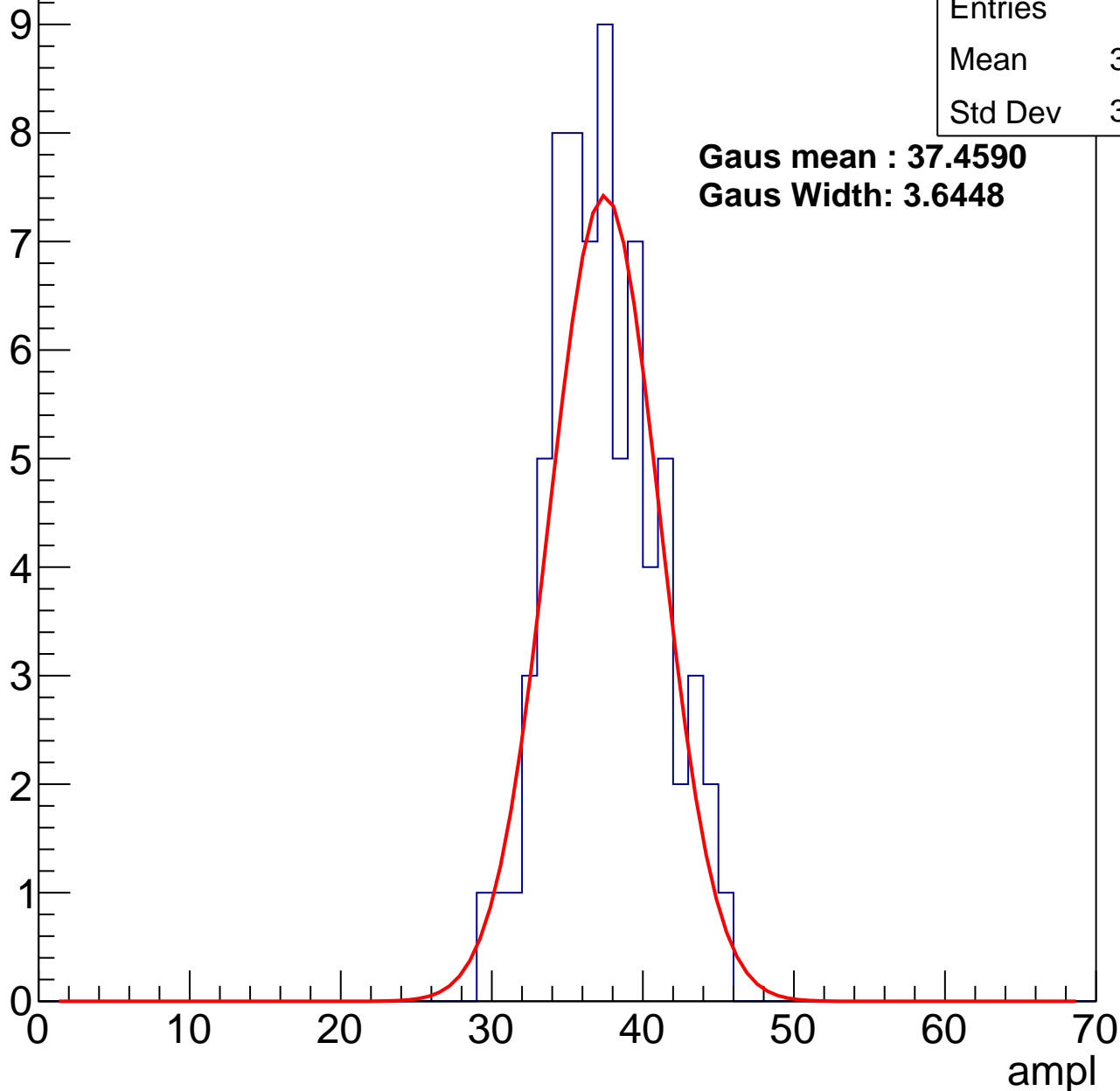
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.97
Std Dev	3.504

Gaus mean : 37.4590

Gaus Width: 3.6448



B0L000S, U2-ch34, adc2

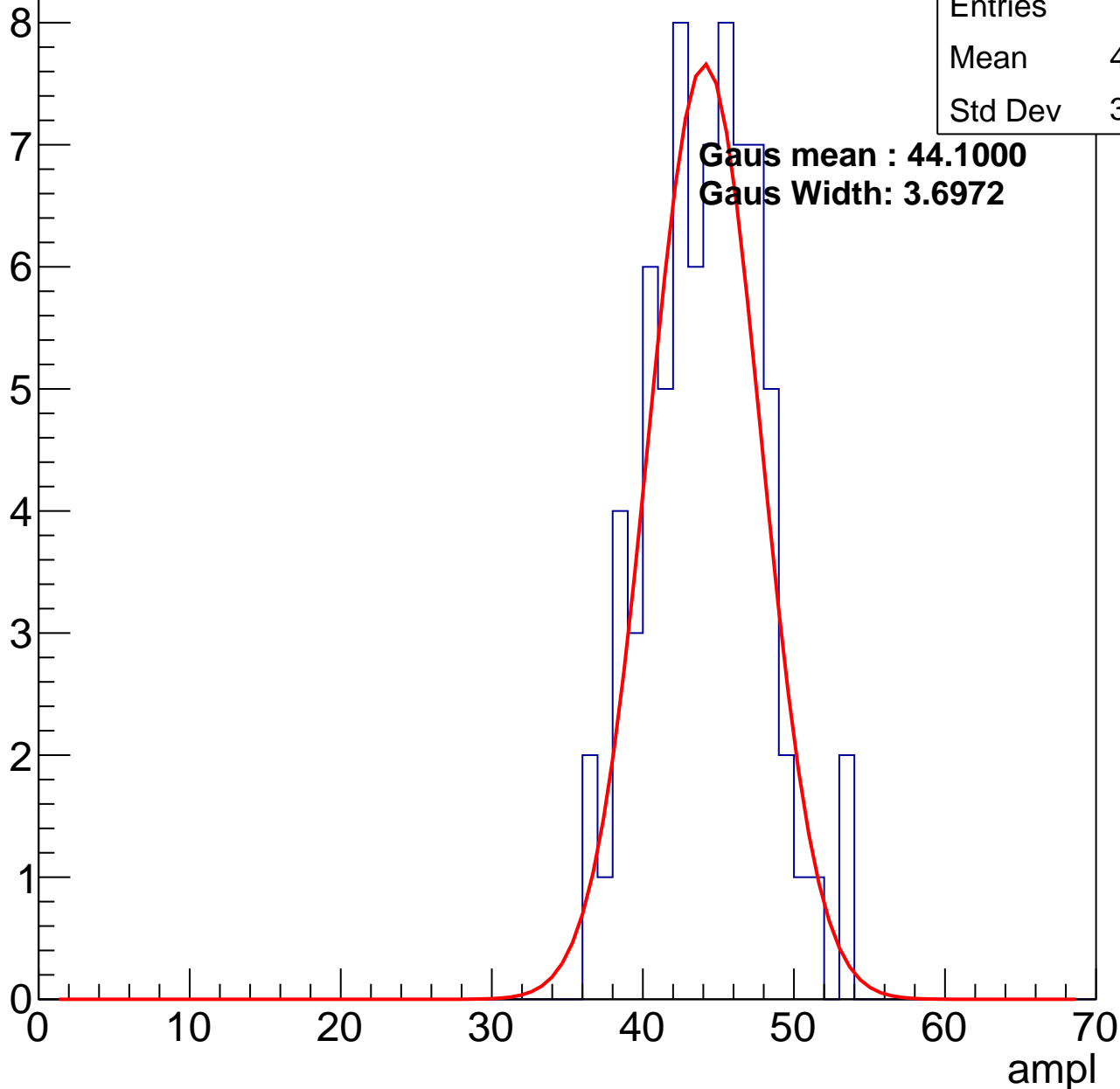
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	43.75
Std Dev	3.735

Gaus mean : 44.1000

Gaus Width: 3.6972

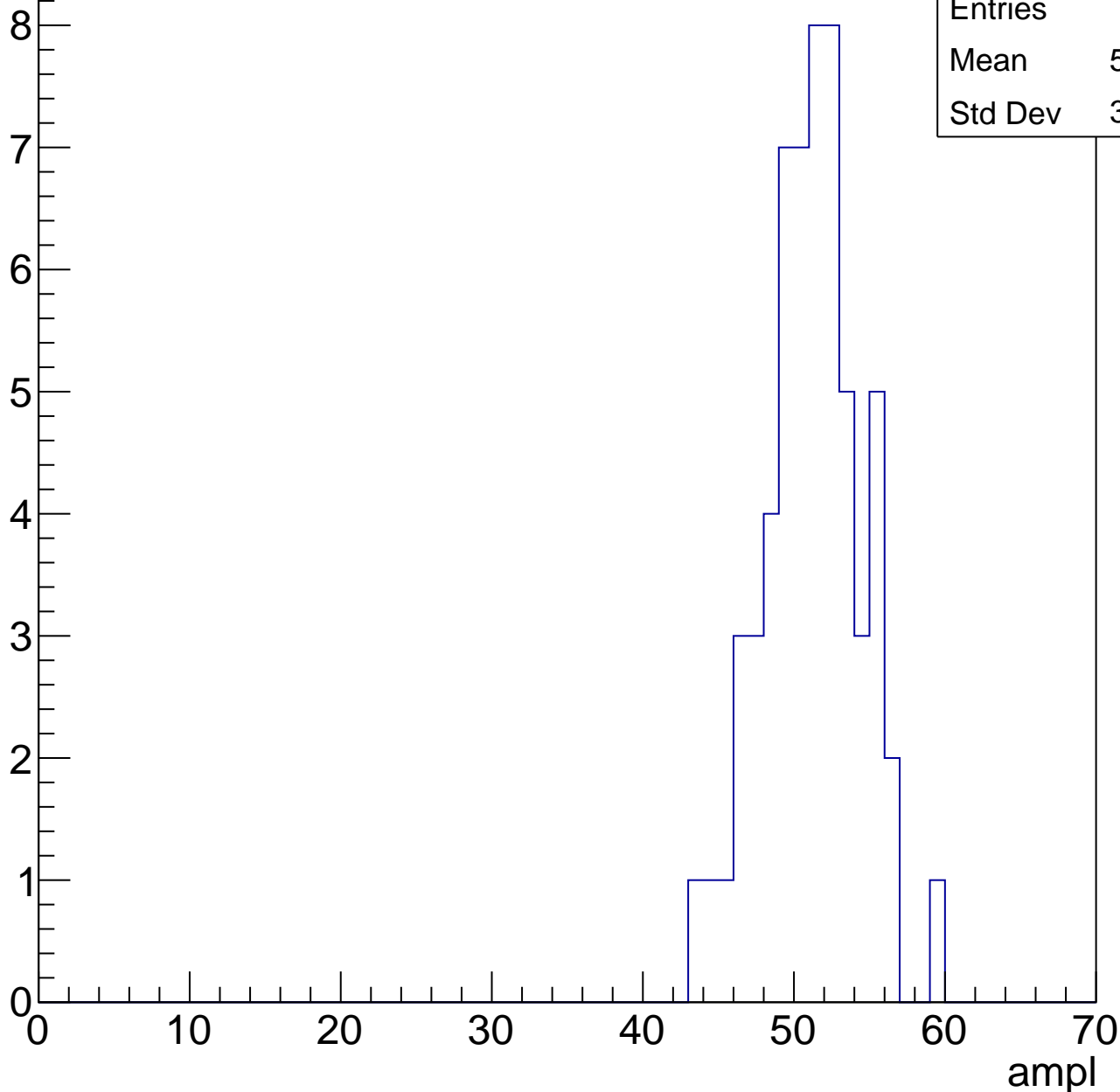


B0L000S, U2-ch34, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	50.73
Std Dev	3.156

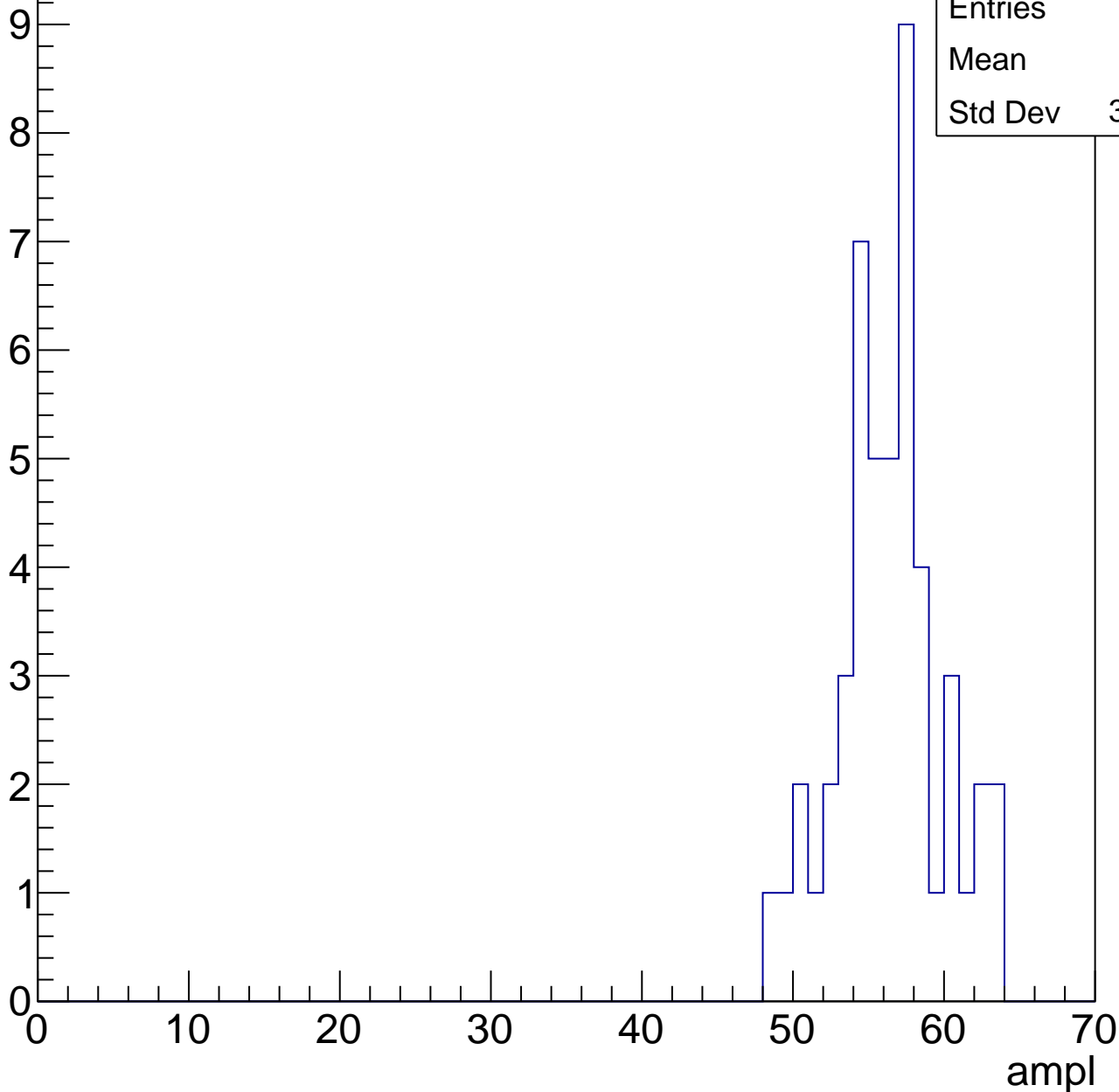


B0L000S, U2-ch34, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.9
Std Dev	3.436



B0L000S, U2-ch34, adc5

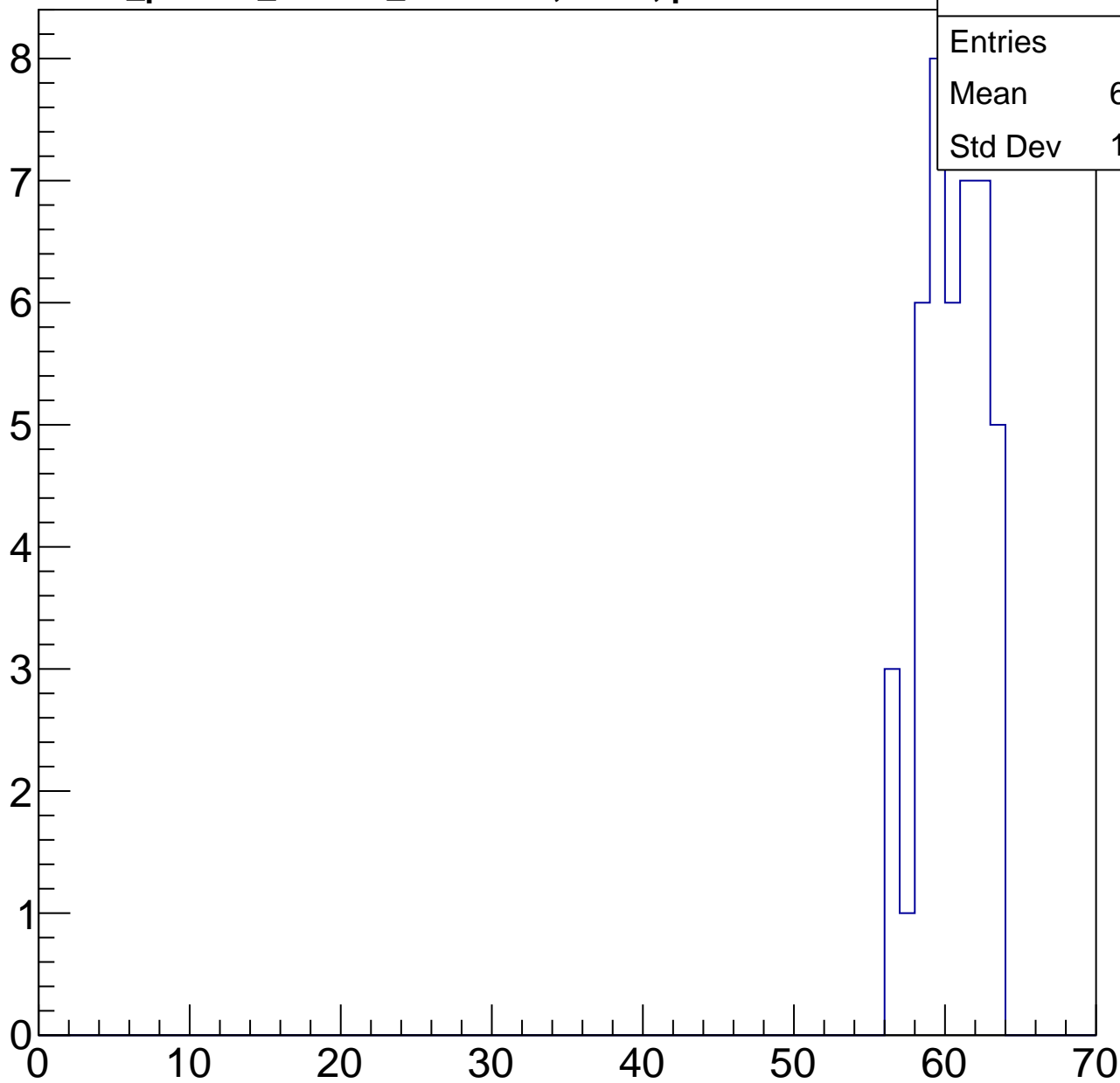
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	60.02
Std Dev	1.982

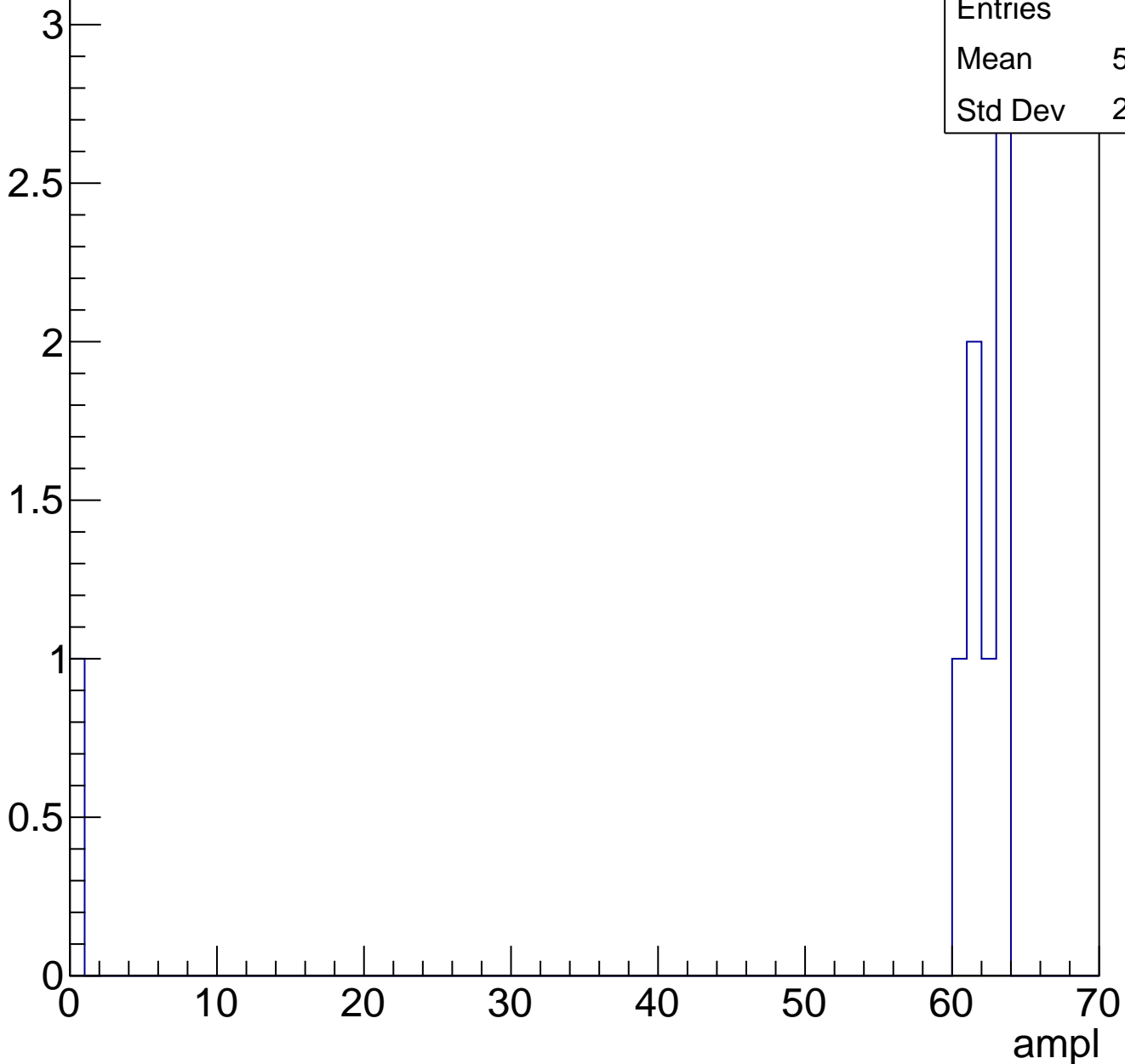
ampl



B0L000S, U2-ch34, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	8
Mean	54.12
Std Dev	20.48

B0L000S, U2-ch34, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch35, adc0

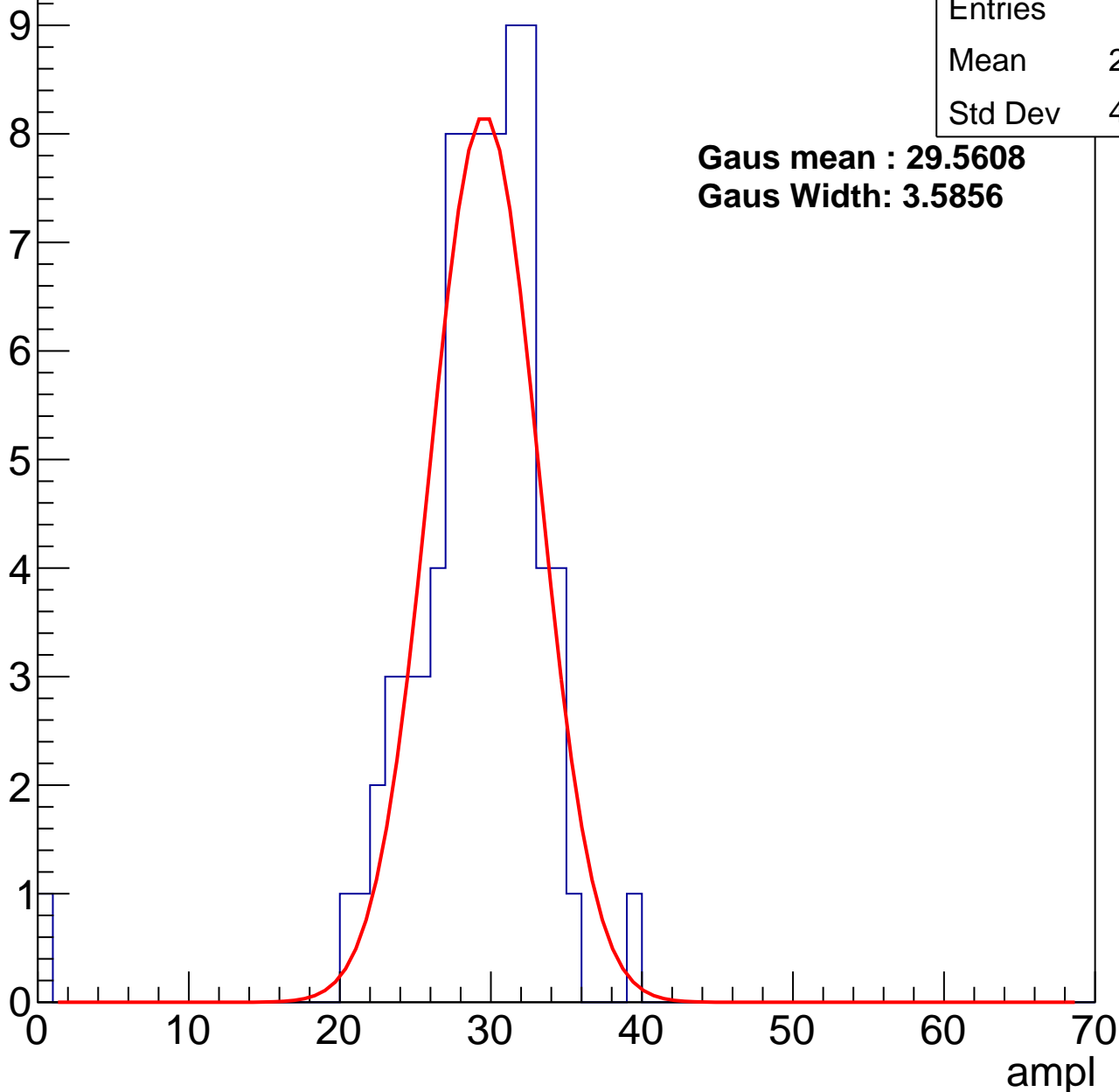
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.54
Std Dev	4.803

Gaus mean : 29.5608

Gaus Width: 3.5856



B0L000S, U2-ch35, adc1

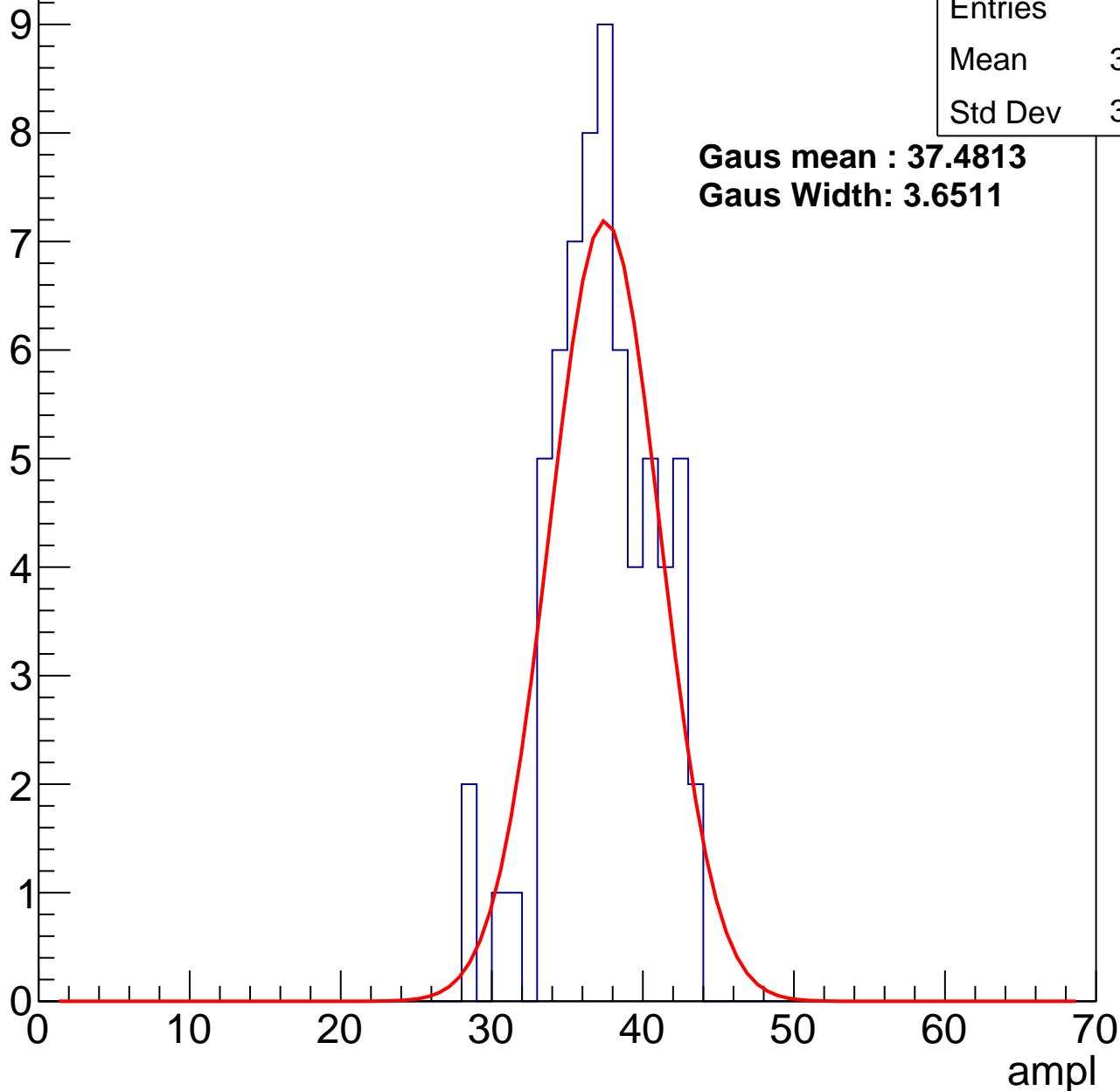
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.86
Std Dev	3.383

Gaus mean : 37.4813

Gaus Width: 3.6511



B0L000S, U2-ch35, adc2

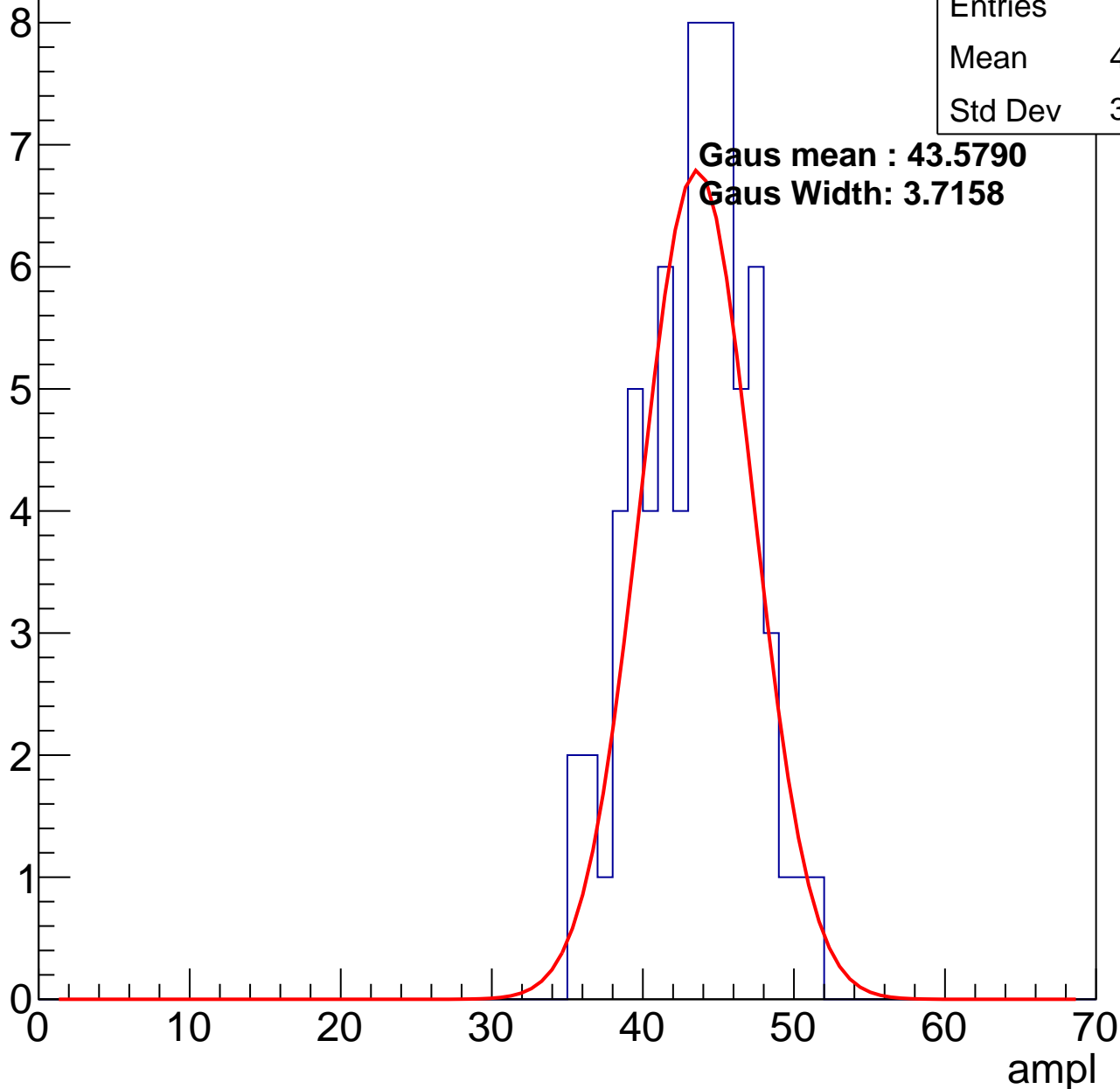
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.93
Std Dev	3.633

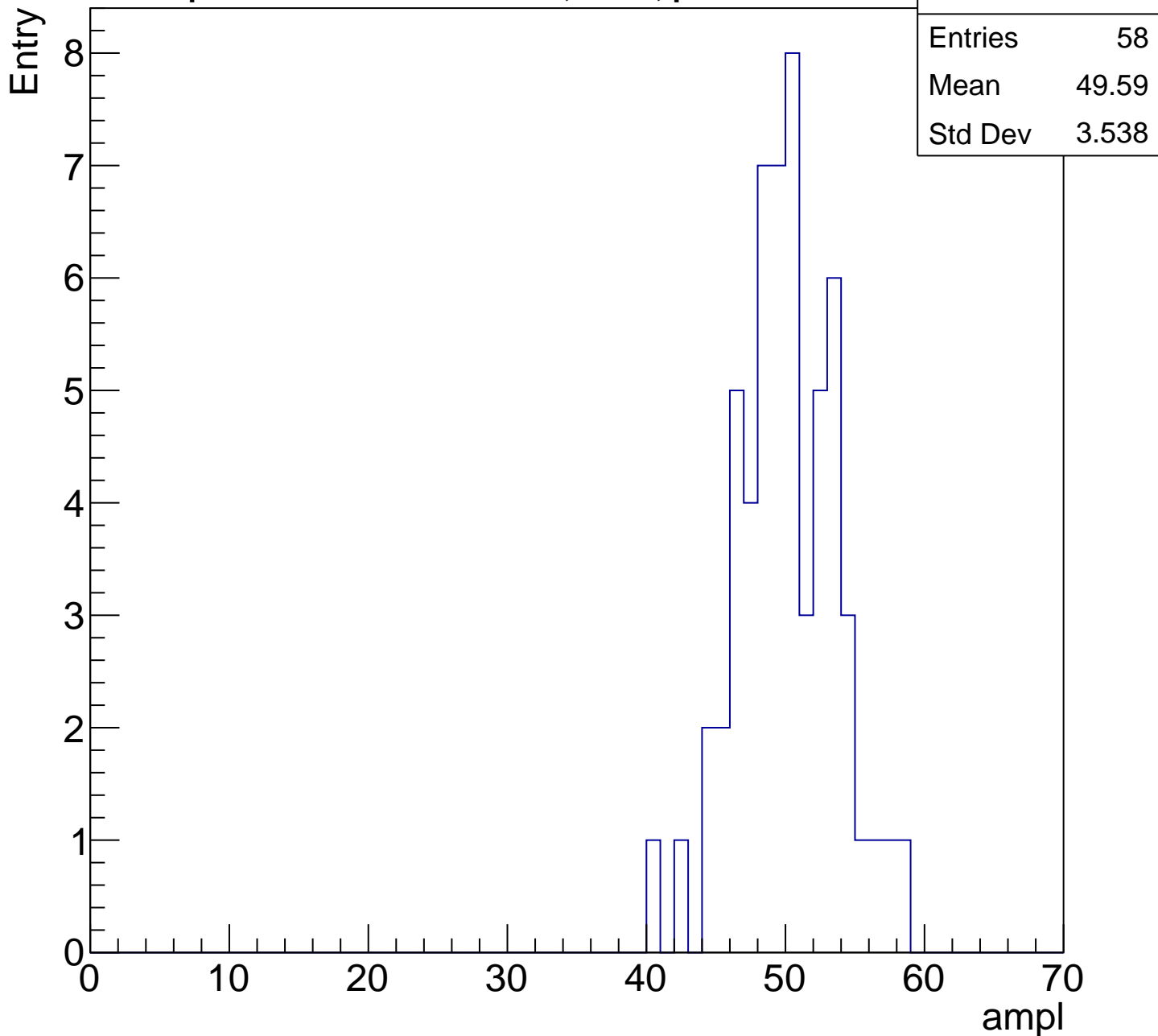
Gaus mean : 43.5790

Gaus Width: 3.7158



B0L000S, U2-ch35, adc3

calib_packv5_042523_0143.root, FC#5, port B1

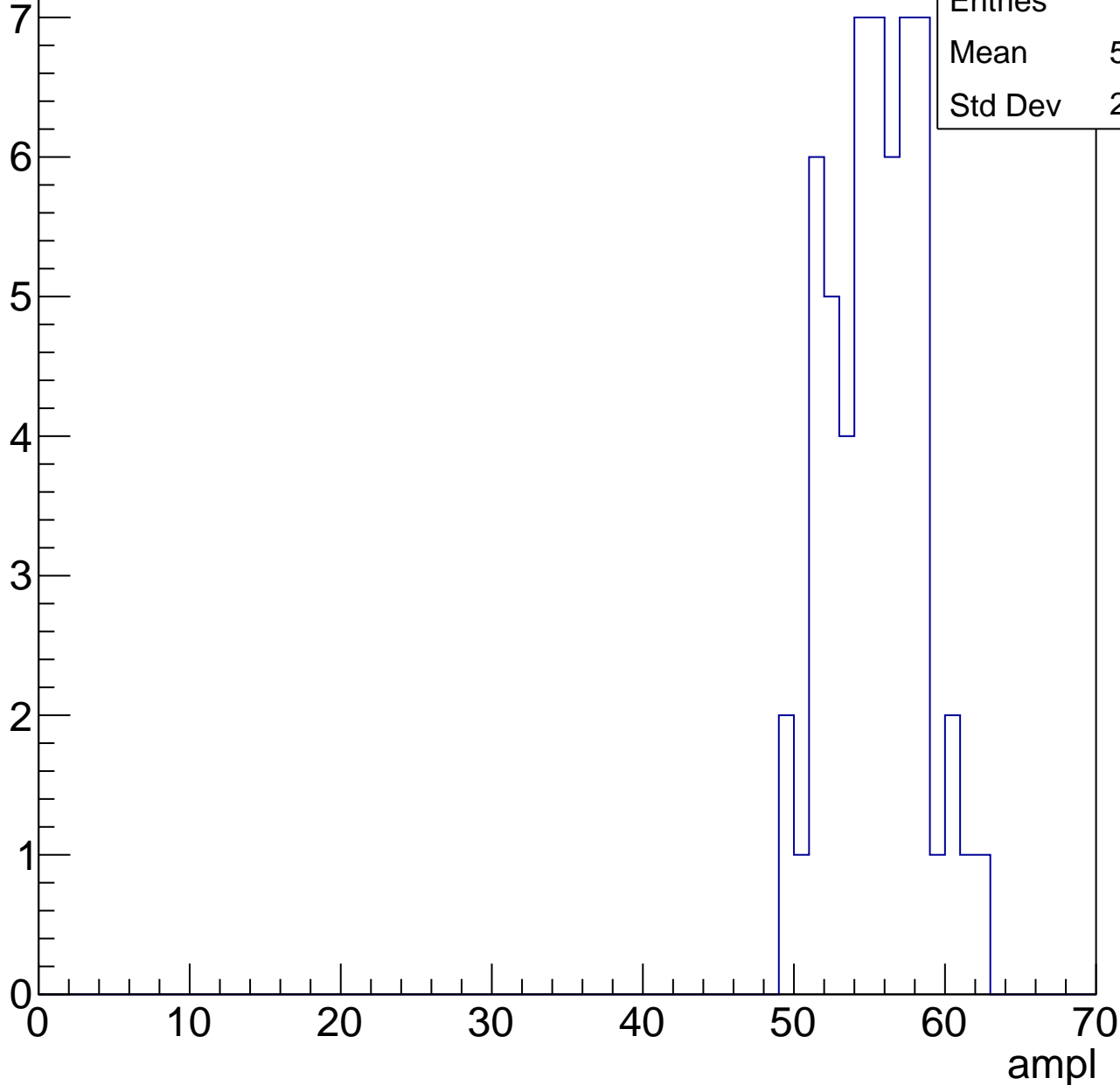


B0L000S, U2-ch35, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	54.95
Std Dev	2.988



B0L000S, U2-ch35, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.9
Std Dev	8.698

ampl

0

10

20

30

40

50

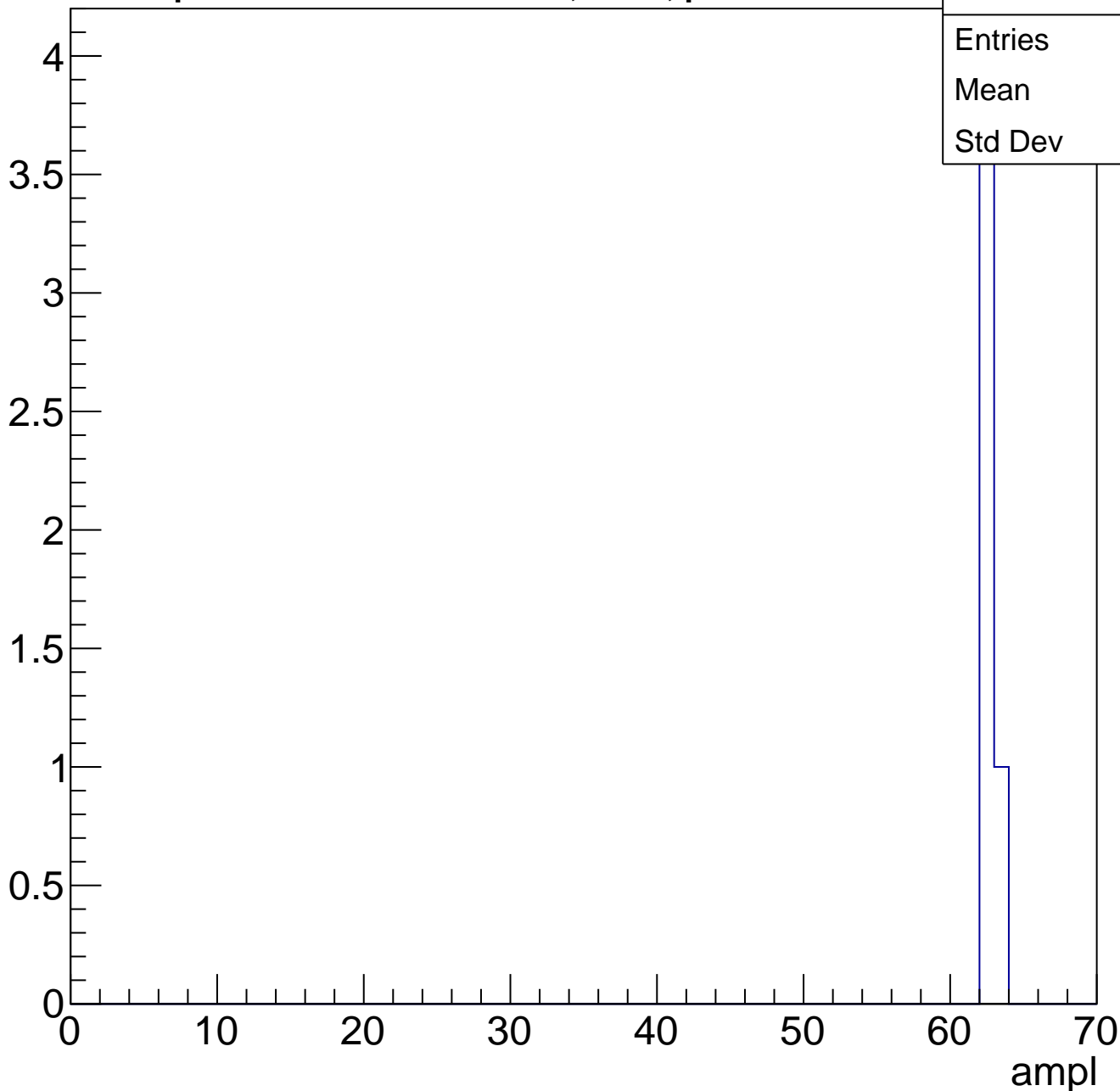
60

70

B0L000S, U2-ch35, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch35, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch36, adc0

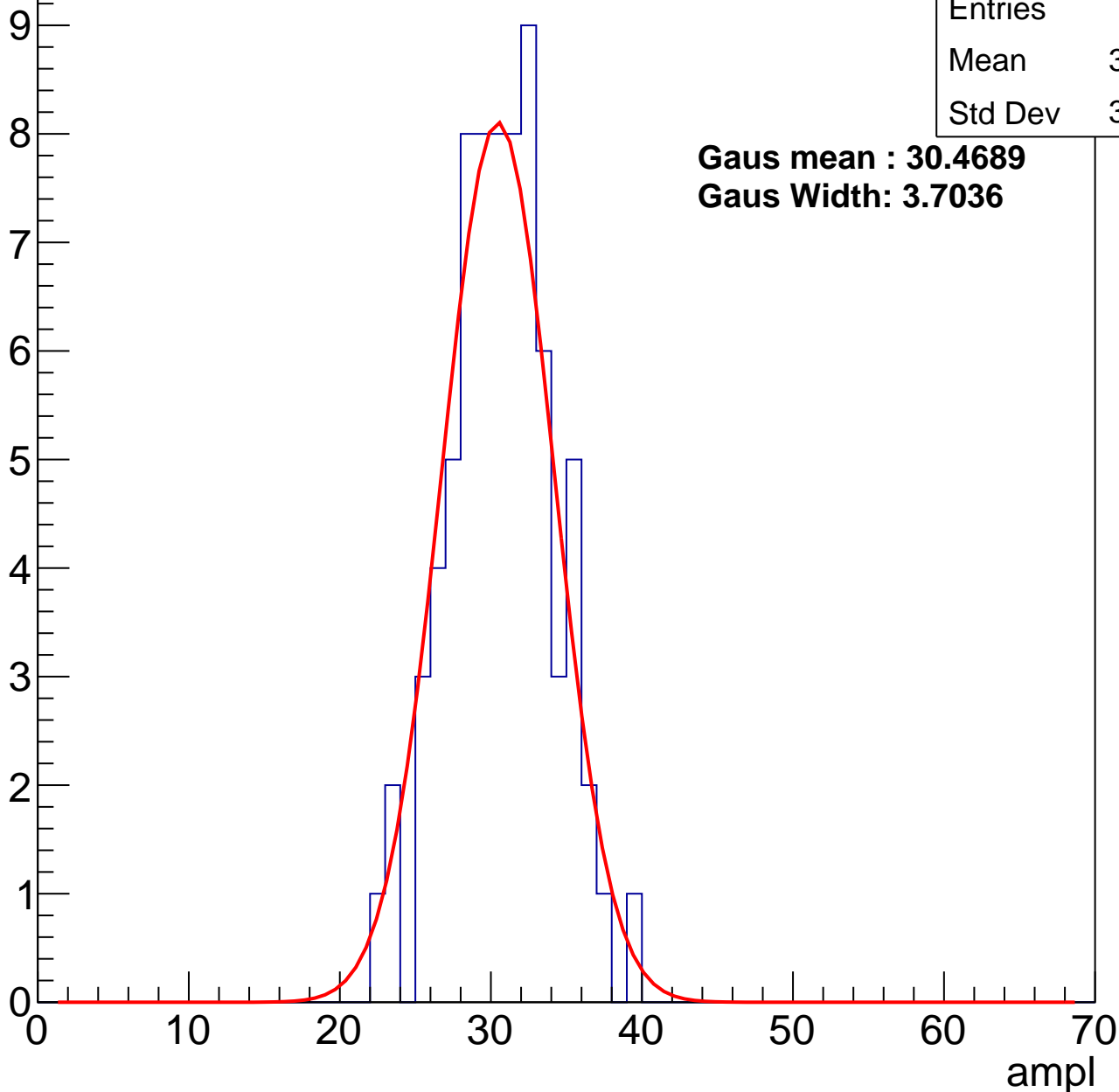
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	30.23
Std Dev	3.399

Gaus mean : 30.4689

Gaus Width: 3.7036



B0L000S, U2-ch36, adc1

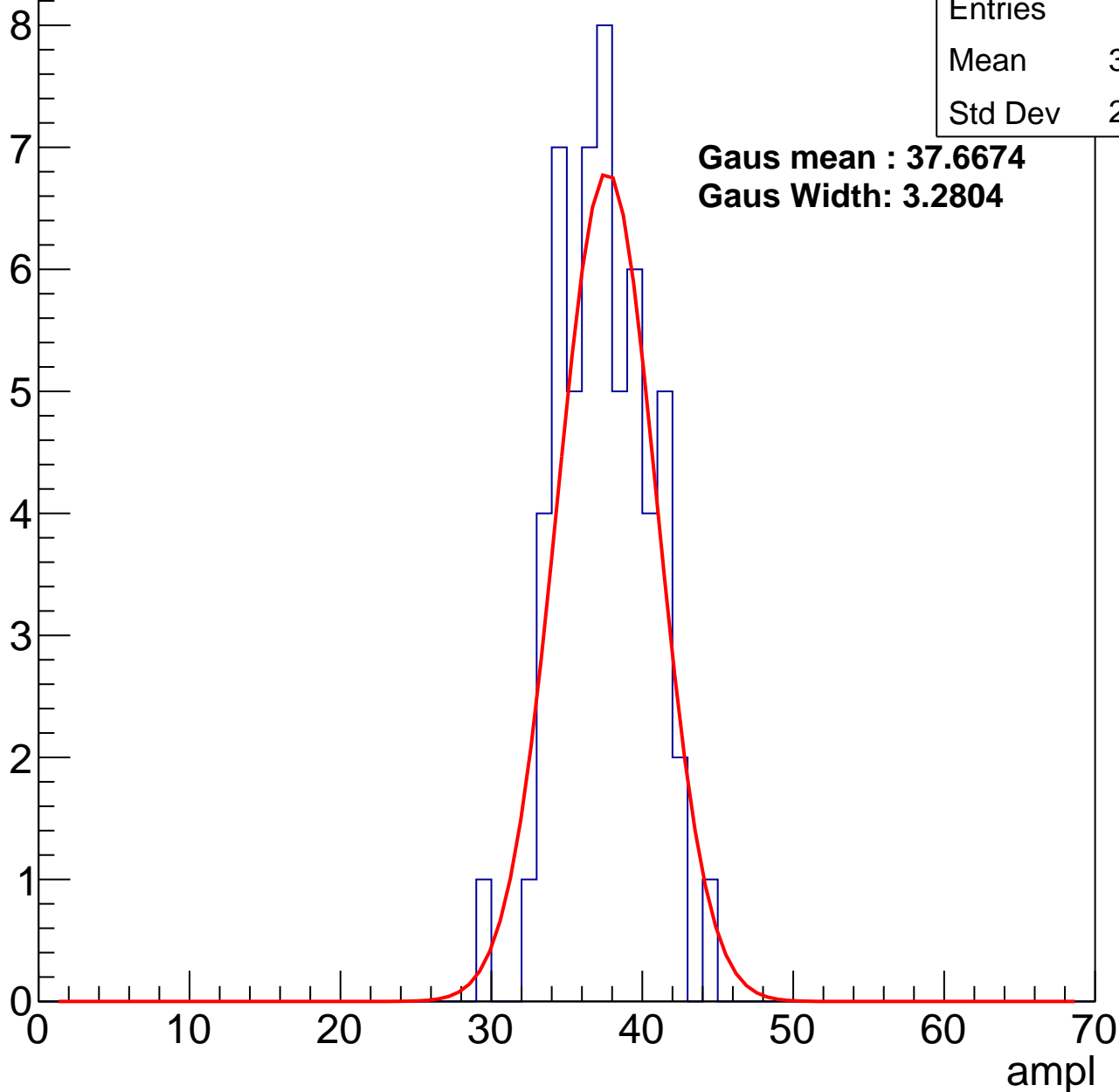
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.98
Std Dev	2.949

Gaus mean : 37.6674

Gaus Width: 3.2804



B0L000S, U2-ch36, adc2

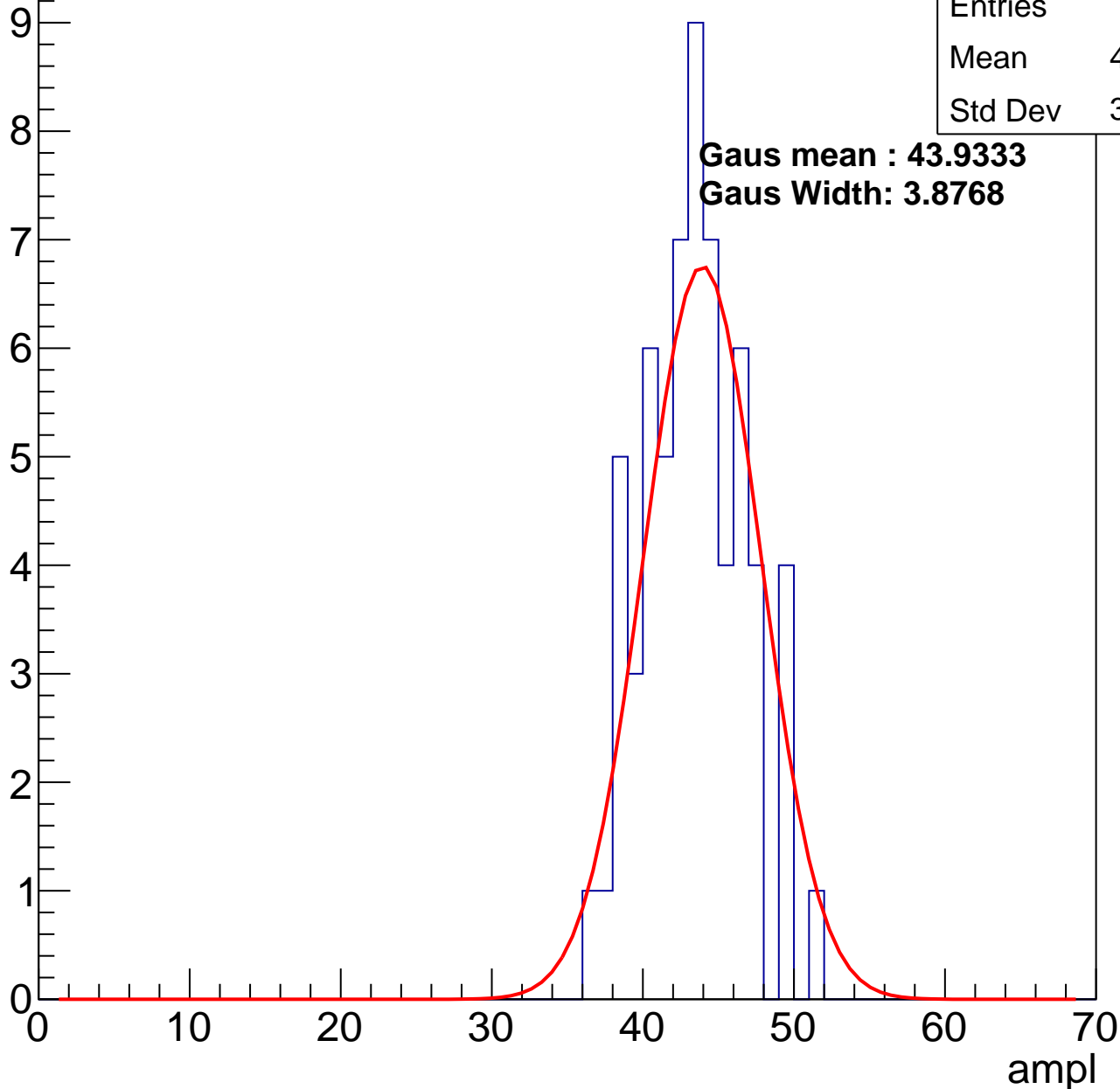
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.94
Std Dev	3.304

Gaus mean : 43.9333

Gaus Width: 3.8768

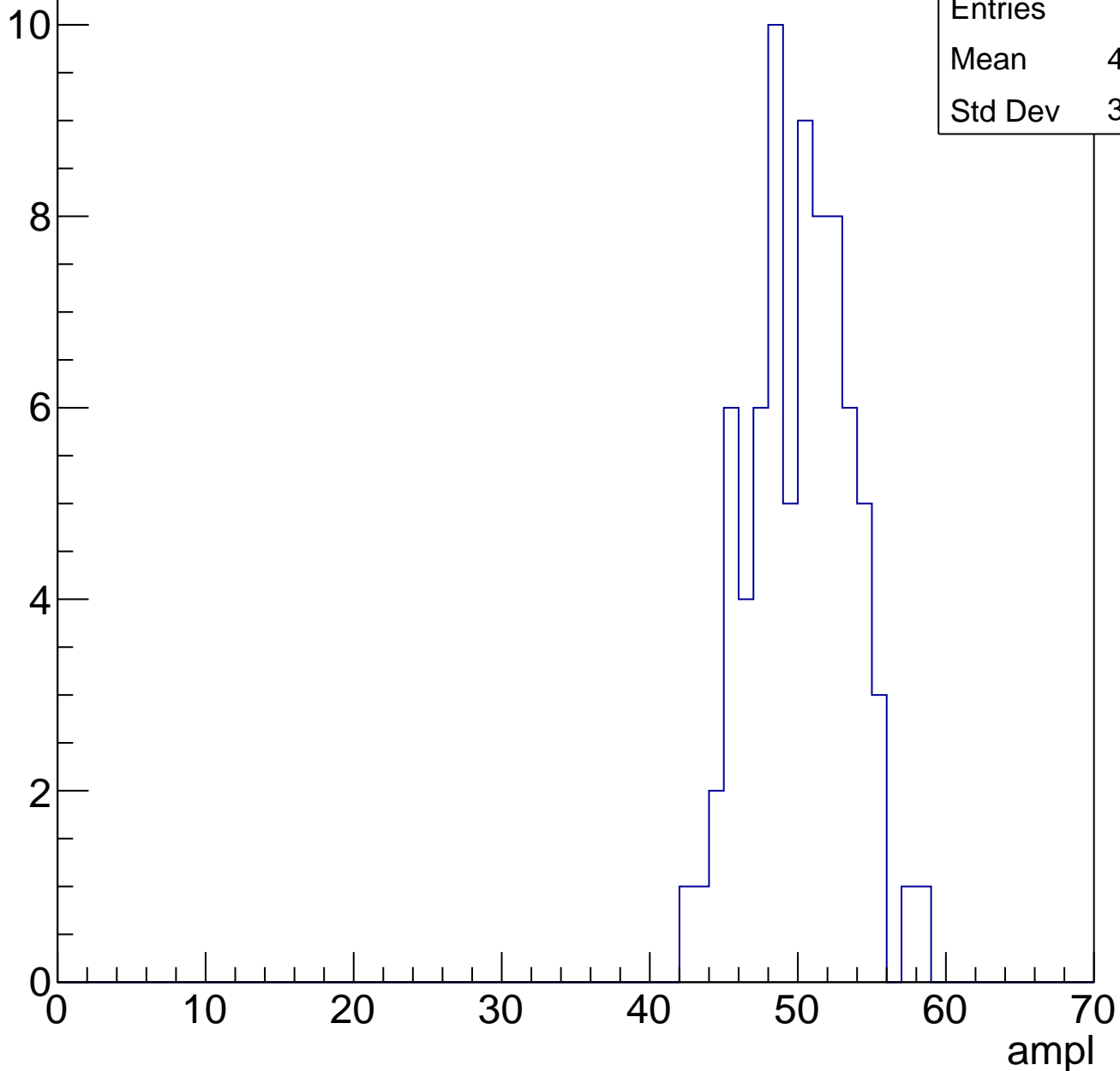


B0L000S, U2-ch36, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	76
Mean	49.68
Std Dev	3.349

Entry

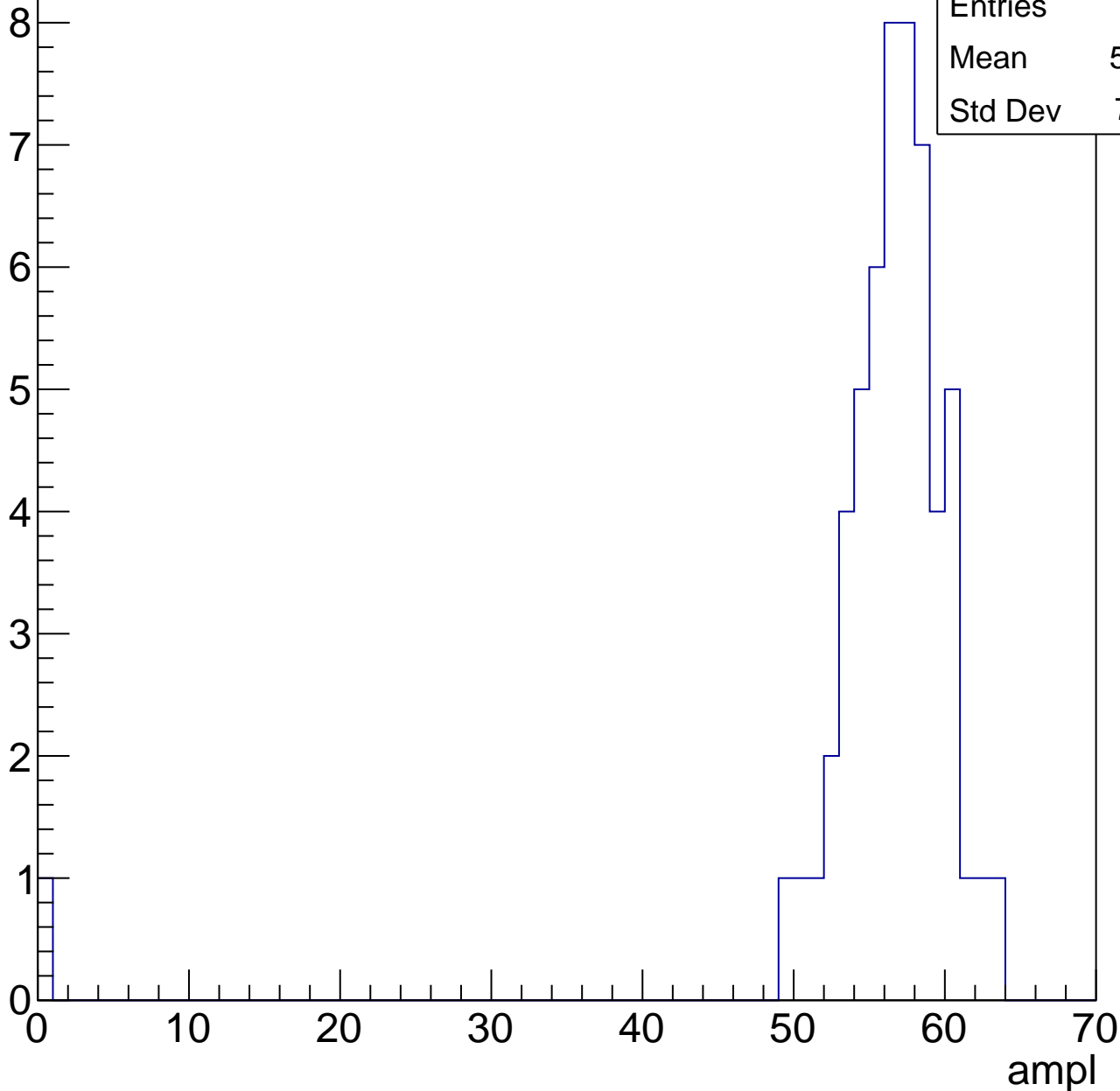


B0L000S, U2-ch36, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.32
Std Dev	7.991

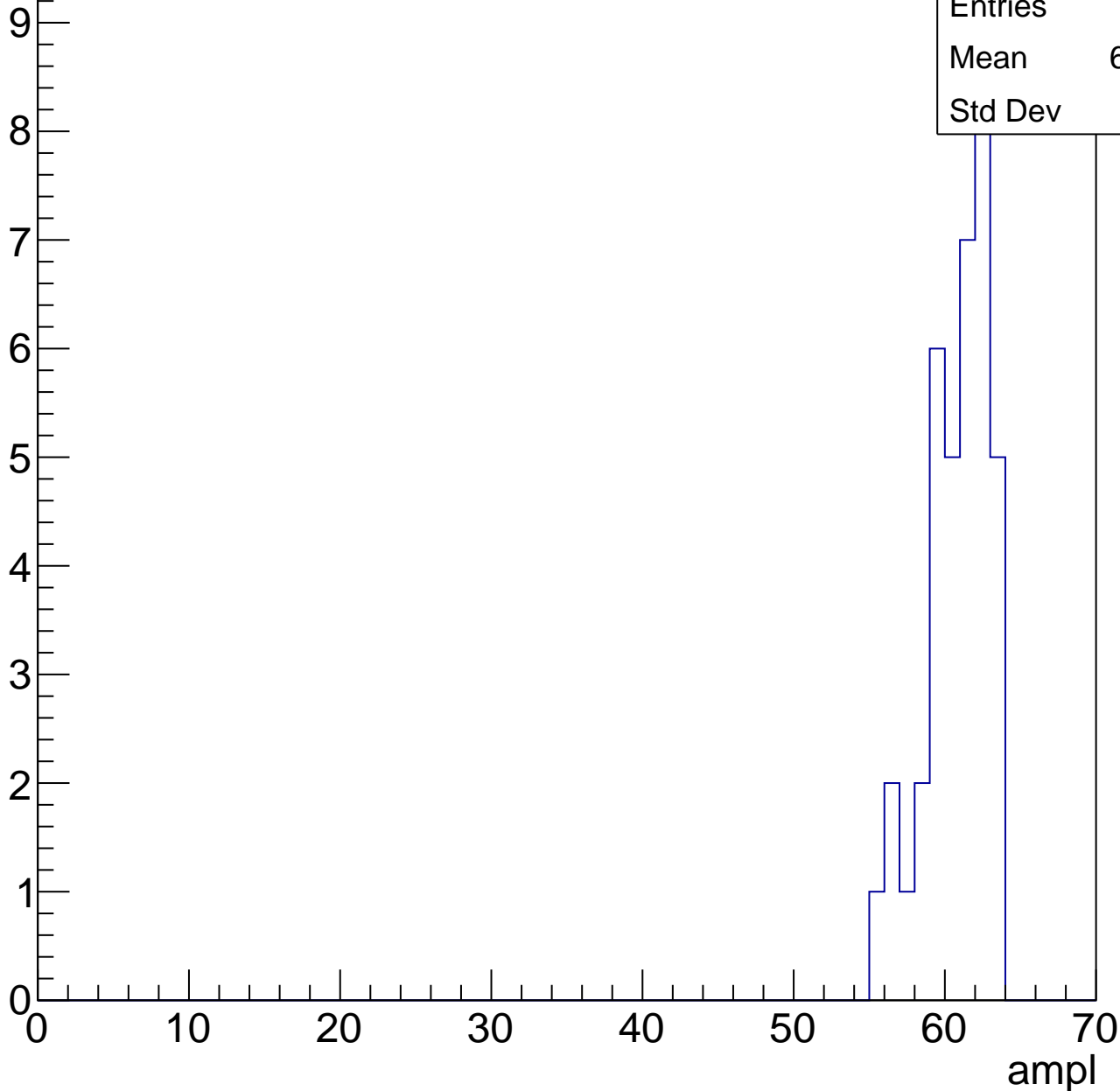


B0L000S, U2-ch36, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

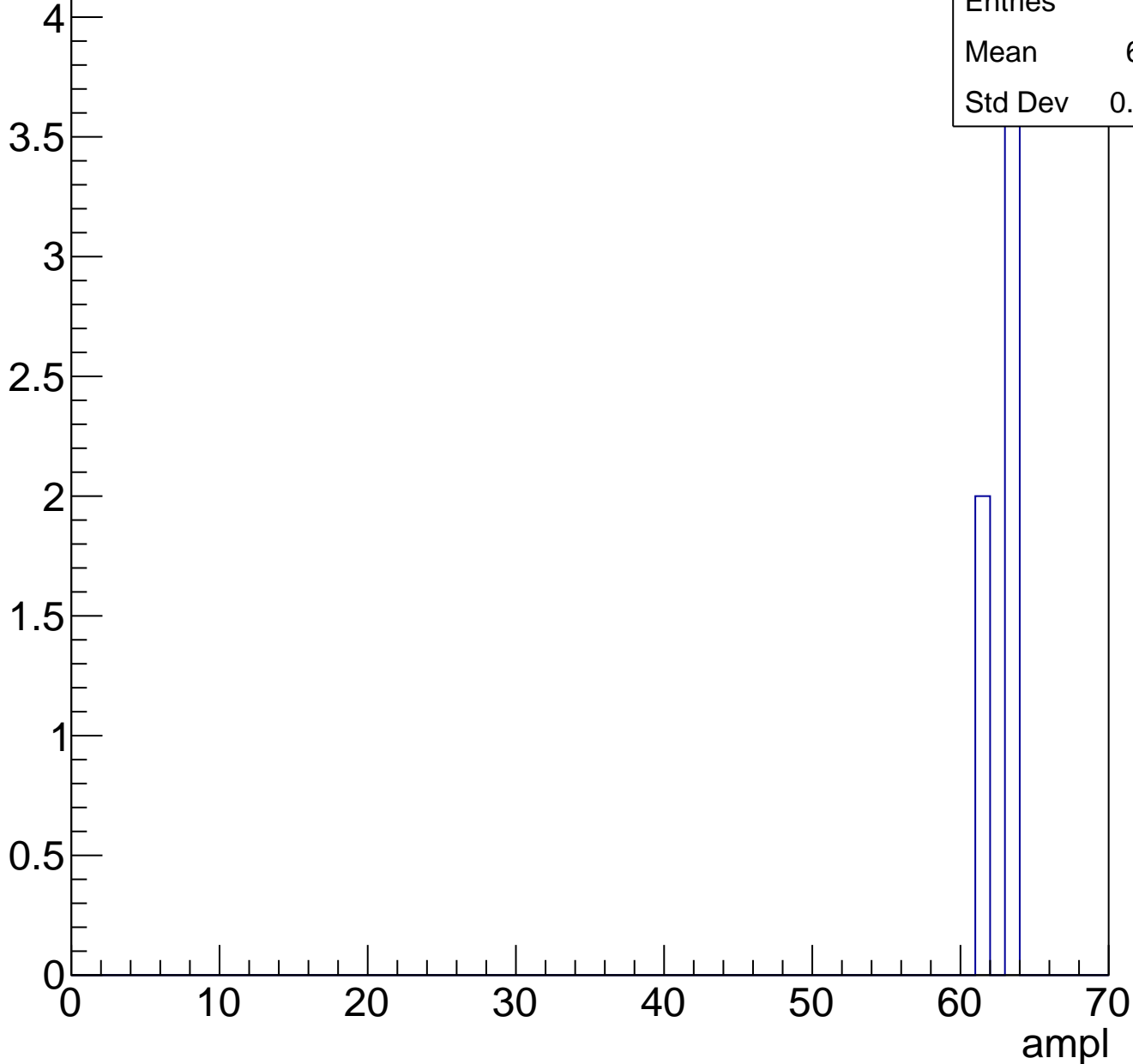
Entries	38
Mean	60.37
Std Dev	2.07



B0L000S, U2-ch36, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch36, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch37, adc0

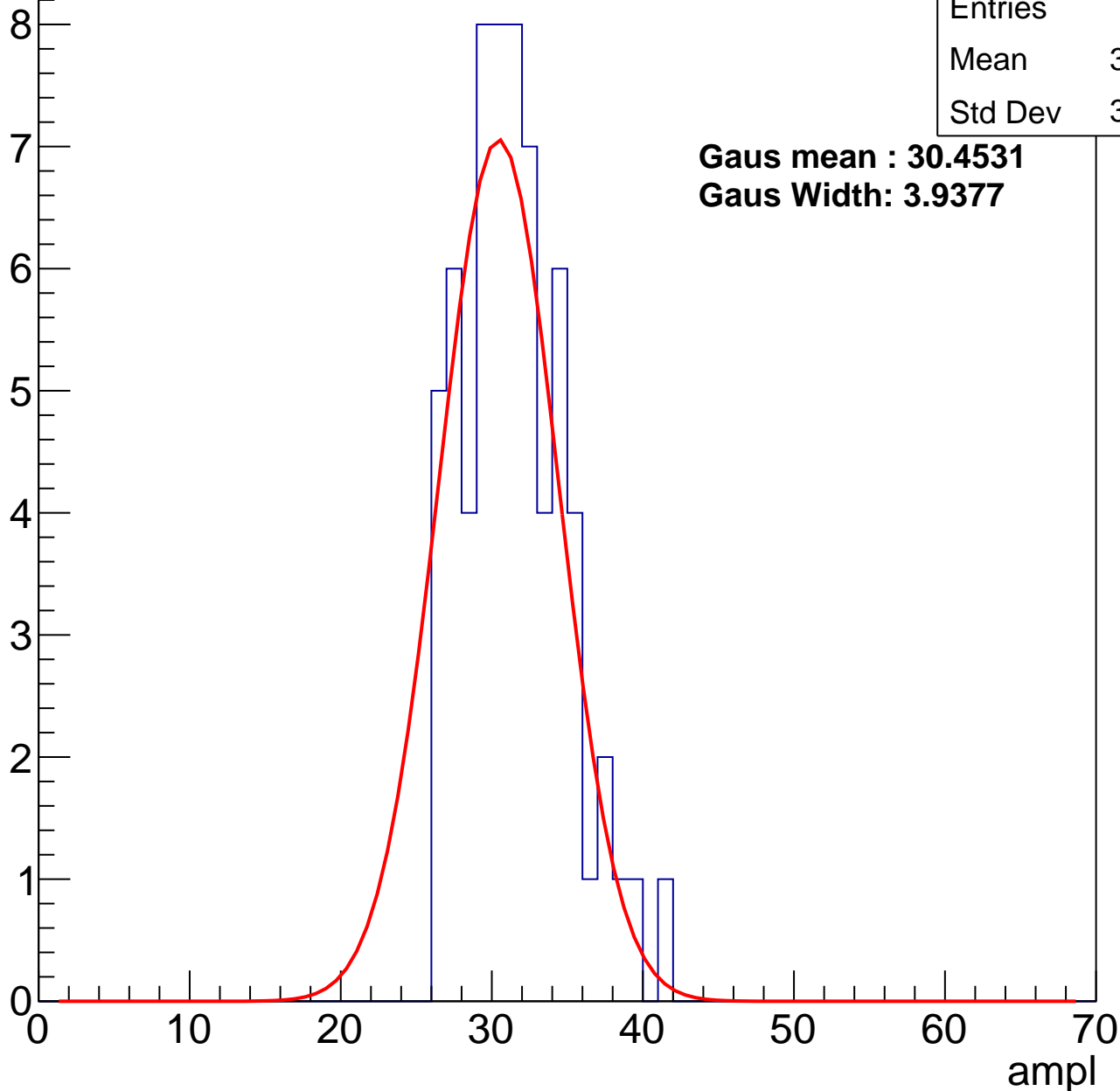
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	31.09
Std Dev	3.365

Gaus mean : 30.4531

Gaus Width: 3.9377



B0L000S, U2-ch37, adc1

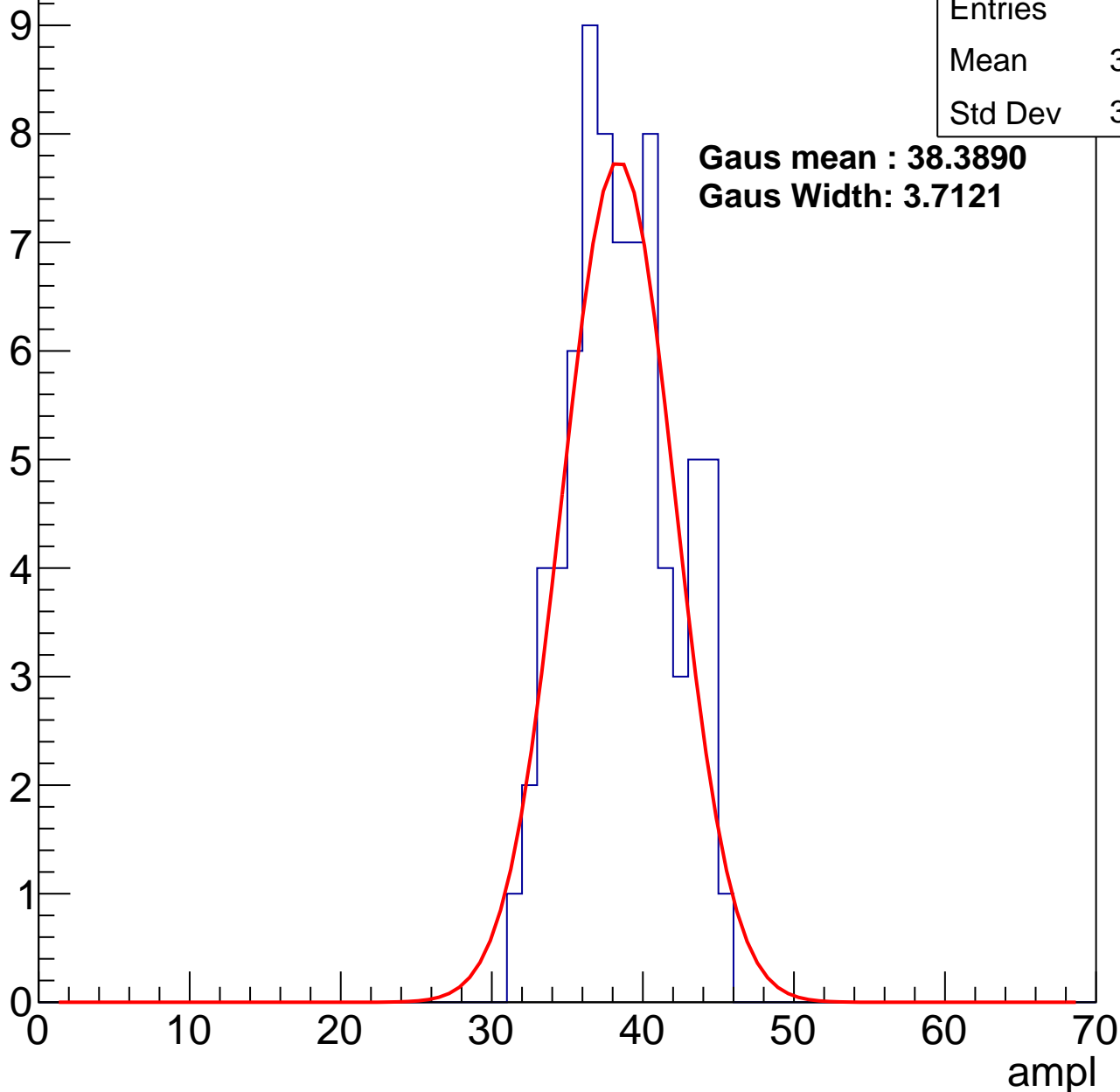
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	38.14
Std Dev	3.406

Gaus mean : 38.3890

Gaus Width: 3.7121



B0L000S, U2-ch37, adc2

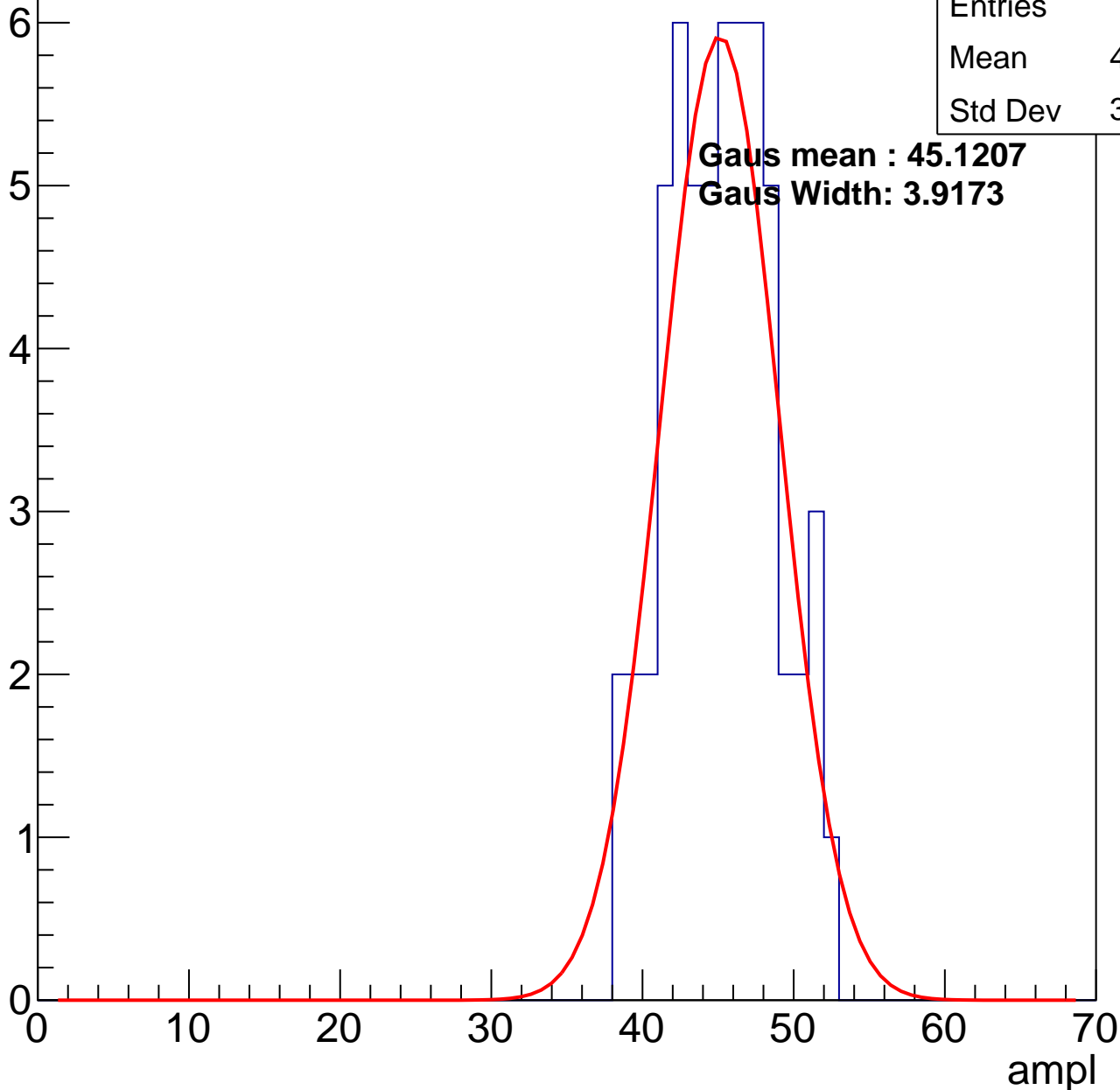
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.78
Std Dev	3.449

Gaus mean : 45.1207

Gaus Width: 3.9173

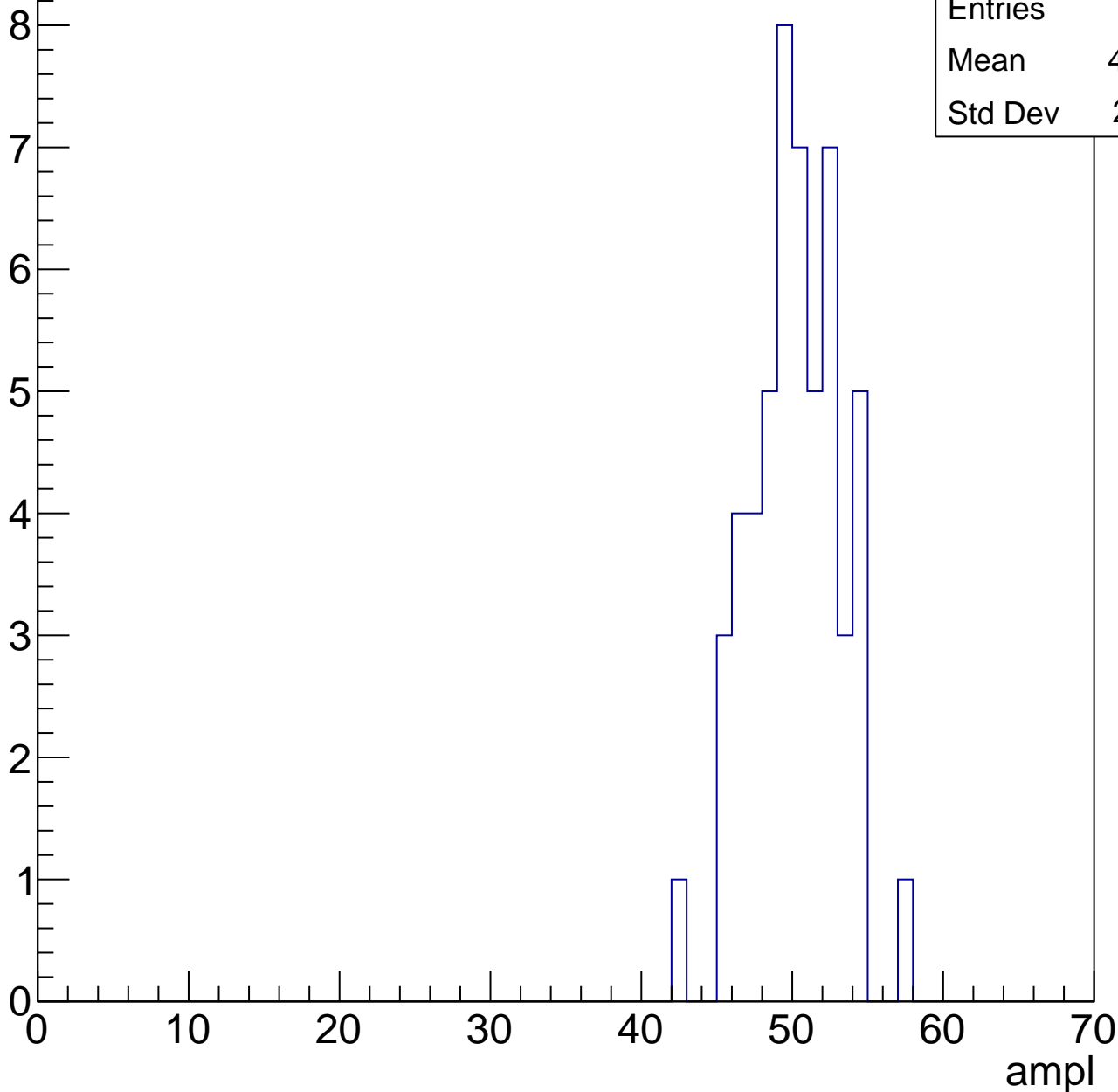


B0L000S, U2-ch37, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	49.74
Std Dev	2.921

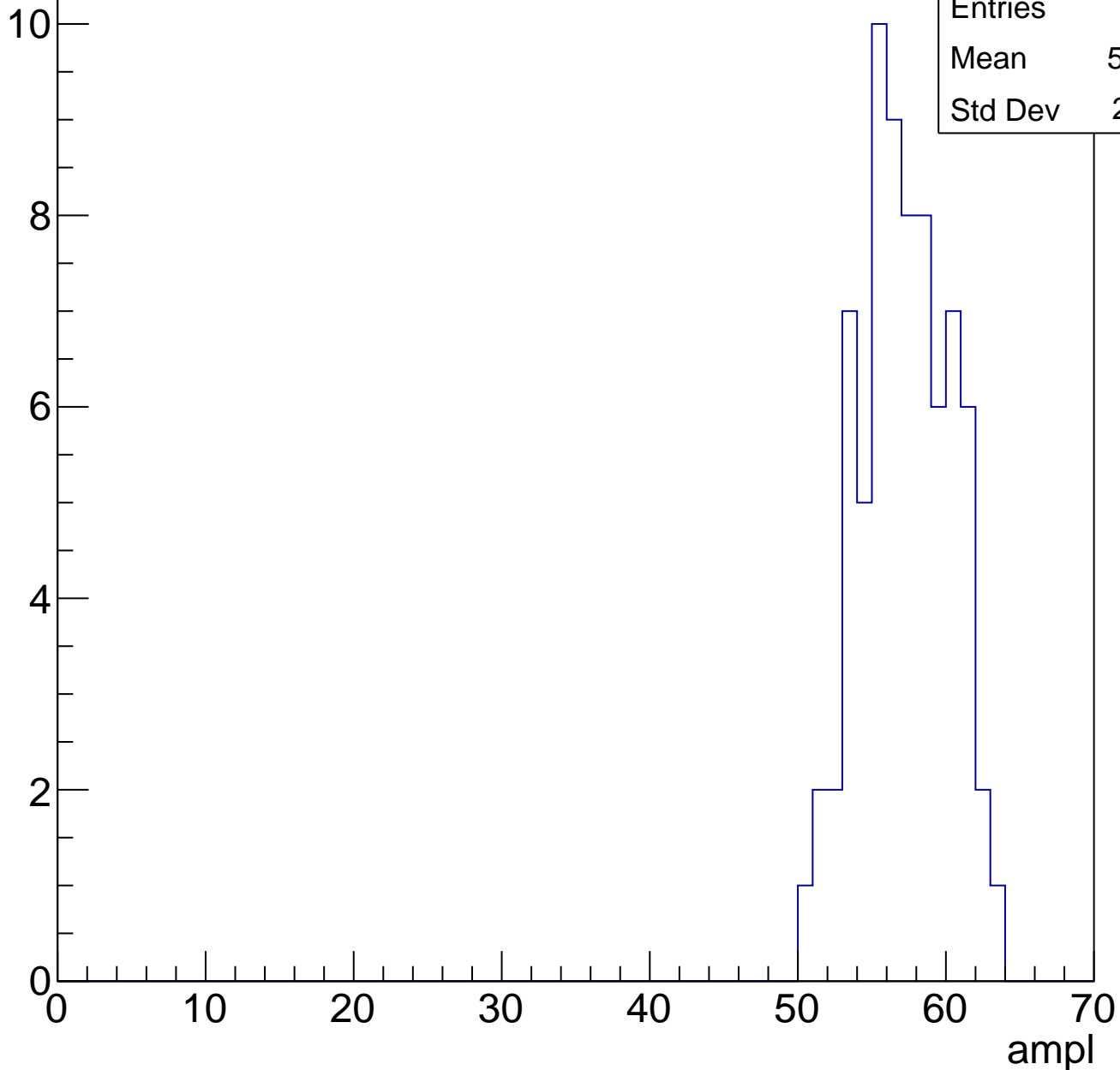


B0L000S, U2-ch37, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	74
Mean	56.73
Std Dev	2.961

Entry

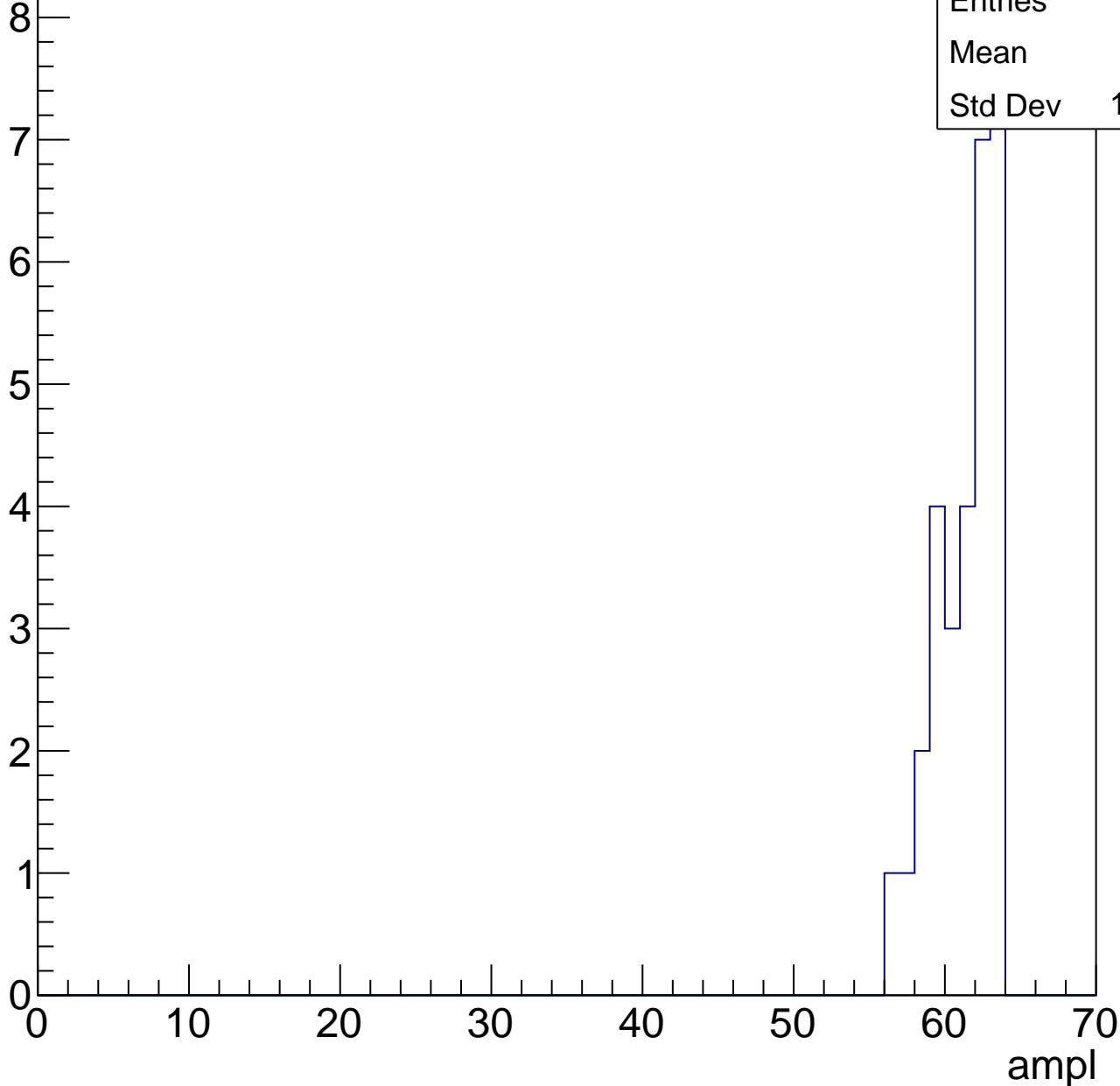


B0L000S, U2-ch37, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

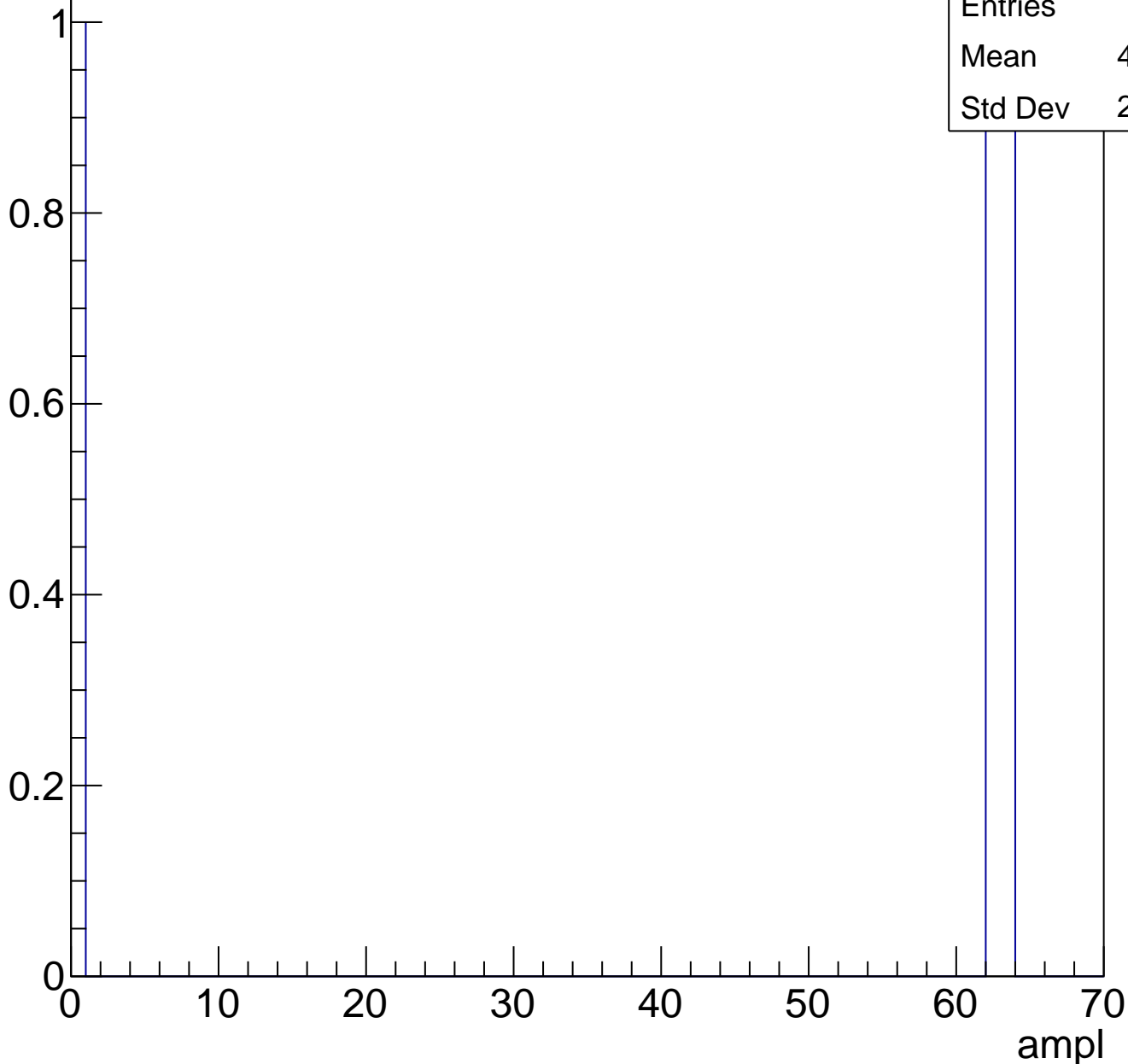
Entries	30
Mean	60.9
Std Dev	1.972



B0L000S, U2-ch37, adc6

calib_packv5_042523_0143.root, FC#5, port B1

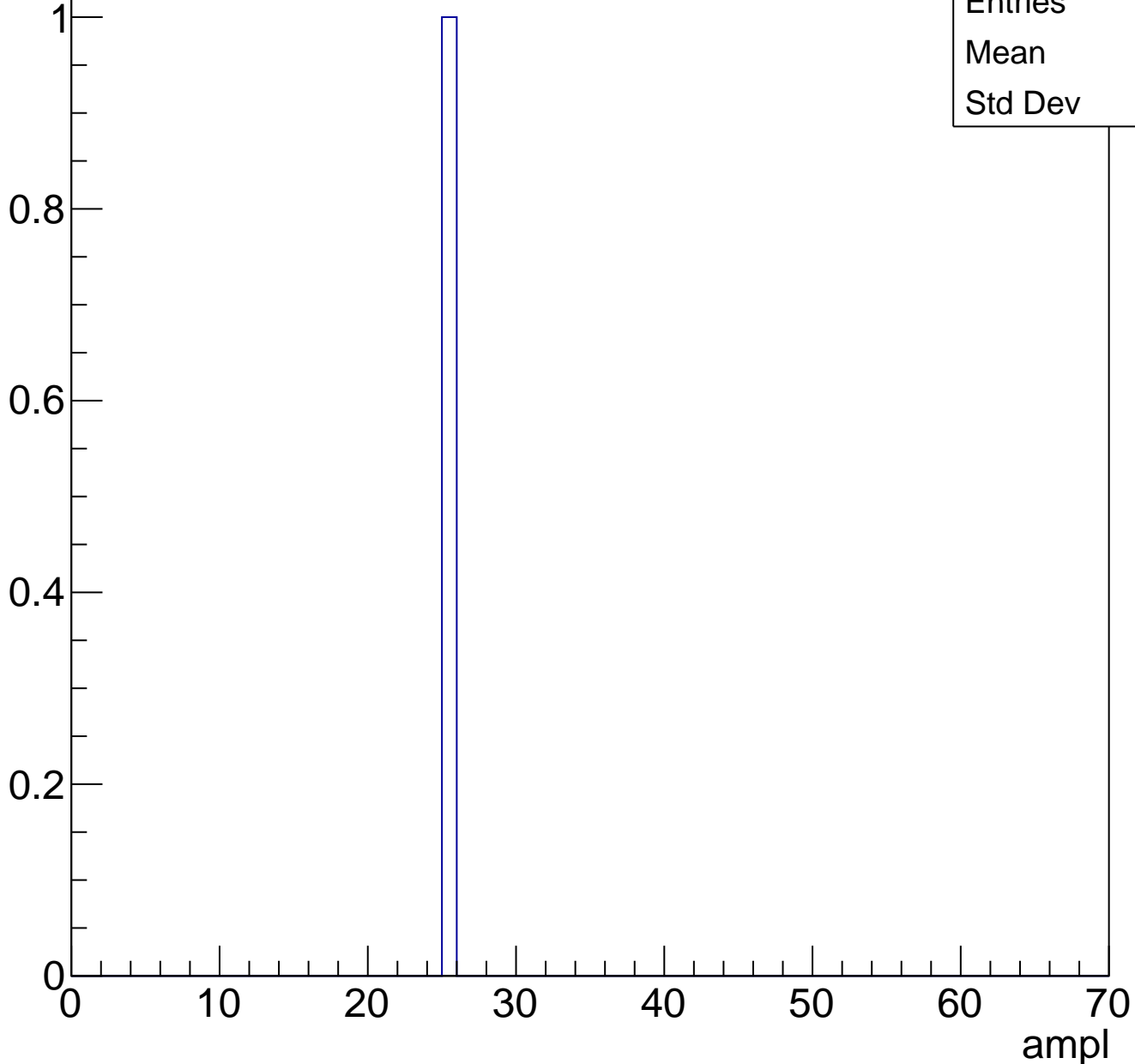
Entry



B0L000S, U2-ch37, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	25
Std Dev	0

B0L000S, U2-ch38, adc0

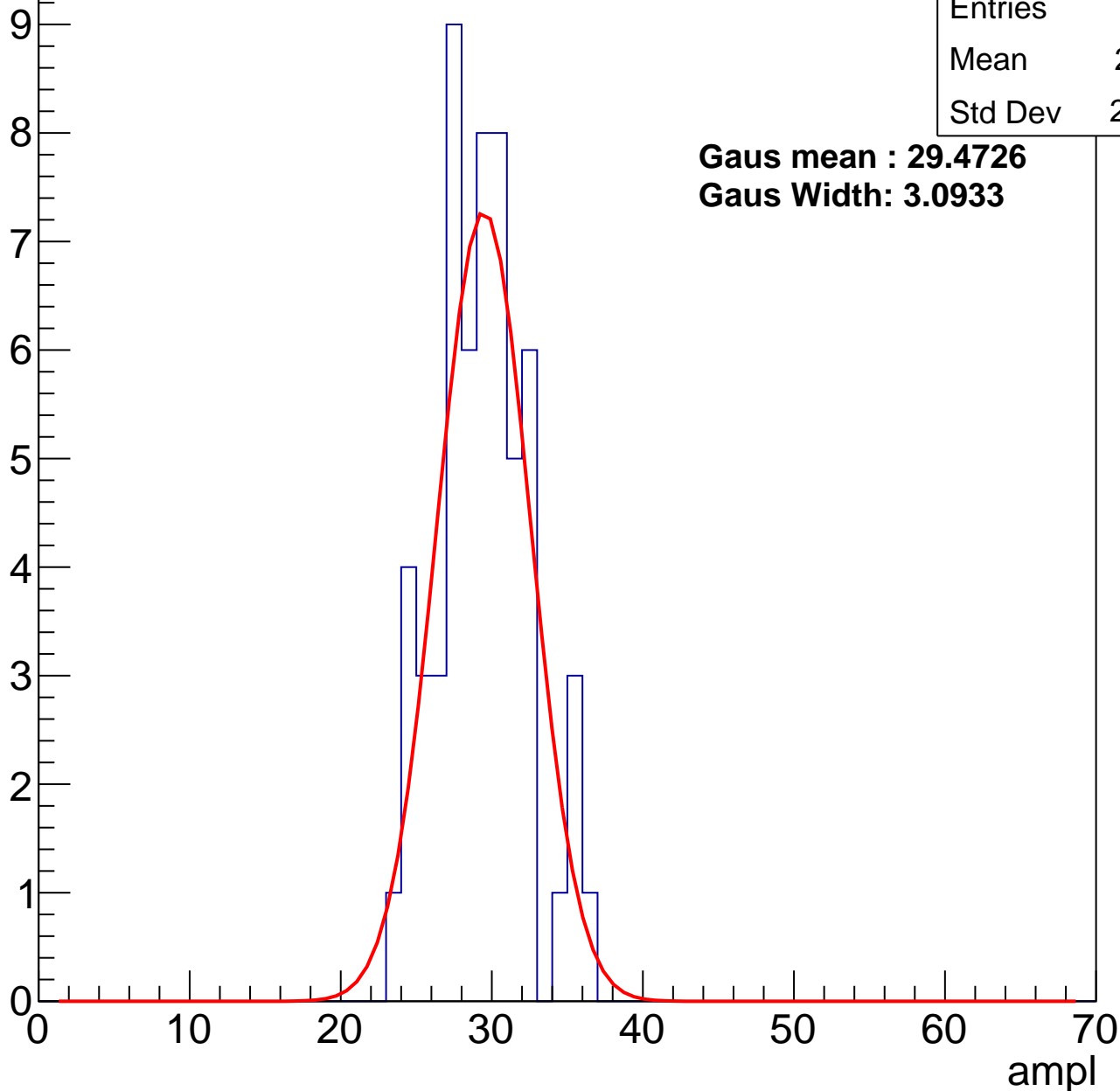
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	28.91
Std Dev	2.984

Gaus mean : 29.4726

Gaus Width: 3.0933



B0L000S, U2-ch38, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	86
Mean	35.98
Std Dev	3.42

Gaus mean : 36.5551

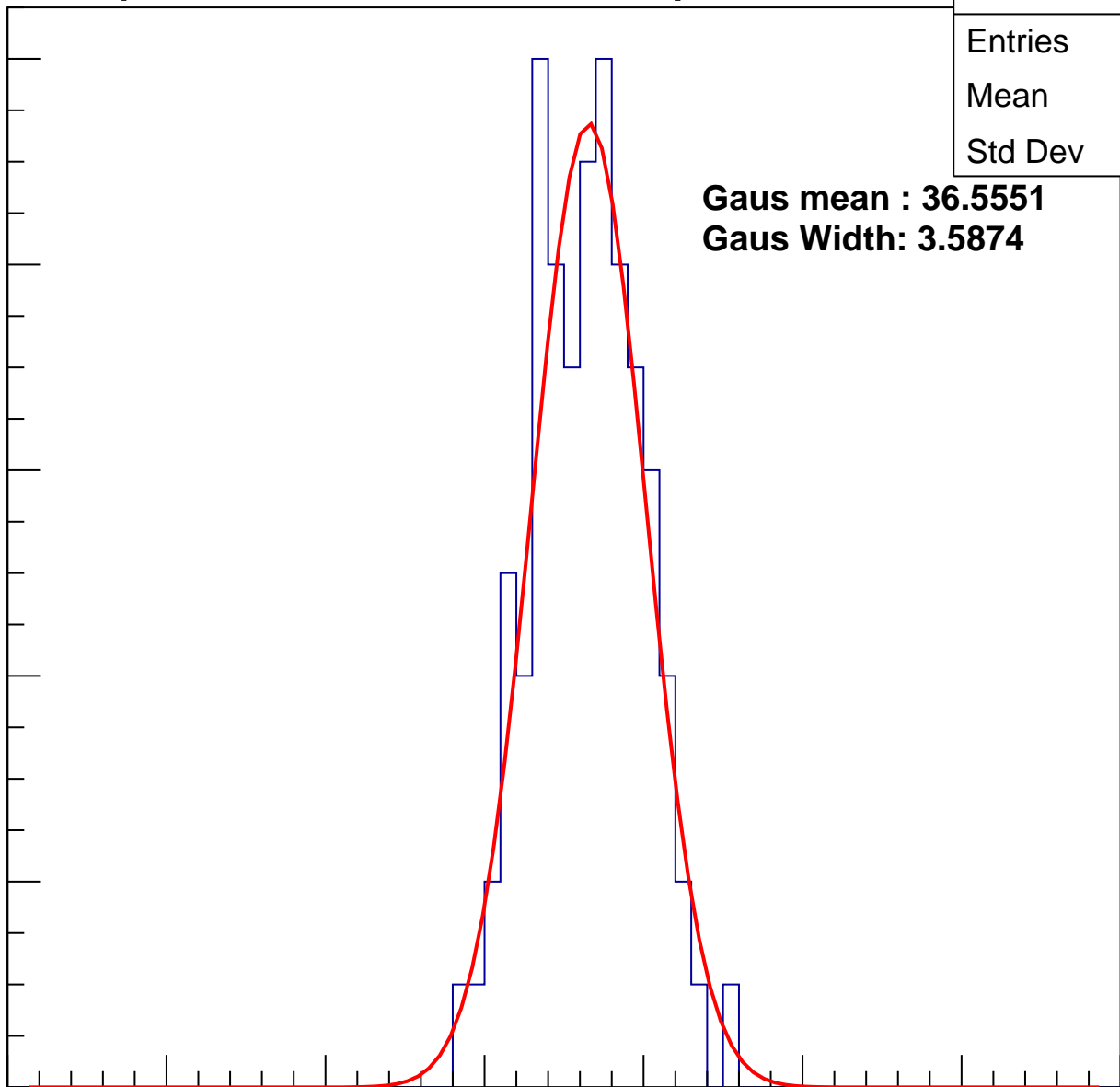
Gaus Width: 3.5874

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch38, adc2

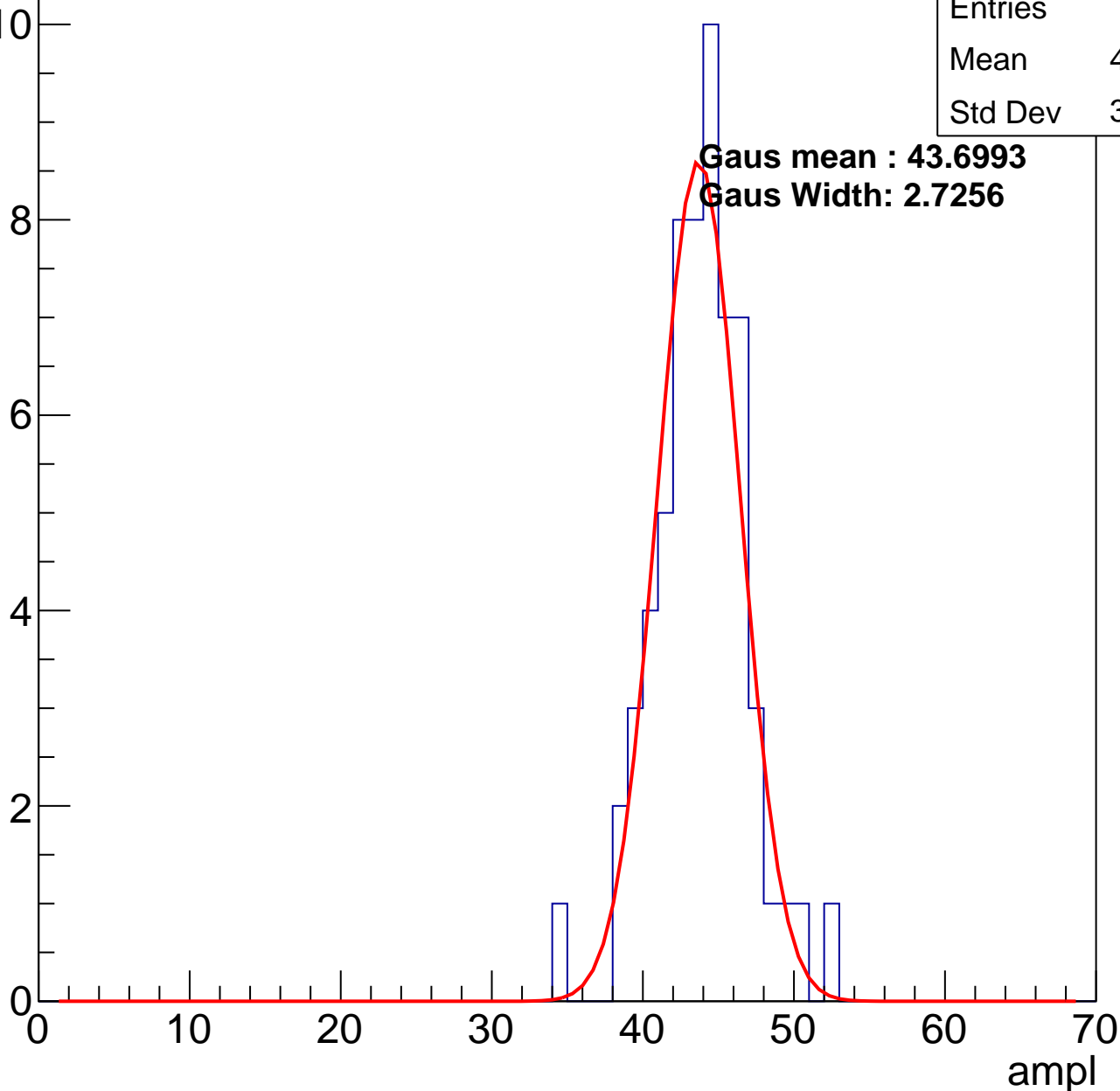
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.37
Std Dev	3.044

Gaus mean : 43.6993

Gaus Width: 2.7256

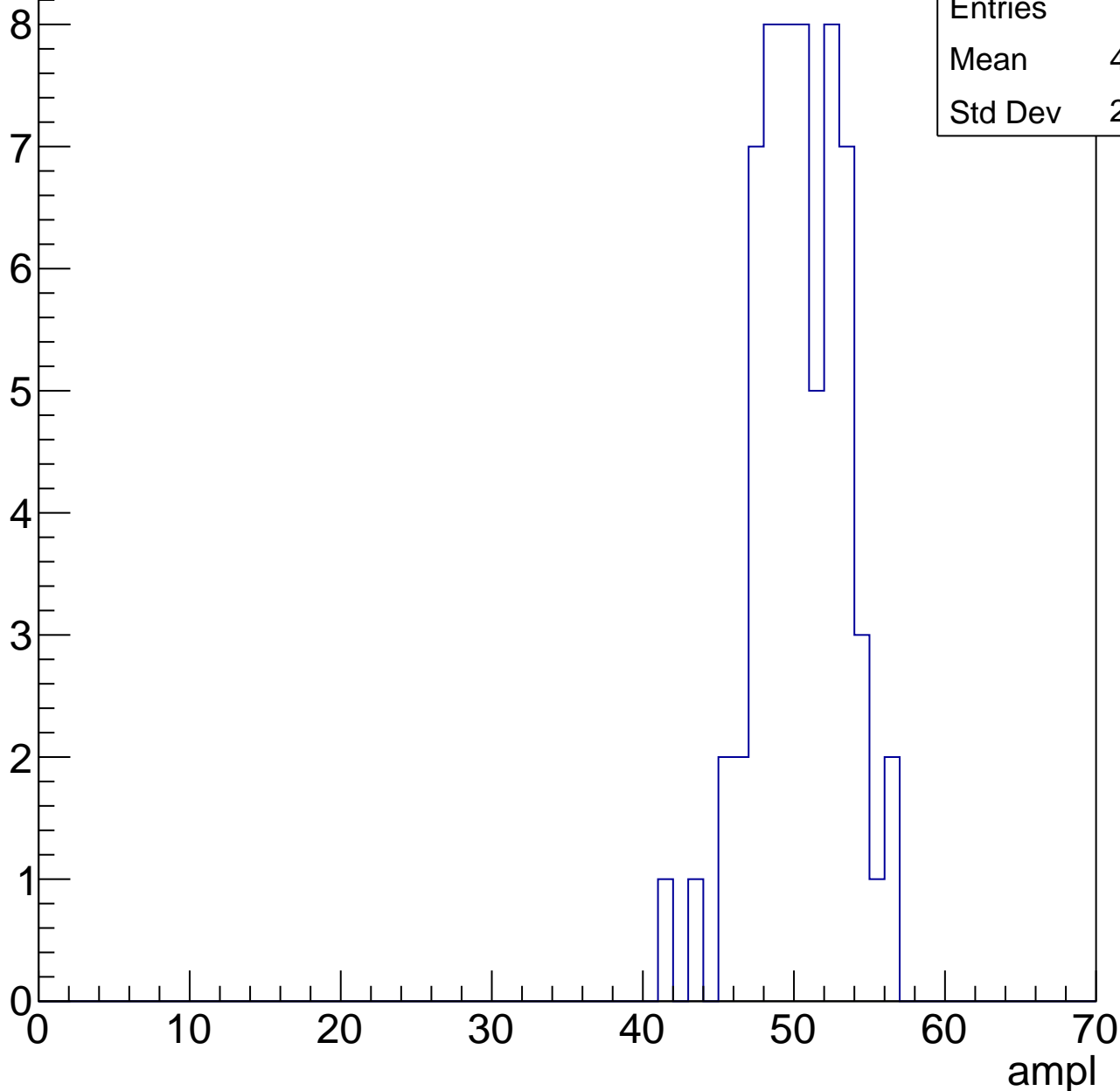


B0L000S, U2-ch38, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	49.87
Std Dev	2.979



B0L000S, U2-ch38, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	55
Mean	55.33
Std Dev	3.298

Entry

10

8

6

4

2

0

0

10

20

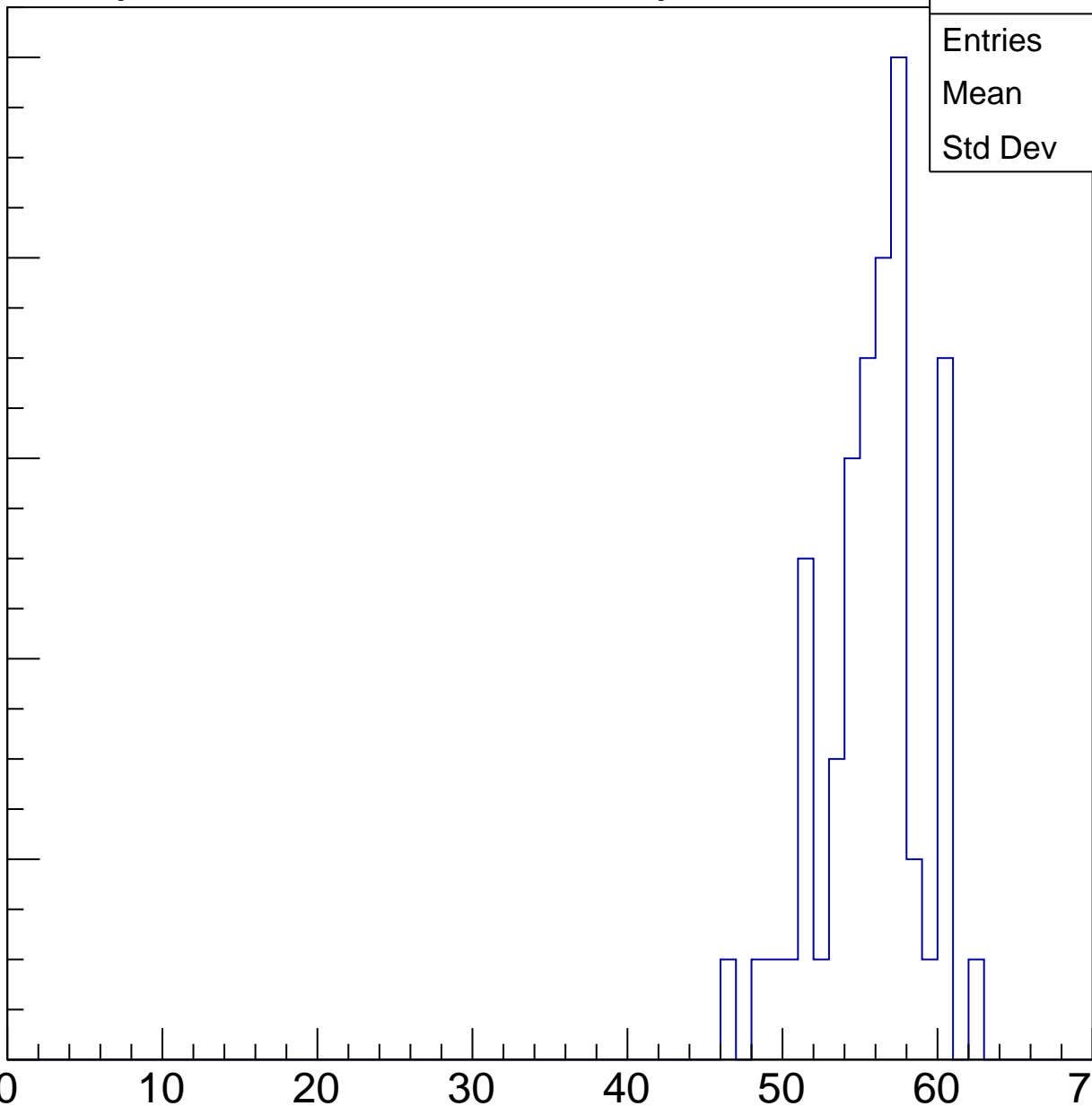
30

40

50

60

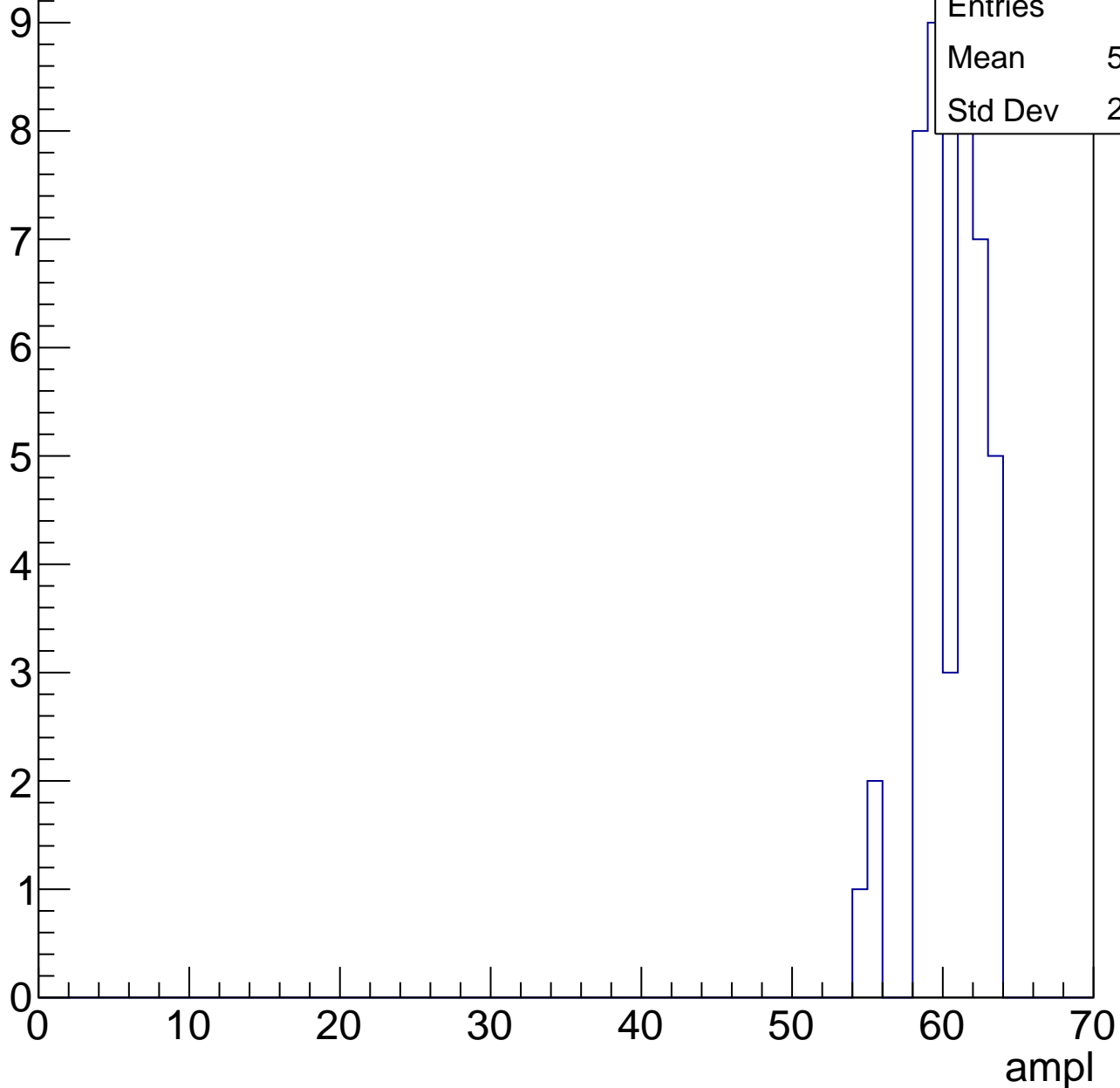
ampl



B0L000S, U2-ch38, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch38, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch38, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch39, adc0

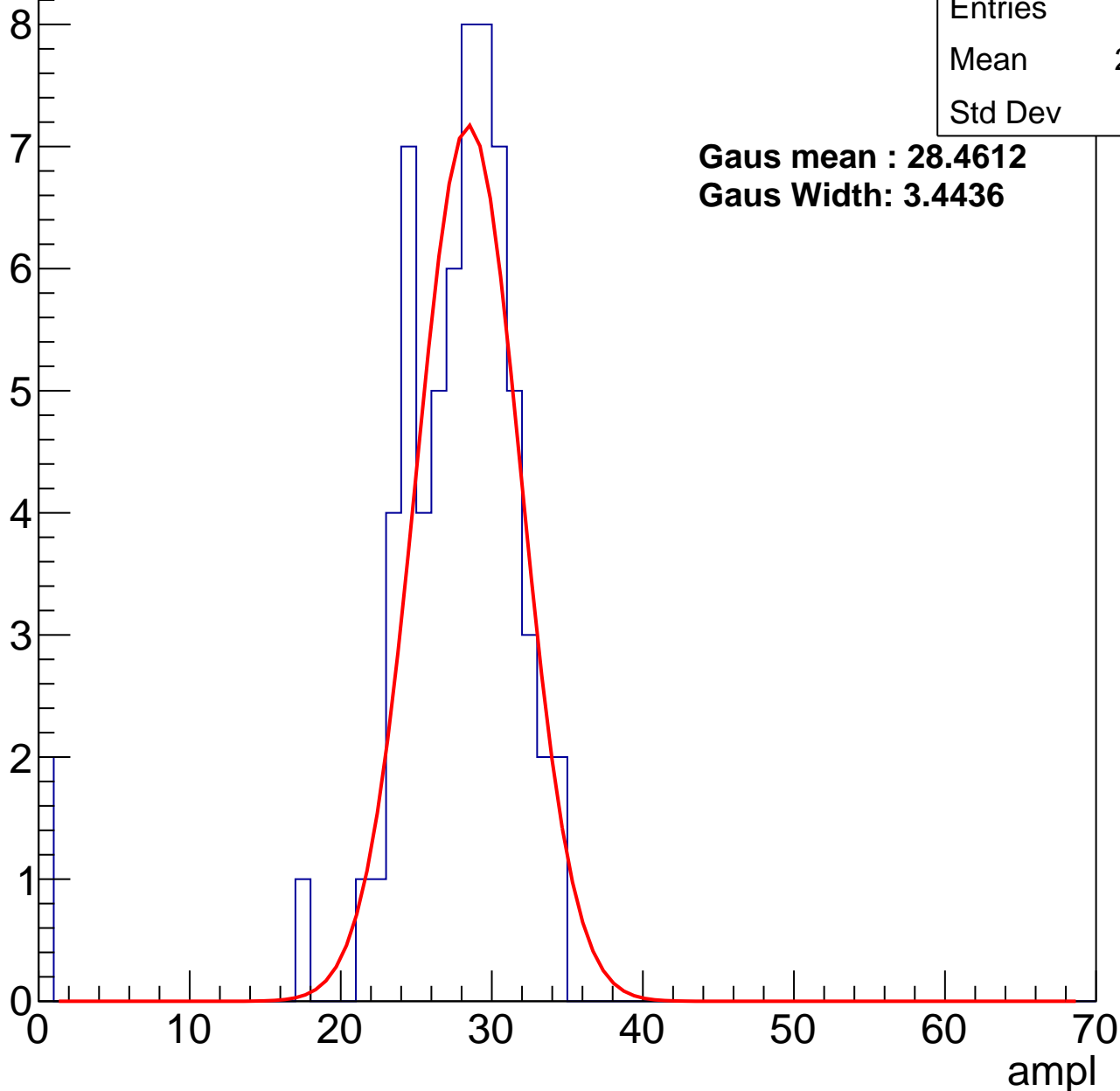
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	26.71
Std Dev	5.76

Gaus mean : 28.4612

Gaus Width: 3.4436



B0L000S, U2-ch39, adc1

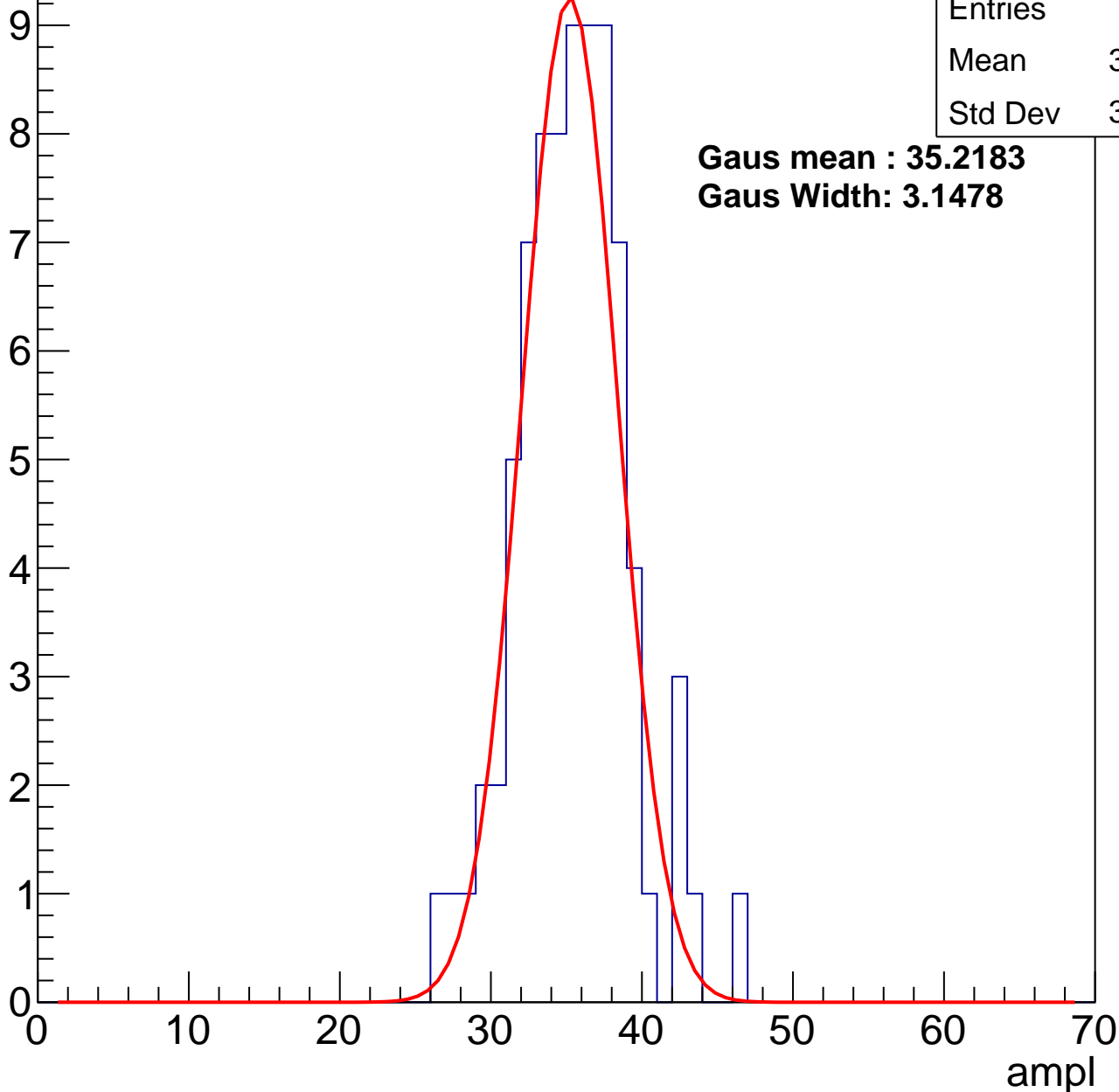
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	79
Mean	34.97
Std Dev	3.607

Gaus mean : 35.2183

Gaus Width: 3.1478



B0L000S, U2-ch39, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	60
Mean	41.87
Std Dev	2.997

Gaus mean : 42.2913

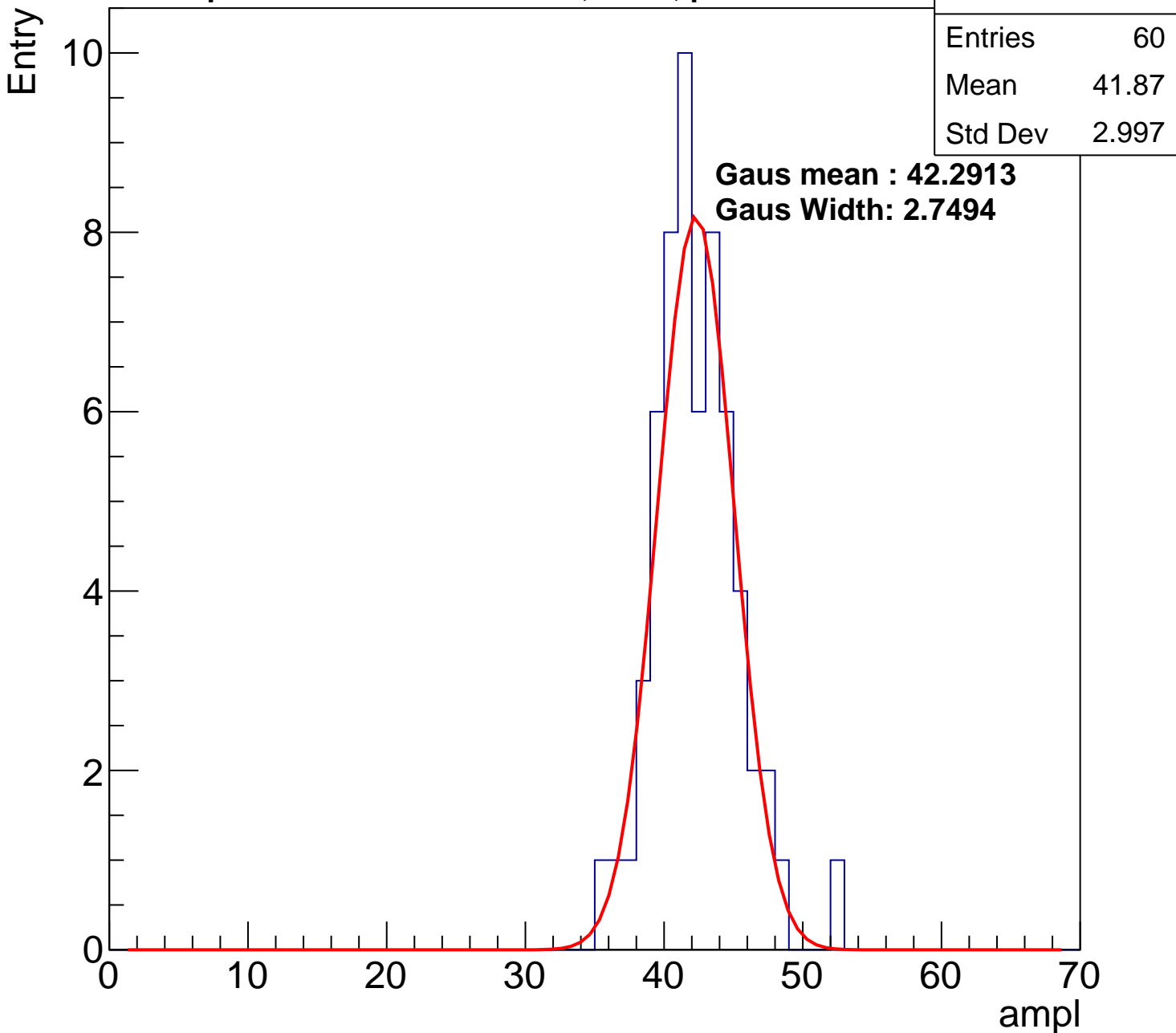
Gaus Width: 2.7494

Entry

10
8
6
4
2
0

ampl

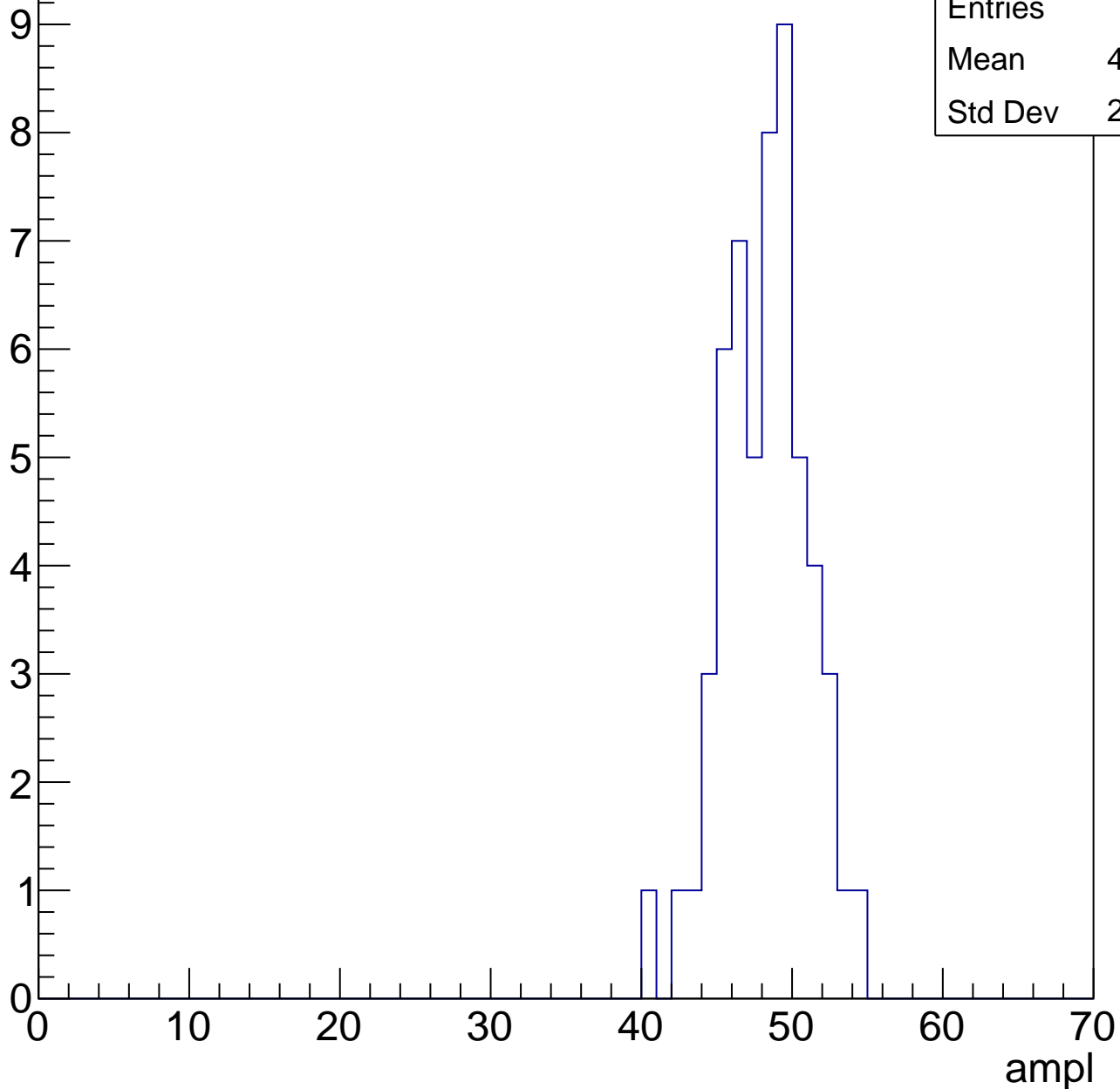
0 10 20 30 40 50 60 70



B0L000S, U2-ch39, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

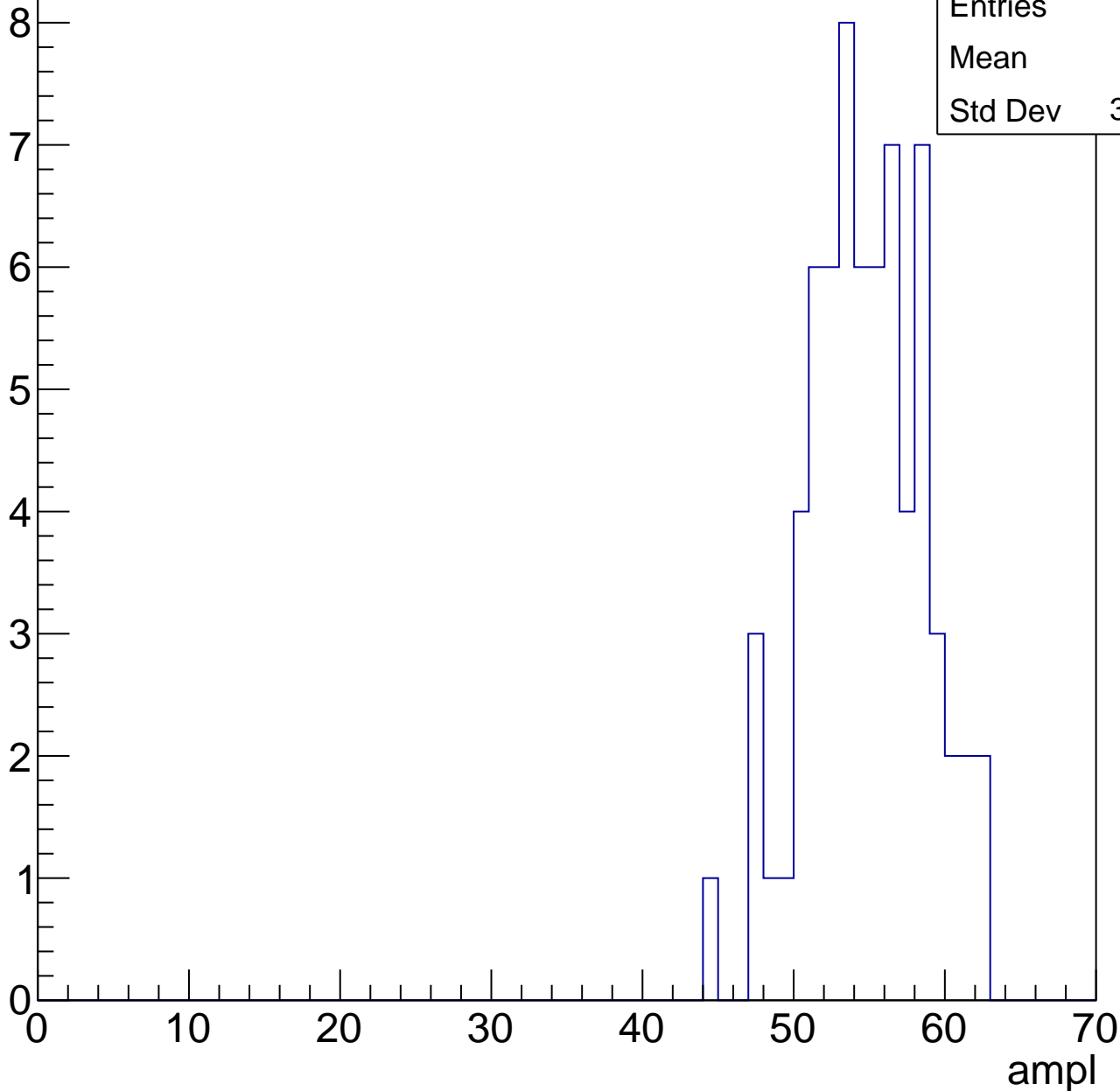


B0L000S, U2-ch39, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	54.3
Std Dev	3.819

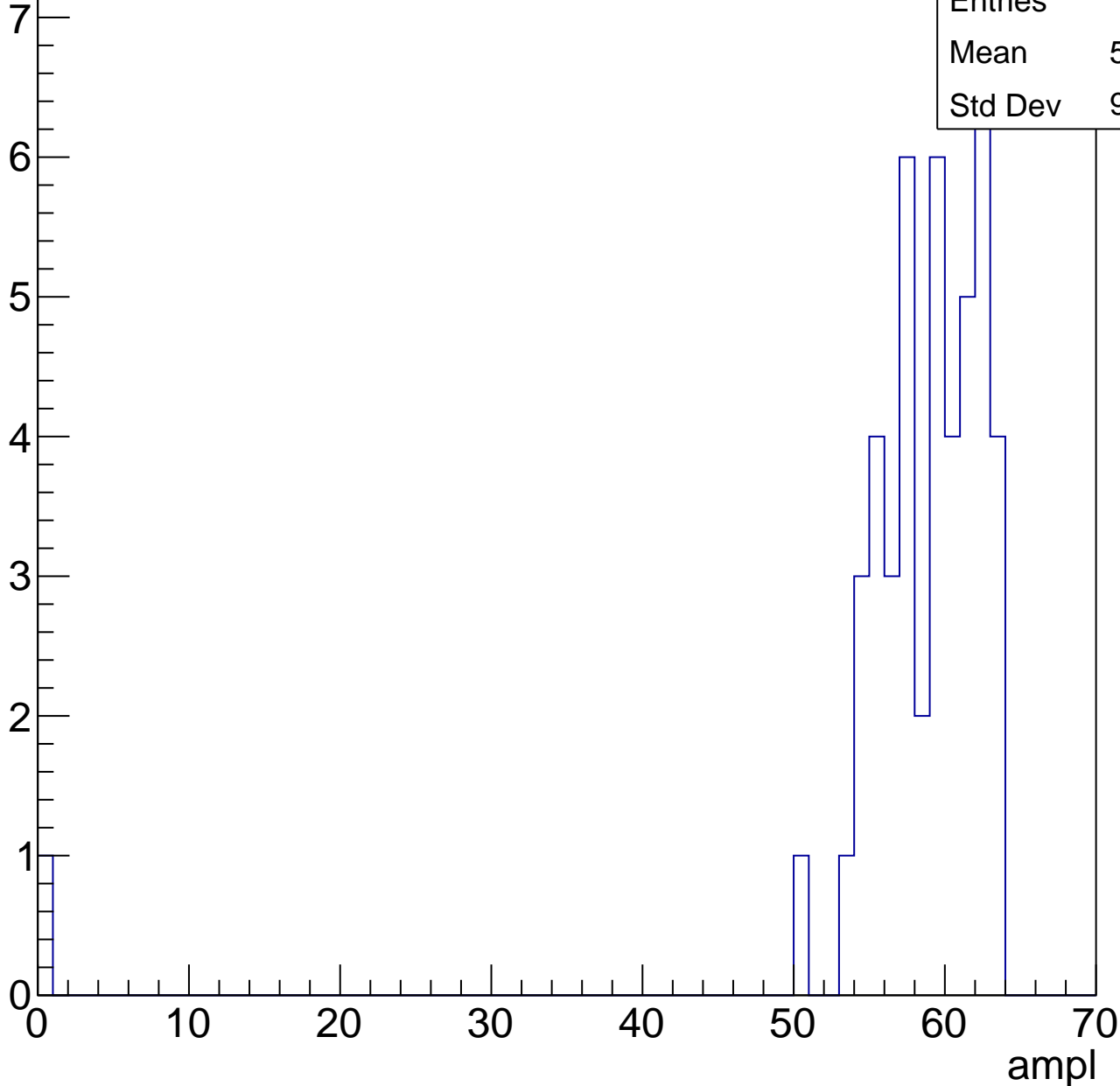


B0L000S, U2-ch39, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	57.36
Std Dev	9.009

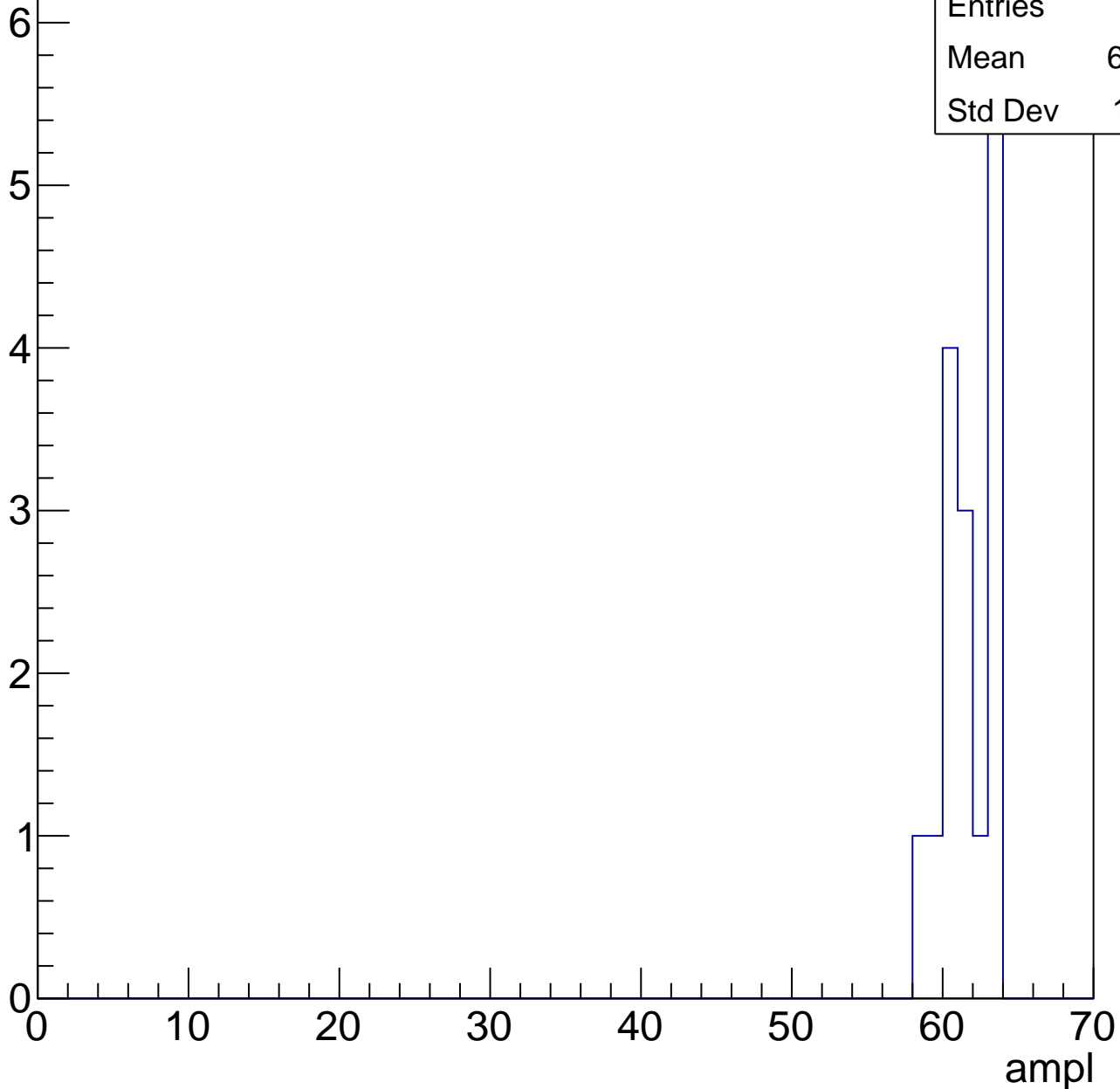


B0L000S, U2-ch39, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.25
Std Dev	1.601



B0L000S, U2-ch39, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch40, adc0

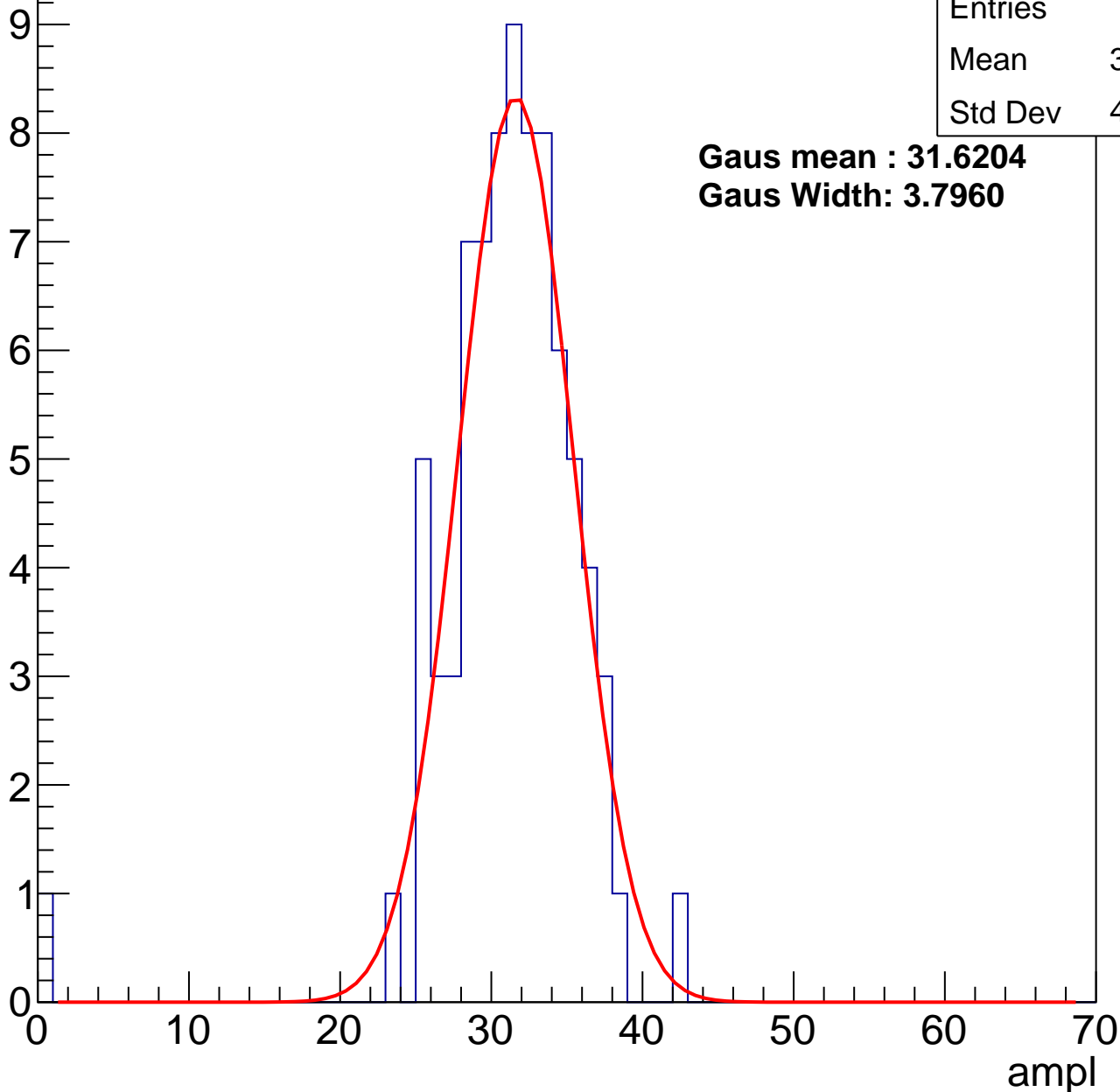
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	30.74
Std Dev	4.967

Gaus mean : 31.6204

Gaus Width: 3.7960



B0L000S, U2-ch40, adc1

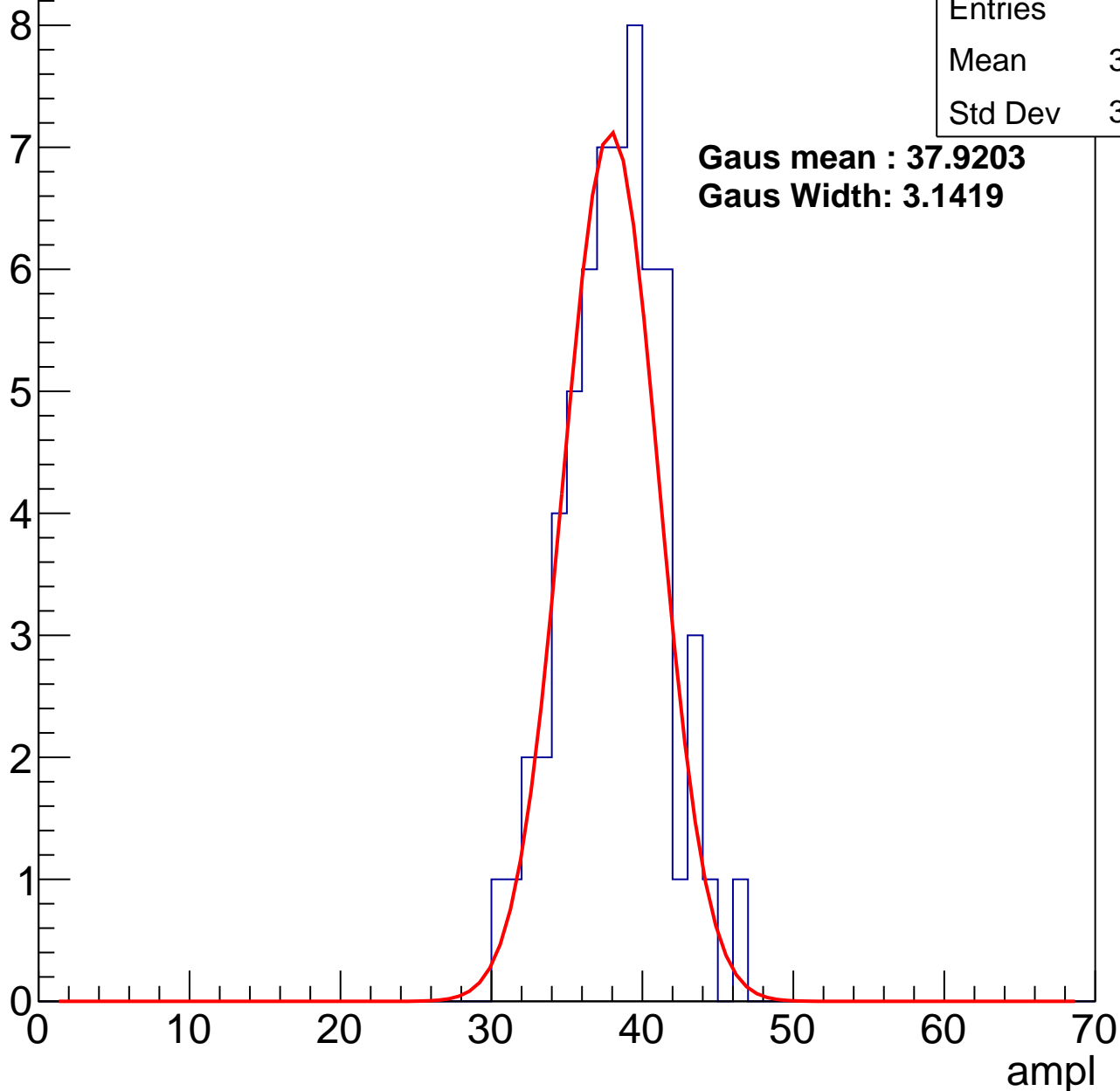
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.74
Std Dev	3.259

Gaus mean : 37.9203

Gaus Width: 3.1419



B0L000S, U2-ch40, adc2

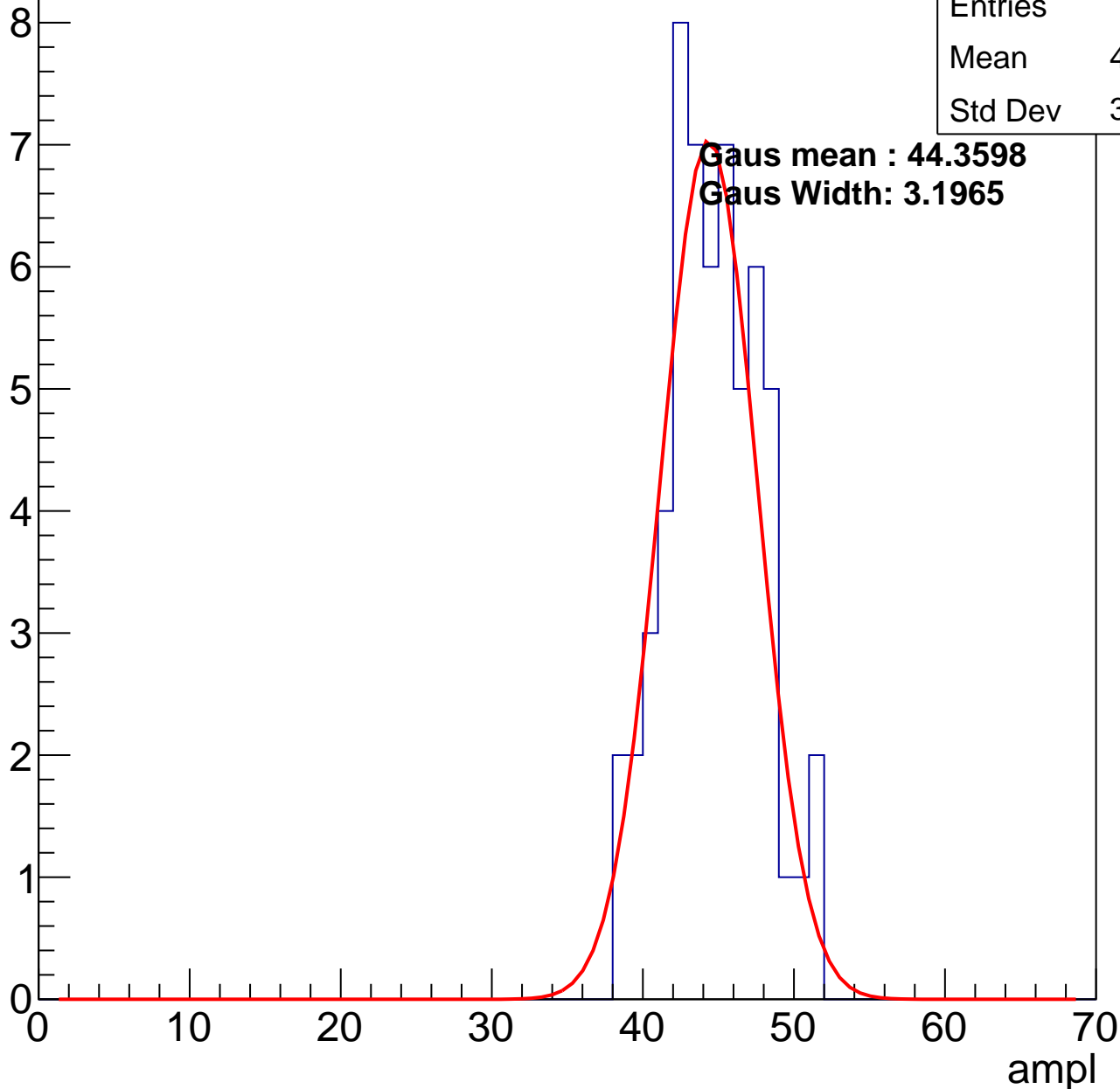
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	44.19
Std Dev	3.089

Gaus mean : 44.3598

Gaus Width: 3.1965

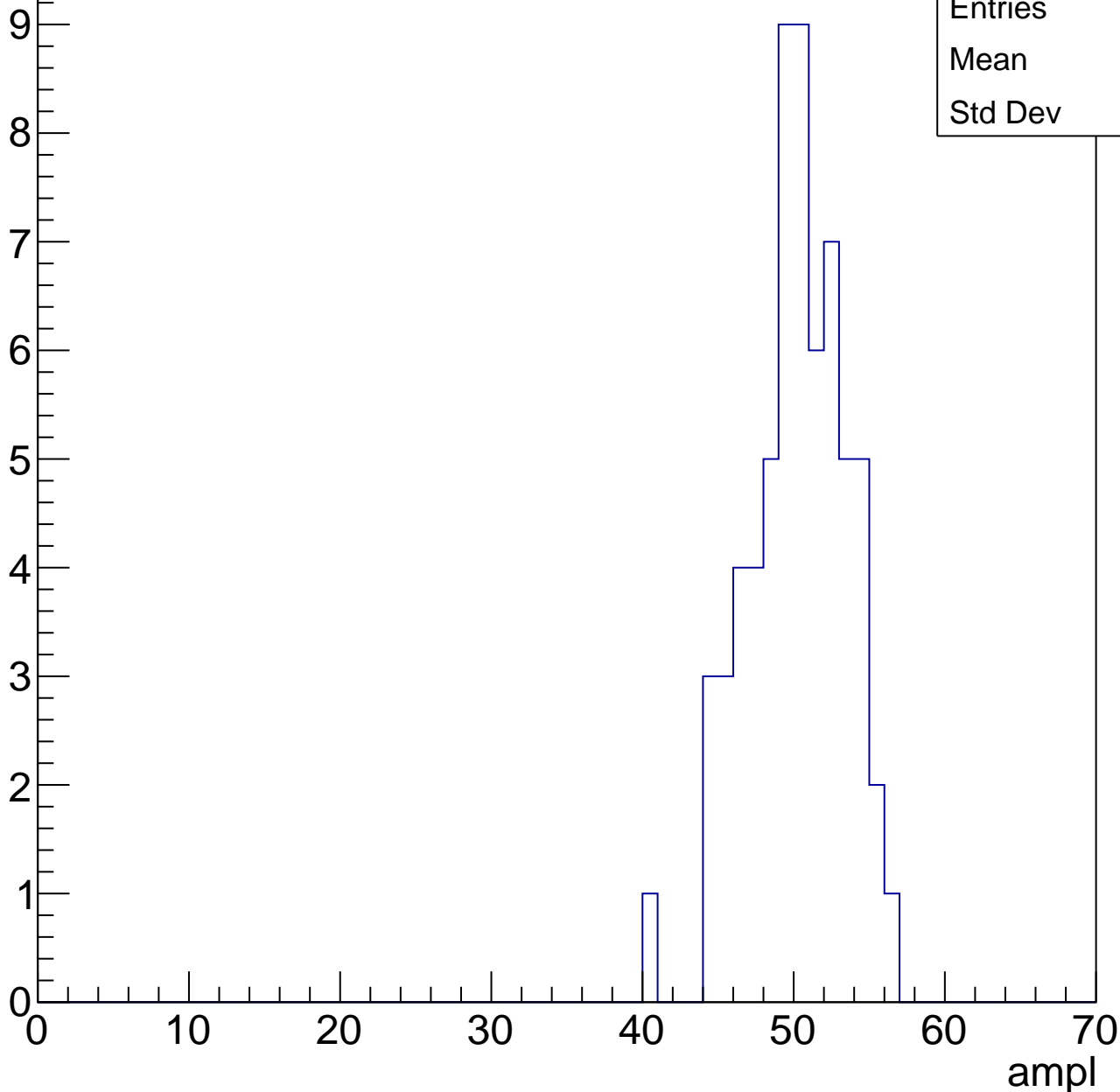


B0L000S, U2-ch40, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.7
Std Dev	3.19

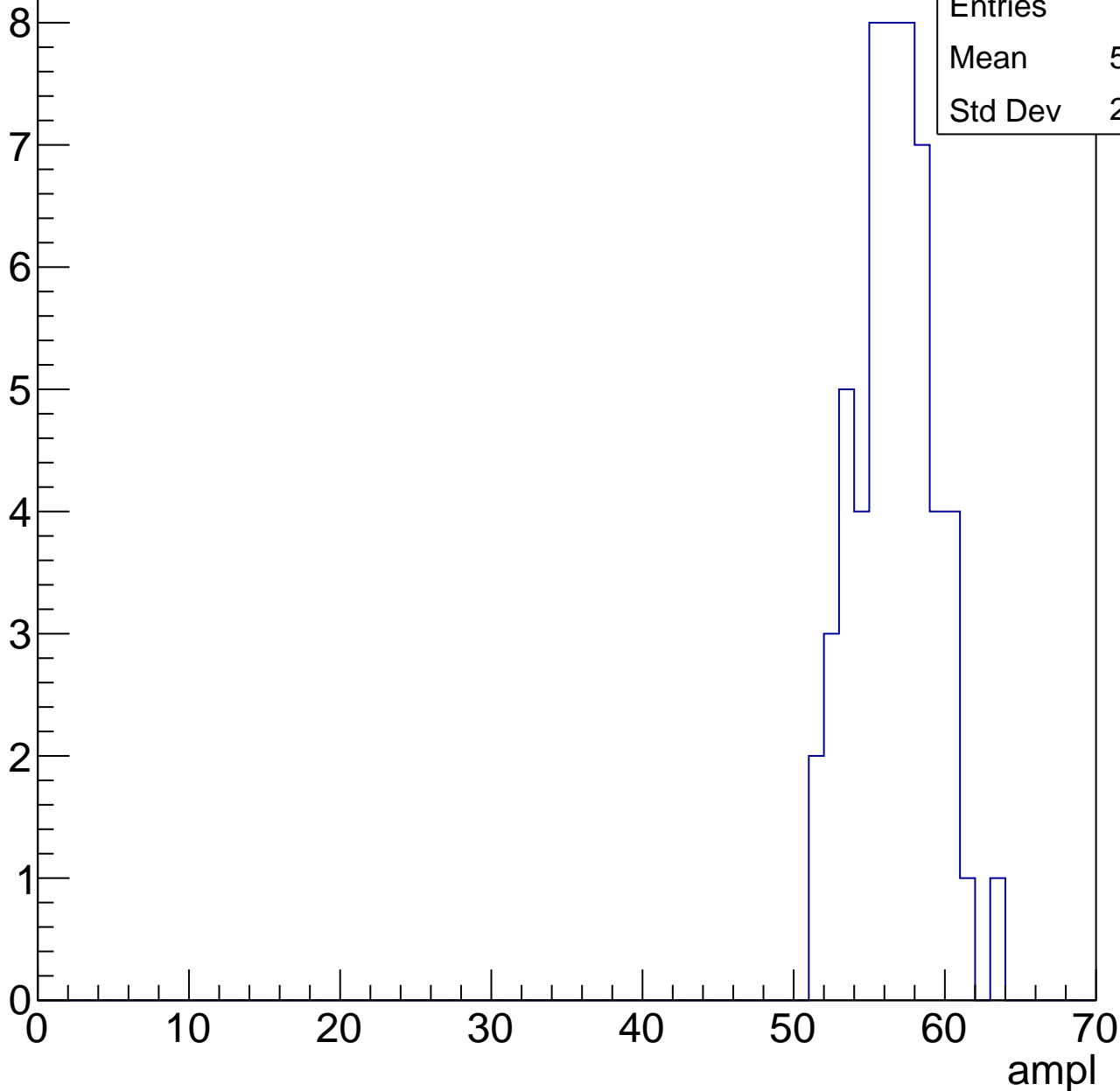


B0L000S, U2-ch40, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.16
Std Dev	2.613

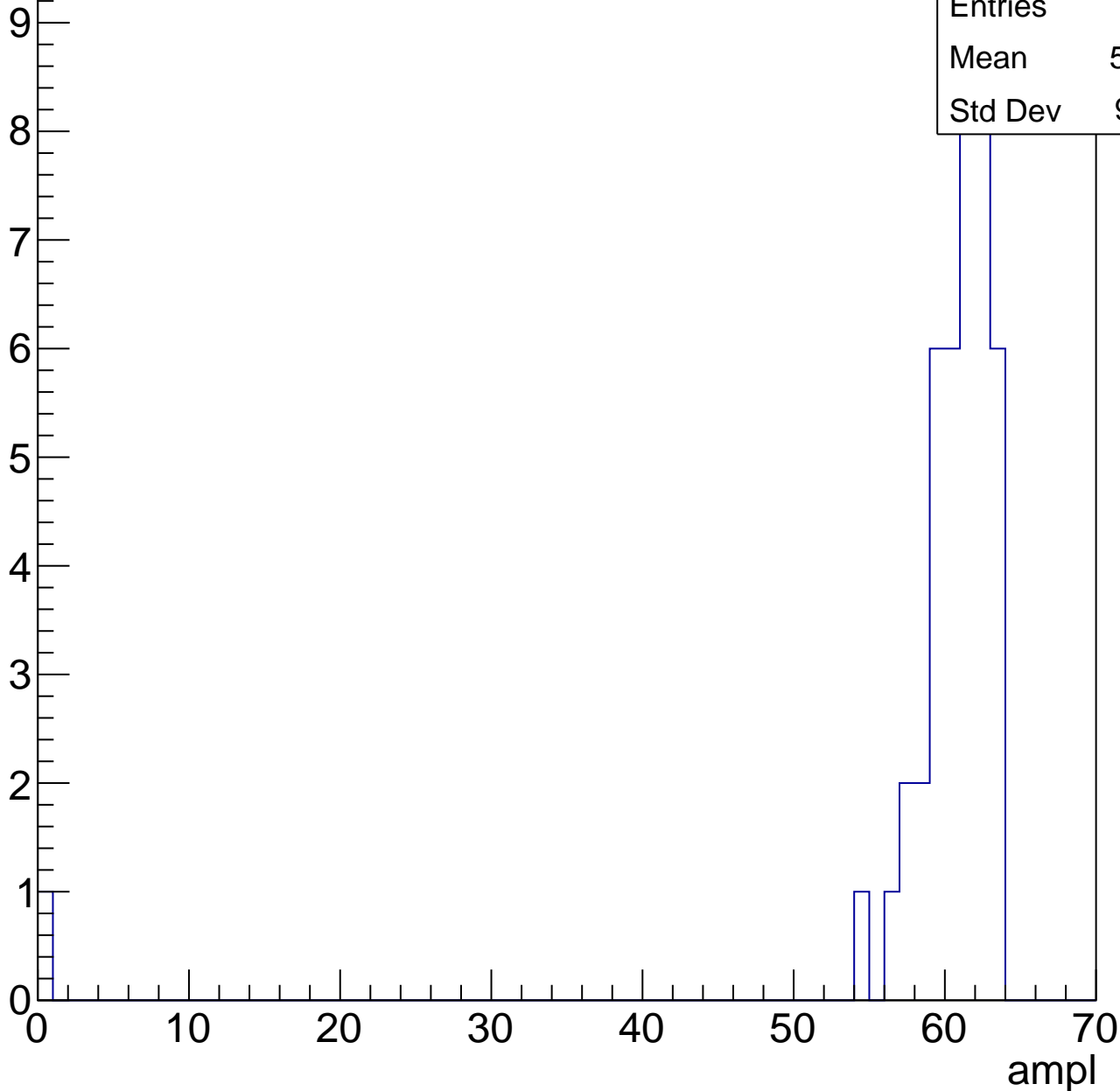


B0L000S, U2-ch40, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

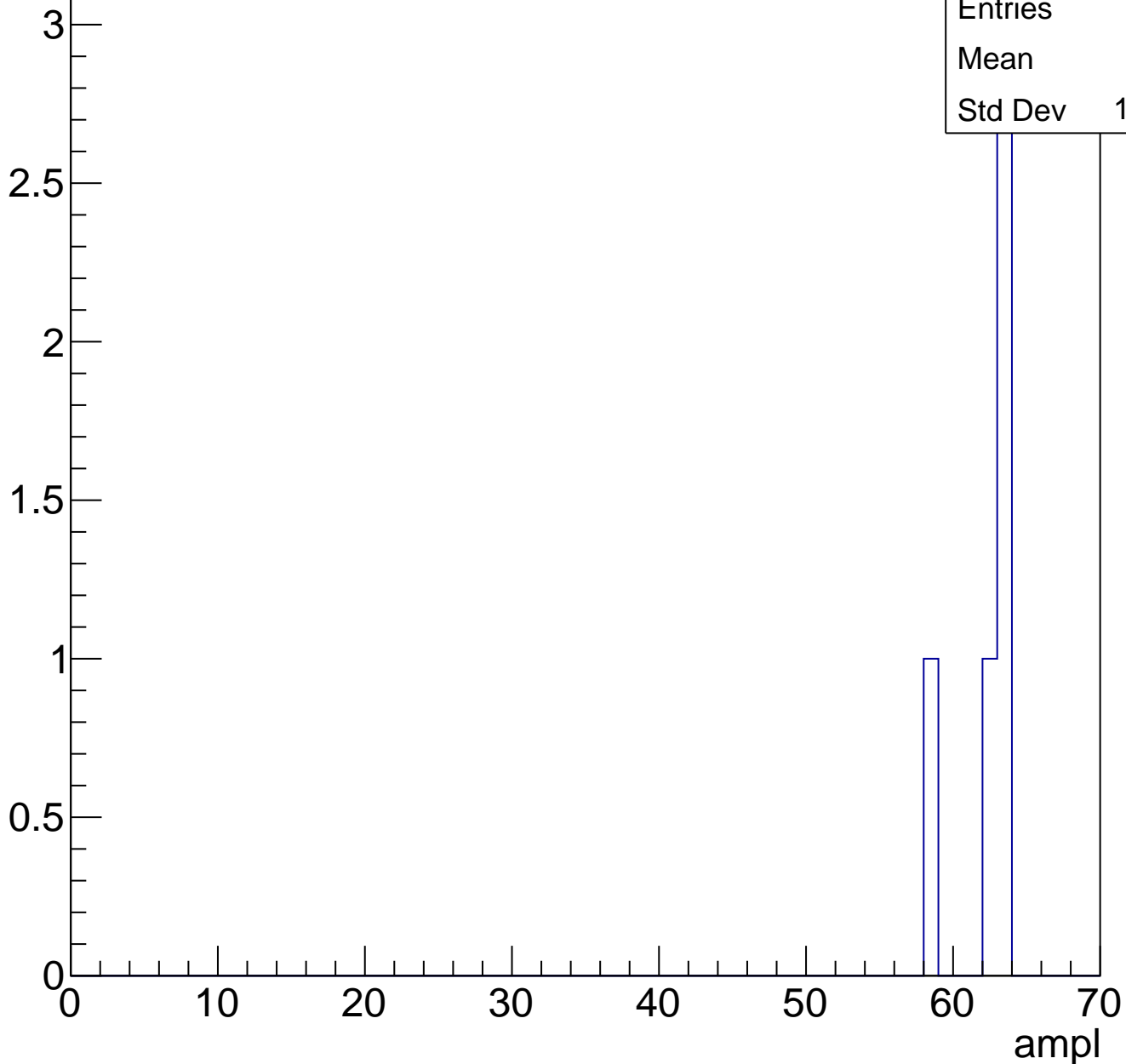
Entries	43
Mean	59.05
Std Dev	9.331



B0L000S, U2-ch40, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch40, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

B0L000S, U2-ch41, adc0

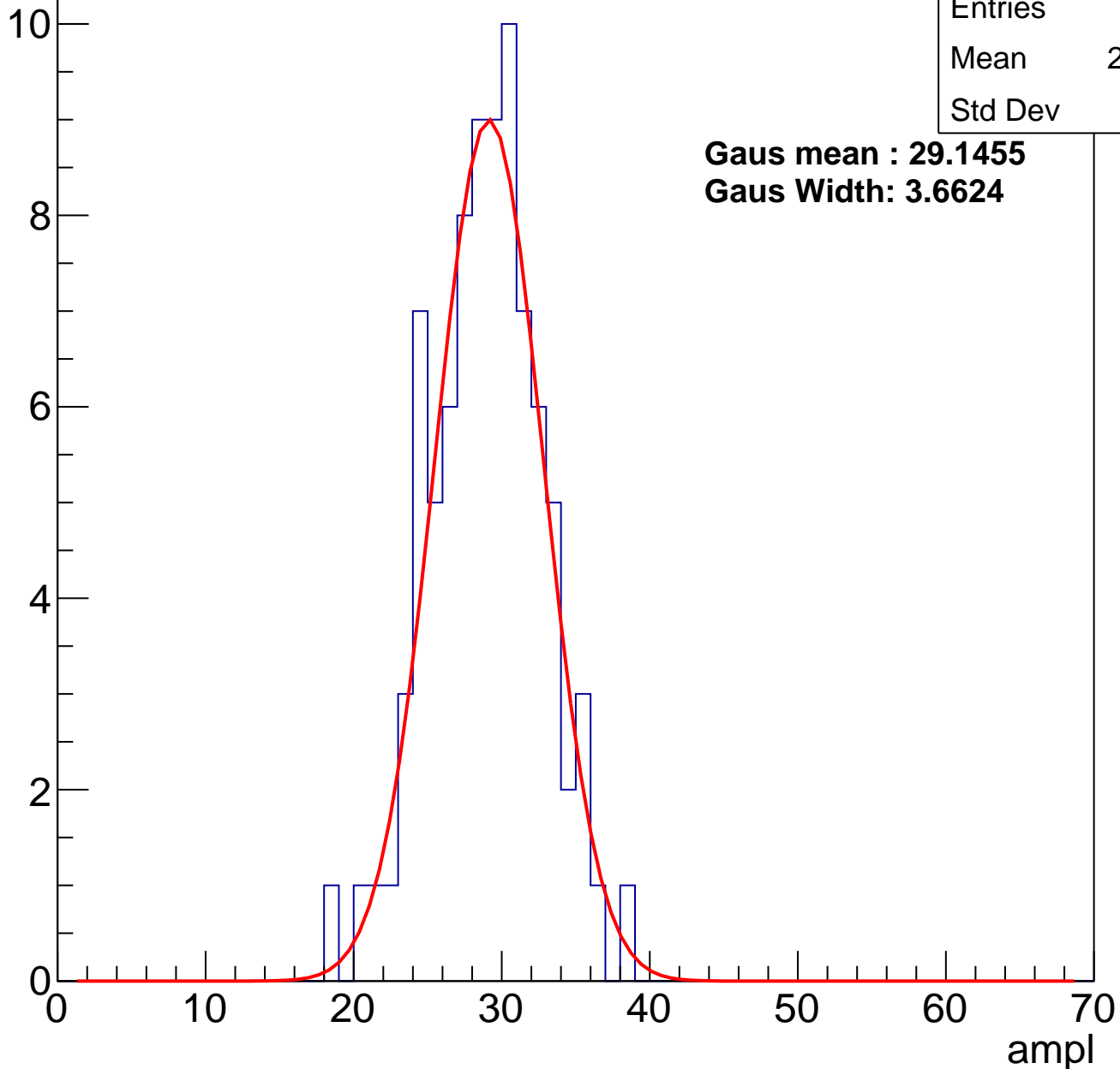
calib_packv5_042523_0143.root, FC#5, port B1

Entries	86
Mean	28.48
Std Dev	3.75

Gaus mean : 29.1455

Gaus Width: 3.6624

Entry



B0L000S, U2-ch41, adc1

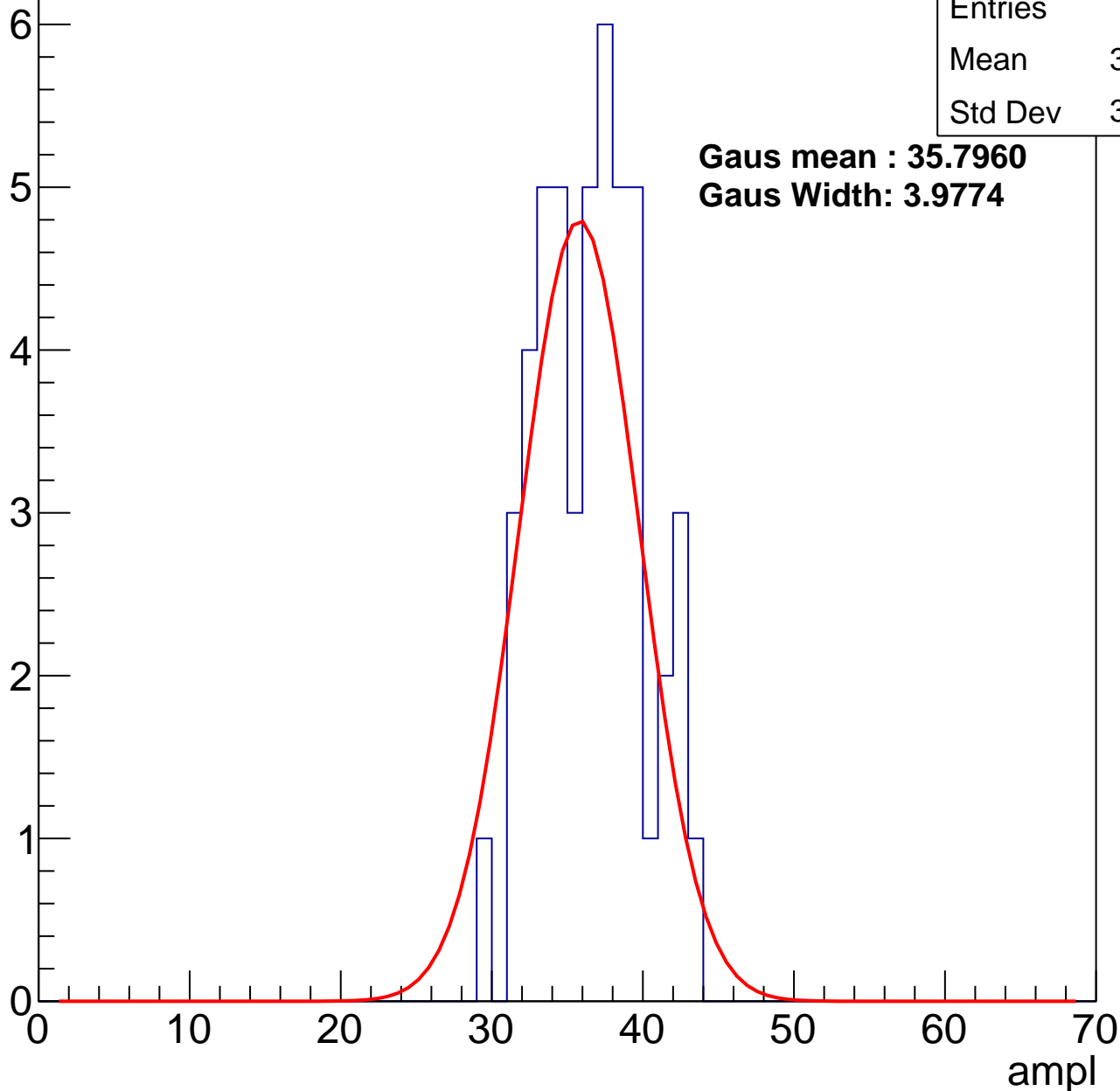
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.08
Std Dev	3.349

Gaus mean : 35.7960

Gaus Width: 3.9774



B0L000S, U2-ch41, adc2

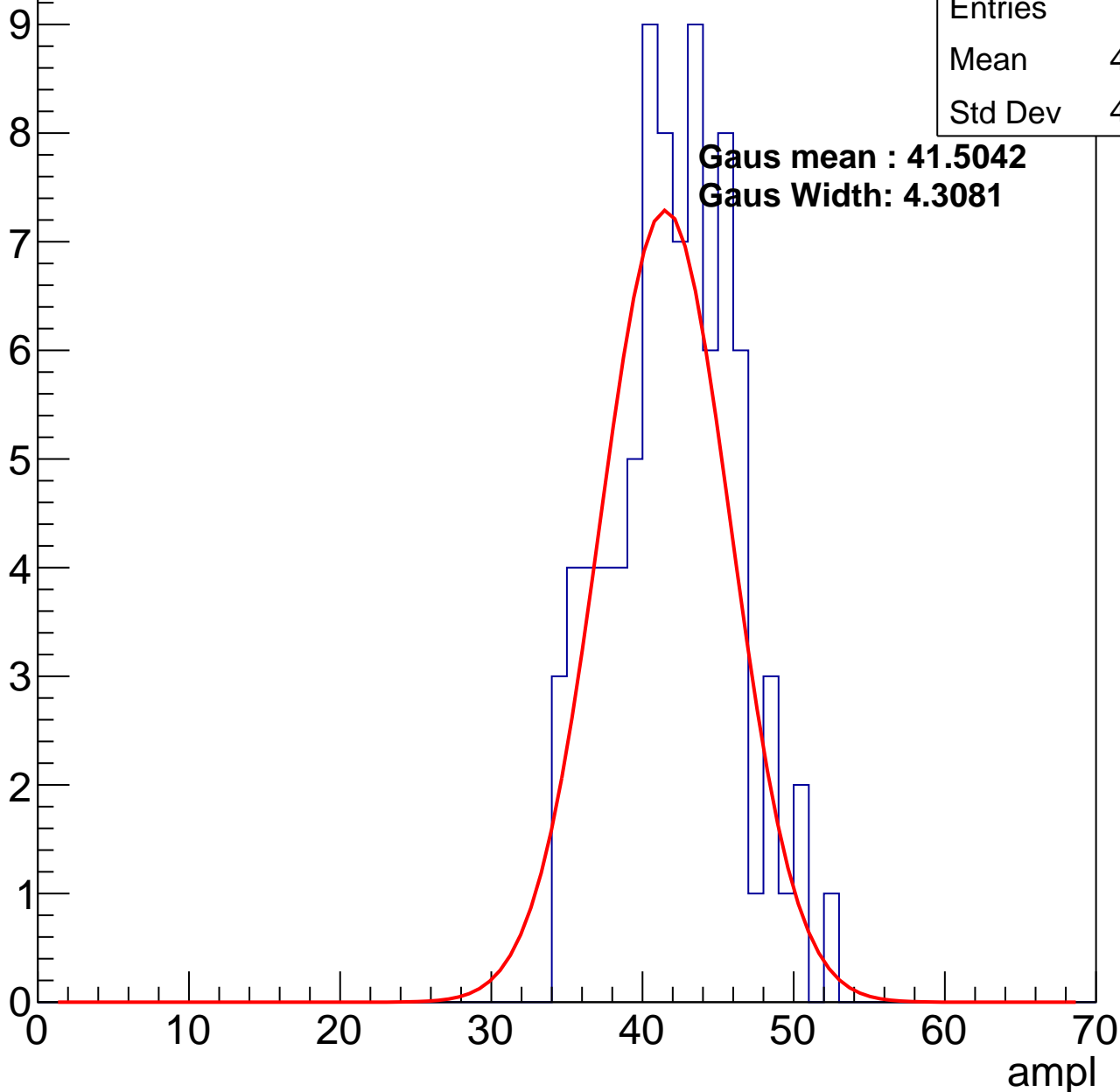
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	85
Mean	41.67
Std Dev	4.039

Gaus mean : 41.5042

Gaus Width: 4.3081

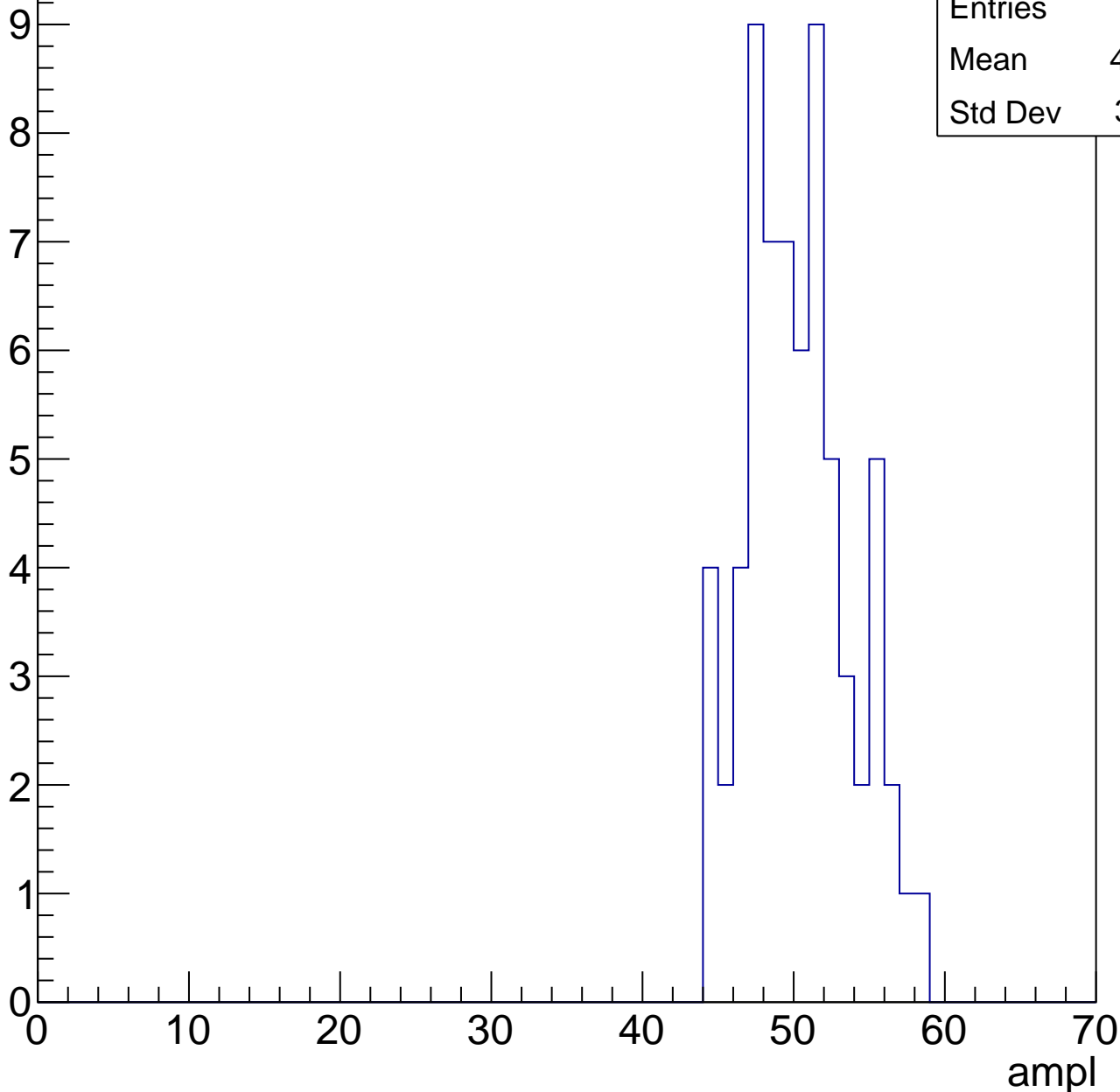


B0L000S, U2-ch41, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.85
Std Dev	3.391

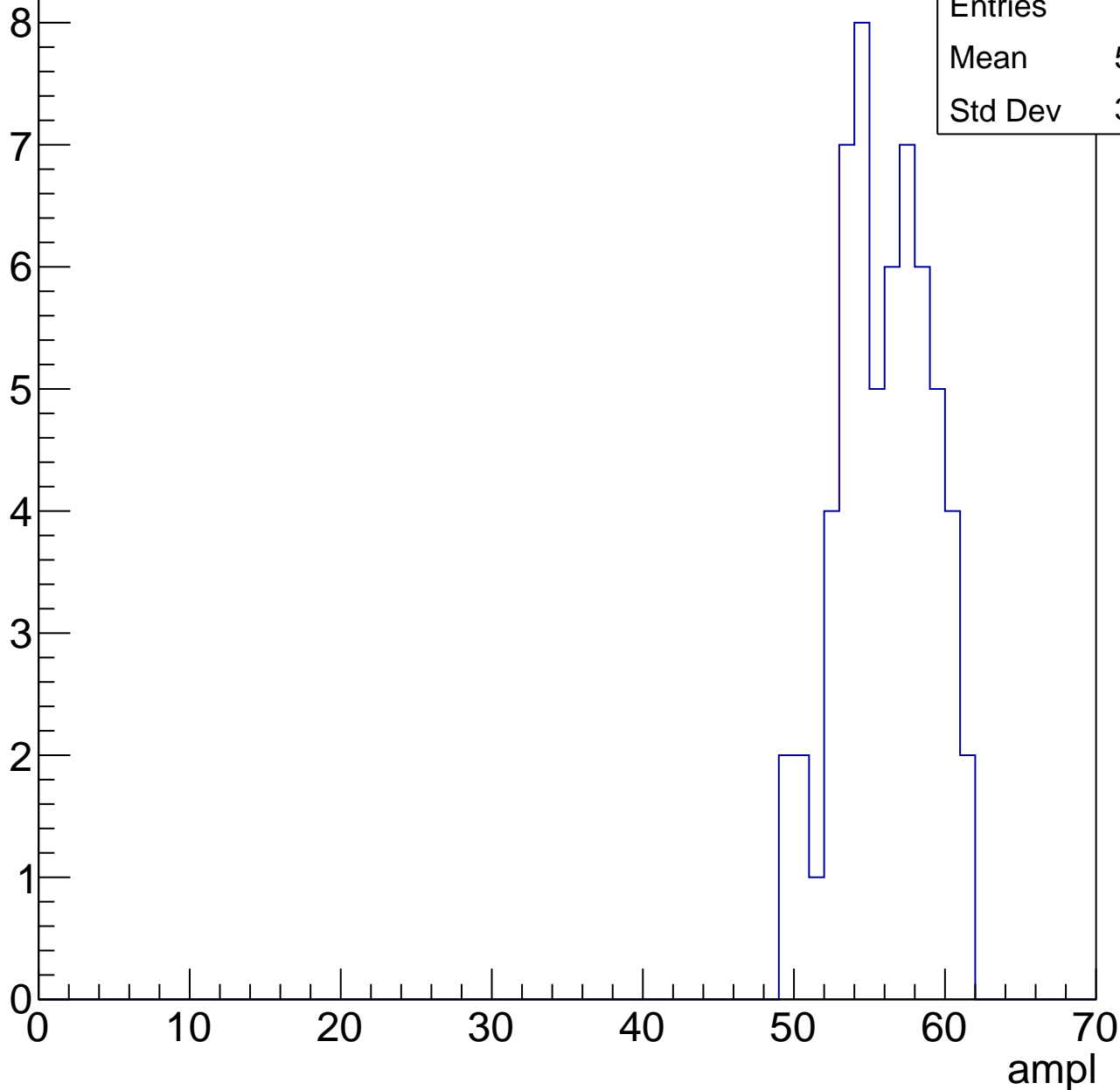


B0L000S, U2-ch41, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.51
Std Dev	3.011

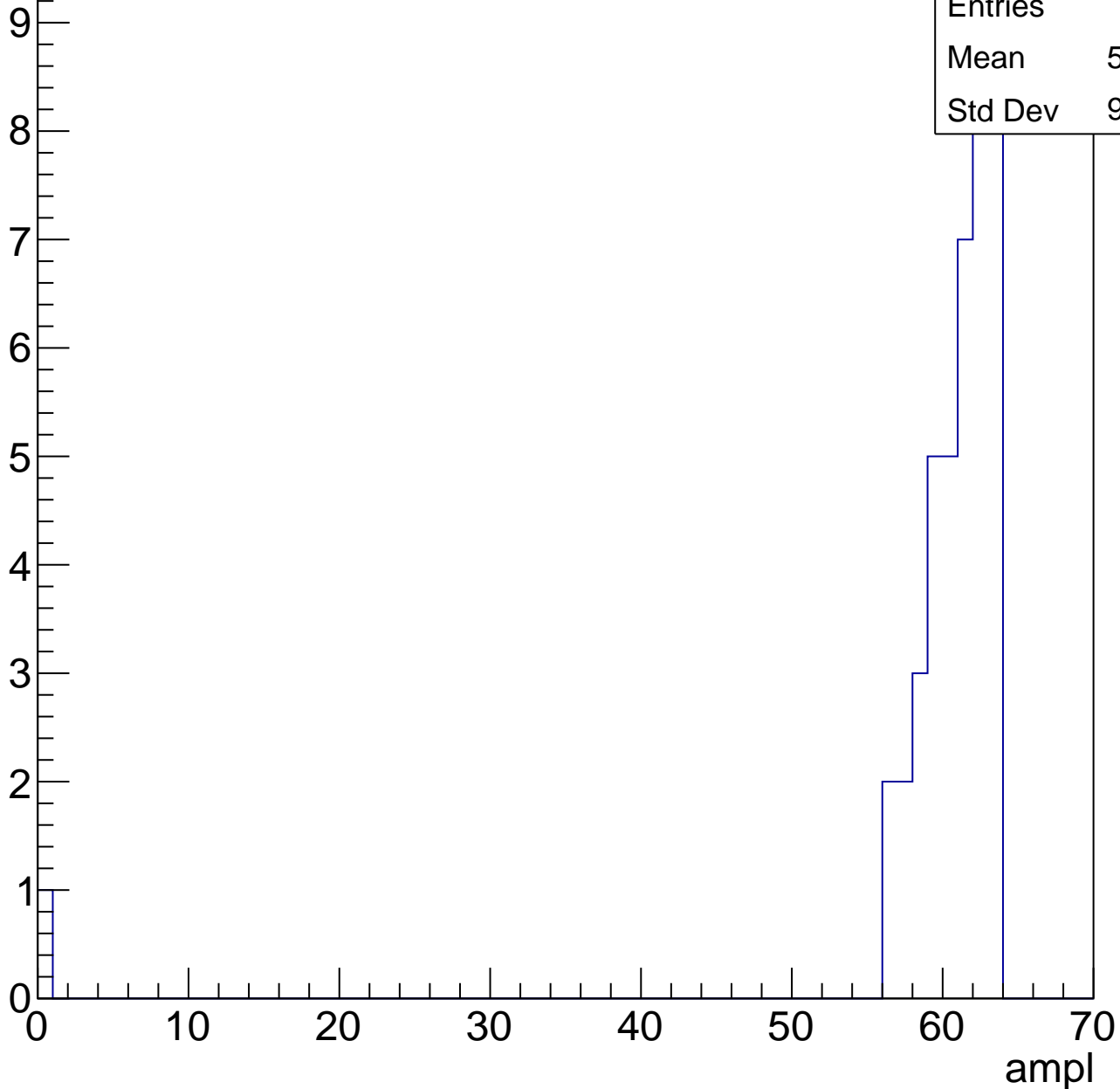


B0L000S, U2-ch41, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	43
Mean	59.23
Std Dev	9.358



B0L000S, U2-ch41, adc6

calib_packv5_042523_0143.root, FC#5, port B1

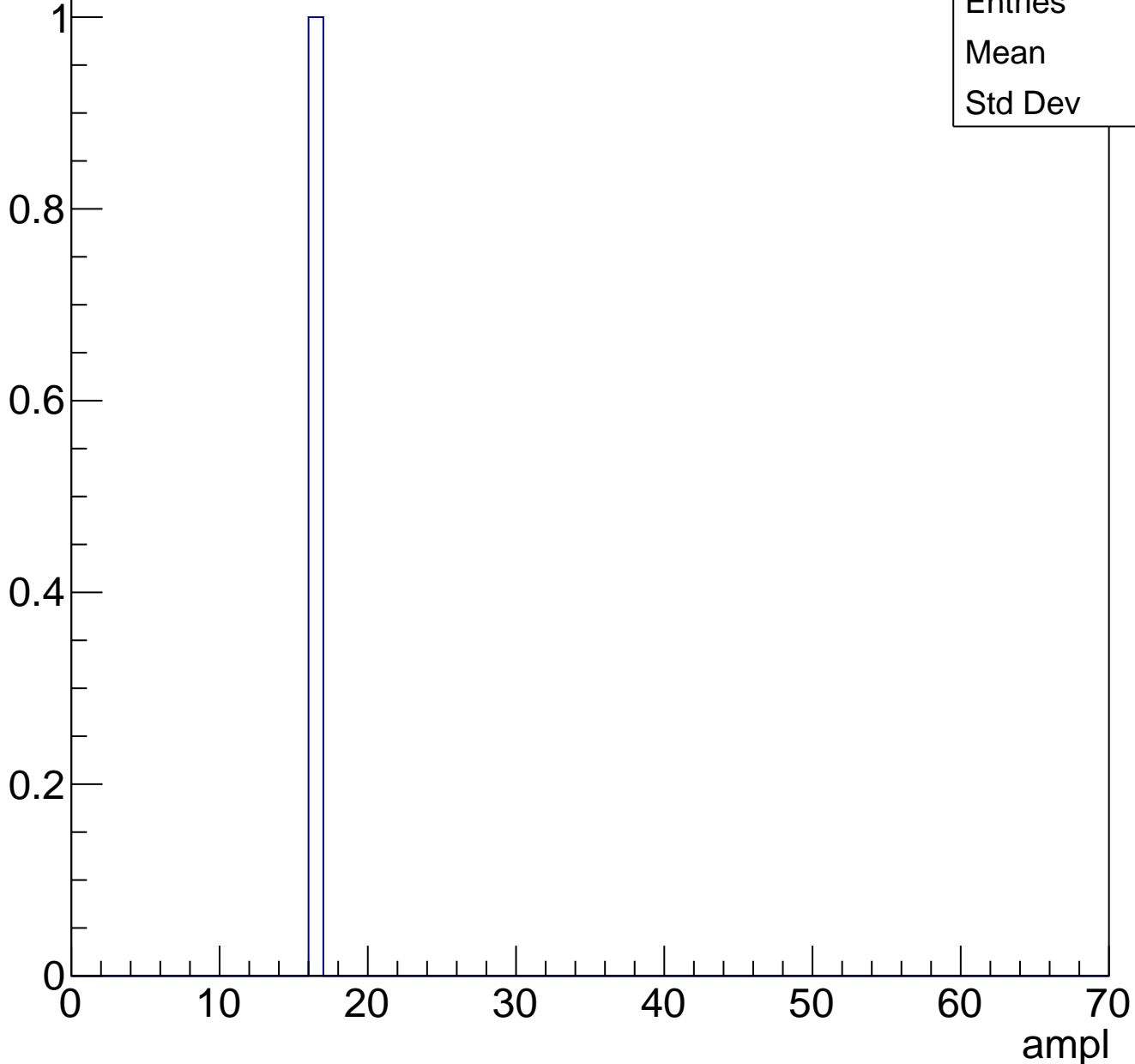
Entry



B0L000S, U2-ch41, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	16
Std Dev	0

B0L000S, U2-ch42, adc0

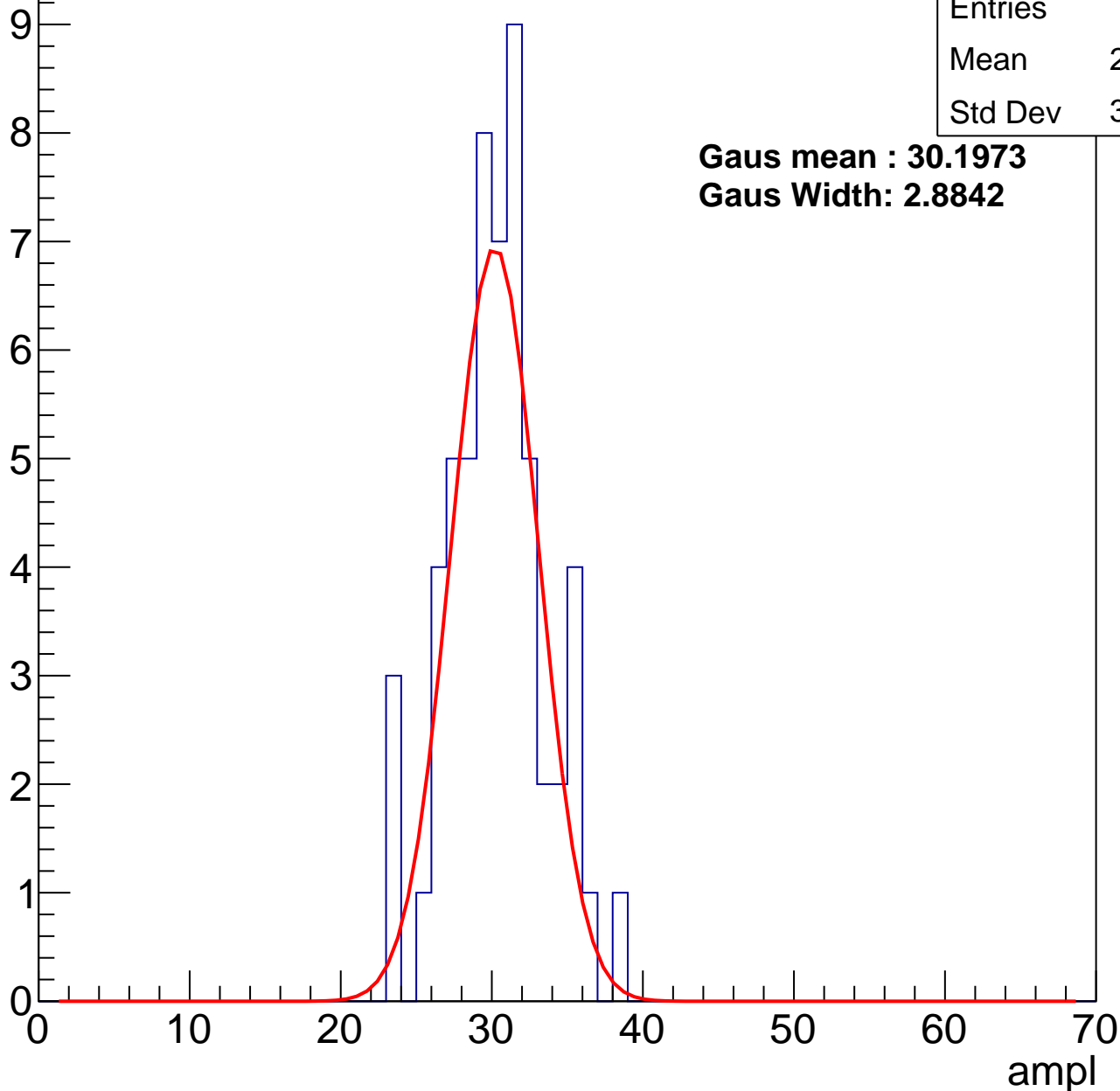
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	29.86
Std Dev	3.209

Gaus mean : 30.1973

Gaus Width: 2.8842



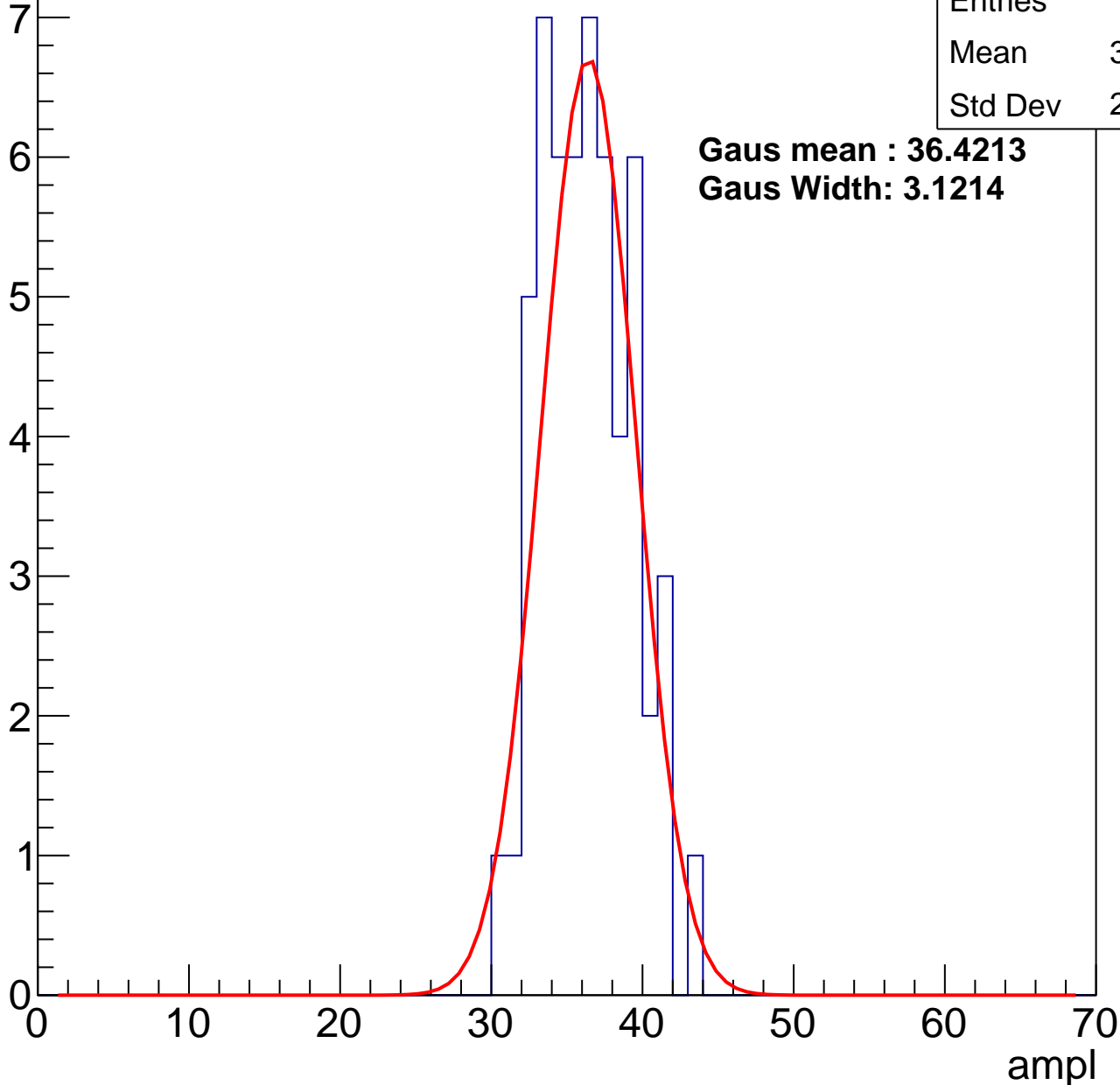
B0L000S, U2-ch42, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	35.85
Std Dev	2.907

Gaus mean : 36.4213
Gaus Width: 3.1214



B0L000S, U2-ch42, adc2

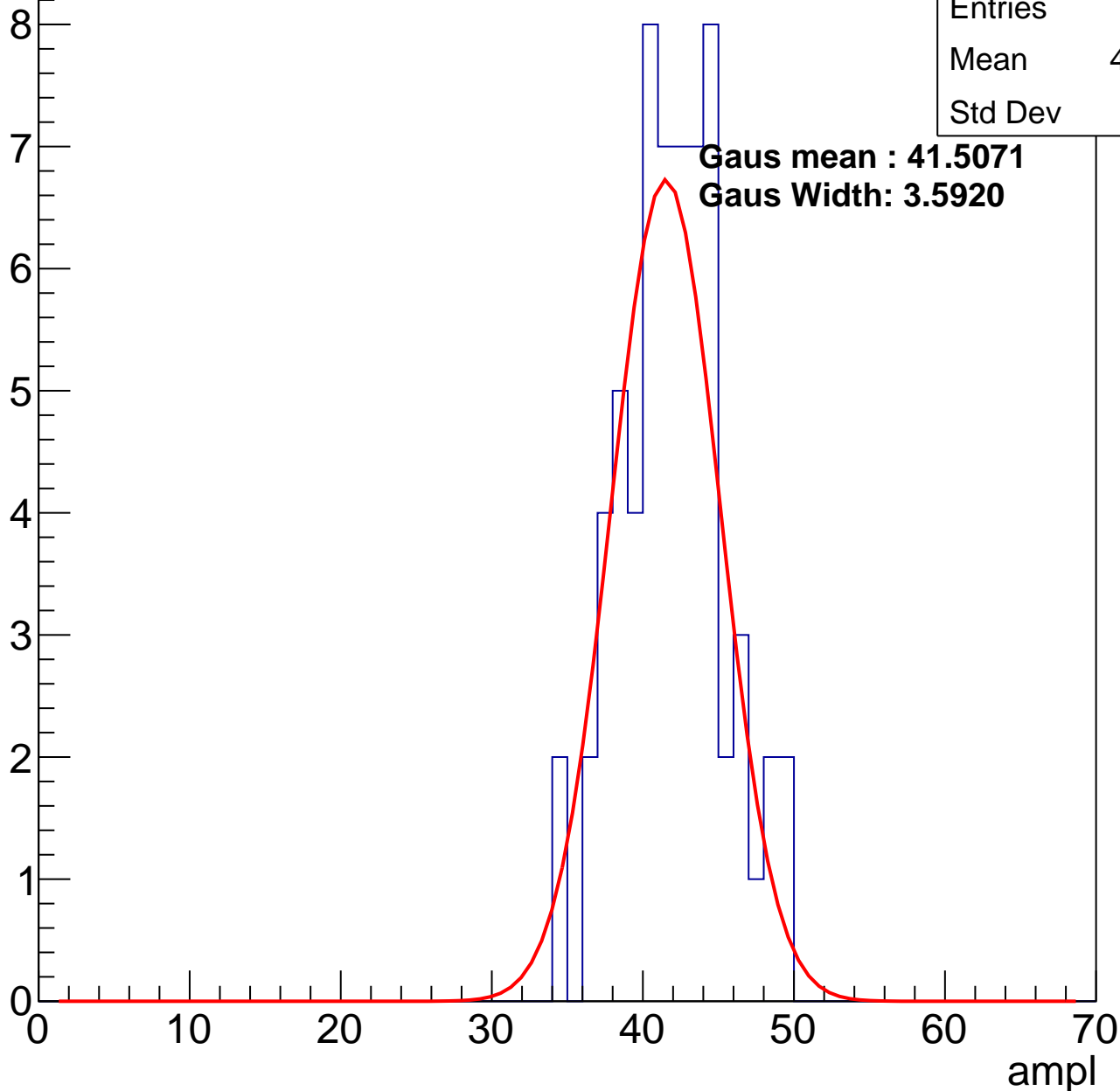
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.52
Std Dev	3.4

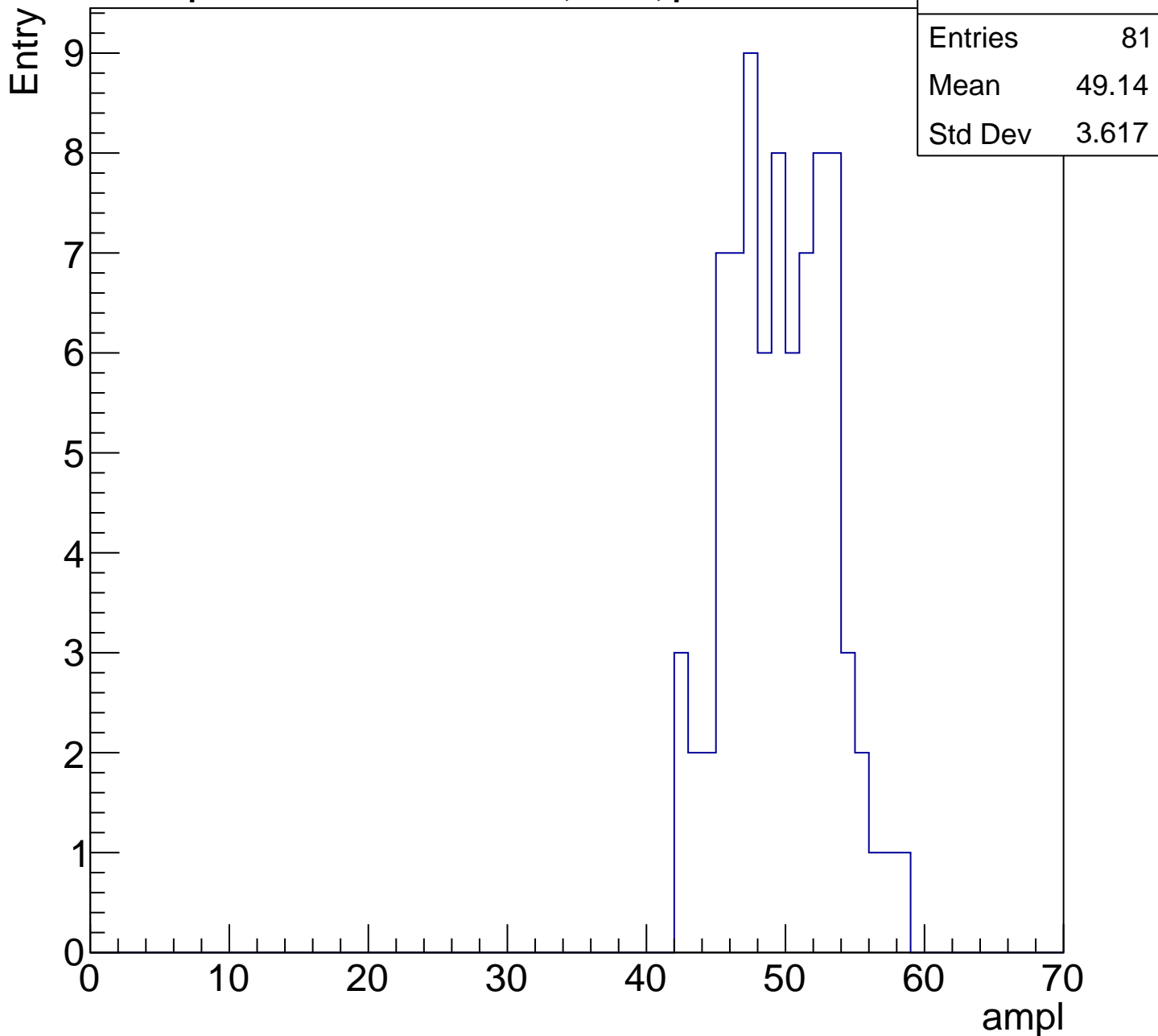
Gaus mean : 41.5071

Gaus Width: 3.5920



B0L000S, U2-ch42, adc3

calib_packv5_042523_0143.root, FC#5, port B1

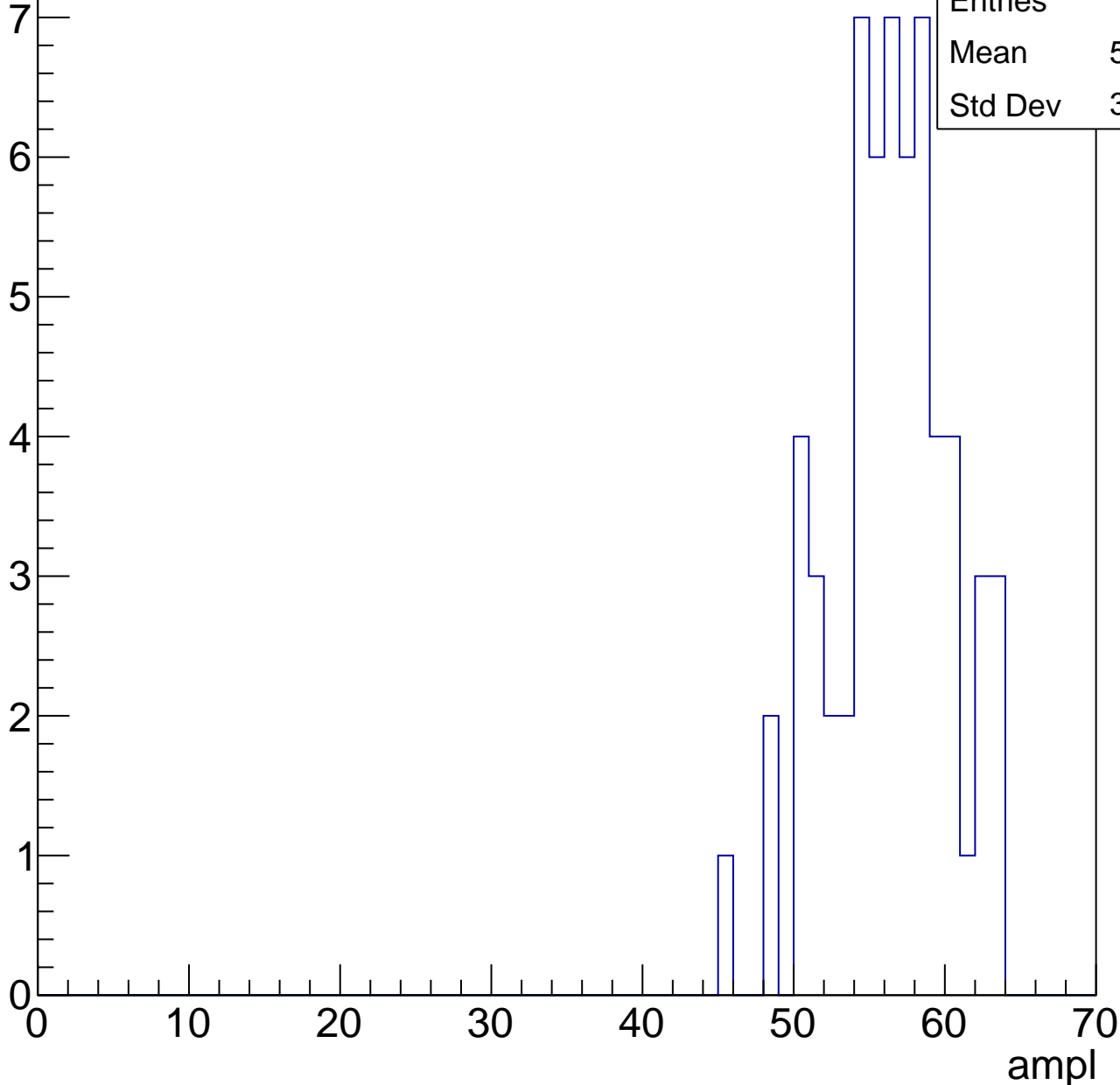


B0L000S, U2-ch42, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

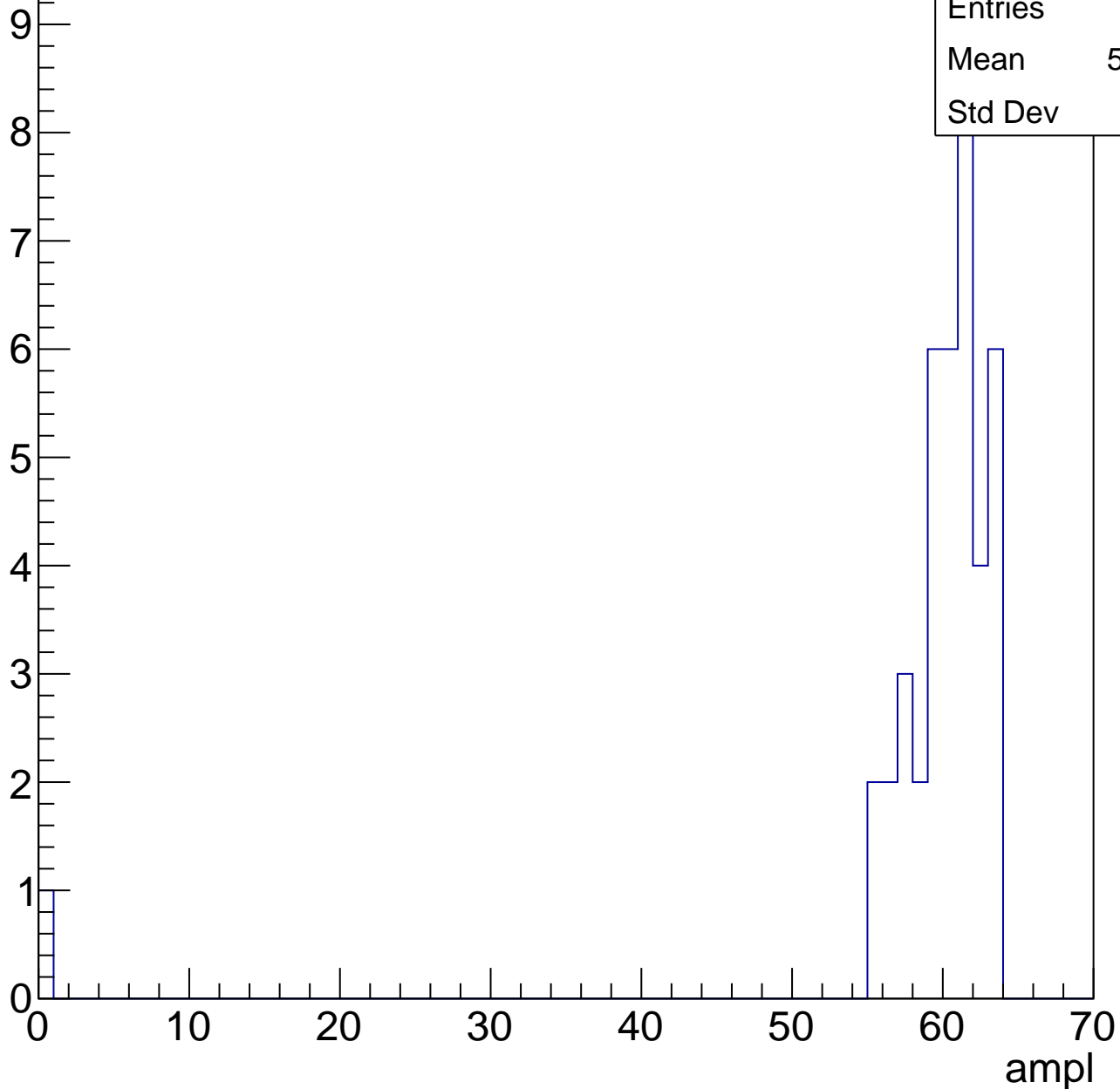
Entries	62
Mean	55.87
Std Dev	3.945



B0L000S, U2-ch42, adc5

calib_packv5_042523_0143.root, FC#5, port B1

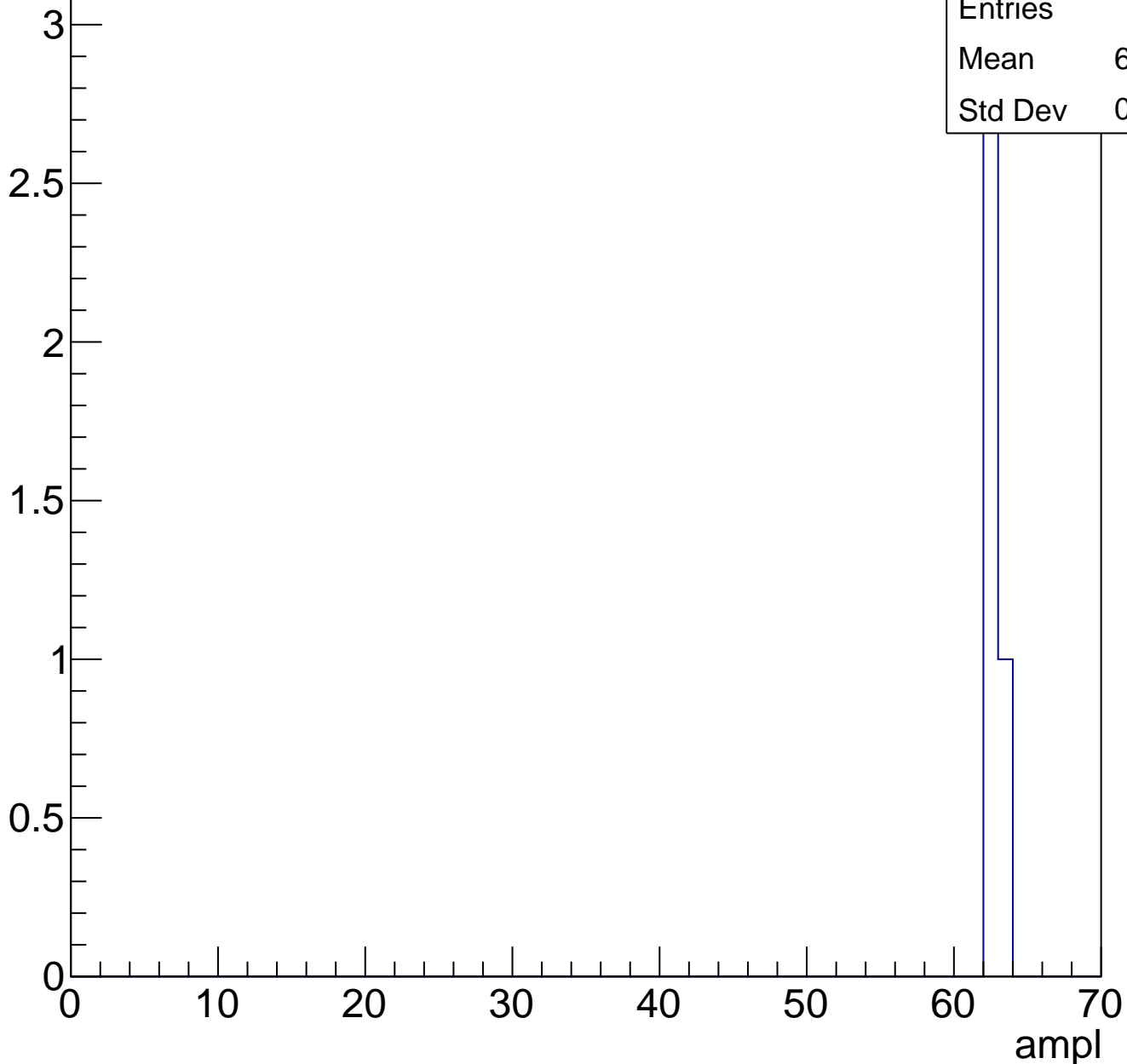
Entry



B0L000S, U2-ch42, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch42, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch43, adc0

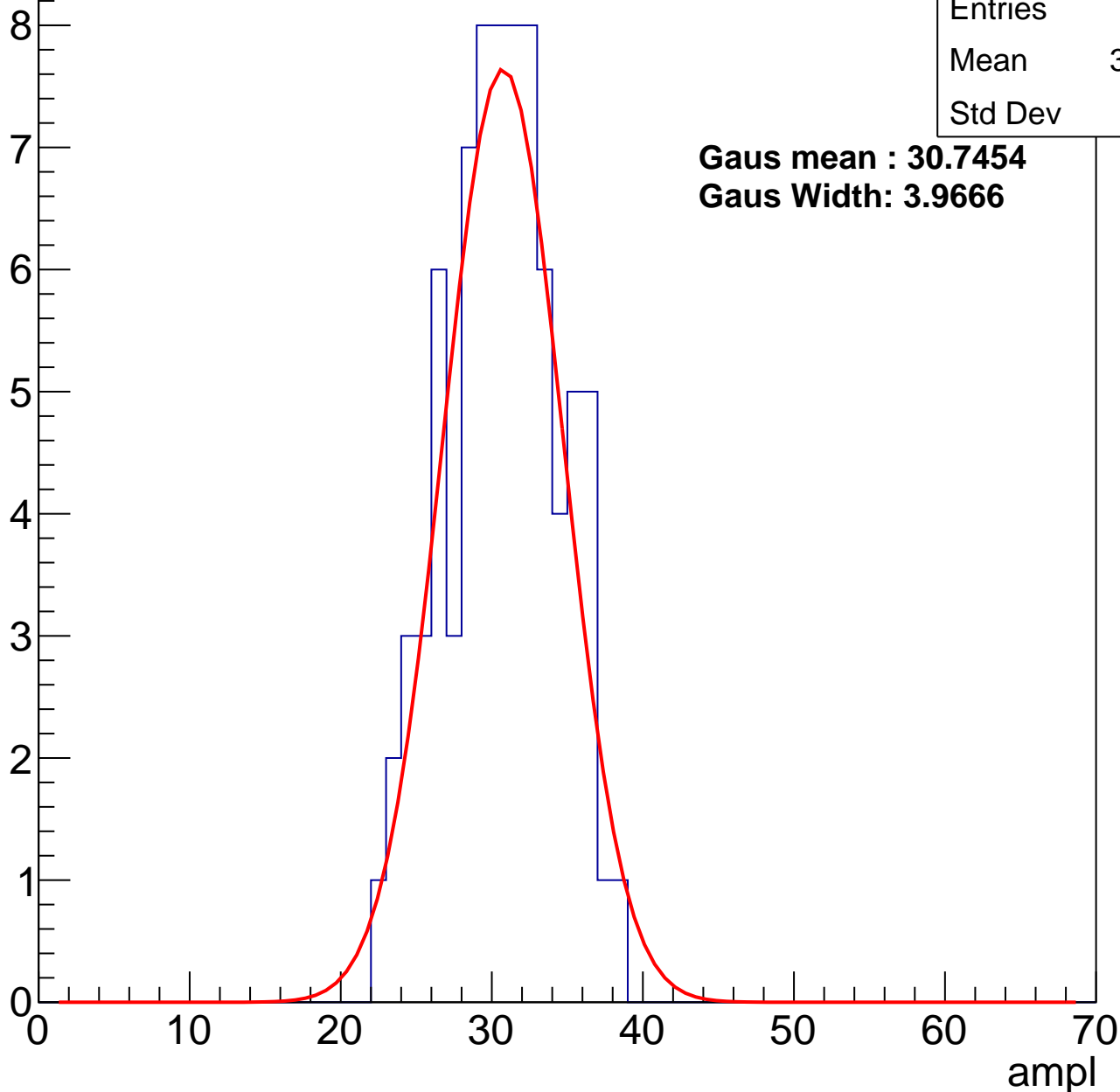
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	79
Mean	30.23
Std Dev	3.69

Gaus mean : 30.7454

Gaus Width: 3.9666



B0L000S, U2-ch43, adc1

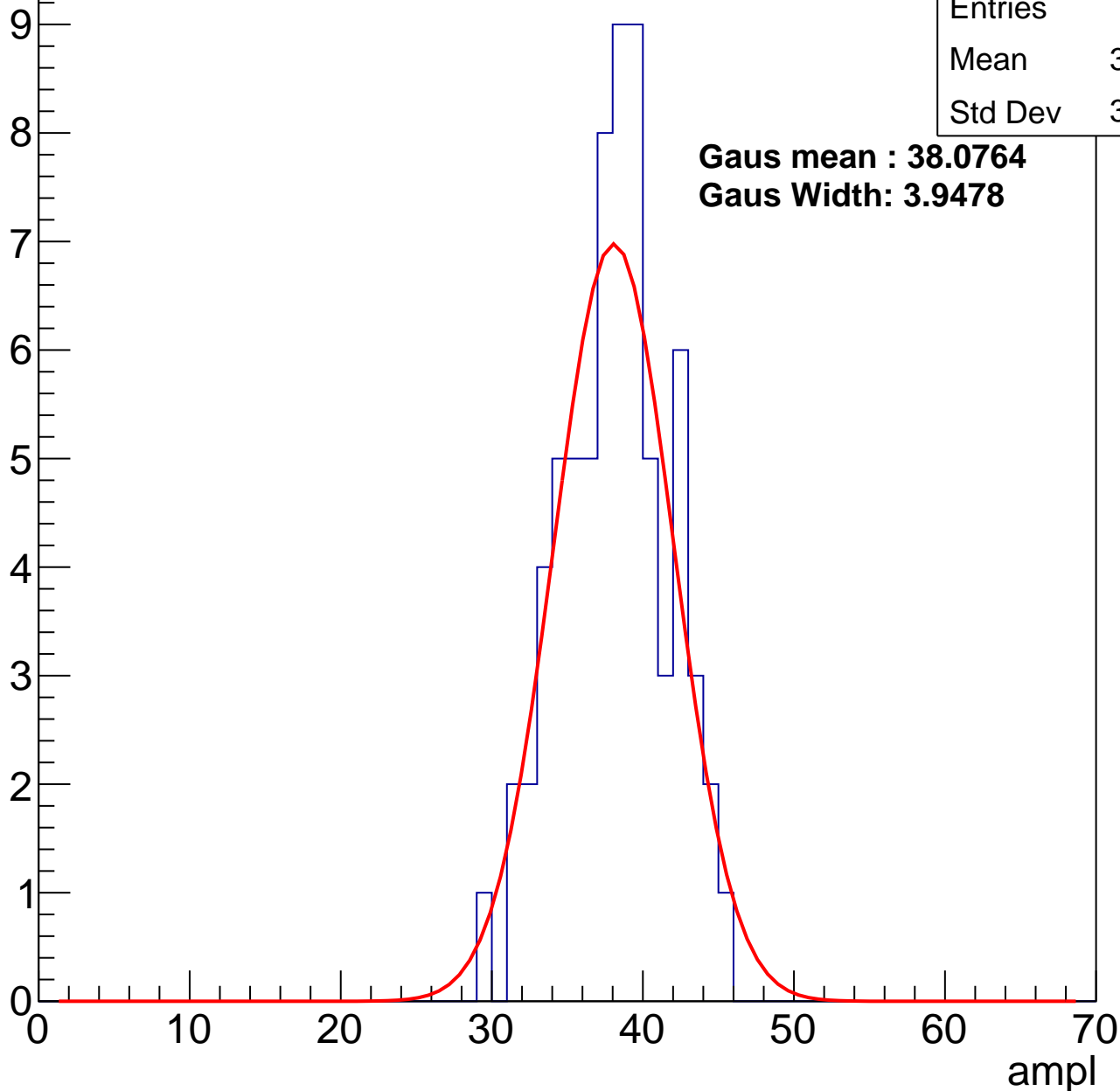
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.69
Std Dev	3.475

Gaus mean : 38.0764

Gaus Width: 3.9478



B0L000S, U2-ch43, adc2

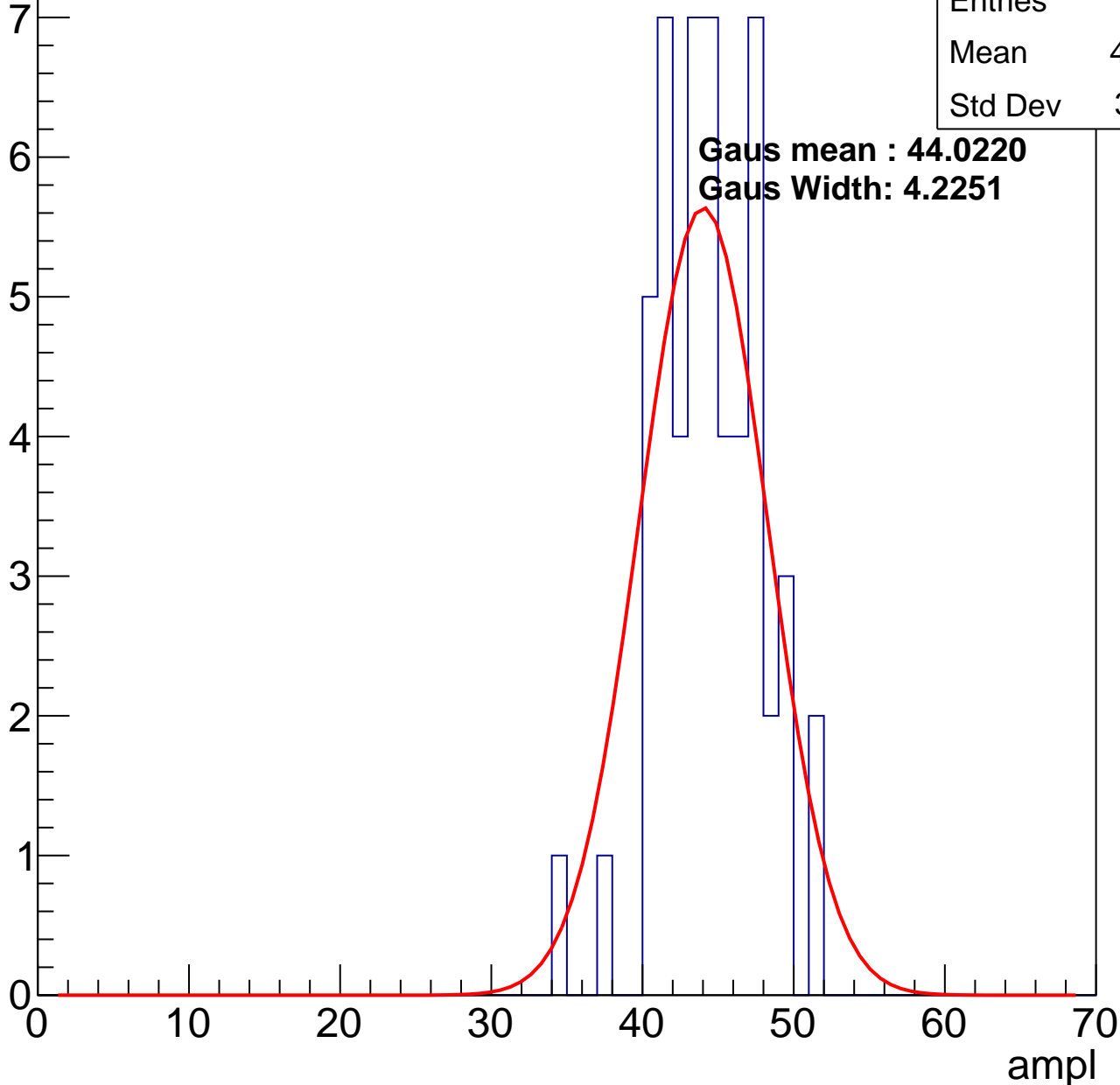
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	43.94
Std Dev	3.341

Gaus mean : 44.0220

Gaus Width: 4.2251

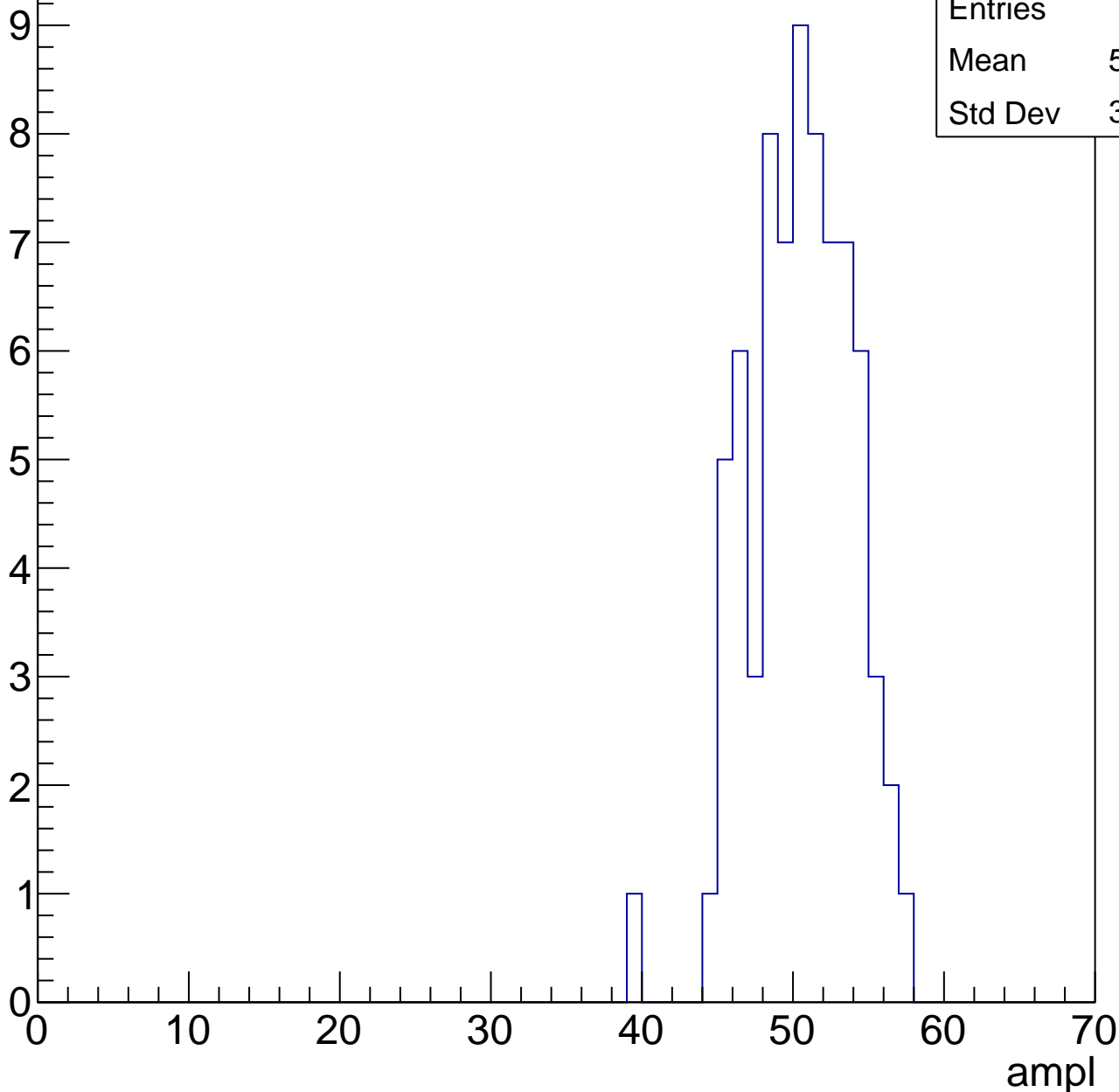


B0L000S, U2-ch43, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

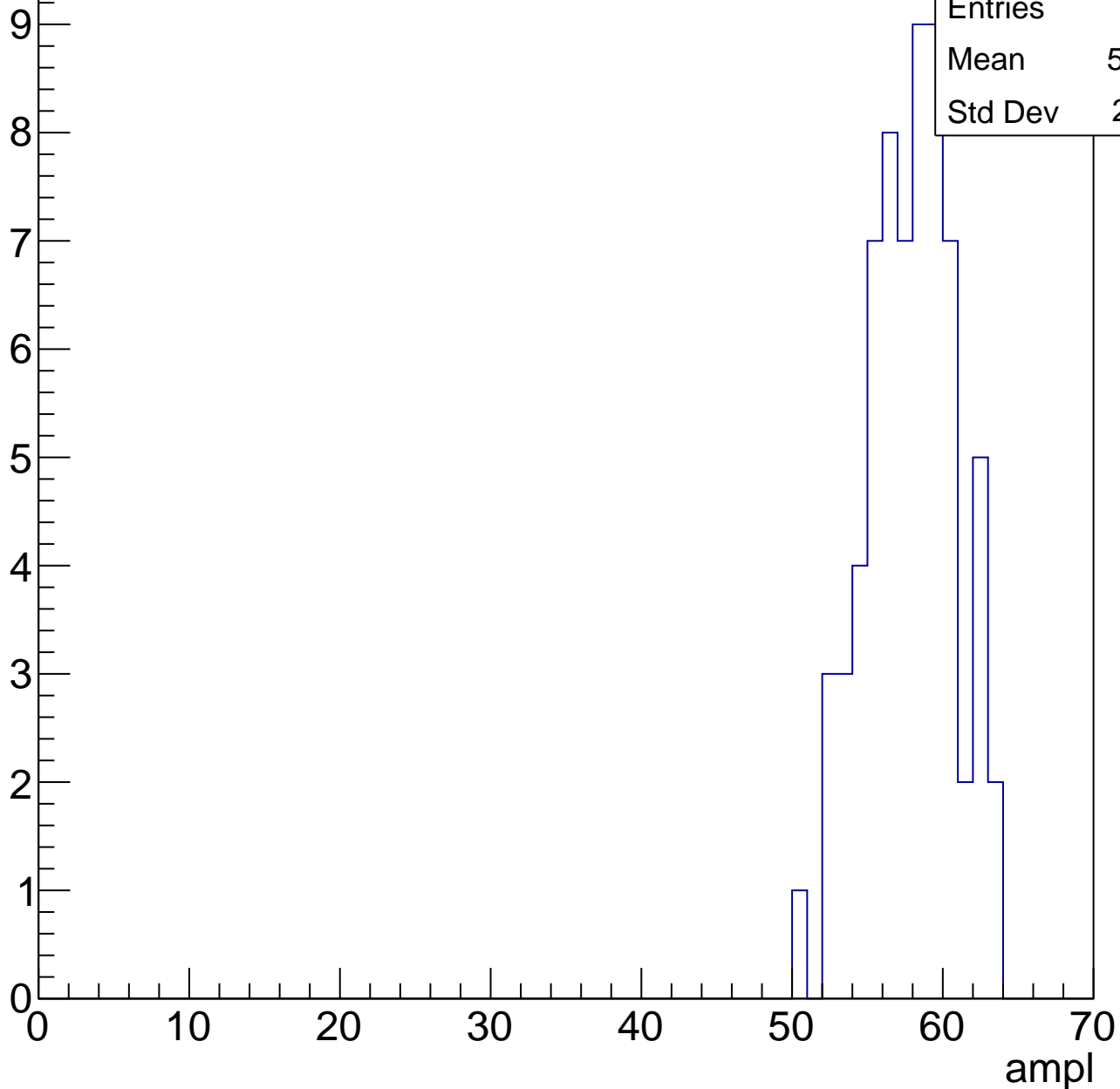
Entries	74
Mean	50.04
Std Dev	3.359



B0L000S, U2-ch43, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch43, adc5

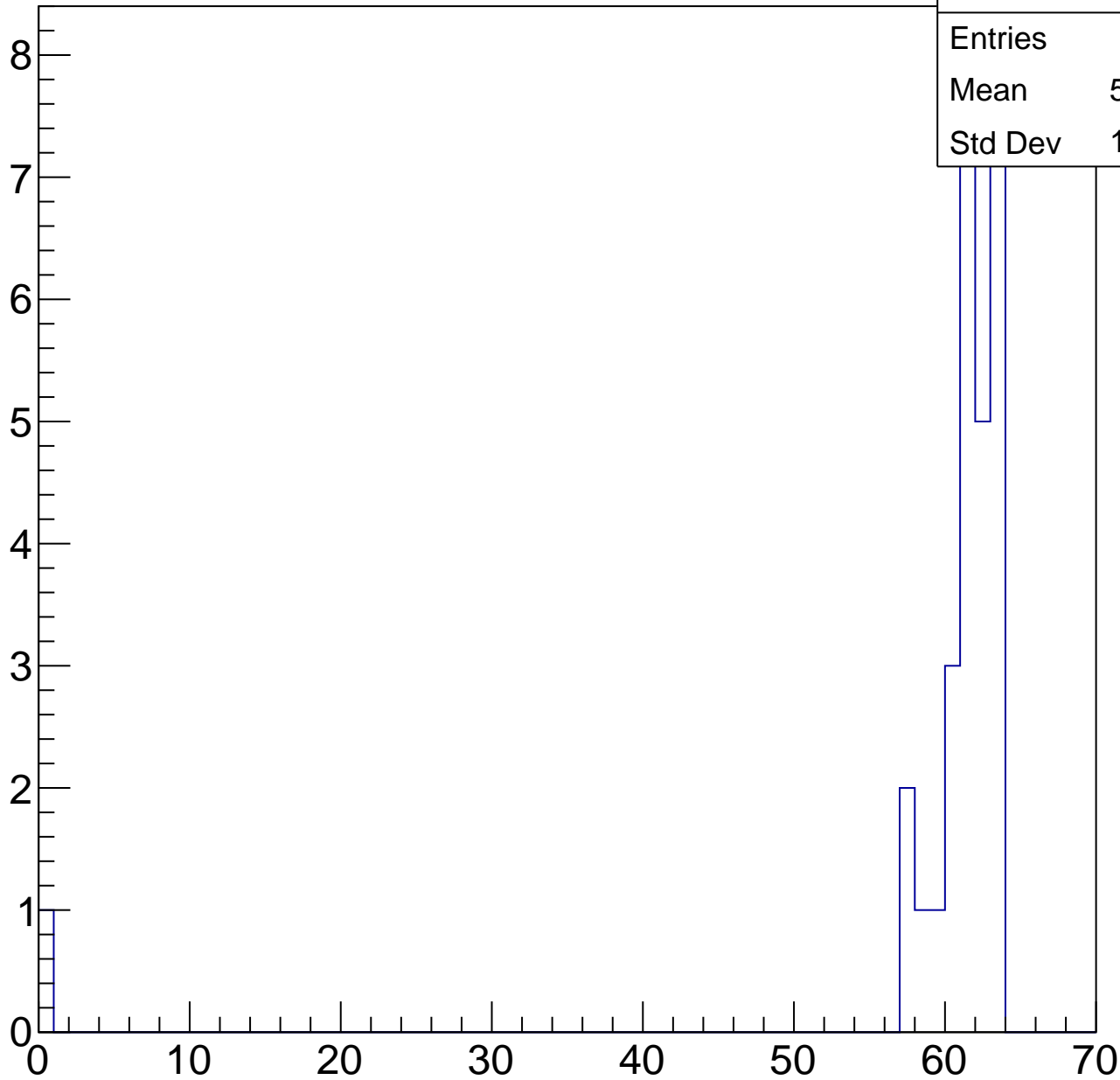
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	29
Mean	59.07
Std Dev	11.29

ampl



B0L000S, U2-ch43, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch43, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch44, adc0

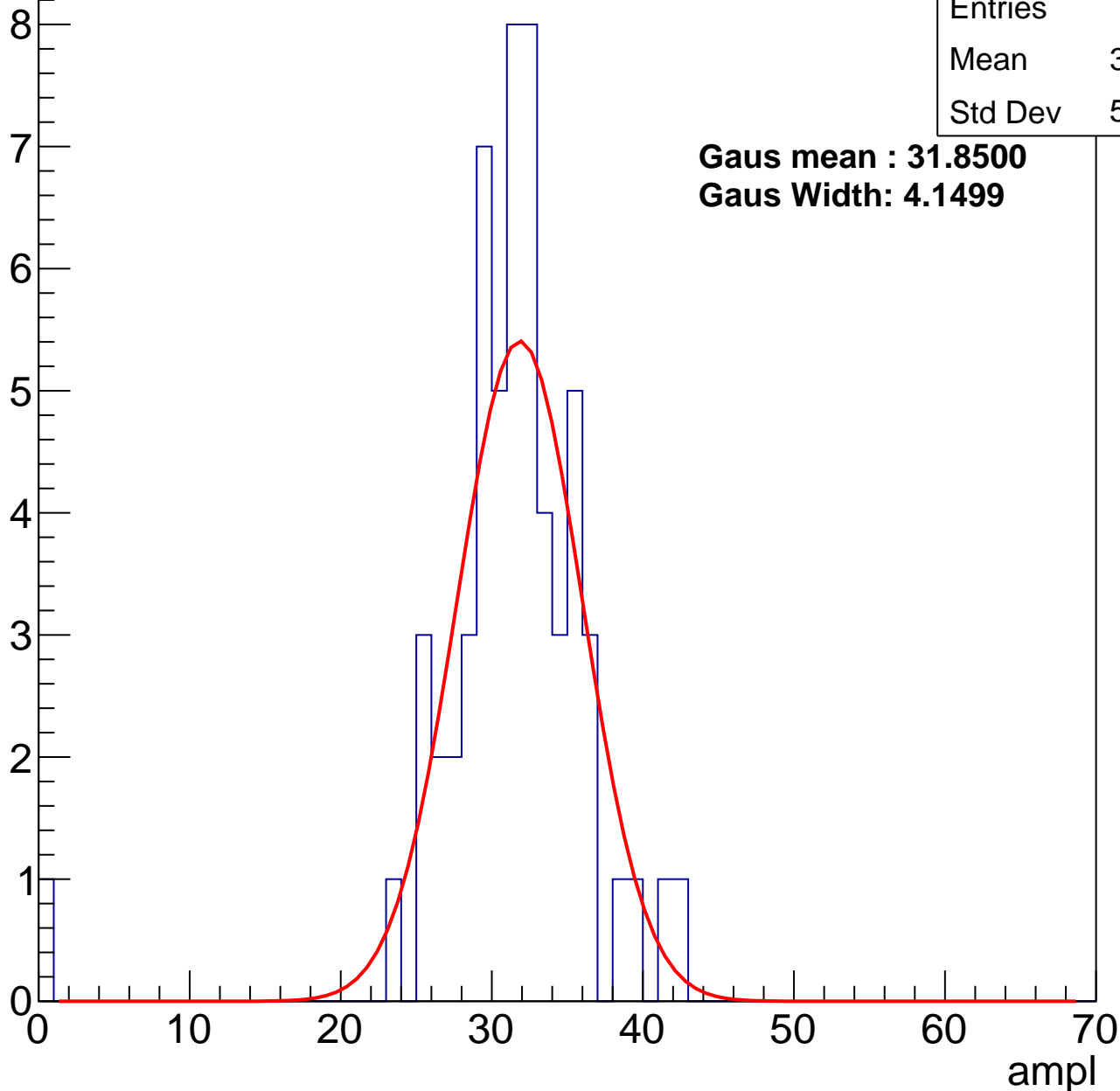
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	30.88
Std Dev	5.545

Gaus mean : 31.8500

Gaus Width: 4.1499



B0L000S, U2-ch44, adc1

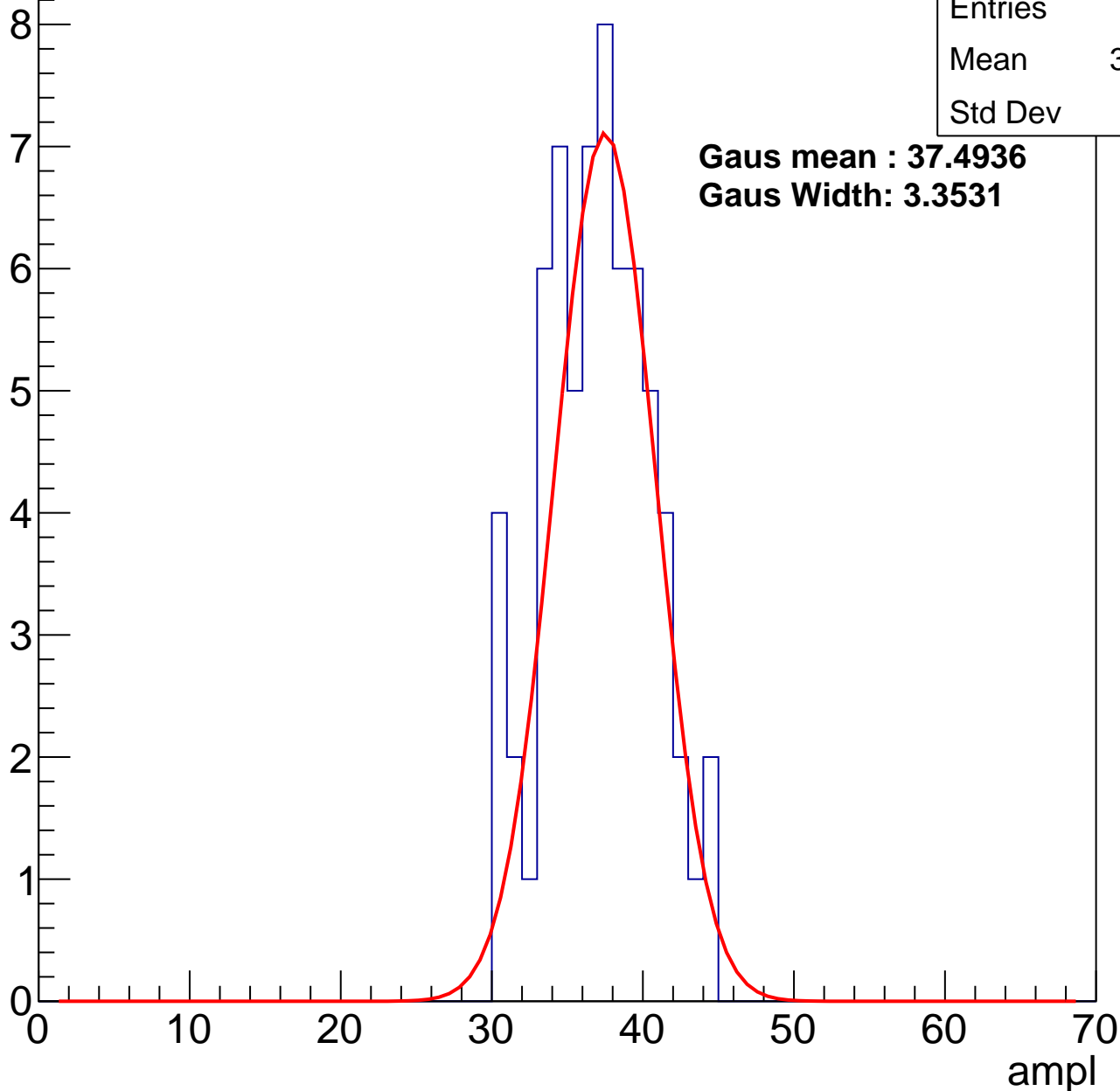
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.58
Std Dev	3.46

Gaus mean : 37.4936

Gaus Width: 3.3531



B0L000S, U2-ch44, adc2

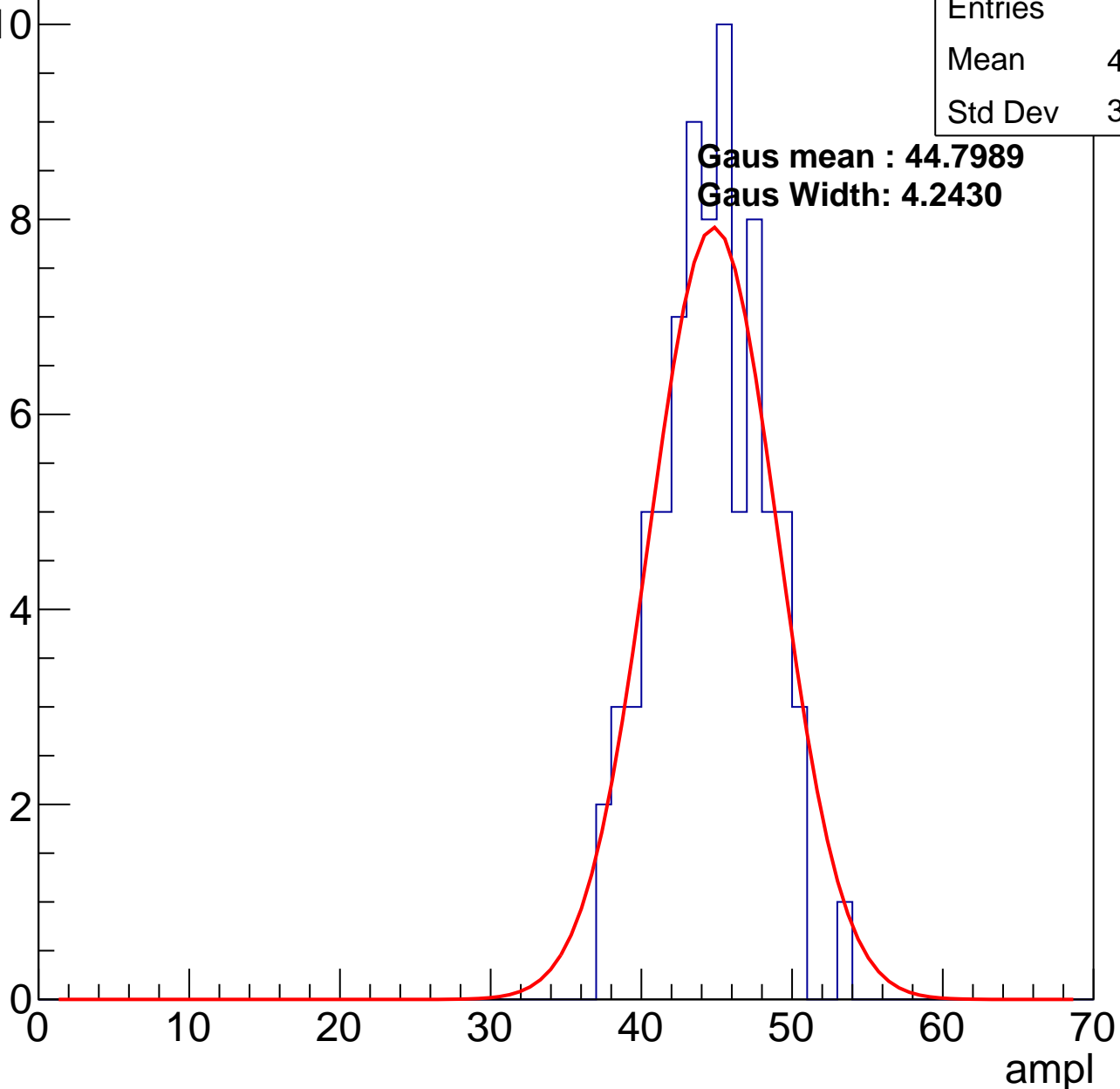
calib_packv5_042523_0143.root, FC#5, port B1

Entries	79
Mean	44.14
Std Dev	3.445

Gaus mean : 44.7989

Gaus Width: 4.2430

Entry

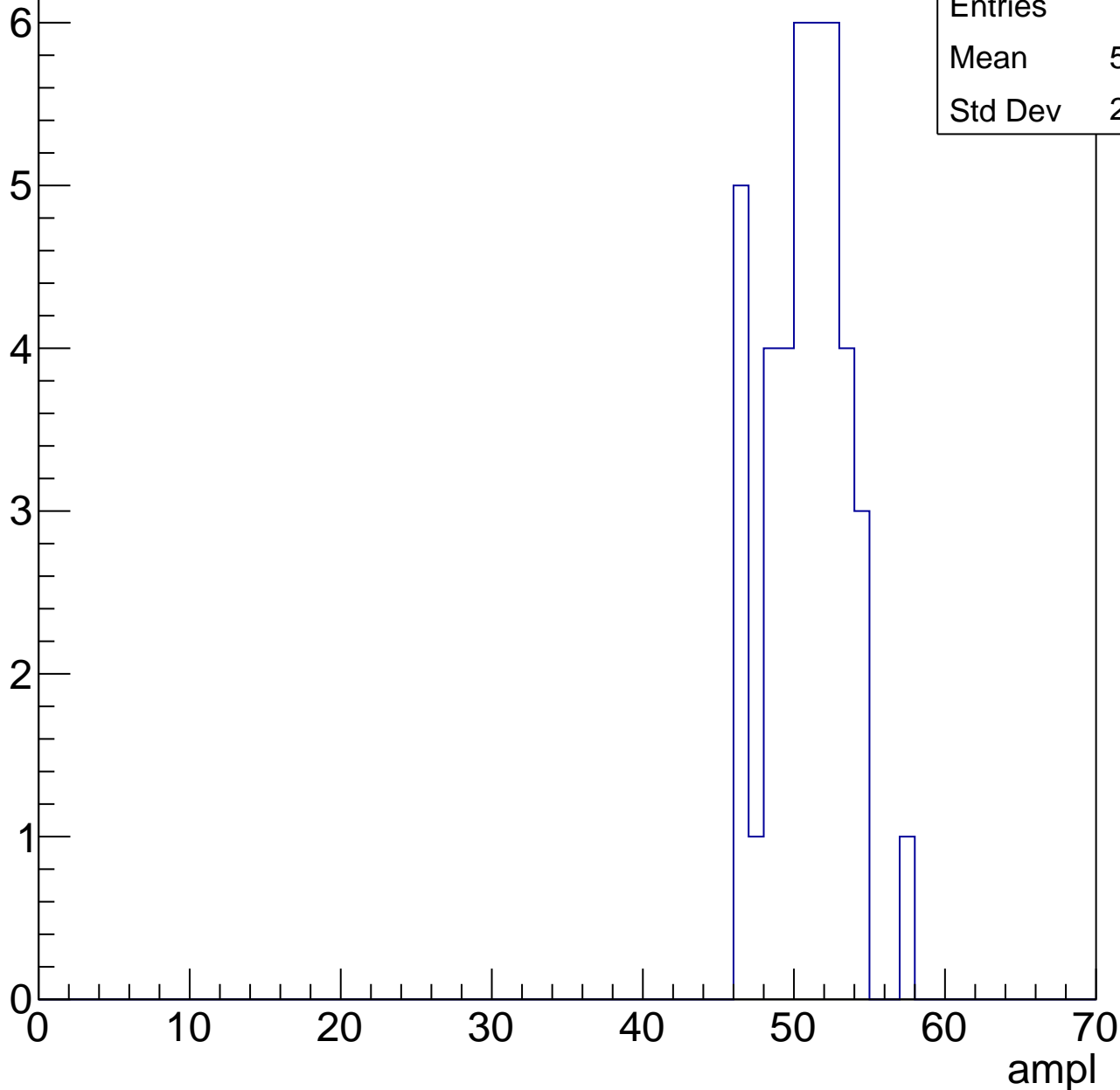


B0L000S, U2-ch44, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	40
Mean	50.35
Std Dev	2.584



B0L000S, U2-ch44, adc4

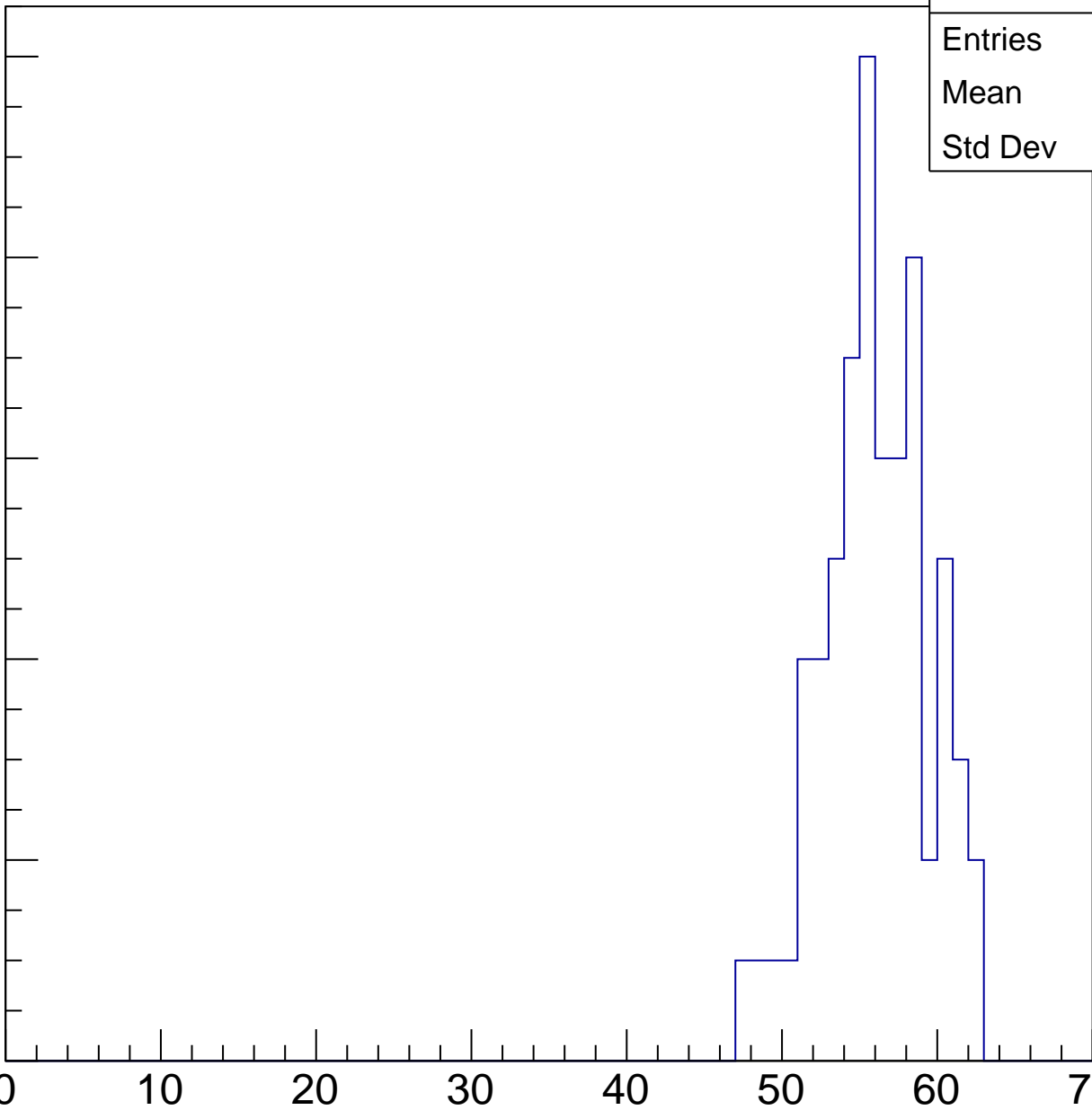
calib_packv5_042523_0143.root, FC#5, port B1

Entry

10
8
6
4
2
0

Entries	66
Mean	55.55
Std Dev	3.363

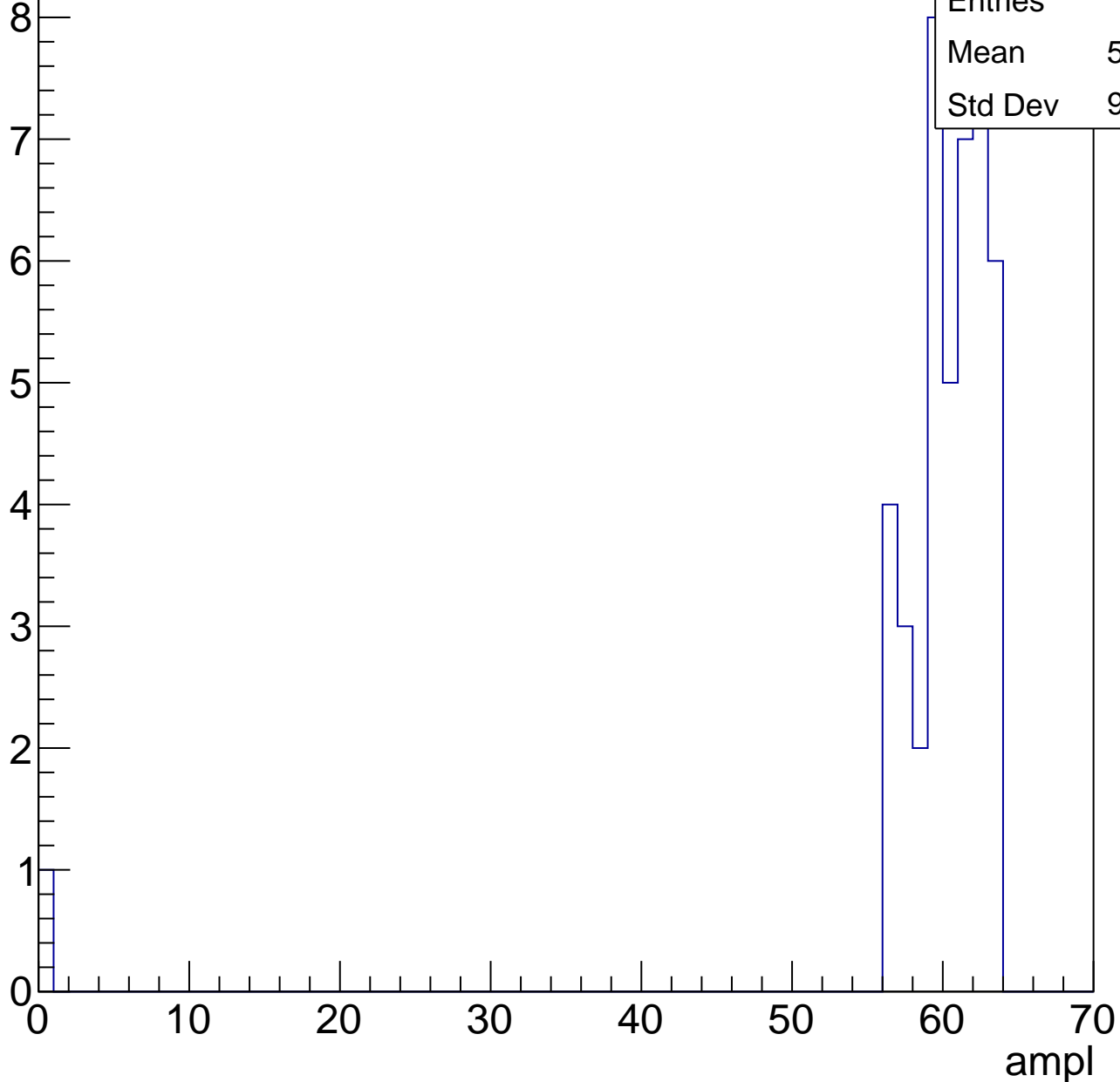
ampl



B0L000S, U2-ch44, adc5

calib_packv5_042523_0143.root, FC#5, port B1

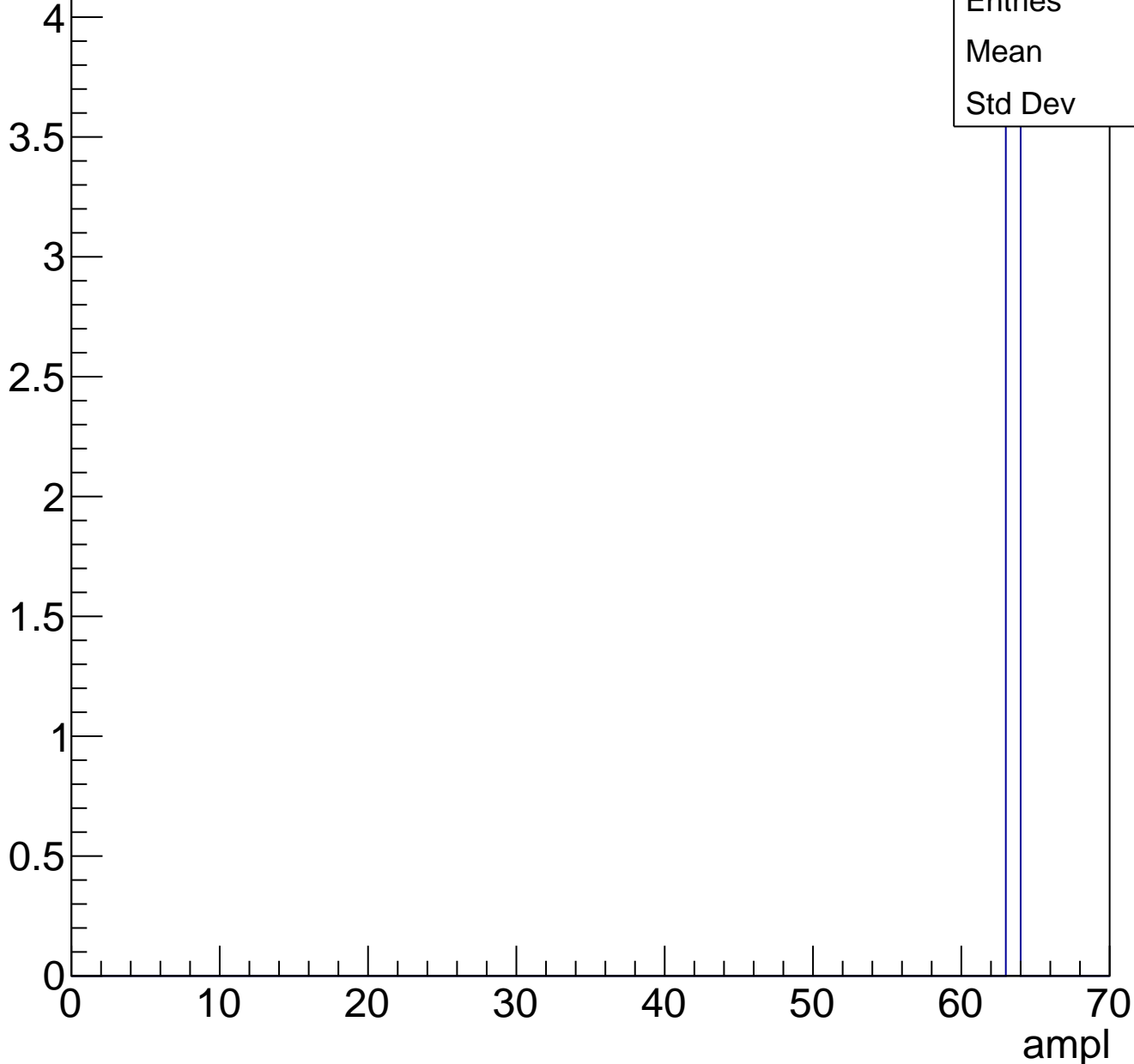
Entry



B0L000S, U2-ch44, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch44, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch45, adc0

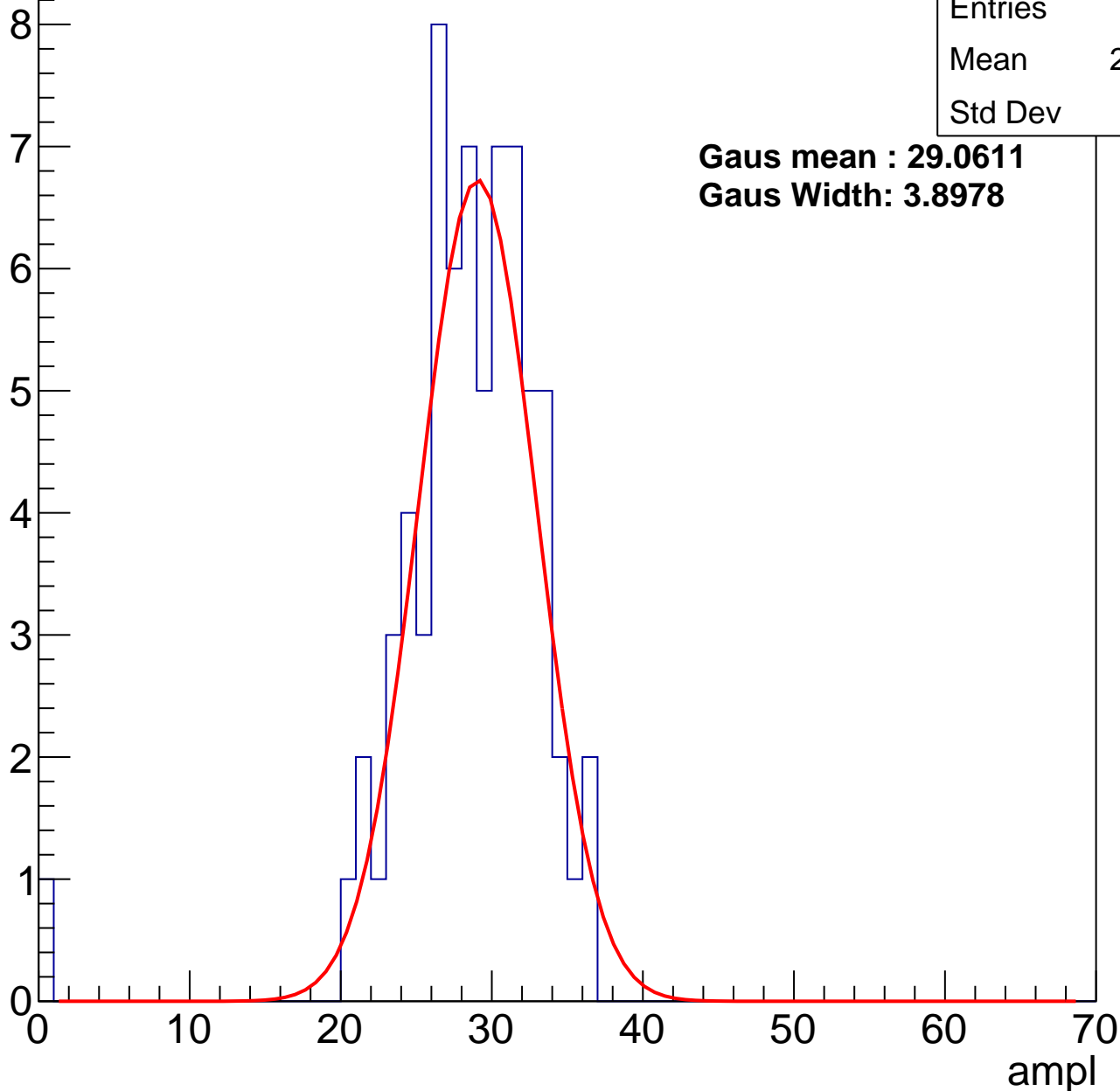
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	28.03
Std Dev	4.98

Gaus mean : 29.0611

Gaus Width: 3.8978



B0L000S, U2-ch45, adc1

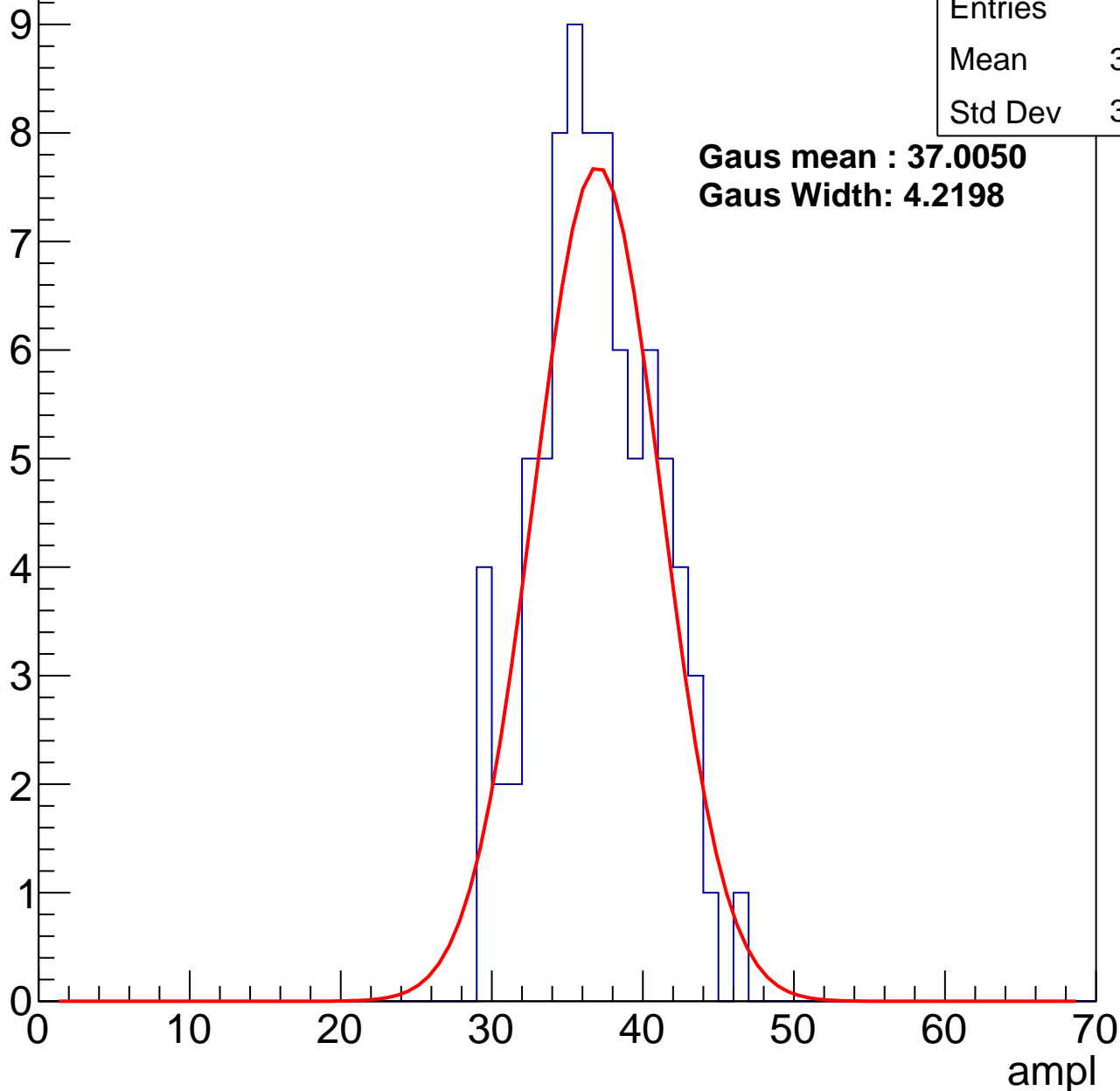
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	36.45
Std Dev	3.858

Gaus mean : 37.0050

Gaus Width: 4.2198



B0L000S, U2-ch45, adc2

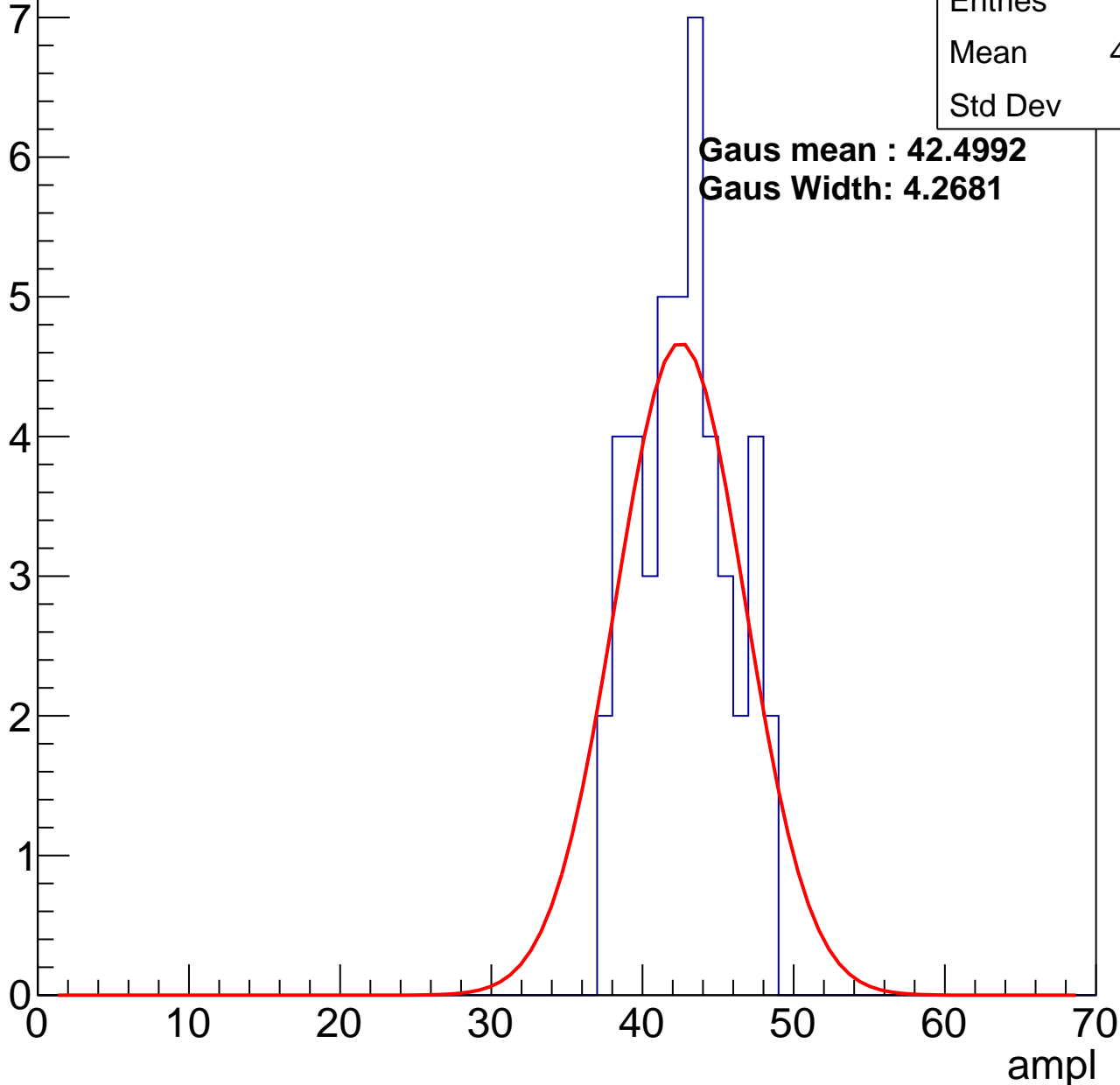
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	45
Mean	42.33
Std Dev	3.04

Gaus mean : 42.4992

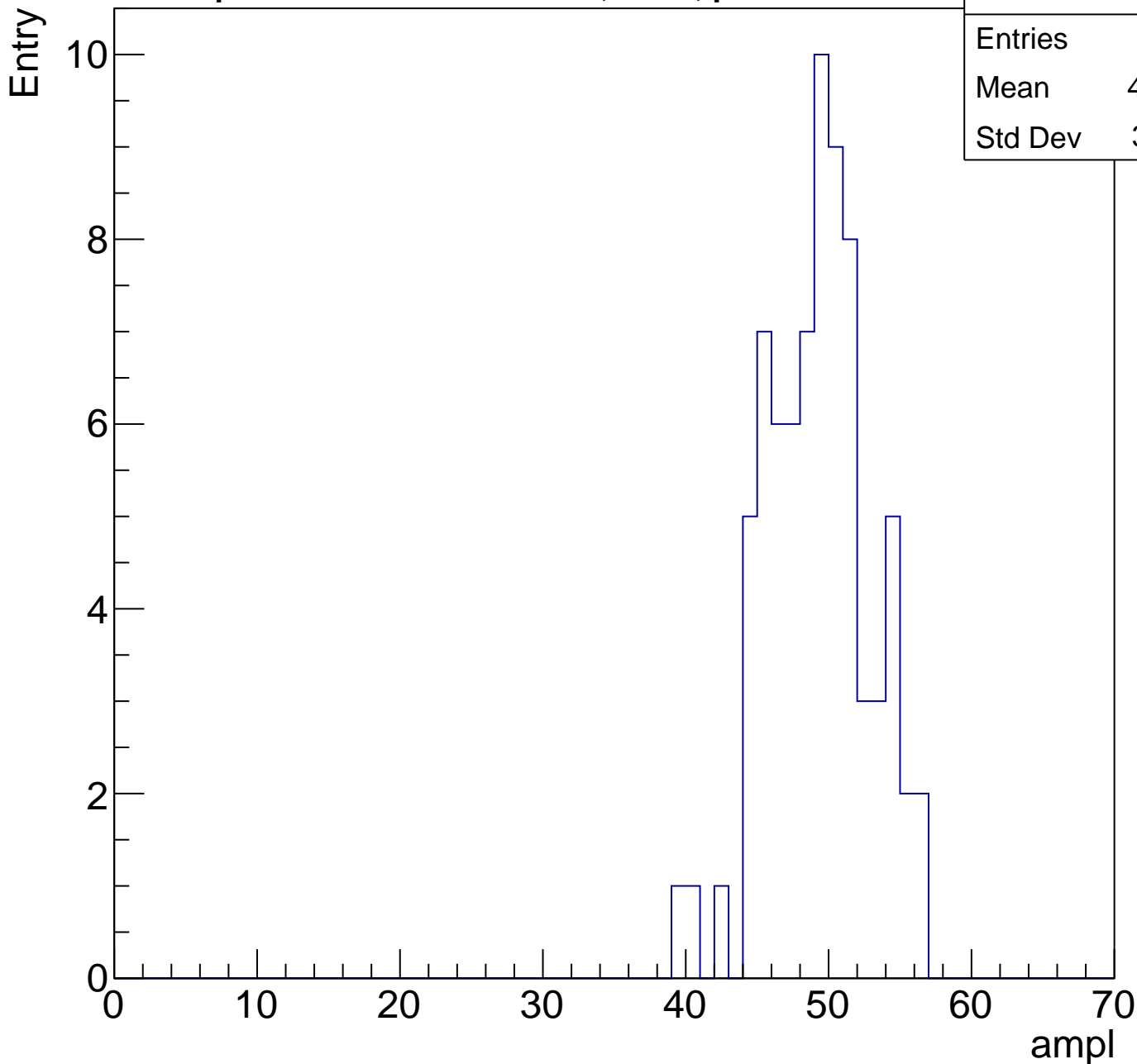
Gaus Width: 4.2681



B0L000S, U2-ch45, adc3

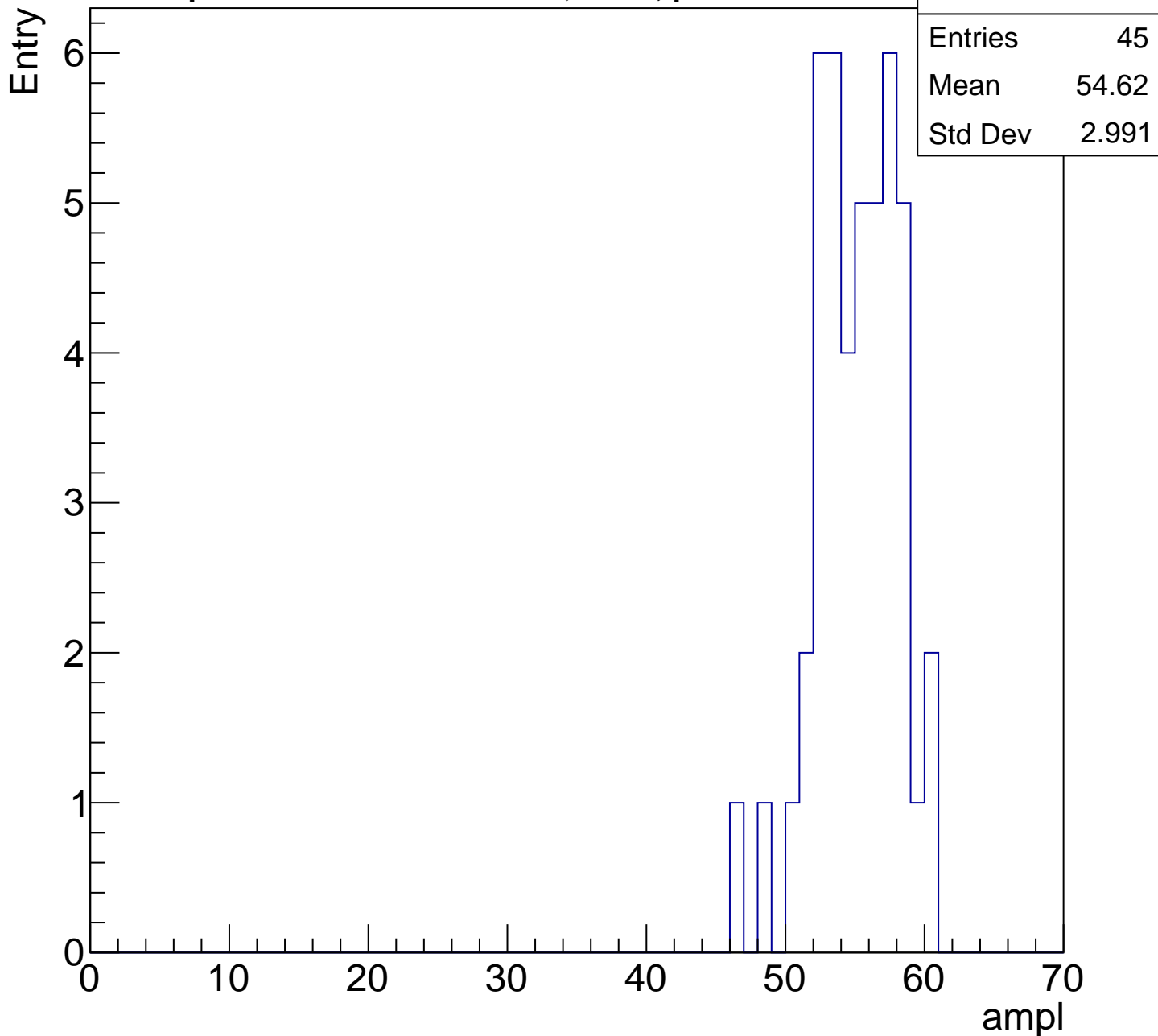
calib_packv5_042523_0143.root, FC#5, port B1

Entries	76
Mean	48.75
Std Dev	3.551



B0L000S, U2-ch45, adc4

calib_packv5_042523_0143.root, FC#5, port B1

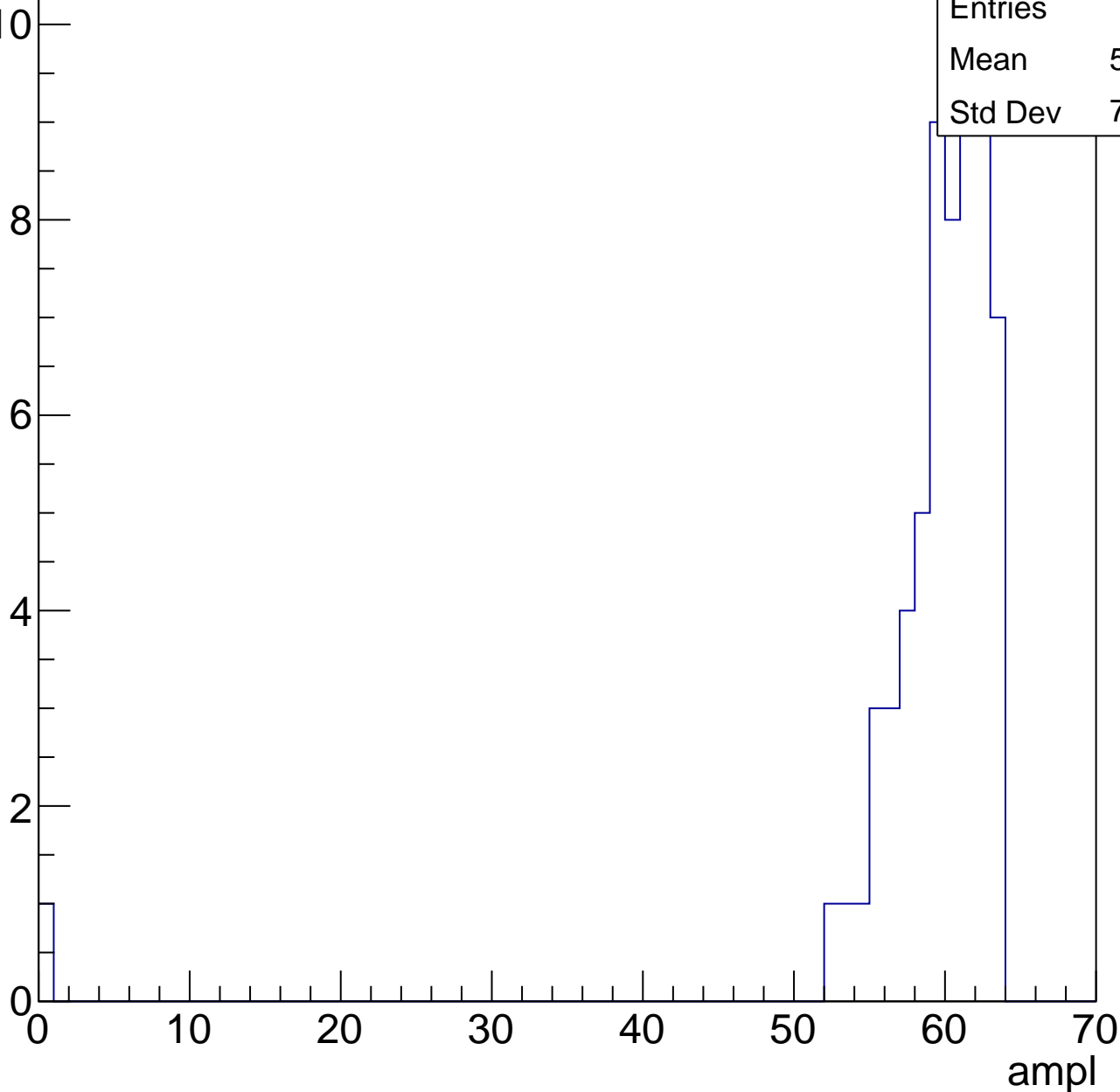


B0L000S, U2-ch45, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	58.55
Std Dev	7.947



B0L000S, U2-ch45, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch45, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L000S, U2-ch46, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	83
Mean	28.81
Std Dev	5.936

Gaus mean : 29.7552

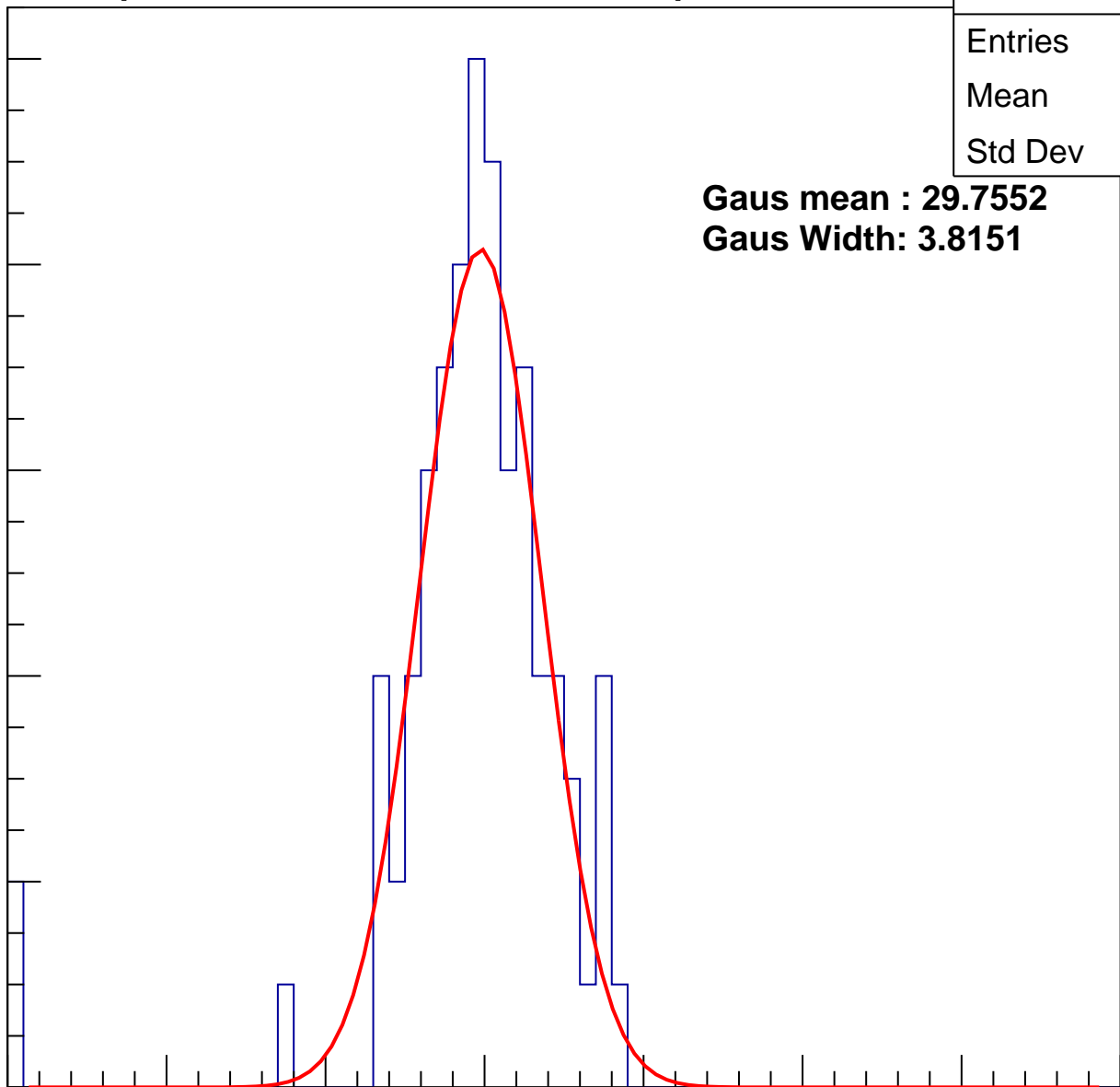
Gaus Width: 3.8151

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch46, adc1

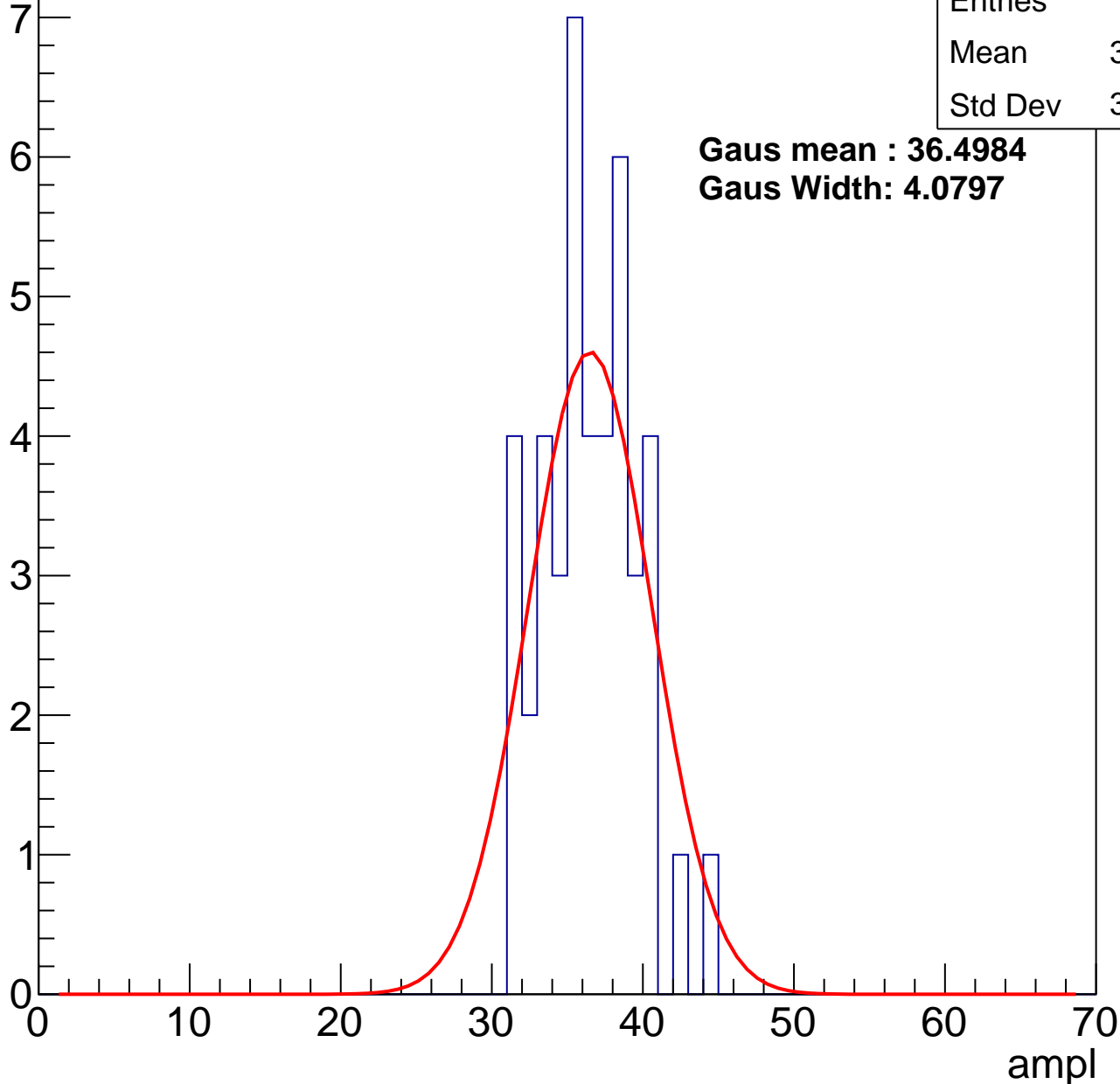
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	43
Mean	36.05
Std Dev	3.072

Gaus mean : 36.4984

Gaus Width: 4.0797



B0L000S, U2-ch46, adc2

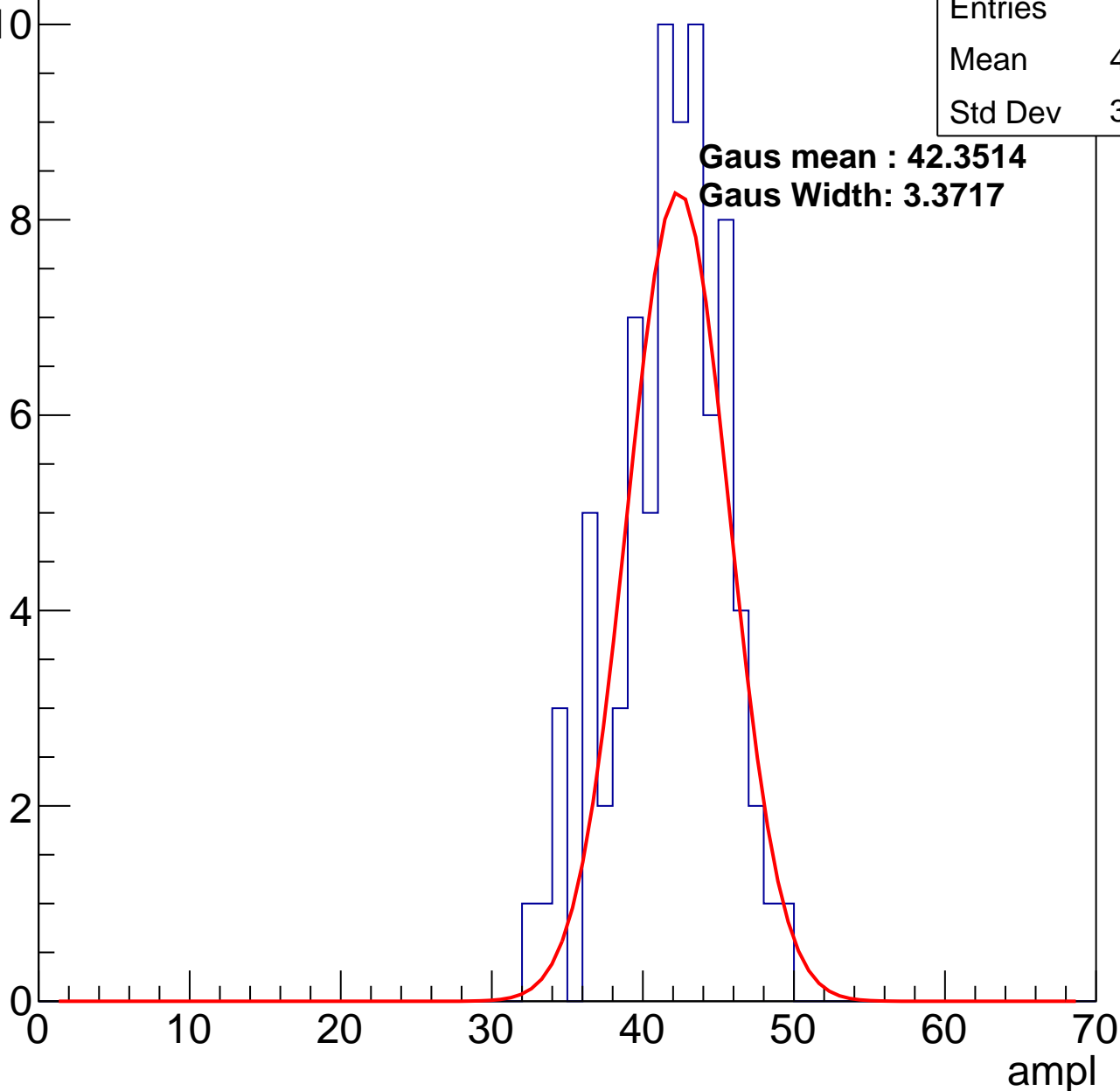
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	41.35
Std Dev	3.608

Gaus mean : 42.3514

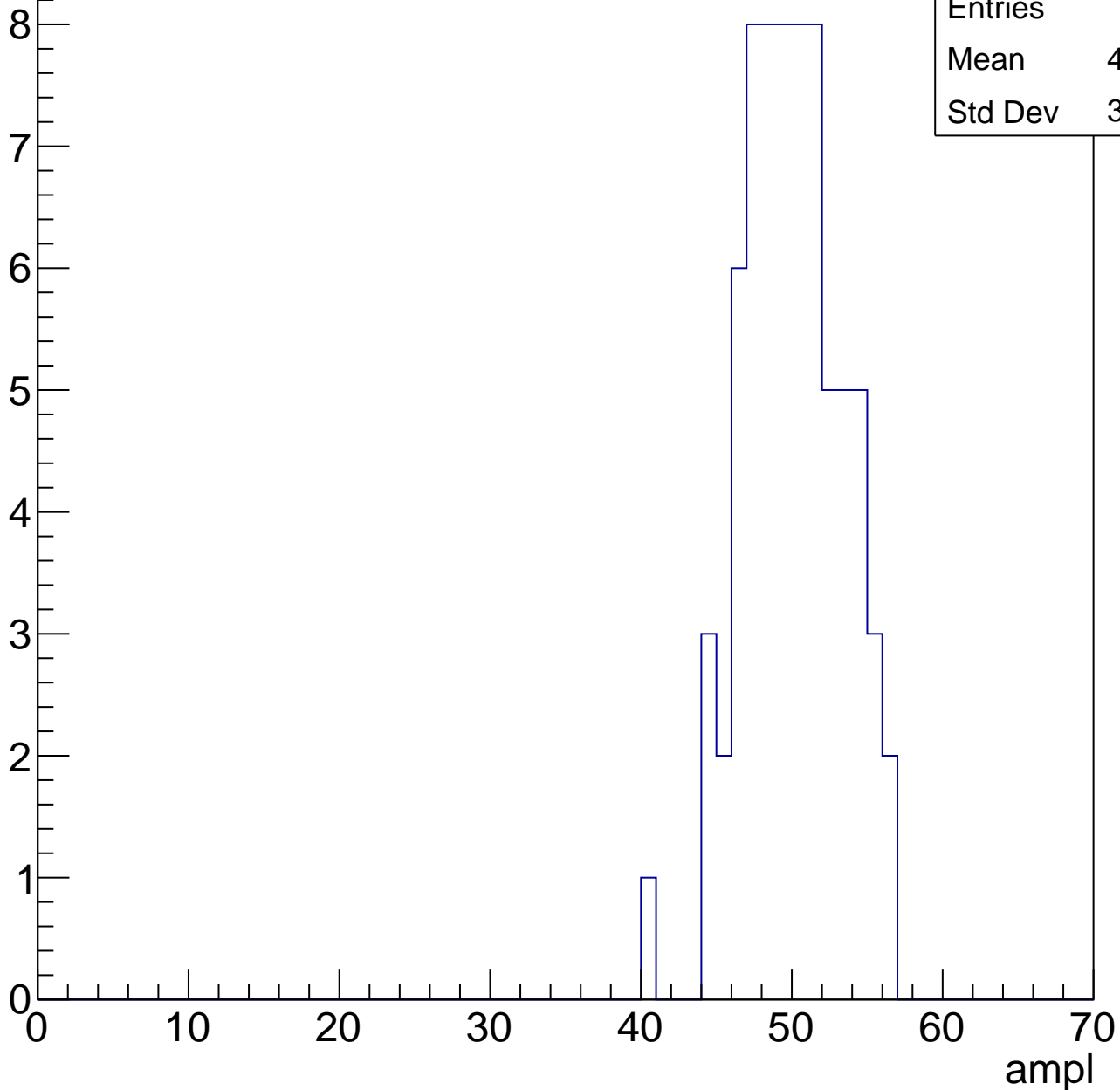
Gaus Width: 3.3717



B0L000S, U2-ch46, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



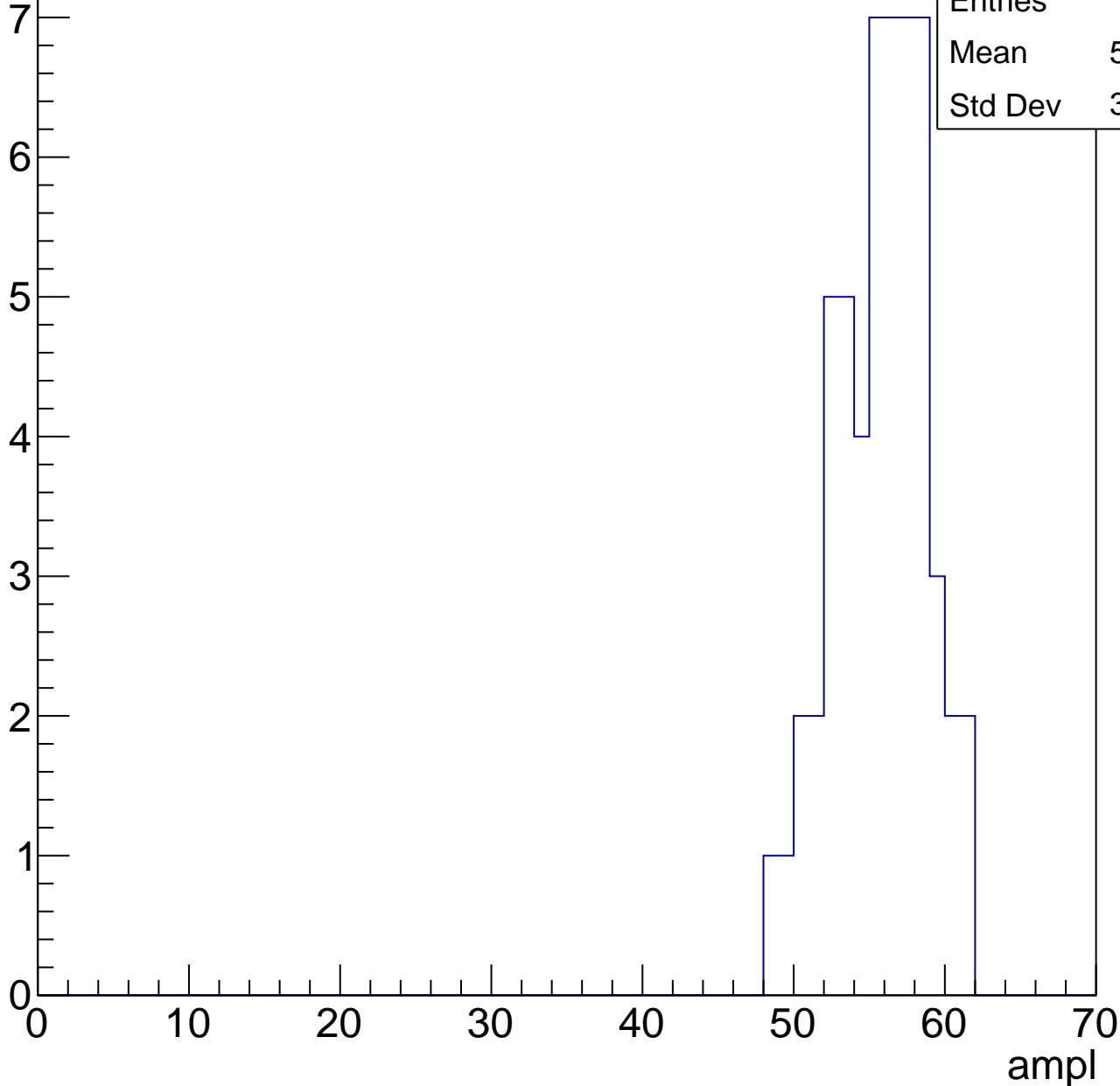
Entries	72
Mean	49.58
Std Dev	3.235

B0L000S, U2-ch46, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.29
Std Dev	3.013



B0L000S, U2-ch46, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.51
Std Dev	9.467

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

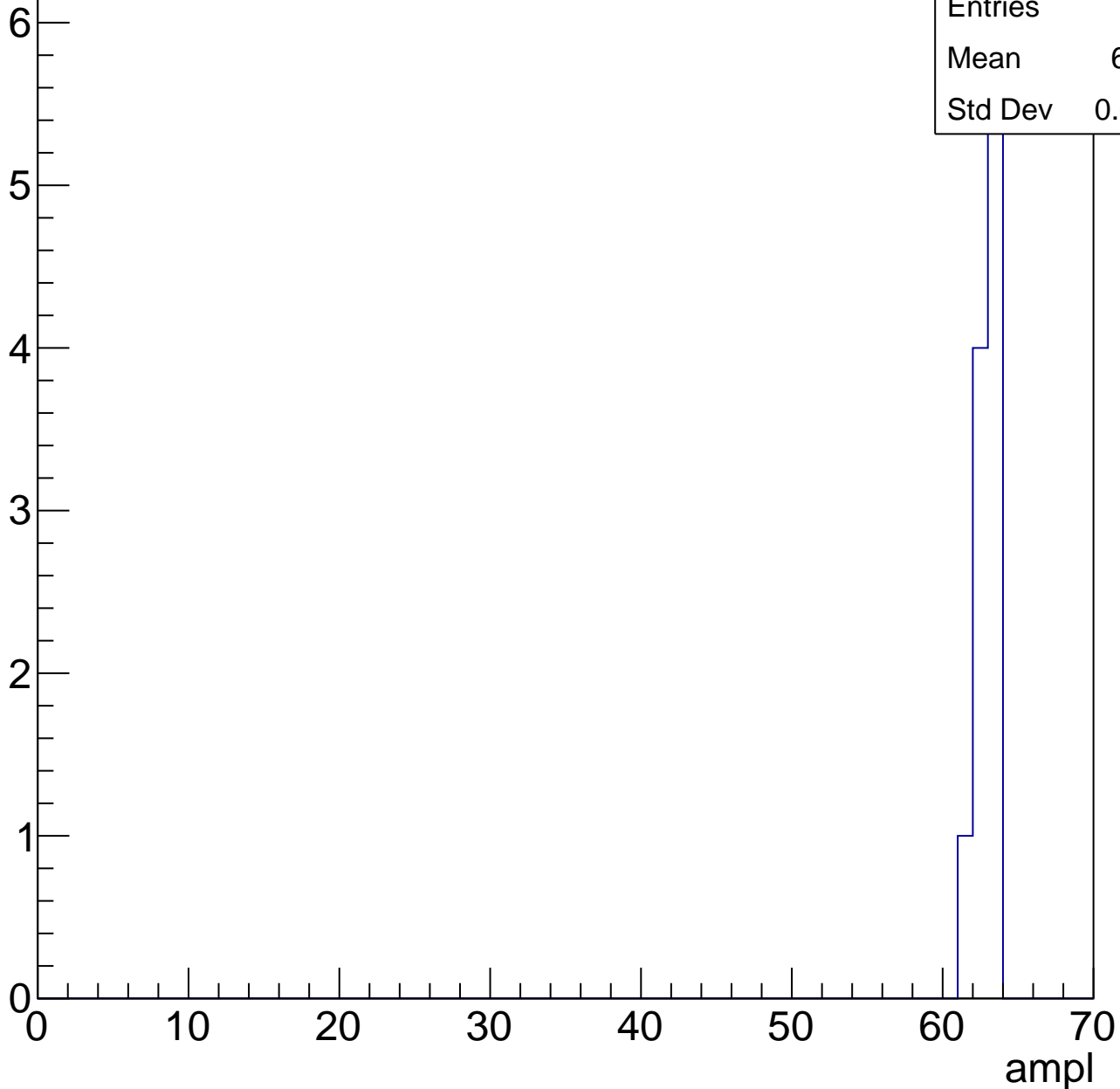
8

B0L000S, U2-ch46, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	11
Mean	62.45
Std Dev	0.6556



B0L000S, U2-ch46, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch47, adc0

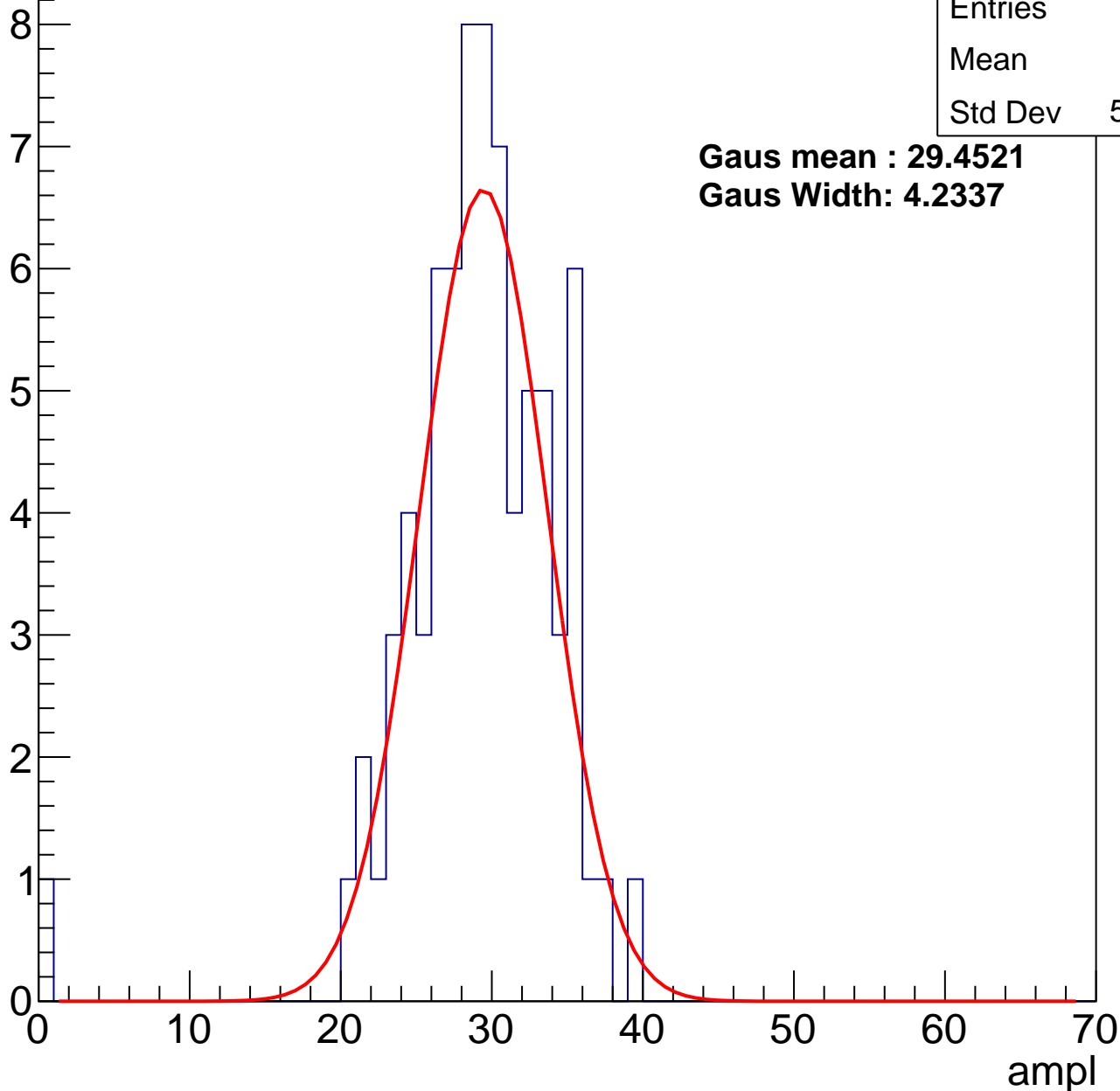
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	28.7
Std Dev	5.239

Gaus mean : 29.4521

Gaus Width: 4.2337



B0L000S, U2-ch47, adc1

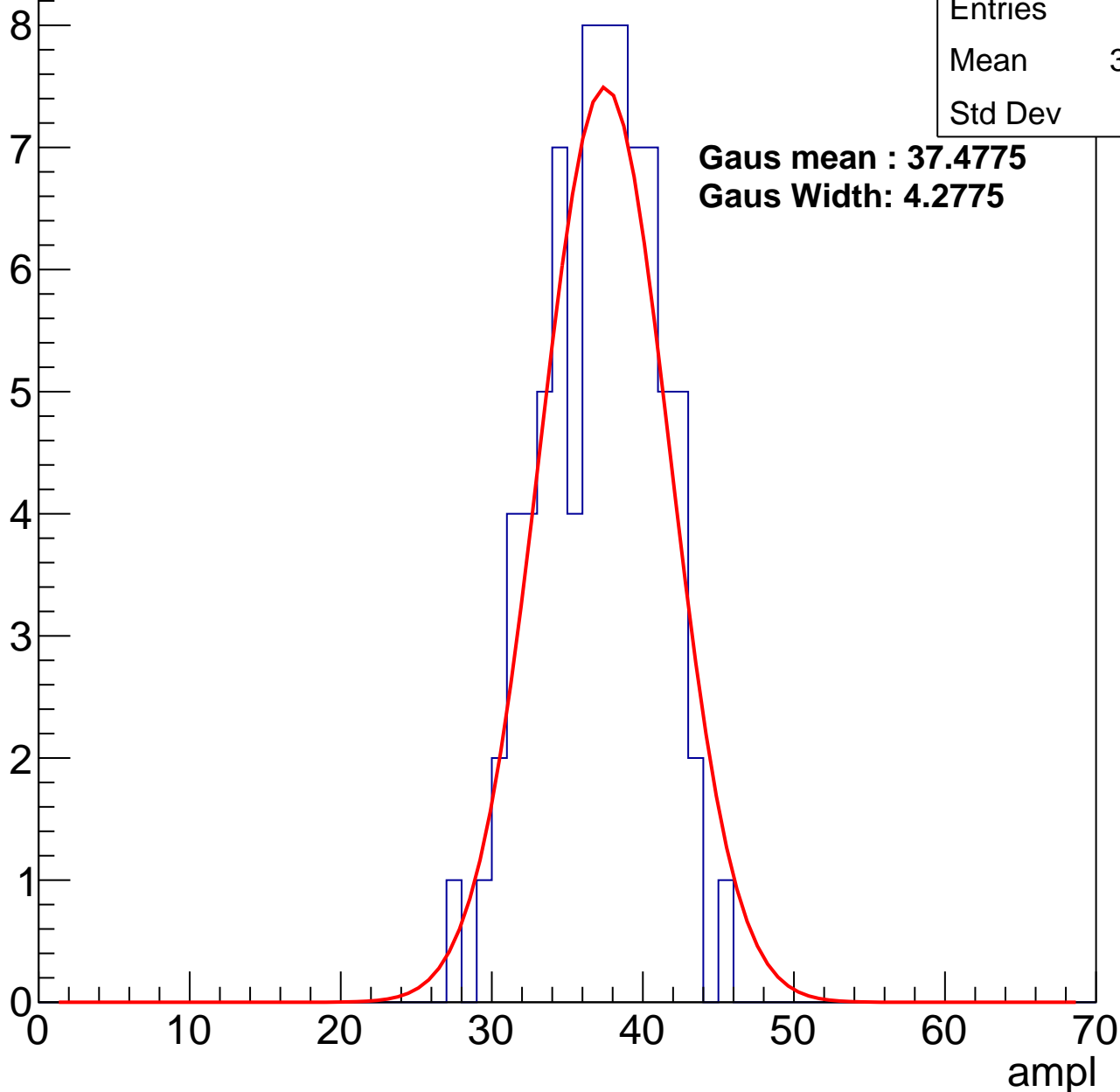
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	79
Mean	36.68
Std Dev	3.74

Gaus mean : 37.4775

Gaus Width: 4.2775



B0L000S, U2-ch47, adc2

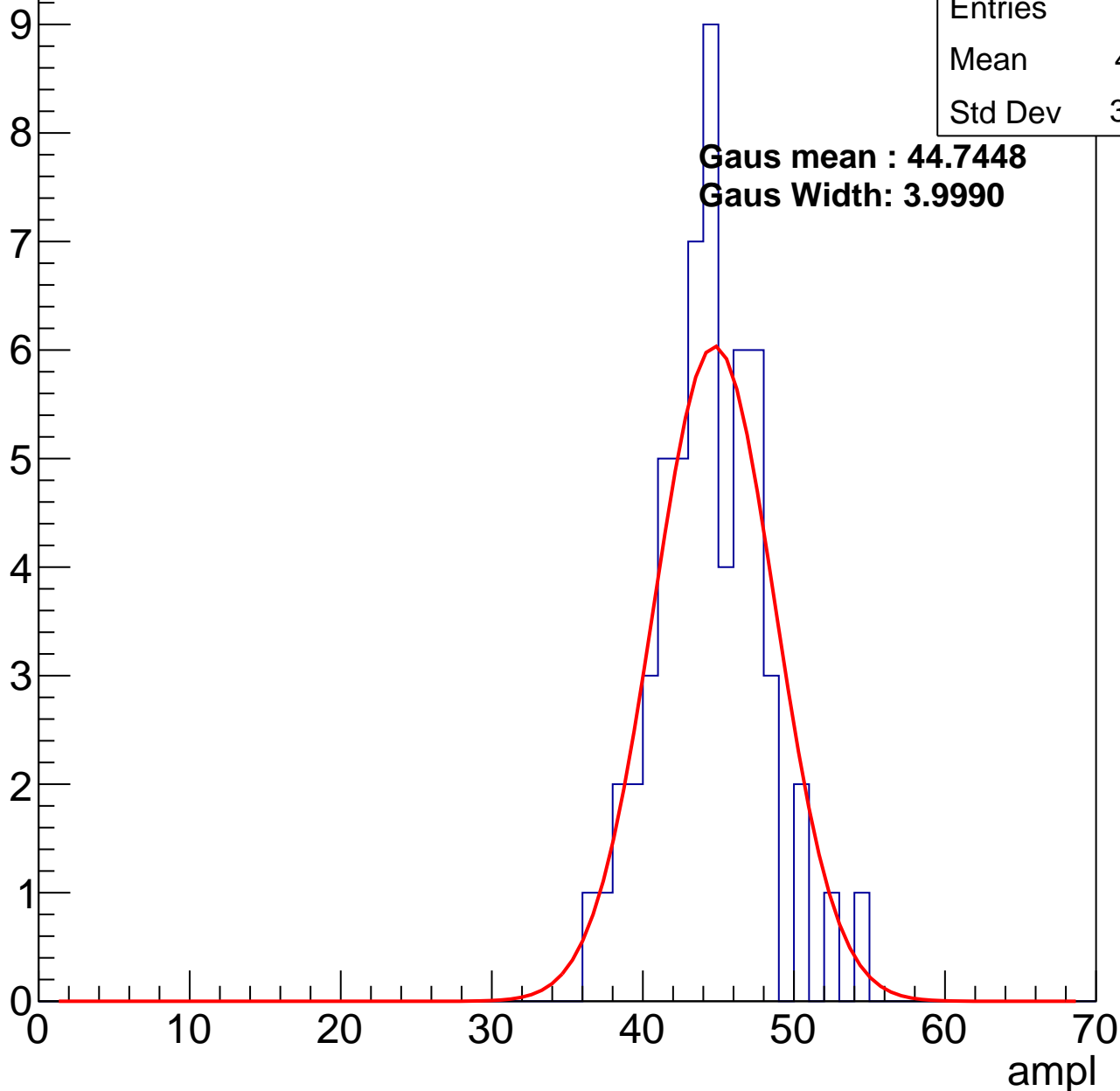
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.91
Std Dev	3.525

Gaus mean : 44.7448

Gaus Width: 3.9990



B0L000S, U2-ch47, adc3

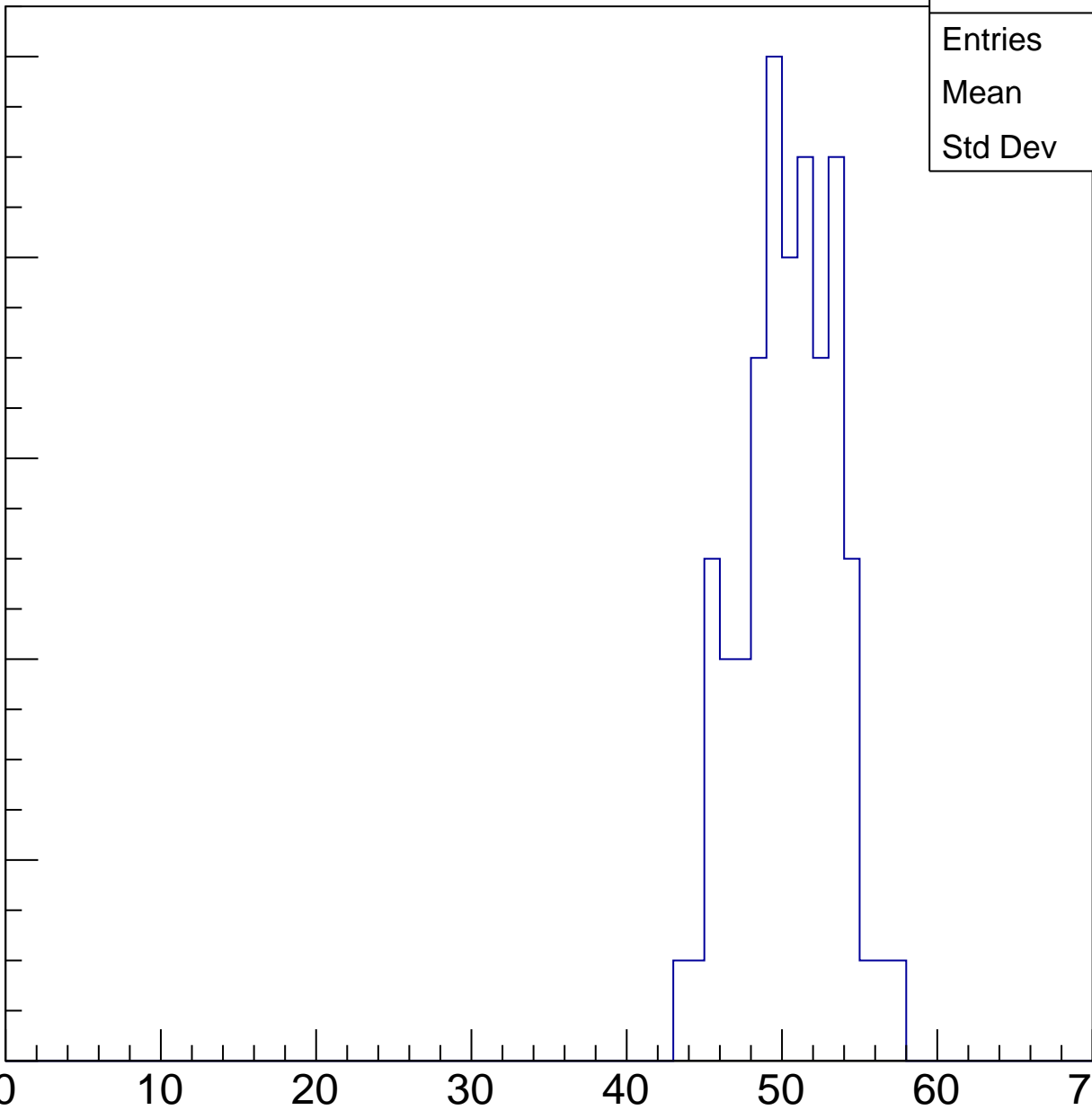
calib_packv5_042523_0143.root, FC#5, port B1

Entry

10
8
6
4
2
0

Entries	73
Mean	49.97
Std Dev	2.998

ampl



B0L000S, U2-ch47, adc4

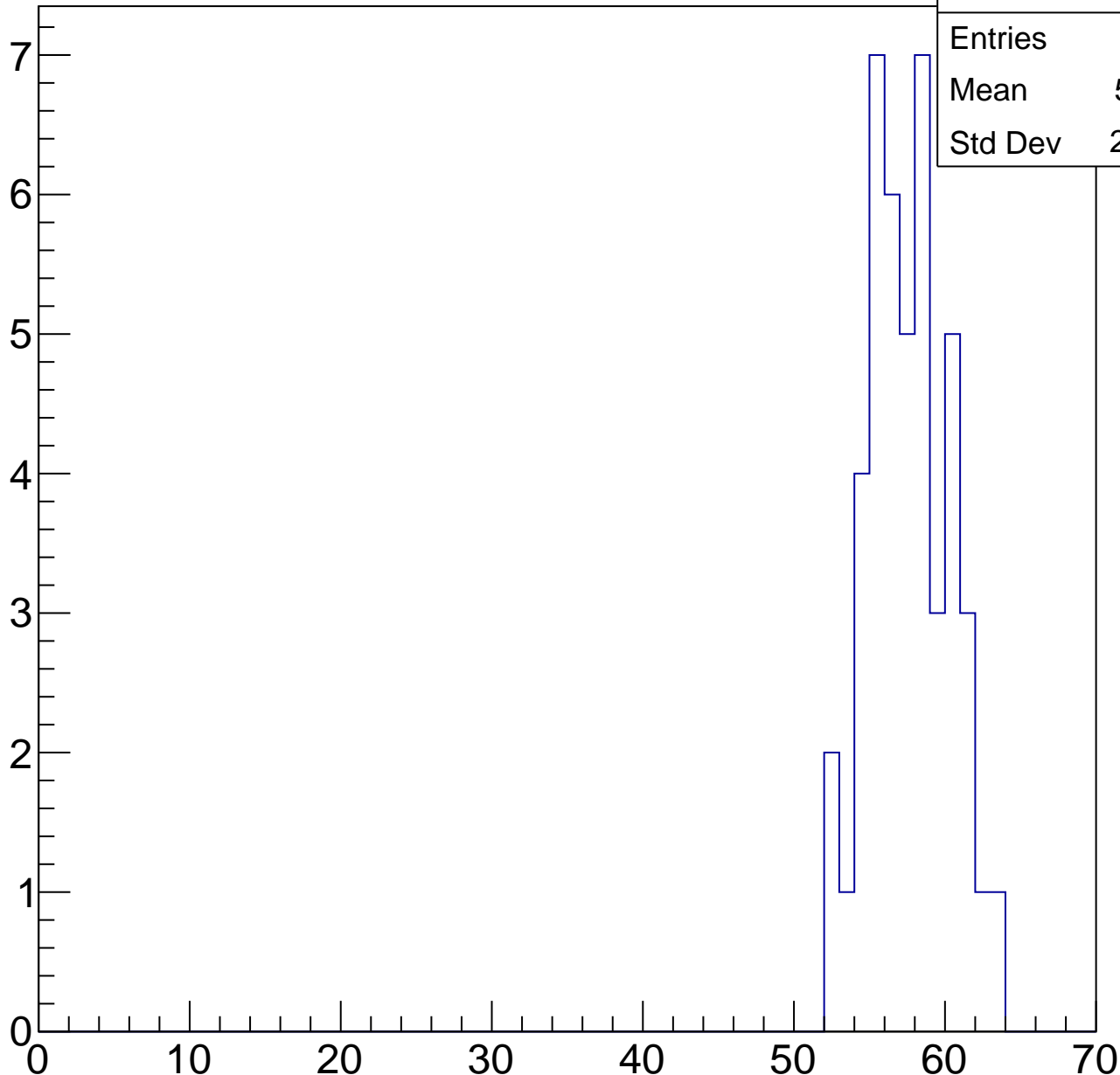
calib_packv5_042523_0143.root, FC#5, port B1

Entry

7
6
5
4
3
2
1
0

Entries	45
Mean	57.11
Std Dev	2.618

ampl

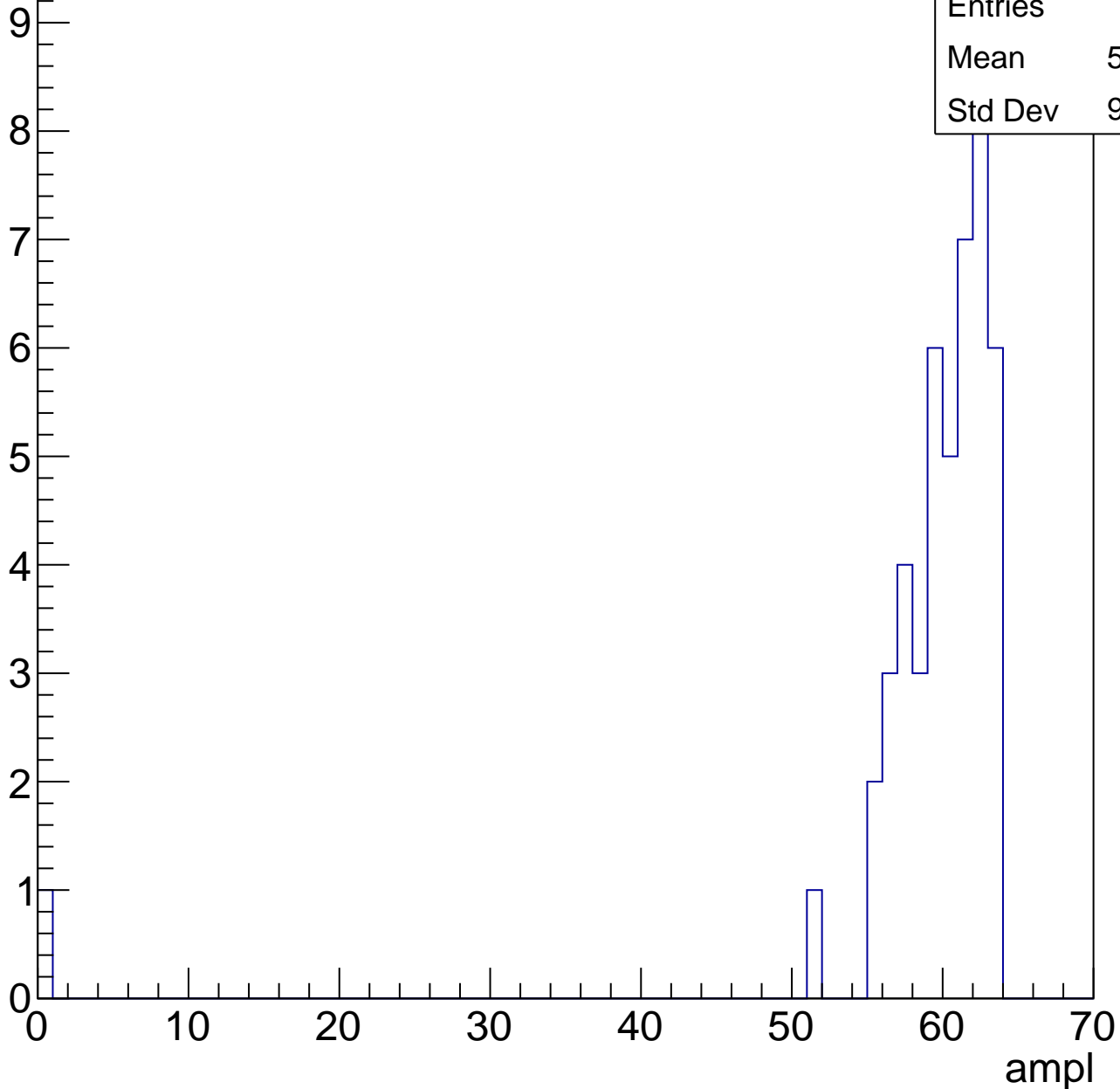


B0L000S, U2-ch47, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

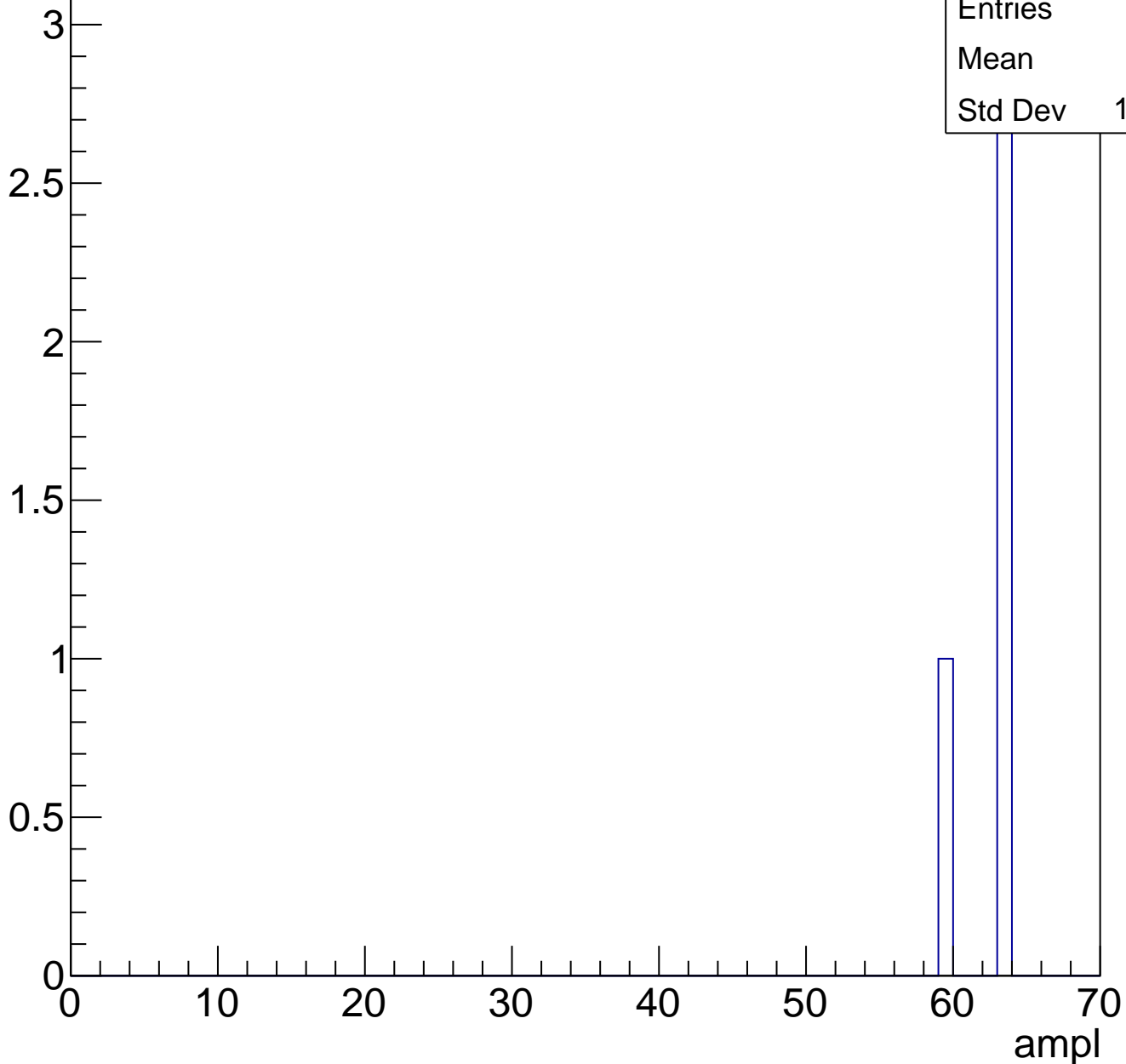
Entries	47
Mean	58.47
Std Dev	9.015



B0L000S, U2-ch47, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch47, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch48, adc0

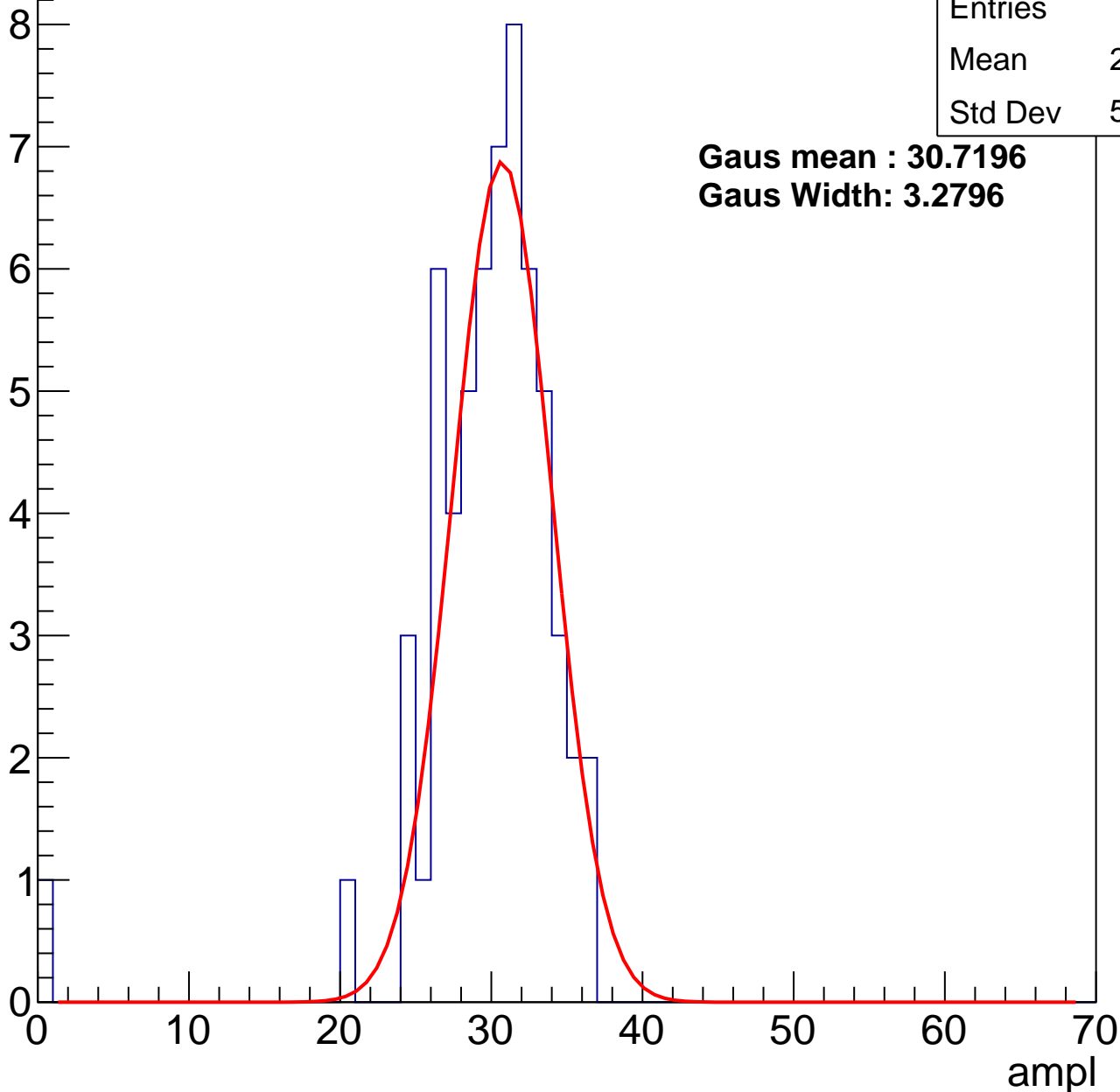
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.23
Std Dev	5.005

Gaus mean : 30.7196

Gaus Width: 3.2796



B0L000S, U2-ch48, adc1

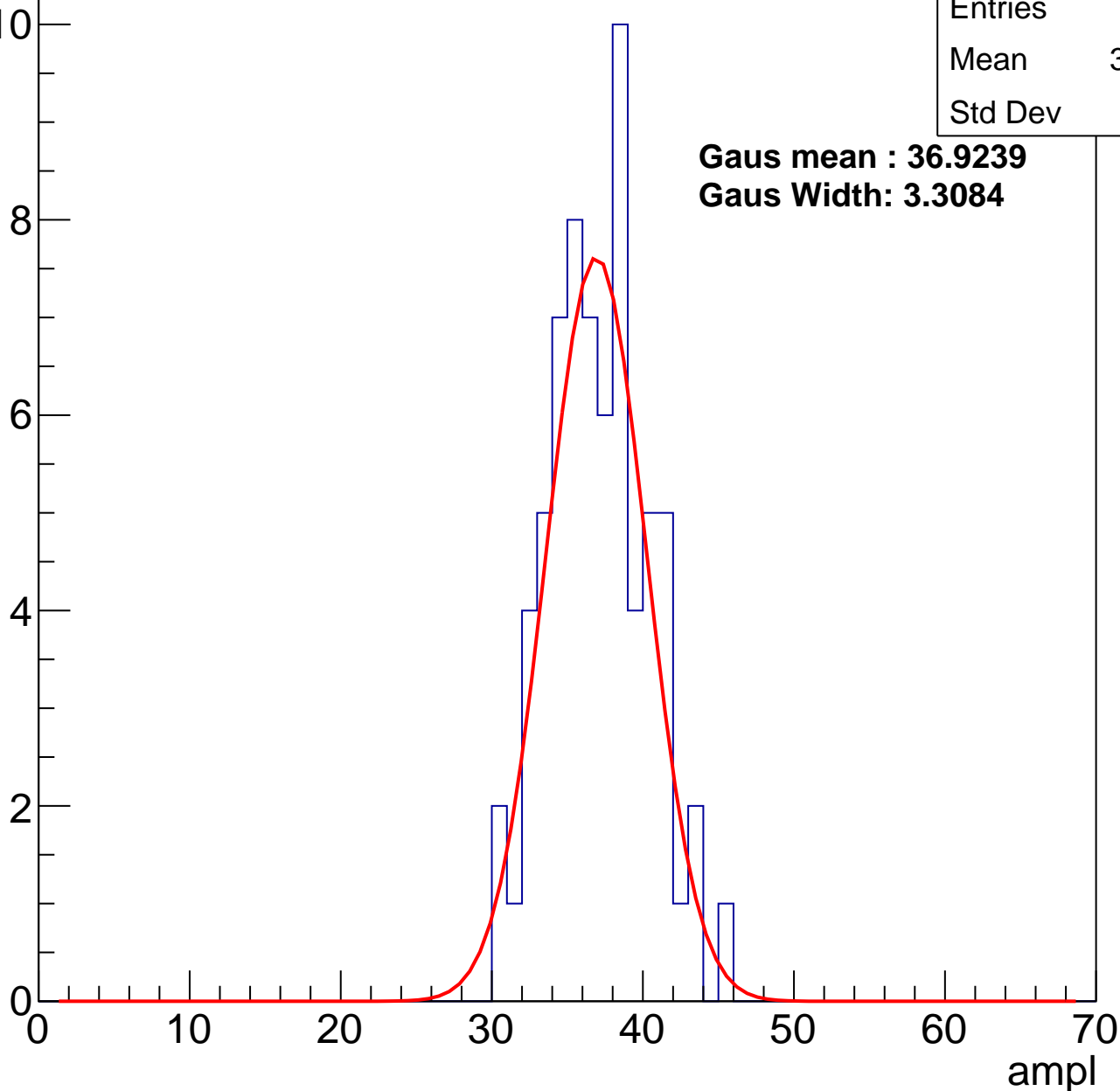
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.62
Std Dev	3.25

Gaus mean : 36.9239

Gaus Width: 3.3084



B0L000S, U2-ch48, adc2

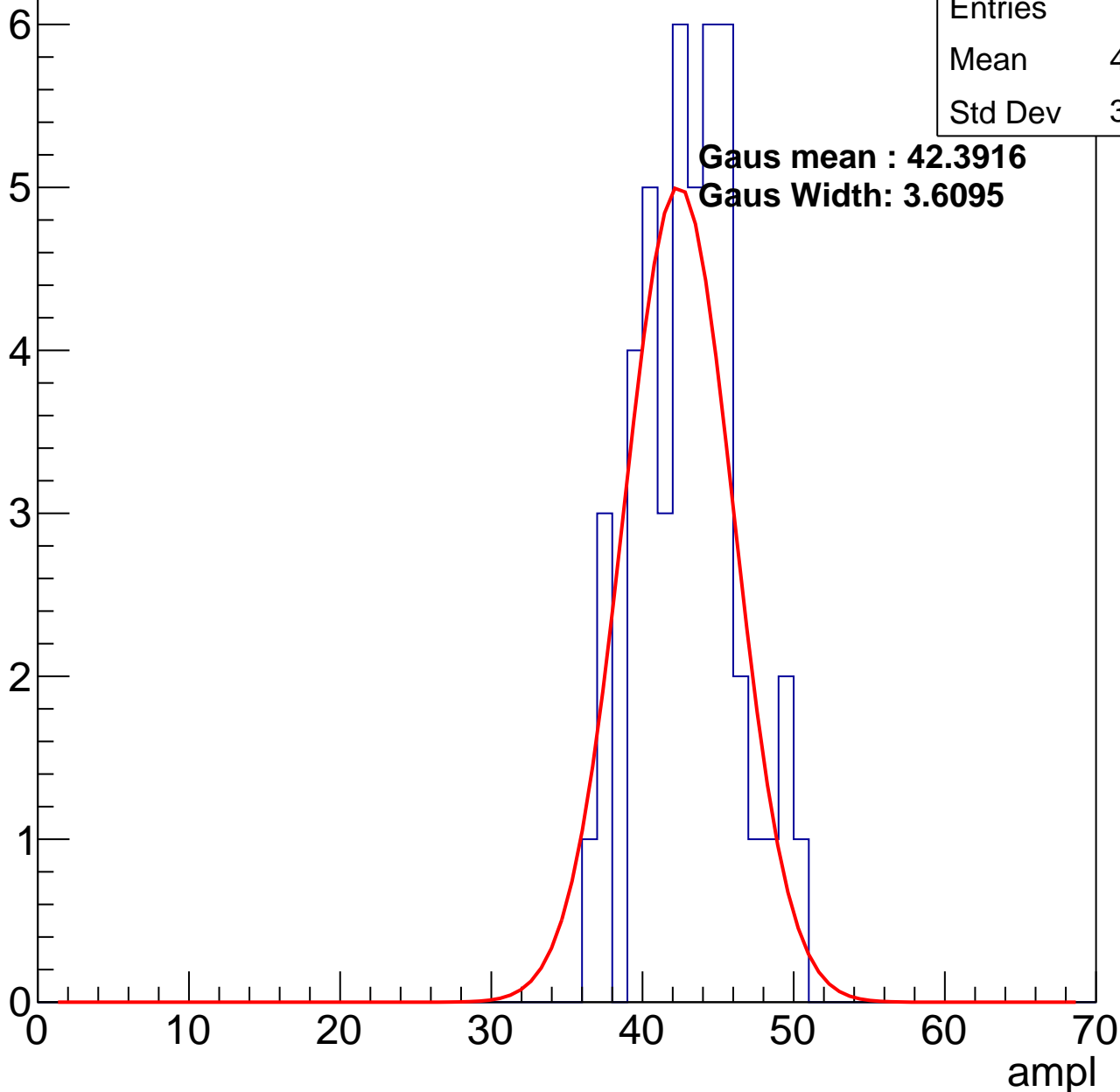
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	42.65
Std Dev	3.259

Gaus mean : 42.3916

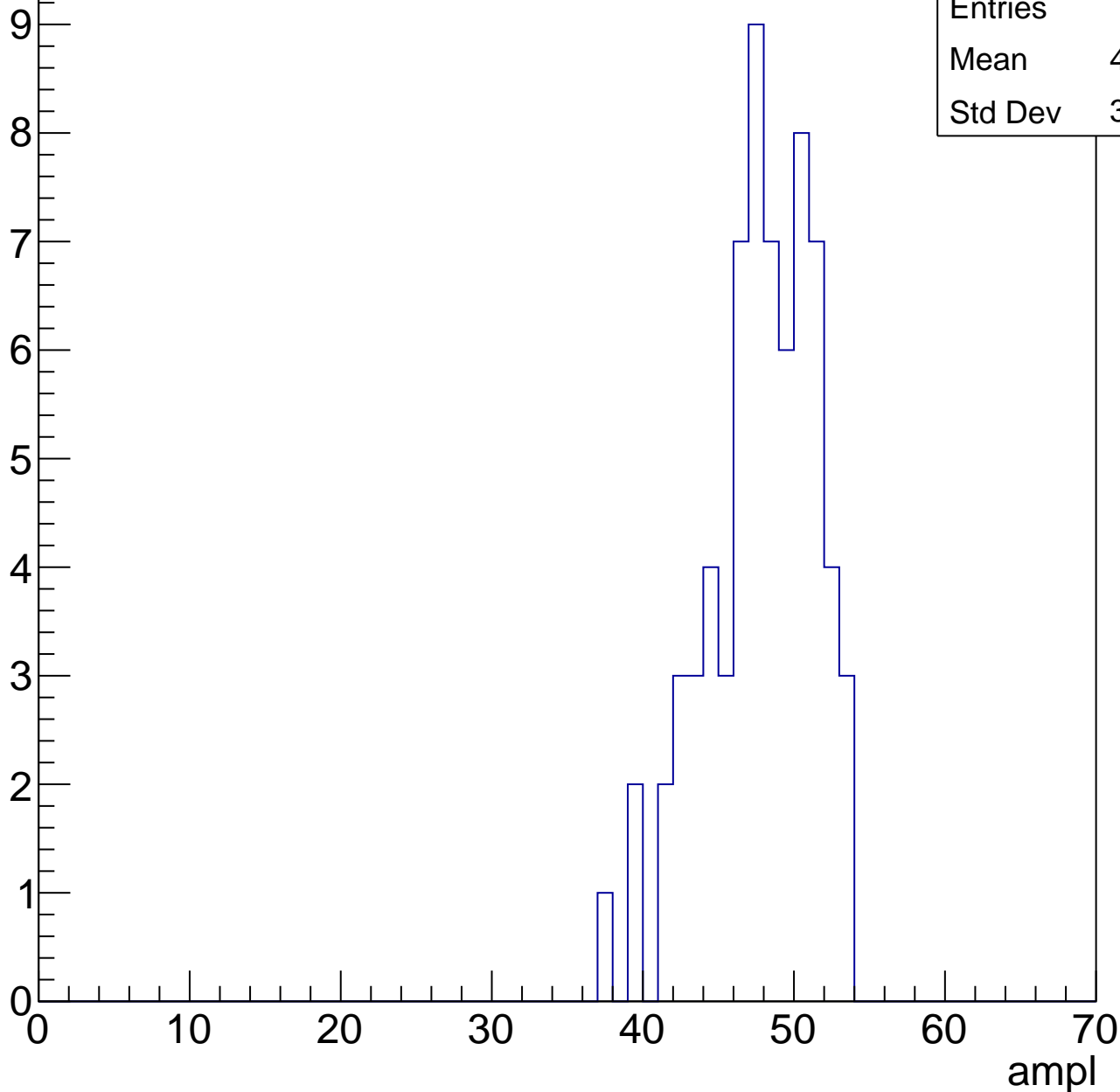
Gaus Width: 3.6095



B0L000S, U2-ch48, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



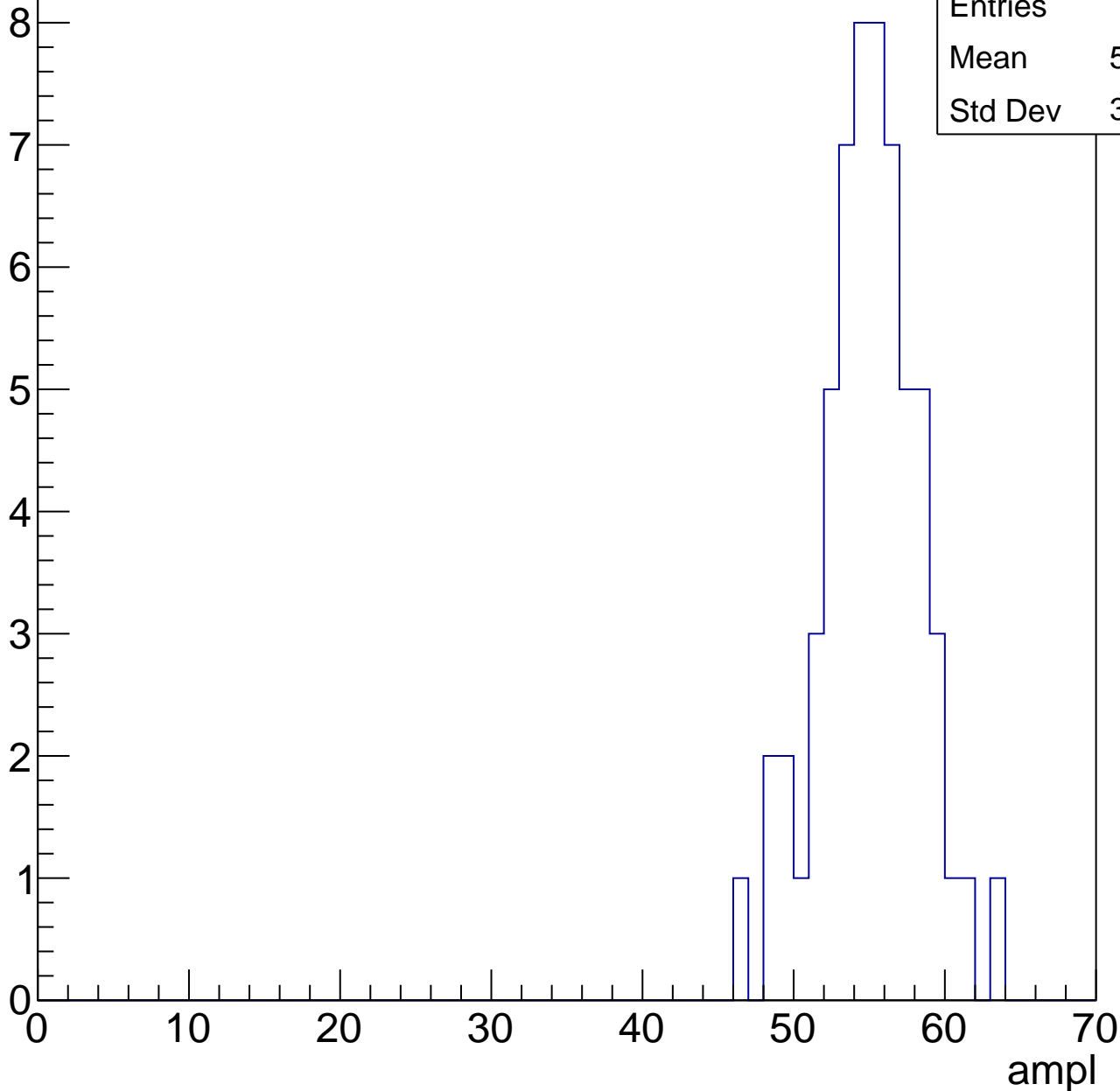
Entries	69
Mean	47.28
Std Dev	3.603

B0L000S, U2-ch48, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.57
Std Dev	3.263



B0L000S, U2-ch48, adc5

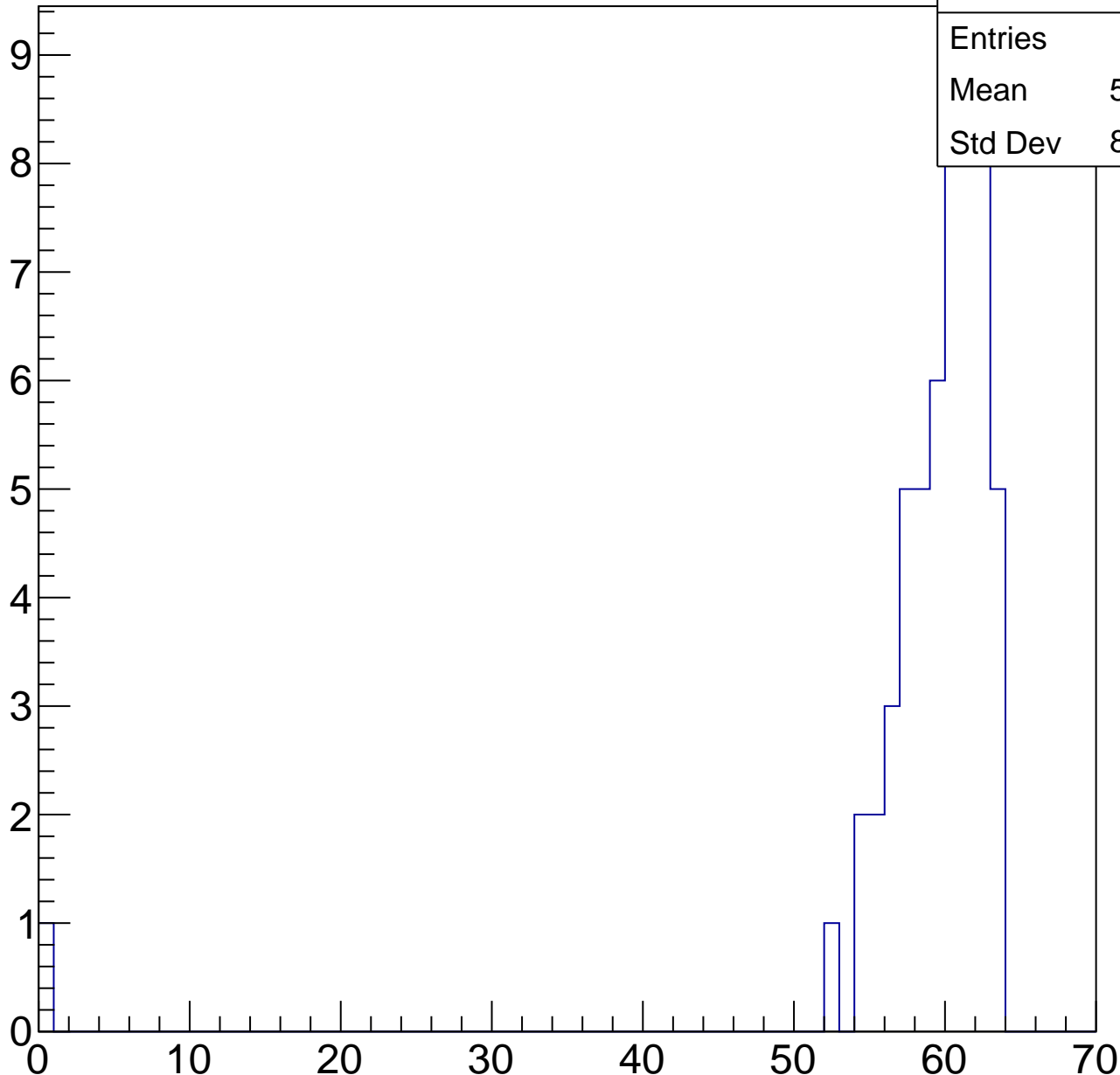
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.36
Std Dev	8.284

ampl



B0L000S, U2-ch48, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch48, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch49, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	70
Mean	30.27
Std Dev	5

Gaus mean : 30.6233

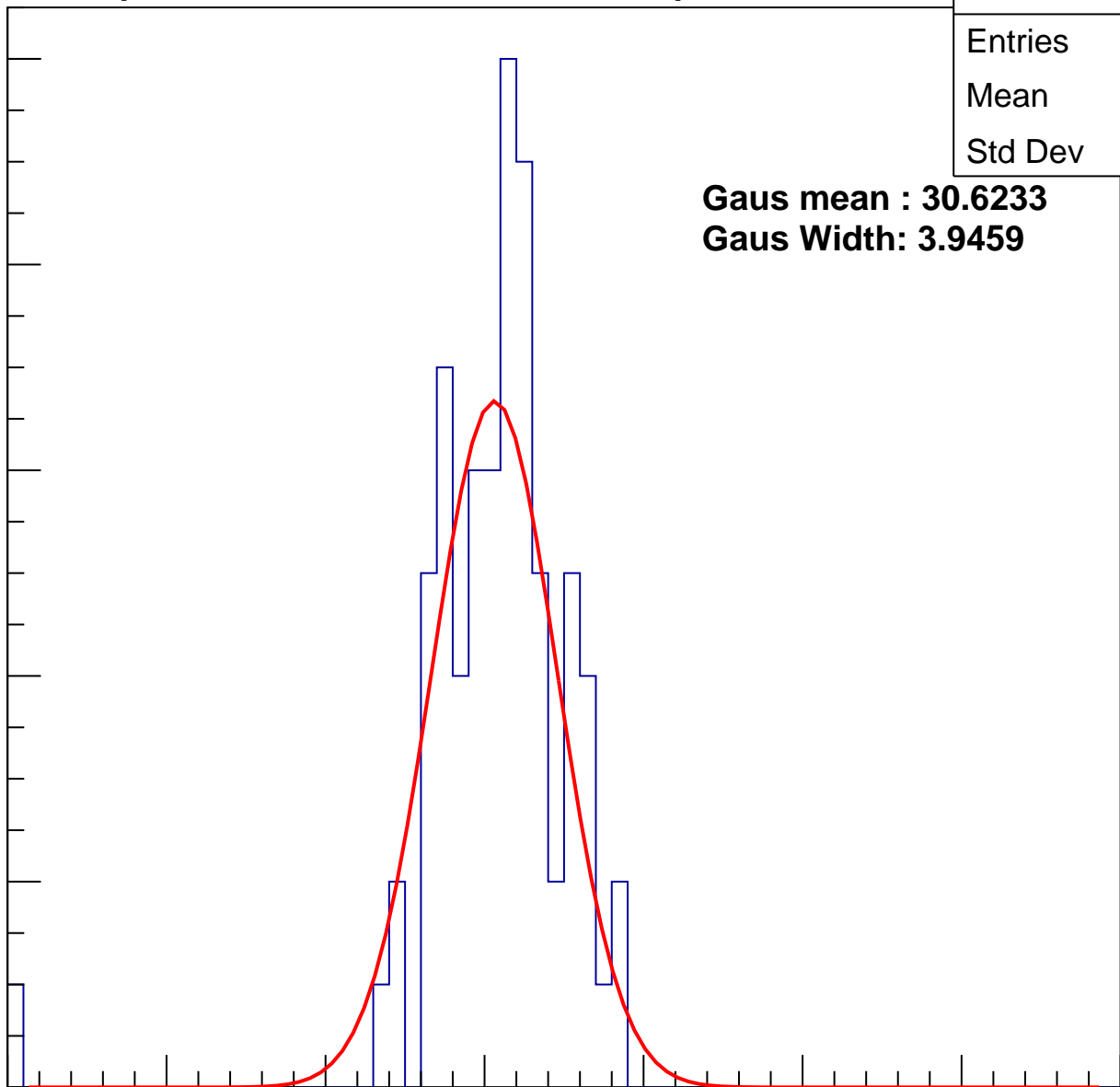
Gaus Width: 3.9459

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch49, adc1

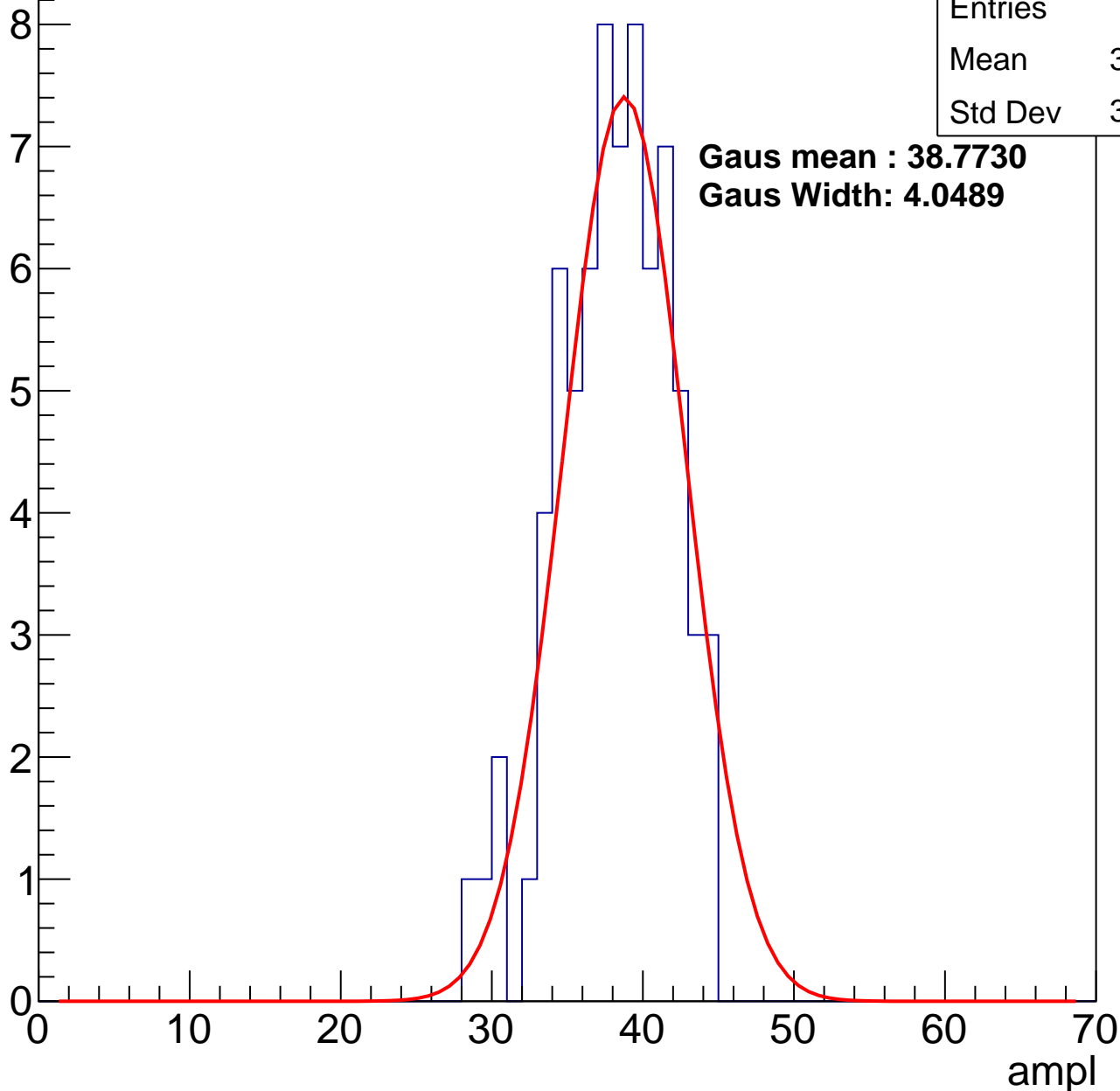
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	37.64
Std Dev	3.639

Gaus mean : 38.7730

Gaus Width: 4.0489



B0L000S, U2-ch49, adc2

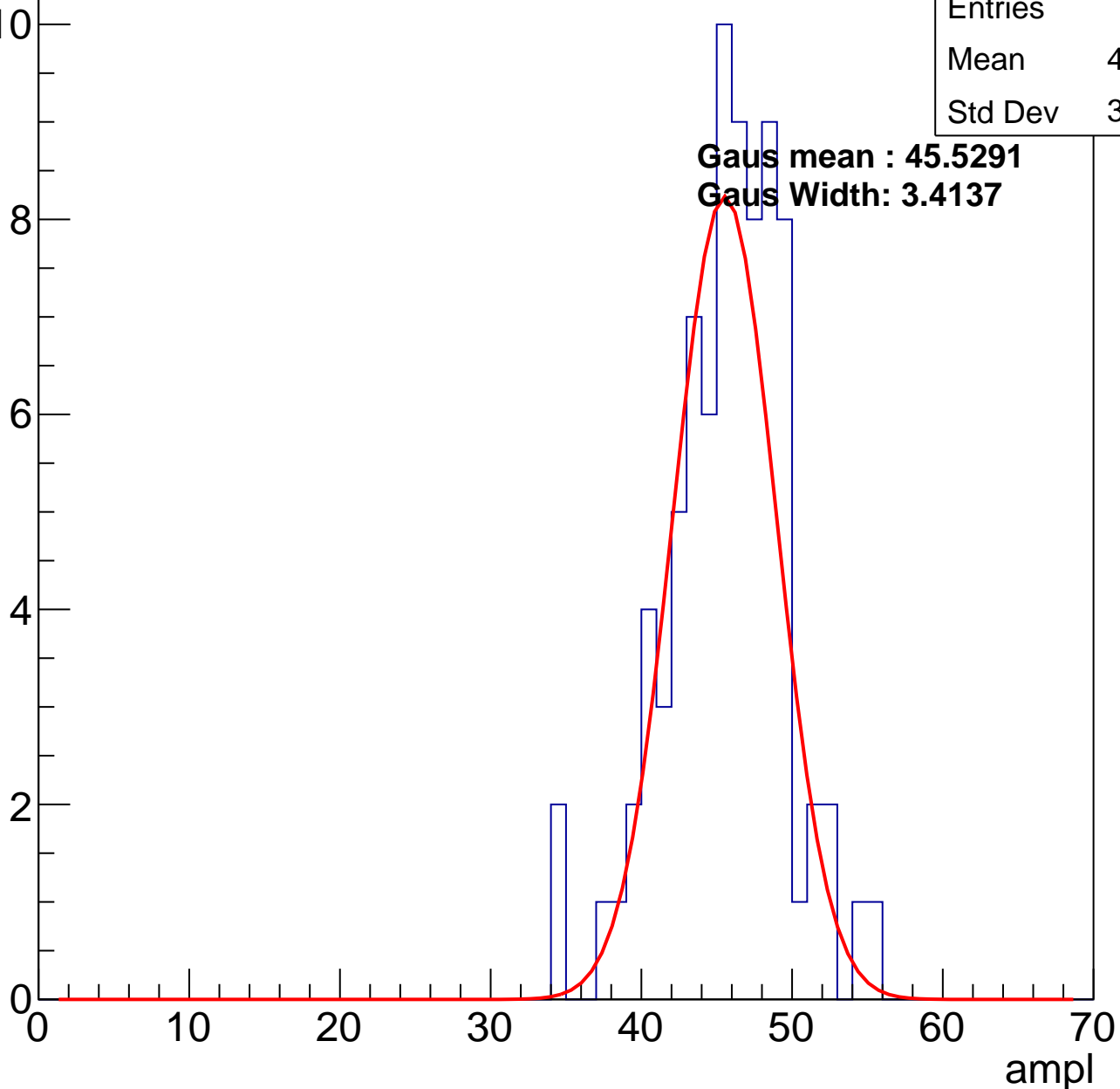
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	45.22
Std Dev	3.945

Gaus mean : 45.5291

Gaus Width: 3.4137

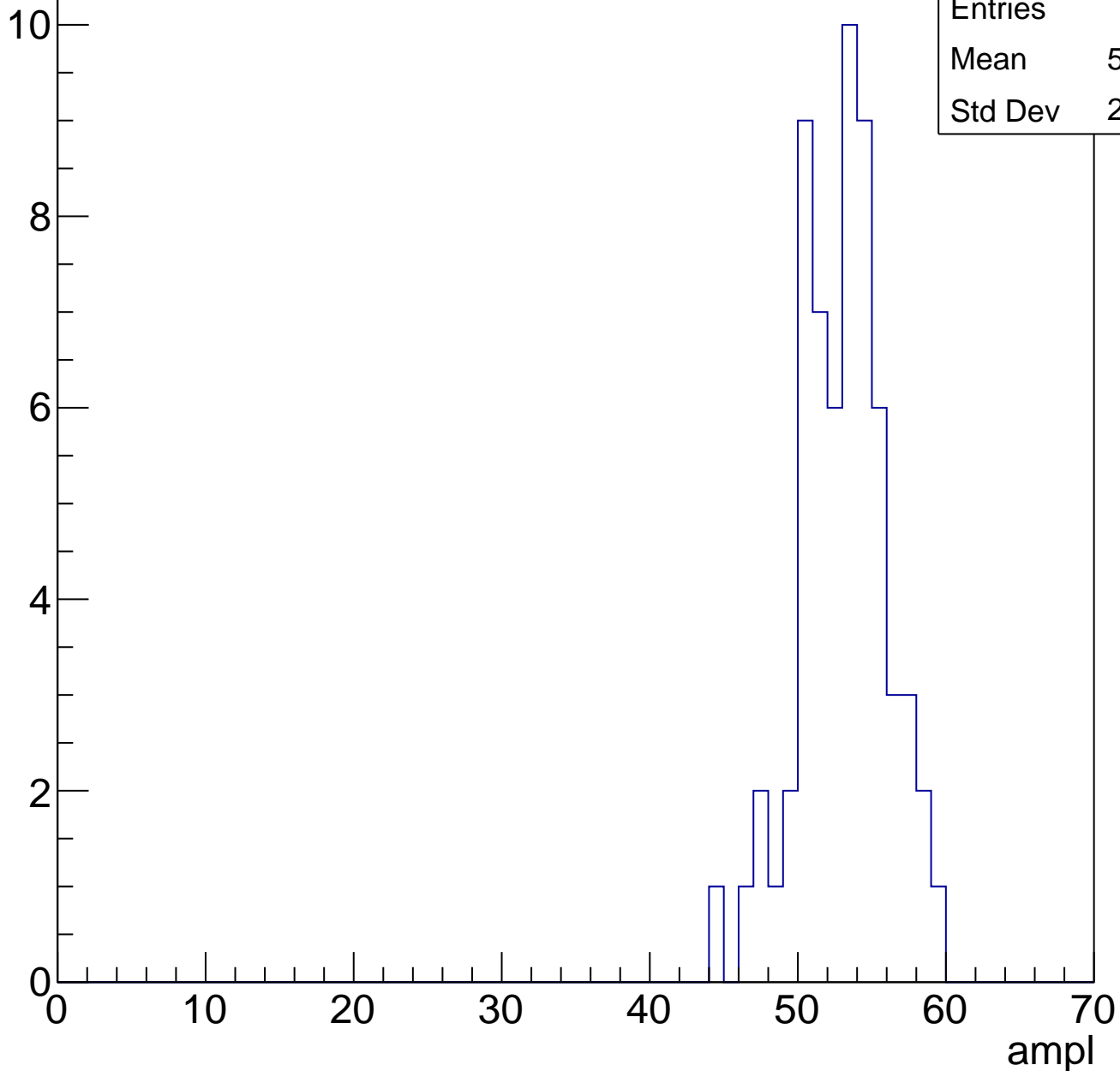


B0L000S, U2-ch49, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	63
Mean	52.52
Std Dev	2.965

Entry

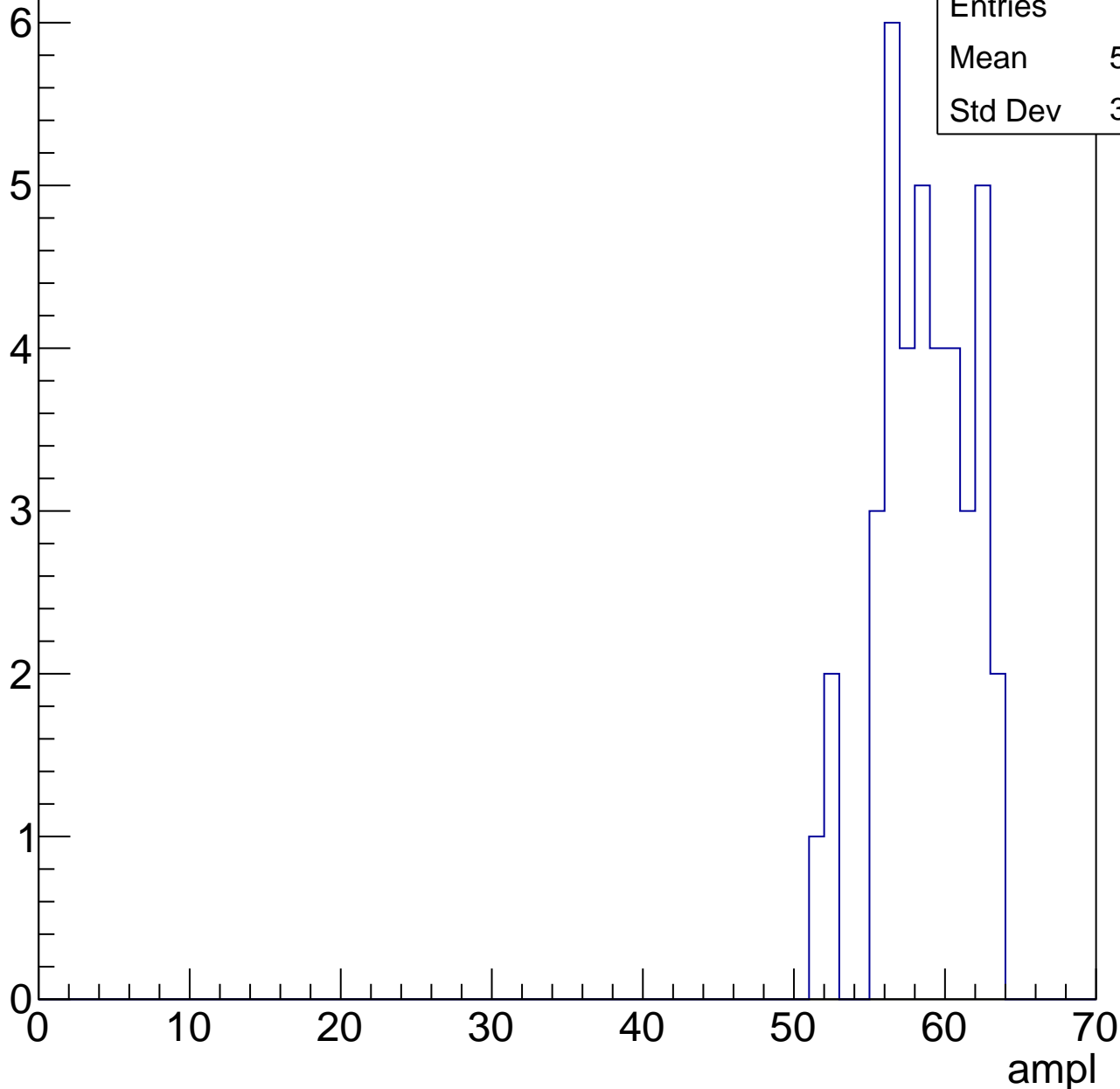


B0L000S, U2-ch49, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.18
Std Dev	3.003

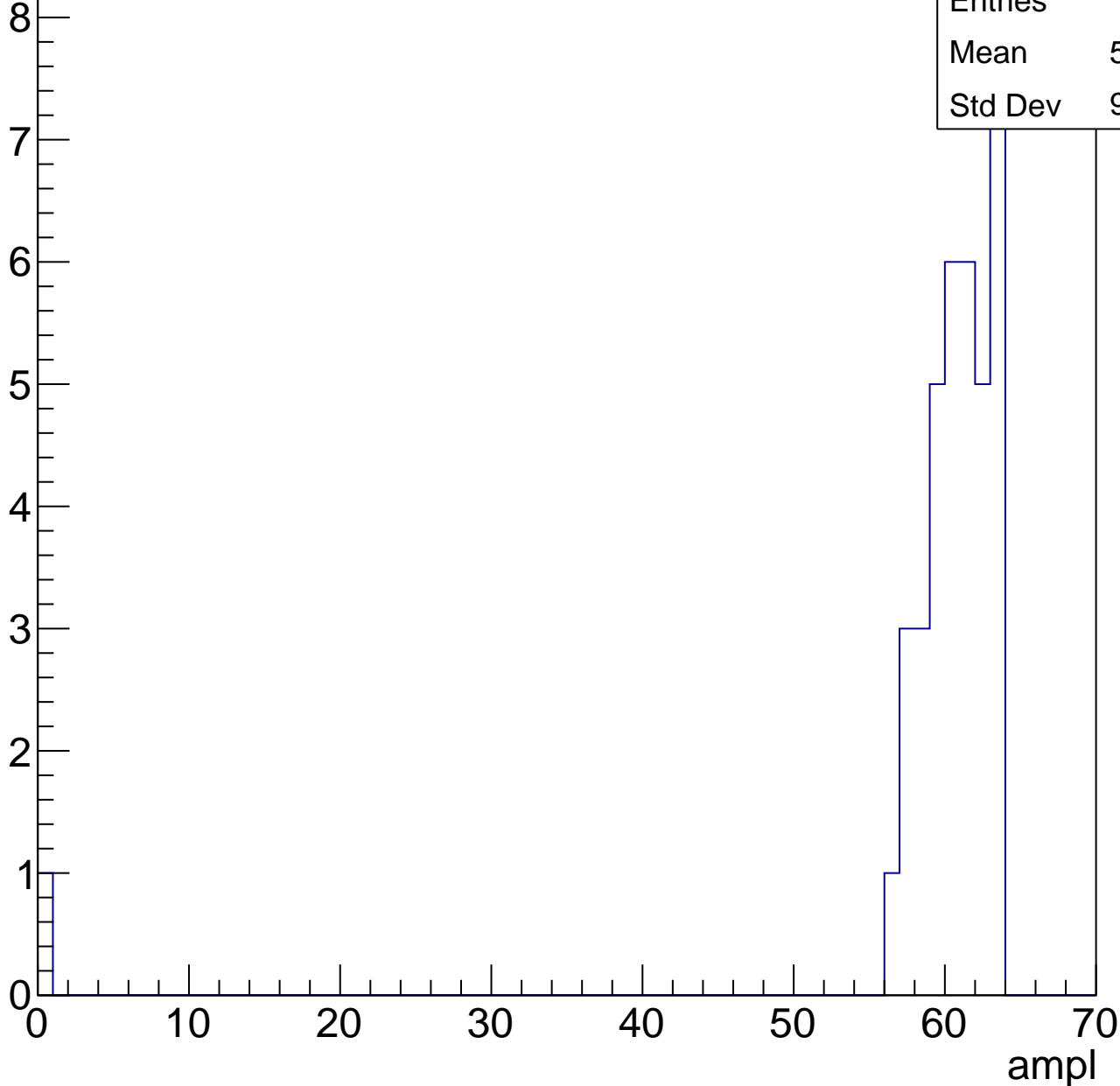


B0L000S, U2-ch49, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

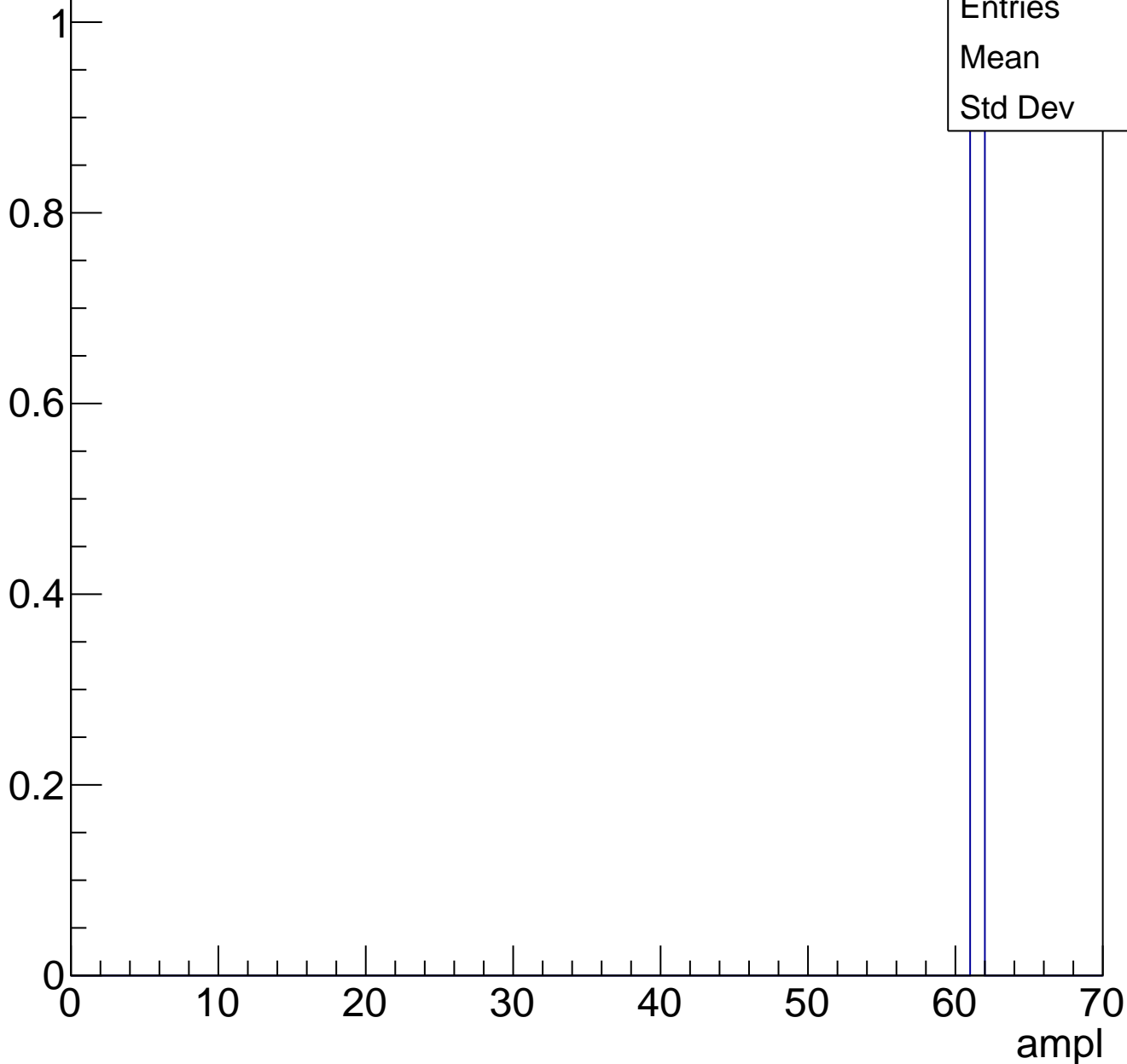
Entries	38
Mean	58.84
Std Dev	9.877



B0L000S, U2-ch49, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch49, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch50, adc0

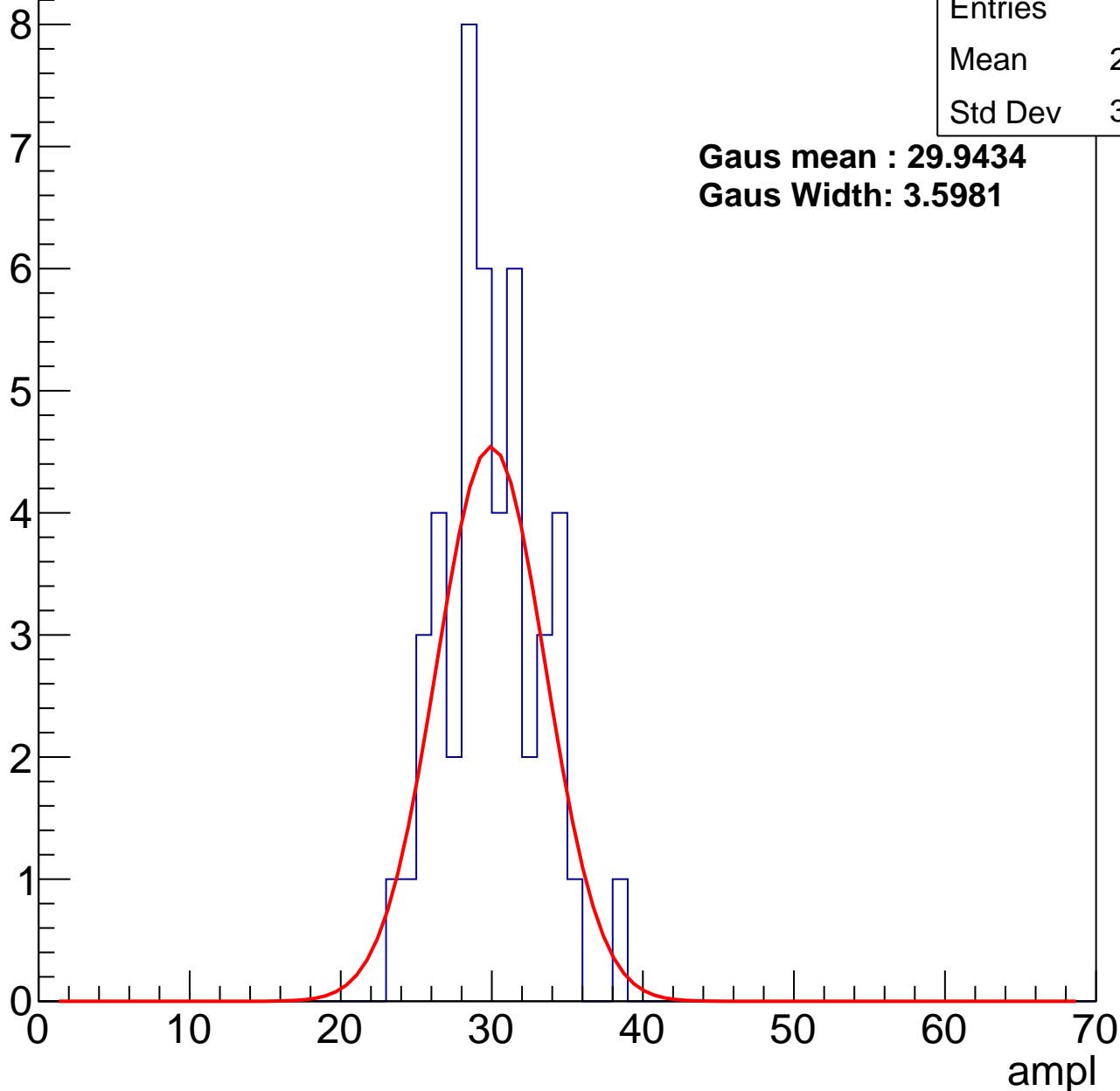
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	29.48
Std Dev	3.167

Gaus mean : 29.9434

Gaus Width: 3.5981



B0L000S, U2-ch50, adc1

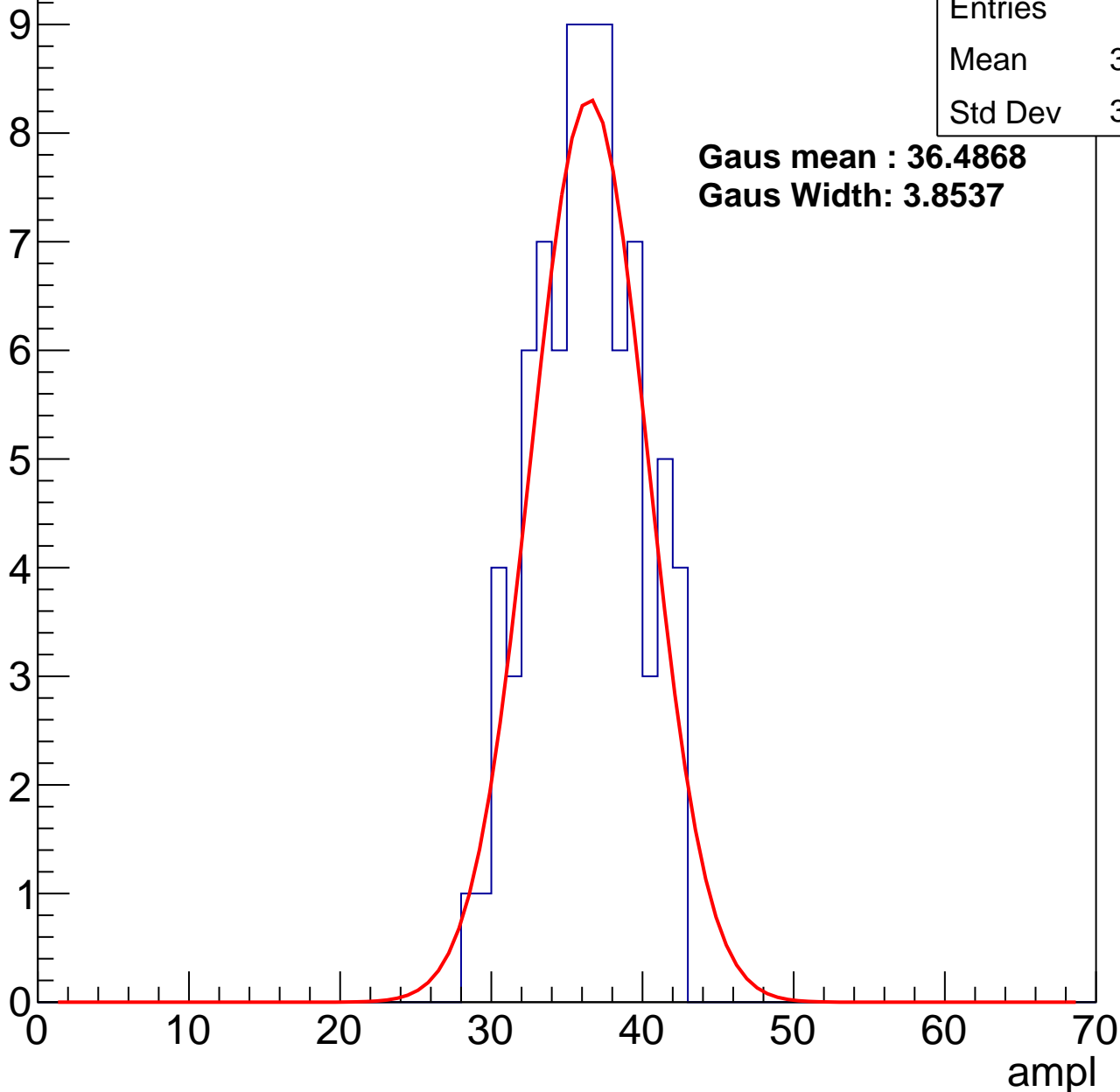
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	35.79
Std Dev	3.416

Gaus mean : 36.4868

Gaus Width: 3.8537



B0L000S, U2-ch50, adc2

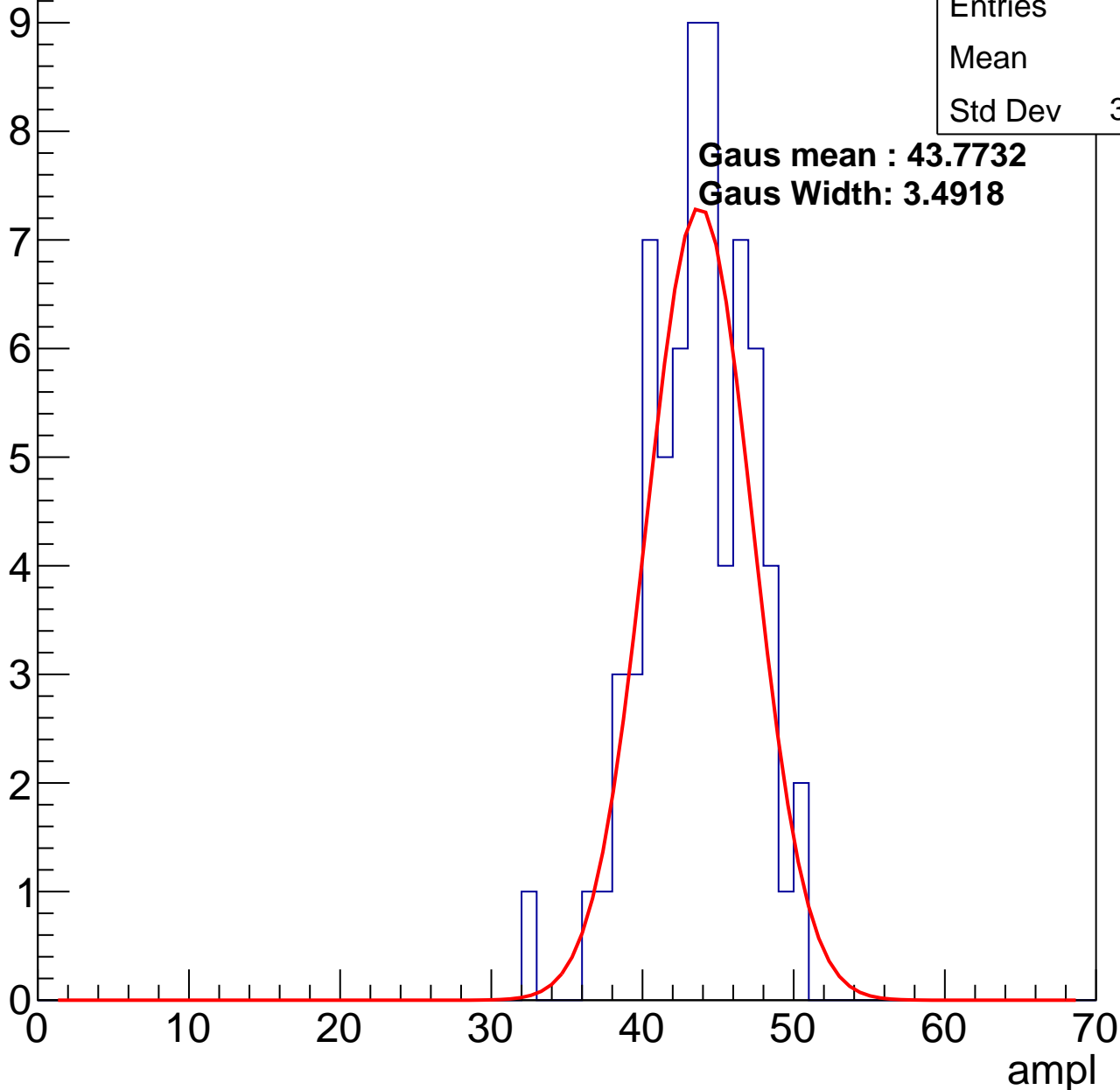
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.2
Std Dev	3.475

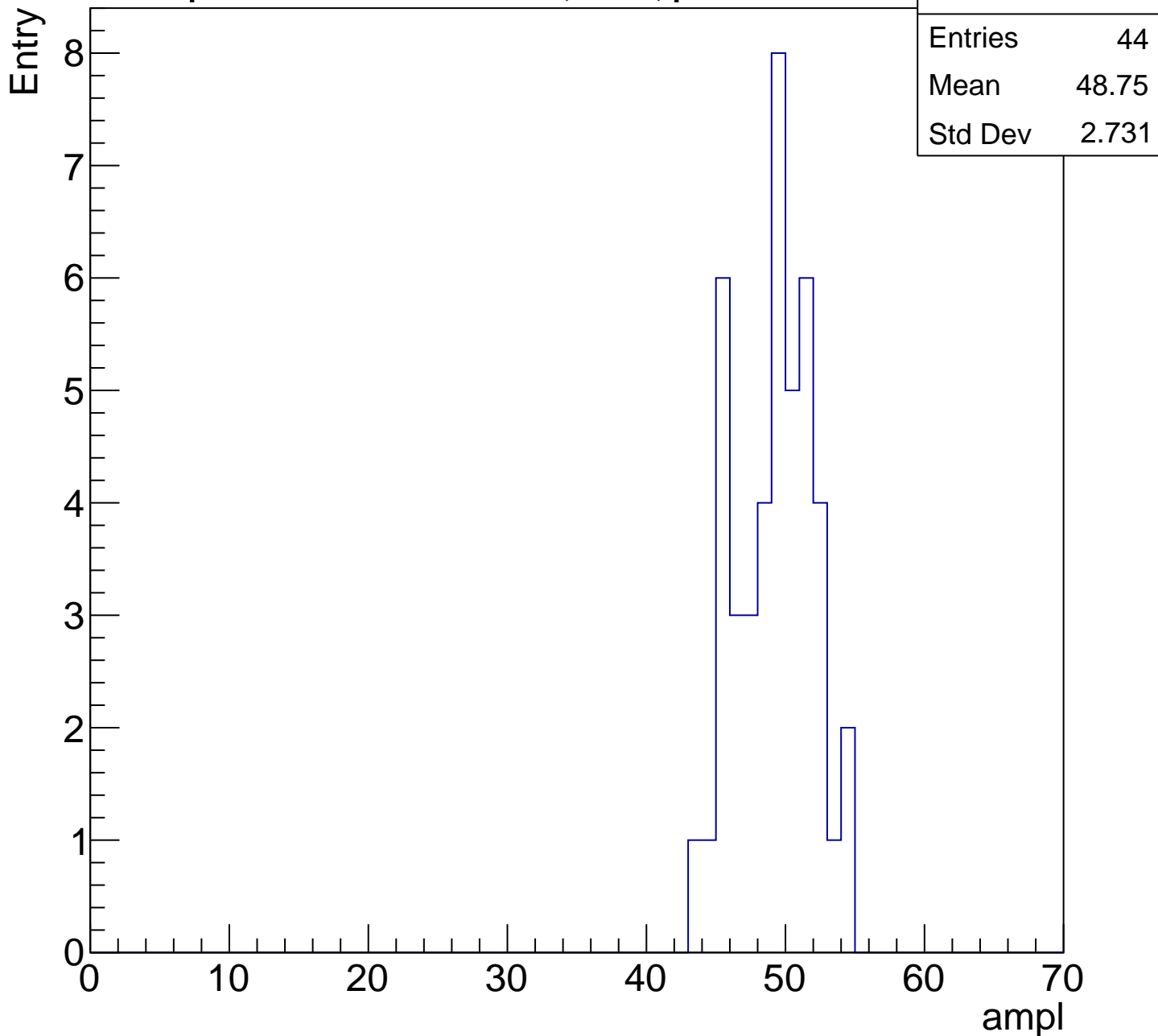
Gaus mean : 43.7732

Gaus Width: 3.4918



B0L000S, U2-ch50, adc3

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch50, adc4

calib_packv5_042523_0143.root, FC#5, port B1

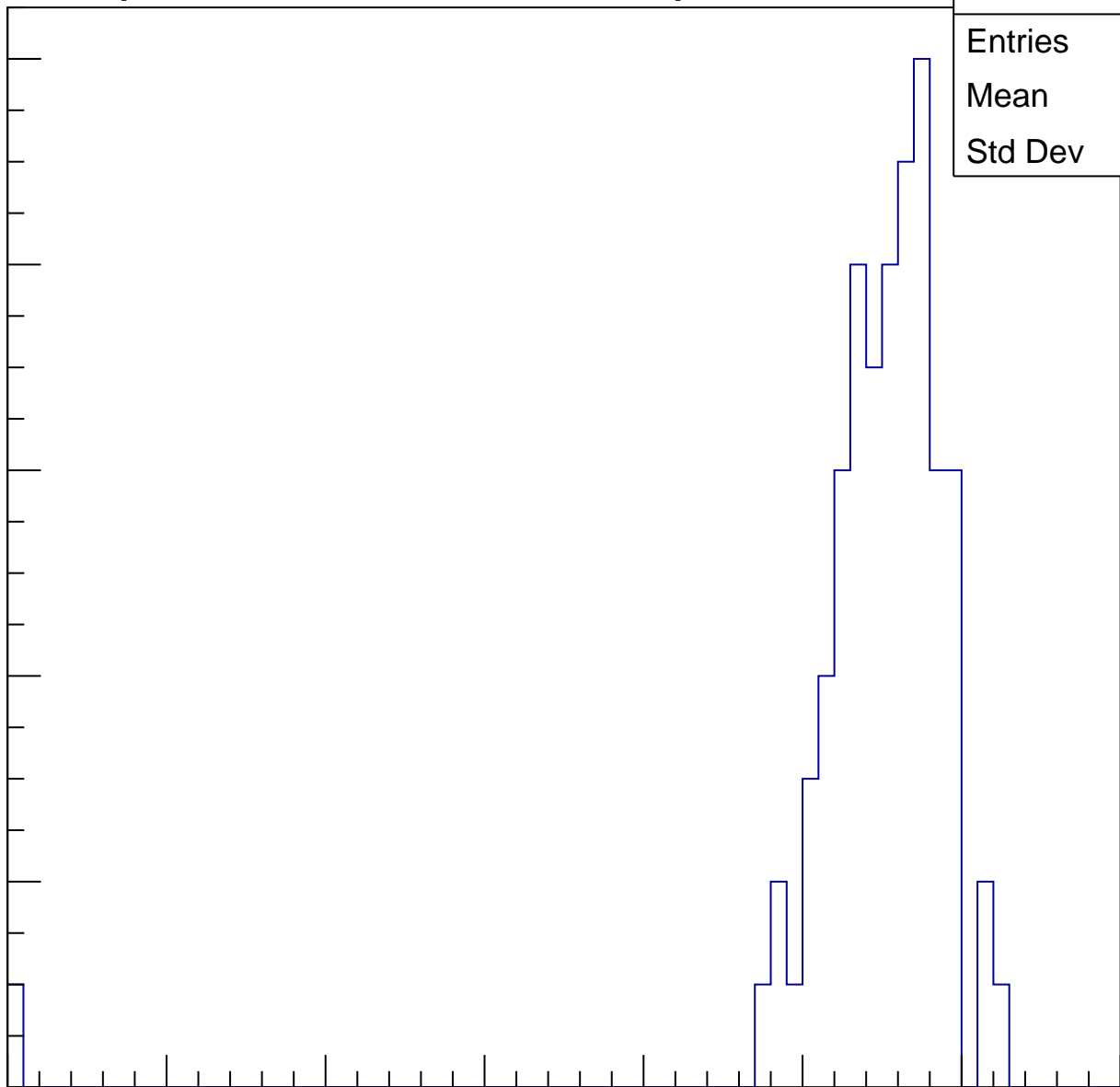
Entries	75
Mean	54.13
Std Dev	7.041

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch50, adc5

calib_packv5_042523_0143.root, FC#5, port B1

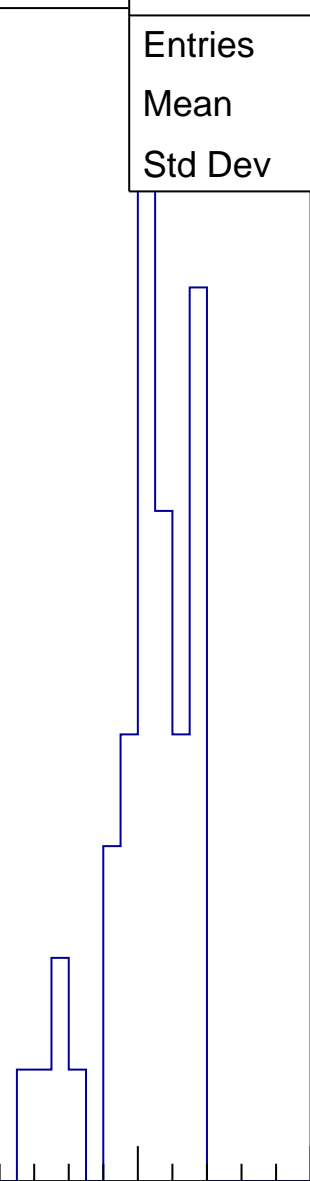
Entries	40
Mean	60.02
Std Dev	2.554

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

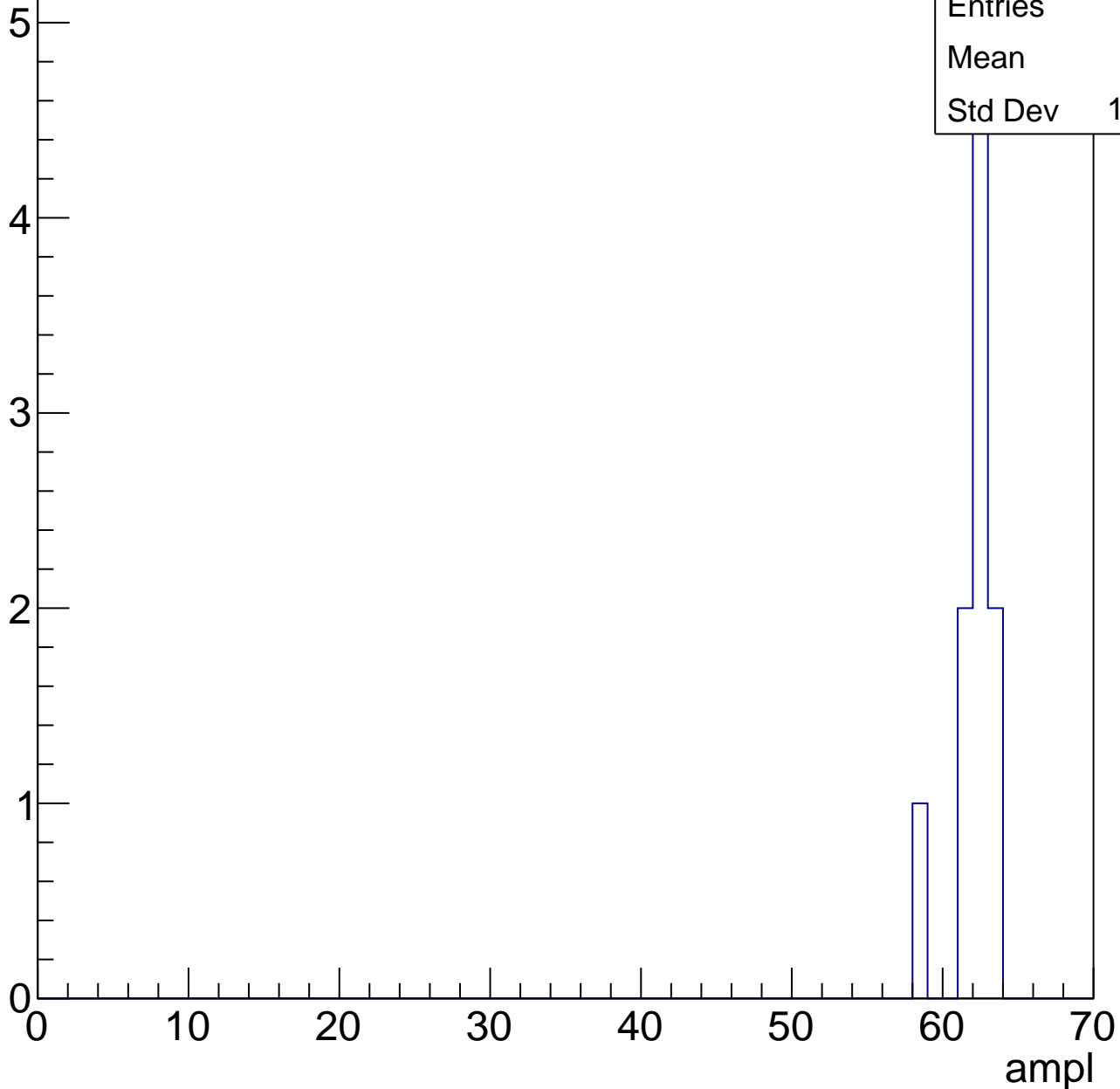


B0L000S, U2-ch50, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	10
Mean	61.6
Std Dev	1.356



B0L000S, U2-ch50, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L000S, U2-ch51, adc0

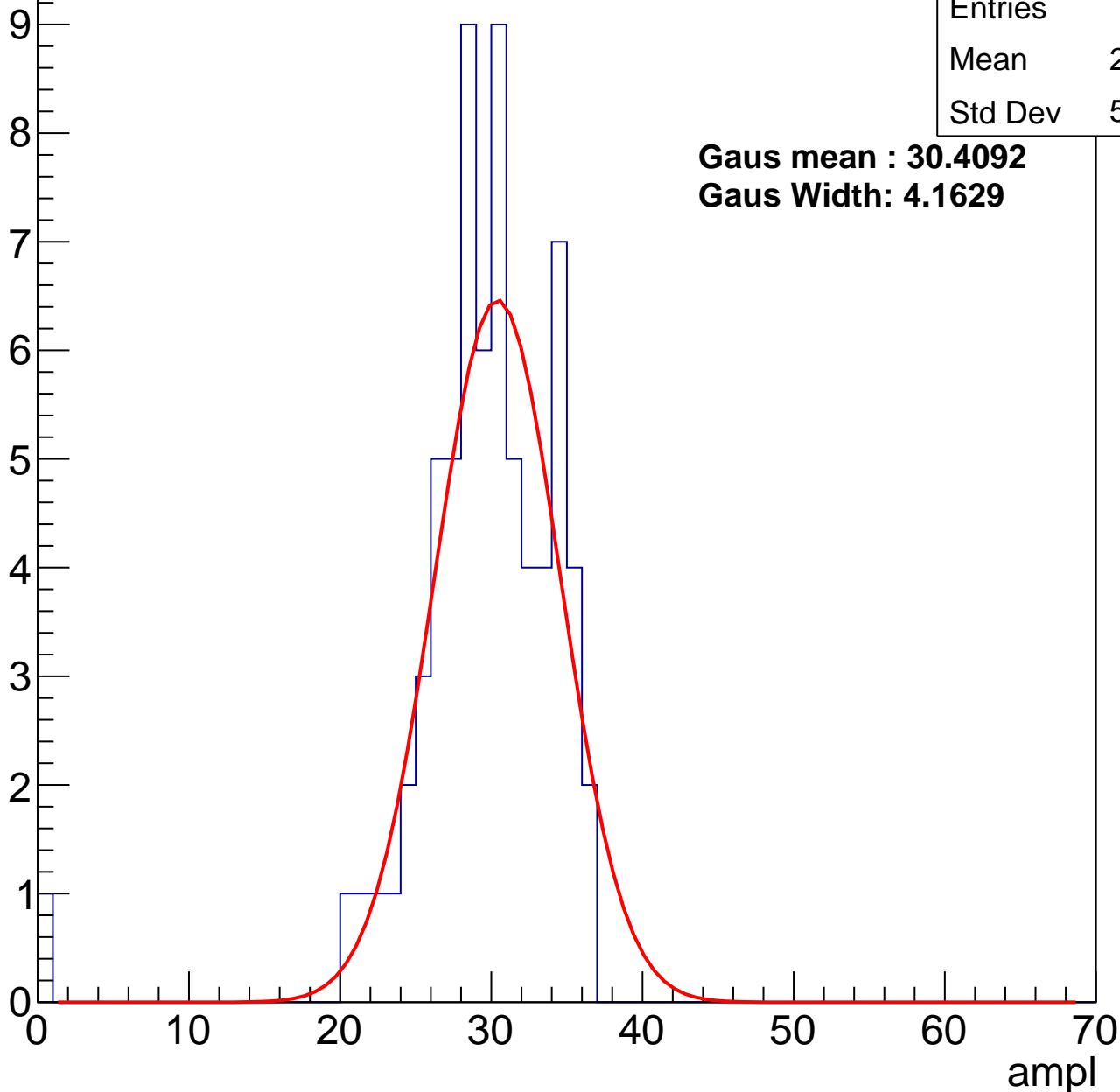
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.07
Std Dev	5.055

Gaus mean : 30.4092

Gaus Width: 4.1629



B0L000S, U2-ch51, adc1

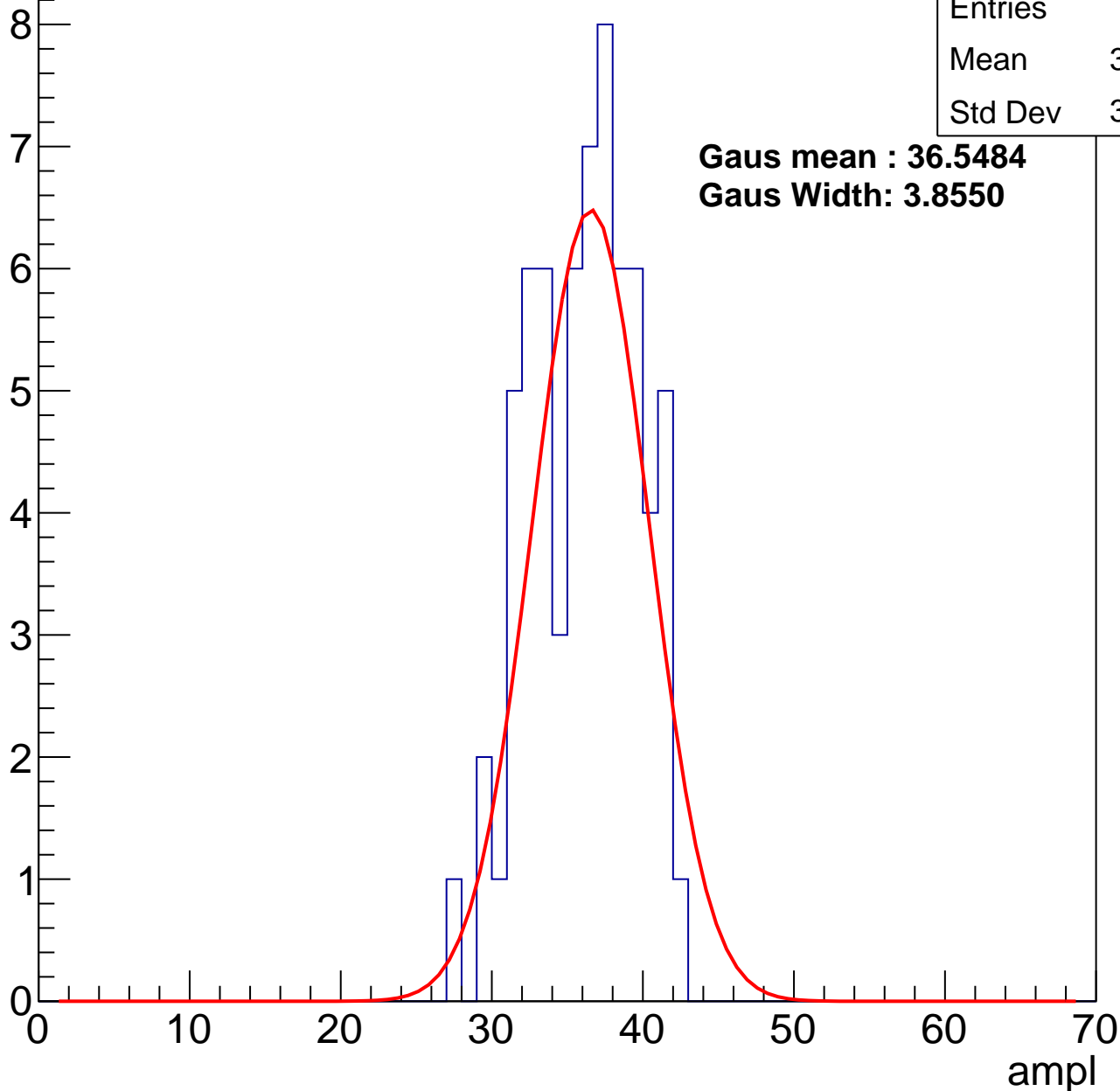
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.66
Std Dev	3.479

Gaus mean : 36.5484

Gaus Width: 3.8550



B0L000S, U2-ch51, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	42.44
Std Dev	3.076

Gaus mean : 42.6647

Gaus Width: 3.1666

Entry

10

8

6

4

2

0

0

10

20

30

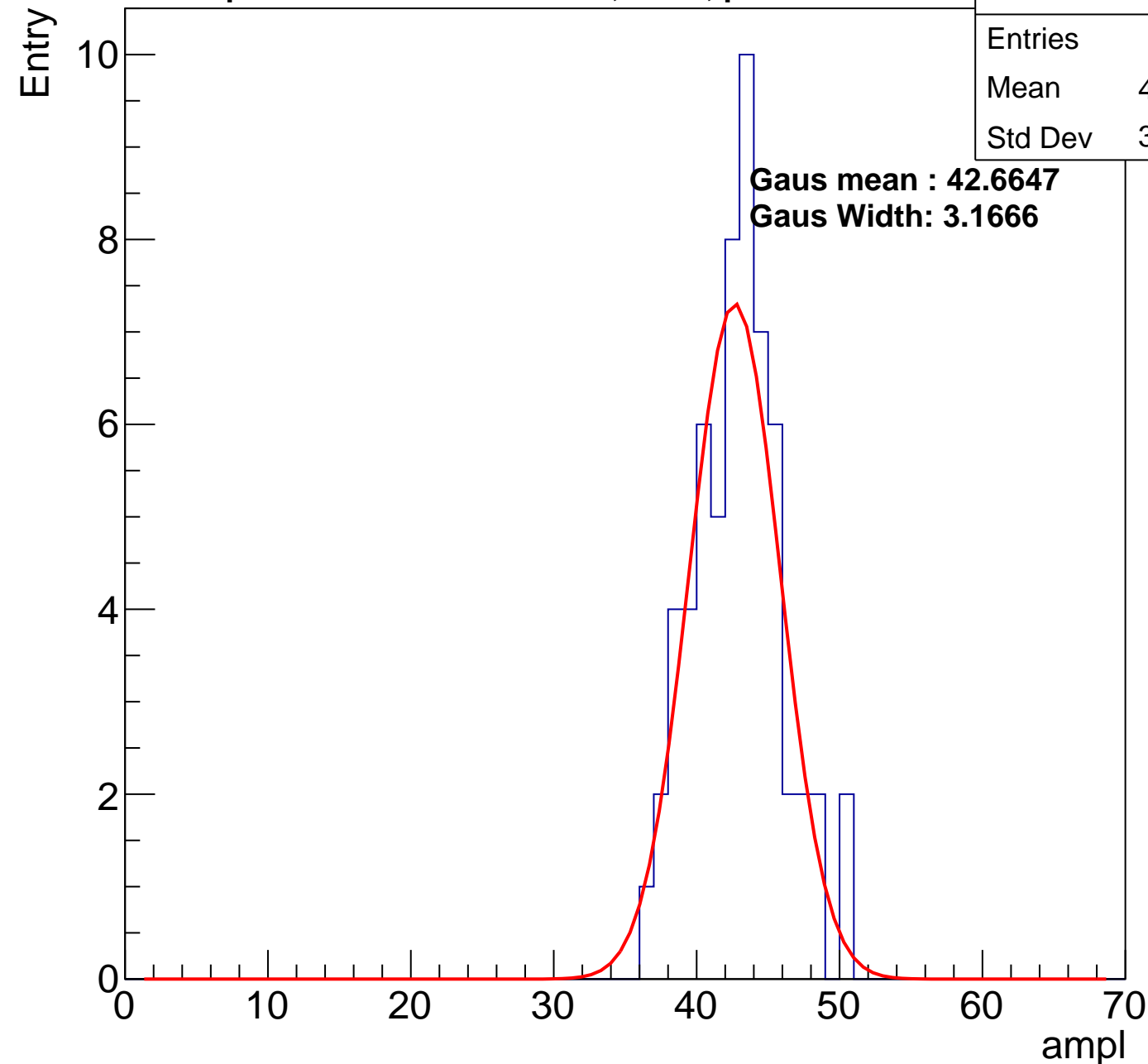
40

50

60

70

ampl

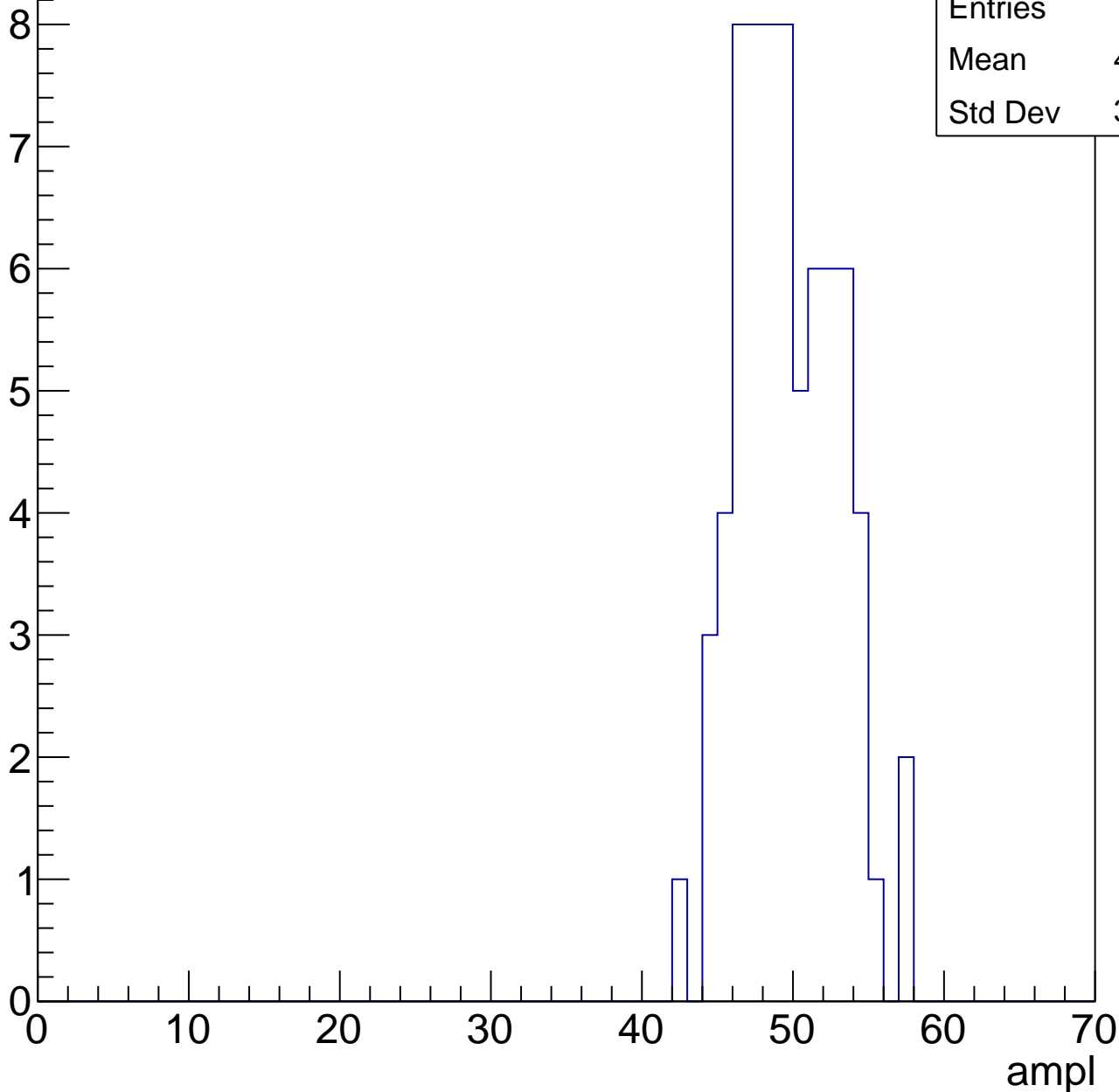


B0L000S, U2-ch51, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.21
Std Dev	3.251

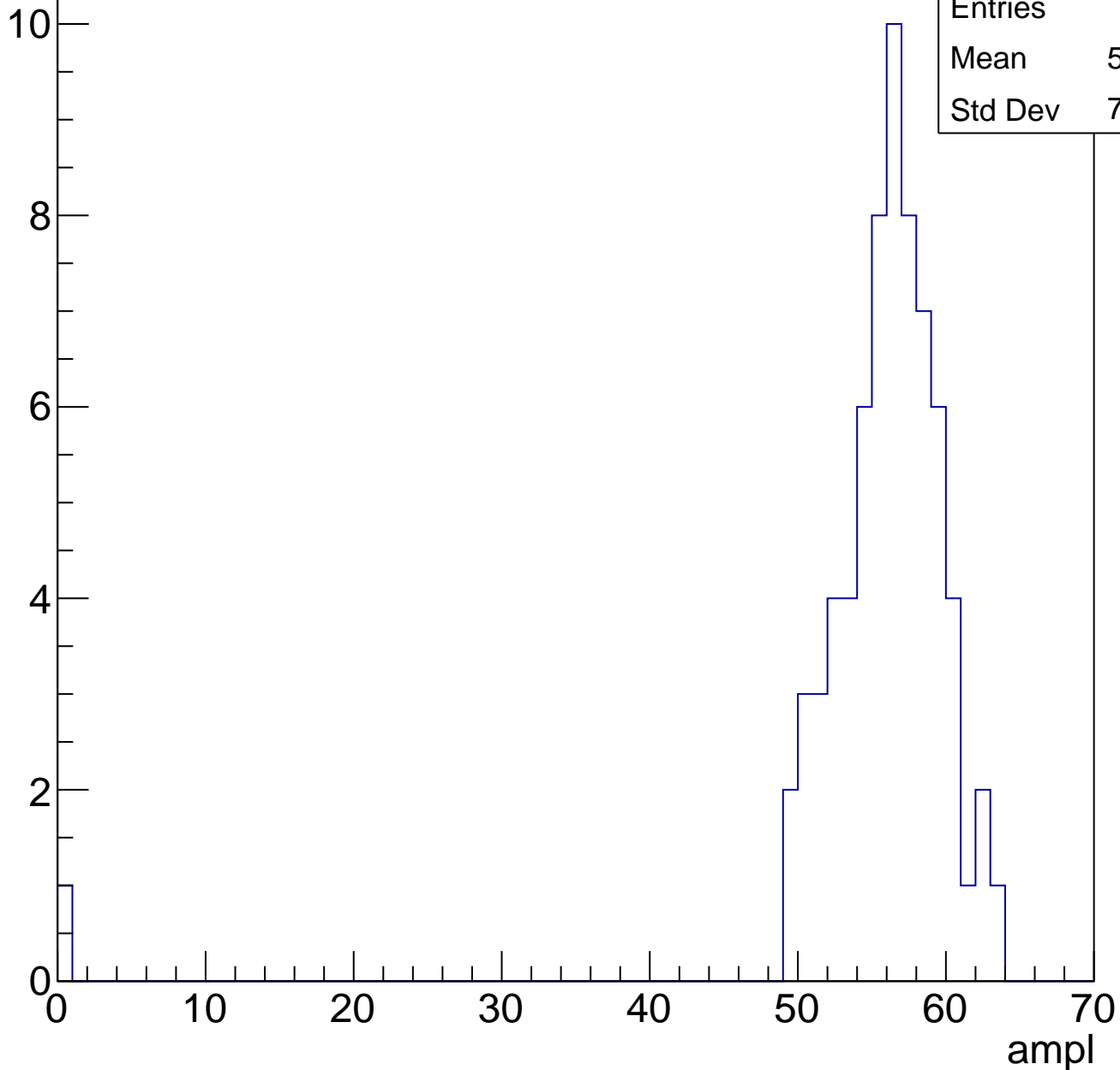


B0L000S, U2-ch51, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	70
Mean	54.99
Std Dev	7.346

Entry



B0L000S, U2-ch51, adc5

calib_packv5_042523_0143.root, FC#5, port B1

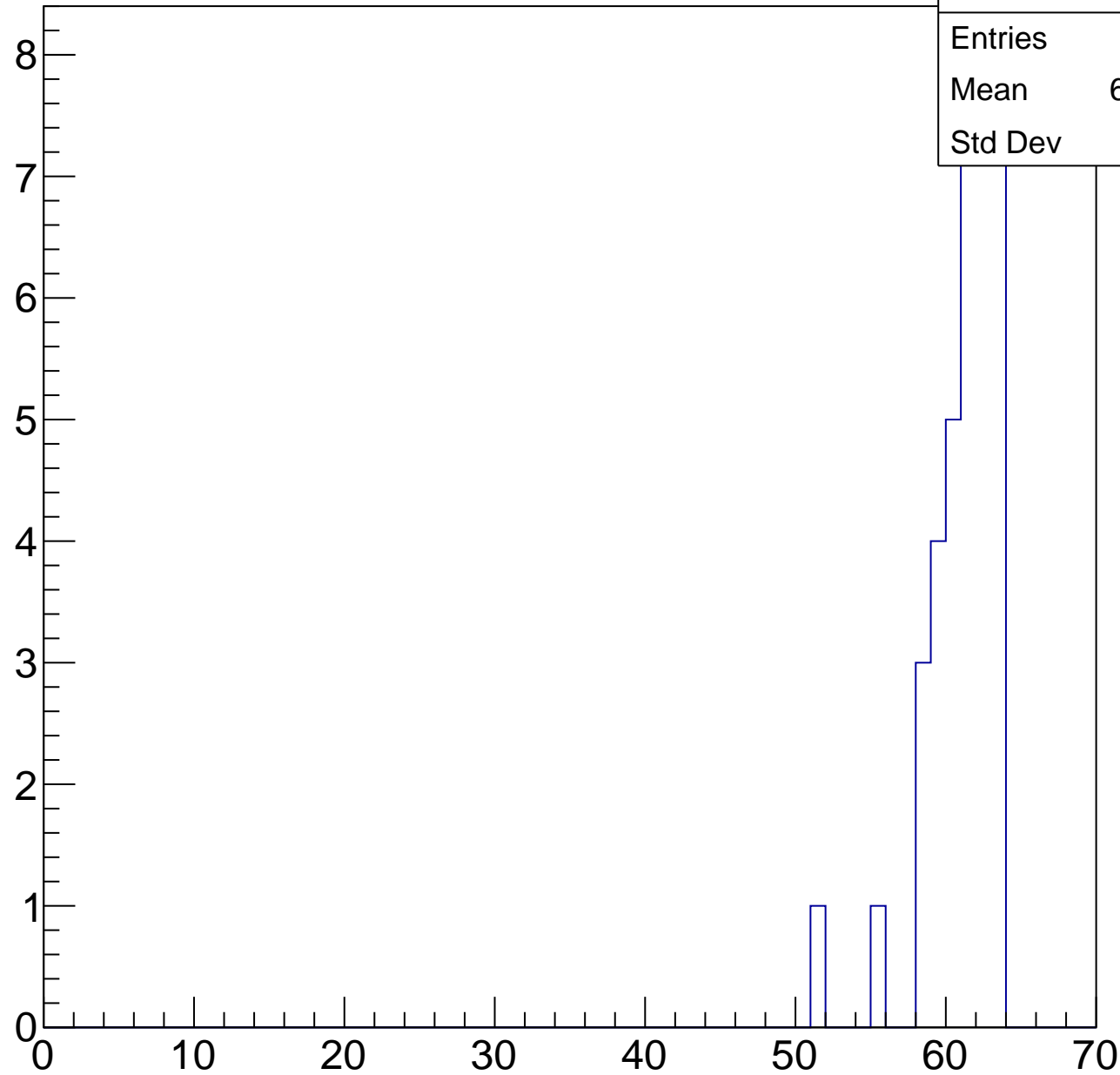
Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.63
Std Dev	2.4

ampl

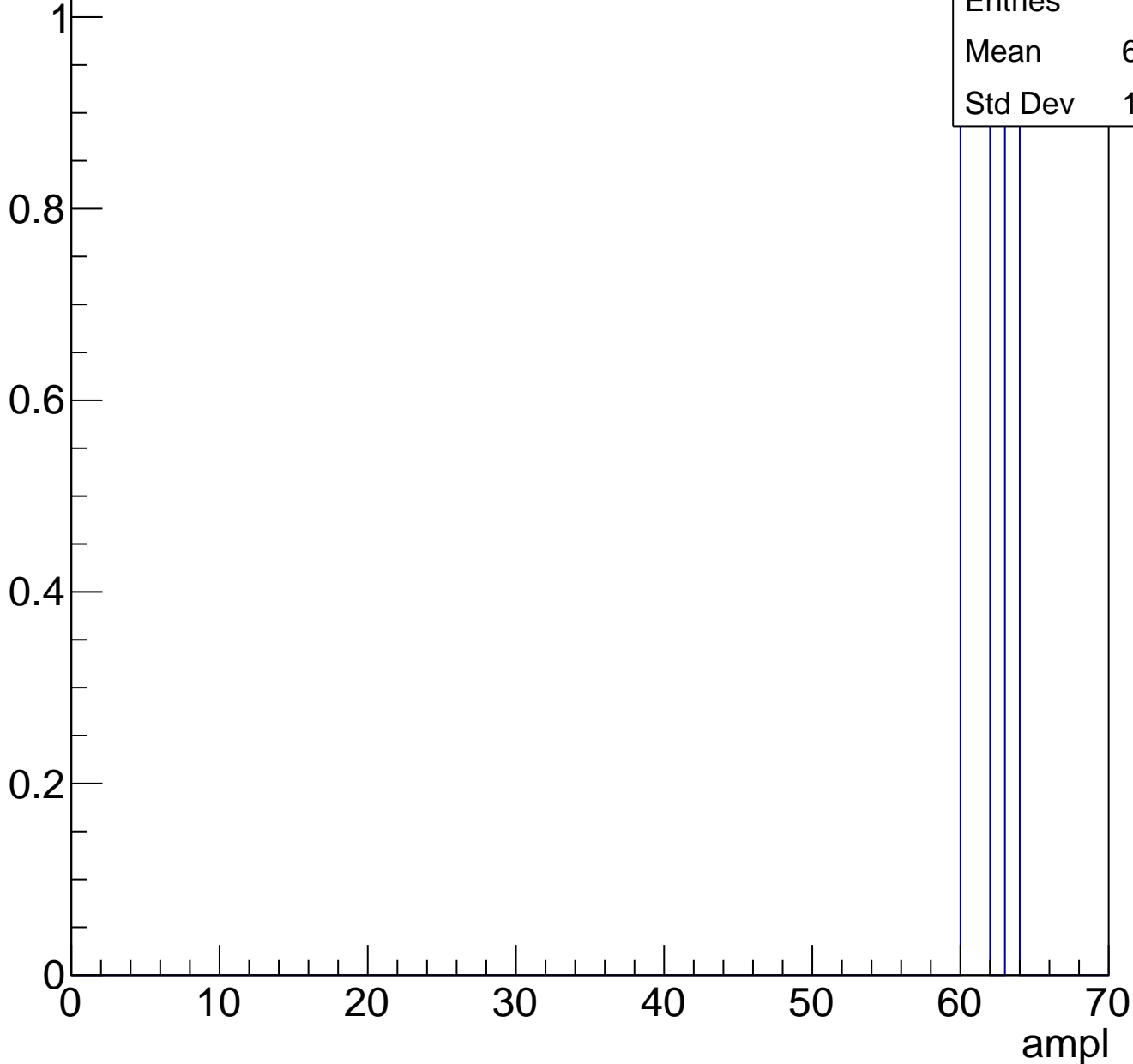
0 10 20 30 40 50 60 70



B0L000S, U2-ch51, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch51, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch52, adc0

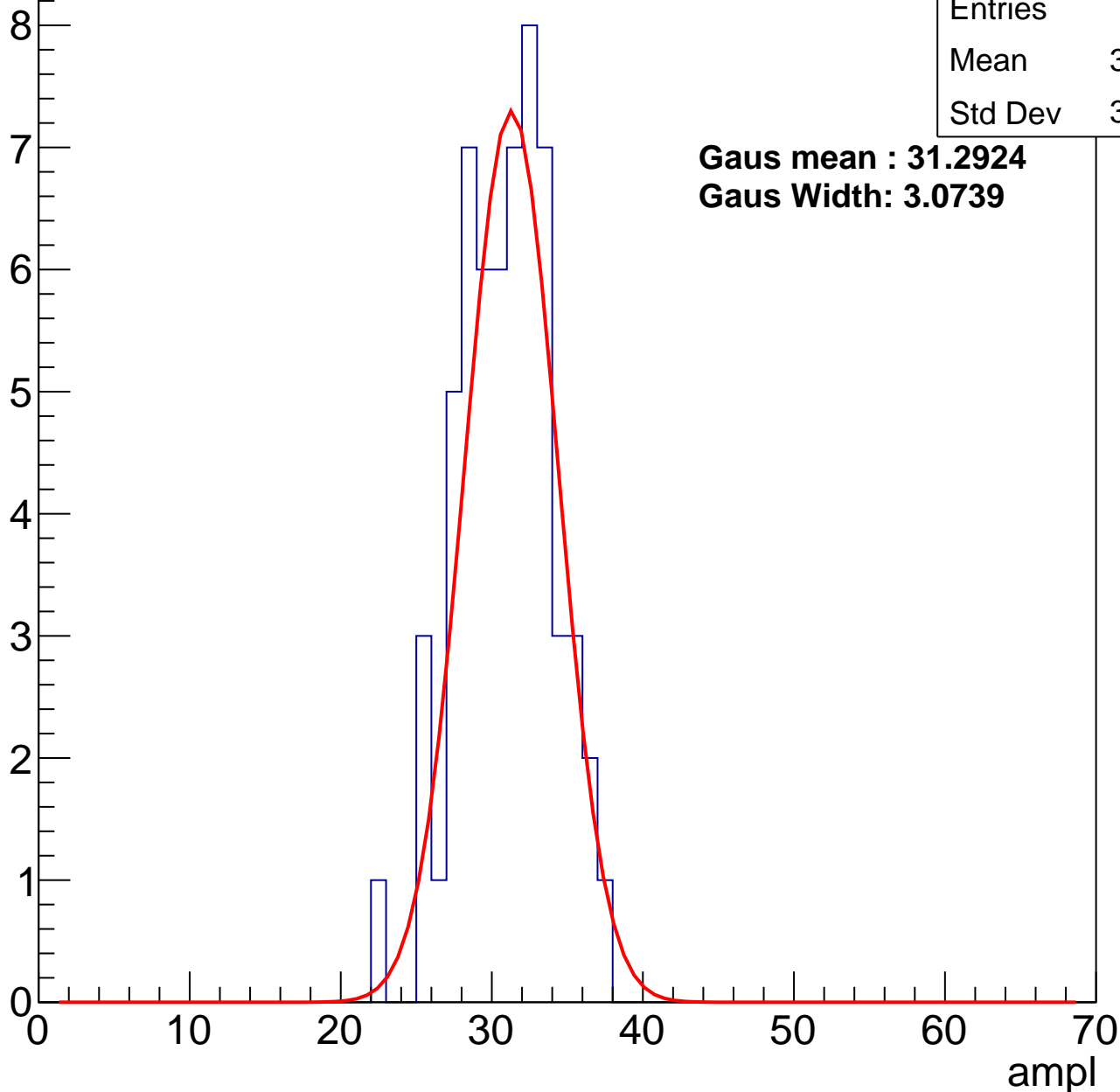
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.47
Std Dev	3.074

Gaus mean : 31.2924

Gaus Width: 3.0739



B0L000S, U2-ch52, adc1

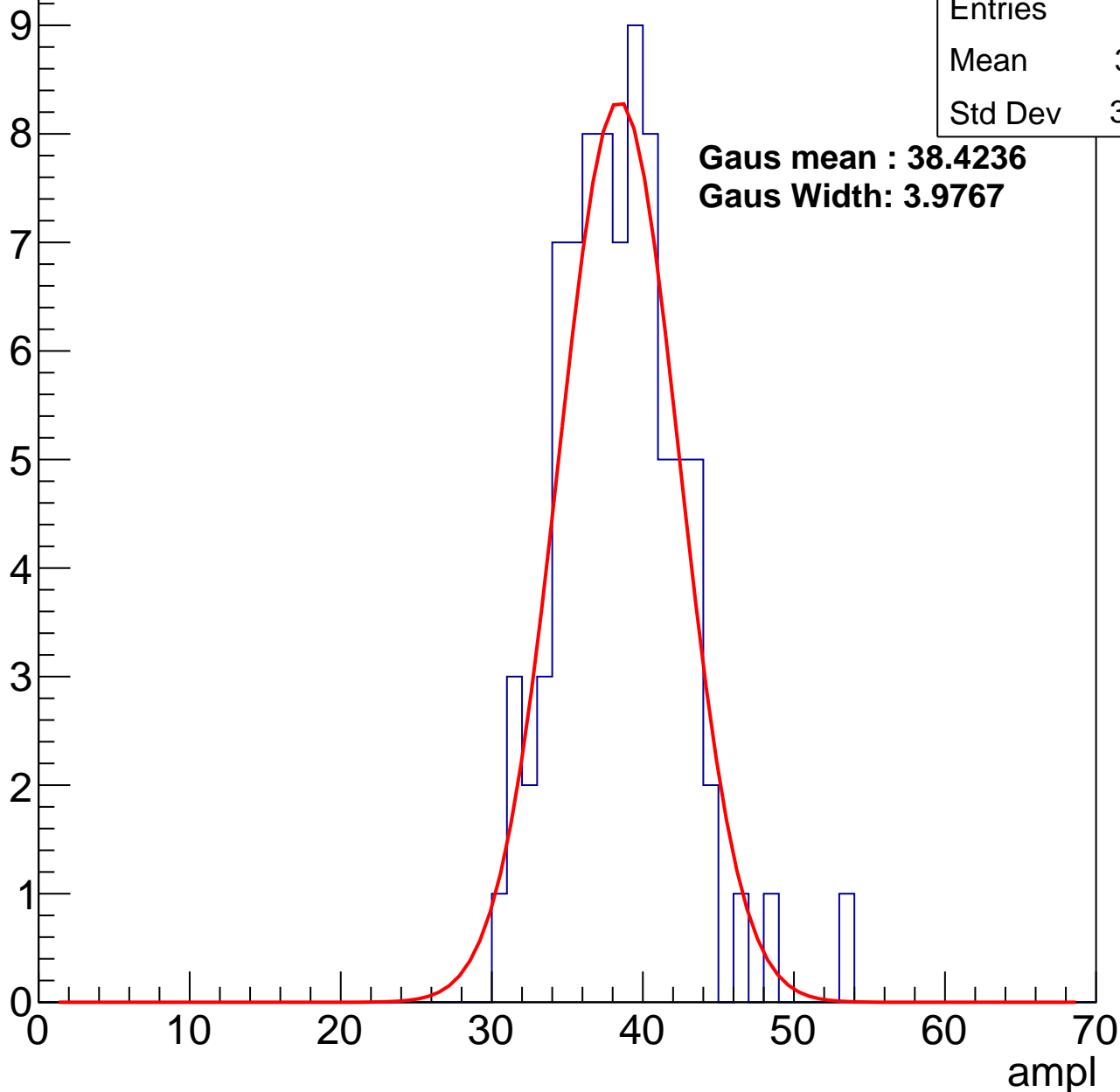
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	83
Mean	38.01
Std Dev	3.995

Gaus mean : 38.4236

Gaus Width: 3.9767

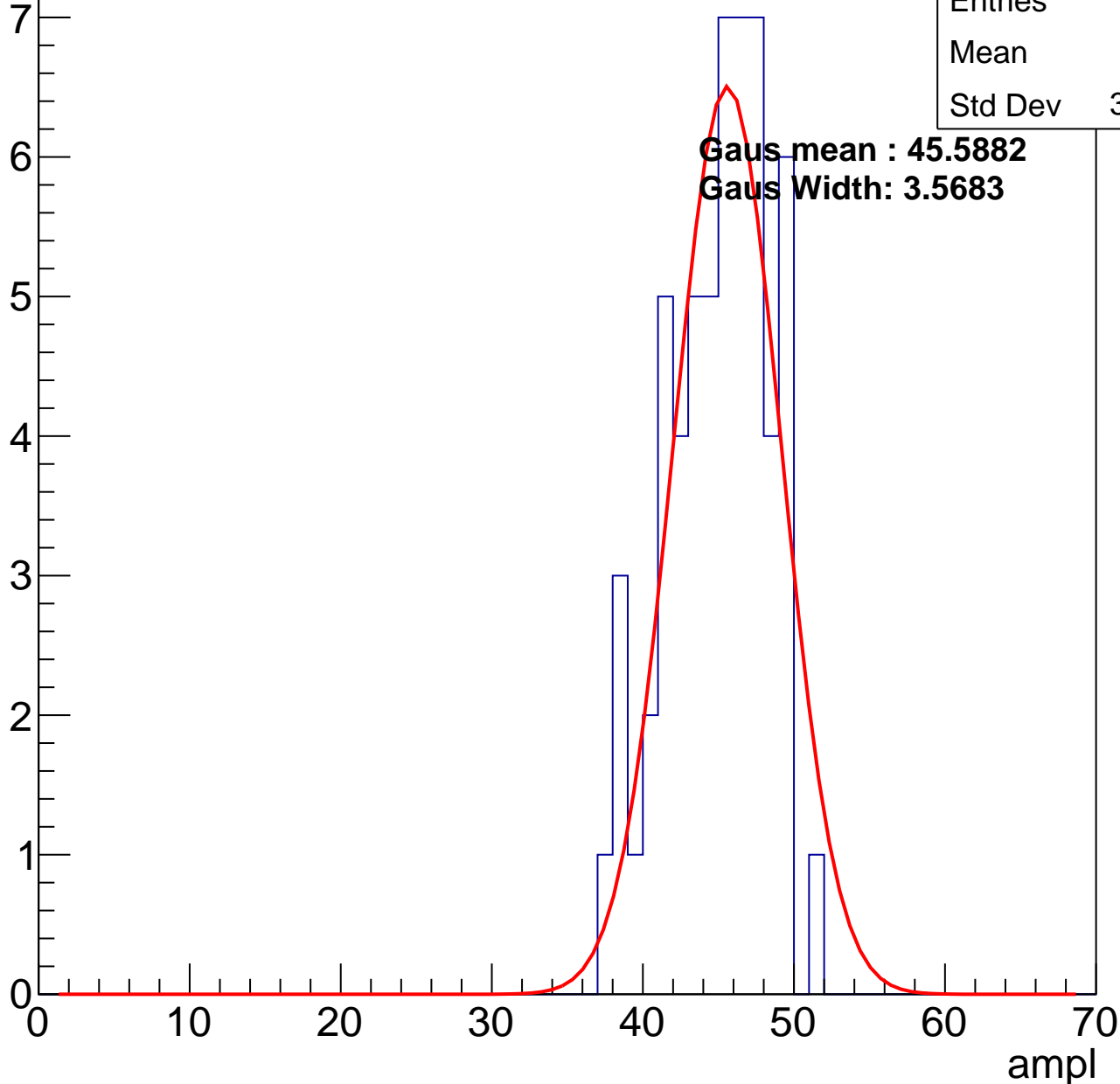


B0L000S, U2-ch52, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.5
Std Dev	3.287

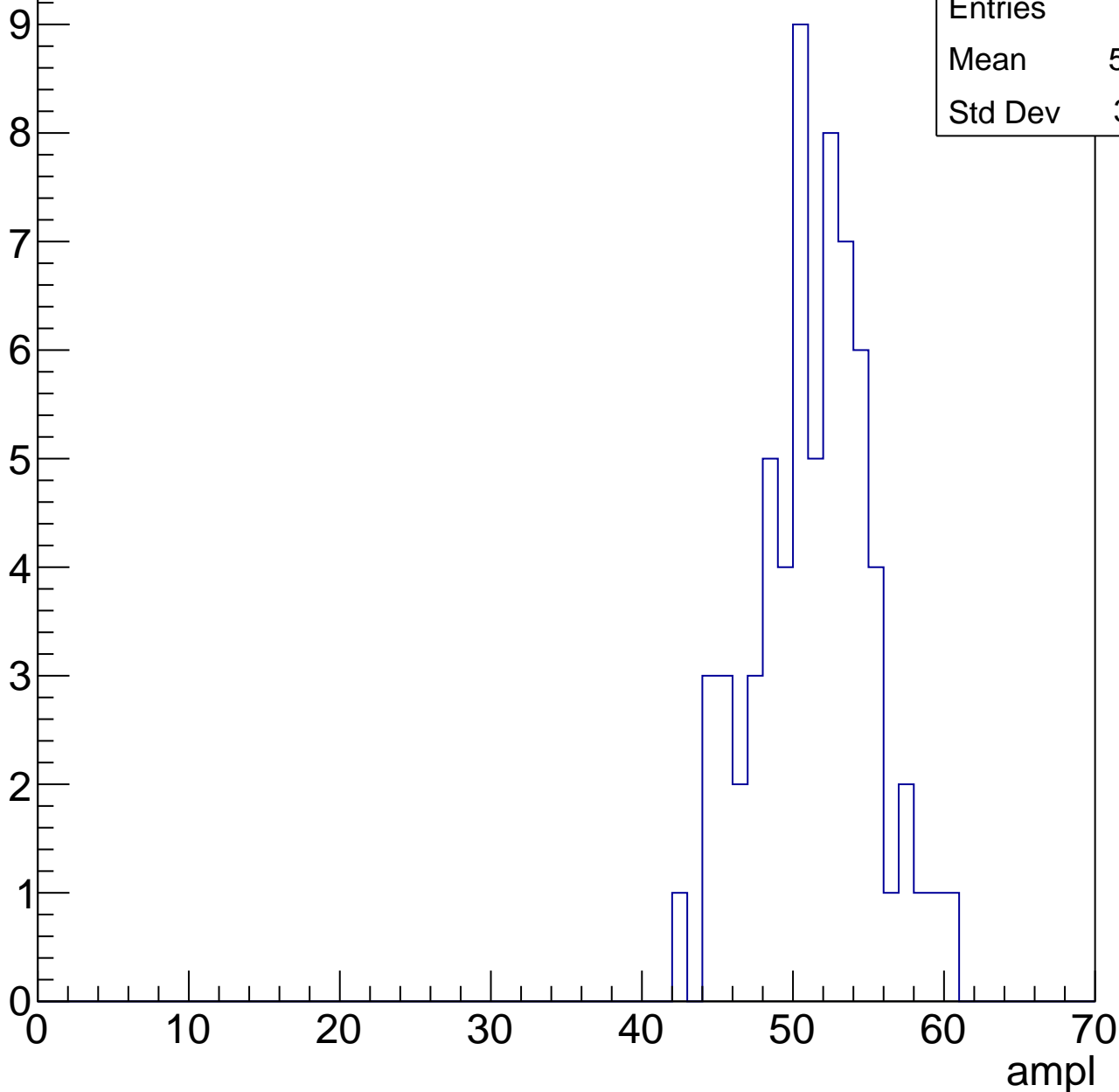


B0L000S, U2-ch52, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.92
Std Dev	3.791

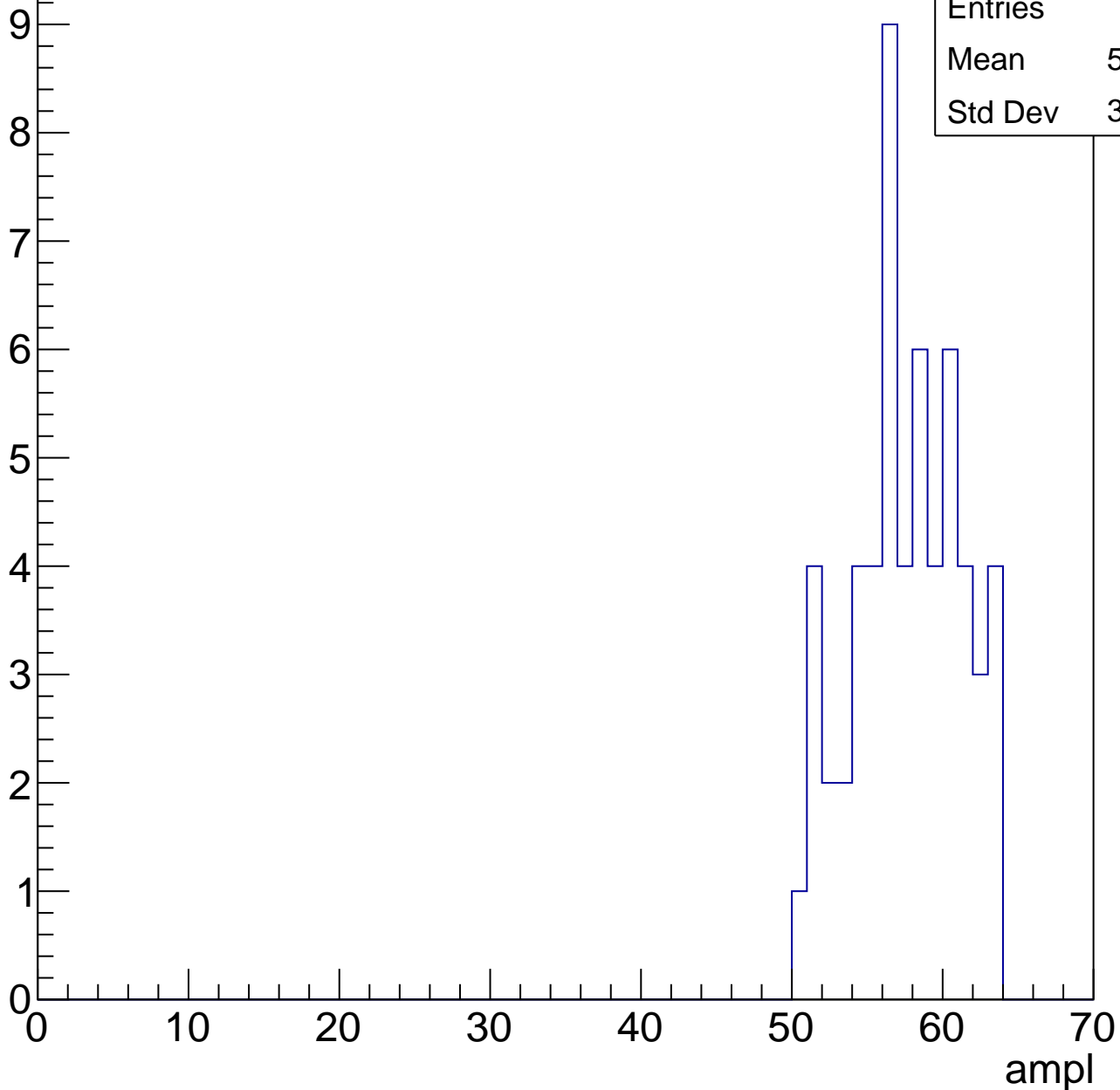


B0L000S, U2-ch52, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	57.16
Std Dev	3.488

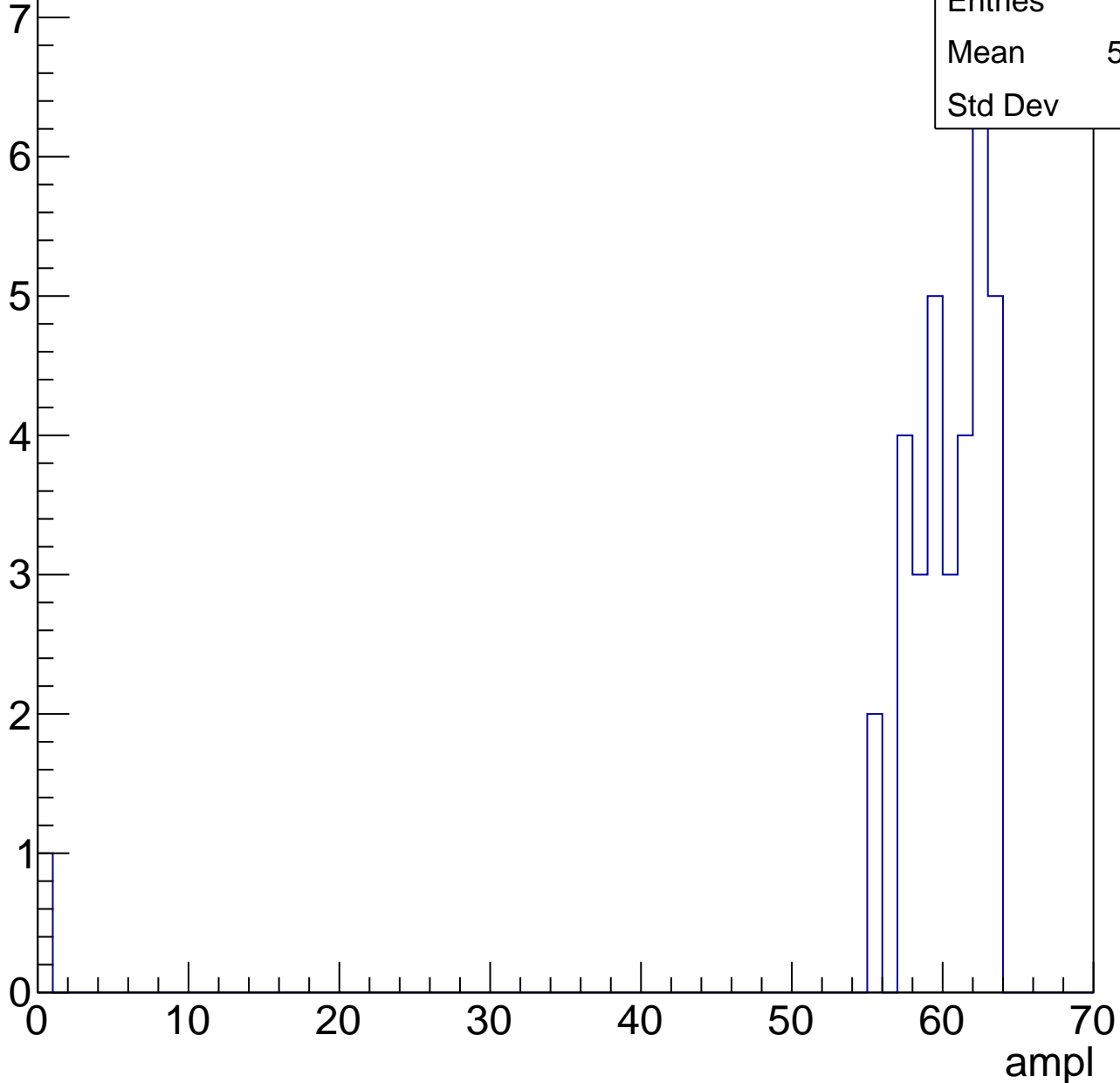


B0L000S, U2-ch52, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

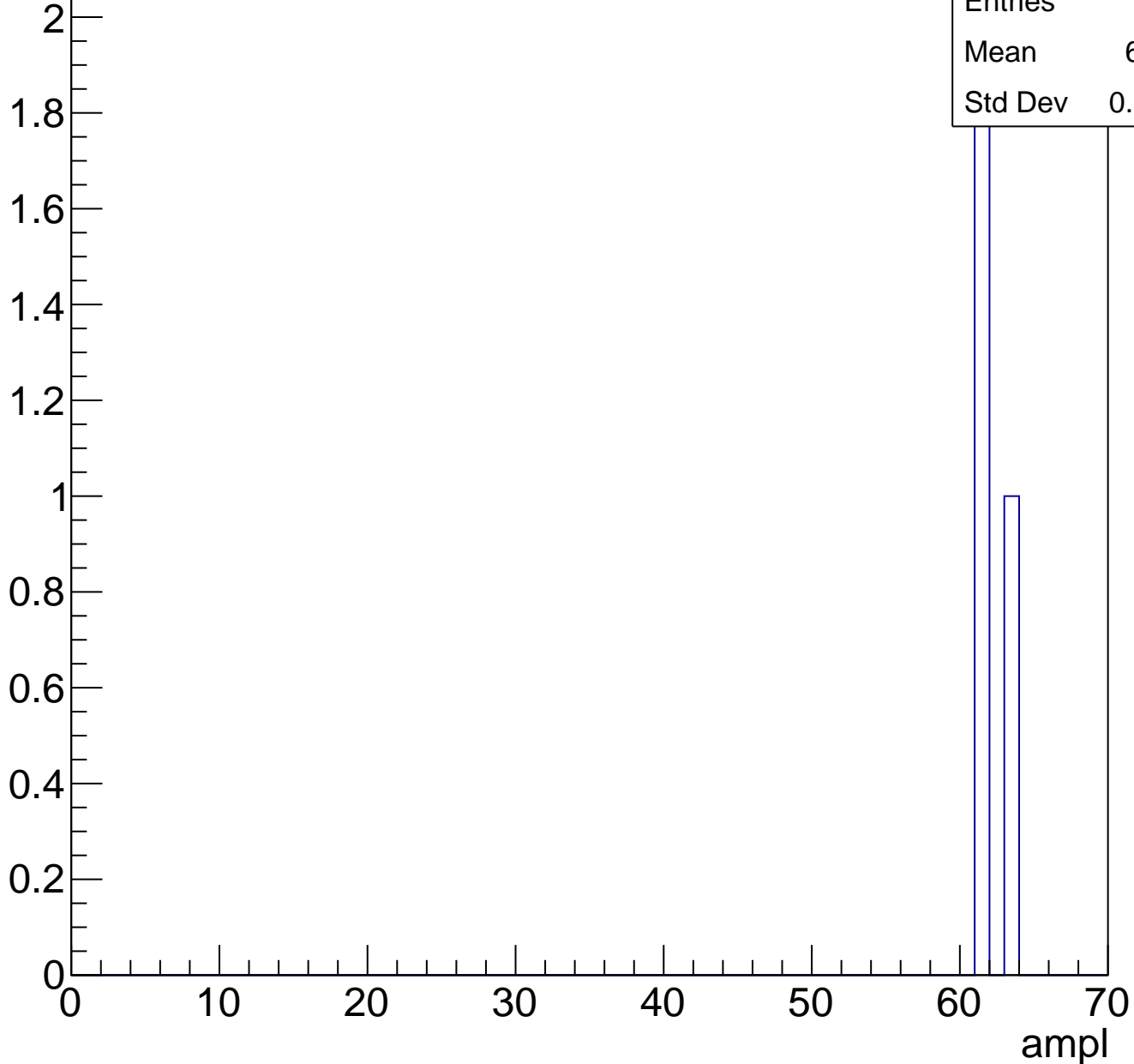
Entries	34
Mean	58.24
Std Dev	10.4



B0L000S, U2-ch52, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch52, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch53, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	68
Mean	30.34
Std Dev	3.445

Gaus mean : 30.5142

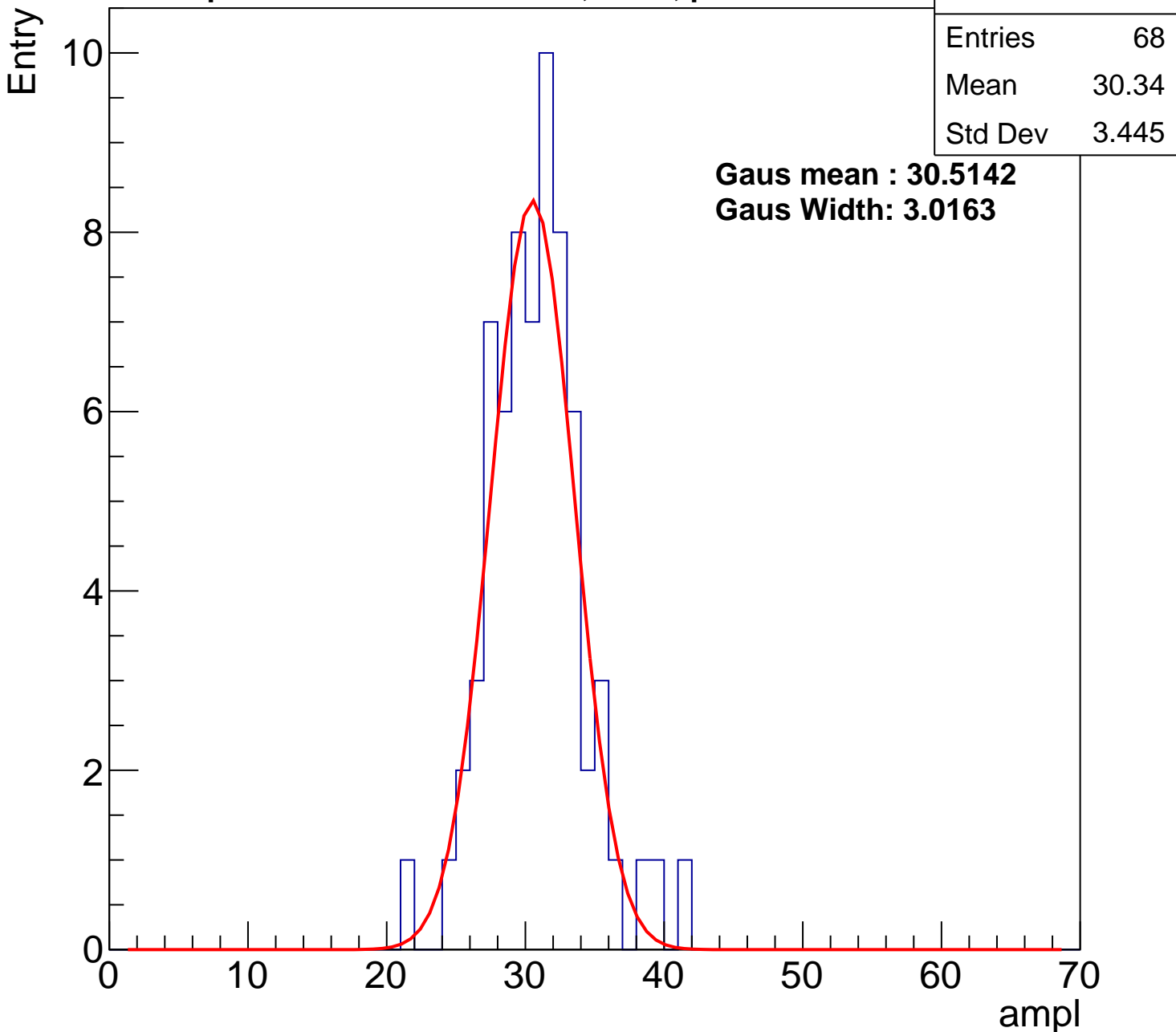
Gaus Width: 3.0163

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch53, adc1

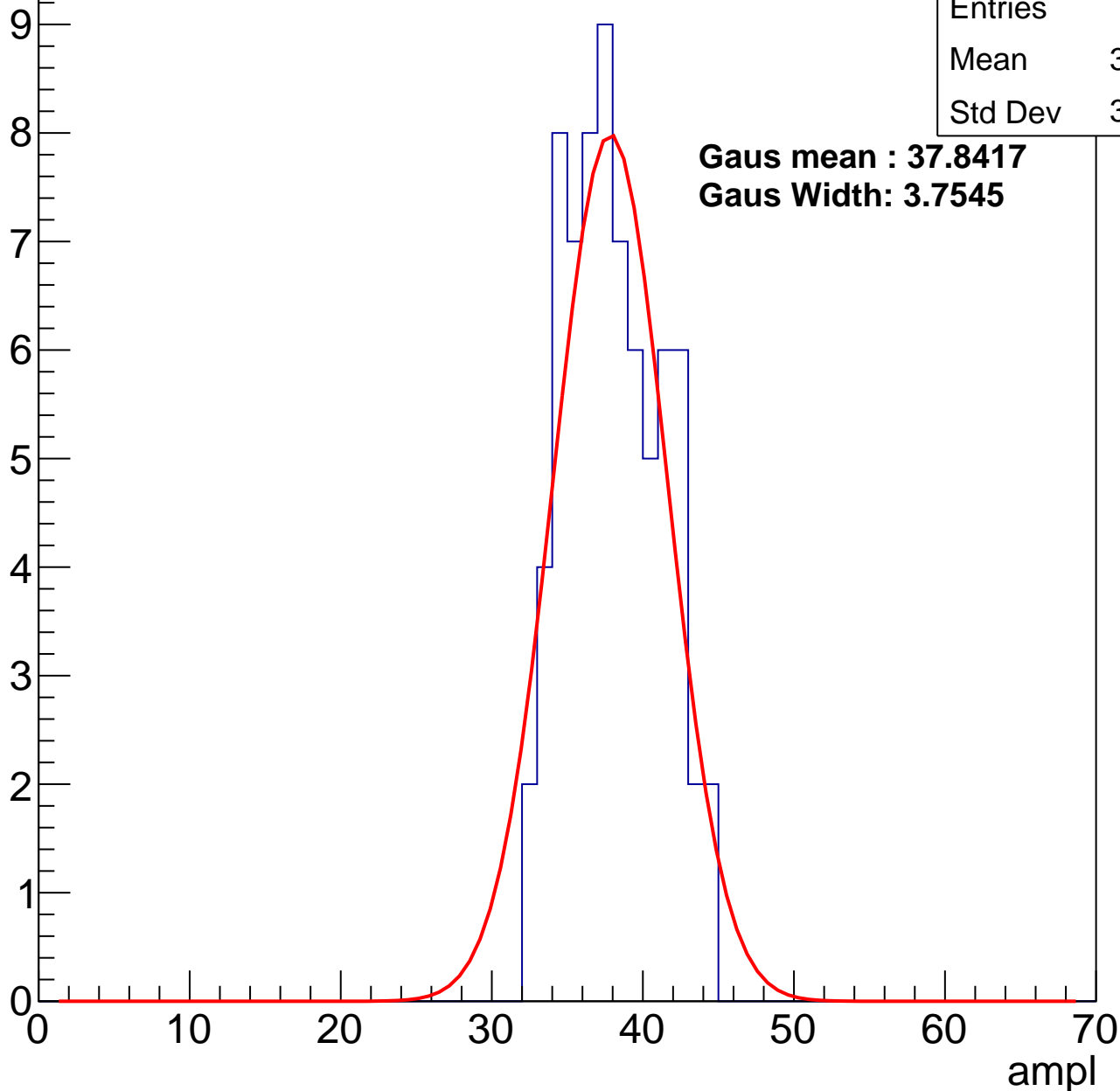
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	37.58
Std Dev	3.095

Gaus mean : 37.8417

Gaus Width: 3.7545



B0L000S, U2-ch53, adc2

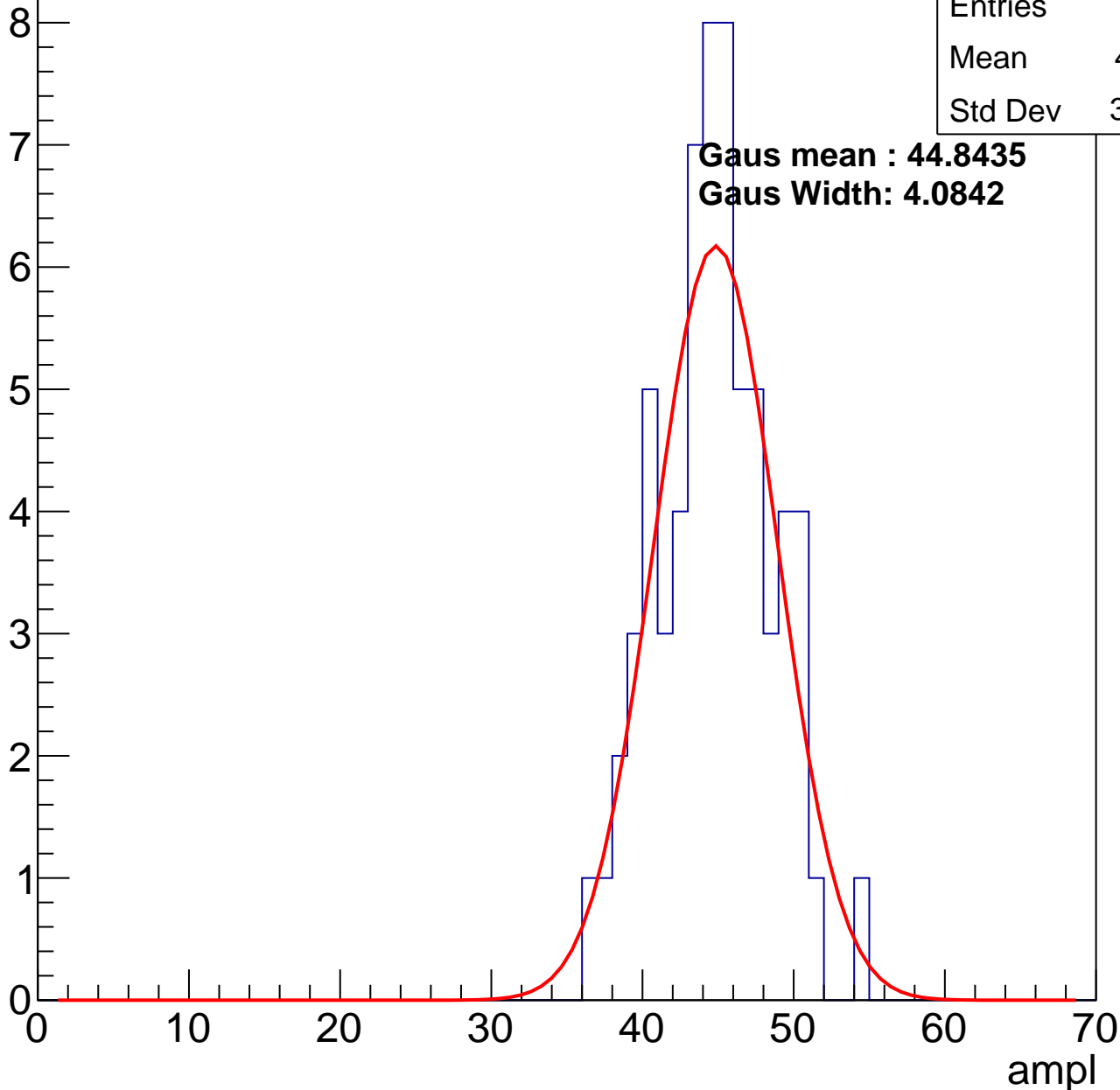
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.31
Std Dev	3.717

Gaus mean : 44.8435

Gaus Width: 4.0842

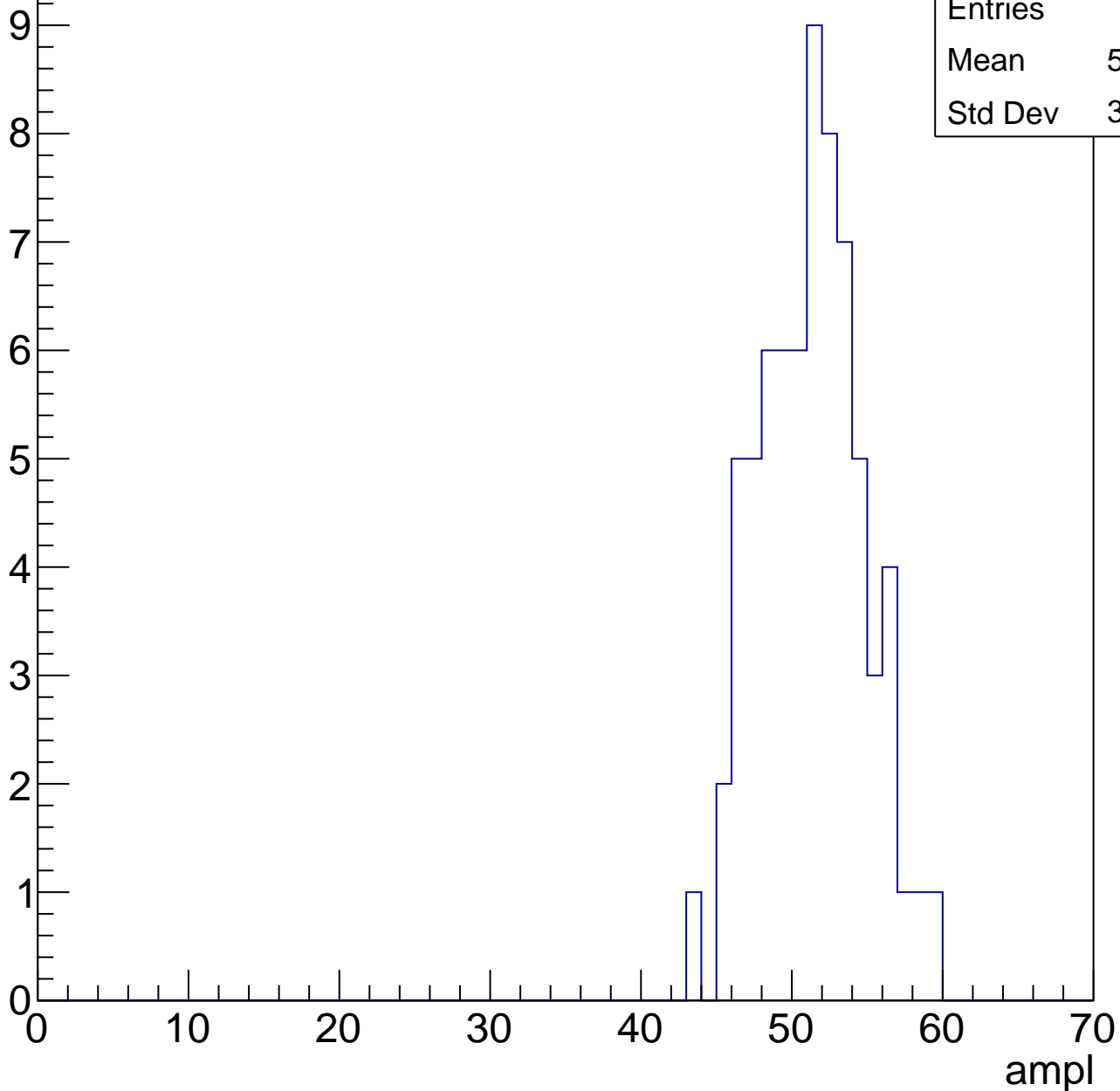


B0L000S, U2-ch53, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	50.84
Std Dev	3.383

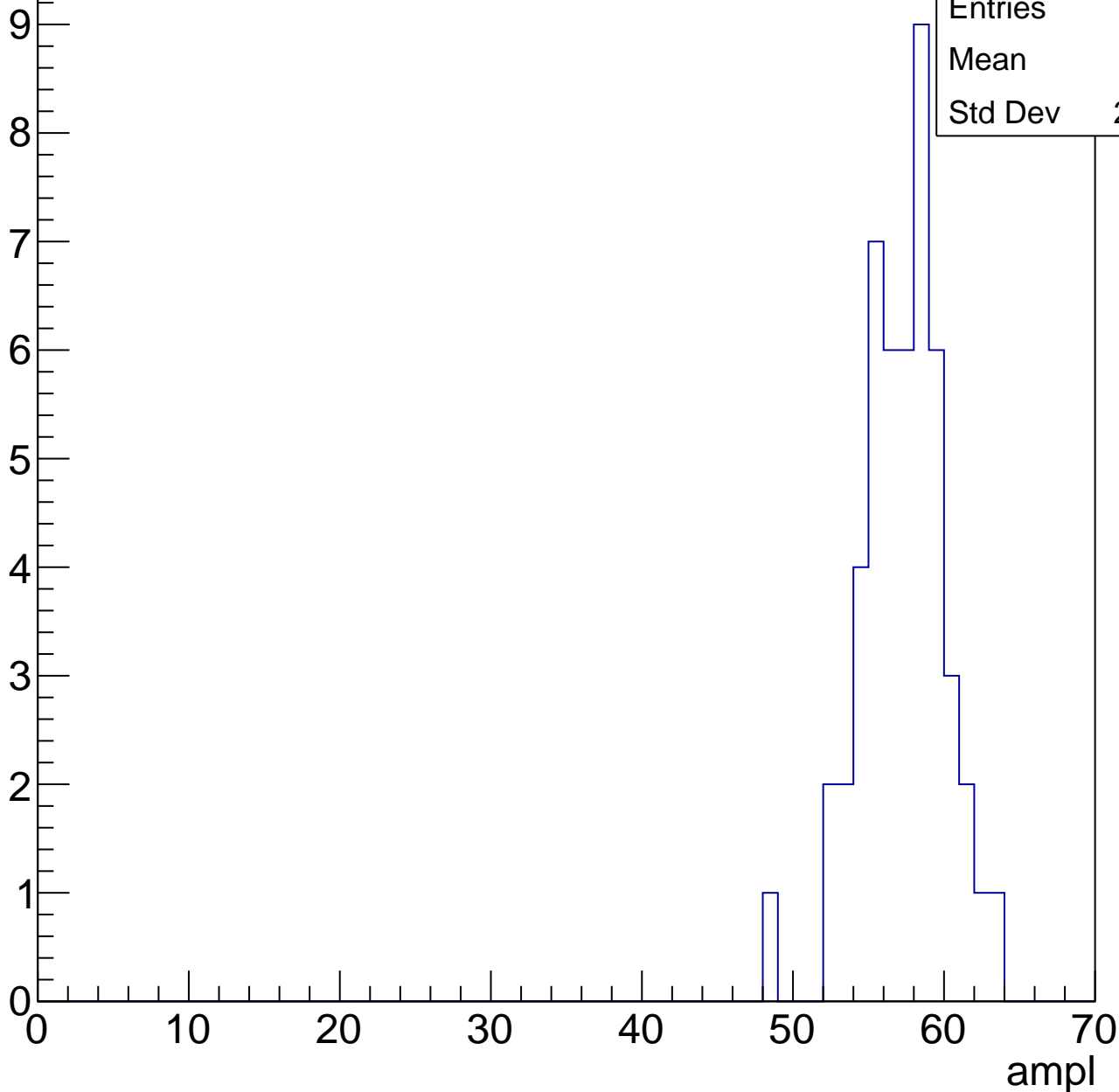


B0L000S, U2-ch53, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.8
Std Dev	2.771

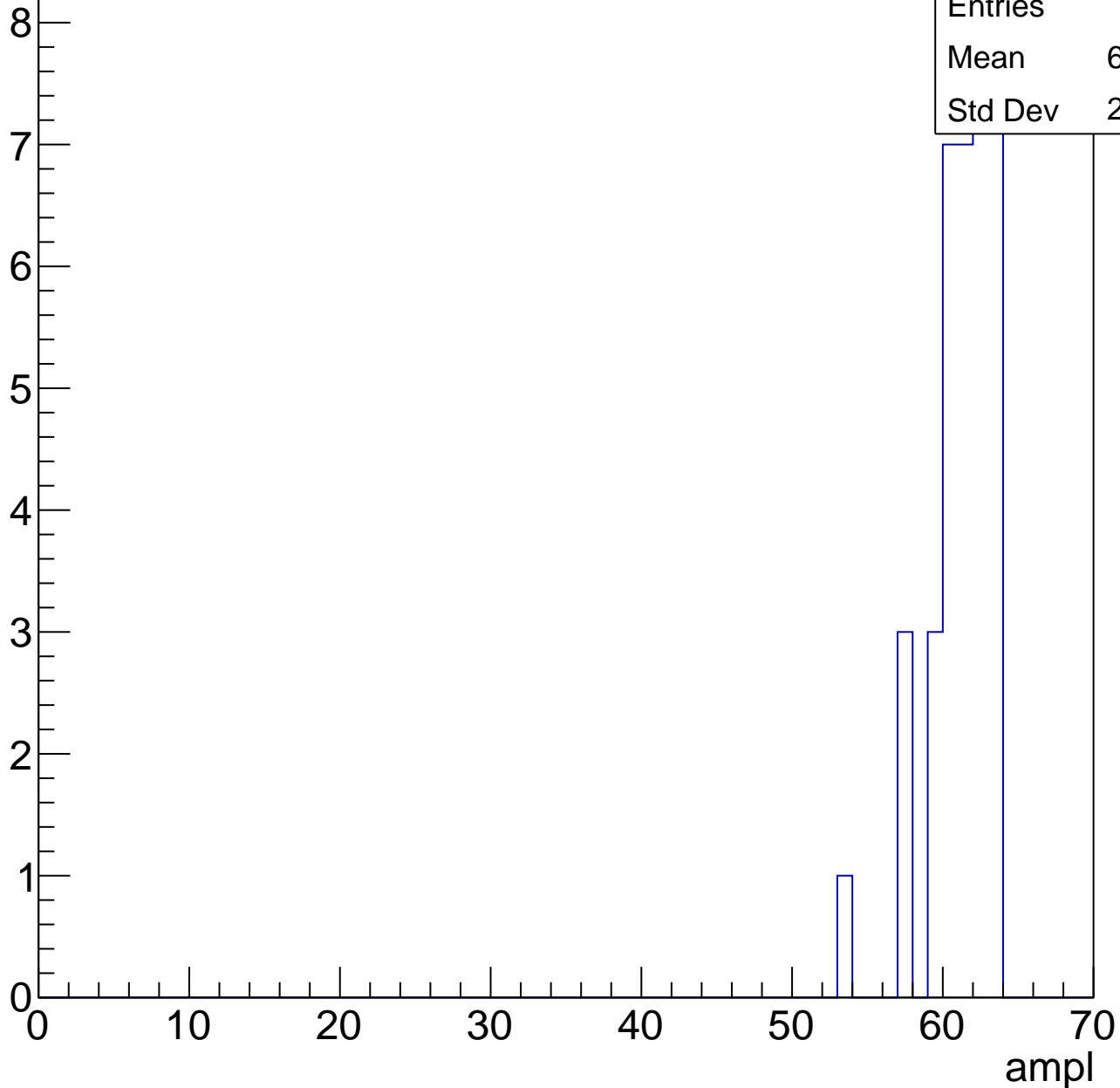


B0L000S, U2-ch53, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	37
Mean	60.76
Std Dev	2.136



B0L000S, U2-ch53, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch53, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch54, adc0

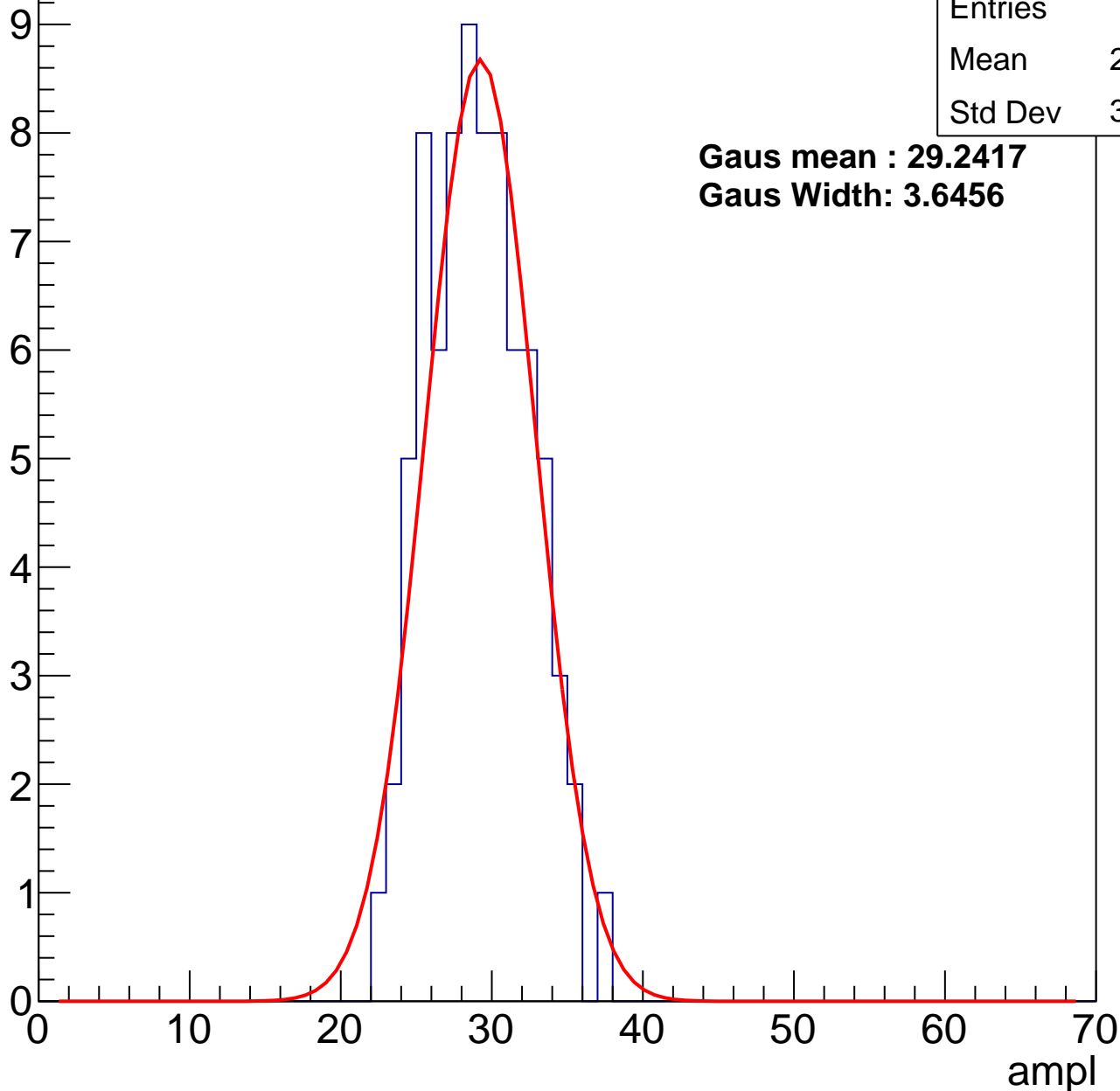
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.67
Std Dev	3.276

Gaus mean : 29.2417

Gaus Width: 3.6456



B0L000S, U2-ch54, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	78
Mean	36.03
Std Dev	3.655

Gaus mean : 36.6241

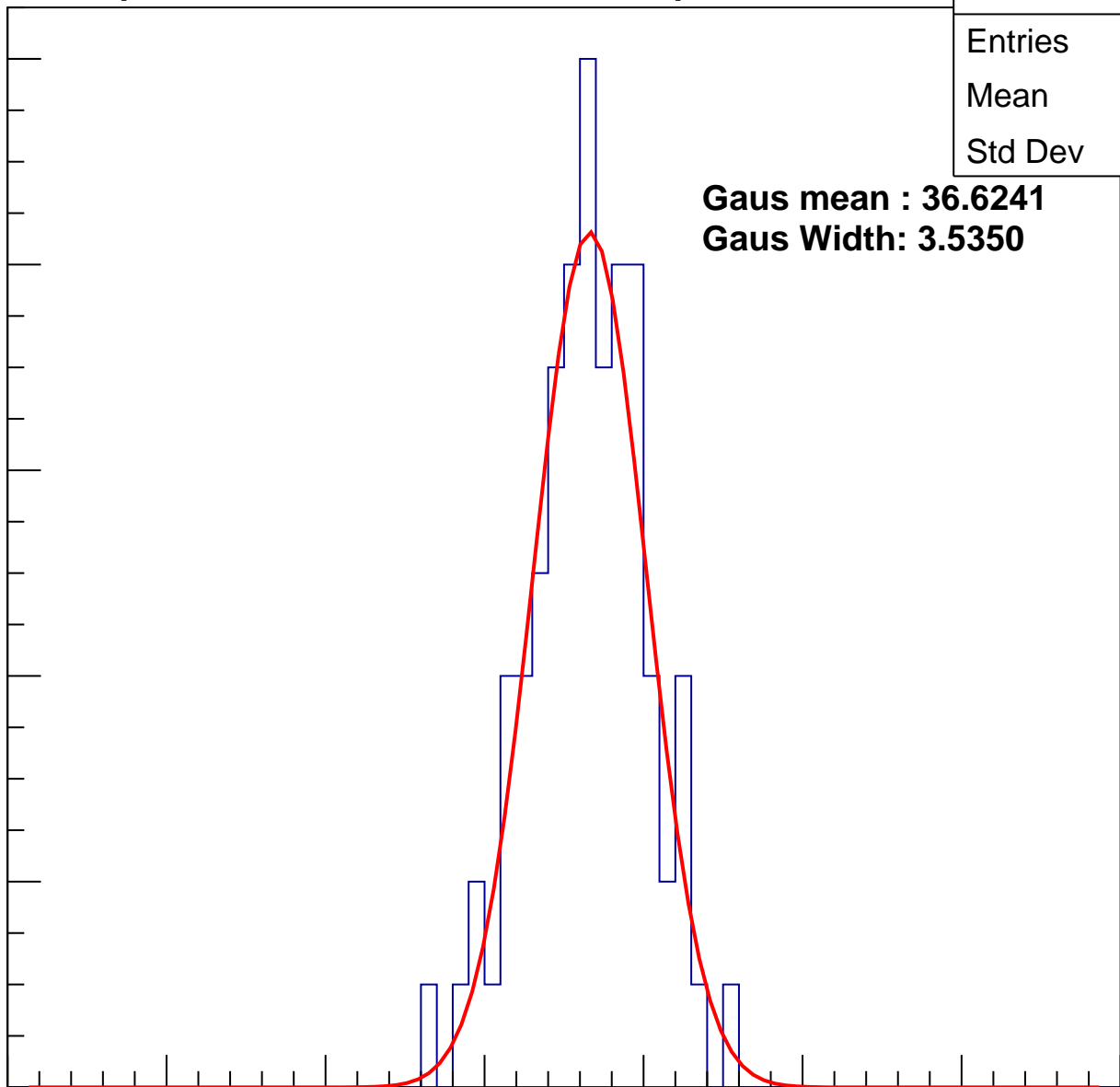
Gaus Width: 3.5350

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch54, adc2

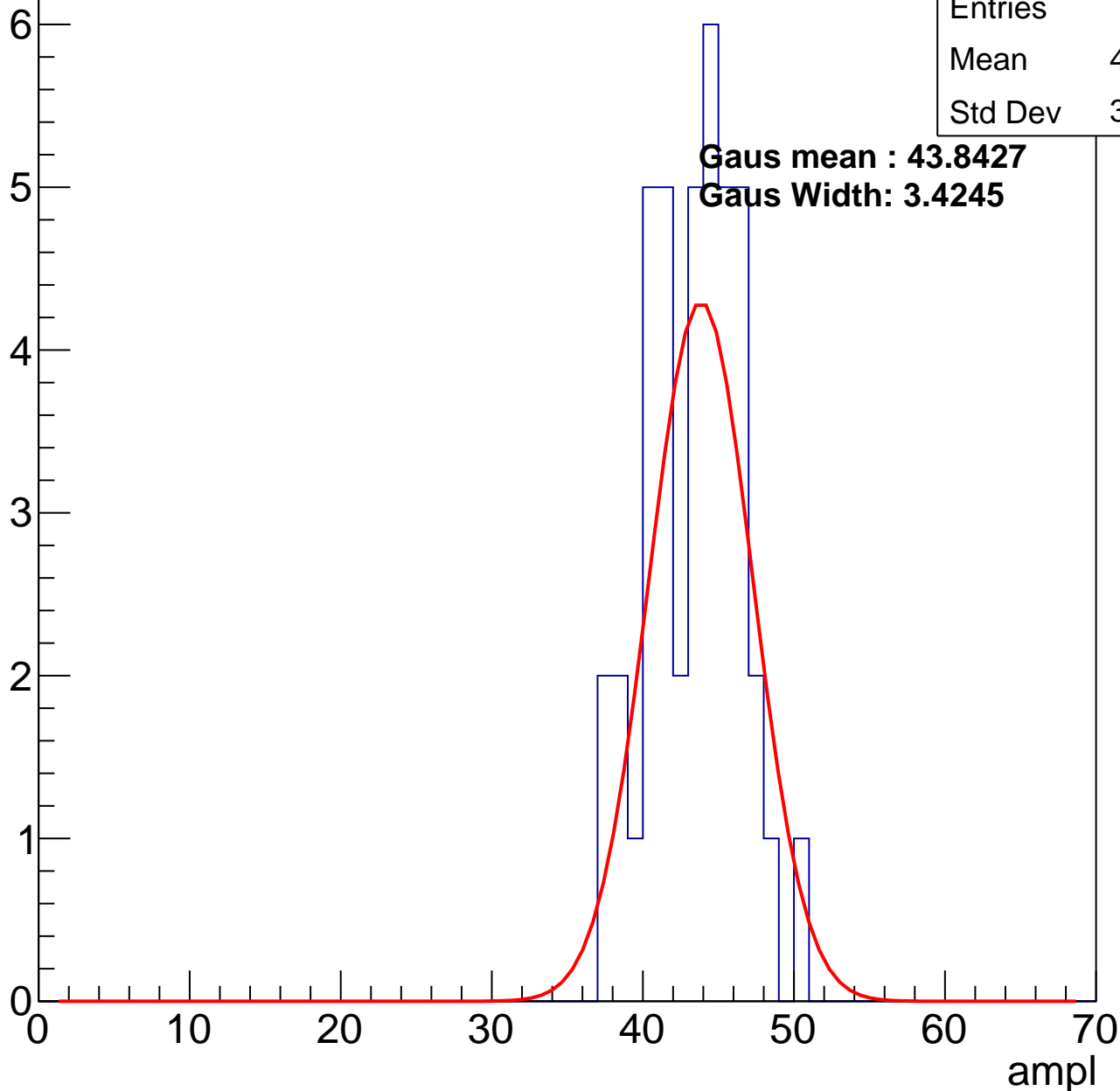
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	42
Mean	42.95
Std Dev	3.015

Gaus mean : 43.8427

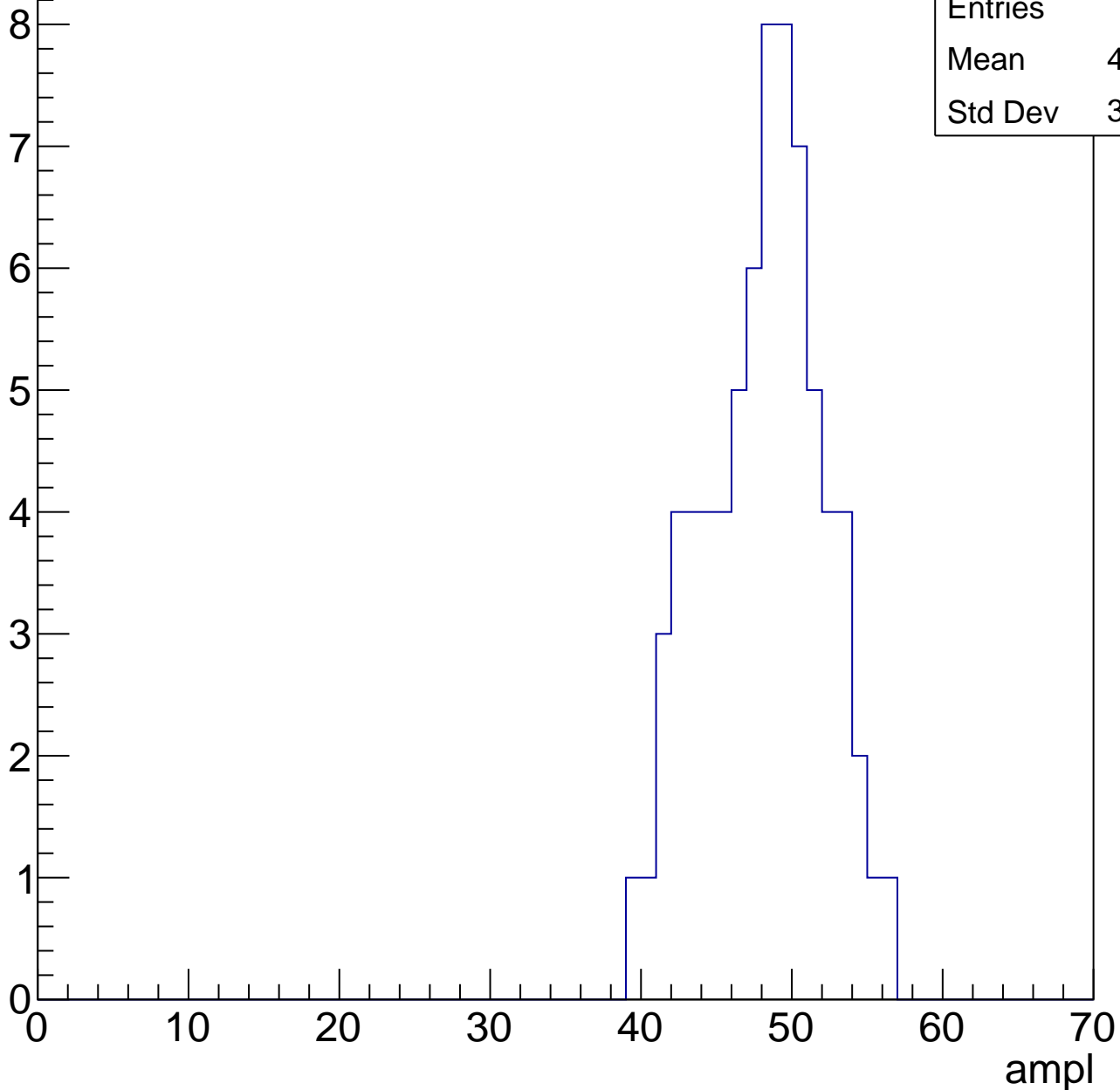
Gaus Width: 3.4245



B0L000S, U2-ch54, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

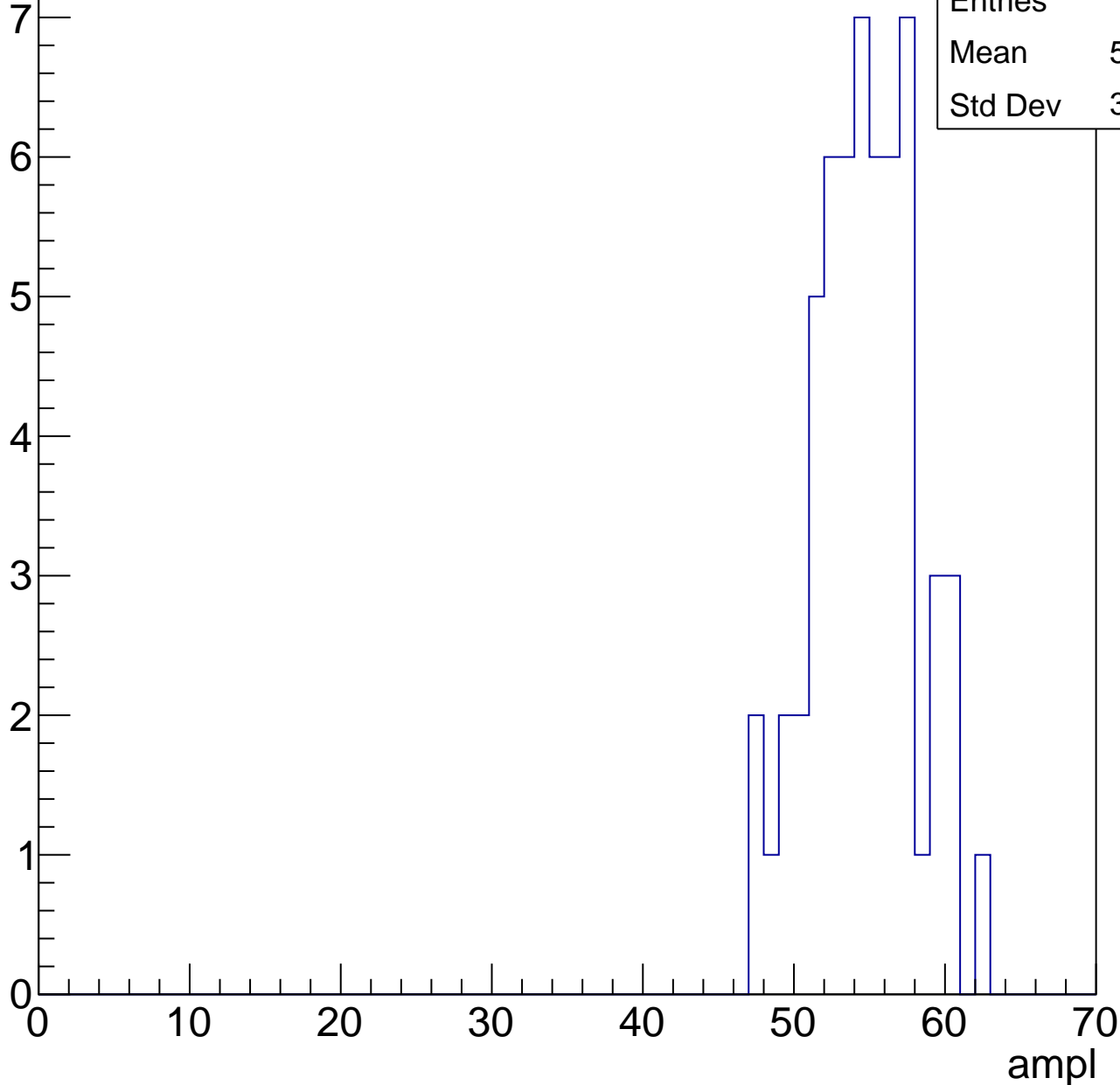


Entries	72
Mean	47.64
Std Dev	3.878

B0L000S, U2-ch54, adc4

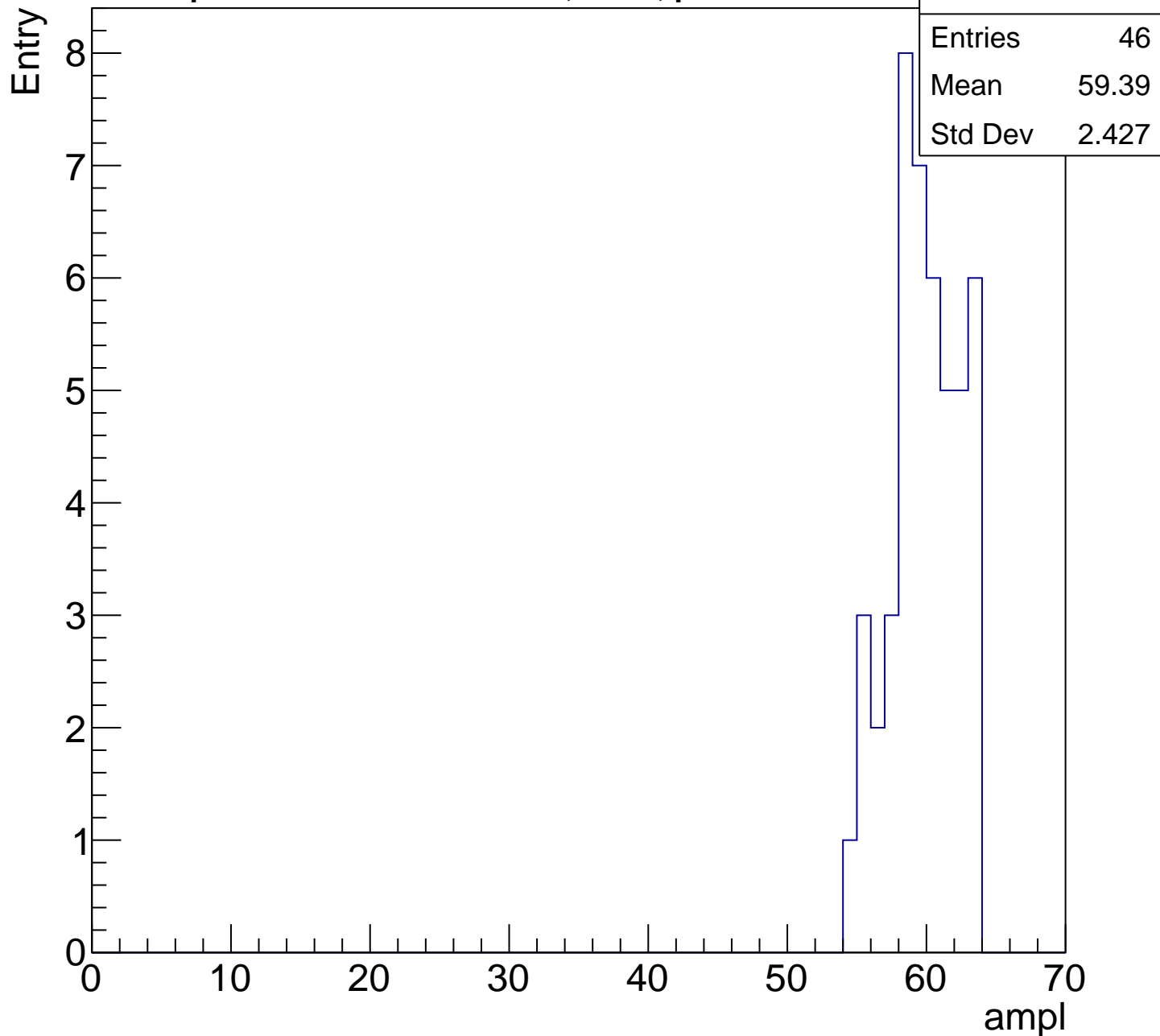
calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch54, adc5

calib_packv5_042523_0143.root, FC#5, port B1

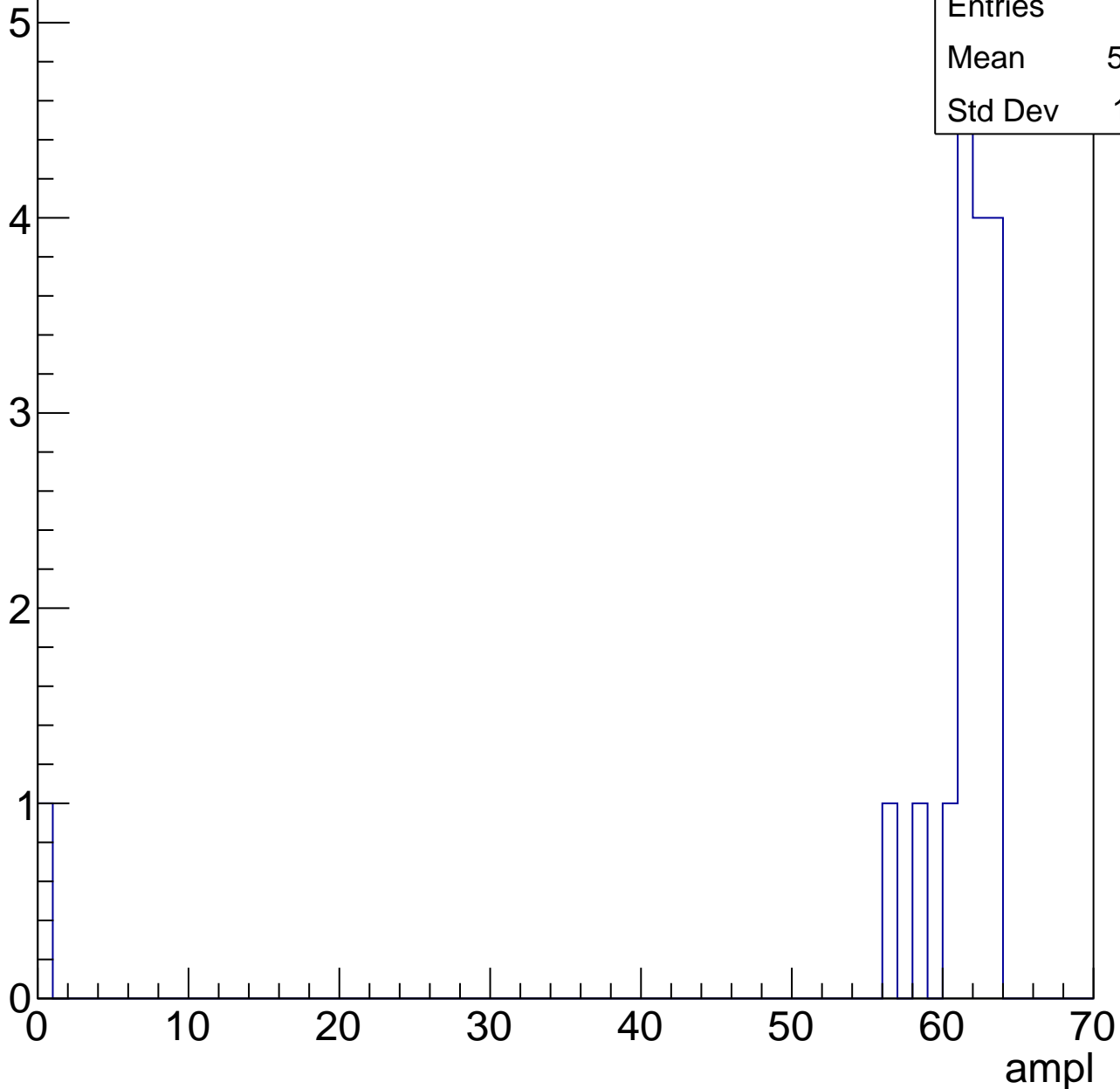


B0L000S, U2-ch54, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	17
Mean	57.59
Std Dev	14.51



B0L000S, U2-ch54, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch55, adc0

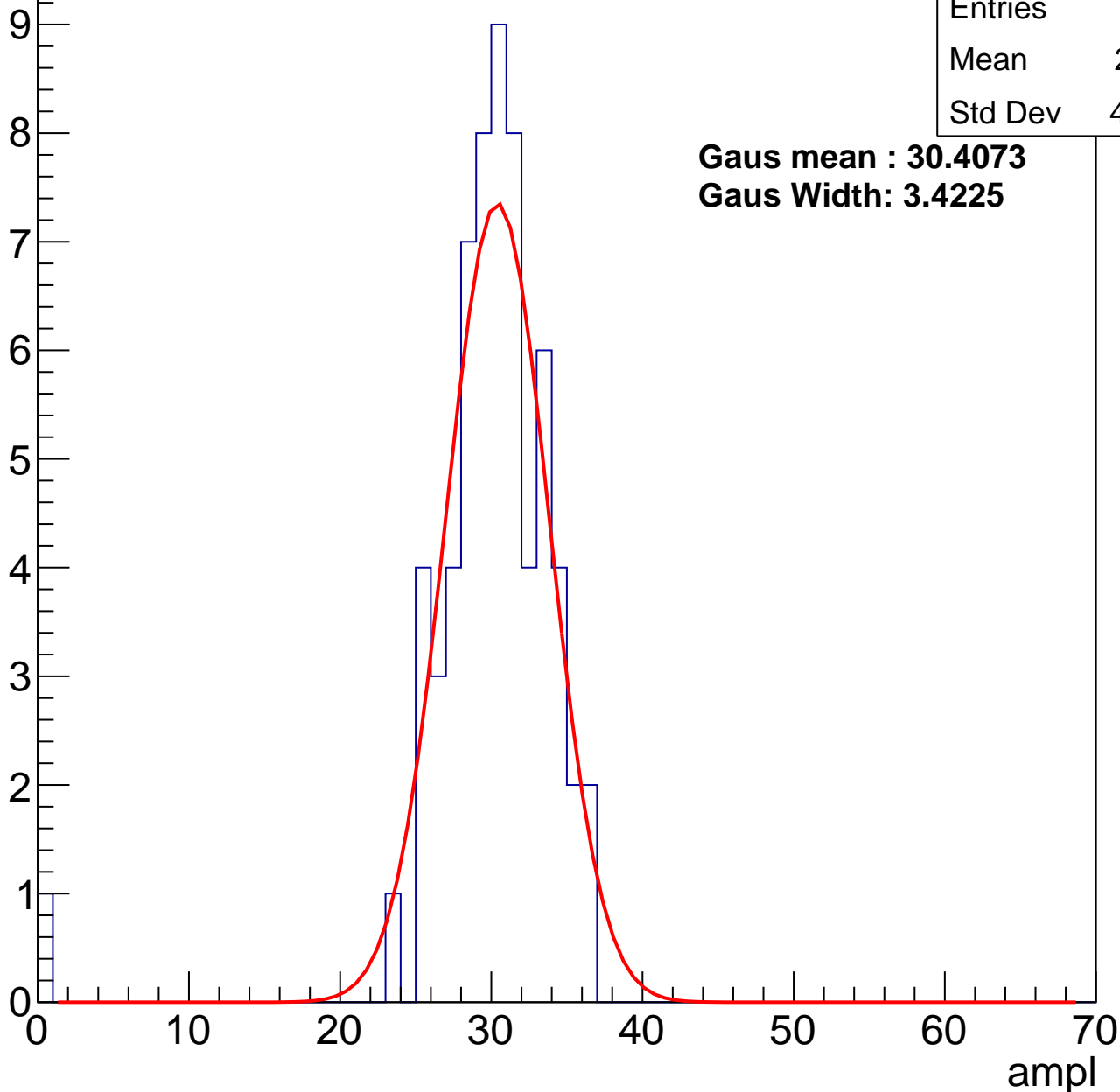
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.51
Std Dev	4.744

Gaus mean : 30.4073

Gaus Width: 3.4225



B0L000S, U2-ch55, adc1

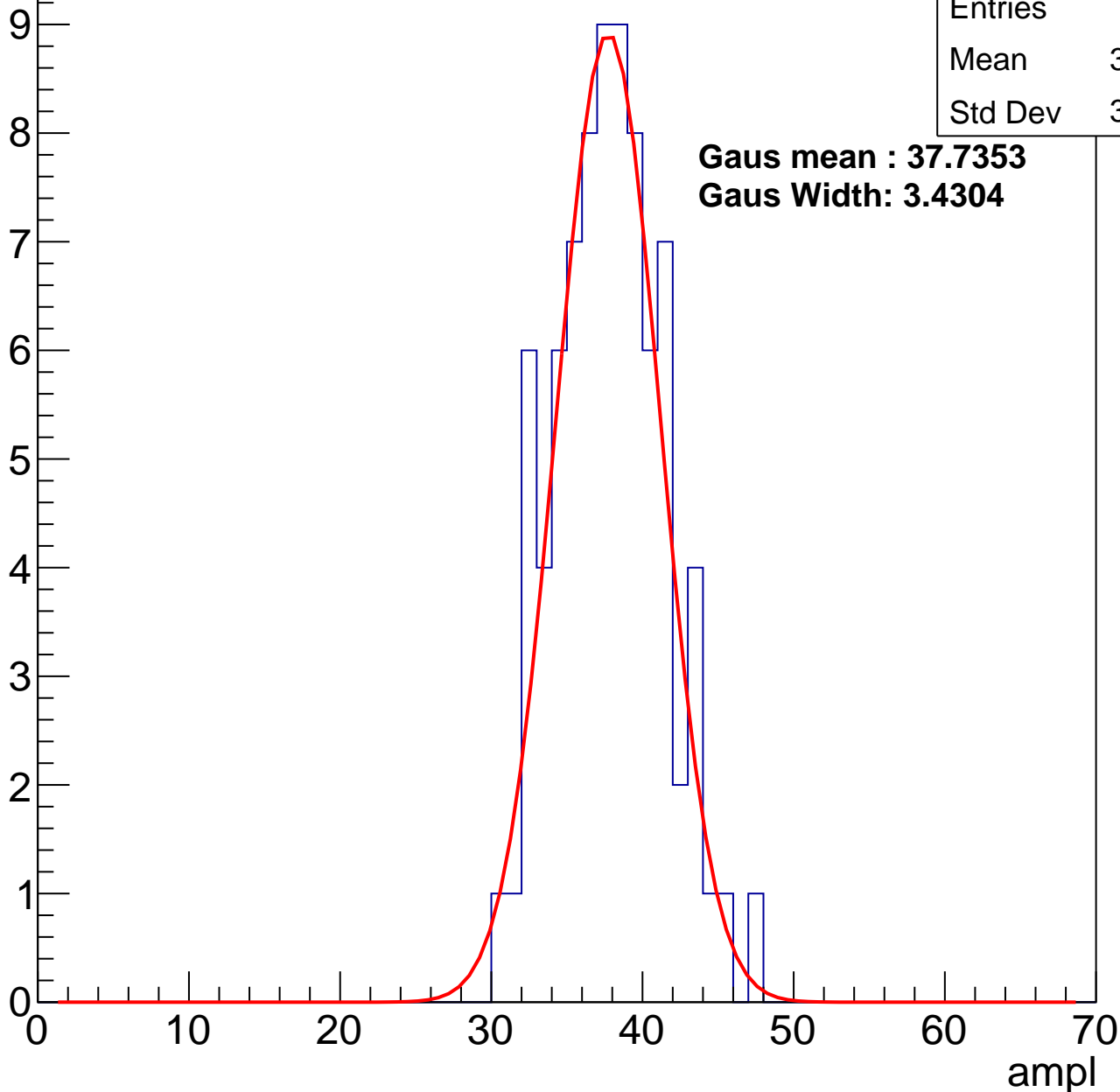
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	81
Mean	37.38
Std Dev	3.498

Gaus mean : 37.7353

Gaus Width: 3.4304



B0L000S, U2-ch55, adc2

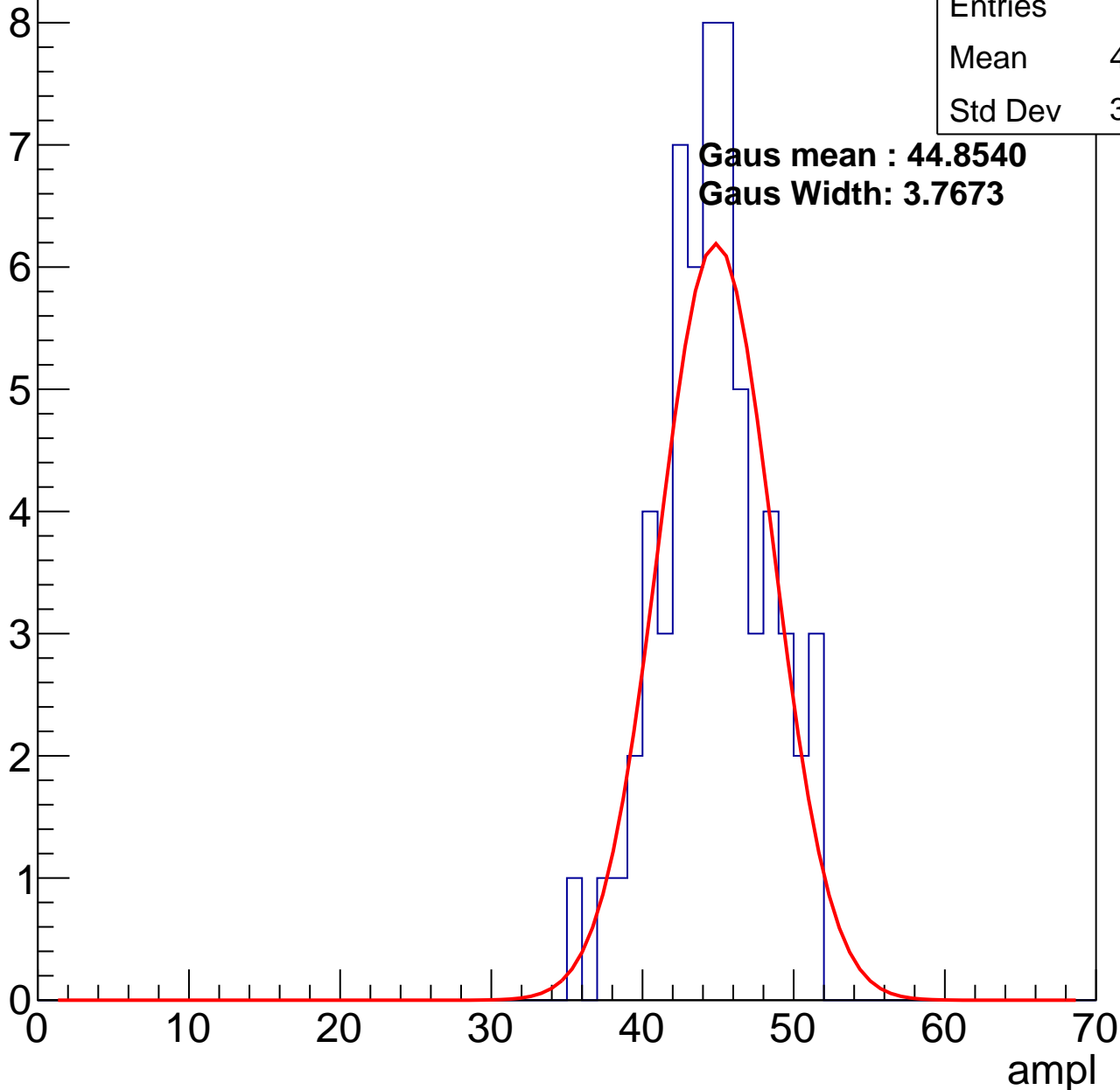
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.23
Std Dev	3.508

Gaus mean : 44.8540

Gaus Width: 3.7673

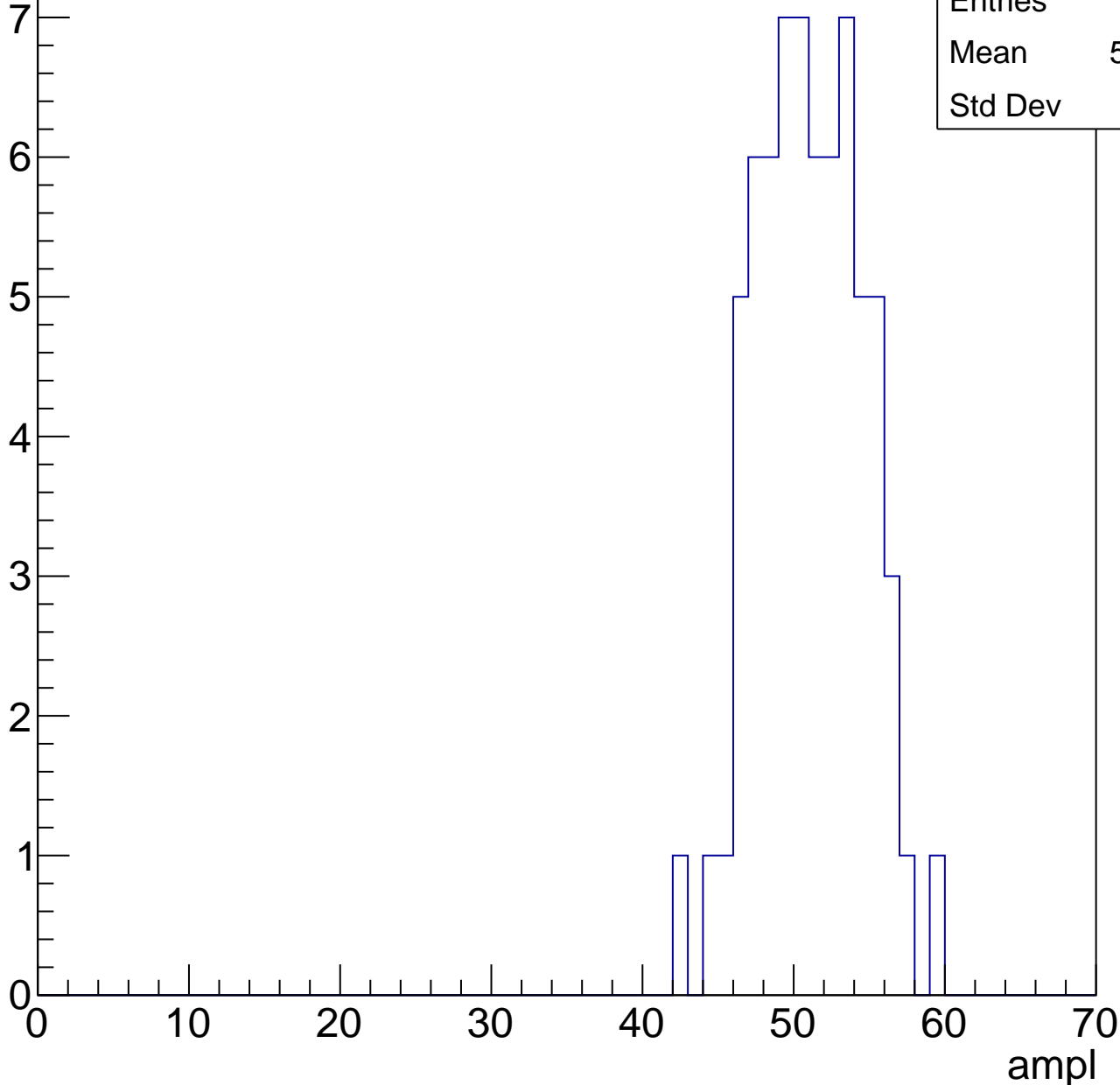


B0L000S, U2-ch55, adc3

calib_packv5_042523_0143.root, FC#5, port B1

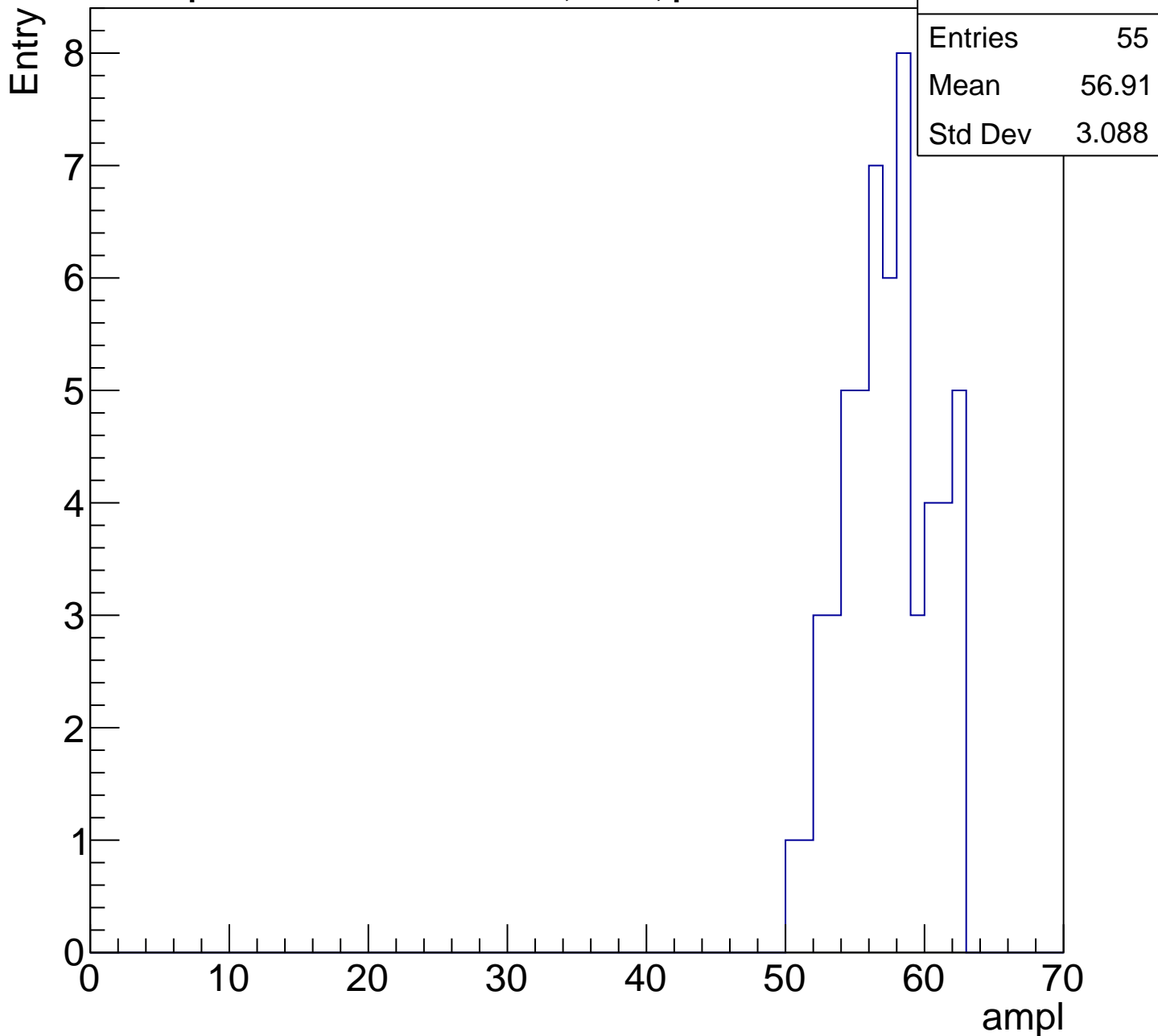
Entry

Entries	68
Mean	50.62
Std Dev	3.43



B0L000S, U2-ch55, adc4

calib_packv5_042523_0143.root, FC#5, port B1

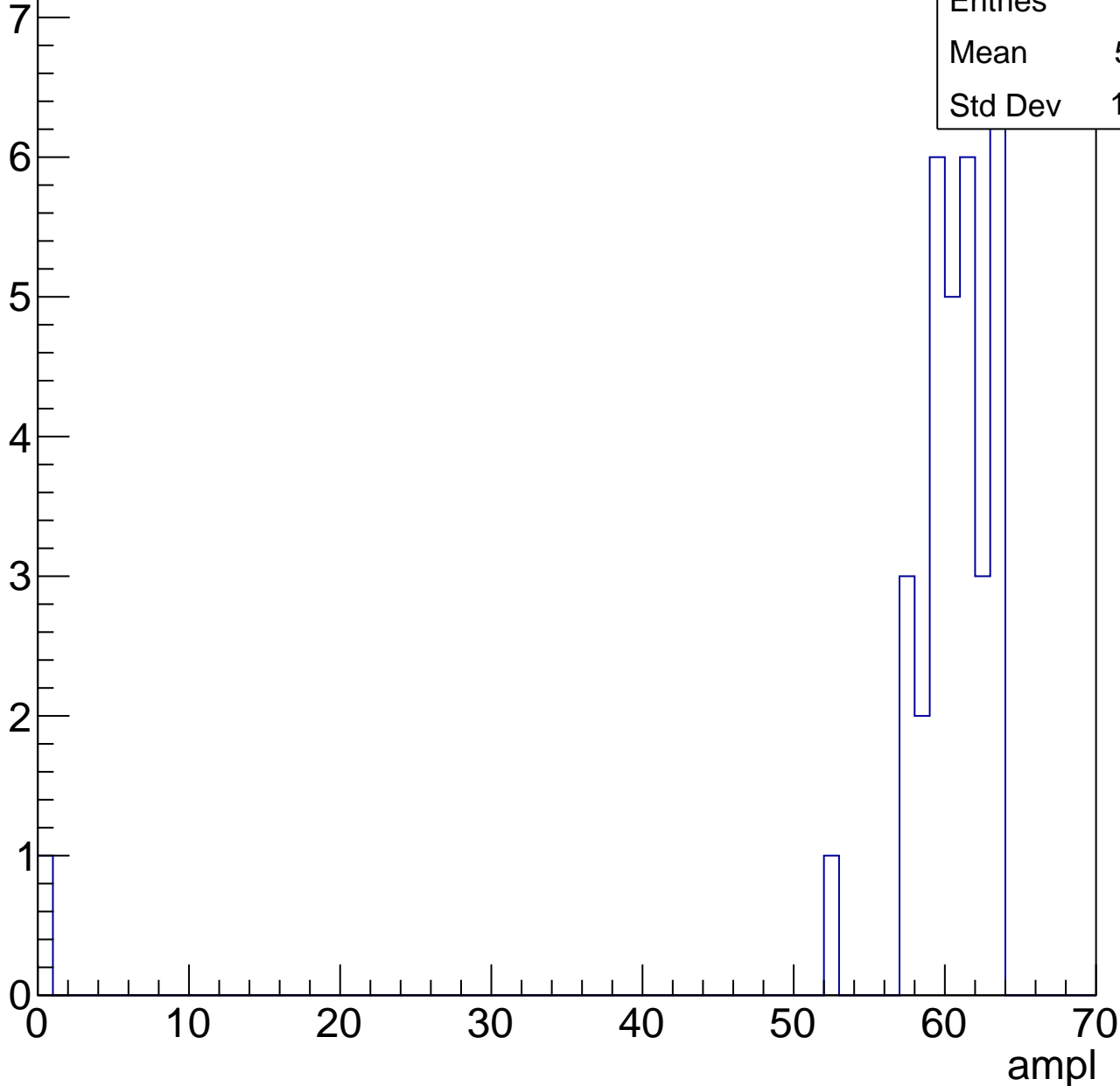


B0L000S, U2-ch55, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.41
Std Dev	10.43



B0L000S, U2-ch55, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch55, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch56, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	75
Mean	30.37
Std Dev	3.662

Gaus mean : 30.8041

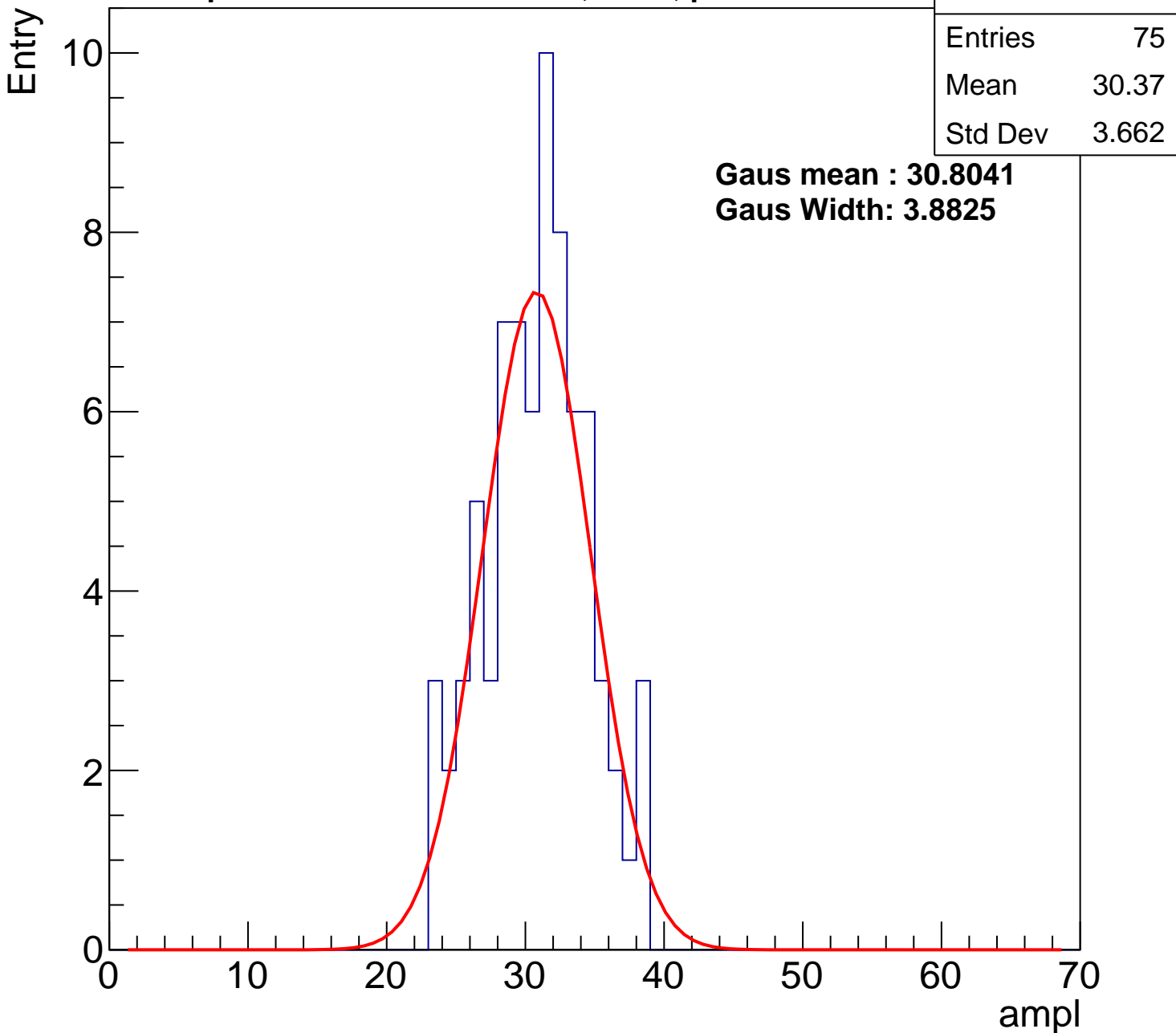
Gaus Width: 3.8825

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch56, adc1

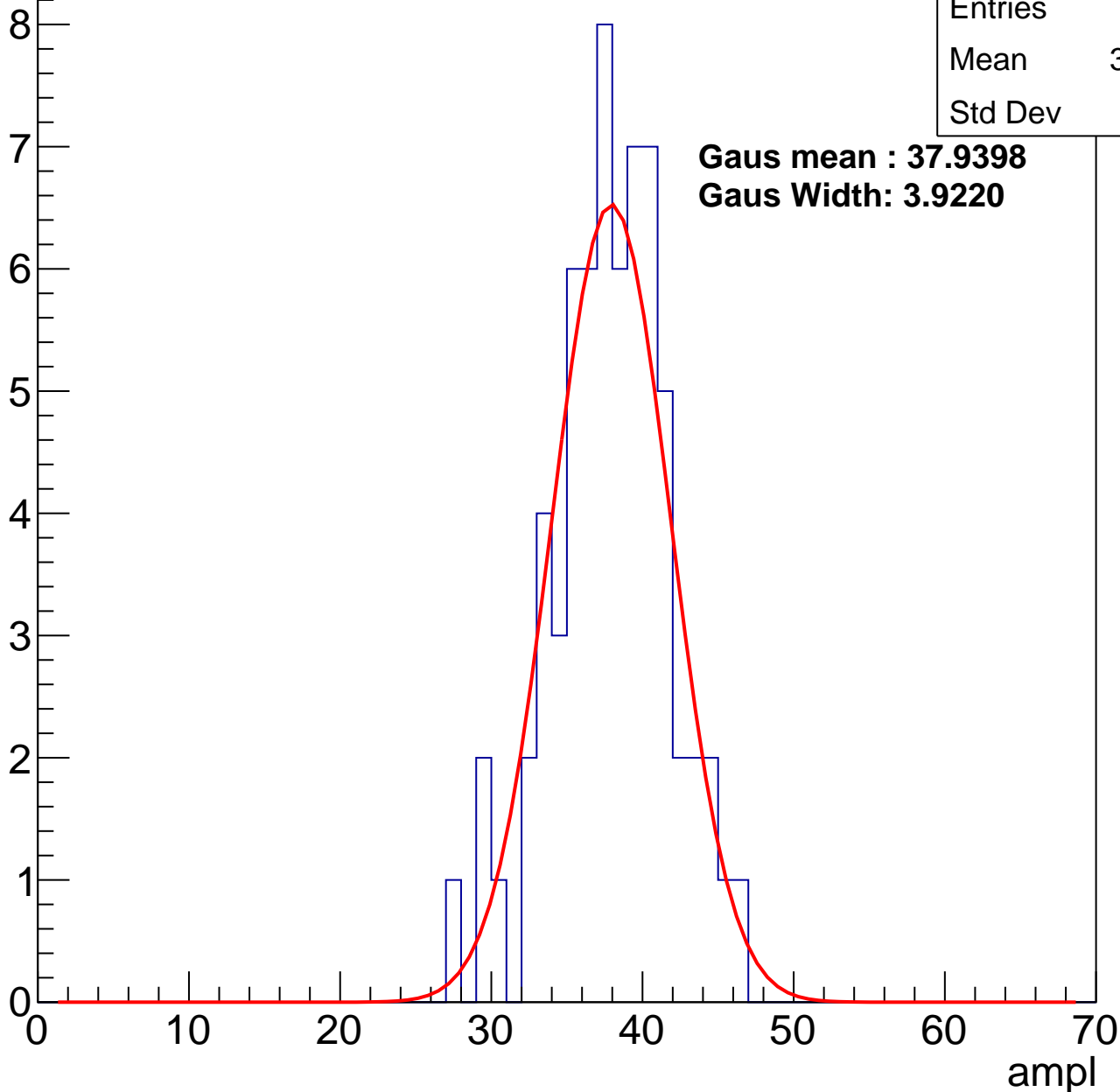
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.42
Std Dev	3.85

Gaus mean : 37.9398

Gaus Width: 3.9220



B0L000S, U2-ch56, adc2

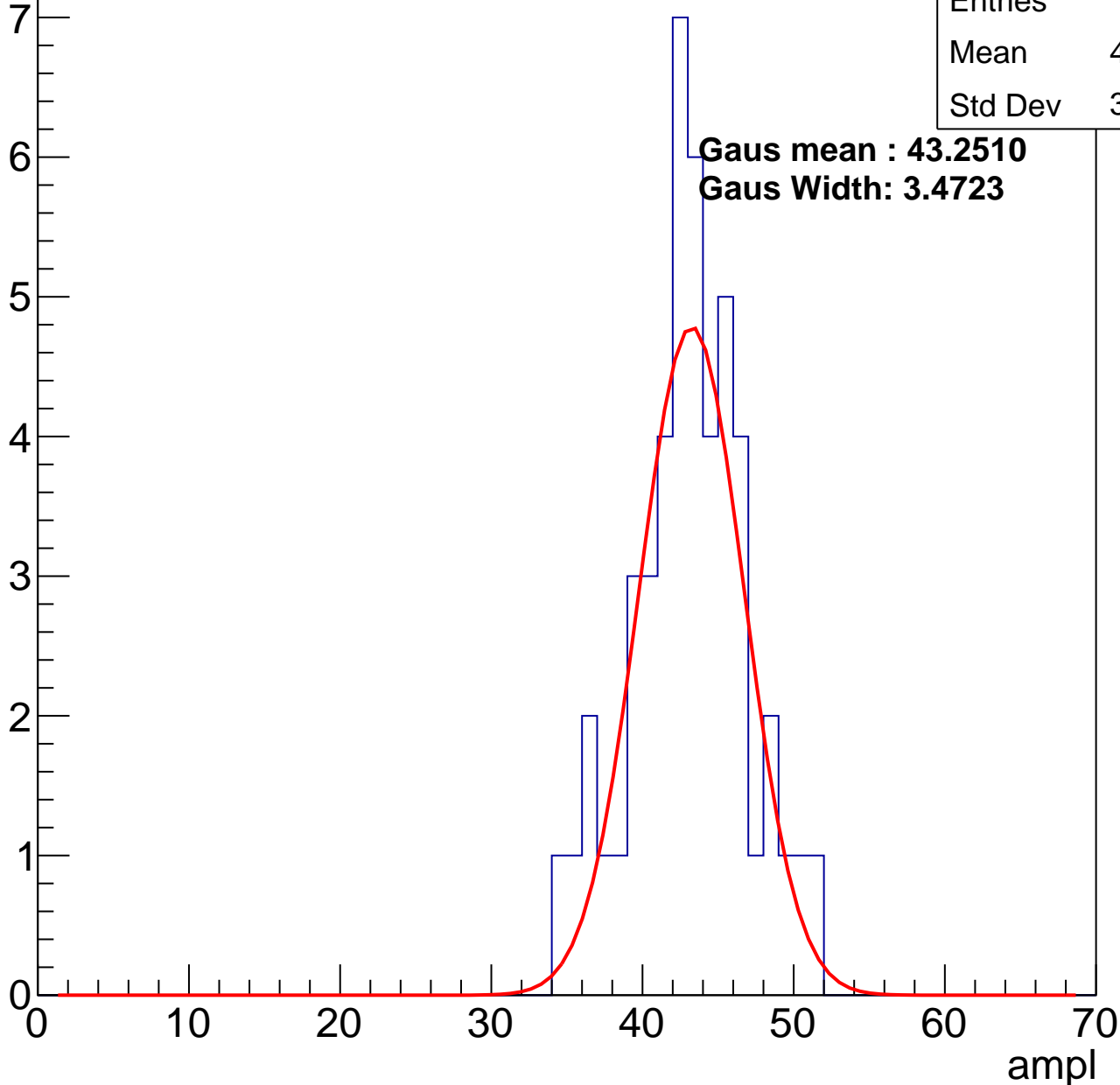
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	42.65
Std Dev	3.739

Gaus mean : 43.2510

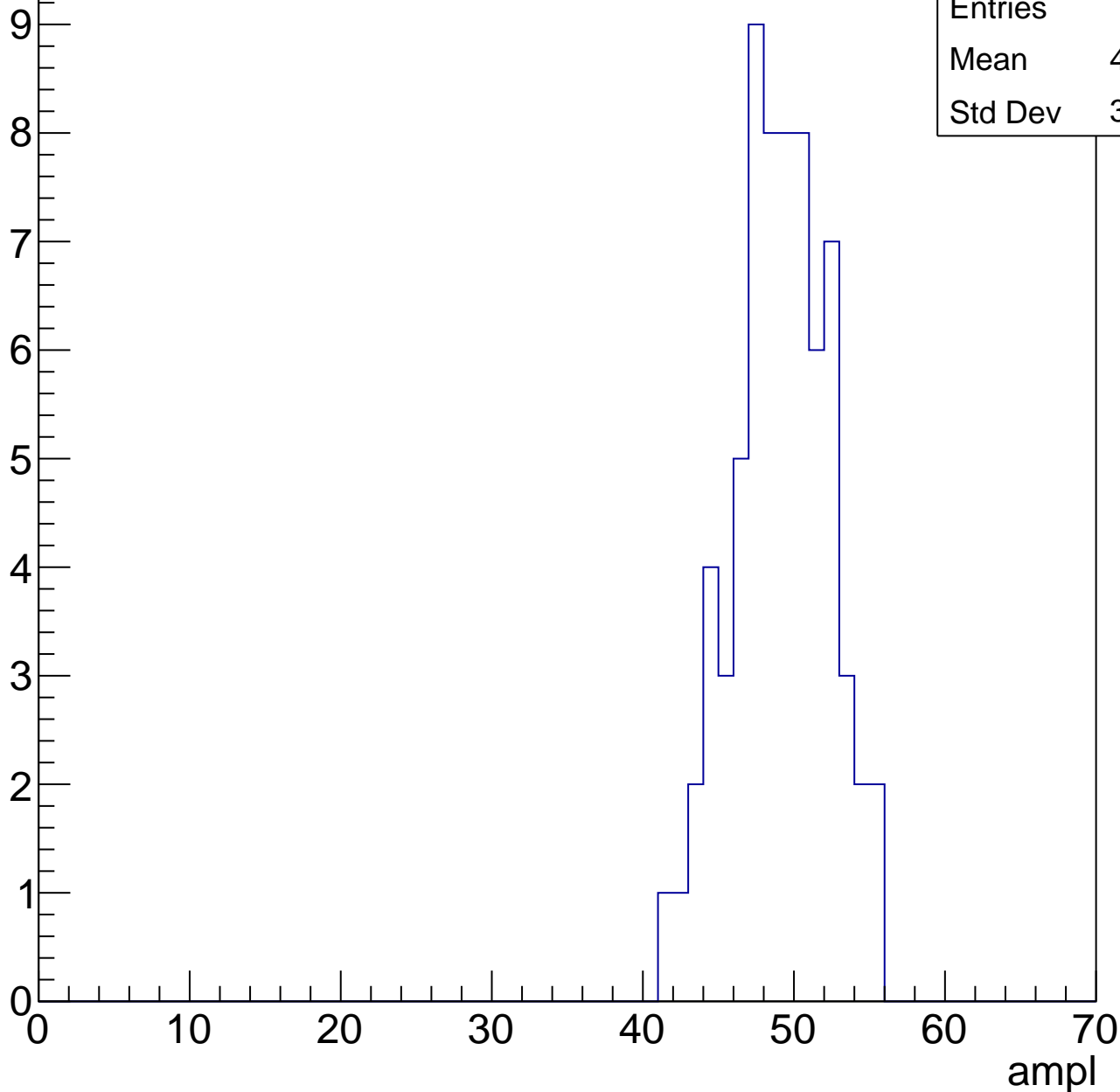
Gaus Width: 3.4723



B0L000S, U2-ch56, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

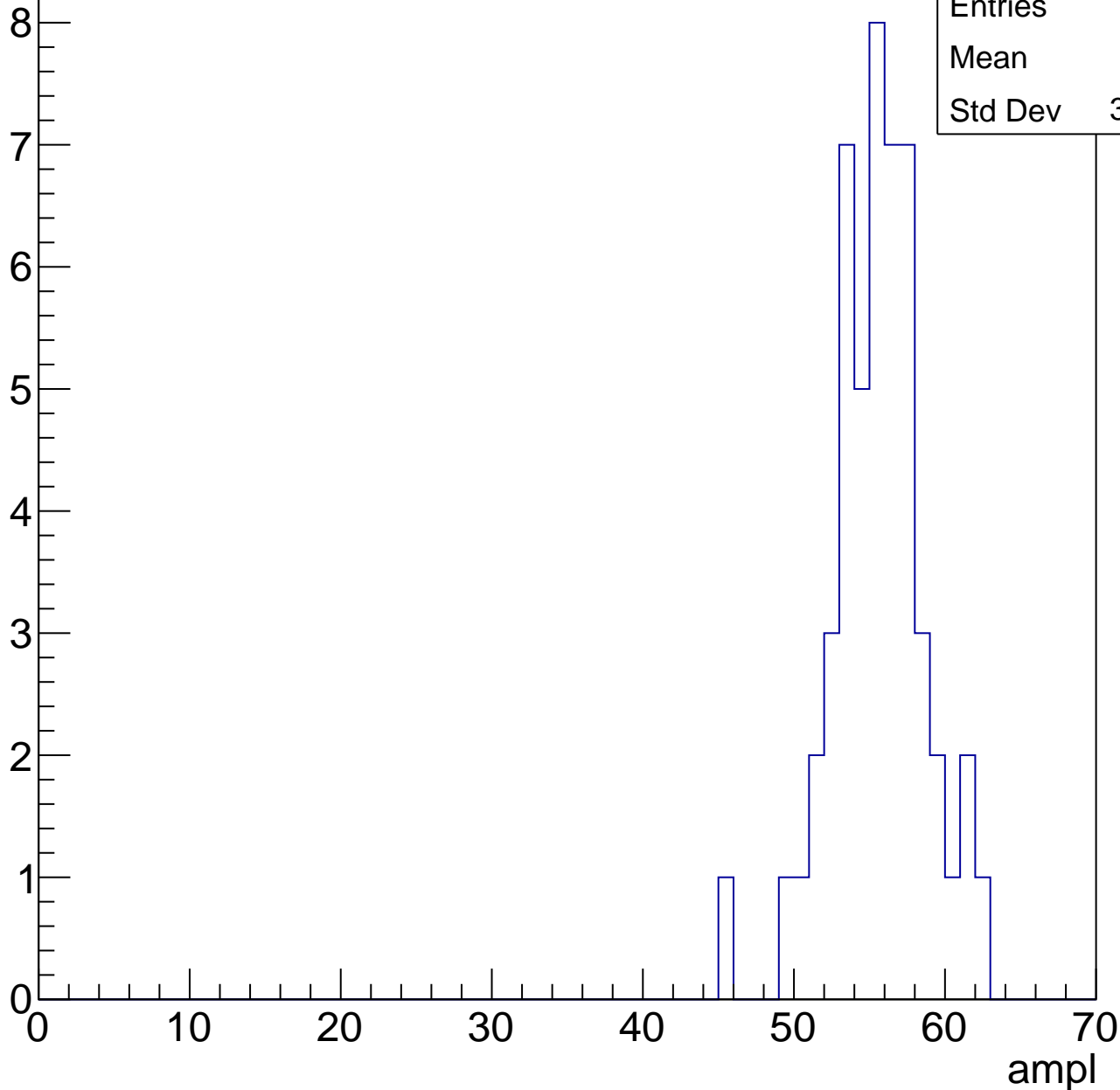


B0L000S, U2-ch56, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	55.1
Std Dev	3.108

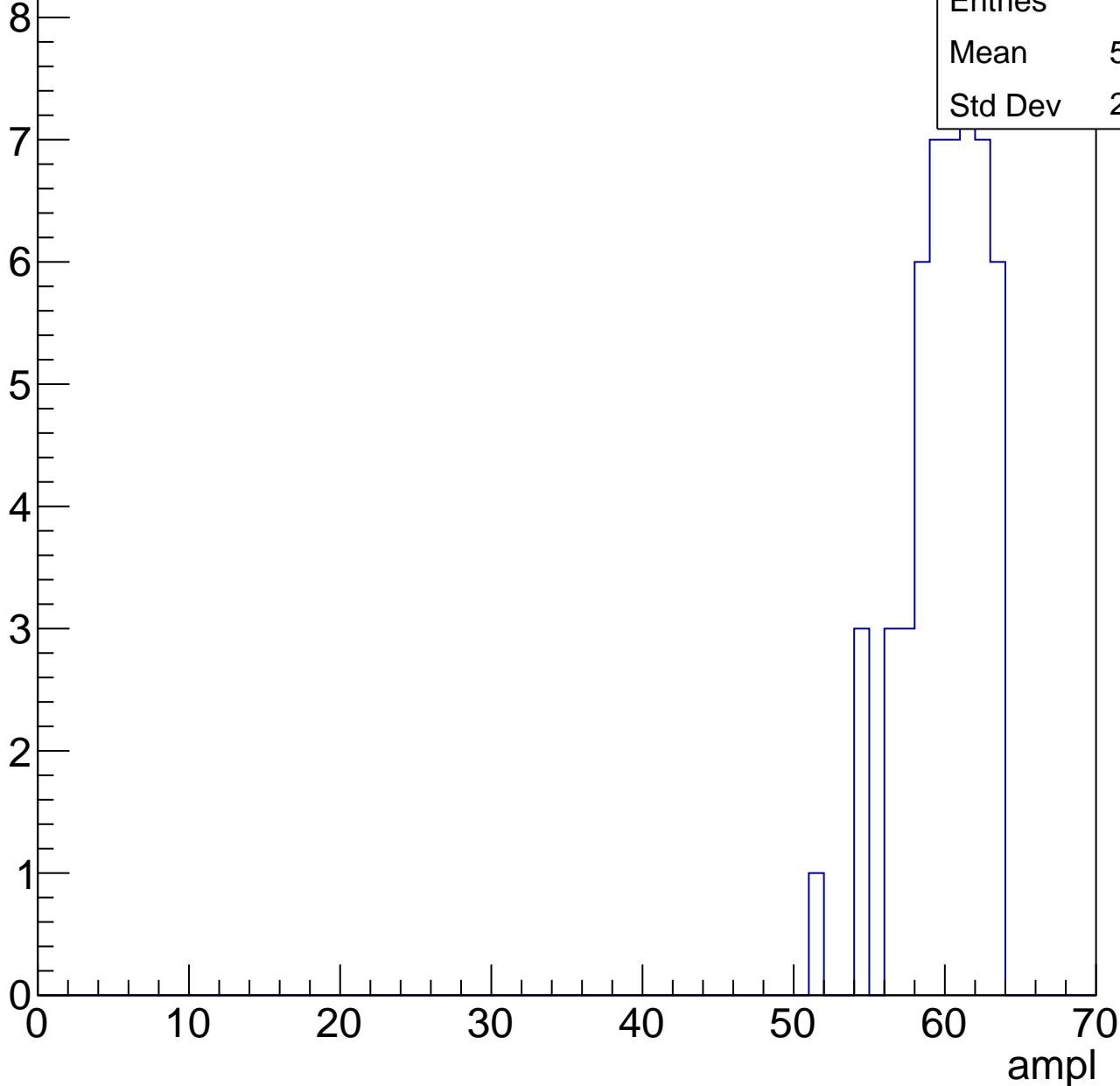


B0L000S, U2-ch56, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

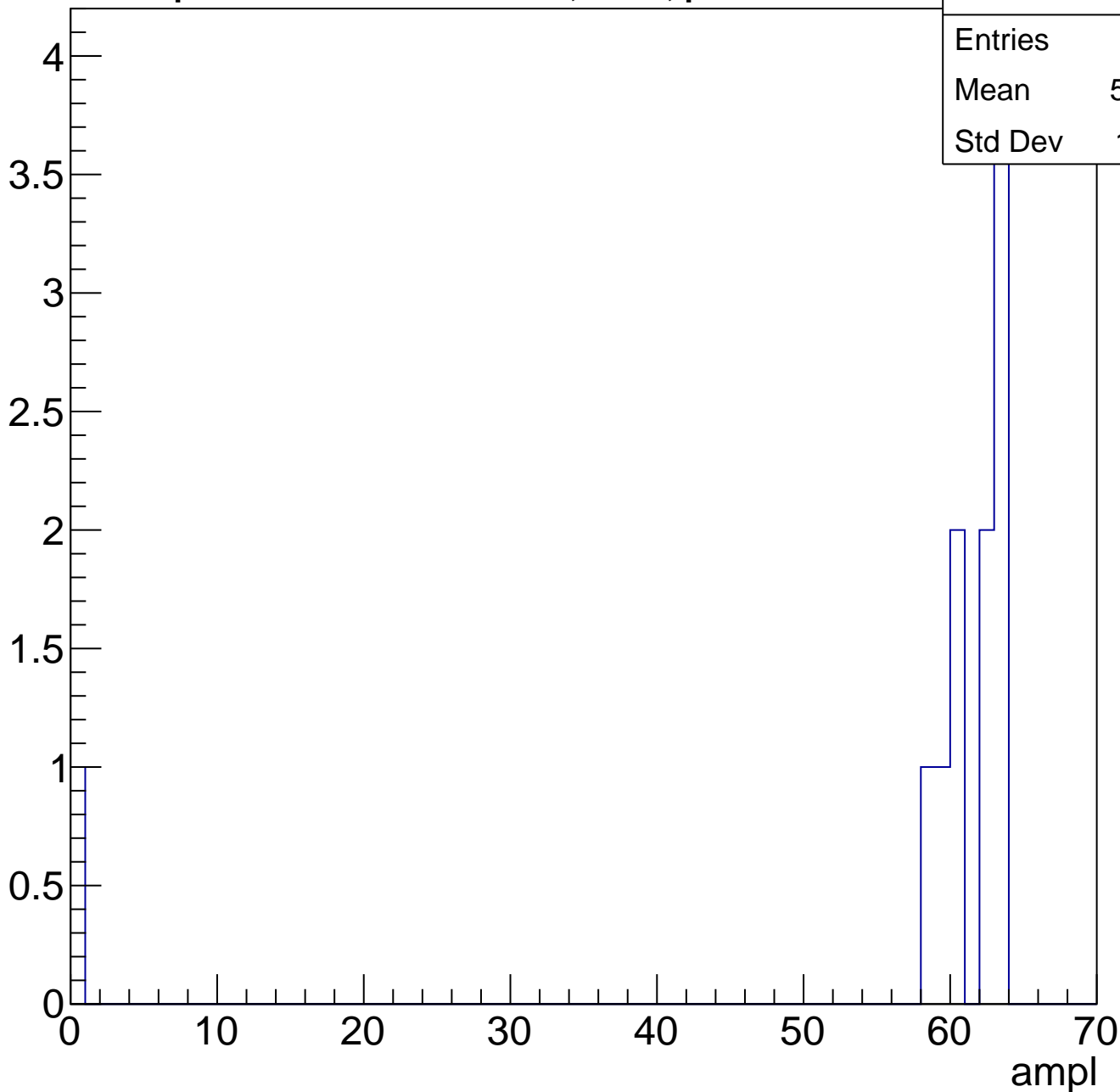
Entries	51
Mean	59.47
Std Dev	2.696



B0L000S, U2-ch56, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch56, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch57, adc0

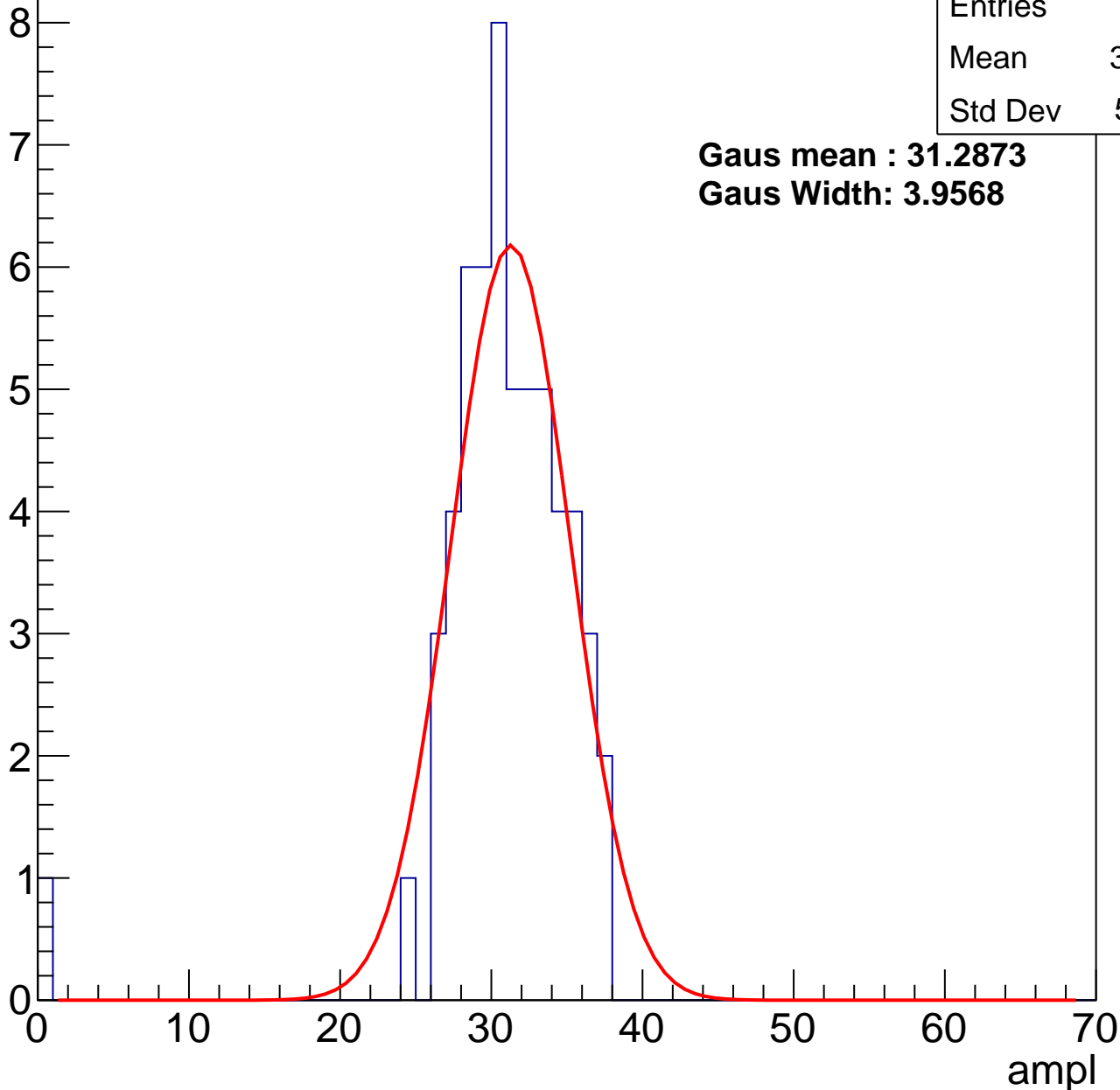
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	30.35
Std Dev	5.101

Gaus mean : 31.2873

Gaus Width: 3.9568



B0L000S, U2-ch57, adc1

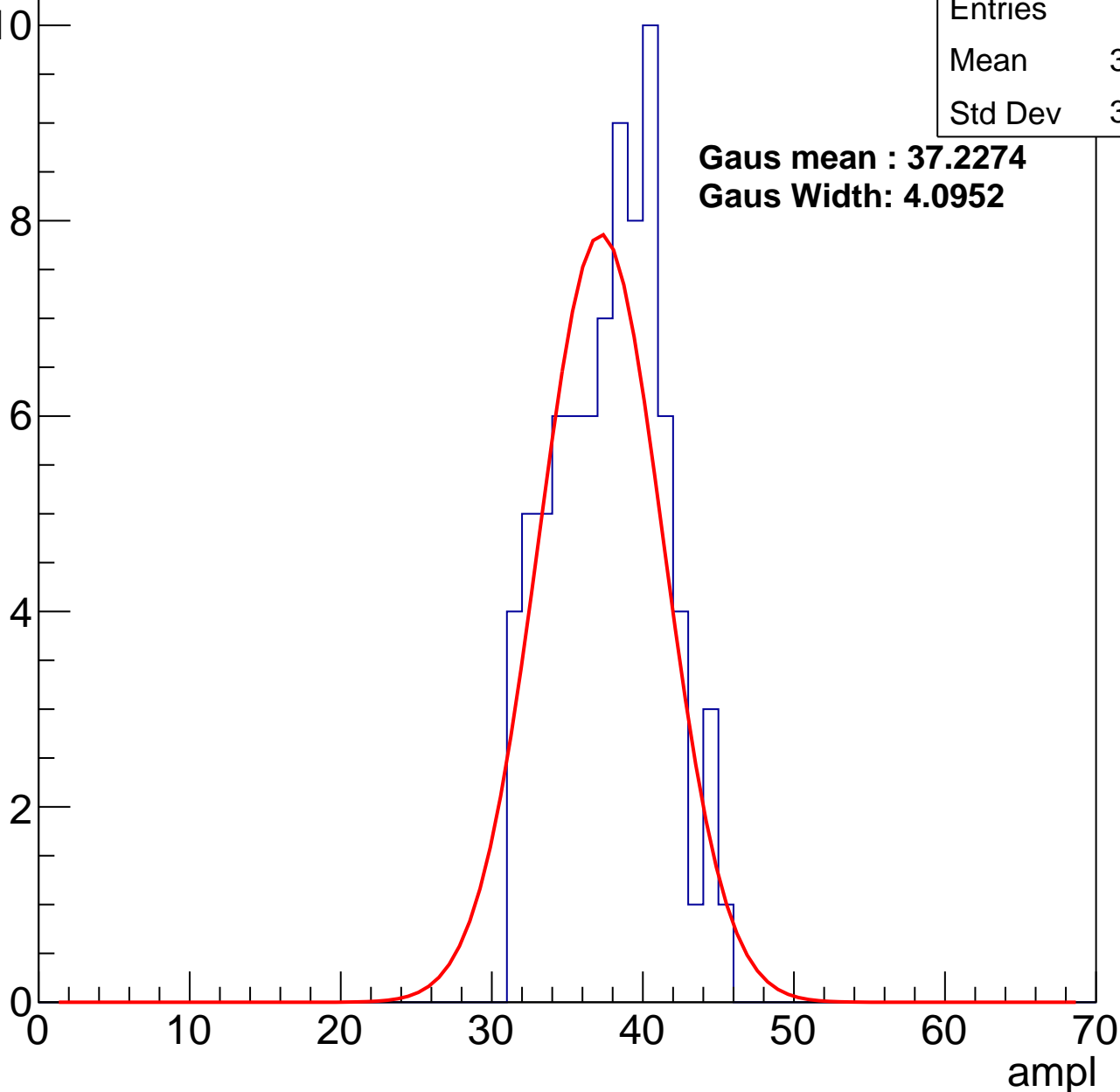
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	81
Mean	37.36
Std Dev	3.508

Gaus mean : 37.2274

Gaus Width: 4.0952



B0L000S, U2-ch57, adc2

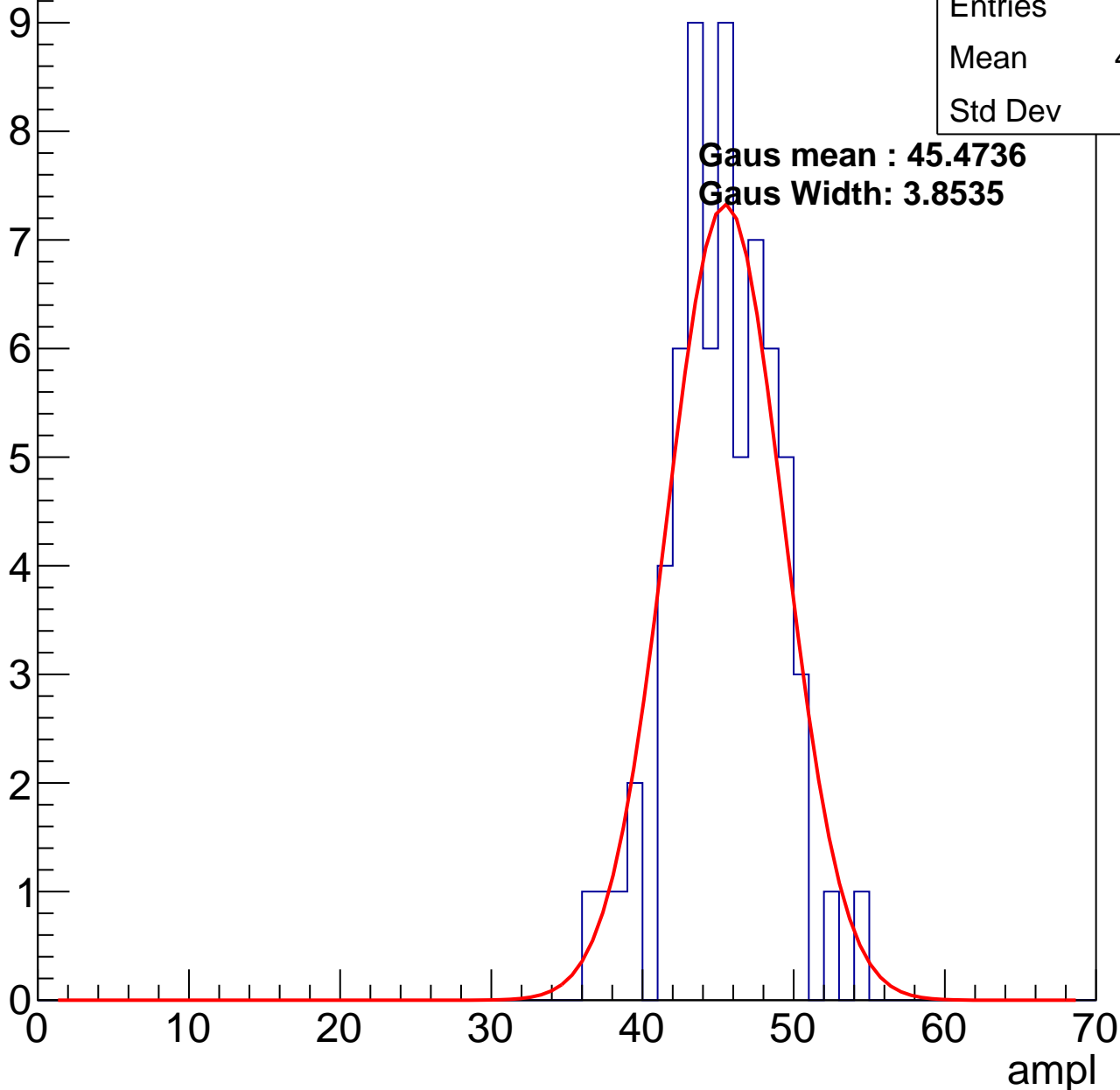
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	44.91
Std Dev	3.45

Gaus mean : 45.4736

Gaus Width: 3.8535

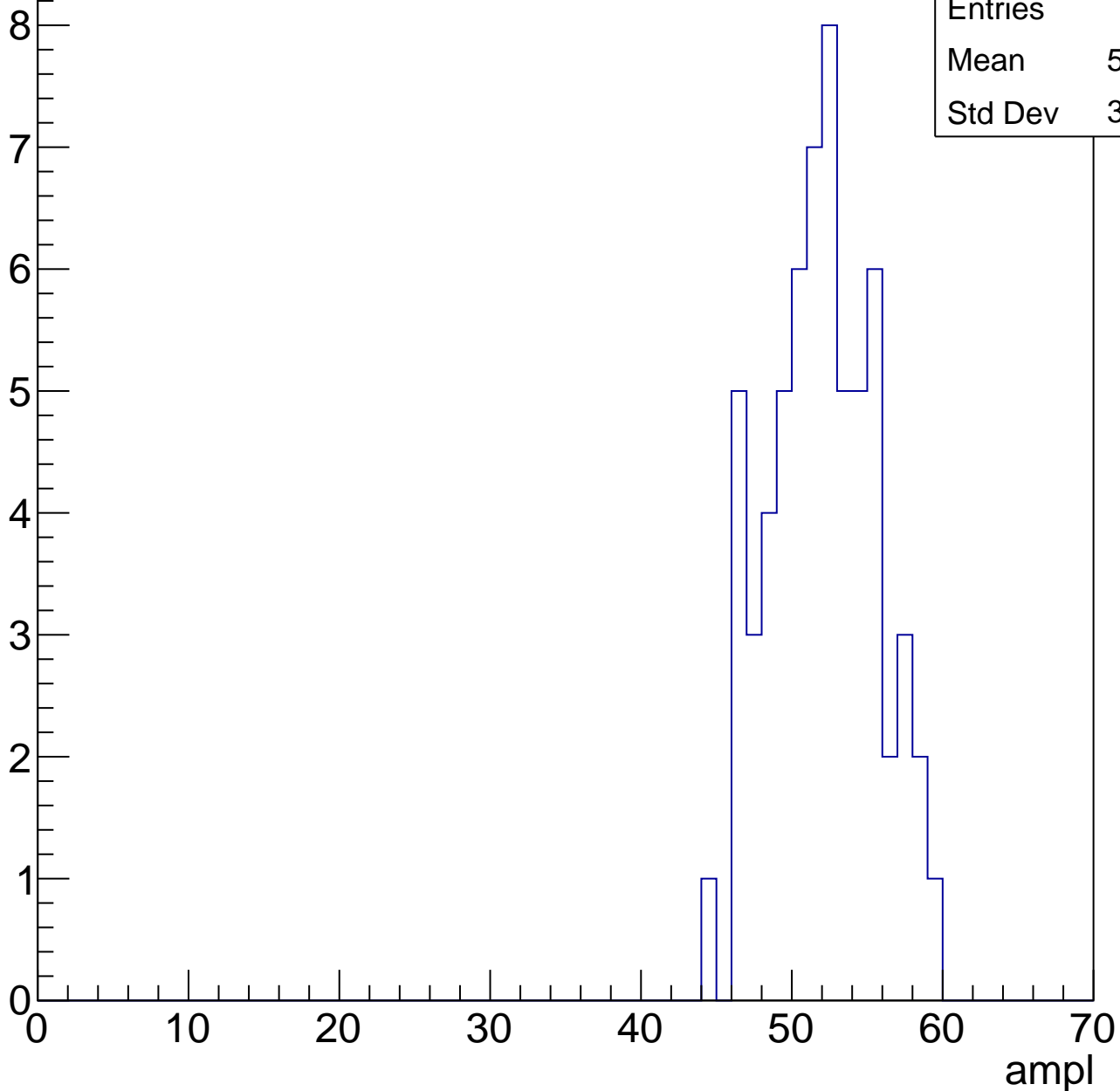


B0L000S, U2-ch57, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	51.56
Std Dev	3.458

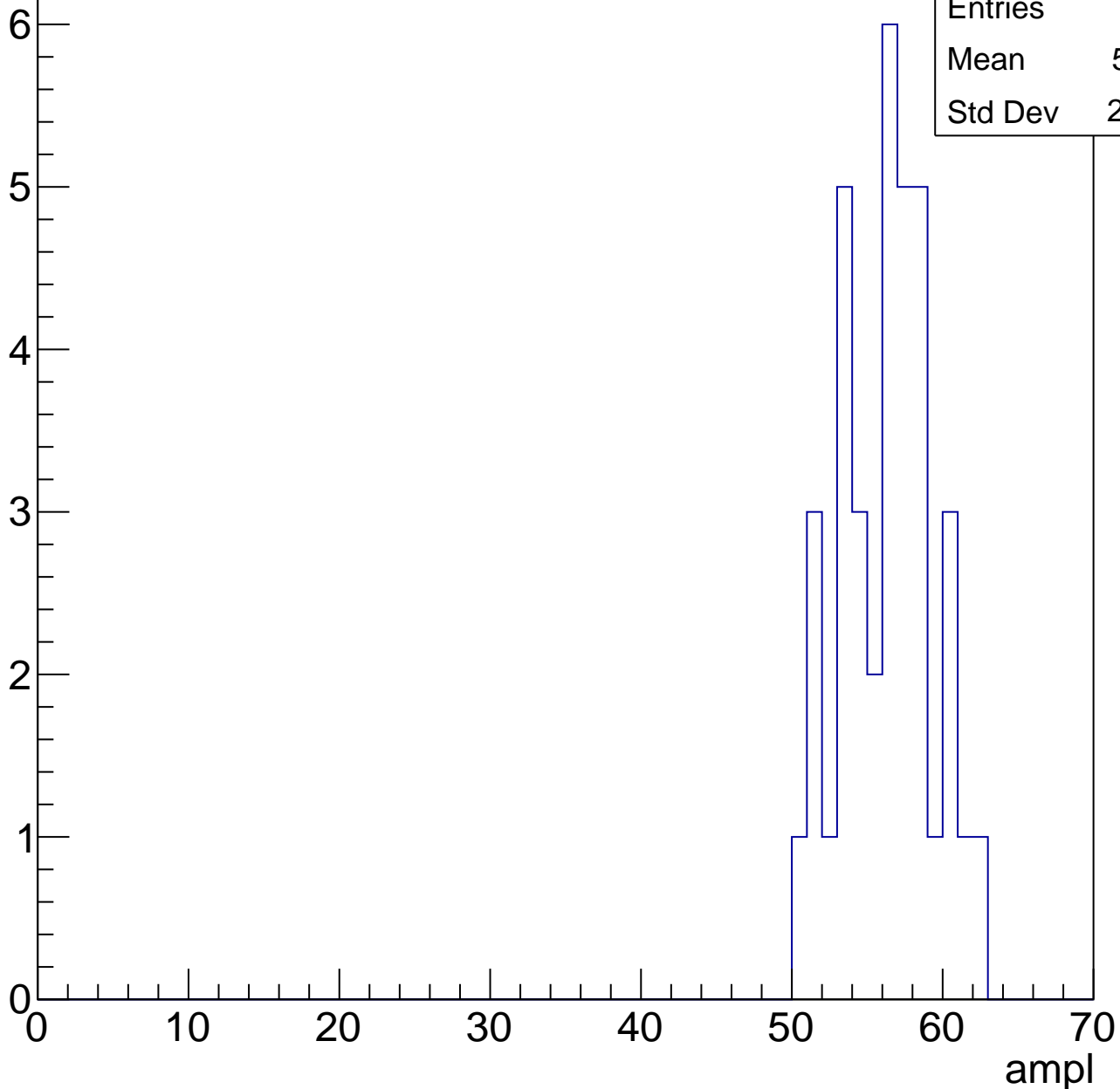


B0L000S, U2-ch57, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	37
Mean	55.81
Std Dev	2.976

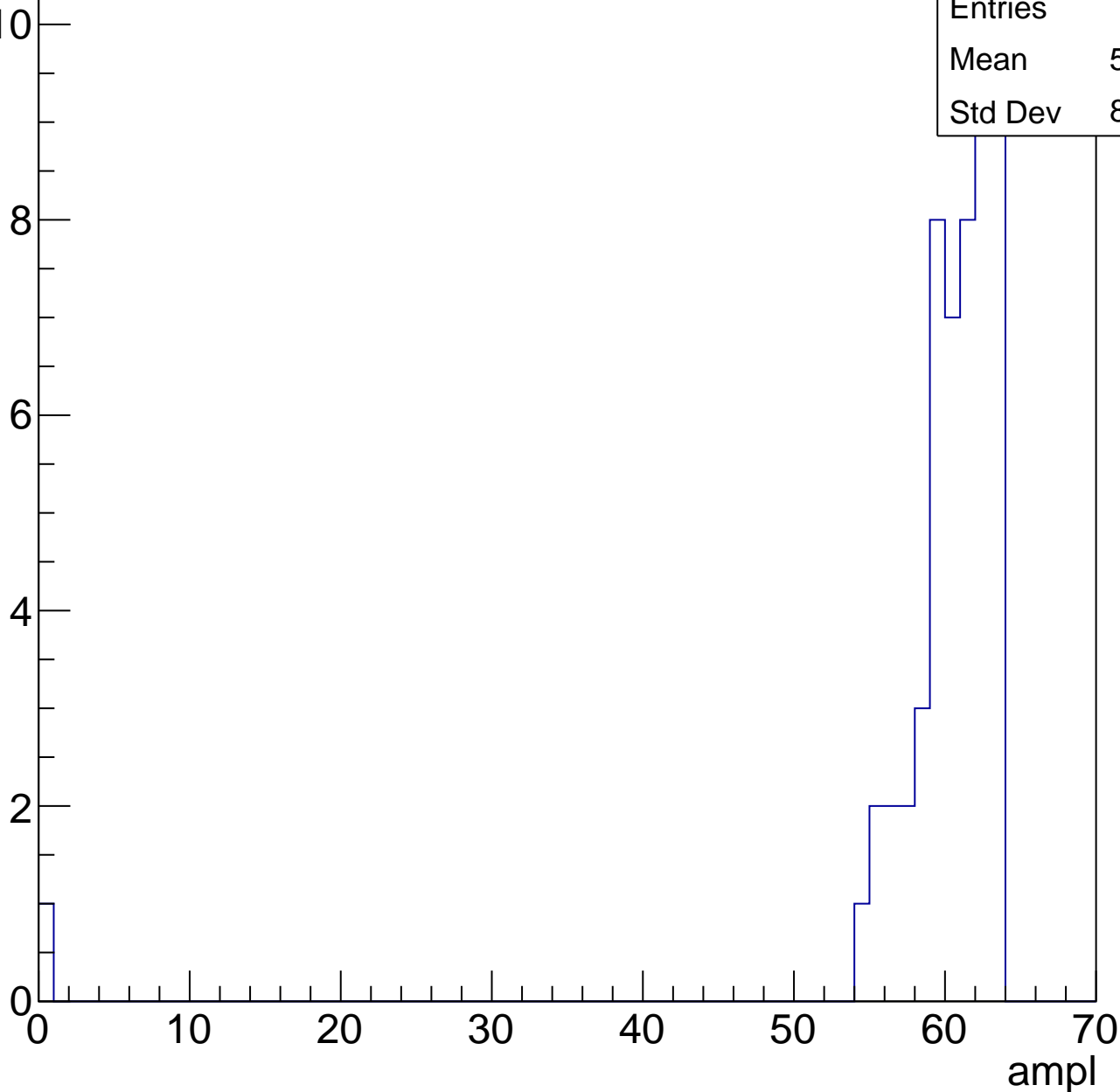


B0L000S, U2-ch57, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	59.09
Std Dev	8.519



B0L000S, U2-ch57, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch57, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch58, adc0

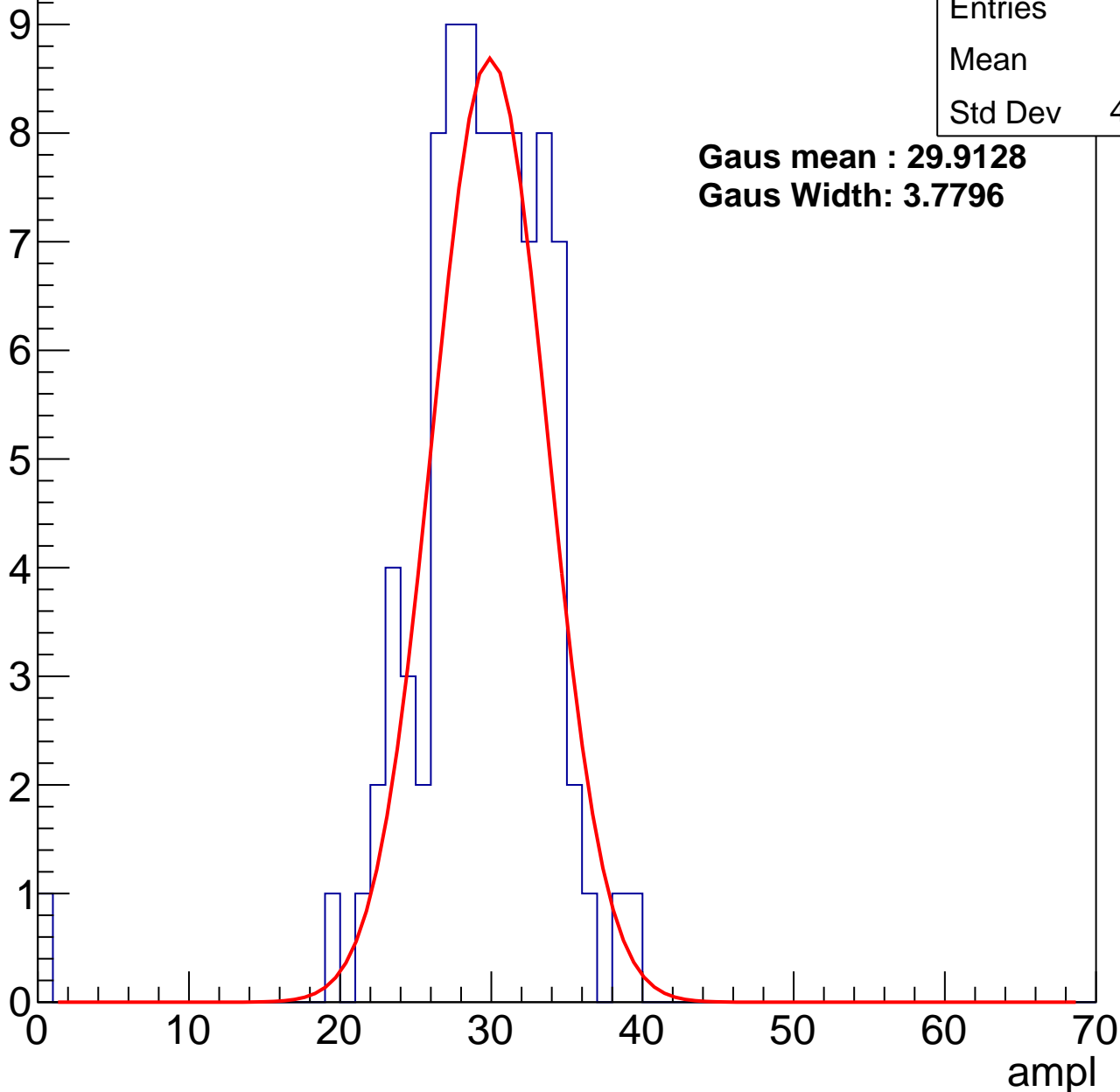
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	91
Mean	28.9
Std Dev	4.892

Gaus mean : 29.9128

Gaus Width: 3.7796



B0L000S, U2-ch58, adc1

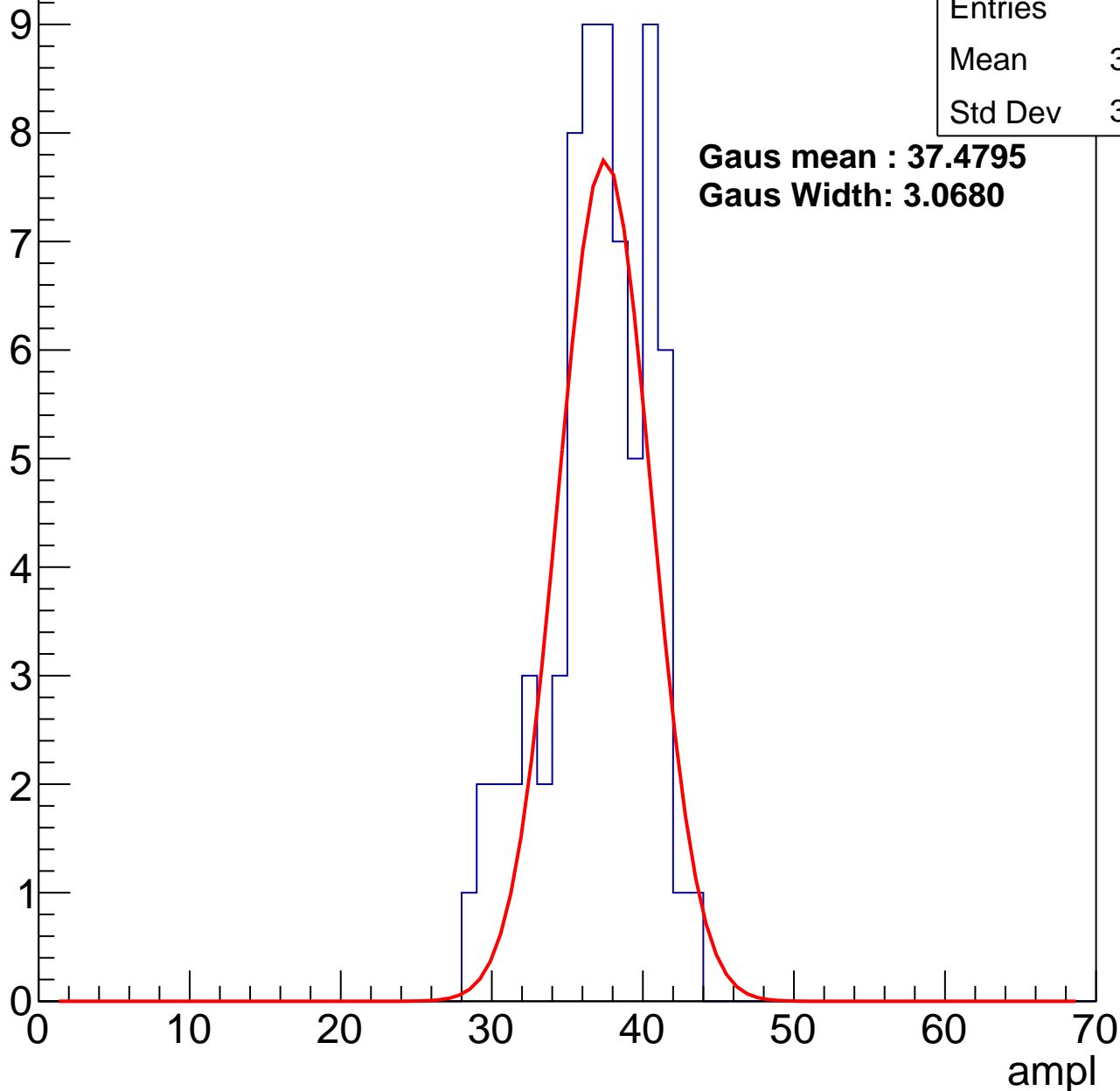
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.59
Std Dev	3.395

Gaus mean : 37.4795

Gaus Width: 3.0680



B0L000S, U2-ch58, adc2

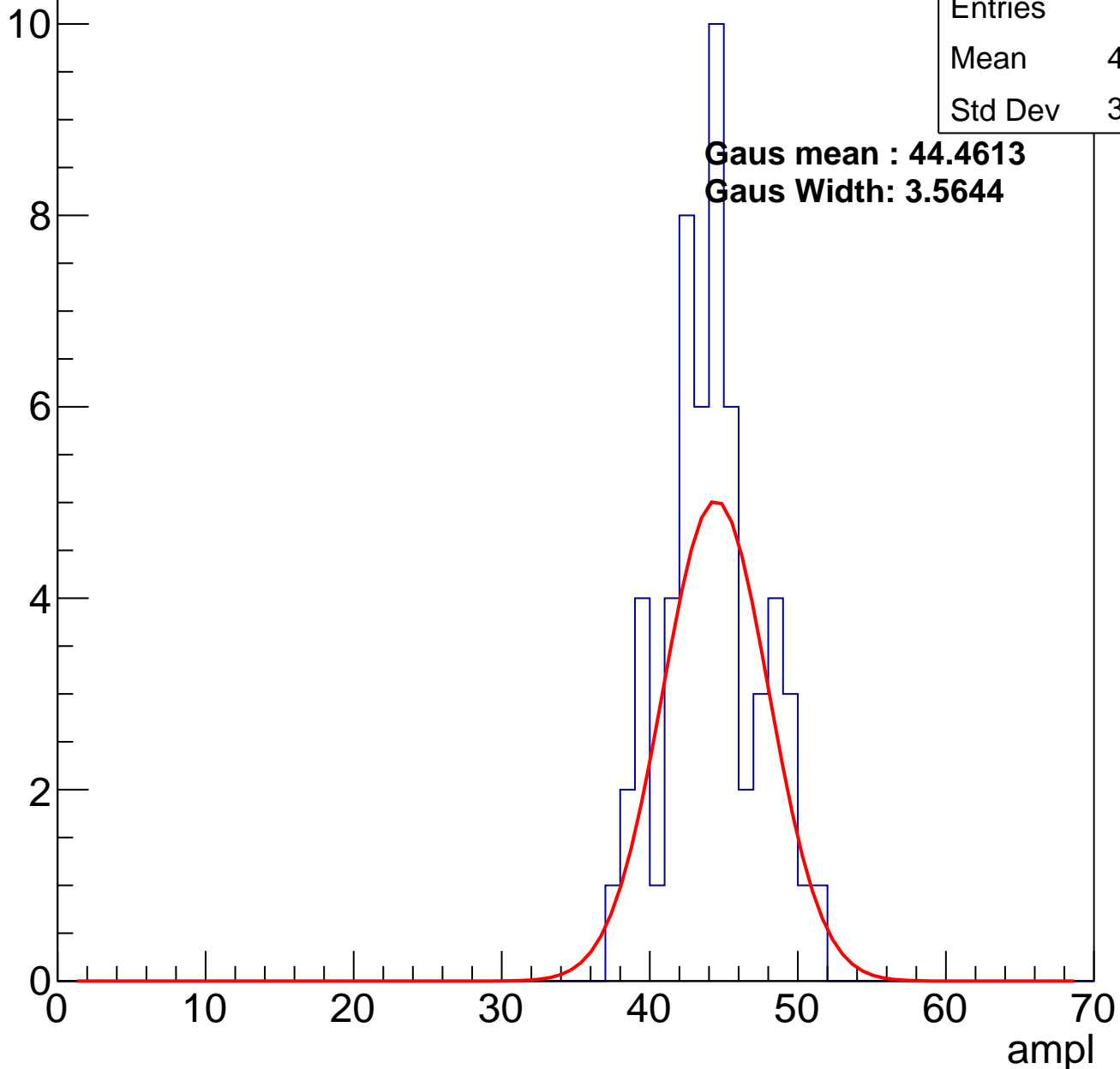
calib_packv5_042523_0143.root, FC#5, port B1

Entries	56
Mean	43.75
Std Dev	3.197

Gaus mean : 44.4613

Gaus Width: 3.5644

Entry

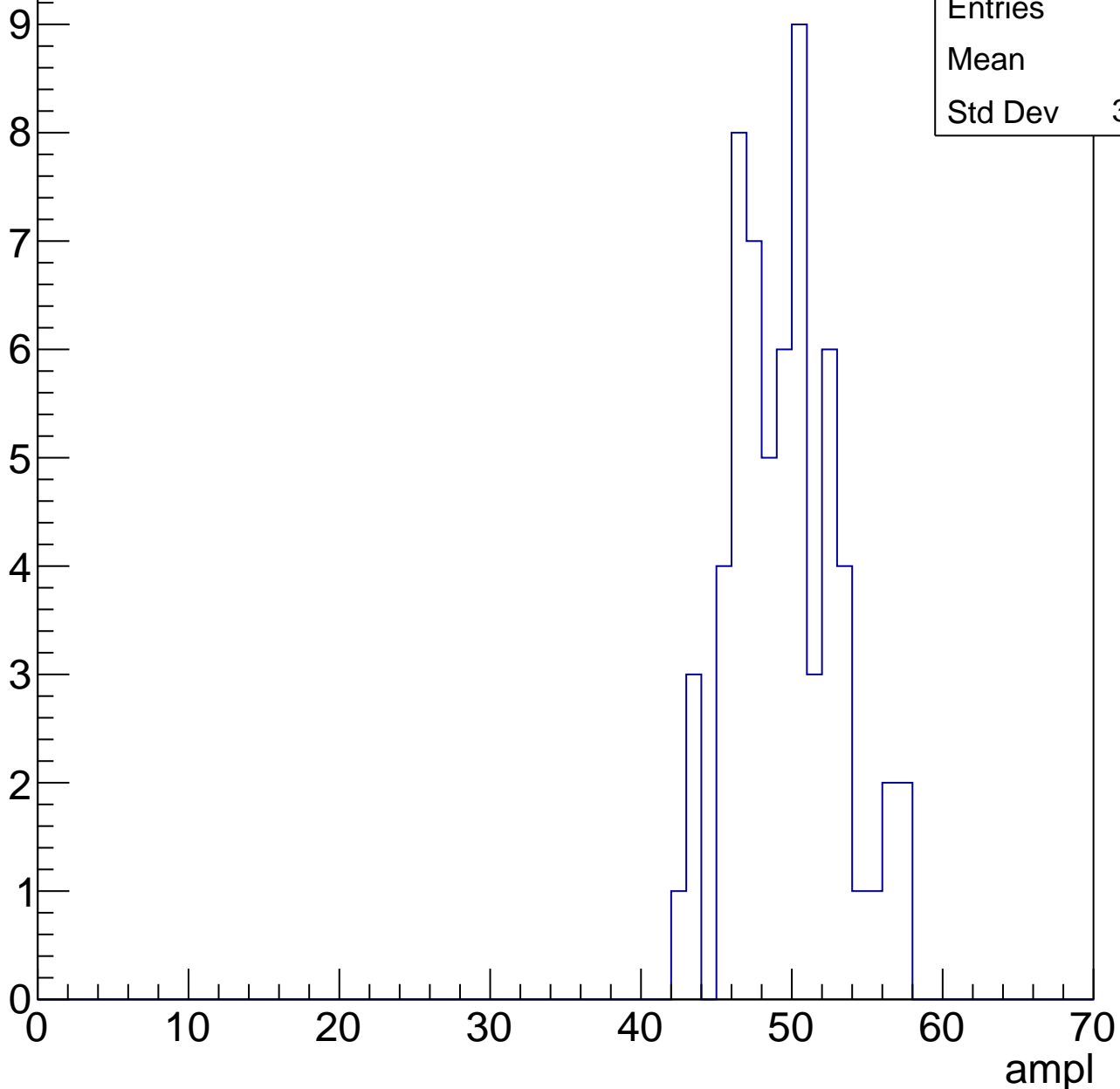


B0L000S, U2-ch58, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

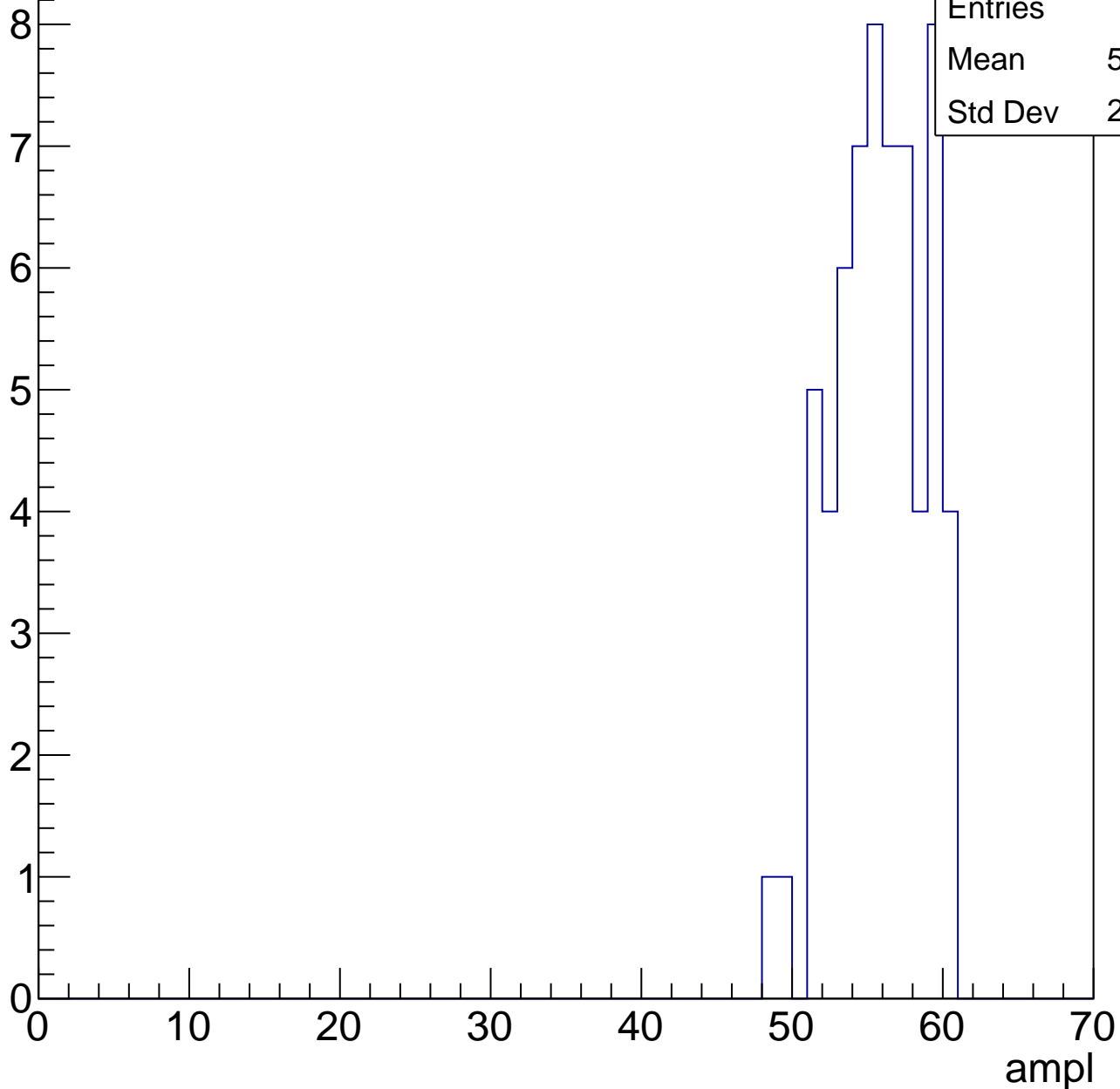
Entries	62
Mean	49.1
Std Dev	3.481



B0L000S, U2-ch58, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

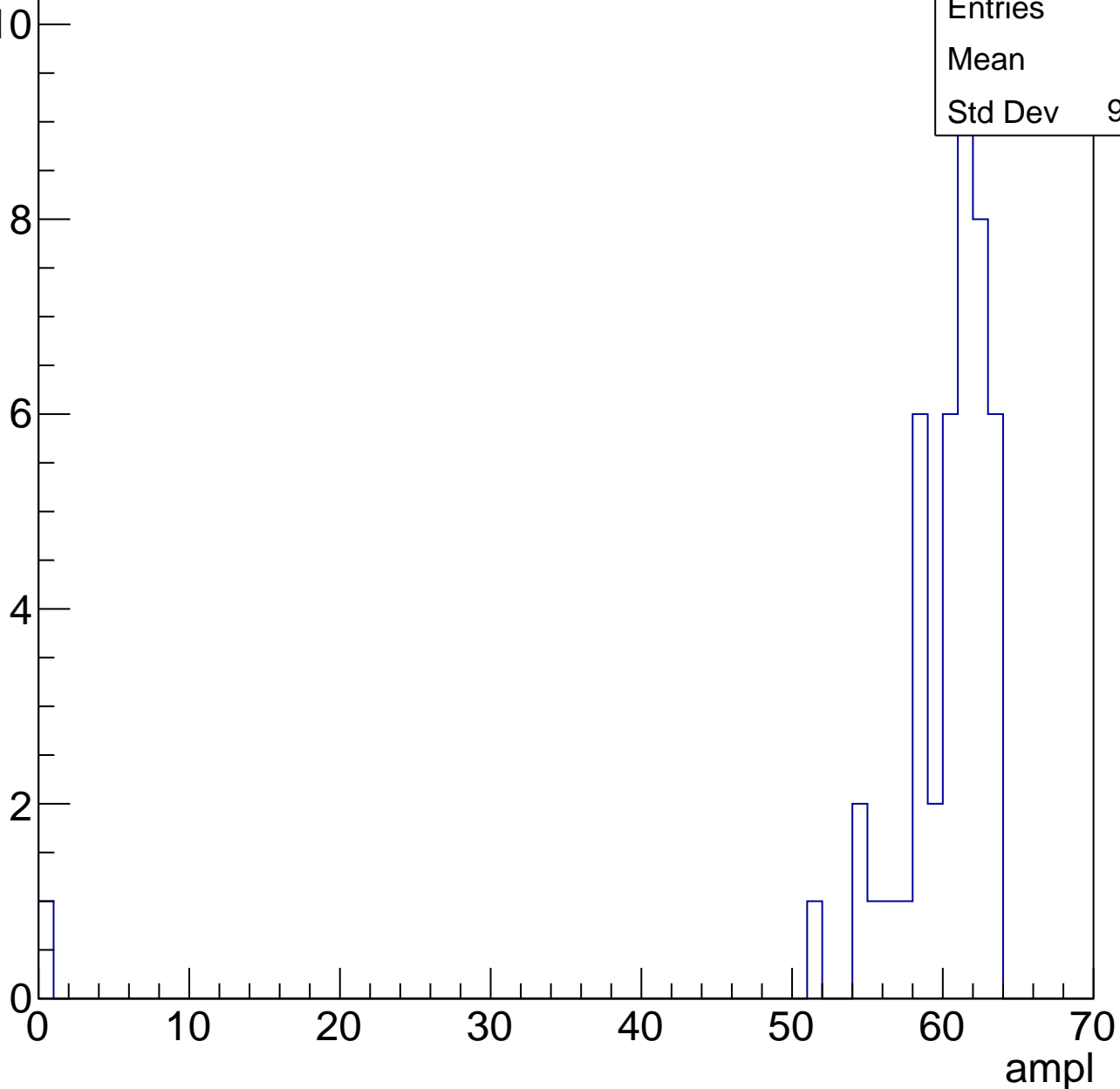


B0L000S, U2-ch58, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

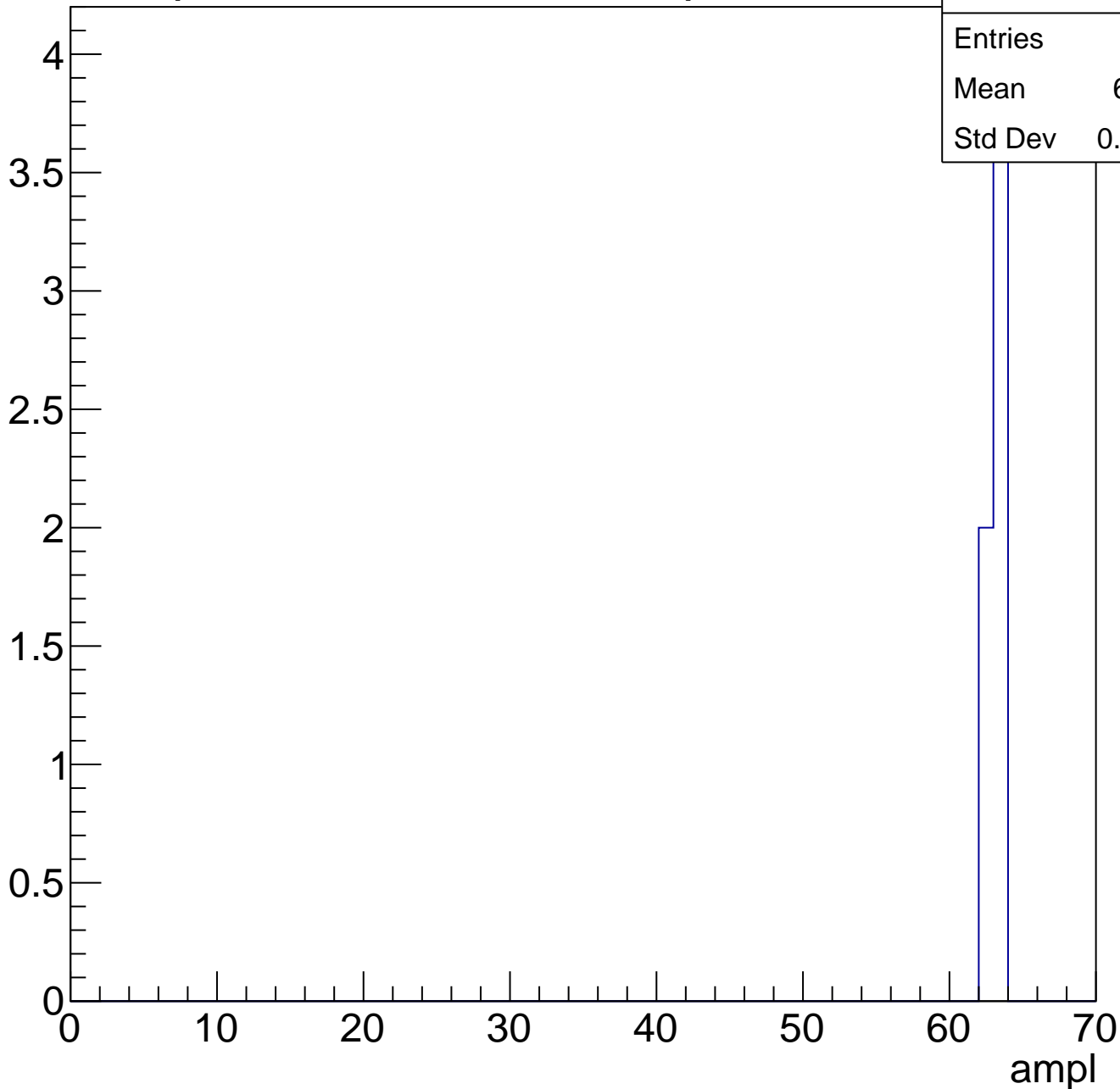
Entries	45
Mean	58.6
Std Dev	9.234



B0L000S, U2-ch58, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch58, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch59, adc0

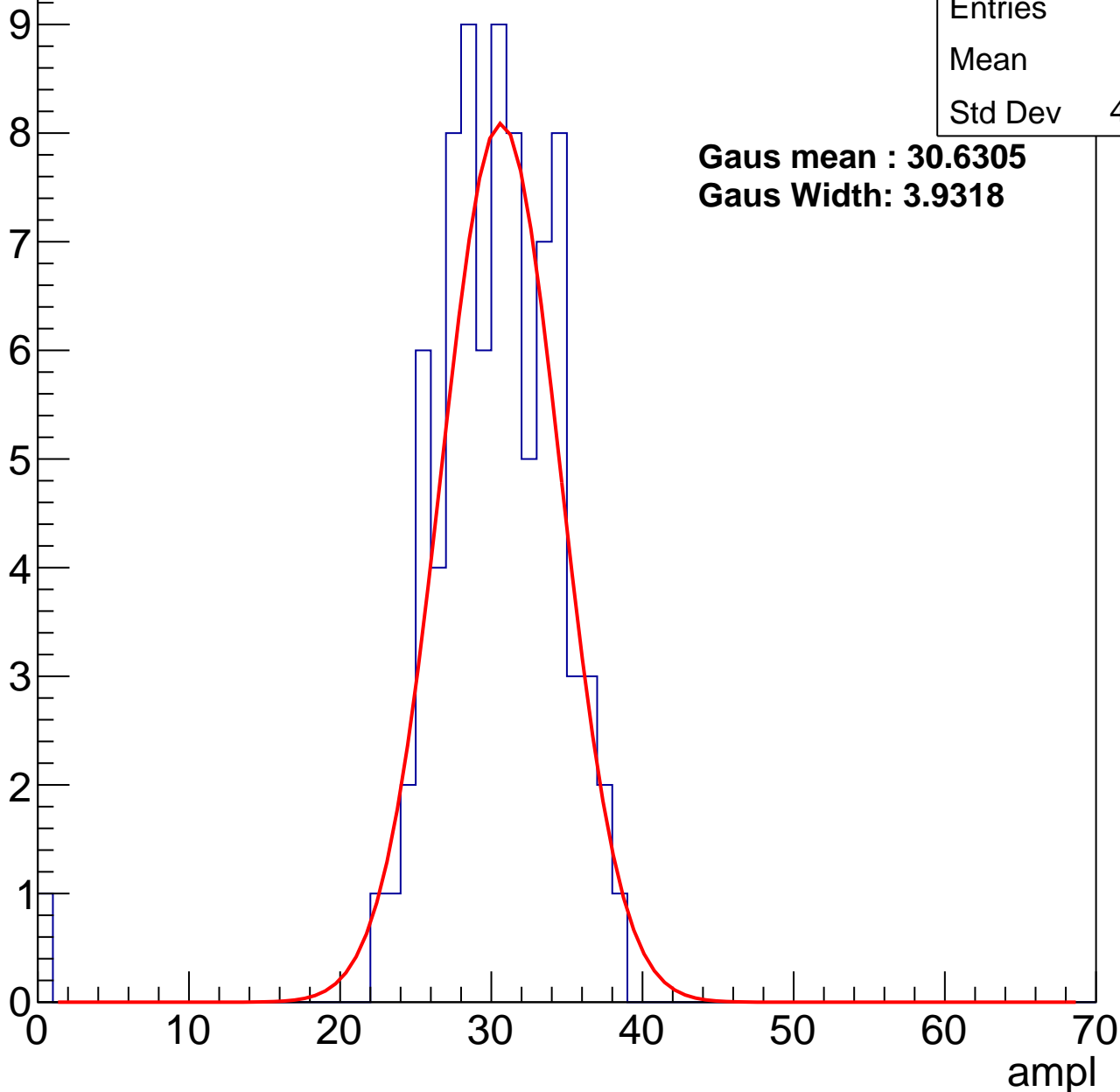
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	84
Mean	29.7
Std Dev	4.842

Gaus mean : 30.6305

Gaus Width: 3.9318

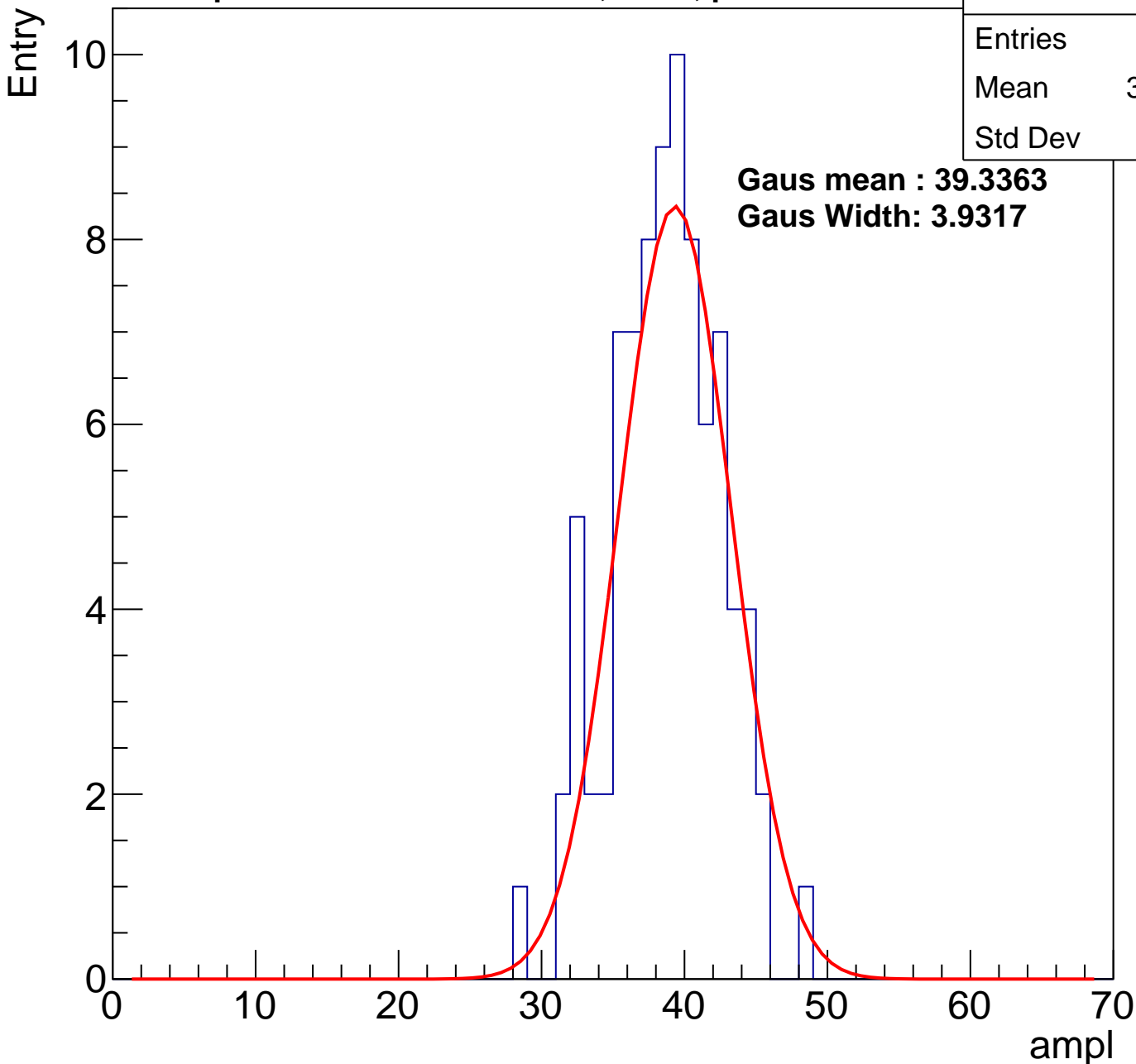


B0L000S, U2-ch59, adc1

calib_packv5_042523_0143.root, FC#5, port B1

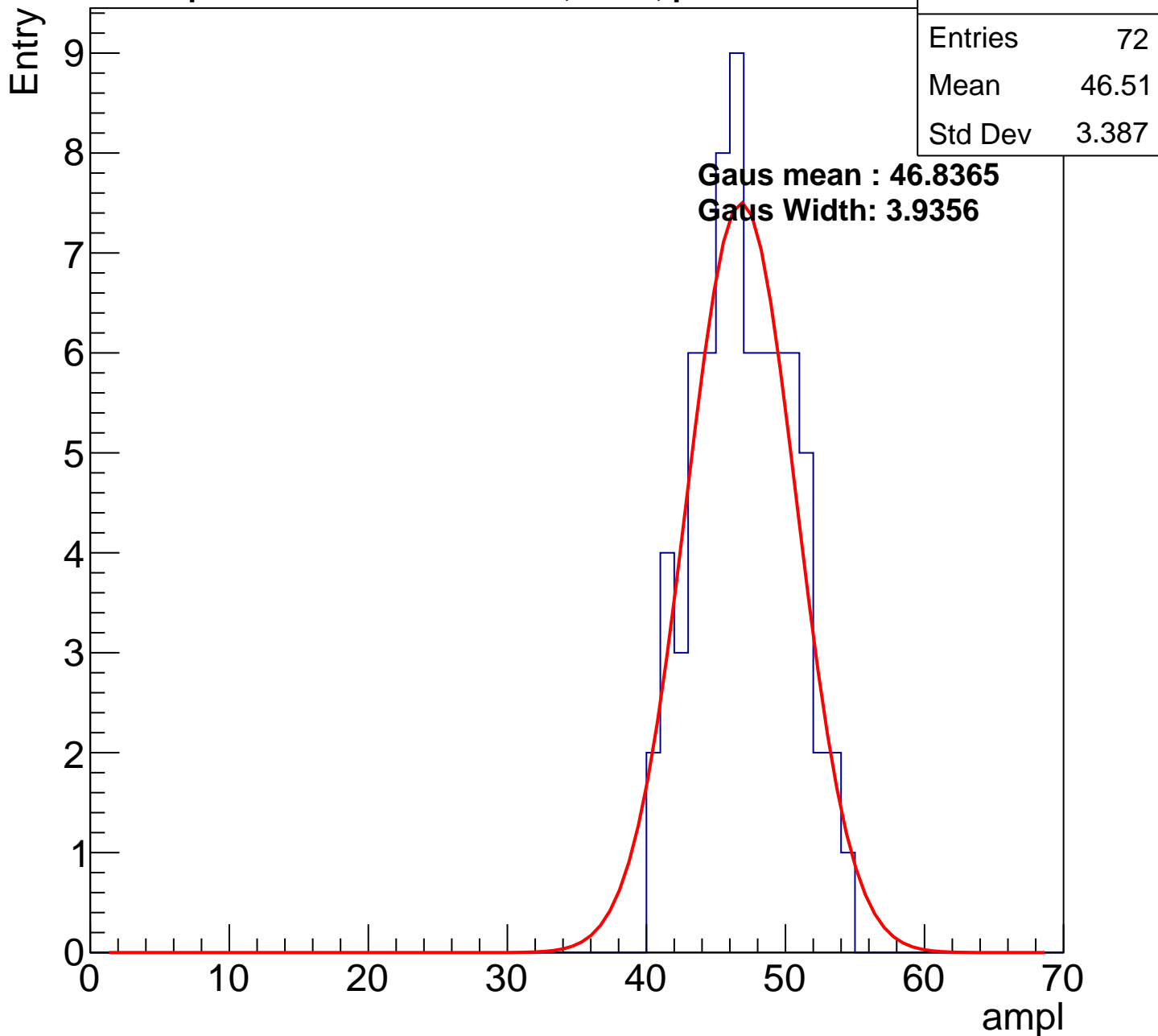
Entries	85
Mean	38.29
Std Dev	3.76

Gaus mean : 39.3363
Gaus Width: 3.9317



B0L000S, U2-ch59, adc2

calib_packv5_042523_0143.root, FC#5, port B1

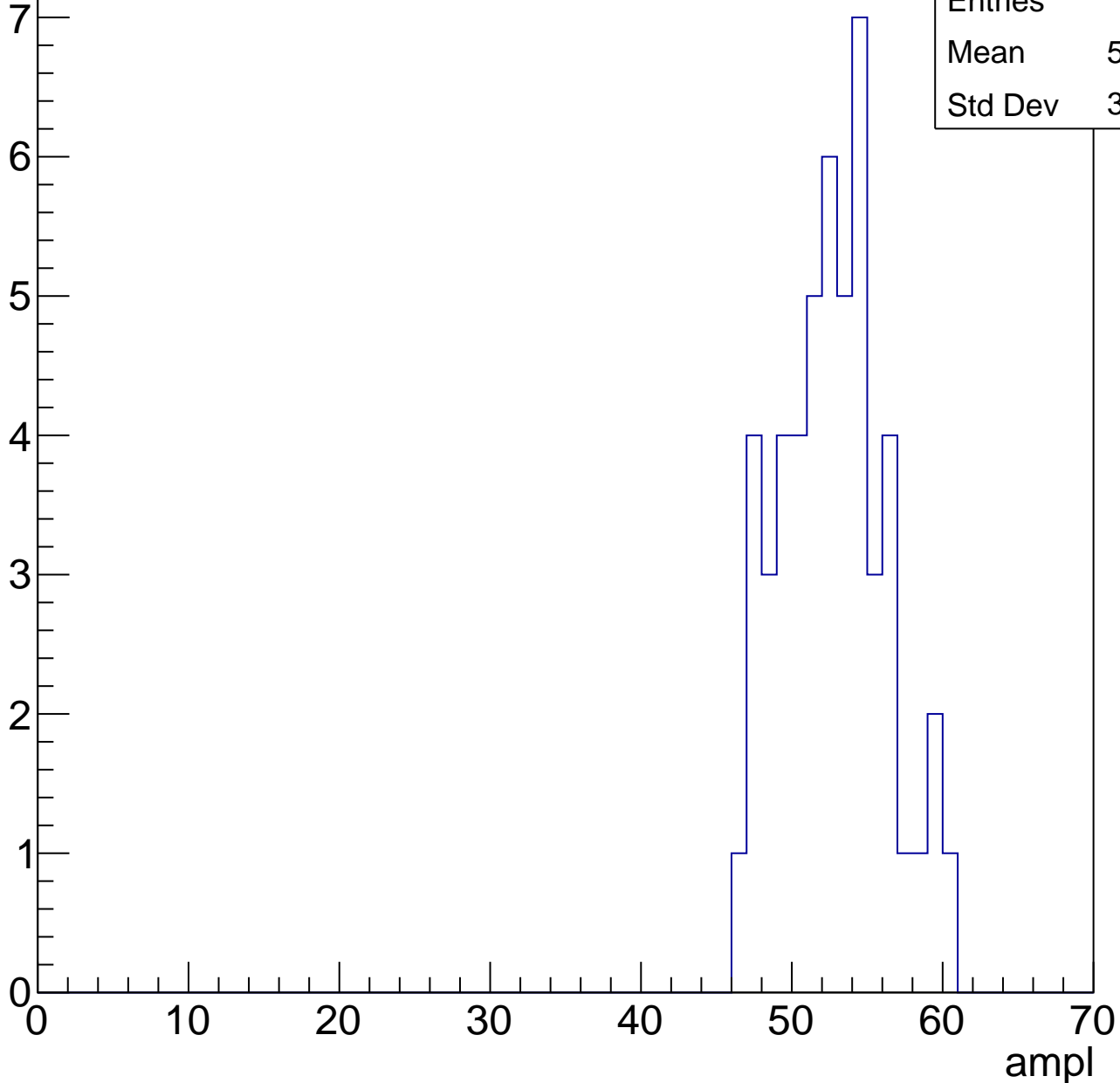


B0L000S, U2-ch59, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	52.27
Std Dev	3.384

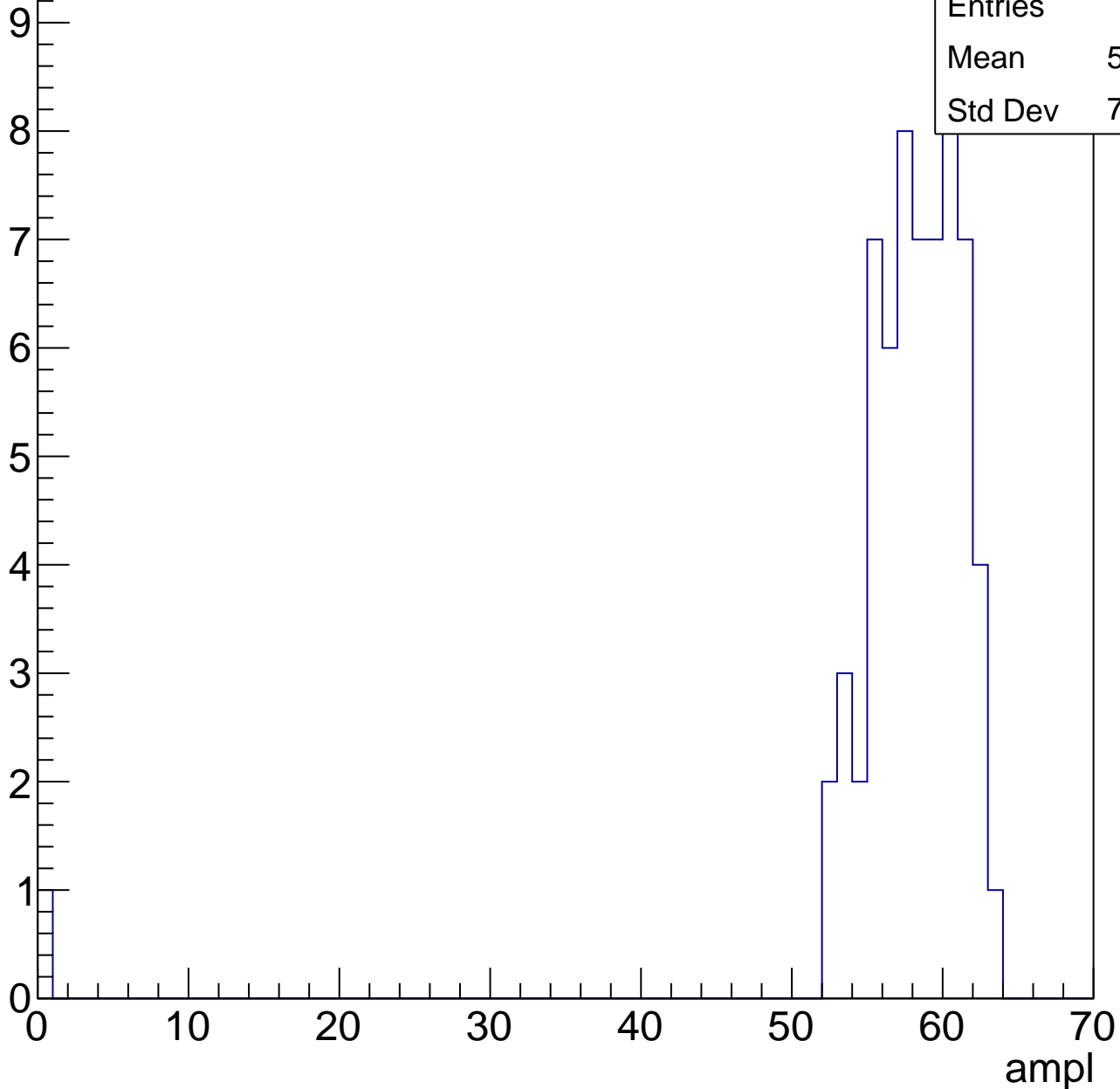


B0L000S, U2-ch59, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

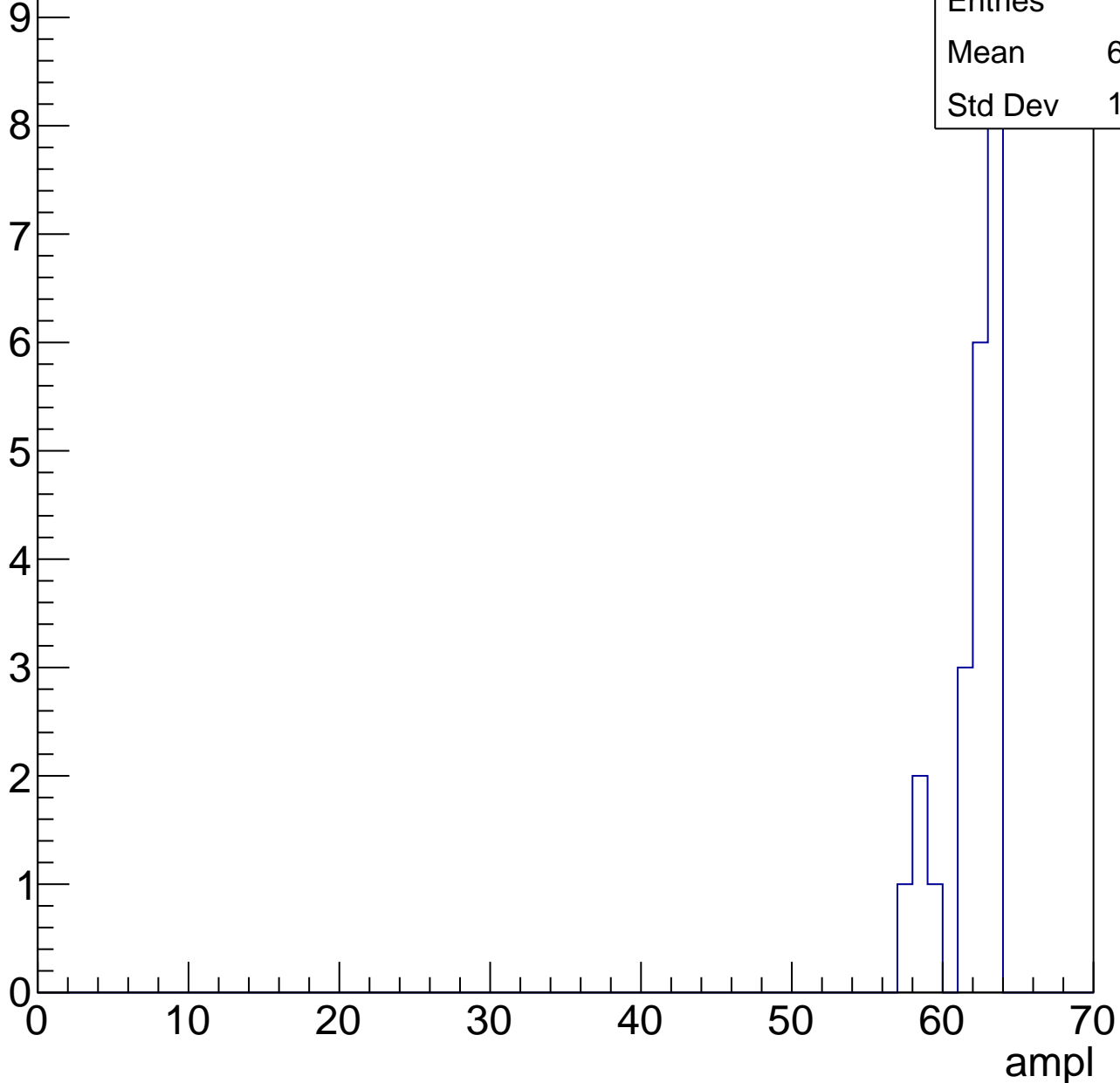
Entries	64
Mean	56.95
Std Dev	7.668



B0L000S, U2-ch59, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch59, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch59, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch60, adc0

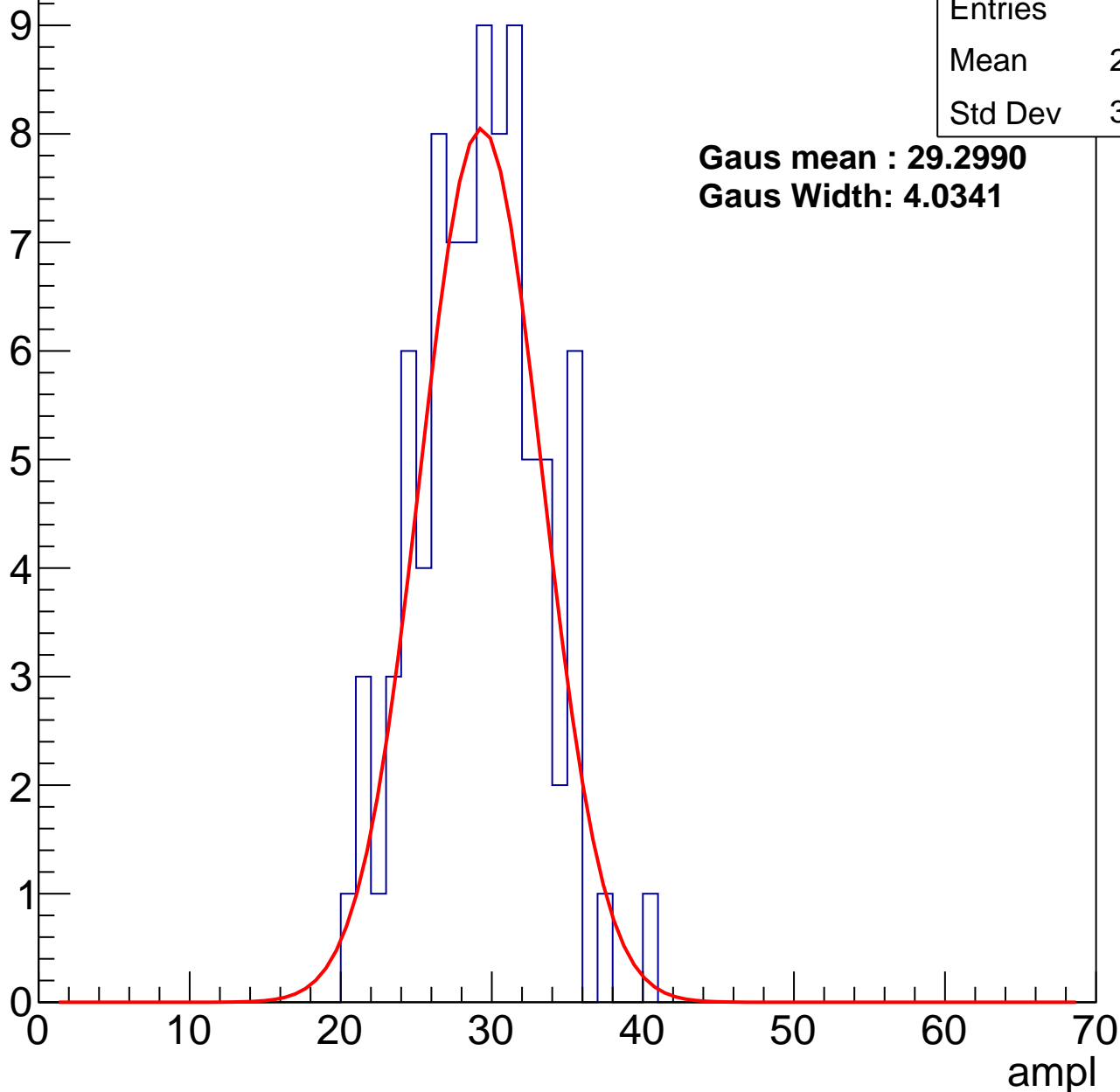
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	86
Mean	28.73
Std Dev	3.984

Gaus mean : 29.2990

Gaus Width: 4.0341



B0L000S, U2-ch60, adc1

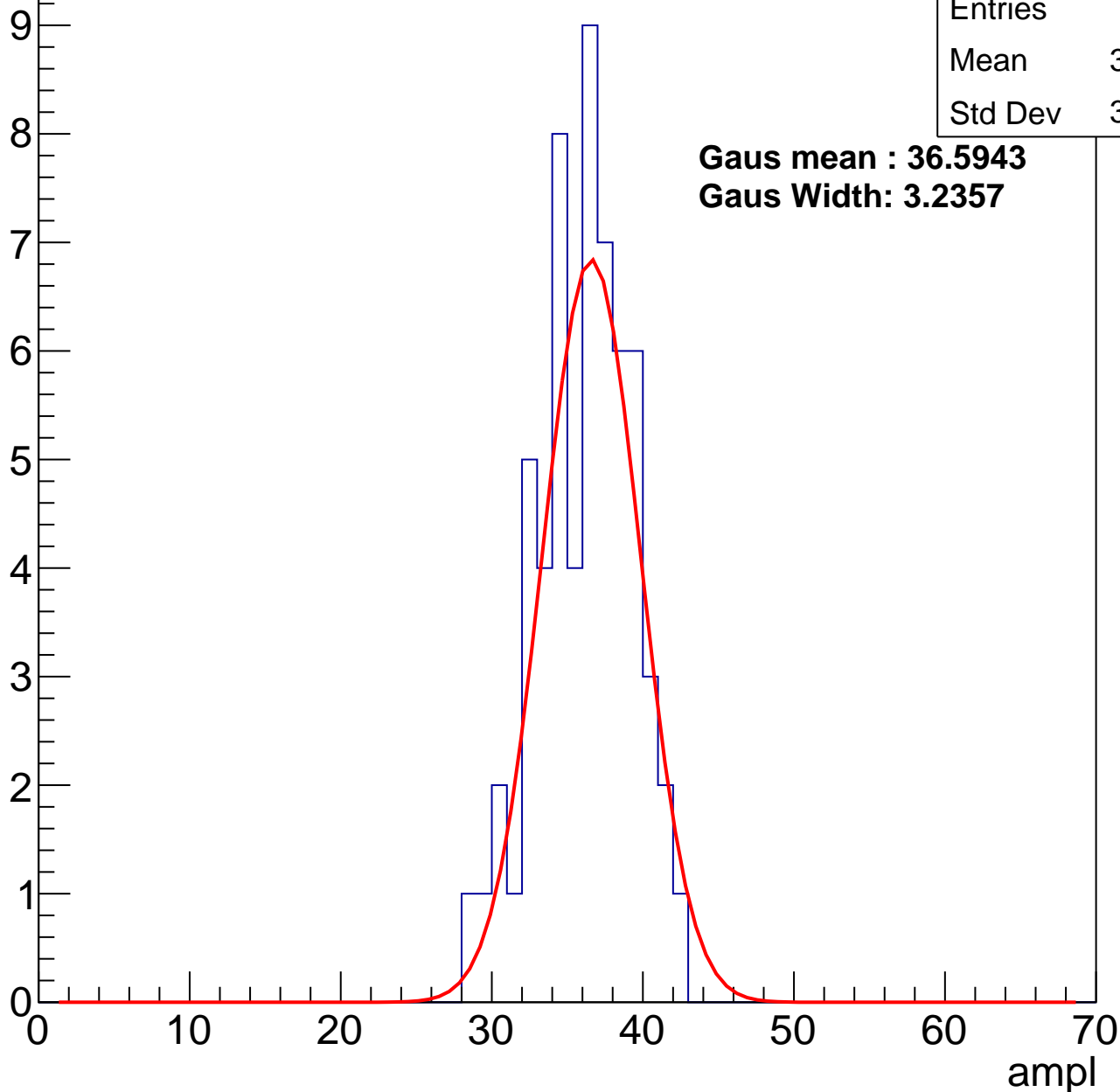
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	35.68
Std Dev	3.096

Gaus mean : 36.5943

Gaus Width: 3.2357



B0L000S, U2-ch60, adc2

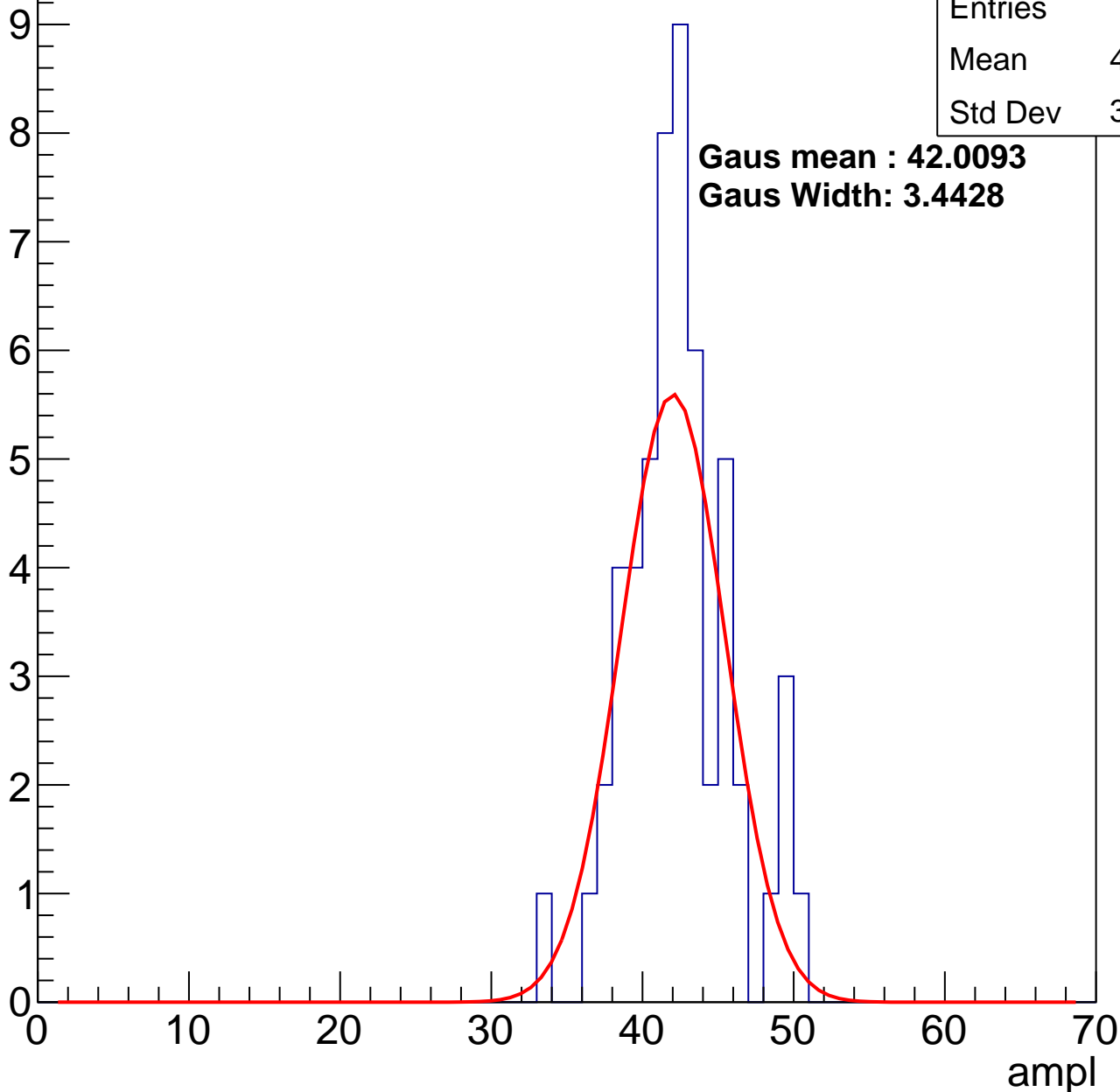
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	41.94
Std Dev	3.423

Gaus mean : 42.0093

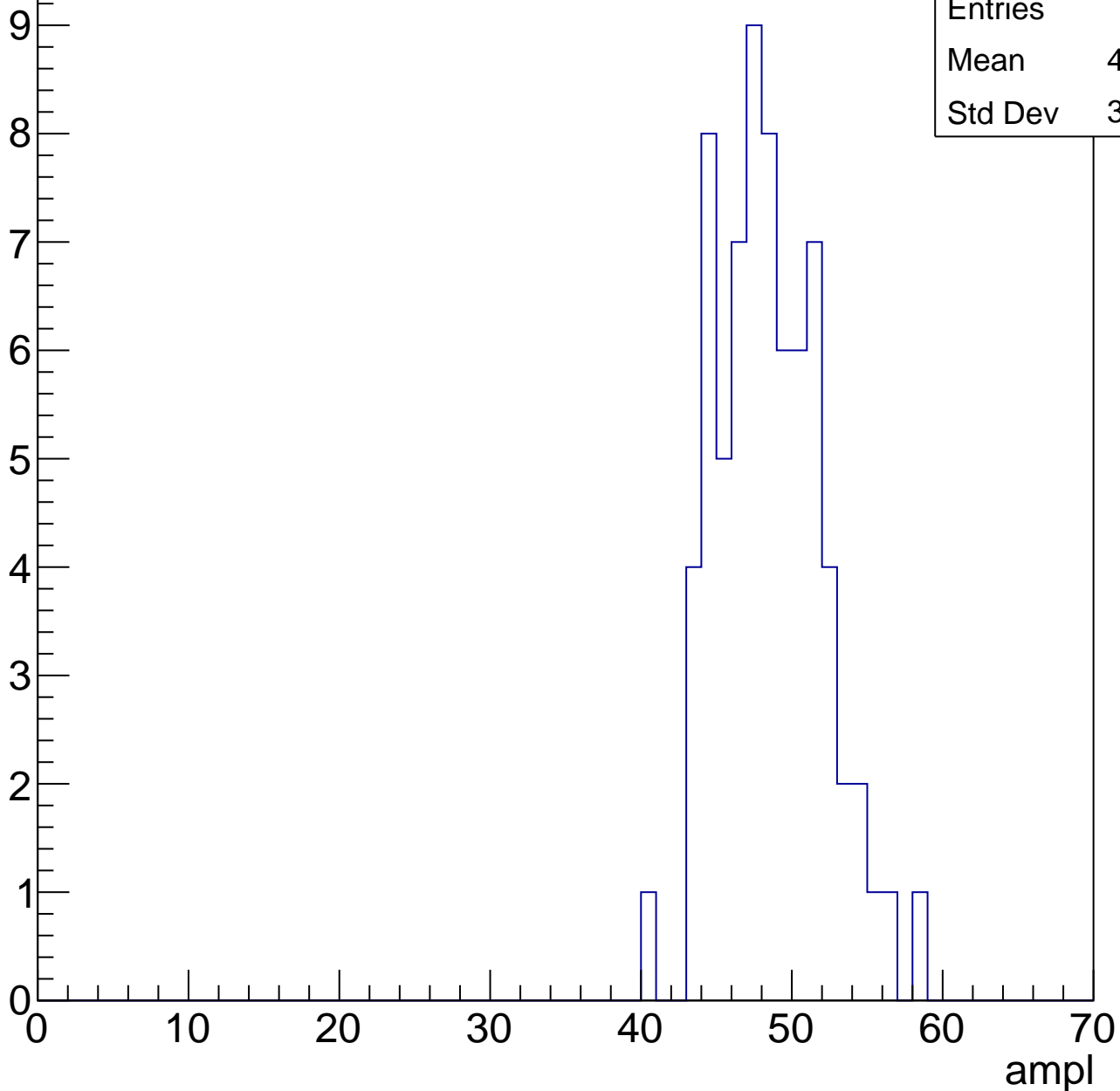
Gaus Width: 3.4428



B0L000S, U2-ch60, adc3

calib_packv5_042523_0143.root, FC#5, port B1

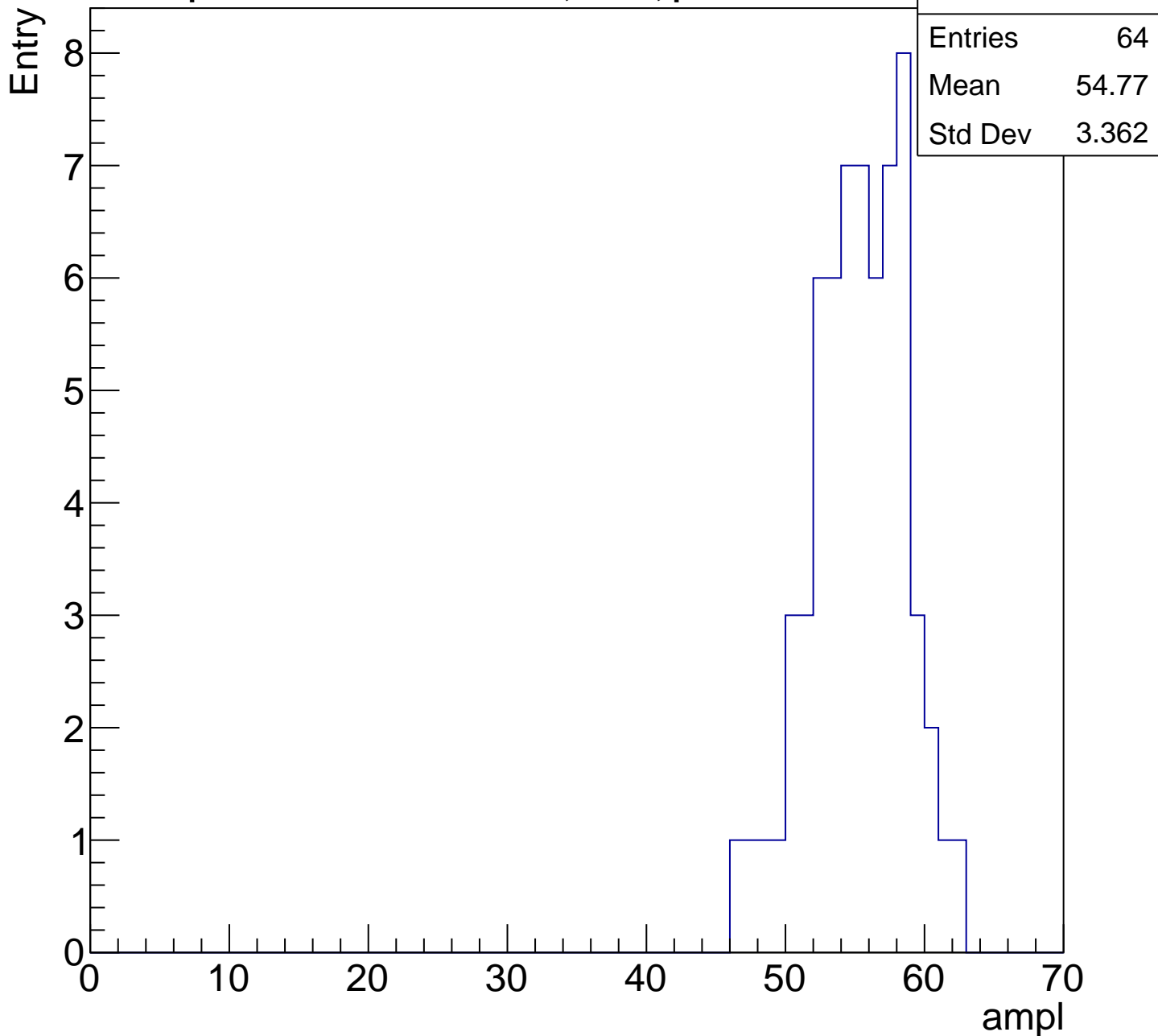
Entry



Entries	72
Mean	48.06
Std Dev	3.468

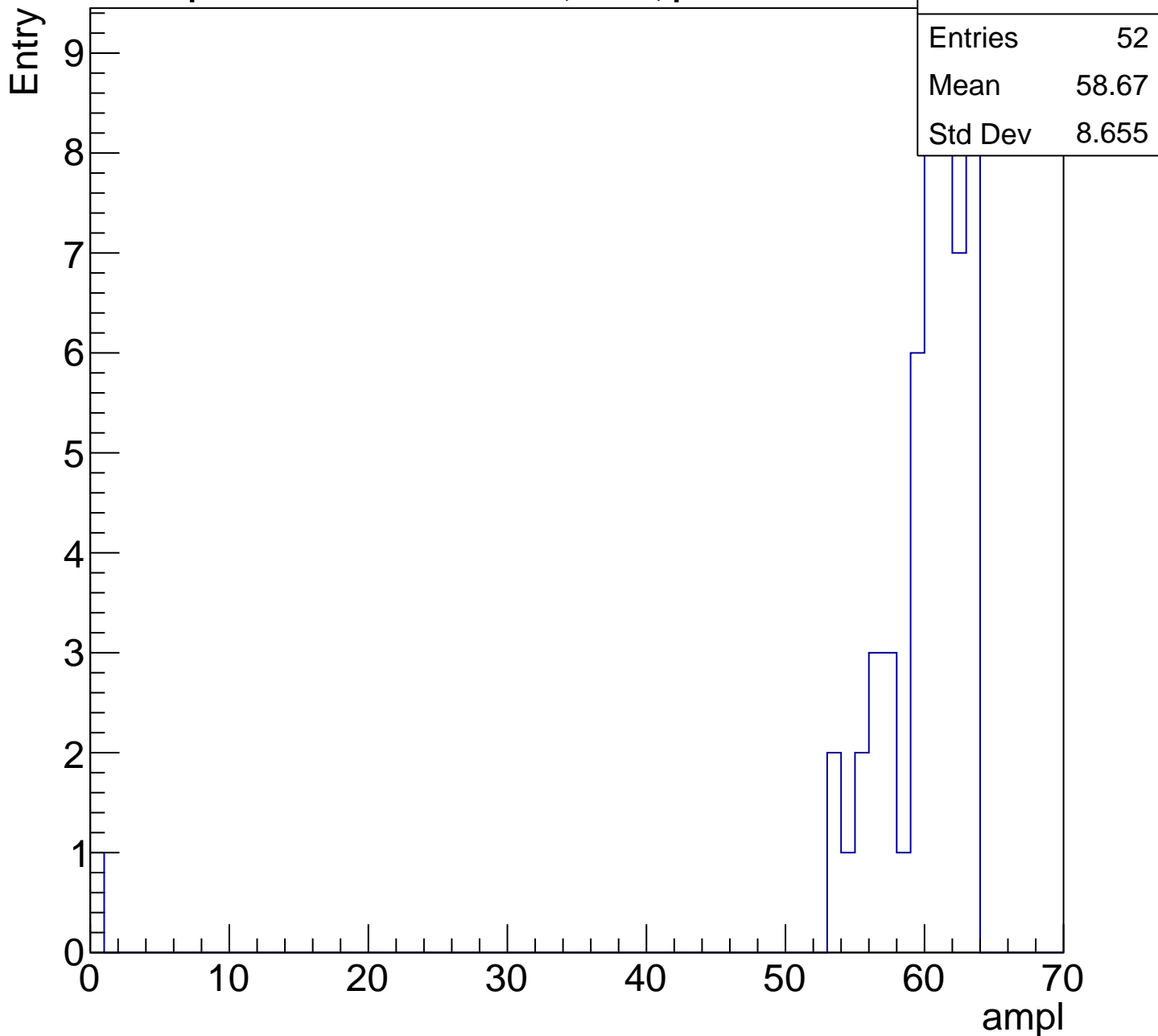
B0L000S, U2-ch60, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch60, adc5

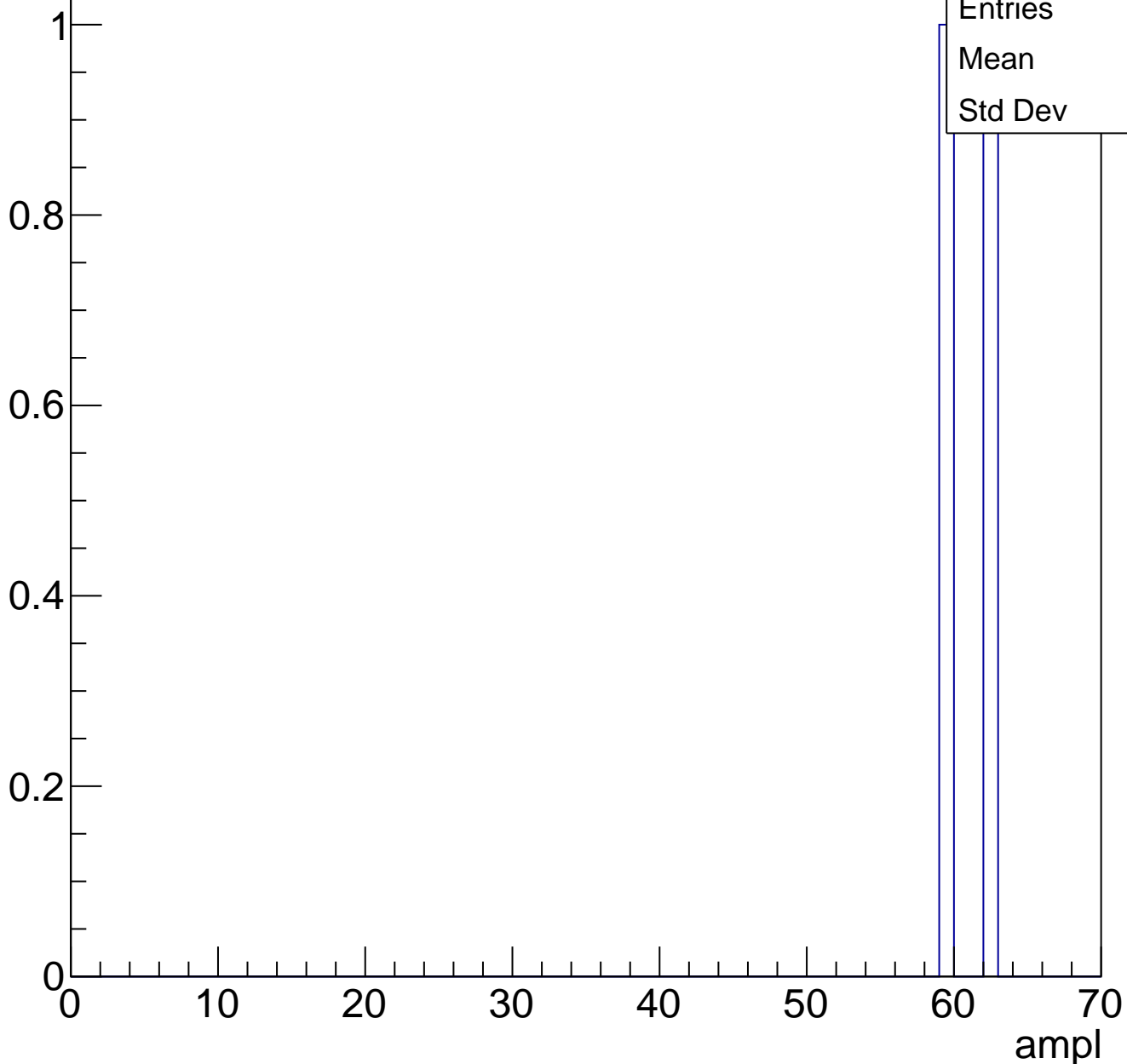
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch60, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch60, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch61, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	31.02
Std Dev	3.123

Gaus mean : 31.1885

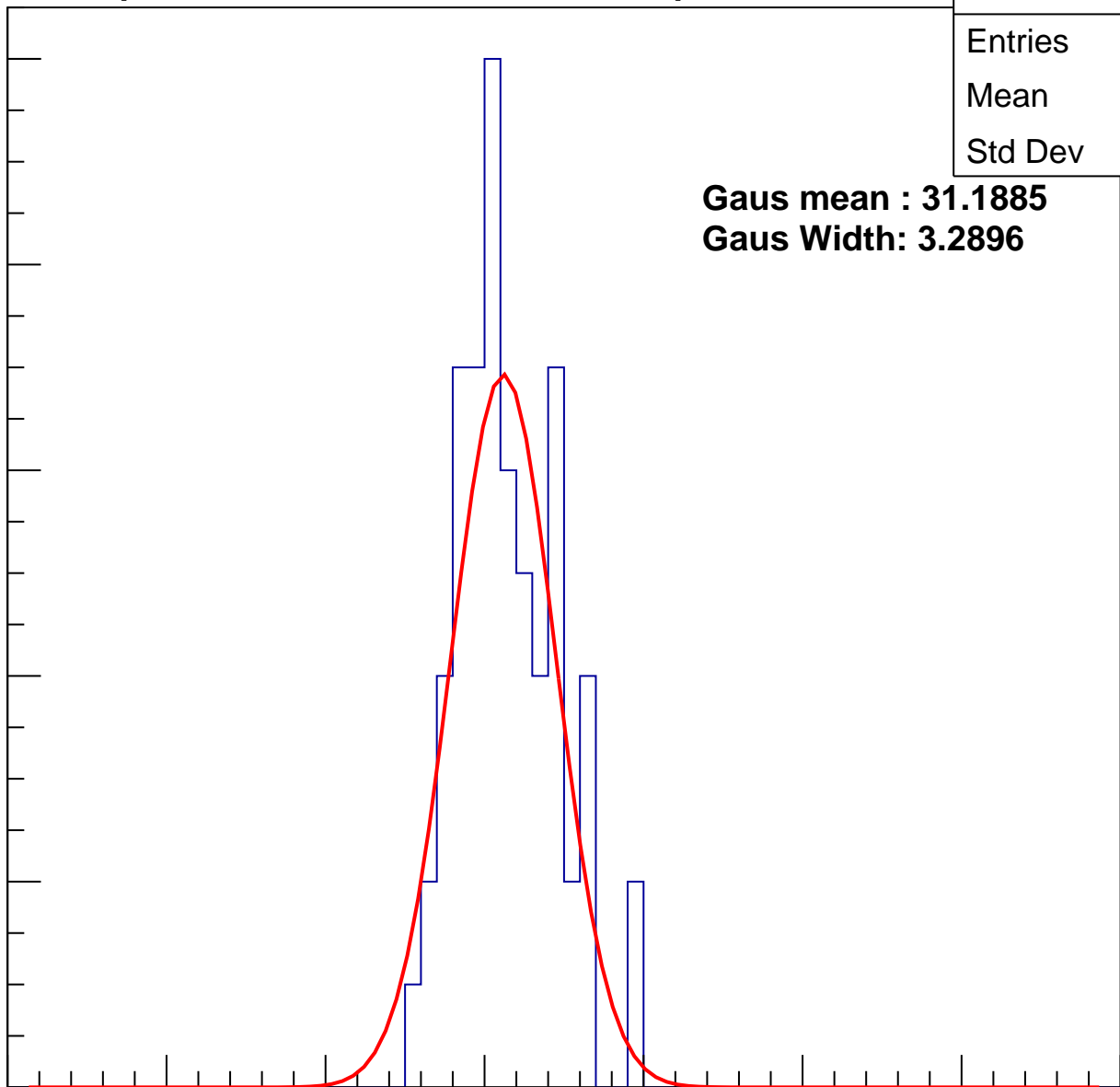
Gaus Width: 3.2896

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch61, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	36.68
Std Dev	3.459

Gaus mean : 36.9234

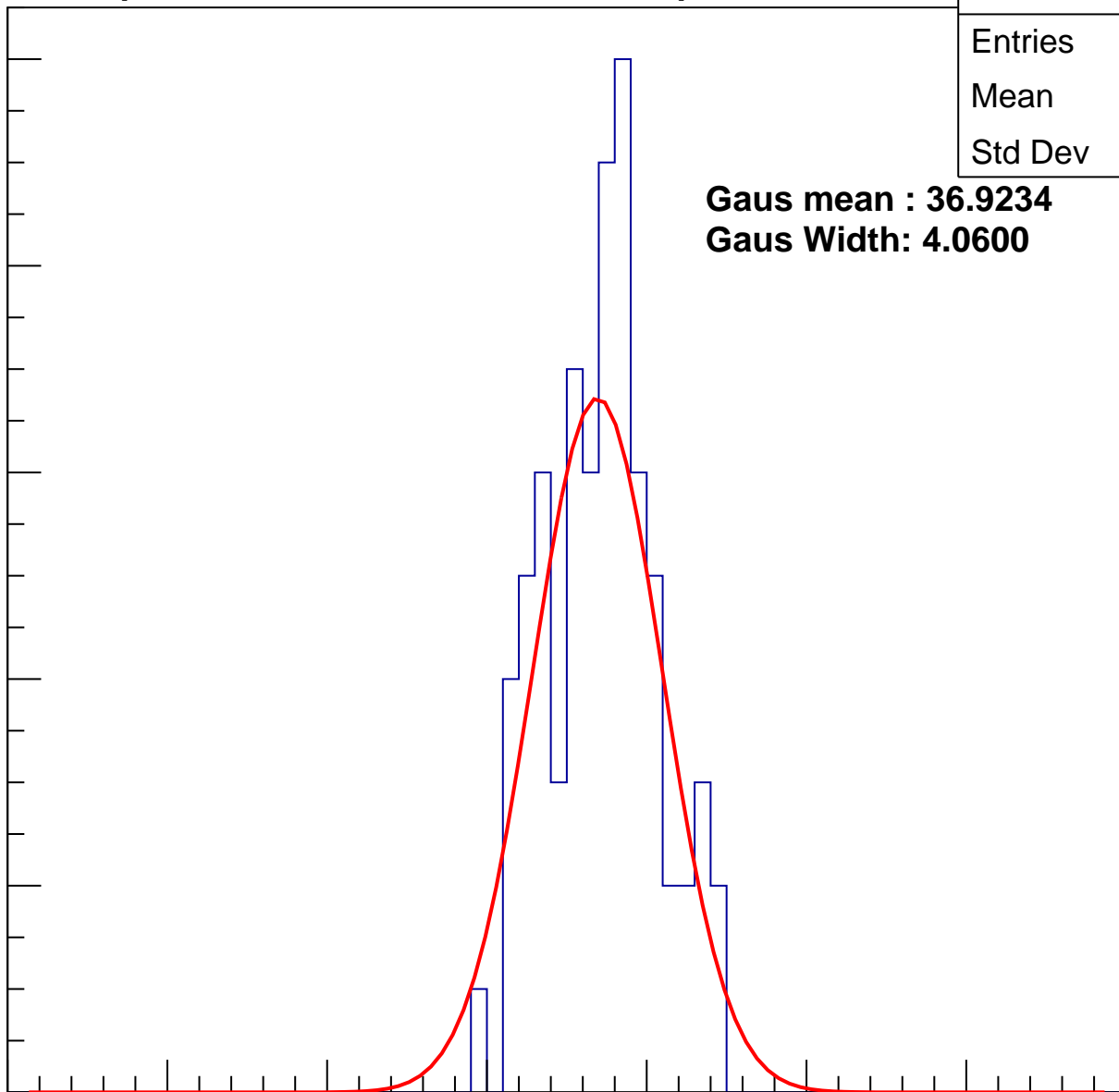
Gaus Width: 4.0600

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch61, adc2

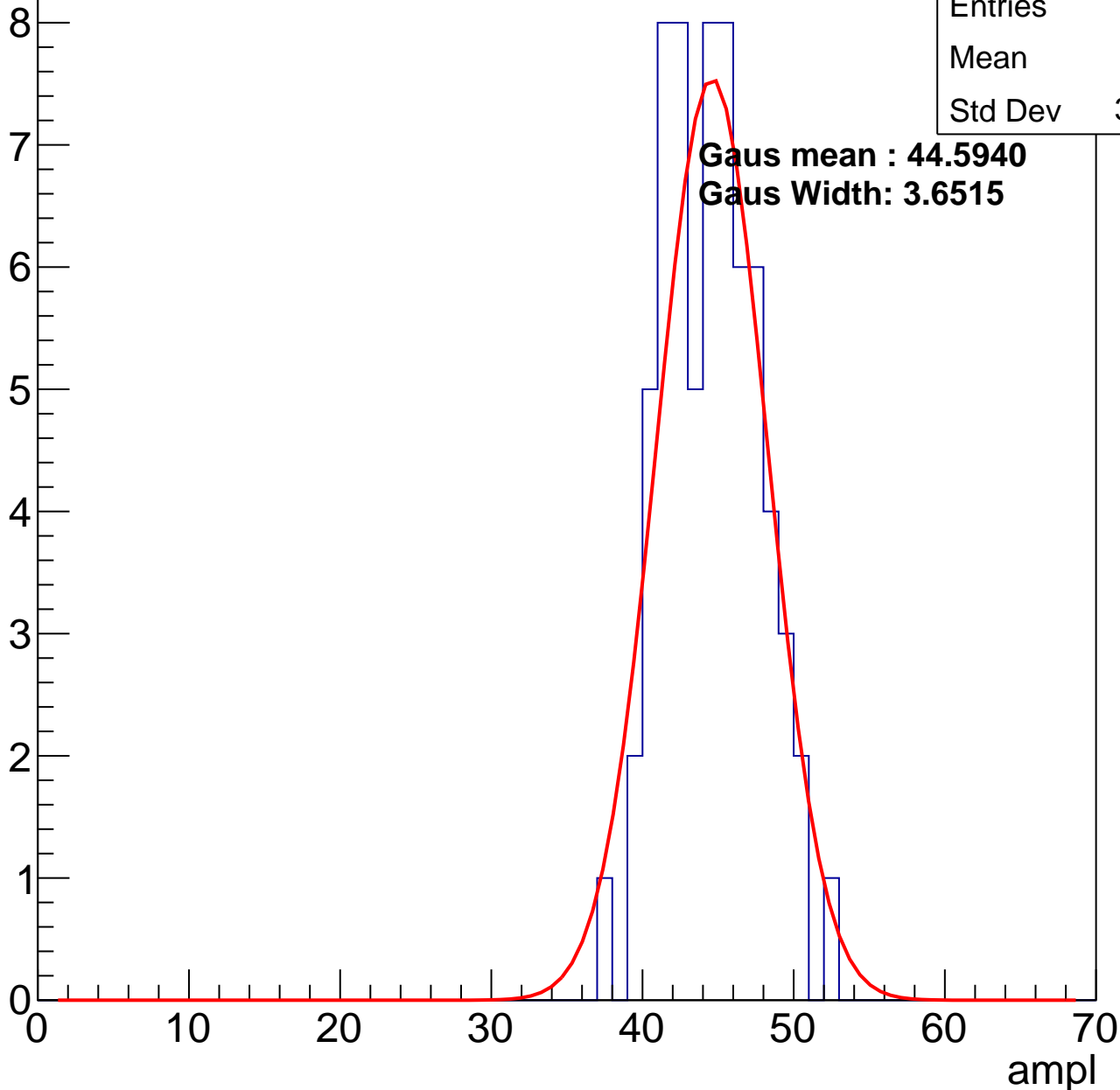
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	44.1
Std Dev	3.111

Gaus mean : 44.5940

Gaus Width: 3.6515

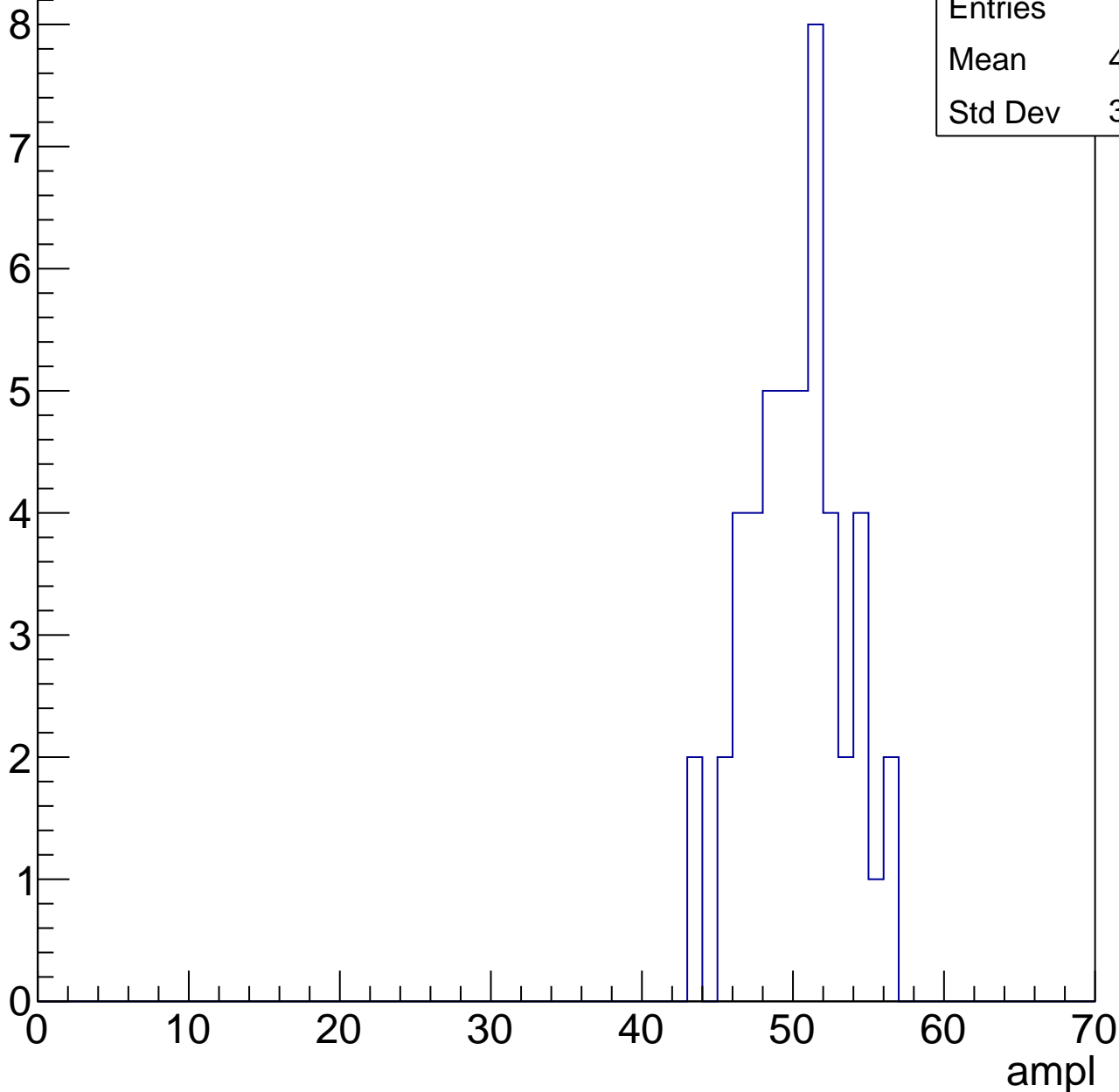


B0L000S, U2-ch61, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

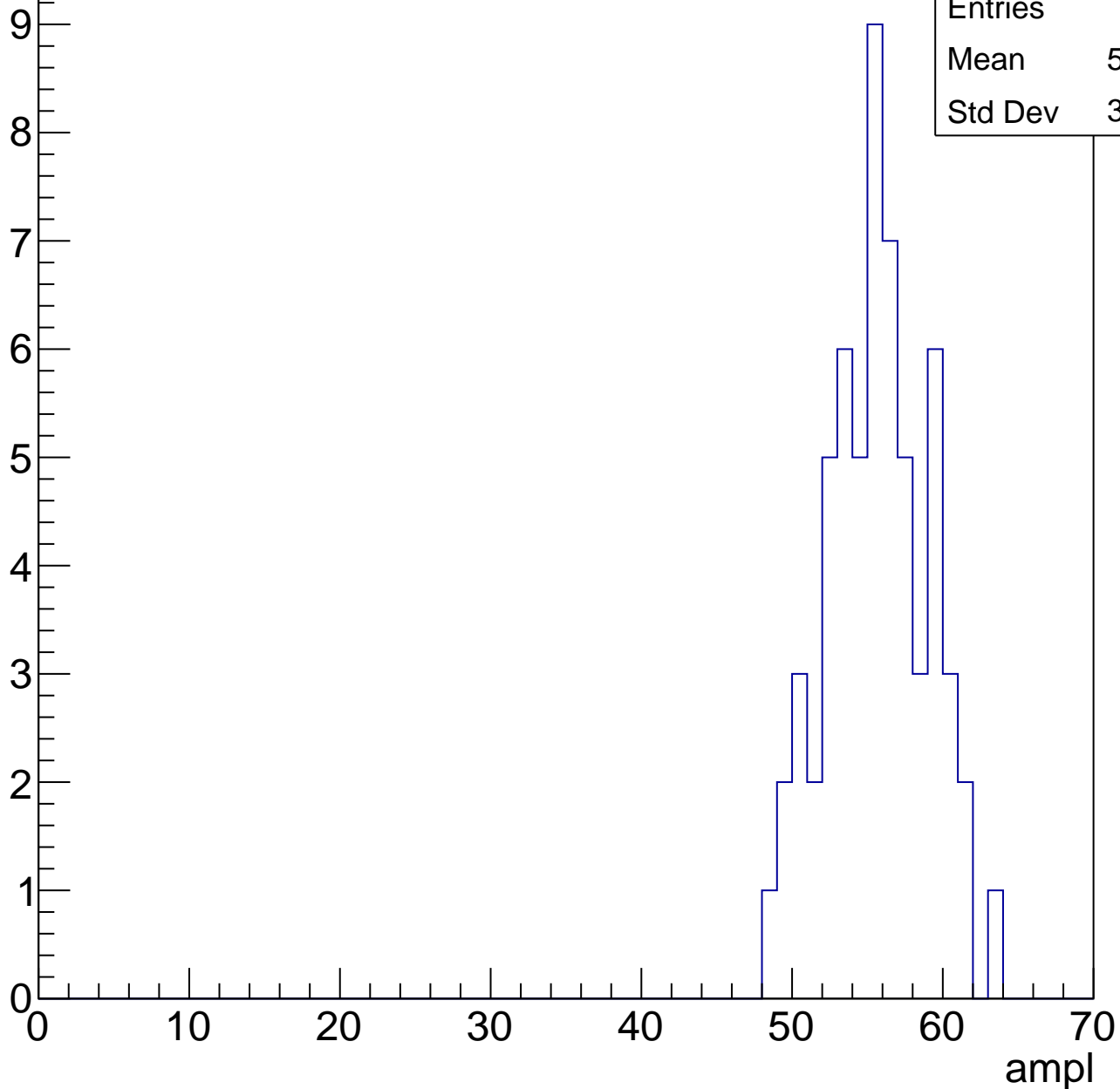
Entries	48
Mean	49.75
Std Dev	3.139



B0L000S, U2-ch61, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

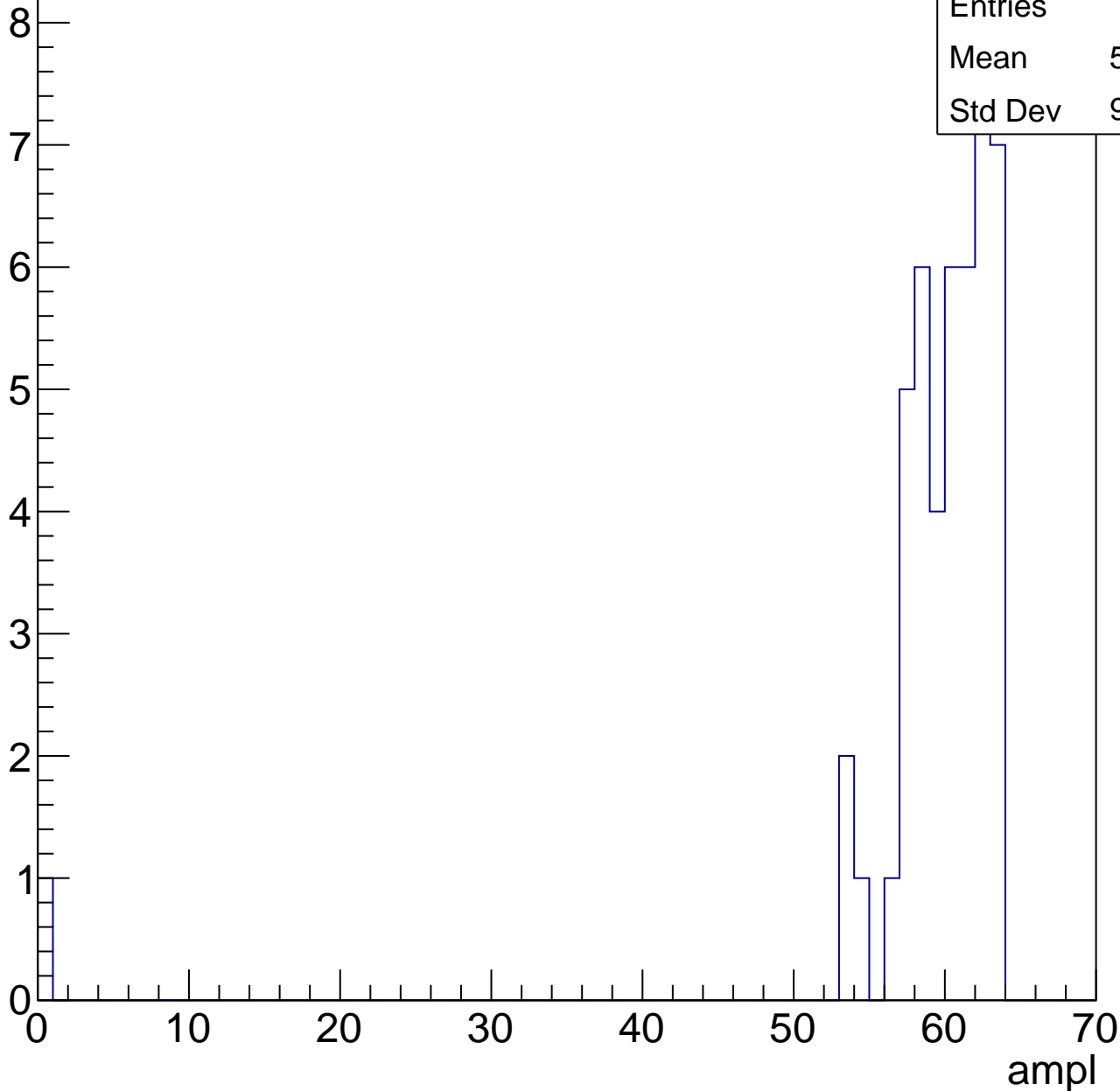


B0L000S, U2-ch61, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	58.47
Std Dev	9.008



B0L000S, U2-ch61, adc6

calib_packv5_042523_0143.root, FC#5, port B1

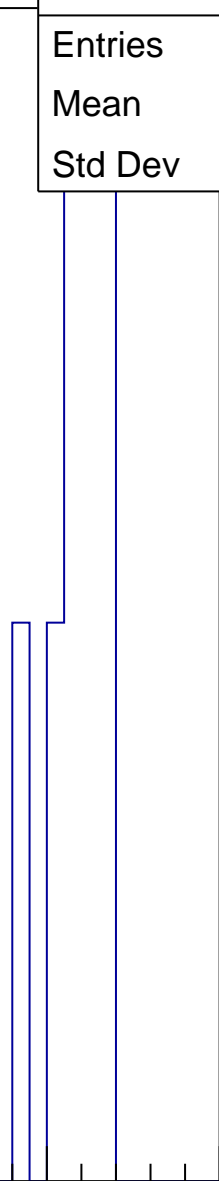
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.25
Std Dev	1.561

0 10 20 30 40 50 60 70

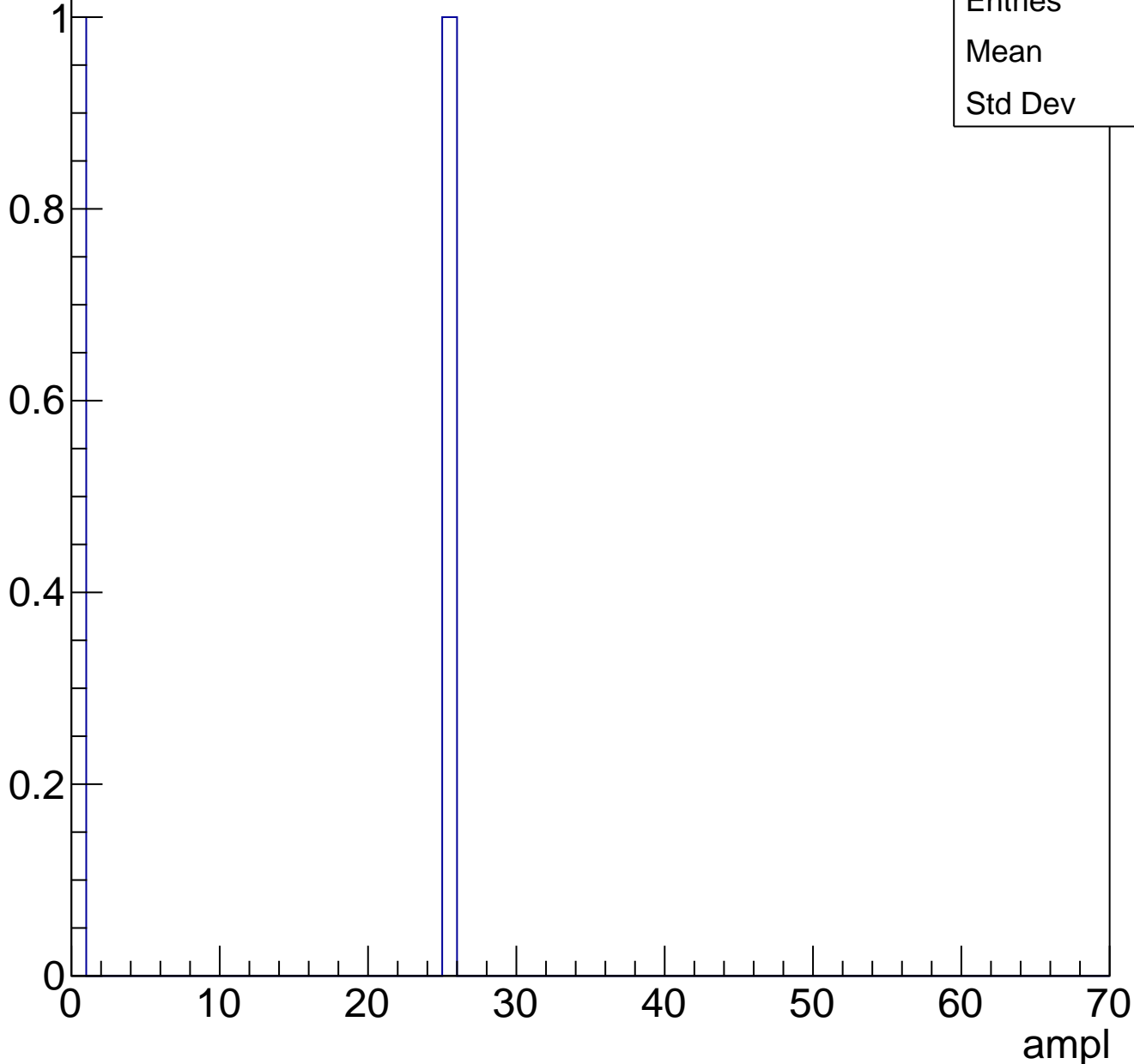
ampl



B0L000S, U2-ch61, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L000S, U2-ch62, adc0

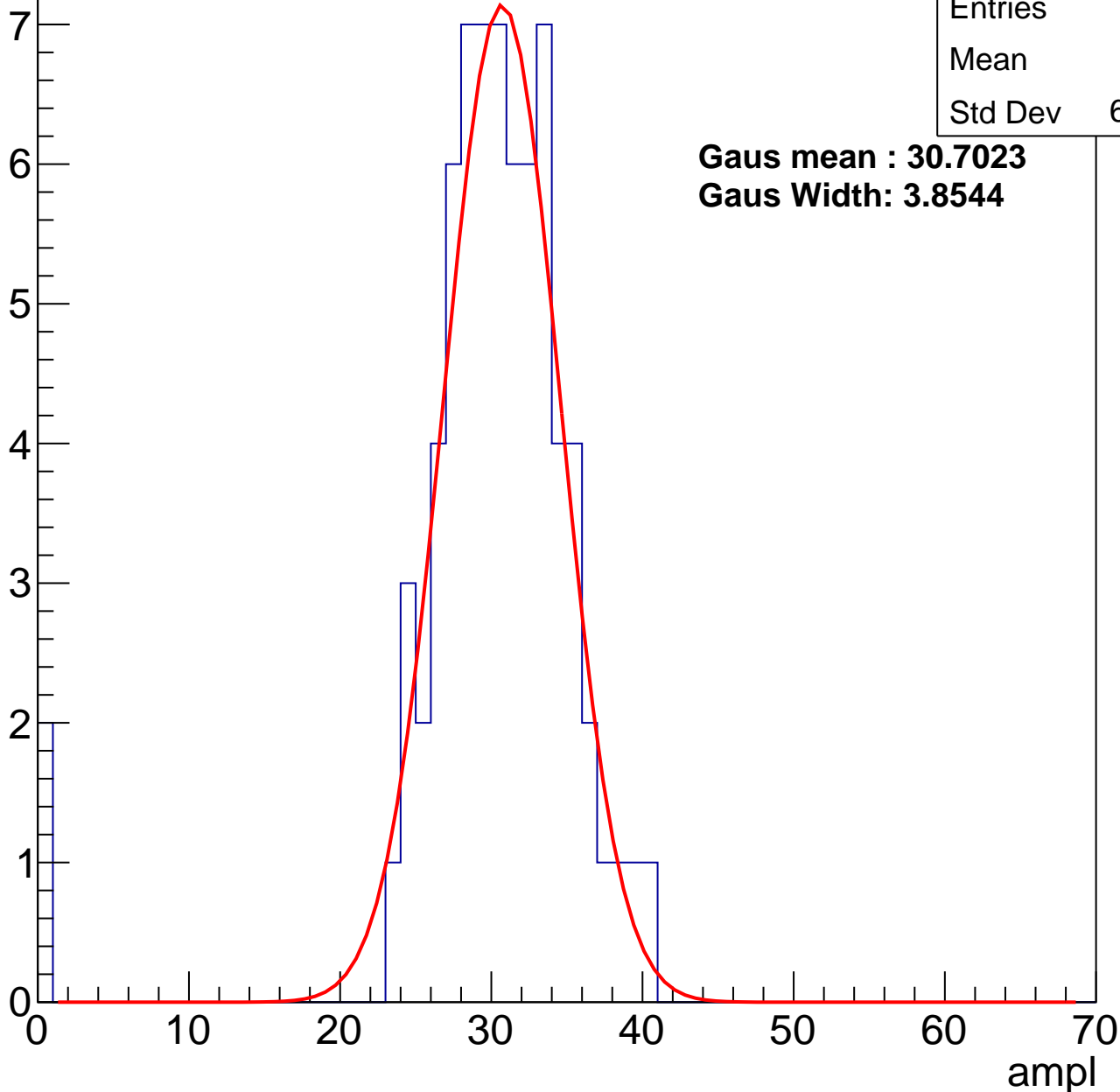
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	29.6
Std Dev	6.204

Gaus mean : 30.7023

Gaus Width: 3.8544



B0L000S, U2-ch62, adc1

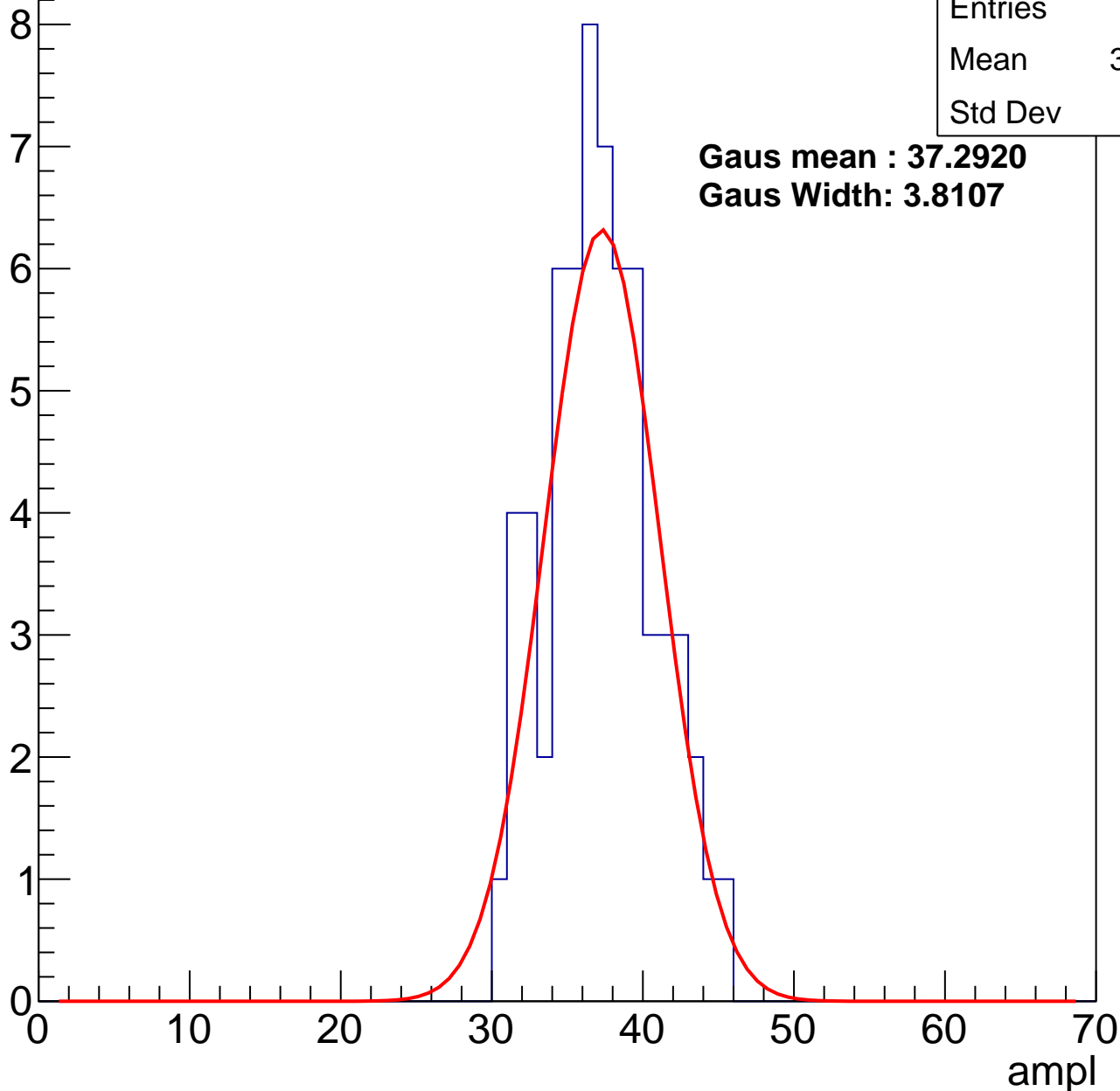
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.75
Std Dev	3.5

Gaus mean : 37.2920

Gaus Width: 3.8107

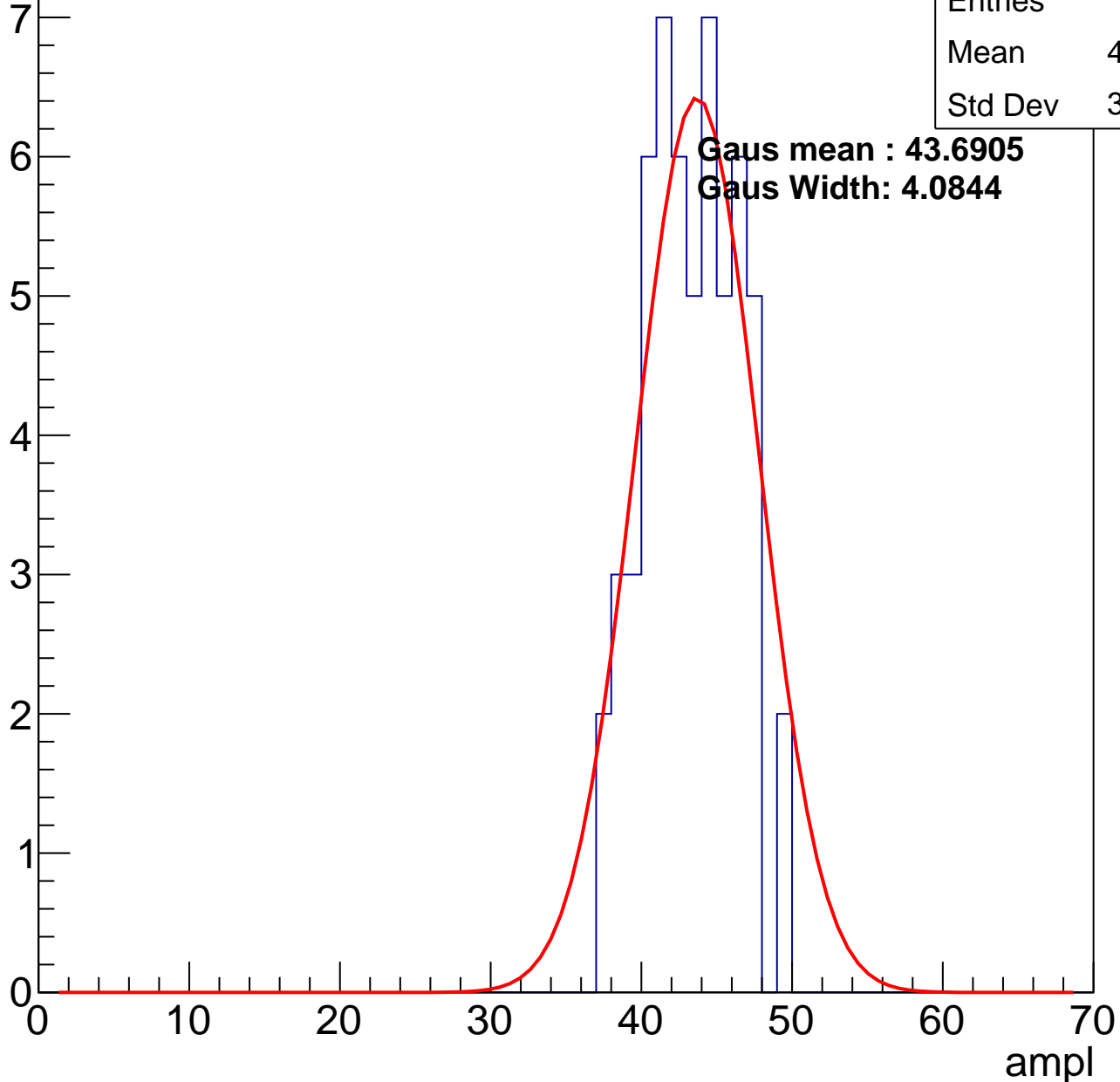


B0L000S, U2-ch62, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

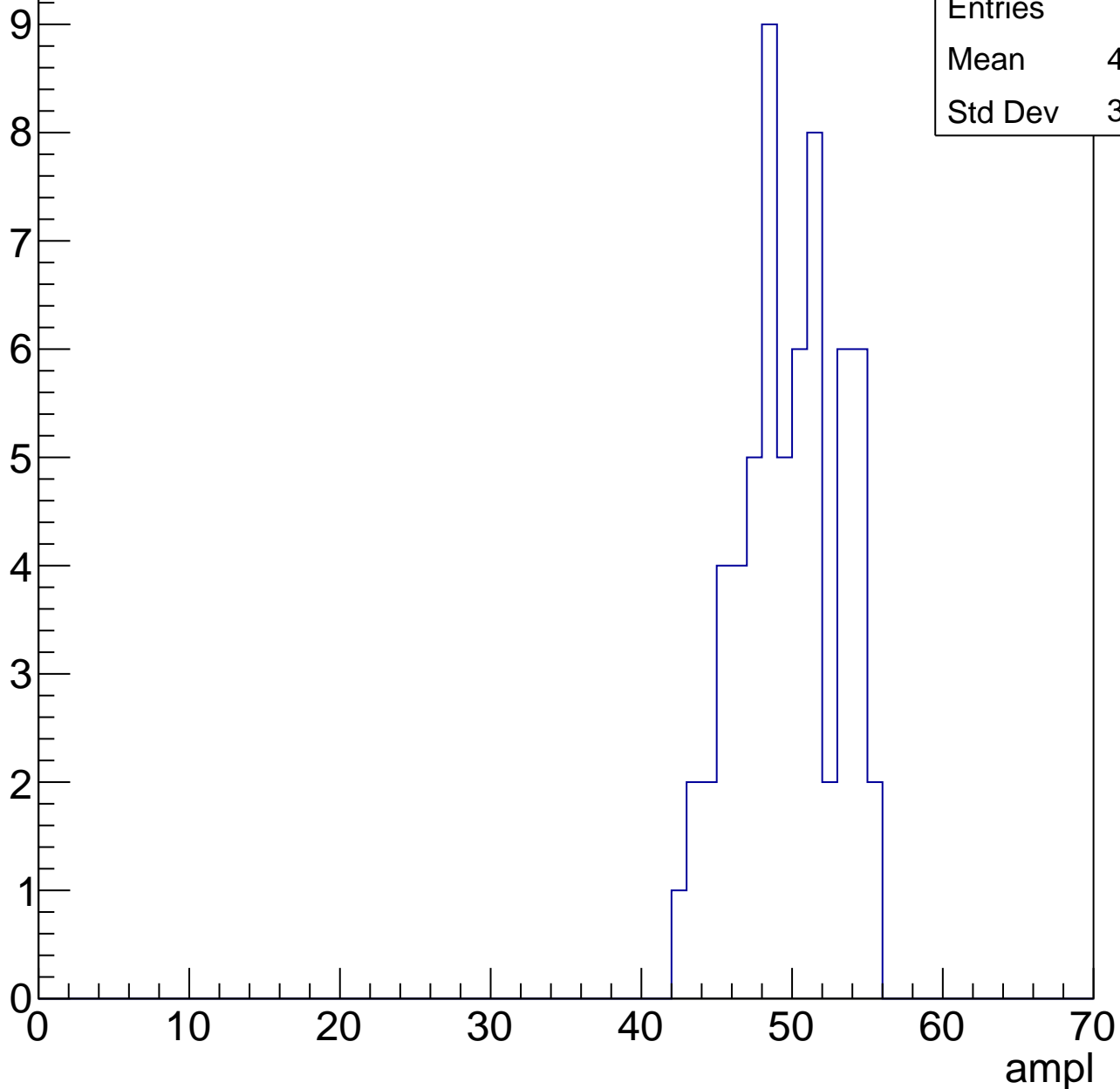
Entries	57
Mean	42.82
Std Dev	3.004



B0L000S, U2-ch62, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

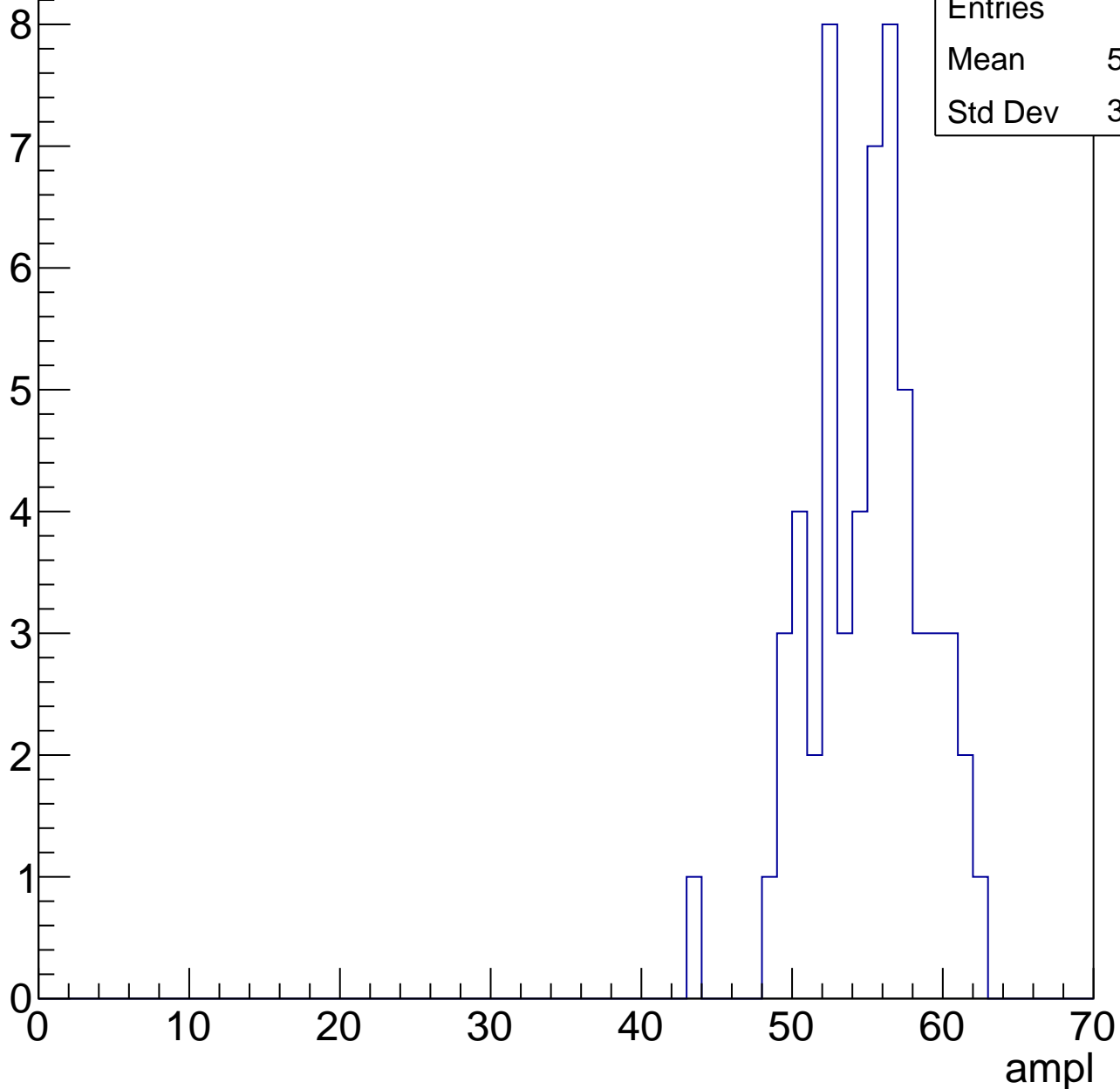


B0L000S, U2-ch62, adc4

calib_packv5_042523_0143.root, FC#5, port B1

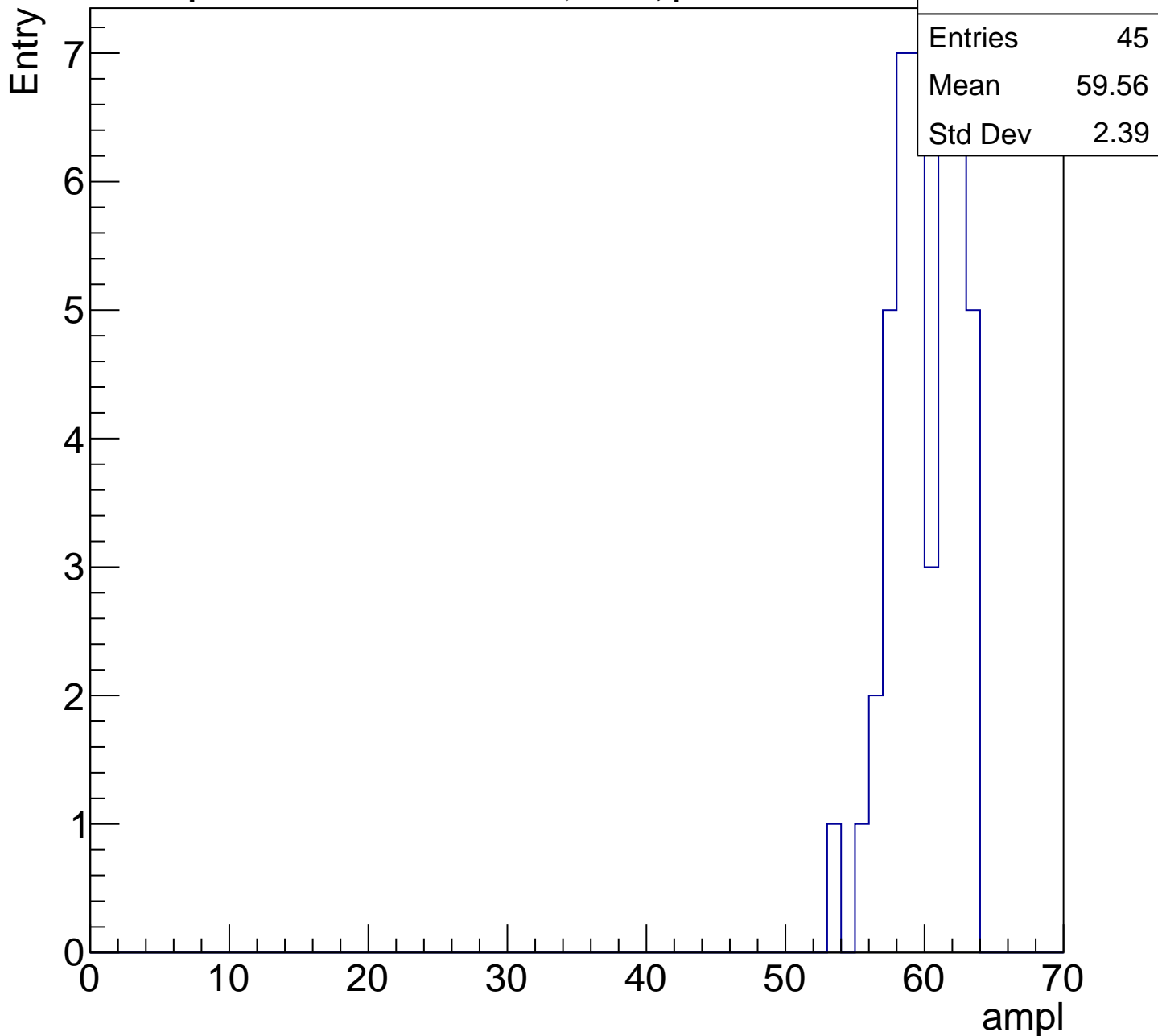
Entry

Entries	58
Mean	54.55
Std Dev	3.733



B0L000S, U2-ch62, adc5

calib_packv5_042523_0143.root, FC#5, port B1

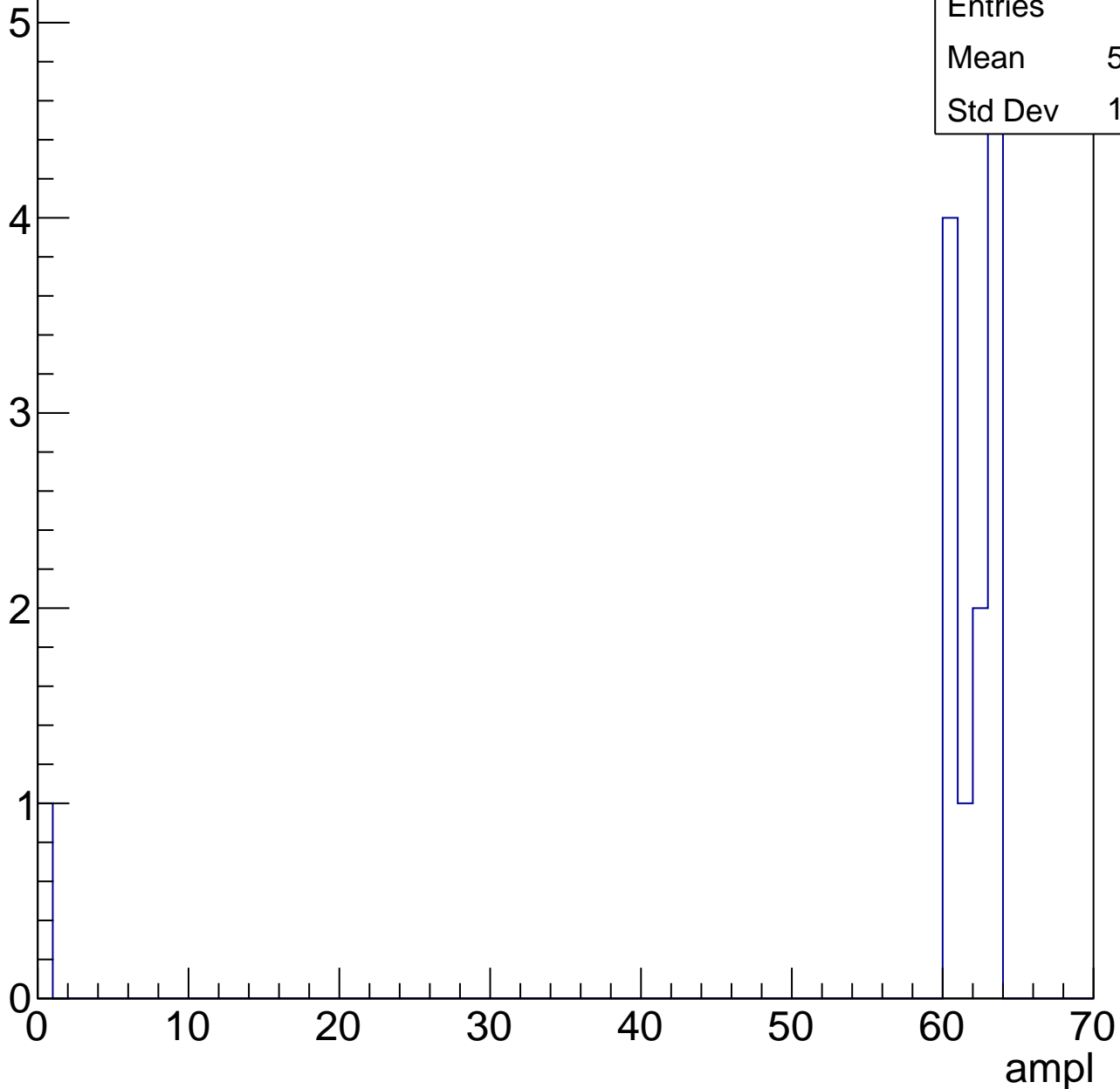


B0L000S, U2-ch62, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	13
Mean	56.92
Std Dev	16.48



B0L000S, U2-ch62, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L000S, U2-ch63, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	69
Mean	29.62
Std Dev	3.293

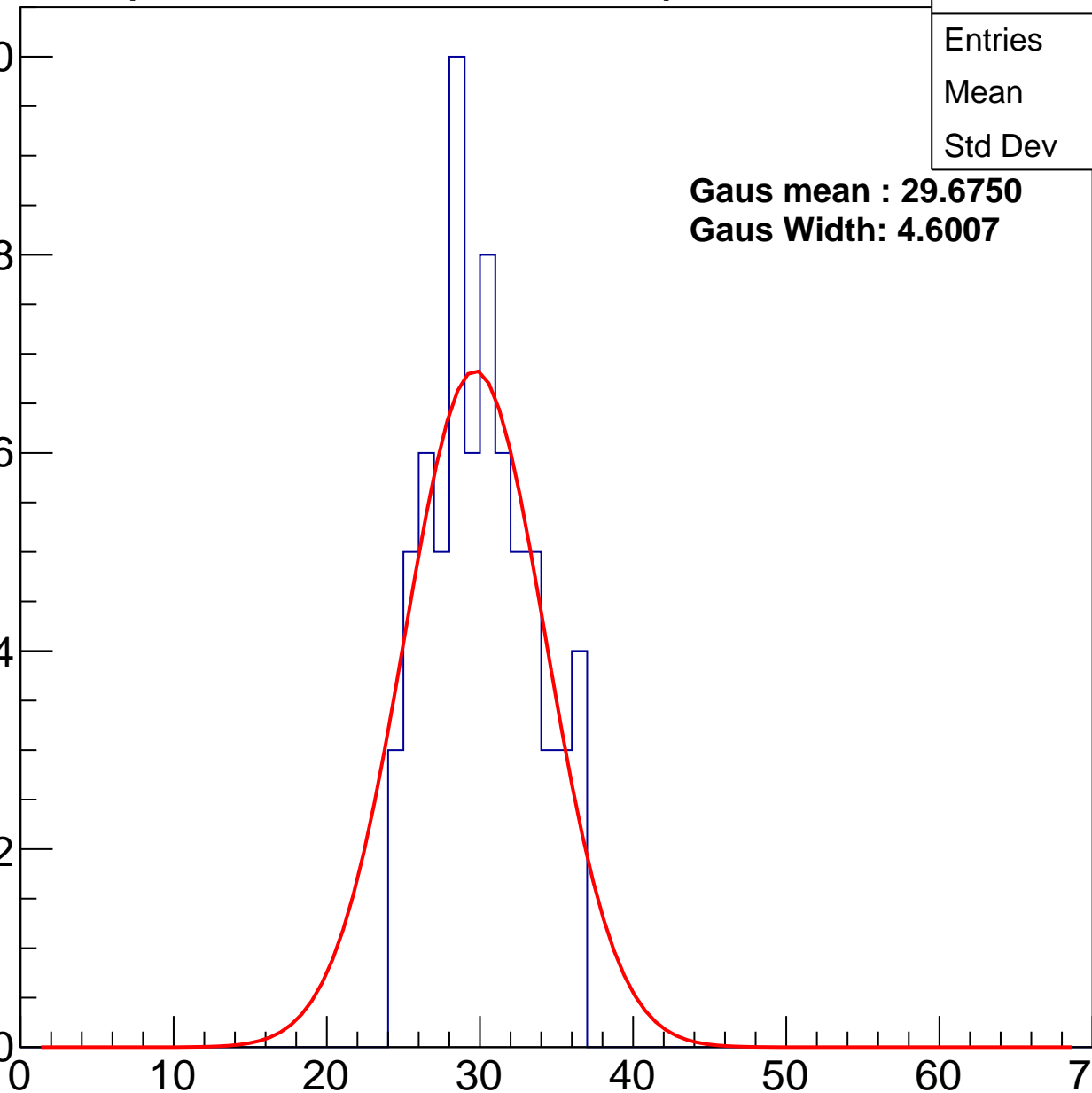
Gaus mean : 29.6750

Gaus Width: 4.6007

Entry

10
8
6
4
2
0

ampl



B0L000S, U2-ch63, adc1

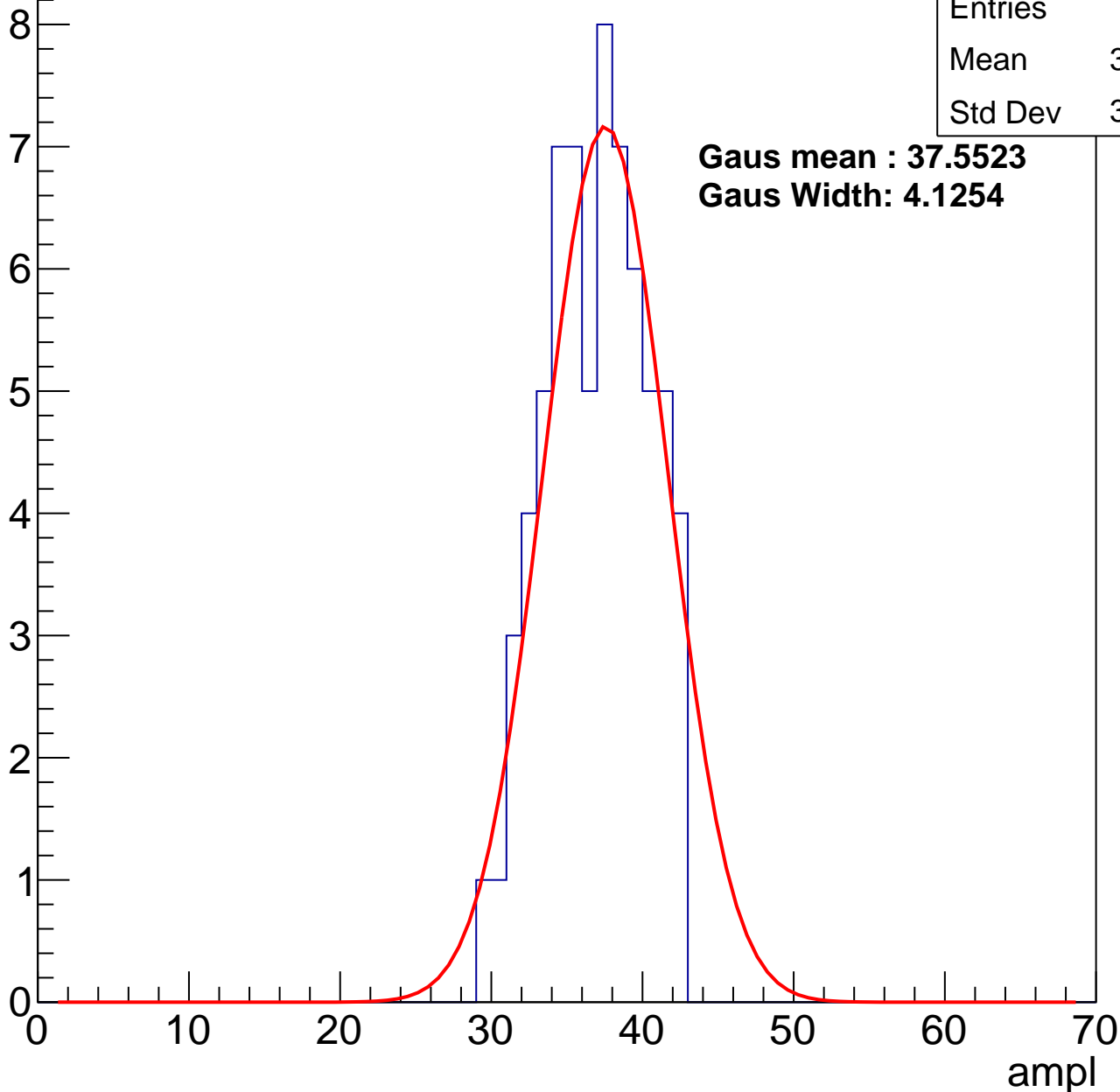
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.43
Std Dev	3.278

Gaus mean : 37.5523

Gaus Width: 4.1254



B0L000S, U2-ch63, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	76
Mean	43.74
Std Dev	3.439

Gaus mean : 44.1975

Gaus Width: 3.6387

Entry

10

8

6

4

2

0

0

10

20

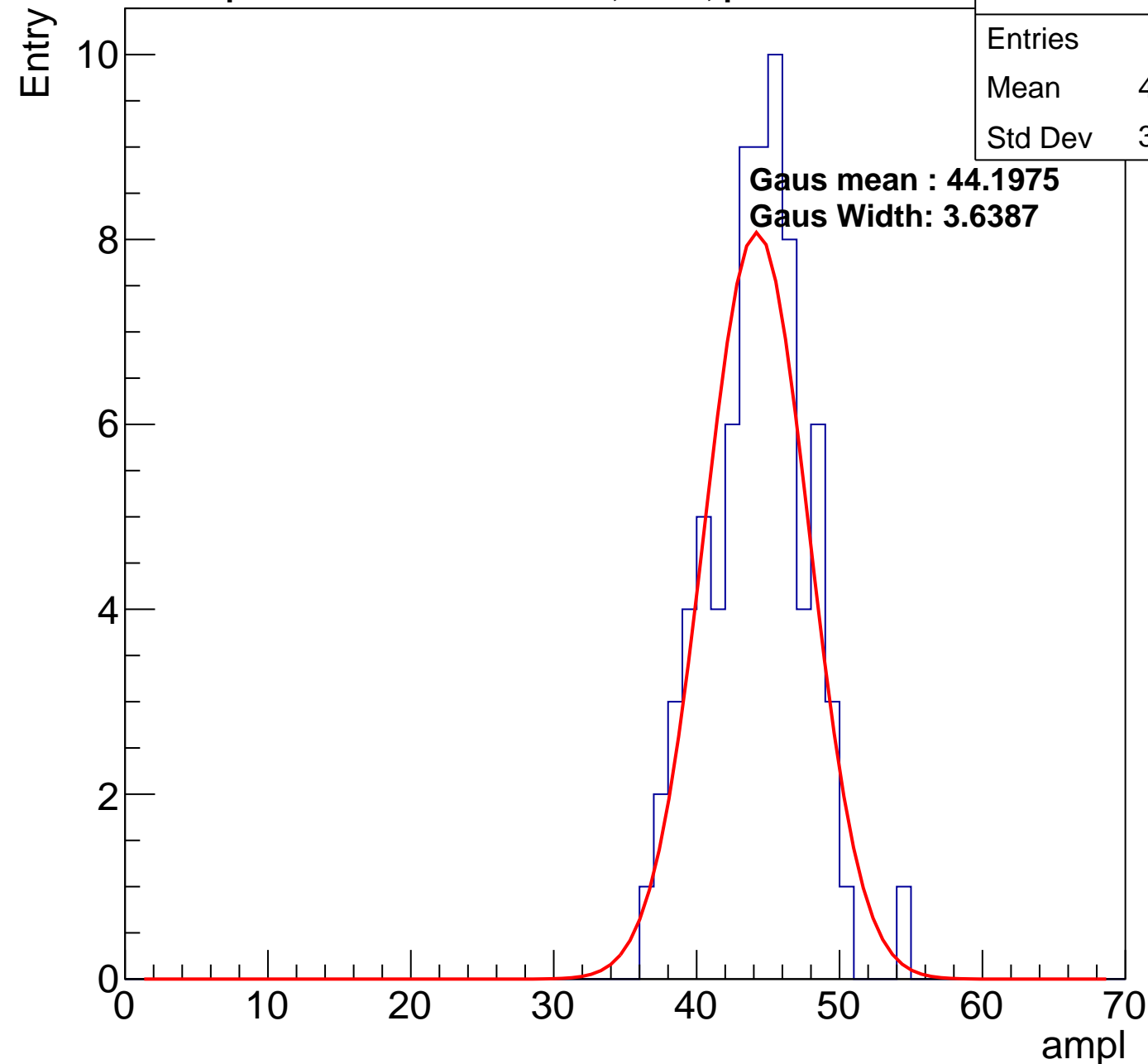
30

40

50

60

ampl

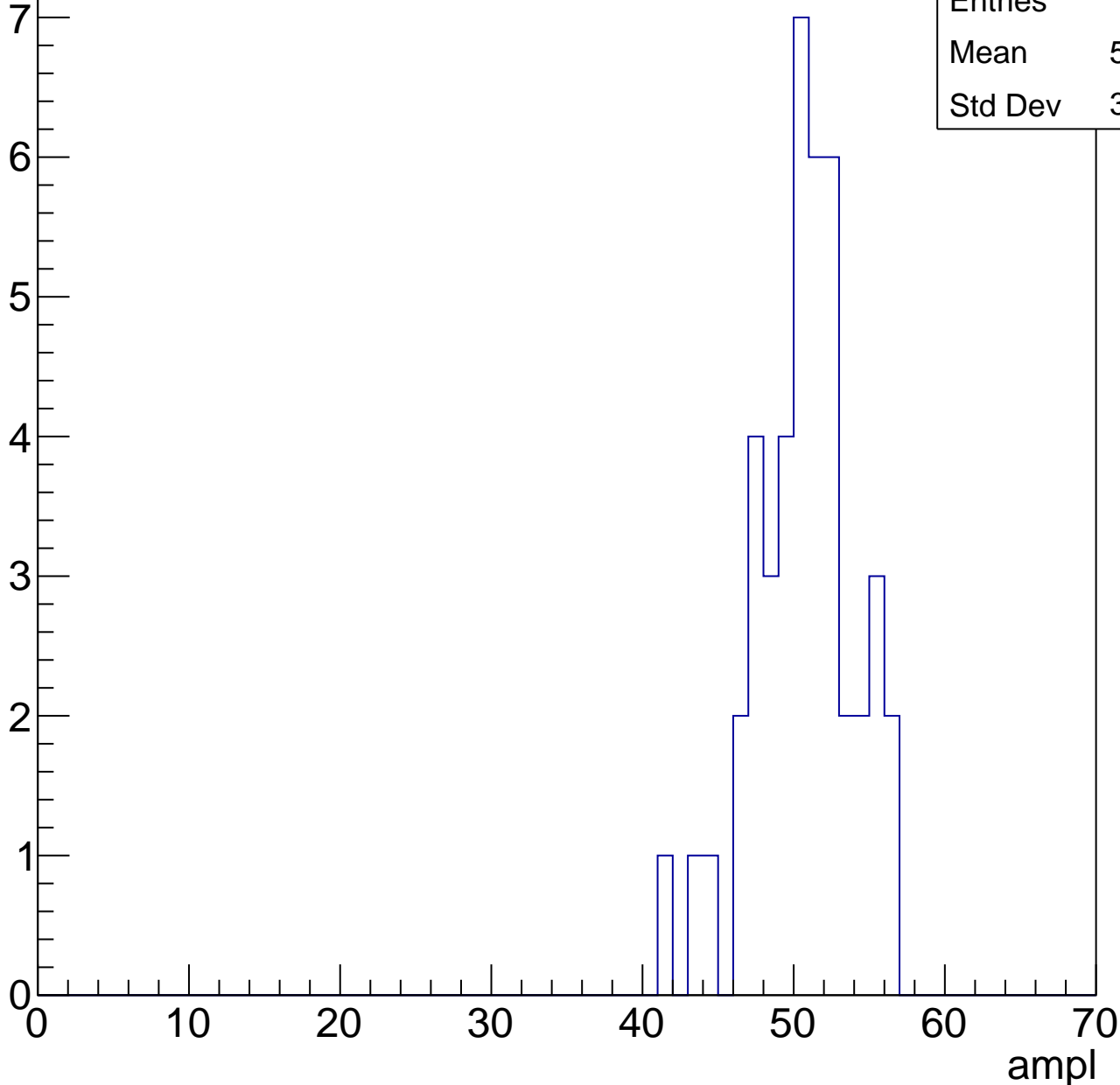


B0L000S, U2-ch63, adc3

calib_packv5_042523_0143.root, FC#5, port B1

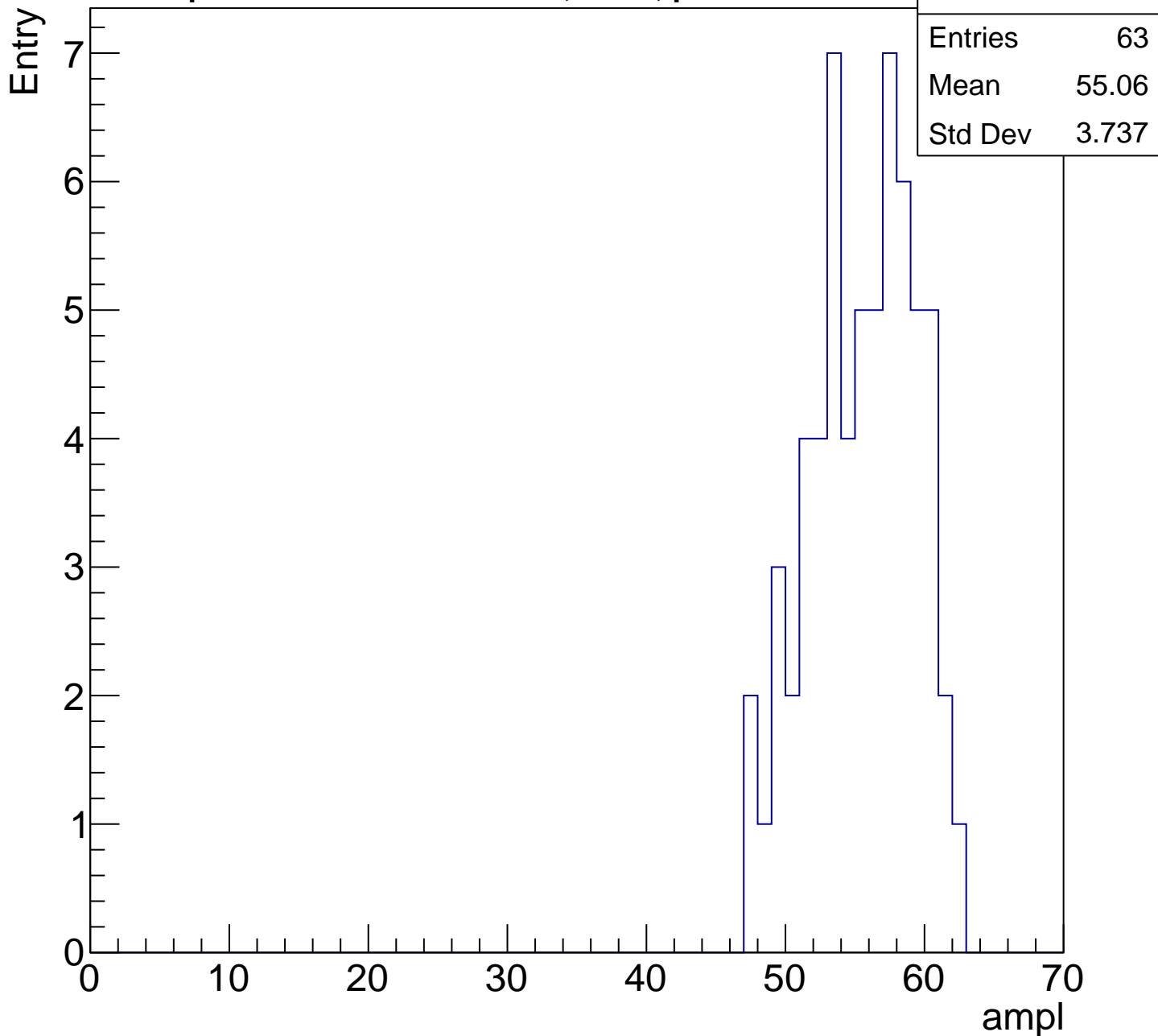
Entry

Entries	44
Mean	50.16
Std Dev	3.289



B0L000S, U2-ch63, adc4

calib_packv5_042523_0143.root, FC#5, port B1

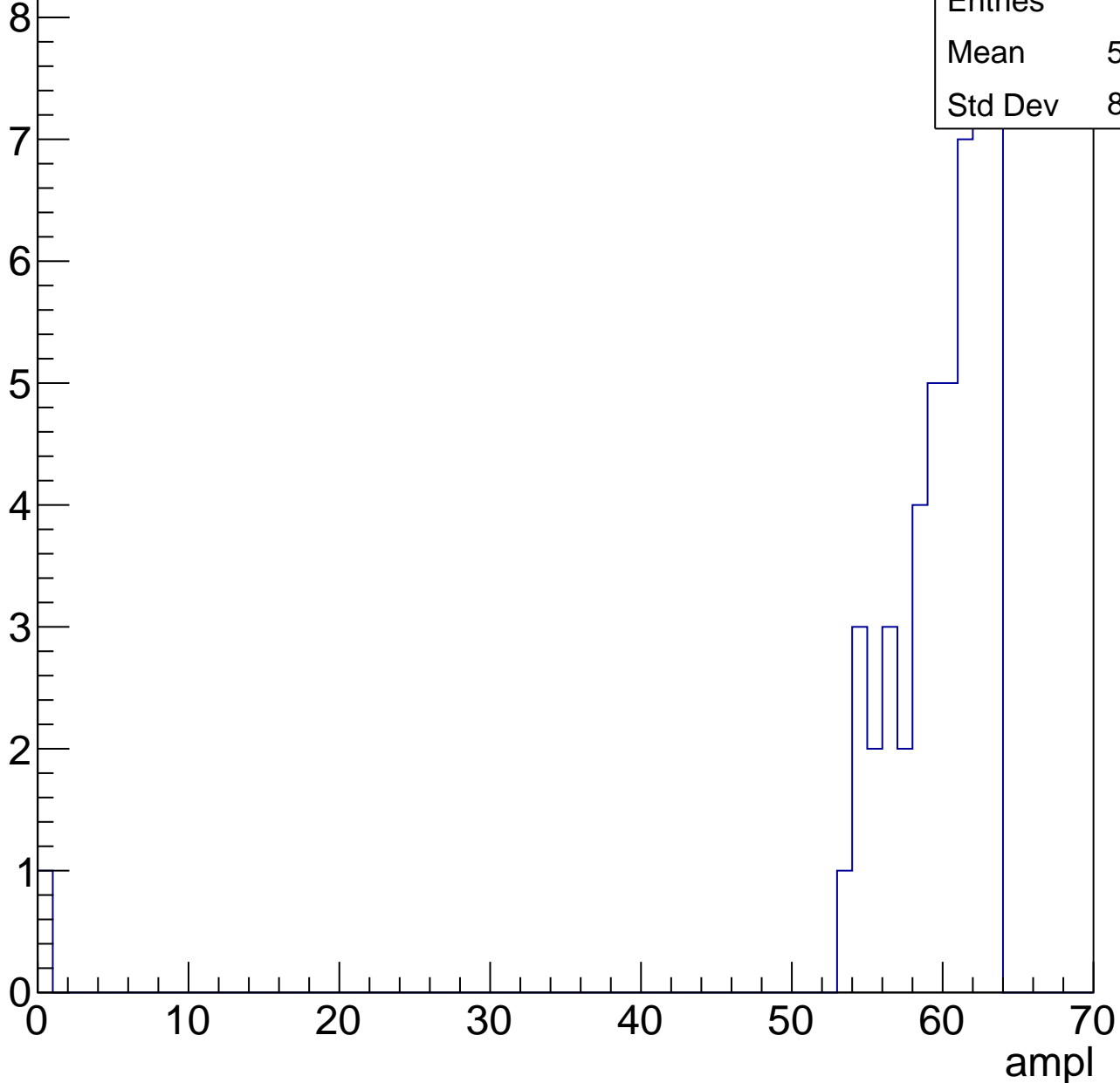


B0L000S, U2-ch63, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

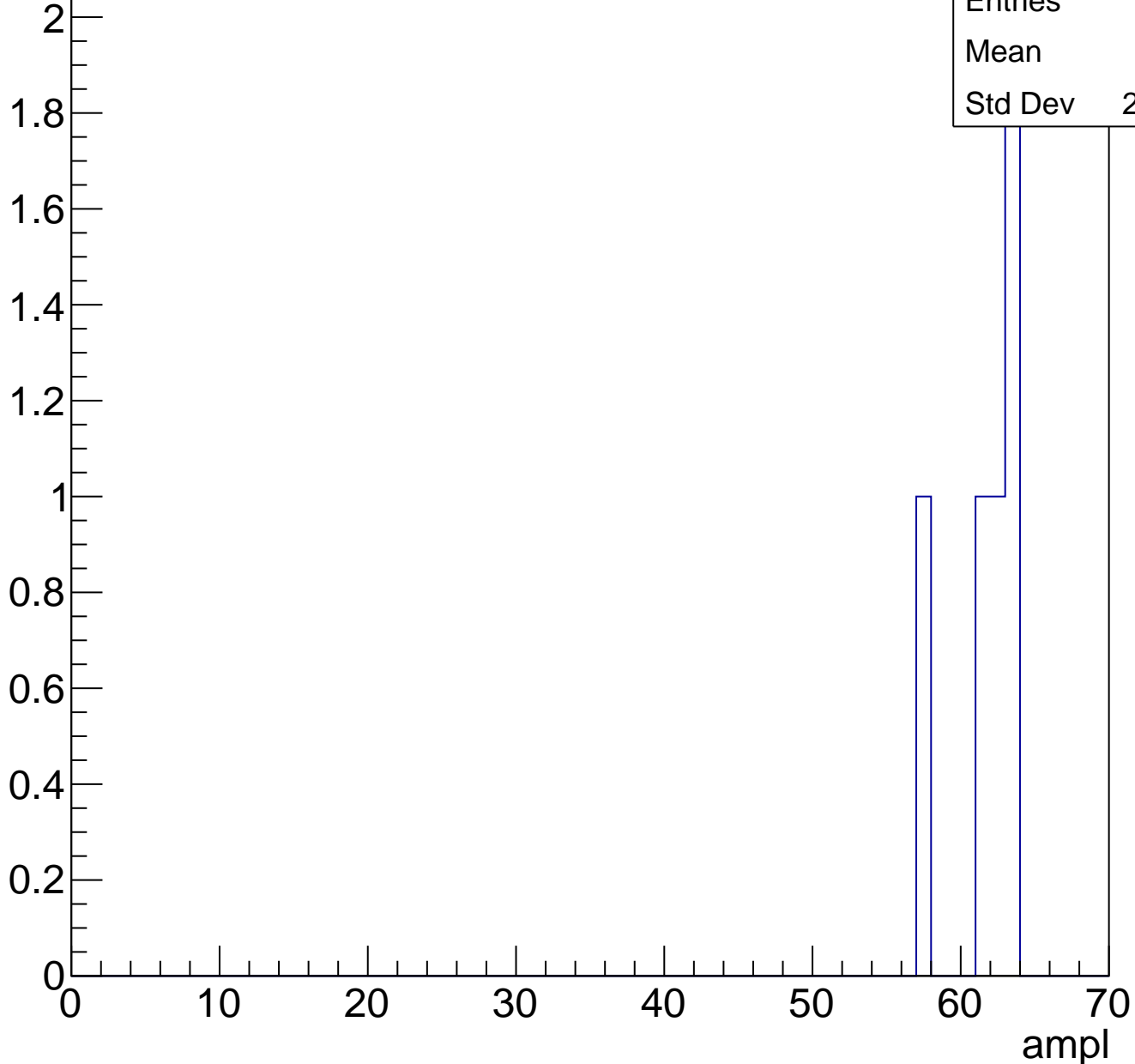
Entries	49
Mean	58.39
Std Dev	8.896



B0L000S, U2-ch63, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch63, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch64, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	85
Mean	31.46
Std Dev	4.965

Gaus mean : 32.2129

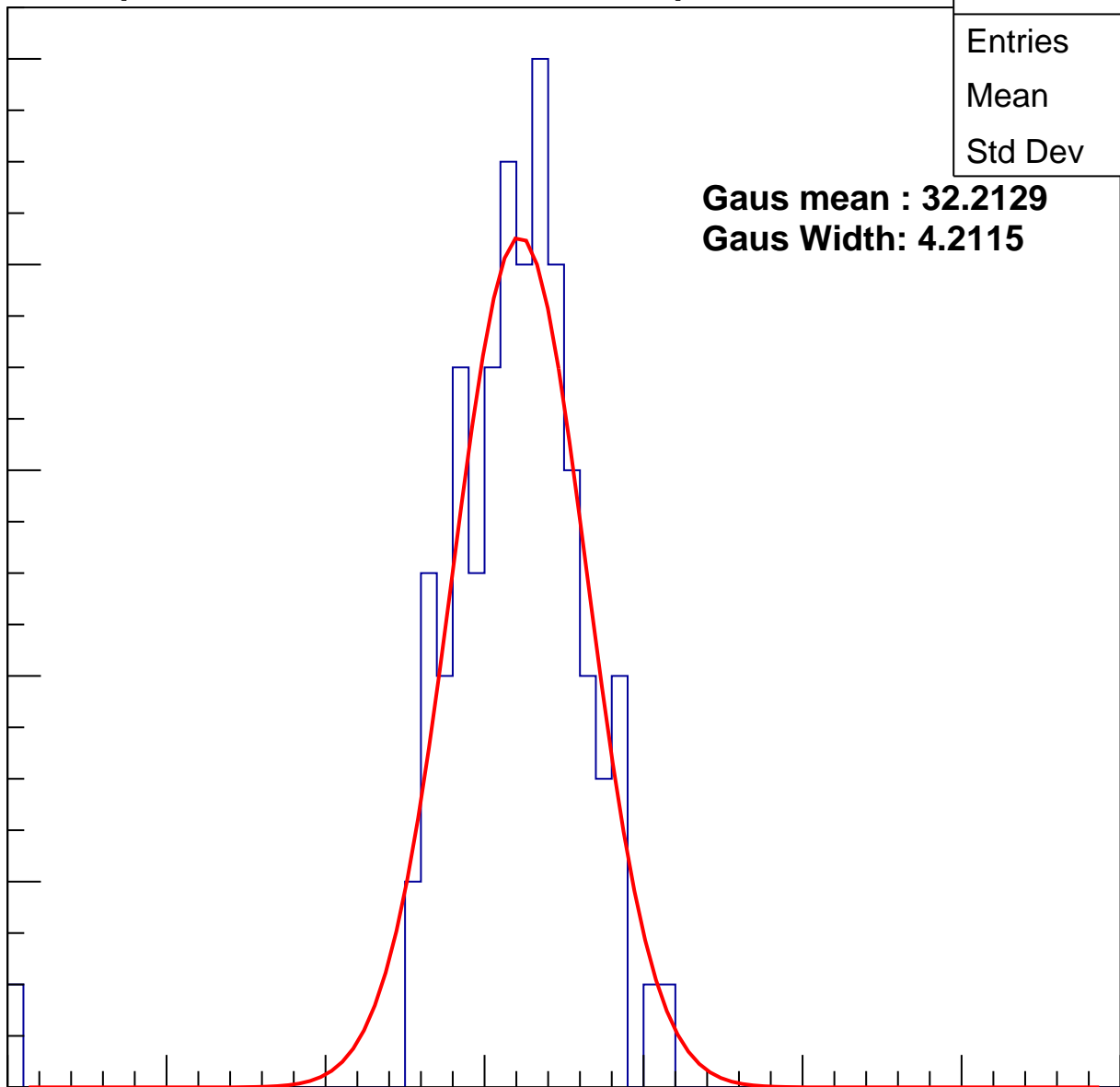
Gaus Width: 4.2115

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch64, adc1

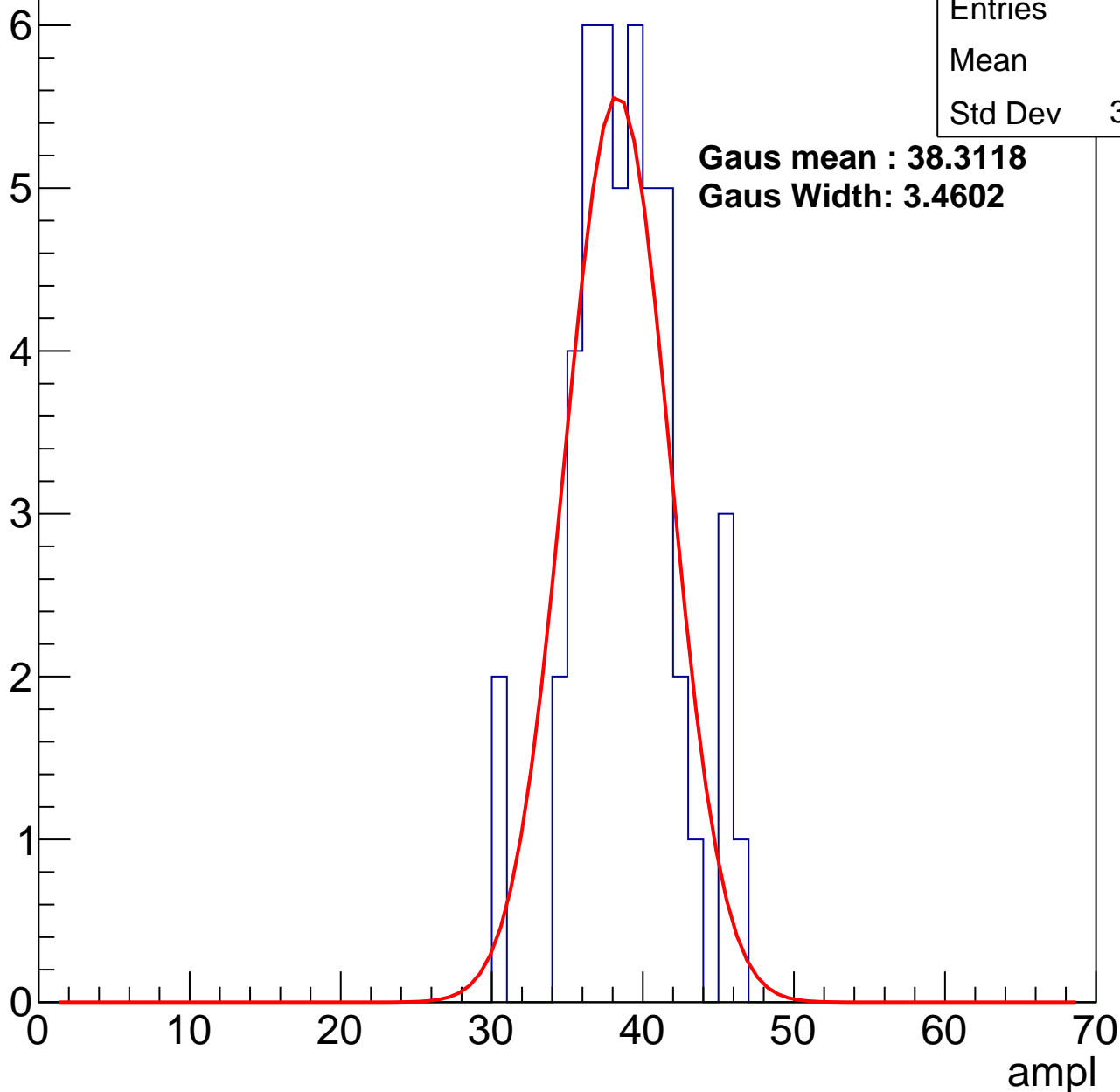
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	38.4
Std Dev	3.408

Gaus mean : 38.3118

Gaus Width: 3.4602



B0L000S, U2-ch64, adc2

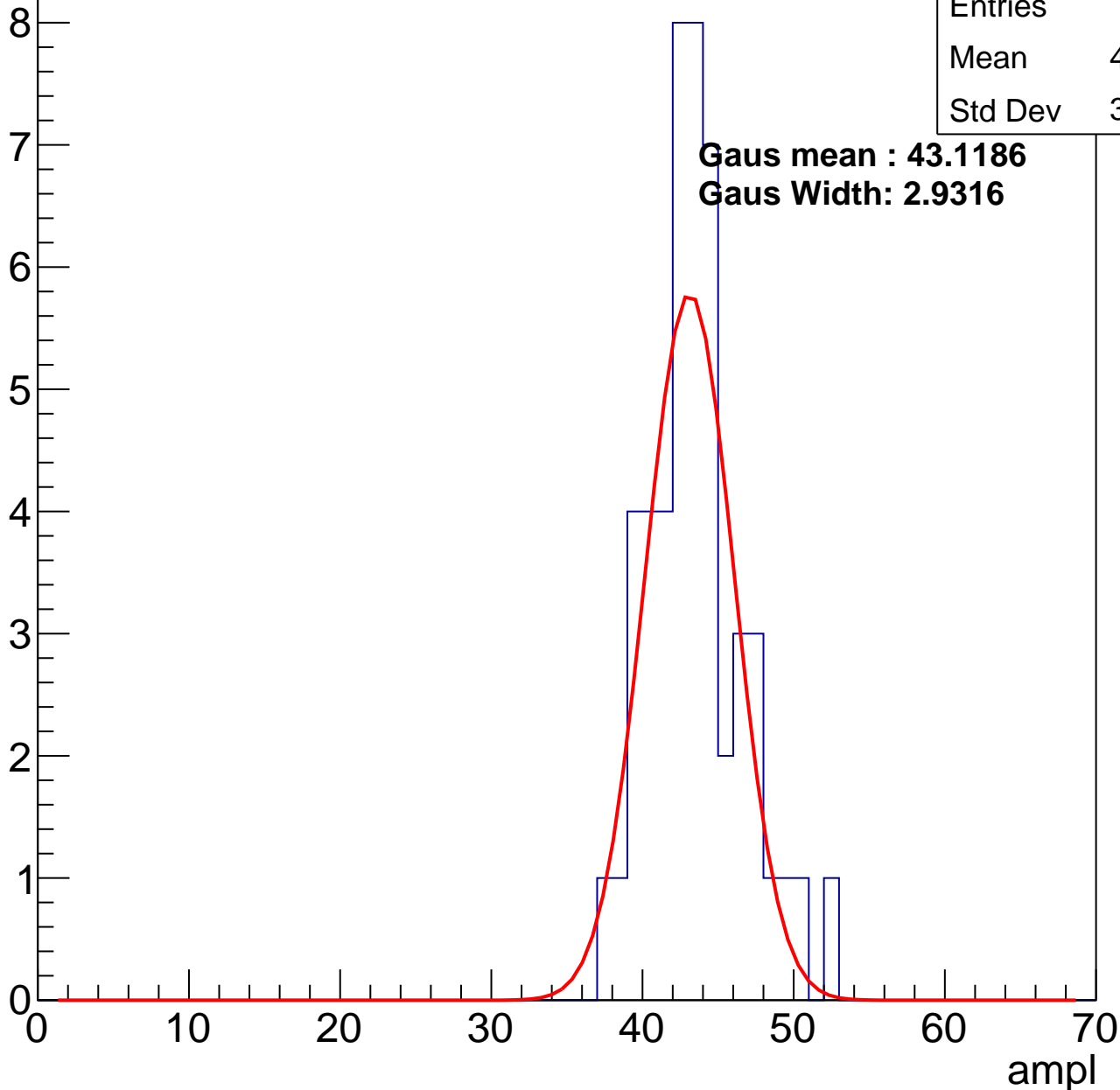
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	43.08
Std Dev	3.083

Gaus mean : 43.1186

Gaus Width: 2.9316

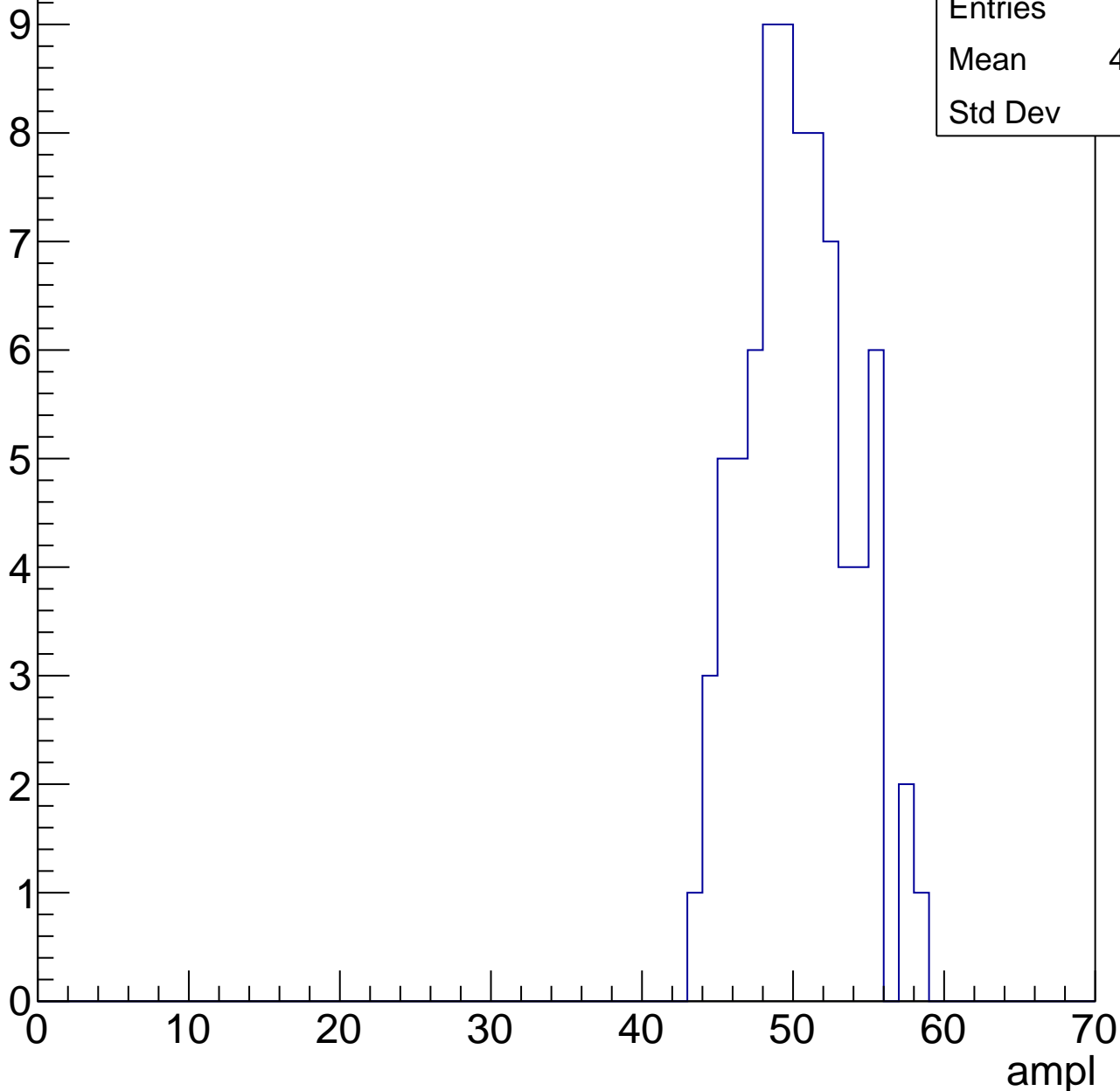


B0L000S, U2-ch64, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	49.83
Std Dev	3.41

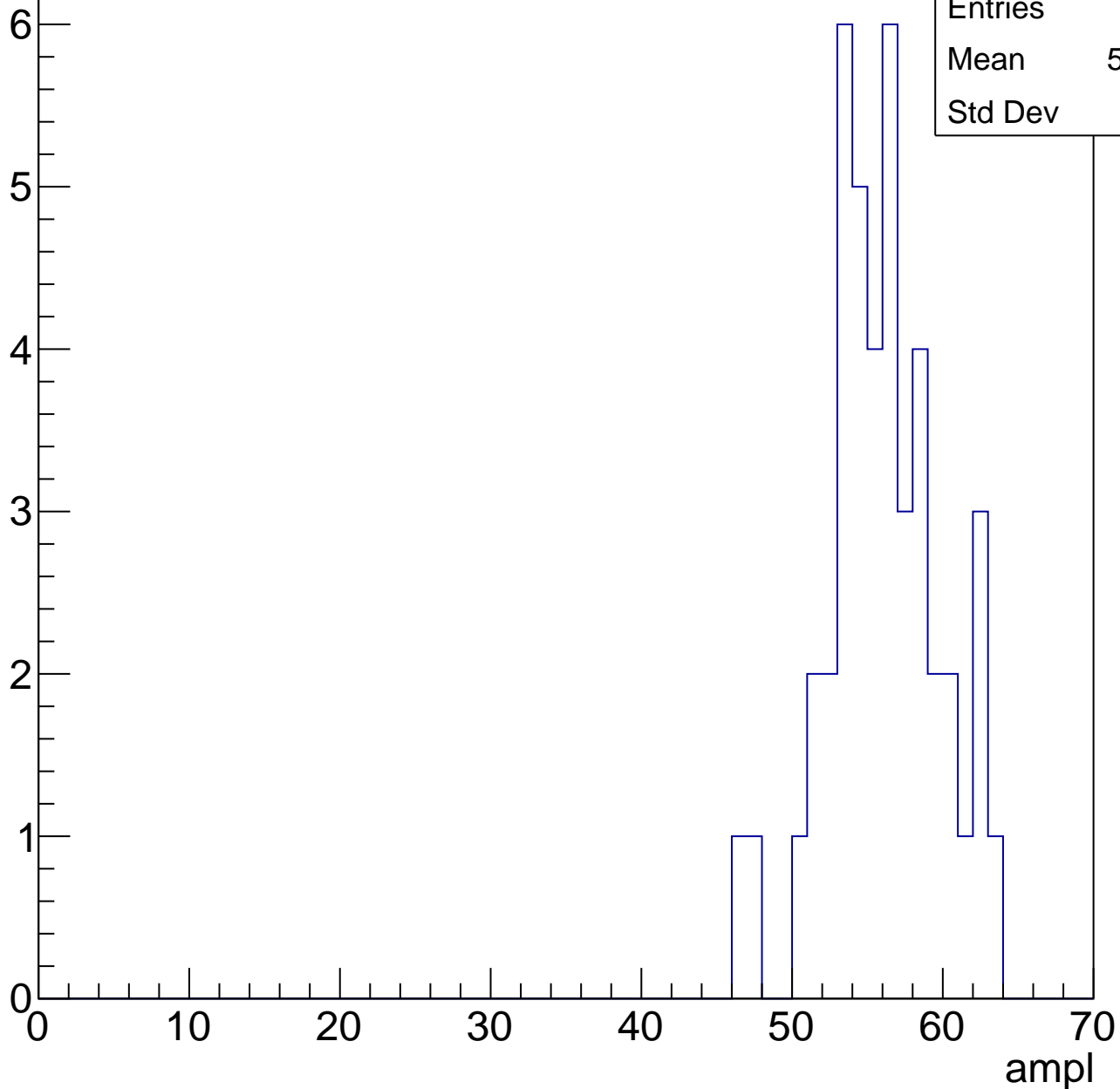


B0L000S, U2-ch64, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	55.55
Std Dev	3.75



B0L000S, U2-ch64, adc5

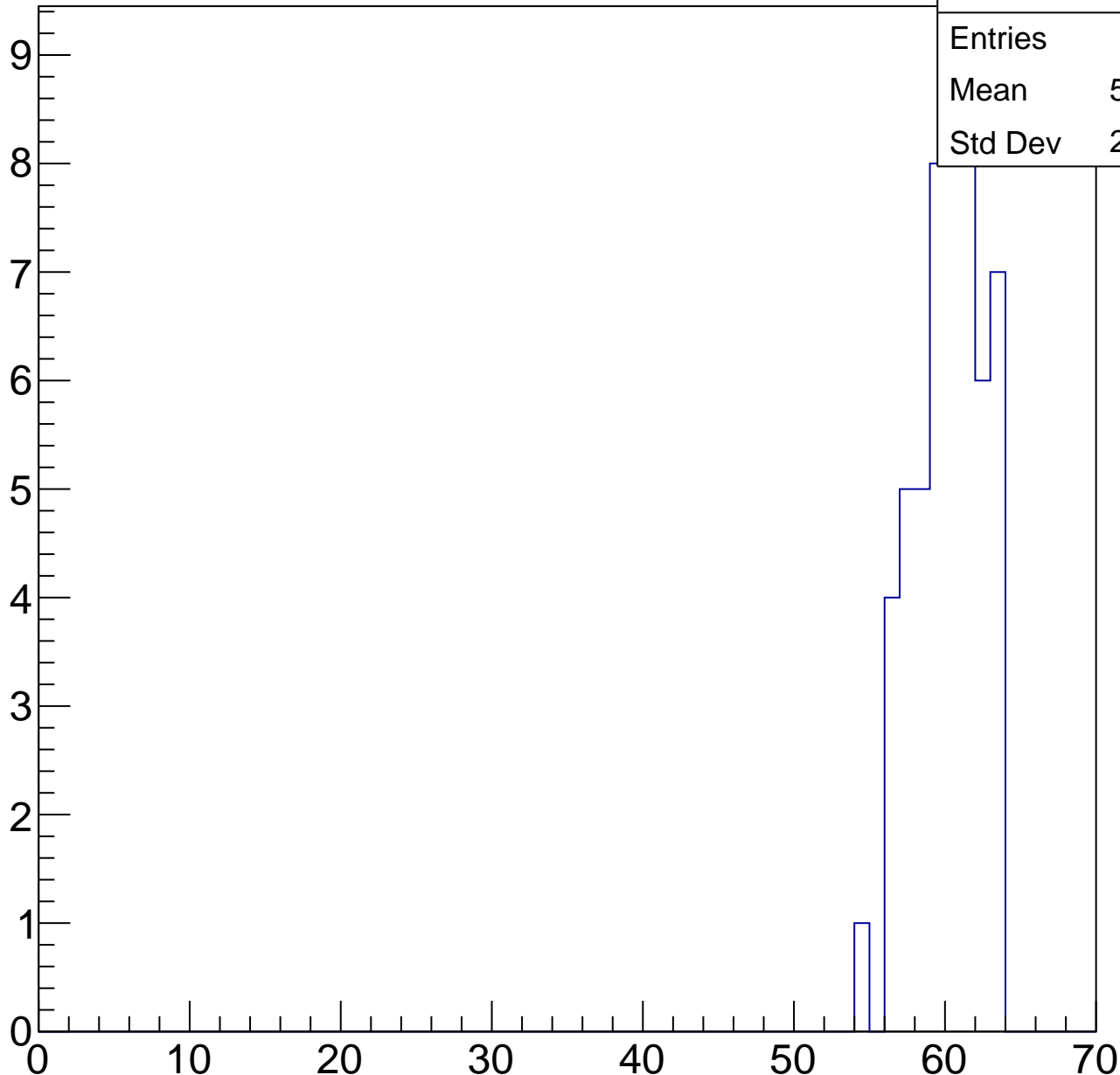
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.75
Std Dev	2.239

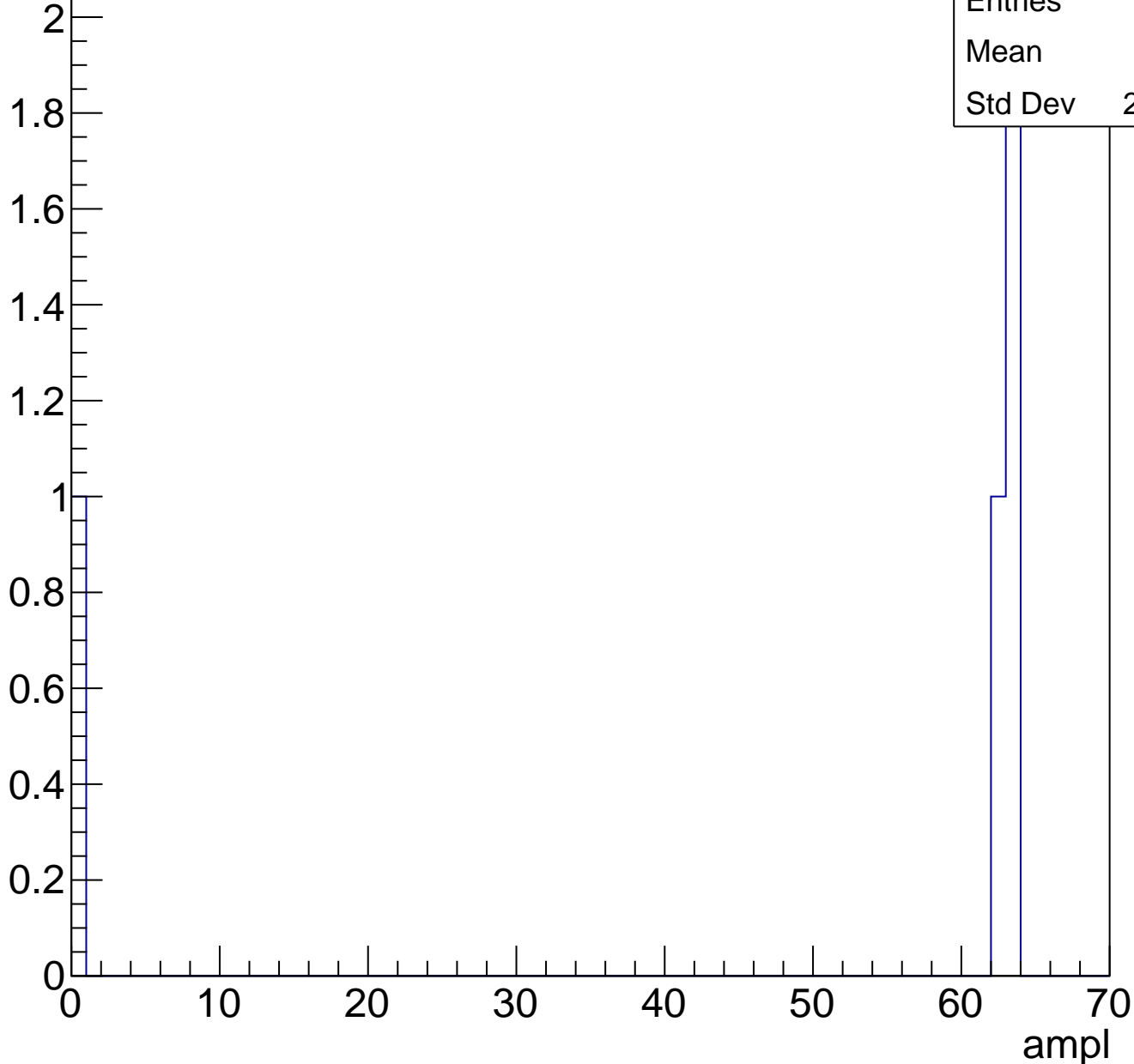
ampl



B0L000S, U2-ch64, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	4
Mean	47
Std Dev	27.14

B0L000S, U2-ch64, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch65, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	69
Mean	29.12
Std Dev	7.866

Gaus mean : 30.6998

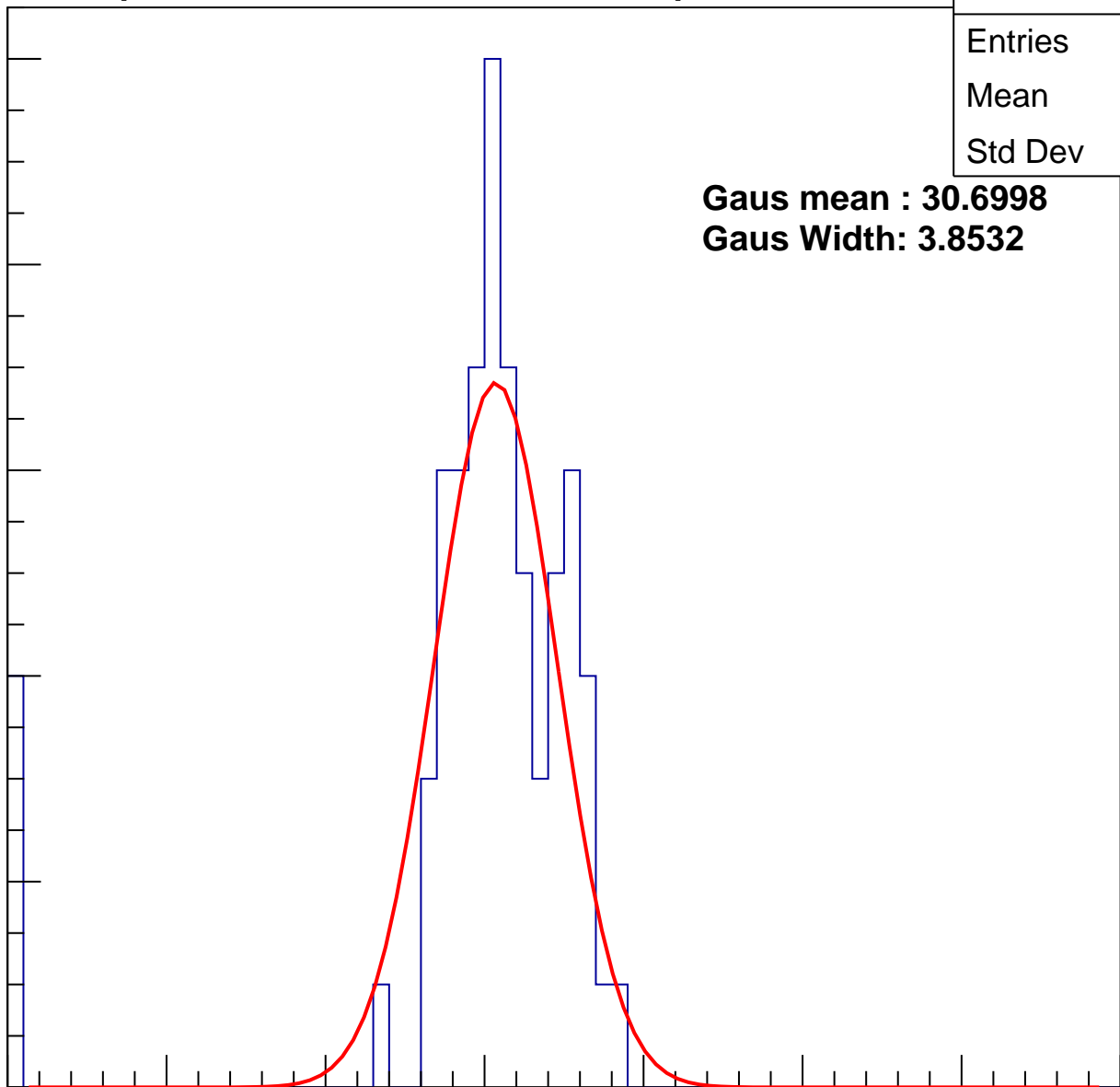
Gaus Width: 3.8532

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch65, adc1

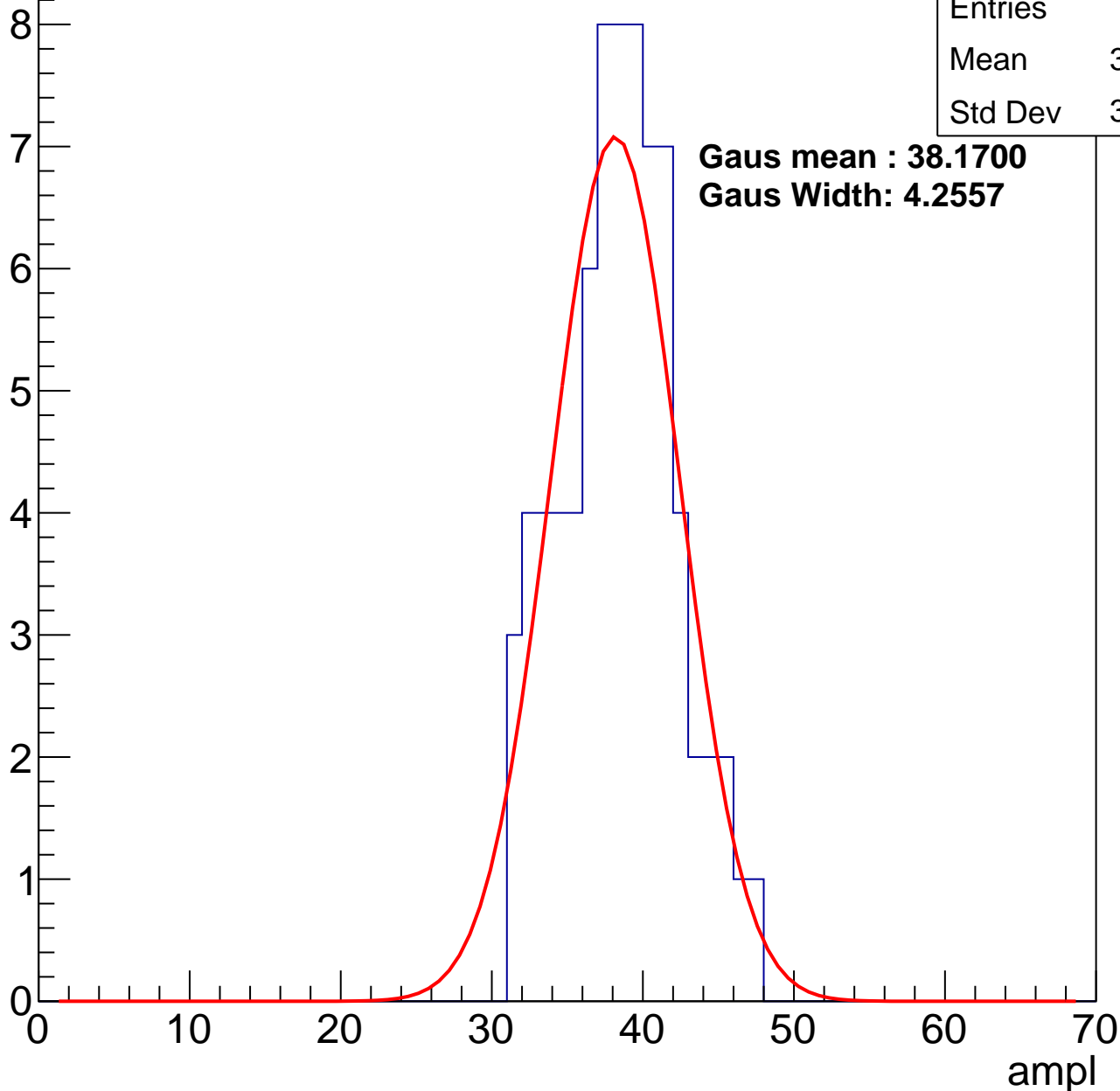
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.99
Std Dev	3.743

Gaus mean : 38.1700

Gaus Width: 4.2557



B0L000S, U2-ch65, adc2

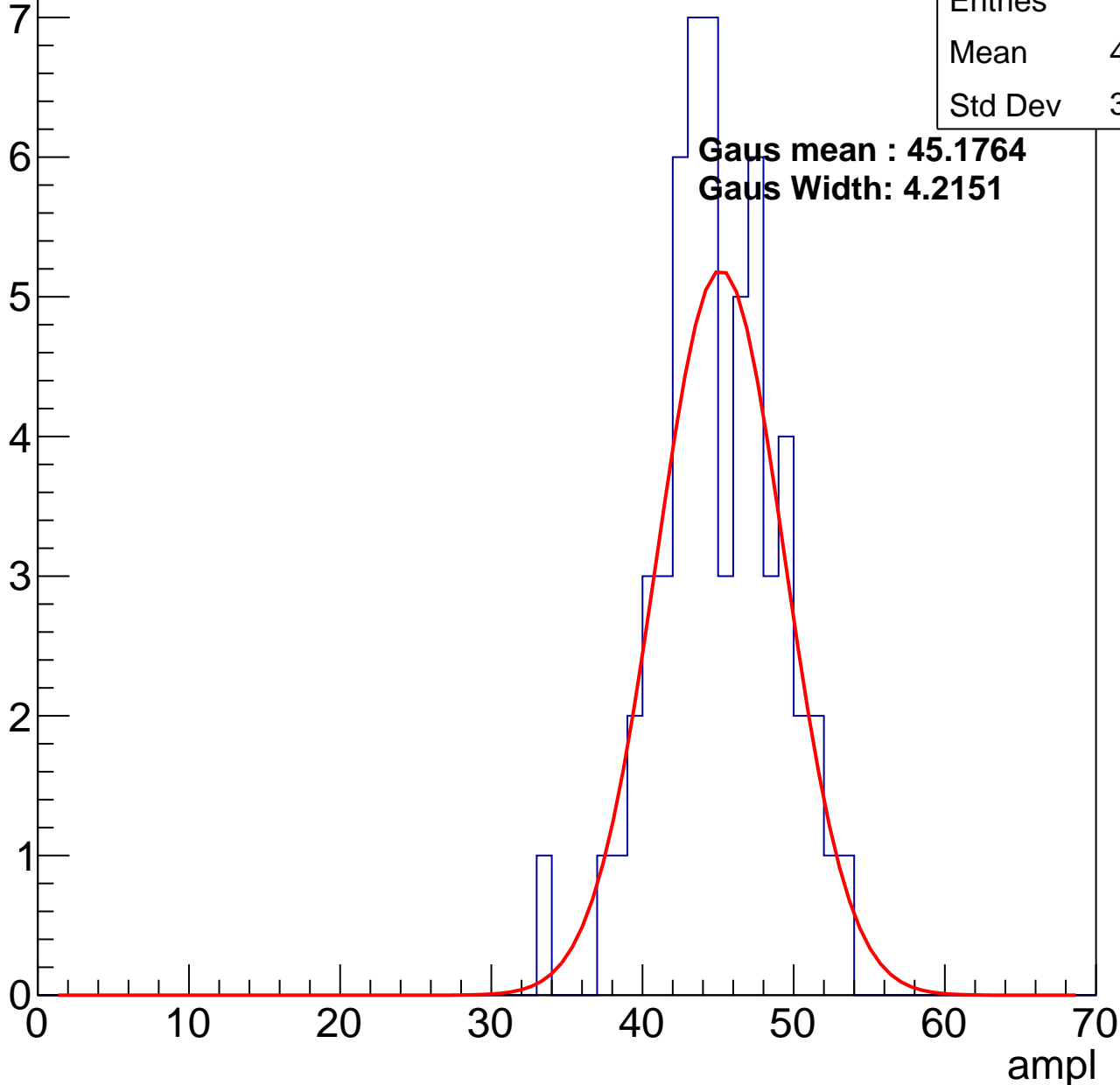
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.55
Std Dev	3.905

Gaus mean : 45.1764

Gaus Width: 4.2151

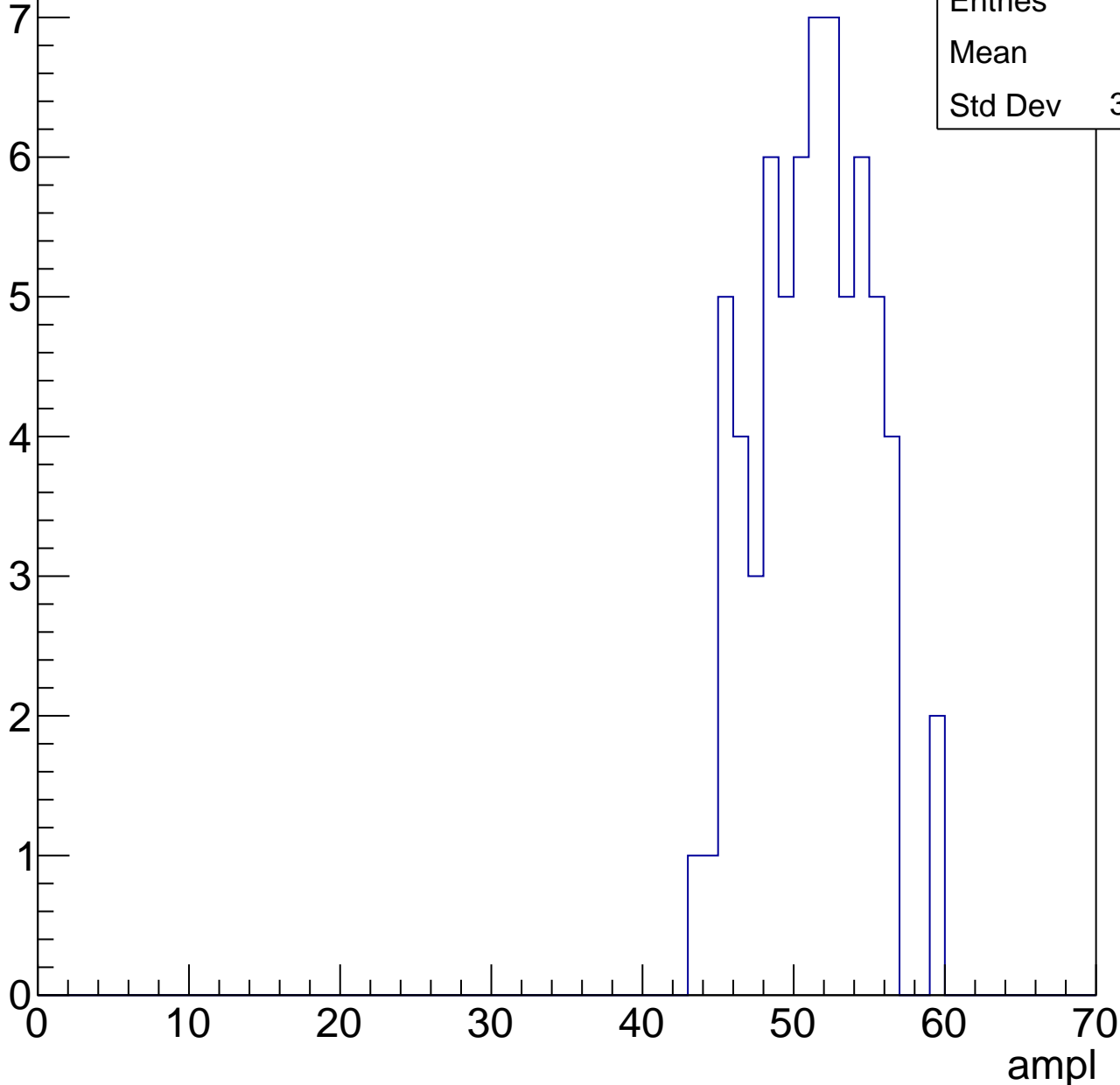


B0L000S, U2-ch65, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	50.7
Std Dev	3.673



B0L000S, U2-ch65, adc4

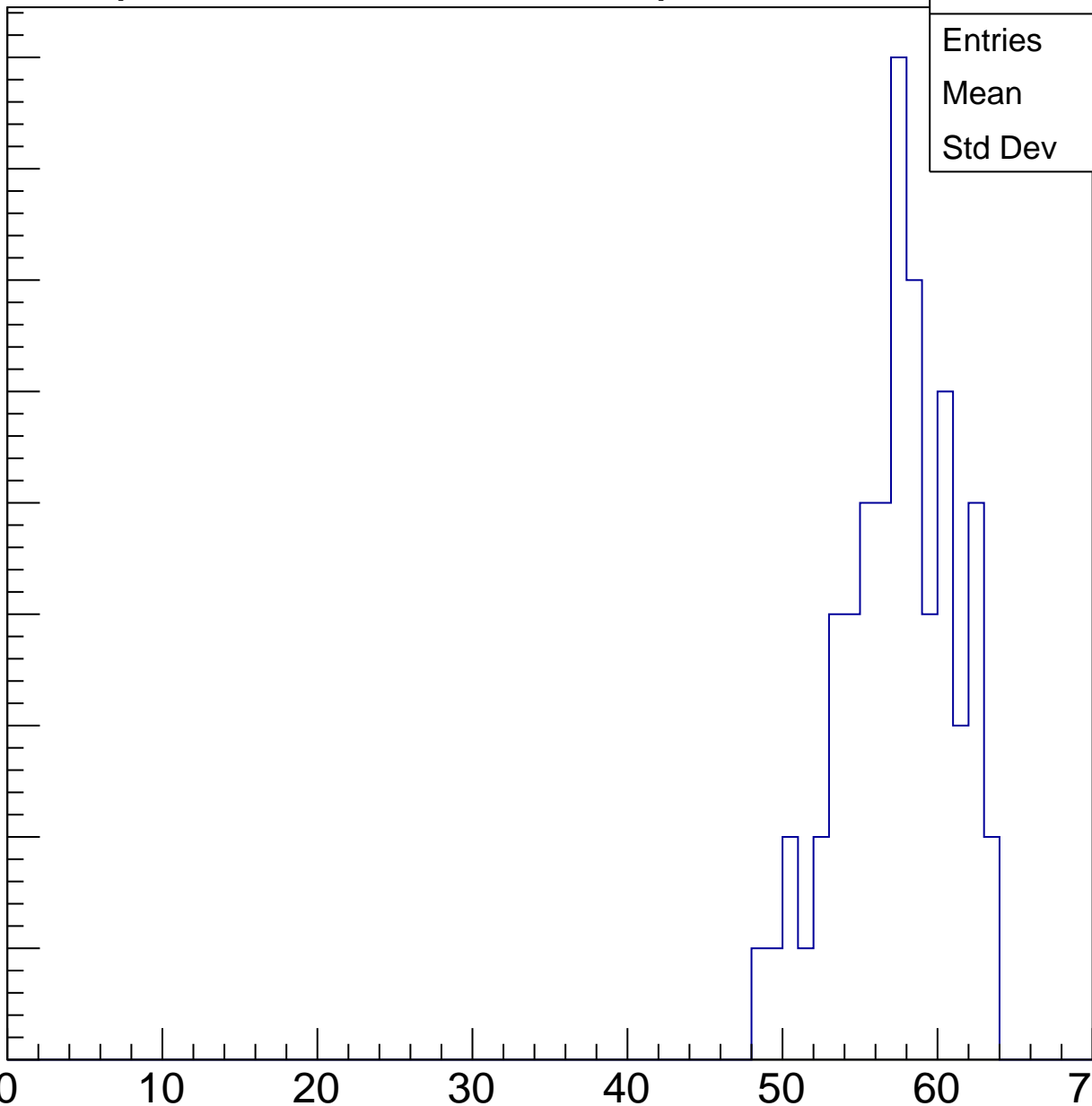
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	56.87
Std Dev	3.564

ampl

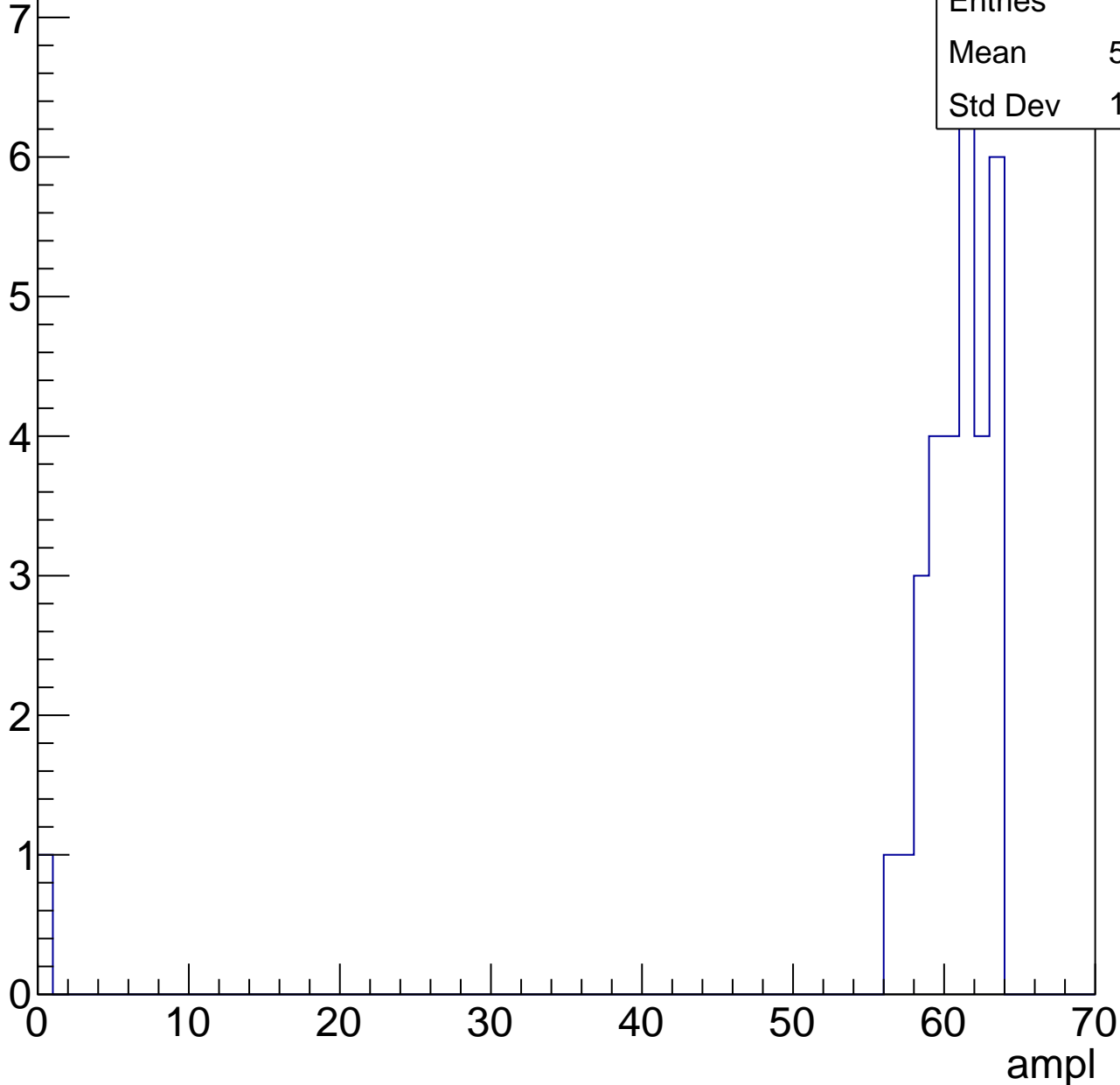


B0L000S, U2-ch65, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	31
Mean	58.58
Std Dev	10.86



B0L000S, U2-ch65, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L000S, U2-ch65, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch66, adc0

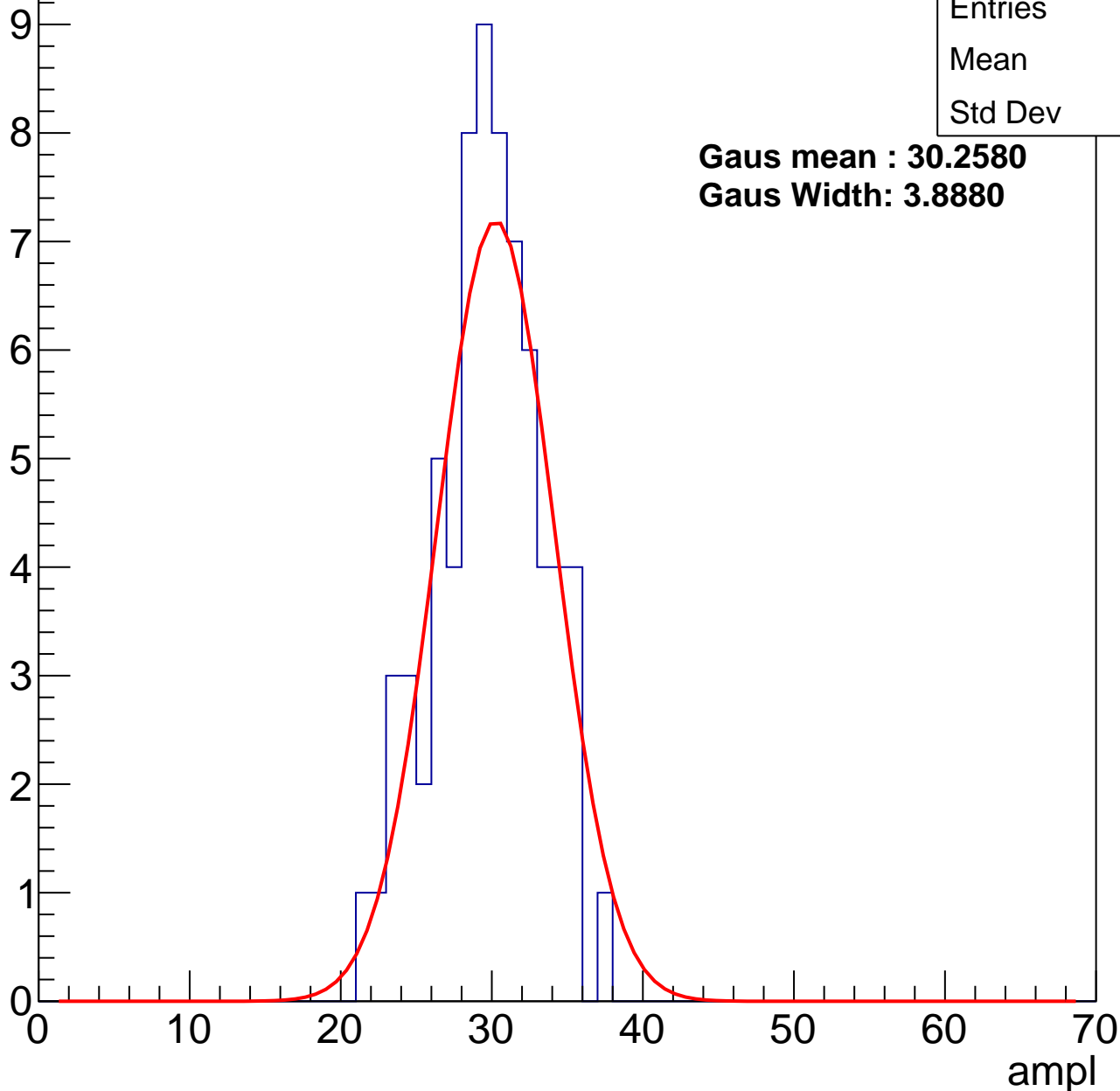
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.3
Std Dev	3.49

Gaus mean : 30.2580

Gaus Width: 3.8880



B0L000S, U2-ch66, adc1

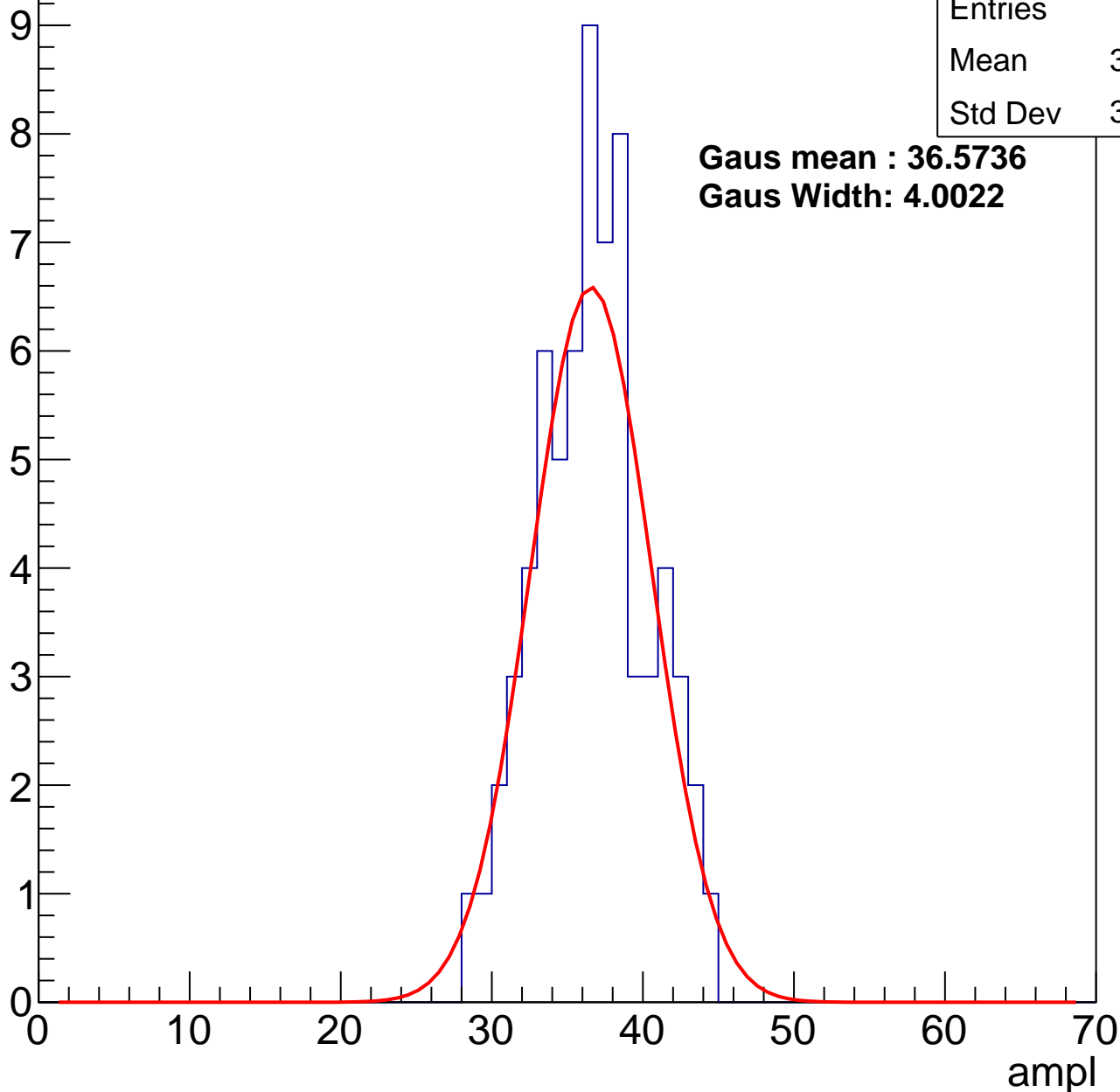
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.18
Std Dev	3.609

Gaus mean : 36.5736

Gaus Width: 4.0022

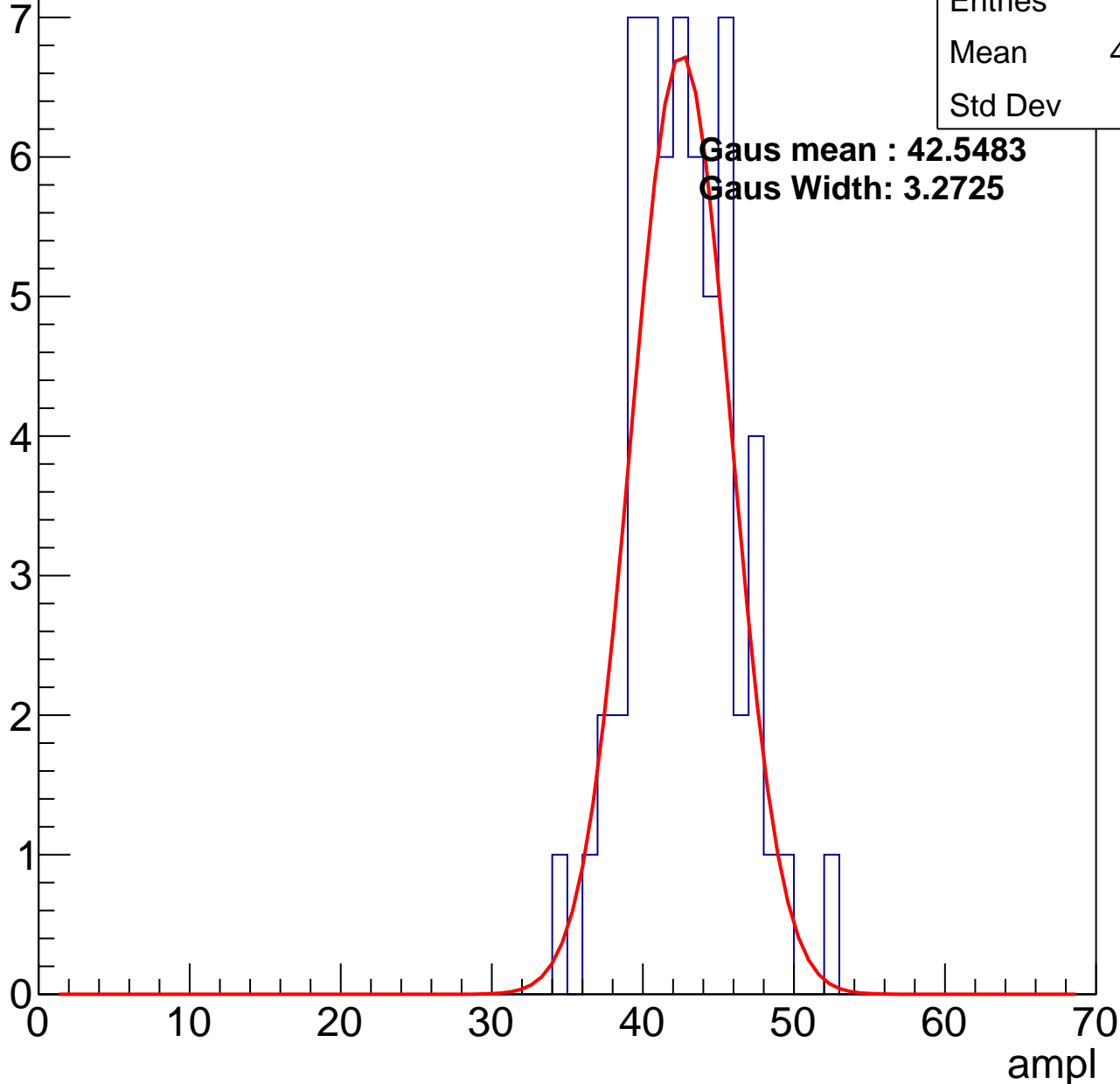


B0L000S, U2-ch66, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.25
Std Dev	3.37

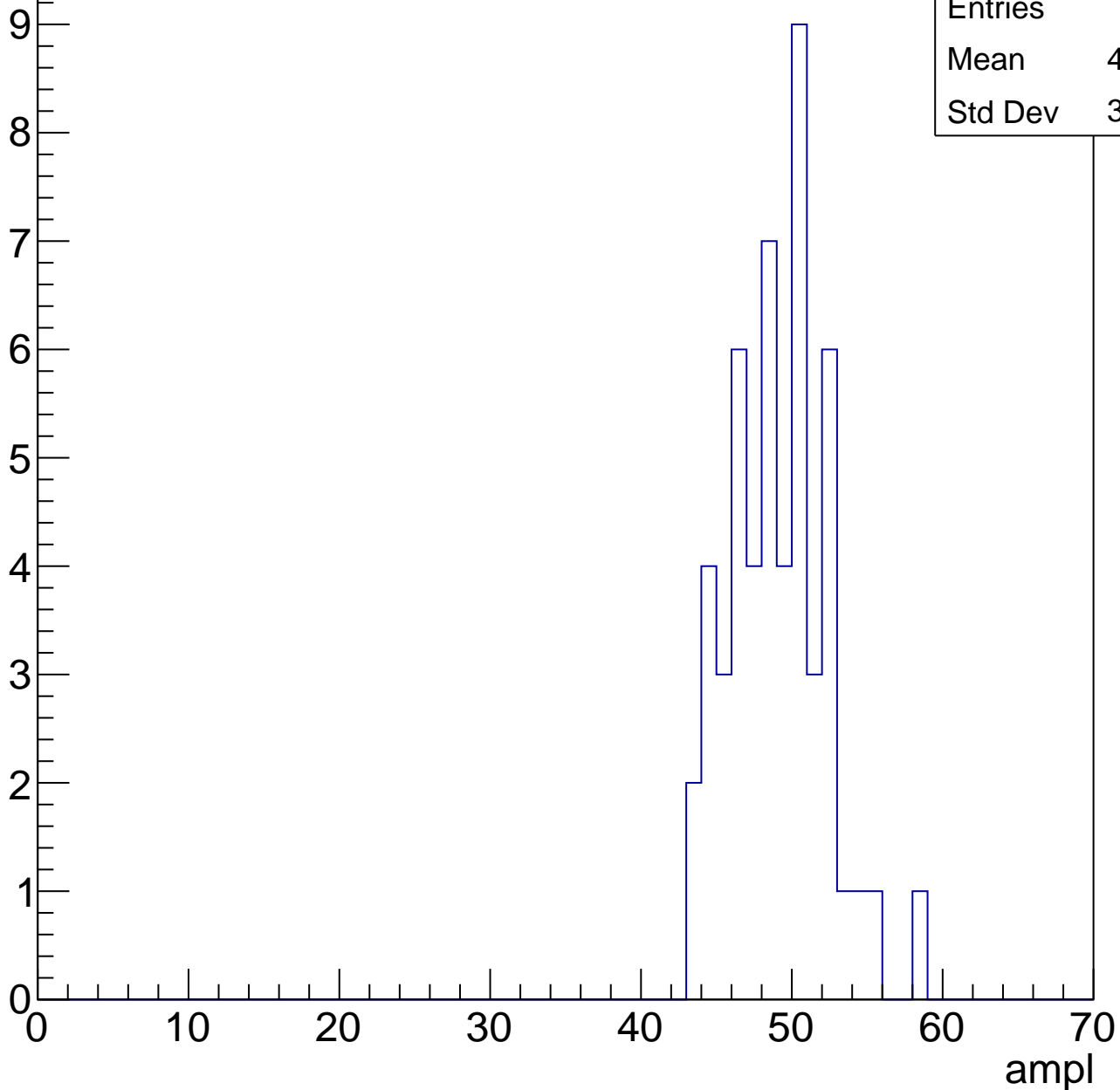


B0L000S, U2-ch66, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	48.62
Std Dev	3.169

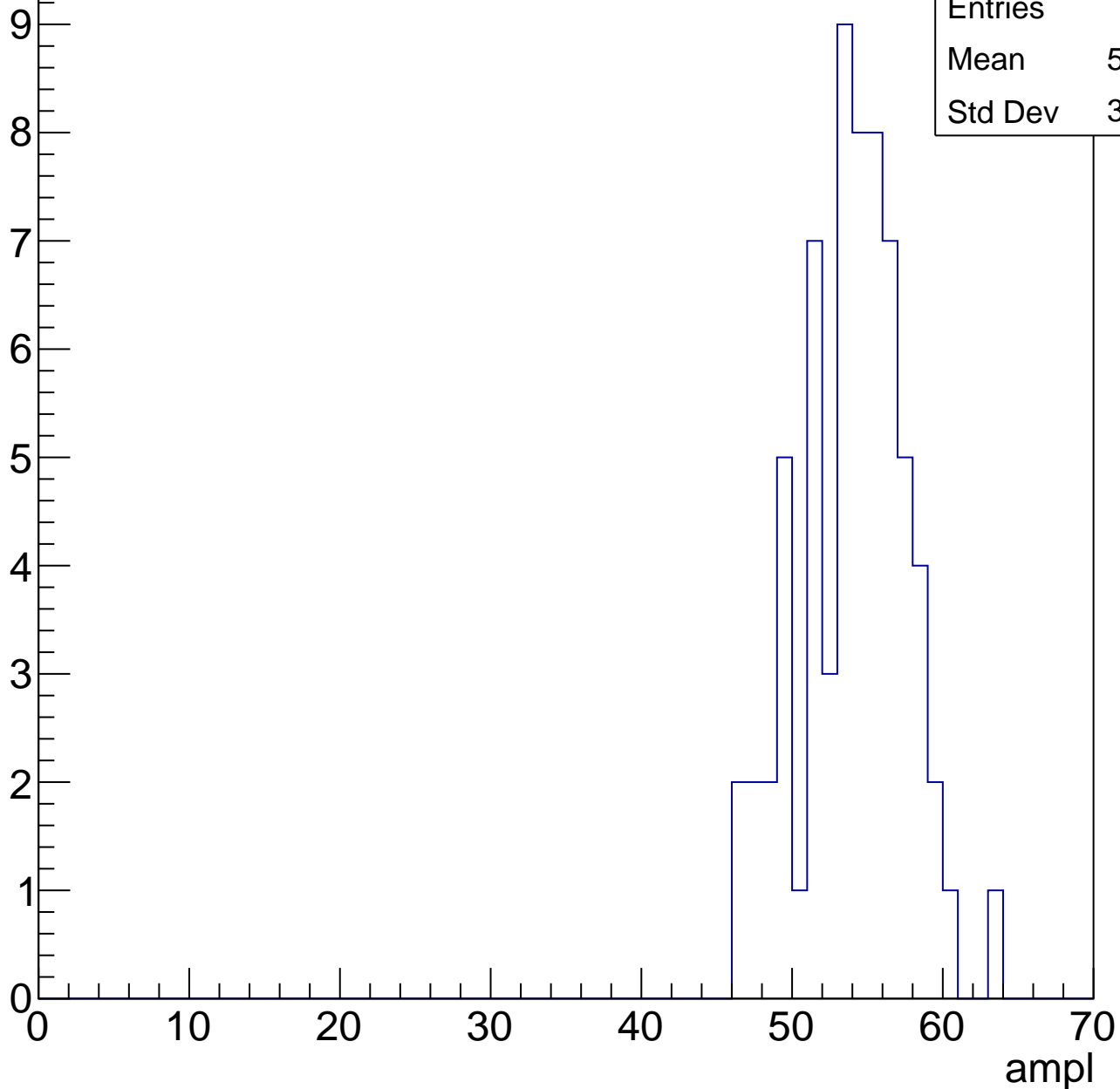


B0L000S, U2-ch66, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

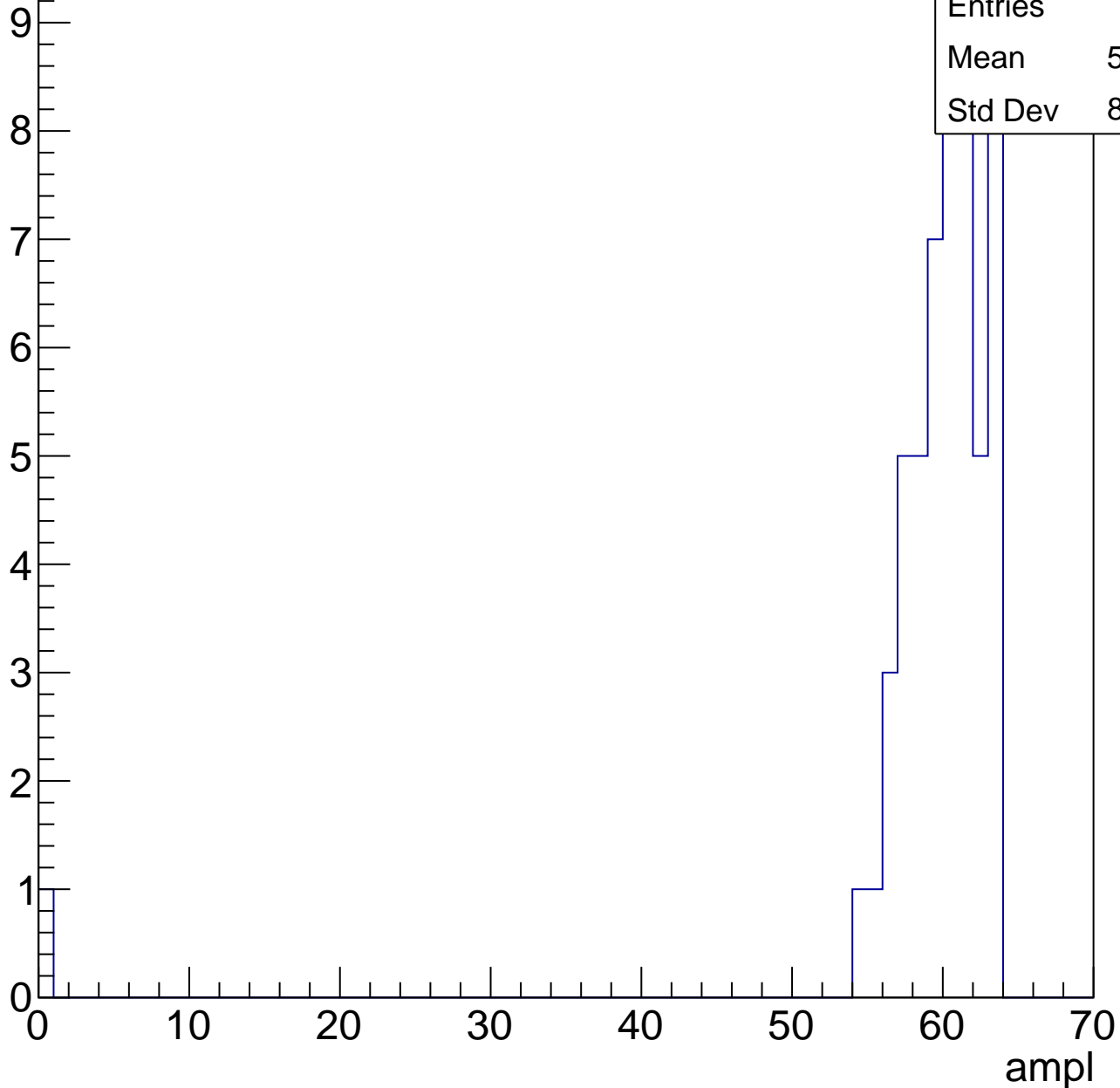
Entries	67
Mean	53.57
Std Dev	3.504



B0L000S, U2-ch66, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



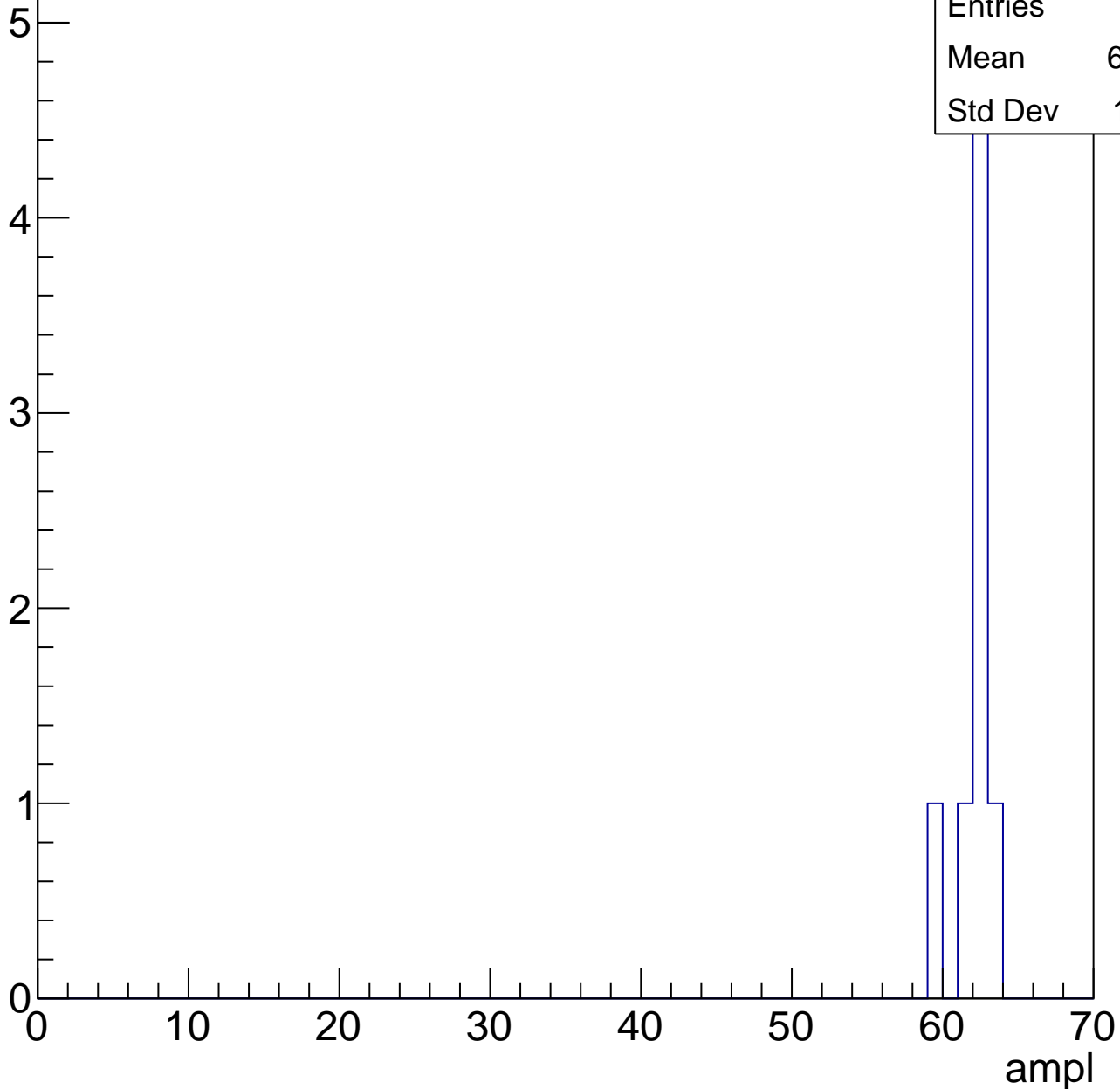
Entries	54
Mean	58.67
Std Dev	8.373

B0L000S, U2-ch66, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	8
Mean	61.62
Std Dev	1.111



B0L000S, U2-ch66, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch67, adc0

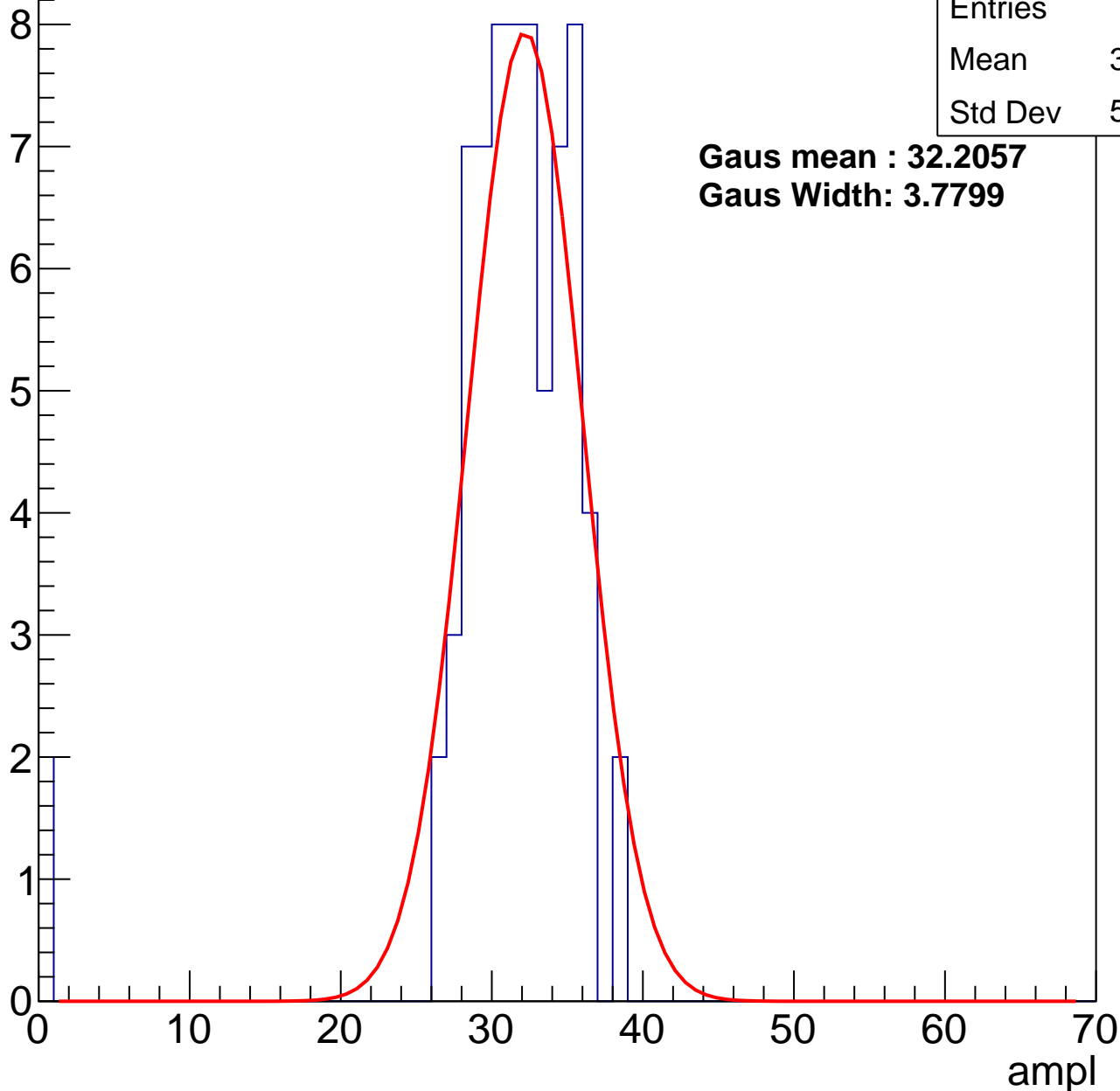
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.69
Std Dev	5.968

Gaus mean : 32.2057

Gaus Width: 3.7799



B0L000S, U2-ch67, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	59
Mean	37.64
Std Dev	3.978

Gaus mean : 38.8606

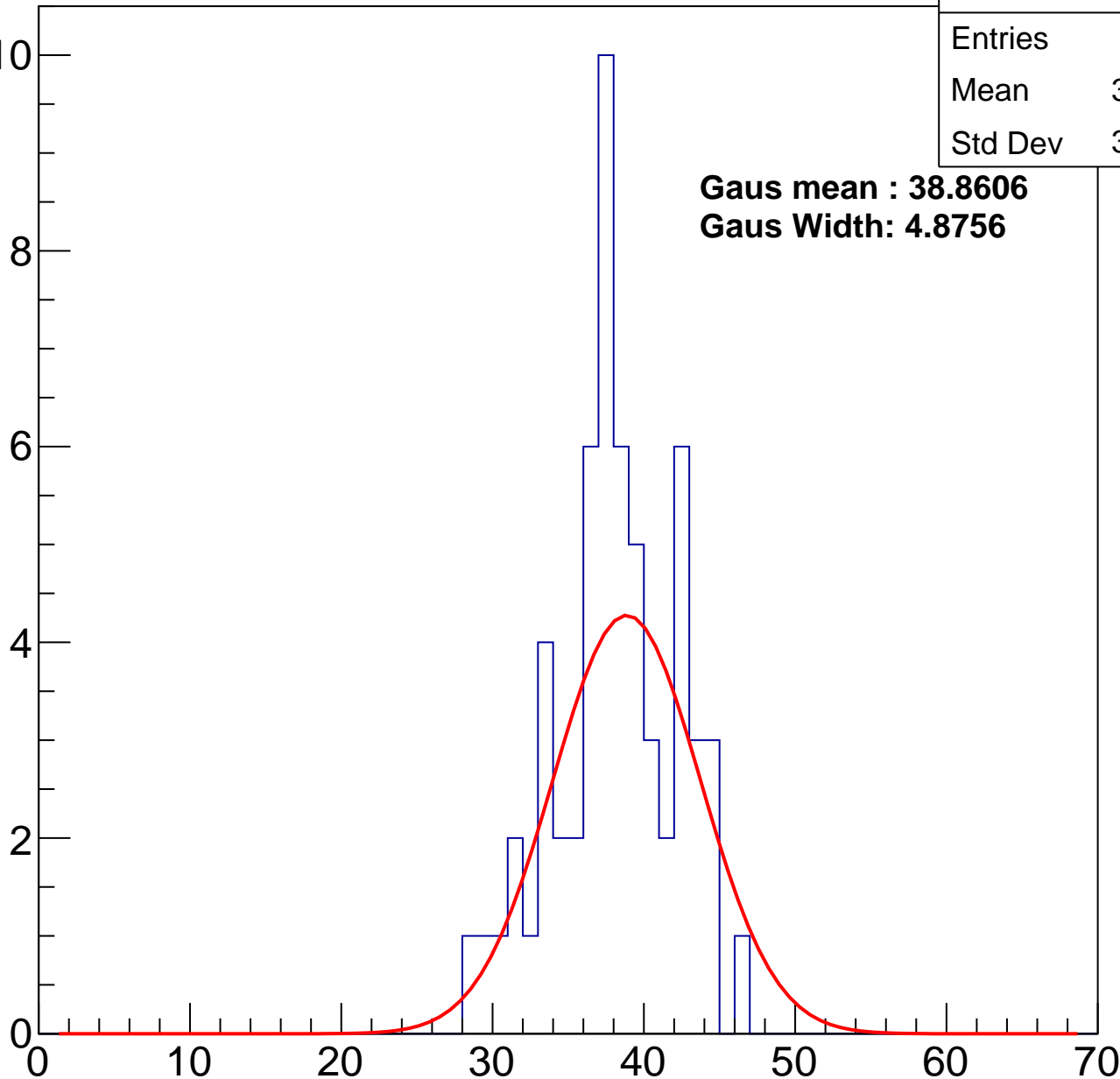
Gaus Width: 4.8756

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch67, adc2

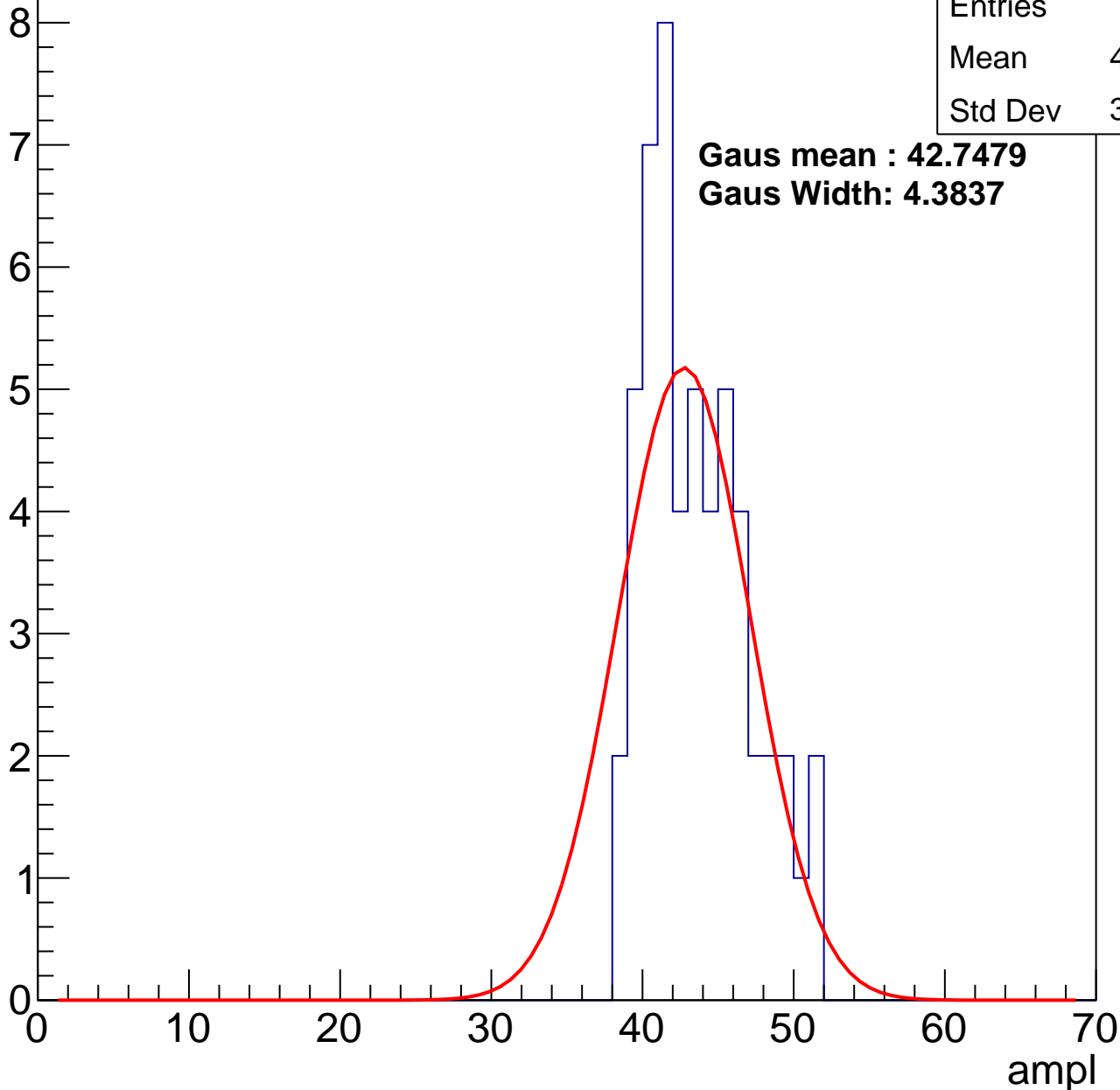
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.15
Std Dev	3.417

Gaus mean : 42.7479

Gaus Width: 4.3837

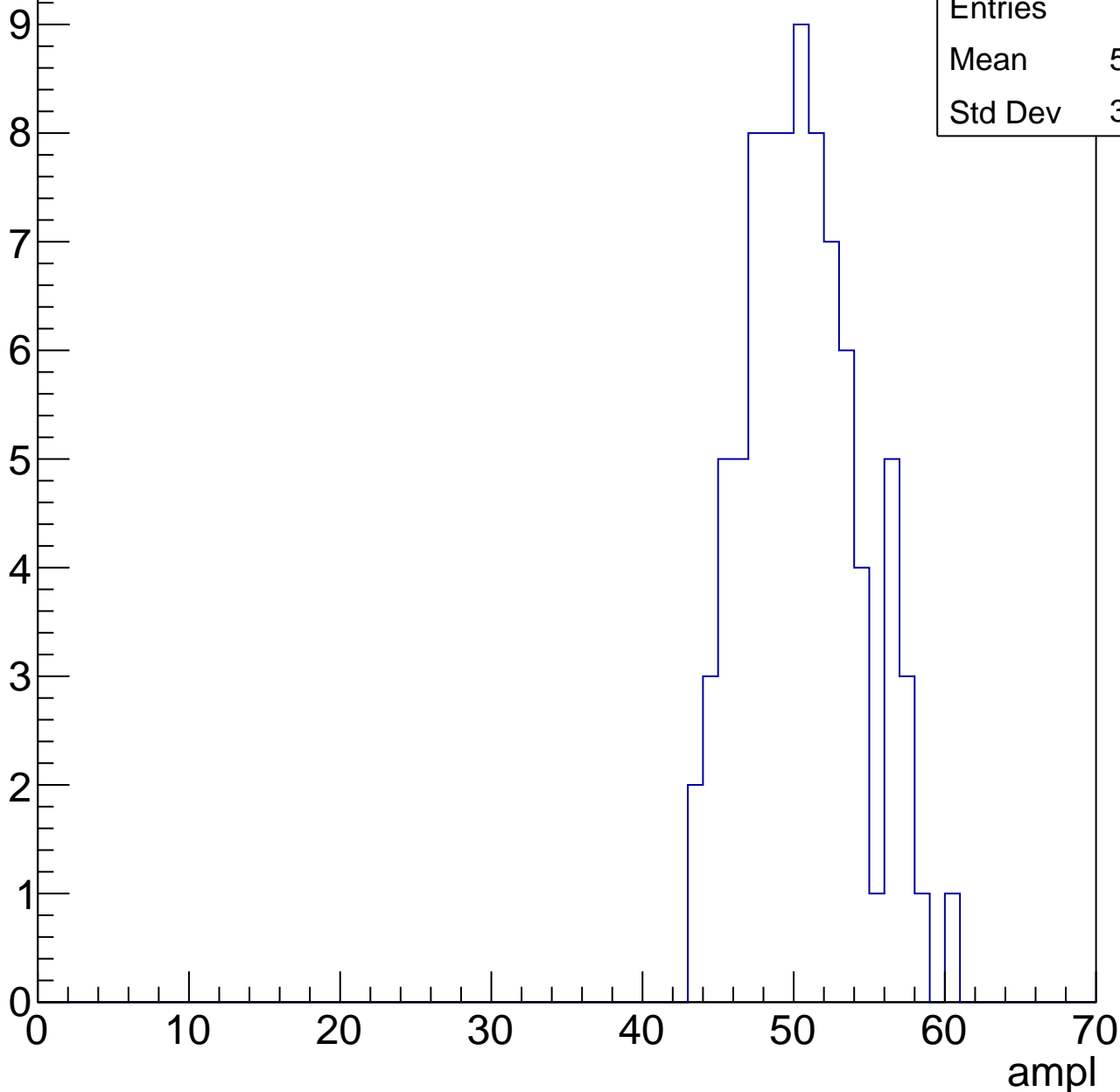


B0L000S, U2-ch67, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

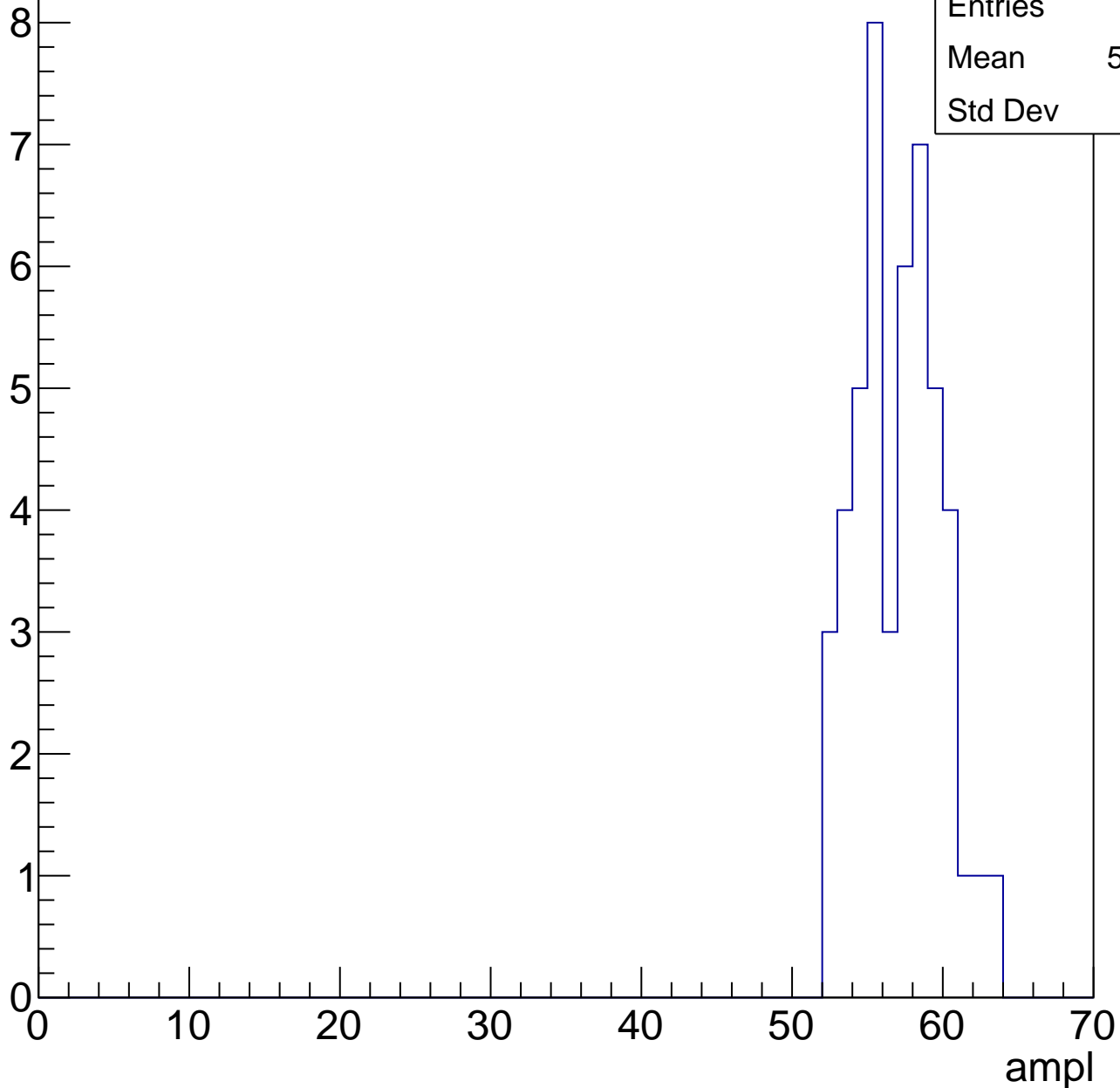
Entries	84
Mean	50.06
Std Dev	3.768



B0L000S, U2-ch67, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



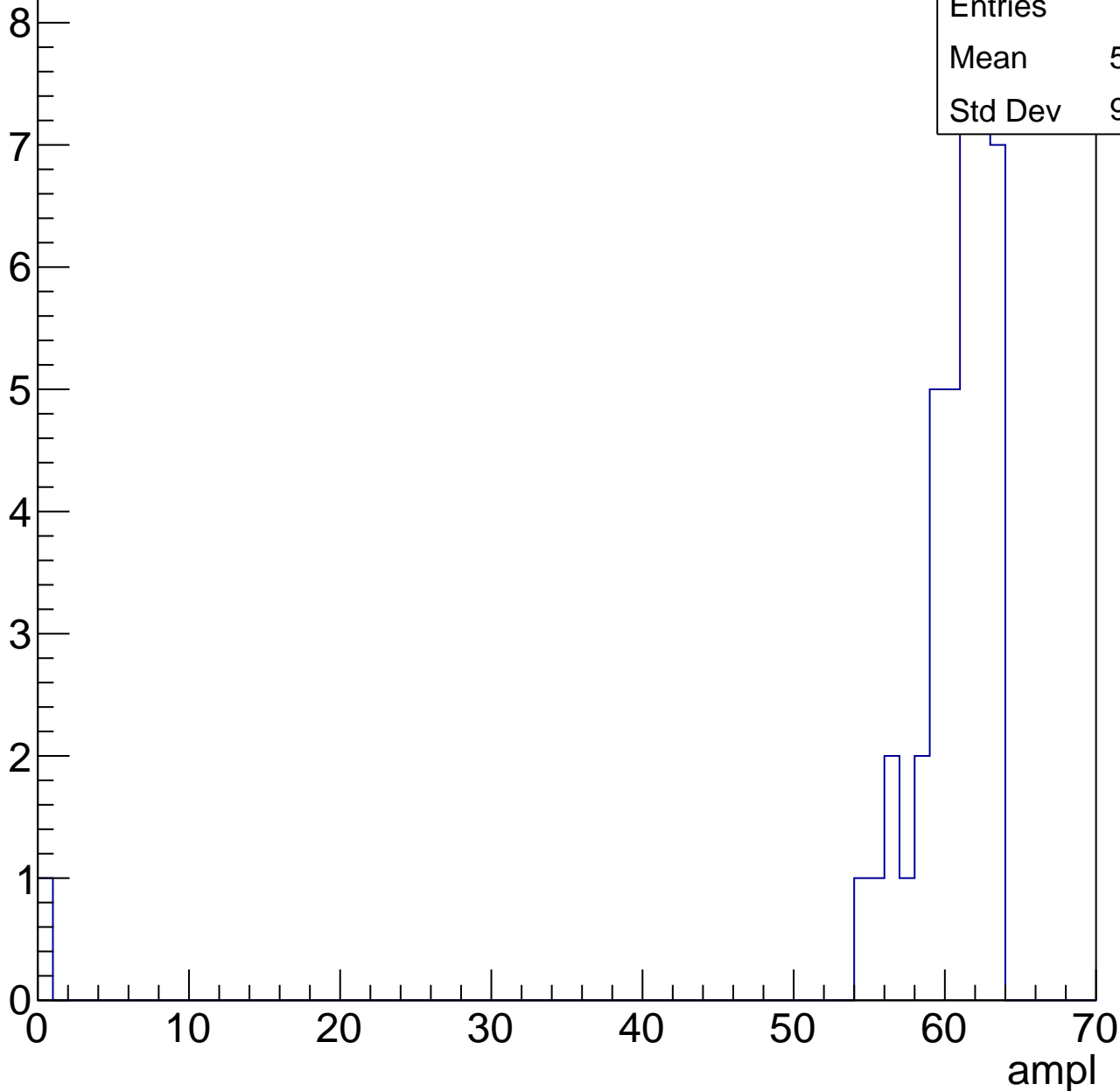
Entries	48
Mean	56.56
Std Dev	2.7

B0L000S, U2-ch67, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

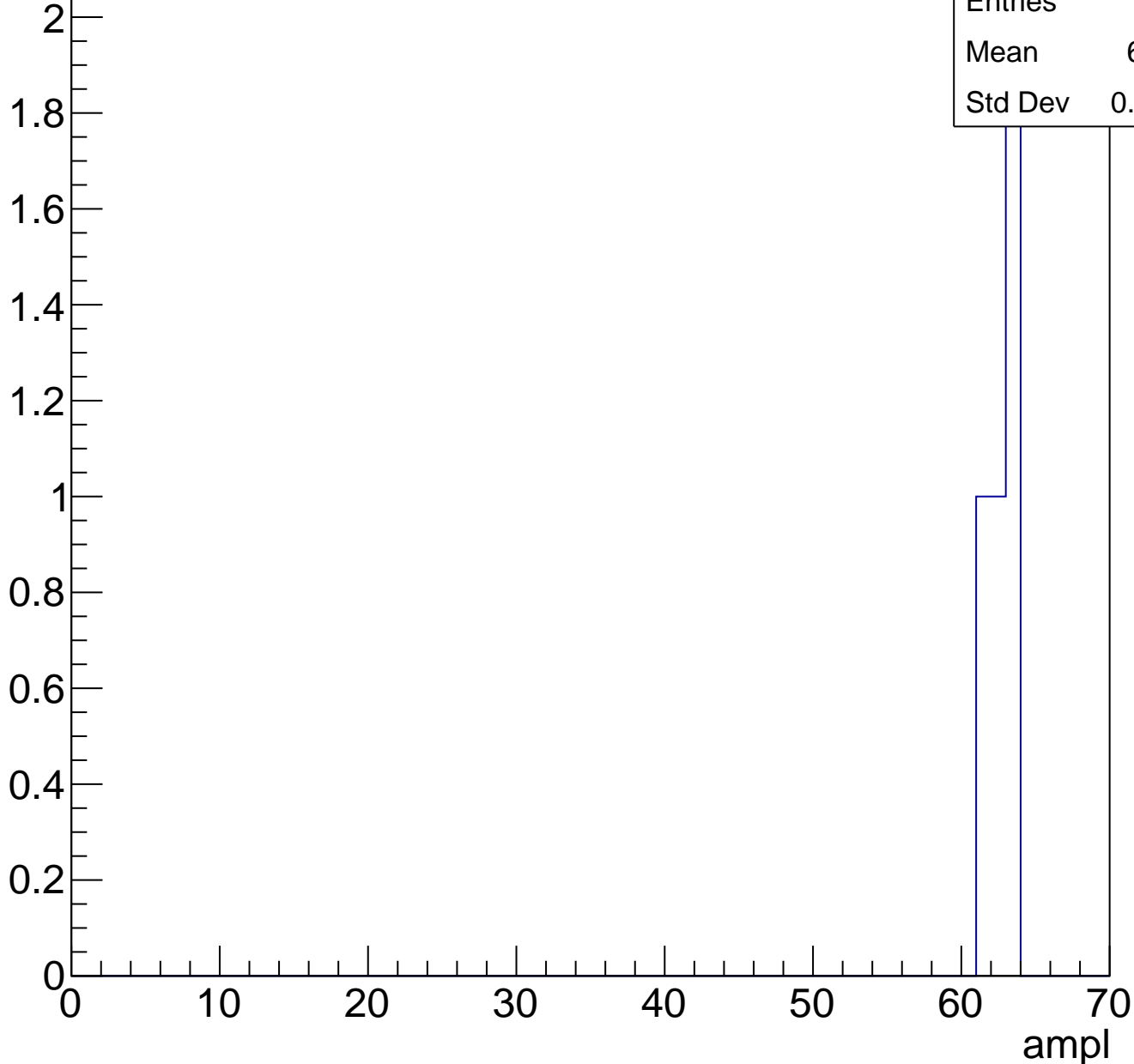
Entries	41
Mean	58.88
Std Dev	9.585



B0L000S, U2-ch67, adc6

calib_packv5_042523_0143.root, FC#5, port B1

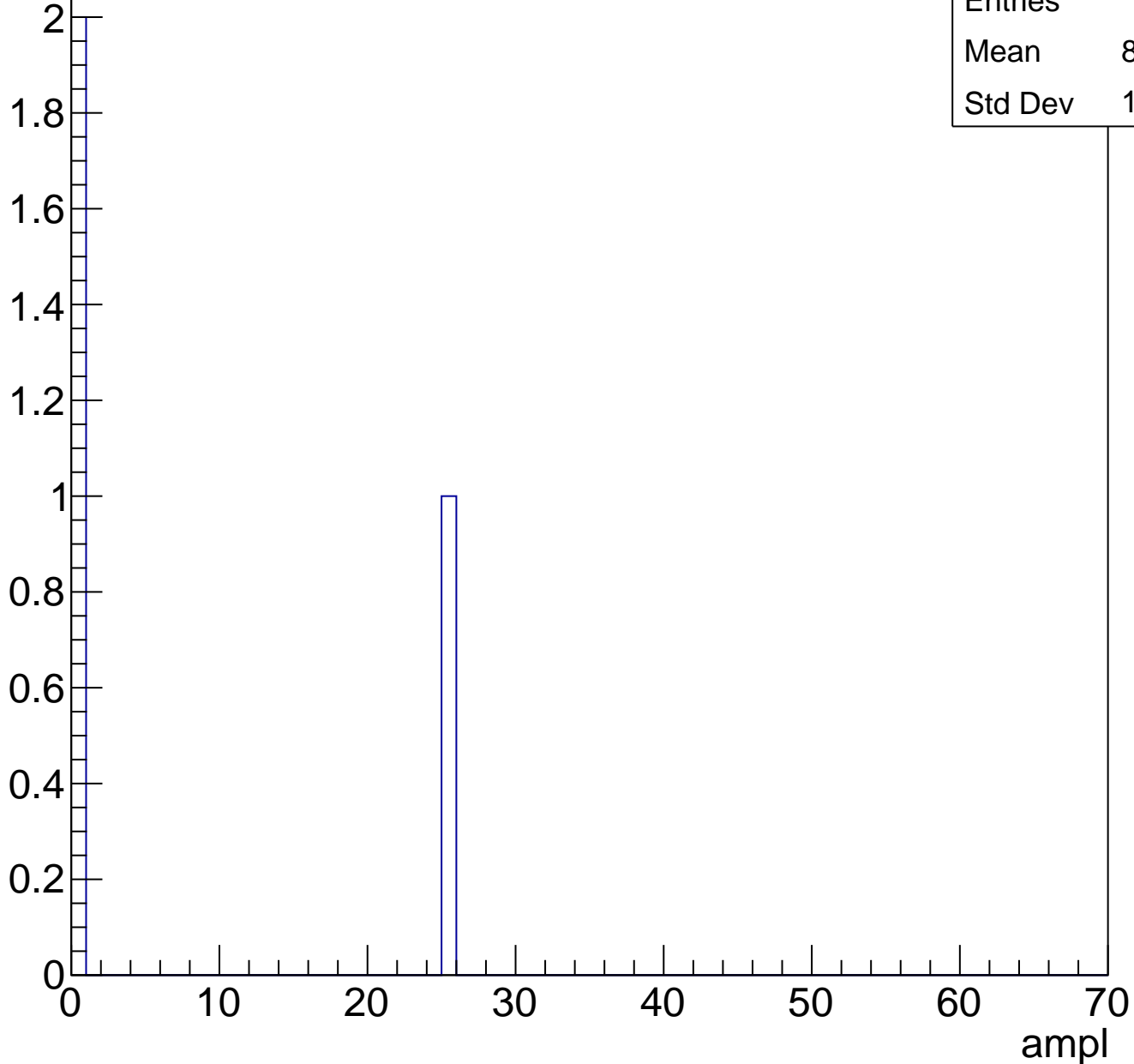
Entry



B0L000S, U2-ch67, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L000S, U2-ch68, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	71
Mean	28.9
Std Dev	5.13

Gaus mean : 28.9455

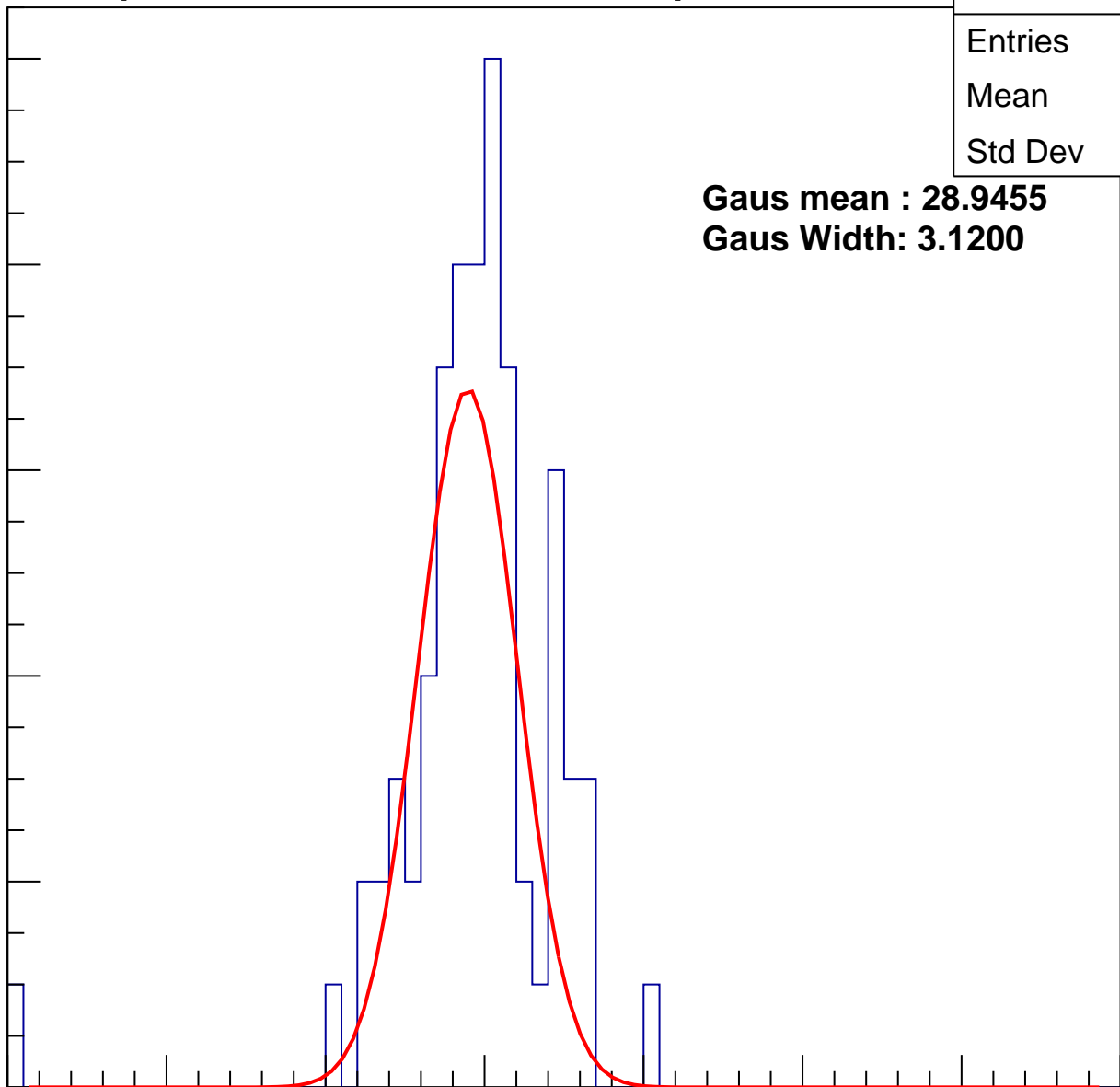
Gaus Width: 3.1200

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch68, adc1

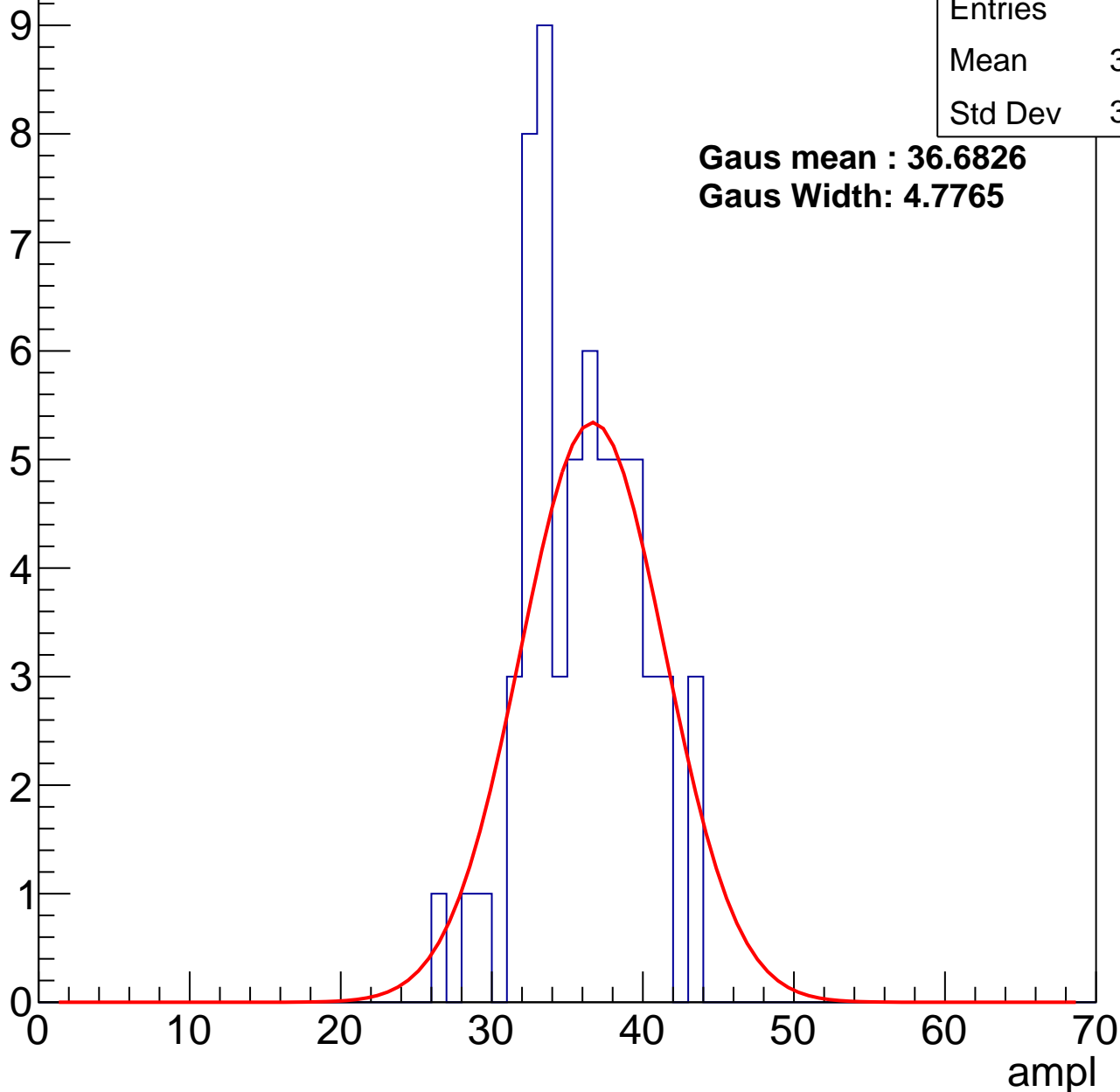
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.48
Std Dev	3.705

Gaus mean : 36.6826

Gaus Width: 4.7765



B0L000S, U2-ch68, adc2

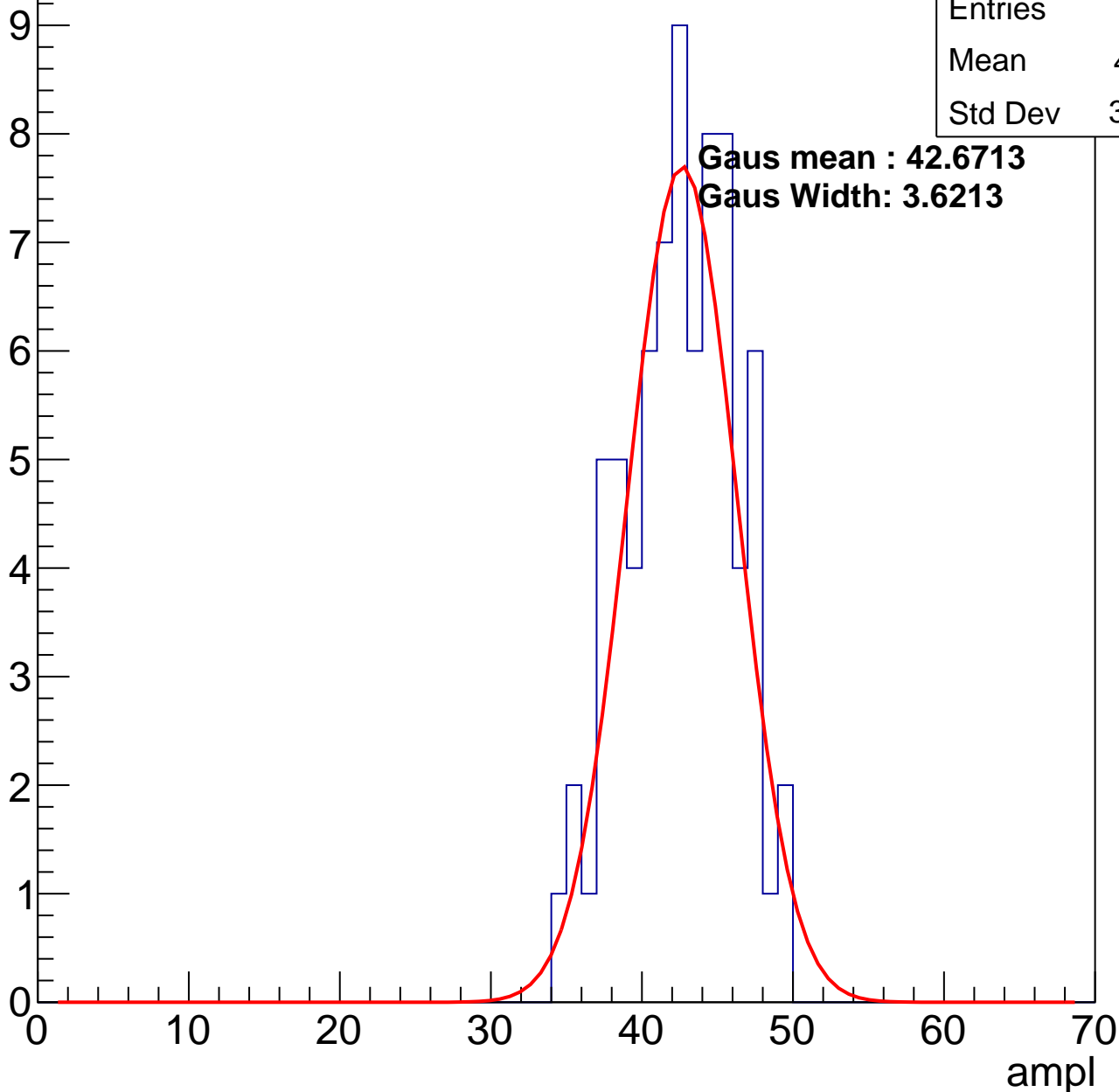
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	42.11
Std Dev	3.516

Gaus mean : 42.6713

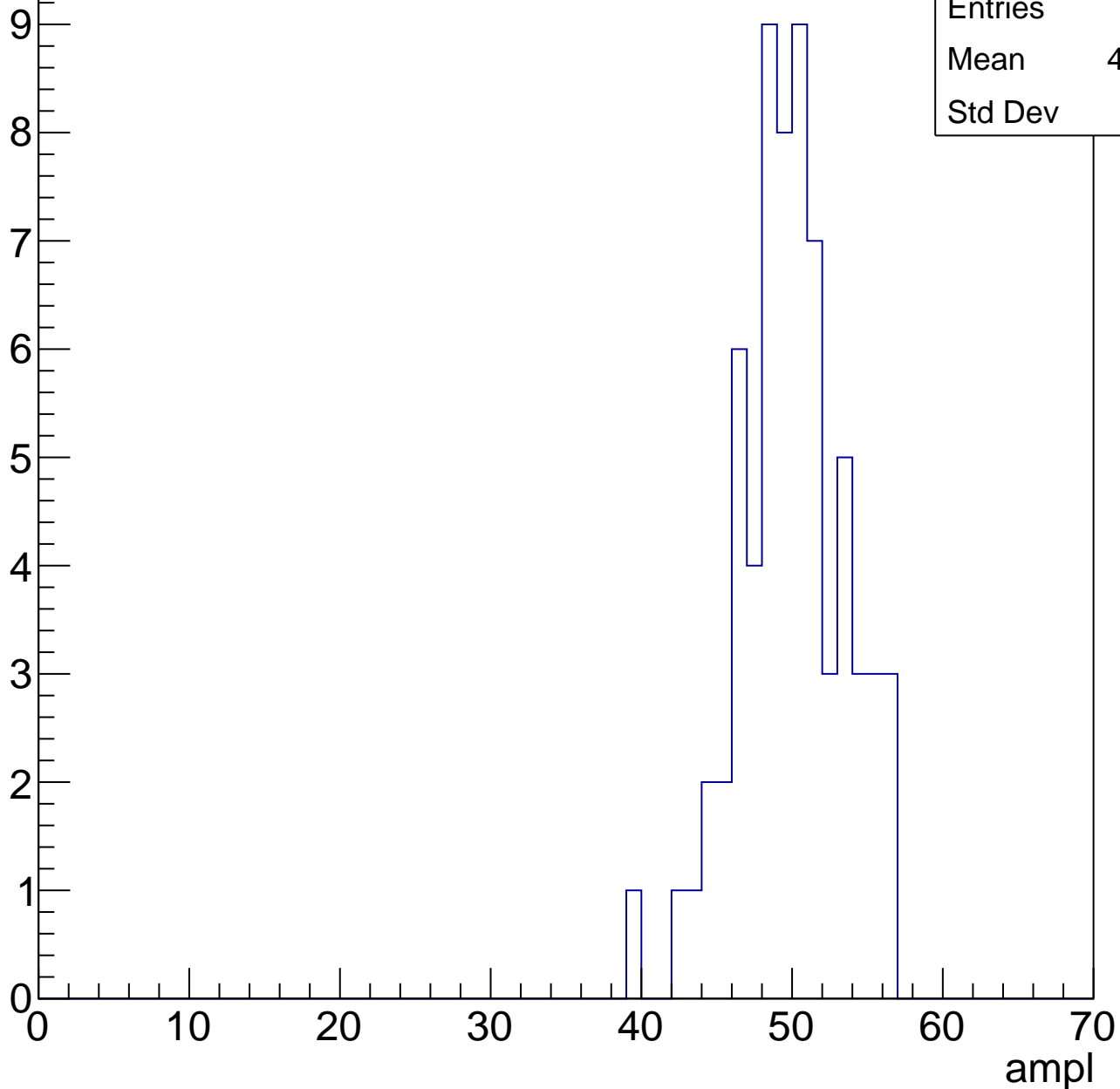
Gaus Width: 3.6213



B0L000S, U2-ch68, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

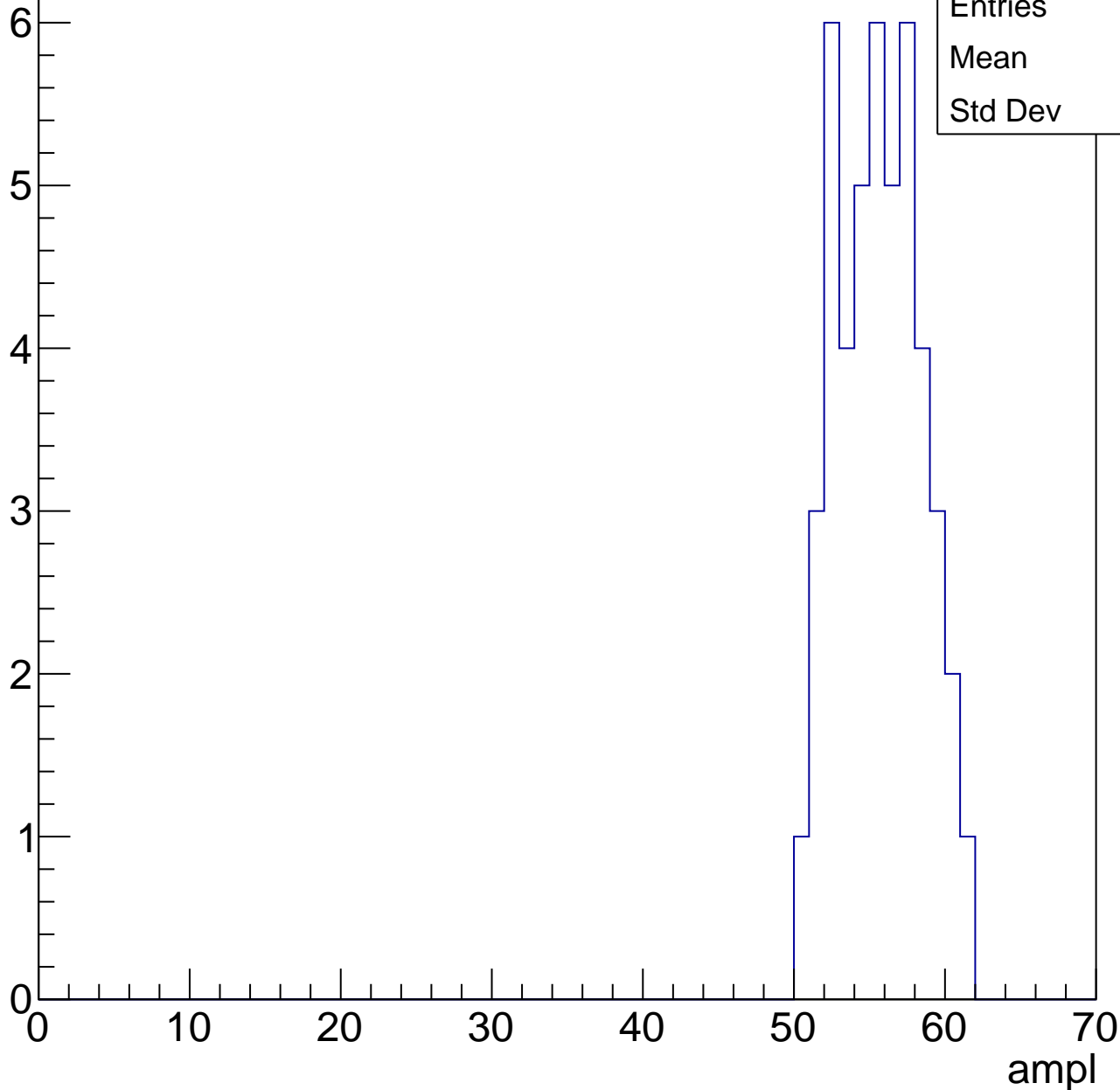


Entries	67
Mean	49.45
Std Dev	3.47

B0L000S, U2-ch68, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch68, adc5

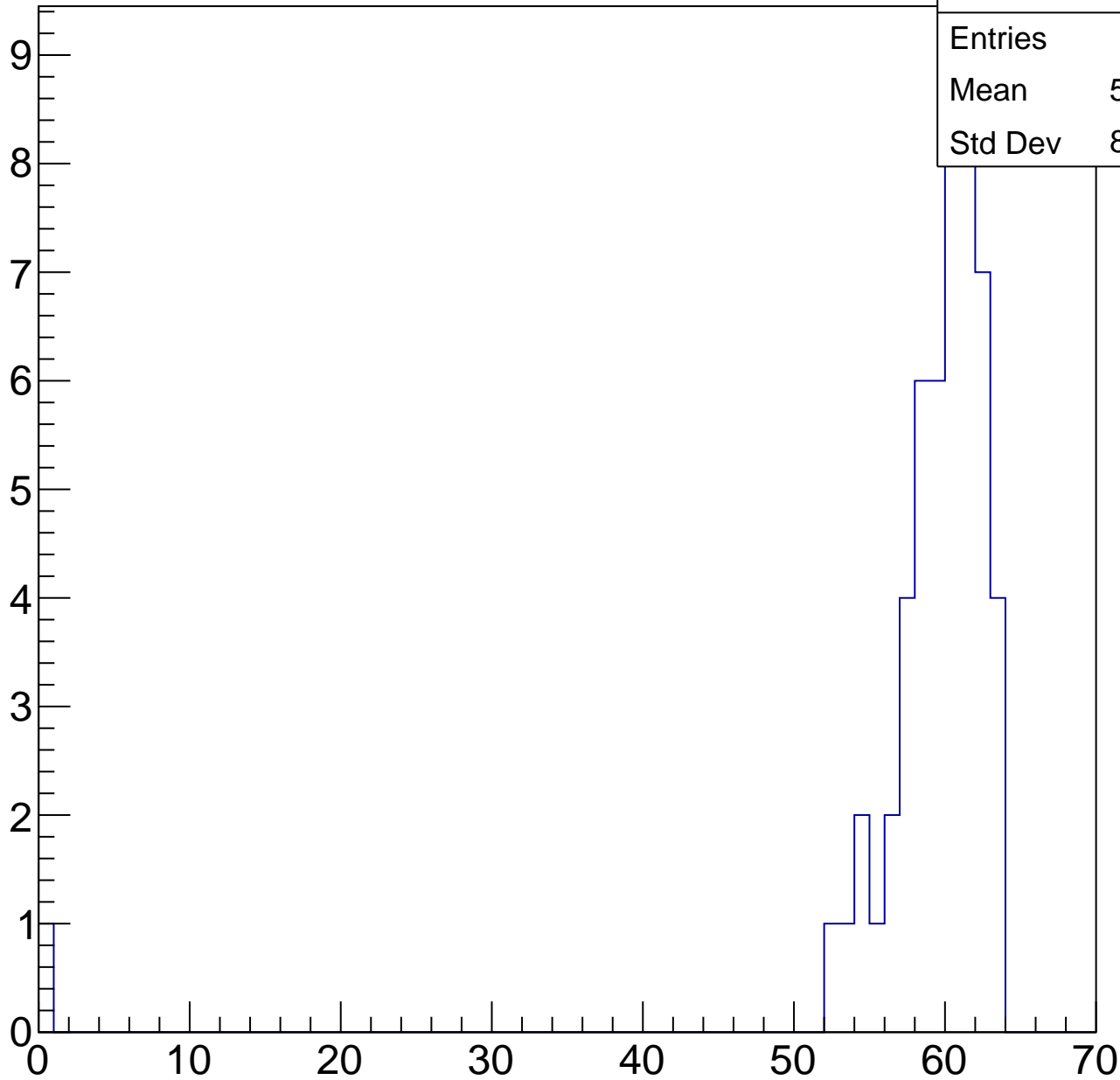
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.17
Std Dev	8.557

ampl

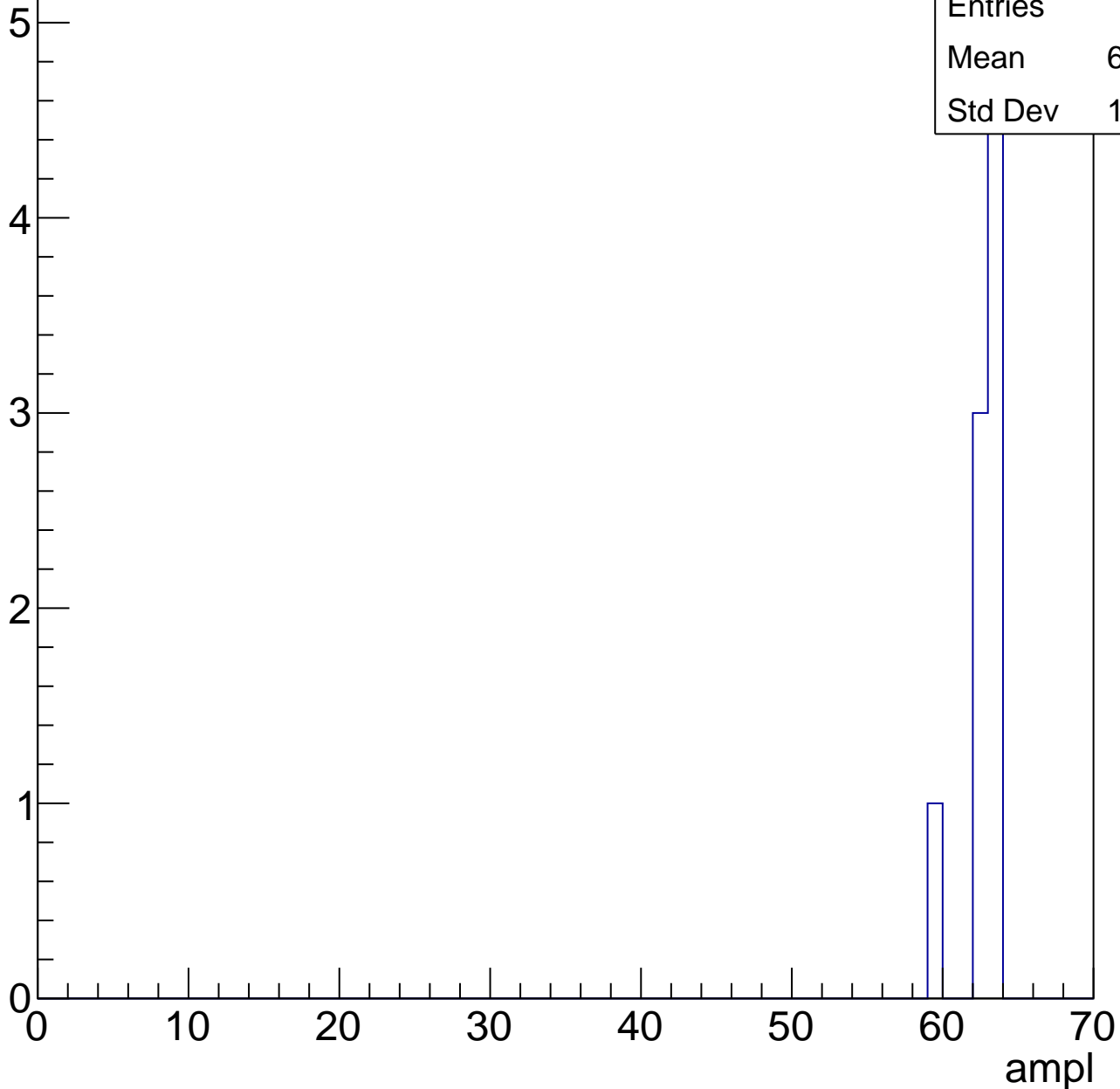


B0L000S, U2-ch68, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.22
Std Dev	1.227



B0L000S, U2-ch68, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L000S, U2-ch69, adc0

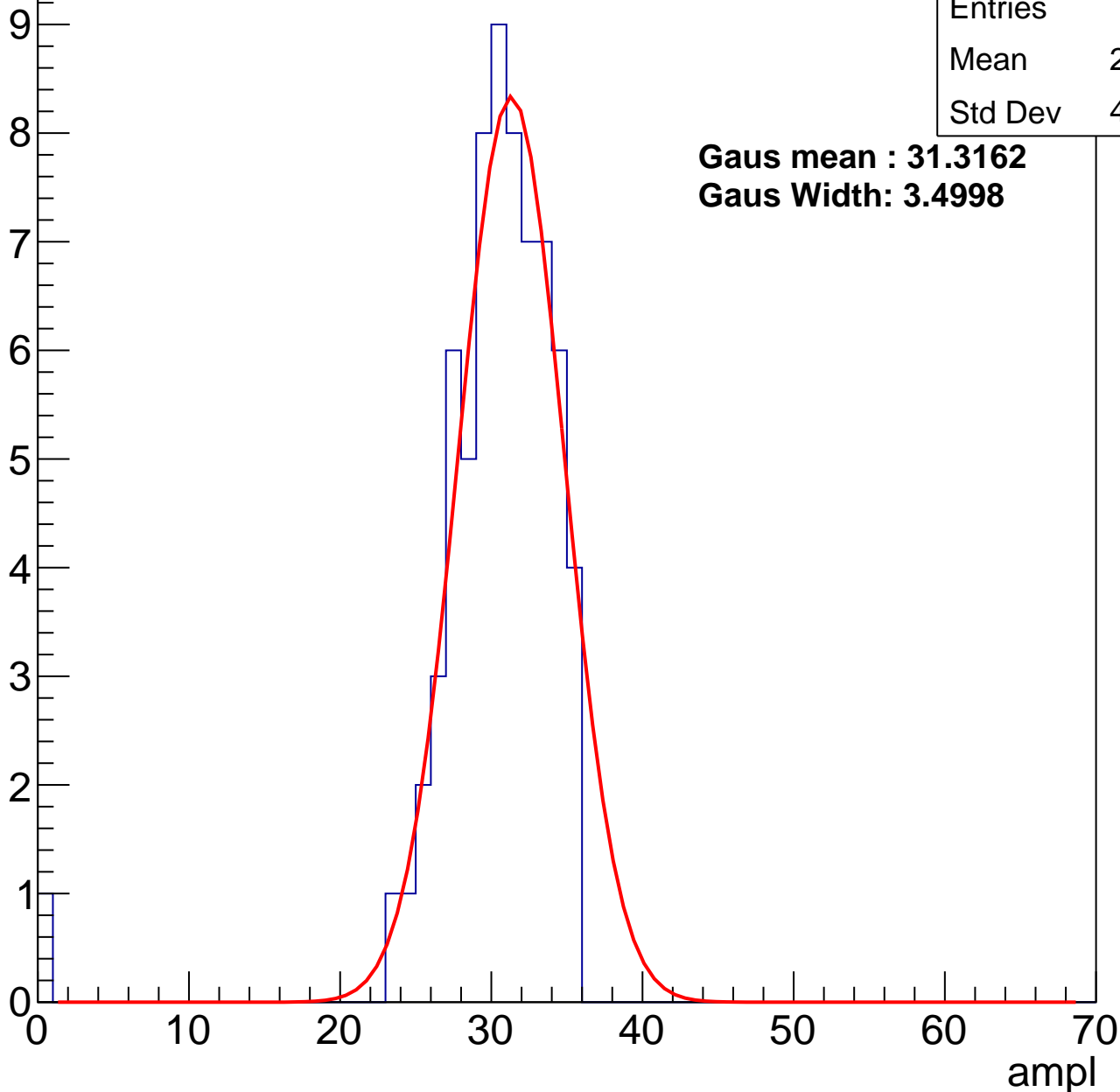
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	29.79
Std Dev	4.629

Gaus mean : 31.3162

Gaus Width: 3.4998



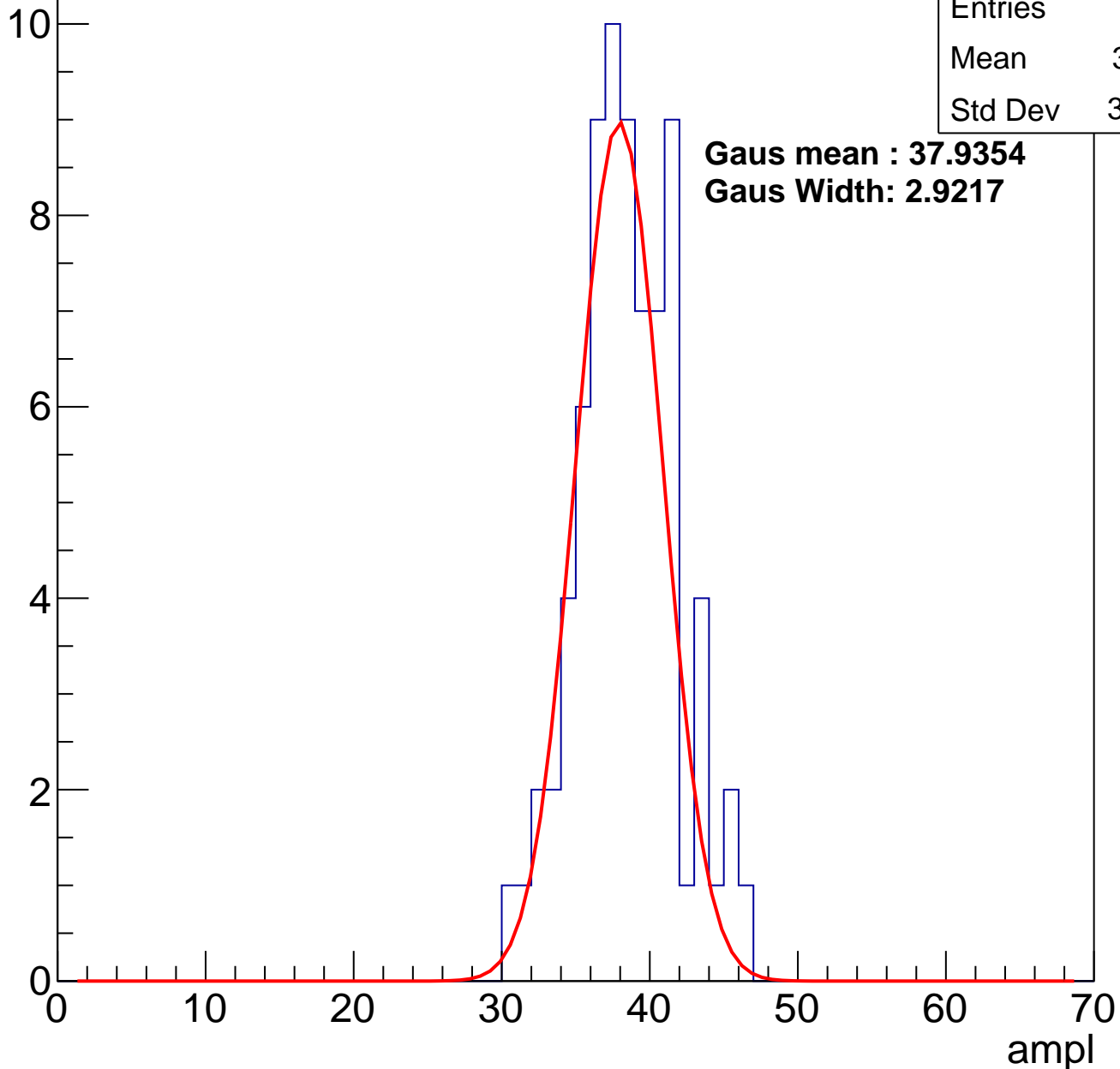
B0L000S, U2-ch69, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	76
Mean	38.01
Std Dev	3.303

Gaus mean : 37.9354
Gaus Width: 2.9217

Entry



B0L000S, U2-ch69, adc2

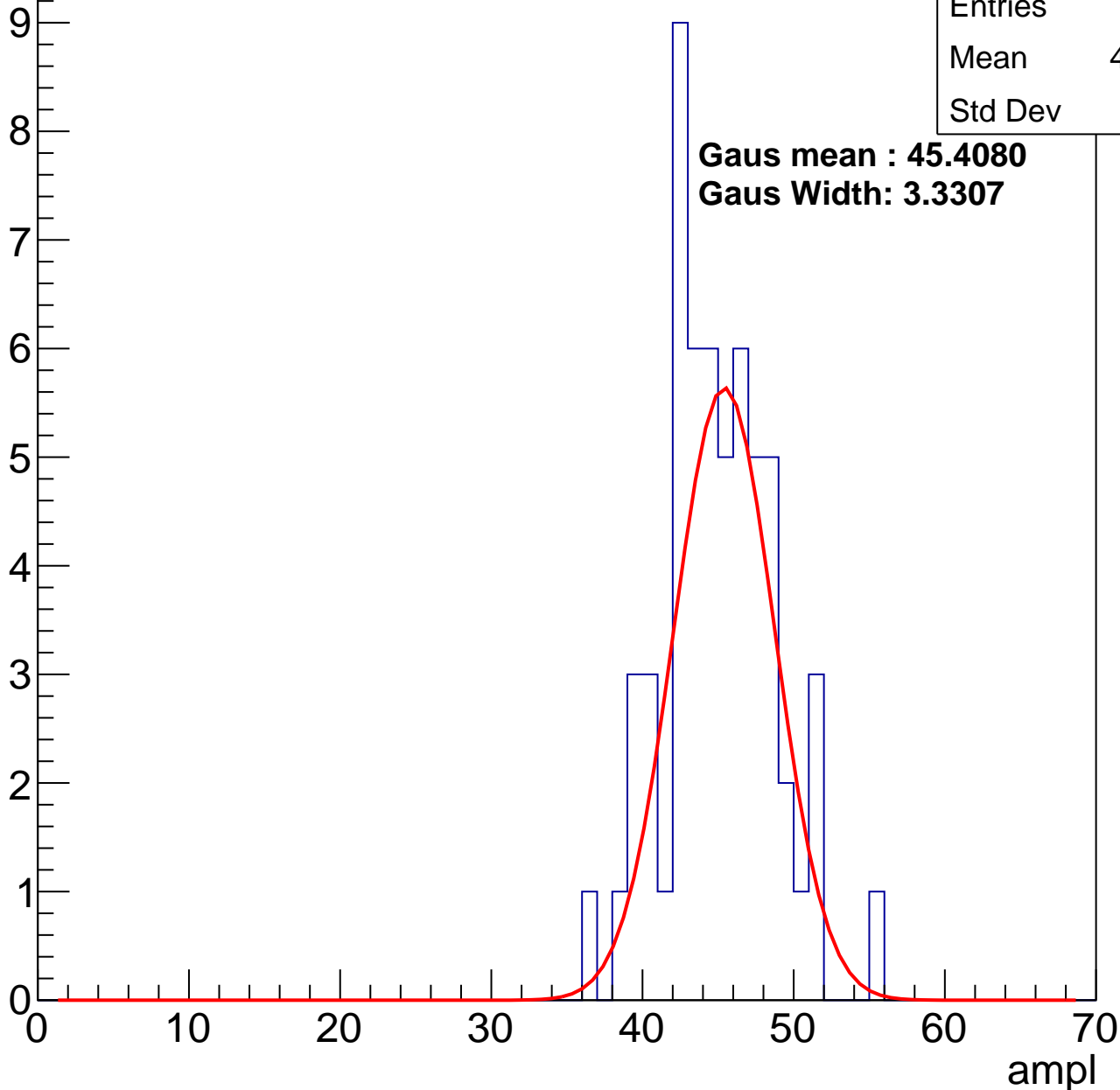
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.55
Std Dev	3.63

Gaus mean : 45.4080

Gaus Width: 3.3307

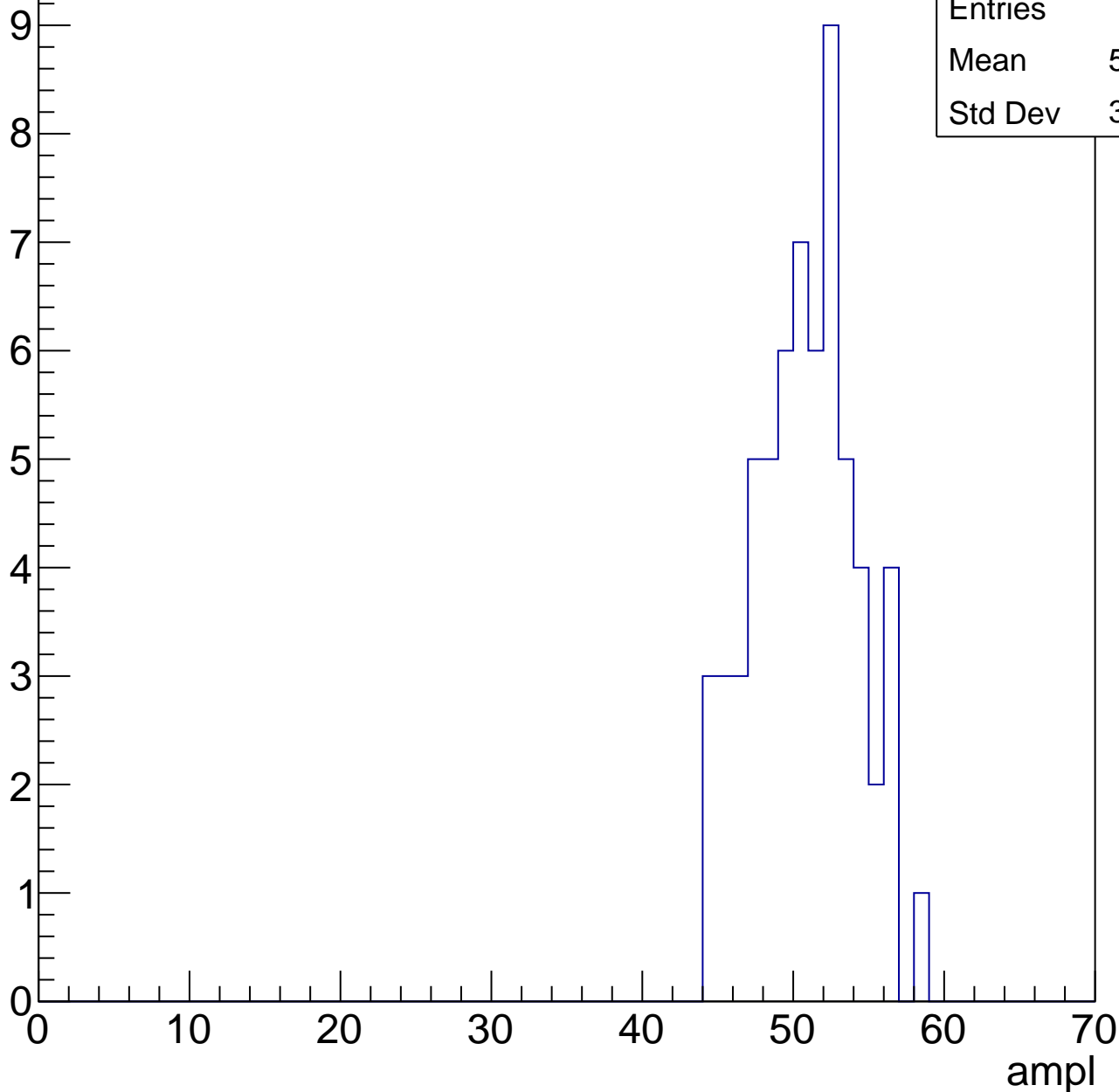


B0L000S, U2-ch69, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	50.33
Std Dev	3.343



B0L000S, U2-ch69, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	72
Mean	56.86
Std Dev	3.246

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

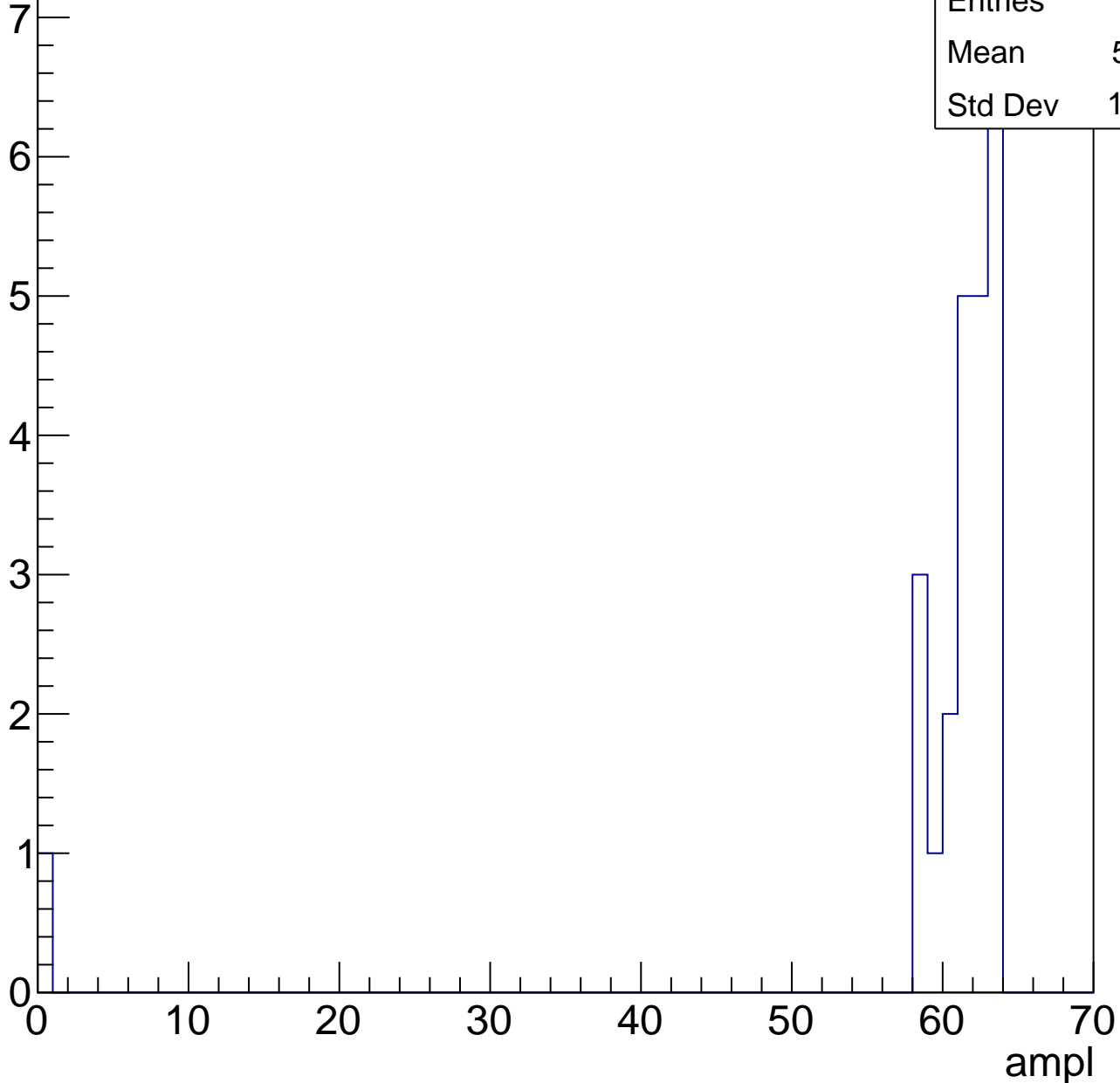
10

B0L000S, U2-ch69, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

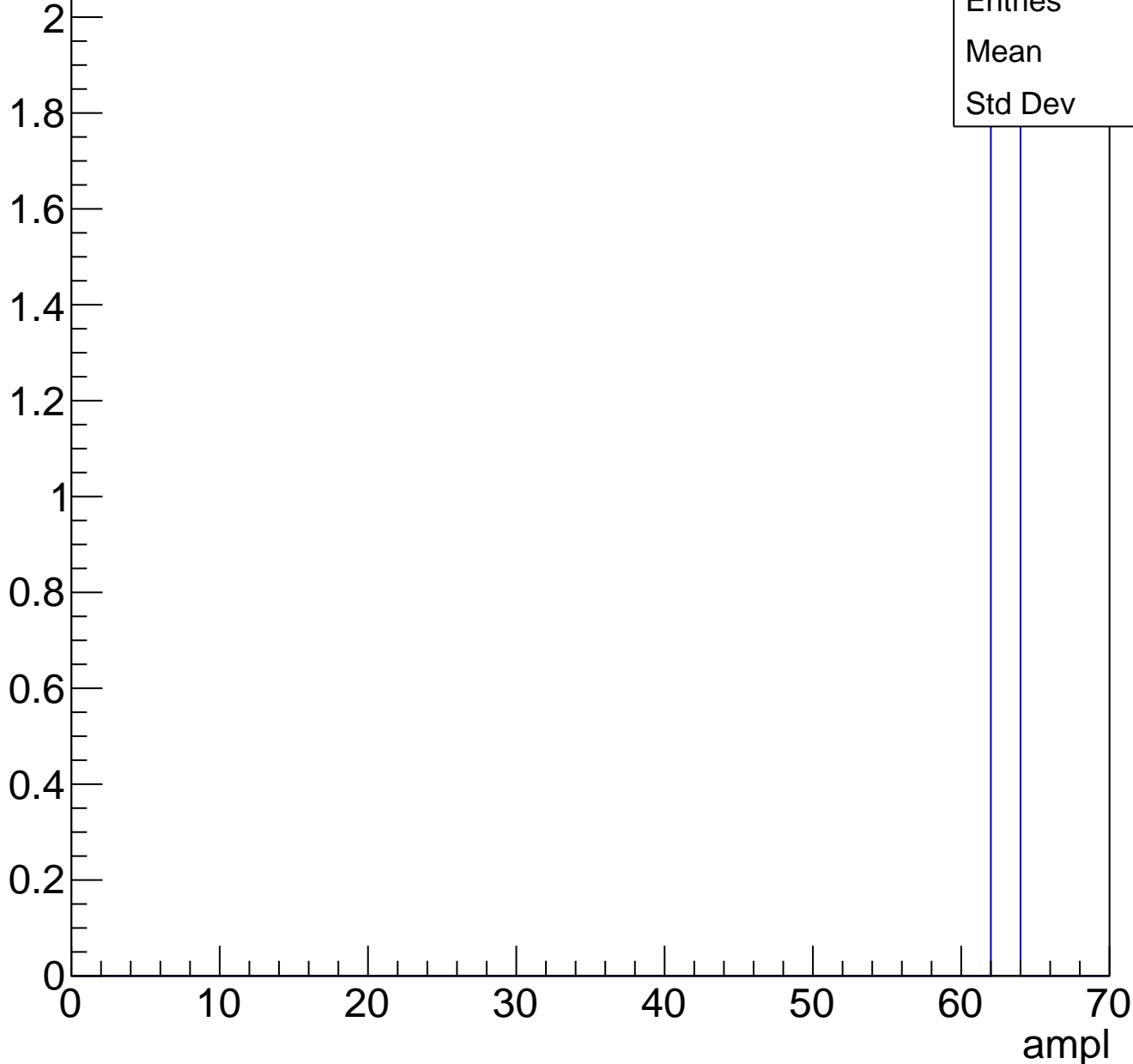
Entries	24
Mean	58.71
Std Dev	12.35



B0L000S, U2-ch69, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch69, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L000S, U2-ch70, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	79
Mean	29.2
Std Dev	3.365

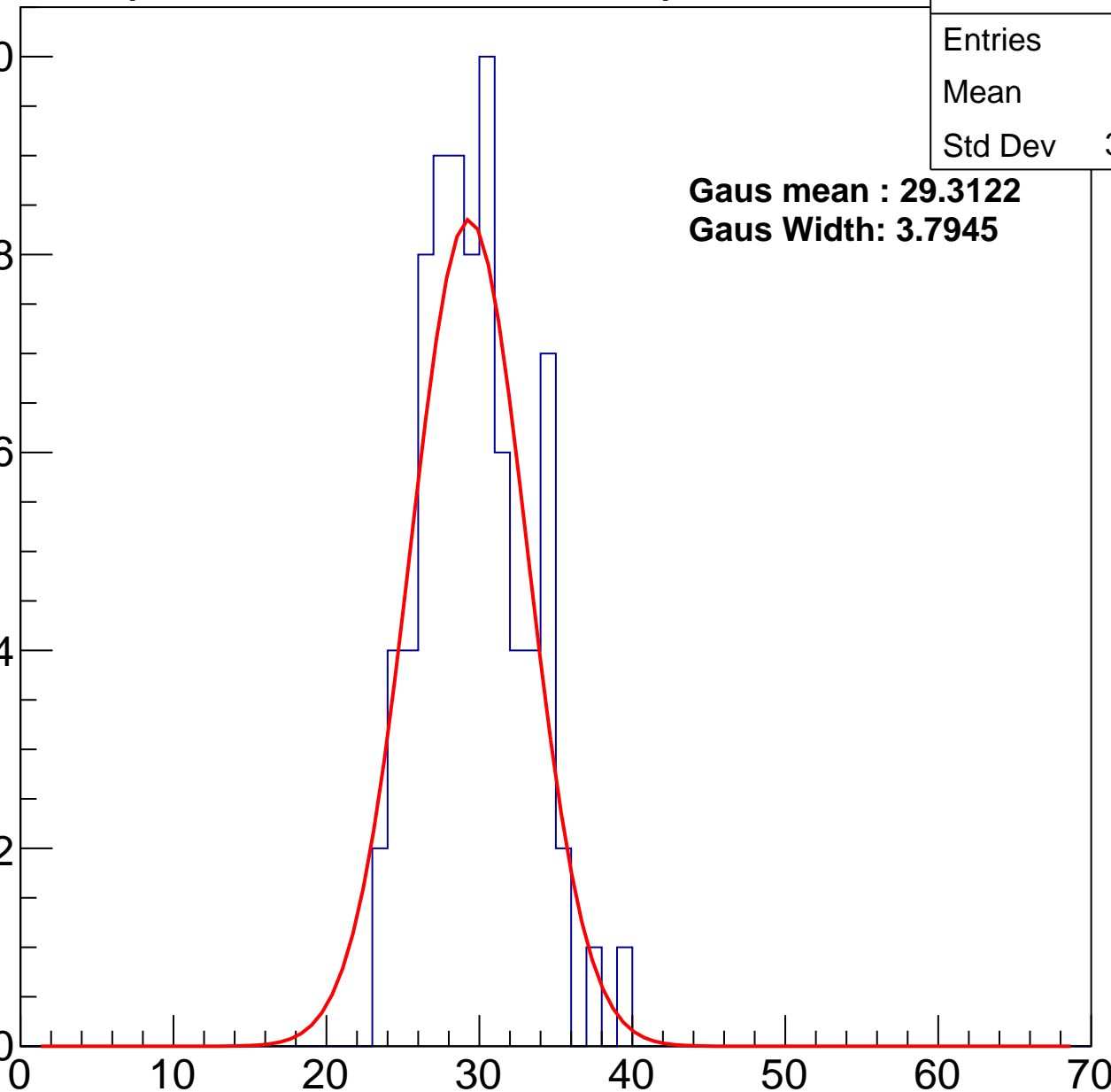
Gaus mean : 29.3122

Gaus Width: 3.7945

Entry

10
8
6
4
2
0

ampl



B0L000S, U2-ch70, adc1

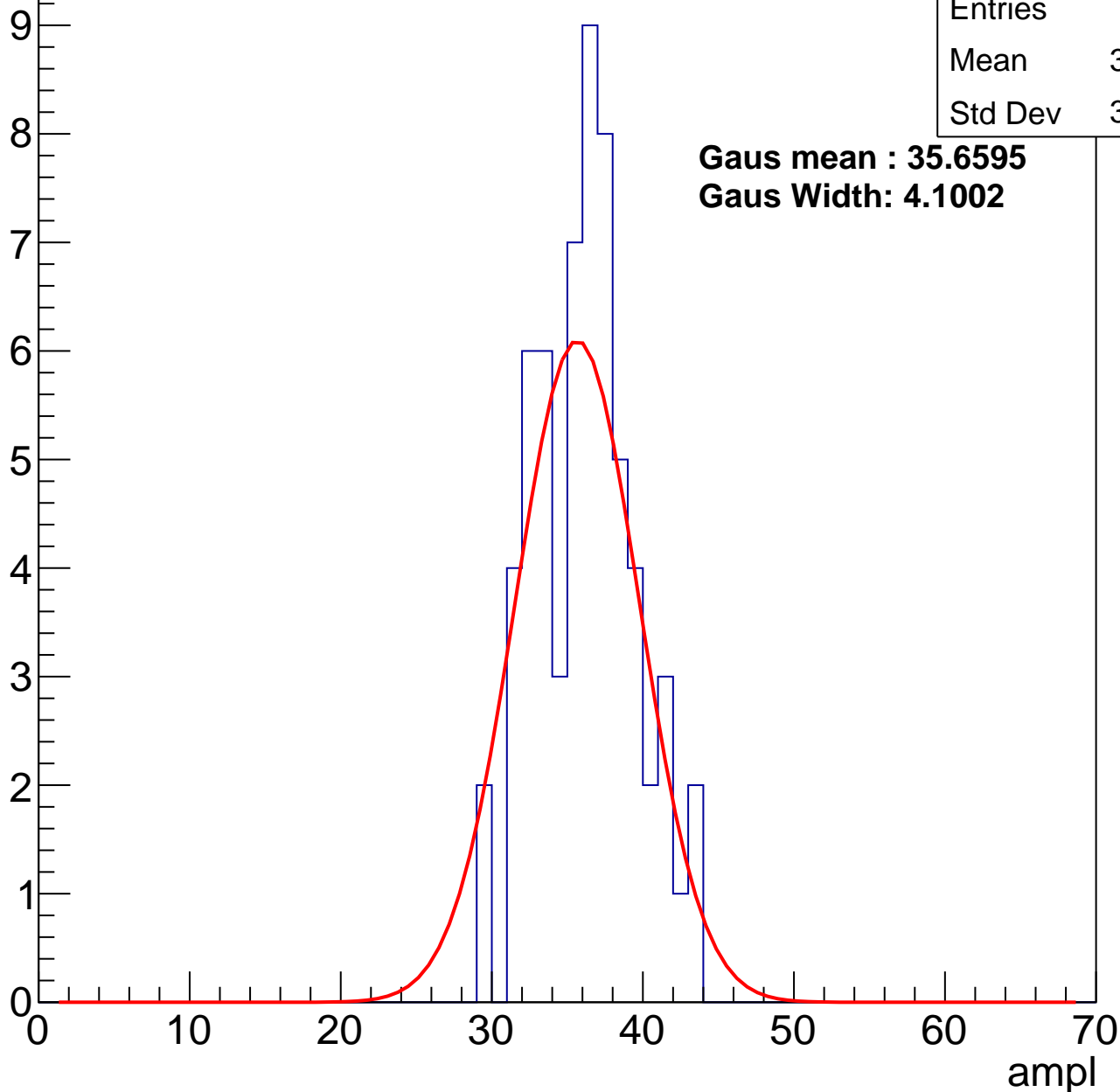
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	35.74
Std Dev	3.282

Gaus mean : 35.6595

Gaus Width: 4.1002



B0L000S, U2-ch70, adc2

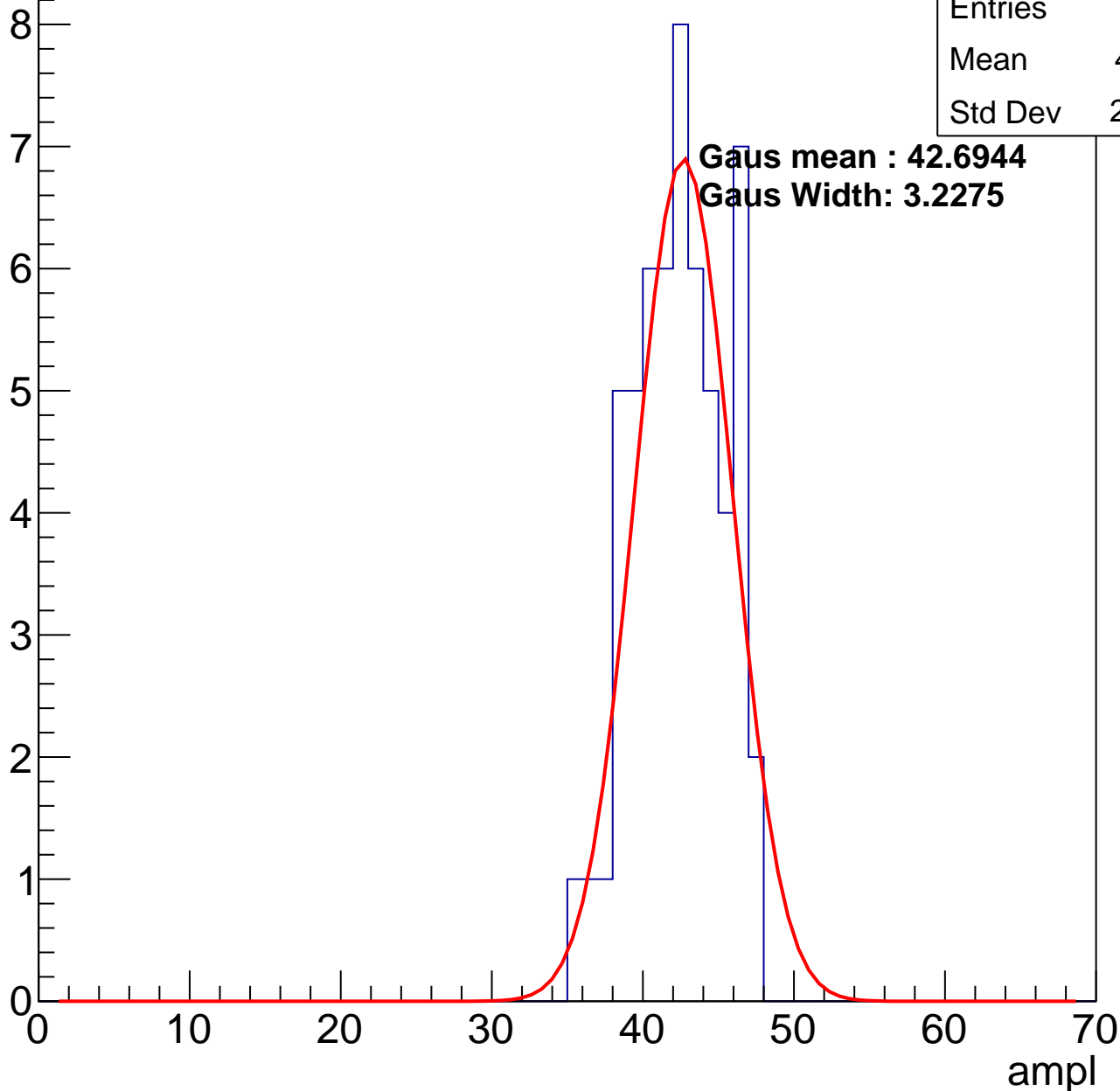
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	41.91
Std Dev	2.928

Gaus mean : 42.6944

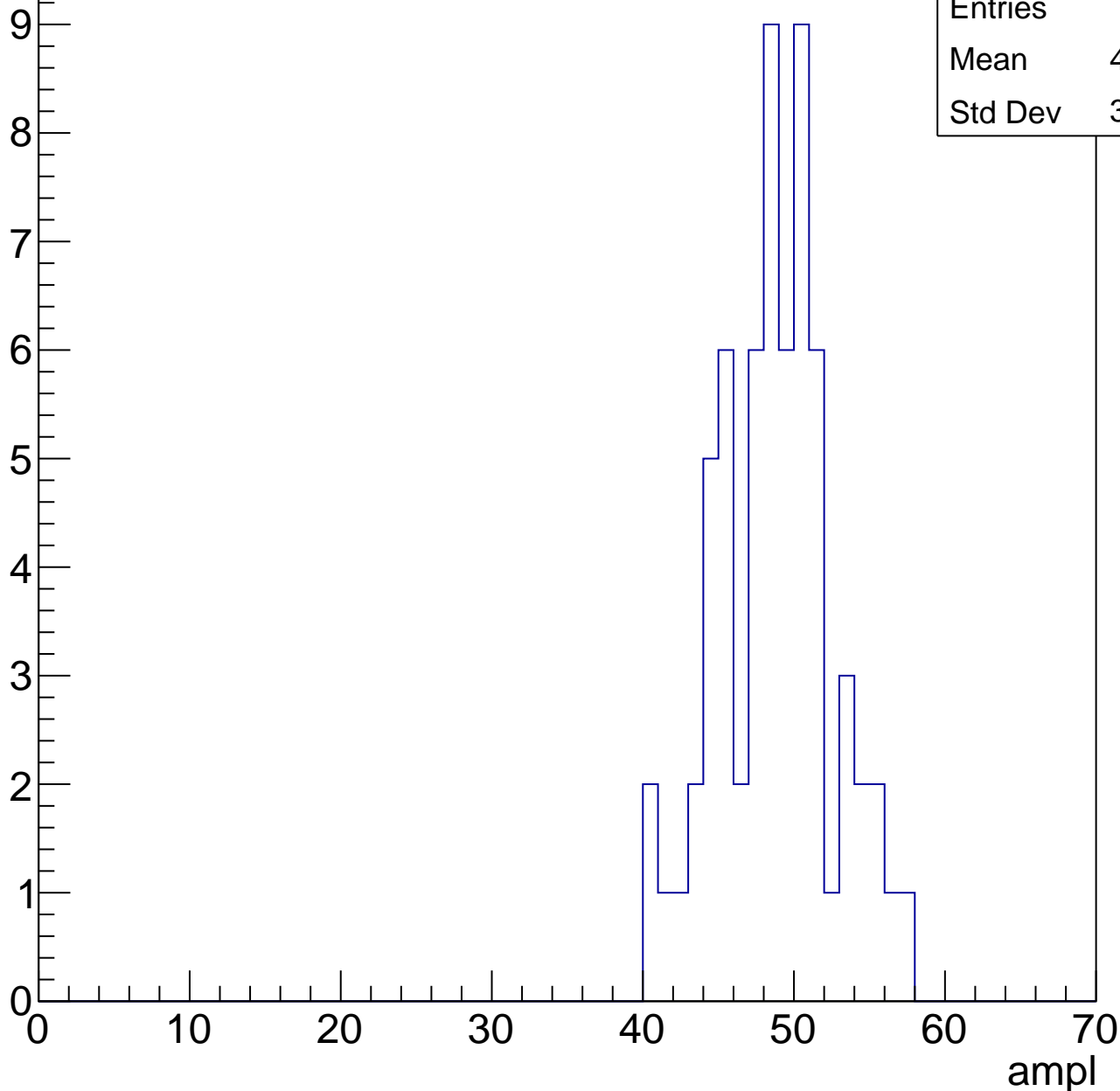
Gaus Width: 3.2275



B0L000S, U2-ch70, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



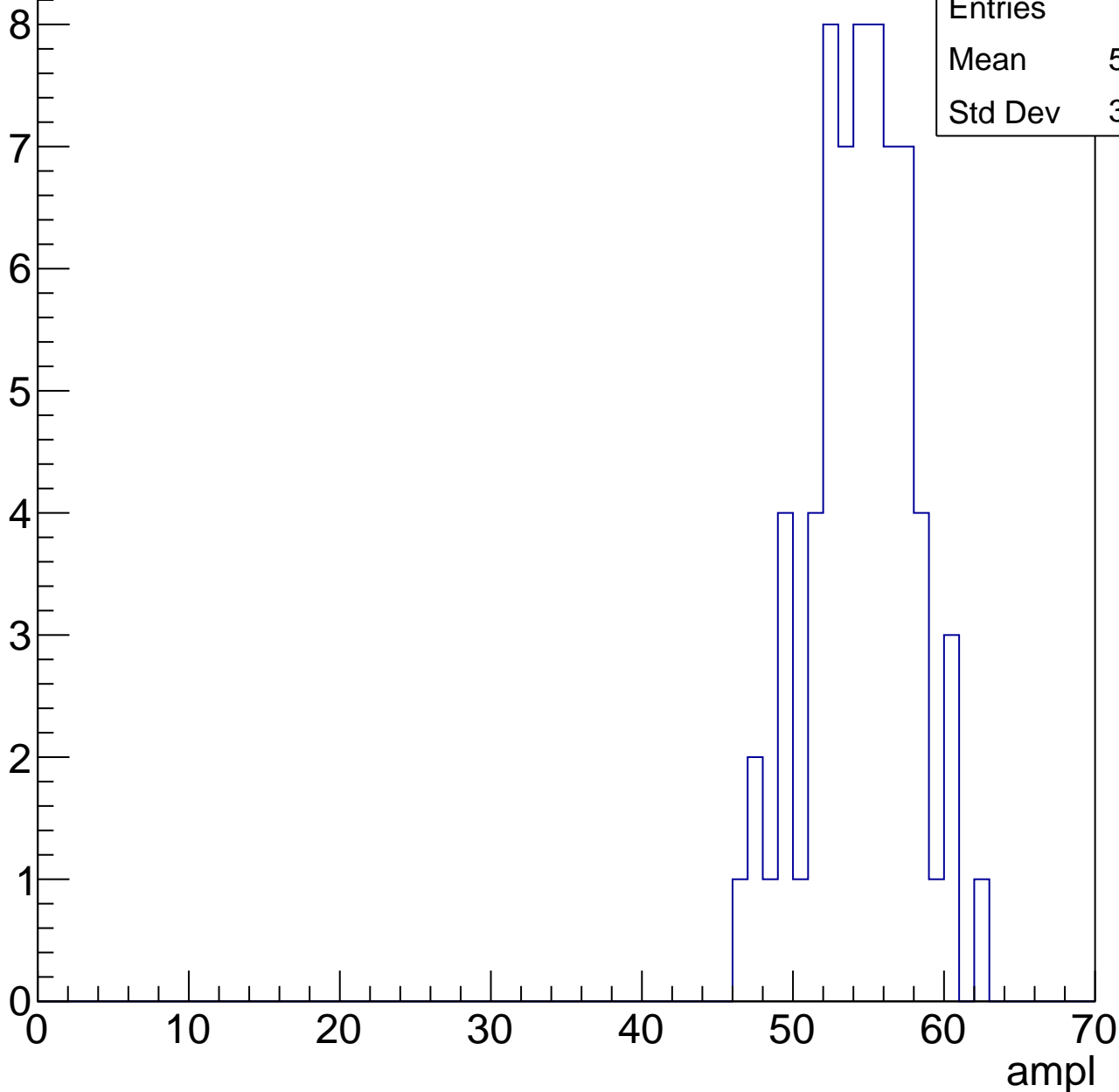
Entries	65
Mean	48.26
Std Dev	3.739

B0L000S, U2-ch70, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	54.04
Std Dev	3.343



B0L000S, U2-ch70, adc5

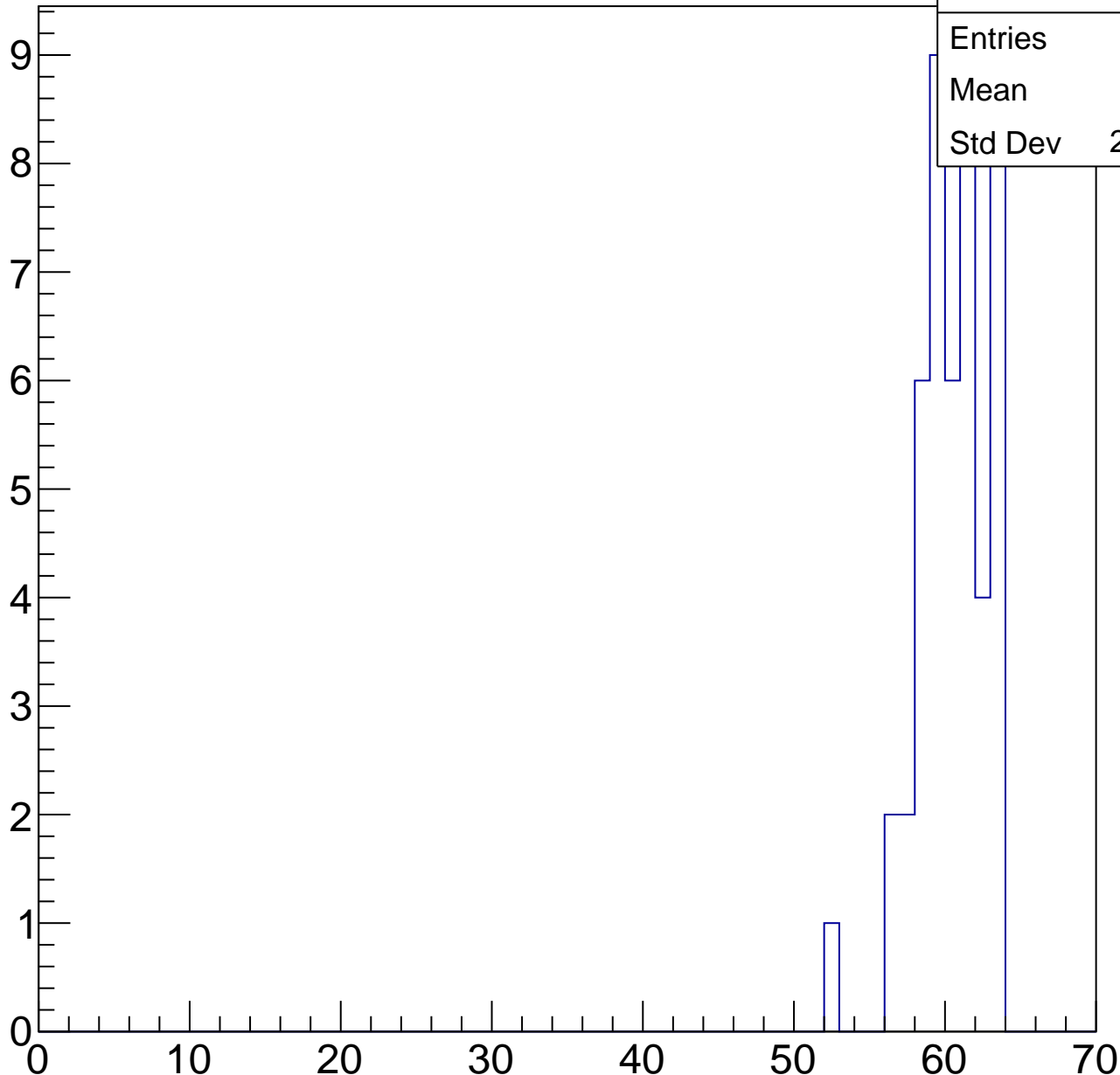
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	60
Std Dev	2.316

ampl

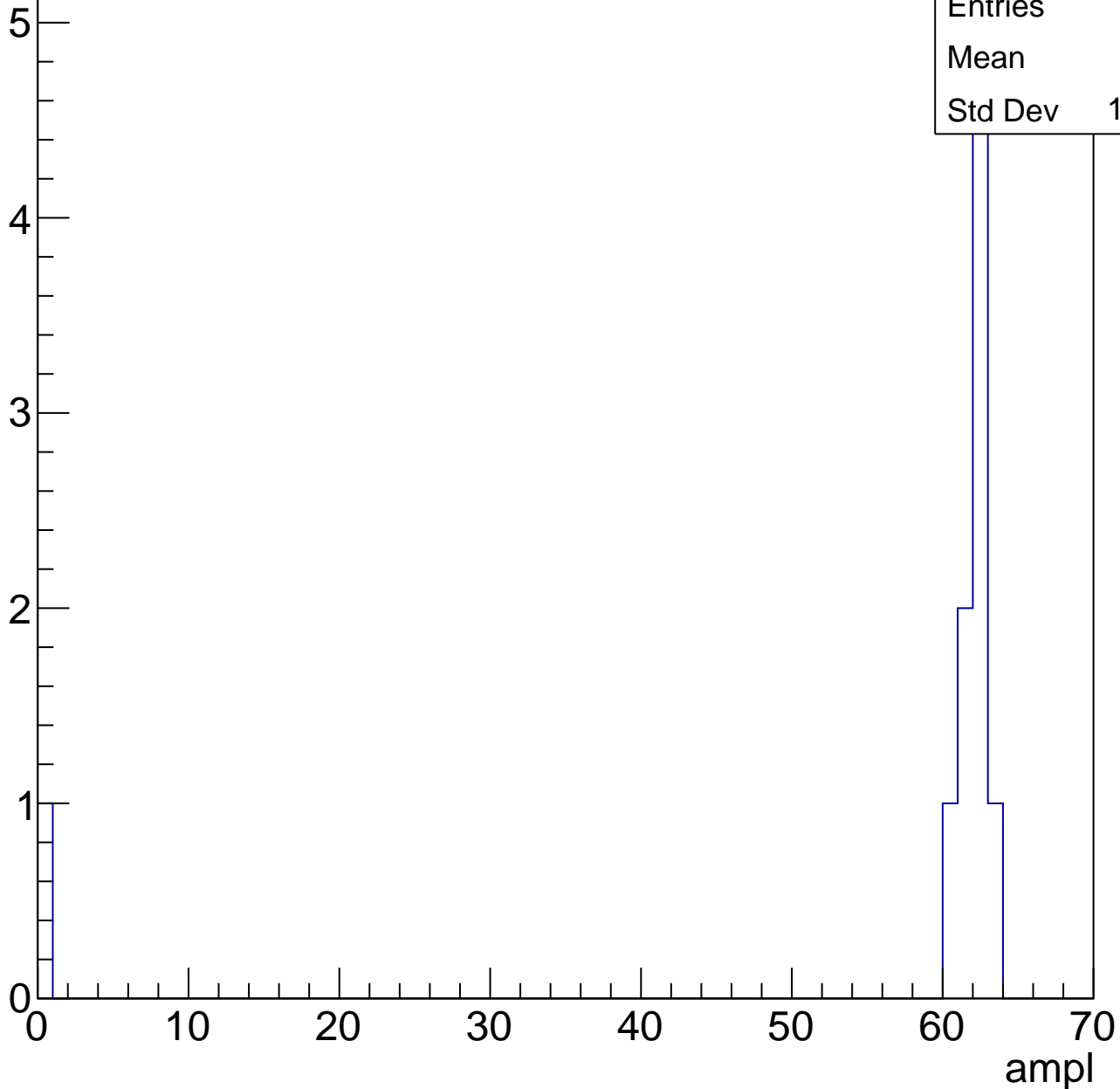


B0L000S, U2-ch70, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	10
Mean	55.5
Std Dev	18.52



B0L000S, U2-ch70, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch71, adc0

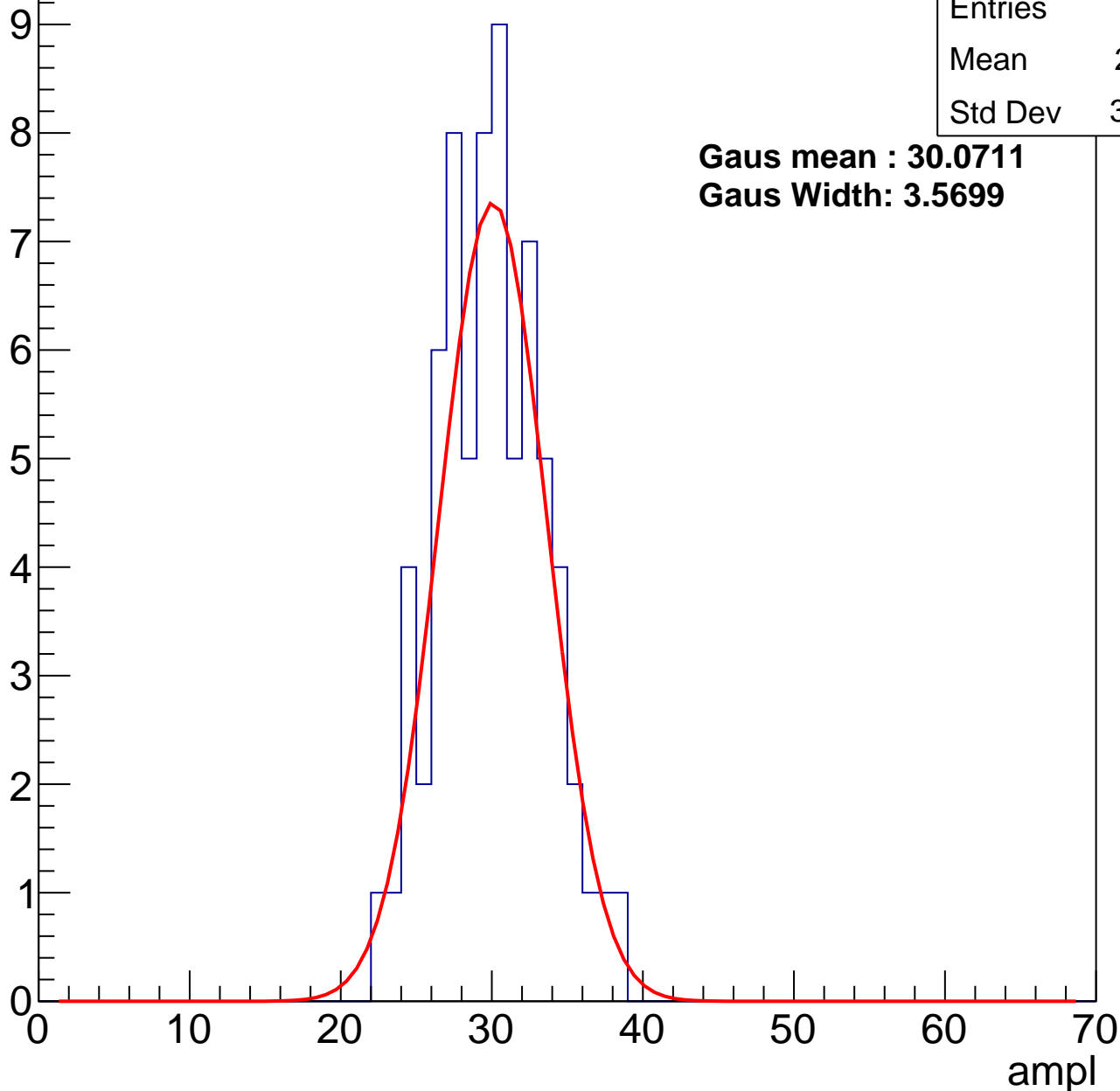
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.51
Std Dev	3.438

Gaus mean : 30.0711

Gaus Width: 3.5699



B0L000S, U2-ch71, adc1

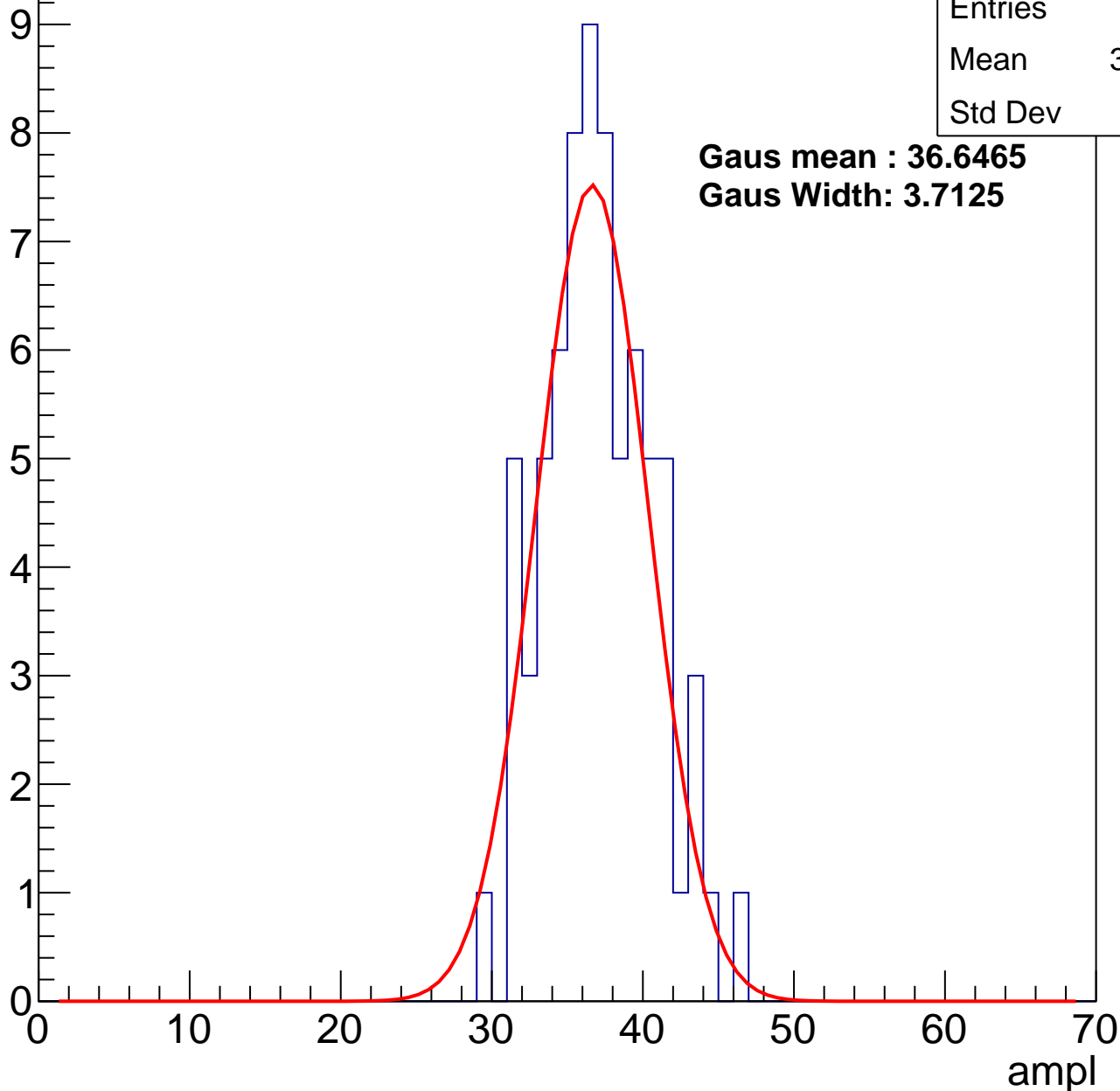
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.65
Std Dev	3.54

Gaus mean : 36.6465

Gaus Width: 3.7125



B0L000S, U2-ch71, adc2

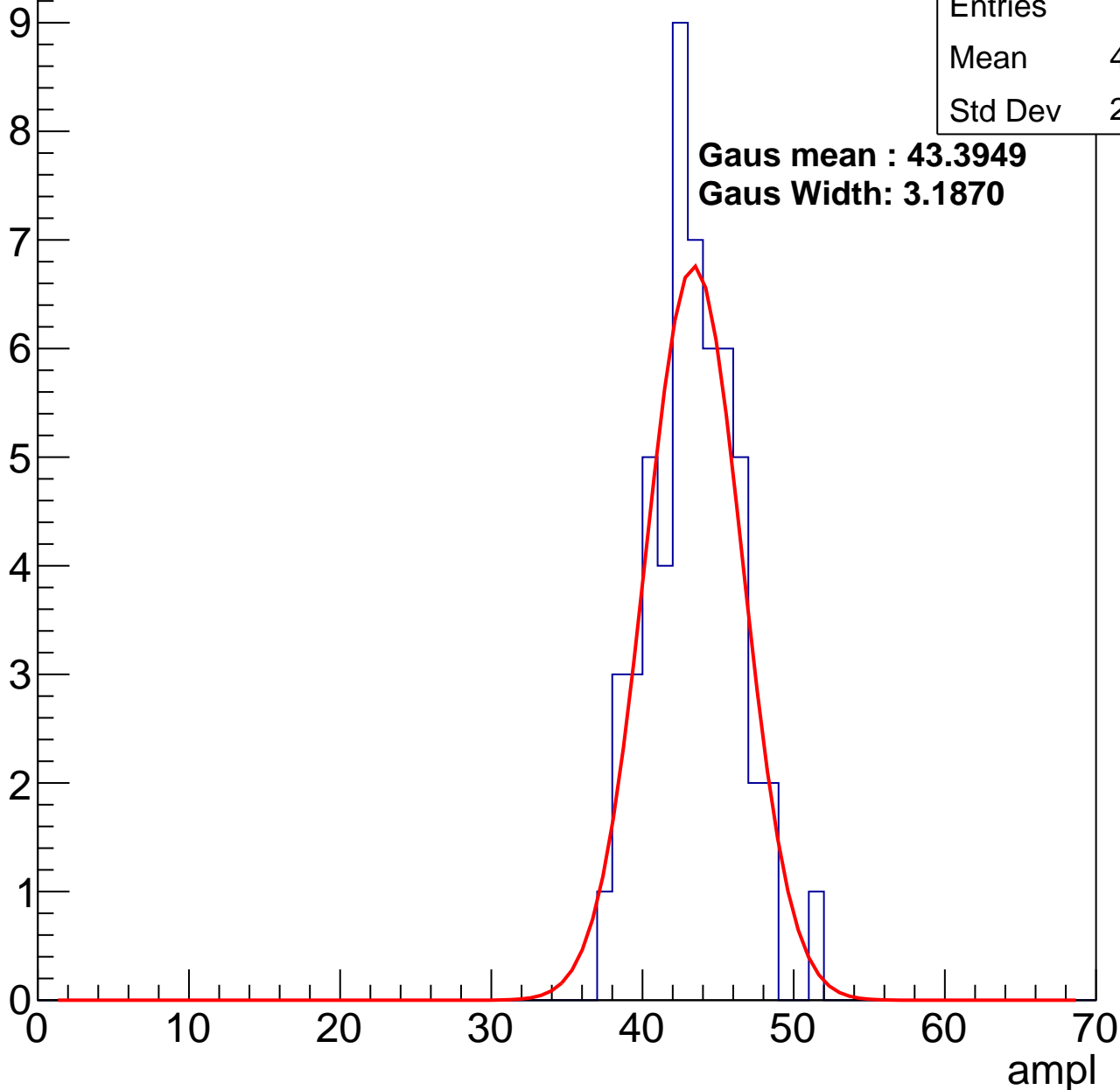
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	42.89
Std Dev	2.885

Gaus mean : 43.3949

Gaus Width: 3.1870



B0L000S, U2-ch71, adc3

calib_packv5_042523_0143.root, FC#5, port B1

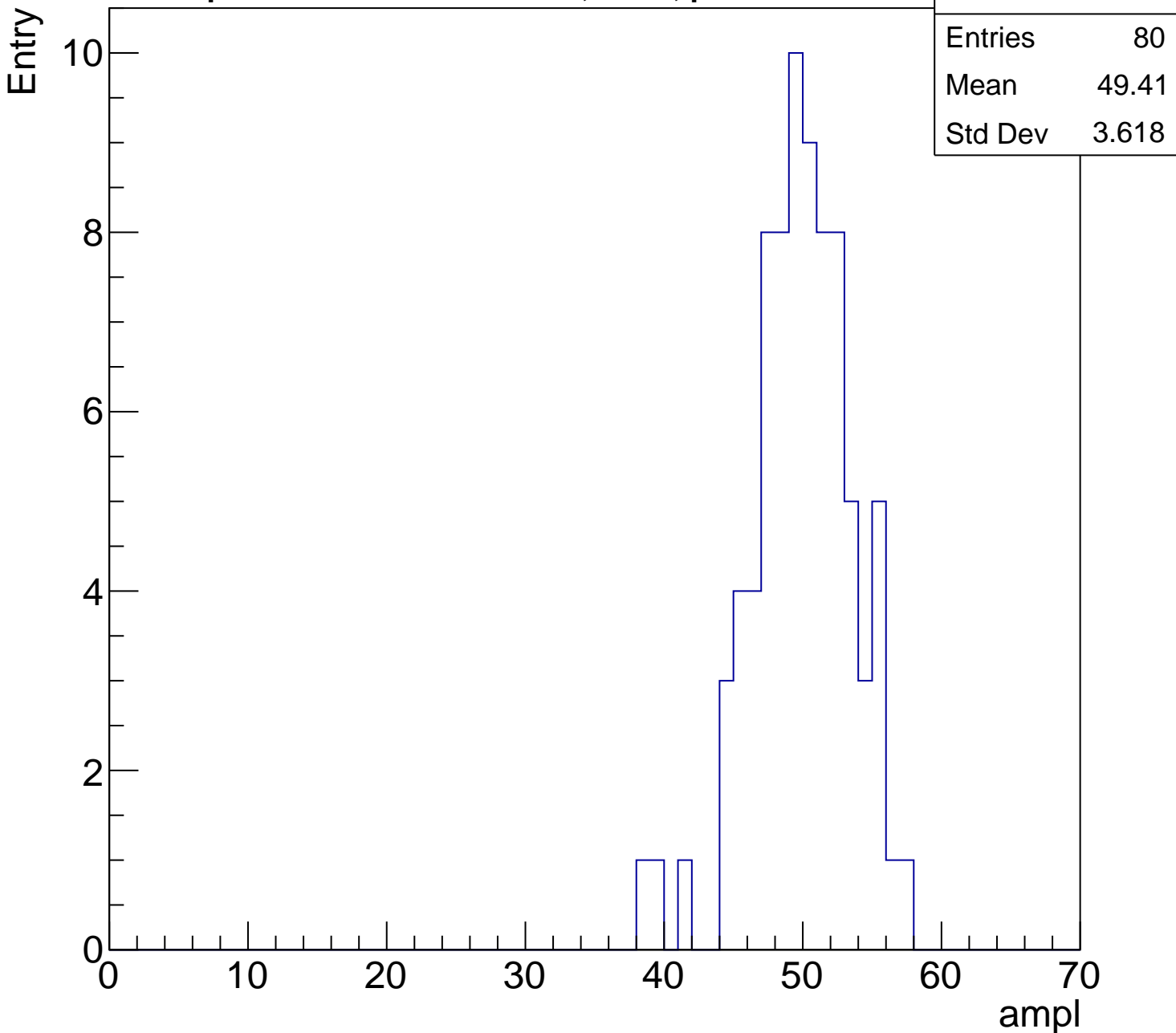
Entries	80
Mean	49.41
Std Dev	3.618

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

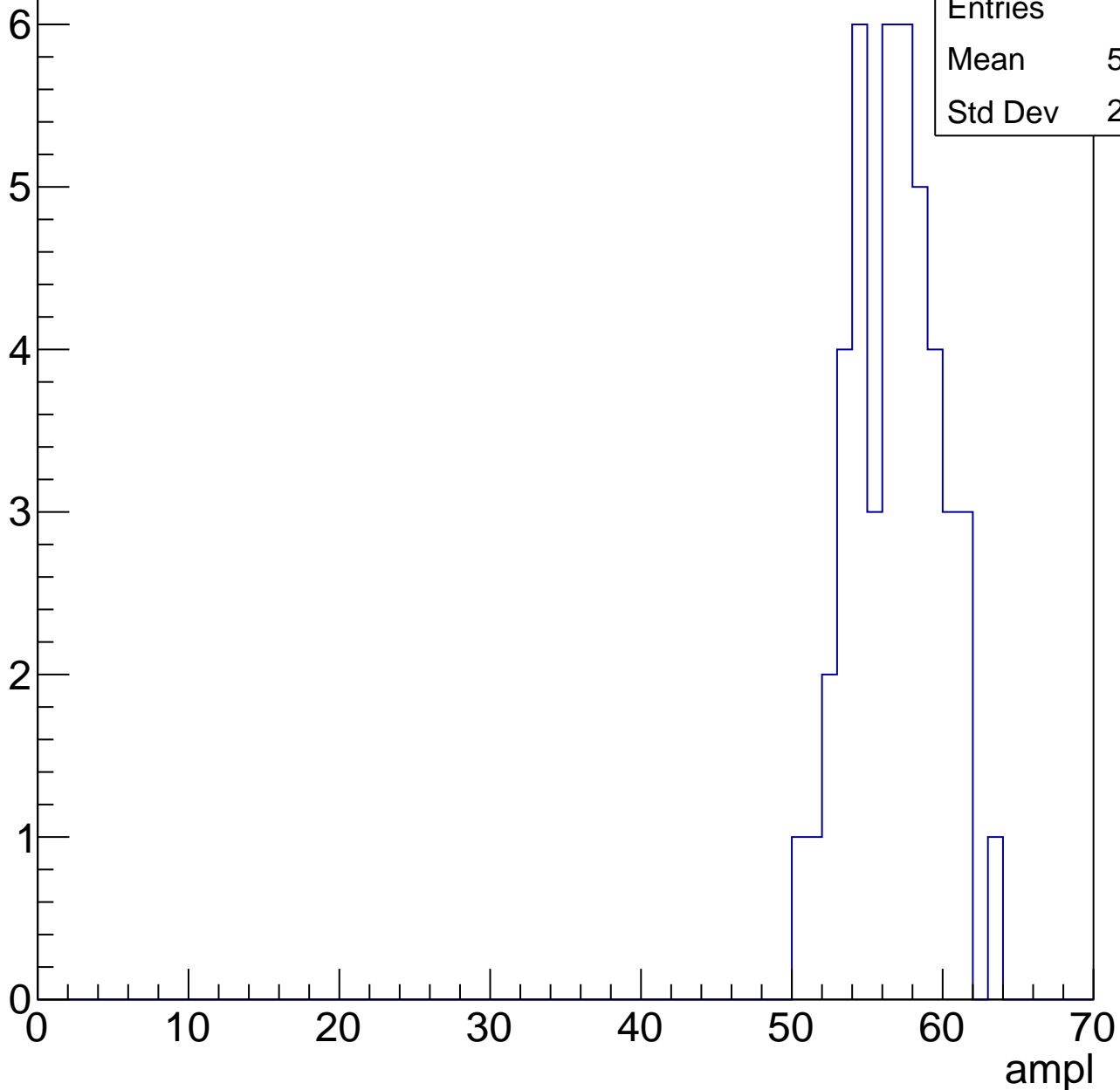


B0L000S, U2-ch71, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	45
Mean	56.36
Std Dev	2.922

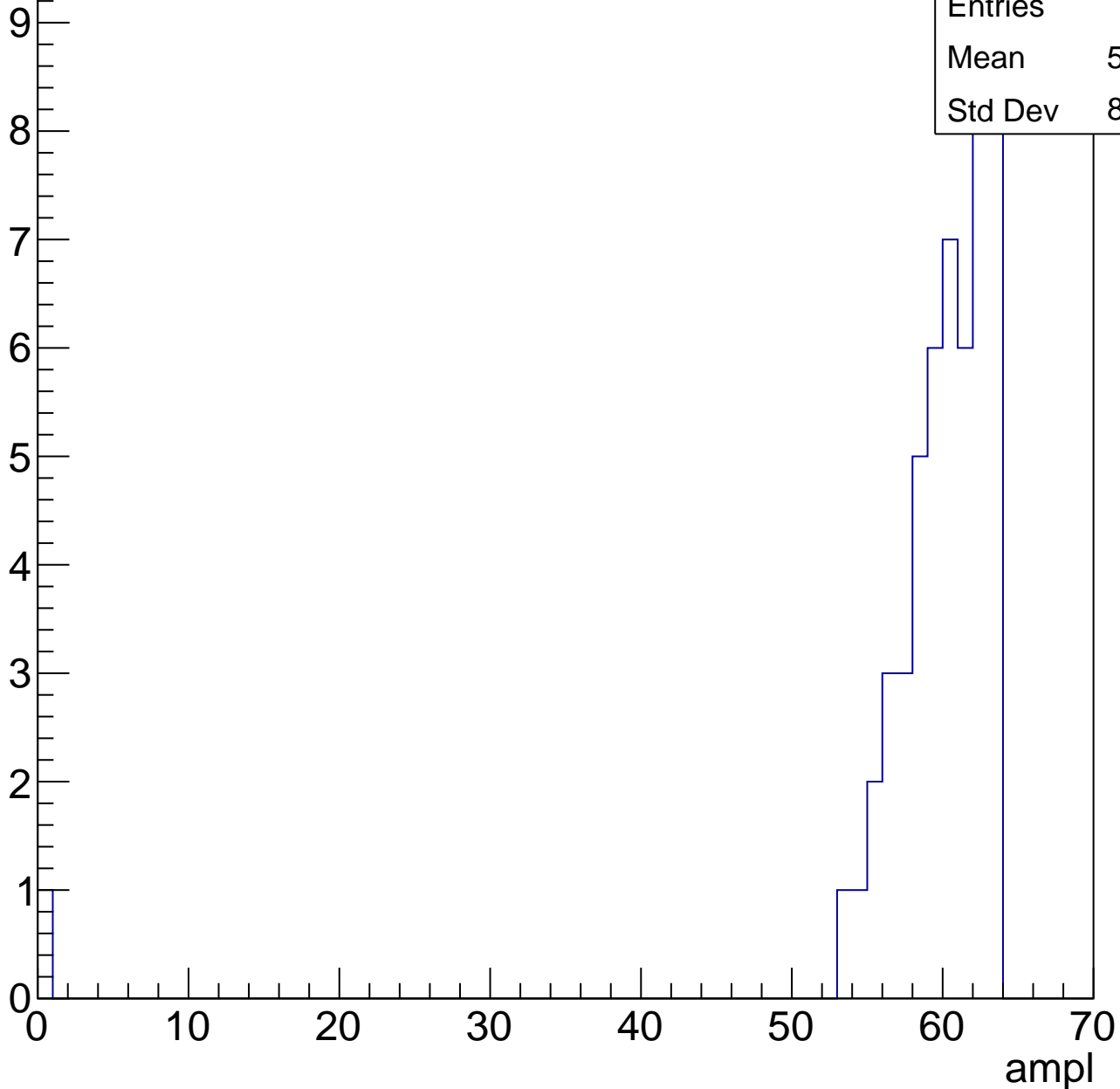


B0L000S, U2-ch71, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

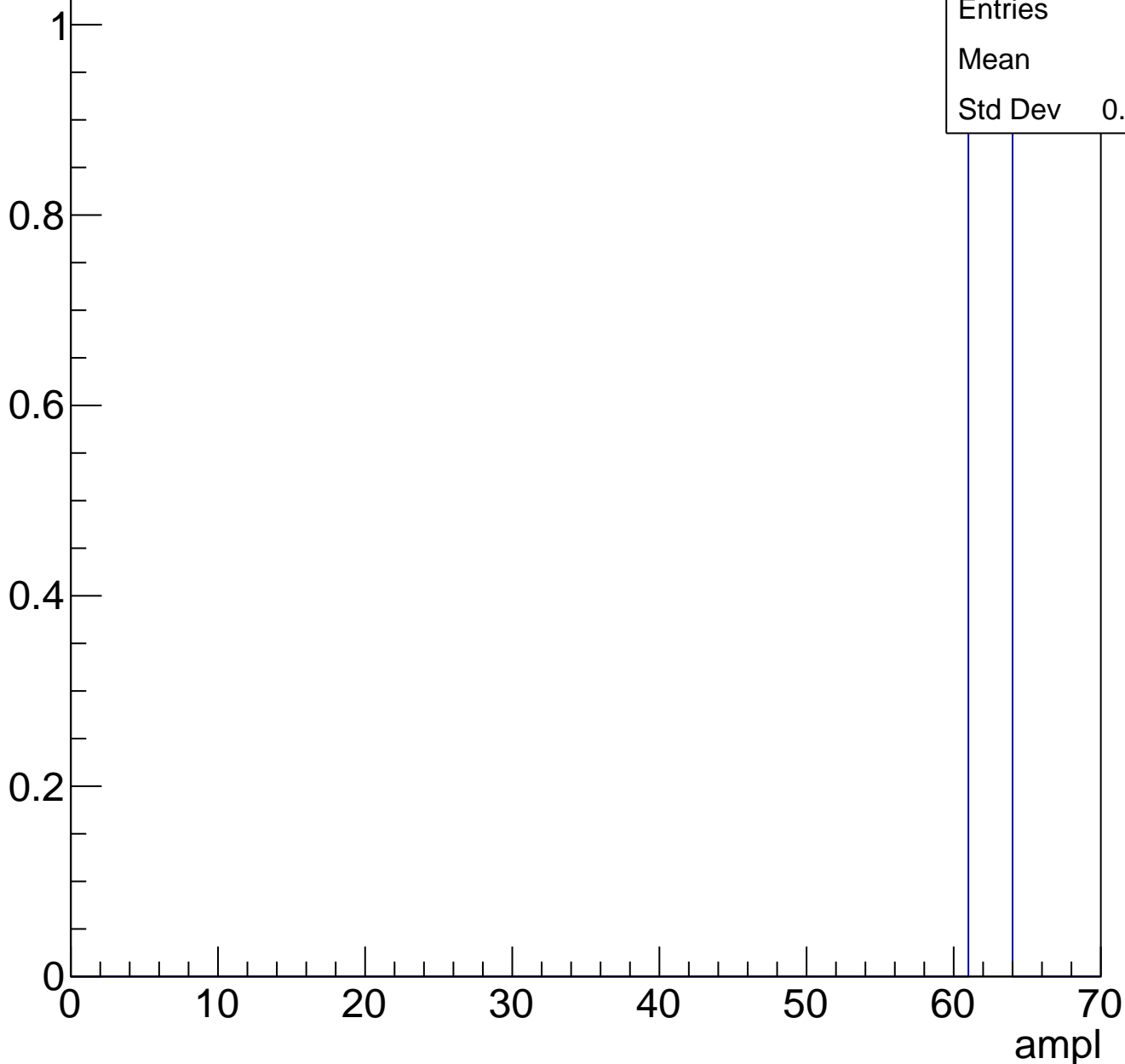
Entries	52
Mean	58.62
Std Dev	8.605



B0L000S, U2-ch71, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch71, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch72, adc0

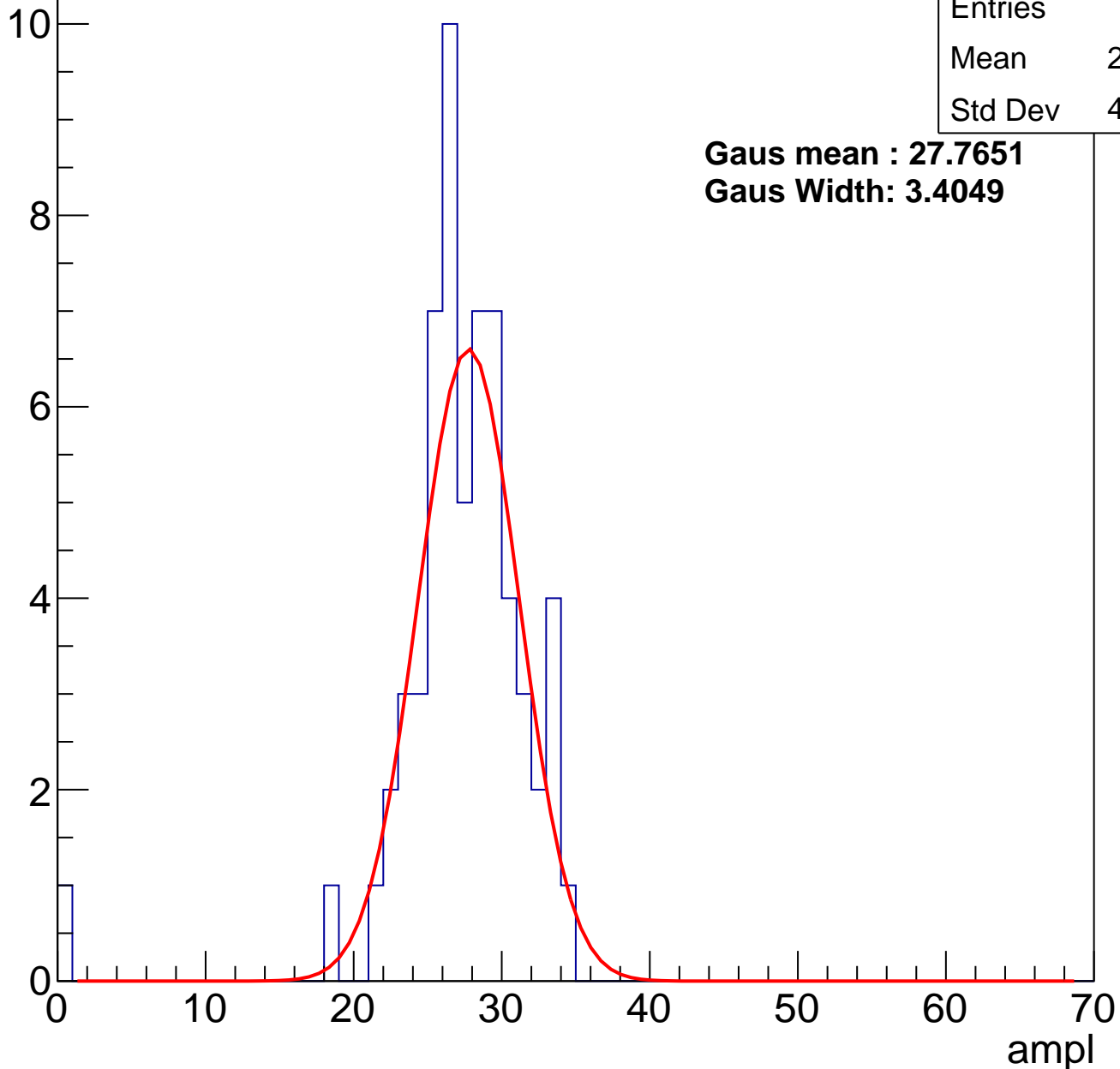
calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	26.82
Std Dev	4.737

Gaus mean : 27.7651

Gaus Width: 3.4049

Entry



B0L000S, U2-ch72, adc1

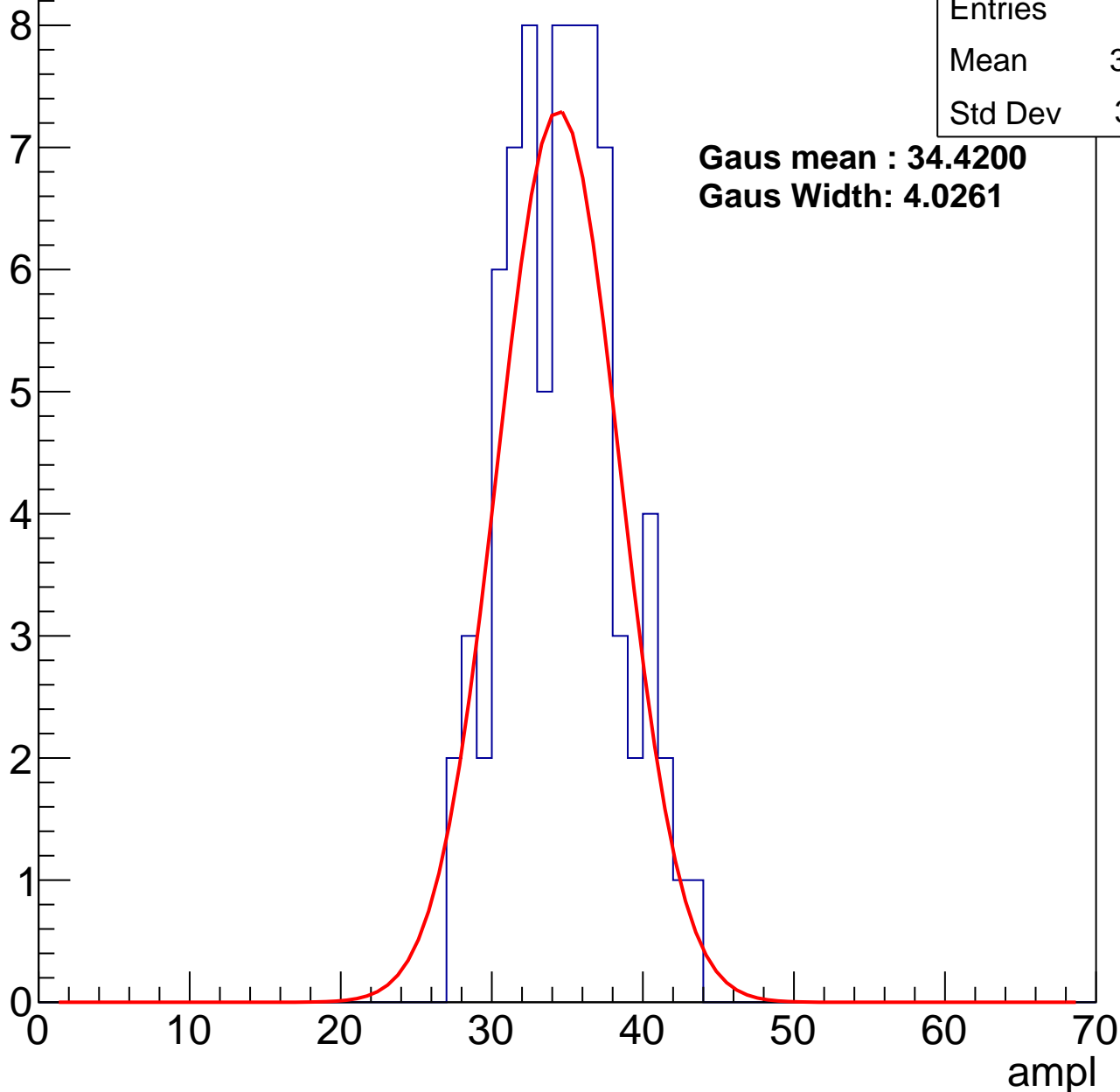
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	34.18
Std Dev	3.671

Gaus mean : 34.4200

Gaus Width: 4.0261



B0L000S, U2-ch72, adc2

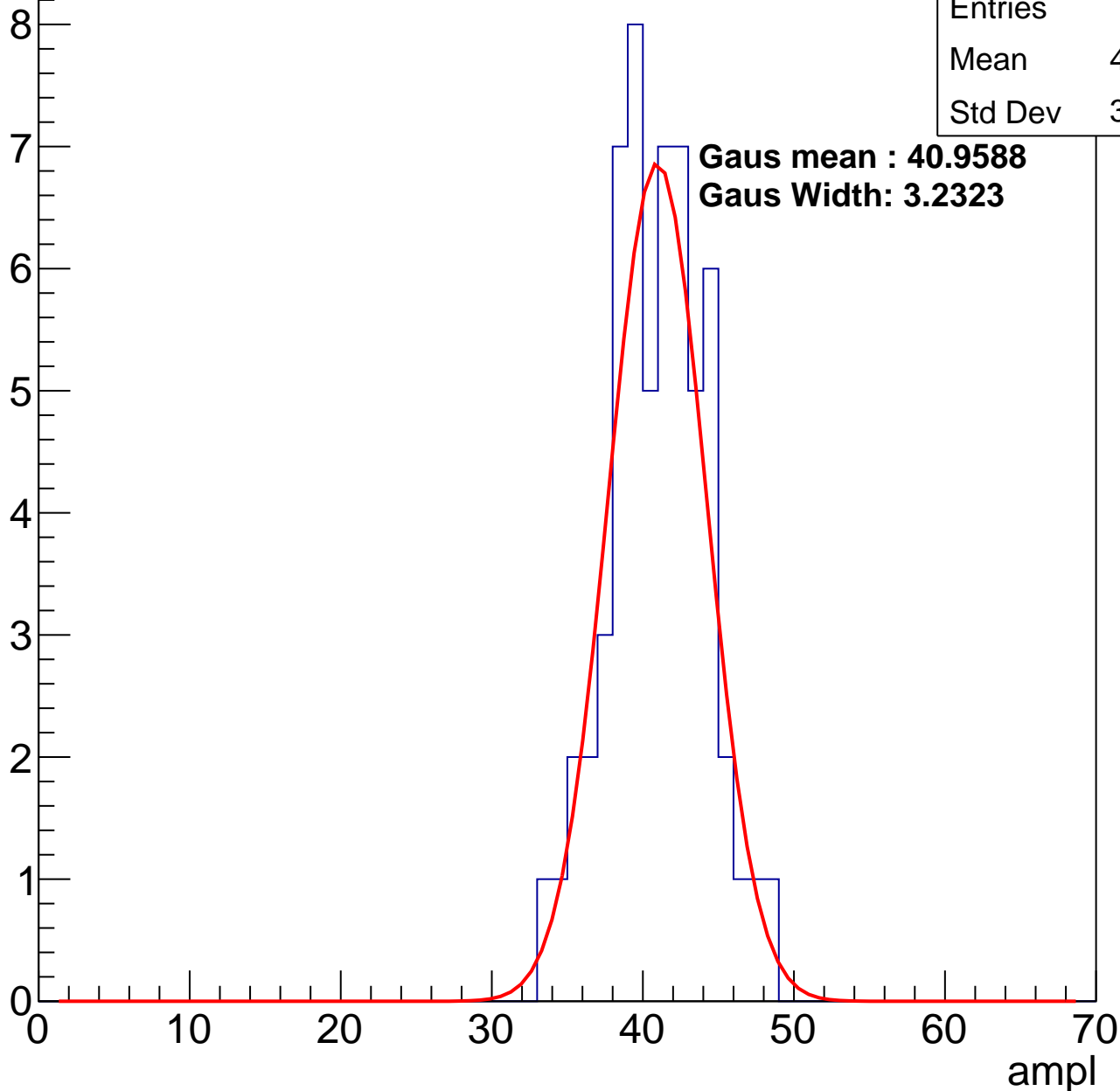
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	40.49
Std Dev	3.164

Gaus mean : 40.9588

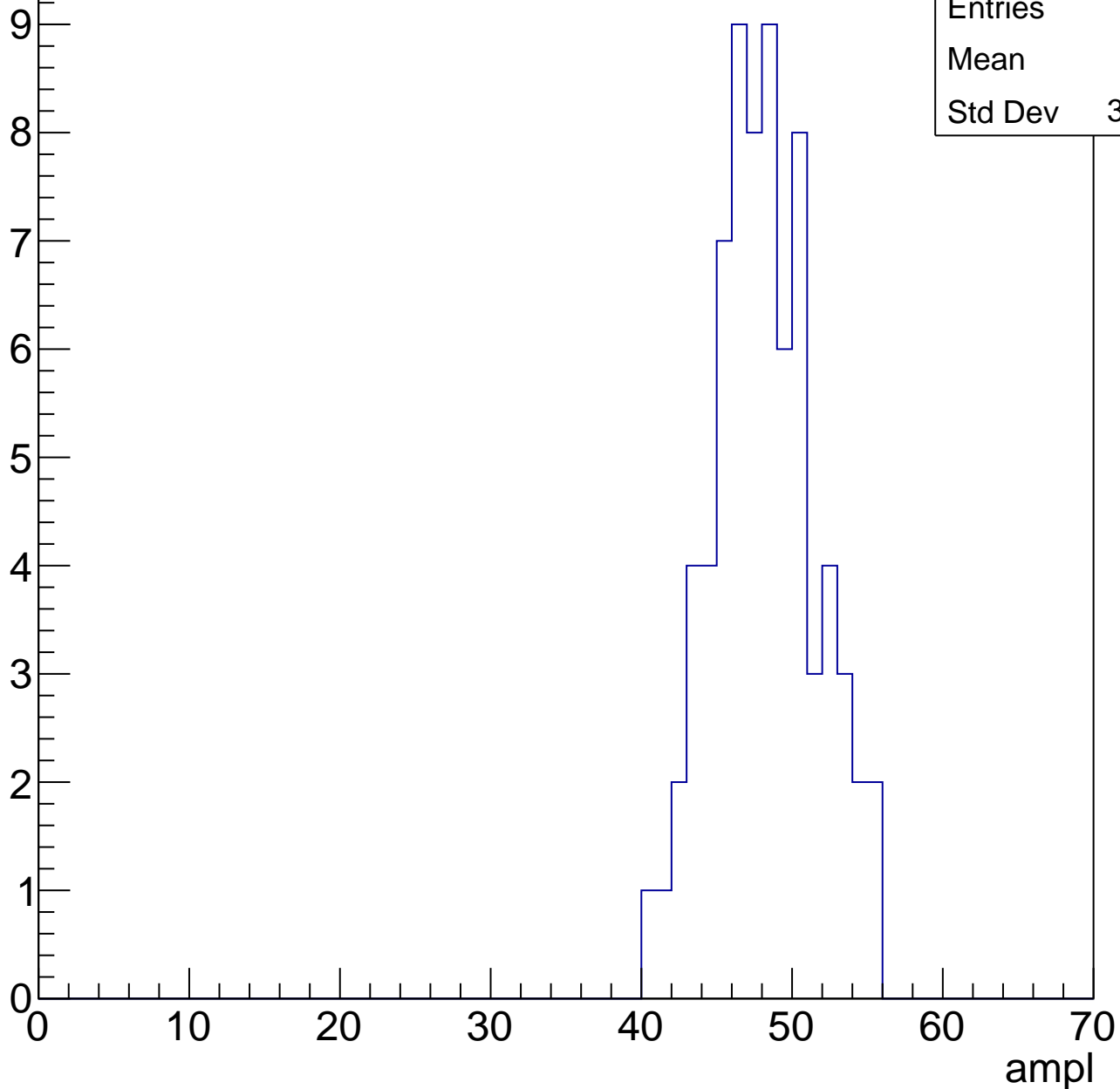
Gaus Width: 3.2323



B0L000S, U2-ch72, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



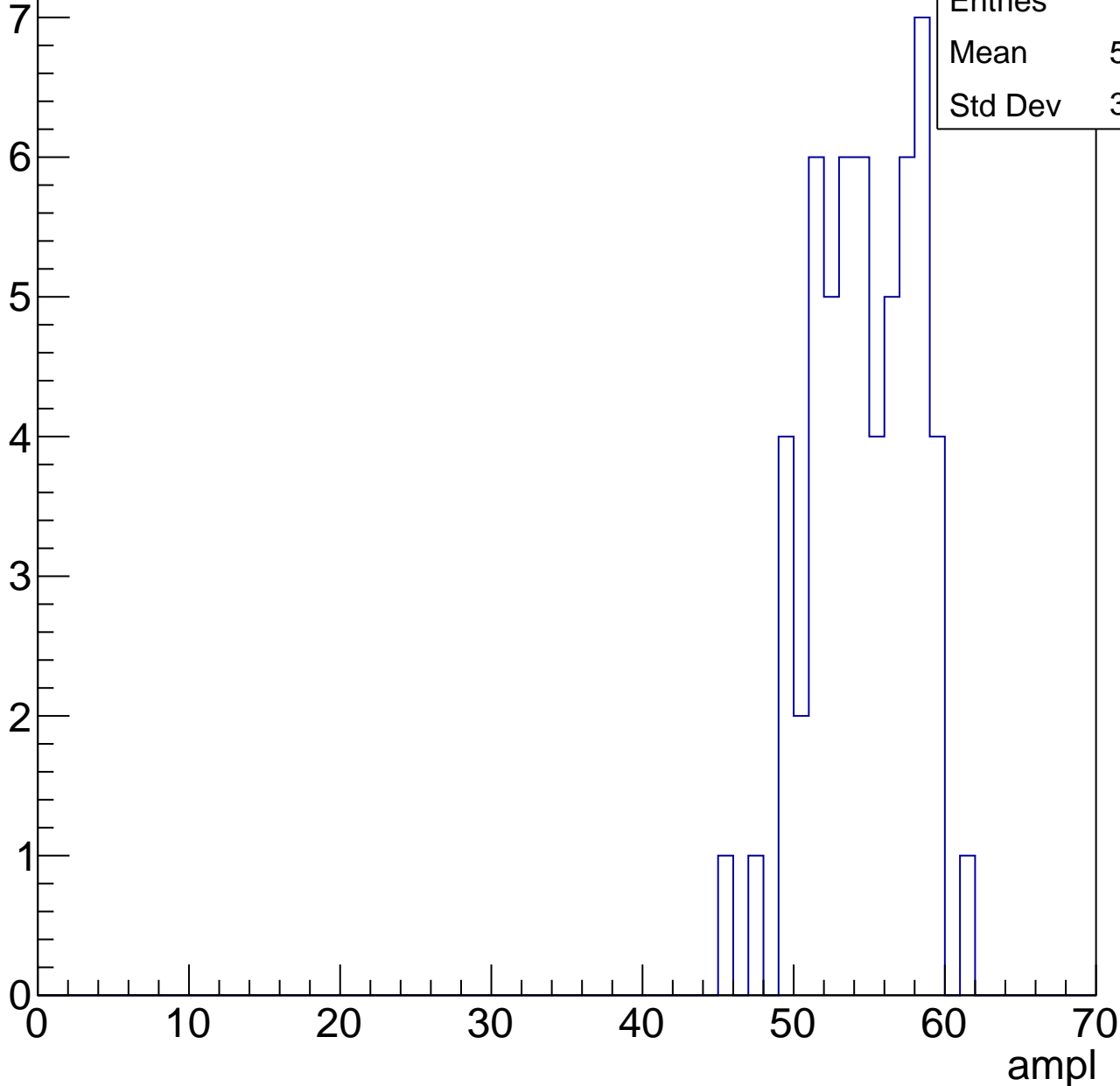
Entries	73
Mean	47.7
Std Dev	3.358

B0L000S, U2-ch72, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	54.16
Std Dev	3.423

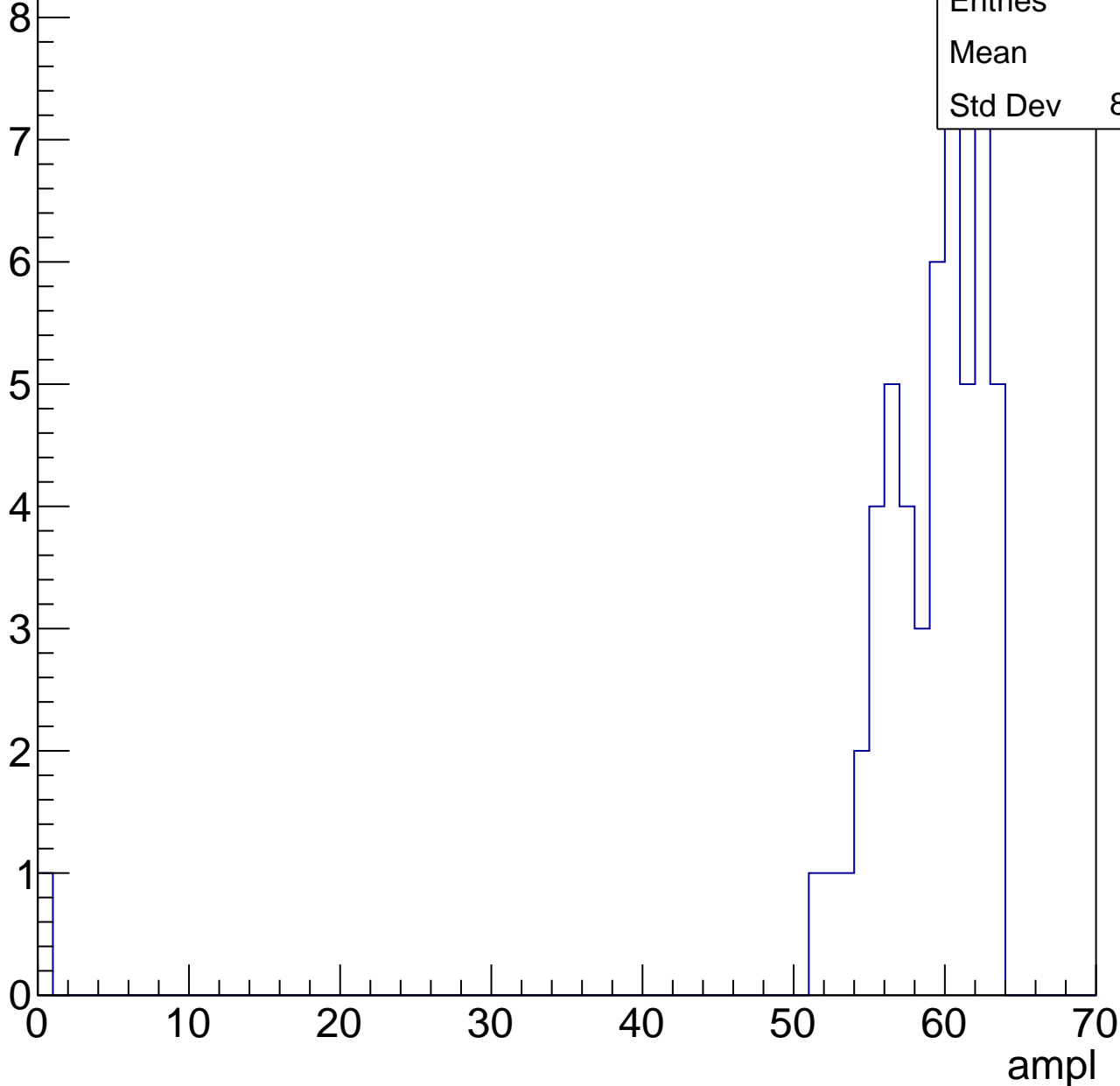


B0L000S, U2-ch72, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	57.7
Std Dev	8.495

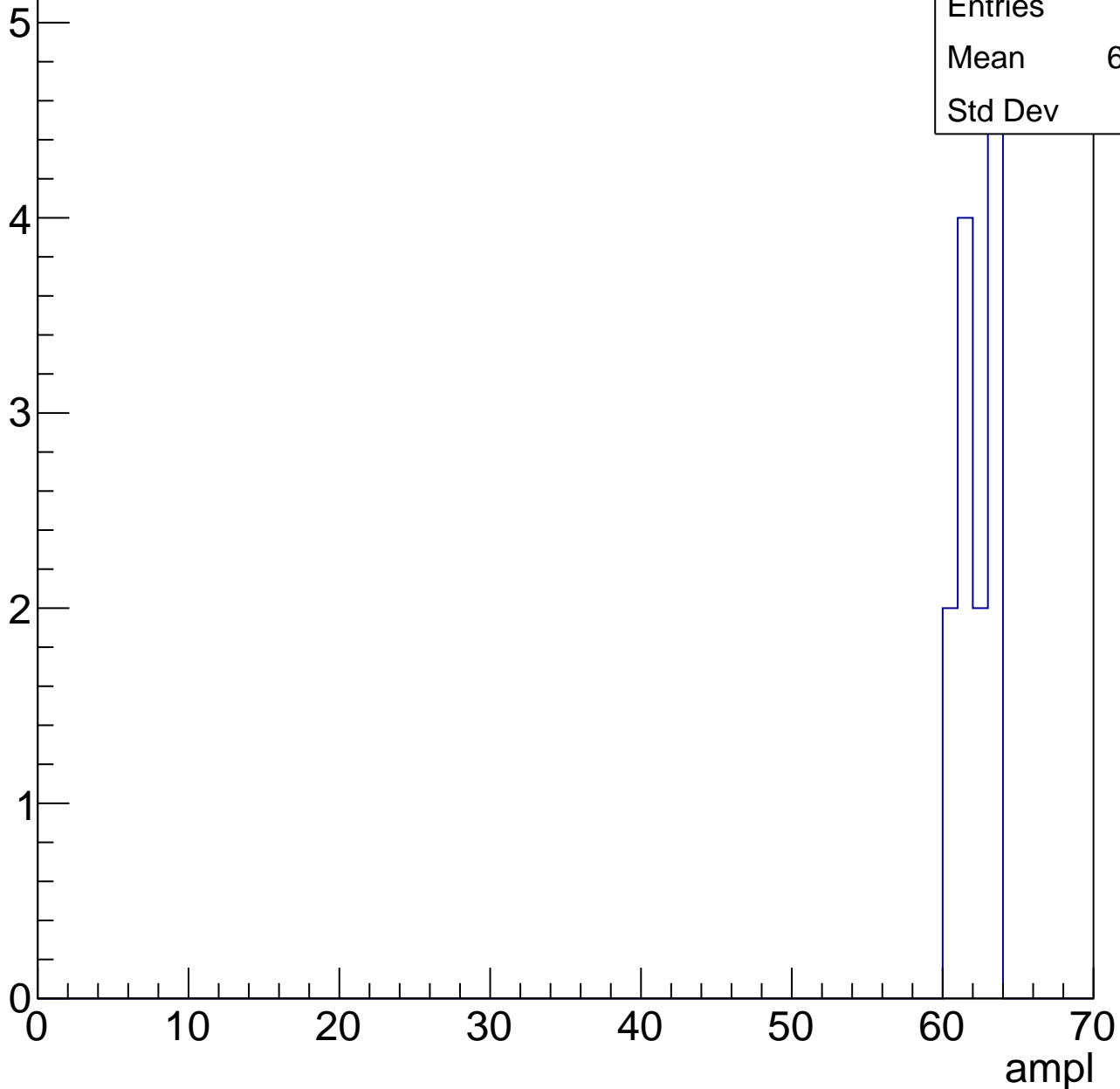


B0L000S, U2-ch72, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	13
Mean	61.77
Std Dev	1.12



B0L000S, U2-ch72, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch73, adc0

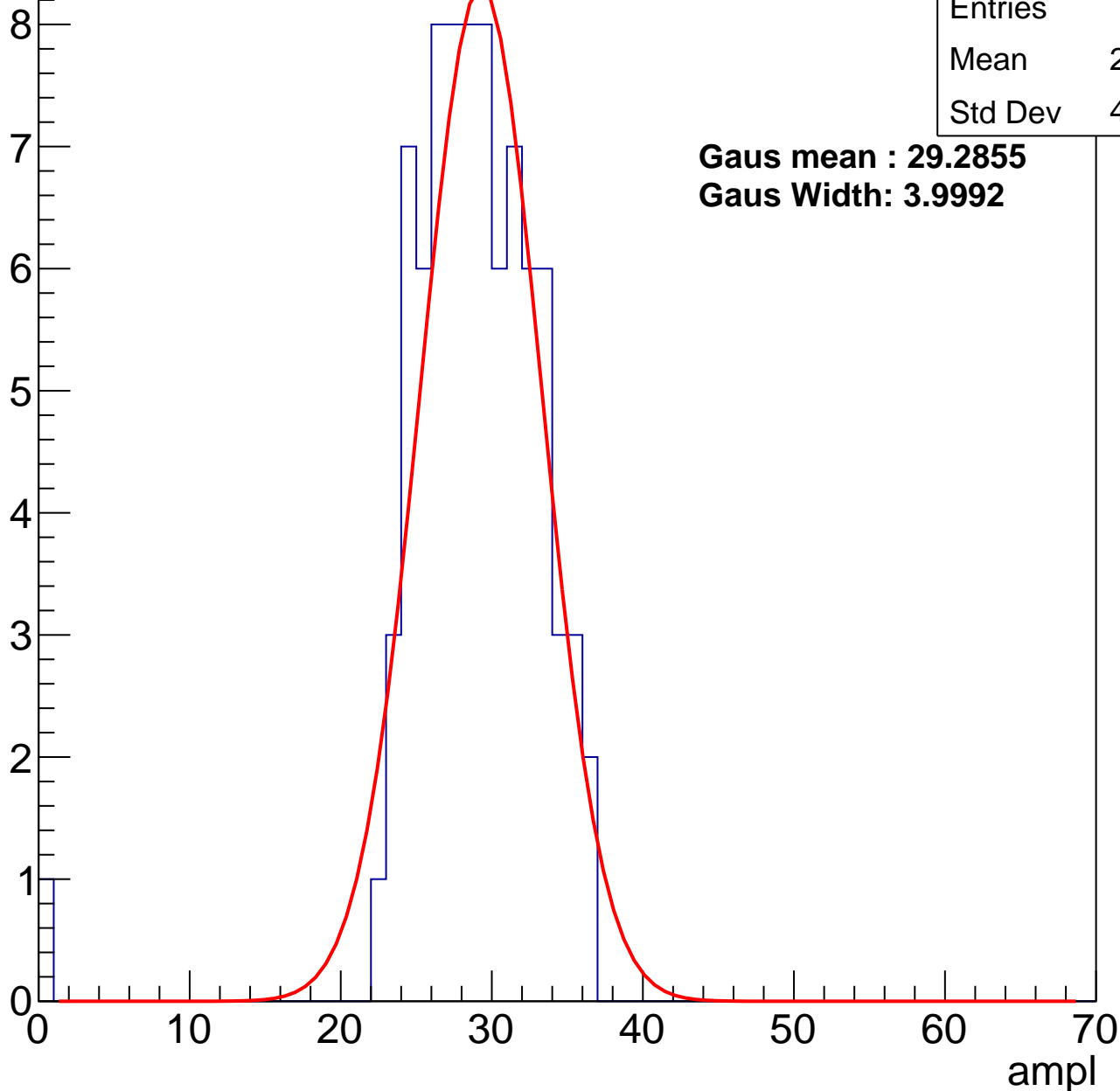
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	83
Mean	28.37
Std Dev	4.674

Gaus mean : 29.2855

Gaus Width: 3.9992



B0L000S, U2-ch73, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	54
Mean	35.09
Std Dev	3.622

Gaus mean : 35.3313

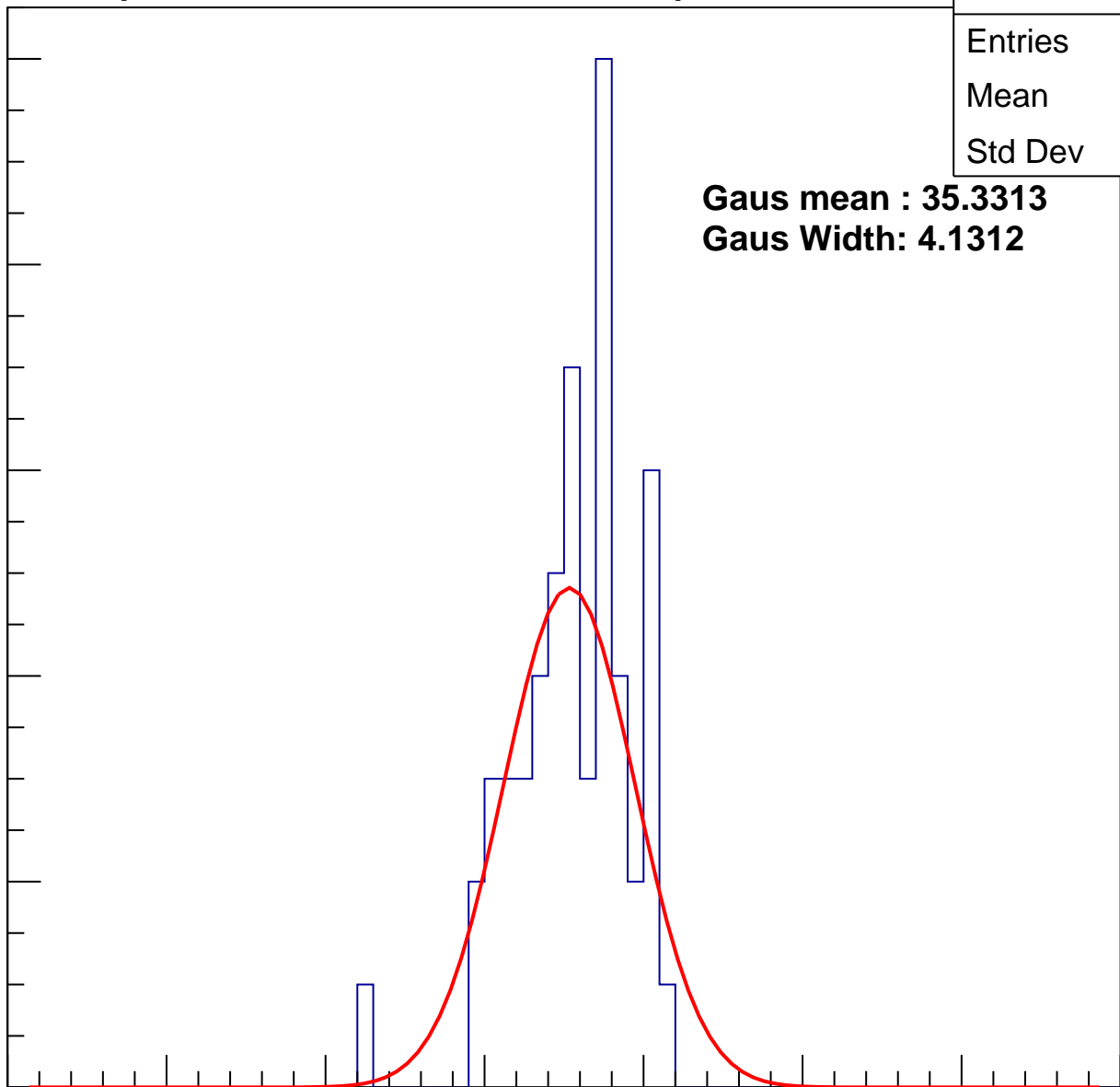
Gaus Width: 4.1312

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch73, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	79
Mean	42.01
Std Dev	3.549

Gaus mean : 41.8902

Gaus Width: 4.4673

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

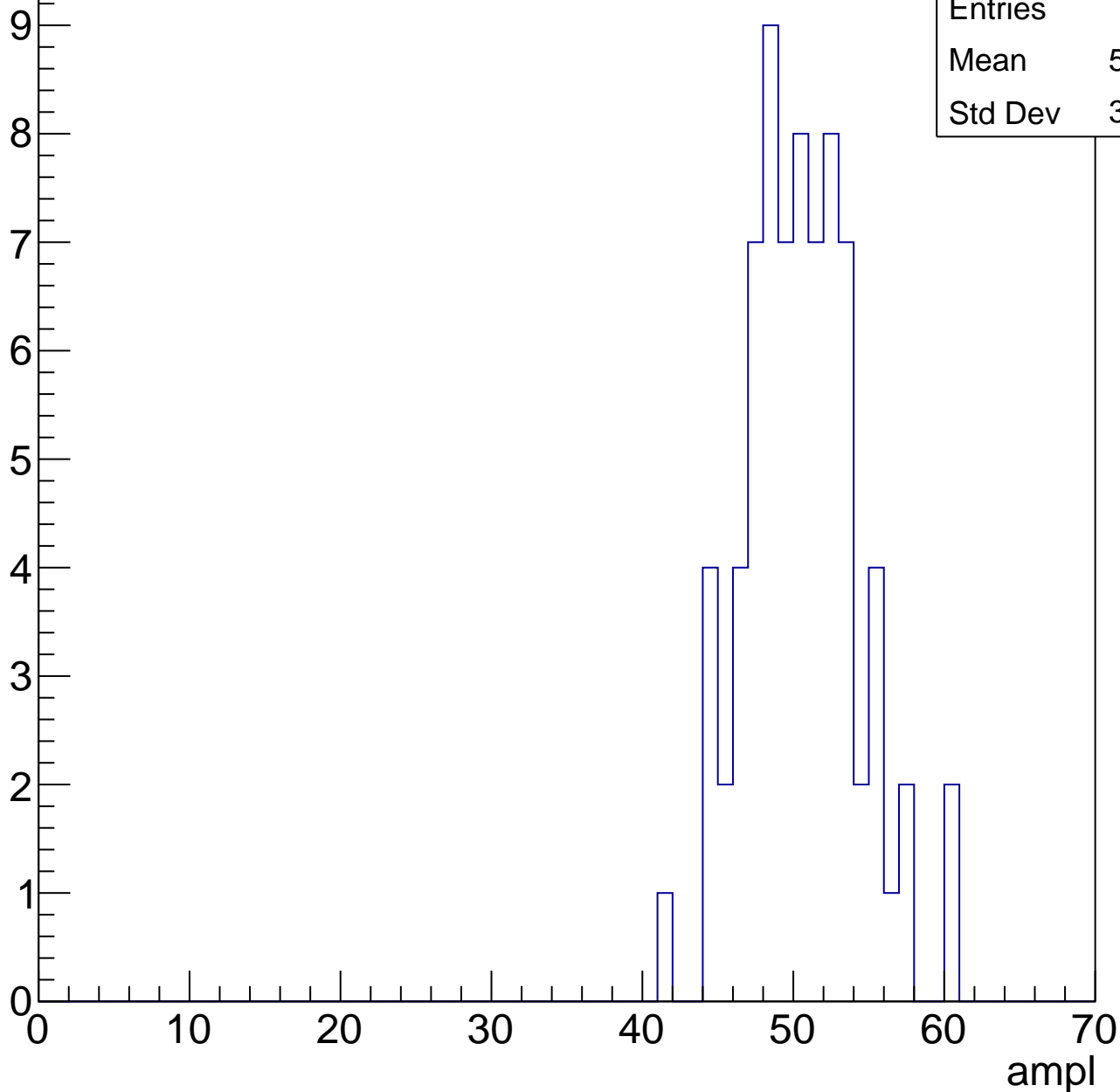
35 40 45 50 55

B0L000S, U2-ch73, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	50.09
Std Dev	3.674

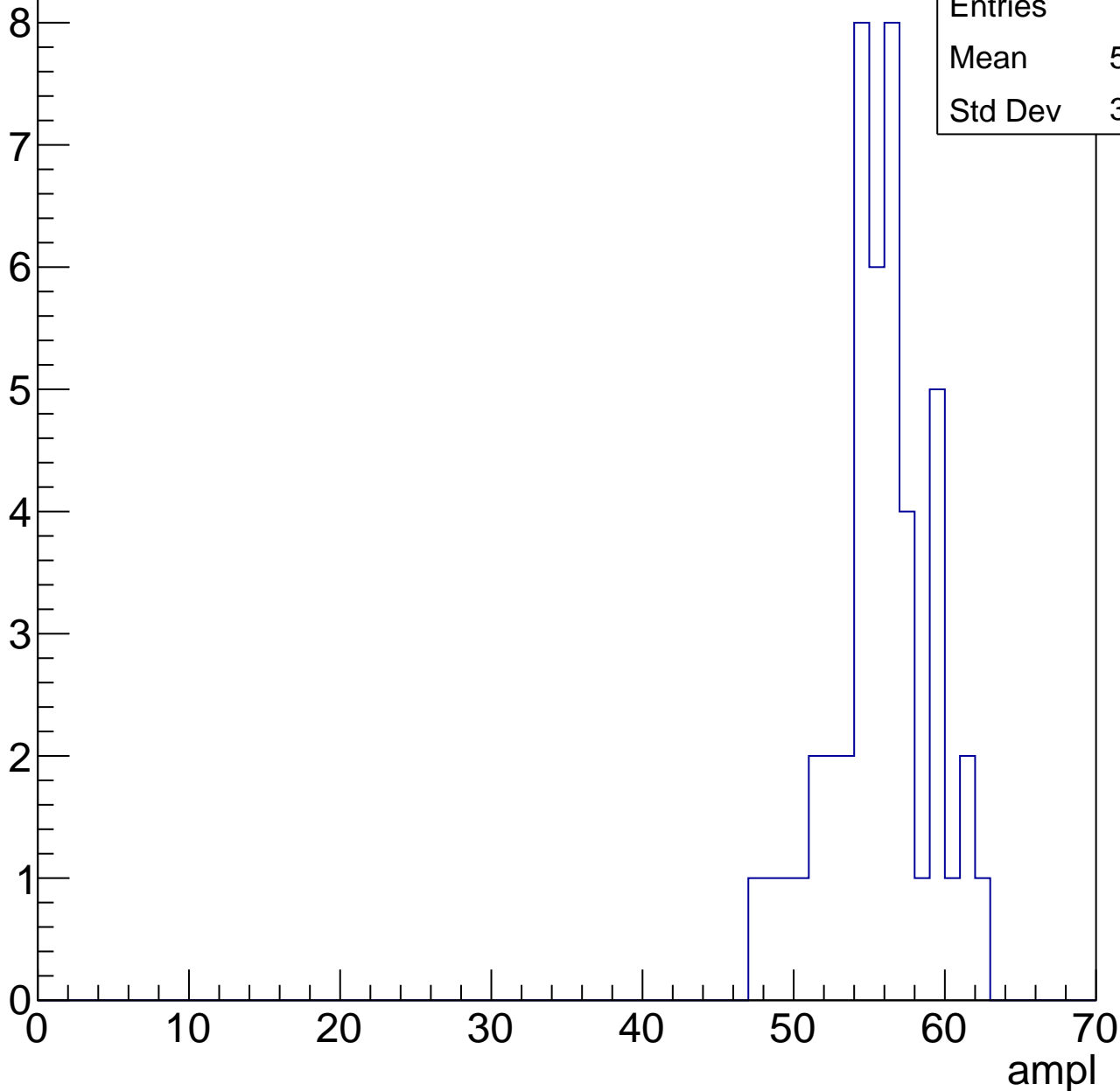


B0L000S, U2-ch73, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

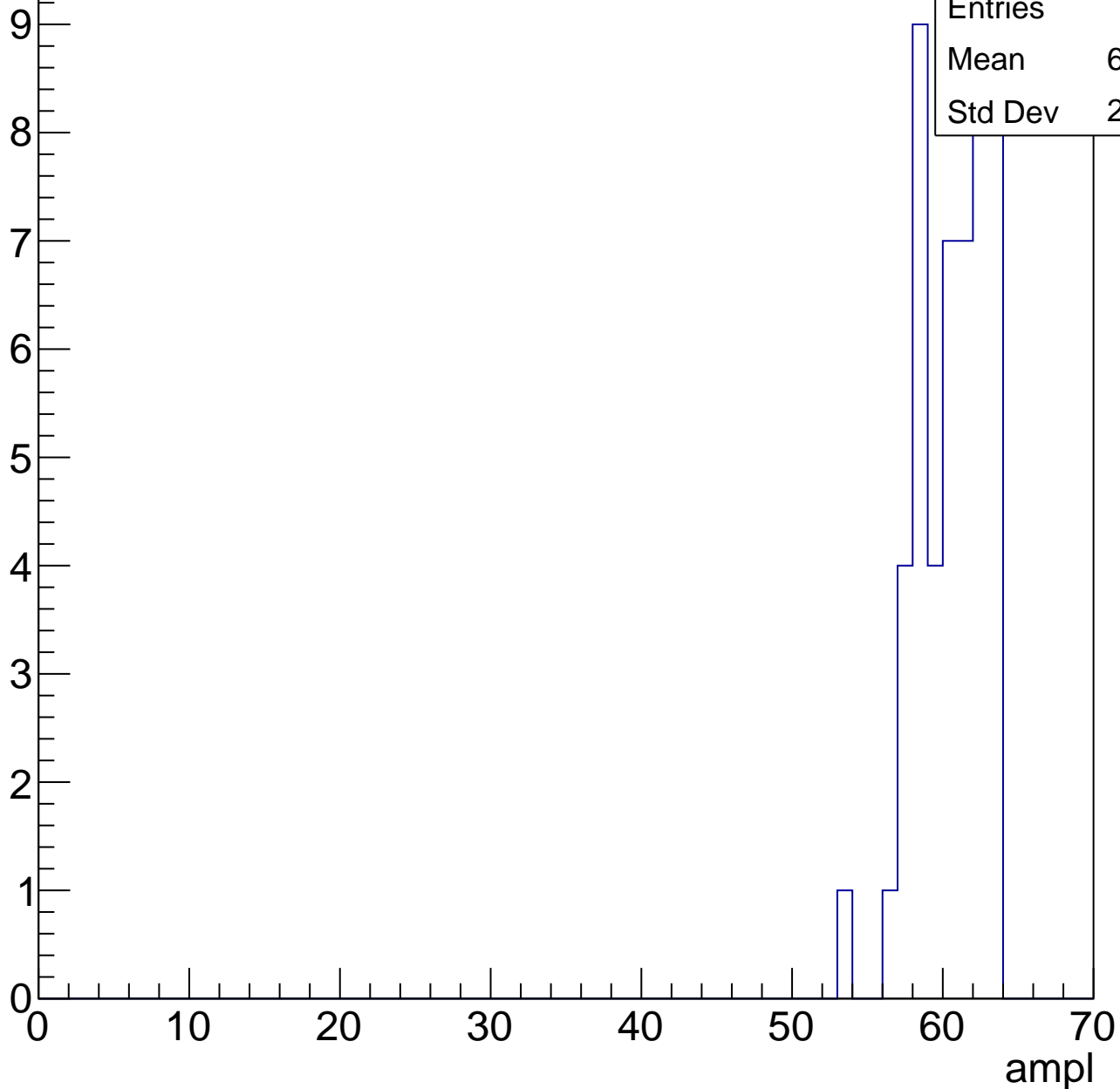
Entries	46
Mean	55.24
Std Dev	3.285



B0L000S, U2-ch73, adc5

calib_packv5_042523_0143.root, FC#5, port B1

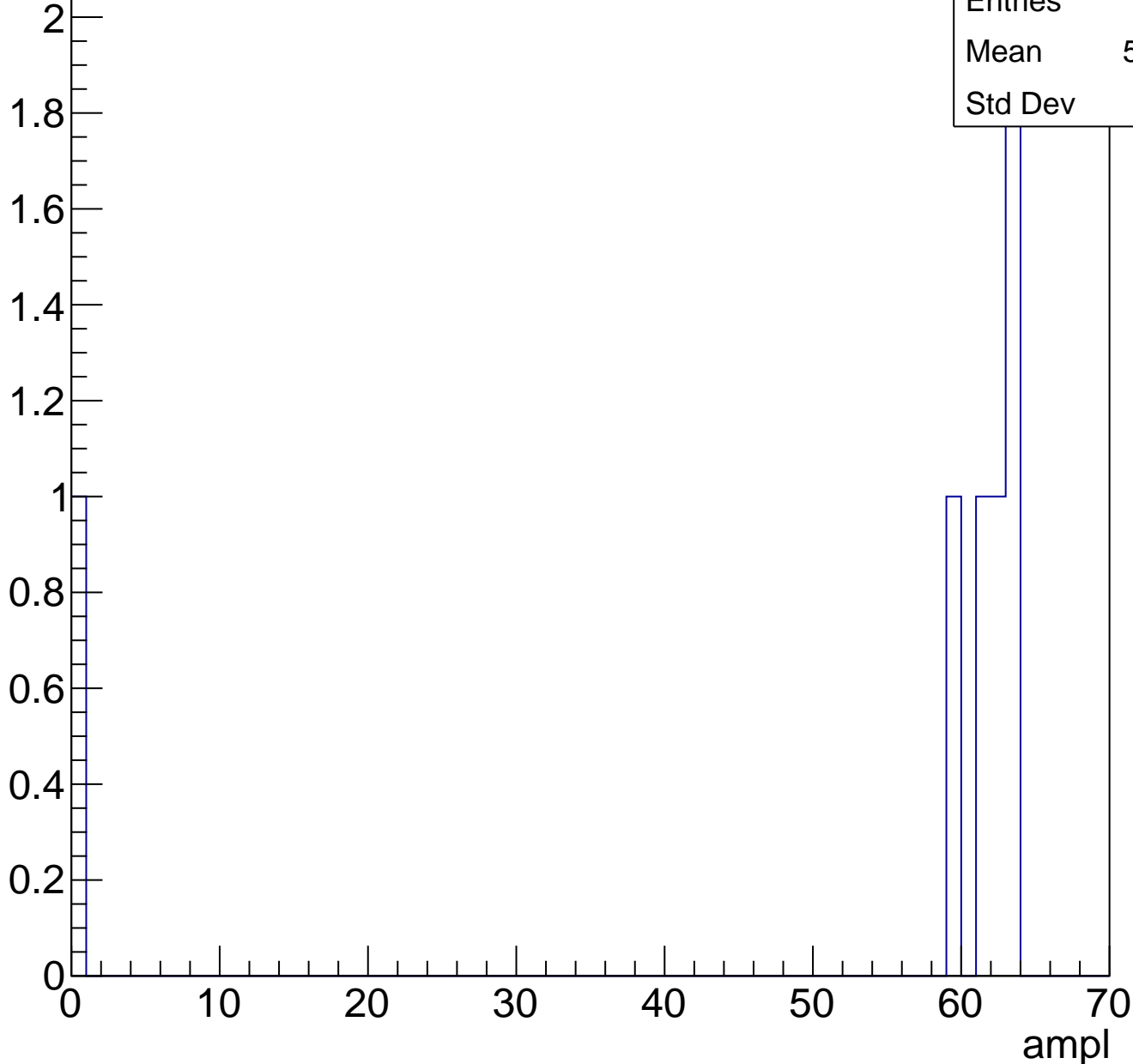
Entry



B0L000S, U2-ch73, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch73, adc7

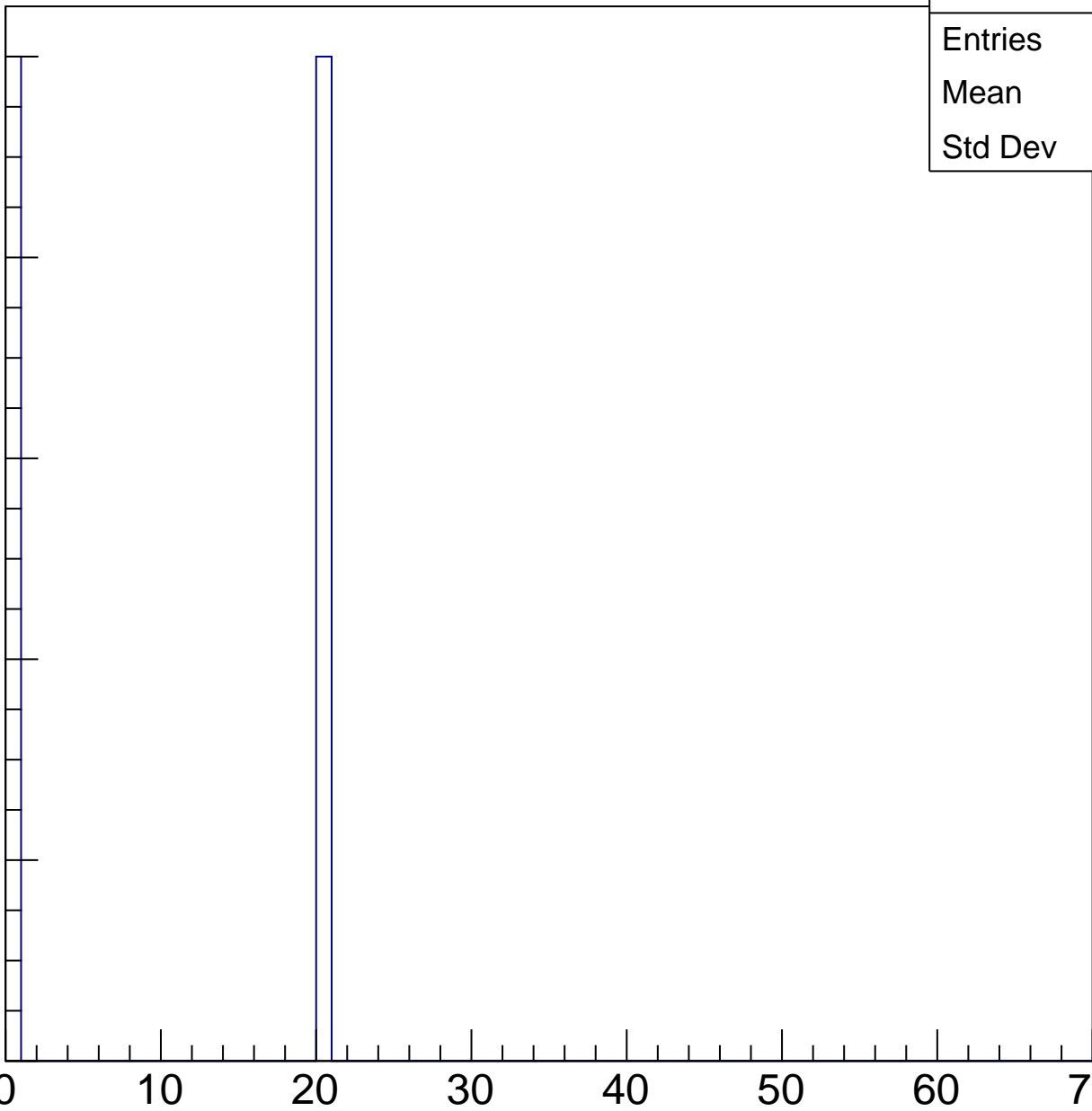
calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	10
Std Dev	10

ampl



B0L000S, U2-ch74, adc0

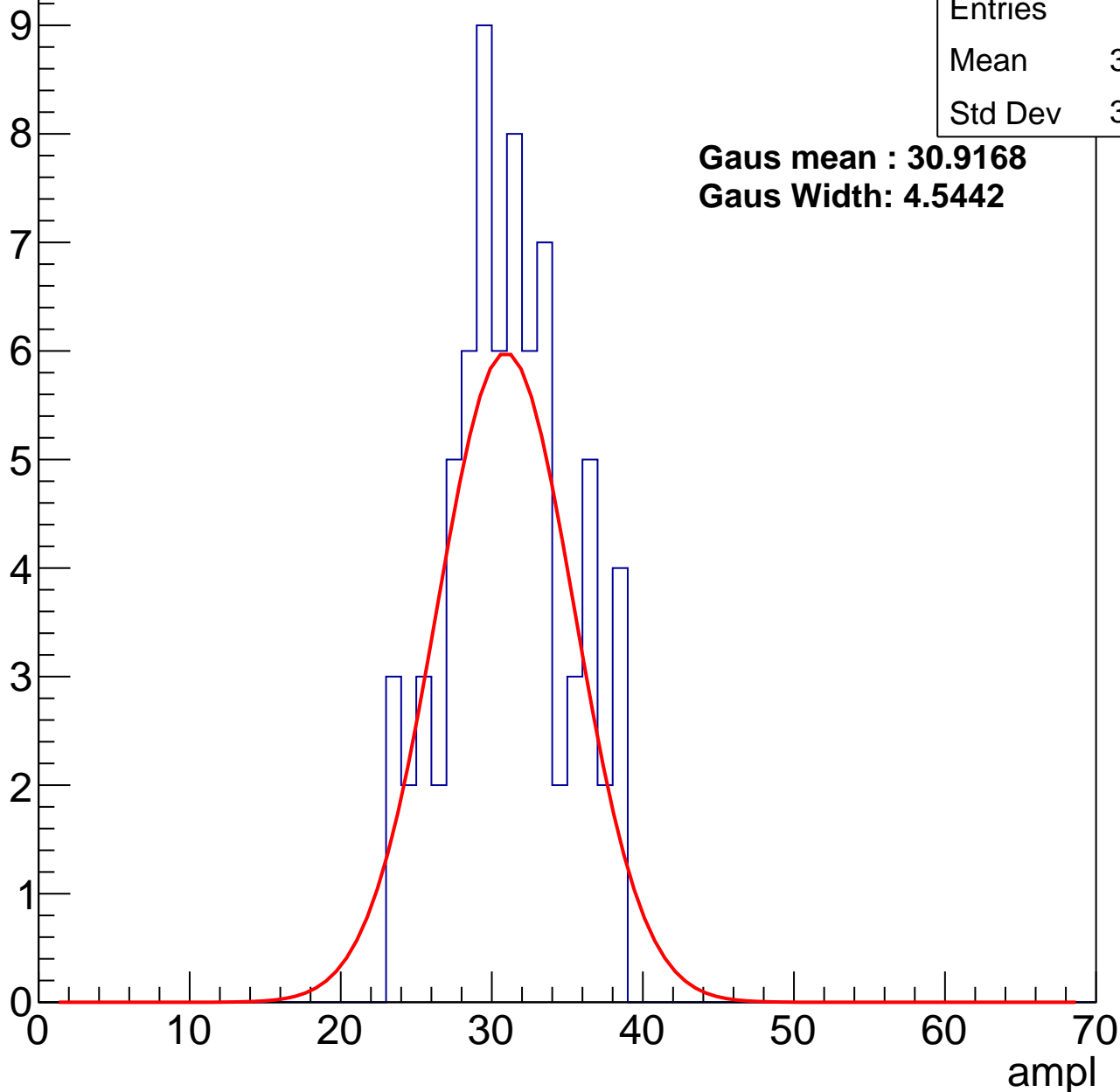
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	30.66
Std Dev	3.897

Gaus mean : 30.9168

Gaus Width: 4.5442



B0L000S, U2-ch74, adc1

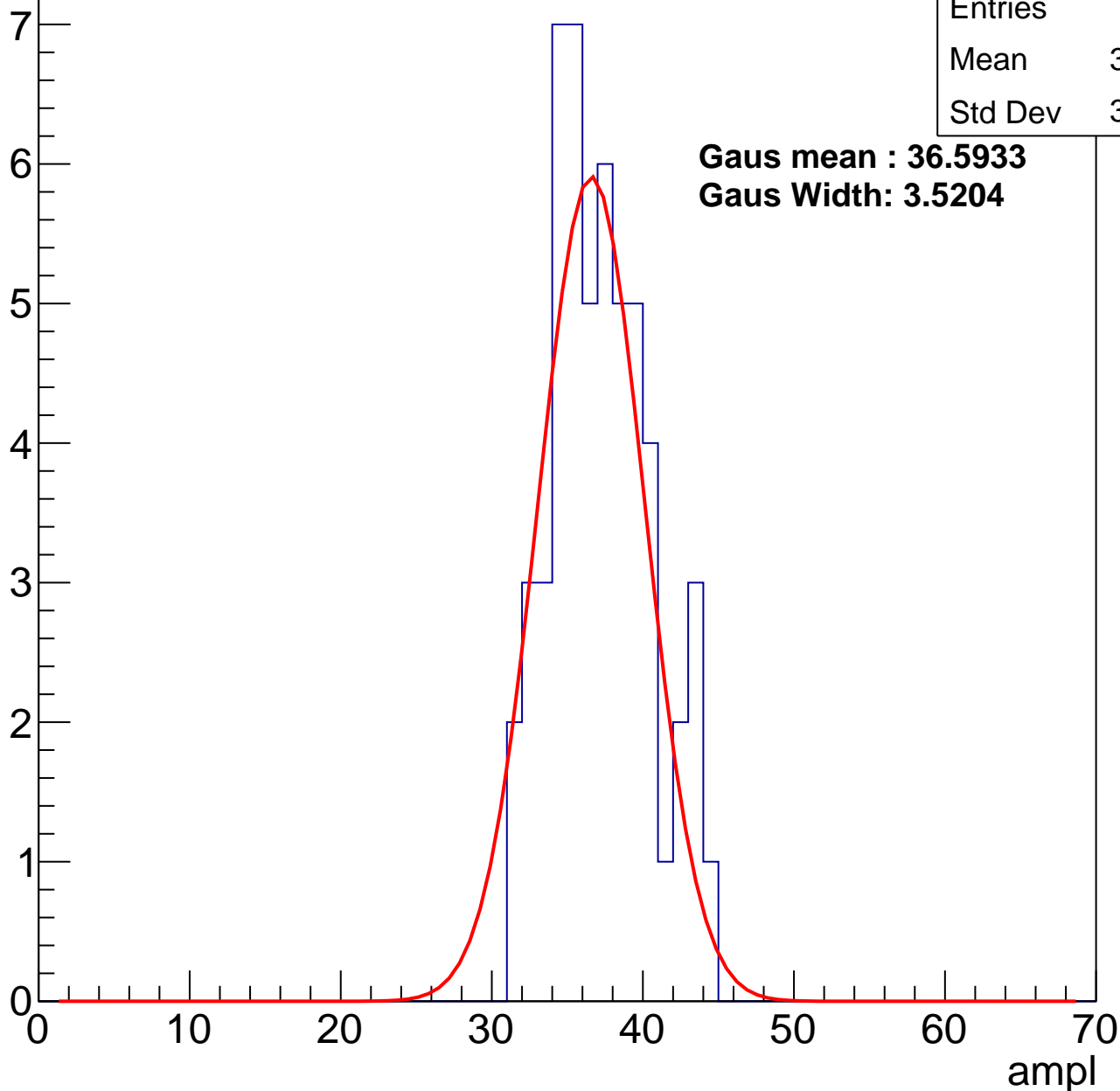
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	36.76
Std Dev	3.254

Gaus mean : 36.5933

Gaus Width: 3.5204



B0L000S, U2-ch74, adc2

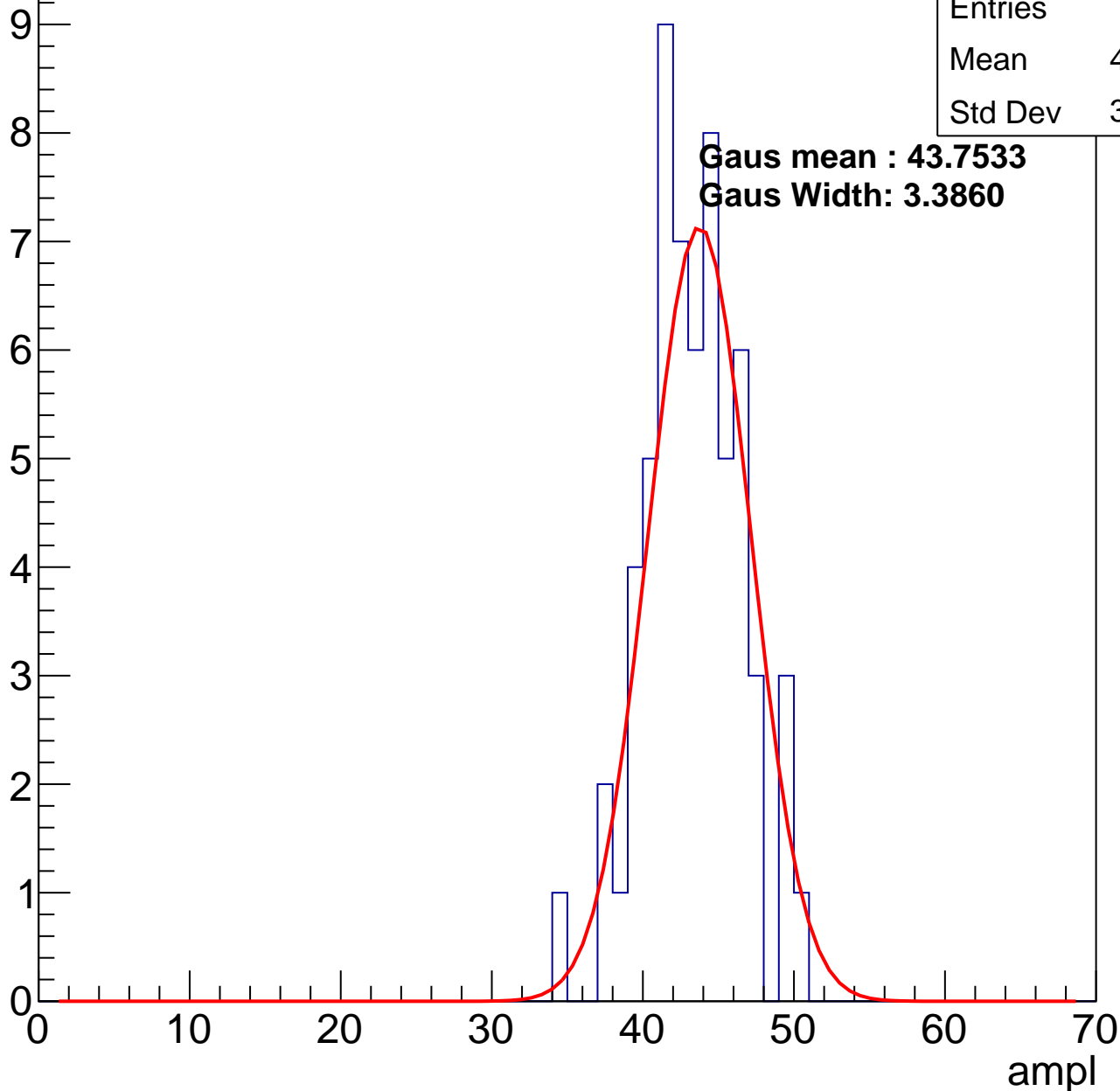
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42.85
Std Dev	3.177

Gaus mean : 43.7533

Gaus Width: 3.3860

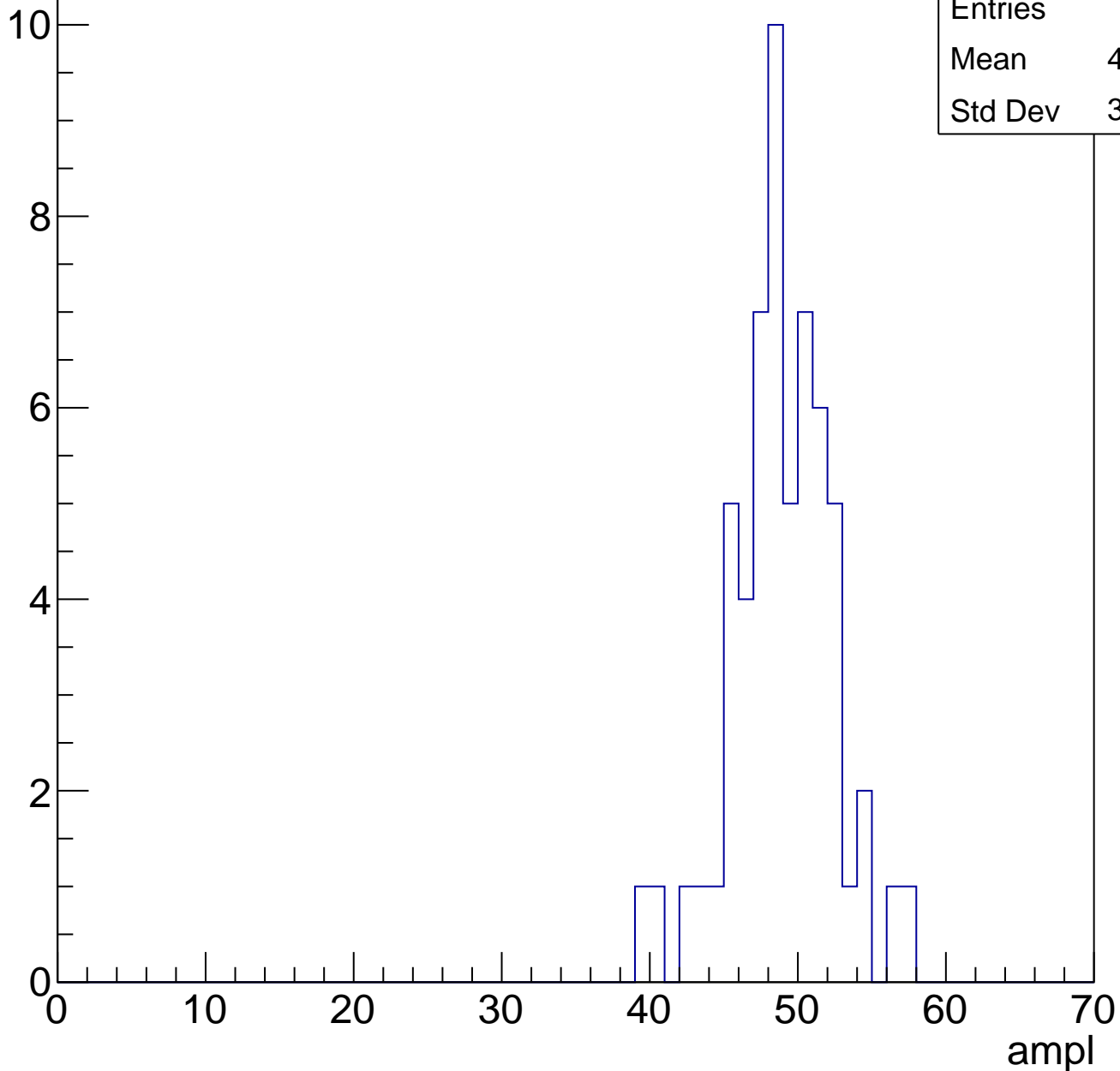


B0L000S, U2-ch74, adc3

calib_packv5_042523_0143.root, FC#5, port B1

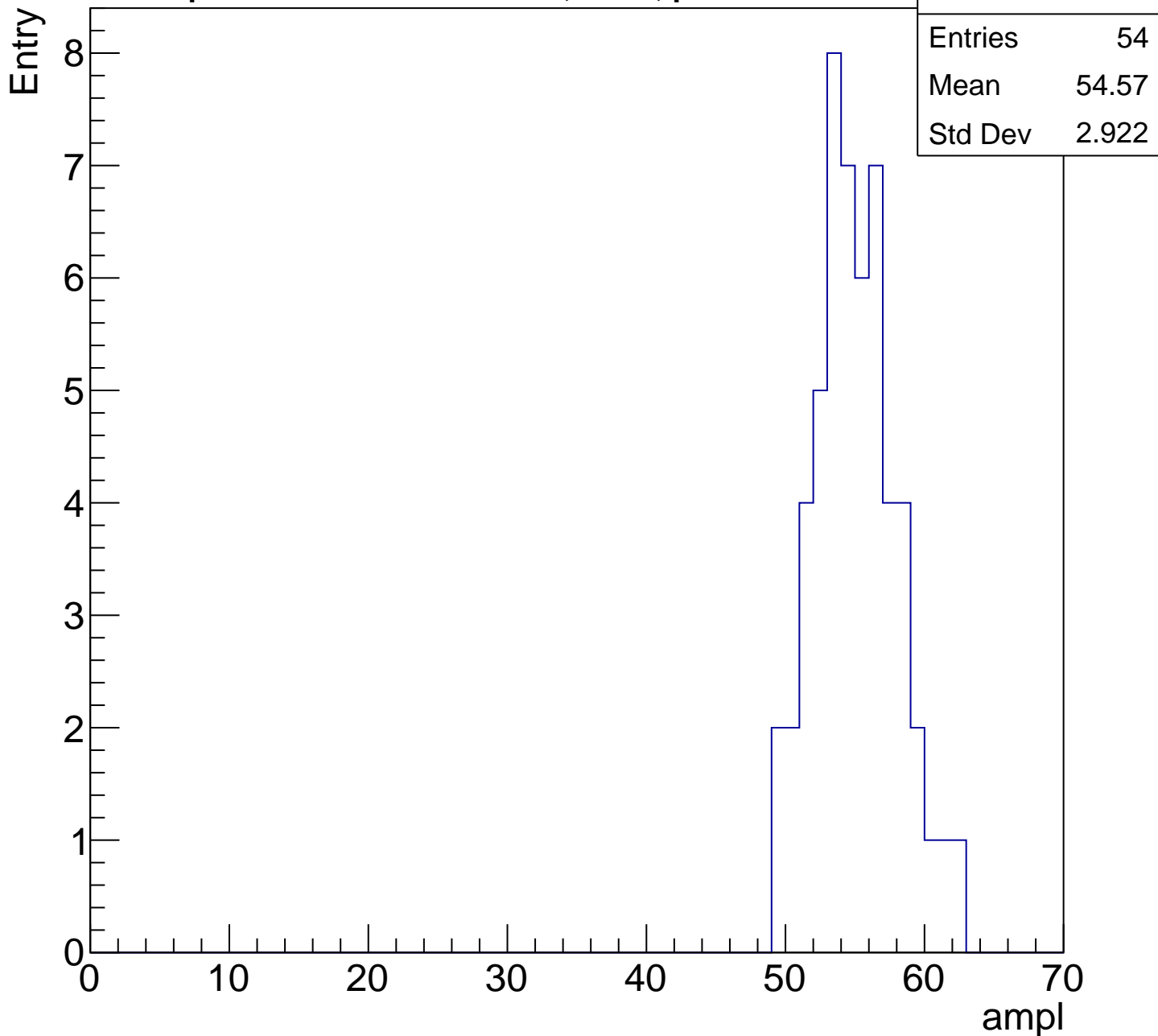
Entries	59
Mean	48.49
Std Dev	3.407

Entry



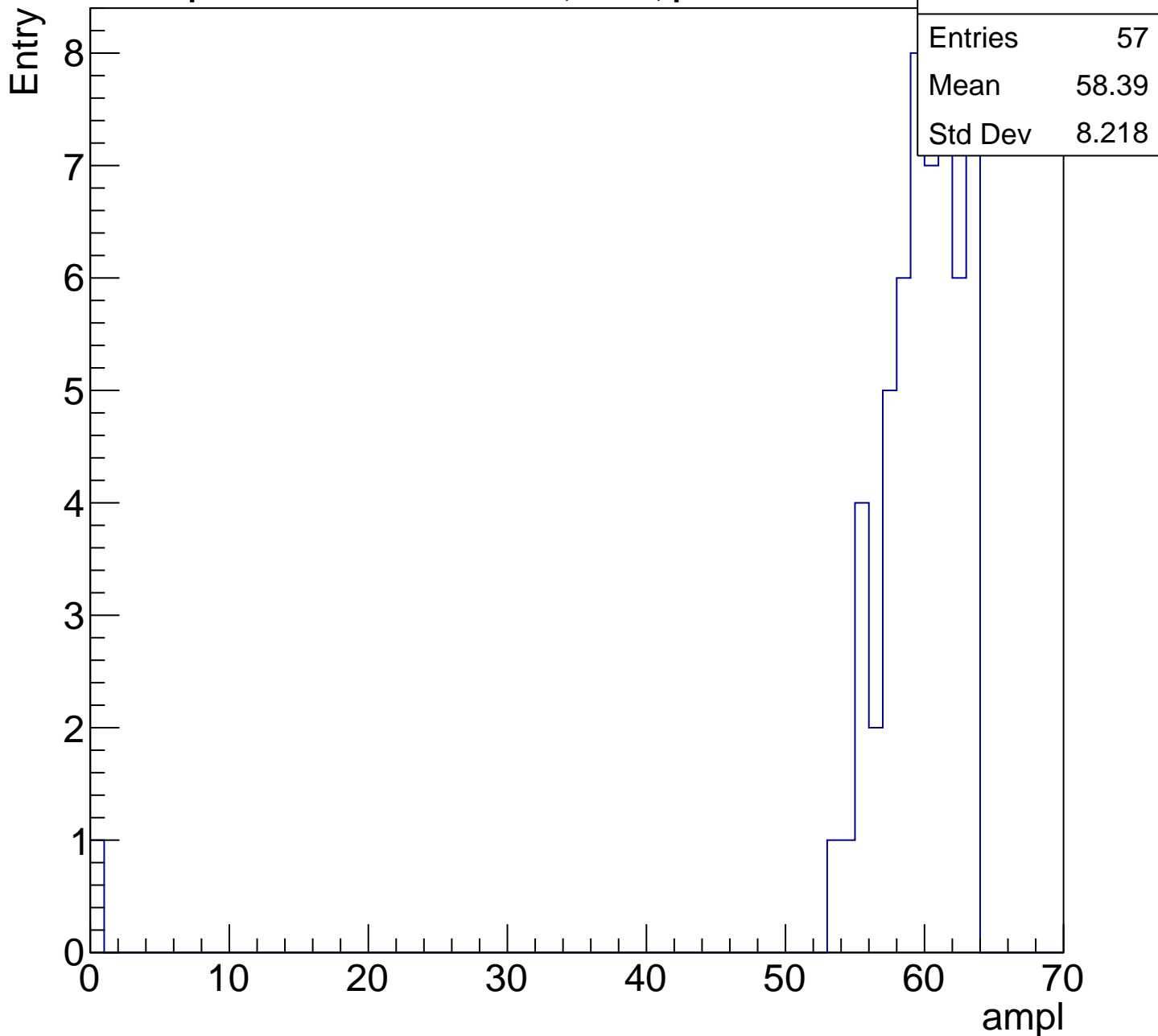
B0L000S, U2-ch74, adc4

calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch74, adc5

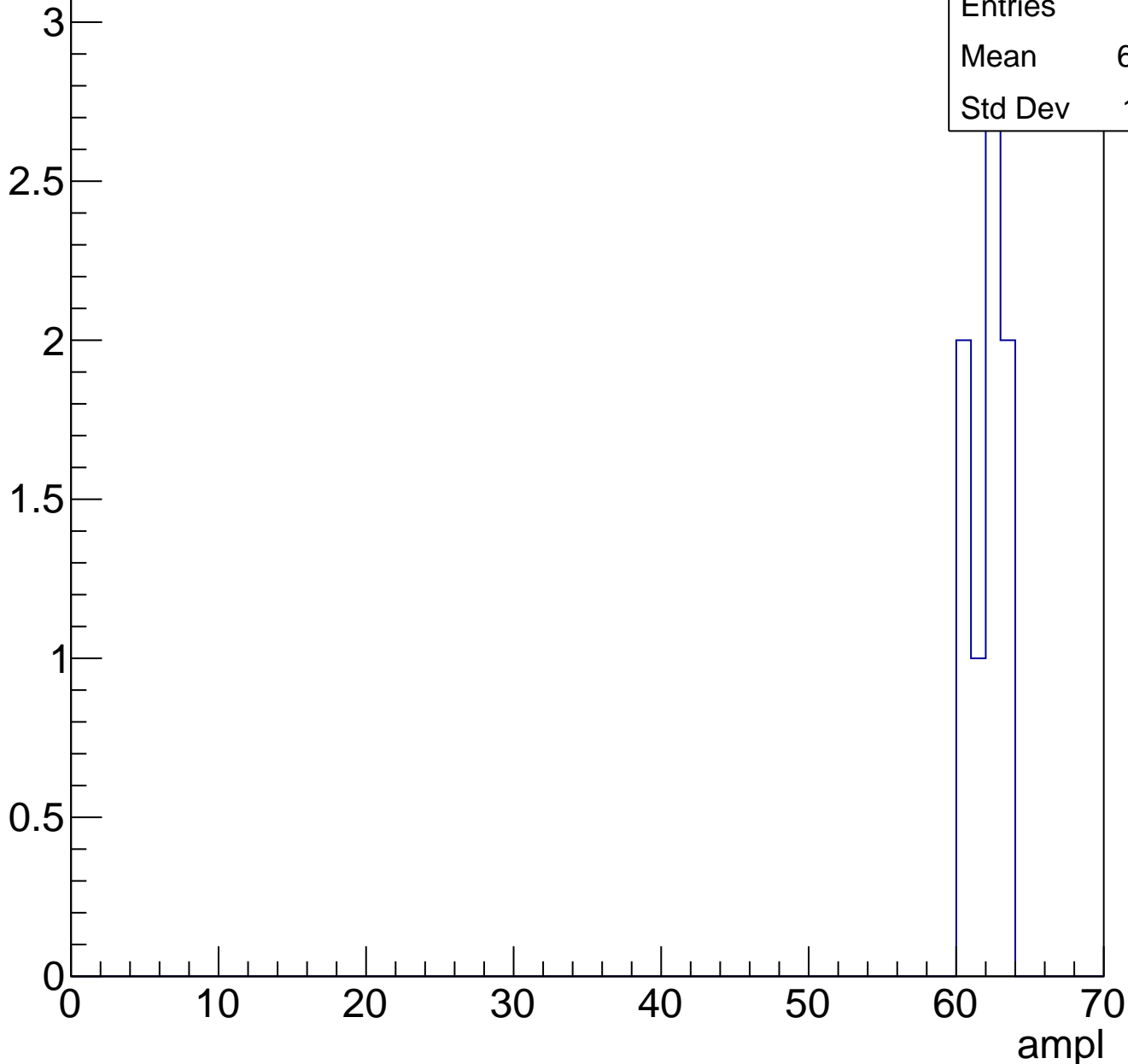
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch74, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch74, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch75, adc0

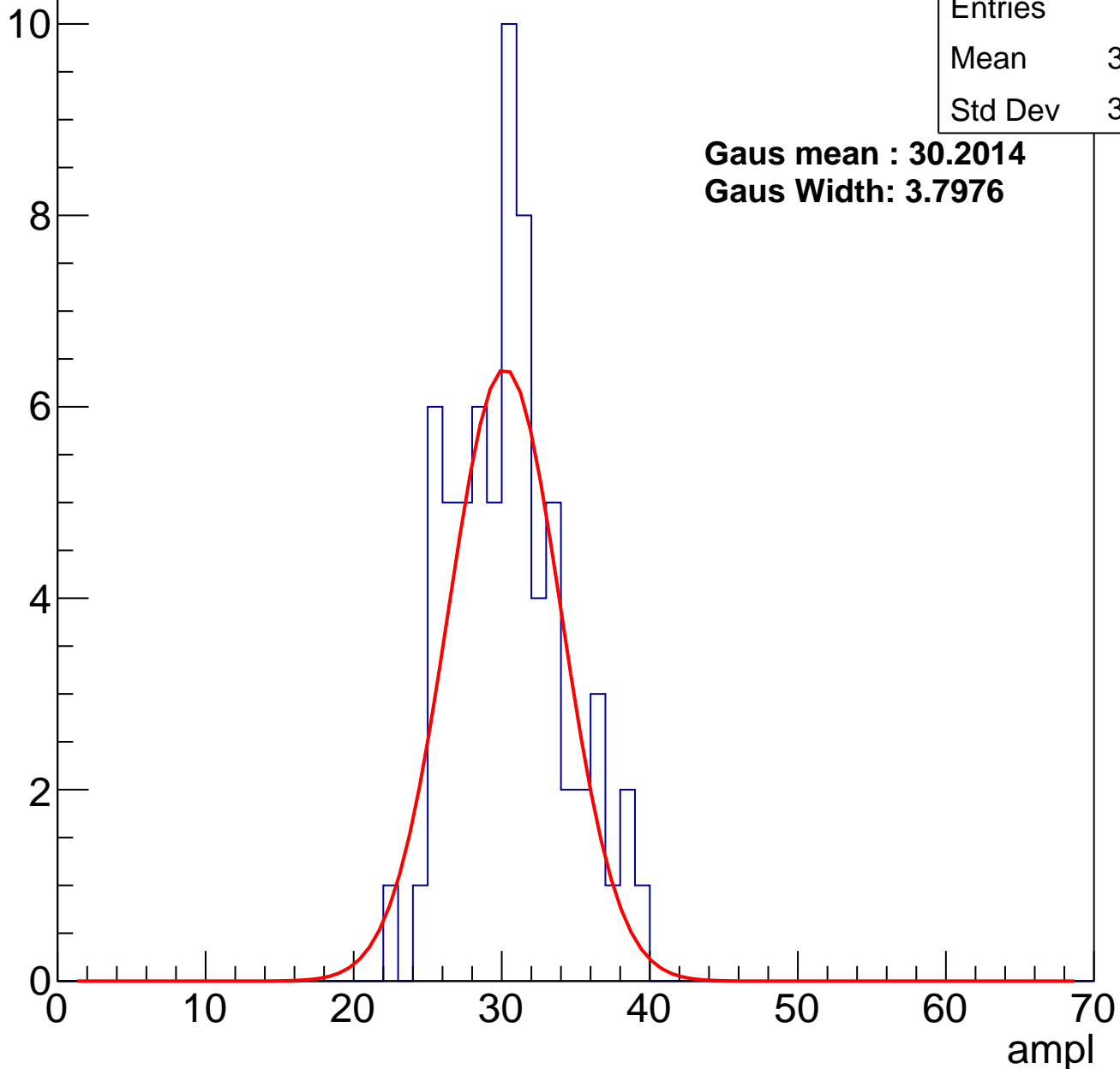
calib_packv5_042523_0143.root, FC#5, port B1

Entries	67
Mean	30.04
Std Dev	3.707

Gaus mean : 30.2014

Gaus Width: 3.7976

Entry



B0L000S, U2-ch75, adc1

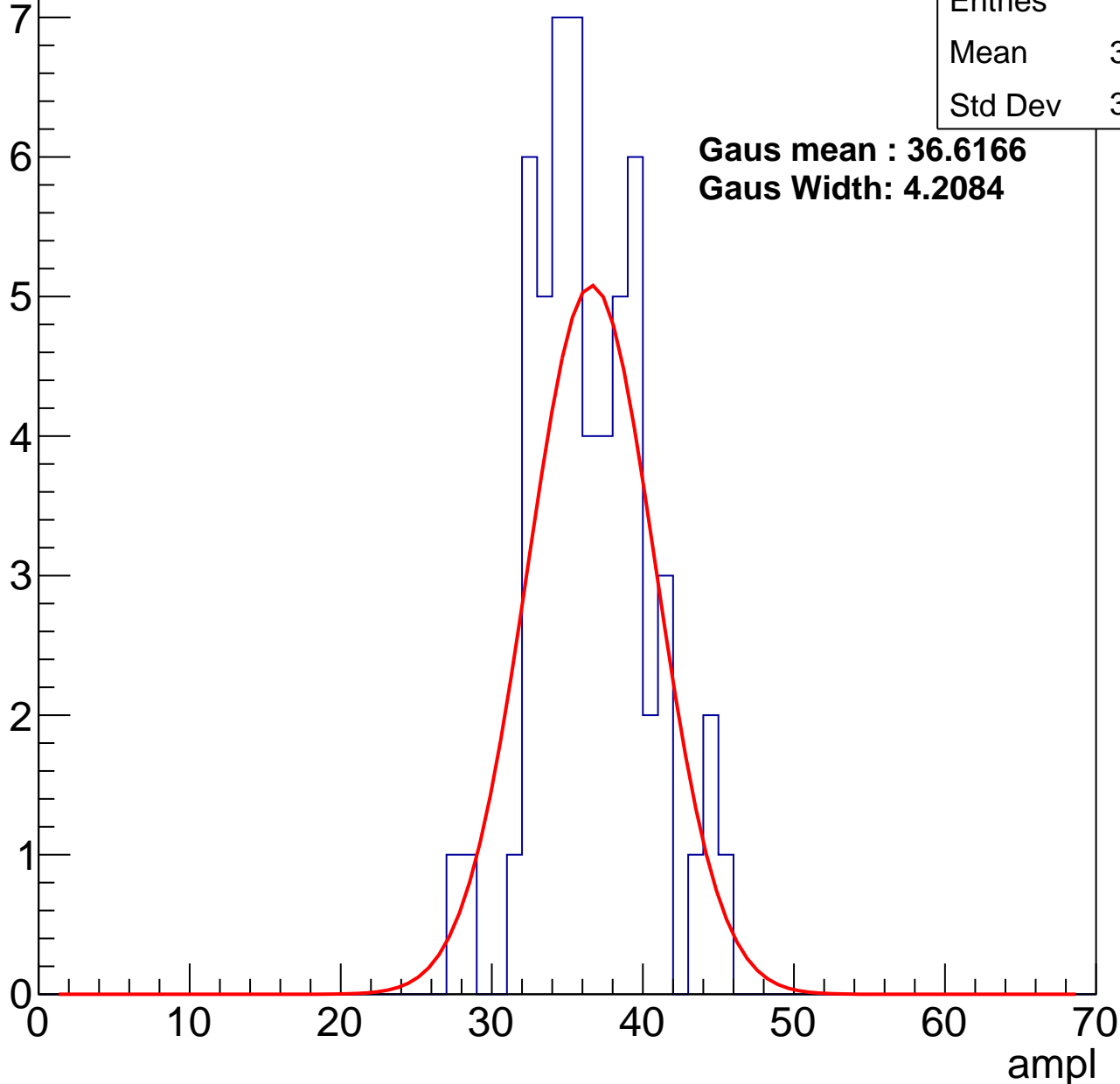
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.09
Std Dev	3.753

Gaus mean : 36.6166

Gaus Width: 4.2084



B0L000S, U2-ch75, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

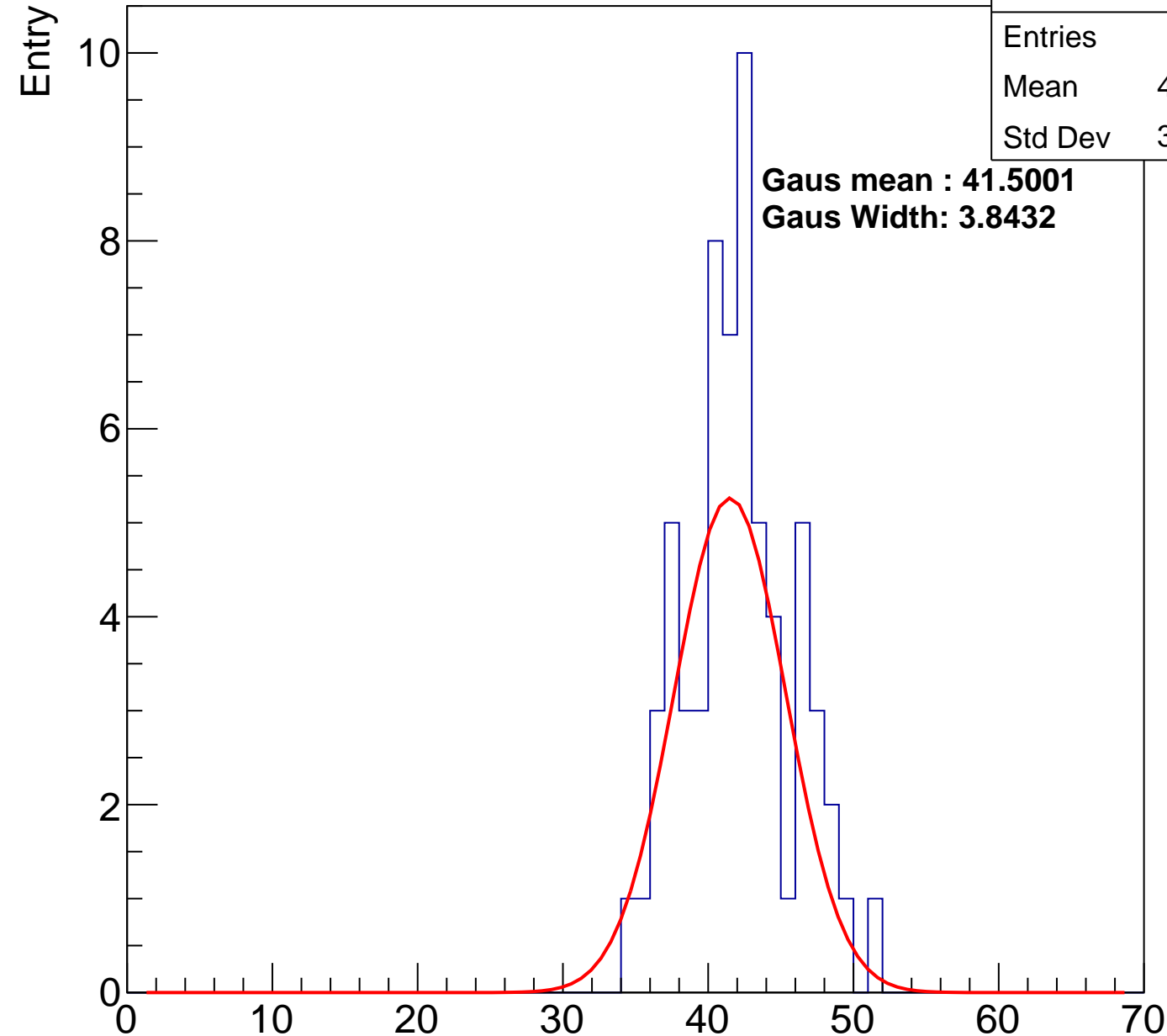
Entries	63
Mean	41.63
Std Dev	3.648

Gaus mean : 41.5001

Gaus Width: 3.8432

10
8
6
4
2
0

ampl

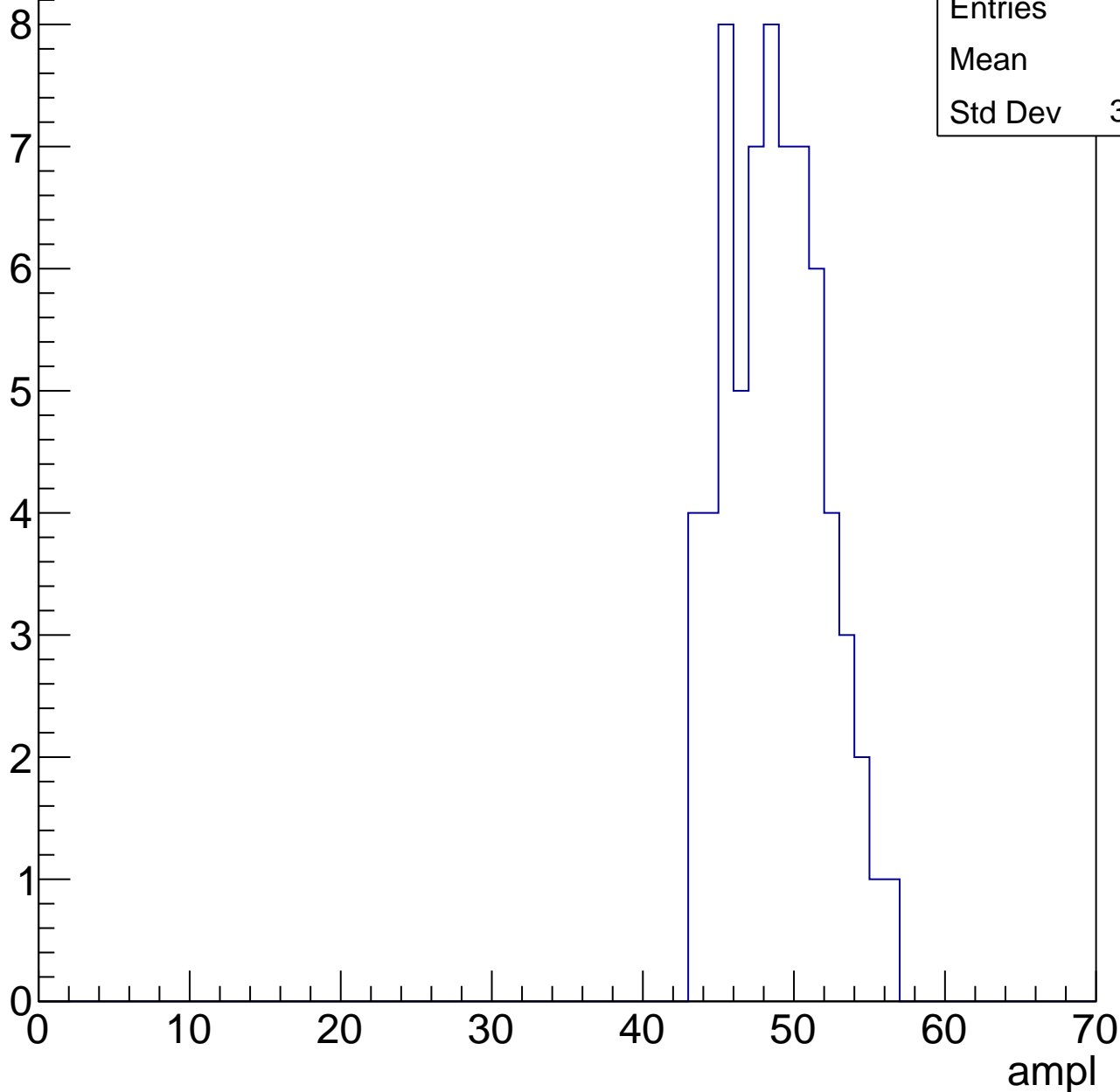


B0L000S, U2-ch75, adc3

calib_packv5_042523_0143.root, FC#5, port B1

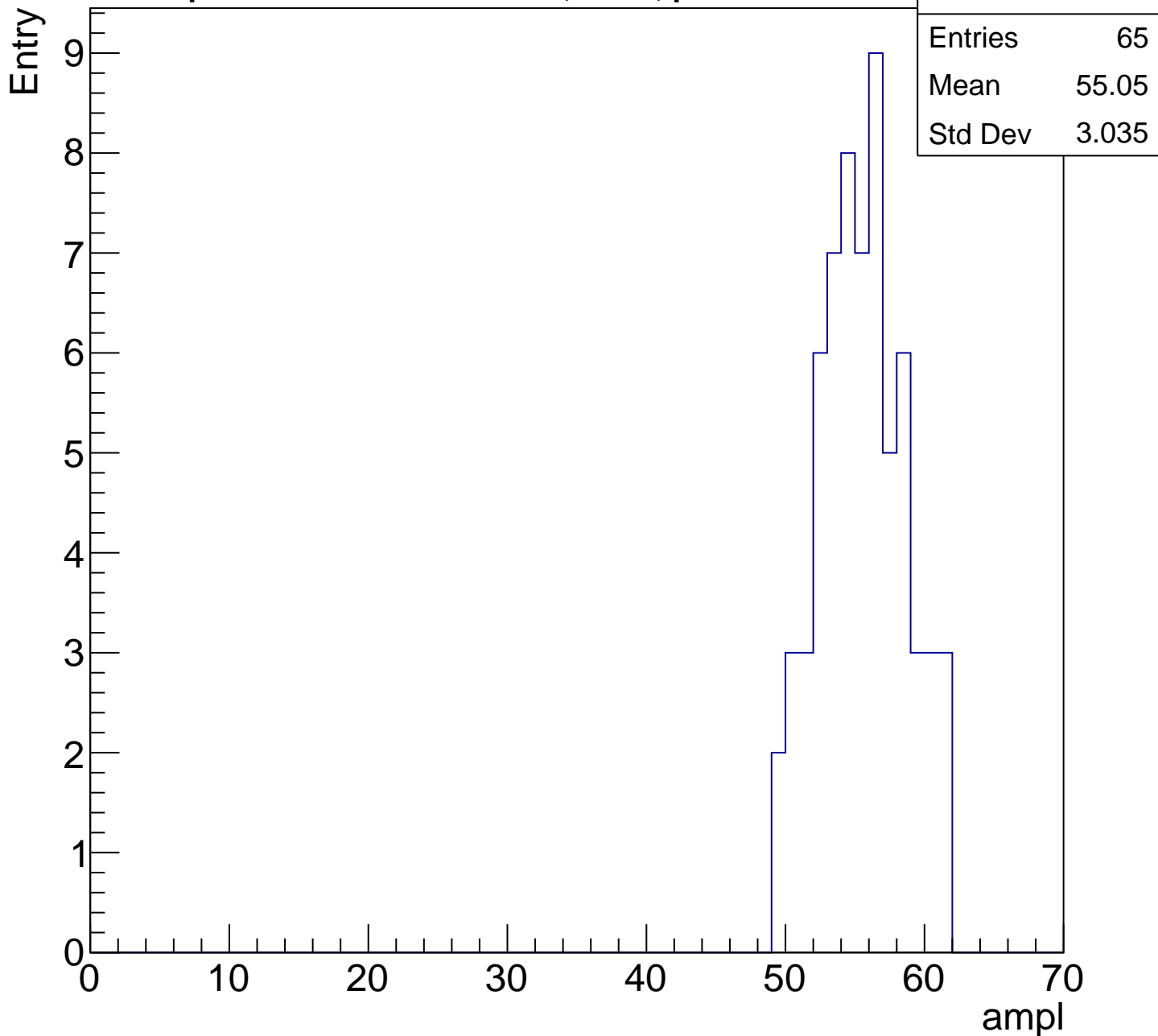
Entry

Entries	67
Mean	48.3
Std Dev	3.162



B0L000S, U2-ch75, adc4

calib_packv5_042523_0143.root, FC#5, port B1

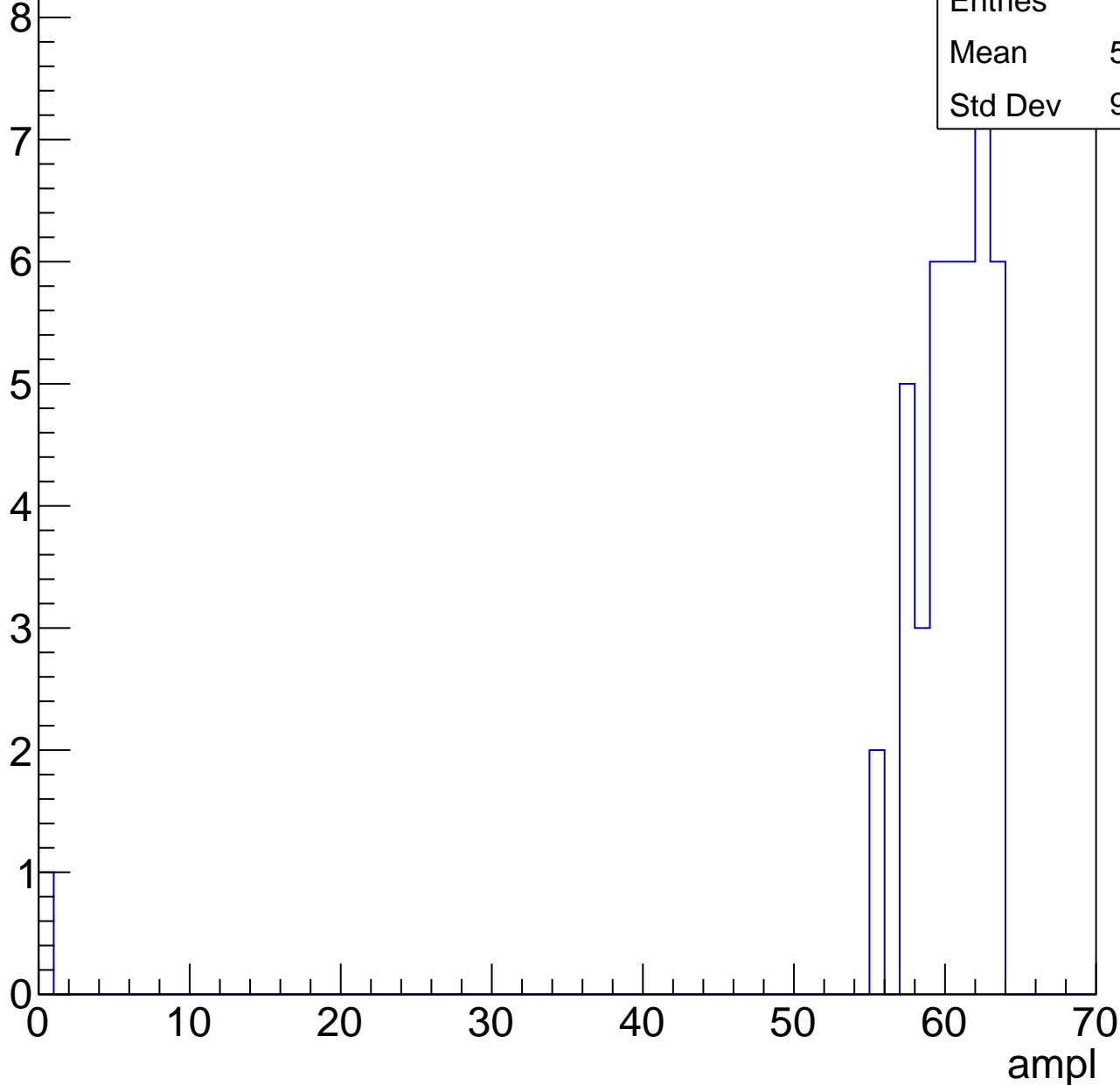


B0L000S, U2-ch75, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

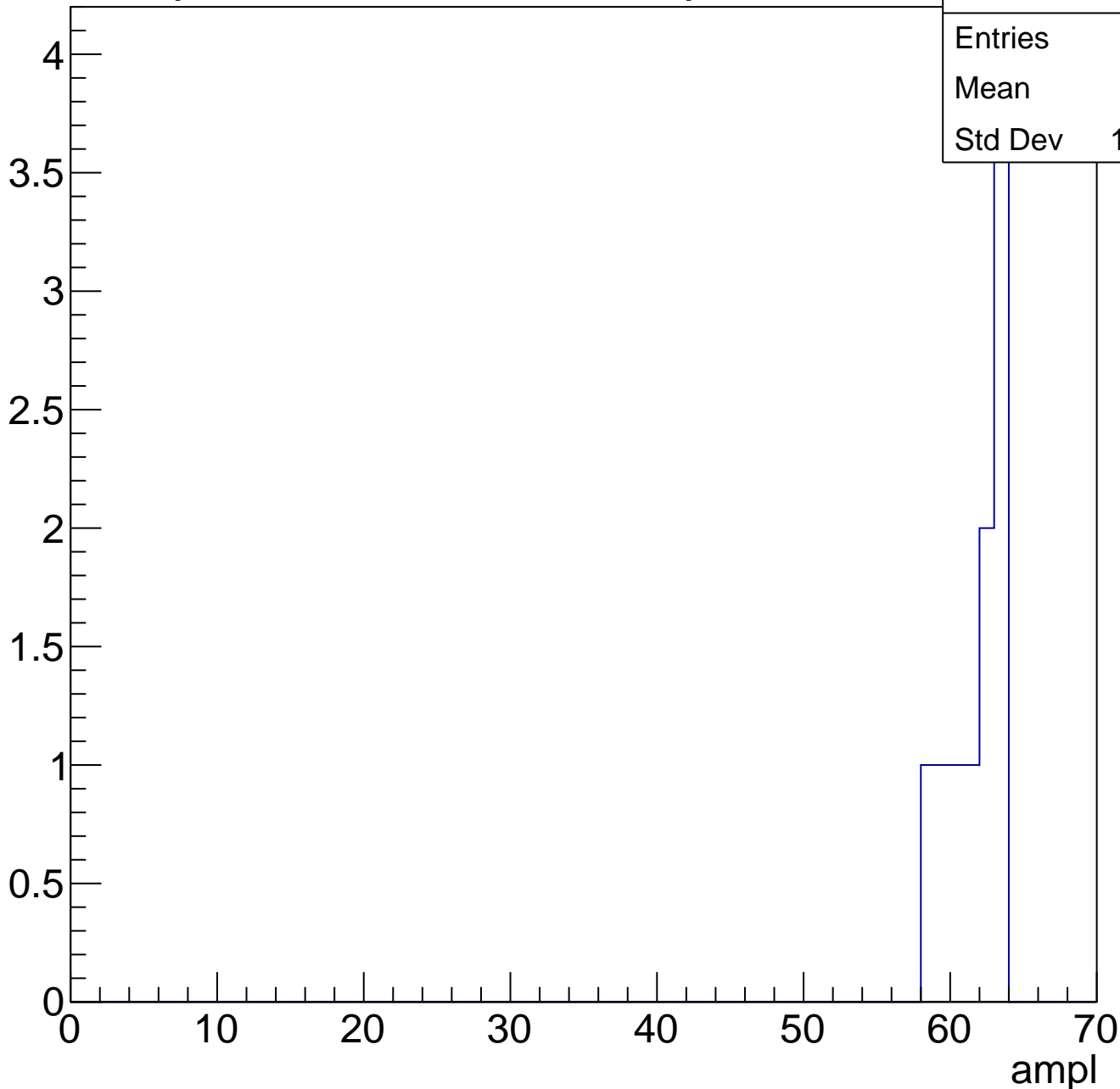
Entries	43
Mean	58.67
Std Dev	9.313



B0L000S, U2-ch75, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	10
Mean	61.4
Std Dev	1.744

B0L000S, U2-ch75, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch76, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	73
Mean	28.67
Std Dev	4.478

Gaus mean : 29.3289

Gaus Width: 3.2901

Entry

10

8

6

4

2

0

0

10

20

30

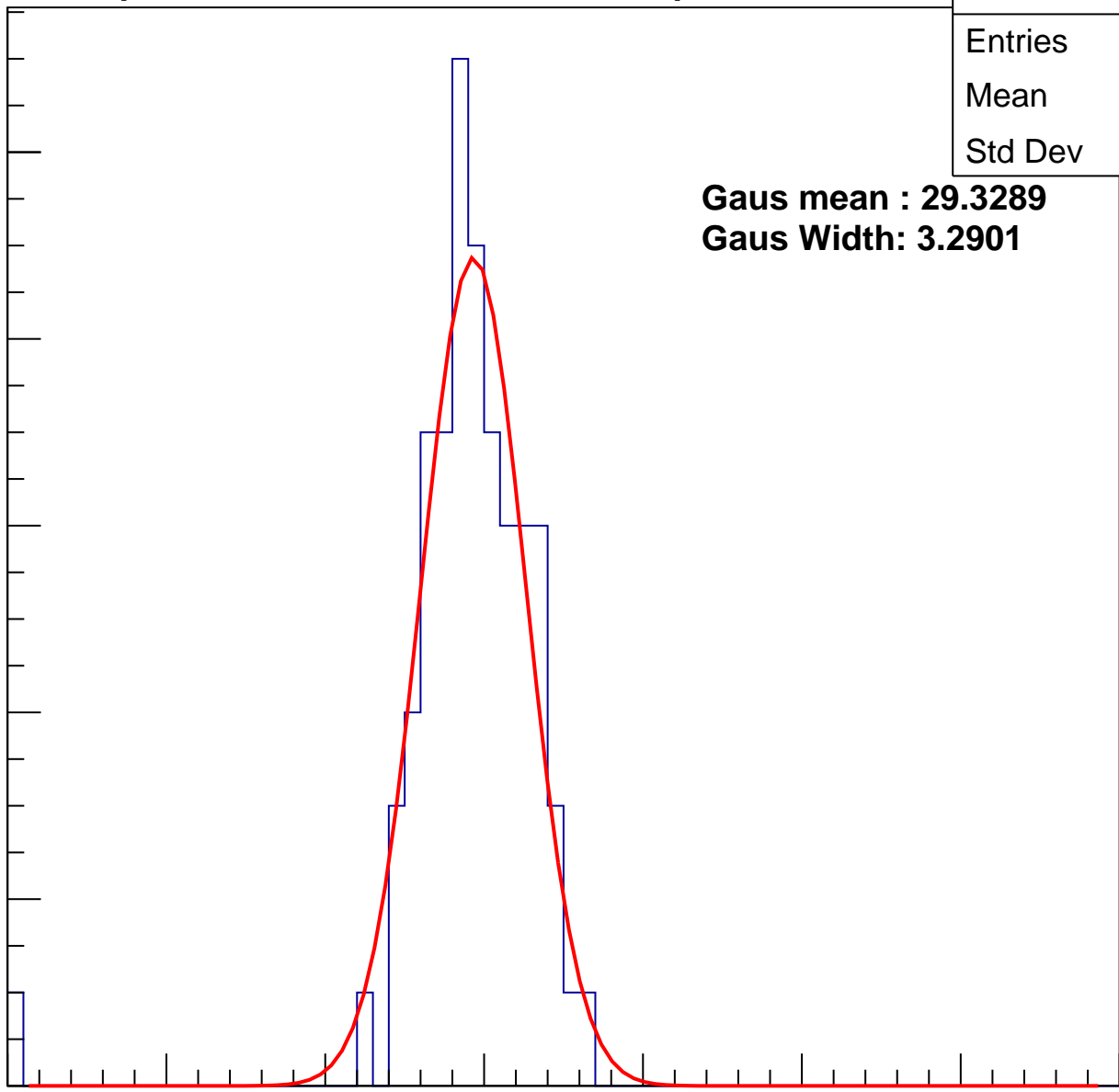
40

50

60

70

ampl



B0L000S, U2-ch76, adc1

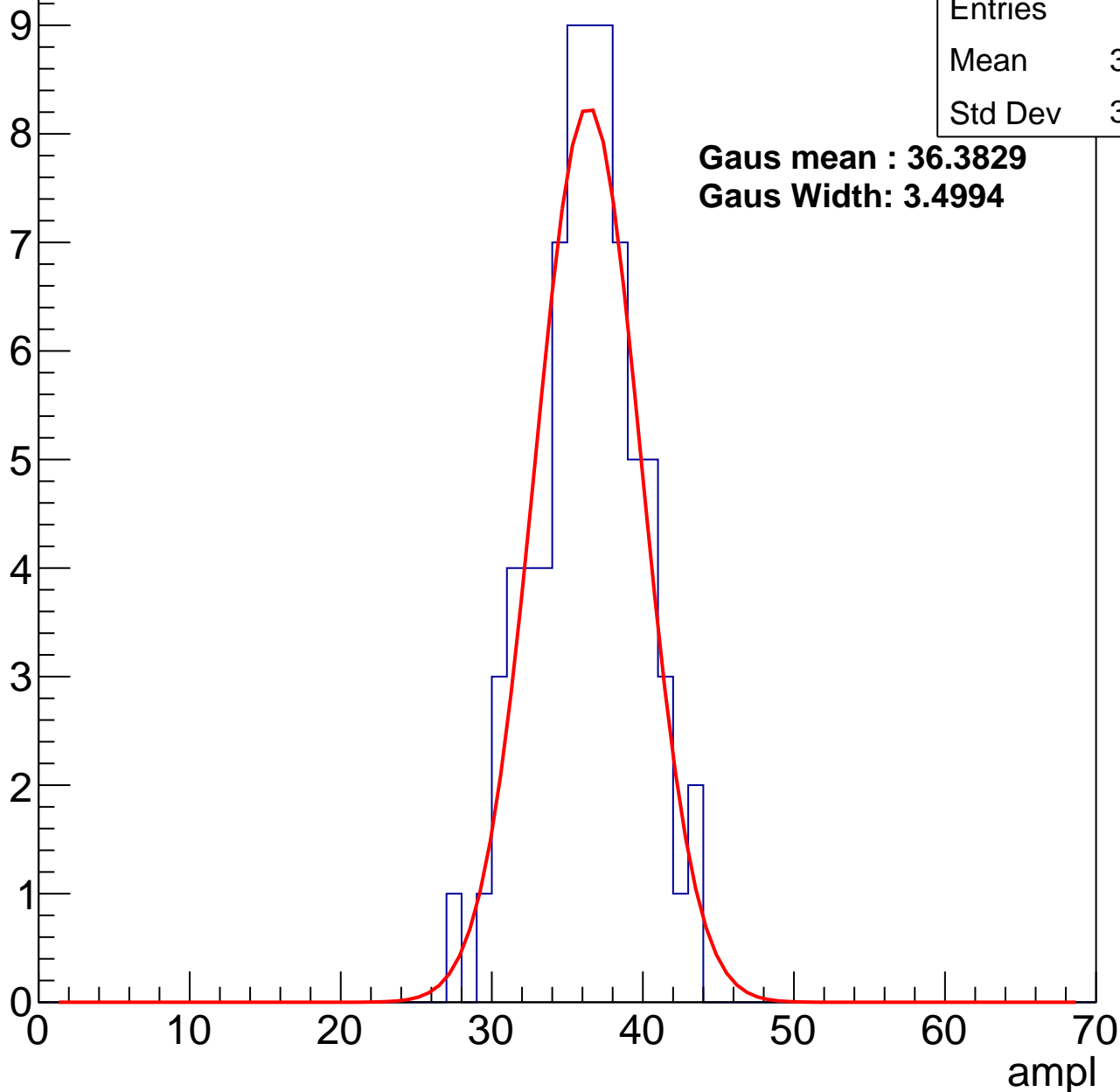
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	35.84
Std Dev	3.377

Gaus mean : 36.3829

Gaus Width: 3.4994



B0L000S, U2-ch76, adc2

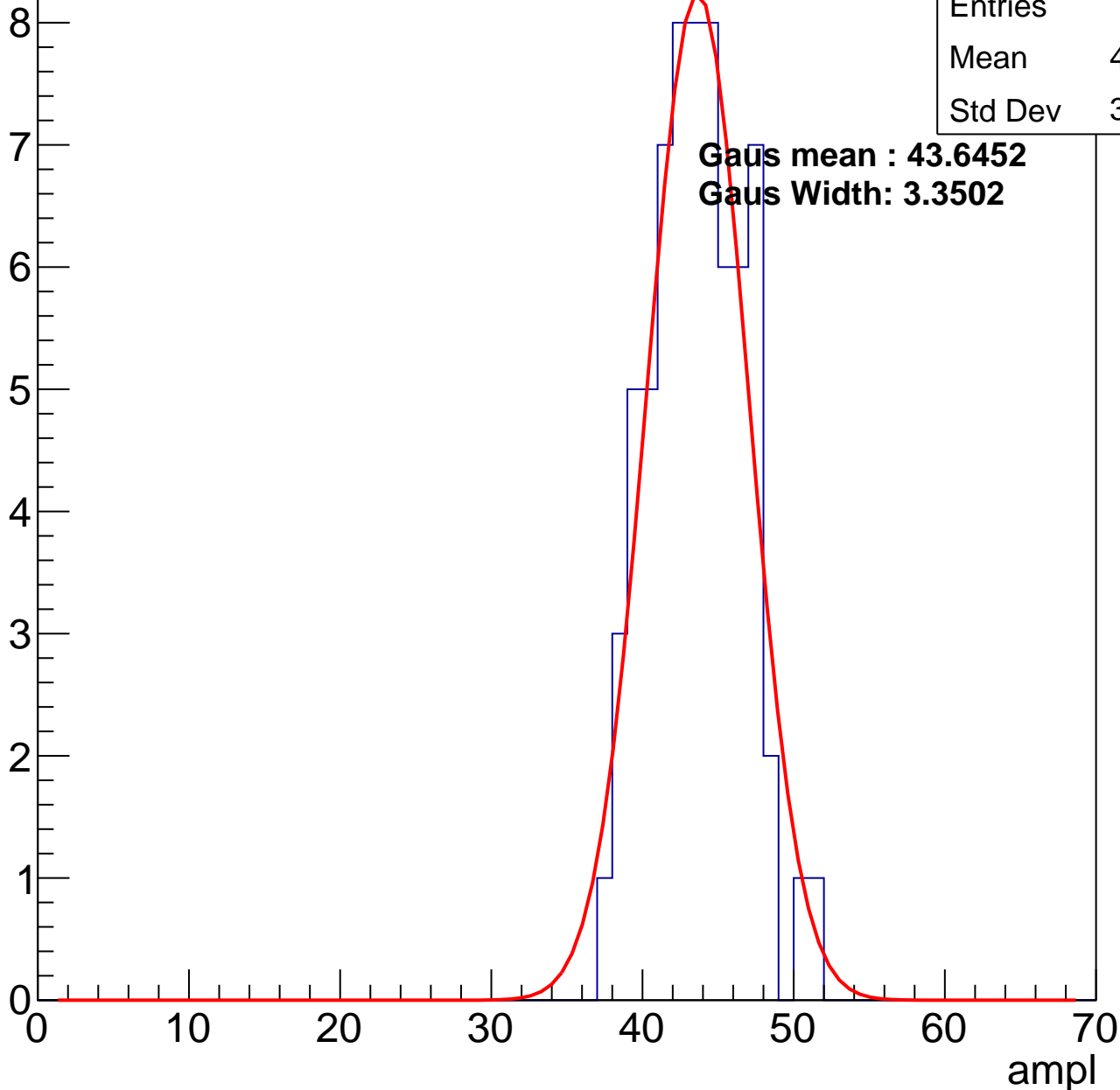
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.19
Std Dev	3.045

Gaus mean : 43.6452

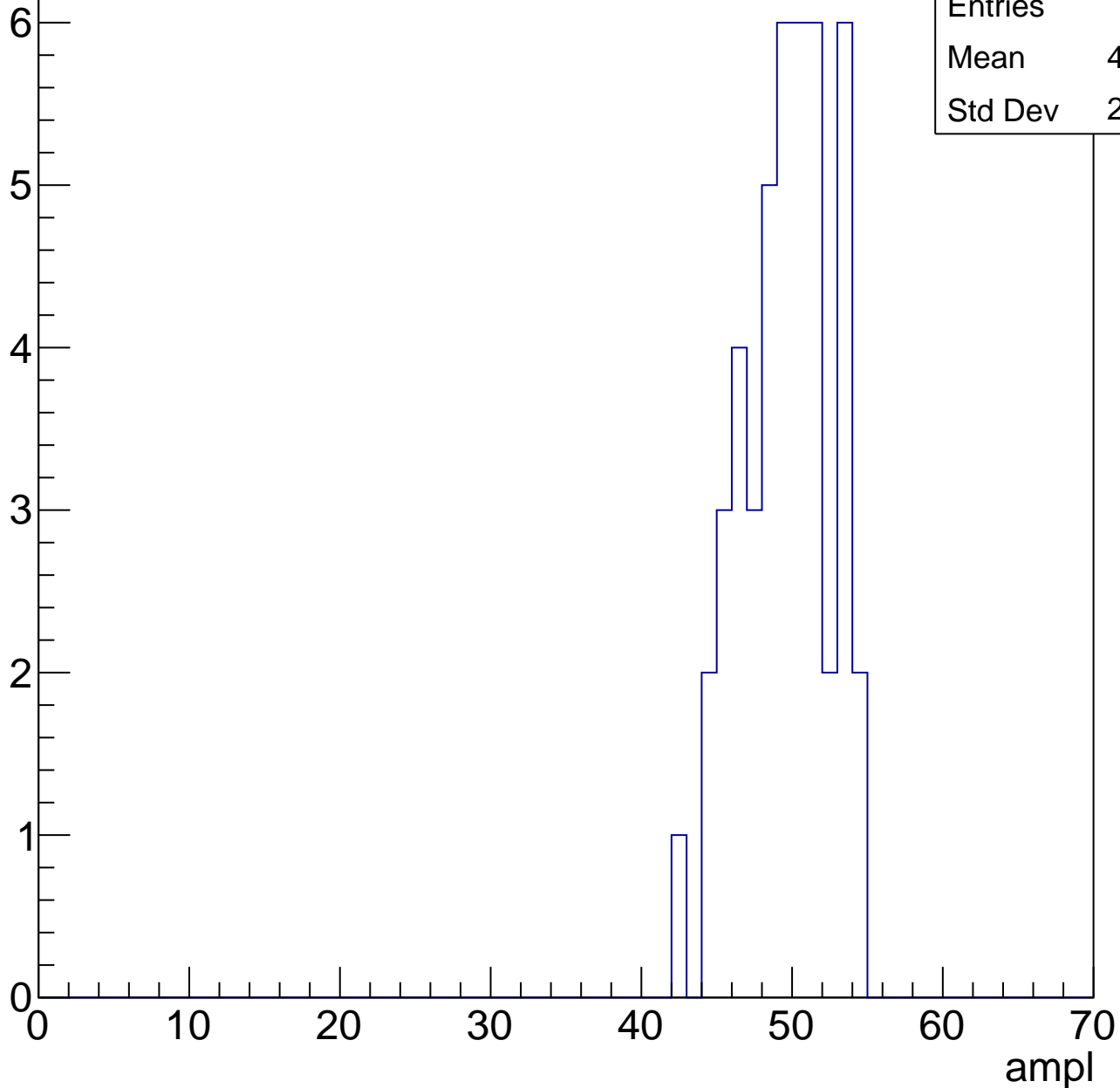
Gaus Width: 3.3502



B0L000S, U2-ch76, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



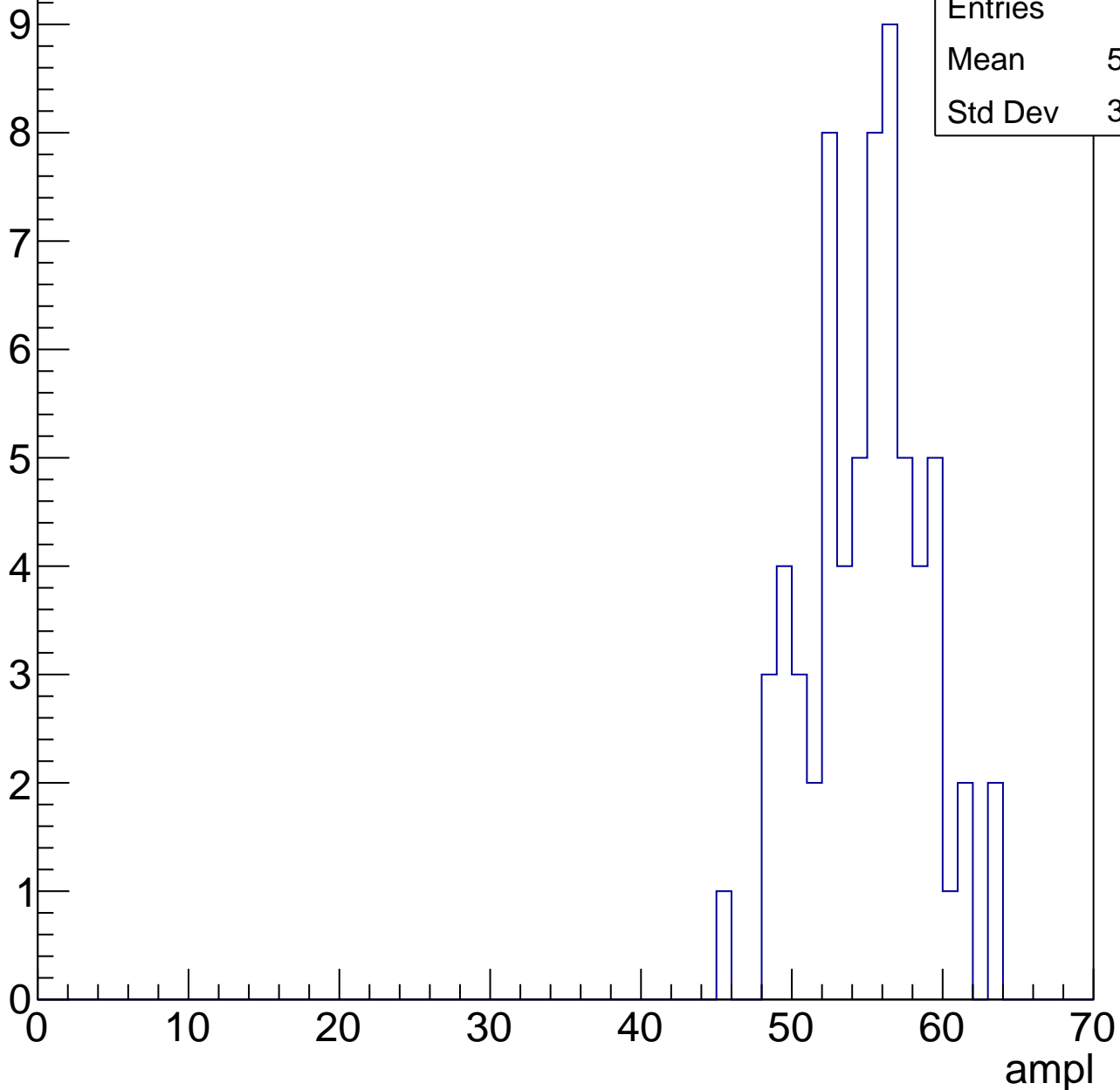
Entries	46
Mean	49.13
Std Dev	2.924

B0L000S, U2-ch76, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	54.53
Std Dev	3.787

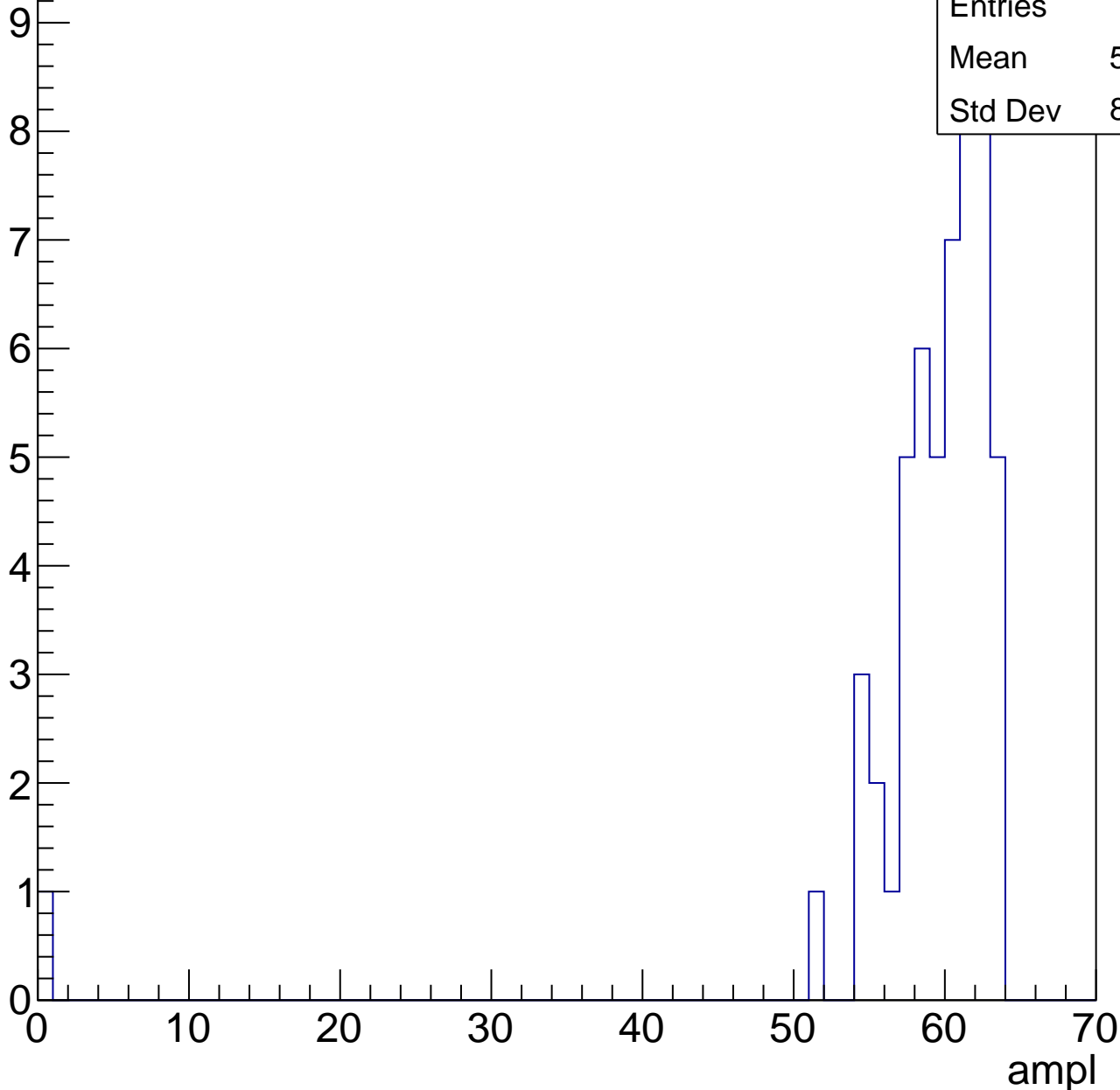


B0L000S, U2-ch76, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

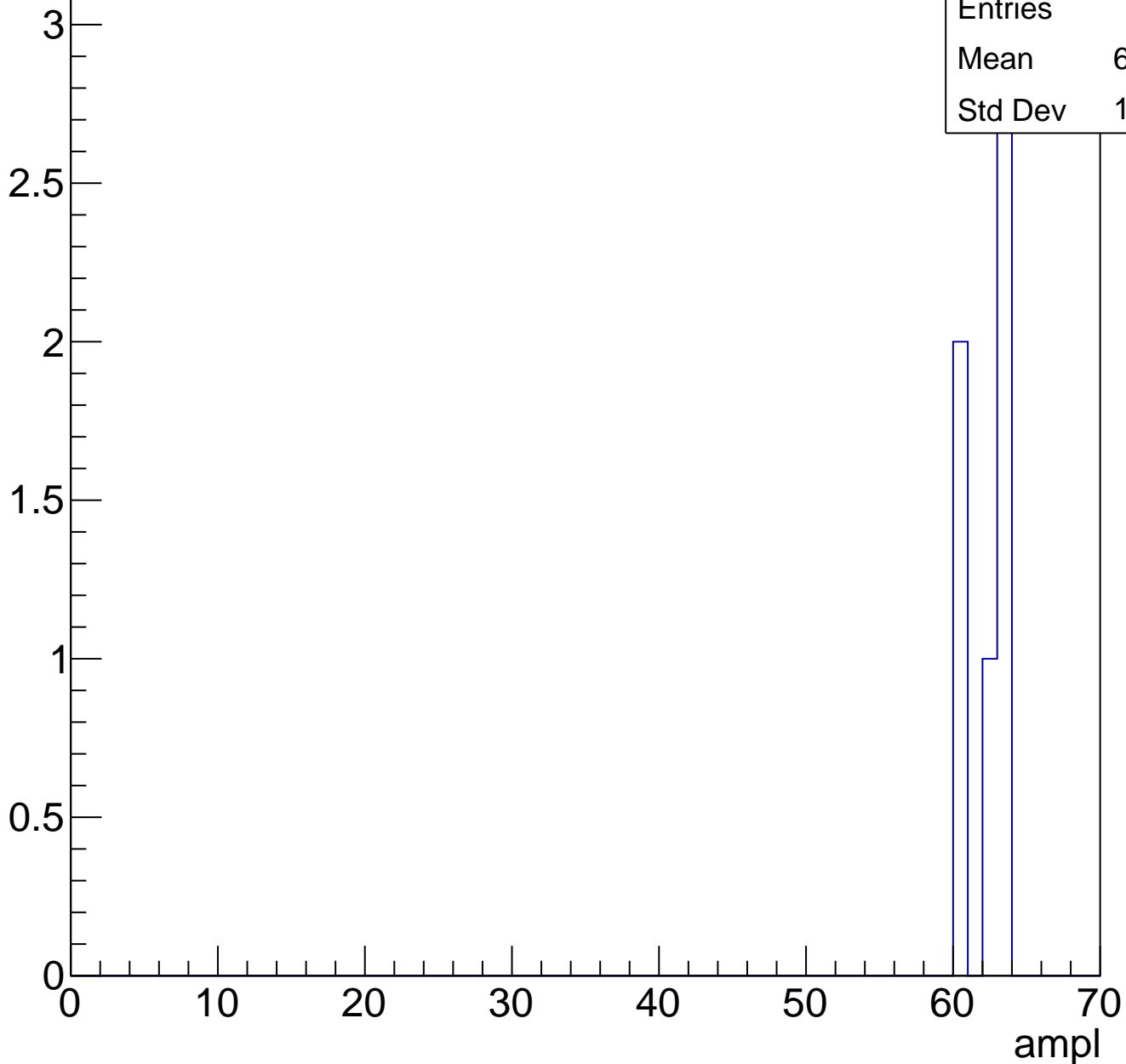
Entries	53
Mean	58.26
Std Dev	8.532



B0L000S, U2-ch76, adc6

calib_packv5_042523_0143.root, FC#5, port B1

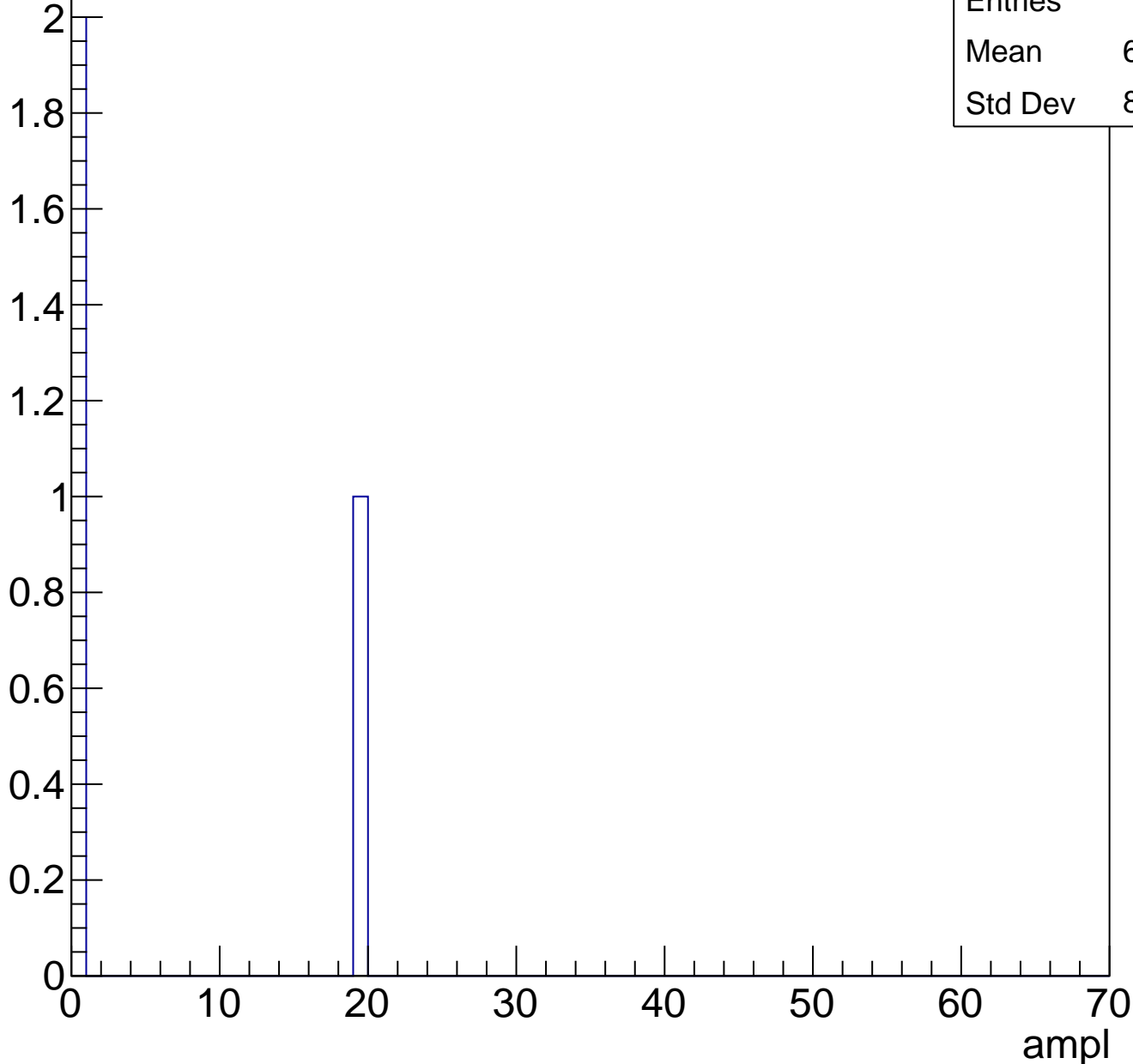
Entry



B0L000S, U2-ch76, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B0L000S, U2-ch77, adc0

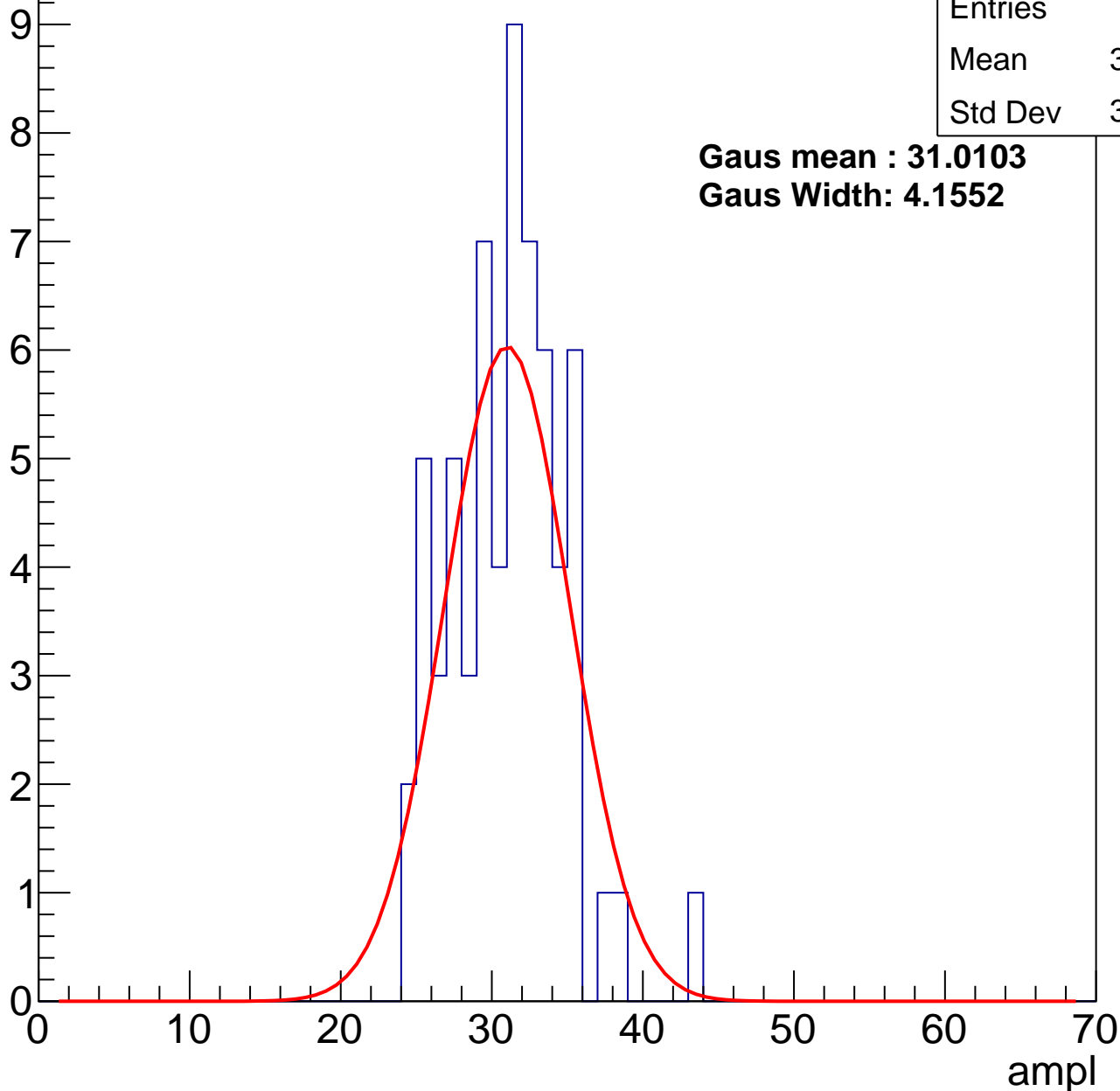
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.59
Std Dev	3.694

Gaus mean : 31.0103

Gaus Width: 4.1552



B0L000S, U2-ch77, adc1

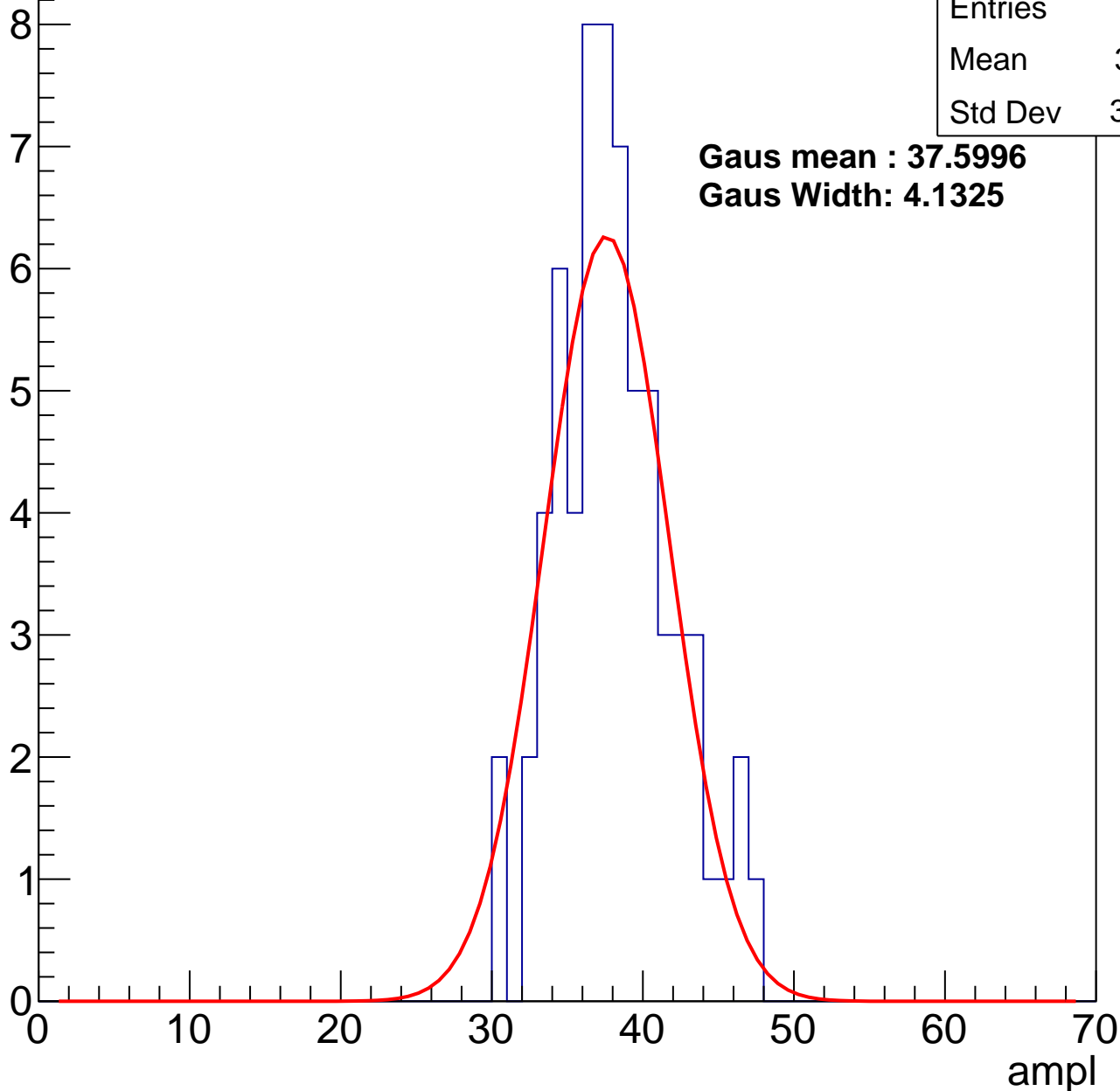
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.71
Std Dev	3.794

Gaus mean : 37.5996

Gaus Width: 4.1325



B0L000S, U2-ch77, adc2

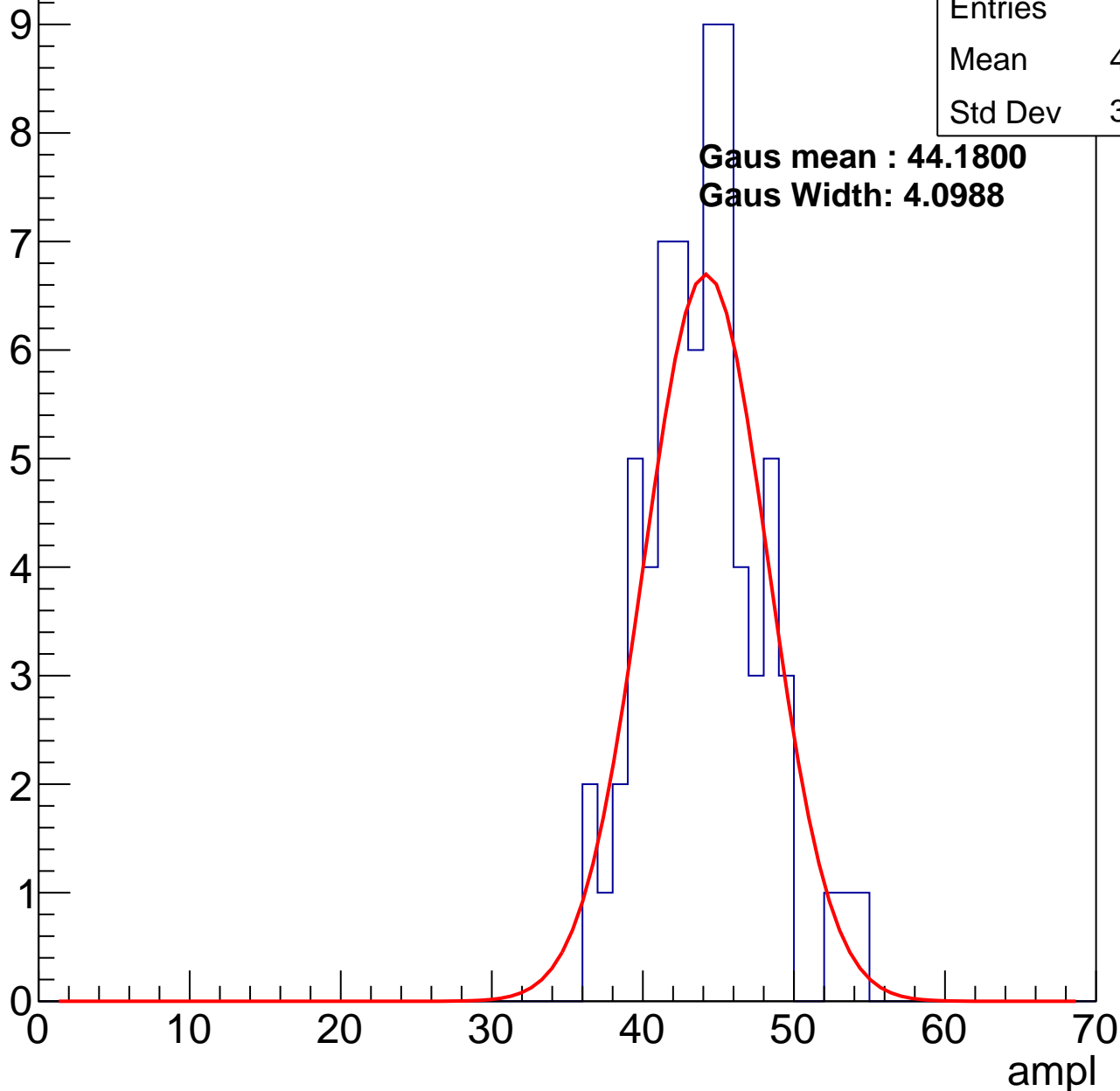
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.59
Std Dev	3.732

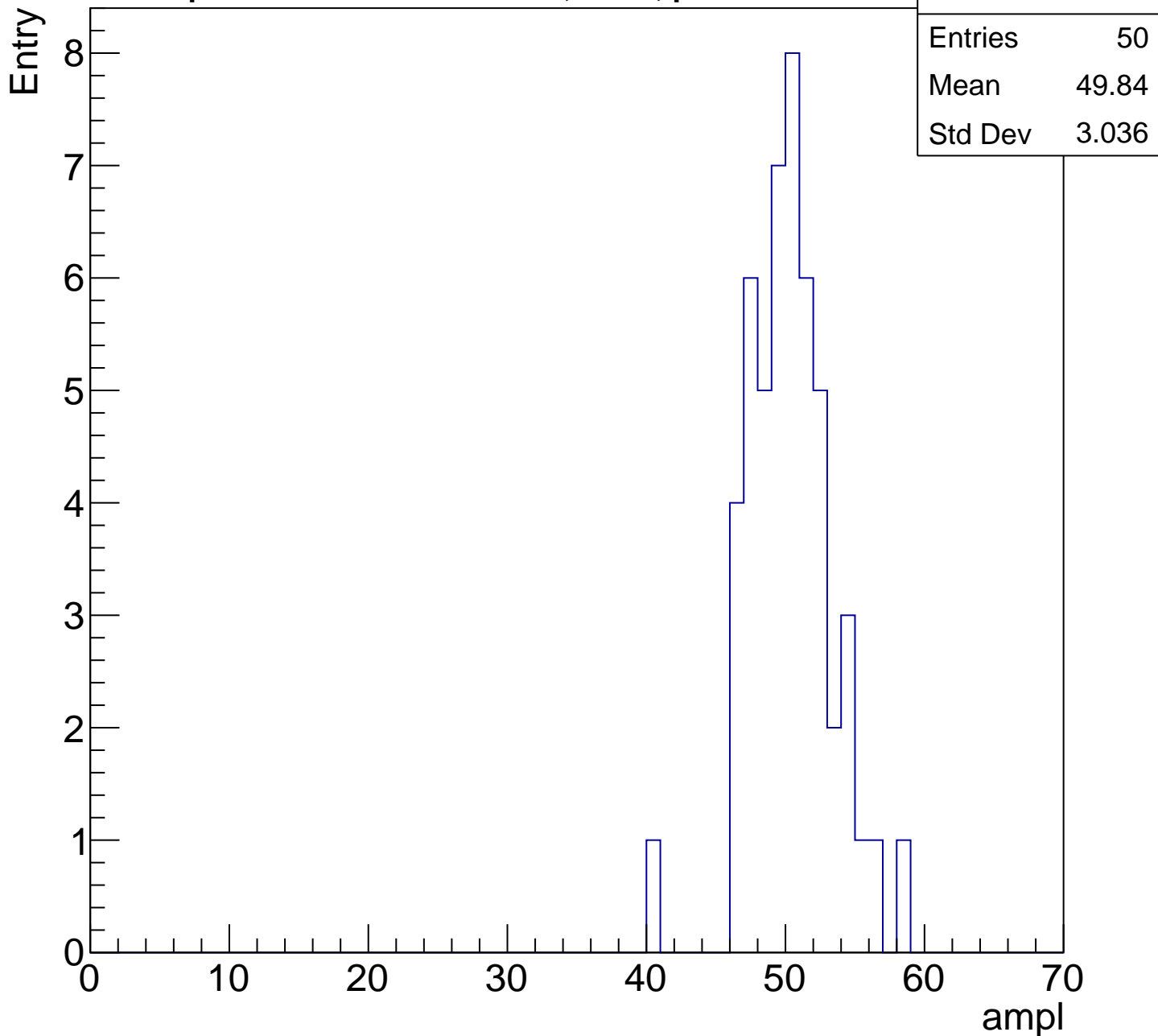
Gaus mean : 44.1800

Gaus Width: 4.0988



B0L000S, U2-ch77, adc3

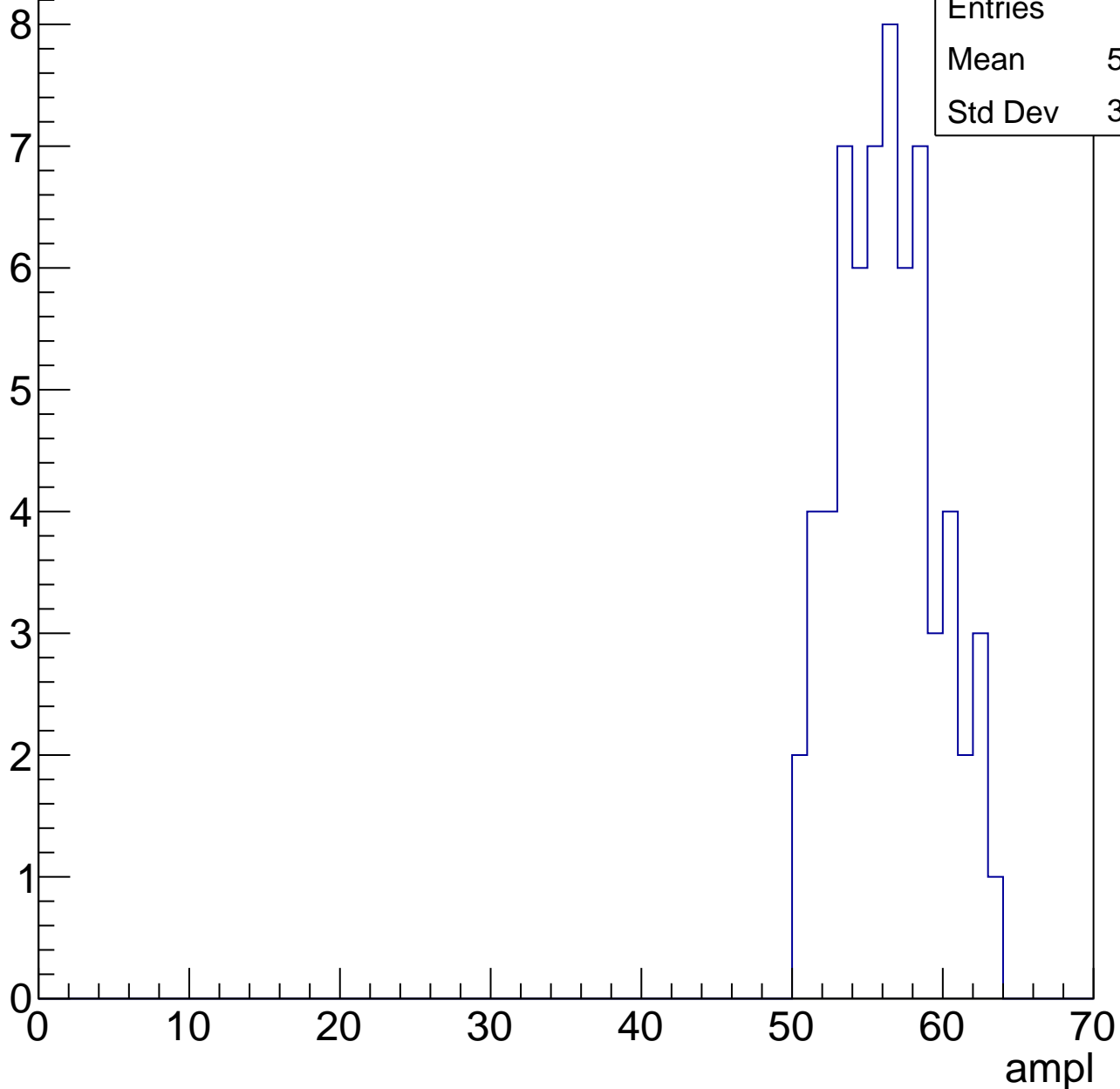
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch77, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch77, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.73
Std Dev	9.566

ampl

0

10

20

30

40

50

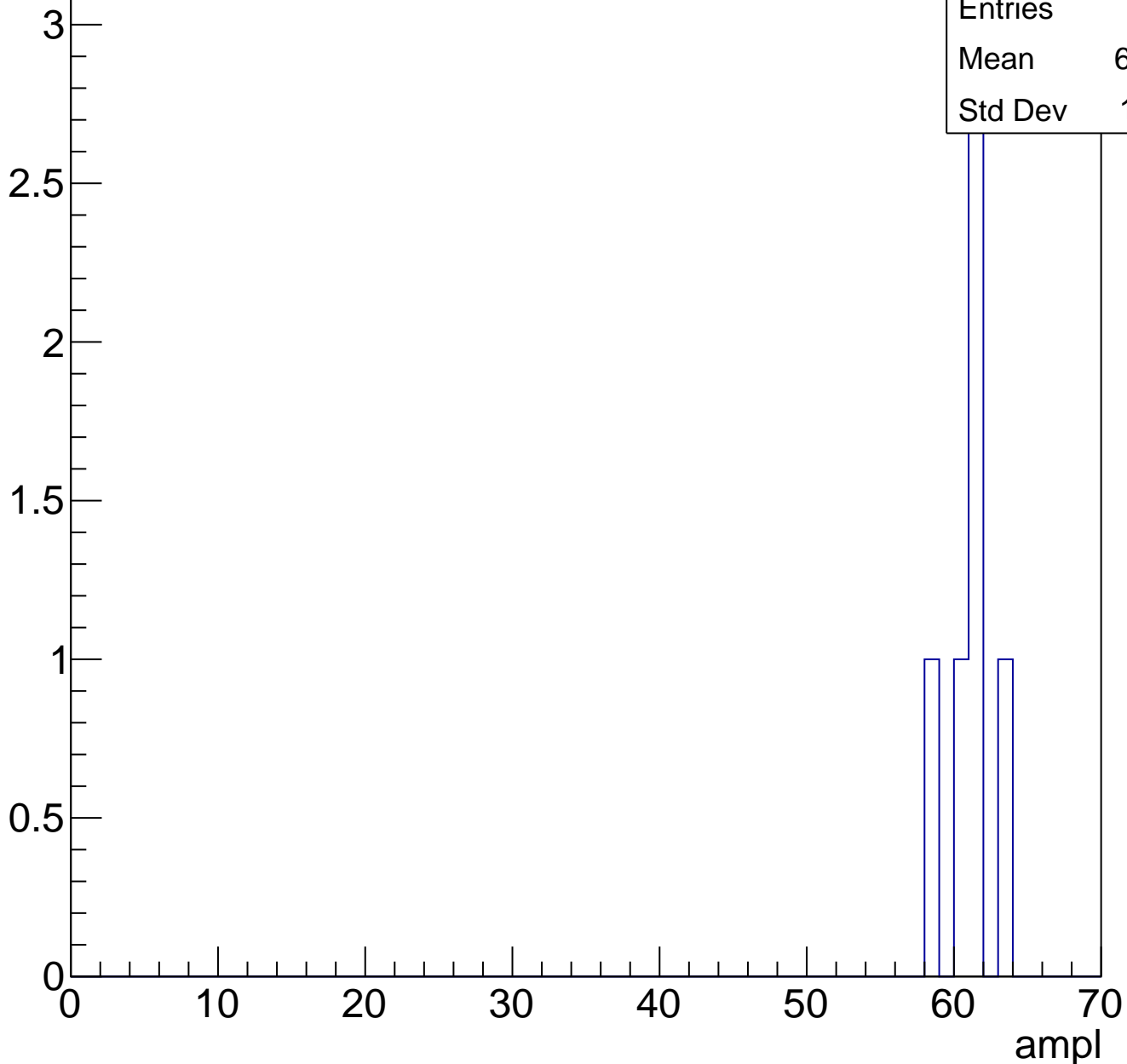
60

70

B0L000S, U2-ch77, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch77, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch78, adc0

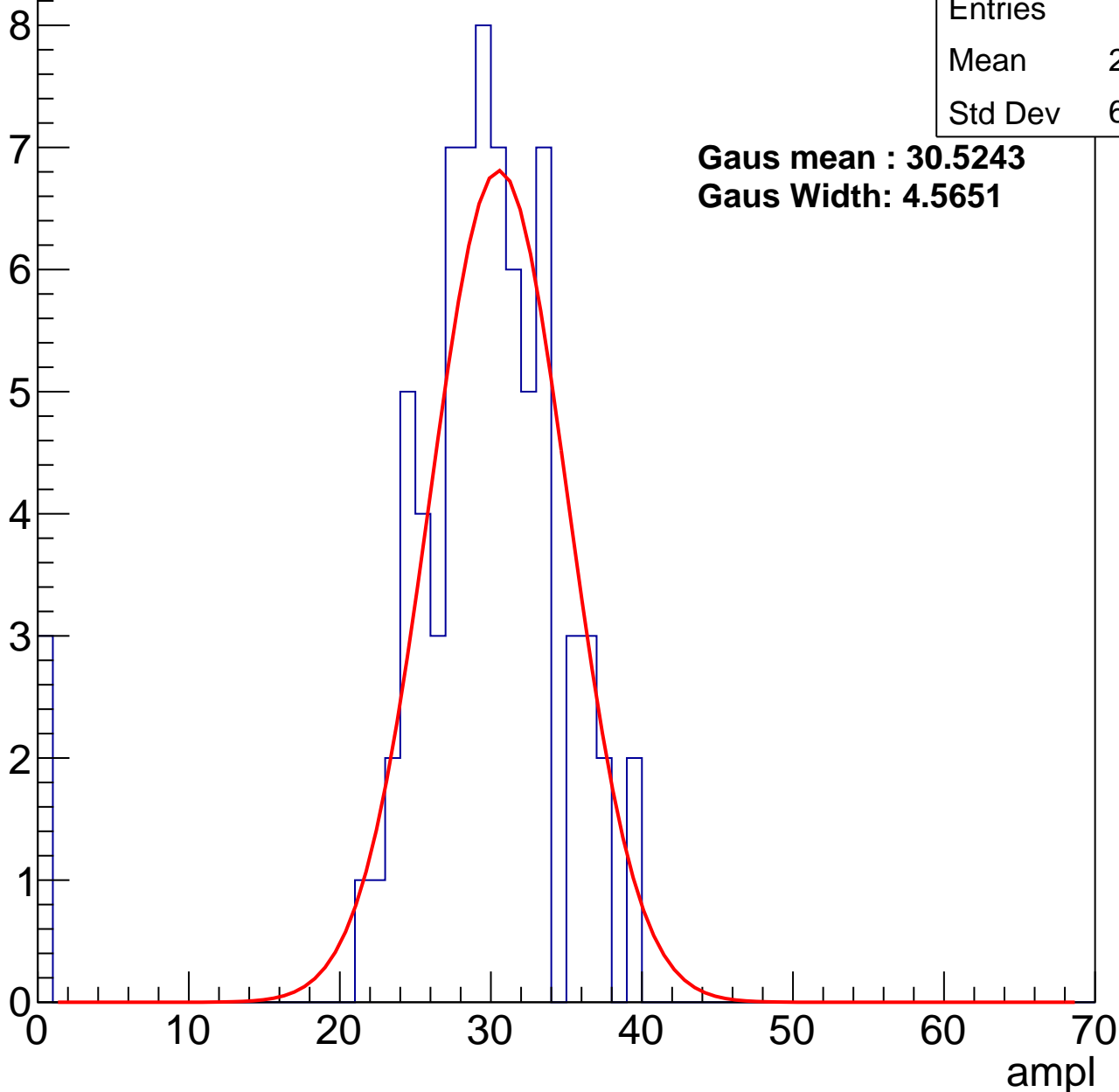
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	28.37
Std Dev	6.977

Gaus mean : 30.5243

Gaus Width: 4.5651



B0L000S, U2-ch78, adc1

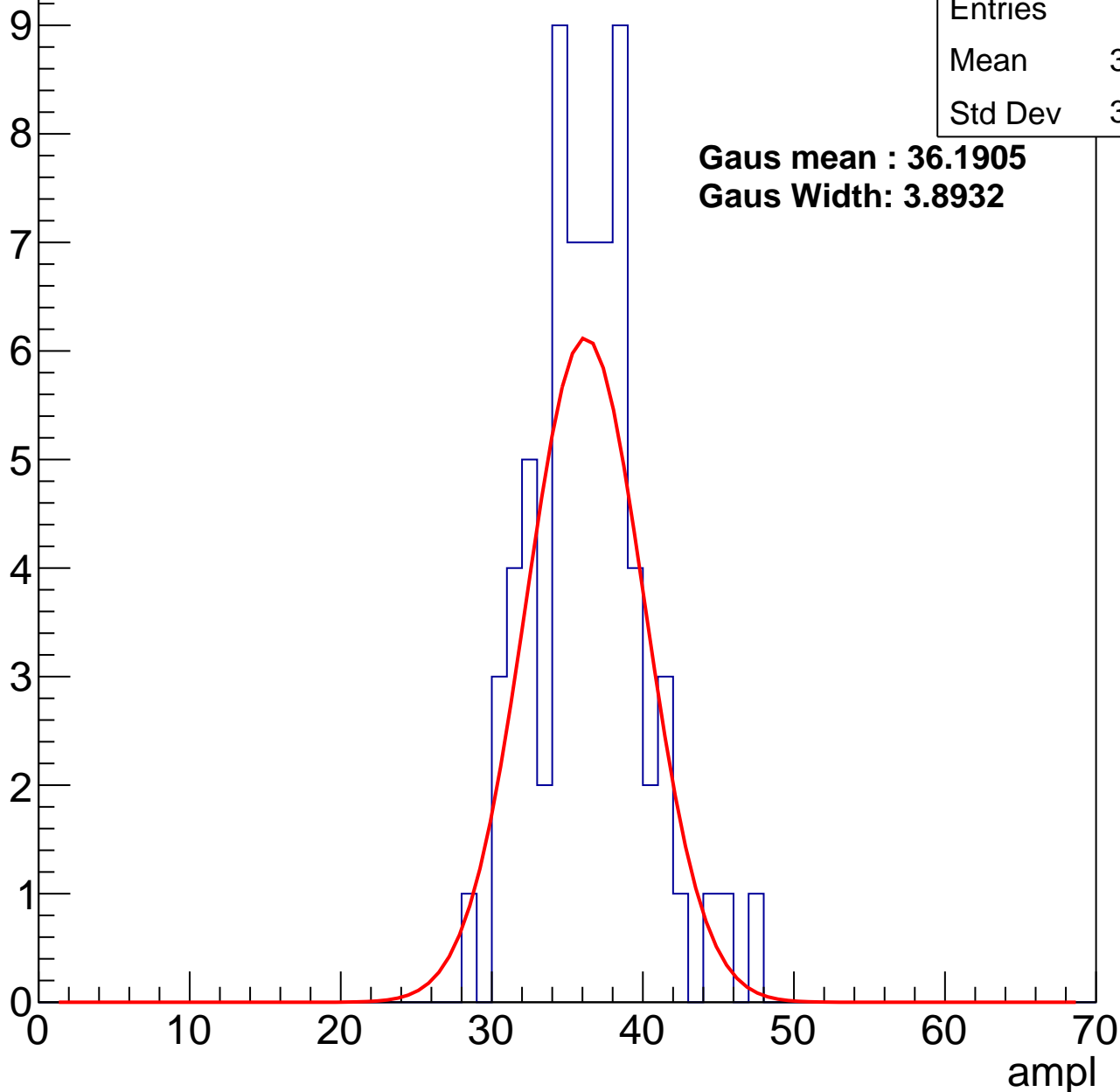
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.96
Std Dev	3.667

Gaus mean : 36.1905

Gaus Width: 3.8932



B0L000S, U2-ch78, adc2

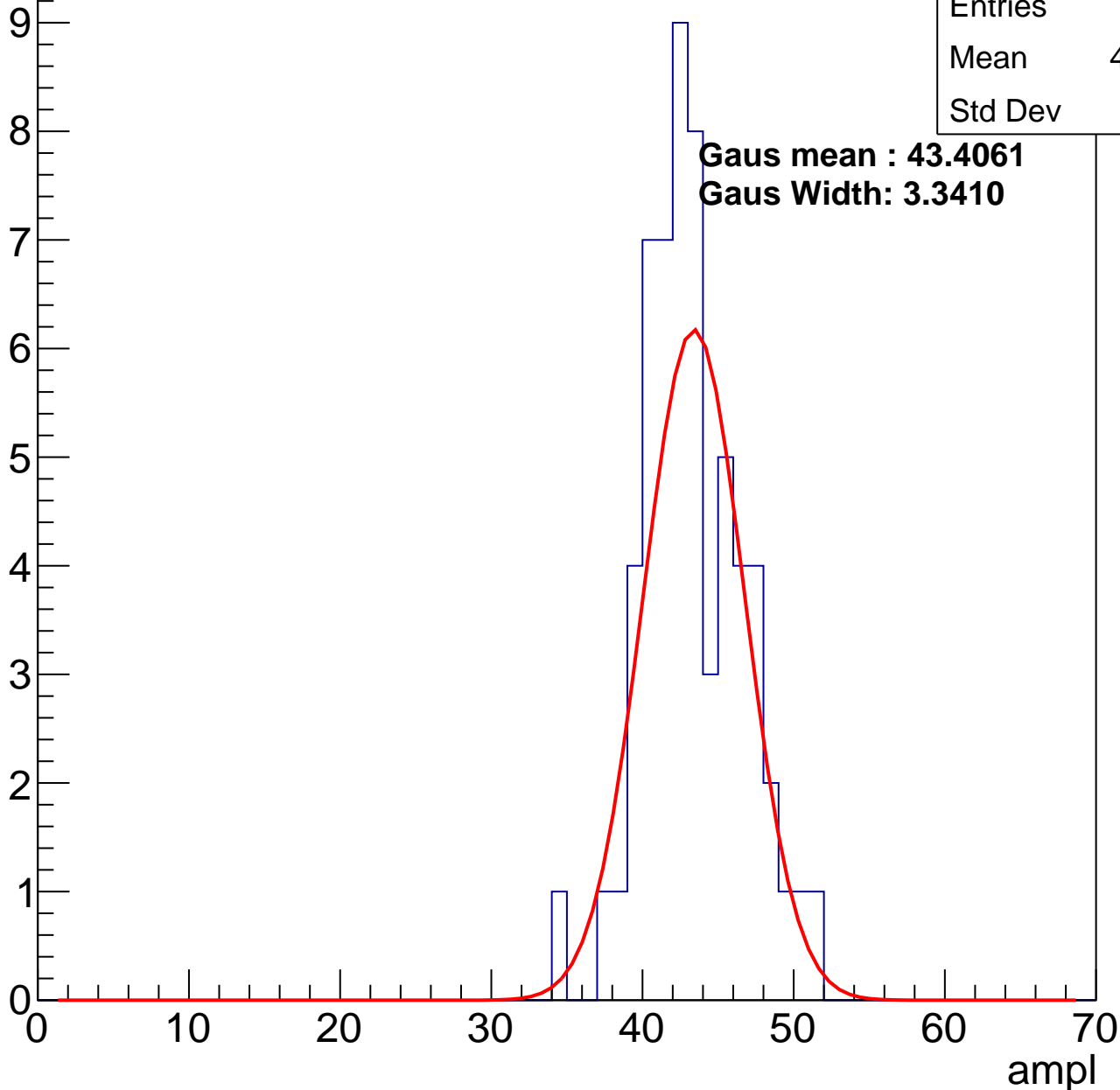
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.86
Std Dev	3.26

Gaus mean : 43.4061

Gaus Width: 3.3410

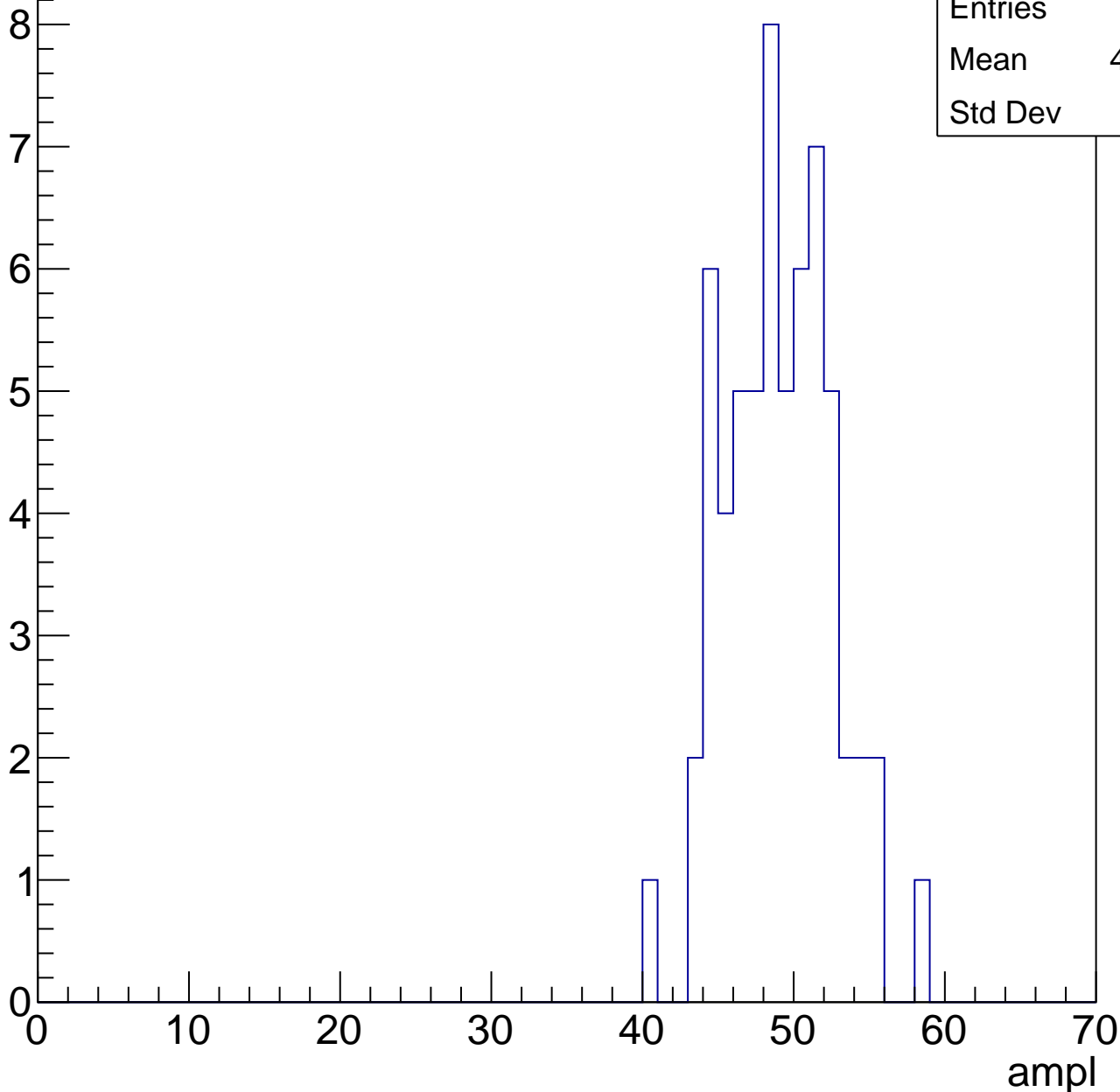


B0L000S, U2-ch78, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	48.57
Std Dev	3.49

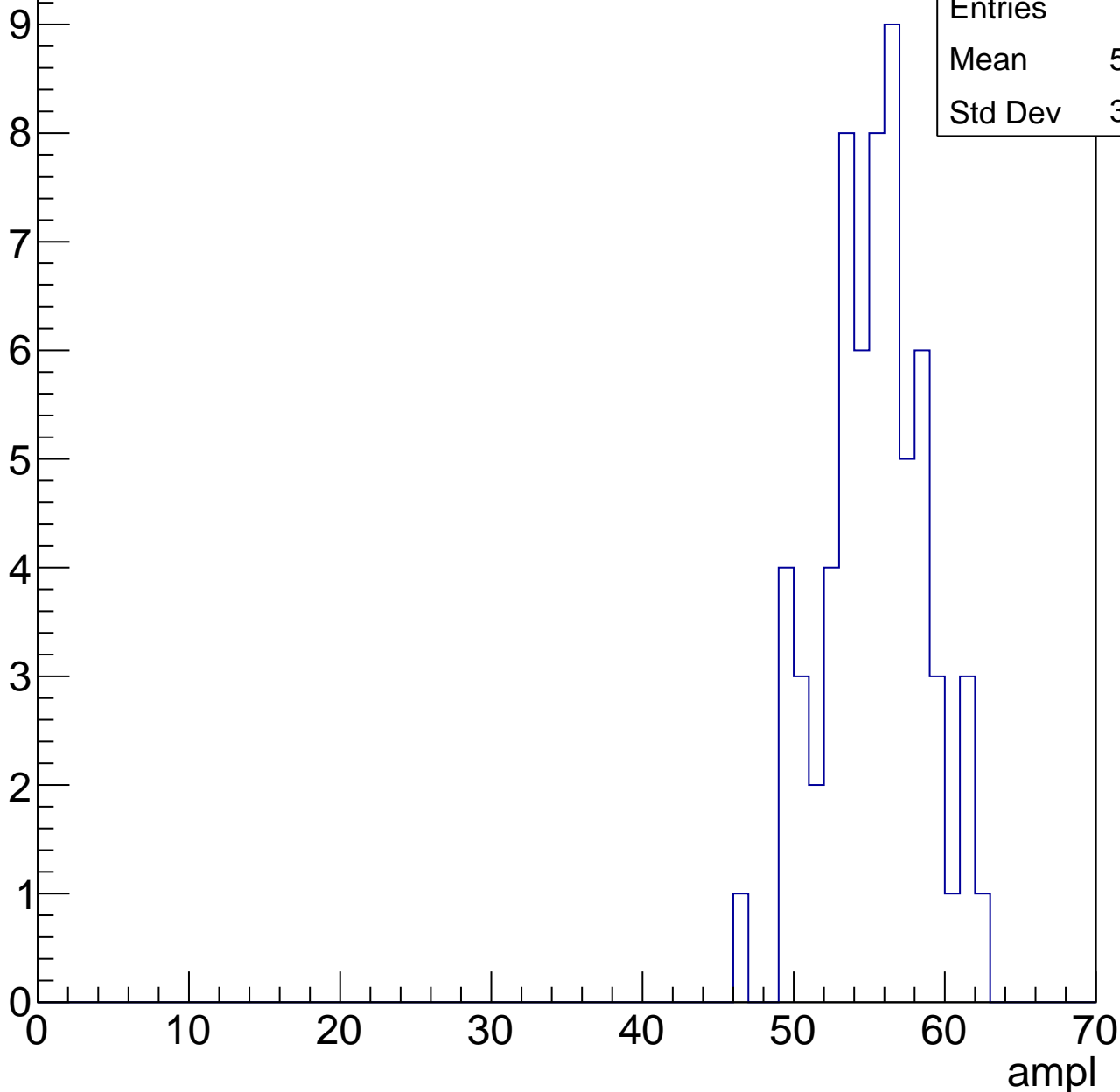


B0L000S, U2-ch78, adc4

calib_packv5_042523_0143.root, FC#5, port B1

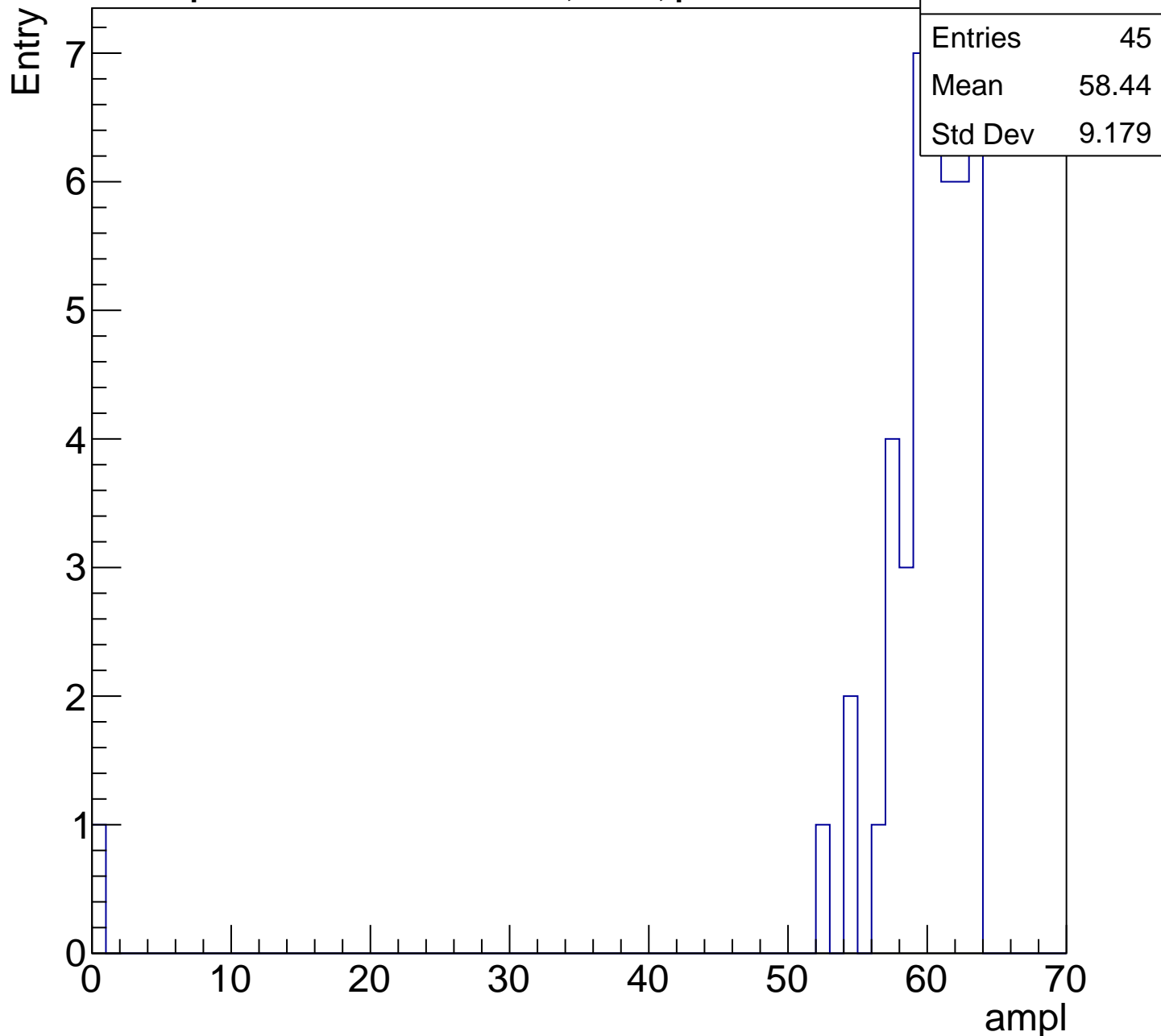
Entry

Entries	64
Mean	54.83
Std Dev	3.347



B0L000S, U2-ch78, adc5

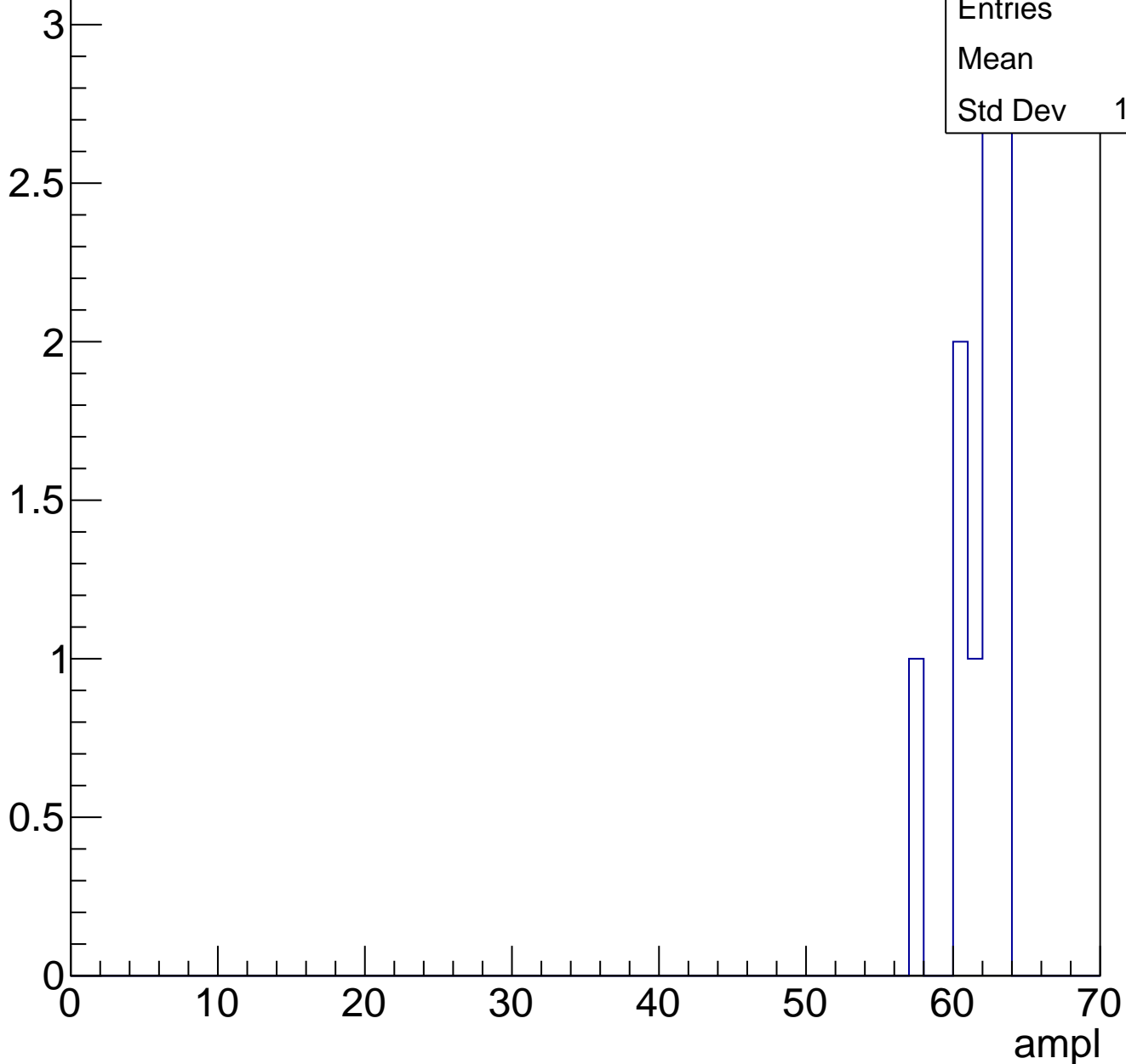
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch78, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch78, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch79, adc0

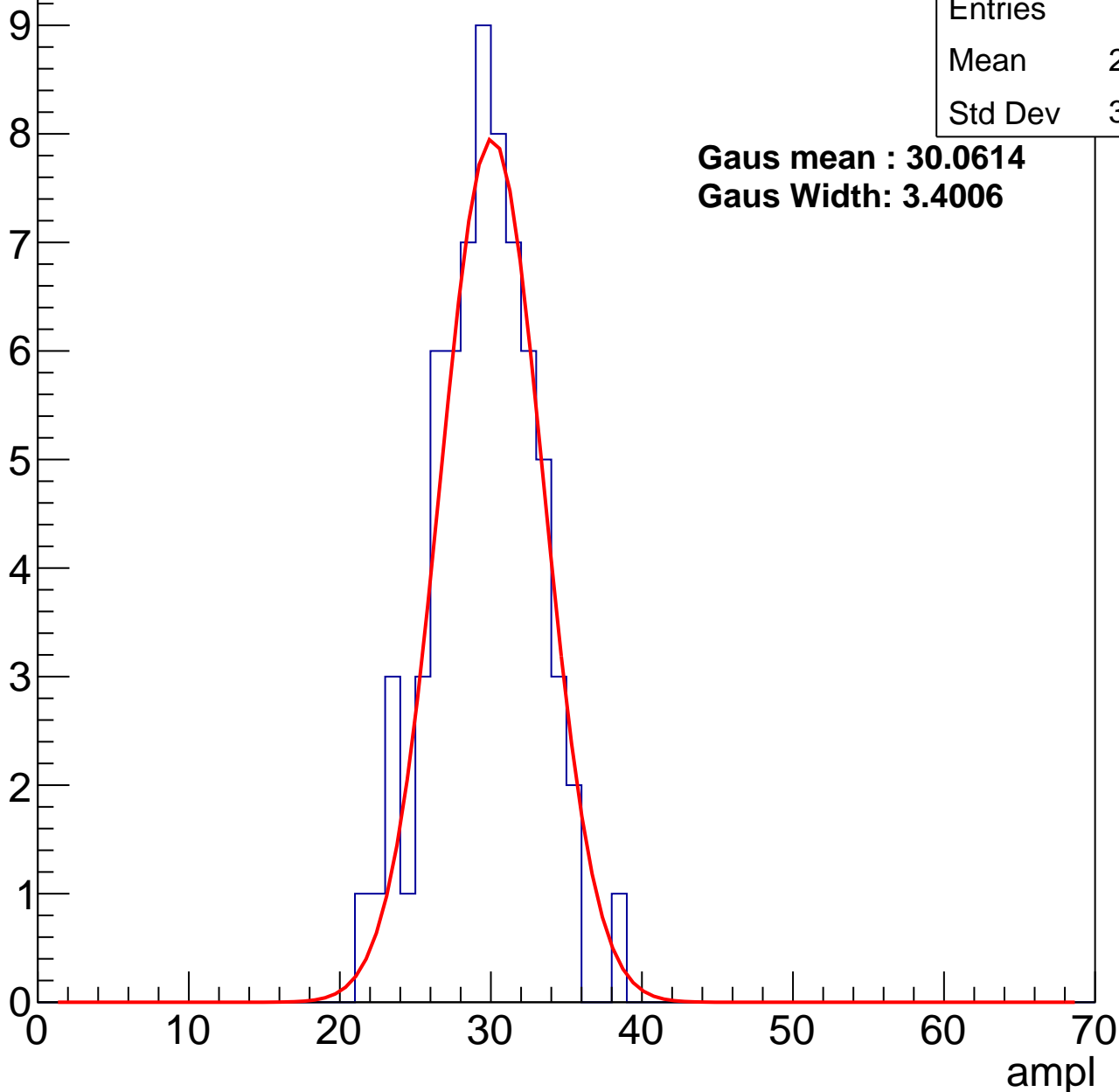
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.13
Std Dev	3.353

Gaus mean : 30.0614

Gaus Width: 3.4006



B0L000S, U2-ch79, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	73
Mean	36.47
Std Dev	3.585

Gaus mean : 36.6168

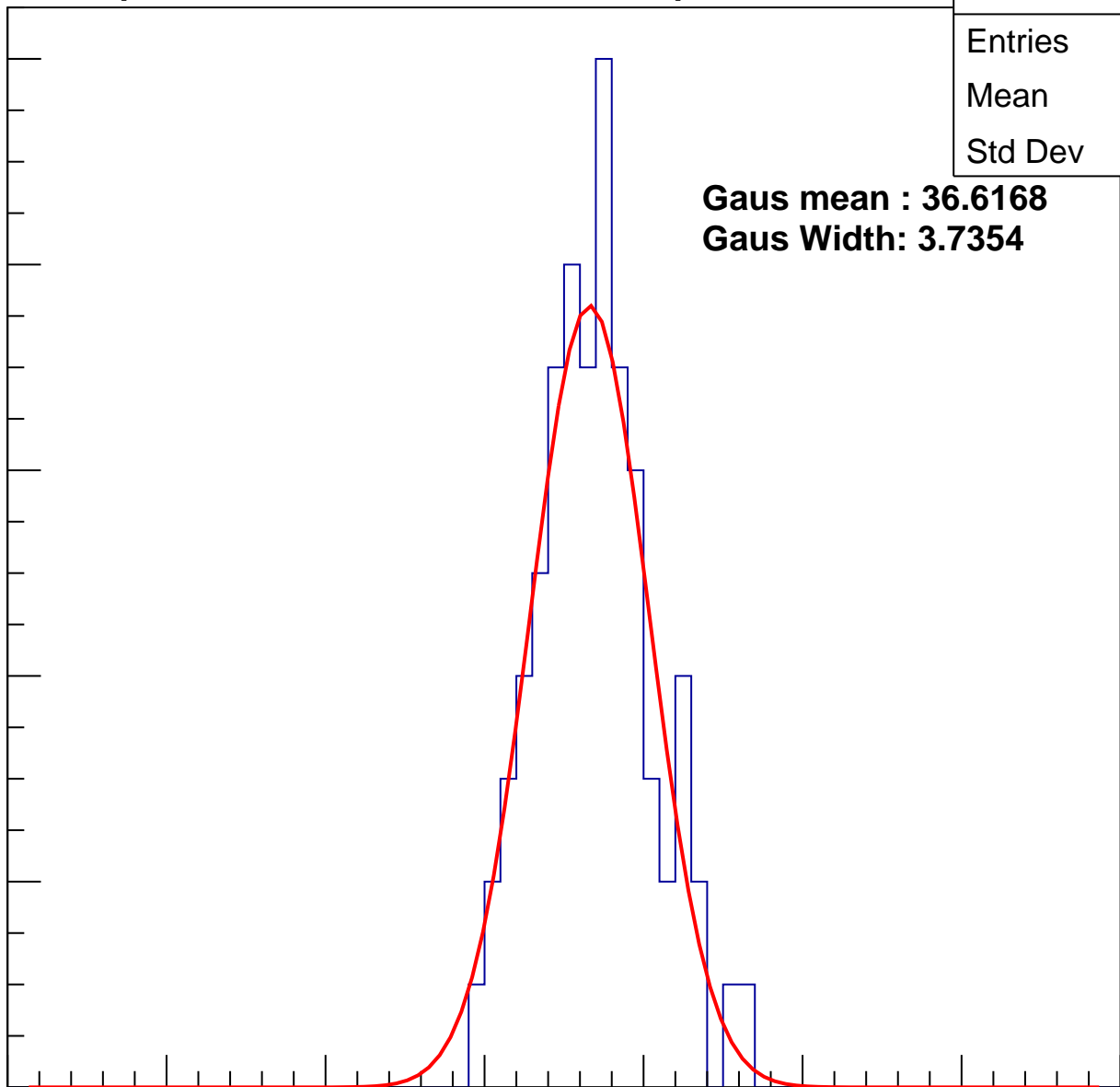
Gaus Width: 3.7354

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch79, adc2

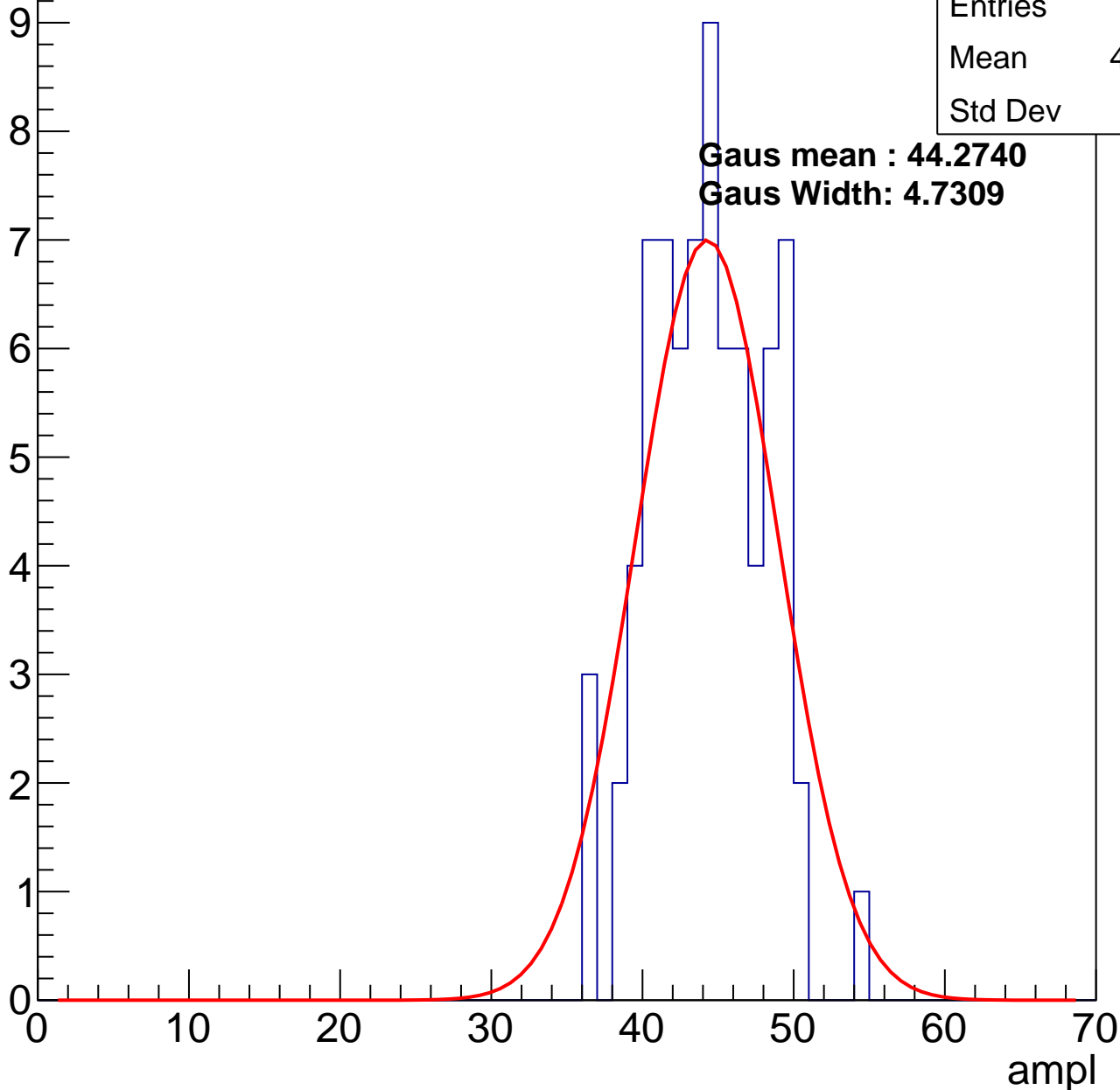
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	43.83
Std Dev	3.74

Gaus mean : 44.2740

Gaus Width: 4.7309

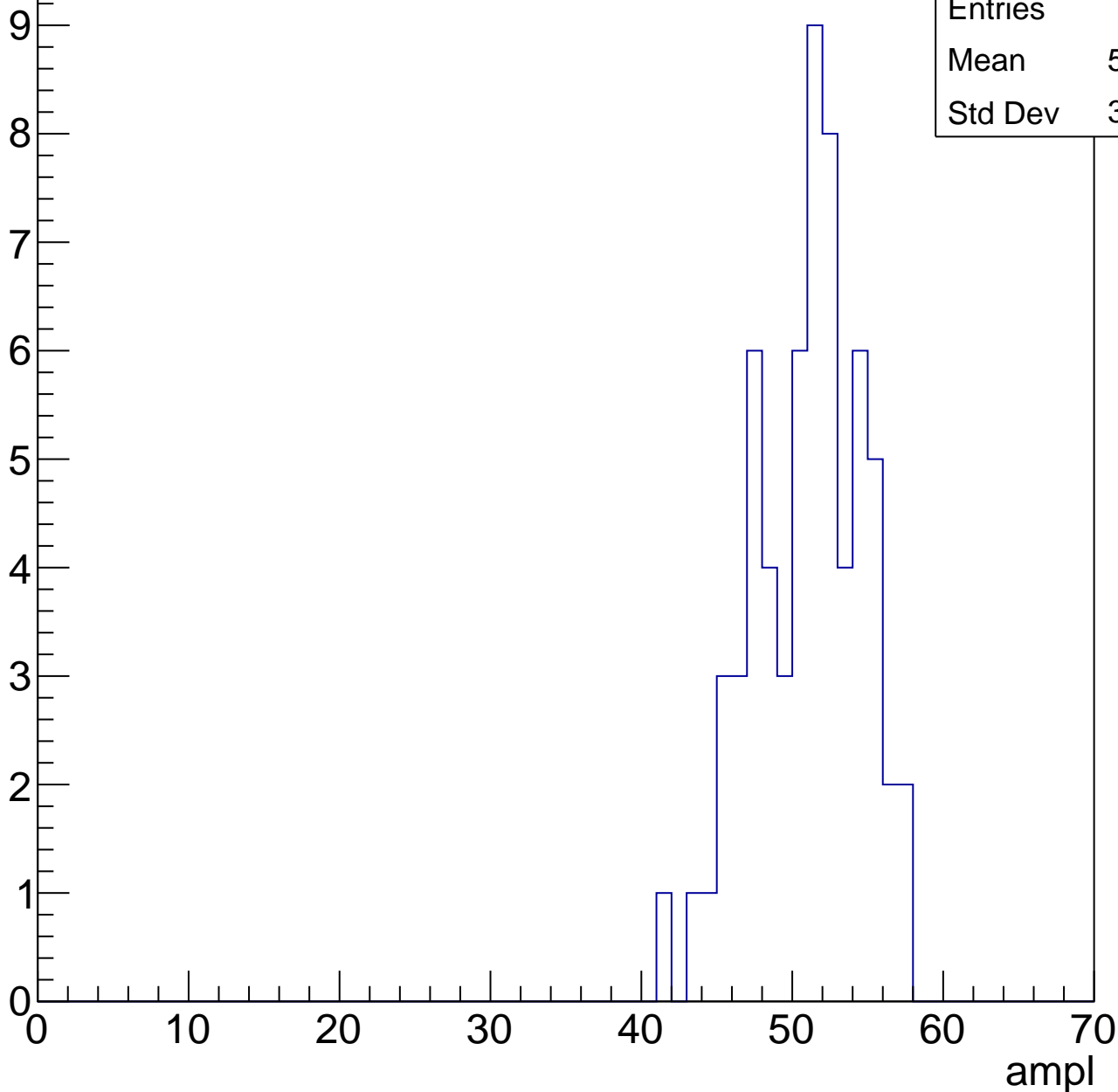


B0L000S, U2-ch79, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	50.53
Std Dev	3.562

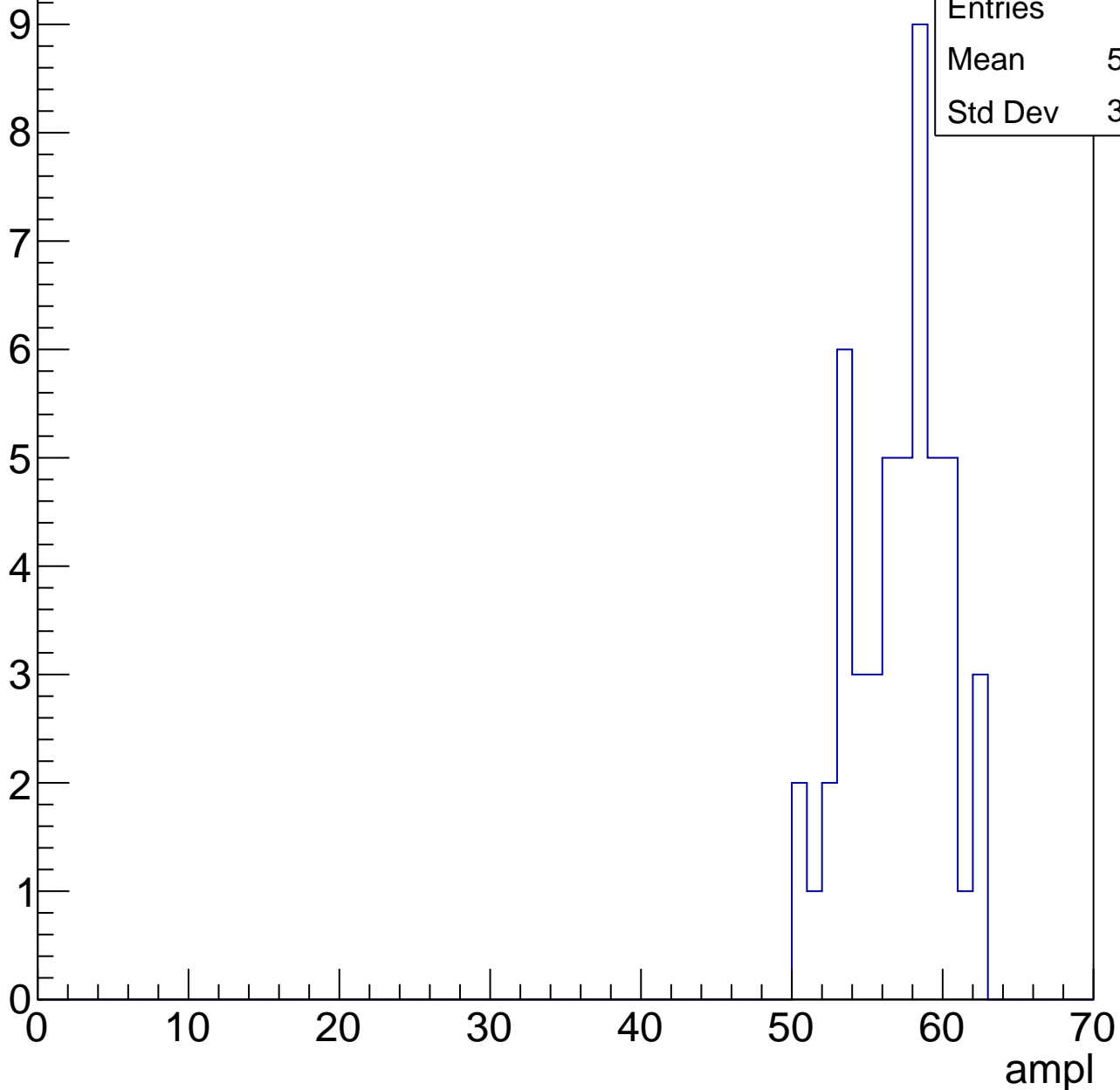


B0L000S, U2-ch79, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.58
Std Dev	3.099



B0L000S, U2-ch79, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.84
Std Dev	9.388

ampl

0

10

20

30

40

50

60

70

B0L000S, U2-ch79, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	3
Mean	61.67
Std Dev	1.247

B0L000S, U2-ch79, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch80, adc0

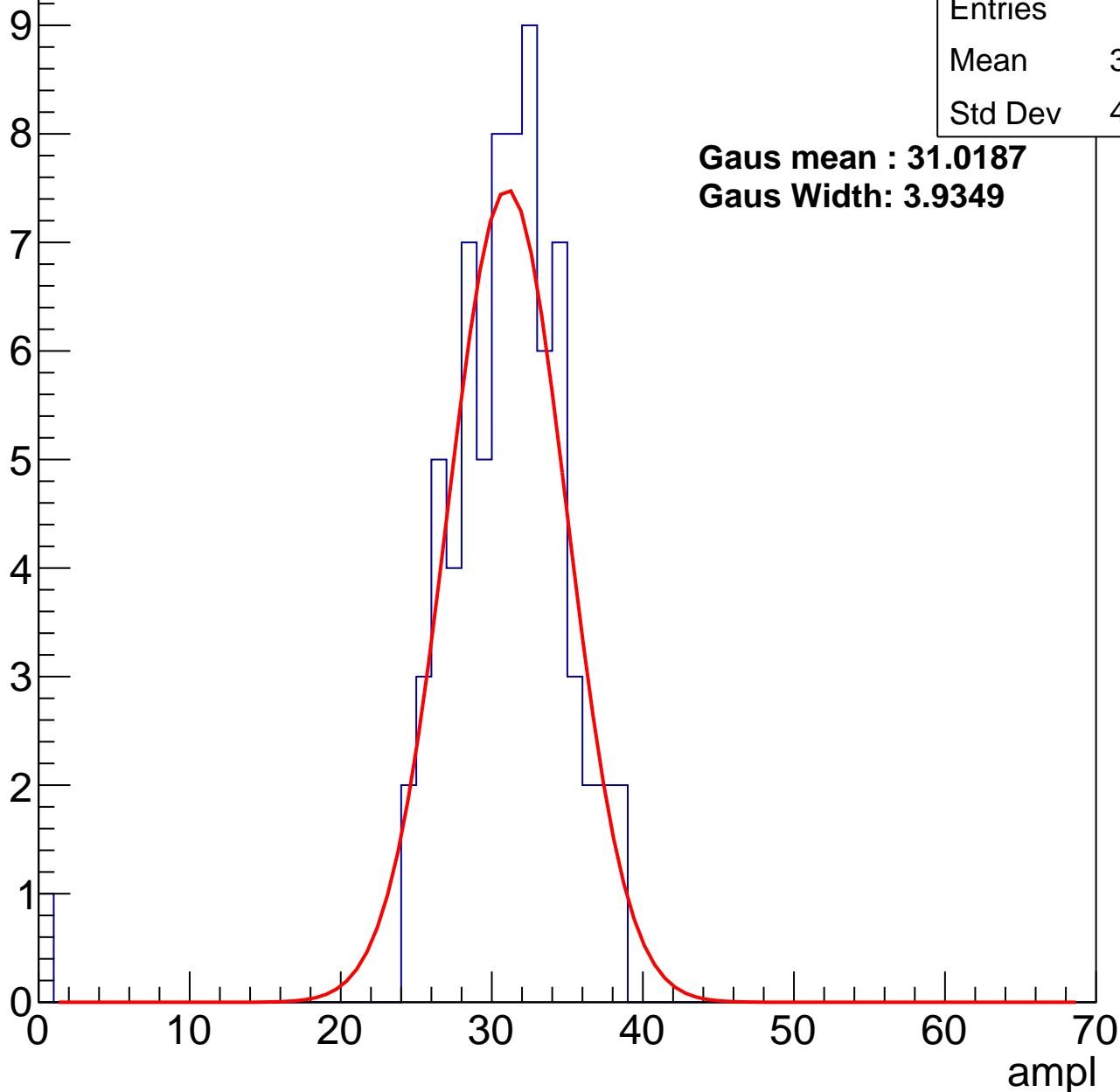
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	30.28
Std Dev	4.895

Gaus mean : 31.0187

Gaus Width: 3.9349



B0L000S, U2-ch80, adc1

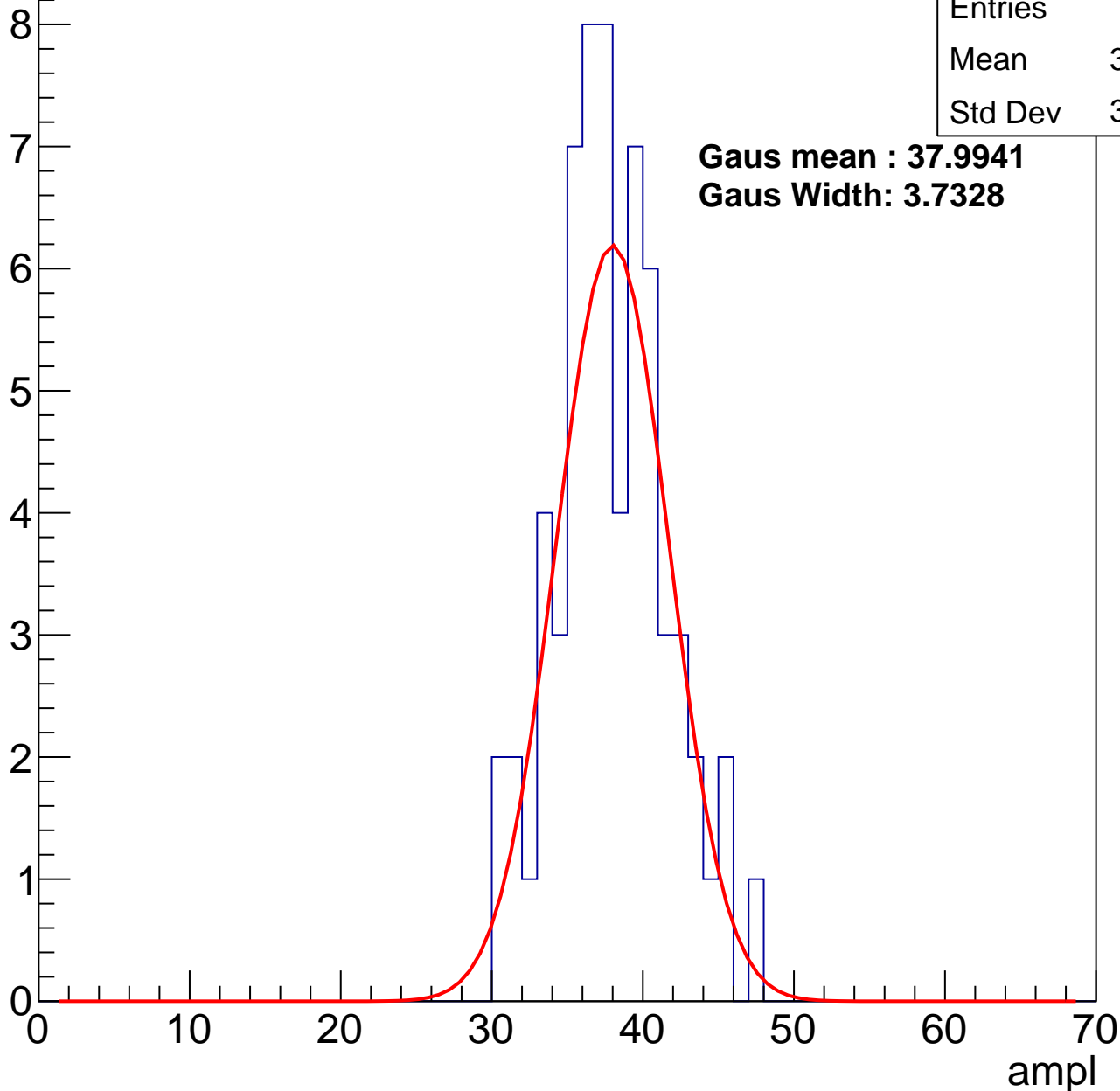
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	37.47
Std Dev	3.678

Gaus mean : 37.9941

Gaus Width: 3.7328



B0L000S, U2-ch80, adc2

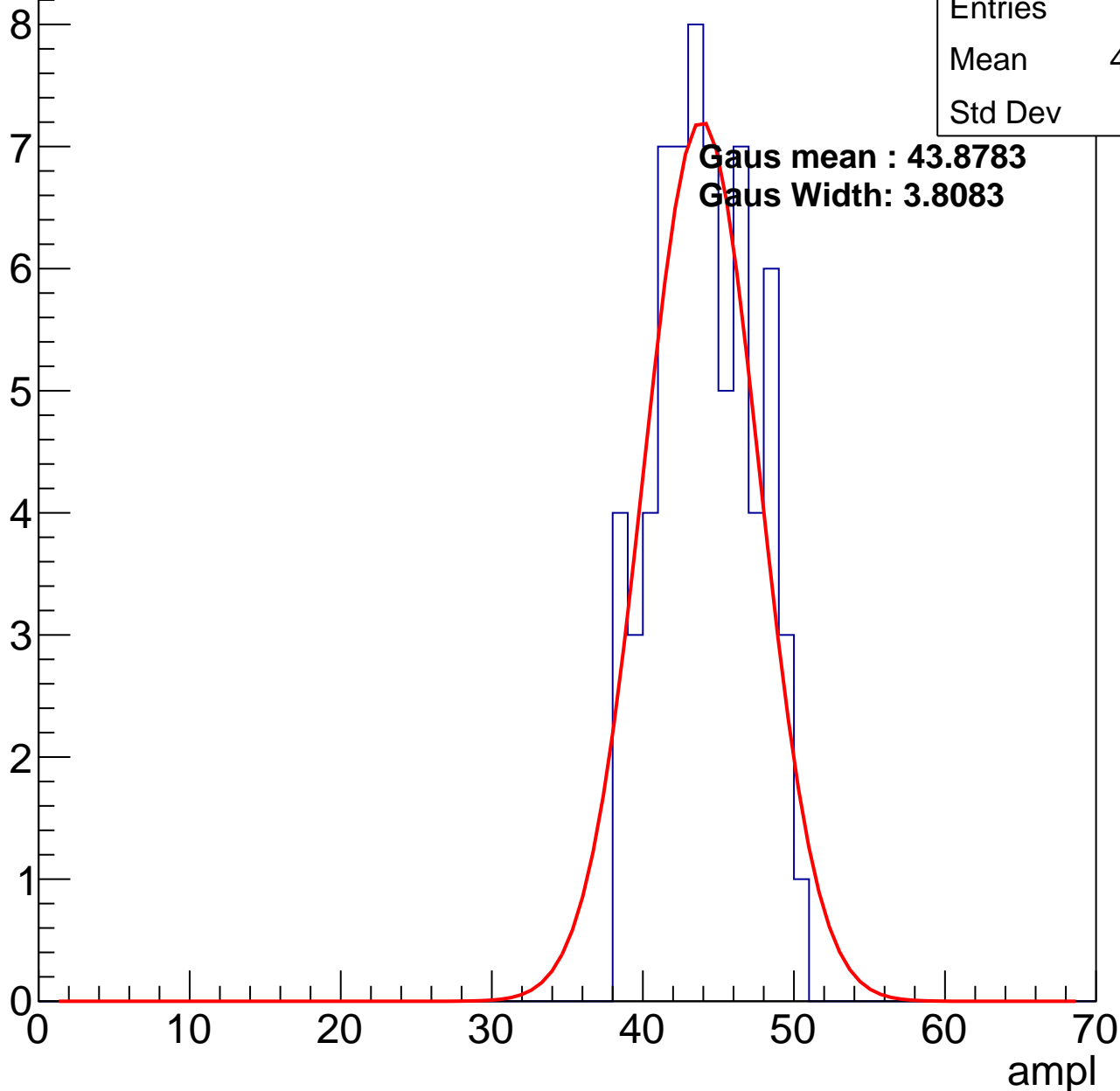
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.67
Std Dev	3.14

Gaus mean : 43.8783

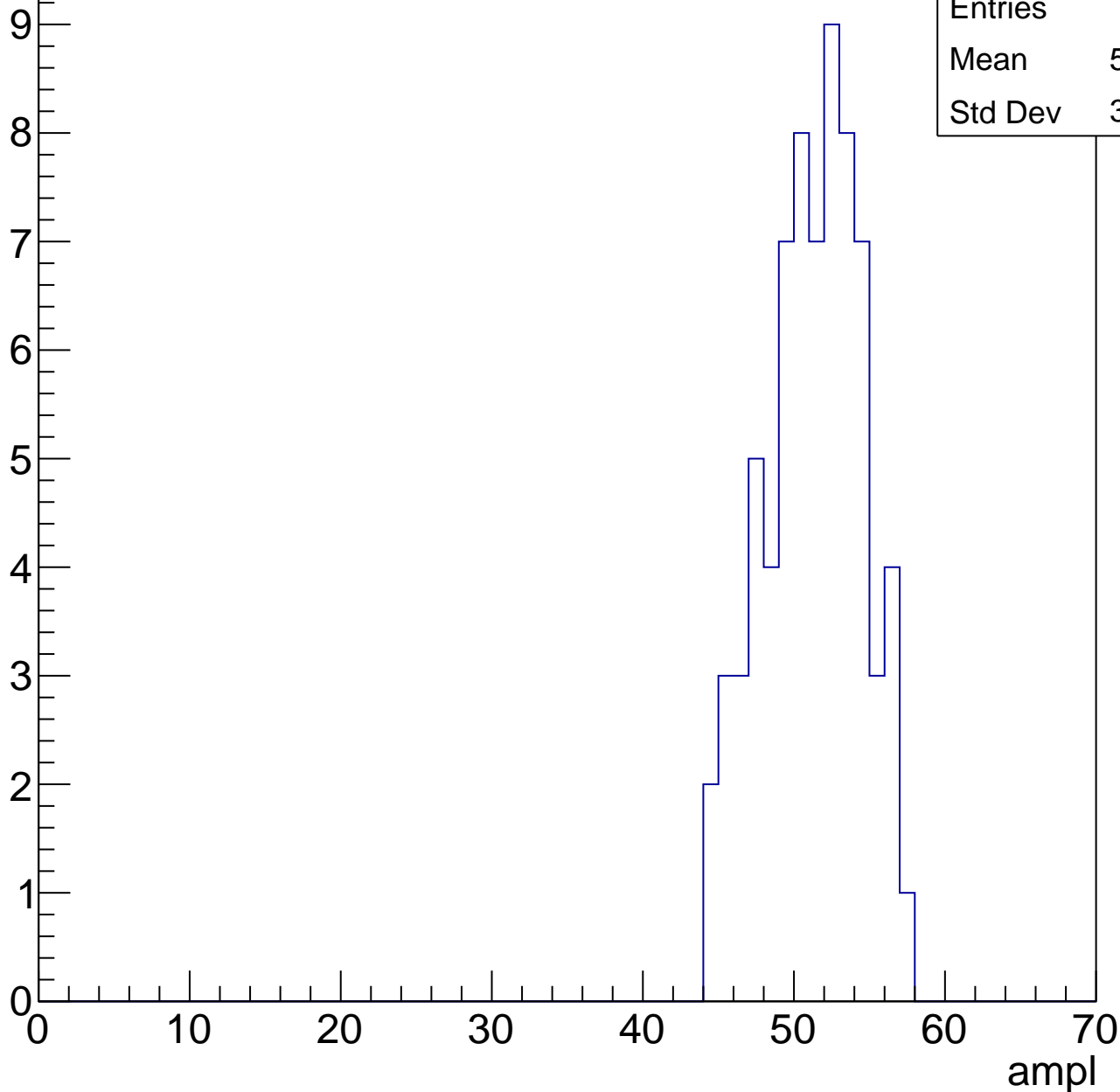
Gaus Width: 3.8083



B0L000S, U2-ch80, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

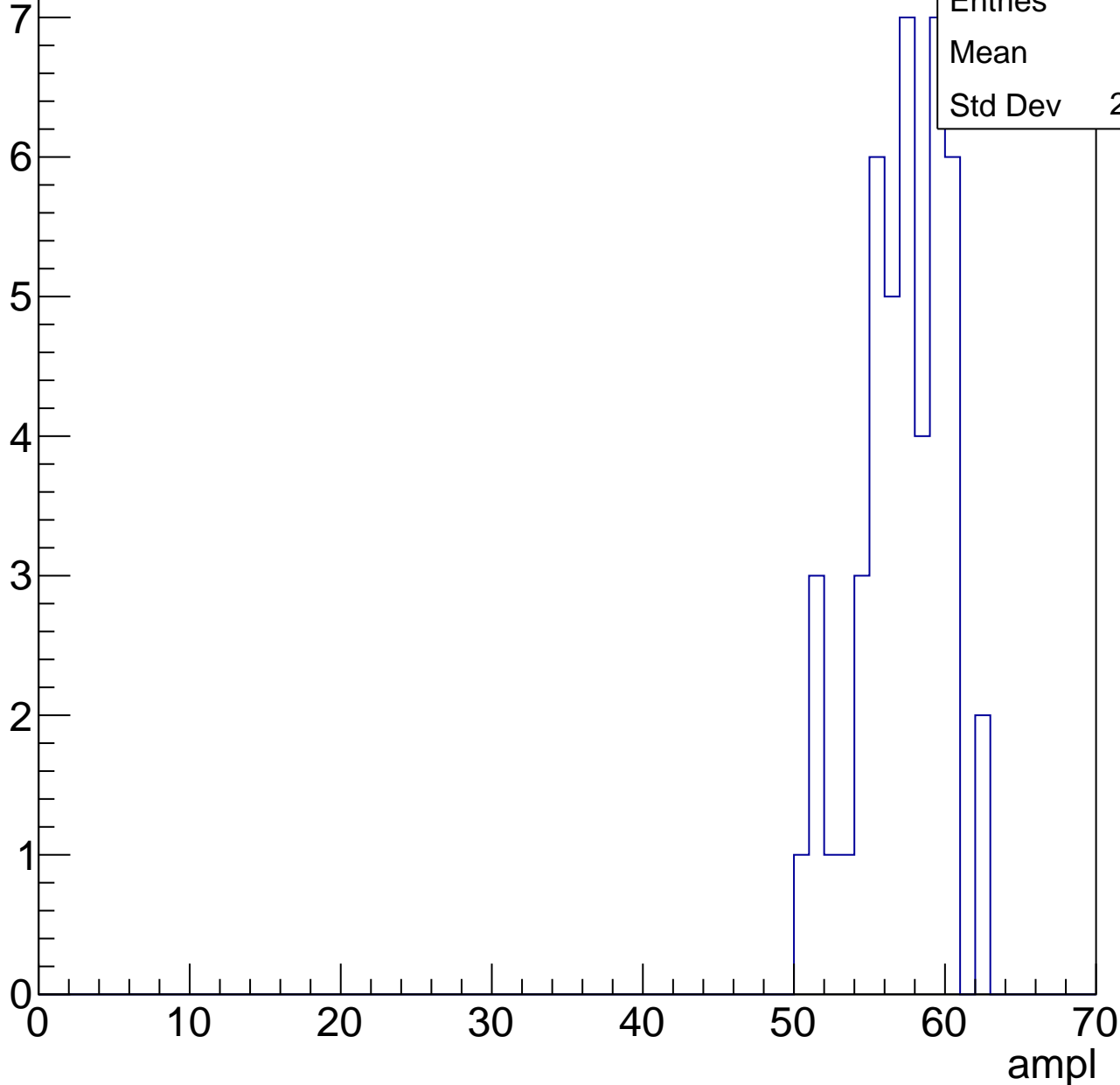


Entries	71
Mean	50.76
Std Dev	3.178

B0L000S, U2-ch80, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

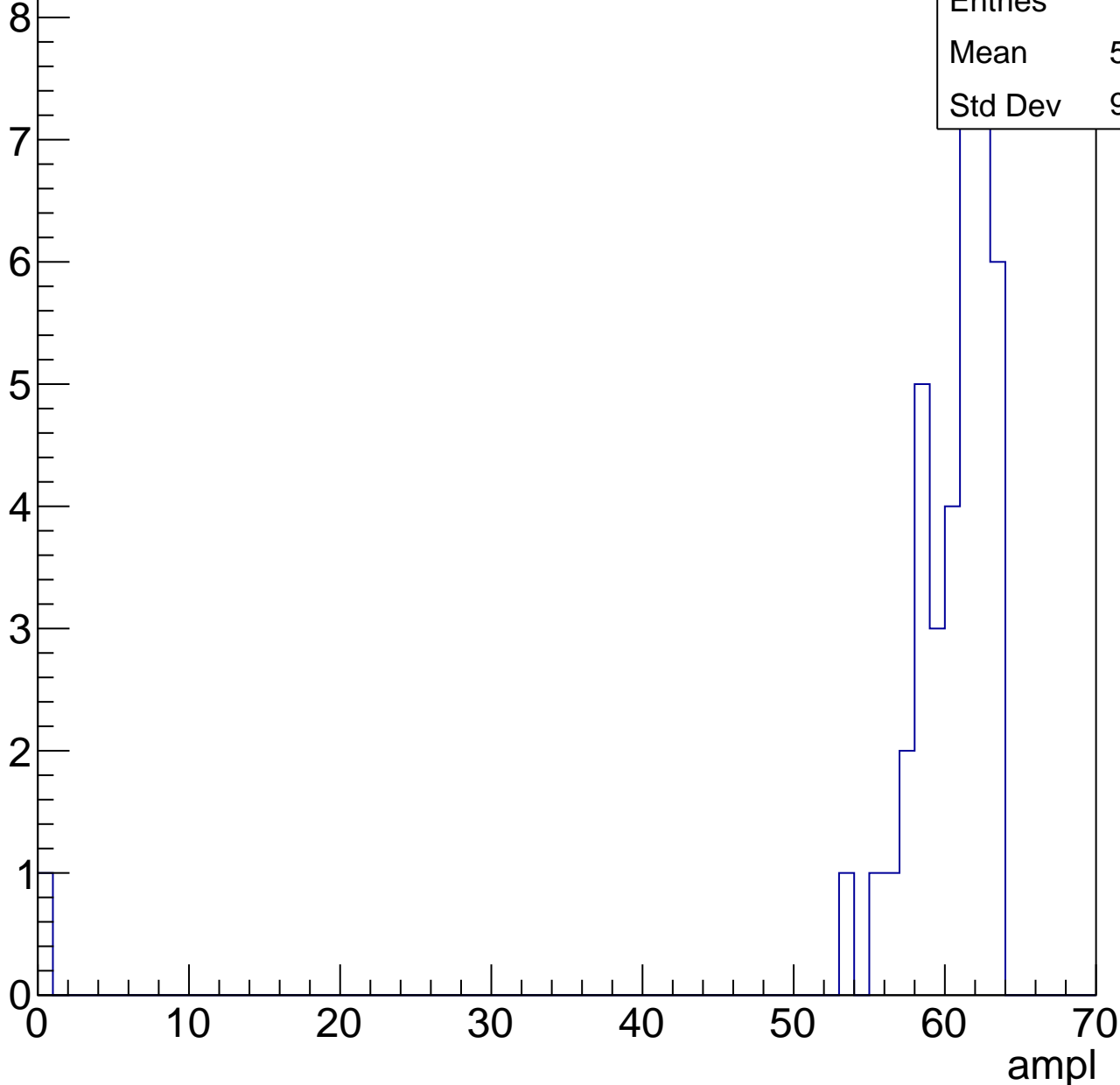


B0L000S, U2-ch80, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

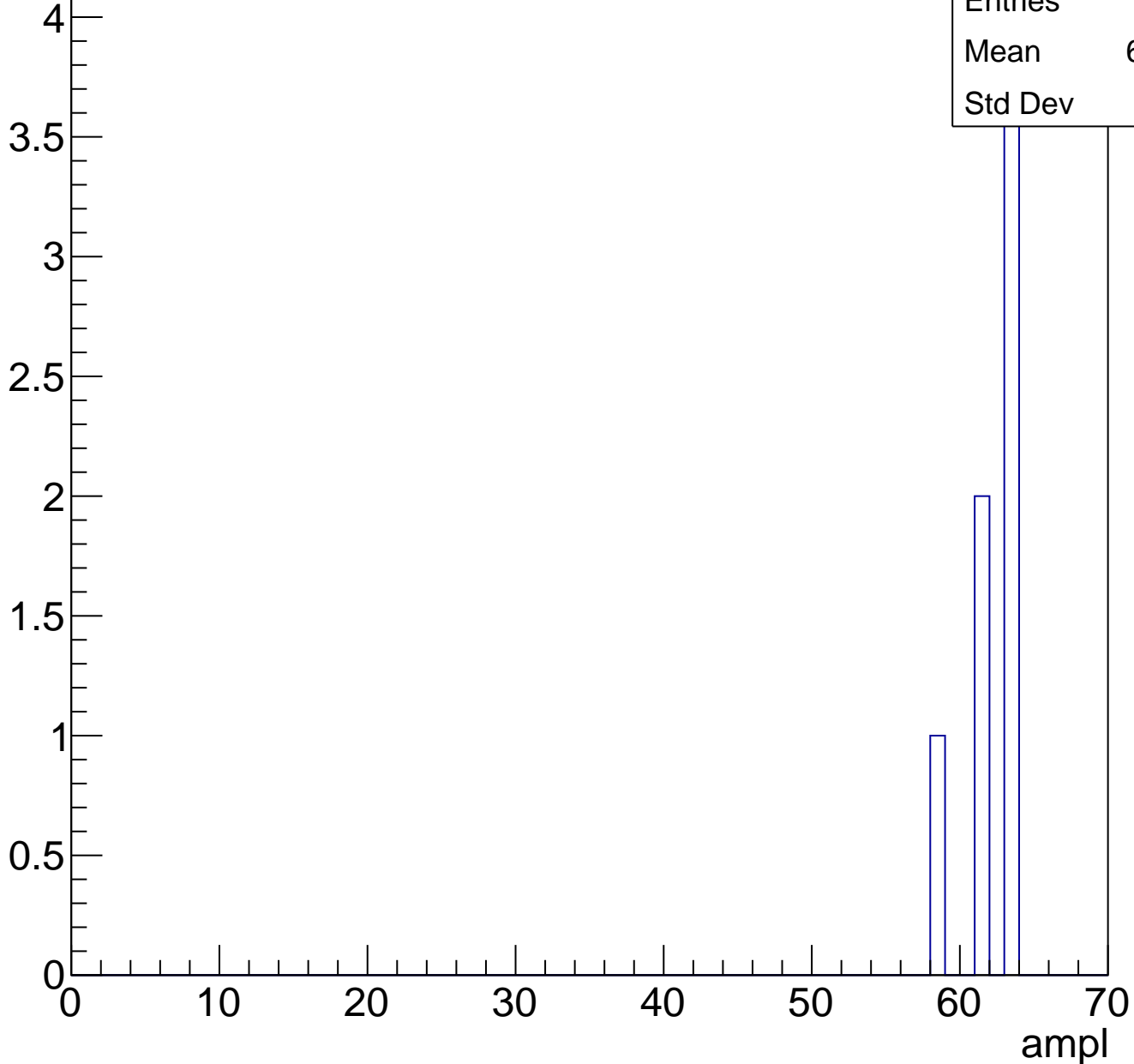
Entries	40
Mean	58.67
Std Dev	9.689



B0L000S, U2-ch80, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch80, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch81, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	72
Mean	29.56
Std Dev	5.932

Gaus mean : 31.4835

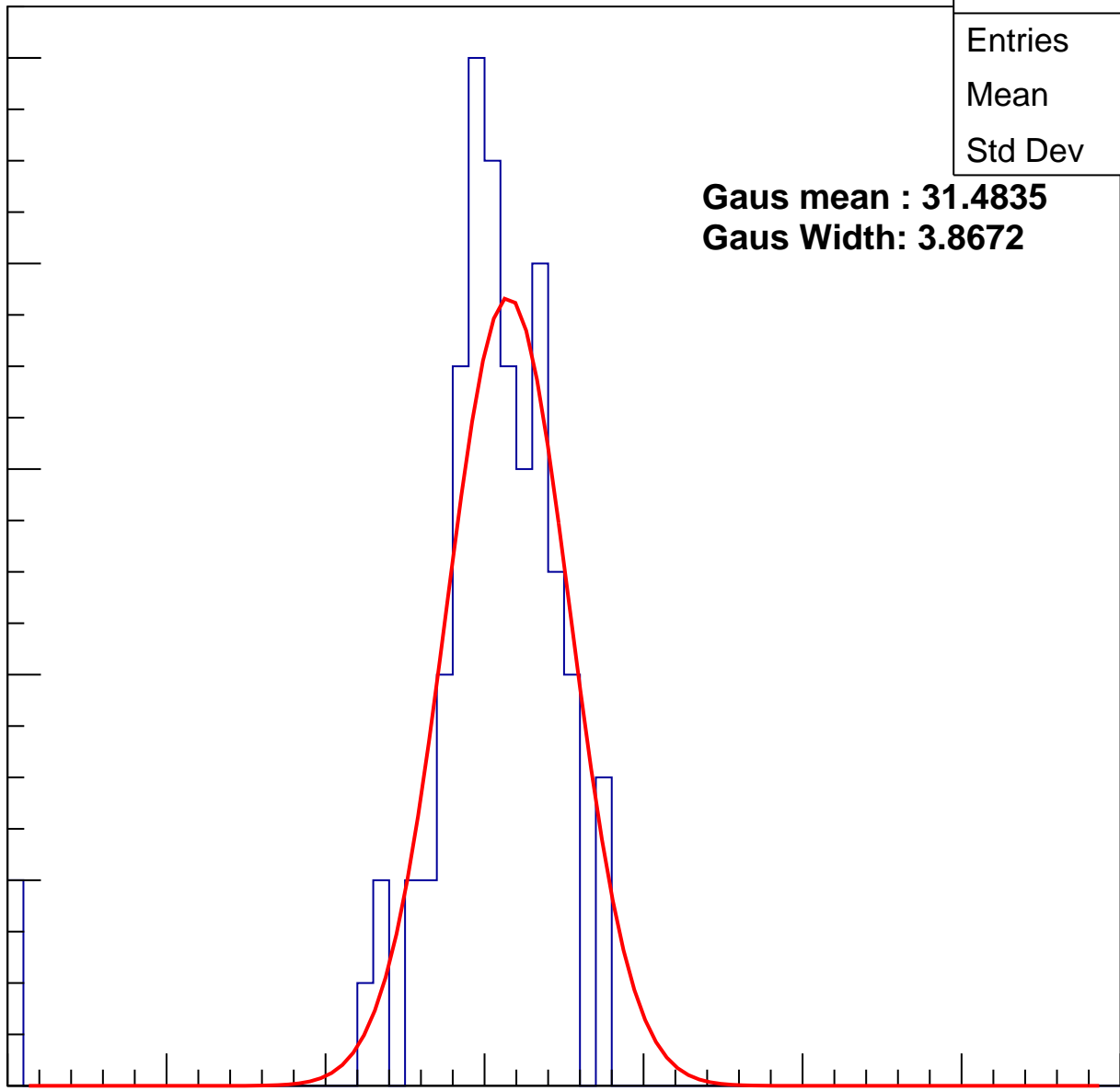
Gaus Width: 3.8672

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch81, adc1

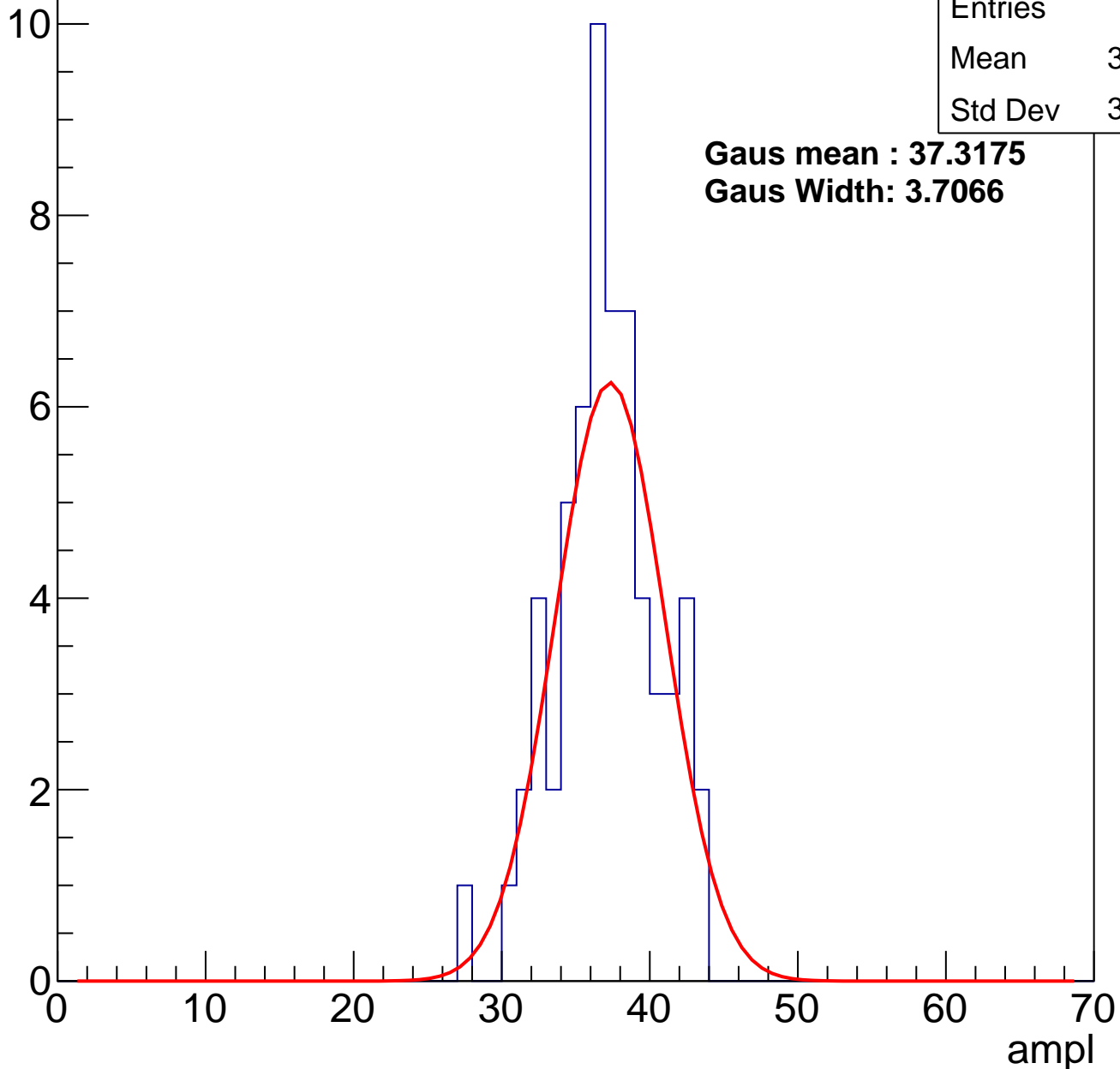
calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	36.57
Std Dev	3.365

Gaus mean : 37.3175

Gaus Width: 3.7066

Entry

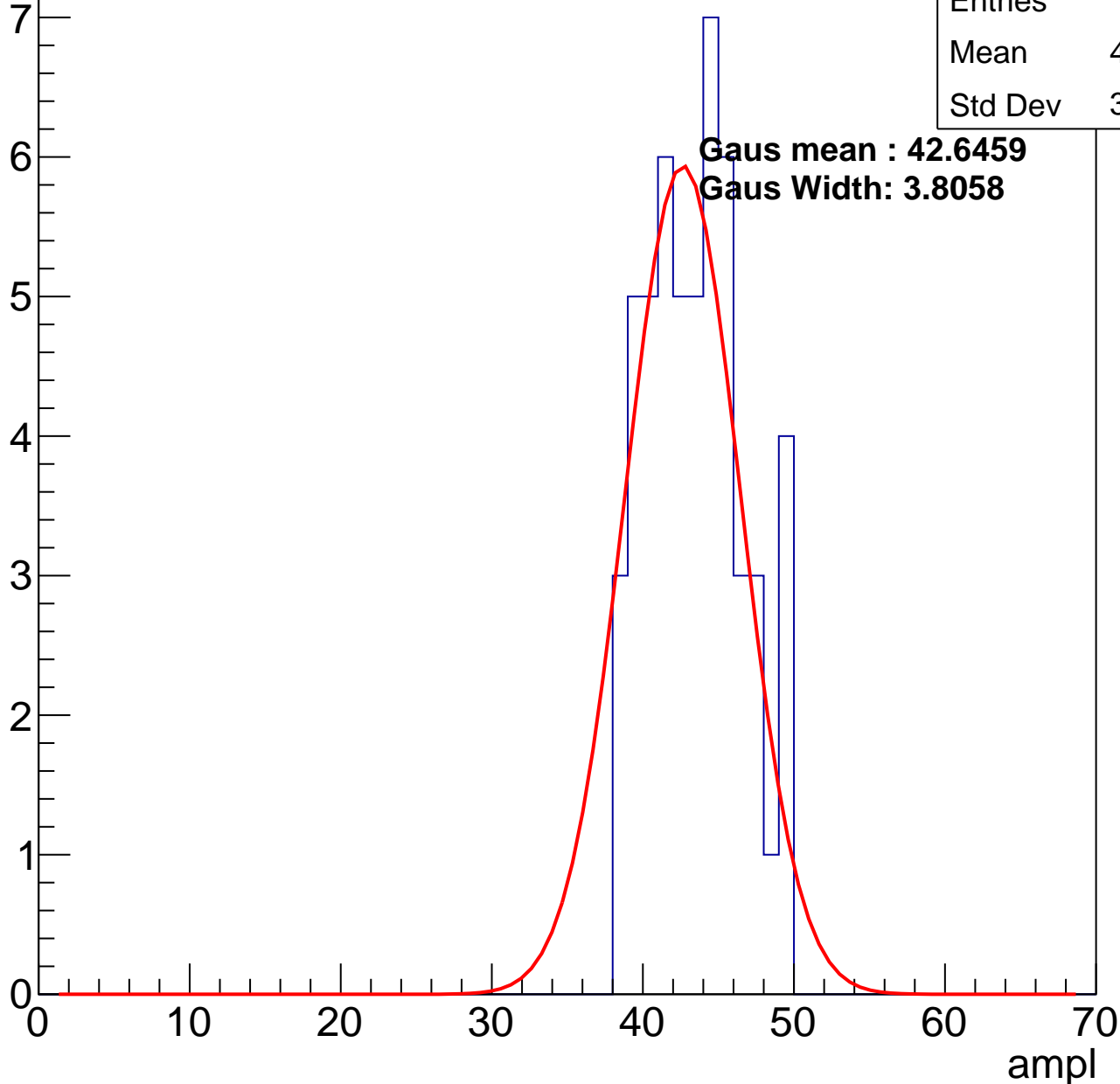


B0L000S, U2-ch81, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.04
Std Dev	3.084

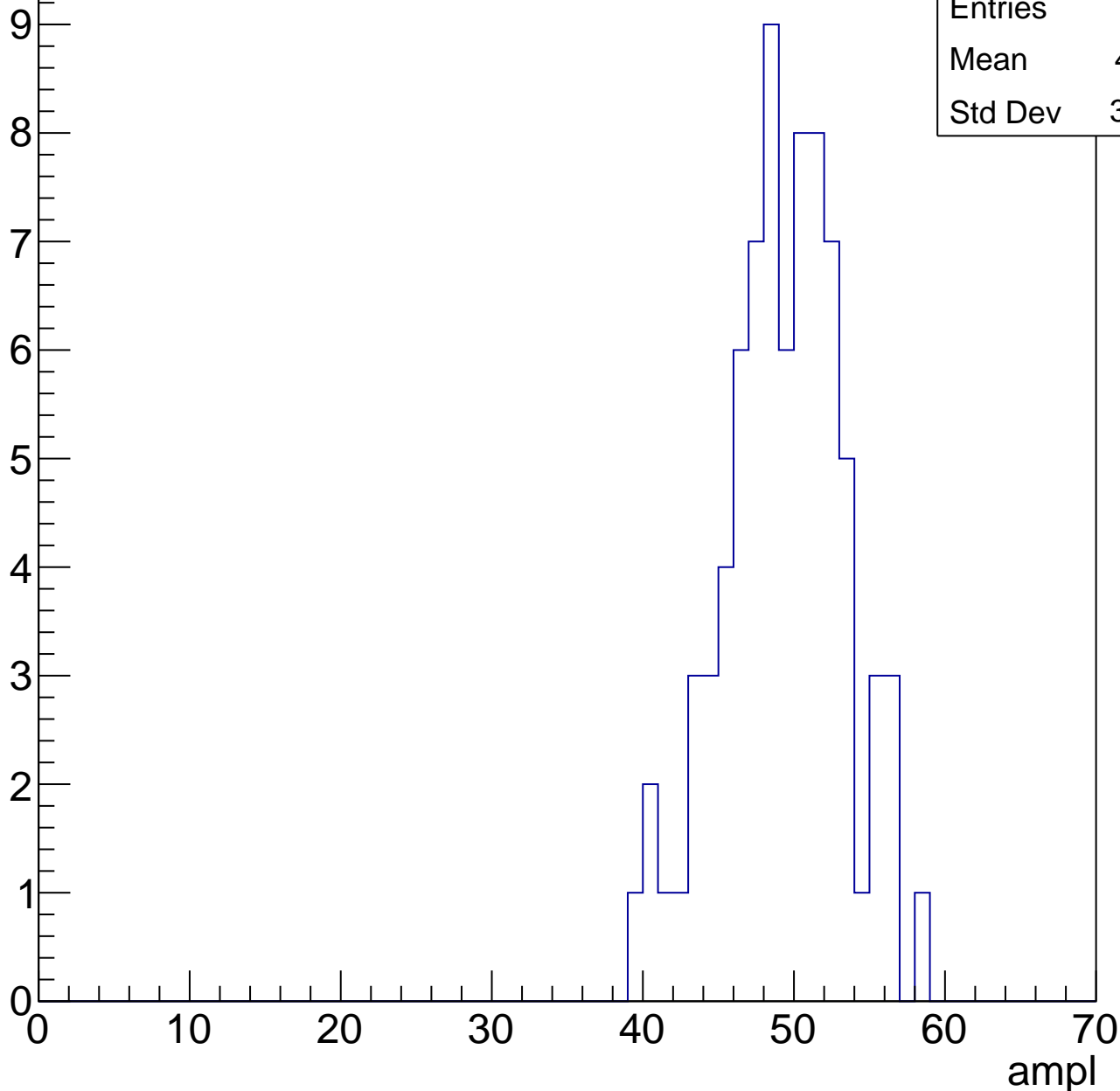


B0L000S, U2-ch81, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	79
Mean	48.81
Std Dev	3.984

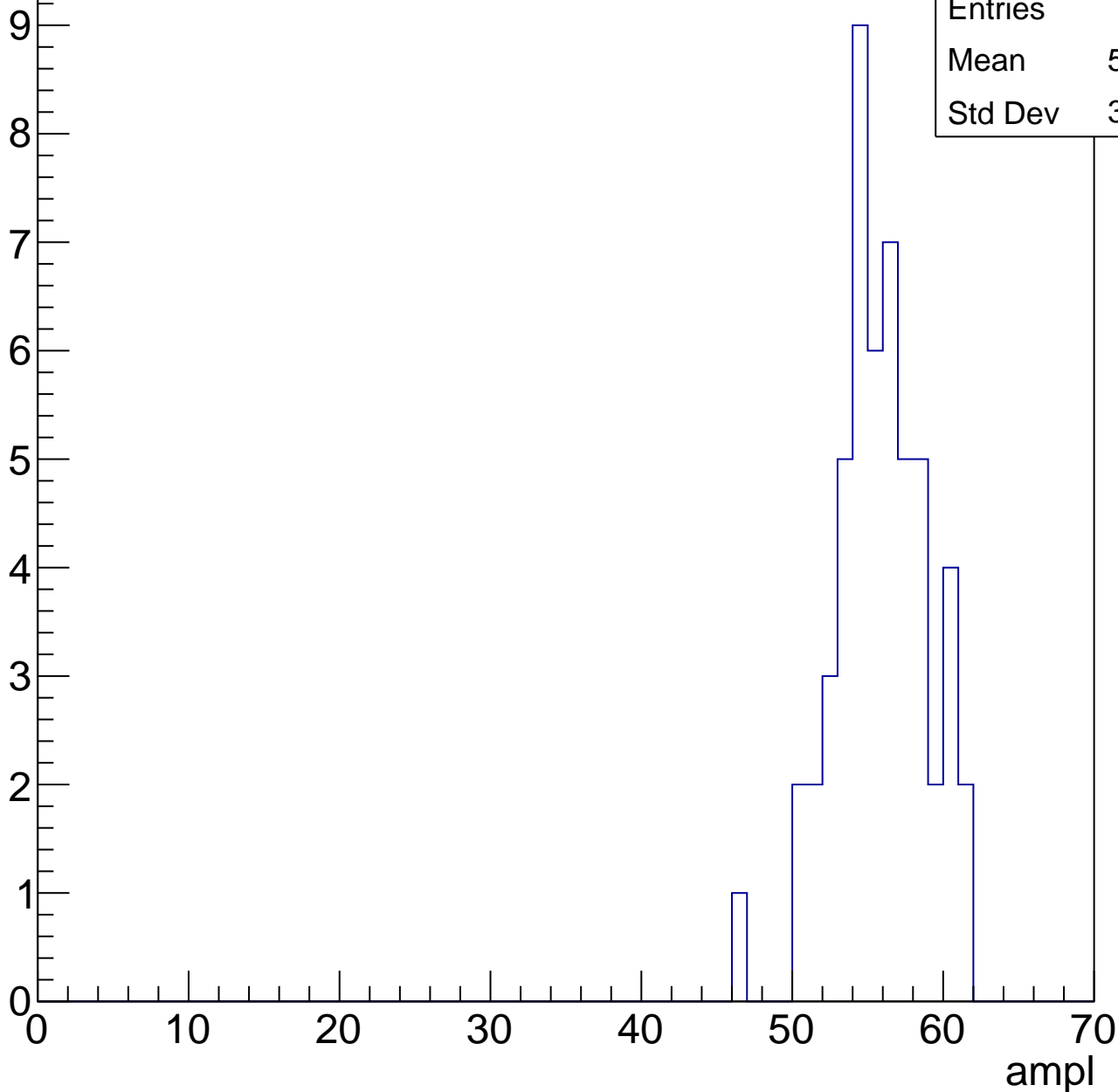


B0L000S, U2-ch81, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	55.32
Std Dev	3.039

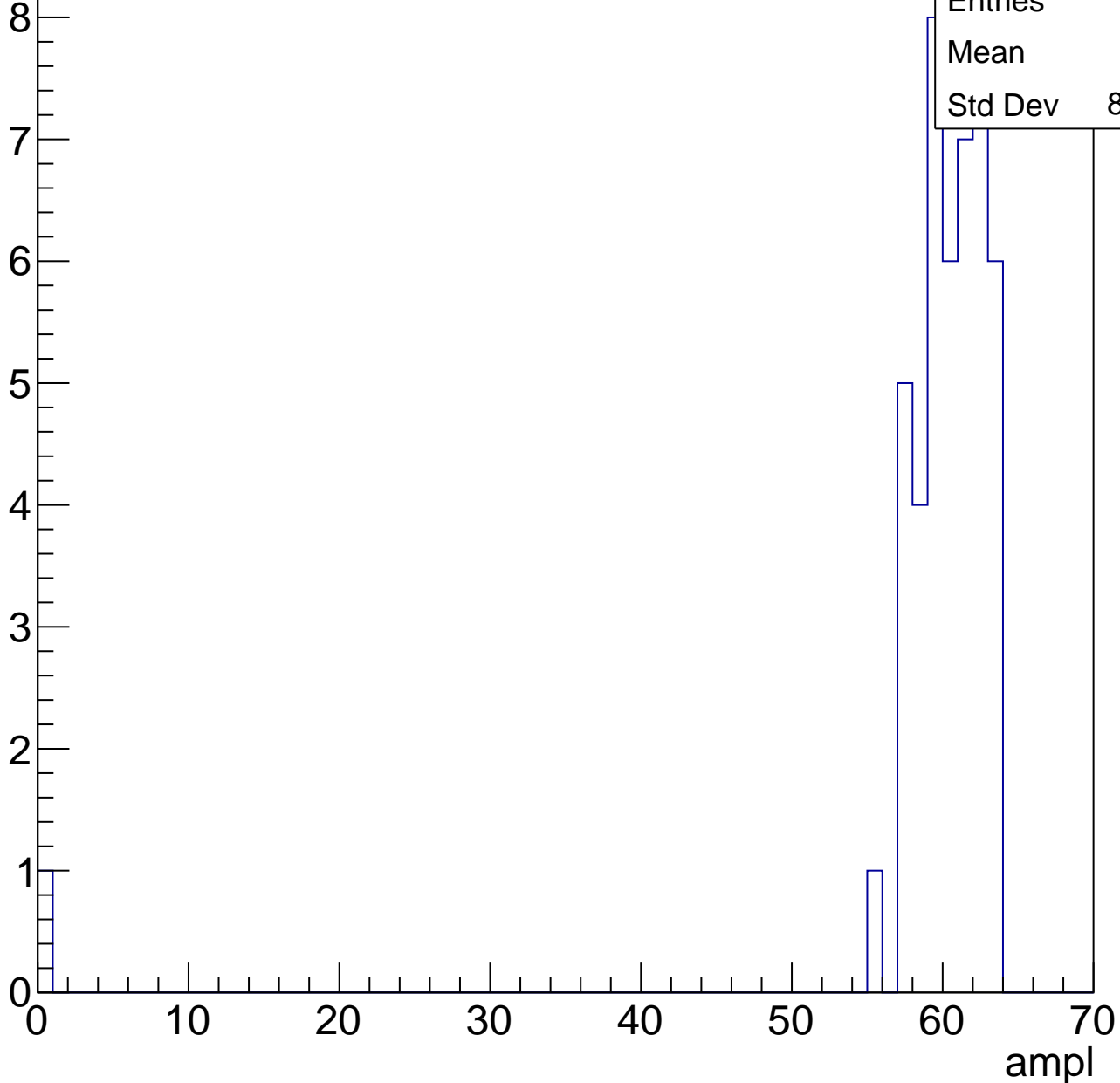


B0L000S, U2-ch81, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

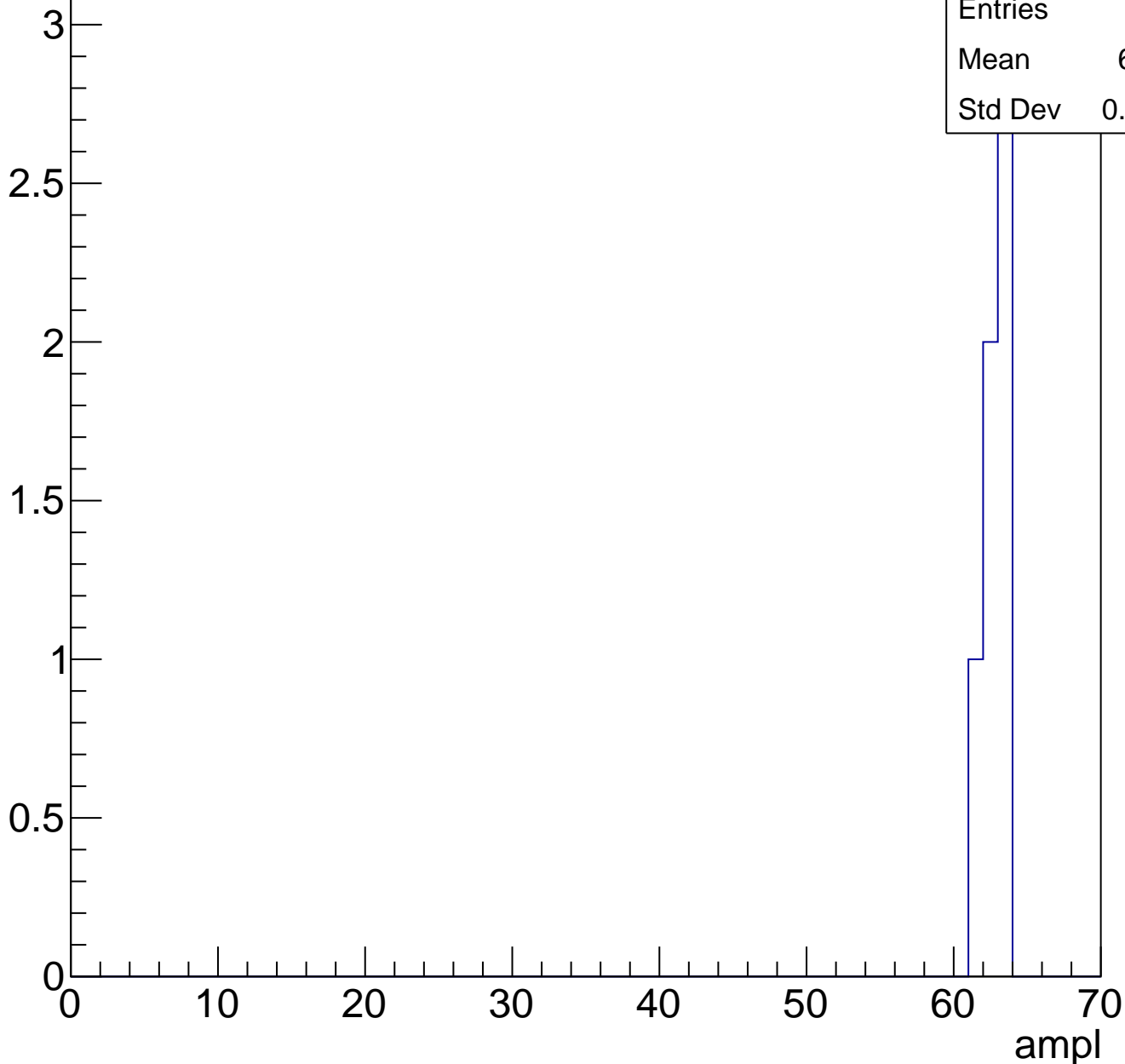
Entries	46
Mean	58.8
Std Dev	8.994



B0L000S, U2-ch81, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch81, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch82, adc0

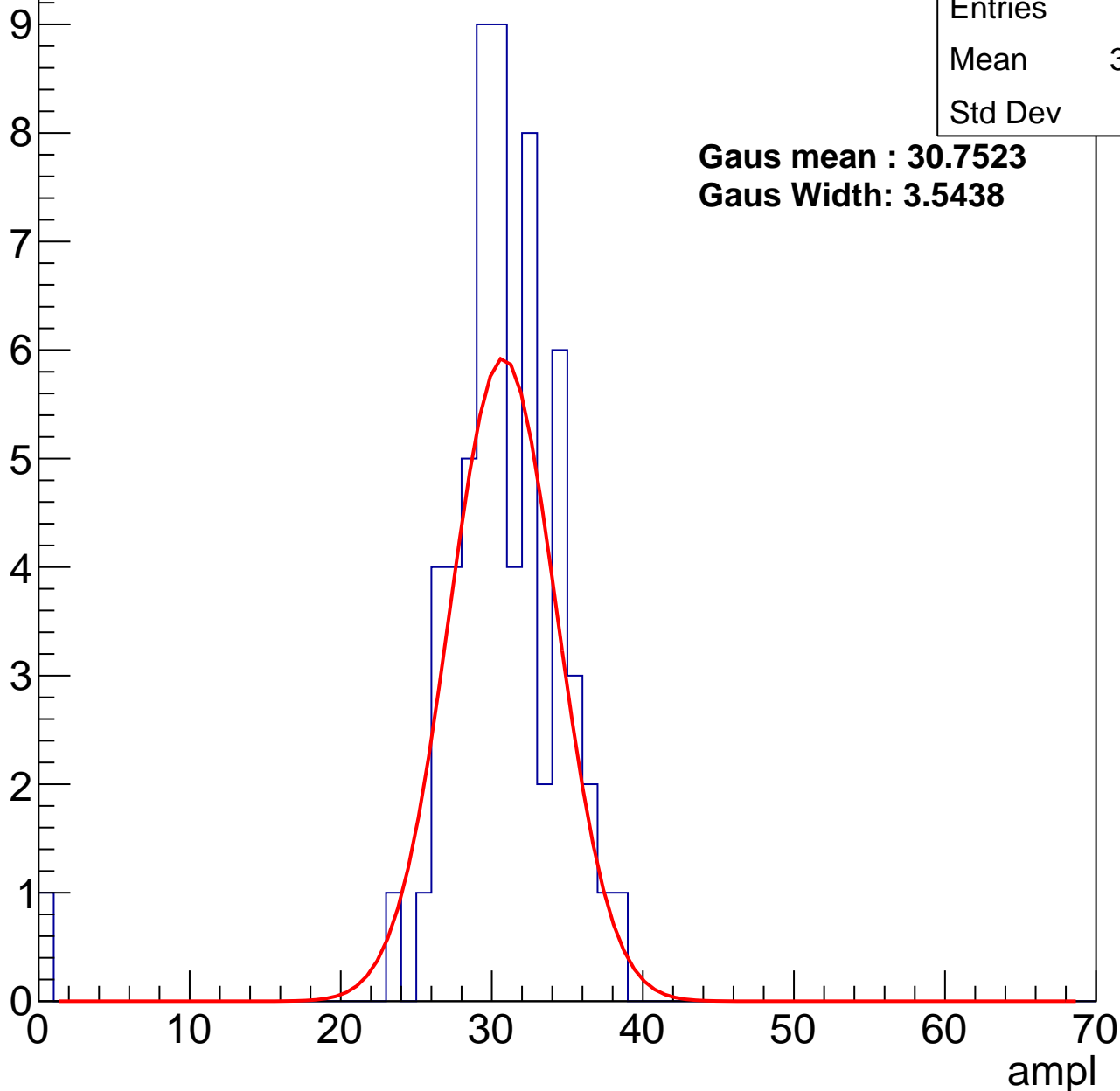
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	30.05
Std Dev	4.98

Gaus mean : 30.7523

Gaus Width: 3.5438



B0L000S, U2-ch82, adc1

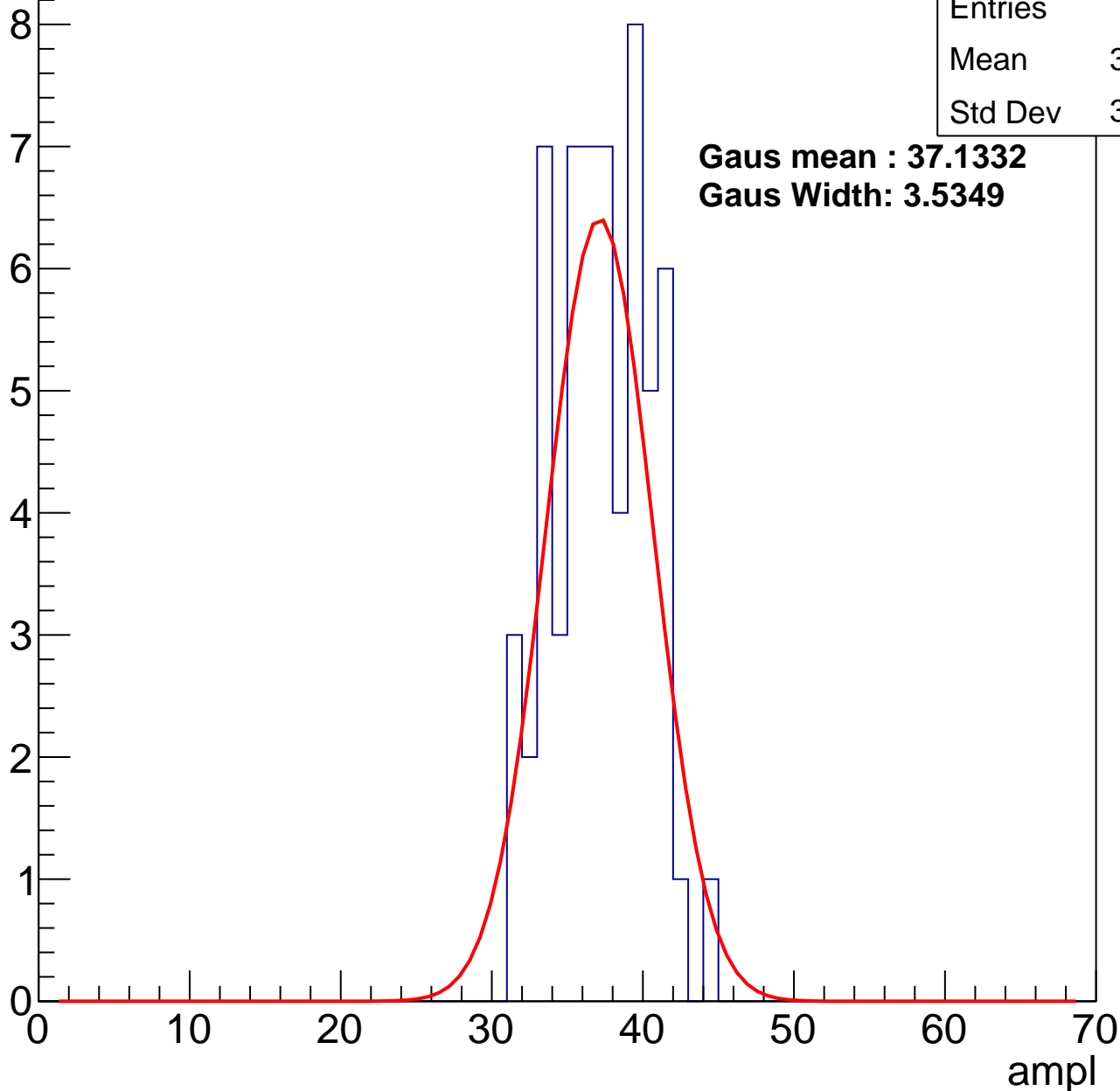
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.75
Std Dev	3.082

Gaus mean : 37.1332

Gaus Width: 3.5349



B0L000S, U2-ch82, adc2

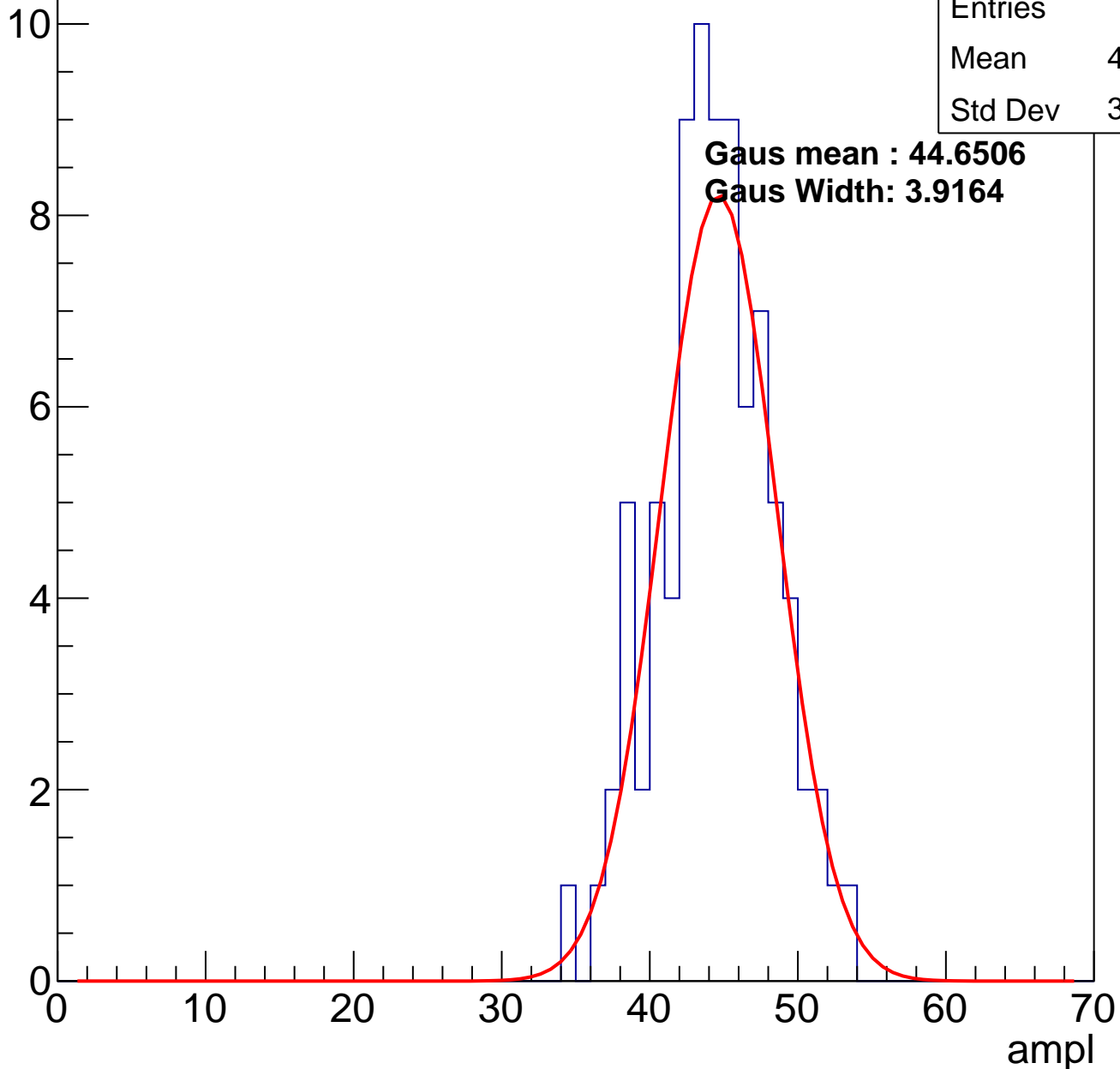
calib_packv5_042523_0143.root, FC#5, port B1

Entries	85
Mean	43.92
Std Dev	3.826

Gaus mean : 44.6506

Gaus Width: 3.9164

Entry

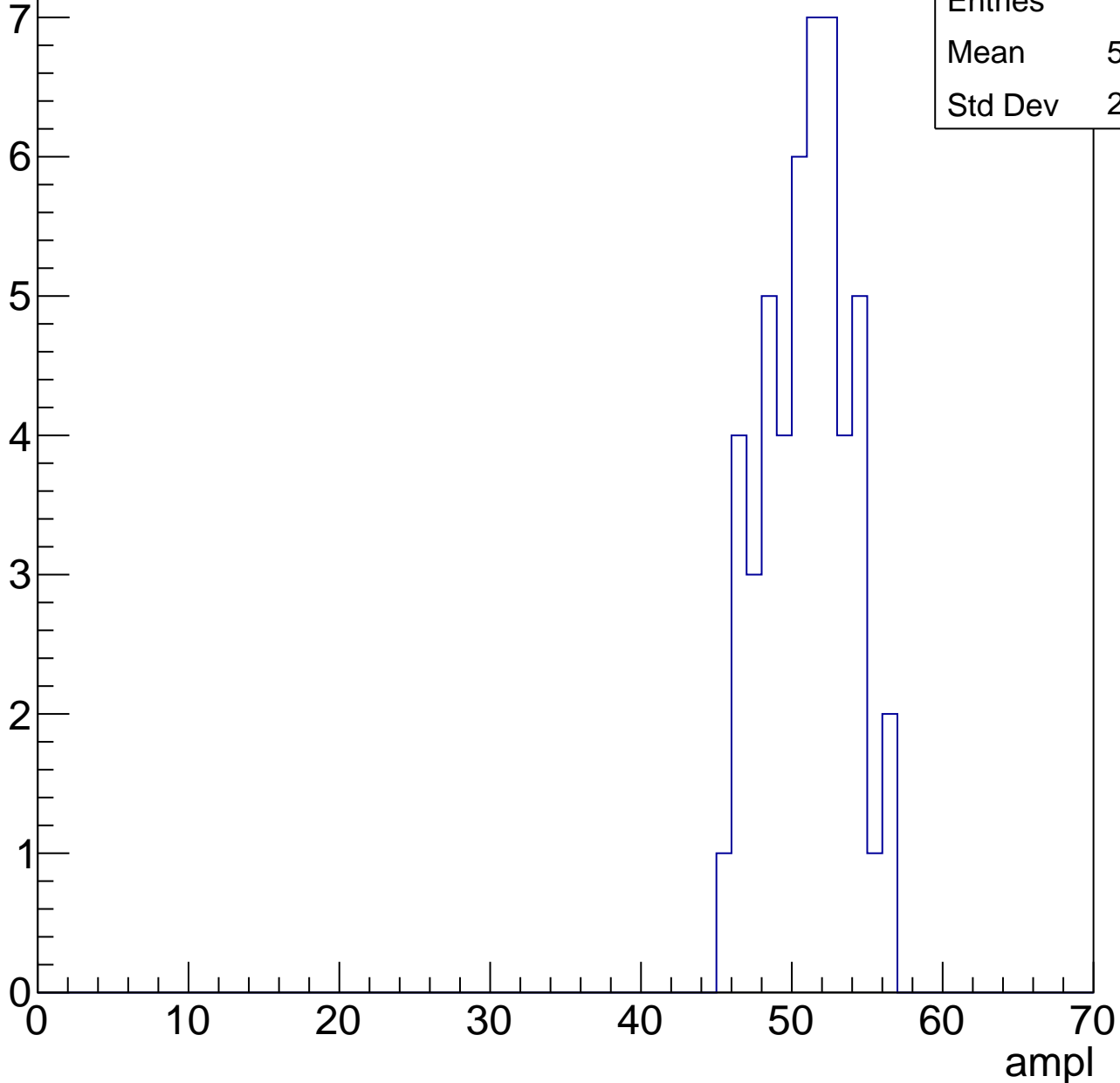


B0L000S, U2-ch82, adc3

calib_packv5_042523_0143.root, FC#5, port B1

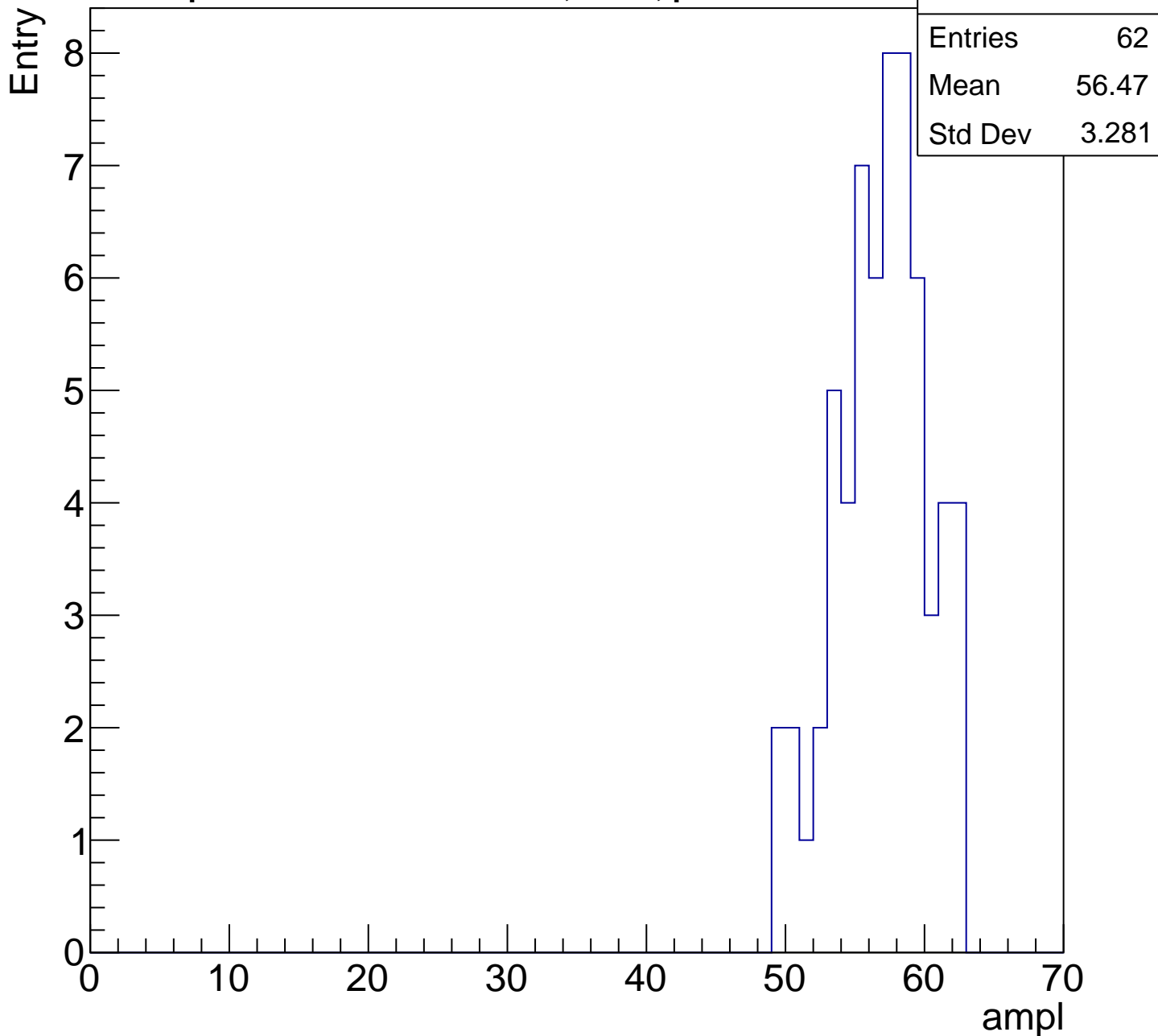
Entry

Entries	49
Mean	50.53
Std Dev	2.763



B0L000S, U2-ch82, adc4

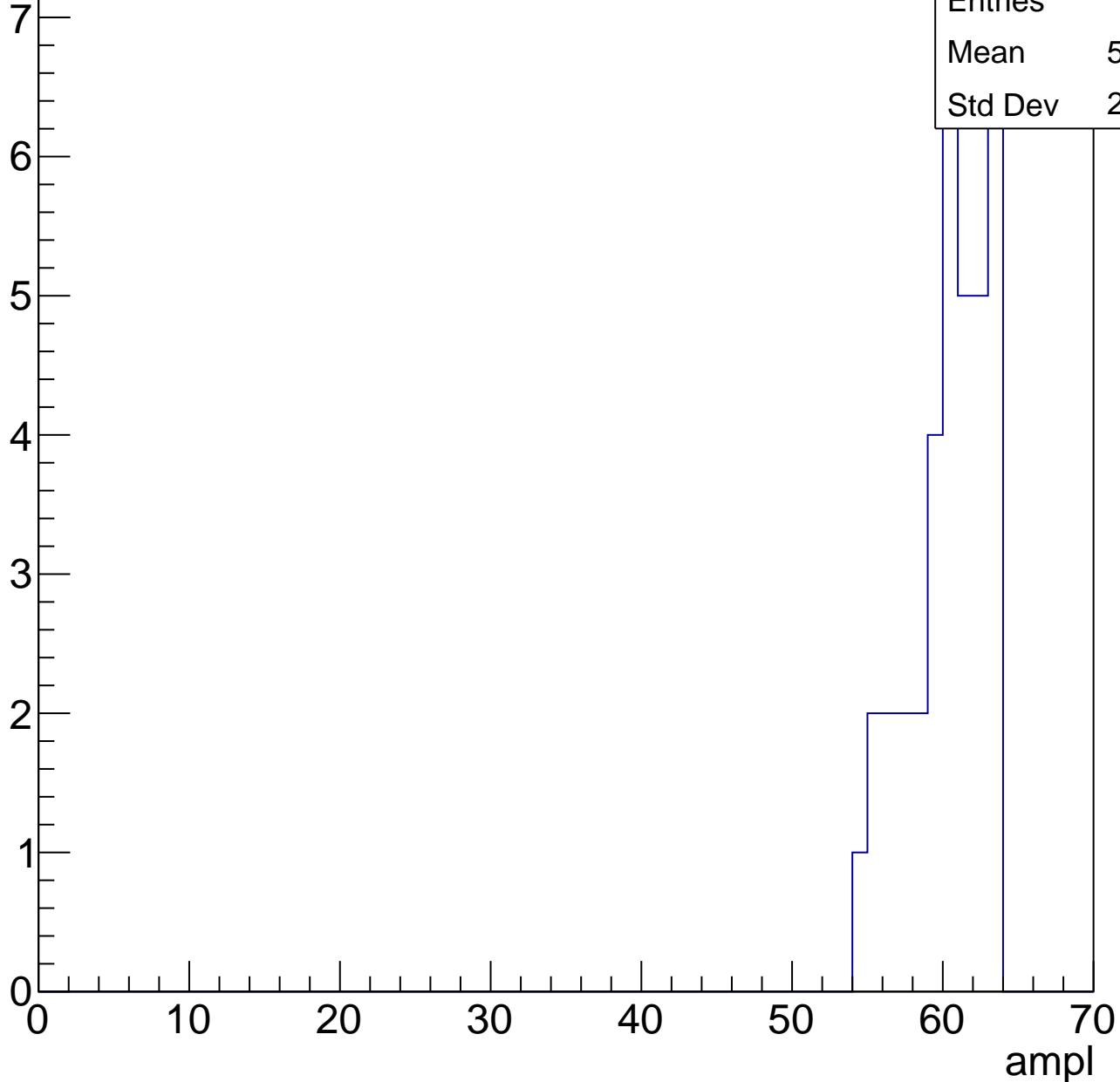
calib_packv5_042523_0143.root, FC#5, port B1



B0L000S, U2-ch82, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

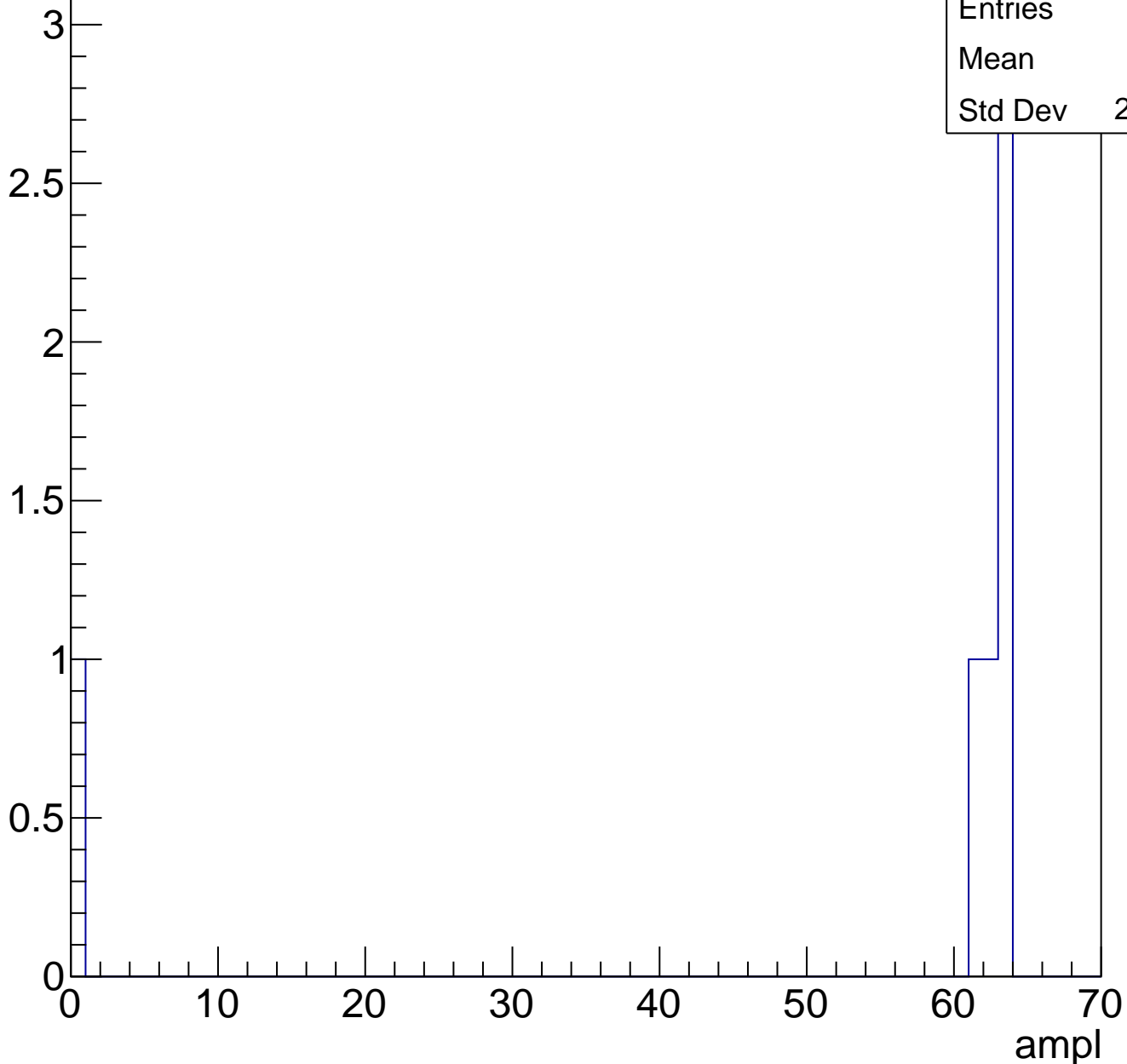


Entries	37
Mean	59.95
Std Dev	2.525

B0L000S, U2-ch82, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	6
Mean	52
Std Dev	23.27

B0L000S, U2-ch82, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch83, adc0

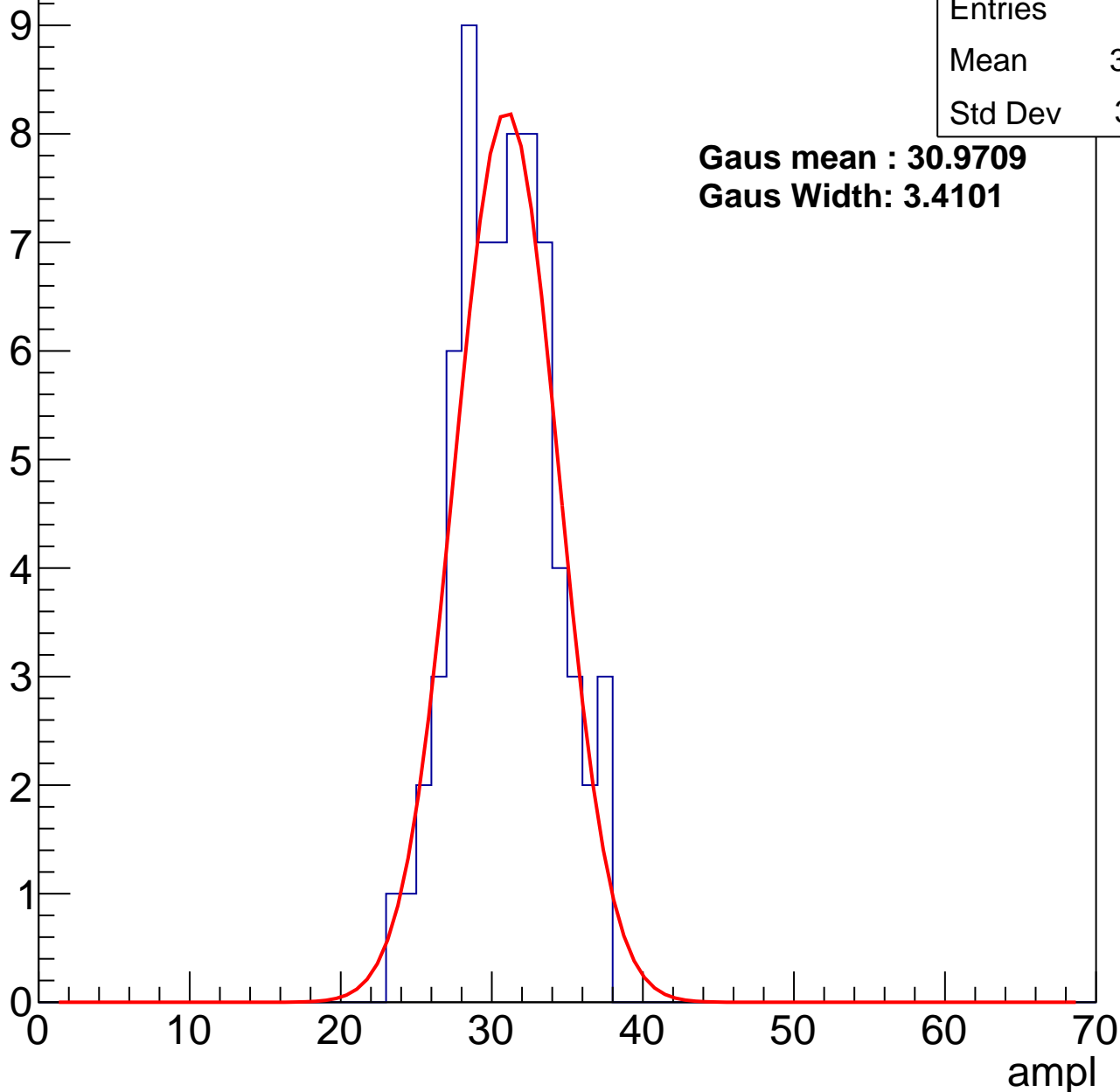
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.44
Std Dev	3.201

Gaus mean : 30.9709

Gaus Width: 3.4101



B0L000S, U2-ch83, adc1

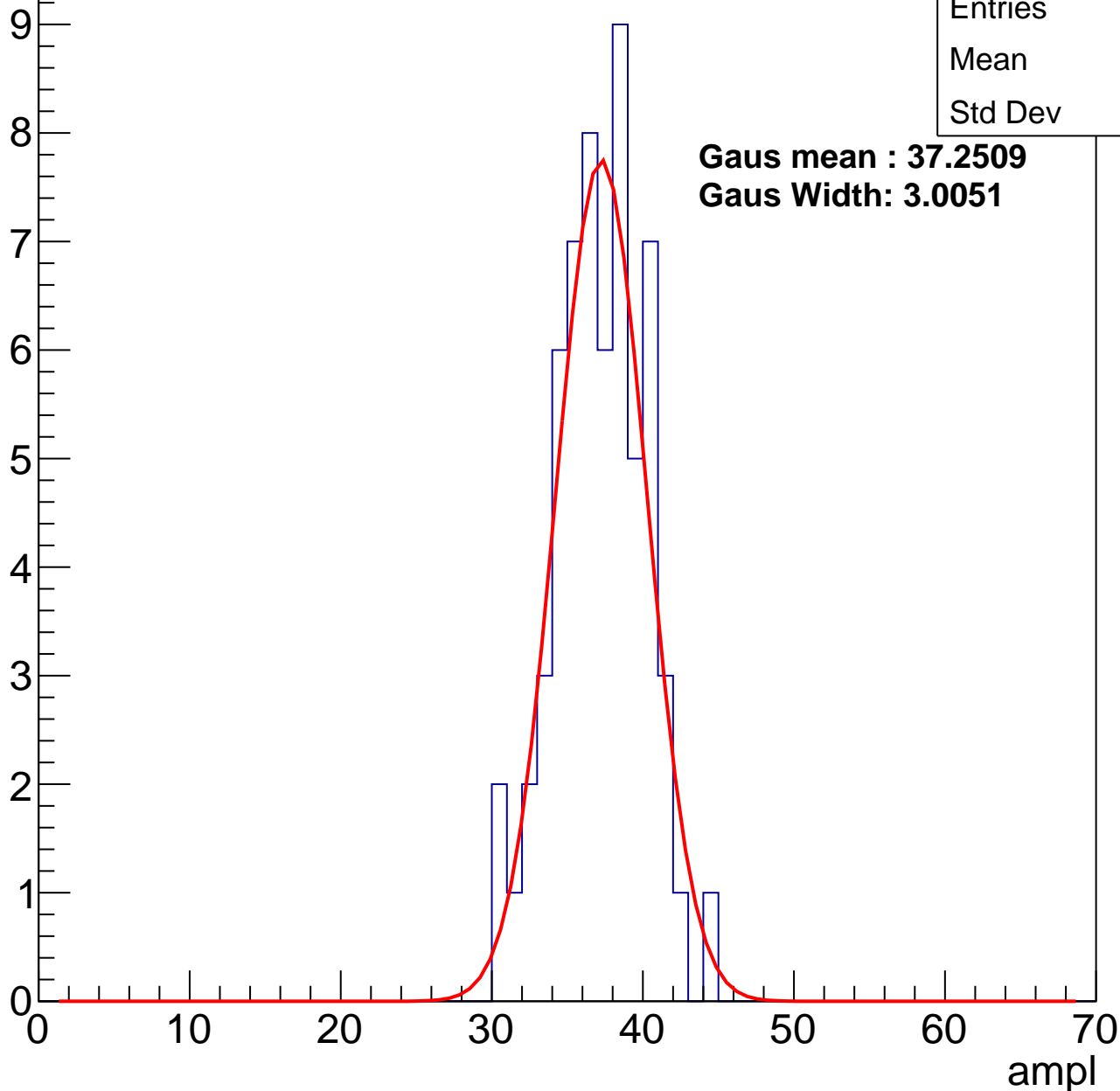
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.7
Std Dev	2.95

Gaus mean : 37.2509

Gaus Width: 3.0051



B0L000S, U2-ch83, adc2

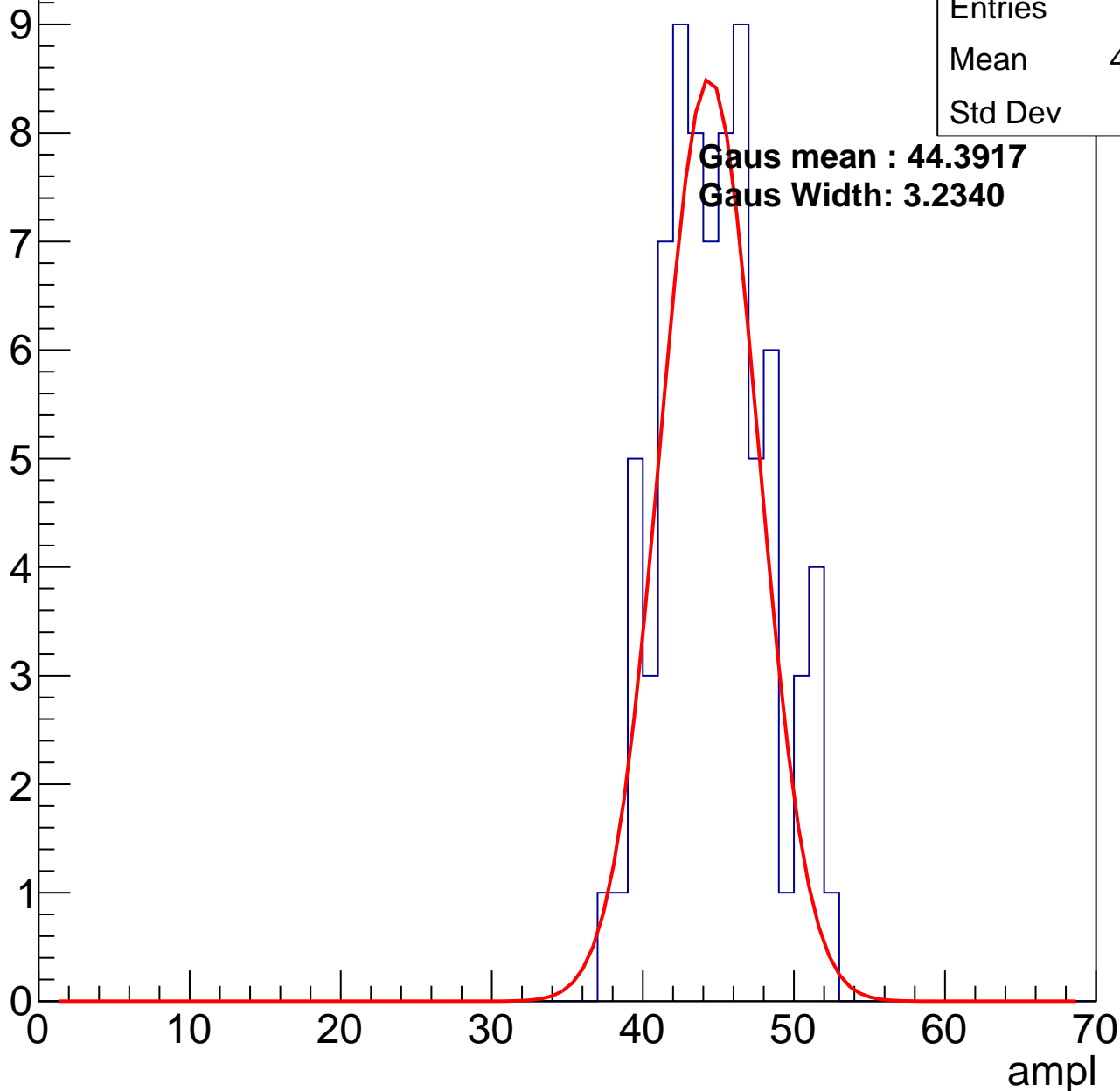
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	44.35
Std Dev	3.46

Gaus mean : 44.3917

Gaus Width: 3.2340

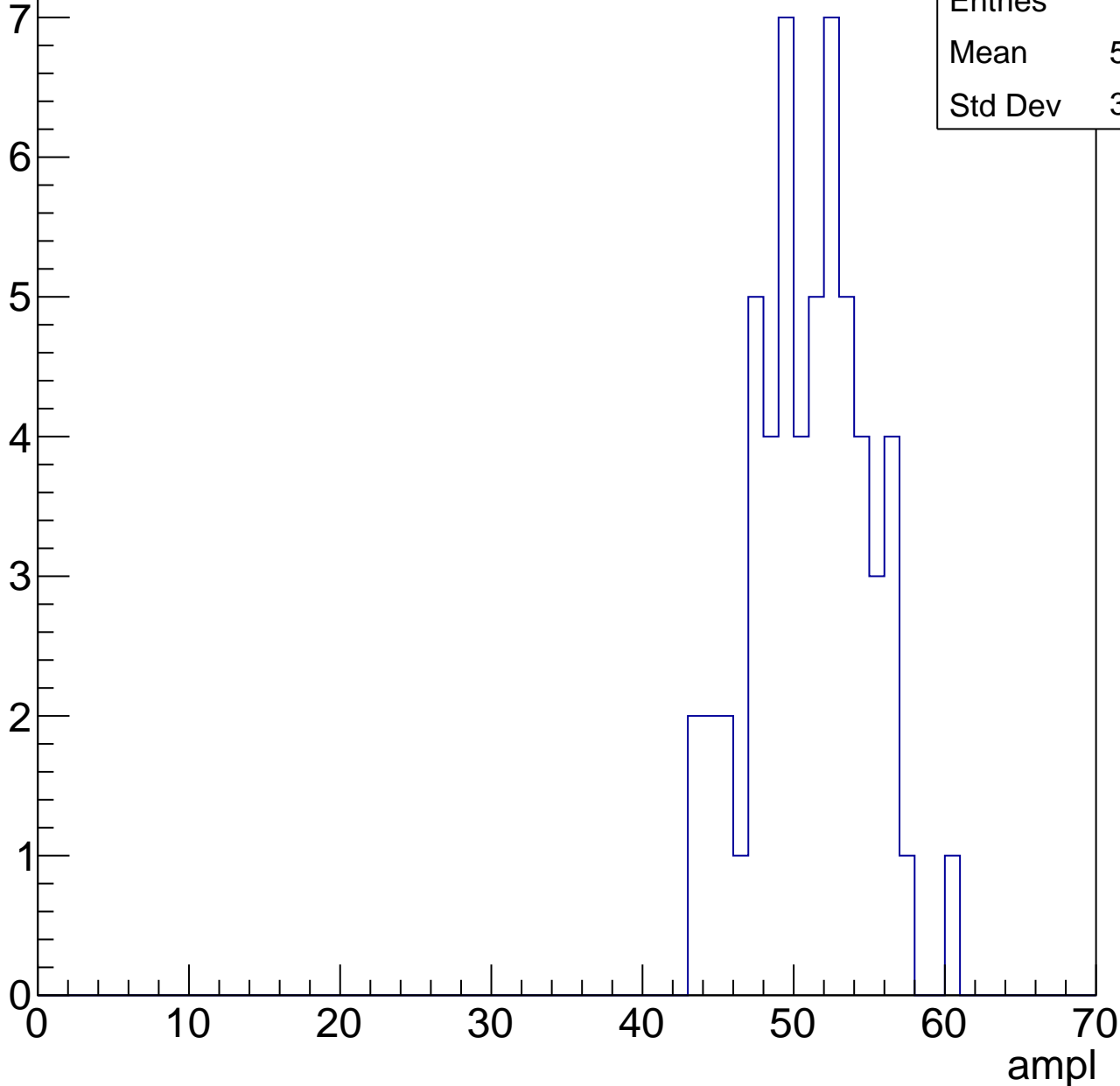


B0L000S, U2-ch83, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	50.63
Std Dev	3.726

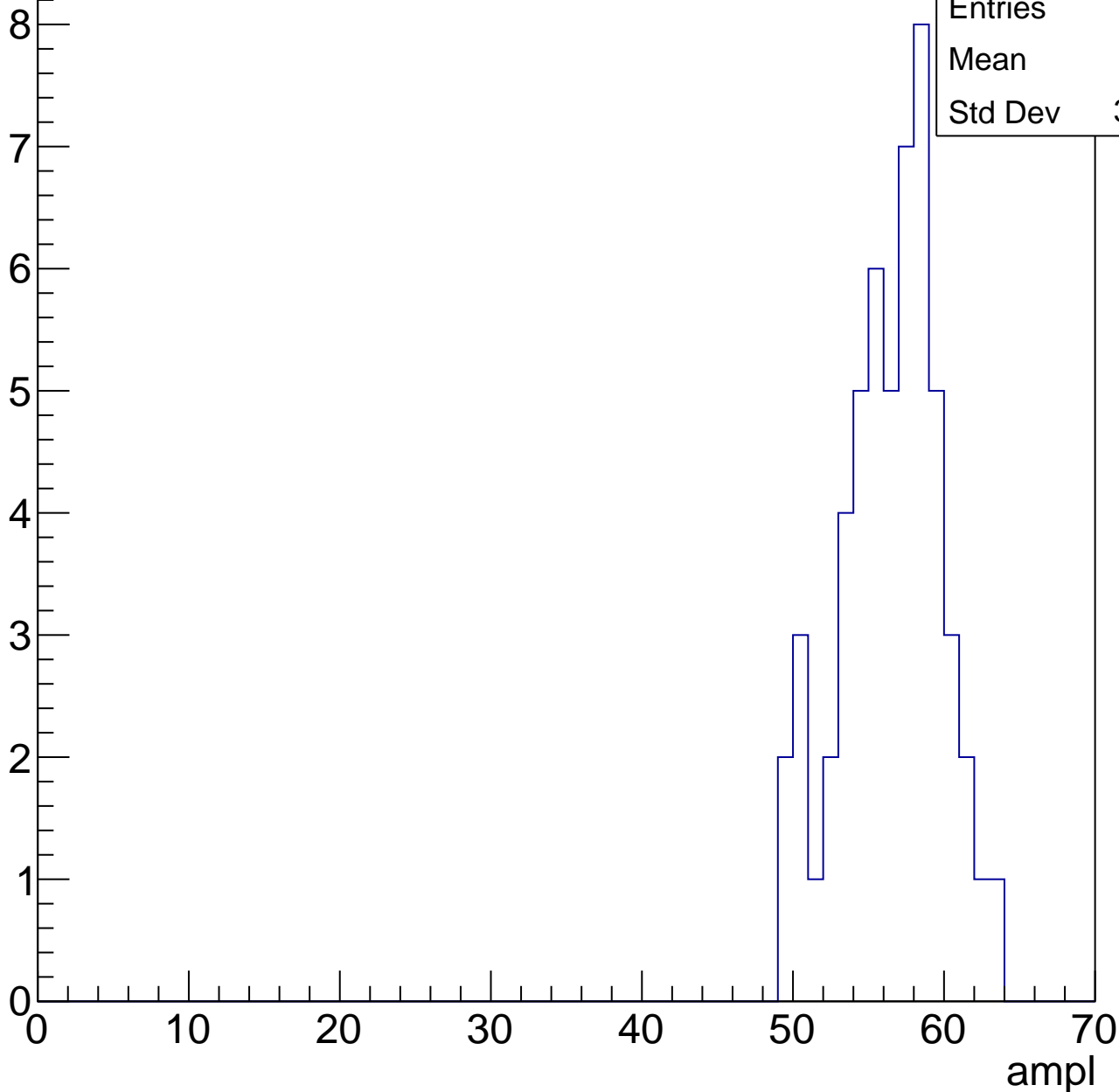


B0L000S, U2-ch83, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56
Std Dev	3.281



B0L000S, U2-ch83, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries	38
Mean	58.5
Std Dev	9.957

ampl

10

20

30

40

50

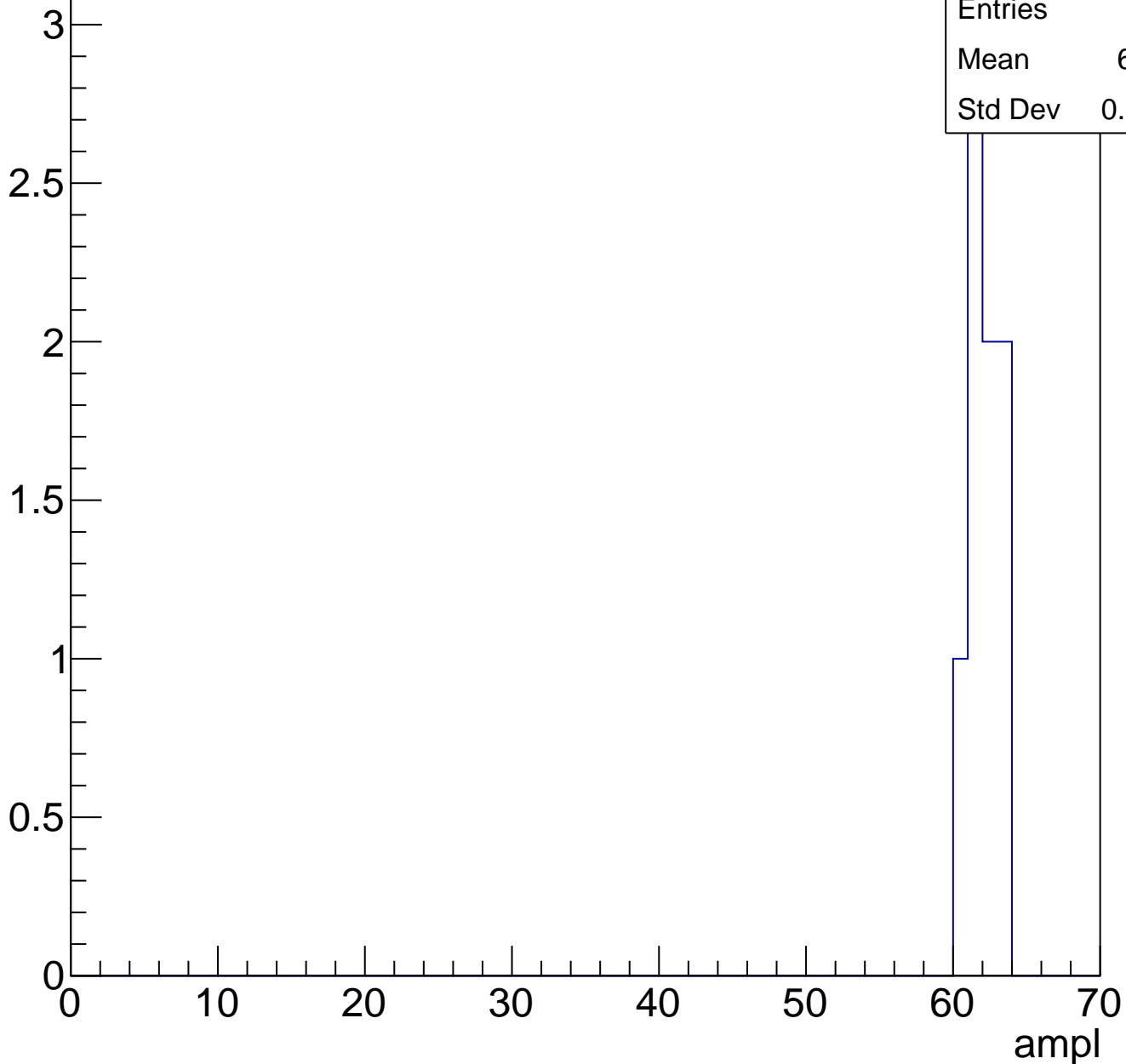
60

70

B0L000S, U2-ch83, adc6

calib_packv5_042523_0143.root, FC#5, port B1

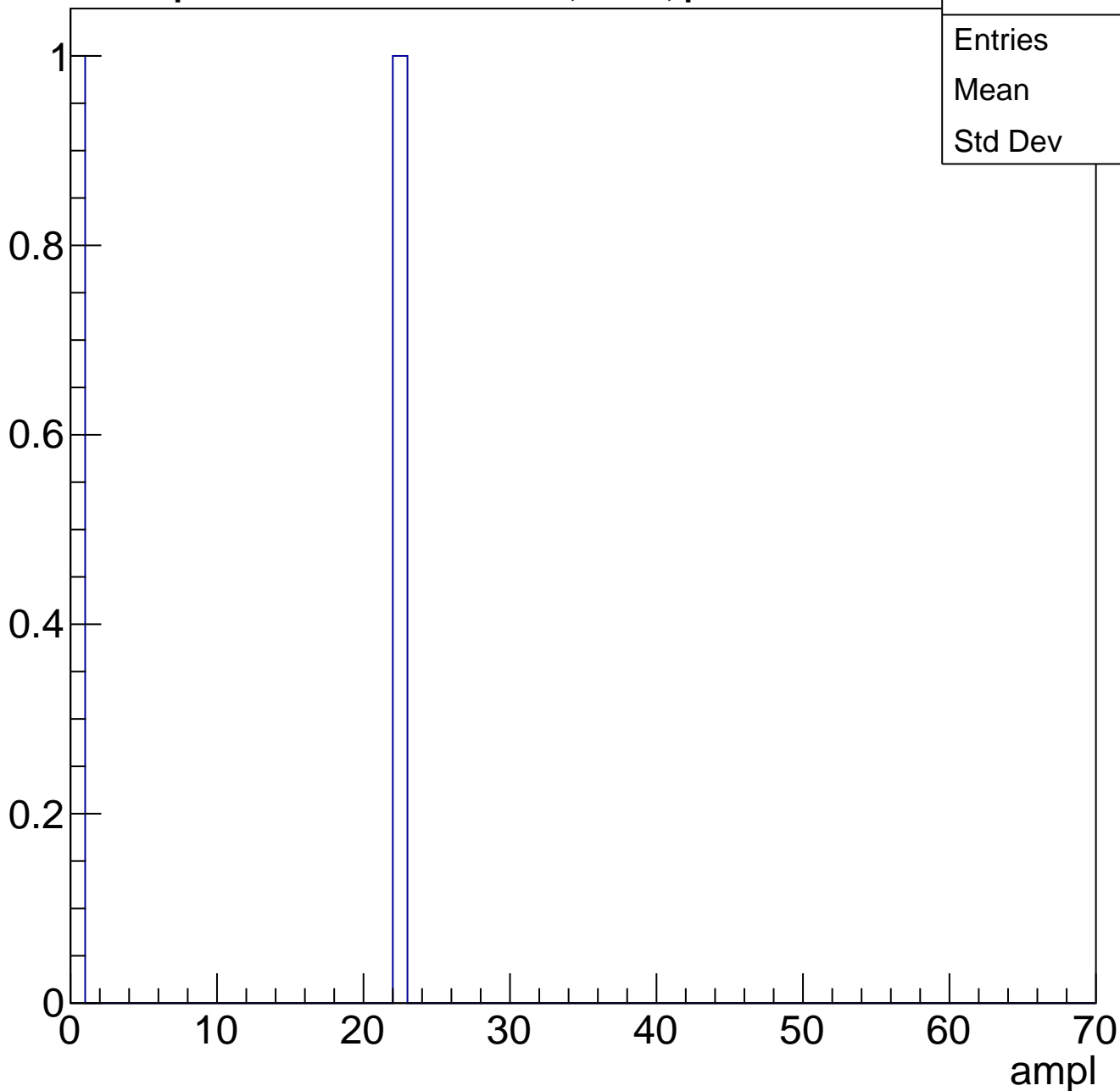
Entry



B0L000S, U2-ch83, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

B0L000S, U2-ch84, adc0

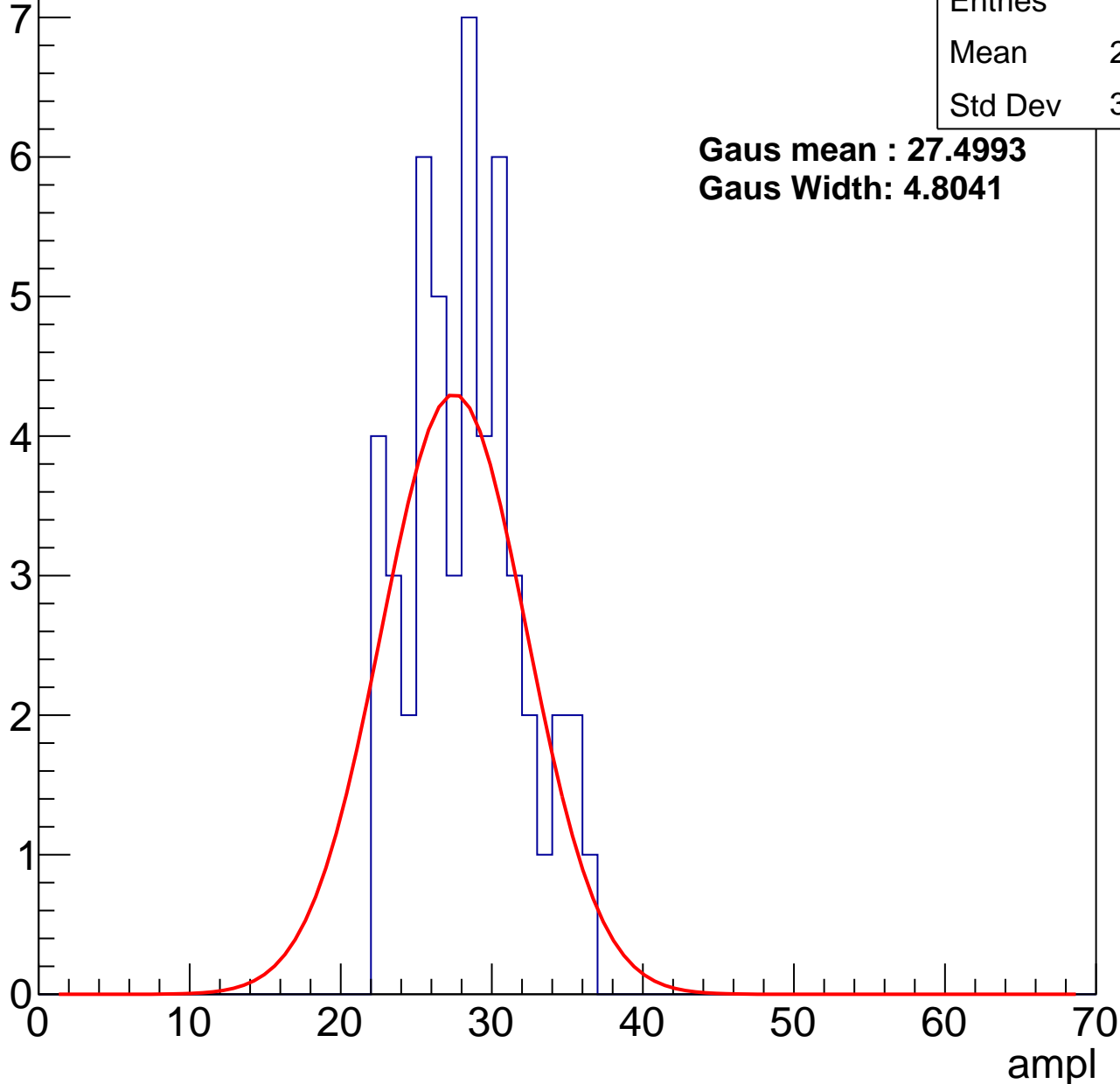
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	27.88
Std Dev	3.633

Gaus mean : 27.4993

Gaus Width: 4.8041



B0L000S, U2-ch84, adc1

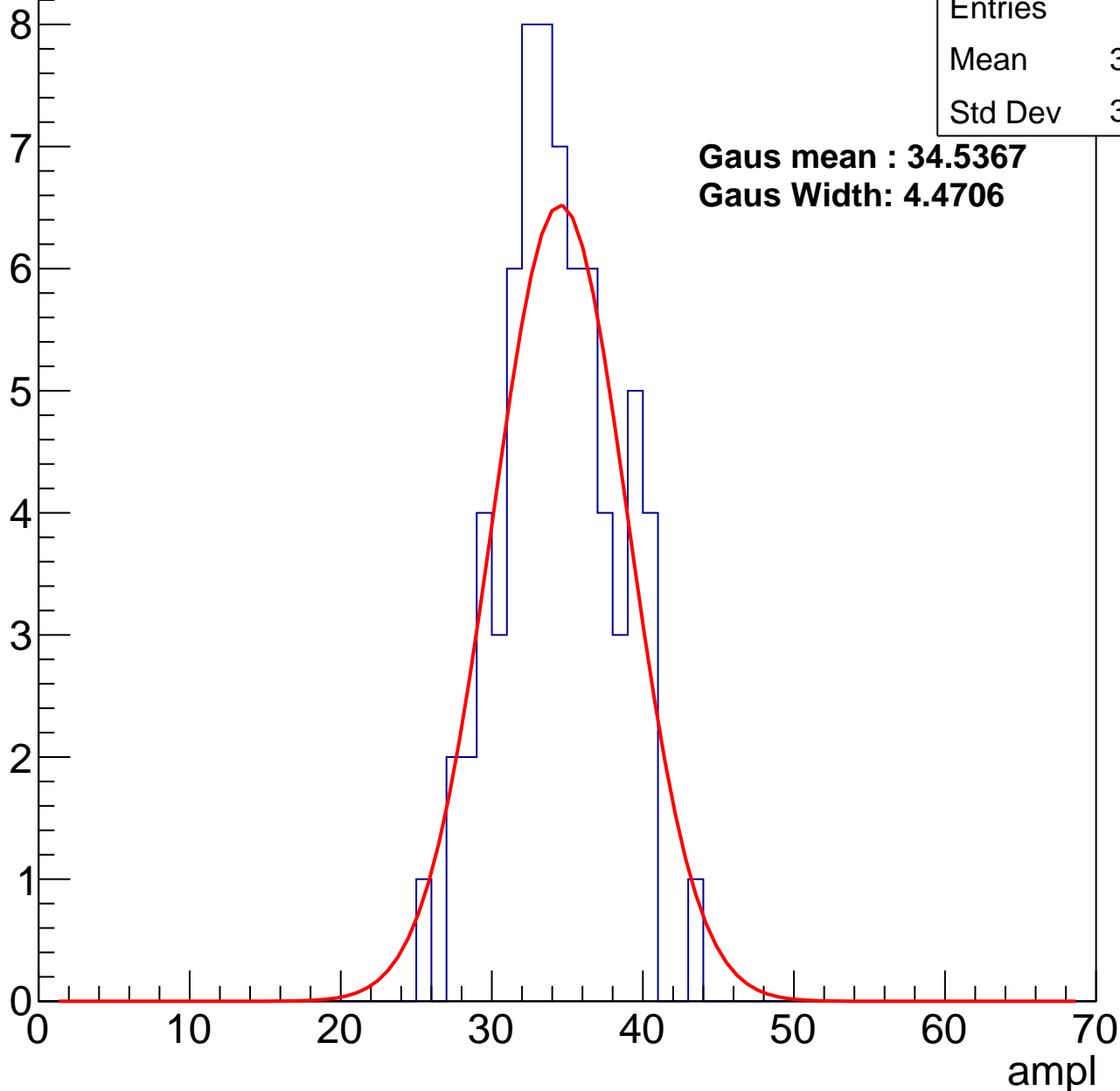
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	33.87
Std Dev	3.695

Gaus mean : 34.5367

Gaus Width: 4.4706



B0L000S, U2-ch84, adc2

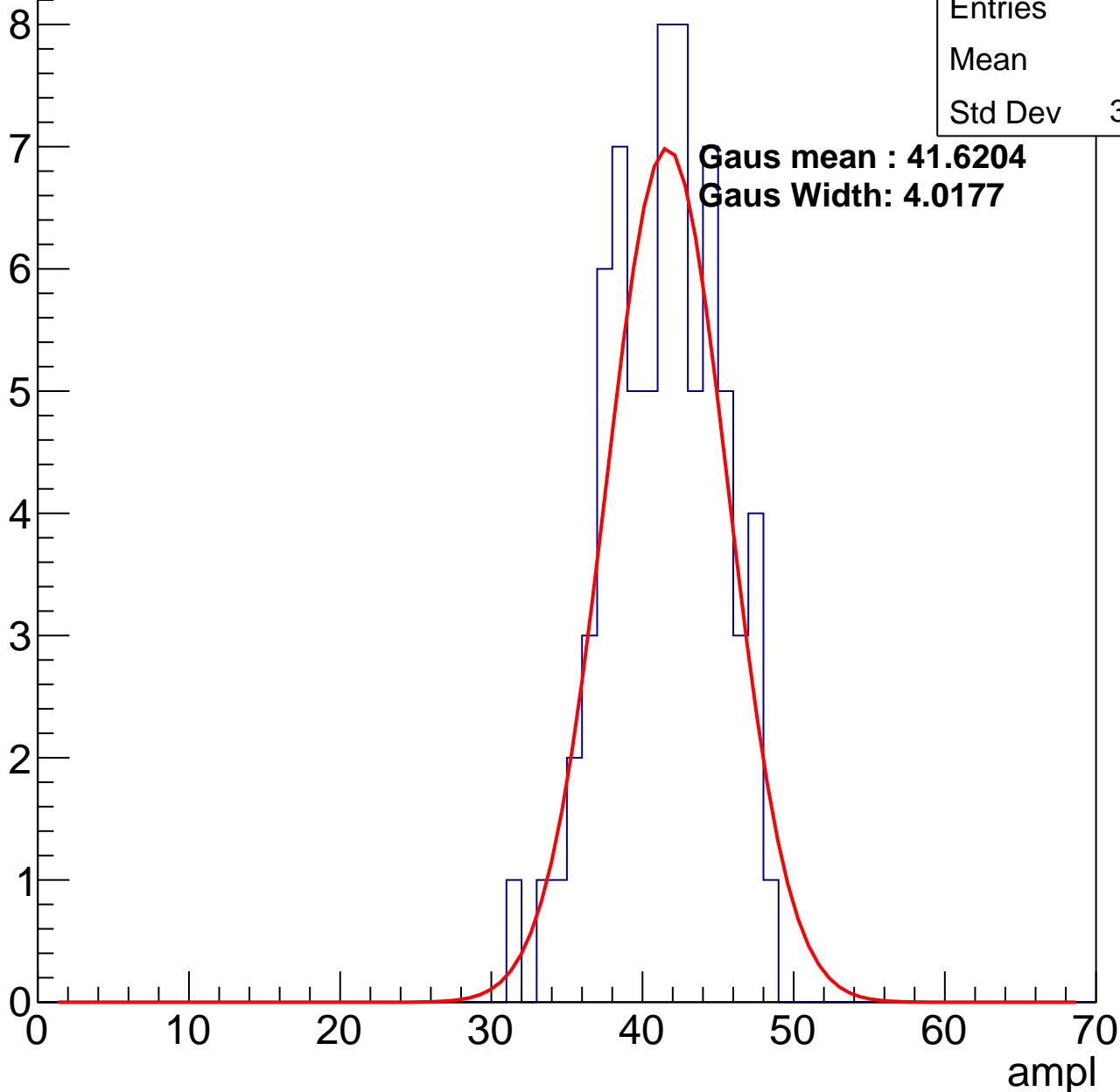
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	40.9
Std Dev	3.694

Gaus mean : 41.6204

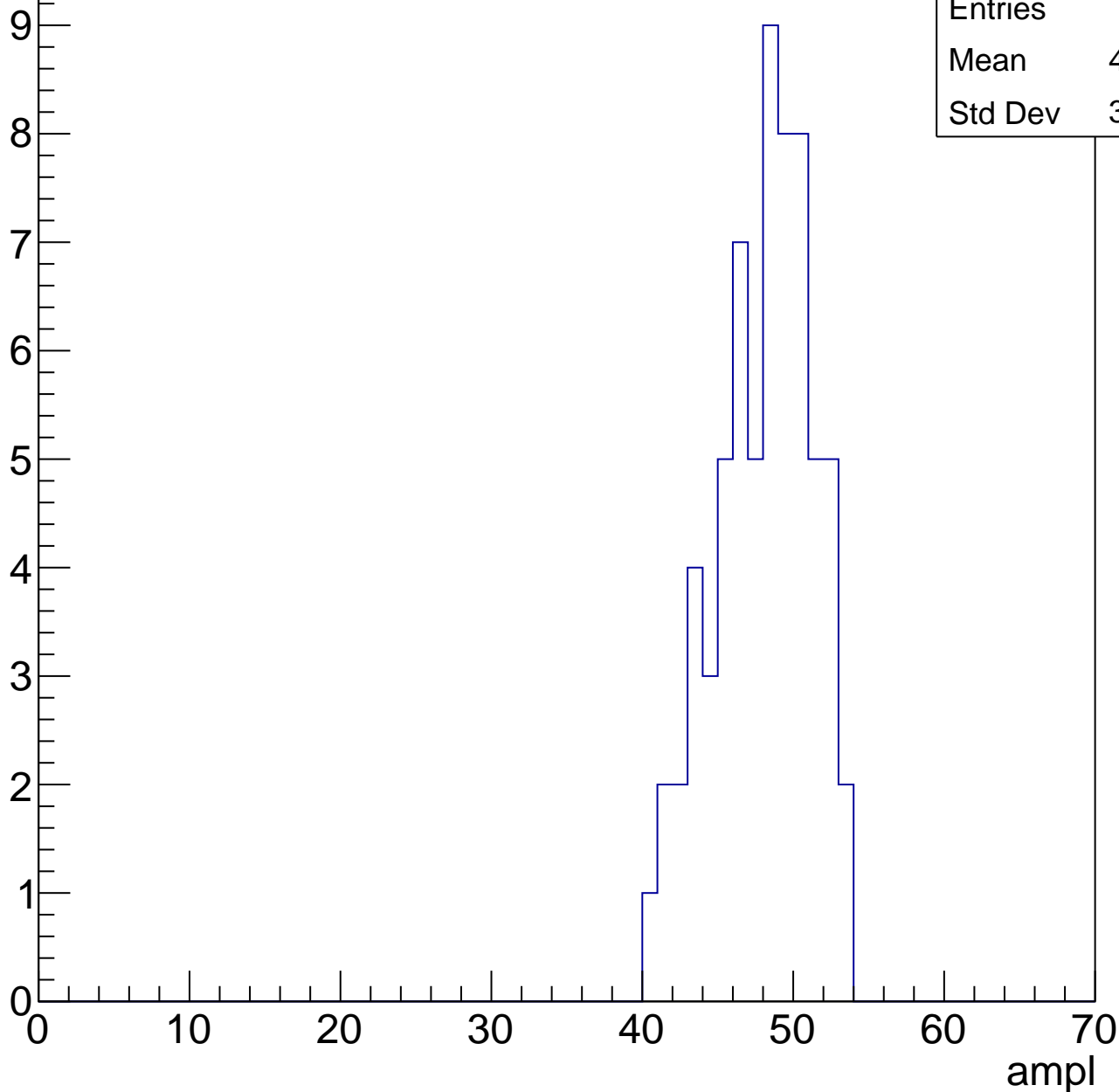
Gaus Width: 4.0177



B0L000S, U2-ch84, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



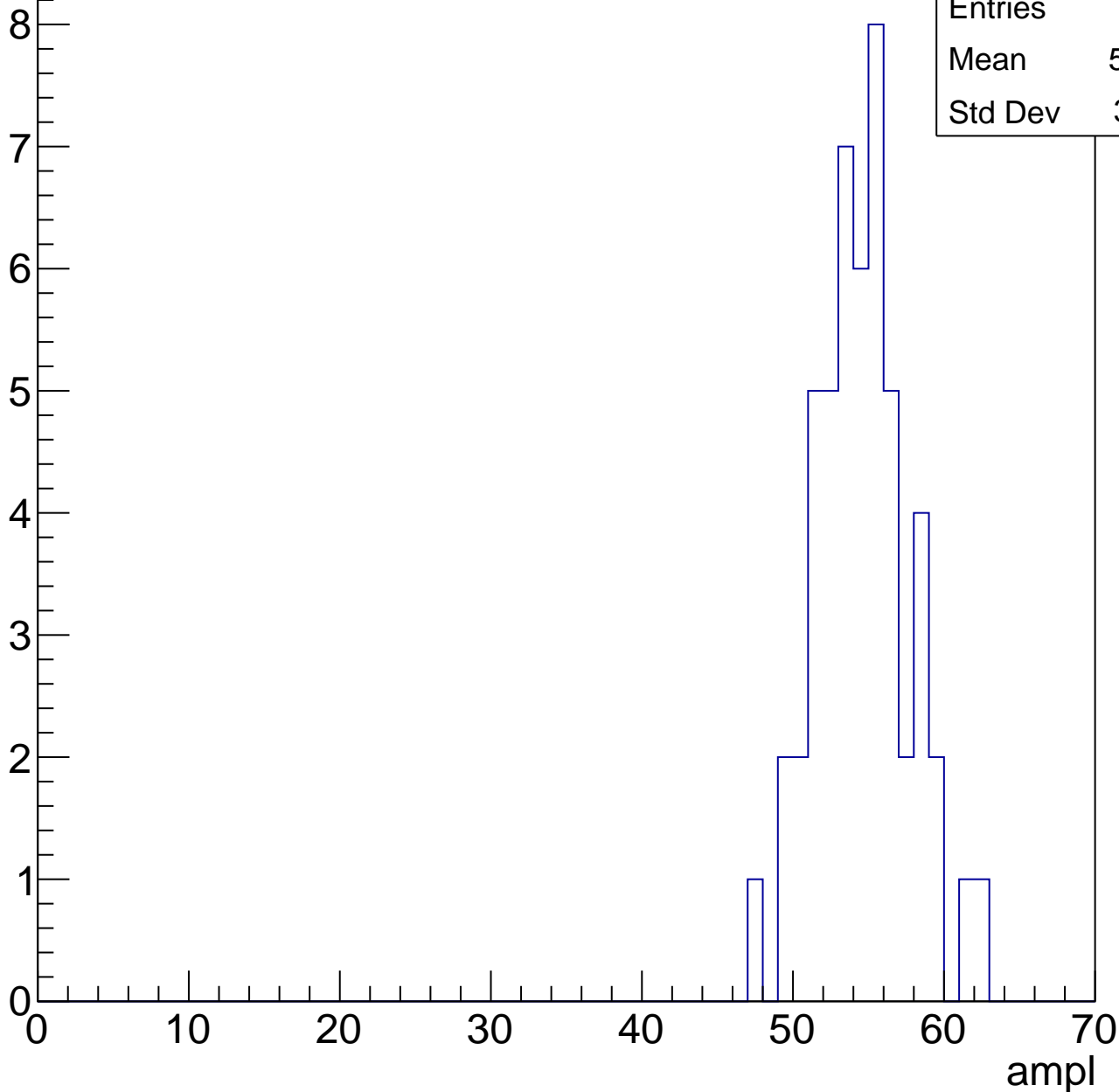
Entries	66
Mean	47.53
Std Dev	3.163

B0L000S, U2-ch84, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

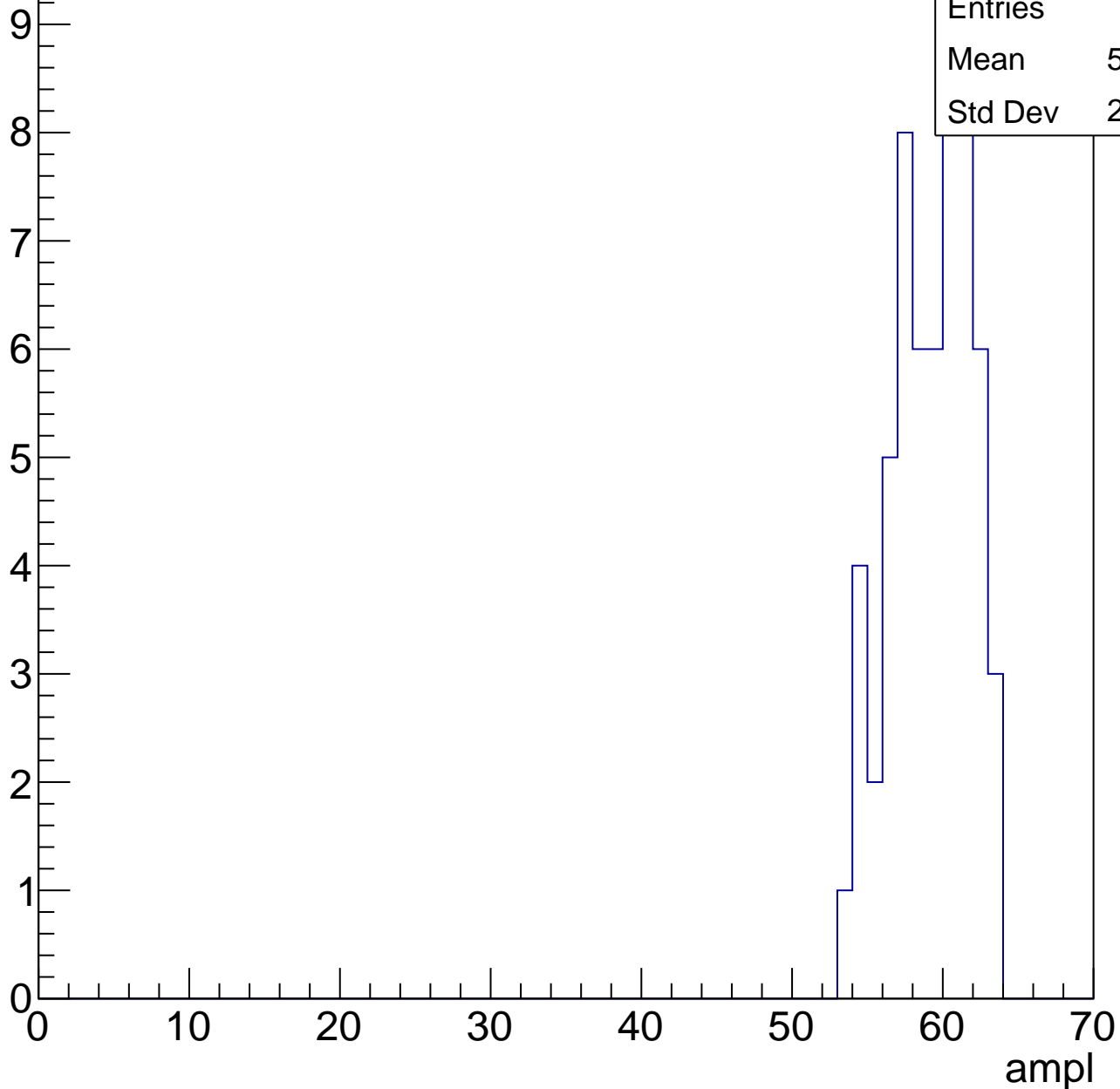
Entries	51
Mean	54.16
Std Dev	3.051



B0L000S, U2-ch84, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



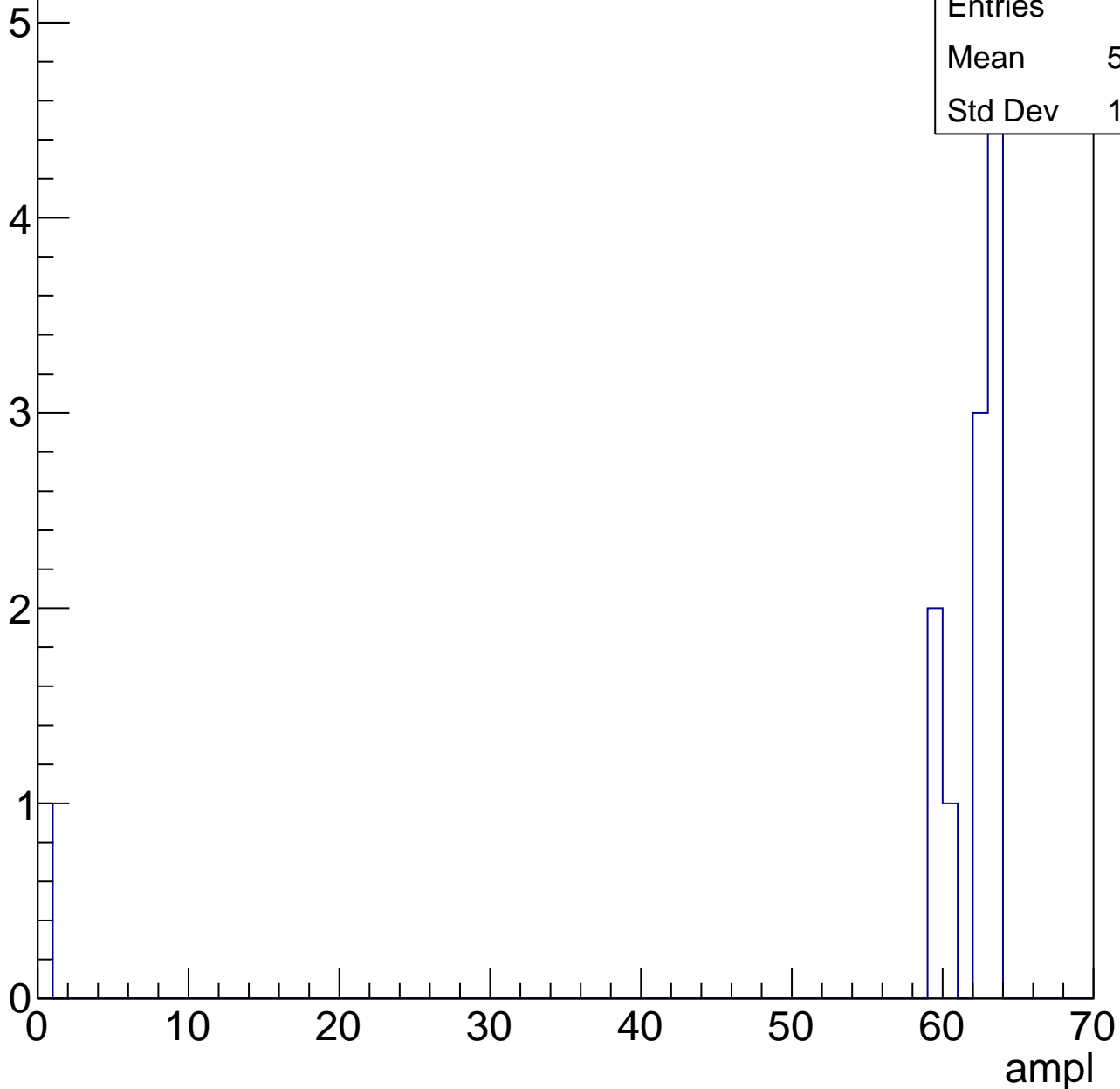
Entries	58
Mean	58.74
Std Dev	2.603

B0L000S, U2-ch84, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

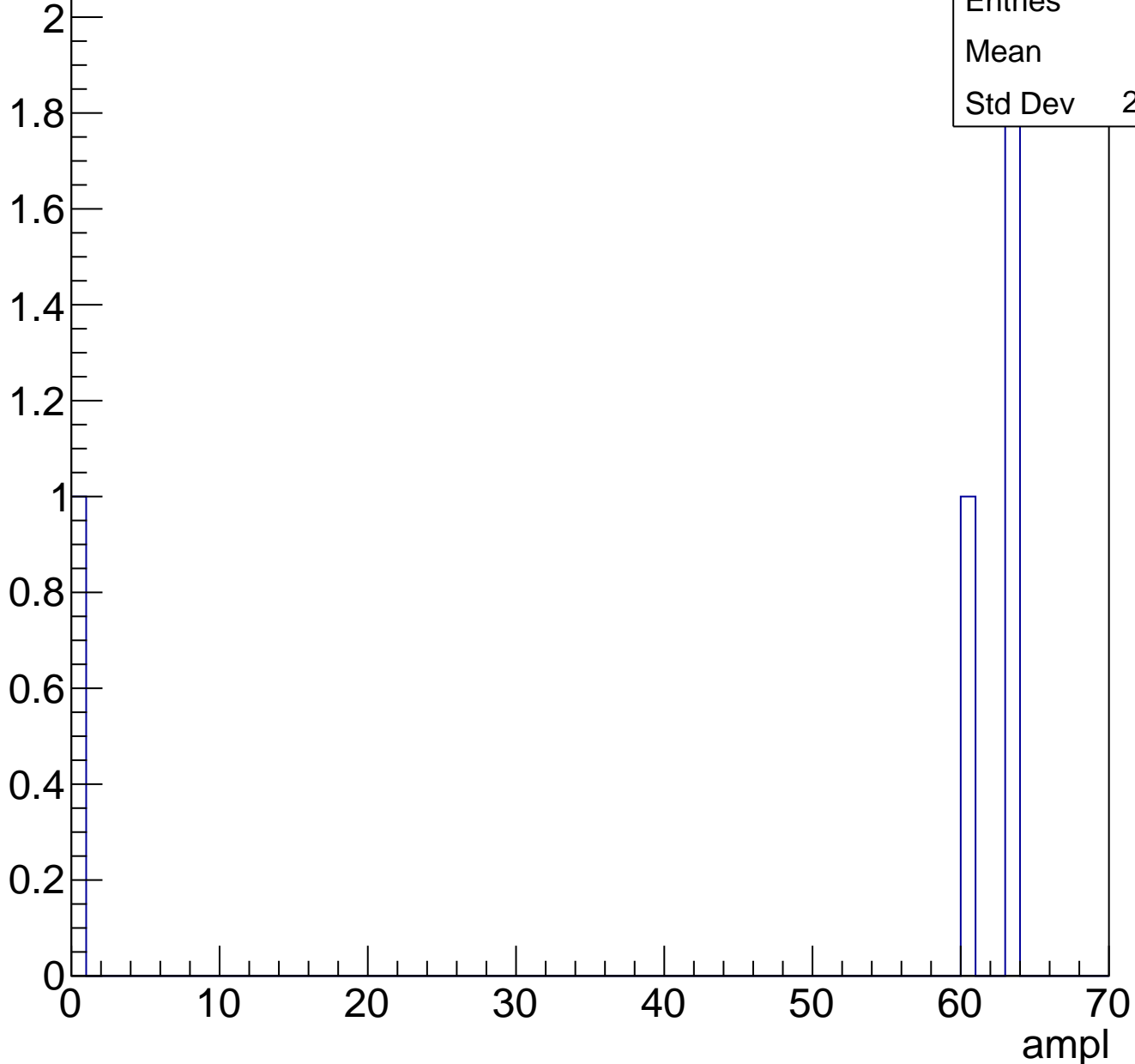
Entries	12
Mean	56.58
Std Dev	17.12



B0L000S, U2-ch84, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch85, adc0

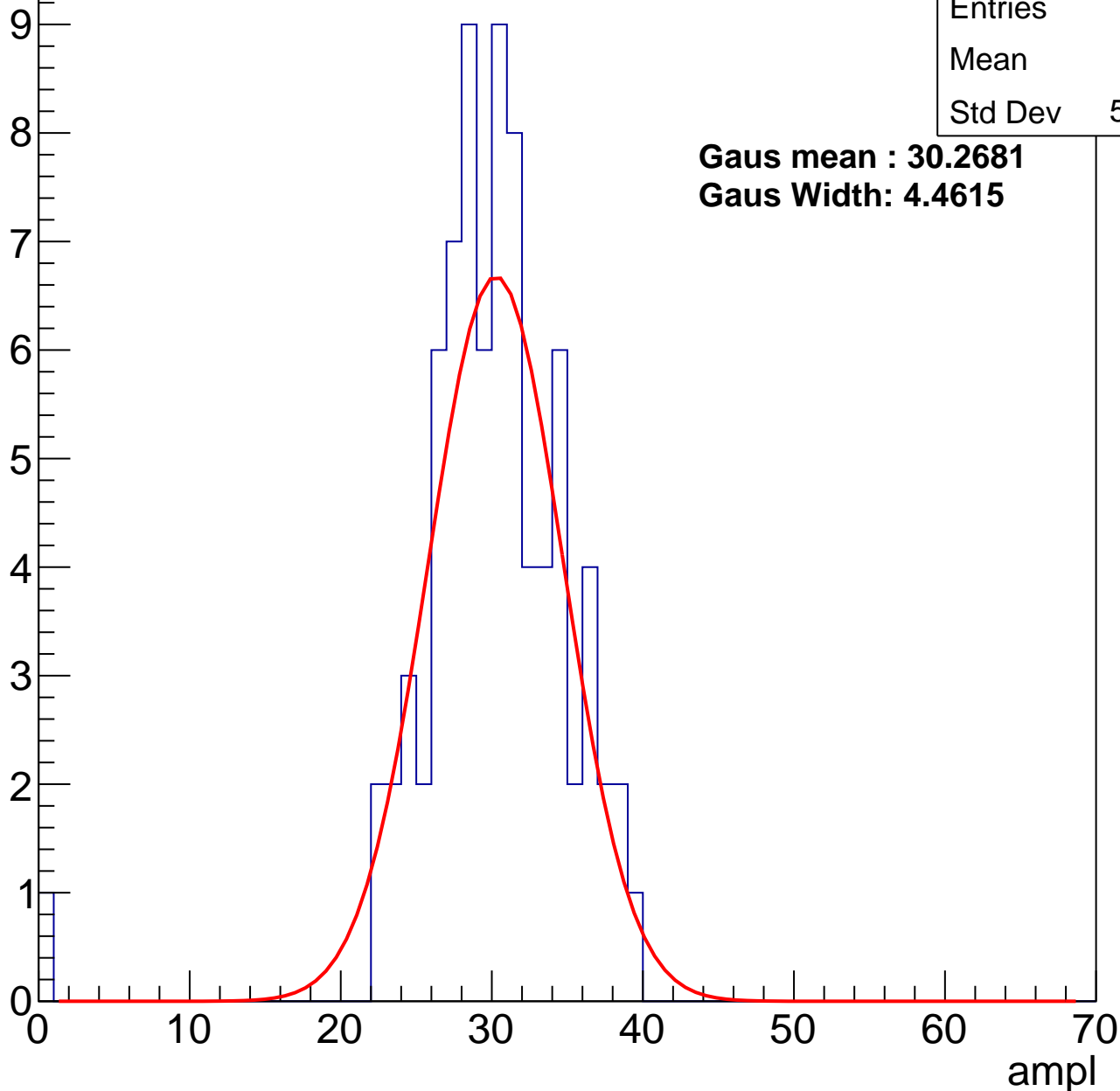
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	80
Mean	29.6
Std Dev	5.159

Gaus mean : 30.2681

Gaus Width: 4.4615



B0L000S, U2-ch85, adc1

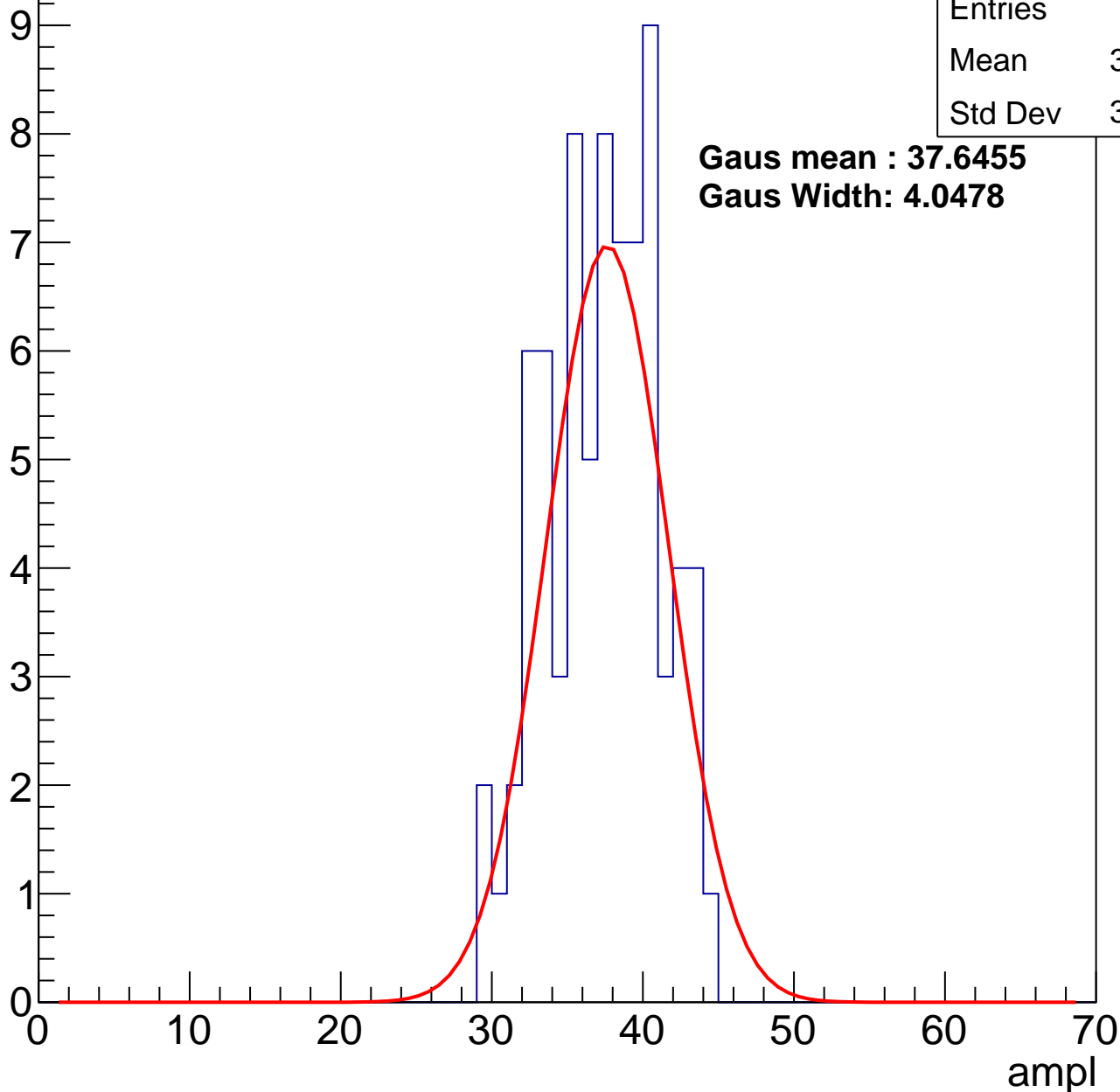
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	36.89
Std Dev	3.655

Gaus mean : 37.6455

Gaus Width: 4.0478



B0L000S, U2-ch85, adc2

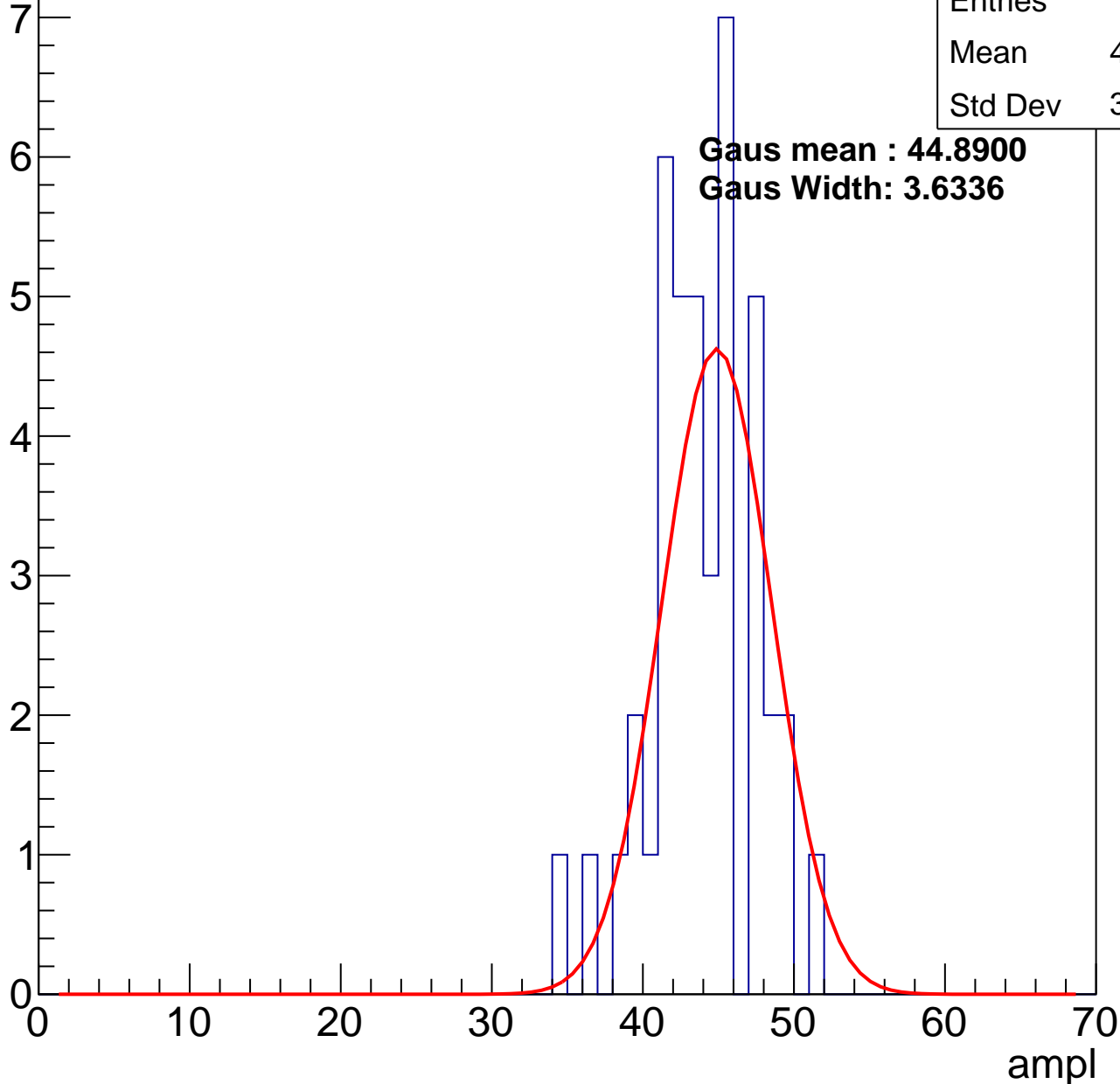
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	42
Mean	43.43
Std Dev	3.499

Gaus mean : 44.8900

Gaus Width: 3.6336



B0L000S, U2-ch85, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	49.06
Std Dev	3.425

Entry

10

8

6

4

2

0

0

10

20

30

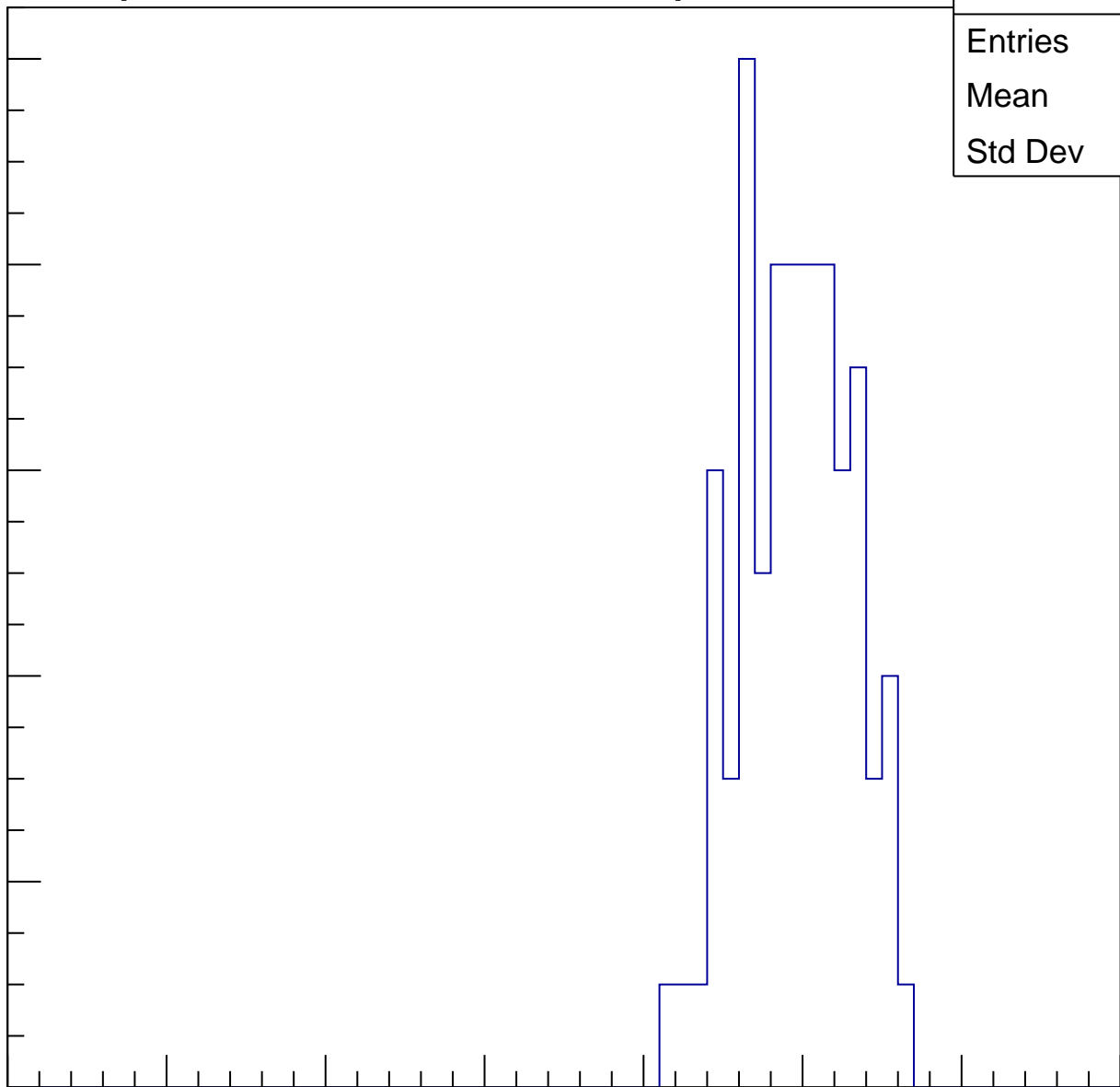
40

50

60

70

ampl

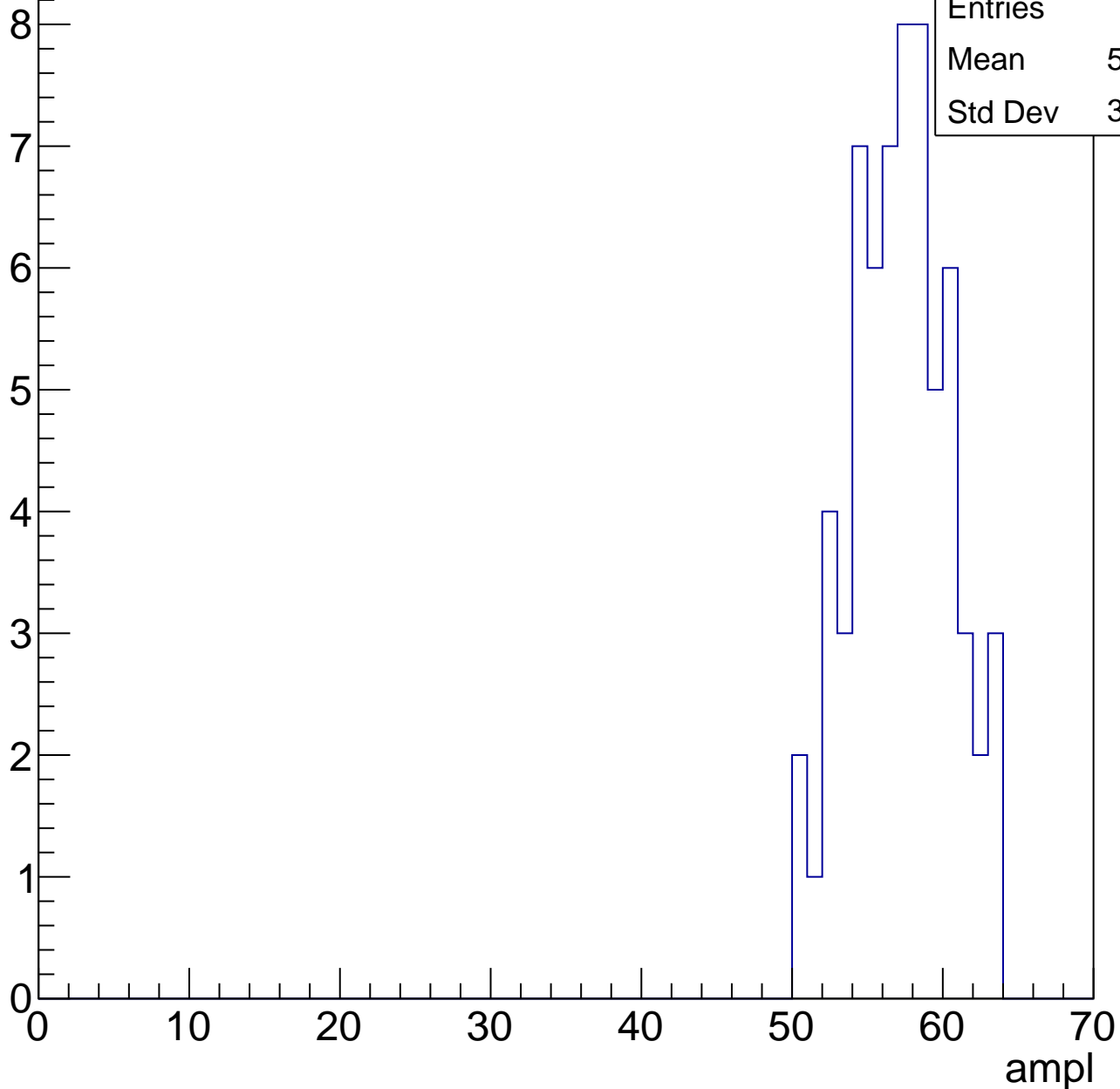


B0L000S, U2-ch85, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

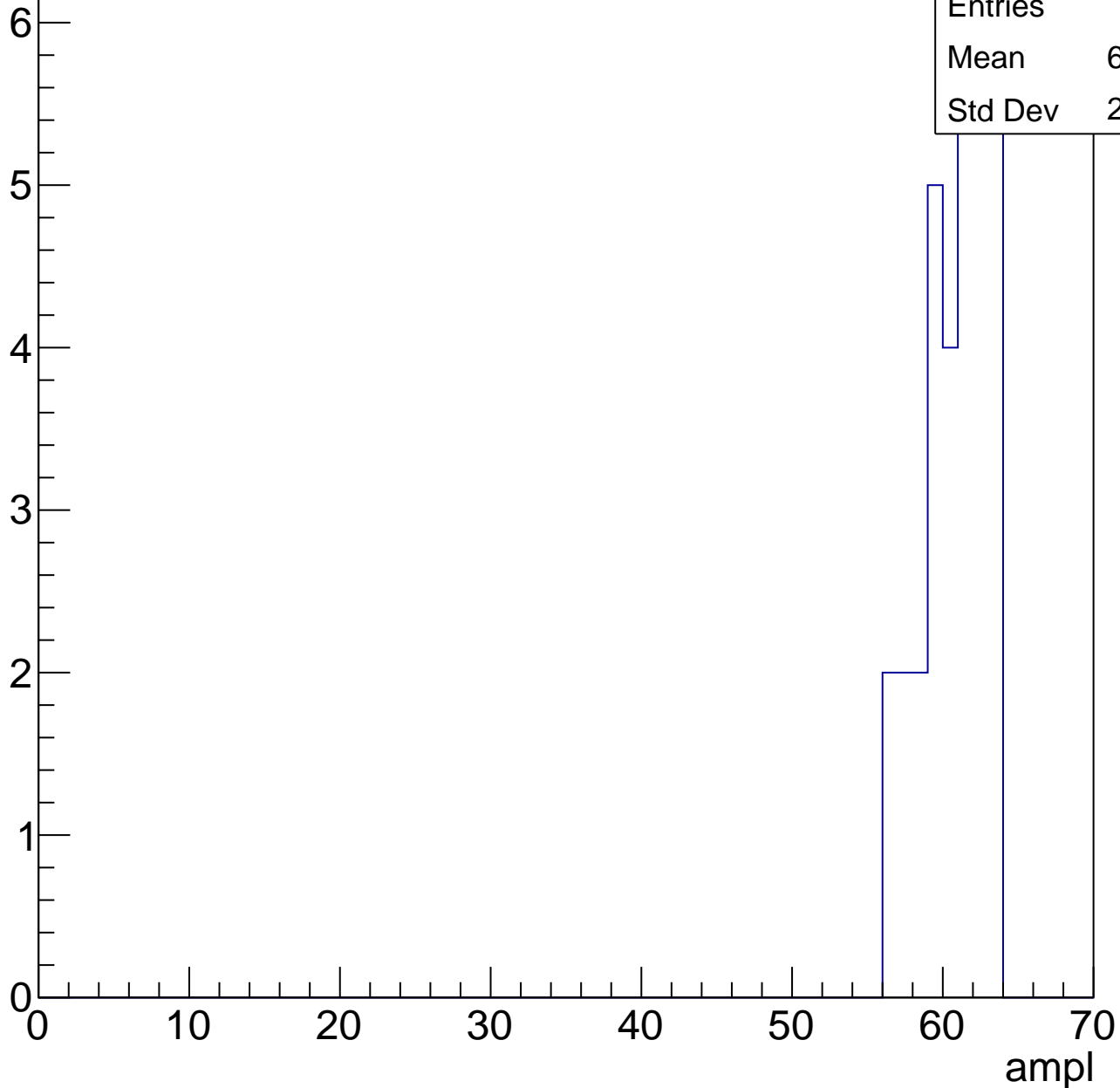
Entries	65
Mean	56.75
Std Dev	3.187



B0L000S, U2-ch85, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	33
Mean	60.39
Std Dev	2.073

B0L000S, U2-ch85, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	5
Mean	49.6
Std Dev	24.81

B0L000S, U2-ch85, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch86, adc0

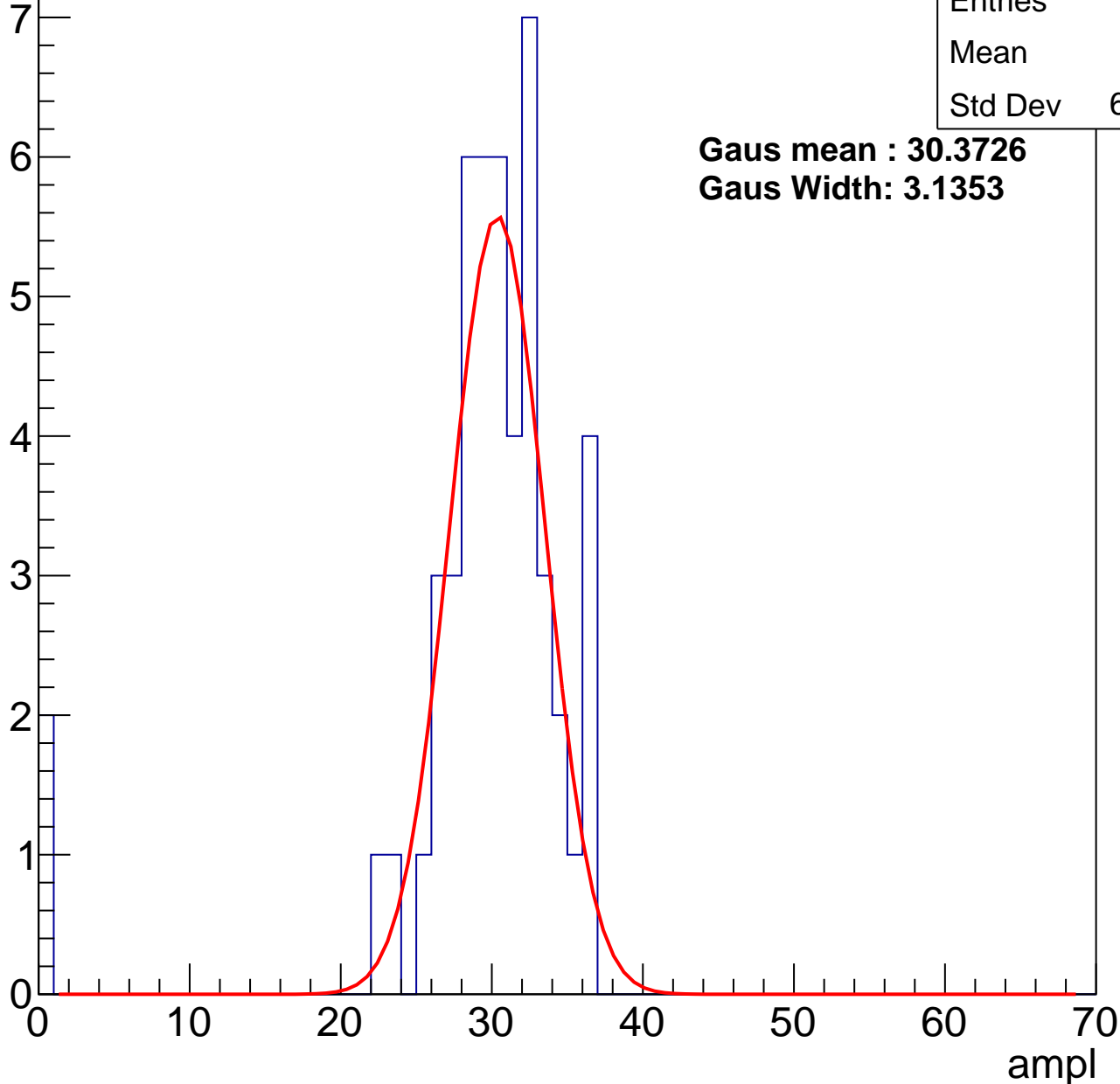
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	28.9
Std Dev	6.697

Gaus mean : 30.3726

Gaus Width: 3.1353



B0L000S, U2-ch86, adc1

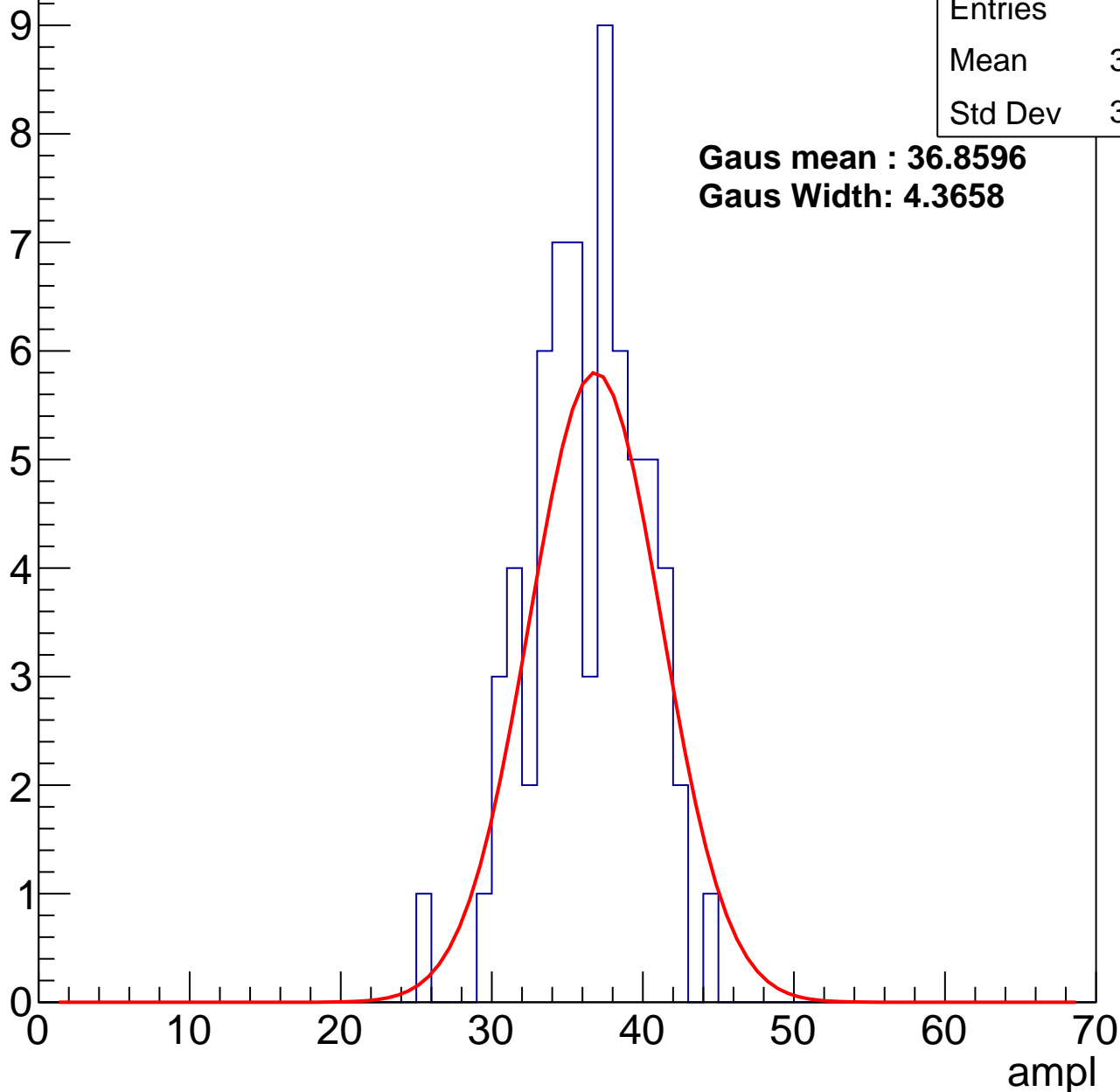
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	35.89
Std Dev	3.677

Gaus mean : 36.8596

Gaus Width: 4.3658



B0L000S, U2-ch86, adc2

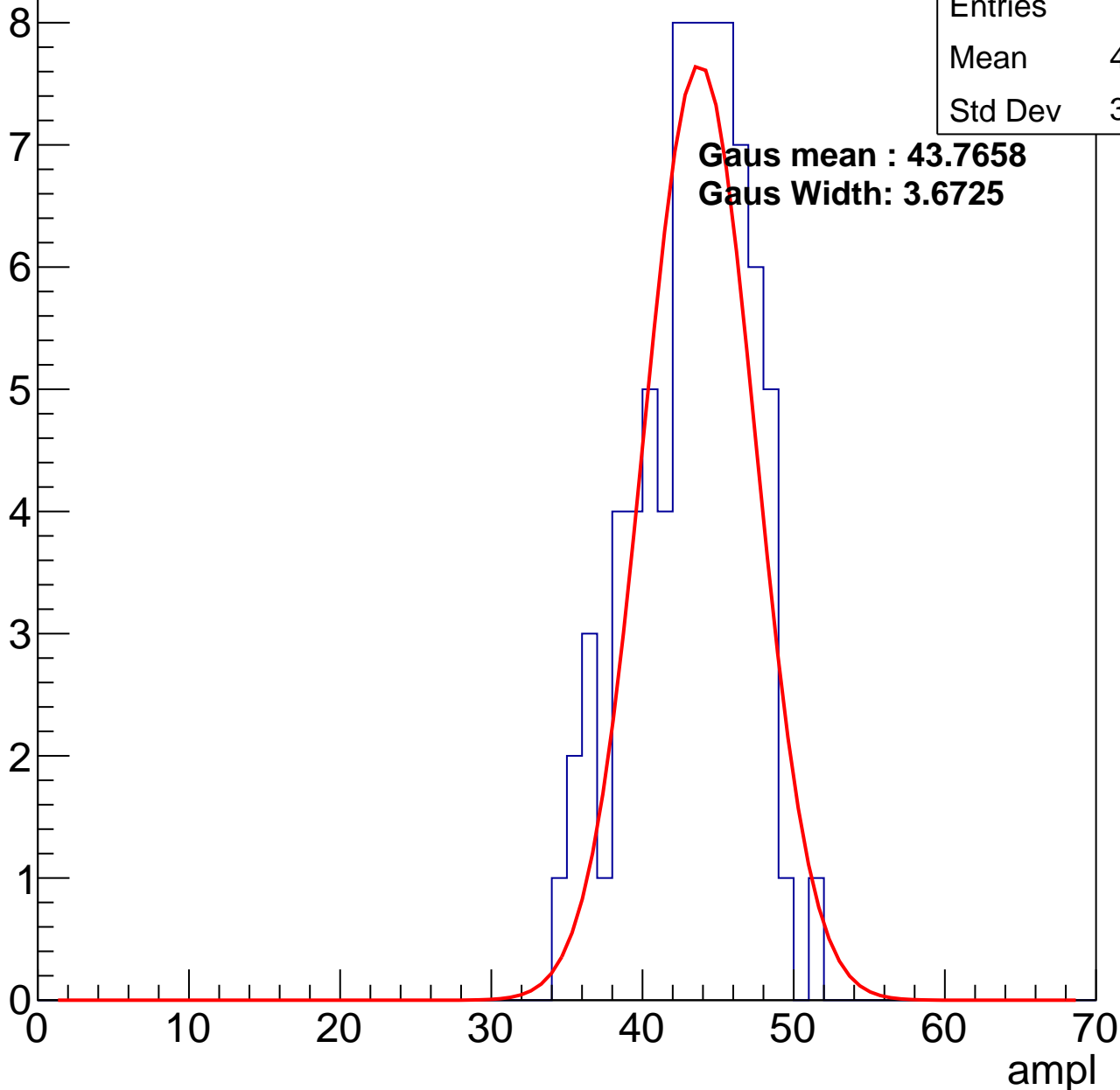
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.86
Std Dev	3.712

Gaus mean : 43.7658

Gaus Width: 3.6725

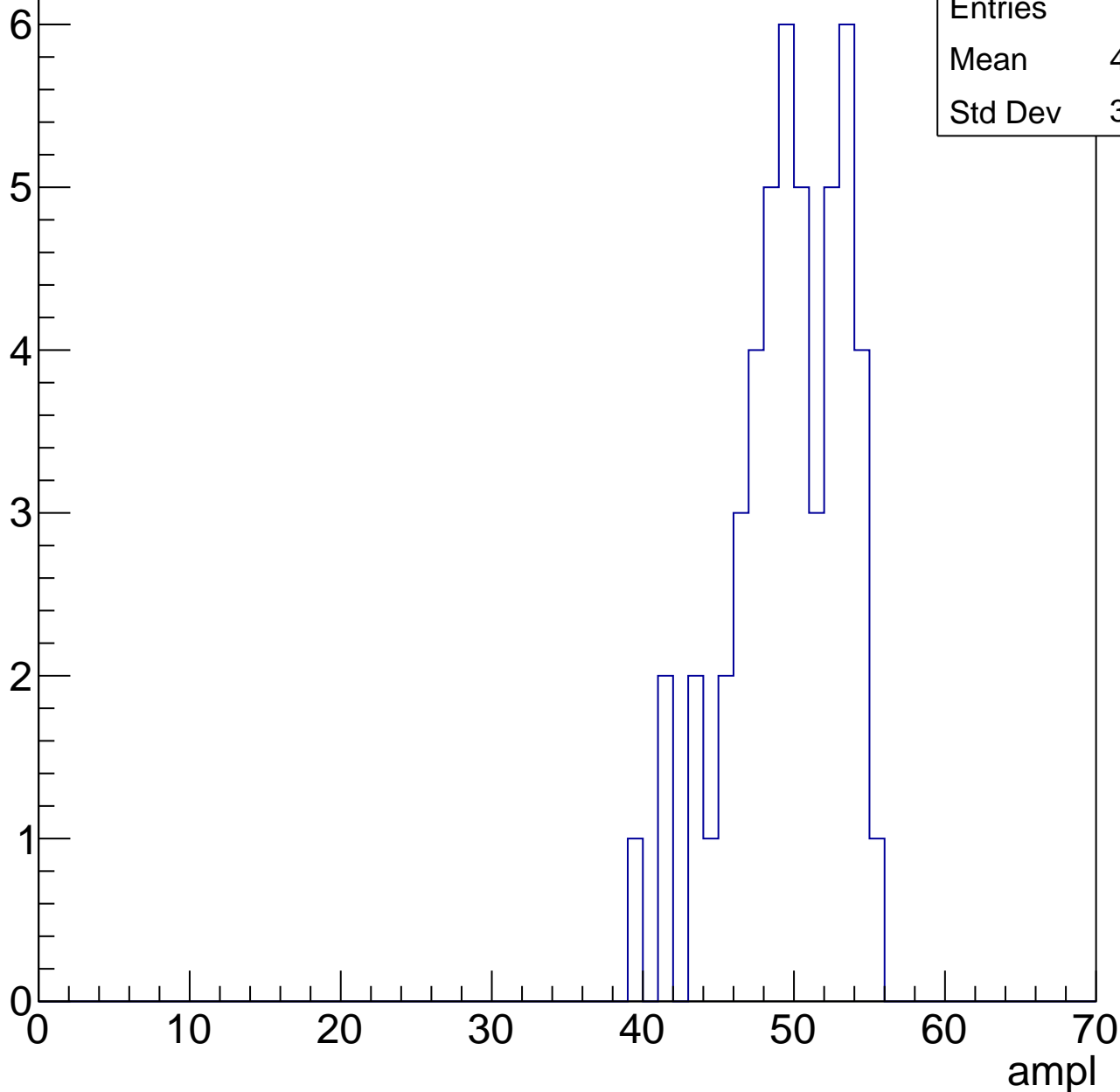


B0L000S, U2-ch86, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

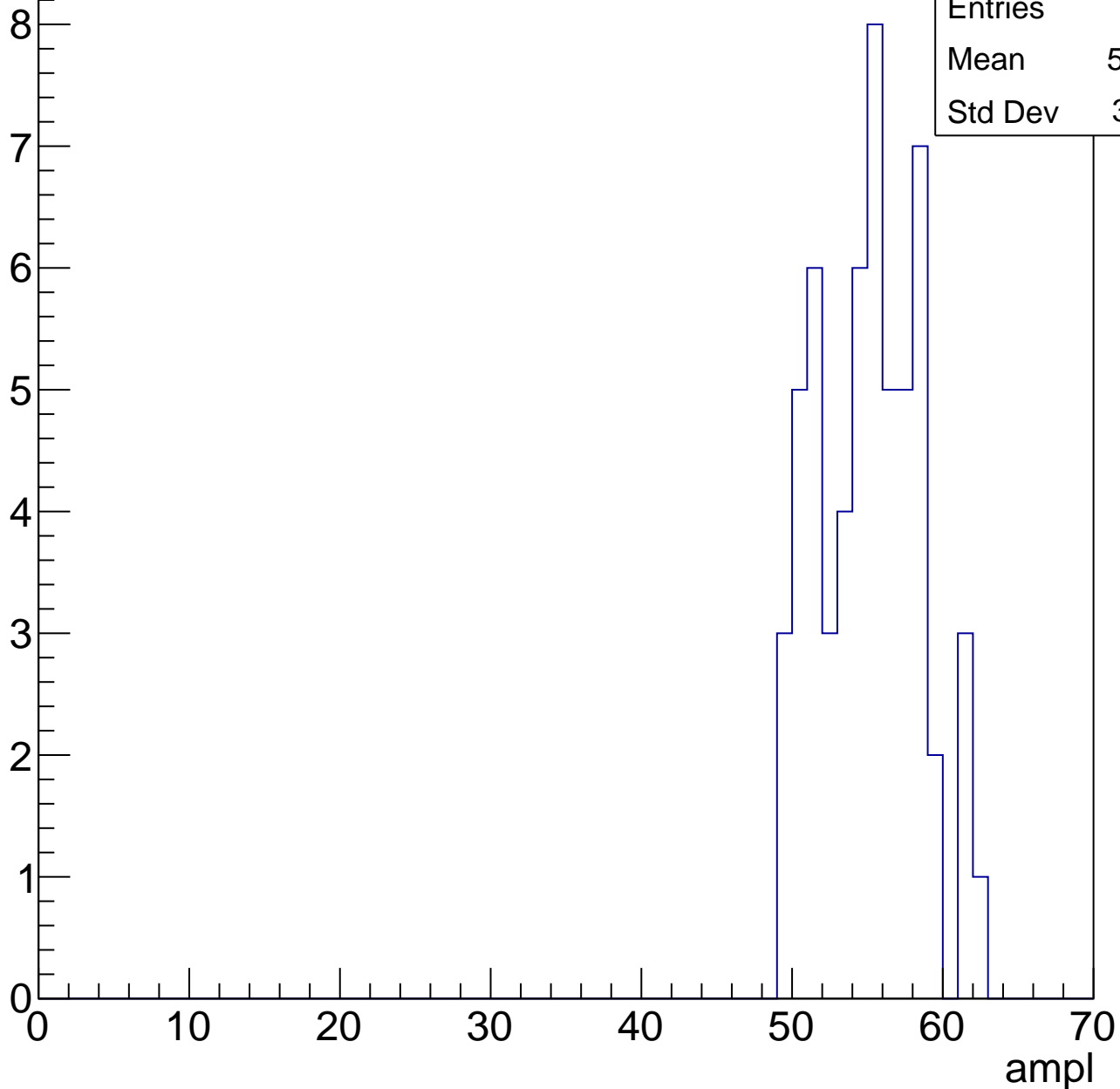
Entries	50
Mean	49.06
Std Dev	3.739



B0L000S, U2-ch86, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

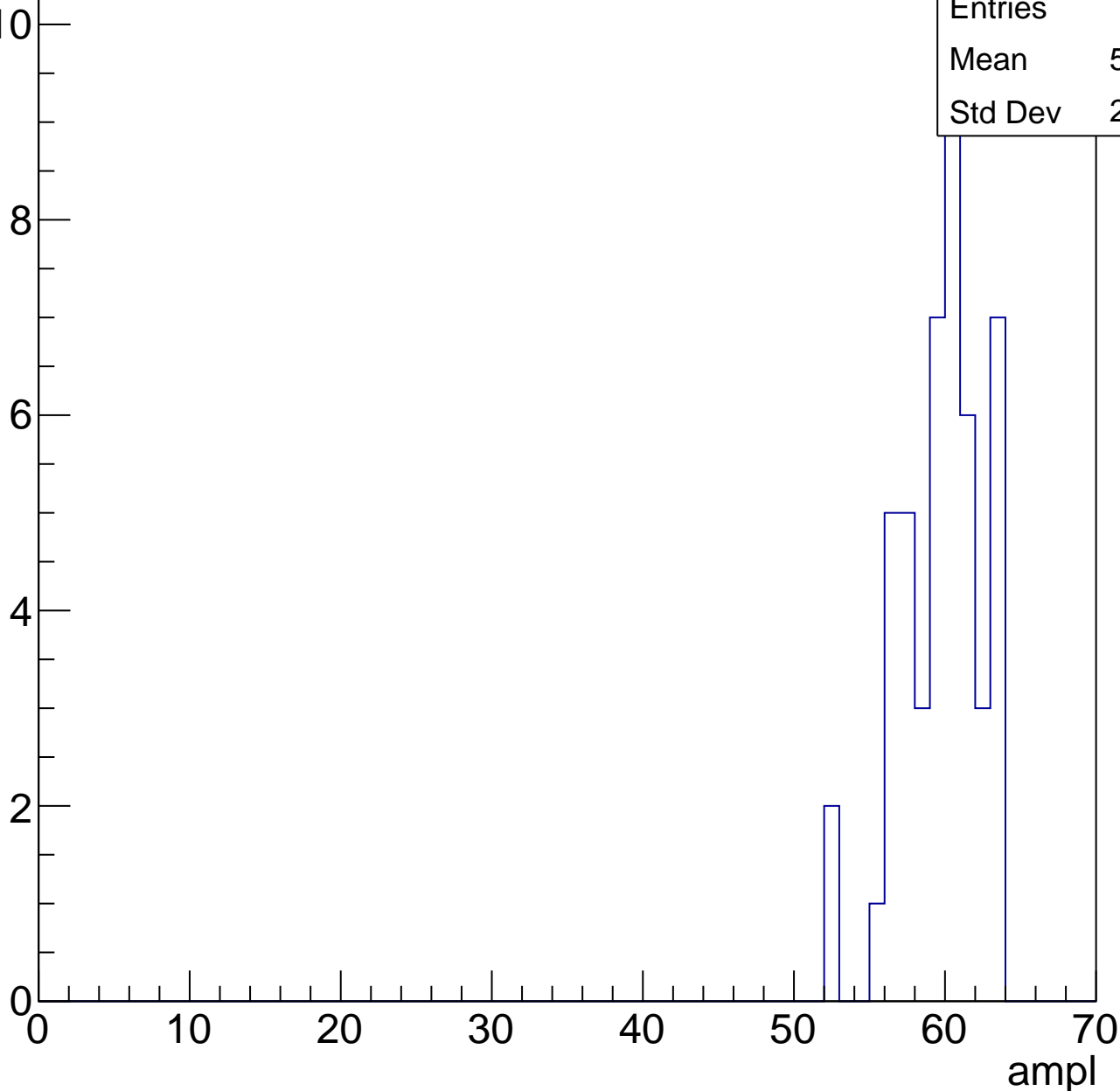


B0L000S, U2-ch86, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	59.27
Std Dev	2.679

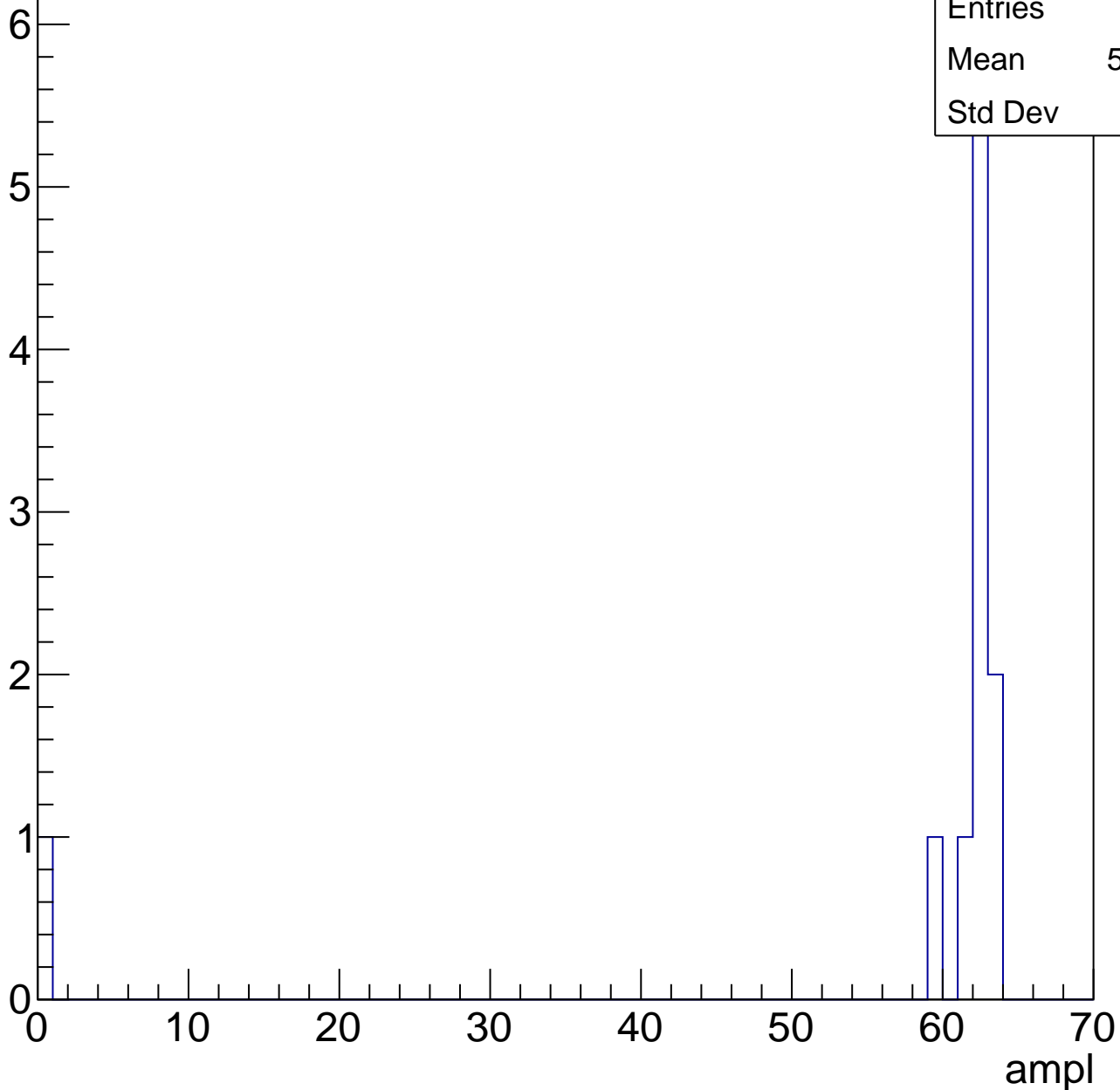


B0L000S, U2-ch86, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

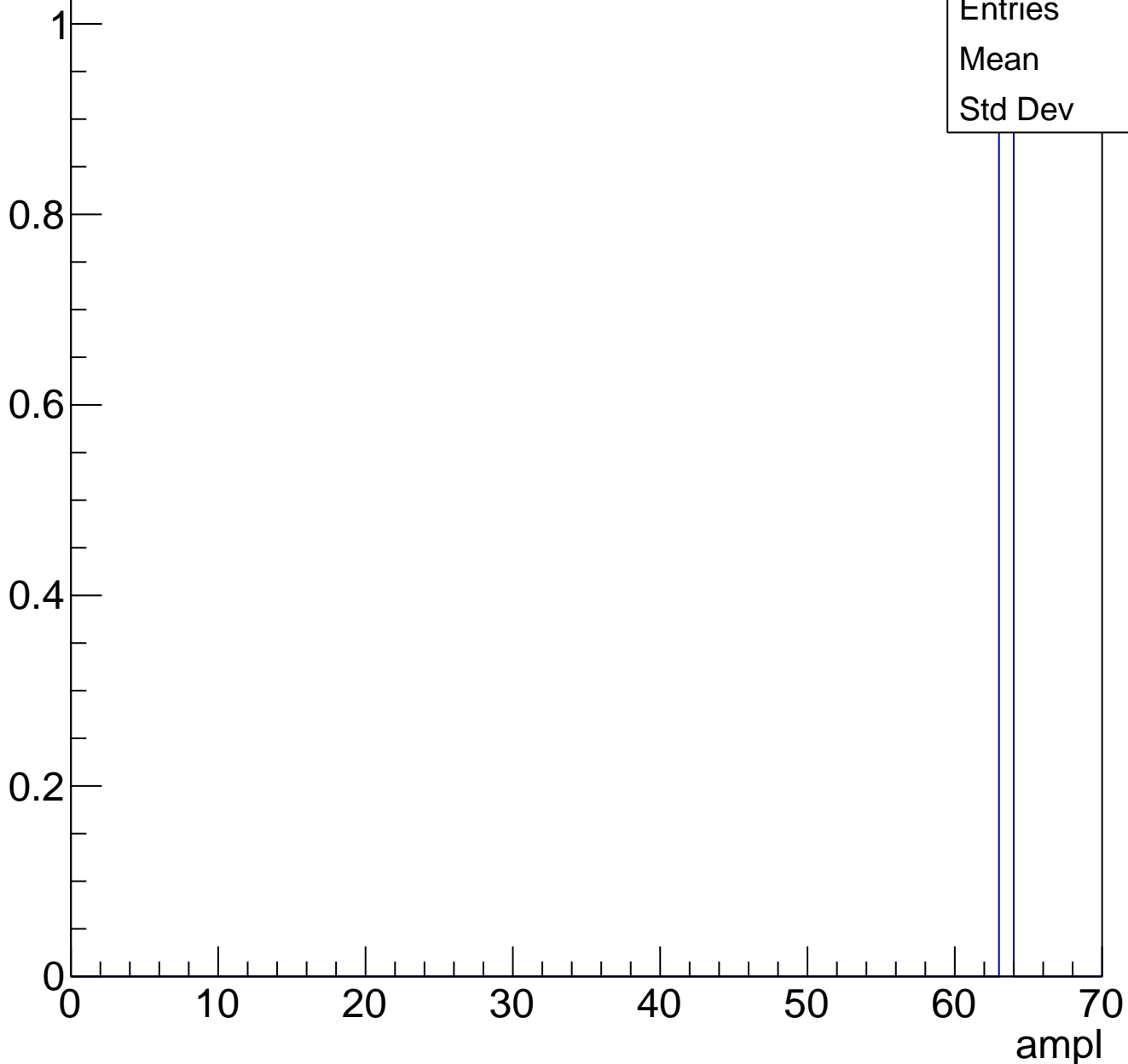
Entries	11
Mean	56.18
Std Dev	17.8



B0L000S, U2-ch86, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch87, adc0

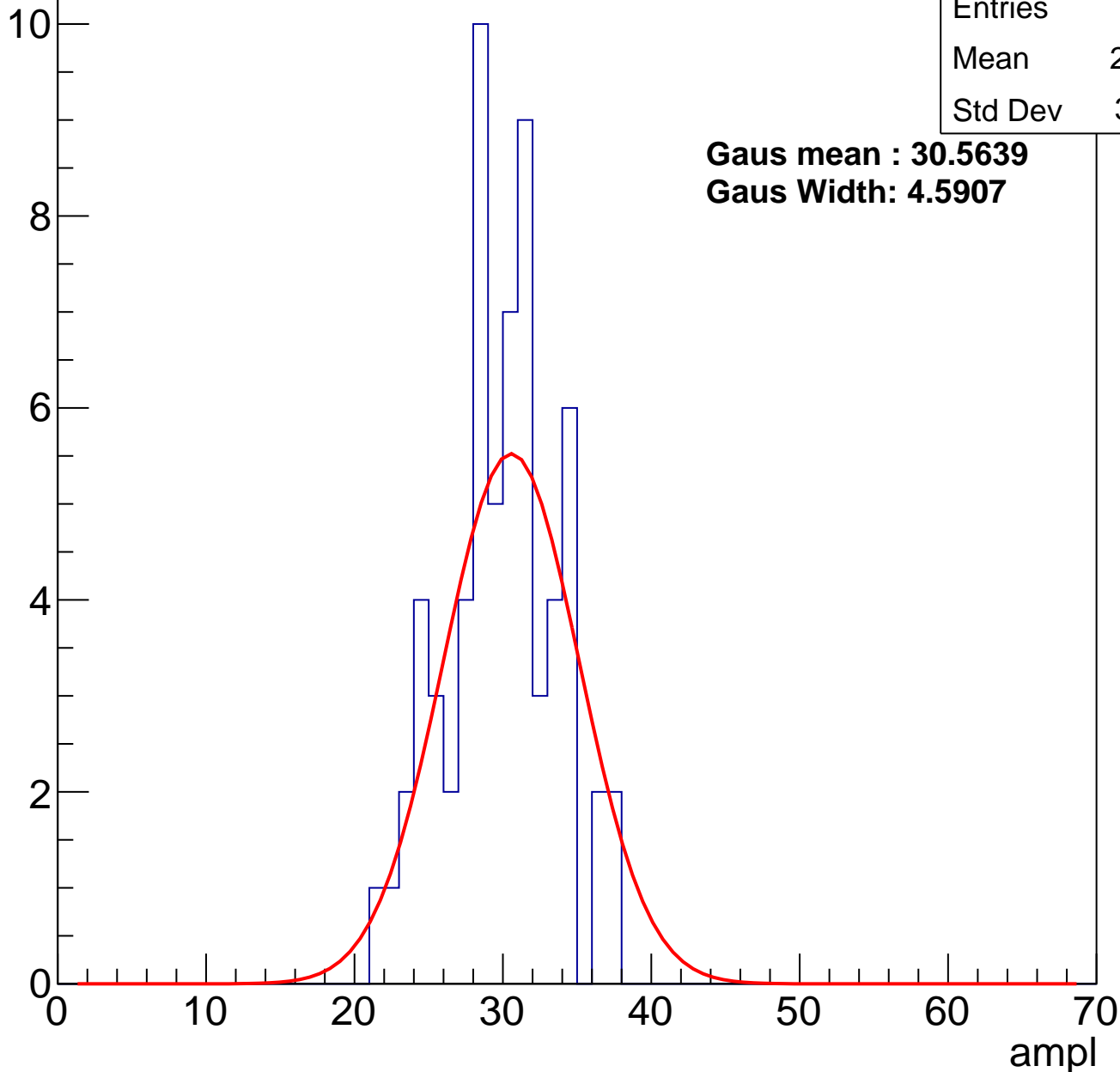
calib_packv5_042523_0143.root, FC#5, port B1

Entries	65
Mean	29.42
Std Dev	3.671

Gaus mean : 30.5639

Gaus Width: 4.5907

Entry



B0L000S, U2-ch87, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	72
Mean	36.46
Std Dev	3.419

Gaus mean : 37.2839

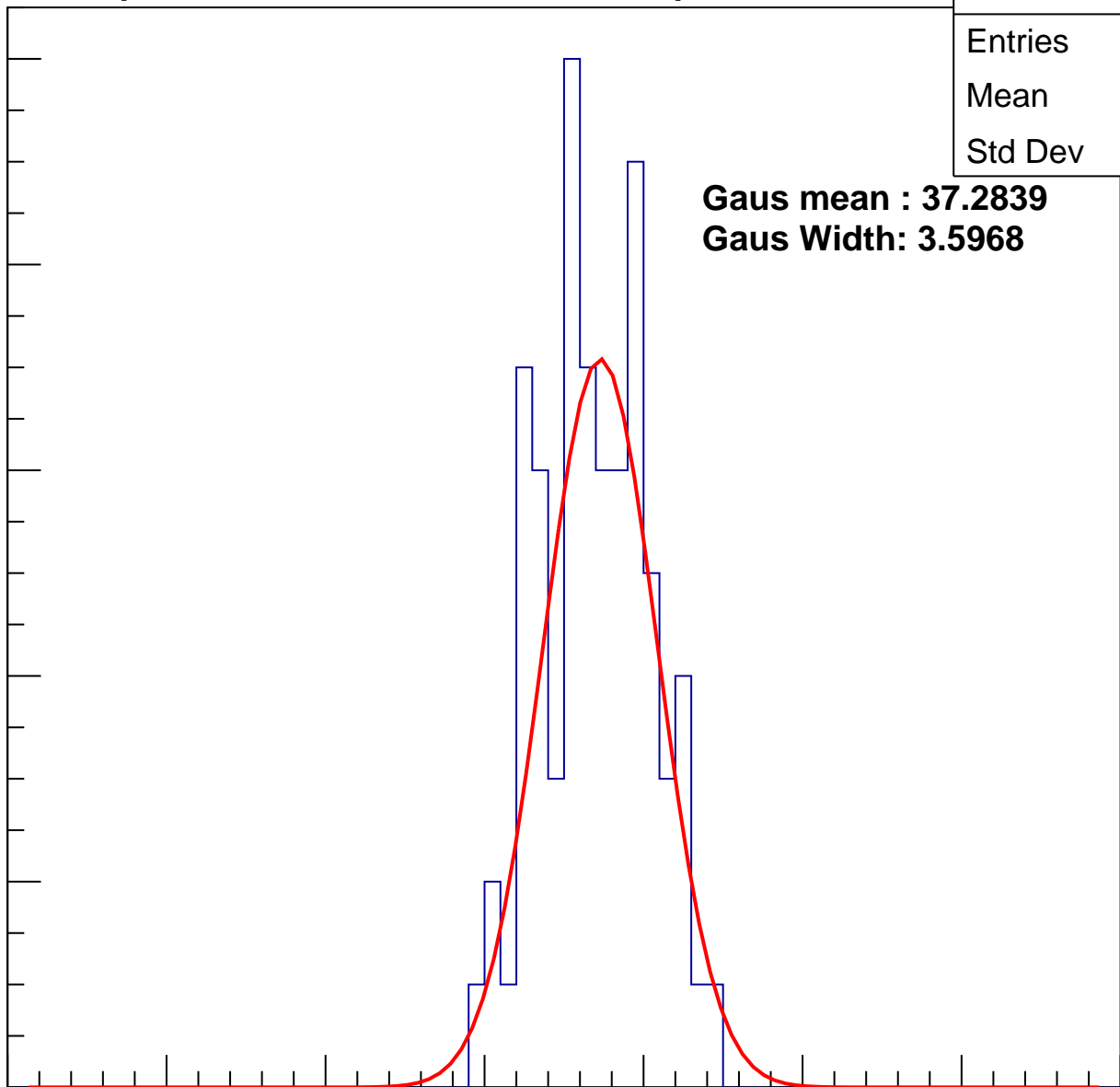
Gaus Width: 3.5968

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch87, adc2

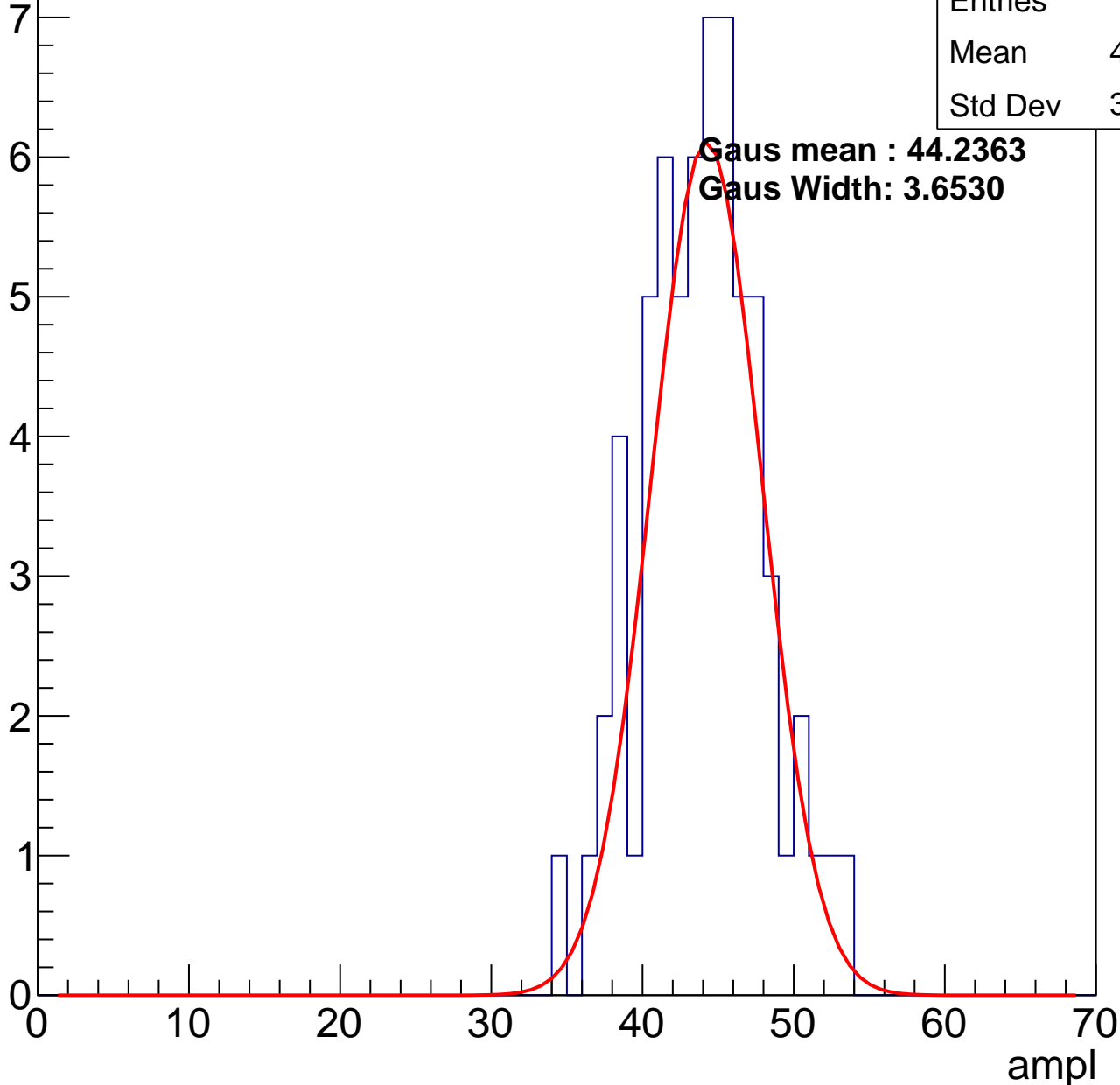
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.53
Std Dev	3.929

Gaus mean : 44.2363

Gaus Width: 3.6530

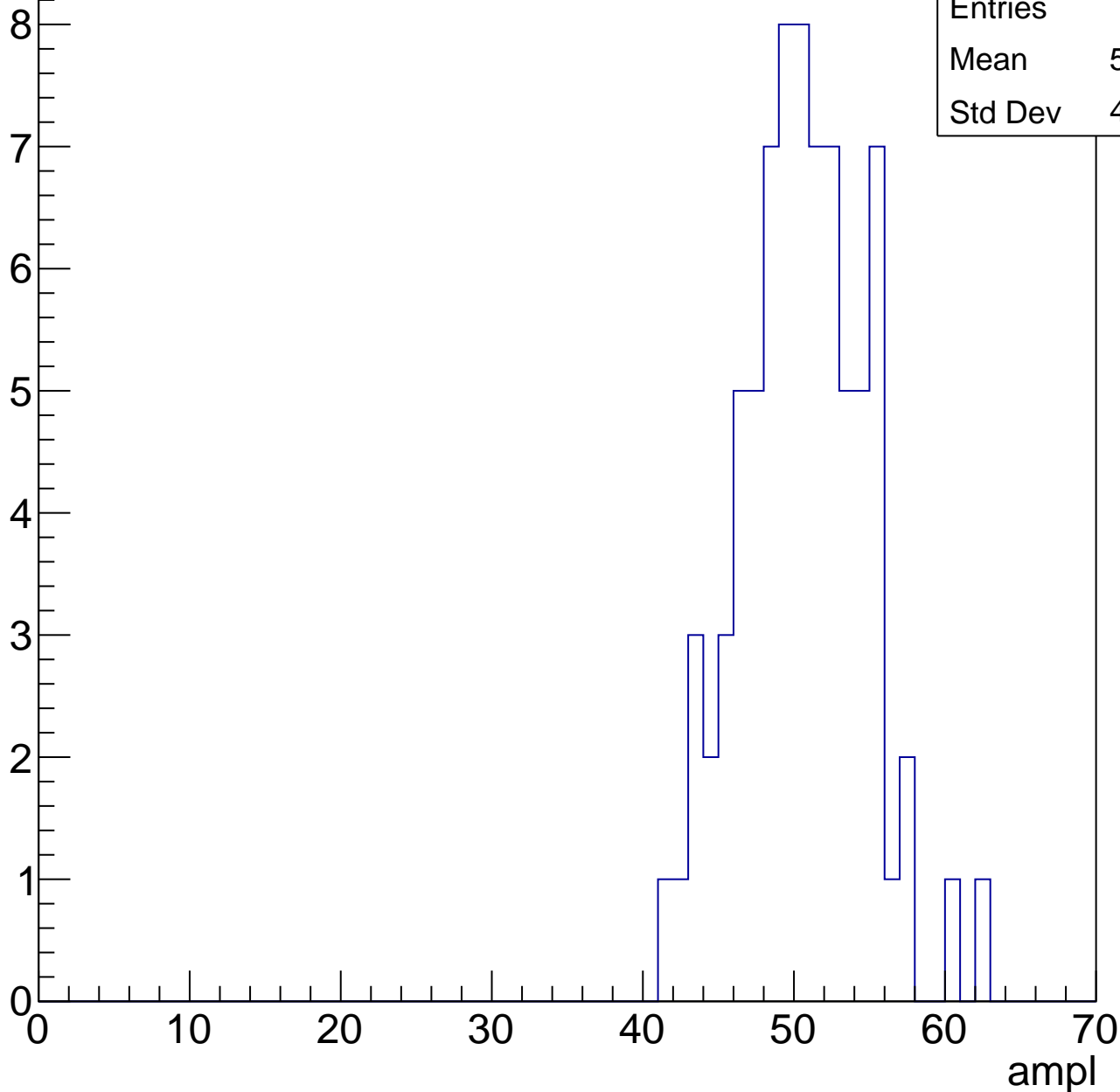


B0L000S, U2-ch87, adc3

calib_packv5_042523_0143.root, FC#5, port B1

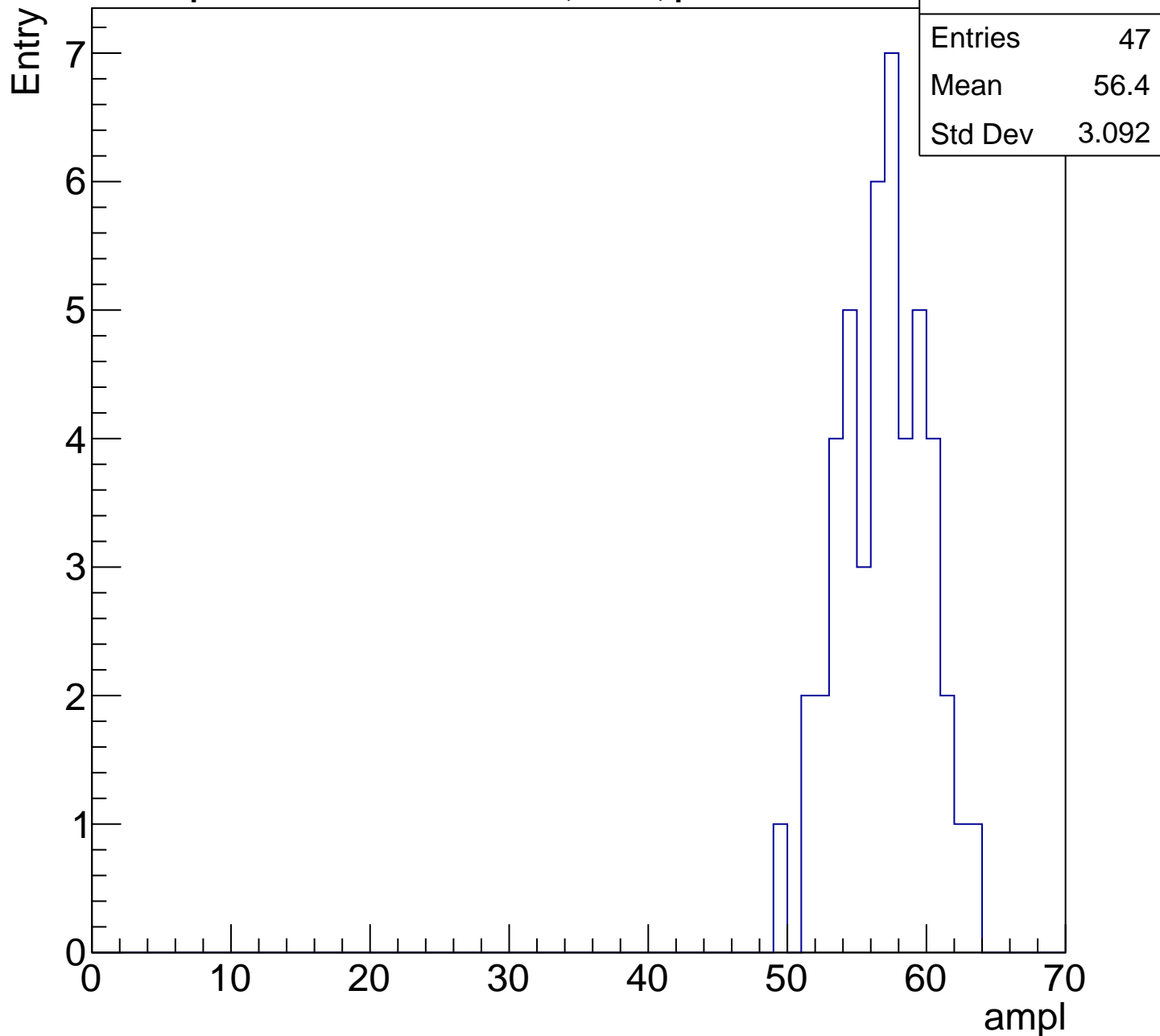
Entry

Entries	79
Mean	50.14
Std Dev	4.074



B0L000S, U2-ch87, adc4

calib_packv5_042523_0143.root, FC#5, port B1

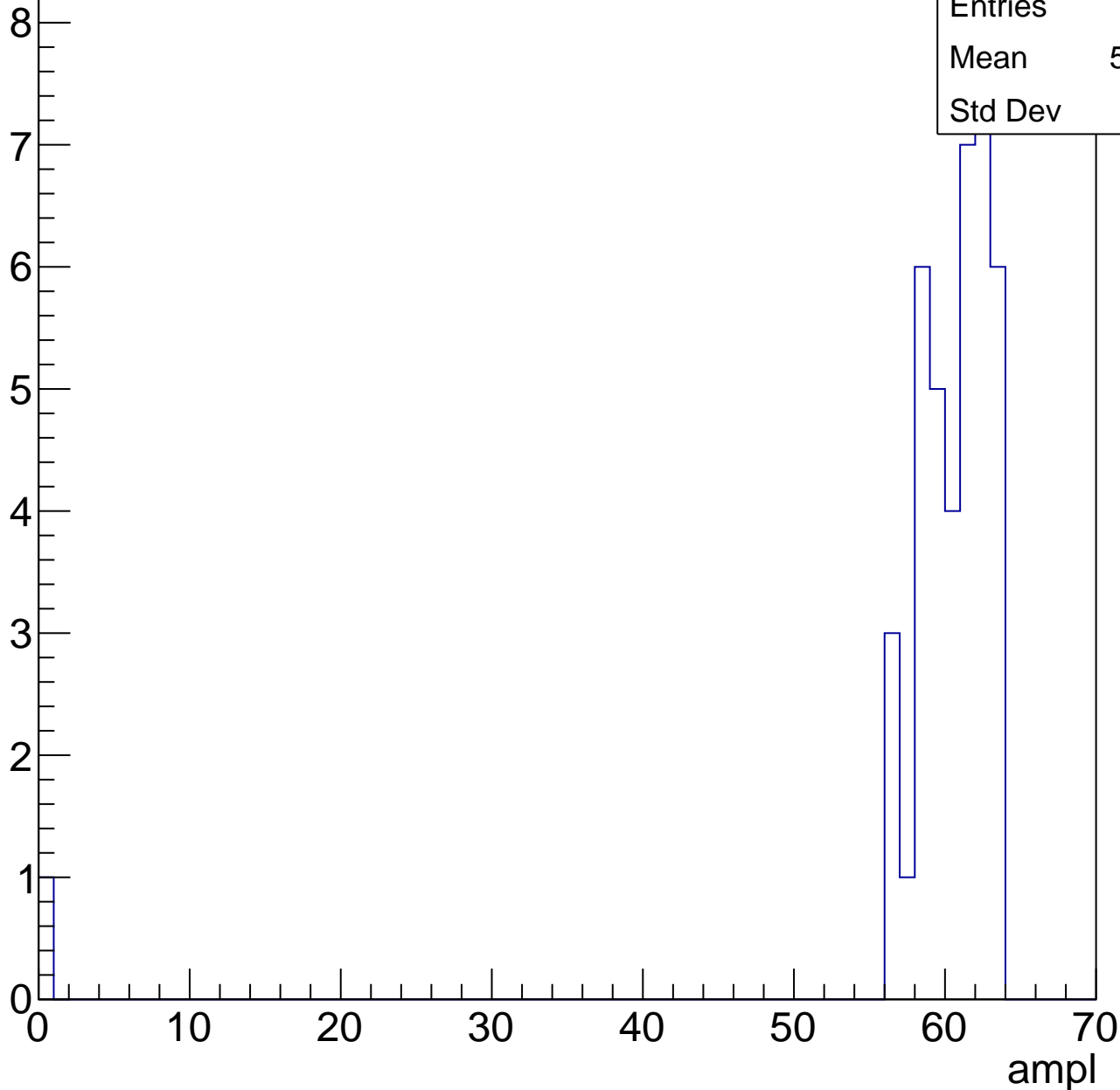


B0L000S, U2-ch87, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.76
Std Dev	9.52



B0L000S, U2-ch87, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch87, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch88, adc0

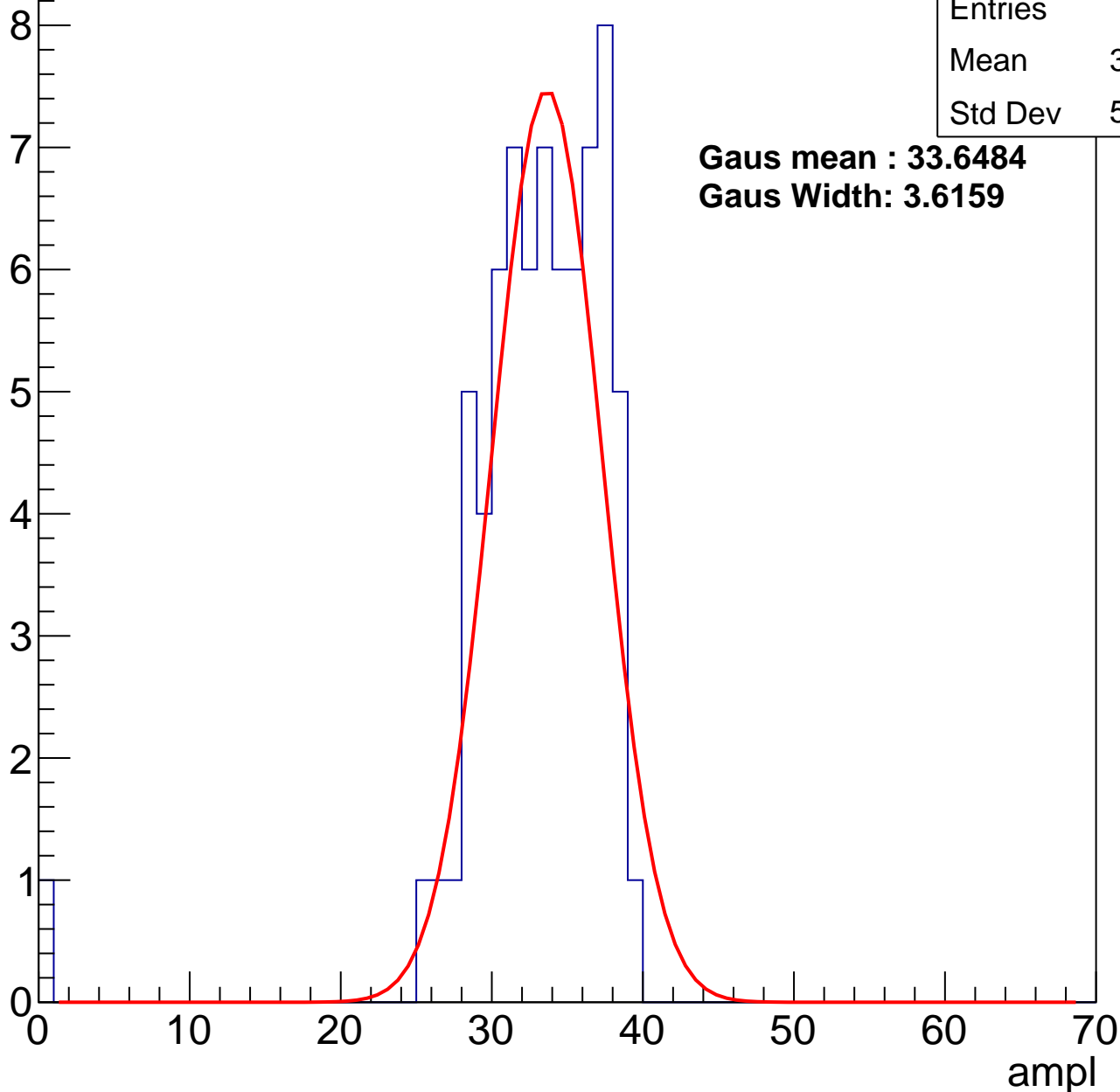
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	32.57
Std Dev	5.115

Gaus mean : 33.6484

Gaus Width: 3.6159



B0L000S, U2-ch88, adc1

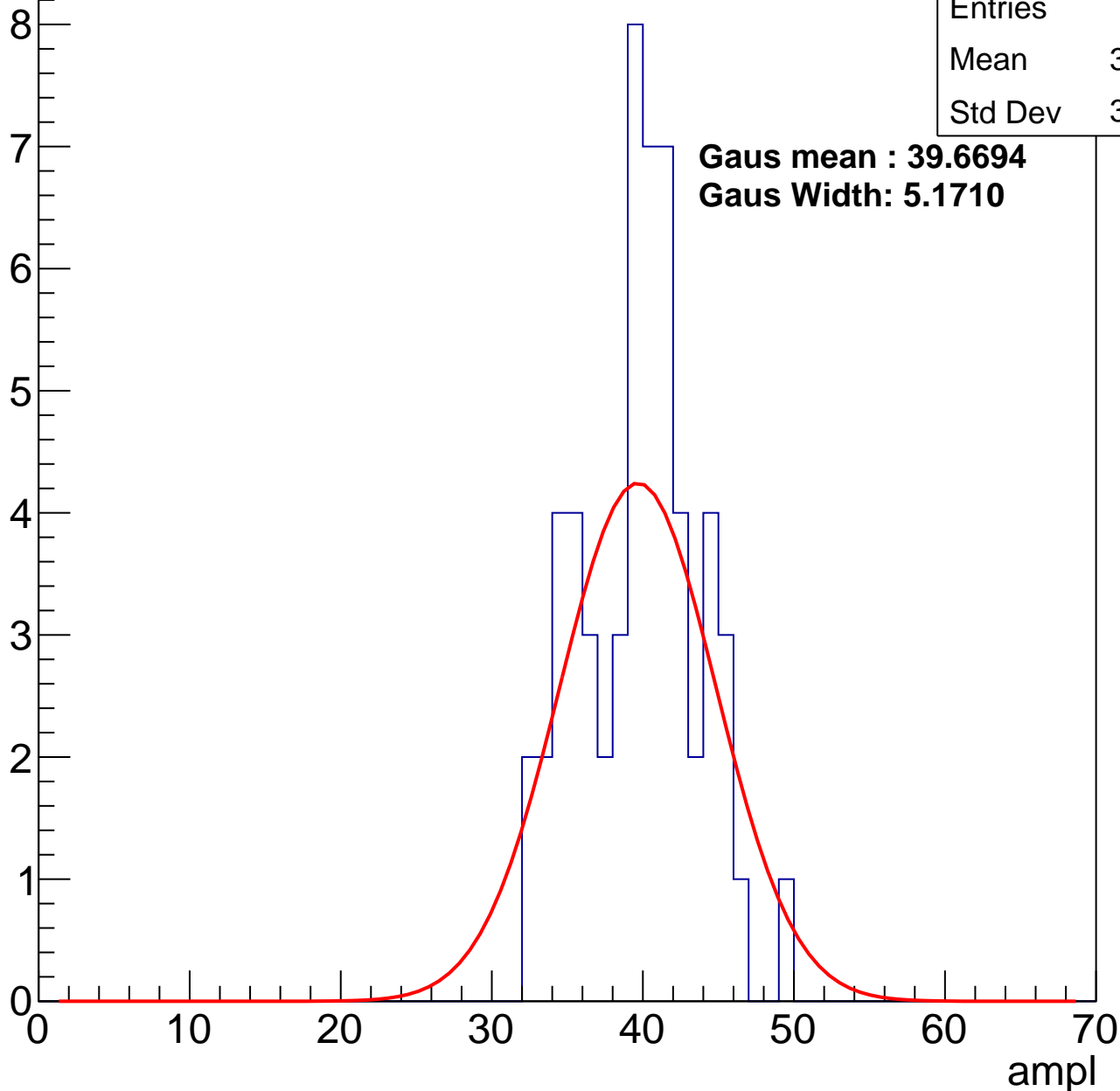
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	39.32
Std Dev	3.807

Gaus mean : 39.6694

Gaus Width: 5.1710

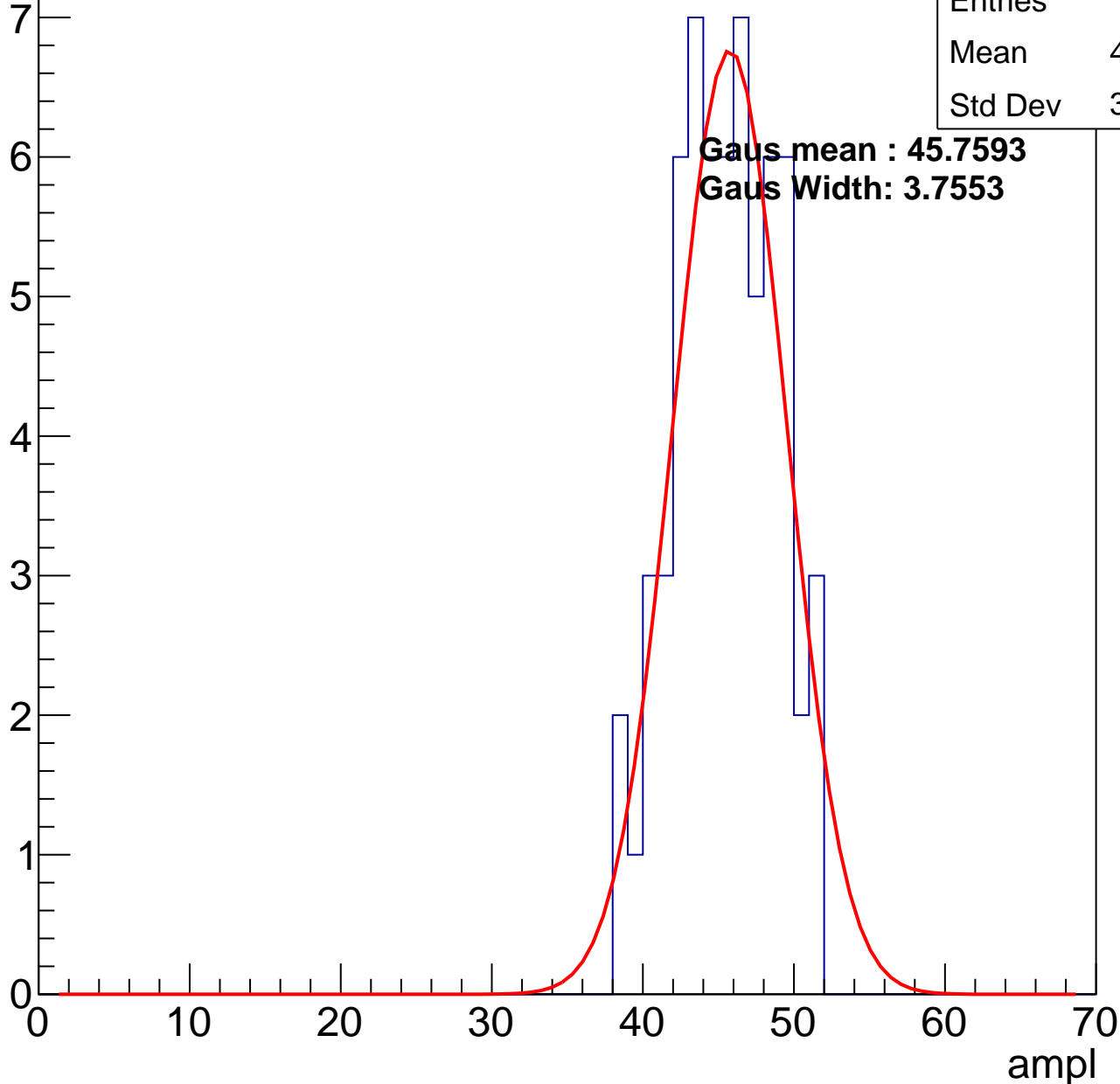


B0L000S, U2-ch88, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	45.03
Std Dev	3.285

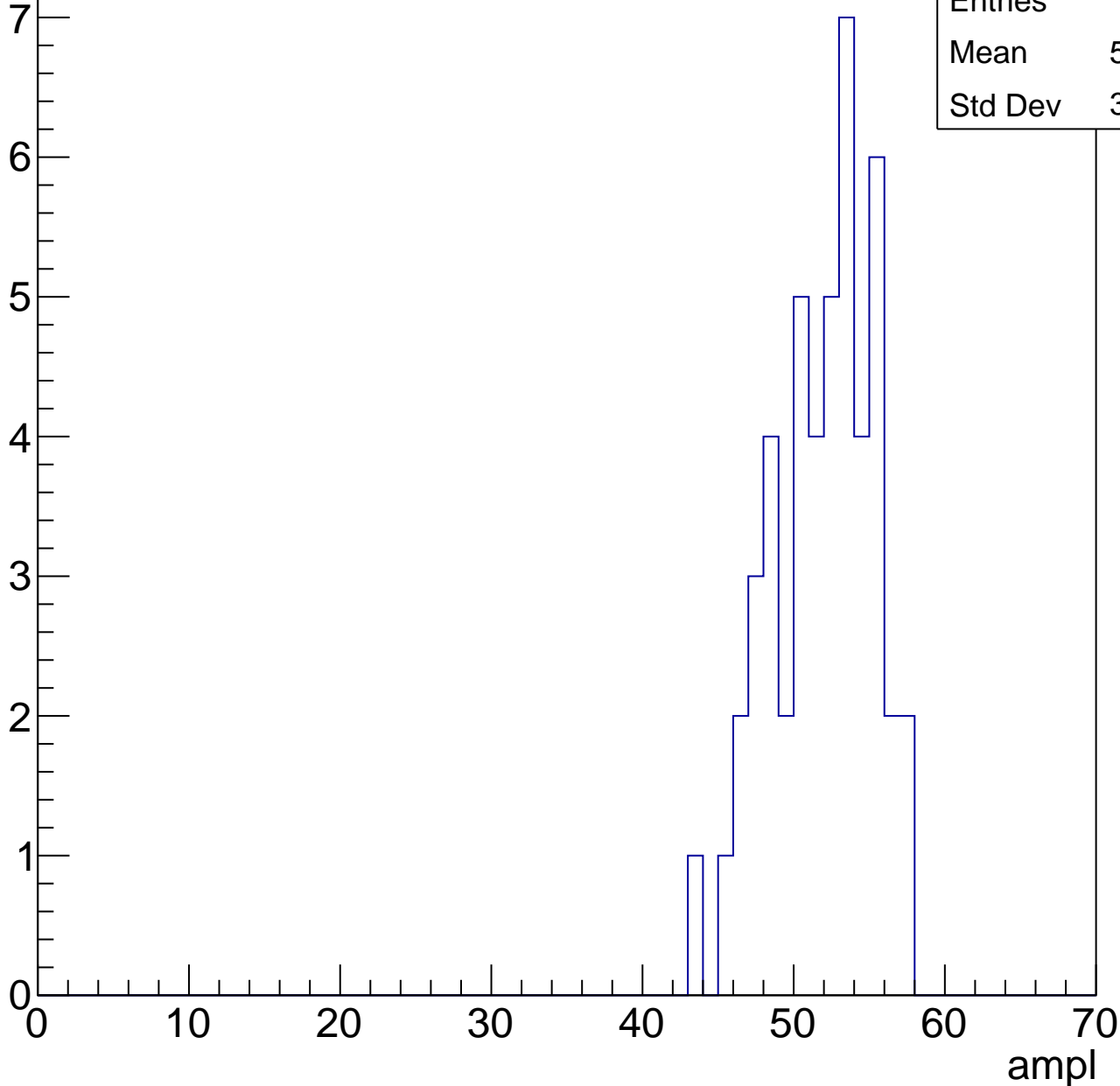


B0L000S, U2-ch88, adc3

calib_packv5_042523_0143.root, FC#5, port B1

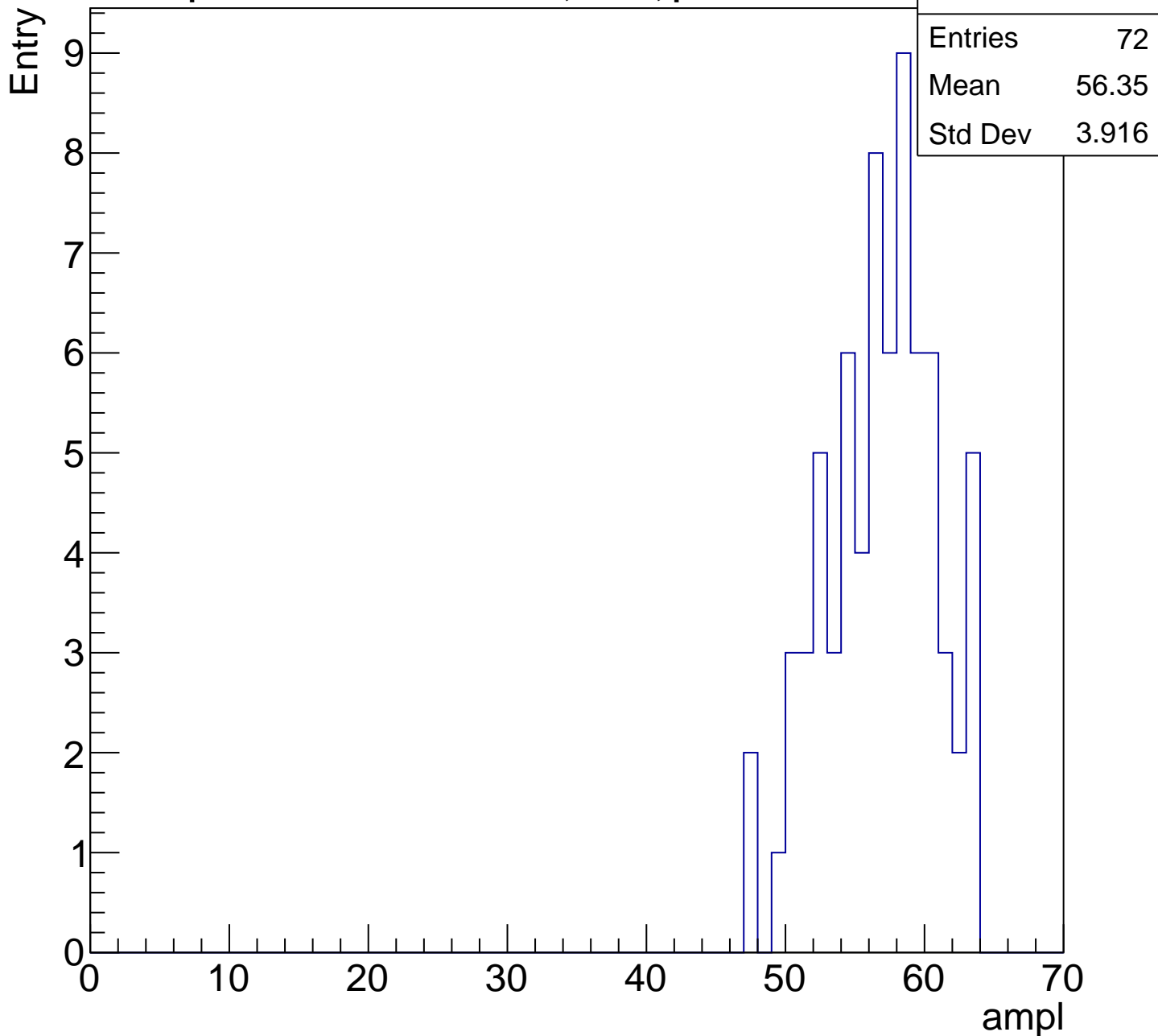
Entry

Entries	48
Mean	51.42
Std Dev	3.309



B0L000S, U2-ch88, adc4

calib_packv5_042523_0143.root, FC#5, port B1

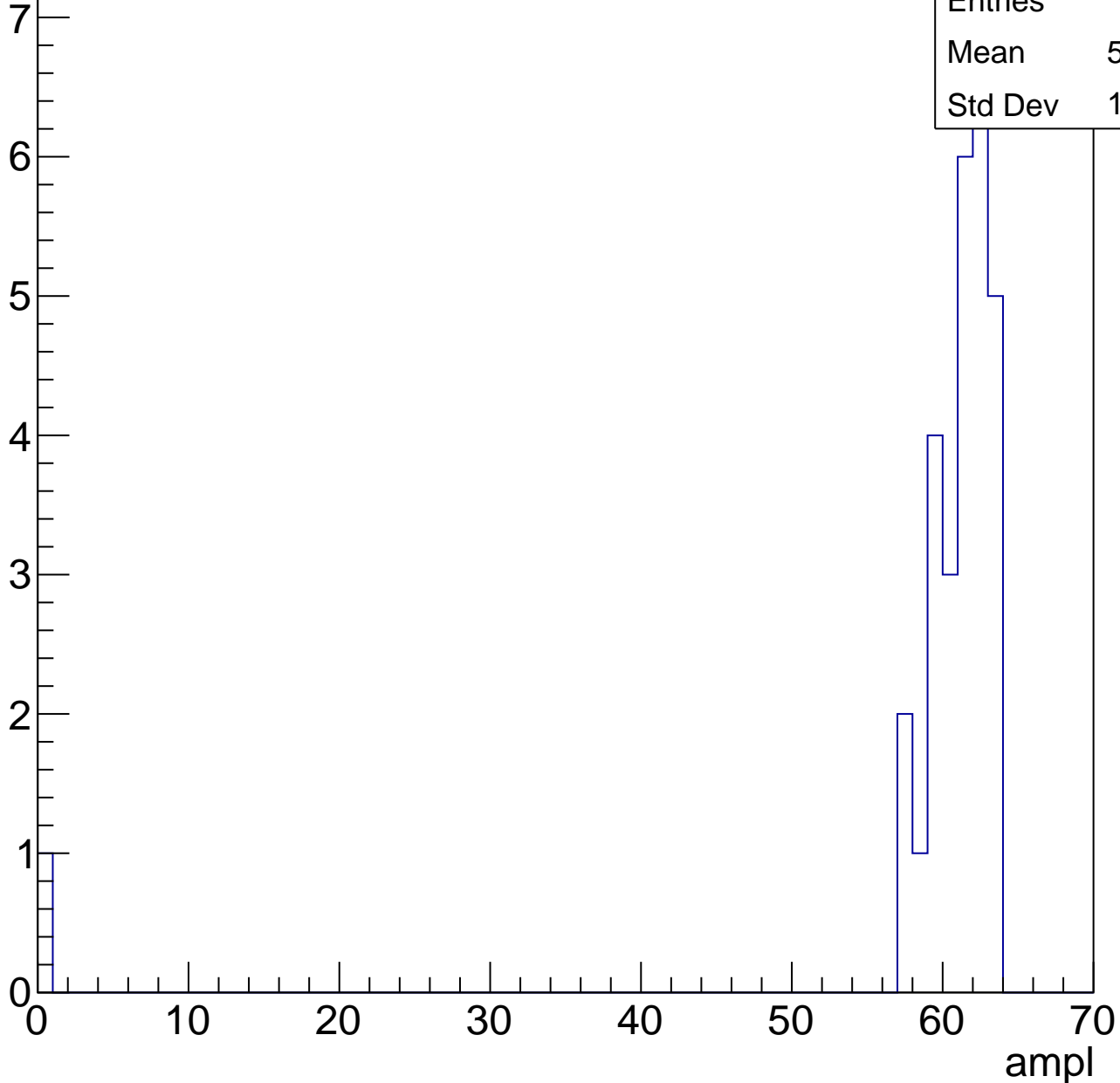


B0L000S, U2-ch88, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

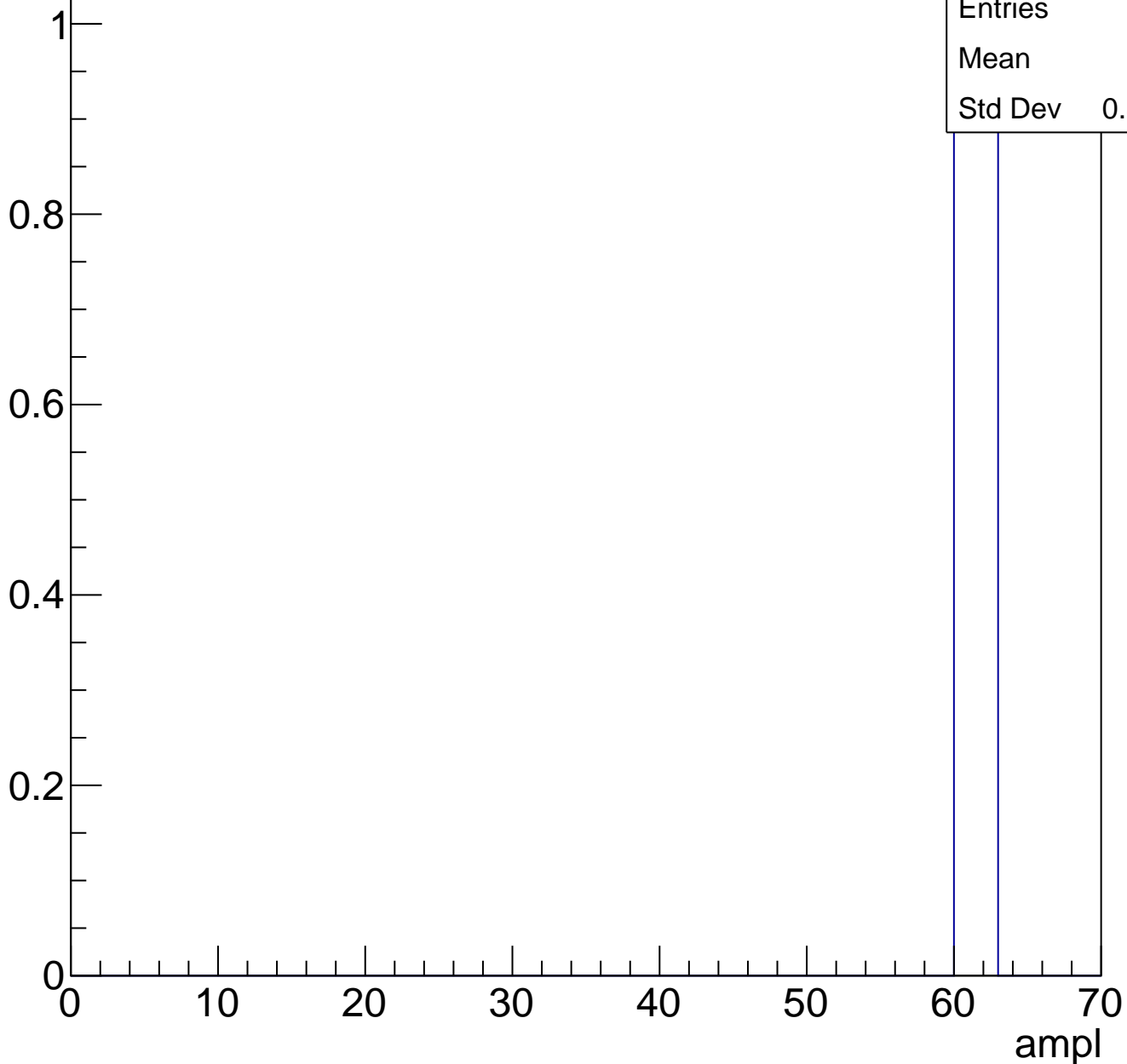
Entries	29
Mean	58.72
Std Dev	11.23



B0L000S, U2-ch88, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch88, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch89, adc0

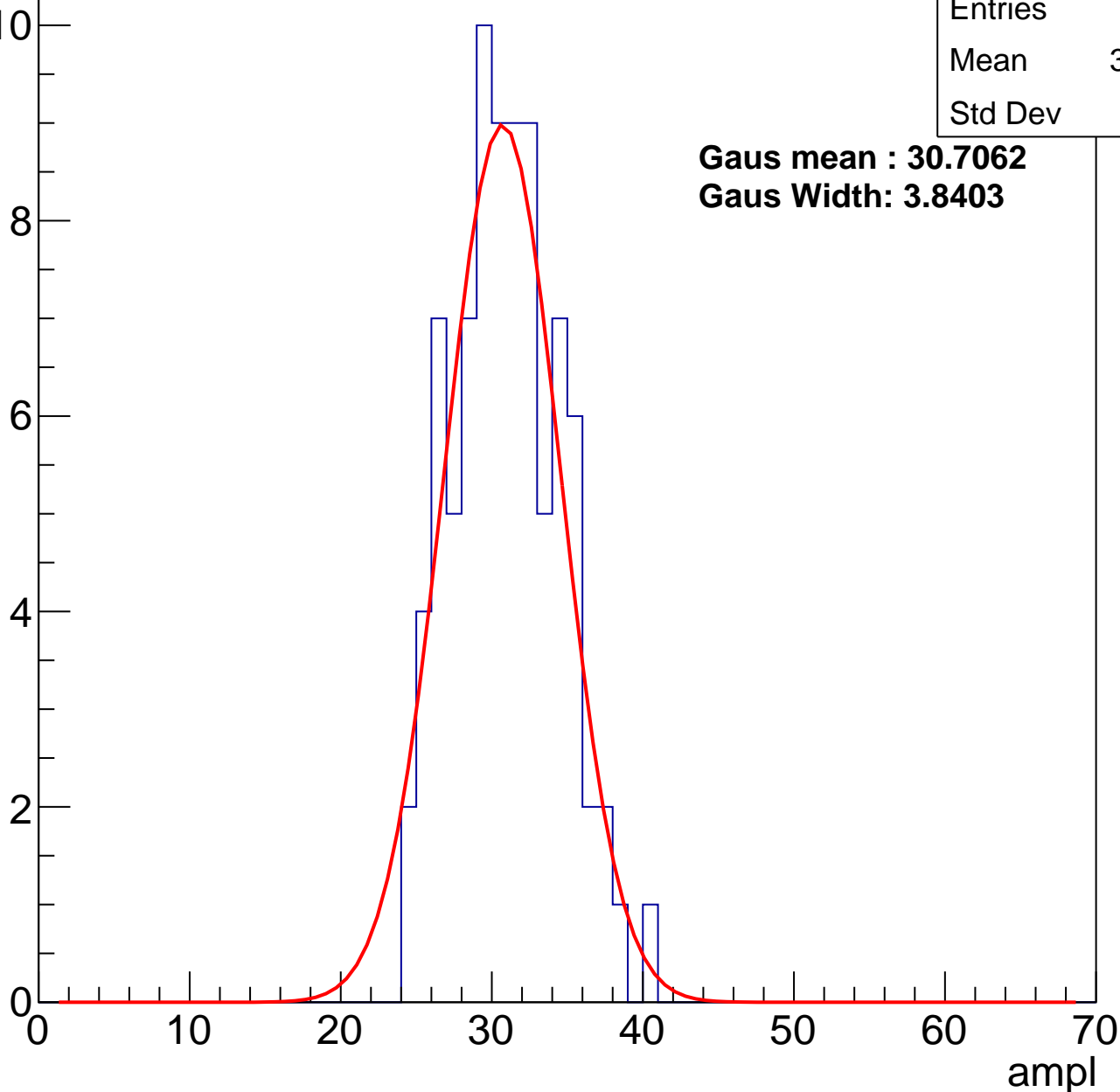
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	86
Mean	30.52
Std Dev	3.46

Gaus mean : 30.7062

Gaus Width: 3.8403



B0L000S, U2-ch89, adc1

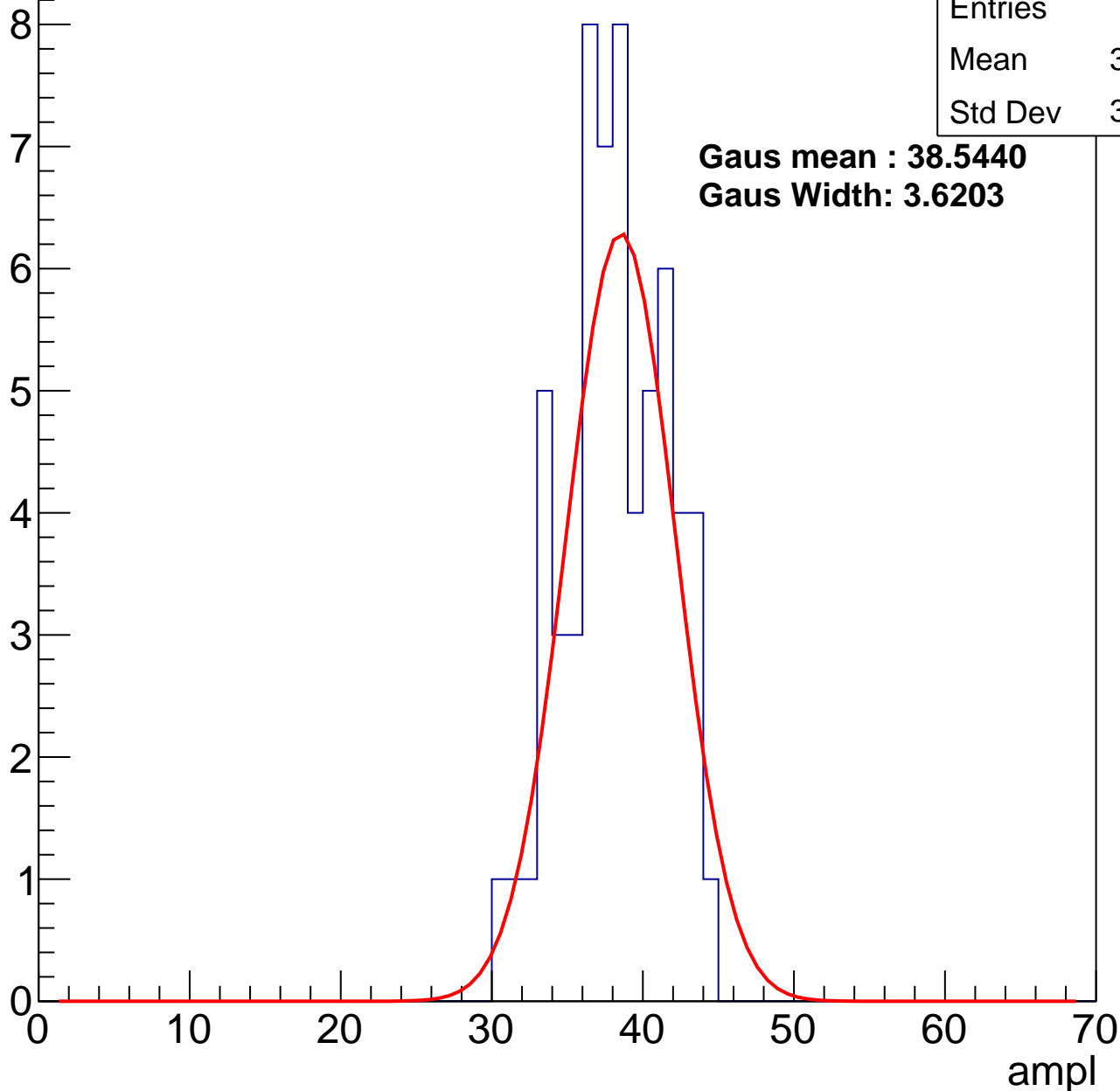
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.74
Std Dev	3.294

Gaus mean : 38.5440

Gaus Width: 3.6203



B0L000S, U2-ch89, adc2

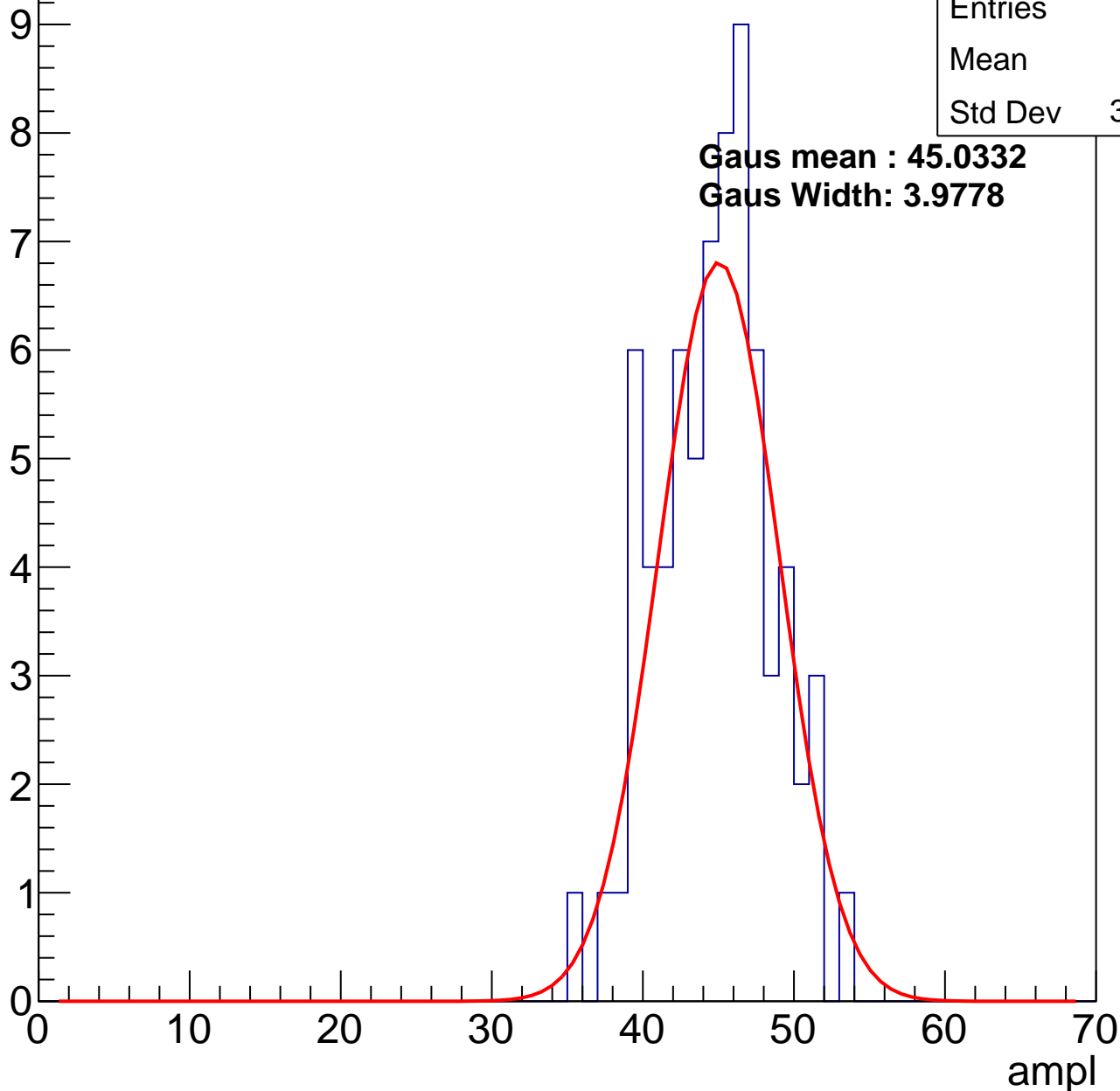
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	44.3
Std Dev	3.724

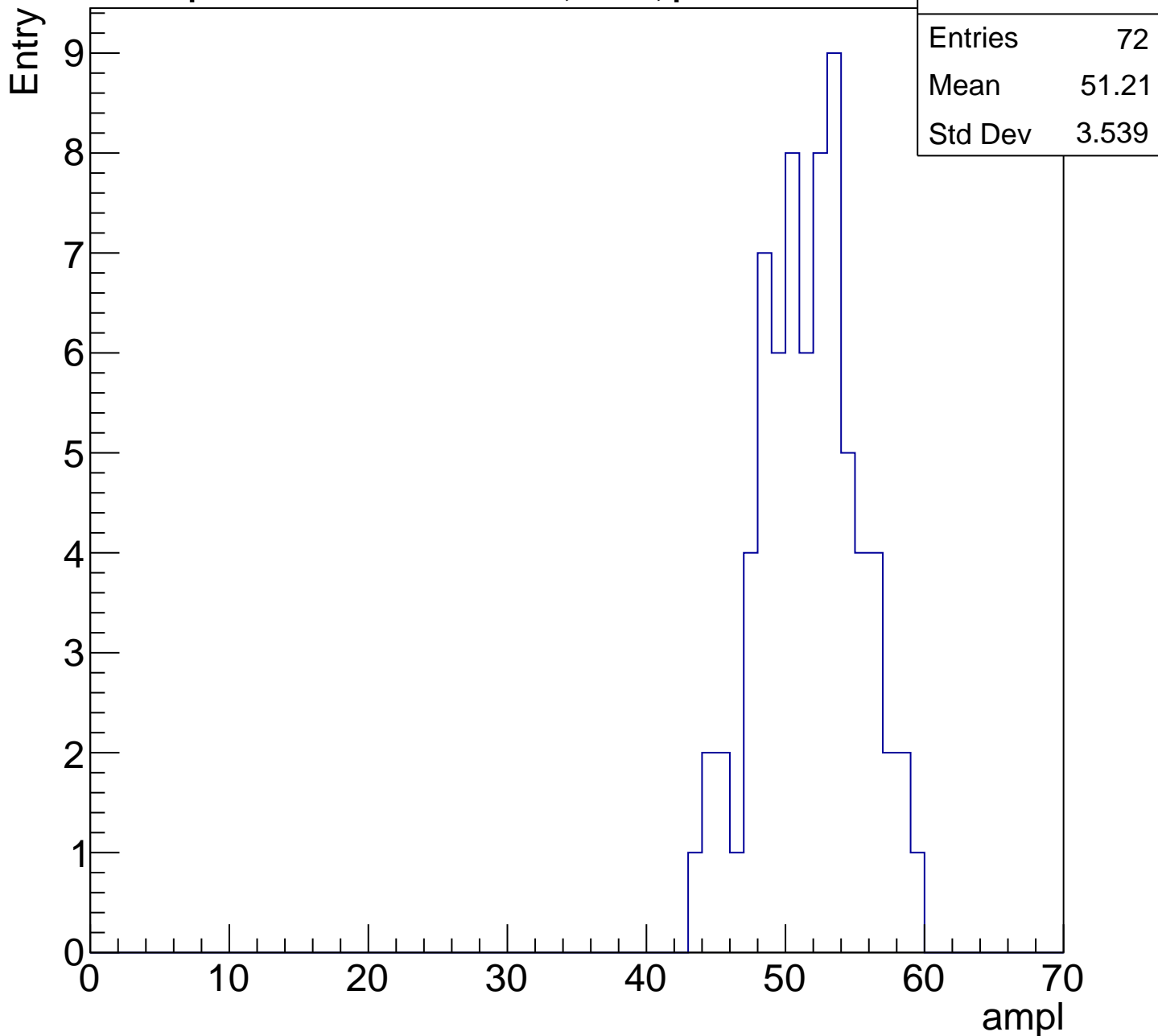
Gaus mean : 45.0332

Gaus Width: 3.9778



B0L000S, U2-ch89, adc3

calib_packv5_042523_0143.root, FC#5, port B1

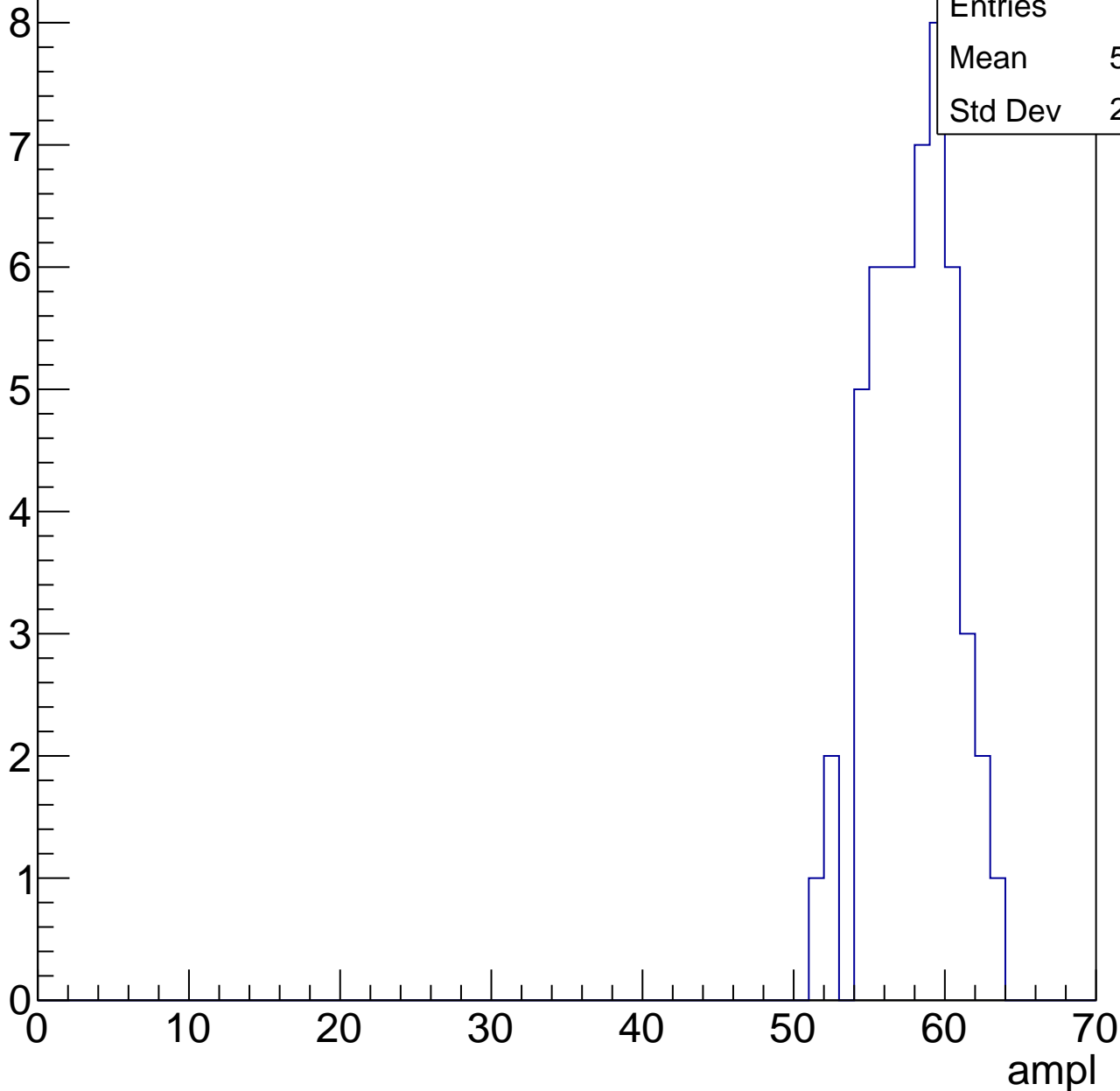


B0L000S, U2-ch89, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.38
Std Dev	2.679

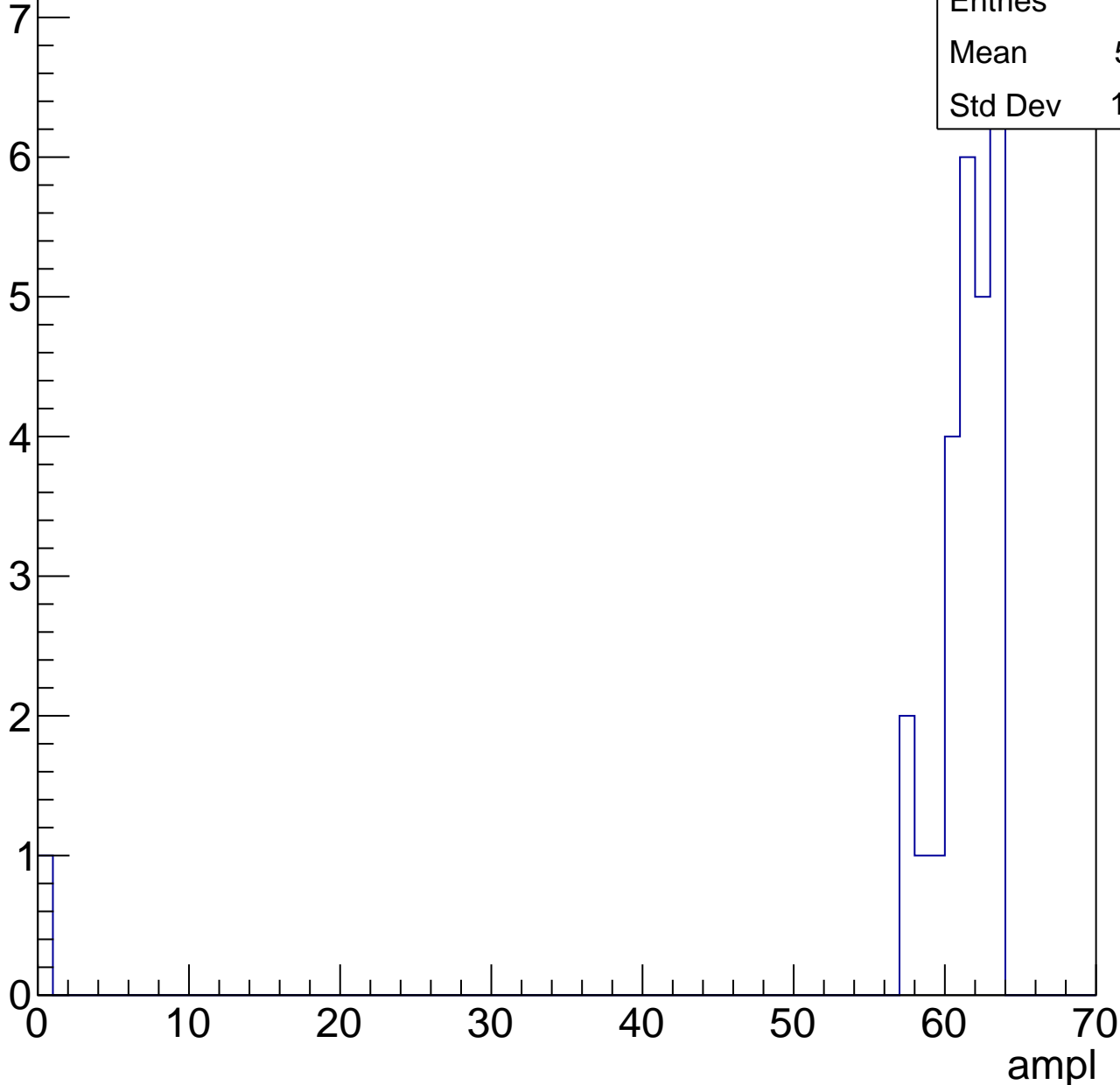


B0L000S, U2-ch89, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

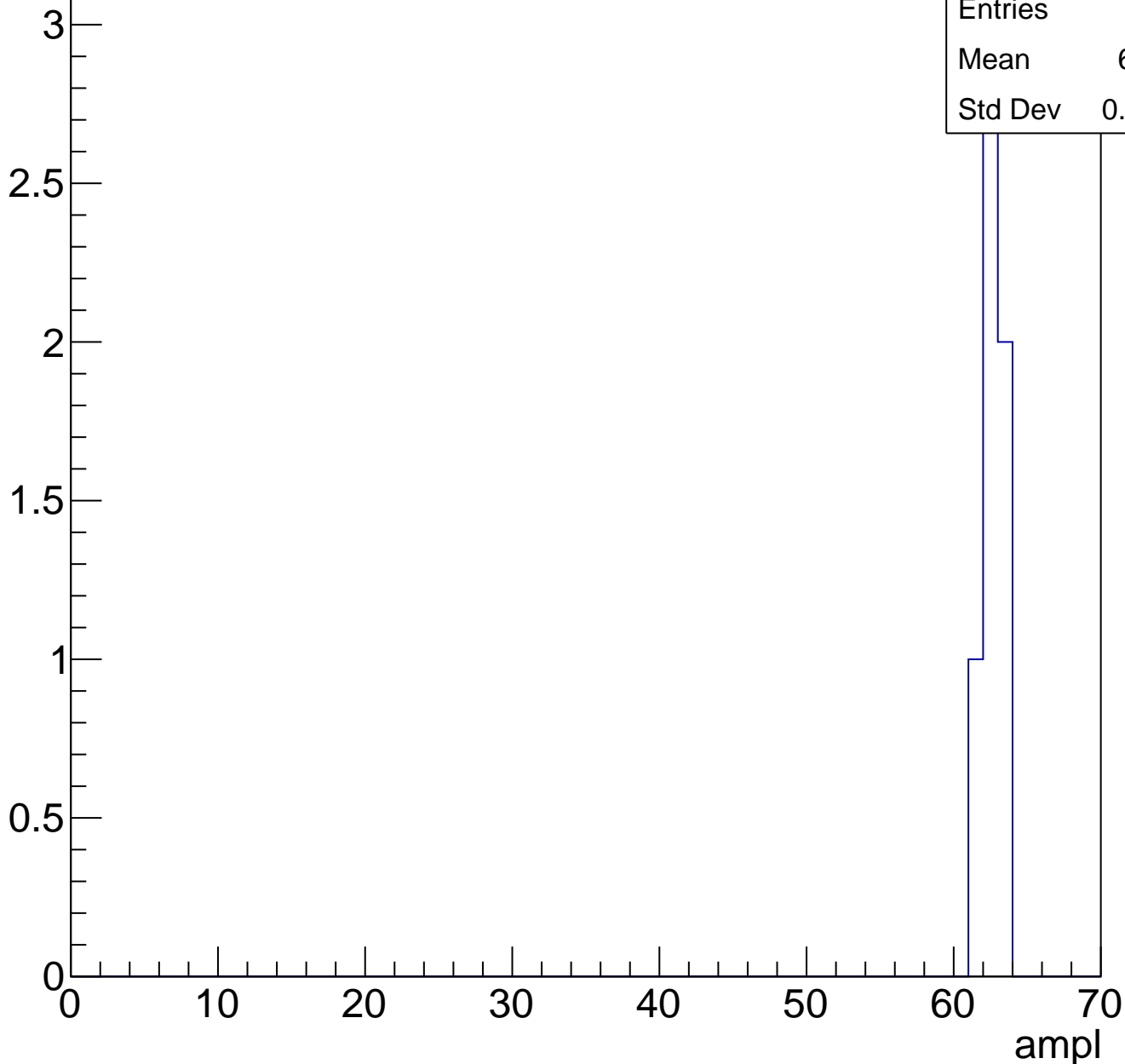
Entries	27
Mean	58.81
Std Dev	11.67



B0L000S, U2-ch89, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch89, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch90, adc0

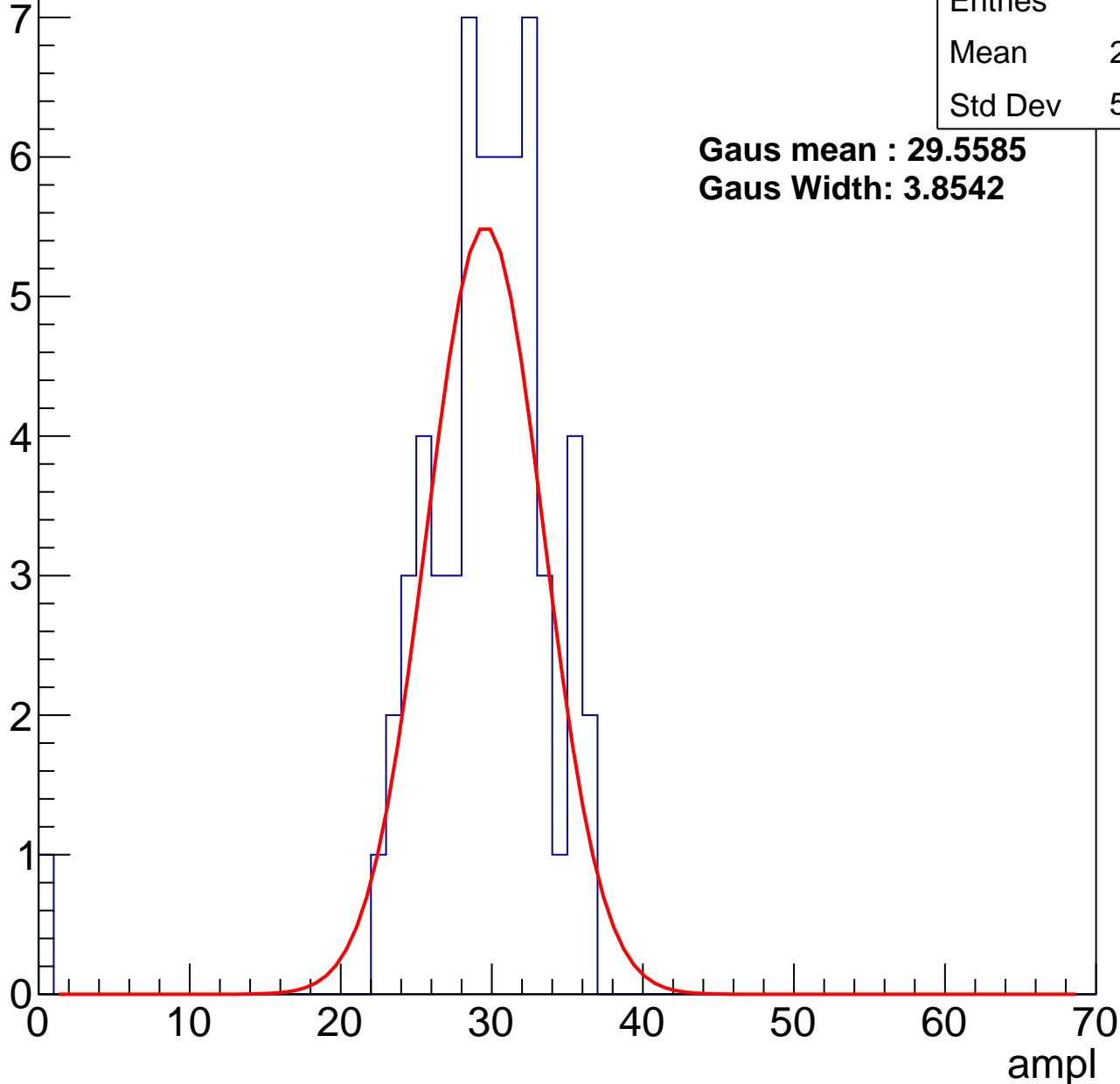
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	28.88
Std Dev	5.132

Gaus mean : 29.5585

Gaus Width: 3.8542



B0L000S, U2-ch90, adc1

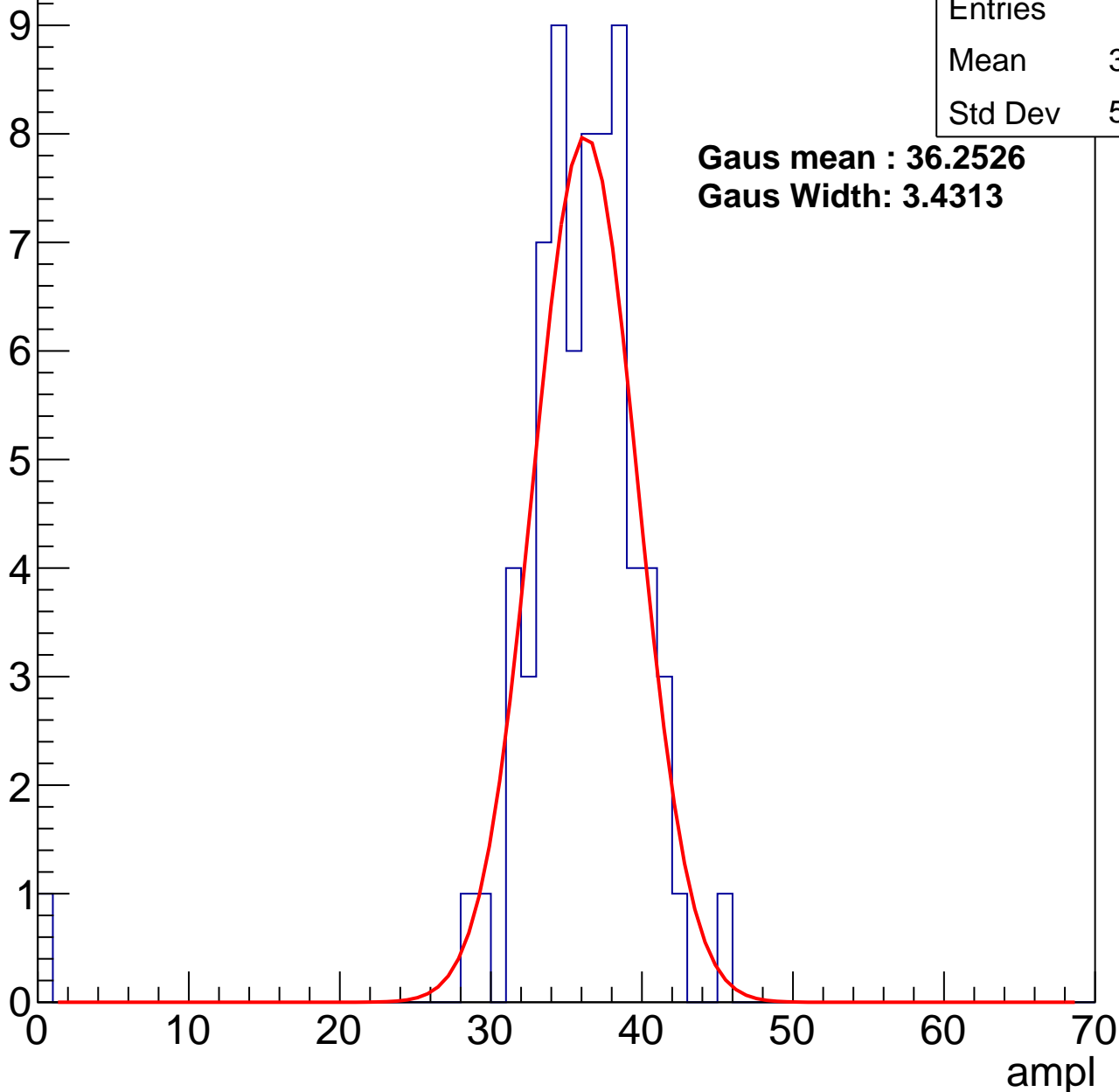
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.37
Std Dev	5.305

Gaus mean : 36.2526

Gaus Width: 3.4313



B0L000S, U2-ch90, adc2

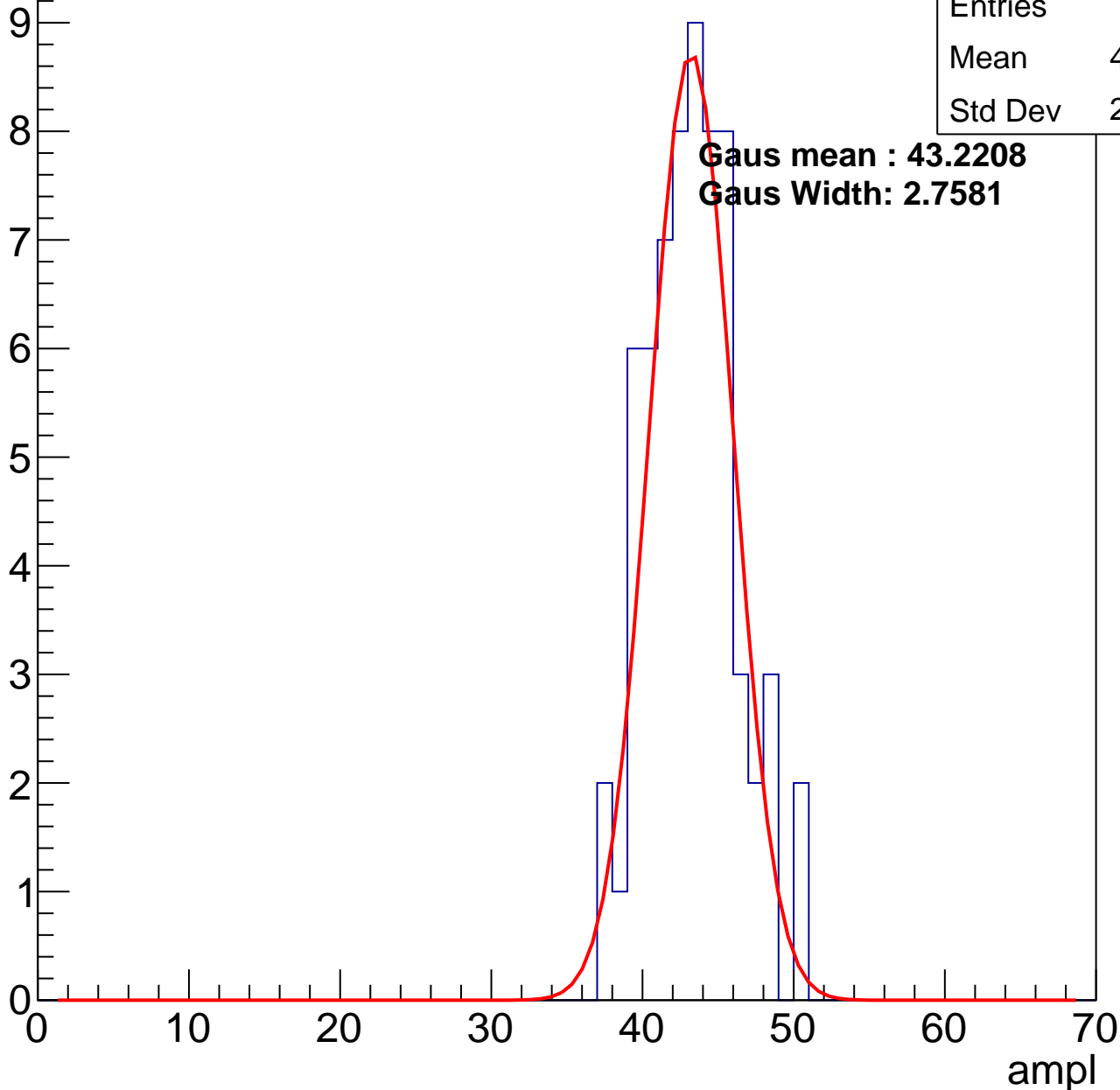
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.83
Std Dev	2.917

Gaus mean : 43.2208

Gaus Width: 2.7581

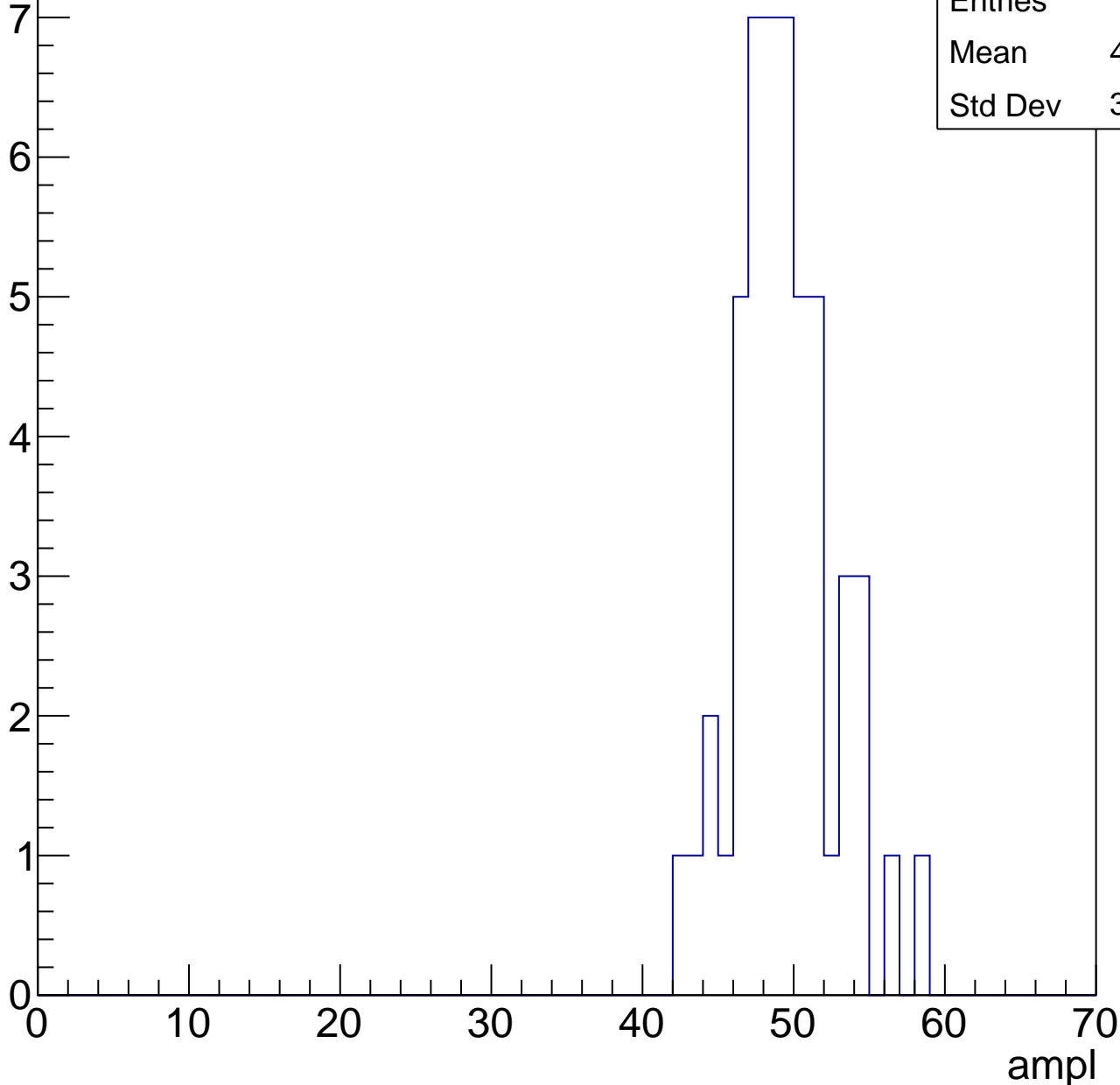


B0L000S, U2-ch90, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	50
Mean	48.96
Std Dev	3.218

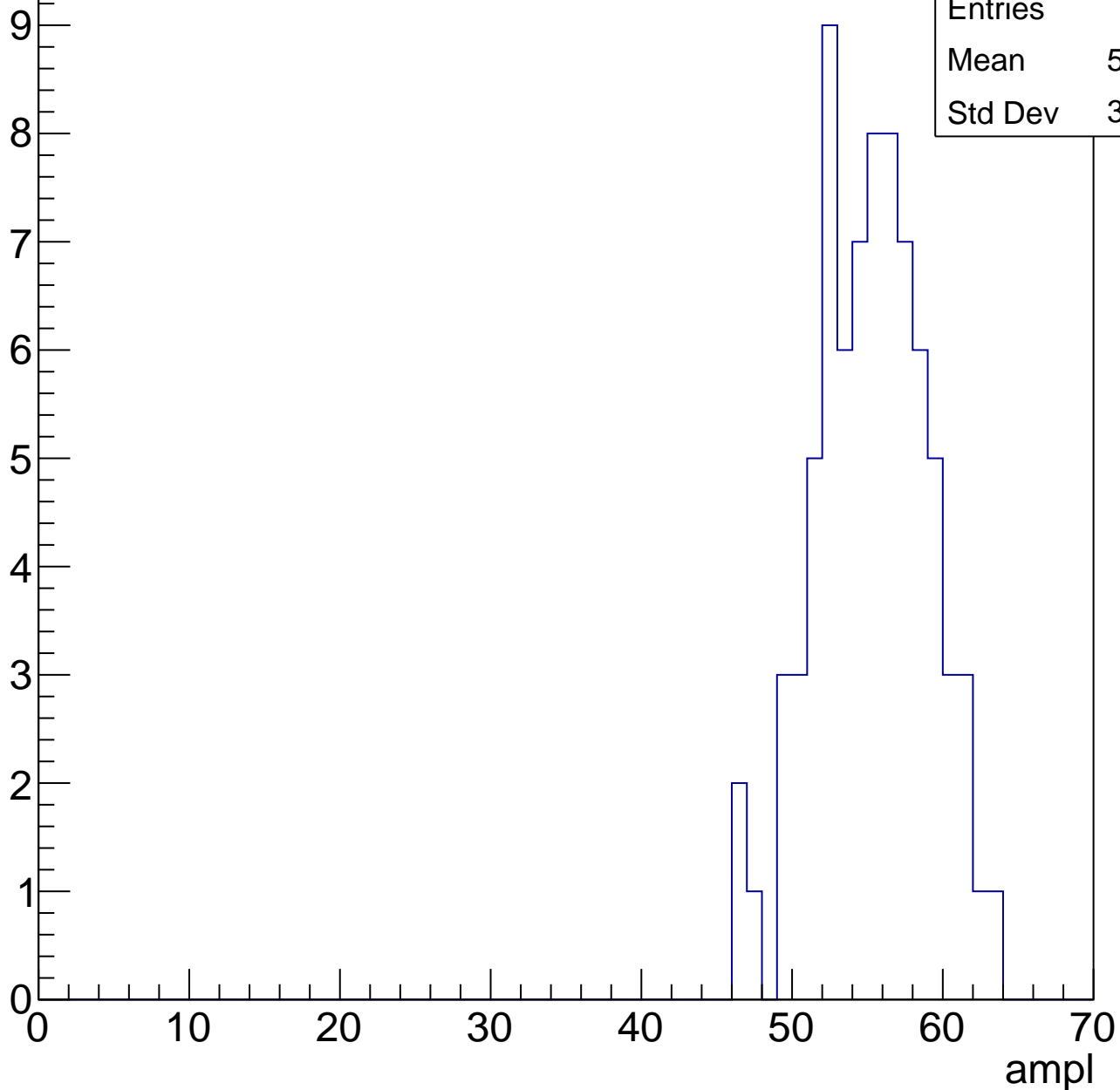


B0L000S, U2-ch90, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	54.78
Std Dev	3.692



B0L000S, U2-ch90, adc5

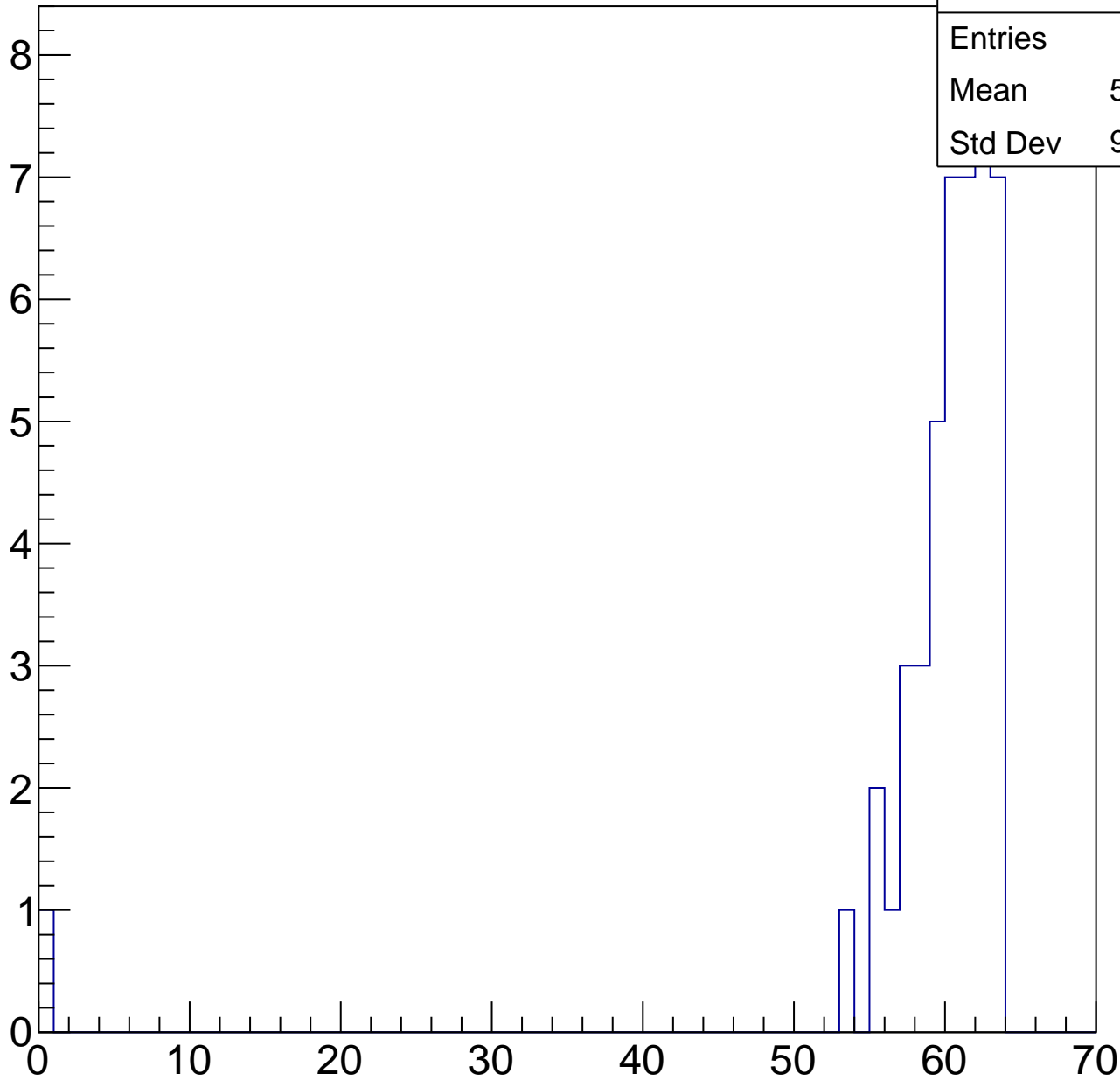
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.73
Std Dev	9.176

ampl



B0L000S, U2-ch90, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch90, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch91, adc0

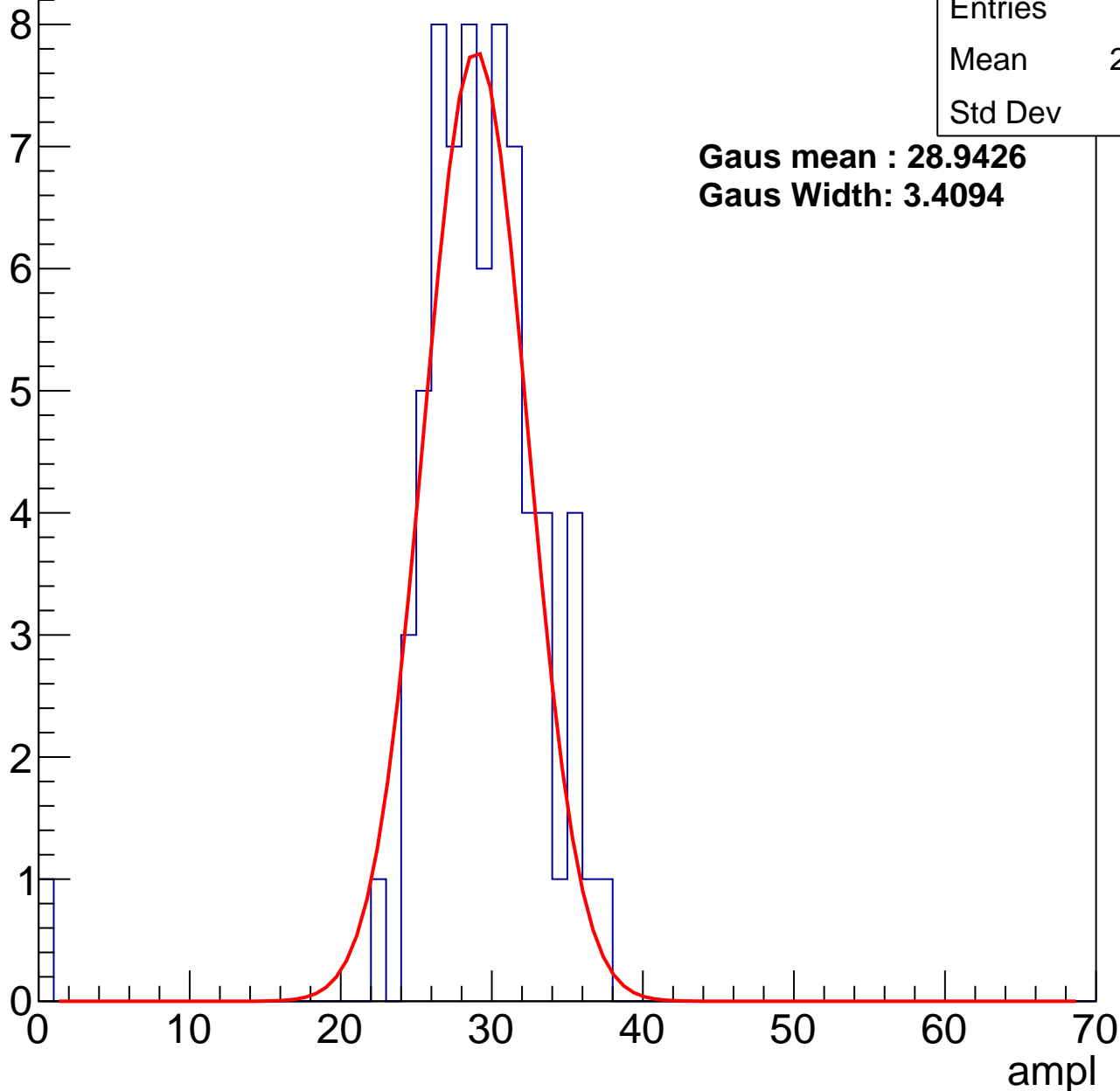
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	28.67
Std Dev	4.76

Gaus mean : 28.9426

Gaus Width: 3.4094



B0L000S, U2-ch91, adc1

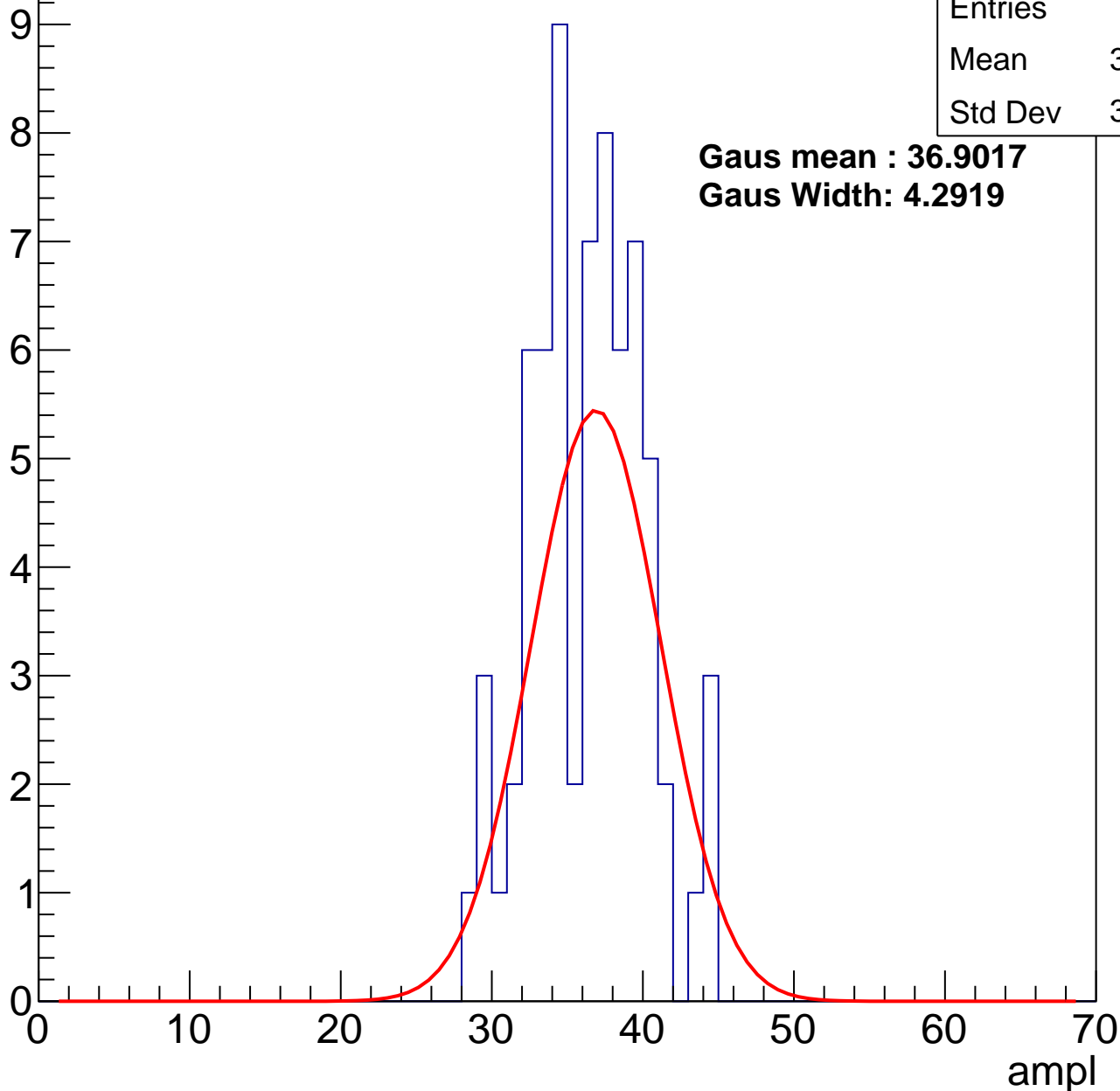
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	35.93
Std Dev	3.712

Gaus mean : 36.9017

Gaus Width: 4.2919



B0L000S, U2-ch91, adc2

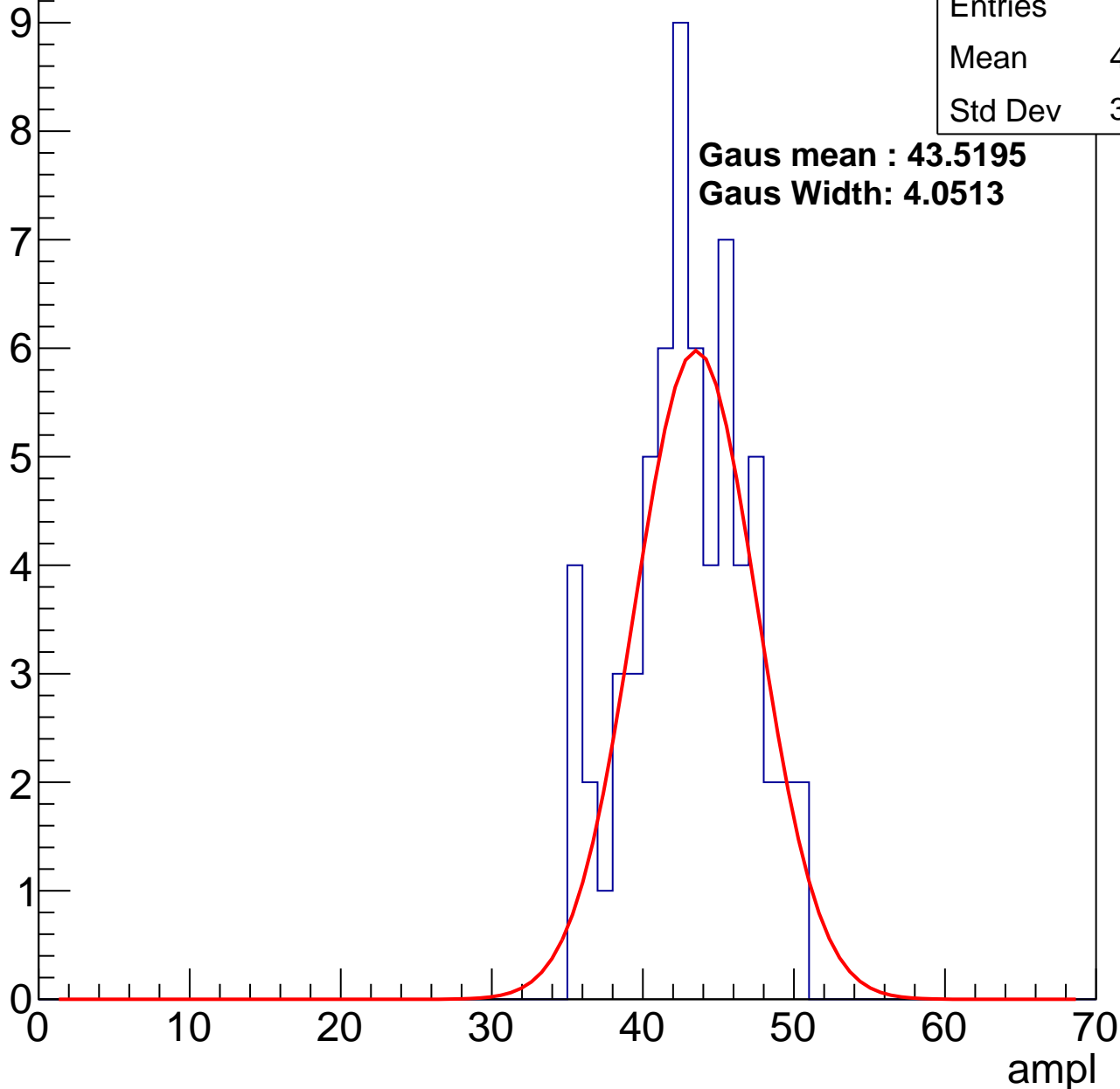
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.55
Std Dev	3.815

Gaus mean : 43.5195

Gaus Width: 4.0513

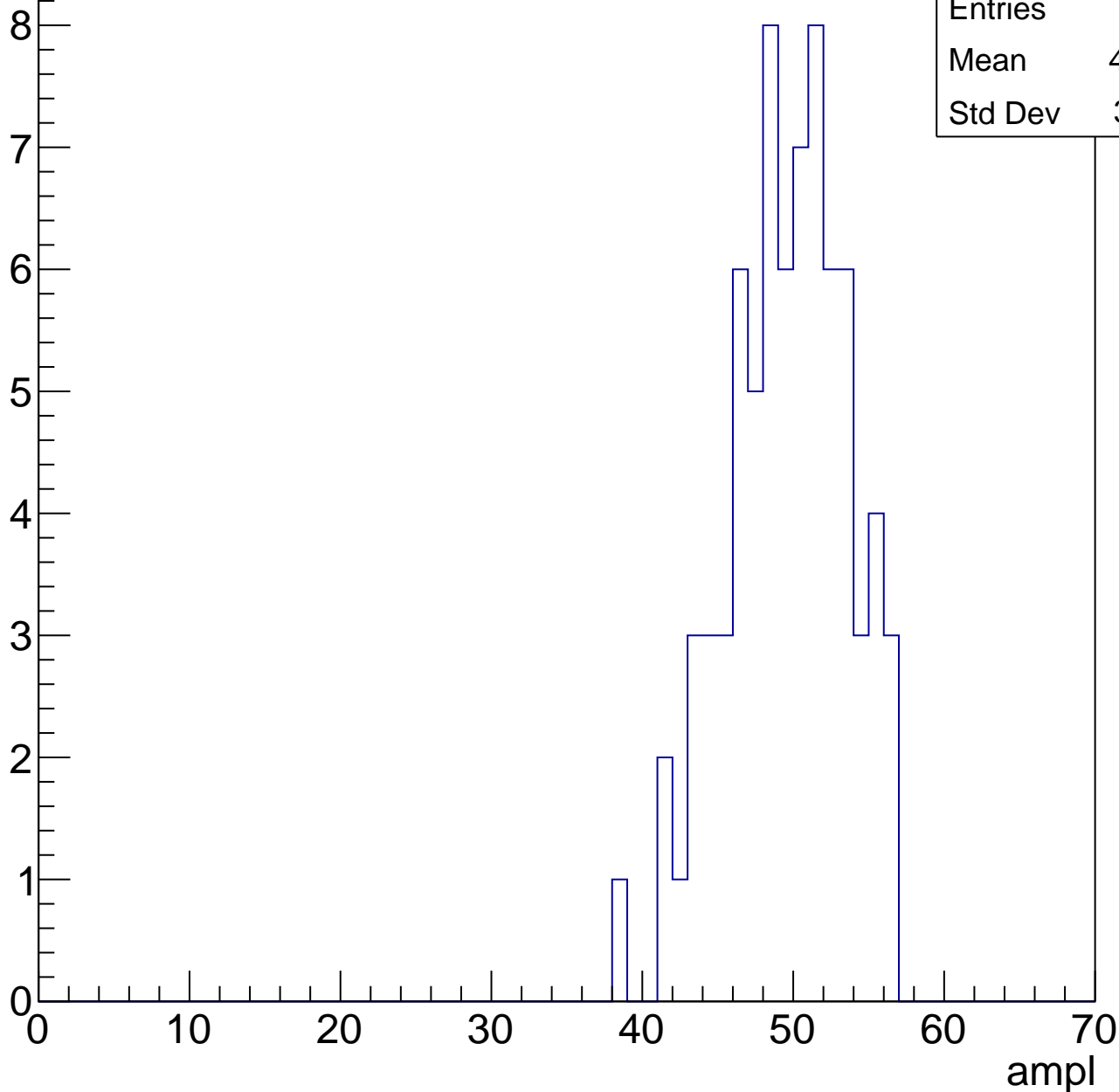


B0L000S, U2-ch91, adc3

calib_packv5_042523_0143.root, FC#5, port B1

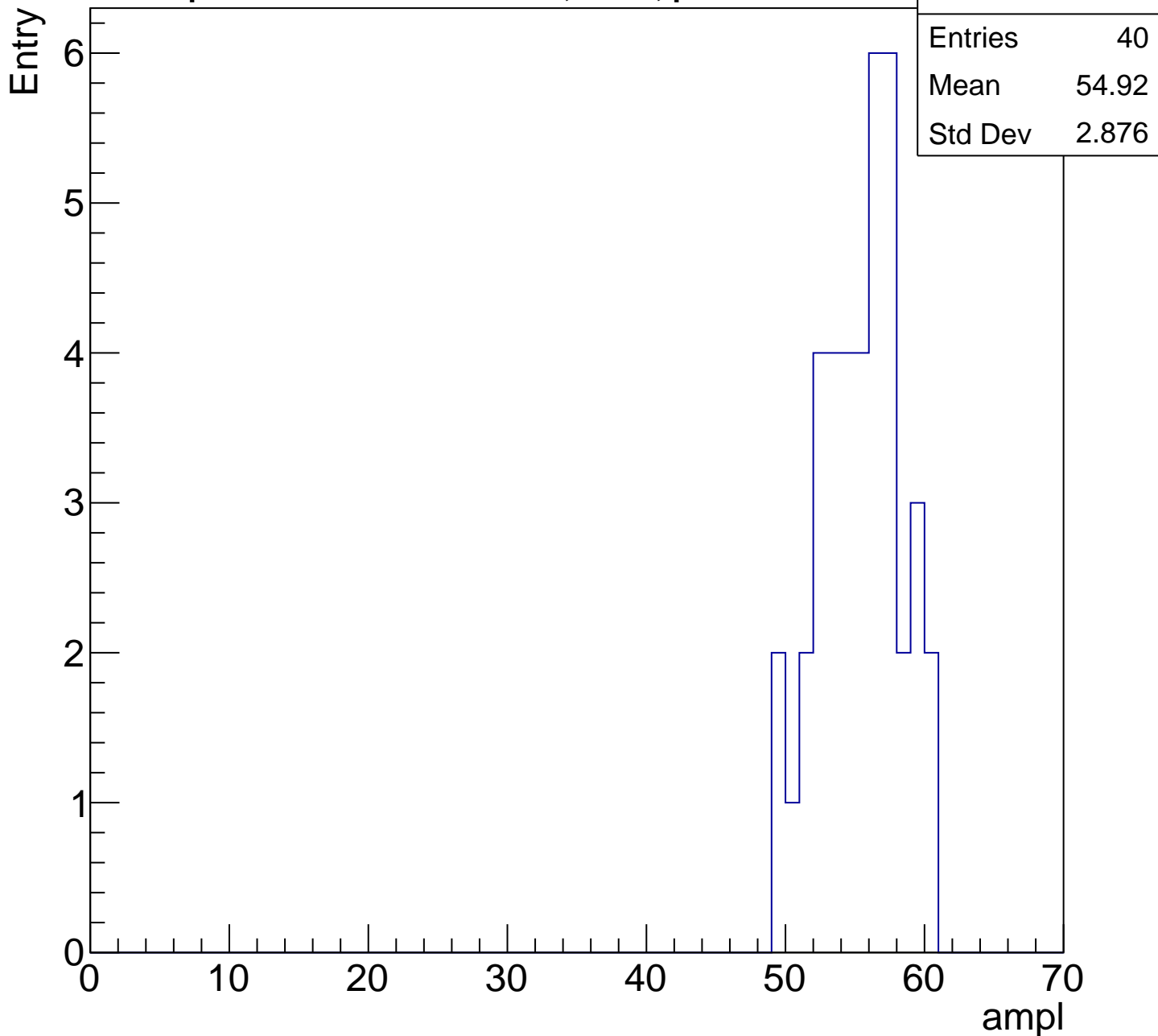
Entry

Entries	75
Mean	49.13
Std Dev	3.941



B0L000S, U2-ch91, adc4

calib_packv5_042523_0143.root, FC#5, port B1

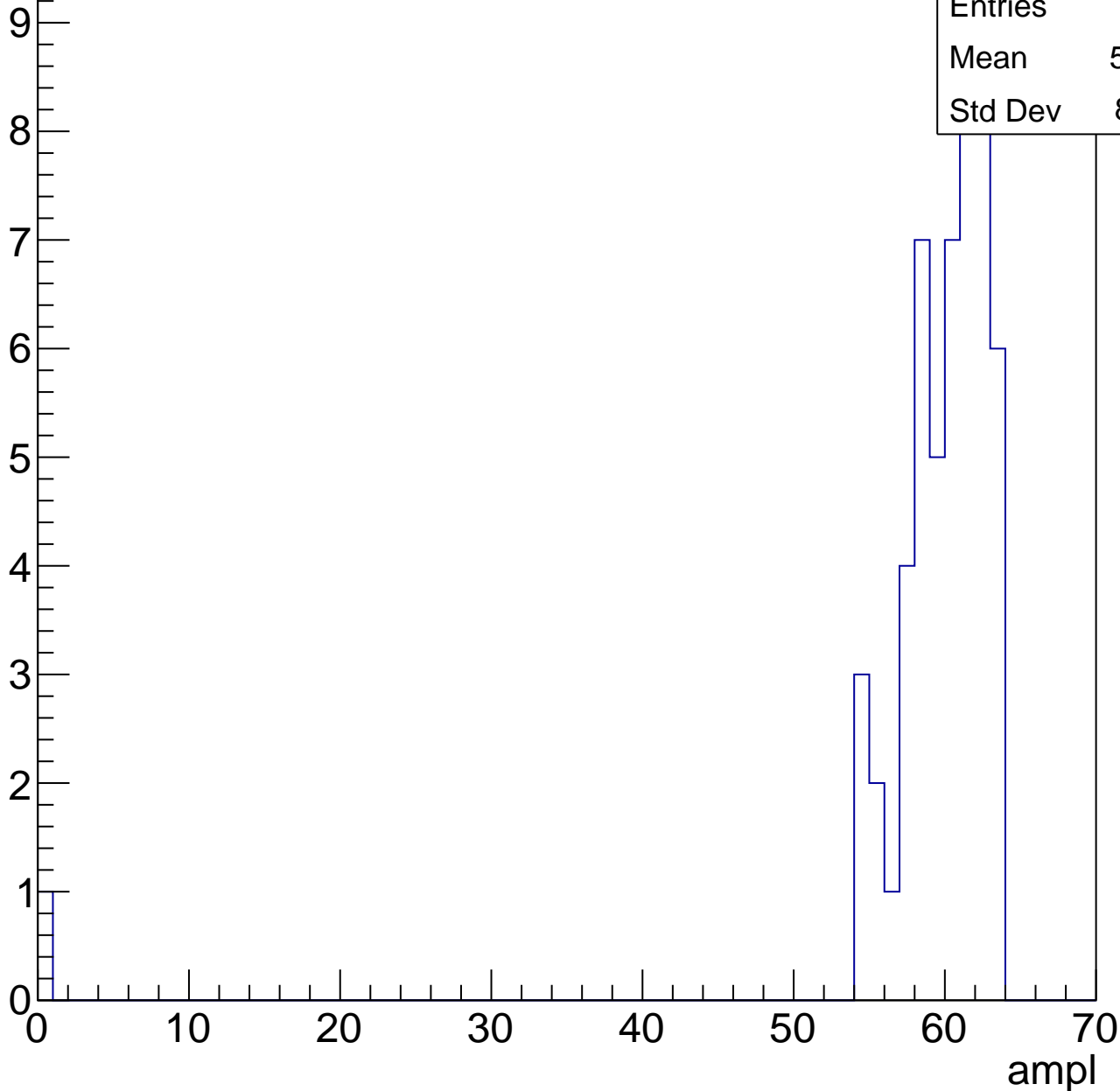


B0L000S, U2-ch91, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

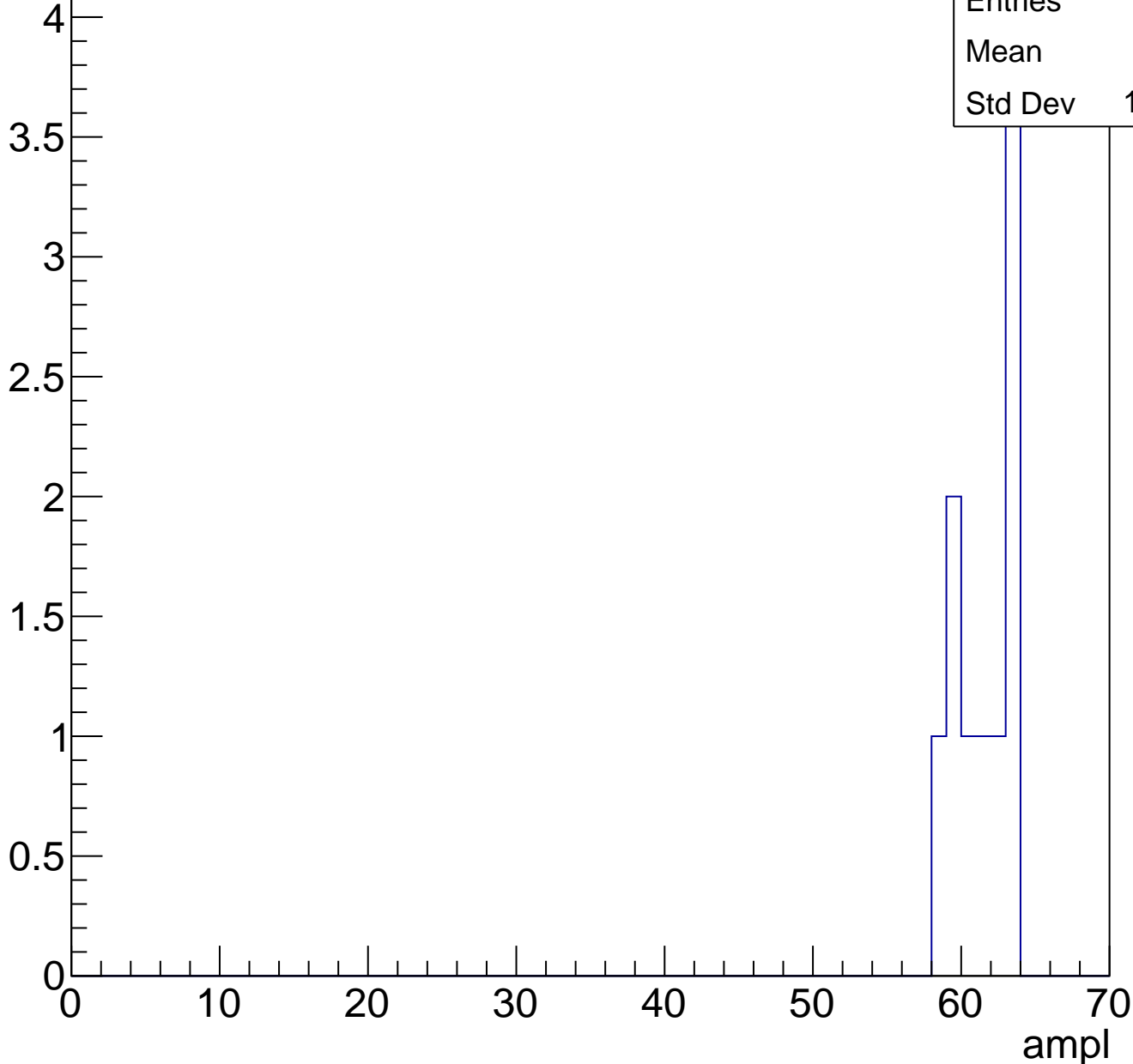
Entries	54
Mean	58.56
Std Dev	8.421



B0L000S, U2-ch91, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch91, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch92, adc0

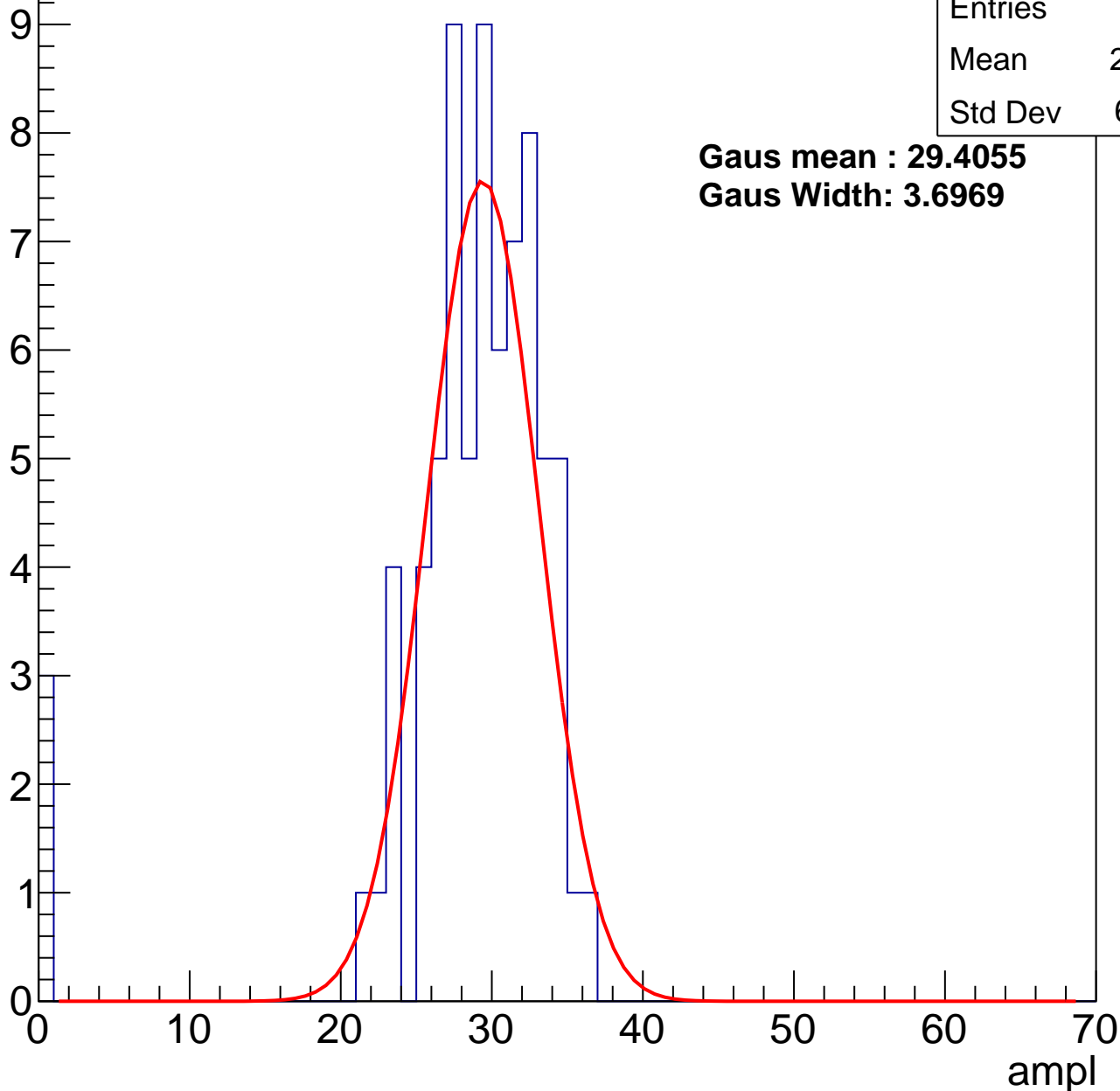
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	27.95
Std Dev	6.621

Gaus mean : 29.4055

Gaus Width: 3.6969



B0L000S, U2-ch92, adc1

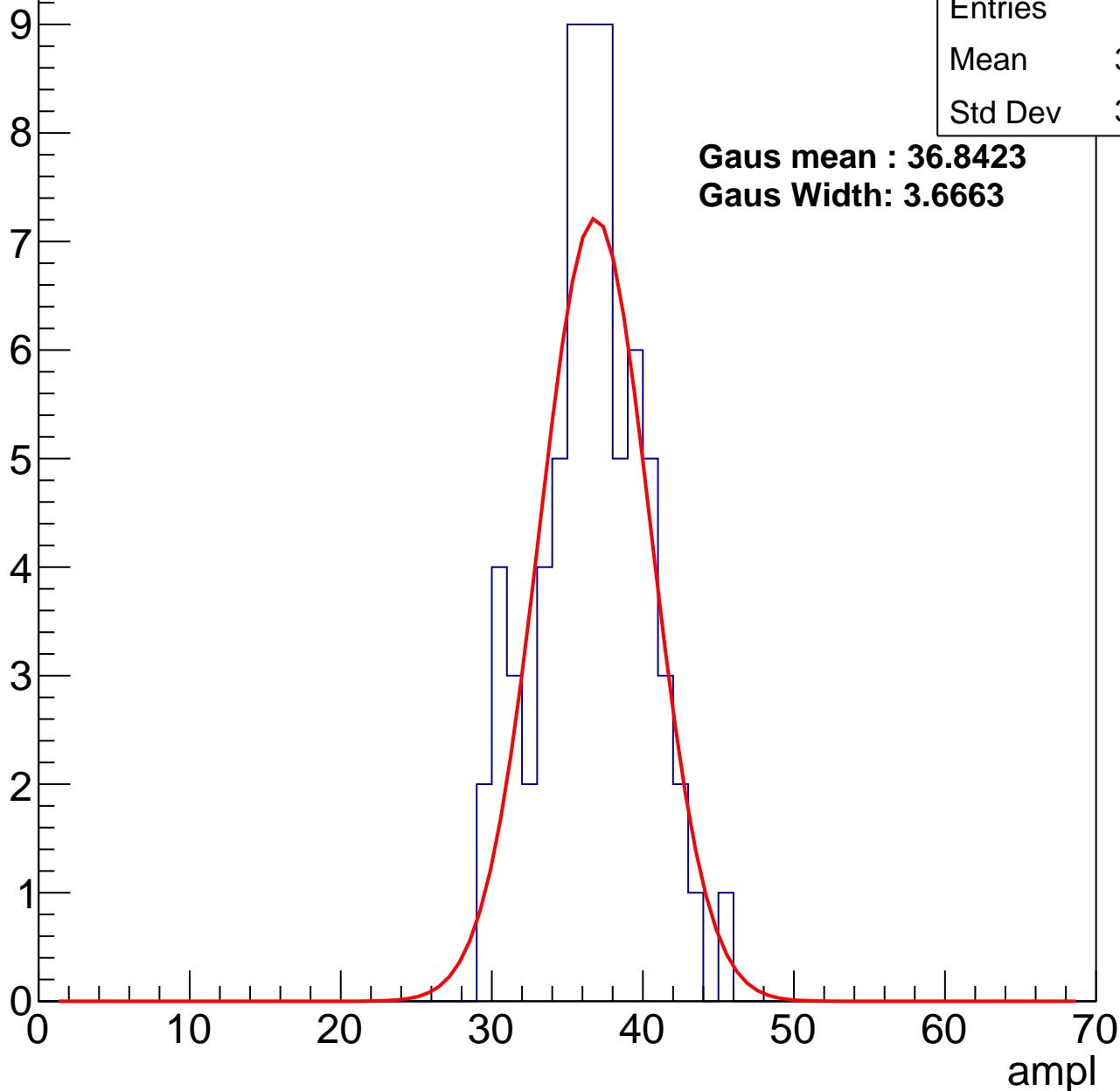
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.11
Std Dev	3.491

Gaus mean : 36.8423

Gaus Width: 3.6663



B0L000S, U2-ch92, adc2

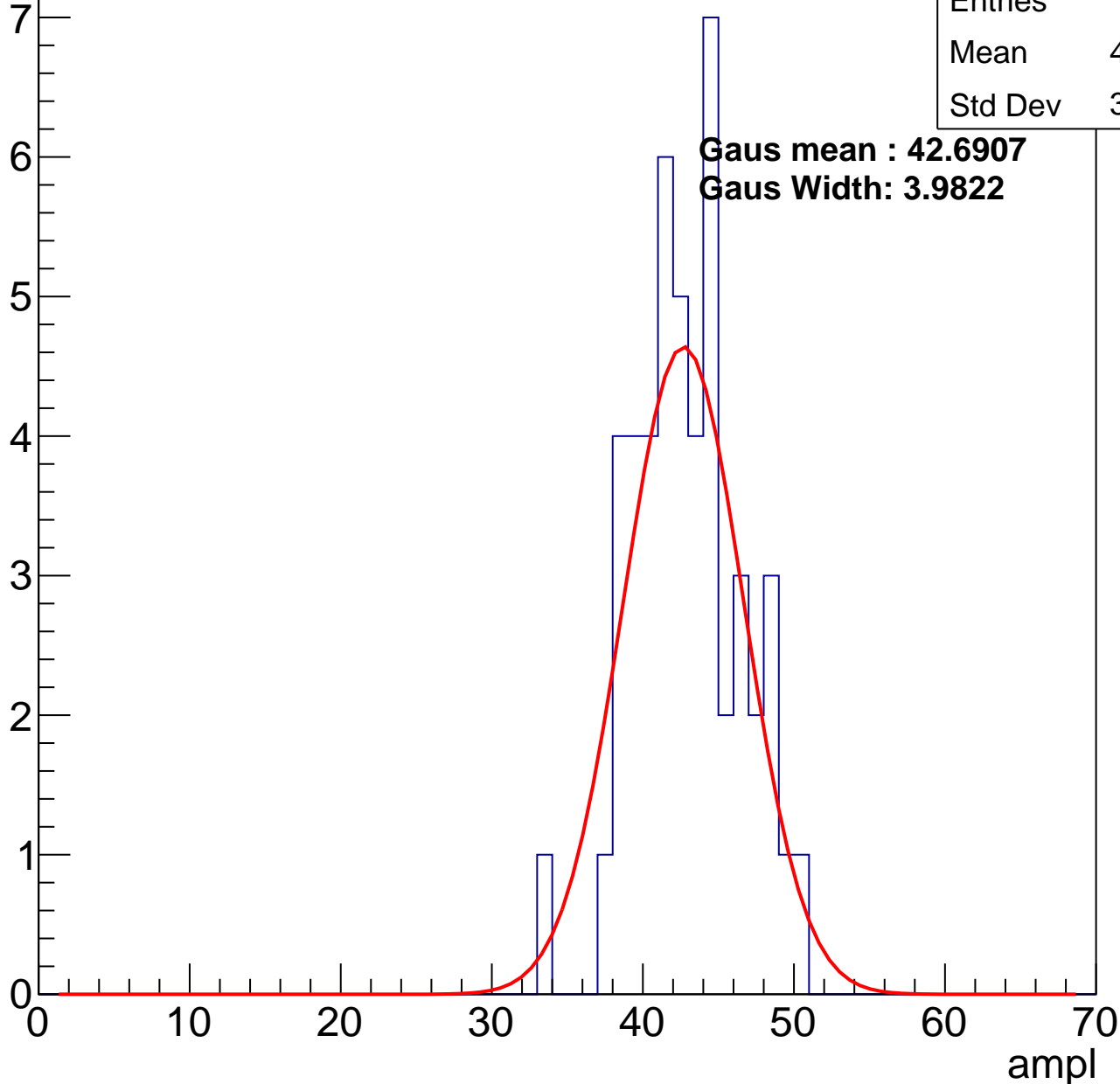
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	42.48
Std Dev	3.494

Gaus mean : 42.6907

Gaus Width: 3.9822

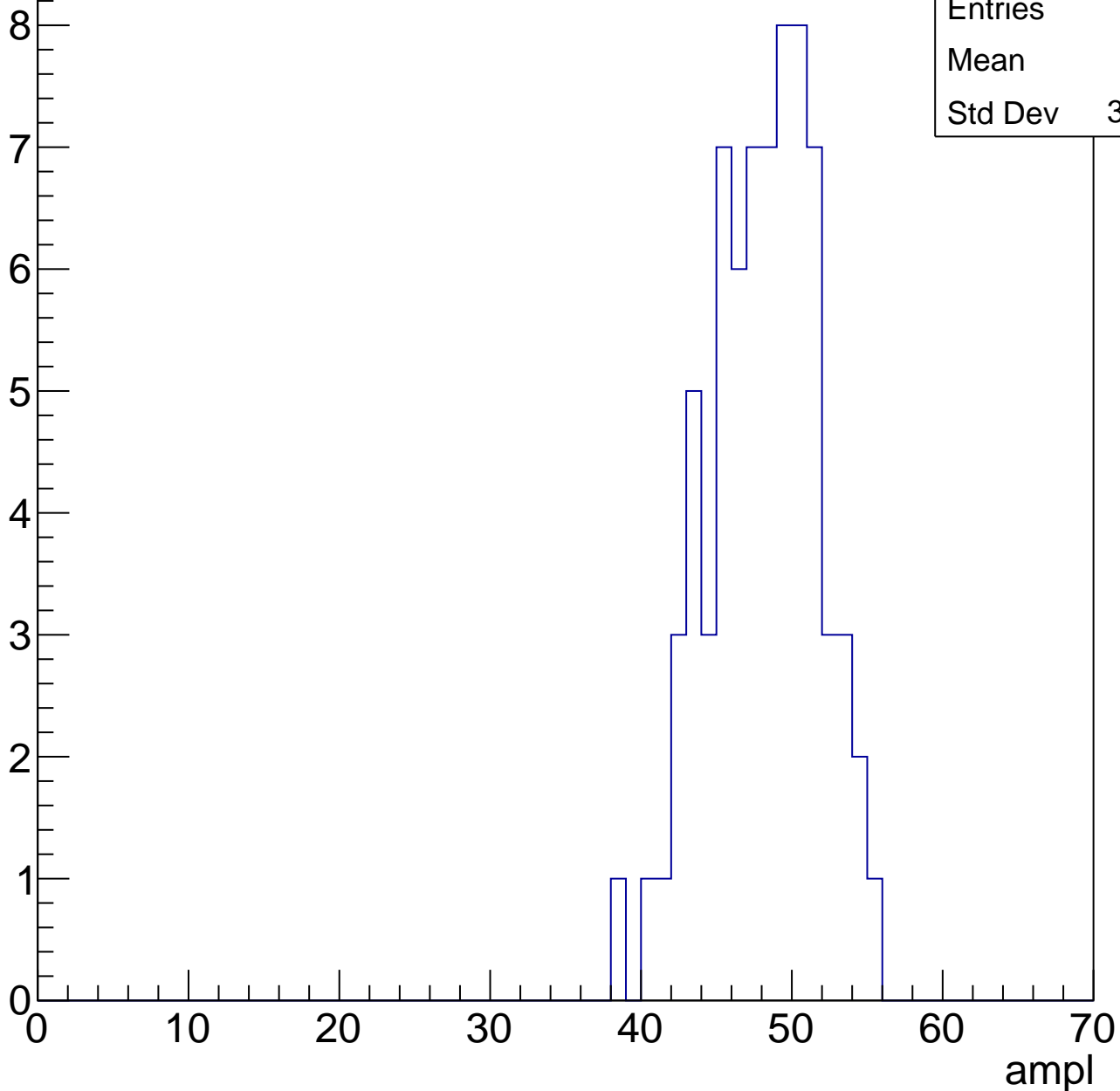


B0L000S, U2-ch92, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	47.6
Std Dev	3.557

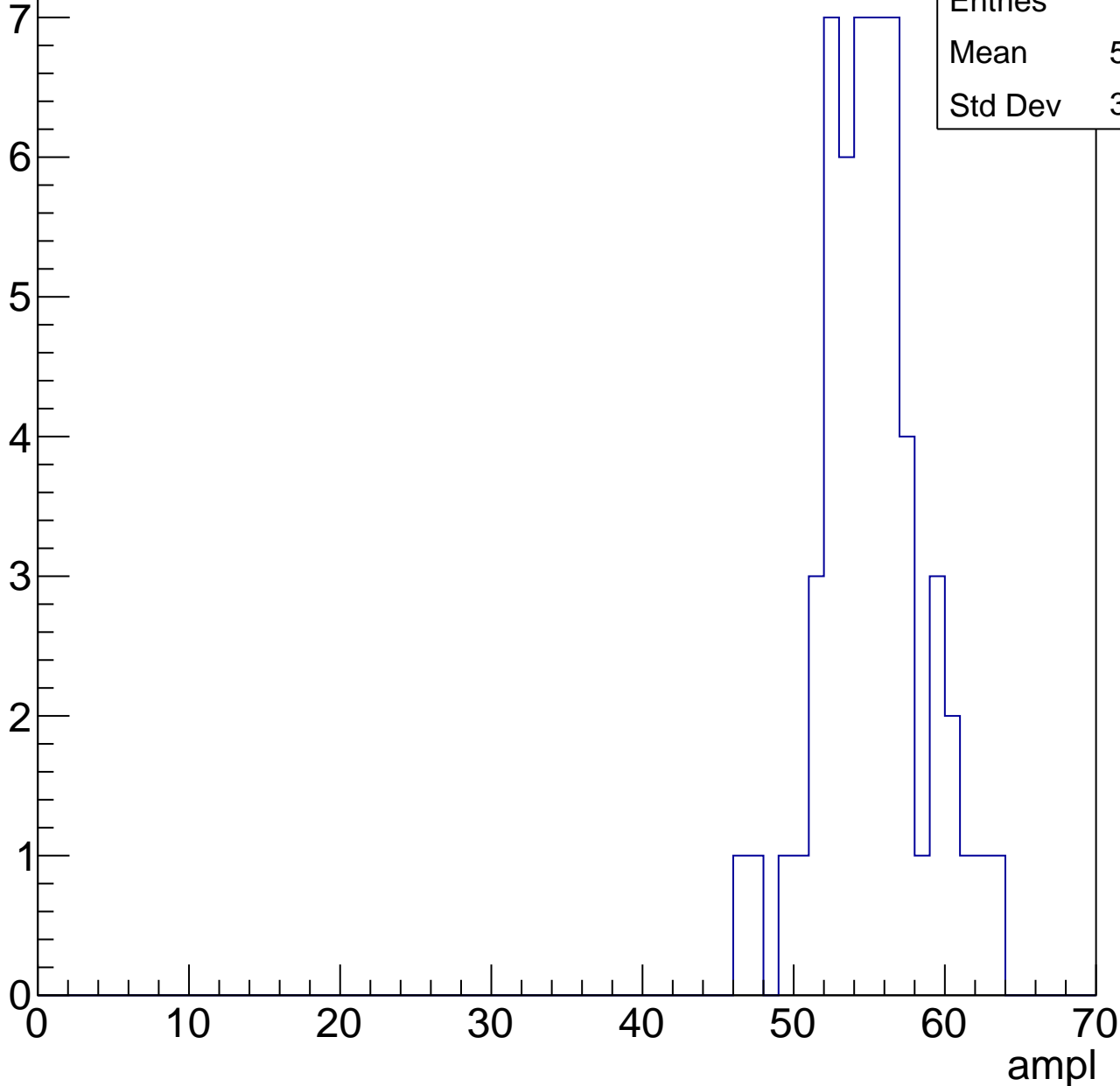


B0L000S, U2-ch92, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	54.65
Std Dev	3.389



B0L000S, U2-ch92, adc5

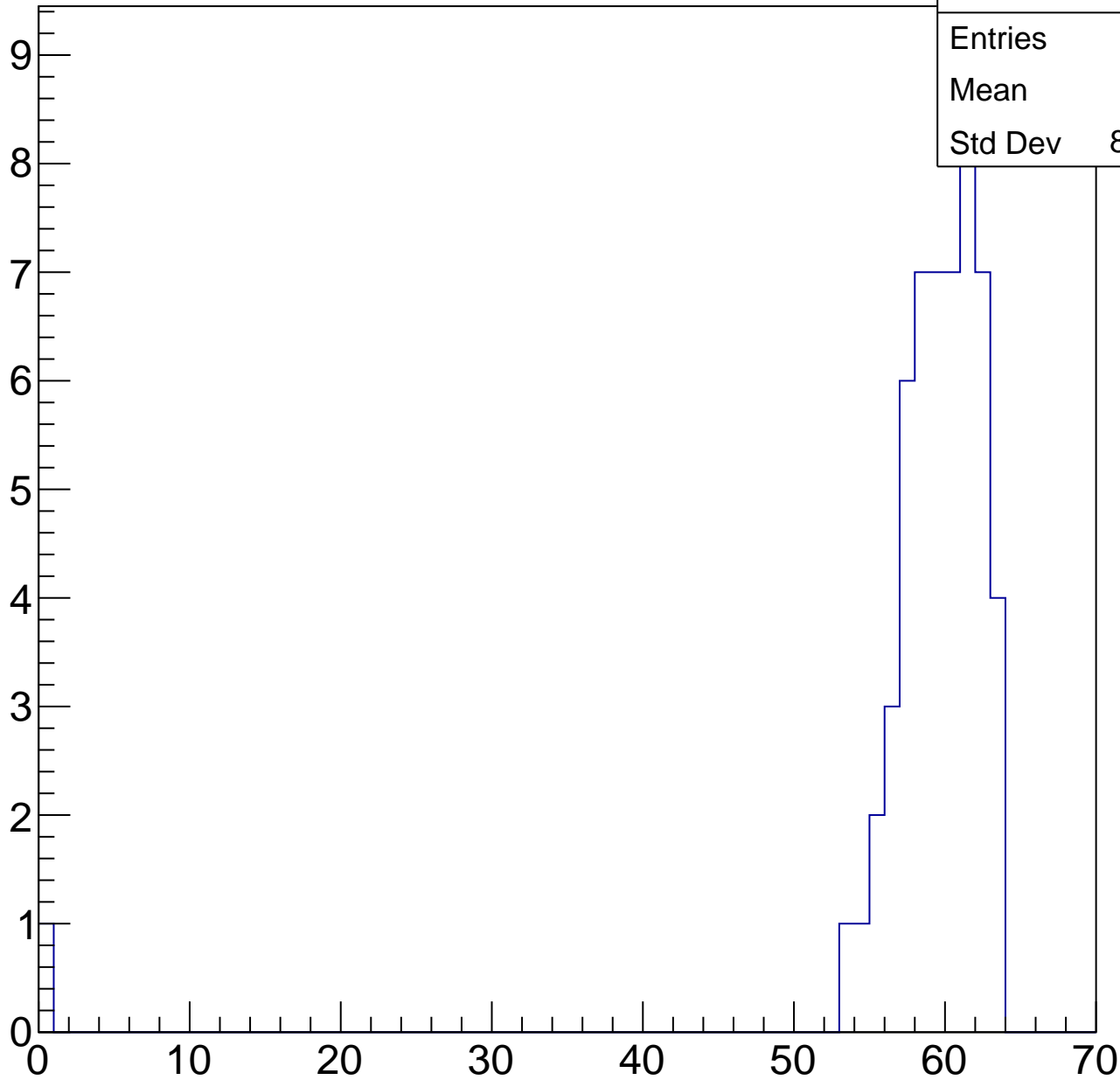
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	58.2
Std Dev	8.276

ampl

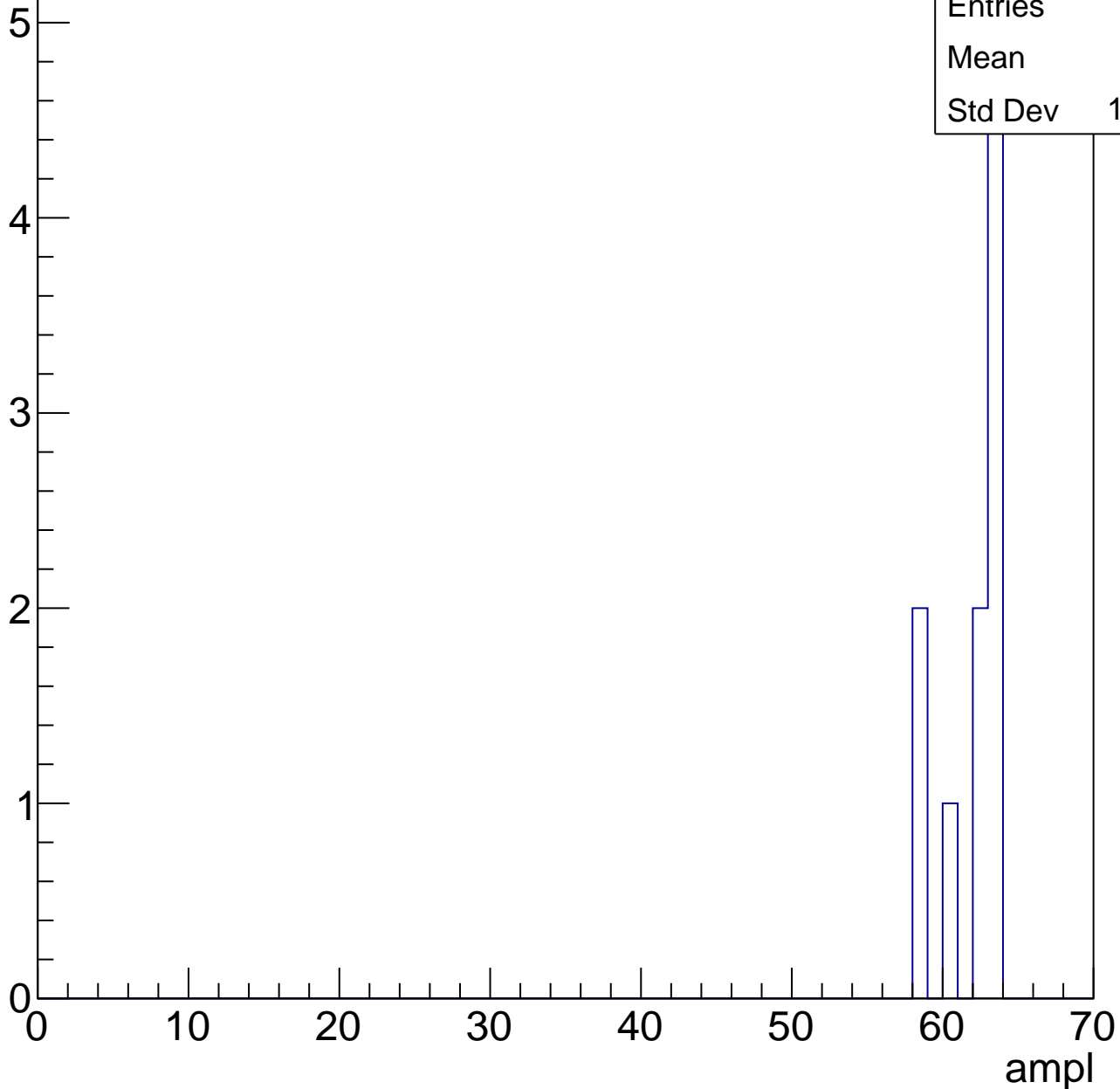


B0L000S, U2-ch92, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	10
Mean	61.5
Std Dev	1.962



B0L000S, U2-ch92, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch93, adc0

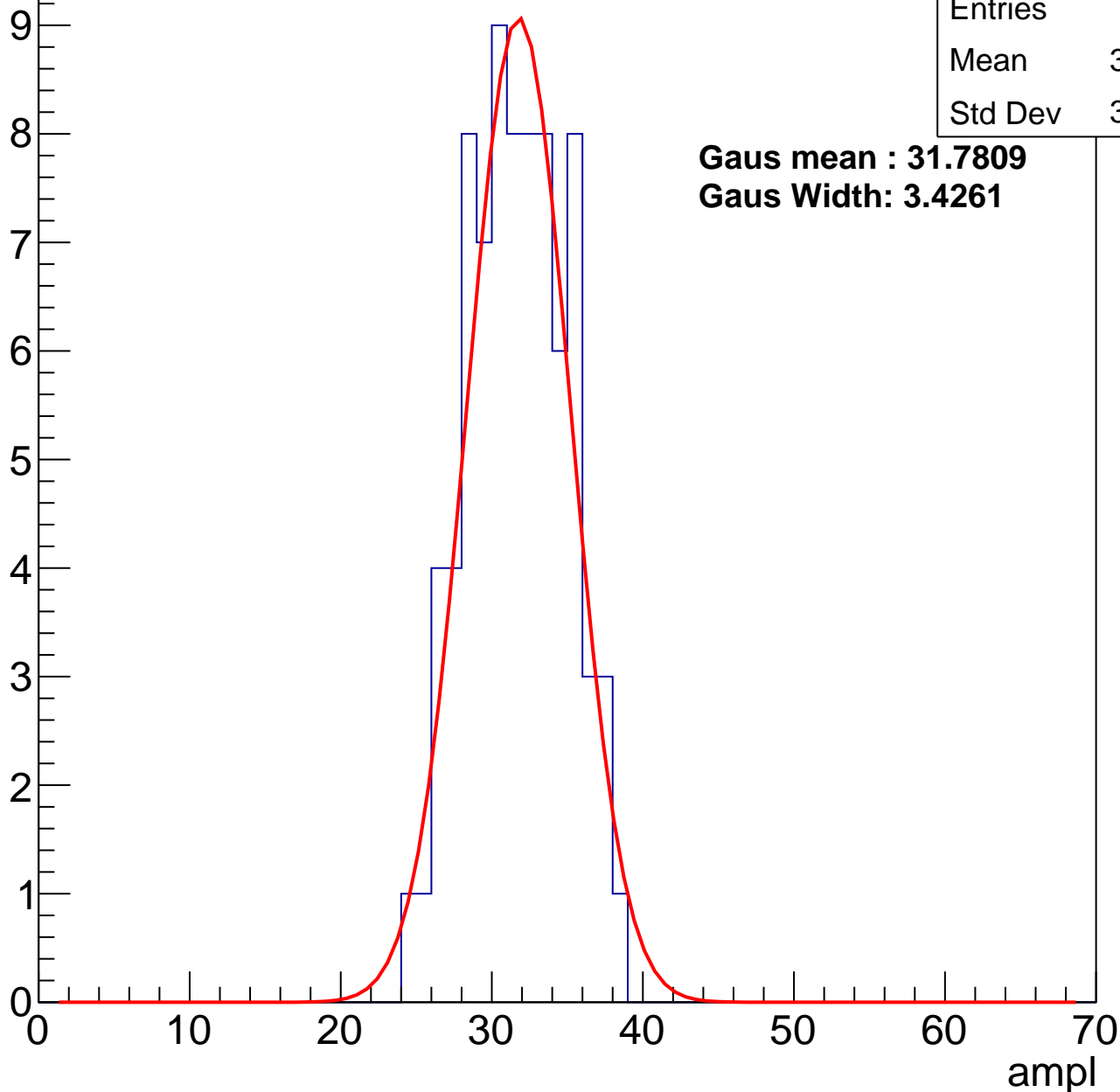
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	79
Mean	31.23
Std Dev	3.198

Gaus mean : 31.7809

Gaus Width: 3.4261



B0L000S, U2-ch93, adc1

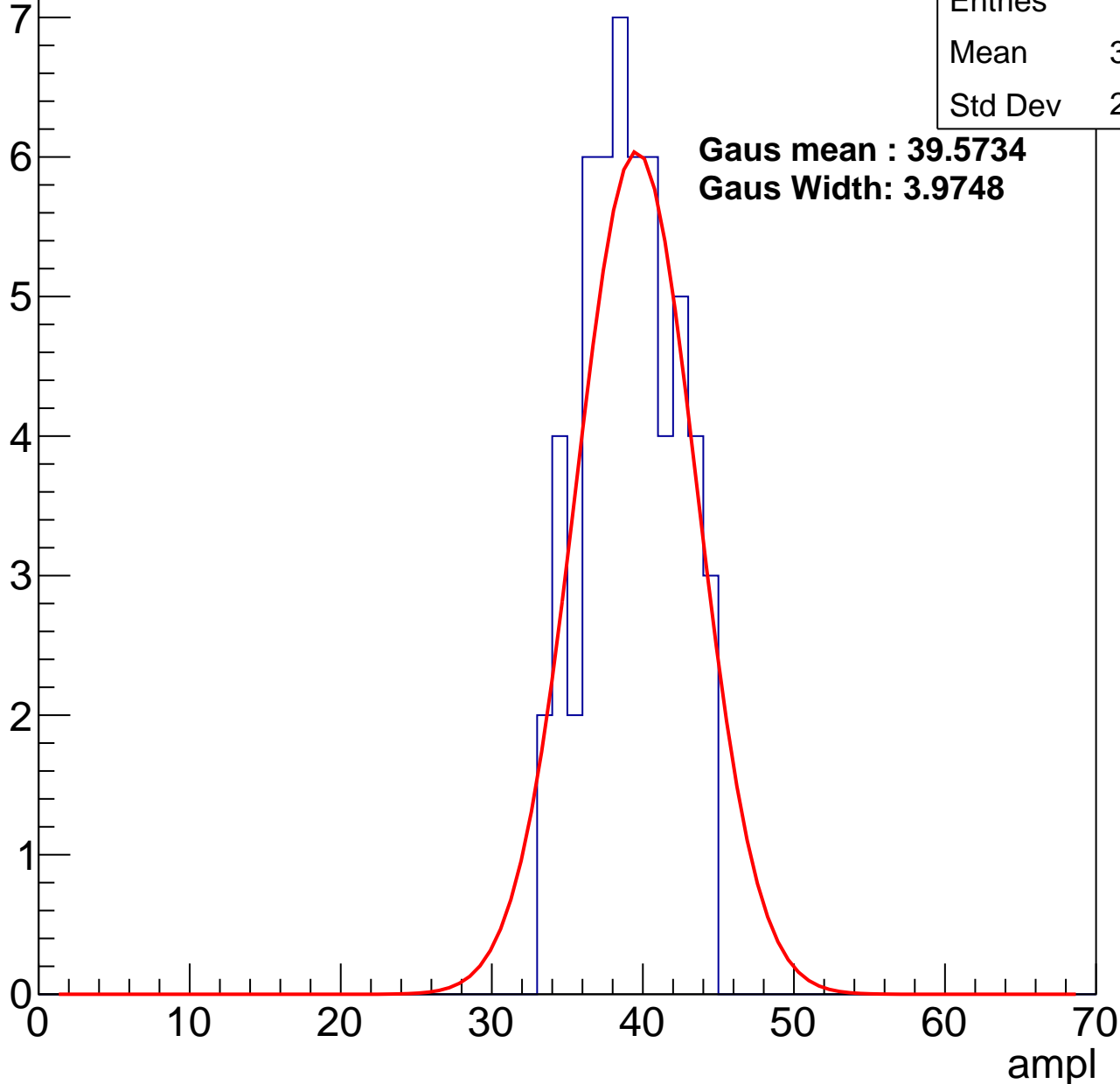
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	38.69
Std Dev	2.984

Gaus mean : 39.5734

Gaus Width: 3.9748



B0L000S, U2-ch93, adc2

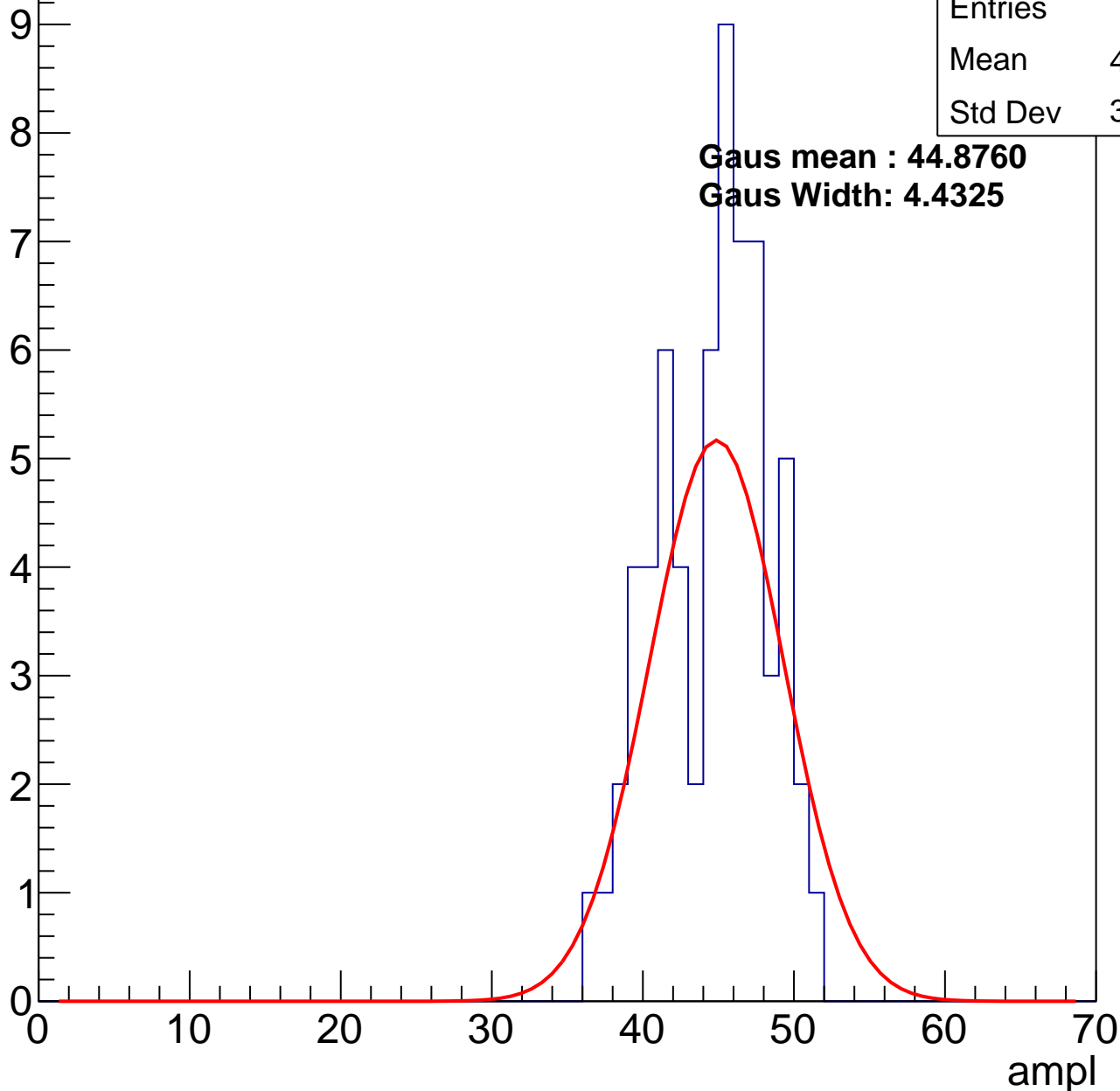
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	44.14
Std Dev	3.566

Gaus mean : 44.8760

Gaus Width: 4.4325

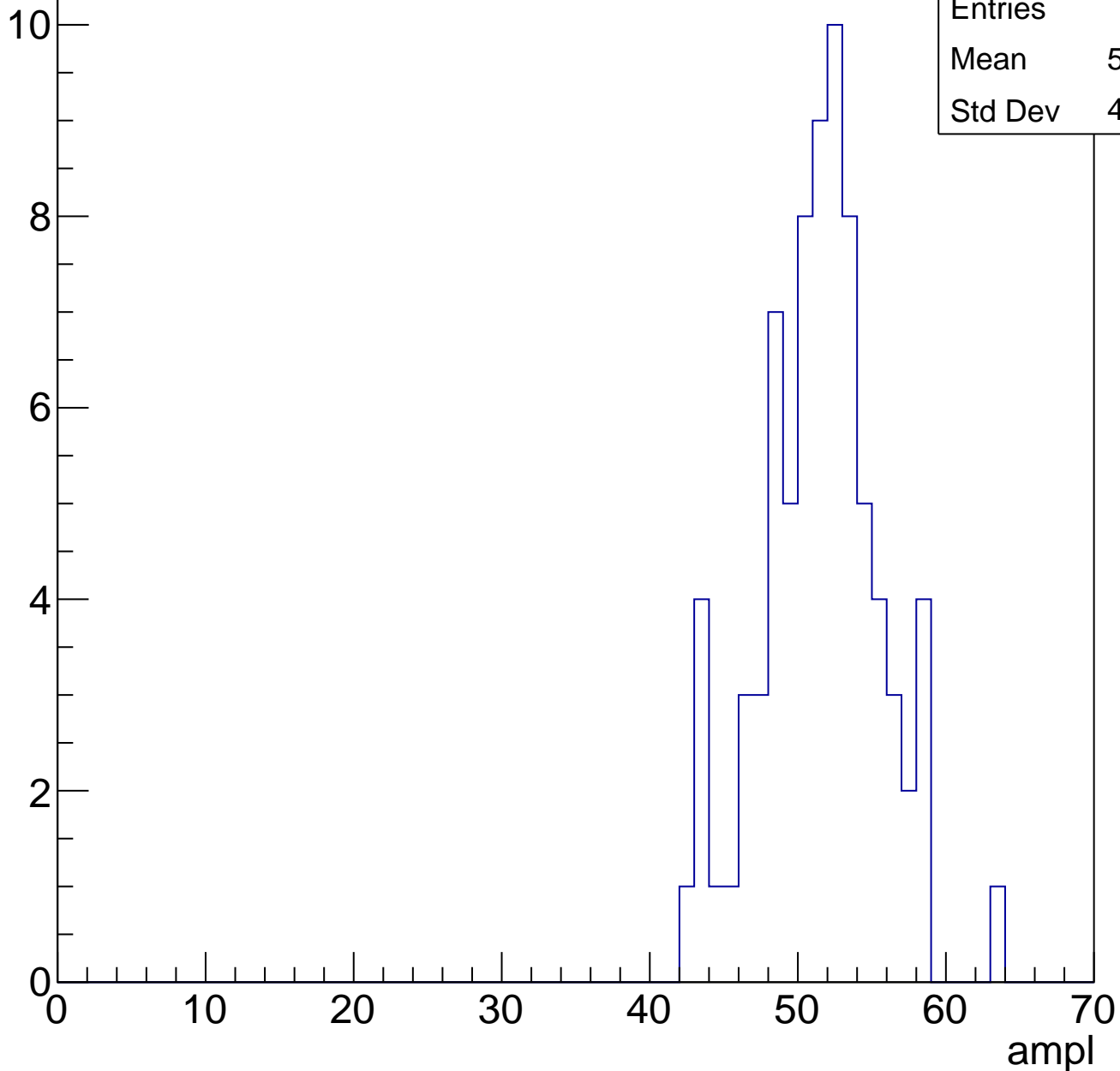


B0L000S, U2-ch93, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	79
Mean	51.05
Std Dev	4.015

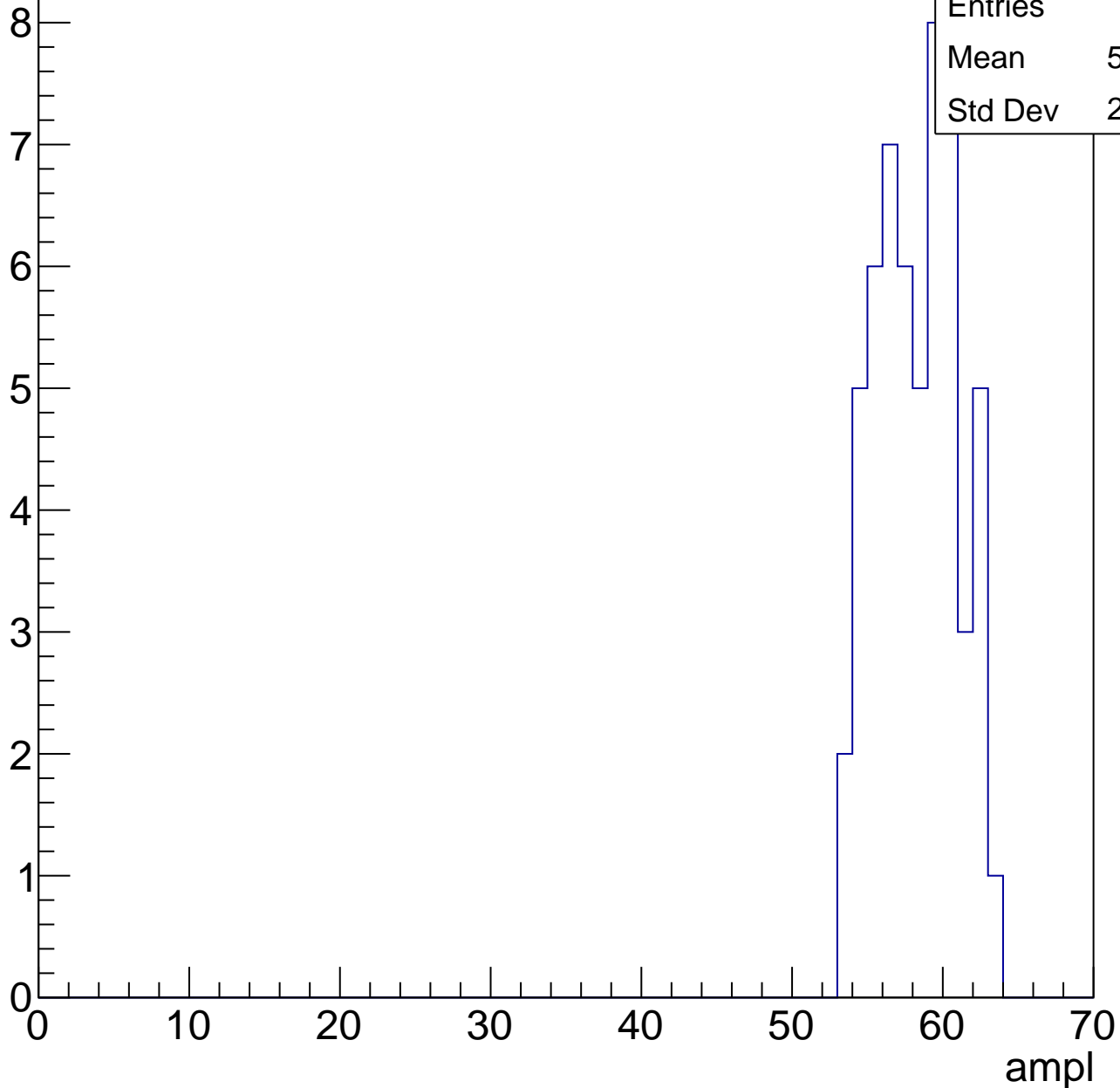
Entry



B0L000S, U2-ch93, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



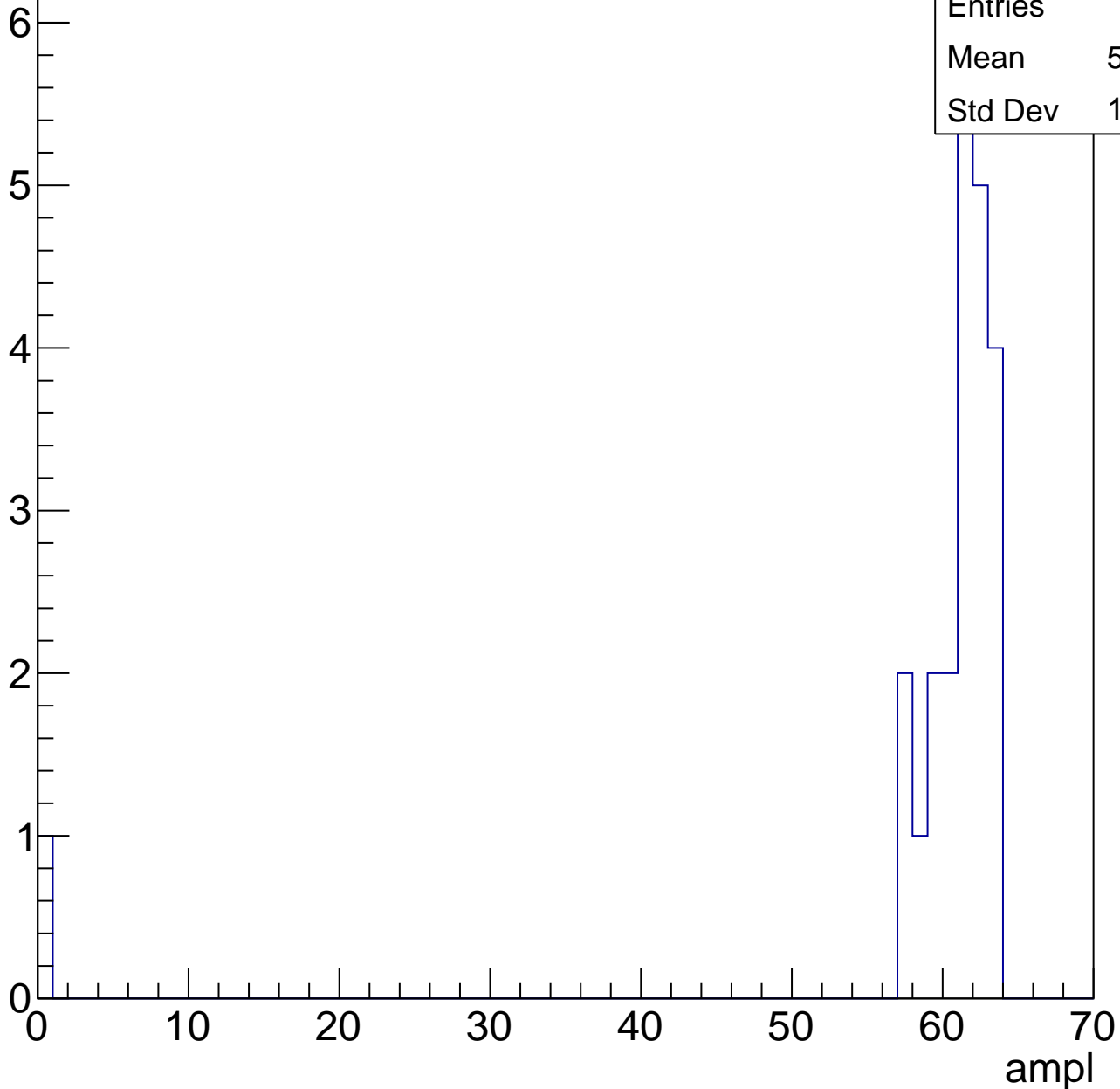
Entries	56
Mean	57.82
Std Dev	2.633

B0L000S, U2-ch93, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

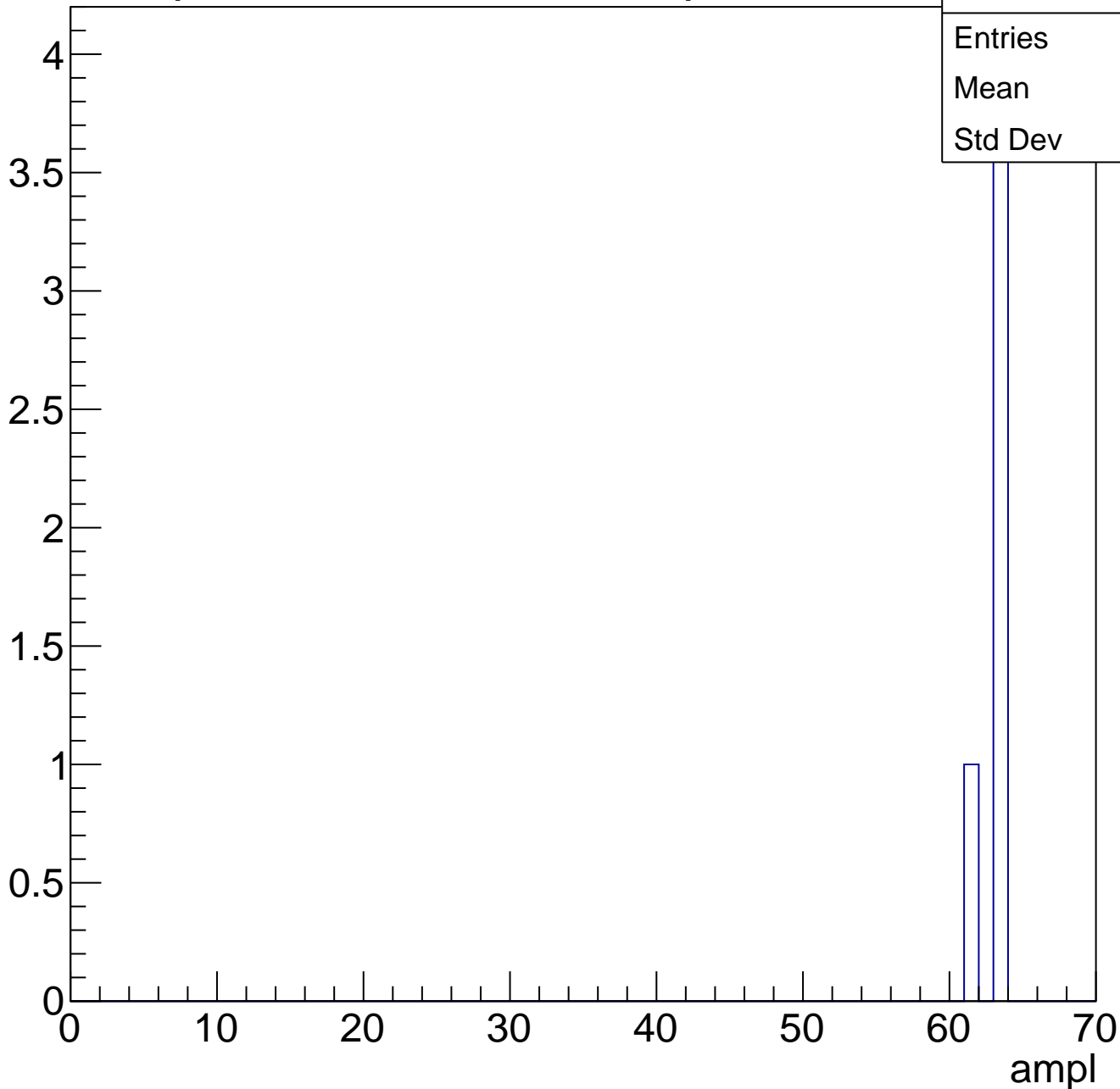
Entries	23
Mean	58.17
Std Dev	12.53



B0L000S, U2-ch93, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch93, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch94, adc0

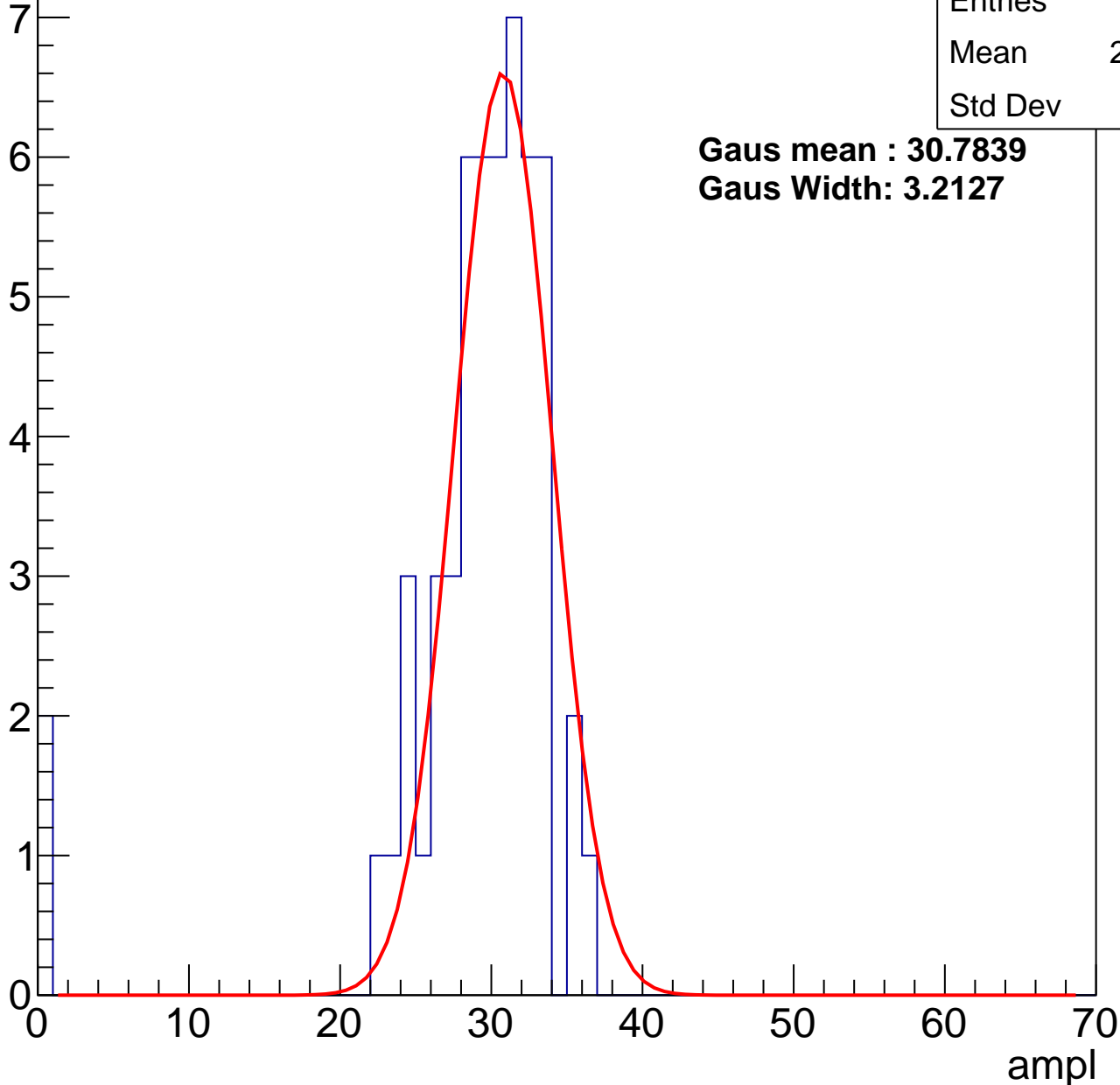
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	28.44
Std Dev	6.37

Gaus mean : 30.7839

Gaus Width: 3.2127



B0L000S, U2-ch94, adc1

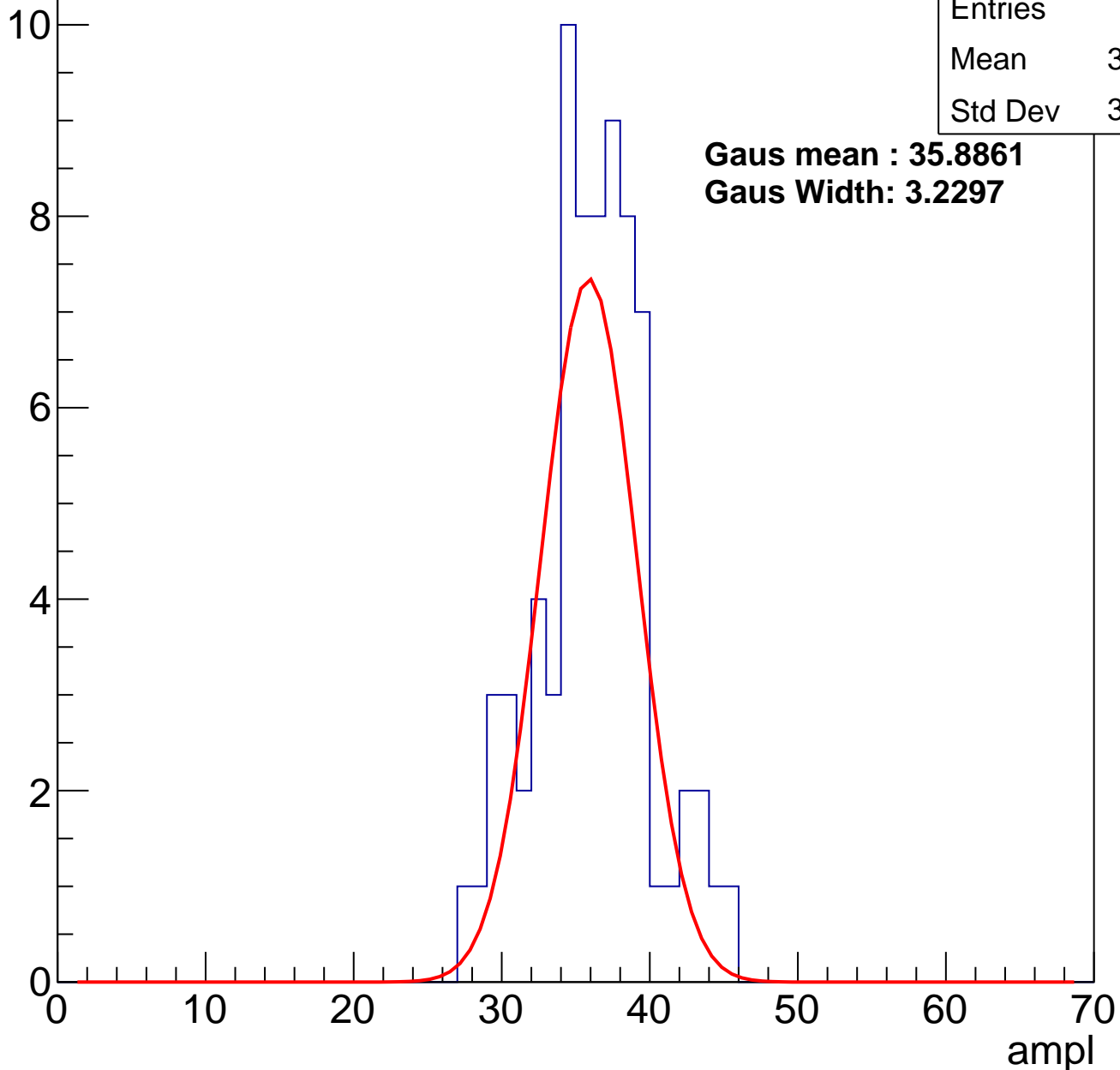
calib_packv5_042523_0143.root, FC#5, port B1

Entries	75
Mean	35.72
Std Dev	3.729

Gaus mean : 35.8861

Gaus Width: 3.2297

Entry



B0L000S, U2-ch94, adc2

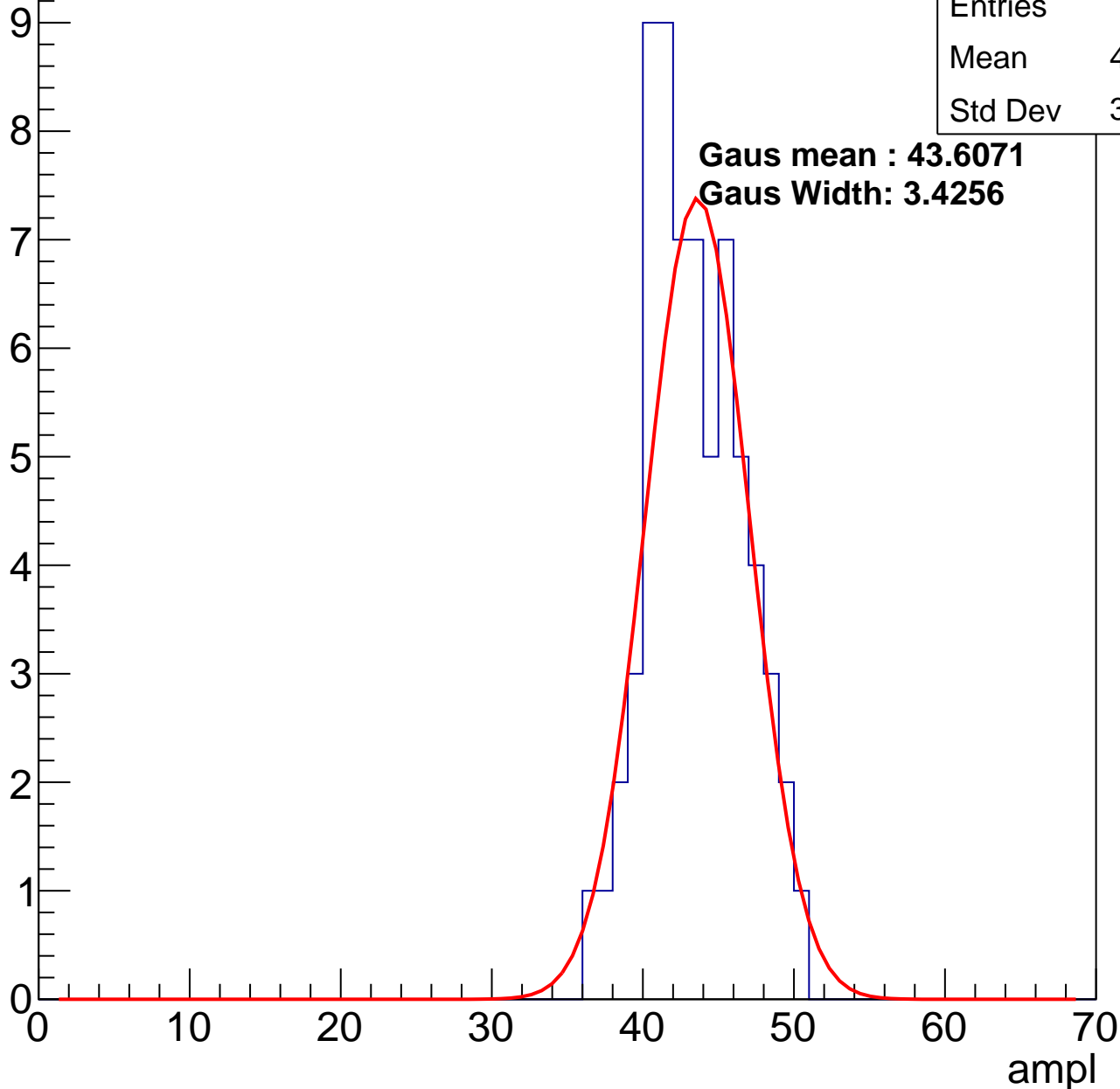
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	42.95
Std Dev	3.126

Gaus mean : 43.6071

Gaus Width: 3.4256

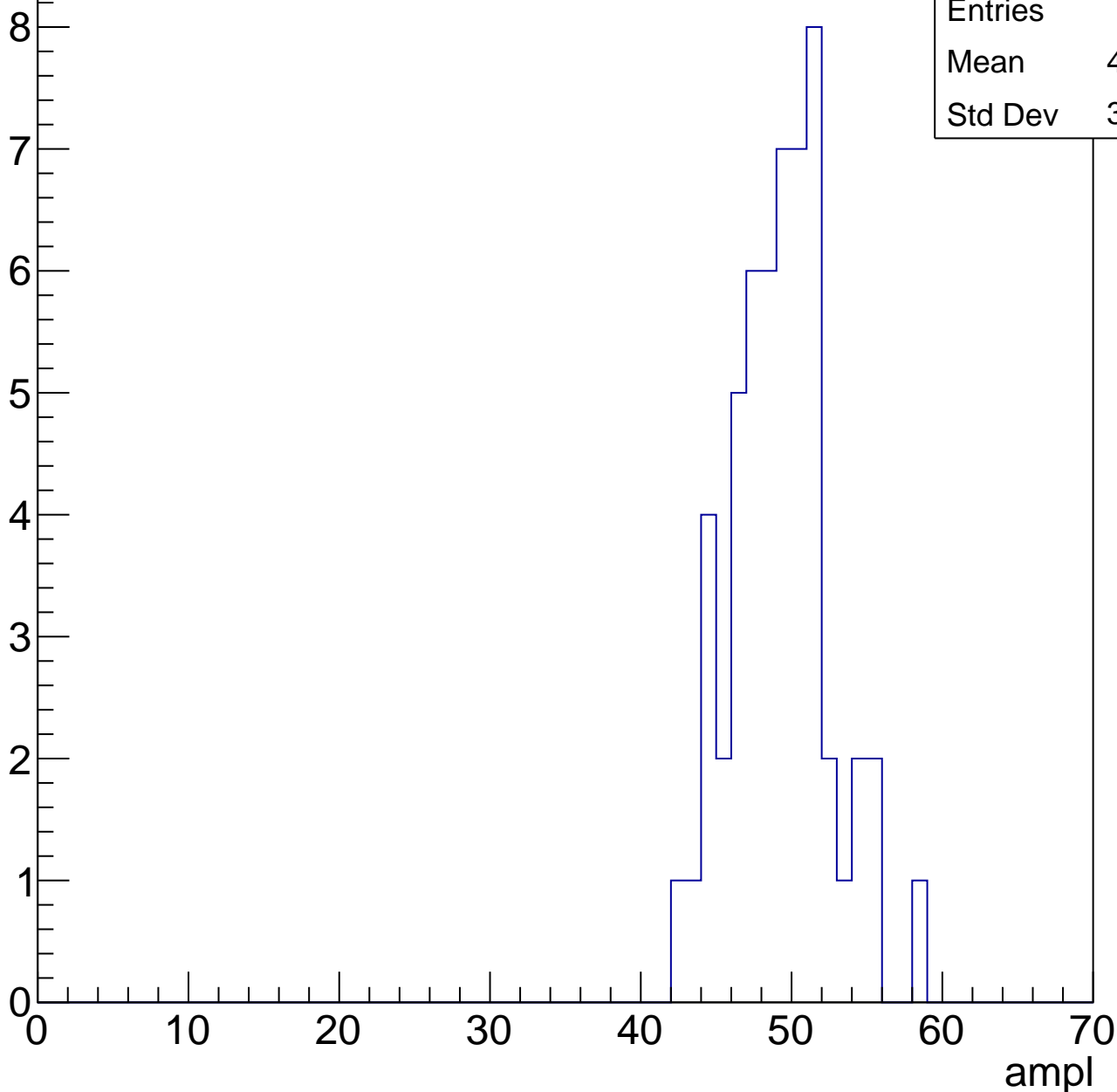


B0L000S, U2-ch94, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	48.82
Std Dev	3.208

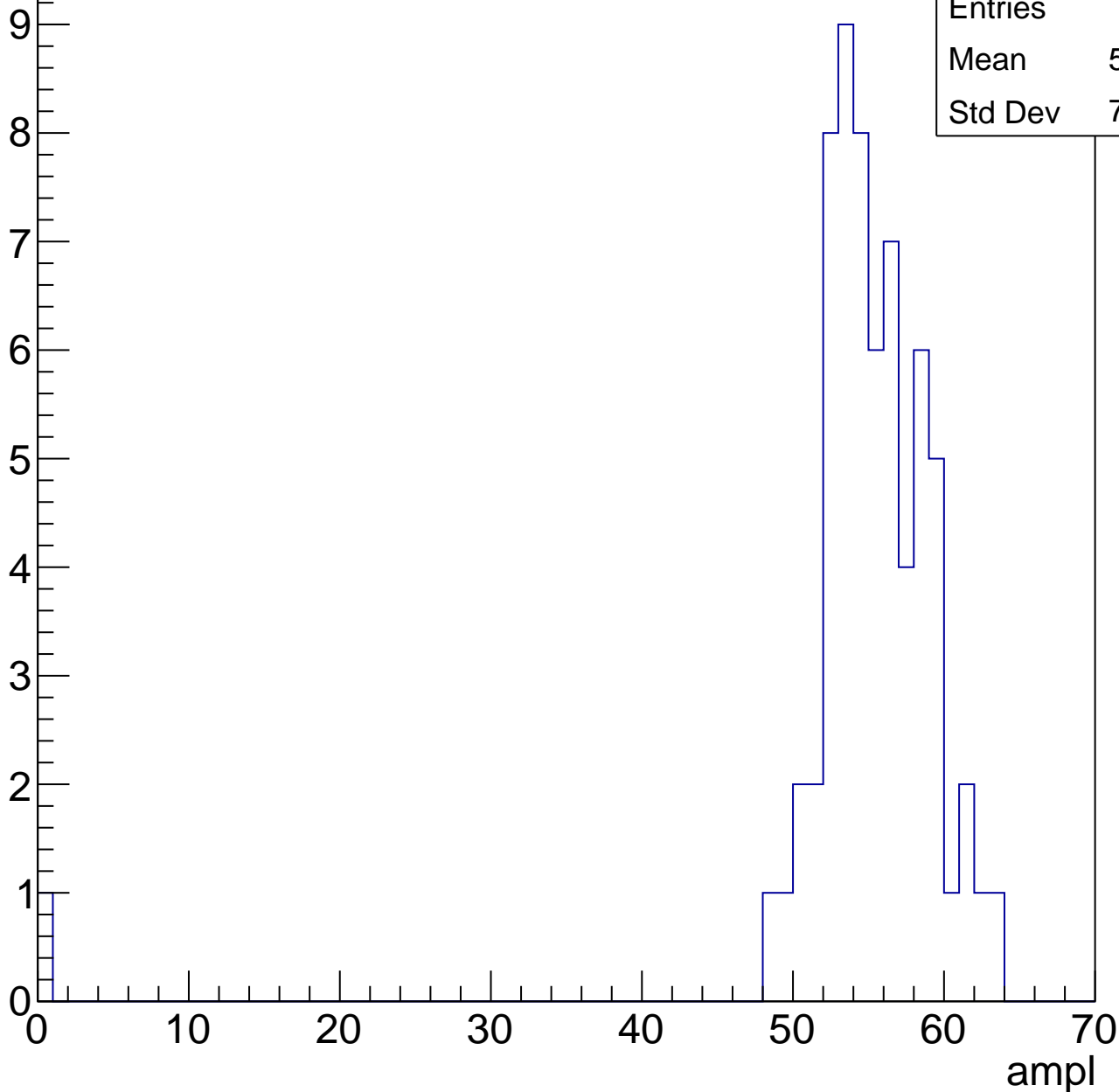


B0L000S, U2-ch94, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	54.22
Std Dev	7.478



B0L000S, U2-ch94, adc5

calib_packv5_042523_0143.root, FC#5, port B1

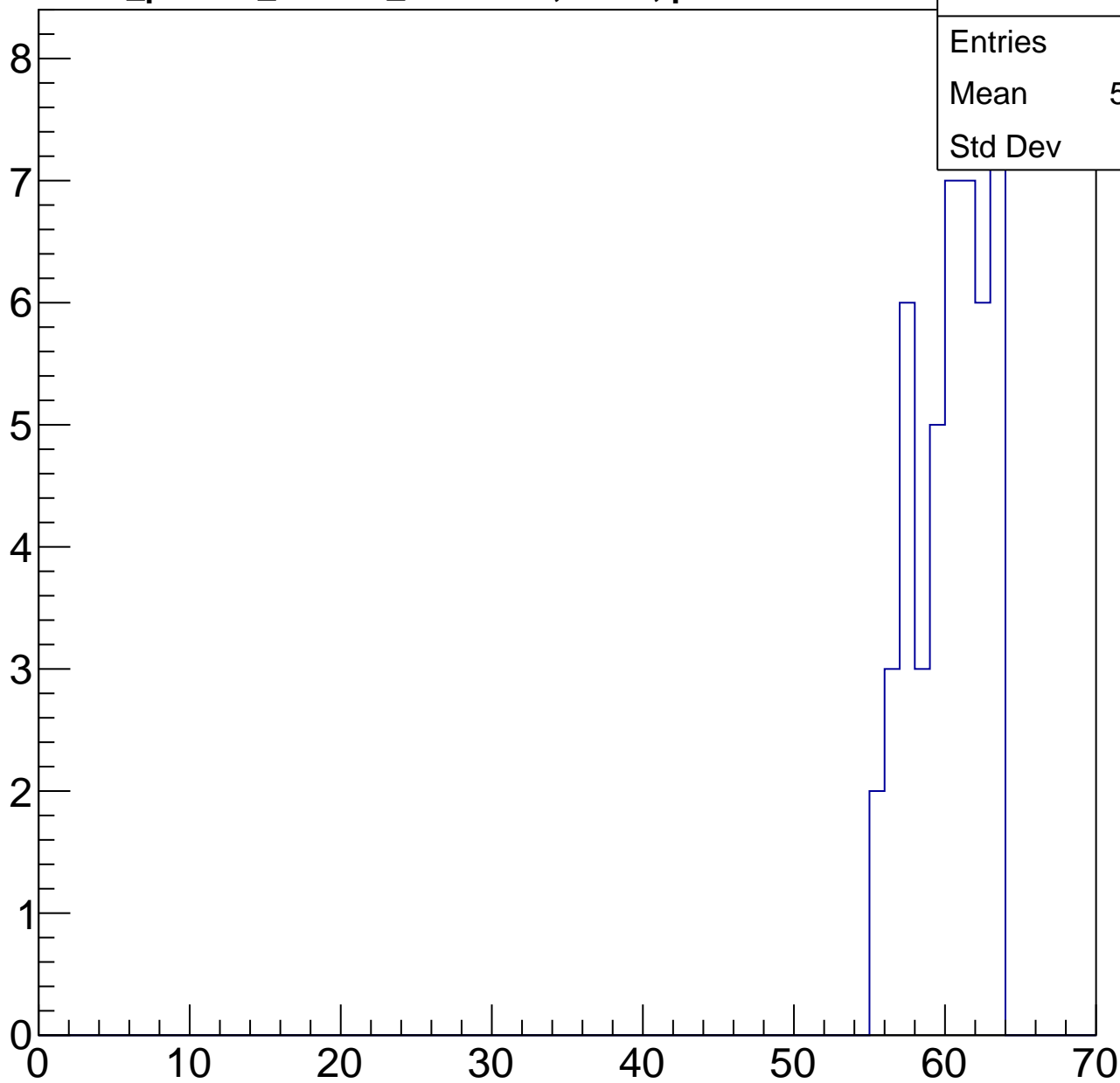
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.83
Std Dev	2.4

ampl

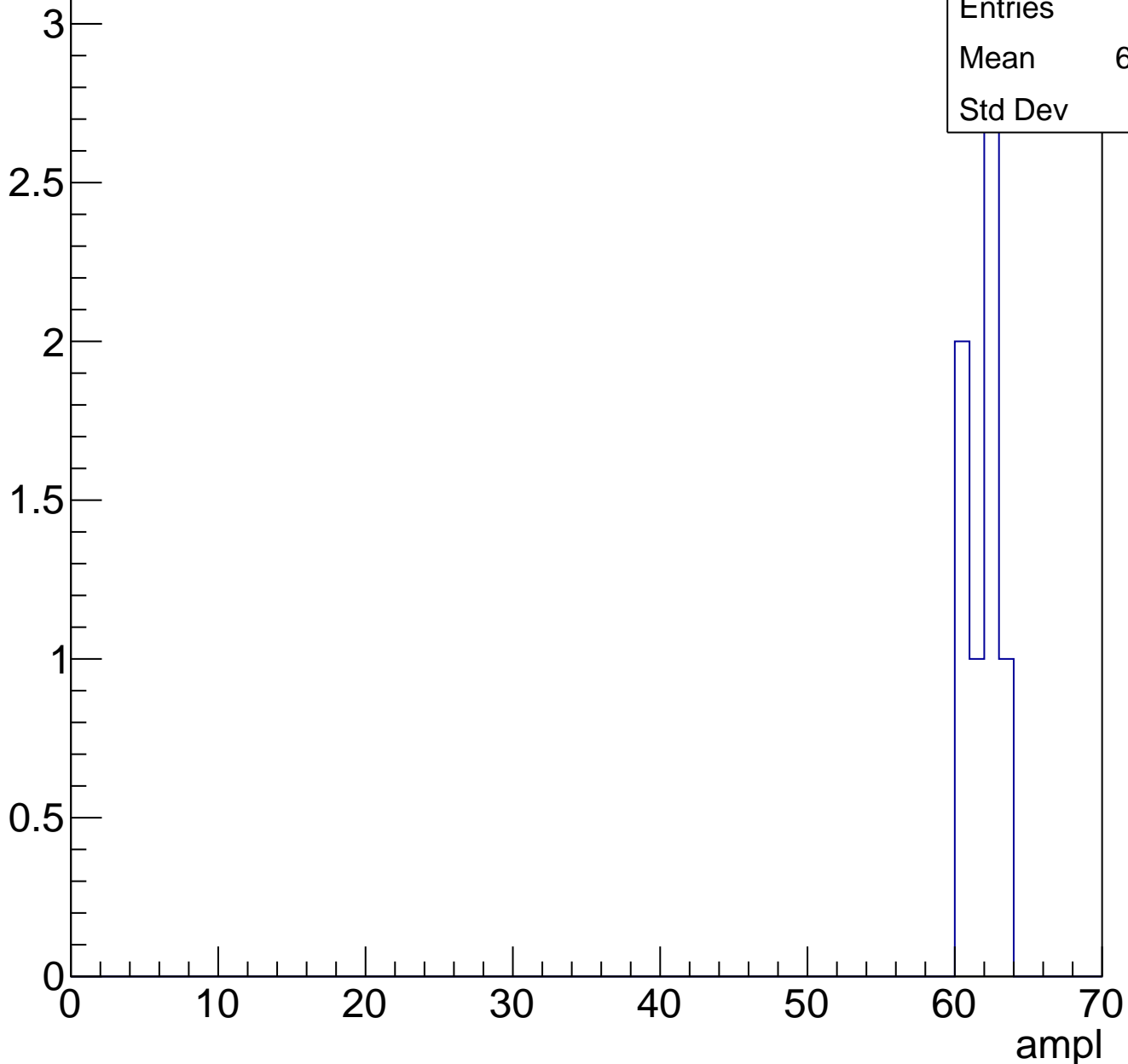
0 10 20 30 40 50 60 70



B0L000S, U2-ch94, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch94, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L000S, U2-ch95, adc0

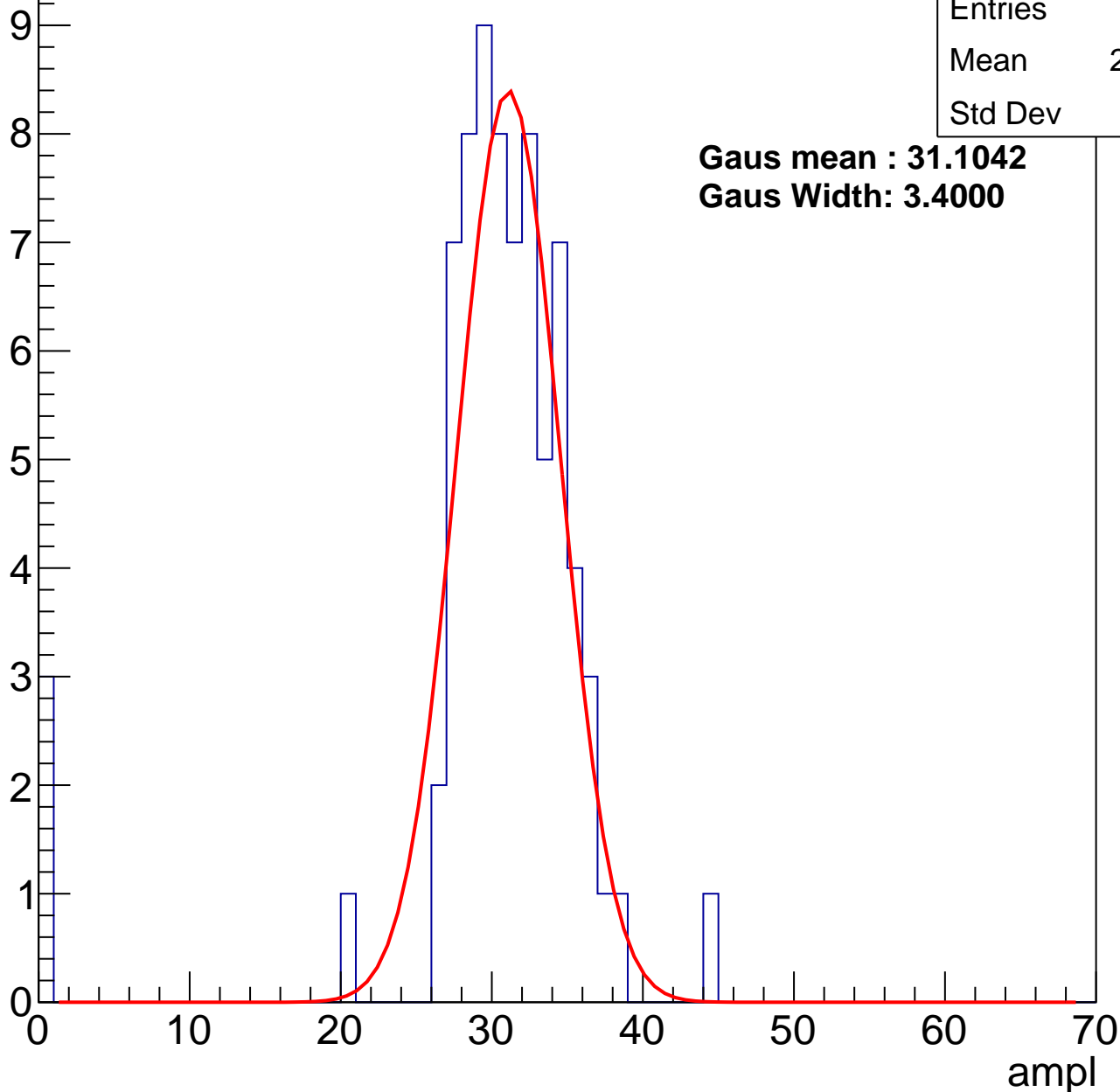
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.72
Std Dev	6.97

Gaus mean : 31.1042

Gaus Width: 3.4000



B0L000S, U2-ch95, adc1

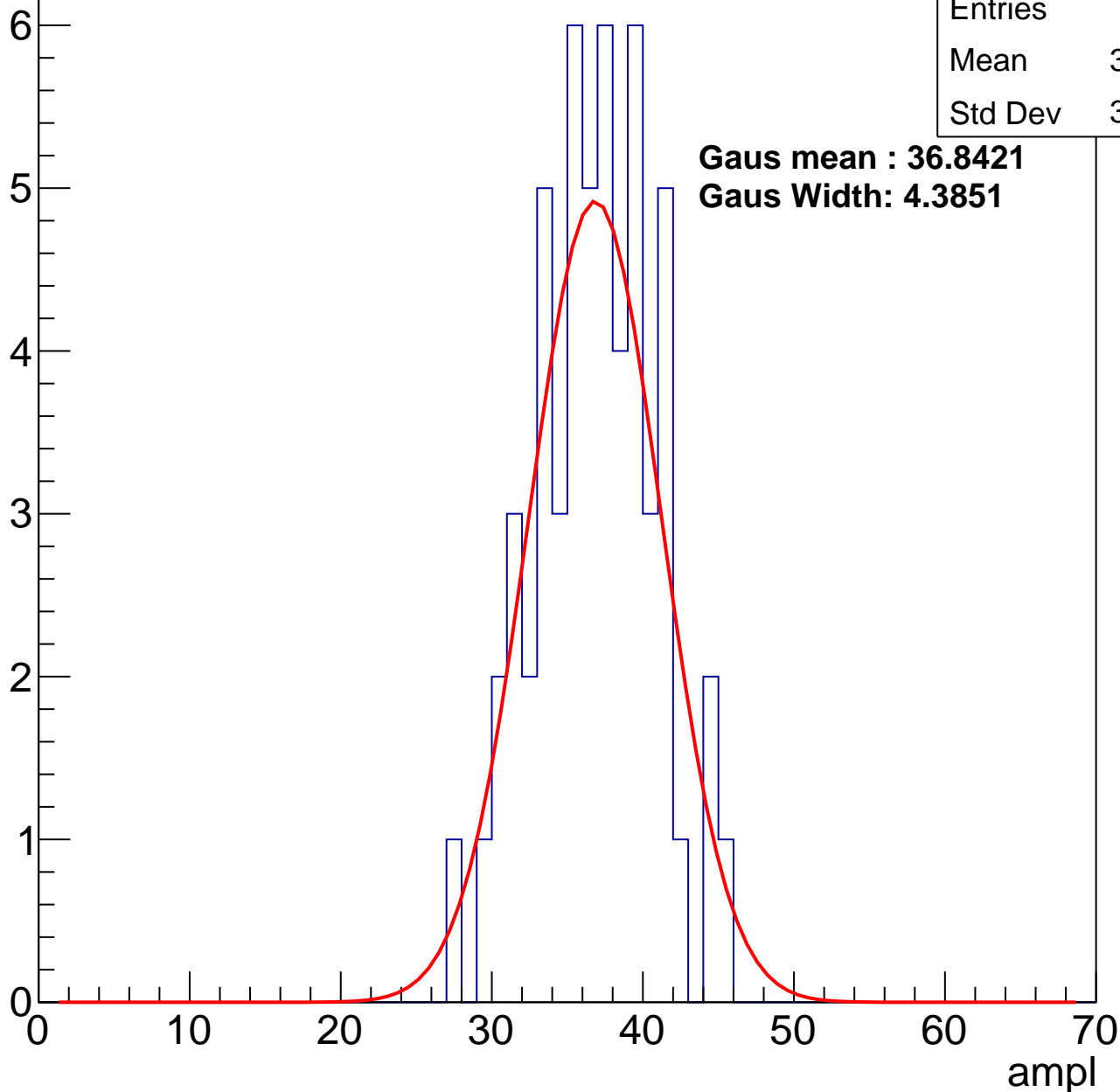
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.39
Std Dev	3.913

Gaus mean : 36.8421

Gaus Width: 4.3851



B0L000S, U2-ch95, adc2

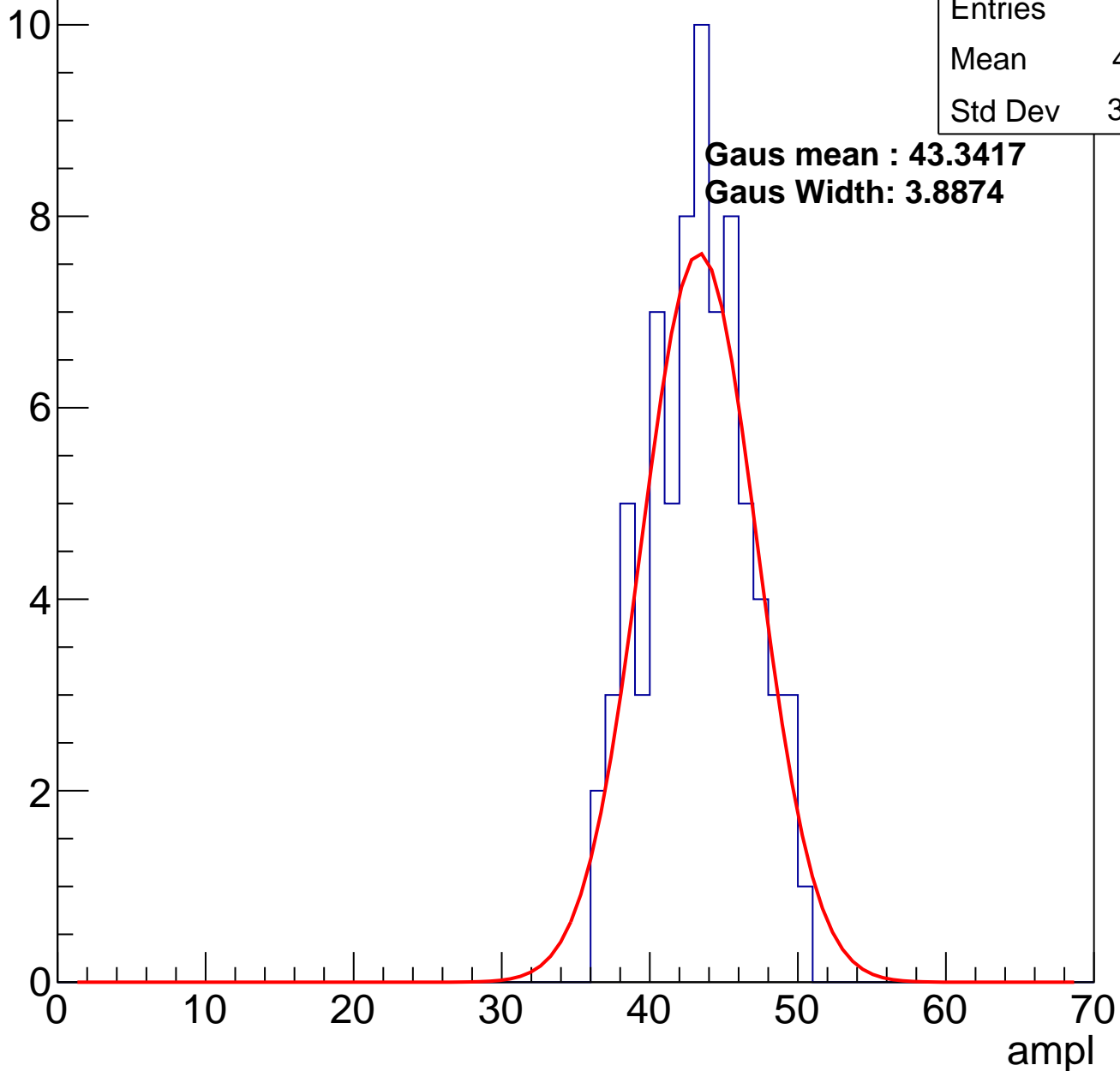
calib_packv5_042523_0143.root, FC#5, port B1

Entries	74
Mean	42.81
Std Dev	3.384

Gaus mean : 43.3417

Gaus Width: 3.8874

Entry

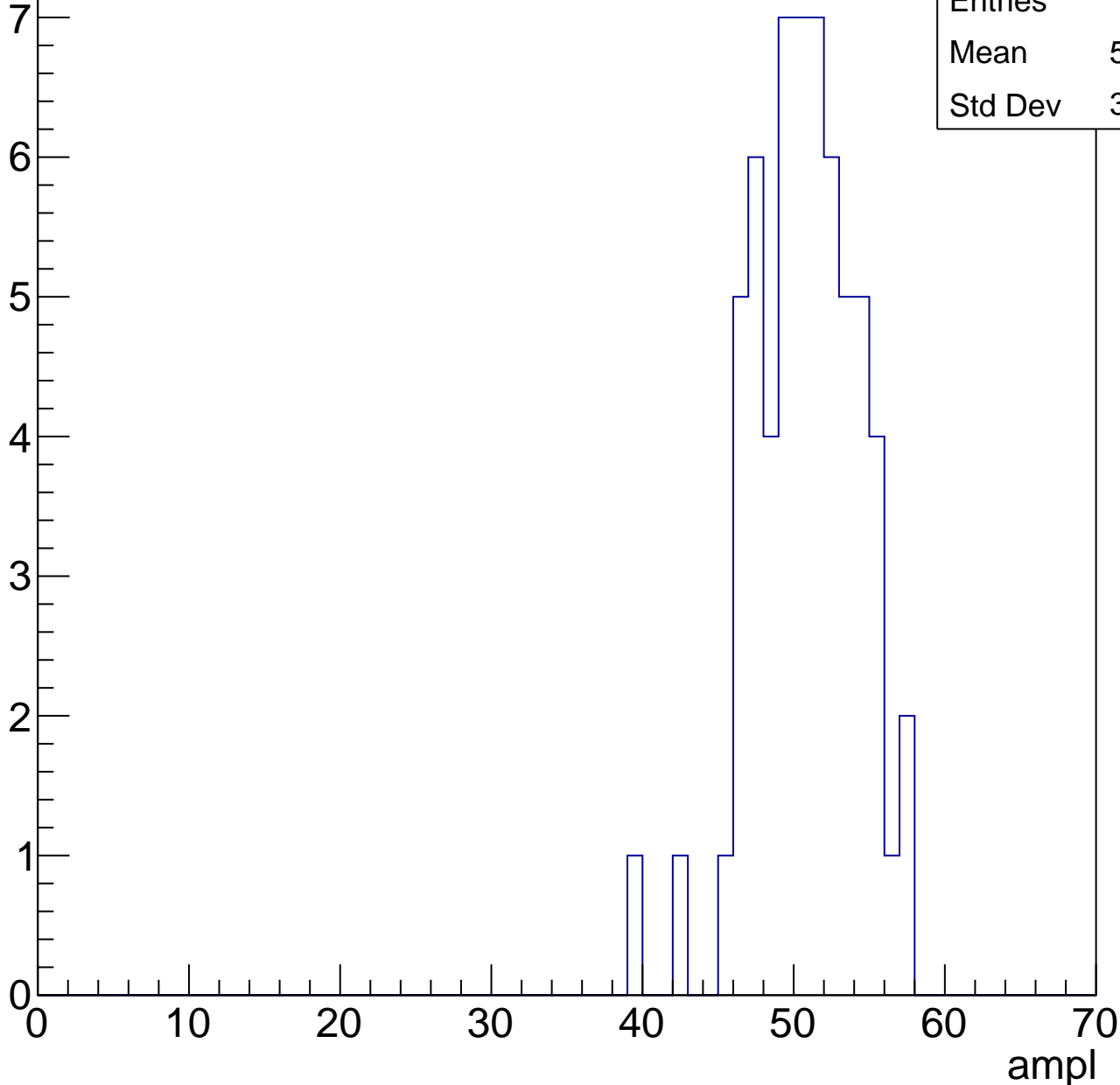


B0L000S, U2-ch95, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.27
Std Dev	3.483

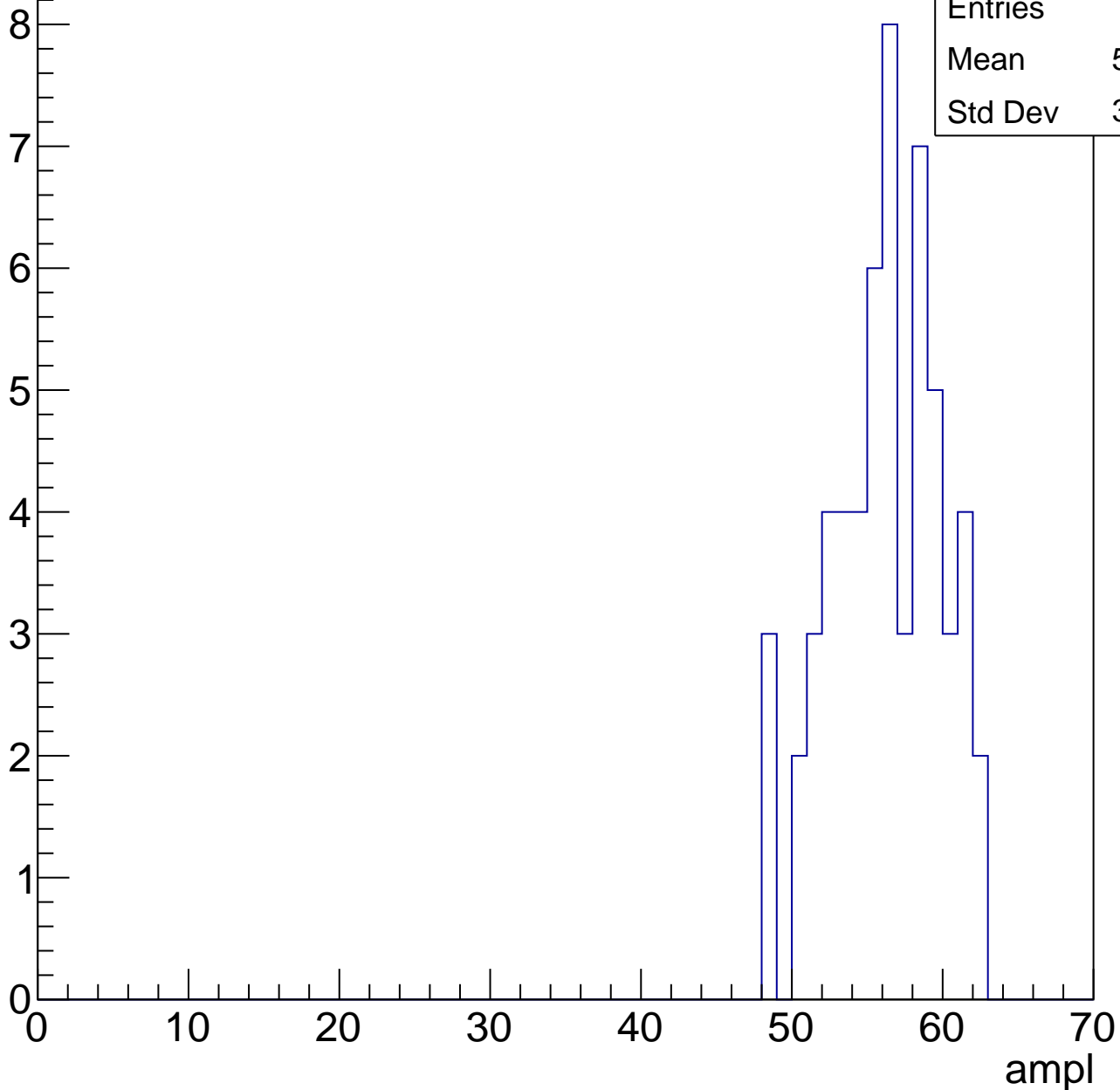


B0L000S, U2-ch95, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.71
Std Dev	3.601

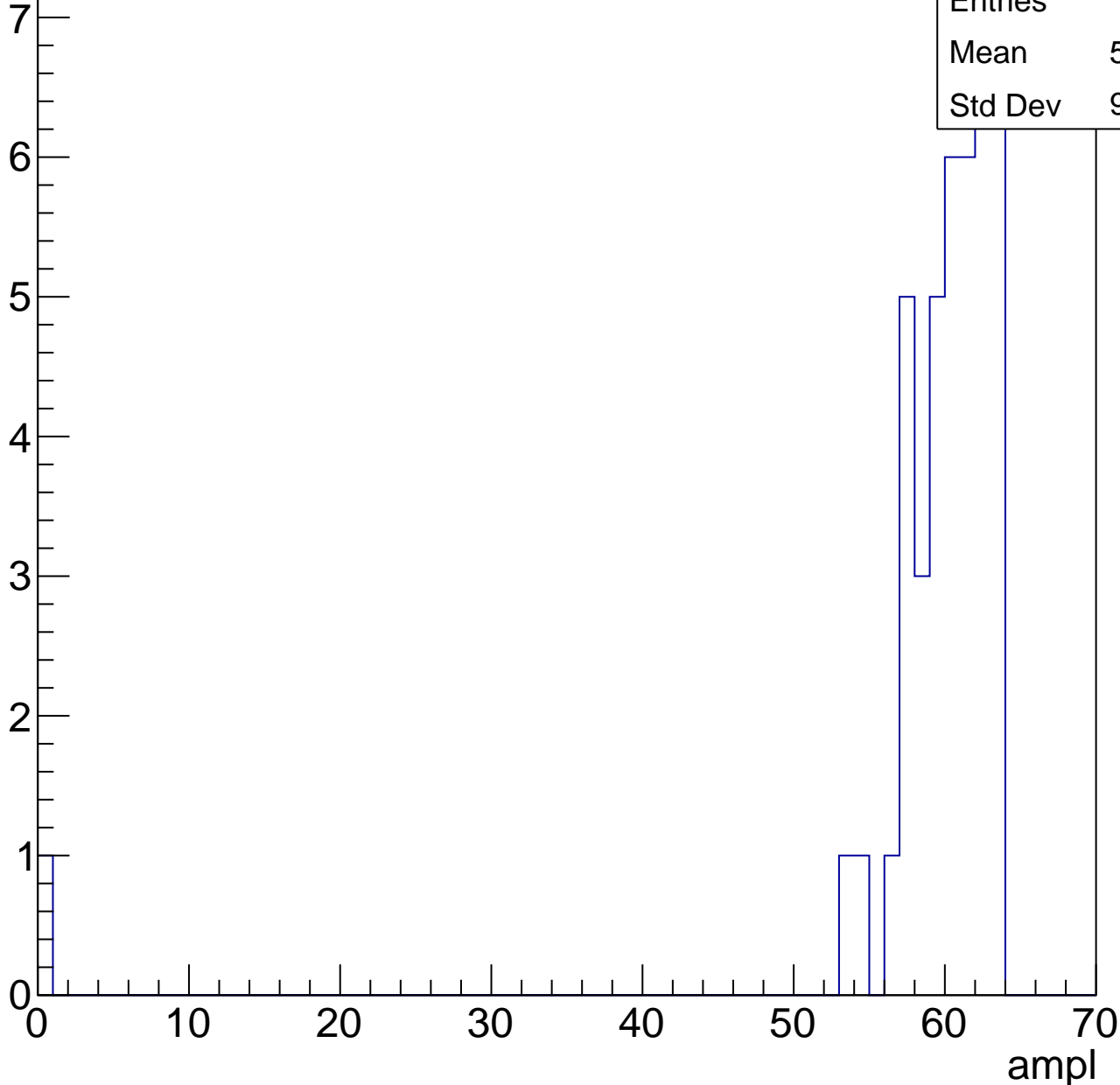


B0L000S, U2-ch95, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.56
Std Dev	9.364



B0L000S, U2-ch95, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch95, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch96, adc0

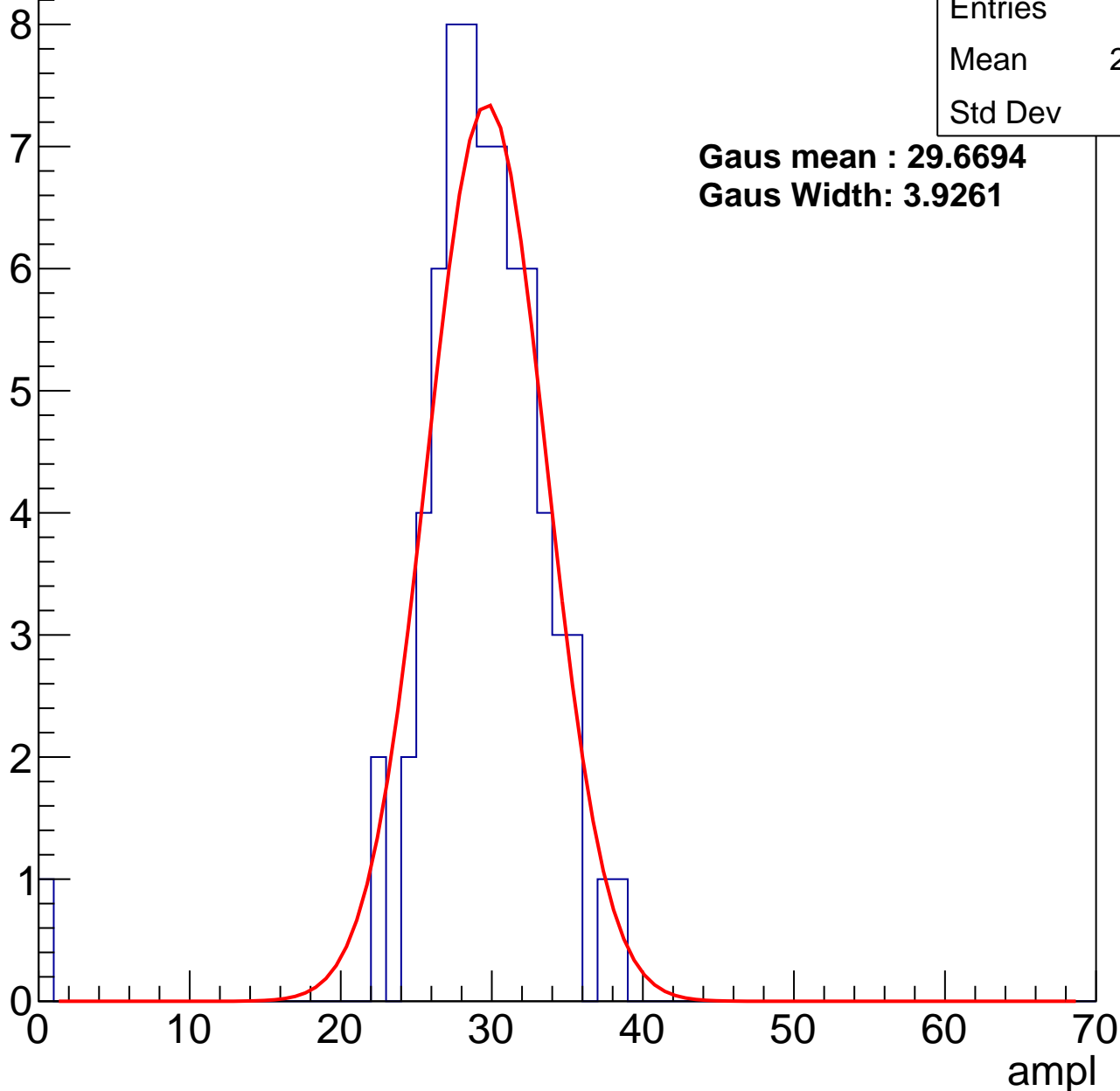
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	28.88
Std Dev	4.85

Gaus mean : 29.6694

Gaus Width: 3.9261



B0L000S, U2-ch96, adc1

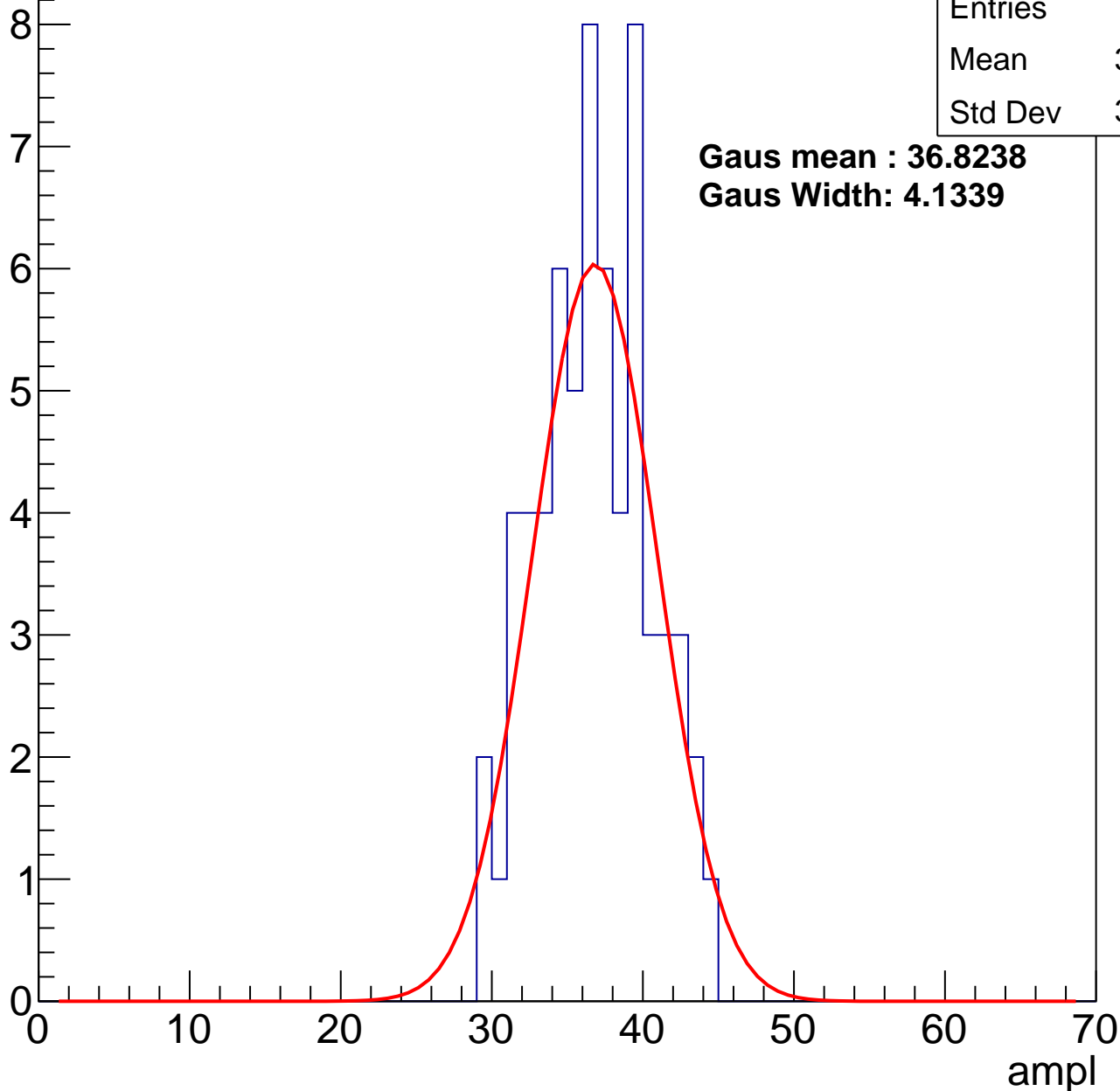
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.31
Std Dev	3.631

Gaus mean : 36.8238

Gaus Width: 4.1339



B0L000S, U2-ch96, adc2

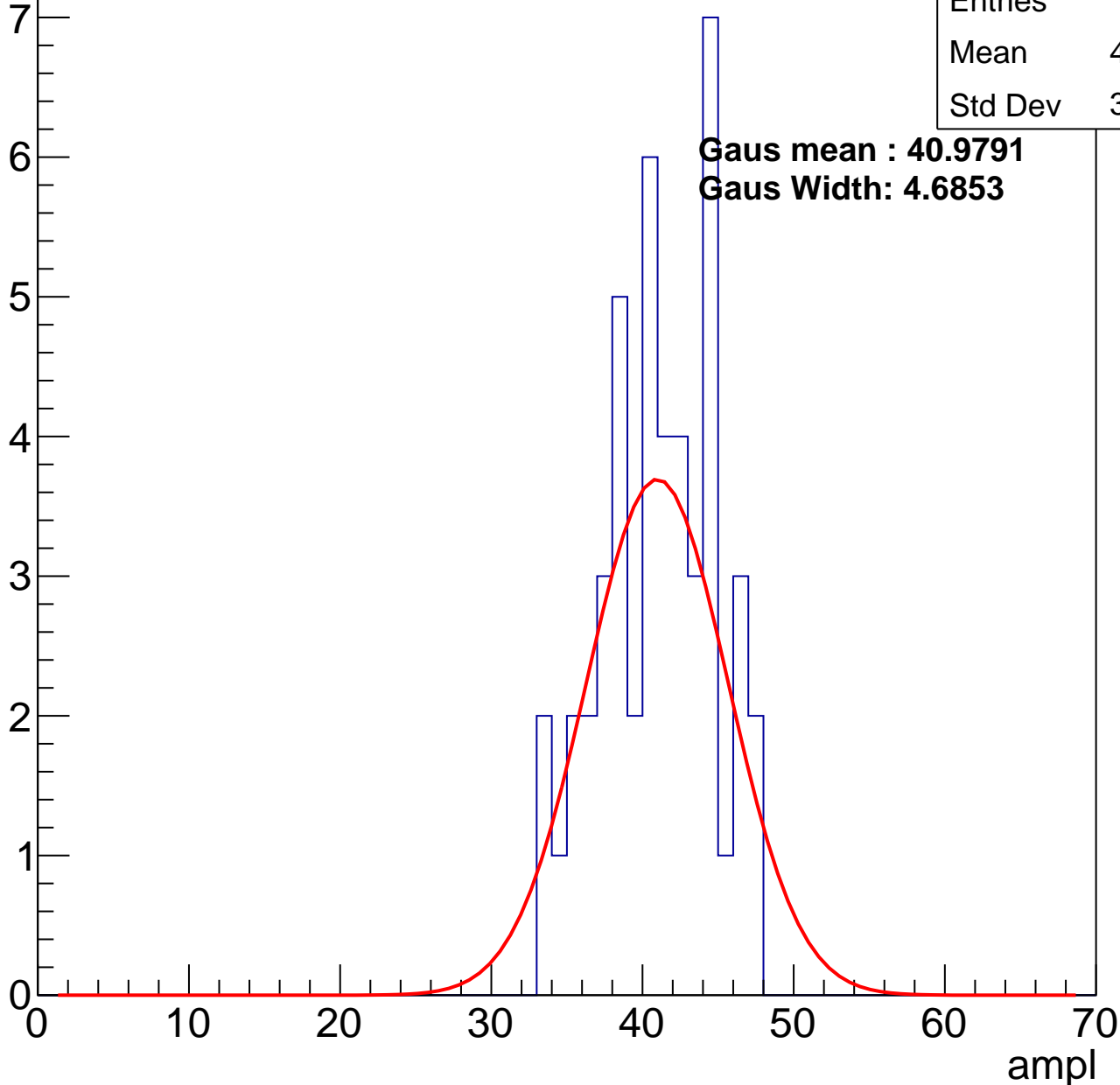
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	40.57
Std Dev	3.689

Gaus mean : 40.9791

Gaus Width: 4.6853

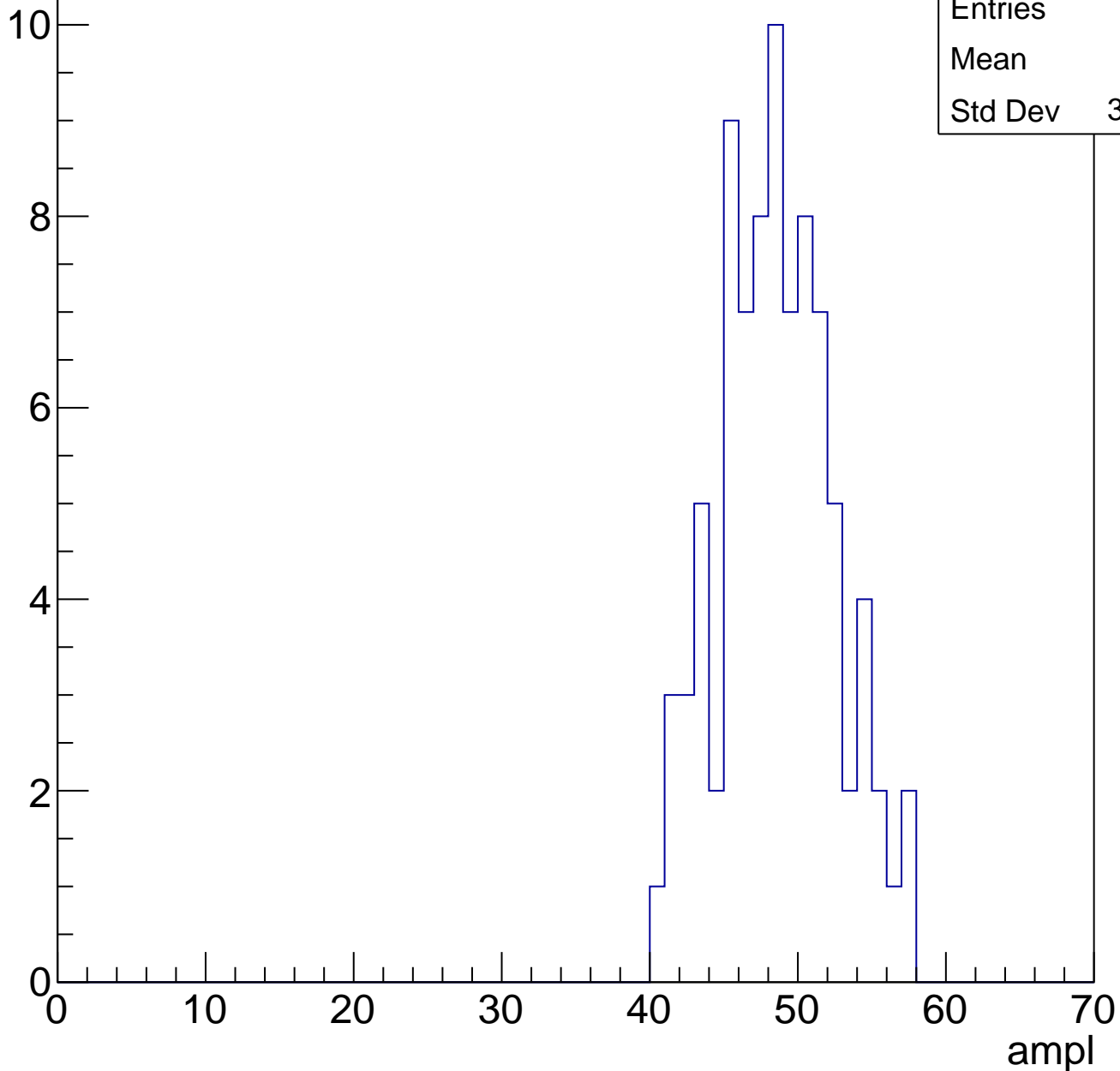


B0L000S, U2-ch96, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	86
Mean	48.1
Std Dev	3.876

Entry

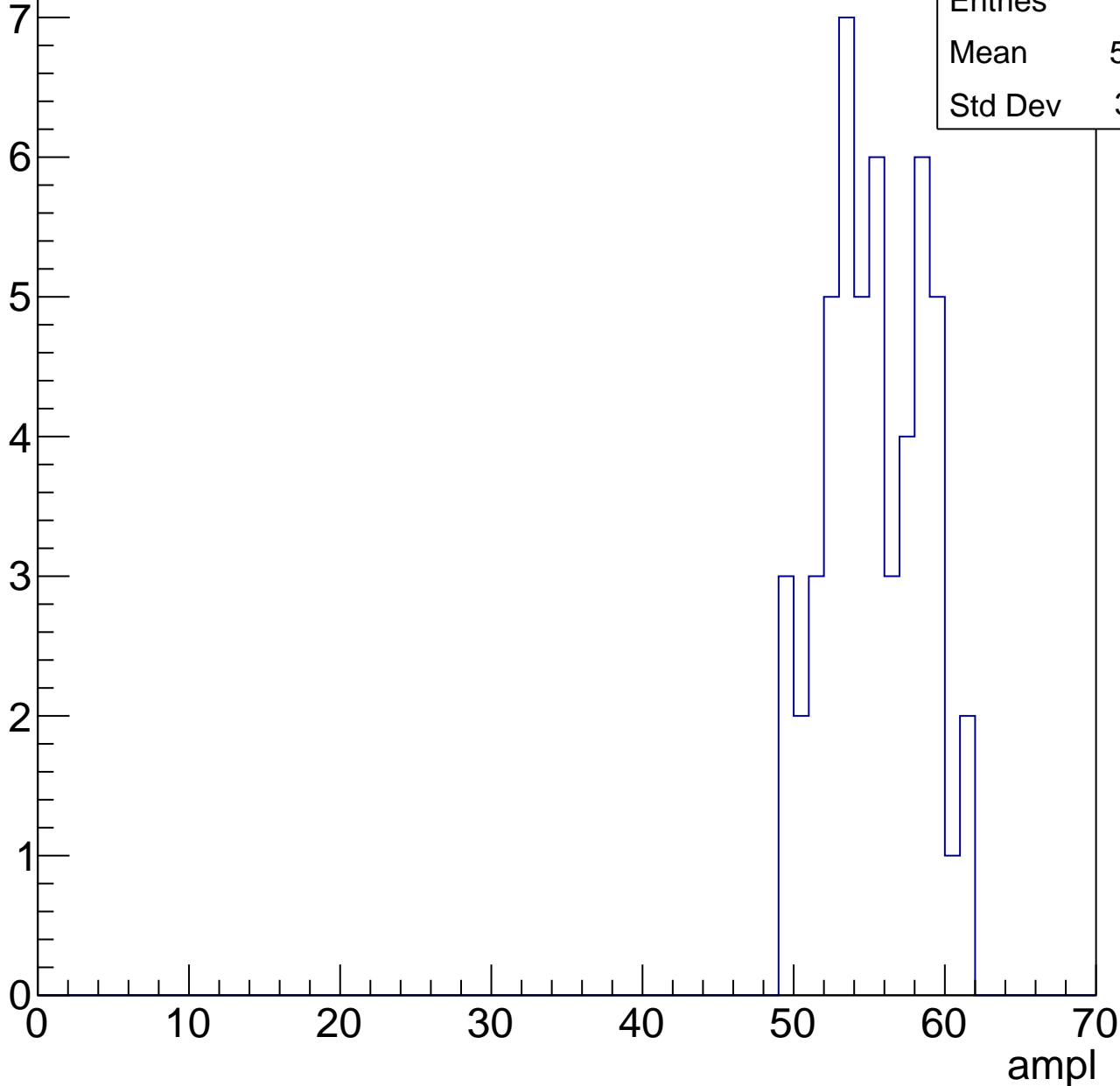


B0L000S, U2-ch96, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	52
Mean	54.85
Std Dev	3.201

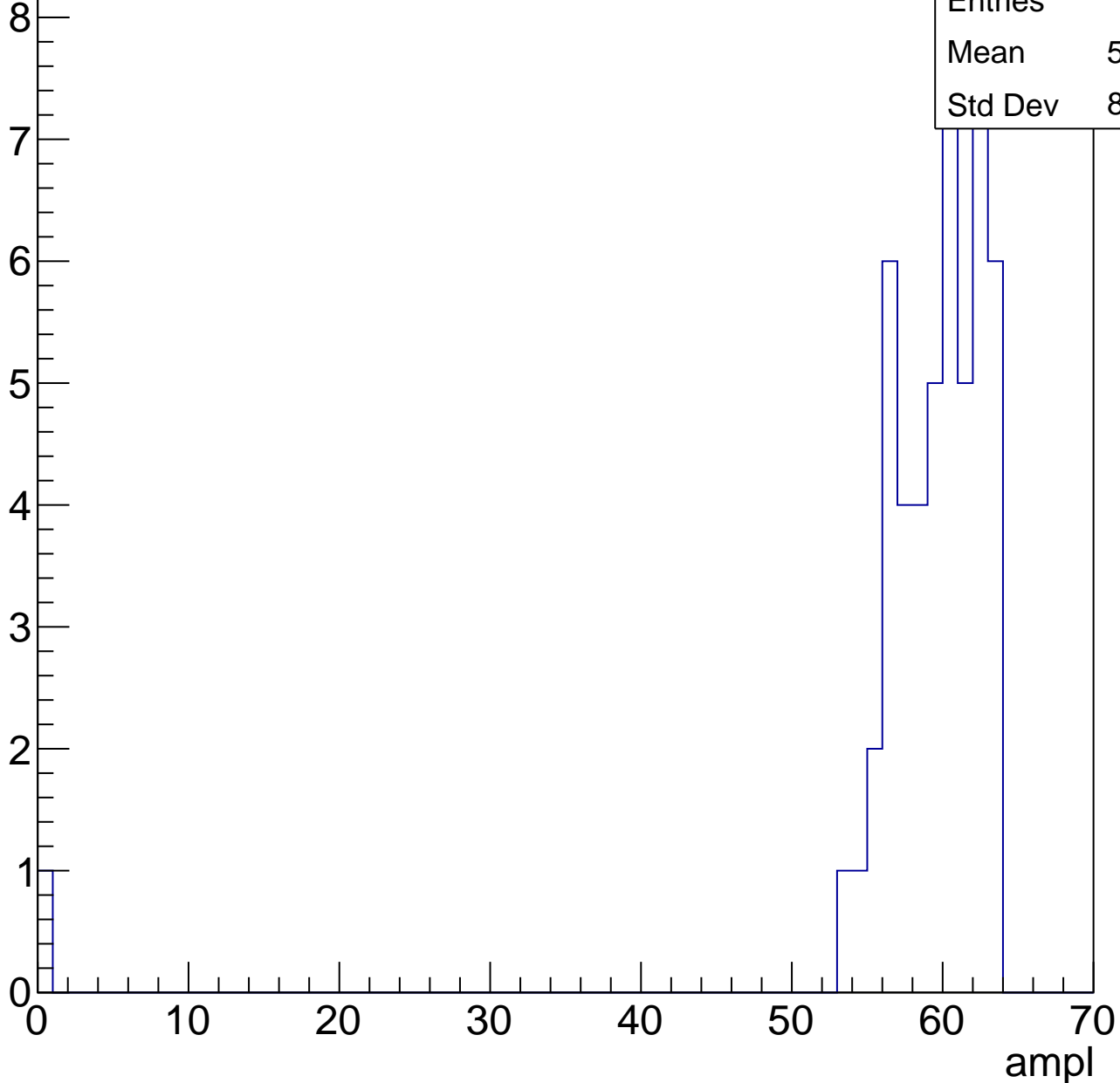


B0L000S, U2-ch96, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

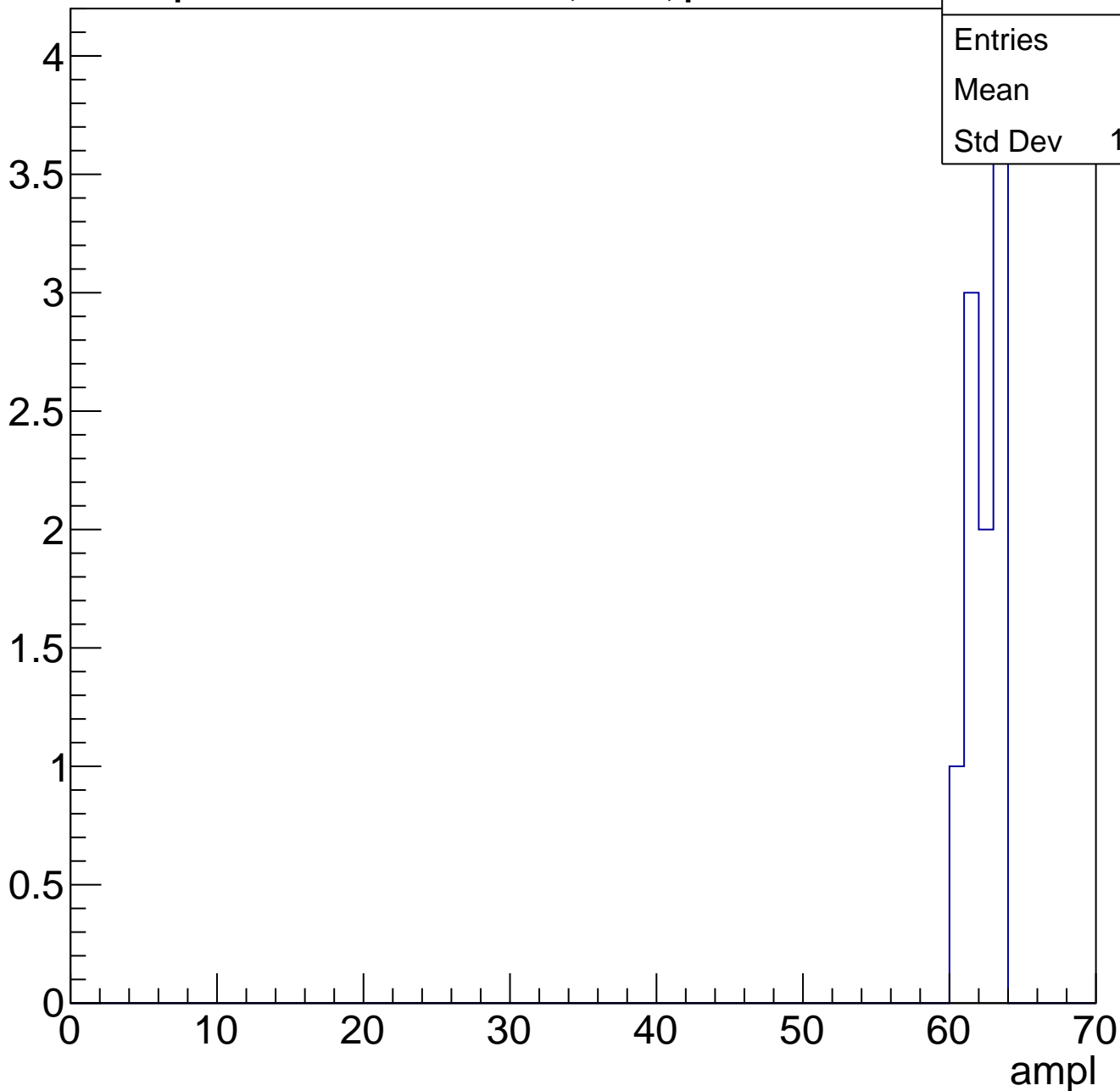
Entries	51
Mean	58.18
Std Dev	8.643



B0L000S, U2-ch96, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	10
Mean	61.9
Std Dev	1.044

B0L000S, U2-ch96, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch97, adc0

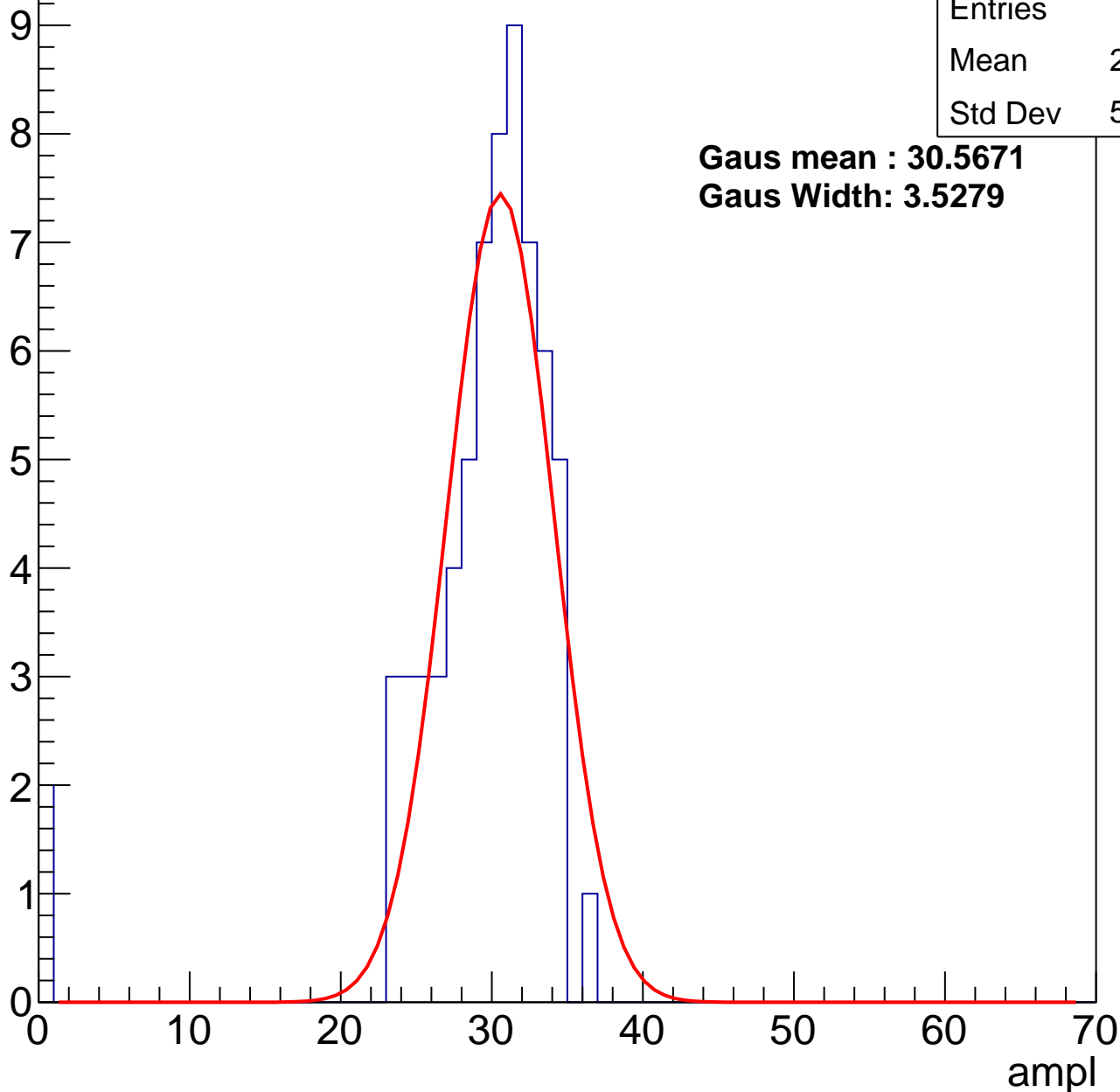
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.67
Std Dev	5.942

Gaus mean : 30.5671

Gaus Width: 3.5279



B0L000S, U2-ch97, adc1

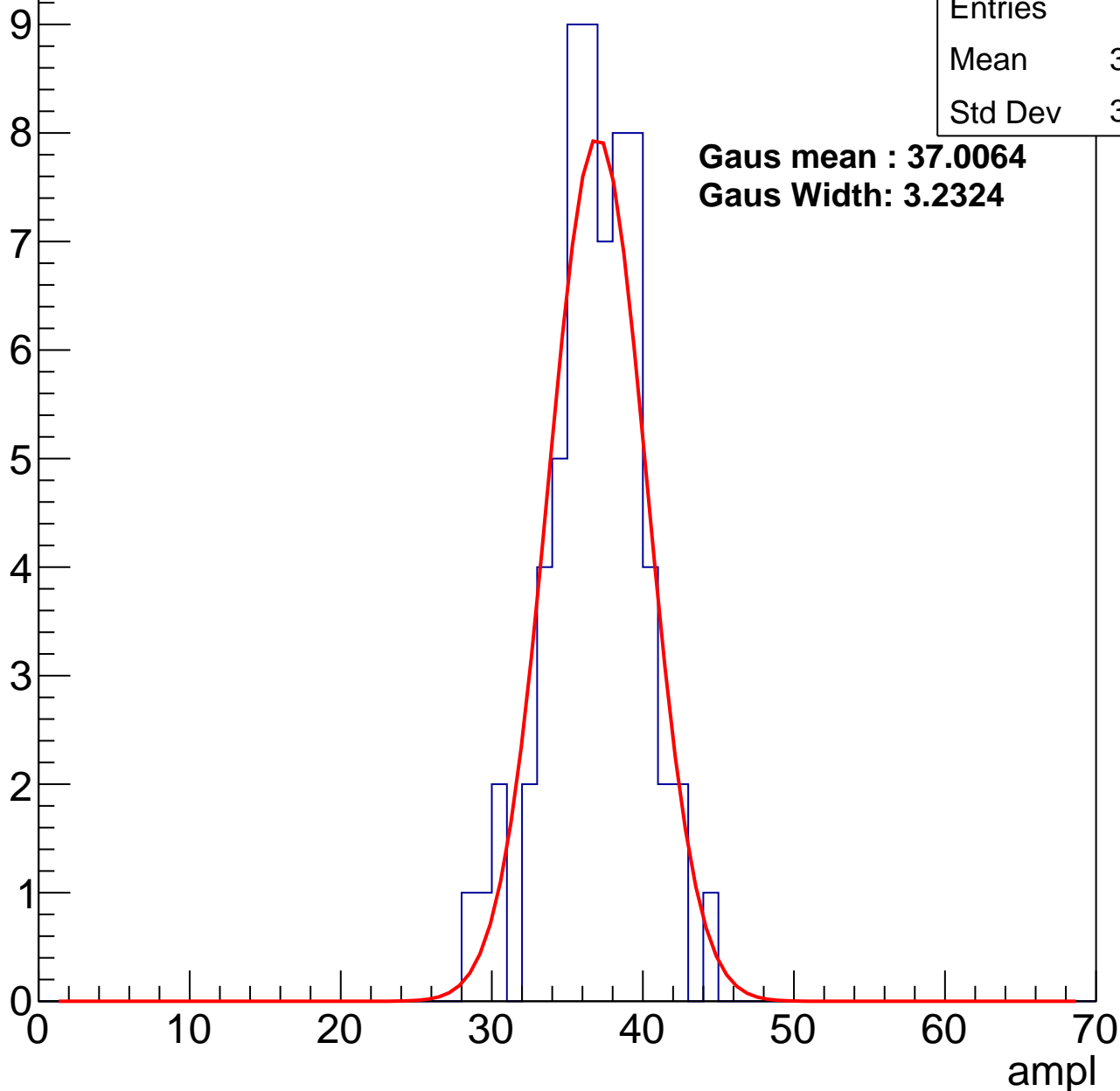
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.42
Std Dev	3.118

Gaus mean : 37.0064

Gaus Width: 3.2324



B0L000S, U2-ch97, adc2

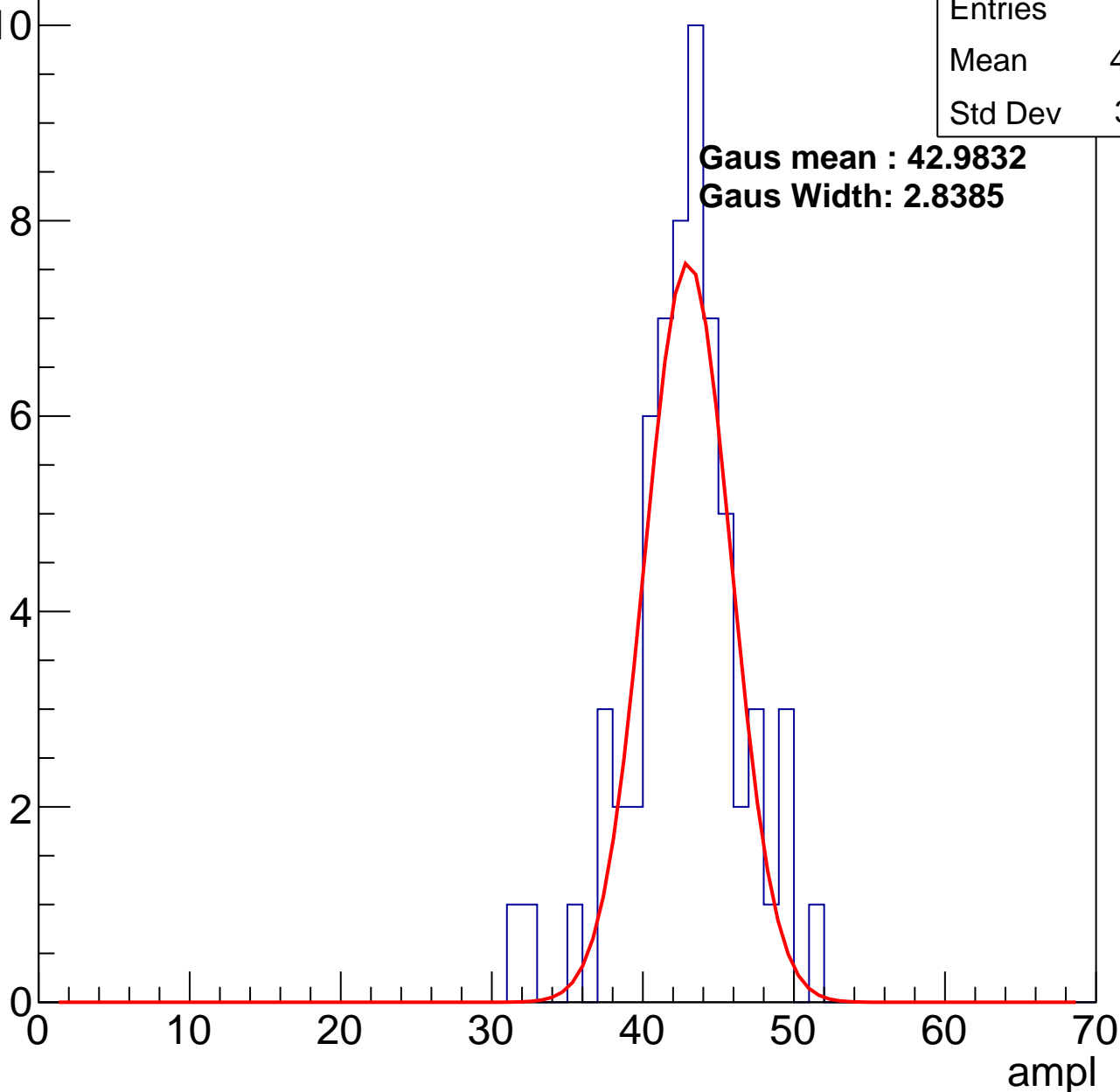
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.35
Std Dev	3.721

Gaus mean : 42.9832

Gaus Width: 2.8385

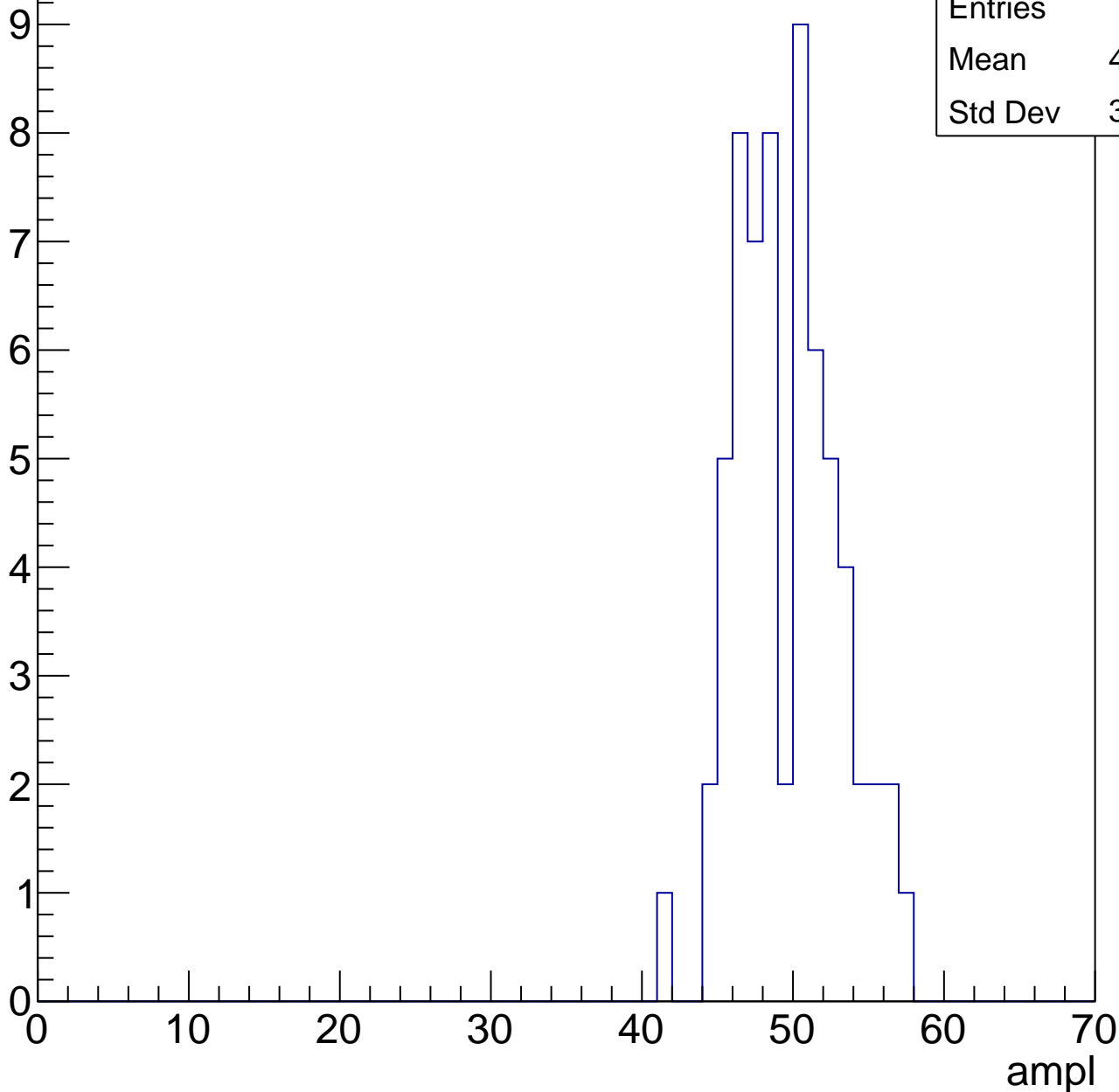


B0L000S, U2-ch97, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.19
Std Dev	3.367

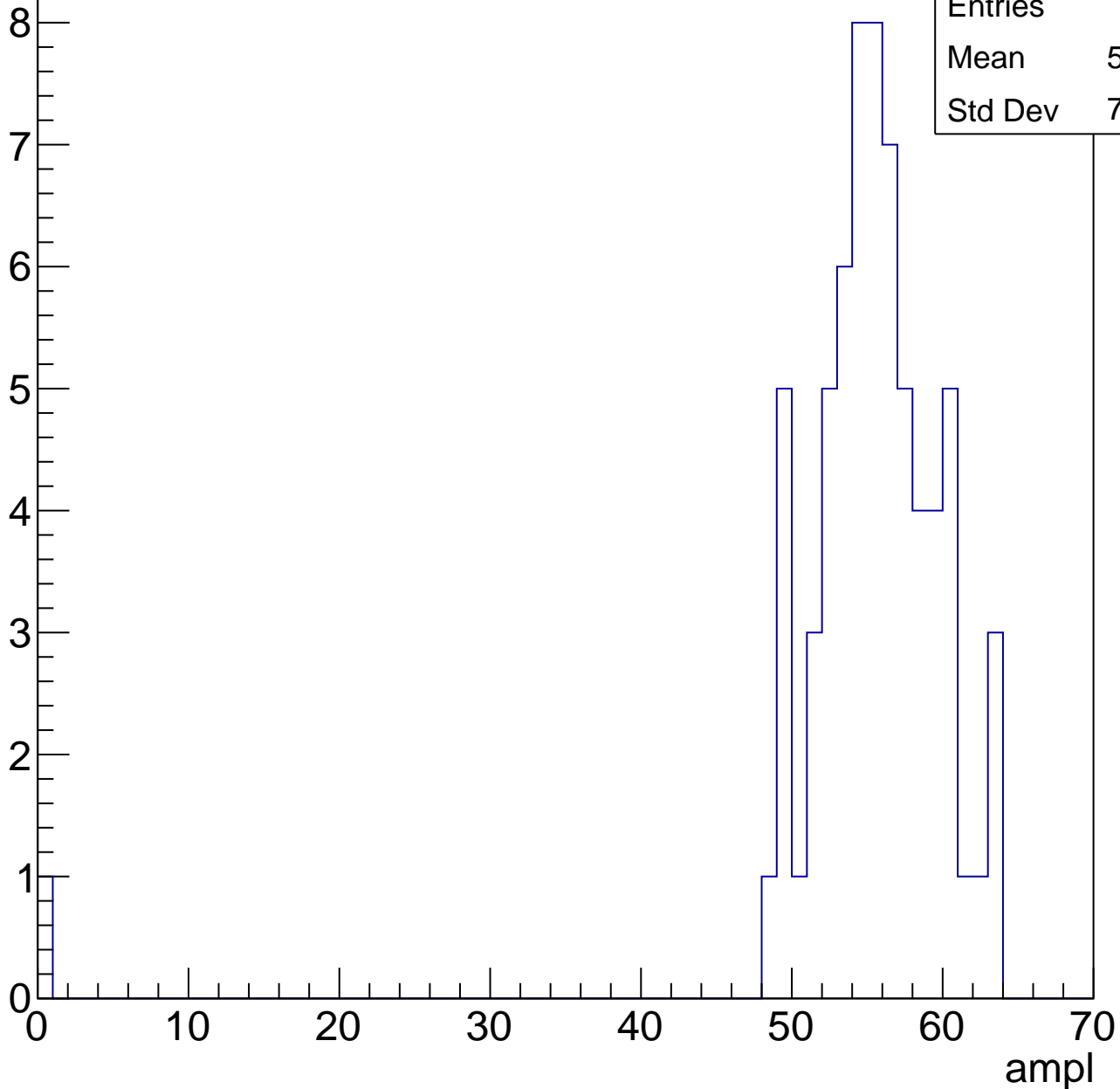


B0L000S, U2-ch97, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	54.46
Std Dev	7.586

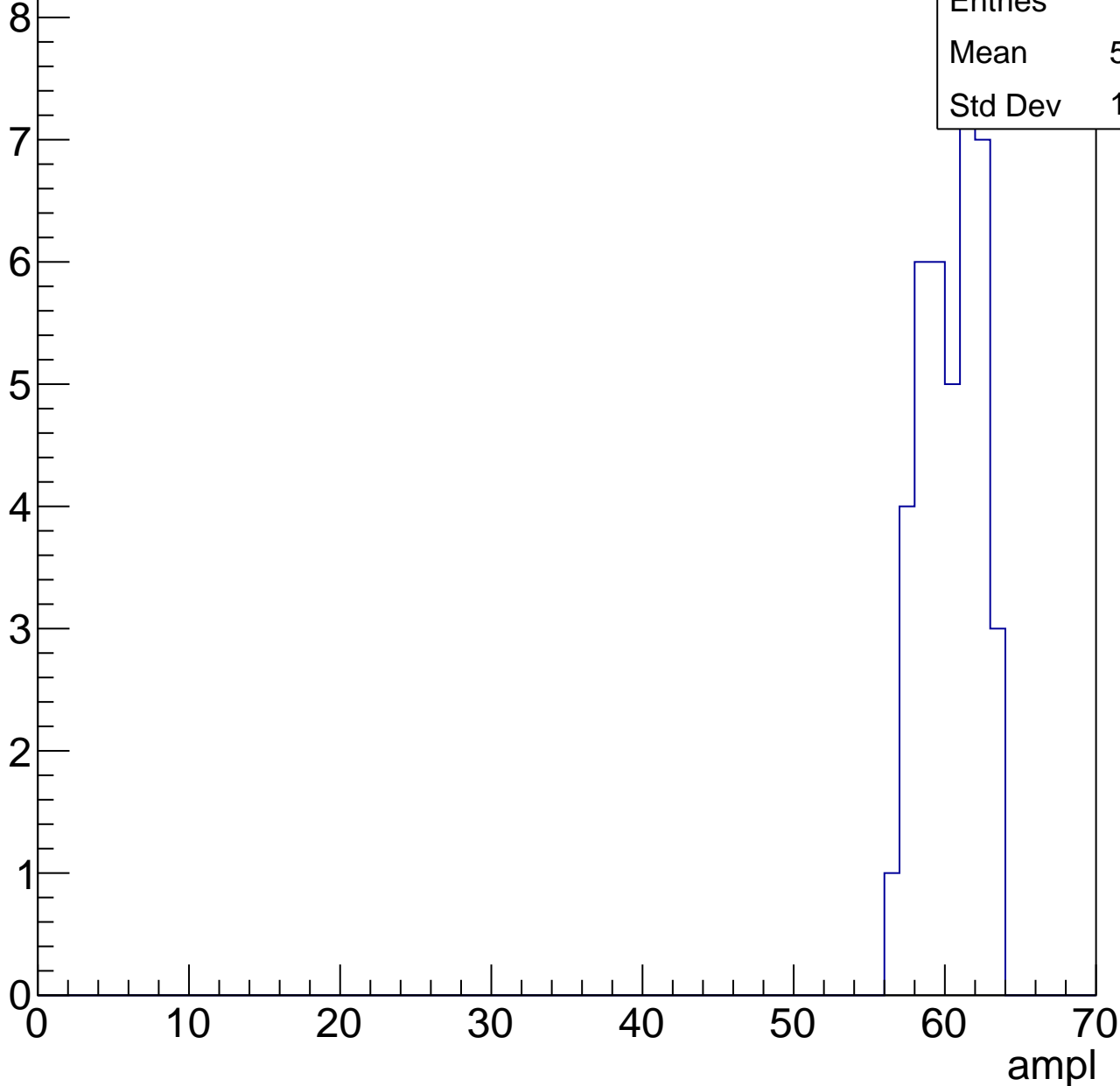


B0L000S, U2-ch97, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

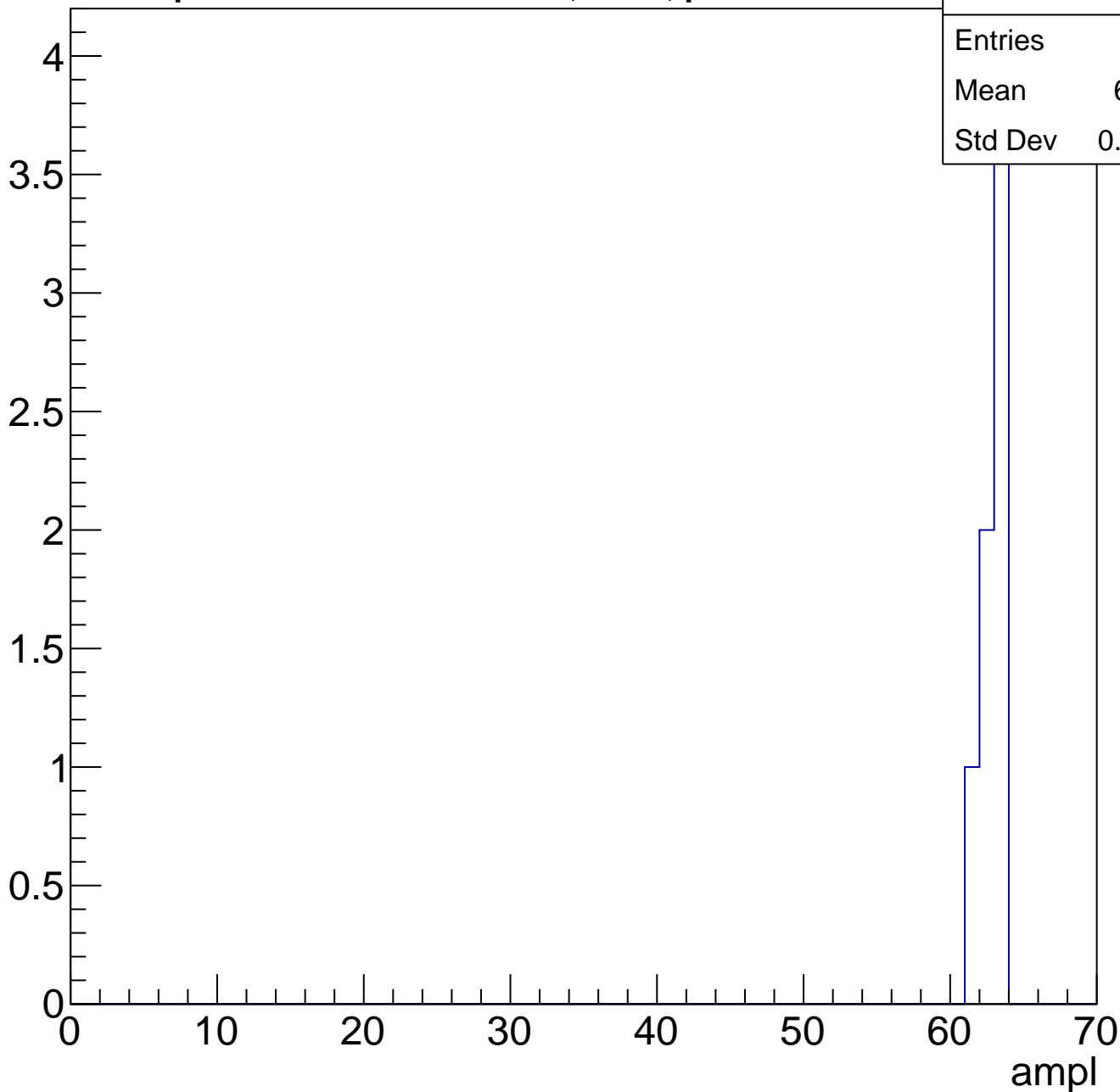
Entries	40
Mean	59.92
Std Dev	1.902



B0L000S, U2-ch97, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch97, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch98, adc0

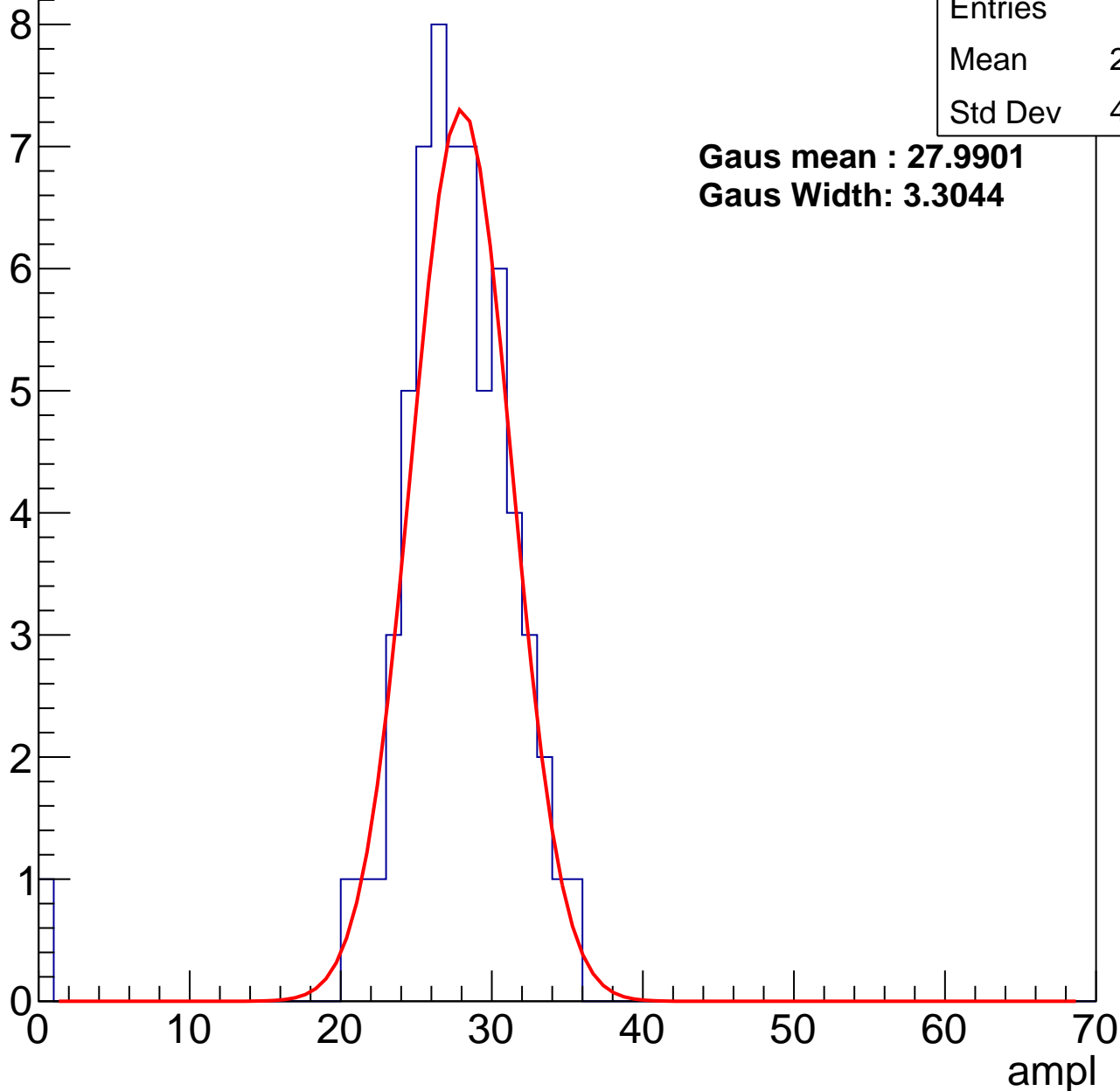
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	26.98
Std Dev	4.668

Gaus mean : 27.9901

Gaus Width: 3.3044



B0L000S, U2-ch98, adc1

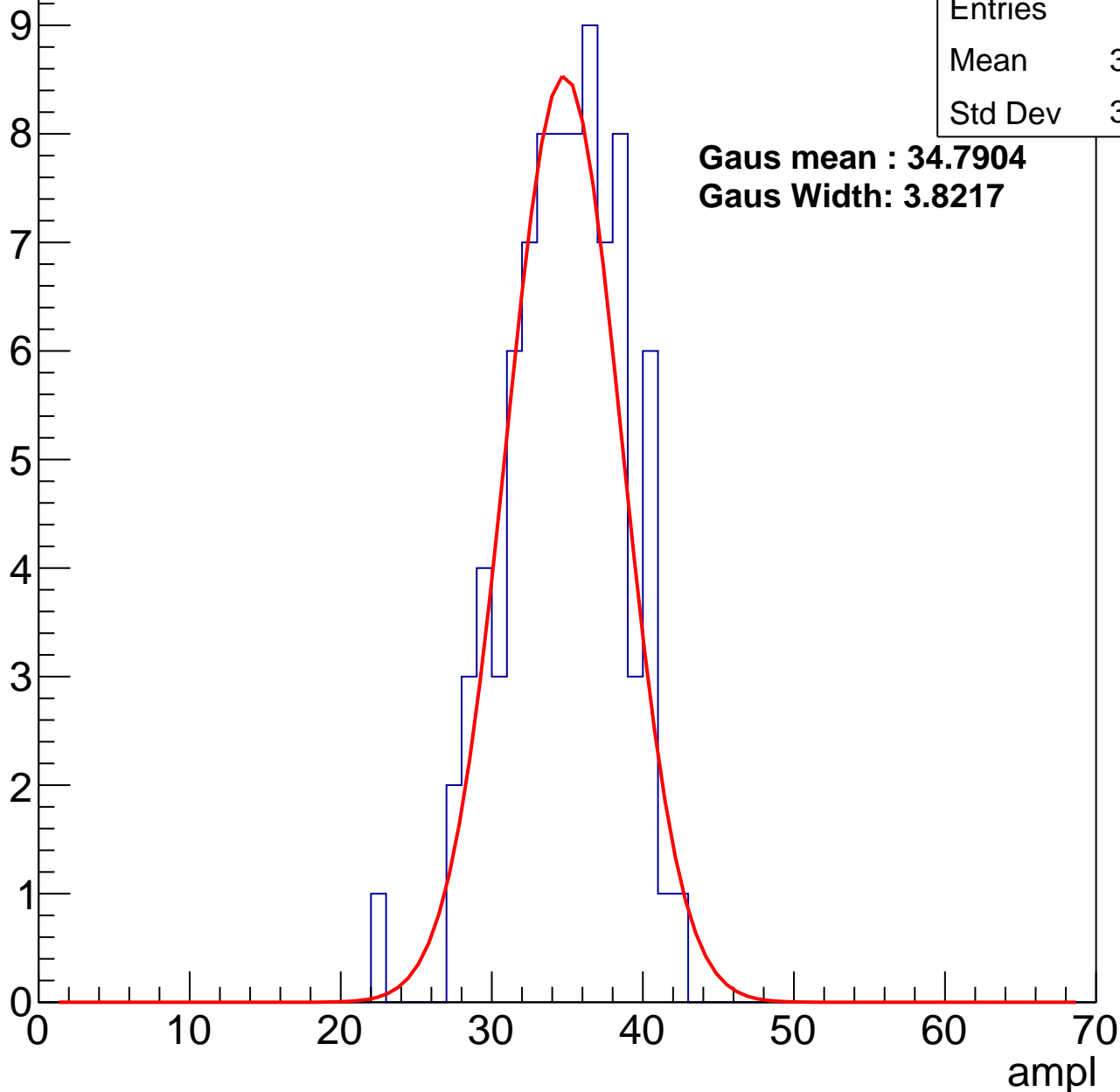
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	85
Mean	34.34
Std Dev	3.784

Gaus mean : 34.7904

Gaus Width: 3.8217



B0L000S, U2-ch98, adc2

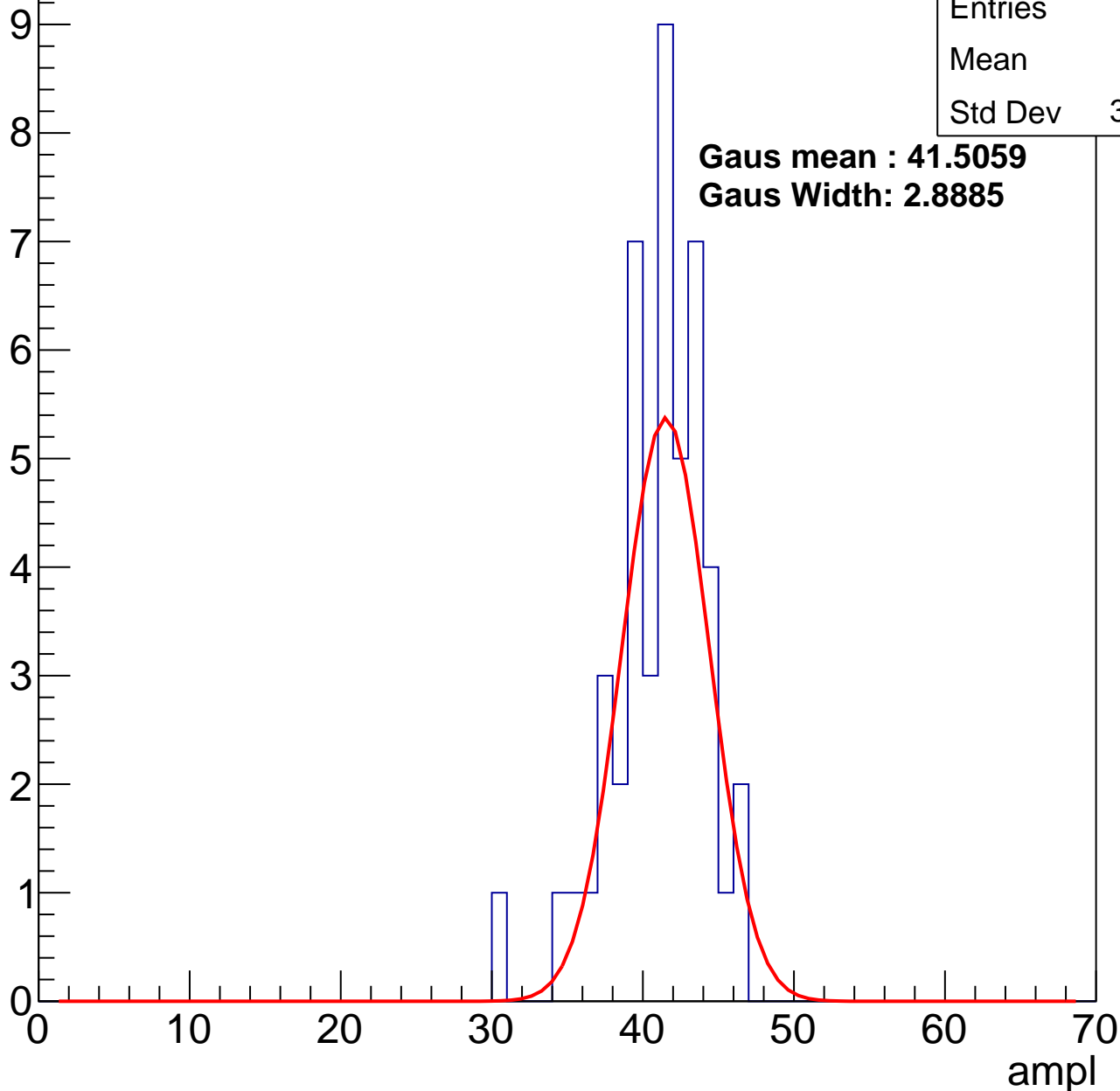
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	40.6
Std Dev	3.112

Gaus mean : 41.5059

Gaus Width: 2.8885

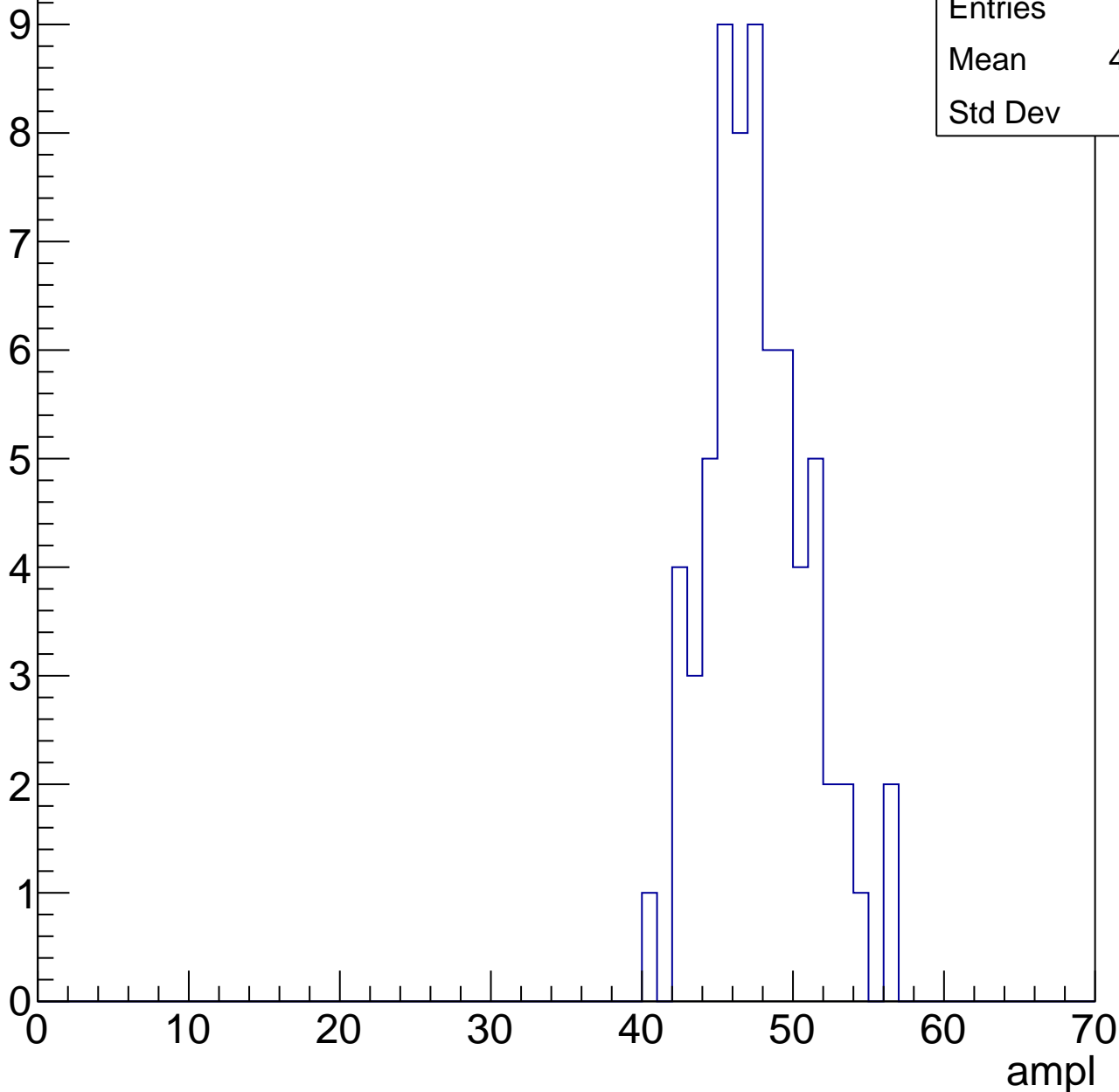


B0L000S, U2-ch98, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	47.25
Std Dev	3.37

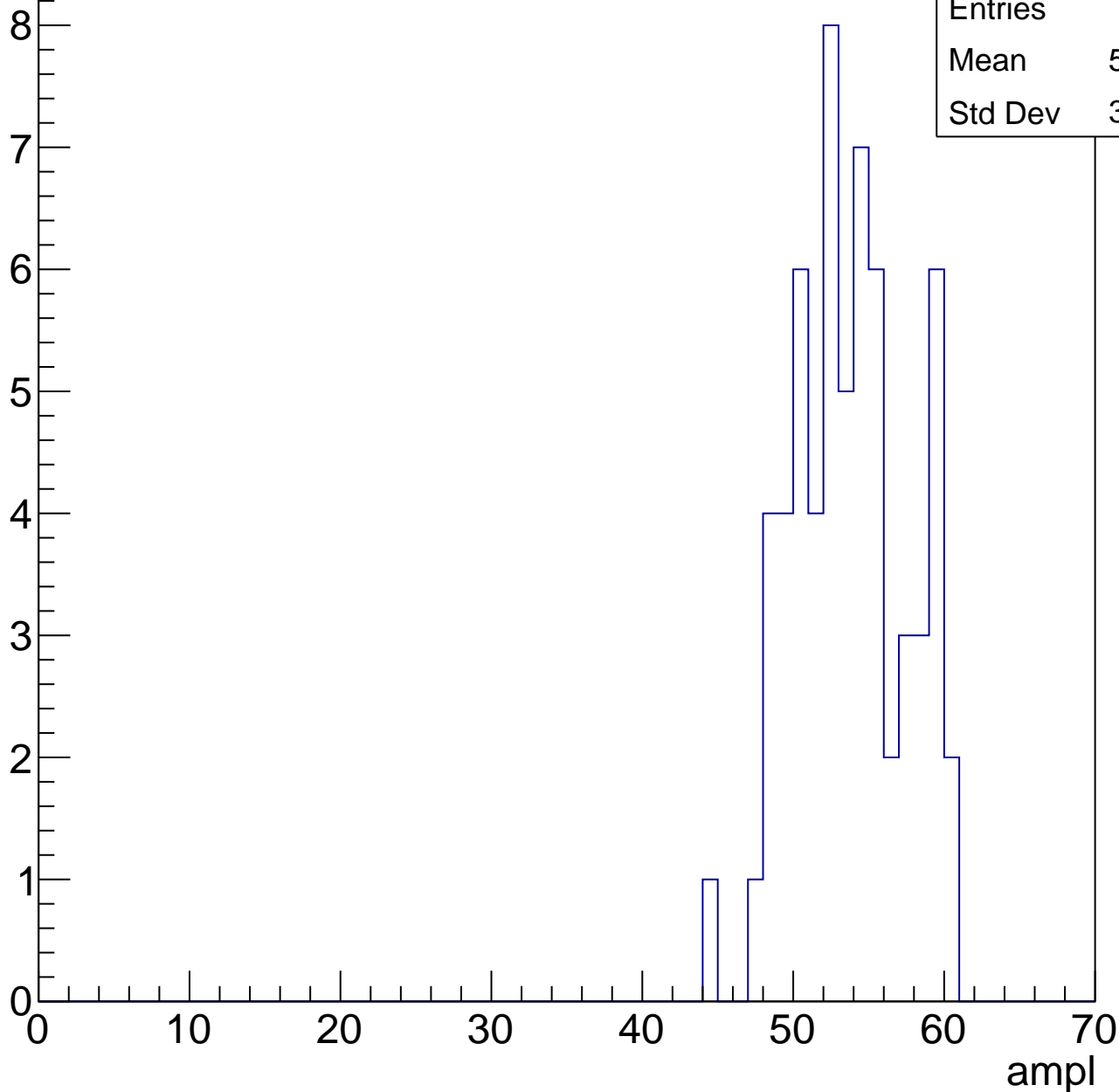


B0L000S, U2-ch98, adc4

calib_packv5_042523_0143.root, FC#5, port B1

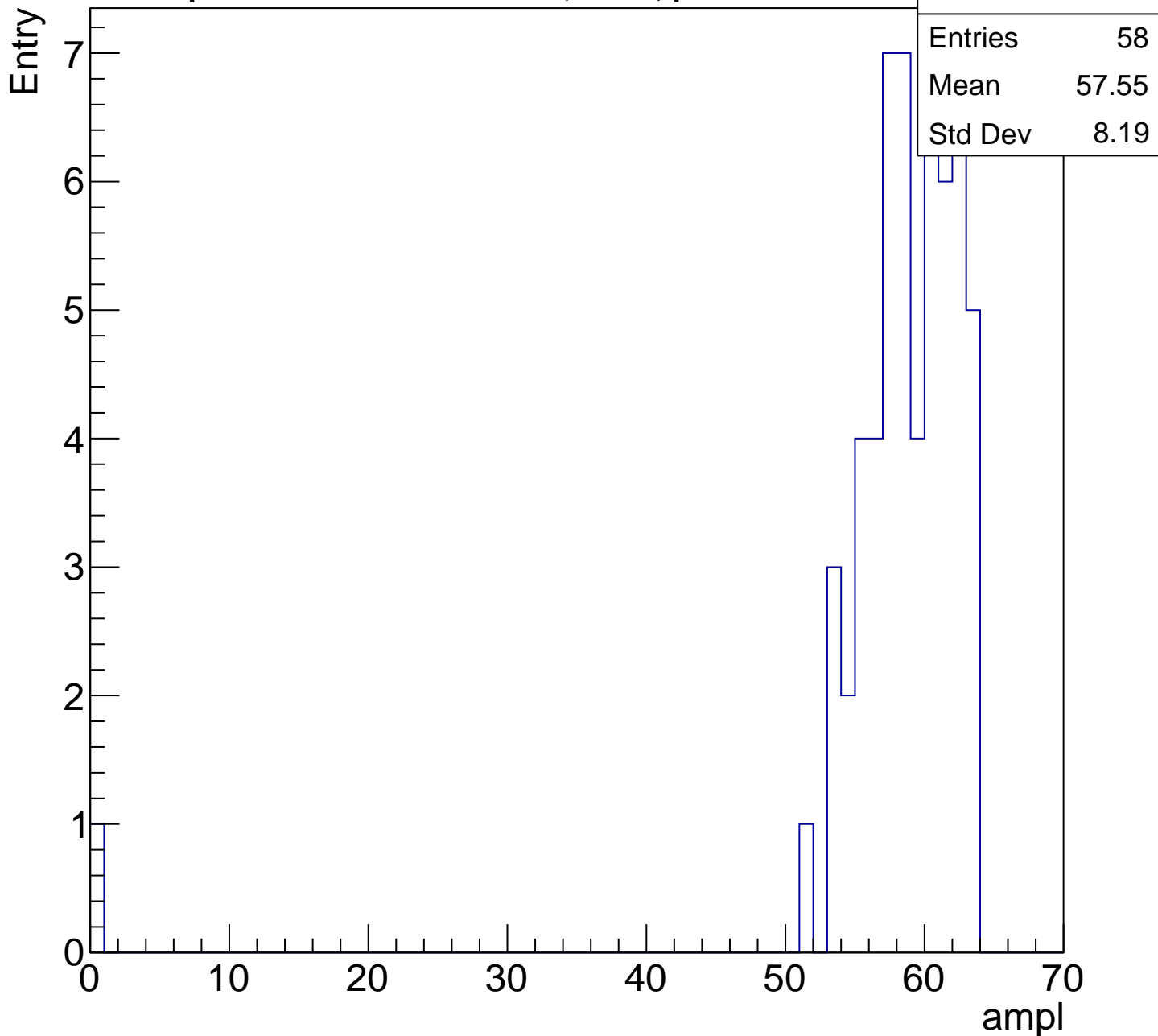
Entry

Entries	62
Mean	53.27
Std Dev	3.682



B0L000S, U2-ch98, adc5

calib_packv5_042523_0143.root, FC#5, port B1

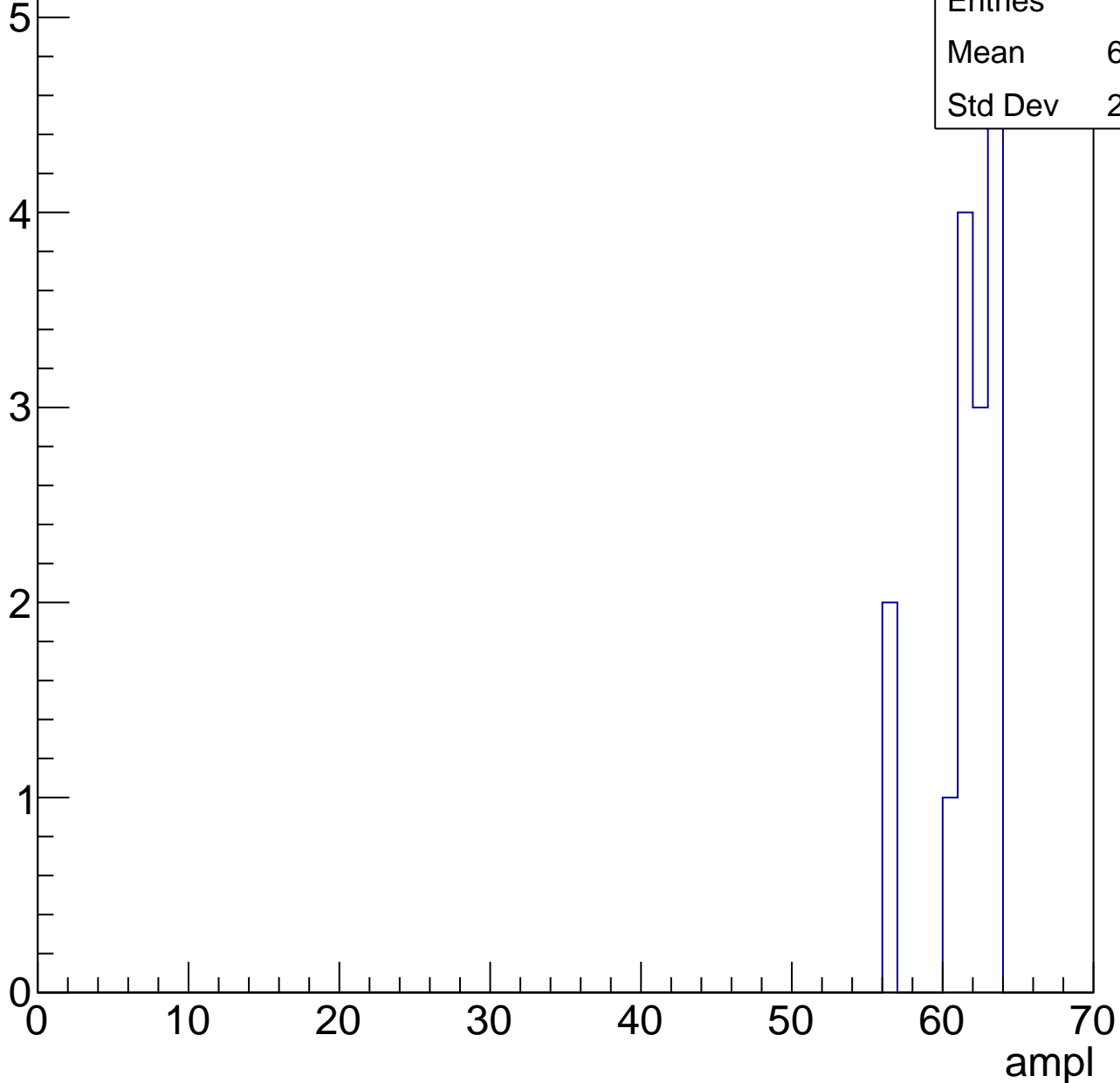


B0L000S, U2-ch98, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	15
Mean	61.13
Std Dev	2.217



B0L000S, U2-ch98, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch99, adc0

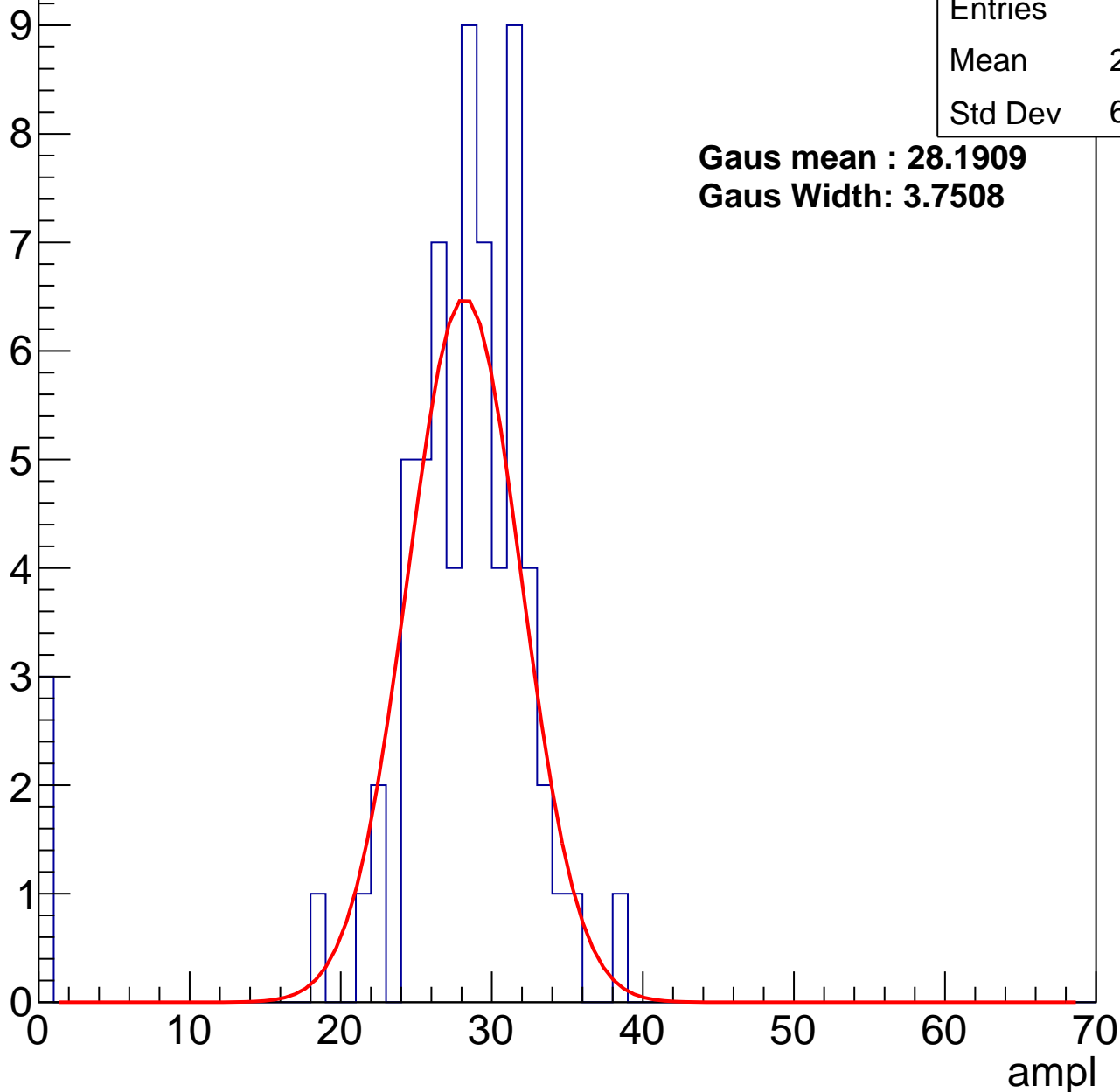
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	26.86
Std Dev	6.795

Gaus mean : 28.1909

Gaus Width: 3.7508



B0L000S, U2-ch99, adc1

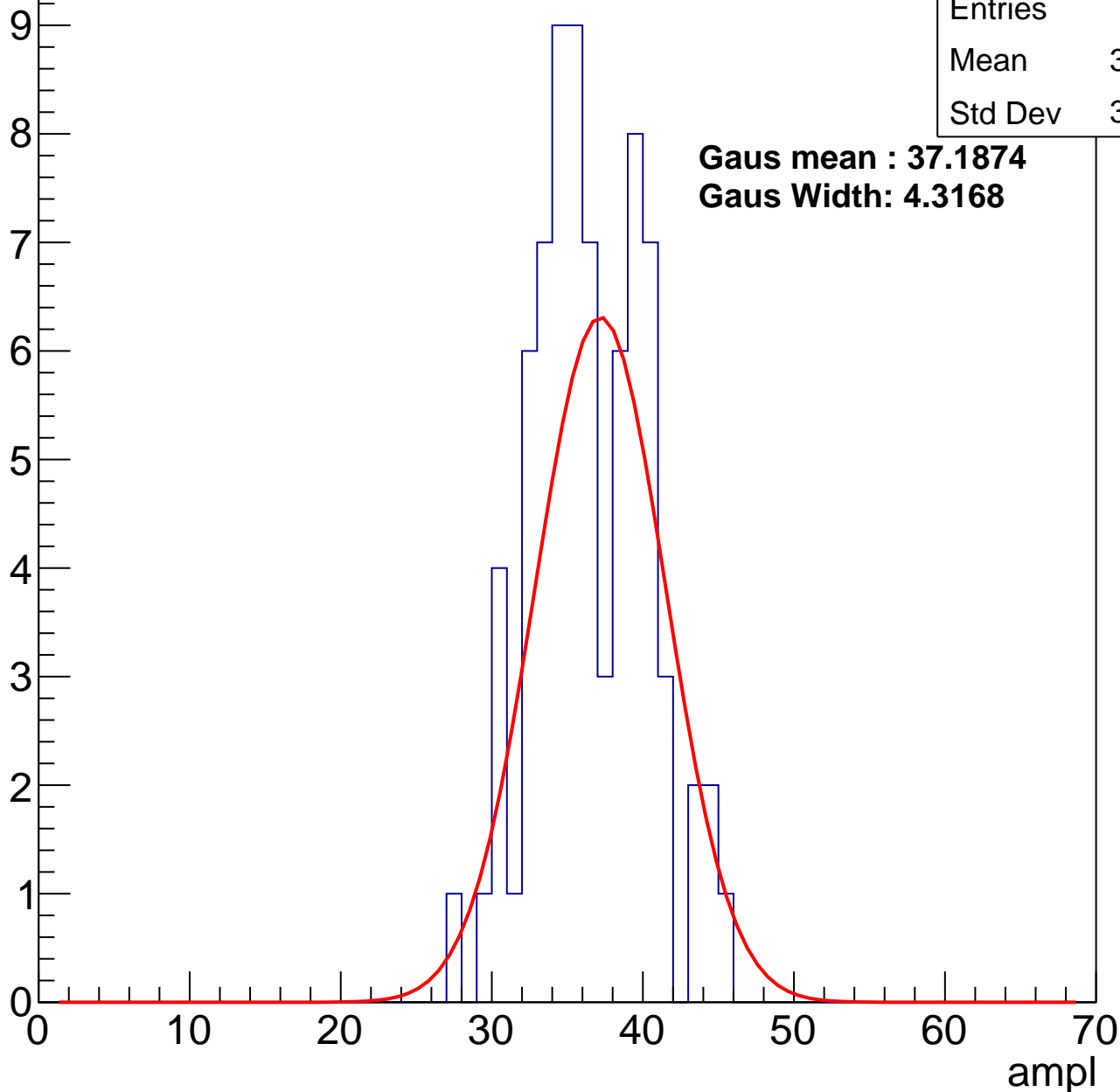
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	36.05
Std Dev	3.772

Gaus mean : 37.1874

Gaus Width: 4.3168



B0L000S, U2-ch99, adc2

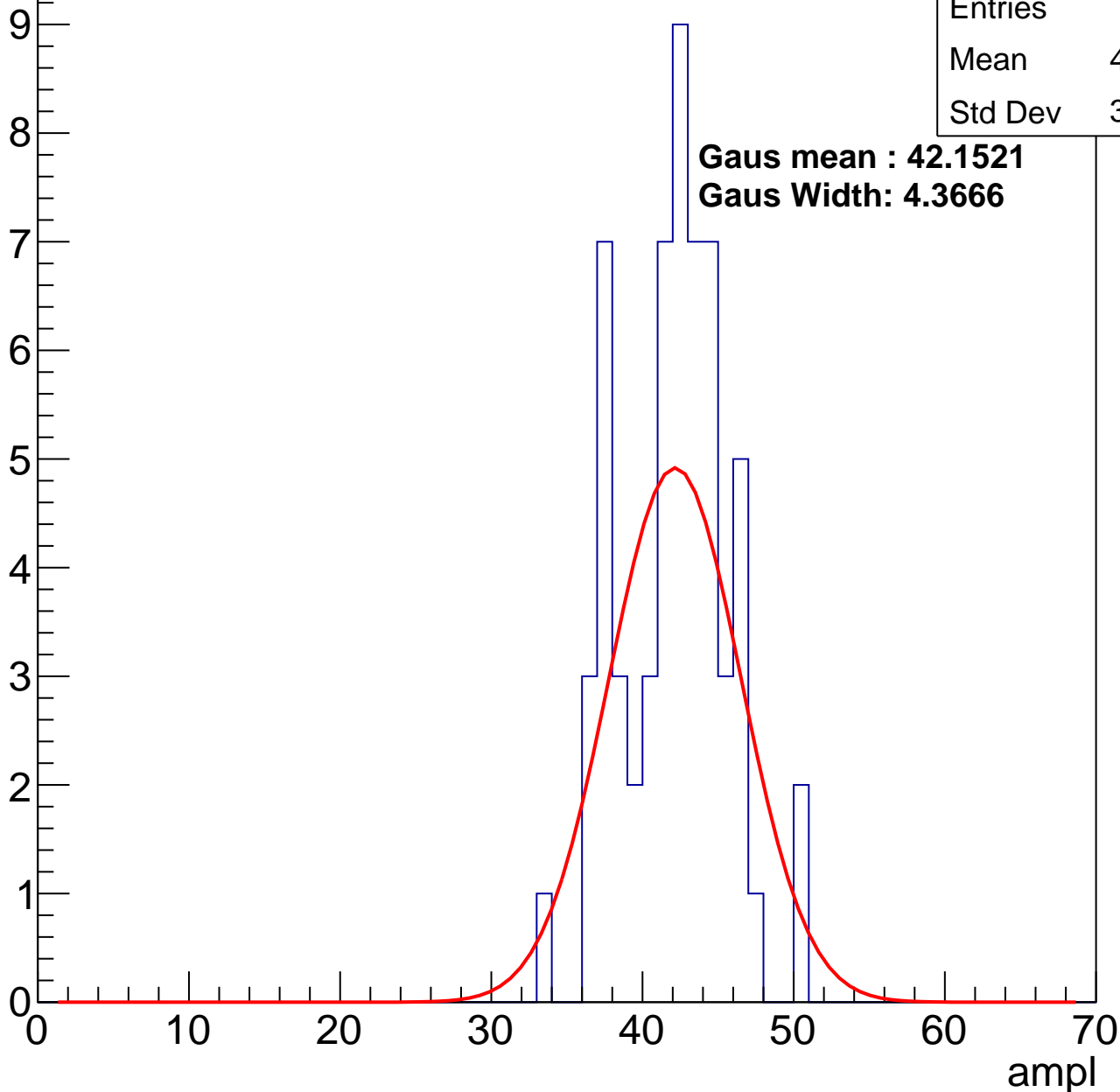
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.63
Std Dev	3.502

Gaus mean : 42.1521

Gaus Width: 4.3666

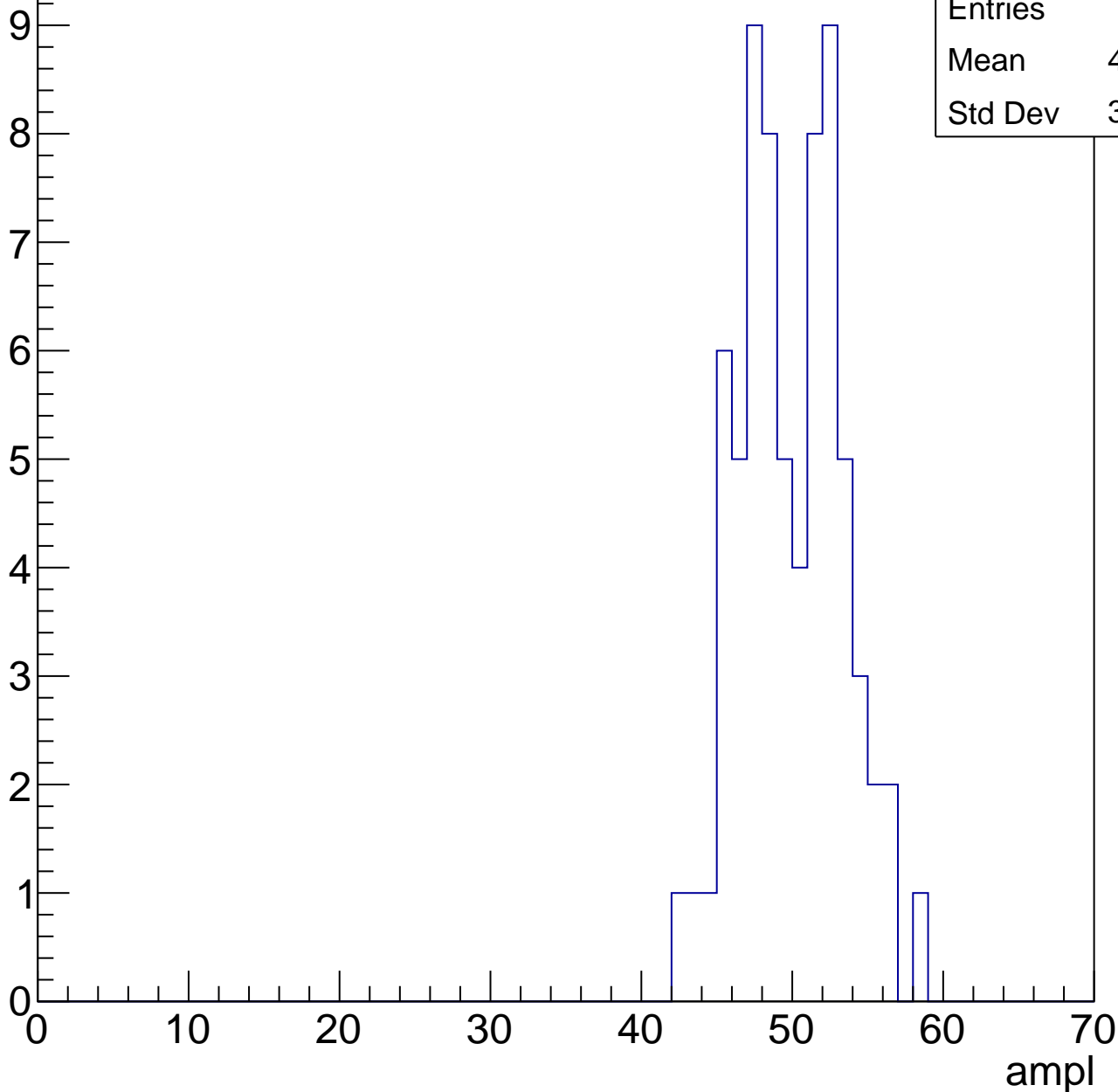


B0L000S, U2-ch99, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.49
Std Dev	3.384

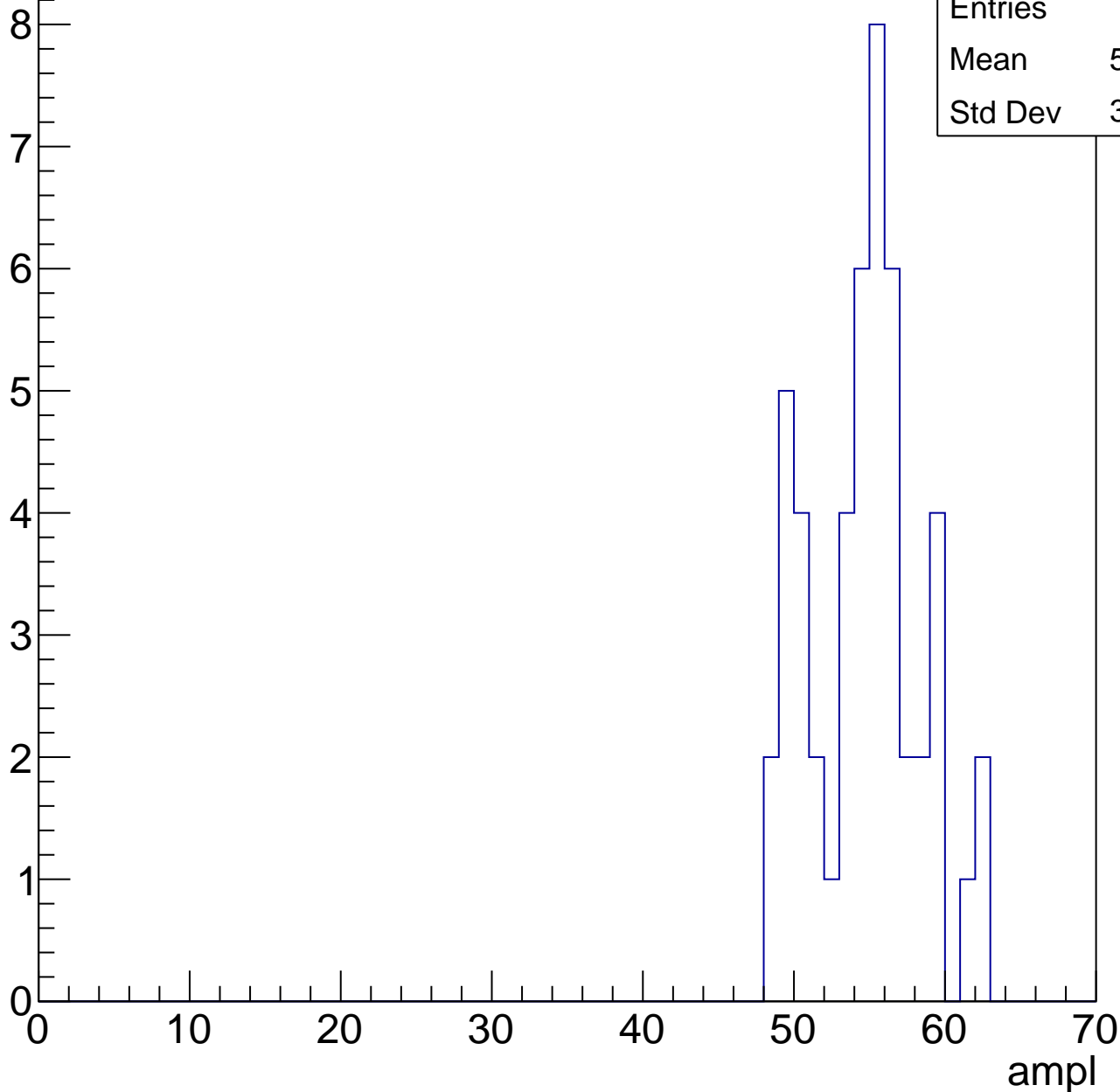


B0L000S, U2-ch99, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	54.24
Std Dev	3.623



B0L000S, U2-ch99, adc5

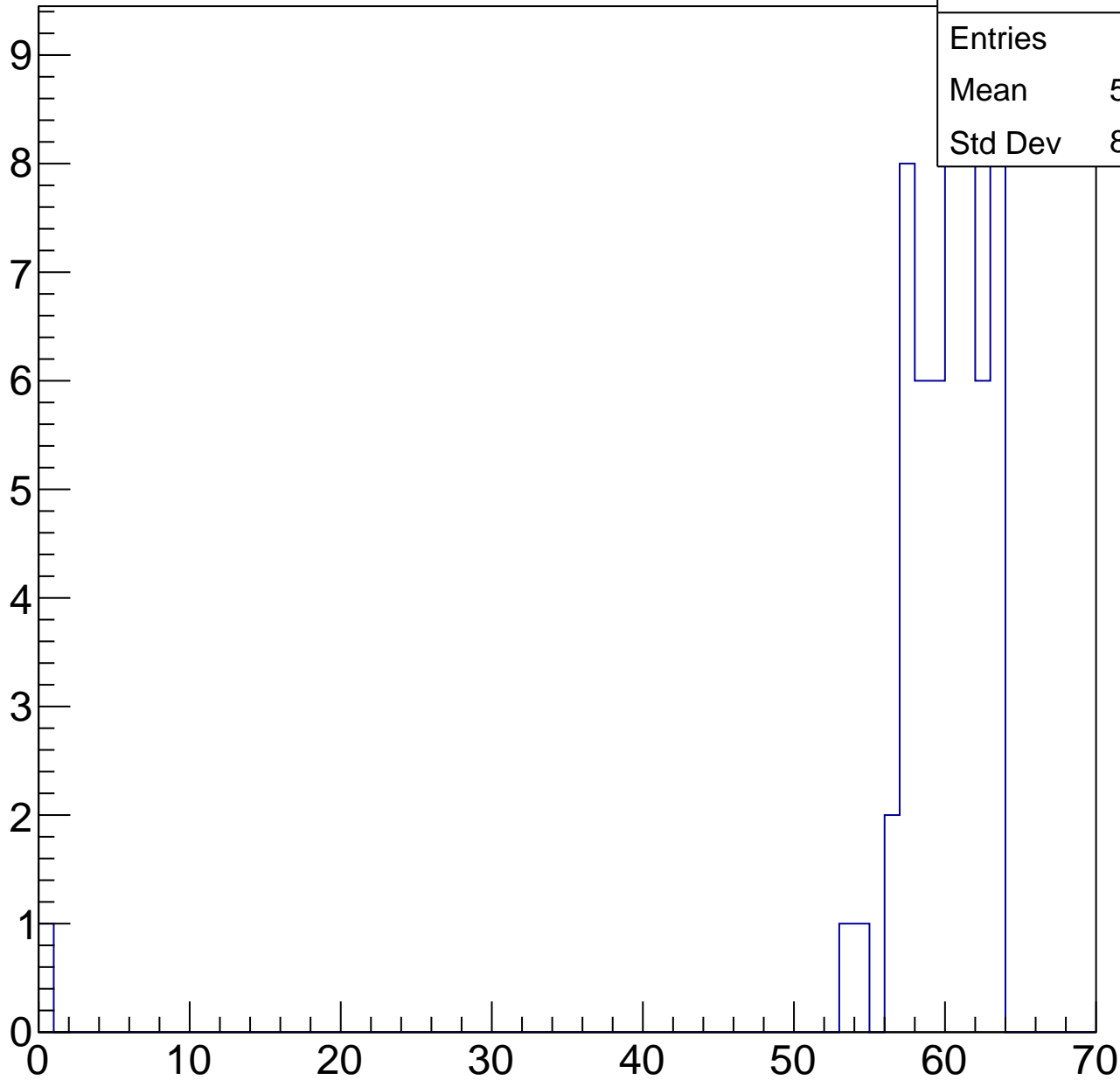
calib_packv5_042523_0143.root, FC#5, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.67
Std Dev	8.198

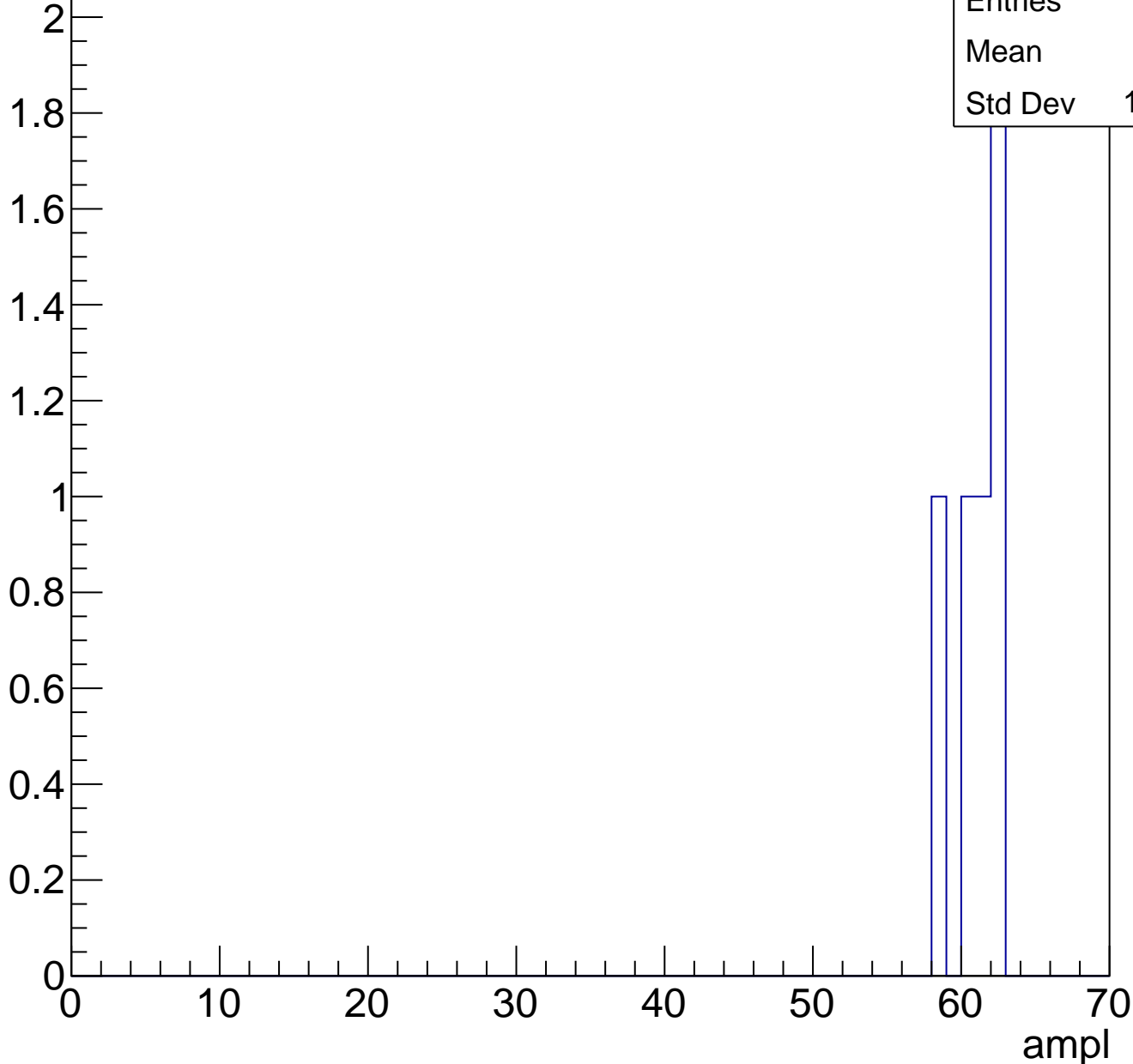
ampl



B0L000S, U2-ch99, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	5
Mean	60.6
Std Dev	1.497

B0L000S, U2-ch99, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch100, adc0

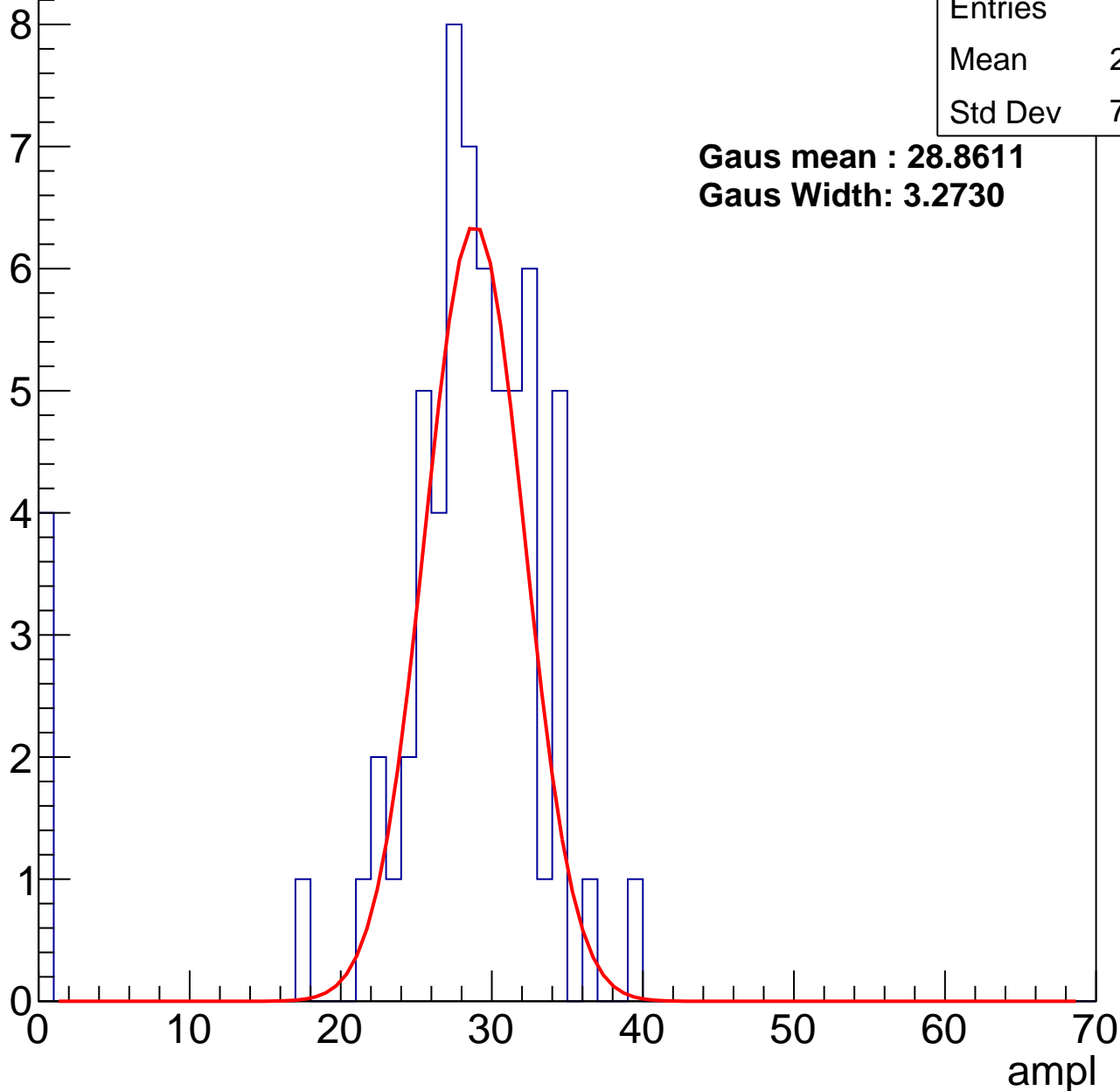
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	26.82
Std Dev	7.815

Gaus mean : 28.8611

Gaus Width: 3.2730



B0L000S, U2-ch100, adc1

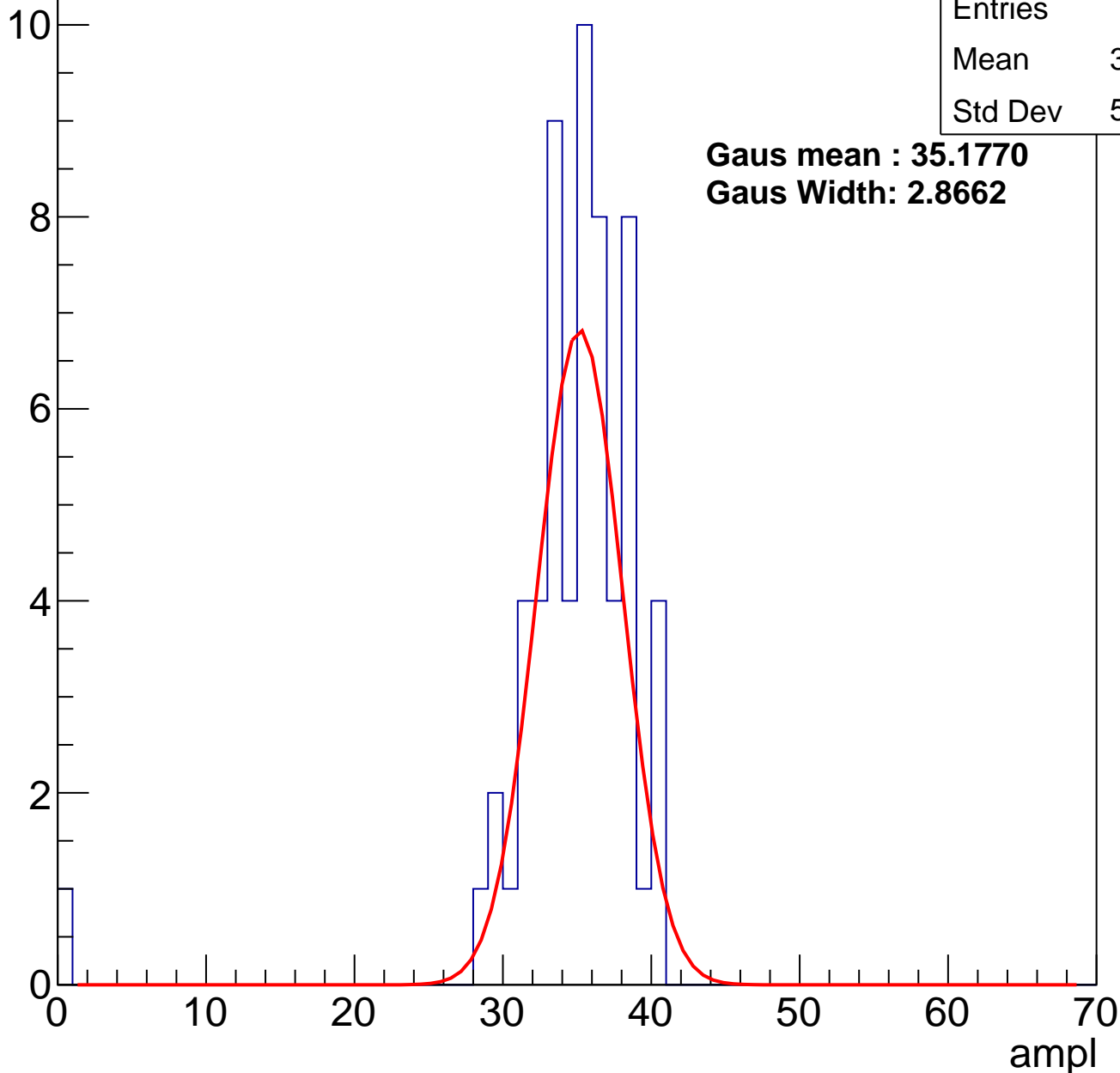
calib_packv5_042523_0143.root, FC#5, port B1

Entries	61
Mean	34.26
Std Dev	5.263

Gaus mean : 35.1770

Gaus Width: 2.8662

Entry



B0L000S, U2-ch100, adc2

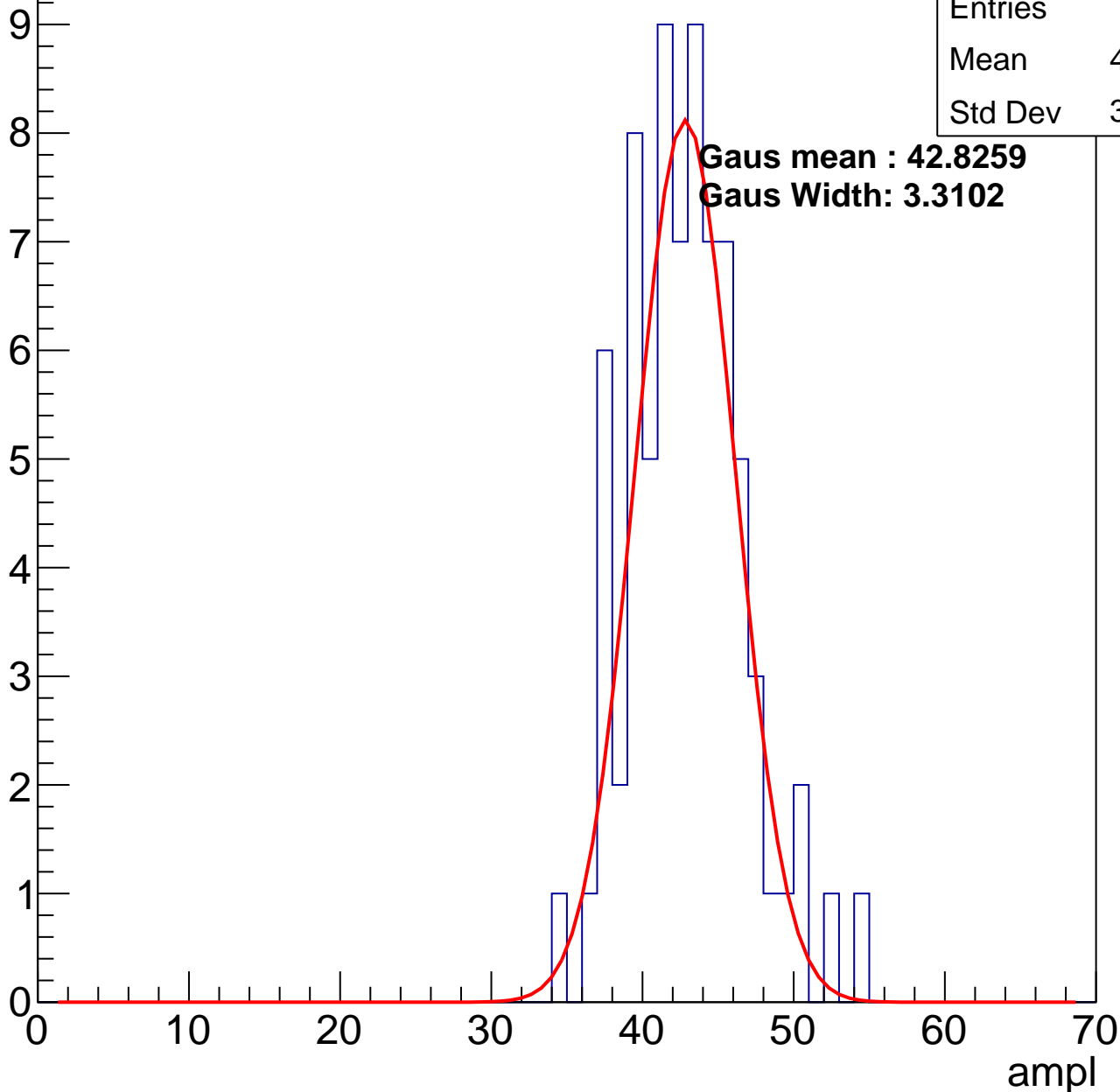
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.46
Std Dev	3.768

Gaus mean : 42.8259

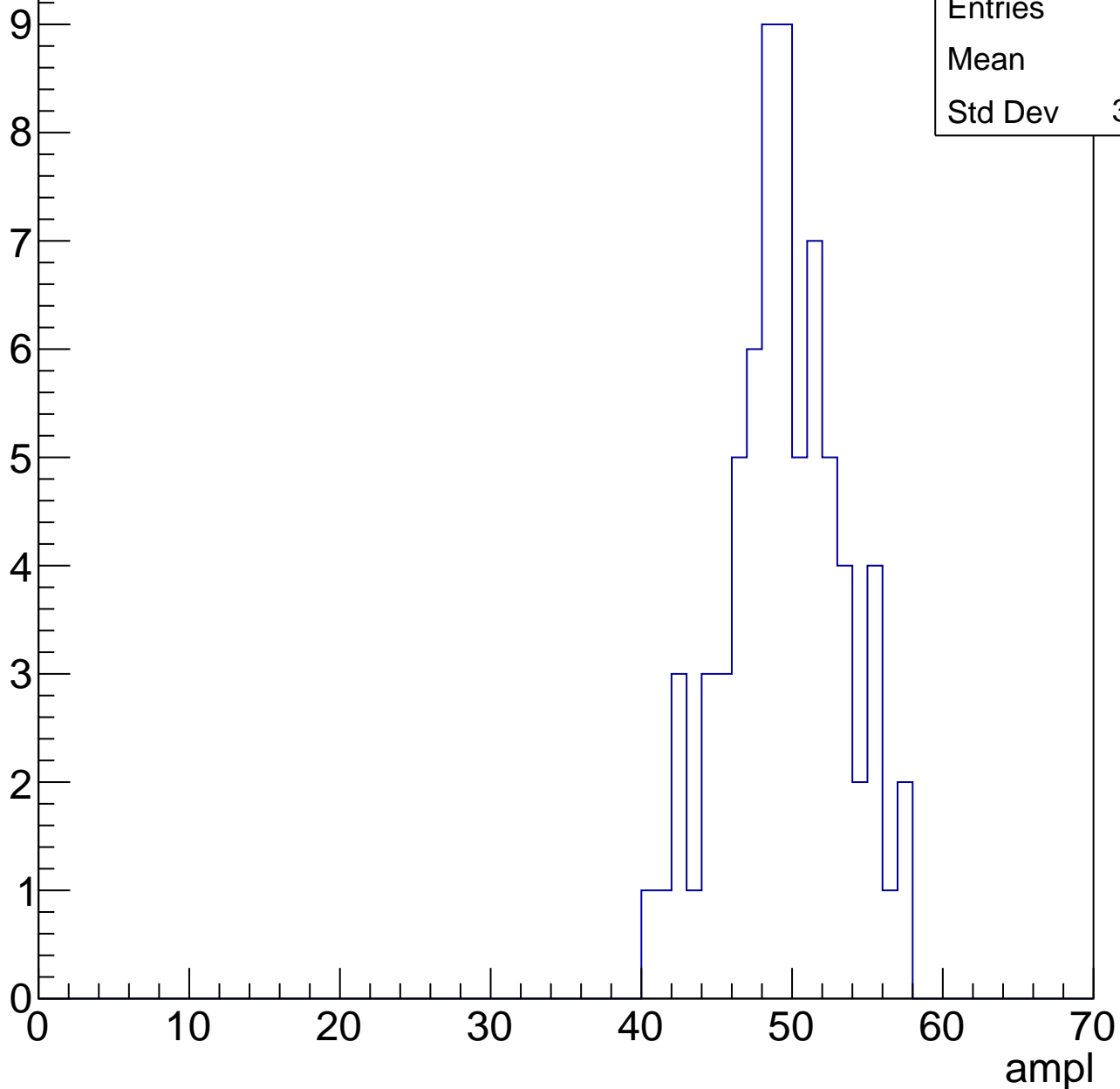
Gaus Width: 3.3102



B0L000S, U2-ch100, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

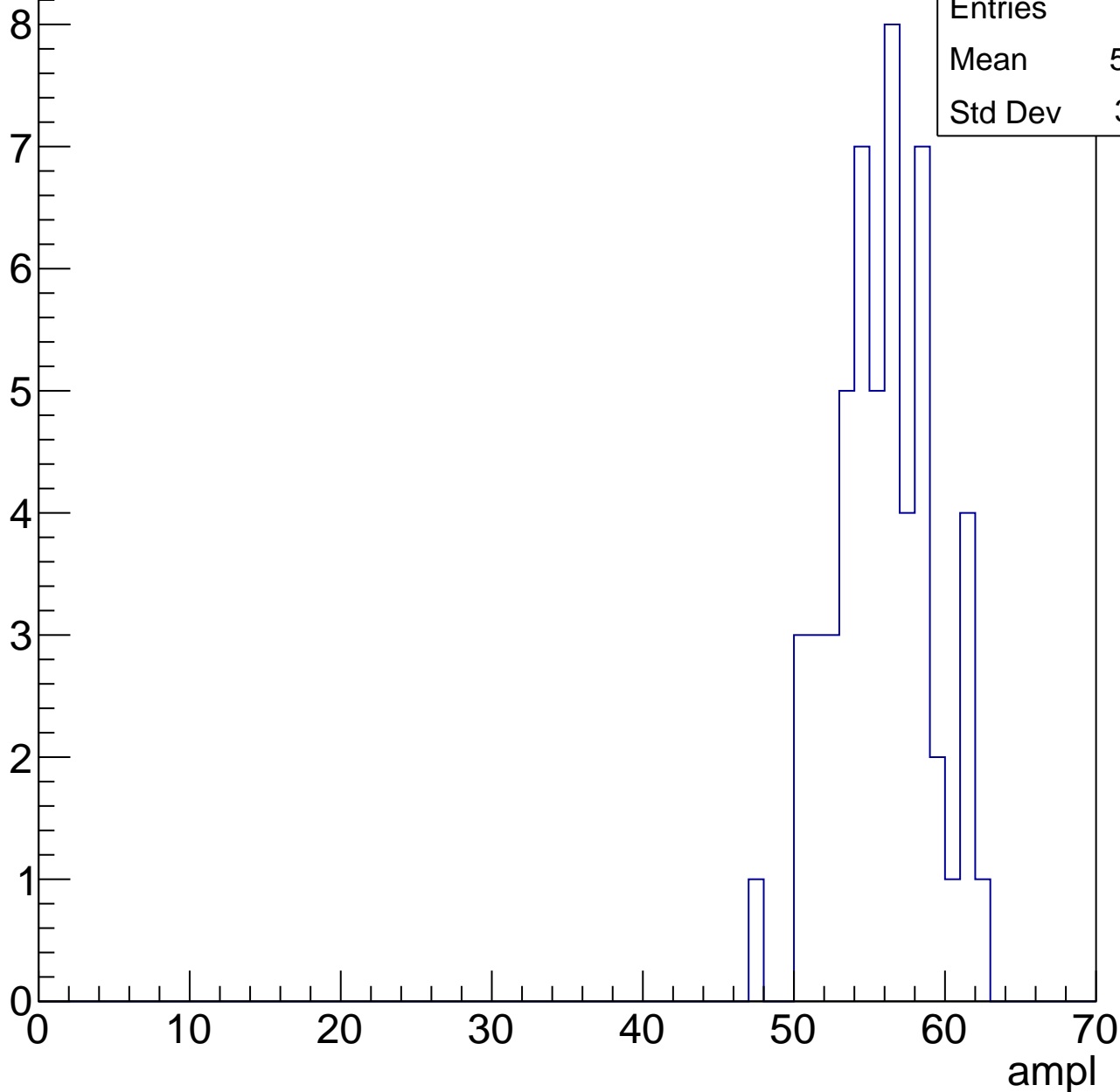


B0L000S, U2-ch100, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.37
Std Dev	3.251

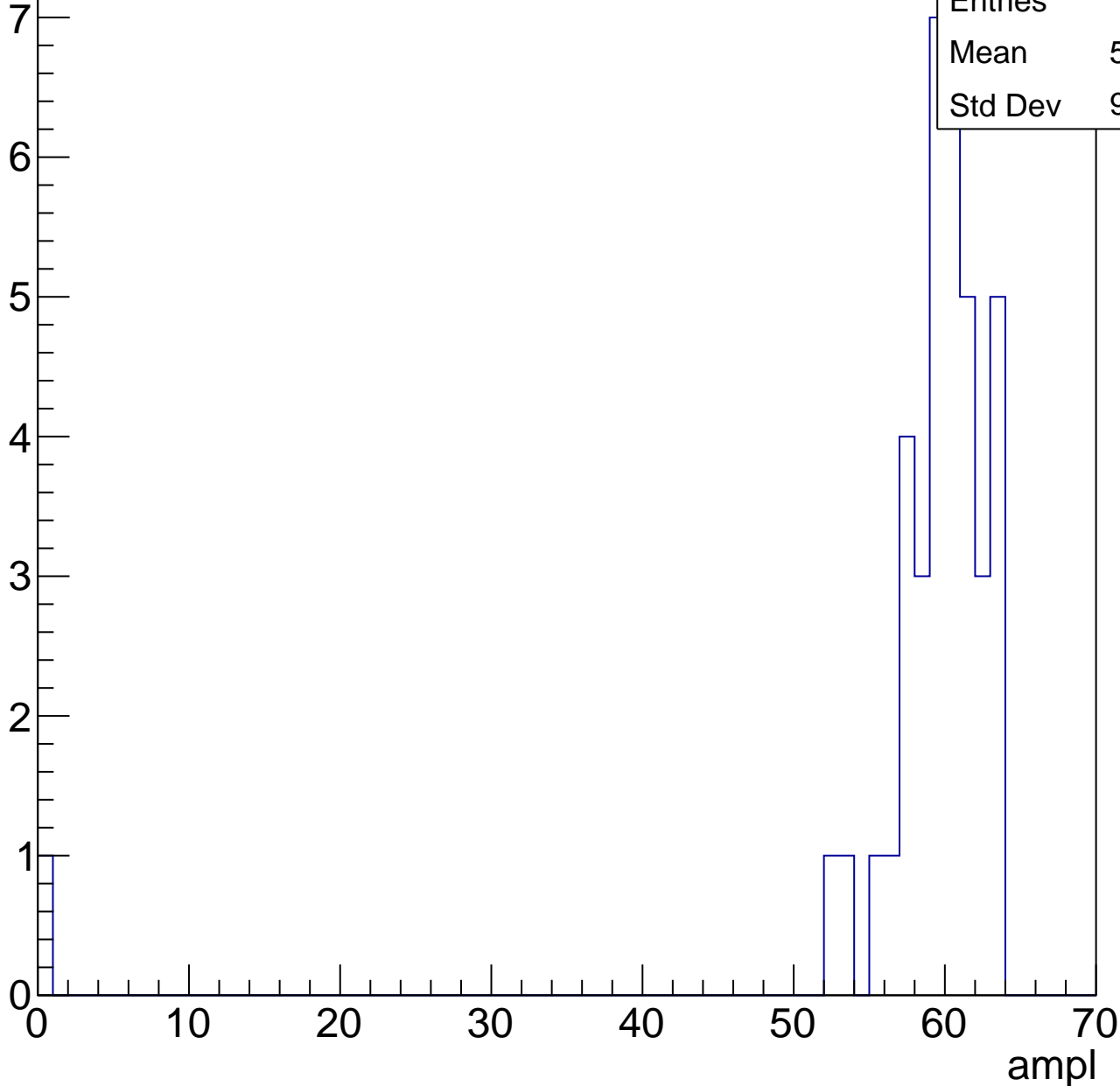


B0L000S, U2-ch100, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	39
Mean	57.87
Std Dev	9.733

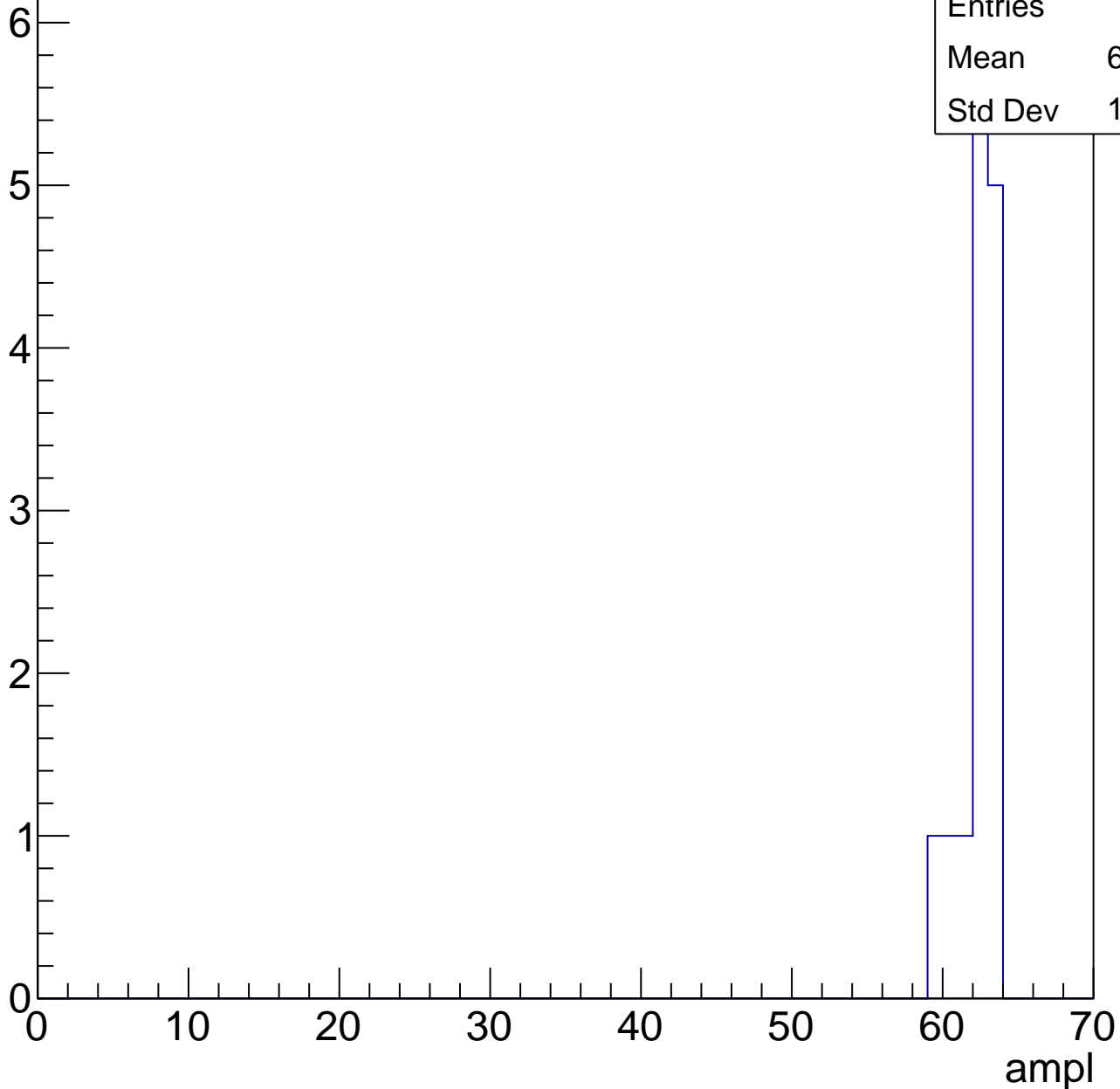


B0L000S, U2-ch100, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	14
Mean	61.93
Std Dev	1.163



B0L000S, U2-ch100, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch101, adc0

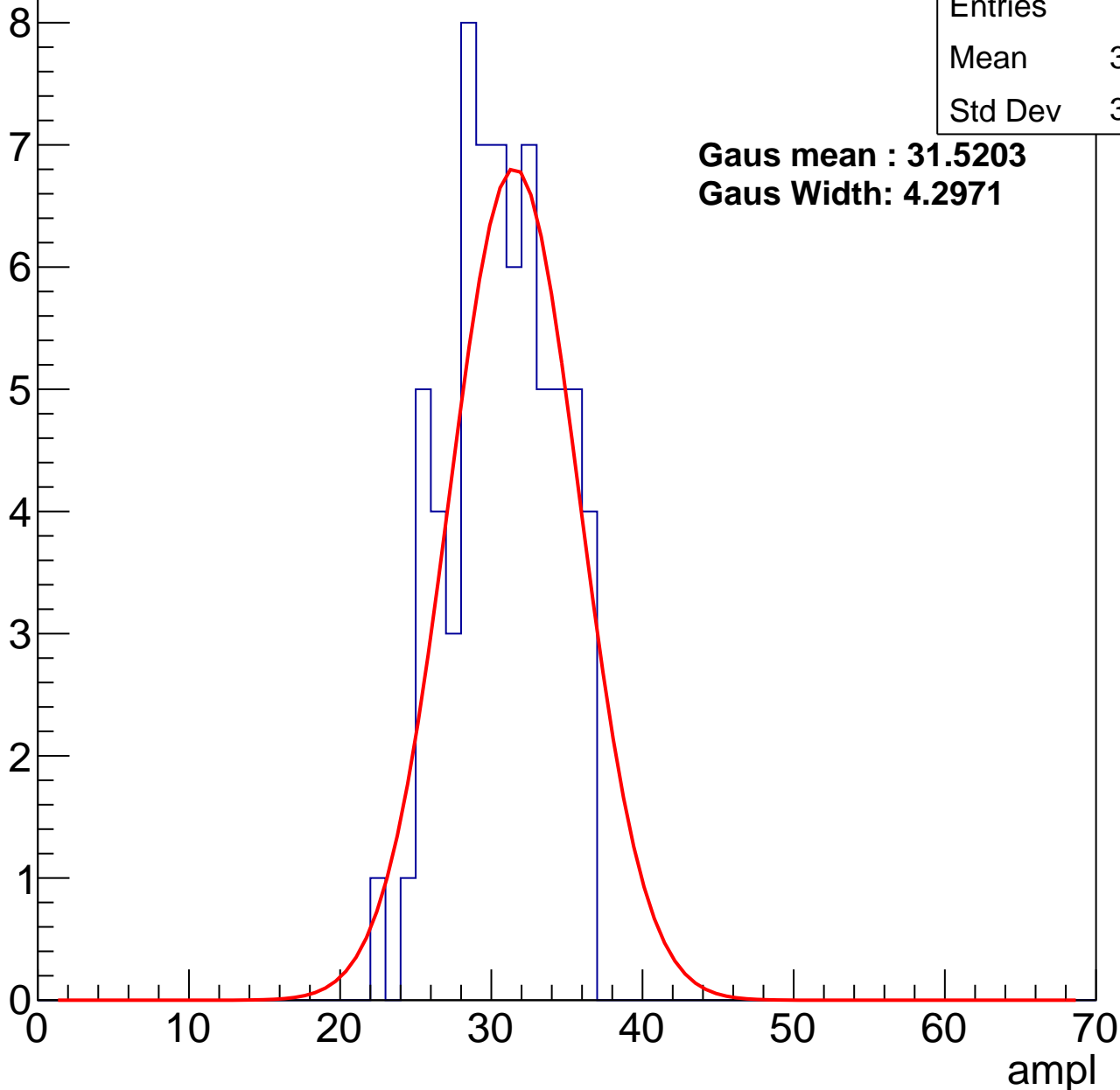
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.25
Std Dev	3.384

Gaus mean : 31.5203

Gaus Width: 4.2971



B0L000S, U2-ch101, adc1

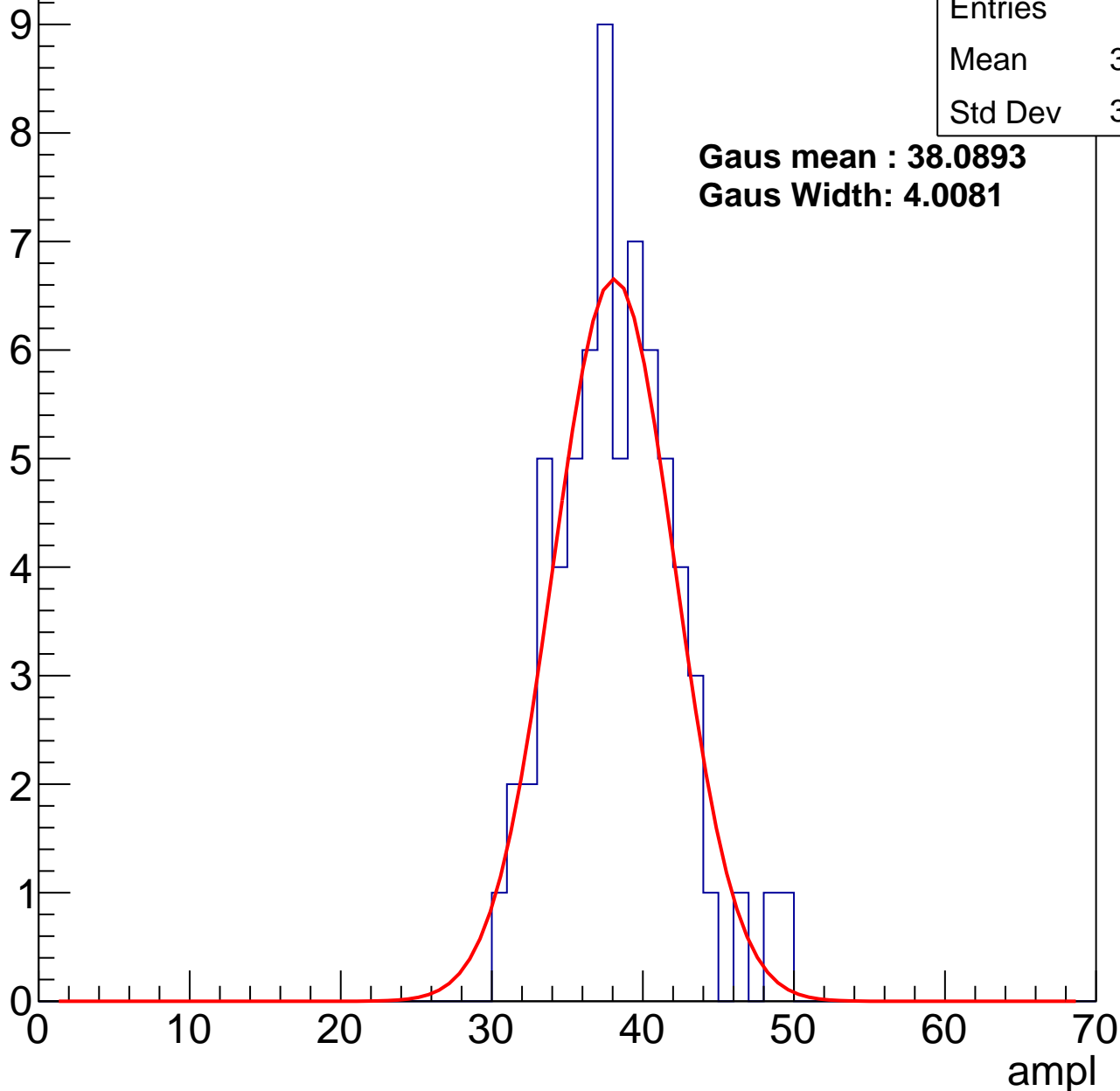
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.84
Std Dev	3.902

Gaus mean : 38.0893

Gaus Width: 4.0081



B0L000S, U2-ch101, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	70
Mean	43.76
Std Dev	3.563

Gaus mean : 44.2528

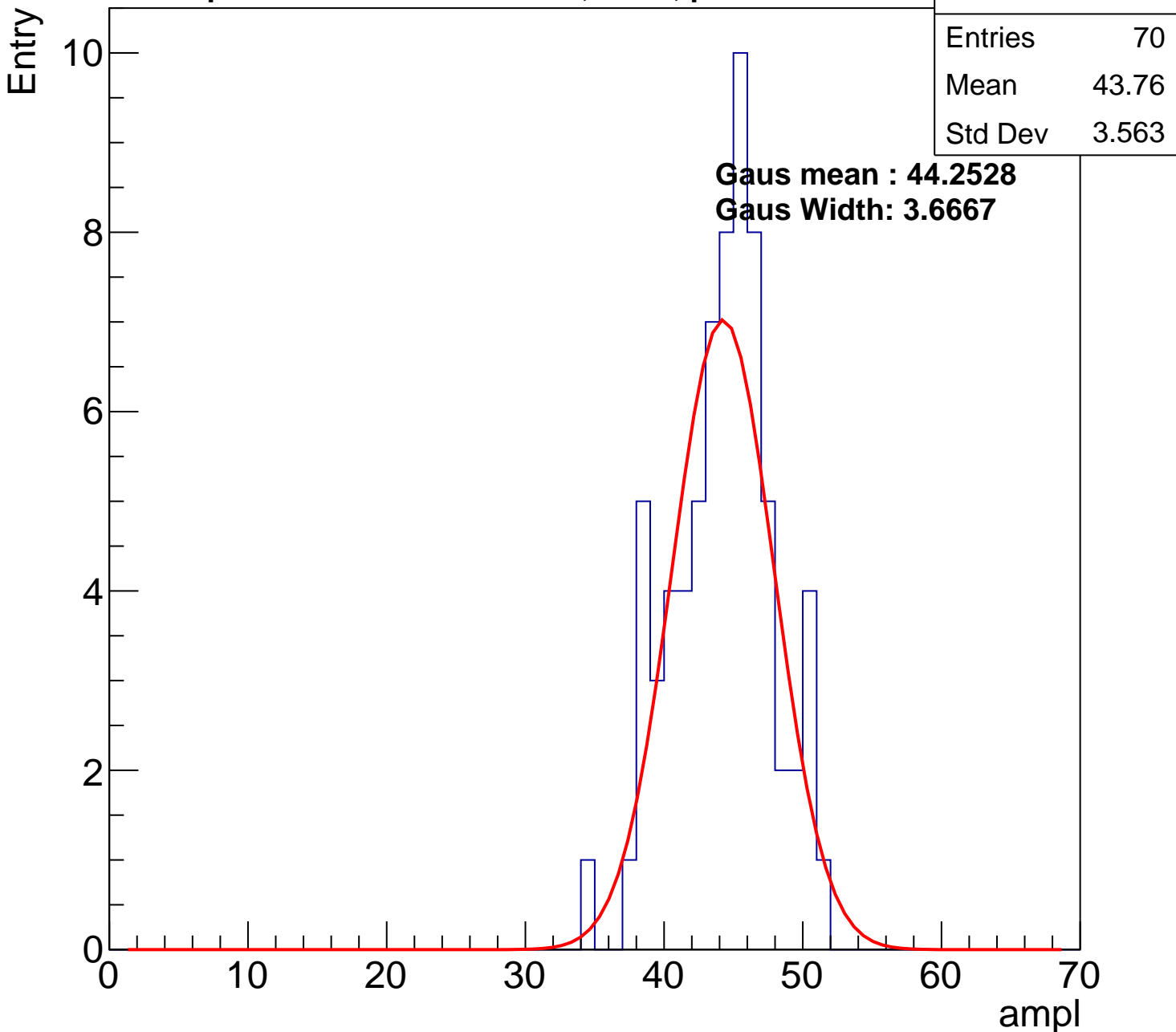
Gaus Width: 3.6667

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

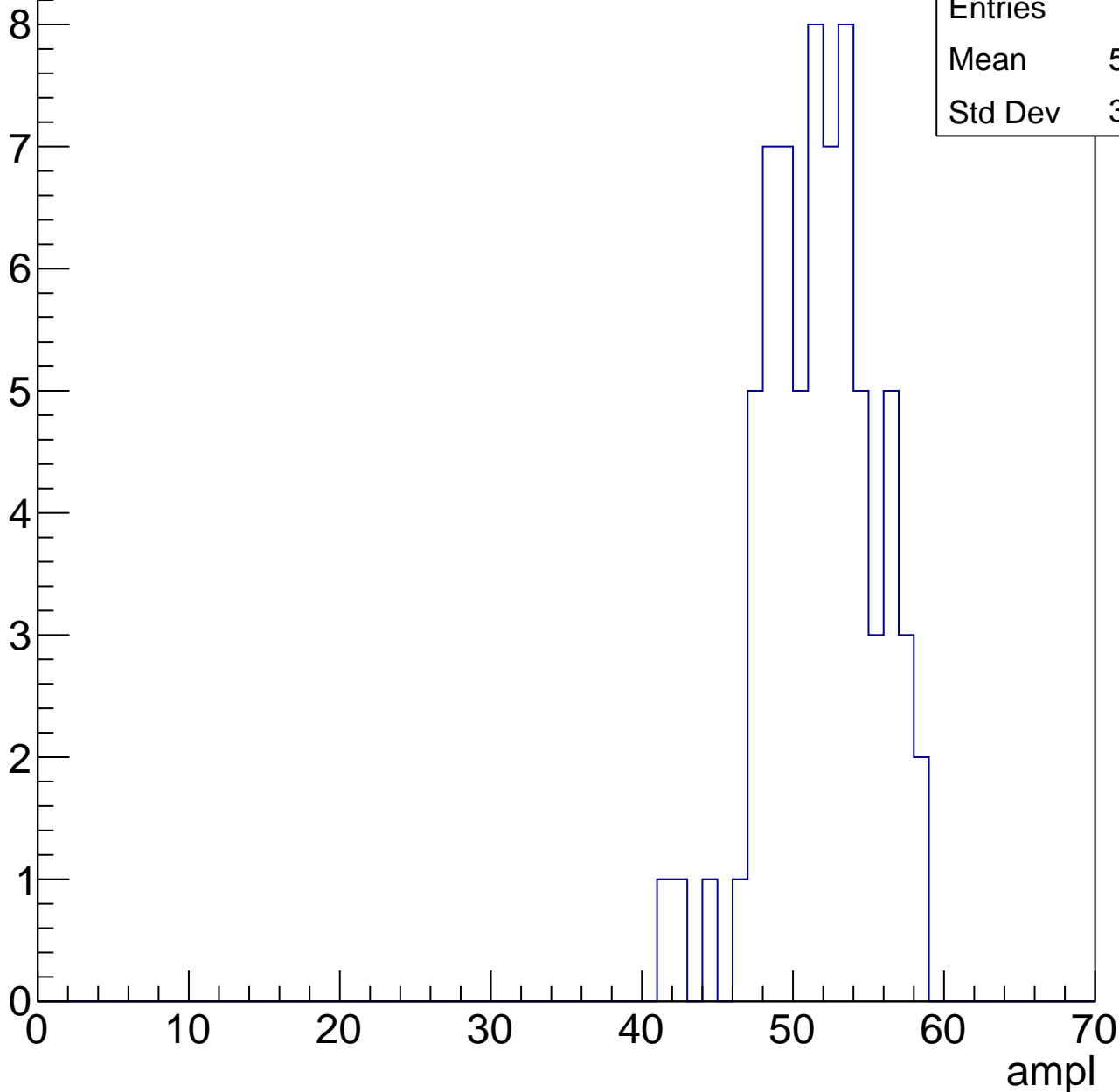


B0L000S, U2-ch101, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	51.23
Std Dev	3.592

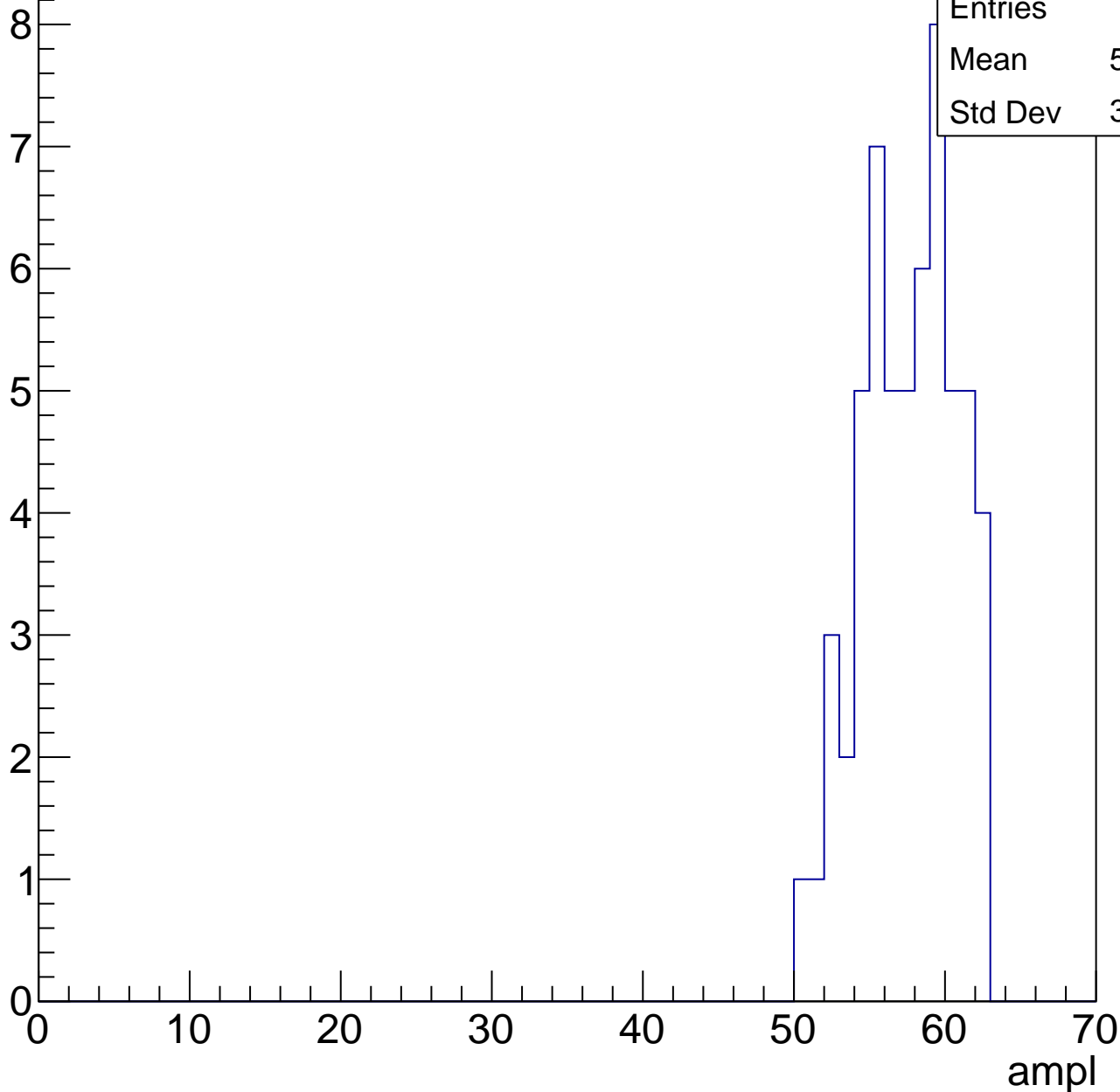


B0L000S, U2-ch101, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	57.12
Std Dev	3.055

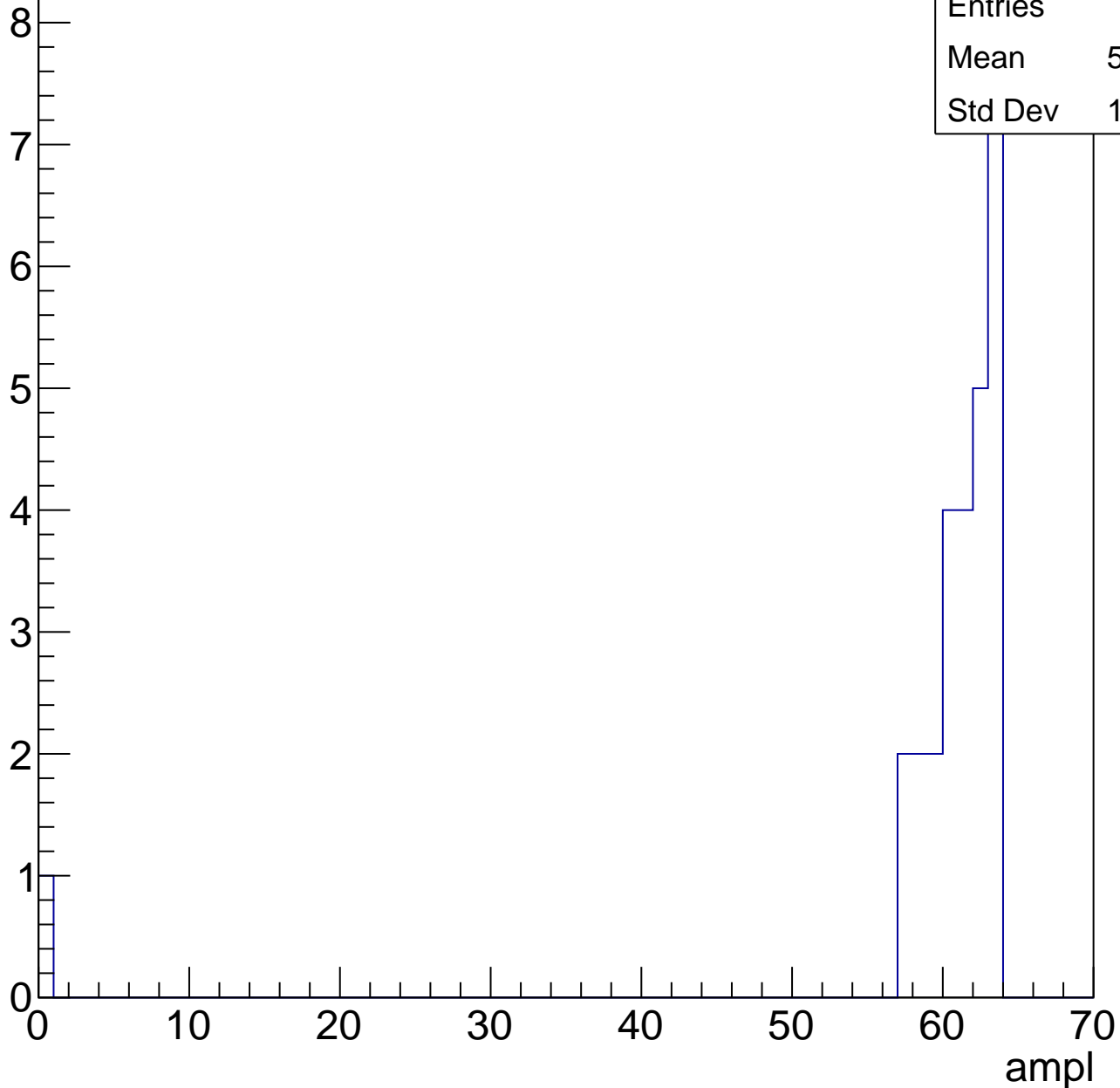


B0L000S, U2-ch101, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.79
Std Dev	11.47



B0L000S, U2-ch101, adc6

calib_packv5_042523_0143.root, FC#5, port B1

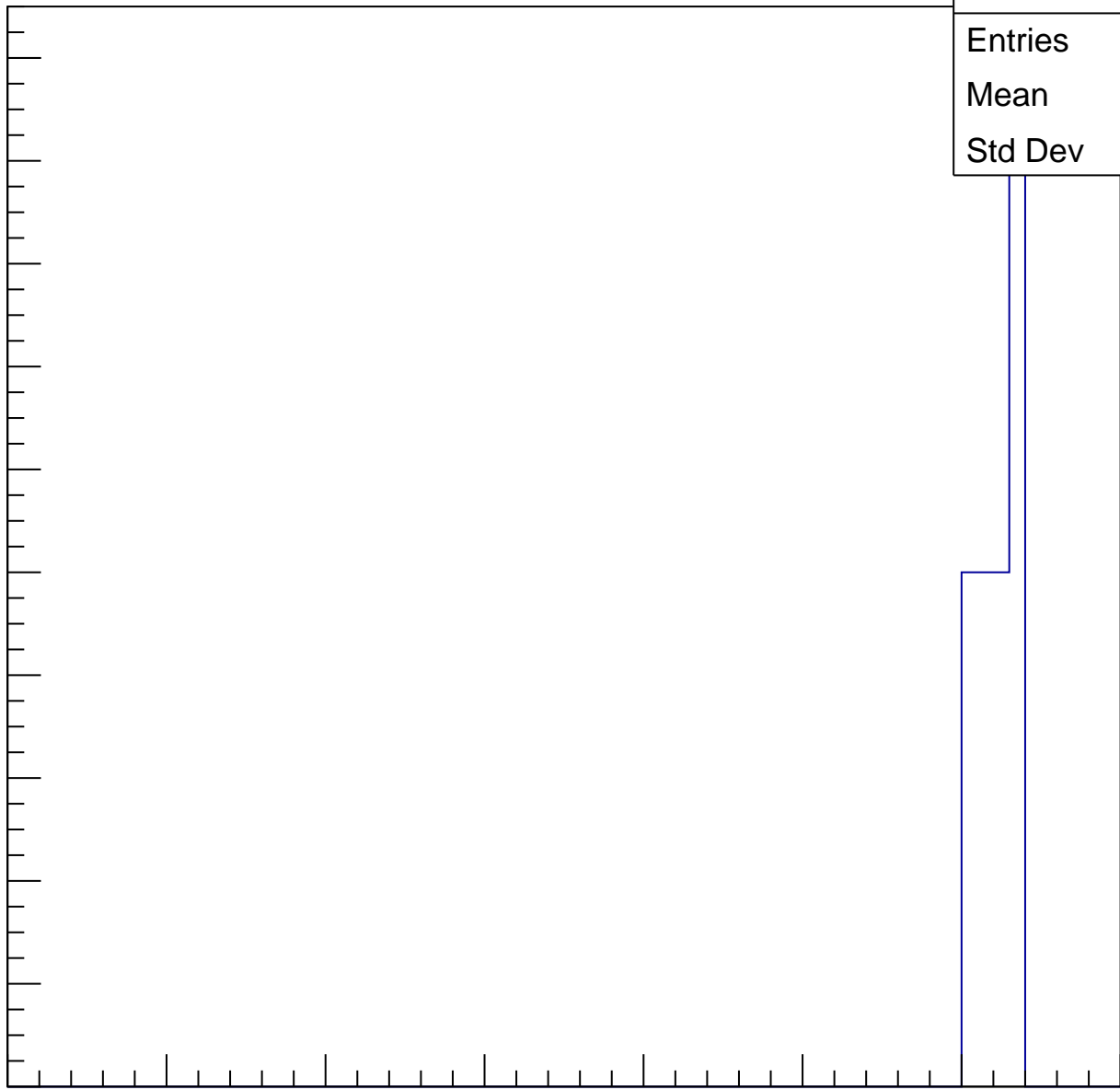
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch101, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch102, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	69
Mean	28.52
Std Dev	4.805

Gaus mean : 29.3528

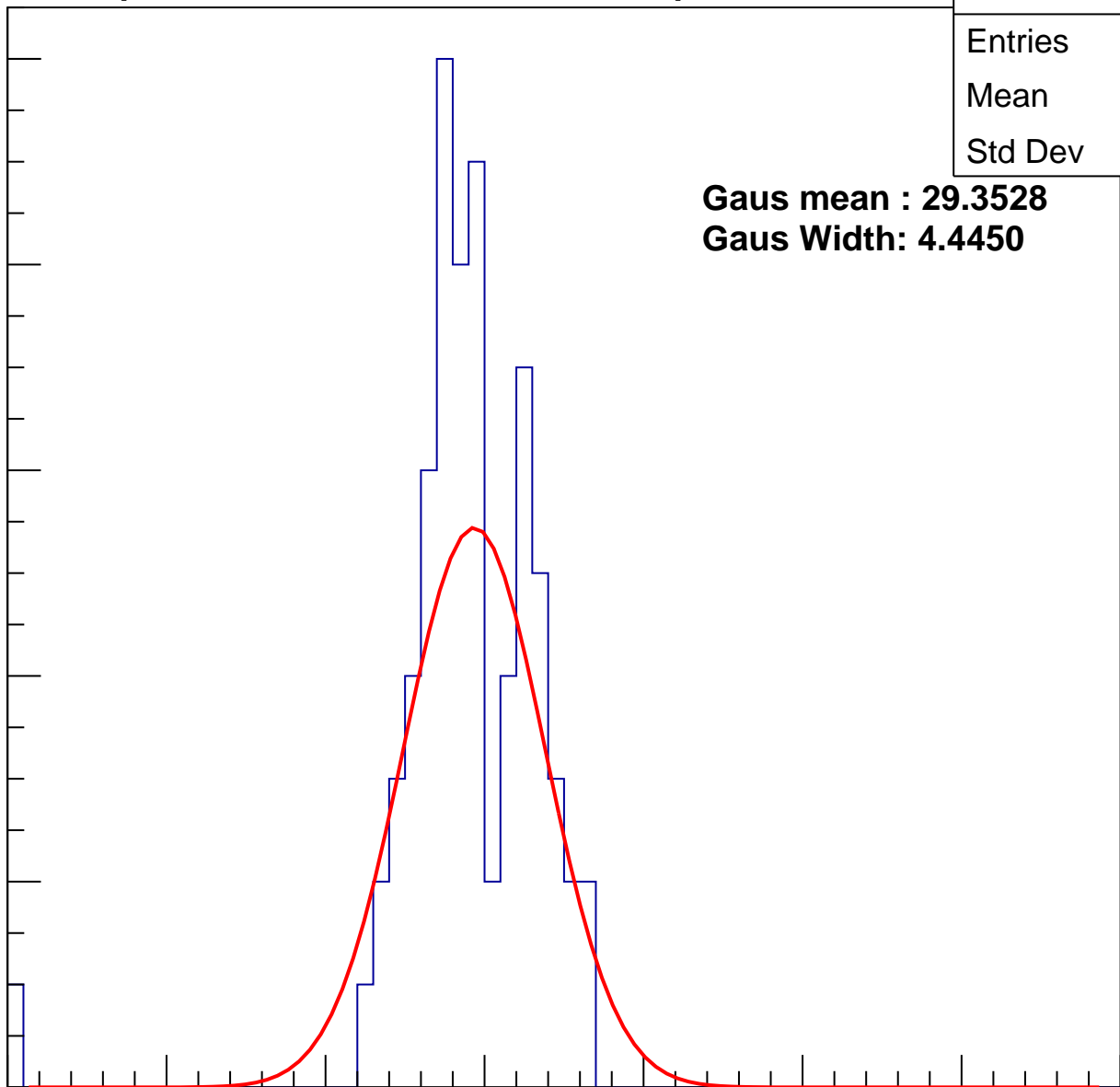
Gaus Width: 4.4450

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch102, adc1

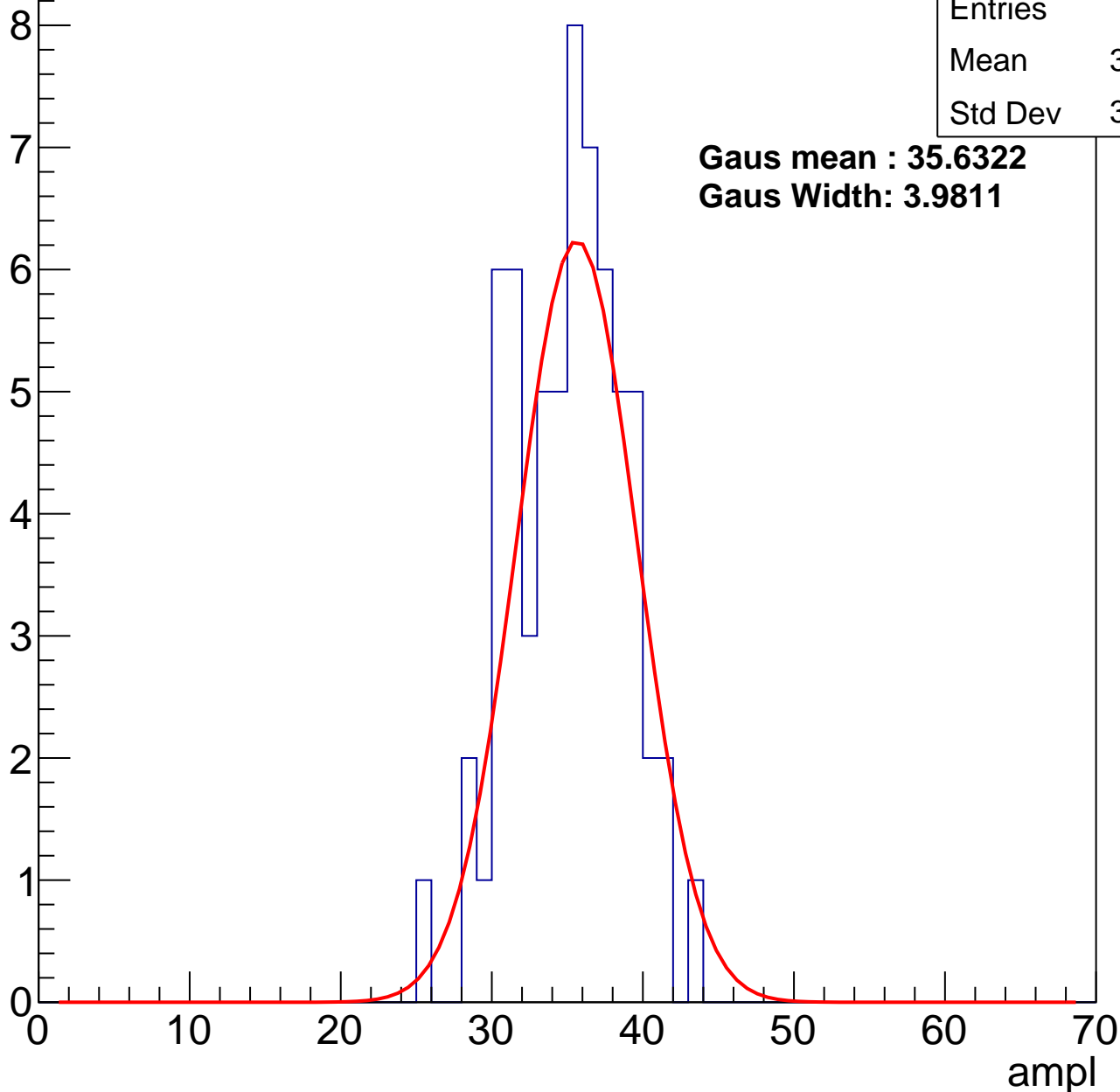
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	34.63
Std Dev	3.648

Gaus mean : 35.6322

Gaus Width: 3.9811



B0L000S, U2-ch102, adc2

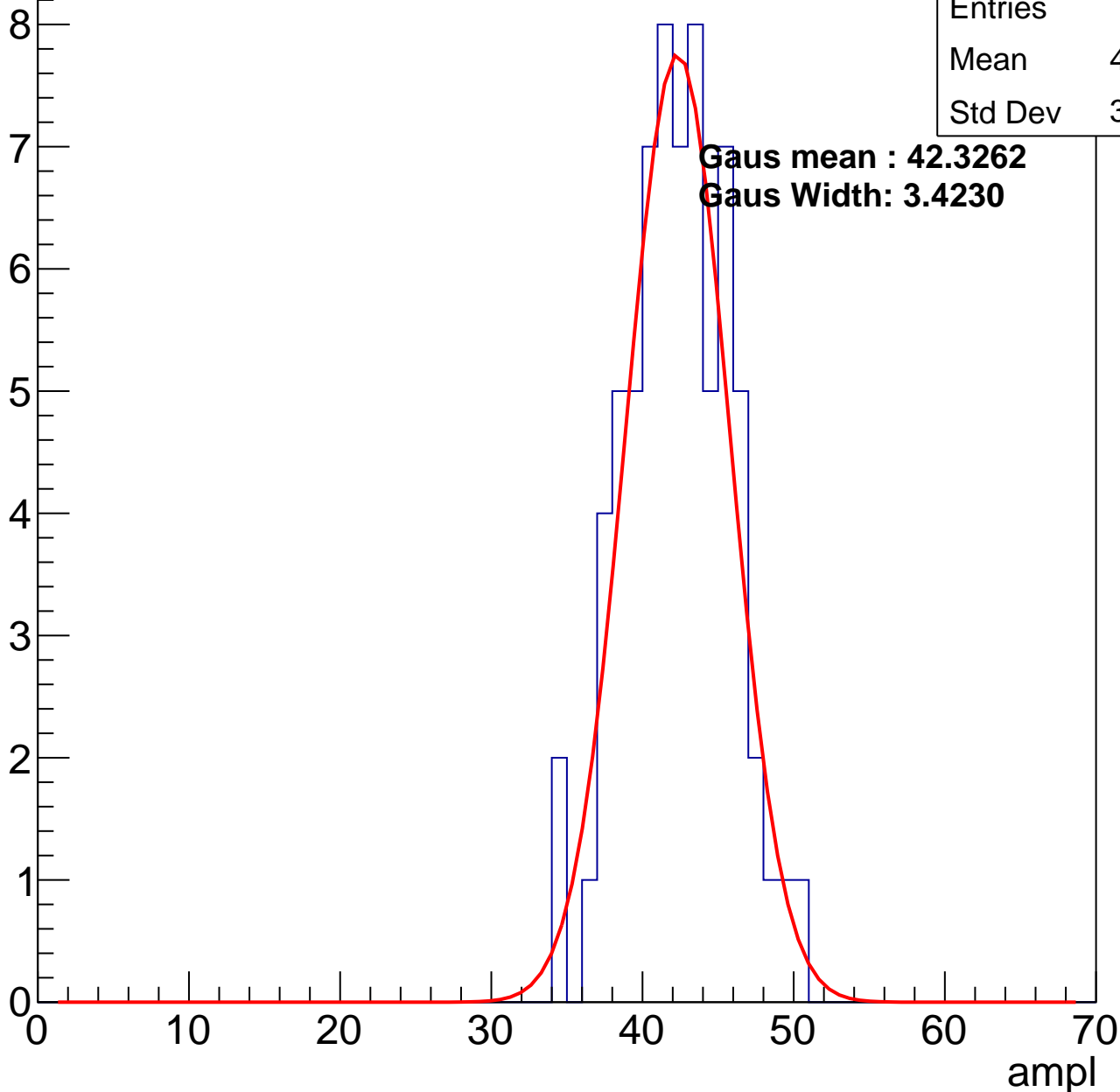
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	41.87
Std Dev	3.392

Gaus mean : 42.3262

Gaus Width: 3.4230

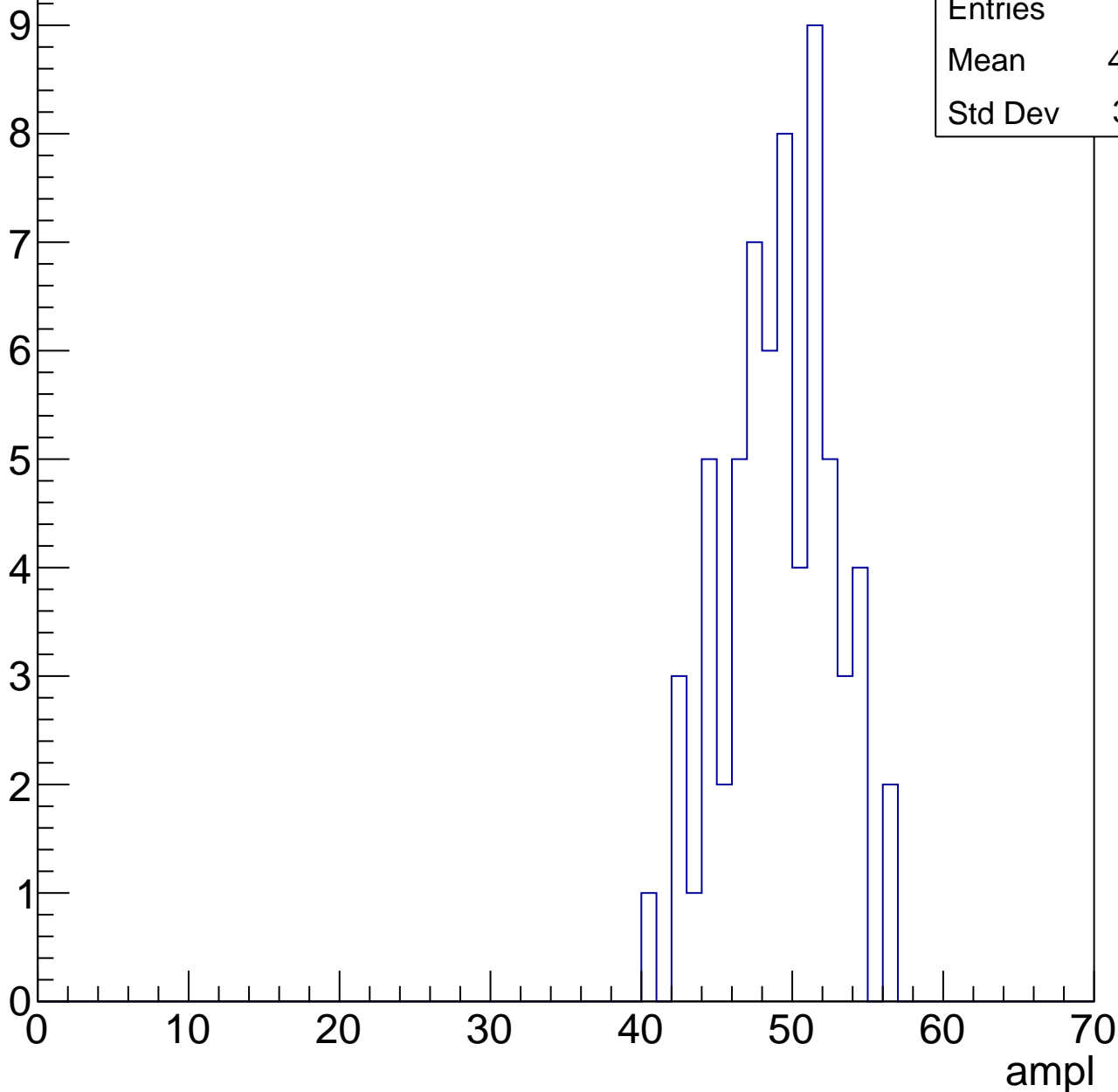


B0L000S, U2-ch102, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.68
Std Dev	3.561

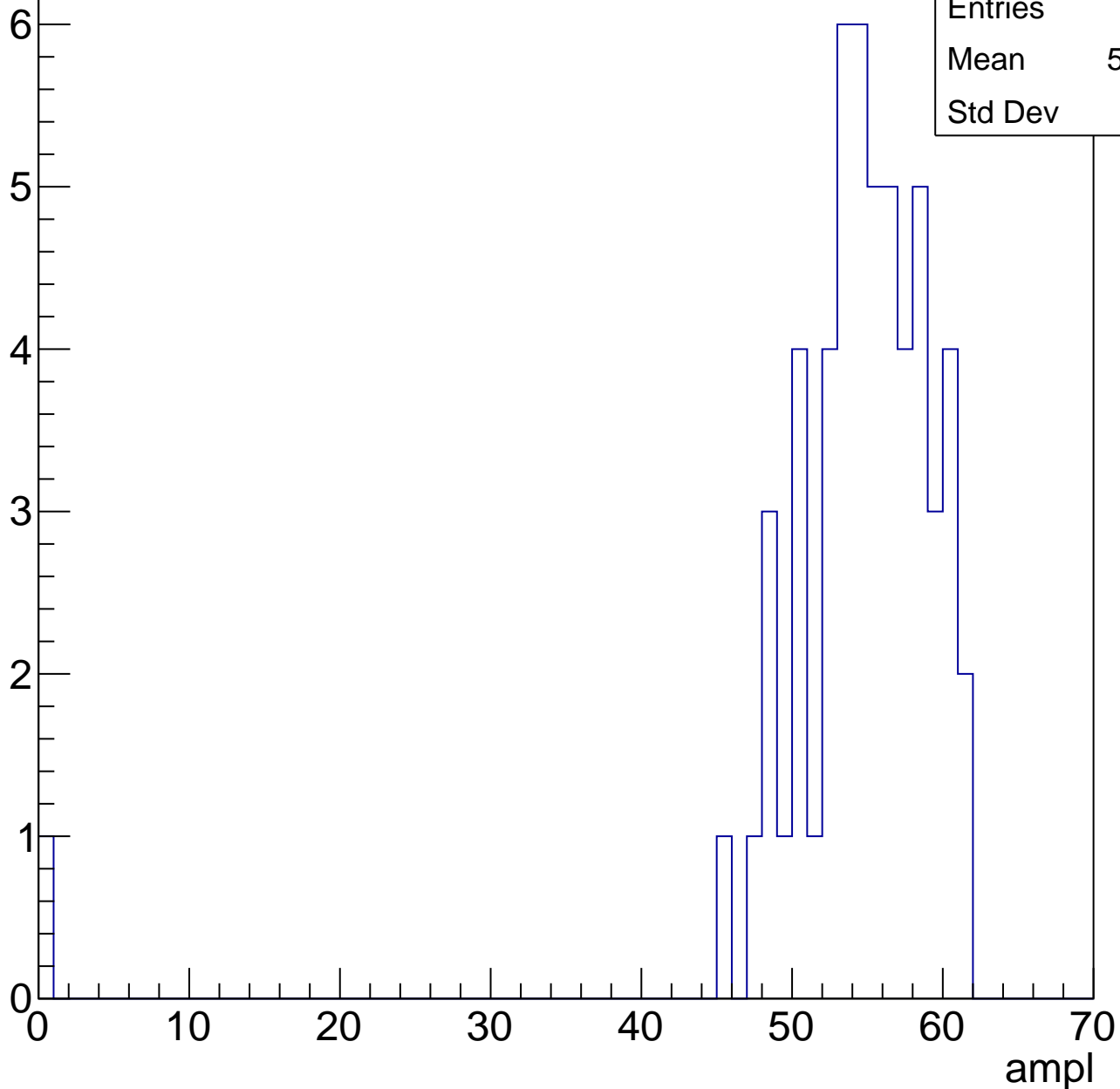


B0L000S, U2-ch102, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	53.54
Std Dev	8.15

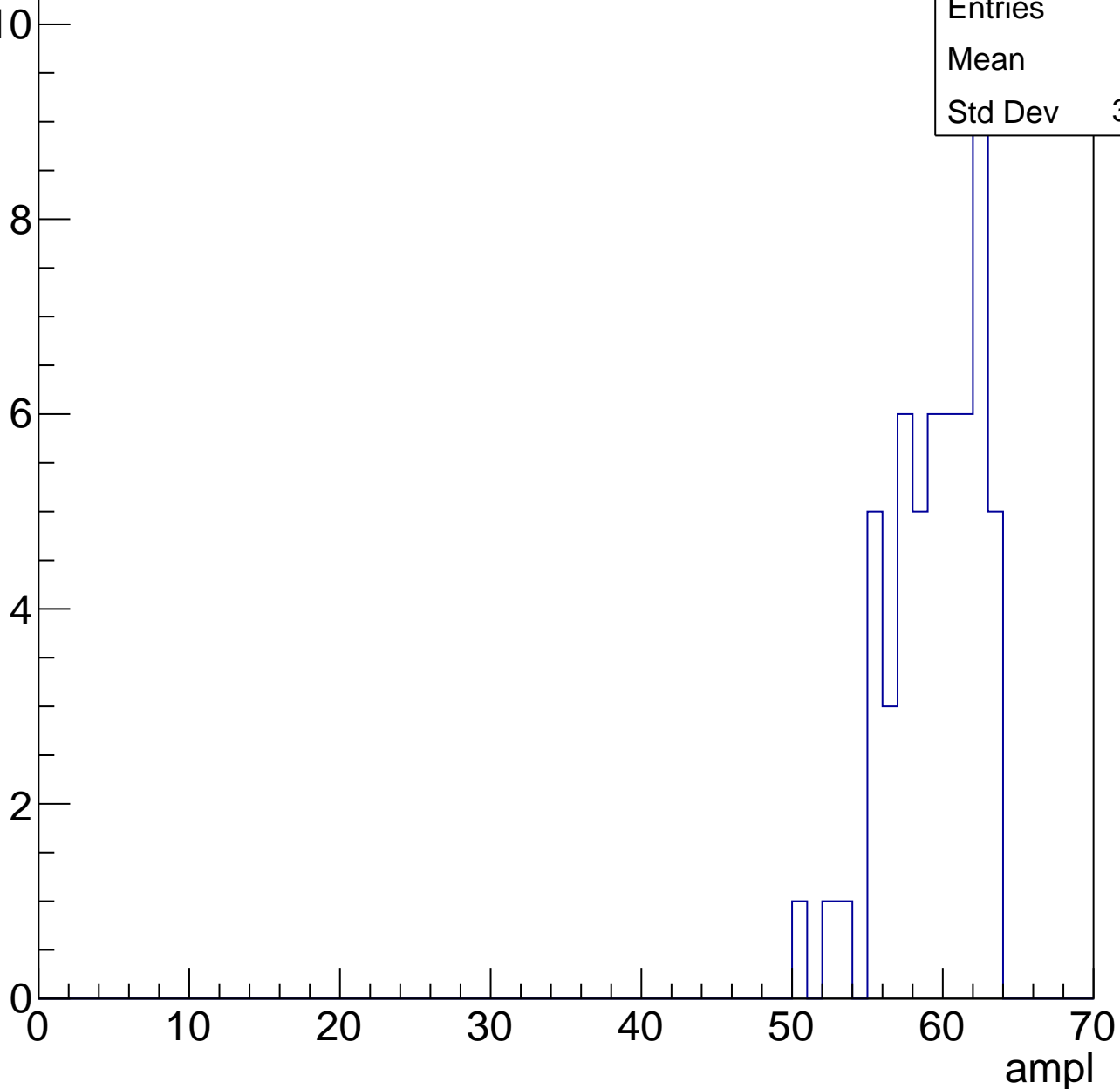


B0L000S, U2-ch102, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

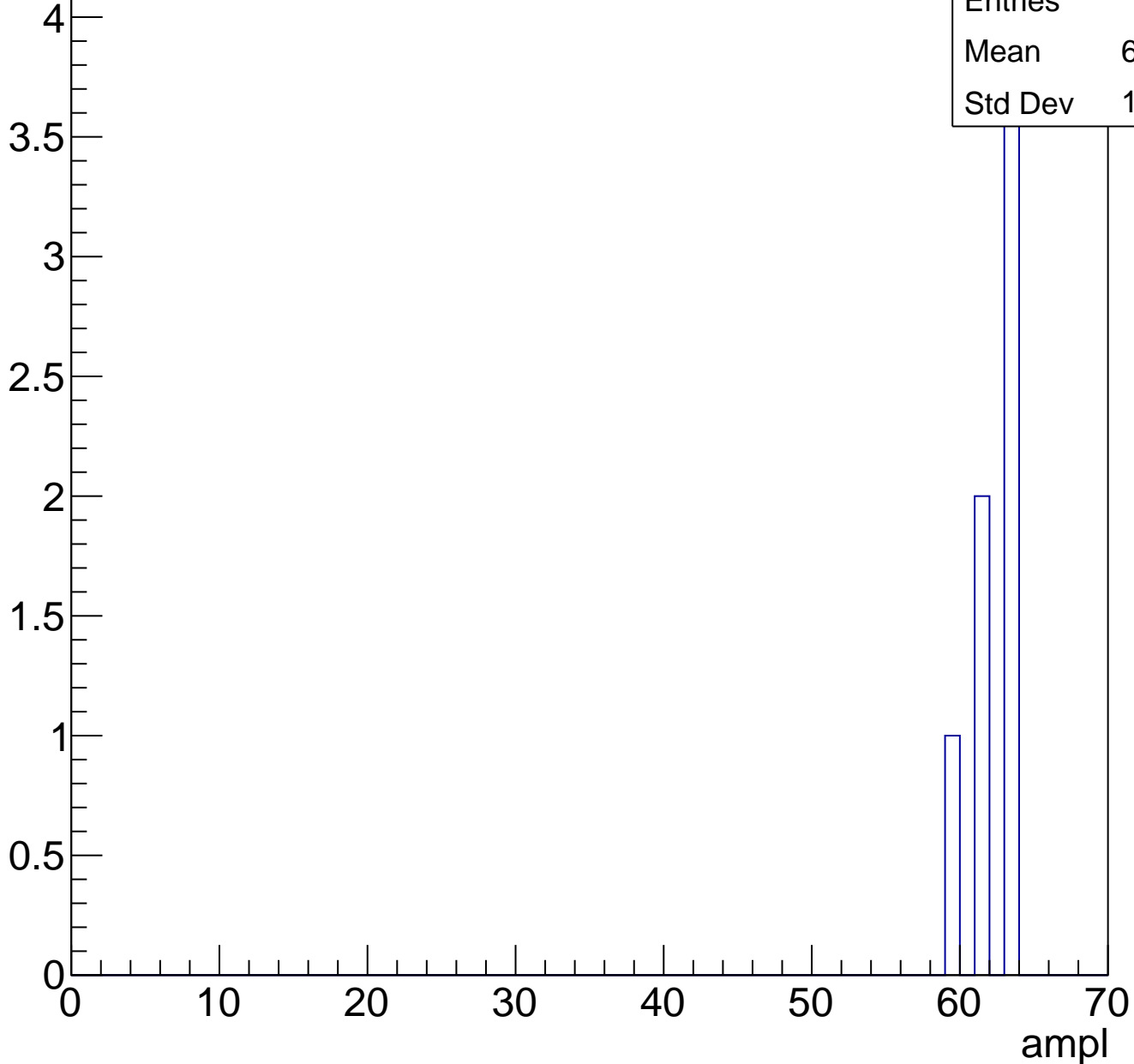
Entries	55
Mean	59
Std Dev	3.021



B0L000S, U2-ch102, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch102, adc7

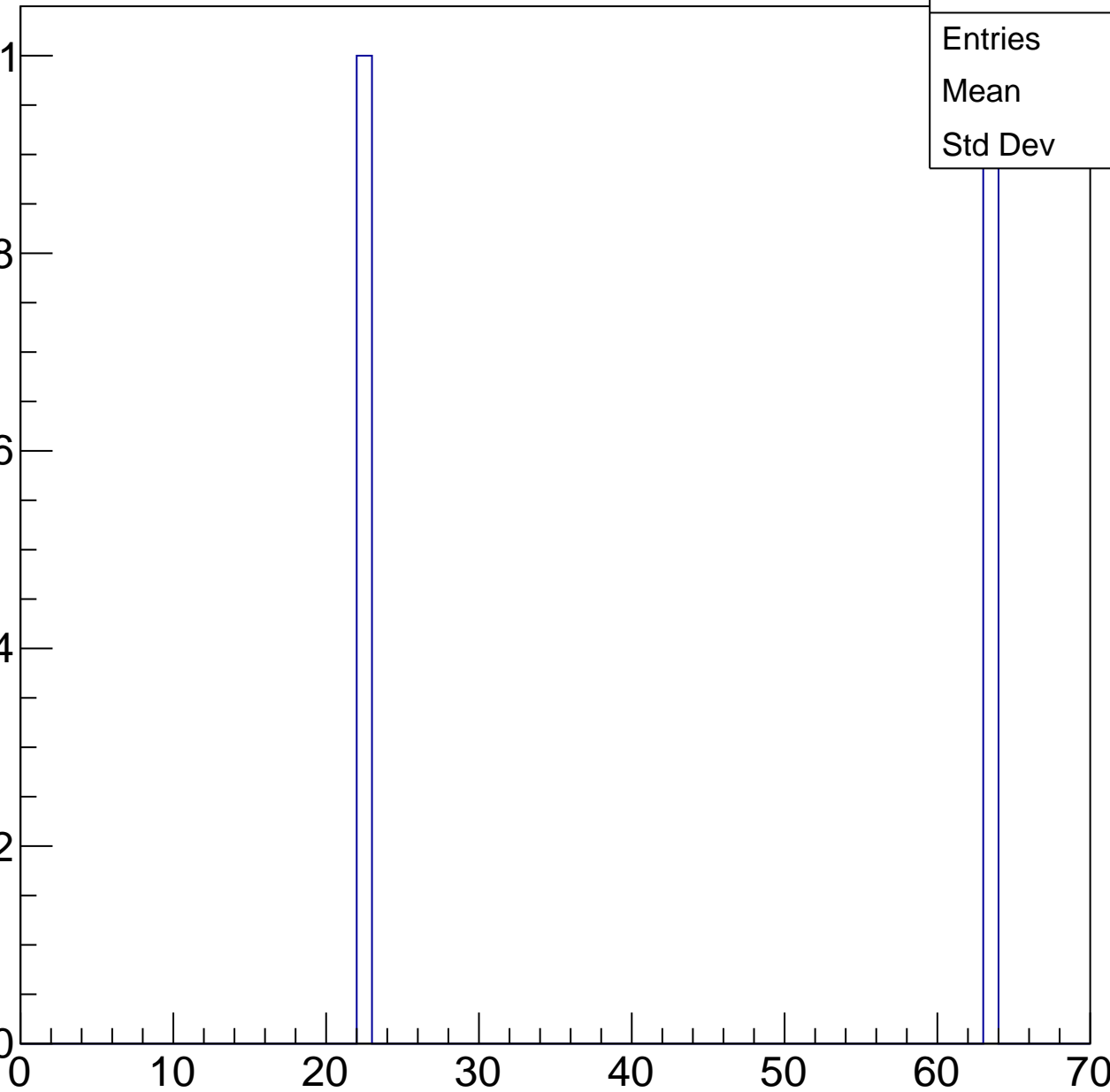
calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl



B0L000S, U2-ch103, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	68
Mean	30.06
Std Dev	3.329

Gaus mean : 30.1244

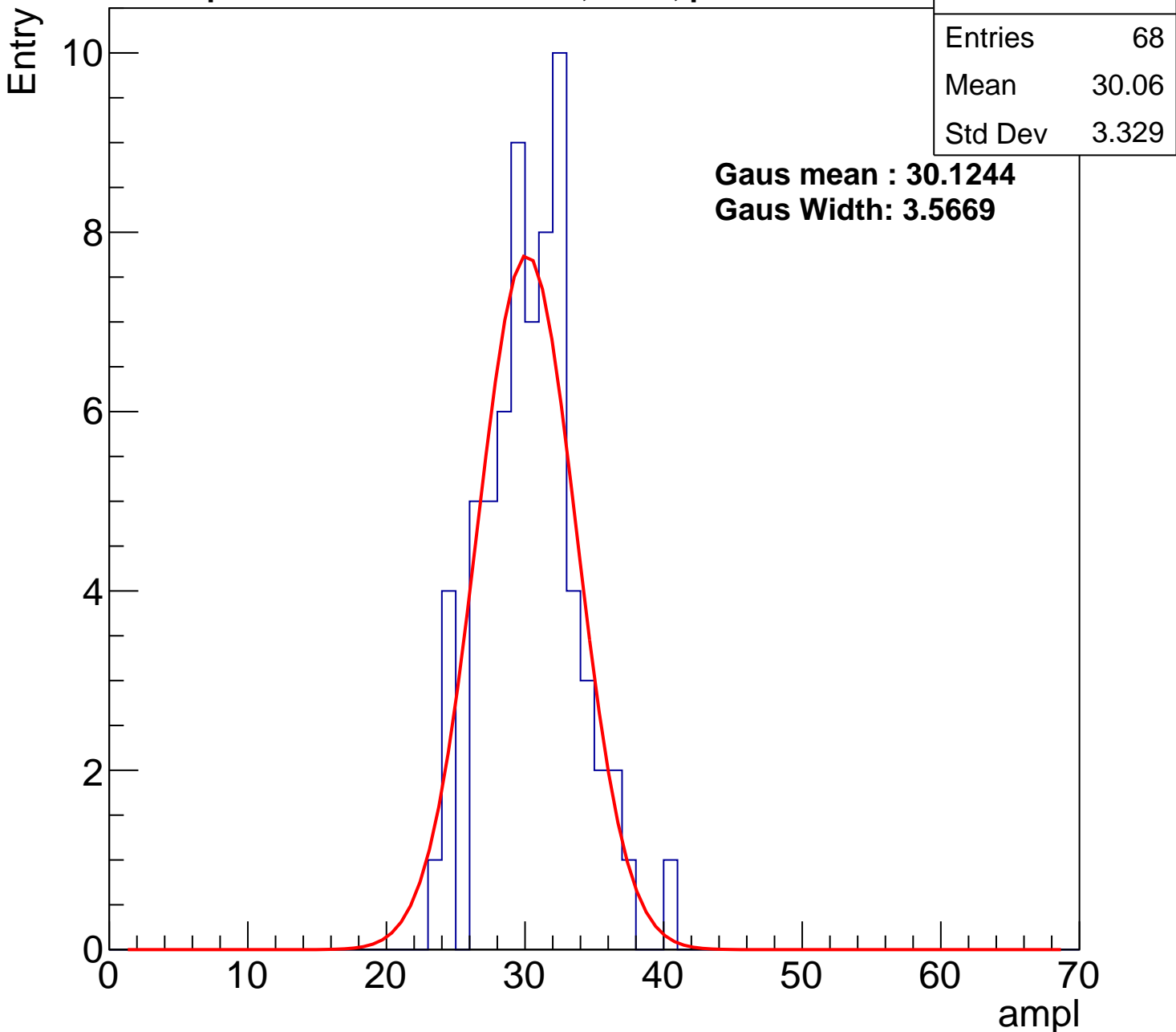
Gaus Width: 3.5669

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch103, adc1

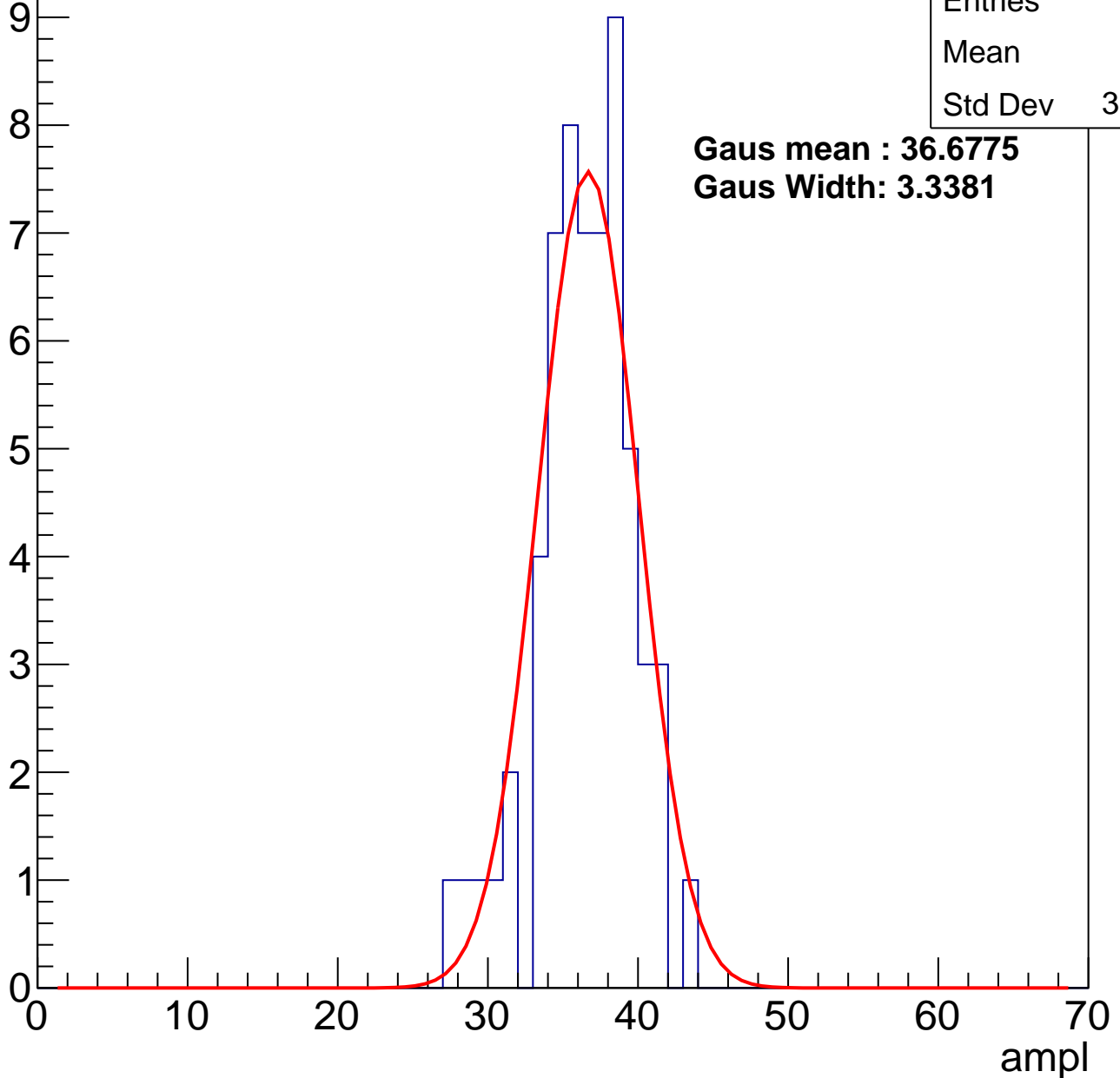
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36
Std Dev	3.194

Gaus mean : 36.6775

Gaus Width: 3.3381



B0L000S, U2-ch103, adc2

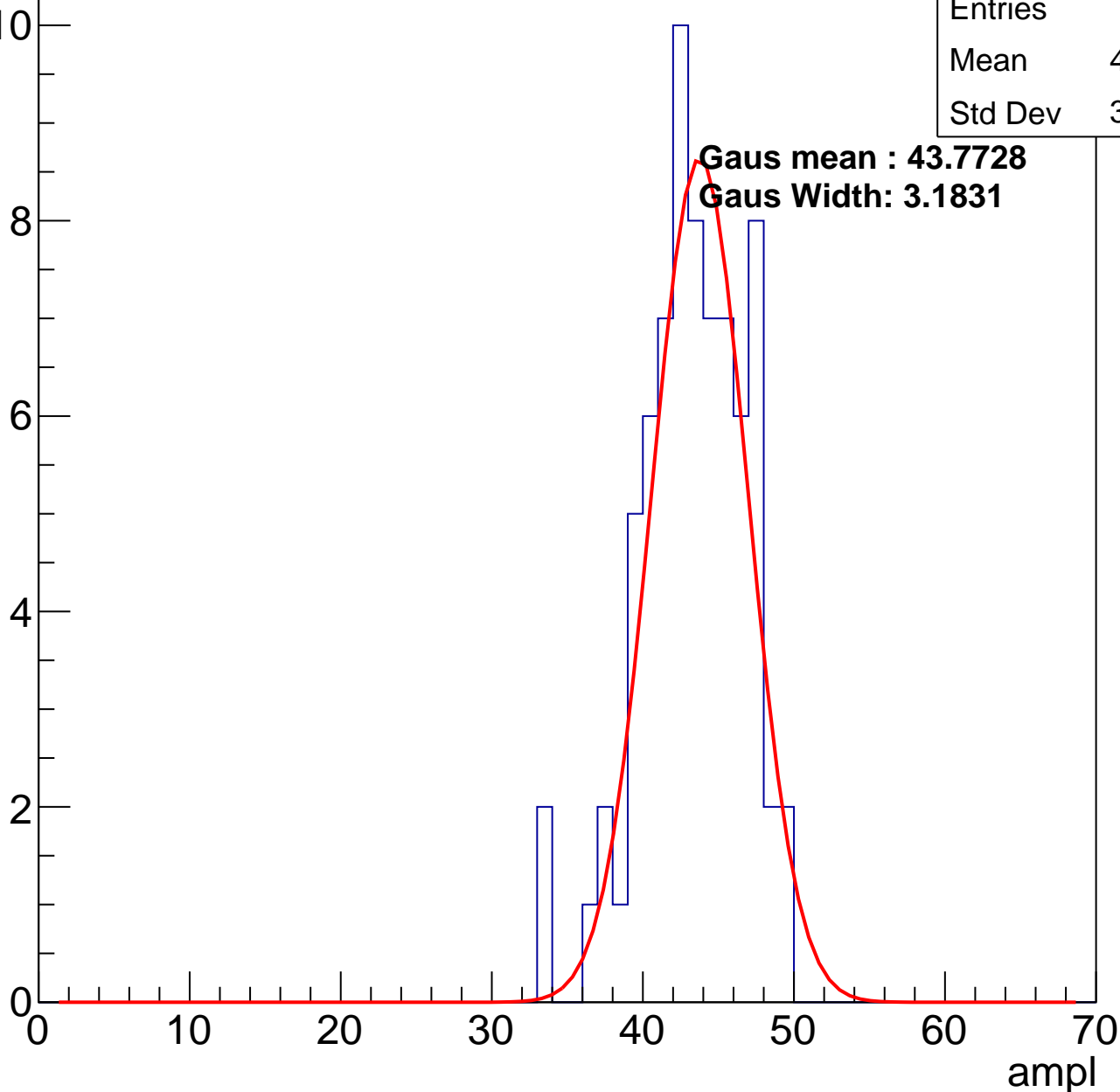
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.82
Std Dev	3.399

Gaus mean : 43.7728

Gaus Width: 3.1831

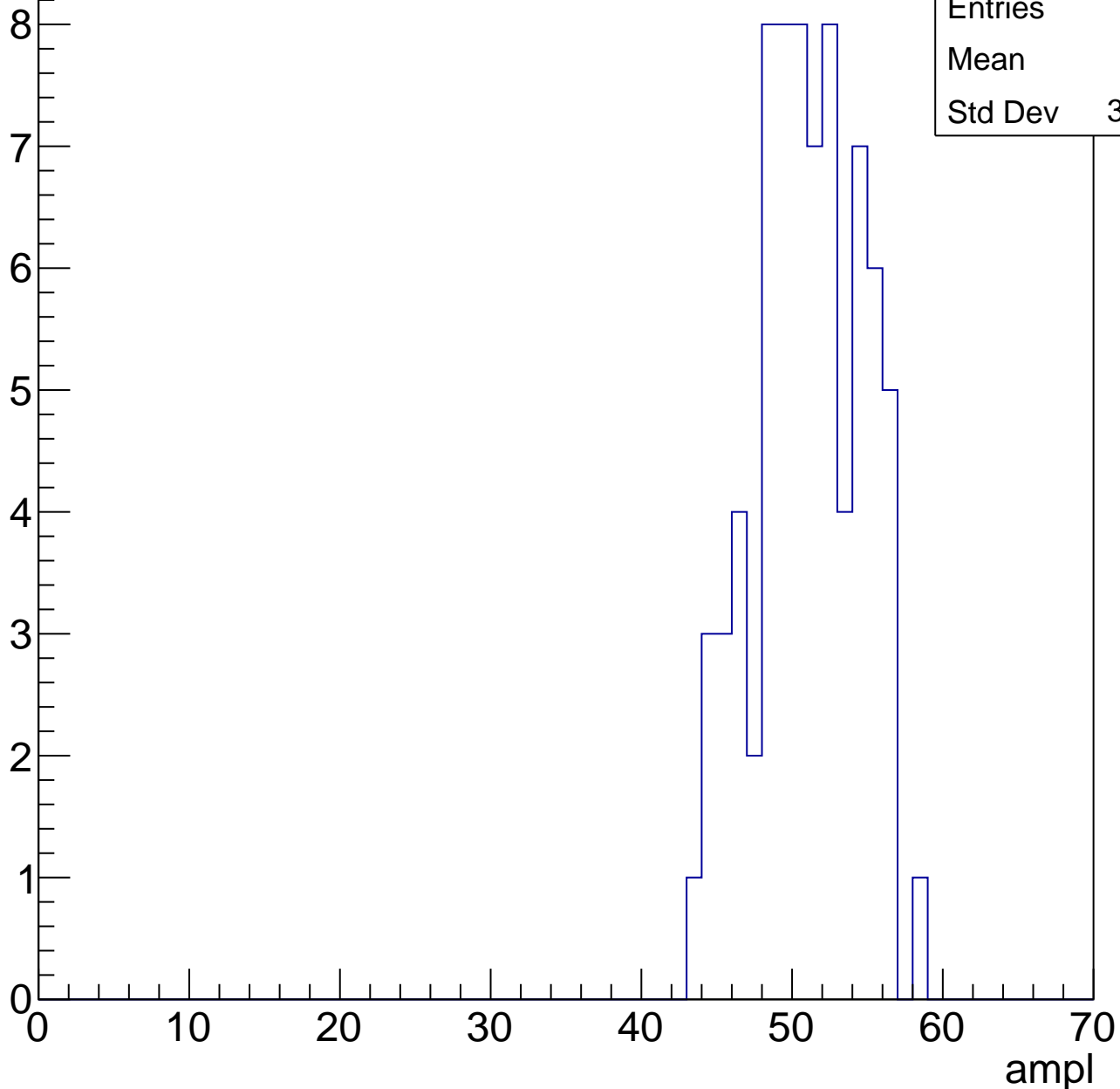


B0L000S, U2-ch103, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	50.6
Std Dev	3.479

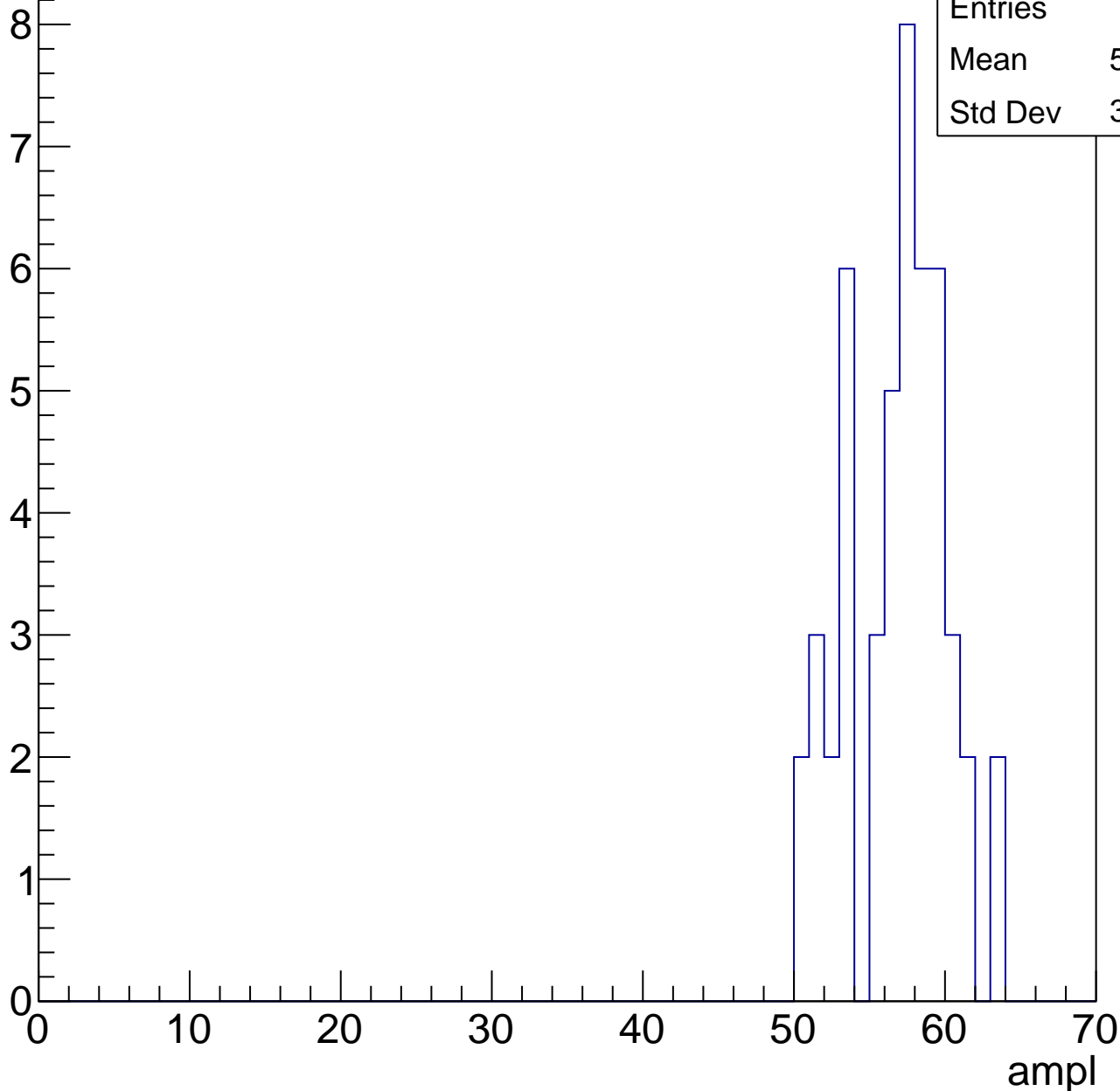


B0L000S, U2-ch103, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	56.38
Std Dev	3.264

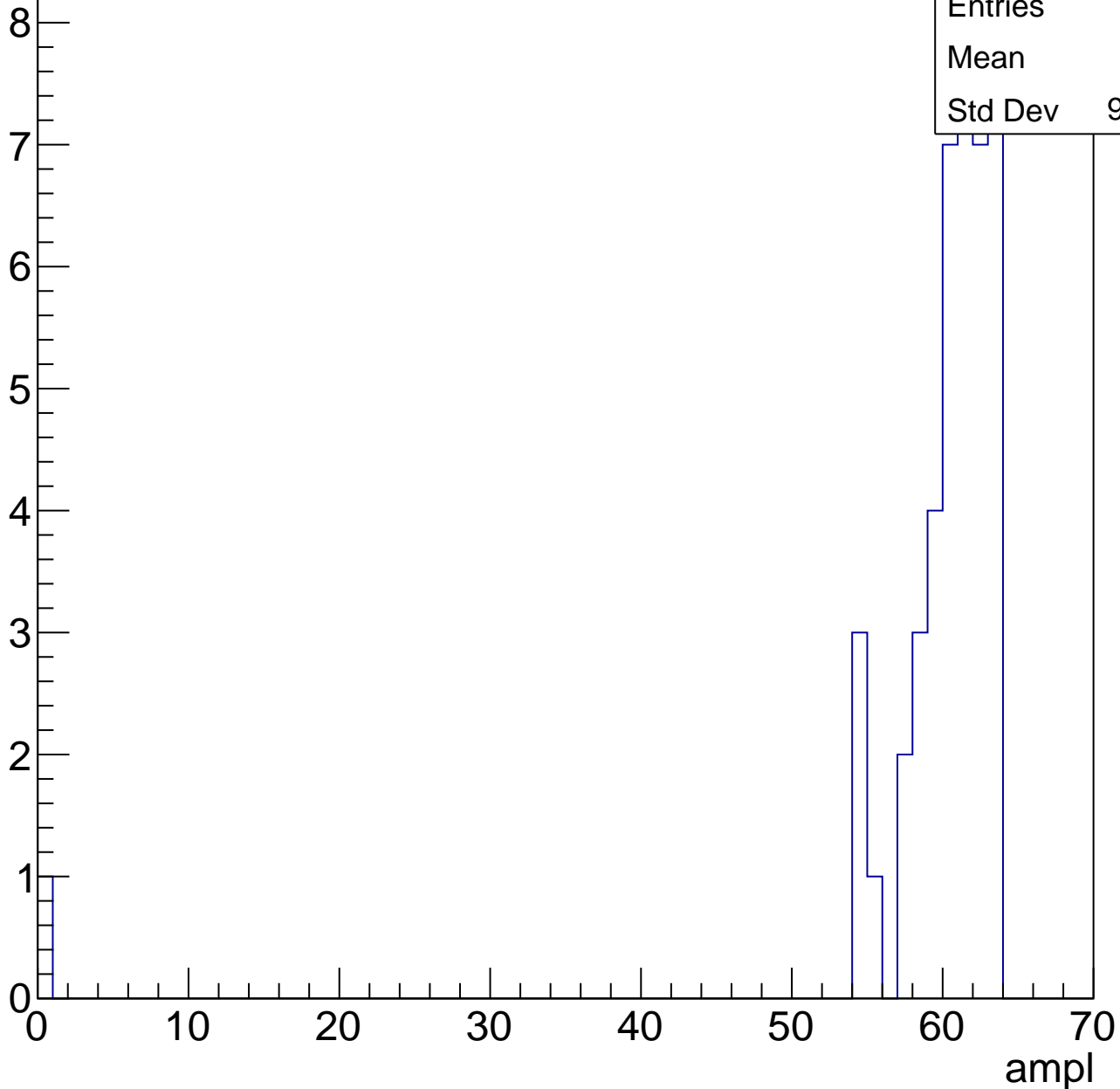


B0L000S, U2-ch103, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.8
Std Dev	9.307



B0L000S, U2-ch103, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch103, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch104, adc0

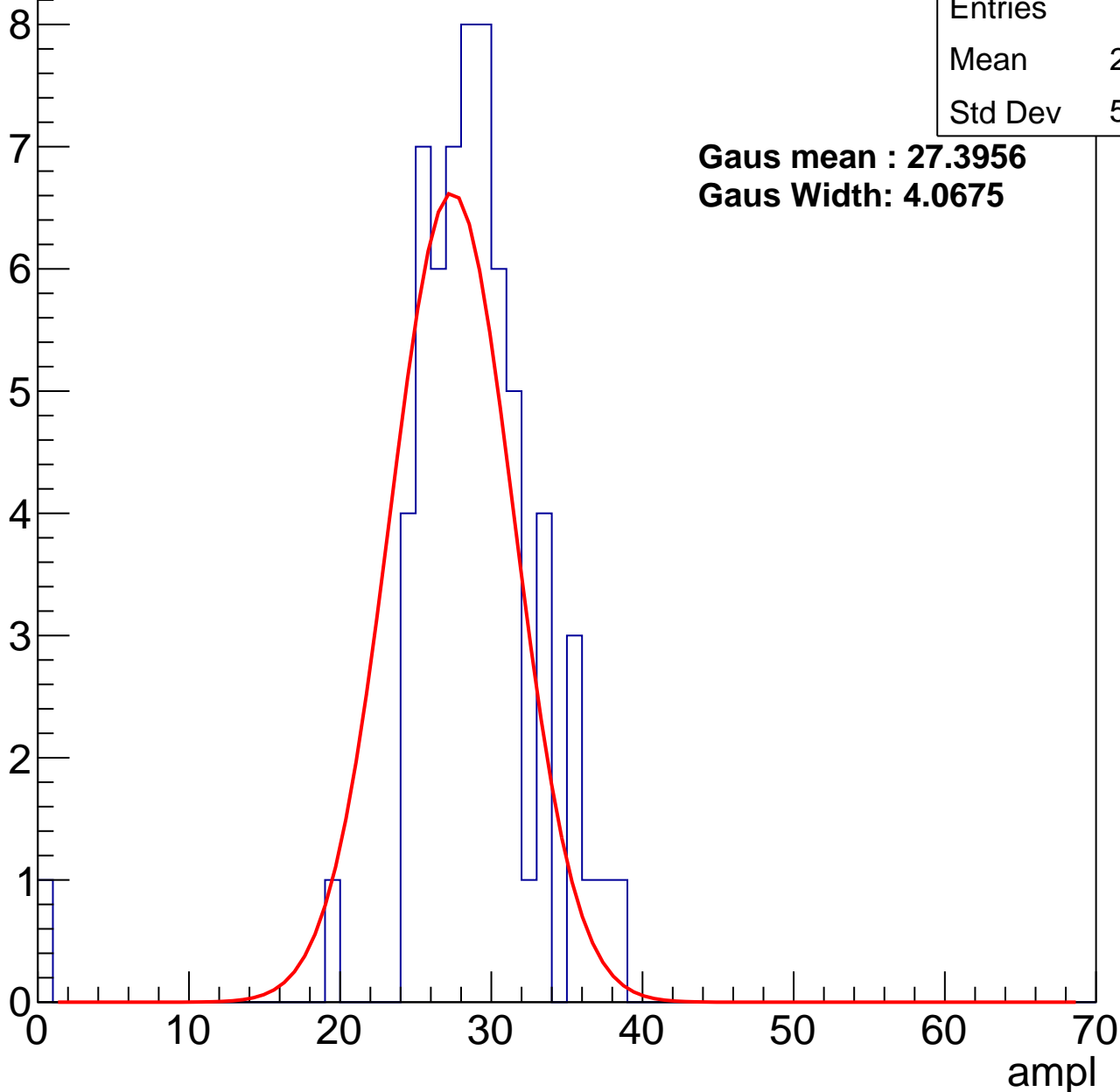
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	28.22
Std Dev	5.008

Gaus mean : 27.3956

Gaus Width: 4.0675



B0L000S, U2-ch104, adc1

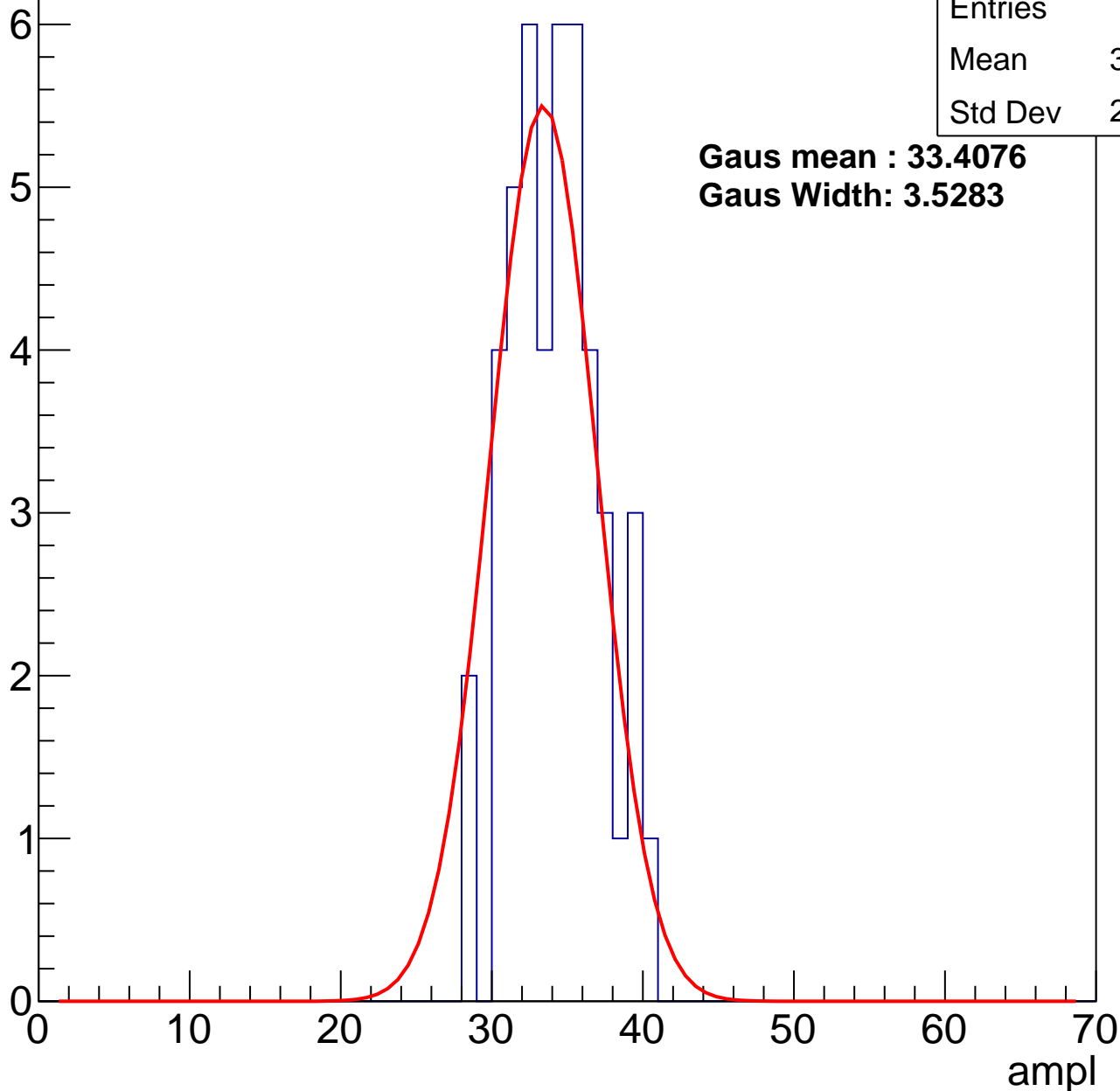
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	45
Mean	33.76
Std Dev	2.915

Gaus mean : 33.4076

Gaus Width: 3.5283



B0L000S, U2-ch104, adc2

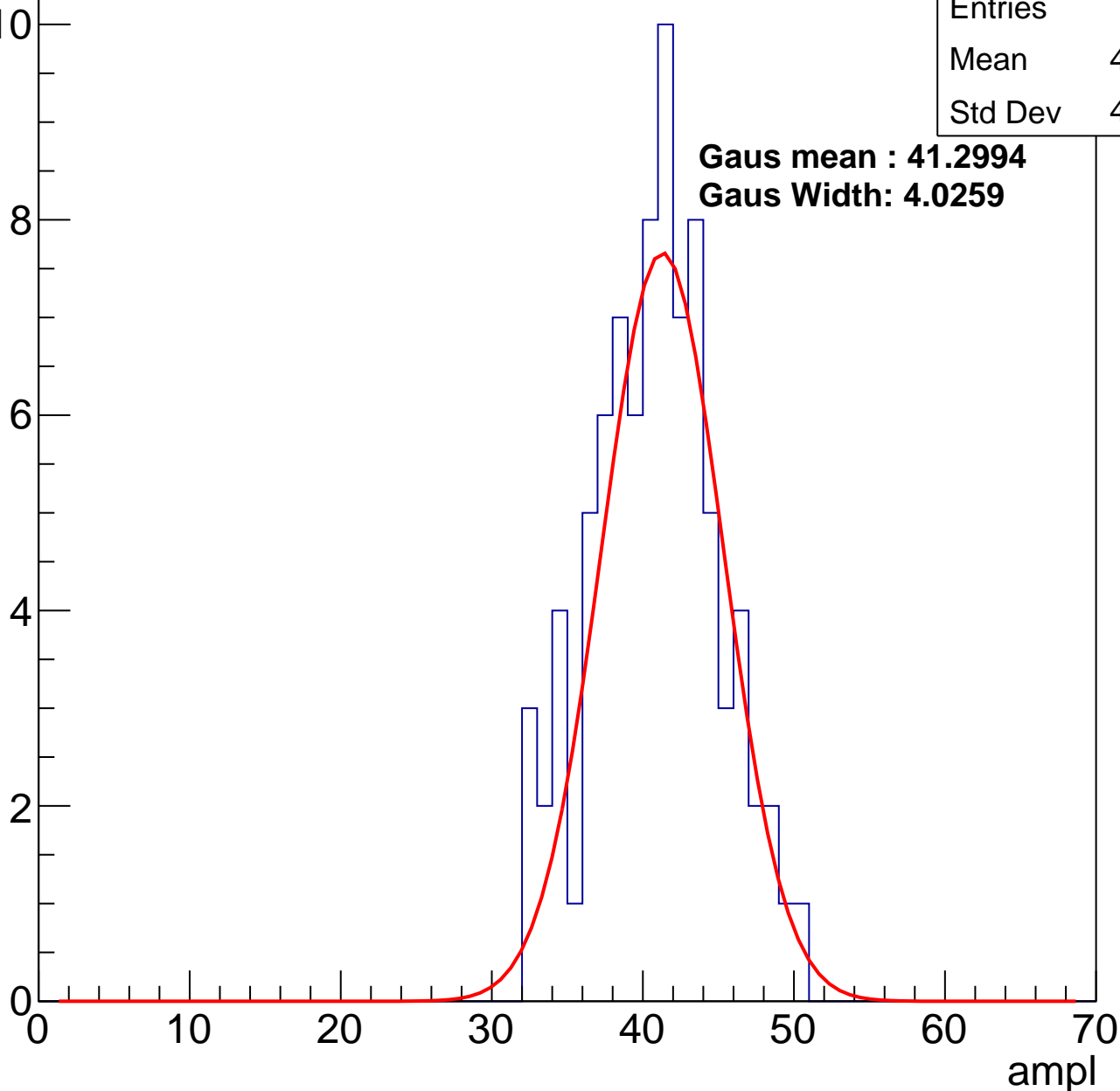
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	85
Mean	40.36
Std Dev	4.104

Gaus mean : 41.2994

Gaus Width: 4.0259

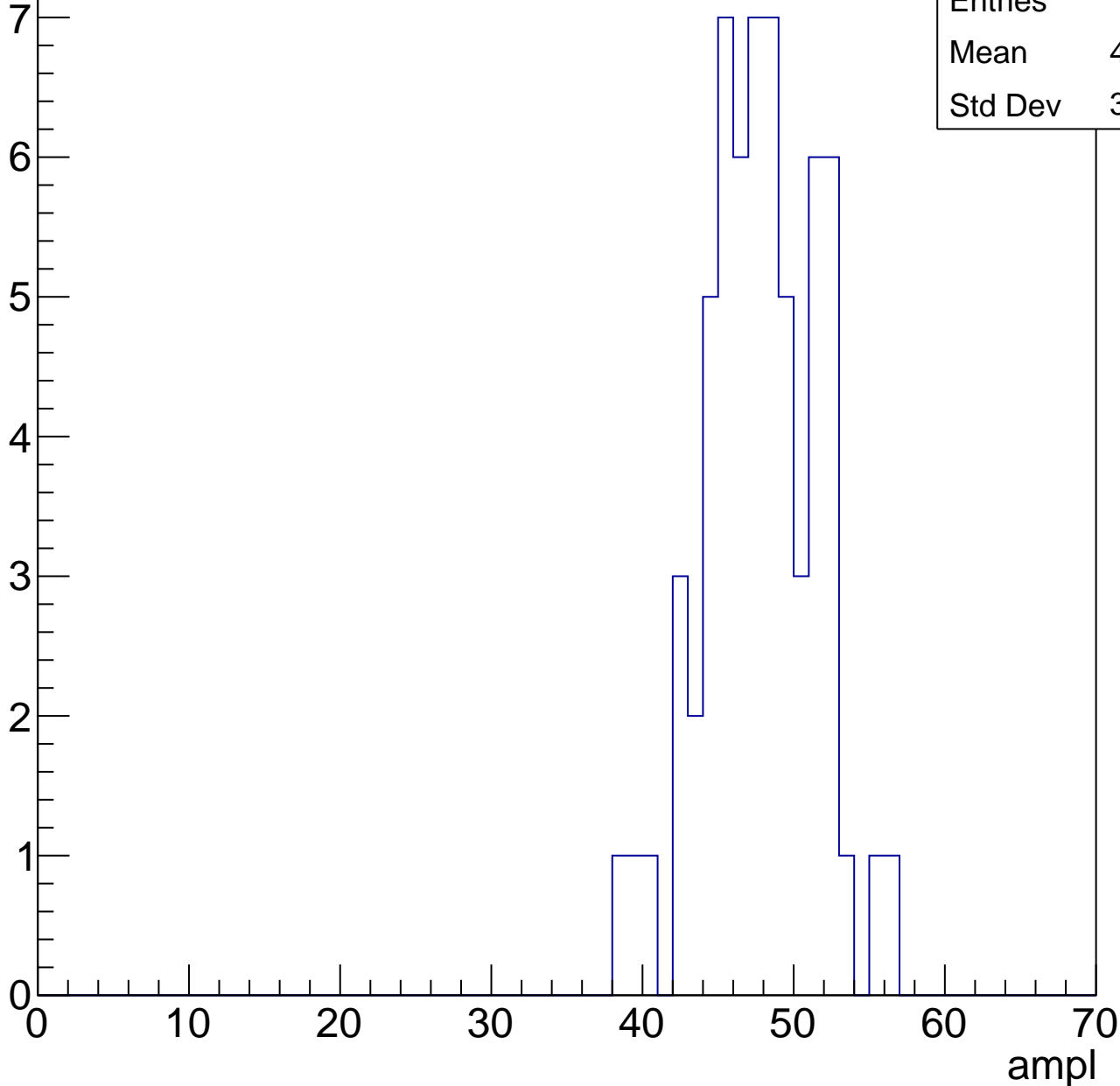


B0L000S, U2-ch104, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	47.33
Std Dev	3.682

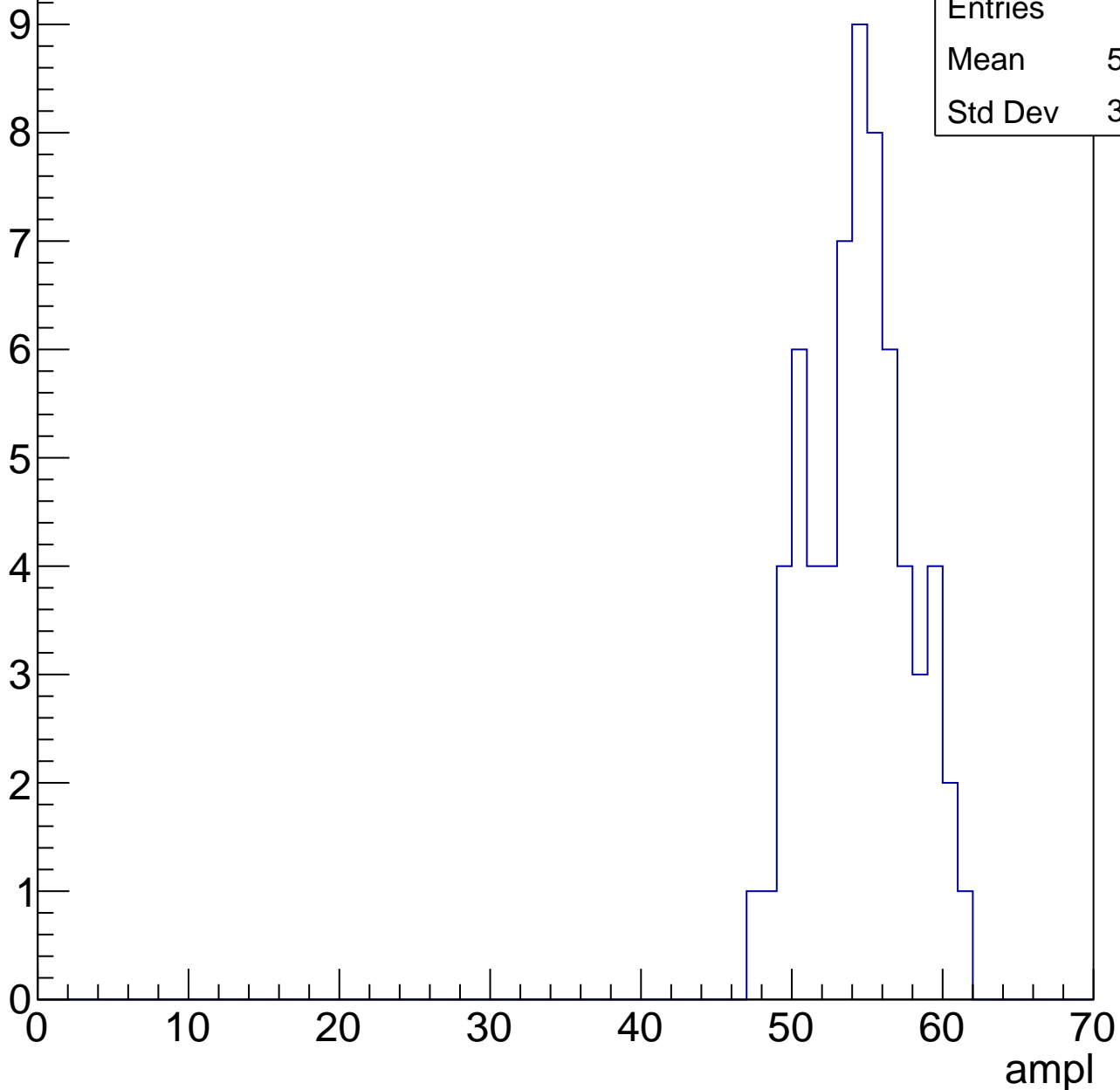


B0L000S, U2-ch104, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	53.98
Std Dev	3.252

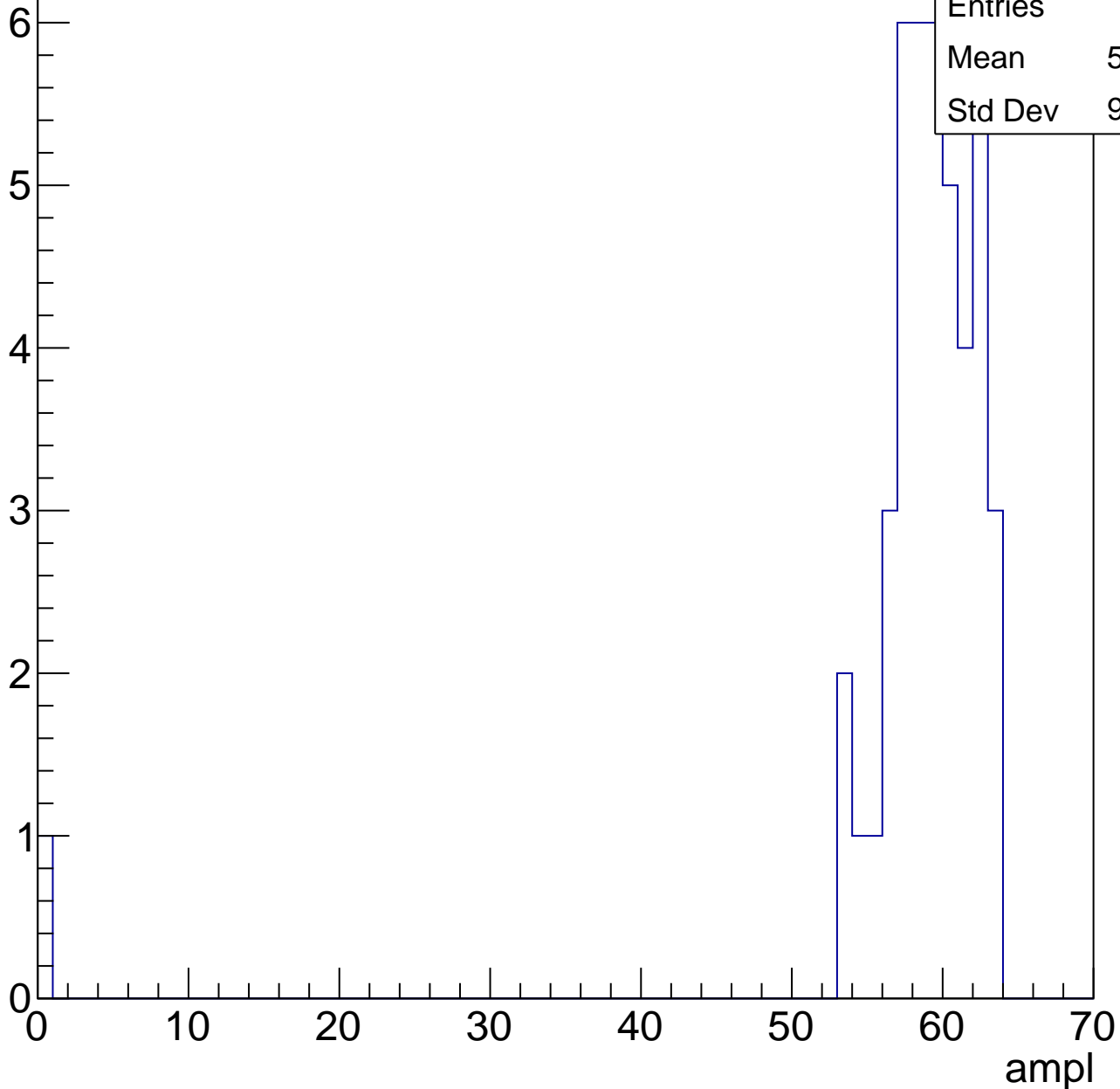


B0L000S, U2-ch104, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	57.55
Std Dev	9.146

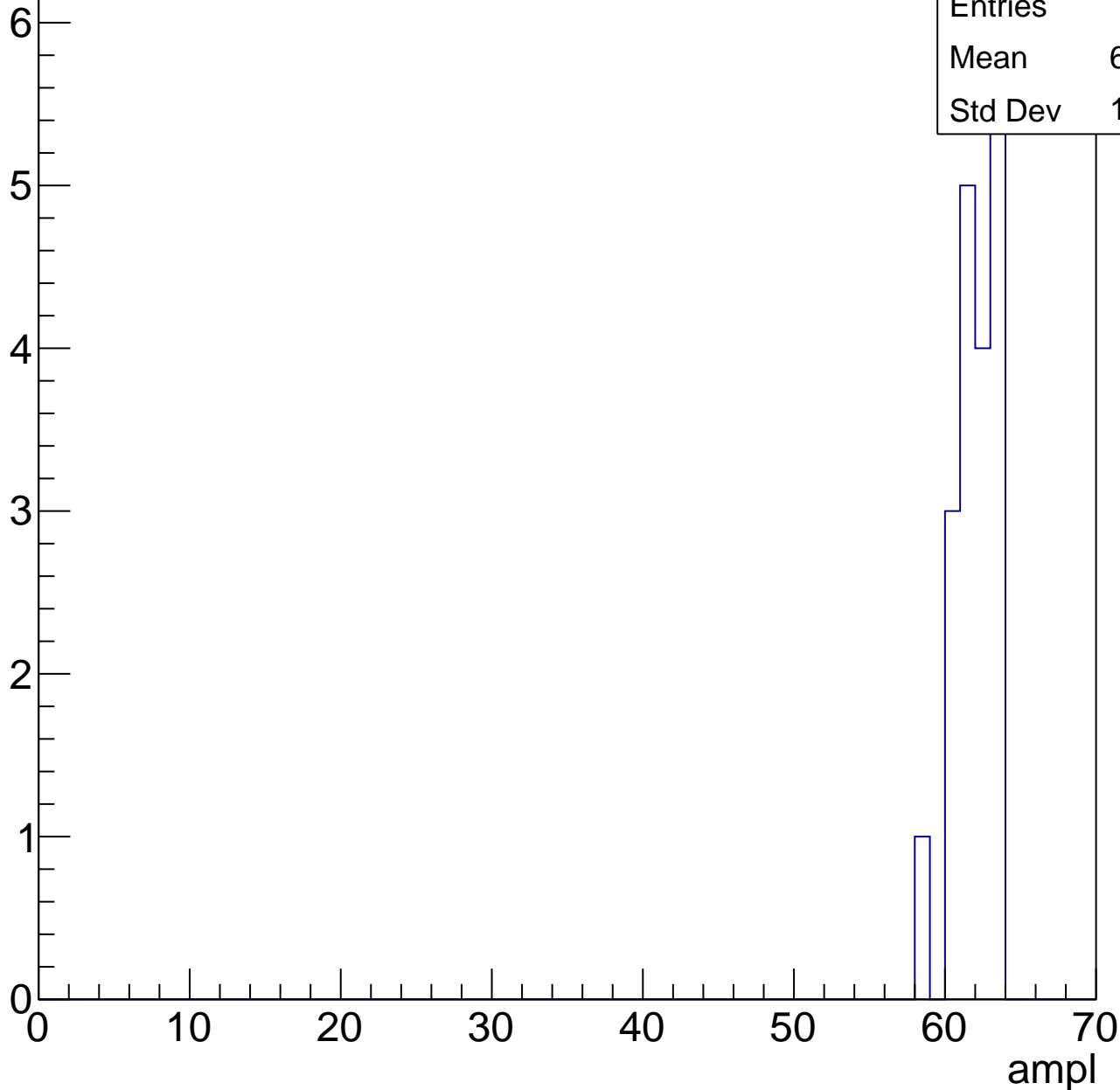


B0L000S, U2-ch104, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	19
Mean	61.53
Std Dev	1.352



B0L000S, U2-ch104, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L000S, U2-ch105, adc0

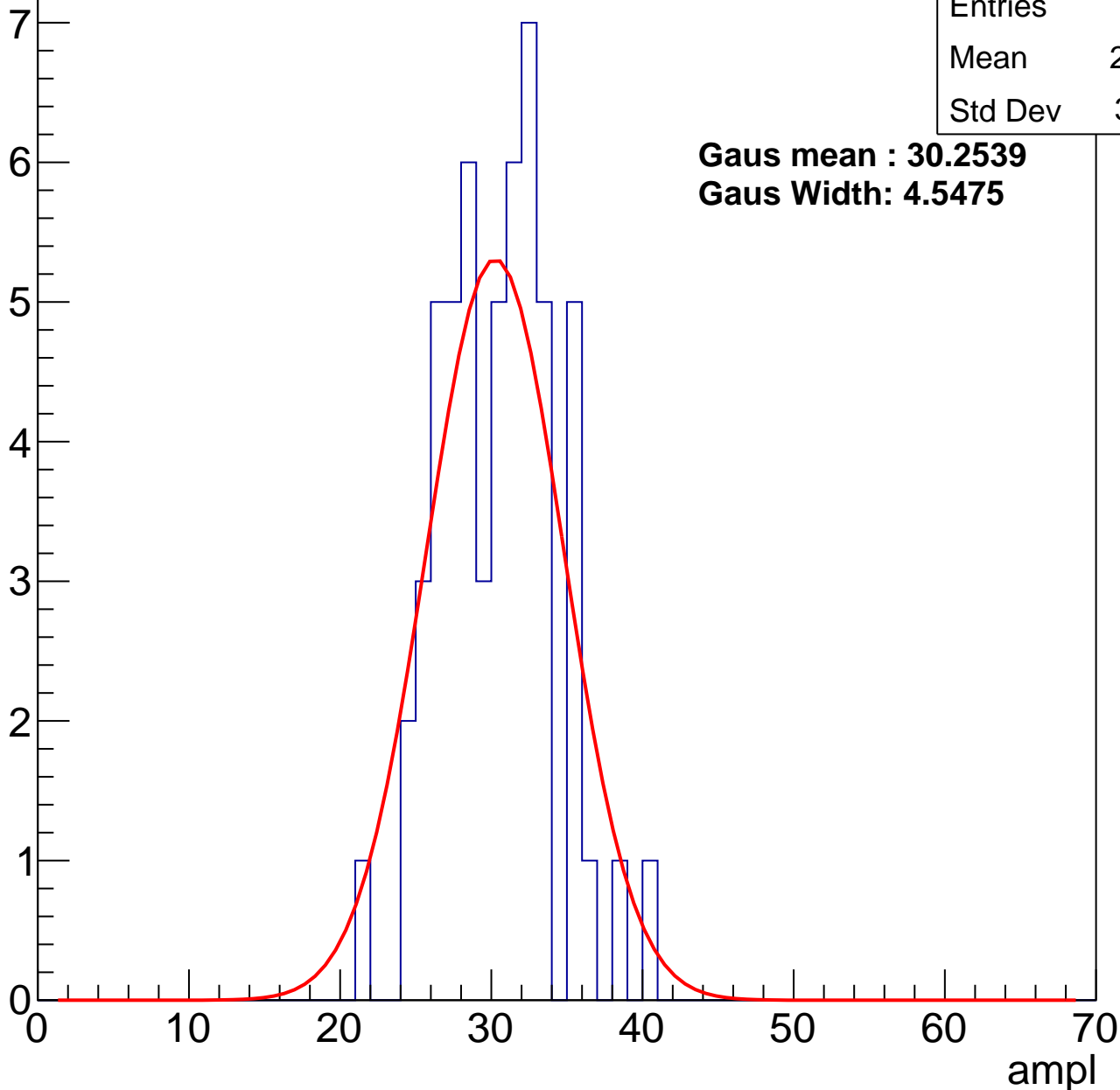
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.96
Std Dev	3.741

Gaus mean : 30.2539

Gaus Width: 4.5475



B0L000S, U2-ch105, adc1

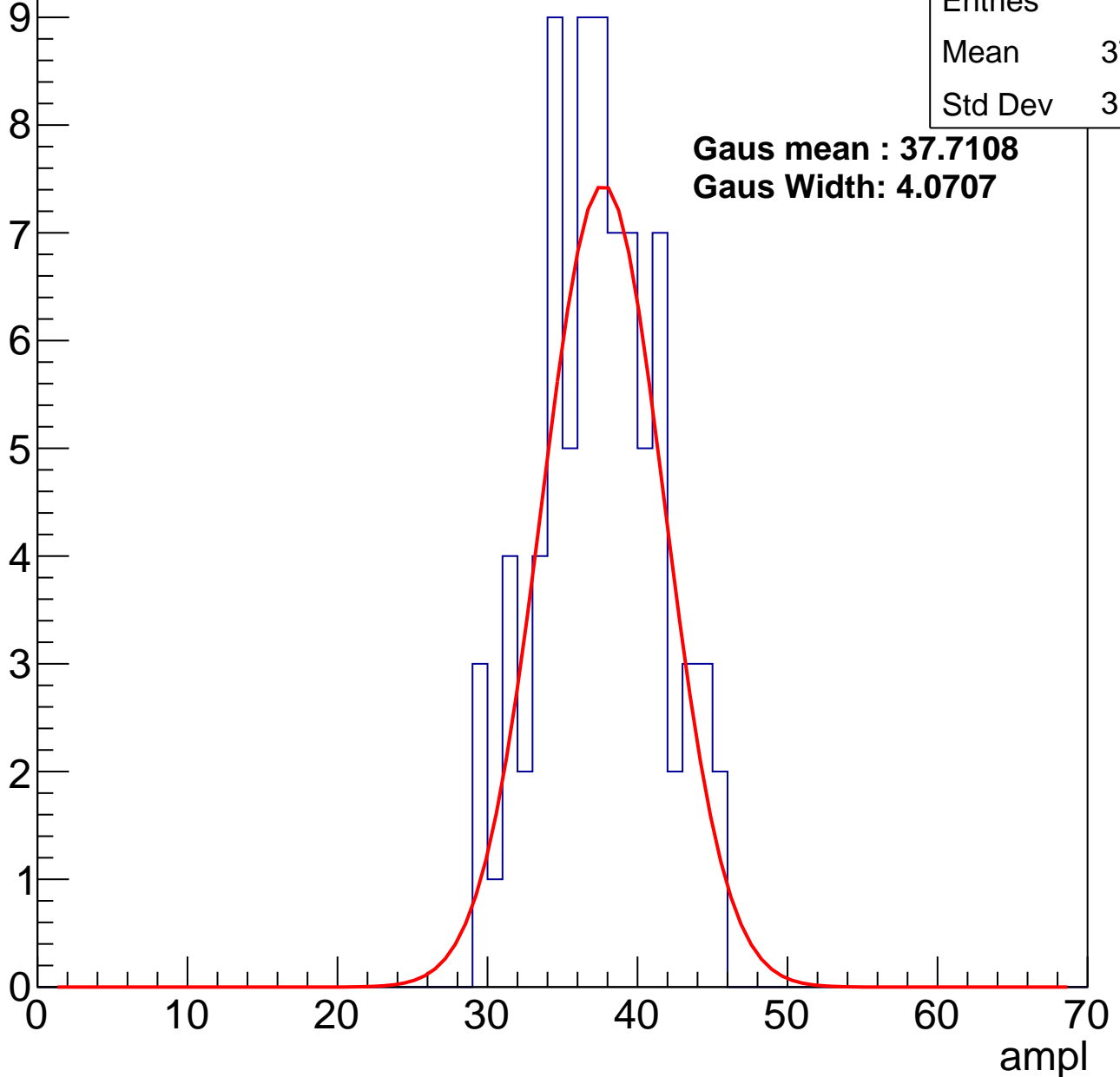
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	37.02
Std Dev	3.879

Gaus mean : 37.7108

Gaus Width: 4.0707



B0L000S, U2-ch105, adc2

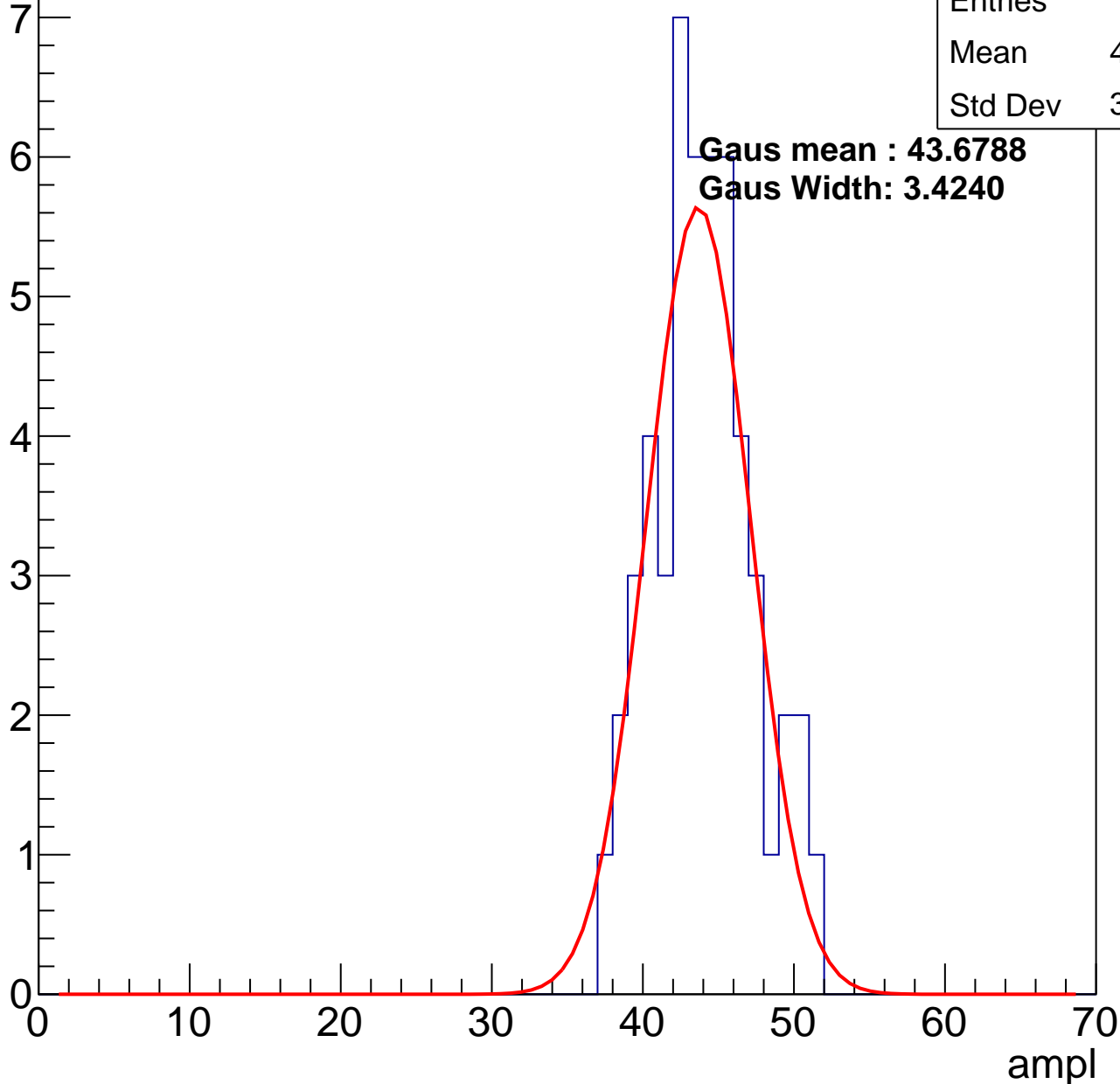
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	43.55
Std Dev	3.274

Gaus mean : 43.6788

Gaus Width: 3.4240

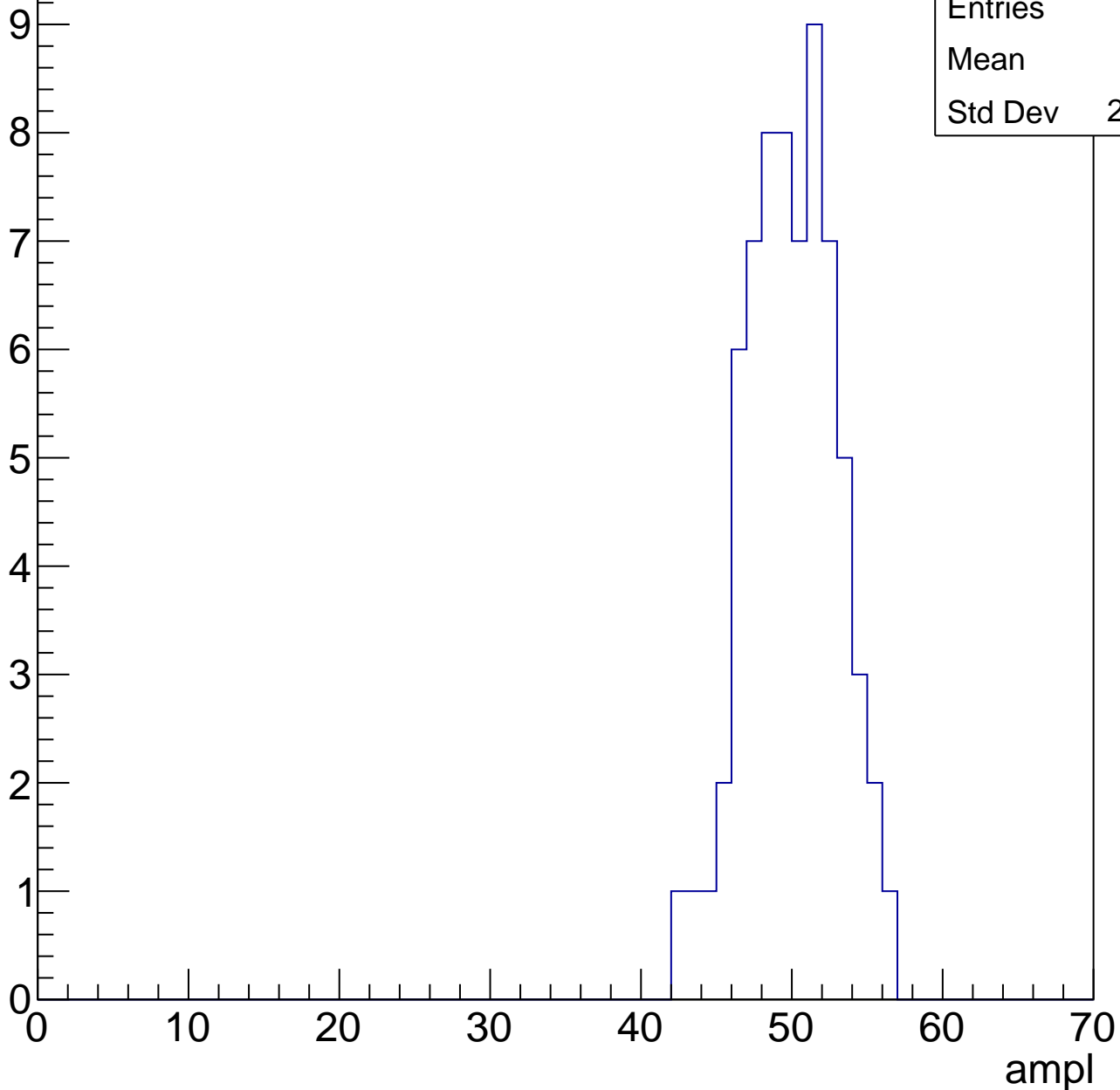


B0L000S, U2-ch105, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.5
Std Dev	2.963

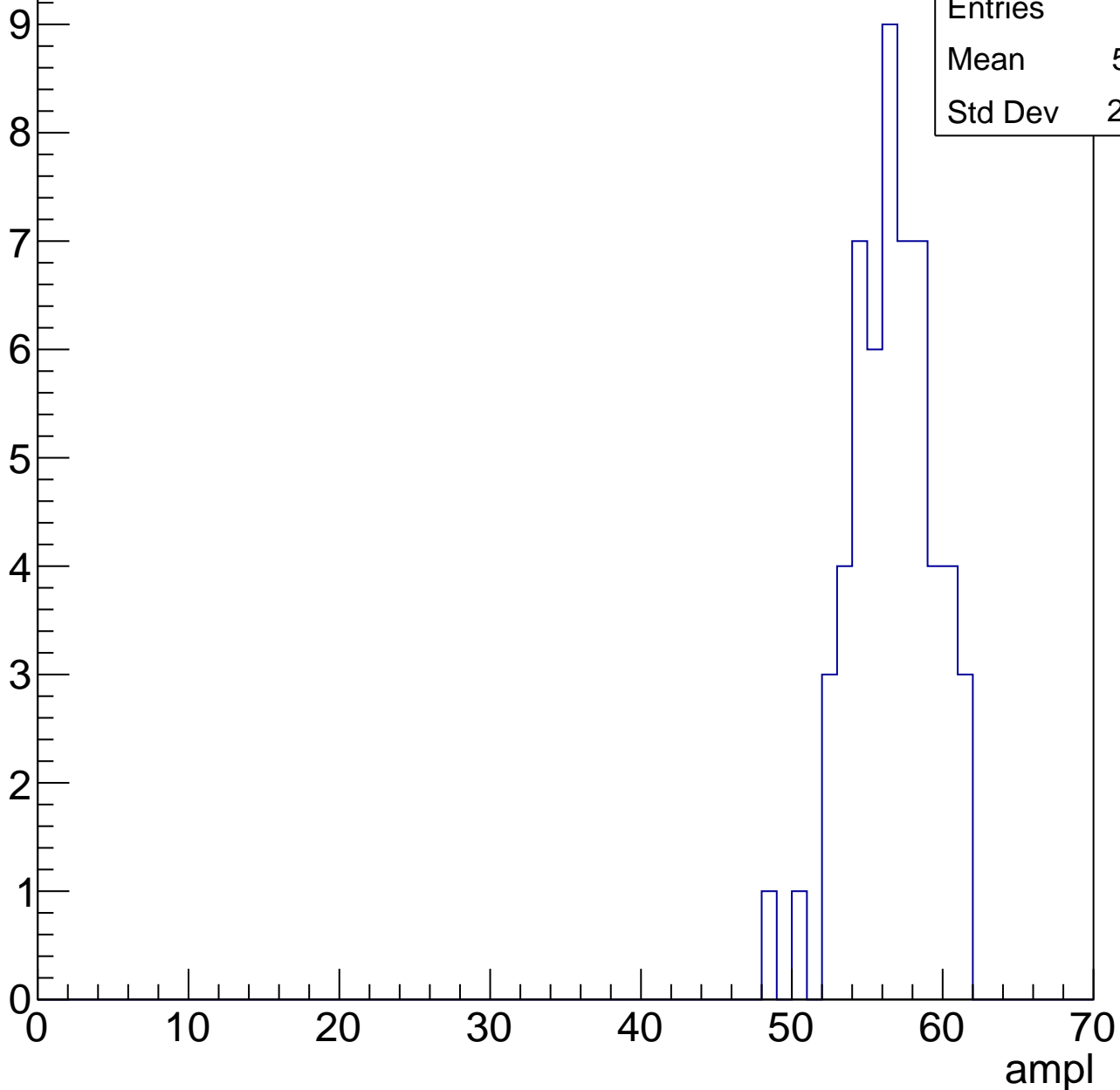


B0L000S, U2-ch105, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	56.11
Std Dev	2.762

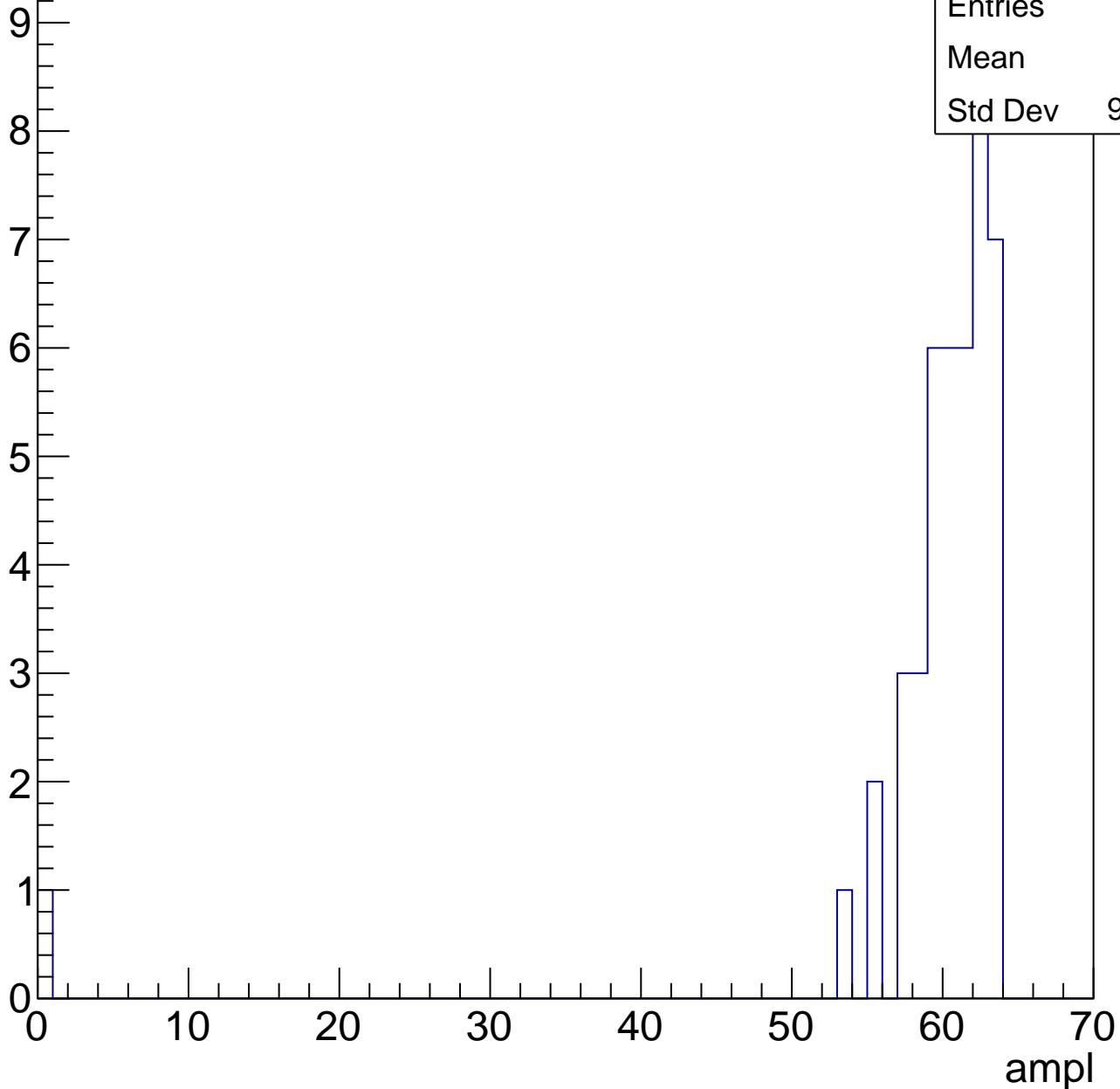


B0L000S, U2-ch105, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.8
Std Dev	9.275



B0L000S, U2-ch105, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	5
Mean	62.4
Std Dev	0.8

B0L000S, U2-ch105, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch106, adc0

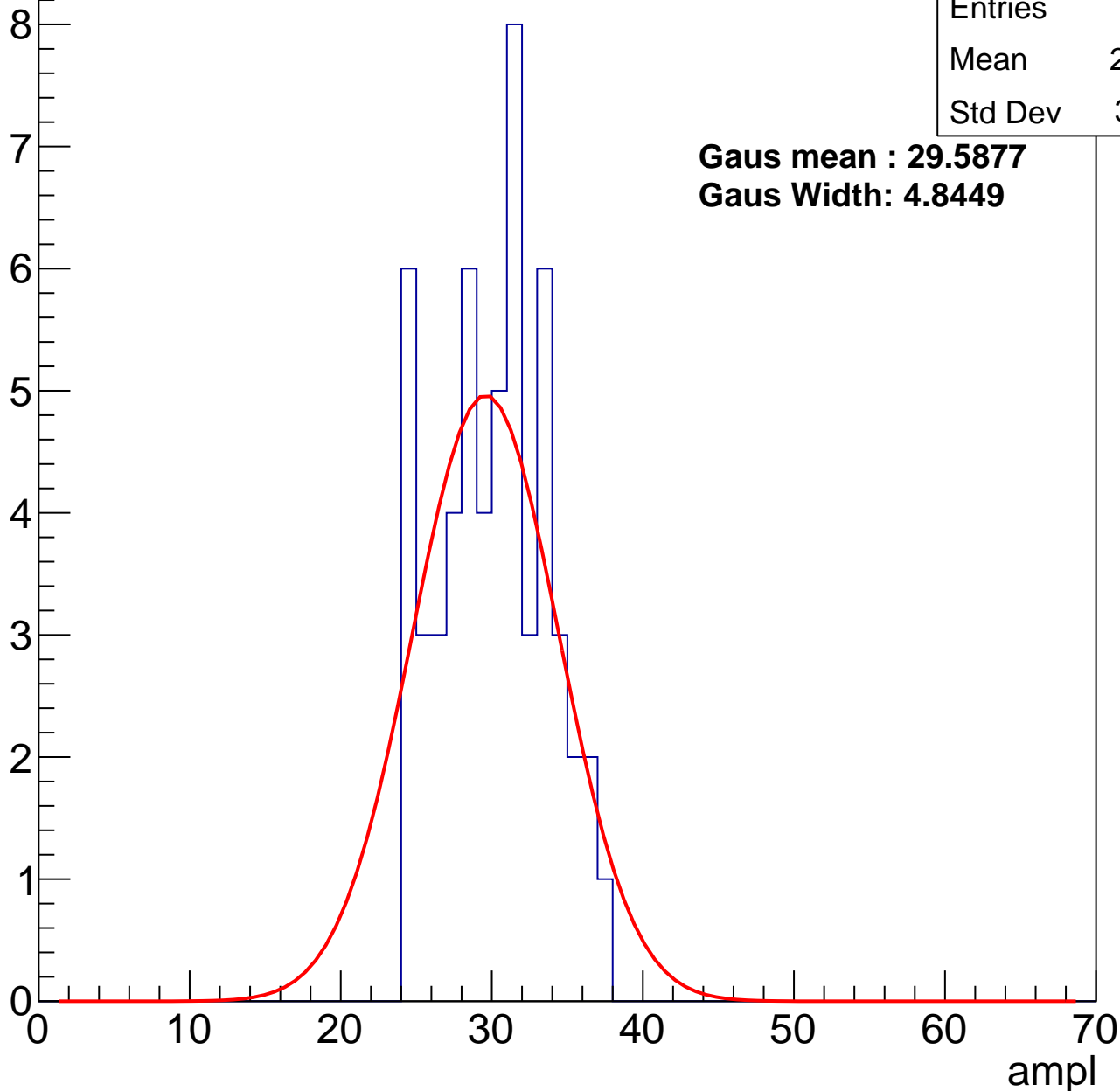
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.68
Std Dev	3.511

Gaus mean : 29.5877

Gaus Width: 4.8449



B0L000S, U2-ch106, adc1

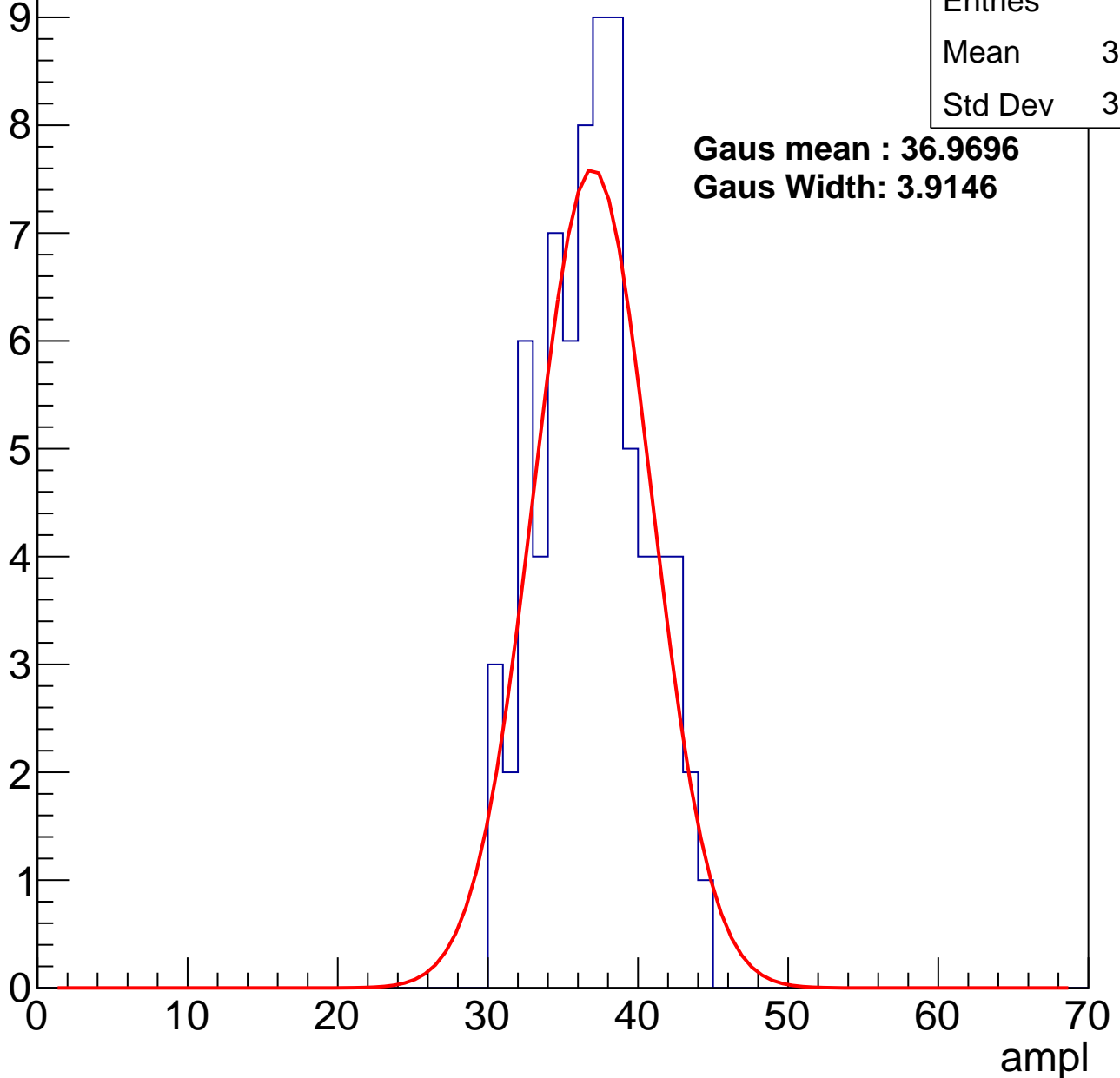
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.54
Std Dev	3.414

Gaus mean : 36.9696

Gaus Width: 3.9146



B0L000S, U2-ch106, adc2

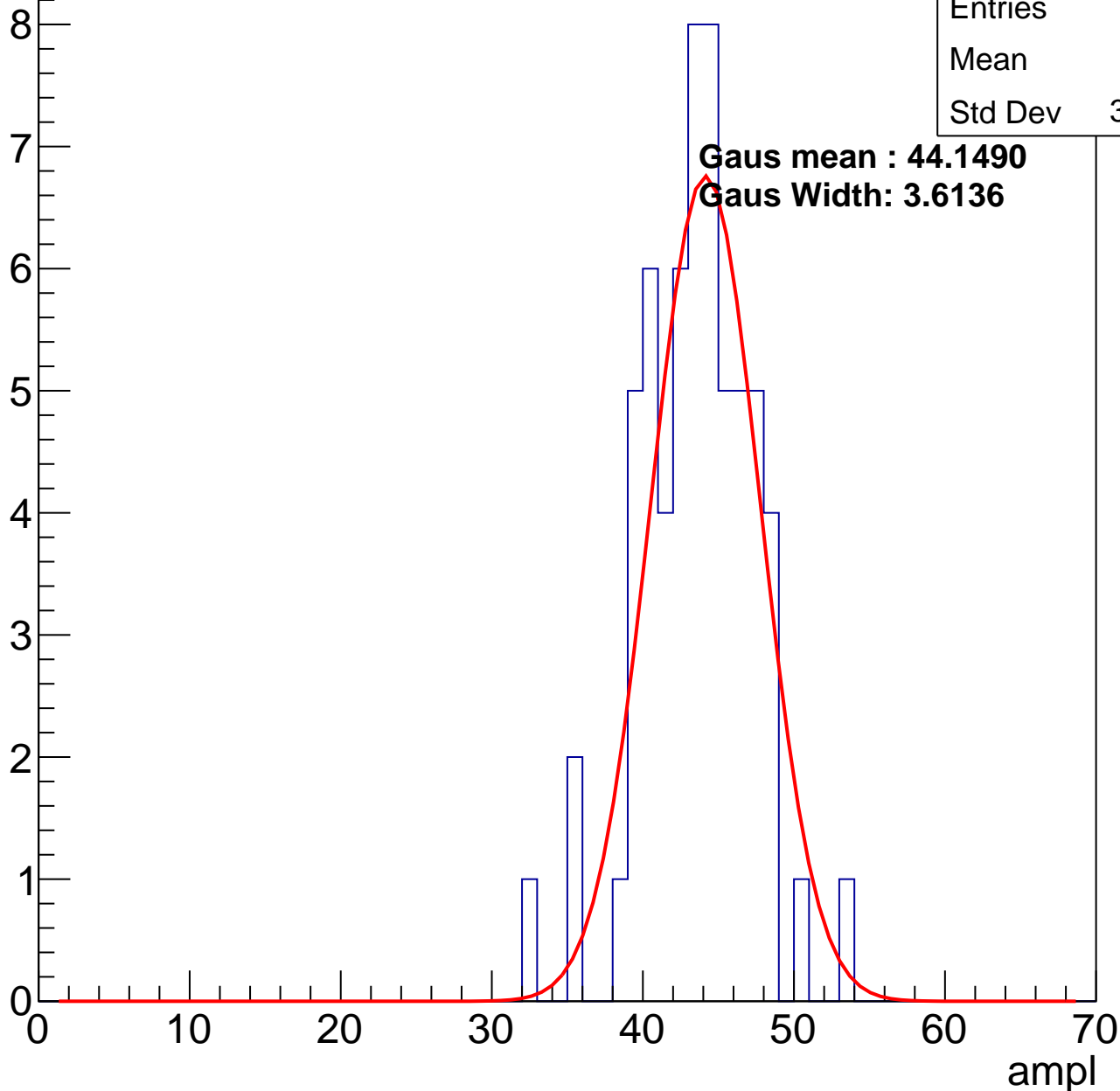
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.1
Std Dev	3.662

Gaus mean : 44.1490

Gaus Width: 3.6136

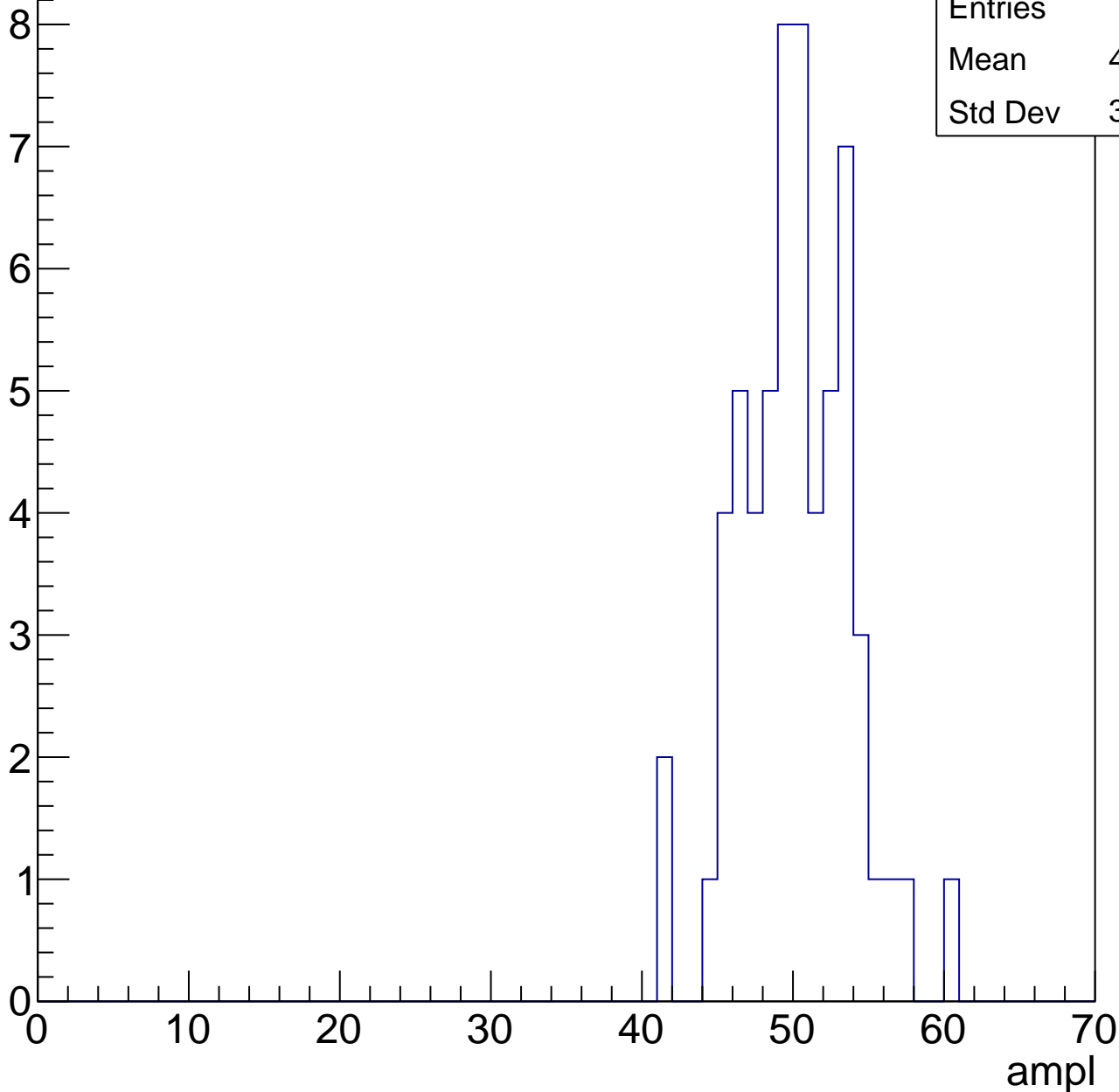


B0L000S, U2-ch106, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	49.68
Std Dev	3.608

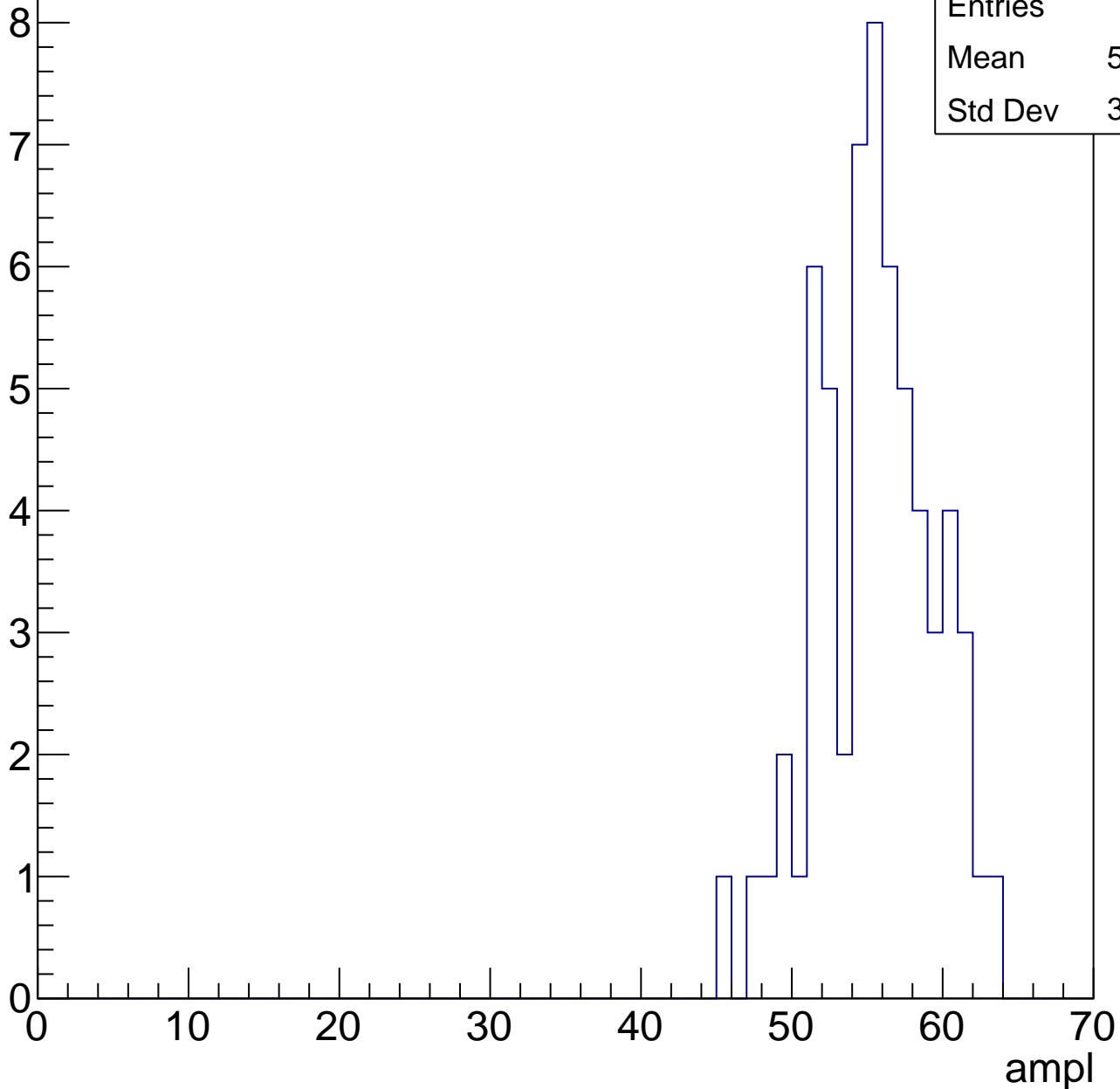


B0L000S, U2-ch106, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	55.02
Std Dev	3.835

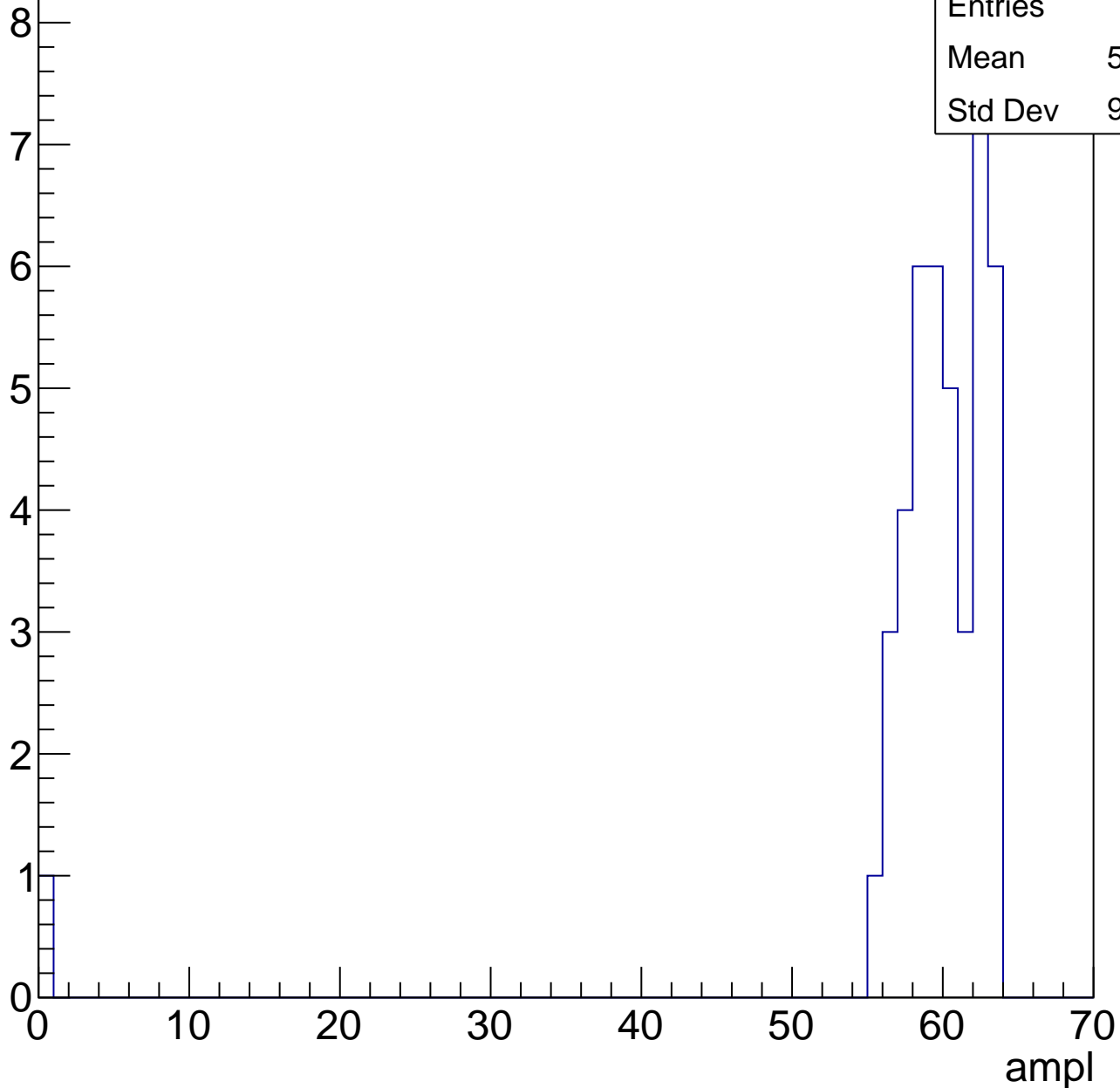


B0L000S, U2-ch106, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

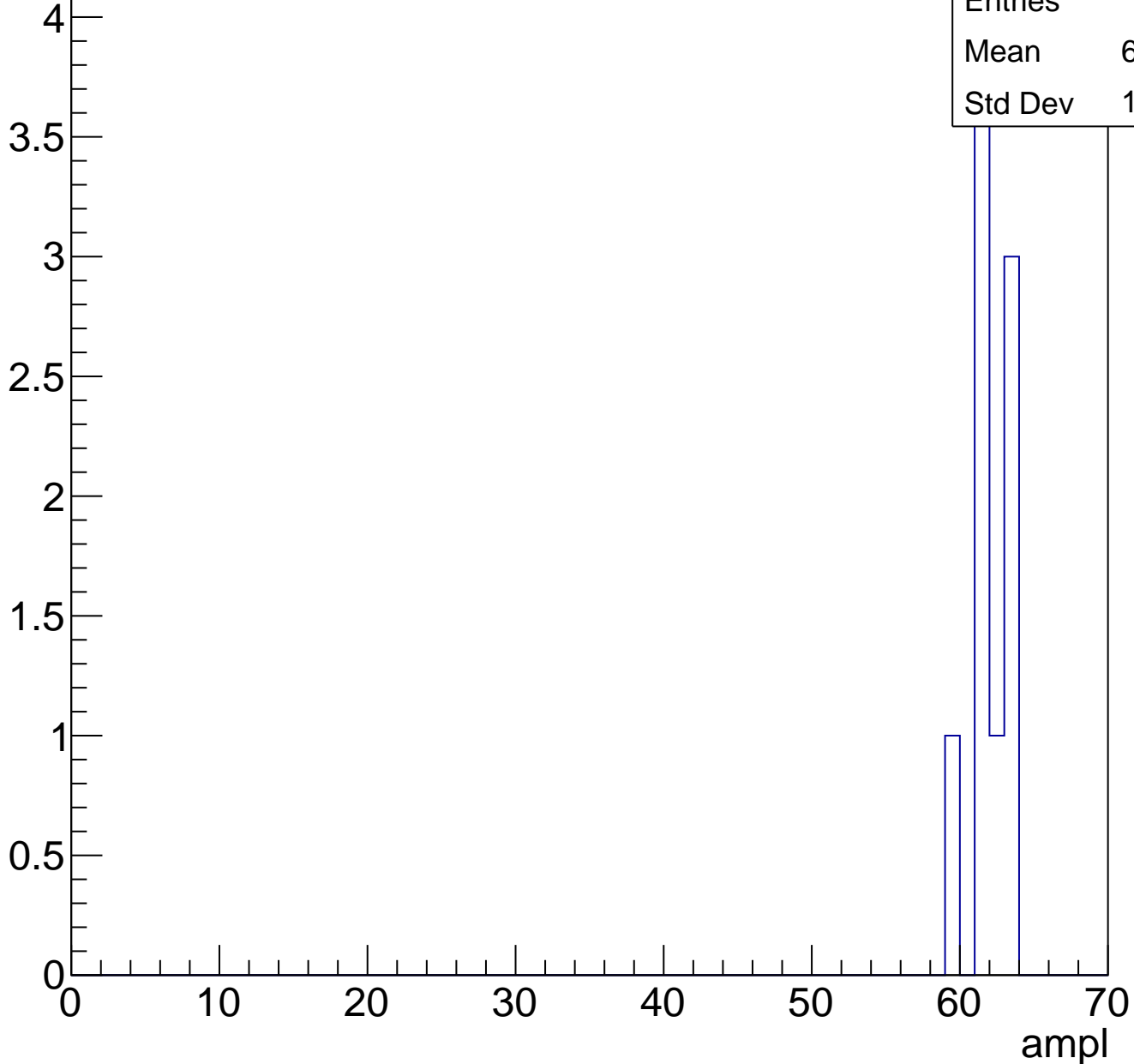
Entries	43
Mean	58.37
Std Dev	9.294



B0L000S, U2-ch106, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch106, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L000S, U2-ch107, adc0

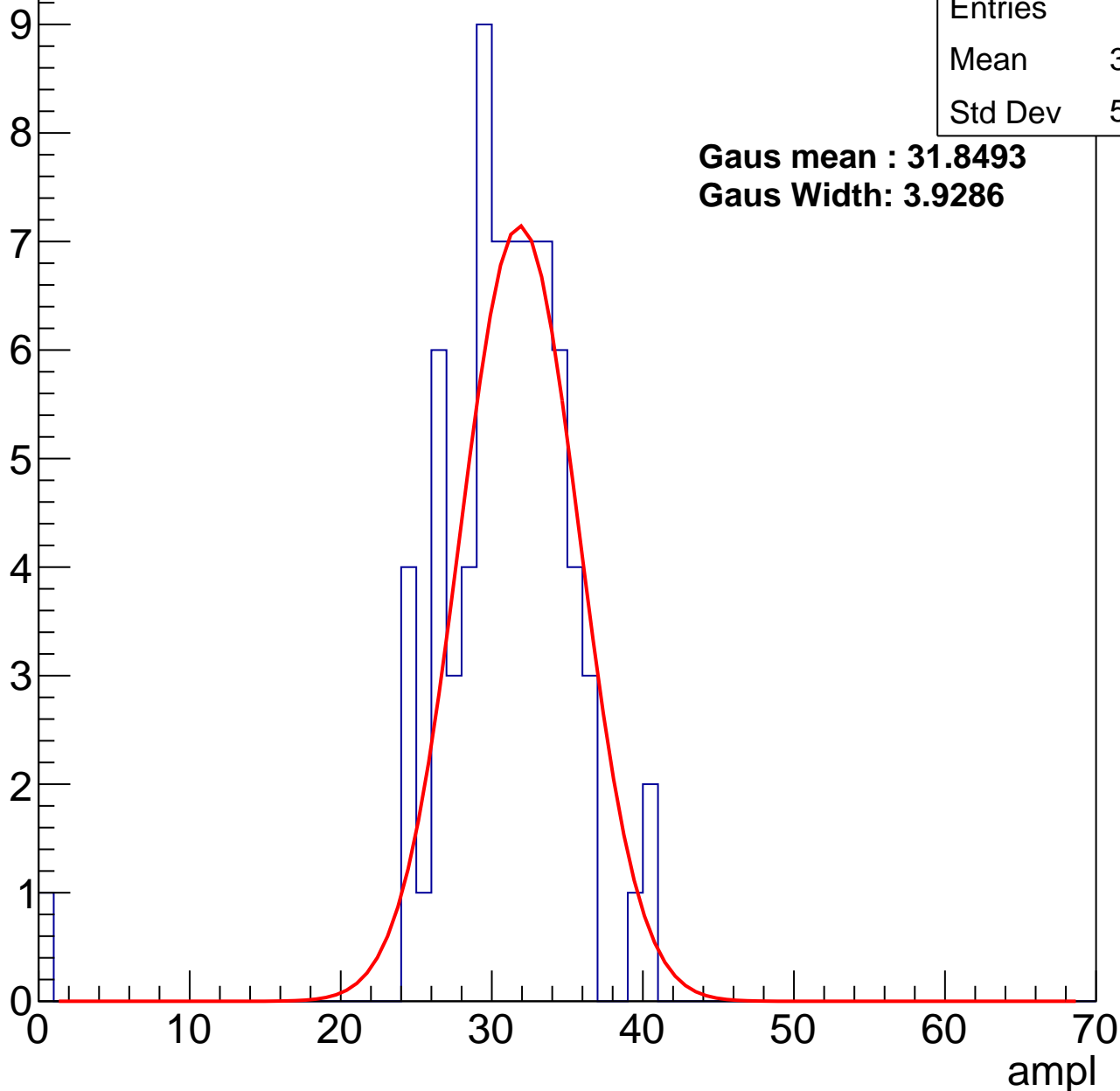
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.33
Std Dev	5.126

Gaus mean : 31.8493

Gaus Width: 3.9286



B0L000S, U2-ch107, adc1

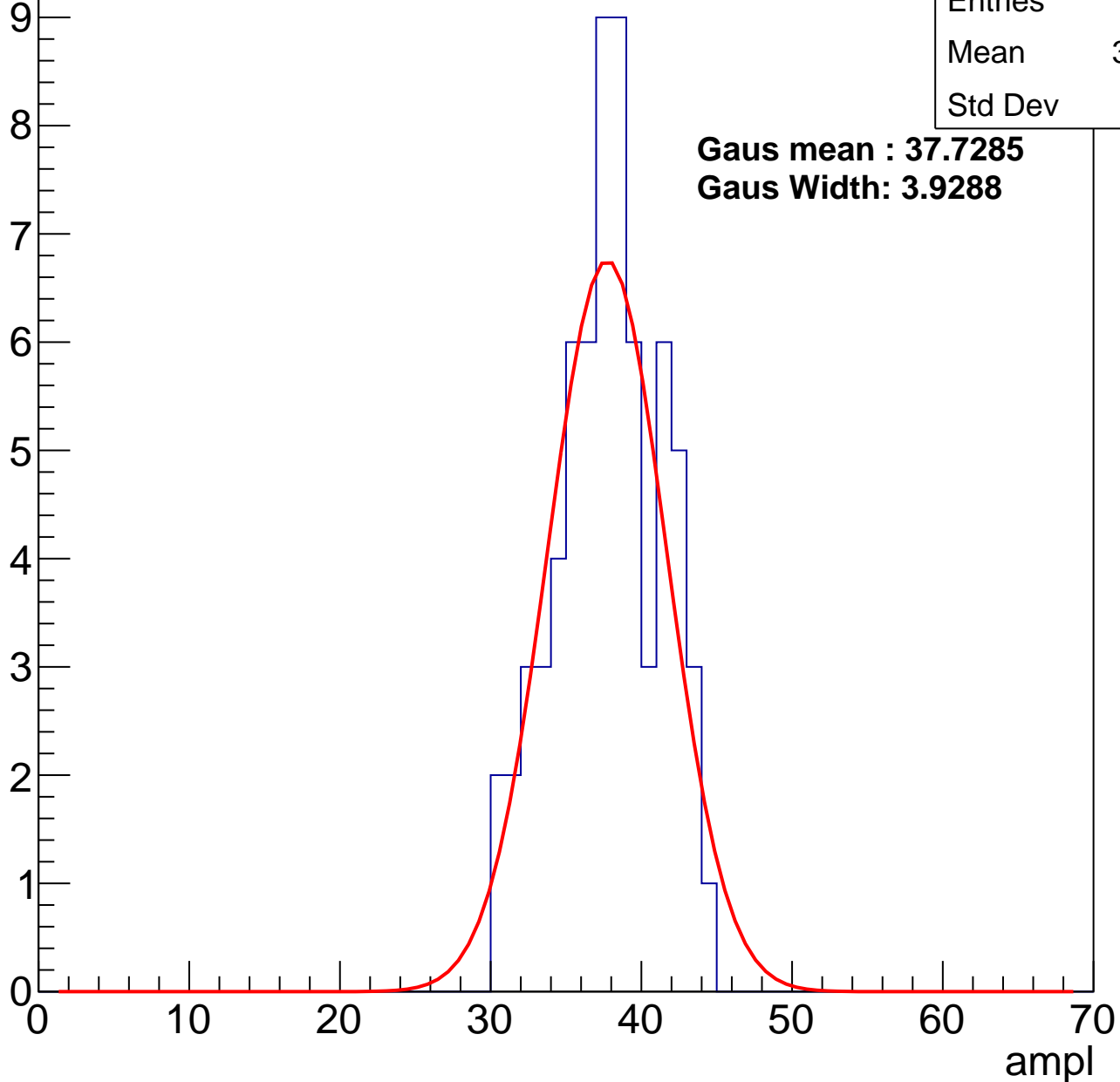
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.31
Std Dev	3.41

Gaus mean : 37.7285

Gaus Width: 3.9288



B0L000S, U2-ch107, adc2

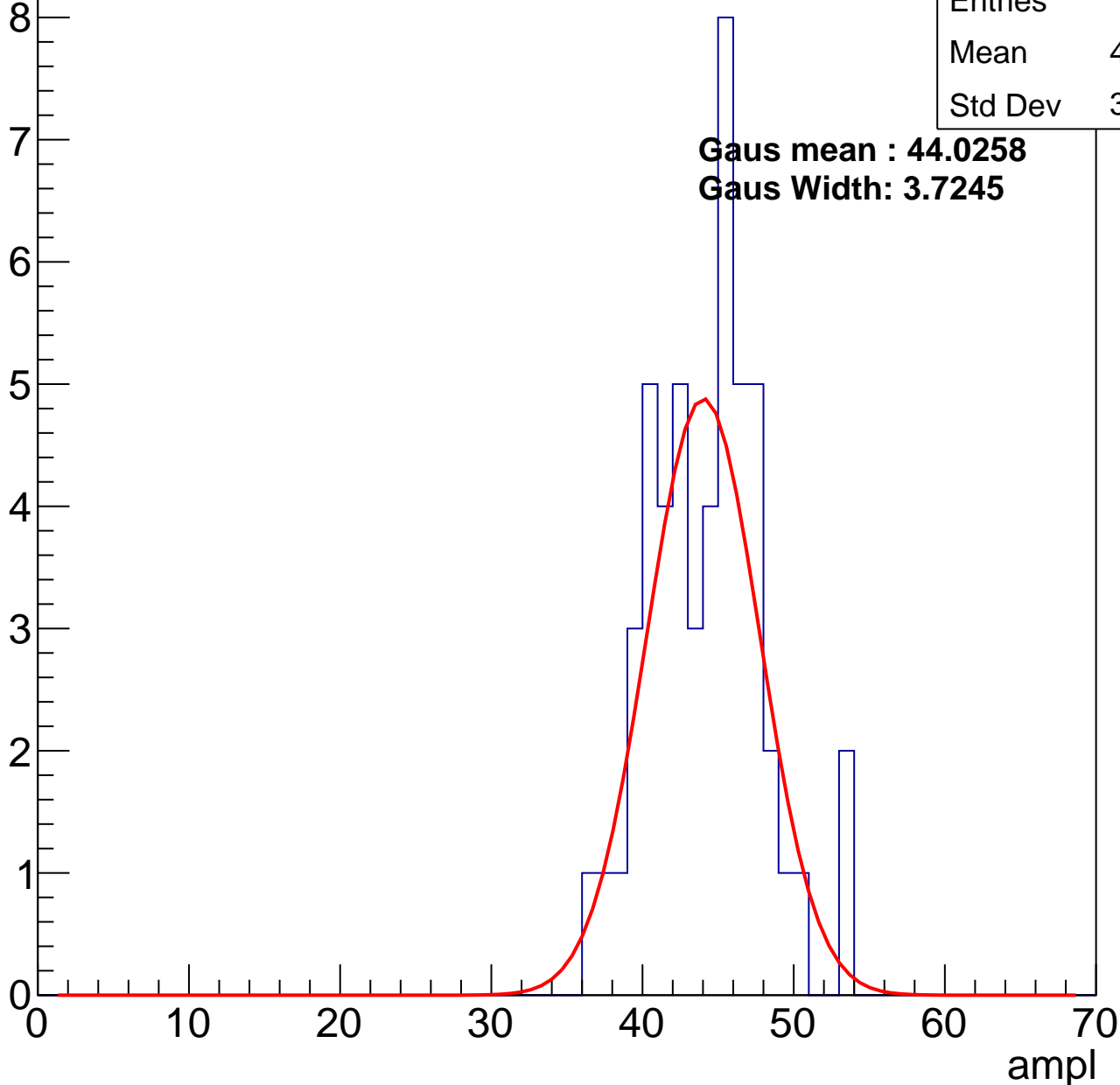
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	43.78
Std Dev	3.675

Gaus mean : 44.0258

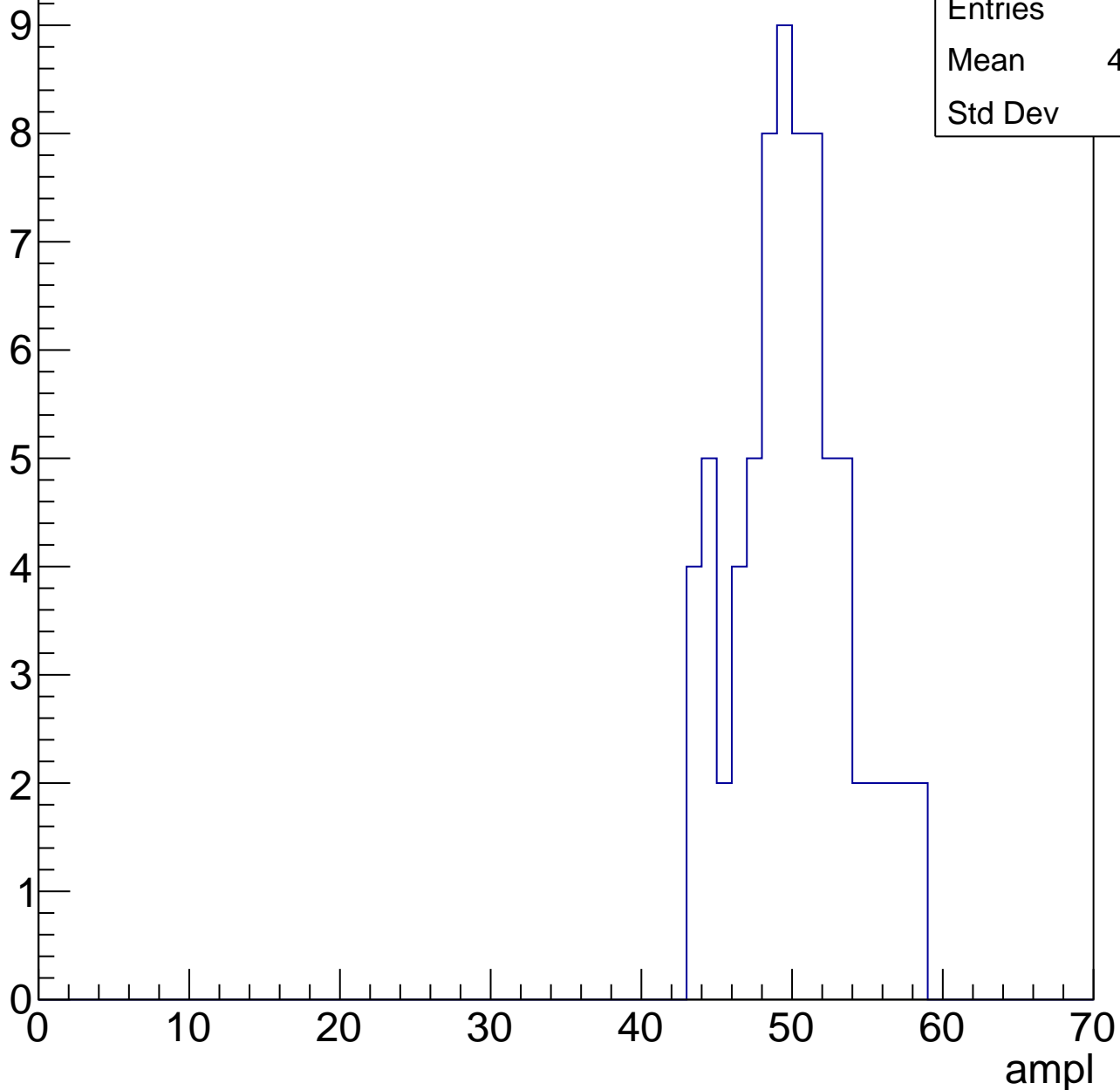
Gaus Width: 3.7245



B0L000S, U2-ch107, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry



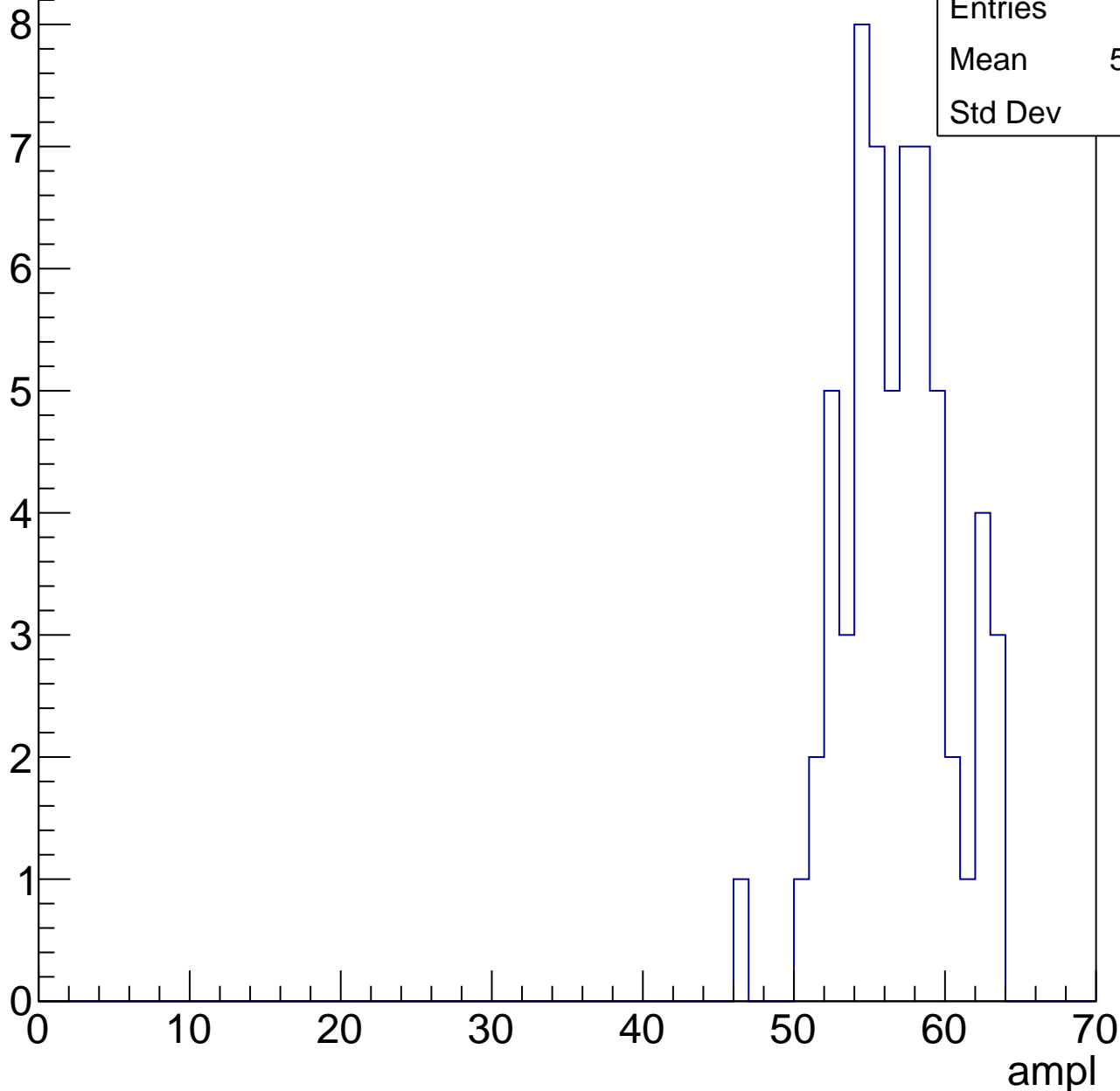
Entries	73
Mean	49.58
Std Dev	3.73

B0L000S, U2-ch107, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

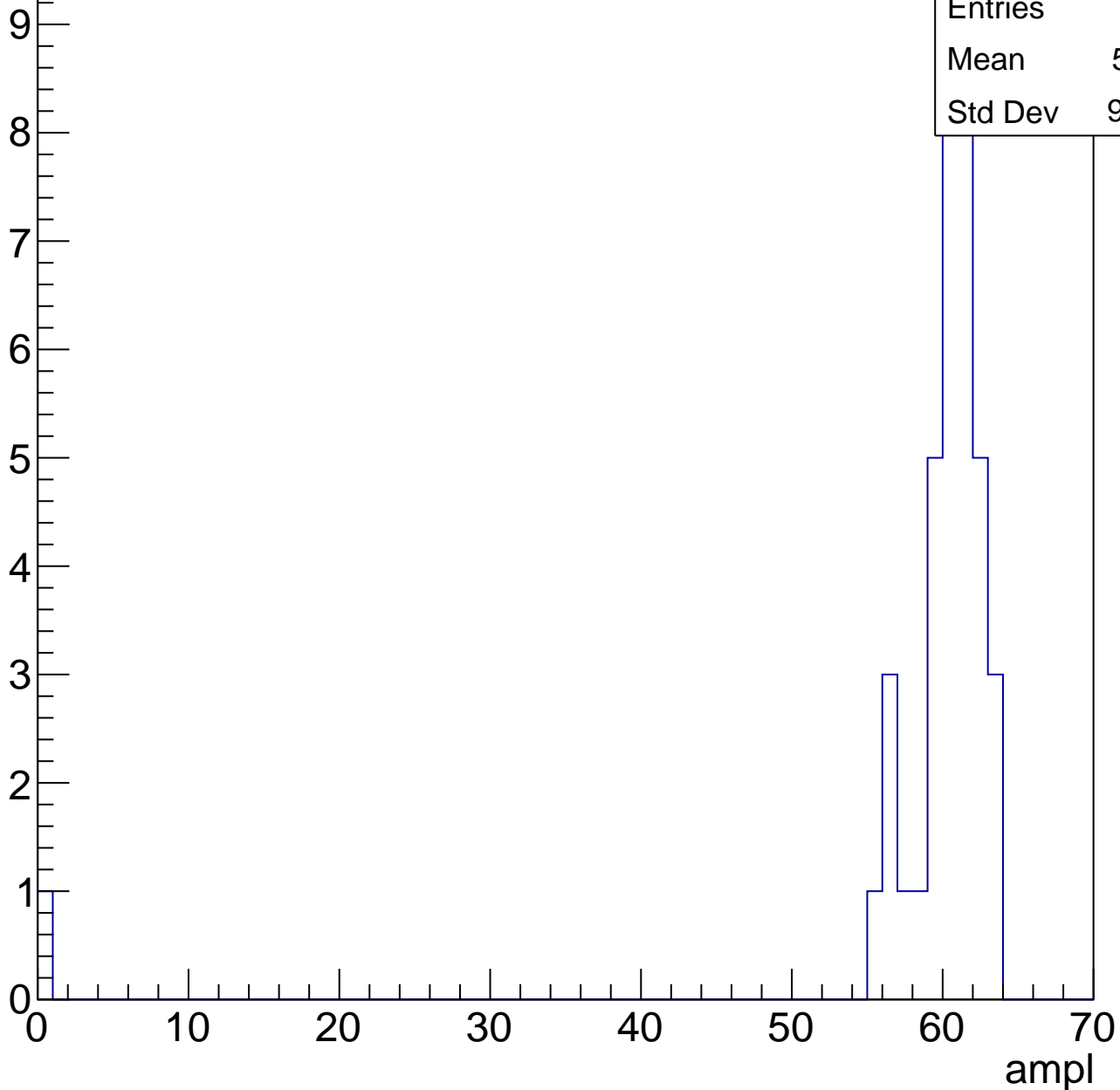
Entries	61
Mean	56.26
Std Dev	3.52



B0L000S, U2-ch107, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch107, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch107, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L000S, U2-ch108, adc0

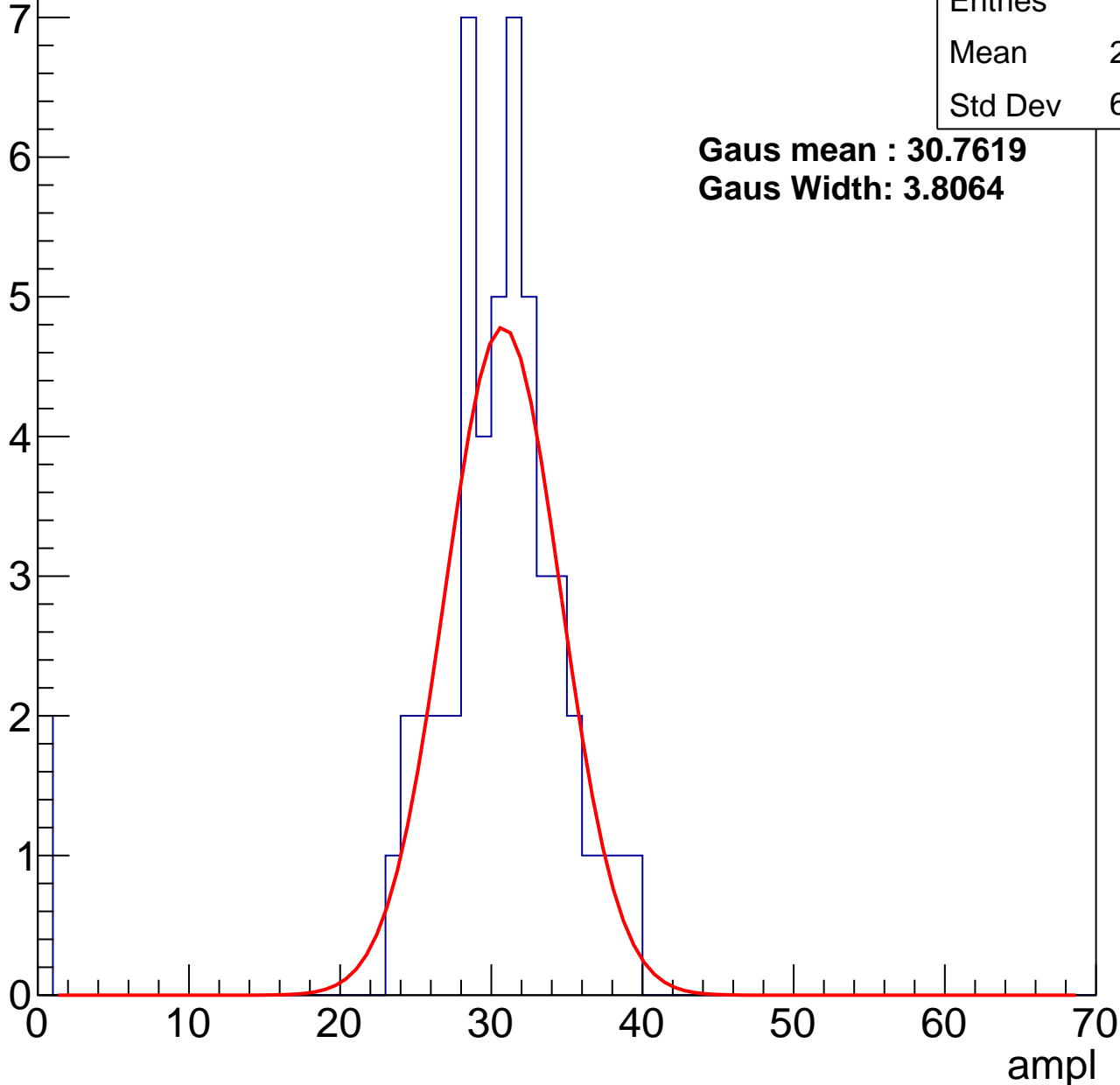
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	51
Mean	29.16
Std Dev	6.858

Gaus mean : 30.7619

Gaus Width: 3.8064



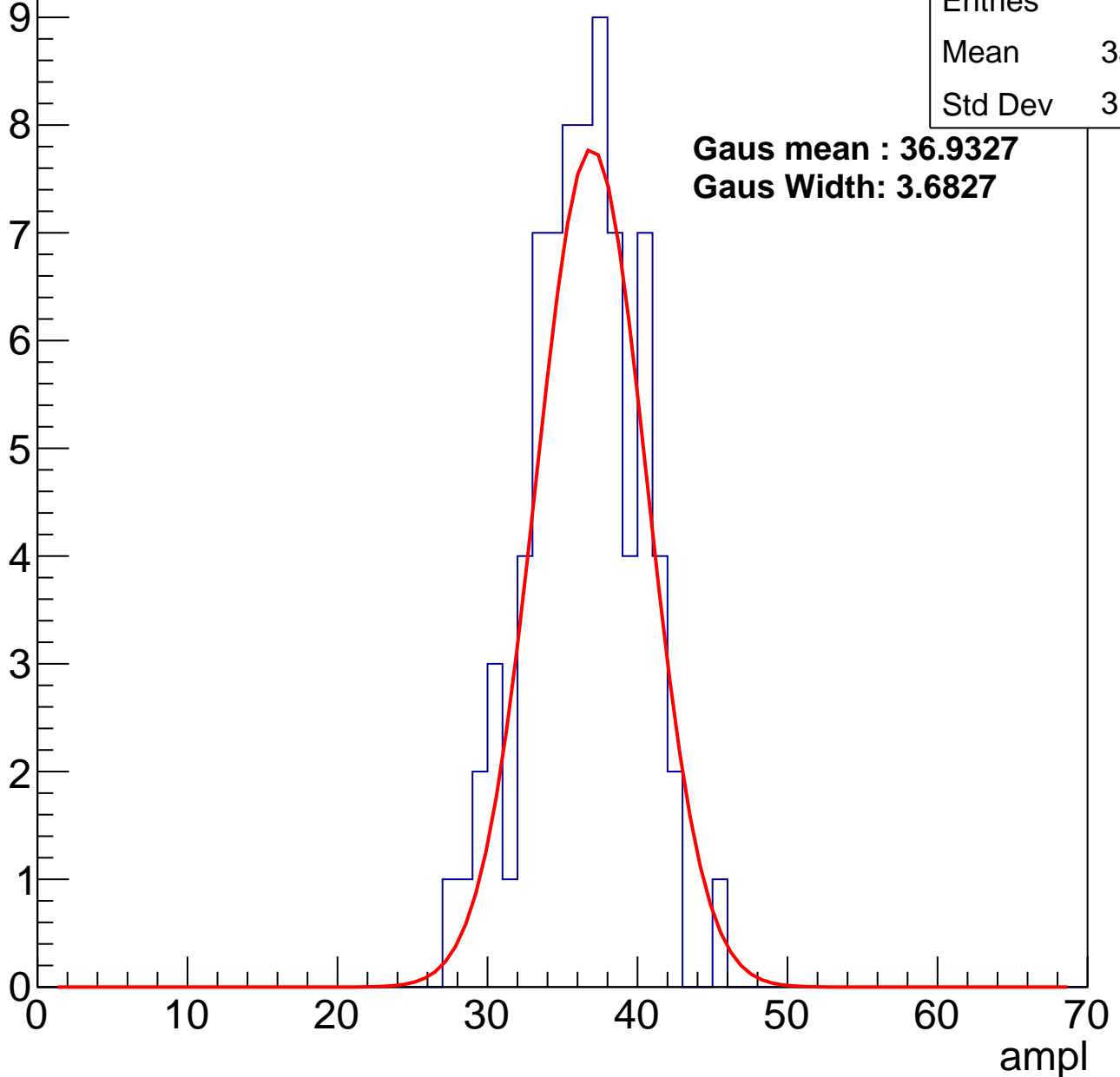
B0L000S, U2-ch108, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	76
Mean	35.88
Std Dev	3.583

Gaus mean : 36.9327
Gaus Width: 3.6827



B0L000S, U2-ch108, adc2

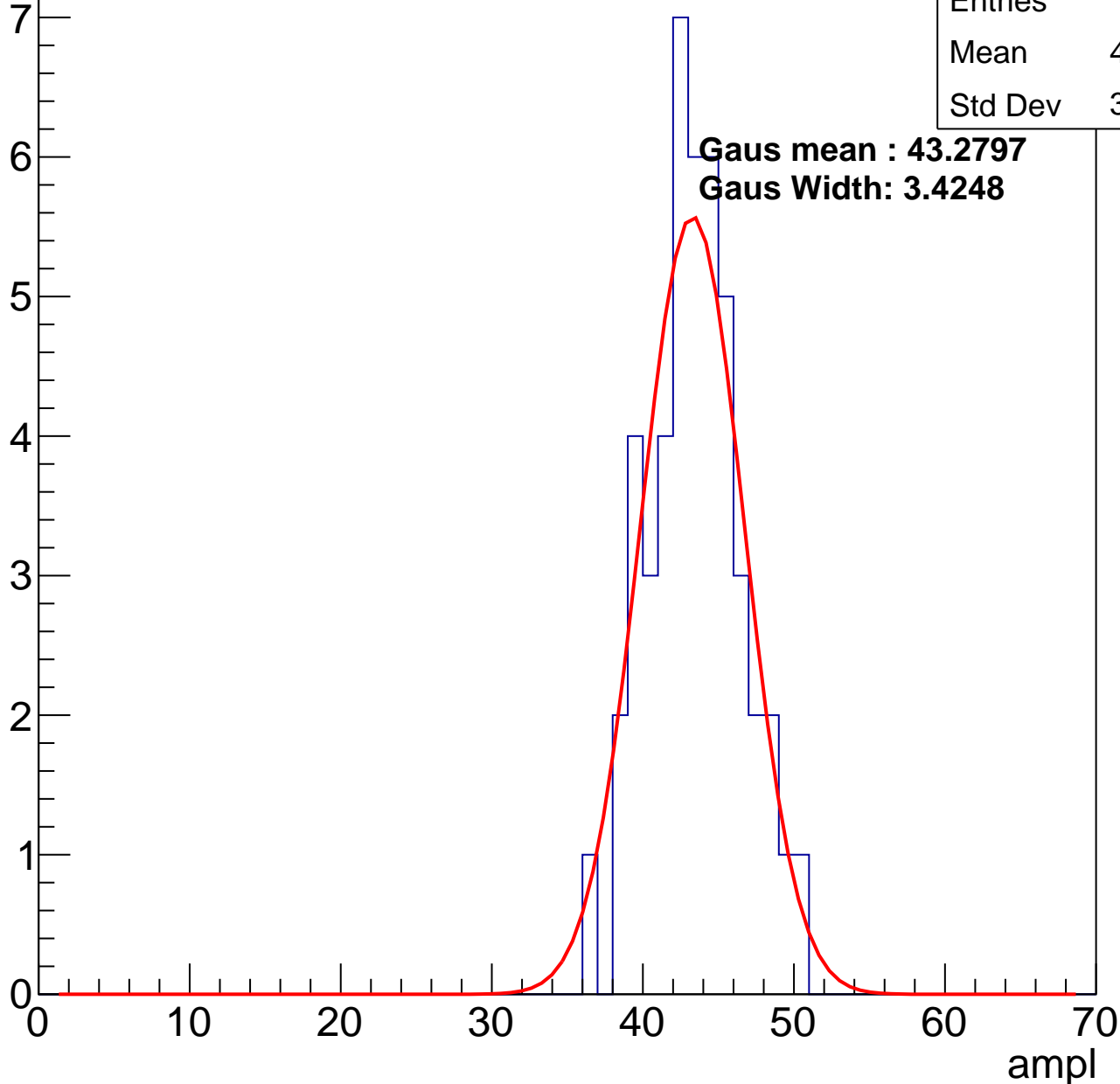
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	42.98
Std Dev	3.035

Gaus mean : 43.2797

Gaus Width: 3.4248

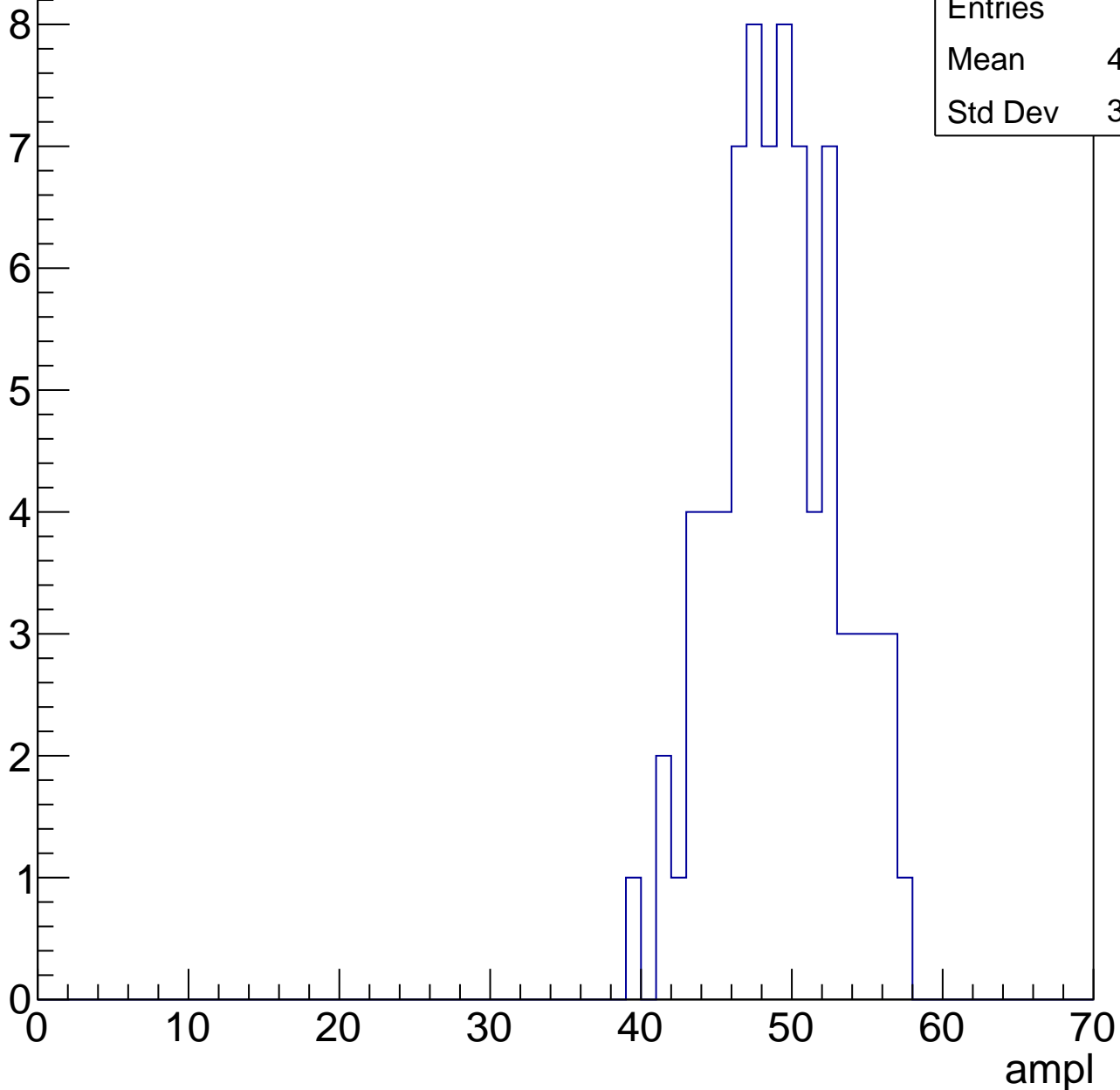


B0L000S, U2-ch108, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

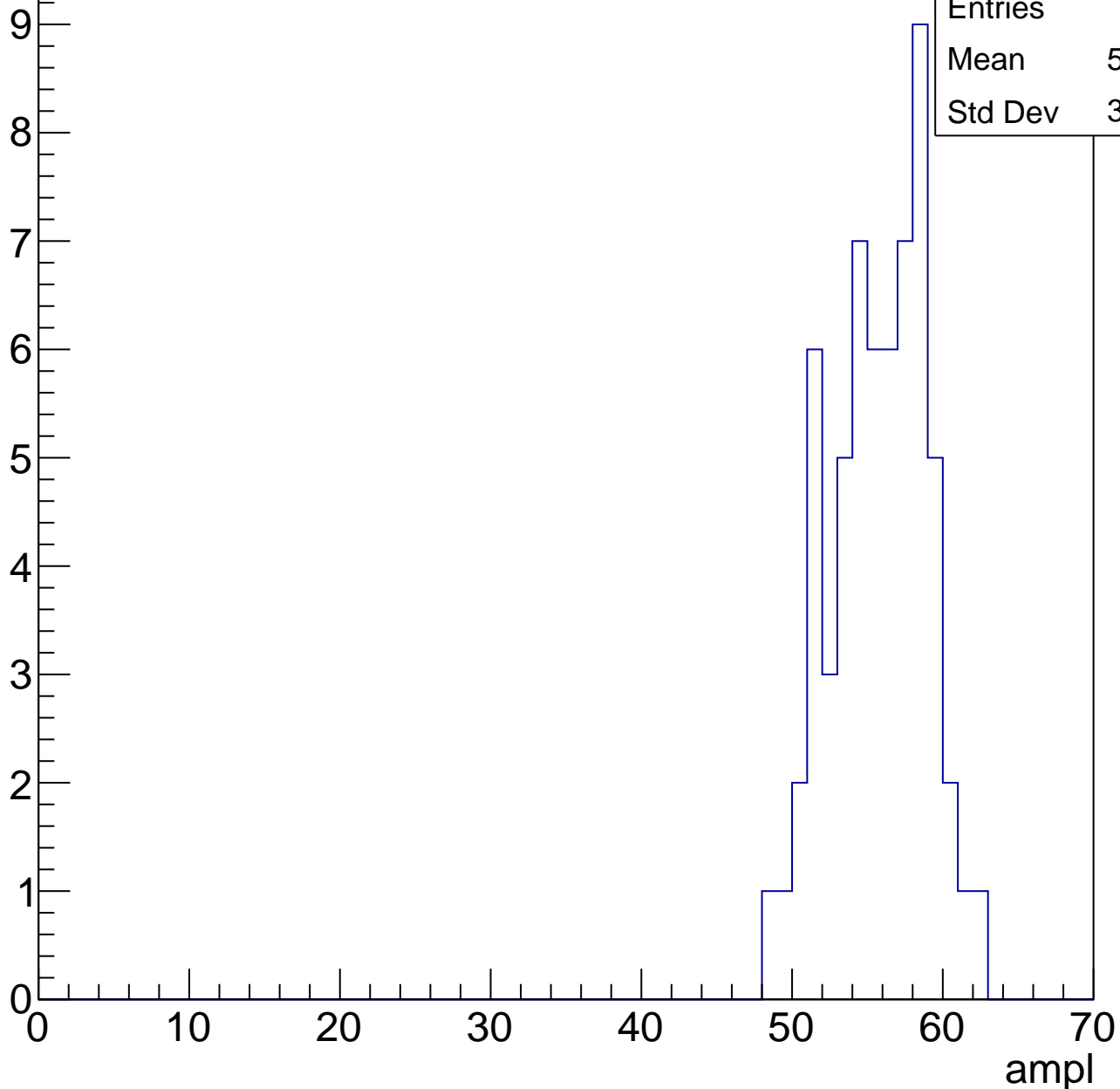
Entries	77
Mean	48.65
Std Dev	3.957



B0L000S, U2-ch108, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch108, adc5

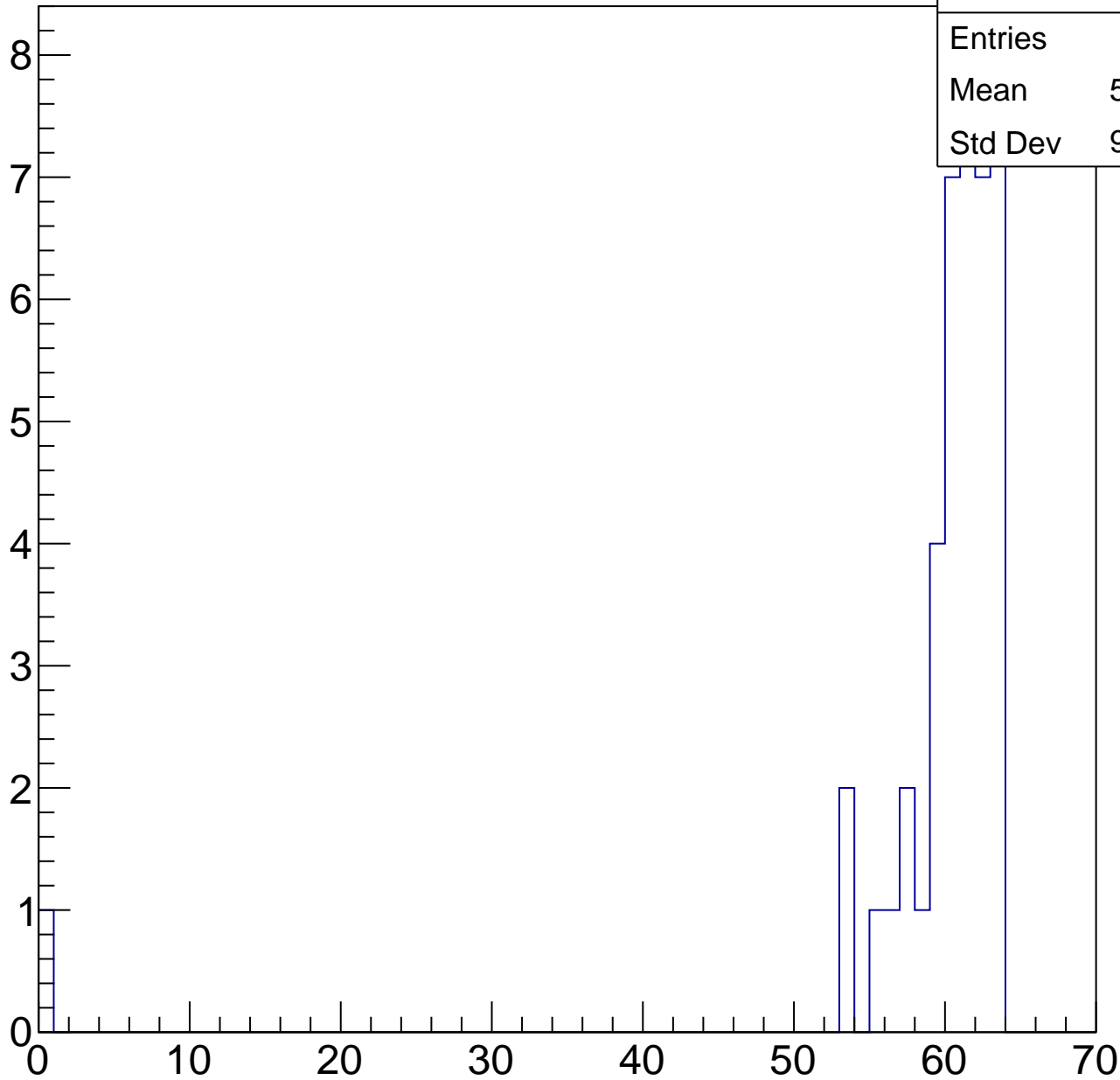
calib_packv5_042523_0143.root, FC#5, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.83
Std Dev	9.532

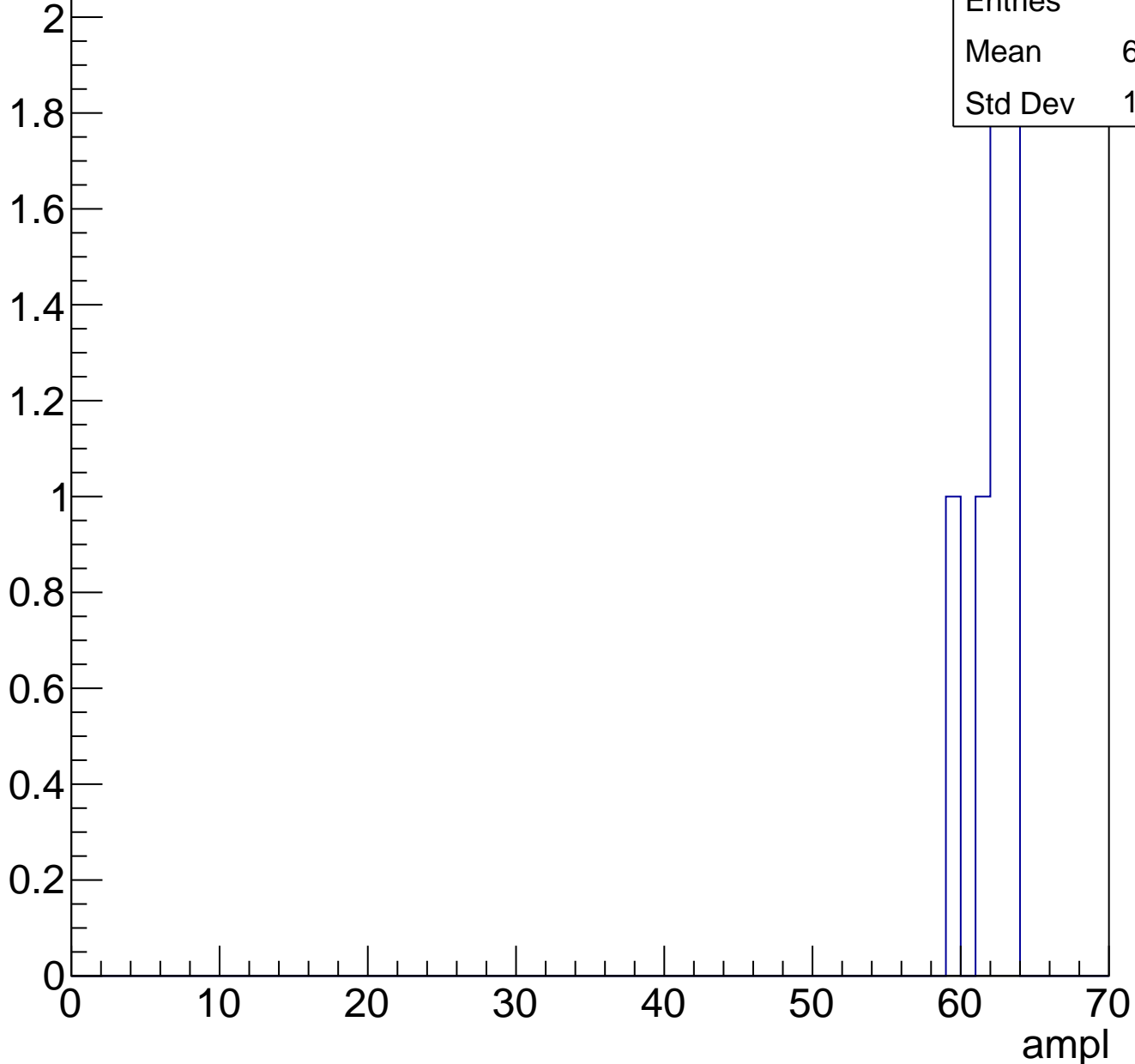
ampl



B0L000S, U2-ch108, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch108, adc7

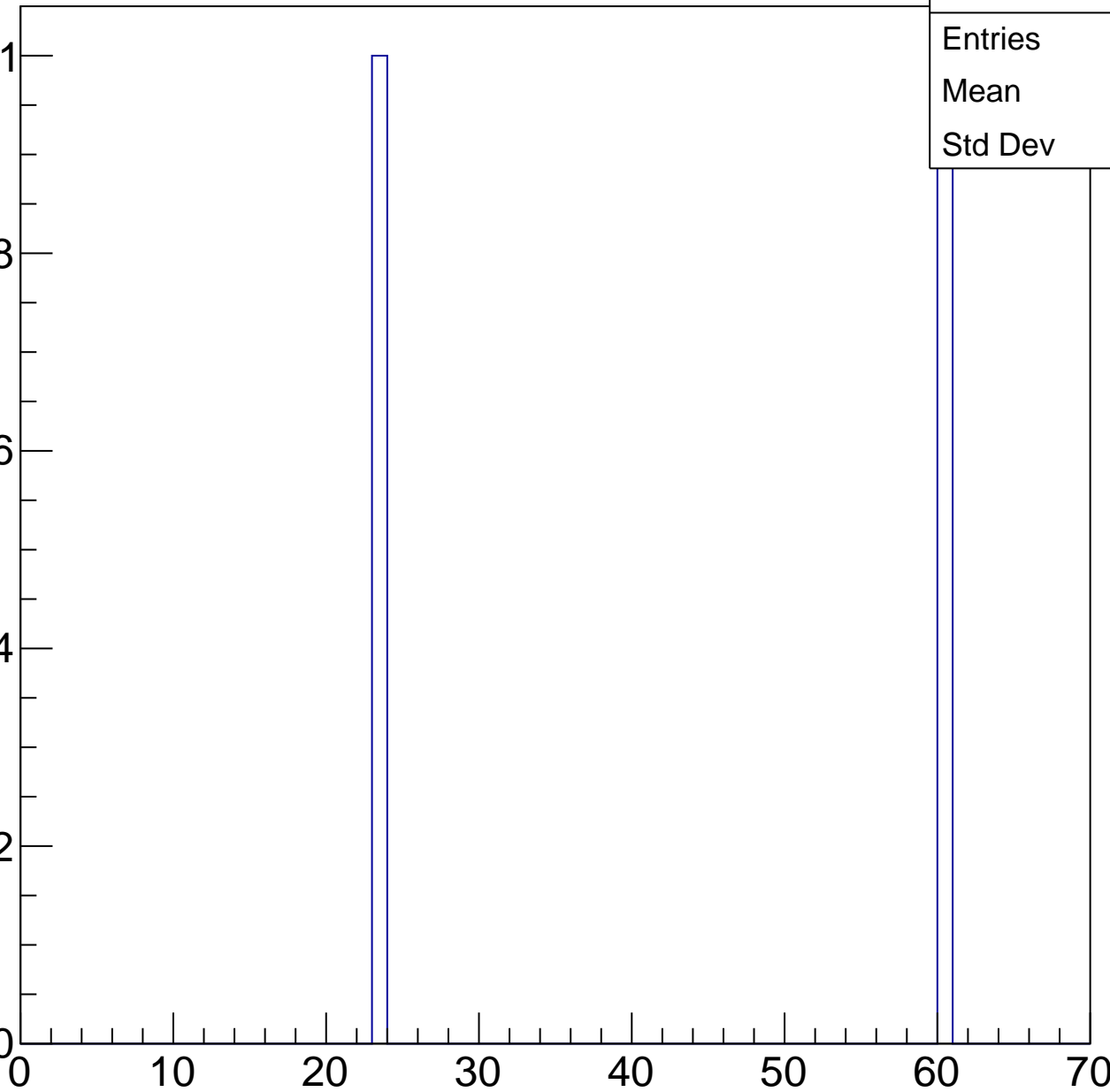
calib_packv5_042523_0143.root, FC#5, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	41.5
Std Dev	18.5

ampl



B0L000S, U2-ch109, adc0

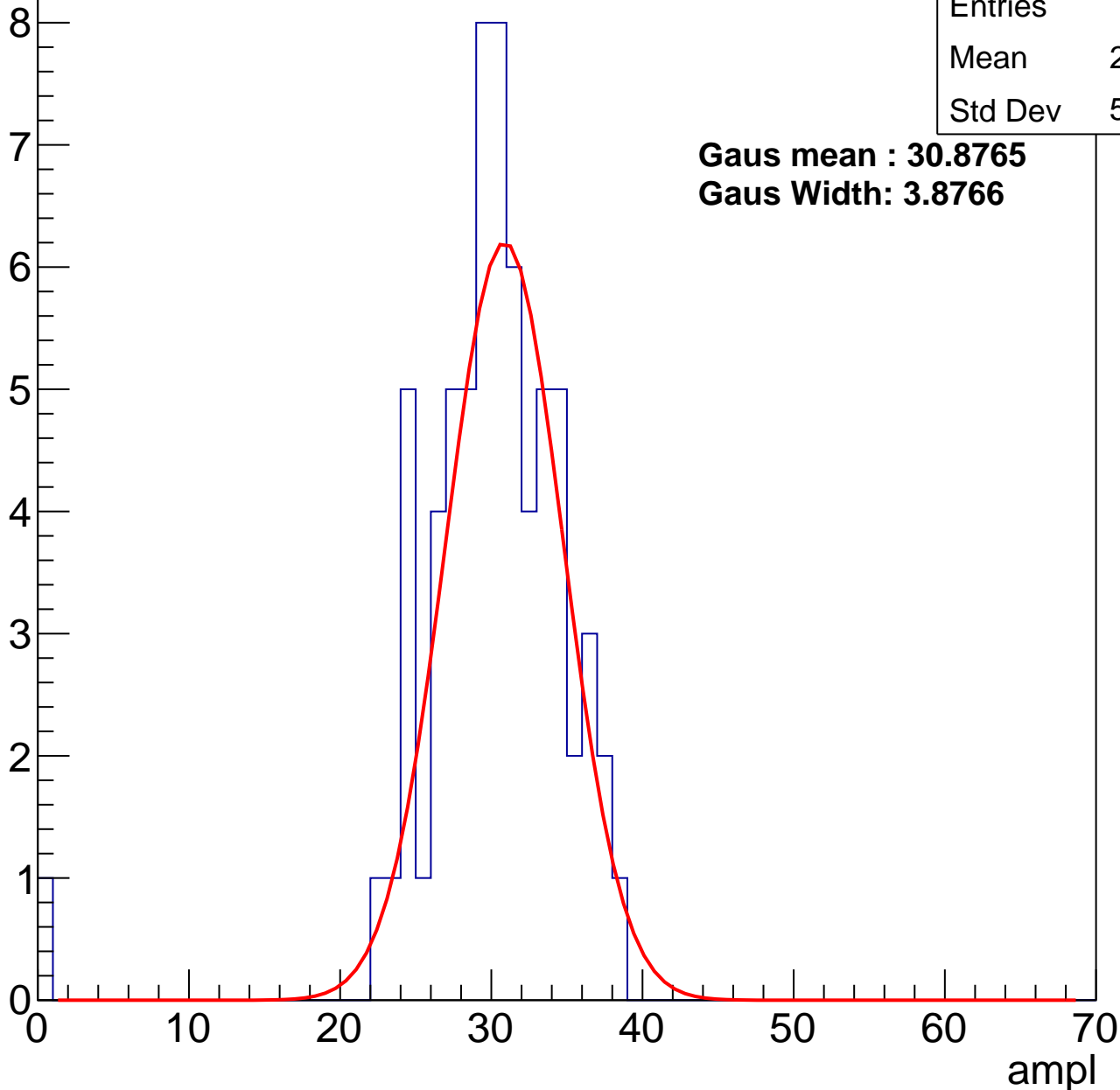
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.55
Std Dev	5.196

Gaus mean : 30.8765

Gaus Width: 3.8766



B0L000S, U2-ch109, adc1

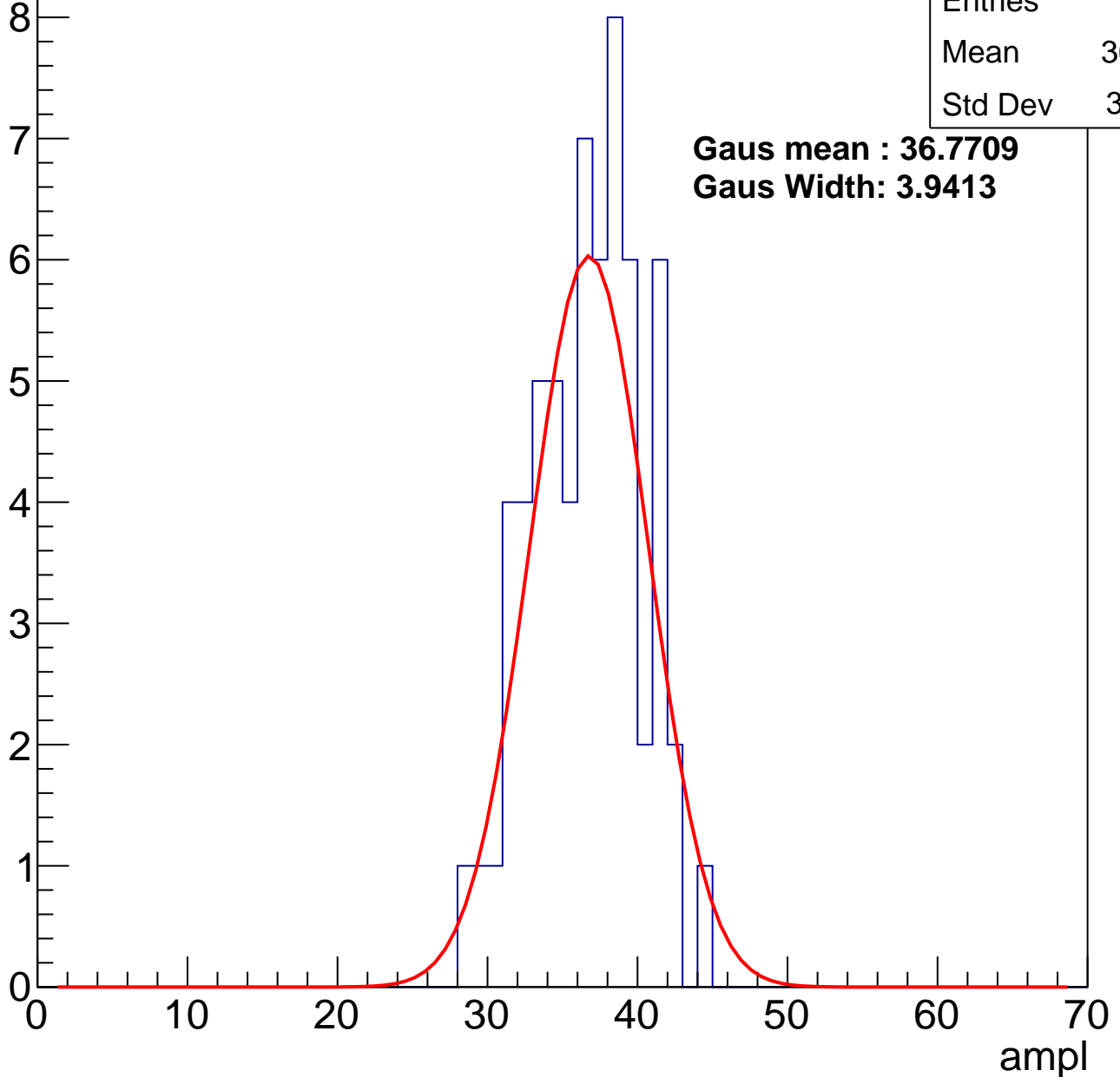
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.19
Std Dev	3.541

Gaus mean : 36.7709

Gaus Width: 3.9413



B0L000S, U2-ch109, adc2

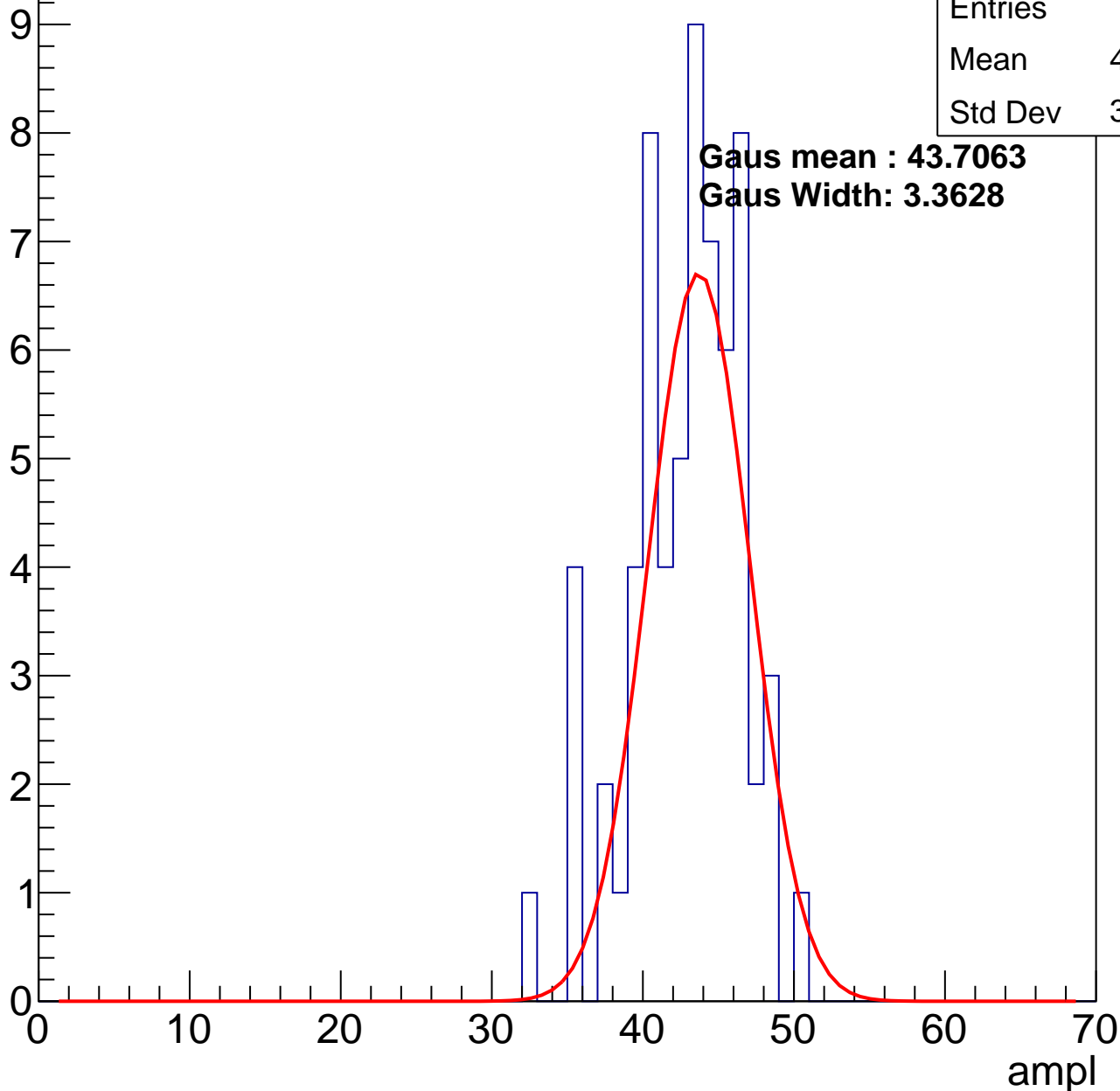
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.38
Std Dev	3.649

Gaus mean : 43.7063

Gaus Width: 3.3628

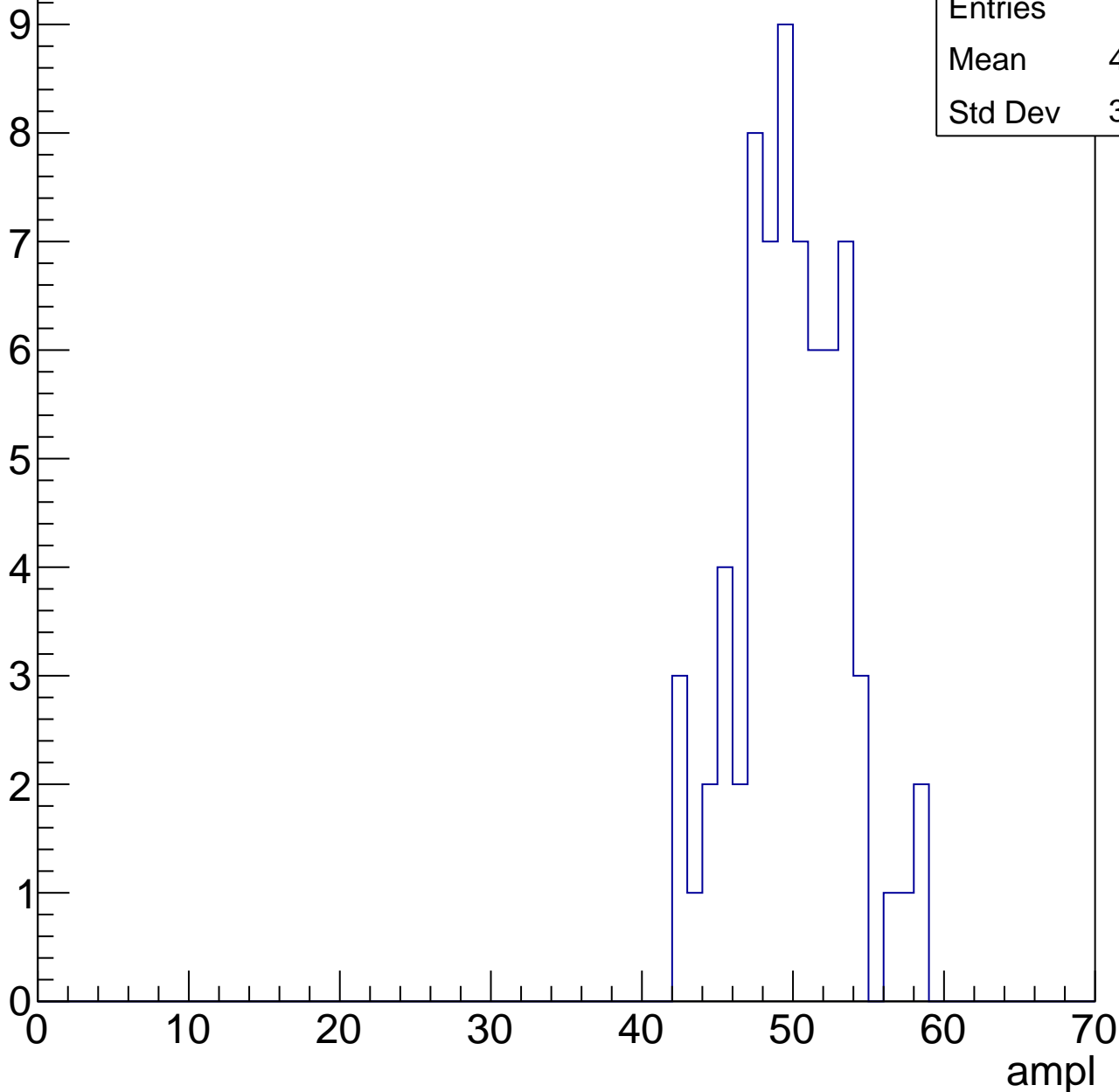


B0L000S, U2-ch109, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

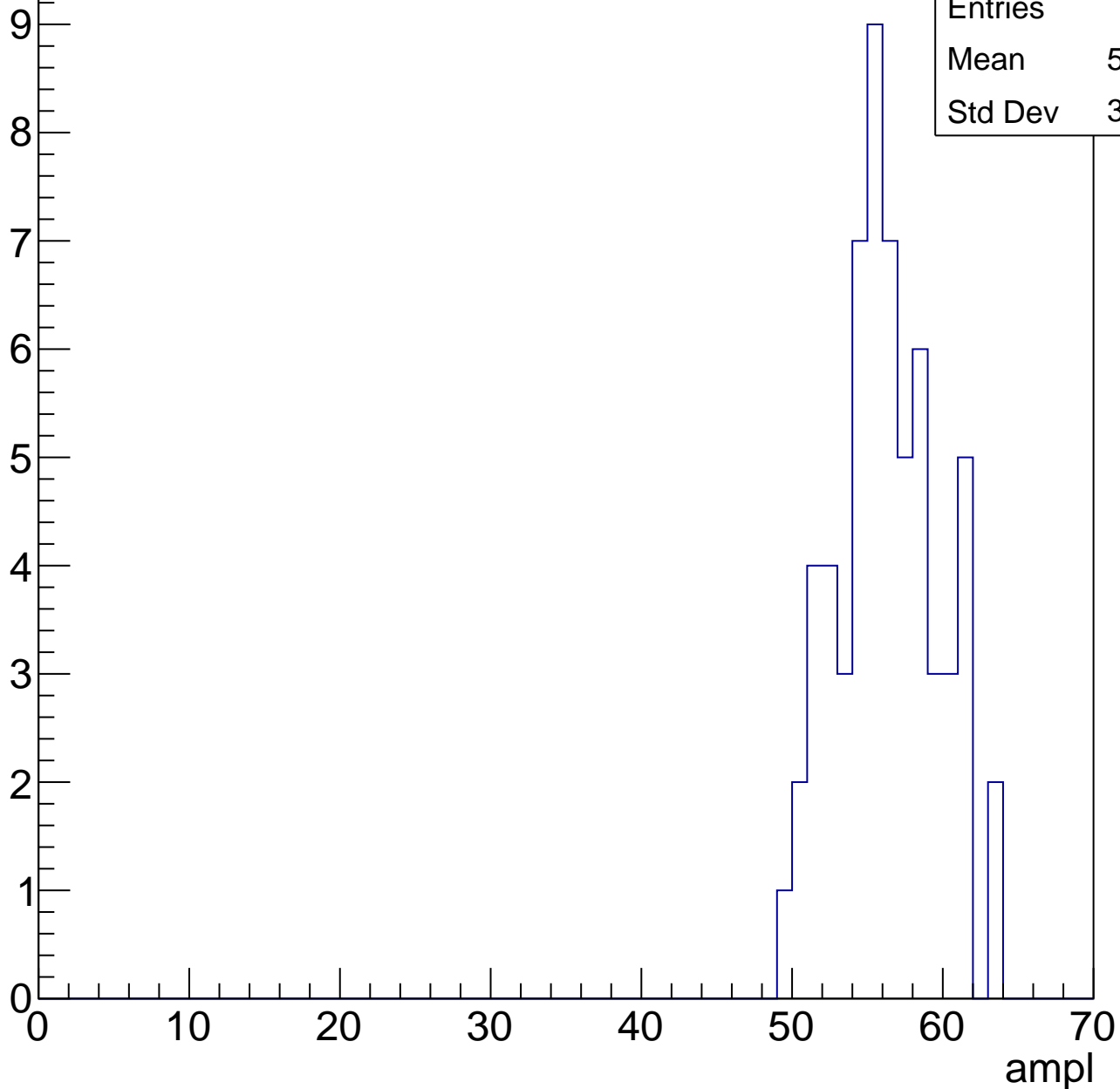
Entries	69
Mean	49.45
Std Dev	3.586



B0L000S, U2-ch109, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	61
Mean	55.84
Std Dev	3.325

B0L000S, U2-ch109, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries

38

Mean

59.89

Std Dev

2.15

ampl

0

10

20

30

40

50

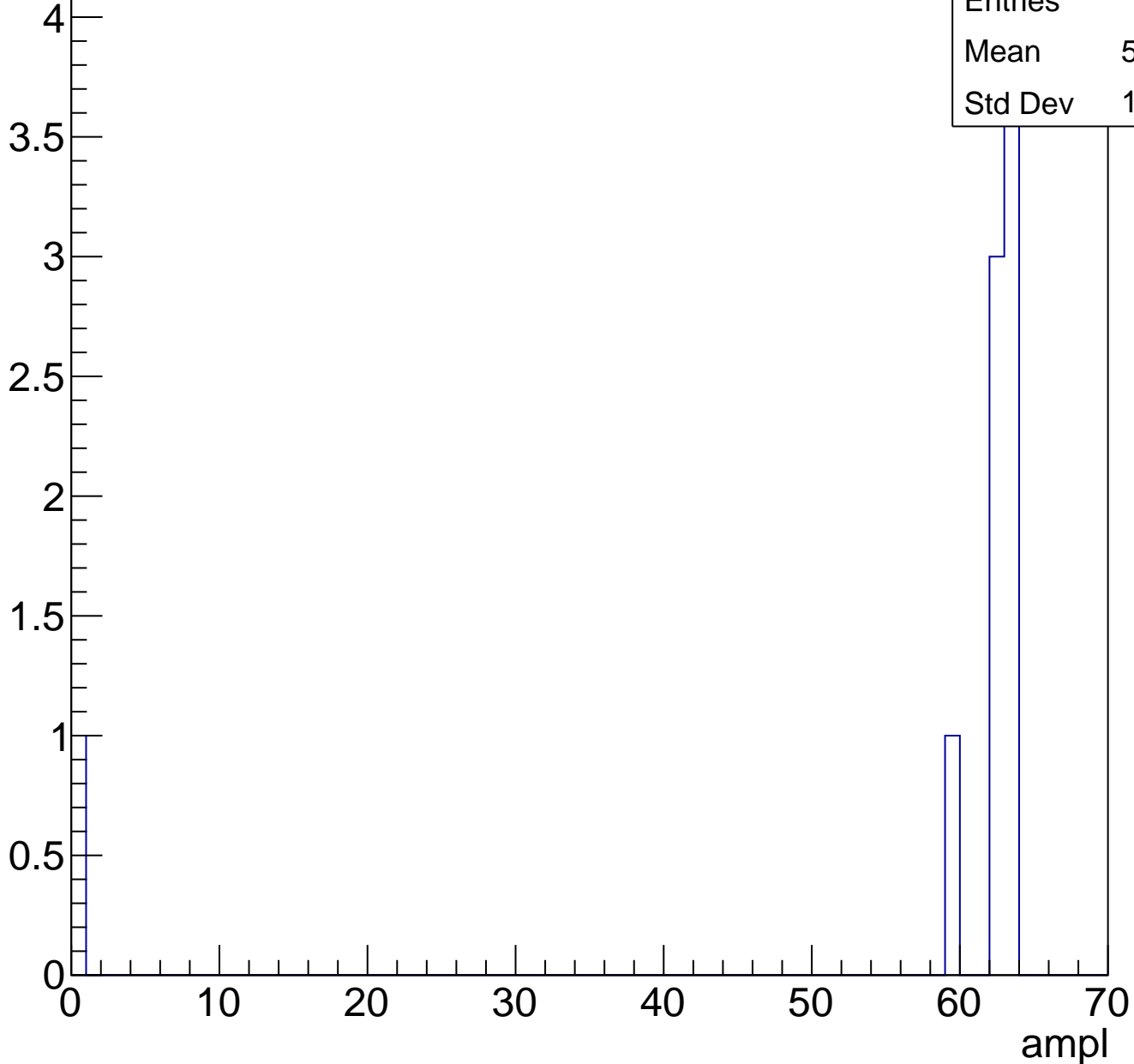
60

70

B0L000S, U2-ch109, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch109, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch110, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	89
Mean	28.31
Std Dev	4.651

Gaus mean : 29.1494

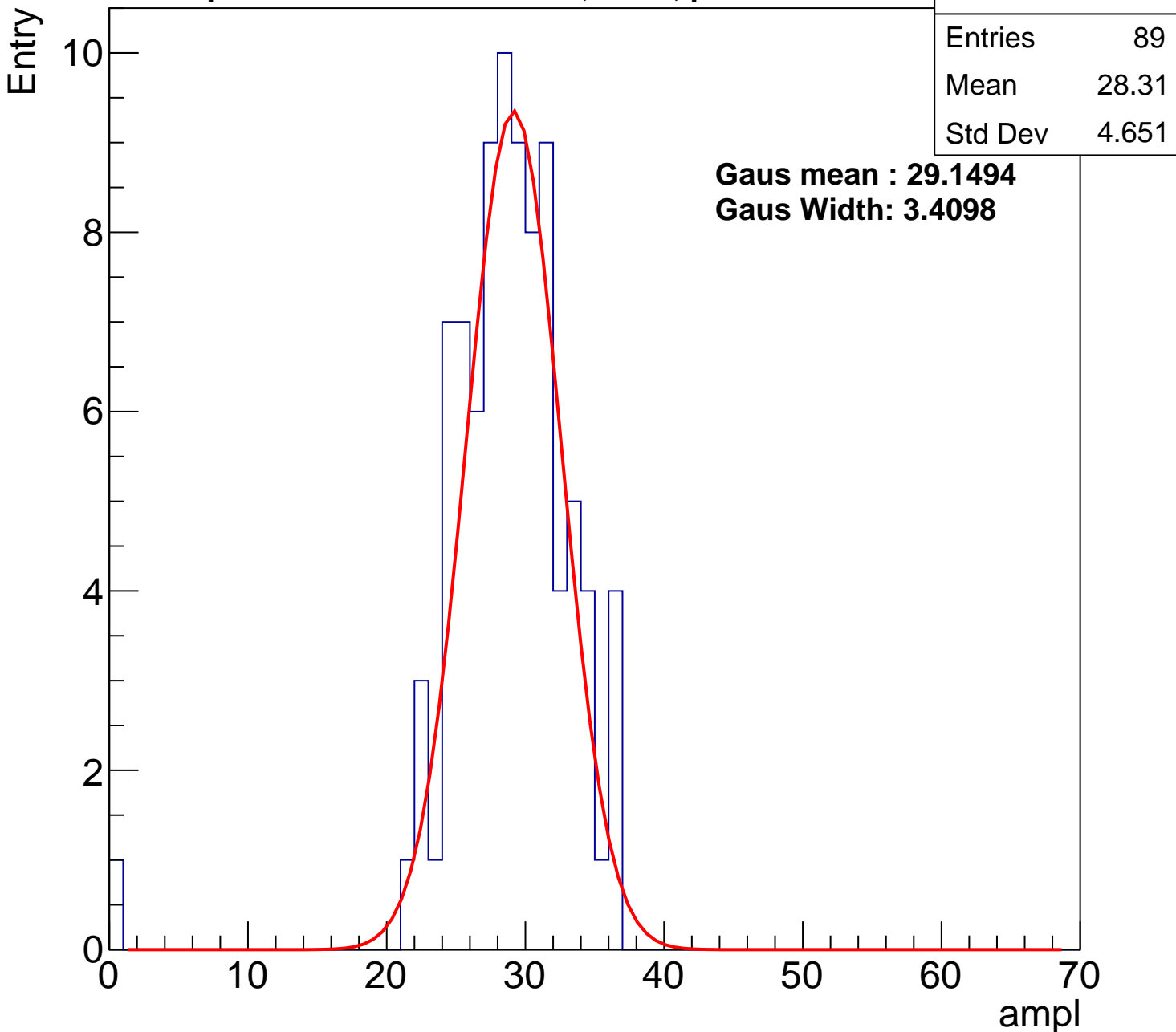
Gaus Width: 3.4098

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch110, adc1

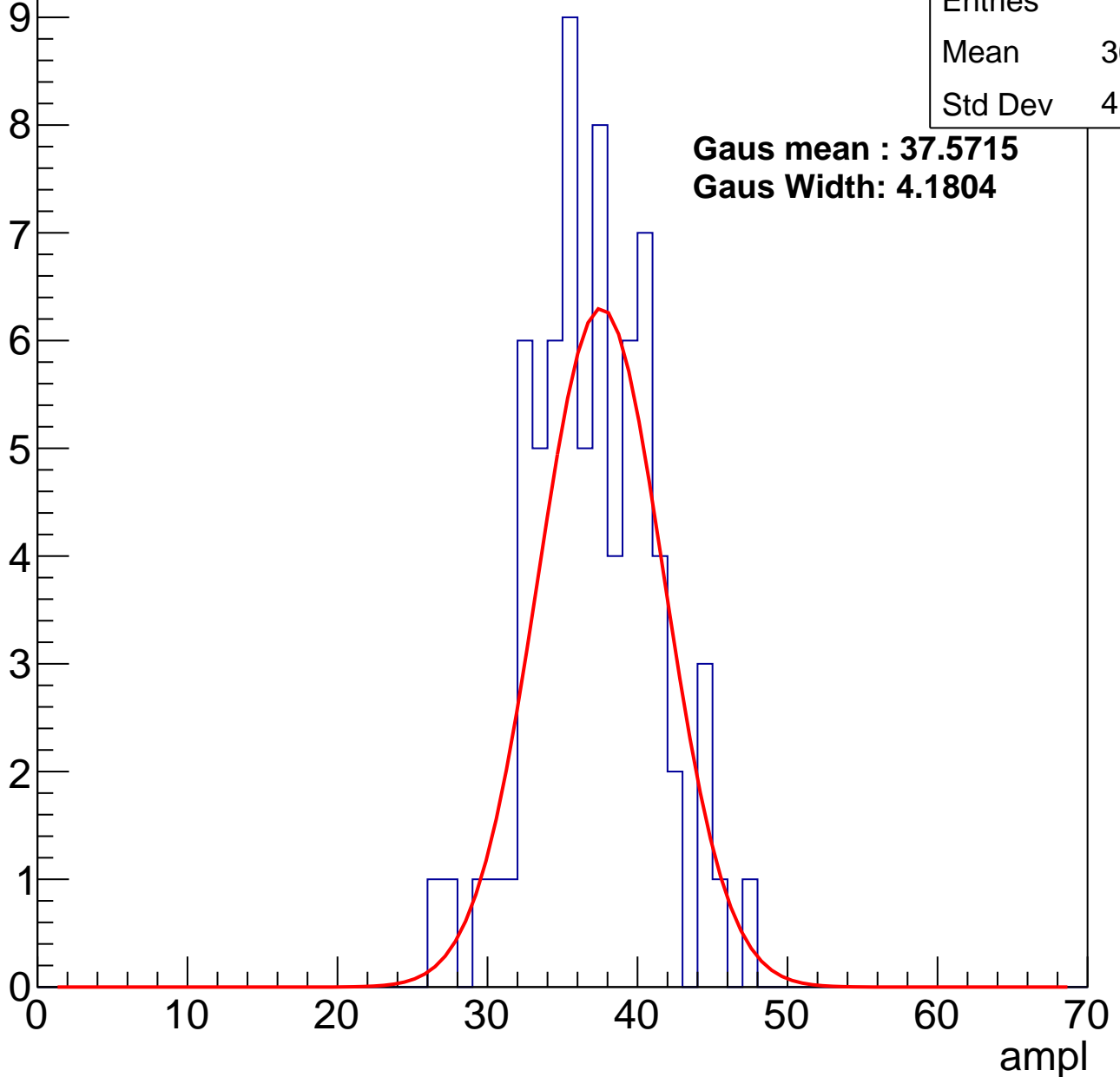
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.57
Std Dev	4.078

Gaus mean : 37.5715

Gaus Width: 4.1804



B0L000S, U2-ch110, adc2

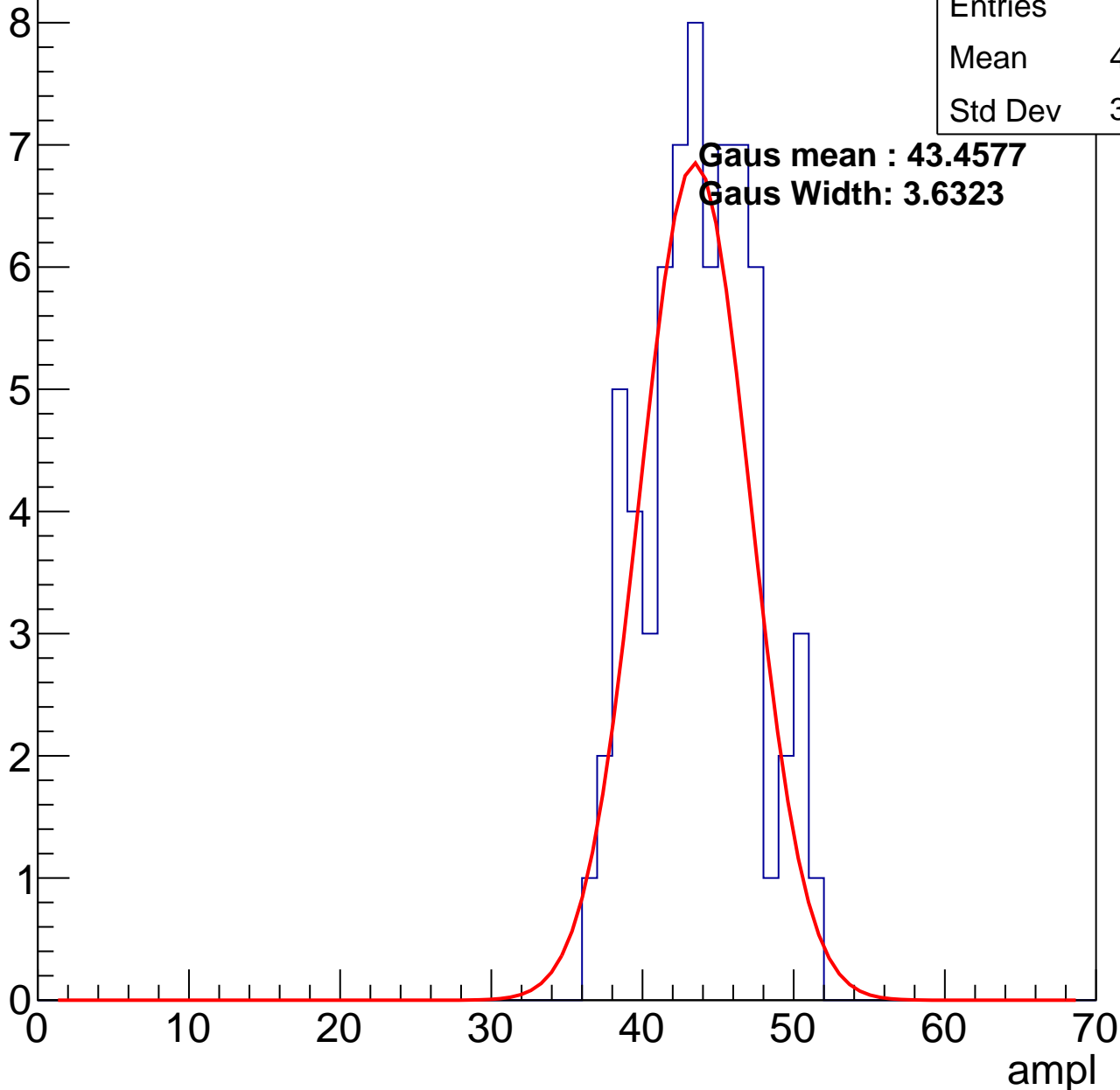
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.33
Std Dev	3.533

Gaus mean : 43.4577

Gaus Width: 3.6323

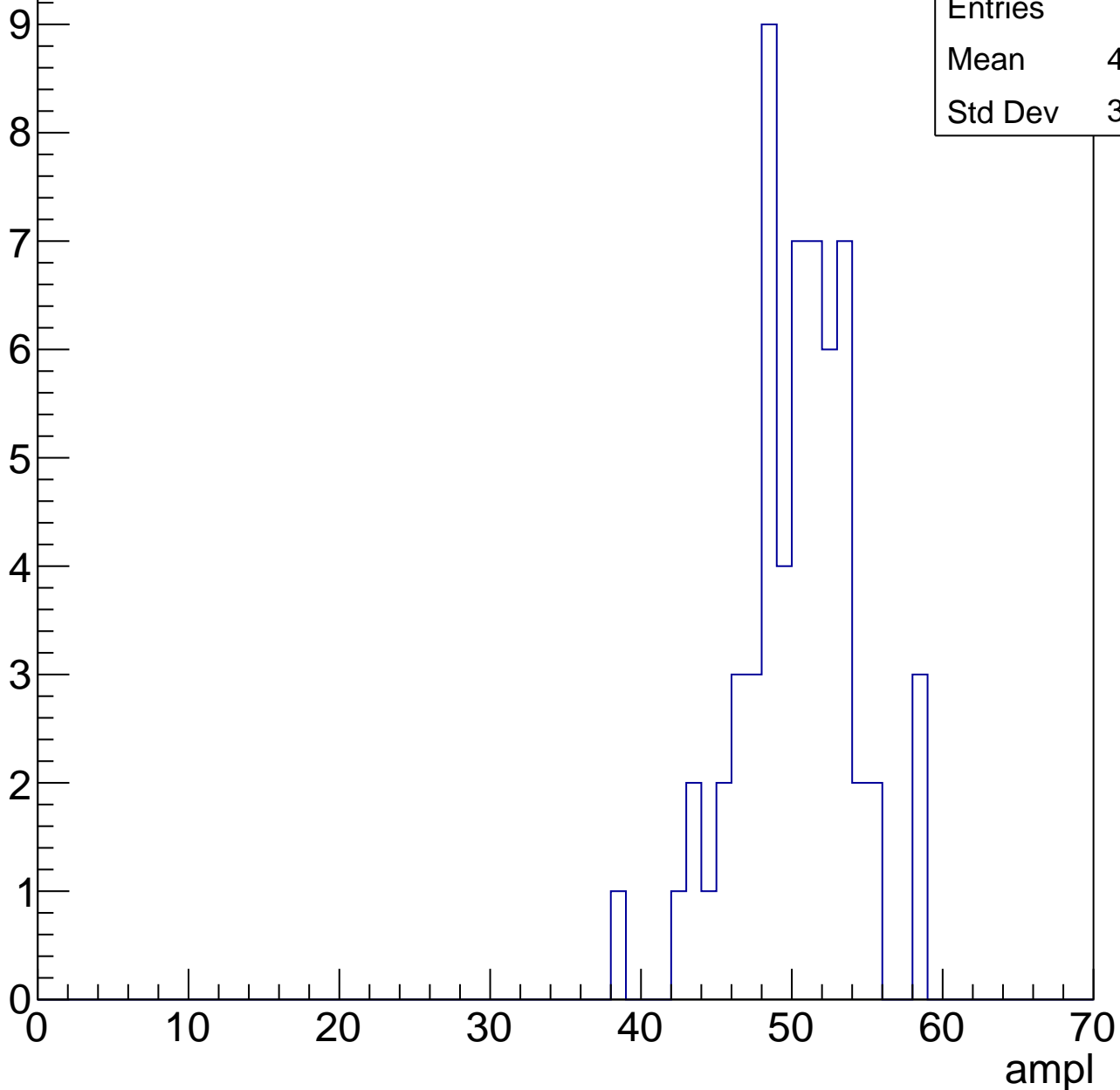


B0L000S, U2-ch110, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	49.82
Std Dev	3.814

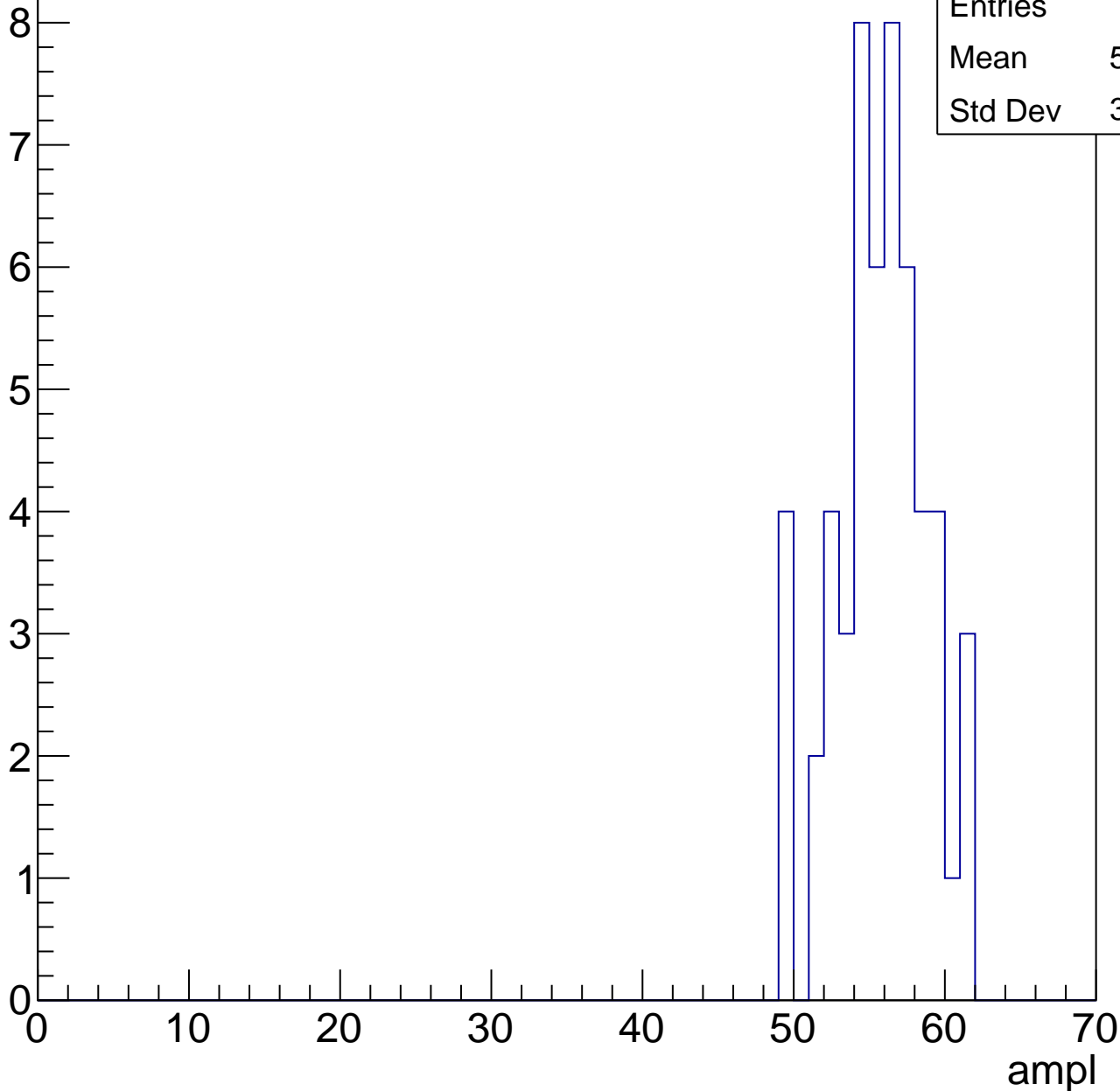


B0L000S, U2-ch110, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	55.25
Std Dev	3.052

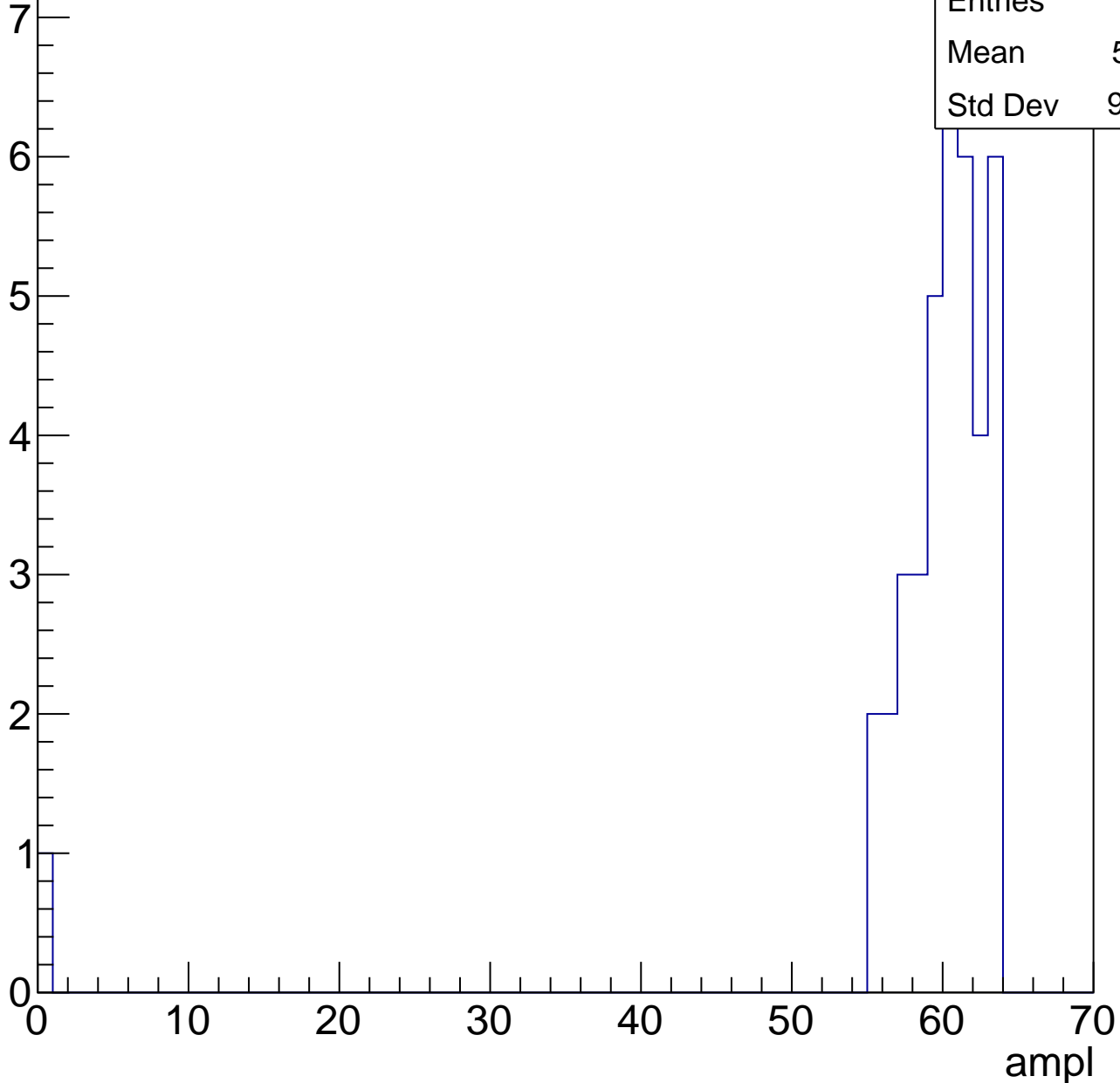


B0L000S, U2-ch110, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.31
Std Dev	9.727

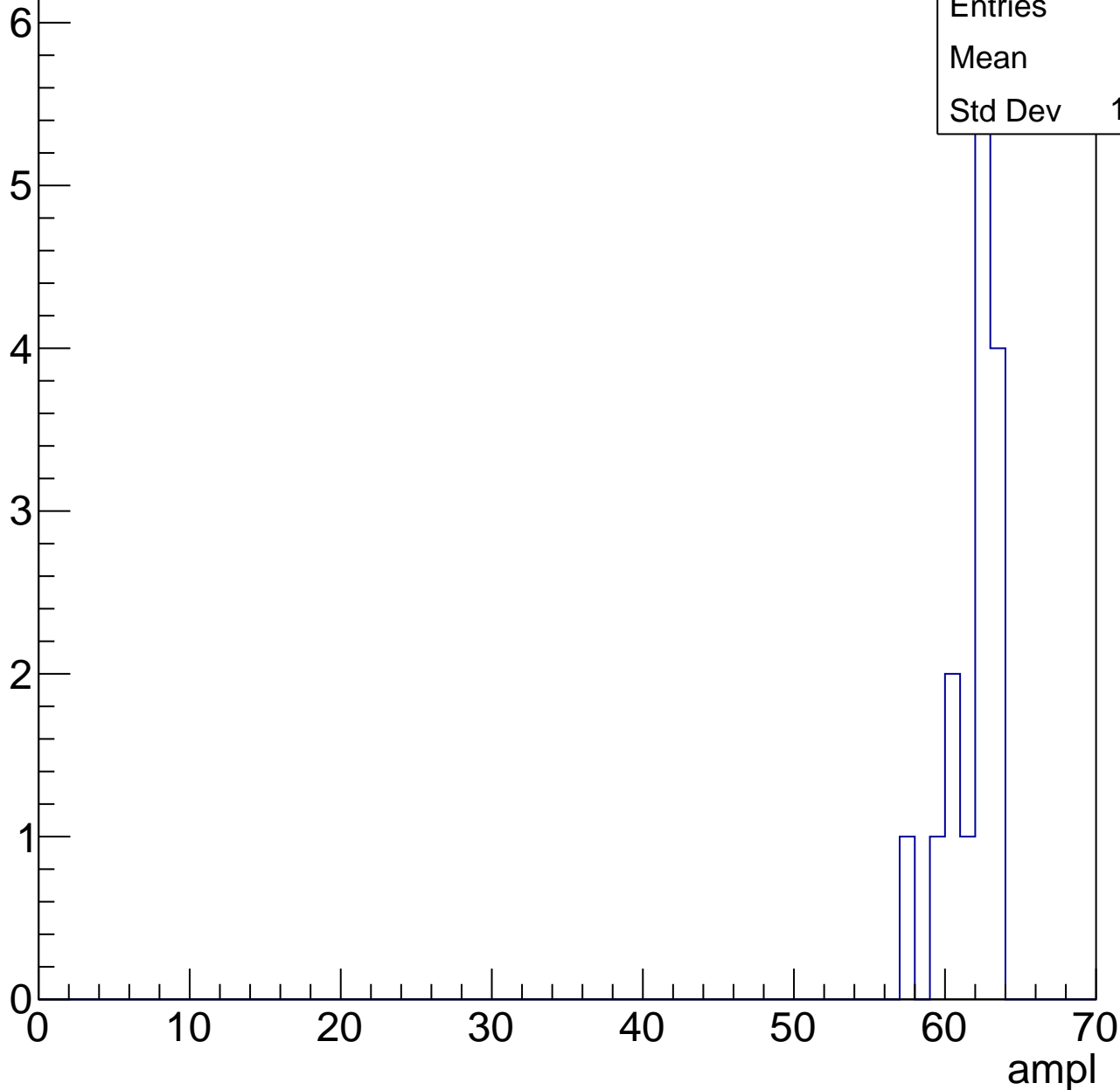


B0L000S, U2-ch110, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	15
Mean	61.4
Std Dev	1.665



B0L000S, U2-ch110, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch111, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	74
Mean	29.23
Std Dev	4.867

Gaus mean : 30.4642

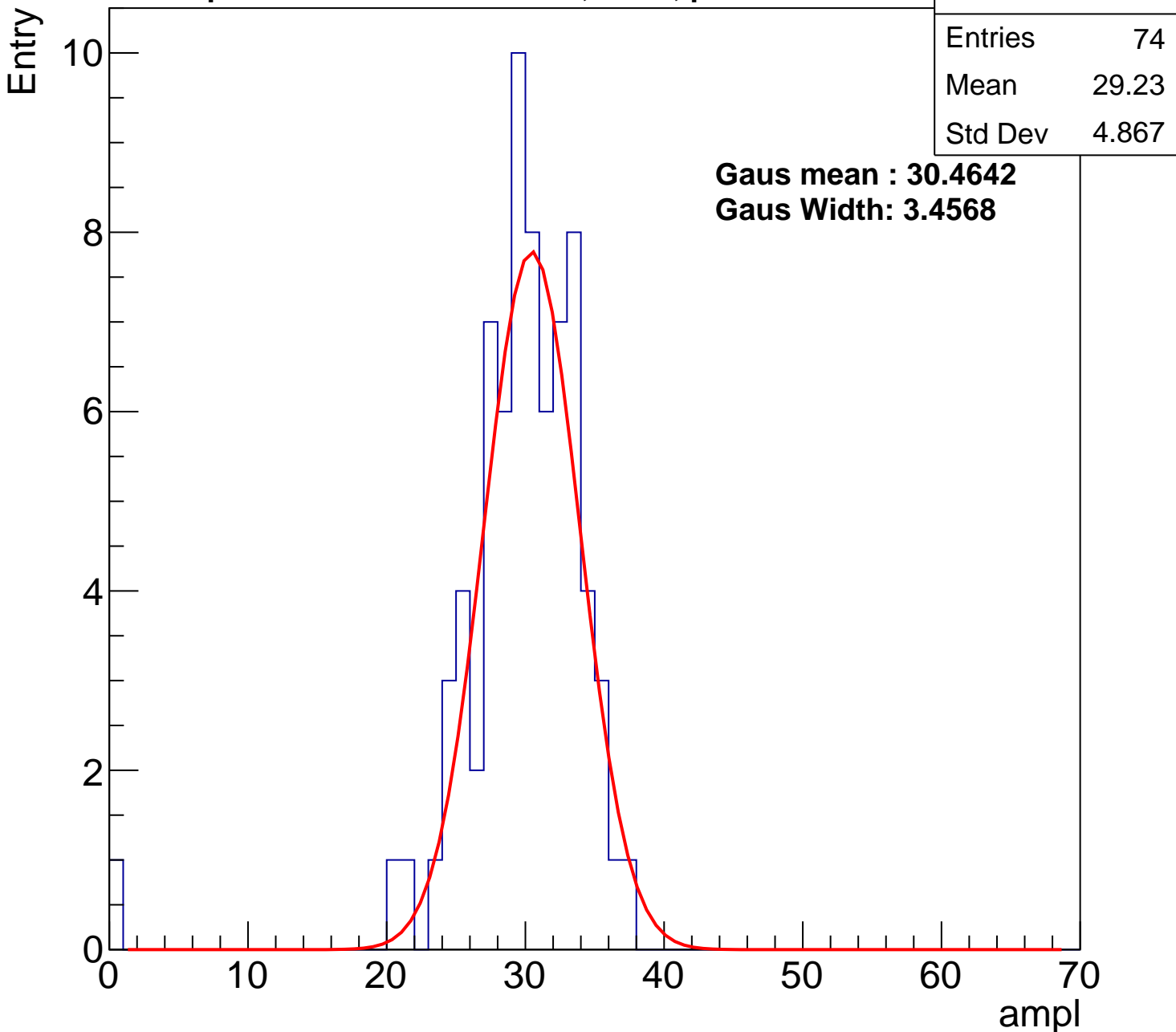
Gaus Width: 3.4568

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



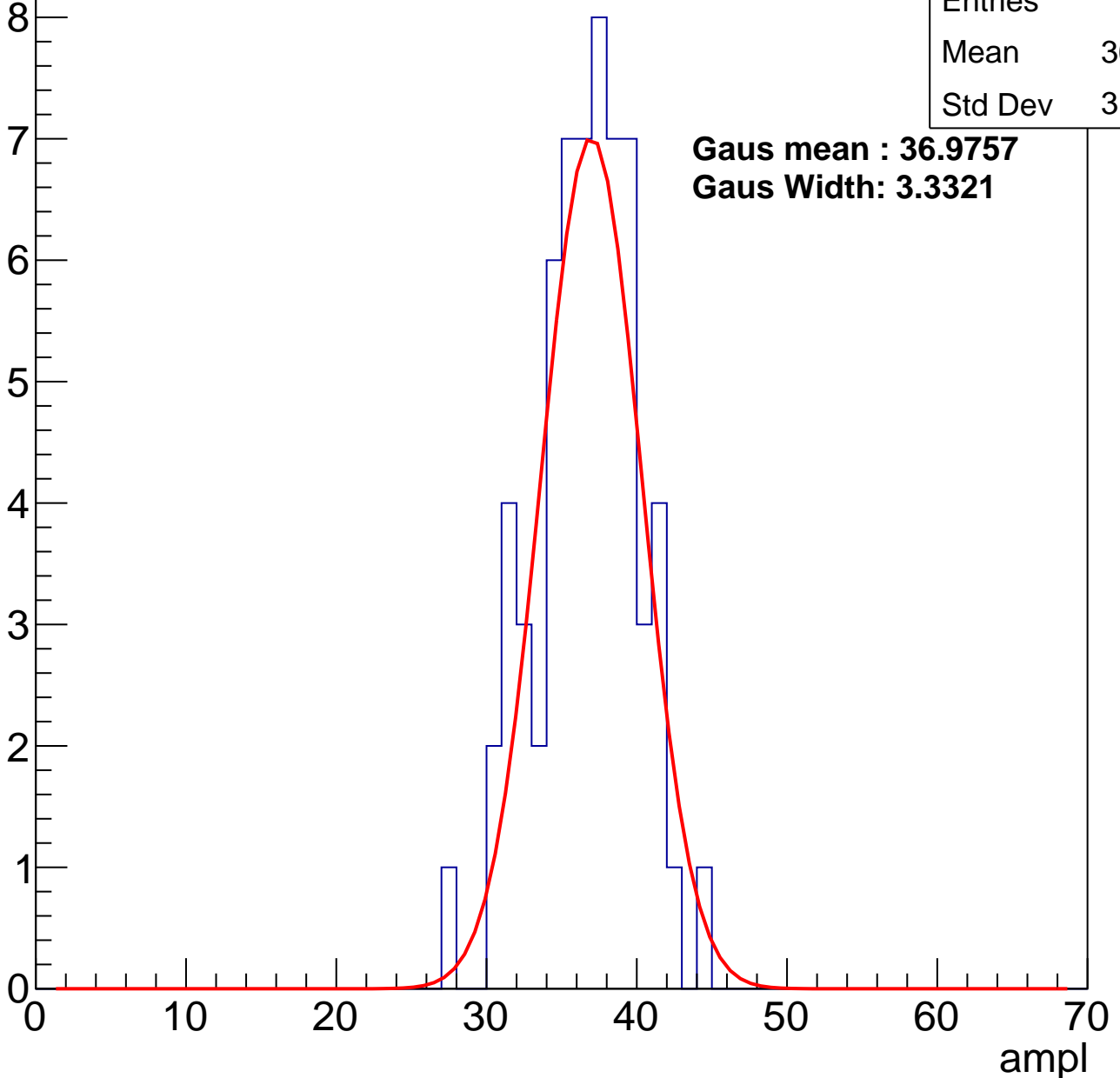
B0L000S, U2-ch111, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.17
Std Dev	3.322

Gaus mean : 36.9757
Gaus Width: 3.3321



B0L000S, U2-ch111, adc2

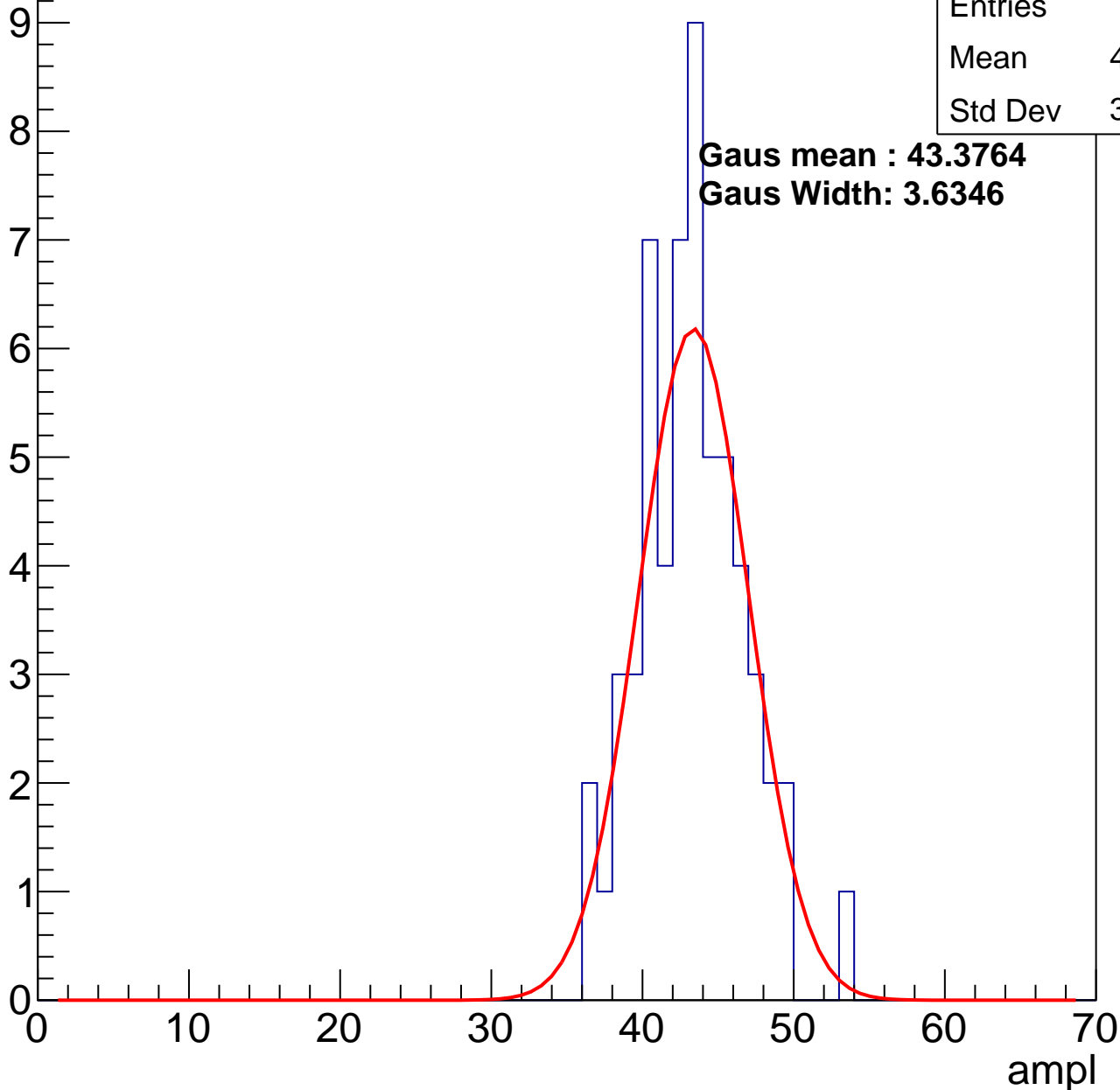
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.79
Std Dev	3.403

Gaus mean : 43.3764

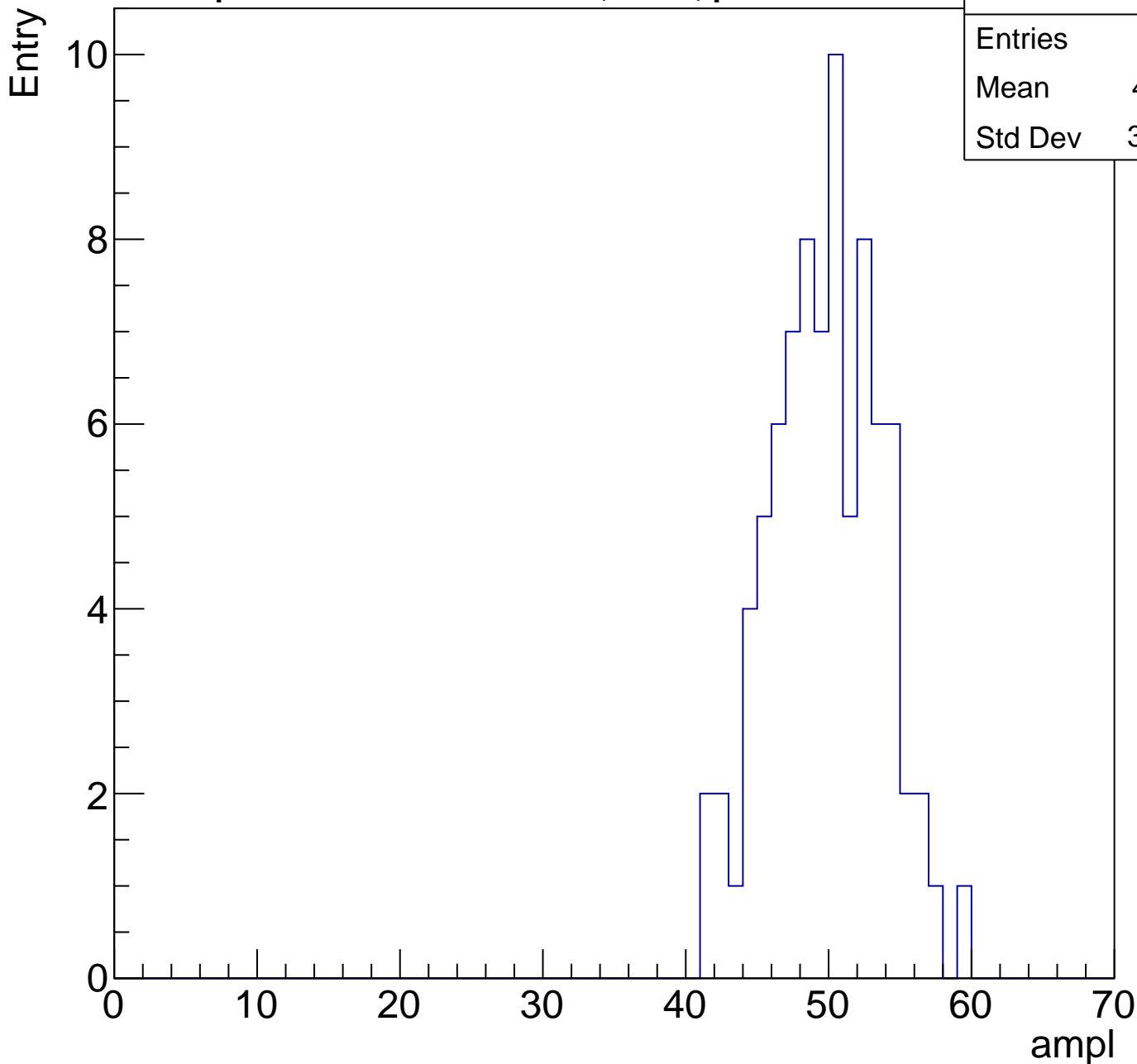
Gaus Width: 3.6346



B0L000S, U2-ch111, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	83
Mean	49.31
Std Dev	3.812

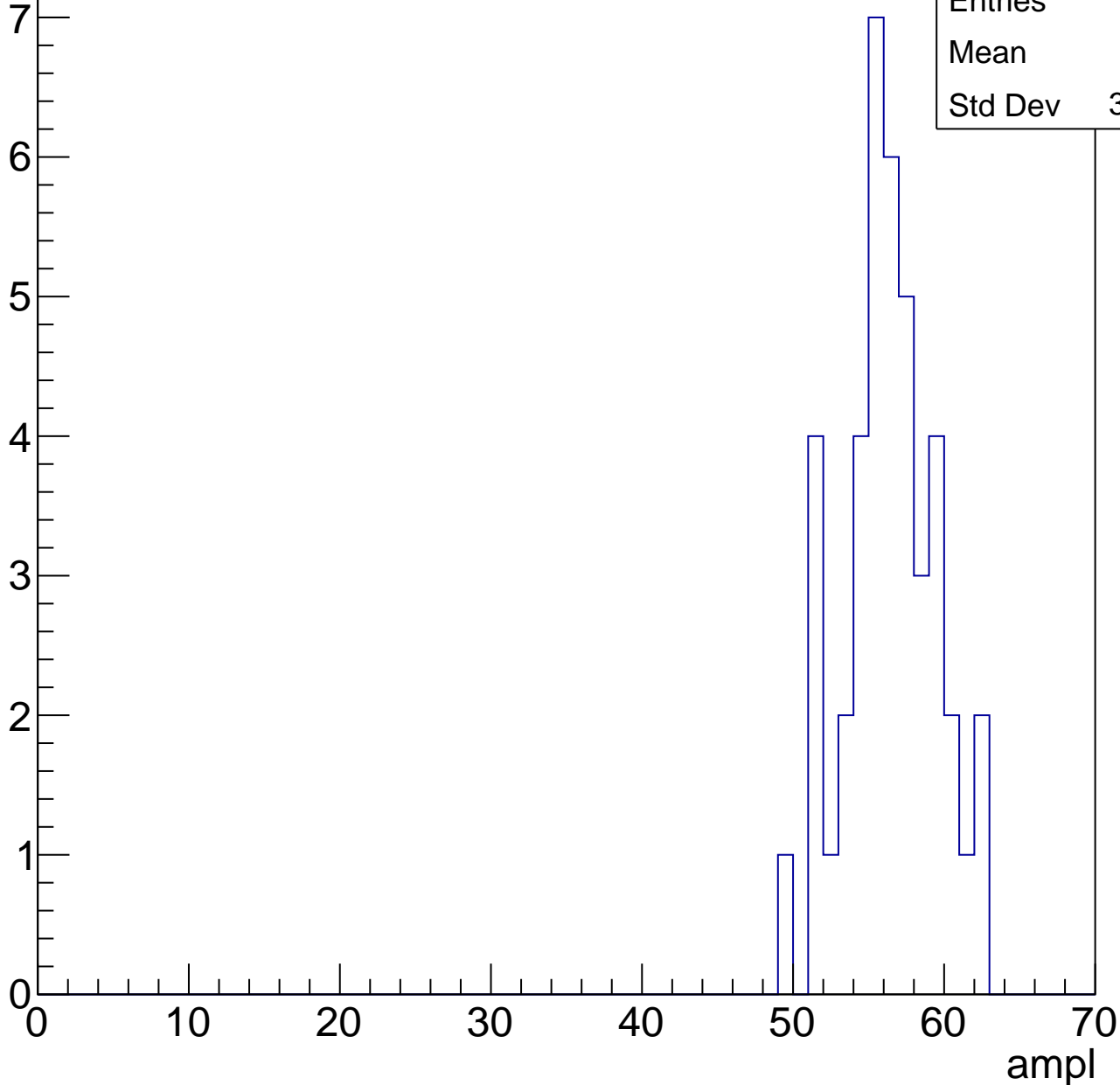


B0L000S, U2-ch111, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

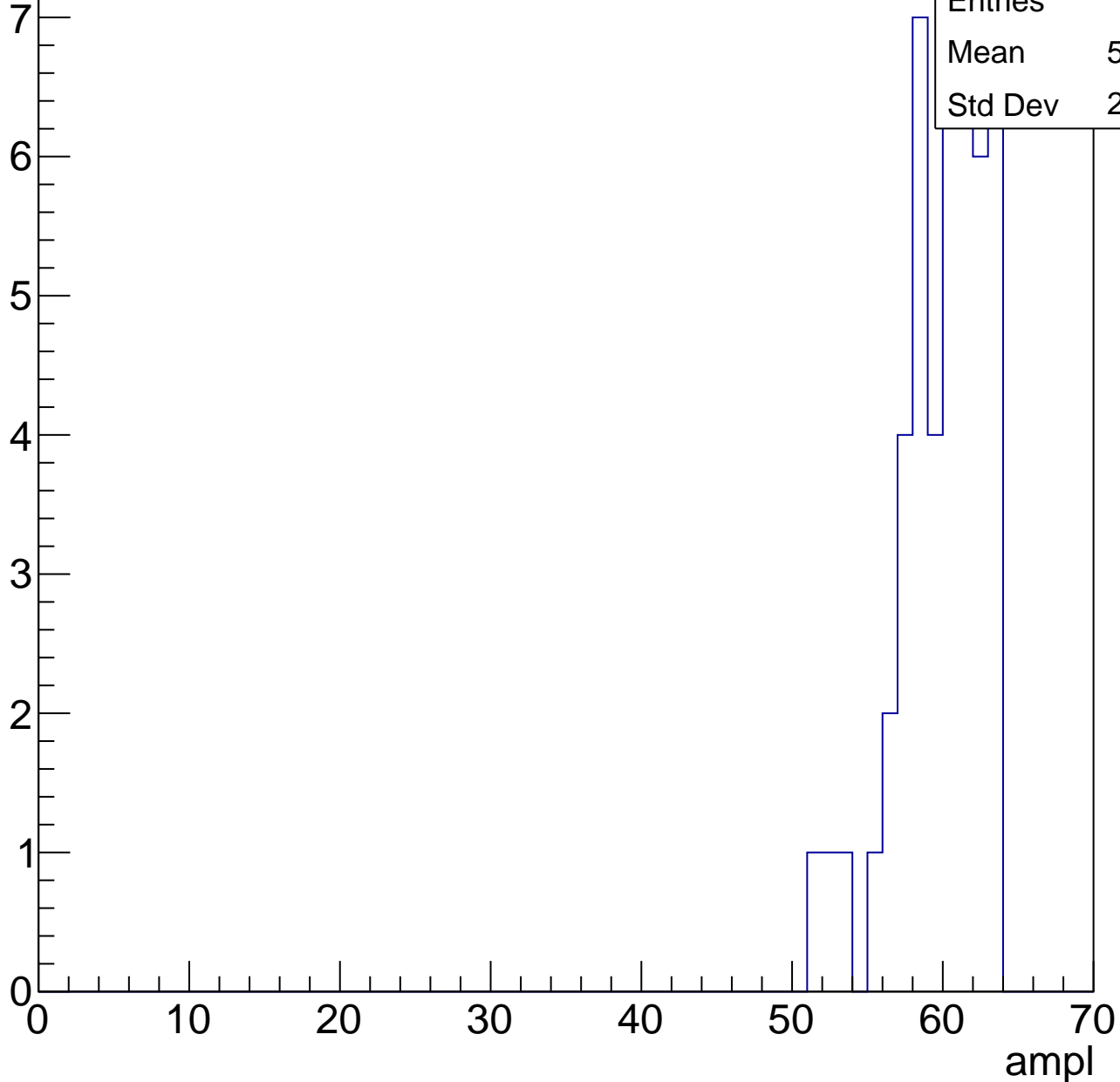
Entries	42
Mean	55.9
Std Dev	3.038



B0L000S, U2-ch111, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

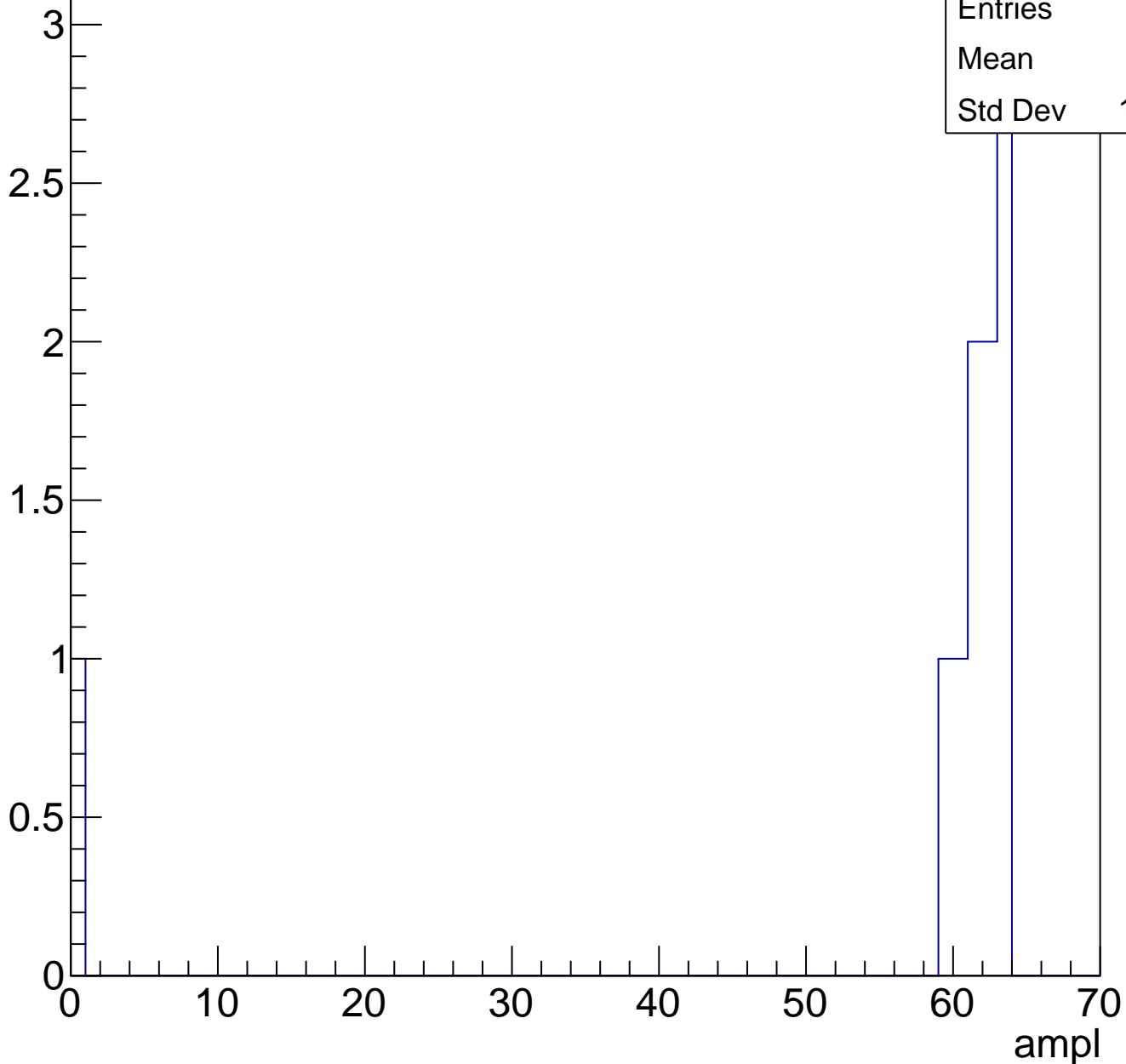


Entries	48
Mean	59.44
Std Dev	2.879

B0L000S, U2-ch111, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch111, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch112, adc0

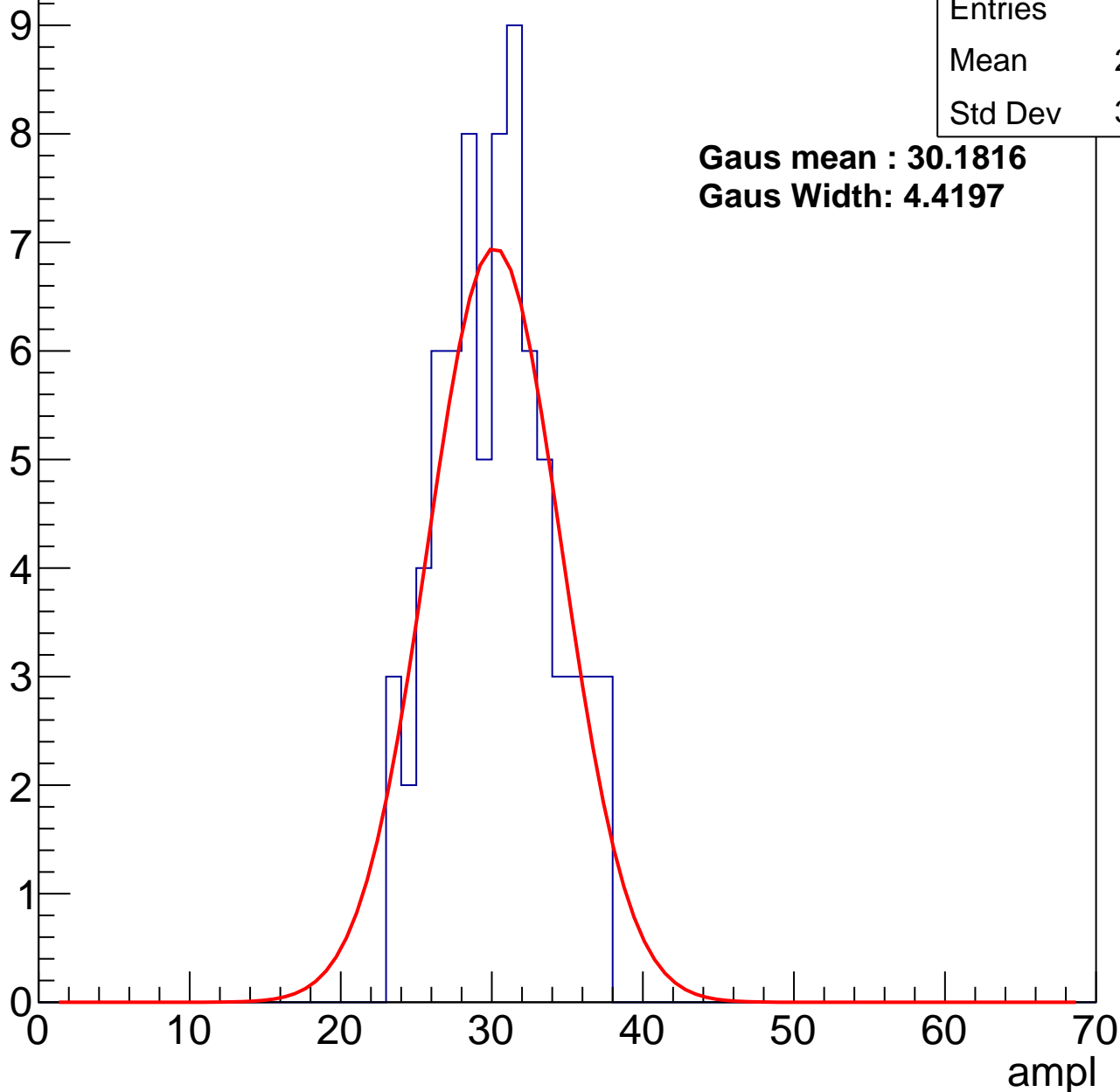
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	29.81
Std Dev	3.601

Gaus mean : 30.1816

Gaus Width: 4.4197



B0L000S, U2-ch112, adc1

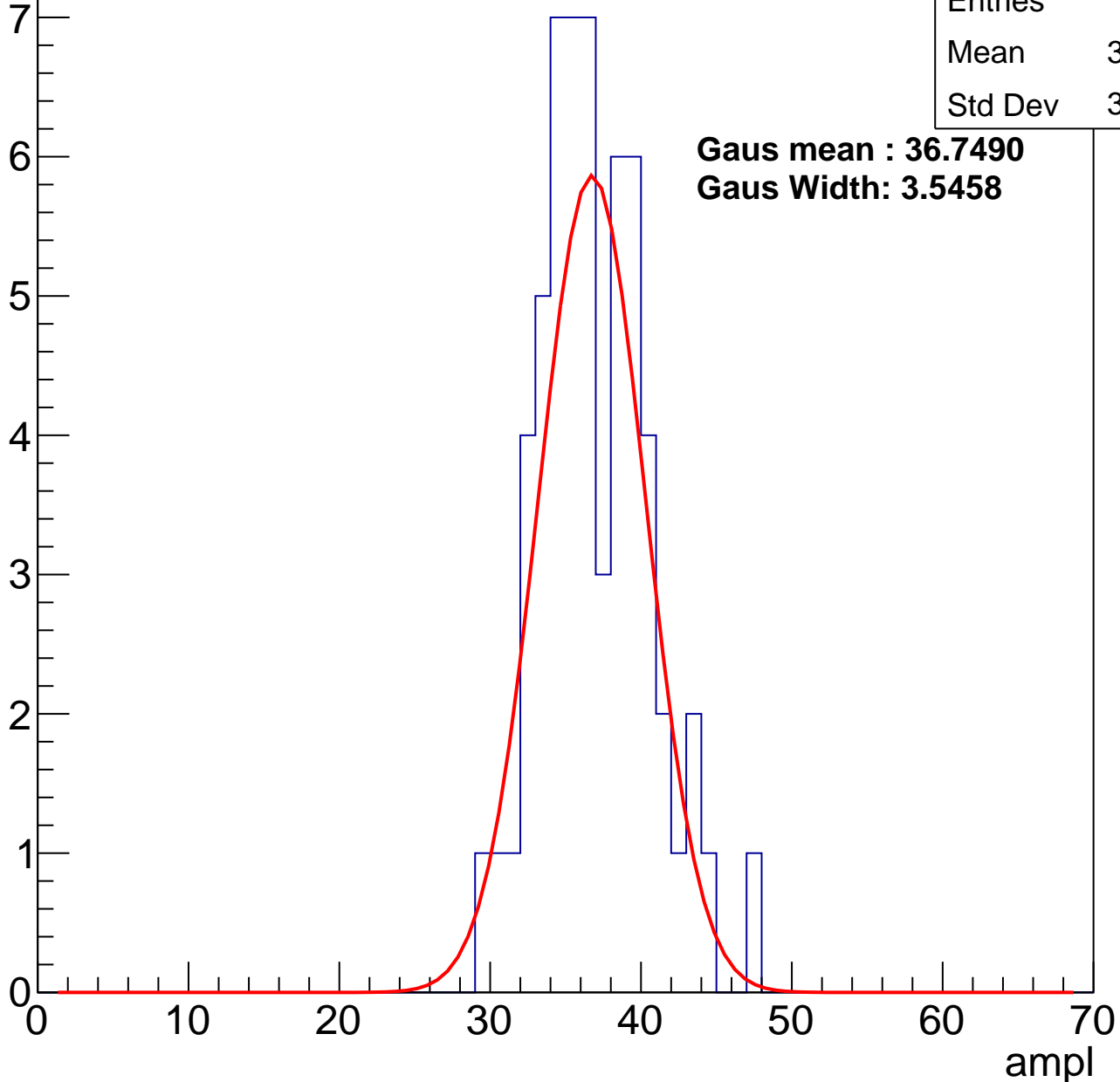
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.47
Std Dev	3.577

Gaus mean : 36.7490

Gaus Width: 3.5458



B0L000S, U2-ch112, adc2

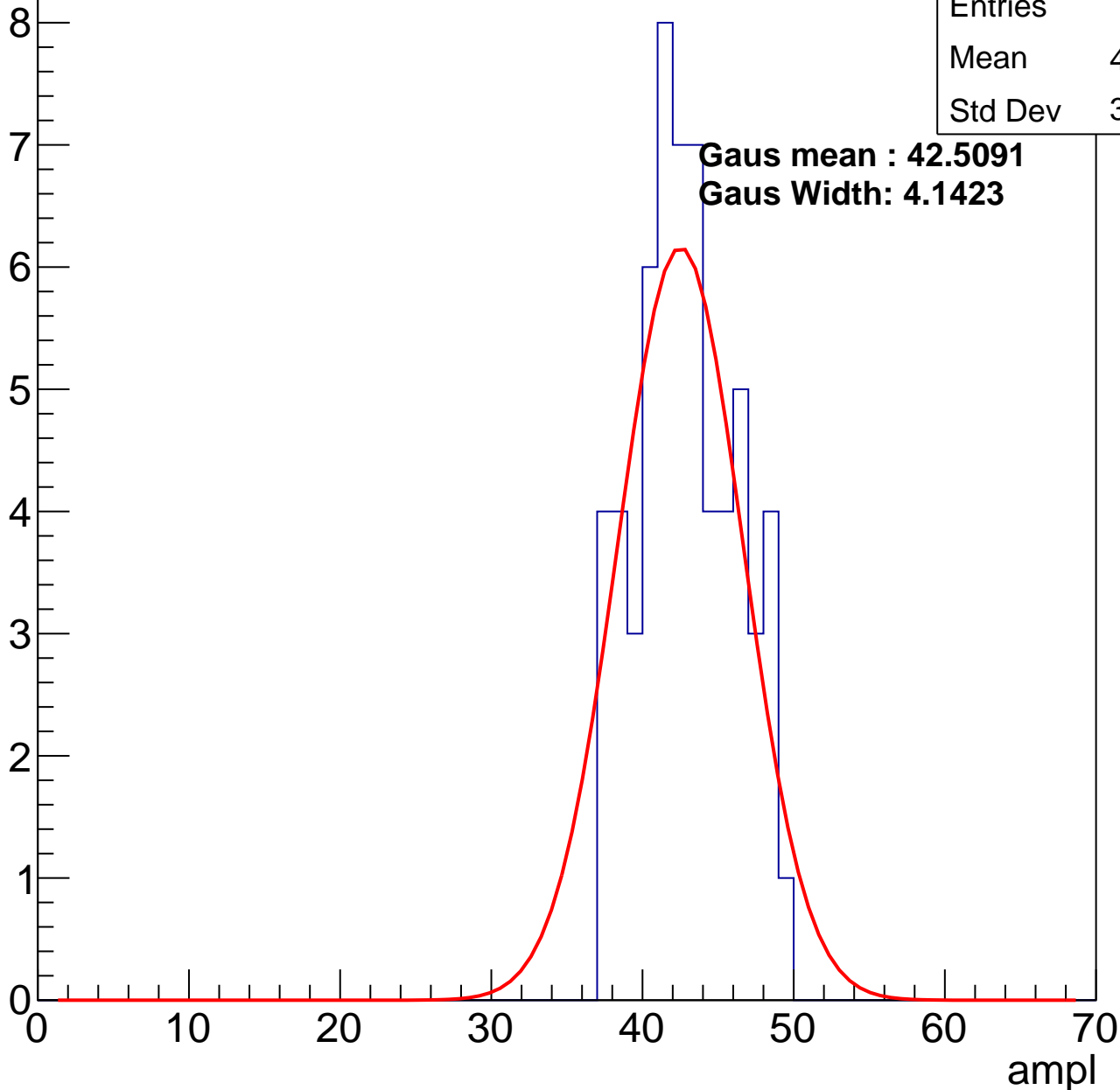
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.47
Std Dev	3.207

Gaus mean : 42.5091

Gaus Width: 4.1423

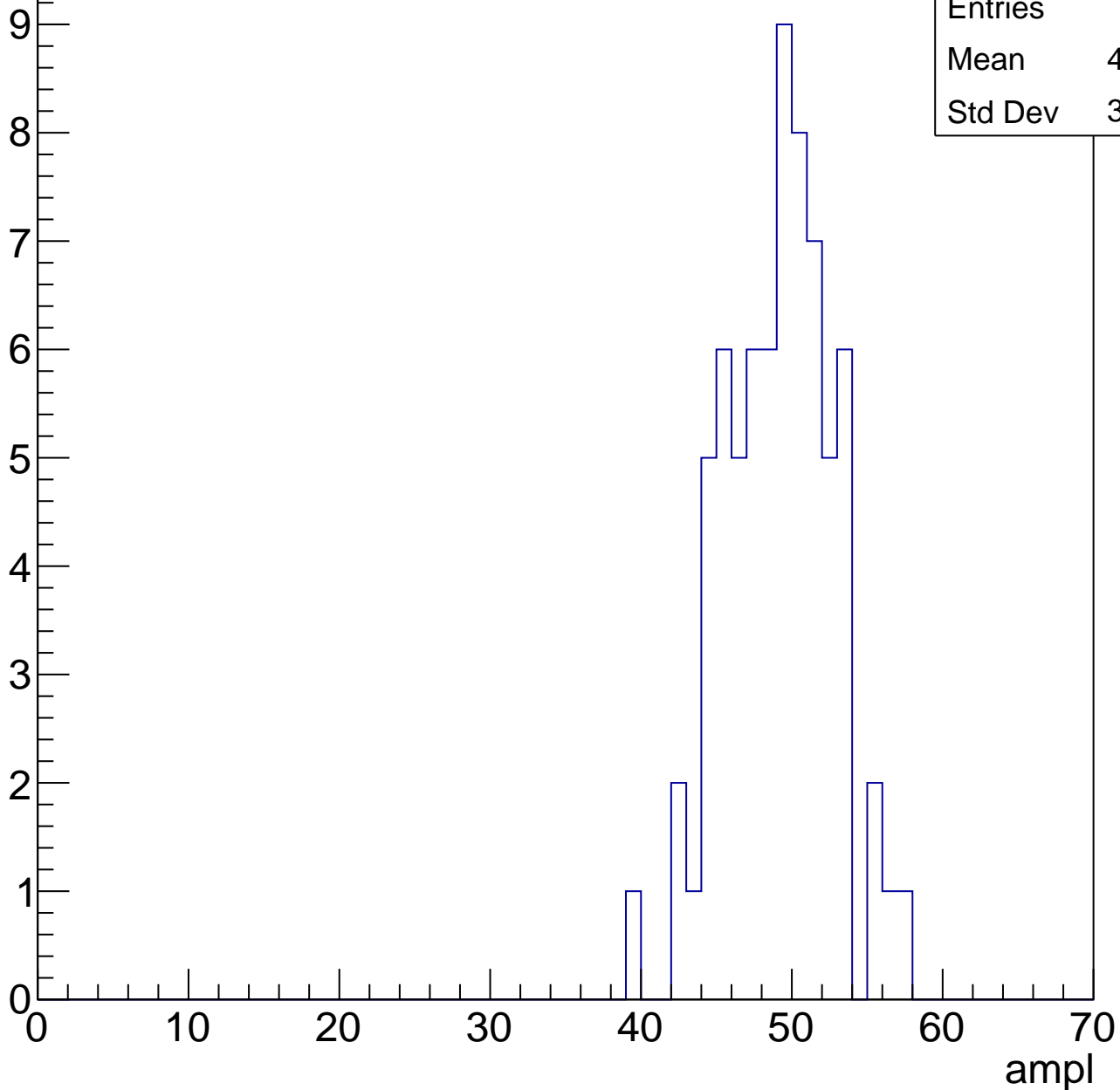


B0L000S, U2-ch112, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	71
Mean	48.66
Std Dev	3.528

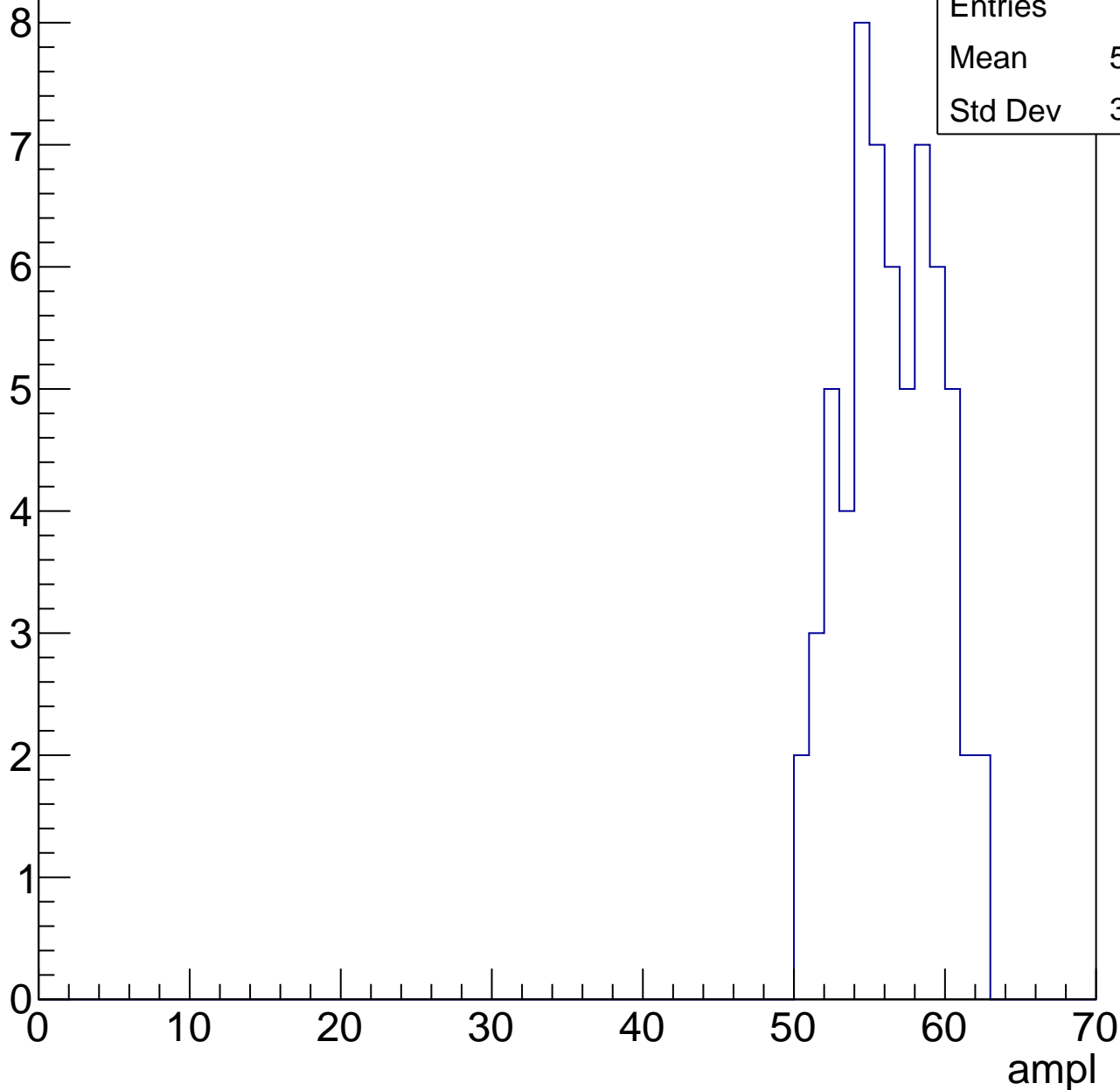


B0L000S, U2-ch112, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.95
Std Dev	3.087

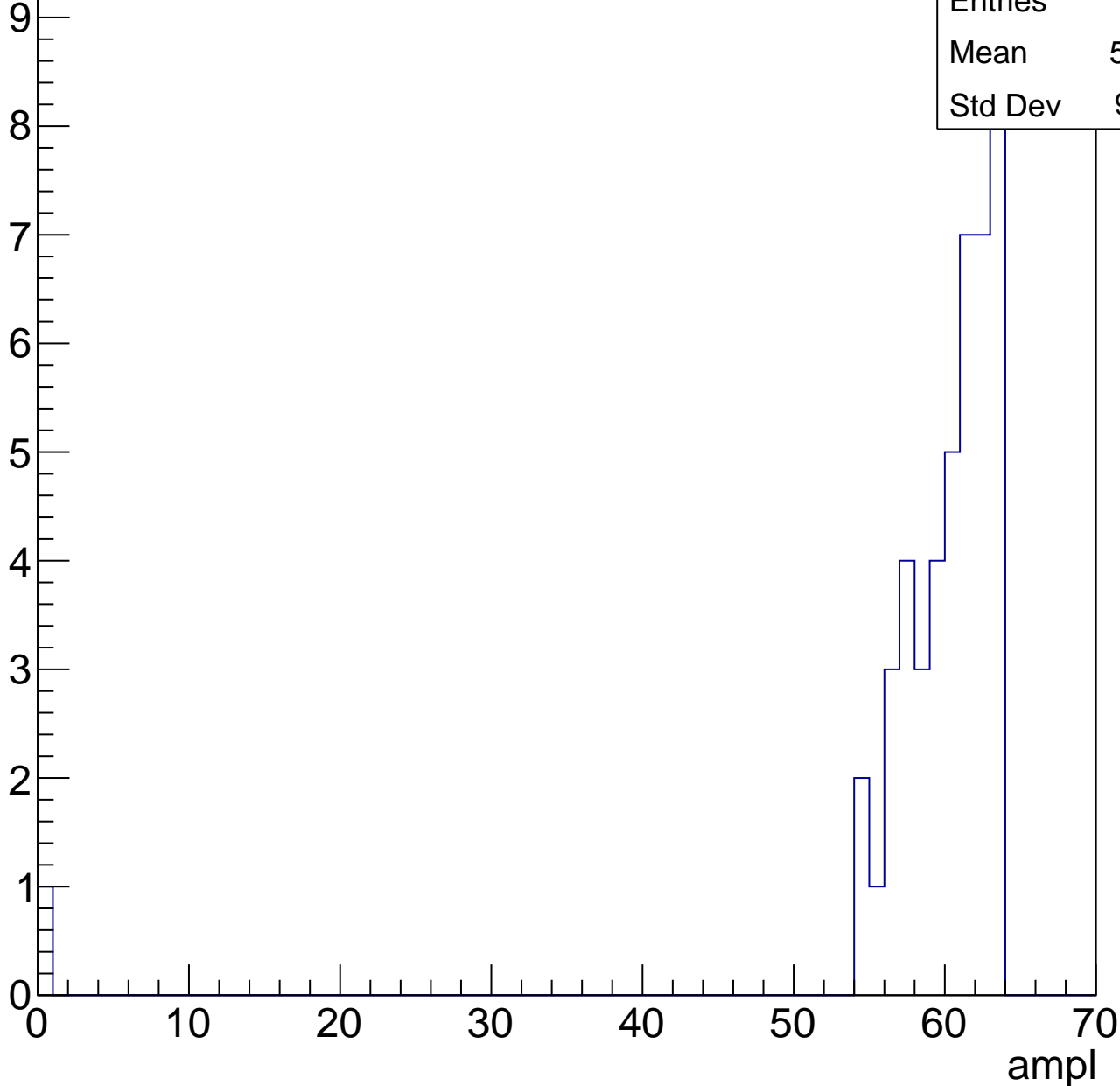


B0L000S, U2-ch112, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.63
Std Dev	9.121



B0L000S, U2-ch112, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch112, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch113, adc0

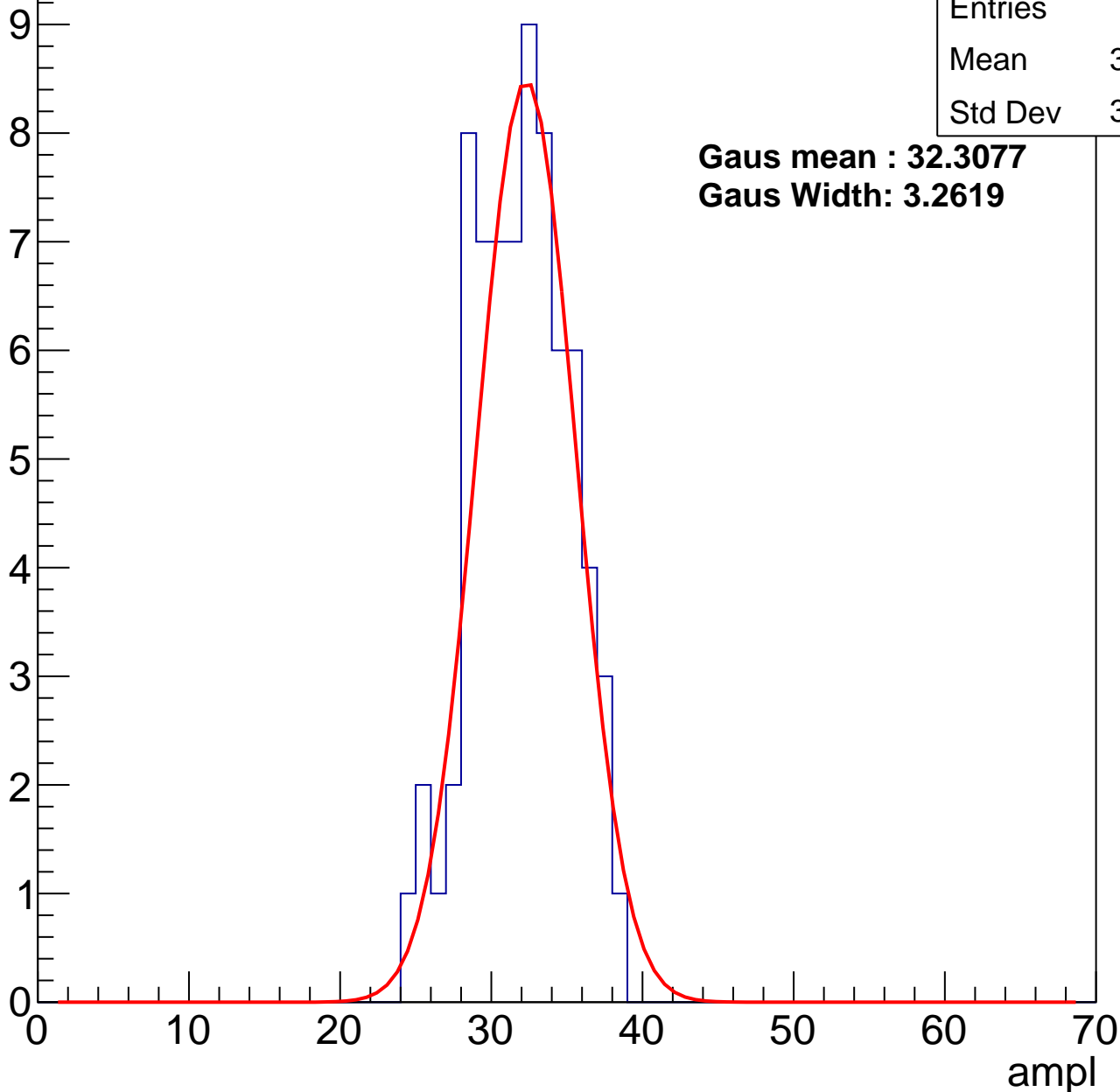
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	72
Mean	31.49
Std Dev	3.153

Gaus mean : 32.3077

Gaus Width: 3.2619



B0L000S, U2-ch113, adc1

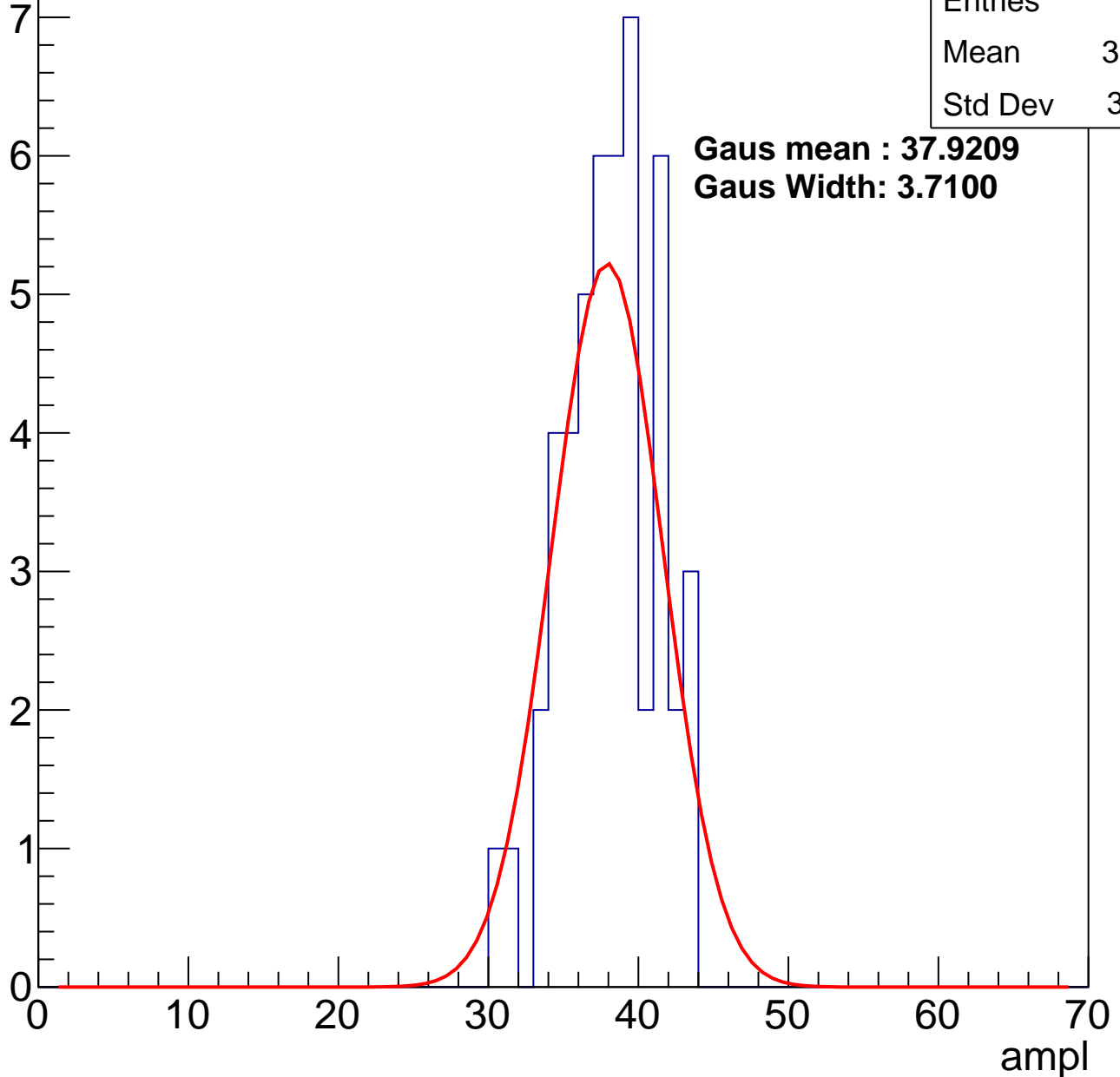
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	37.65
Std Dev	3.061

Gaus mean : 37.9209

Gaus Width: 3.7100



B0L000S, U2-ch113, adc2

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.58
Std Dev	3.357

Gaus mean : 44.1266

Gaus Width: 3.6298

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

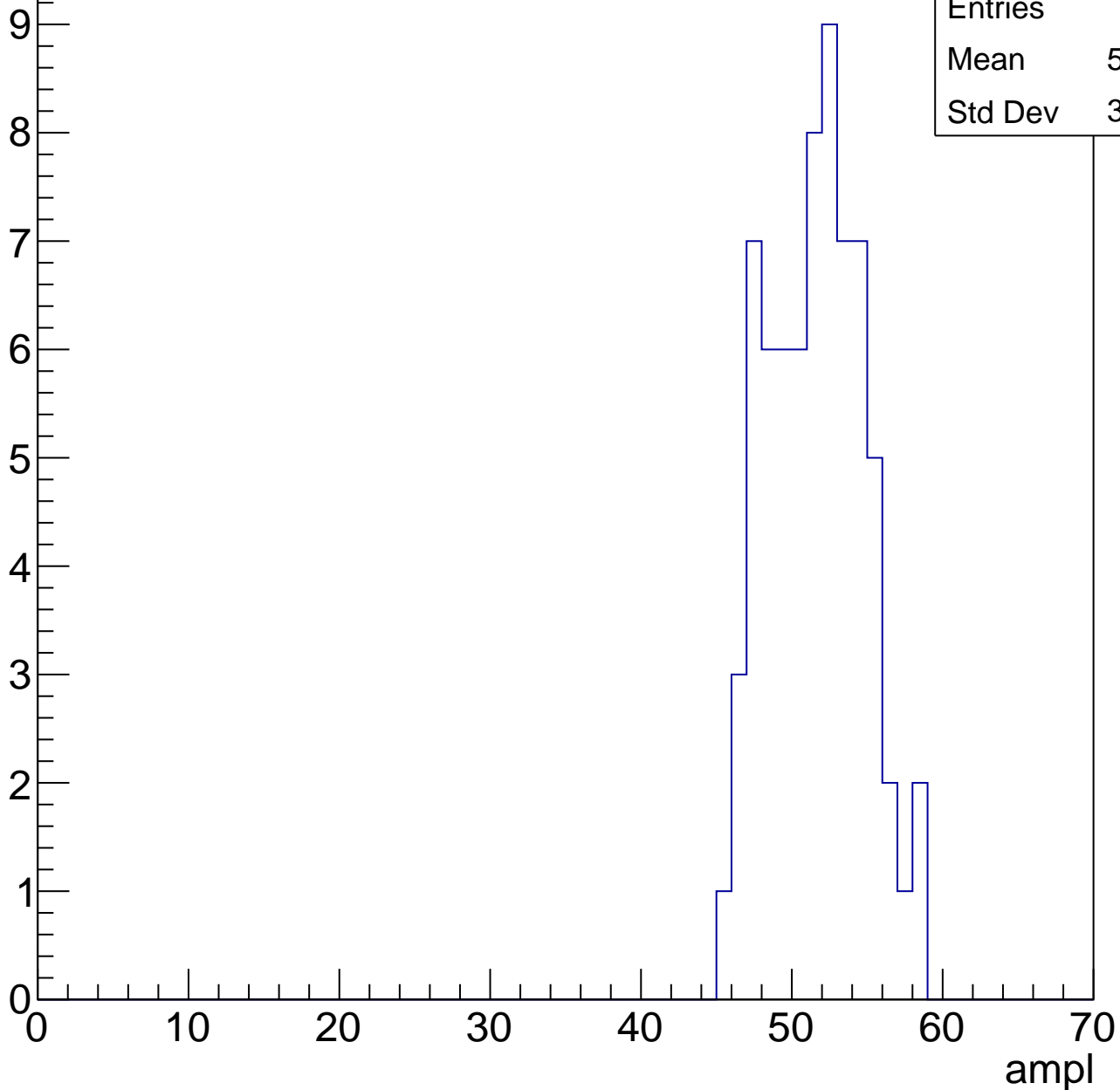
70

B0L000S, U2-ch113, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

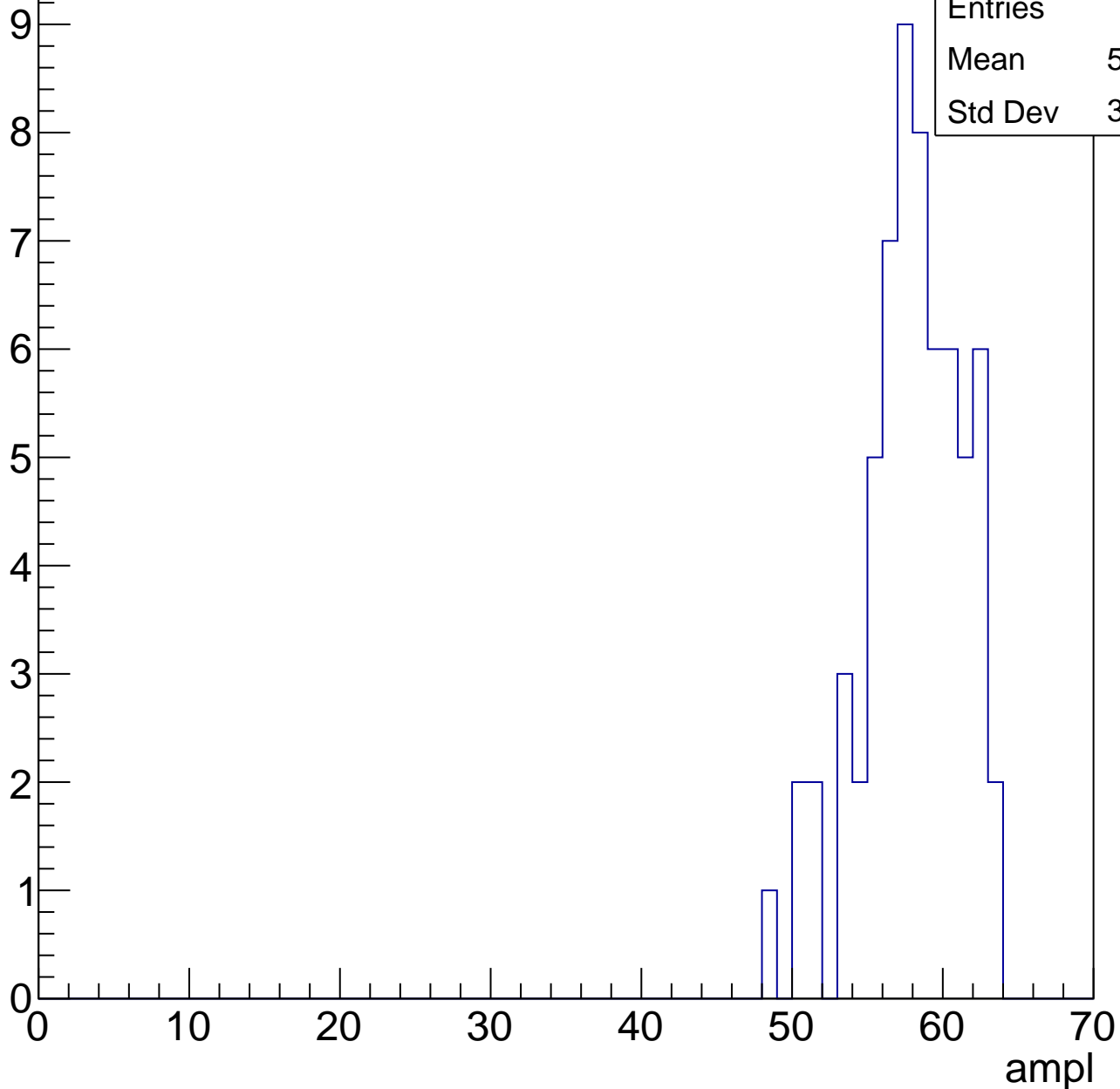
Entries	70
Mean	51.13
Std Dev	3.093



B0L000S, U2-ch113, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

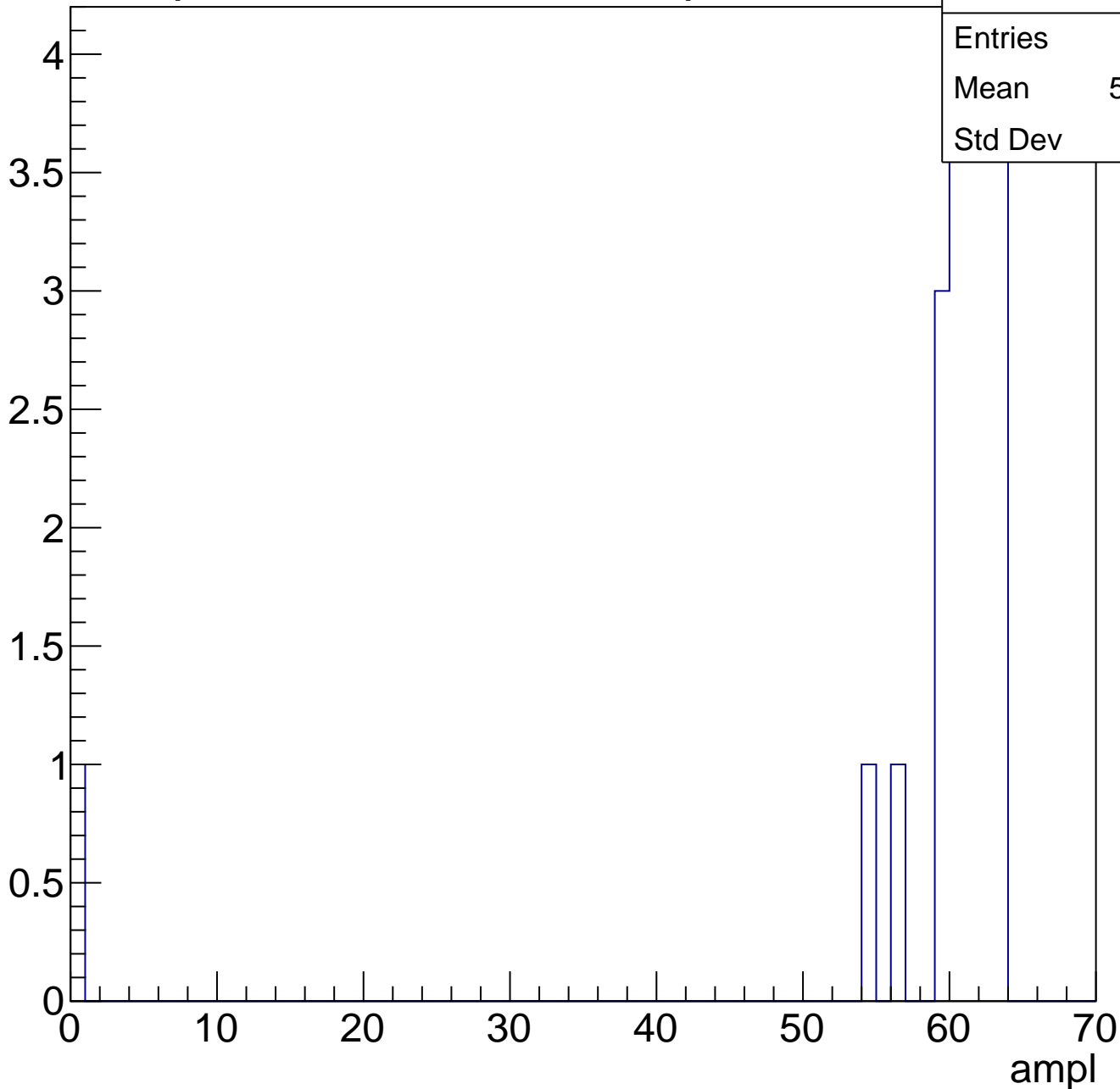


Entries	64
Mean	57.47
Std Dev	3.349

B0L000S, U2-ch113, adc5

calib_packv5_042523_0143.root, FC#5, port B1

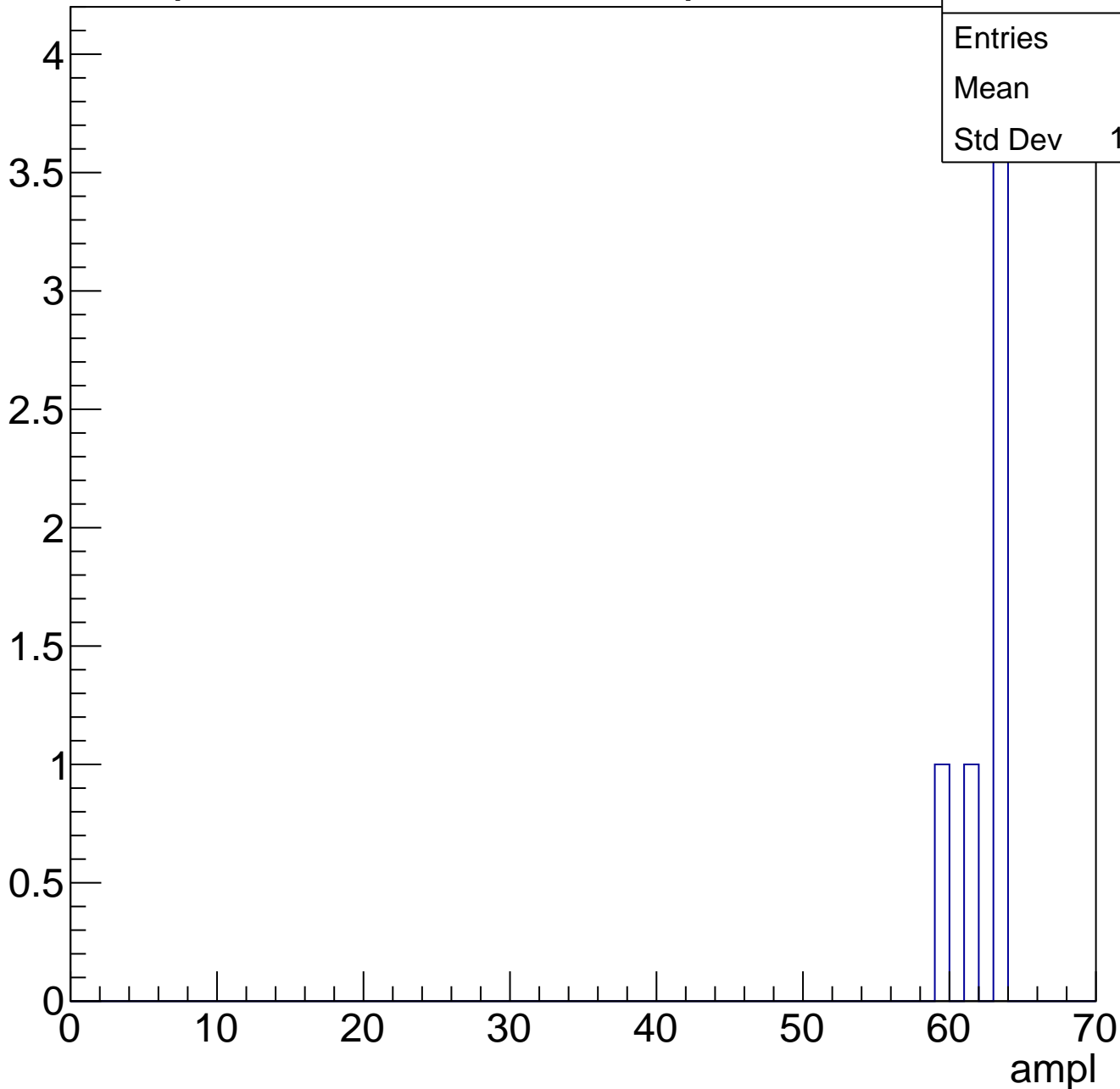
Entry



B0L000S, U2-ch113, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch113, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch114, adc0

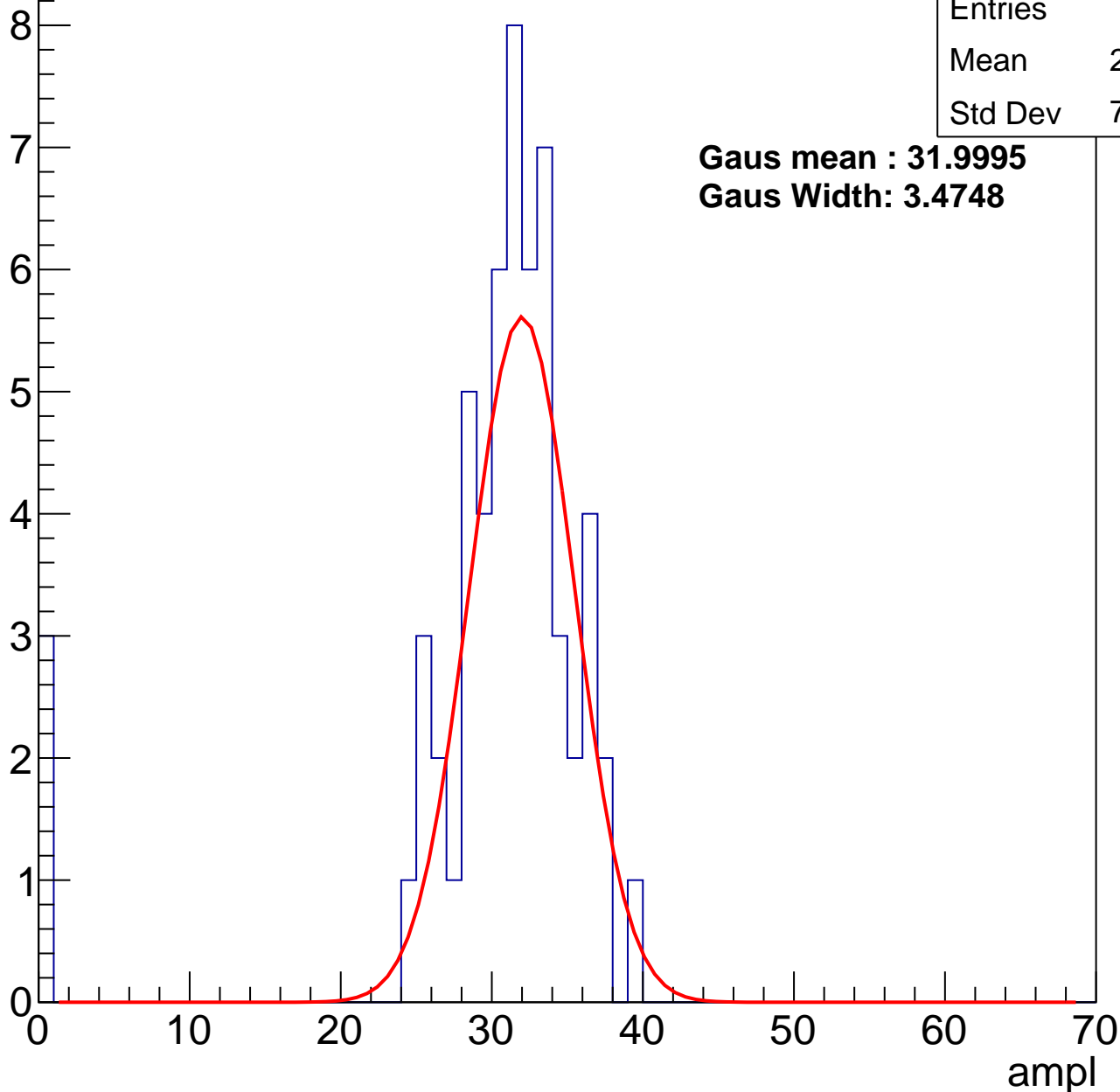
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.55
Std Dev	7.634

Gaus mean : 31.9995

Gaus Width: 3.4748



B0L000S, U2-ch114, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	70
Mean	37.44
Std Dev	3.328

Gaus mean : 38.0054

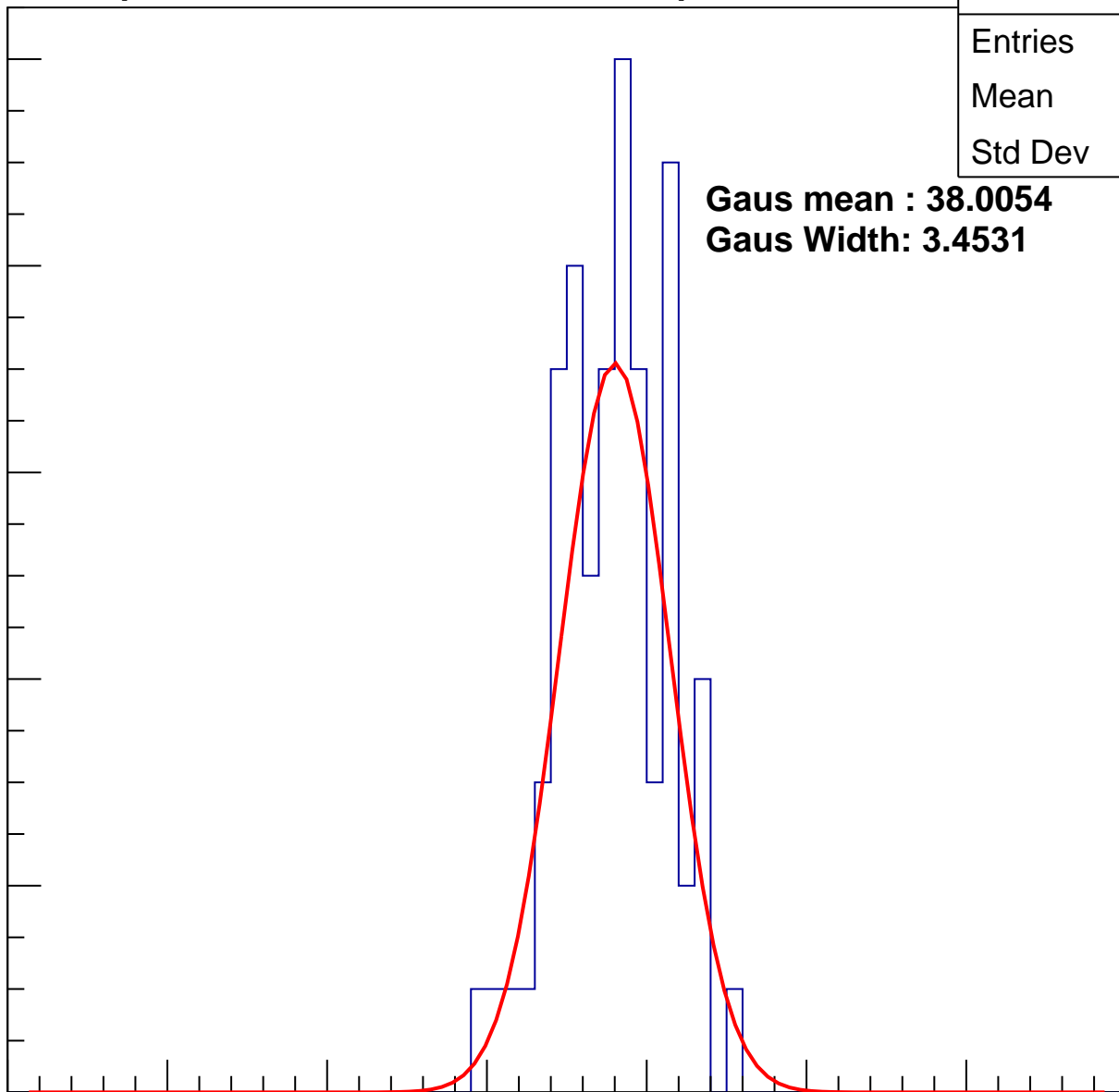
Gaus Width: 3.4531

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch114, adc2

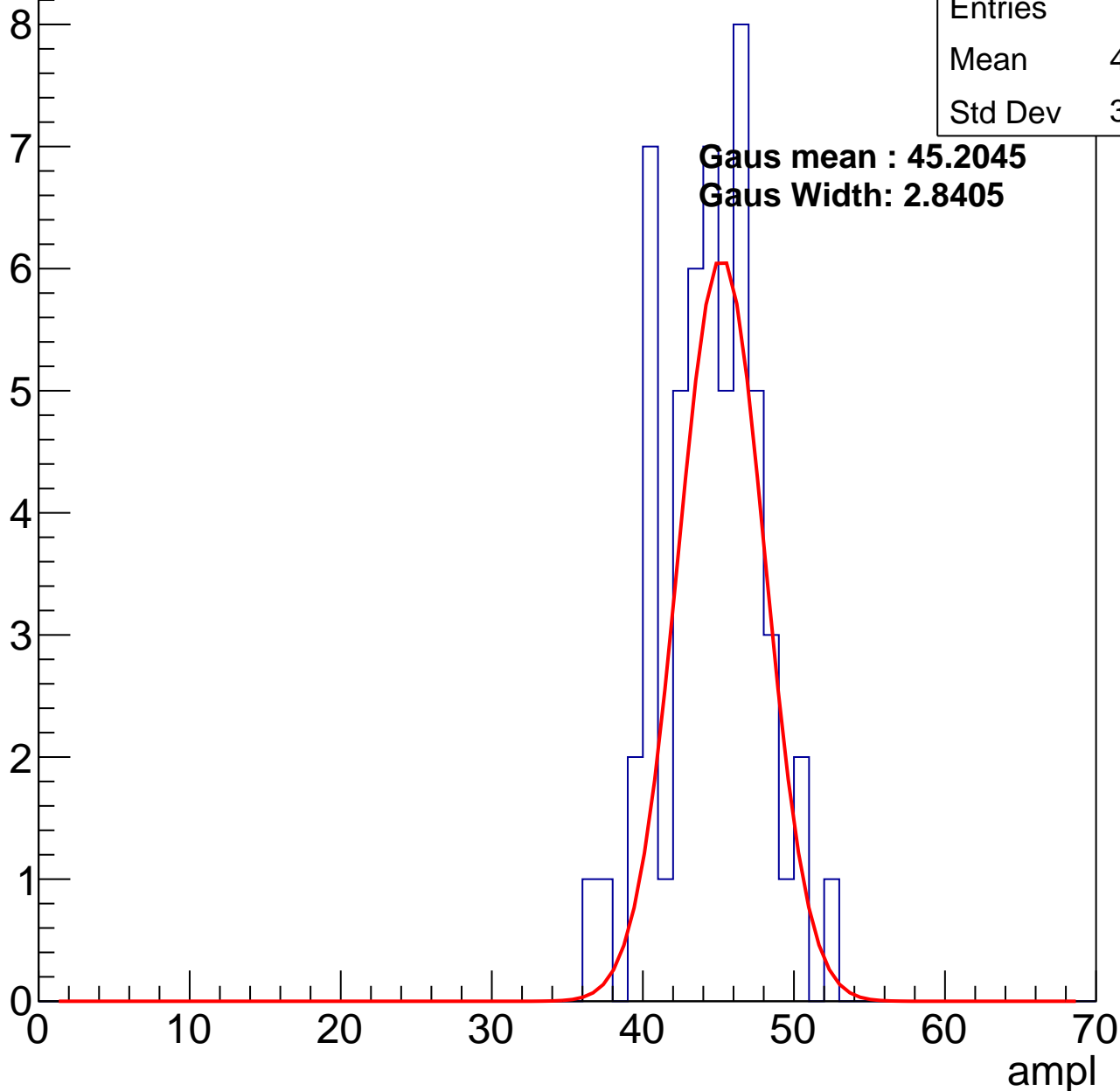
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	44.02
Std Dev	3.306

Gaus mean : 45.2045

Gaus Width: 2.8405

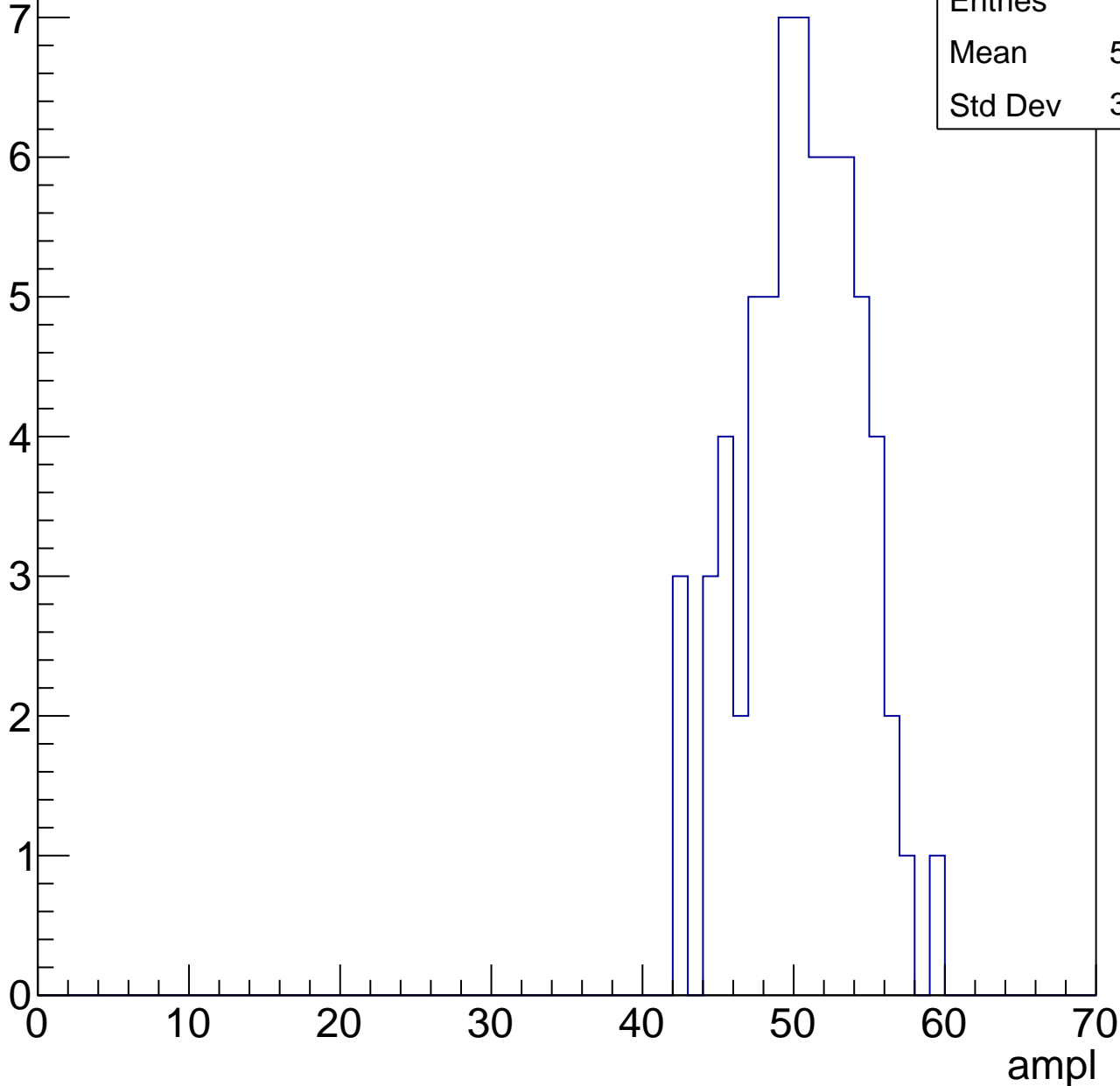


B0L000S, U2-ch114, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	67
Mean	50.03
Std Dev	3.805

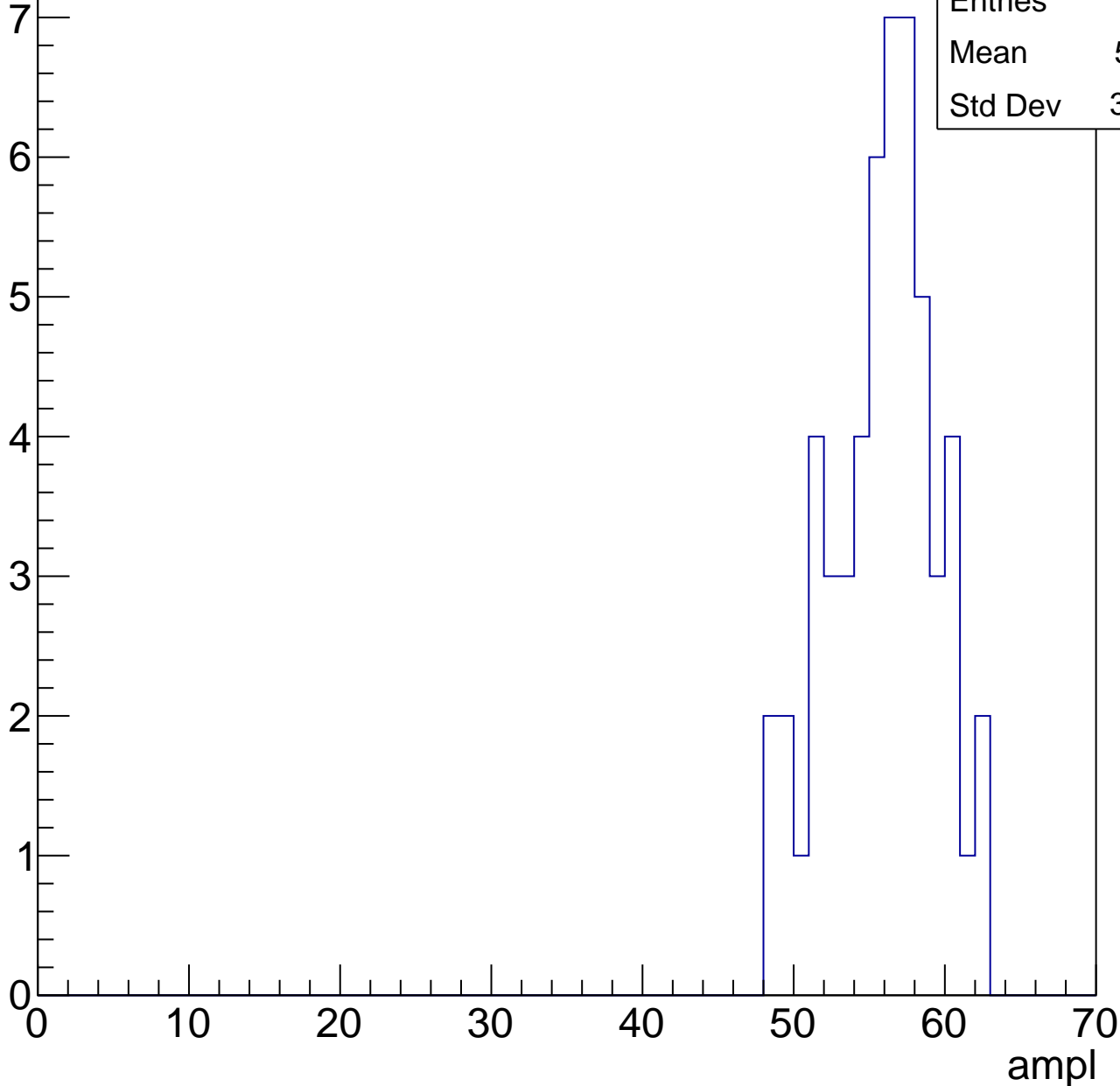


B0L000S, U2-ch114, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.41
Std Dev	3.483

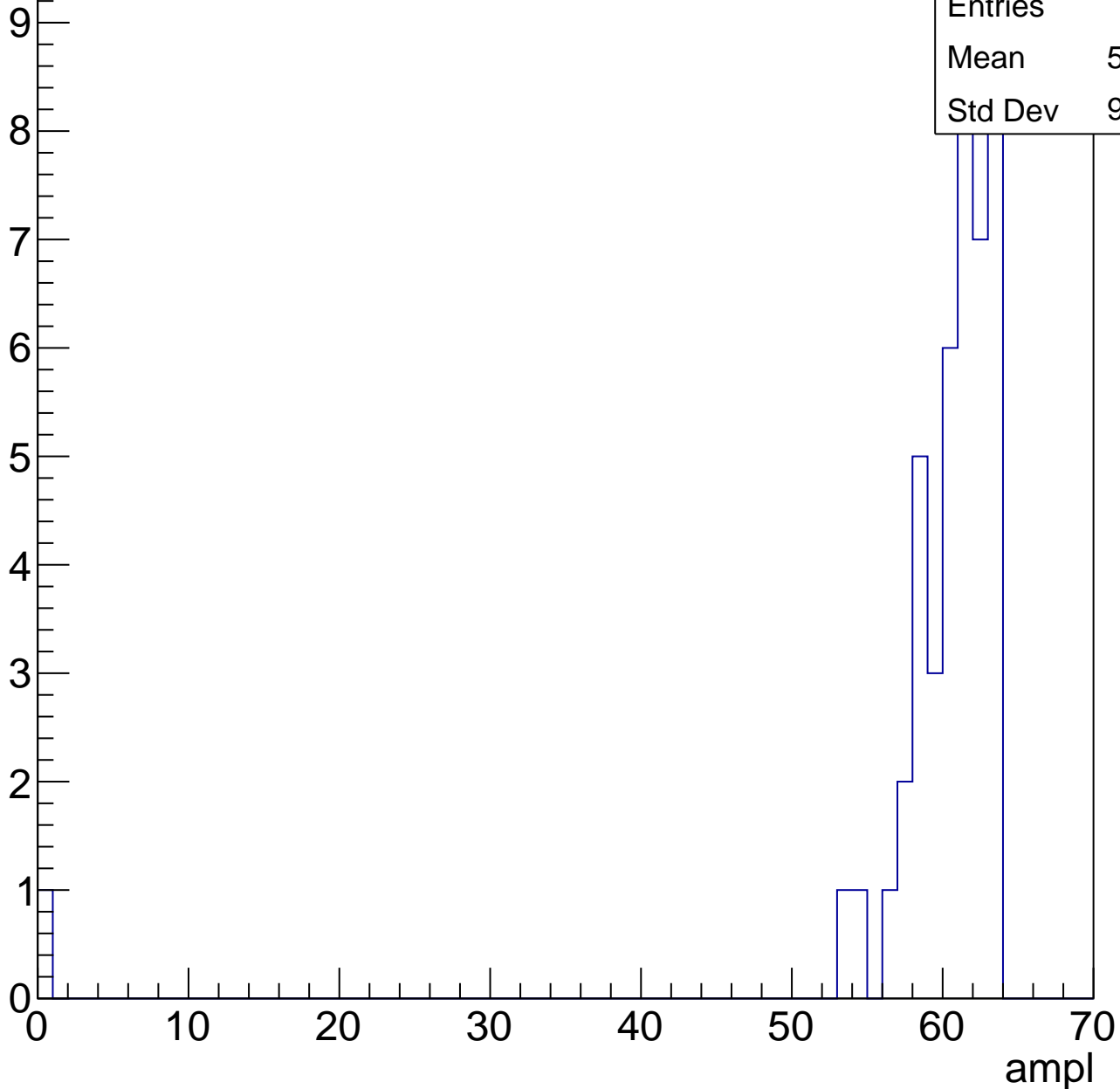


B0L000S, U2-ch114, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

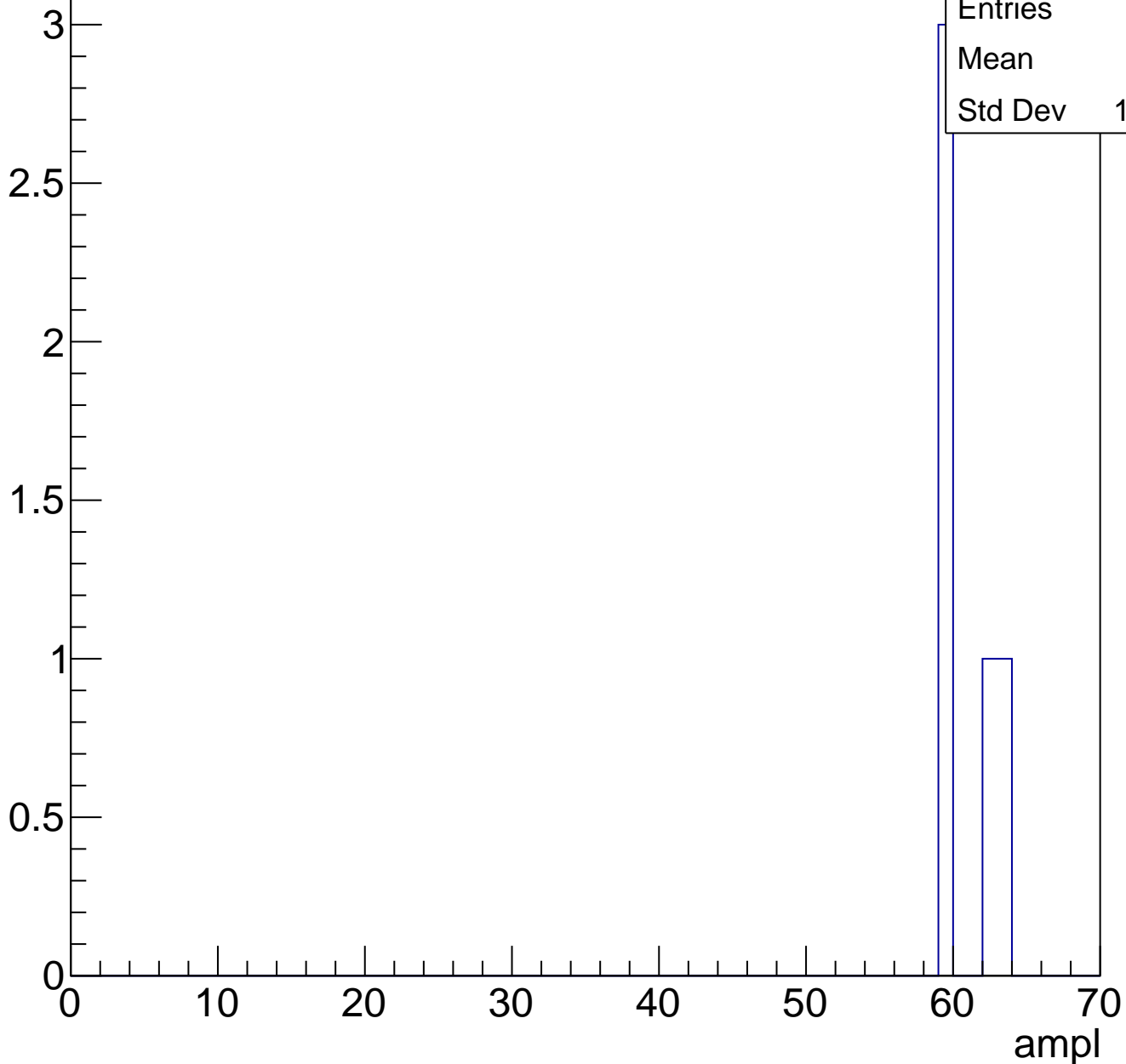
Entries	45
Mean	58.98
Std Dev	9.205



B0L000S, U2-ch114, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch114, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch115, adc0

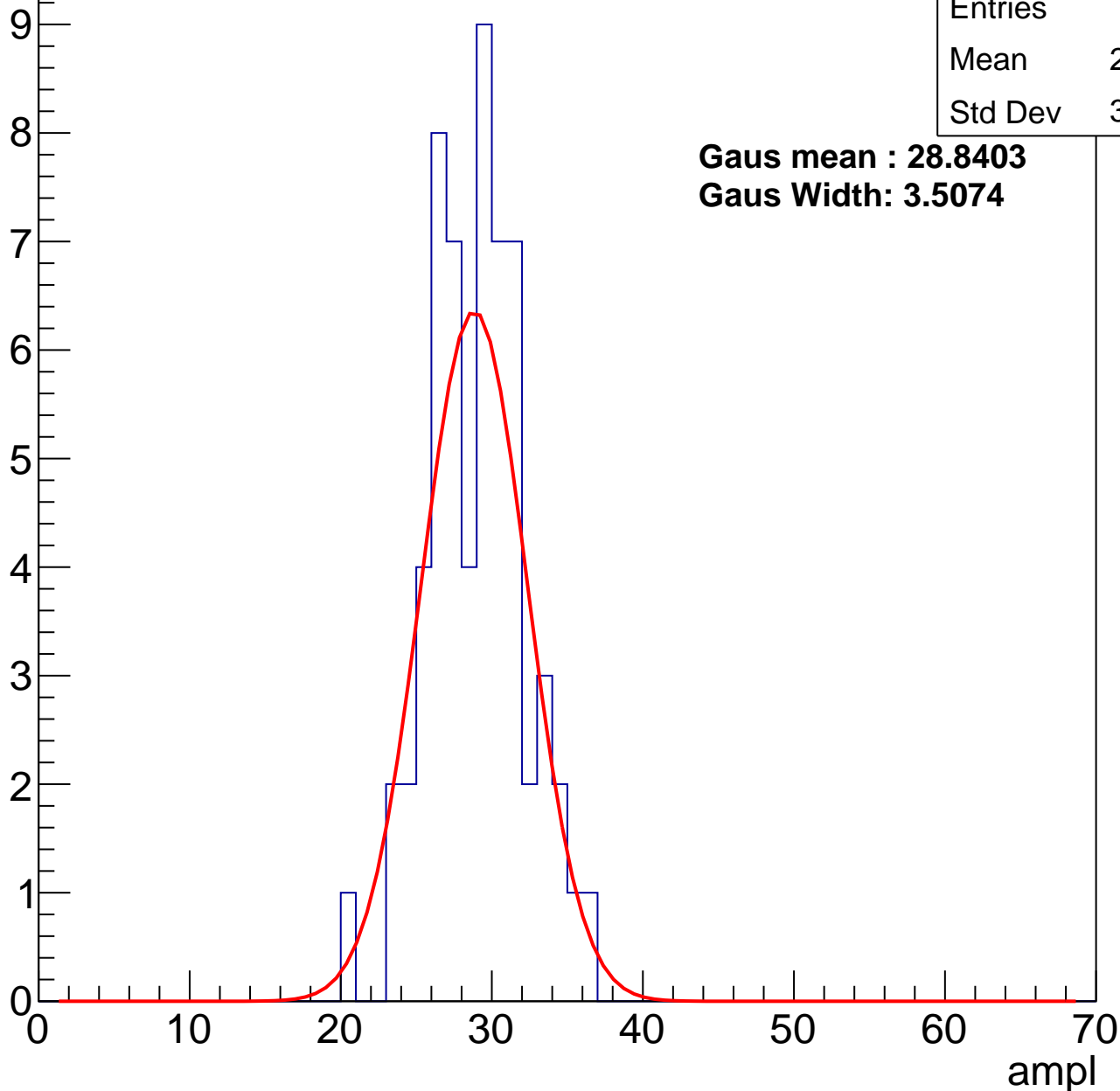
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	28.55
Std Dev	3.149

Gaus mean : 28.8403

Gaus Width: 3.5074



B0L000S, U2-ch115, adc1

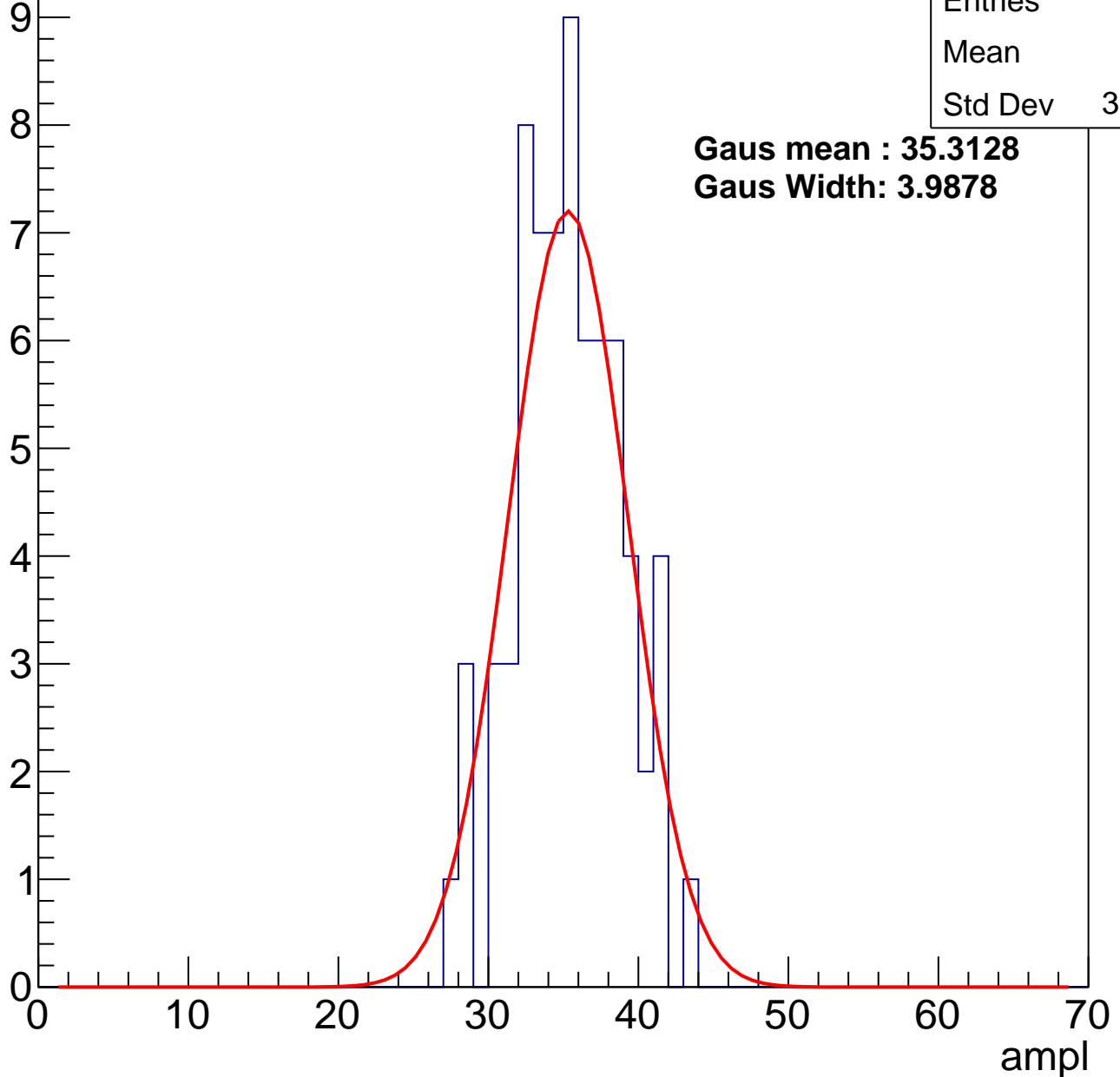
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	34.9
Std Dev	3.477

Gaus mean : 35.3128

Gaus Width: 3.9878



B0L000S, U2-ch115, adc2

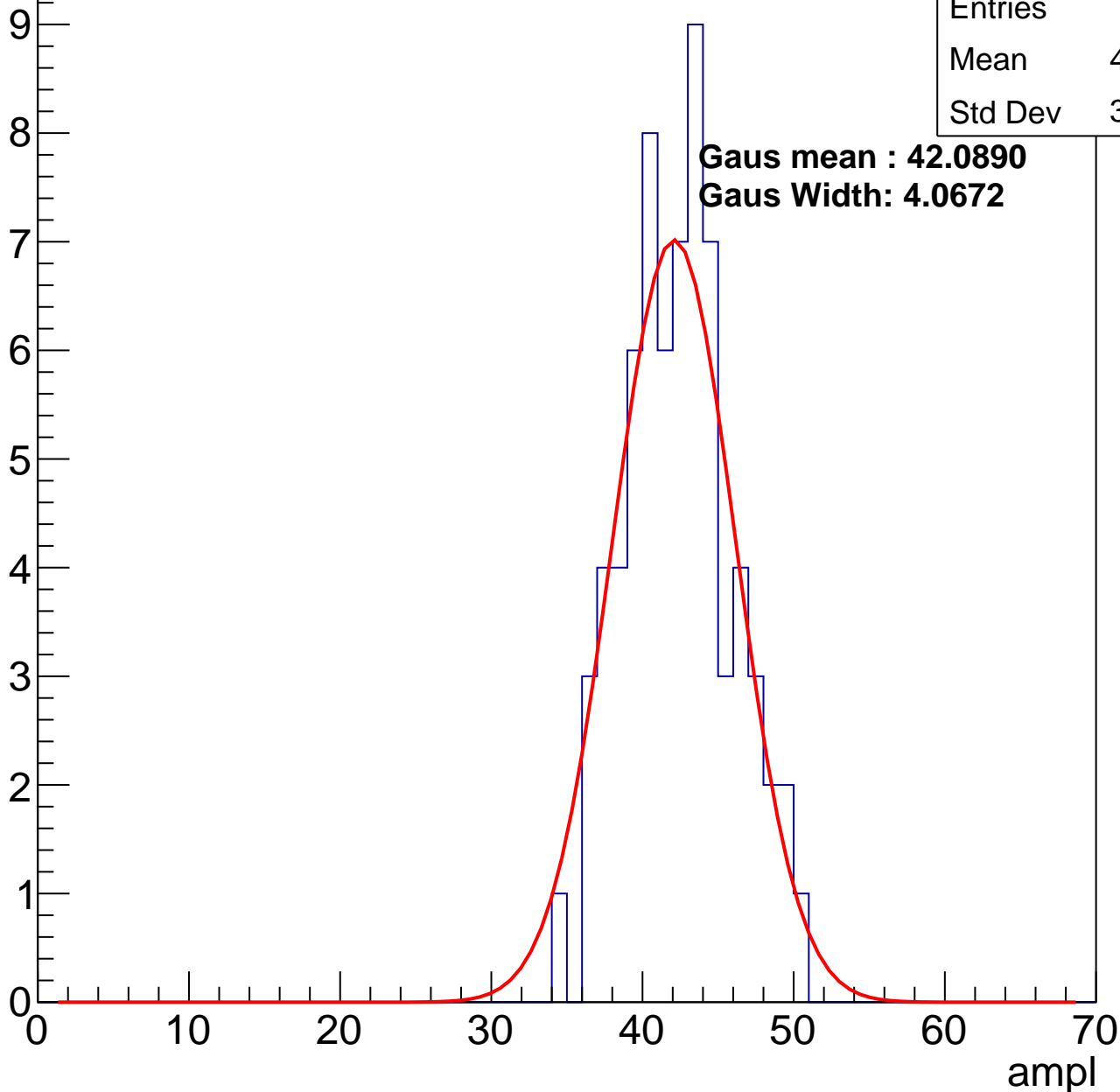
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.93
Std Dev	3.515

Gaus mean : 42.0890

Gaus Width: 4.0672

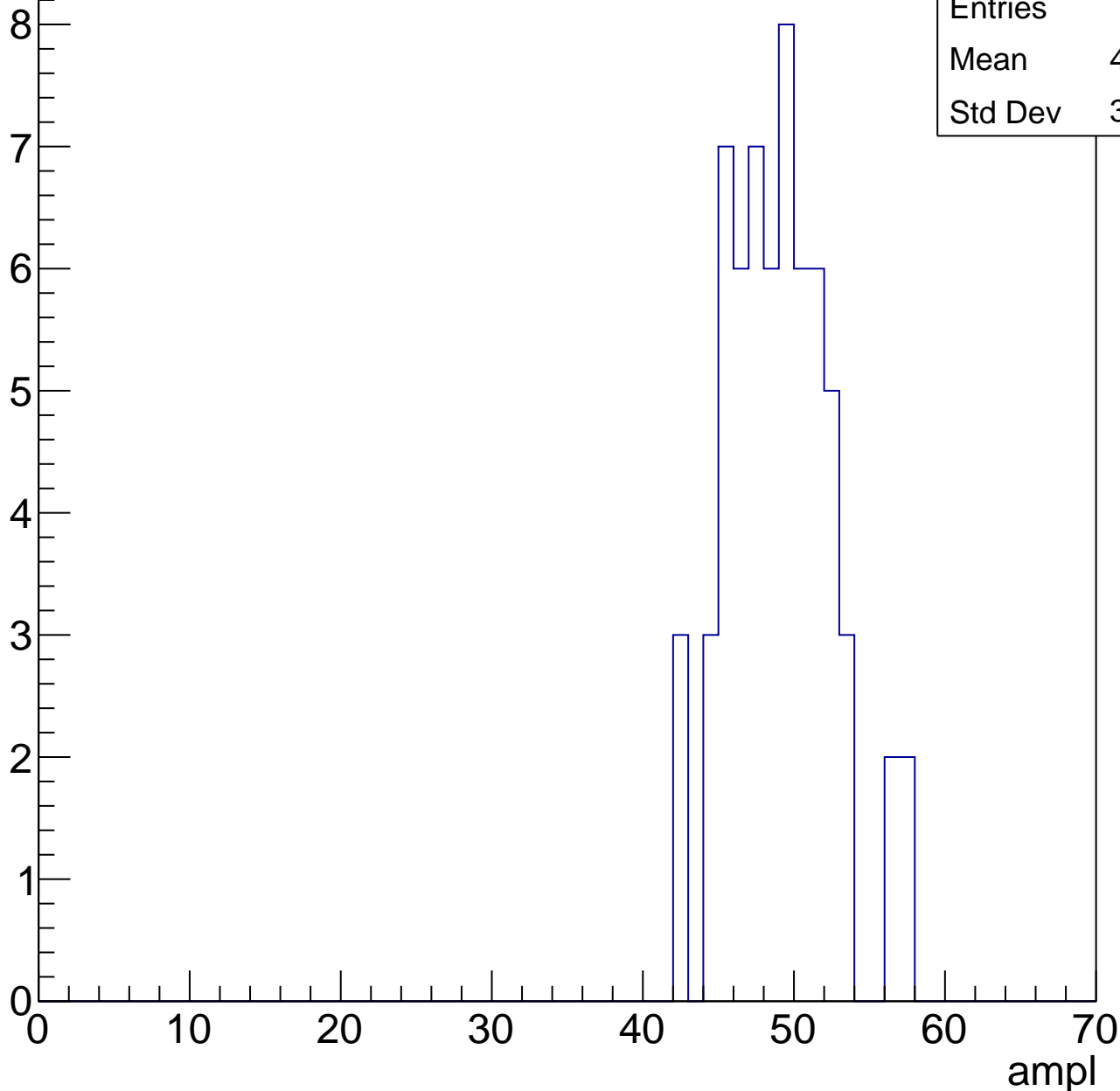


B0L000S, U2-ch115, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	48.58
Std Dev	3.445

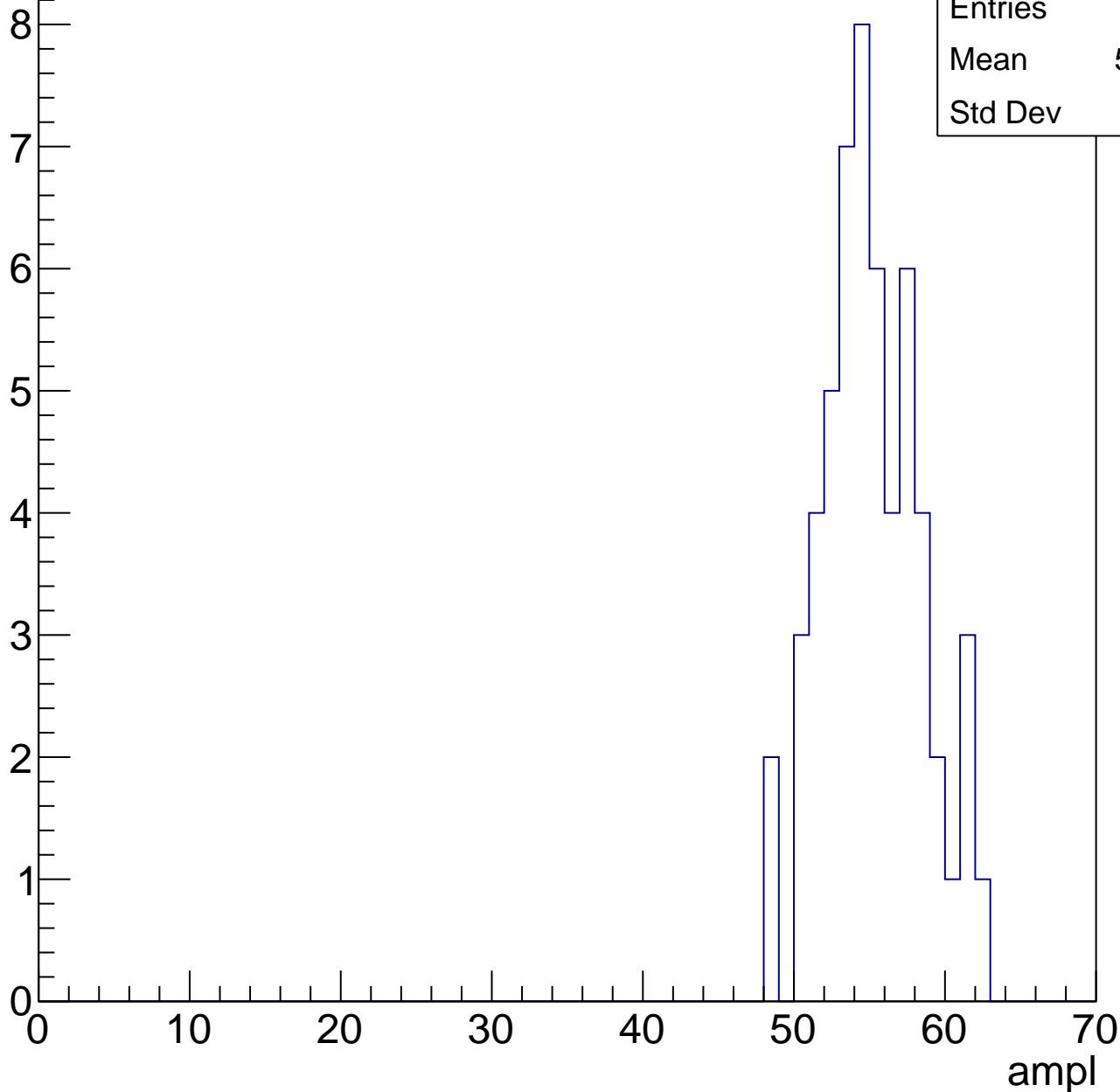


B0L000S, U2-ch115, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	54.71
Std Dev	3.25



B0L000S, U2-ch115, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

10

Entries 55

Mean 58.42

Std Dev 8.377

8

6

4

2

0

0

10

20

30

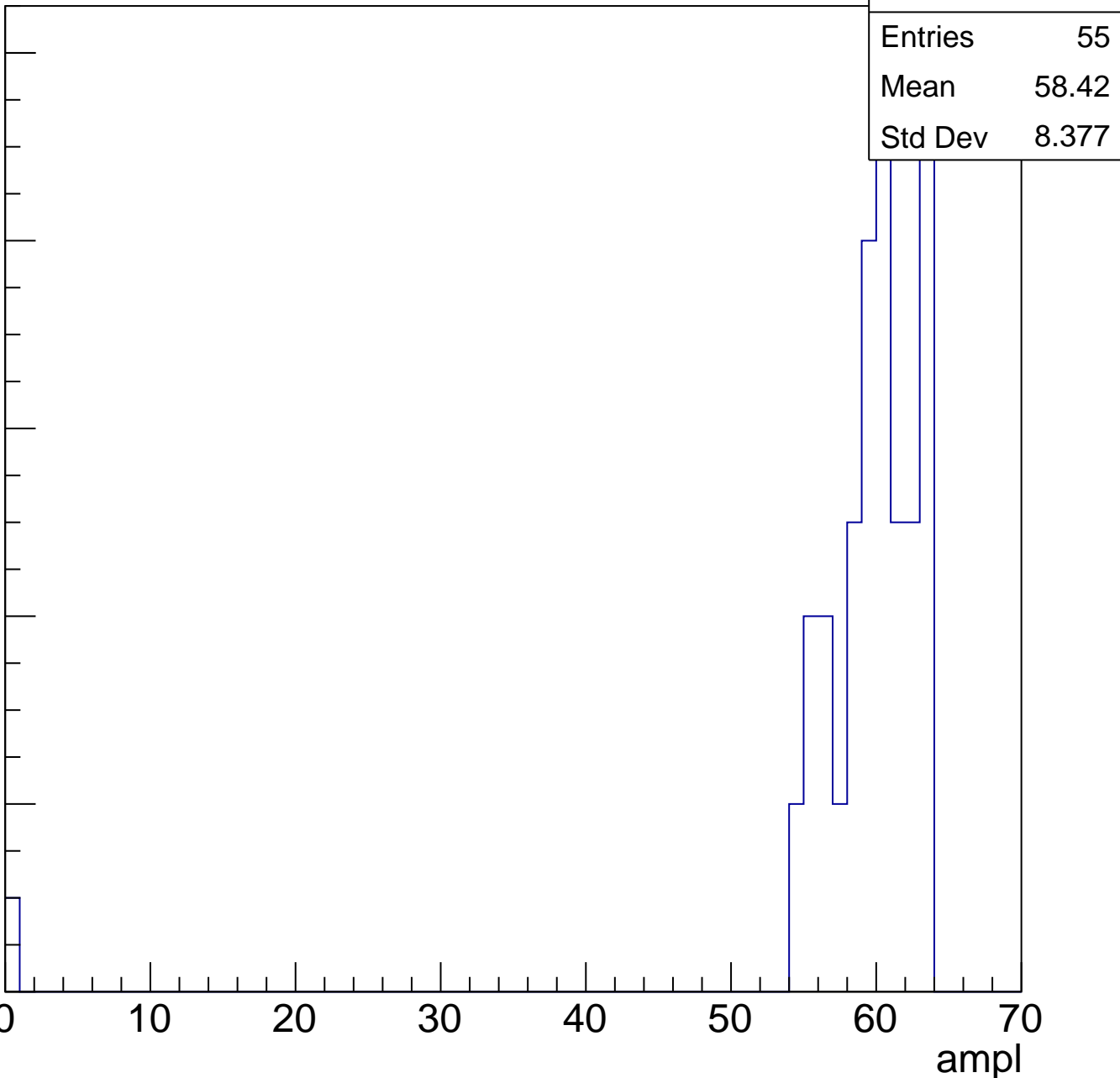
40

50

60

70

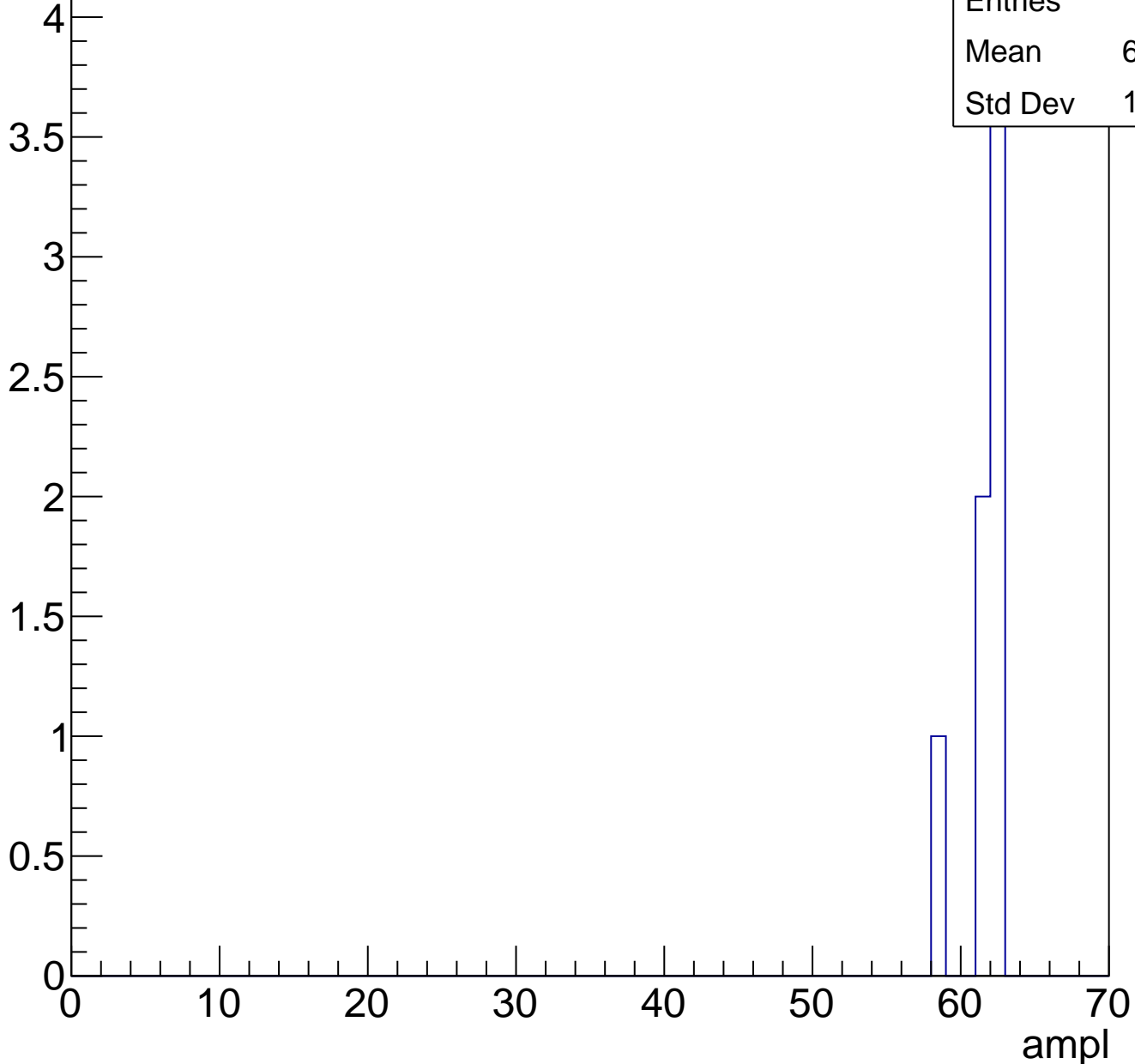
ampl



B0L000S, U2-ch115, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	7
Mean	61.14
Std Dev	1.355

B0L000S, U2-ch115, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch116, adc0

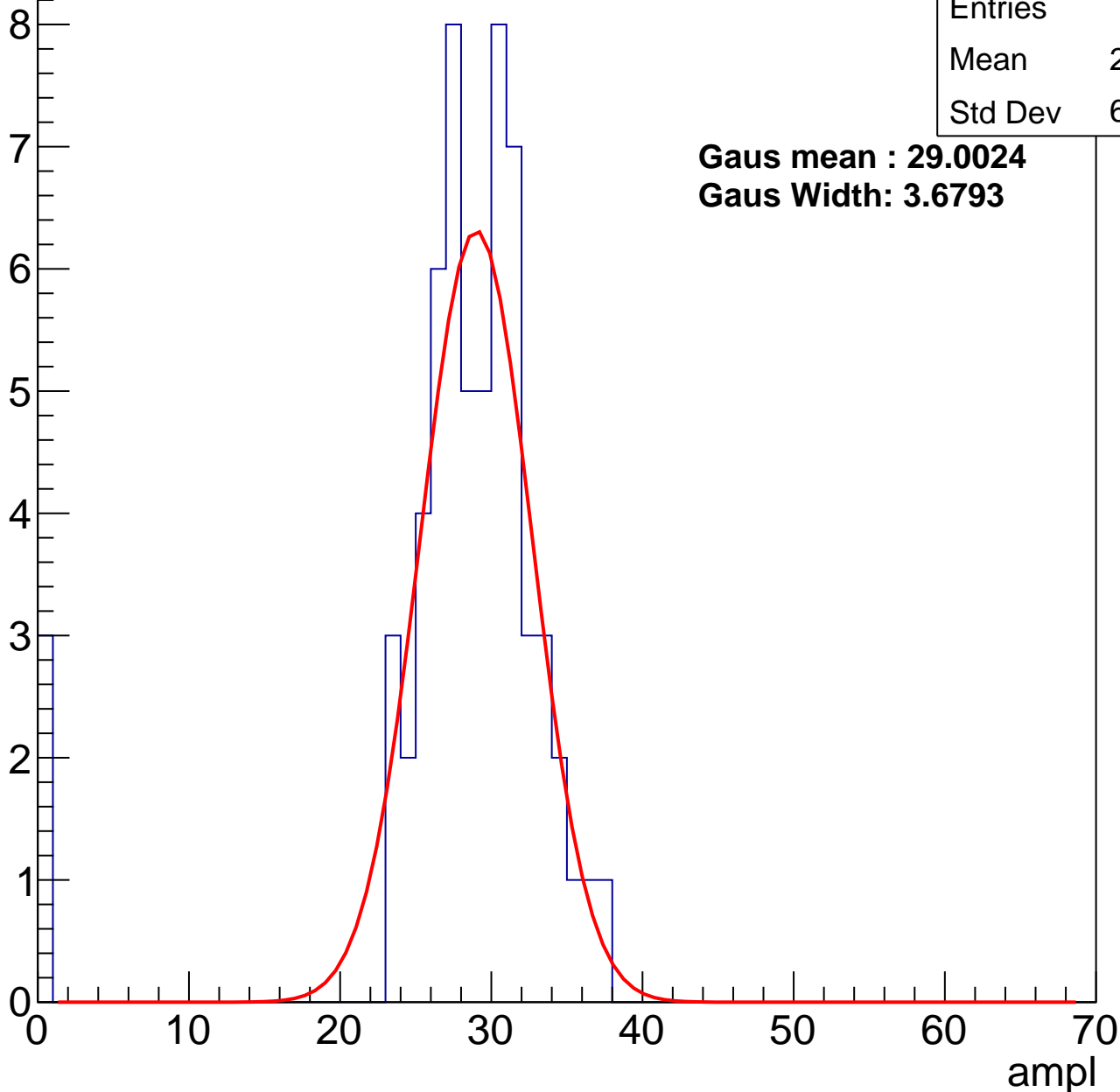
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	27.45
Std Dev	6.953

Gaus mean : 29.0024

Gaus Width: 3.6793



B0L000S, U2-ch116, adc1

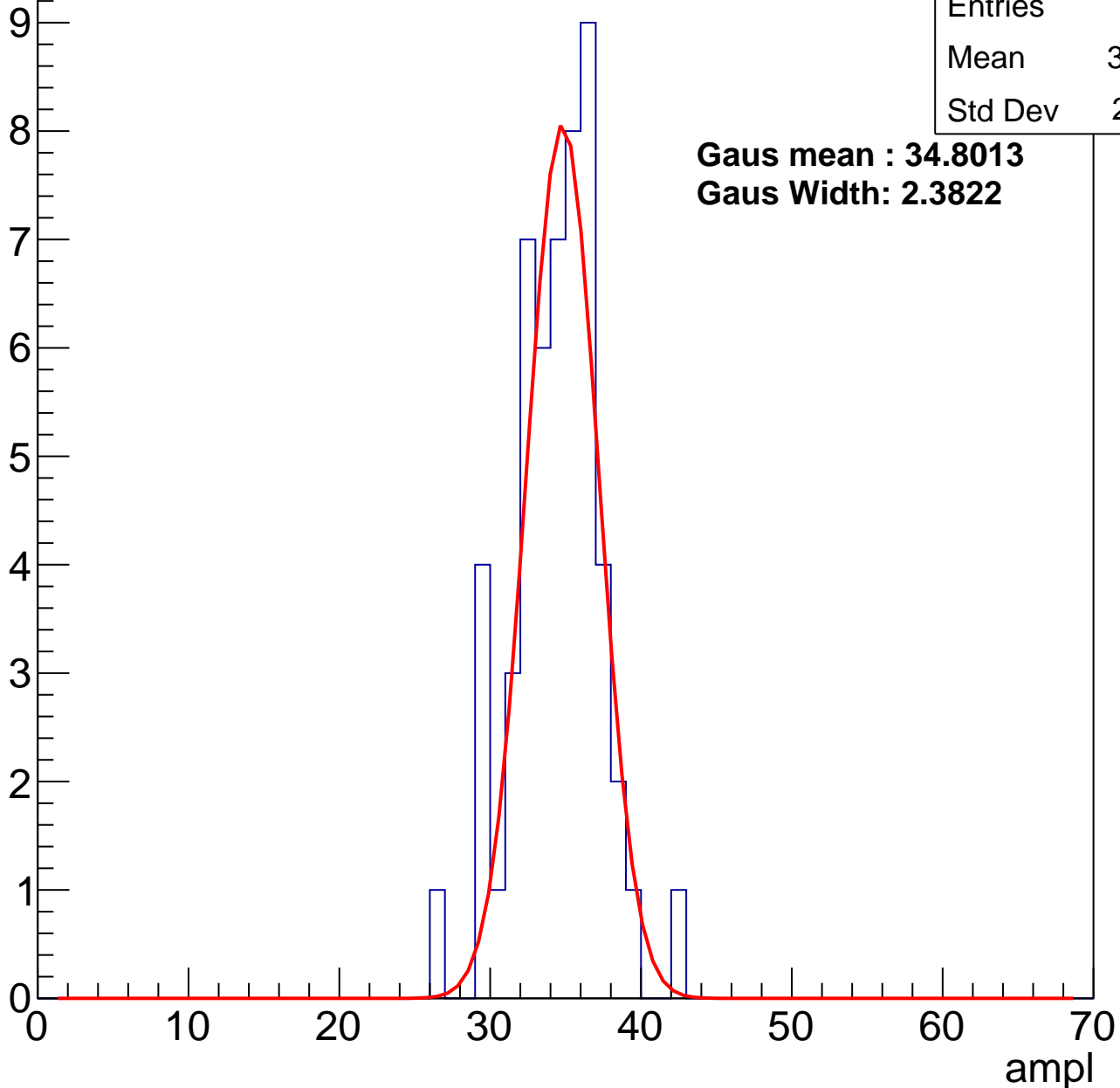
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	33.96
Std Dev	2.861

Gaus mean : 34.8013

Gaus Width: 2.3822



B0L000S, U2-ch116, adc2

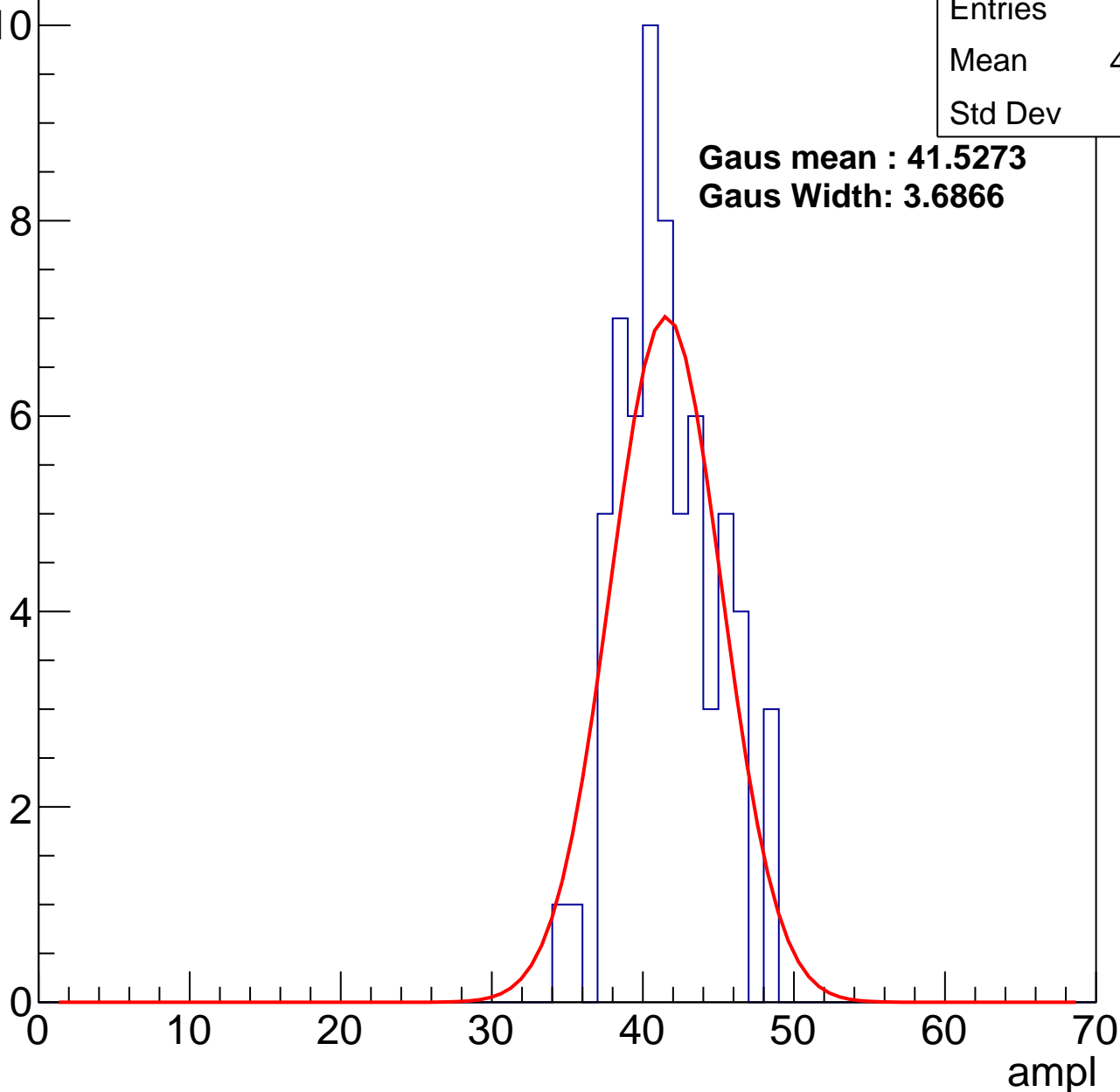
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.17
Std Dev	3.17

Gaus mean : 41.5273

Gaus Width: 3.6866



B0L000S, U2-ch116, adc3

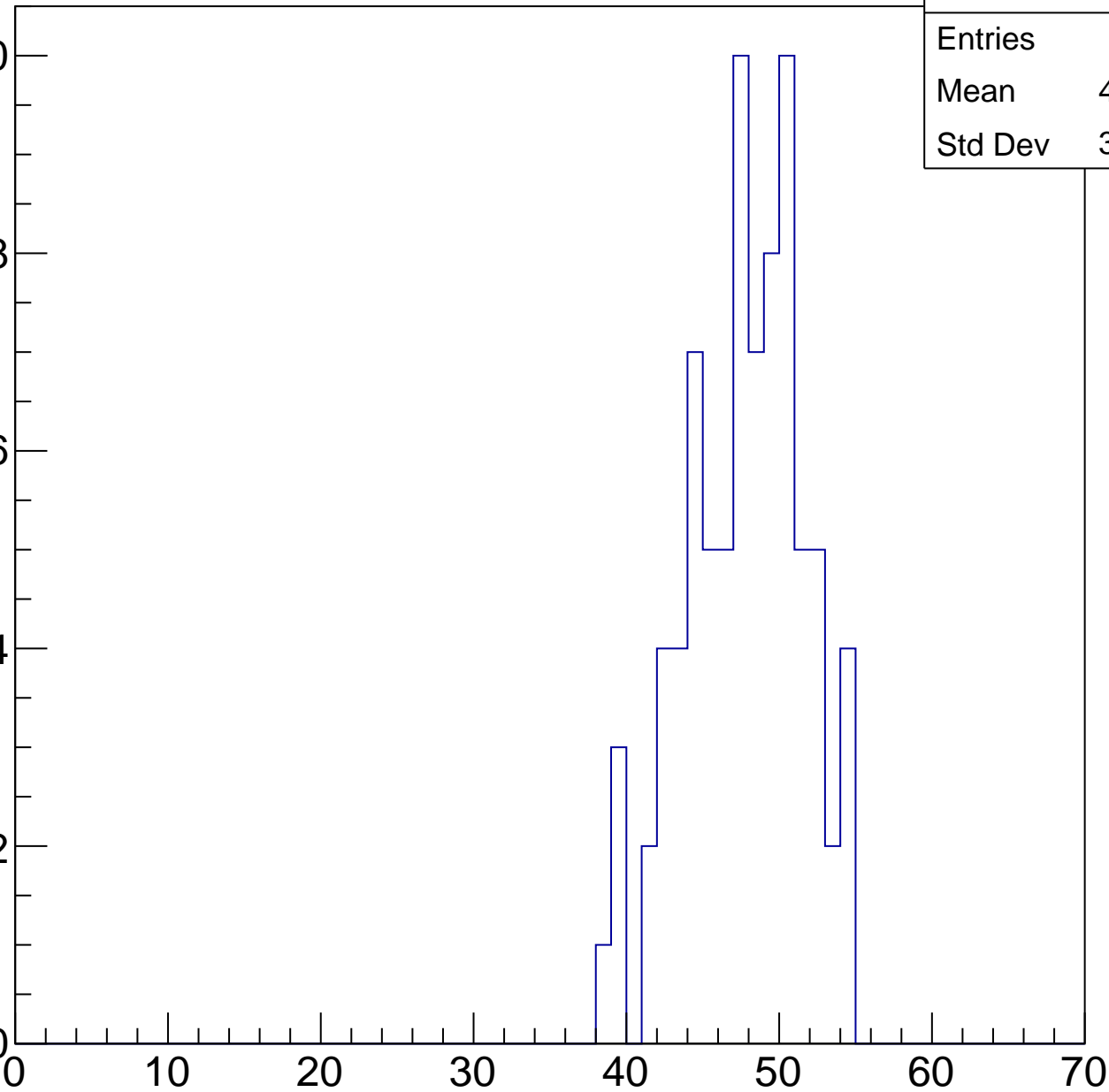
calib_packv5_042523_0143.root, FC#5, port B1

Entries	82
Mean	47.26
Std Dev	3.815

Entry

10
8
6
4
2
0

ampl

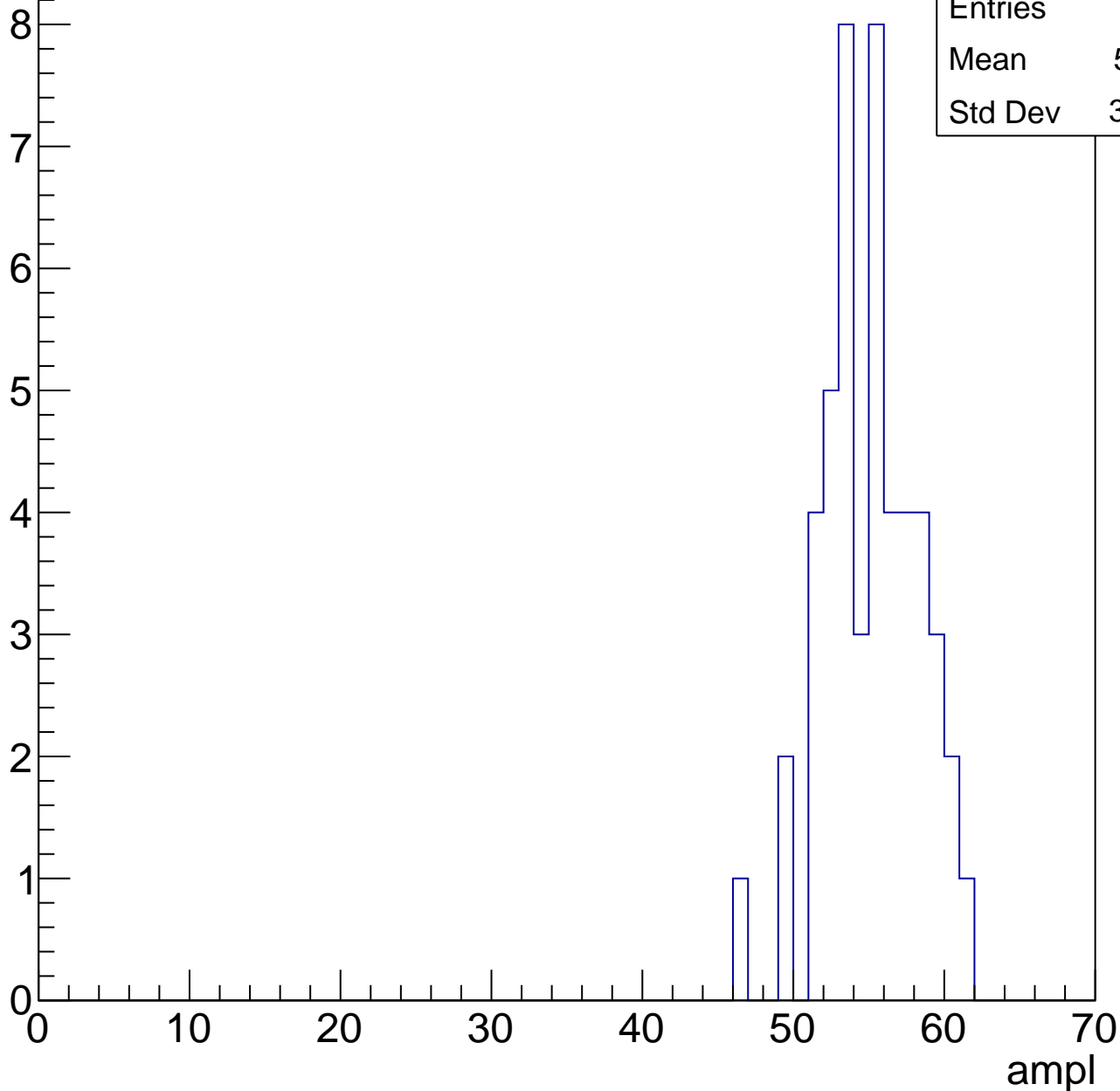


B0L000S, U2-ch116, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	54.61
Std Dev	3.129

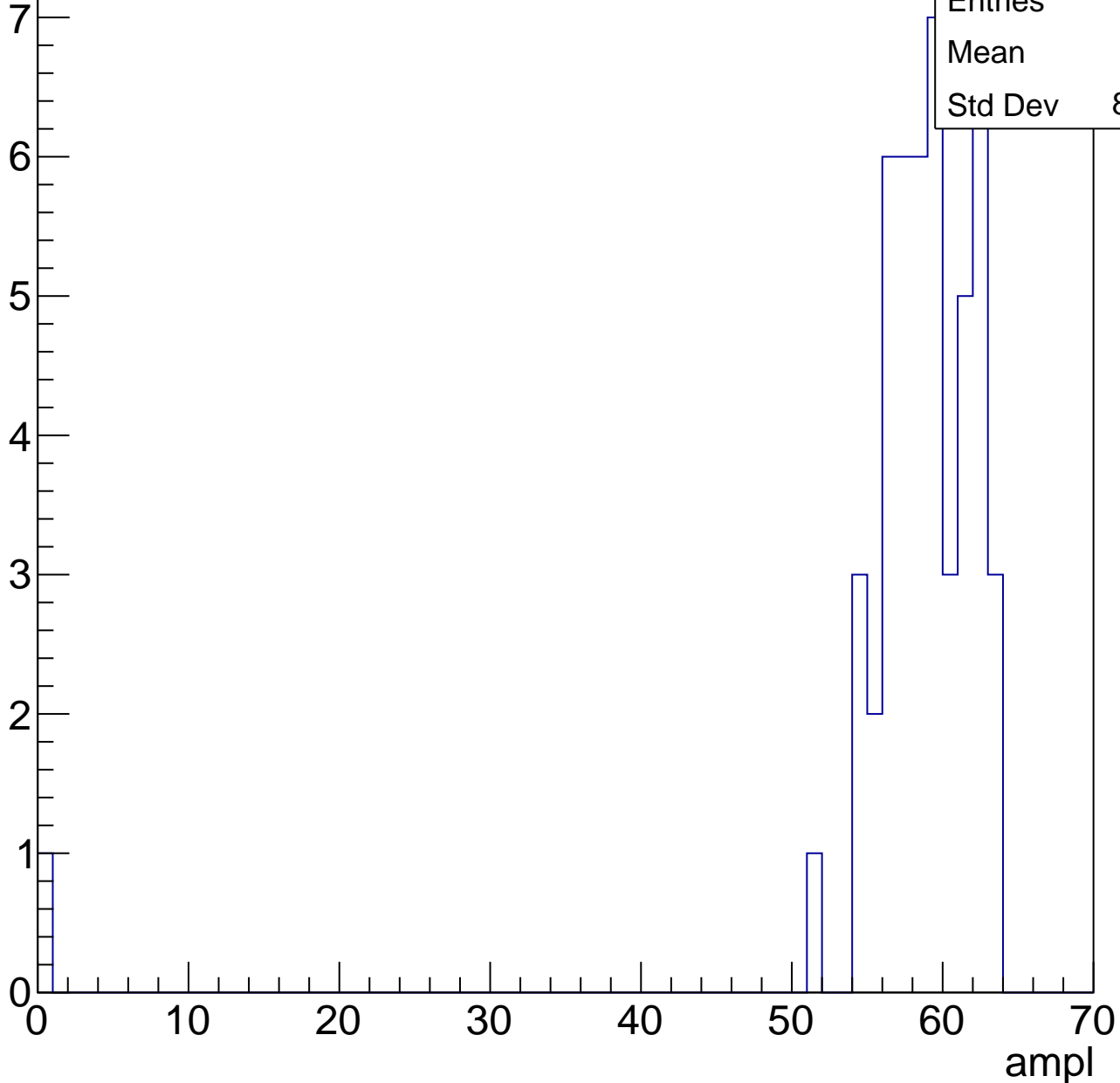


B0L000S, U2-ch116, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

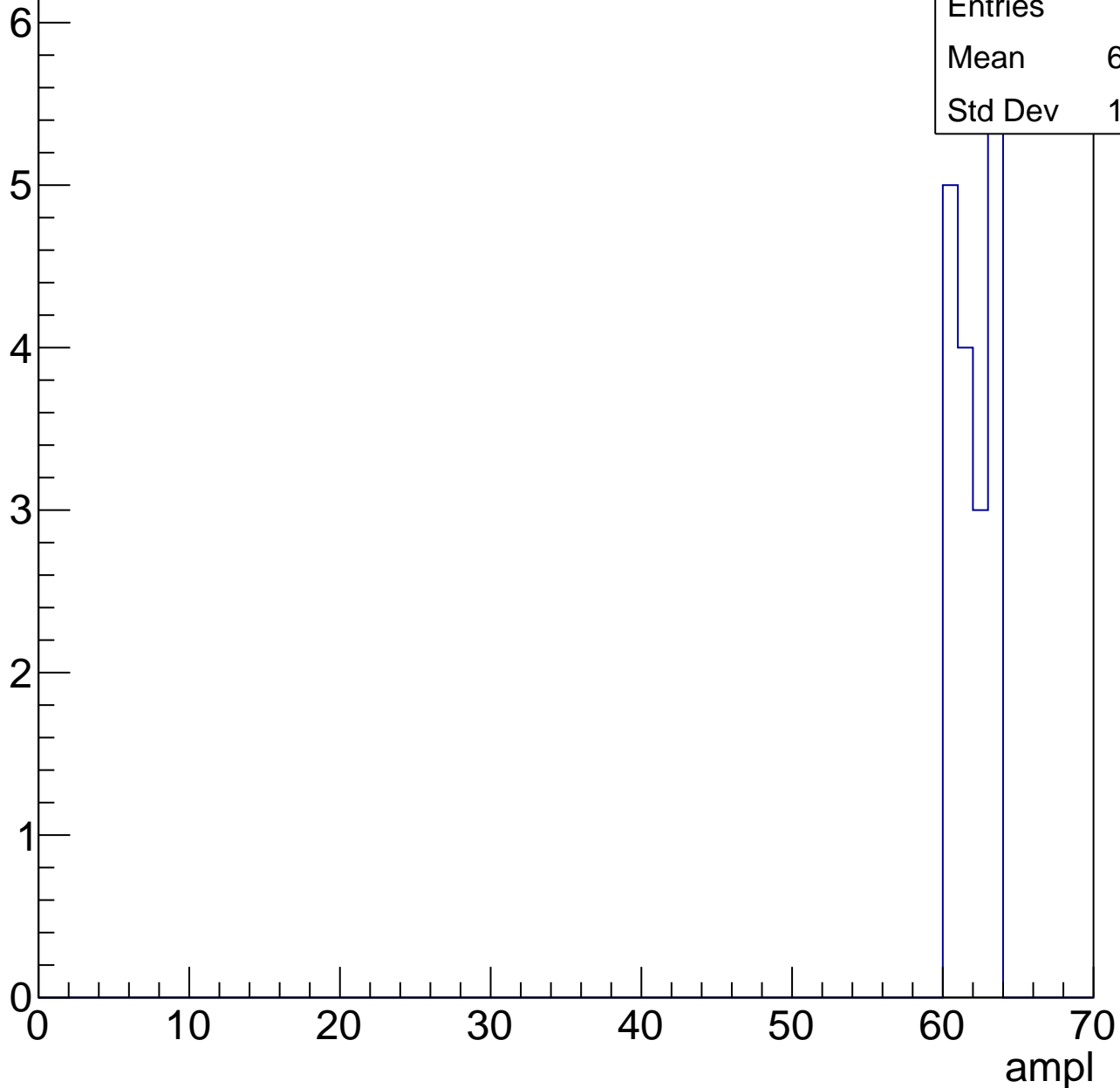
Entries	50
Mean	57.4
Std Dev	8.651



B0L000S, U2-ch116, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch116, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L000S, U2-ch117, adc0

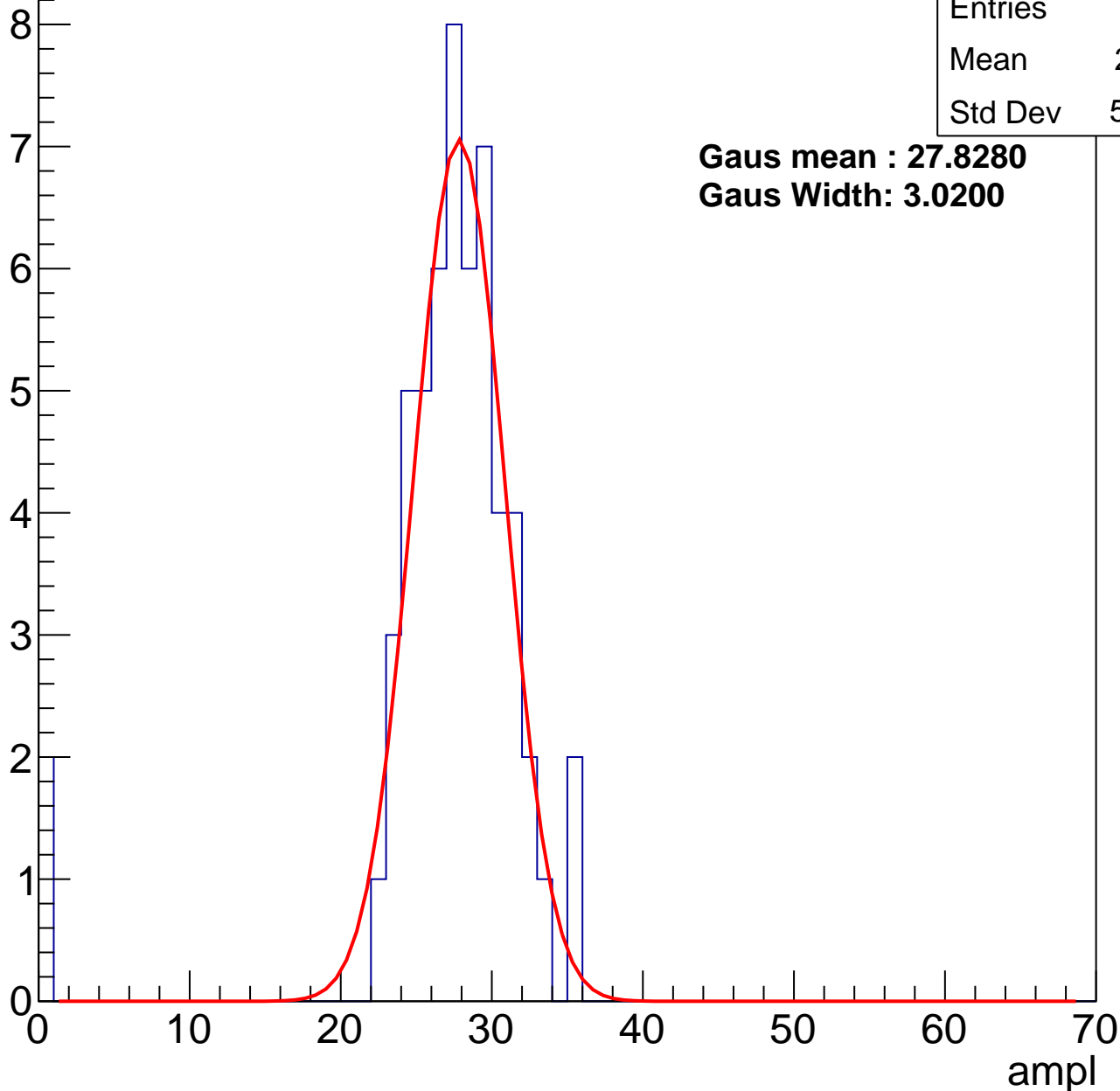
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	56
Mean	26.61
Std Dev	5.888

Gaus mean : 27.8280

Gaus Width: 3.0200



B0L000S, U2-ch117, adc1

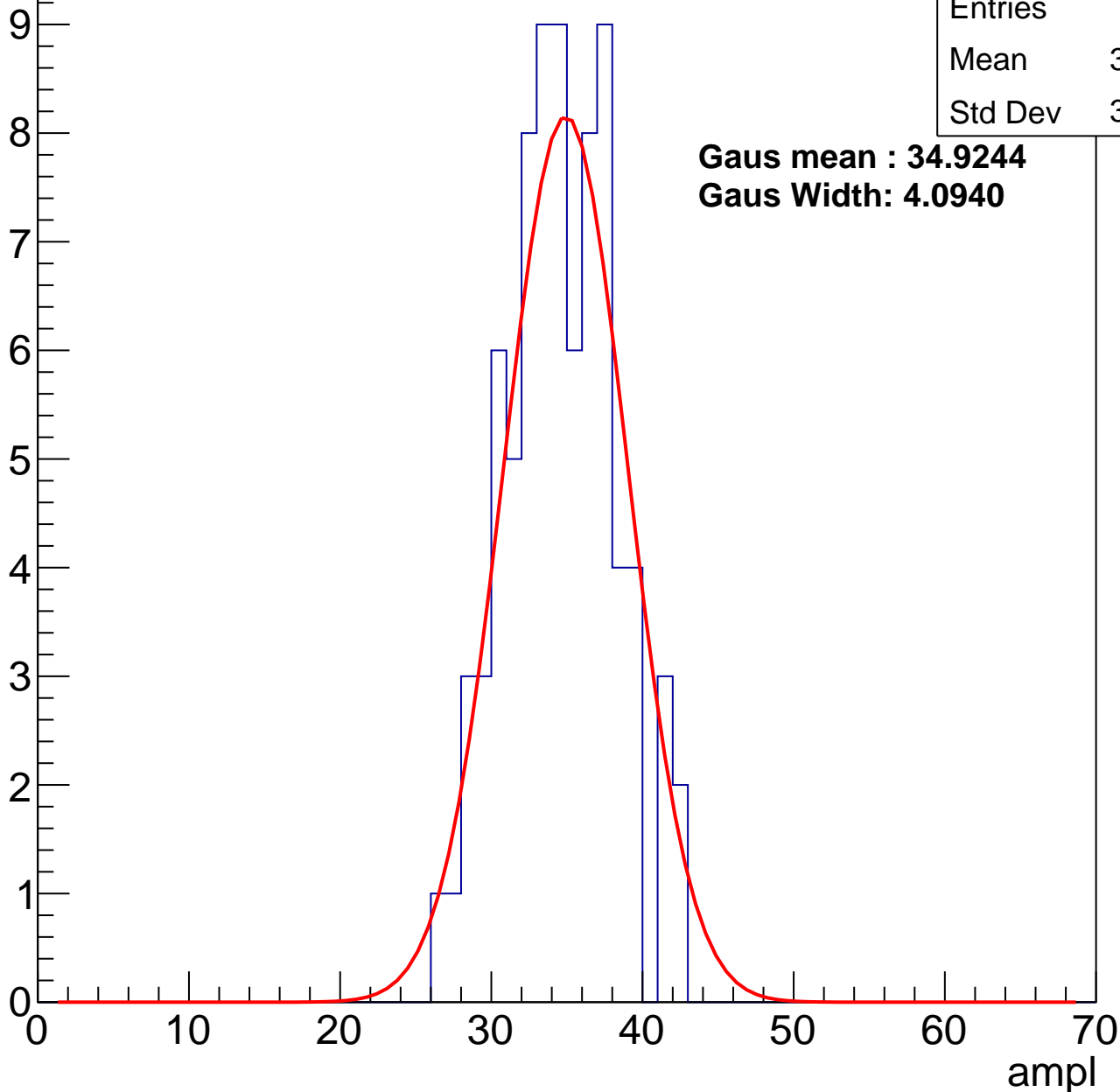
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	81
Mean	34.12
Std Dev	3.574

Gaus mean : 34.9244

Gaus Width: 4.0940



B0L000S, U2-ch117, adc2

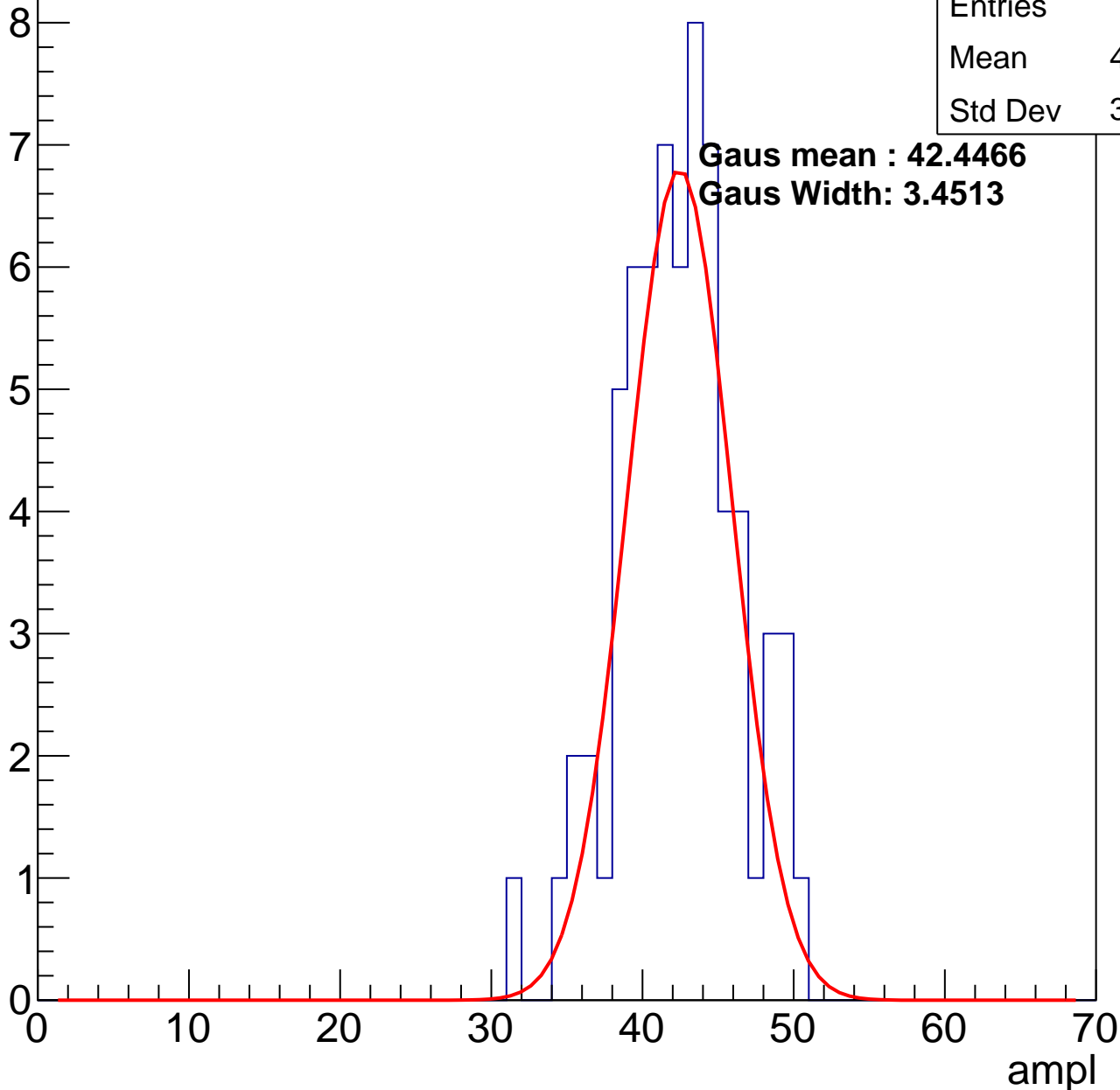
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	41.93
Std Dev	3.882

Gaus mean : 42.4466

Gaus Width: 3.4513

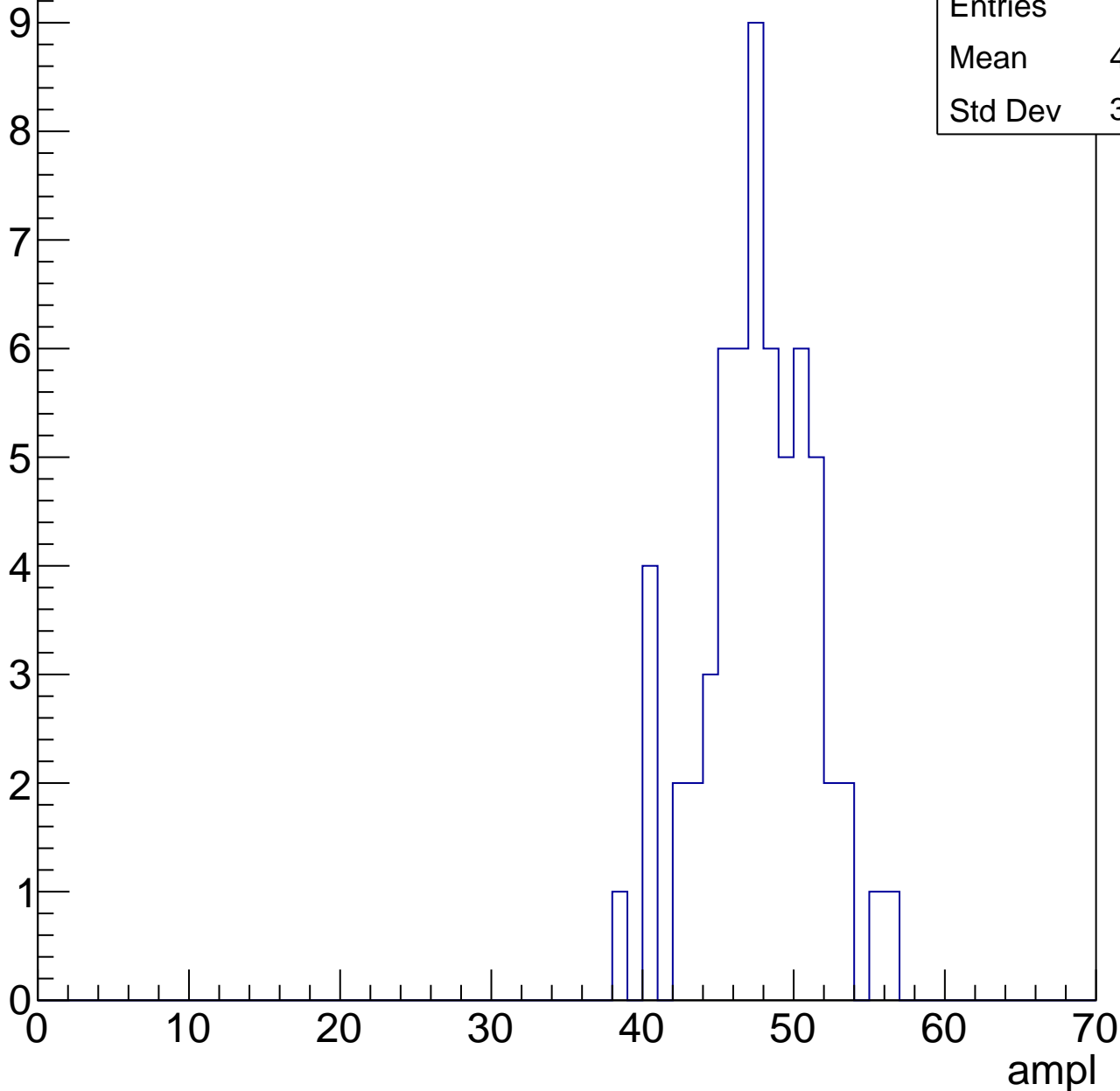


B0L000S, U2-ch117, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

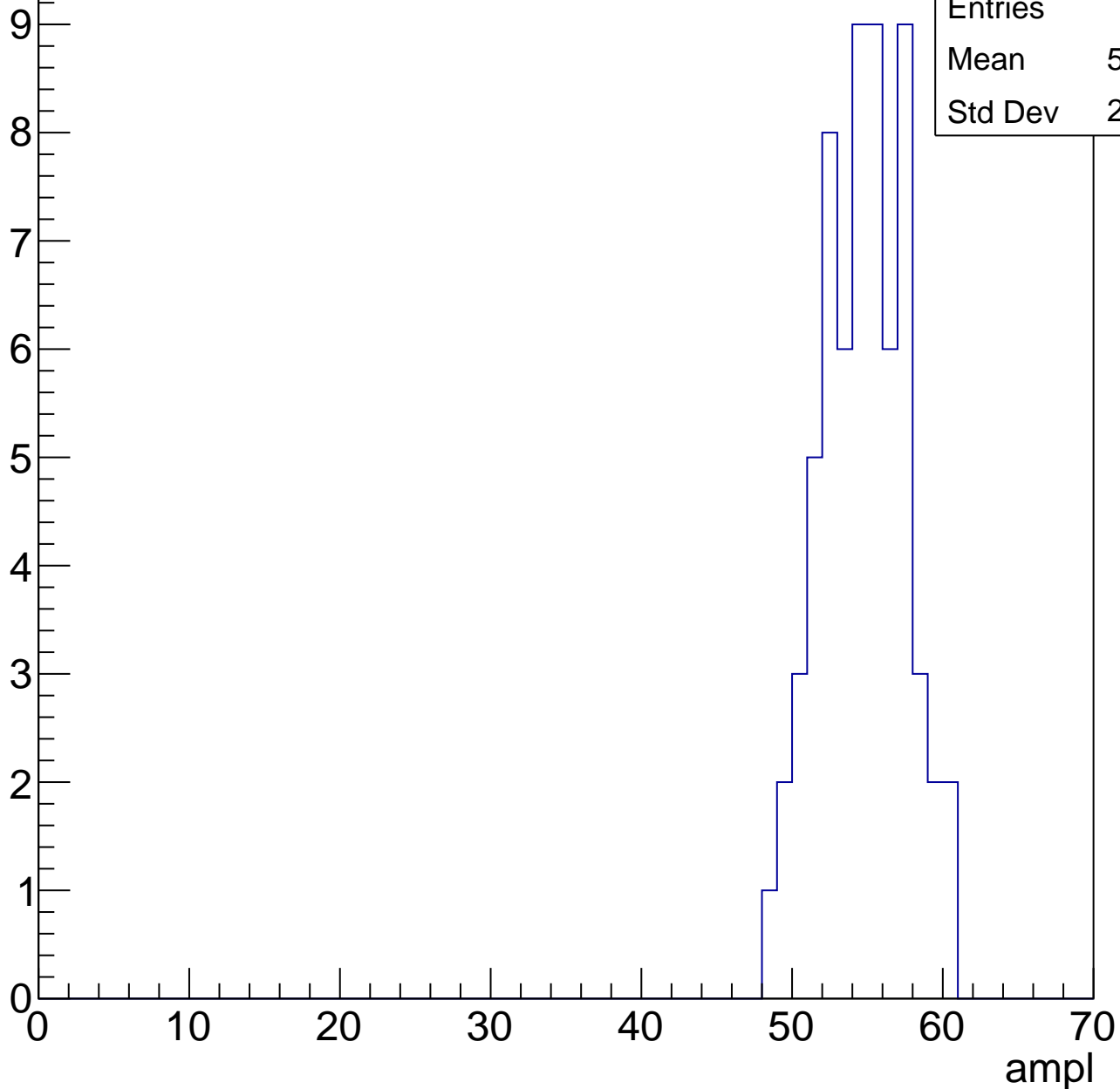
Entries	61
Mean	47.18
Std Dev	3.713



B0L000S, U2-ch117, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



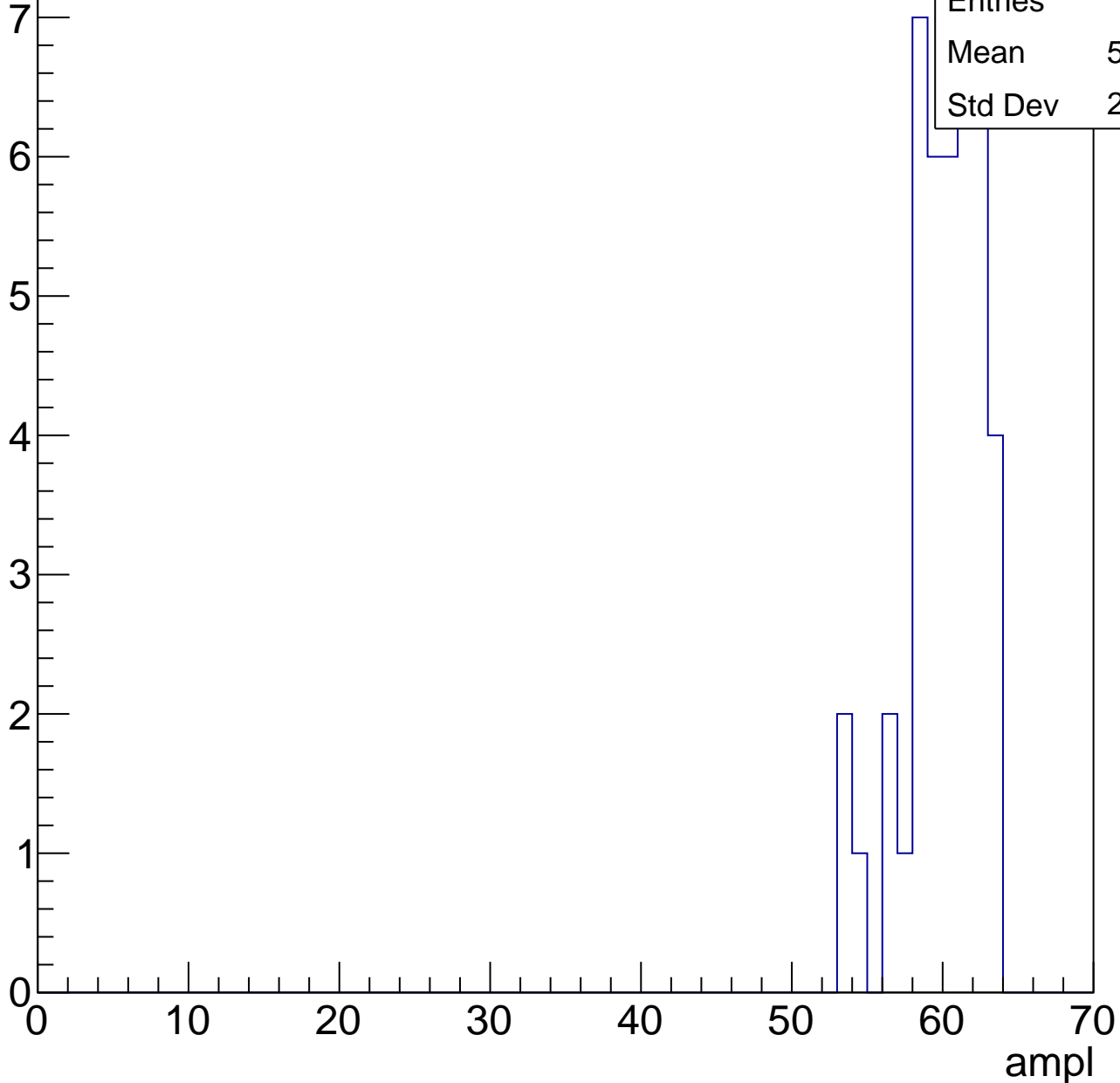
Entries	65
Mean	54.26
Std Dev	2.764

B0L000S, U2-ch117, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	43
Mean	59.58
Std Dev	2.517

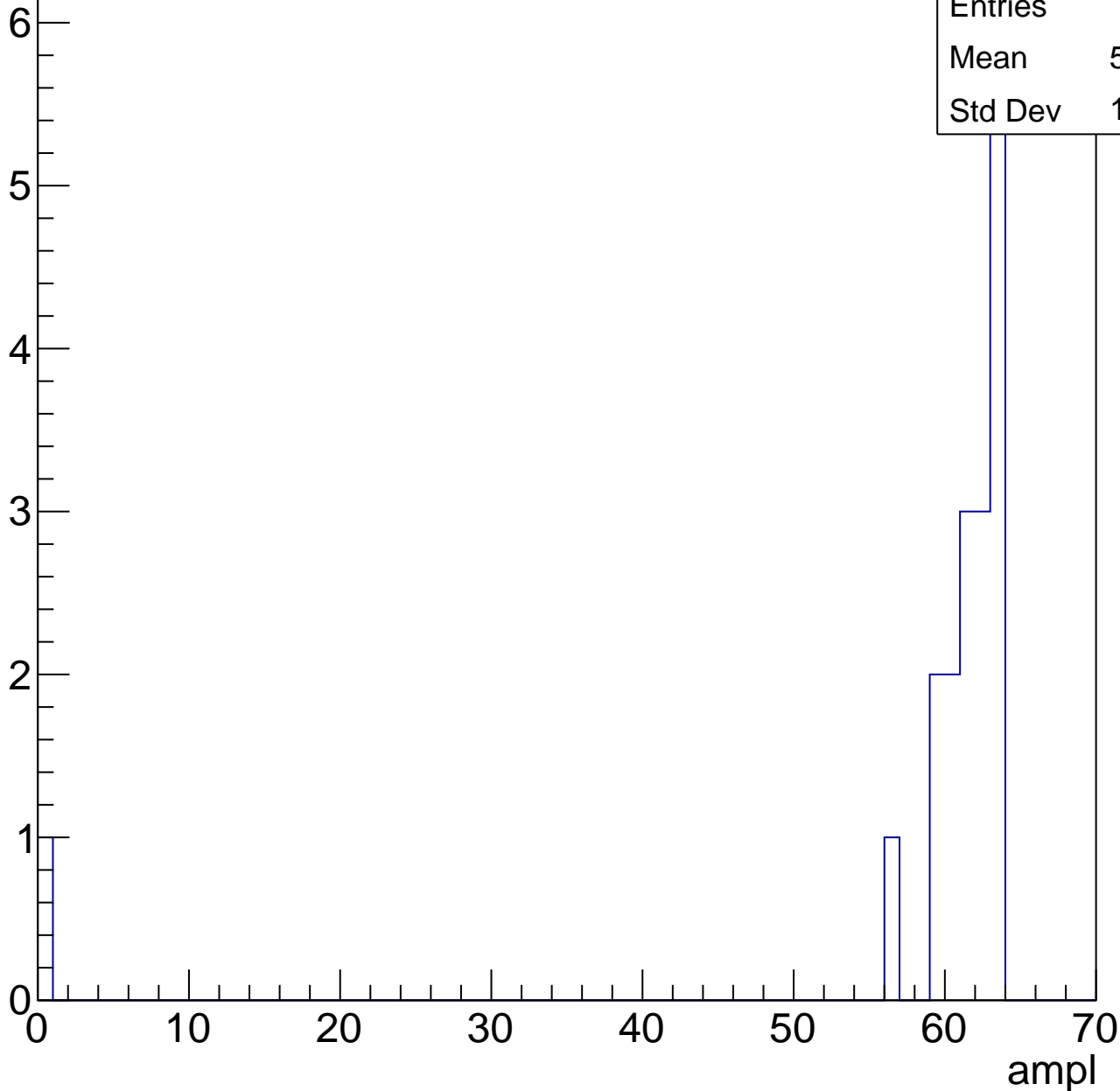


B0L000S, U2-ch117, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	18
Mean	57.83
Std Dev	14.15



B0L000S, U2-ch117, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L000S, U2-ch118, adc0

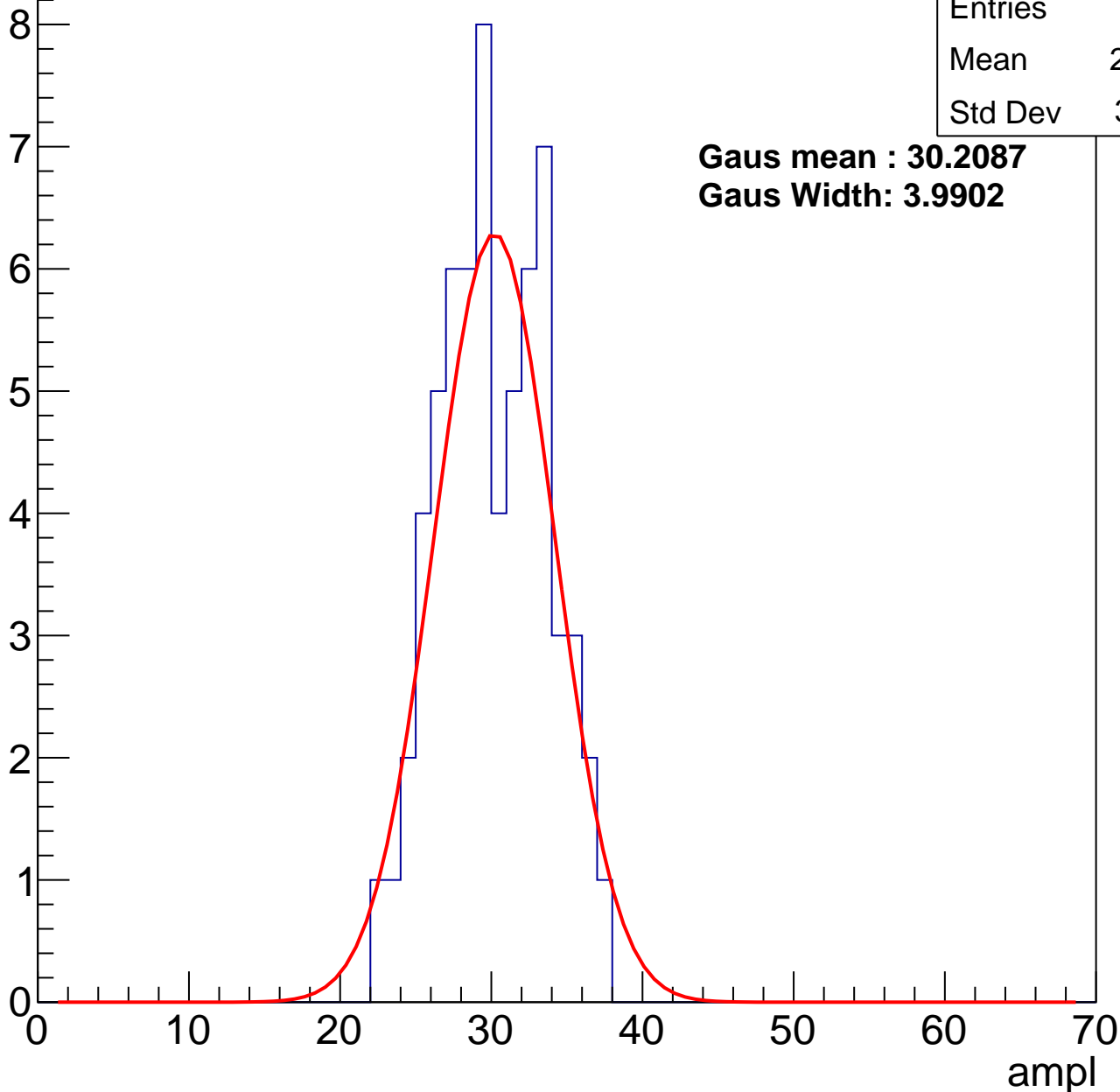
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.67
Std Dev	3.491

Gaus mean : 30.2087

Gaus Width: 3.9902



B0L000S, U2-ch118, adc1

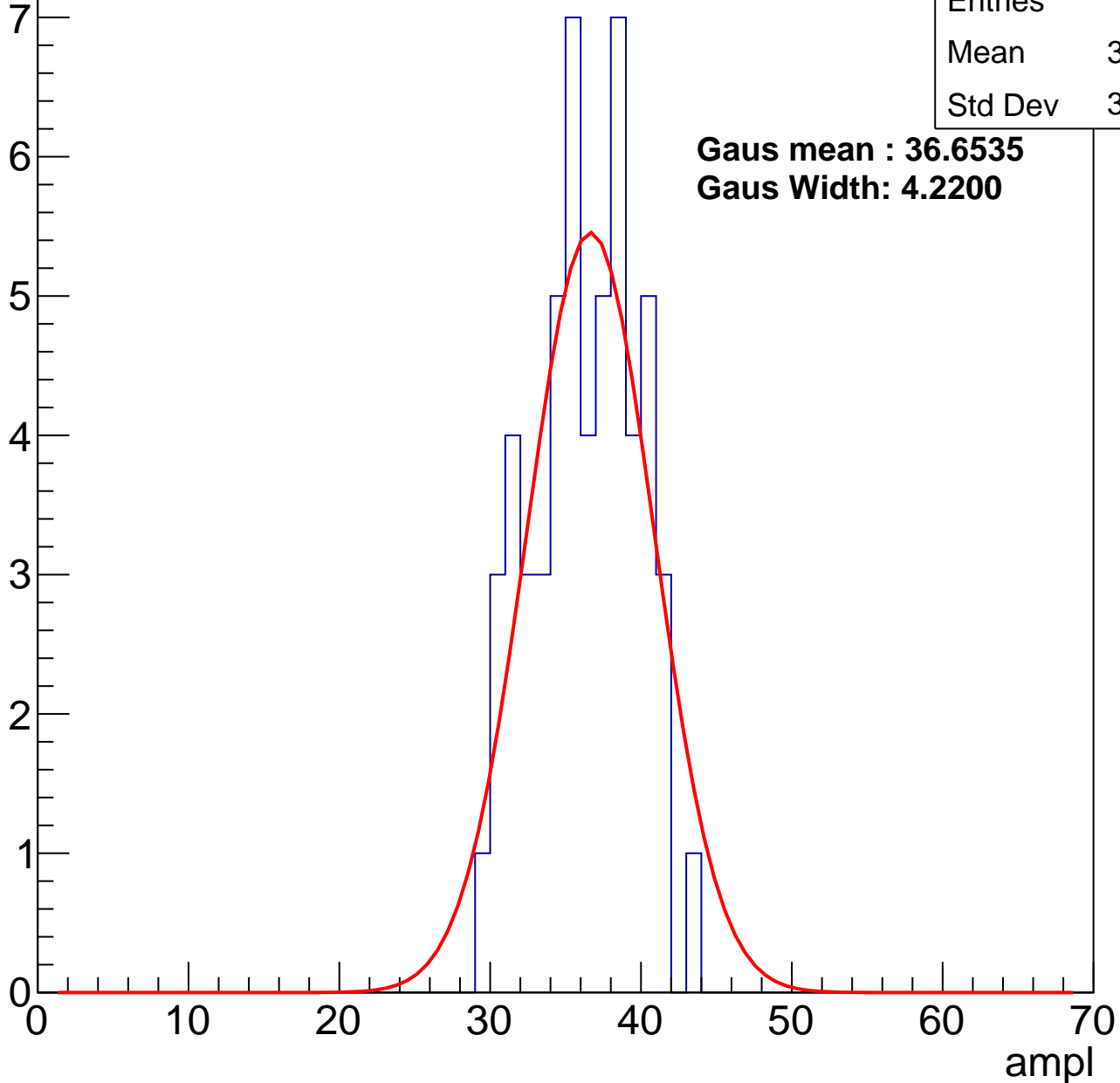
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	35.82
Std Dev	3.385

Gaus mean : 36.6535

Gaus Width: 4.2200



B0L000S, U2-ch118, adc2

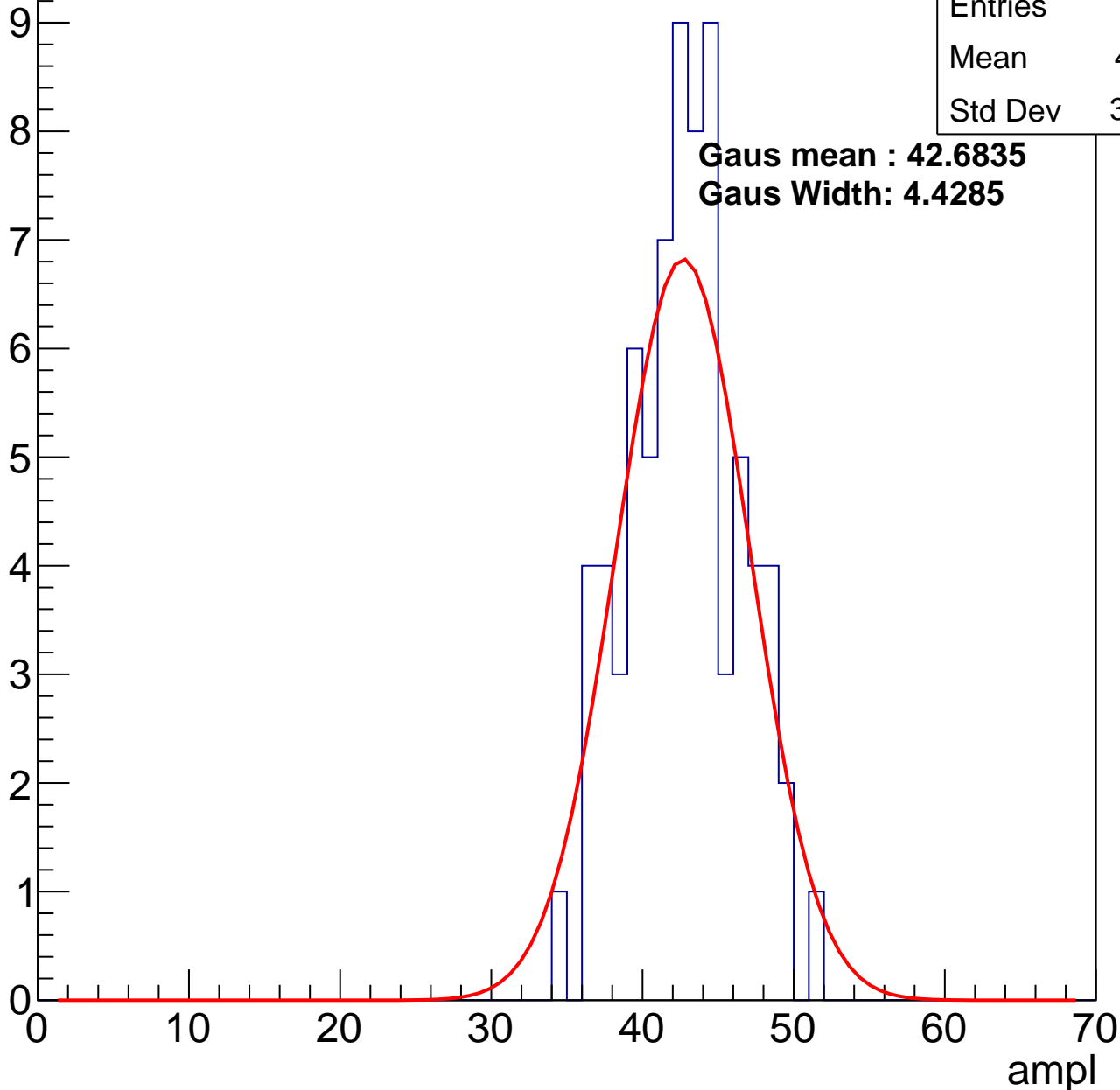
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	75
Mean	42.31
Std Dev	3.659

Gaus mean : 42.6835

Gaus Width: 4.4285

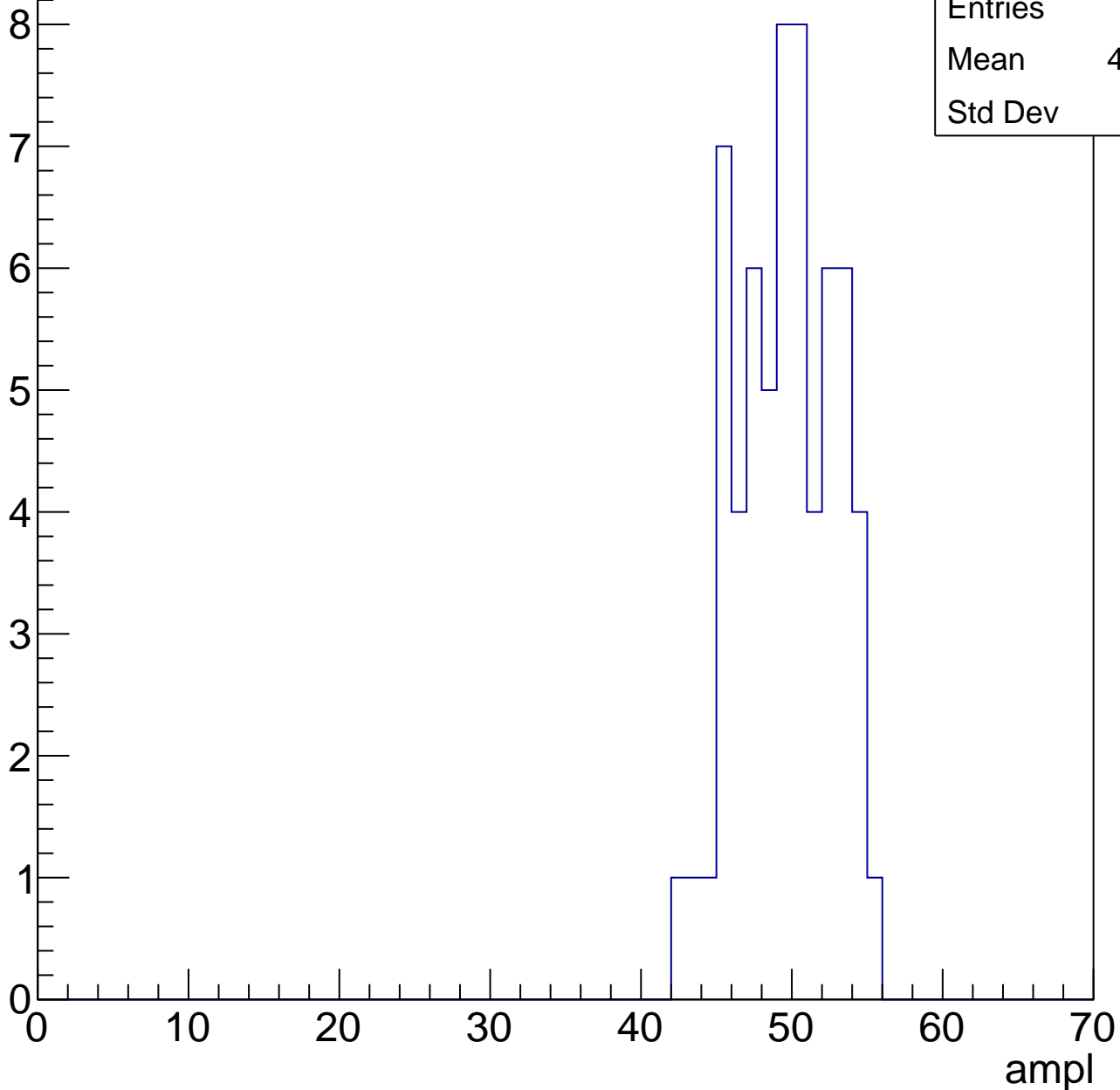


B0L000S, U2-ch118, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	62
Mean	49.15
Std Dev	3.1

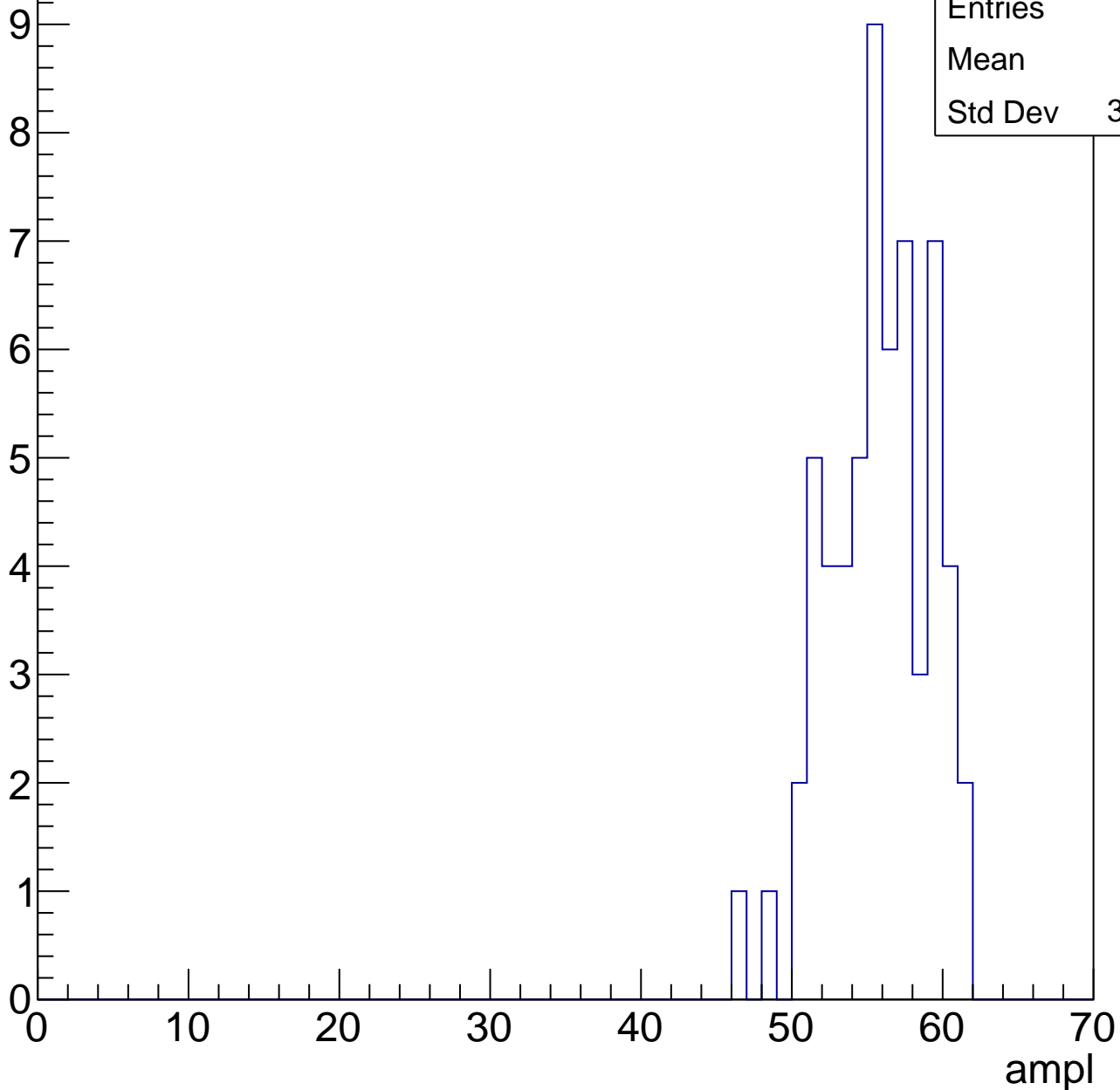


B0L000S, U2-ch118, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	55.3
Std Dev	3.308

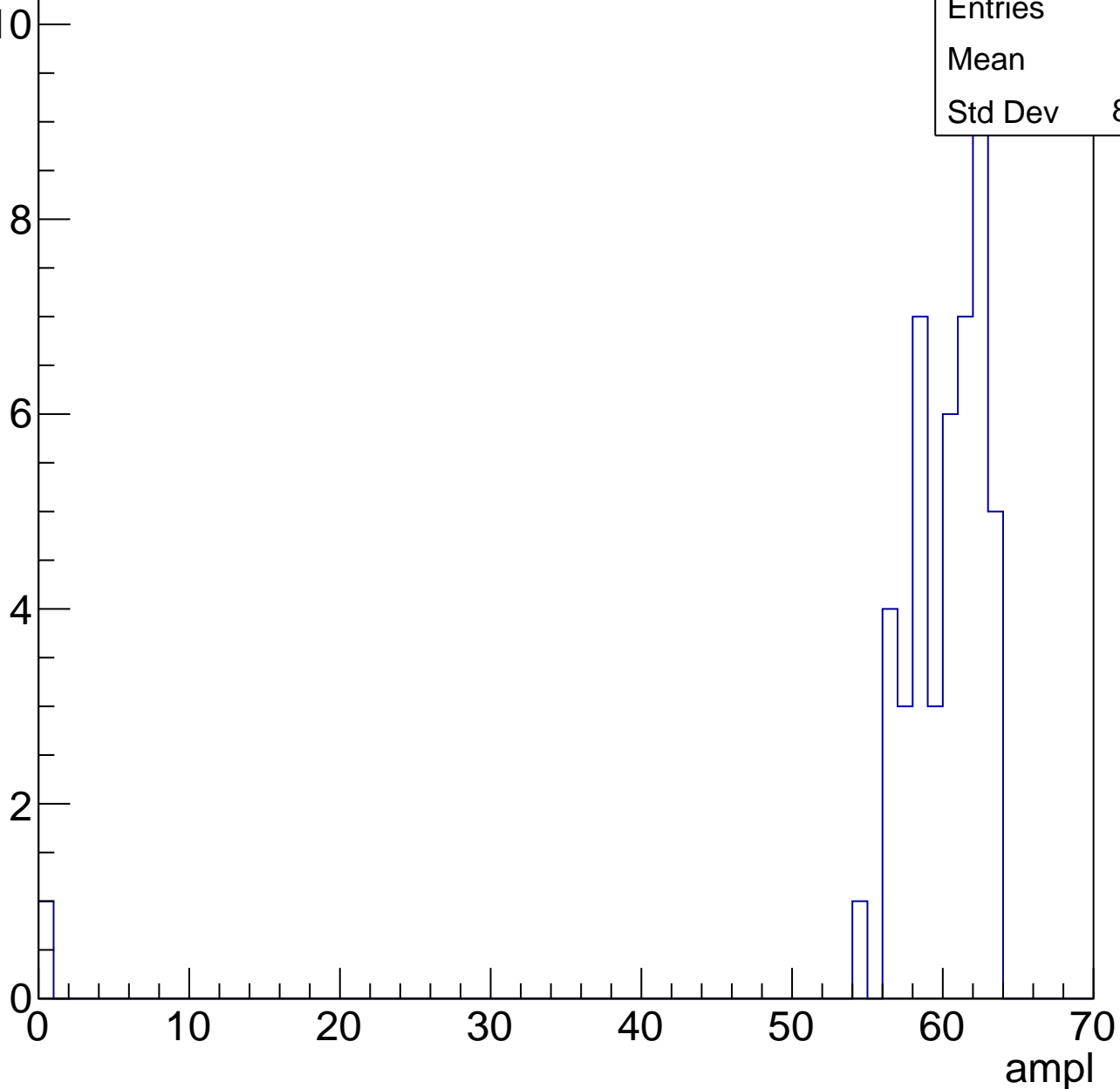


B0L000S, U2-ch118, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	58.6
Std Dev	8.941

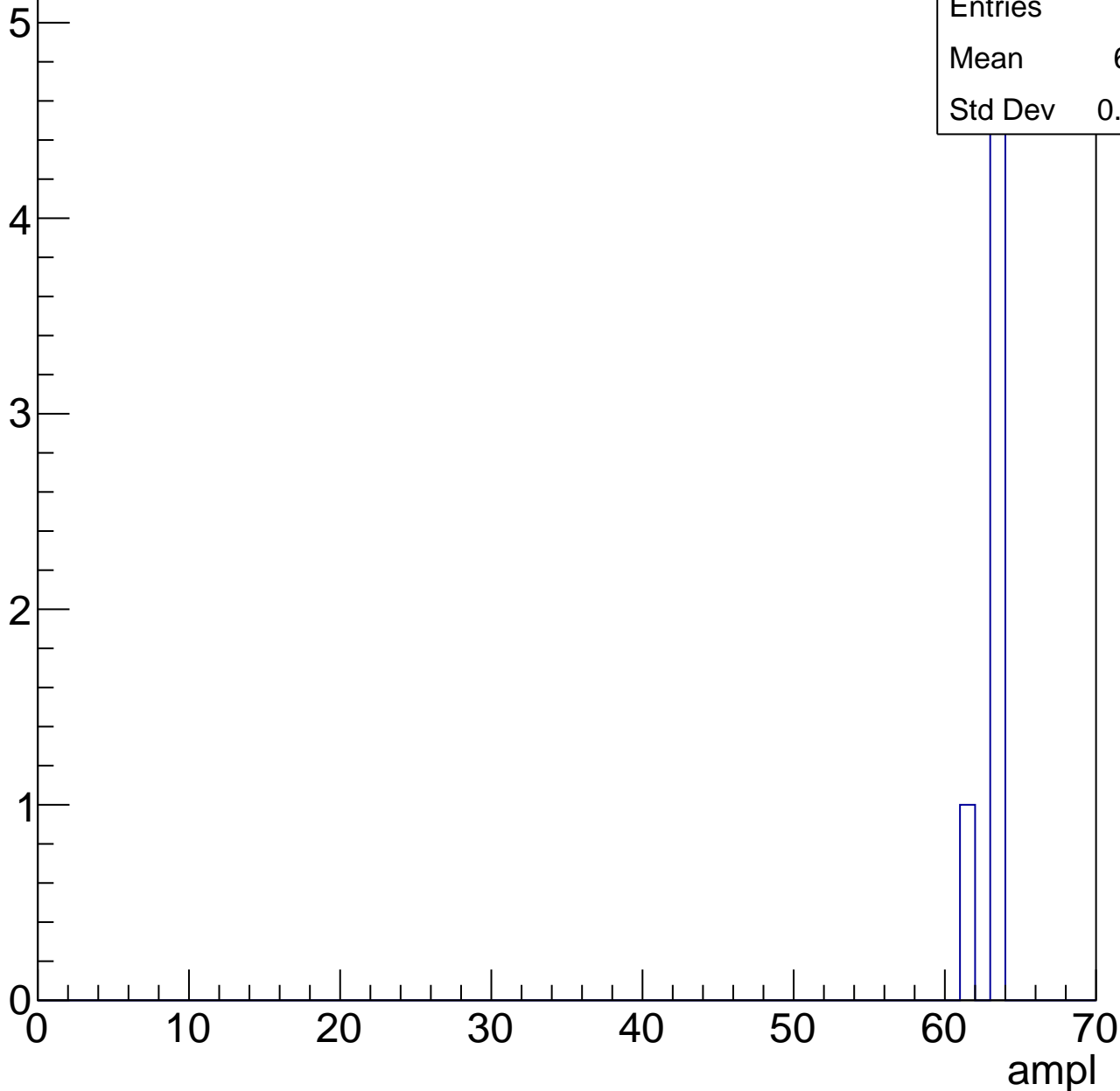


B0L000S, U2-ch118, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	6
Mean	62.67
Std Dev	0.7454



B0L000S, U2-ch118, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch119, adc0

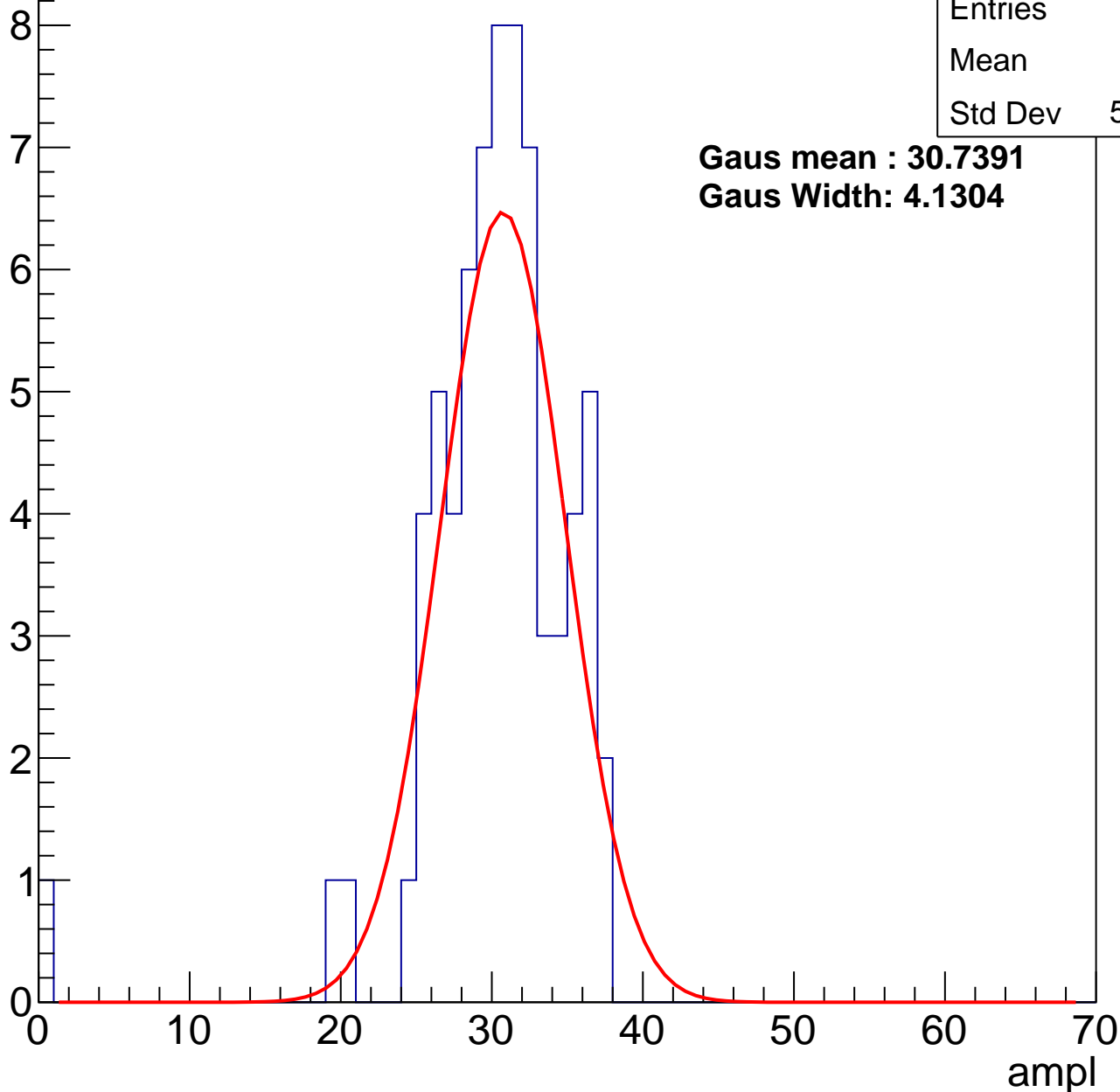
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.7
Std Dev	5.192

Gaus mean : 30.7391

Gaus Width: 4.1304



B0L000S, U2-ch119, adc1

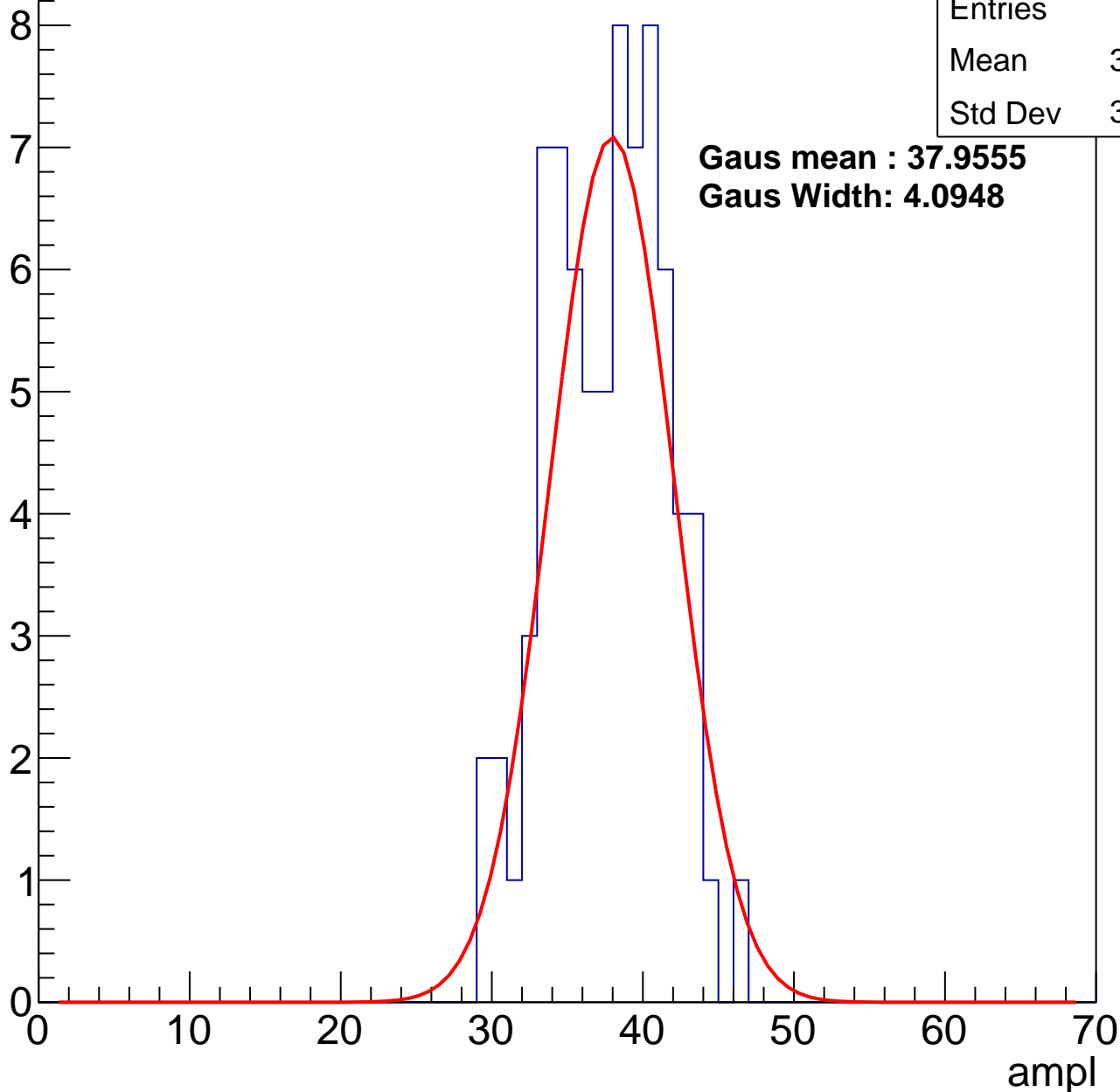
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	37.17
Std Dev	3.808

Gaus mean : 37.9555

Gaus Width: 4.0948



B0L000S, U2-ch119, adc2

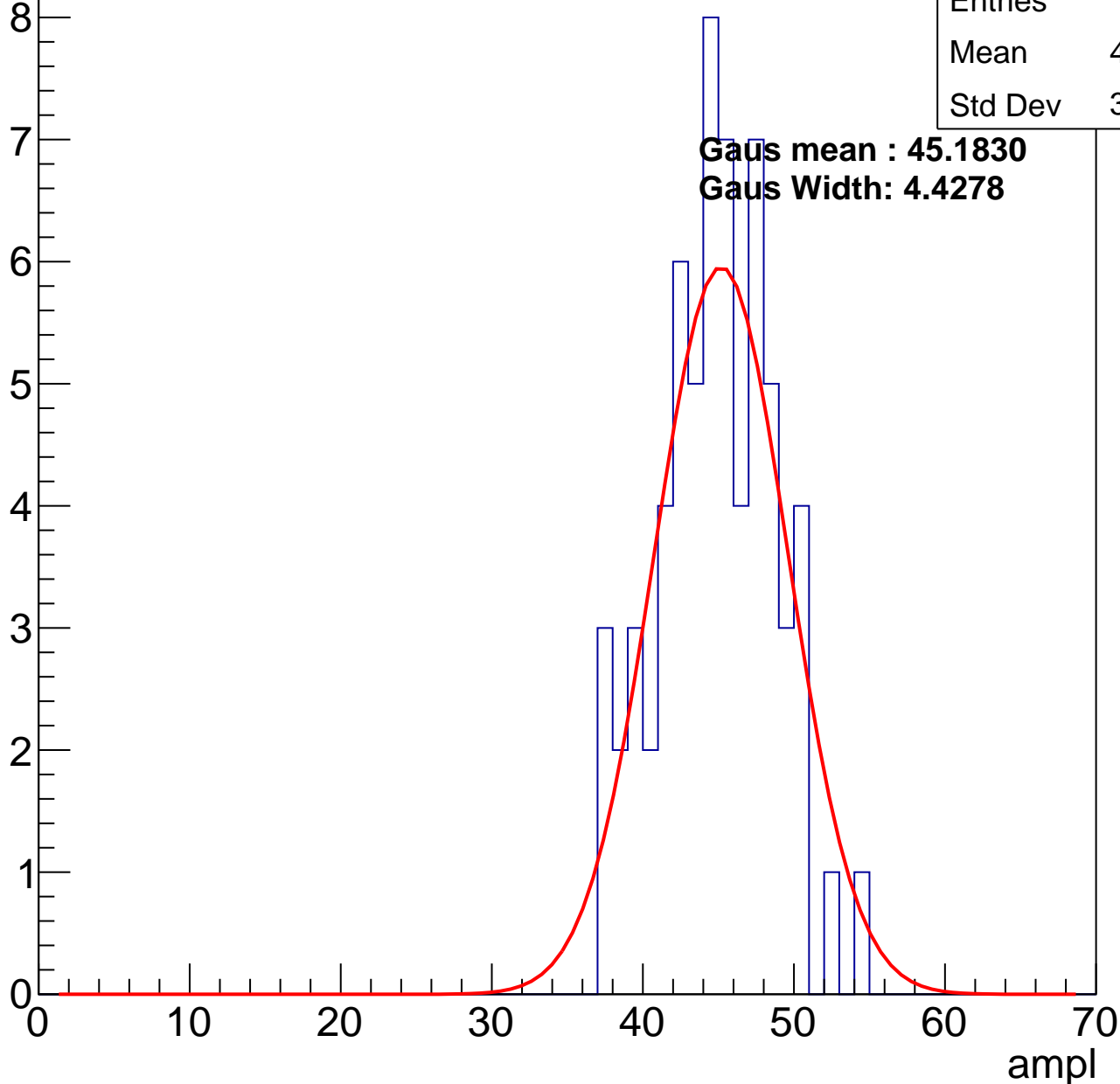
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.43
Std Dev	3.778

Gaus mean : 45.1830

Gaus Width: 4.4278

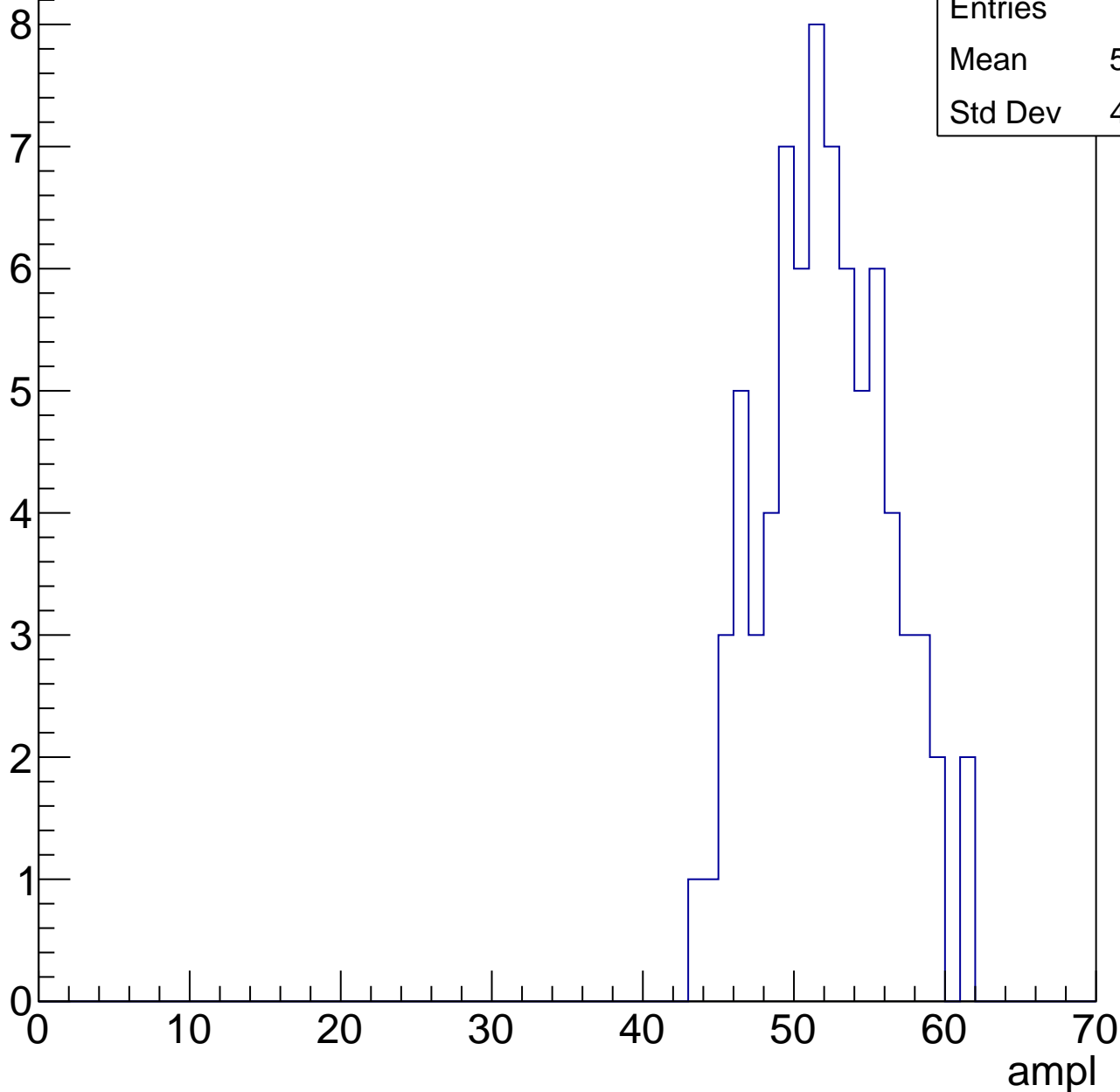


B0L000S, U2-ch119, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

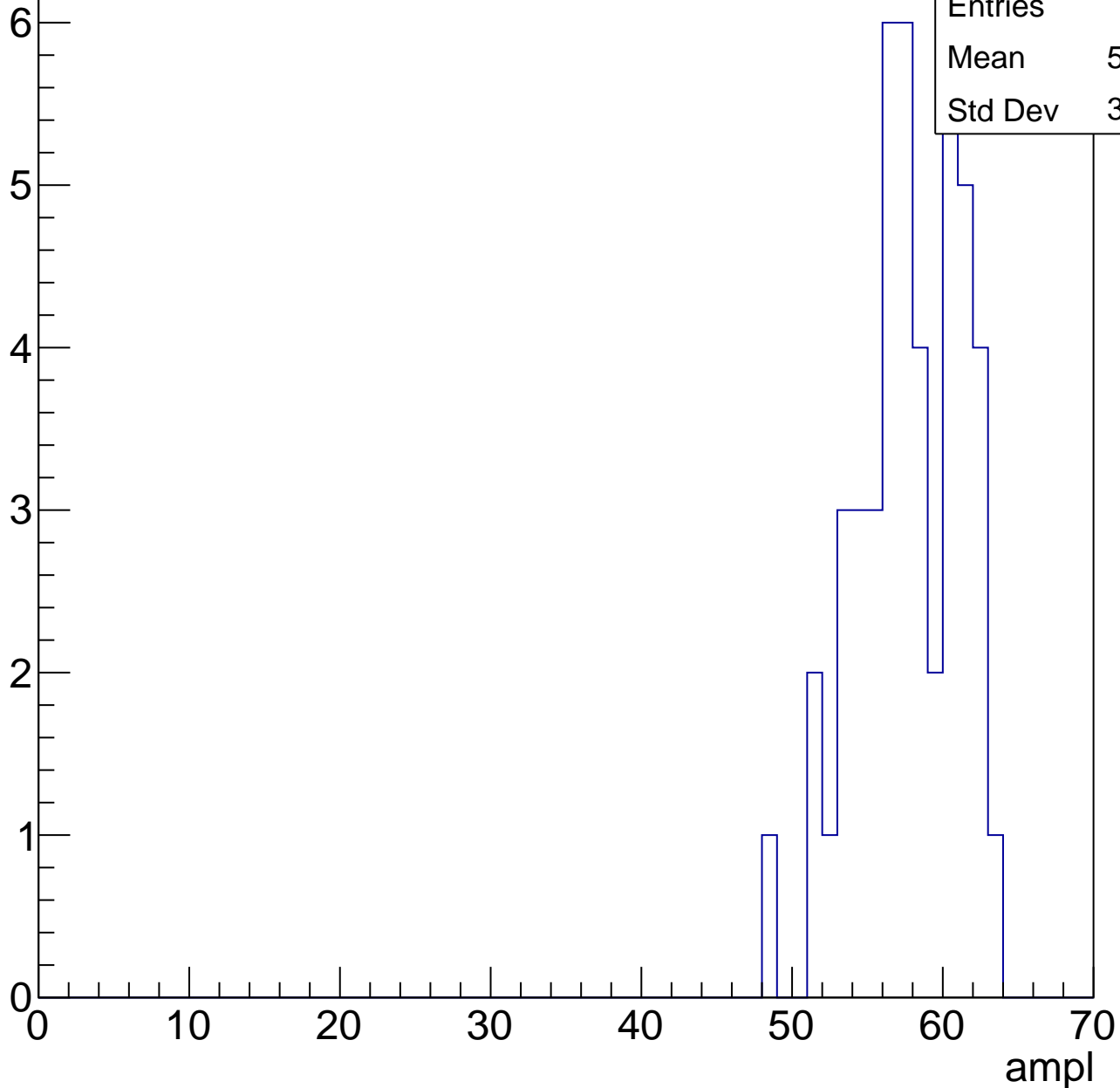
Entries	76
Mean	51.67
Std Dev	4.112



B0L000S, U2-ch119, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



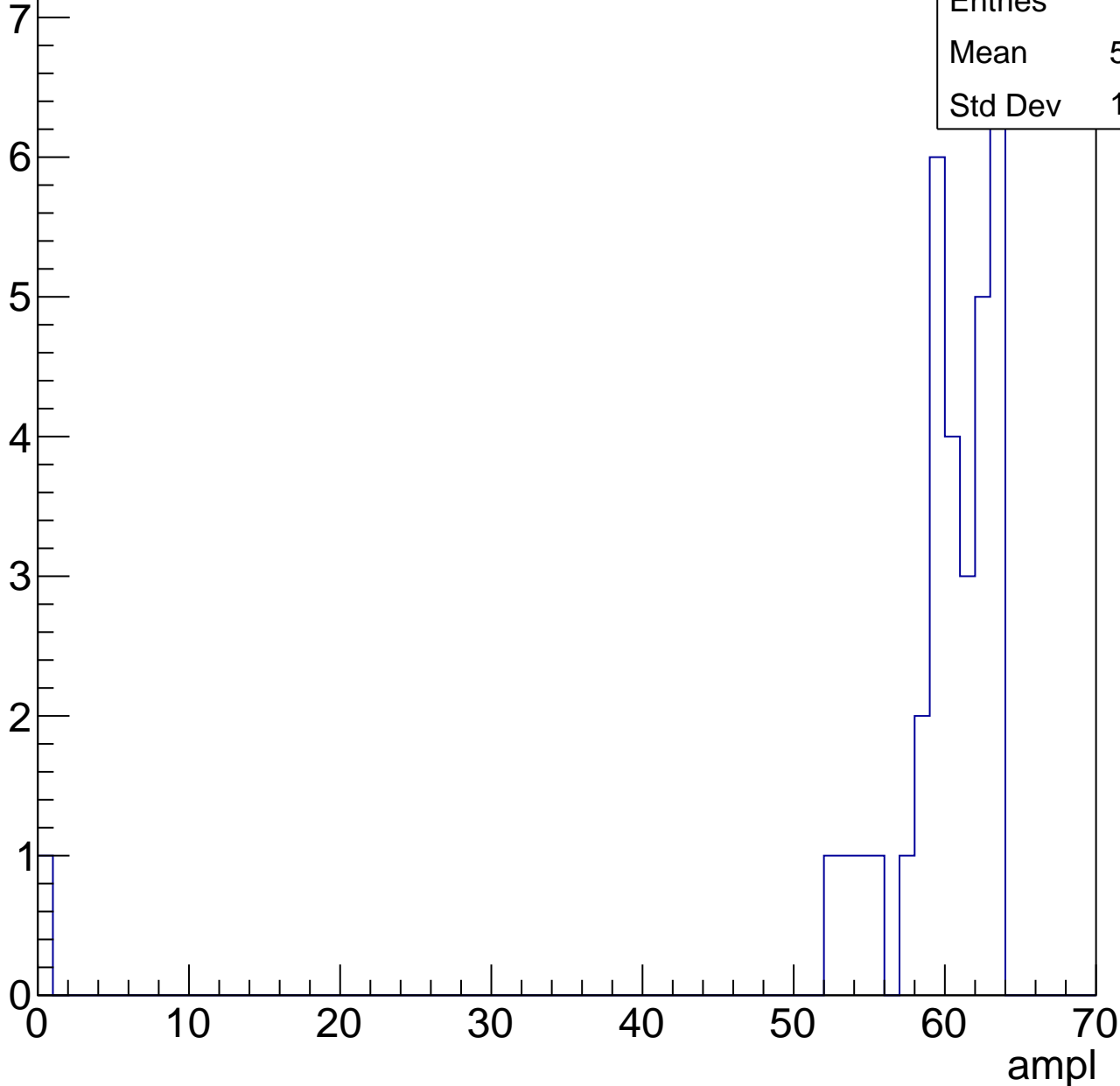
Entries	47
Mean	57.28
Std Dev	3.419

B0L000S, U2-ch119, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	33
Mean	58.03
Std Dev	10.67



B0L000S, U2-ch119, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	4
Mean	61.5
Std Dev	2.062

B0L000S, U2-ch119, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L000S, U2-ch120, adc0

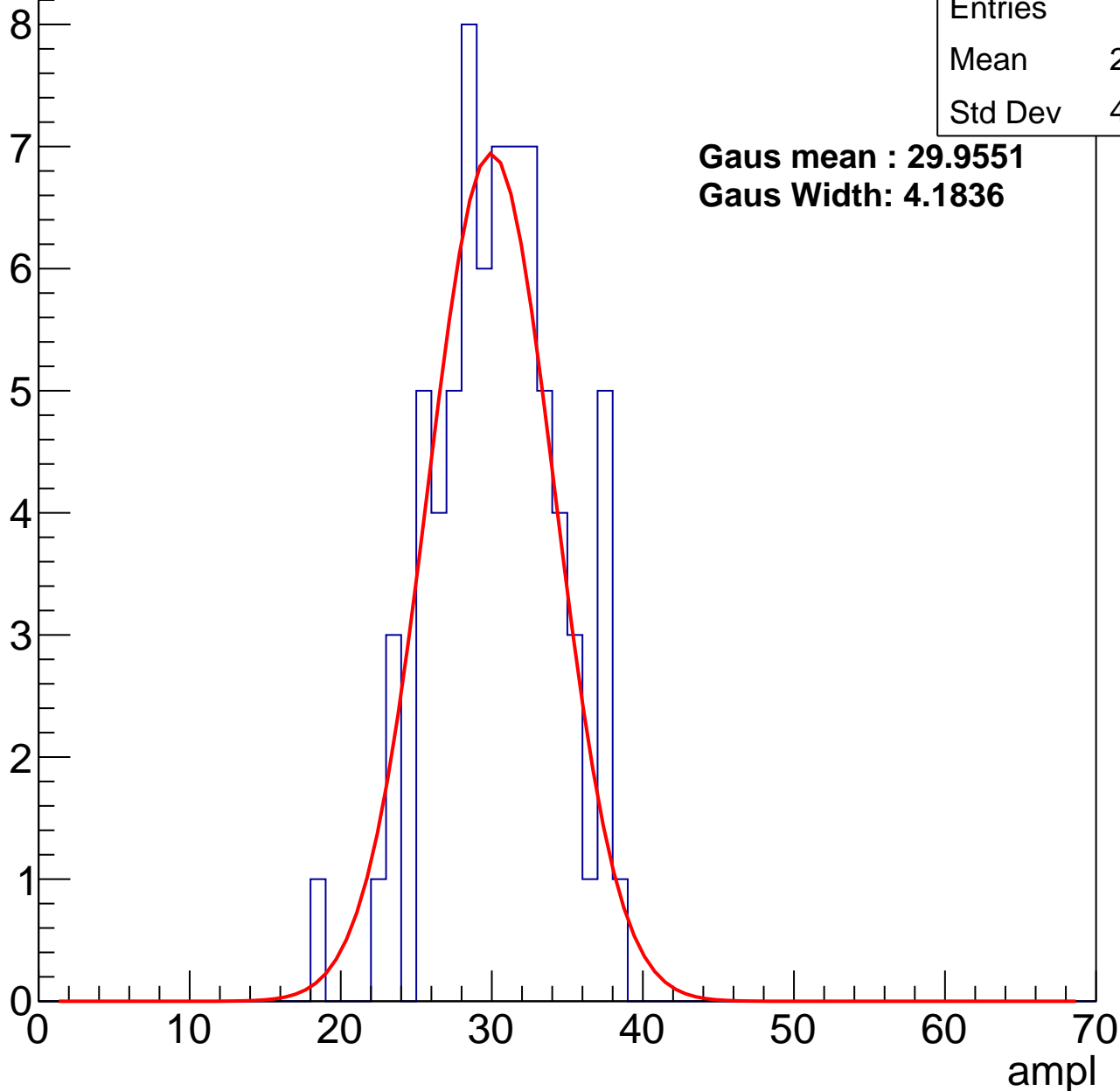
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.96
Std Dev	4.039

Gaus mean : 29.9551

Gaus Width: 4.1836



B0L000S, U2-ch120, adc1

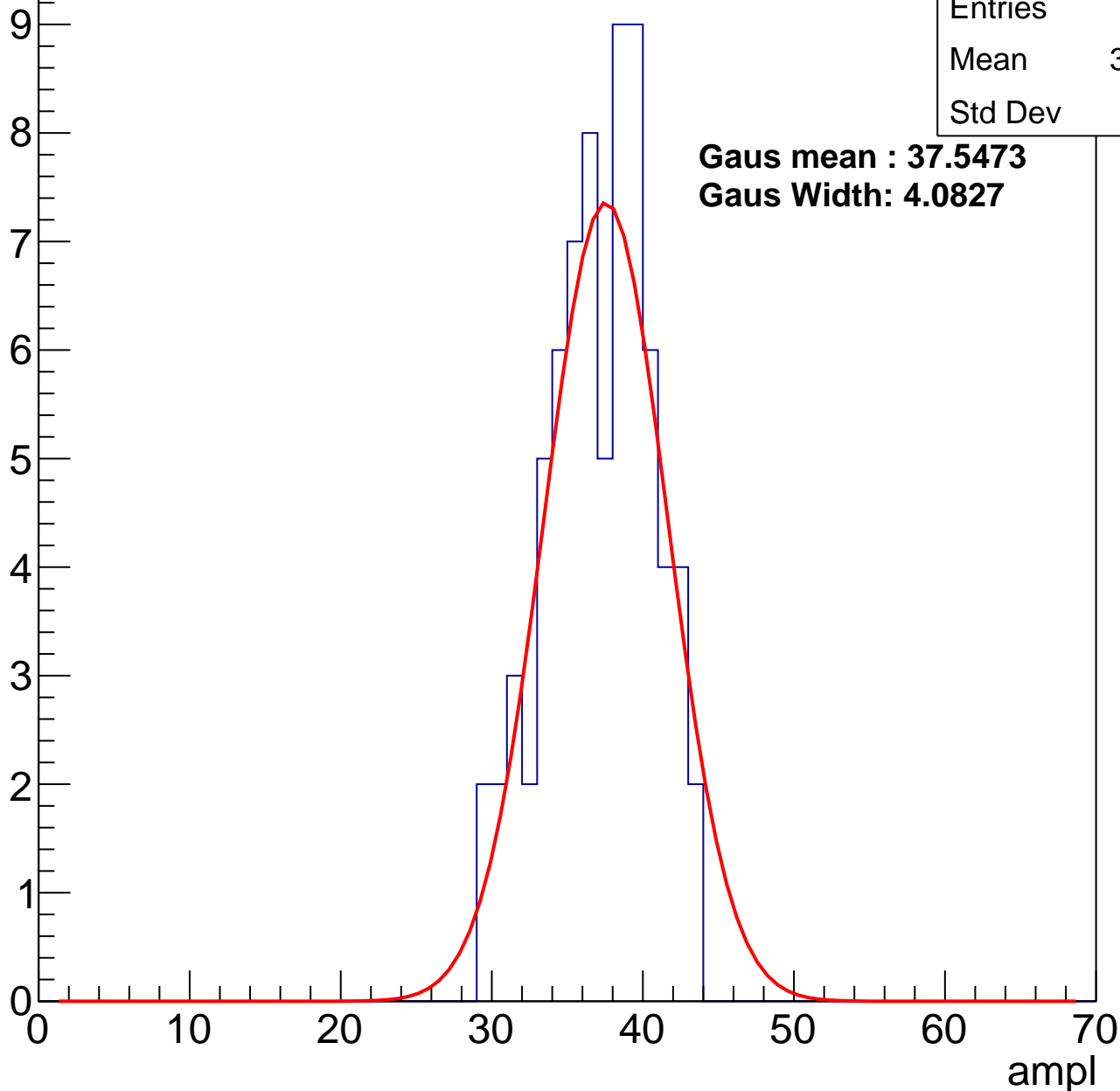
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.66
Std Dev	3.45

Gaus mean : 37.5473

Gaus Width: 4.0827



B0L000S, U2-ch120, adc2

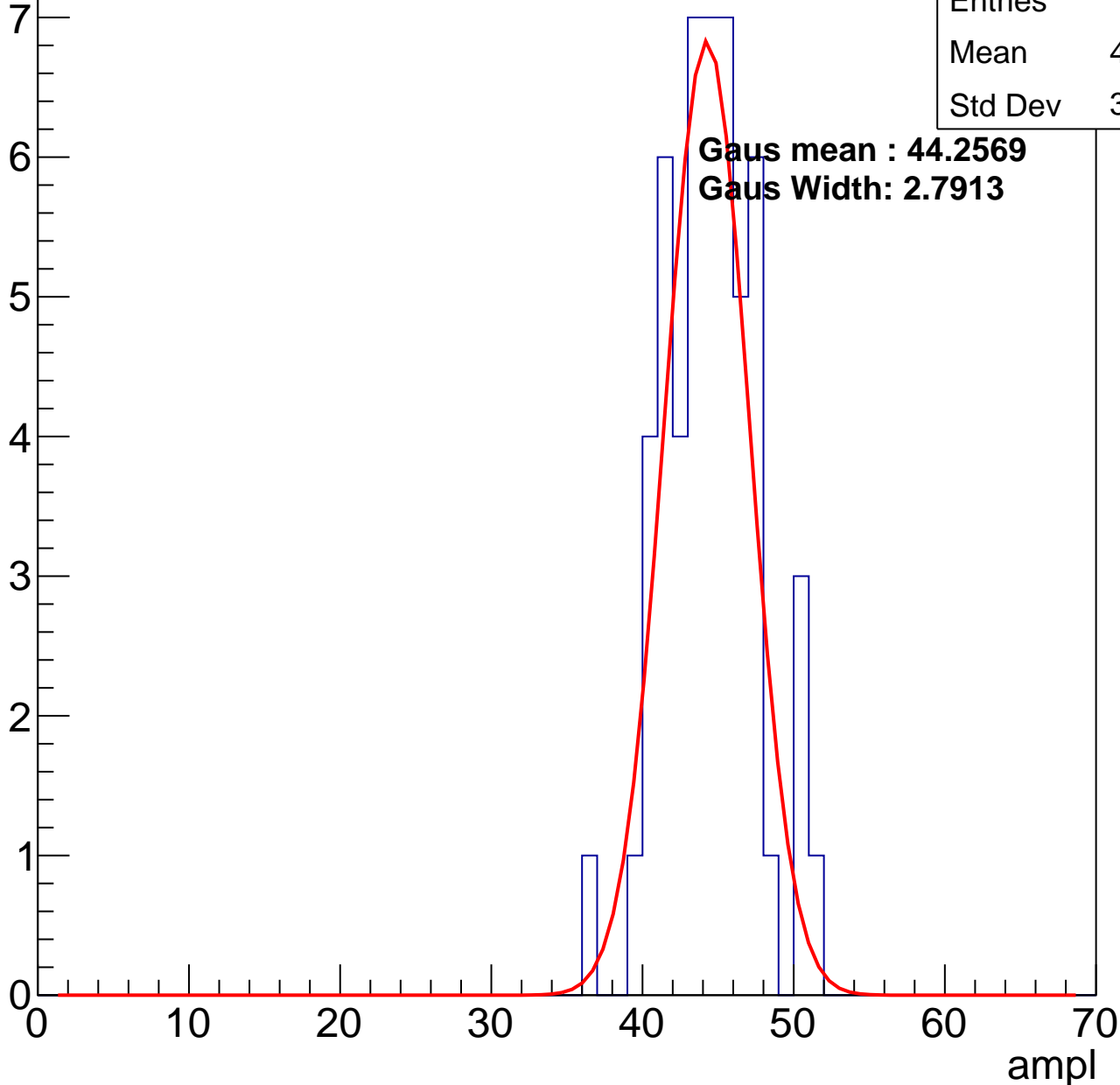
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	44.04
Std Dev	3.022

Gaus mean : 44.2569

Gaus Width: 2.7913

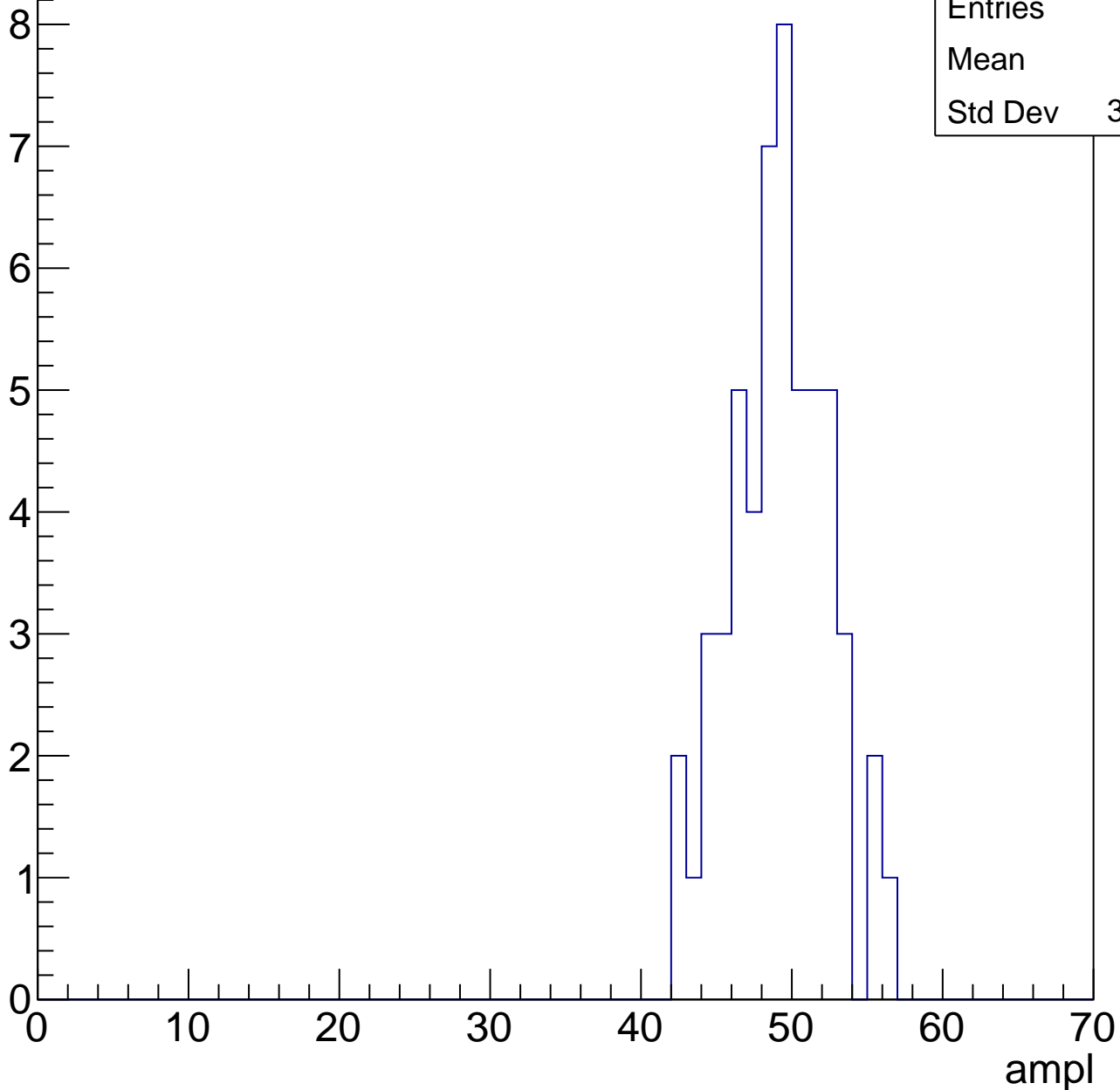


B0L000S, U2-ch120, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	54
Mean	48.7
Std Dev	3.218



B0L000S, U2-ch120, adc4

calib_packv5_042523_0143.root, FC#5, port B1

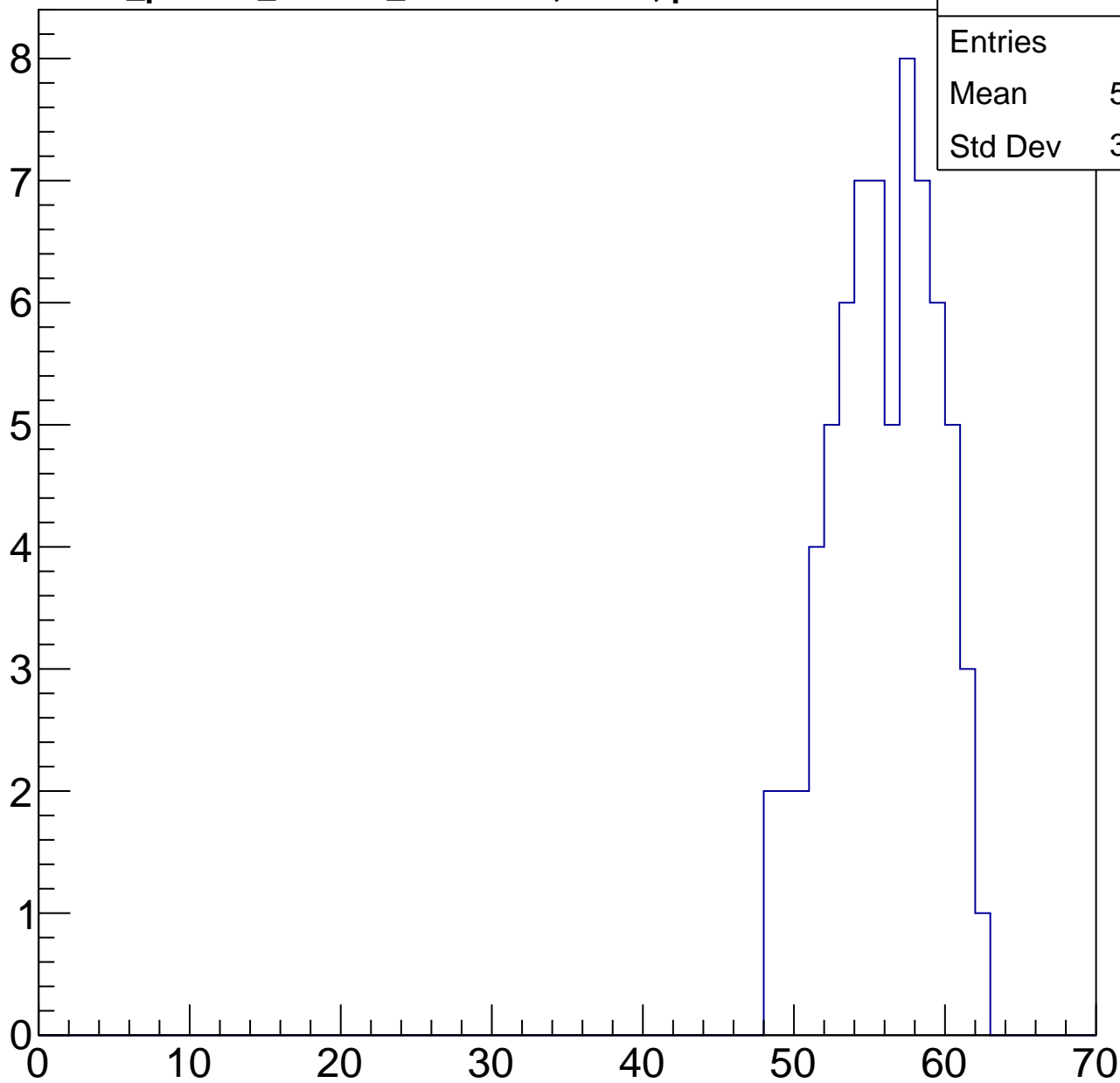
Entry

8
7
6
5
4
3
2
1
0

Entries	70
Mean	55.43
Std Dev	3.433

ampl

0 10 20 30 40 50 60 70

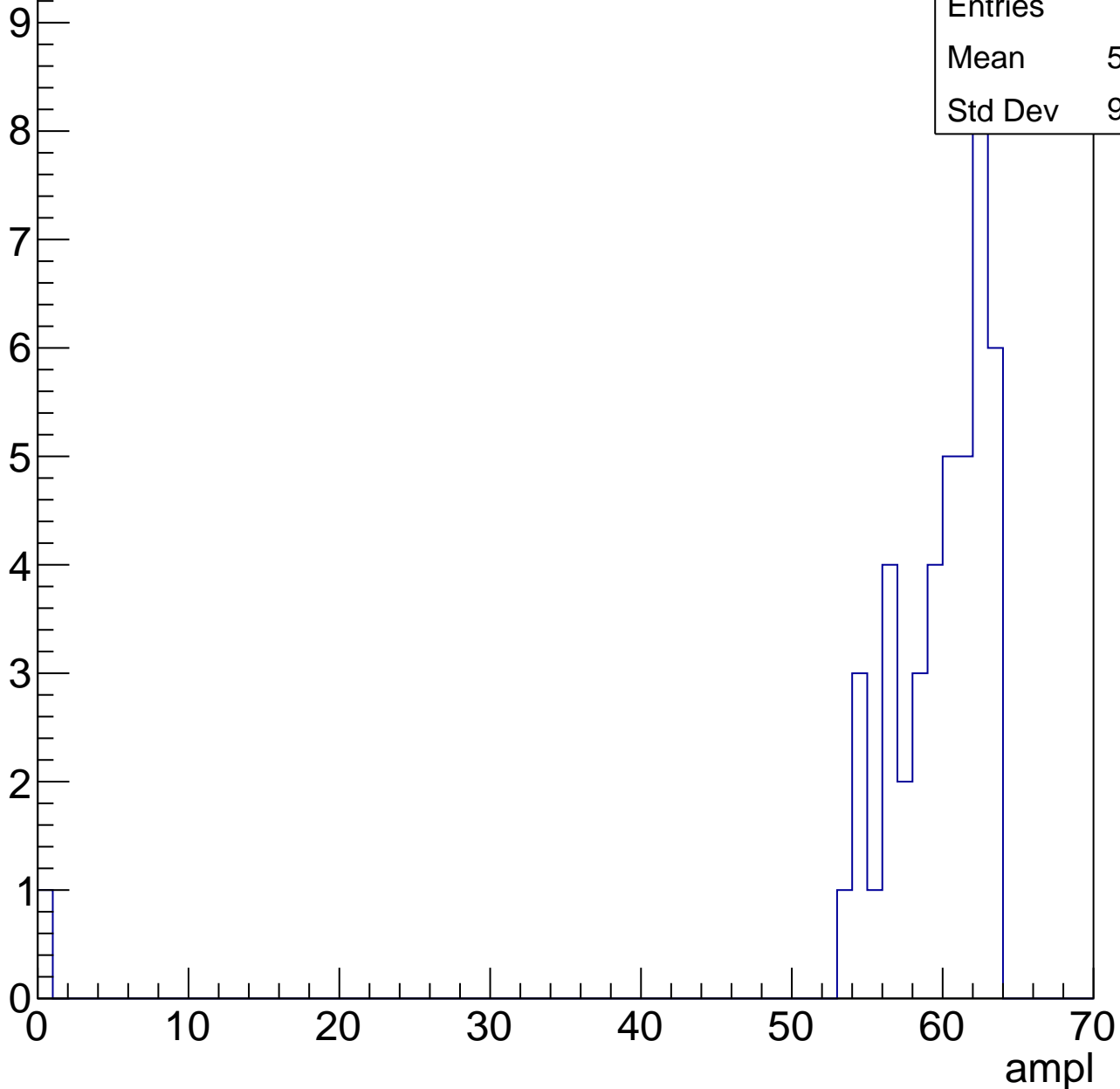


B0L000S, U2-ch120, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

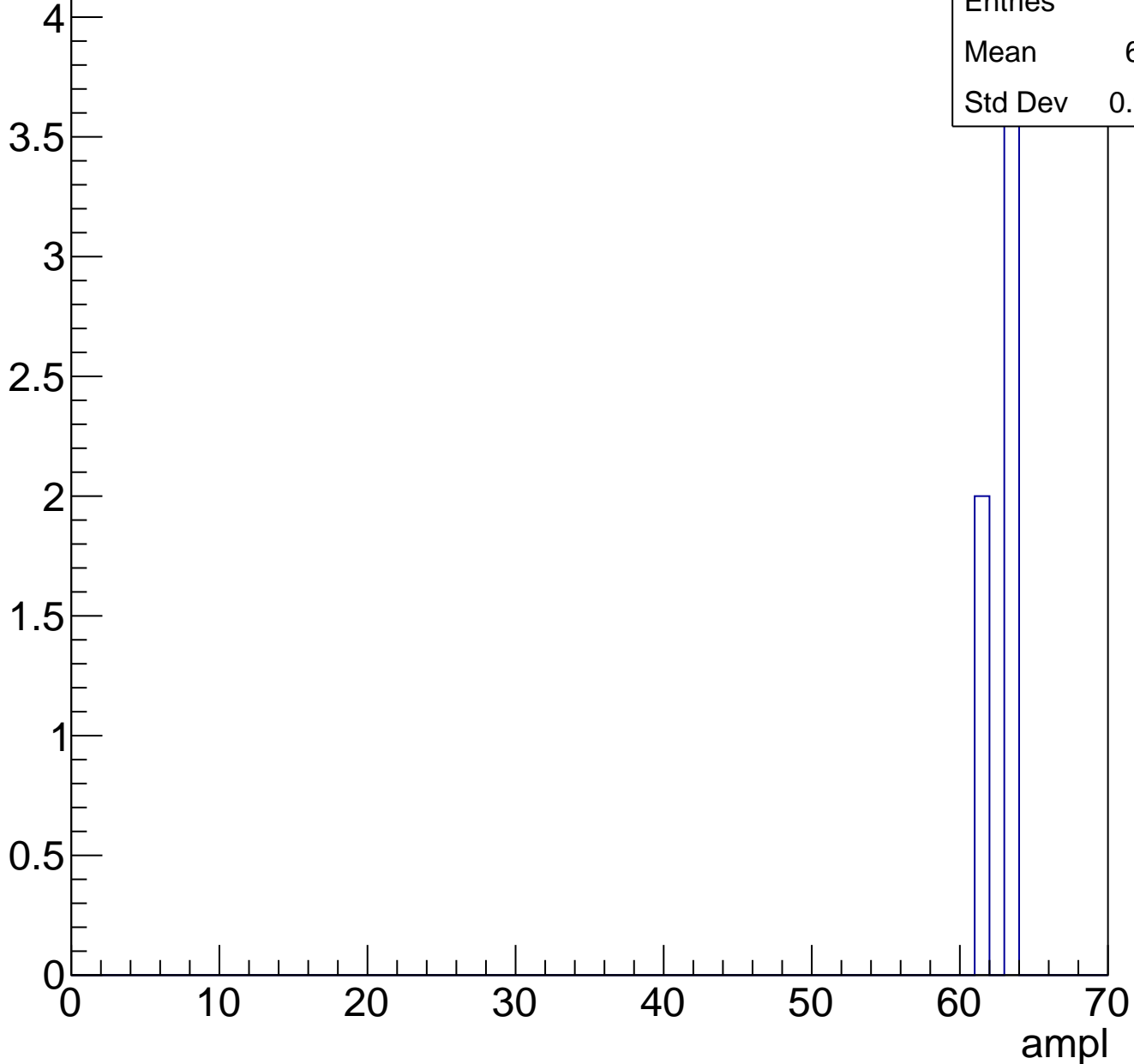
Entries	44
Mean	58.16
Std Dev	9.325



B0L000S, U2-ch120, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch120, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch121, adc0

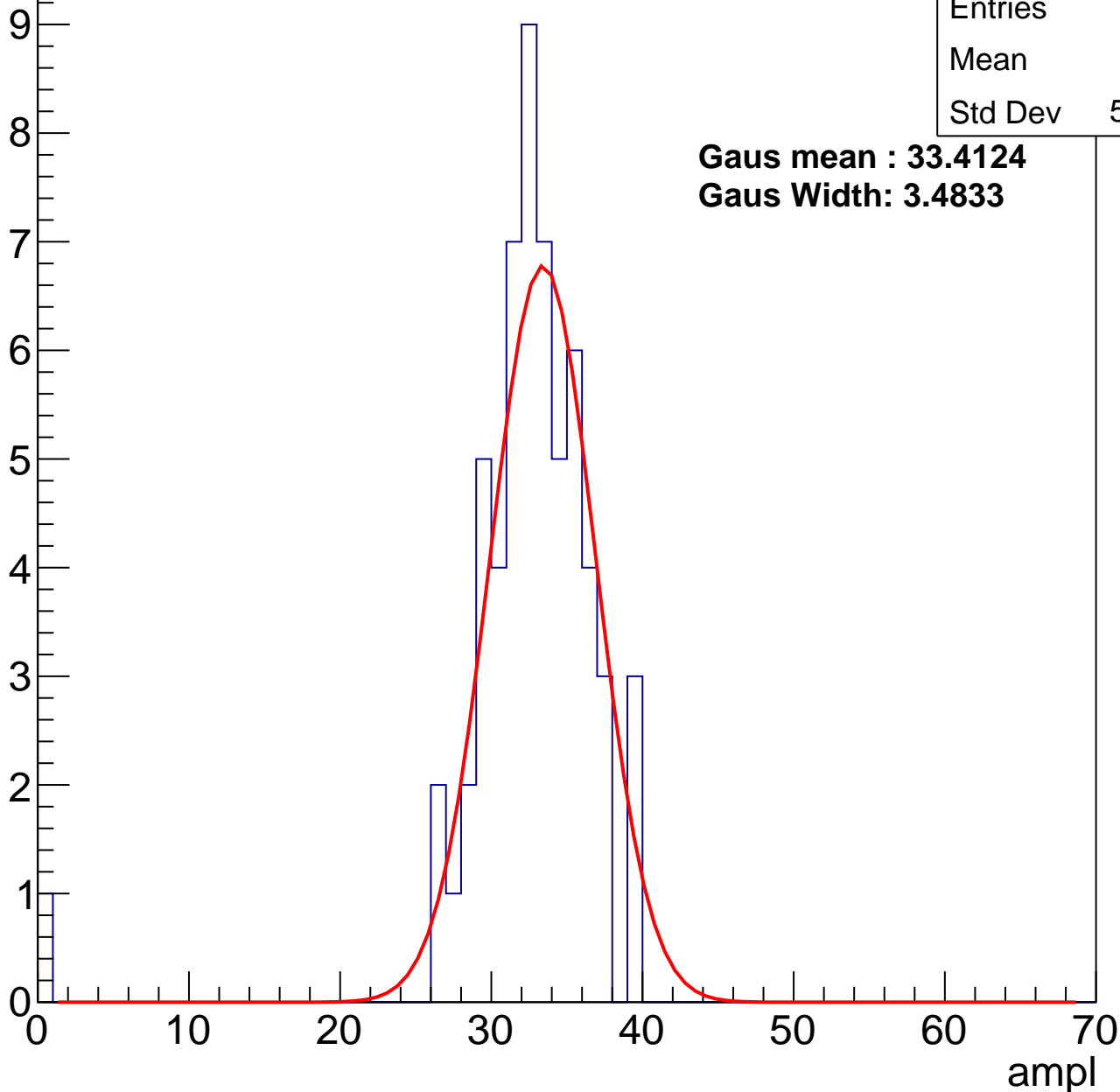
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	59
Mean	32
Std Dev	5.188

Gaus mean : 33.4124

Gaus Width: 3.4833



B0L000S, U2-ch121, adc1

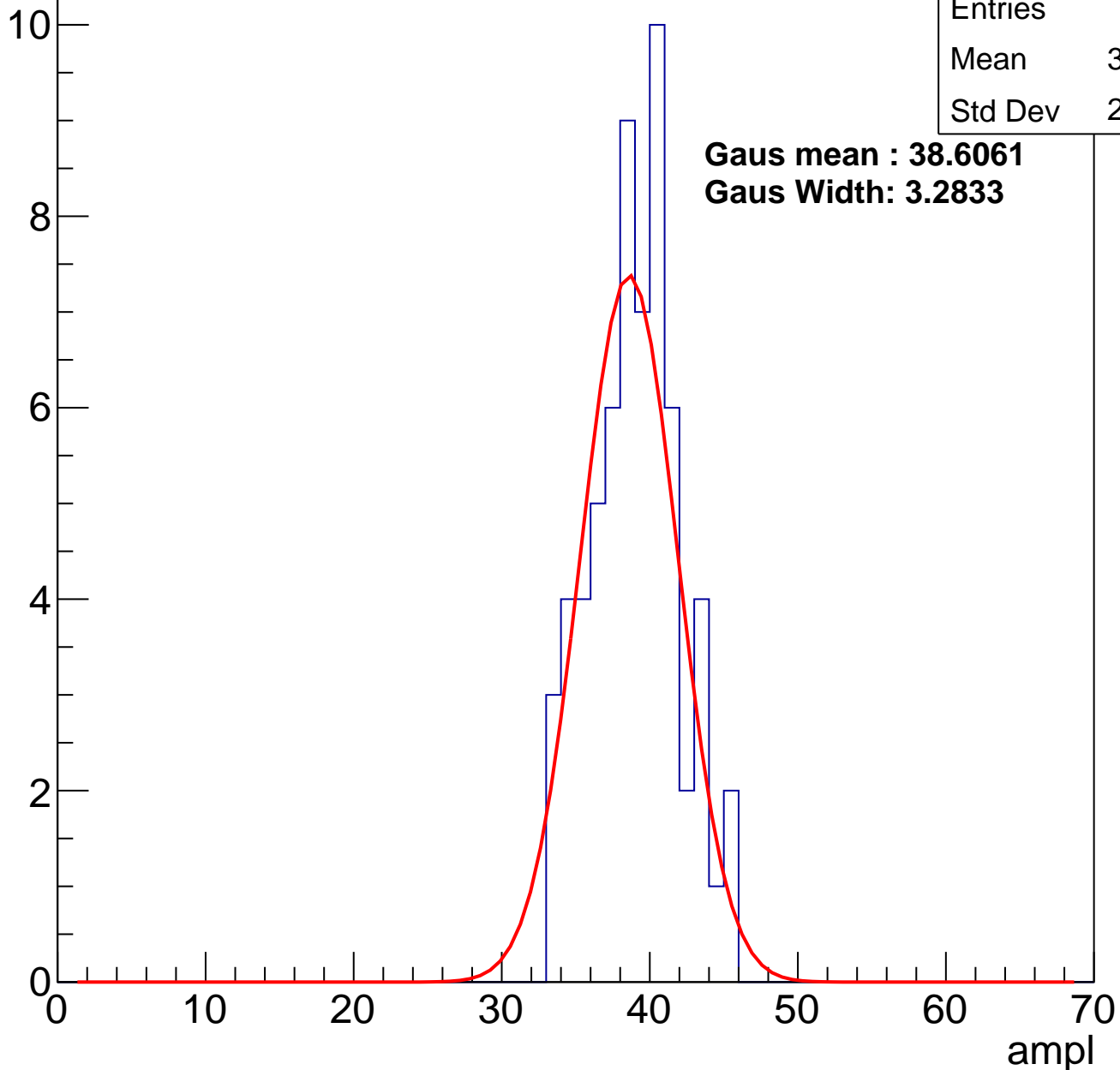
calib_packv5_042523_0143.root, FC#5, port B1

Entries	63
Mean	38.54
Std Dev	2.954

Gaus mean : 38.6061

Gaus Width: 3.2833

Entry



B0L000S, U2-ch121, adc2

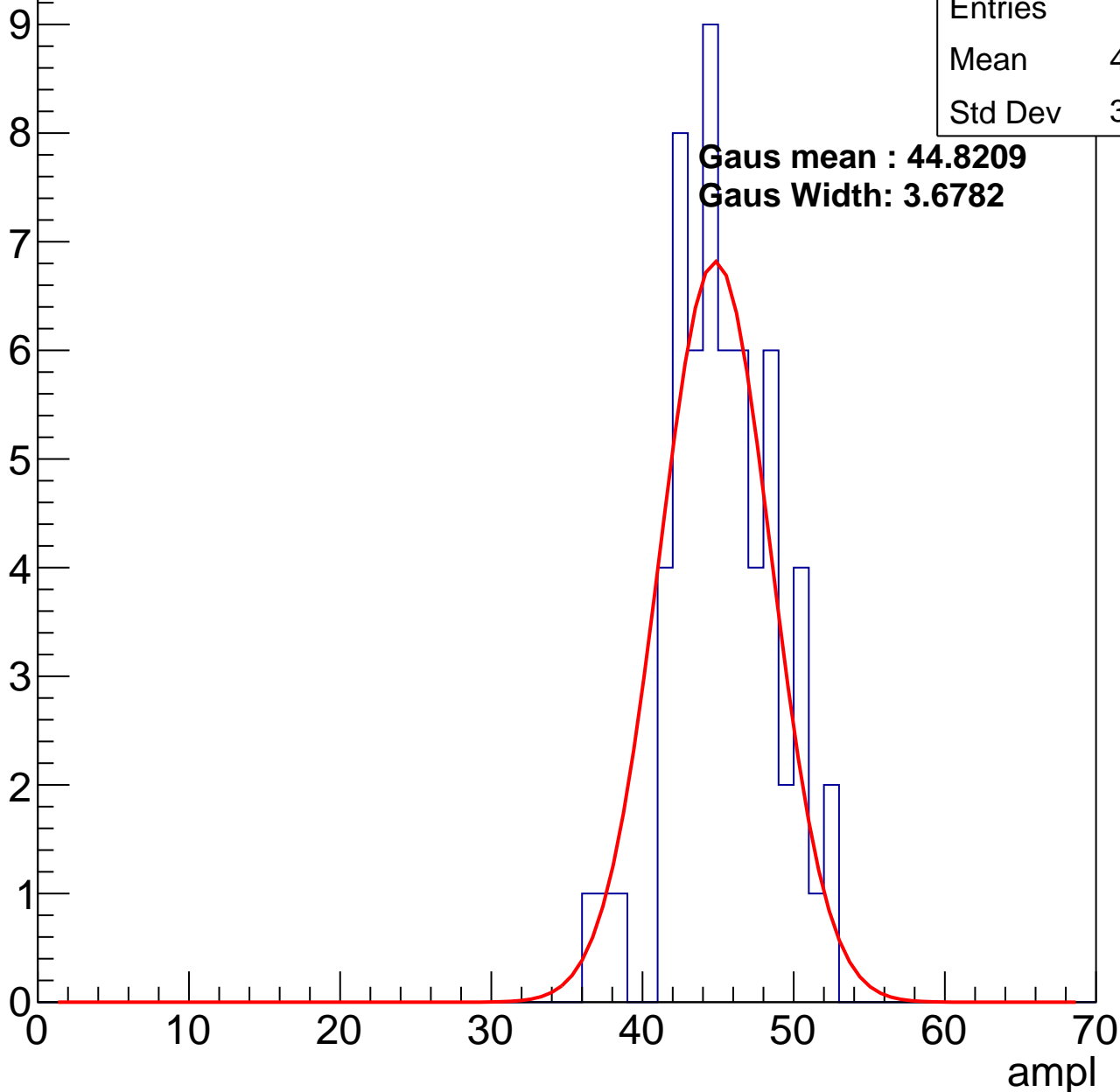
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.92
Std Dev	3.399

Gaus mean : 44.8209

Gaus Width: 3.6782

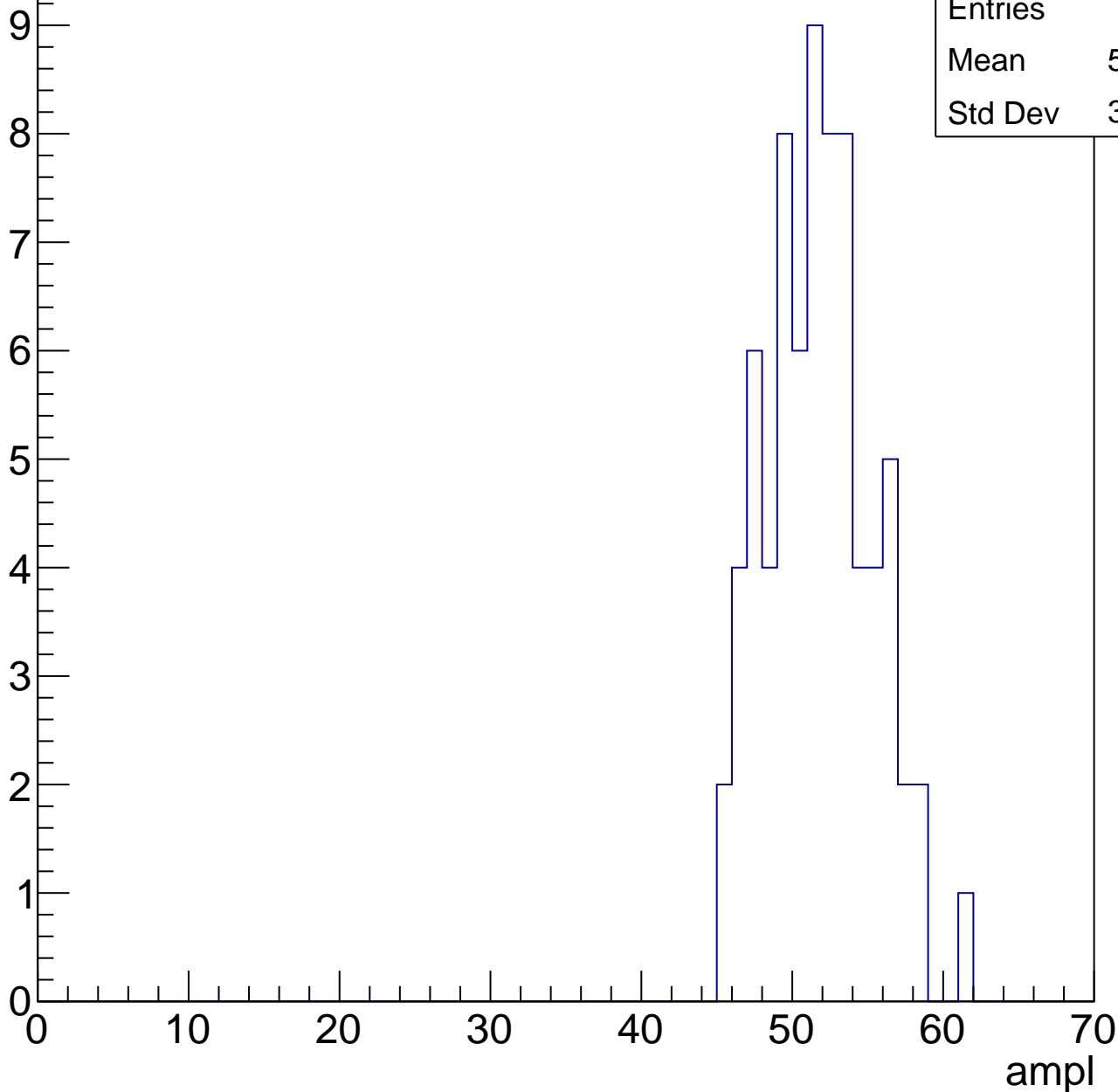


B0L000S, U2-ch121, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	73
Mean	51.32
Std Dev	3.452

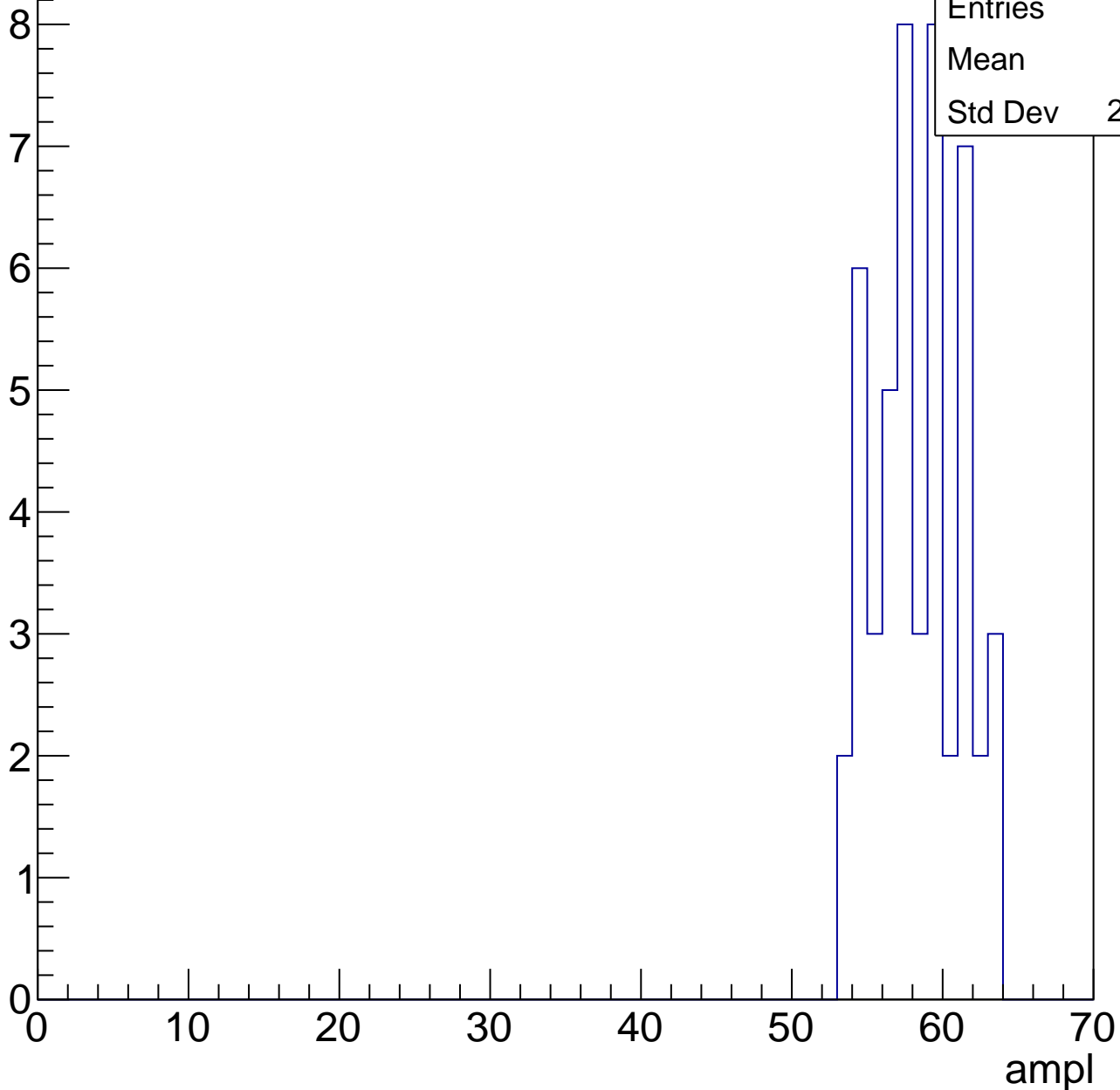


B0L000S, U2-ch121, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	57.9
Std Dev	2.808

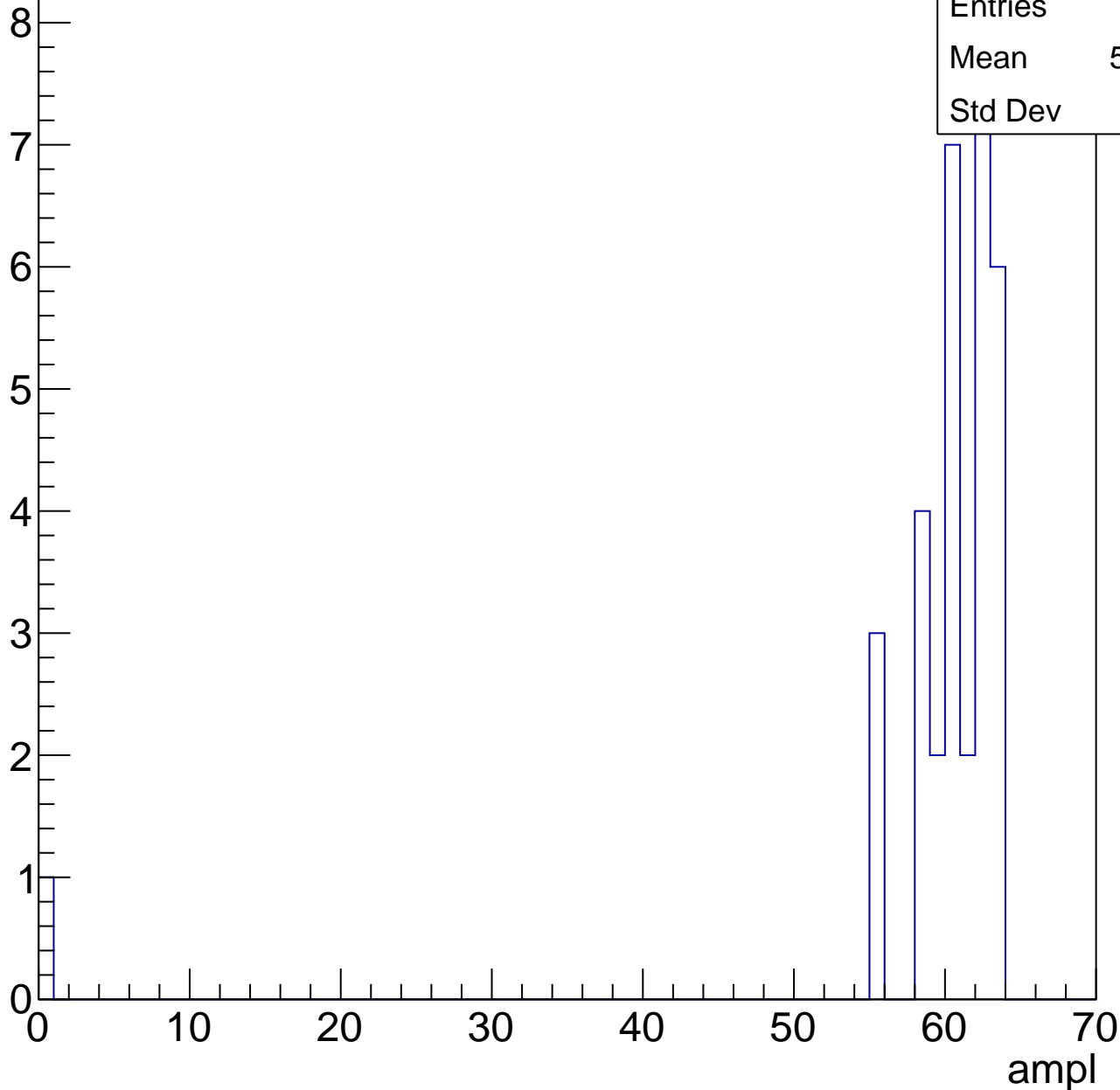


B0L000S, U2-ch121, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

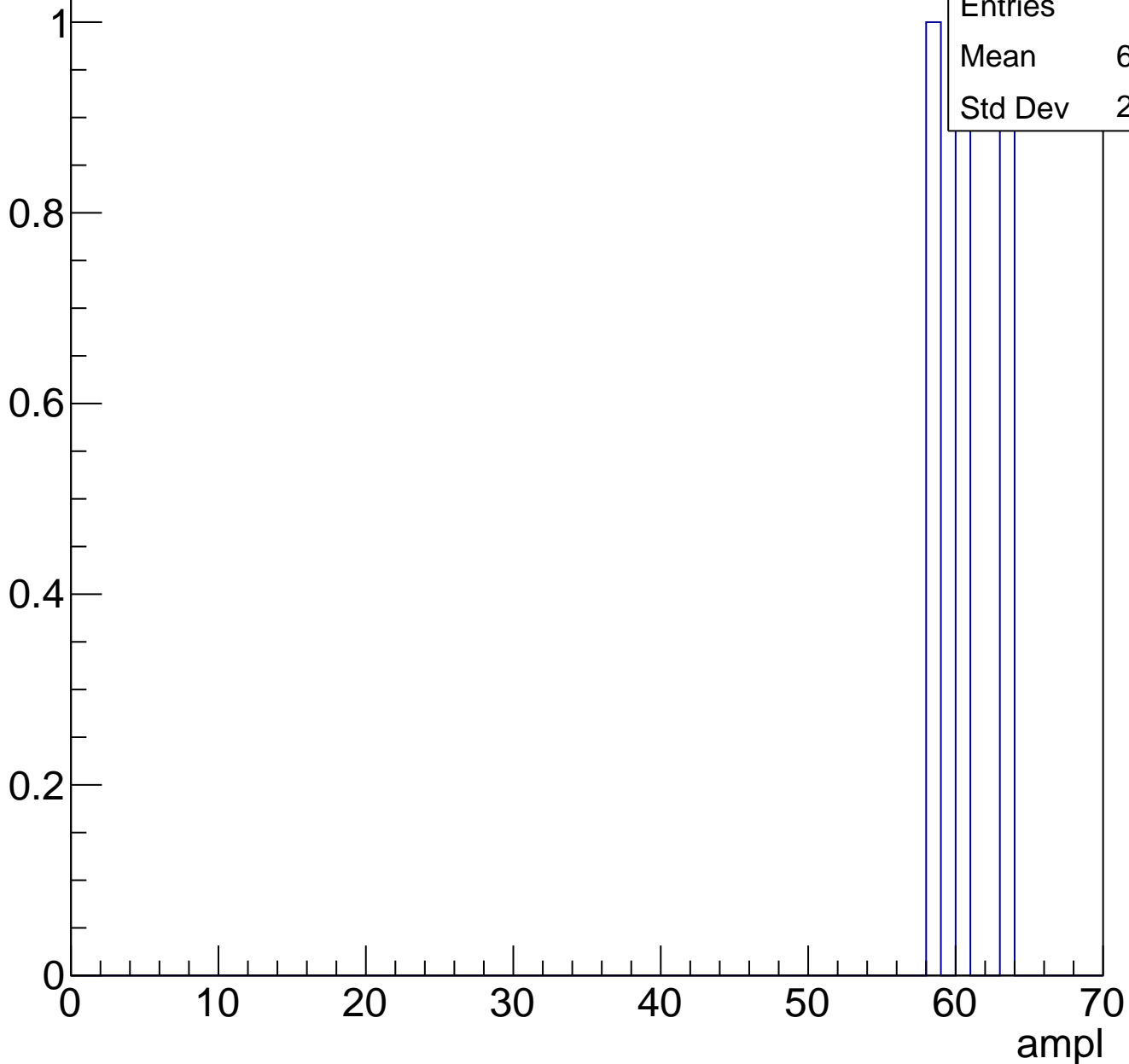
Entries	33
Mean	58.52
Std Dev	10.6



B0L000S, U2-ch121, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch121, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L000S, U2-ch122, adc0

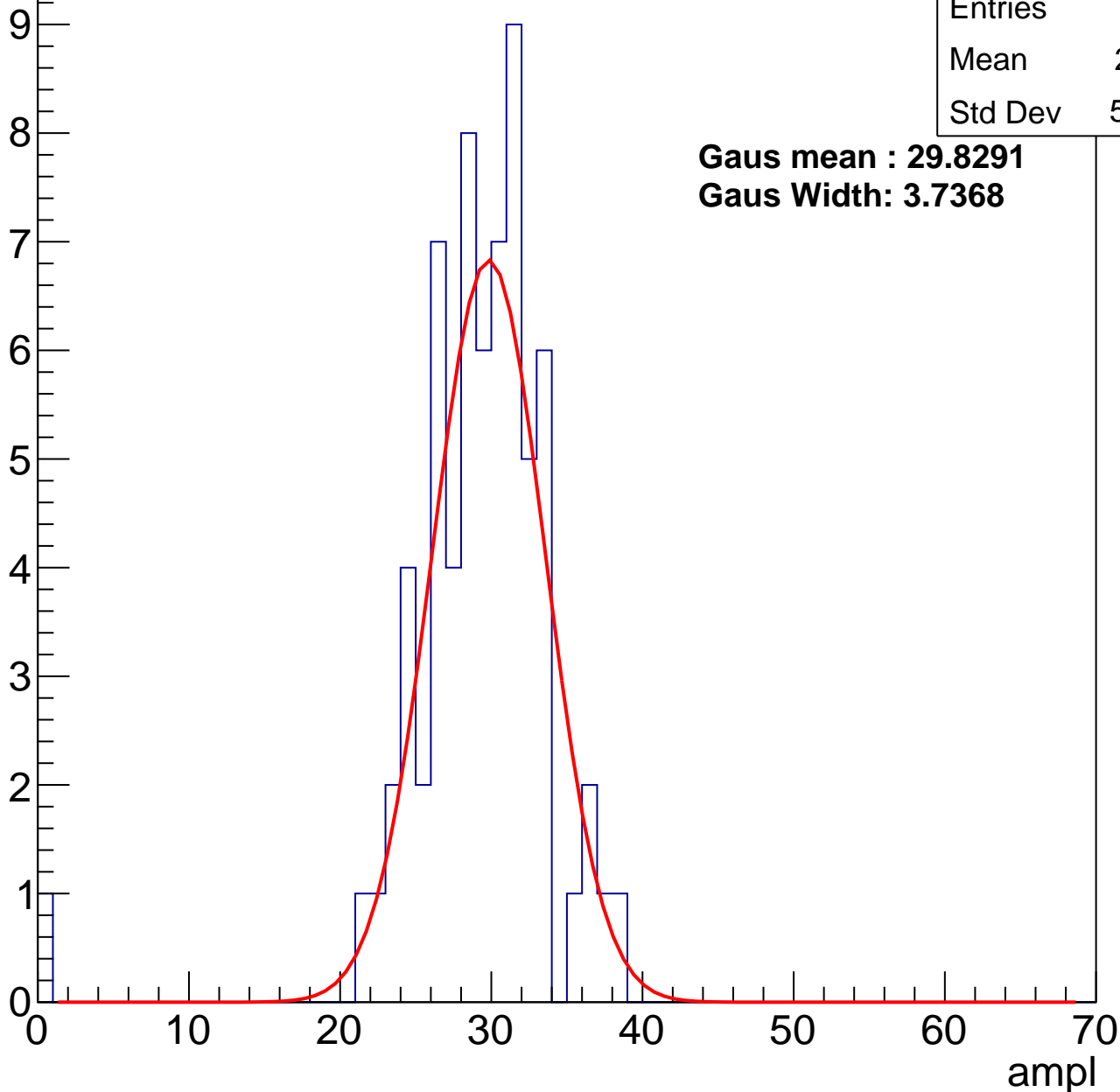
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	68
Mean	28.71
Std Dev	5.003

Gaus mean : 29.8291

Gaus Width: 3.7368



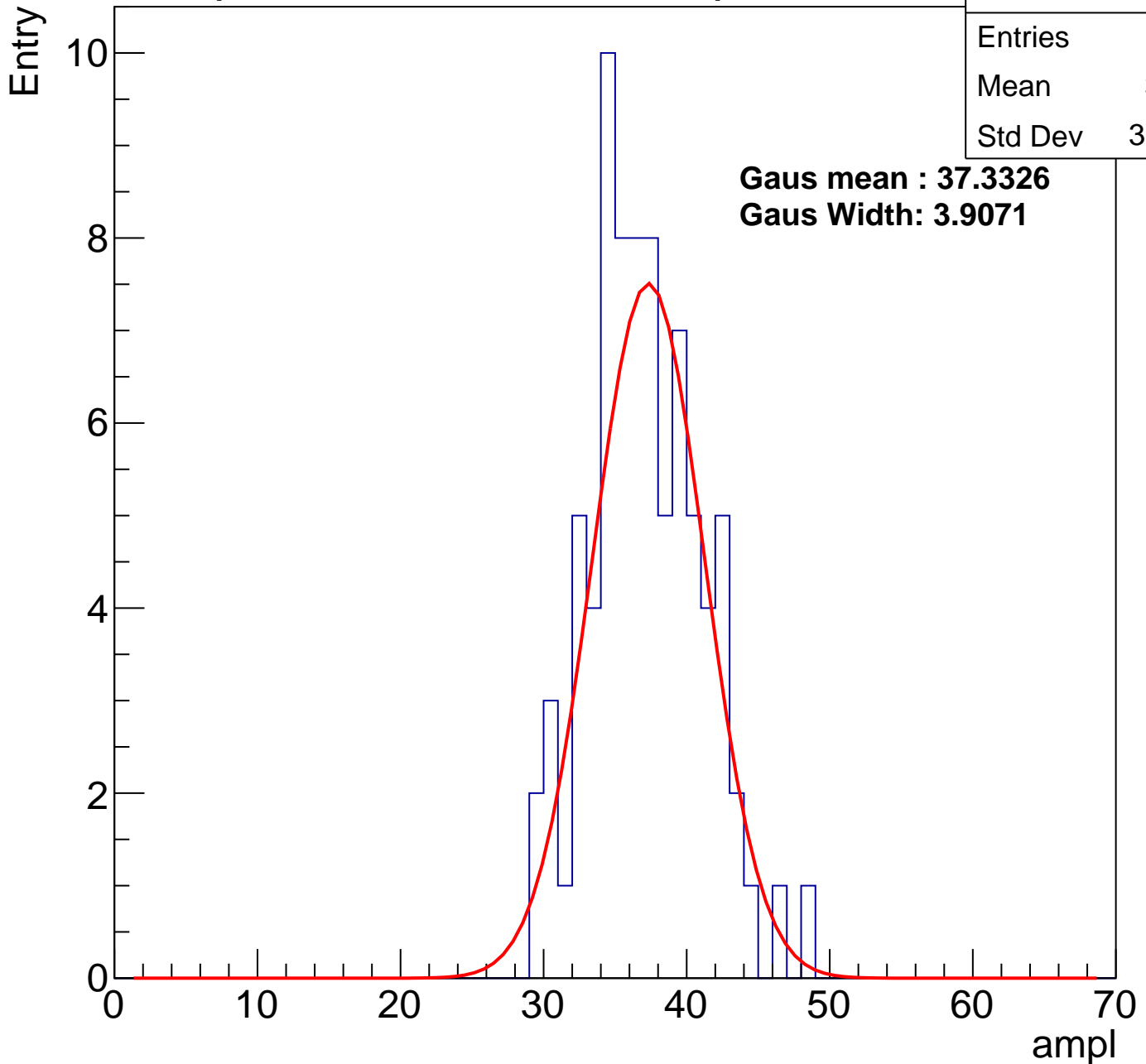
B0L000S, U2-ch122, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	80
Mean	36.7
Std Dev	3.894

Gaus mean : 37.3326

Gaus Width: 3.9071



B0L000S, U2-ch122, adc2

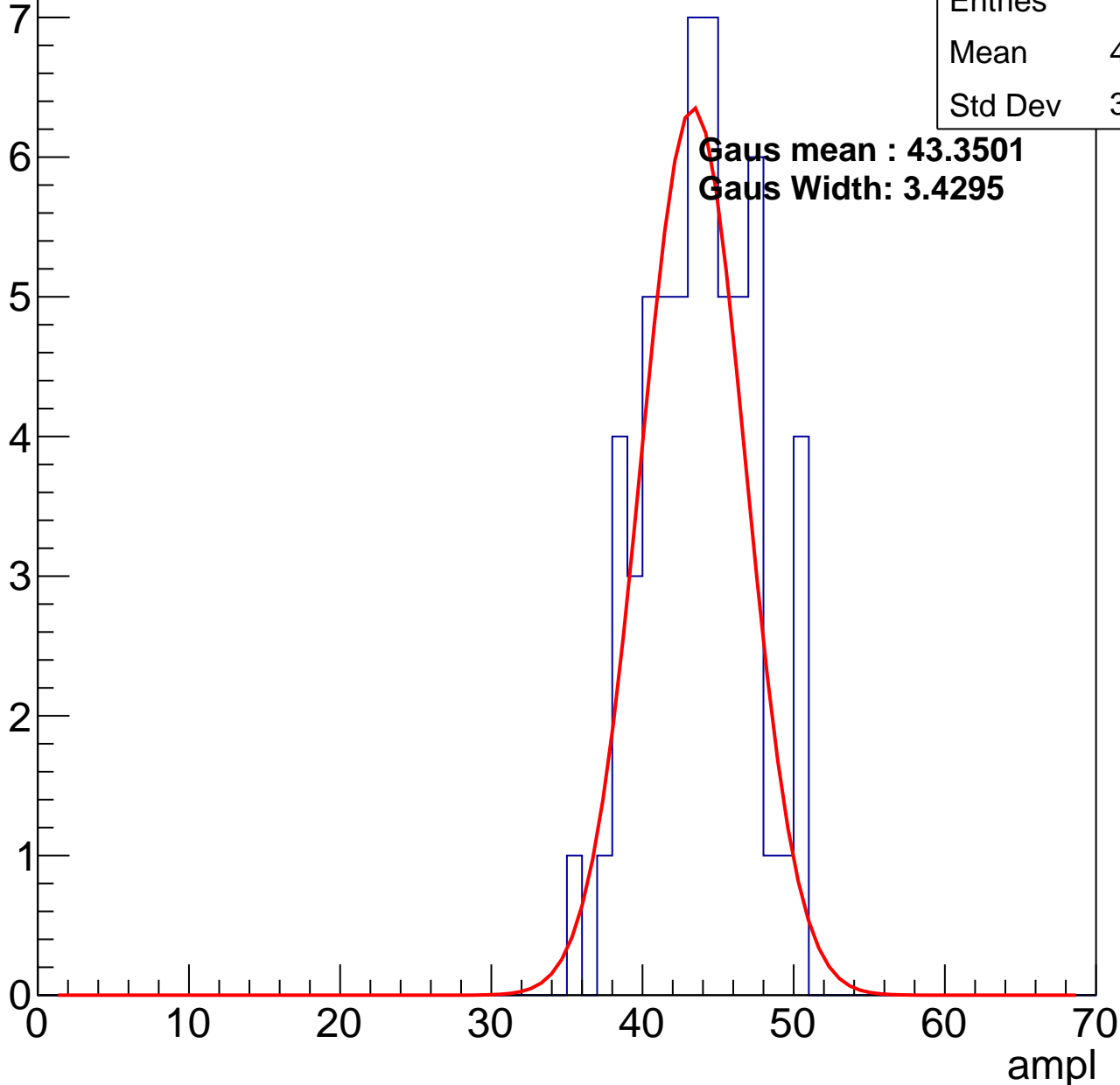
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.32
Std Dev	3.505

Gaus mean : 43.3501

Gaus Width: 3.4295

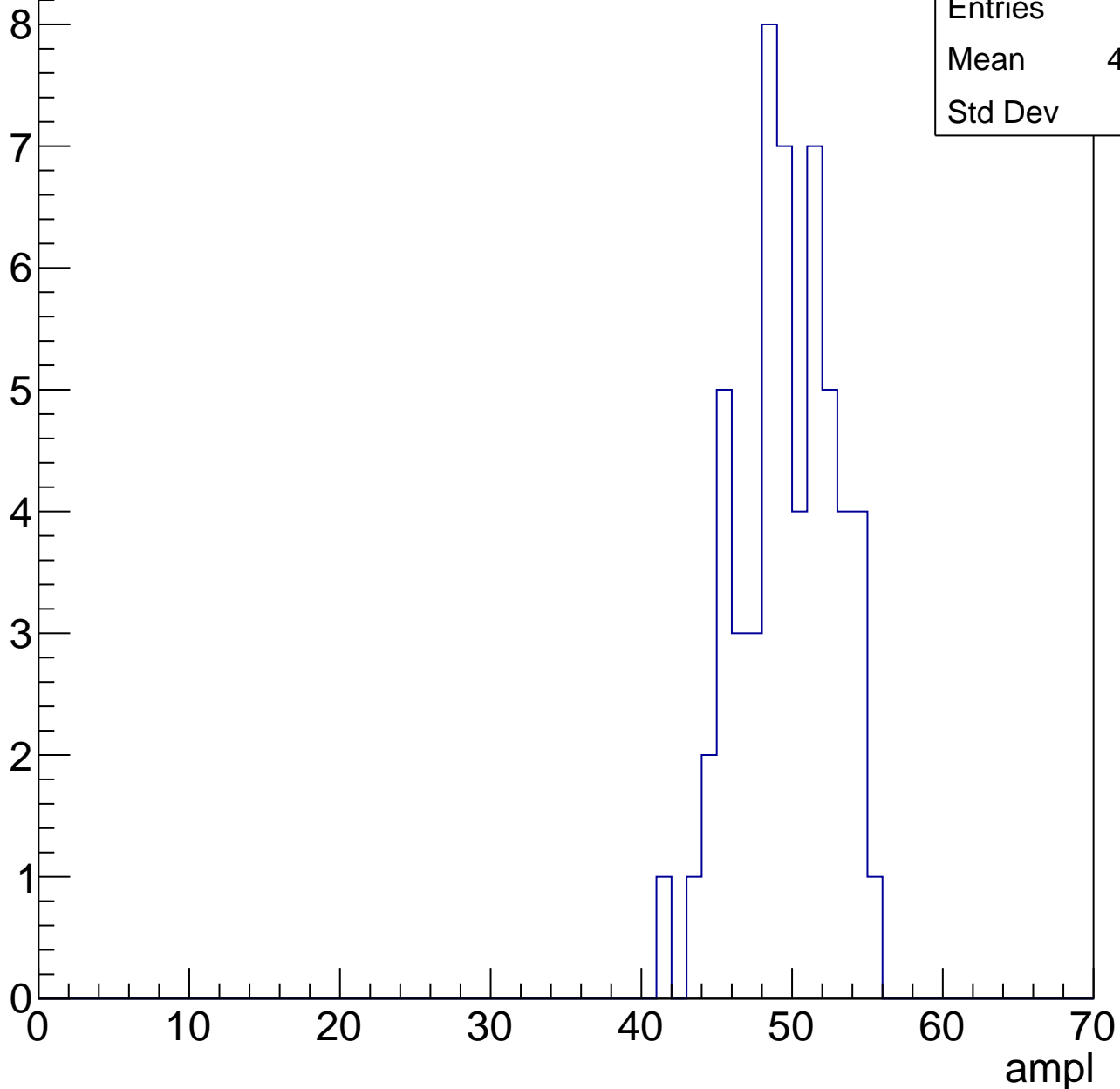


B0L000S, U2-ch122, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.15
Std Dev	3.17

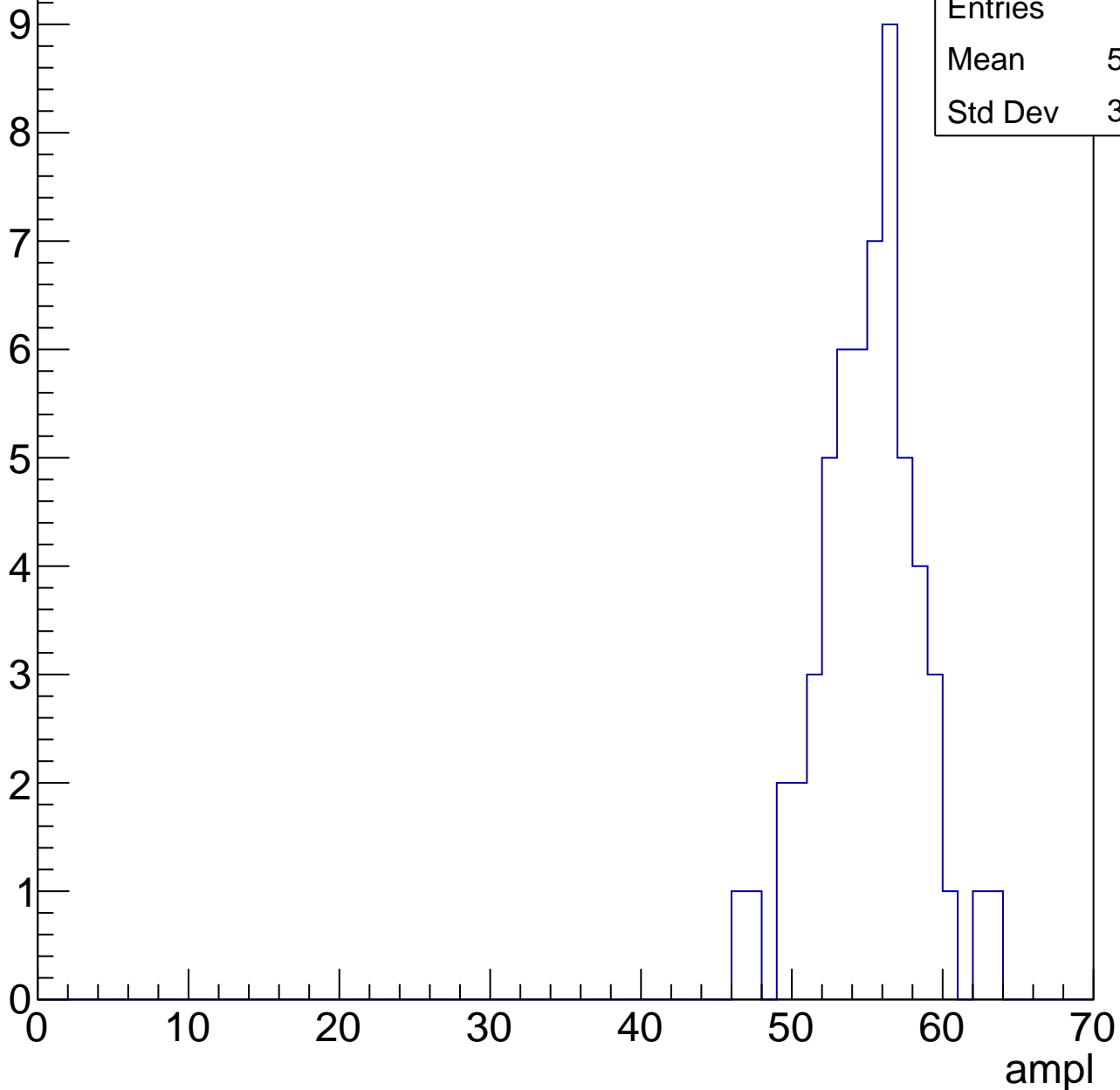


B0L000S, U2-ch122, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

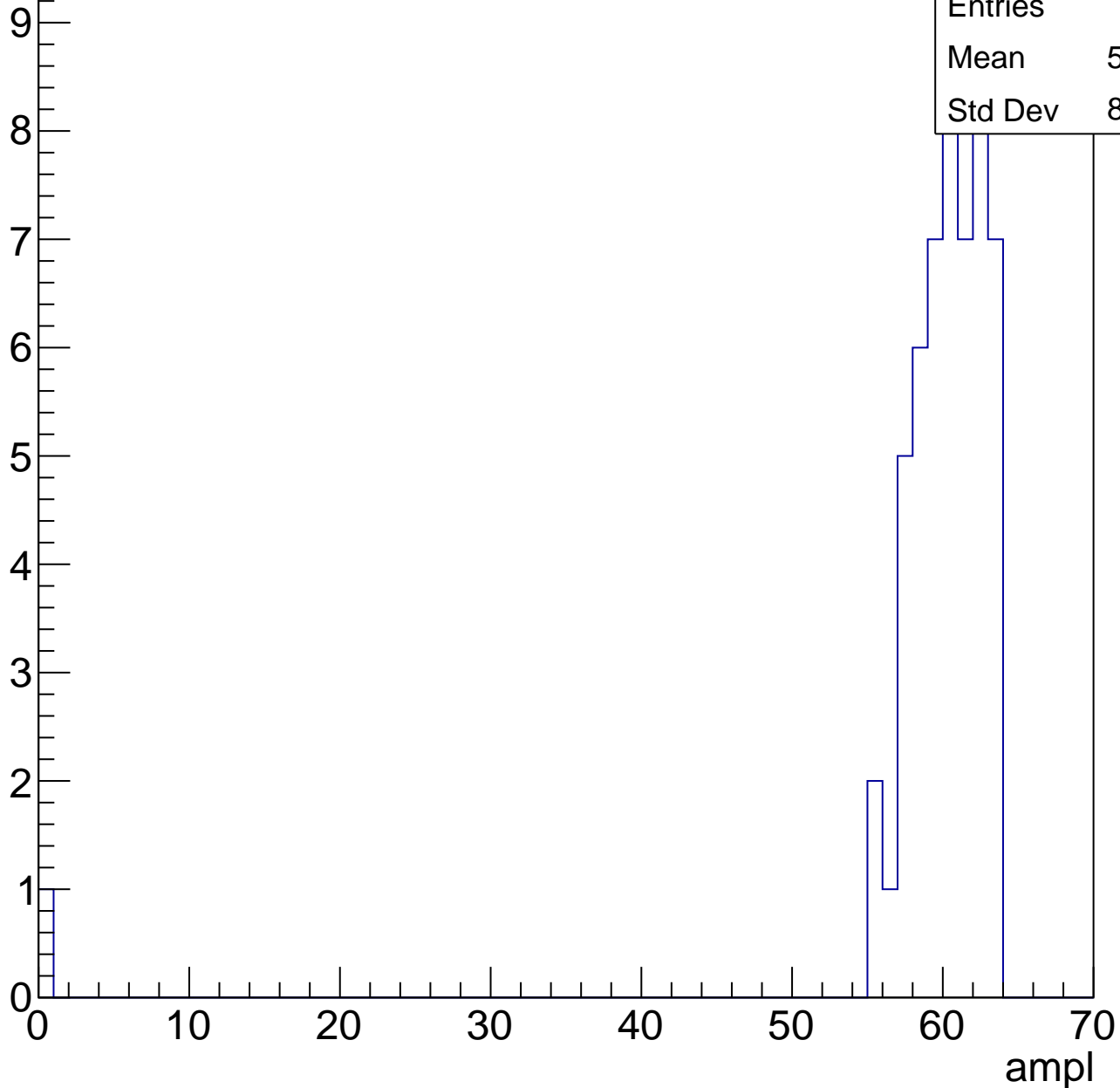
Entries	57
Mean	54.63
Std Dev	3.333



B0L000S, U2-ch122, adc5

calib_packv5_042523_0143.root, FC#5, port B1

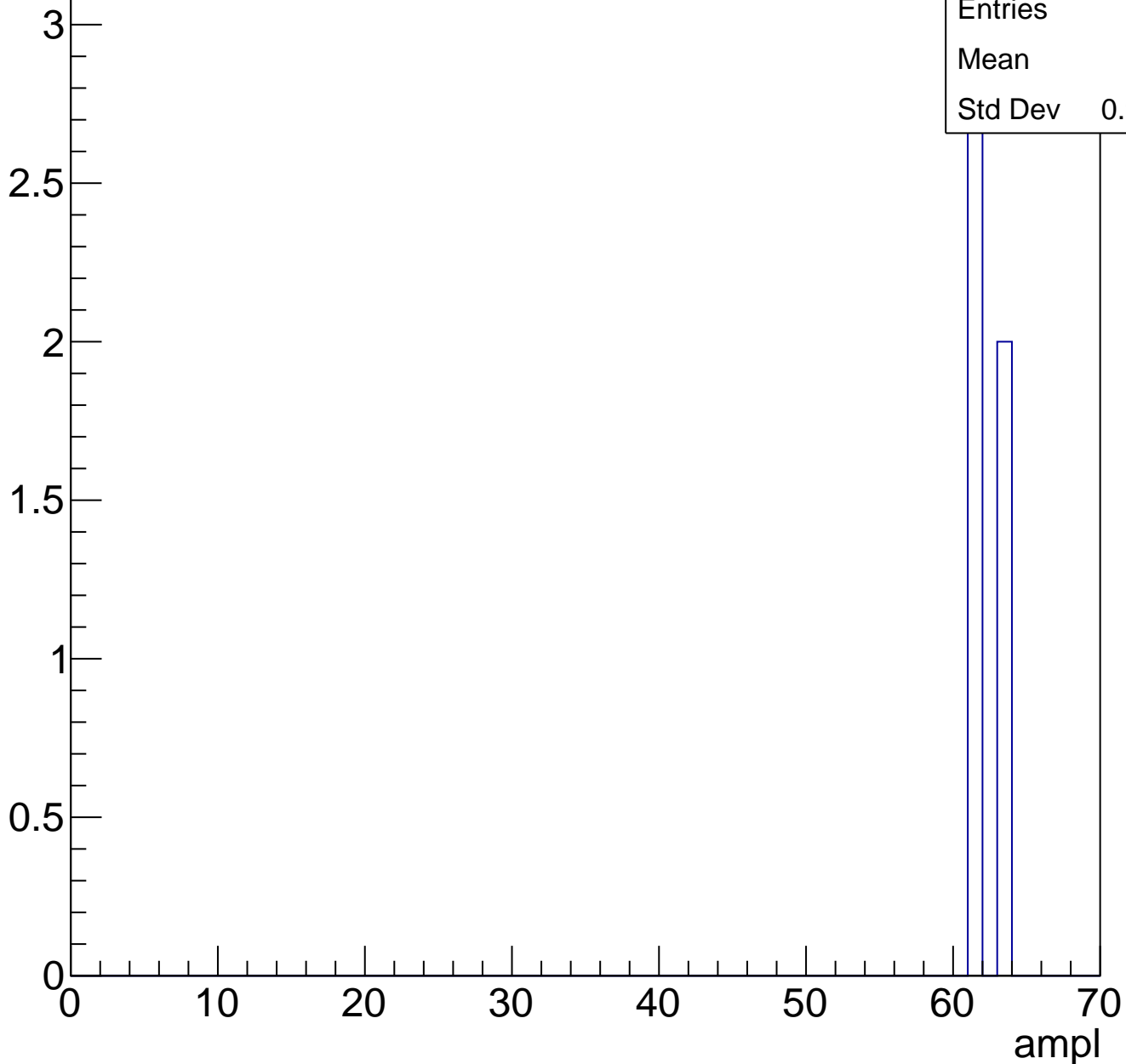
Entry



B0L000S, U2-ch122, adc6

calib_packv5_042523_0143.root, FC#5, port B1

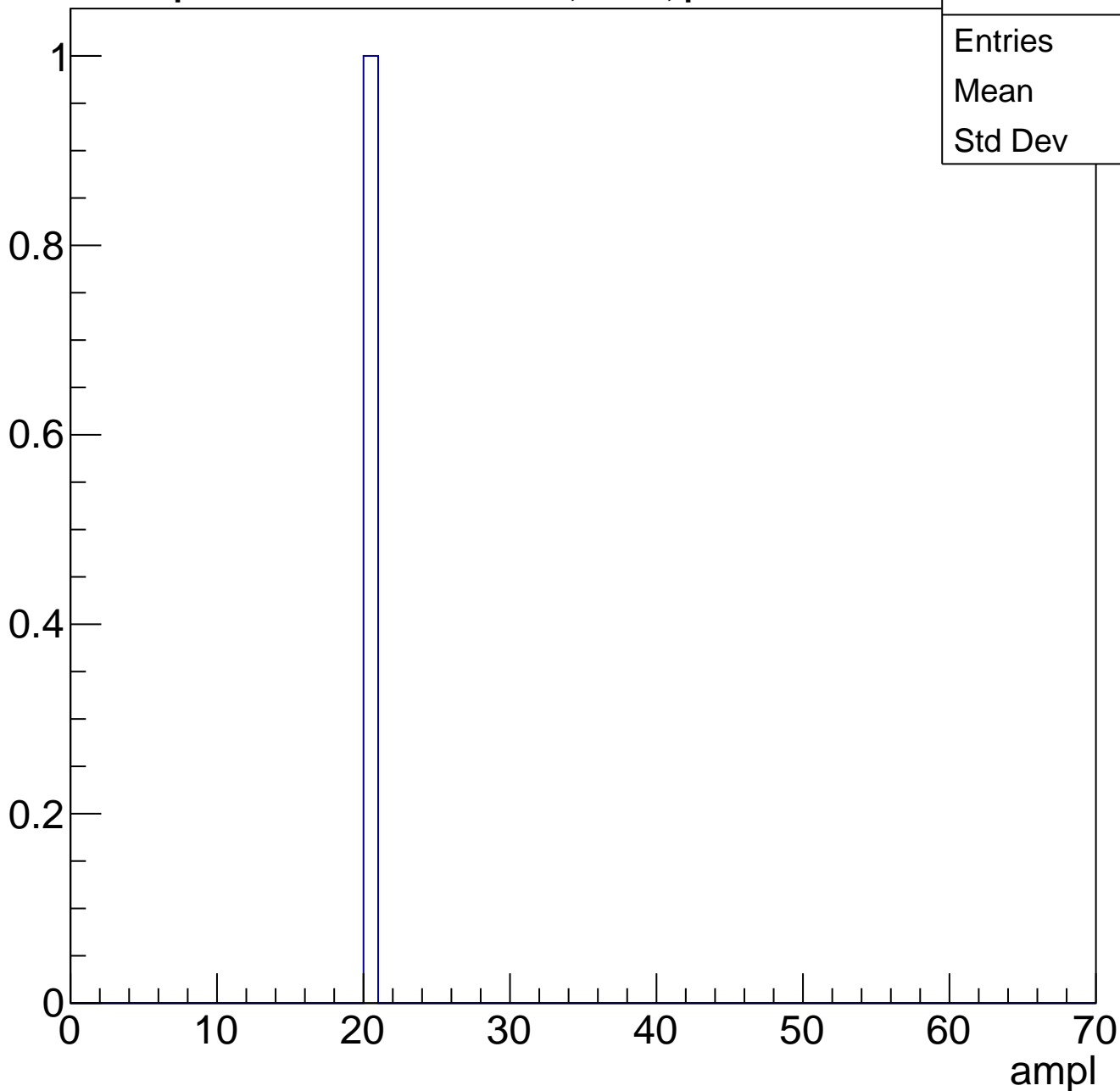
Entry



B0L000S, U2-ch122, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



Entries	1
Mean	20
Std Dev	0

B0L000S, U2-ch123, adc0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	81
Mean	28.91
Std Dev	3.577

Gaus mean : 29.5400

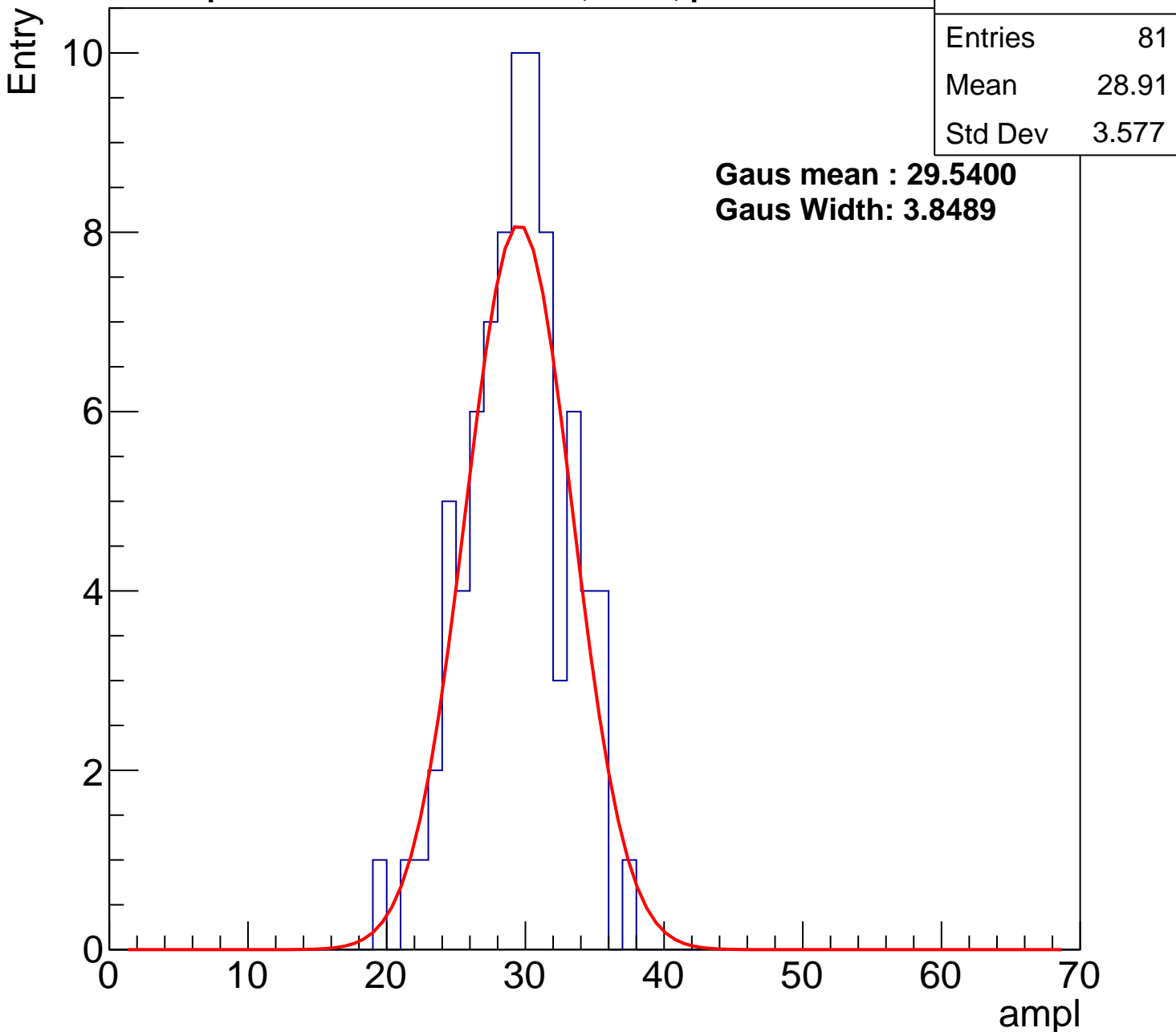
Gaus Width: 3.8489

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U2-ch123, adc1

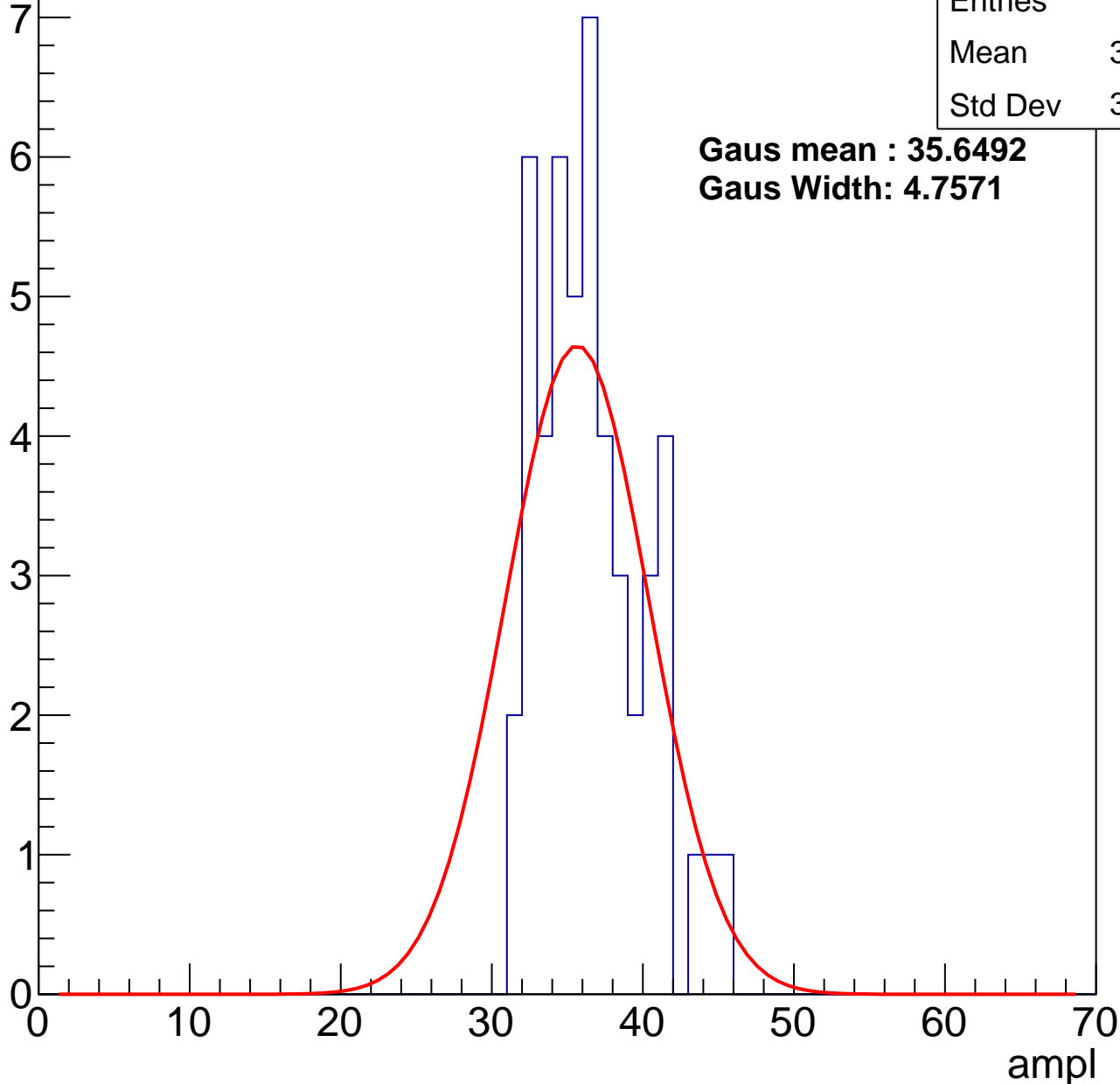
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.18
Std Dev	3.456

Gaus mean : 35.6492

Gaus Width: 4.7571



B0L000S, U2-ch123, adc2

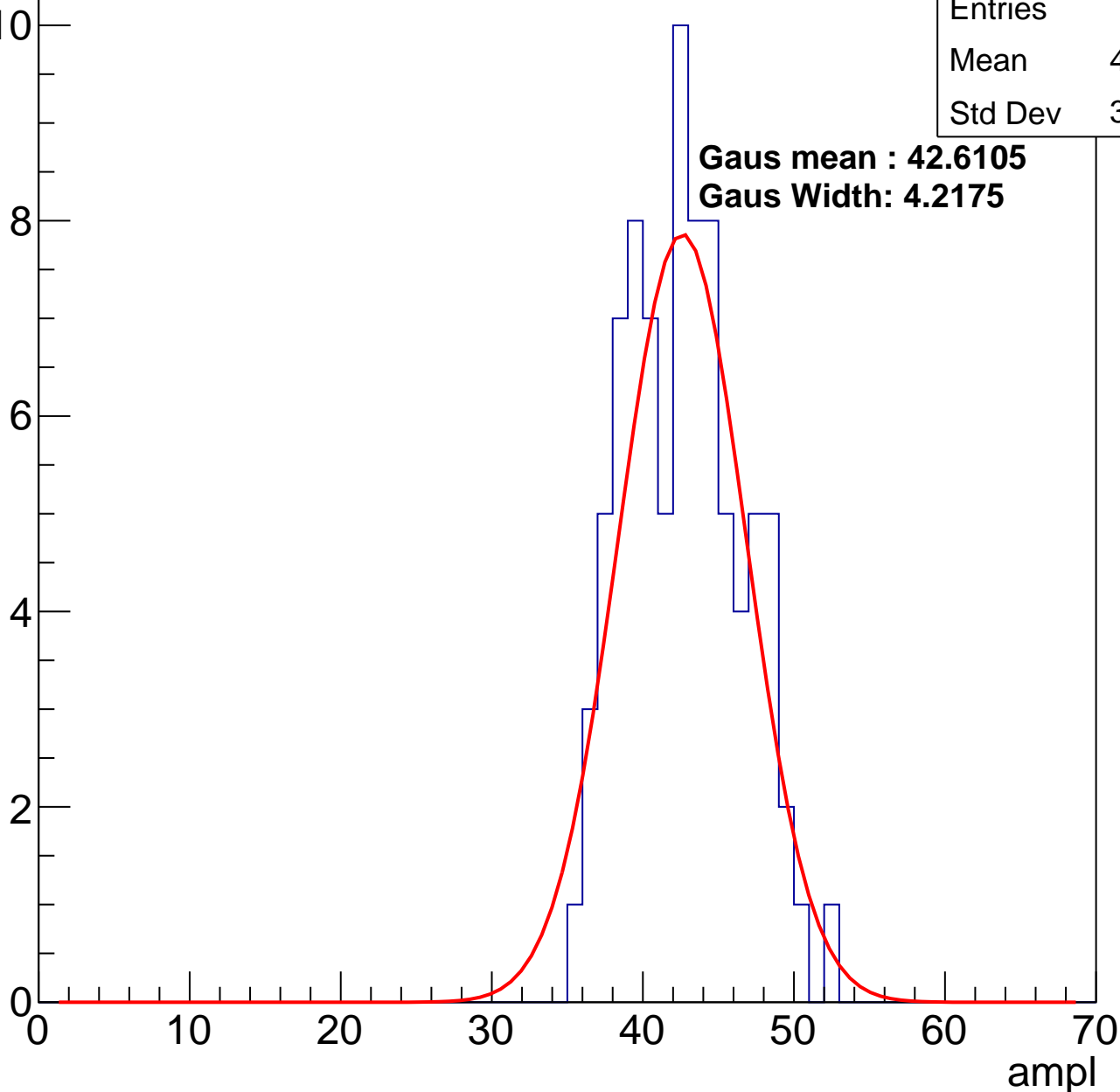
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	85
Mean	42.25
Std Dev	3.773

Gaus mean : 42.6105

Gaus Width: 4.2175

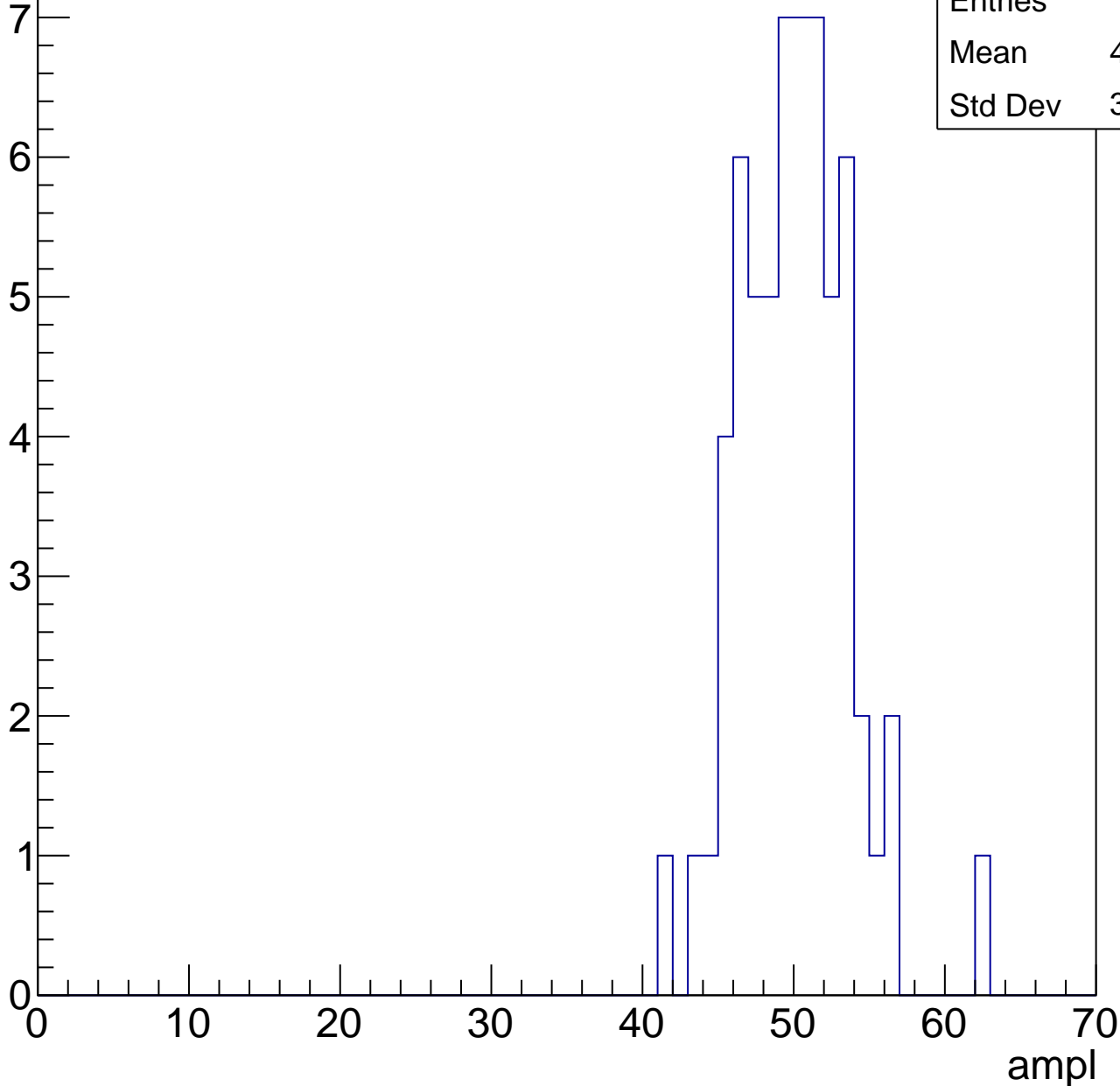


B0L000S, U2-ch123, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

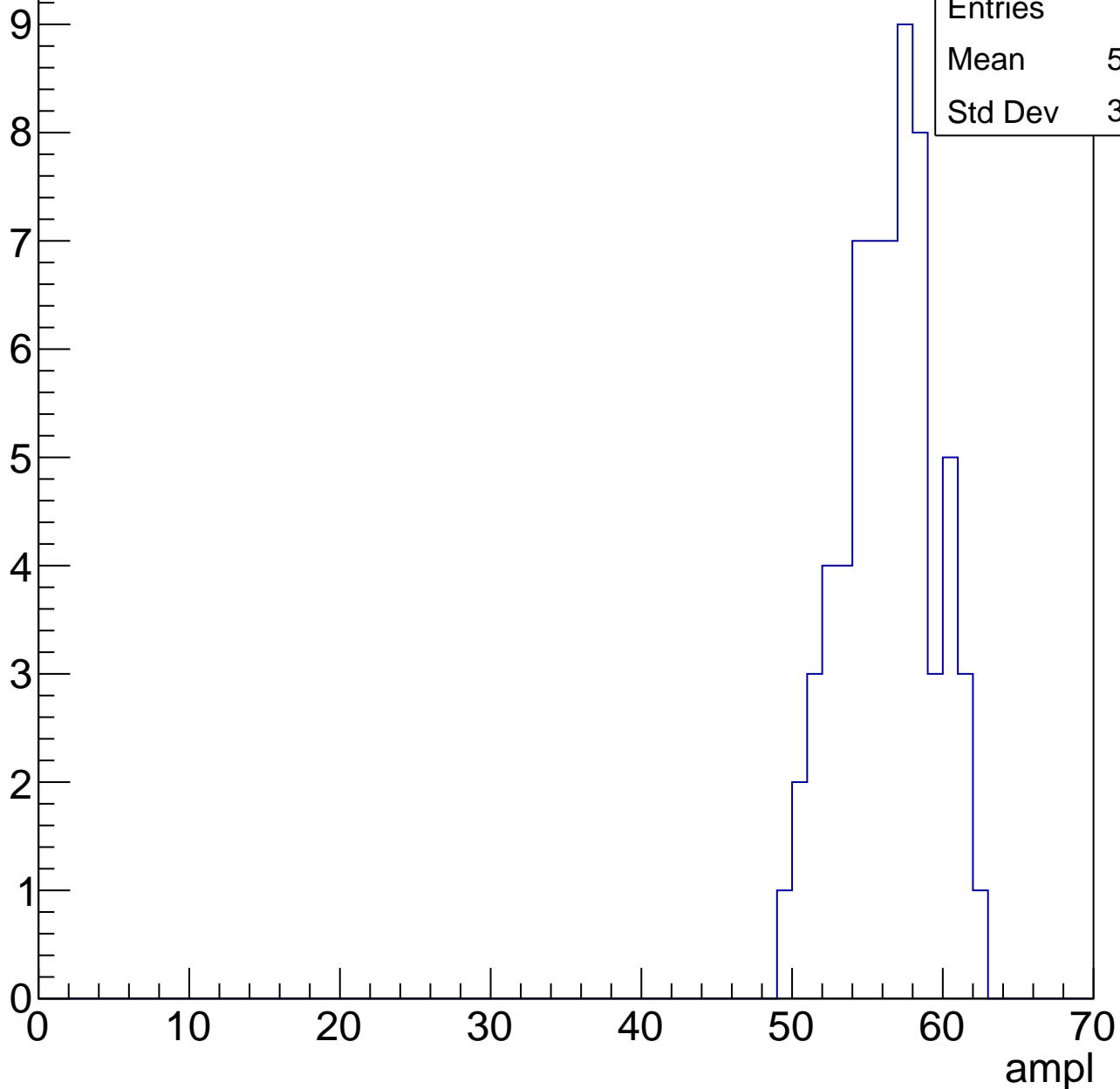
Entries	61
Mean	49.57
Std Dev	3.569



B0L000S, U2-ch123, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry



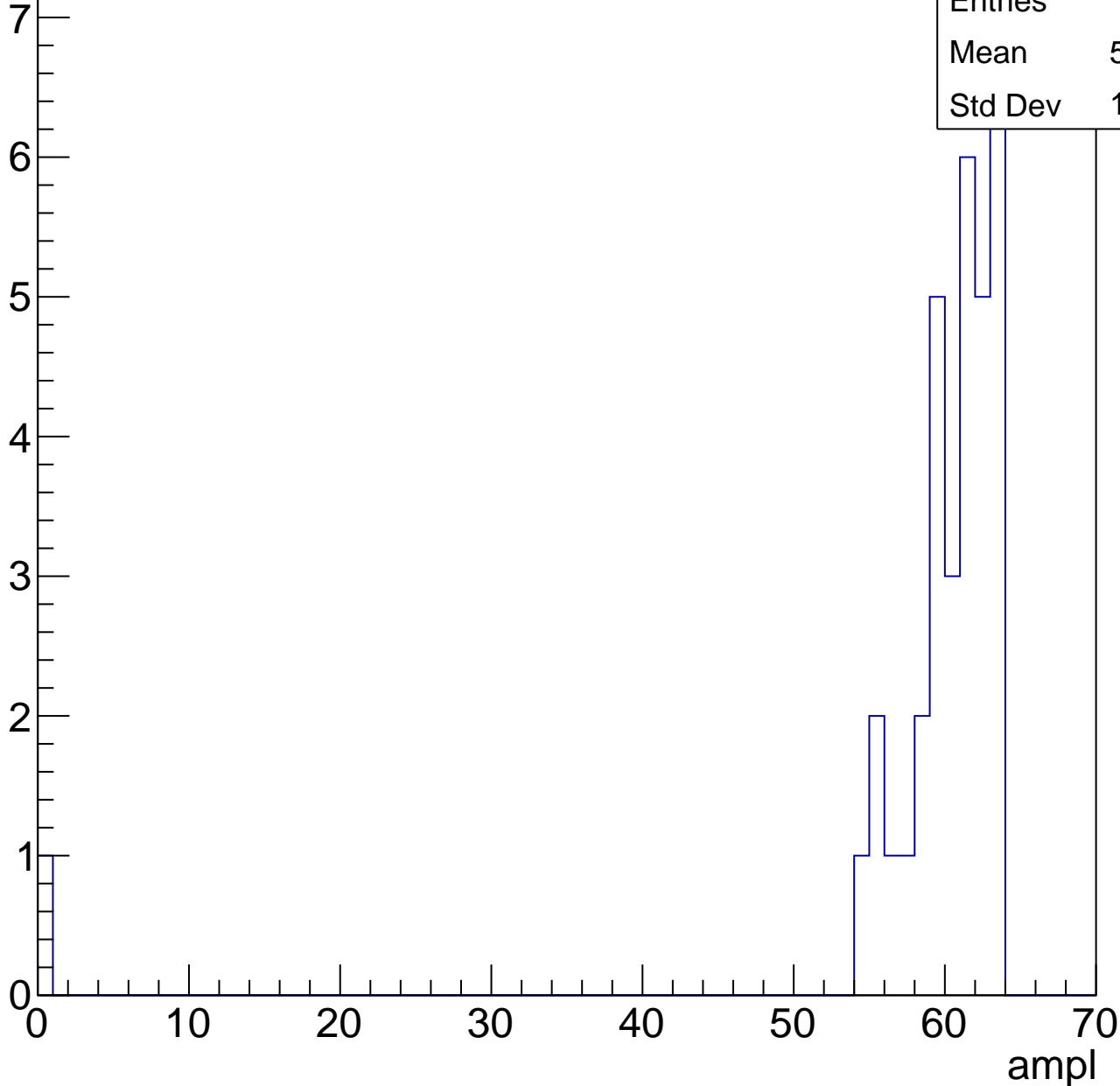
Entries	64
Mean	55.88
Std Dev	3.034

B0L000S, U2-ch123, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

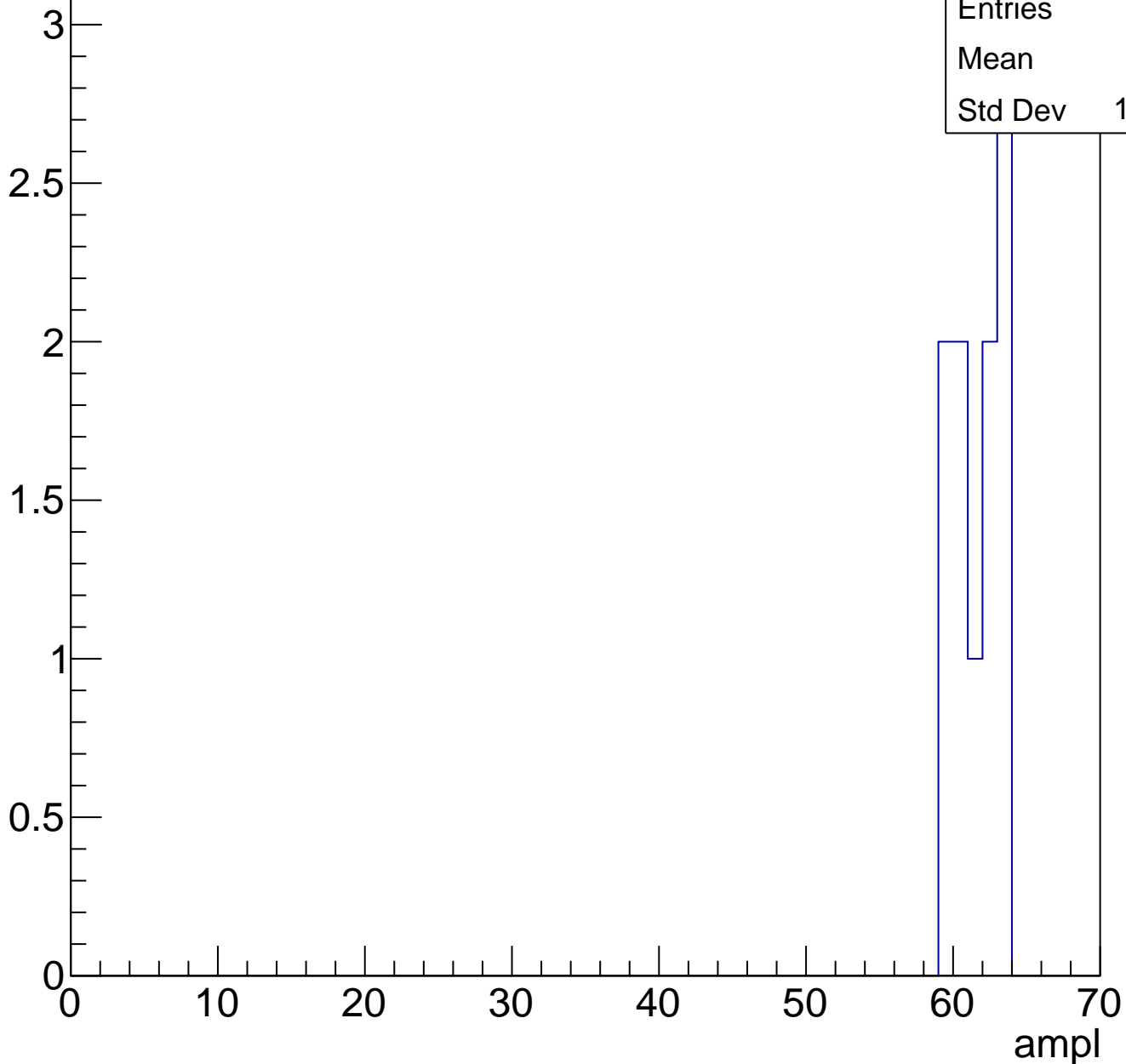
Entries	34
Mean	58.38
Std Dev	10.47



B0L000S, U2-ch123, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch123, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch124, adc0

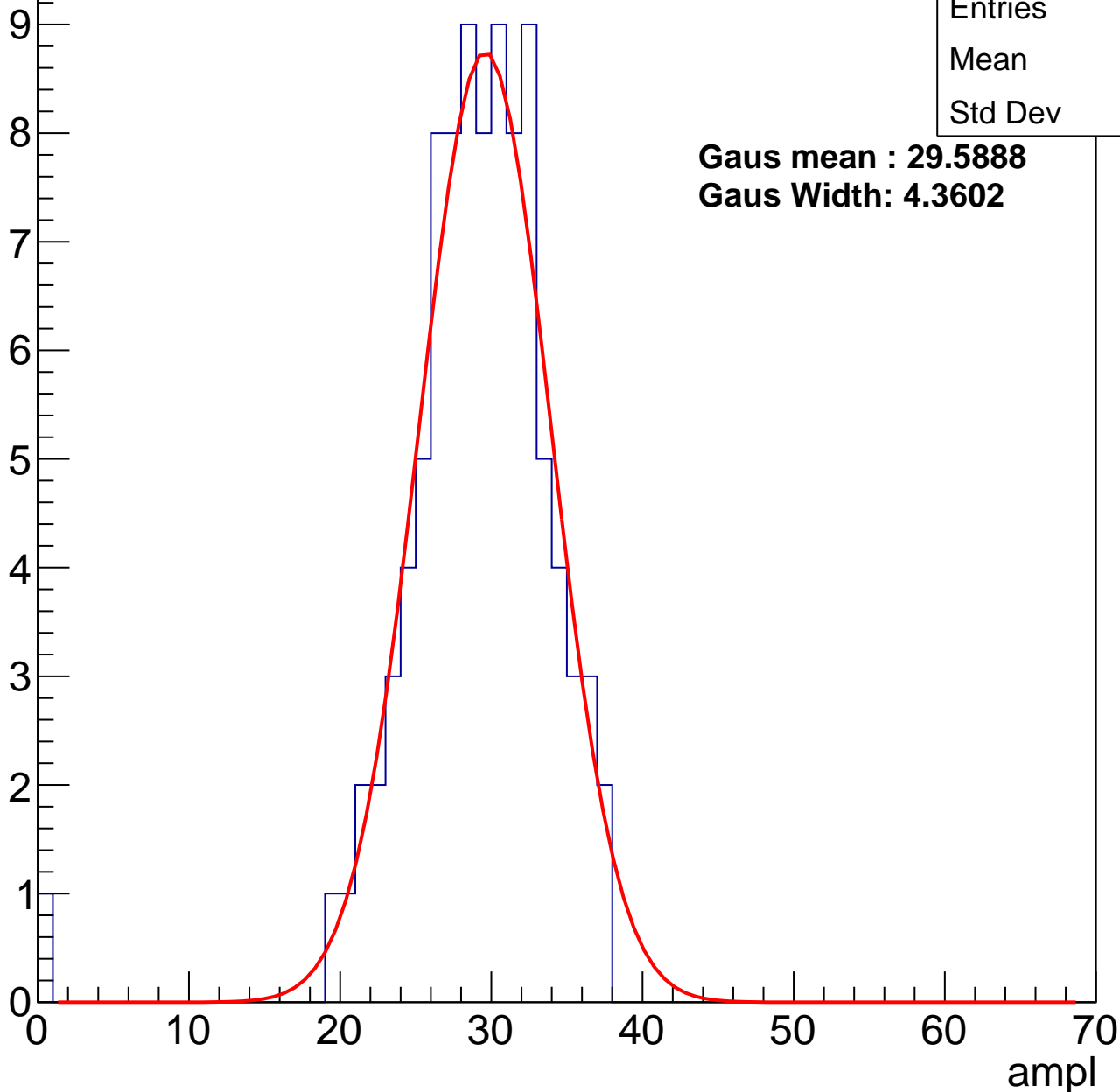
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	95
Mean	28.6
Std Dev	4.94

Gaus mean : 29.5888

Gaus Width: 4.3602



B0L000S, U2-ch124, adc1

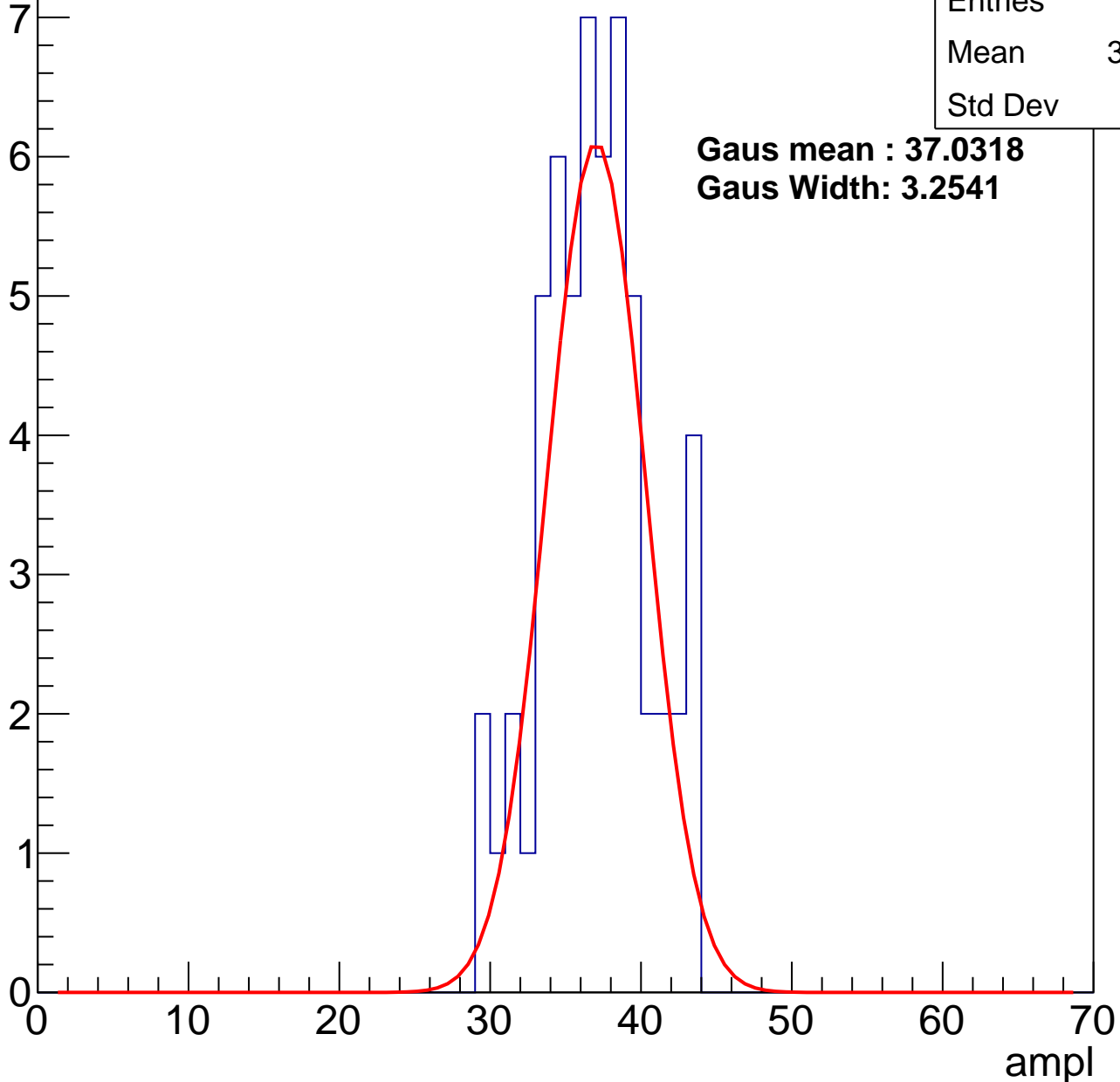
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	36.47
Std Dev	3.48

Gaus mean : 37.0318

Gaus Width: 3.2541



B0L000S, U2-ch124, adc2

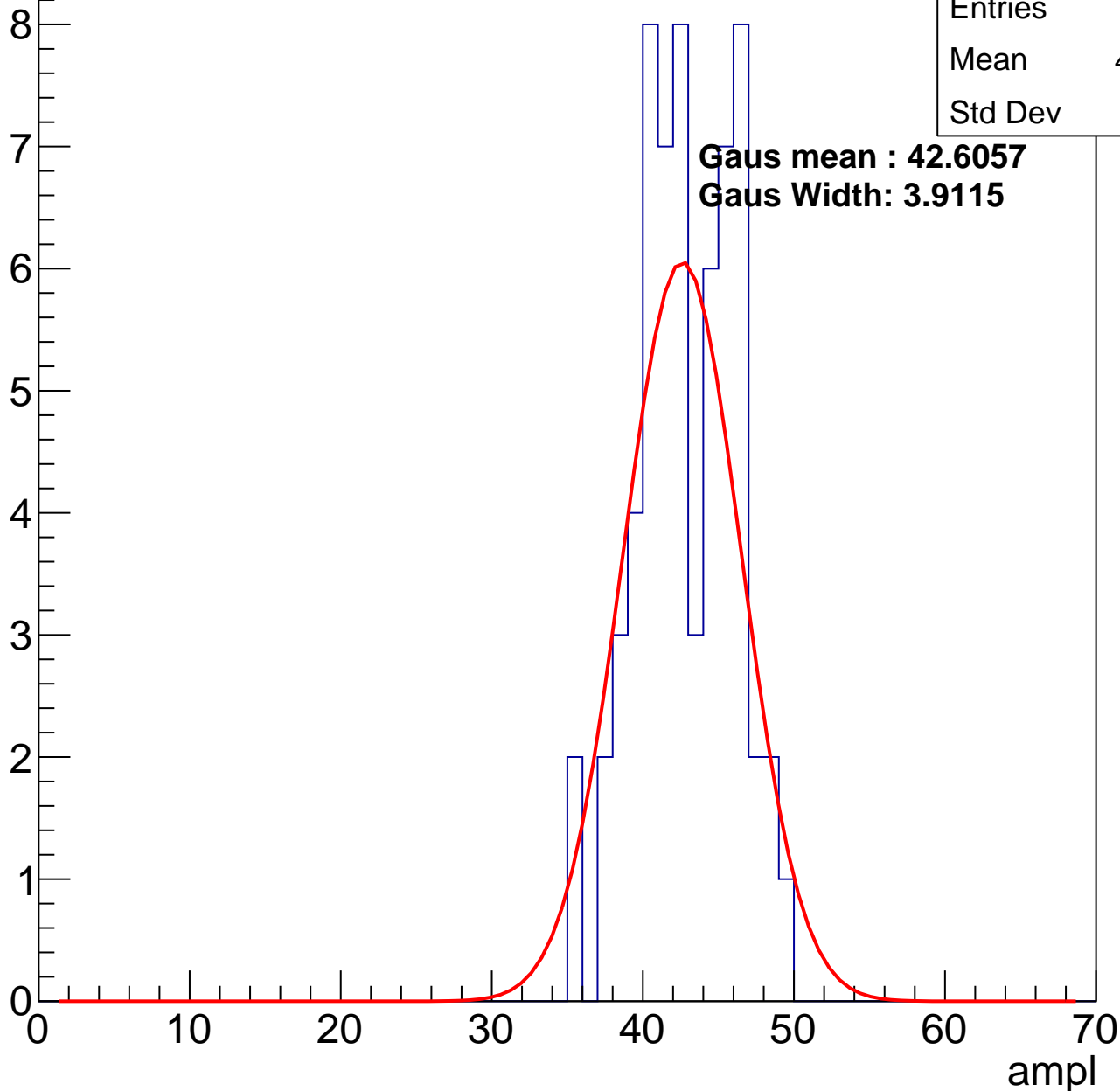
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.41
Std Dev	3.21

Gaus mean : 42.6057

Gaus Width: 3.9115

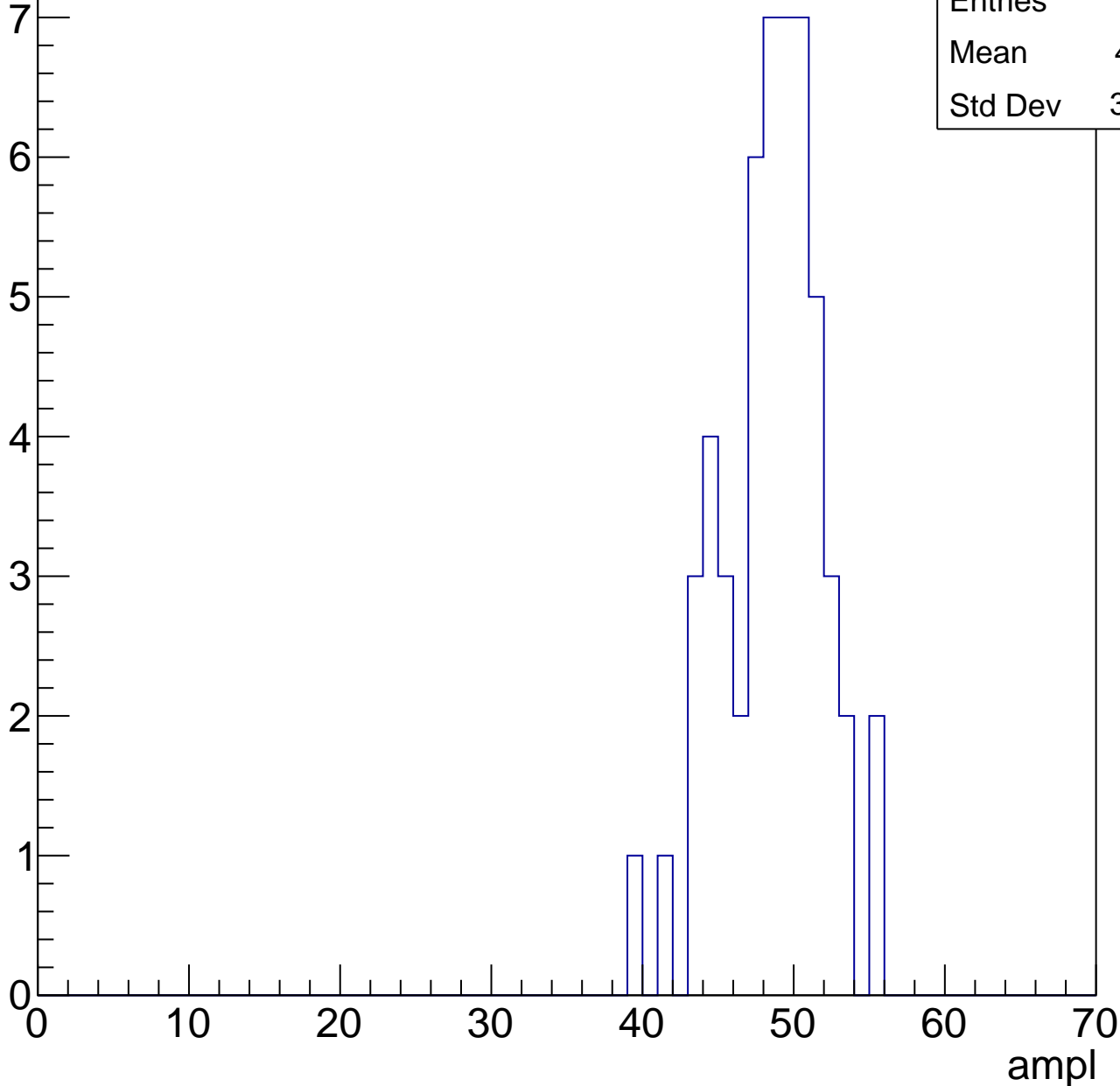


B0L000S, U2-ch124, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	53
Mean	48.11
Std Dev	3.323

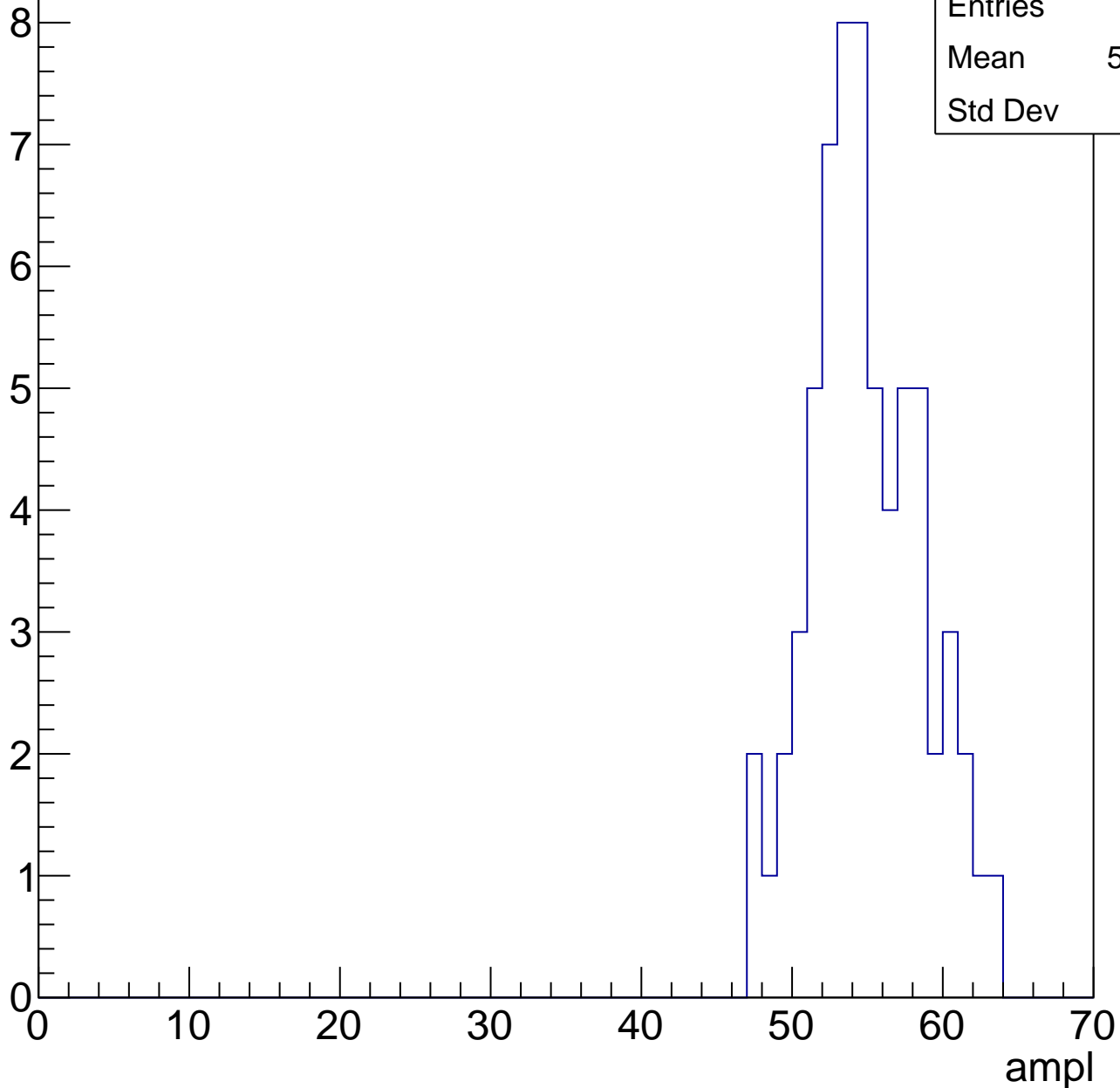


B0L000S, U2-ch124, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.44
Std Dev	3.64

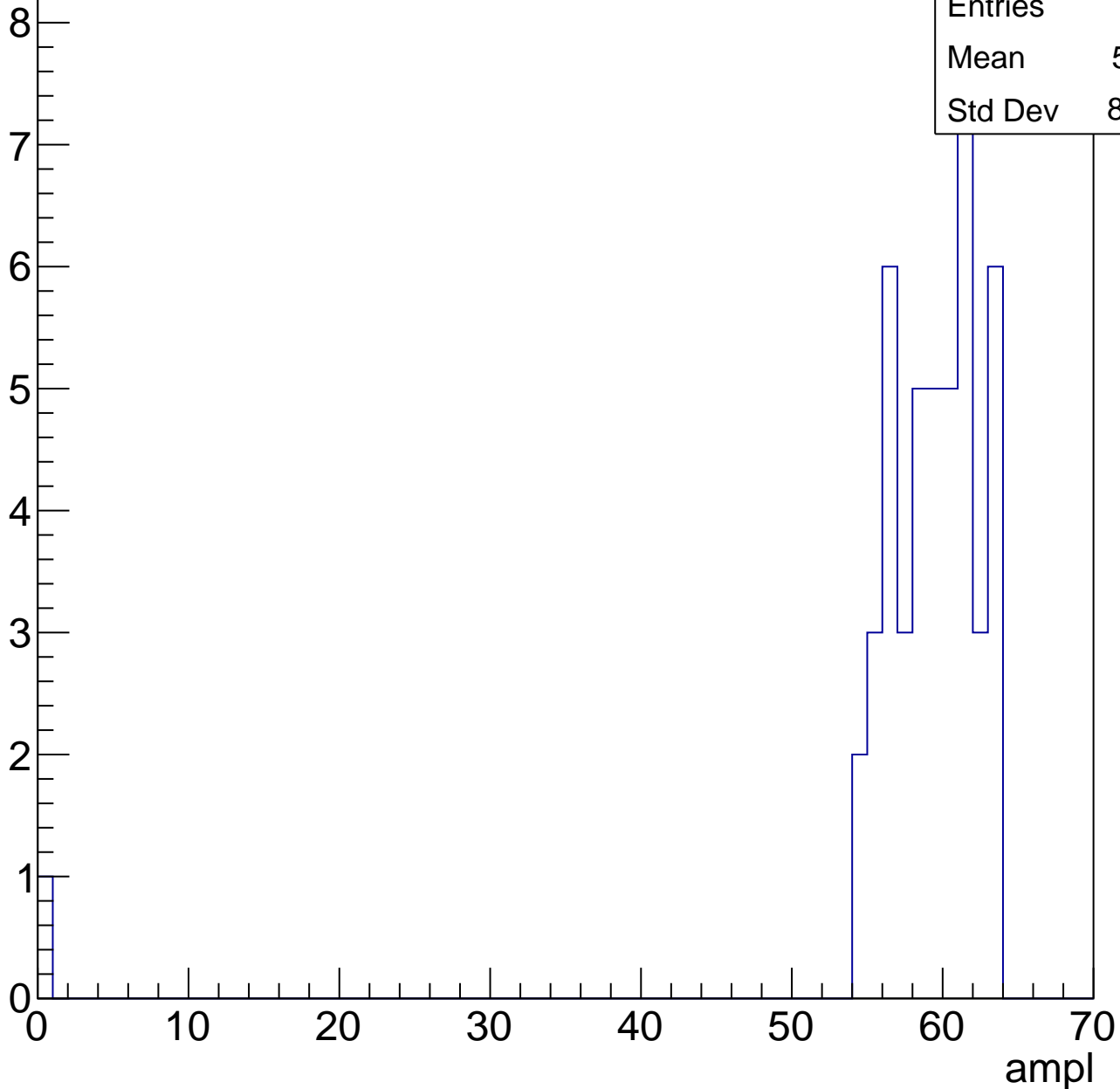


B0L000S, U2-ch124, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	47
Mean	57.81
Std Dev	8.924

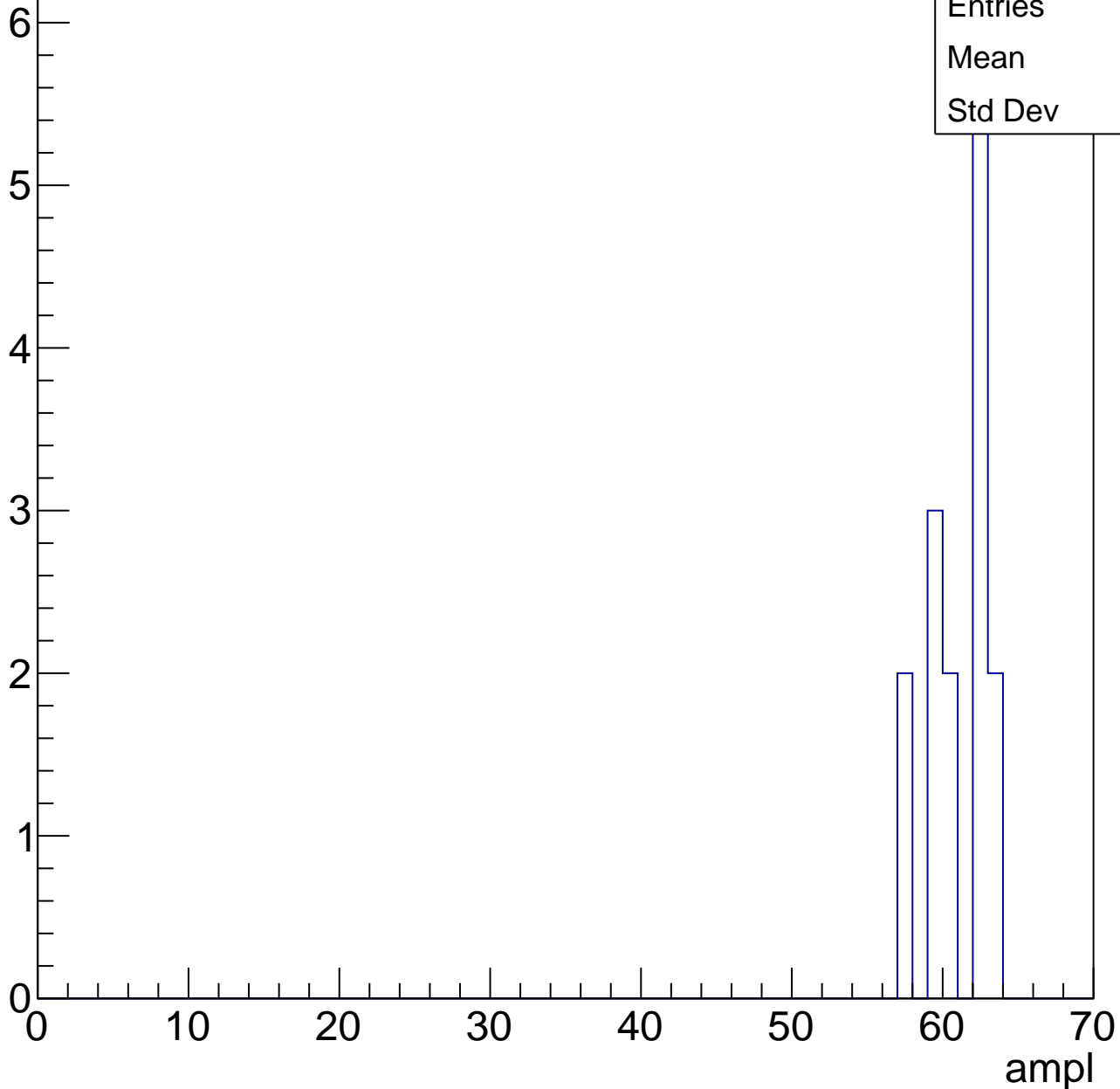


B0L000S, U2-ch124, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry

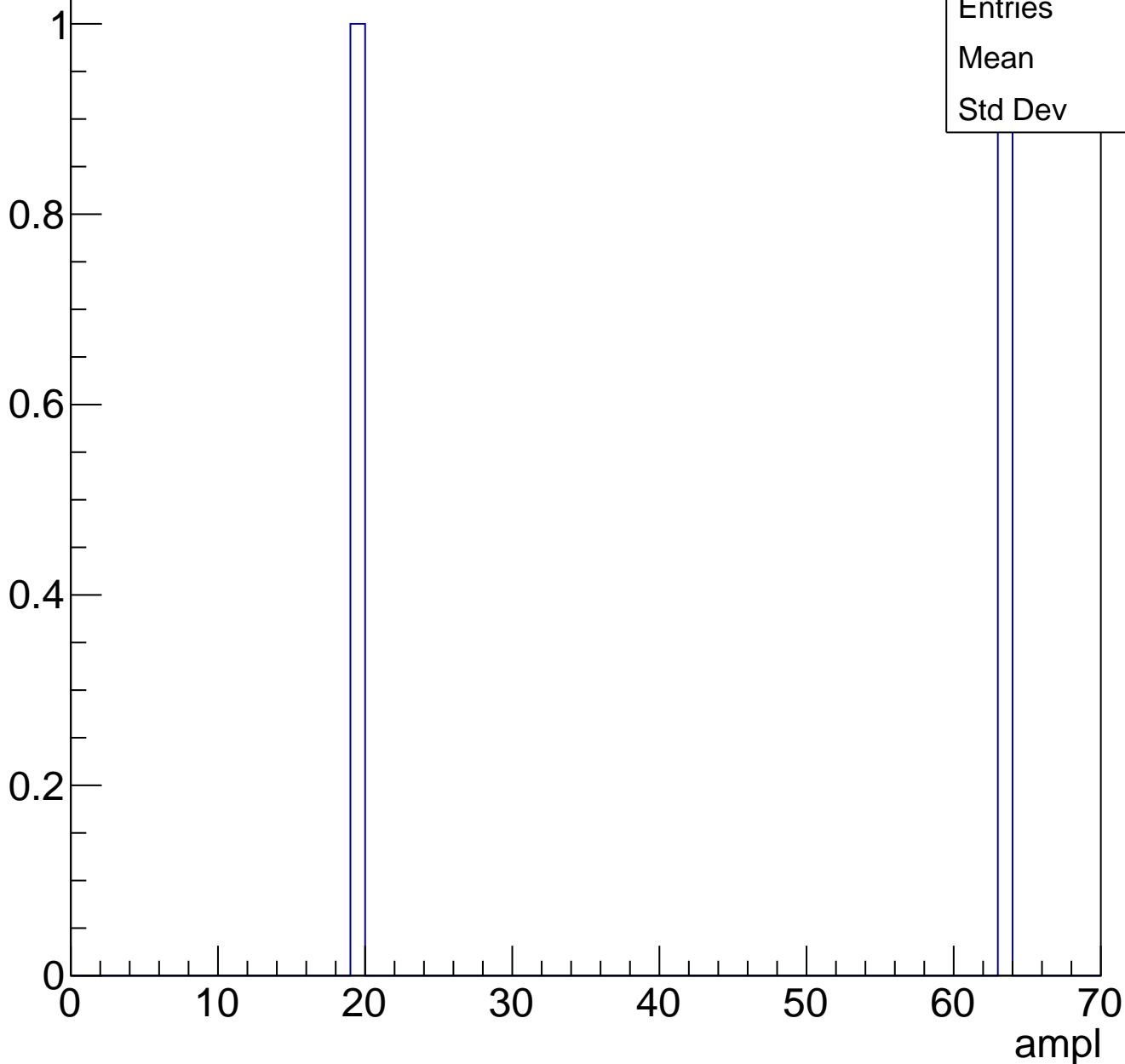
Entries	15
Mean	60.6
Std Dev	1.96



B0L000S, U2-ch124, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch125, adc0

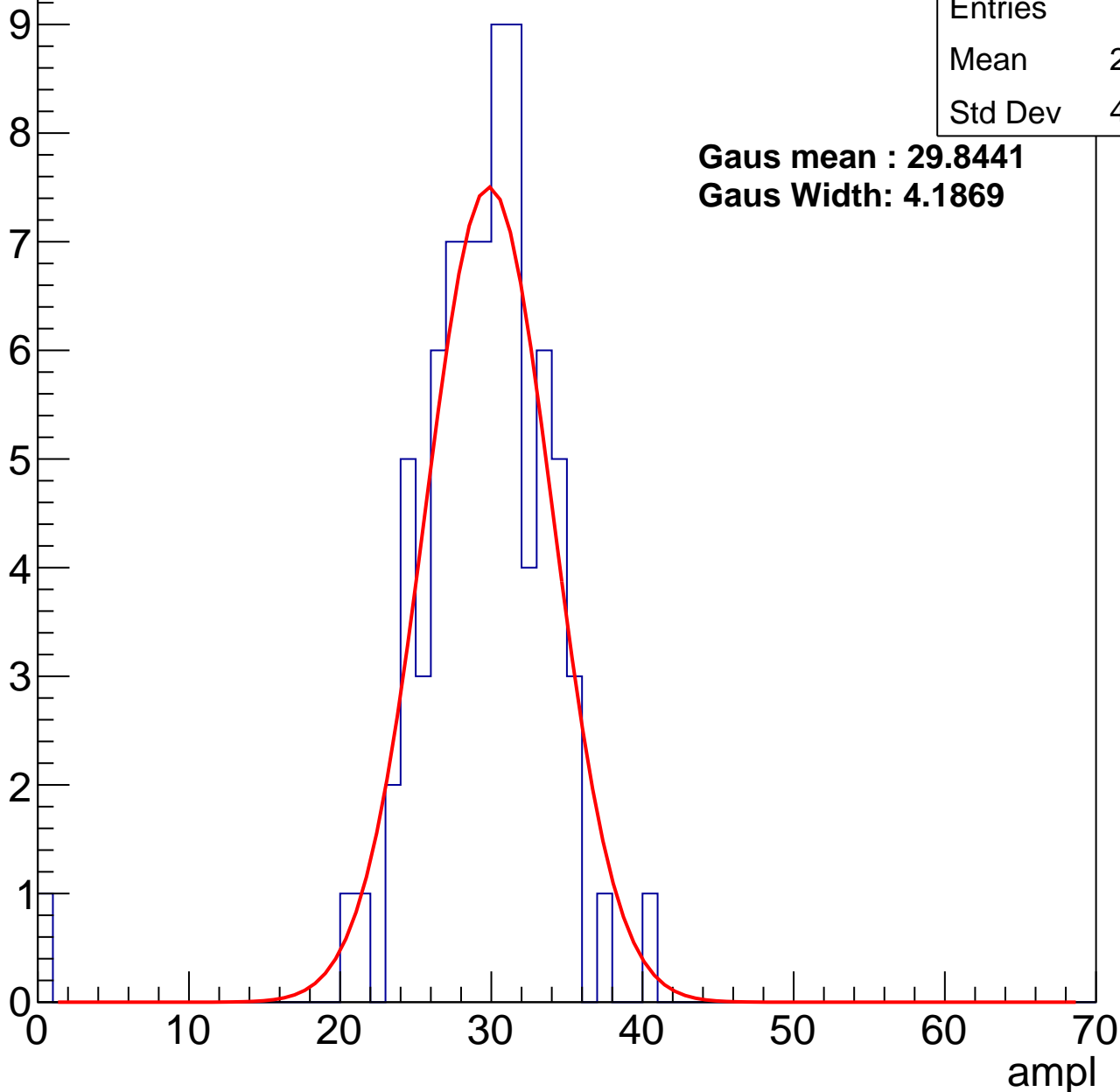
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.88
Std Dev	4.954

Gaus mean : 29.8441

Gaus Width: 4.1869



B0L000S, U2-ch125, adc1

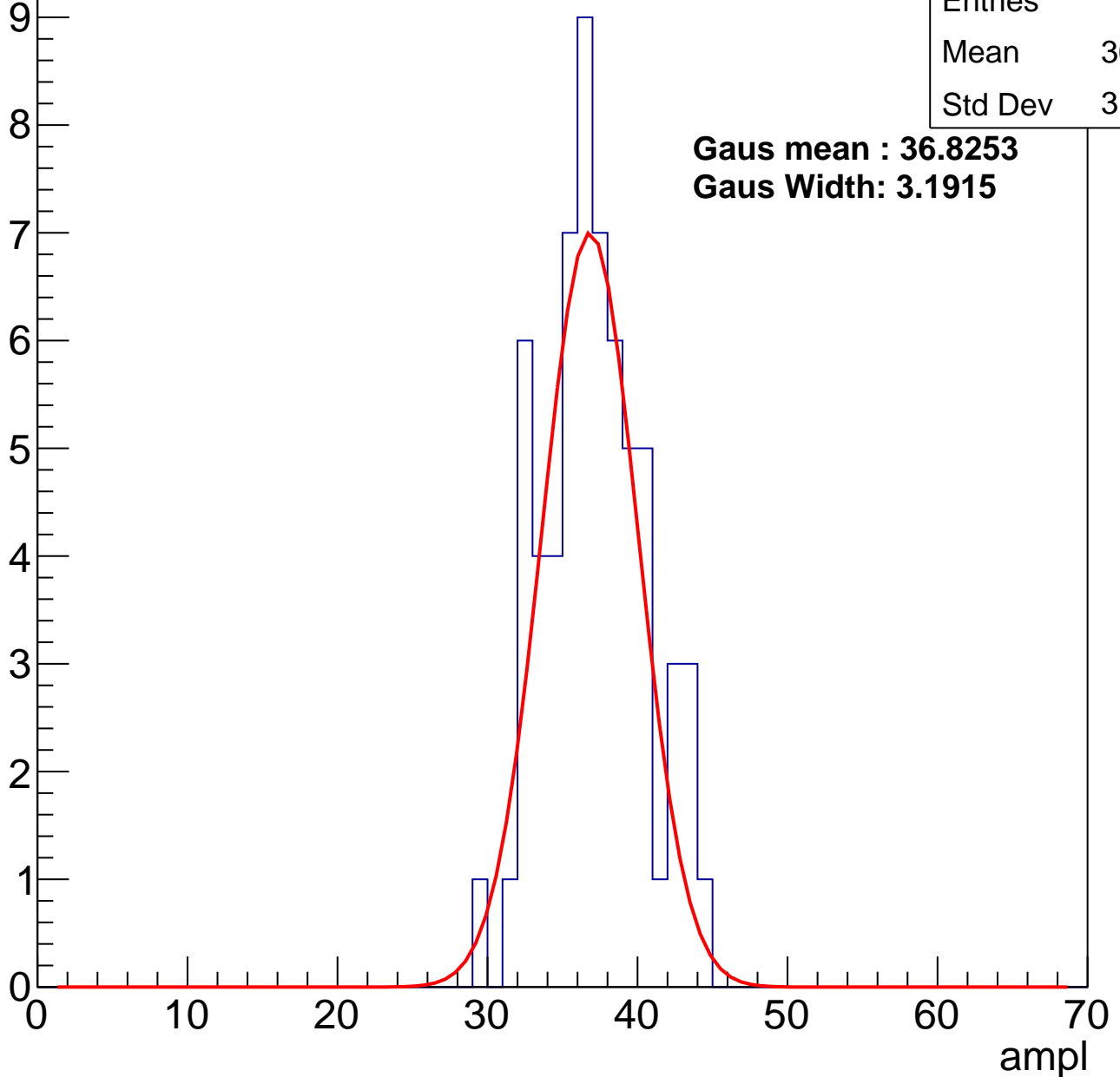
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.68
Std Dev	3.333

Gaus mean : 36.8253

Gaus Width: 3.1915



B0L000S, U2-ch125, adc2

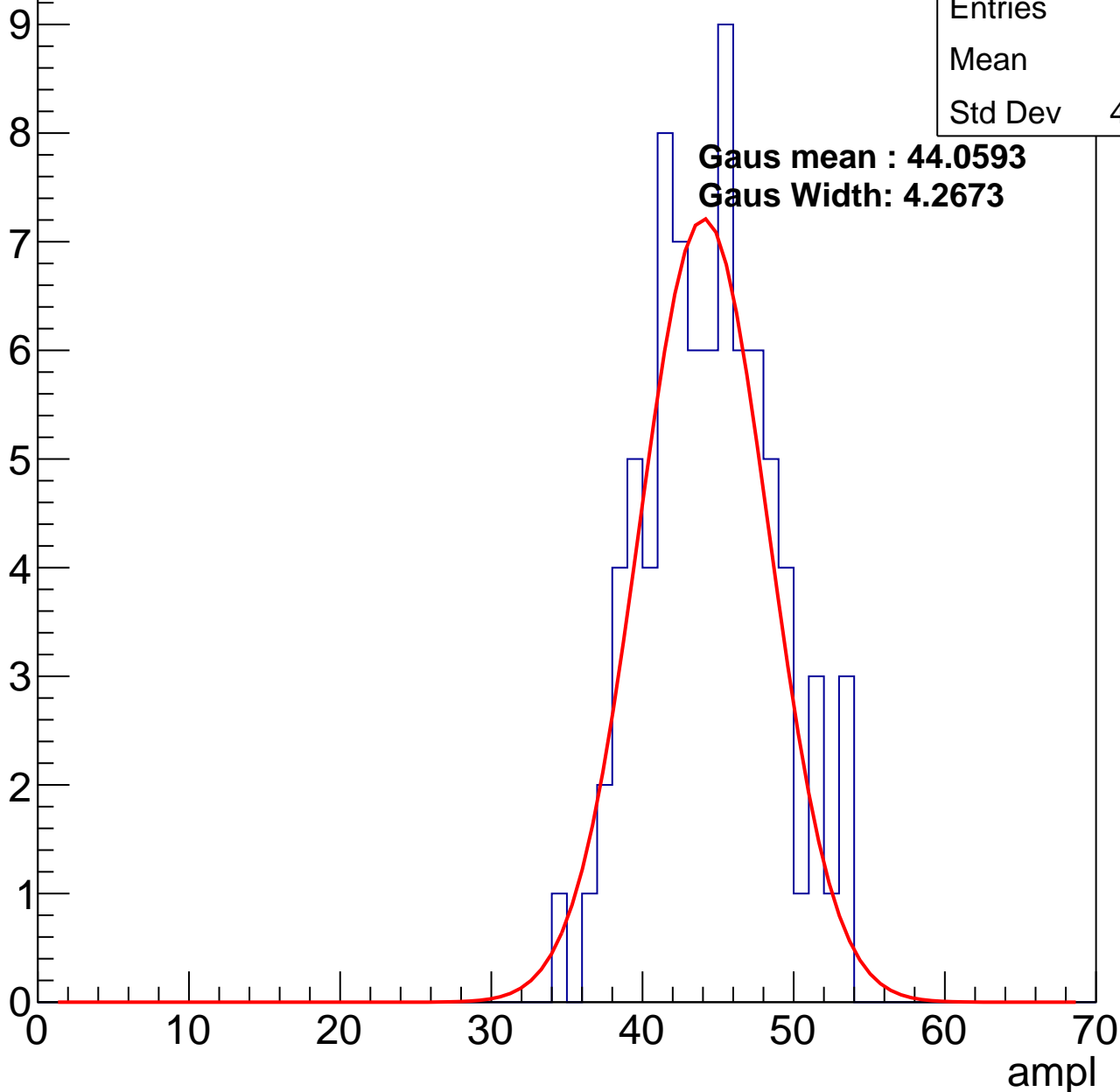
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	82
Mean	44
Std Dev	4.217

Gaus mean : 44.0593

Gaus Width: 4.2673

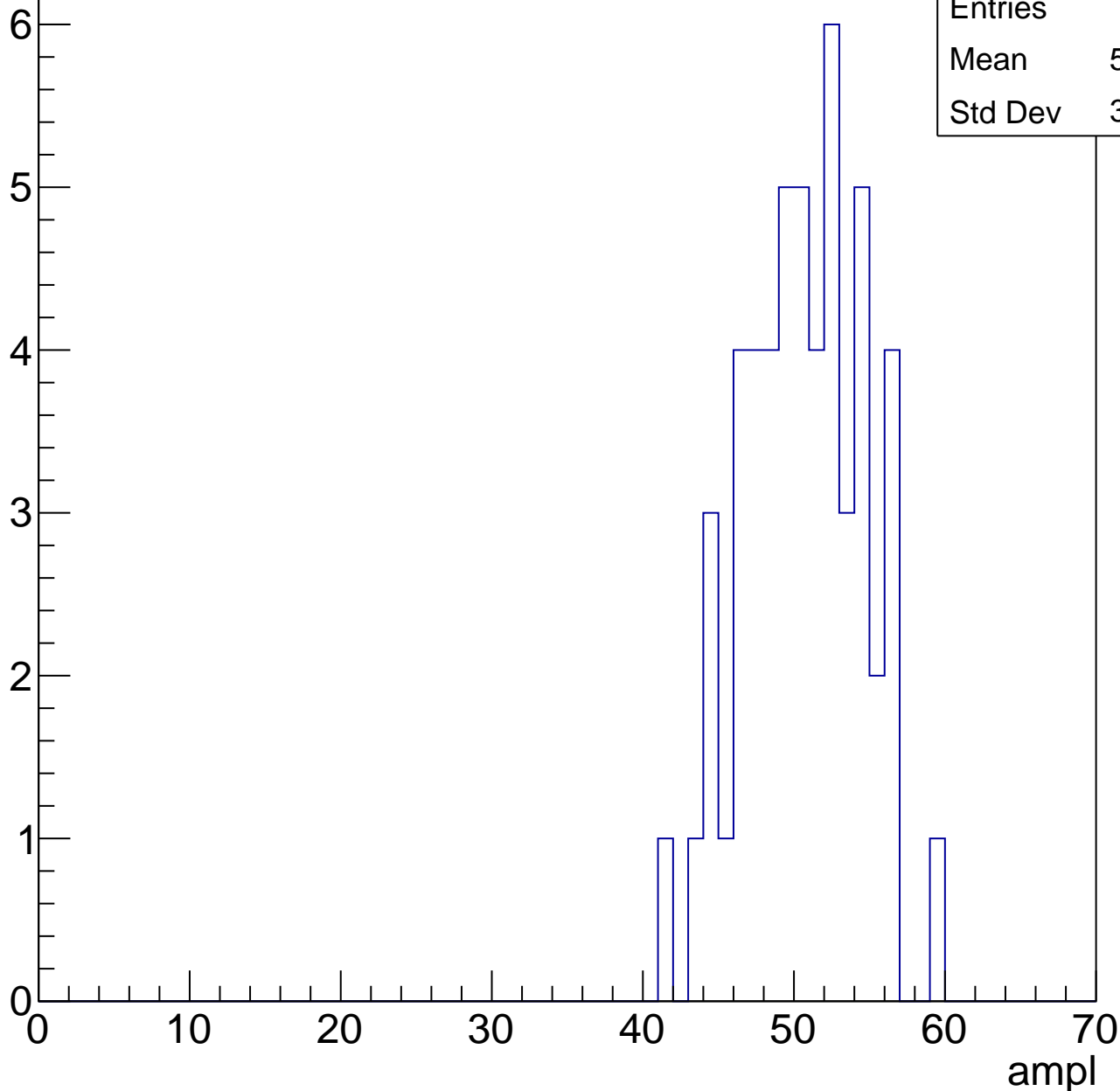


B0L000S, U2-ch125, adc3

calib_packv5_042523_0143.root, FC#5, port B1

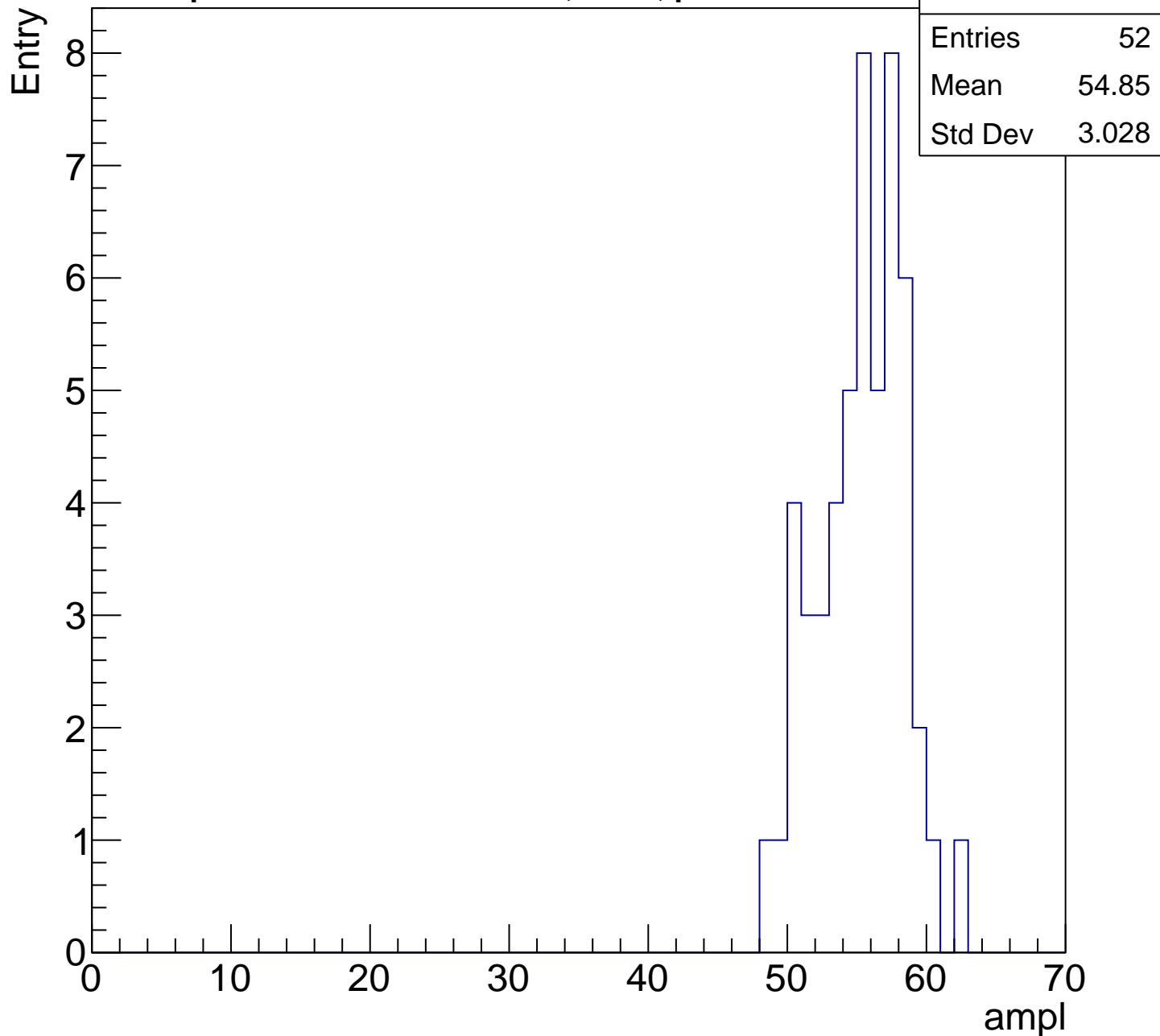
Entry

Entries	53
Mean	50.15
Std Dev	3.868



B0L000S, U2-ch125, adc4

calib_packv5_042523_0143.root, FC#5, port B1

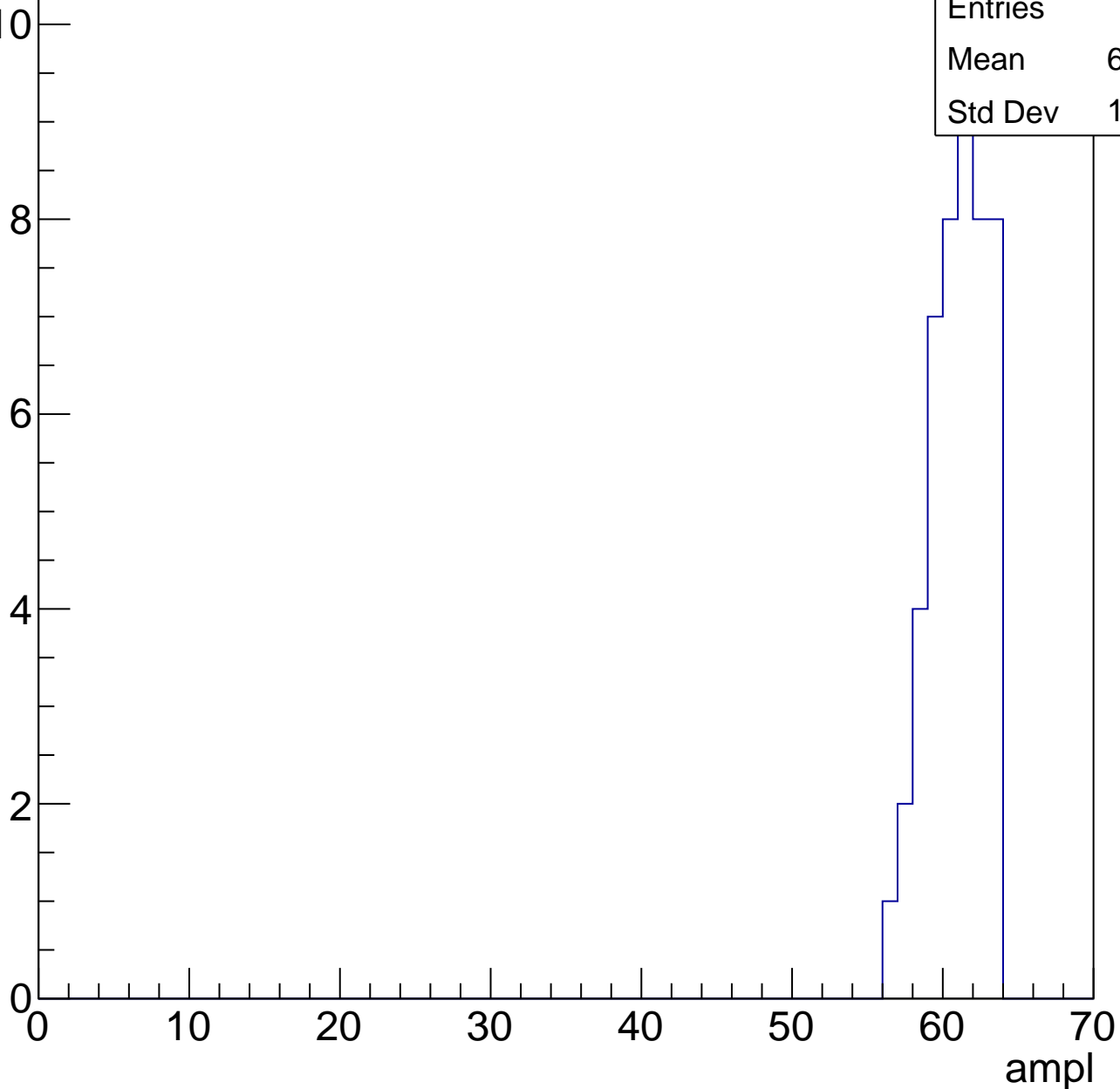


B0L000S, U2-ch125, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	60.52
Std Dev	1.814



B0L000S, U2-ch125, adc6

calib_packv5_042523_0143.root, FC#5, port B1

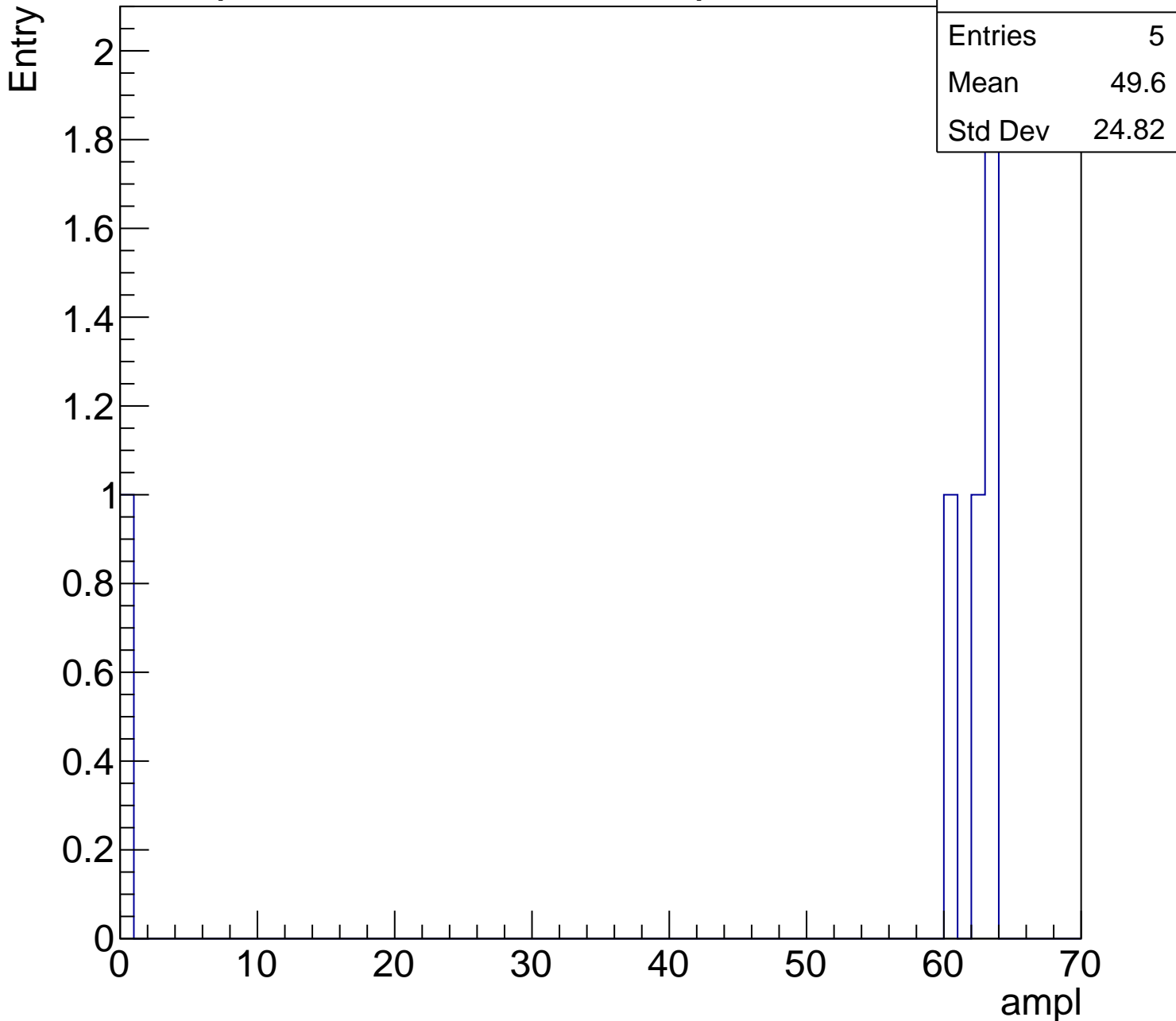
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.6
Std Dev	24.82

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch125, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch126, adc0

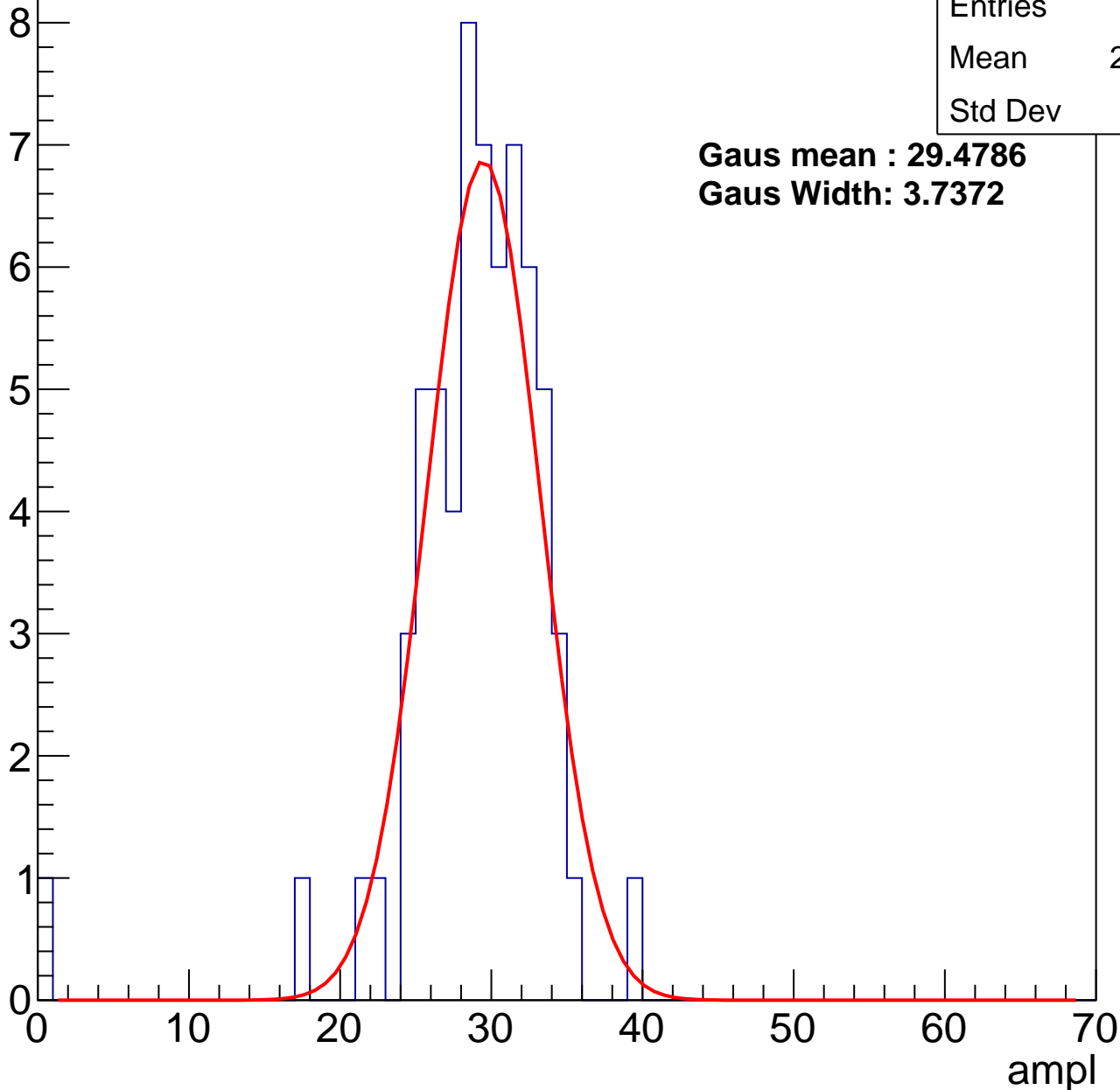
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	65
Mean	28.49
Std Dev	5.09

Gaus mean : 29.4786

Gaus Width: 3.7372



B0L000S, U2-ch126, adc1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	76
Mean	36.01
Std Dev	3.764

Gaus mean : 36.3139

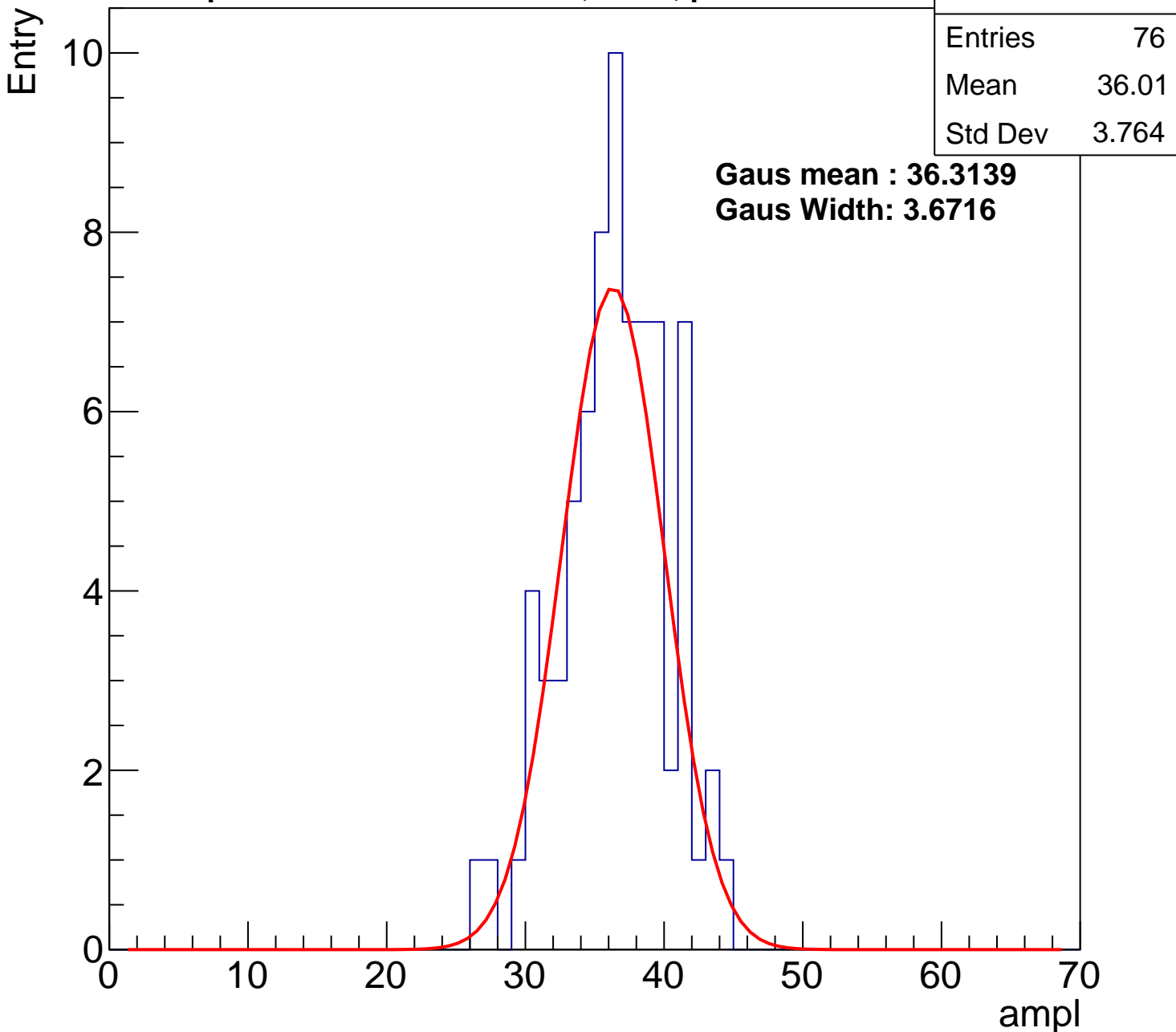
Gaus Width: 3.6716

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U2-ch126, adc2

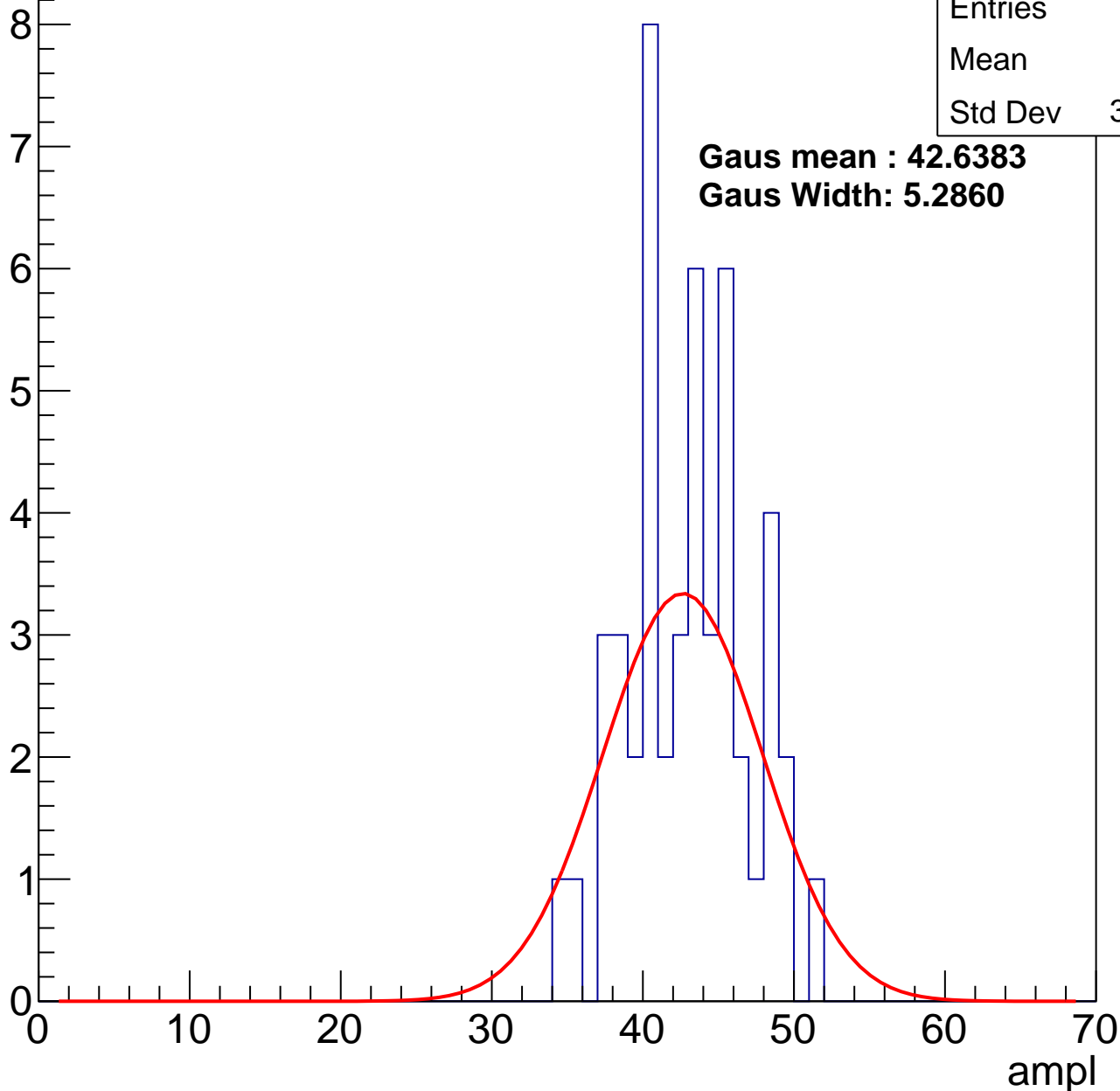
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	48
Mean	42.5
Std Dev	3.894

Gaus mean : 42.6383

Gaus Width: 5.2860

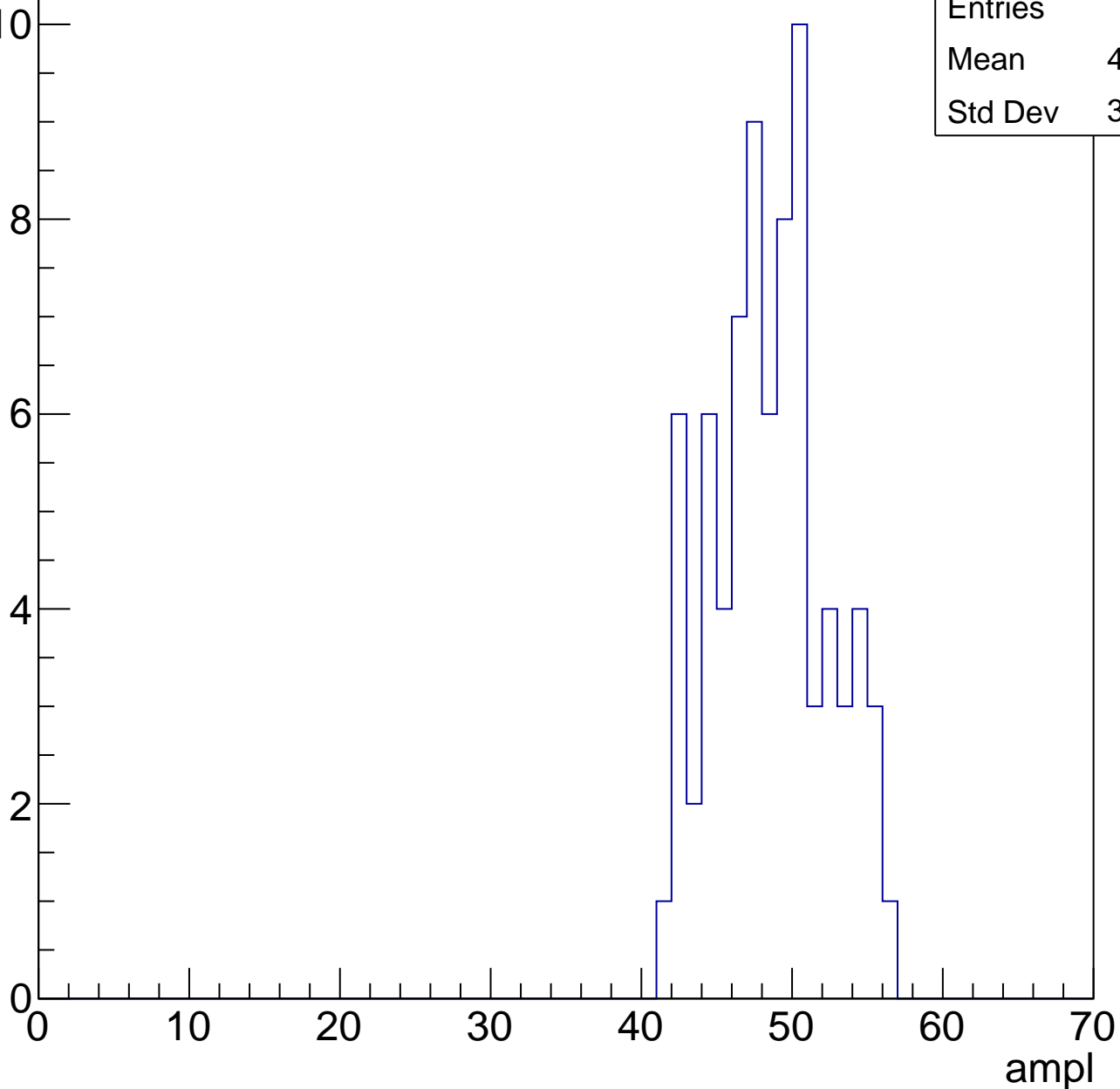


B0L000S, U2-ch126, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	77
Mean	48.12
Std Dev	3.696

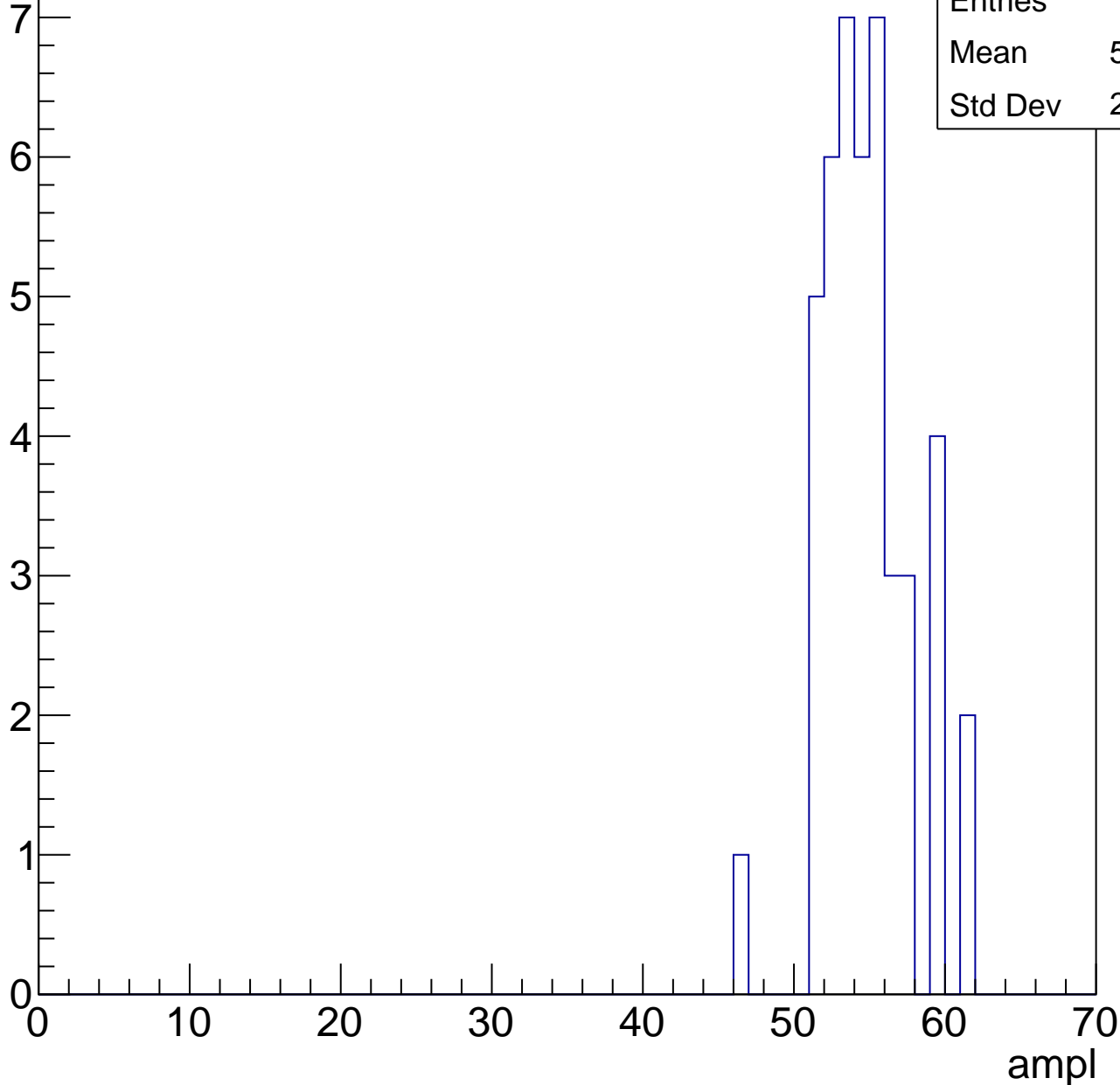


B0L000S, U2-ch126, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	44
Mean	54.32
Std Dev	2.937

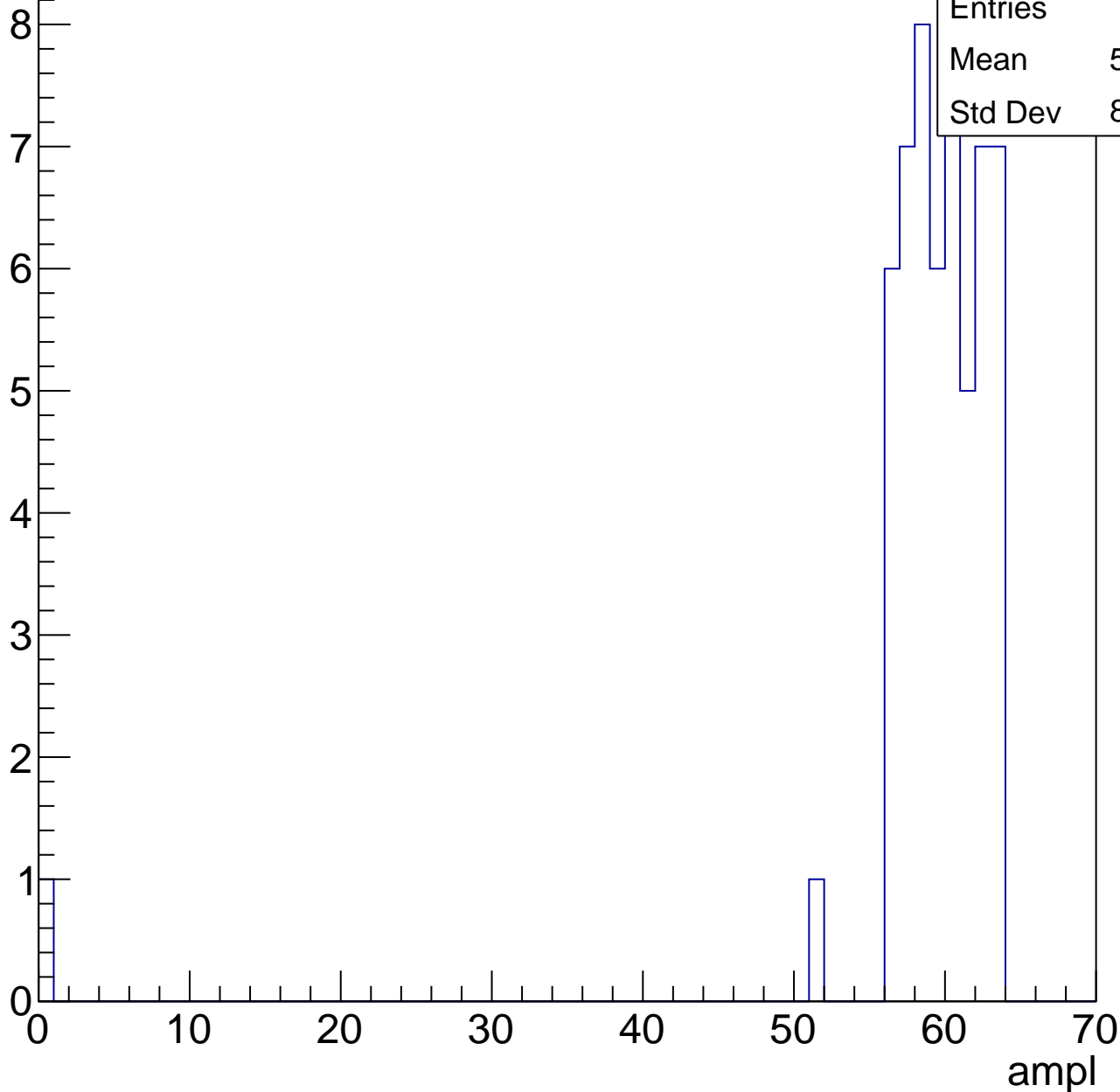


B0L000S, U2-ch126, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

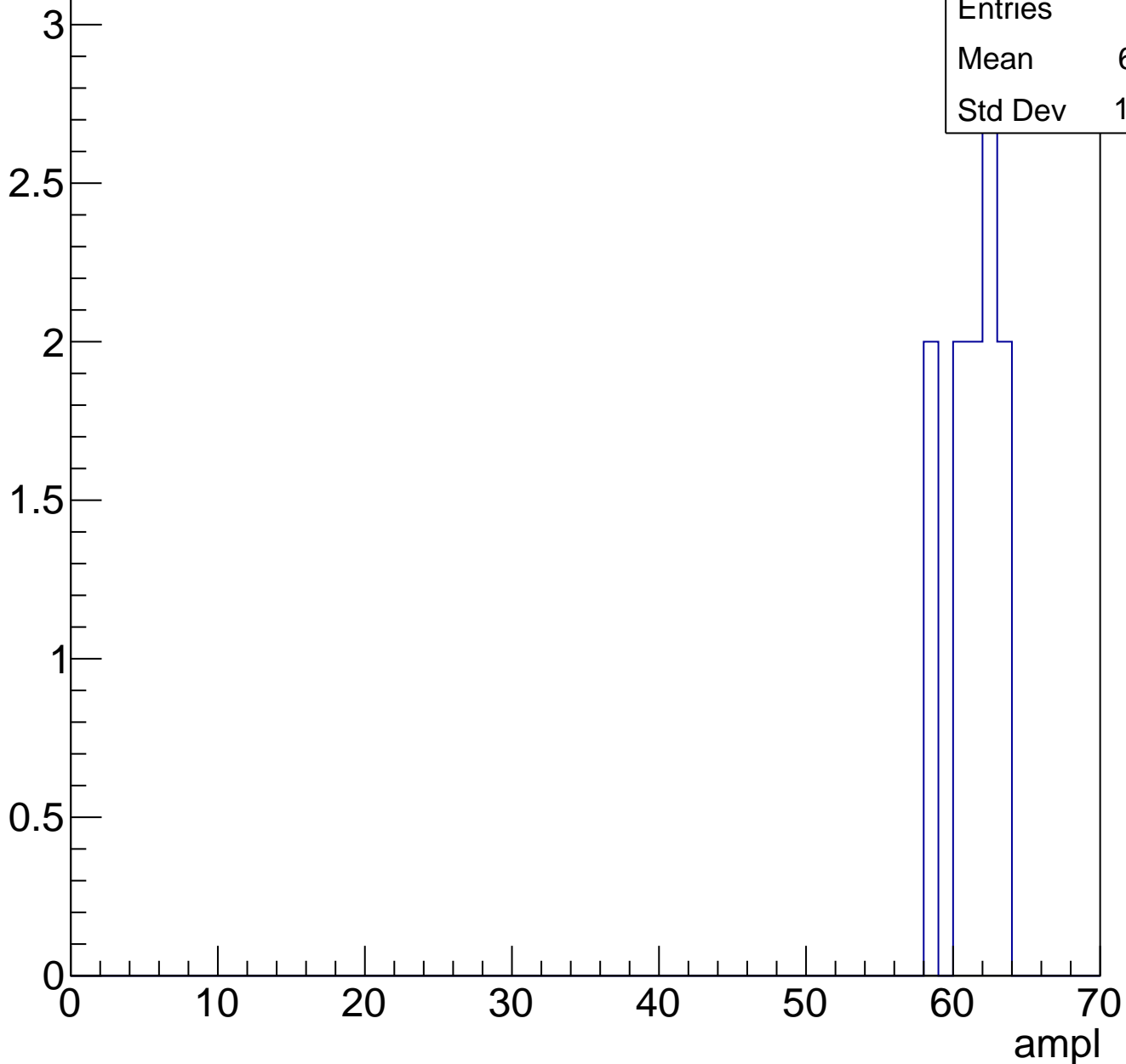
Entries	56
Mean	58.29
Std Dev	8.248



B0L000S, U2-ch126, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch126, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch127, adc0

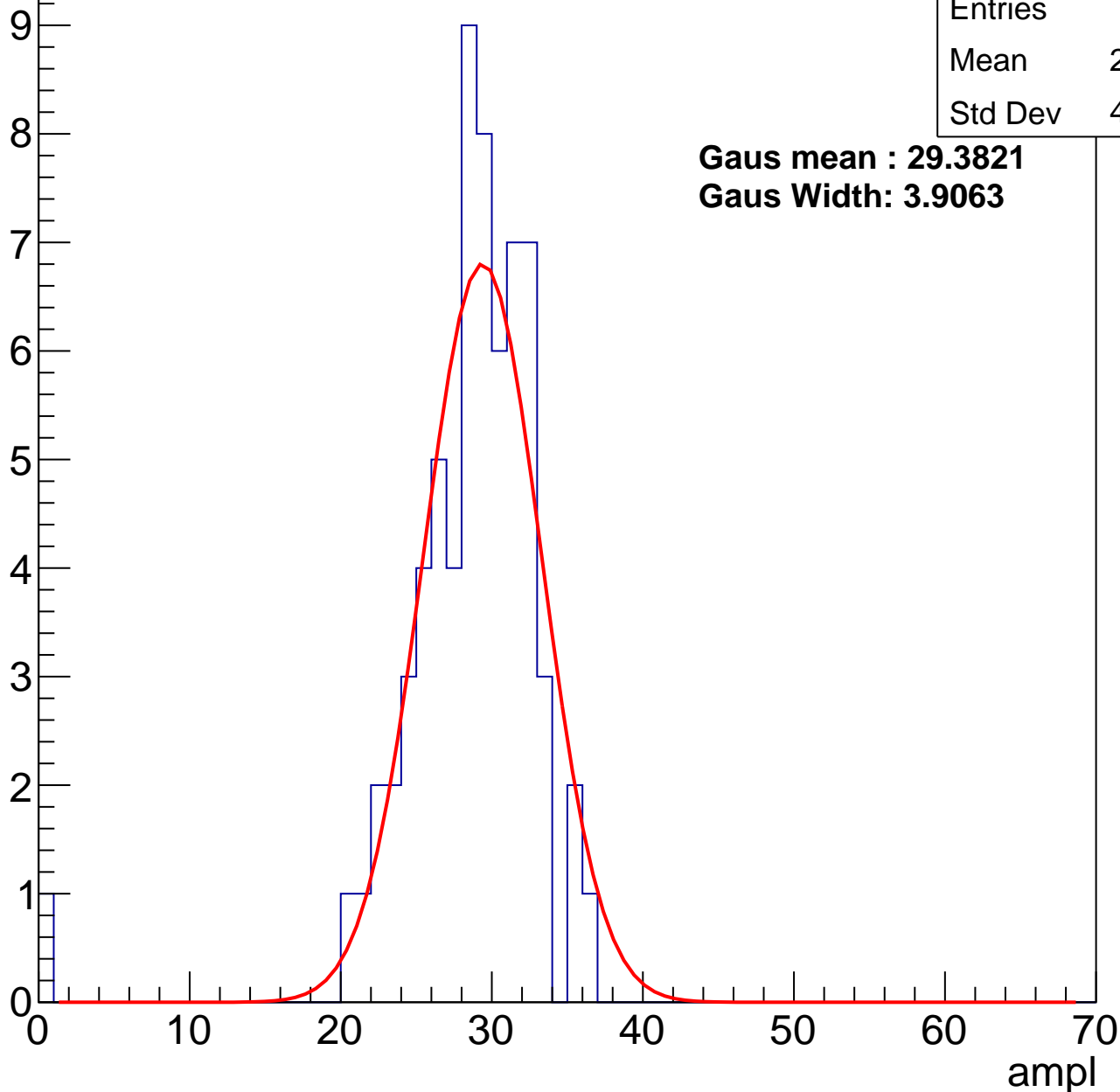
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.05
Std Dev	4.879

Gaus mean : 29.3821

Gaus Width: 3.9063



B0L000S, U2-ch127, adc1

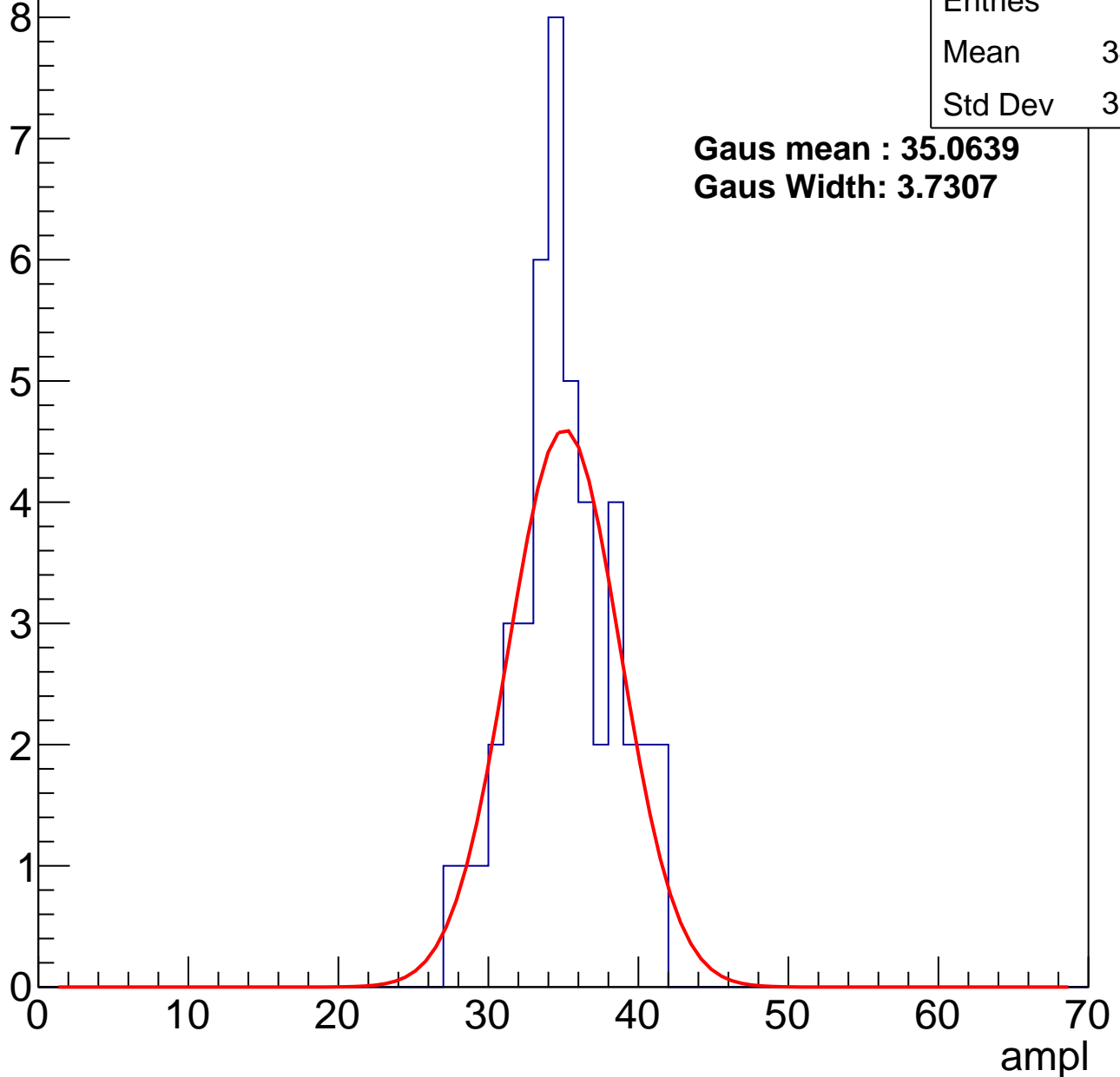
calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	46
Mean	34.52
Std Dev	3.289

Gaus mean : 35.0639

Gaus Width: 3.7307



B0L000S, U2-ch127, adc2

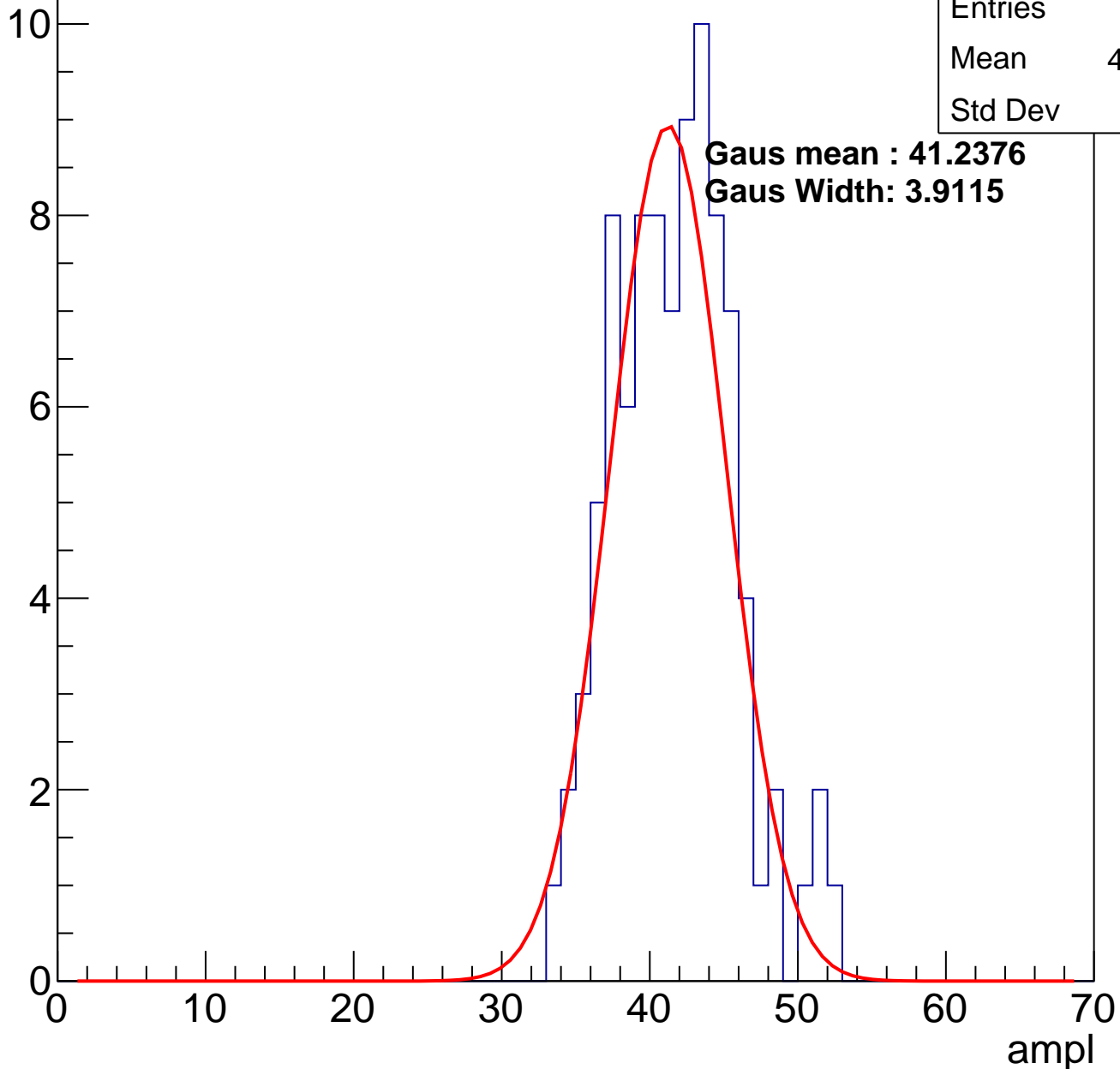
calib_packv5_042523_0143.root, FC#5, port B1

Entries	93
Mean	41.24
Std Dev	3.99

Gaus mean : 41.2376

Gaus Width: 3.9115

Entry

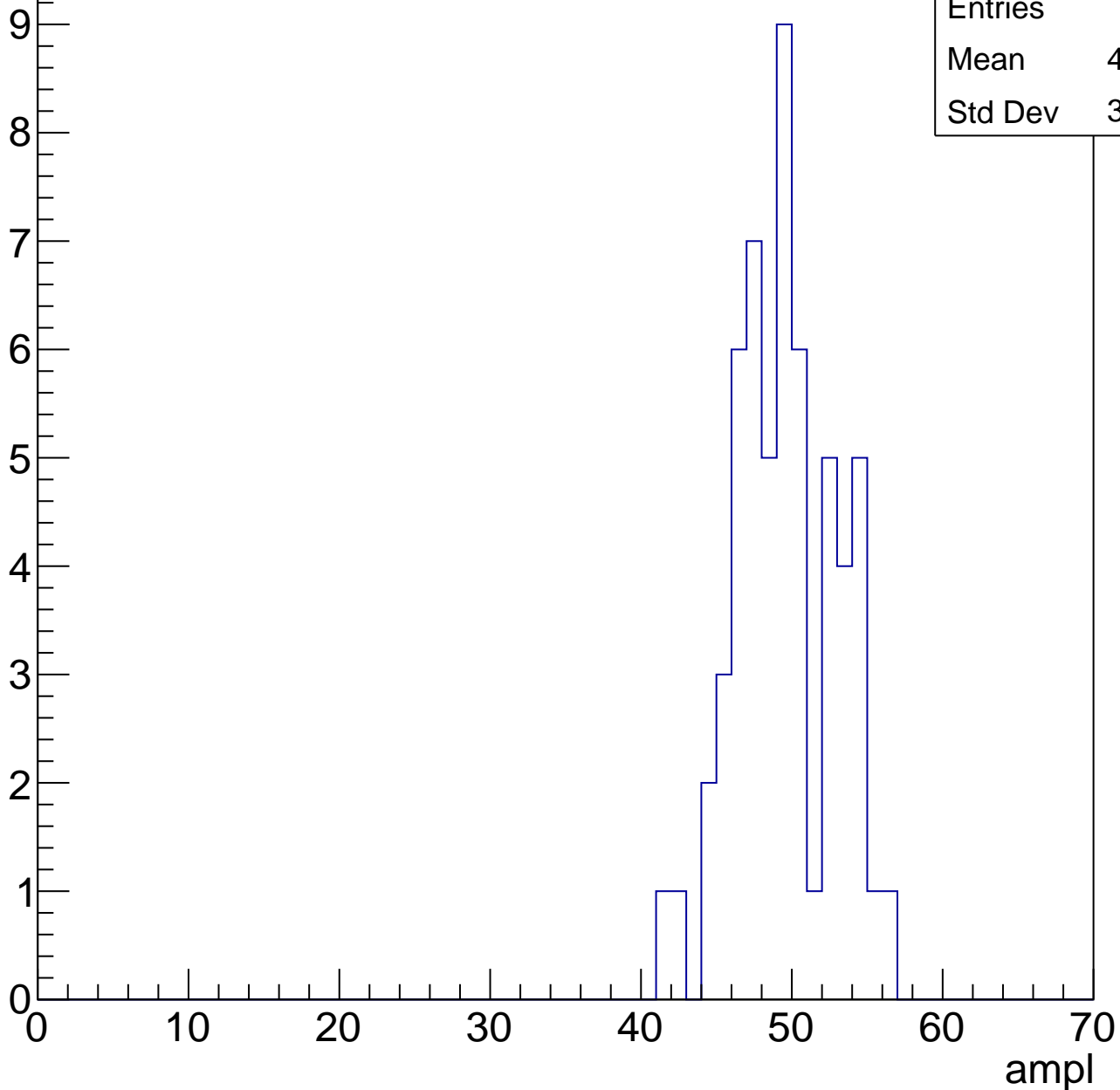


B0L000S, U2-ch127, adc3

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	57
Mean	49.05
Std Dev	3.316

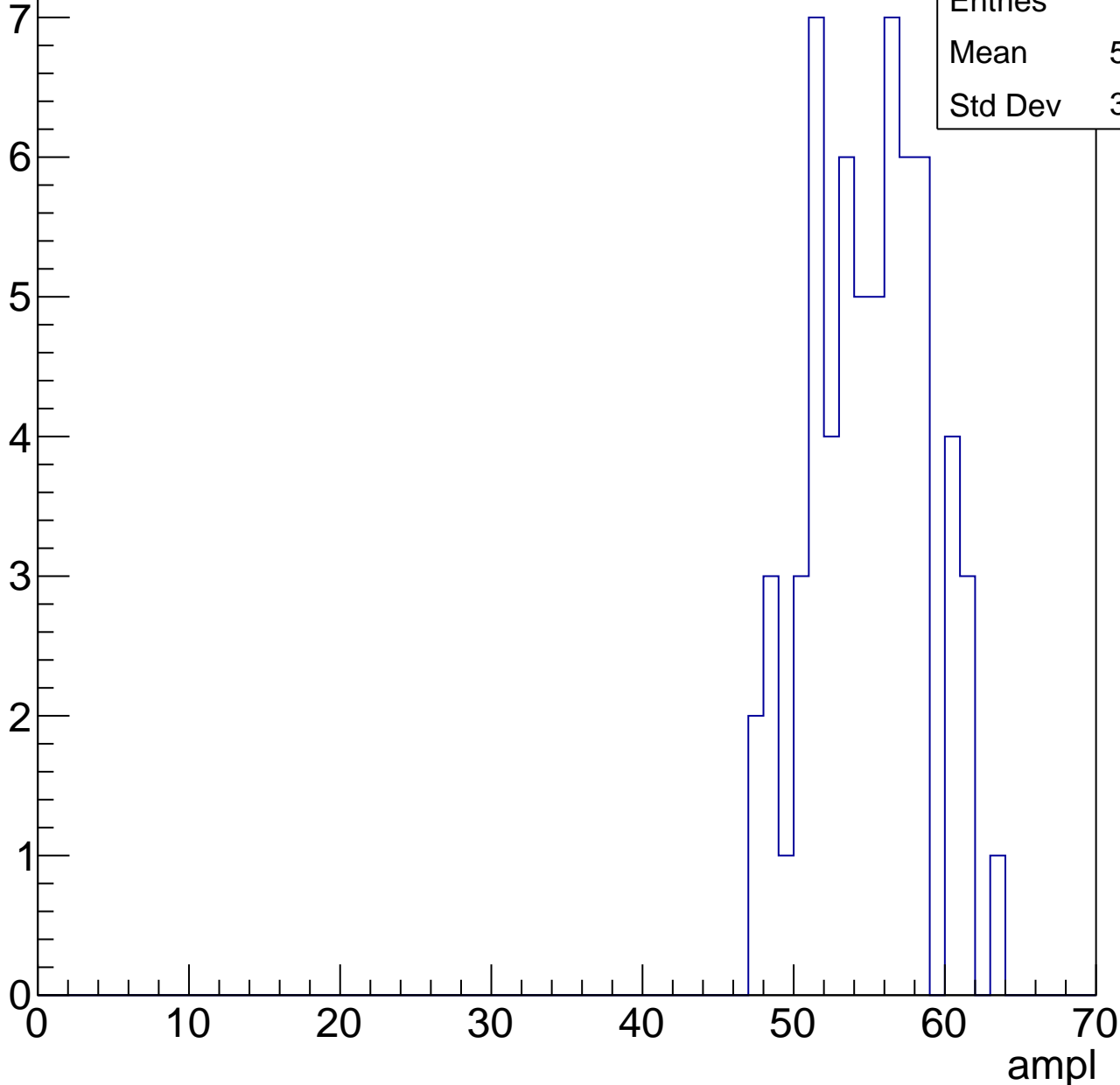


B0L000S, U2-ch127, adc4

calib_packv5_042523_0143.root, FC#5, port B1

Entry

Entries	63
Mean	54.49
Std Dev	3.788



B0L000S, U2-ch127, adc5

calib_packv5_042523_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

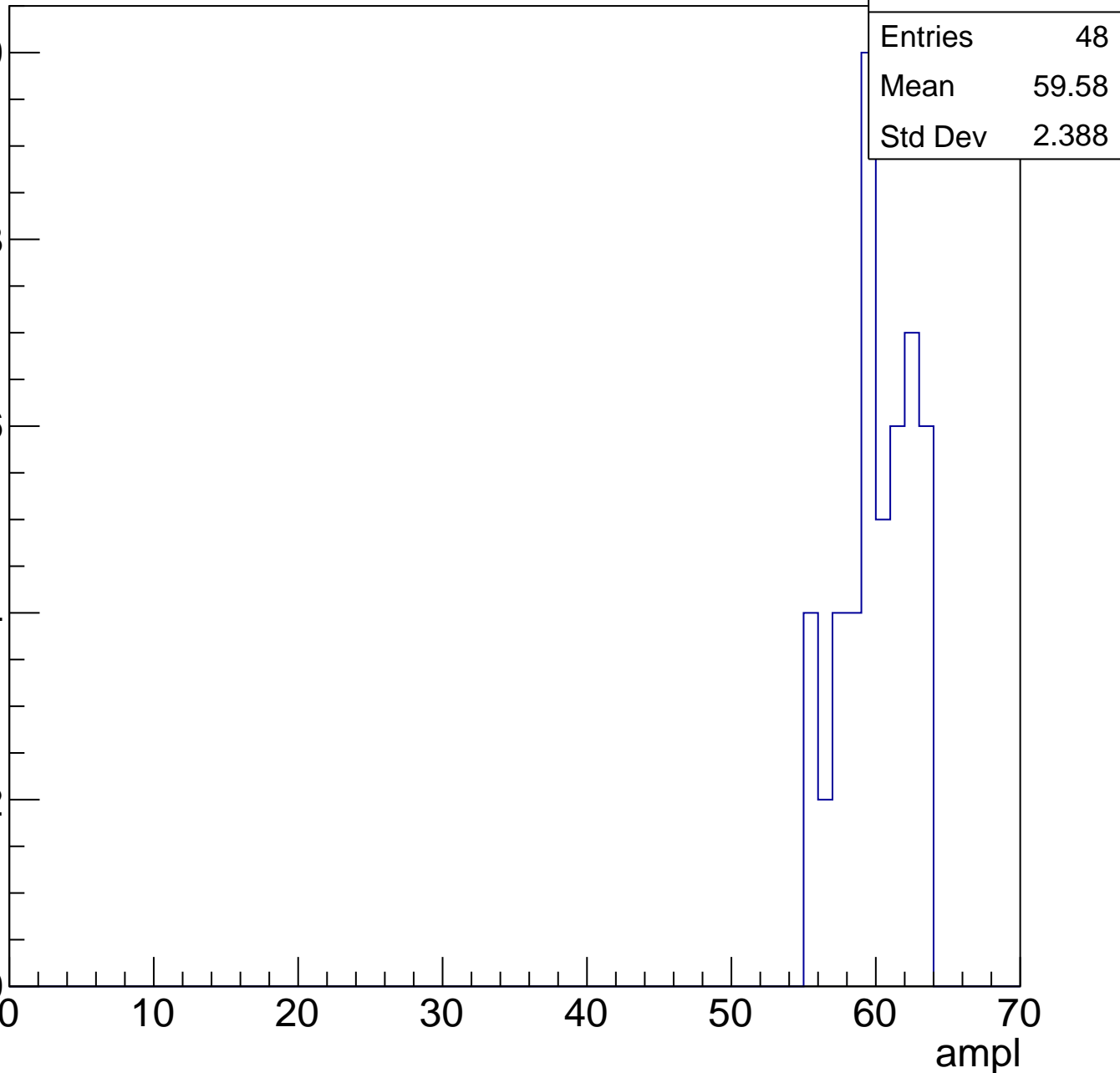
40

50

60

ampl

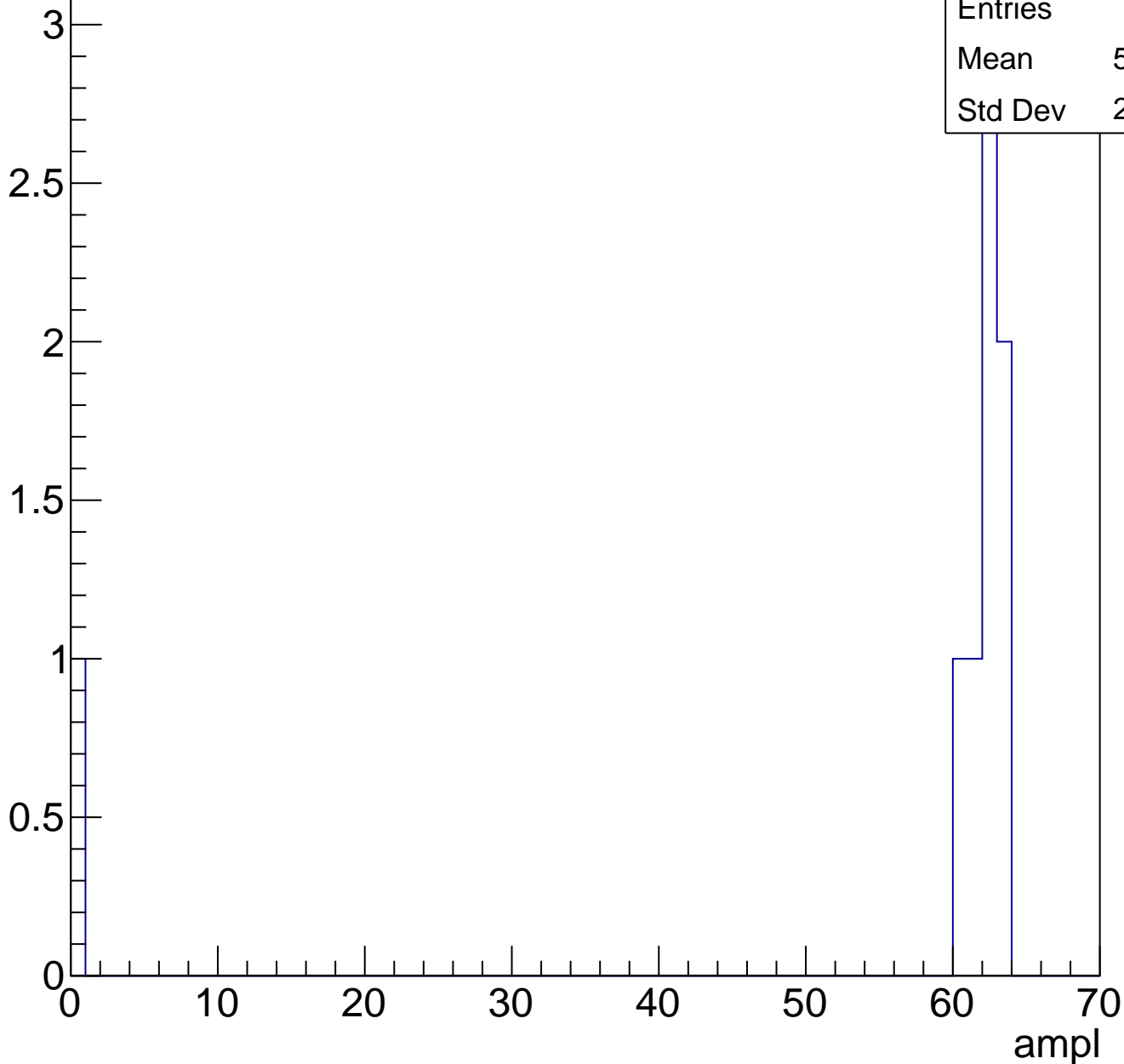
Entries	48
Mean	59.58
Std Dev	2.388



B0L000S, U2-ch127, adc6

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch127, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry



B0L000S, U2-ch127, adc7

calib_packv5_042523_0143.root, FC#5, port B1

Entry

