

B1L100S, U15-ch0, adc0

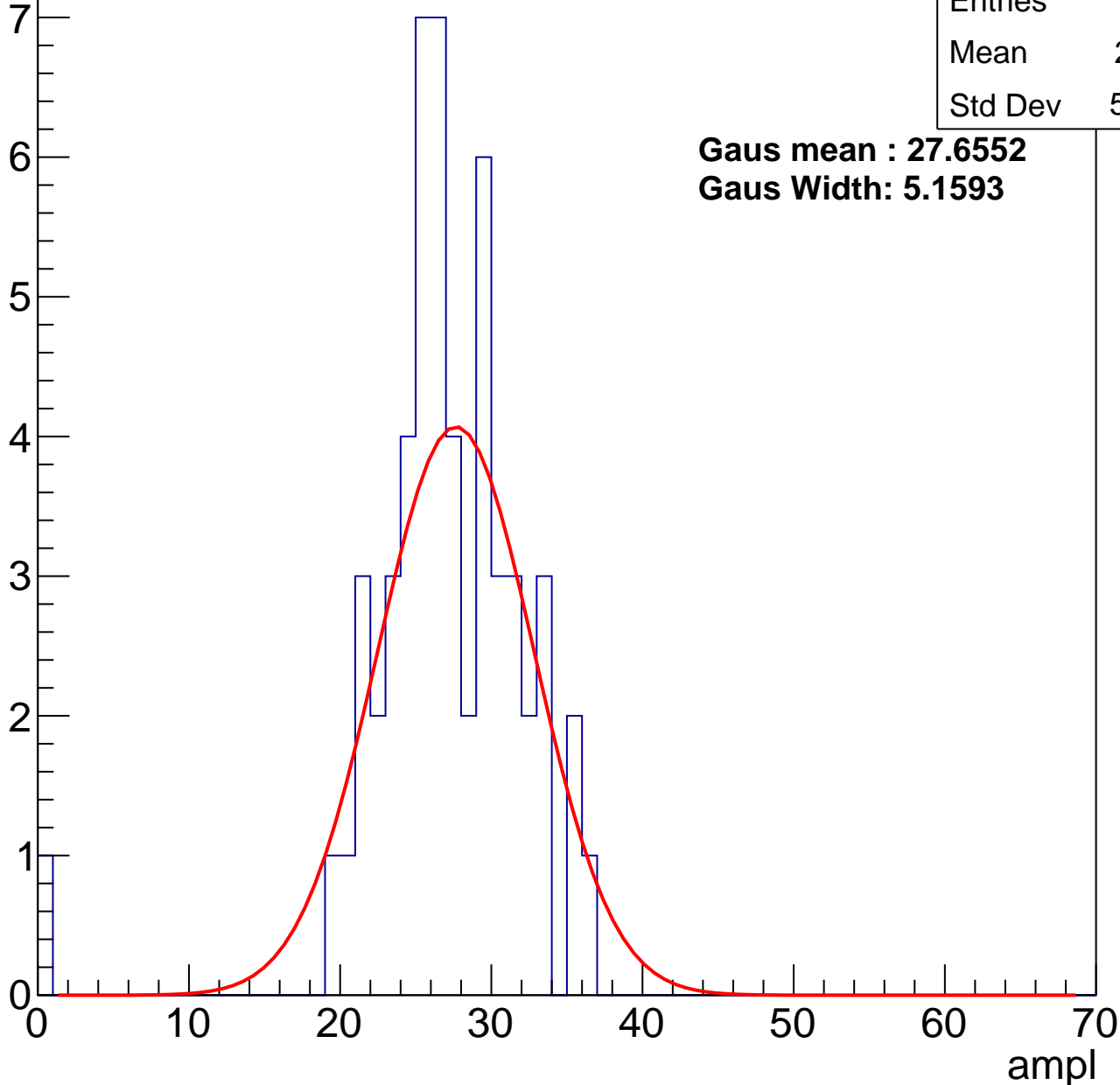
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	26.51
Std Dev	5.349

Gaus mean : 27.6552

Gaus Width: 5.1593



B1L100S, U15-ch0, adc1

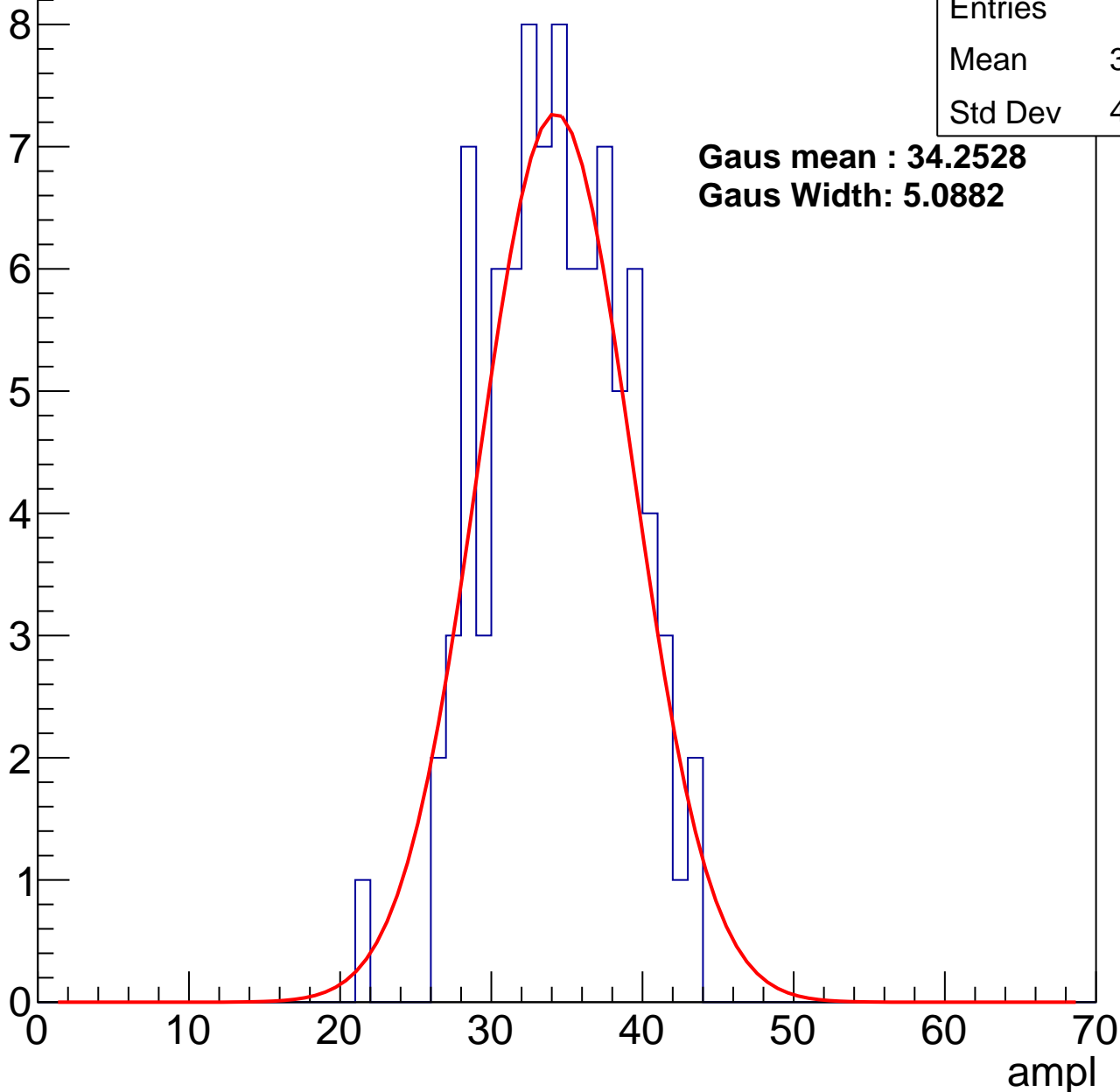
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	91
Mean	33.87
Std Dev	4.446

Gaus mean : 34.2528

Gaus Width: 5.0882



B1L100S, U15-ch0, adc2

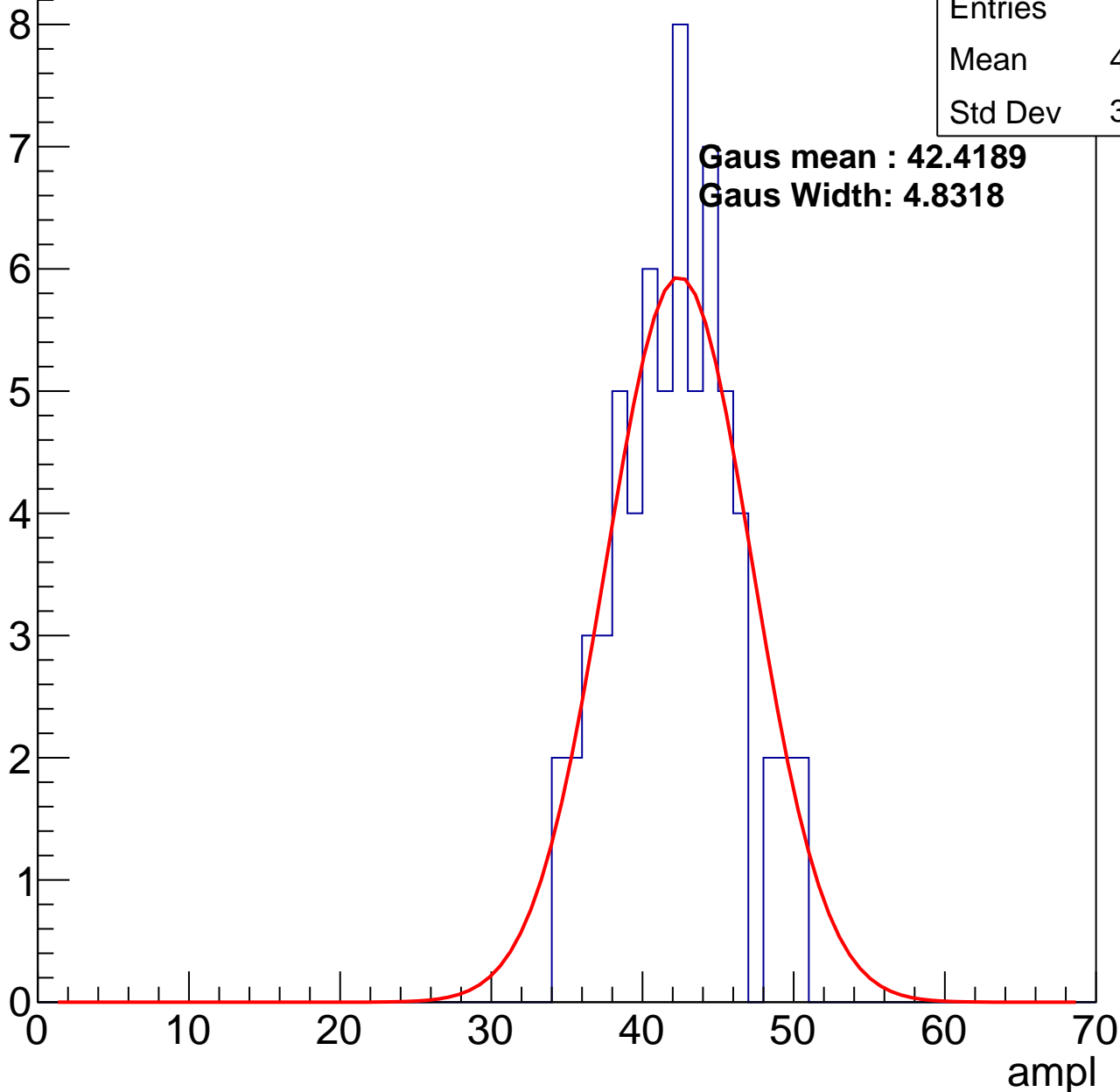
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	41.69
Std Dev	3.898

Gaus mean : 42.4189

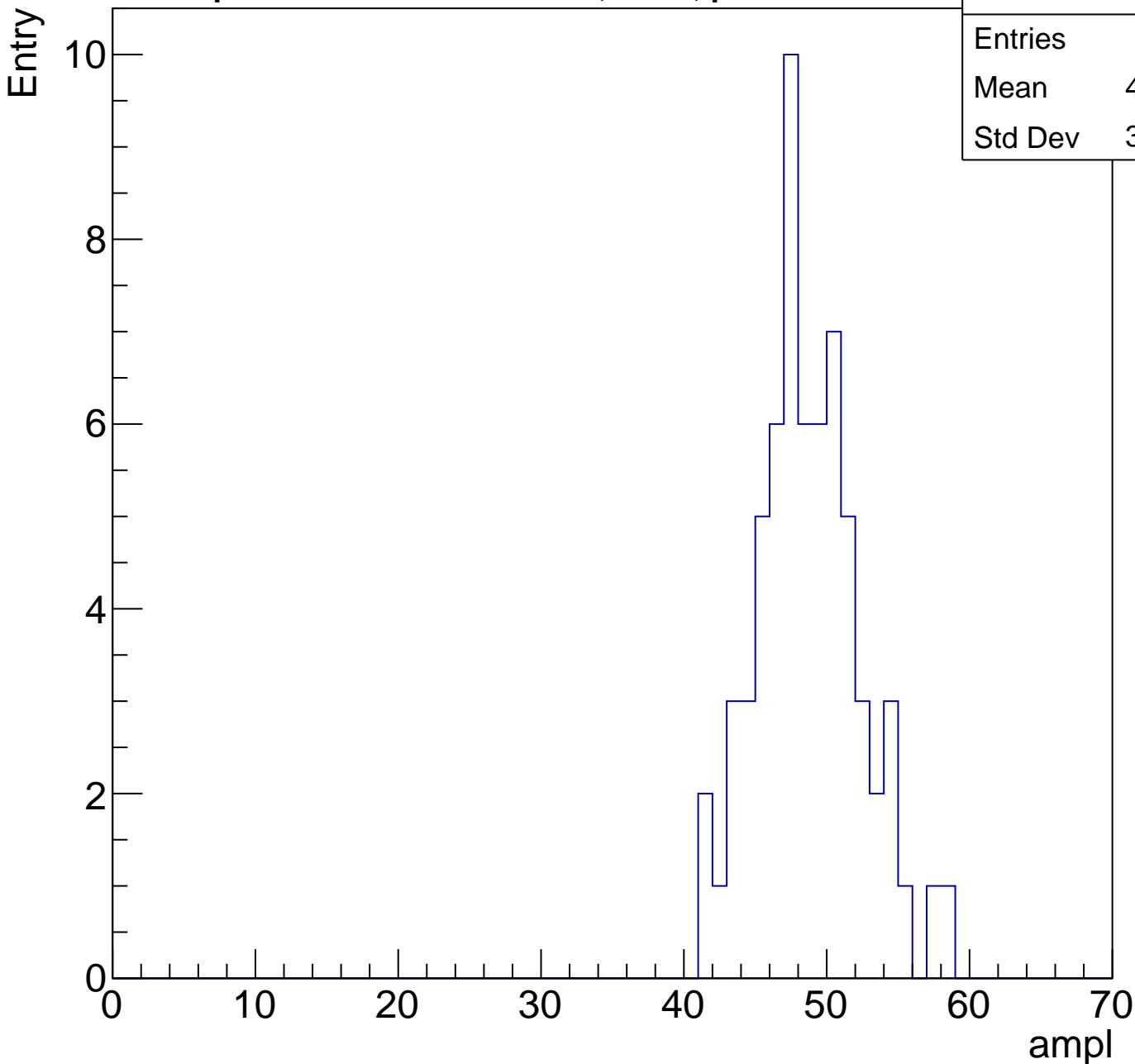
Gaus Width: 4.8318



B1L100S, U15-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

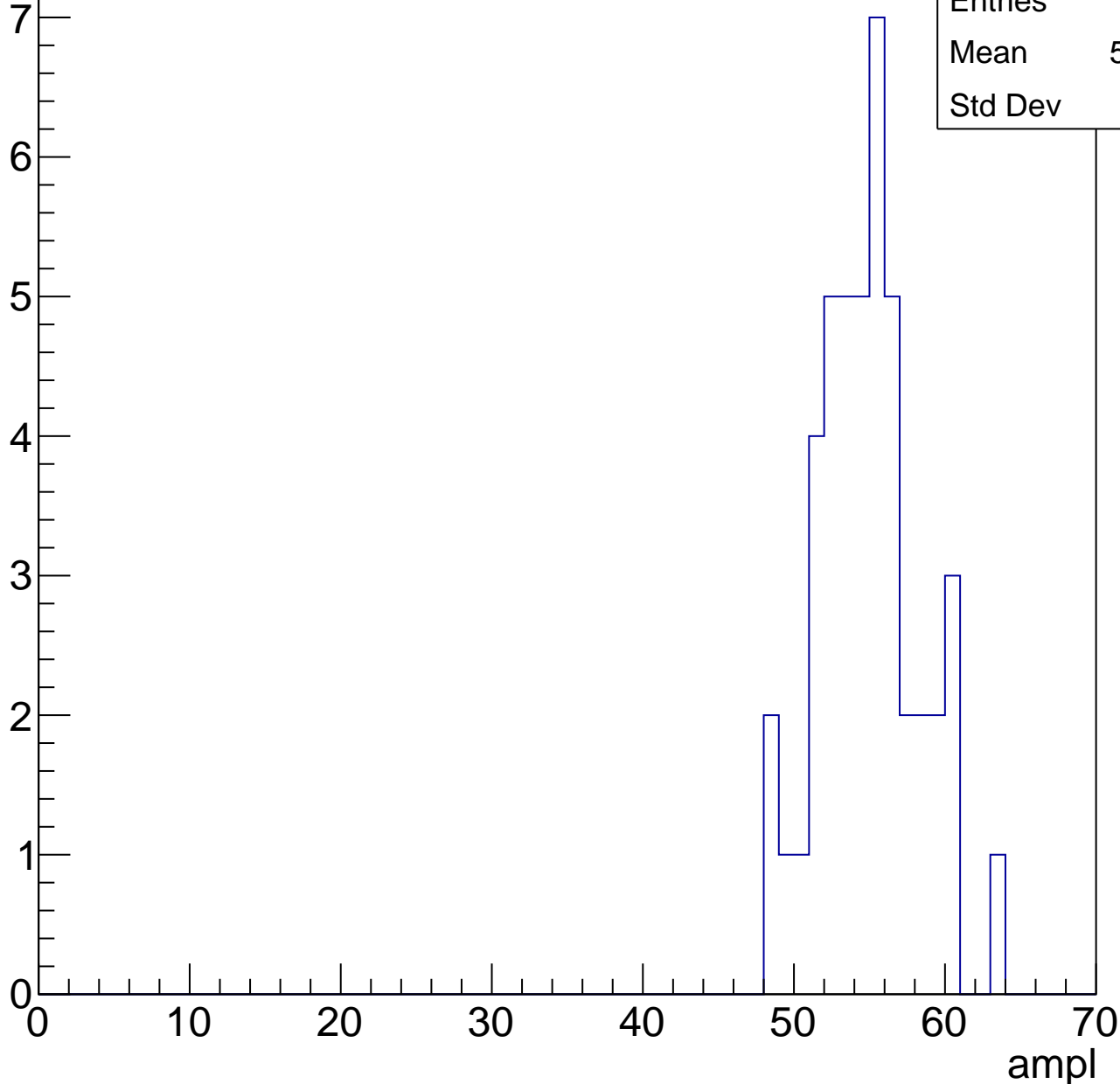
Entries	65
Mean	48.26
Std Dev	3.609



B1L100S, U15-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

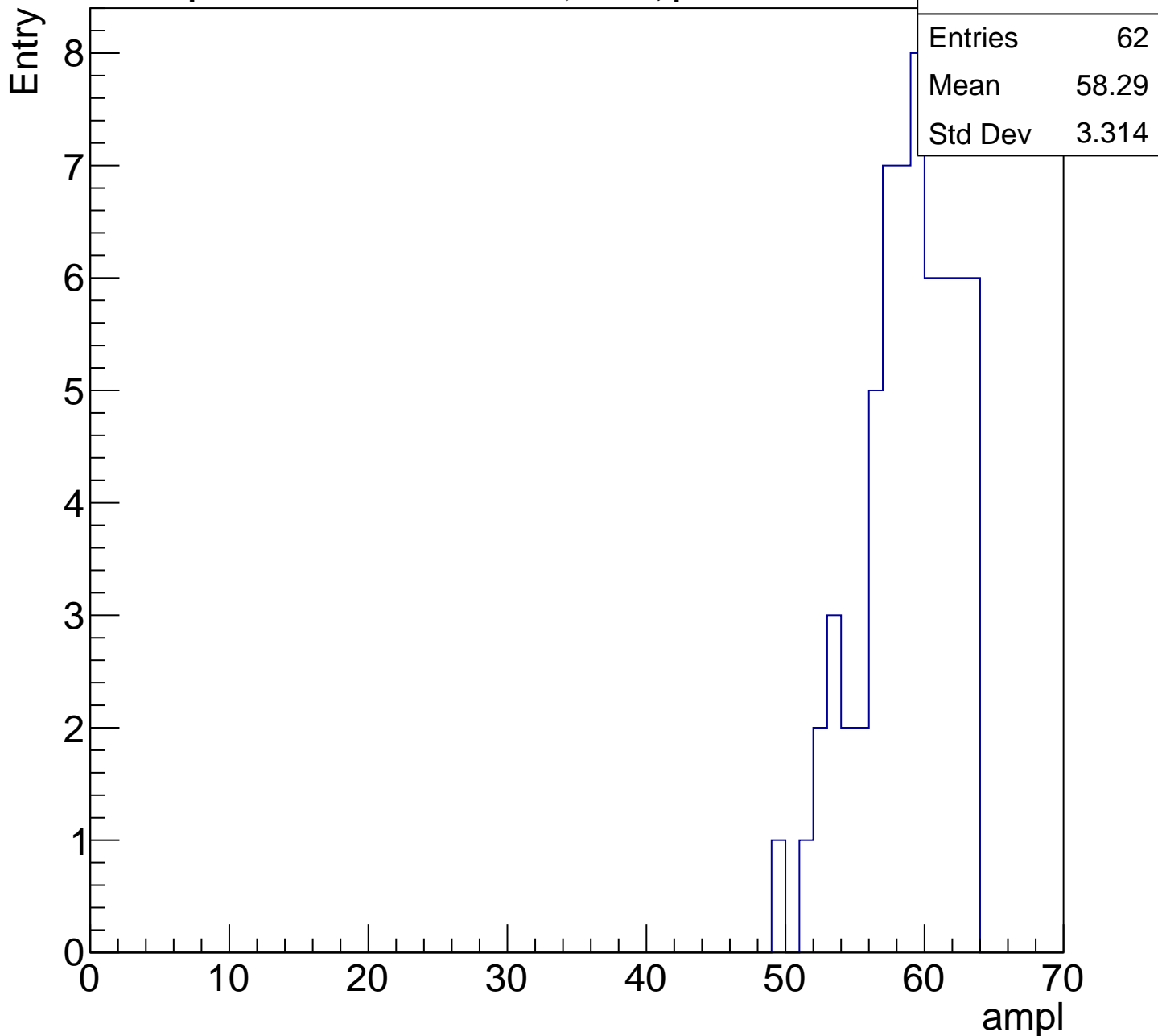
Entry



Entries	45
Mean	54.44
Std Dev	3.27

B1L100S, U15-ch0, adc5

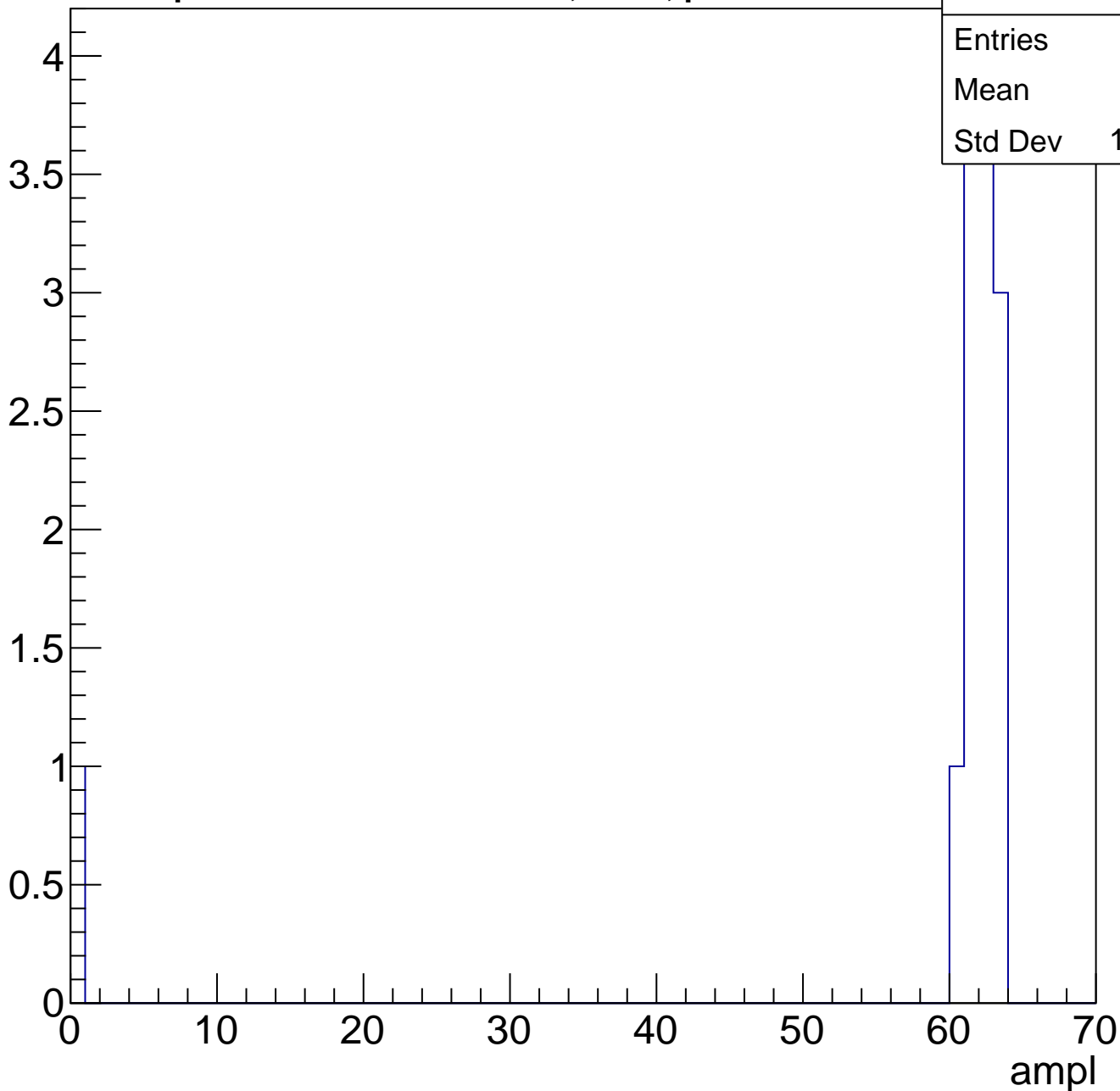
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch1, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	27.7
Std Dev	5.731

Gaus mean : 28.5912

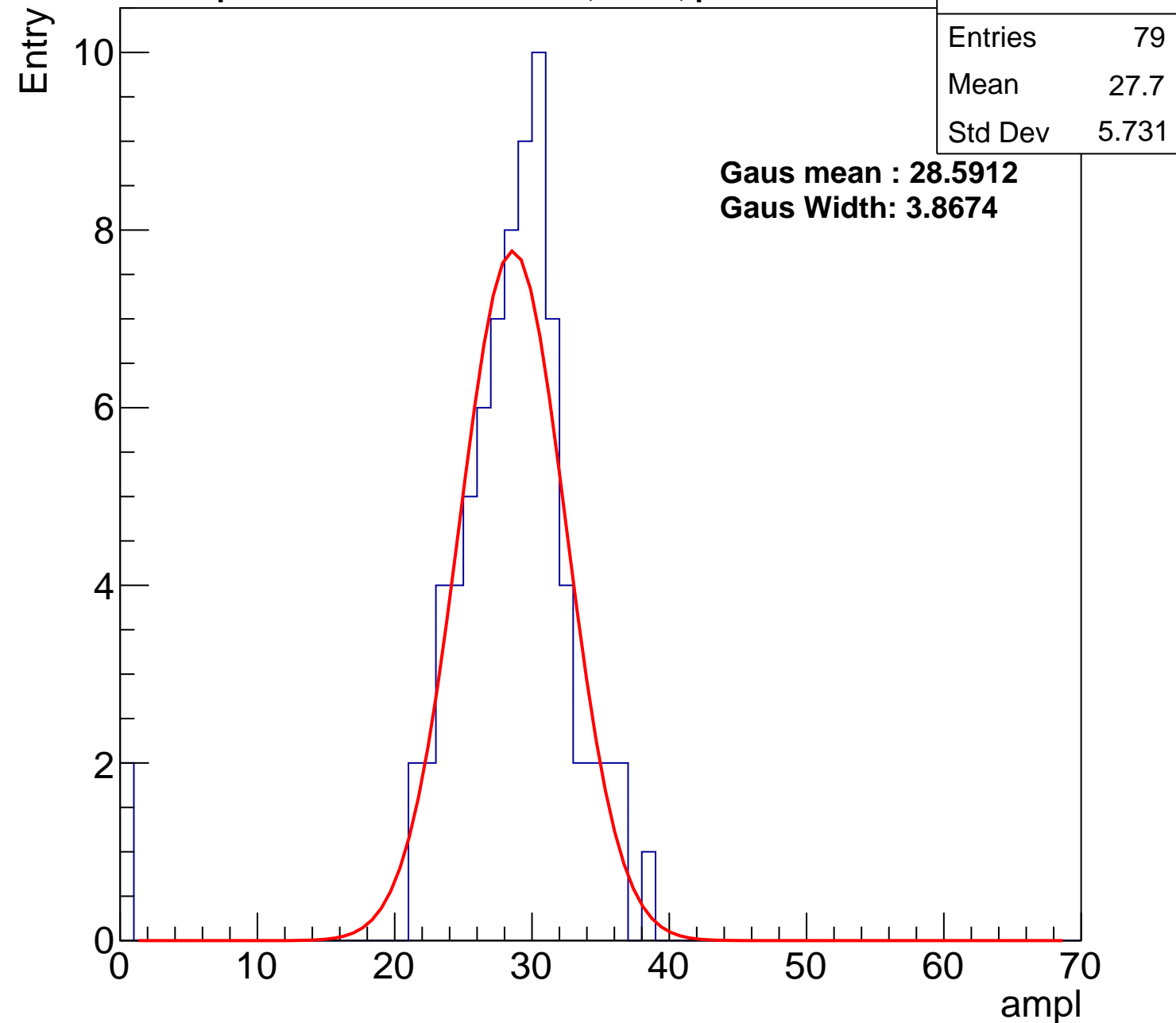
Gaus Width: 3.8674

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch1, adc1

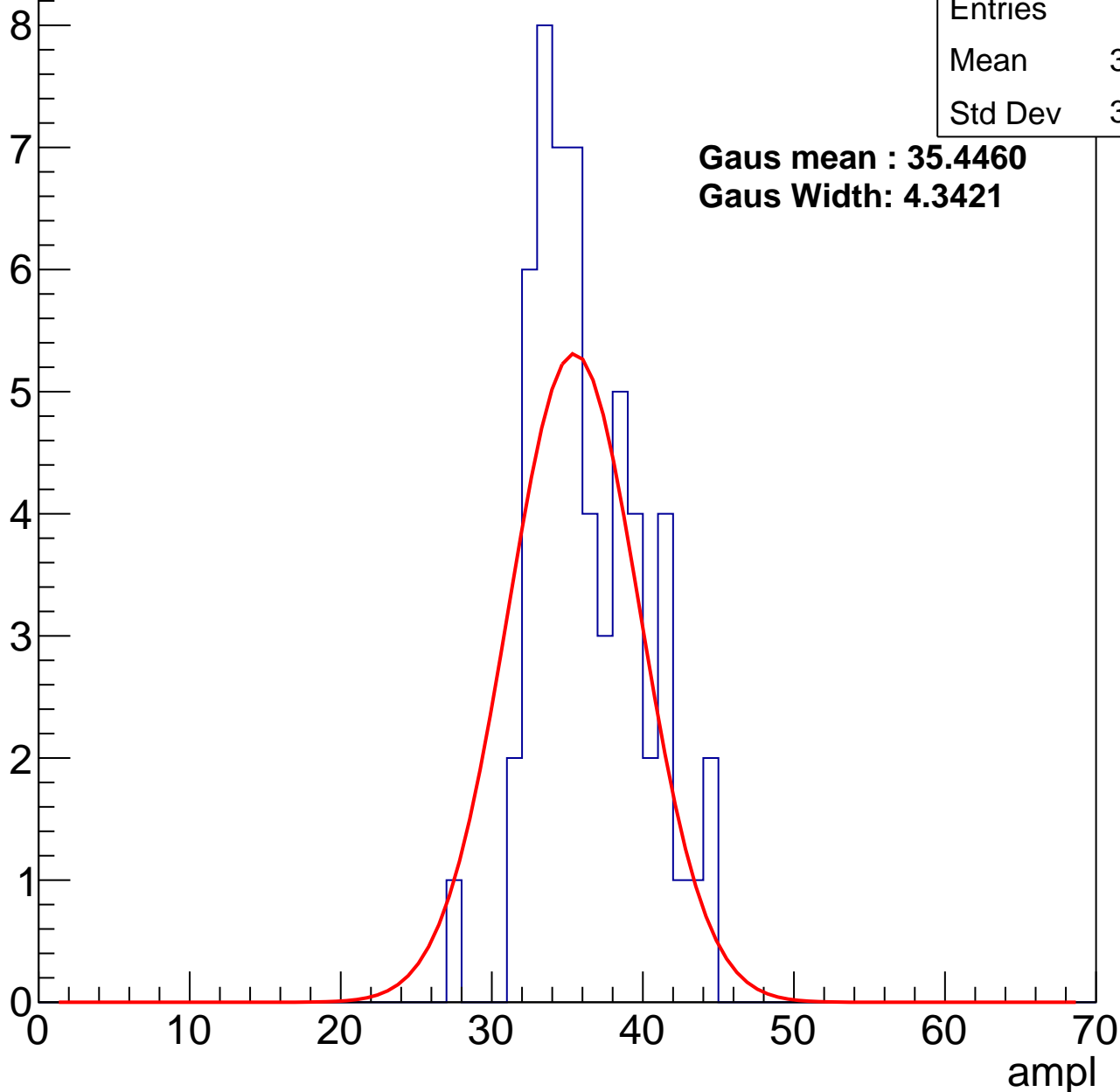
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	35.89
Std Dev	3.597

Gaus mean : 35.4460

Gaus Width: 4.3421

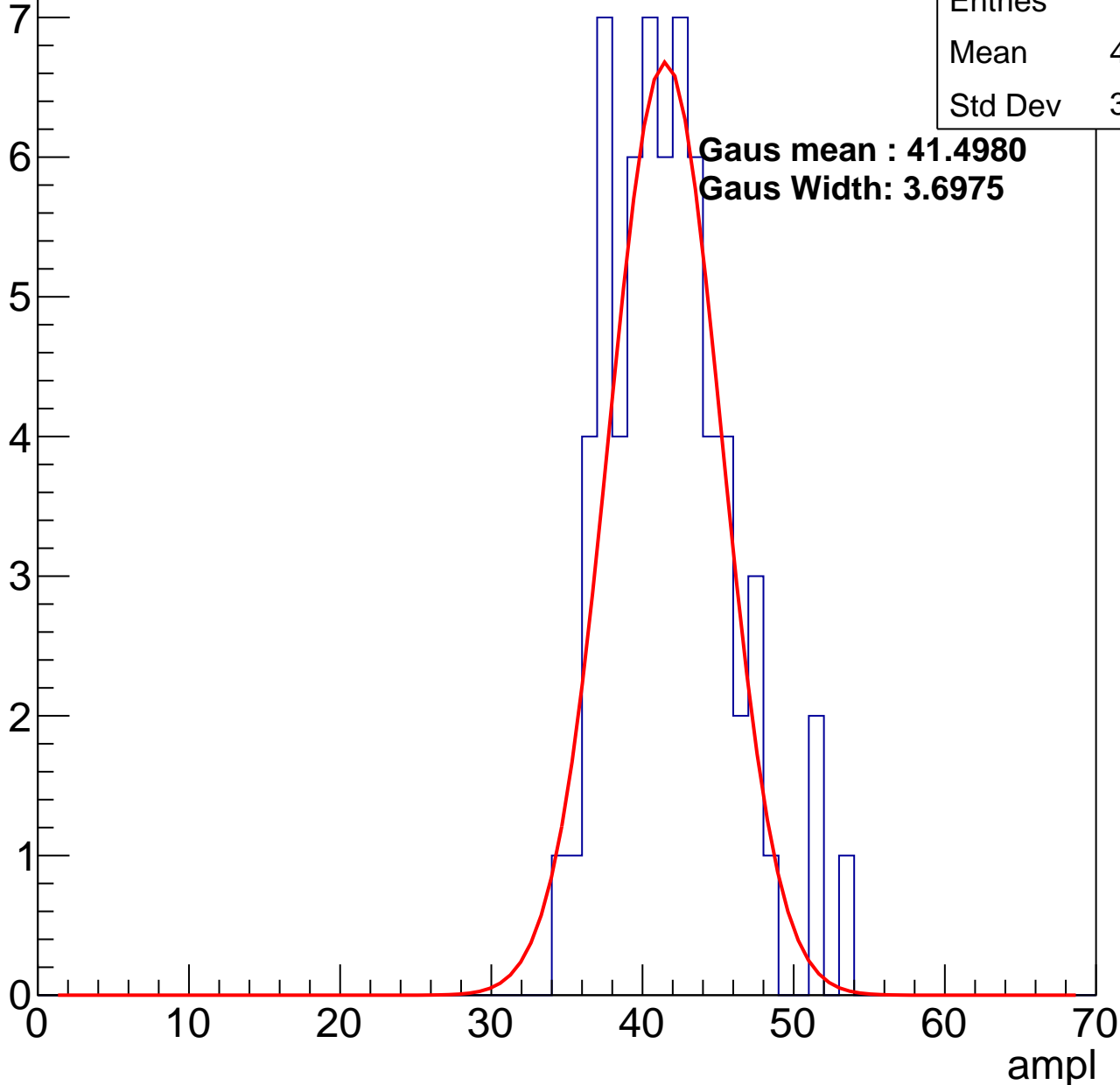


B1L100S, U15-ch1, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

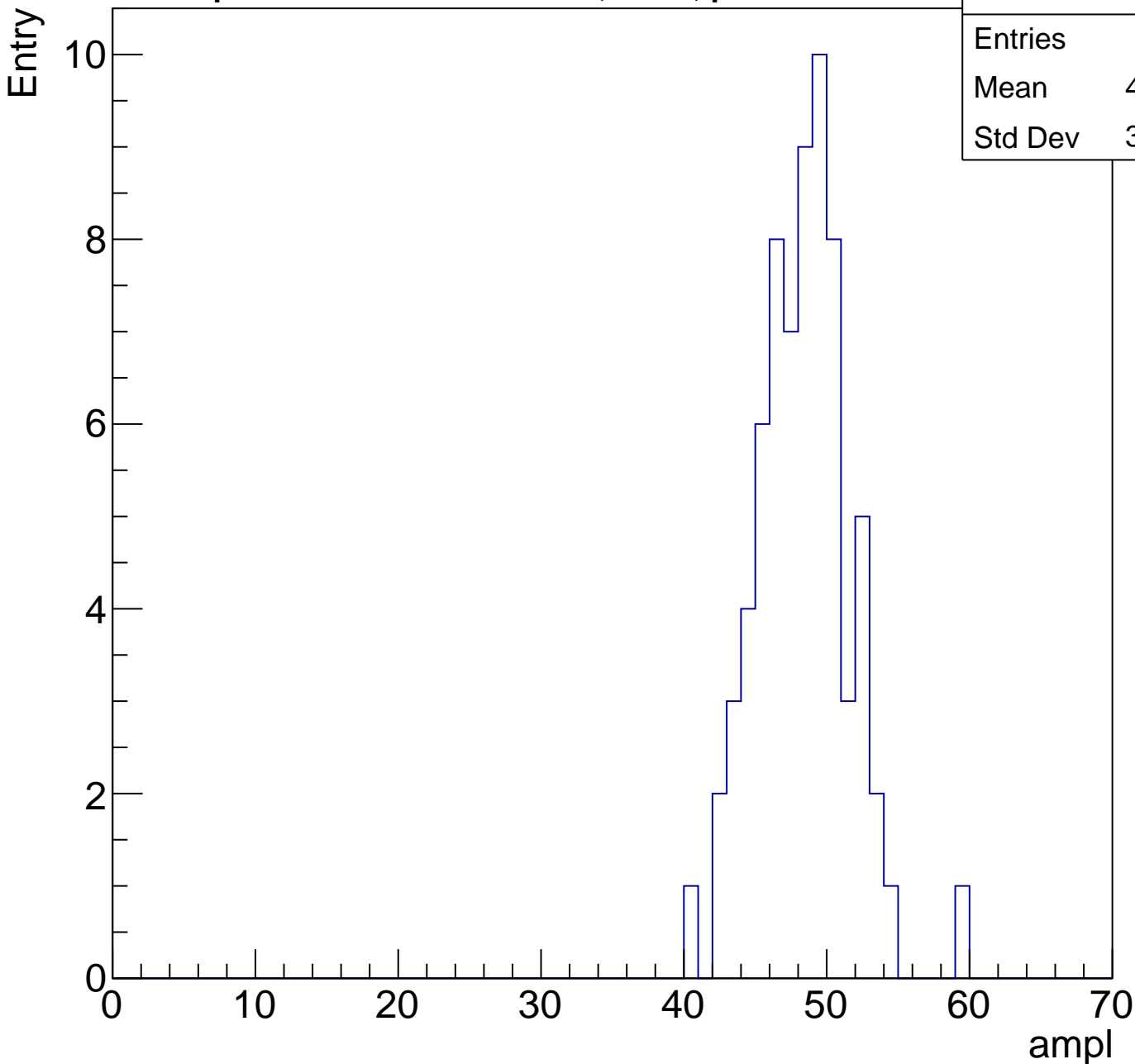
Entries	66
Mean	41.33
Std Dev	3.967



B1L100S, U15-ch1, adc3

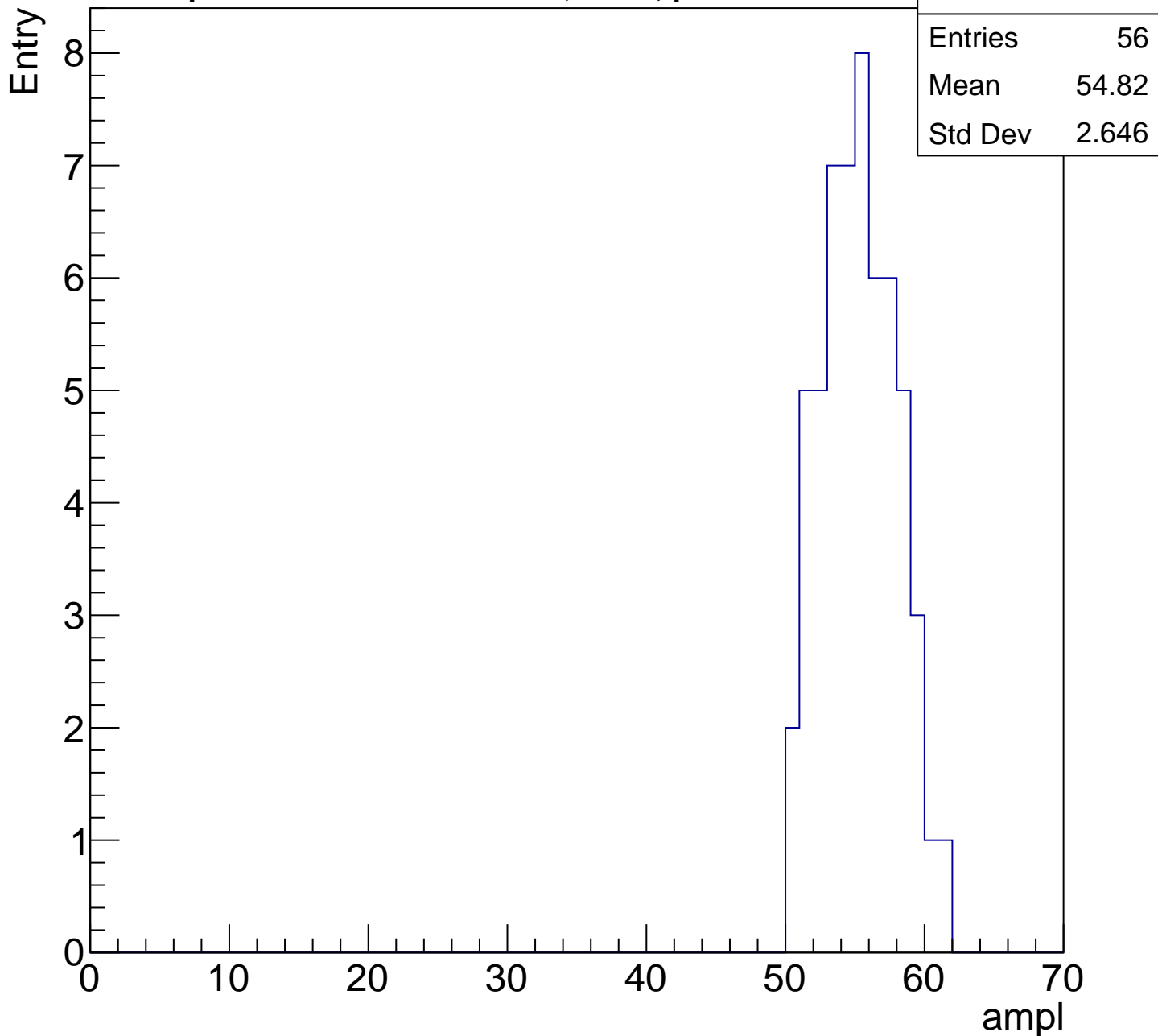
calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	47.86
Std Dev	3.213



B1L100S, U15-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

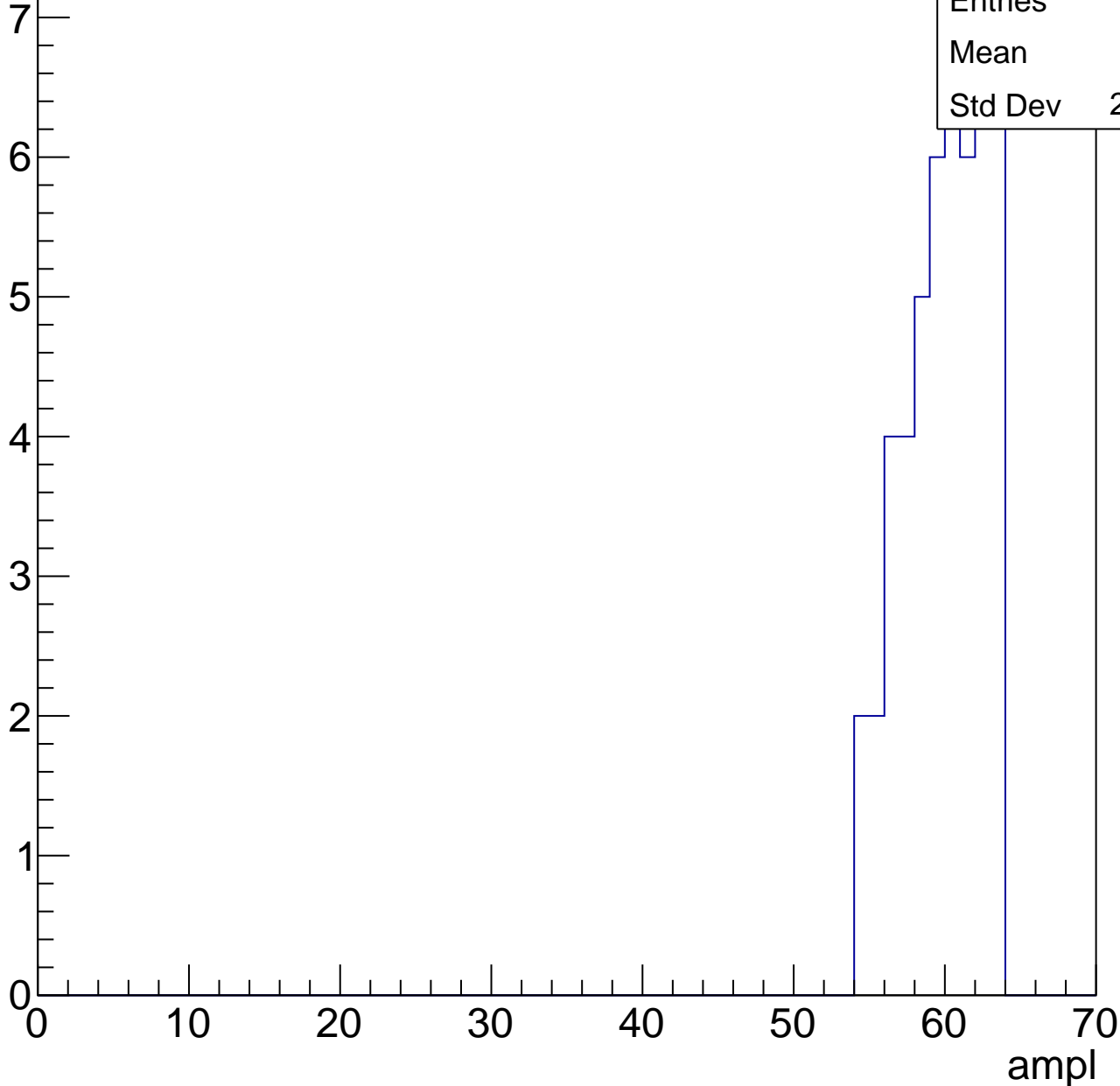


B1L100S, U15-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

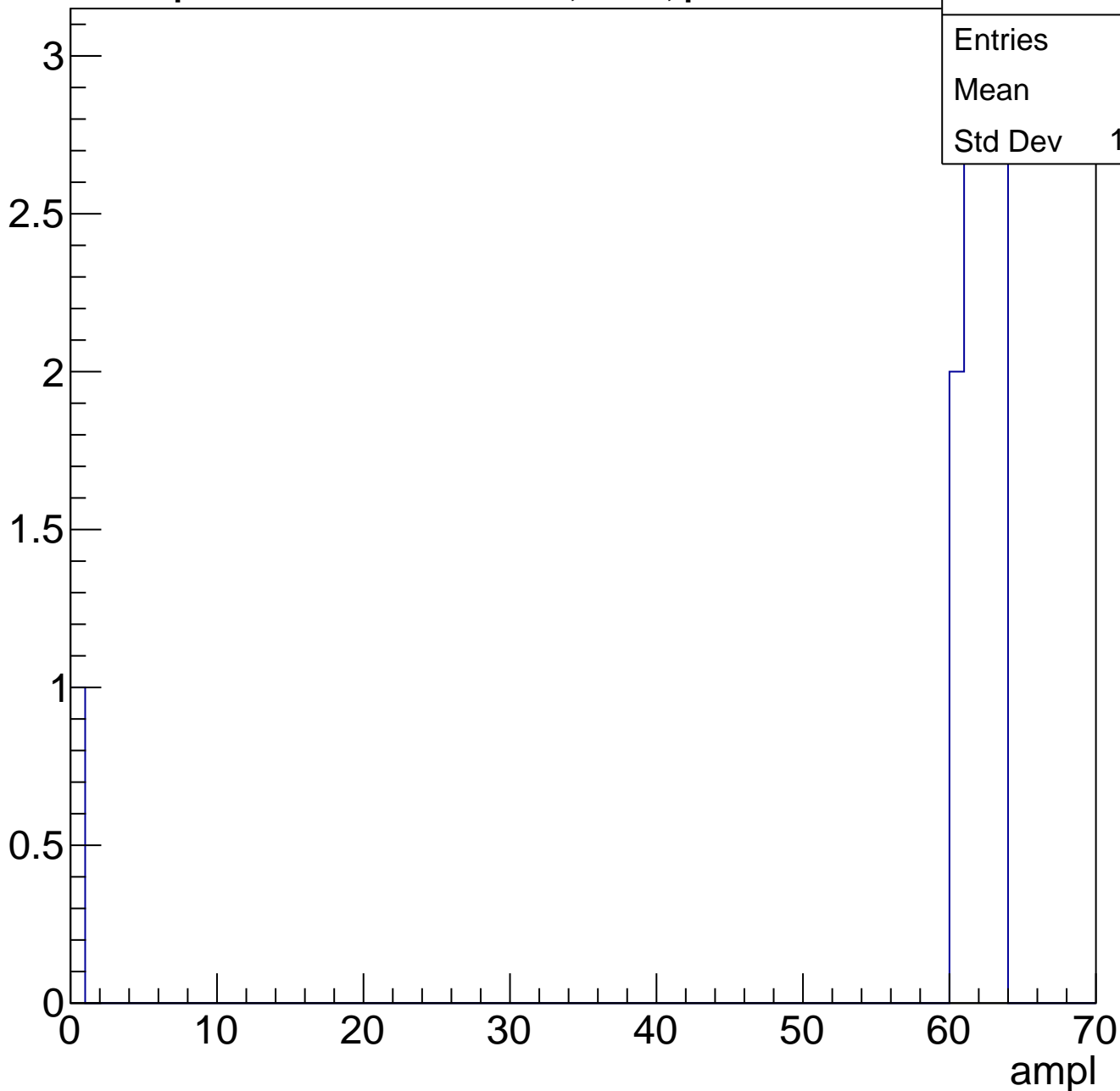
Entries	50
Mean	59.5
Std Dev	2.579



B1L100S, U15-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch2, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	28.61
Std Dev	6.931

Gaus mean : 30.1516

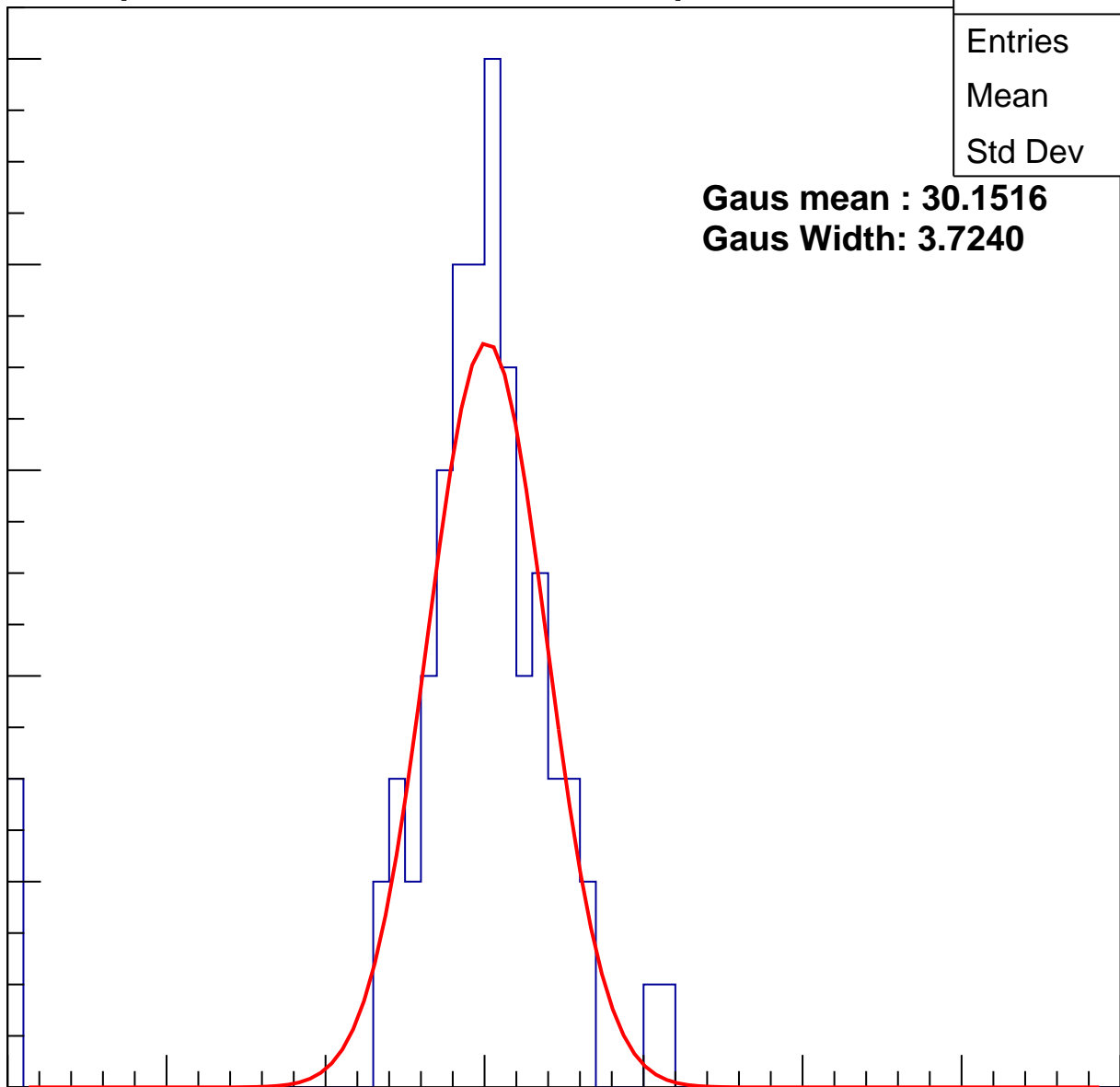
Gaus Width: 3.7240

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch2, adc1

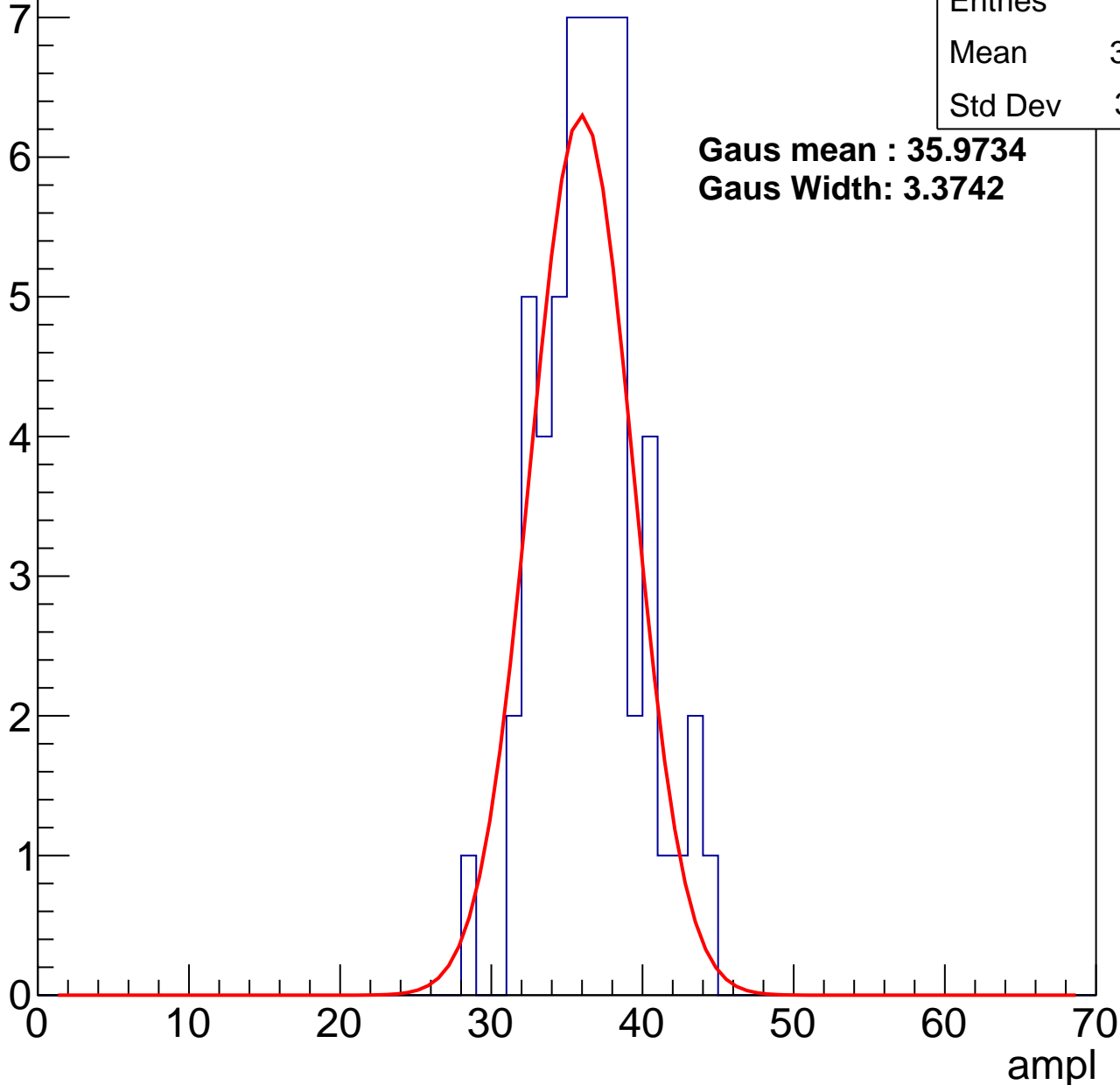
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	36.16
Std Dev	3.261

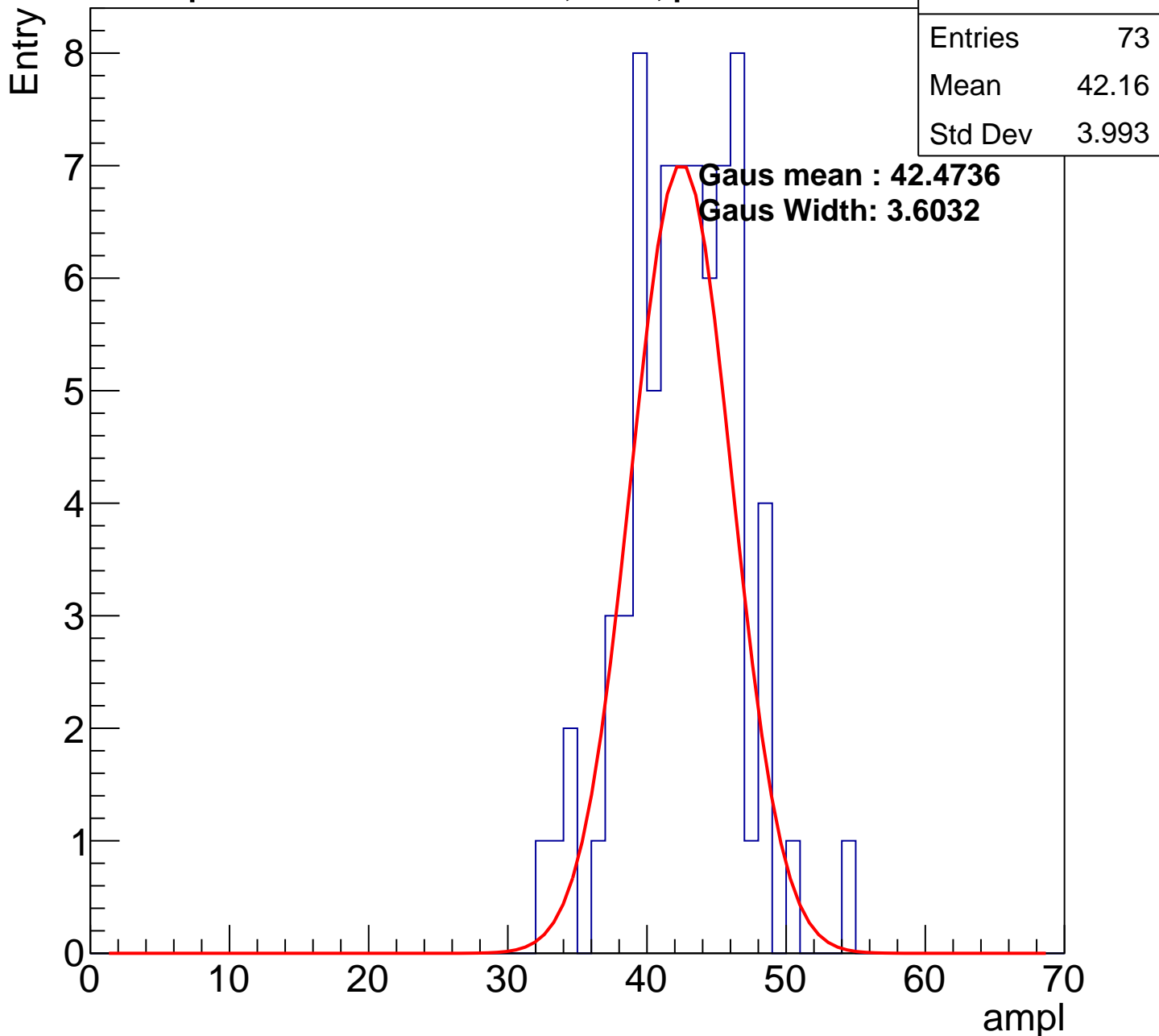
Gaus mean : 35.9734

Gaus Width: 3.3742



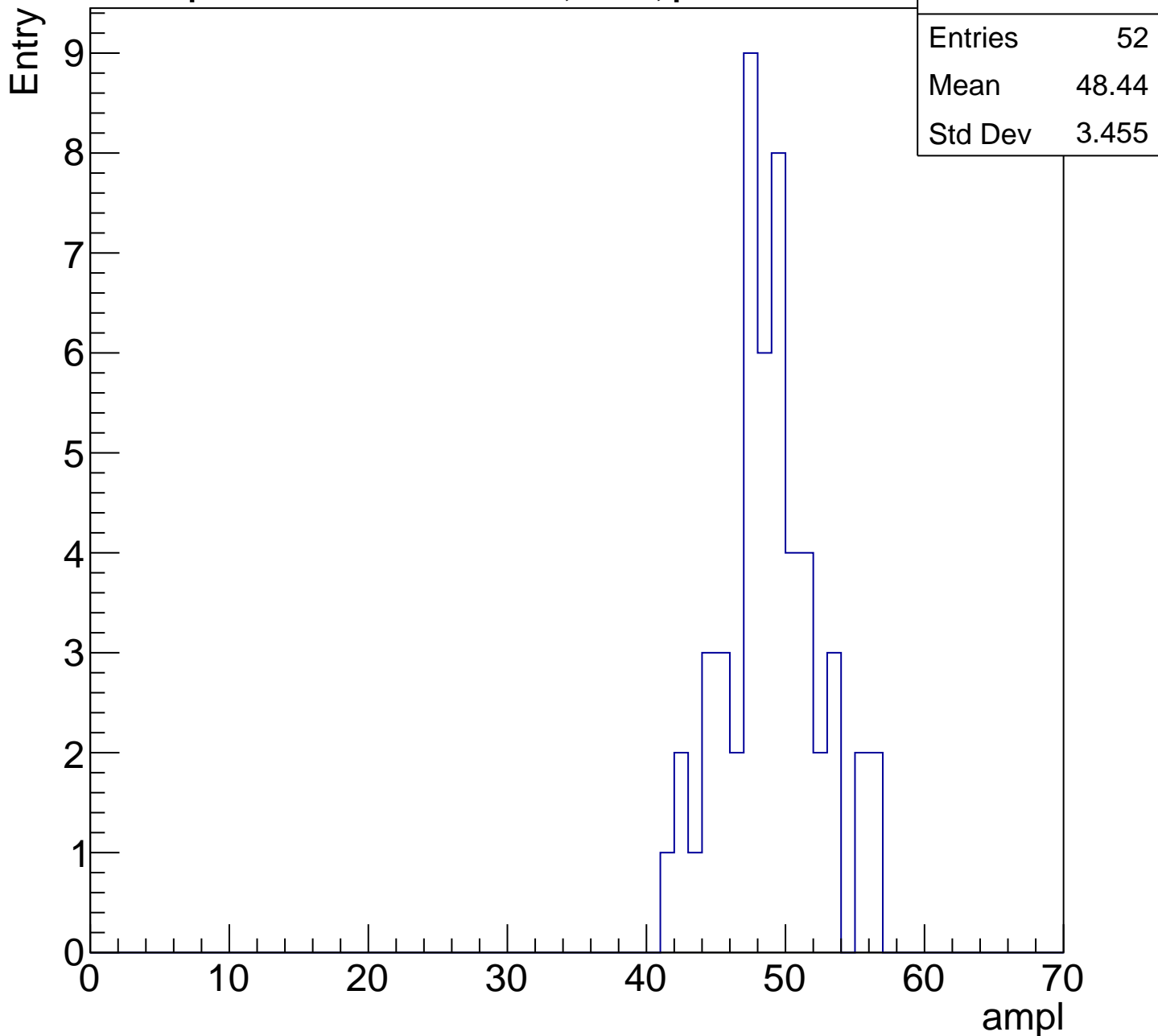
B1L100S, U15-ch2, adc2

calib_packv5_042523_0143.root, FC#4, port A2



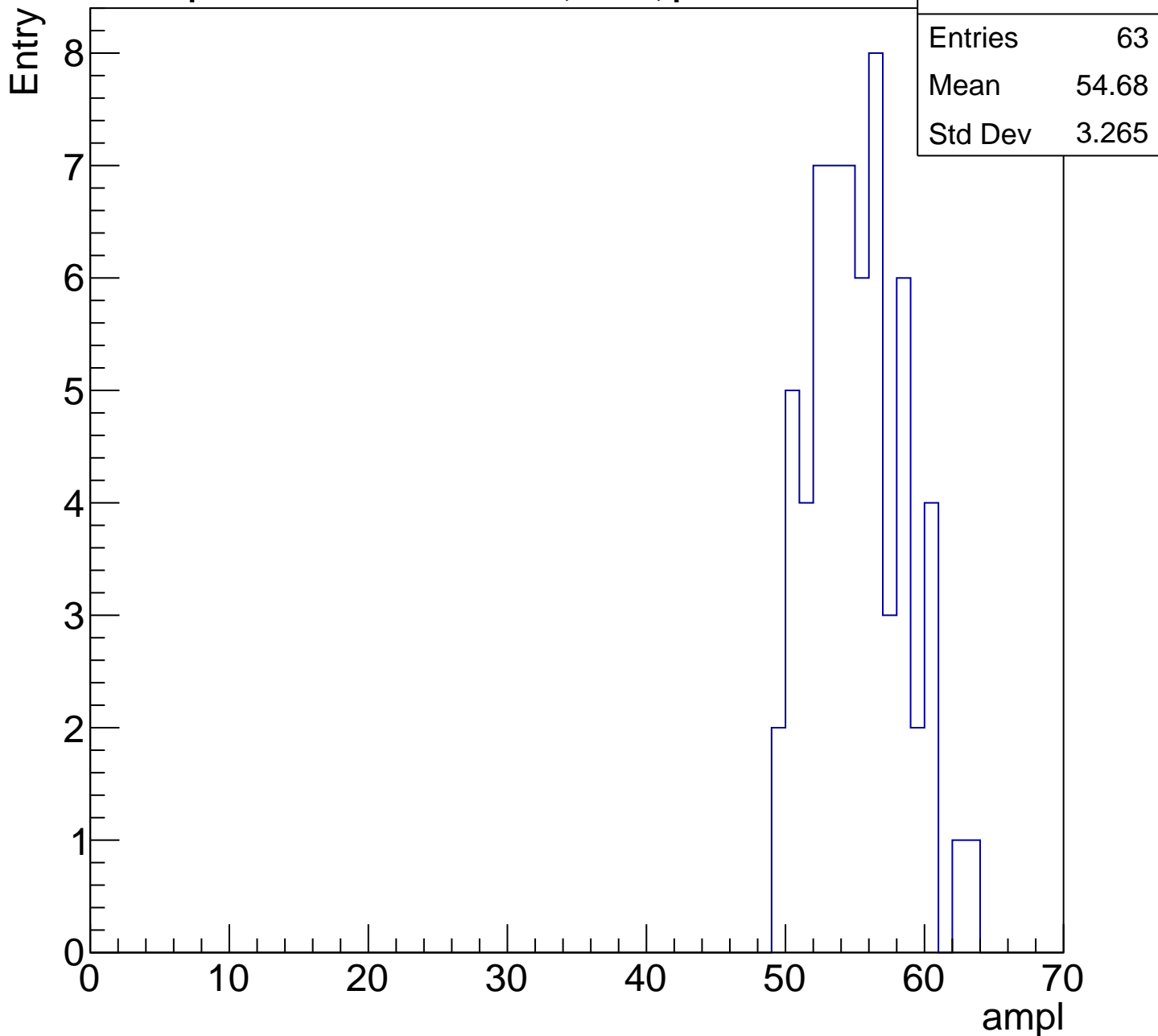
B1L100S, U15-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

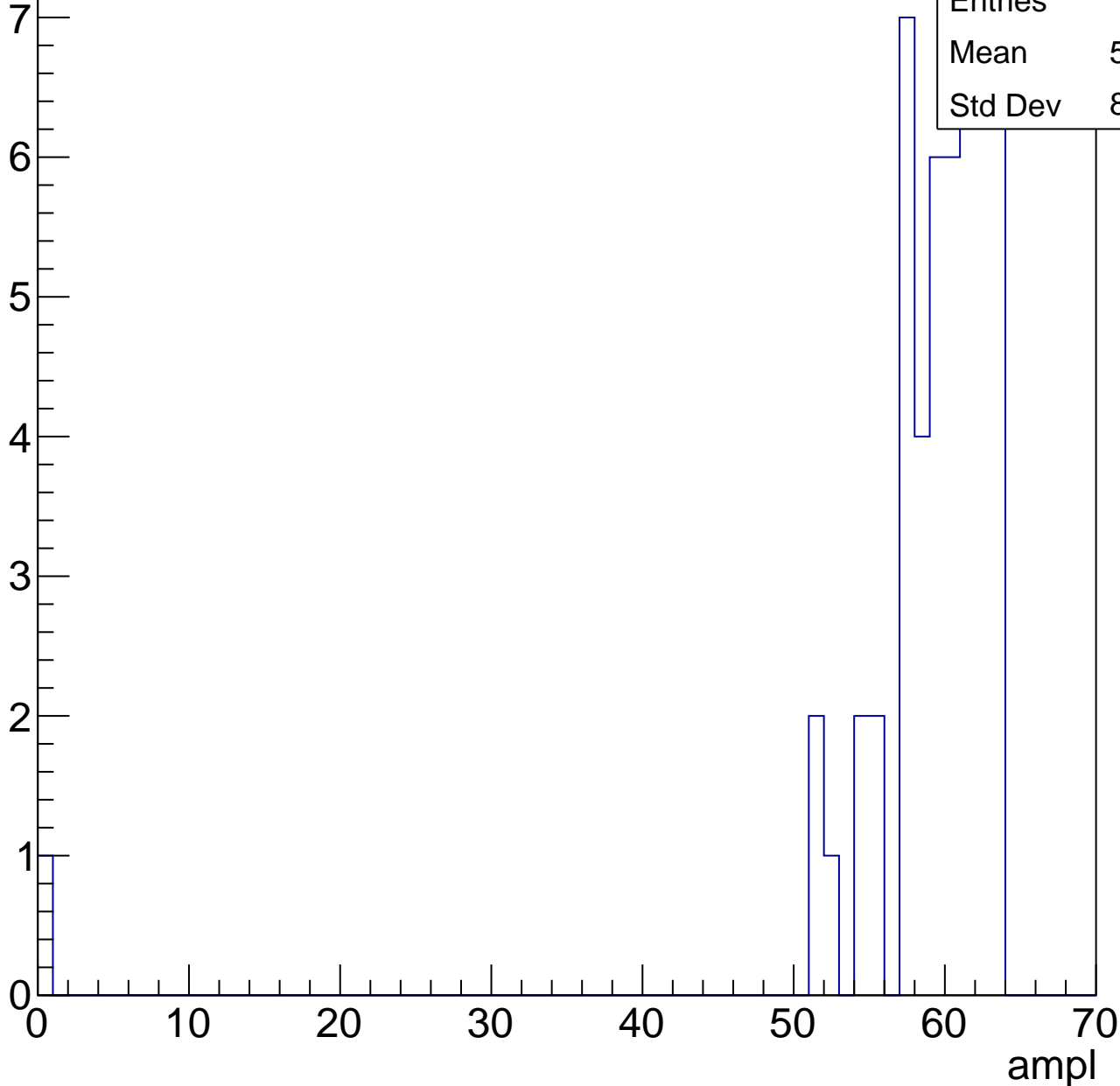


B1L100S, U15-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	58.06
Std Dev	8.699



B1L100S, U15-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

Mean

61.22

Std Dev

1.397

ampl

0 10 20 30 40 50 60 70

3

2.5

2

1.5

1

0.5

0

Entries

9

Mean

61.22

Std Dev

1.397

ampl

0 10 20 30 40 50 60 70

B1L100S, U15-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U15-ch3, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	29.32
Std Dev	4.668

Gaus mean : 29.8928

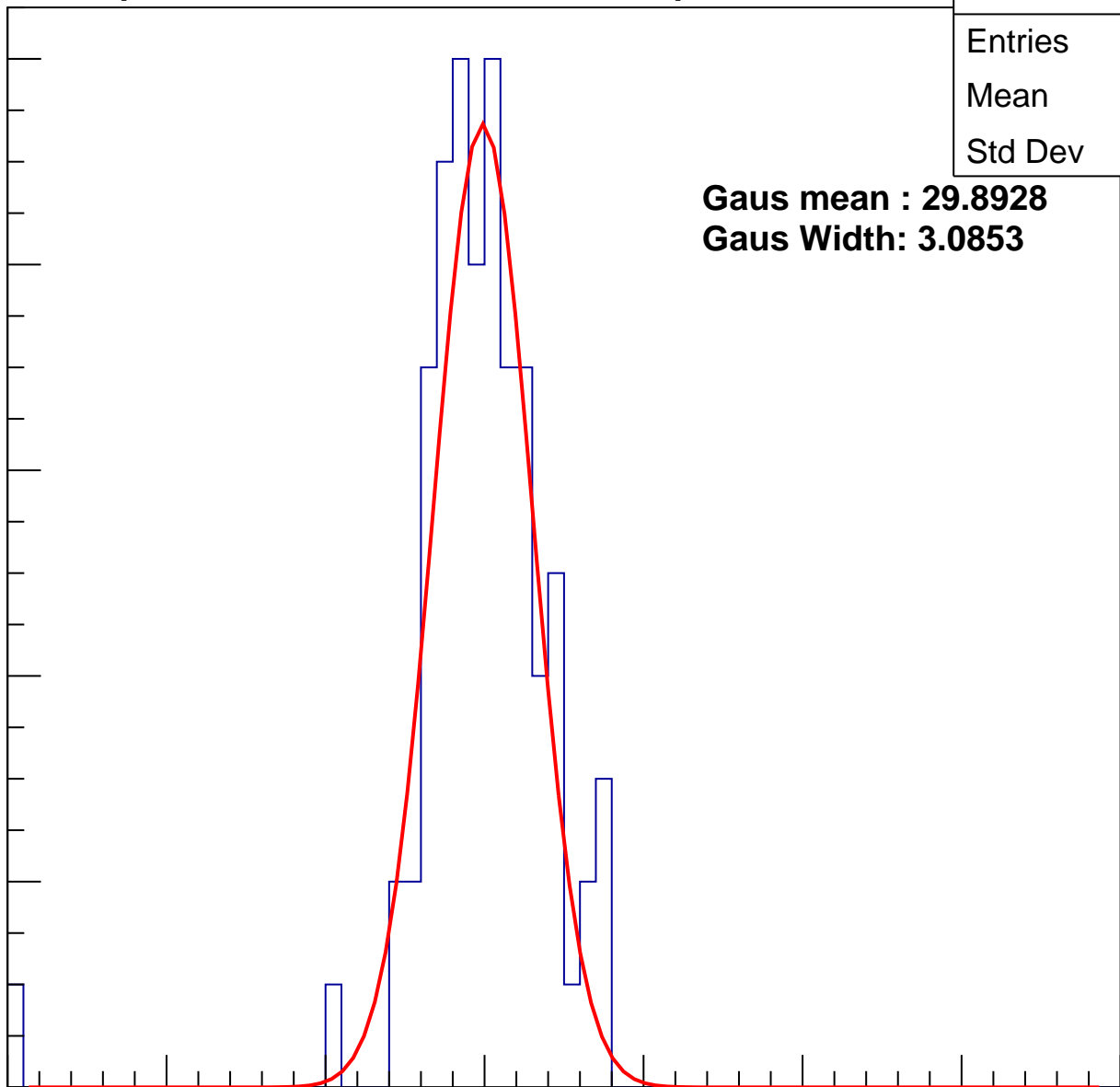
Gaus Width: 3.0853

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch3, adc1

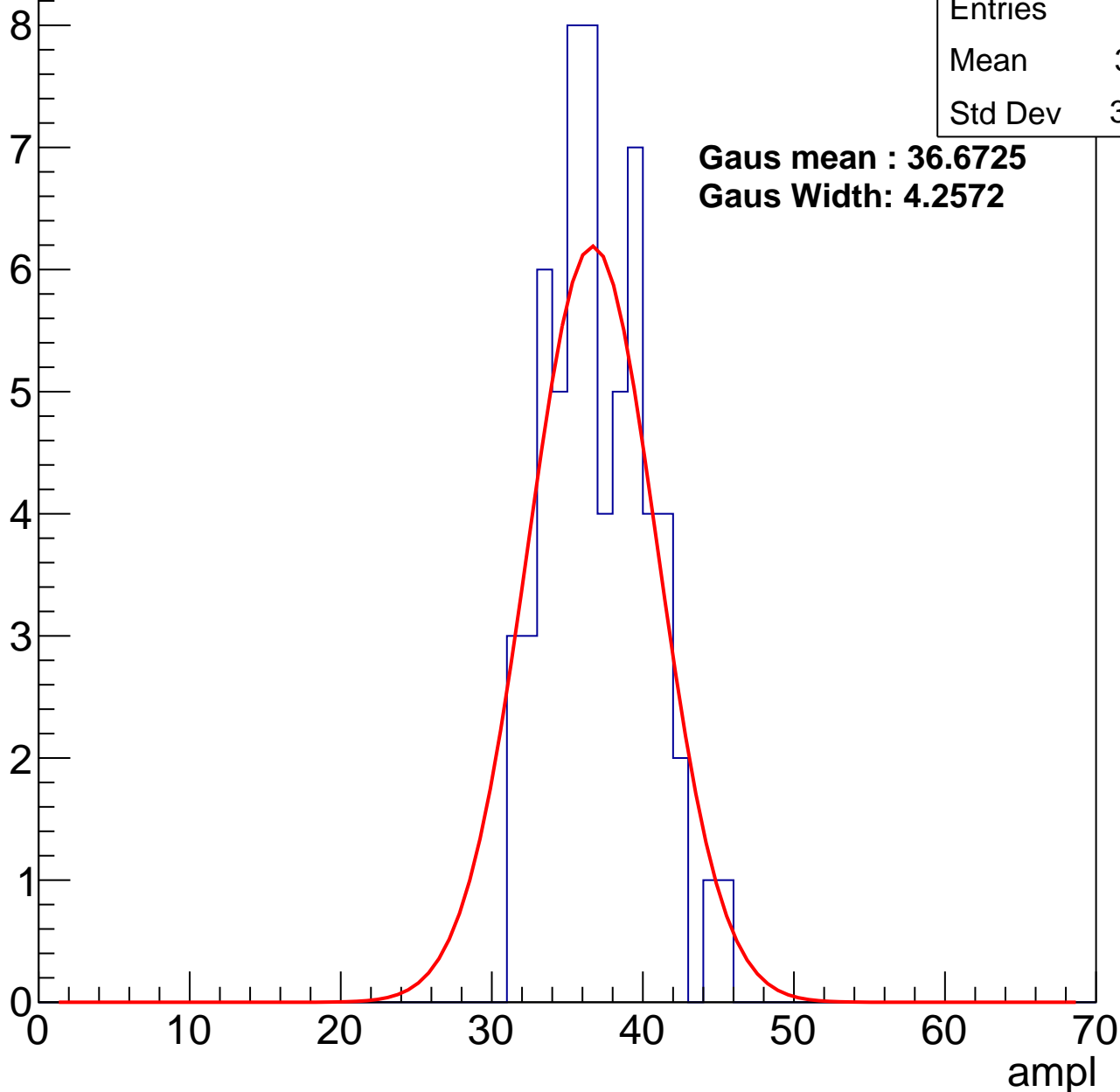
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.61
Std Dev	3.266

Gaus mean : 36.6725

Gaus Width: 4.2572



B1L100S, U15-ch3, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	43.09
Std Dev	3.569

Gaus mean : 43.0801

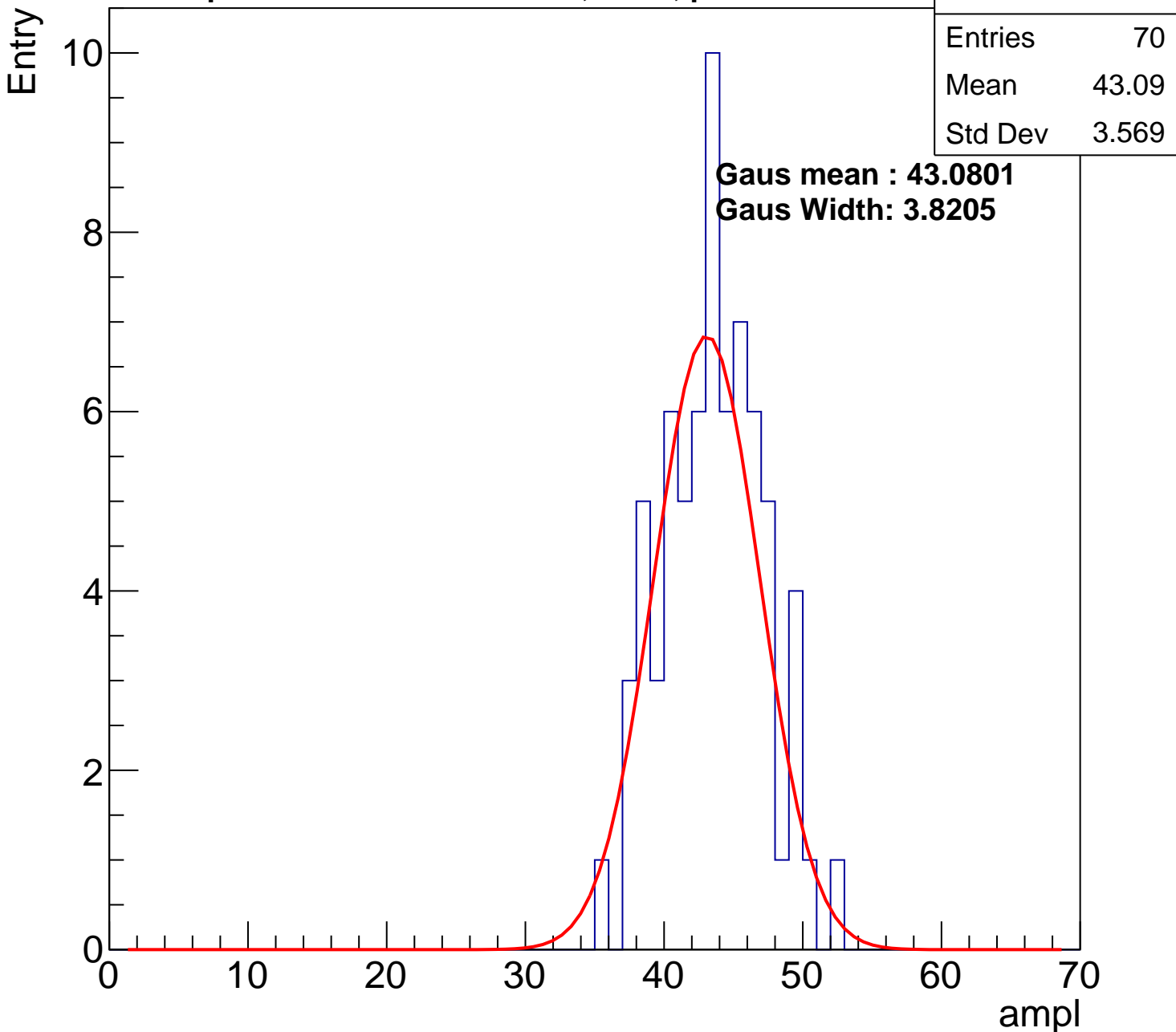
Gaus Width: 3.8205

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

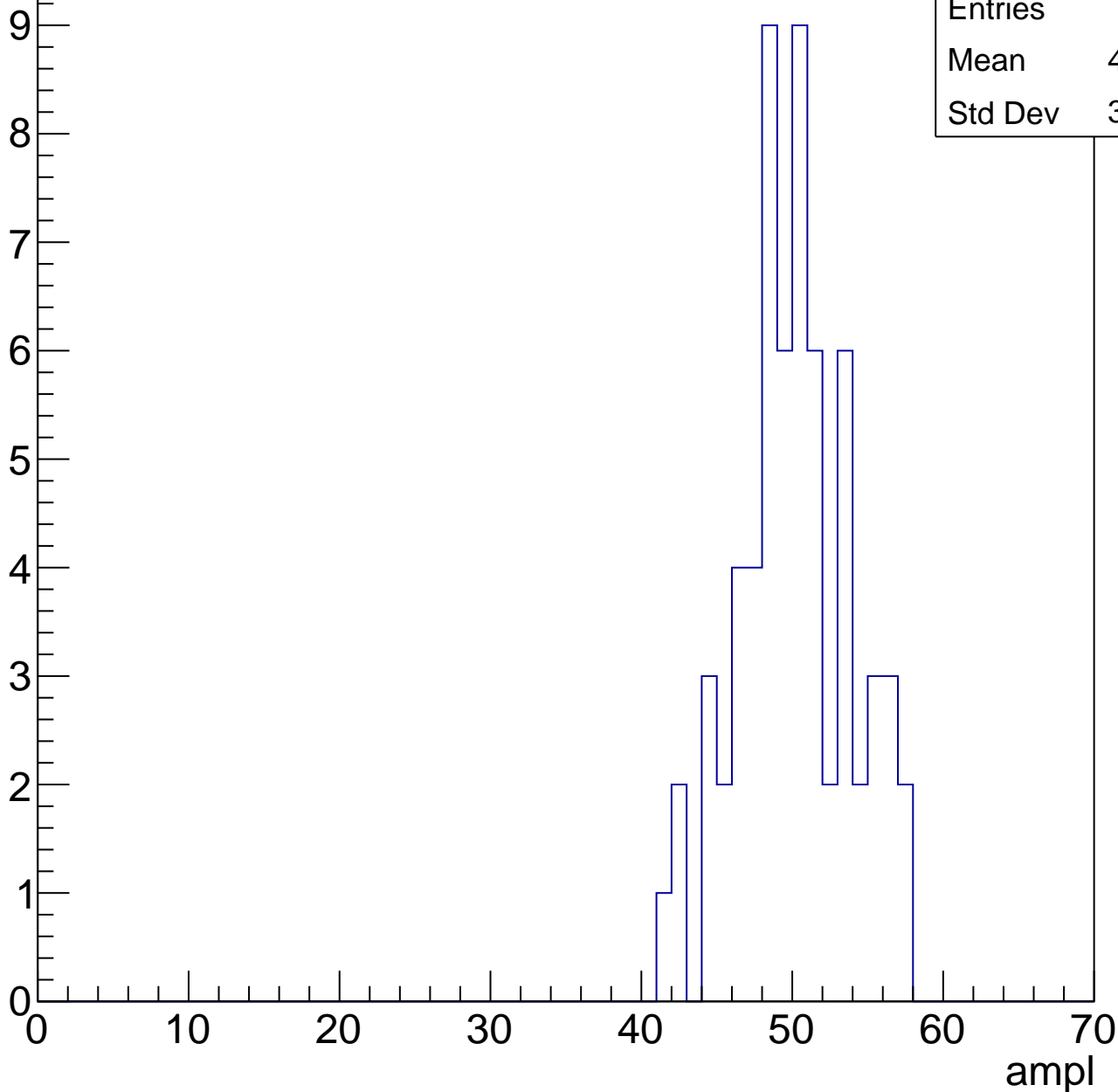


B1L100S, U15-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.66
Std Dev	3.713

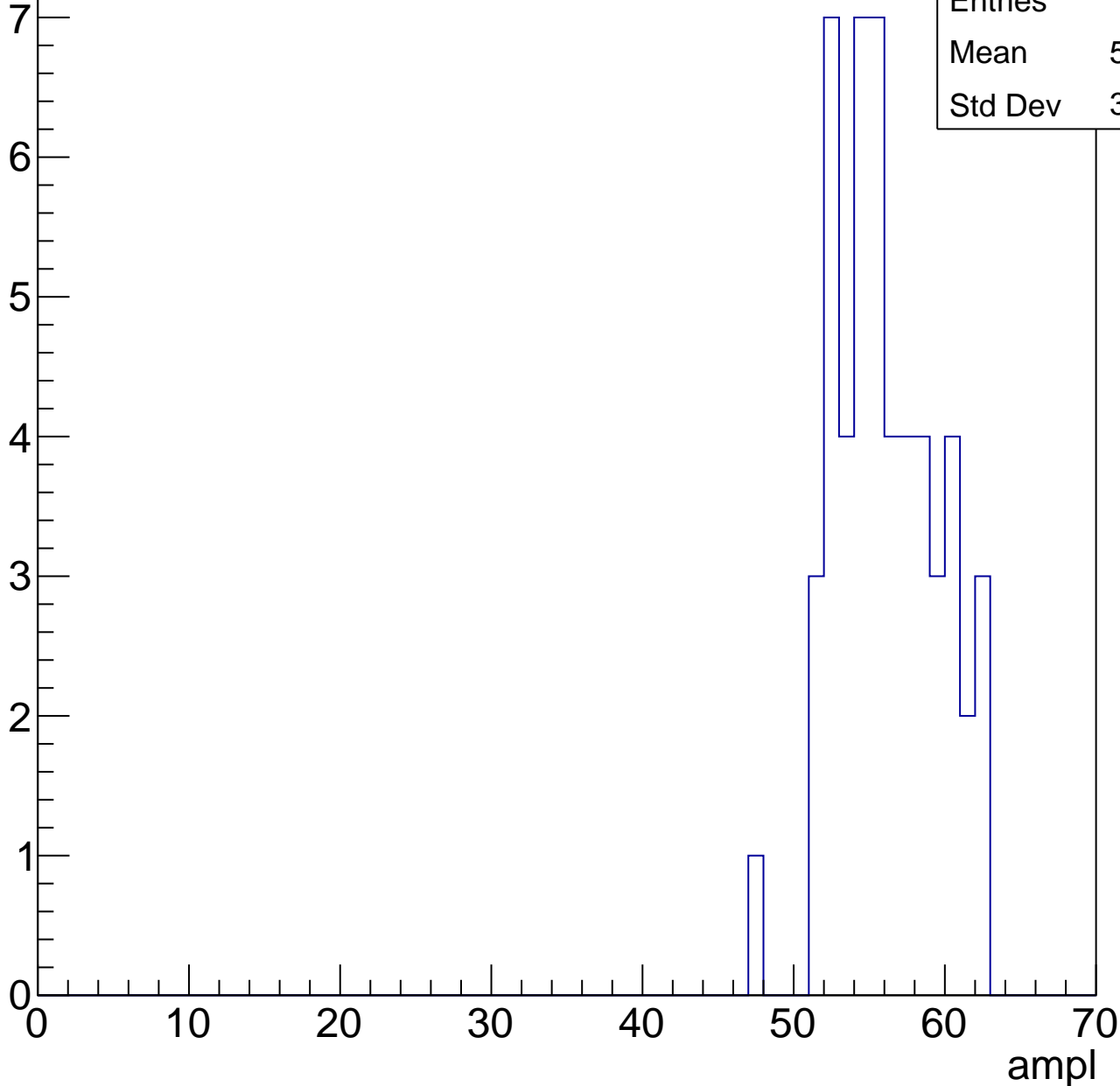


B1L100S, U15-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	55.62
Std Dev	3.366

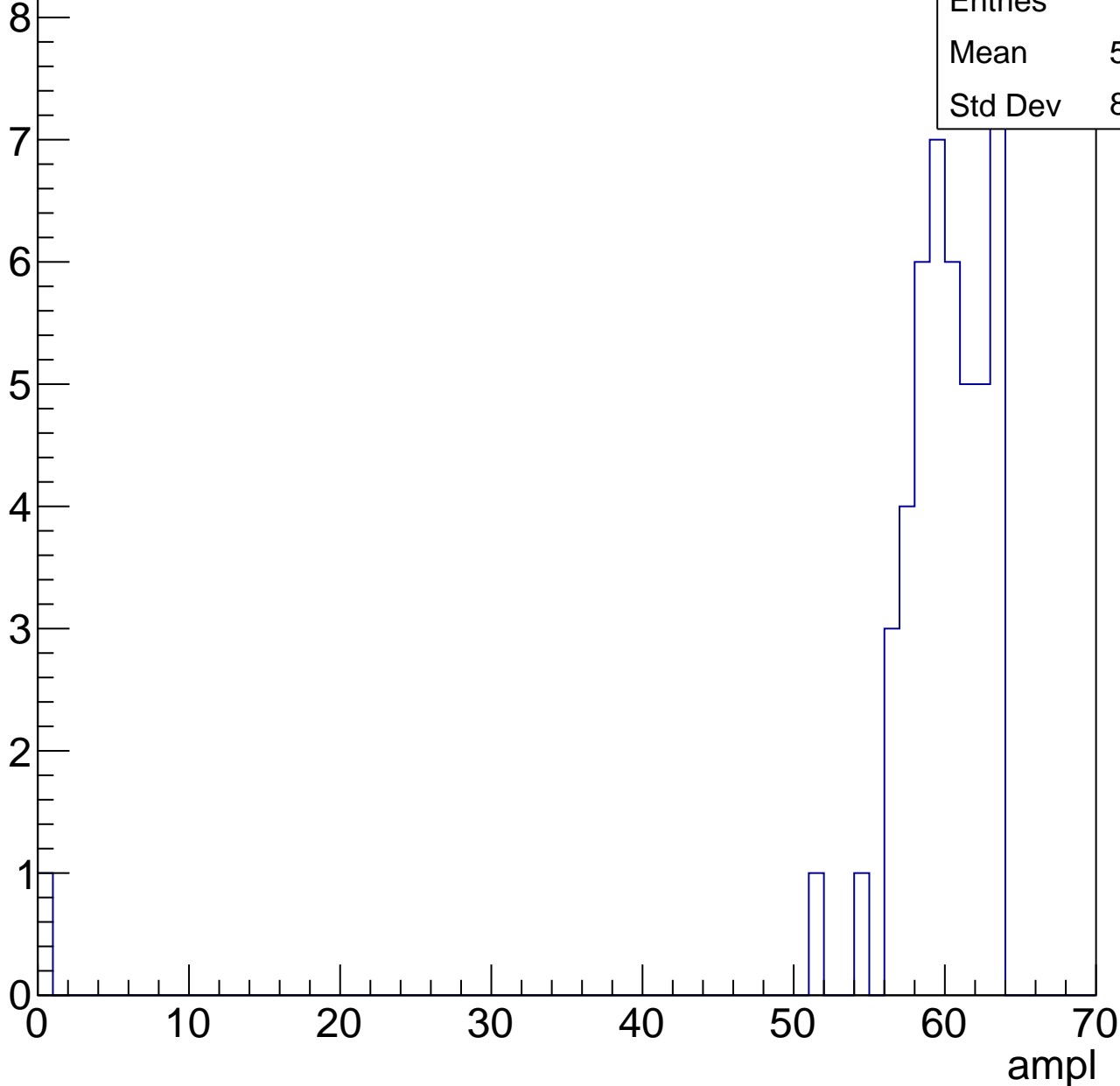


B1L100S, U15-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.32
Std Dev	8.987



B1L100S, U15-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

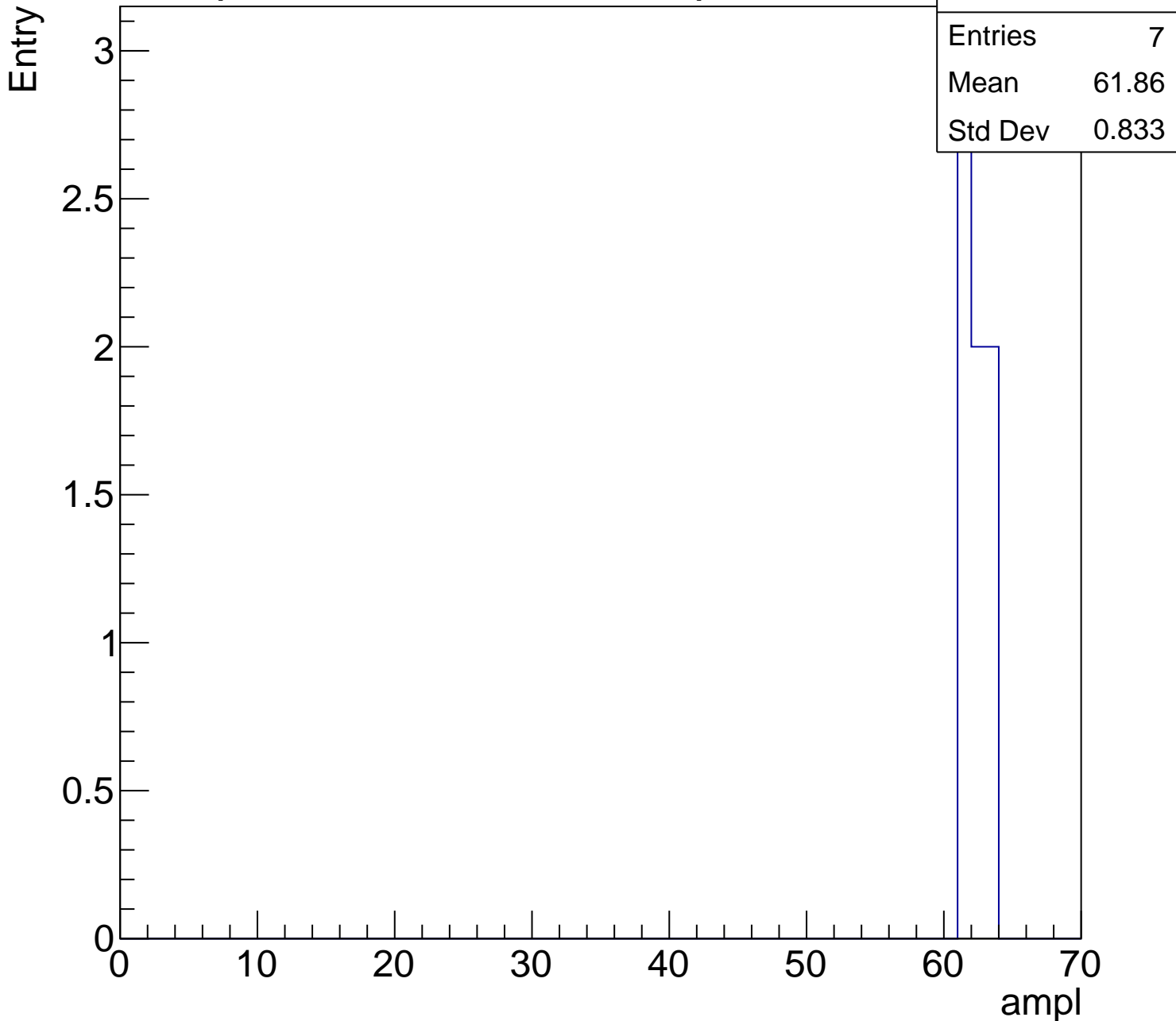
Entry

3
2.5
2
1.5
1
0.5
0

Entries	7
Mean	61.86
Std Dev	0.833

ampl

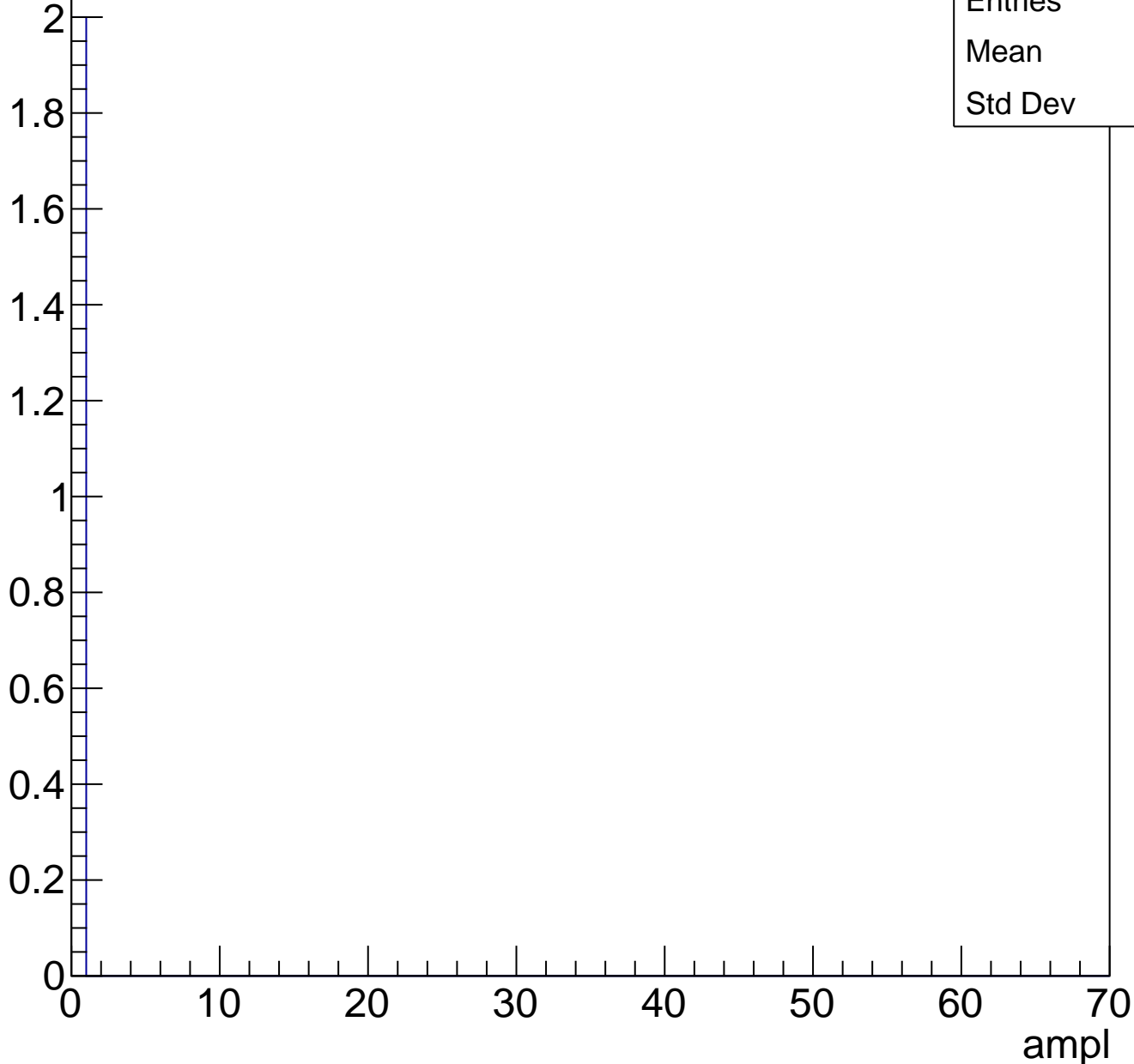
0 10 20 30 40 50 60 70



B1L100S, U15-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch4, adc0

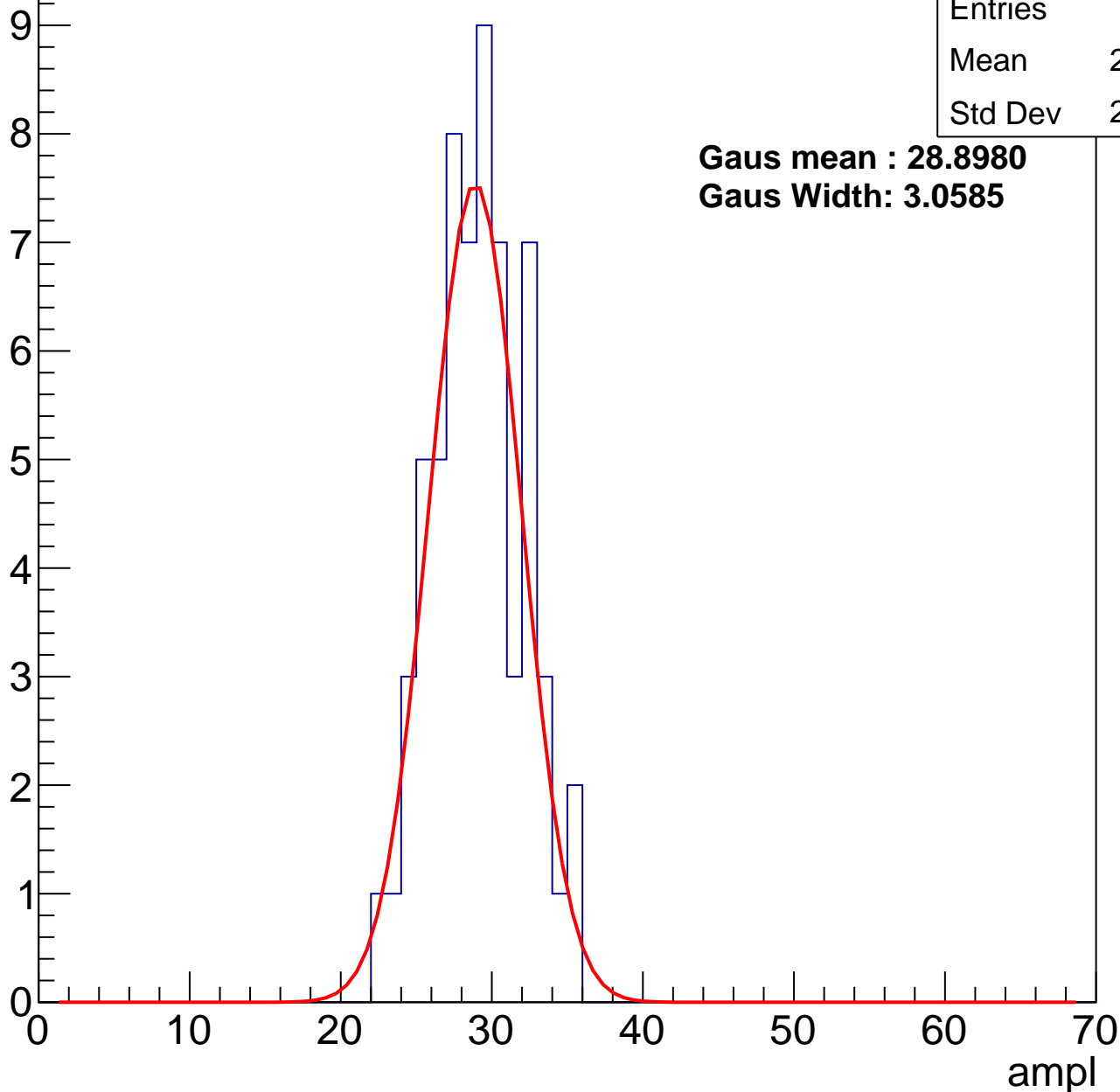
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.63
Std Dev	2.958

Gaus mean : 28.8980

Gaus Width: 3.0585



B1L100S, U15-ch4, adc1

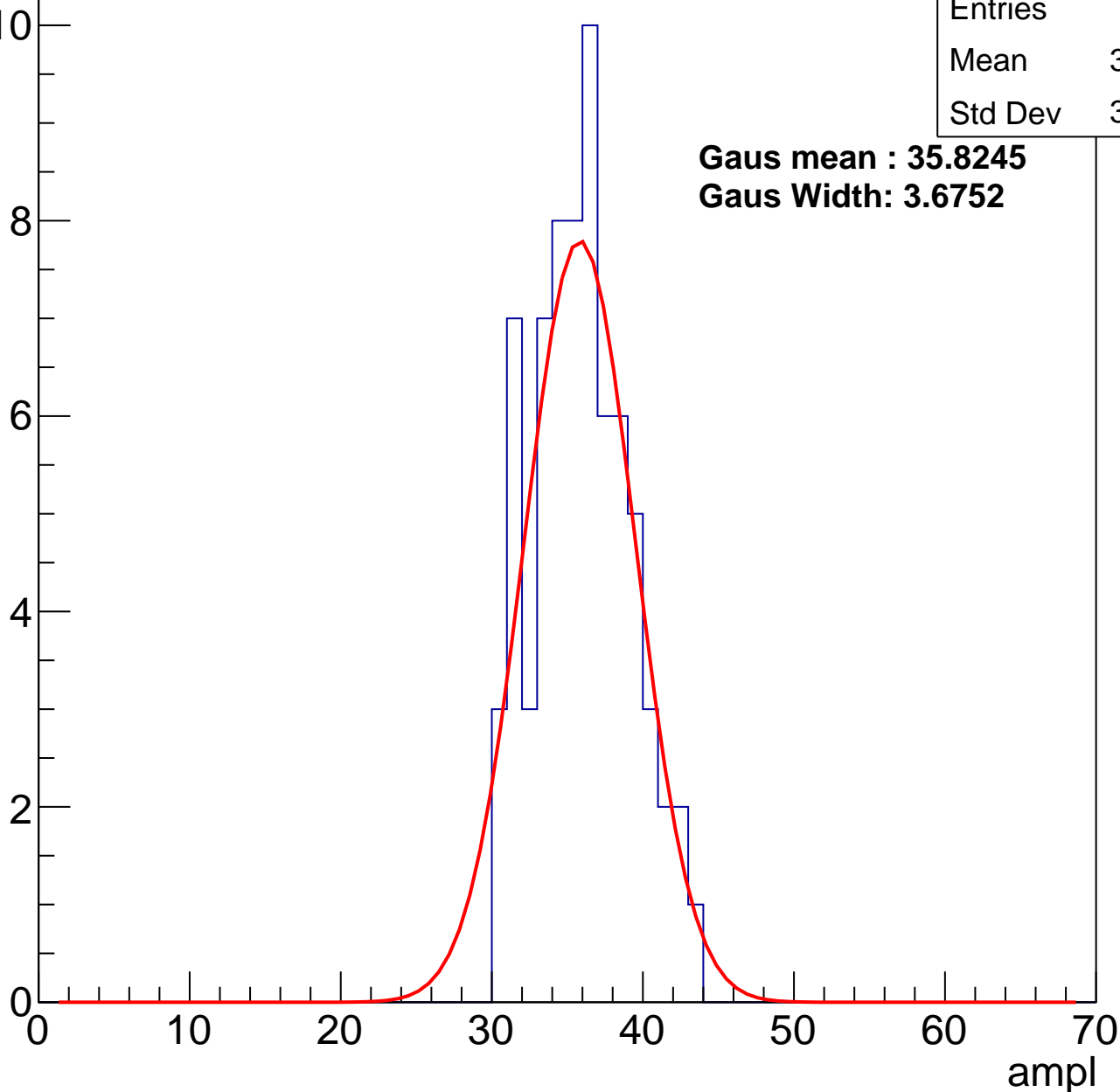
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35.49
Std Dev	3.162

Gaus mean : 35.8245

Gaus Width: 3.6752



B1L100S, U15-ch4, adc2

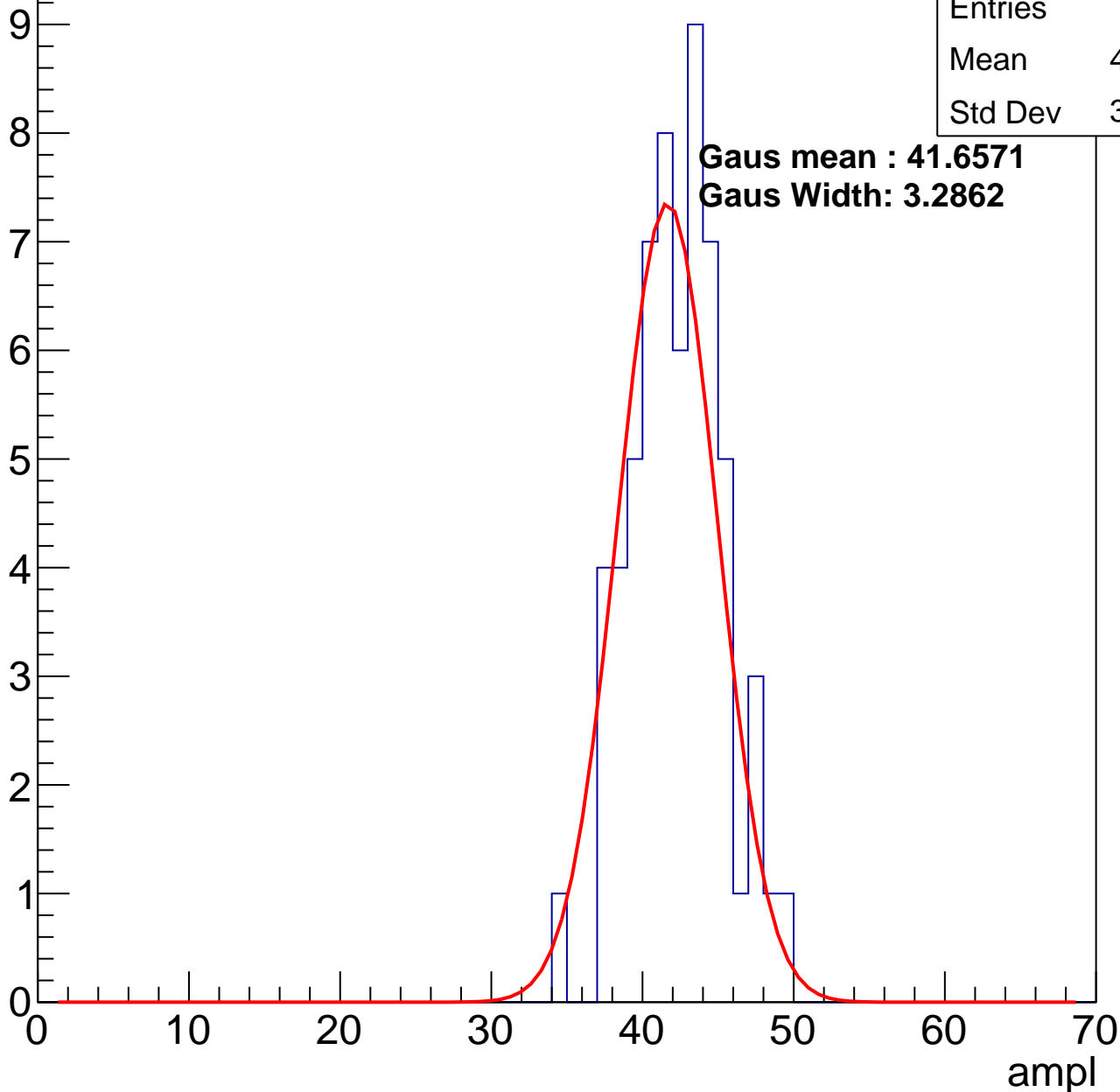
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	41.82
Std Dev	3.019

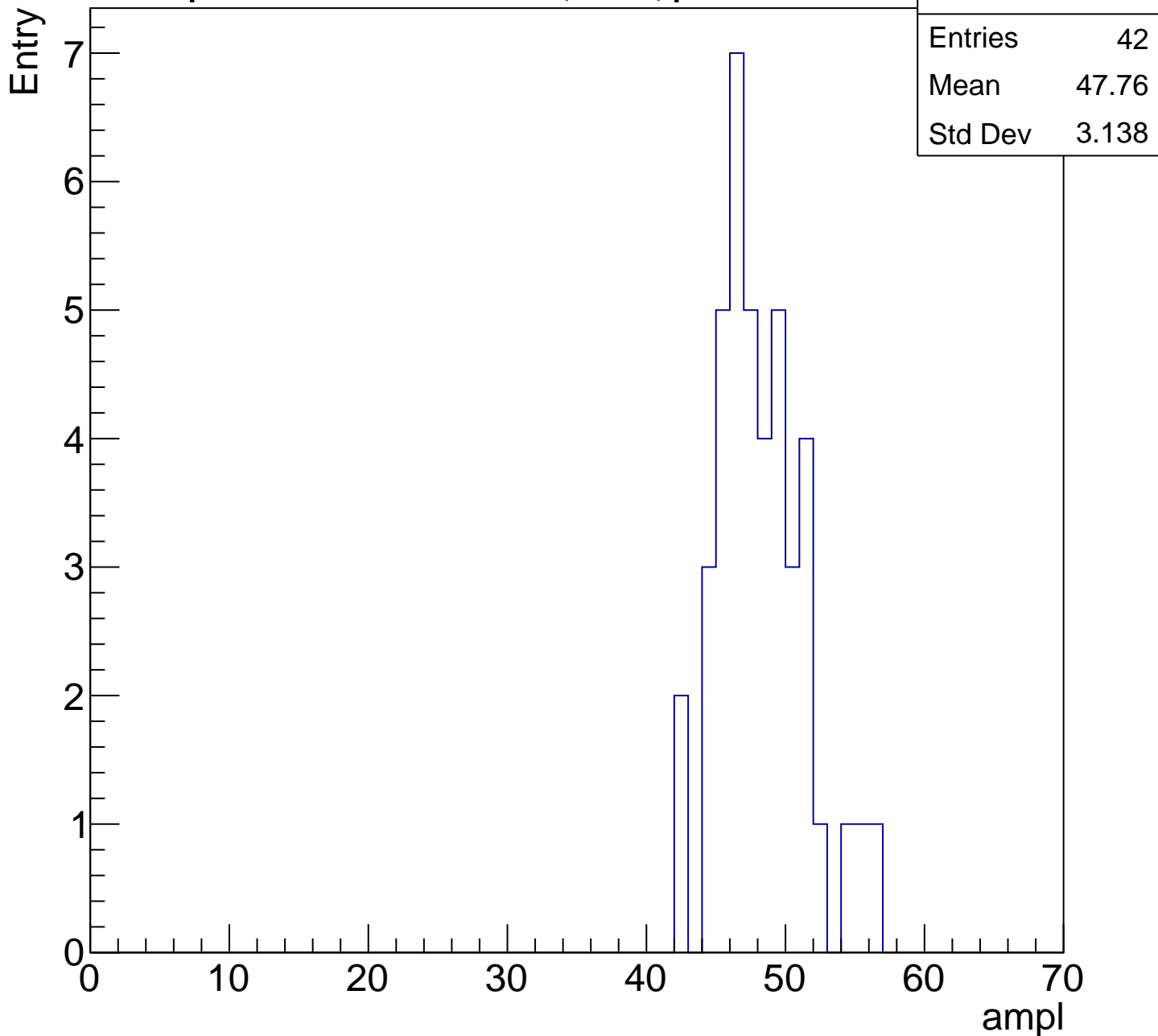
Gaus mean : 41.6571

Gaus Width: 3.2862



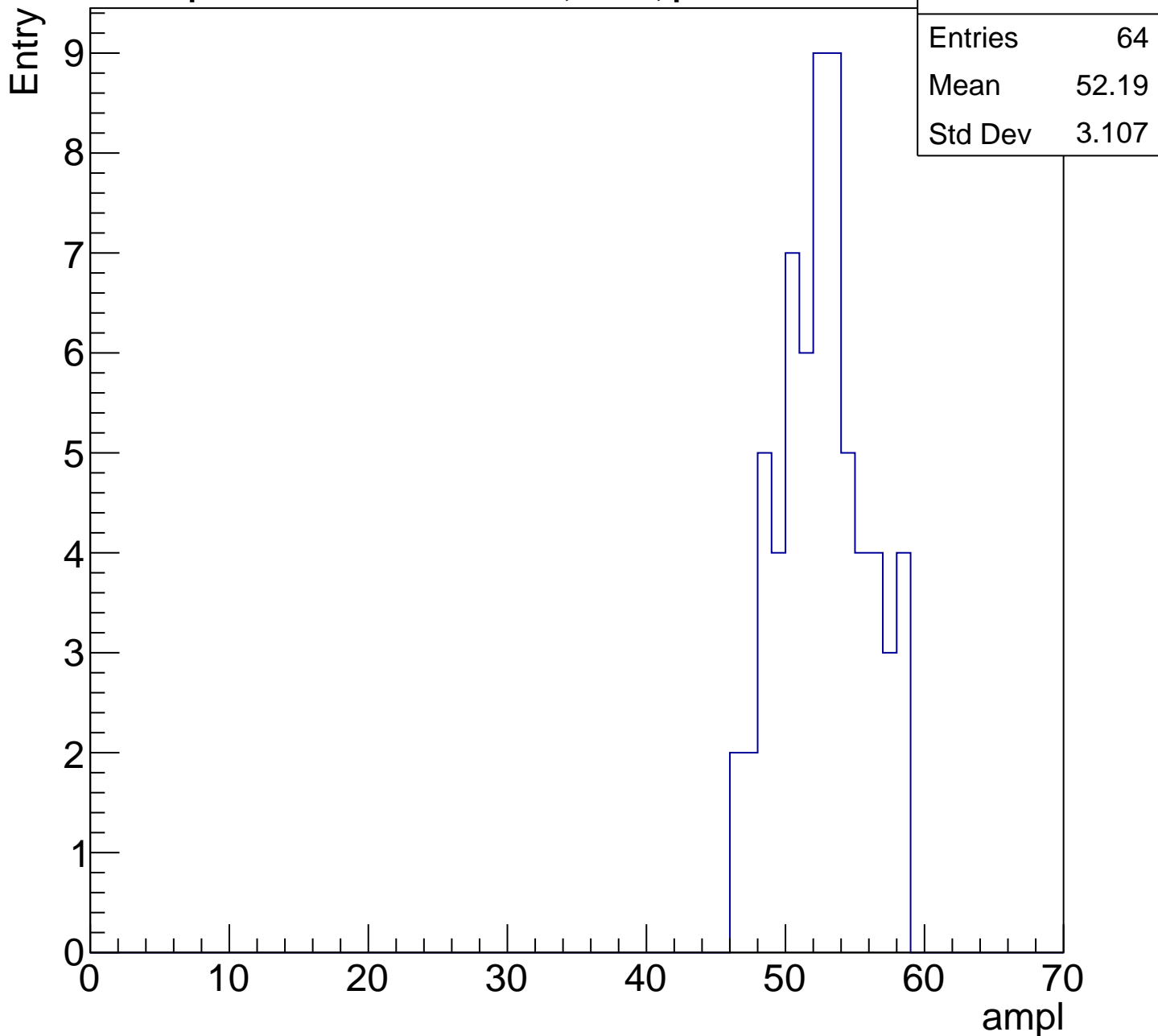
B1L100S, U15-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

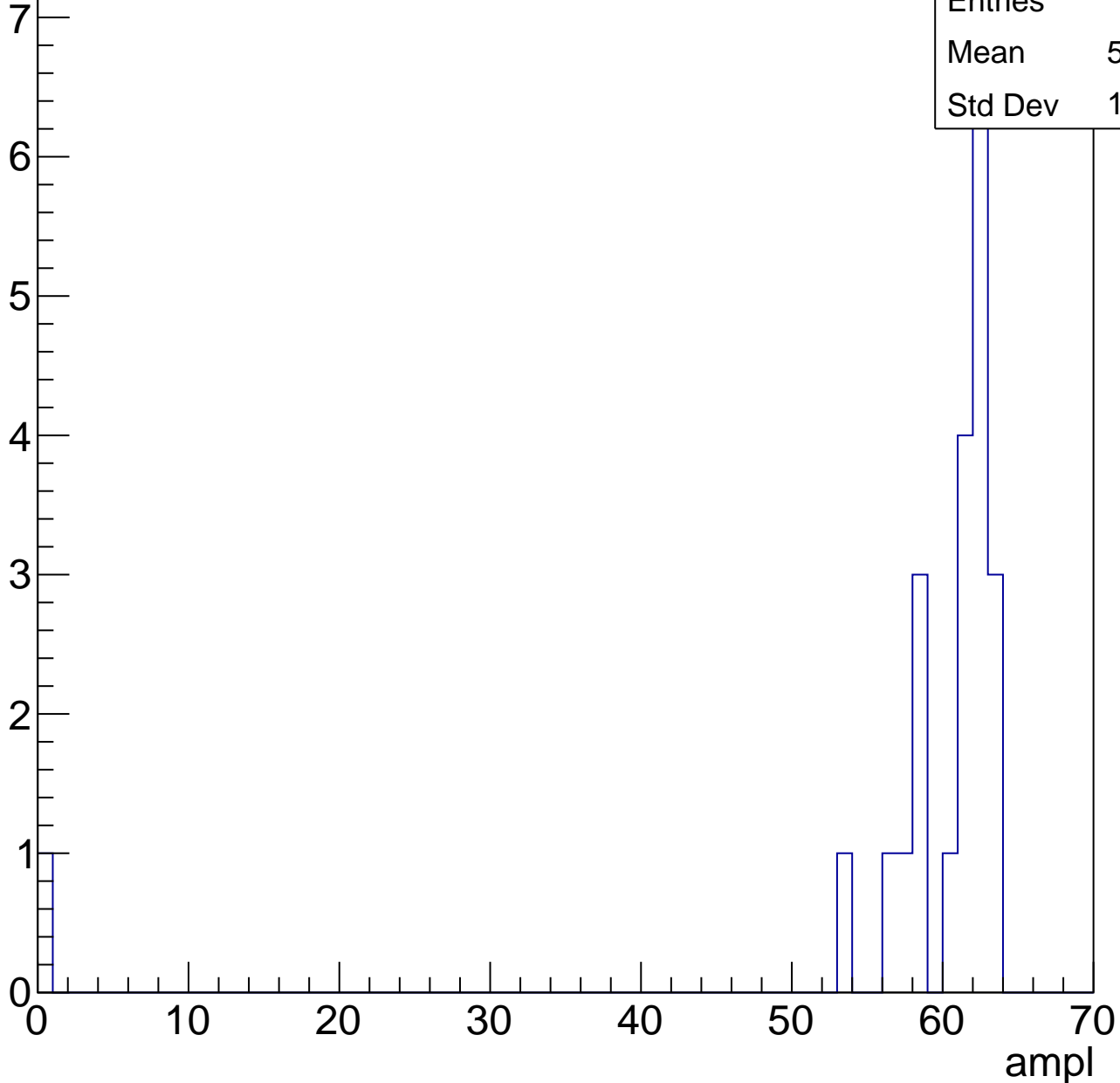
Entries	57
Mean	58.84
Std Dev	2.694

B1L100S, U15-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	57.59
Std Dev	12.82



B1L100S, U15-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch5, adc0

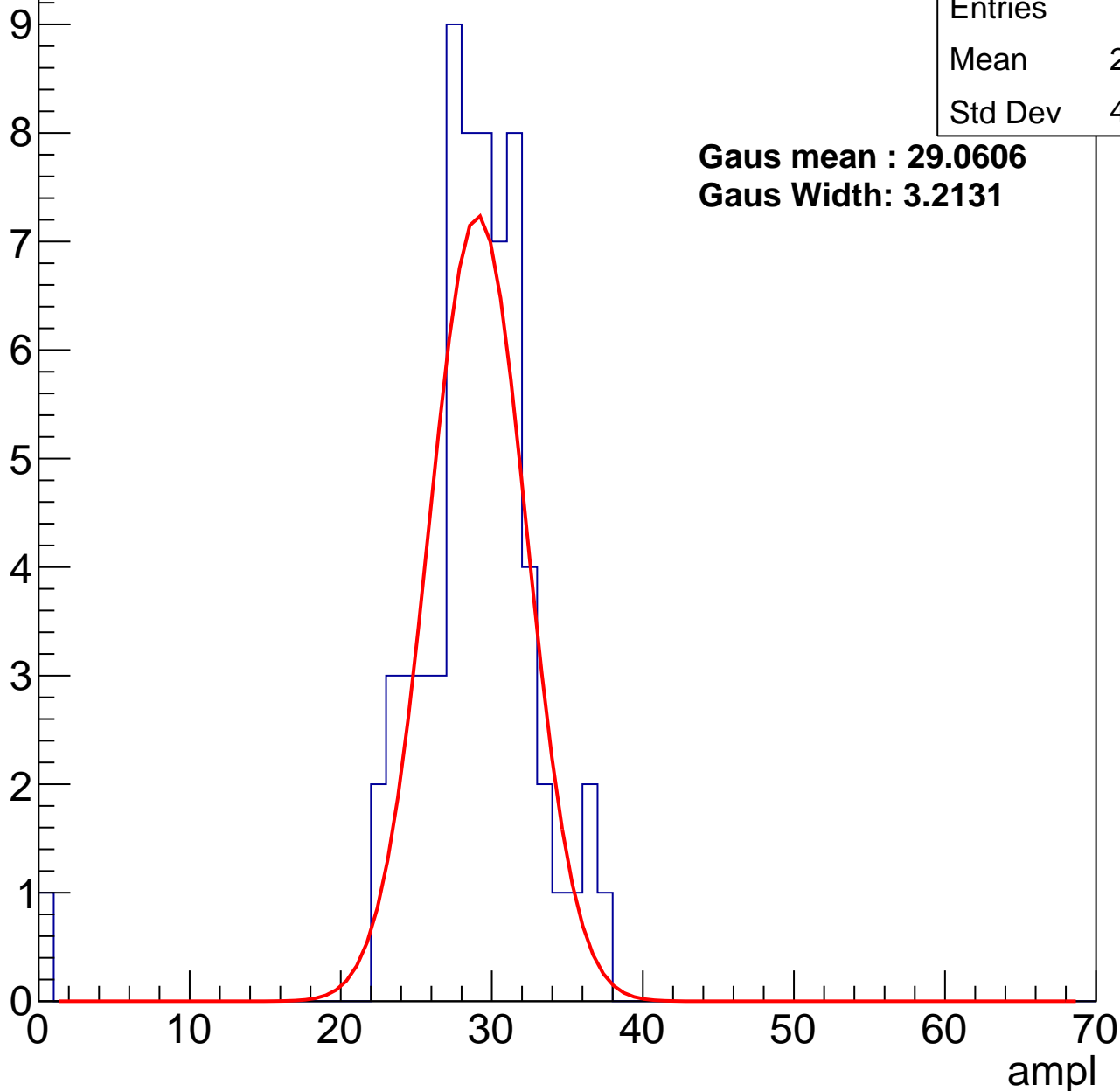
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	28.29
Std Dev	4.827

Gaus mean : 29.0606

Gaus Width: 3.2131



B1L100S, U15-ch5, adc1

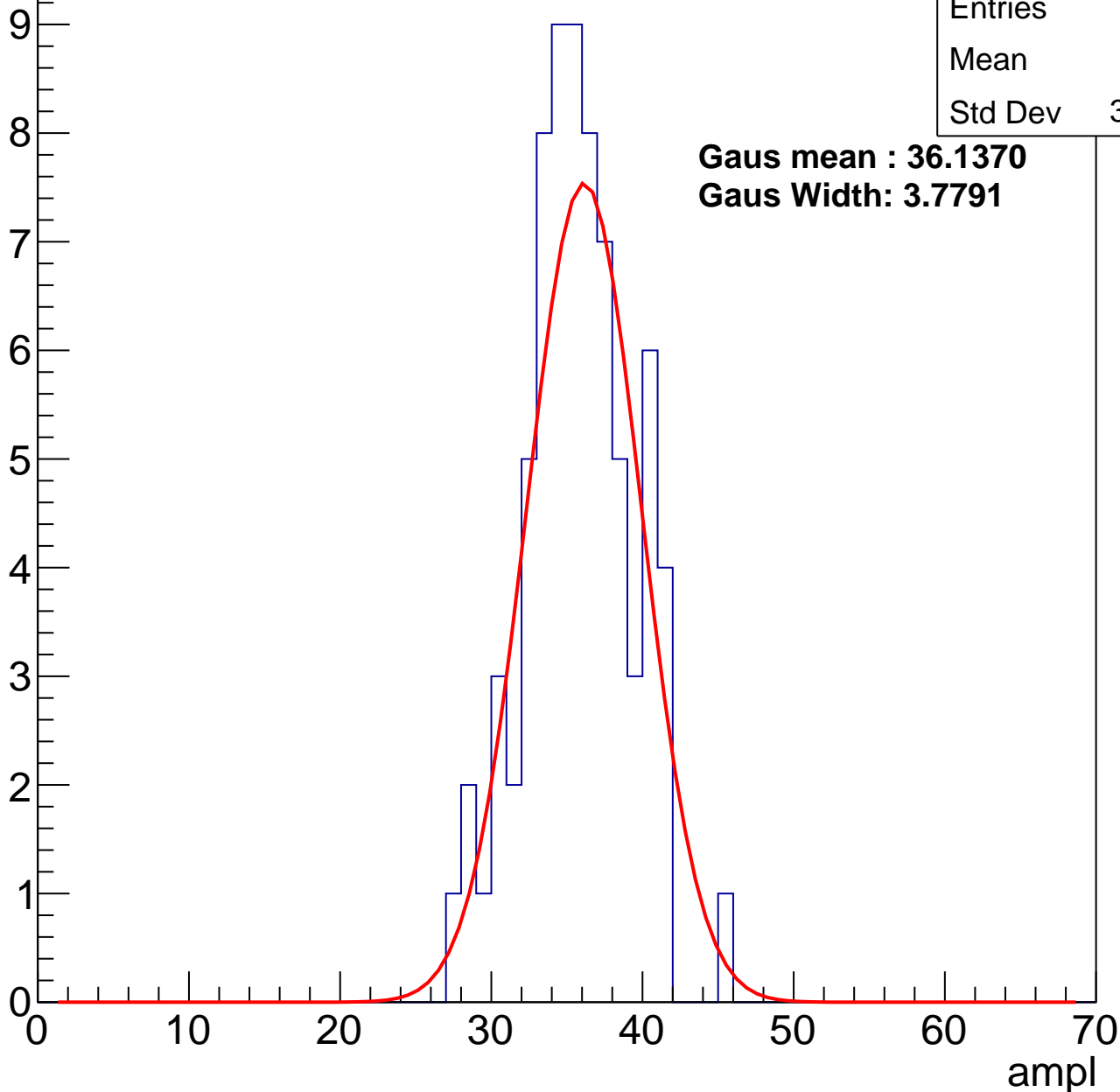
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	35.3
Std Dev	3.502

Gaus mean : 36.1370

Gaus Width: 3.7791



B1L100S, U15-ch5, adc2

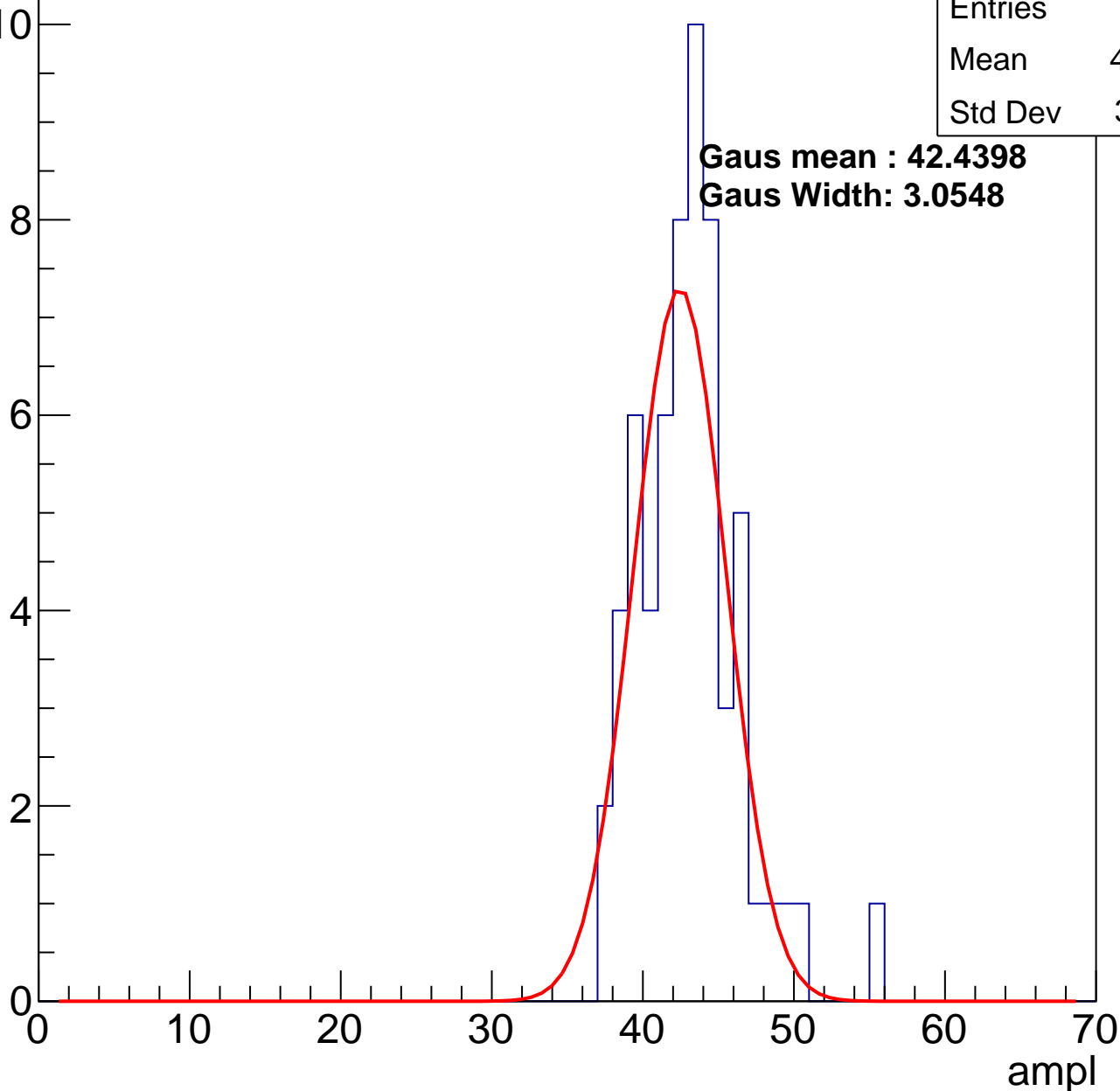
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.59
Std Dev	3.291

Gaus mean : 42.4398

Gaus Width: 3.0548

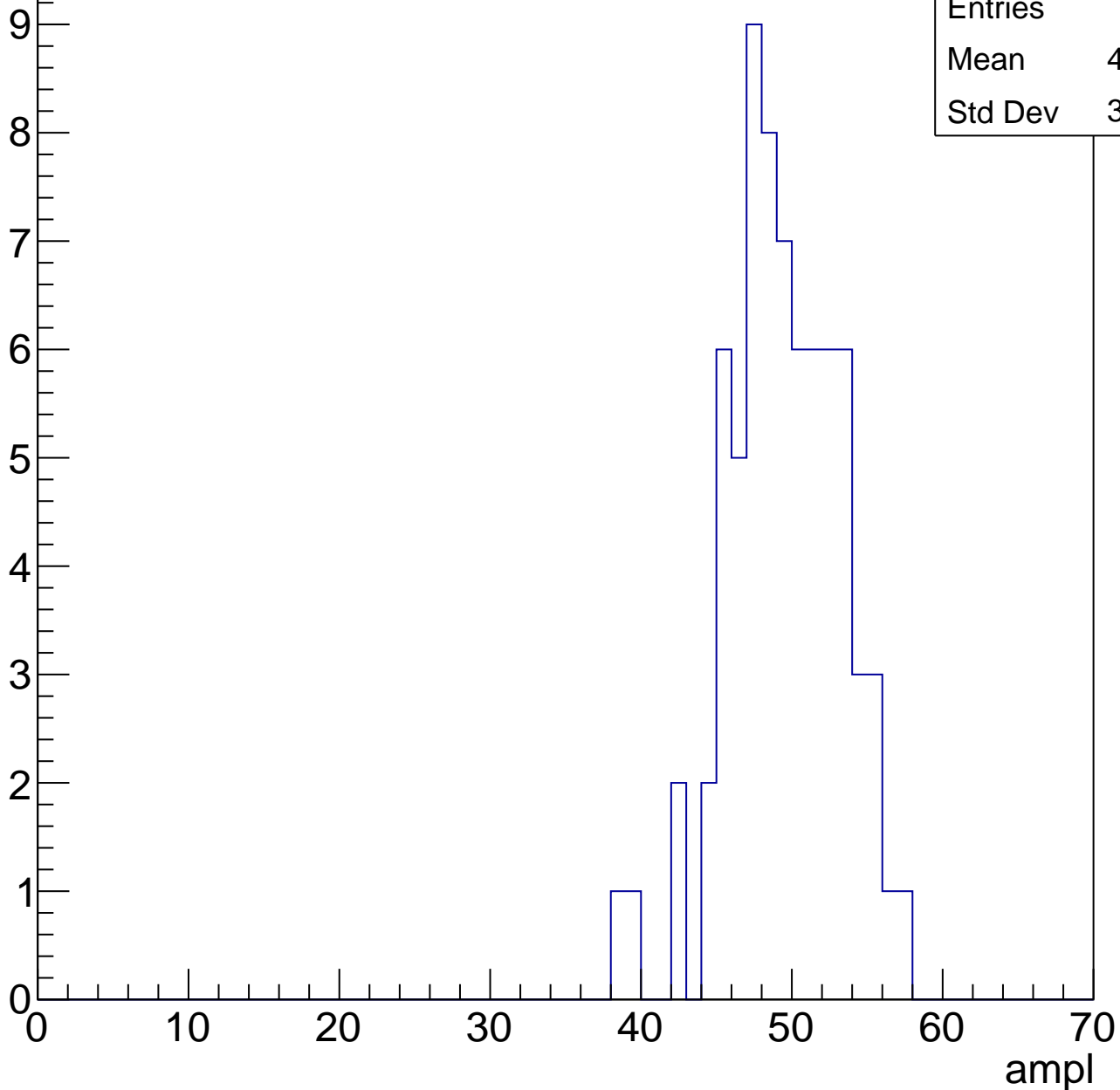


B1L100S, U15-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	48.97
Std Dev	3.756

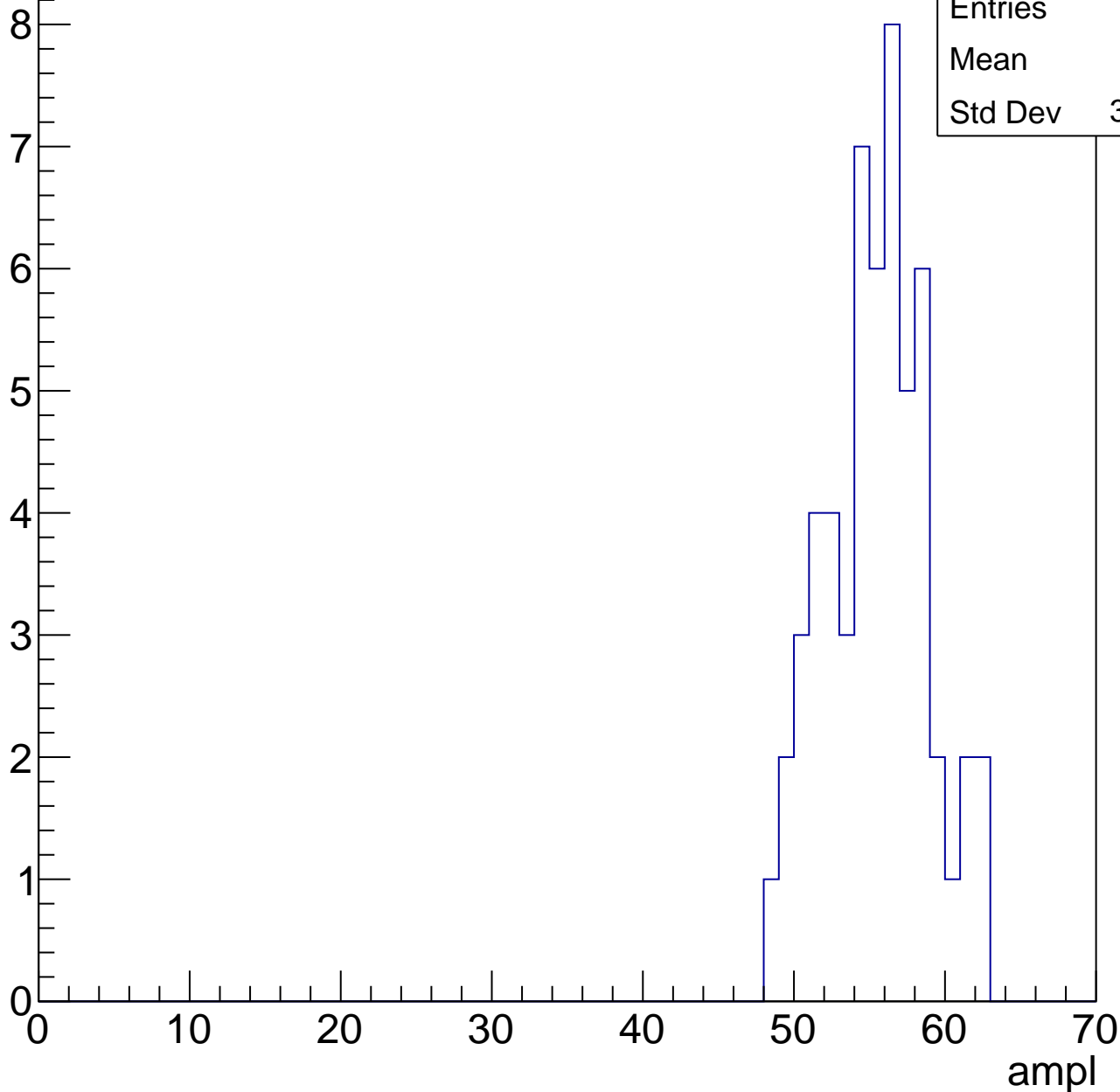


B1L100S, U15-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	55
Std Dev	3.338

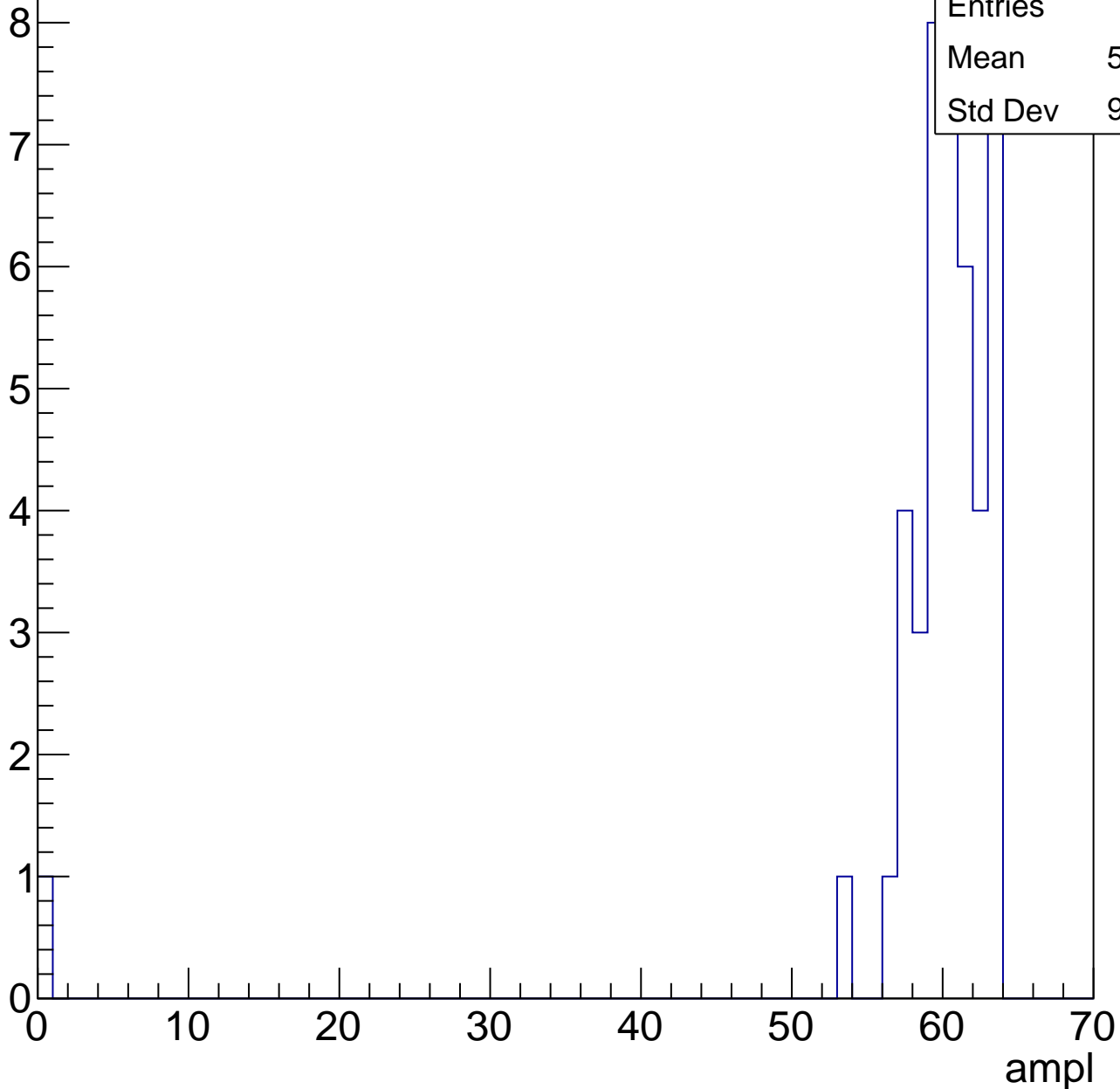


B1L100S, U15-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

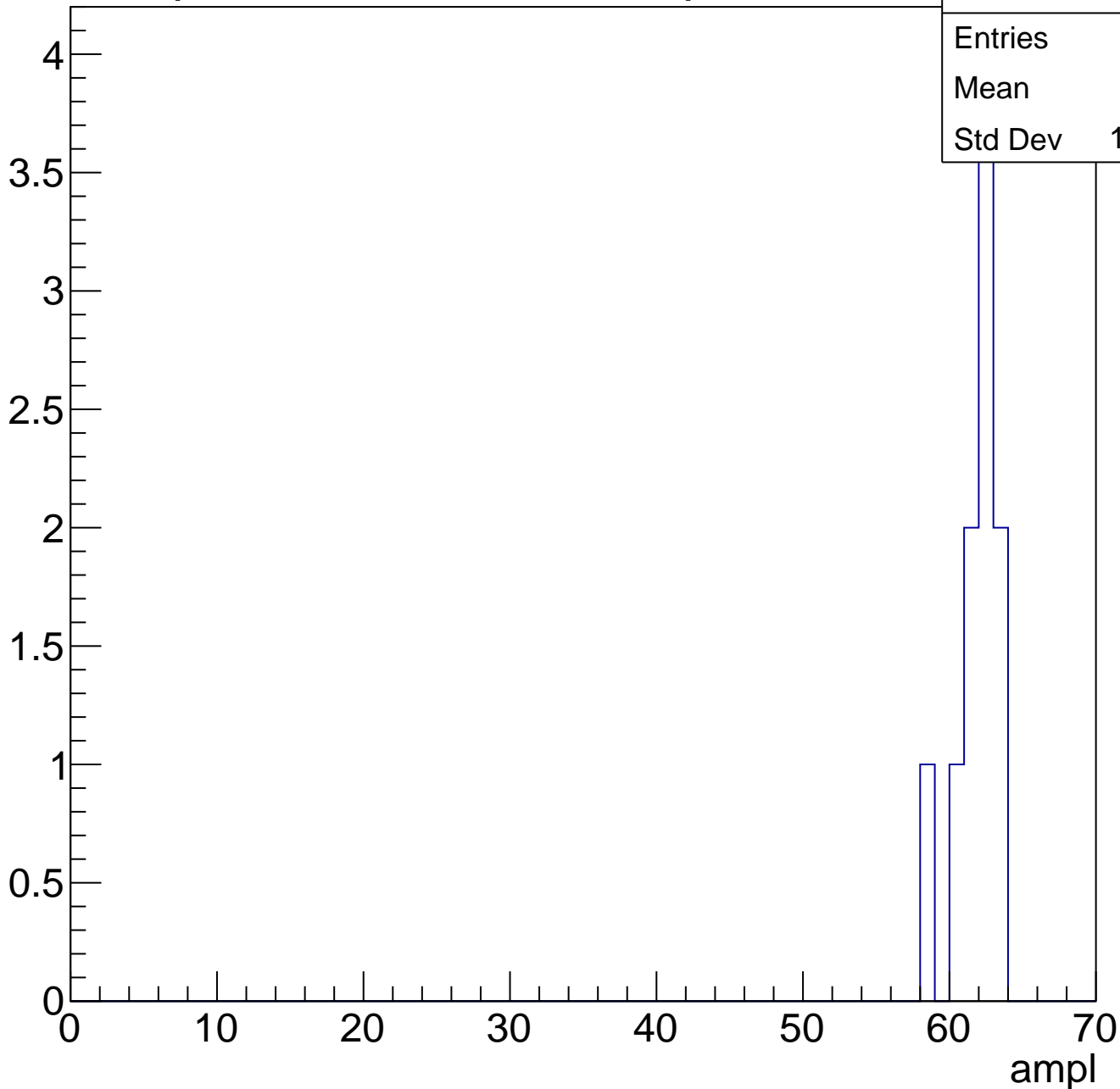
Entries	44
Mean	58.66
Std Dev	9.214



B1L100S, U15-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch6, adc0

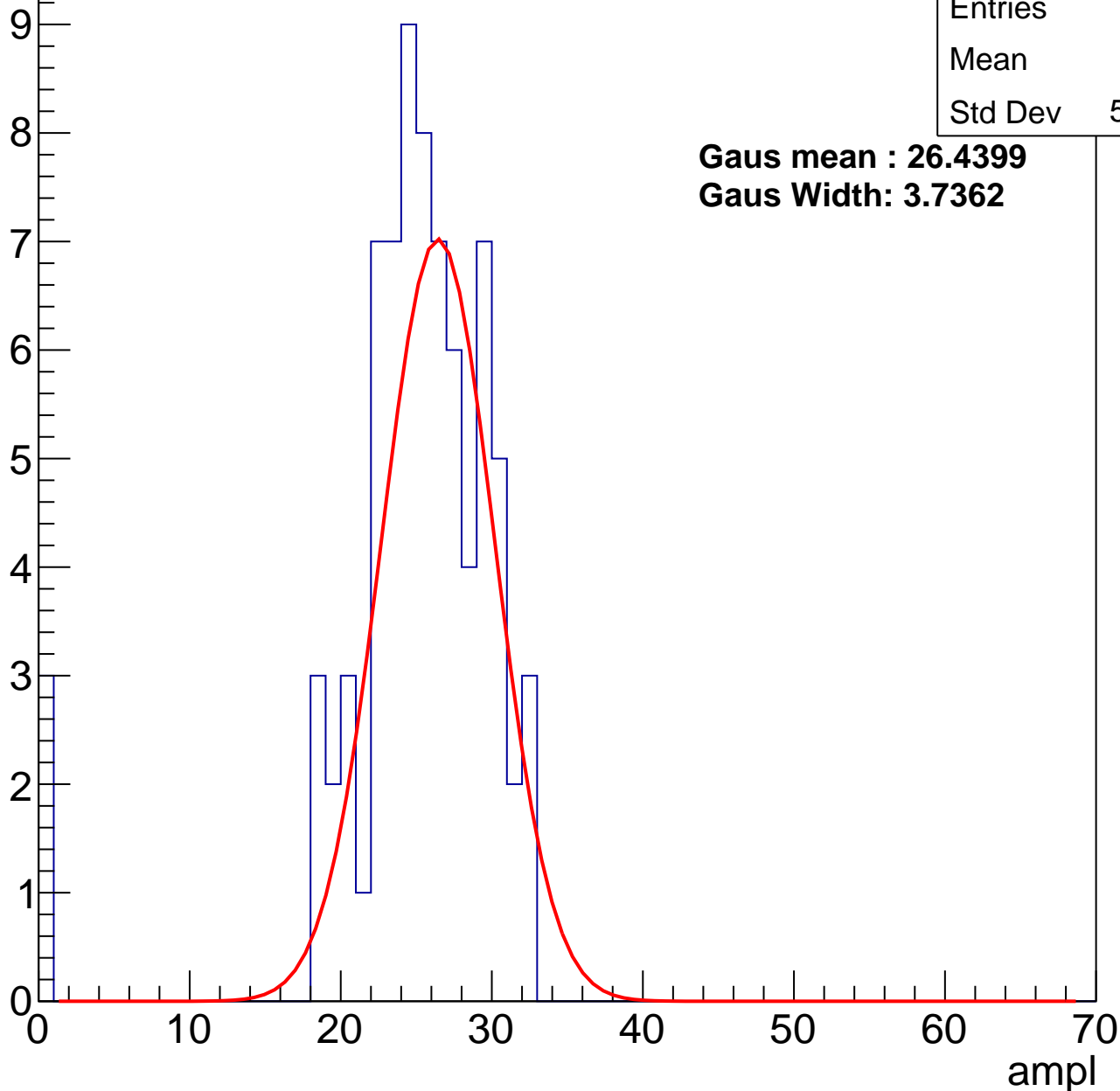
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	24.3
Std Dev	5.998

Gaus mean : 26.4399

Gaus Width: 3.7362



B1L100S, U15-ch6, adc1

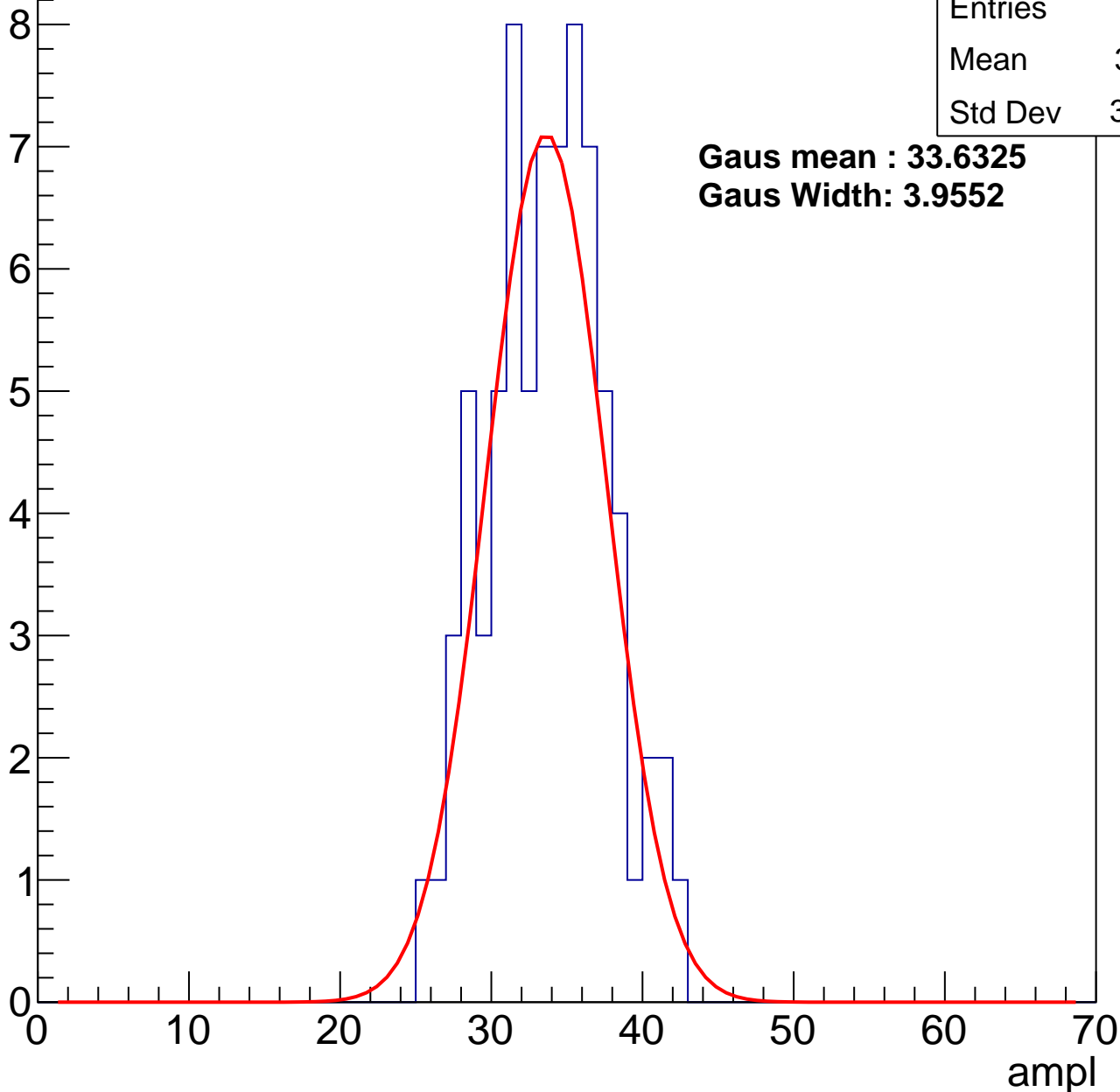
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	33.31
Std Dev	3.819

Gaus mean : 33.6325

Gaus Width: 3.9552



B1L100S, U15-ch6, adc2

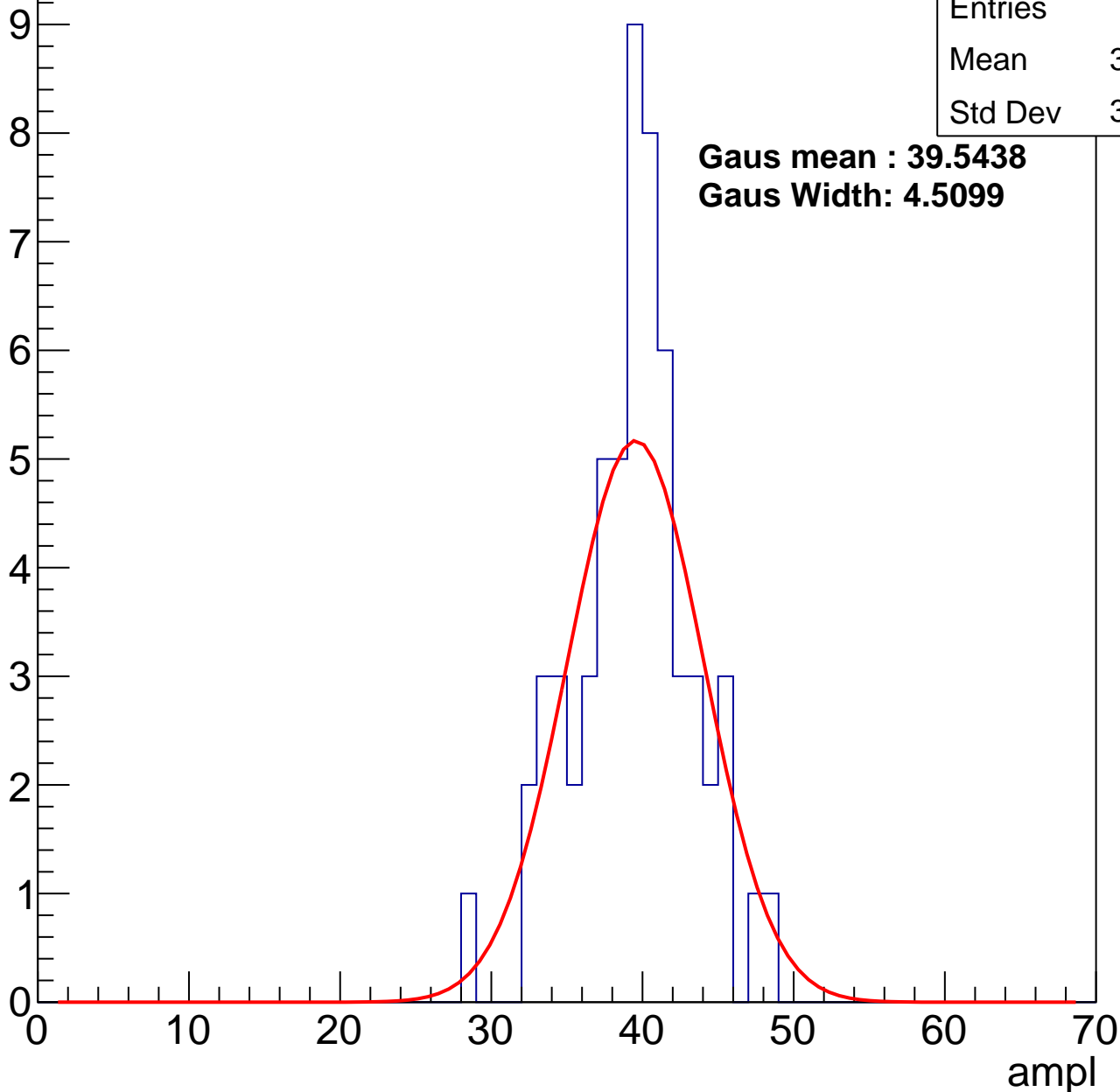
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	38.93
Std Dev	3.864

Gaus mean : 39.5438

Gaus Width: 4.5099

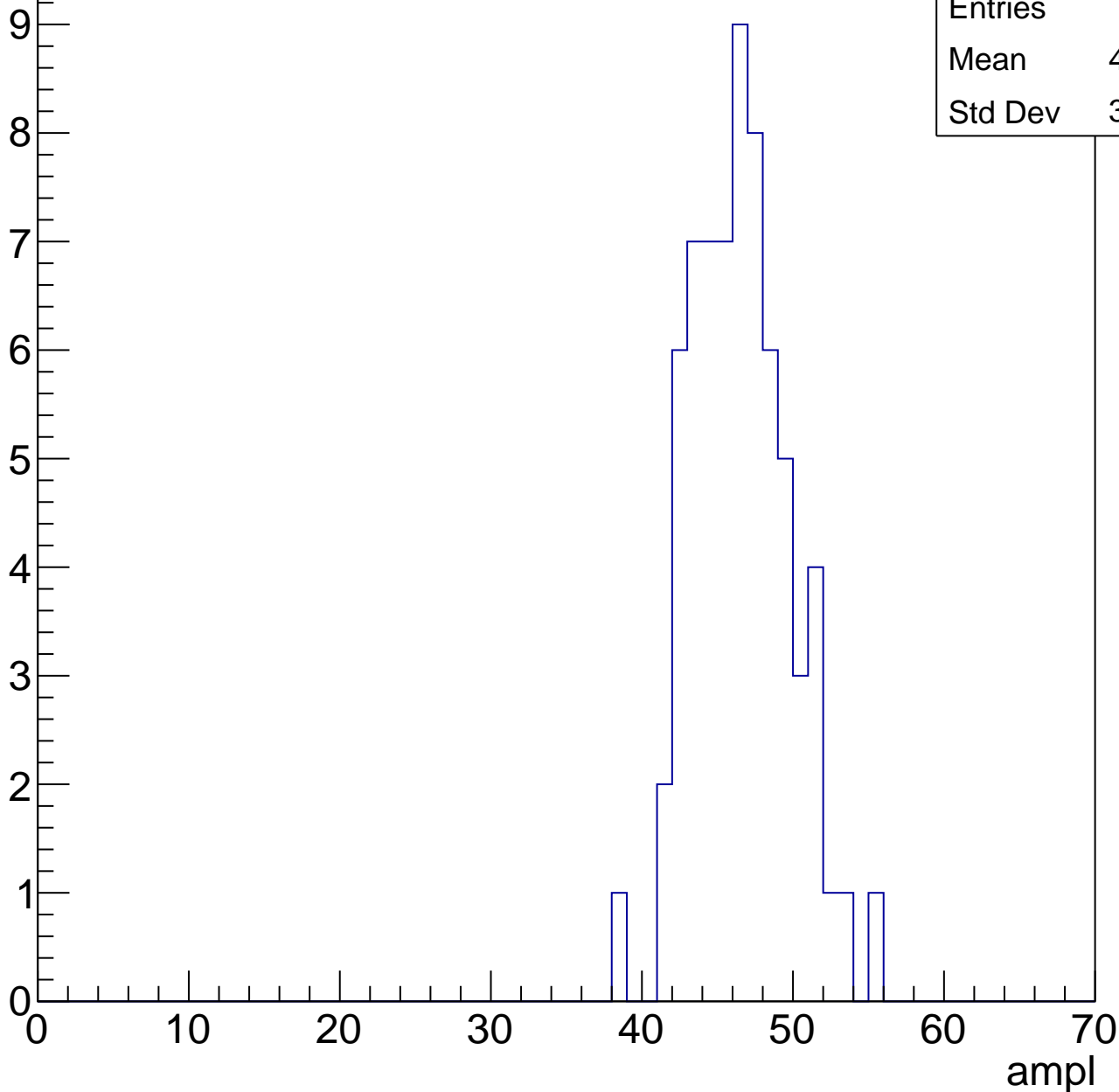


B1L100S, U15-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	46.07
Std Dev	3.205

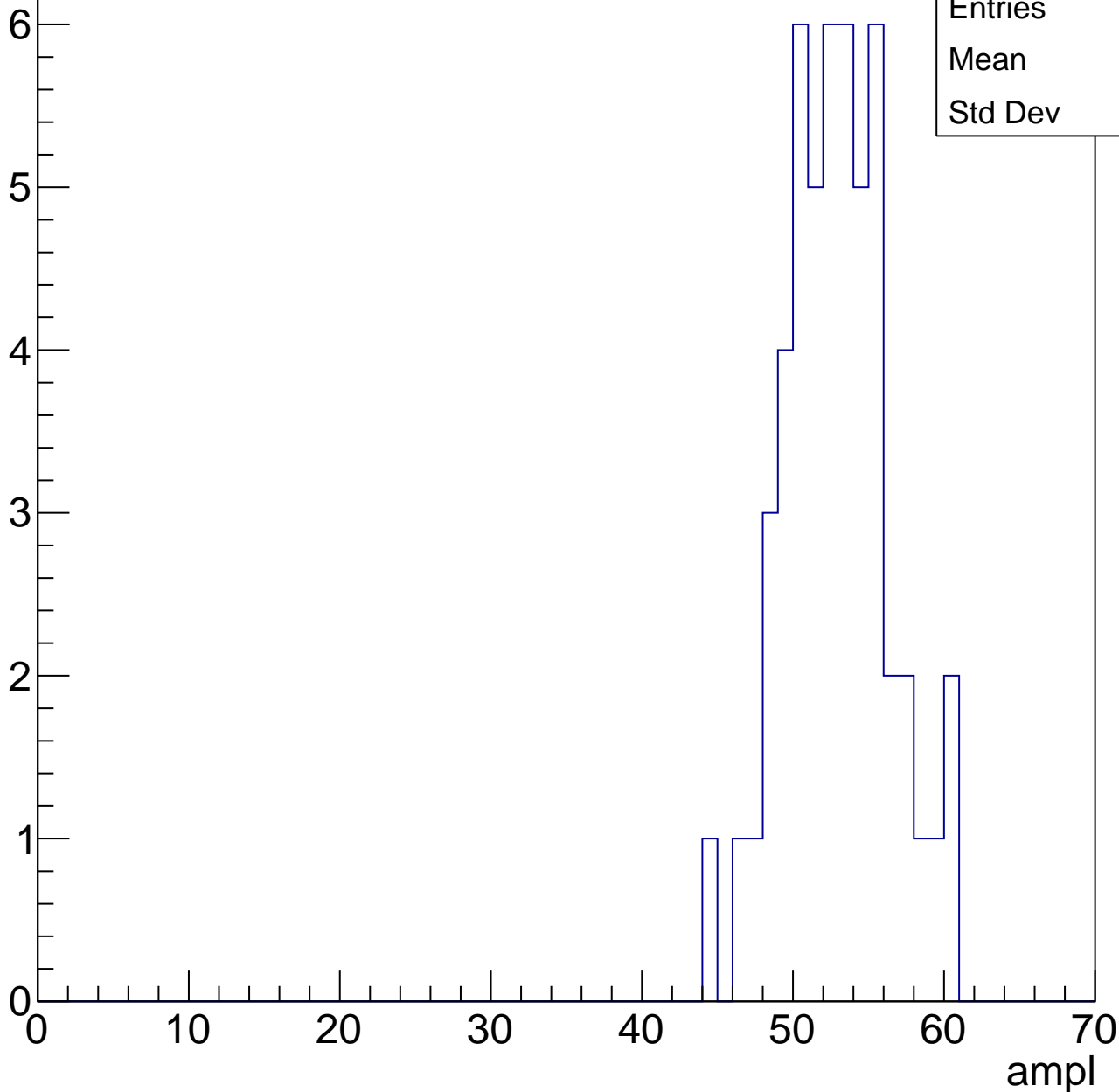


B1L100S, U15-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

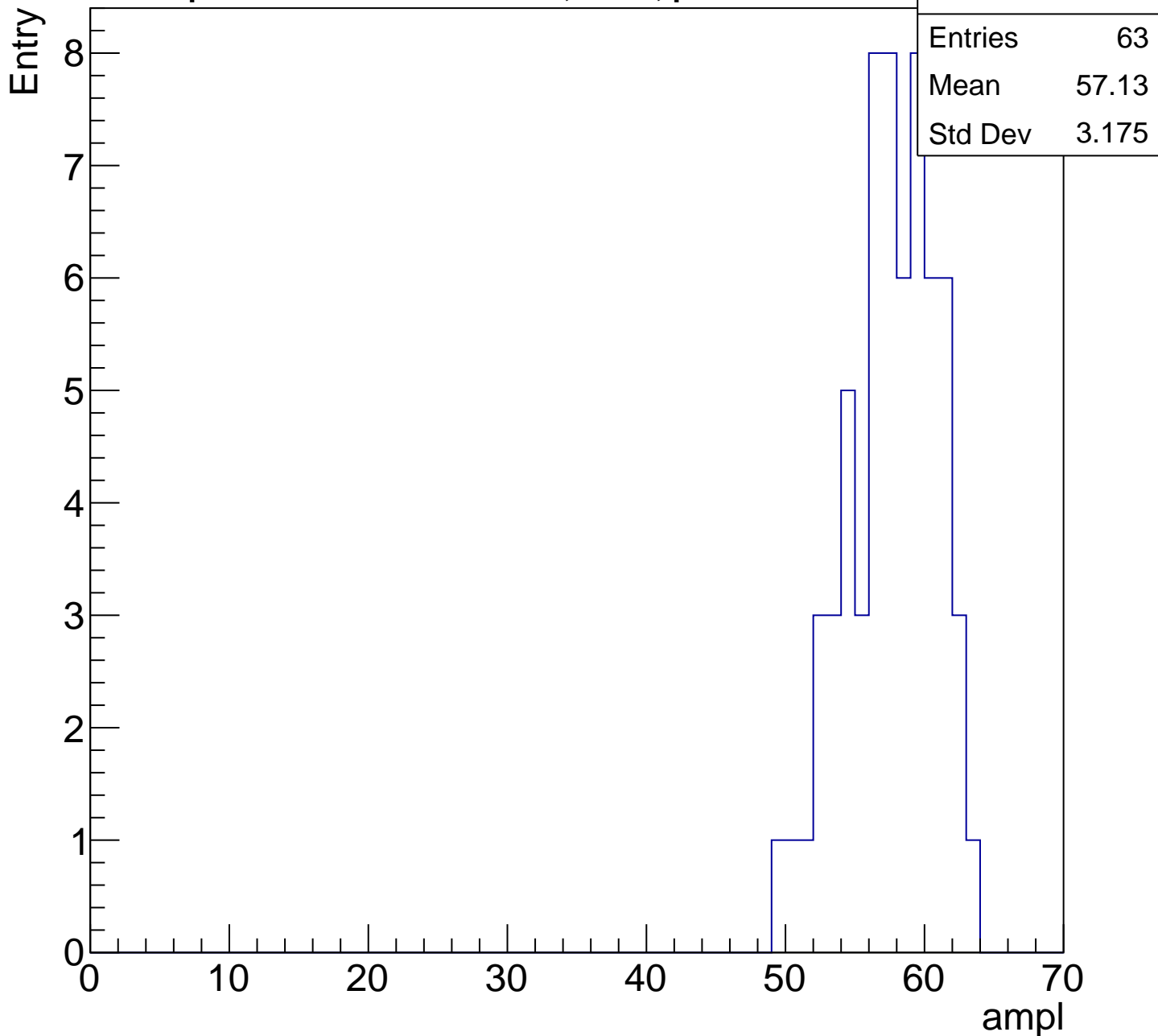
Entry

Entries	52
Mean	52.4
Std Dev	3.41



B1L100S, U15-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

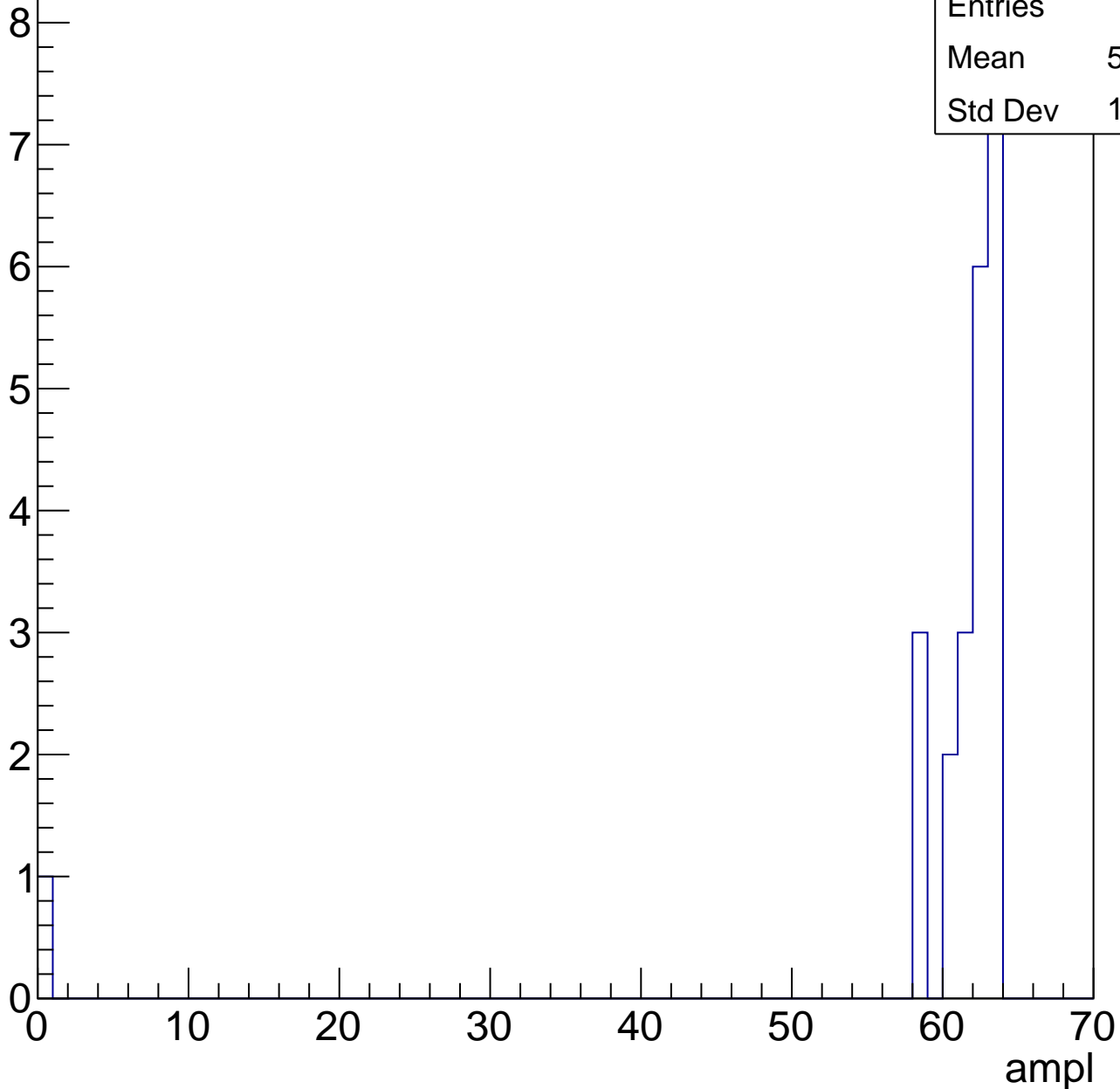


B1L100S, U15-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

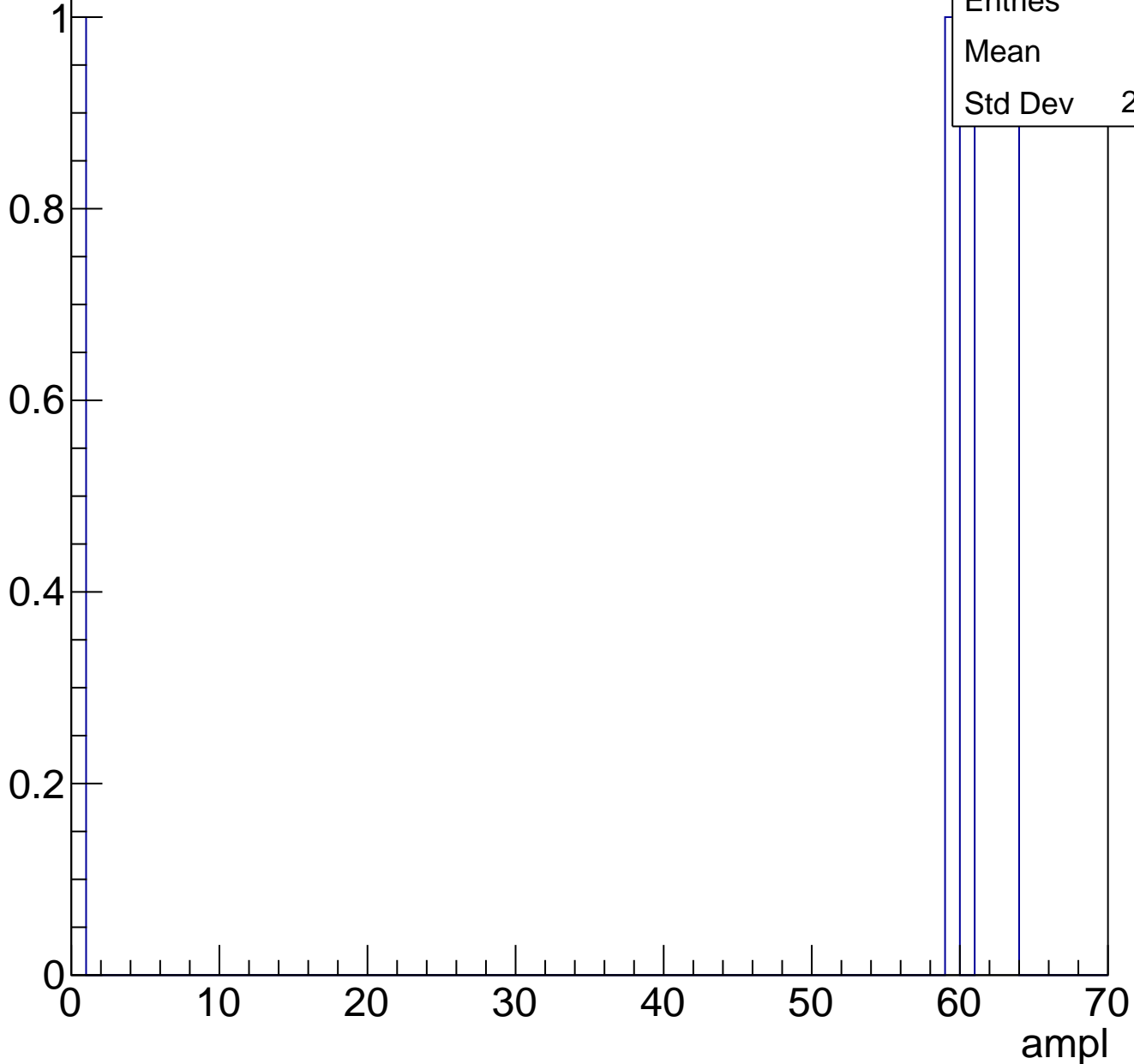
Entries	23
Mean	58.83
Std Dev	12.65



B1L100S, U15-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch7, adc0

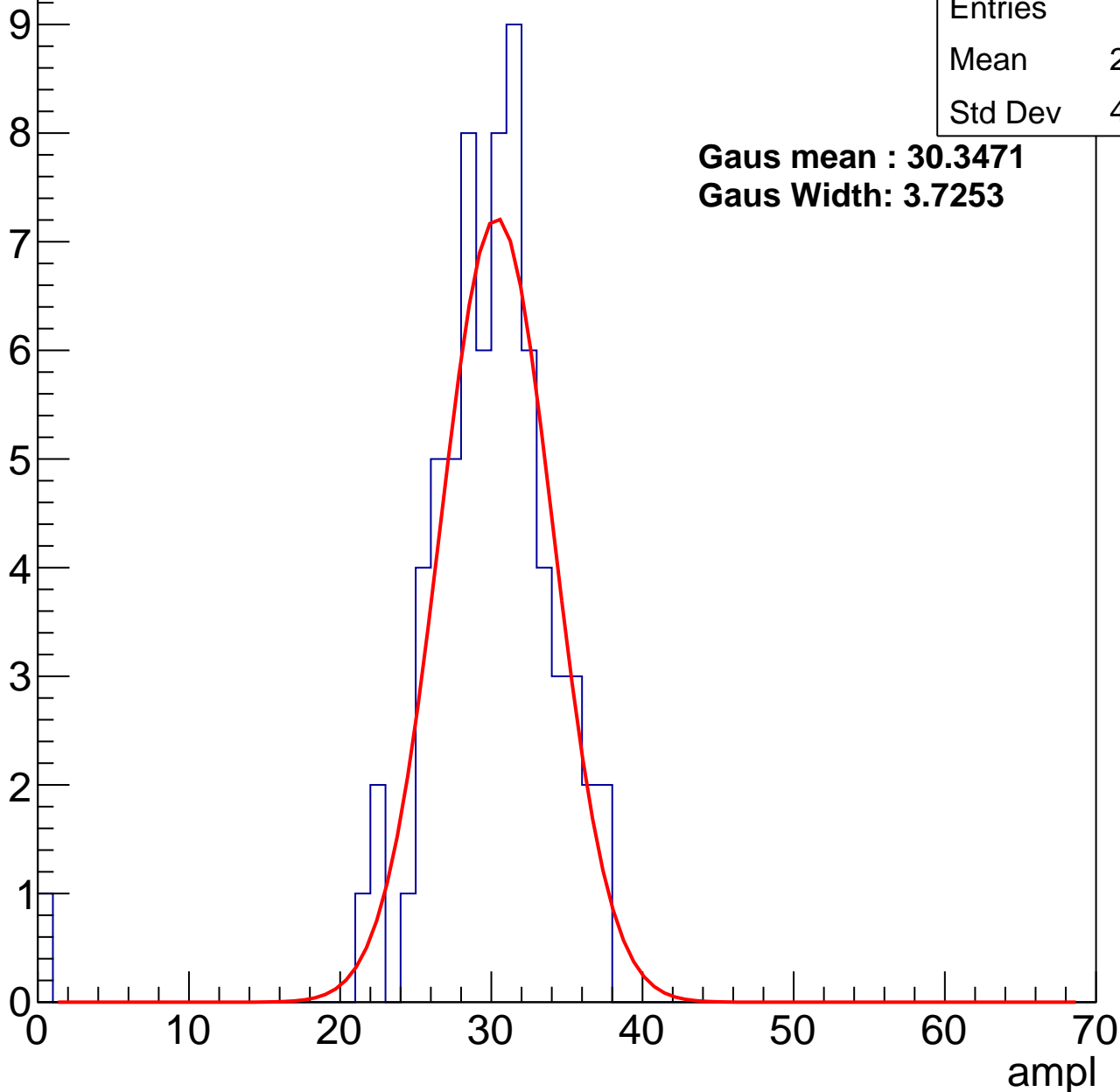
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.26
Std Dev	4.973

Gaus mean : 30.3471

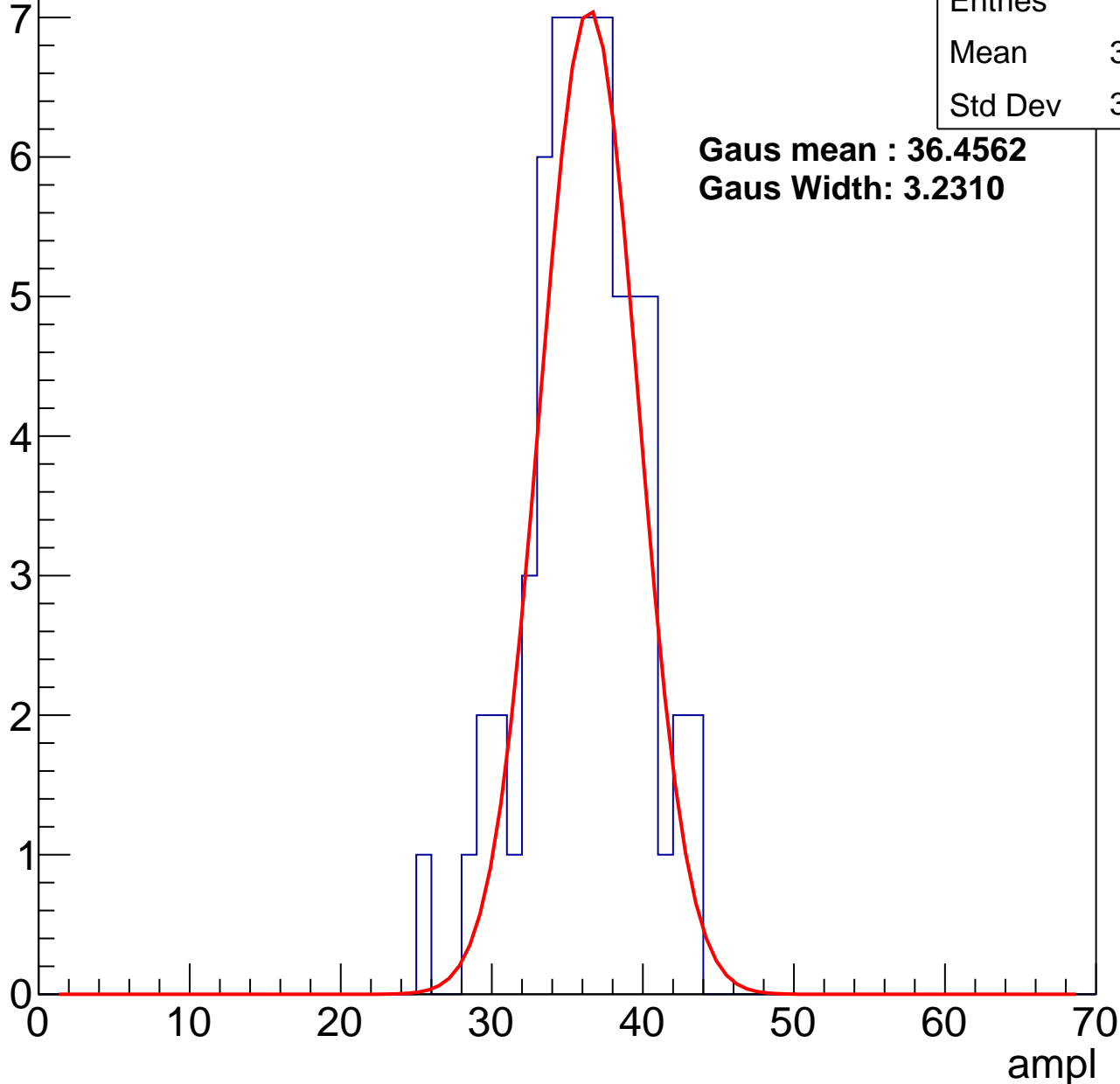
Gaus Width: 3.7253



B1L100S, U15-ch7, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch7, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.59
Std Dev	3.301

Gaus mean : 43.2694

Gaus Width: 3.2174

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

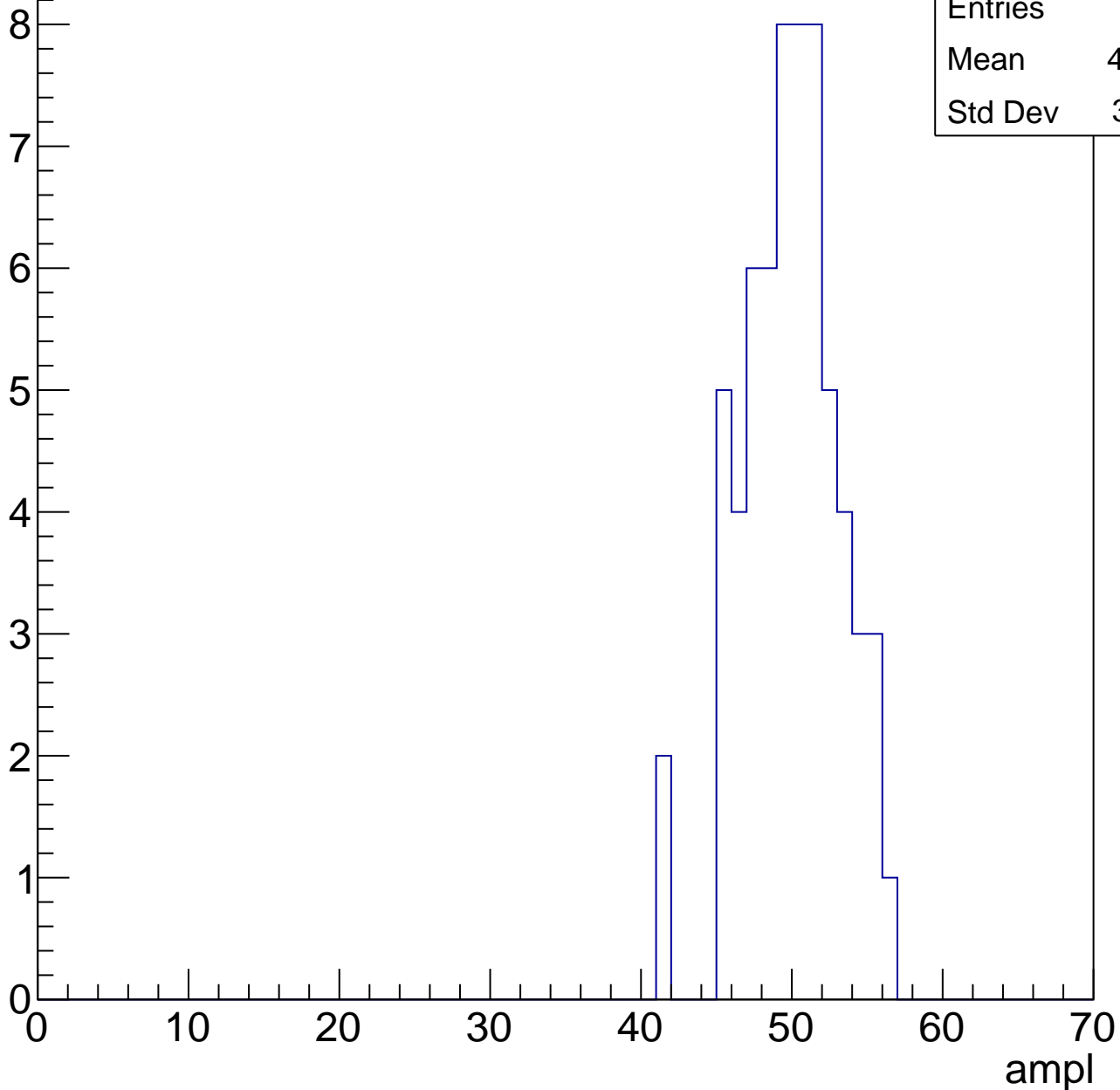
10

B1L100S, U15-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

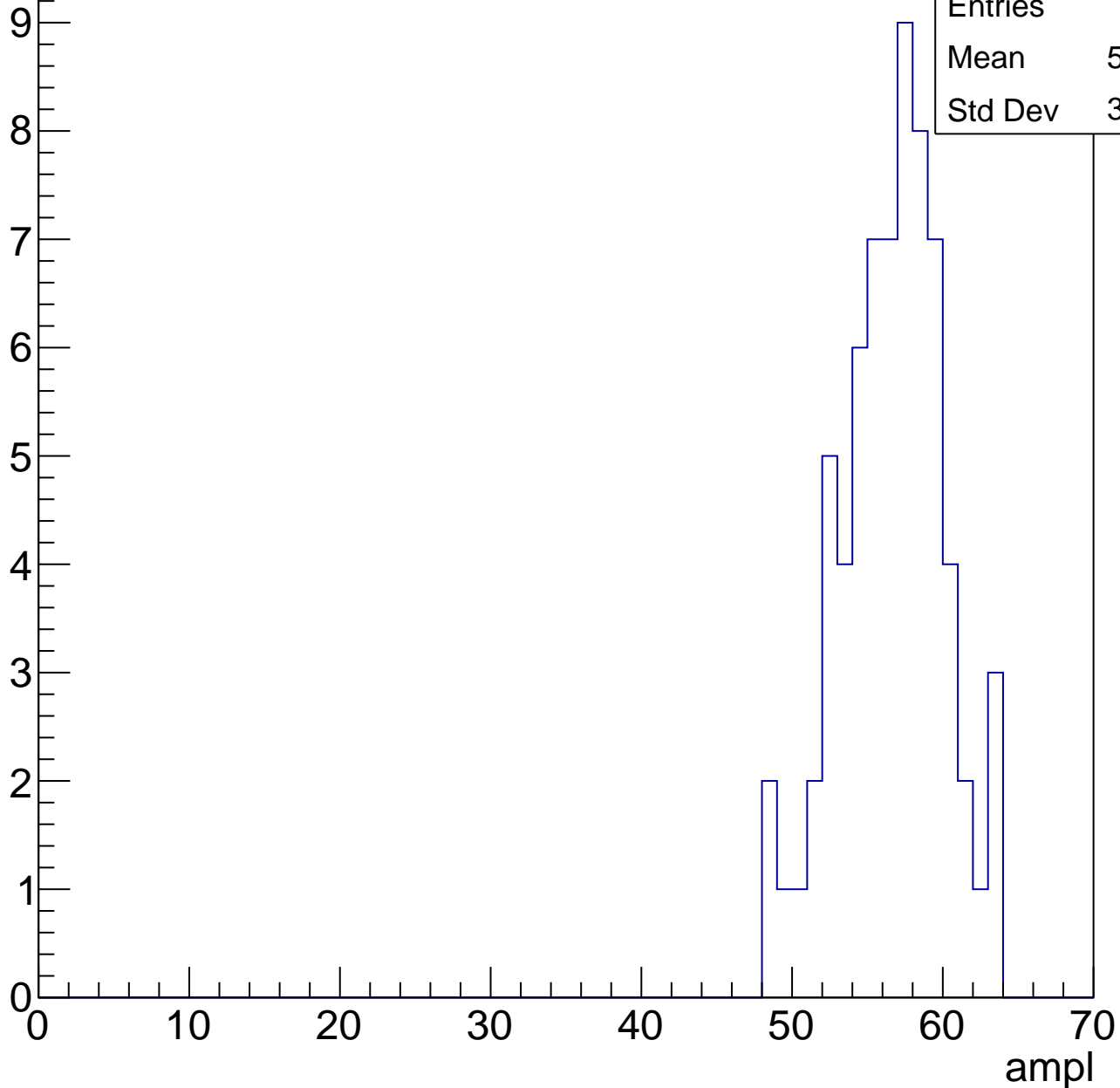
Entries	63
Mean	49.46
Std Dev	3.191



B1L100S, U15-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

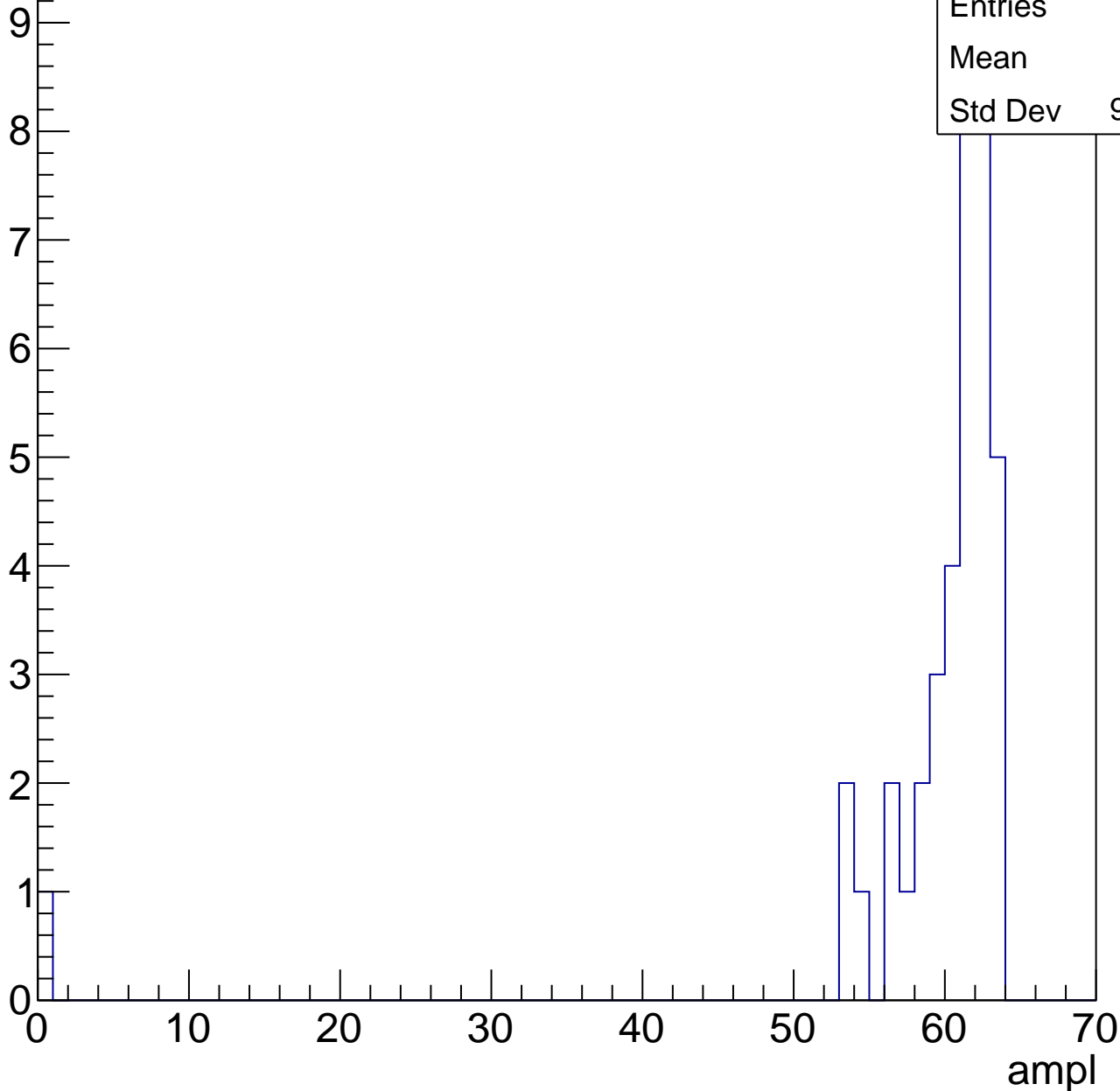


B1L100S, U15-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.5
Std Dev	9.986



B1L100S, U15-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.5
Std Dev	1.5

0 10 20 30 40 50 60 70

ampl

B1L100S, U15-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch8, adc0

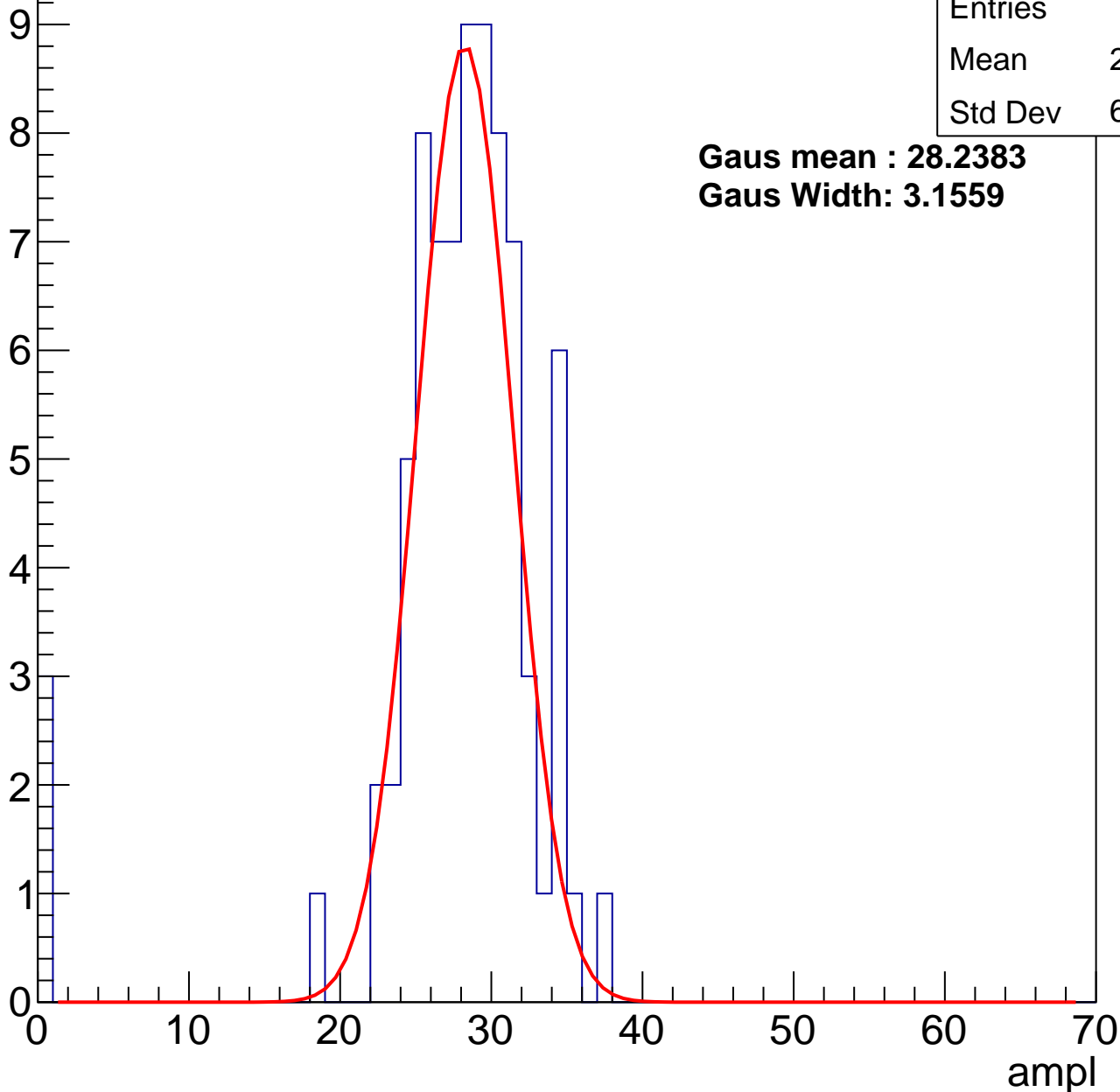
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	27.18
Std Dev	6.354

Gaus mean : 28.2383

Gaus Width: 3.1559



B1L100S, U15-ch8, adc1

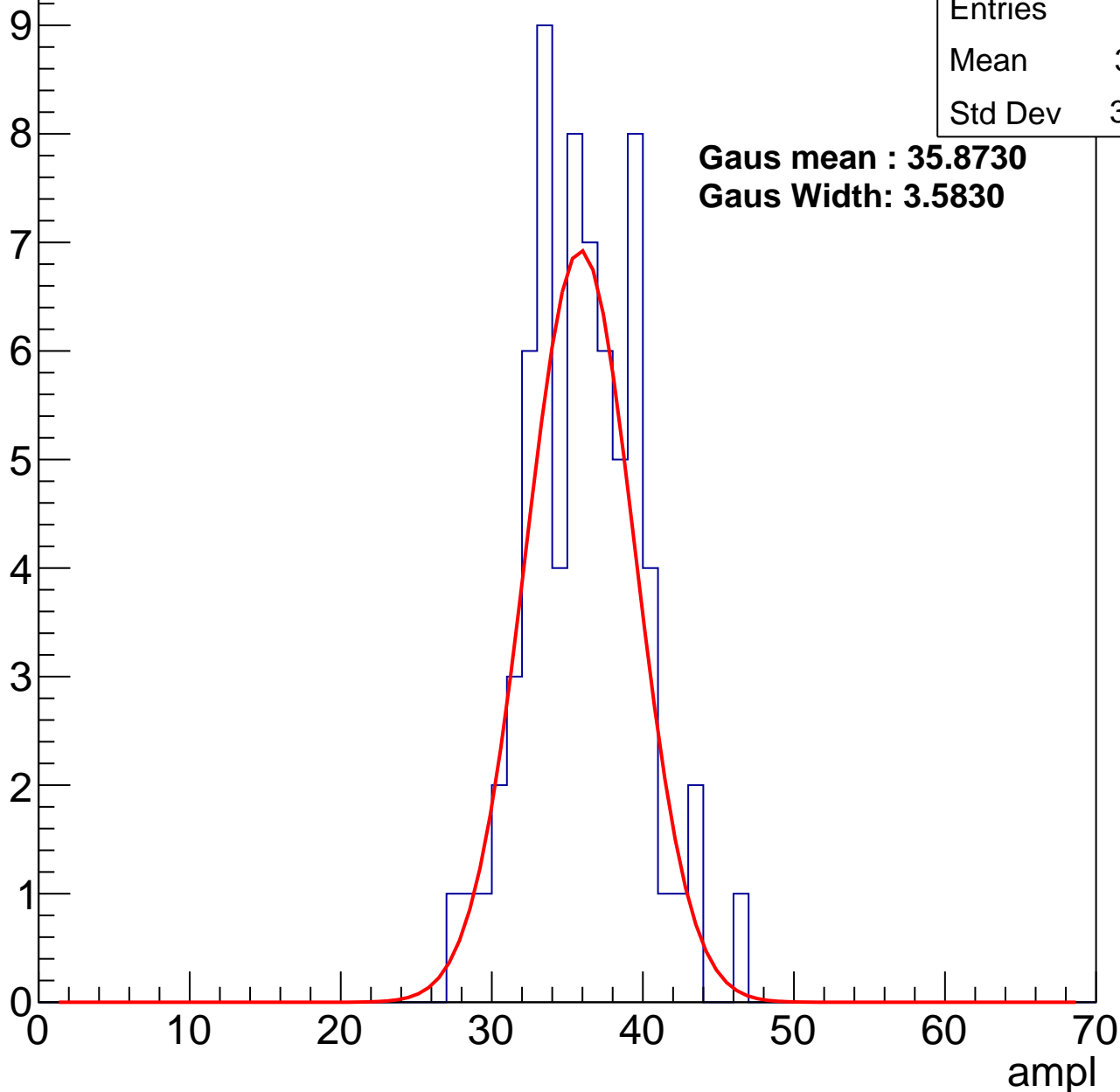
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.61
Std Dev	3.689

Gaus mean : 35.8730

Gaus Width: 3.5830



B1L100S, U15-ch8, adc2

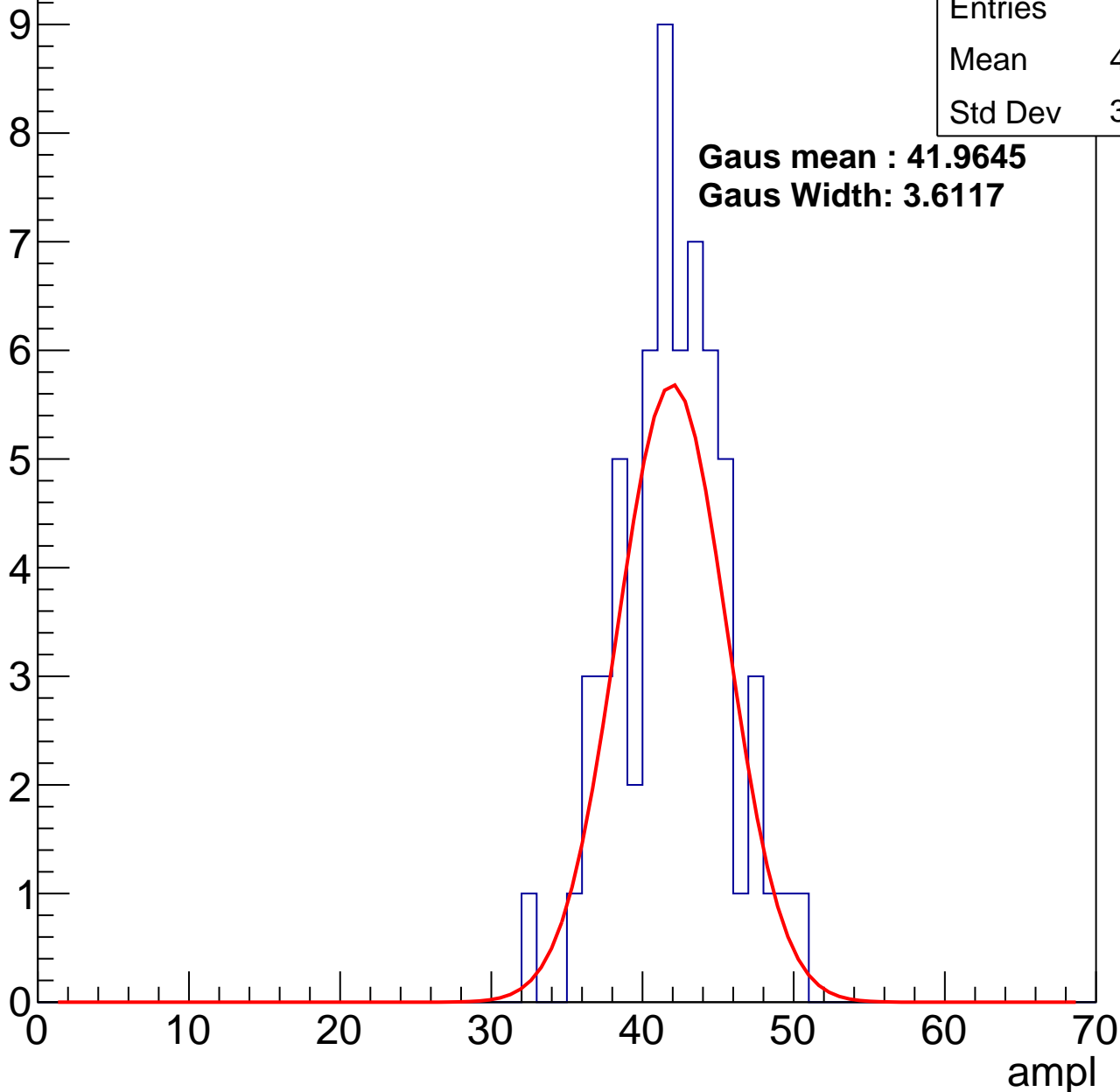
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	41.62
Std Dev	3.554

Gaus mean : 41.9645

Gaus Width: 3.6117

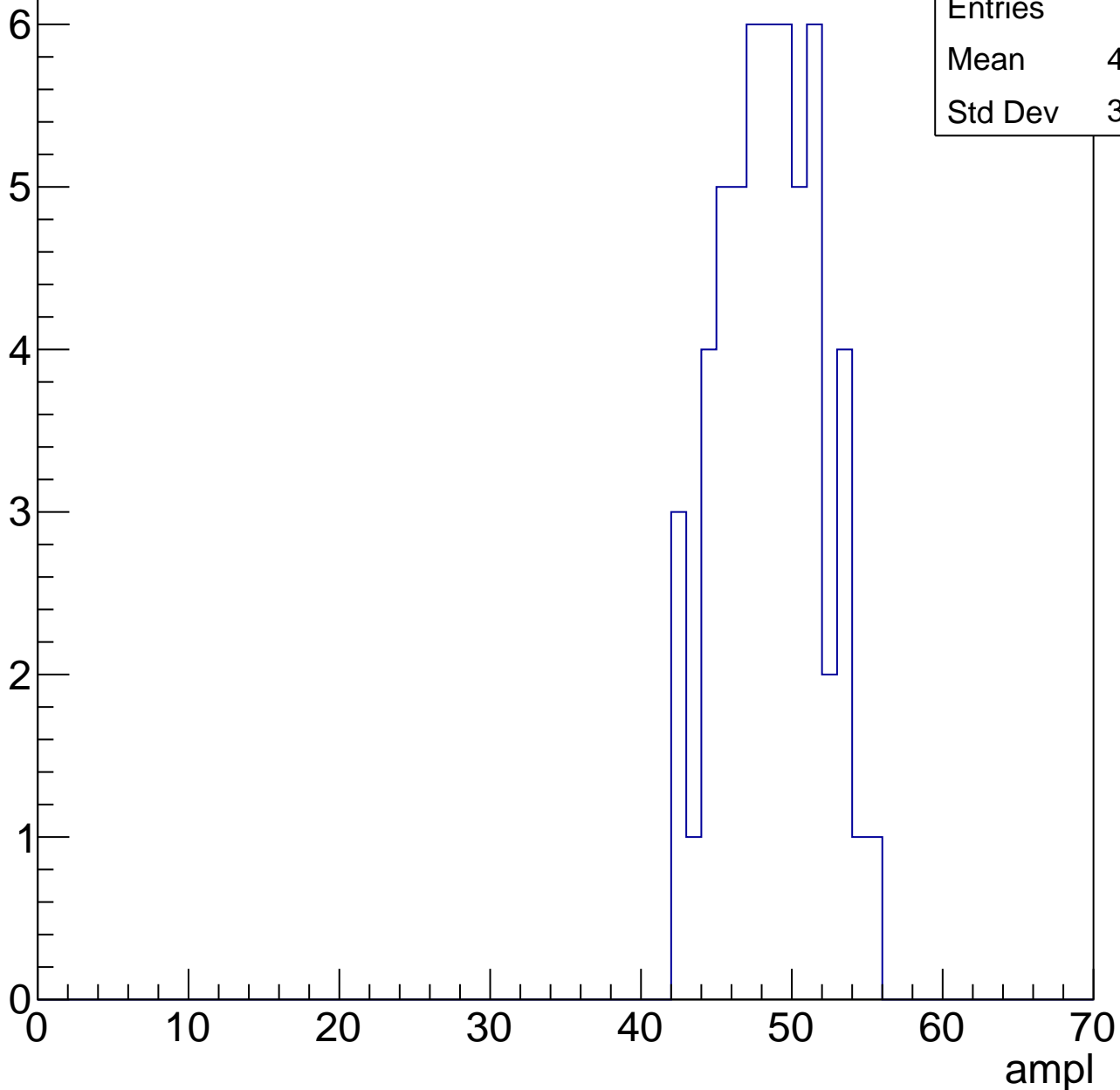


B1L100S, U15-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

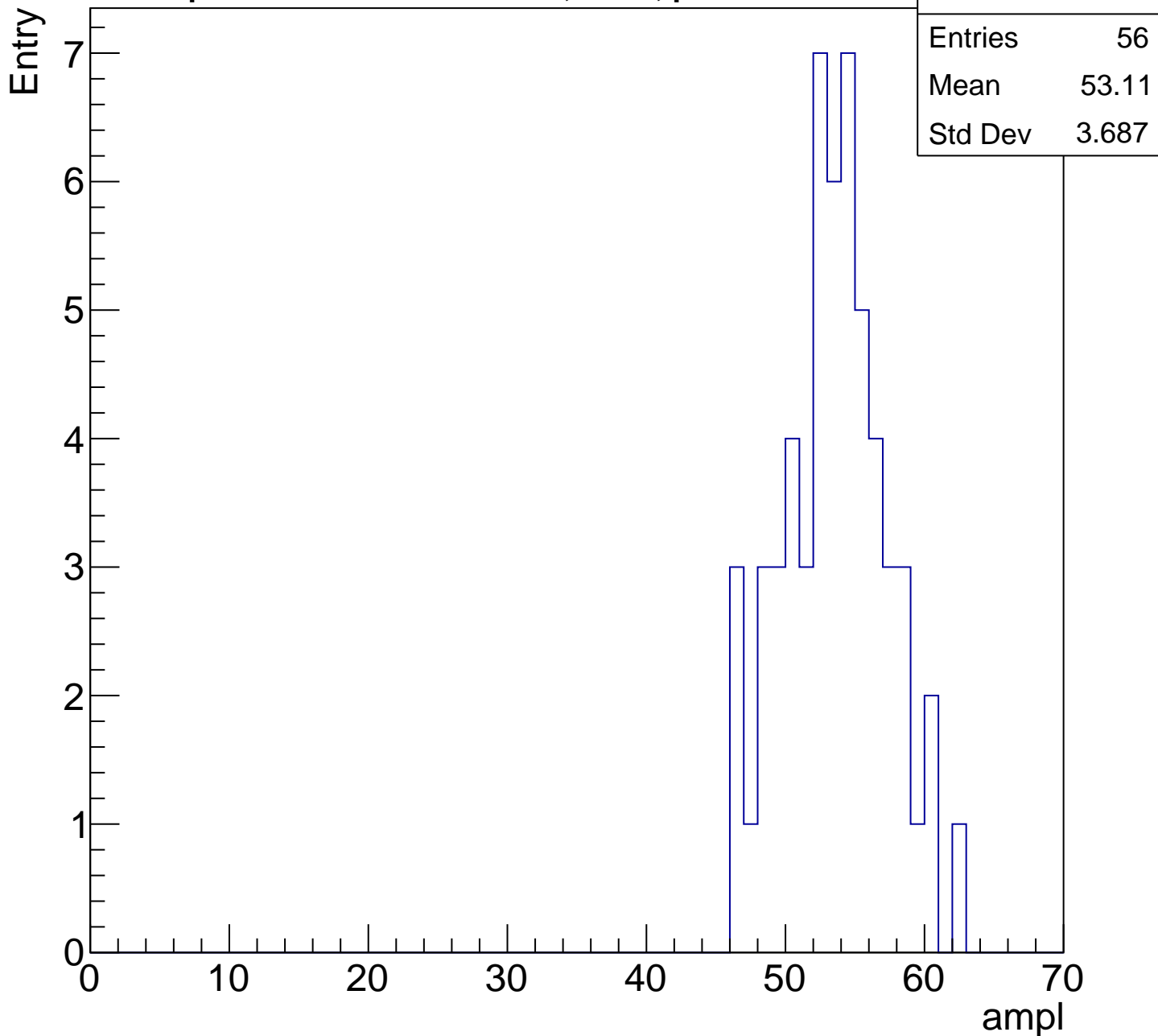
Entry

Entries	55
Mean	48.09
Std Dev	3.204



B1L100S, U15-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

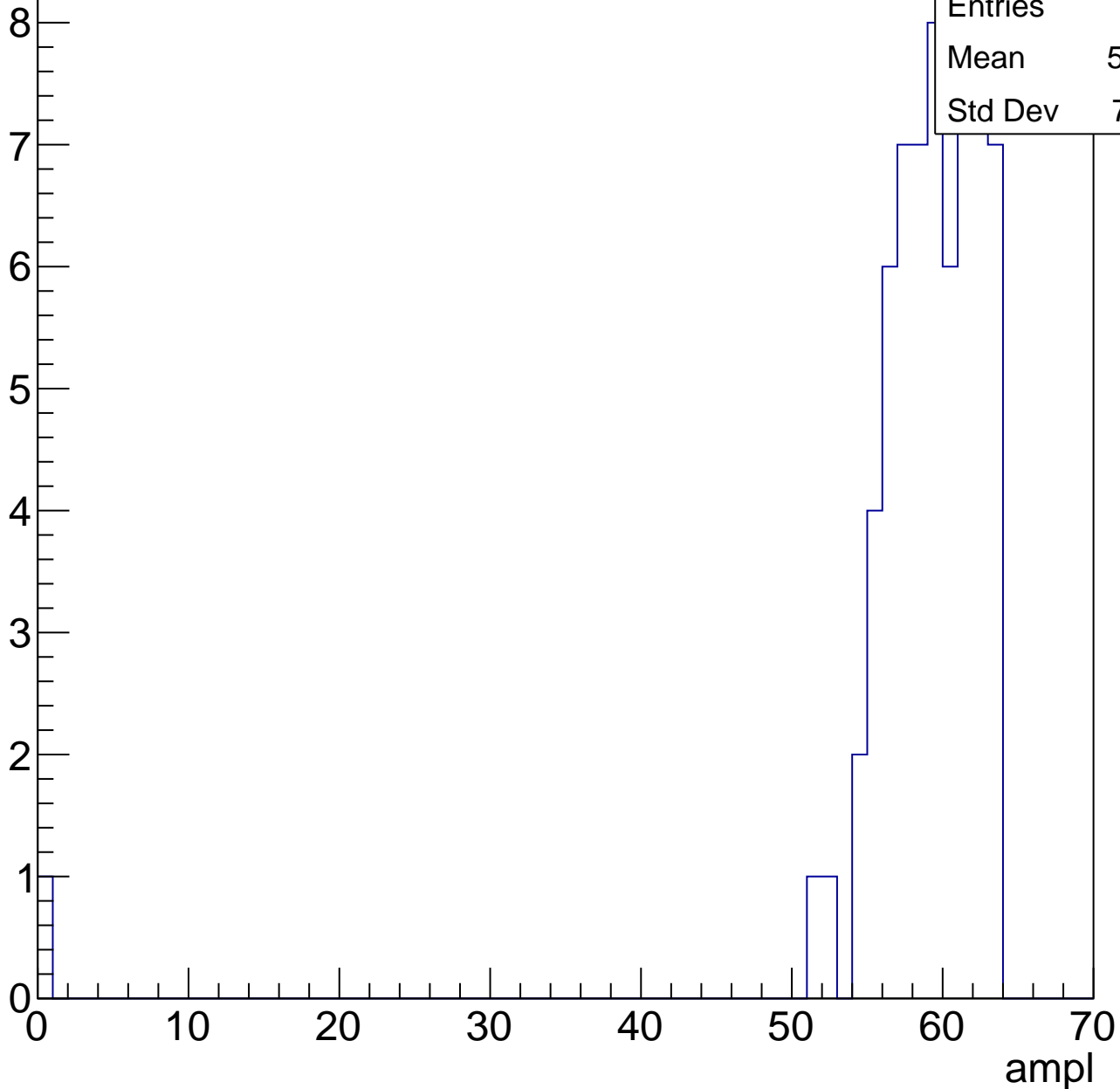


B1L100S, U15-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	58.02
Std Dev	7.741



B1L100S, U15-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.12
Std Dev	1.364

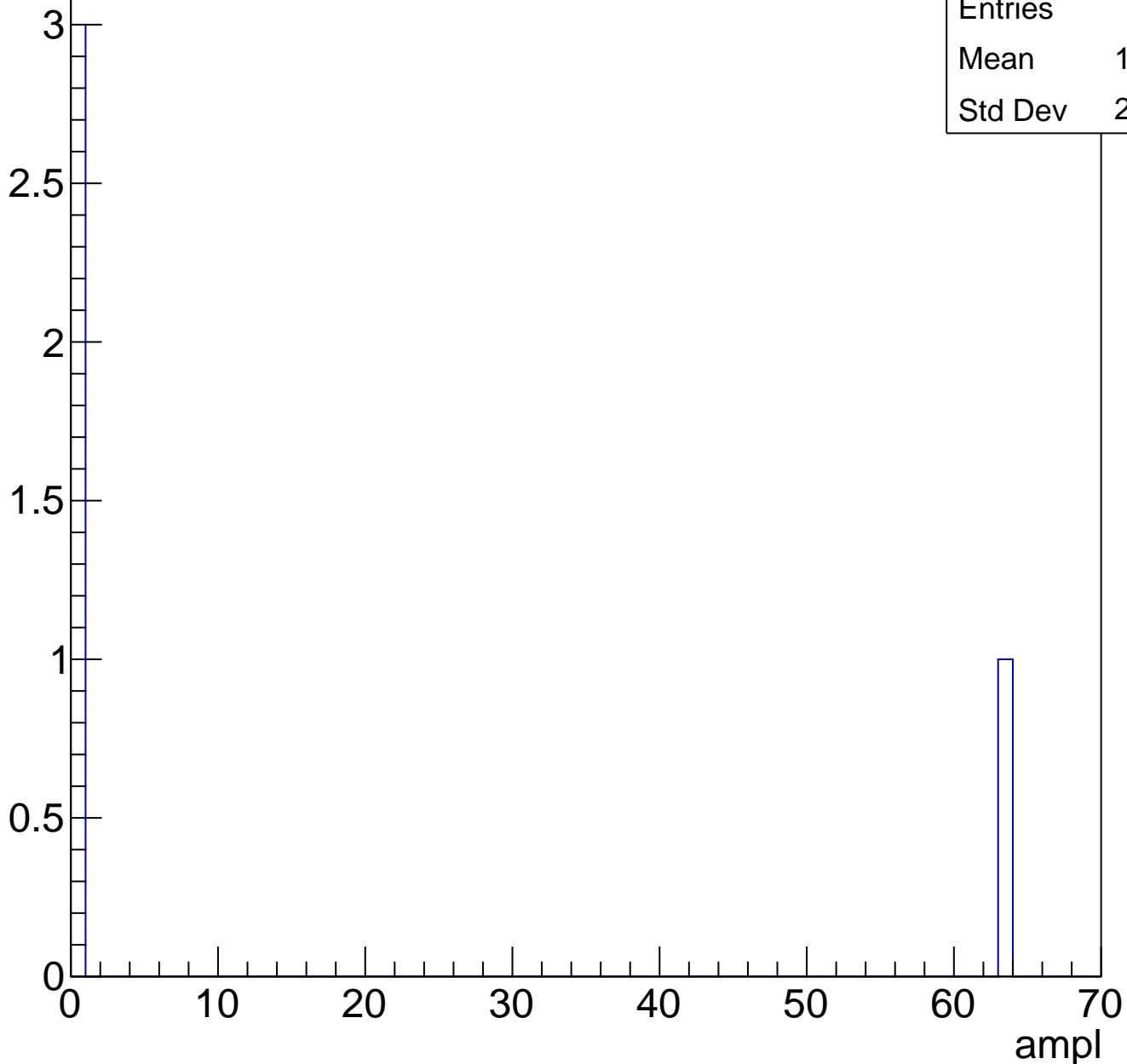
ampl

0 10 20 30 40 50 60 70

B1L100S, U15-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	15.75
Std Dev	27.28

B1L100S, U15-ch9, adc0

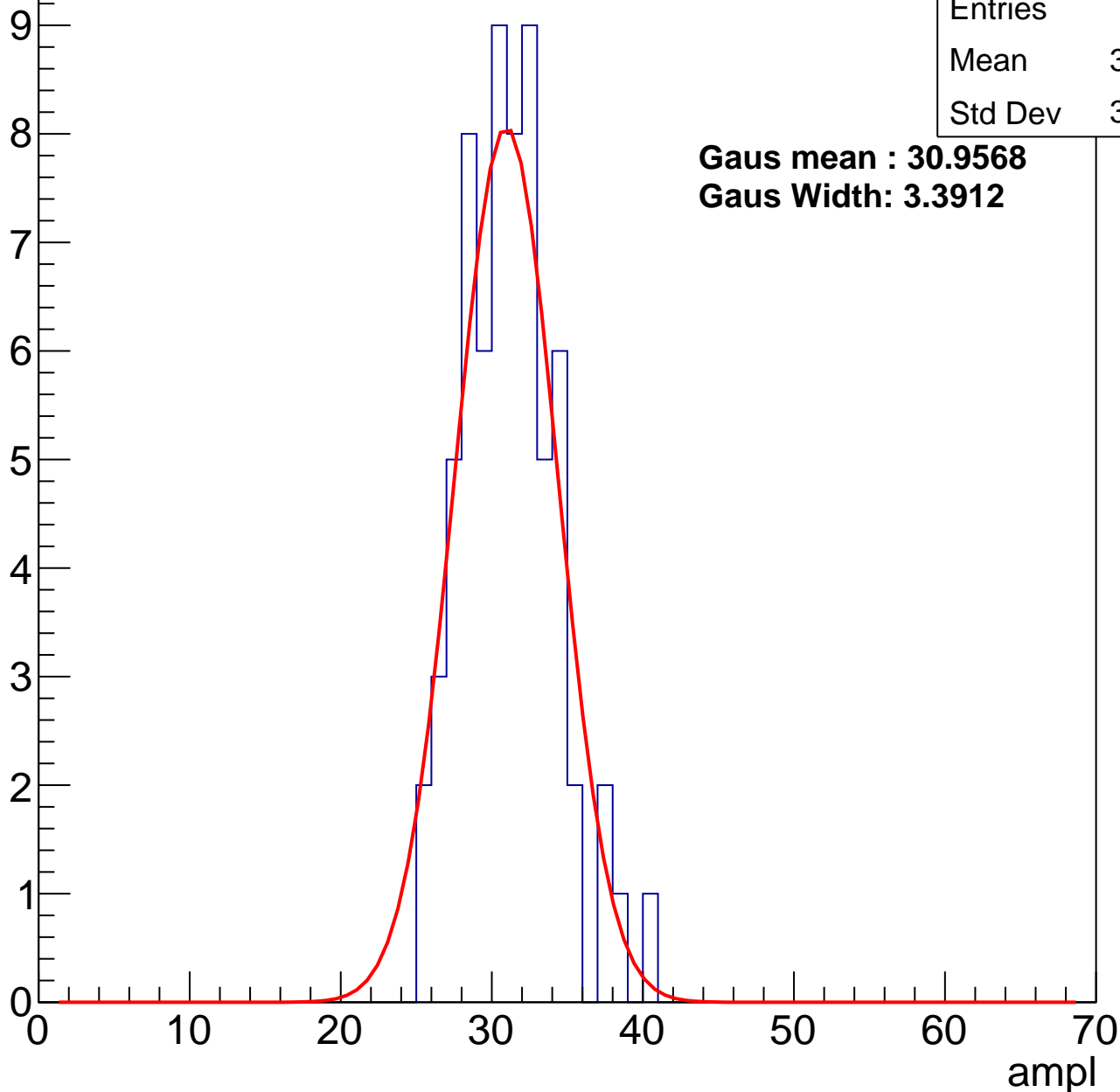
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.72
Std Dev	3.099

Gaus mean : 30.9568

Gaus Width: 3.3912



B1L100S, U15-ch9, adc1

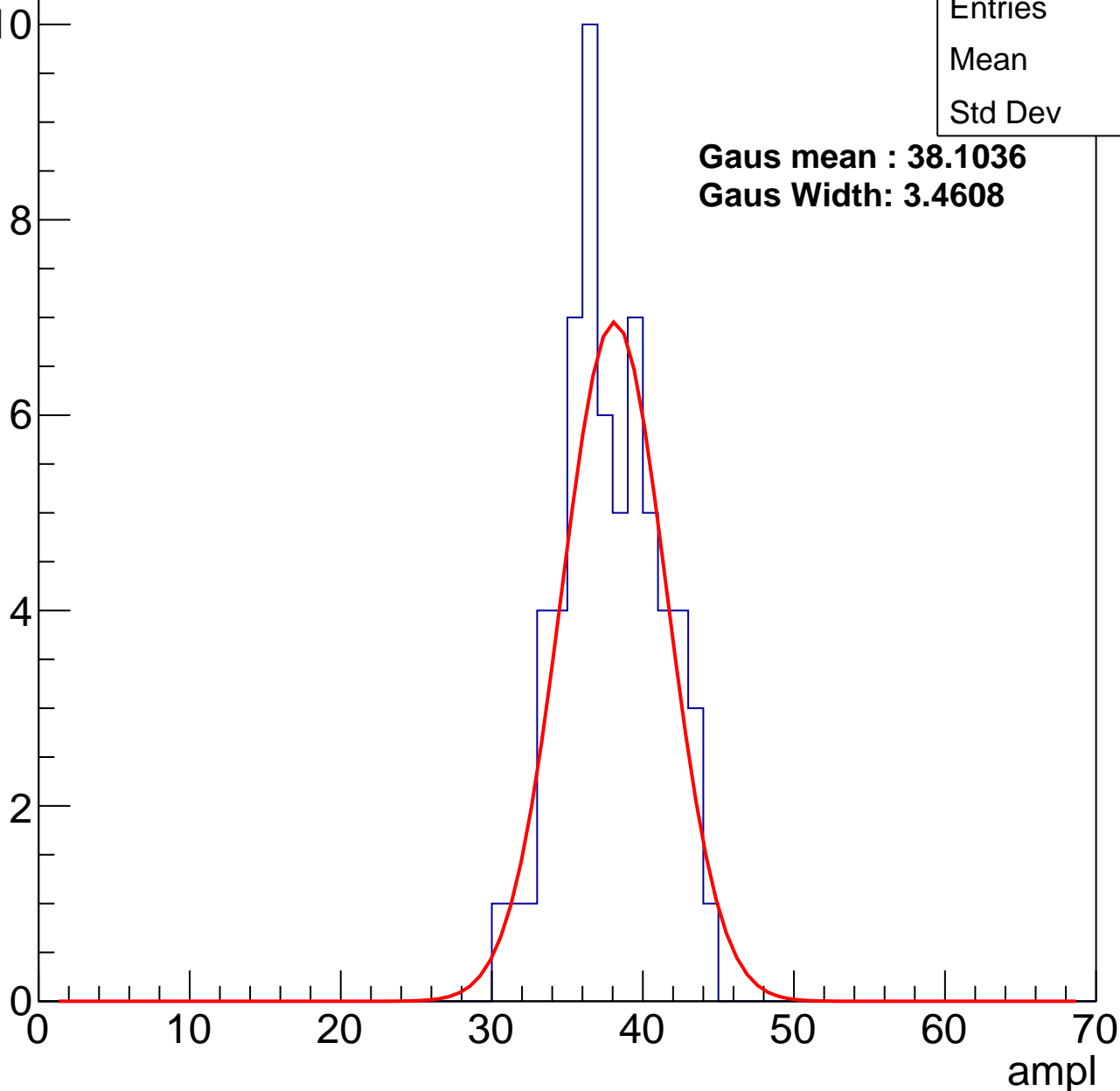
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.4
Std Dev	3.18

Gaus mean : 38.1036

Gaus Width: 3.4608

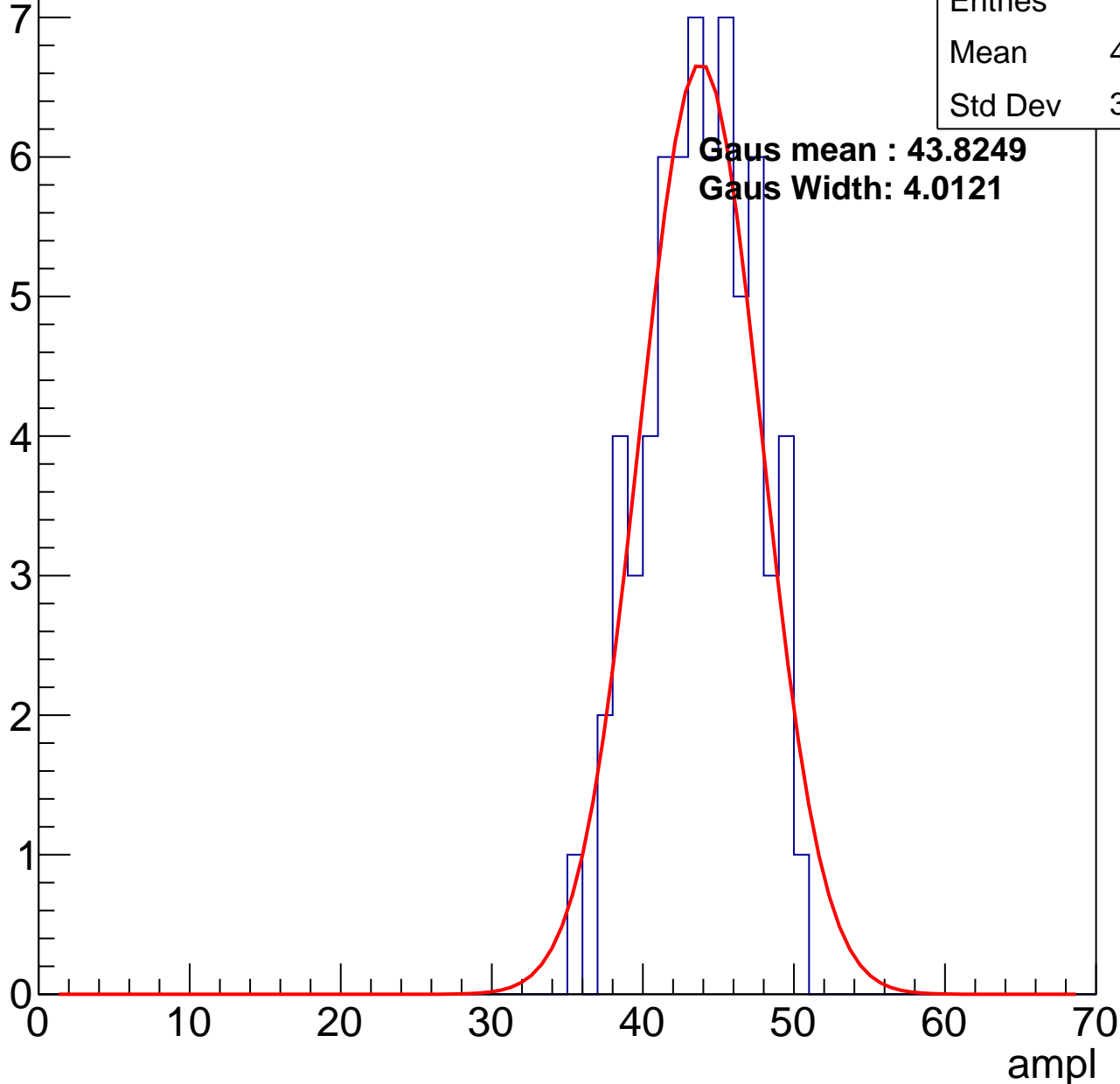


B1L100S, U15-ch9, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.35
Std Dev	3.475

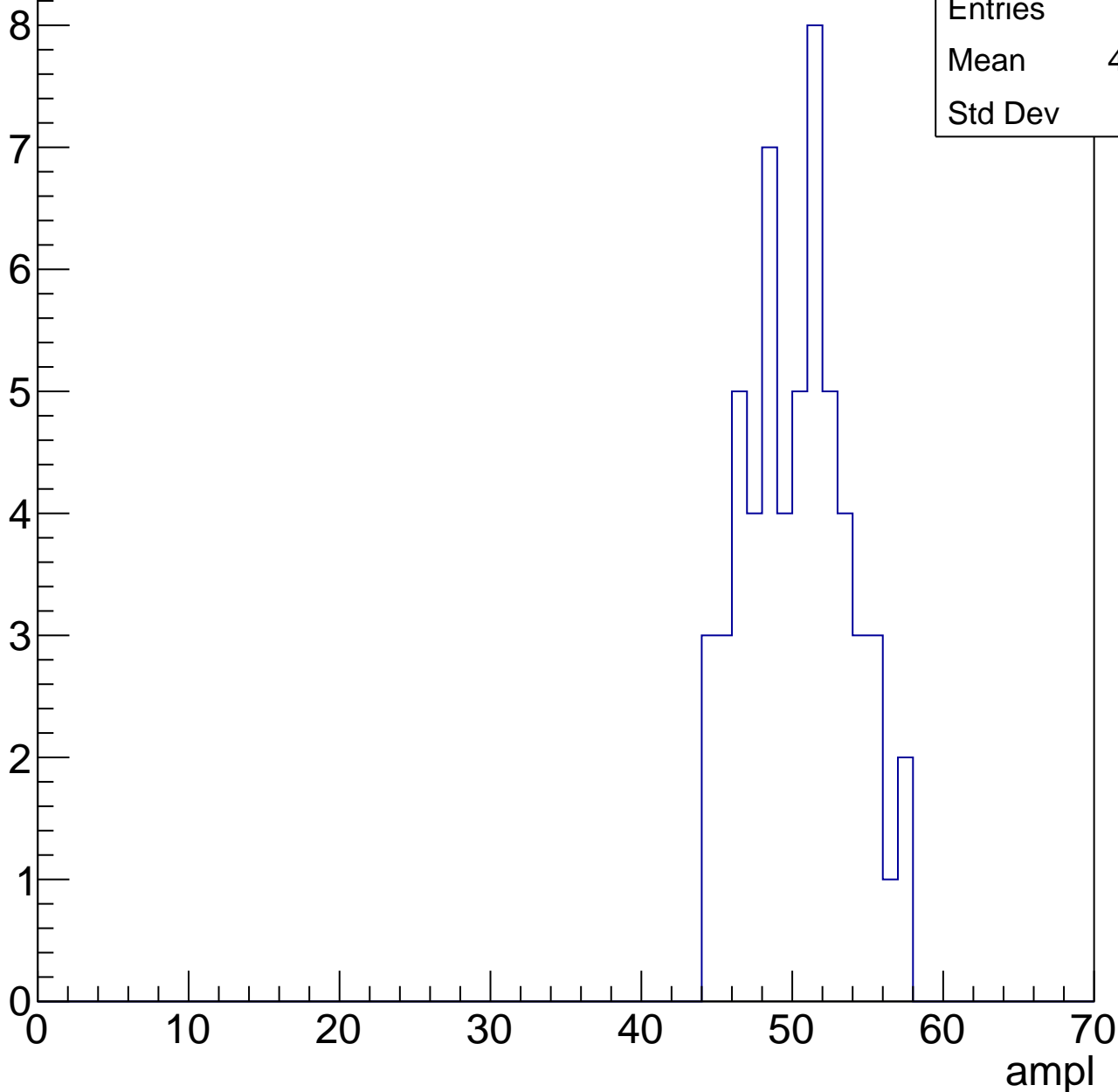


B1L100S, U15-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	49.89
Std Dev	3.38

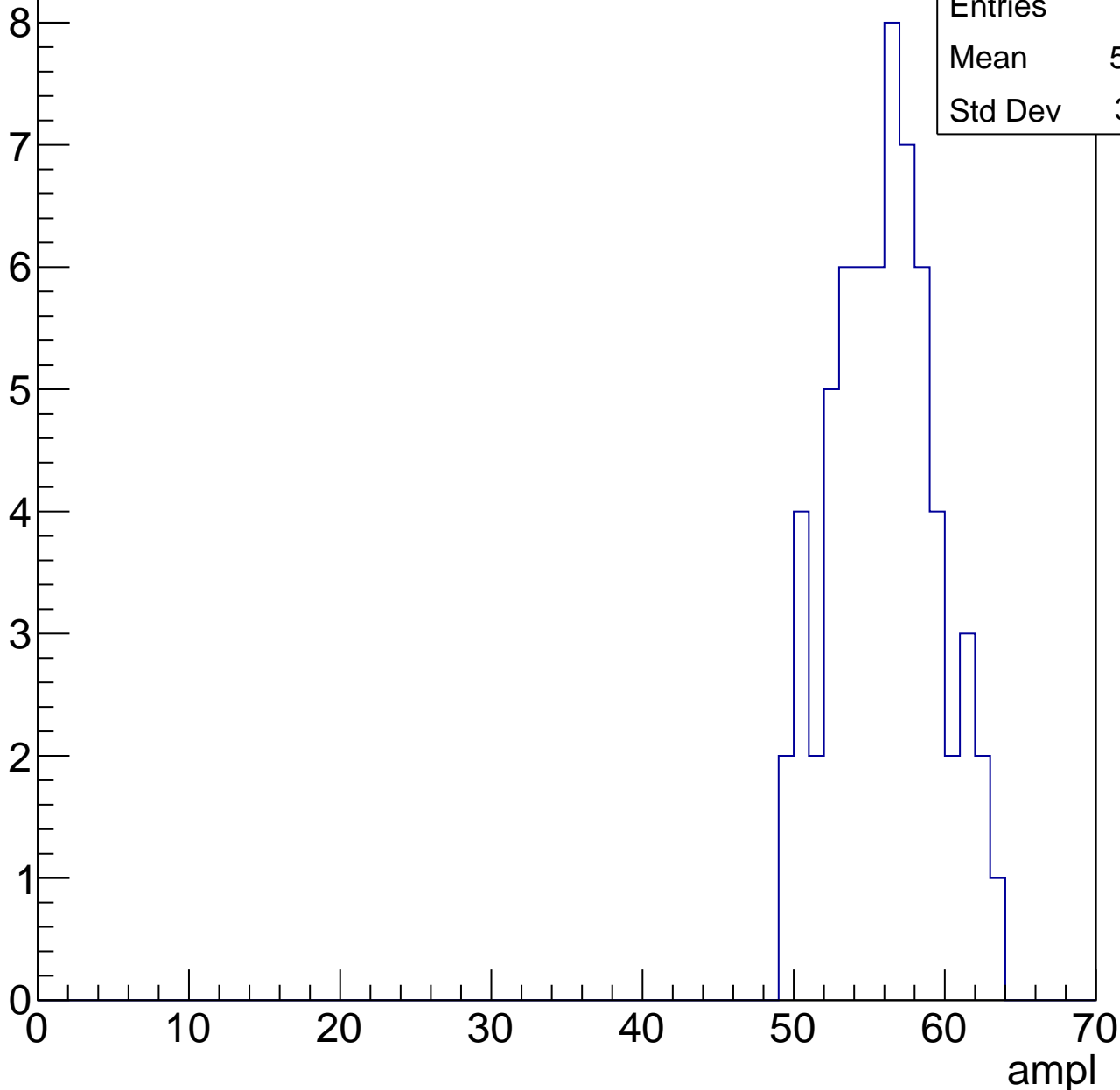


B1L100S, U15-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	55.52
Std Dev	3.391

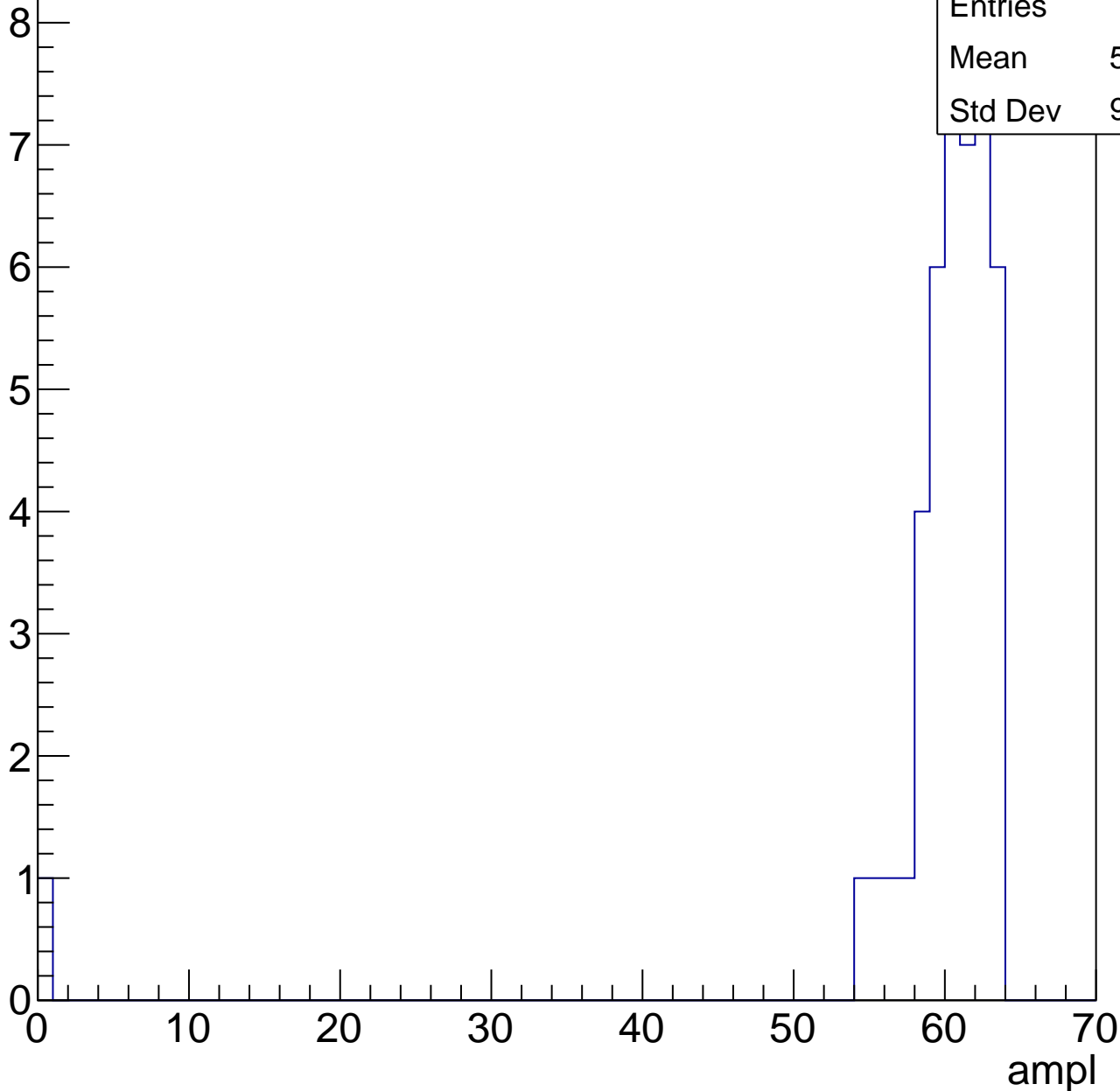


B1L100S, U15-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

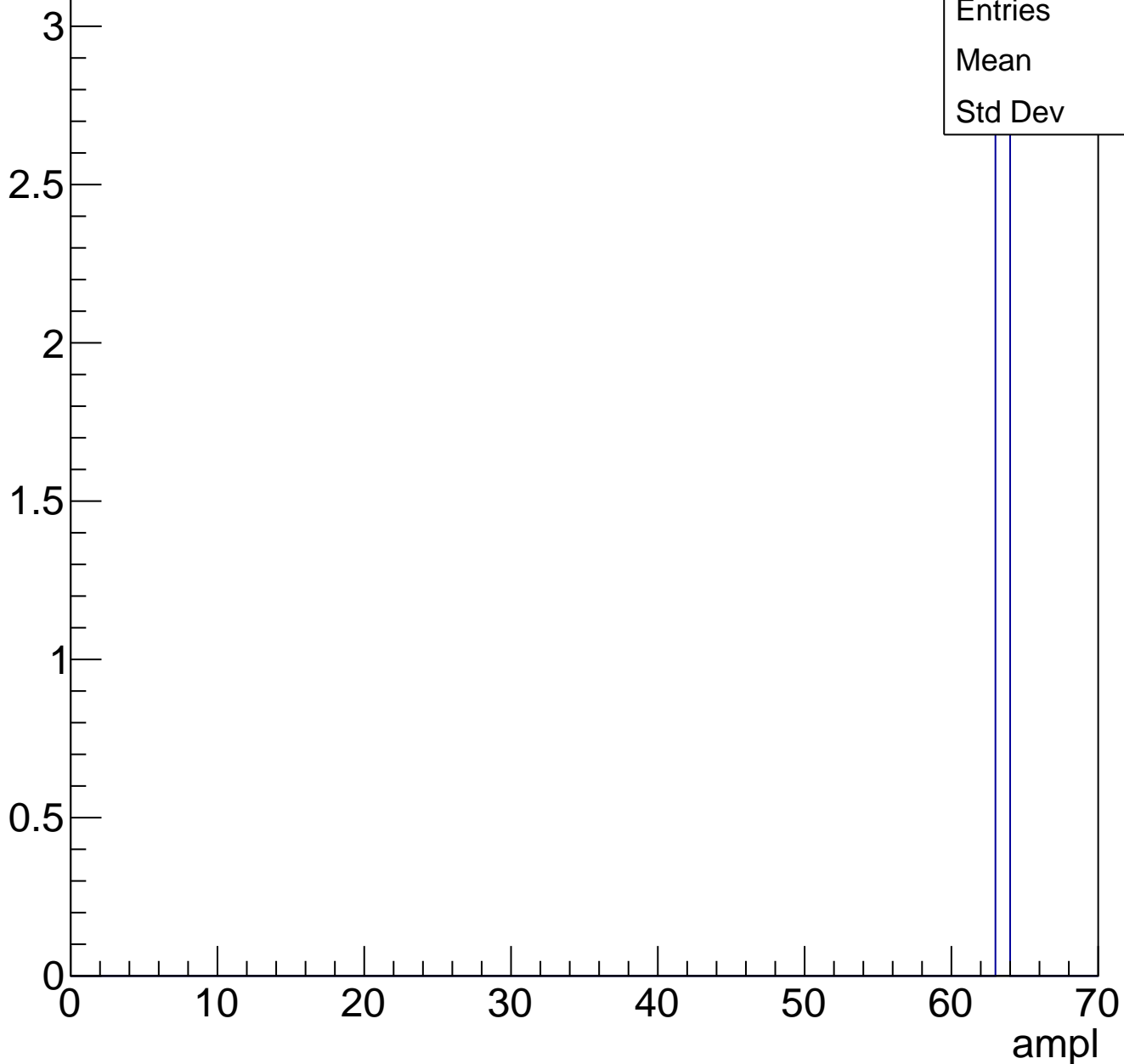
Entries	44
Mean	58.84
Std Dev	9.222



B1L100S, U15-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L100S, U15-ch10, adc0

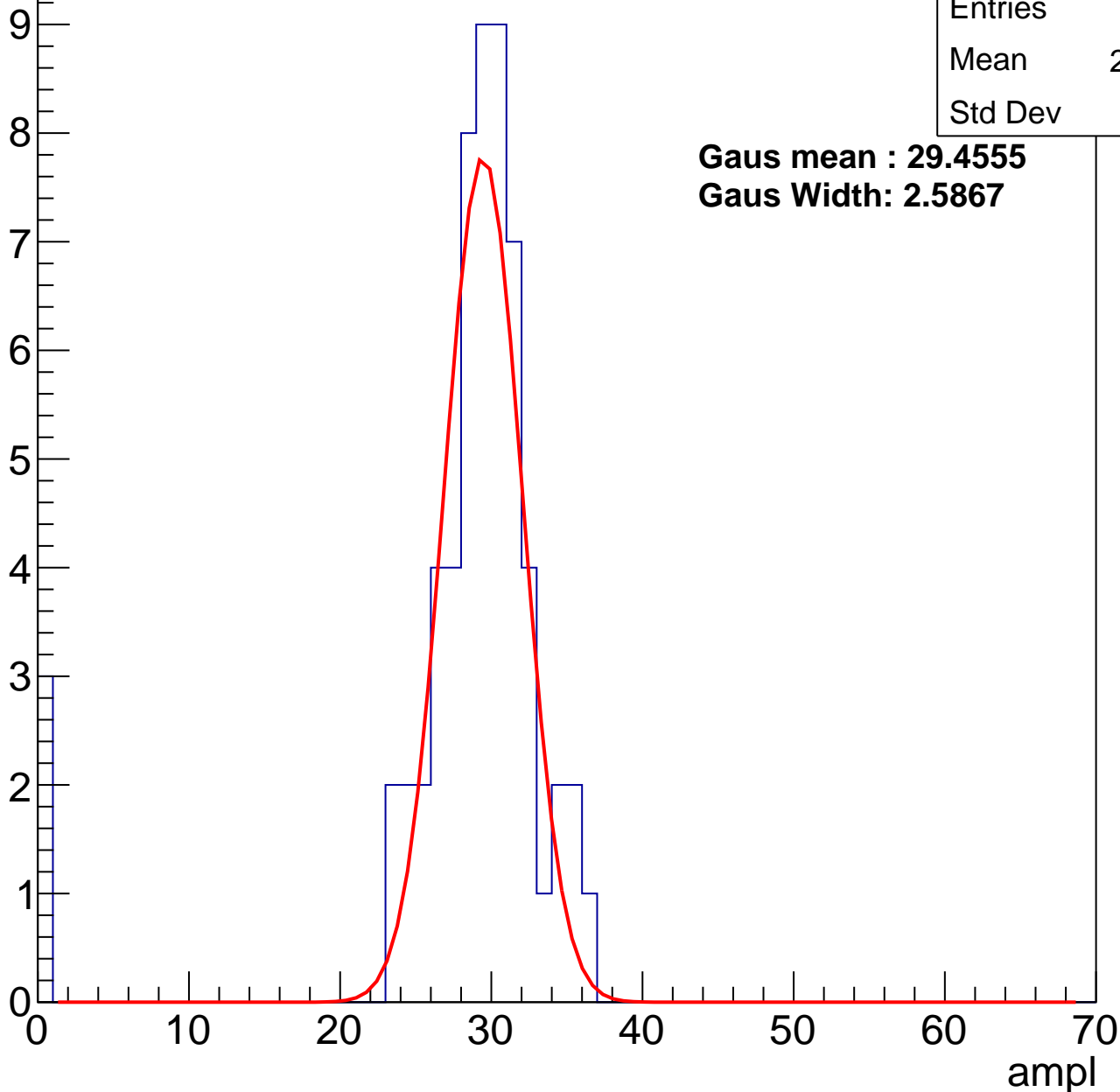
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	27.72
Std Dev	6.95

Gaus mean : 29.4555

Gaus Width: 2.5867



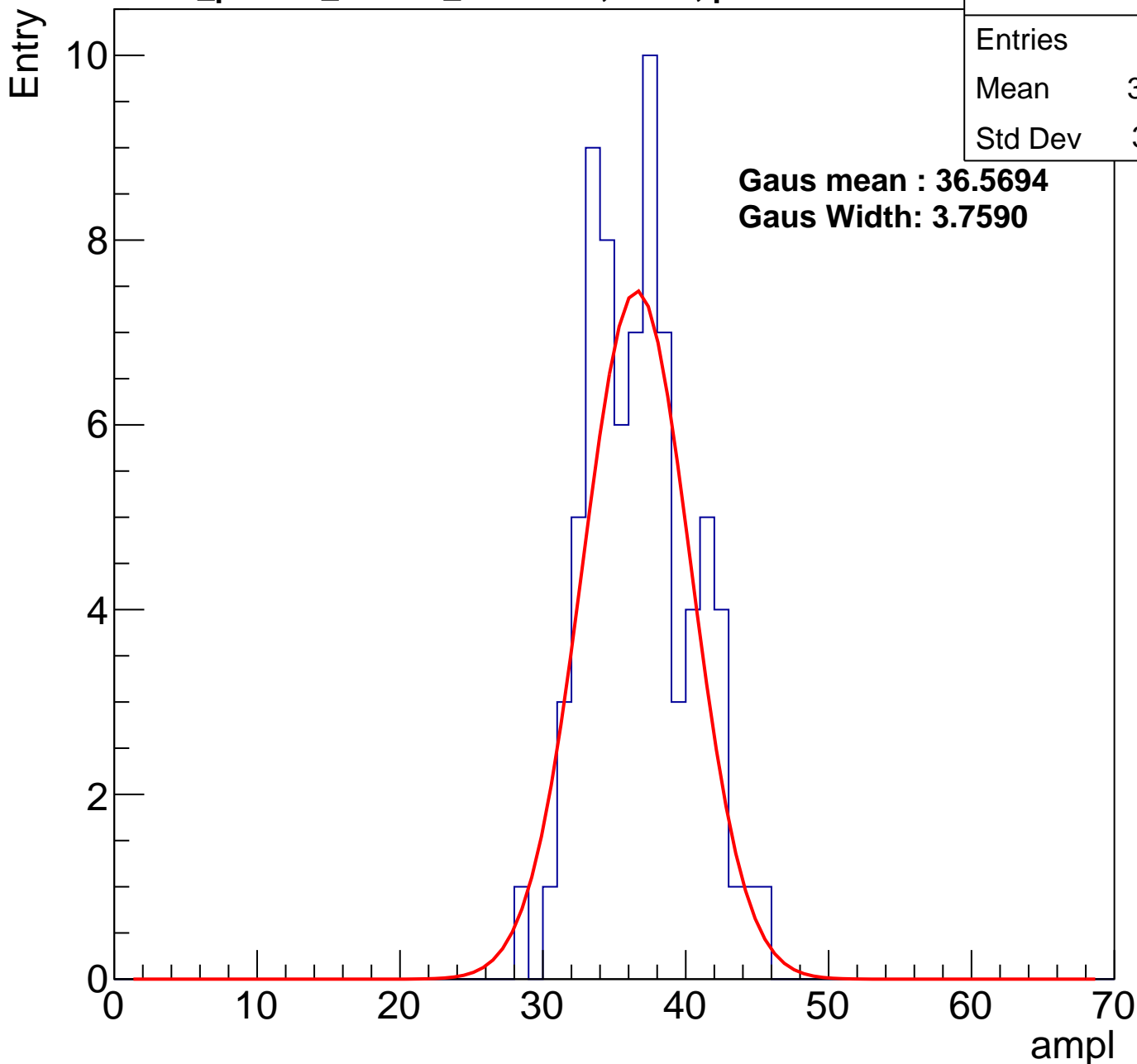
B1L100S, U15-ch10, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	36.32
Std Dev	3.551

Gaus mean : 36.5694

Gaus Width: 3.7590



B1L100S, U15-ch10, adc2

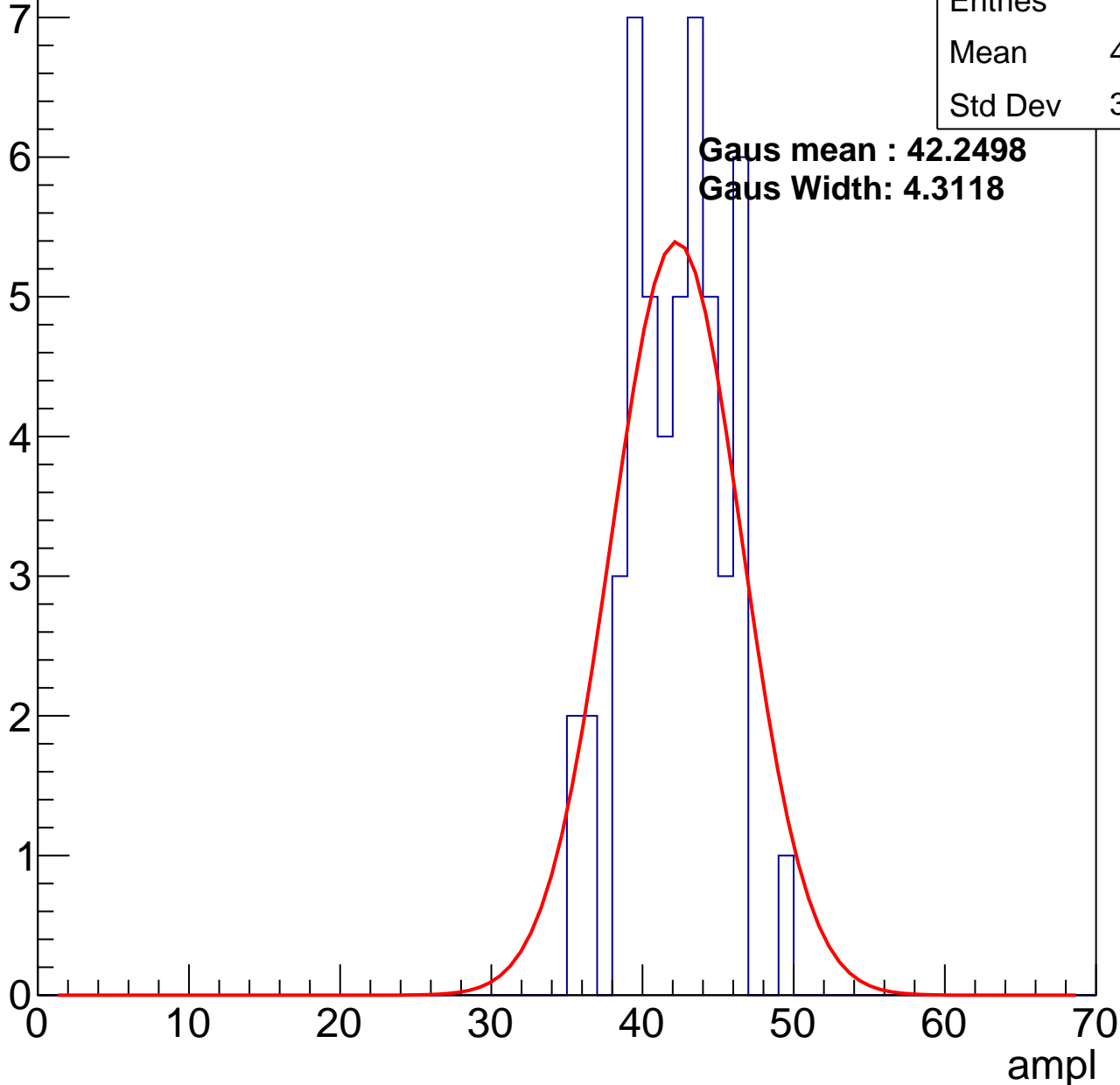
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	41.68
Std Dev	3.159

Gaus mean : 42.2498

Gaus Width: 4.3118

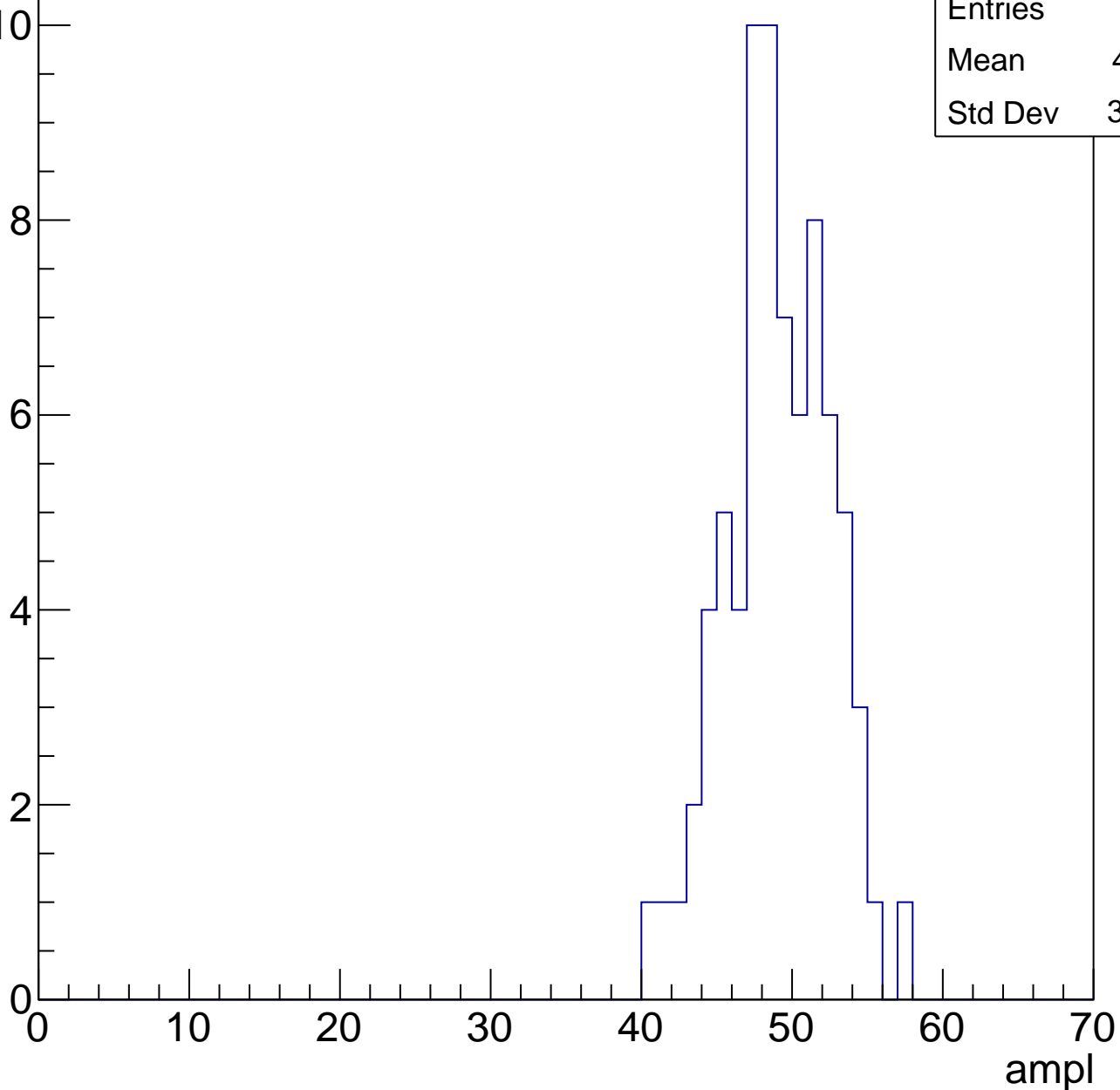


B1L100S, U15-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	48.61
Std Dev	3.402

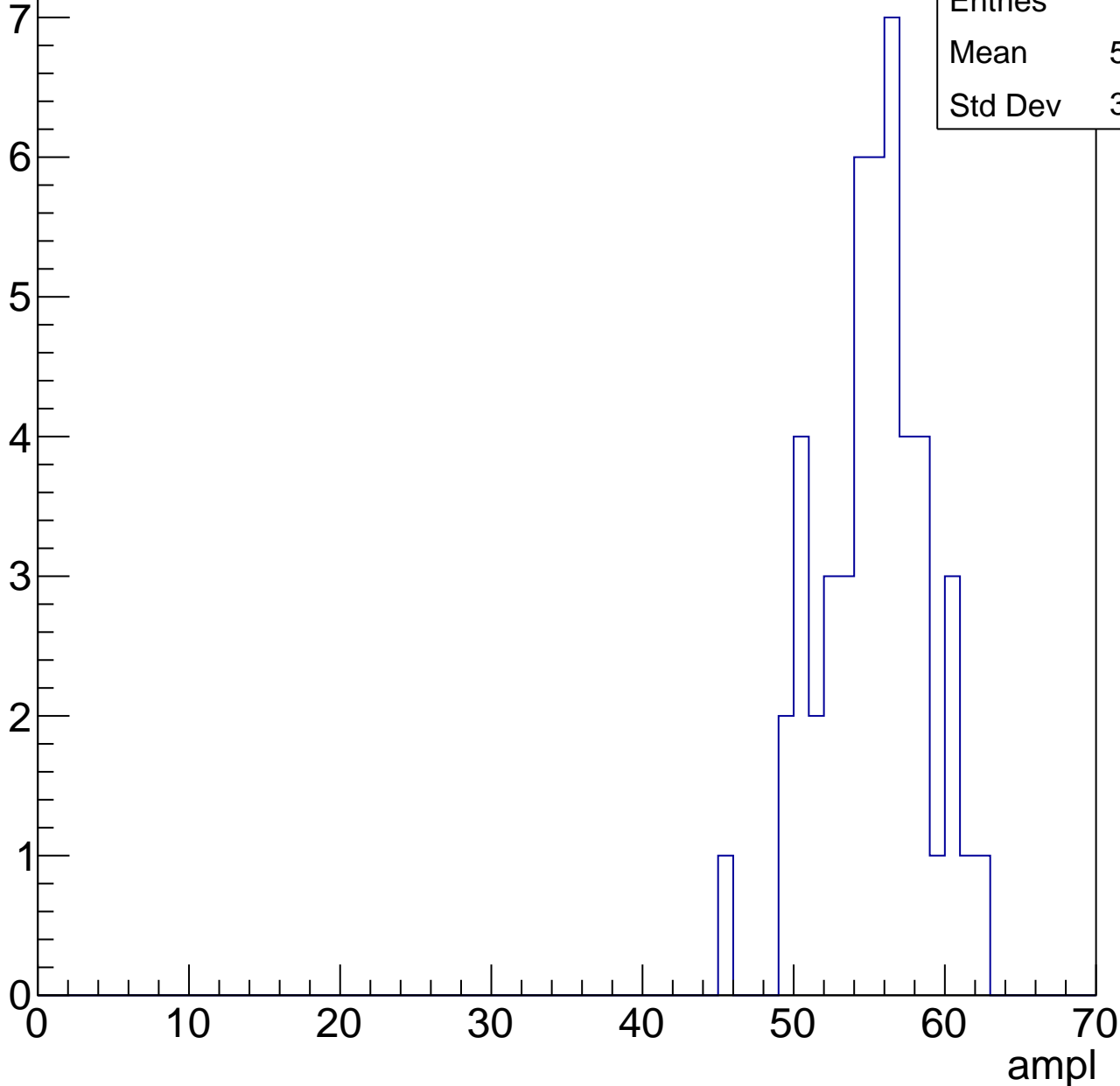


B1L100S, U15-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	54.75
Std Dev	3.479

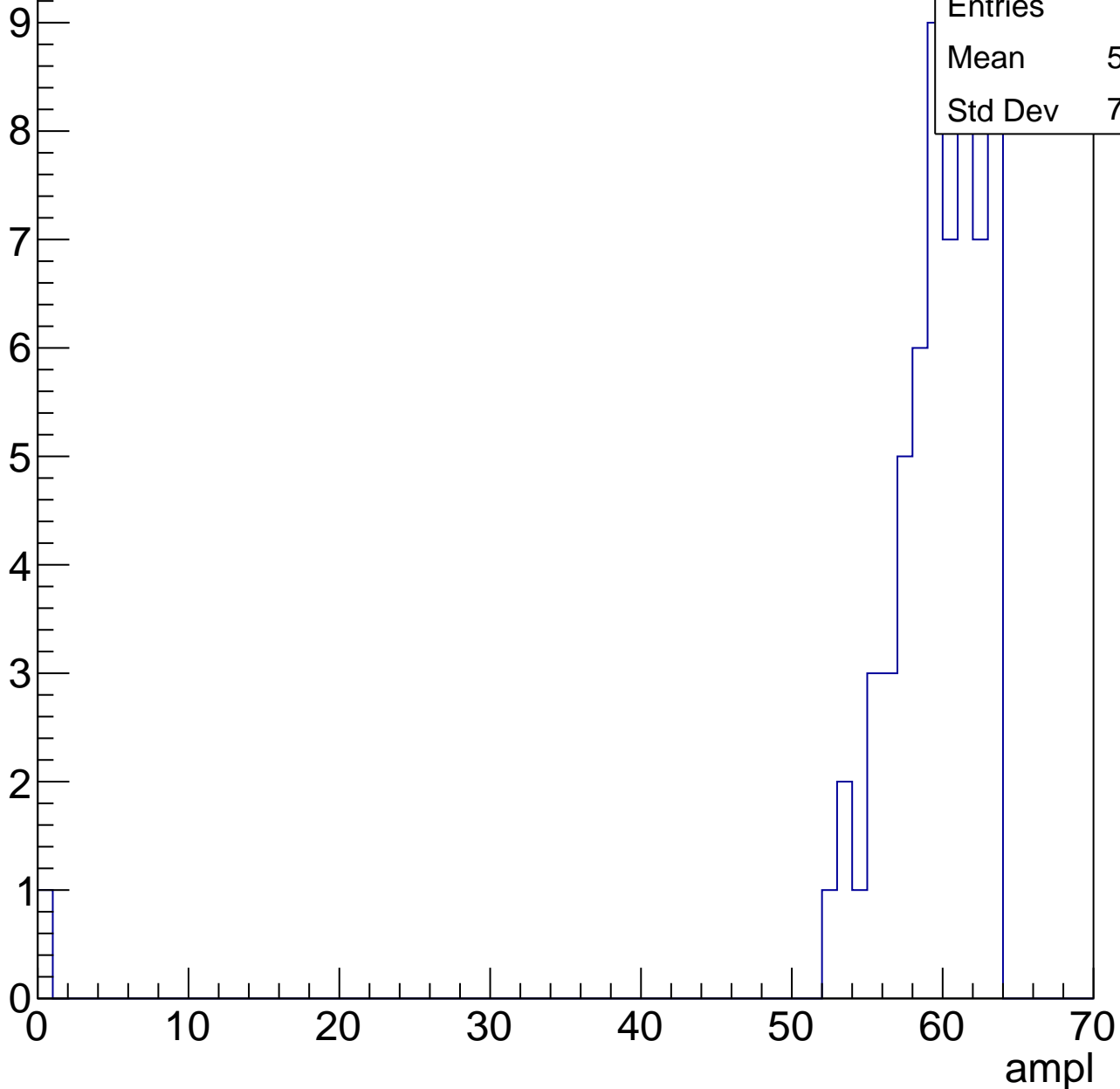


B1L100S, U15-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	58.32
Std Dev	7.965



B1L100S, U15-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

ampl

B1L100S, U15-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch11, adc0

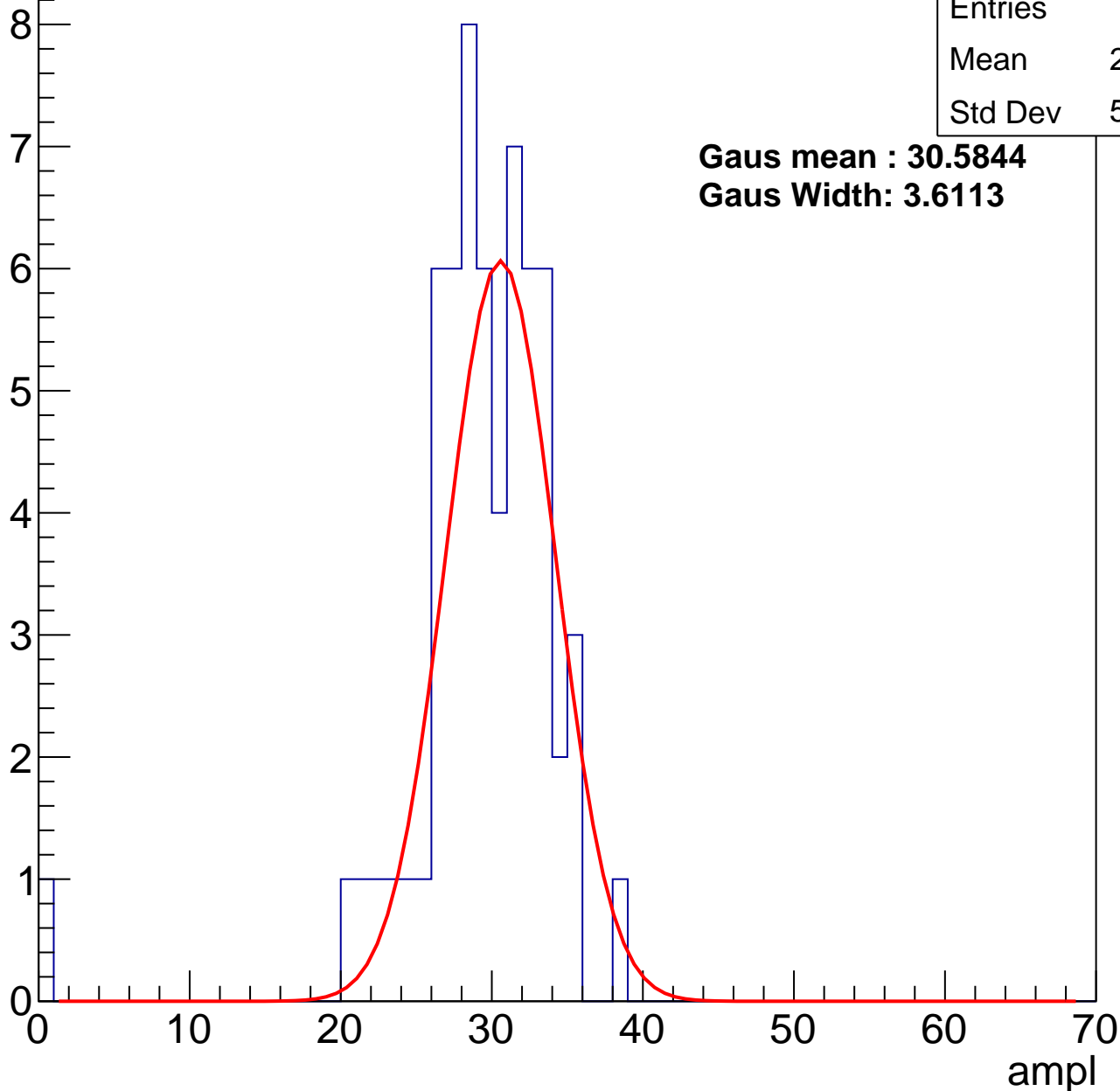
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.85
Std Dev	5.108

Gaus mean : 30.5844

Gaus Width: 3.6113



B1L100S, U15-ch11, adc1

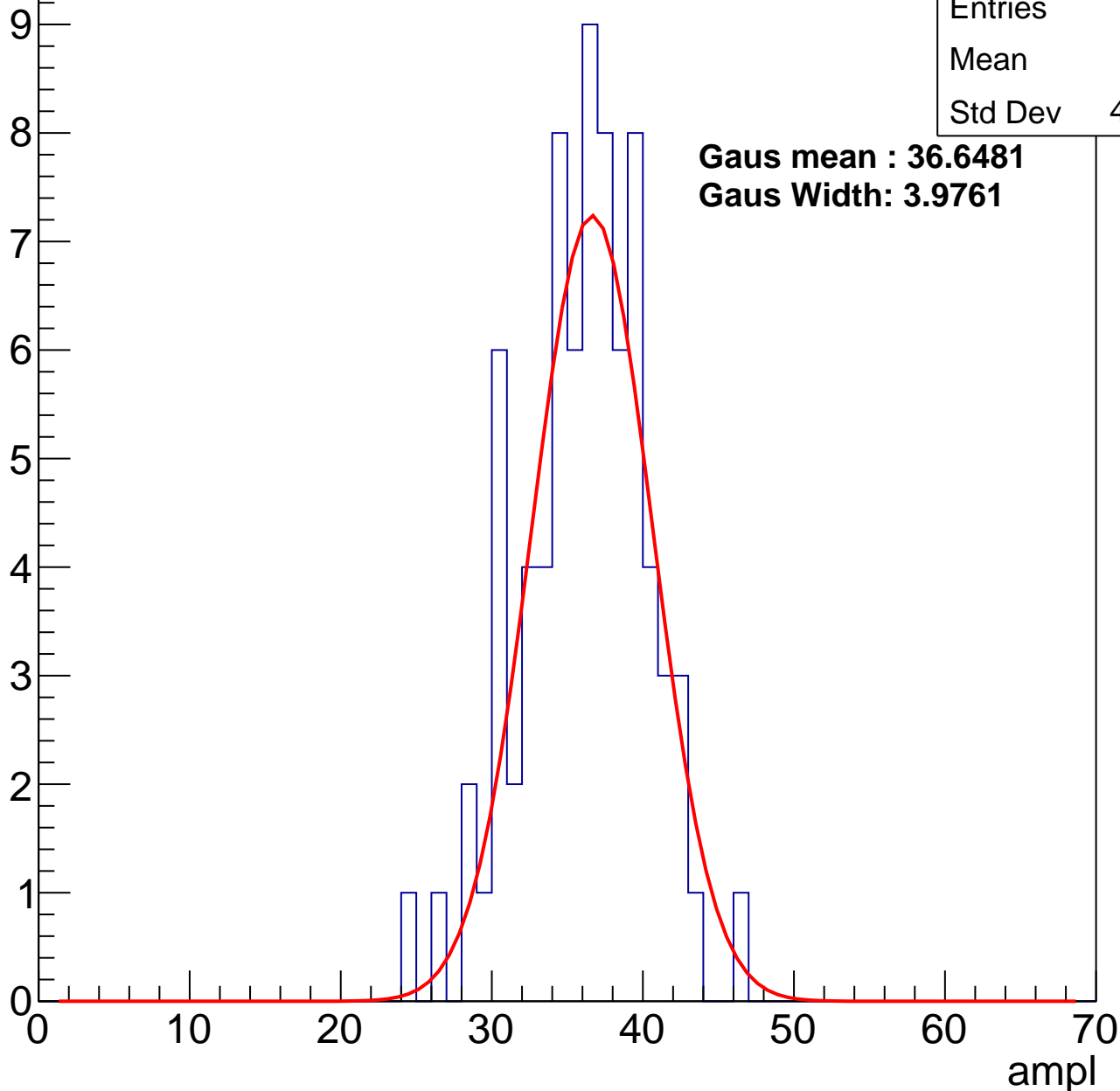
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	35.6
Std Dev	4.102

Gaus mean : 36.6481

Gaus Width: 3.9761



B1L100S, U15-ch11, adc2

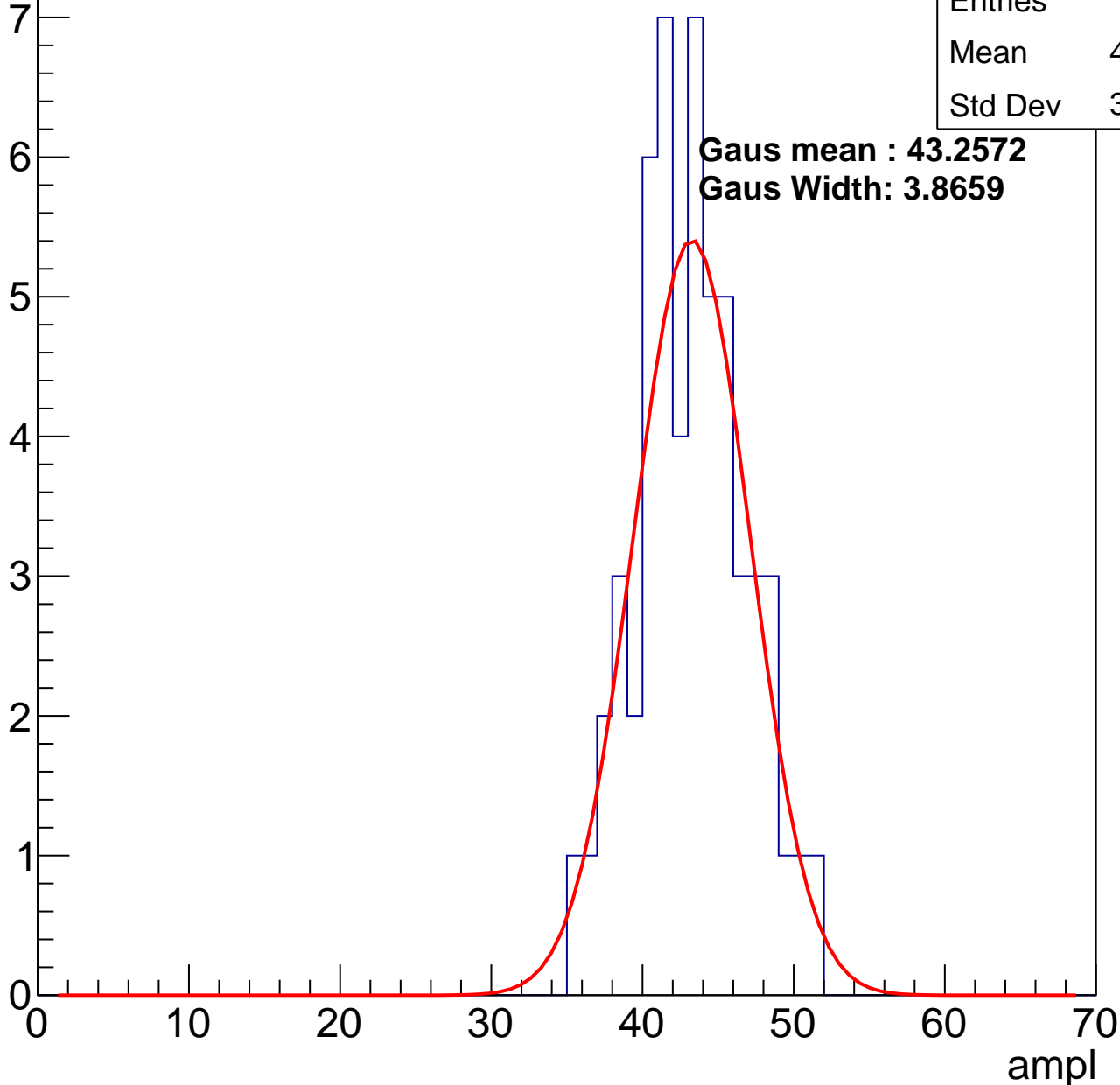
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	42.75
Std Dev	3.564

Gaus mean : 43.2572

Gaus Width: 3.8659

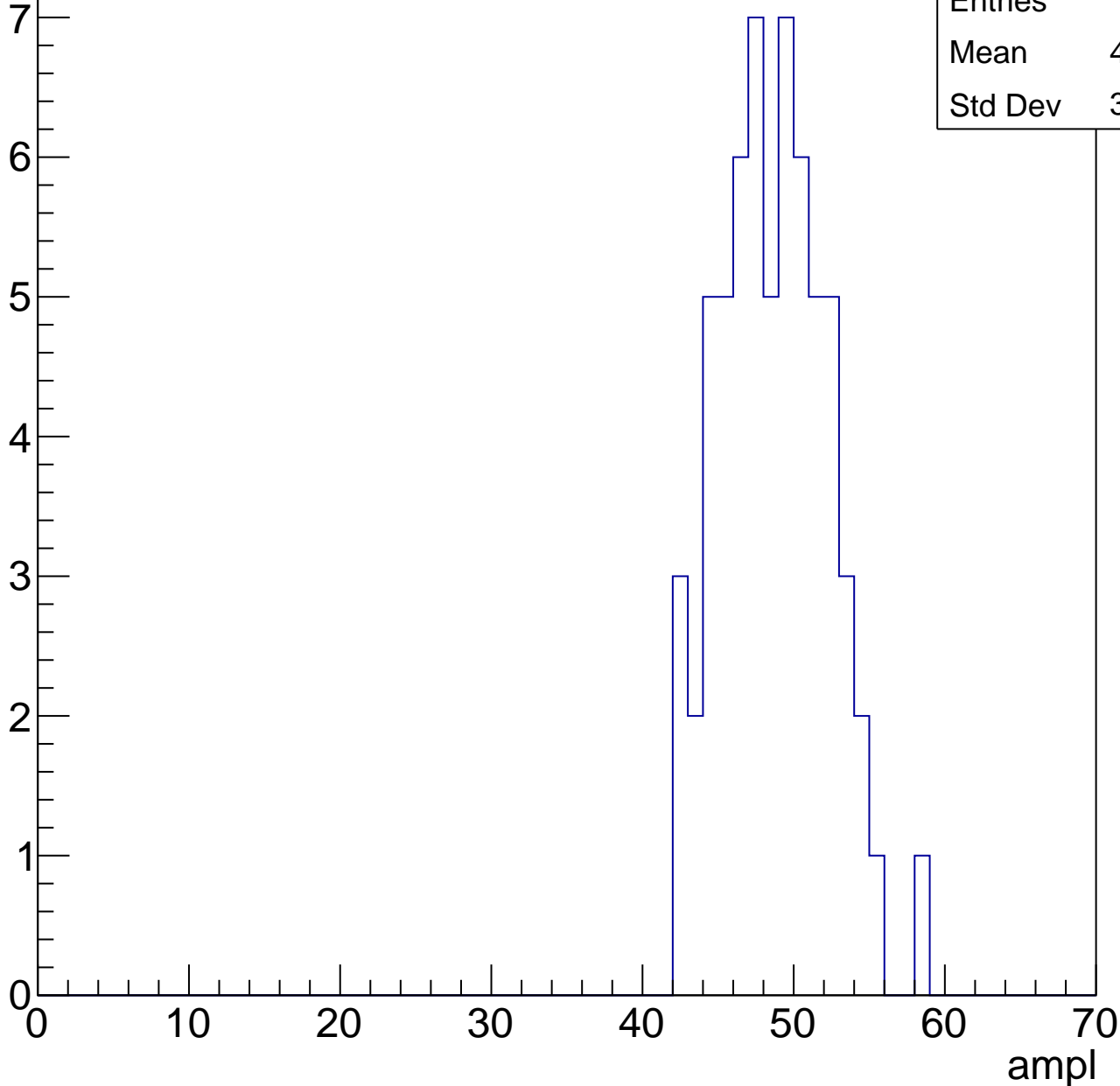


B1L100S, U15-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	48.25
Std Dev	3.478

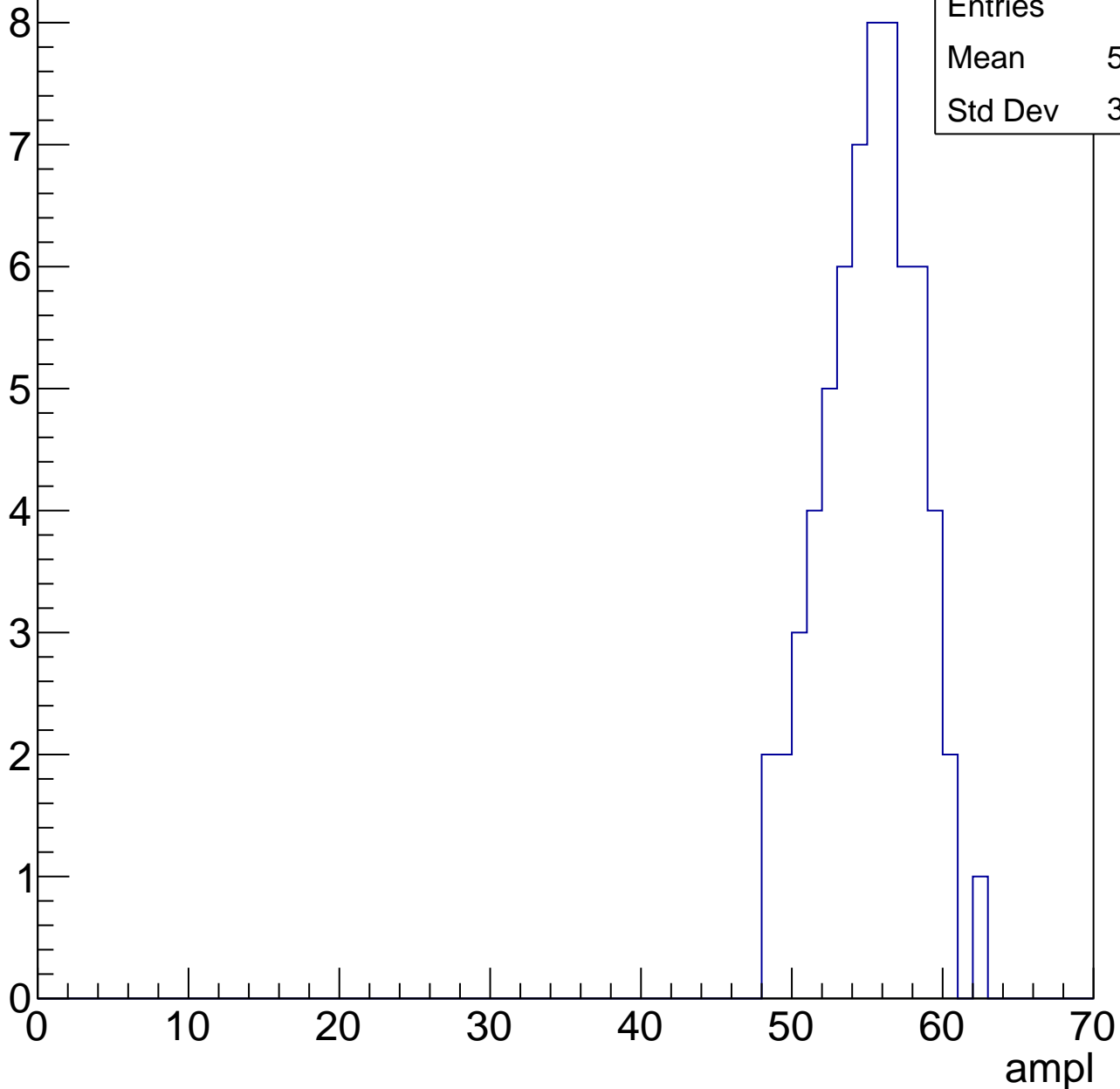


B1L100S, U15-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	54.69
Std Dev	3.132



B1L100S, U15-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

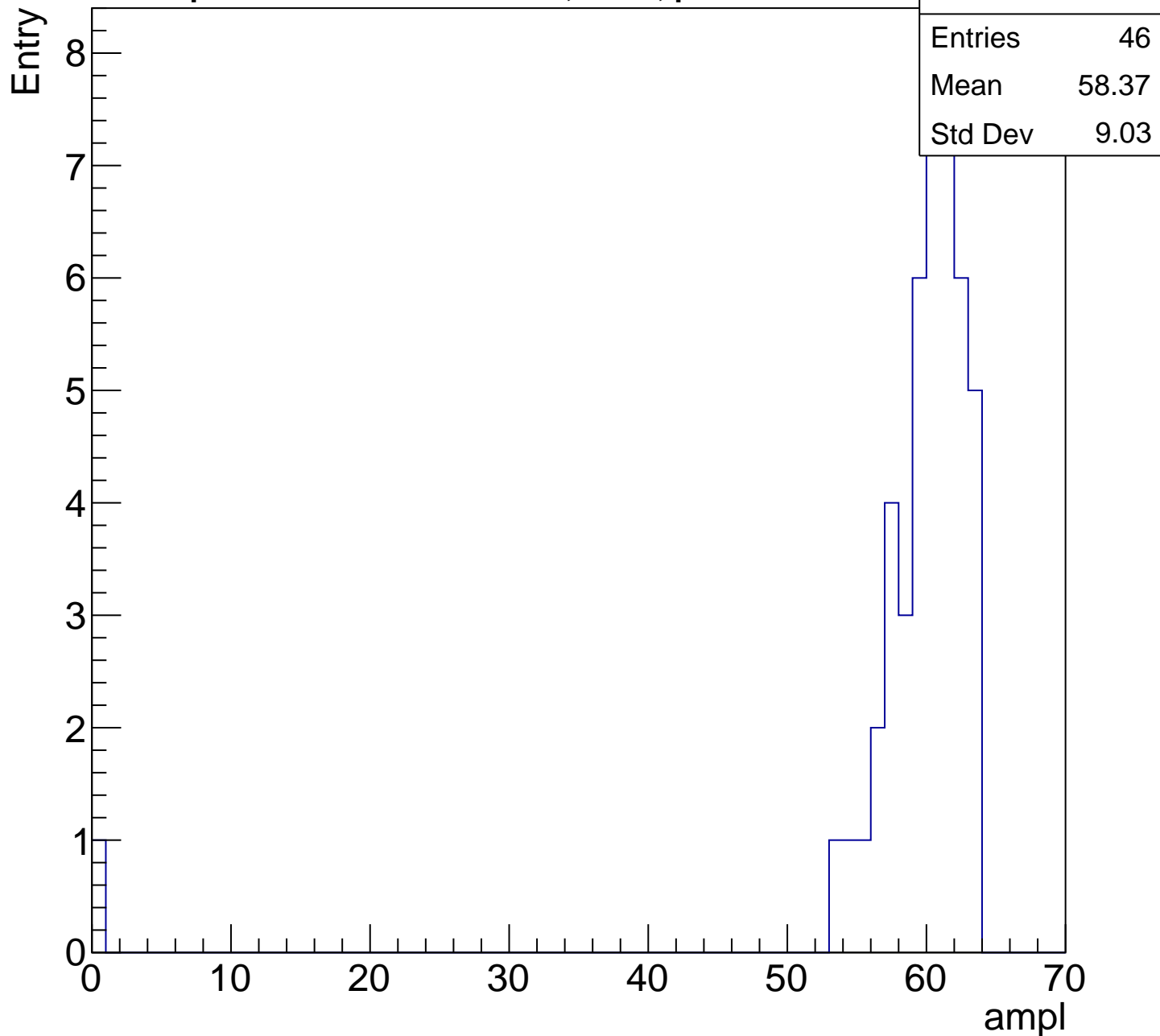
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.37
Std Dev	9.03

ampl

0 10 20 30 40 50 60 70

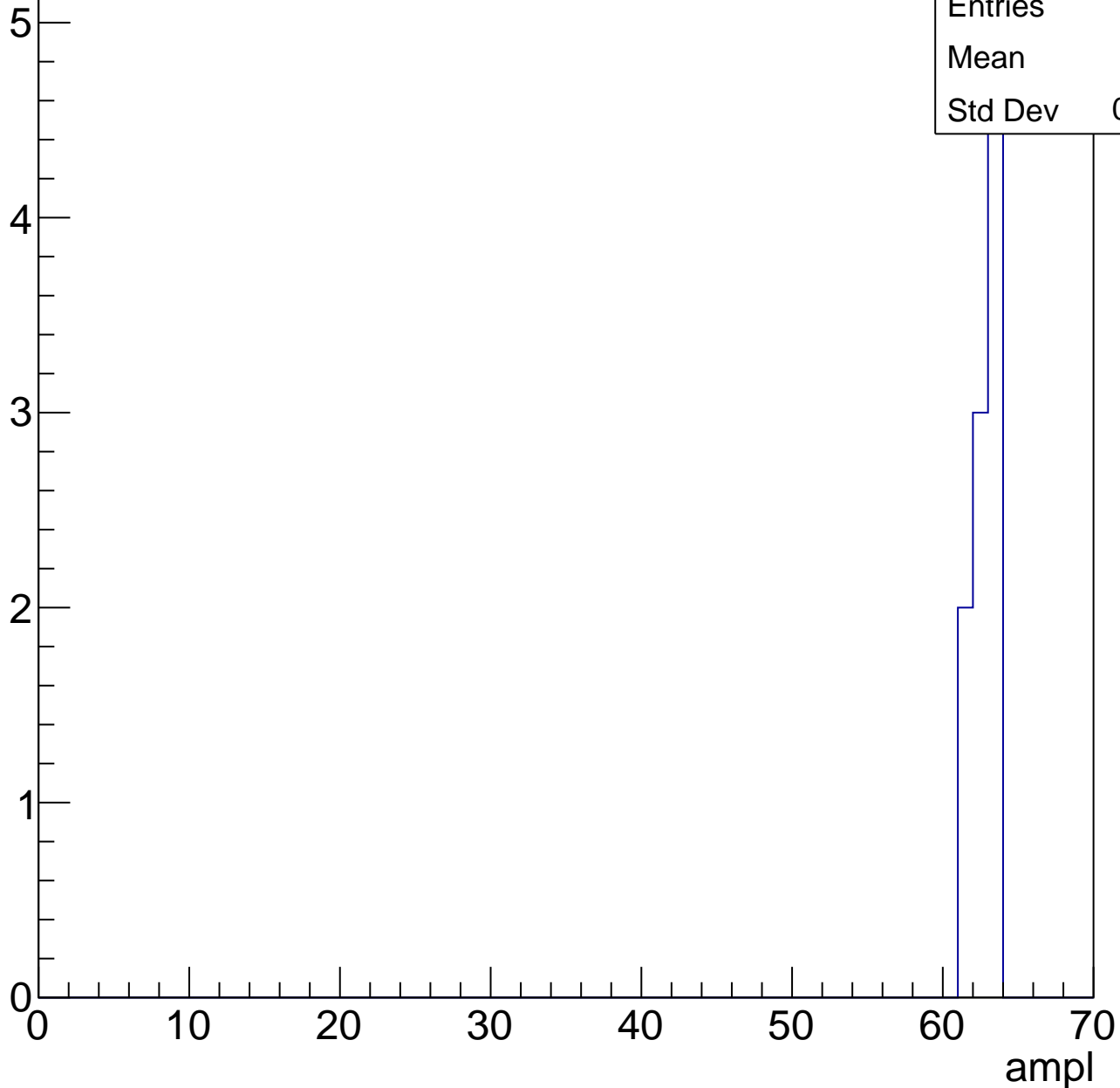


B1L100S, U15-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	62.3
Std Dev	0.781



B1L100S, U15-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch12, adc0

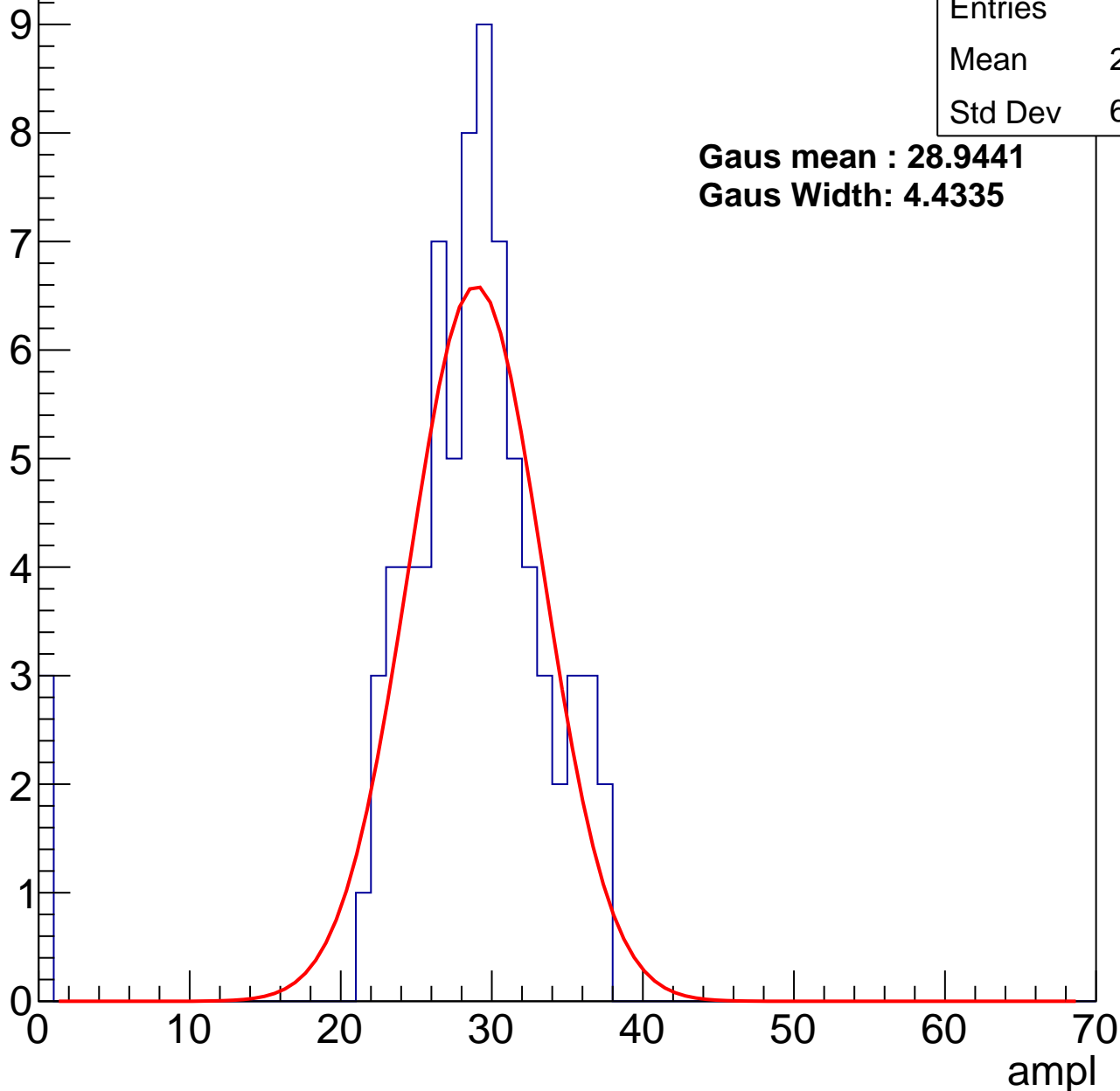
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	27.58
Std Dev	6.764

Gaus mean : 28.9441

Gaus Width: 4.4335



B1L100S, U15-ch12, adc1

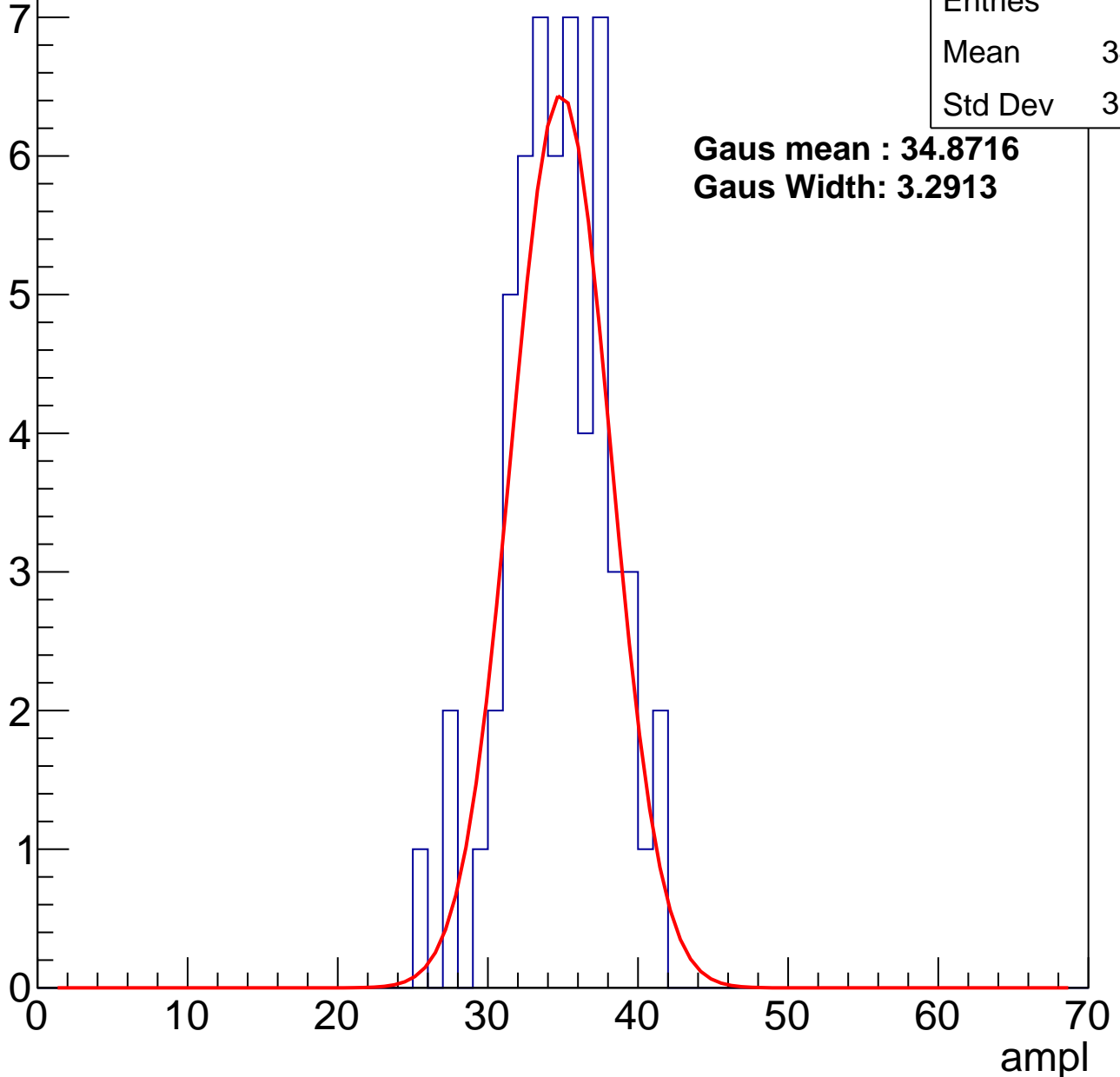
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	34.23
Std Dev	3.382

Gaus mean : 34.8716

Gaus Width: 3.2913



B1L100S, U15-ch12, adc2

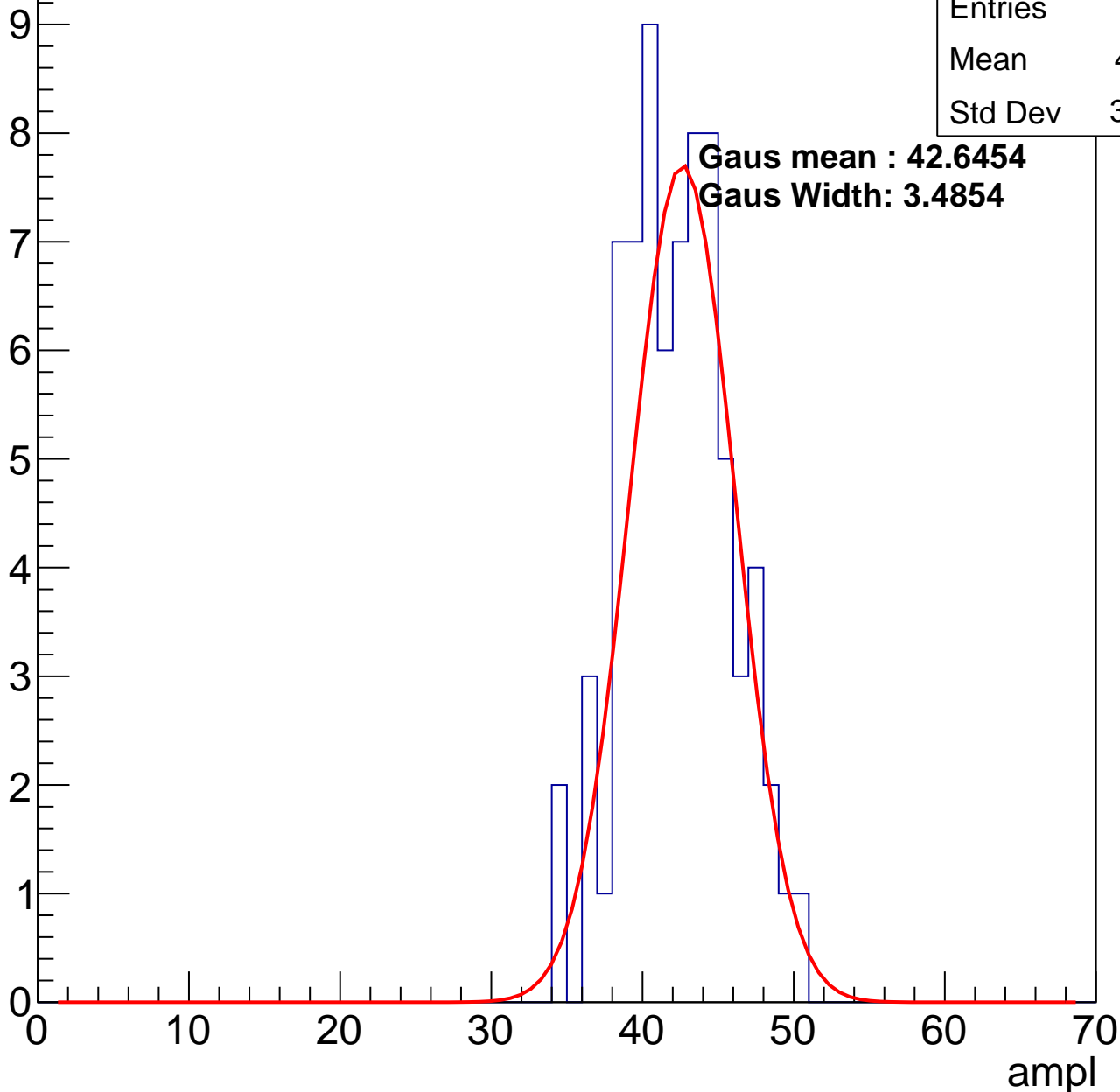
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	41.81
Std Dev	3.475

Gaus mean : 42.6454

Gaus Width: 3.4854

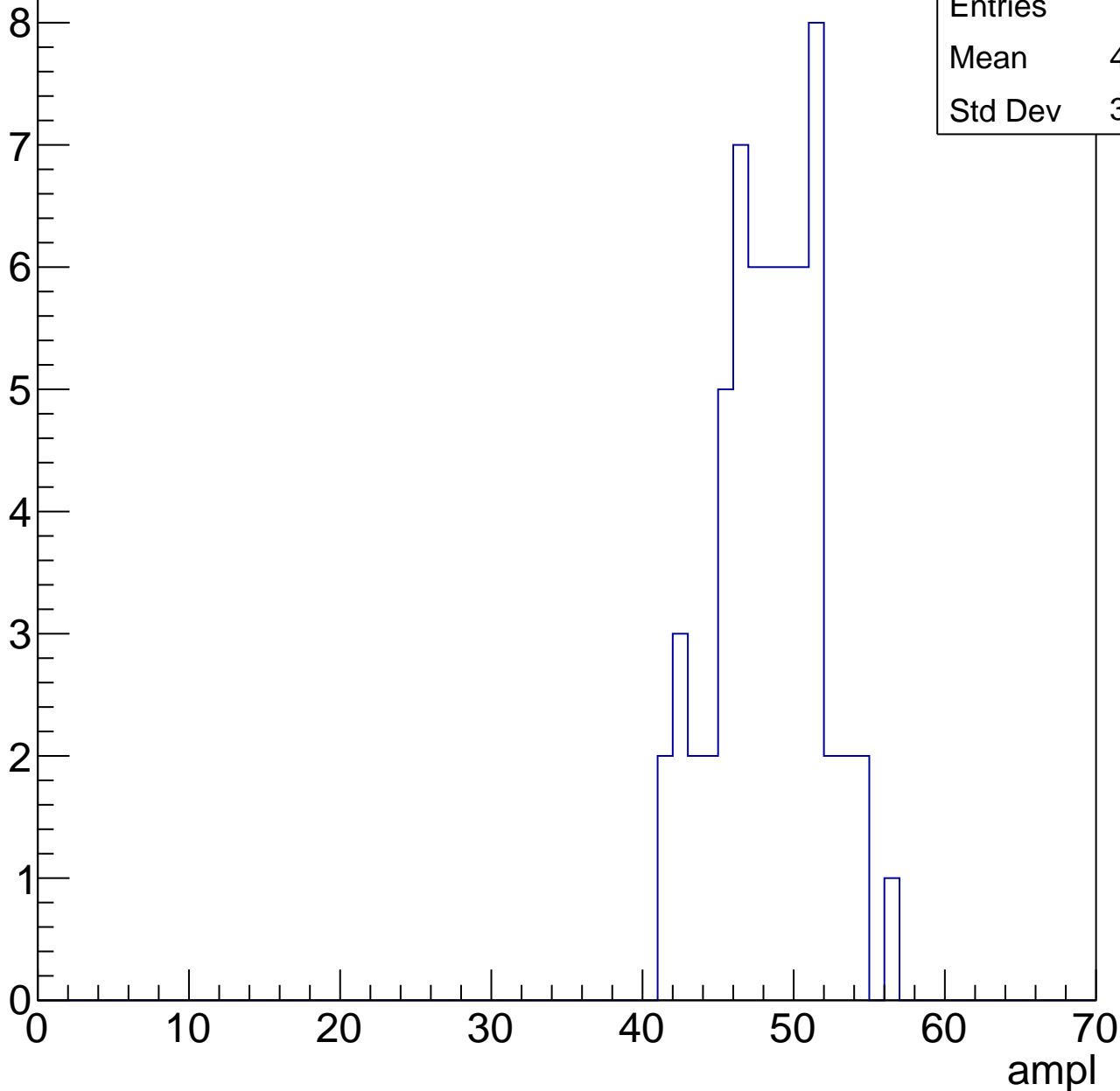


B1L100S, U15-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

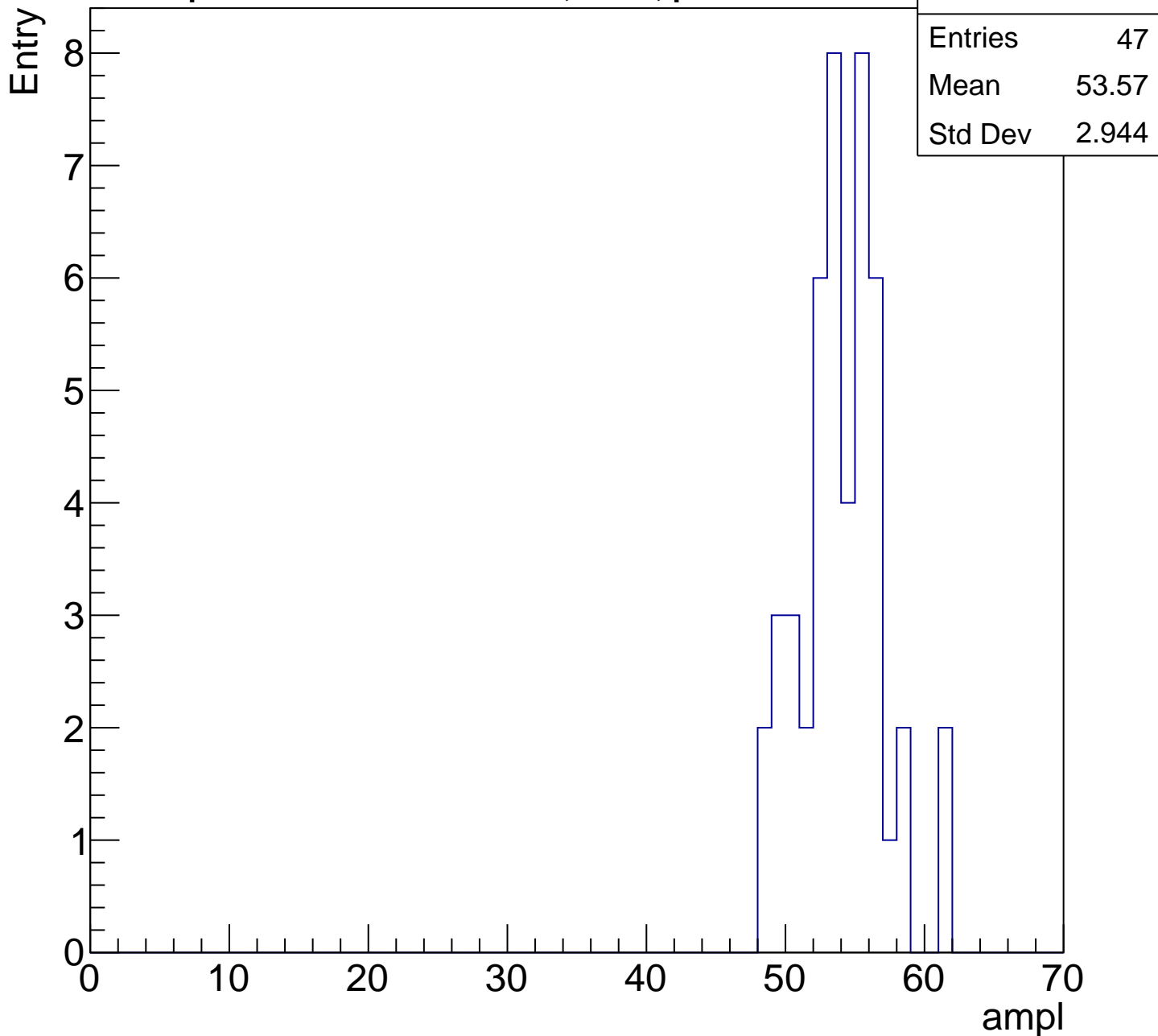
Entry

Entries	60
Mean	47.92
Std Dev	3.383



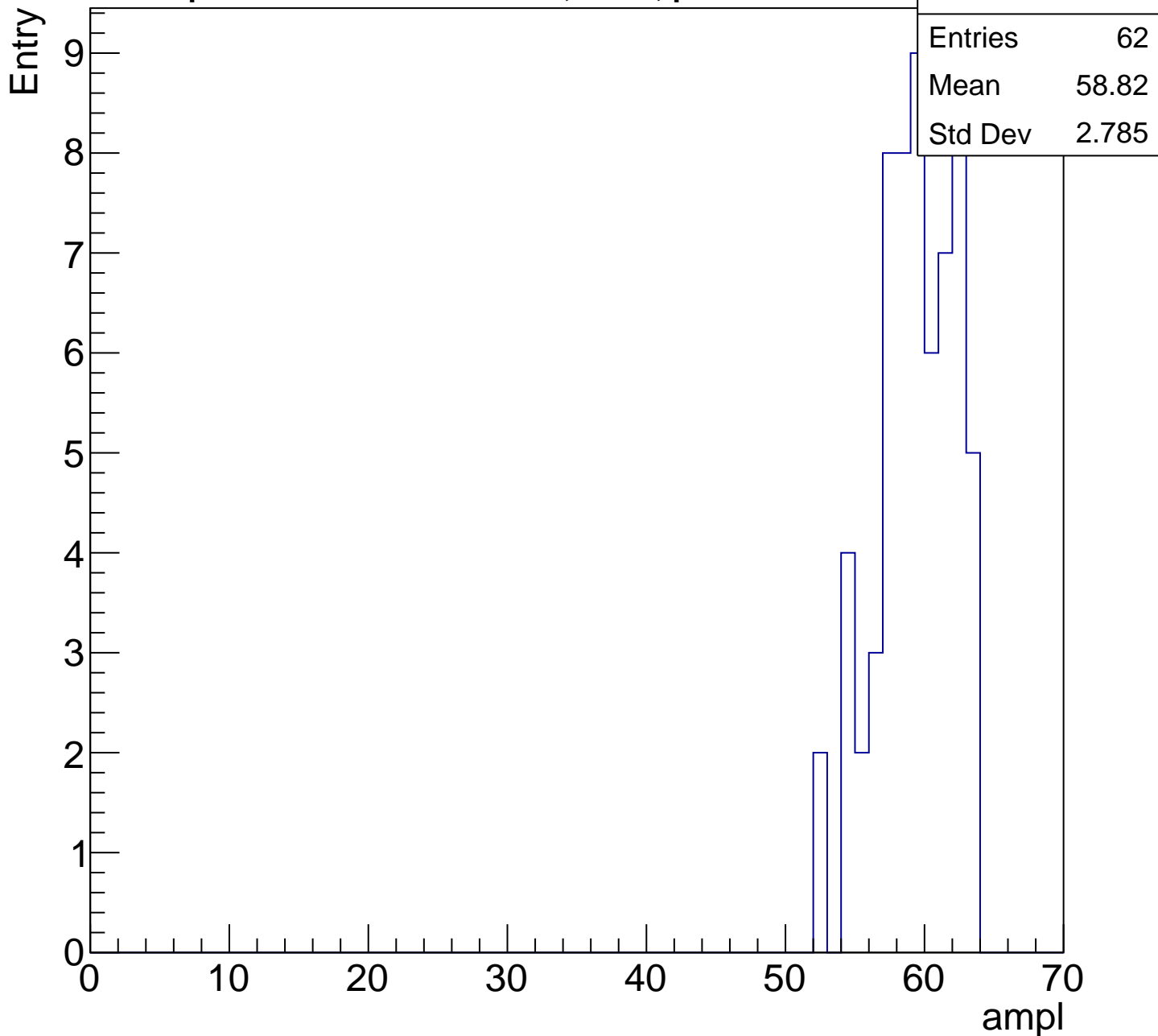
B1L100S, U15-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

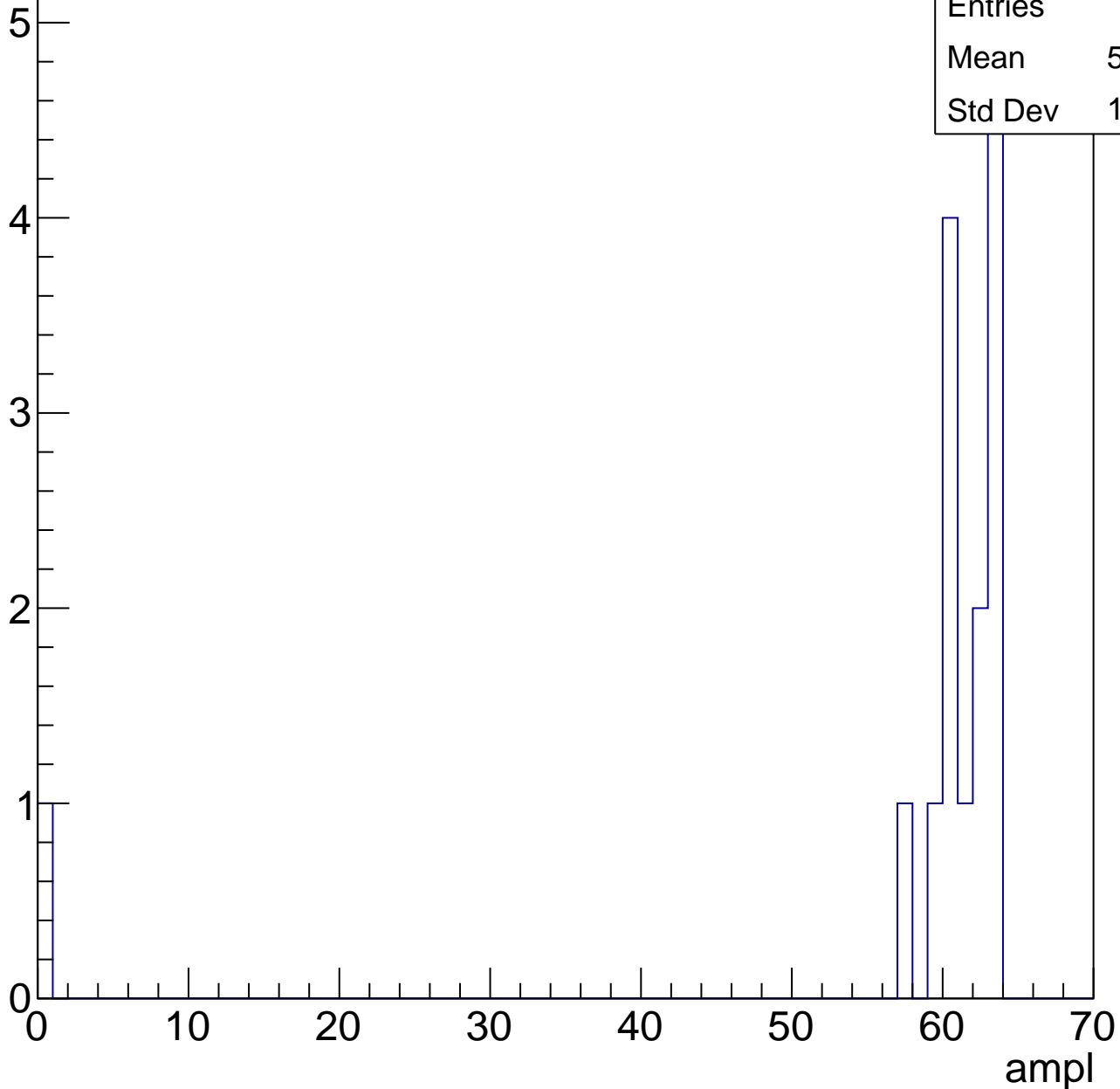


B1L100S, U15-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	57.07
Std Dev	15.35



B1L100S, U15-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch13, adc0

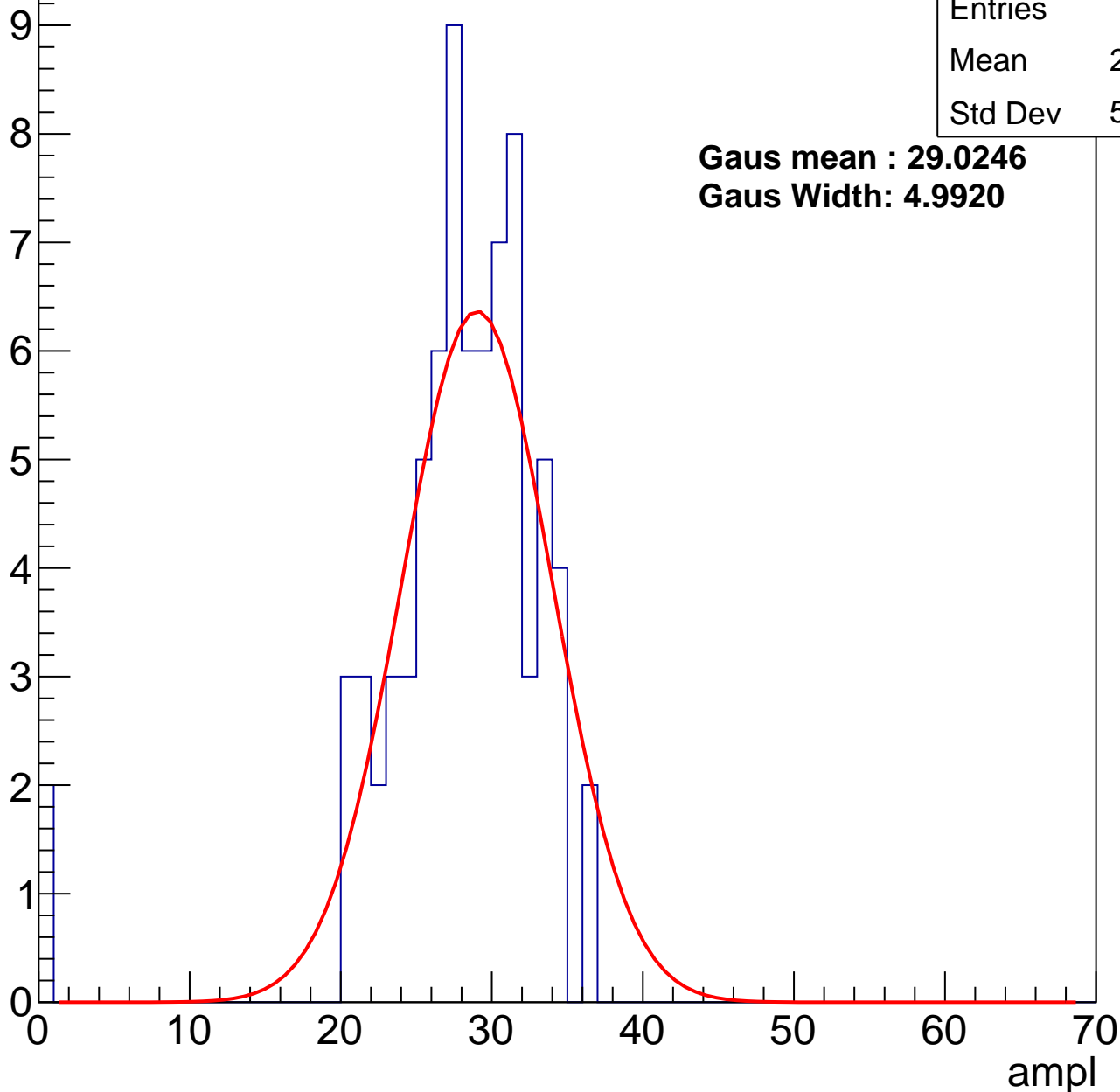
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	27.29
Std Dev	5.912

Gaus mean : 29.0246

Gaus Width: 4.9920



B1L100S, U15-ch13, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	35.42
Std Dev	3.654

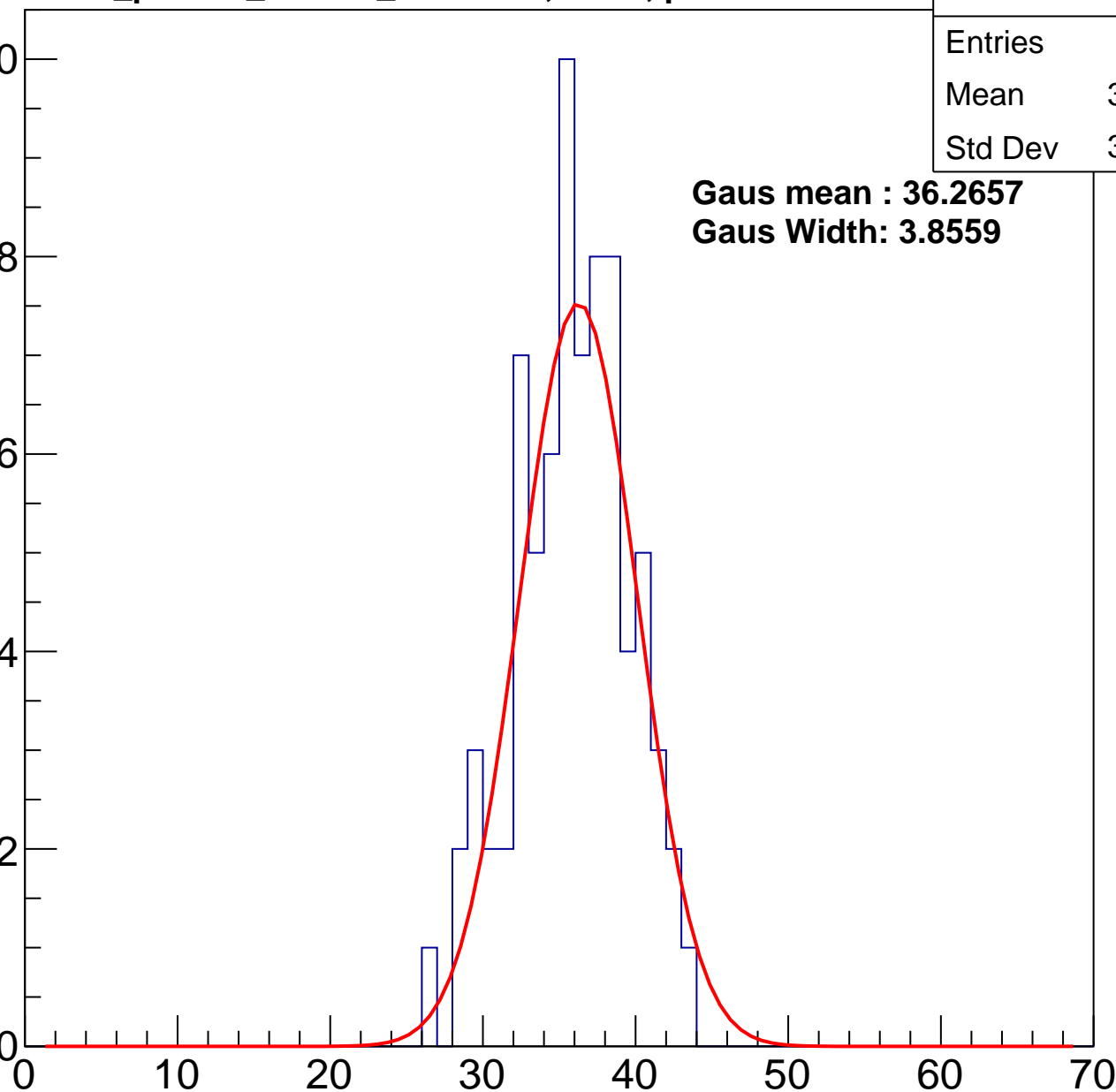
Gaus mean : 36.2657

Gaus Width: 3.8559

Entry

10
8
6
4
2
0

ampl



B1L100S, U15-ch13, adc2

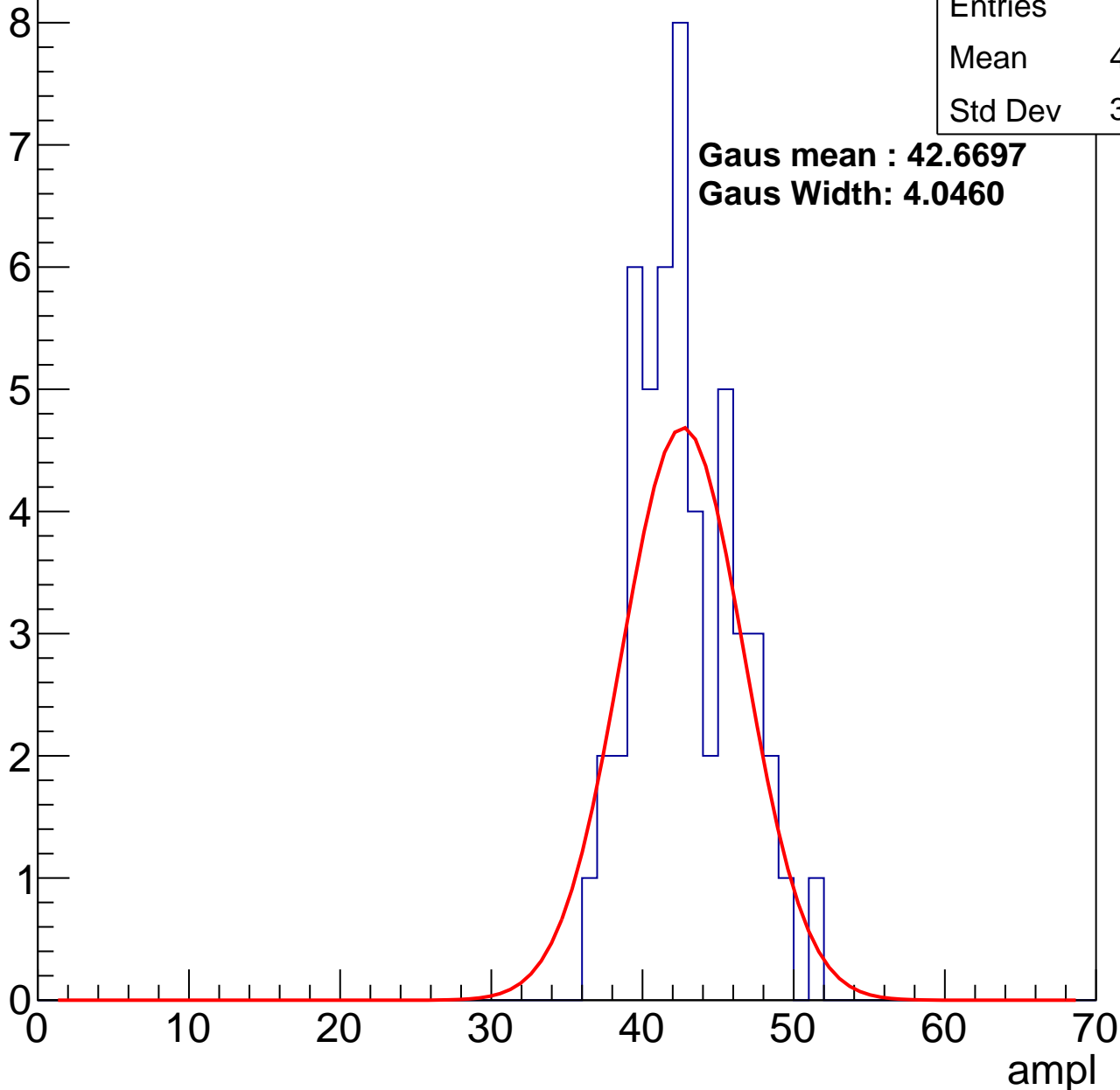
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	42.39
Std Dev	3.349

Gaus mean : 42.6697

Gaus Width: 4.0460

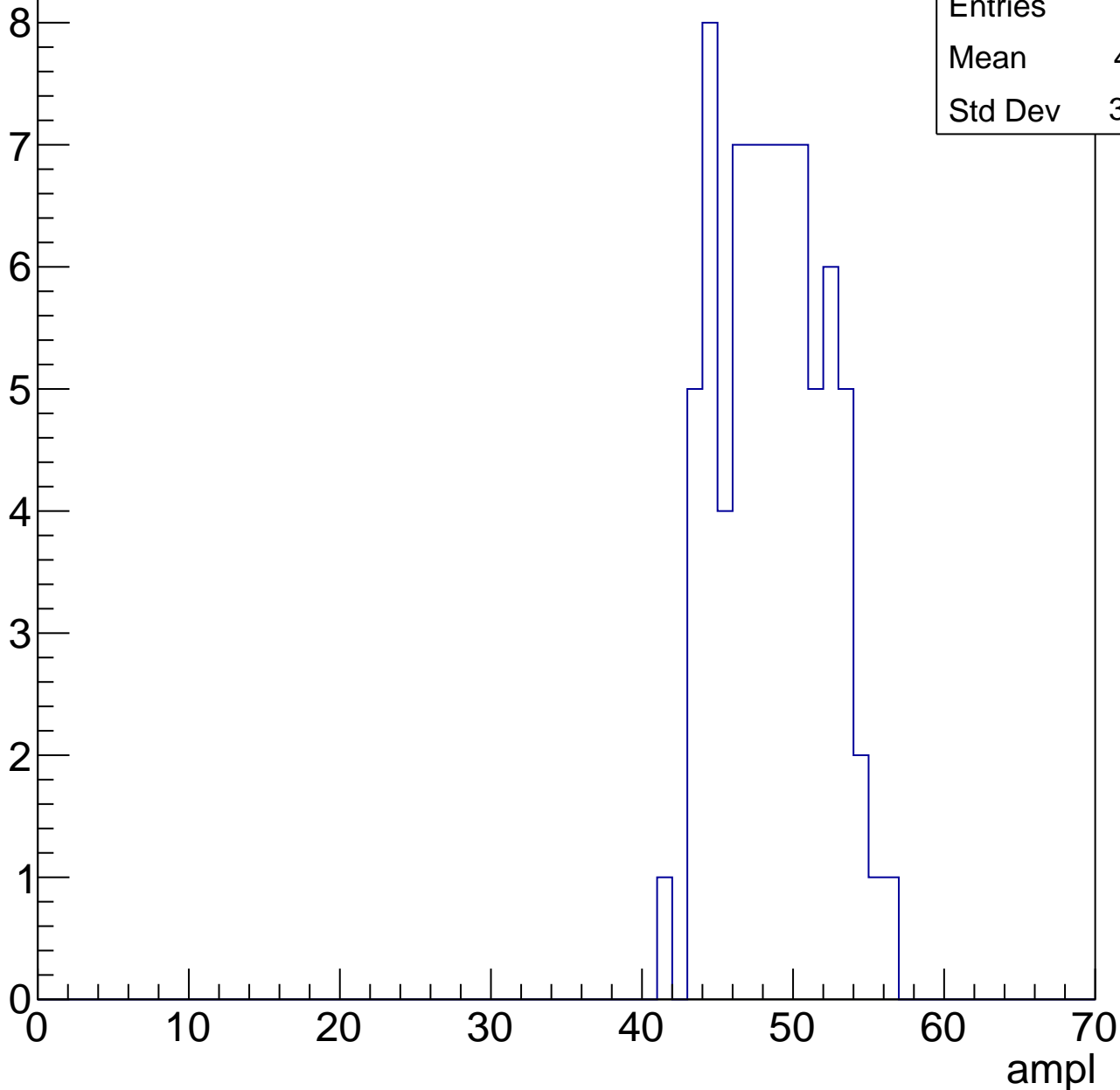


B1L100S, U15-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	48.21
Std Dev	3.424

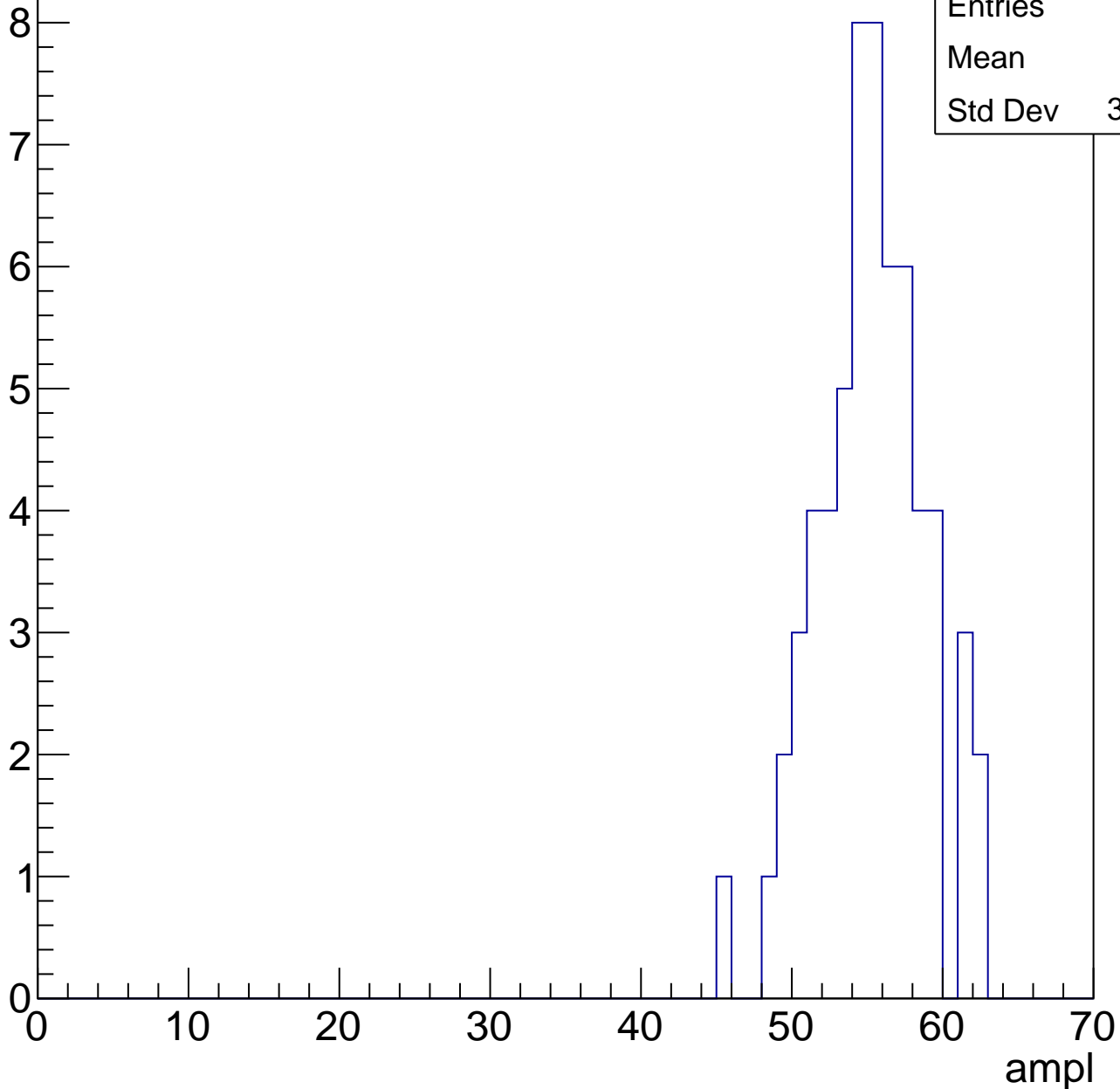


B1L100S, U15-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	54.8
Std Dev	3.524

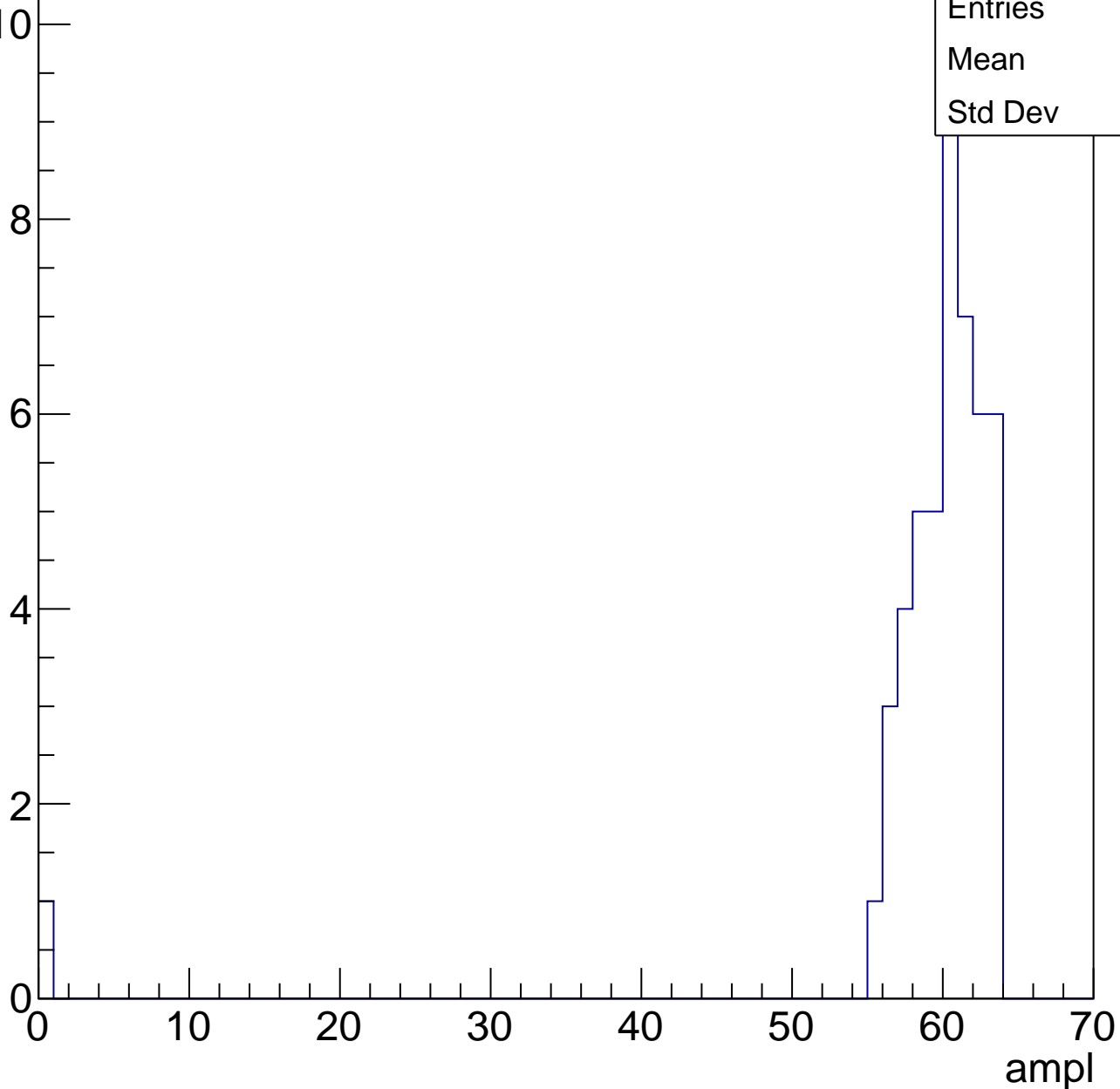


B1L100S, U15-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

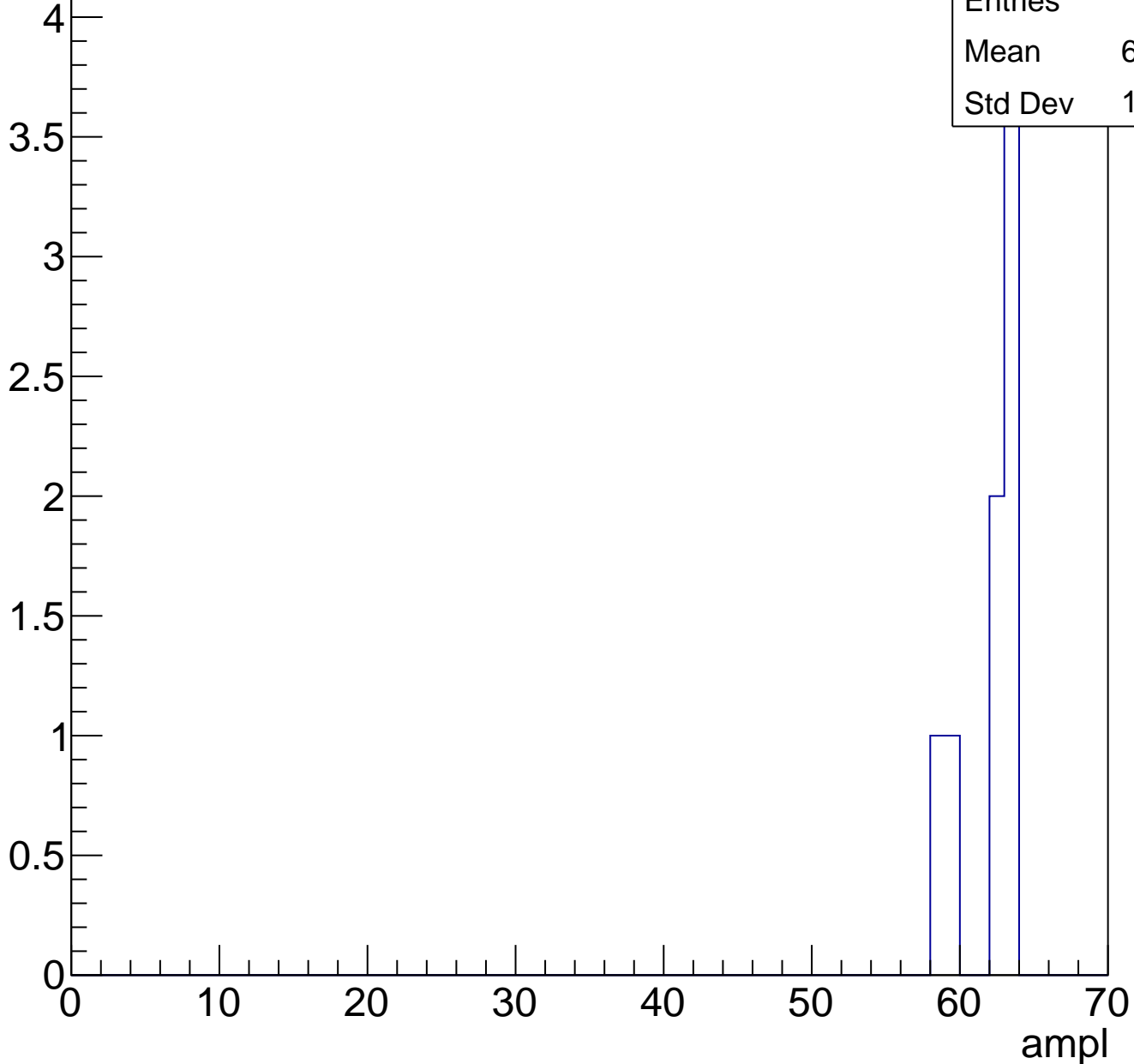
Entries	48
Mean	58.6
Std Dev	8.81



B1L100S, U15-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U15-ch14, adc0

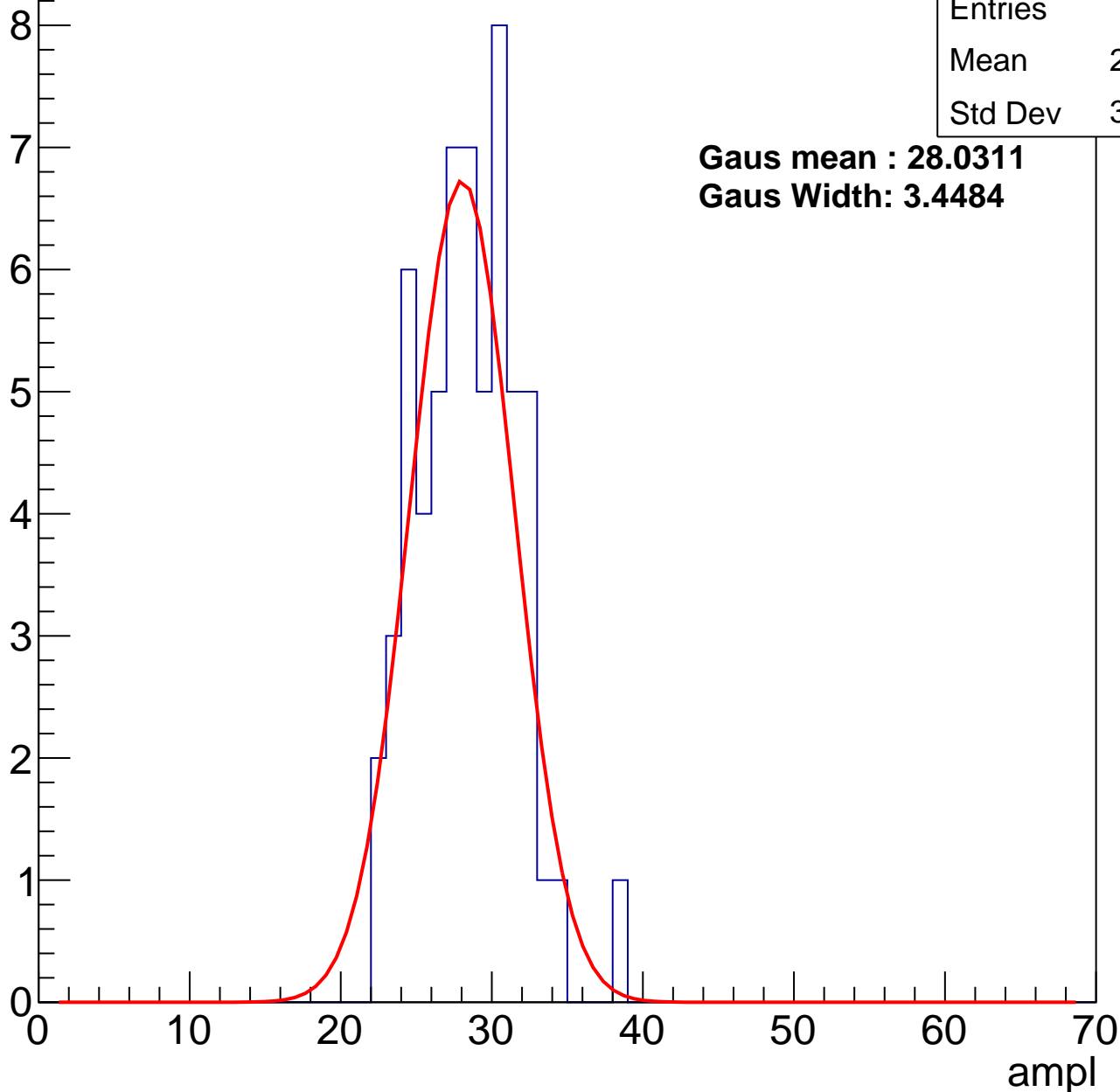
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	27.95
Std Dev	3.237

Gaus mean : 28.0311

Gaus Width: 3.4484



B1L100S, U15-ch14, adc1

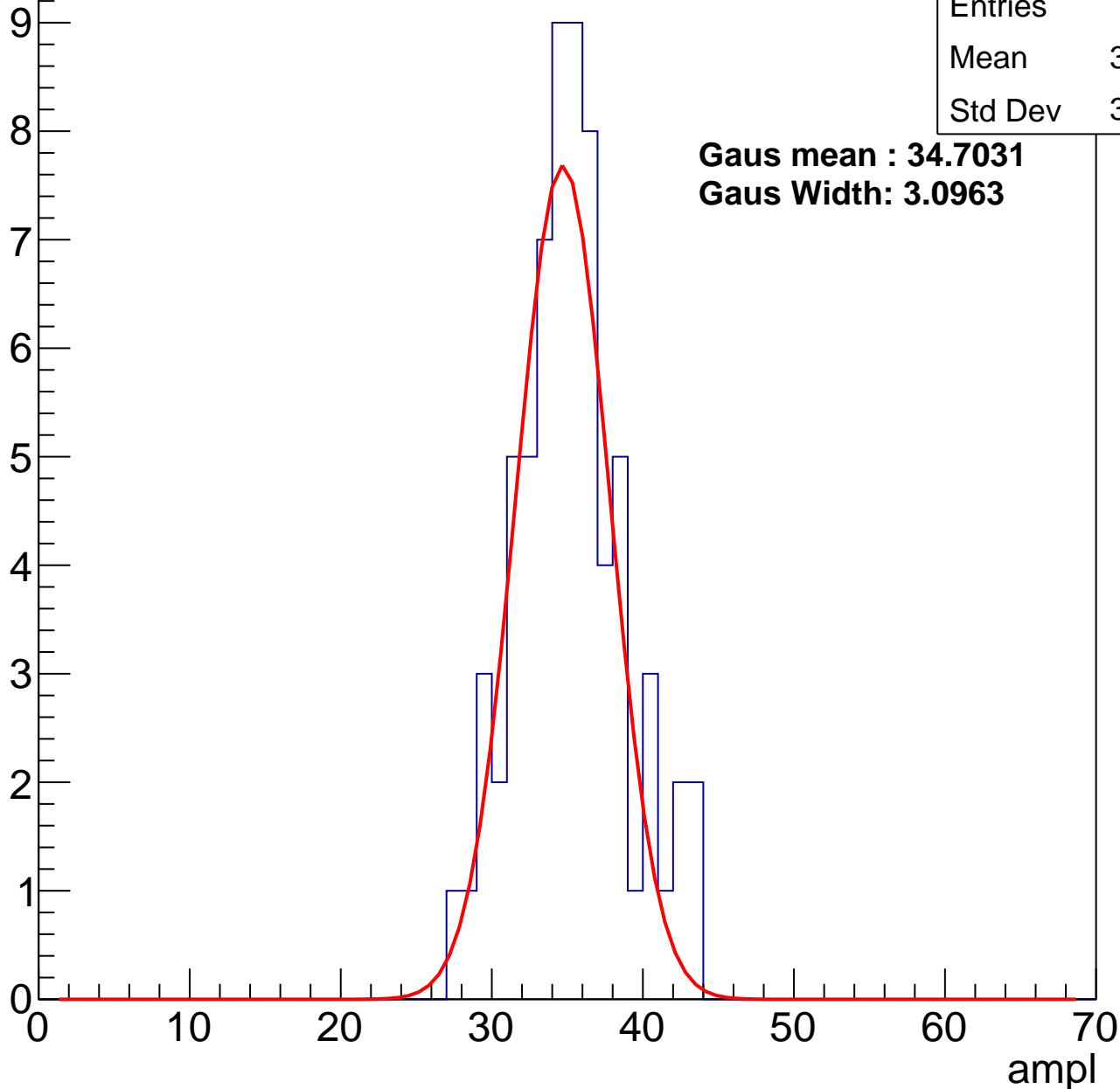
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	34.78
Std Dev	3.539

Gaus mean : 34.7031

Gaus Width: 3.0963



B1L100S, U15-ch14, adc2

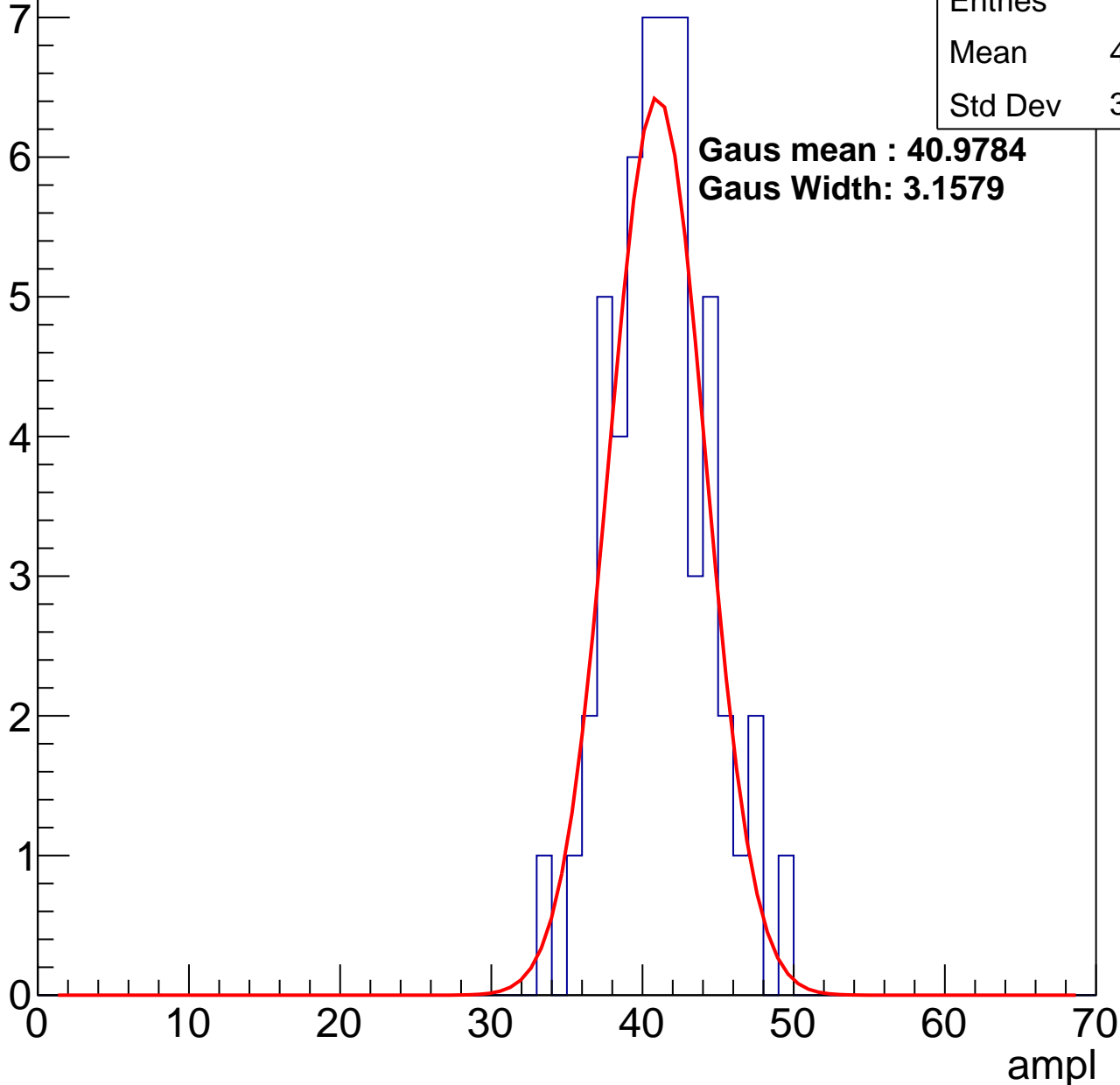
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	40.74
Std Dev	3.192

Gaus mean : 40.9784

Gaus Width: 3.1579

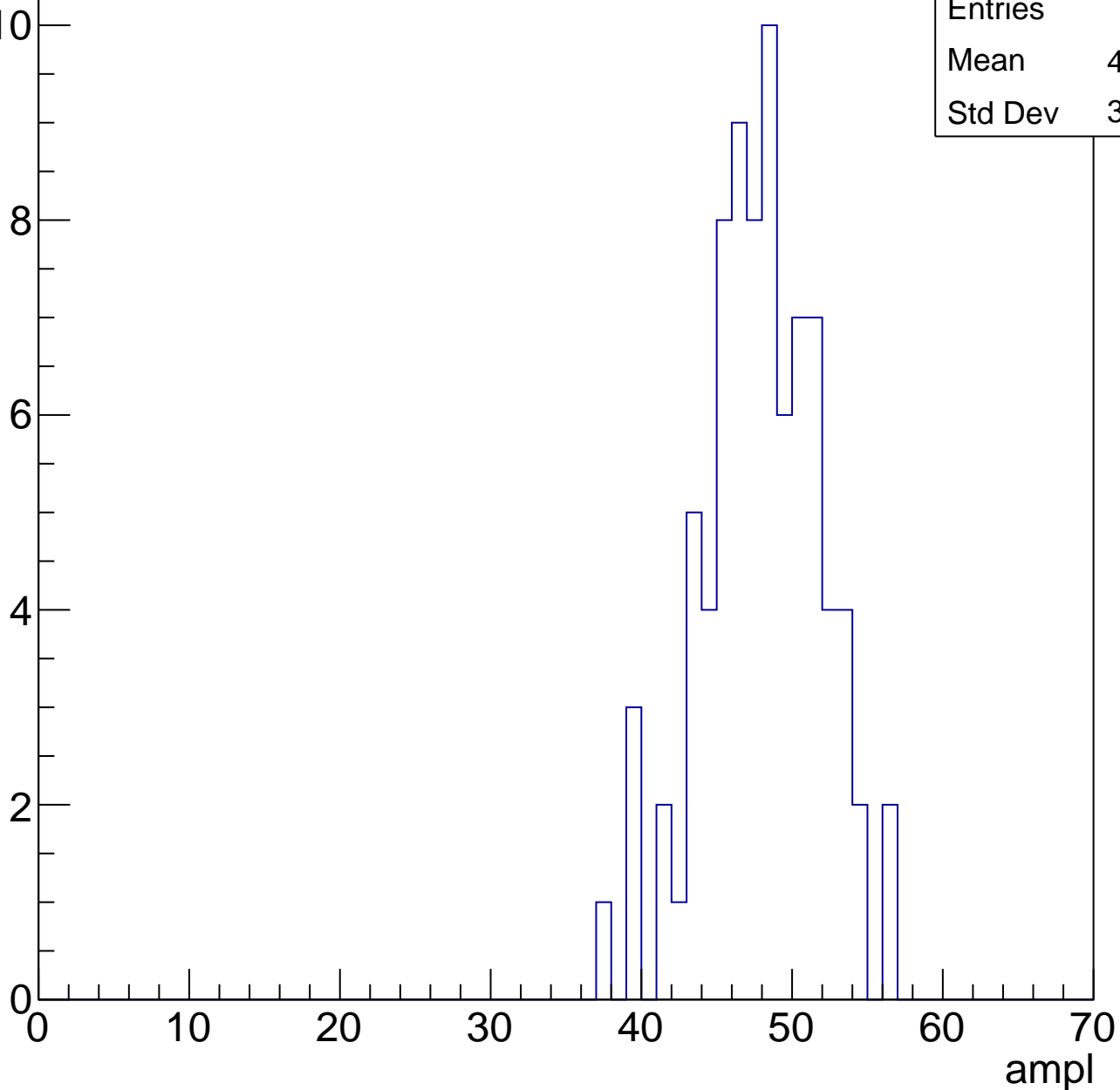


B1L100S, U15-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	47.47
Std Dev	3.866

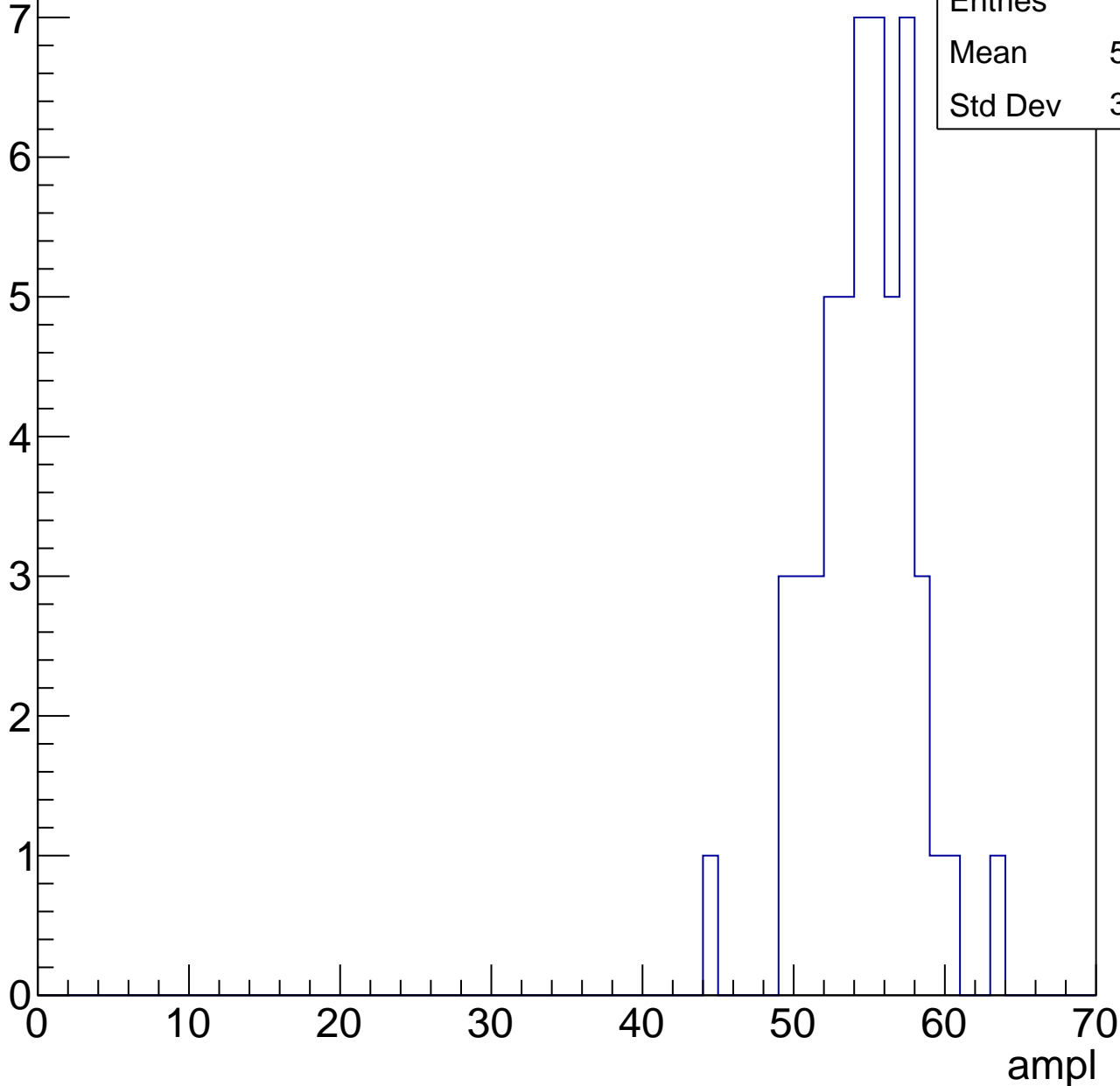


B1L100S, U15-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	54.17
Std Dev	3.262



B1L100S, U15-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 44

Mean 58.98

Std Dev 2.776

0

10

20

30

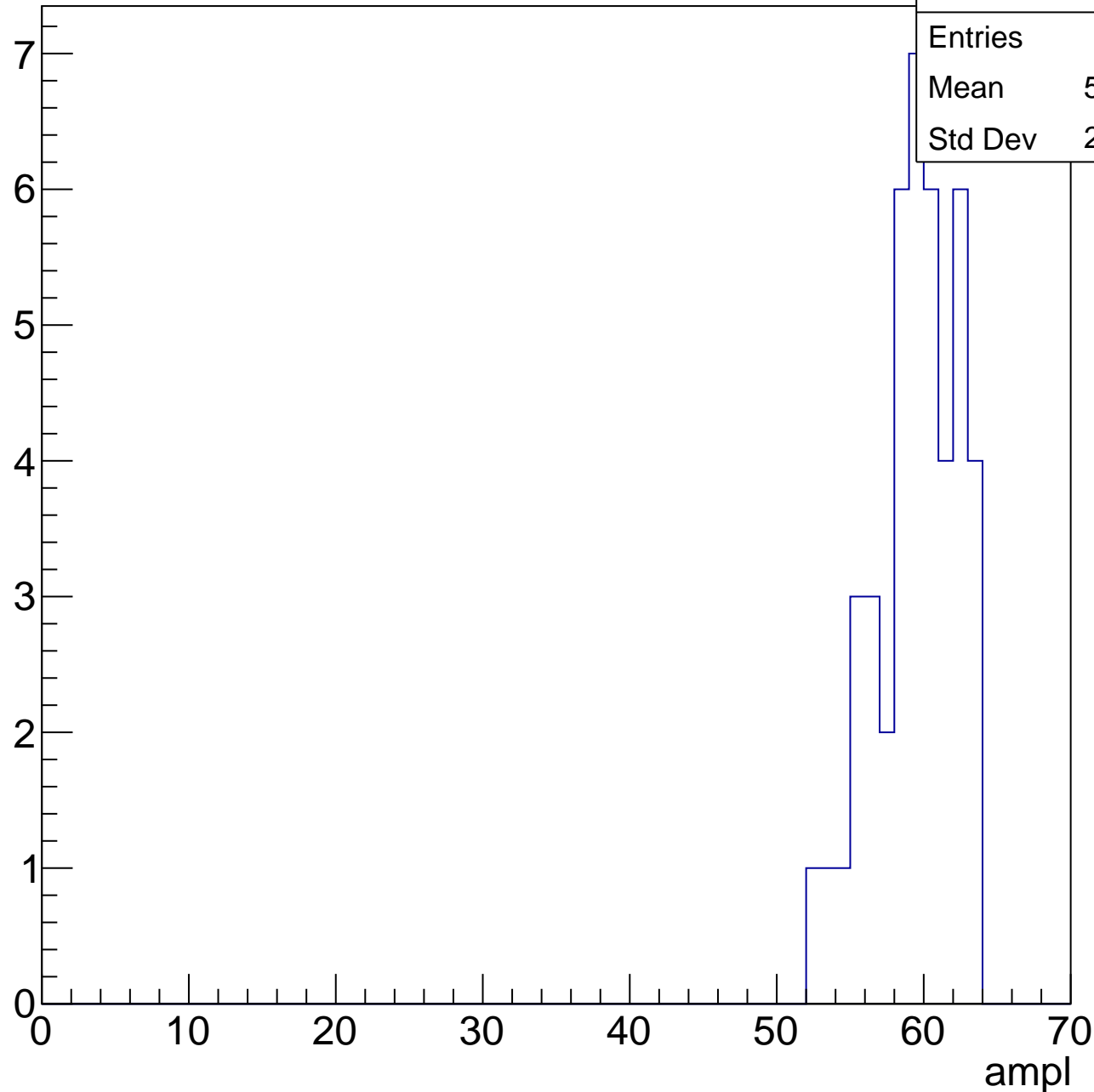
40

50

60

70

ampl

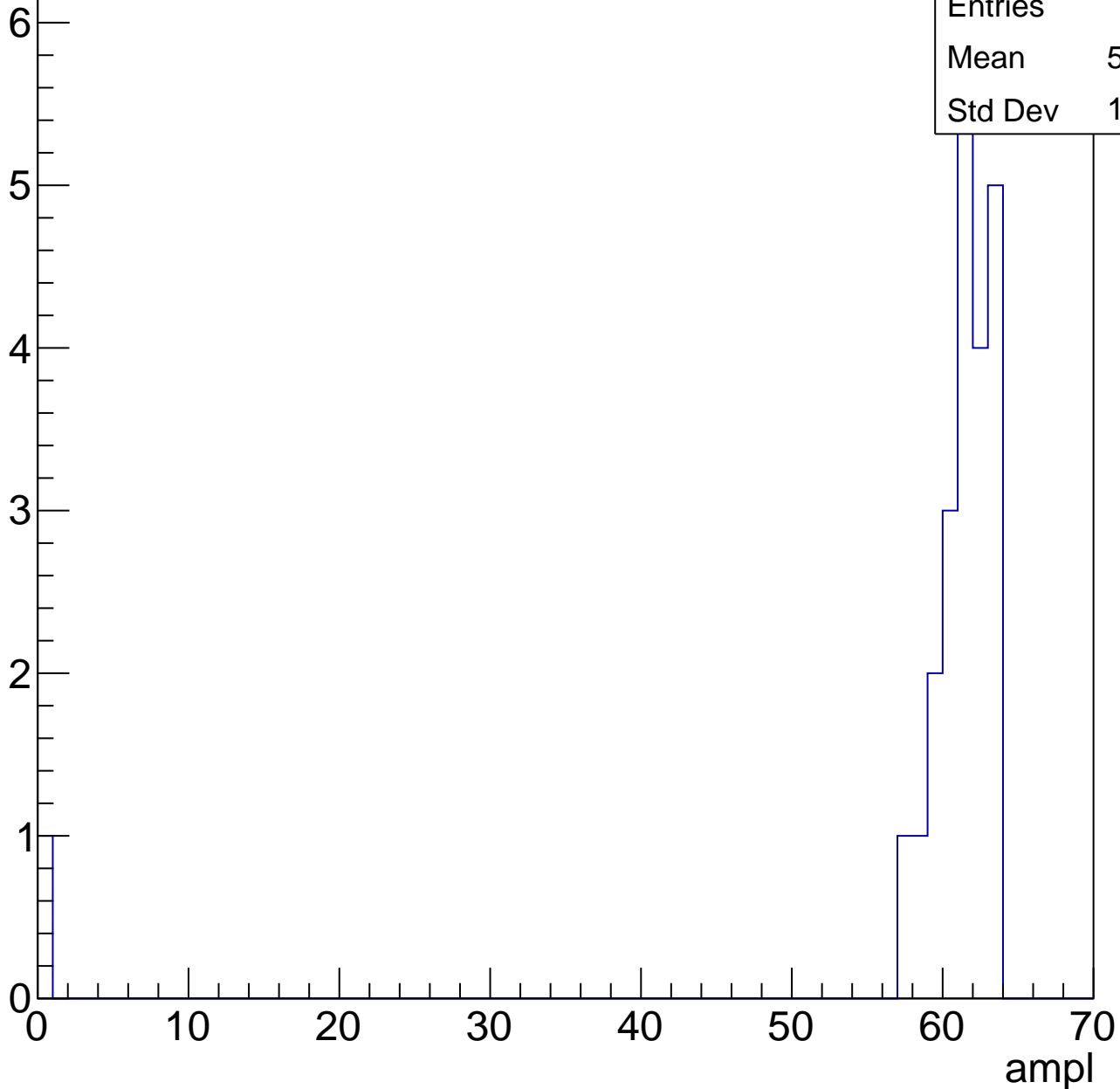


B1L100S, U15-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

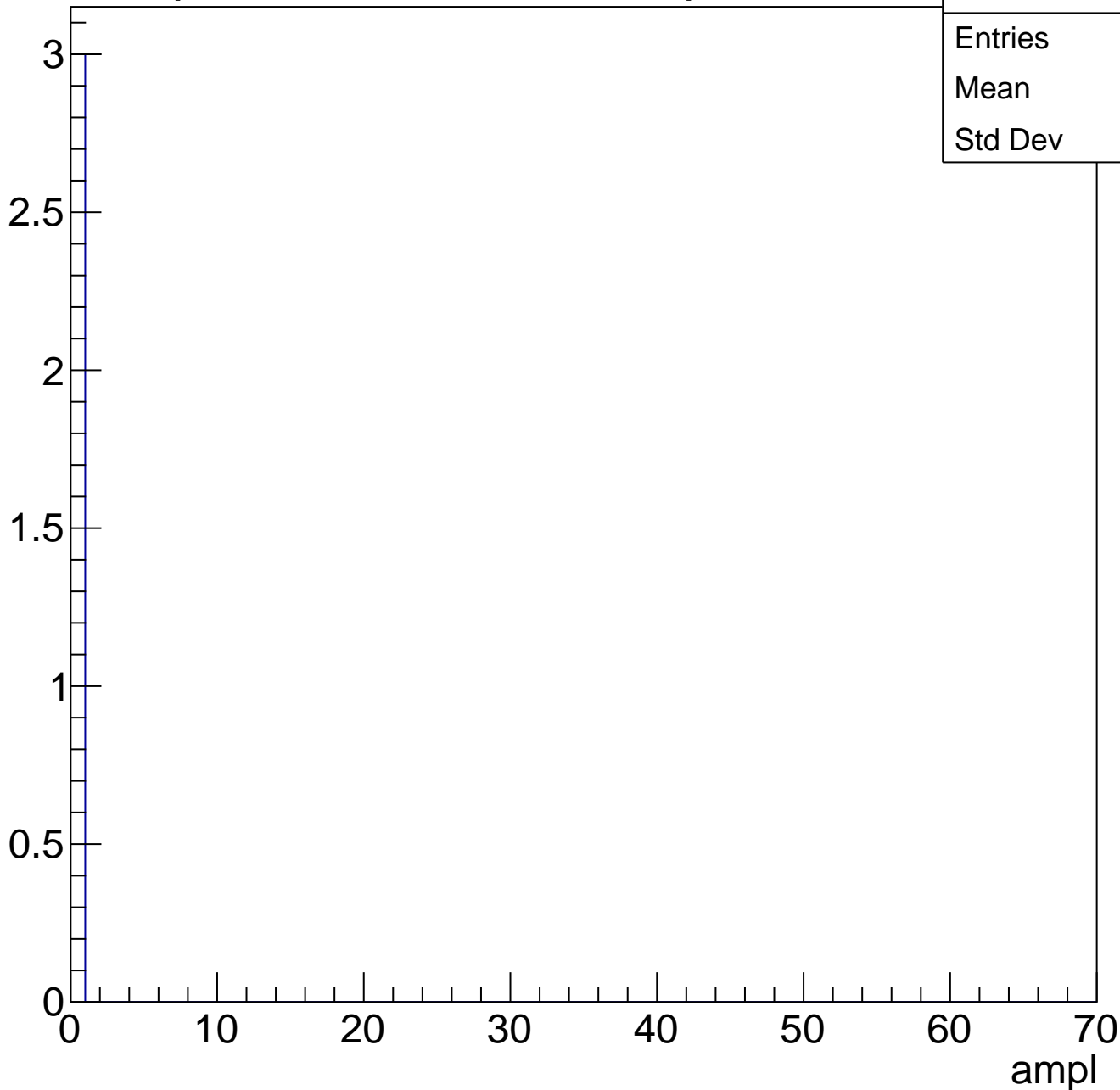
Entries	23
Mean	58.35
Std Dev	12.54



B1L100S, U15-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U15-ch15, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	30.32
Std Dev	3.526

Gaus mean : 30.6449

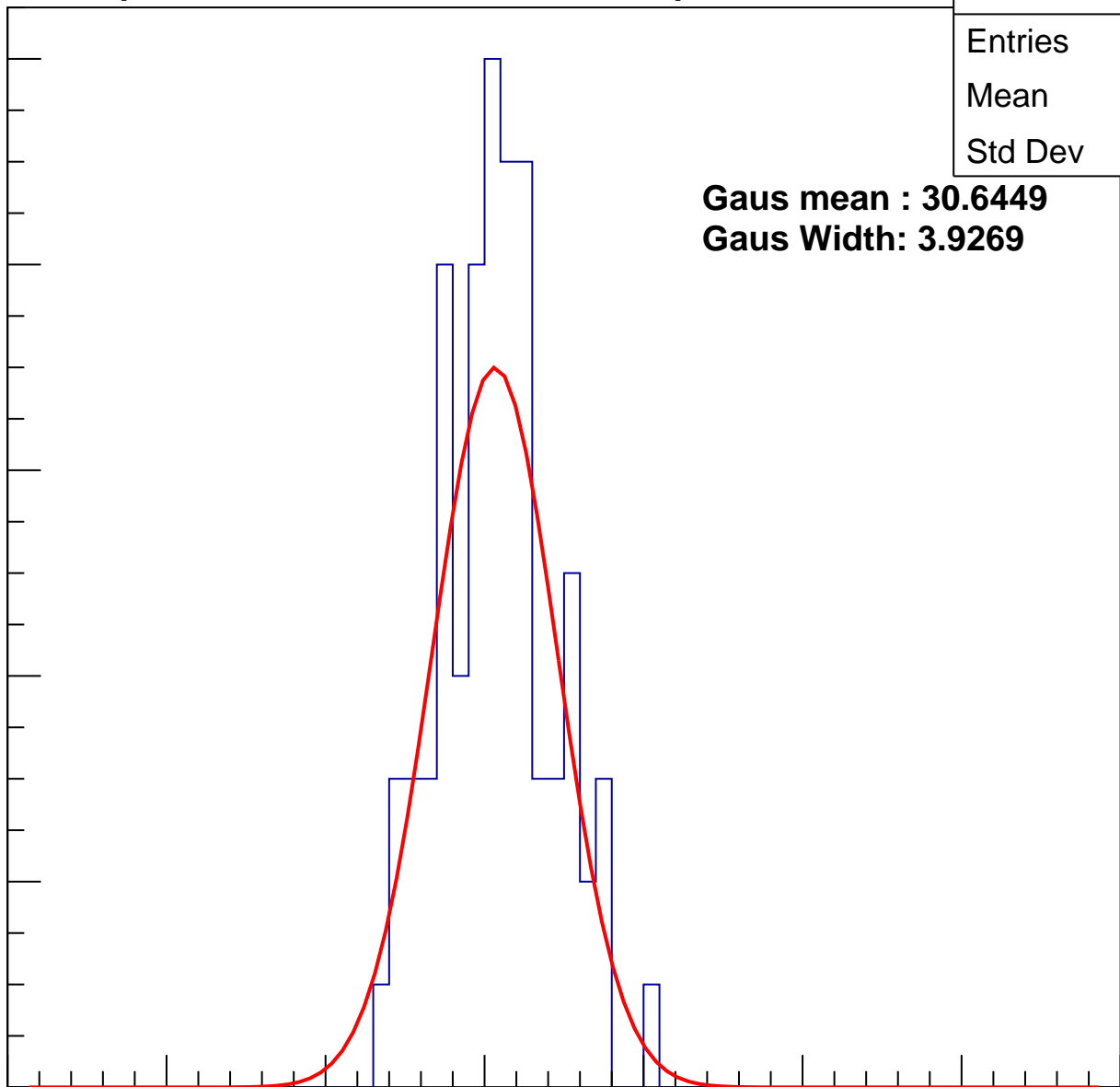
Gaus Width: 3.9269

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch15, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	37.28
Std Dev	3.318

Gaus mean : 38.3522

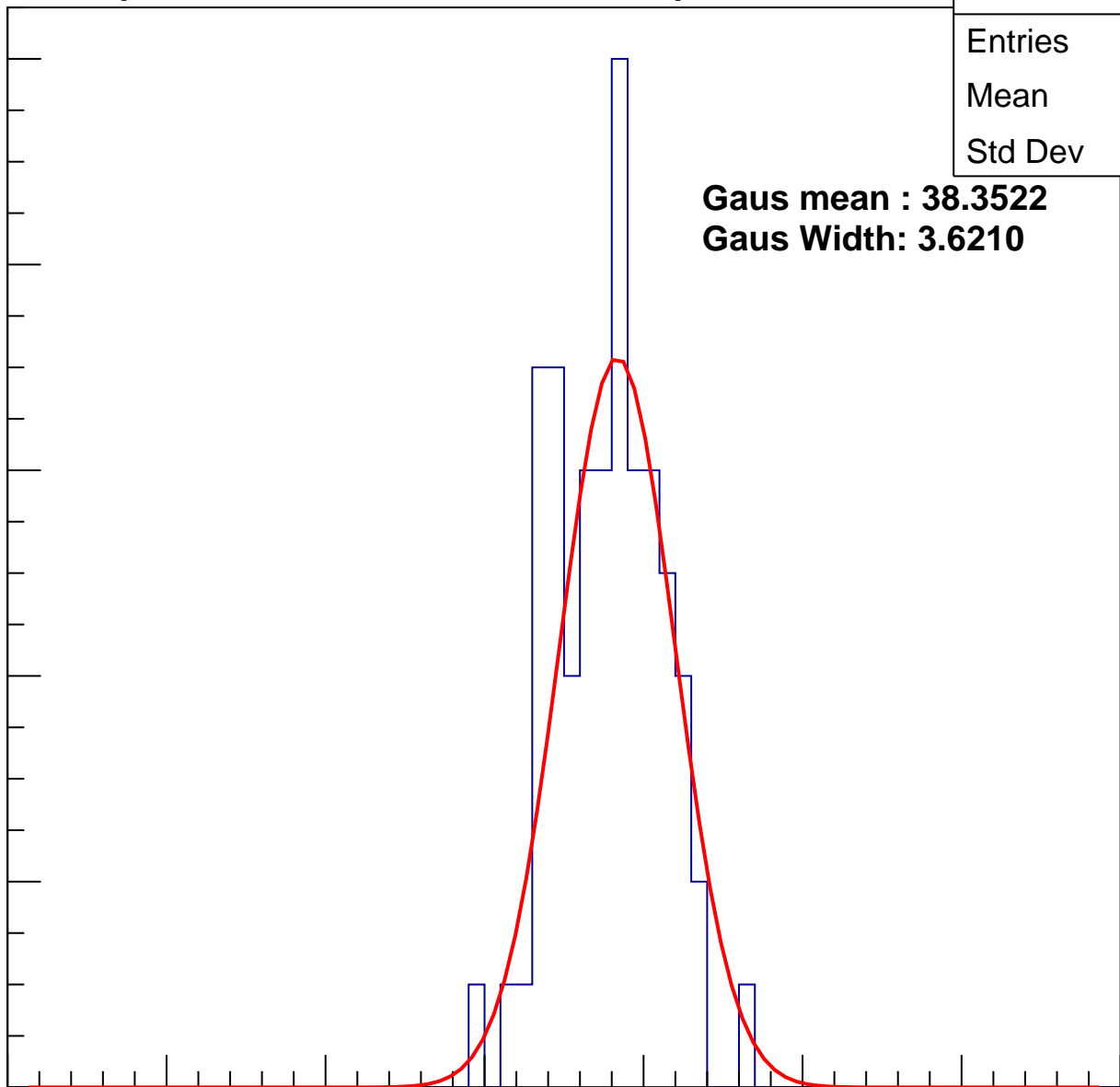
Gaus Width: 3.6210

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U15-ch15, adc2

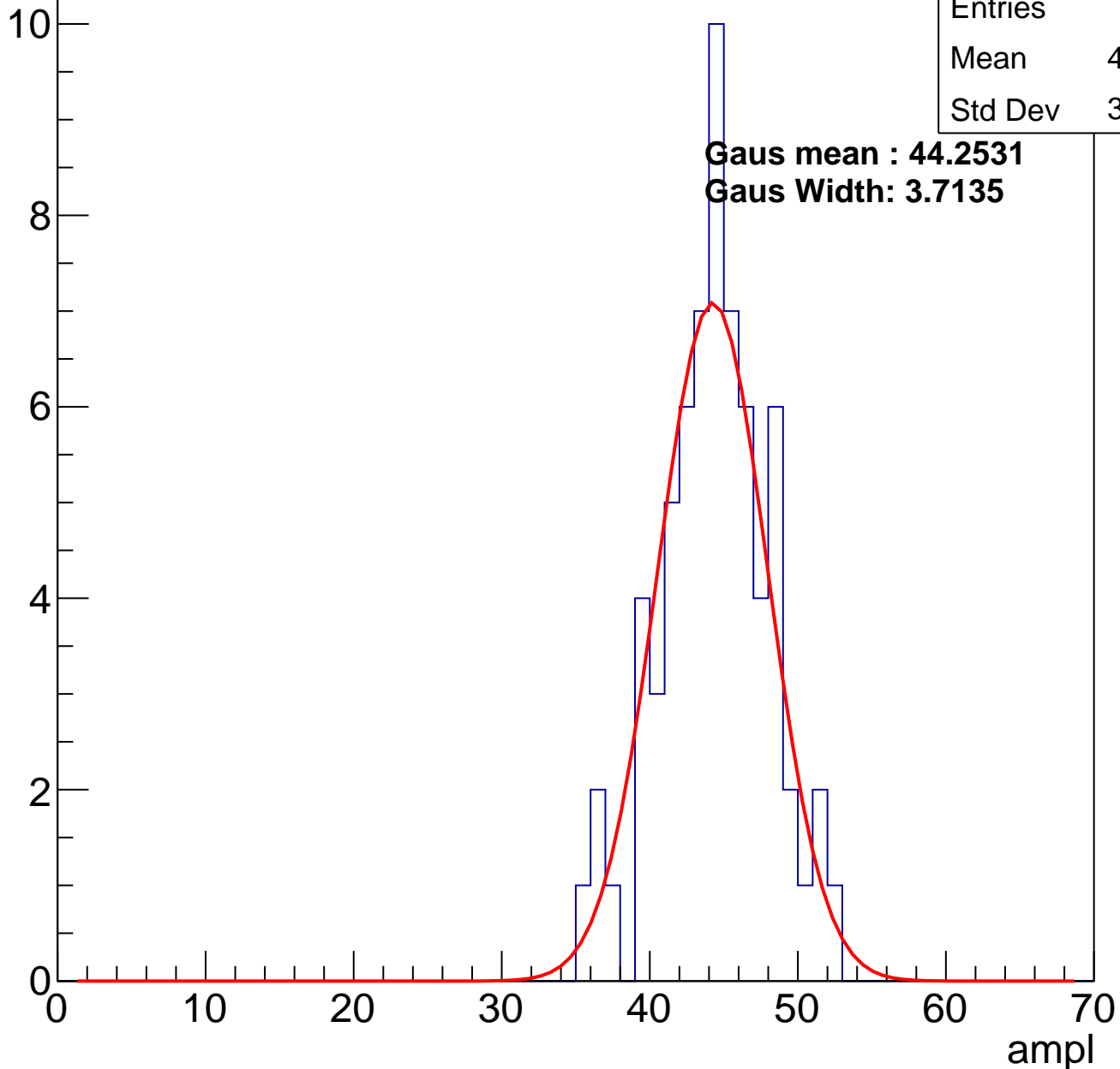
calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	43.93
Std Dev	3.623

Gaus mean : 44.2531

Gaus Width: 3.7135

Entry

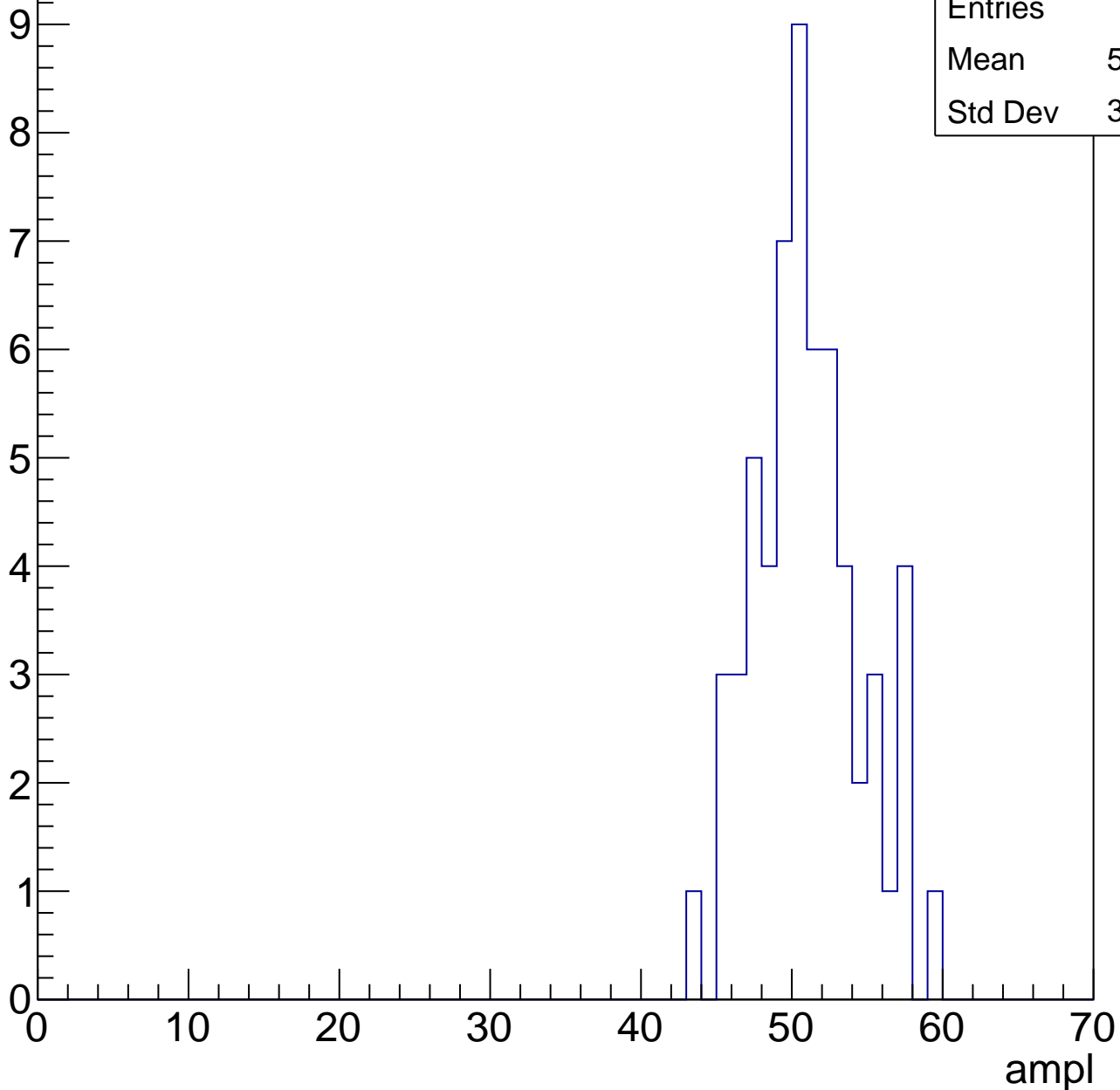


B1L100S, U15-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	50.54
Std Dev	3.466

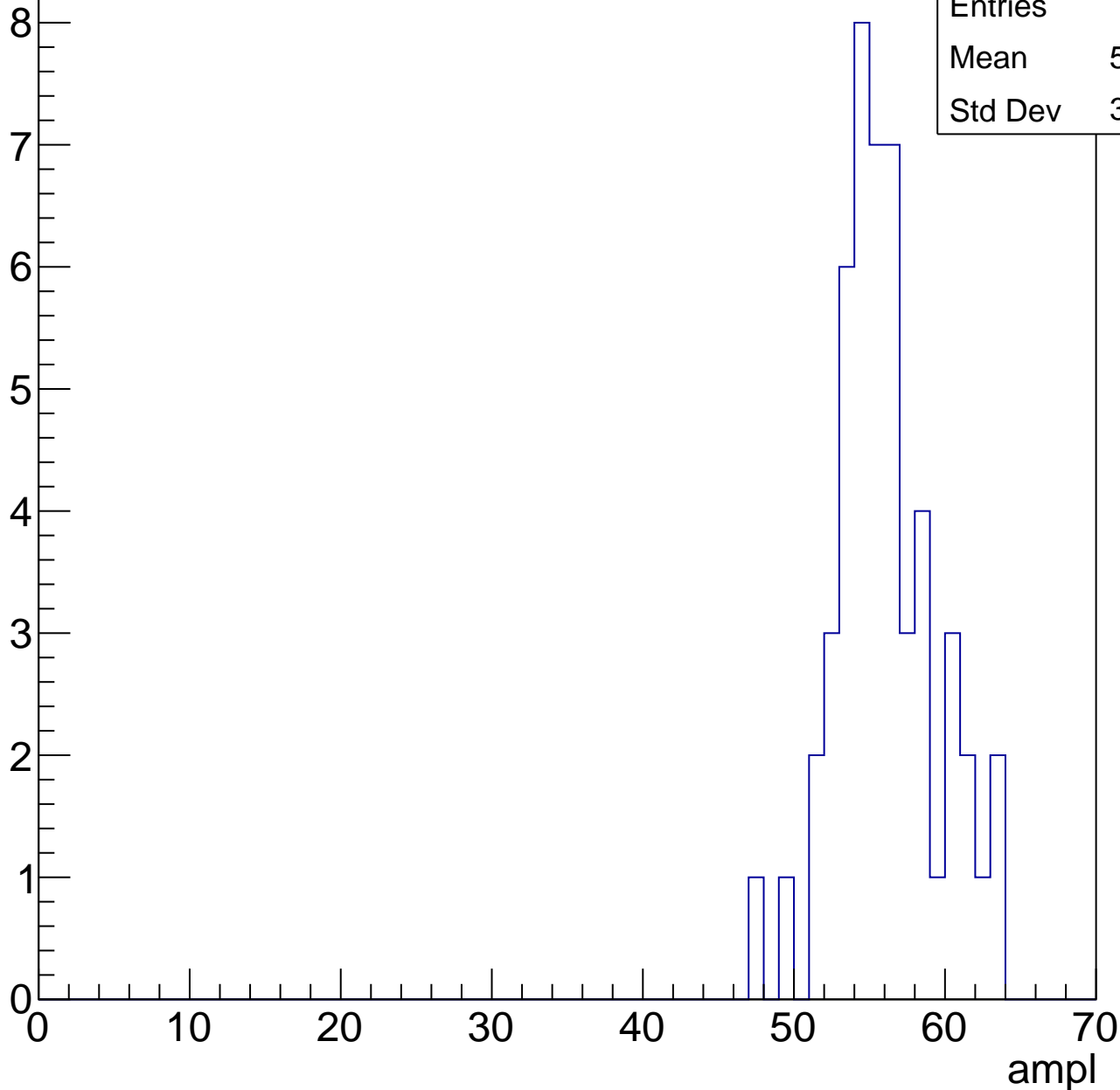


B1L100S, U15-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

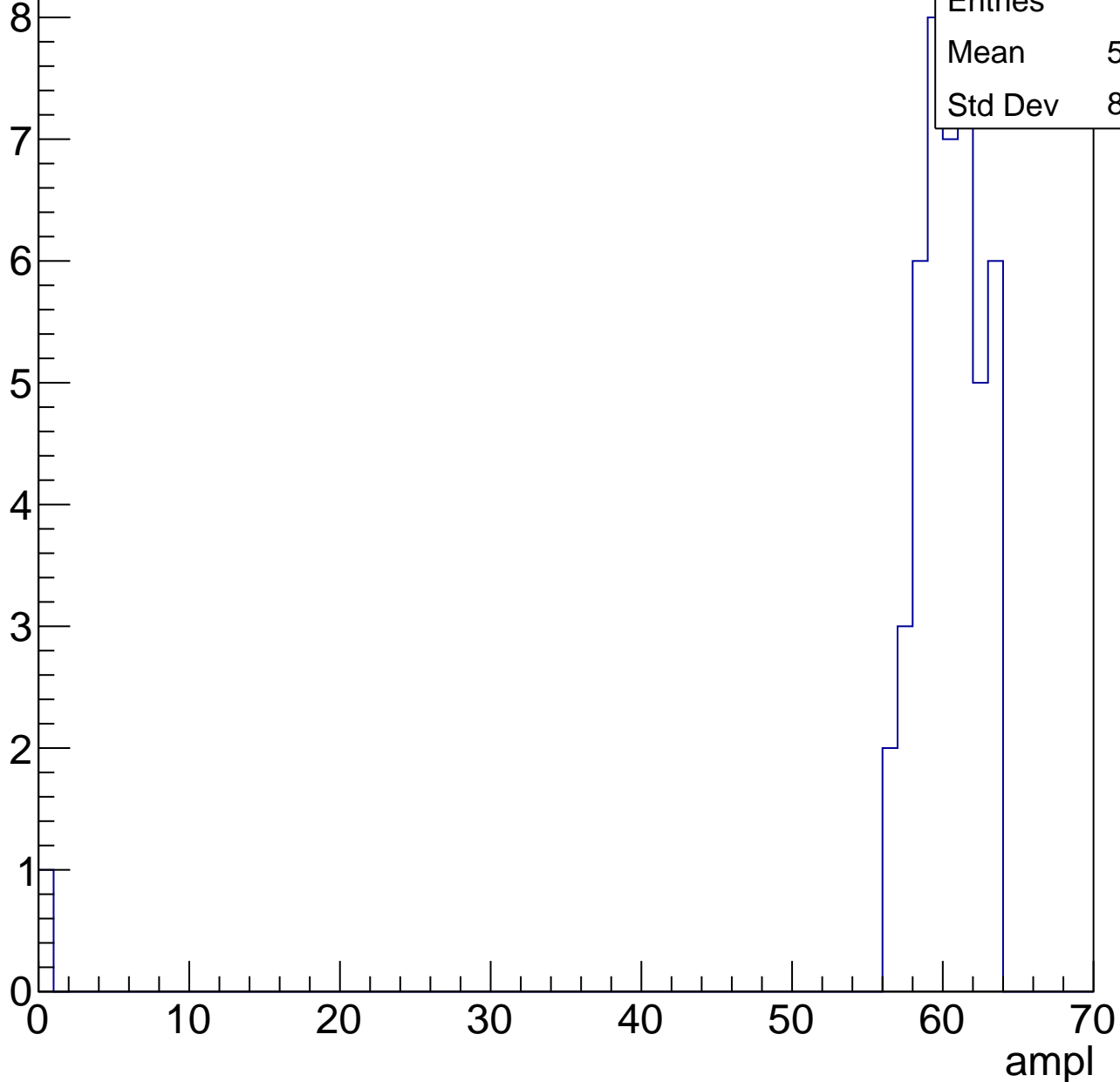
Entries	51
Mean	55.55
Std Dev	3.345



B1L100S, U15-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

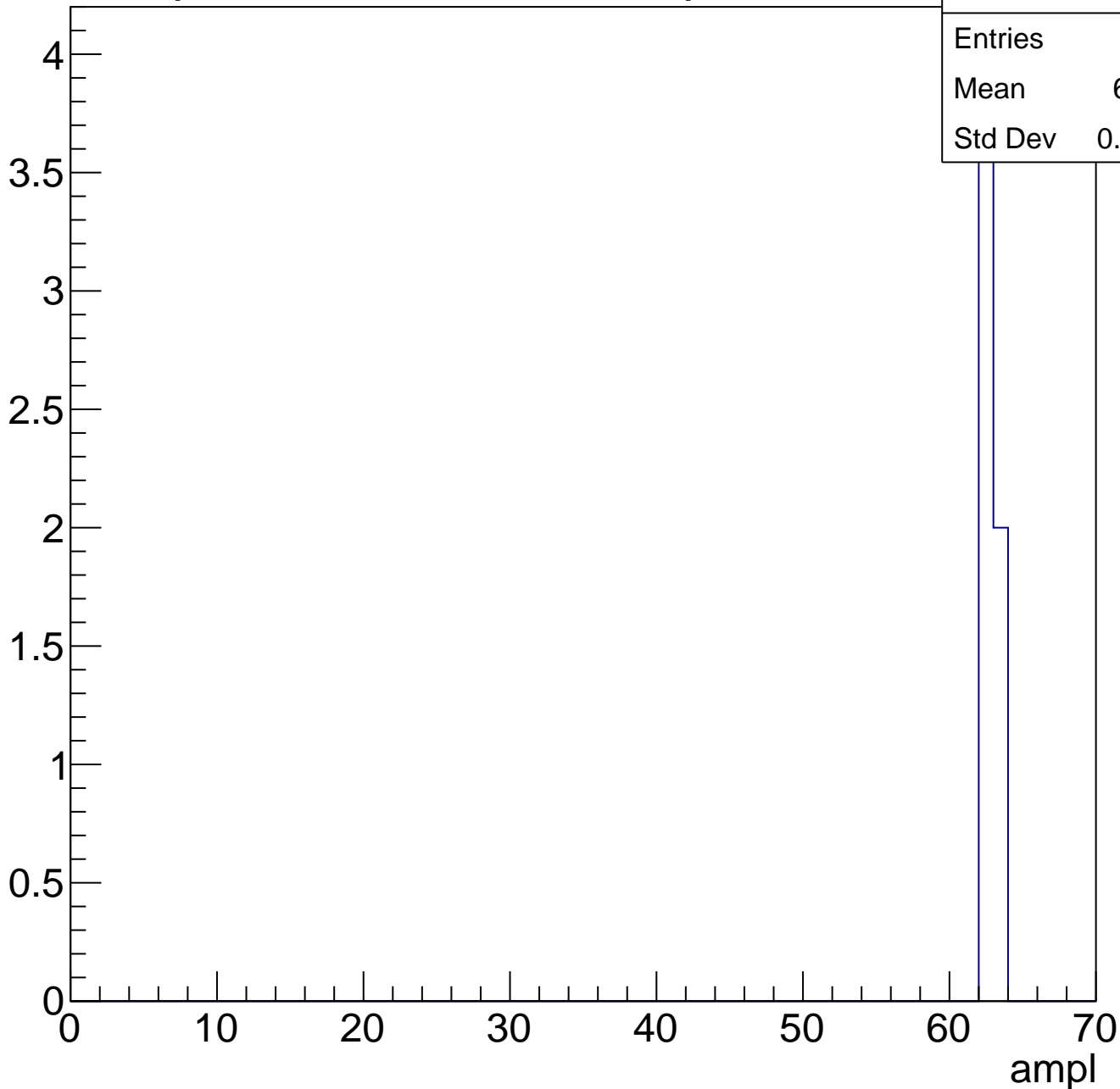
Entry



B1L100S, U15-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U15-ch16, adc0

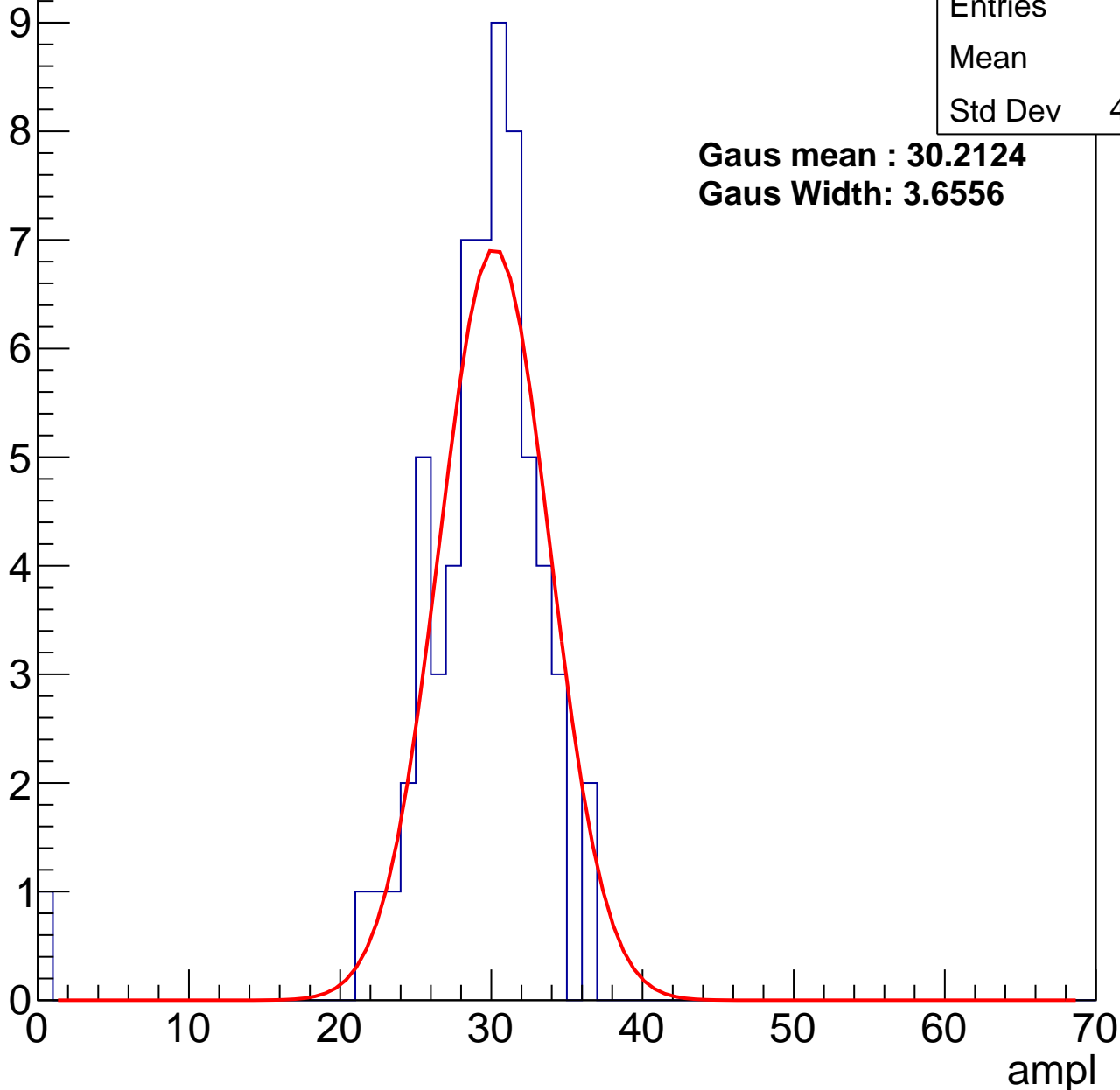
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.7
Std Dev	4.859

Gaus mean : 30.2124

Gaus Width: 3.6556



B1L100S, U15-ch16, adc1

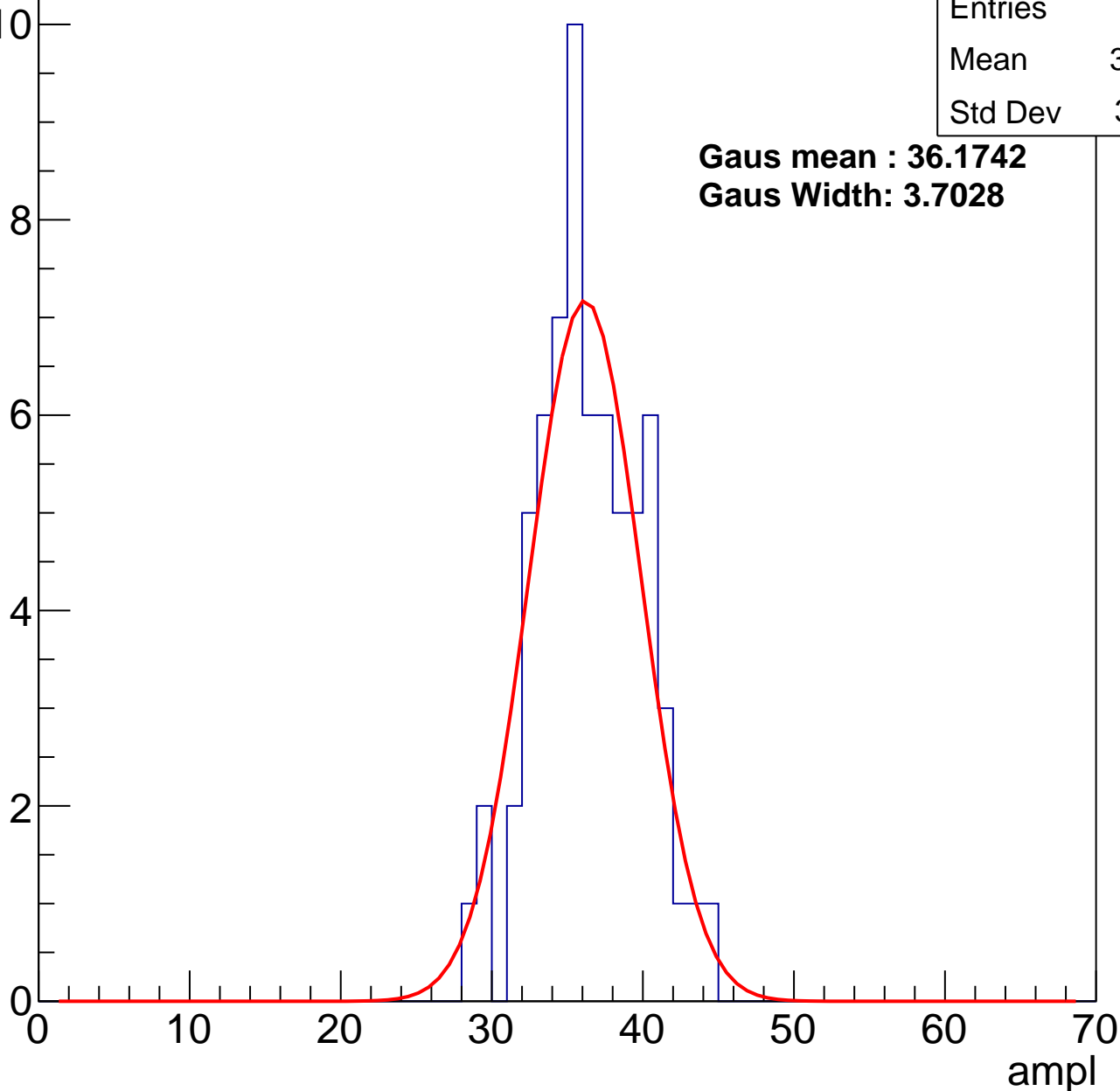
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.96
Std Dev	3.401

Gaus mean : 36.1742

Gaus Width: 3.7028



B1L100S, U15-ch16, adc2

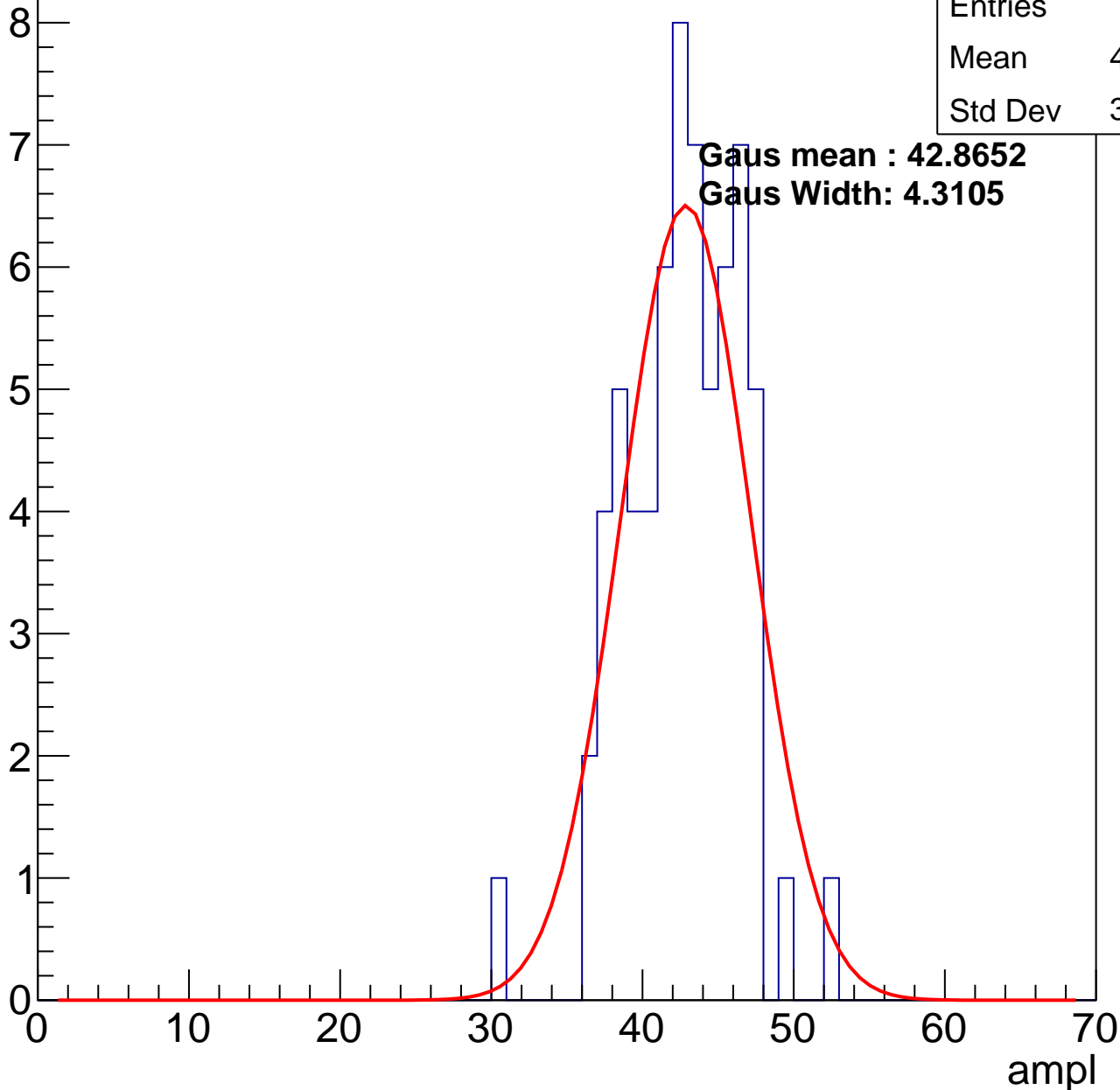
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.23
Std Dev	3.729

Gaus mean : 42.8652

Gaus Width: 4.3105



B1L100S, U15-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	49.19
Std Dev	3.718

Entry

10

8

6

4

2

0

0

10

20

30

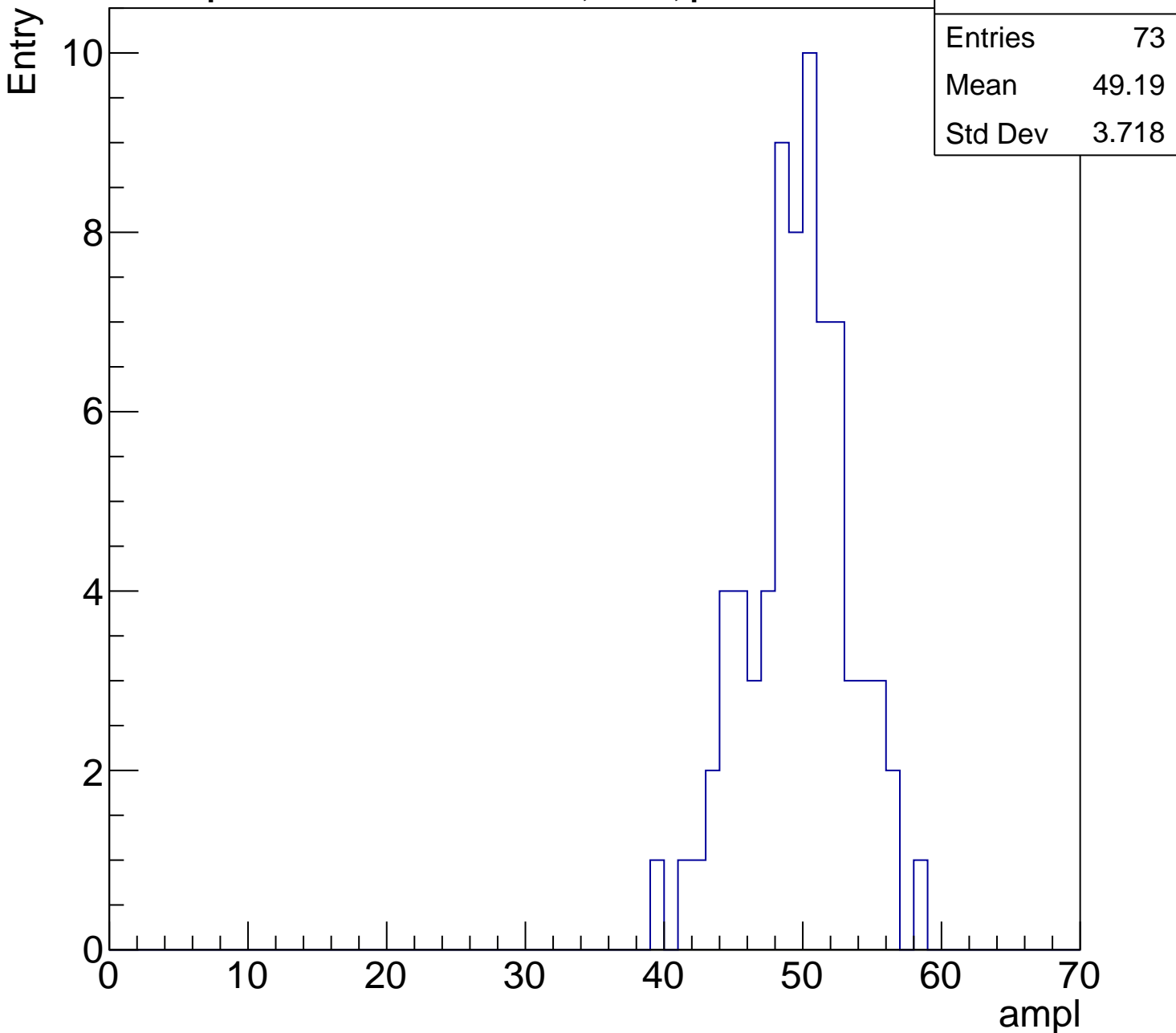
40

50

60

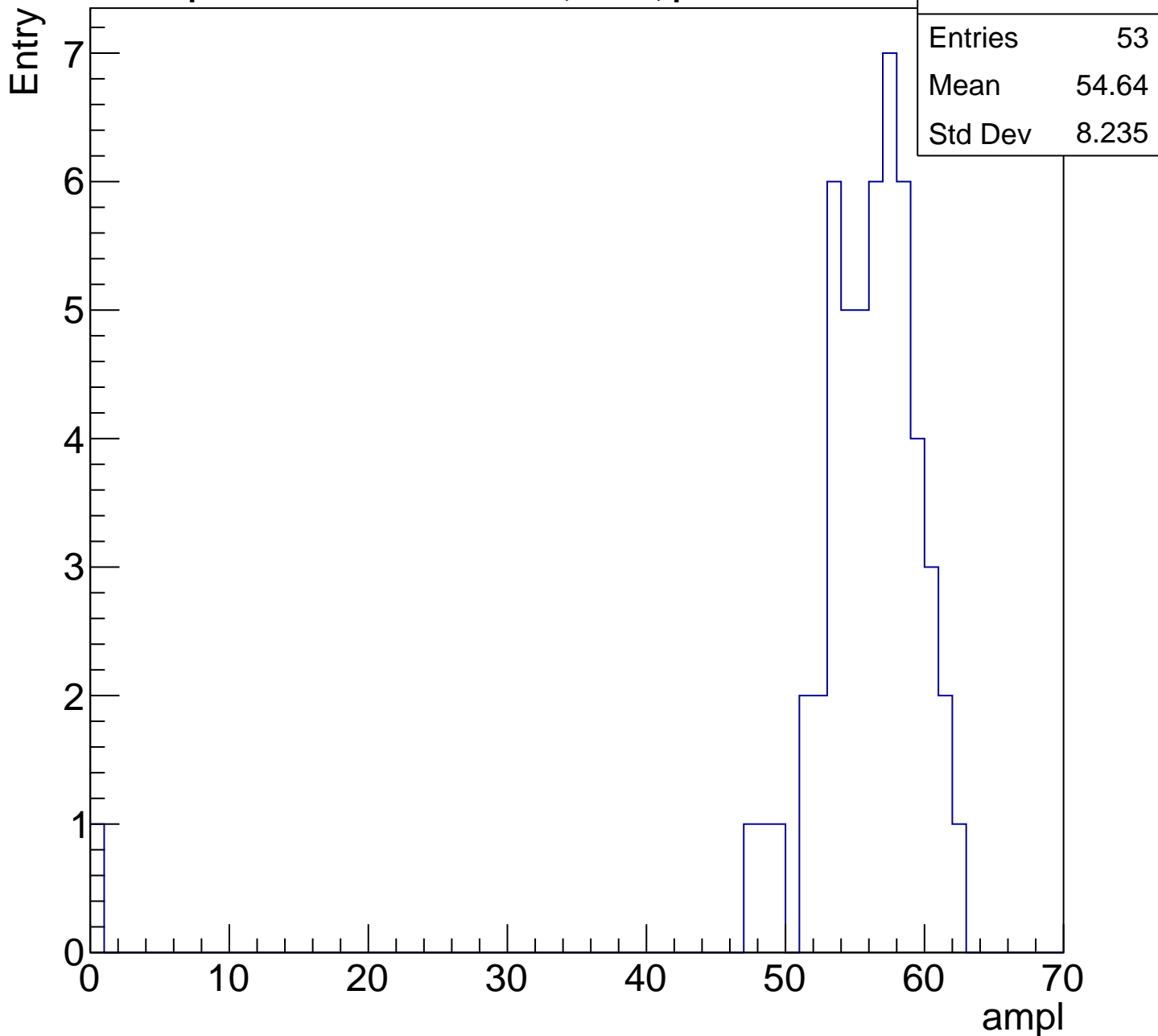
70

ampl



B1L100S, U15-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

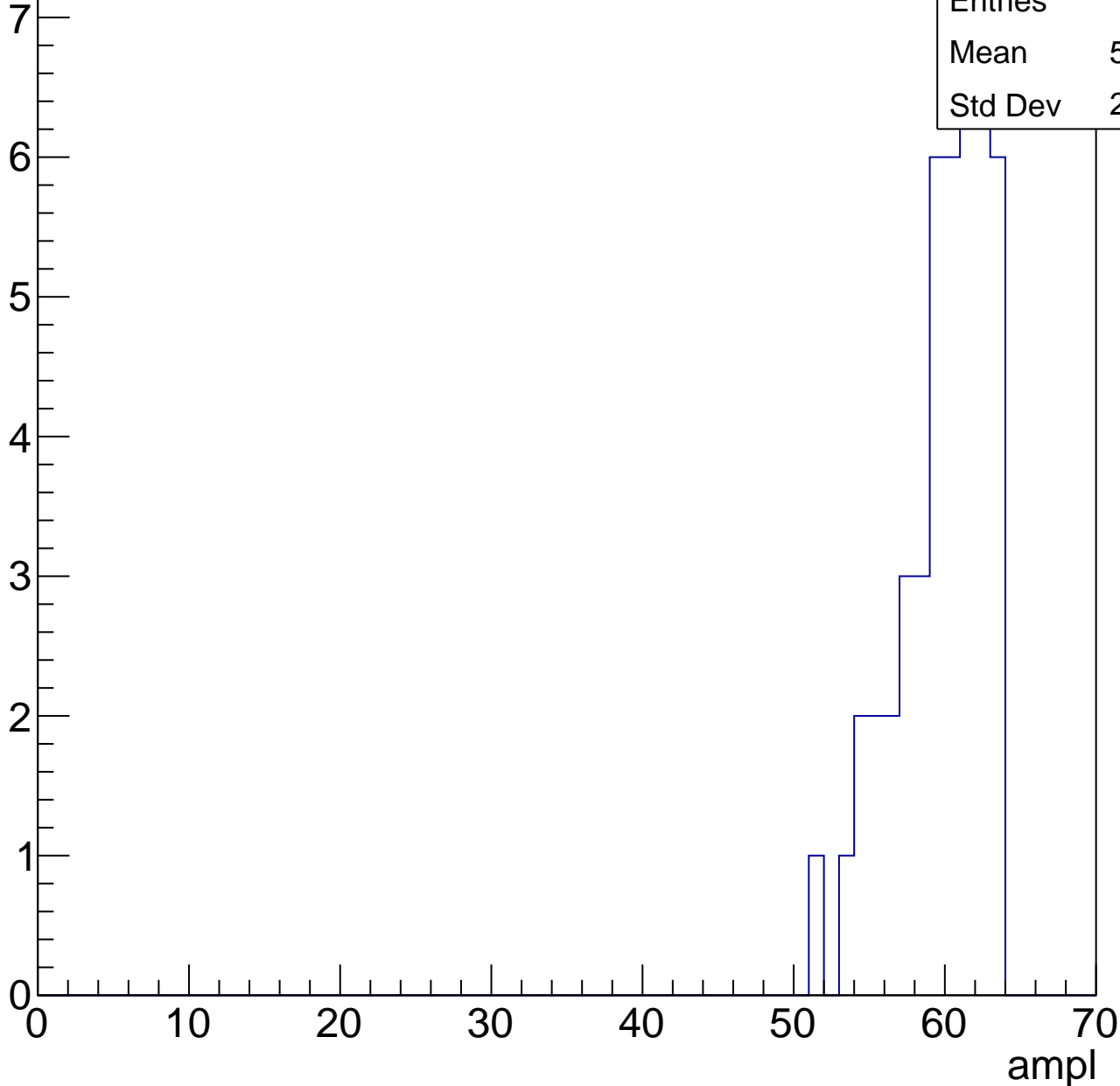


B1L100S, U15-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.39
Std Dev	2.952



B1L100S, U15-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch17, adc0

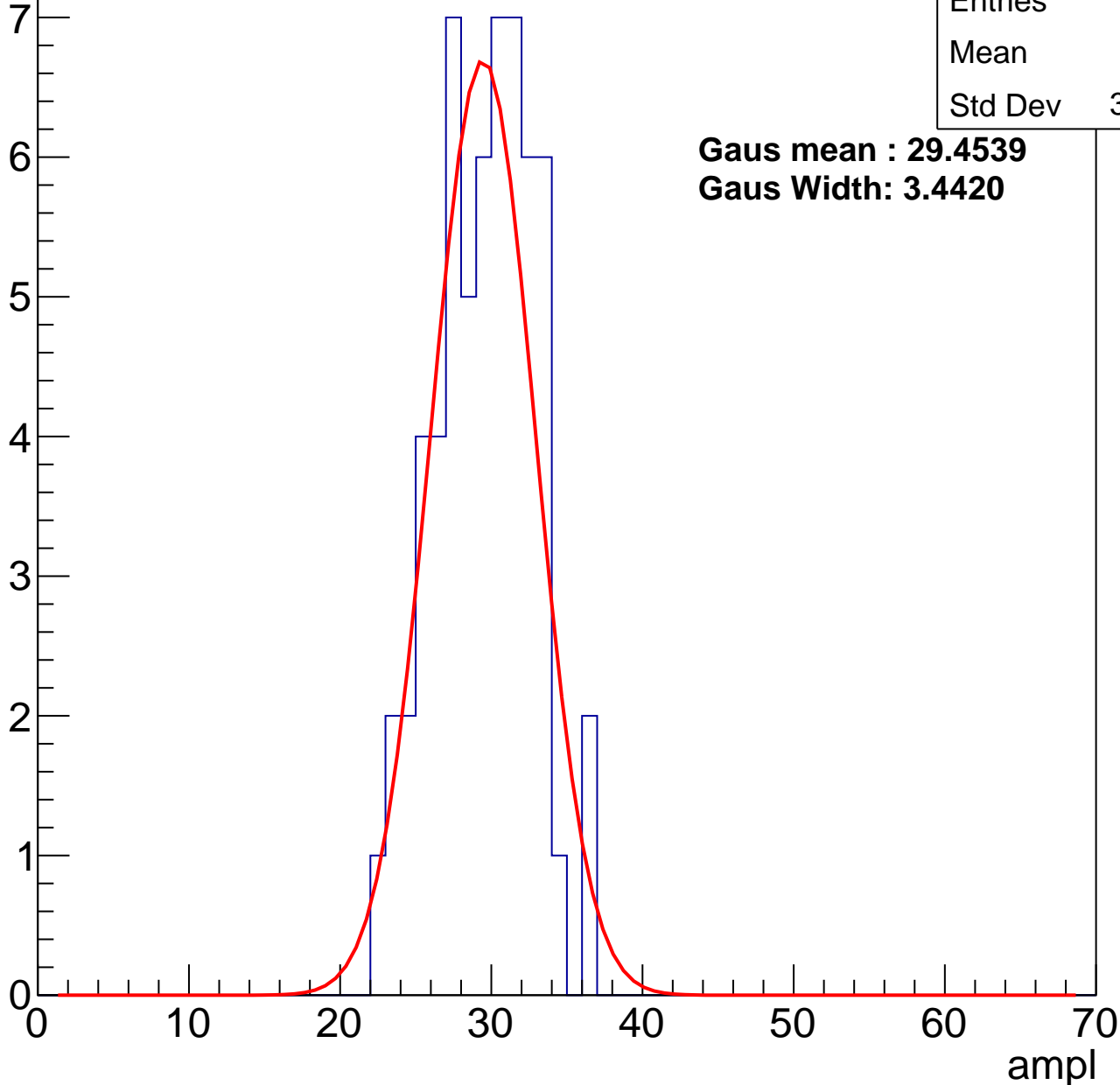
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	29.1
Std Dev	3.192

Gaus mean : 29.4539

Gaus Width: 3.4420



B1L100S, U15-ch17, adc1

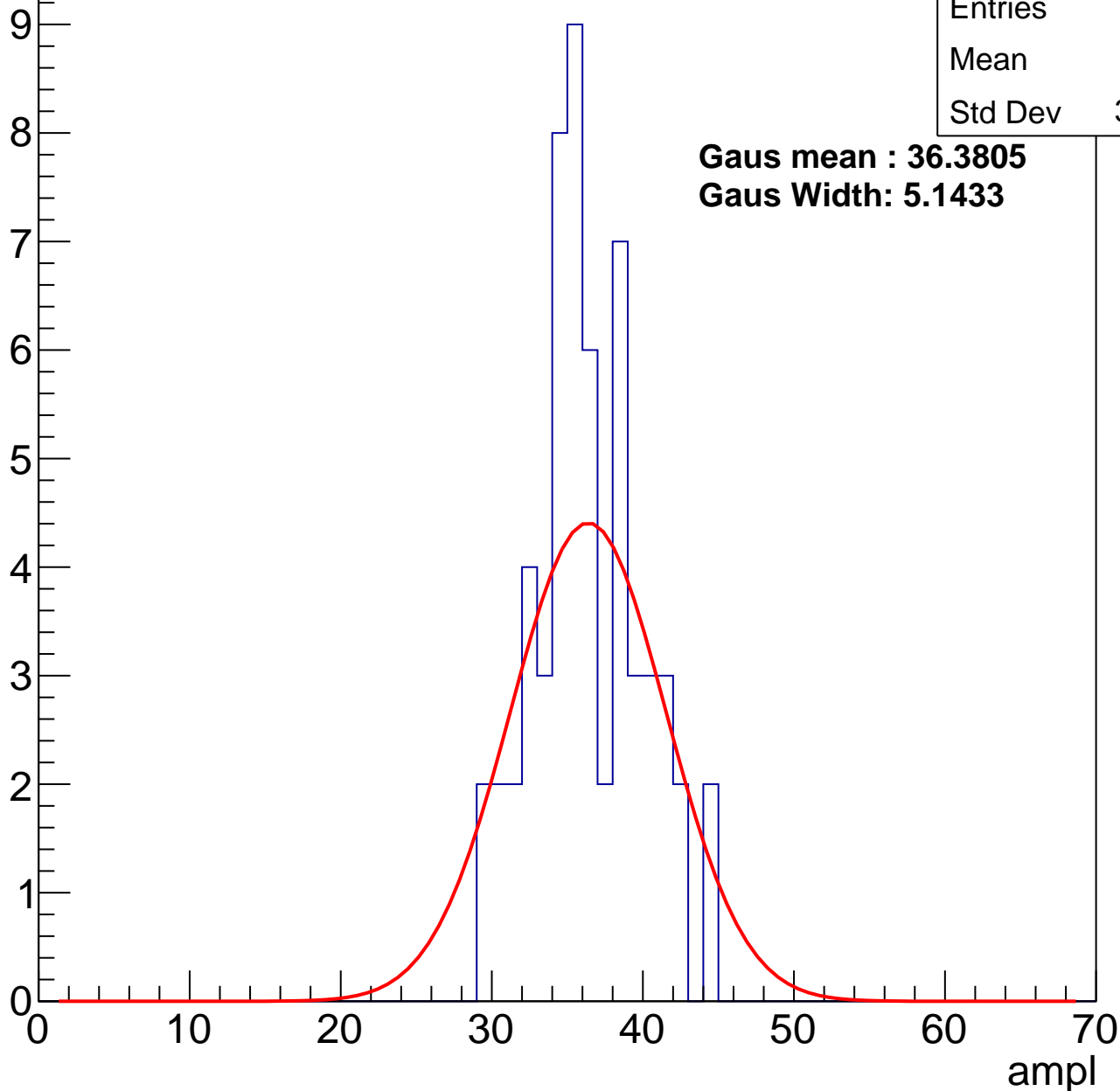
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	35.9
Std Dev	3.551

Gaus mean : 36.3805

Gaus Width: 5.1433



B1L100S, U15-ch17, adc2

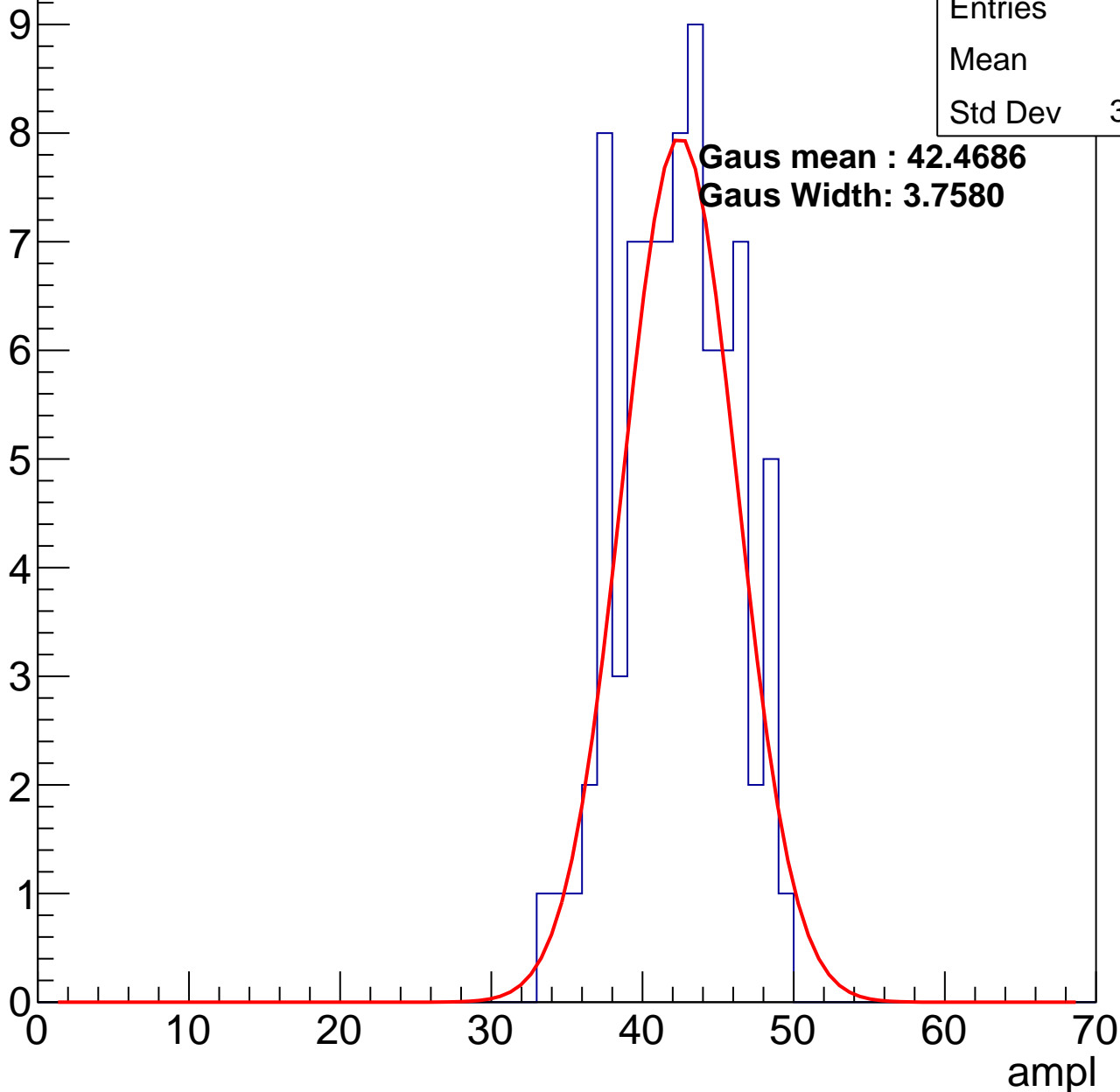
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	41.8
Std Dev	3.663

Gaus mean : 42.4686

Gaus Width: 3.7580

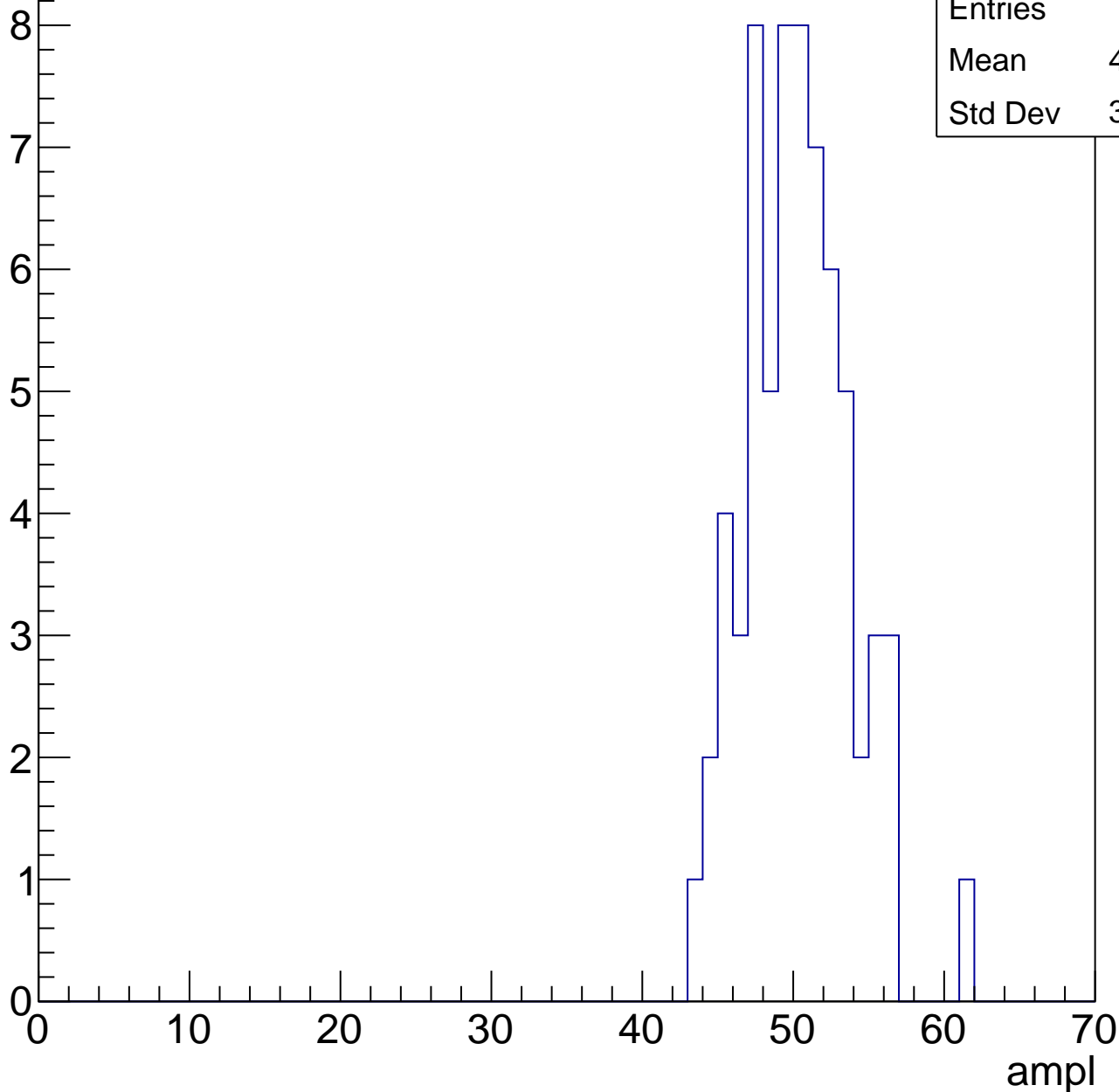


B1L100S, U15-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.89
Std Dev	3.438

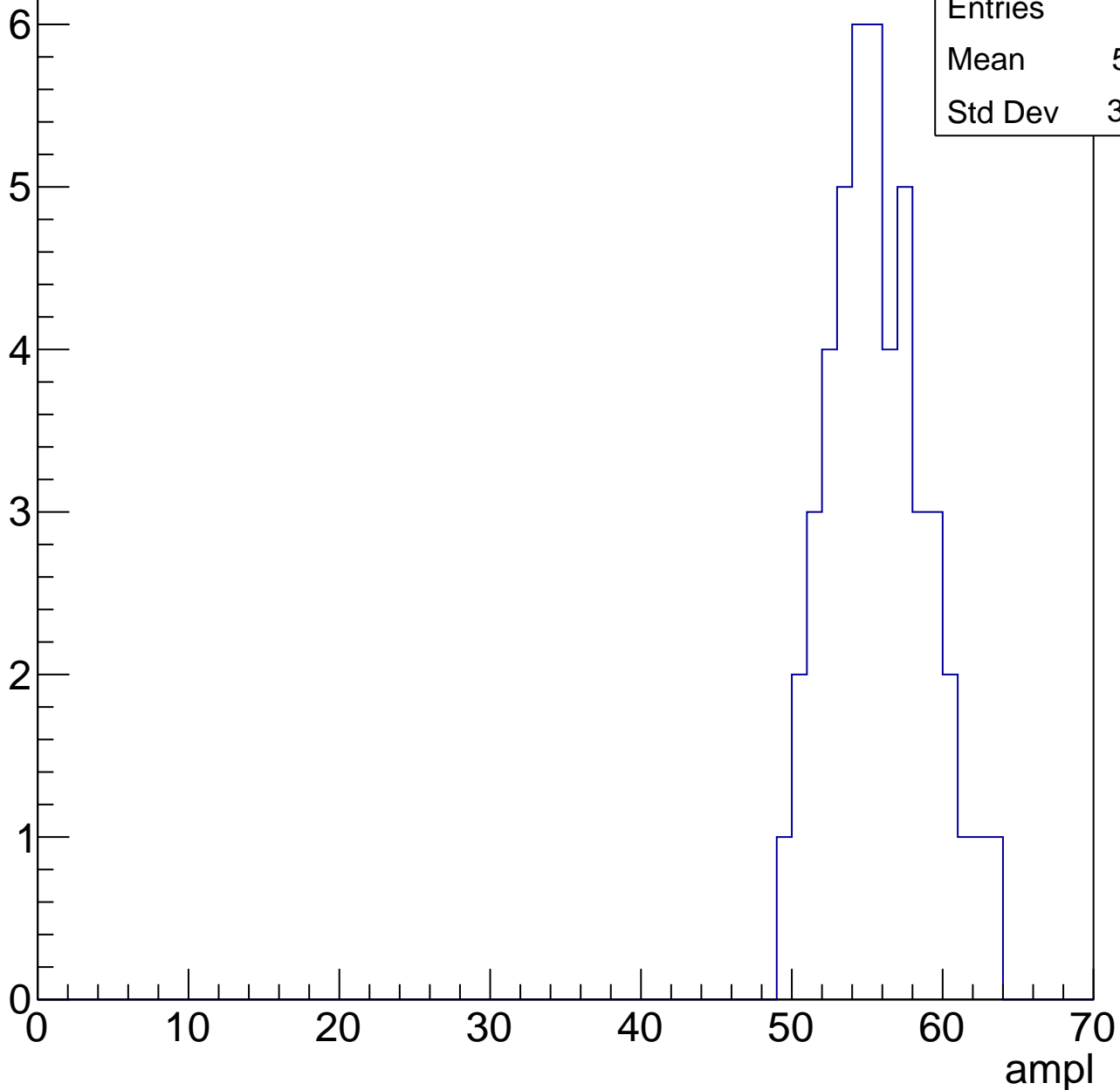


B1L100S, U15-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	55.21
Std Dev	3.235



B1L100S, U15-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

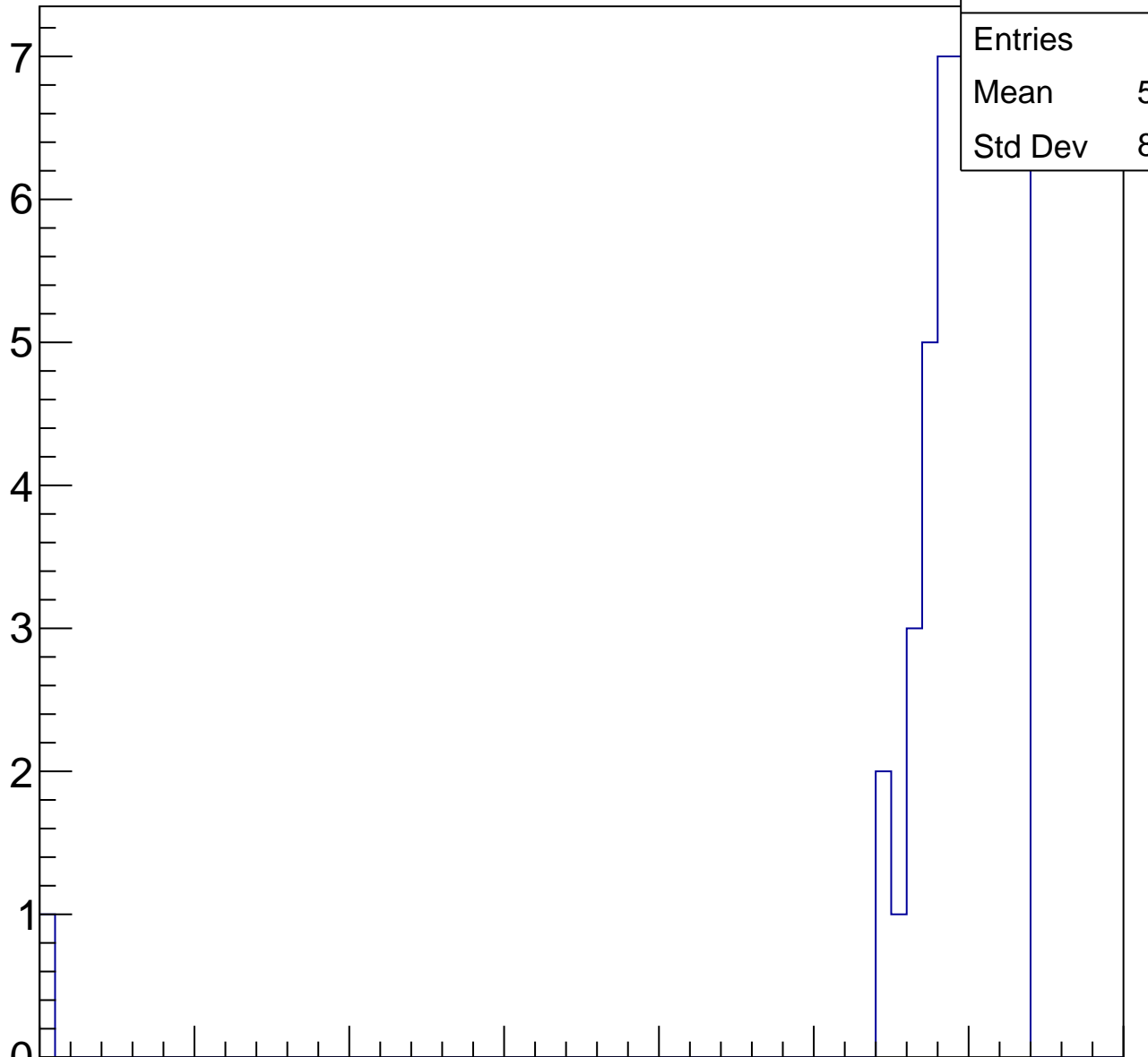
Entry

7
6
5
4
3
2
1
0

Entries	54
Mean	58.46
Std Dev	8.384

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch18, adc0

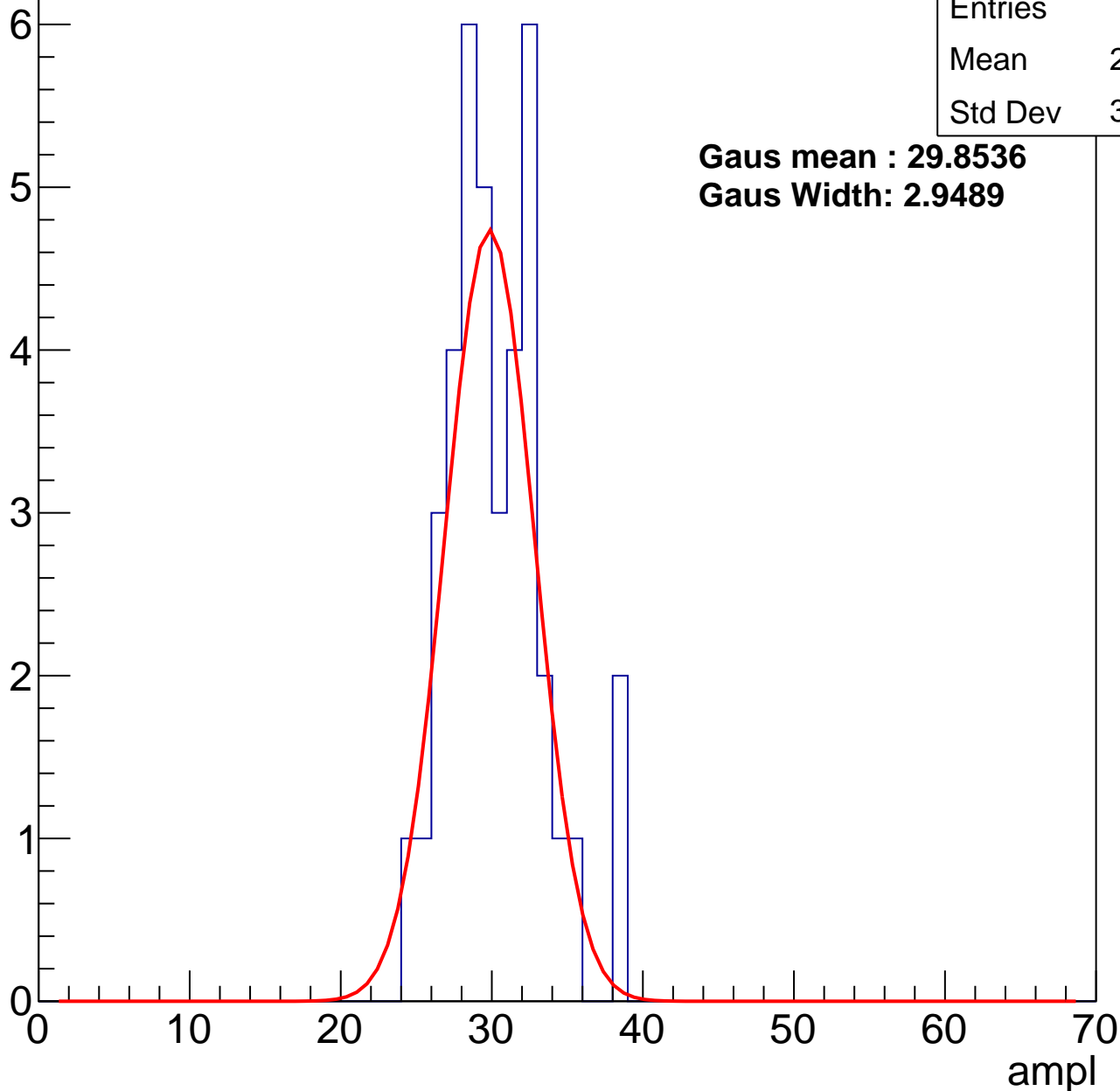
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	29.87
Std Dev	3.156

Gaus mean : 29.8536

Gaus Width: 2.9489



B1L100S, U15-ch18, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	35.43
Std Dev	3.696

Gaus mean : 36.0377

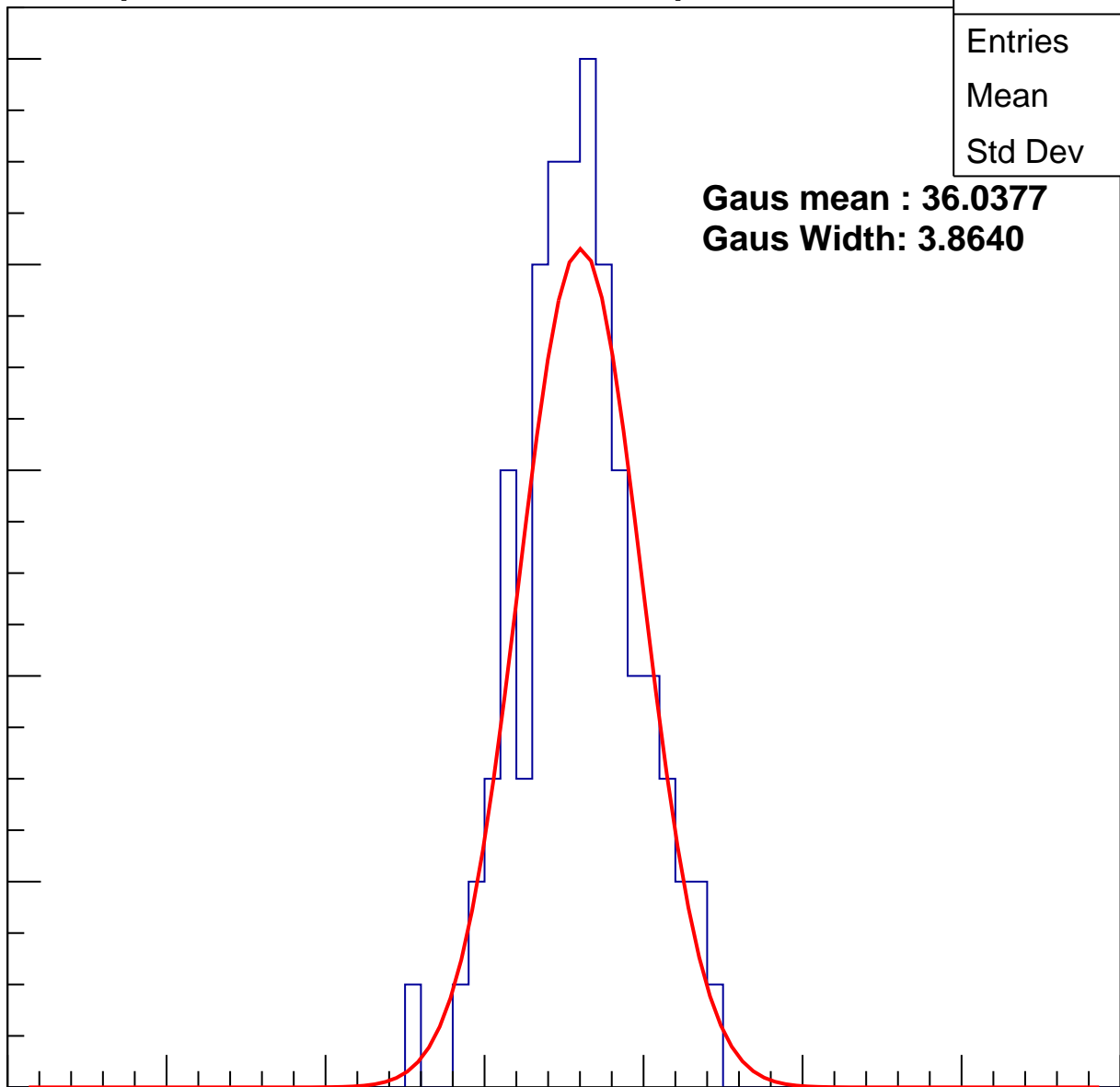
Gaus Width: 3.8640

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch18, adc2

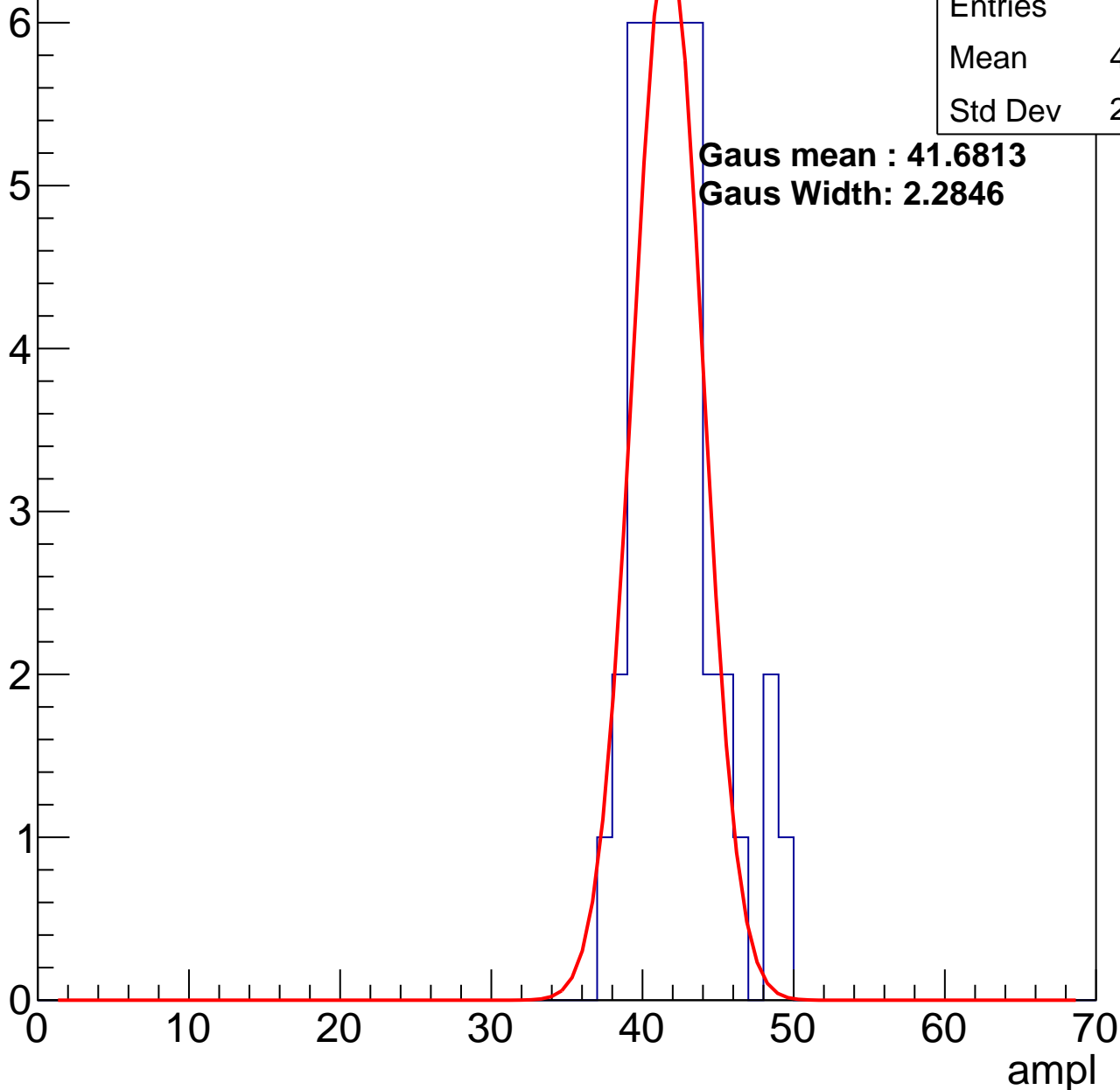
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	41.76
Std Dev	2.739

Gaus mean : 41.6813

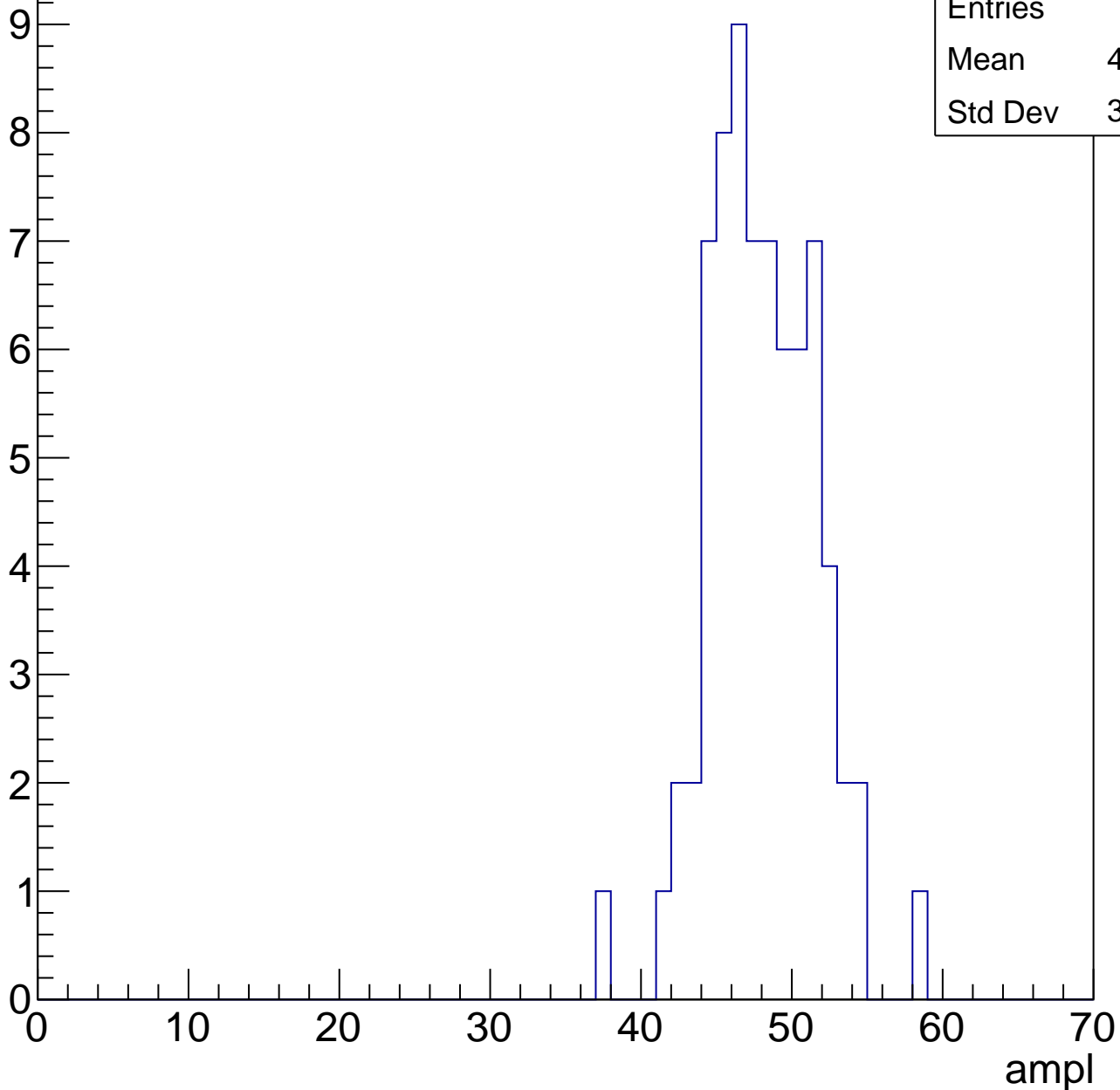
Gaus Width: 2.2846



B1L100S, U15-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



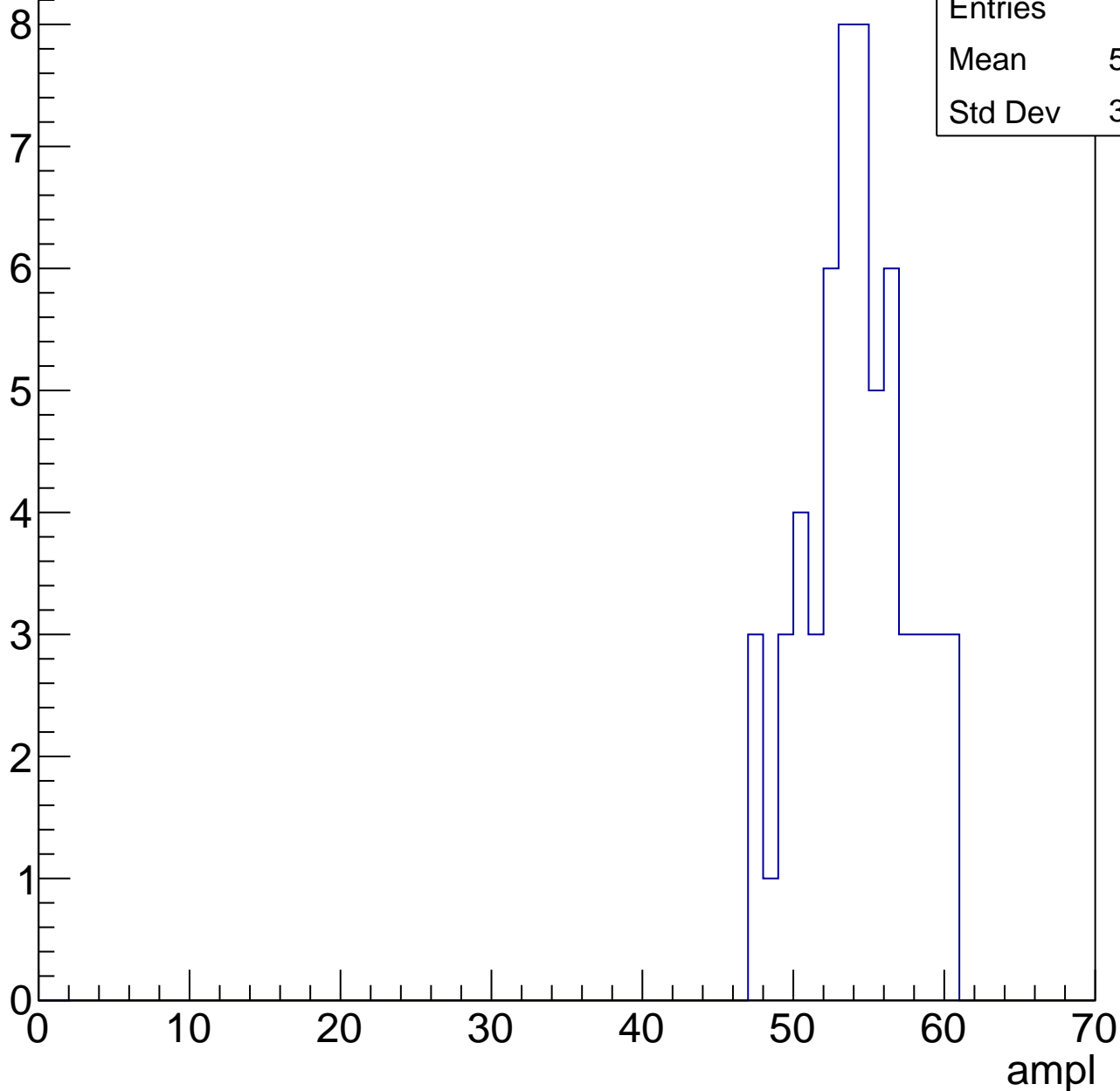
Entries	72
Mean	47.58
Std Dev	3.507

B1L100S, U15-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

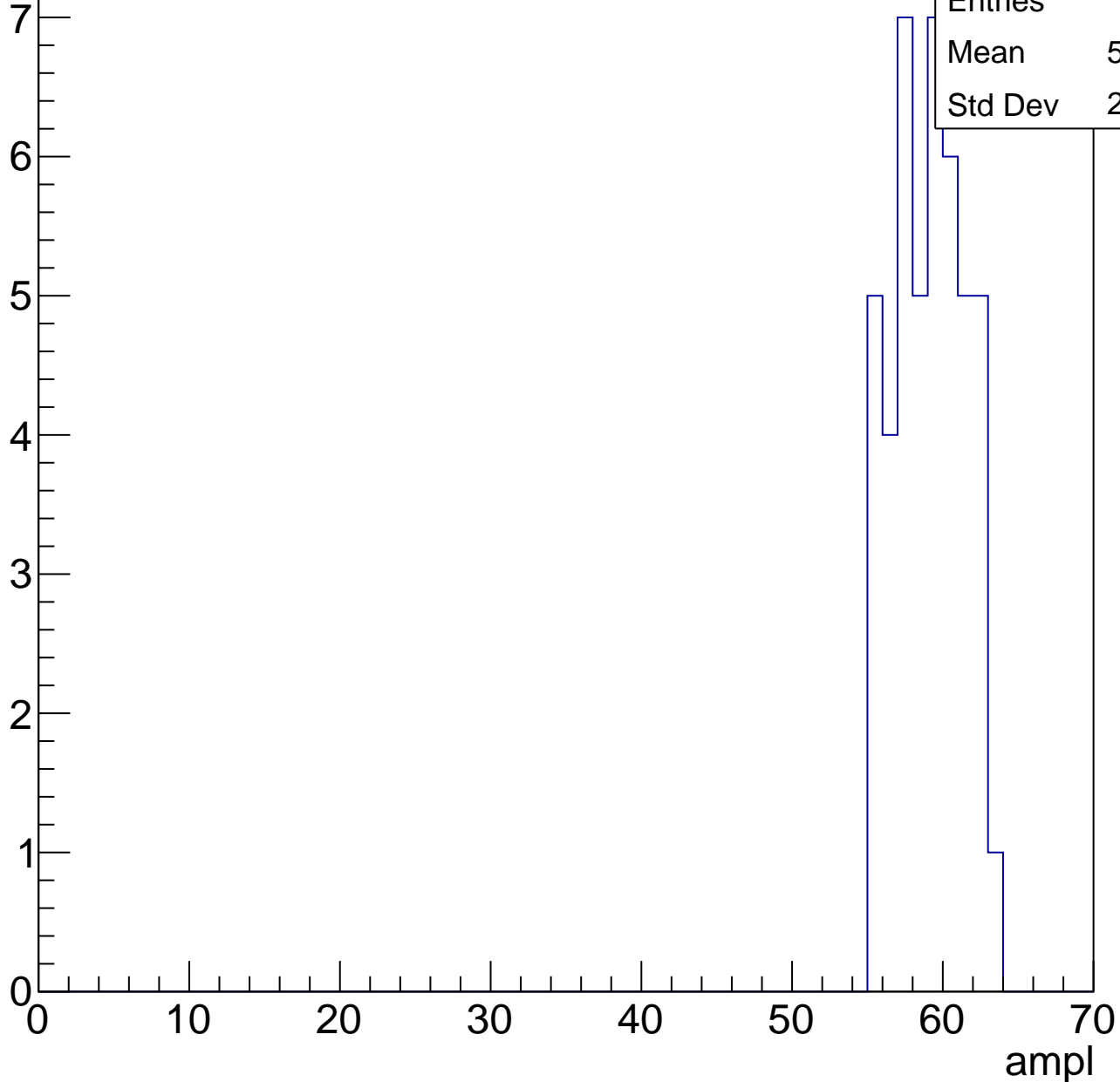
Entries	59
Mean	53.73
Std Dev	3.354



B1L100S, U15-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

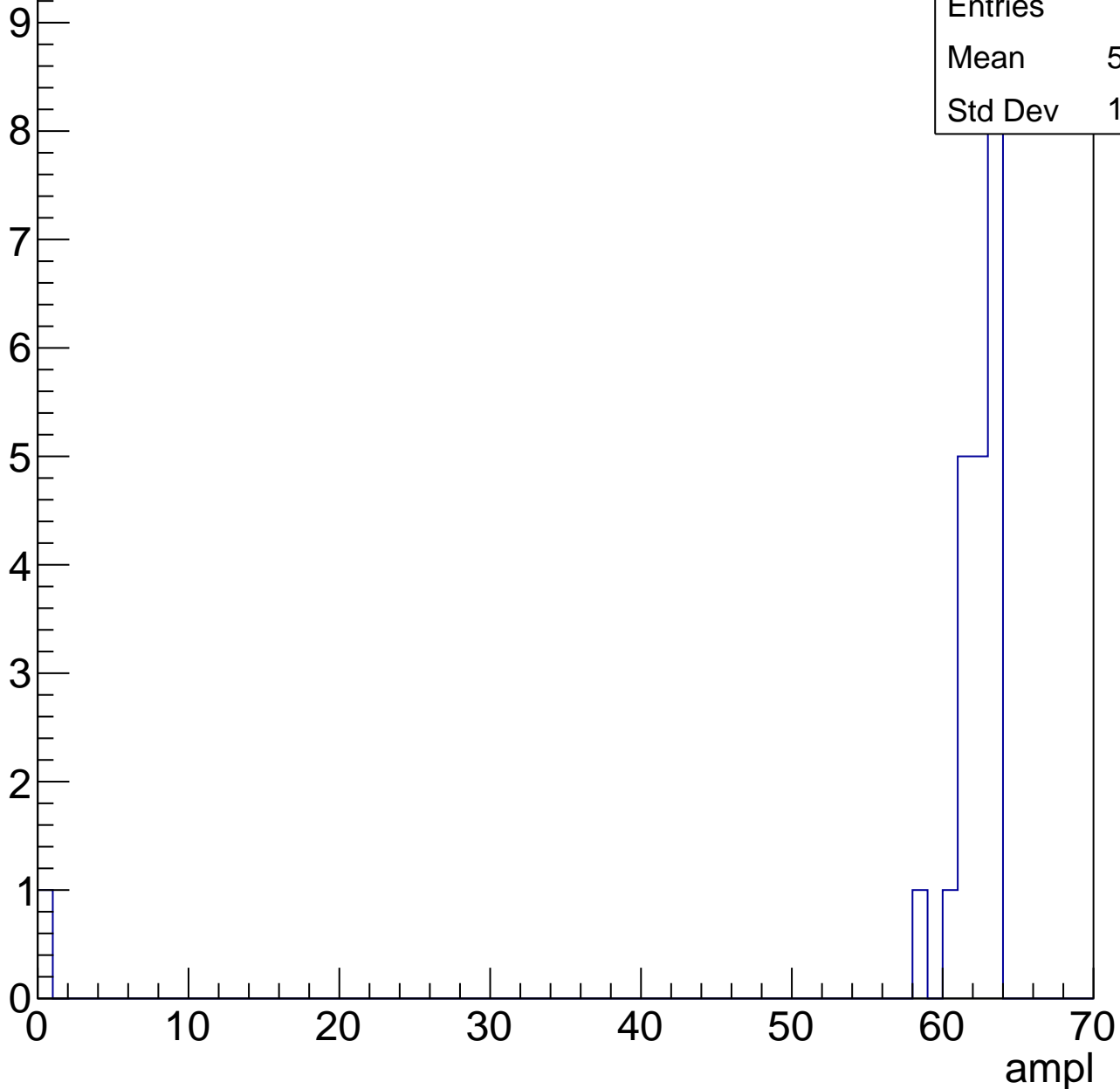


B1L100S, U15-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	59.09
Std Dev	12.95



B1L100S, U15-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch19, adc0

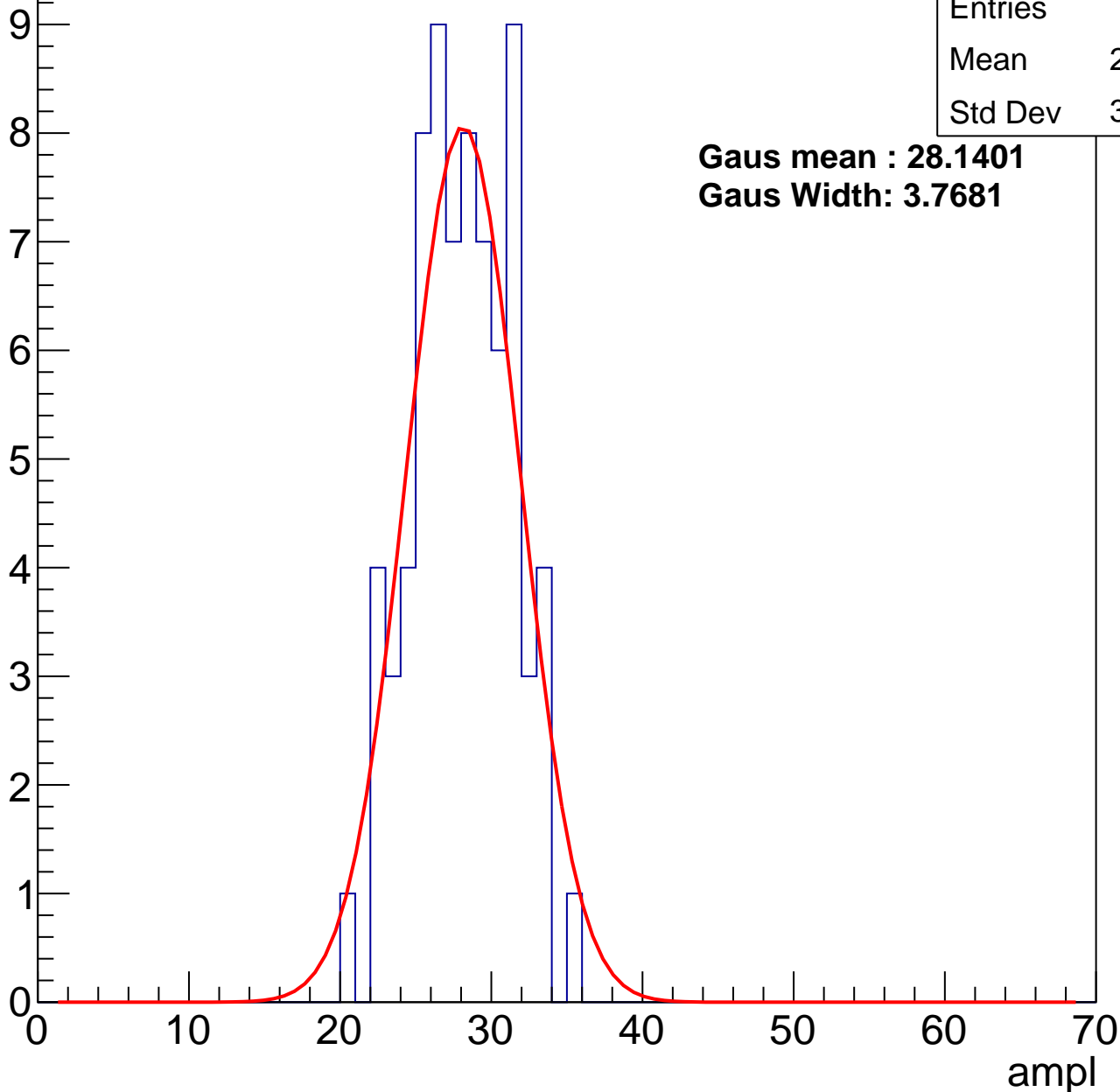
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	27.64
Std Dev	3.207

Gaus mean : 28.1401

Gaus Width: 3.7681

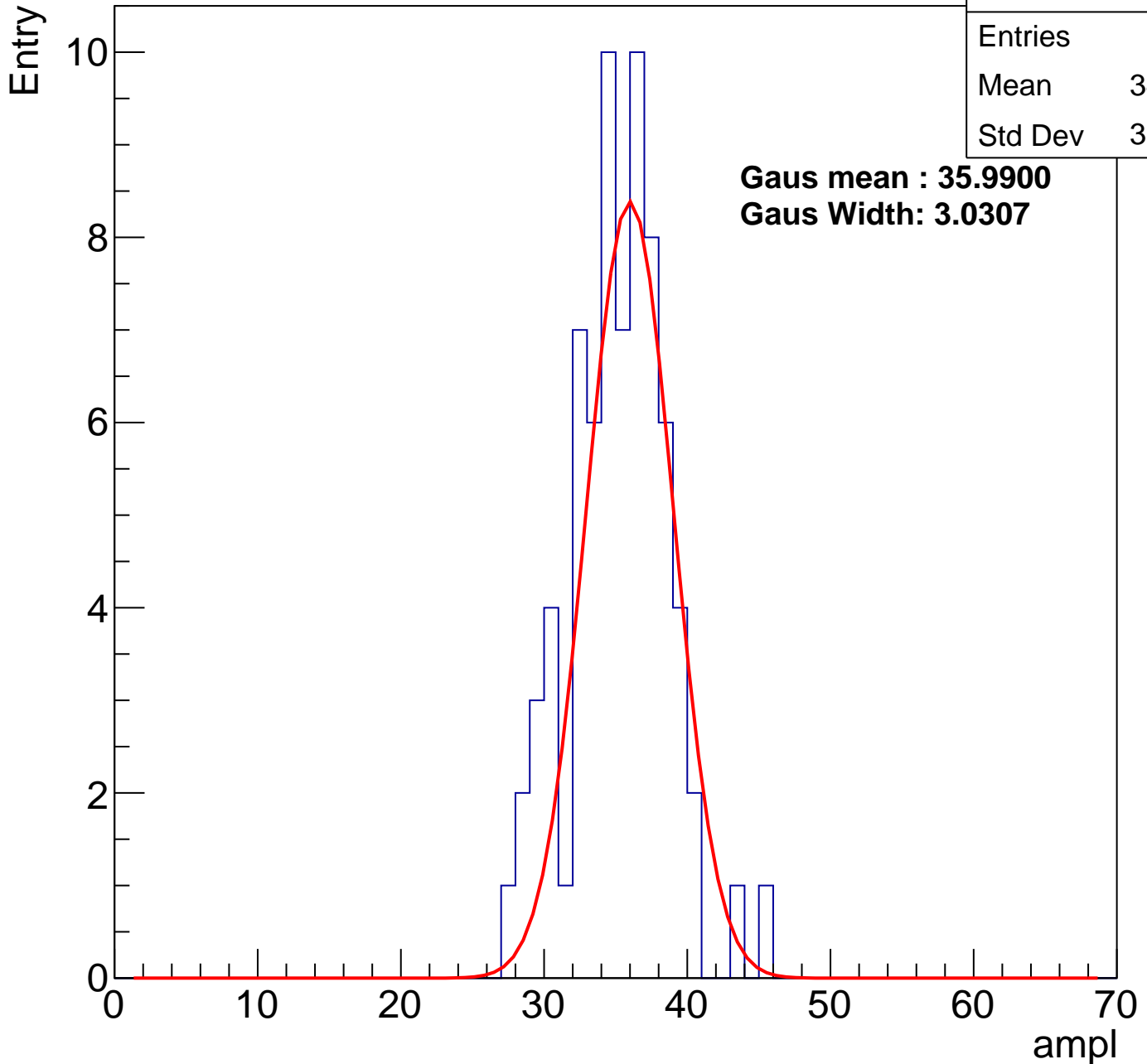


B1L100S, U15-ch19, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	34.74
Std Dev	3.416

Gaus mean : 35.9900
Gaus Width: 3.0307



B1L100S, U15-ch19, adc2

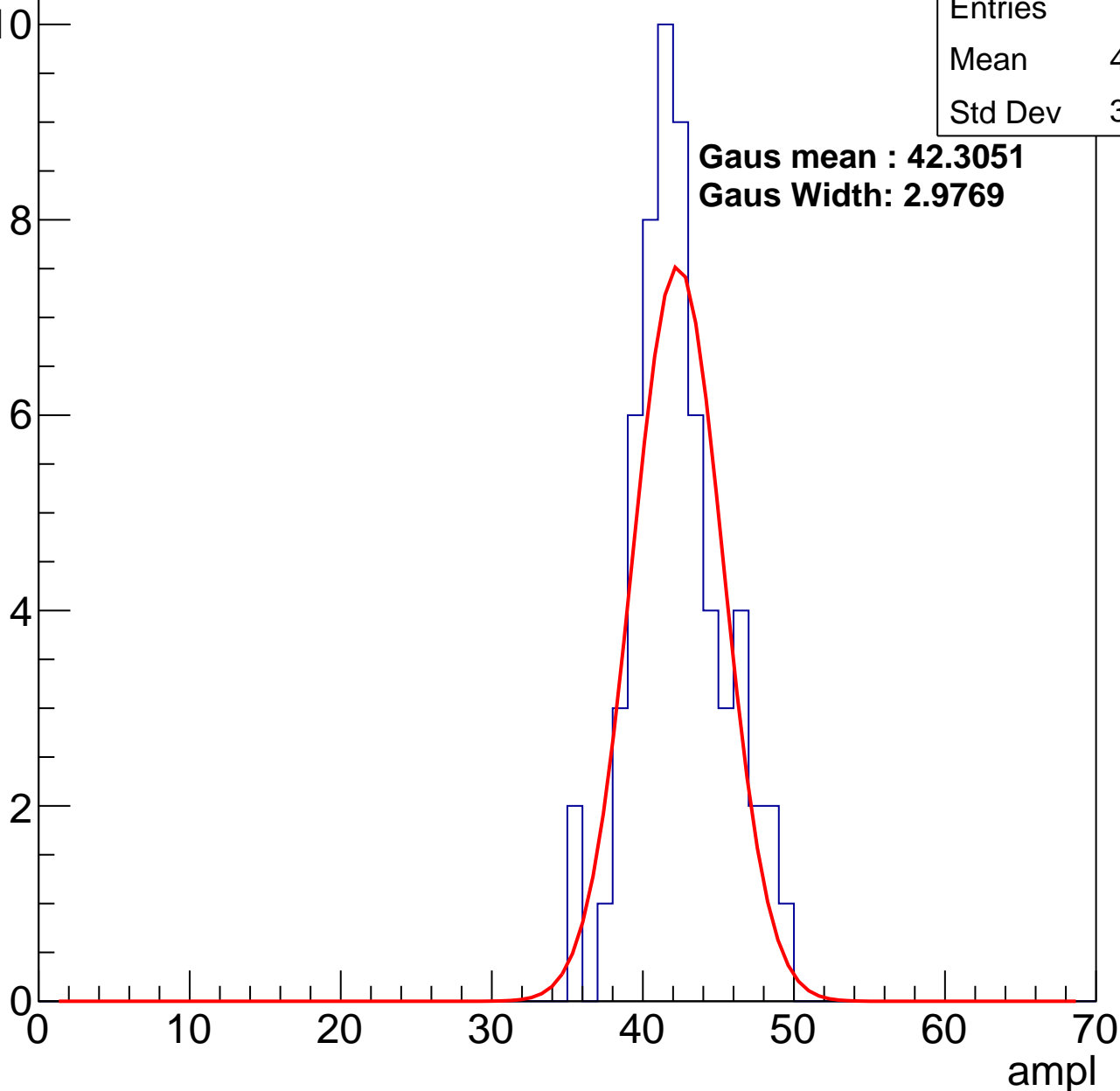
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	41.89
Std Dev	3.003

Gaus mean : 42.3051

Gaus Width: 2.9769

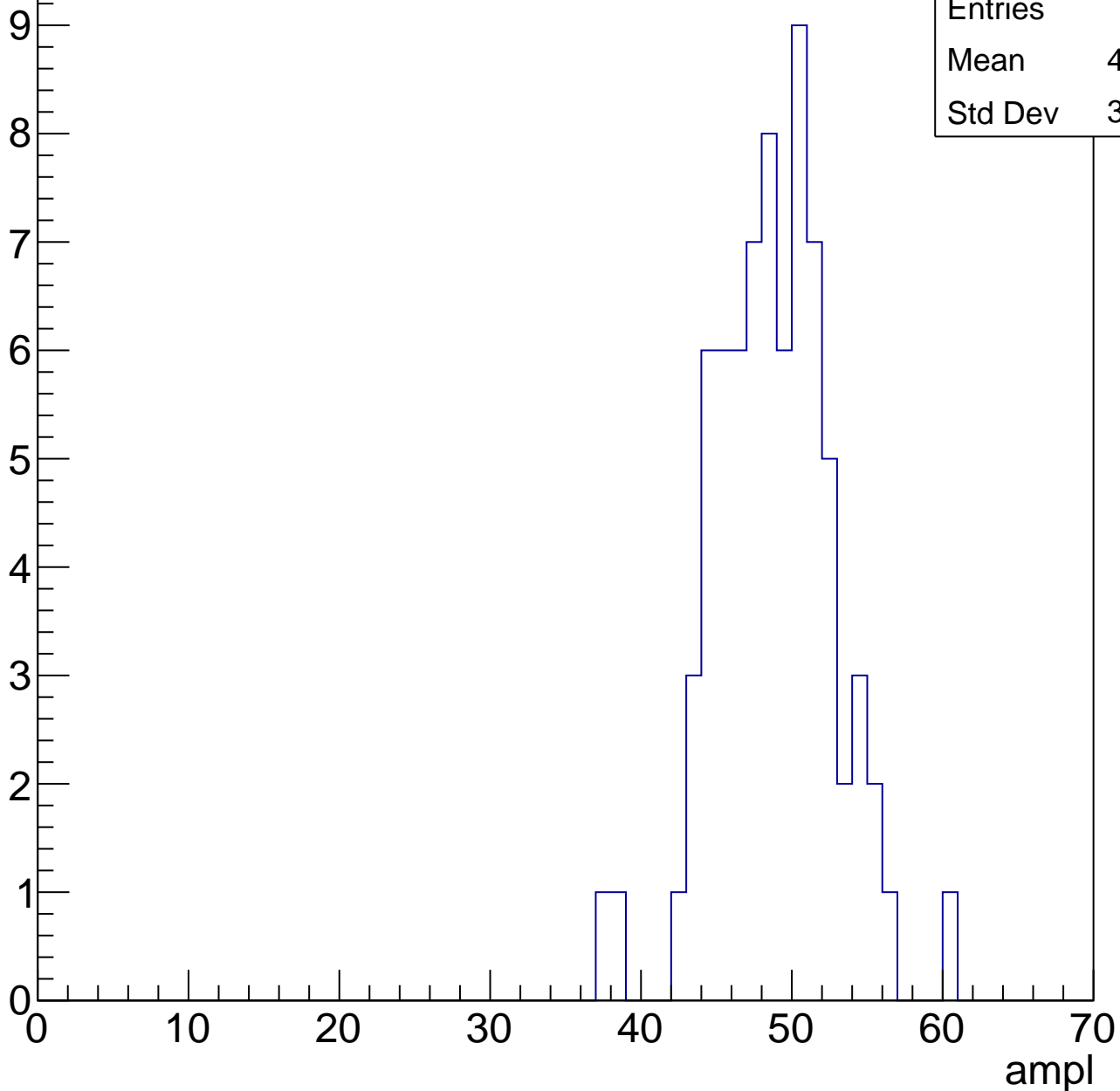


B1L100S, U15-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

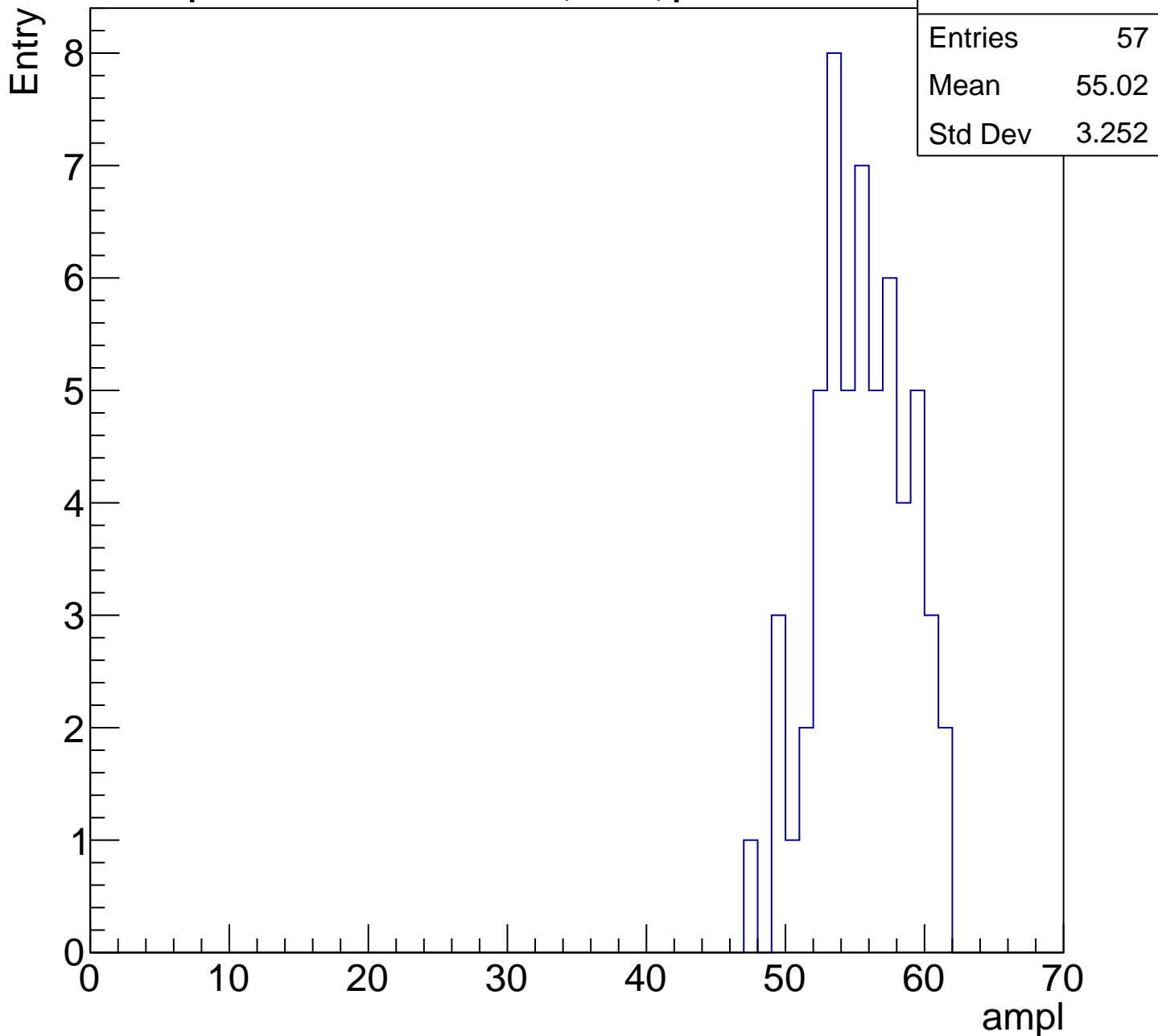
Entry

Entries	75
Mean	48.32
Std Dev	3.923



B1L100S, U15-ch19, adc4

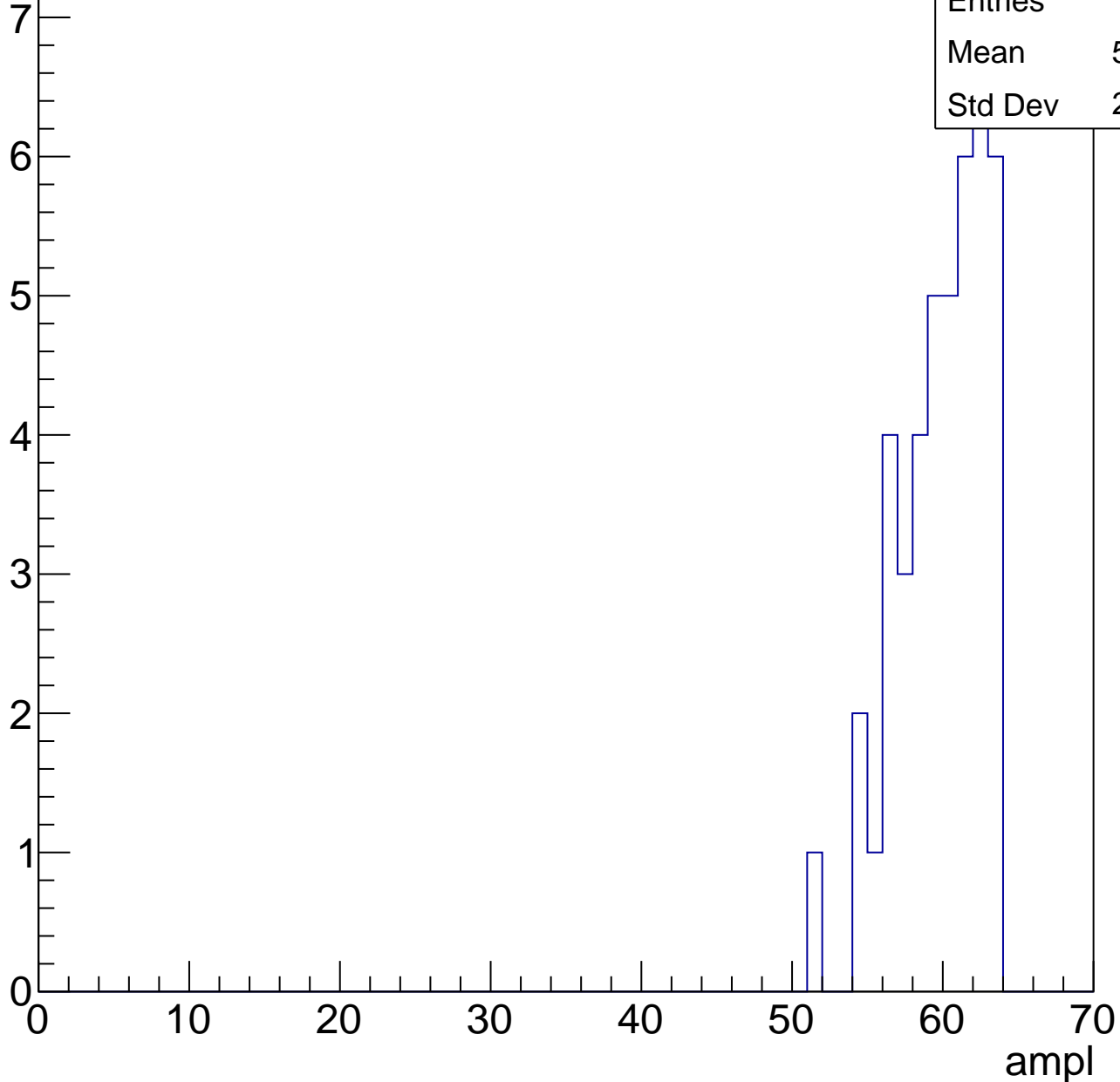
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

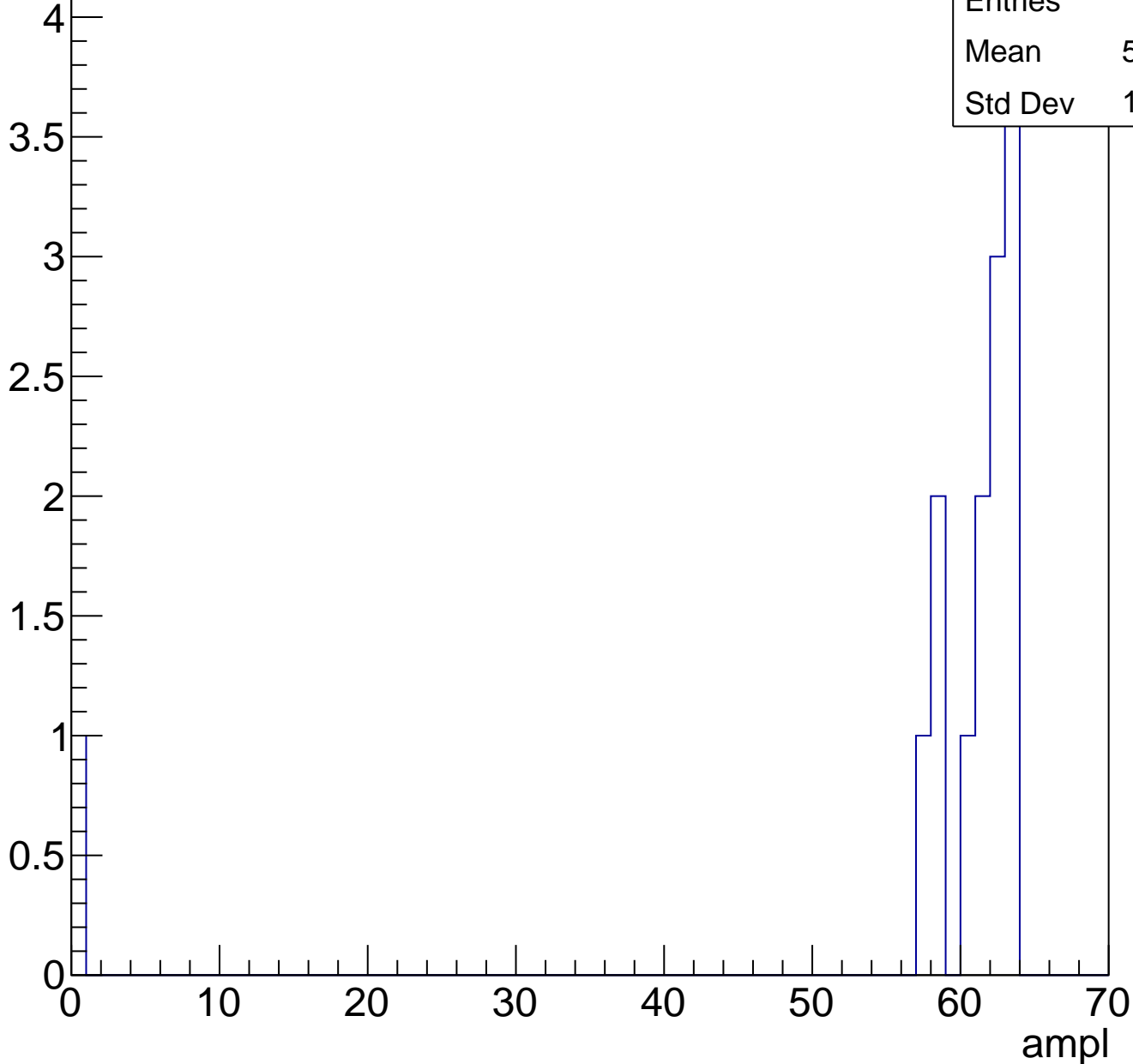
Entry



B1L100S, U15-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch20, adc0

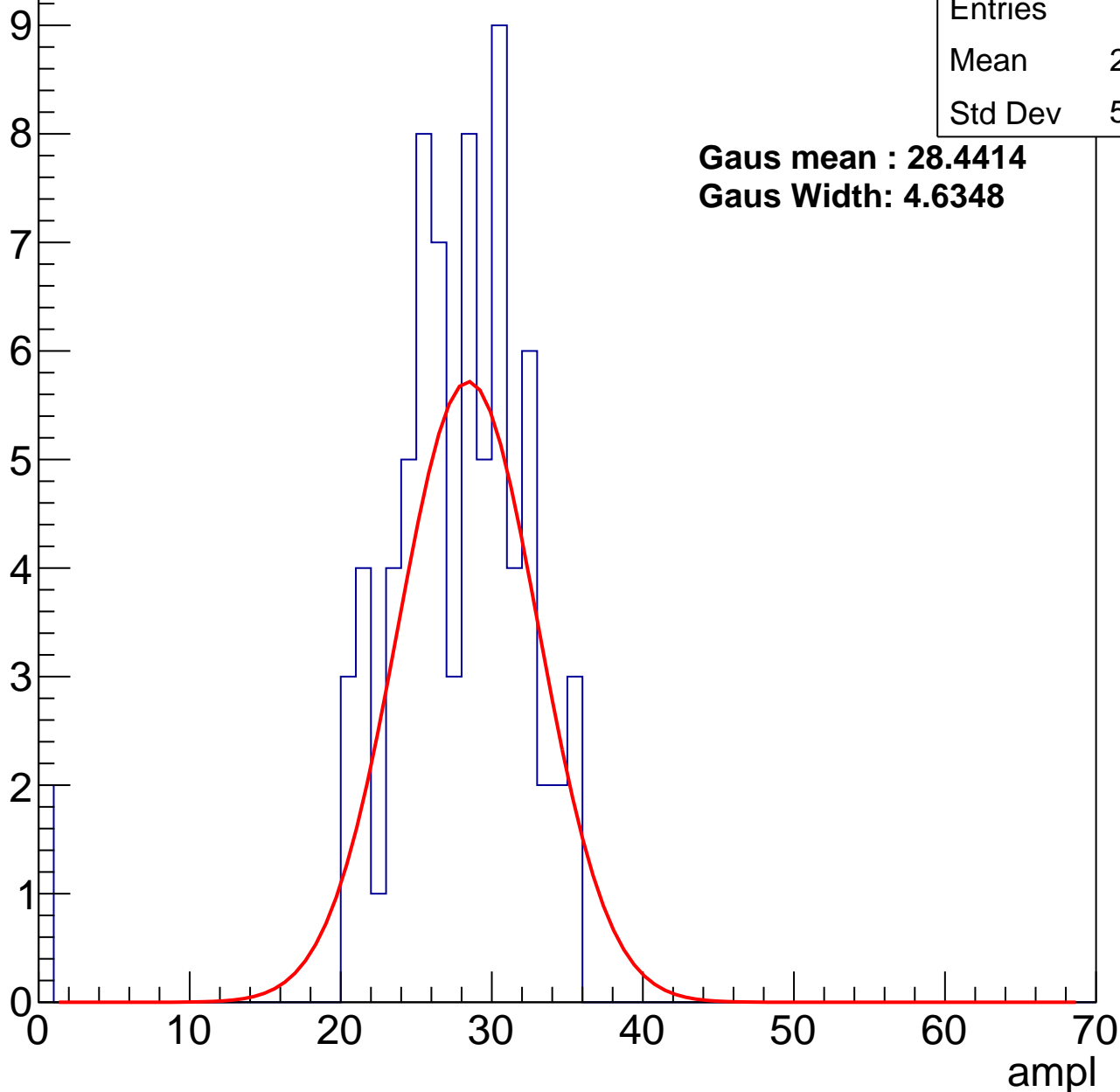
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	26.78
Std Dev	5.853

Gaus mean : 28.4414

Gaus Width: 4.6348



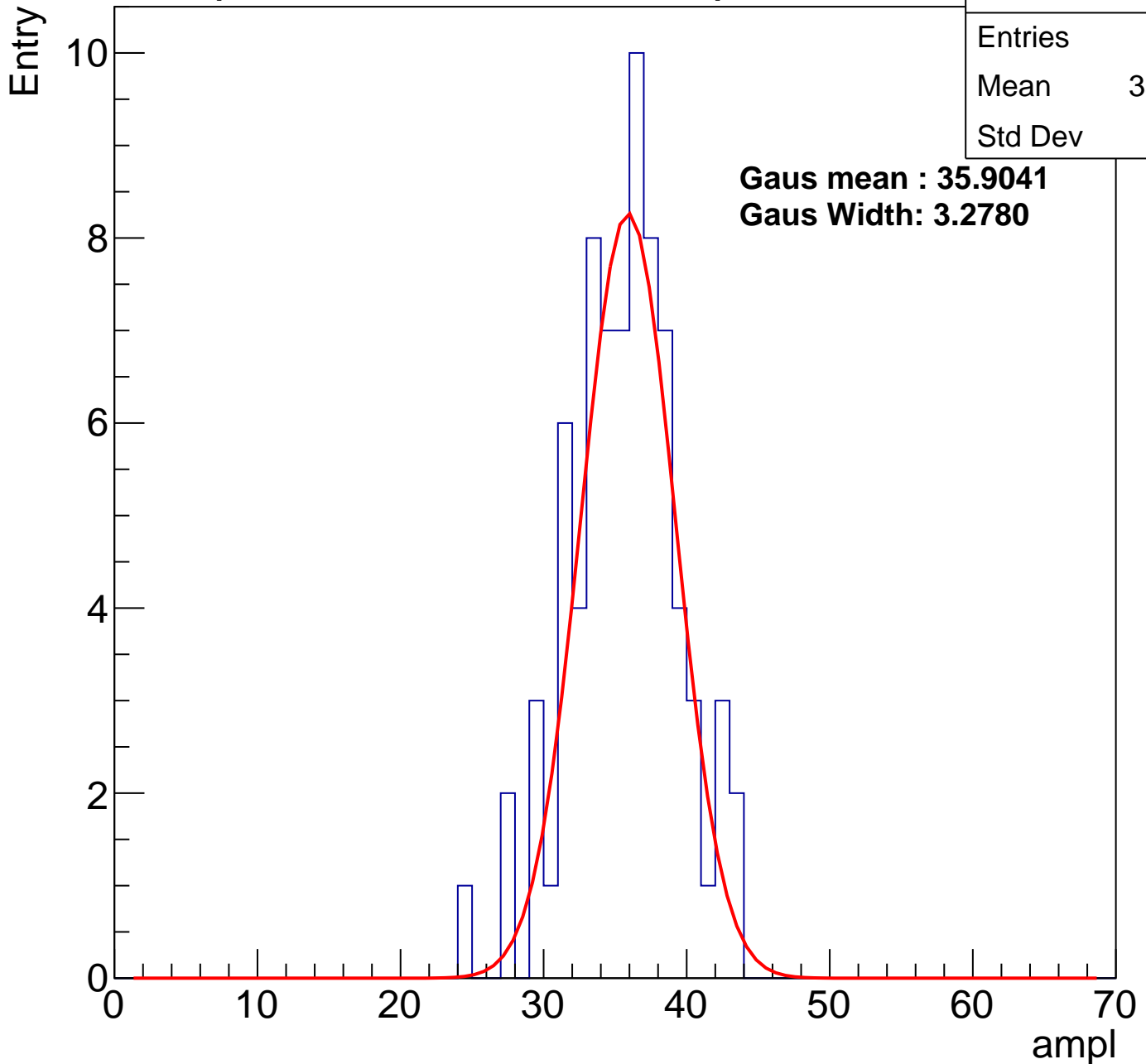
B1L100S, U15-ch20, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	35.16
Std Dev	3.79

Gaus mean : 35.9041

Gaus Width: 3.2780



B1L100S, U15-ch20, adc2

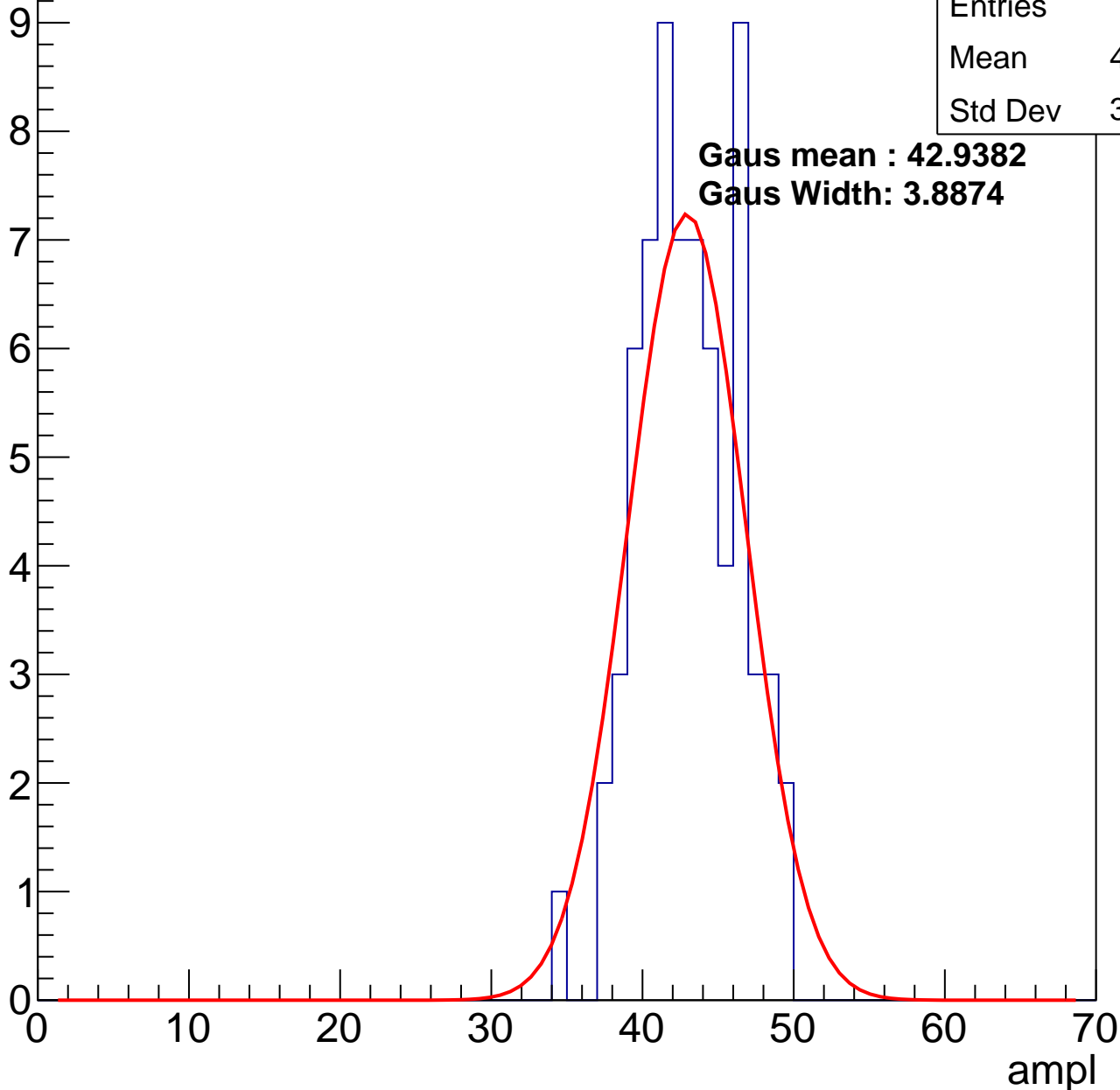
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.62
Std Dev	3.226

Gaus mean : 42.9382

Gaus Width: 3.8874

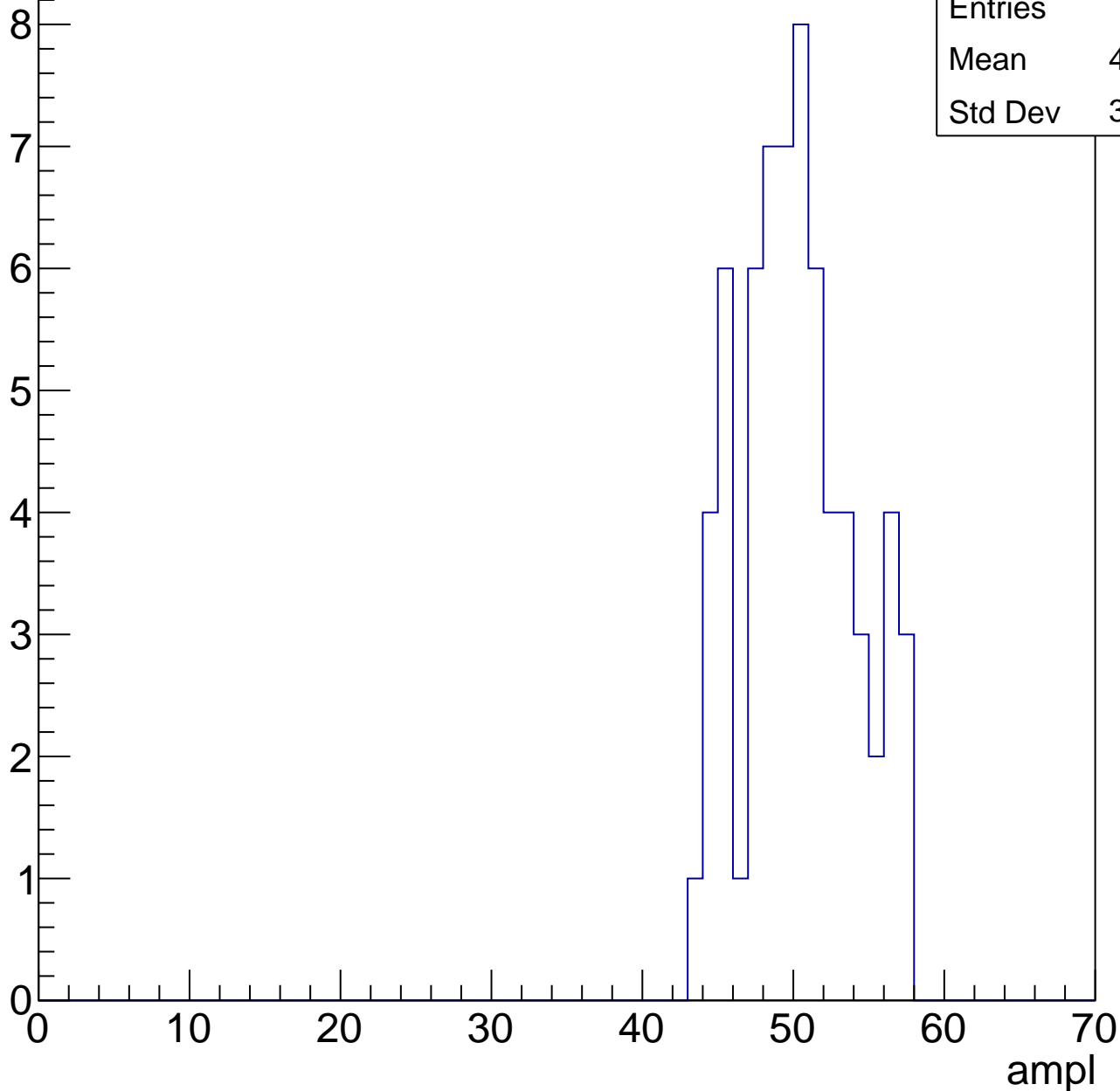


B1L100S, U15-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.83
Std Dev	3.679

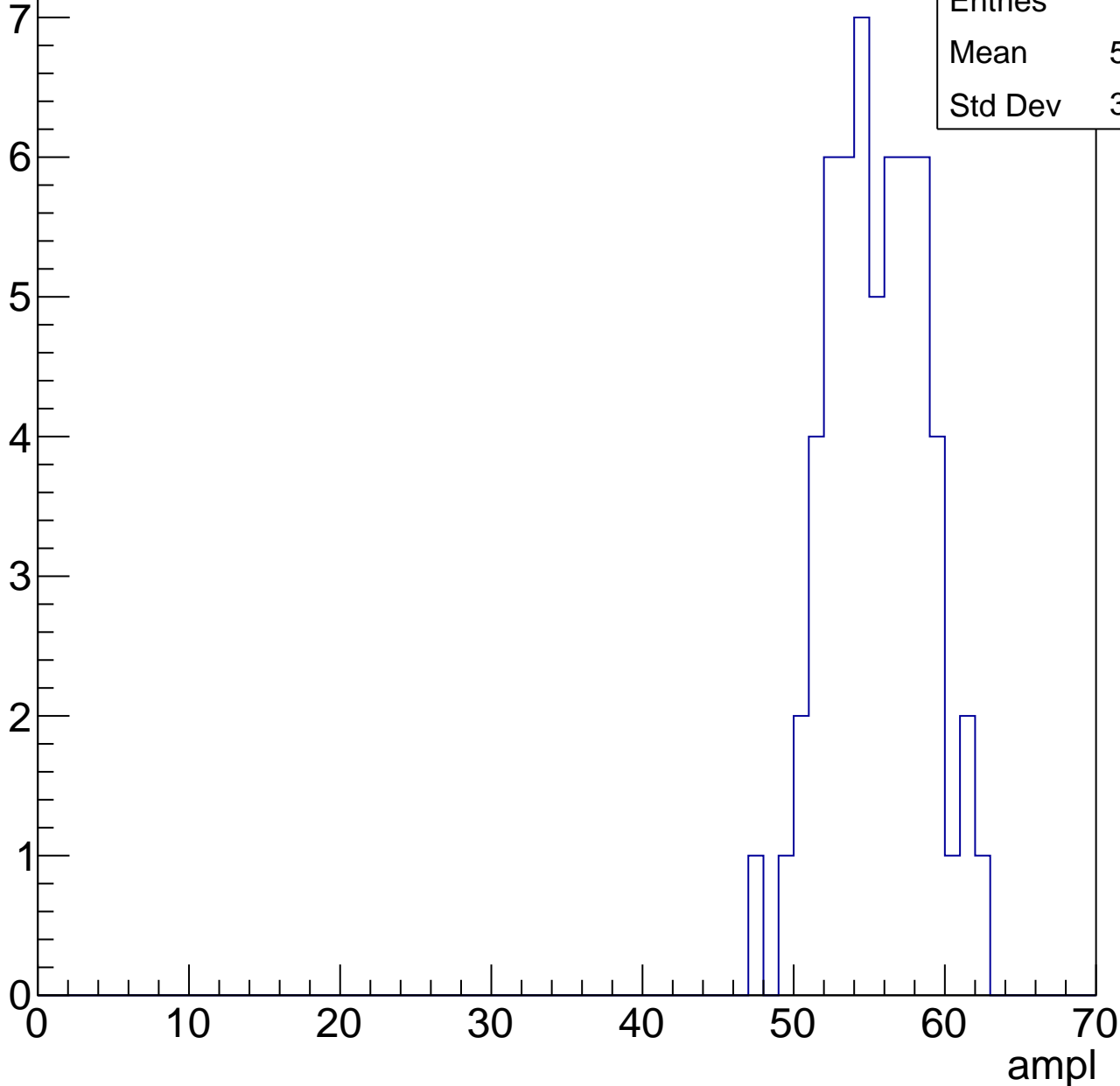


B1L100S, U15-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

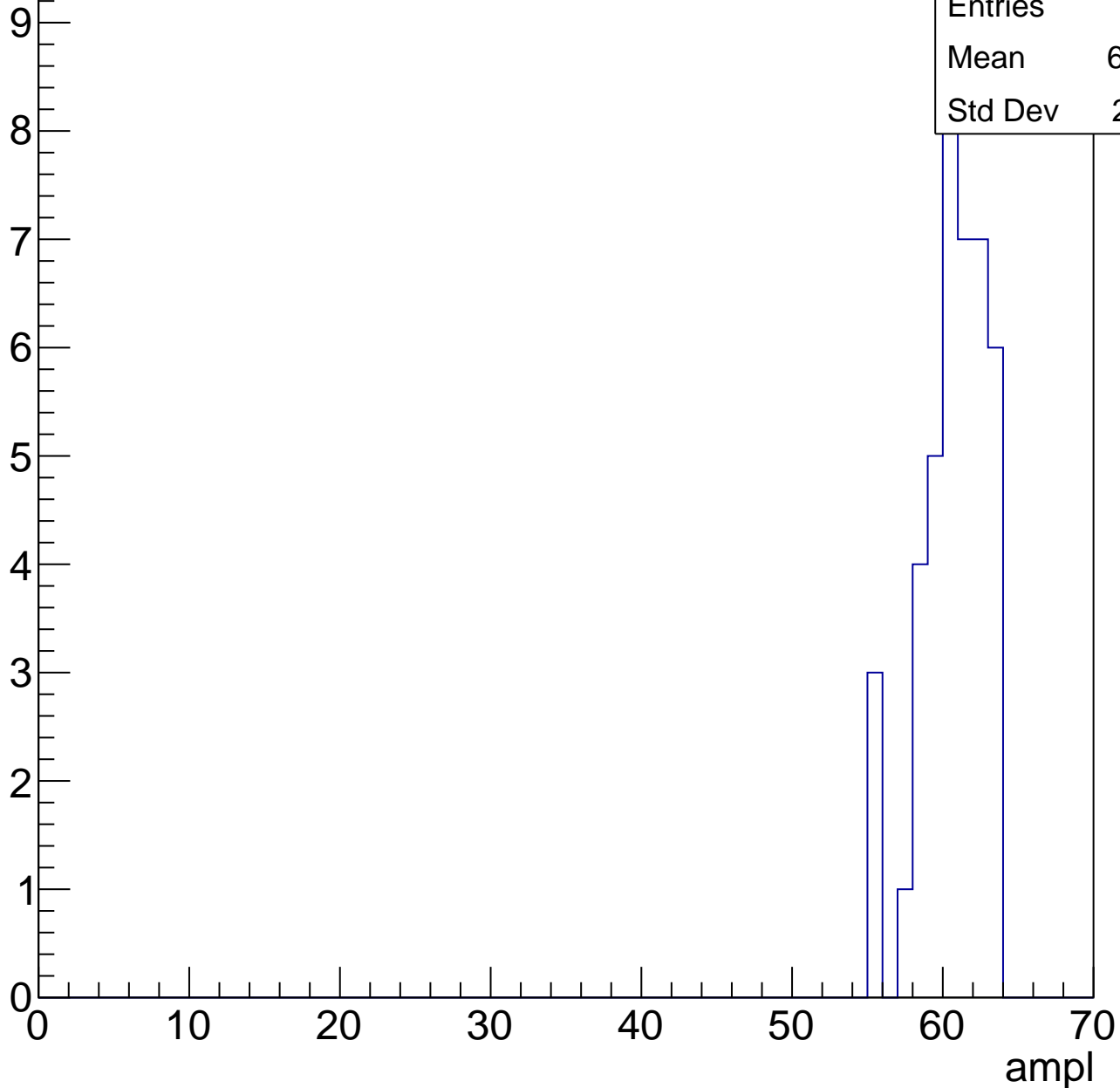
Entries	58
Mean	54.98
Std Dev	3.197



B1L100S, U15-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

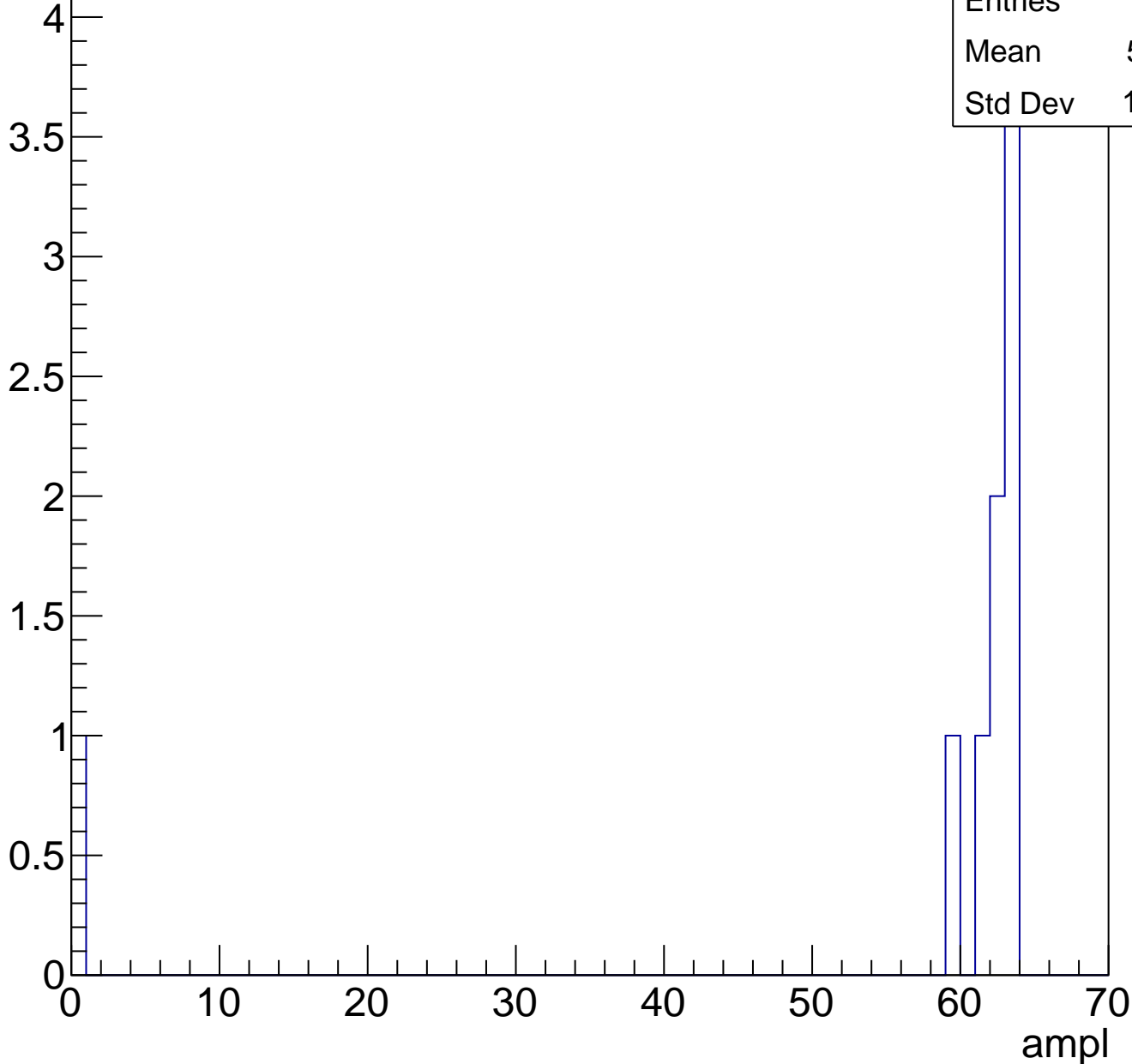
Entry



B1L100S, U15-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

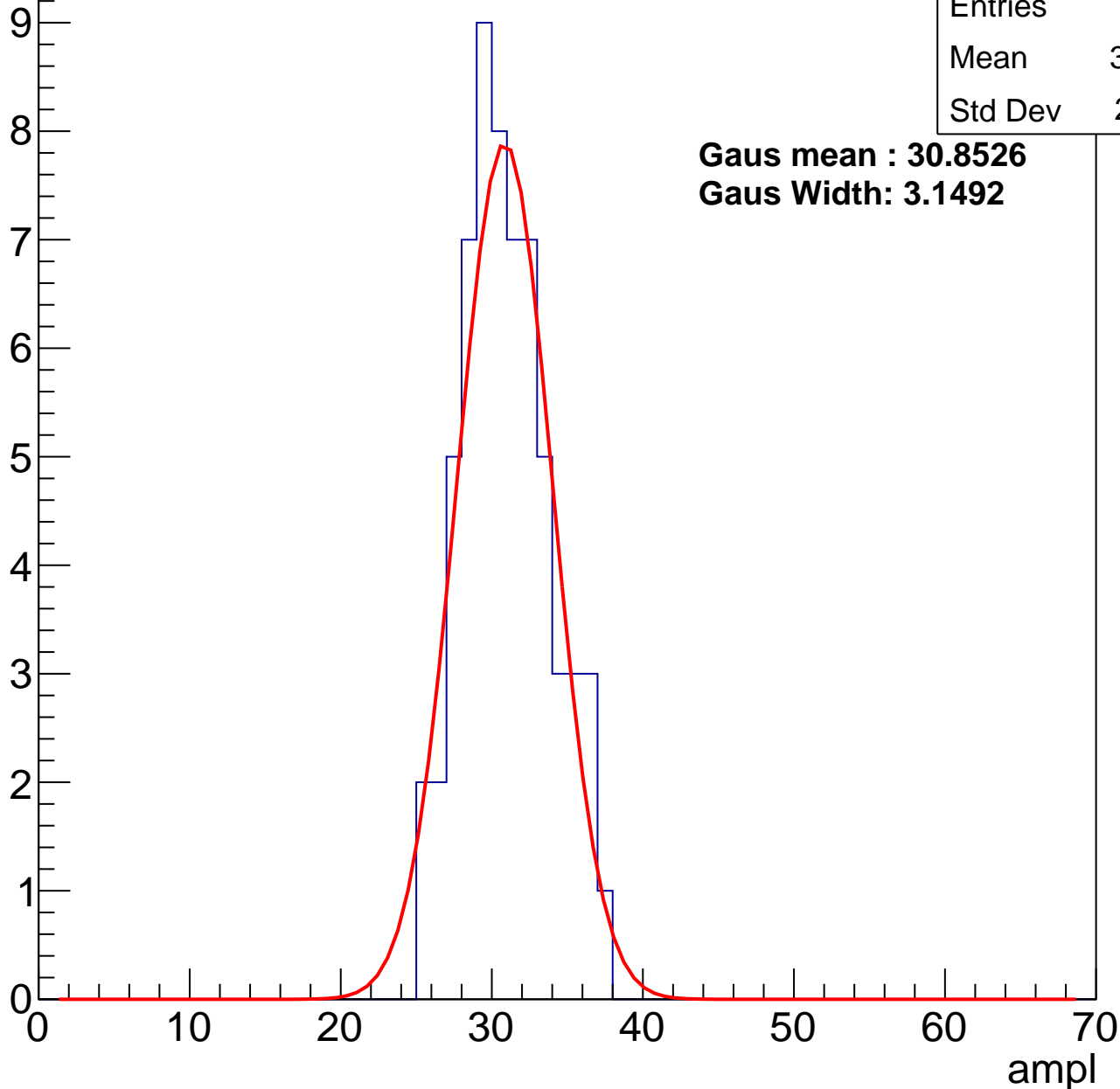


Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch21, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch21, adc1

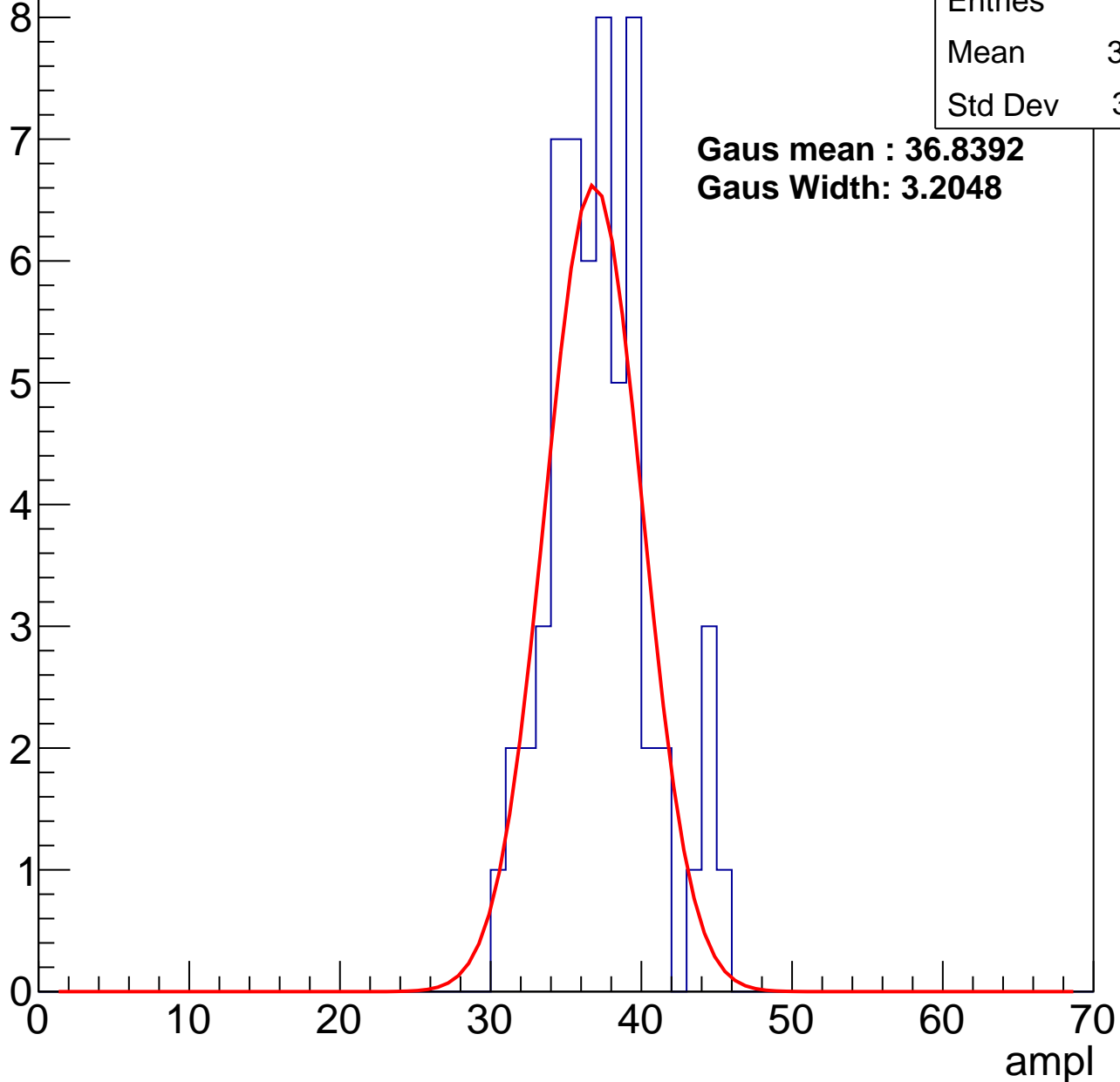
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	36.79
Std Dev	3.341

Gaus mean : 36.8392

Gaus Width: 3.2048



B1L100S, U15-ch21, adc2

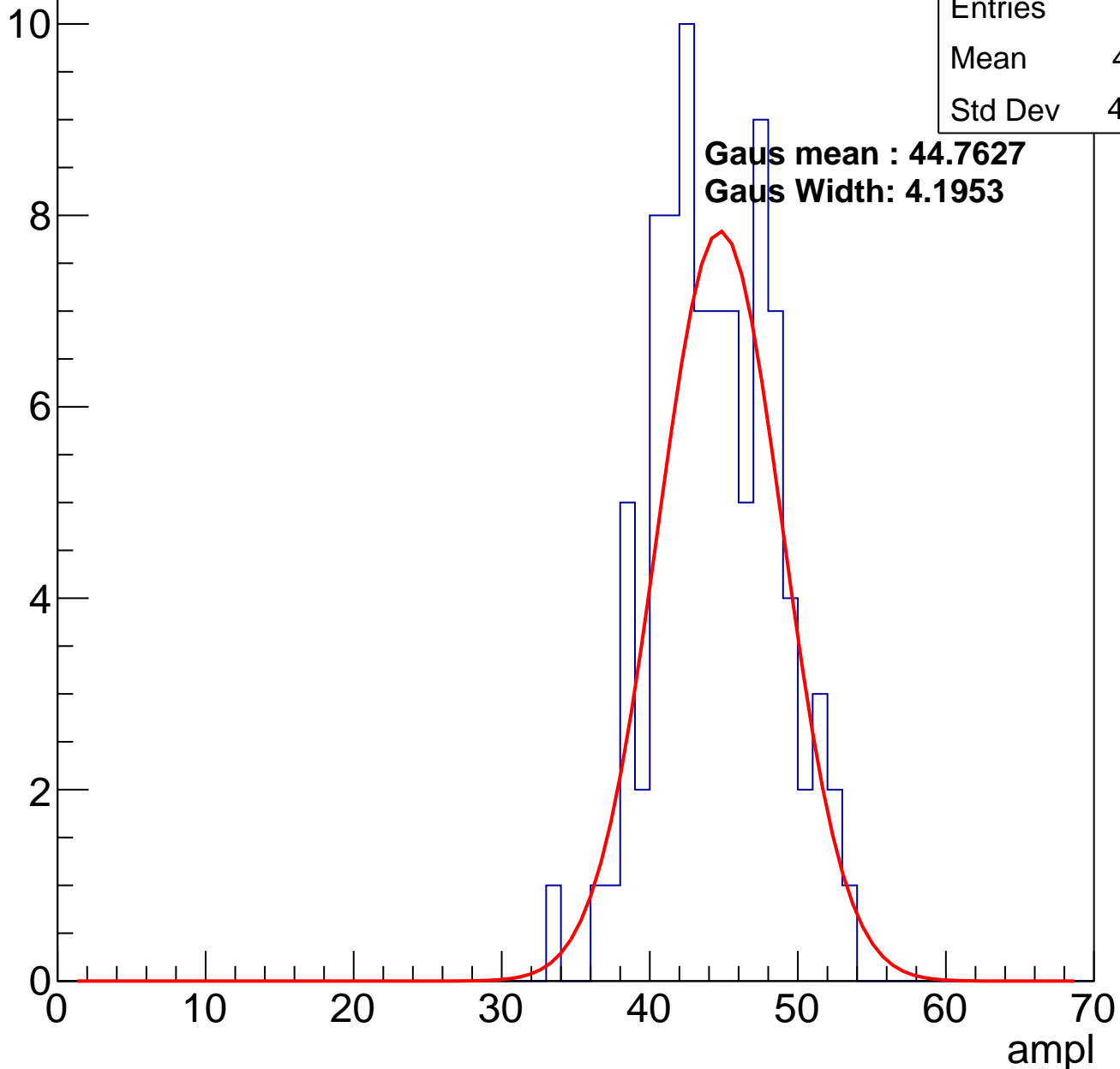
calib_packv5_042523_0143.root, FC#4, port A2

Entries	90
Mean	44.01
Std Dev	4.026

Gaus mean : 44.7627

Gaus Width: 4.1953

Entry

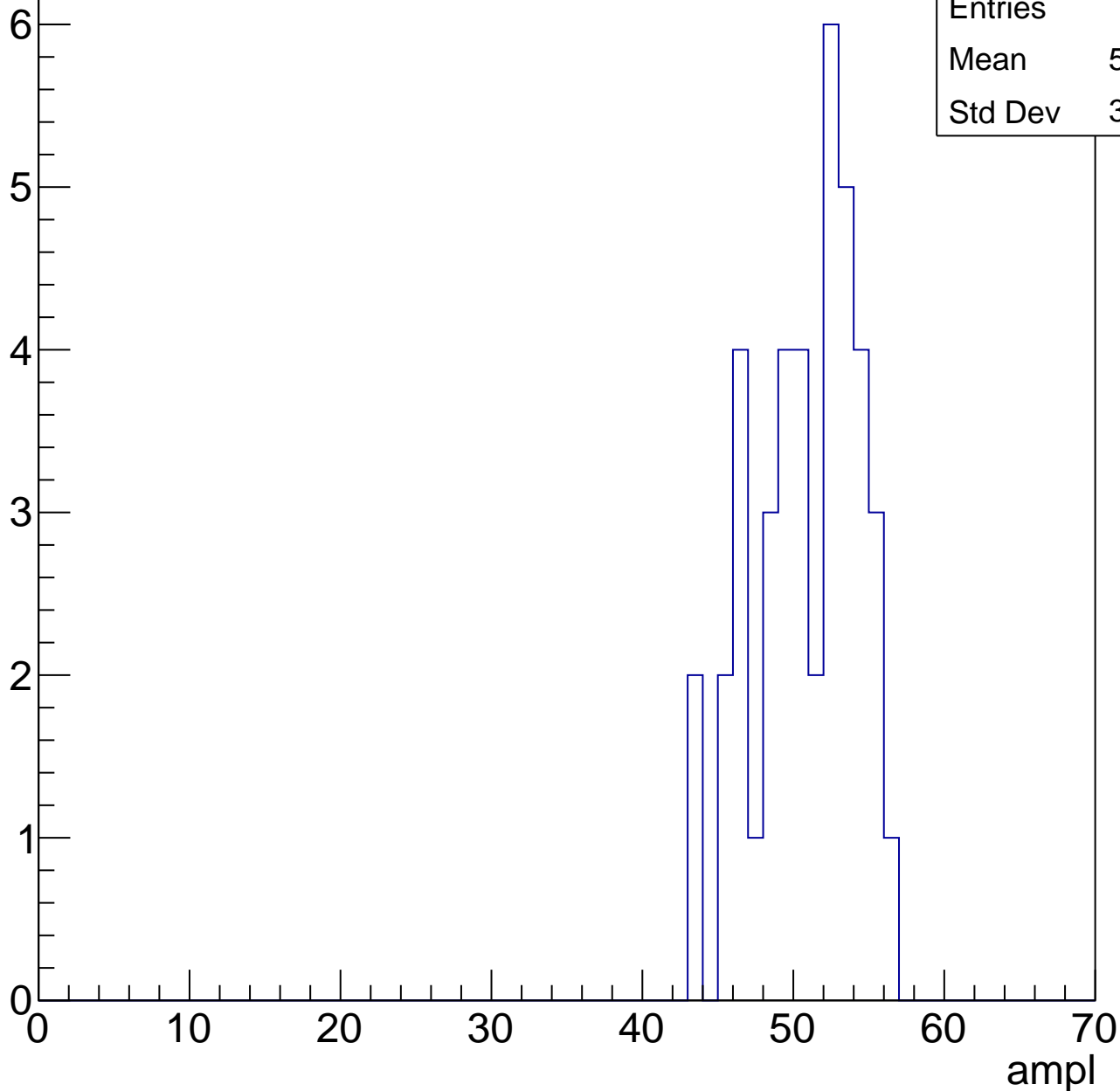


B1L100S, U15-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	50.32
Std Dev	3.418

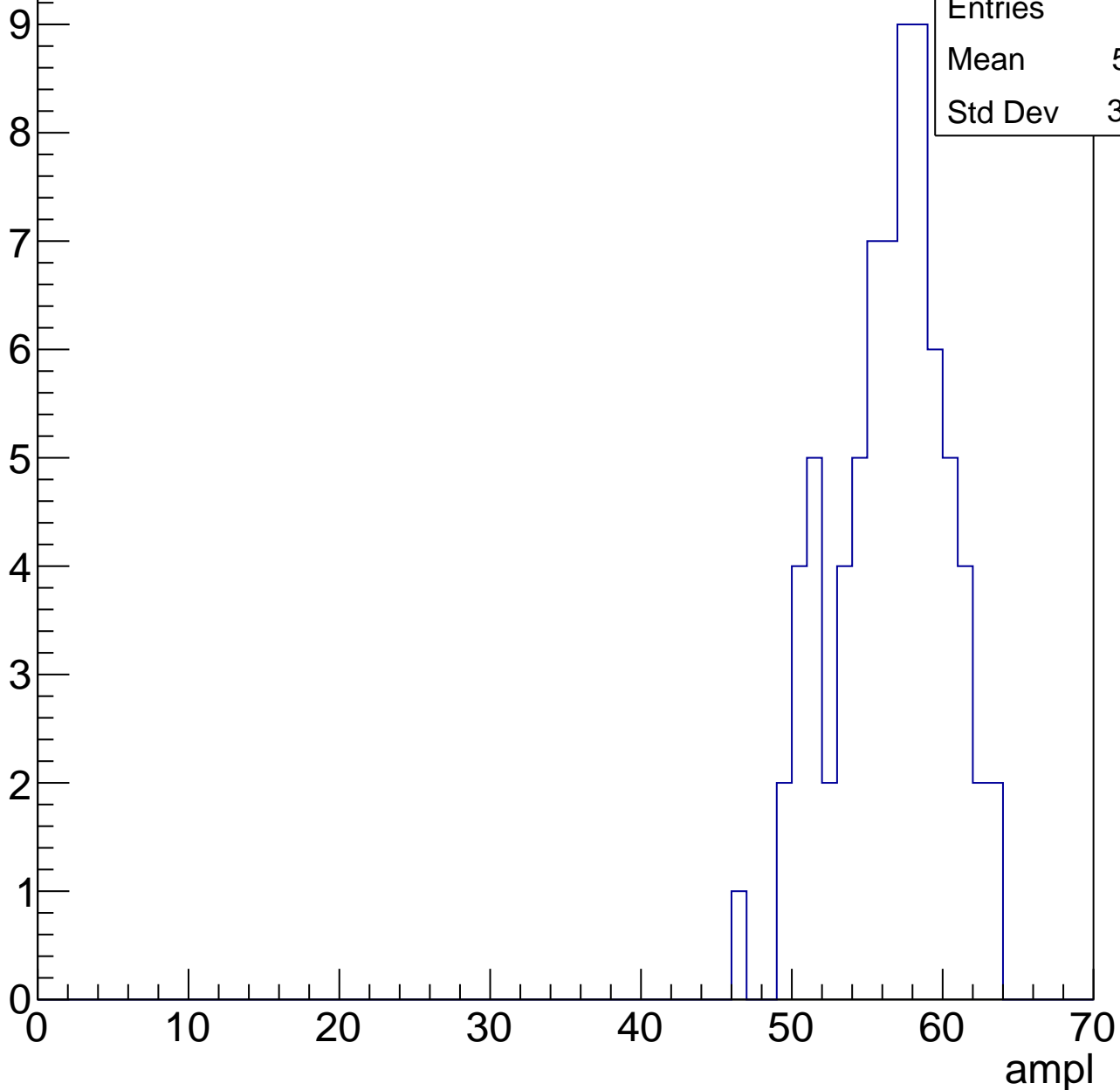


B1L100S, U15-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	56.01
Std Dev	3.696

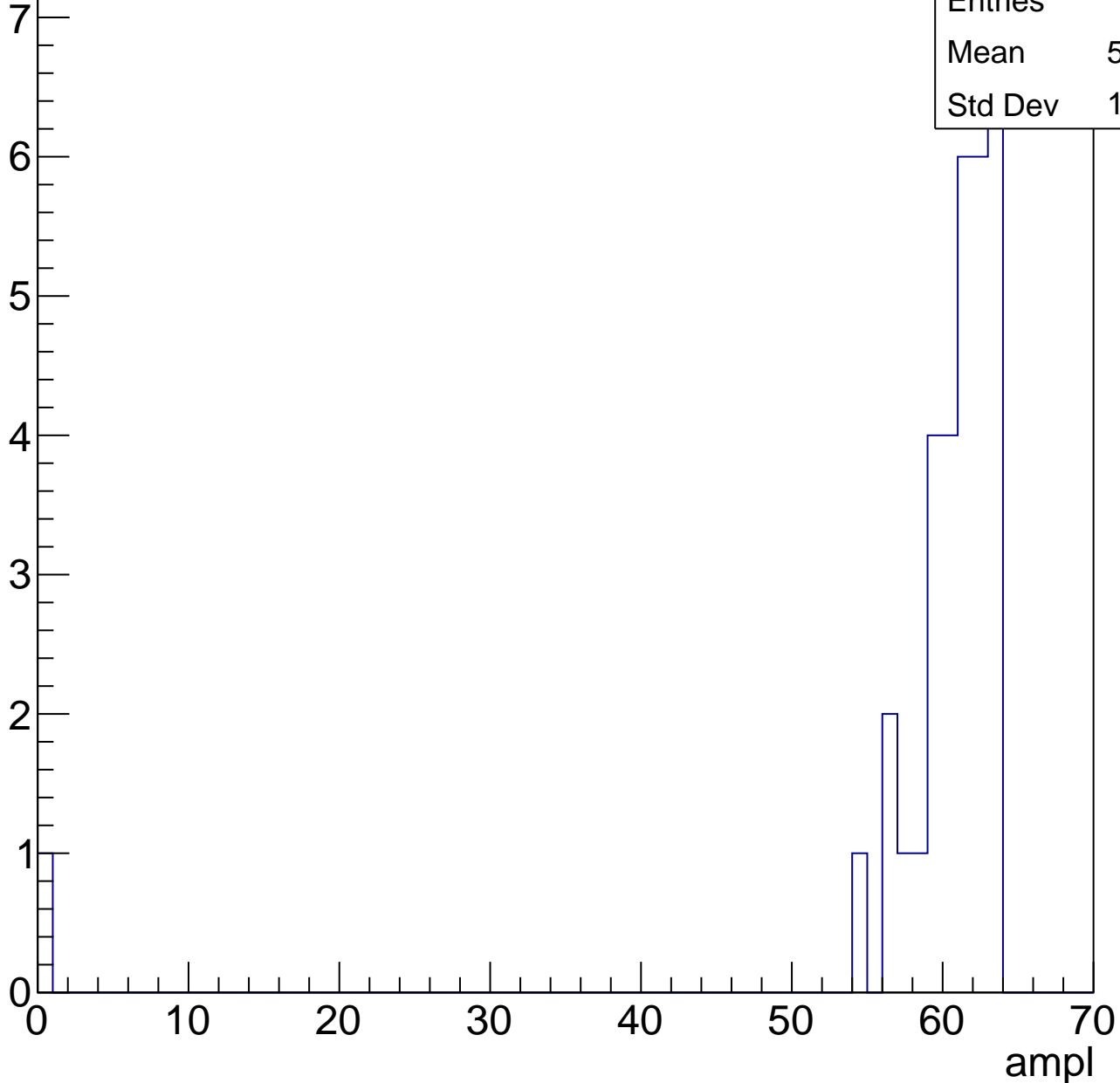


B1L100S, U15-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

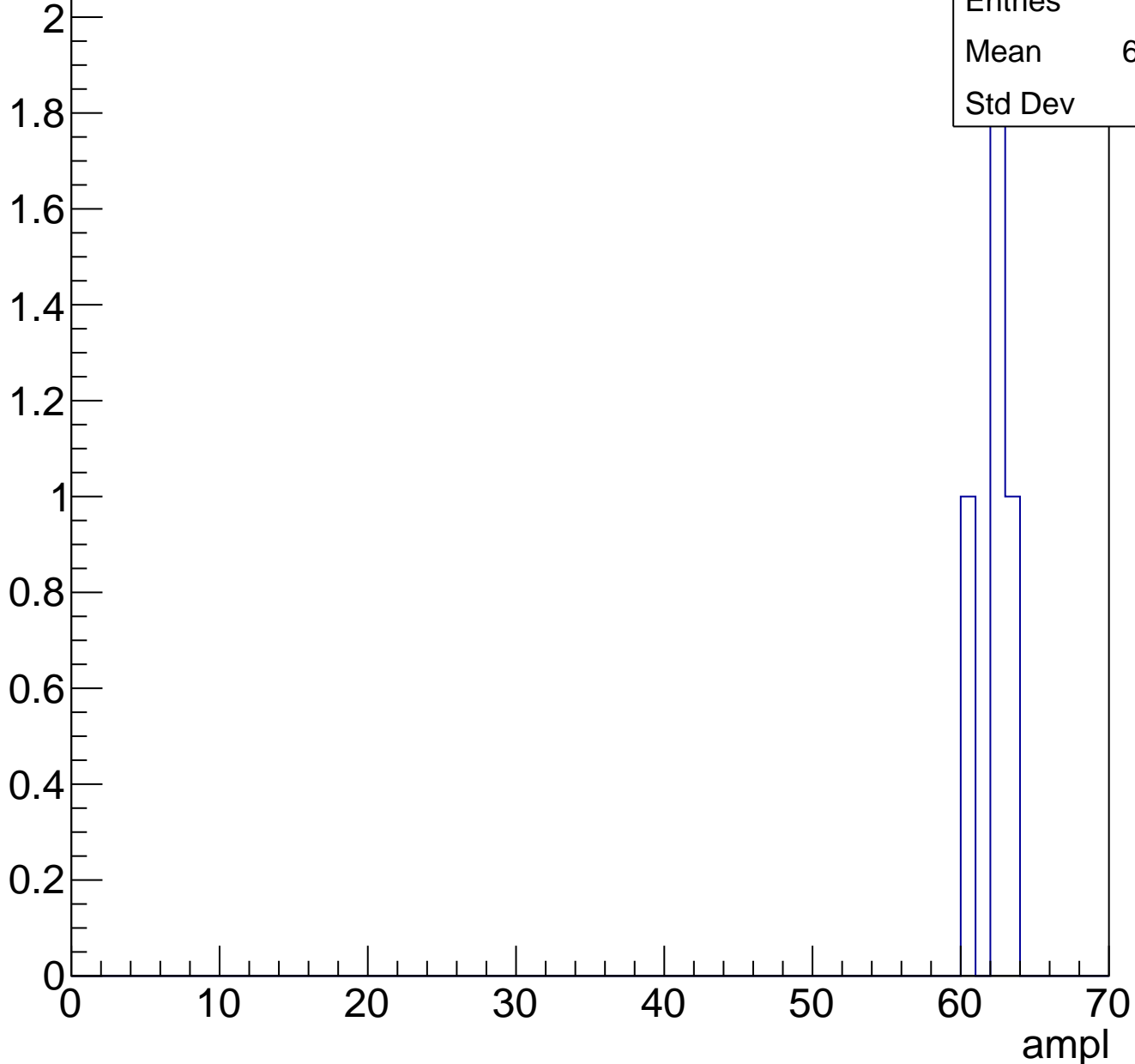
Entries	33
Mean	58.67
Std Dev	10.62



B1L100S, U15-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch22, adc0

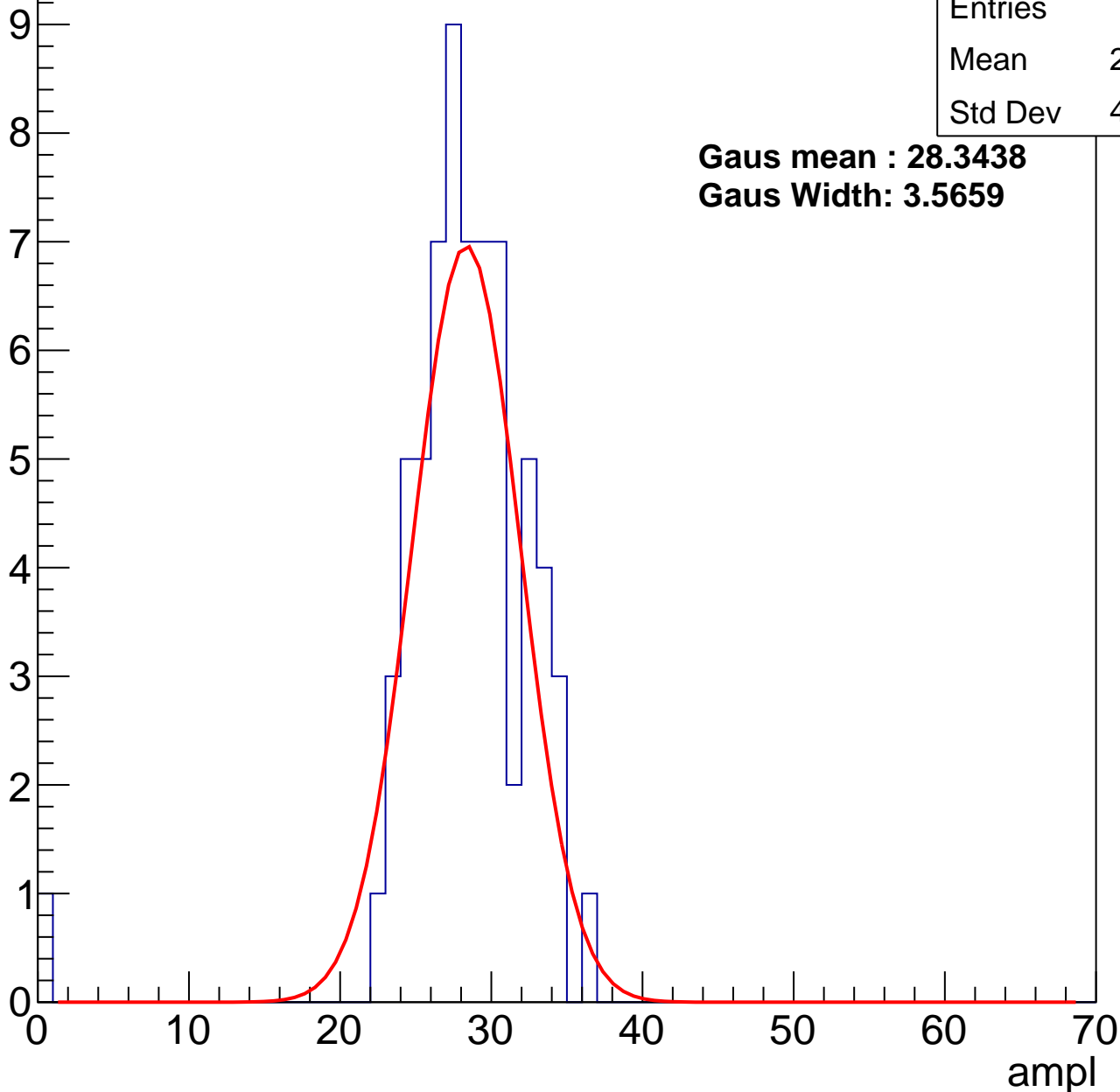
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	27.79
Std Dev	4.667

Gaus mean : 28.3438

Gaus Width: 3.5659



B1L100S, U15-ch22, adc1

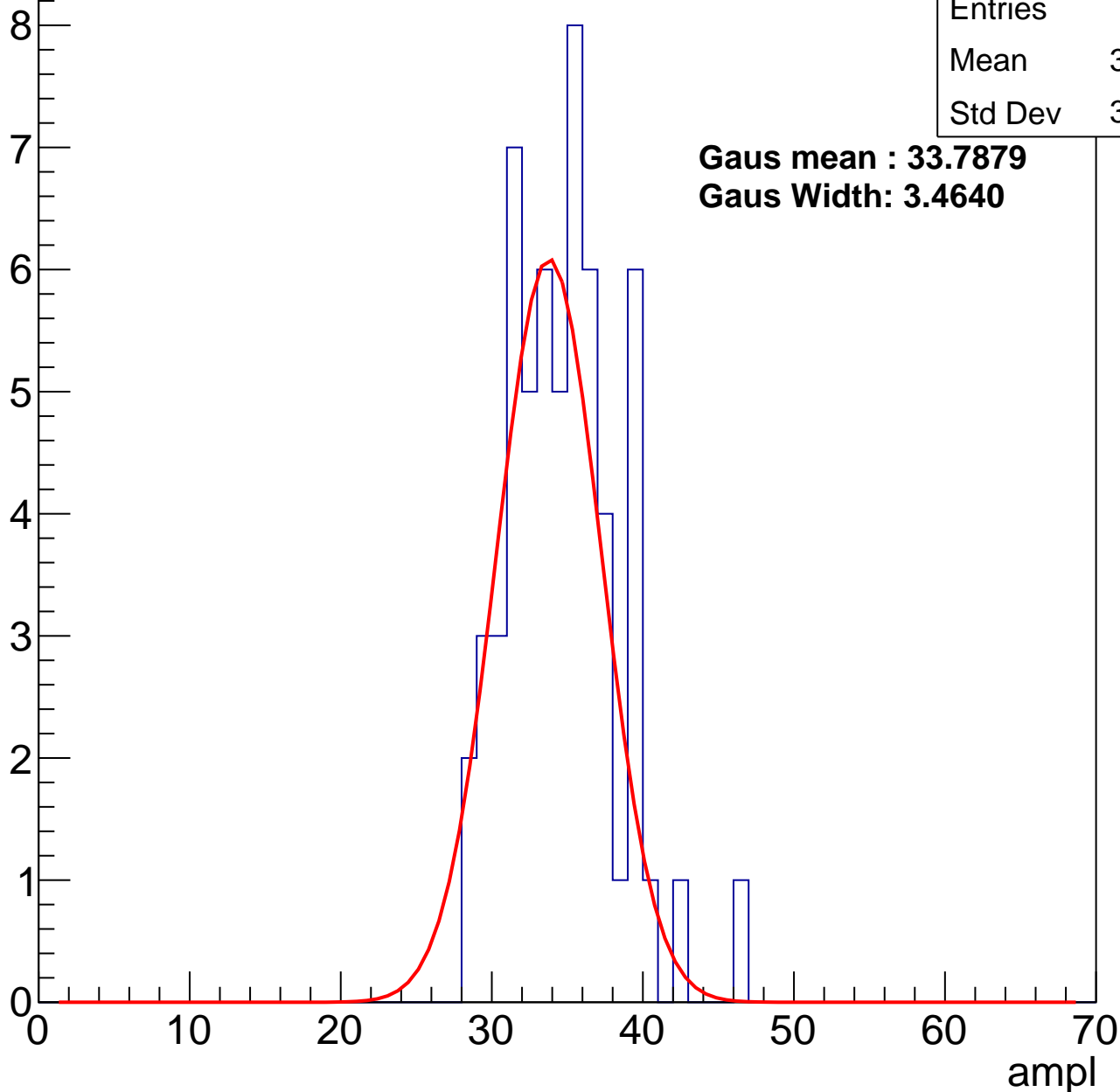
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	34.27
Std Dev	3.584

Gaus mean : 33.7879

Gaus Width: 3.4640



B1L100S, U15-ch22, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8

7

6

5

4

3

2

1

0

Entries	71
Mean	40.85
Std Dev	3.351

Gaus mean : 41.4018

Gaus Width: 3.9065

0

10

20

30

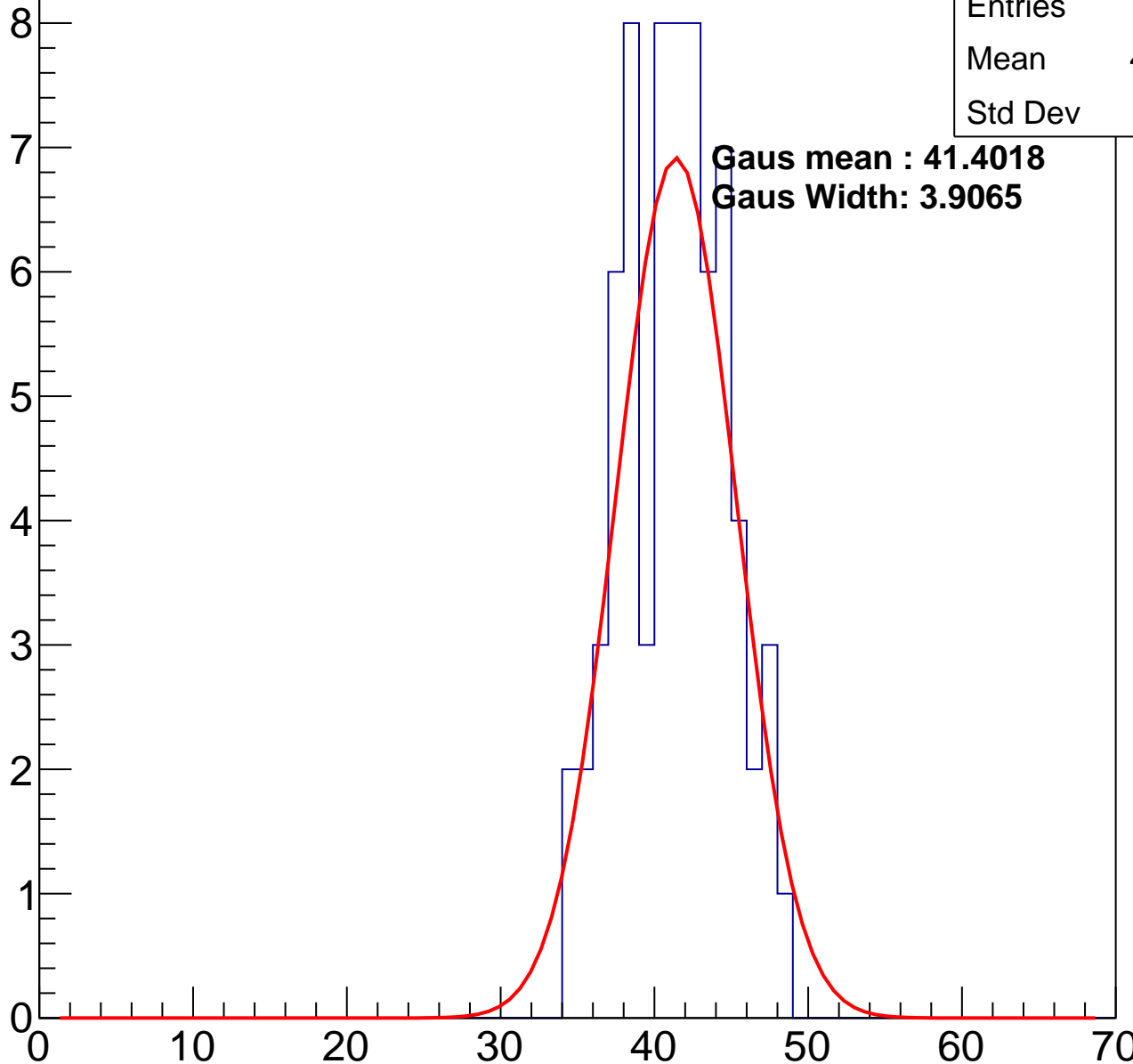
40

50

60

70

ampl

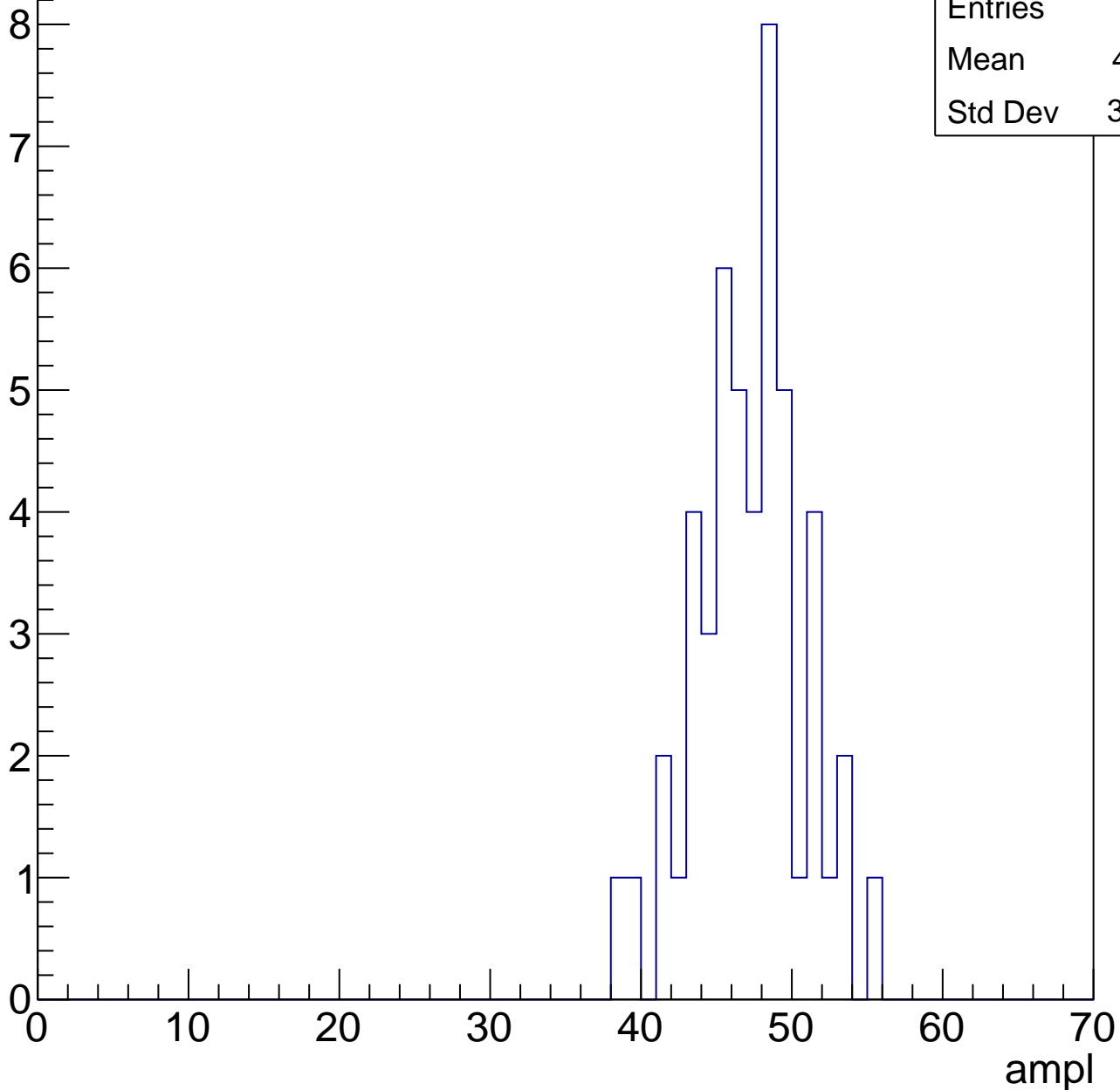


B1L100S, U15-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	46.71
Std Dev	3.557

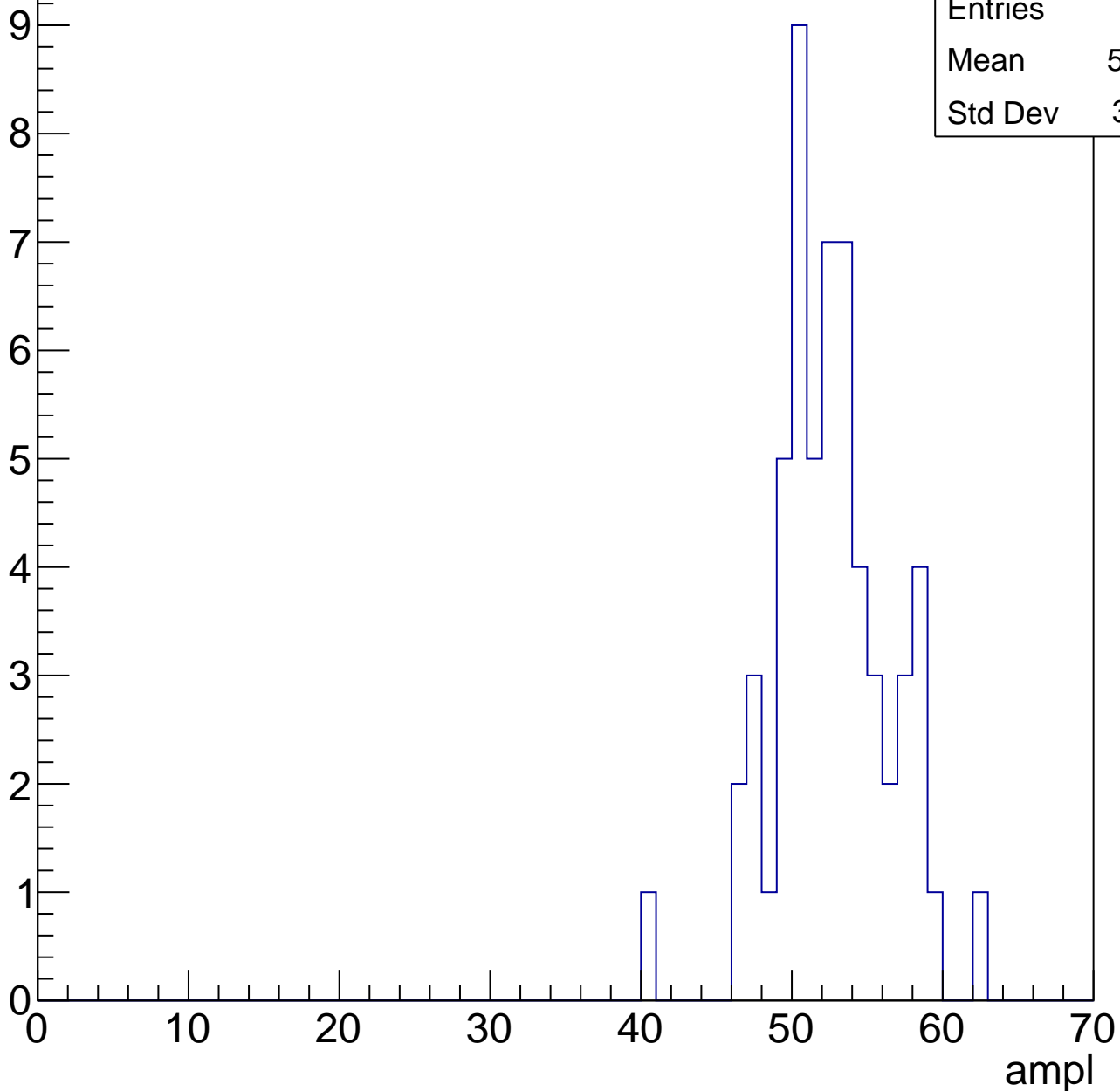


B1L100S, U15-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

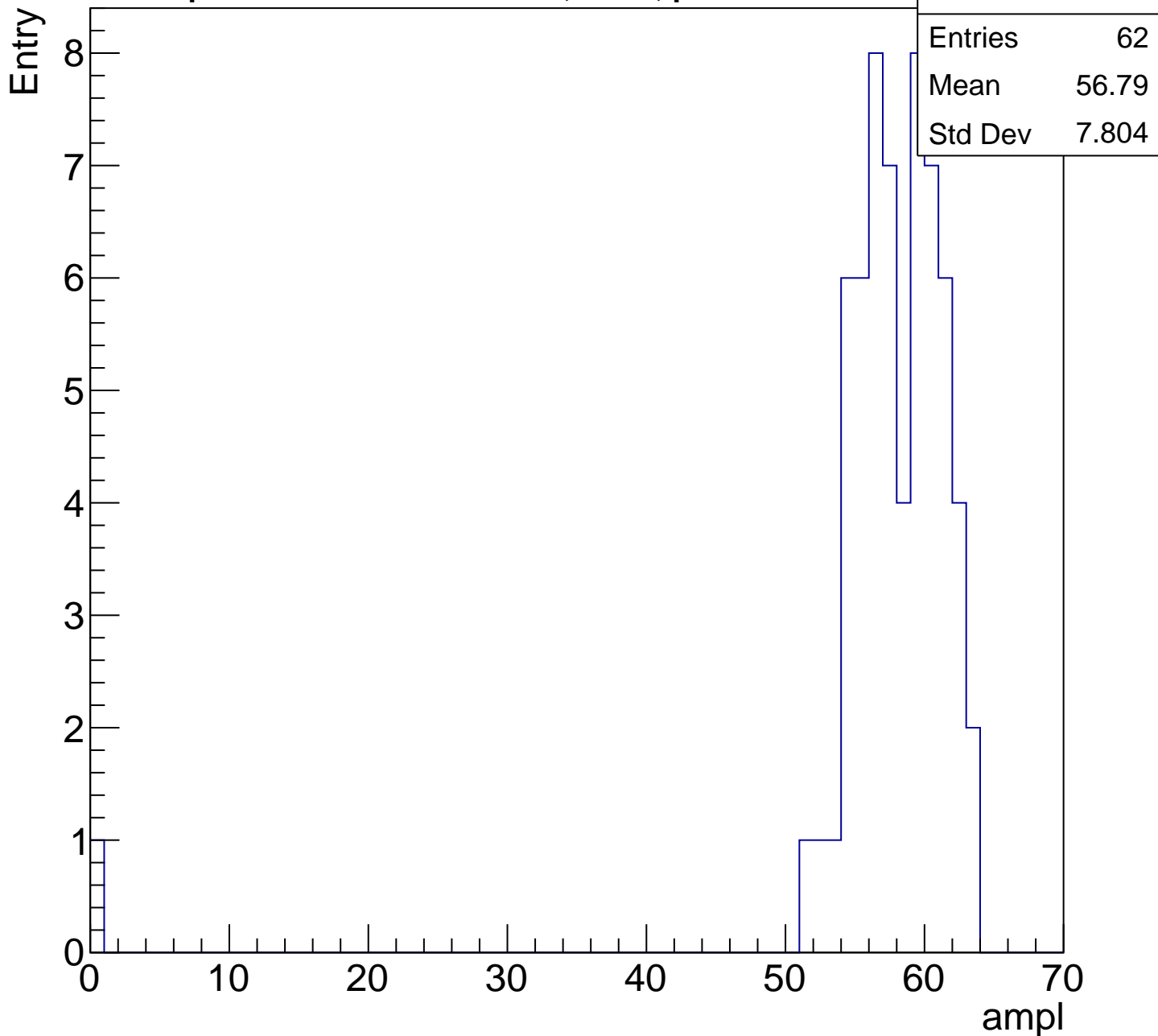
Entry

Entries	58
Mean	52.12
Std Dev	3.811



B1L100S, U15-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

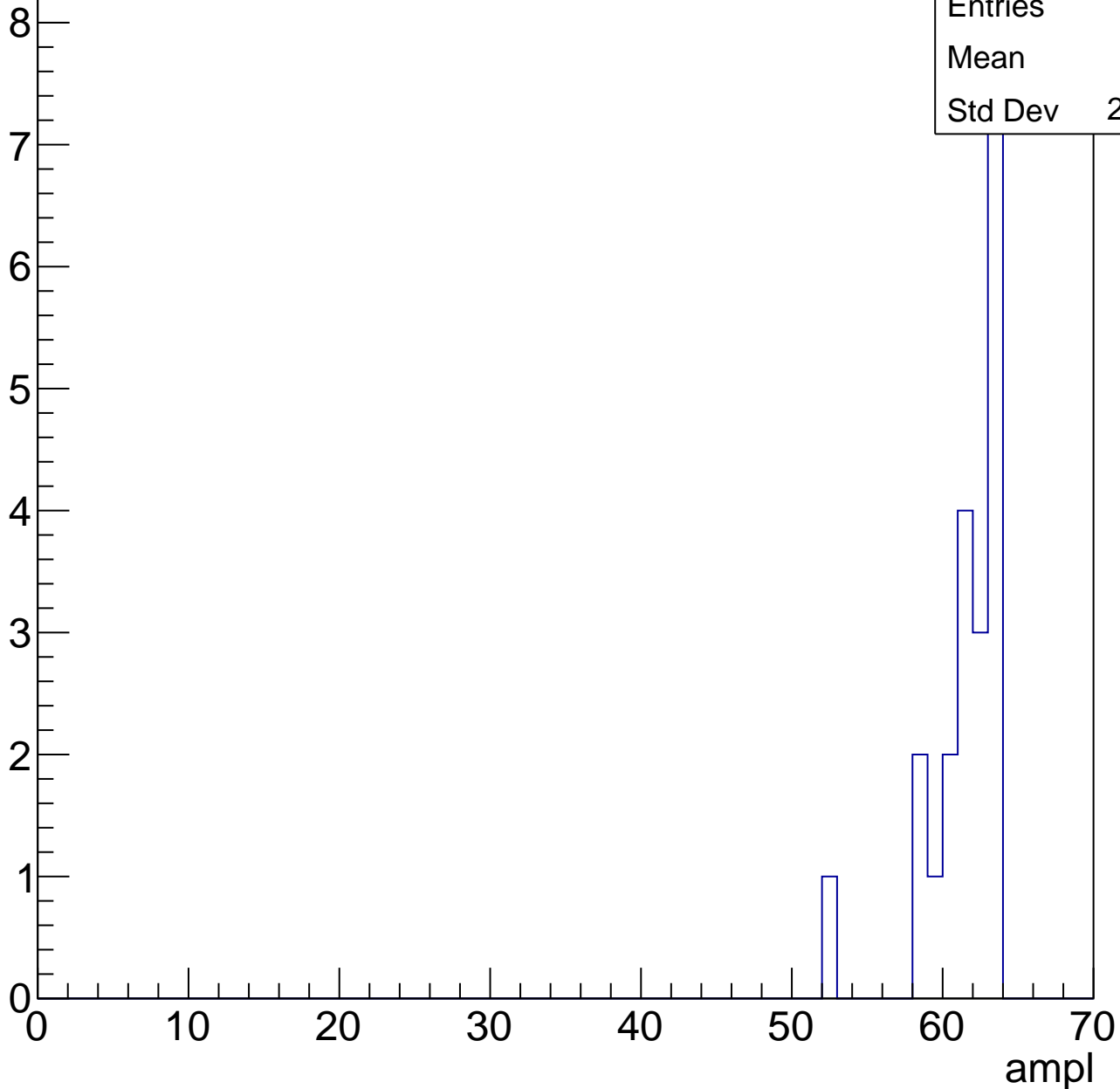


B1L100S, U15-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

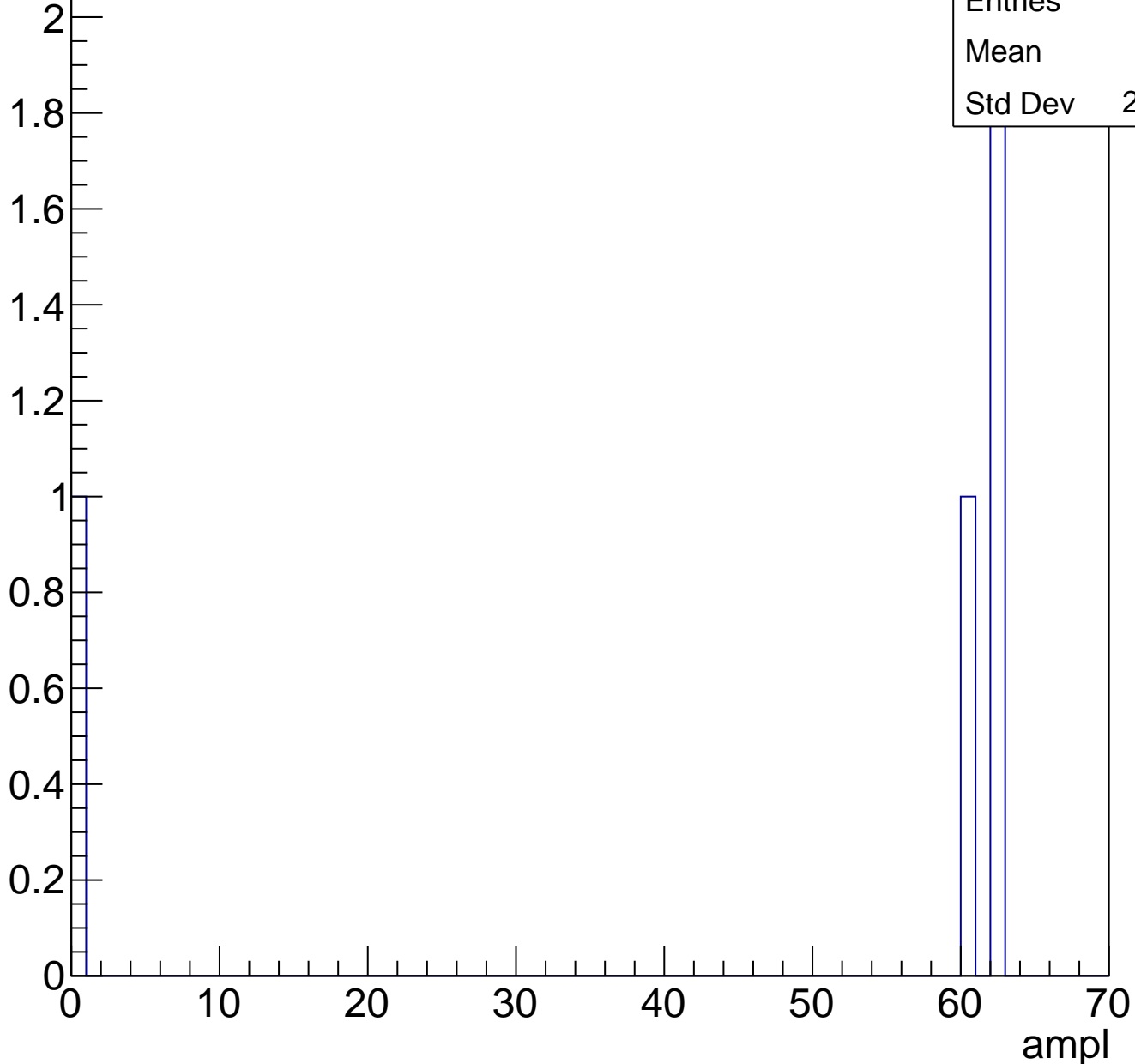
Entries	21
Mean	61
Std Dev	2.582



B1L100S, U15-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch23, adc0

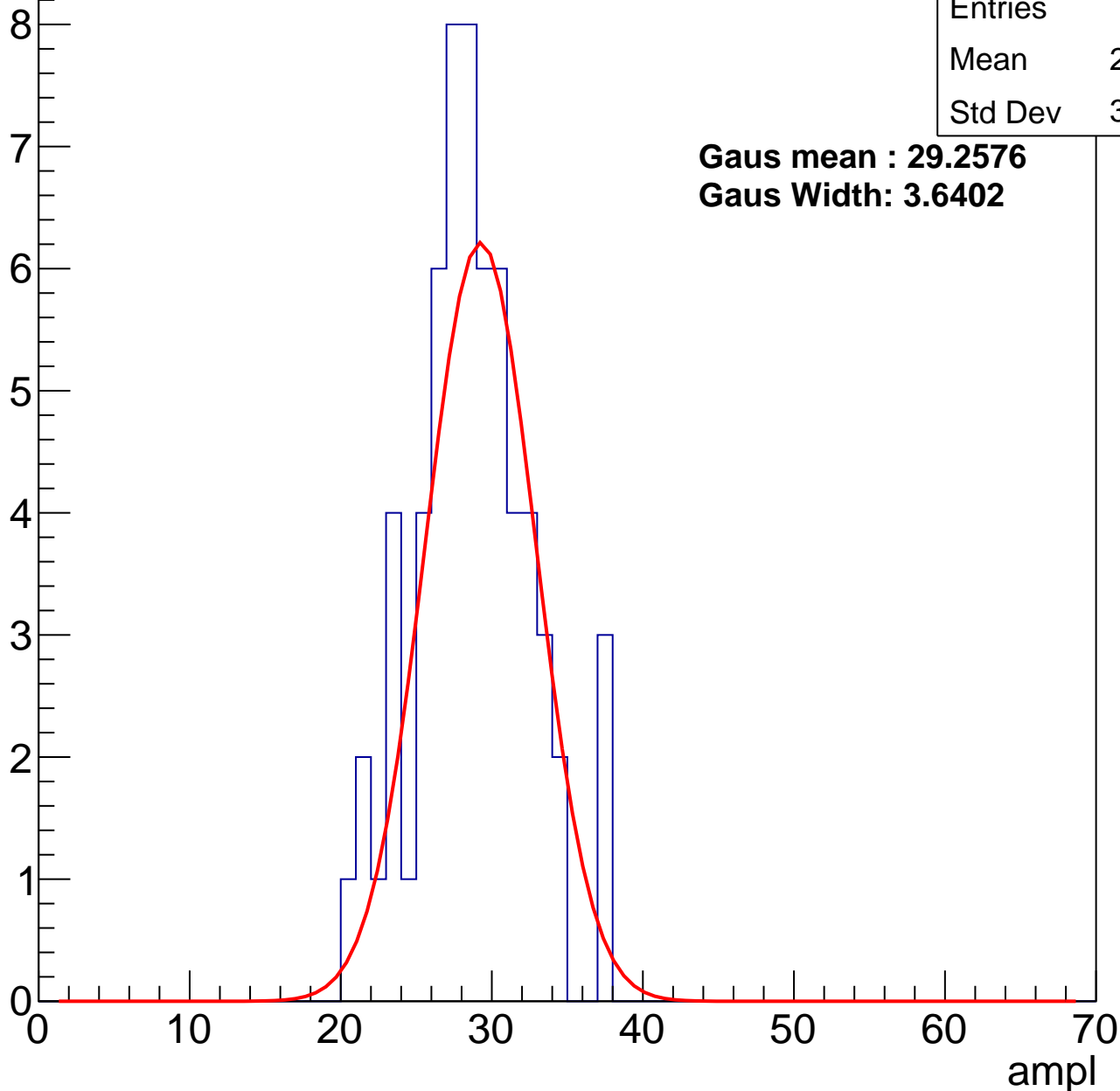
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.25
Std Dev	3.775

Gaus mean : 29.2576

Gaus Width: 3.6402



B1L100S, U15-ch23, adc1

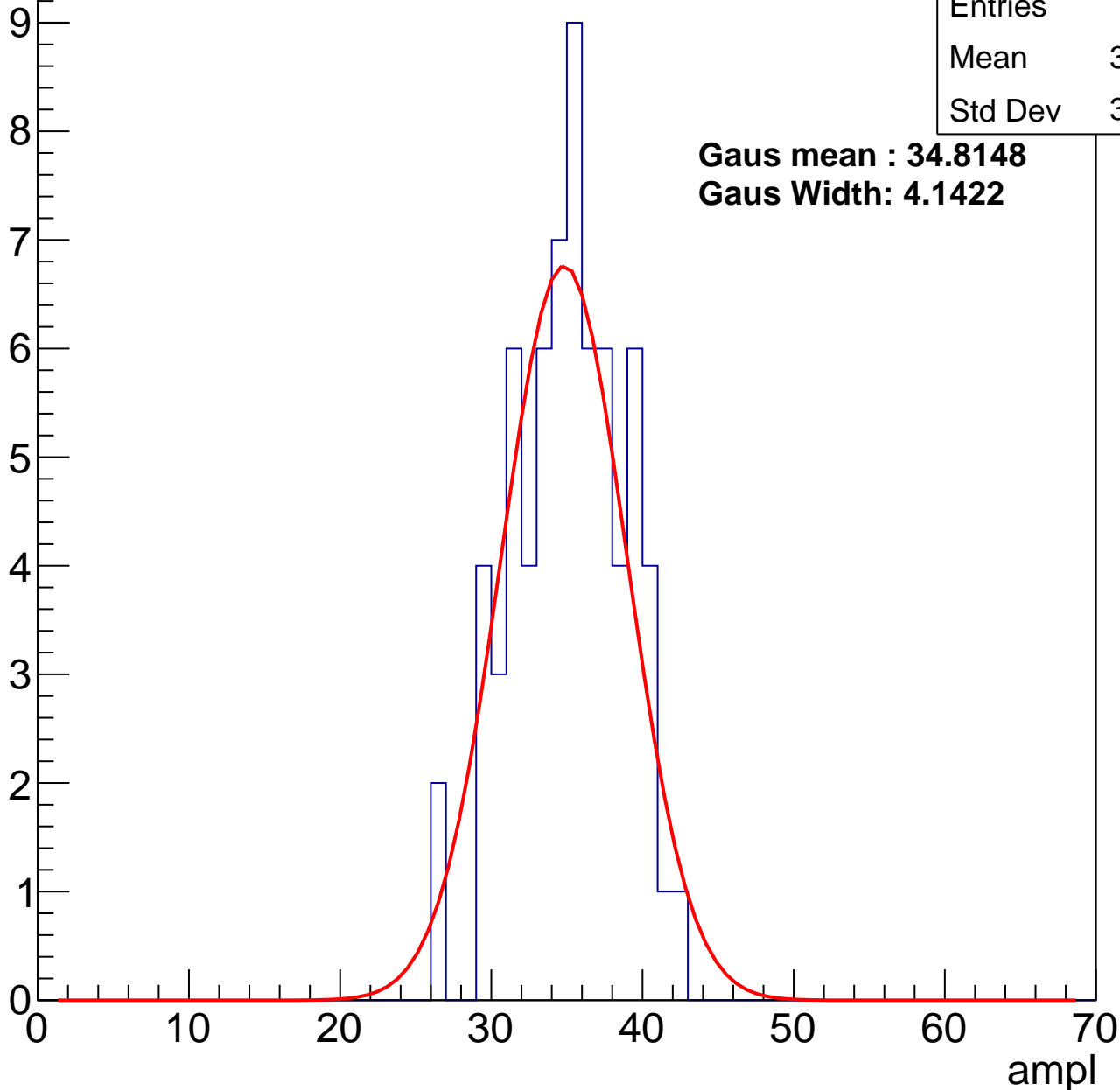
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	34.64
Std Dev	3.575

Gaus mean : 34.8148

Gaus Width: 4.1422



B1L100S, U15-ch23, adc2

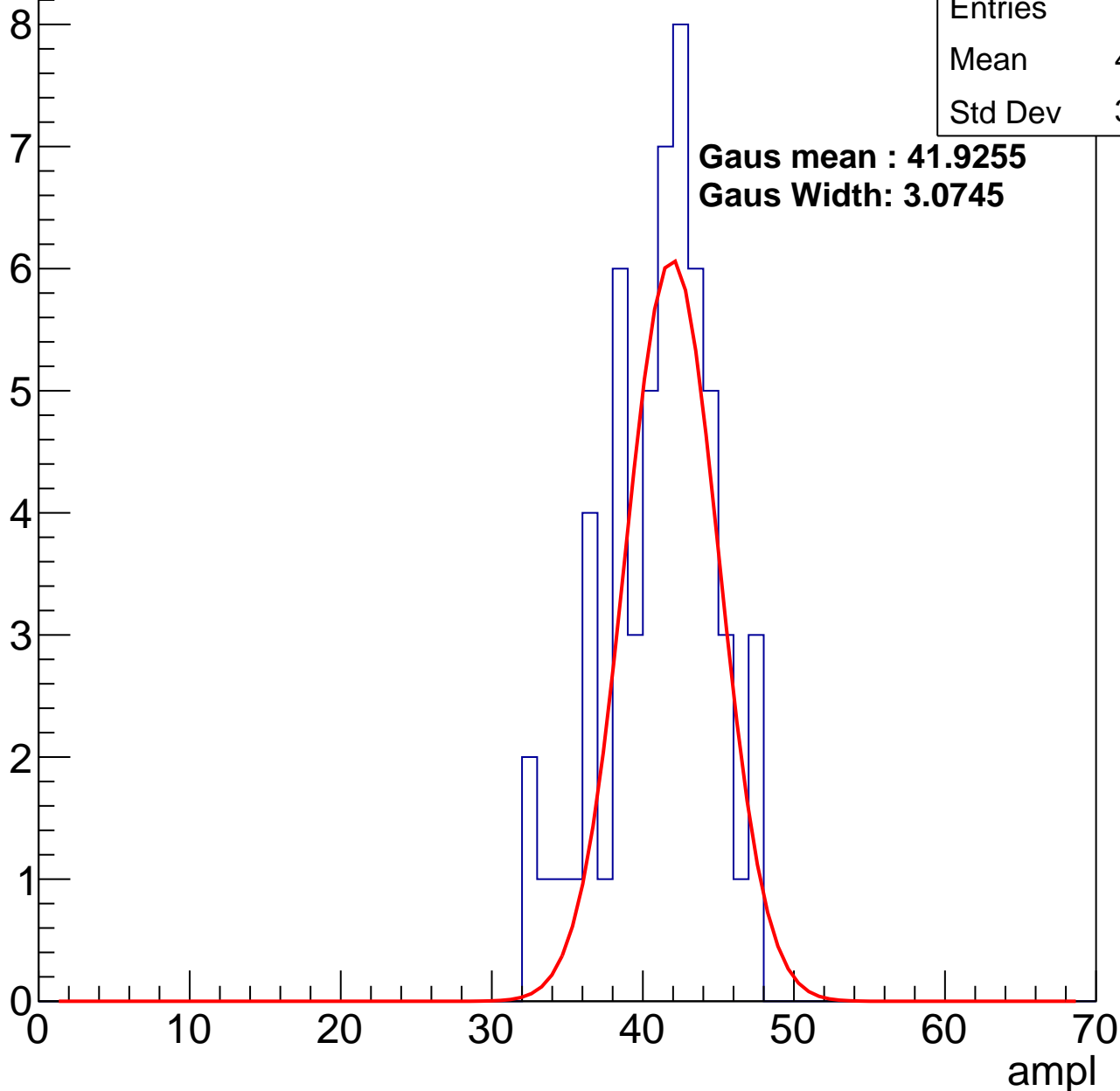
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	40.61
Std Dev	3.621

Gaus mean : 41.9255

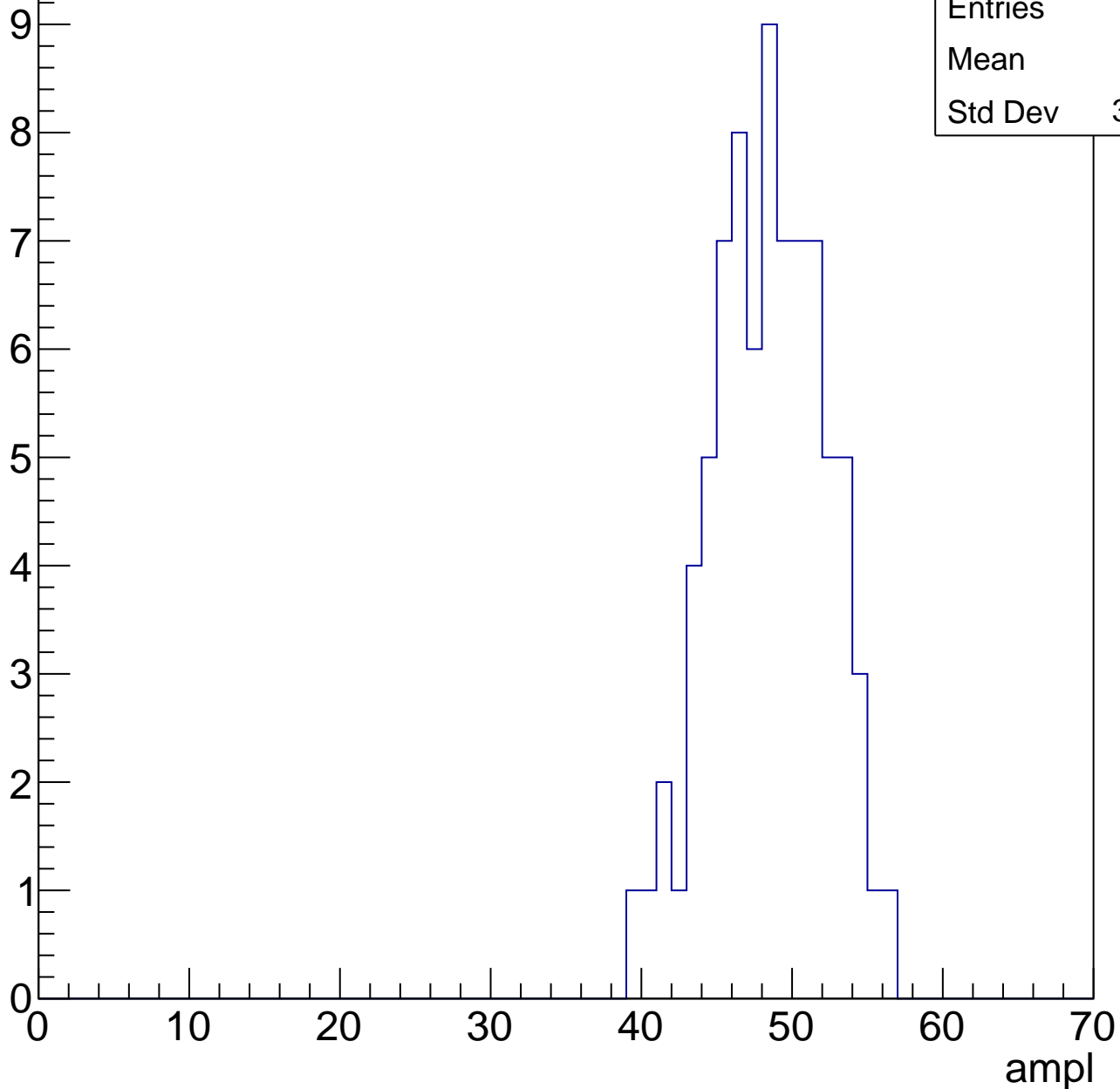
Gaus Width: 3.0745



B1L100S, U15-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

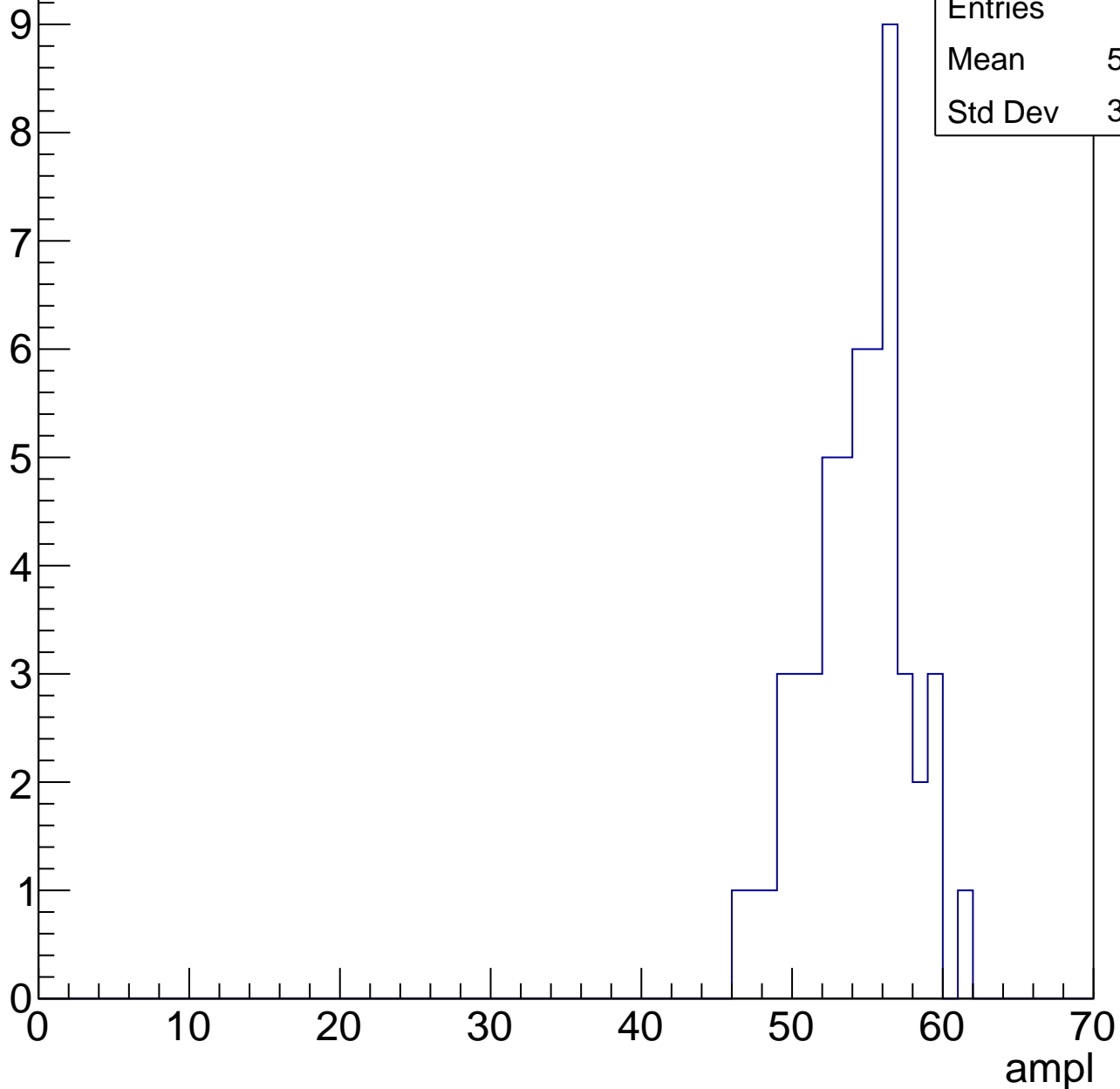
Entry



B1L100S, U15-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

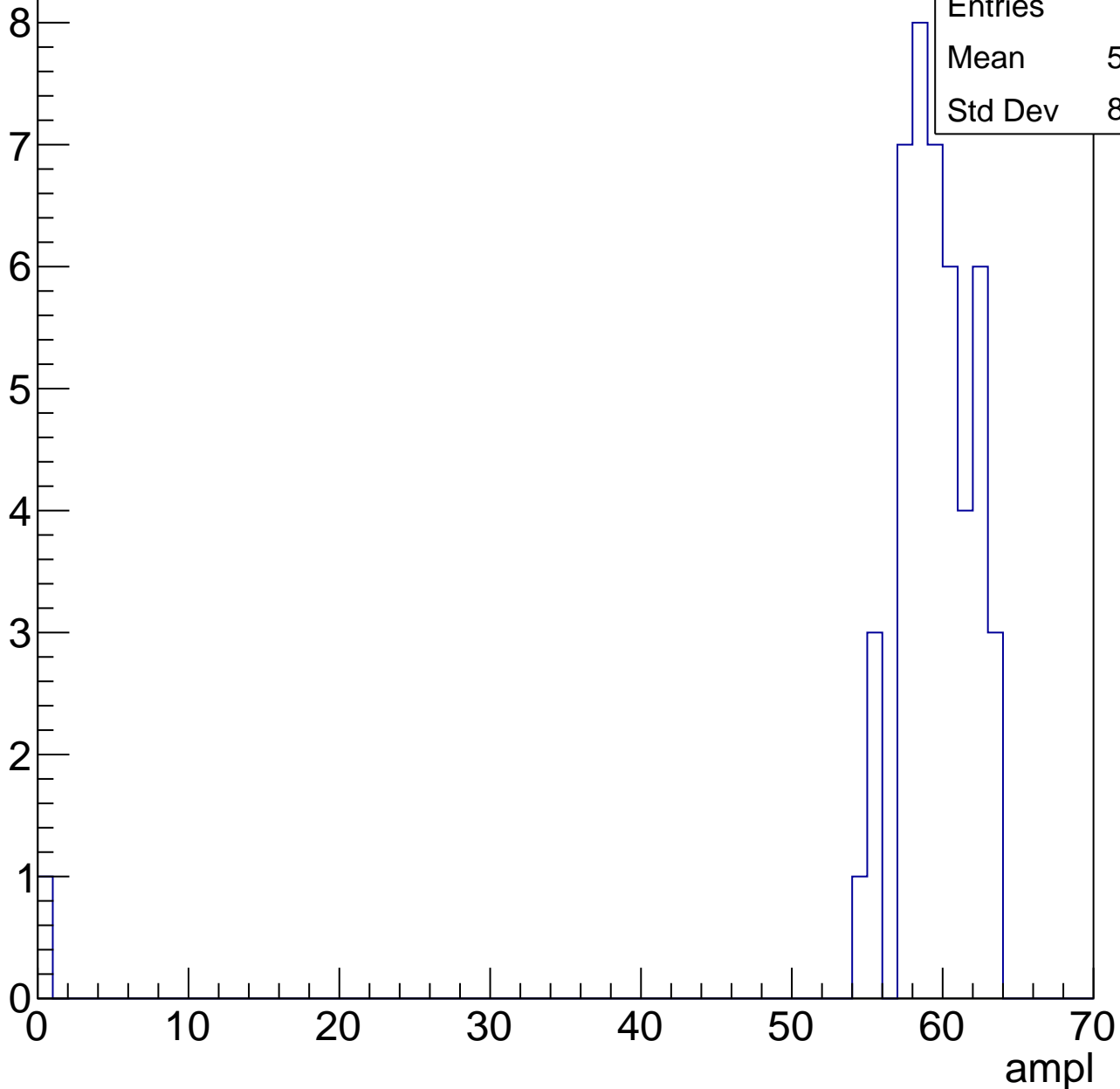


B1L100S, U15-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	57.83
Std Dev	8.908

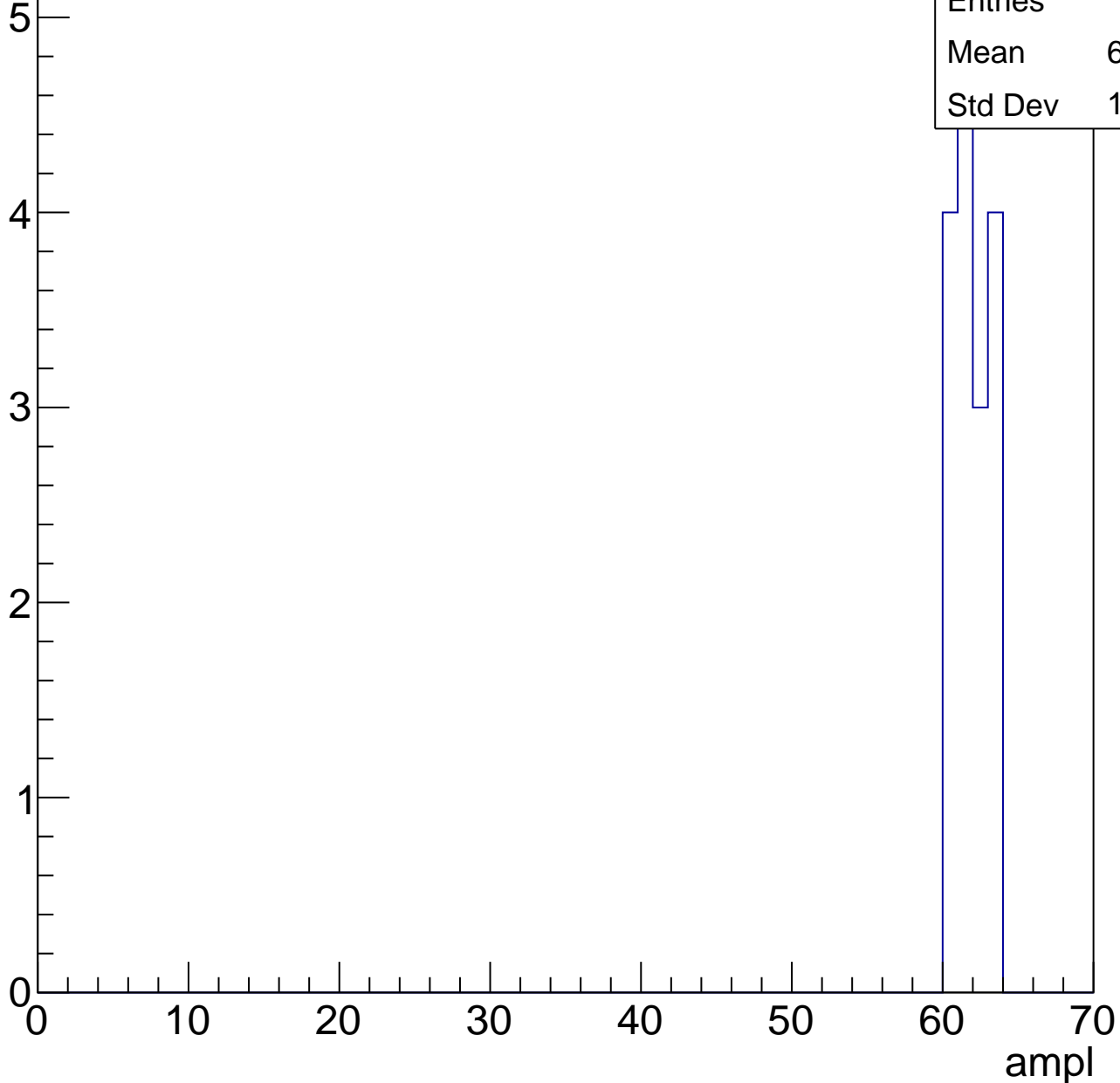


B1L100S, U15-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

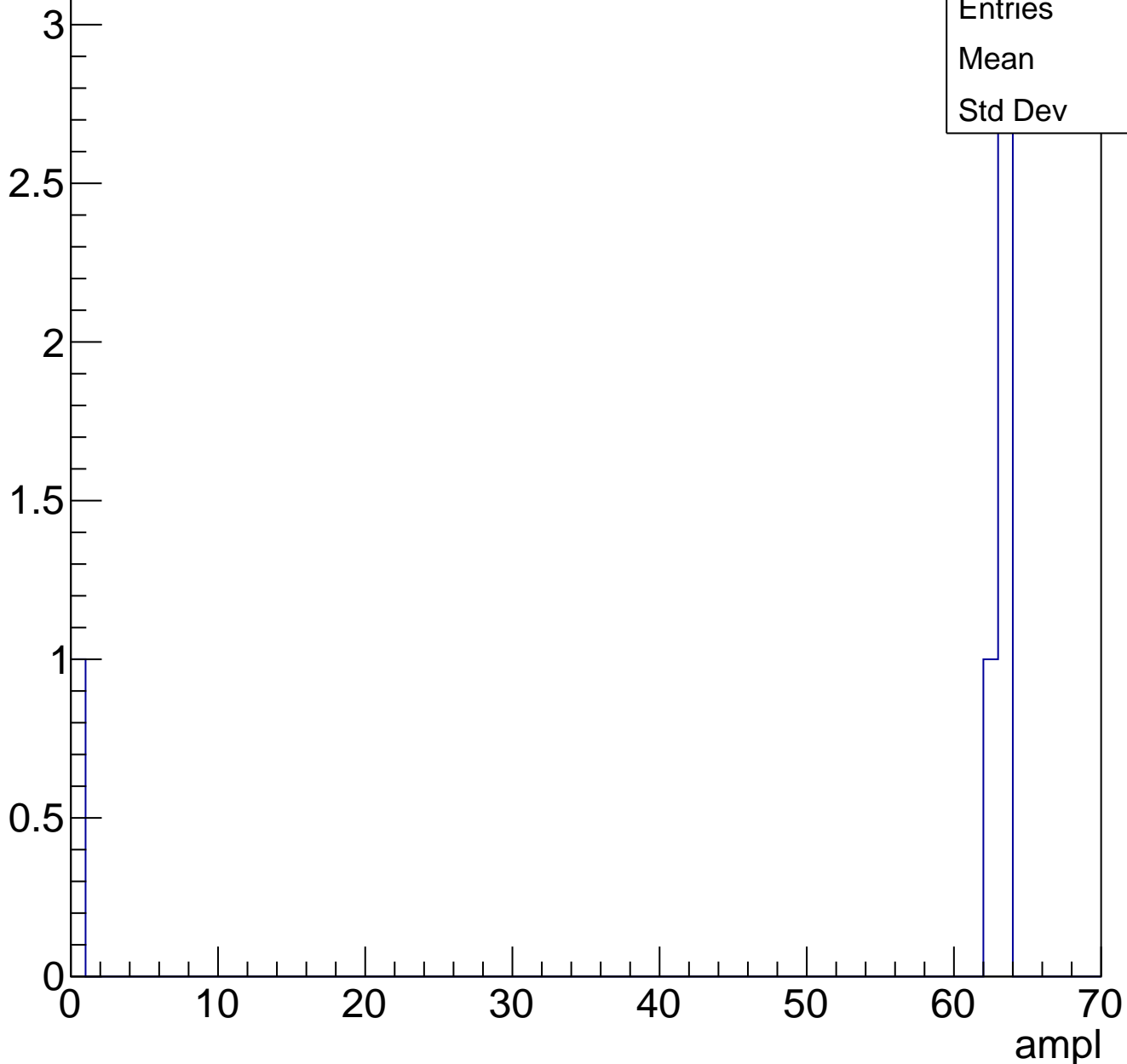
Entries	16
Mean	61.44
Std Dev	1.116



B1L100S, U15-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch24, adc0

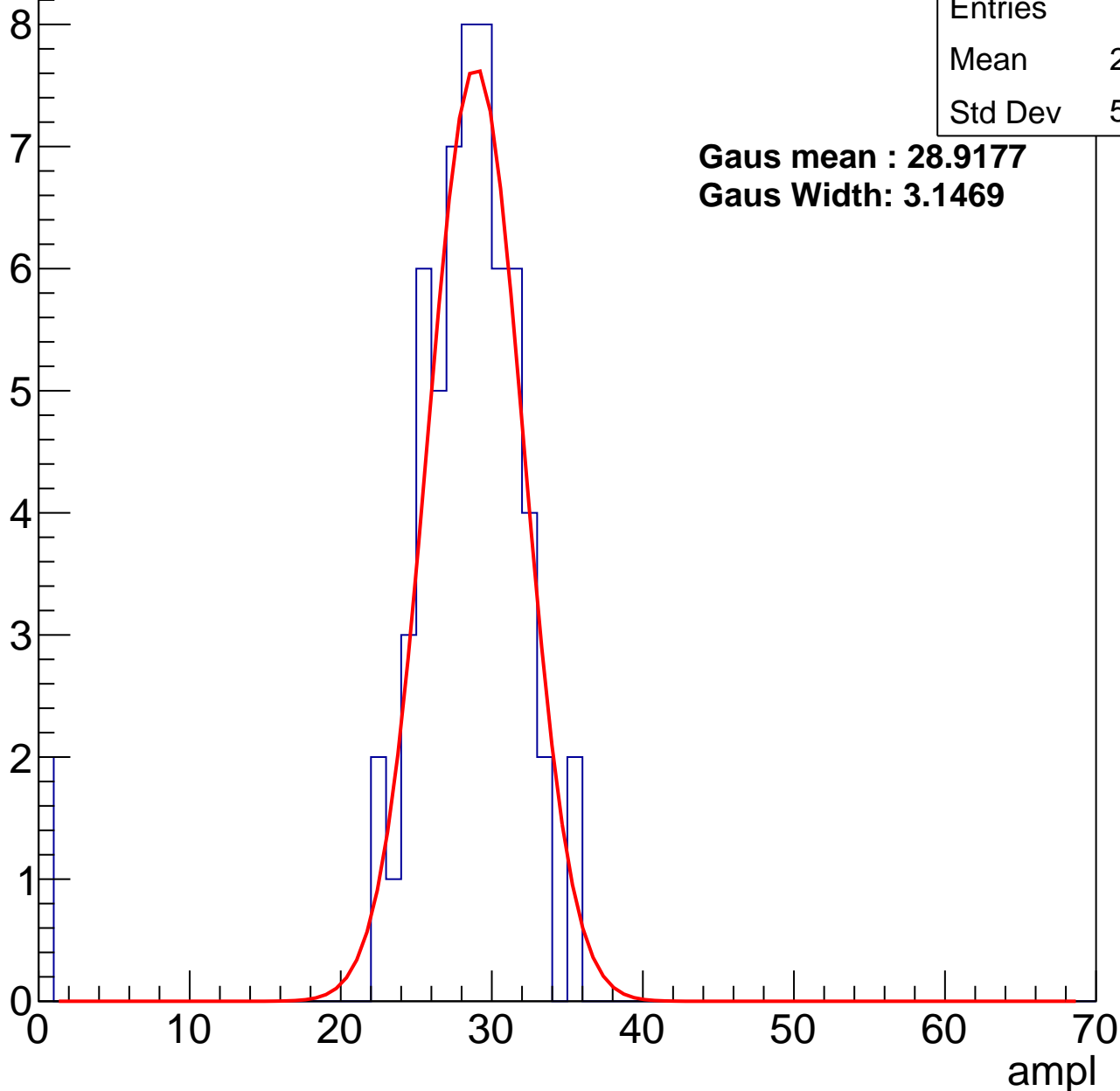
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.32
Std Dev	5.769

Gaus mean : 28.9177

Gaus Width: 3.1469



B1L100S, U15-ch24, adc1

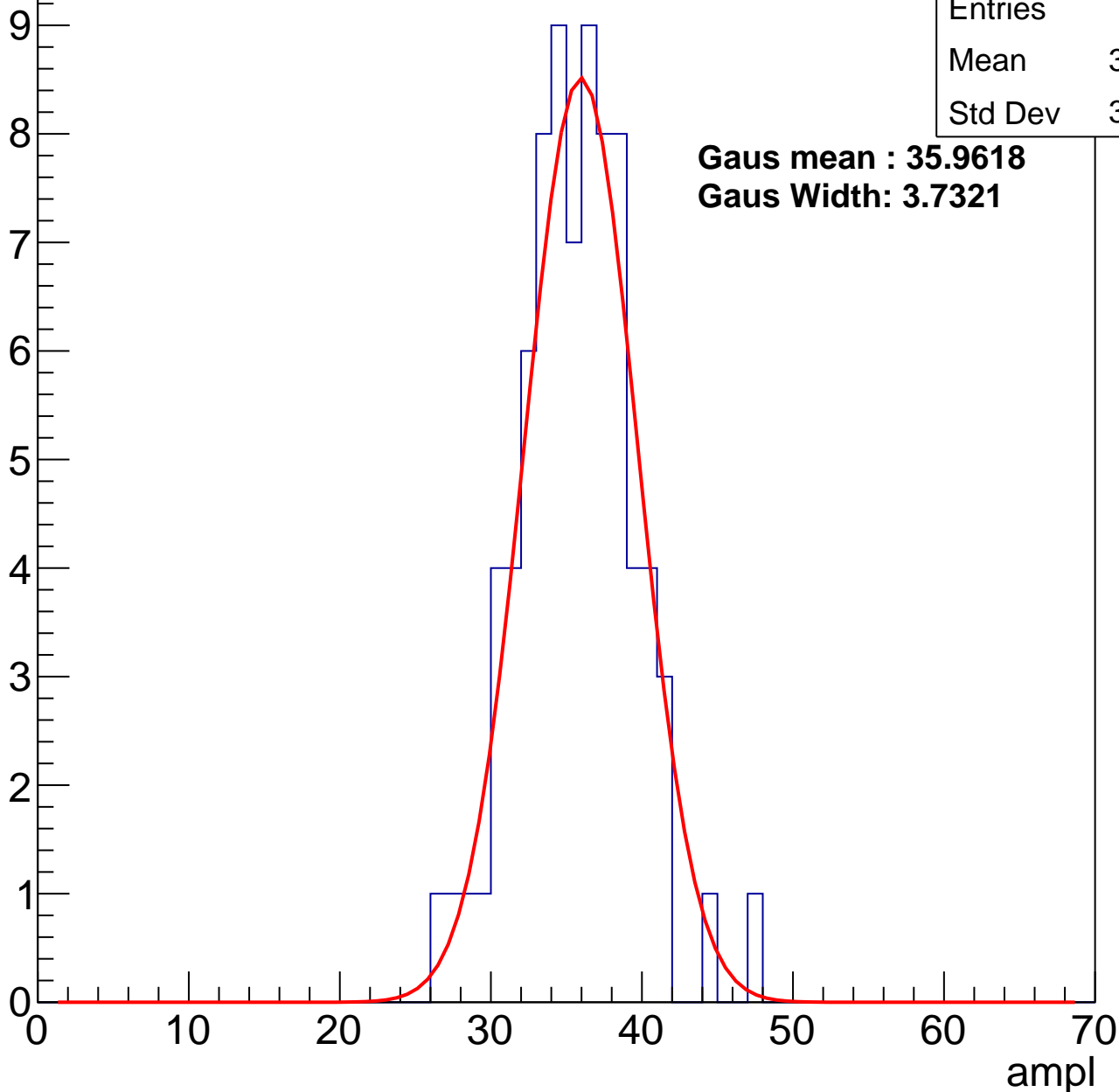
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	35.19
Std Dev	3.702

Gaus mean : 35.9618

Gaus Width: 3.7321



B1L100S, U15-ch24, adc2

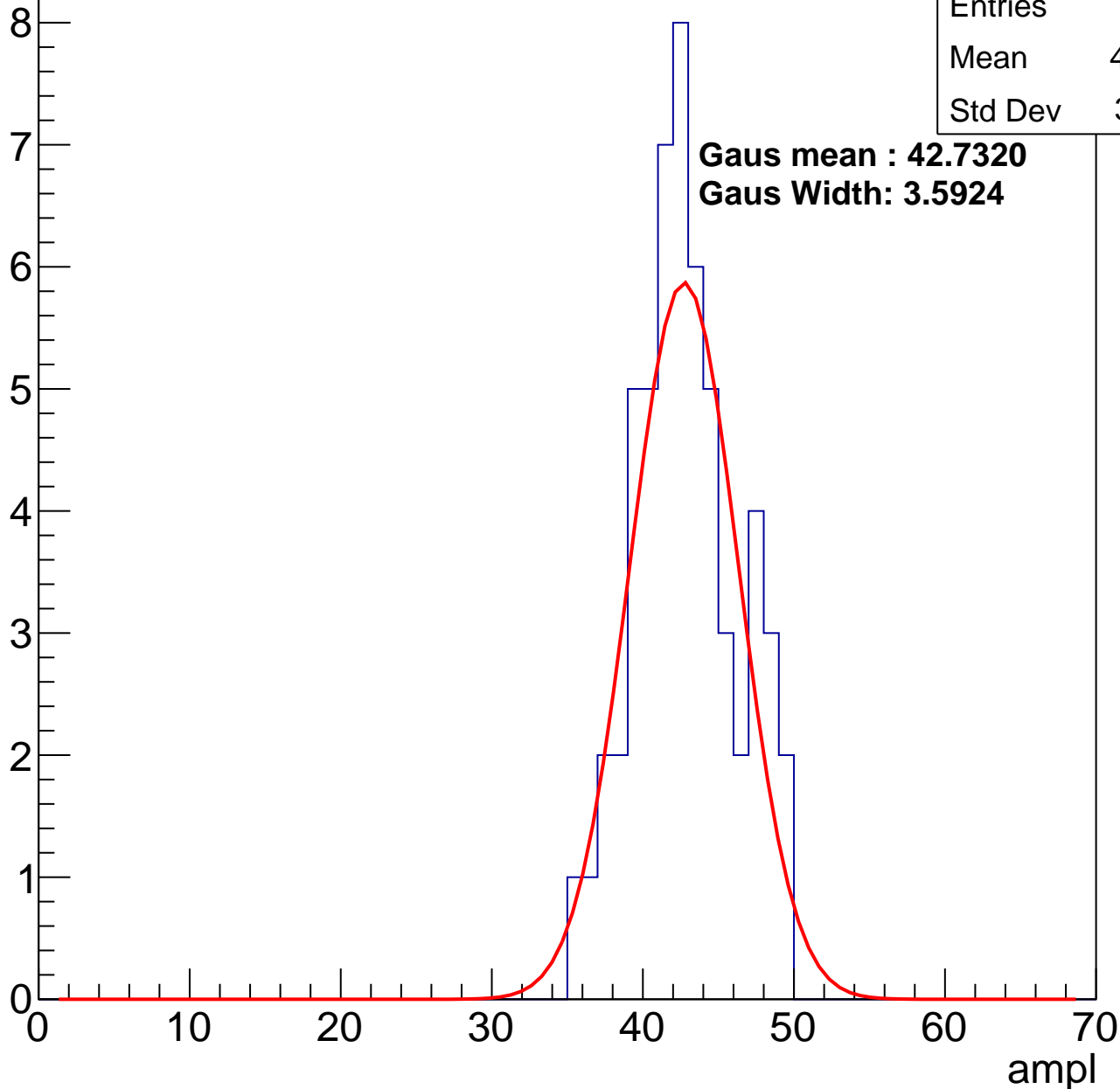
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.39
Std Dev	3.331

Gaus mean : 42.7320

Gaus Width: 3.5924

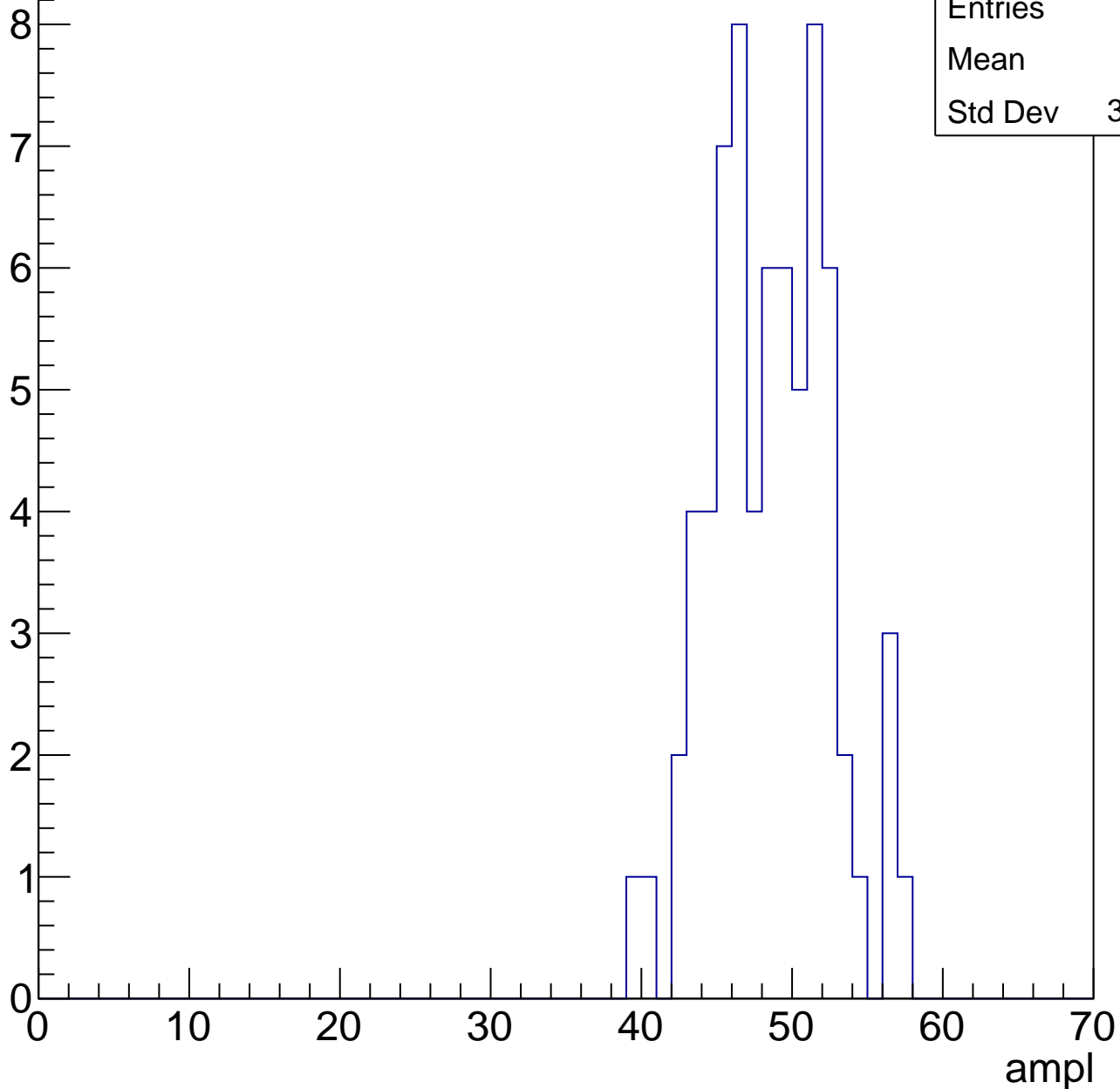


B1L100S, U15-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	48.1
Std Dev	3.868

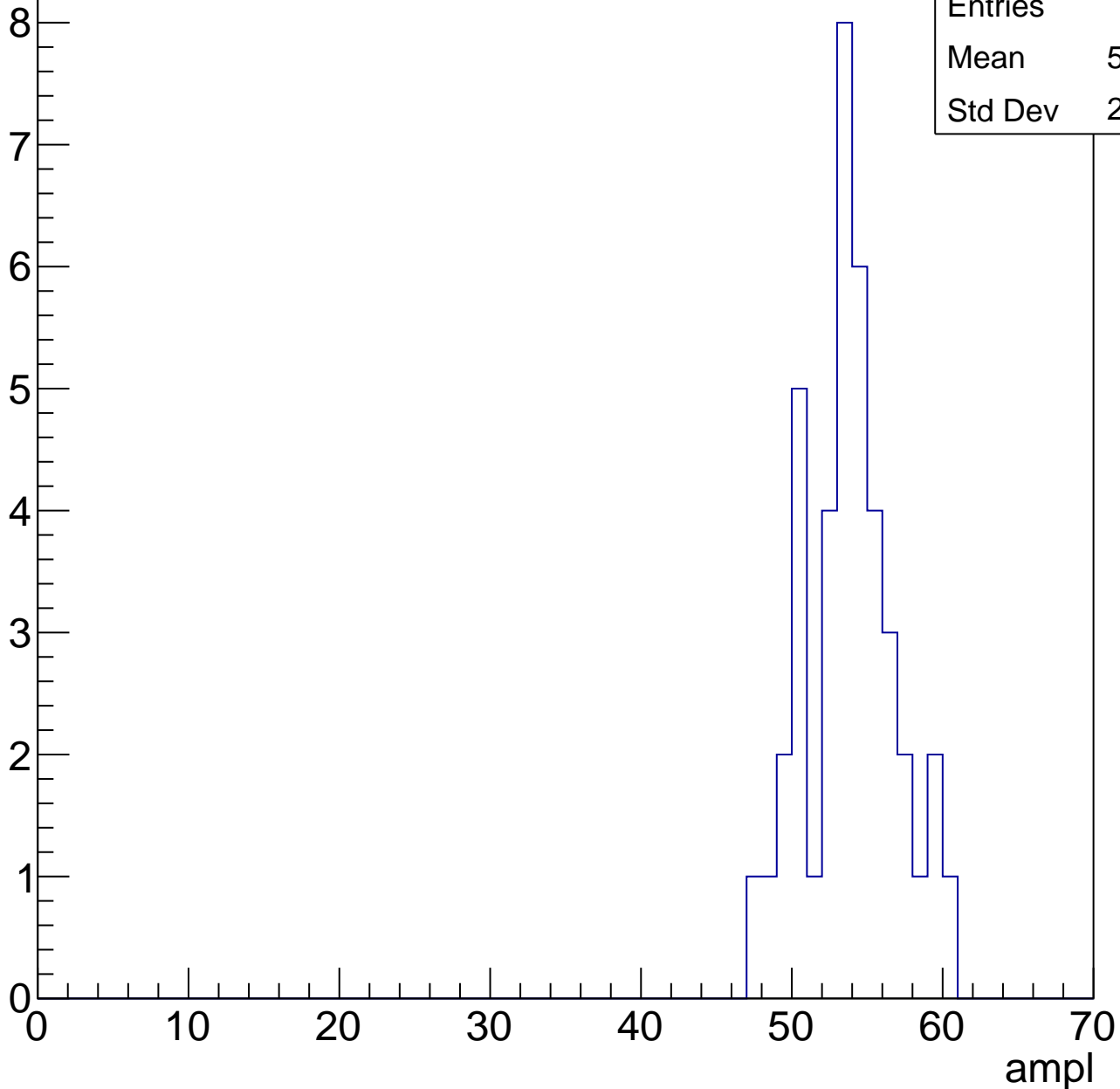


B1L100S, U15-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	53.37
Std Dev	2.994



B1L100S, U15-ch24, adc5

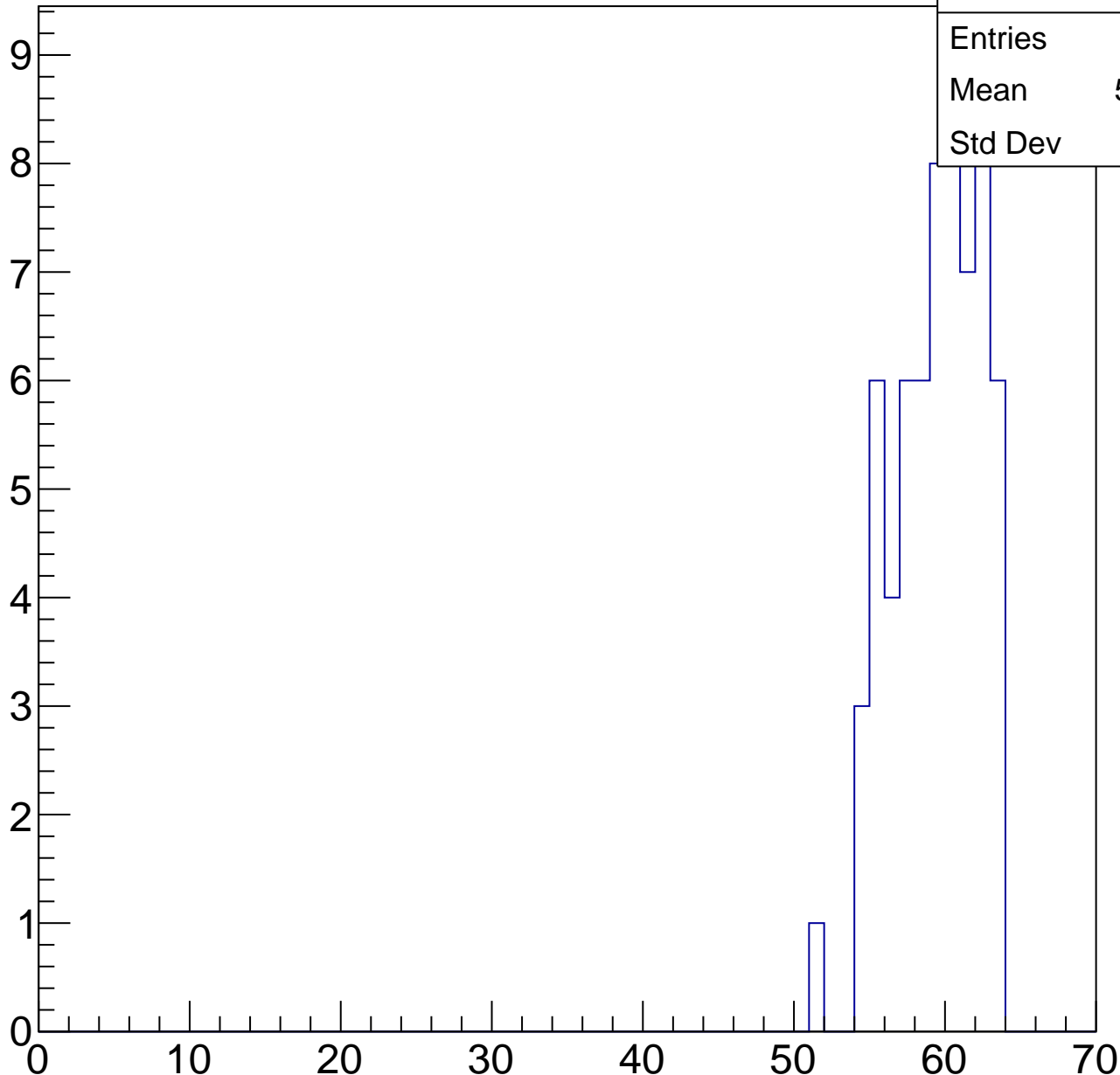
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	64
Mean	58.91
Std Dev	2.81

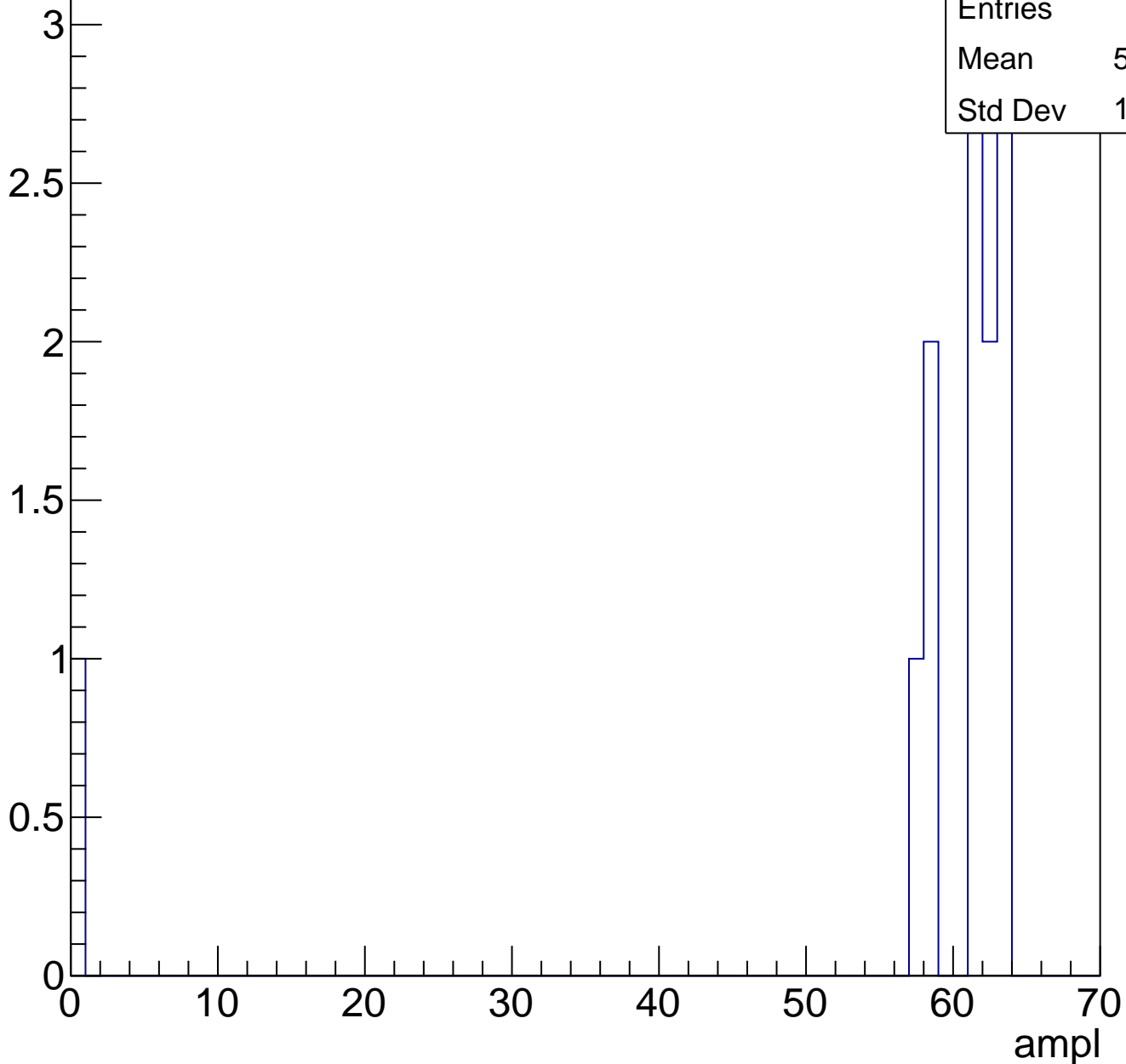
ampl



B1L100S, U15-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

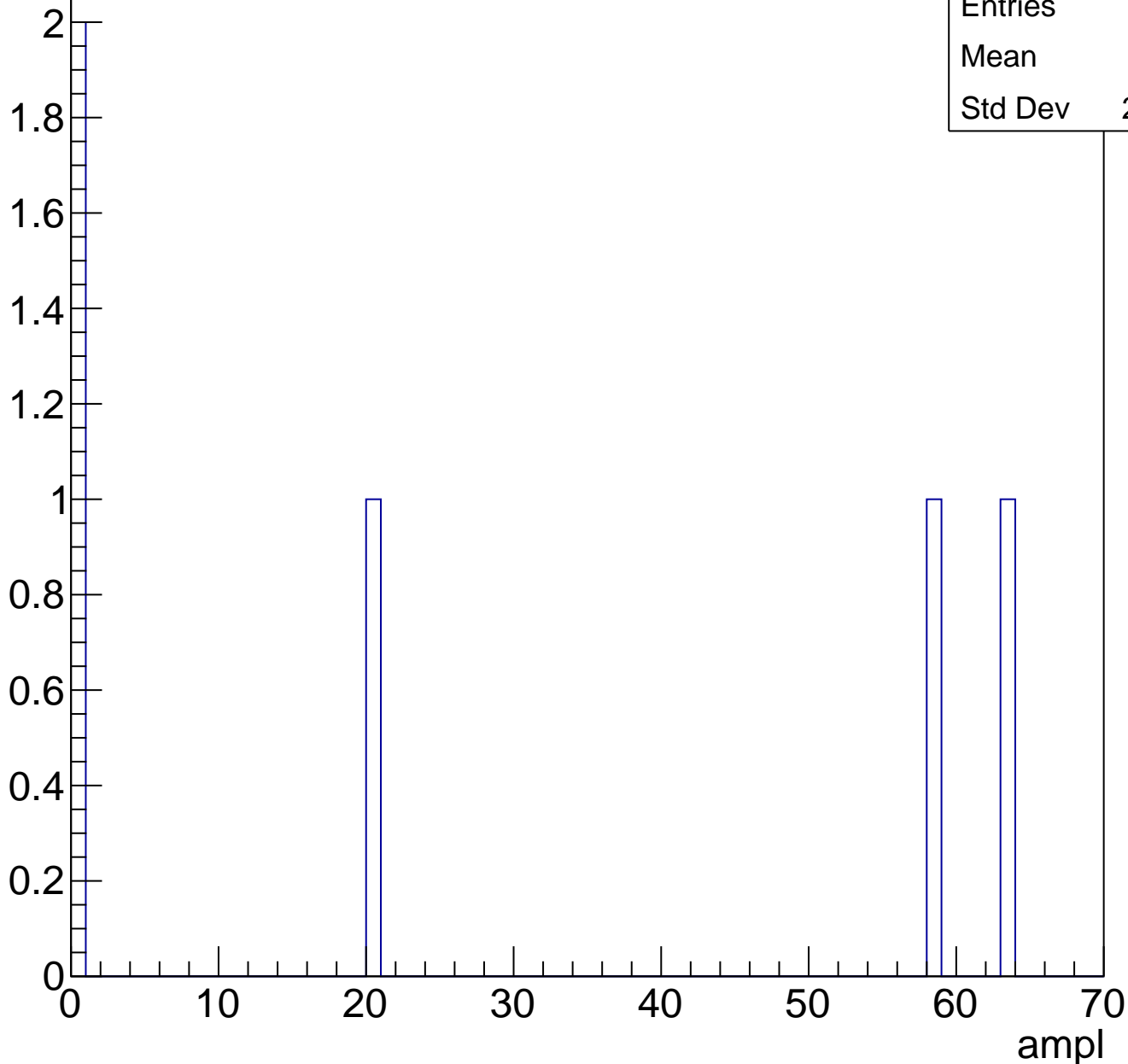
Entry



B1L100S, U15-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch25, adc0

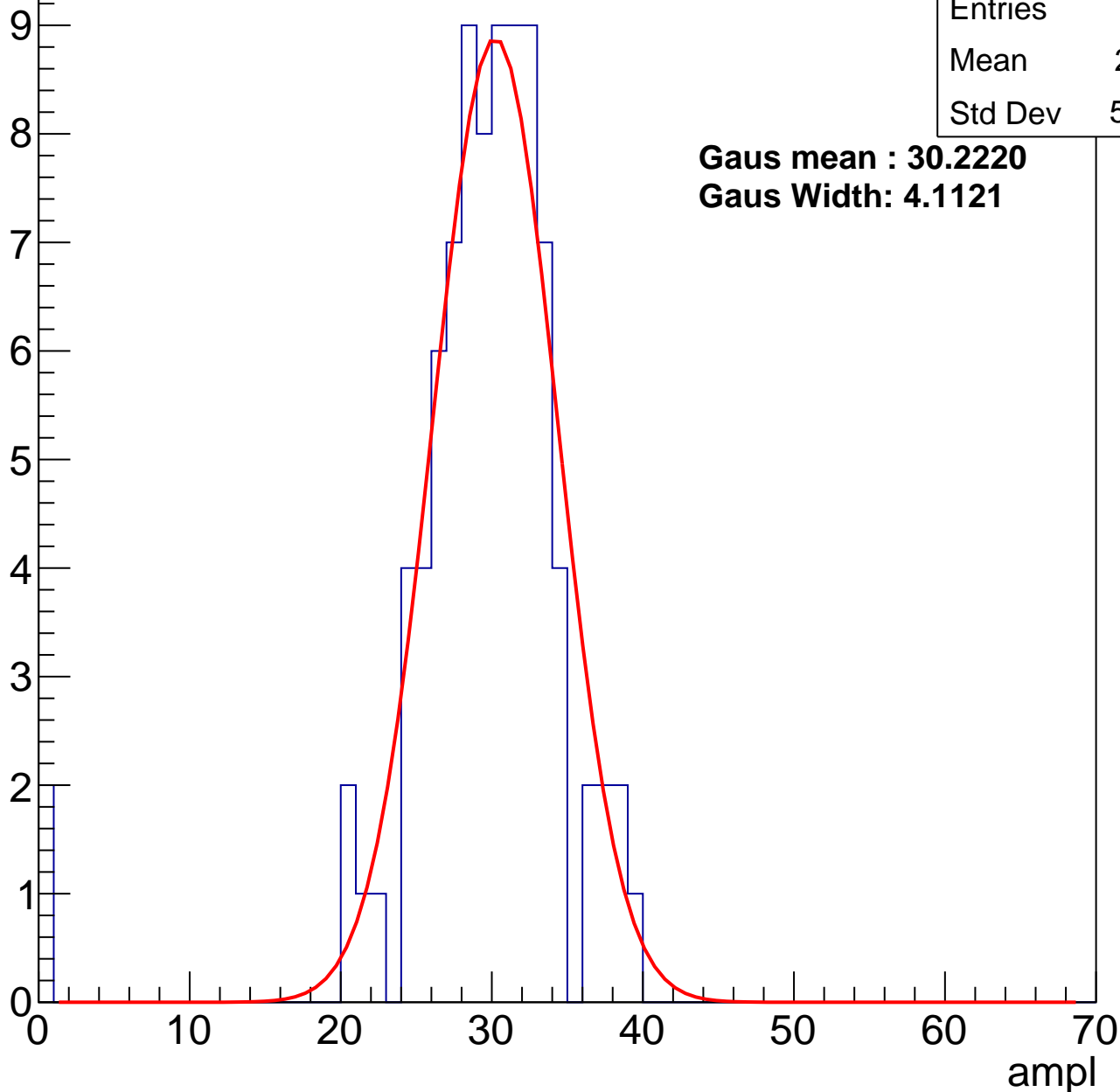
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	28.91
Std Dev	5.842

Gaus mean : 30.2220

Gaus Width: 4.1121



B1L100S, U15-ch25, adc1

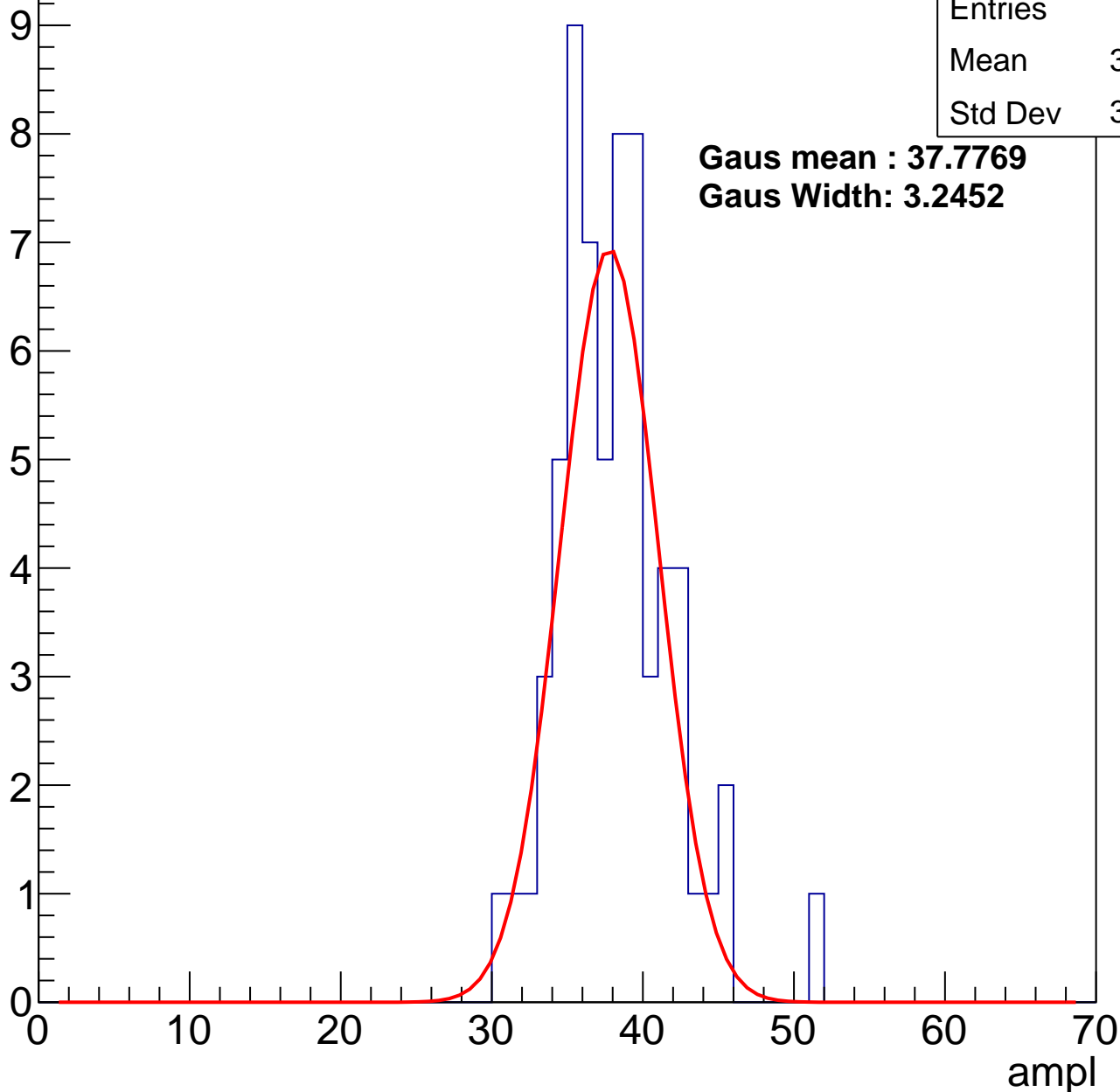
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	37.66
Std Dev	3.662

Gaus mean : 37.7769

Gaus Width: 3.2452



B1L100S, U15-ch25, adc2

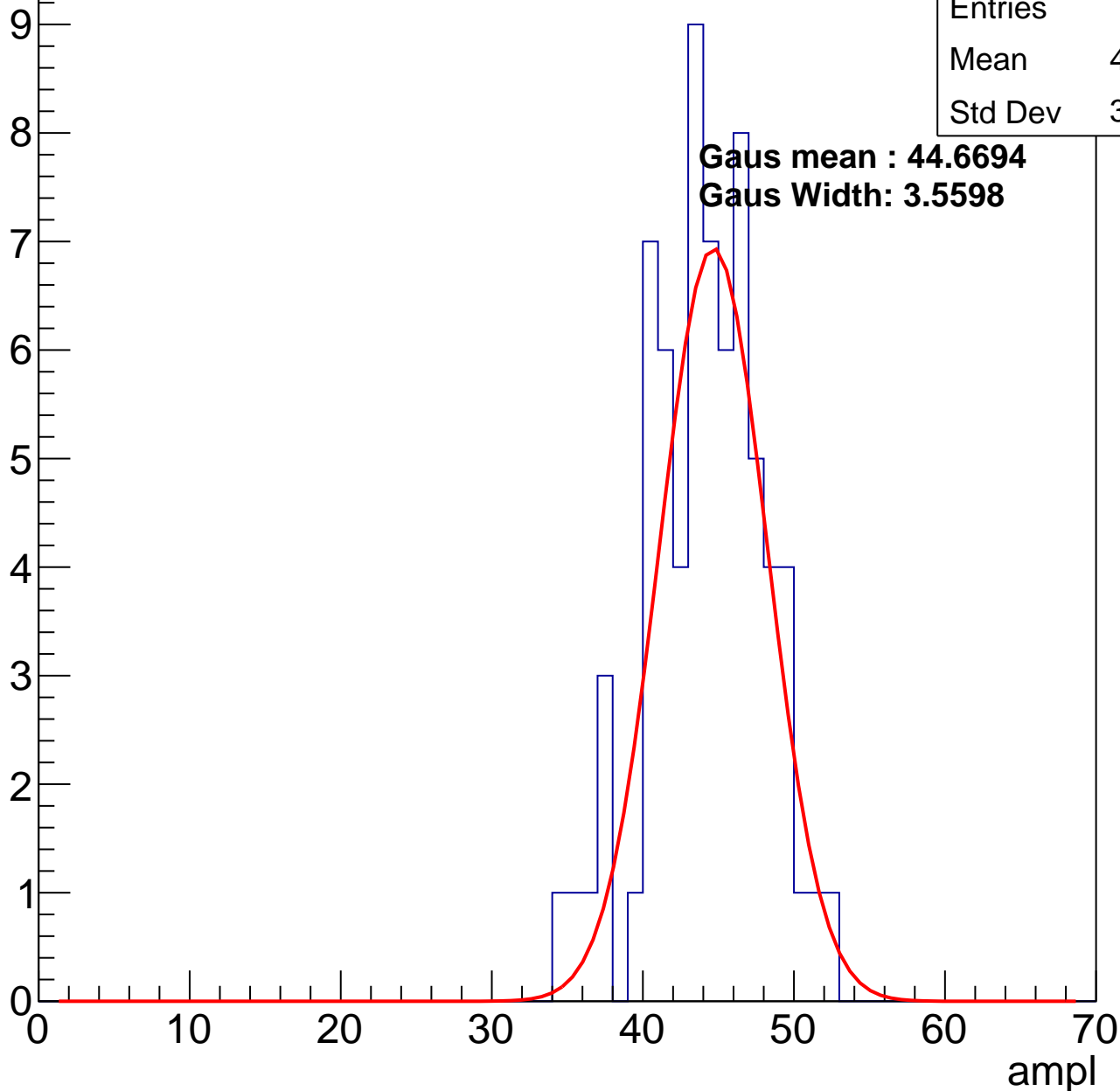
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	43.69
Std Dev	3.774

Gaus mean : 44.6694

Gaus Width: 3.5598

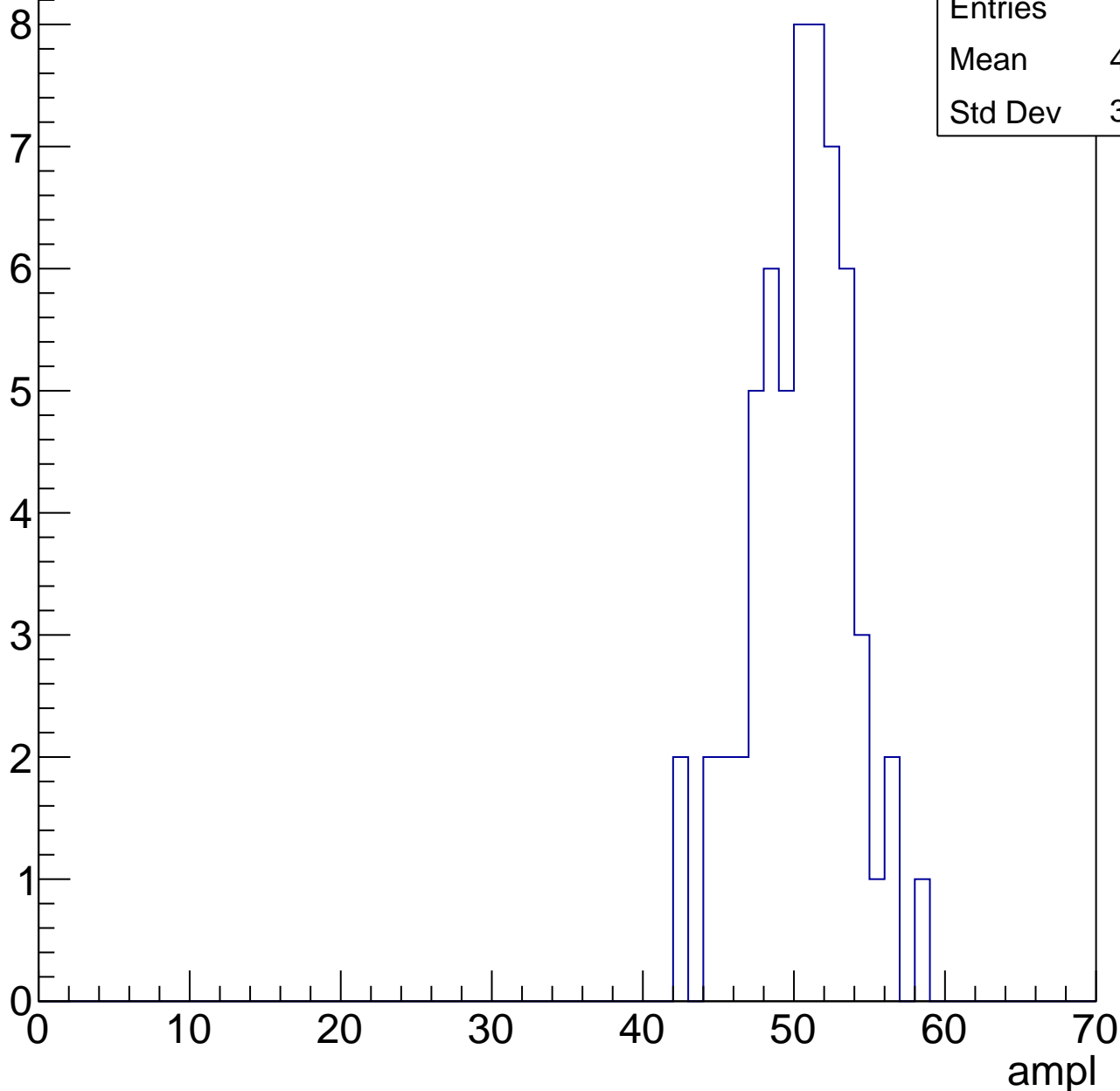


B1L100S, U15-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

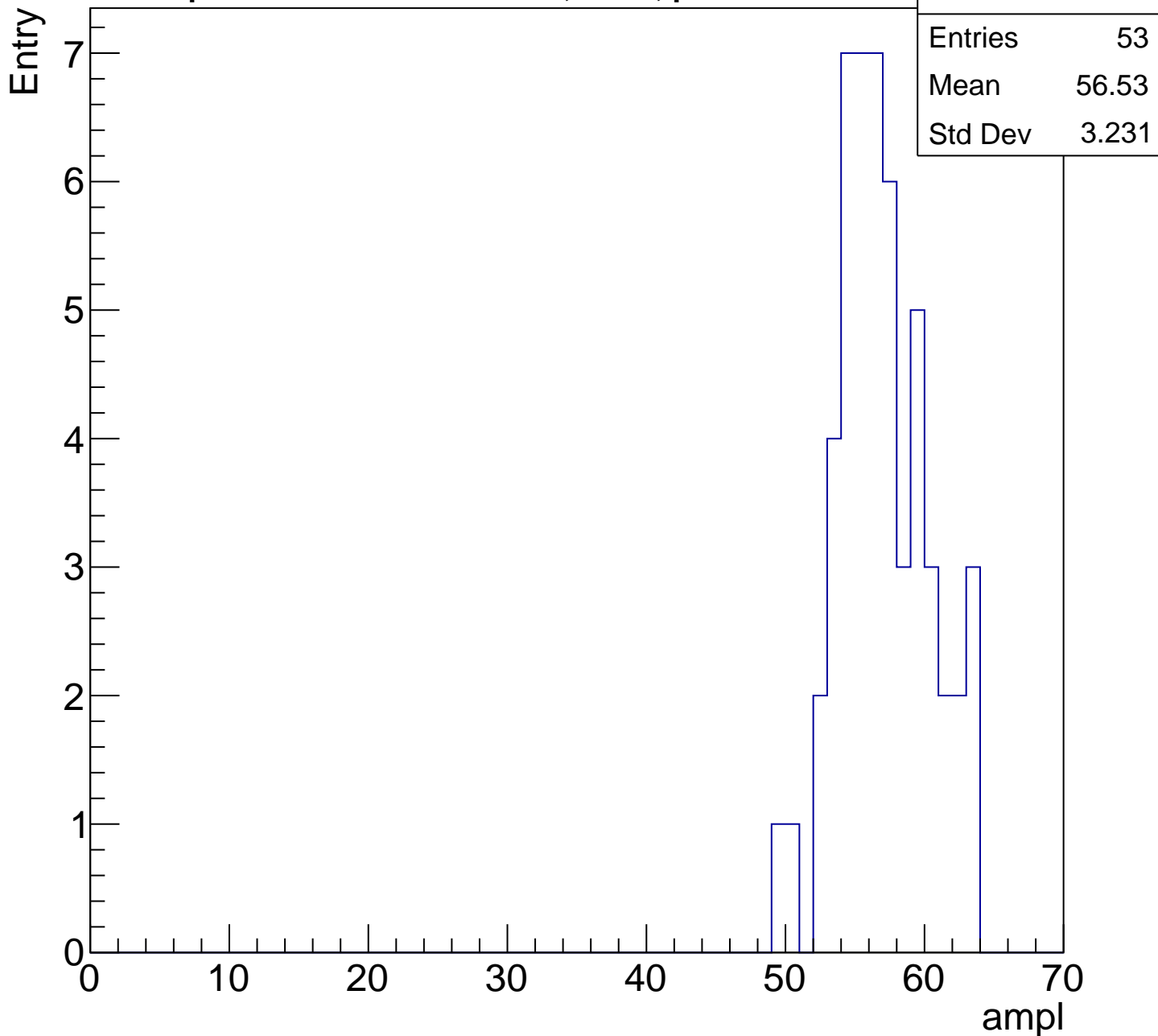
Entry

Entries	60
Mean	49.98
Std Dev	3.304



B1L100S, U15-ch25, adc4

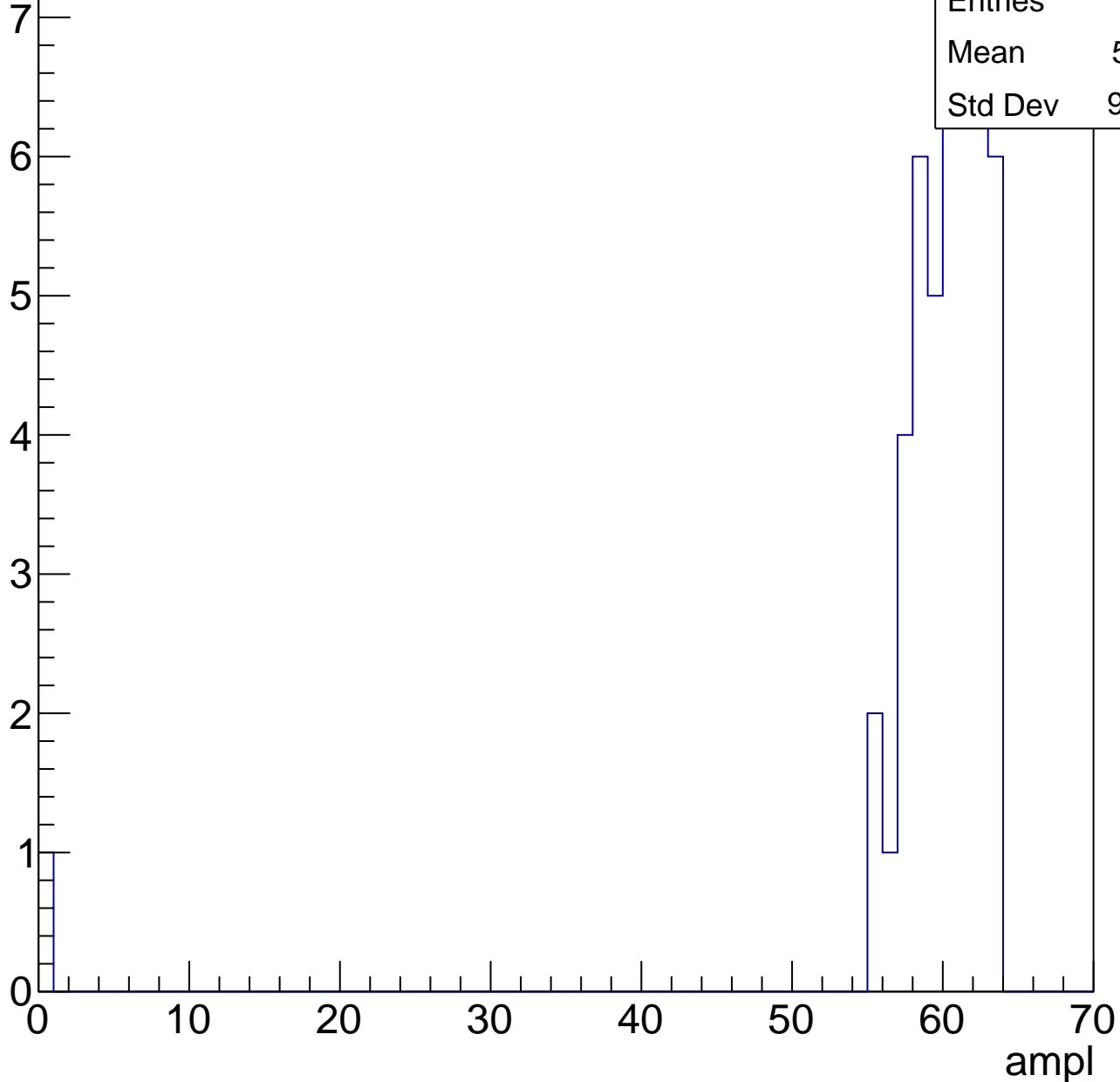
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch26, adc0

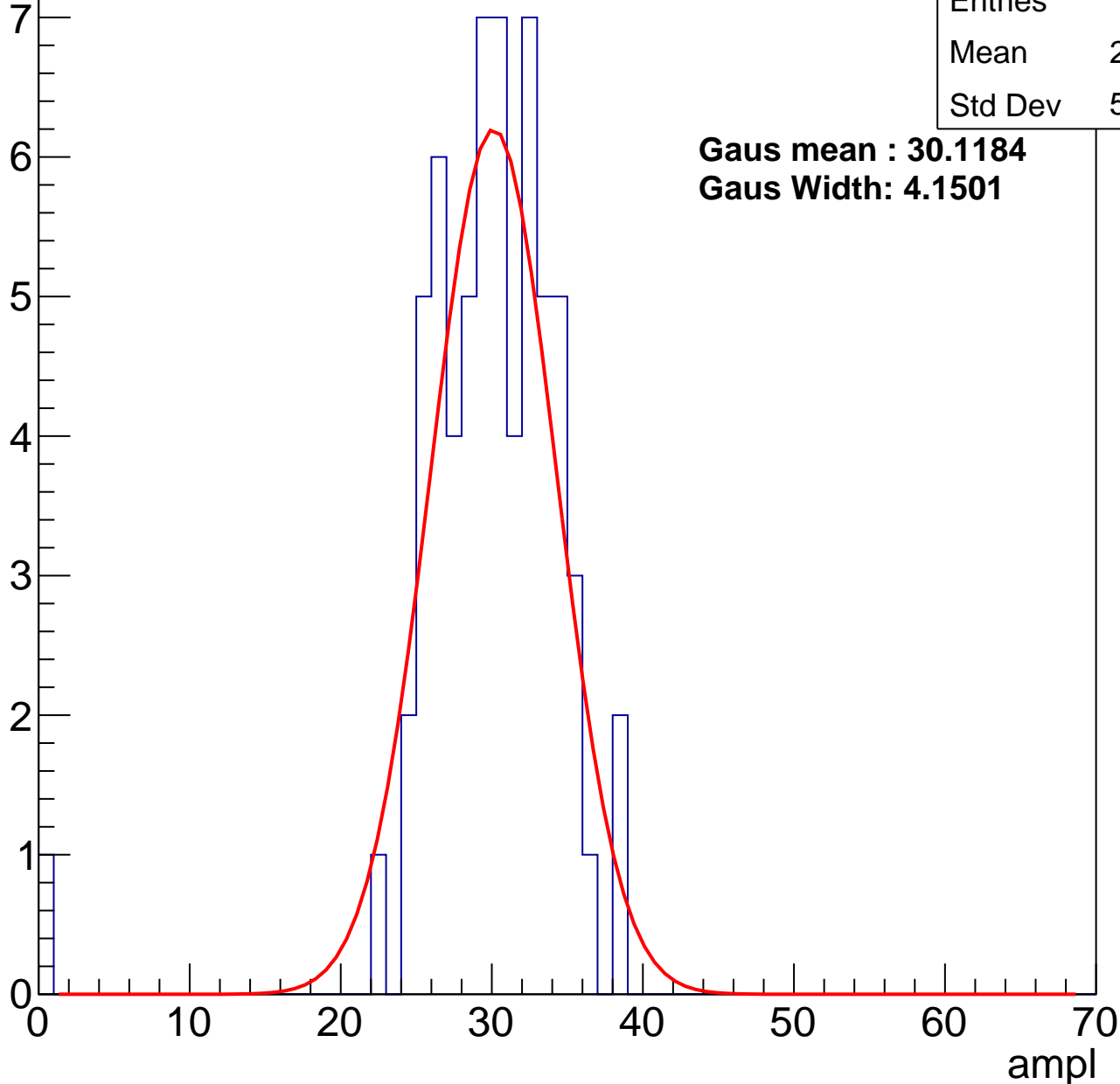
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.42
Std Dev	5.105

Gaus mean : 30.1184

Gaus Width: 4.1501



B1L100S, U15-ch26, adc1

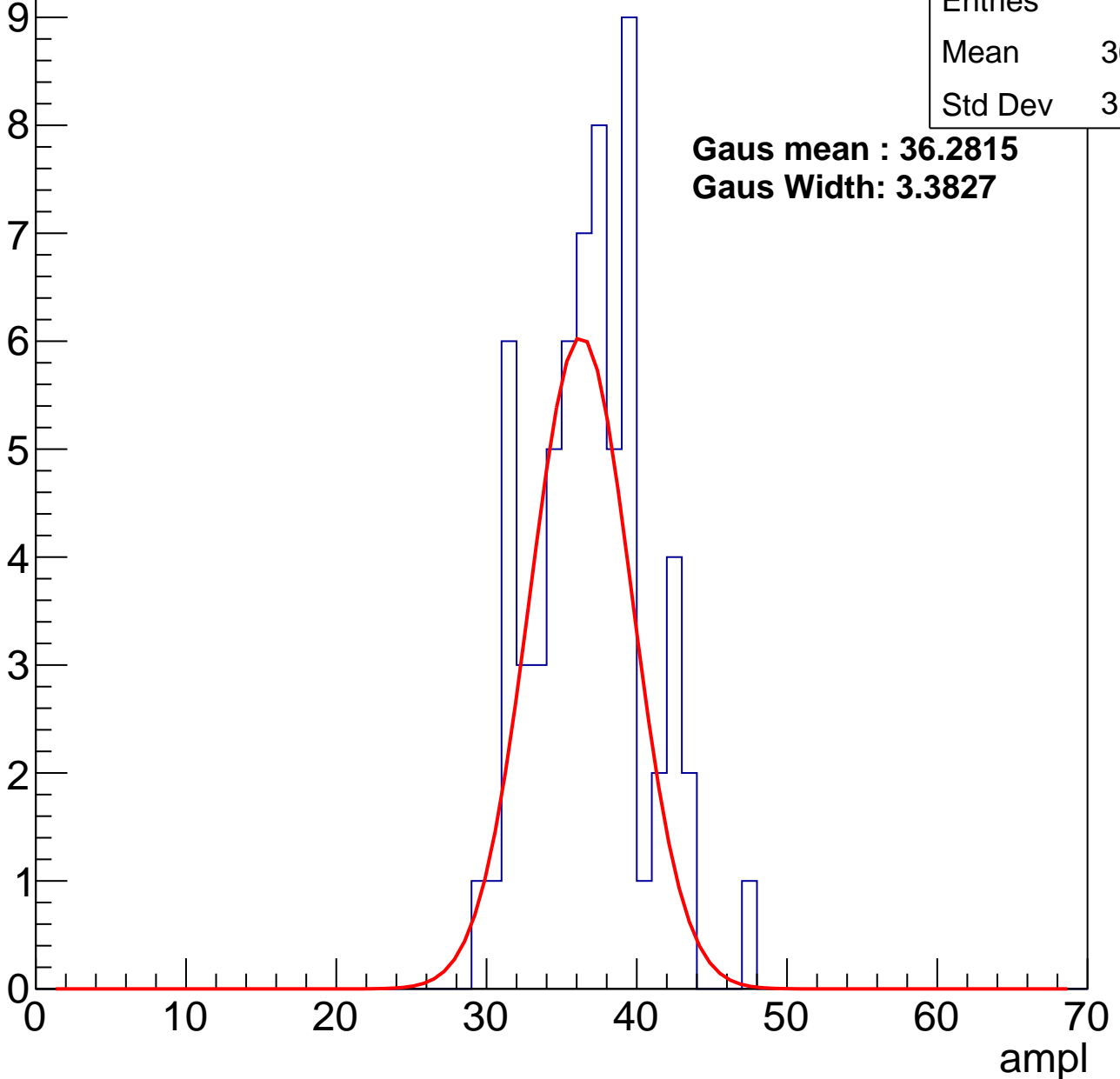
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.44
Std Dev	3.669

Gaus mean : 36.2815

Gaus Width: 3.3827



B1L100S, U15-ch26, adc2

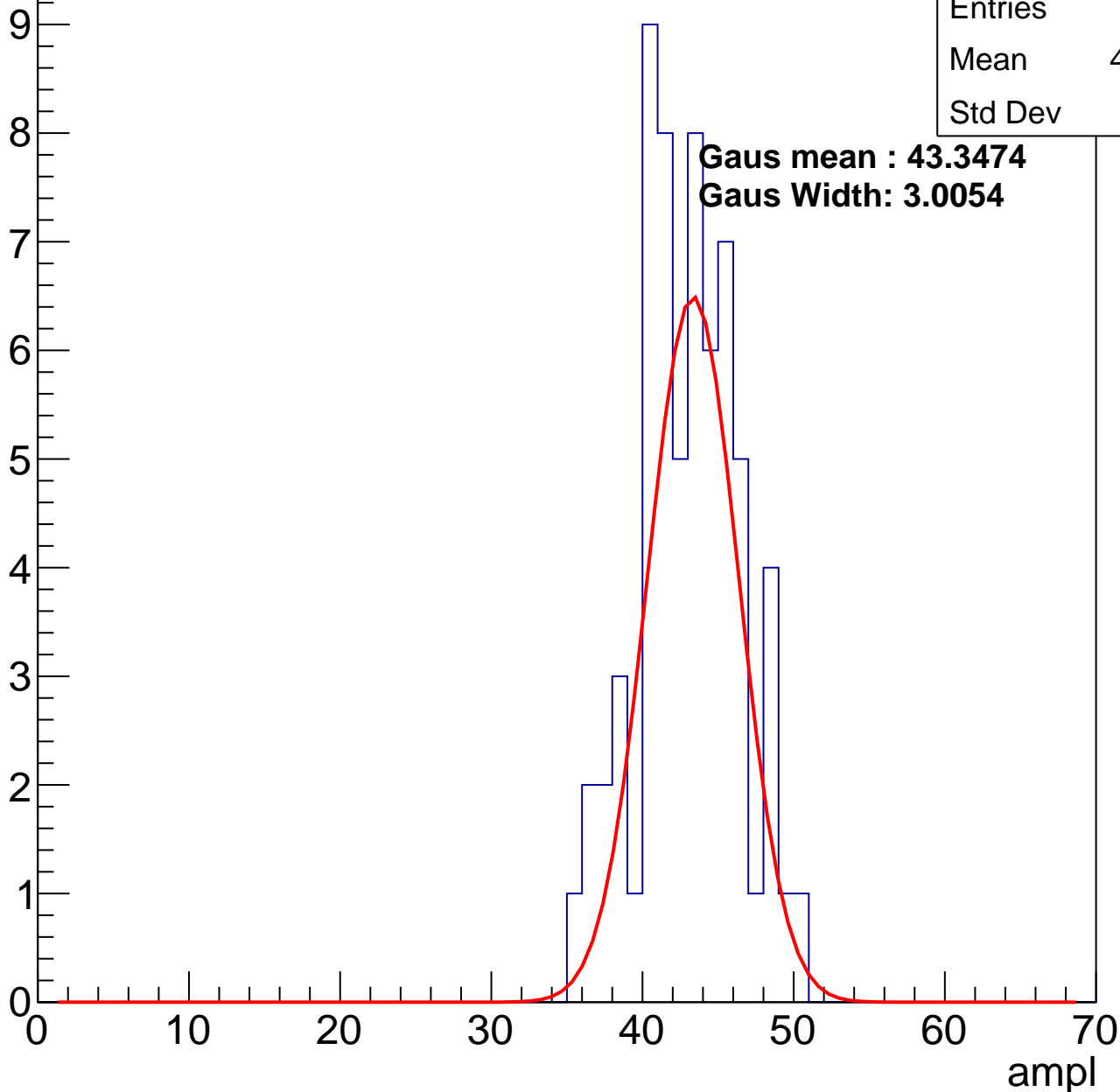
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.55
Std Dev	3.33

Gaus mean : 43.3474

Gaus Width: 3.0054

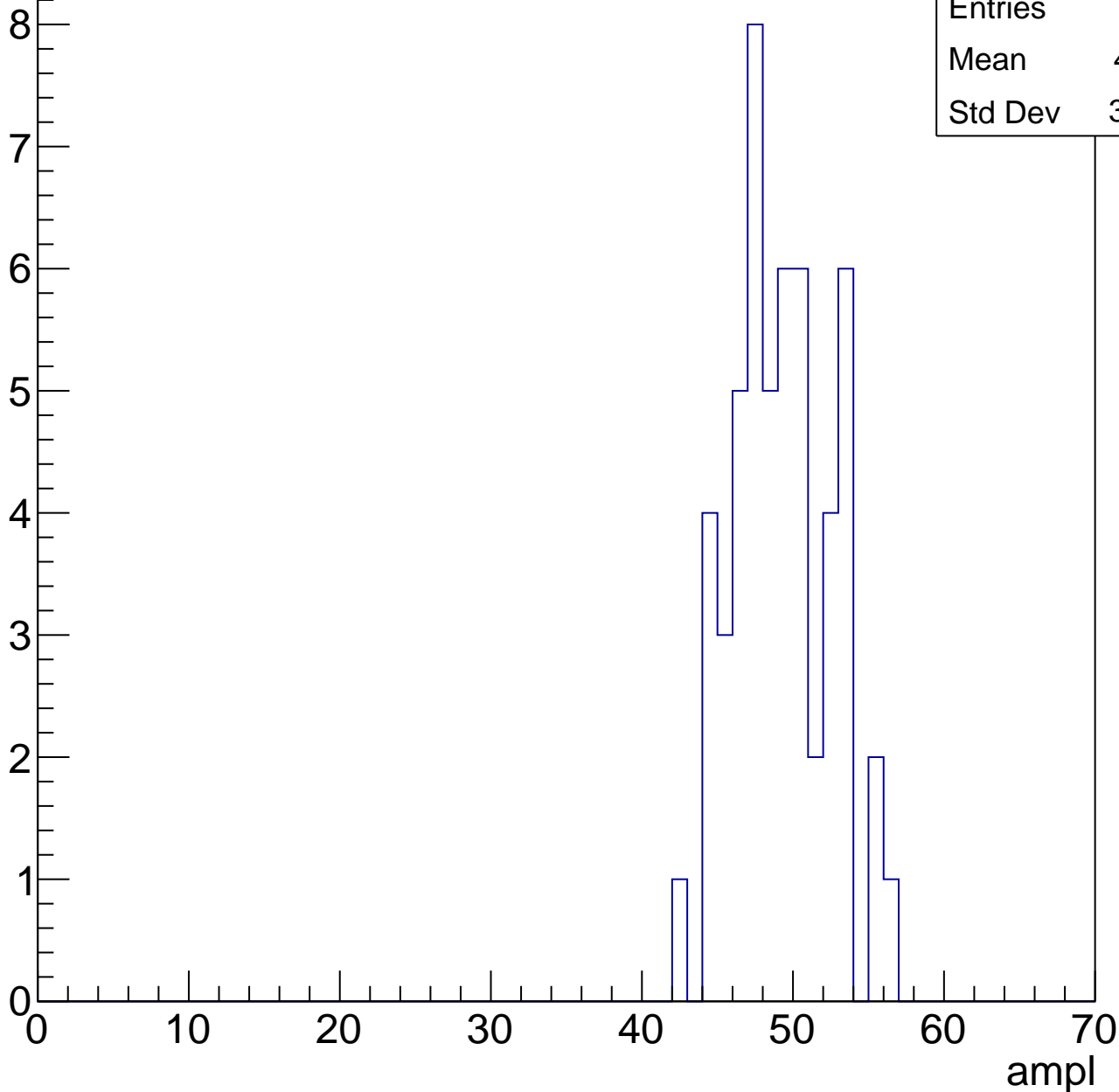


B1L100S, U15-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	48.81
Std Dev	3.204

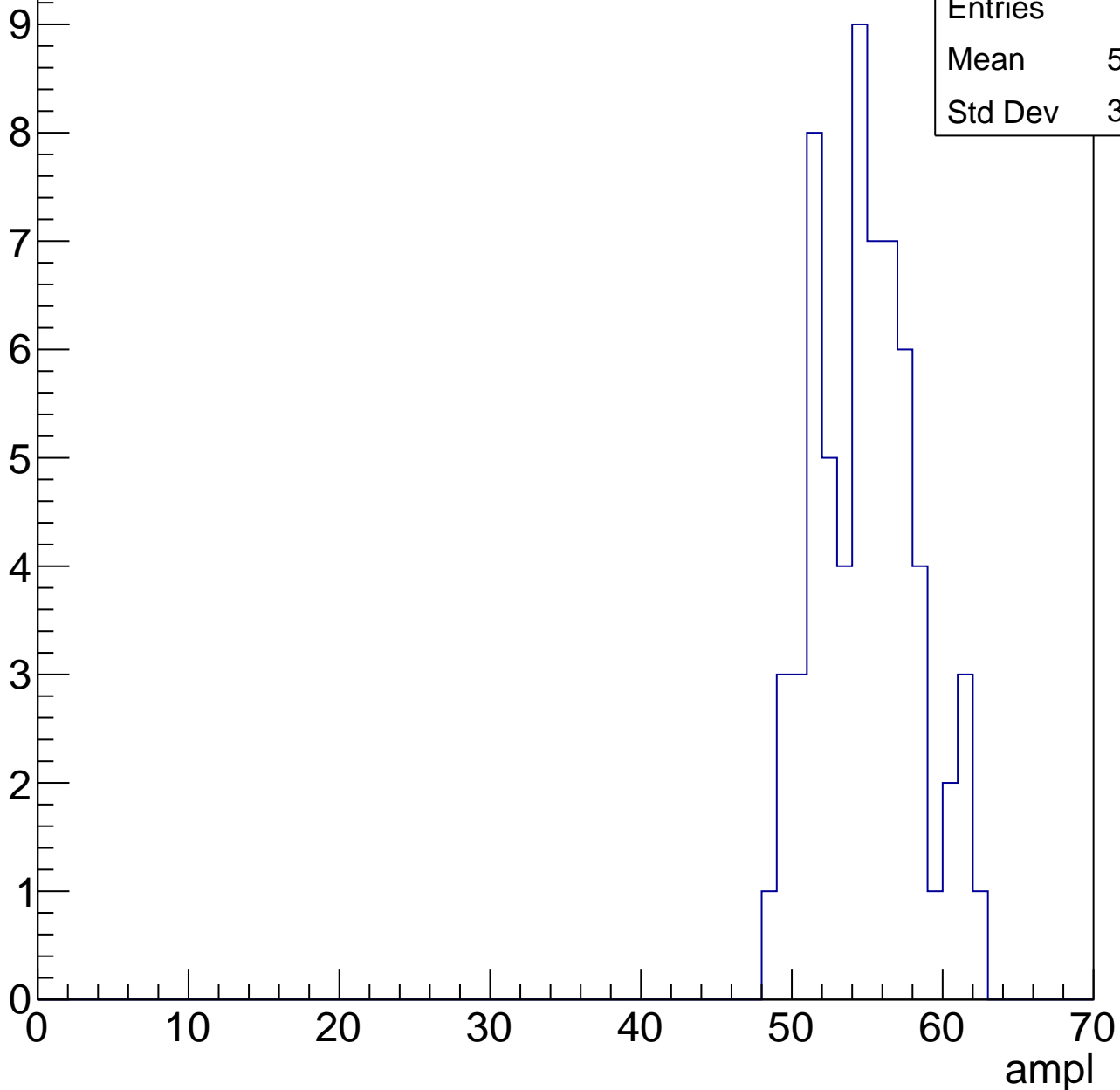


B1L100S, U15-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

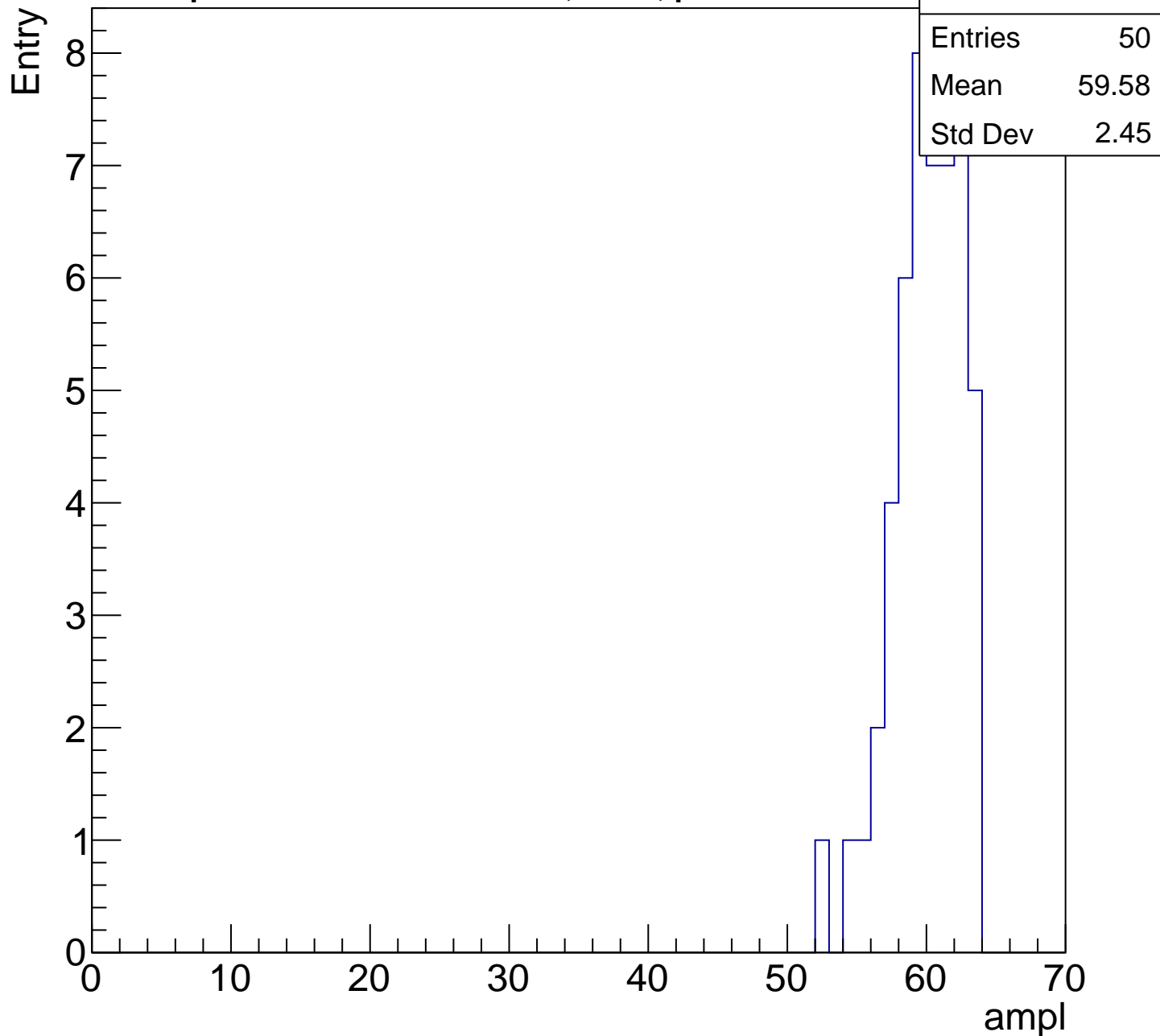
Entry

Entries	64
Mean	54.47
Std Dev	3.312



B1L100S, U15-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

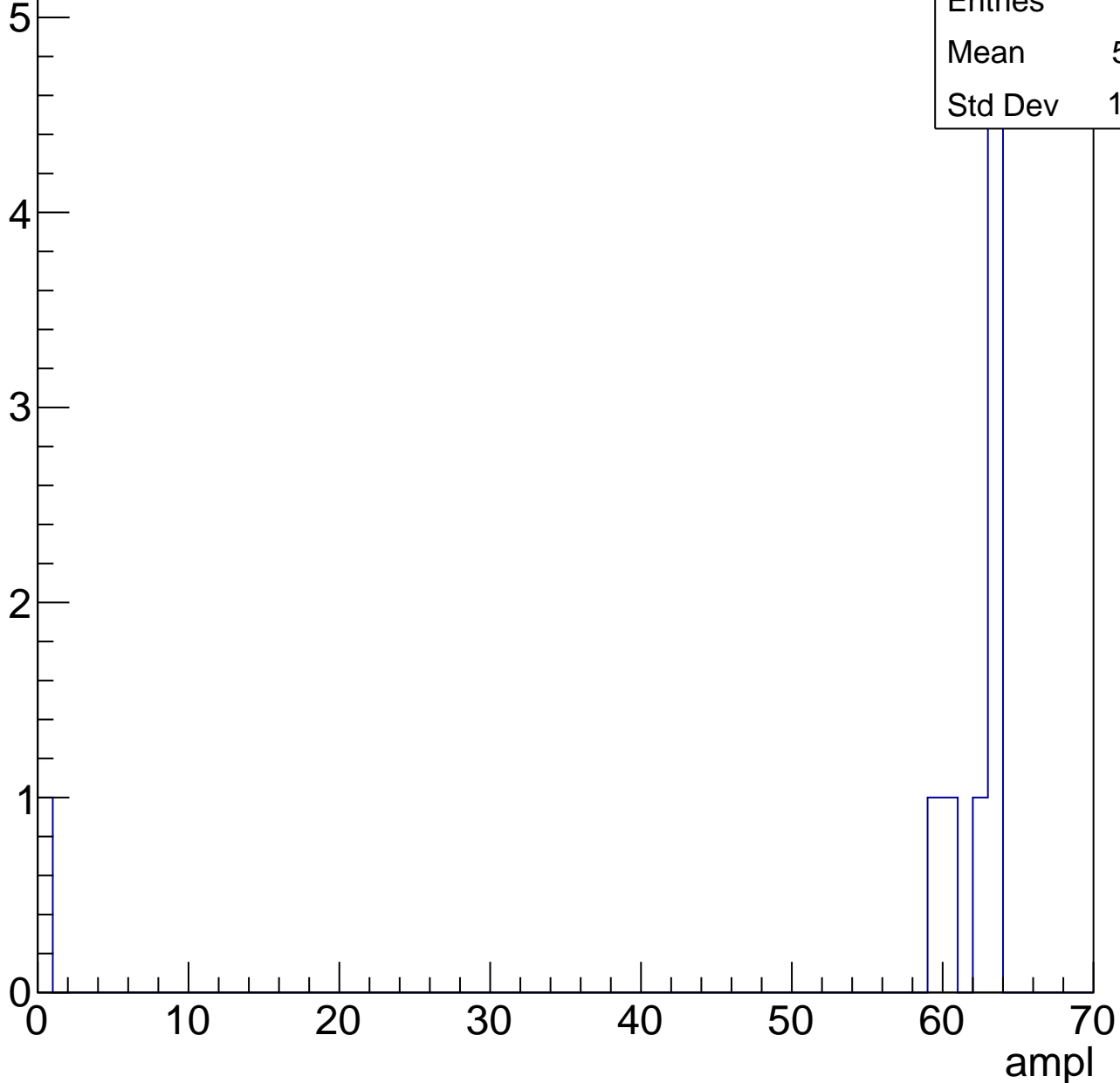


B1L100S, U15-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	9
Mean	55.11
Std Dev	19.54



B1L100S, U15-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch27, adc0

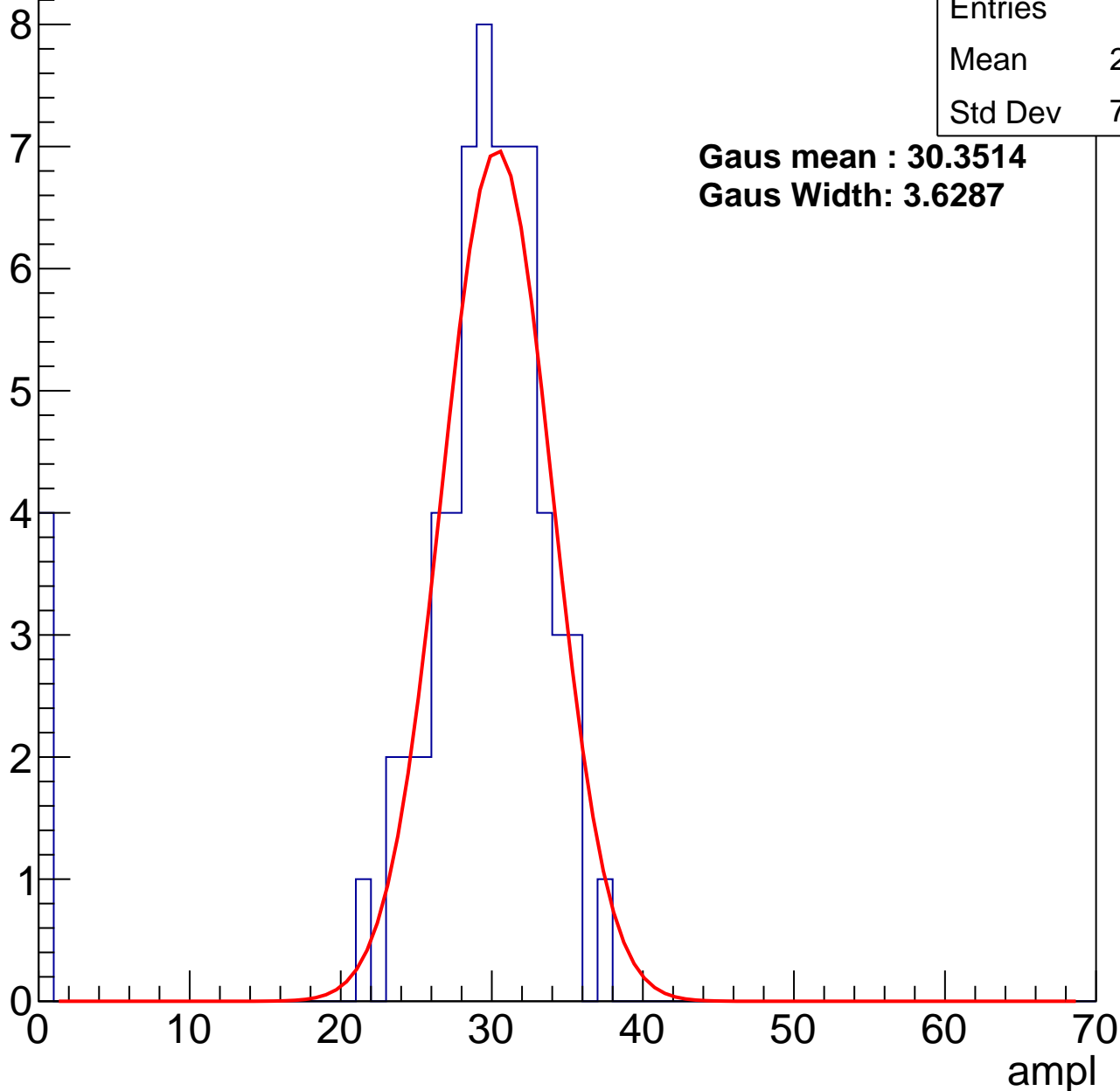
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	27.76
Std Dev	7.734

Gaus mean : 30.3514

Gaus Width: 3.6287



B1L100S, U15-ch27, adc1

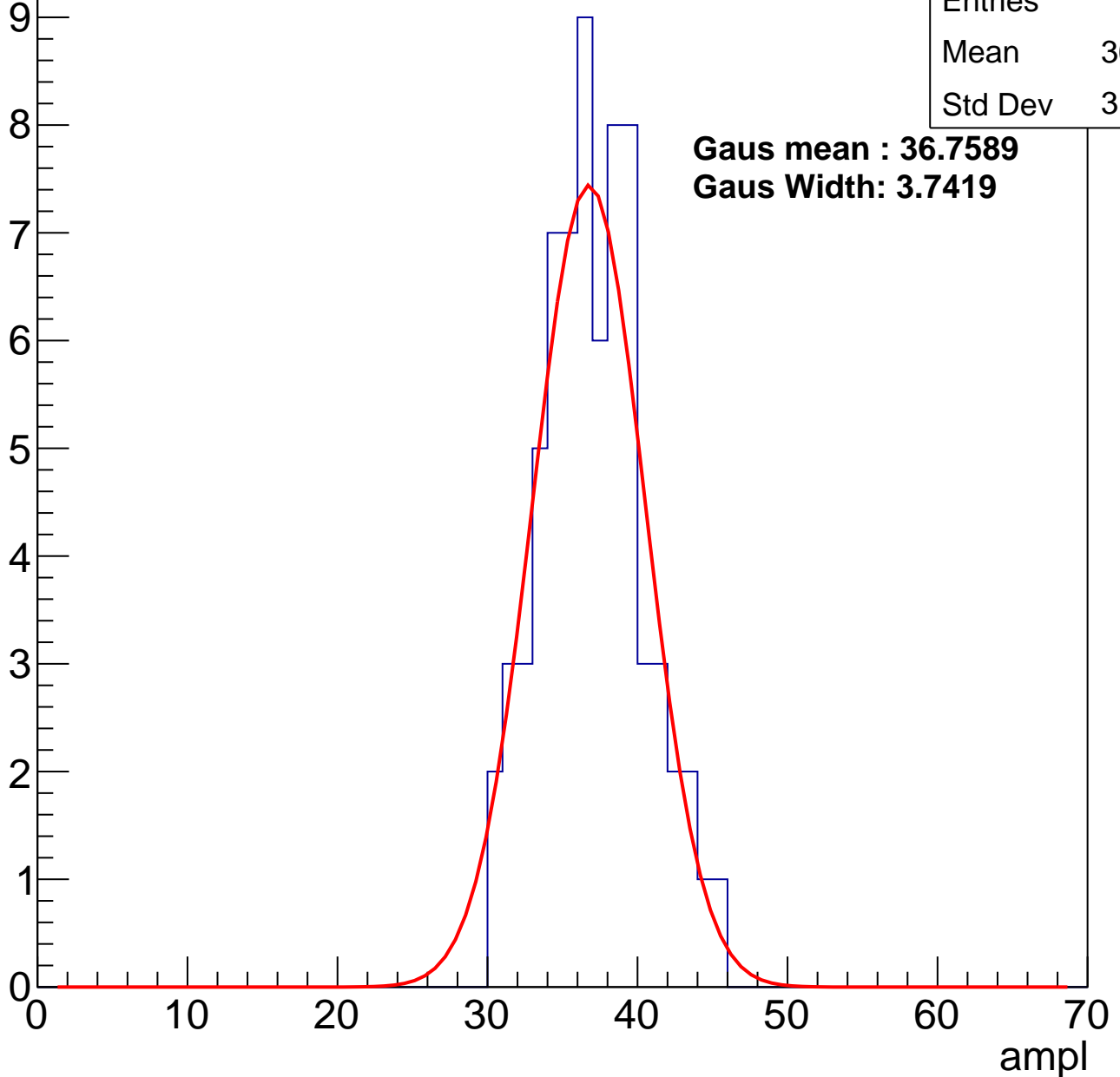
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.59
Std Dev	3.378

Gaus mean : 36.7589

Gaus Width: 3.7419



B1L100S, U15-ch27, adc2

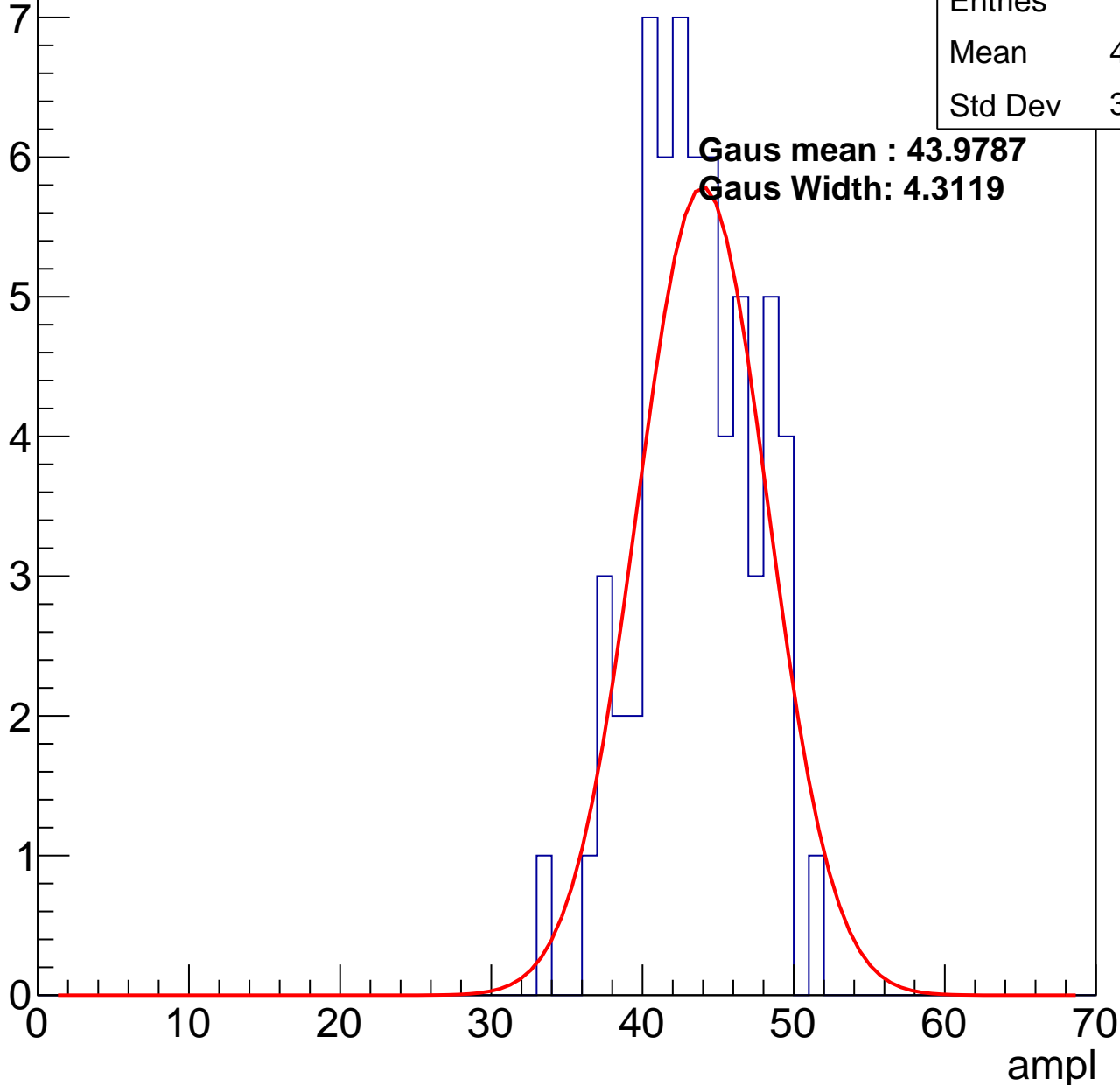
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43.08
Std Dev	3.739

Gaus mean : 43.9787

Gaus Width: 4.3119

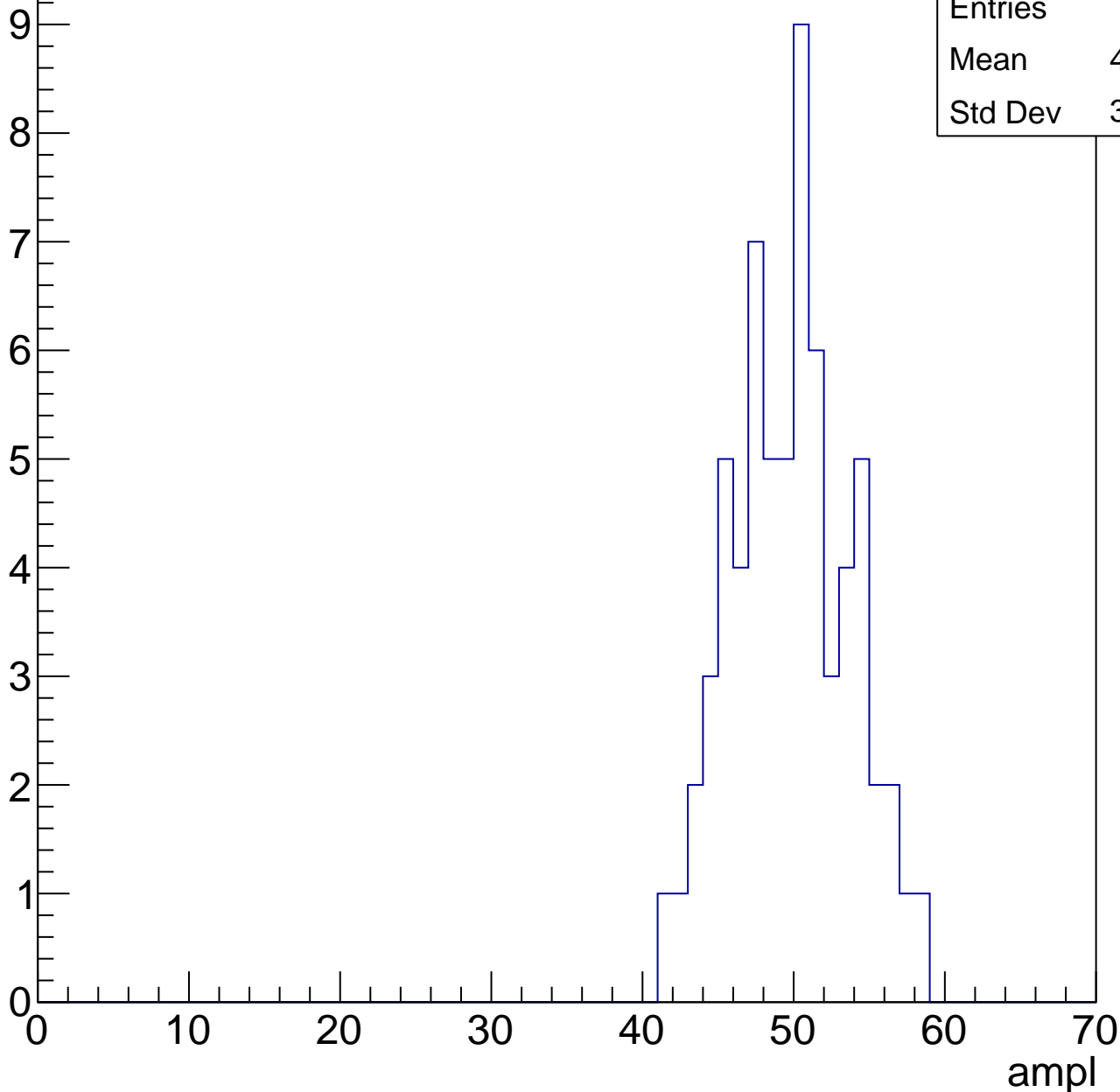


B1L100S, U15-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.32
Std Dev	3.826

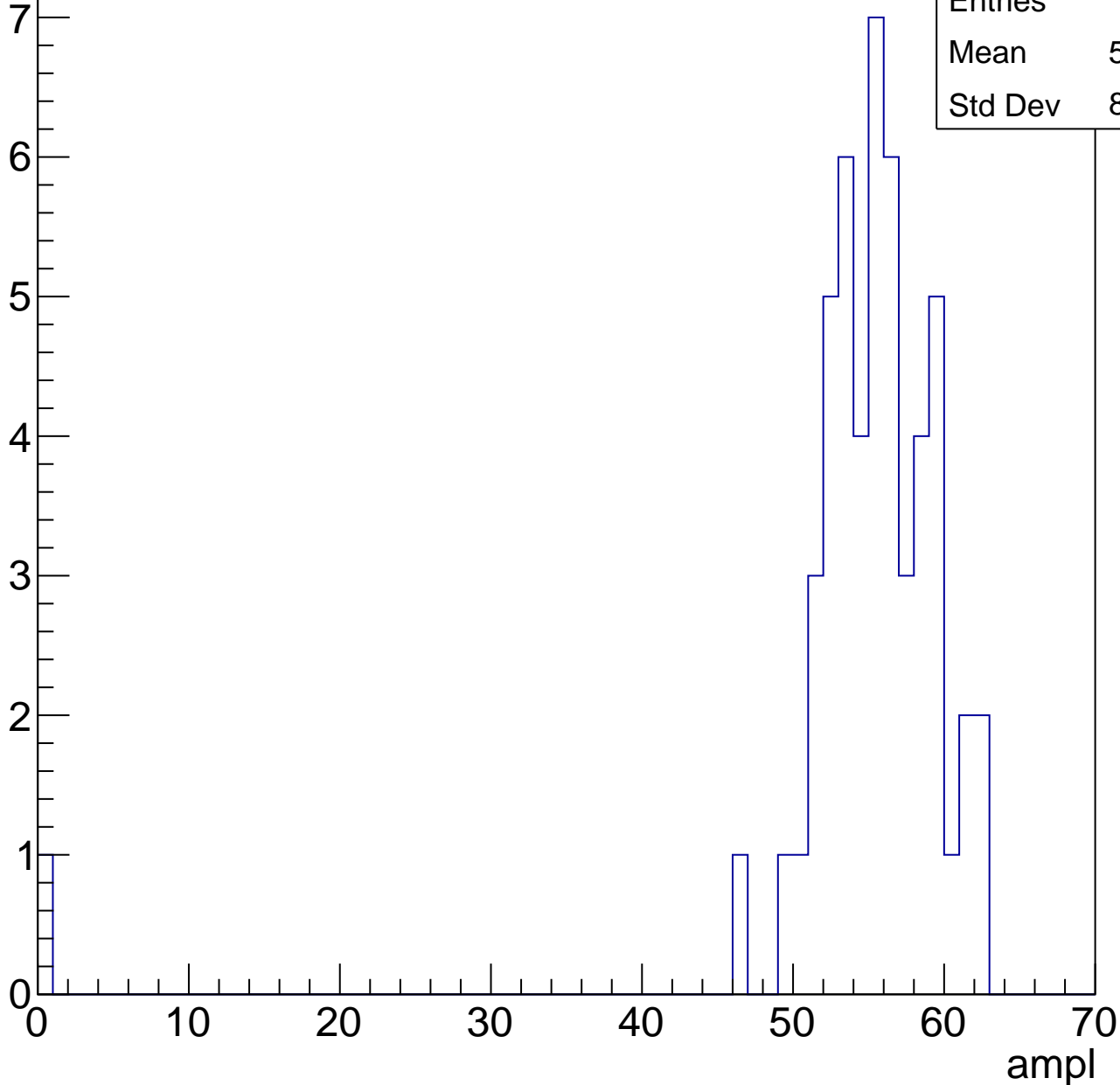


B1L100S, U15-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	54.17
Std Dev	8.299



B1L100S, U15-ch27, adc5

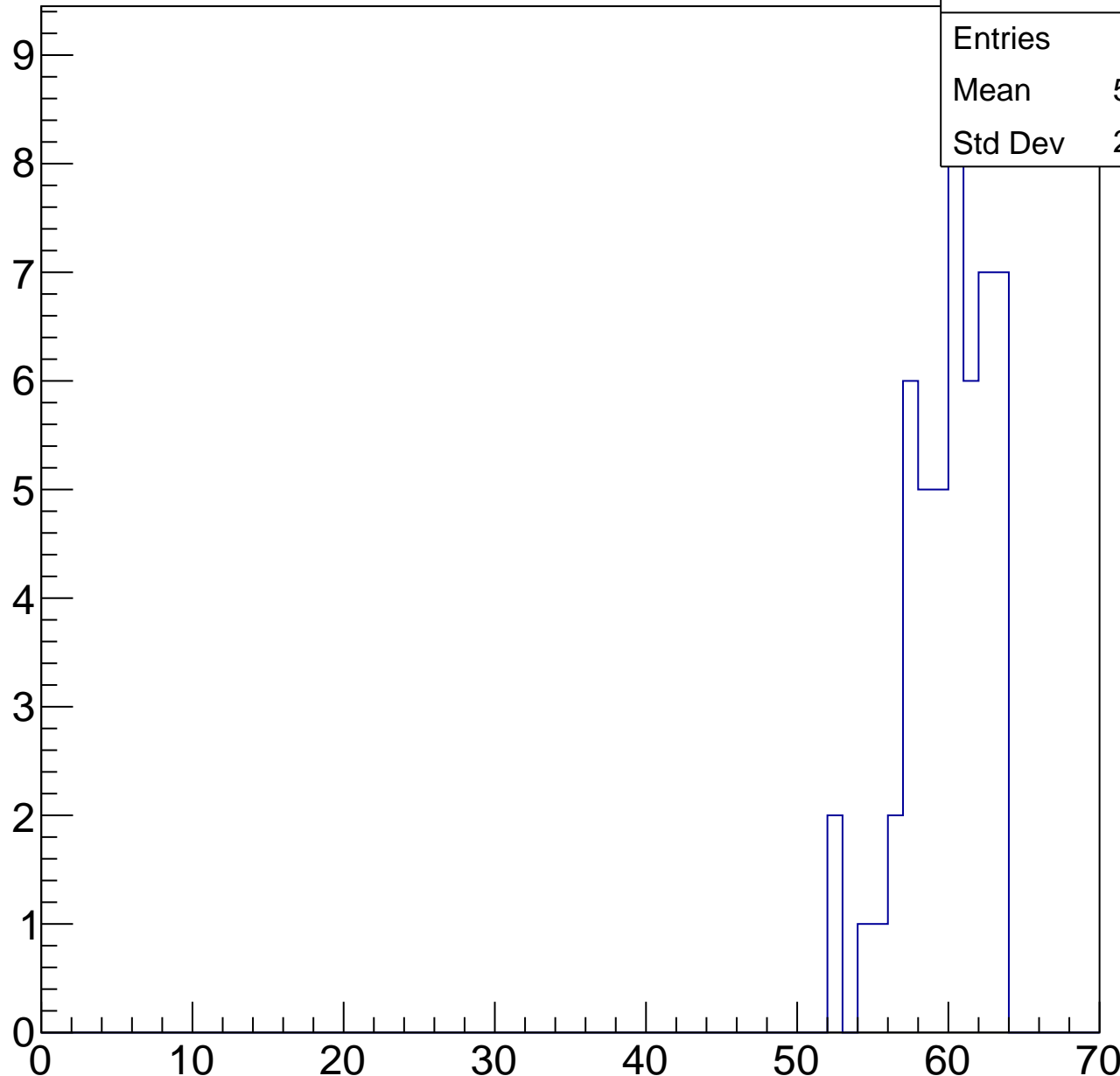
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.47
Std Dev	2.739

ampl



B1L100S, U15-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch28, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	28.27
Std Dev	5.868

Gaus mean : 29.6129

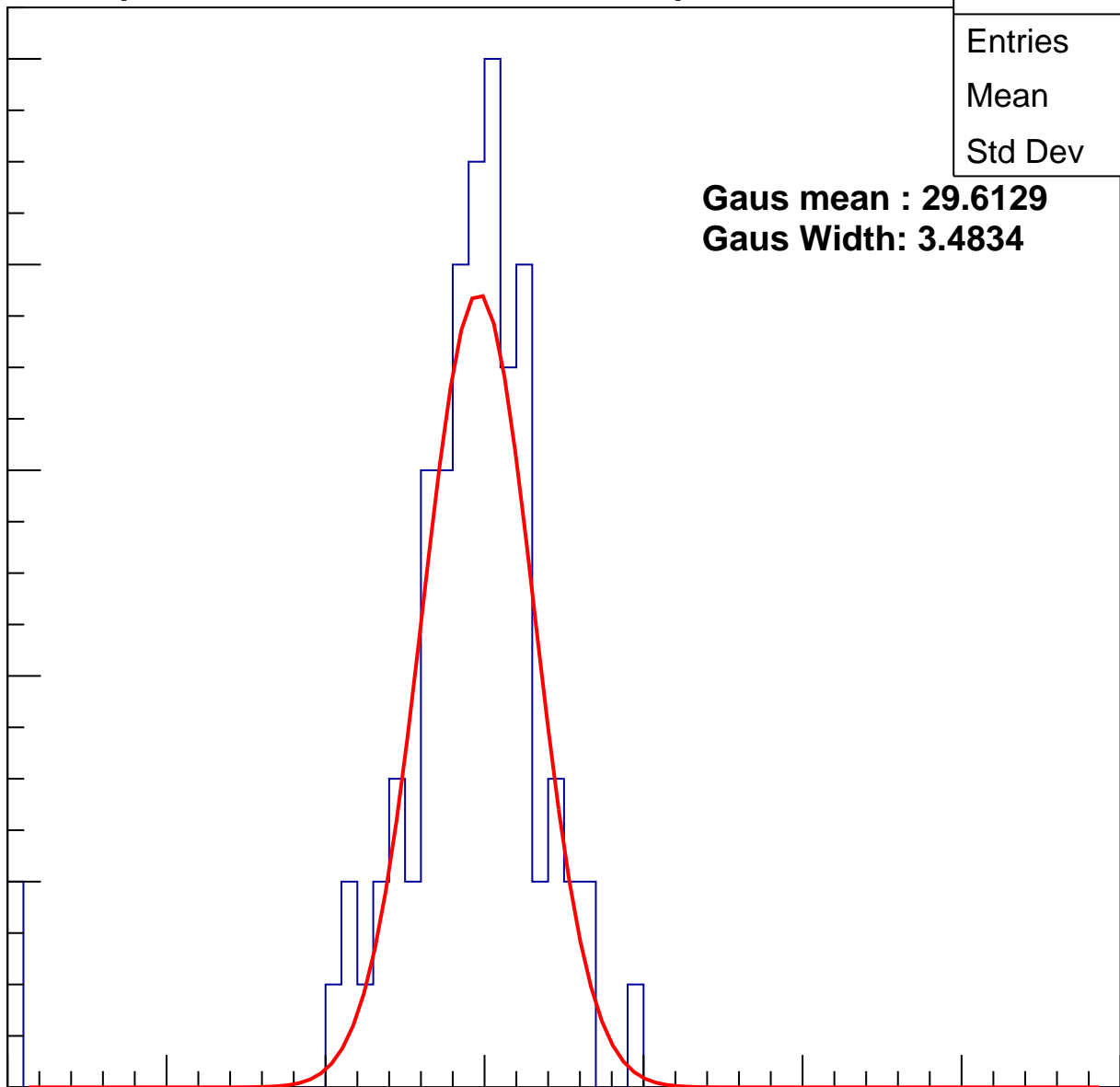
Gaus Width: 3.4834

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch28, adc1

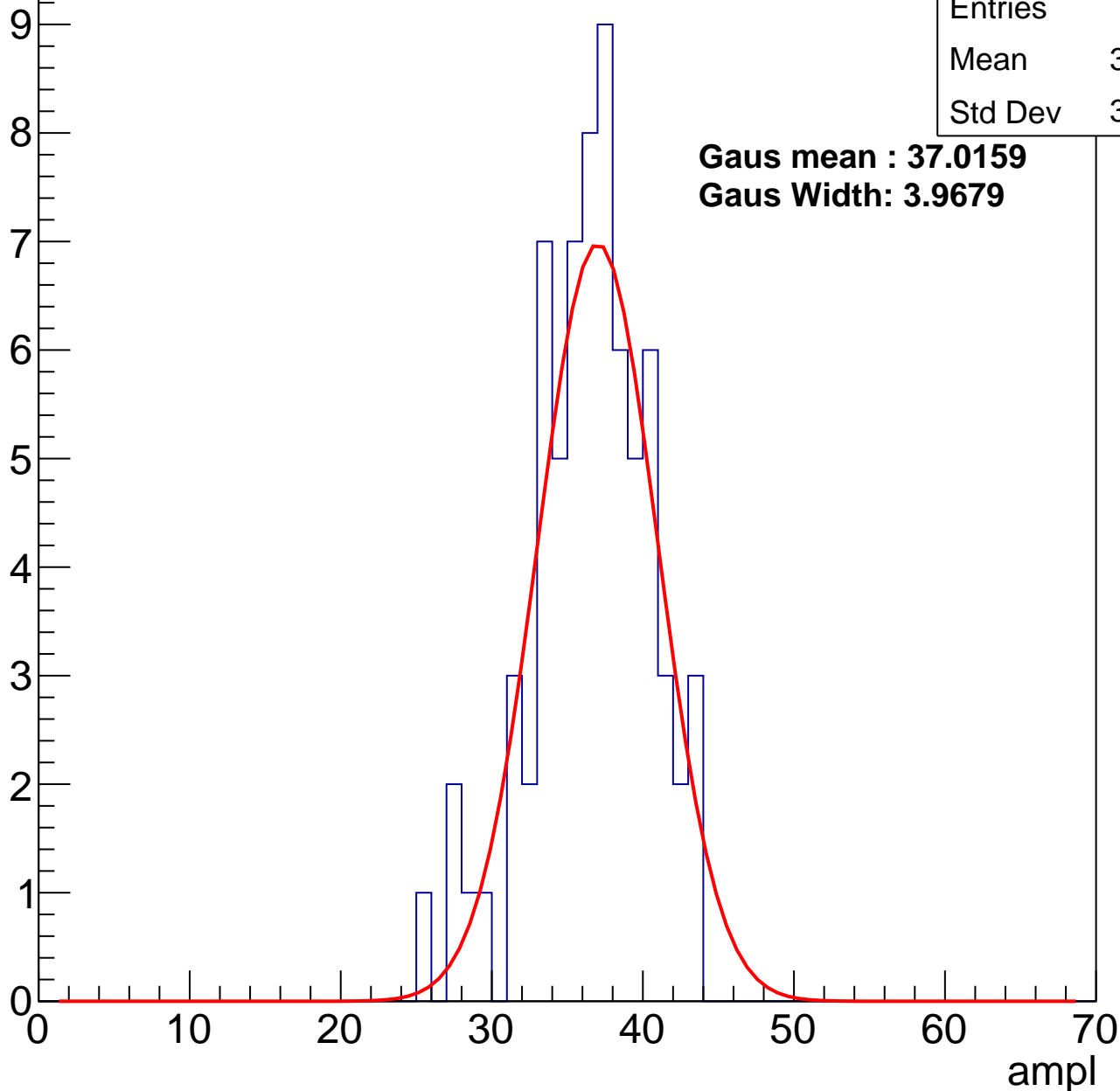
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.04
Std Dev	3.865

Gaus mean : 37.0159

Gaus Width: 3.9679



B1L100S, U15-ch28, adc2

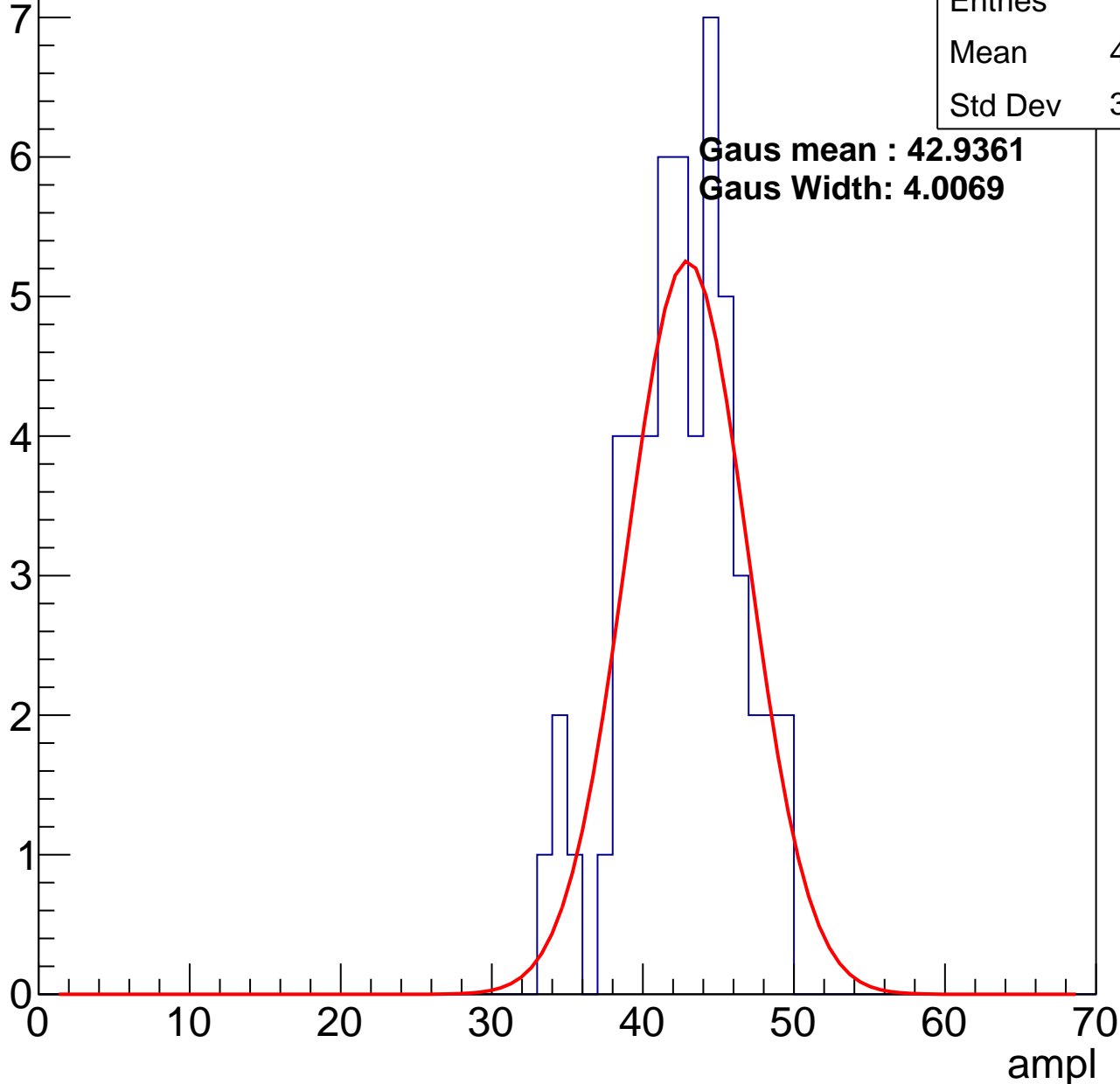
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	42.04
Std Dev	3.727

Gaus mean : 42.9361

Gaus Width: 4.0069

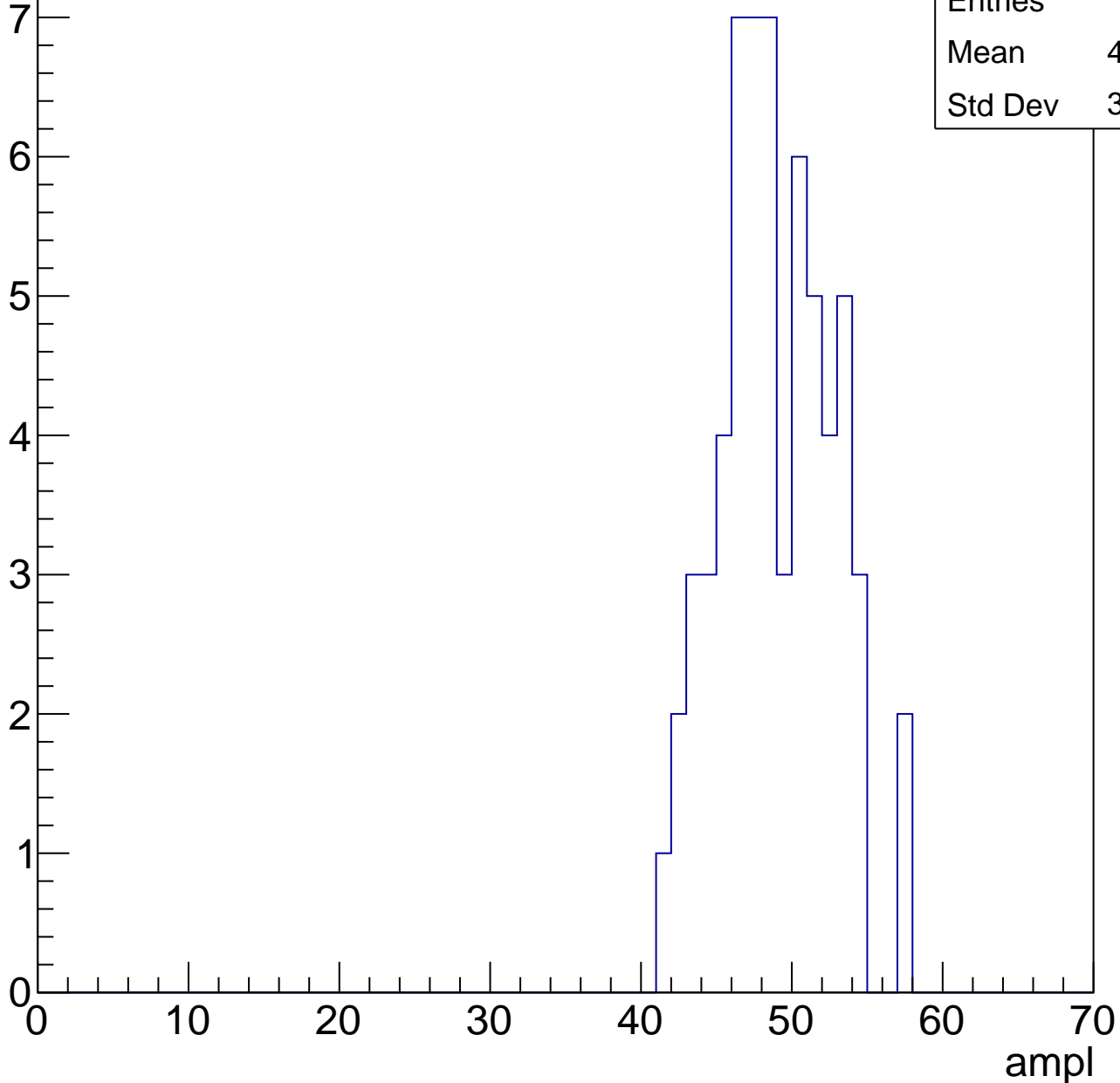


B1L100S, U15-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	48.45
Std Dev	3.666

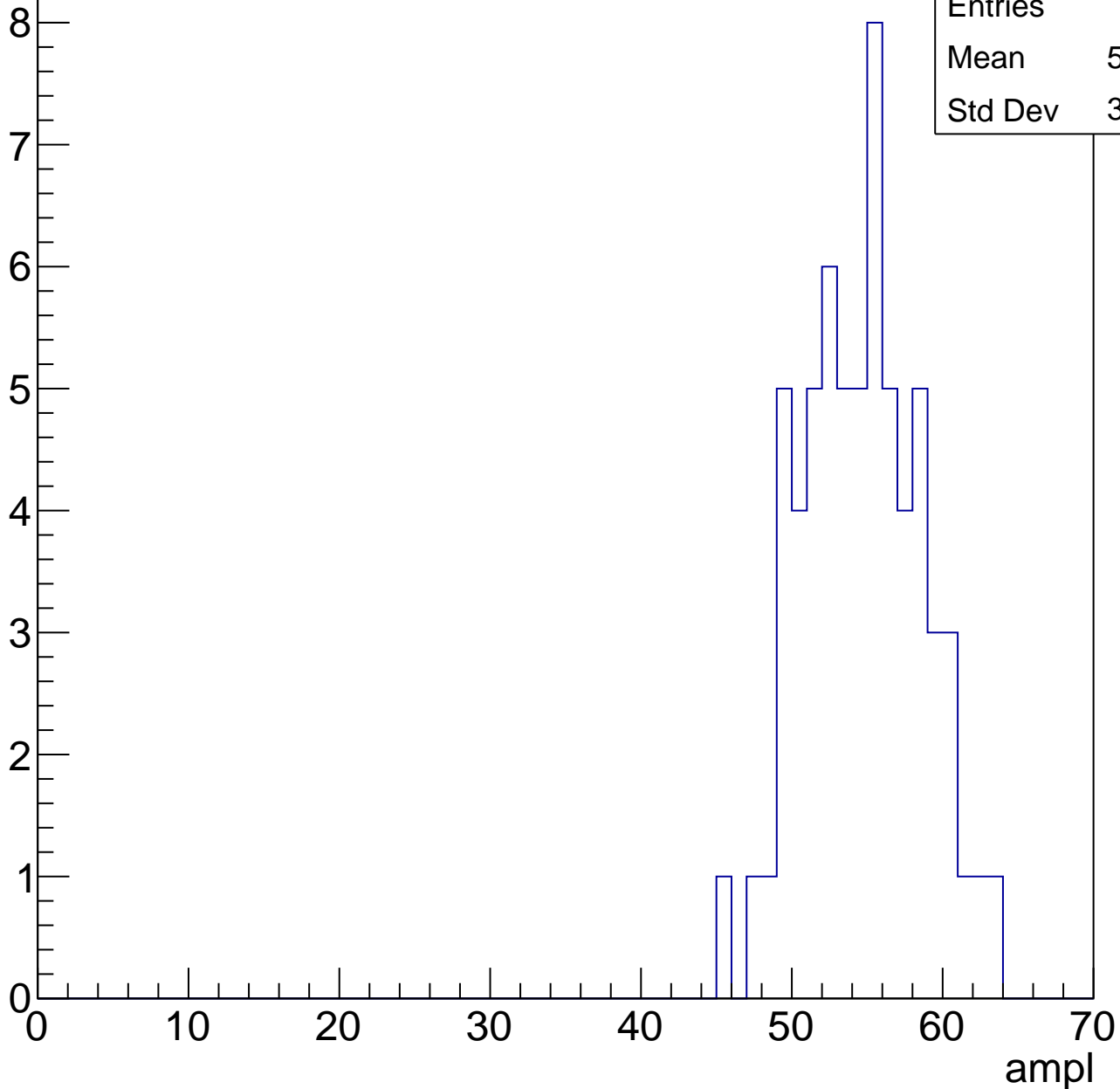


B1L100S, U15-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	54.19
Std Dev	3.848



B1L100S, U15-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.27
Std Dev	8.634

ampl

0

10

20

30

40

50

60

70

B1L100S, U15-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	61.22
Std Dev	1.618

0

B1L100S, U15-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U15-ch29, adc0

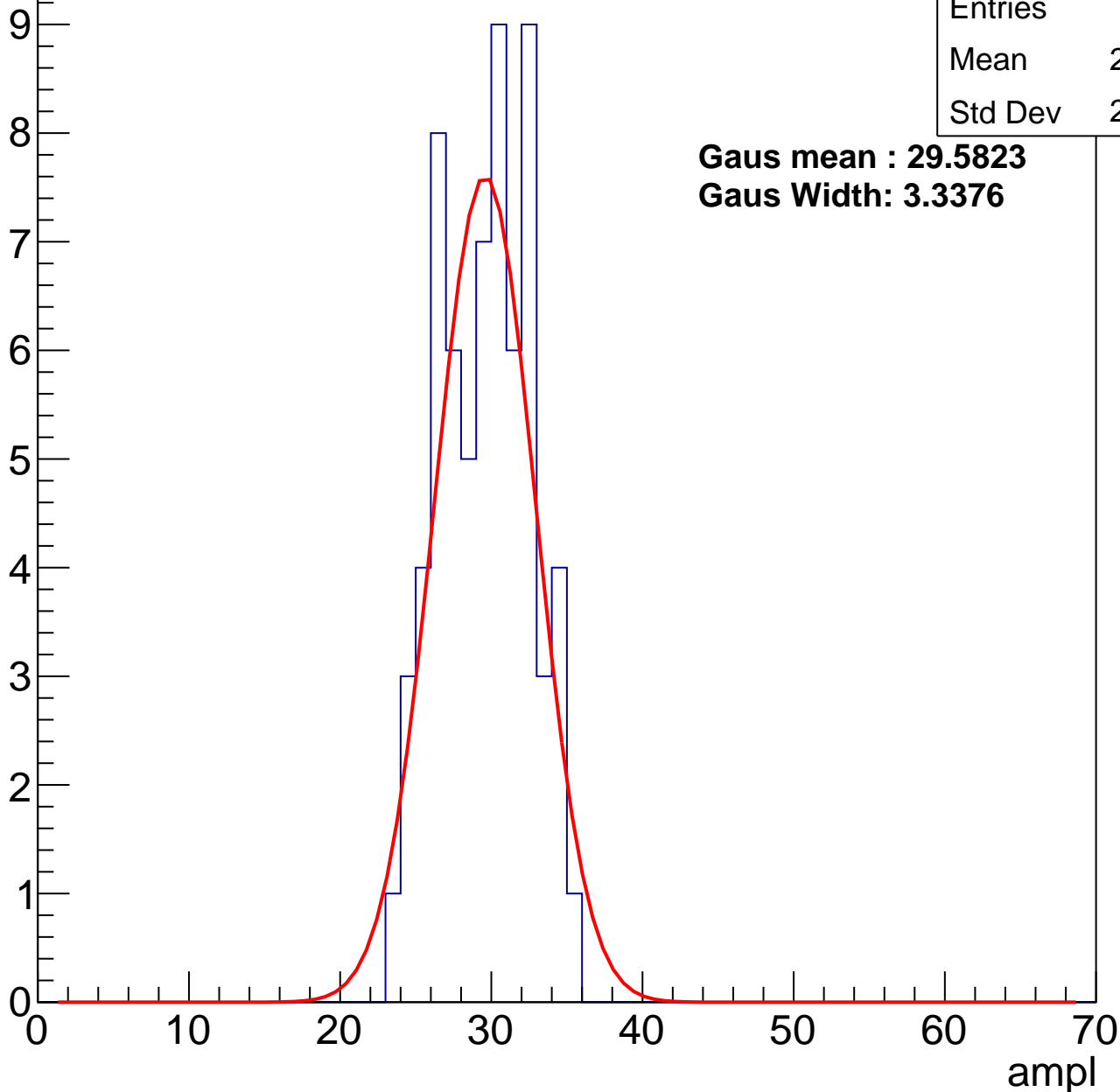
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.12
Std Dev	2.947

Gaus mean : 29.5823

Gaus Width: 3.3376



B1L100S, U15-ch29, adc1

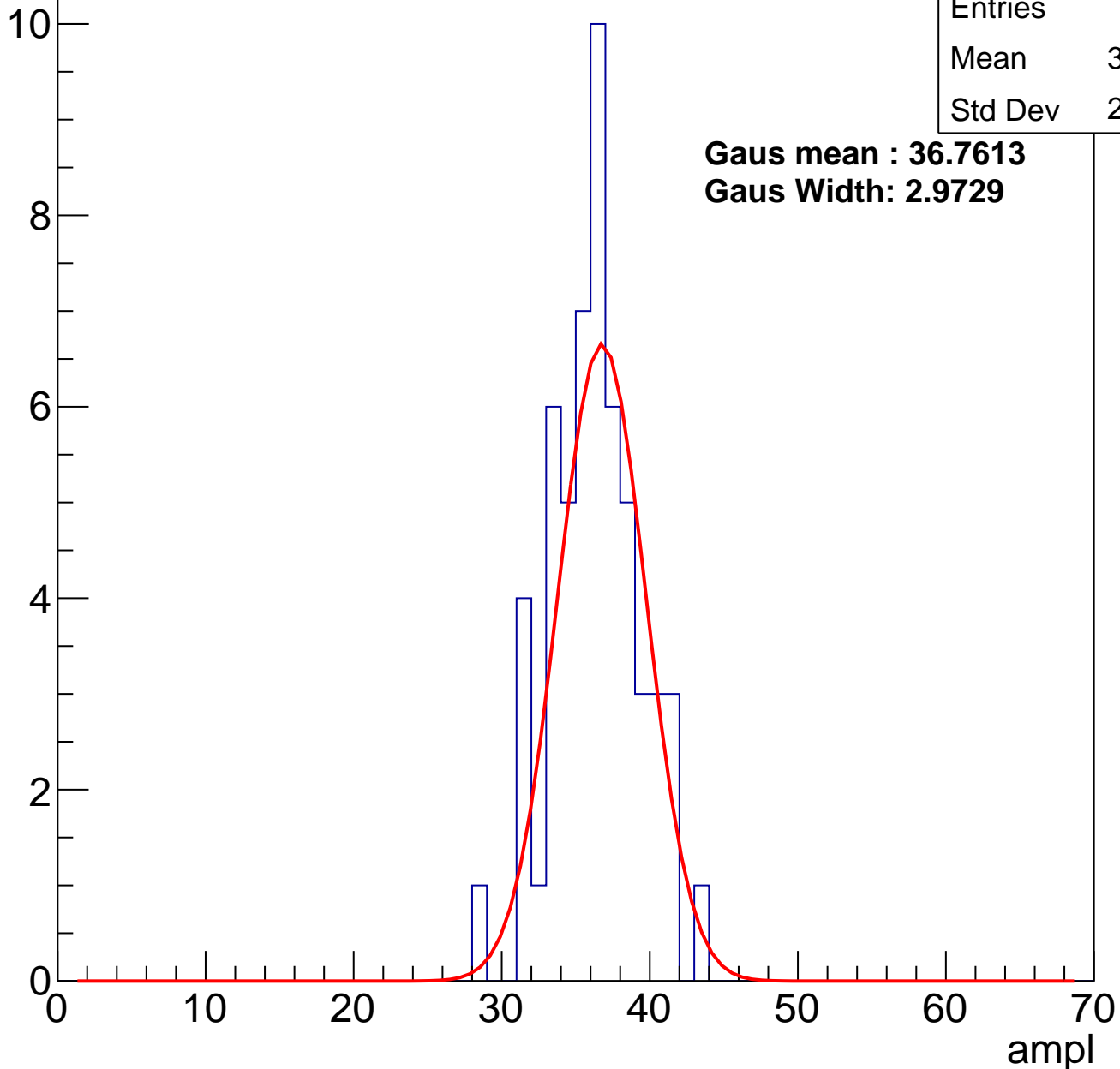
calib_packv5_042523_0143.root, FC#4, port A2

Entries	55
Mean	35.85
Std Dev	2.969

Gaus mean : 36.7613

Gaus Width: 2.9729

Entry



B1L100S, U15-ch29, adc2

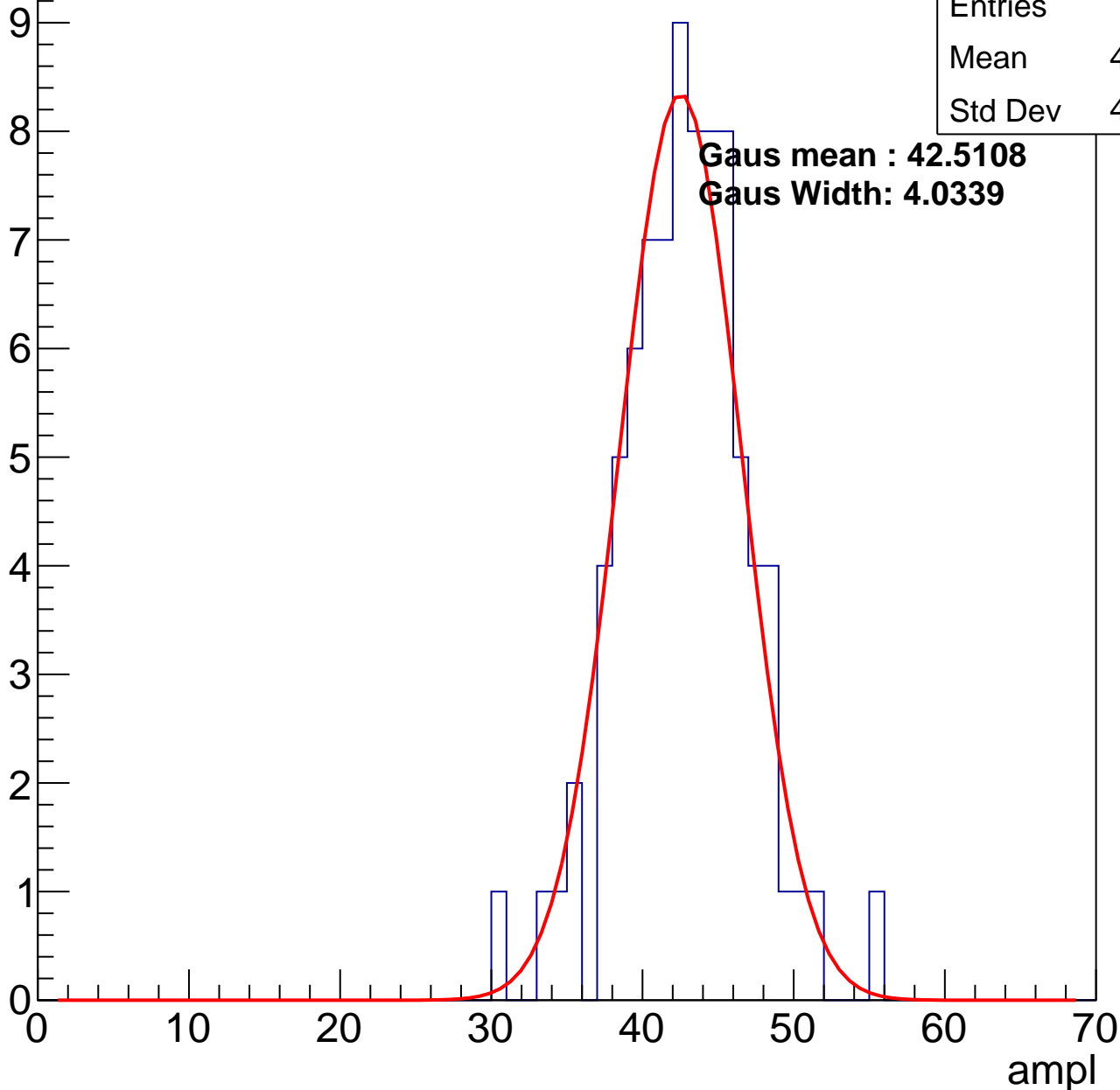
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	42.32
Std Dev	4.149

Gaus mean : 42.5108

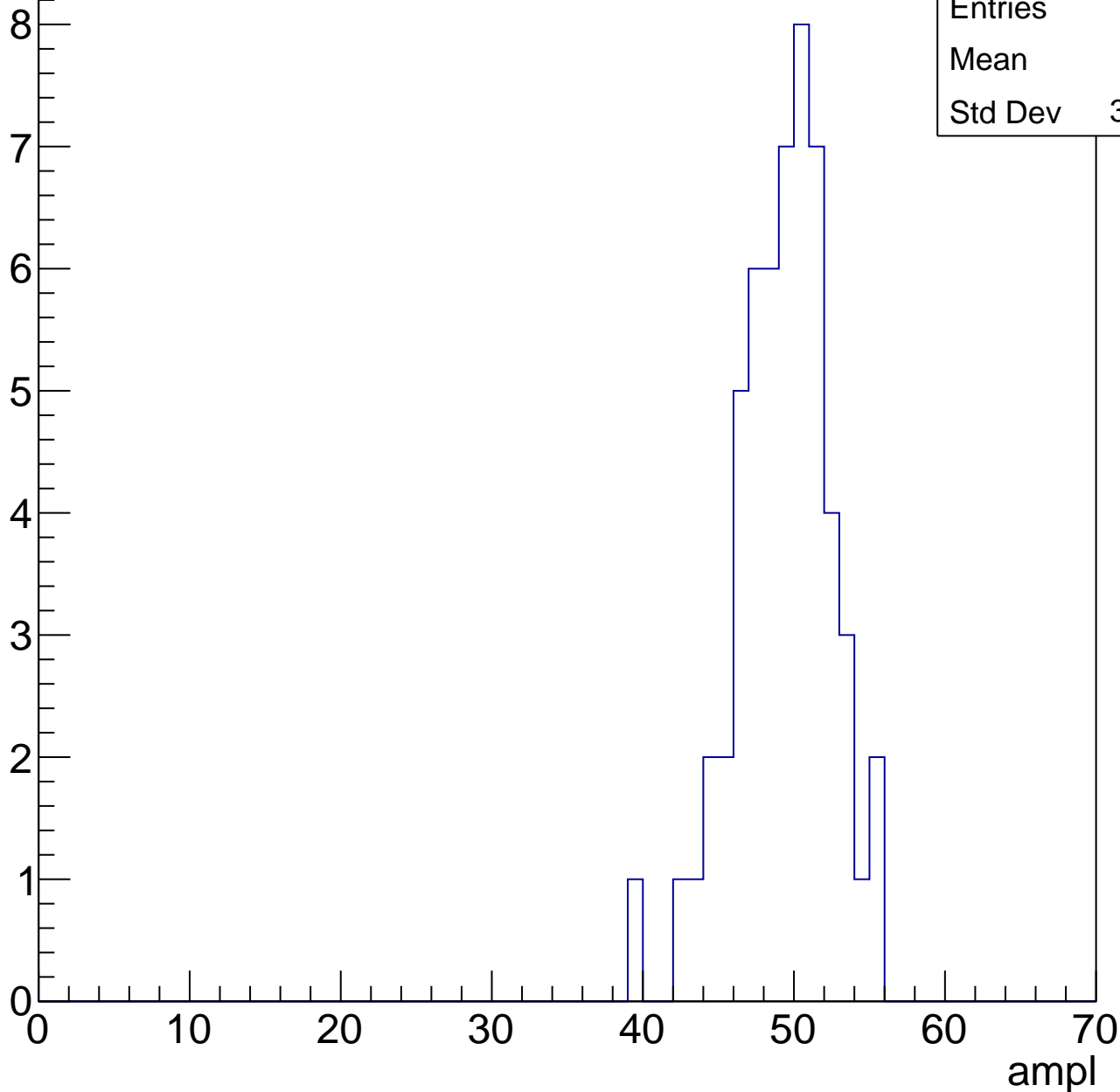
Gaus Width: 4.0339



B1L100S, U15-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



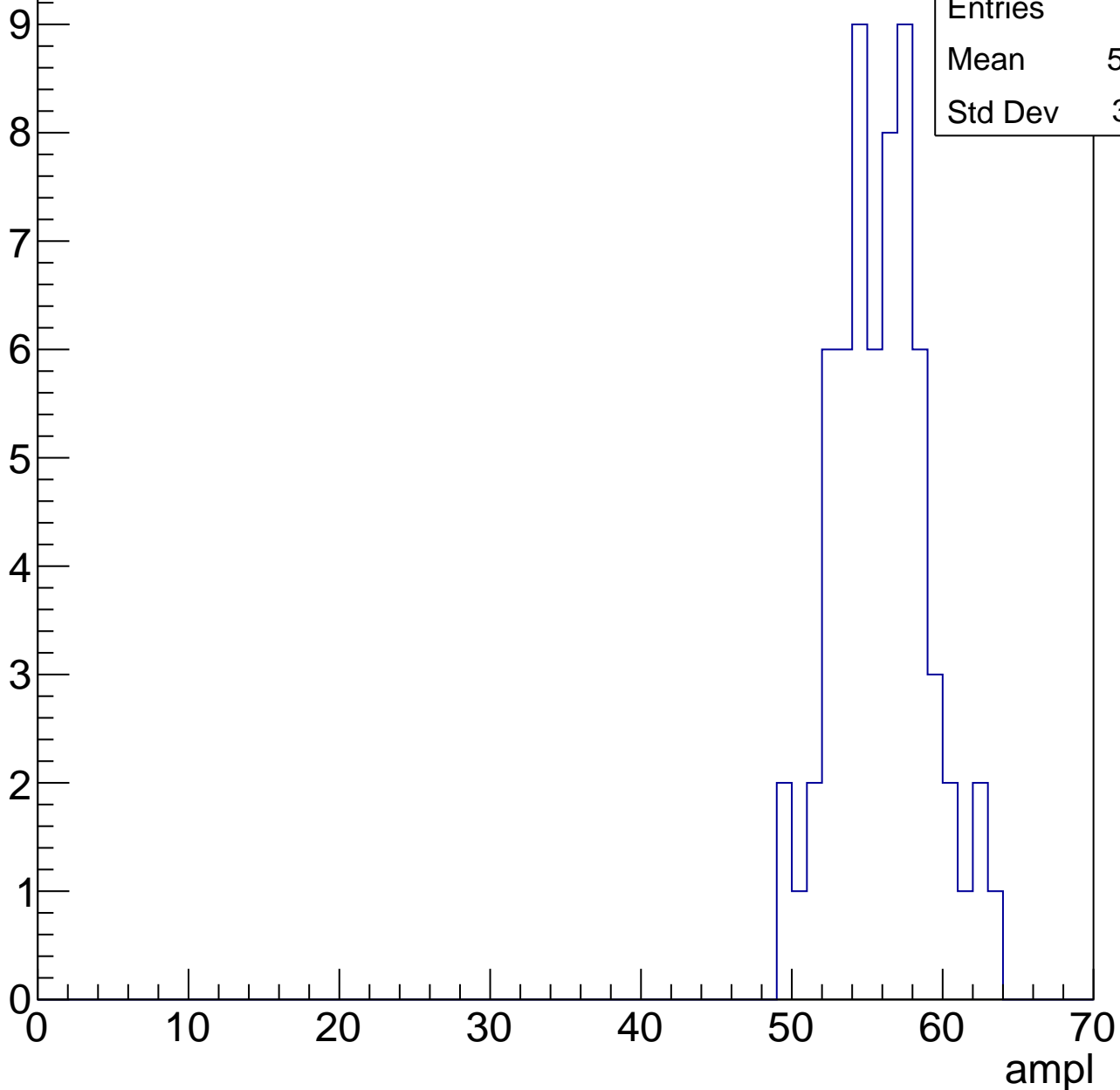
Entries	56
Mean	48.8
Std Dev	3.153

B1L100S, U15-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	55.47
Std Dev	3.051

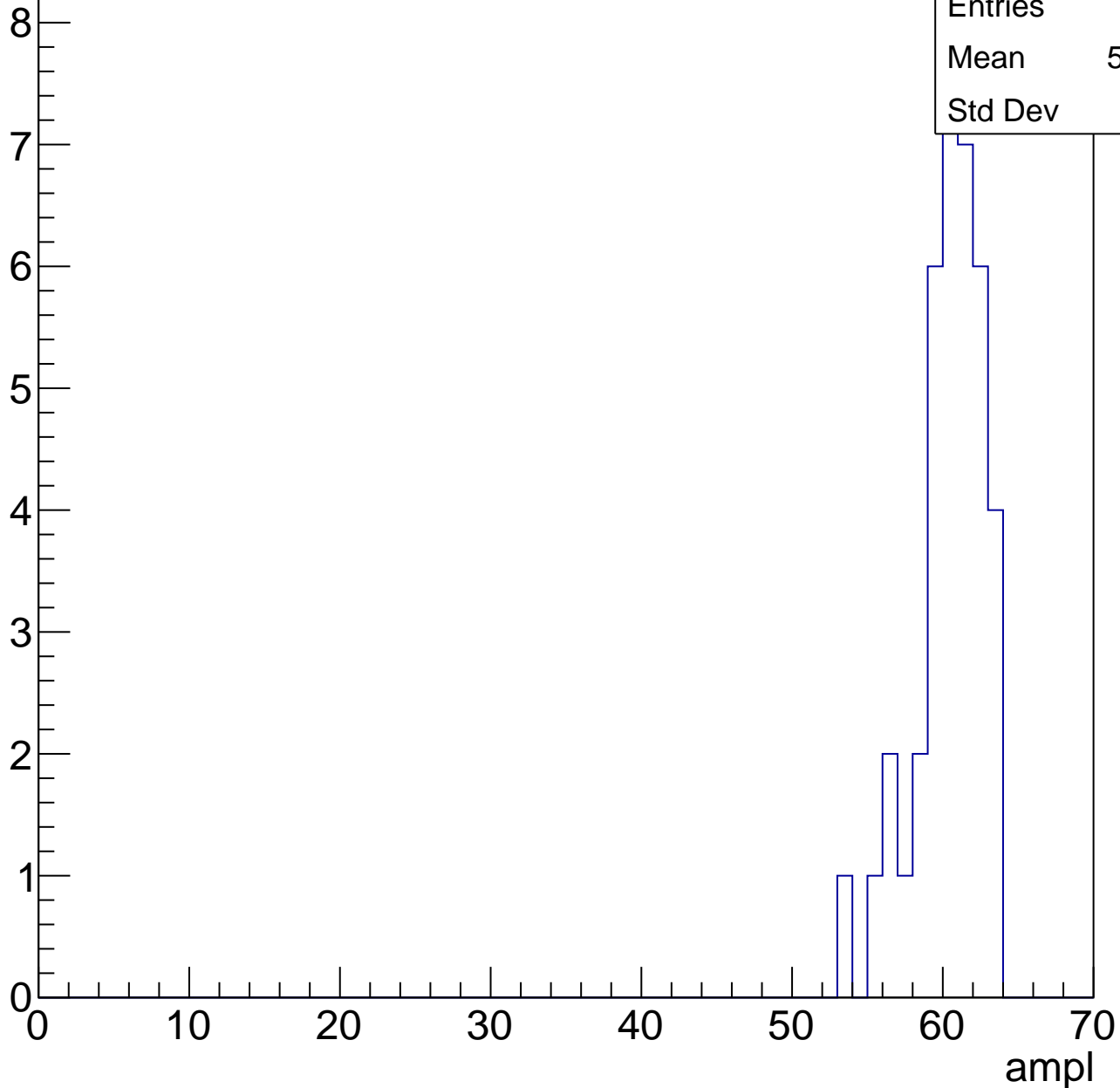


B1L100S, U15-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	59.95
Std Dev	2.27

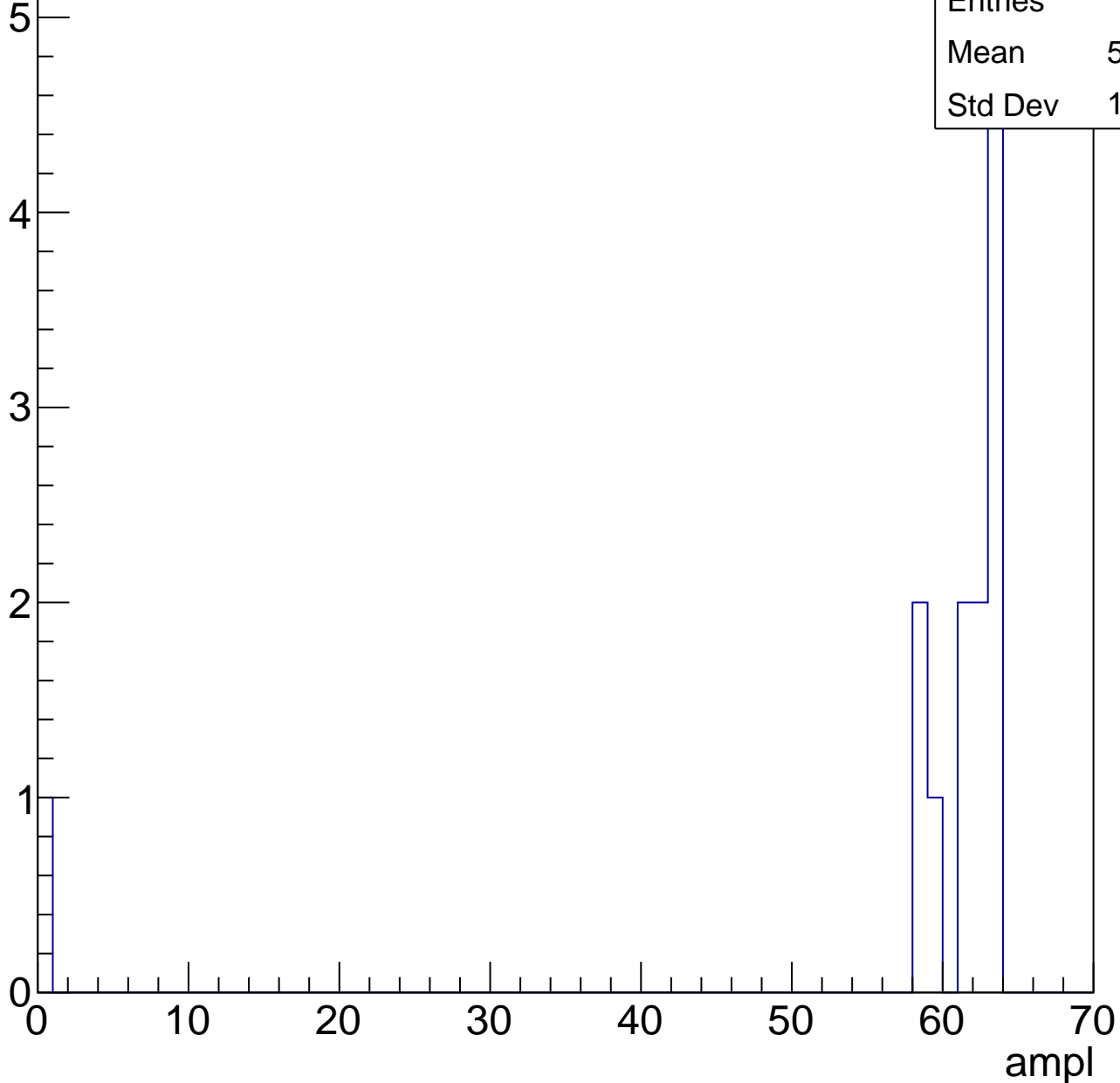


B1L100S, U15-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	13
Mean	56.62
Std Dev	16.44



B1L100S, U15-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch30, adc0

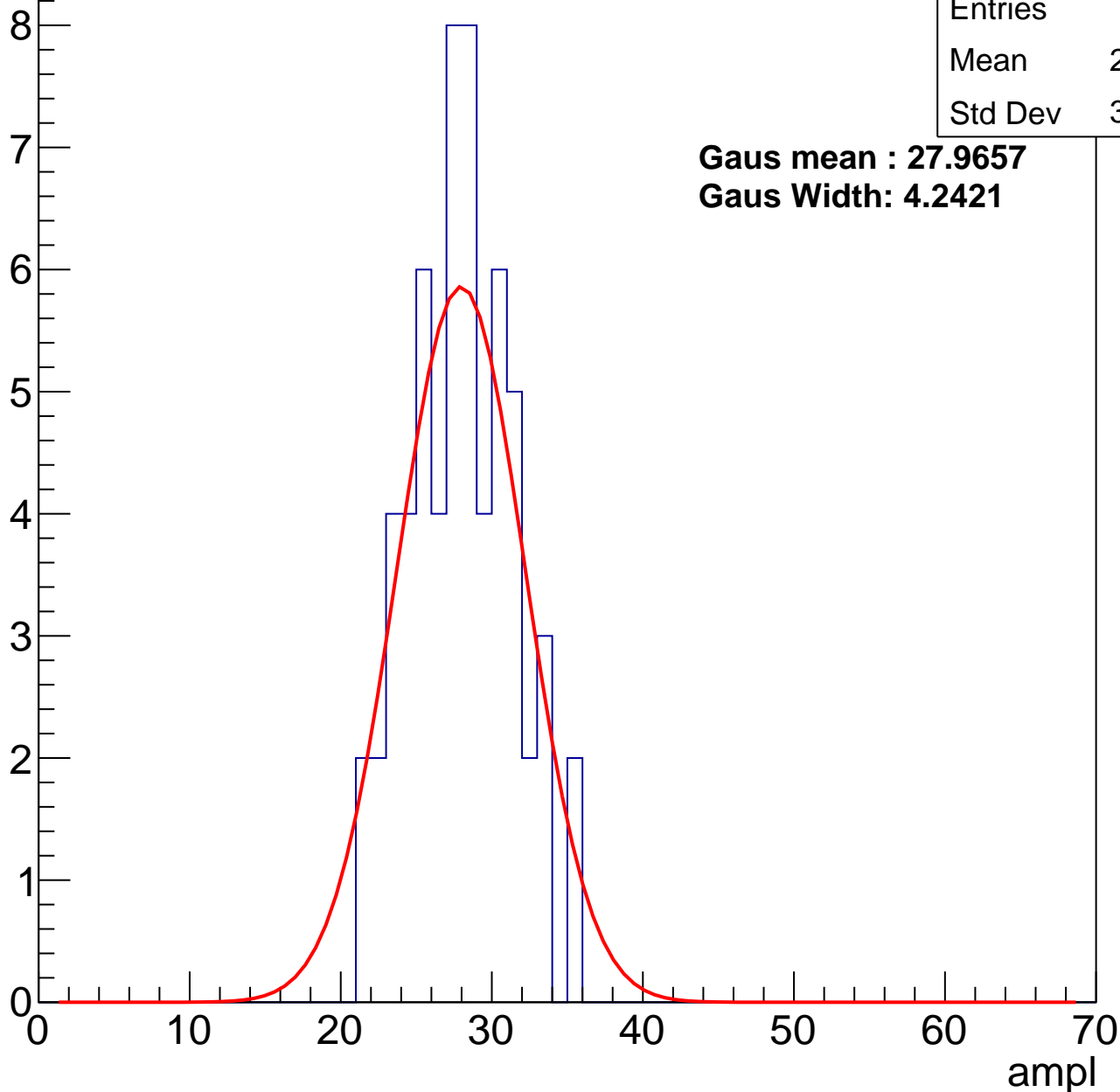
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	27.53
Std Dev	3.359

Gaus mean : 27.9657

Gaus Width: 4.2421



B1L100S, U15-ch30, adc1

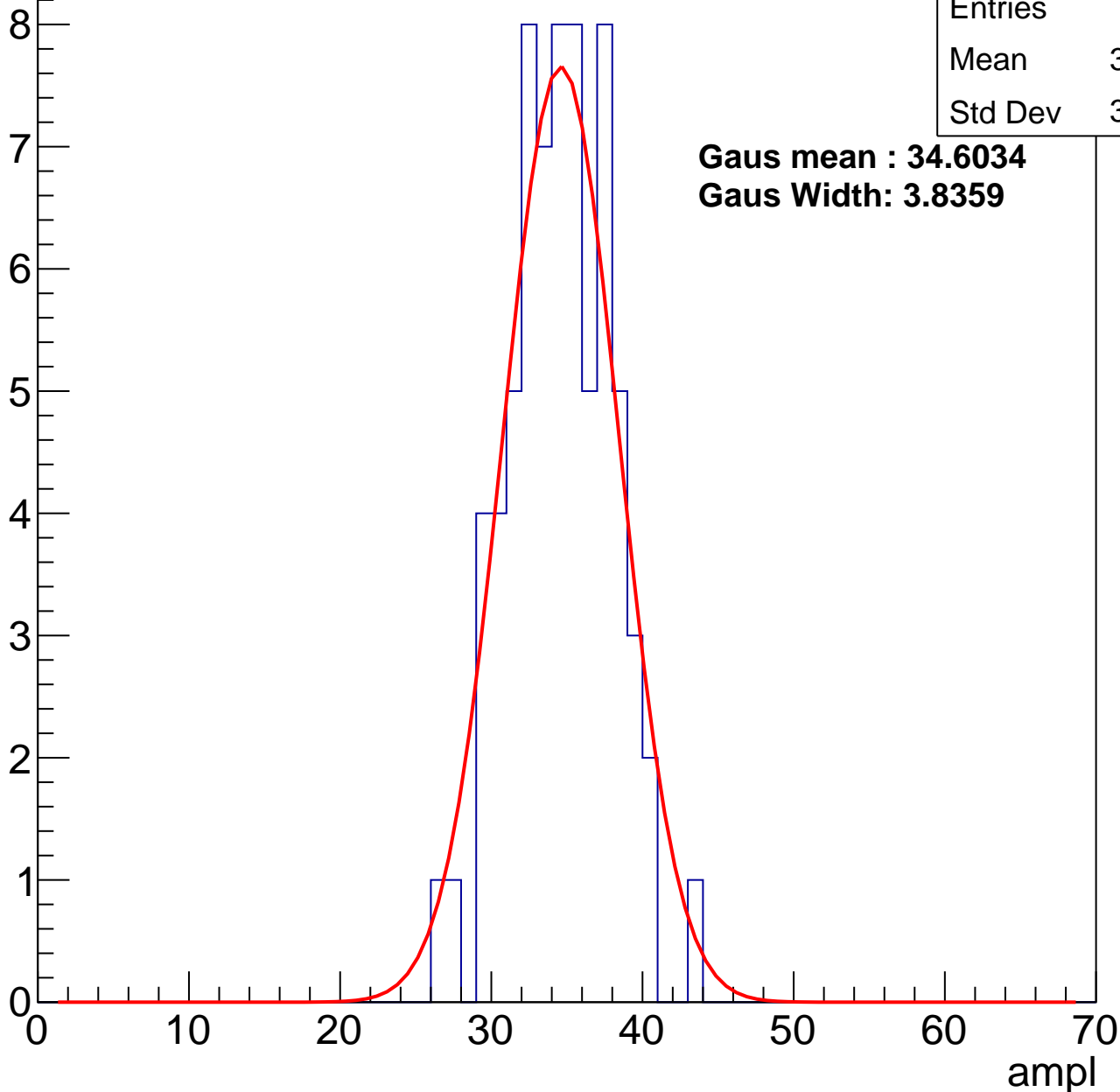
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	34.13
Std Dev	3.312

Gaus mean : 34.6034

Gaus Width: 3.8359



B1L100S, U15-ch30, adc2

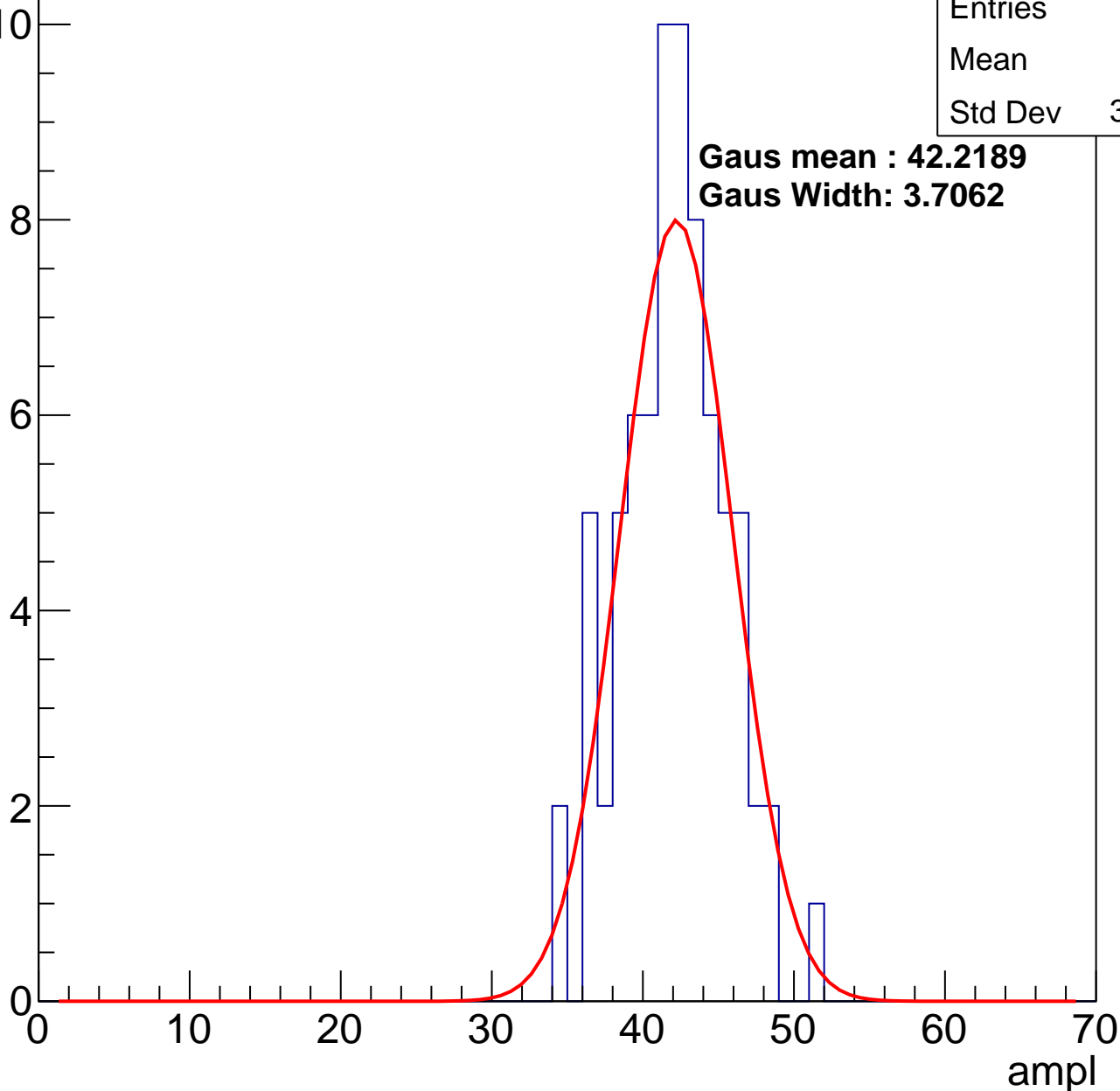
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	41.6
Std Dev	3.414

Gaus mean : 42.2189

Gaus Width: 3.7062

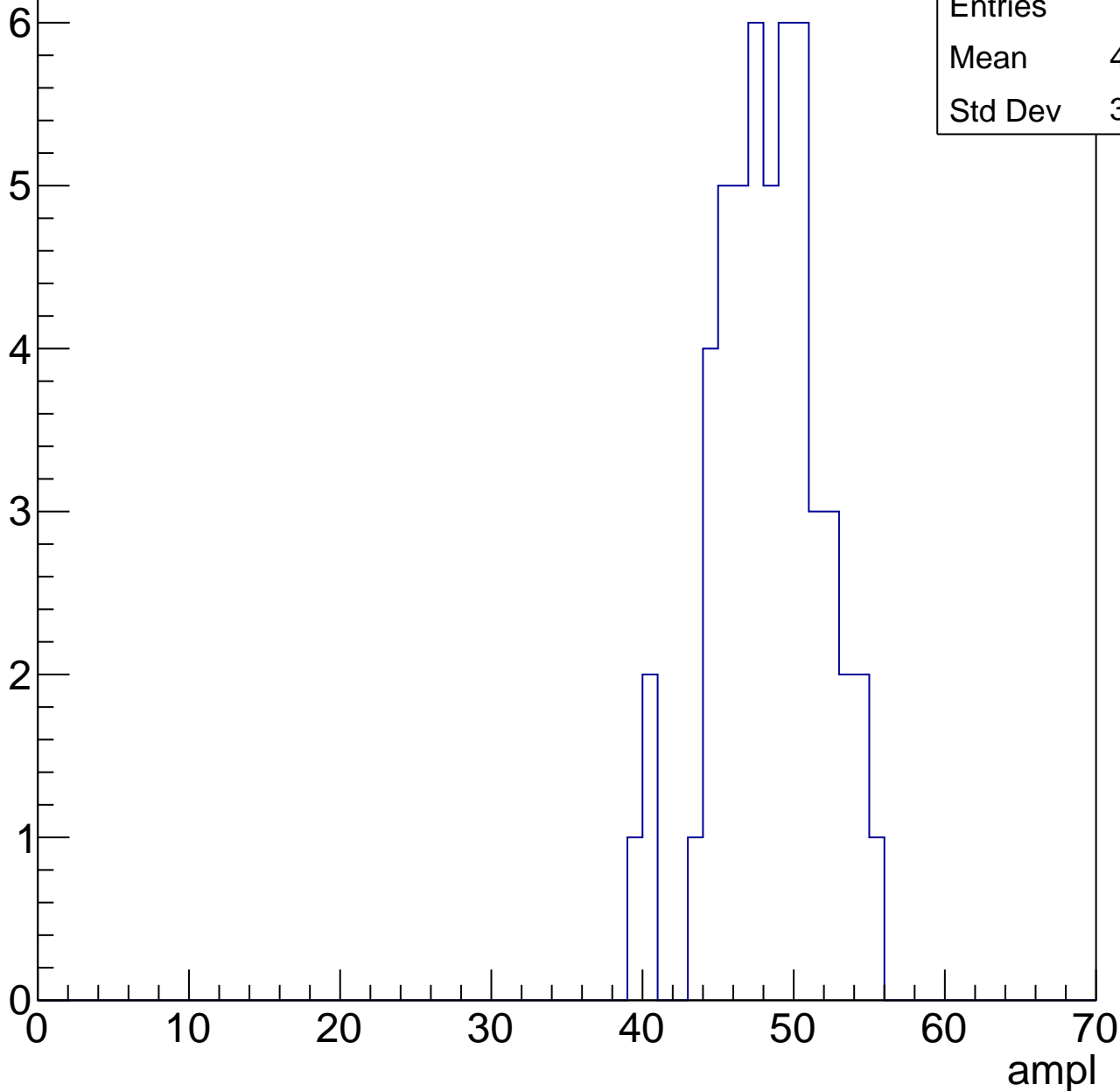


B1L100S, U15-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

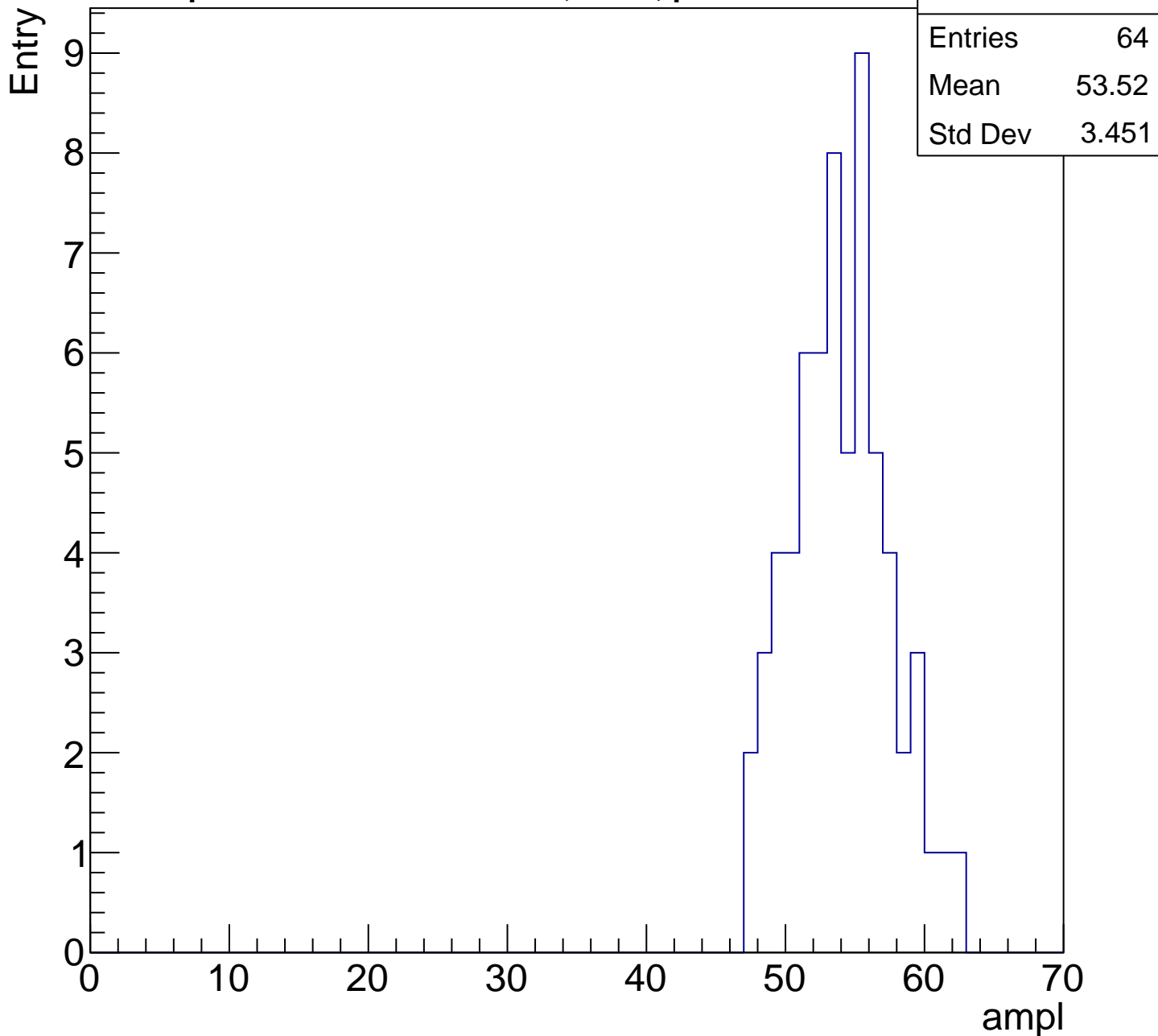
Entry

Entries	52
Mean	47.83
Std Dev	3.518



B1L100S, U15-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

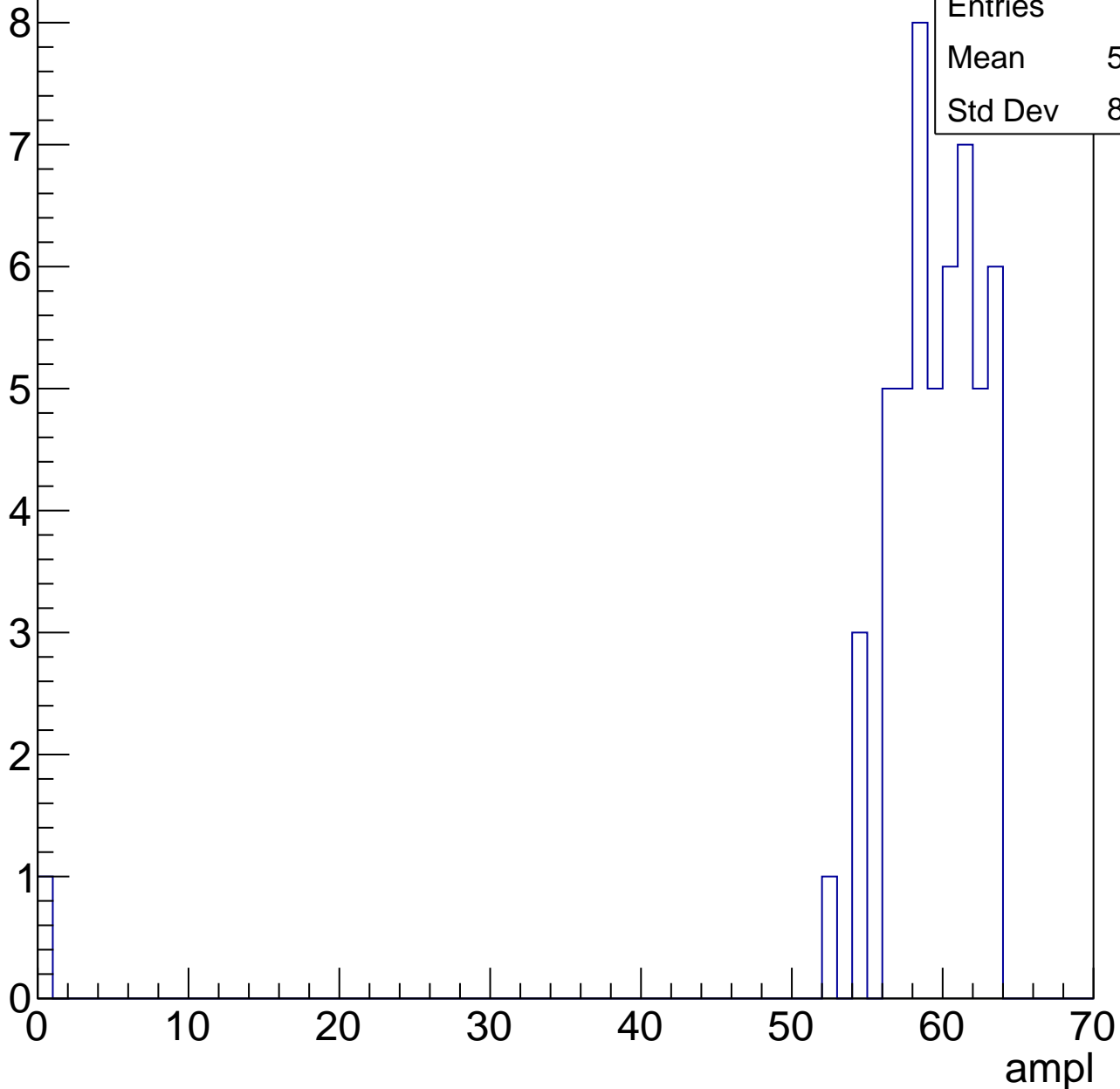


B1L100S, U15-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

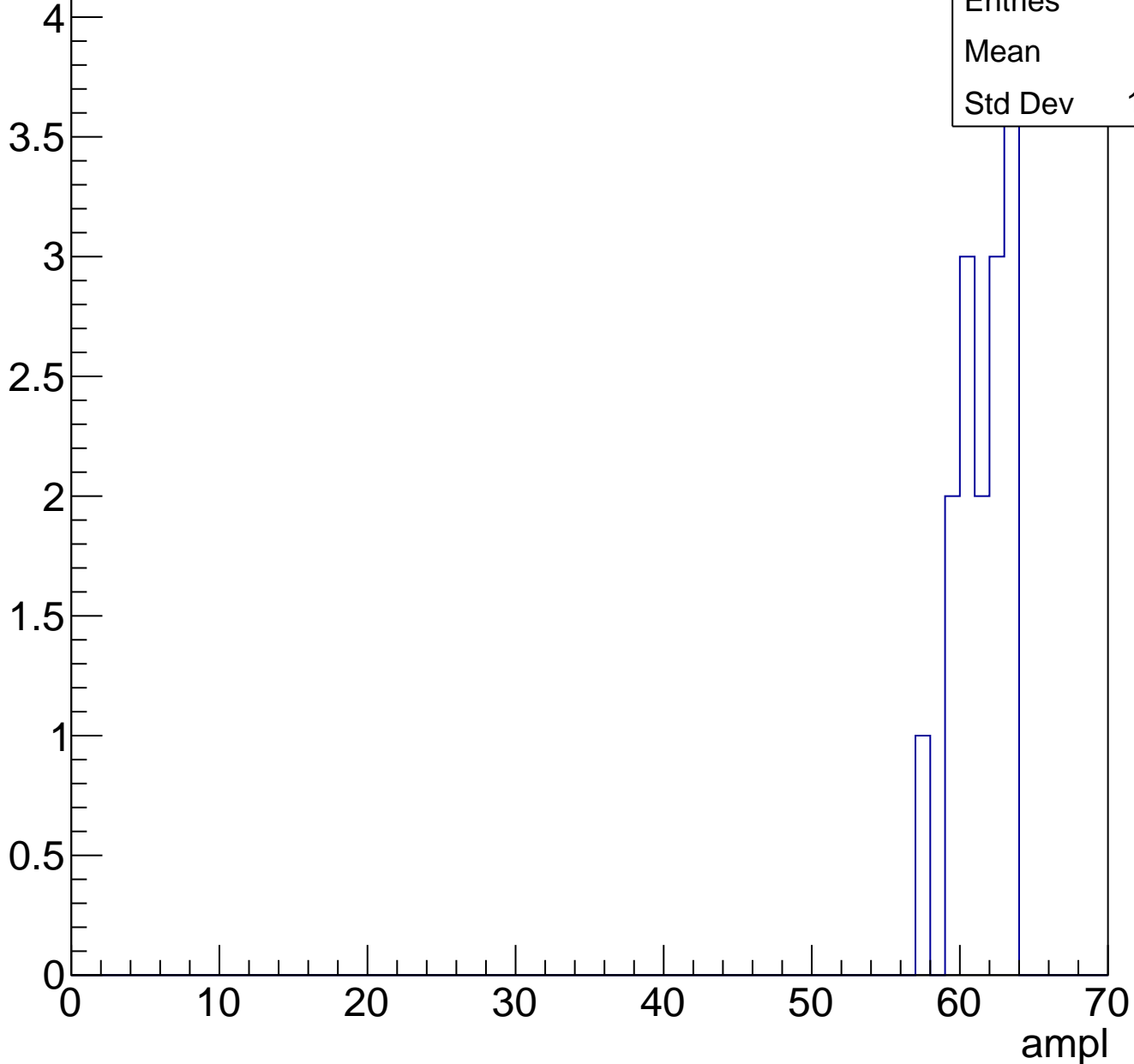
Entries	52
Mean	57.94
Std Dev	8.543



B1L100S, U15-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch31, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	29.51
Std Dev	4.849

Gaus mean : 30.0373

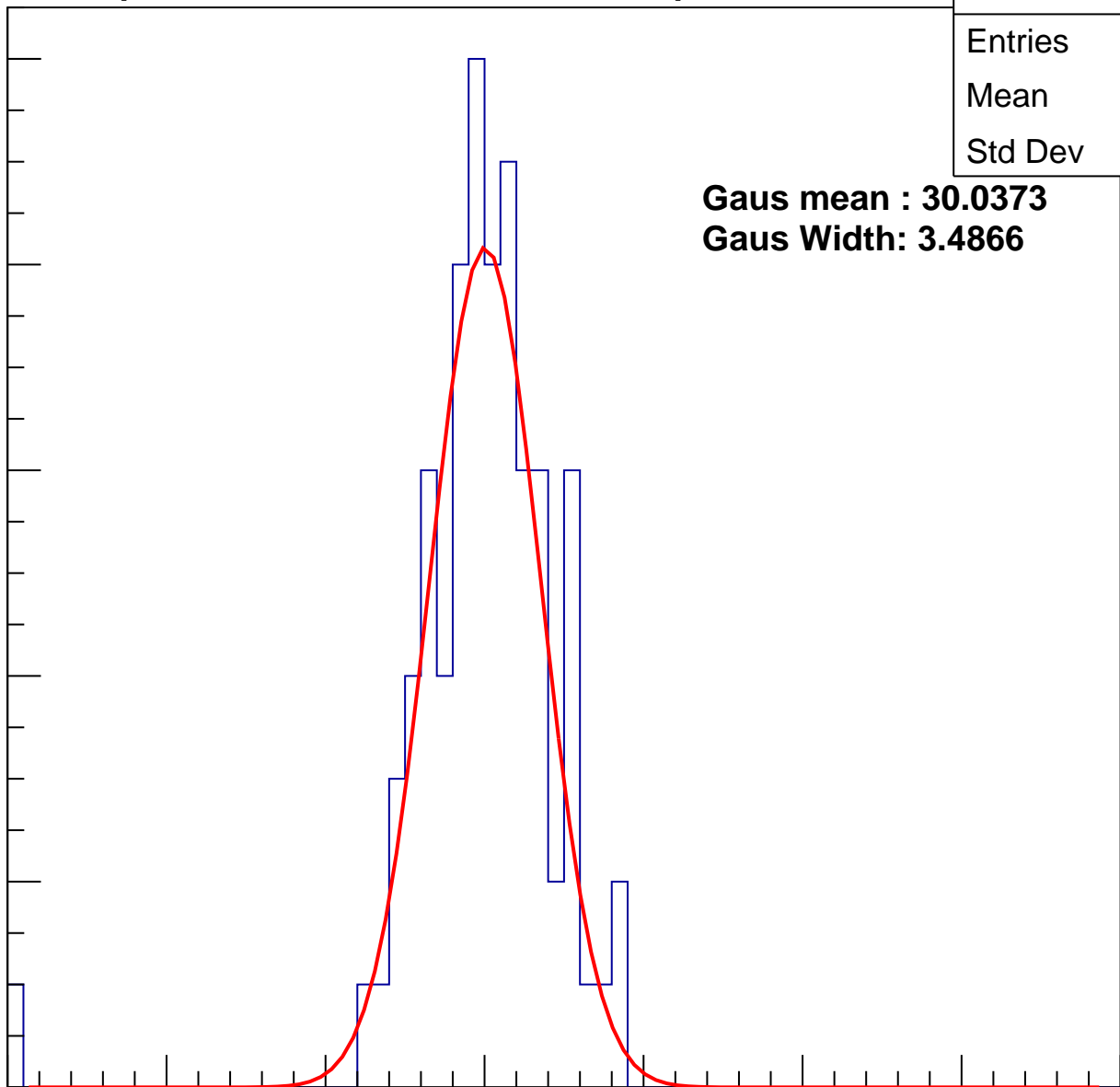
Gaus Width: 3.4866

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



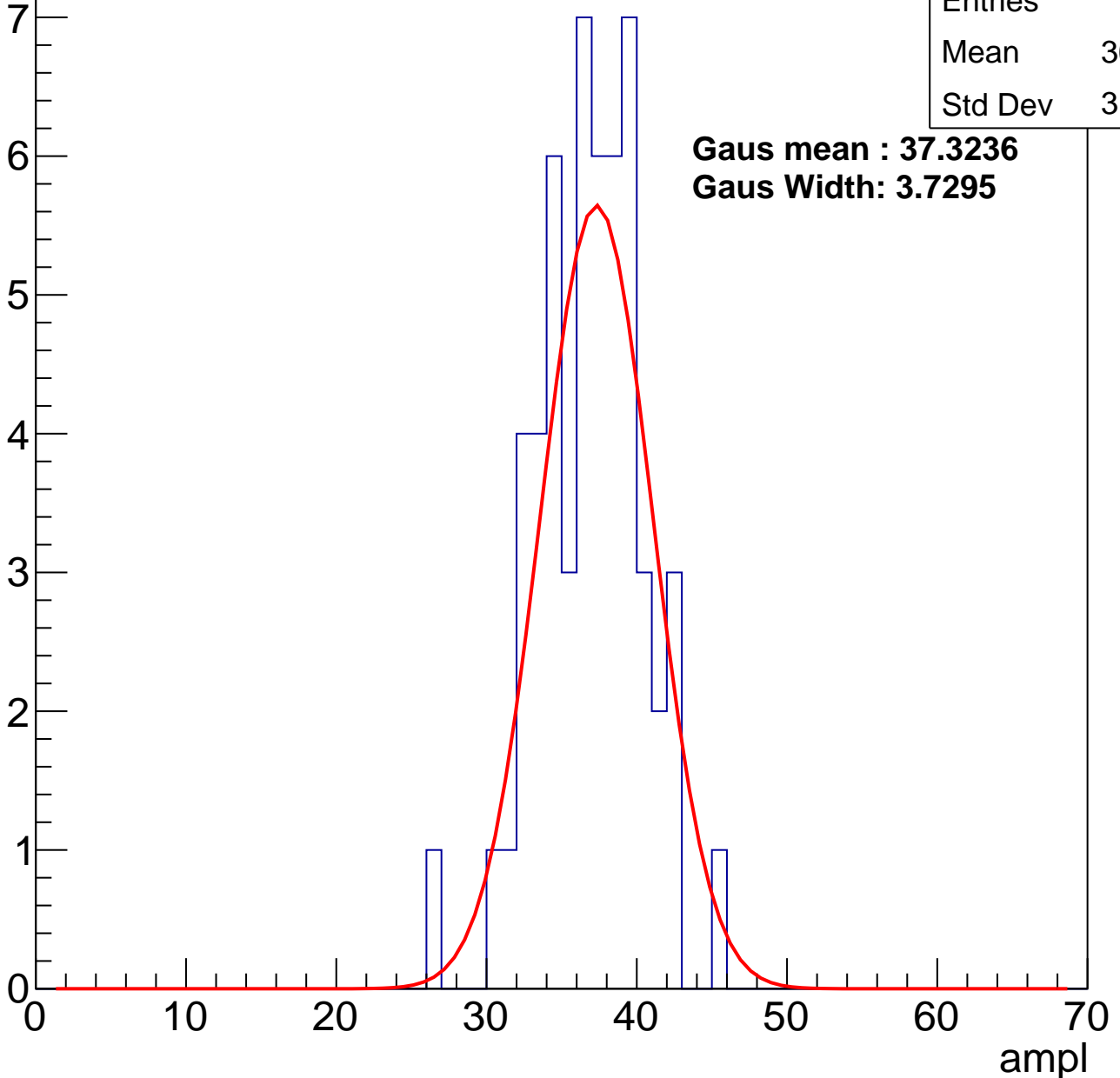
B1L100S, U15-ch31, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	36.44
Std Dev	3.458

Gaus mean : 37.3236
Gaus Width: 3.7295



B1L100S, U15-ch31, adc2

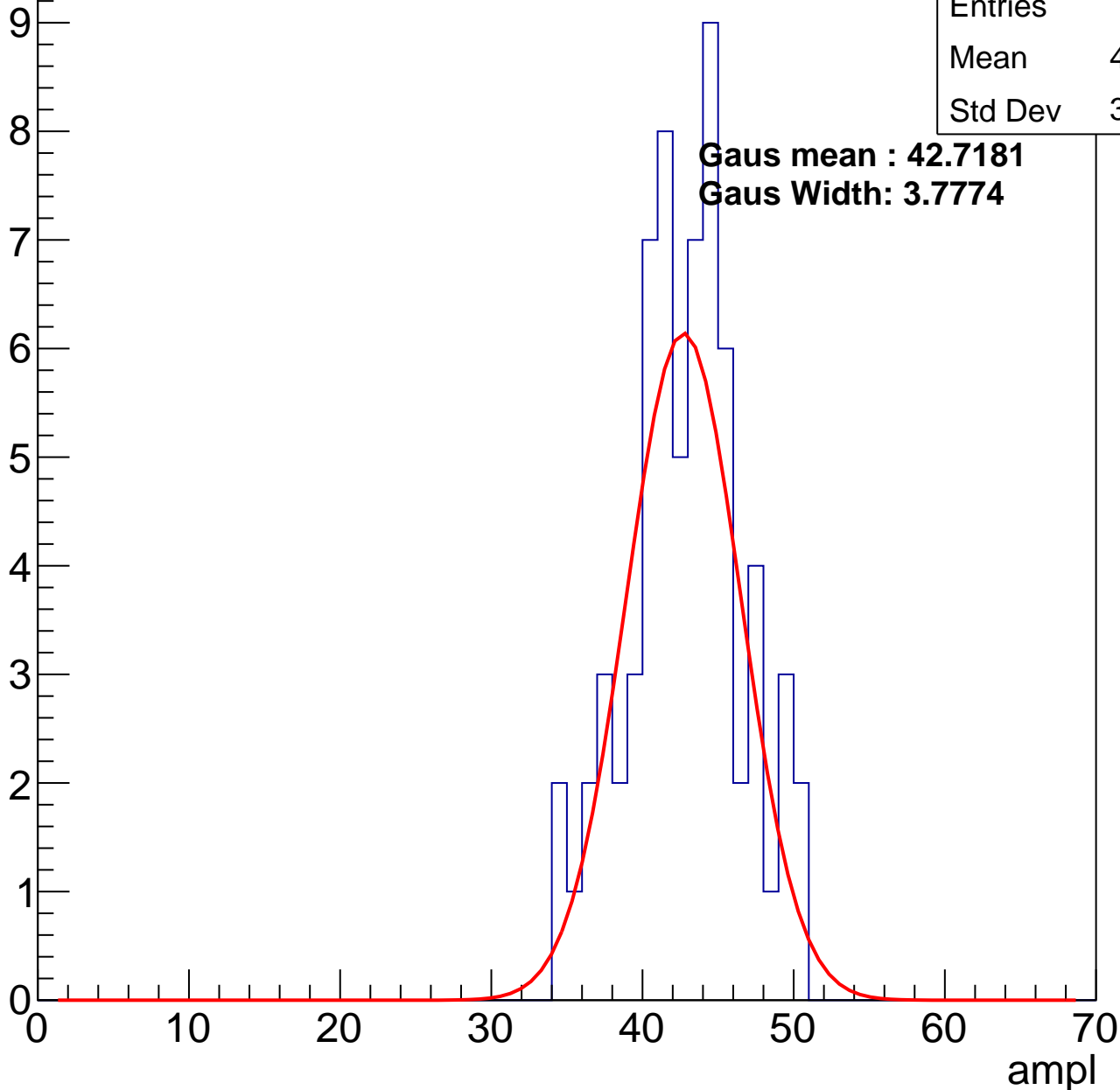
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42.37
Std Dev	3.765

Gaus mean : 42.7181

Gaus Width: 3.7774

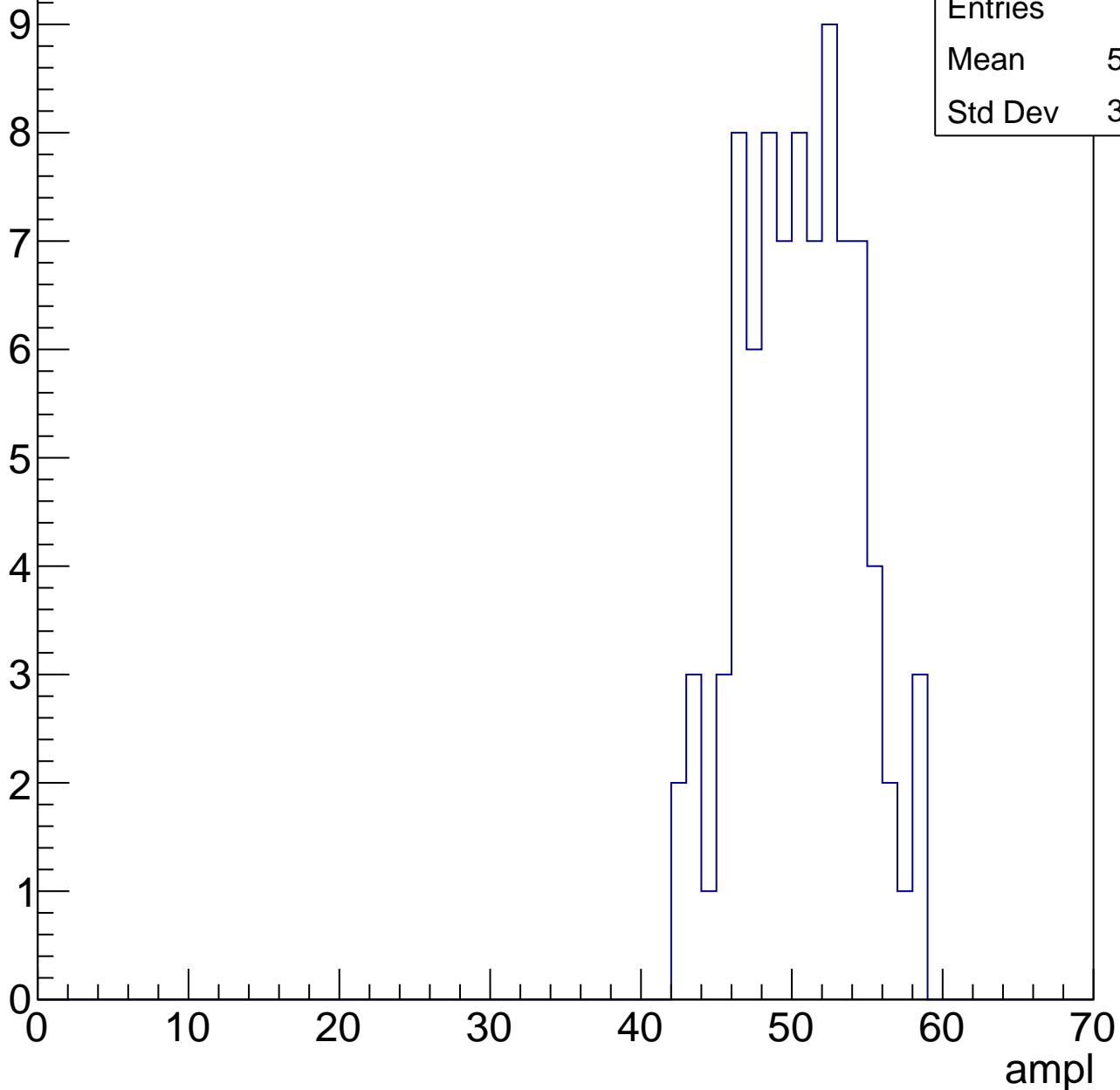


B1L100S, U15-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	50.07
Std Dev	3.793

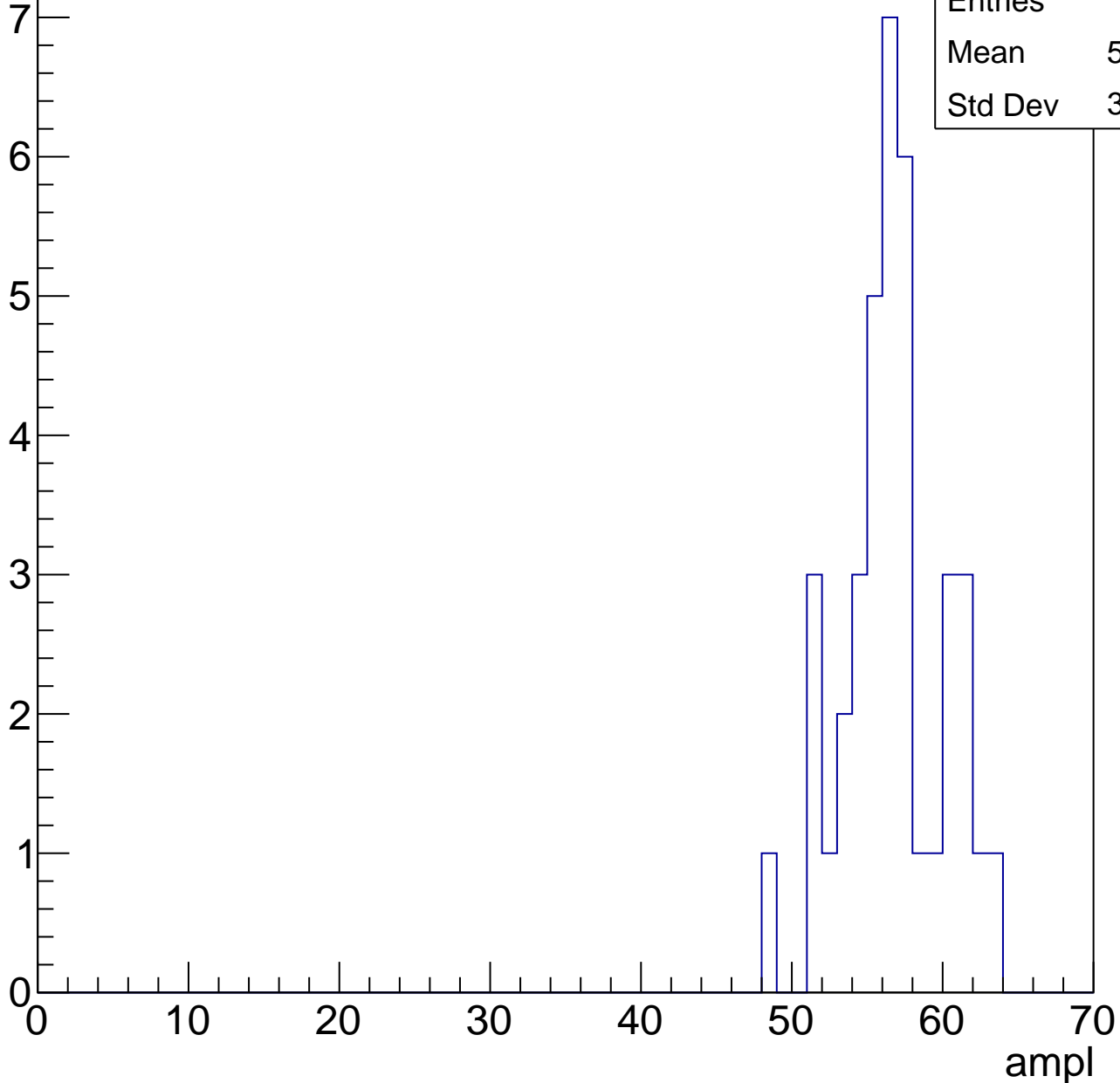


B1L100S, U15-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

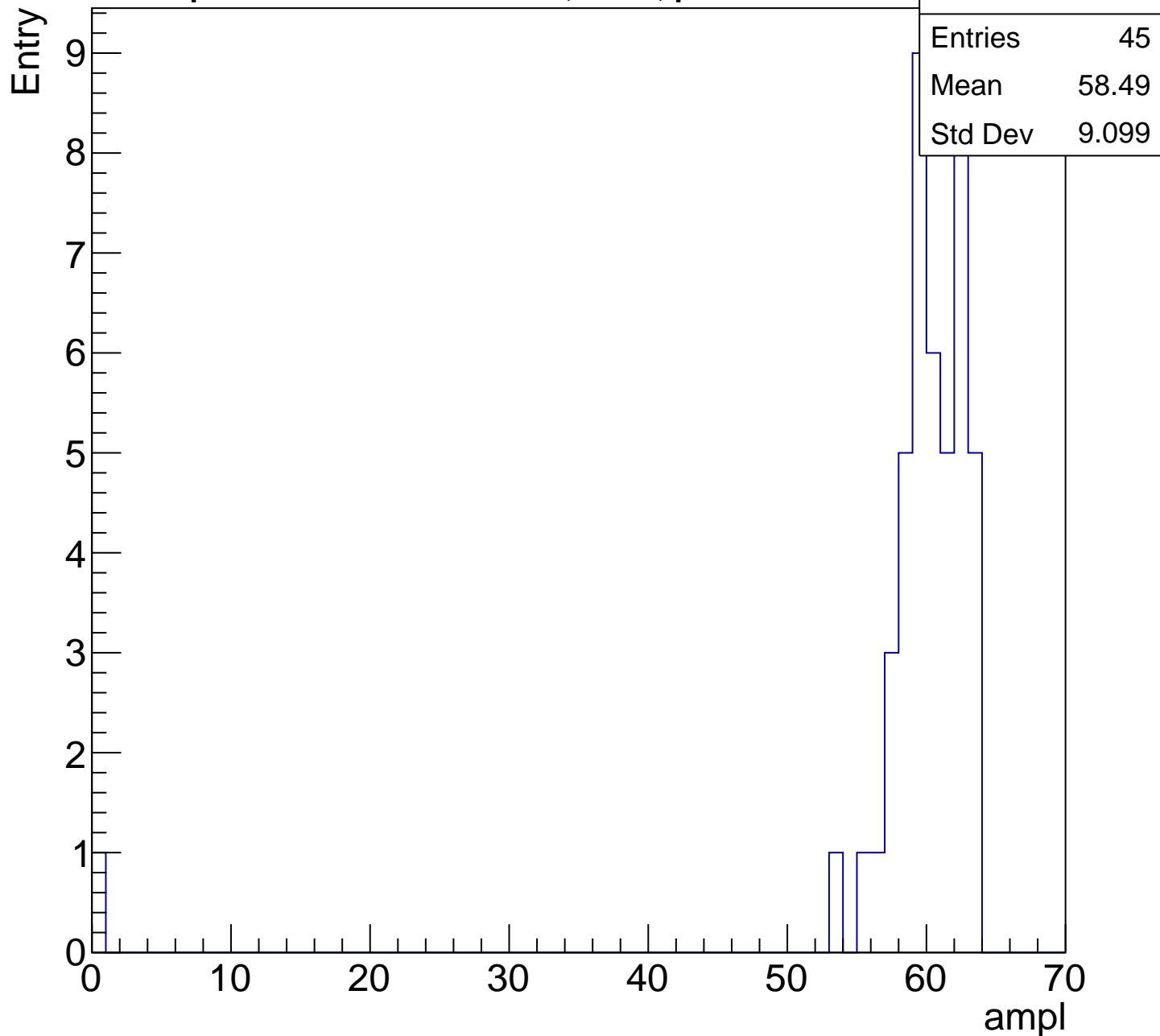
Entry

Entries	38
Mean	56.18
Std Dev	3.308



B1L100S, U15-ch31, adc5

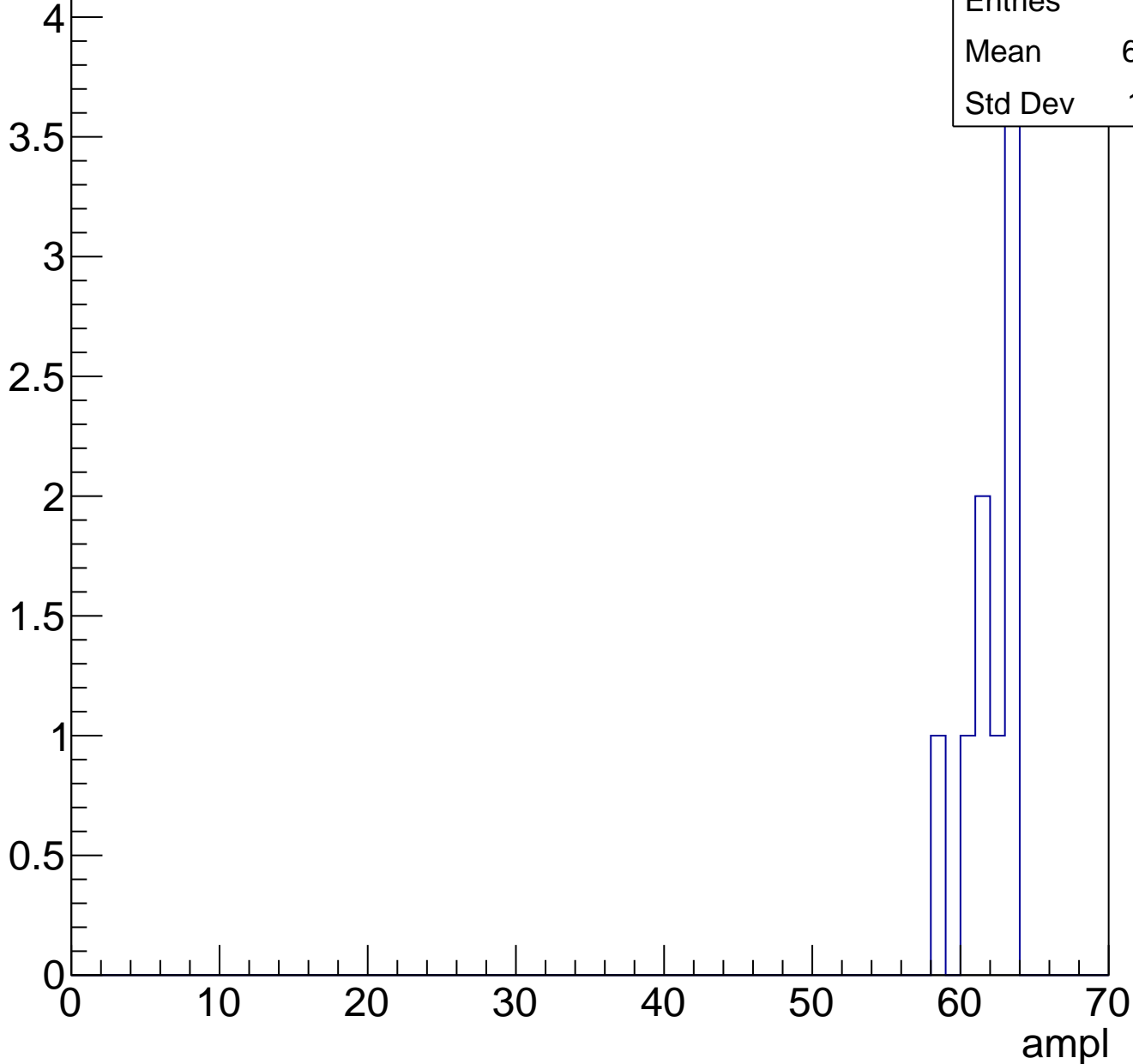
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch32, adc0

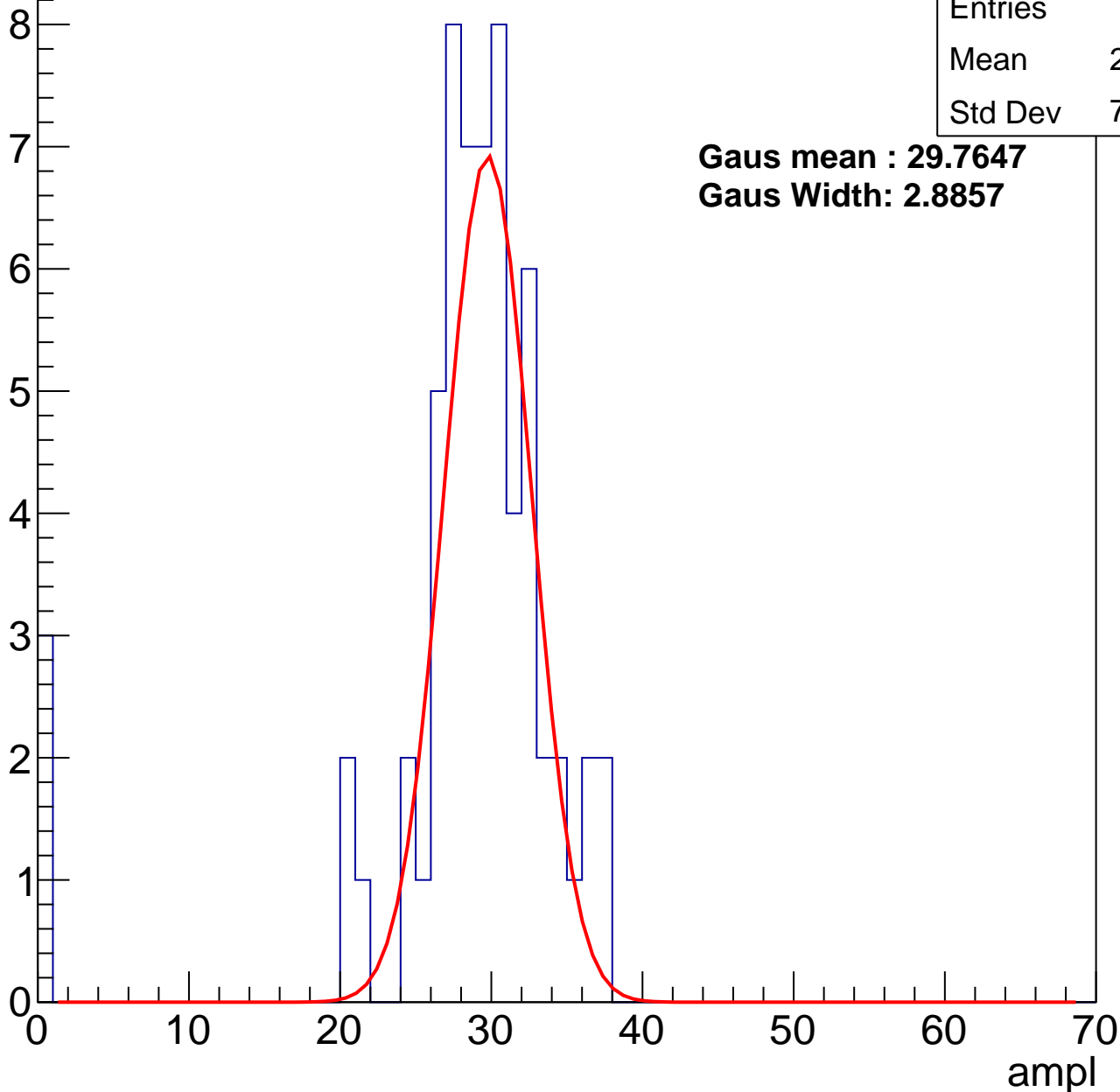
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.78
Std Dev	7.157

Gaus mean : 29.7647

Gaus Width: 2.8857



B1L100S, U15-ch32, adc1

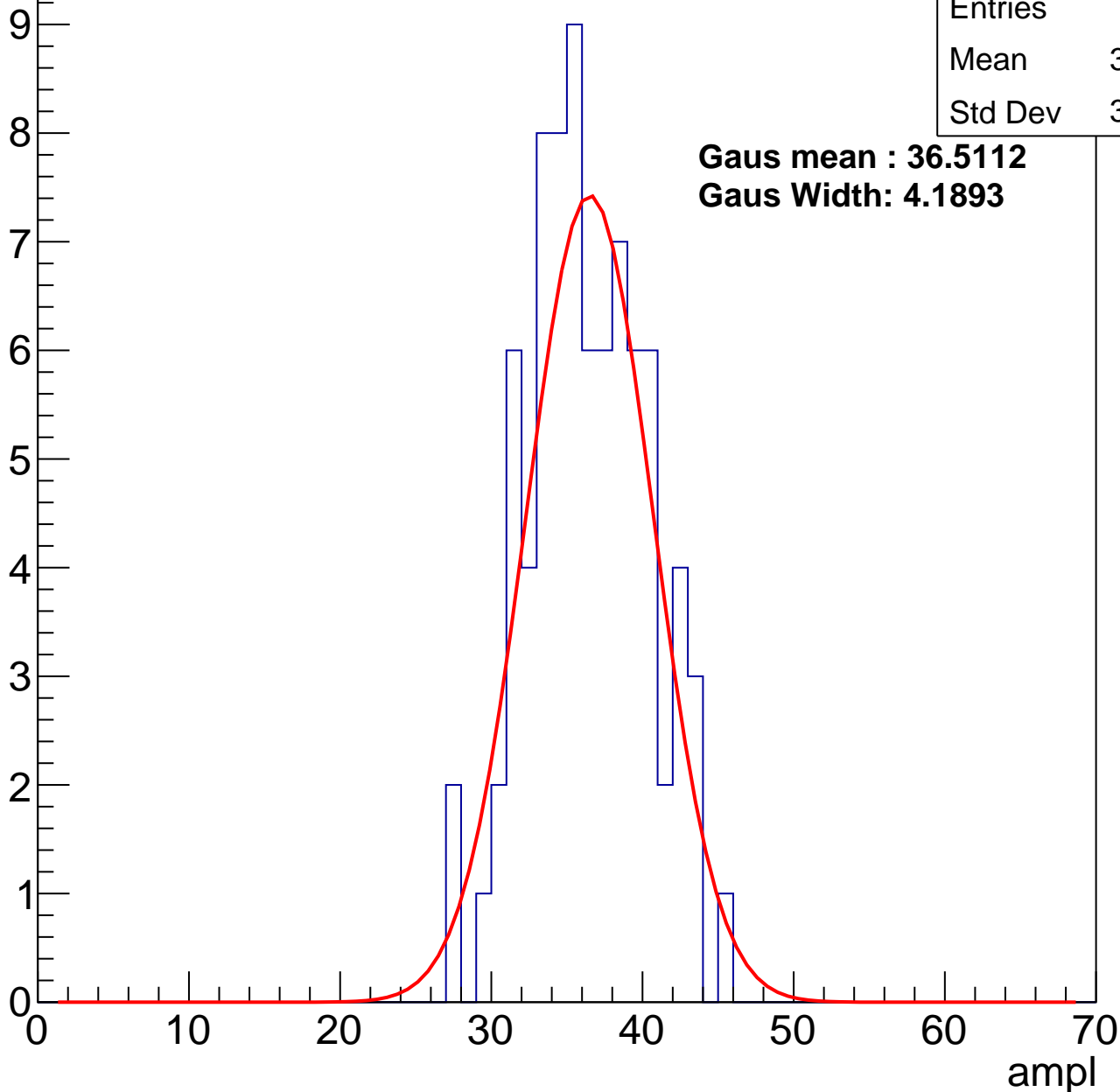
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	35.93
Std Dev	3.874

Gaus mean : 36.5112

Gaus Width: 4.1893



B1L100S, U15-ch32, adc2

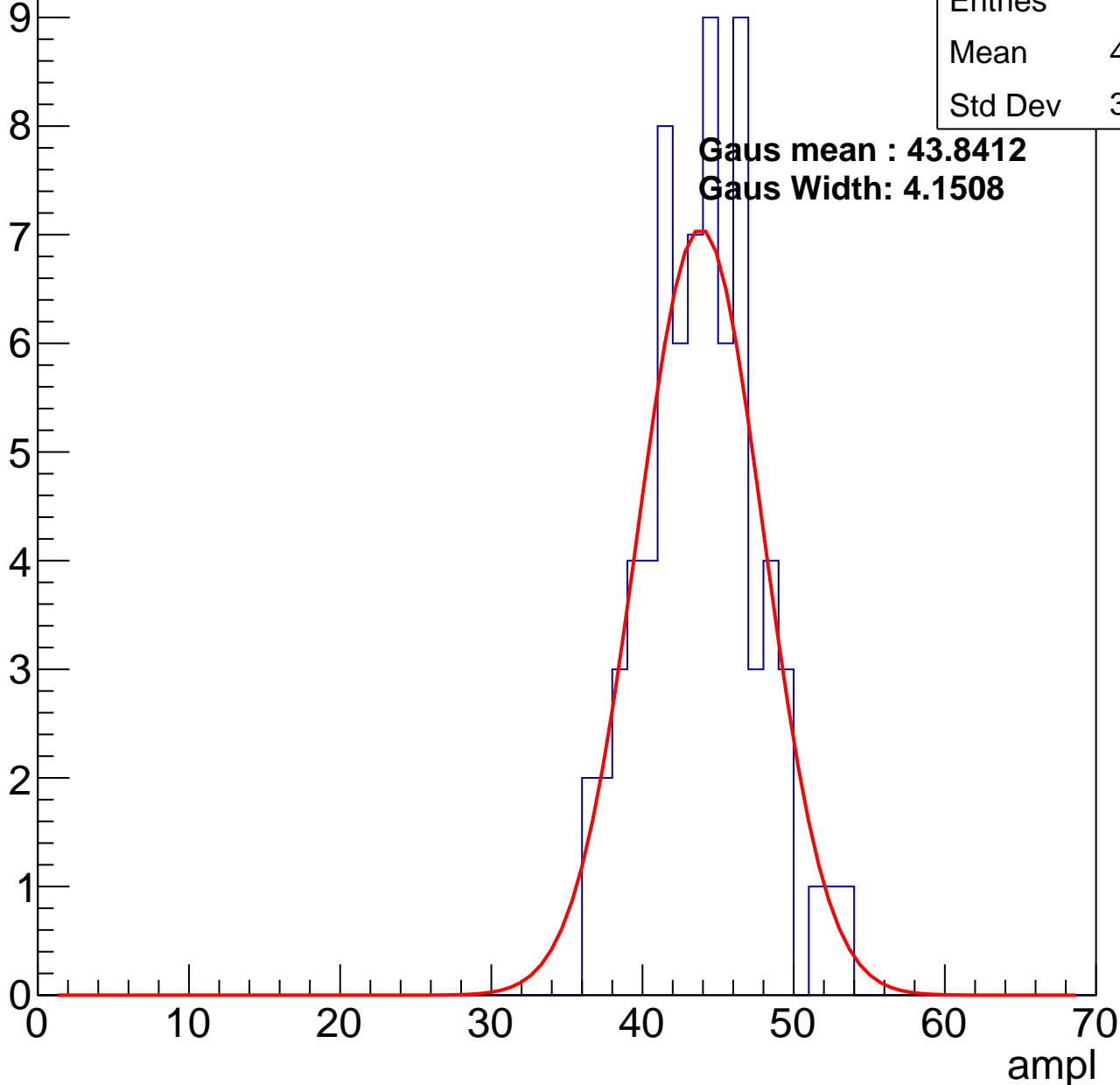
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	43.47
Std Dev	3.668

Gaus mean : 43.8412

Gaus Width: 4.1508

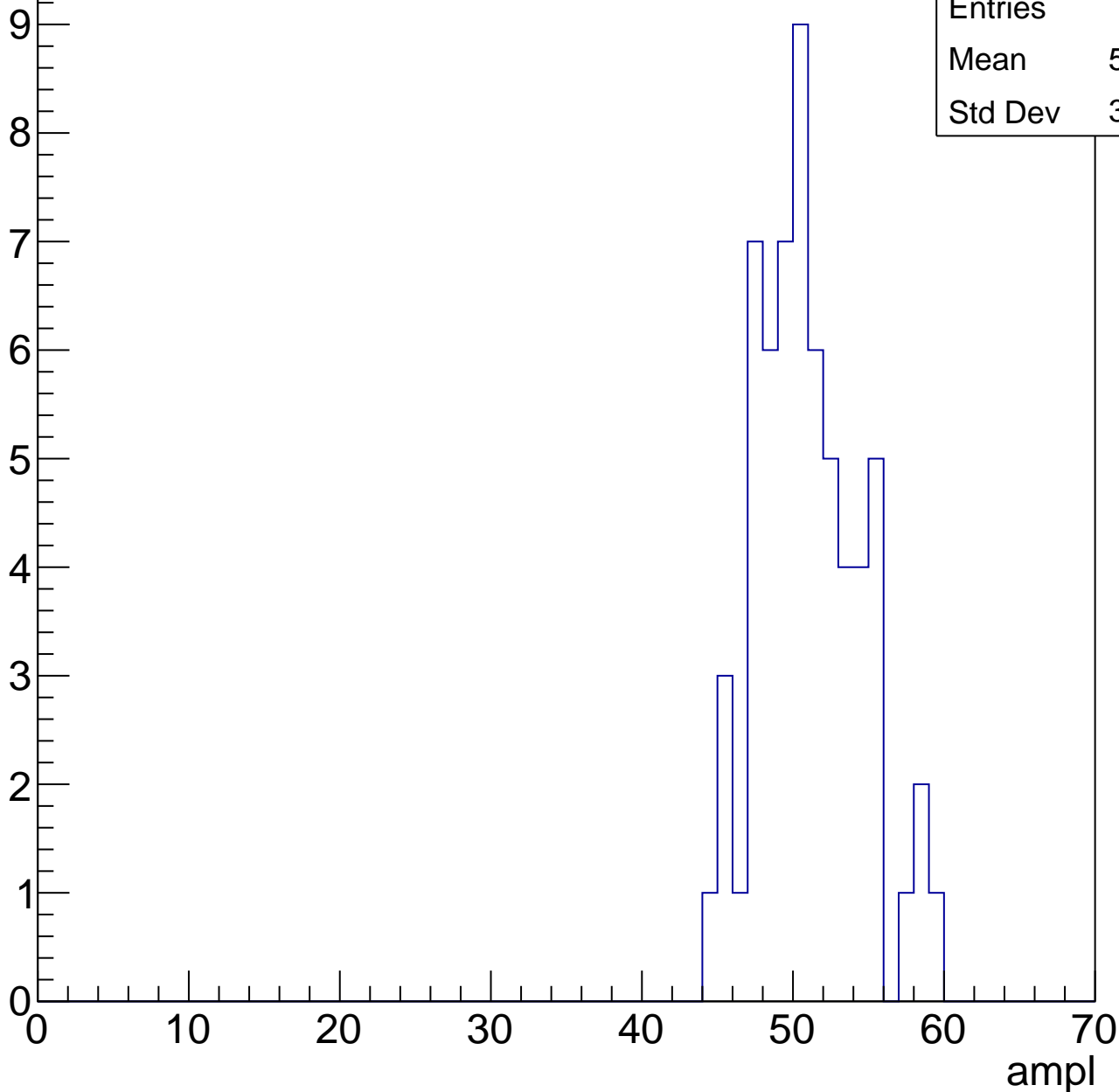


B1L100S, U15-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	50.58
Std Dev	3.382

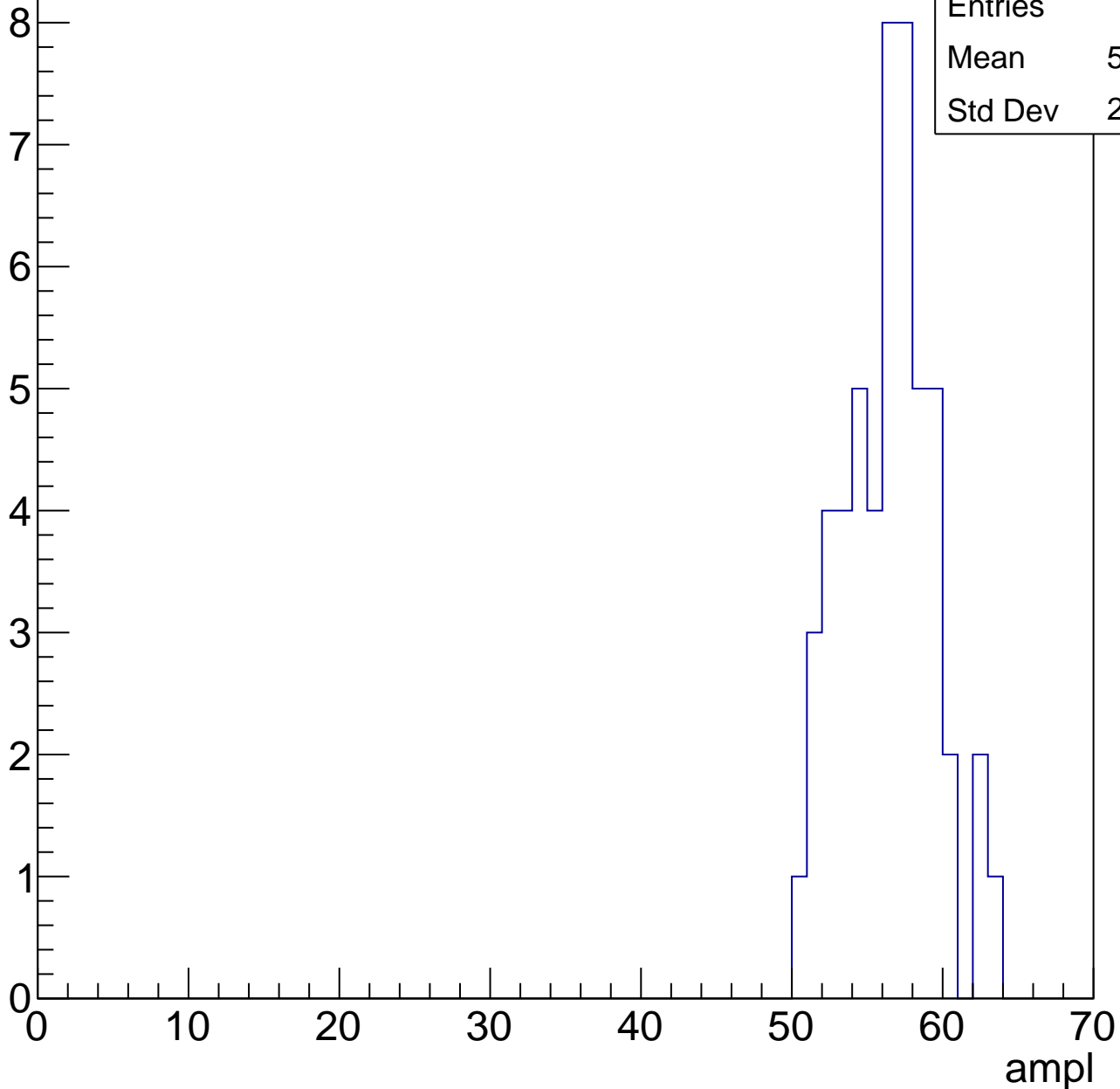


B1L100S, U15-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

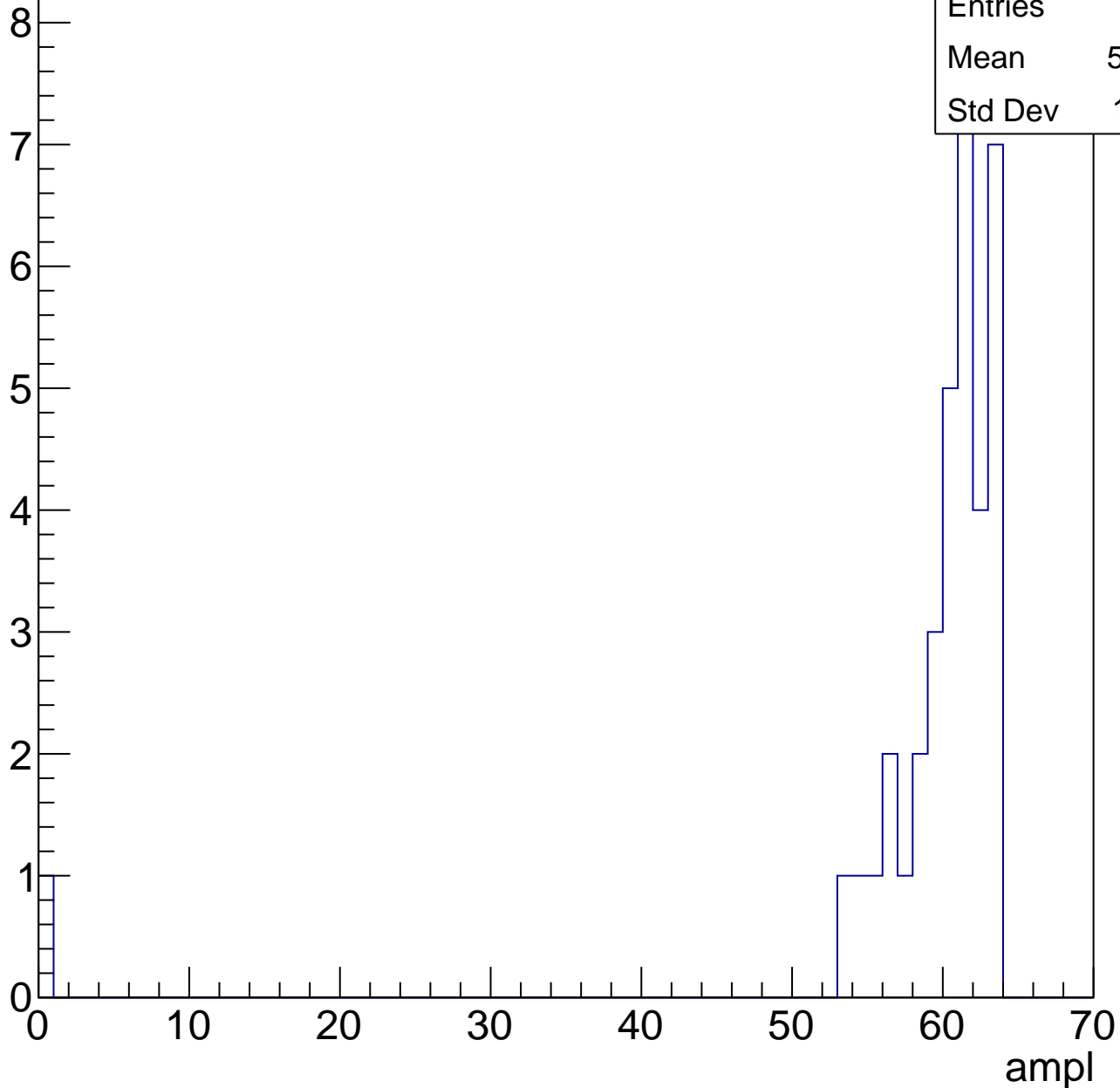
Entries	52
Mean	55.94
Std Dev	2.977



B1L100S, U15-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

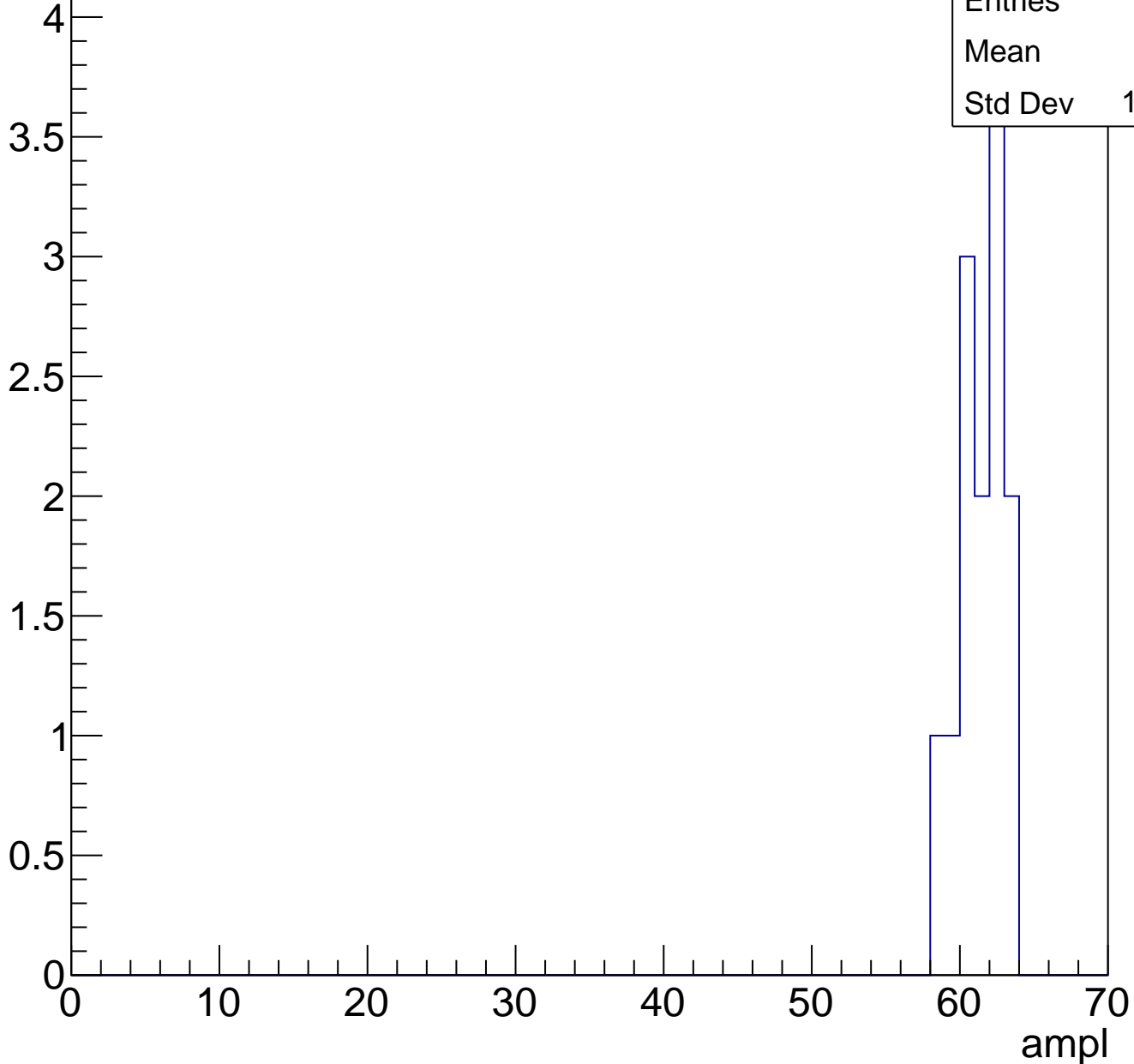
Entry



B1L100S, U15-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch33, adc0

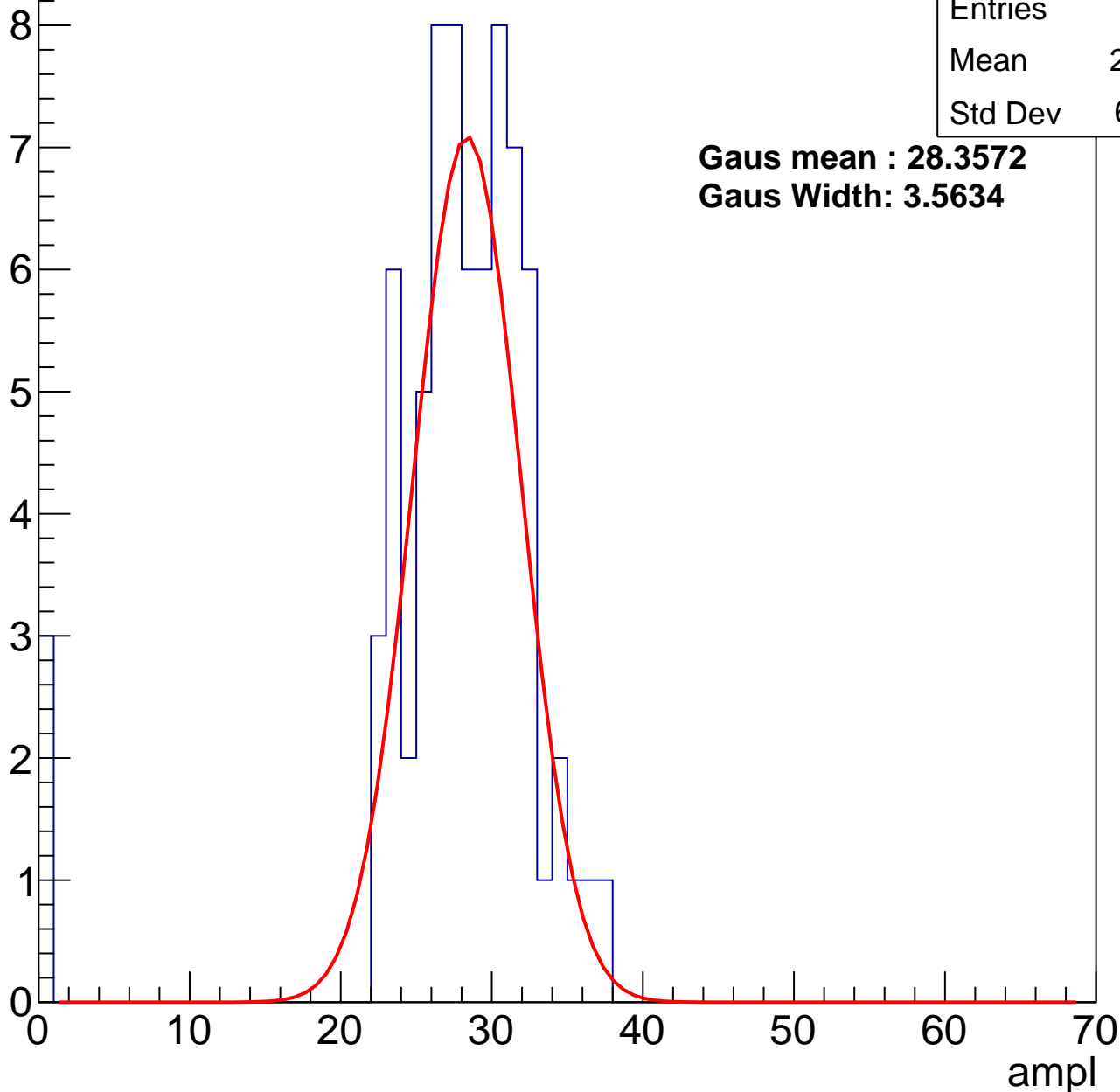
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	27.04
Std Dev	6.521

Gaus mean : 28.3572

Gaus Width: 3.5634



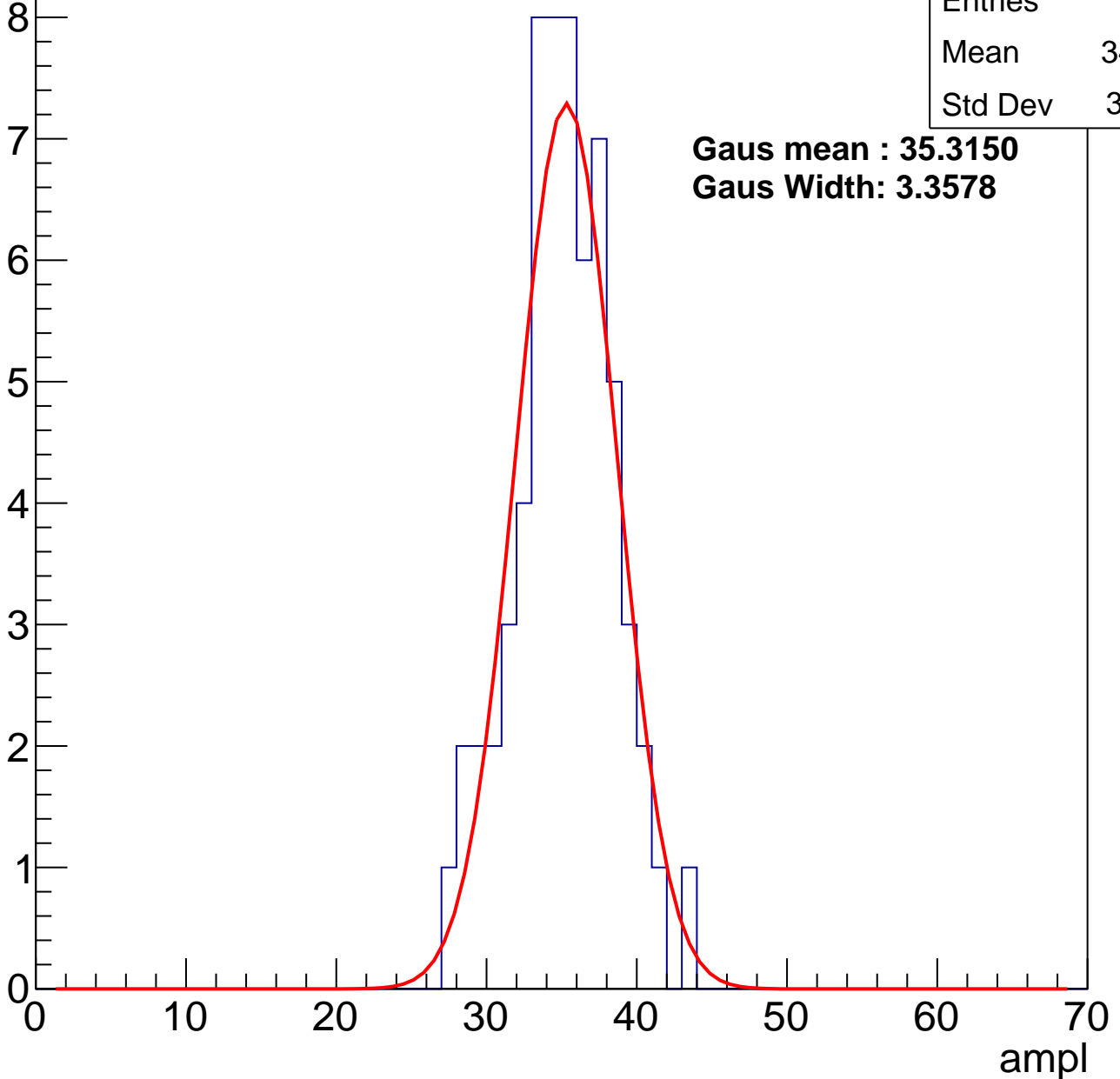
B1L100S, U15-ch33, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	34.67
Std Dev	3.281

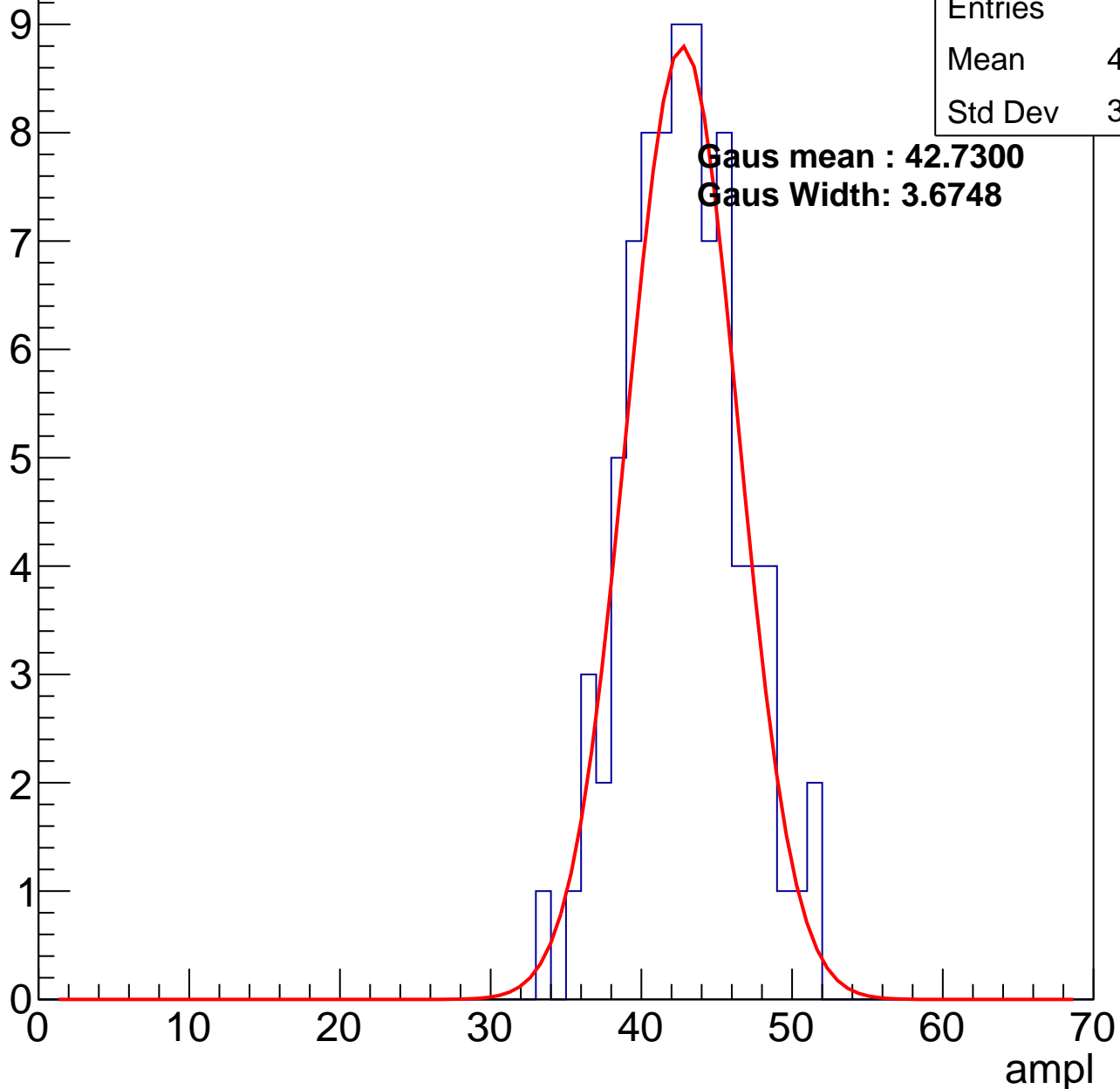
Gaus mean : 35.3150
Gaus Width: 3.3578



B1L100S, U15-ch33, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	84
Mean	42.37
Std Dev	3.703

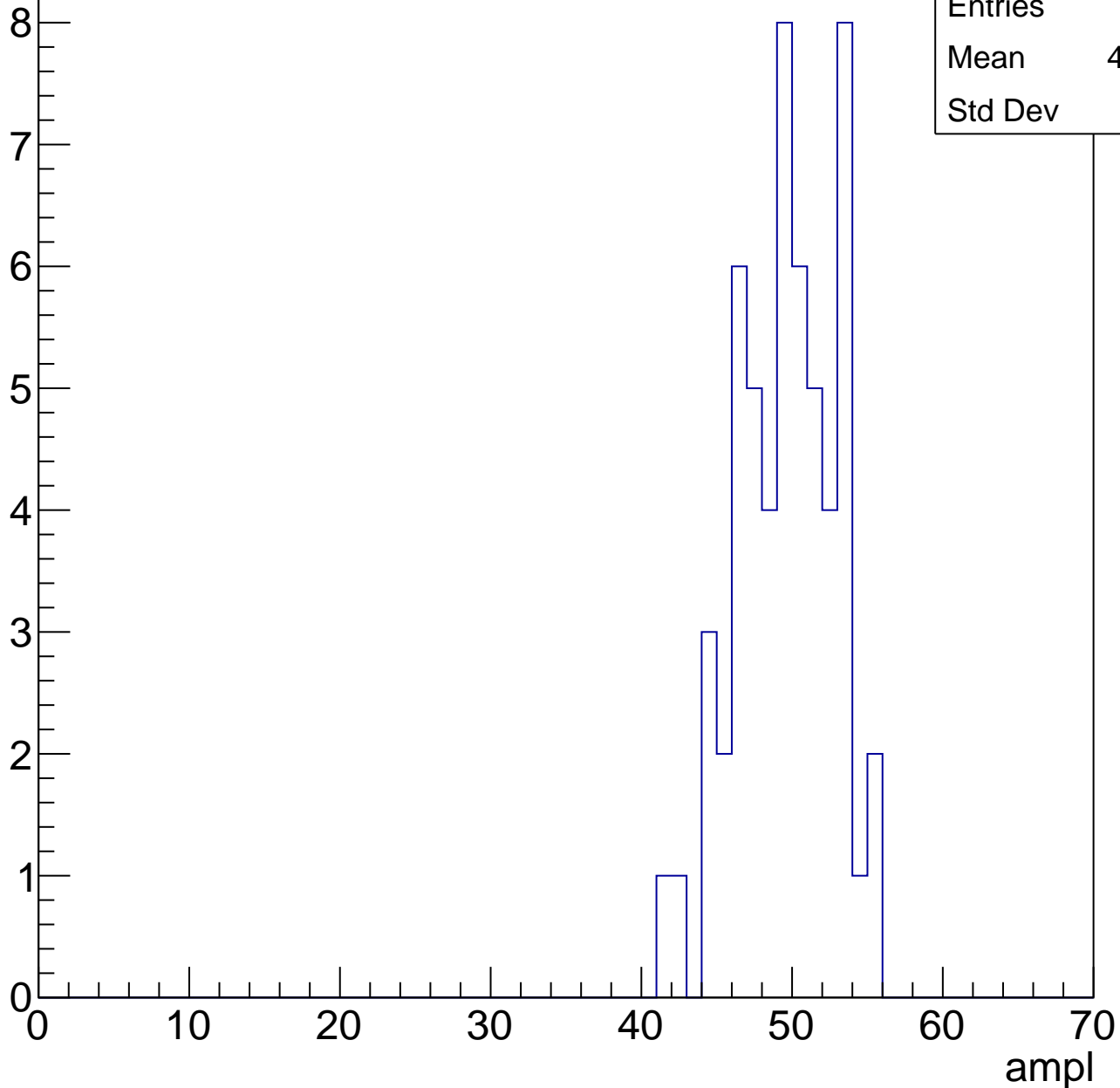
Gaus mean : 42.7300

Gaus Width: 3.6748

B1L100S, U15-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



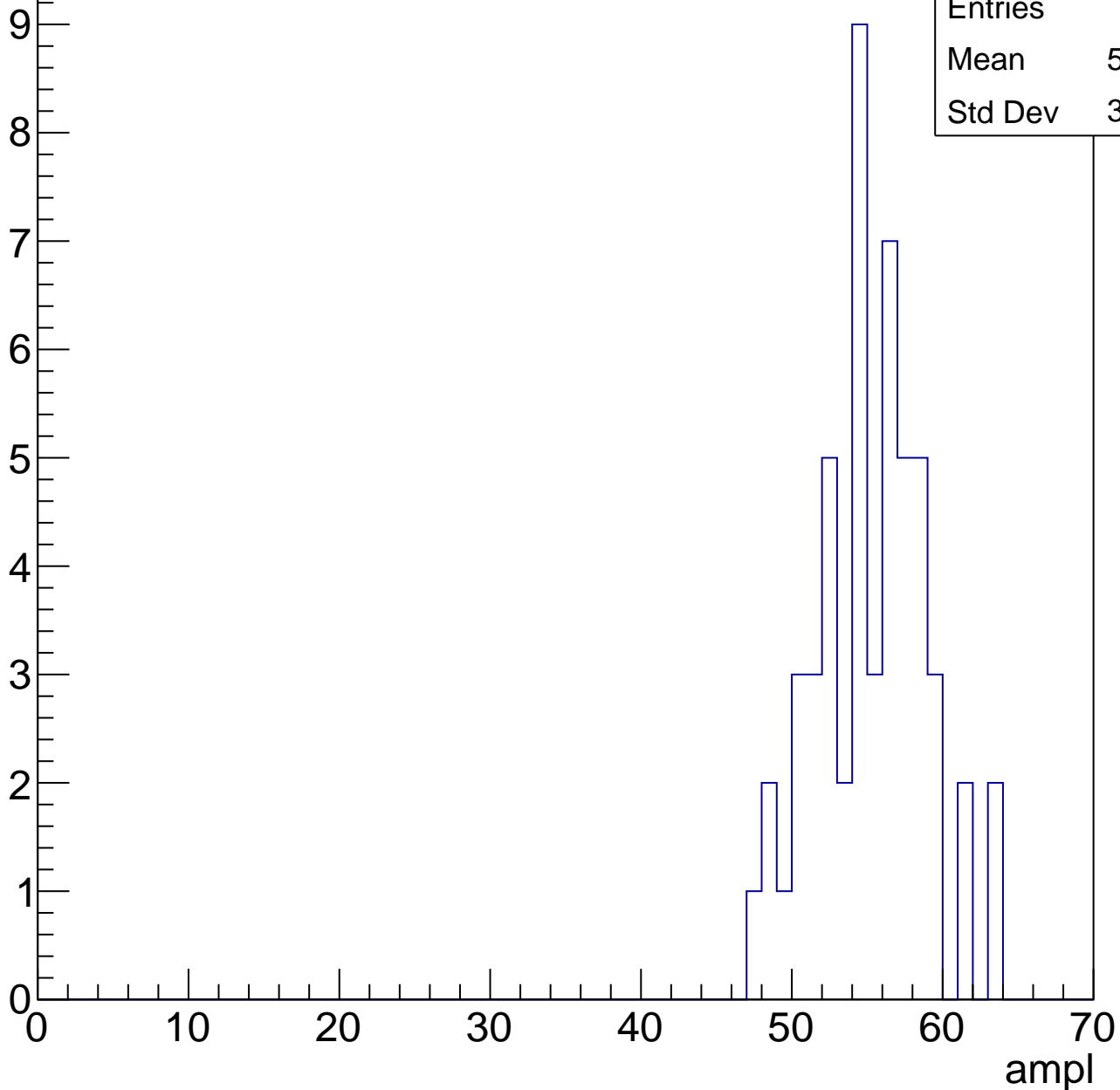
Entries	56
Mean	49.12
Std Dev	3.23

B1L100S, U15-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

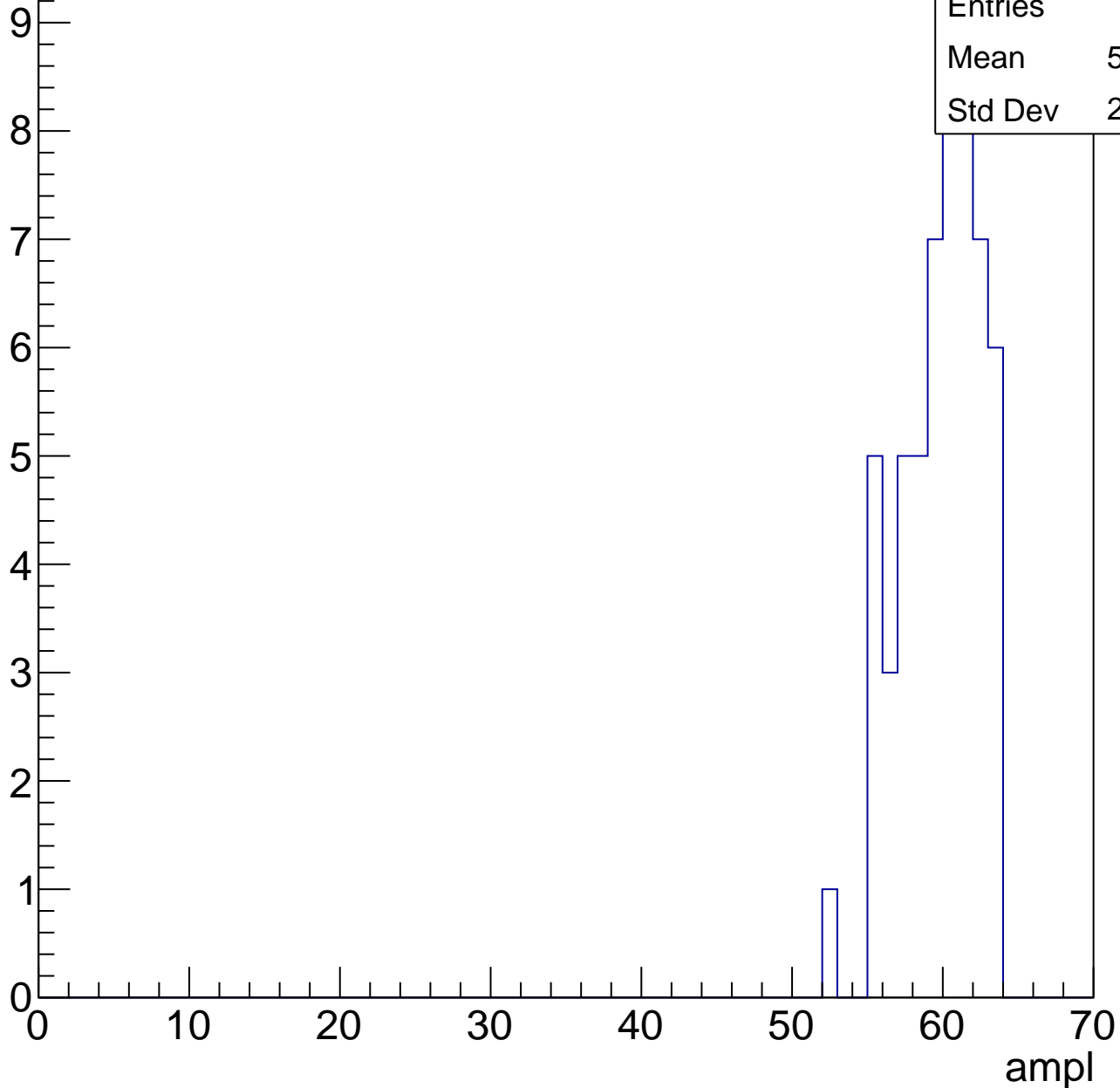
Entries	53
Mean	54.79
Std Dev	3.636



B1L100S, U15-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

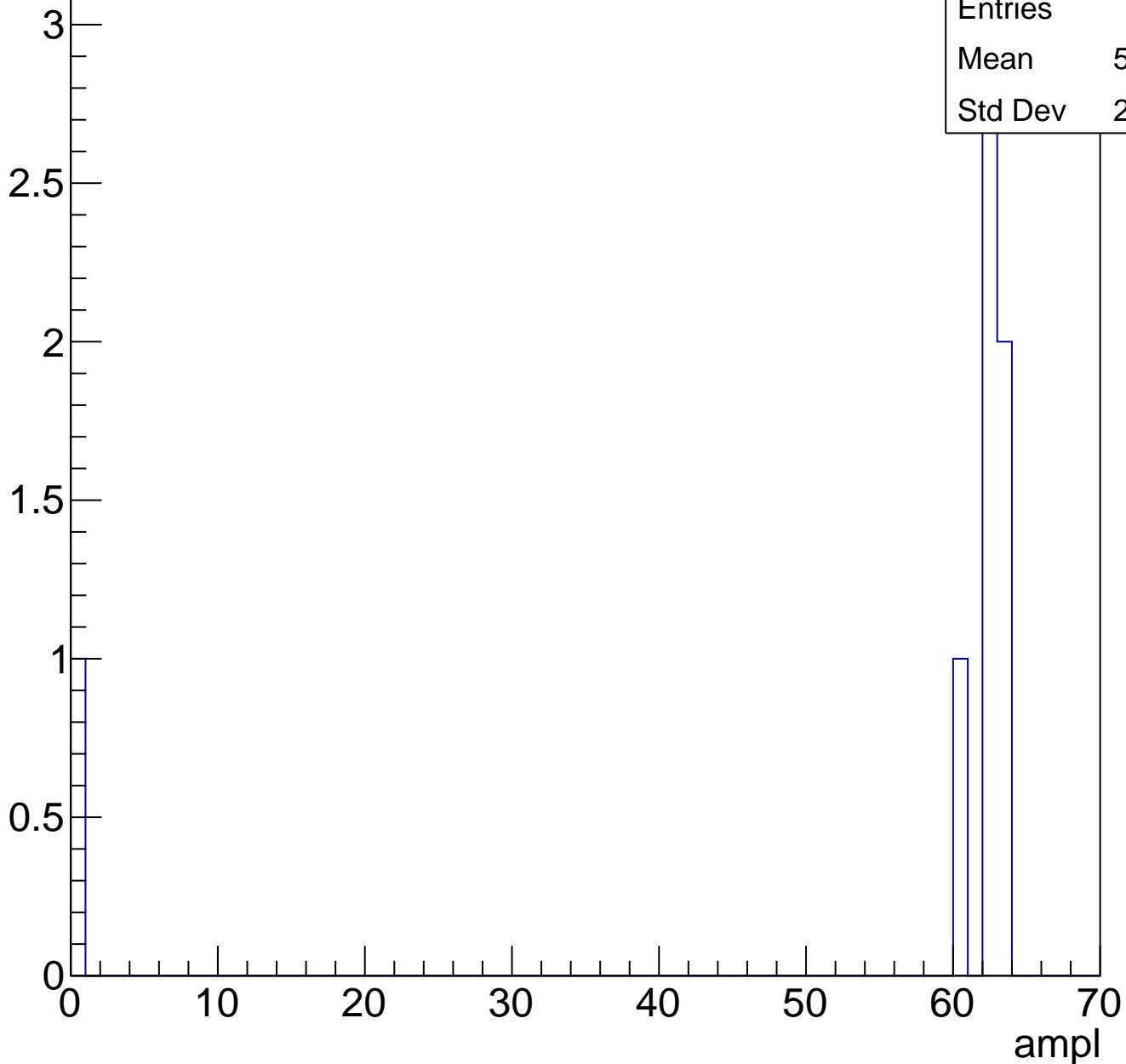
Entry



B1L100S, U15-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch34, adc0

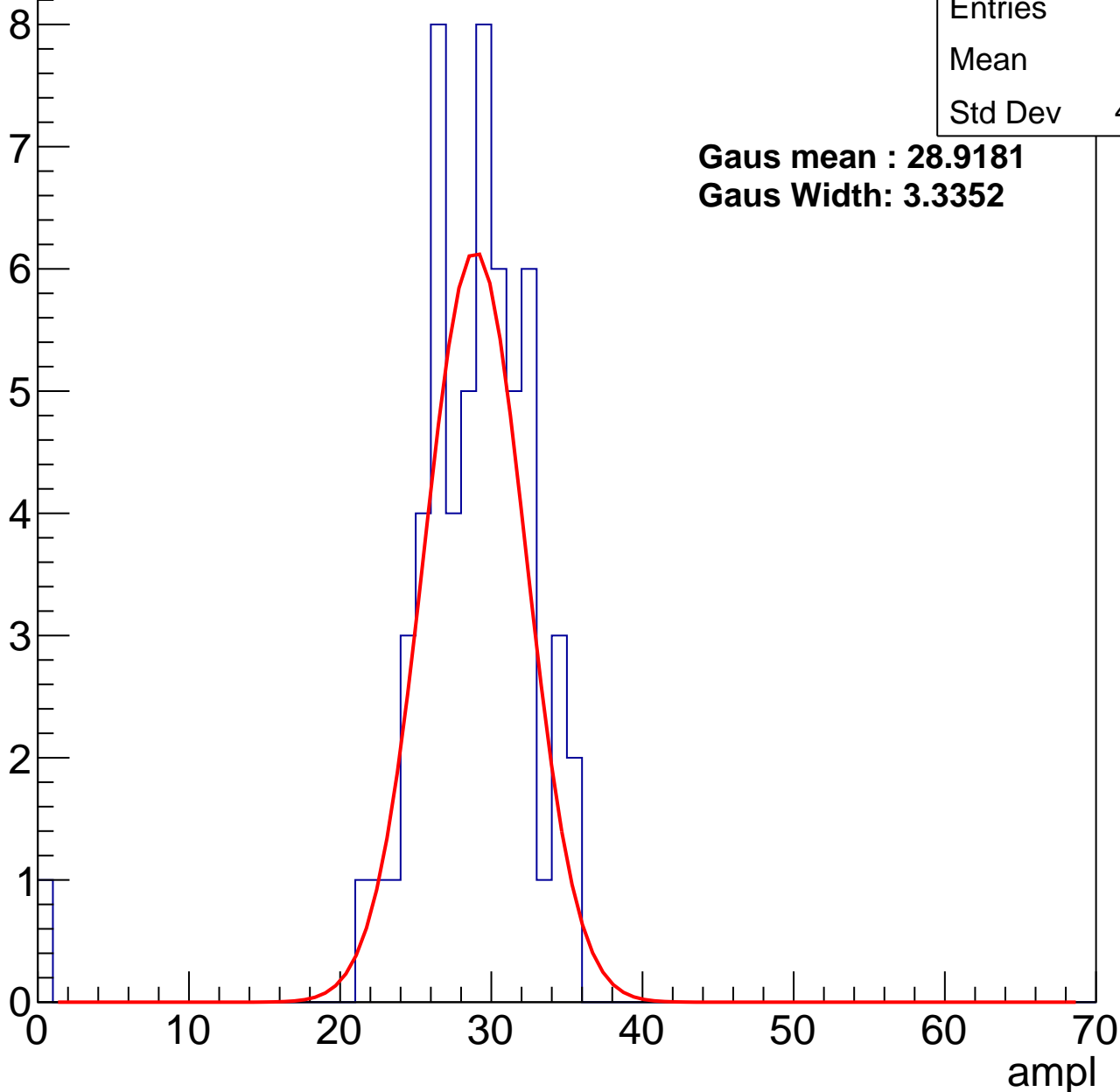
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	28.1
Std Dev	4.901

Gaus mean : 28.9181

Gaus Width: 3.3352



B1L100S, U15-ch34, adc1

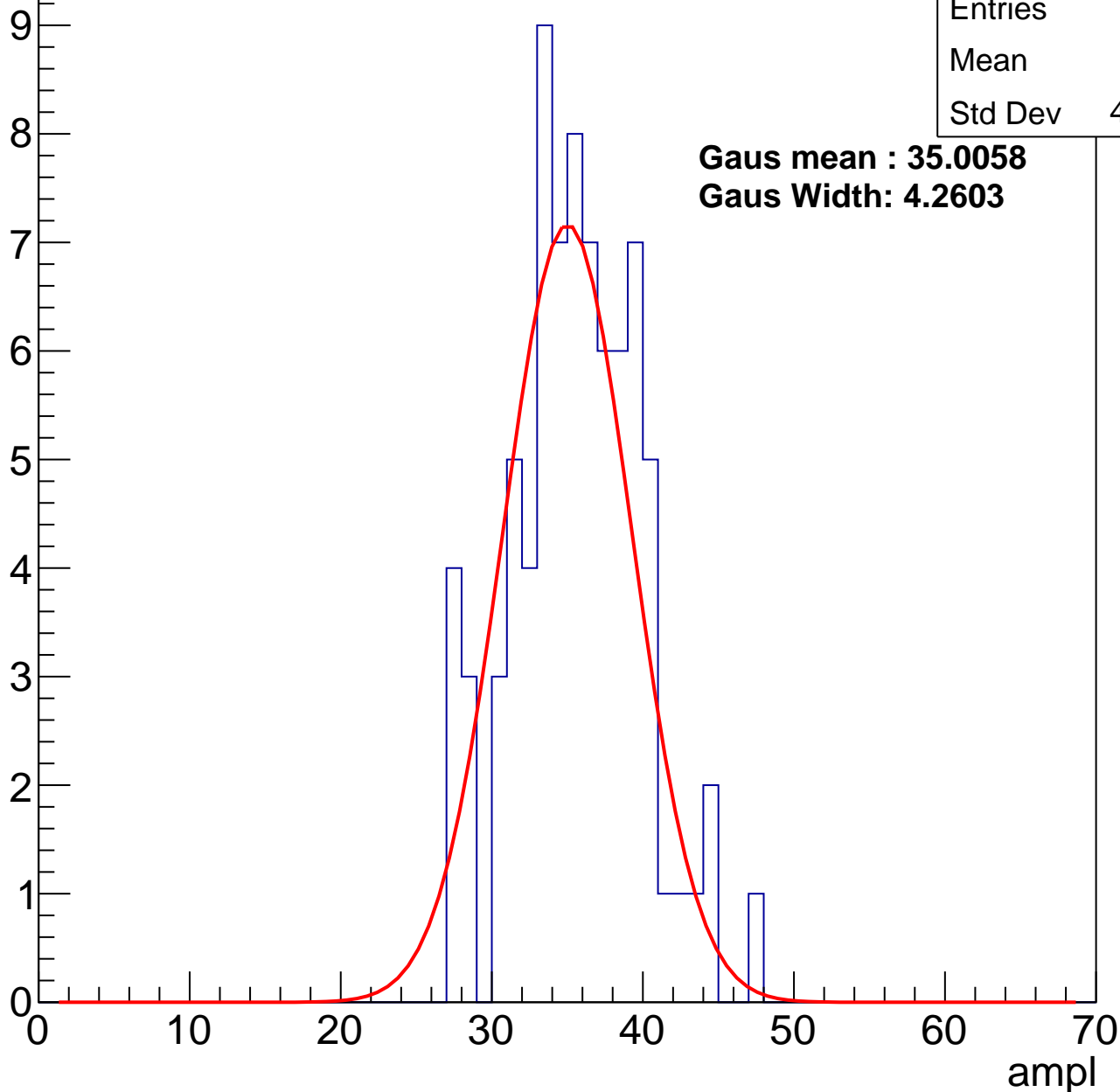
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	35.2
Std Dev	4.199

Gaus mean : 35.0058

Gaus Width: 4.2603



B1L100S, U15-ch34, adc2

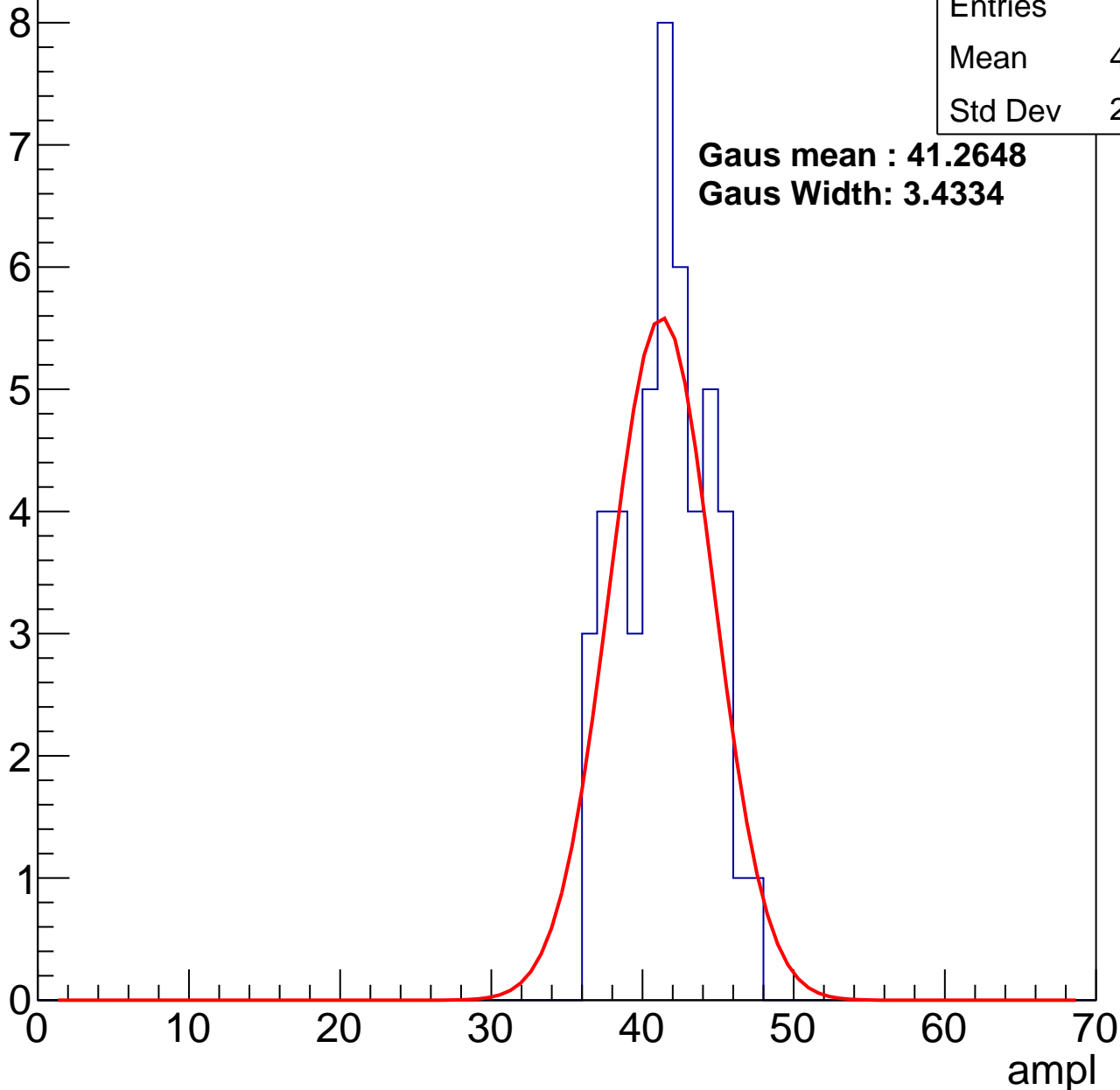
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	41.04
Std Dev	2.828

Gaus mean : 41.2648

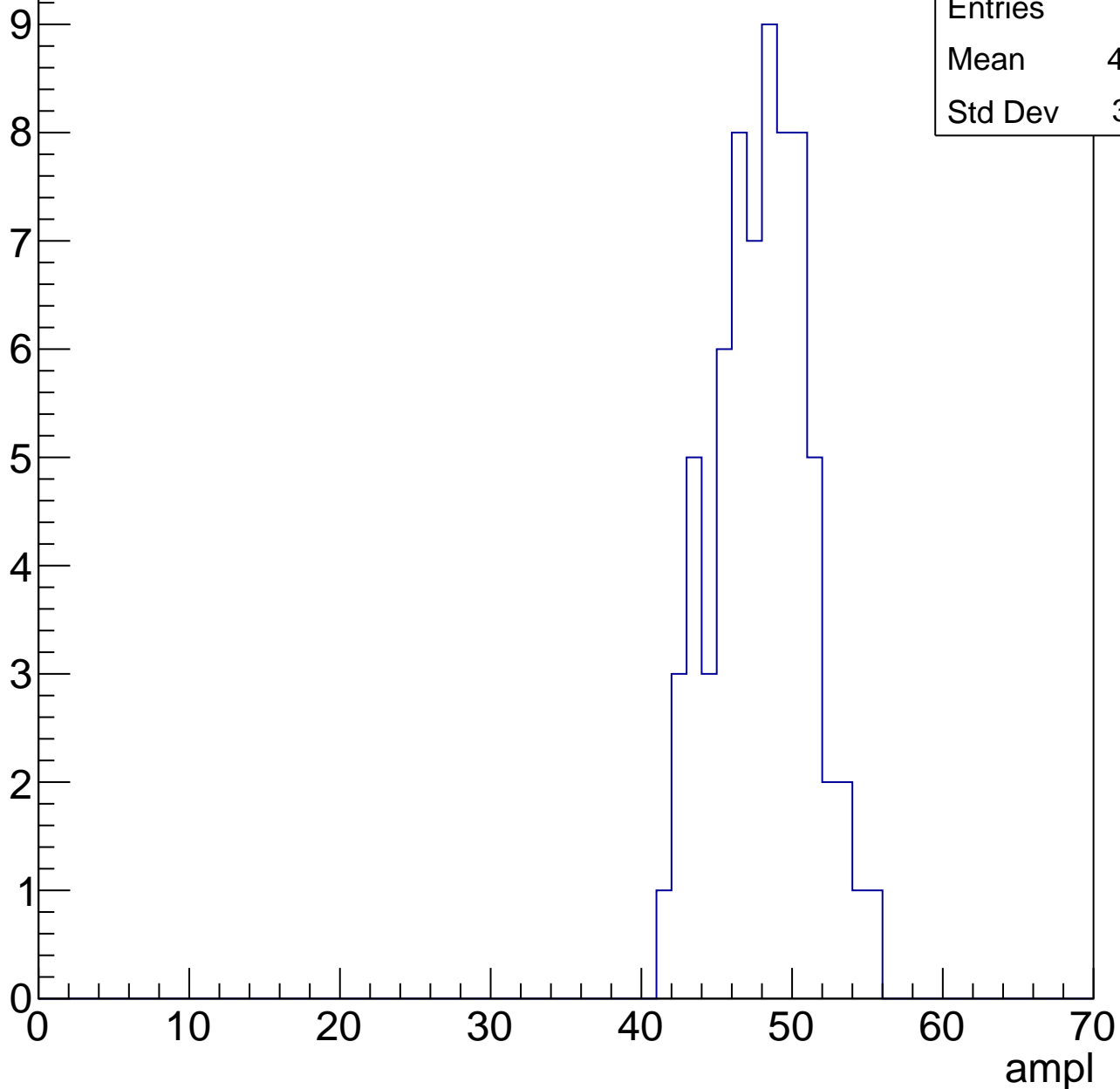
Gaus Width: 3.4334



B1L100S, U15-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



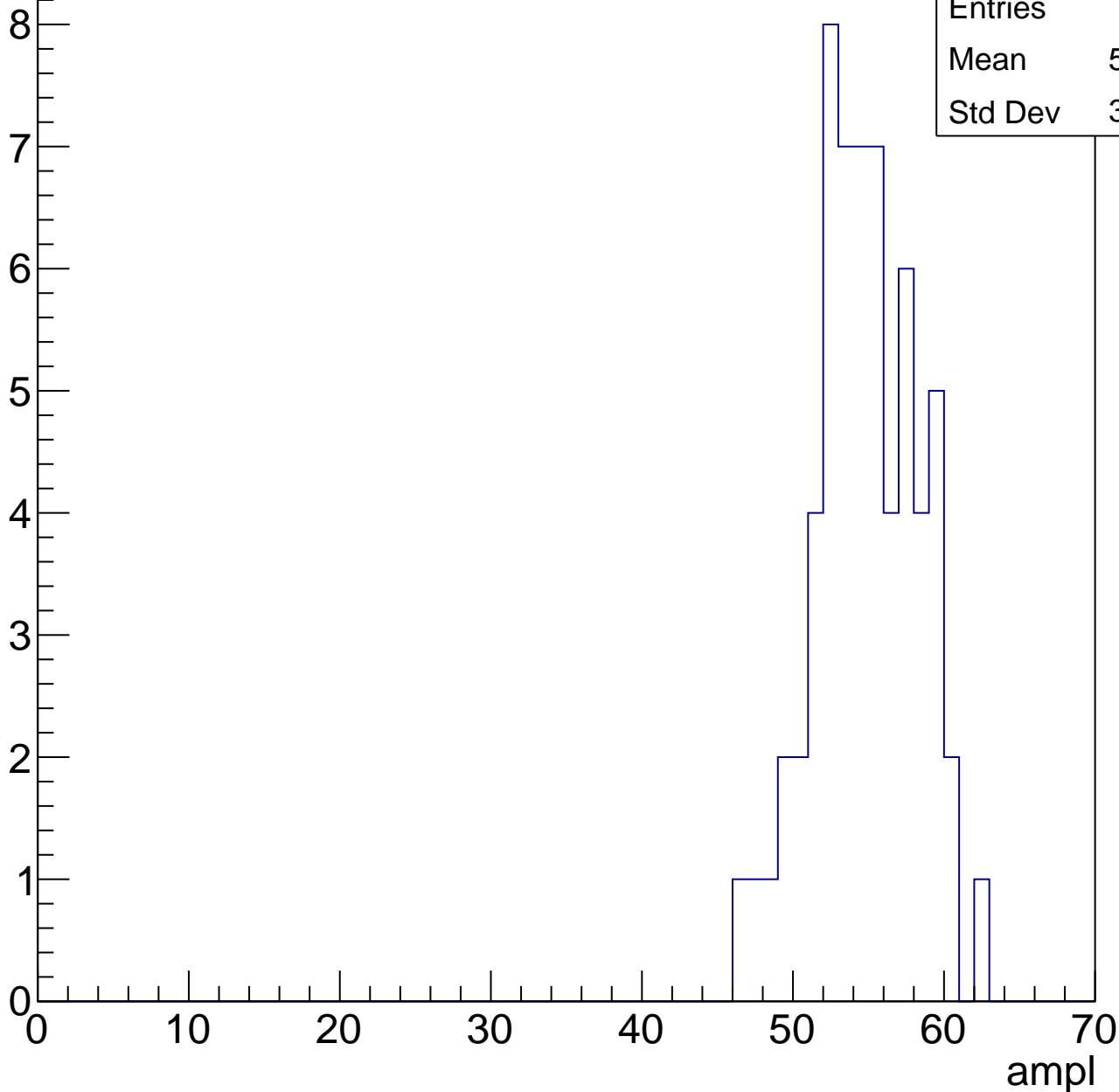
Entries	69
Mean	47.52
Std Dev	3.091

B1L100S, U15-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	54.32
Std Dev	3.364



B1L100S, U15-ch34, adc5

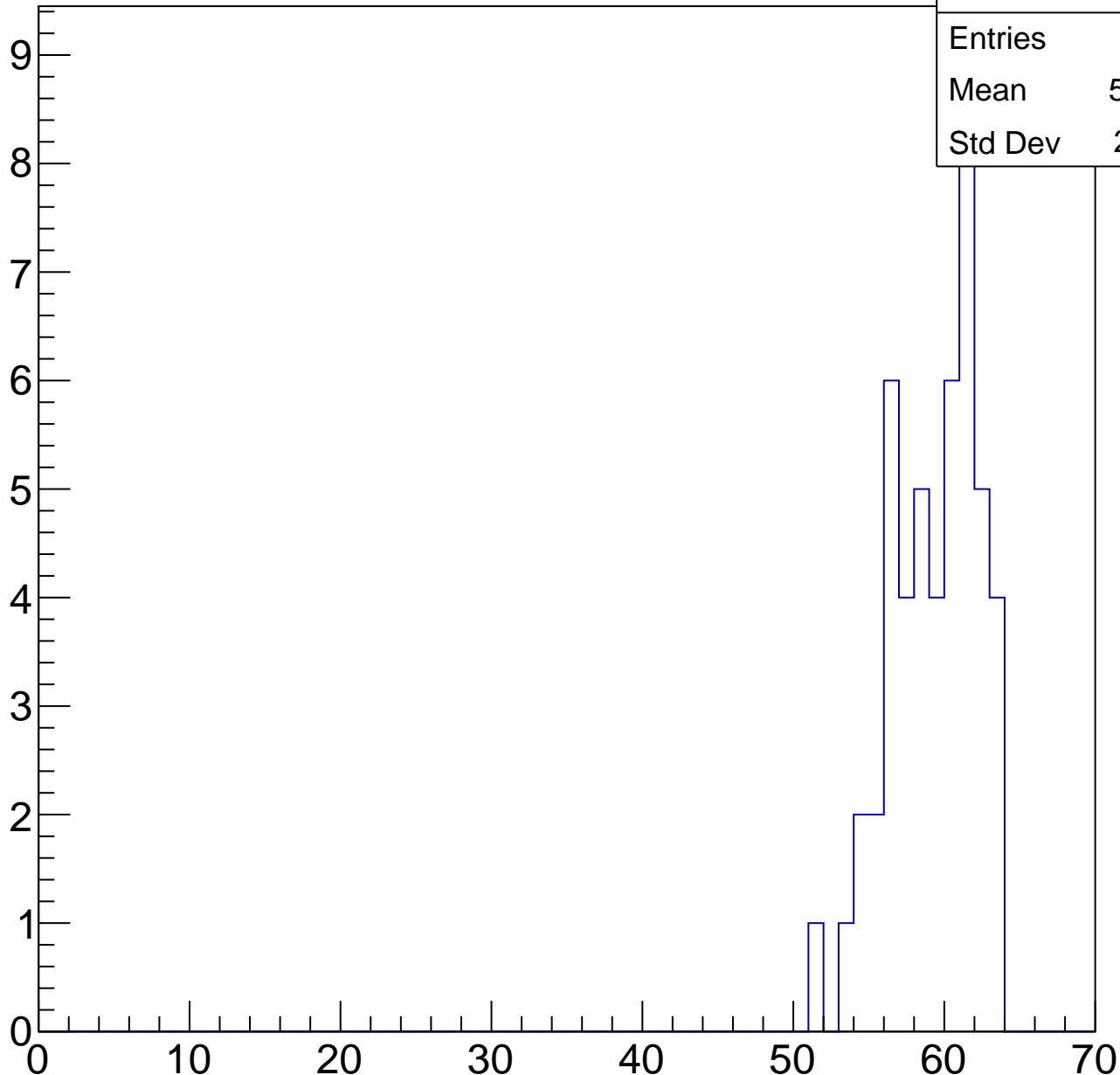
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.84
Std Dev	2.881

ampl

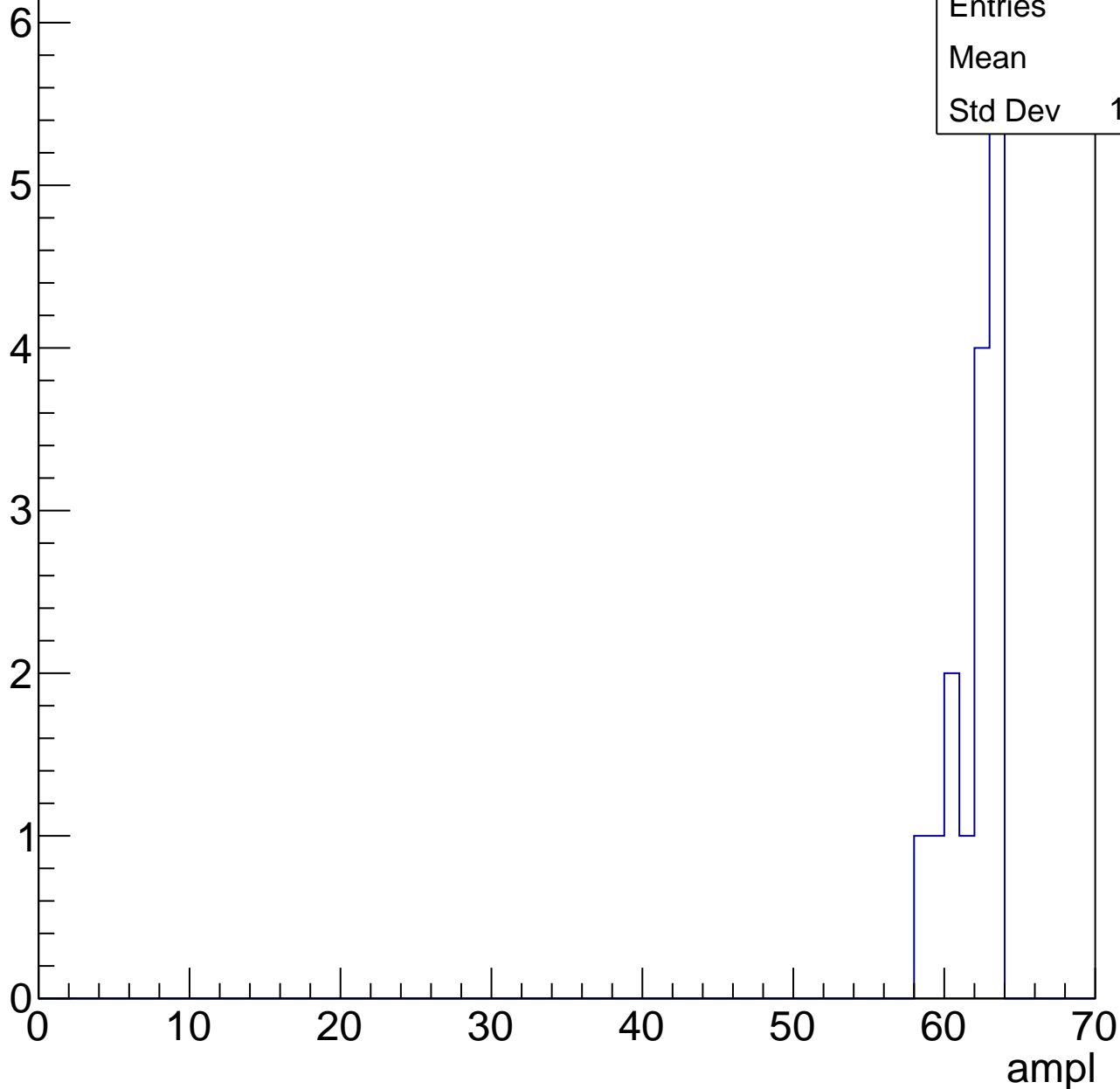


B1L100S, U15-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	61.6
Std Dev	1.583



B1L100S, U15-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch35, adc0

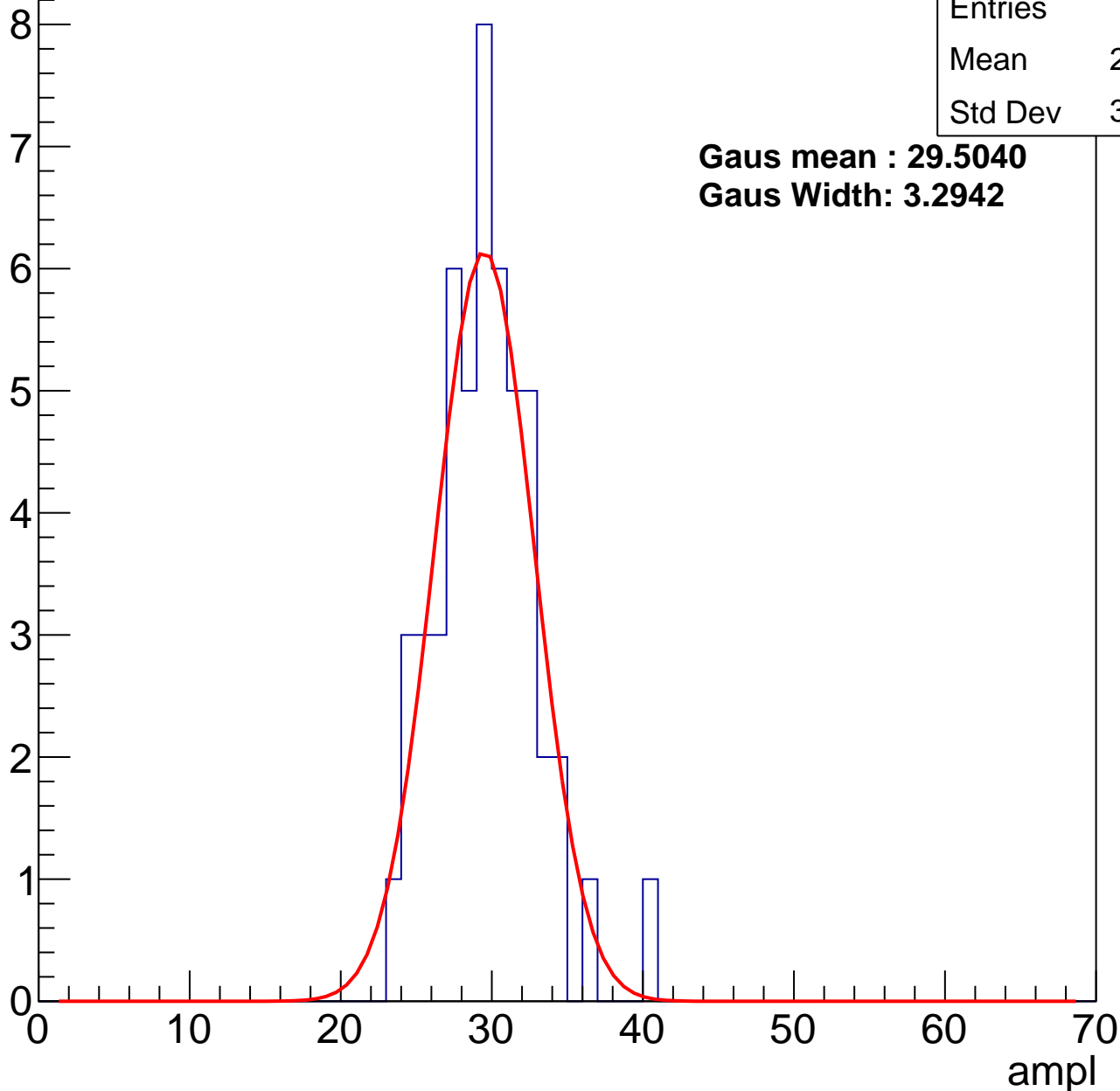
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	29.16
Std Dev	3.244

Gaus mean : 29.5040

Gaus Width: 3.2942



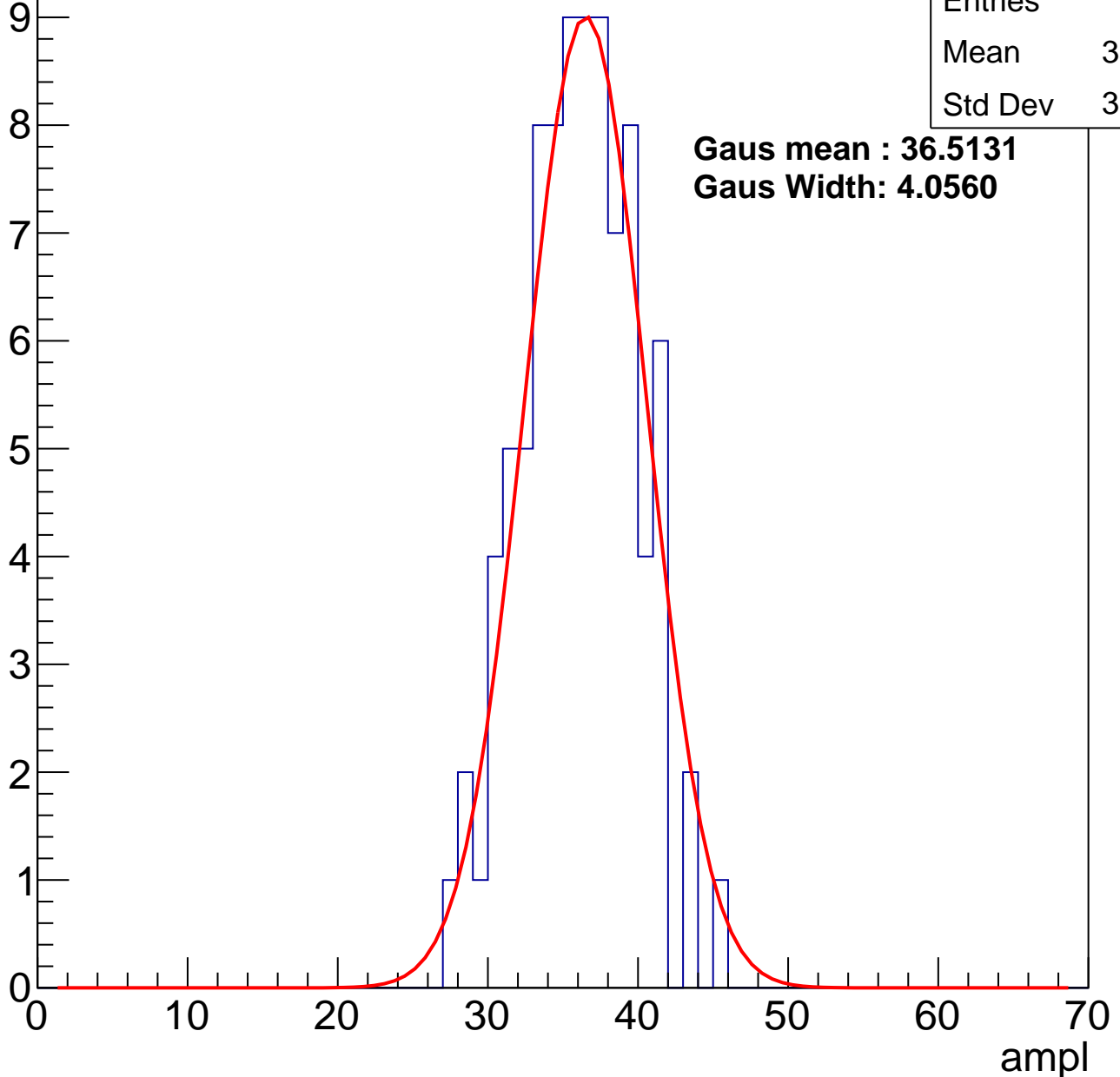
B1L100S, U15-ch35, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	35.62
Std Dev	3.686

Gaus mean : 36.5131
Gaus Width: 4.0560



B1L100S, U15-ch35, adc2

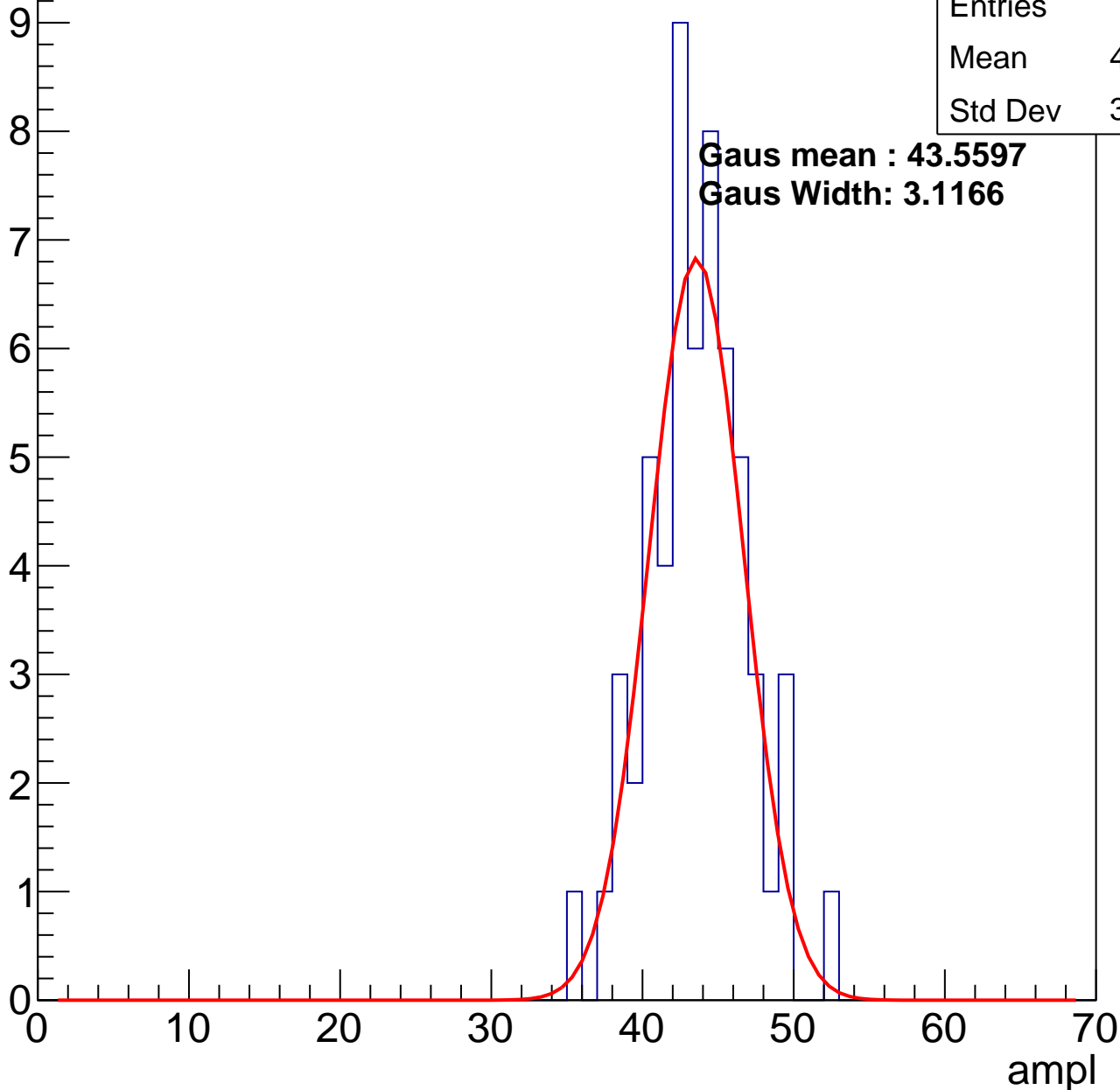
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.17
Std Dev	3.254

Gaus mean : 43.5597

Gaus Width: 3.1166

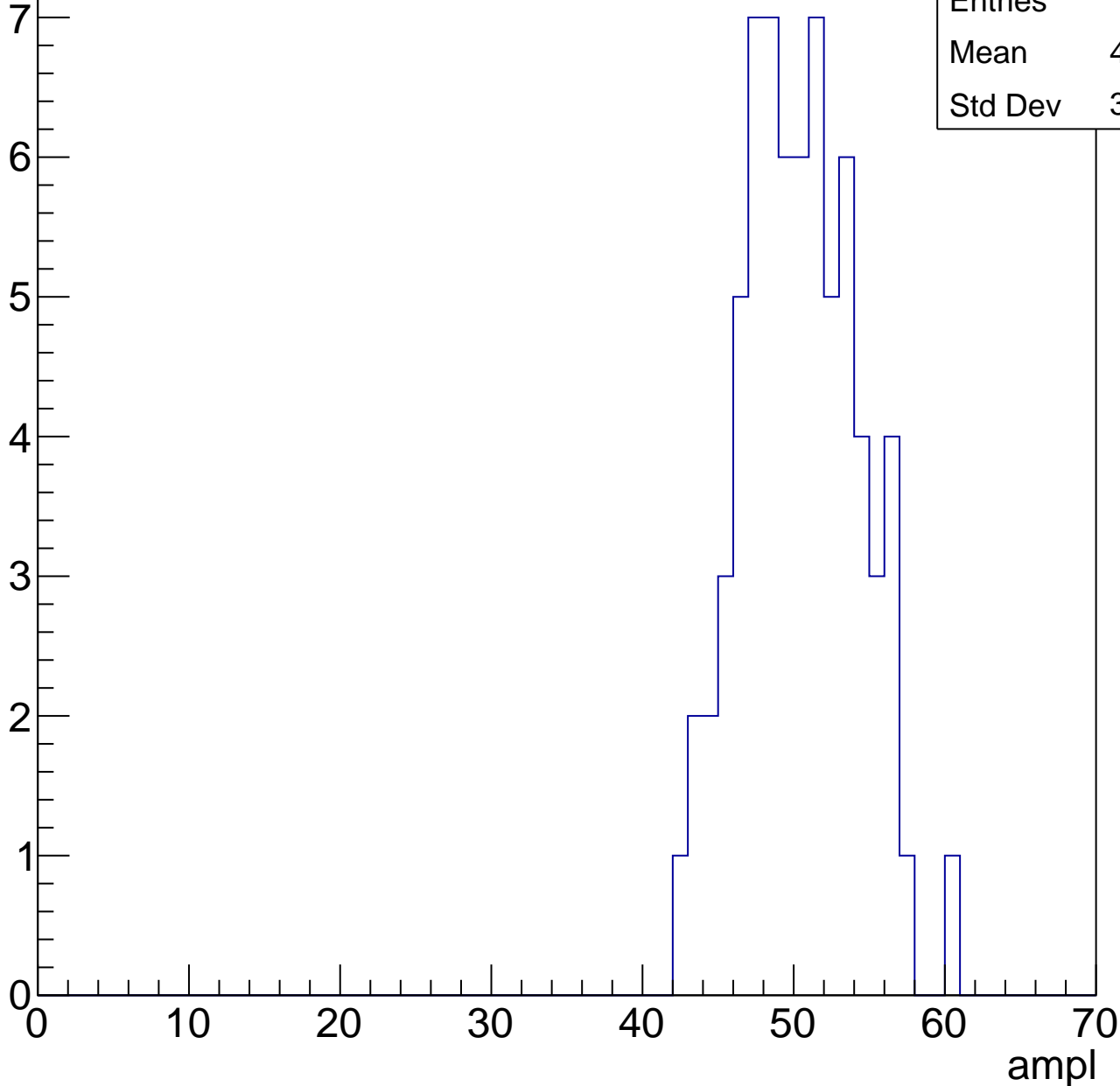


B1L100S, U15-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

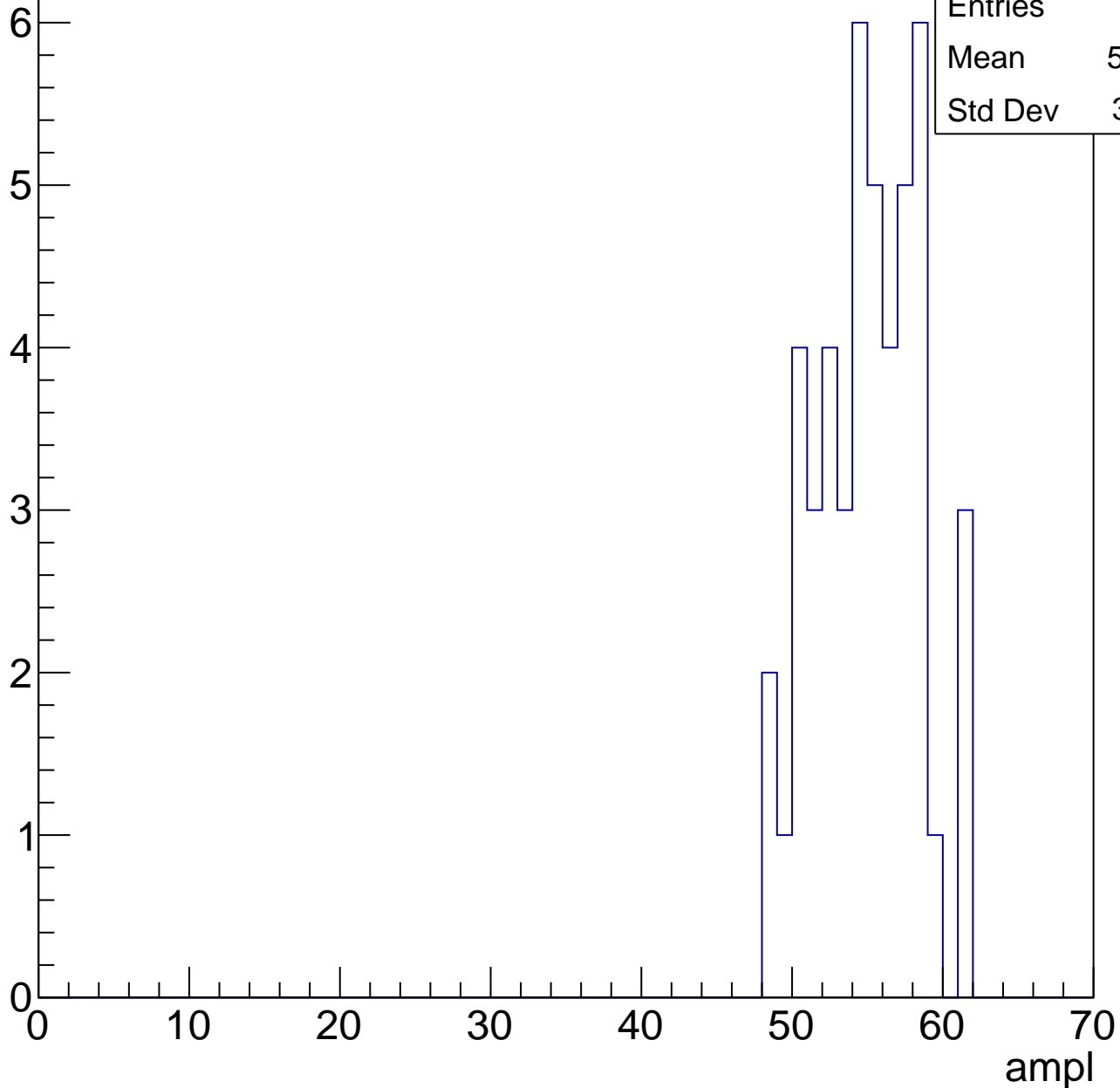
Entries	70
Mean	49.96
Std Dev	3.778



B1L100S, U15-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

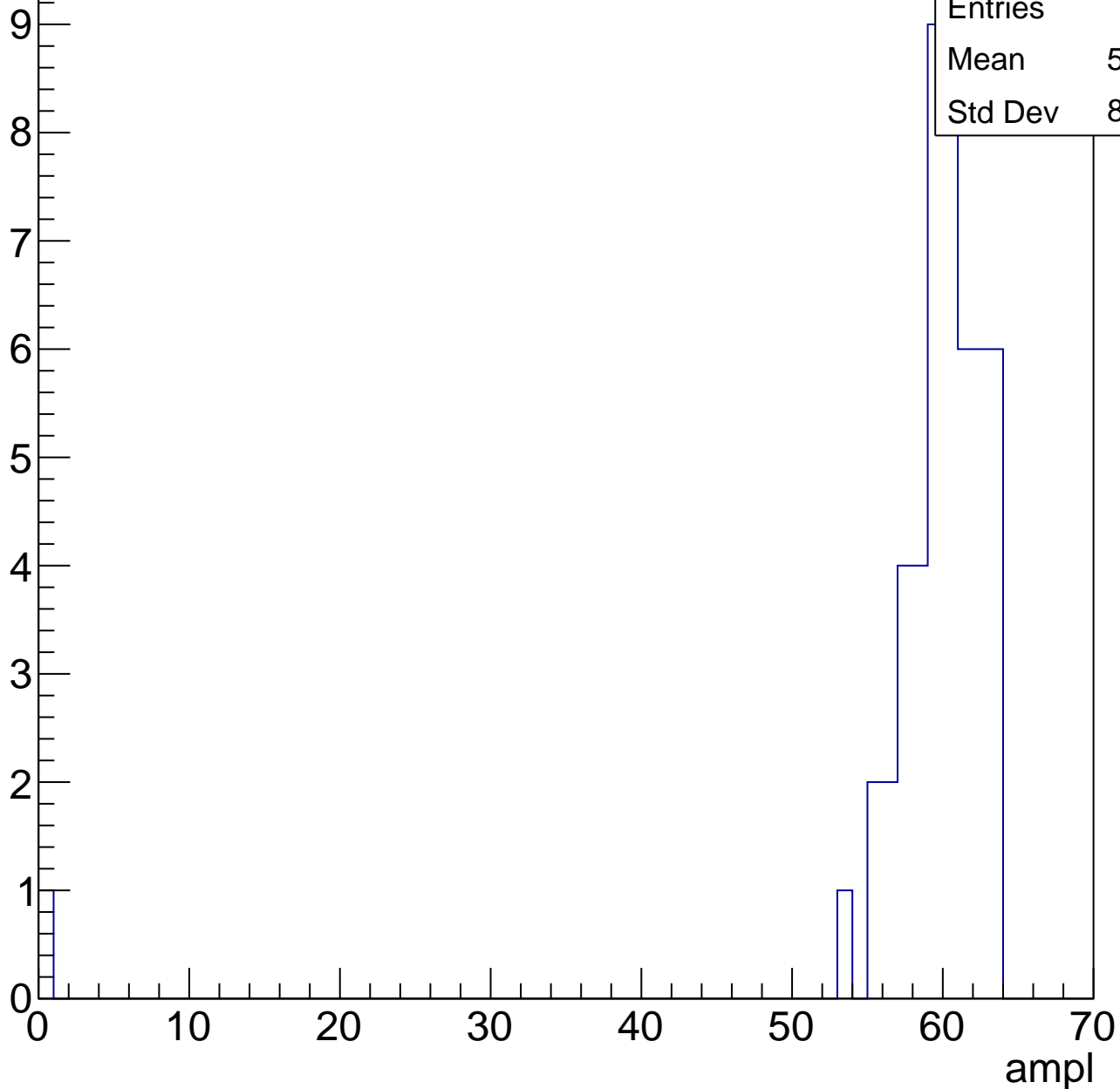


Entries	47
Mean	54.53
Std Dev	3.351

B1L100S, U15-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

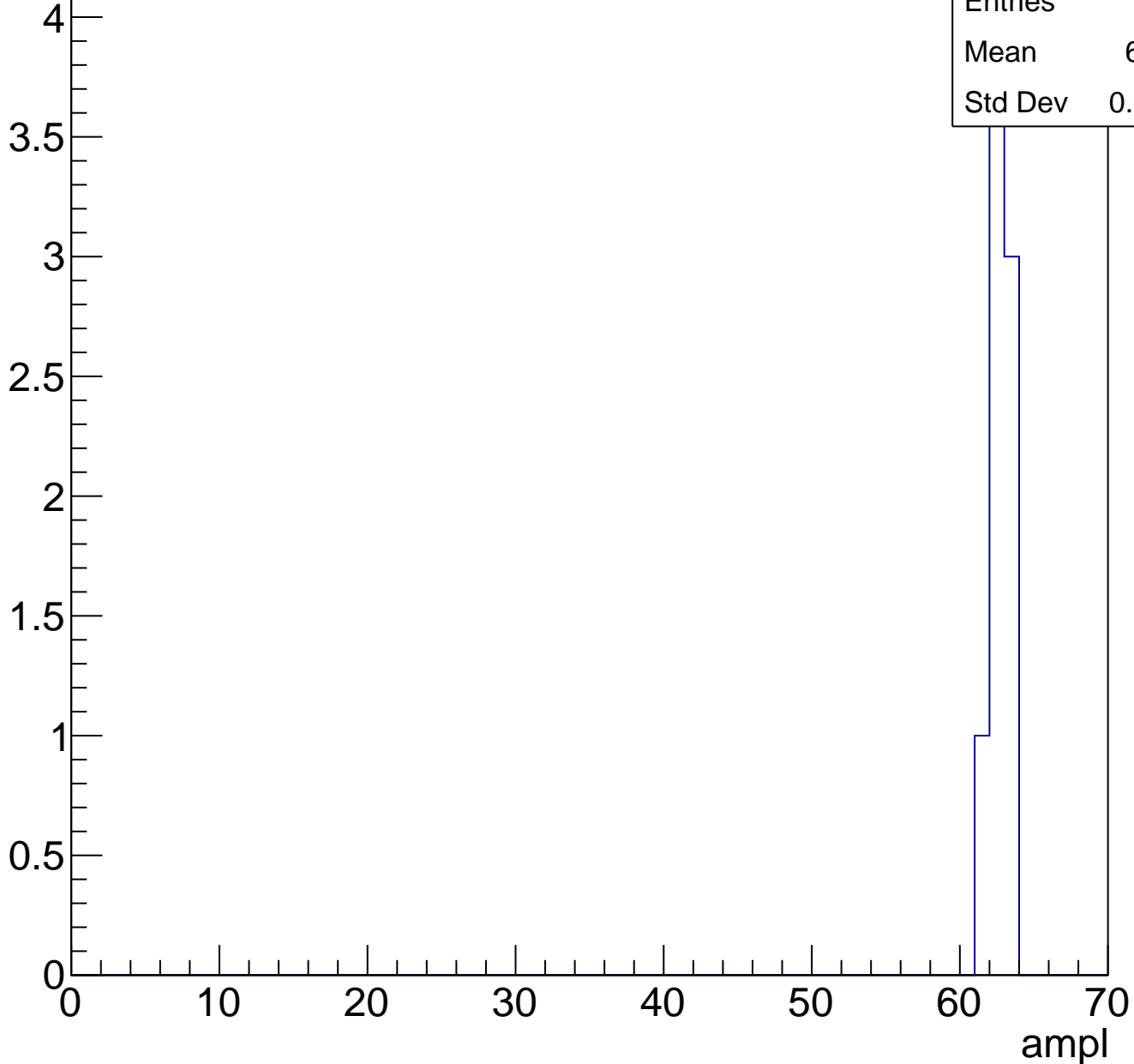
Entry



B1L100S, U15-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch36, adc0

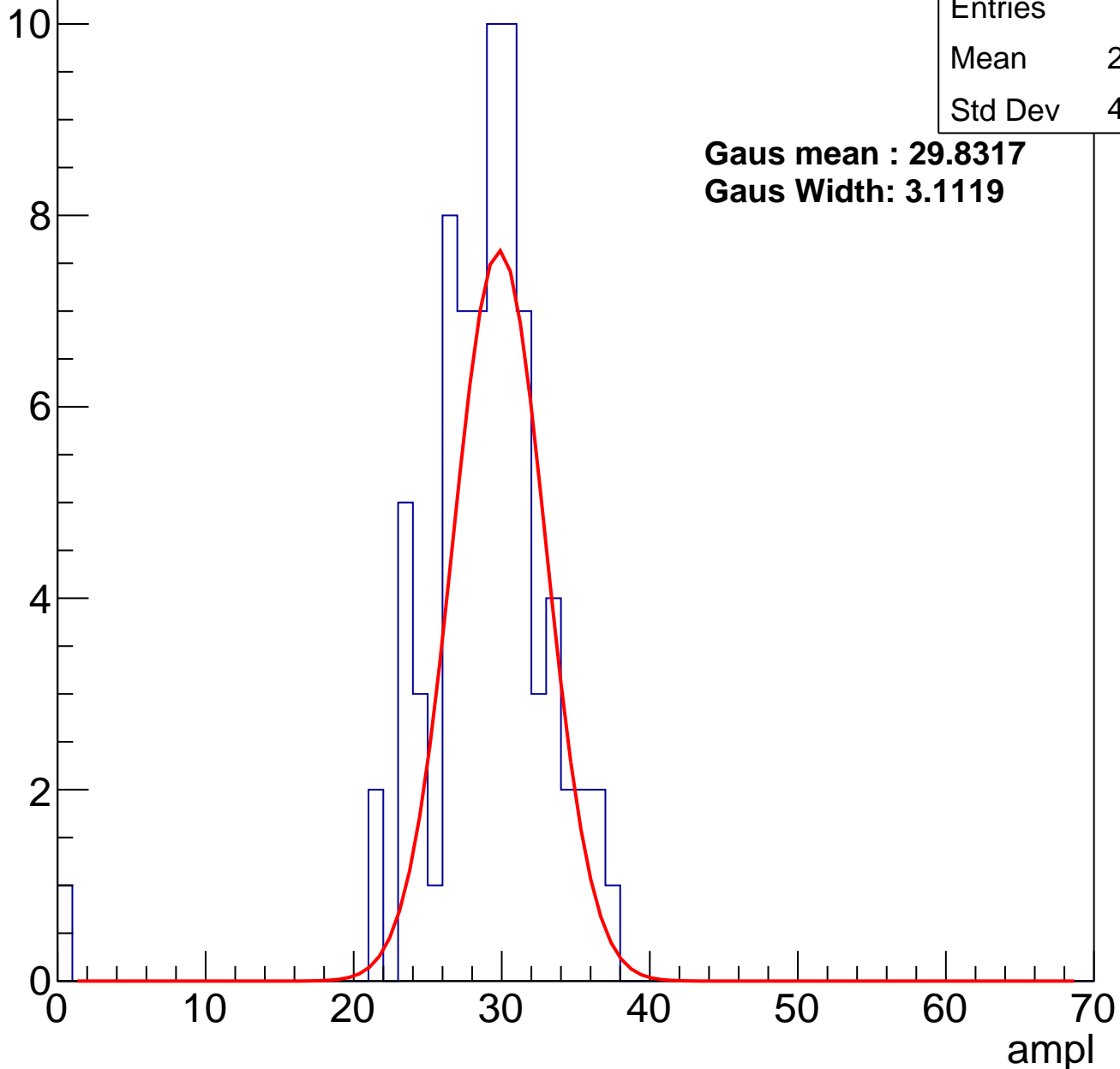
calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	28.39
Std Dev	4.805

Gaus mean : 29.8317

Gaus Width: 3.1119

Entry



B1L100S, U15-ch36, adc1

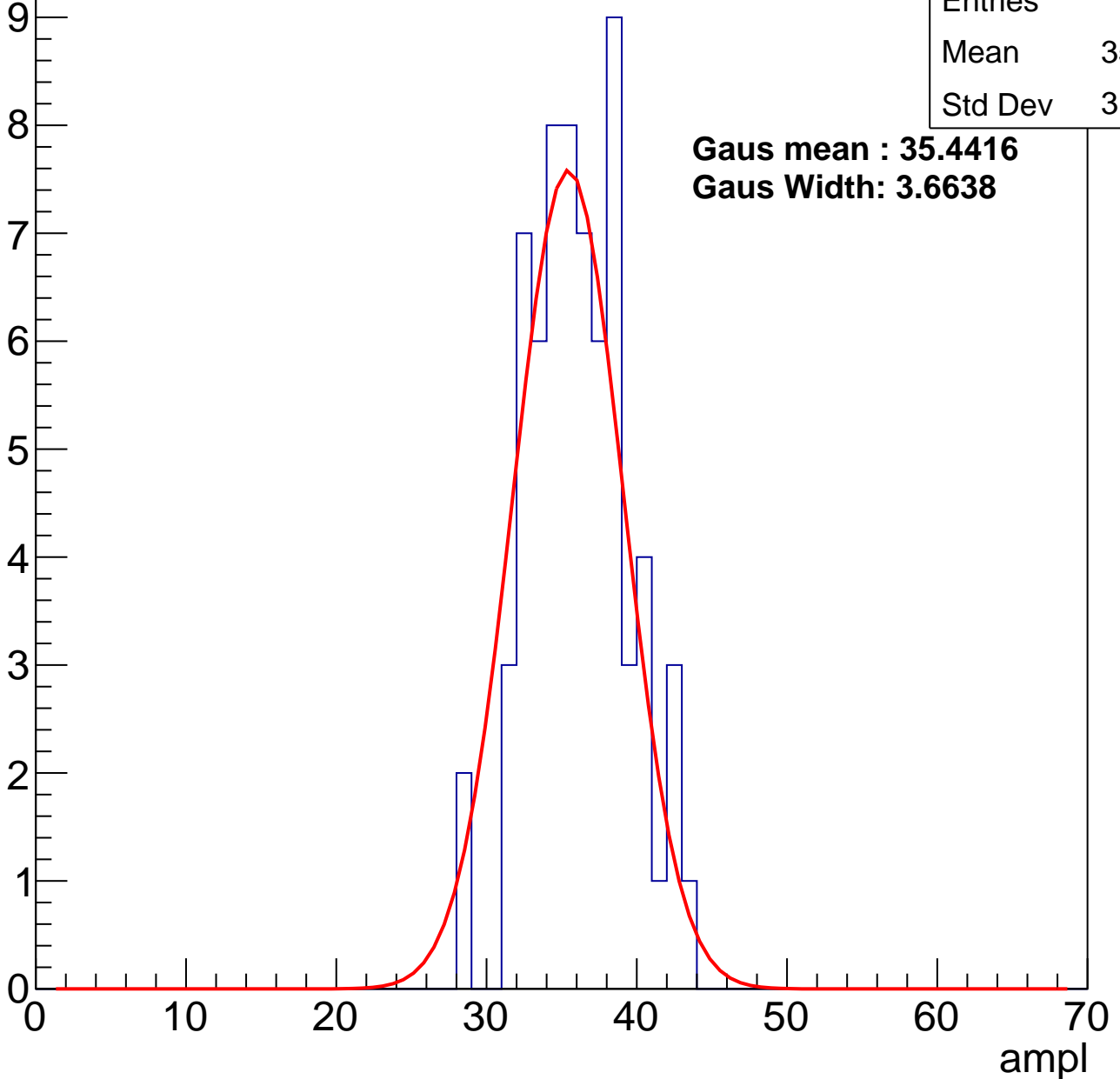
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.68
Std Dev	3.247

Gaus mean : 35.4416

Gaus Width: 3.6638



B1L100S, U15-ch36, adc2

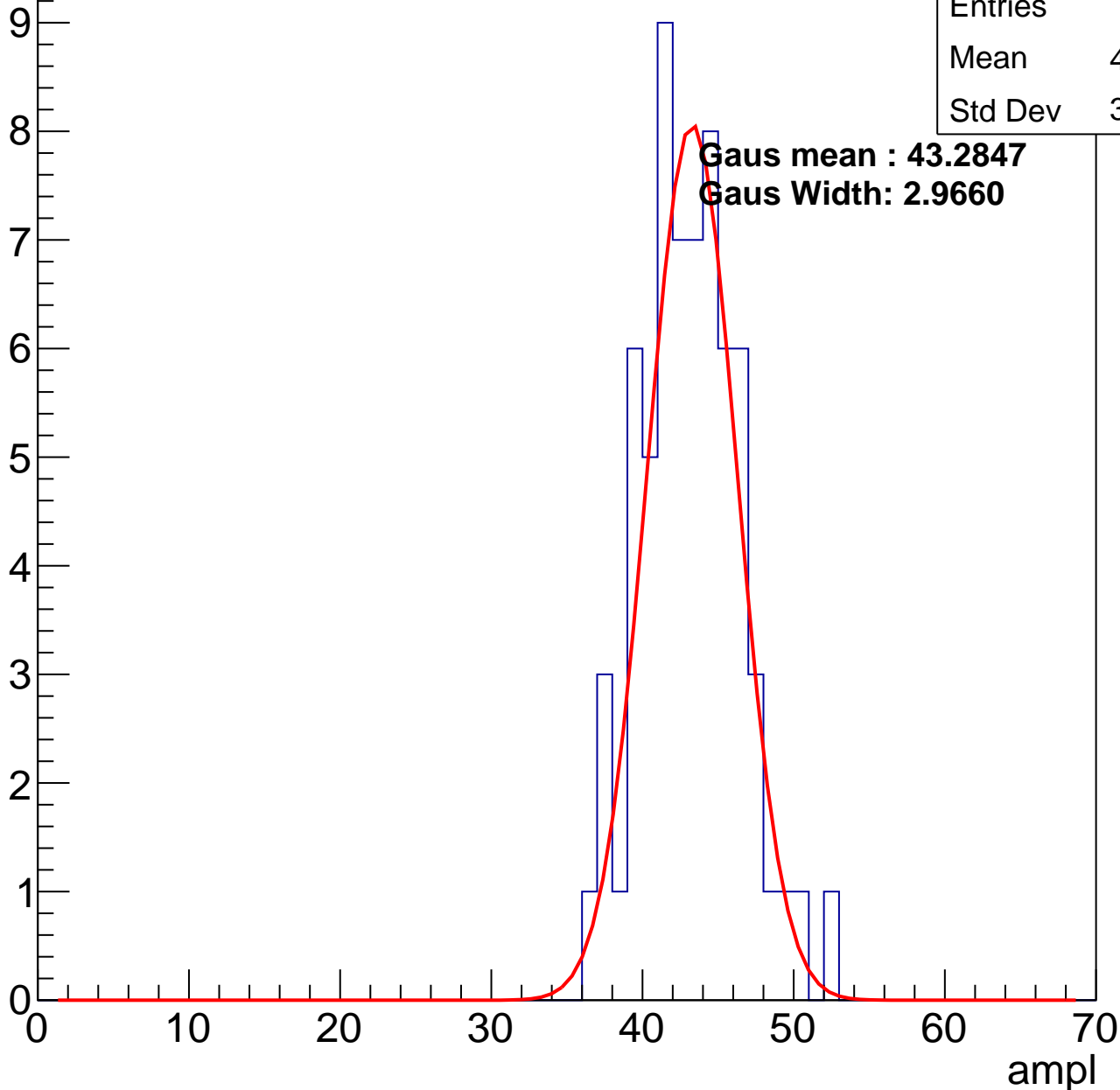
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.74
Std Dev	3.225

Gaus mean : 43.2847

Gaus Width: 2.9660

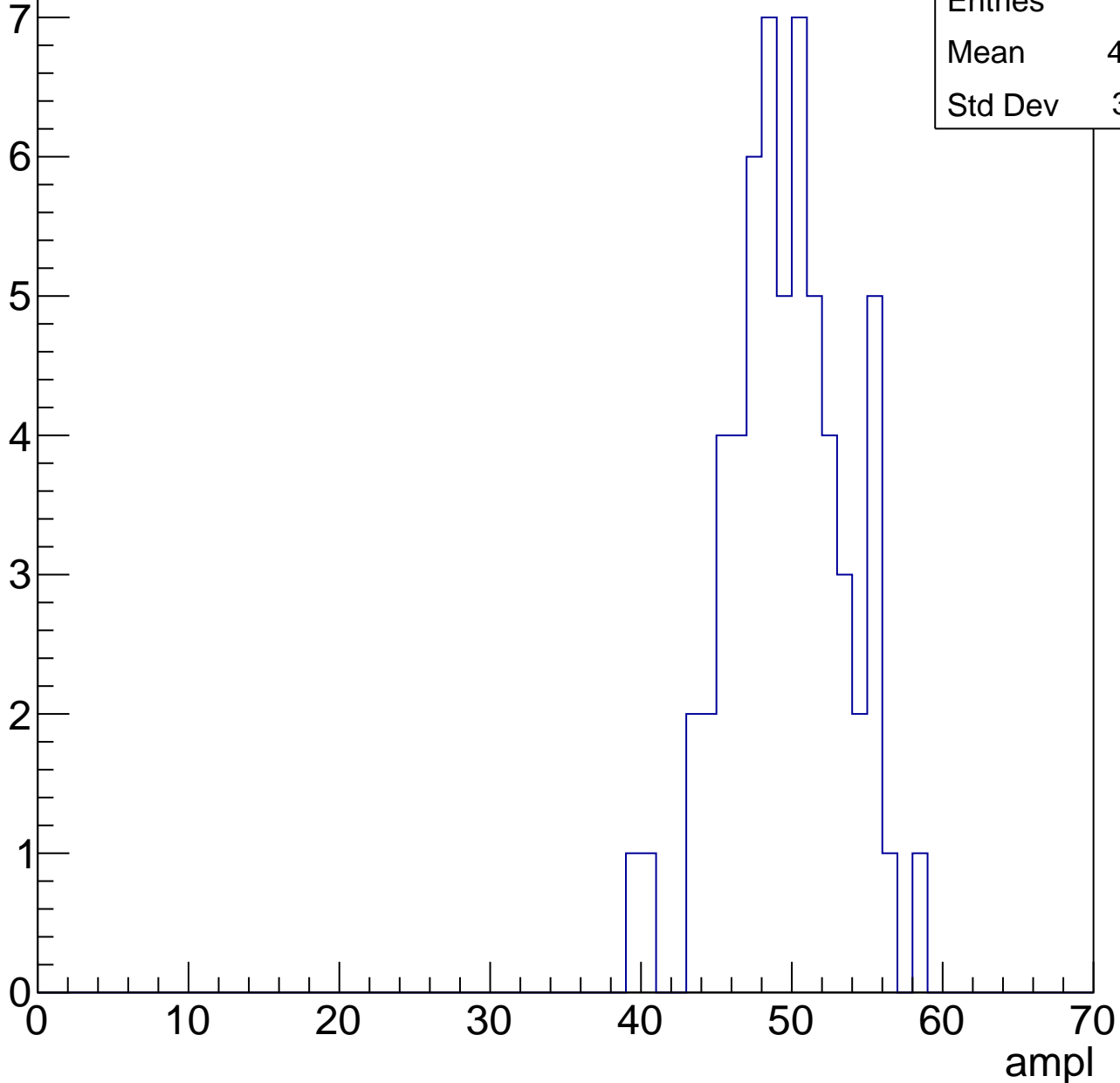


B1L100S, U15-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	49.15
Std Dev	3.881

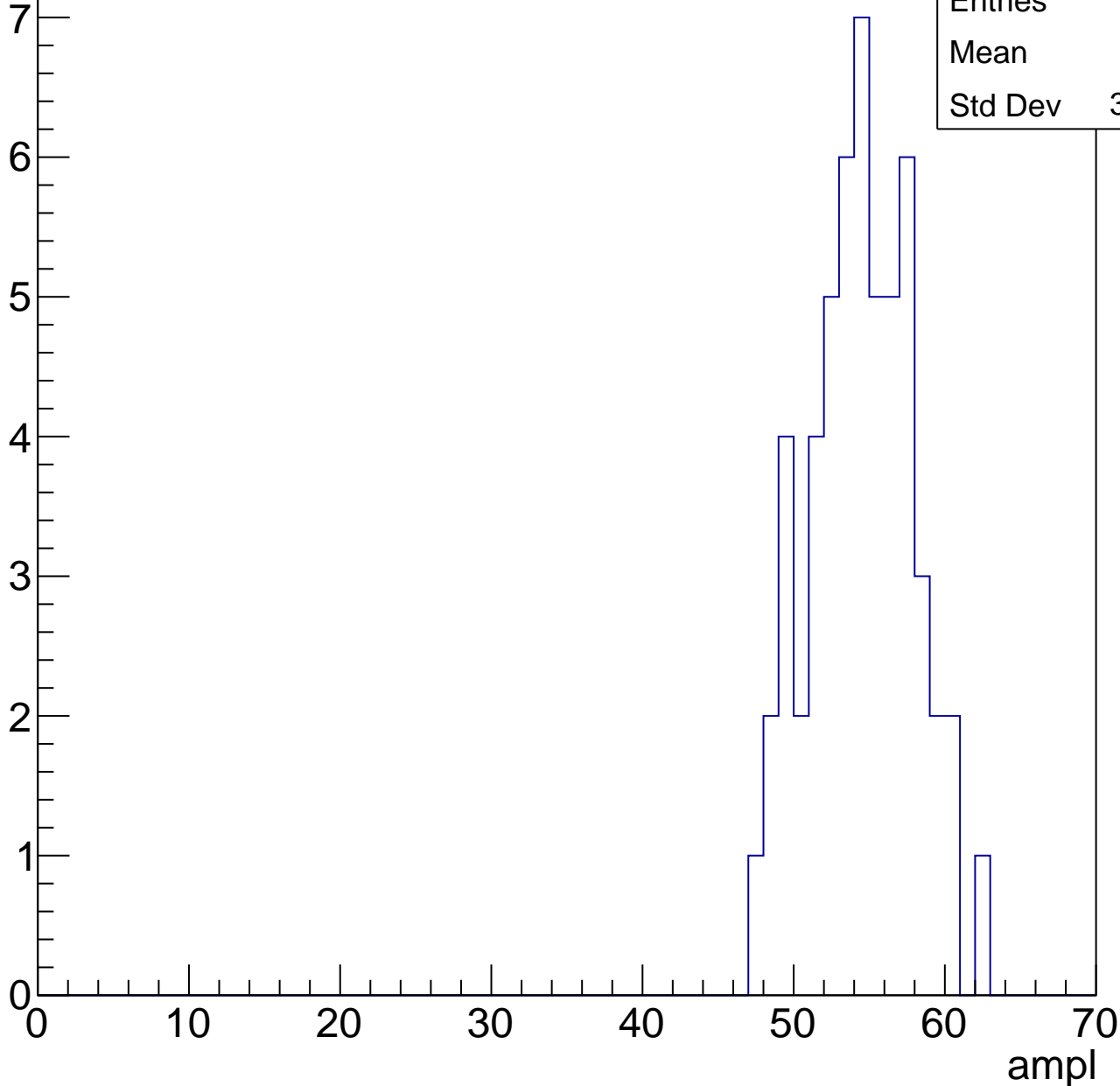


B1L100S, U15-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	54
Std Dev	3.379



B1L100S, U15-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 50

Mean 58.14

Std Dev 8.727

ampl

0

10

20

30

40

50

60

70

0

10

20

30

40

50

60

70

0

10

20

30

40

50

60

70

0

10

20

30

40

50

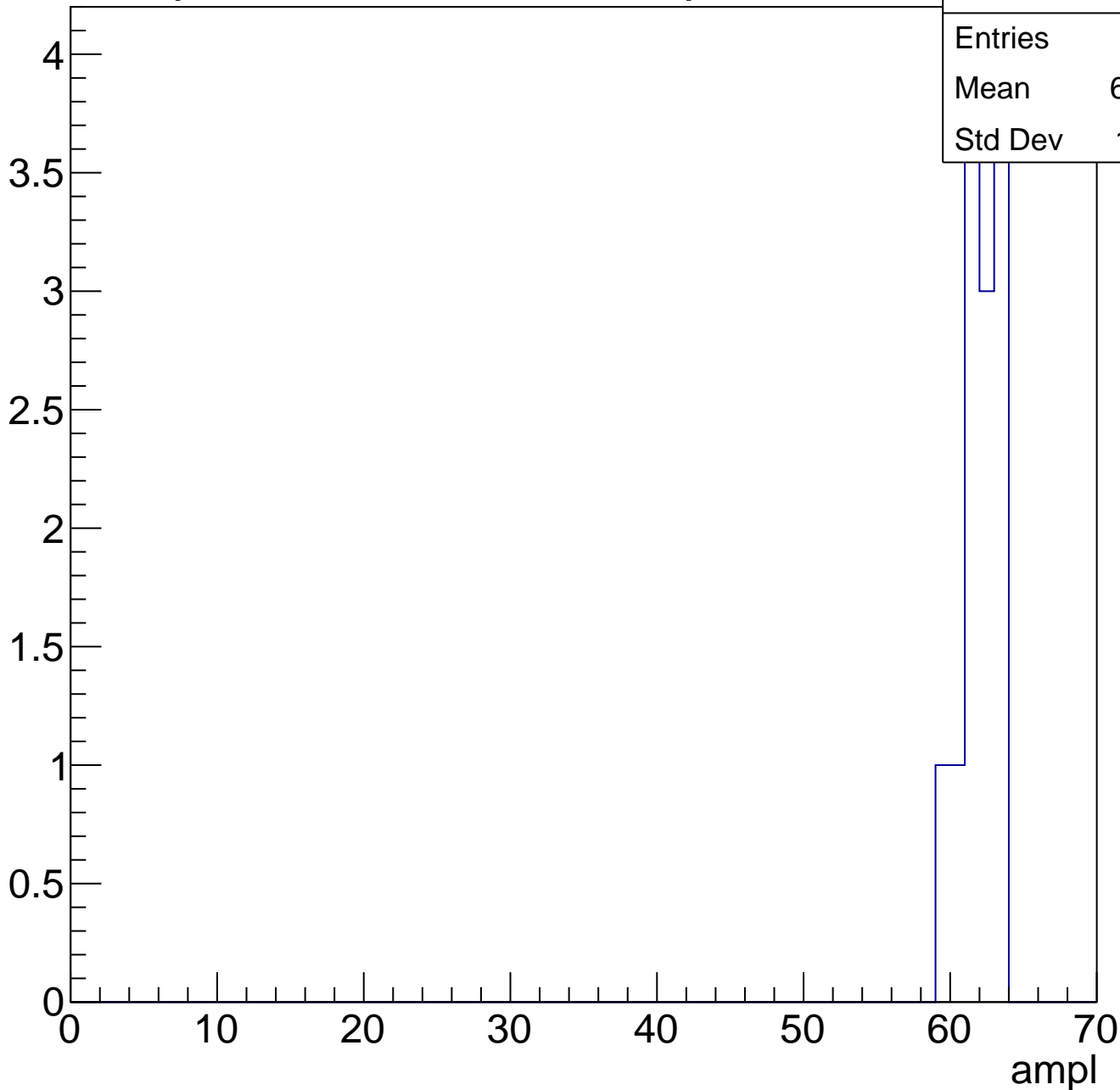
60

70

B1L100S, U15-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch37, adc0

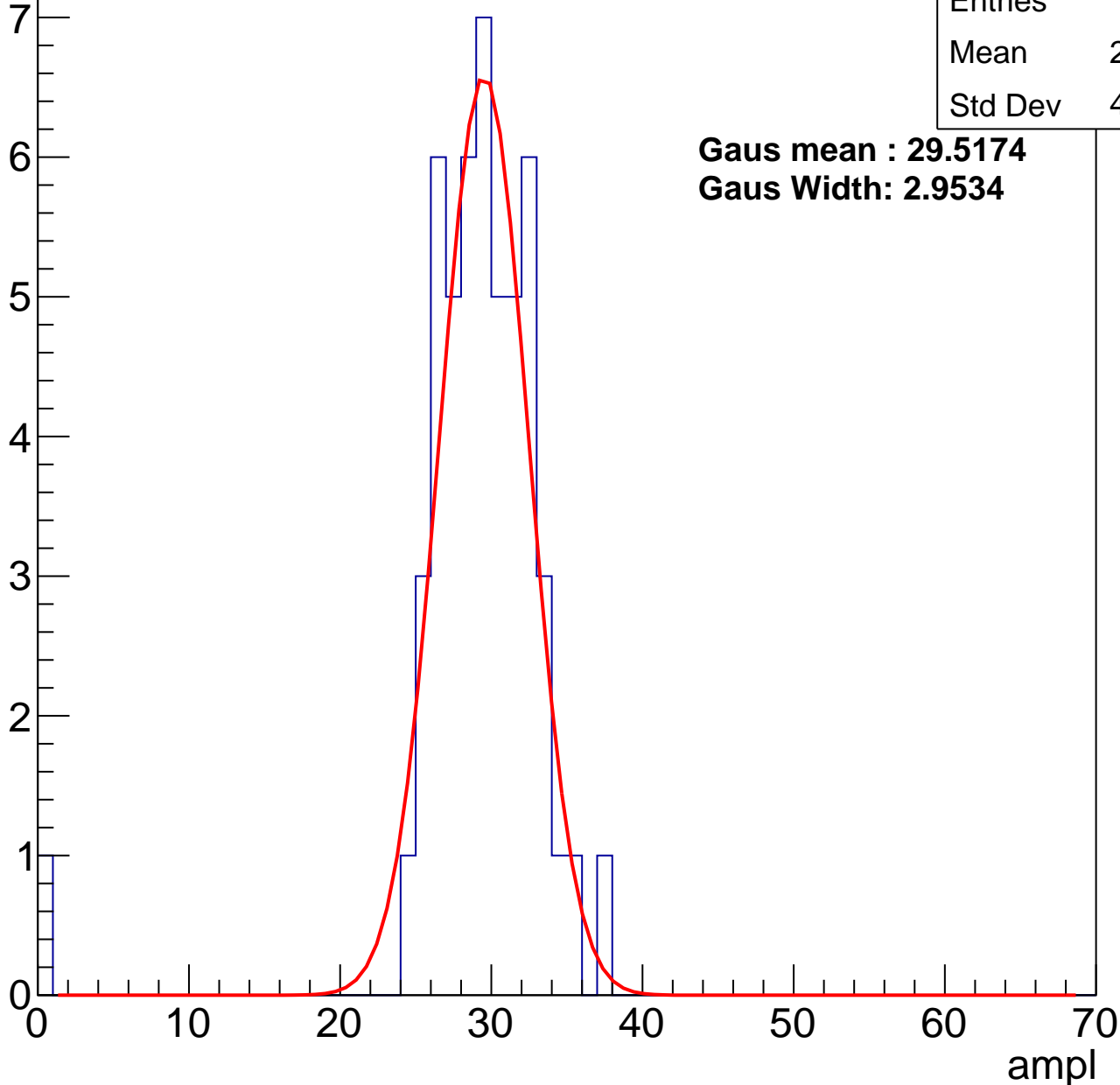
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	28.69
Std Dev	4.933

Gaus mean : 29.5174

Gaus Width: 2.9534



B1L100S, U15-ch37, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	36.44
Std Dev	3.778

Gaus mean : 36.4218

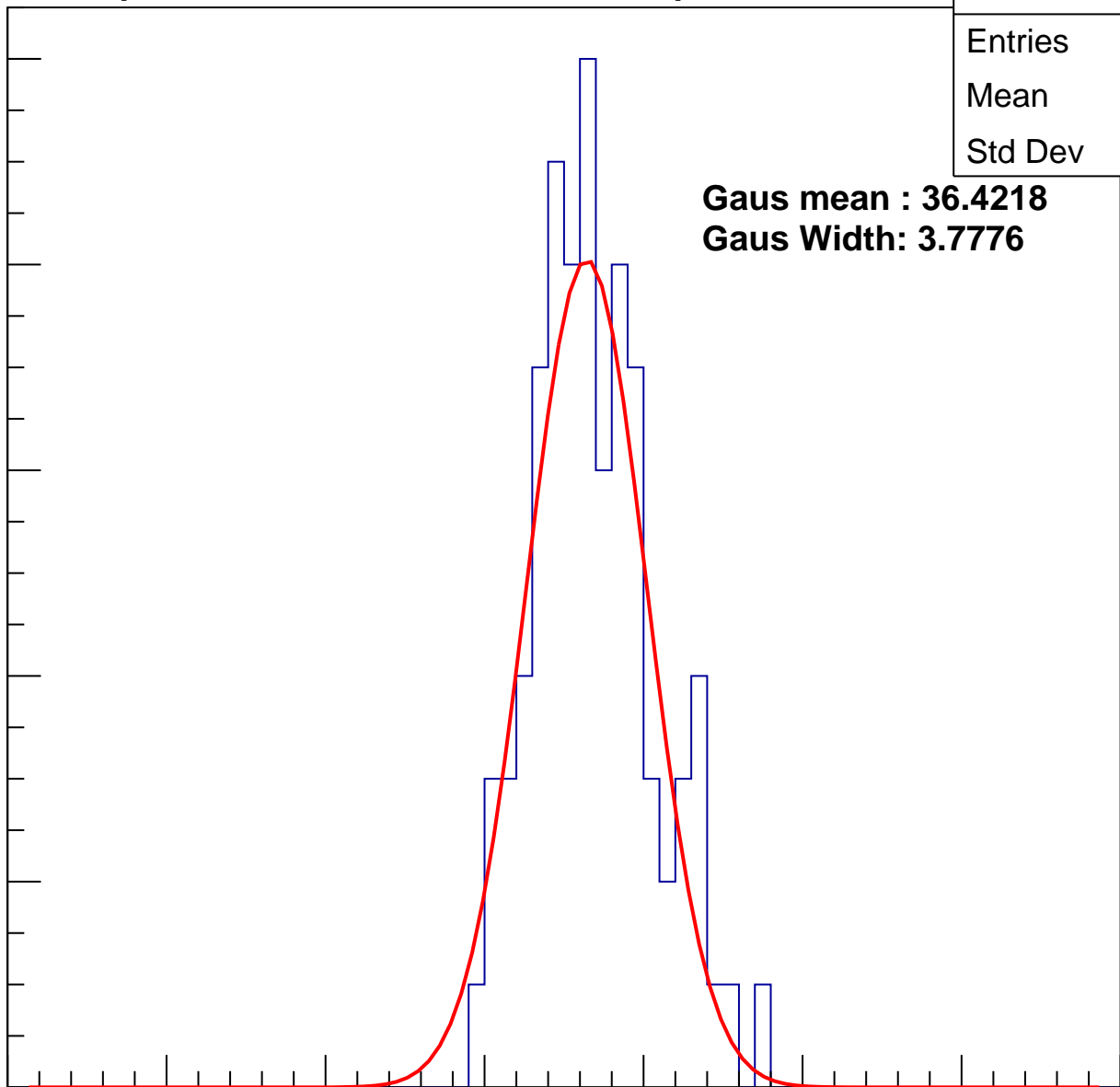
Gaus Width: 3.7776

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U15-ch37, adc2

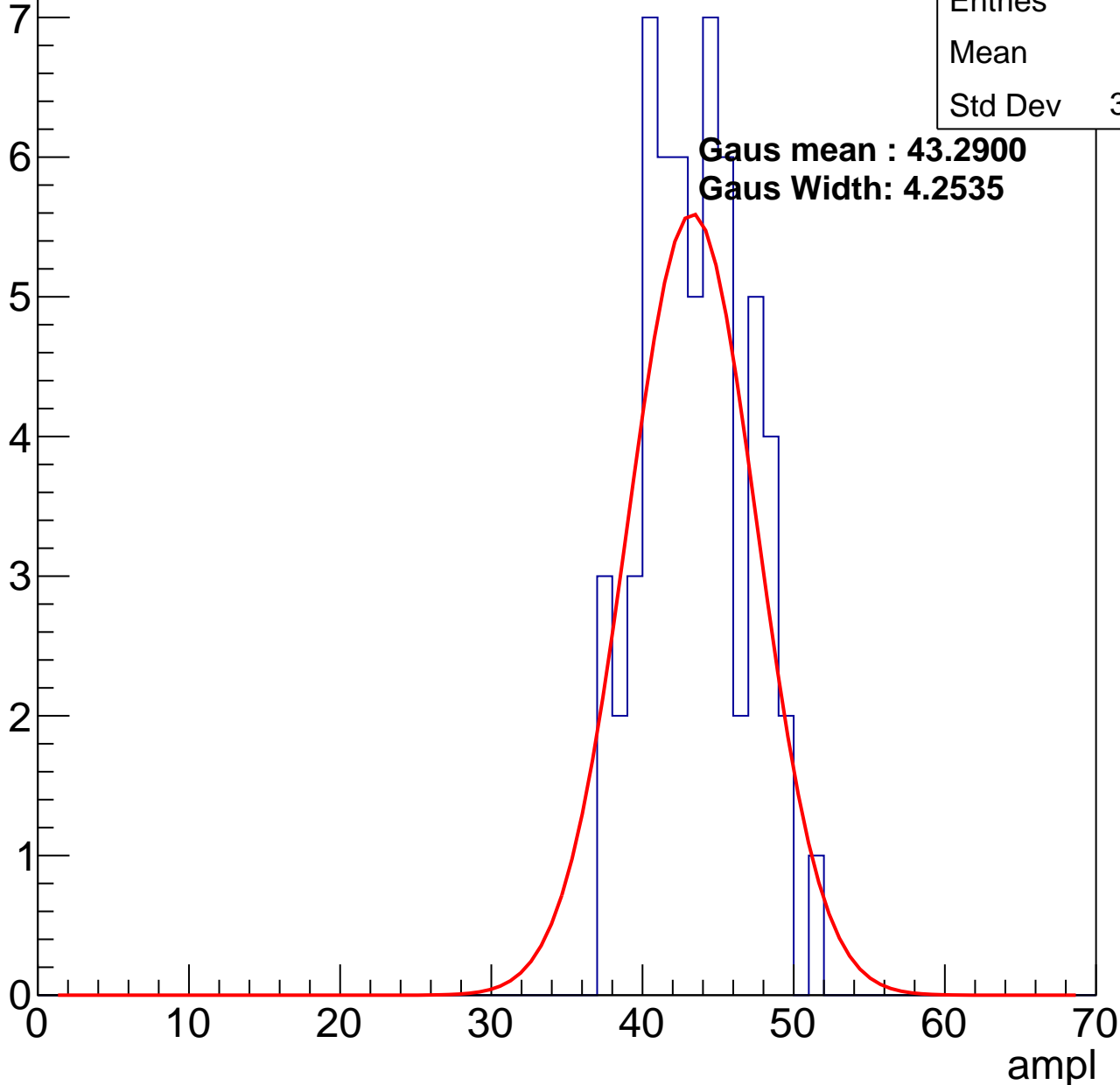
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.1
Std Dev	3.353

Gaus mean : 43.2900

Gaus Width: 4.2535

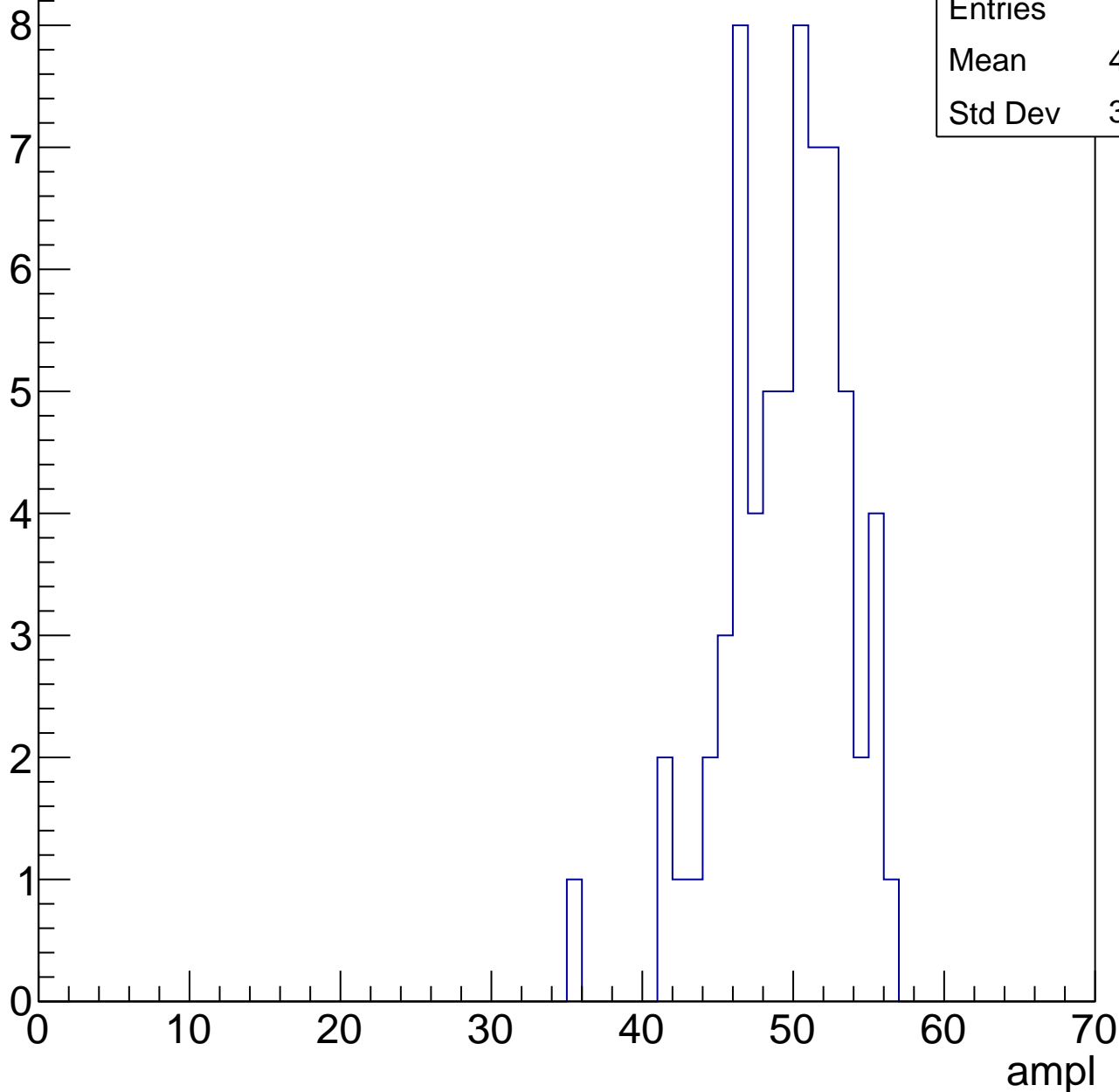


B1L100S, U15-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	49.03
Std Dev	3.935

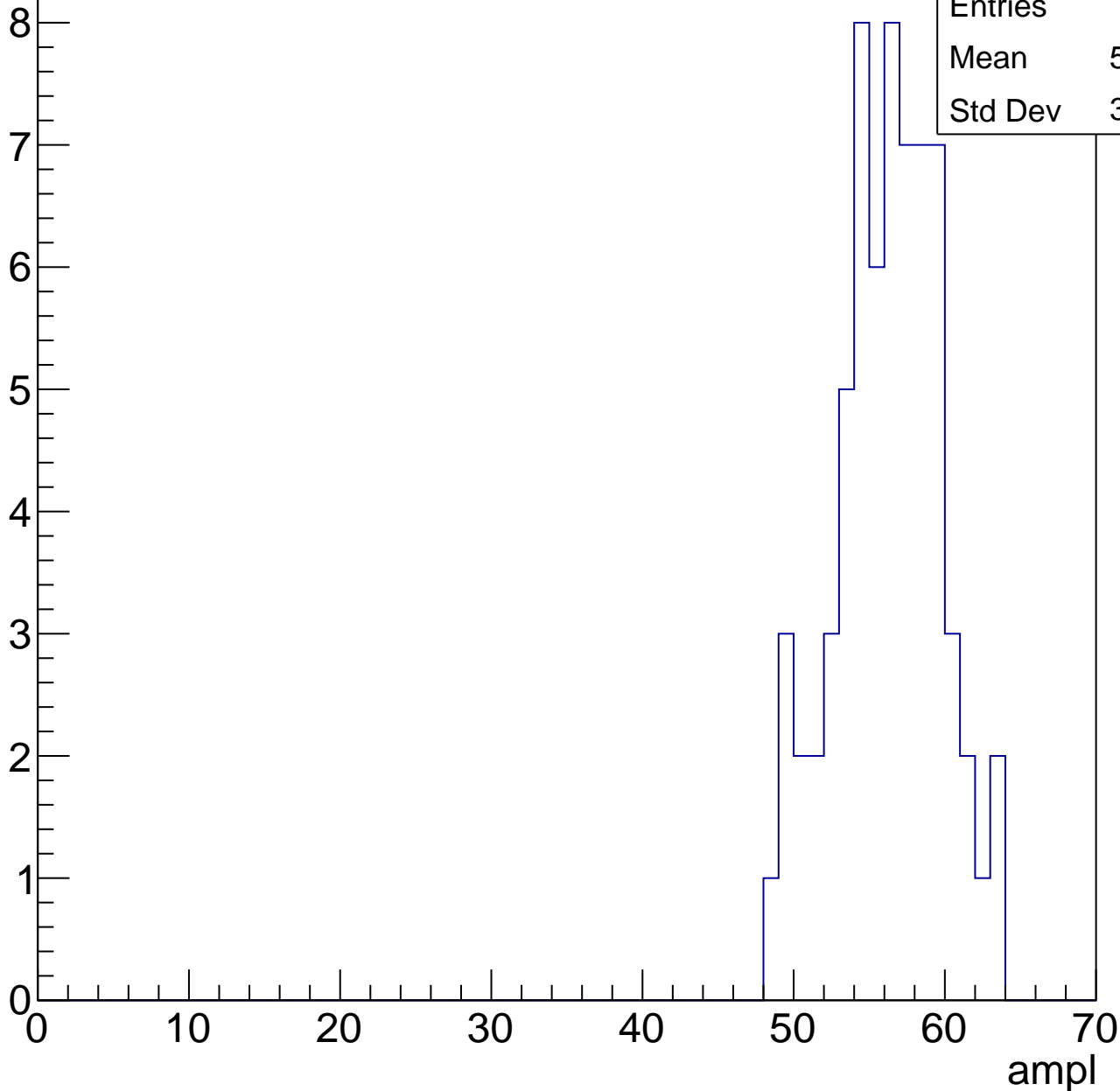


B1L100S, U15-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	55.76
Std Dev	3.434



B1L100S, U15-ch37, adc5

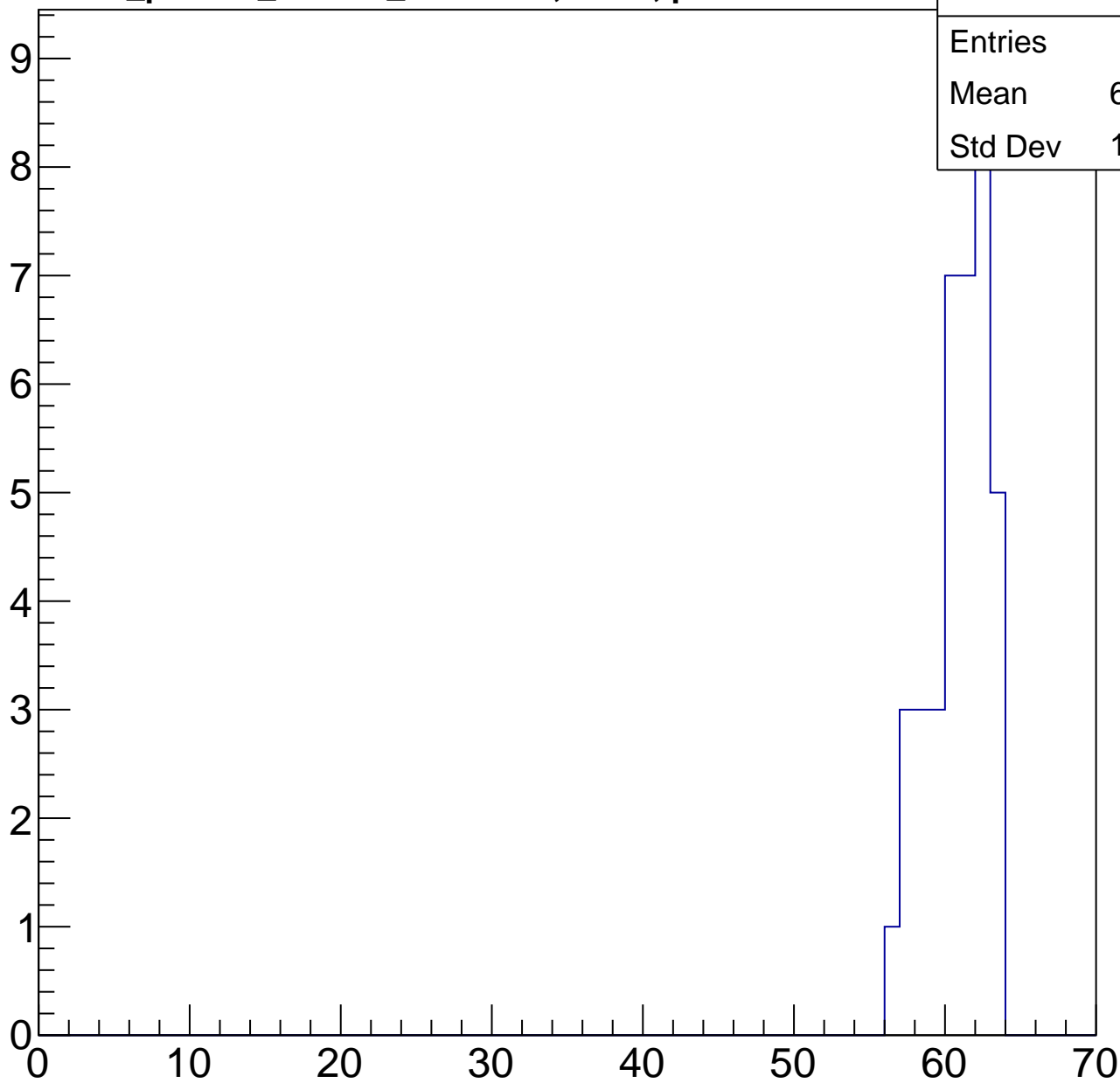
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.47
Std Dev	1.902

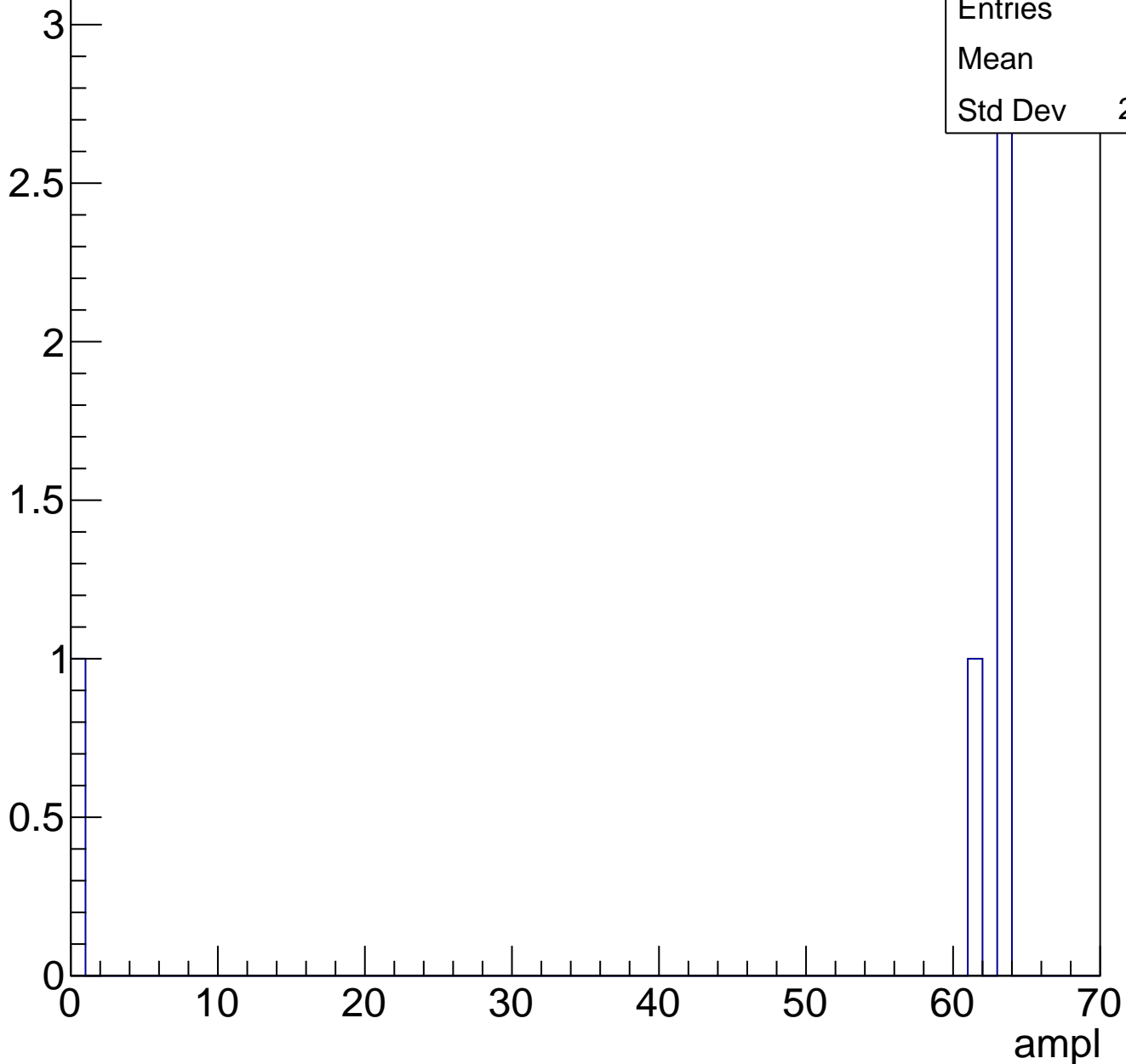
ampl



B1L100S, U15-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch38, adc0

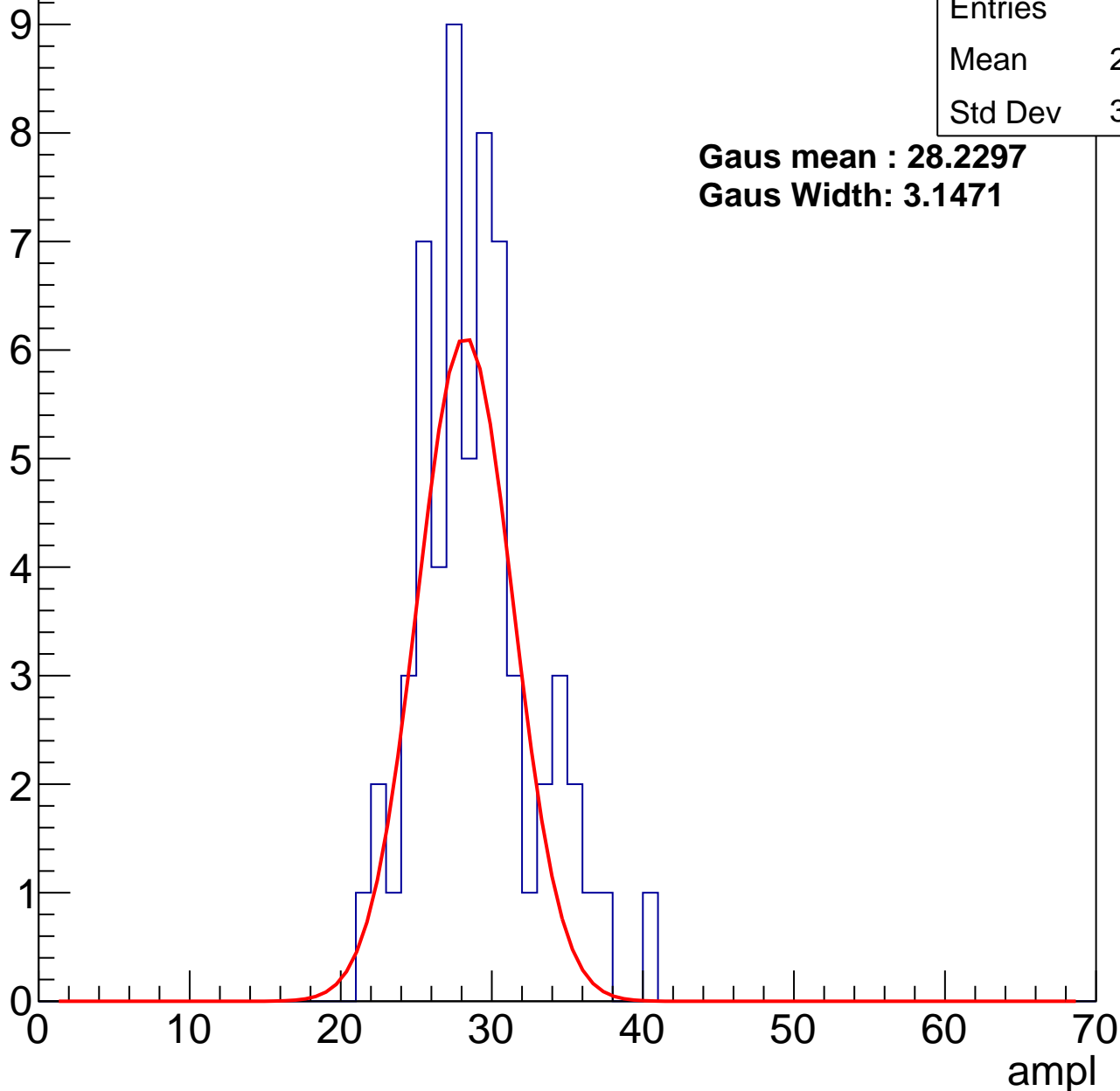
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	28.52
Std Dev	3.818

Gaus mean : 28.2297

Gaus Width: 3.1471



B1L100S, U15-ch38, adc1

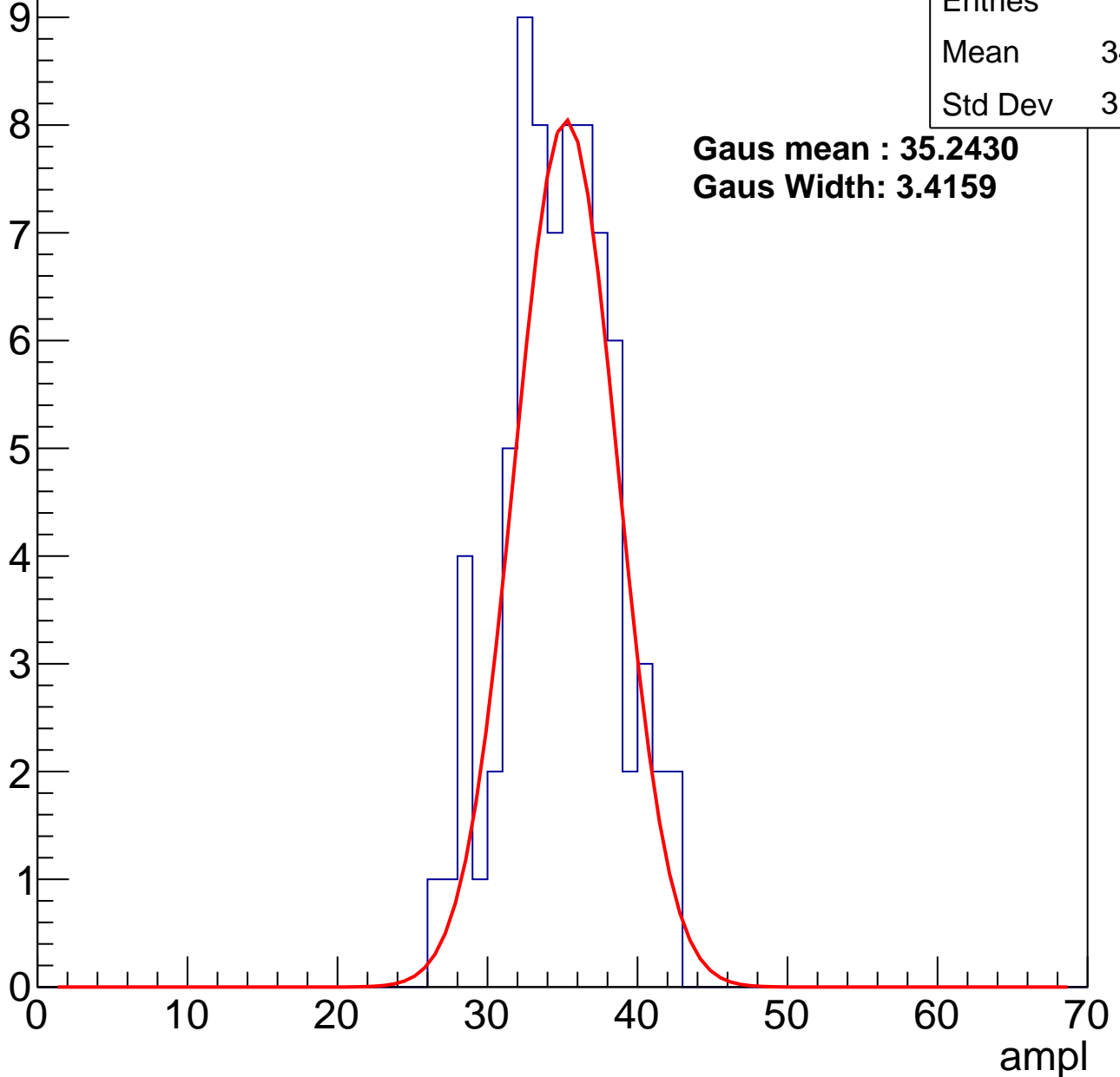
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	34.45
Std Dev	3.574

Gaus mean : 35.2430

Gaus Width: 3.4159



B1L100S, U15-ch38, adc2

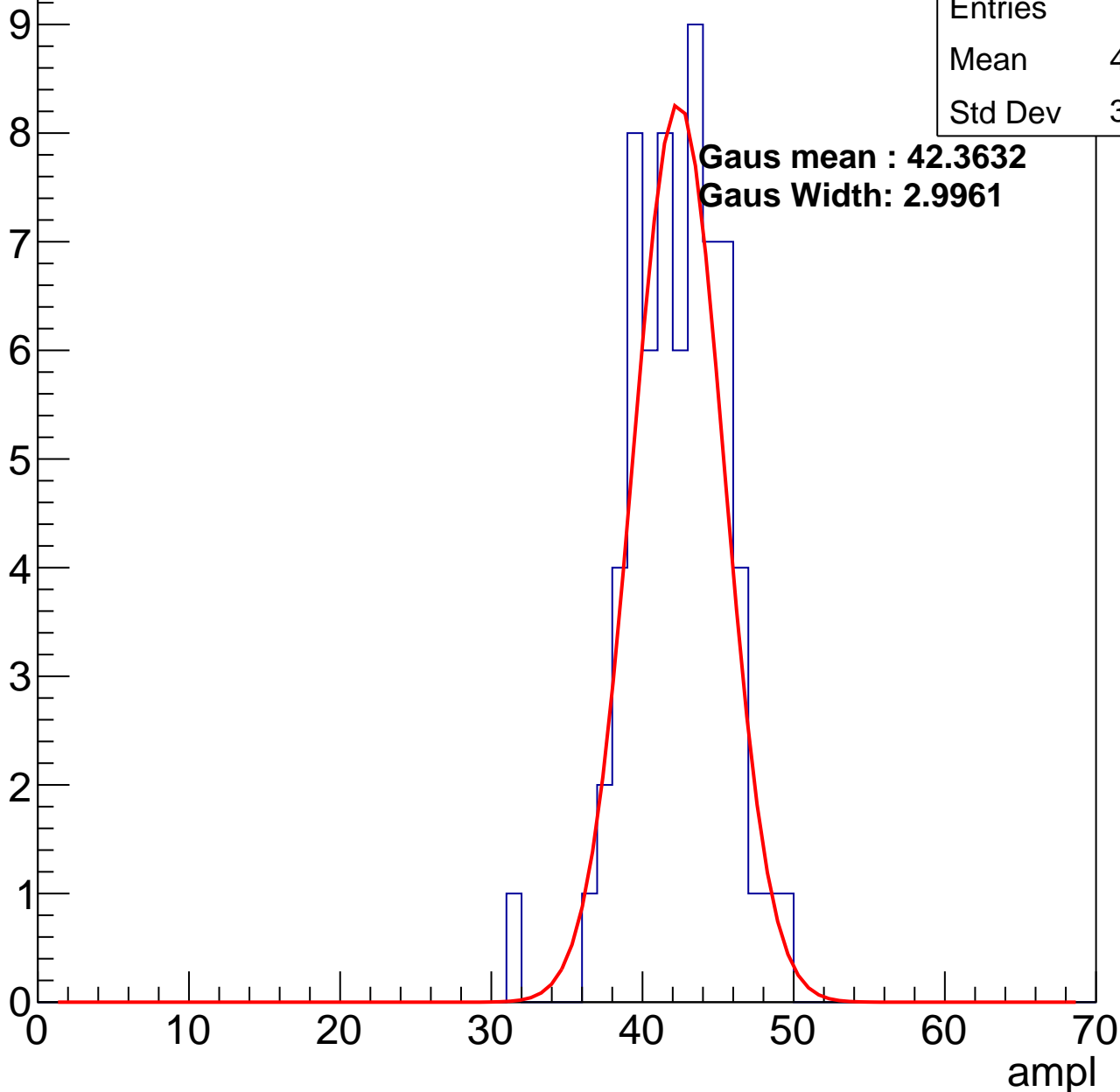
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.86
Std Dev	3.133

Gaus mean : 42.3632

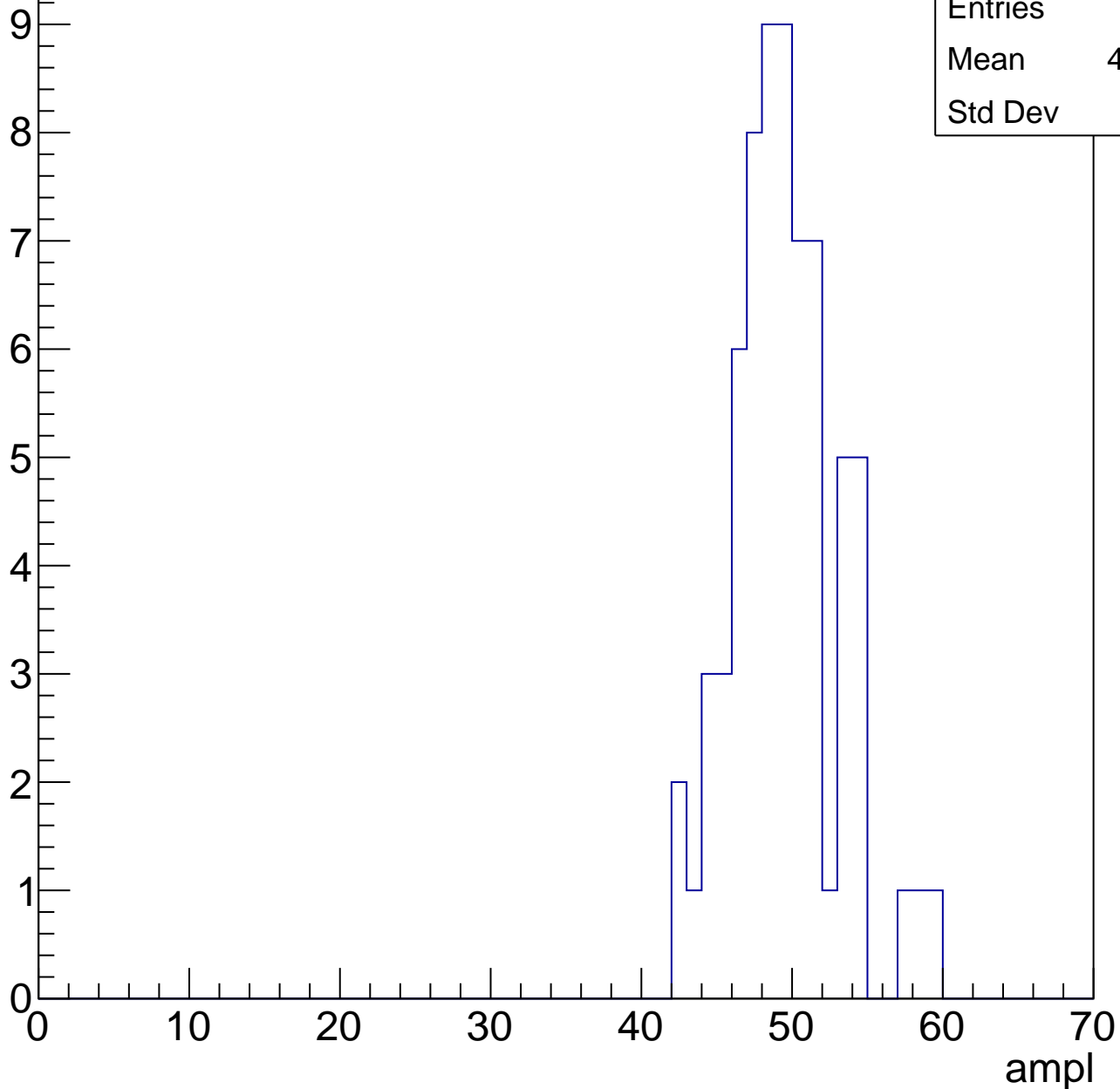
Gaus Width: 2.9961



B1L100S, U15-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



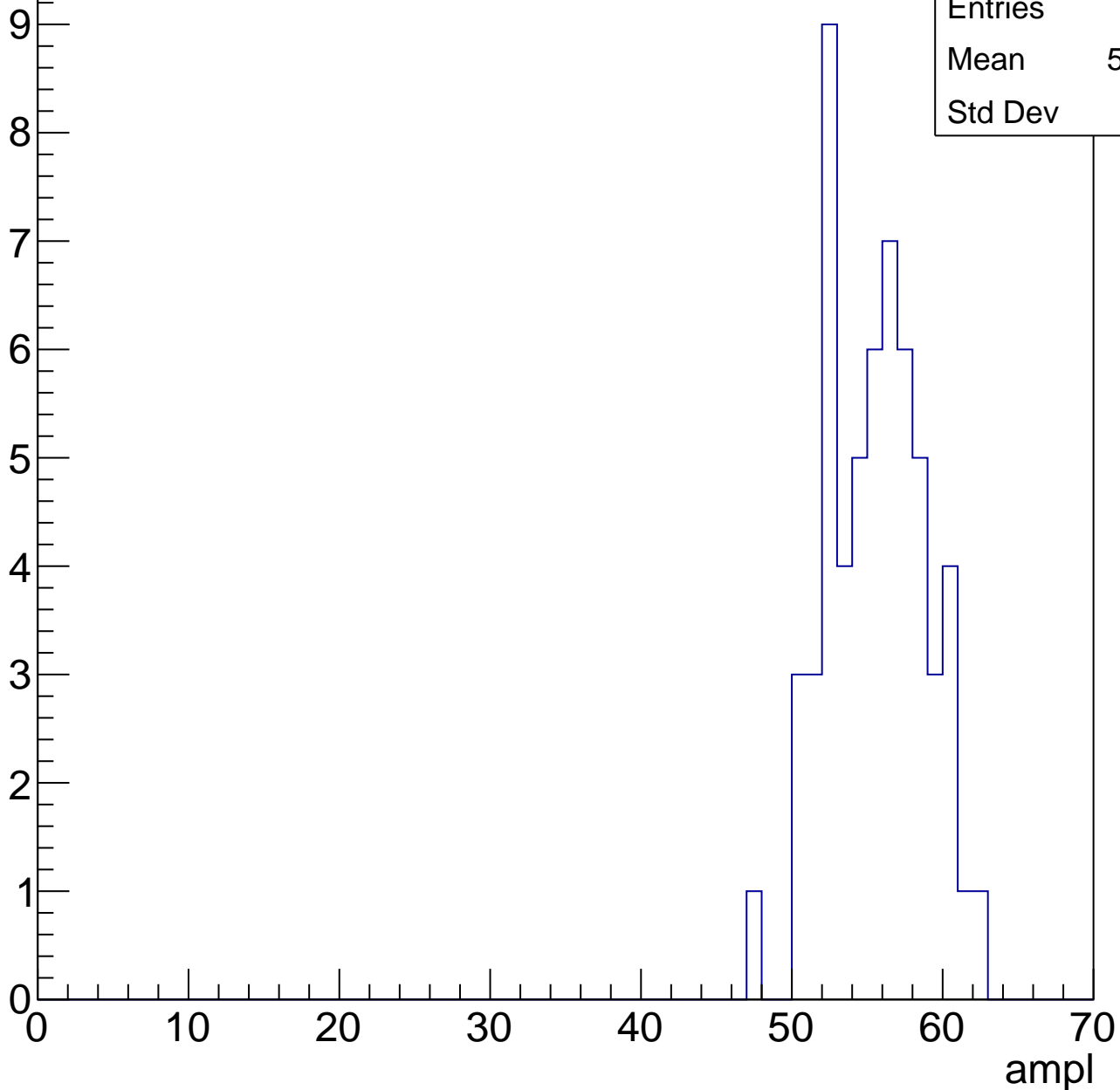
Entries	69
Mean	49.09
Std Dev	3.5

B1L100S, U15-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	55.07
Std Dev	3.21



B1L100S, U15-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

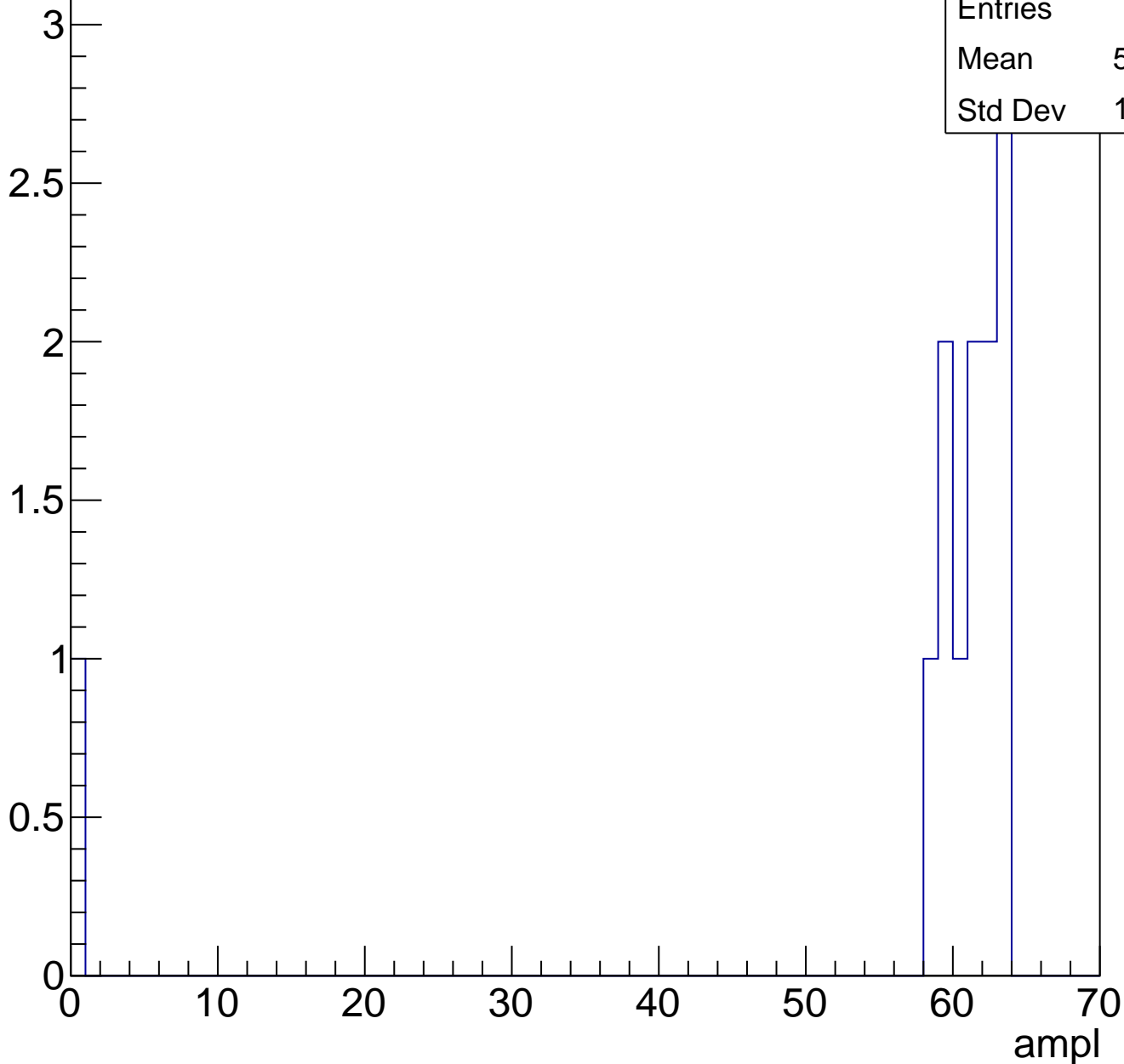
Entries	43
Mean	59.6
Std Dev	2.771

ampl

B1L100S, U15-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch39, adc0

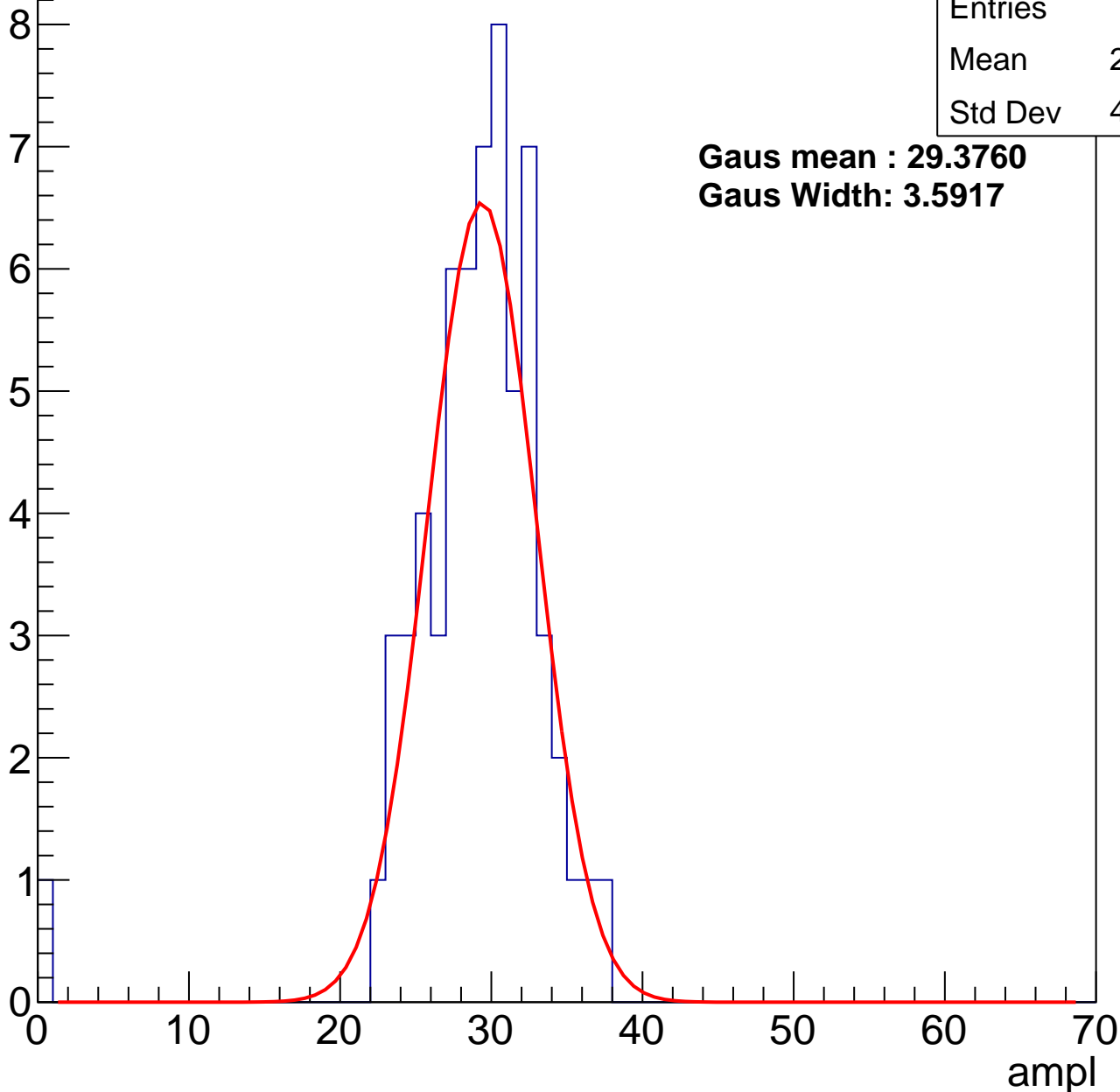
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.52
Std Dev	4.947

Gaus mean : 29.3760

Gaus Width: 3.5917



B1L100S, U15-ch39, adc1

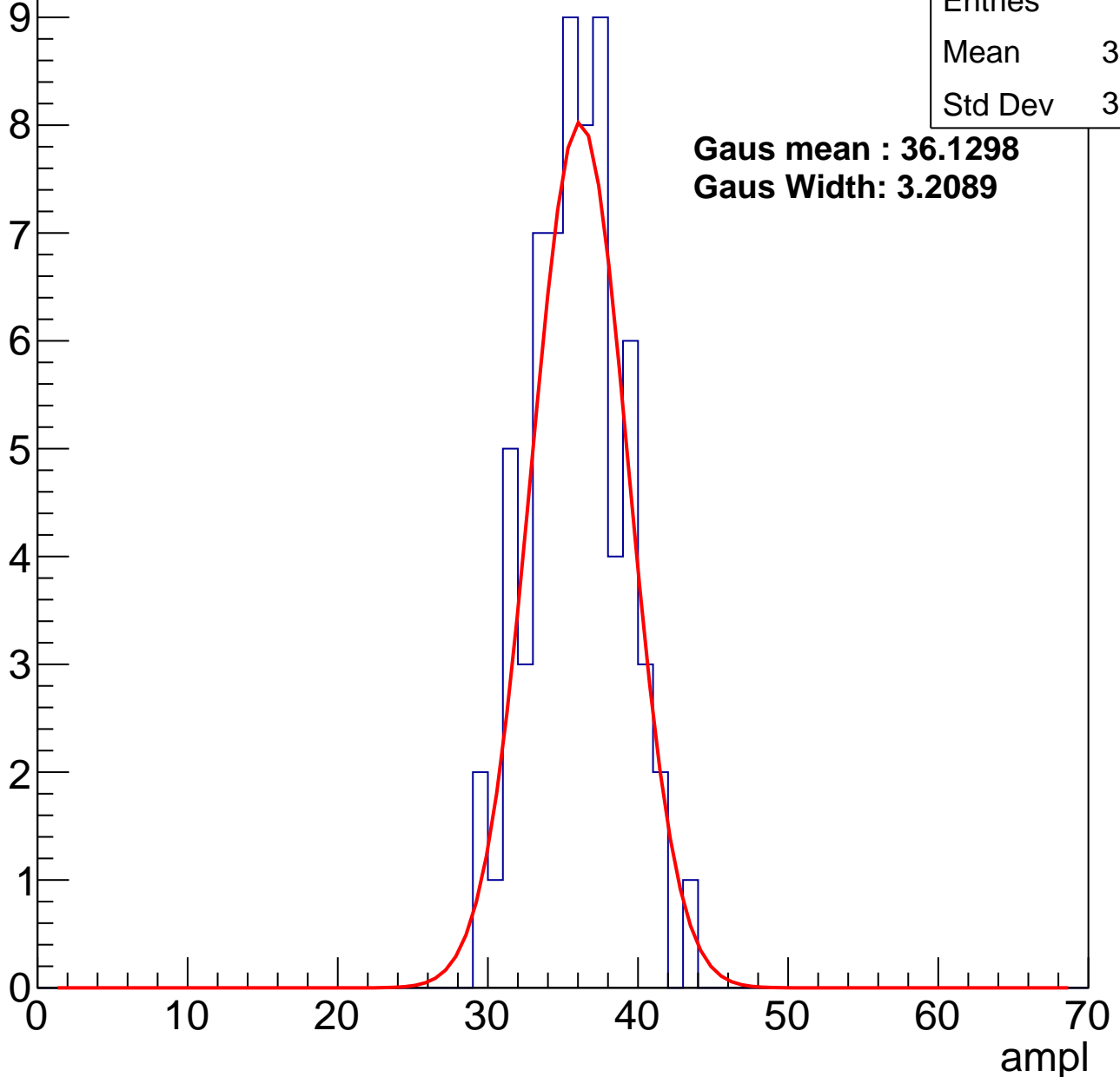
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.45
Std Dev	3.024

Gaus mean : 36.1298

Gaus Width: 3.2089



B1L100S, U15-ch39, adc2

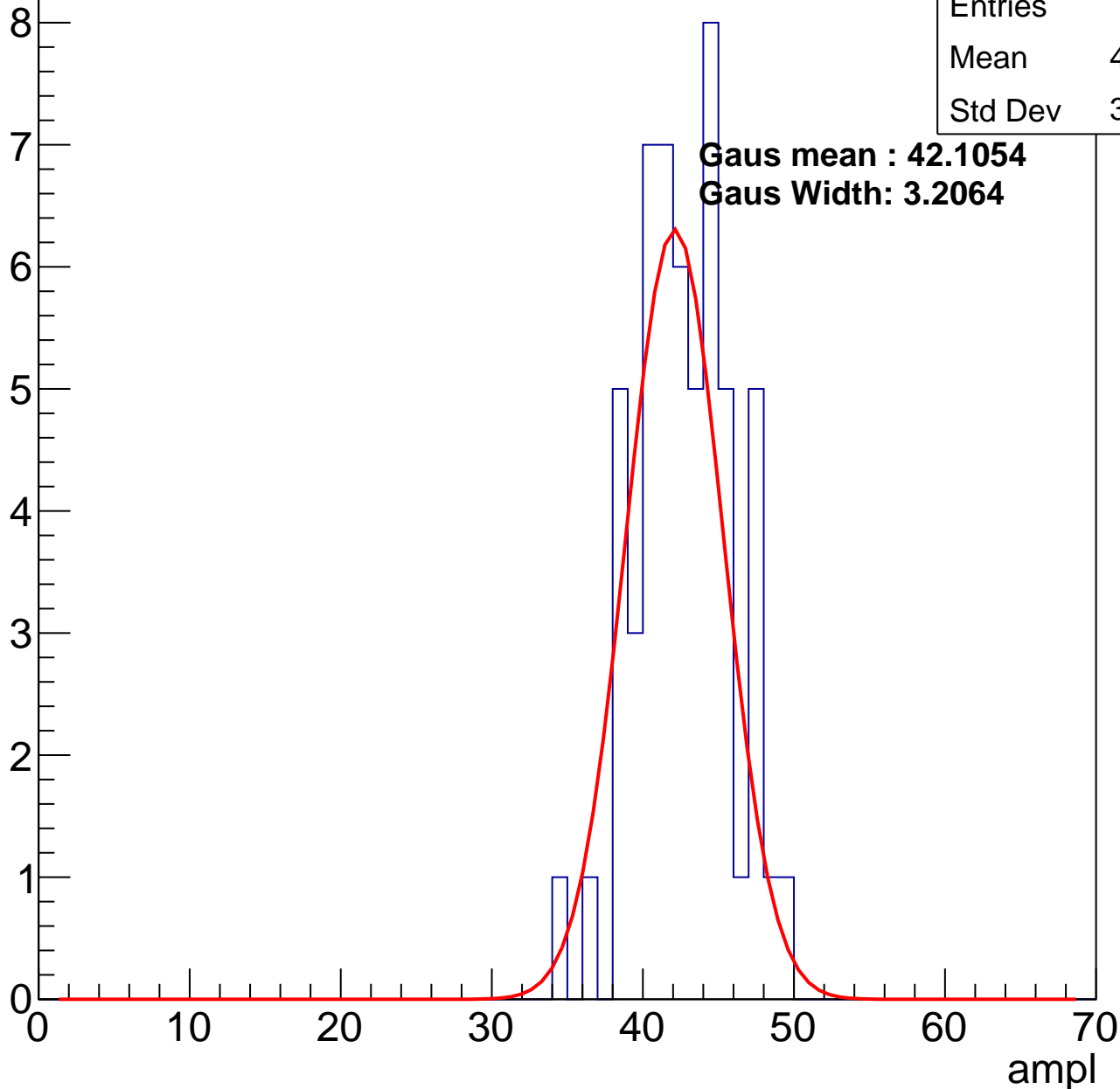
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.25
Std Dev	3.124

Gaus mean : 42.1054

Gaus Width: 3.2064

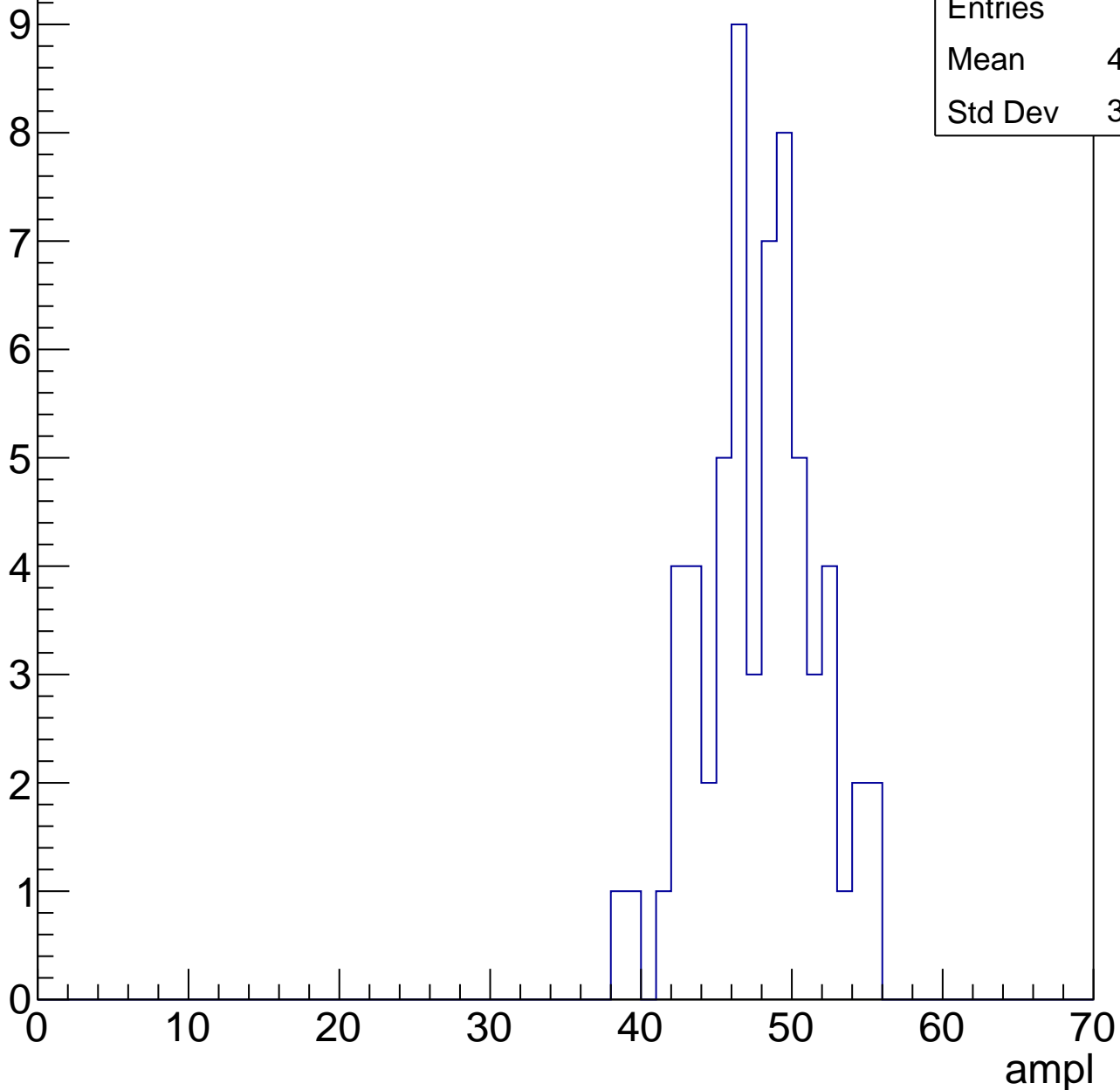


B1L100S, U15-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	47.35
Std Dev	3.764

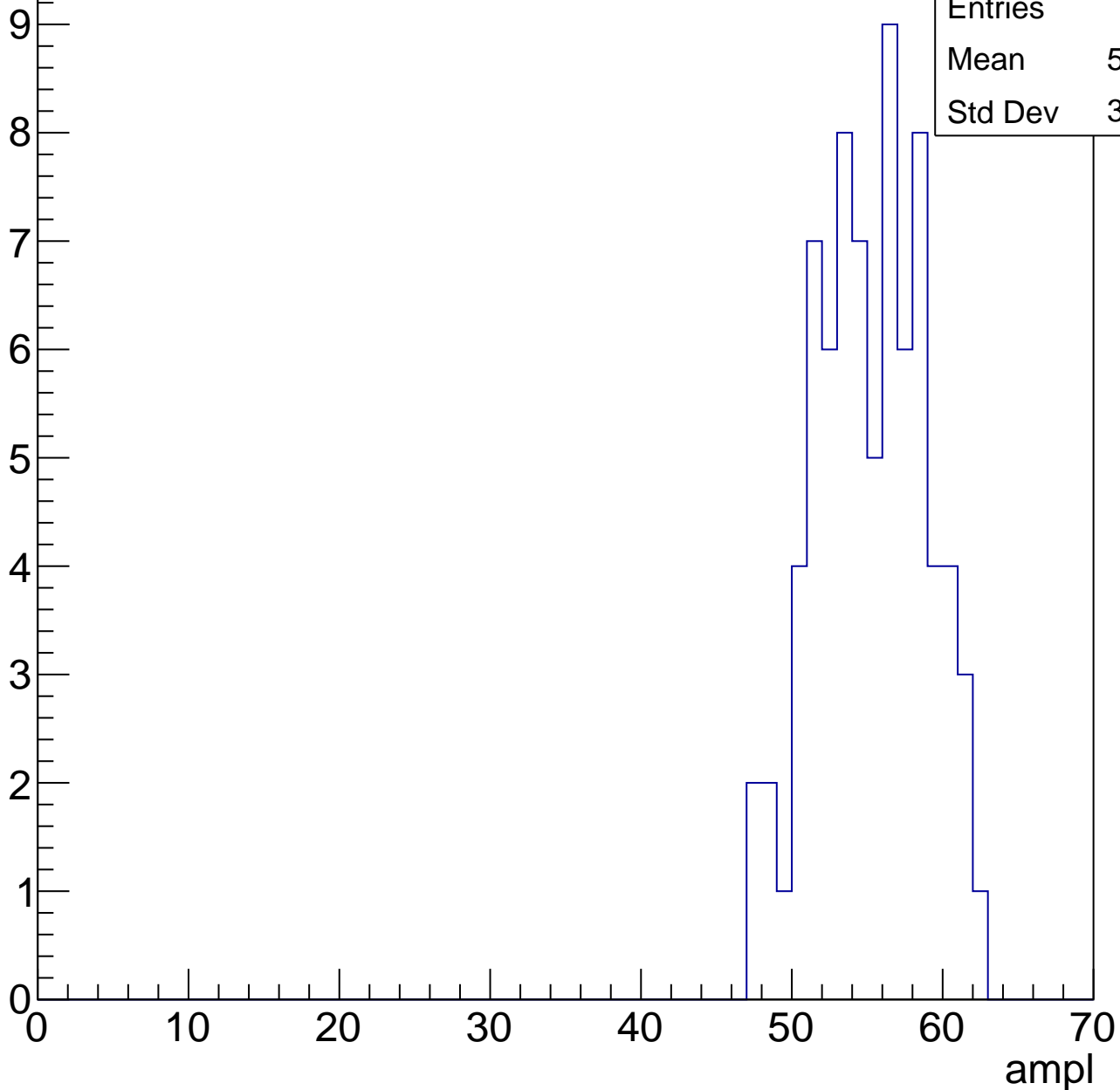


B1L100S, U15-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	54.75
Std Dev	3.583

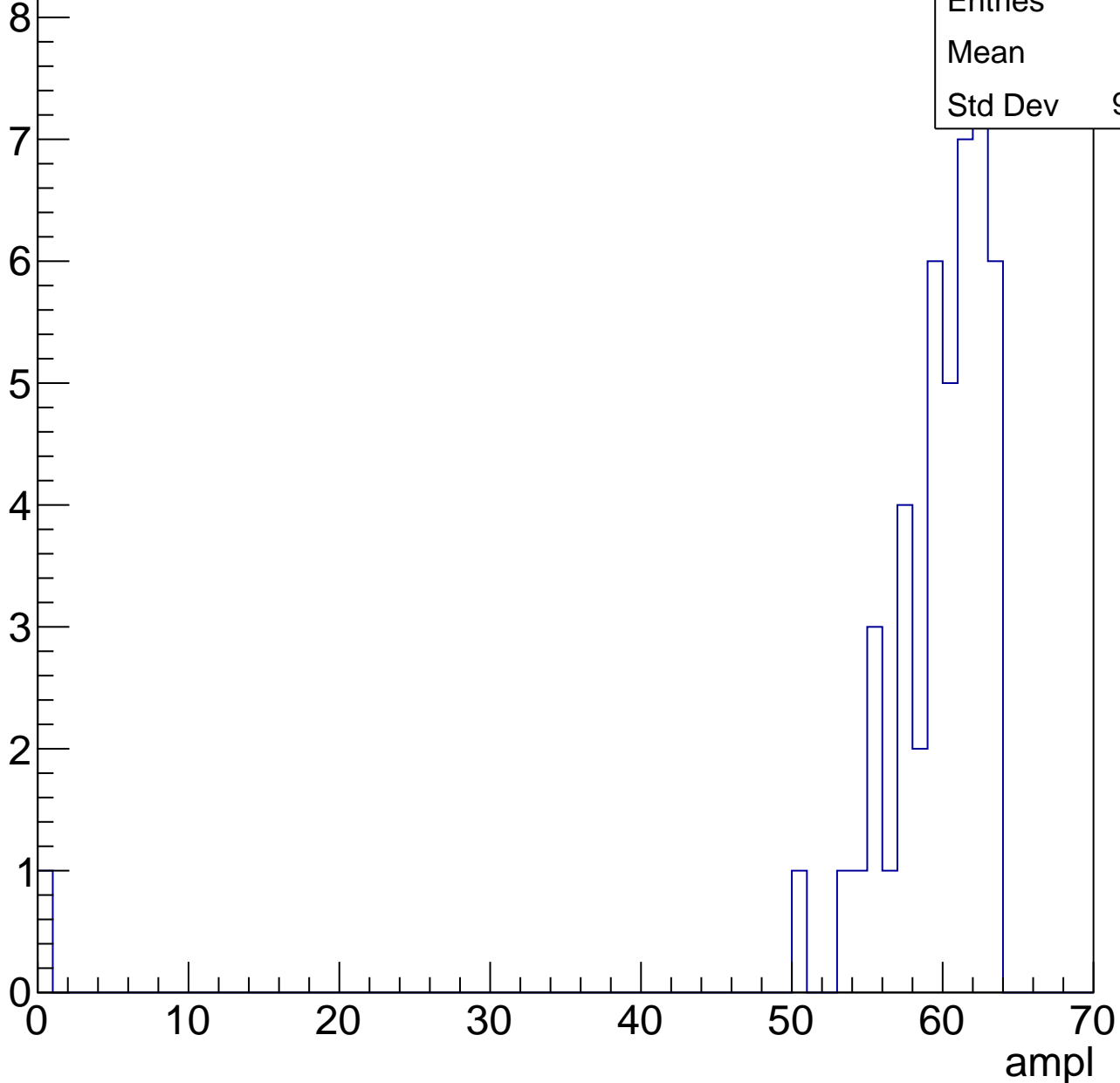


B1L100S, U15-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

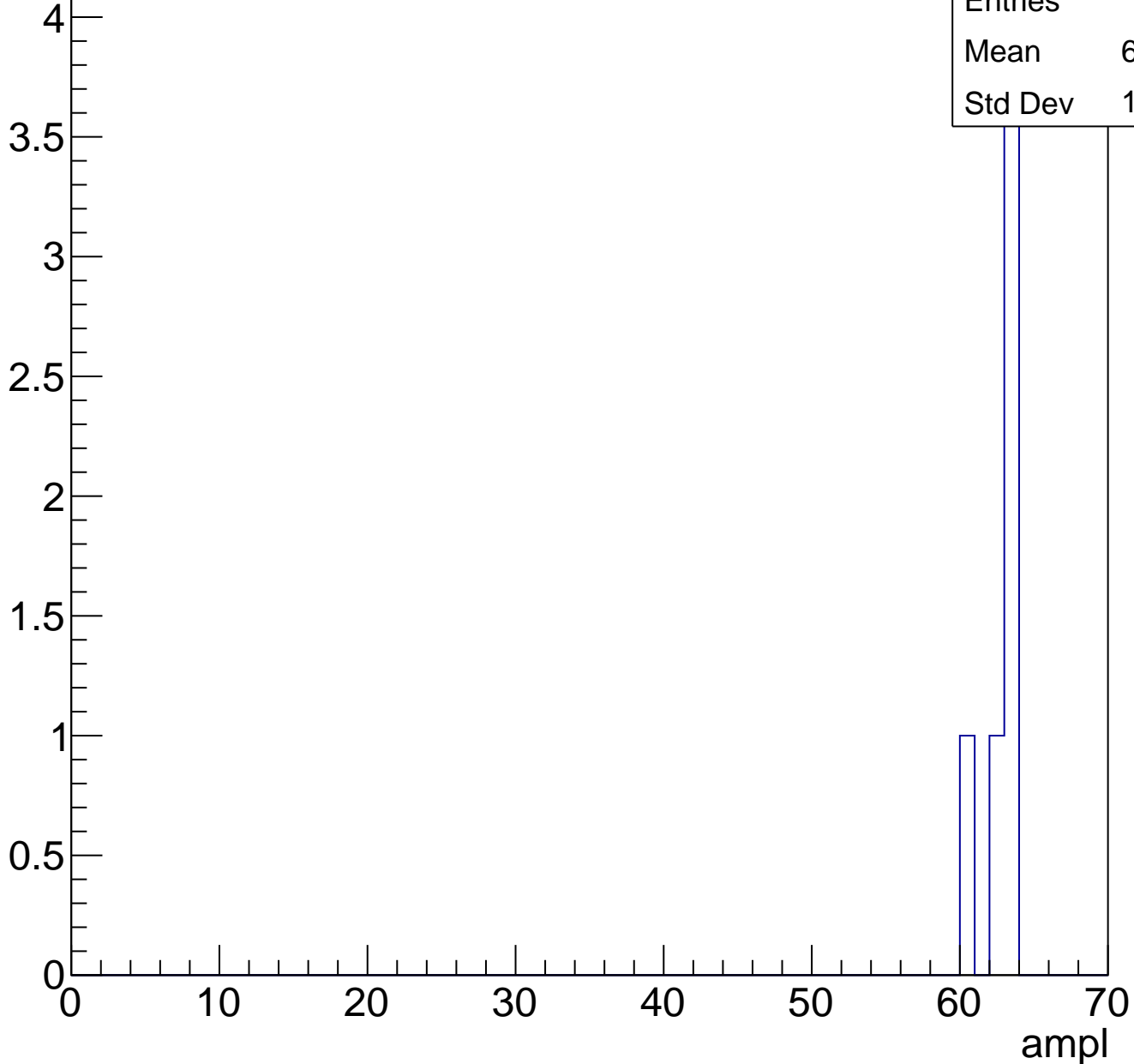
Entries	46
Mean	58.2
Std Dev	9.171



B1L100S, U15-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch40, adc0

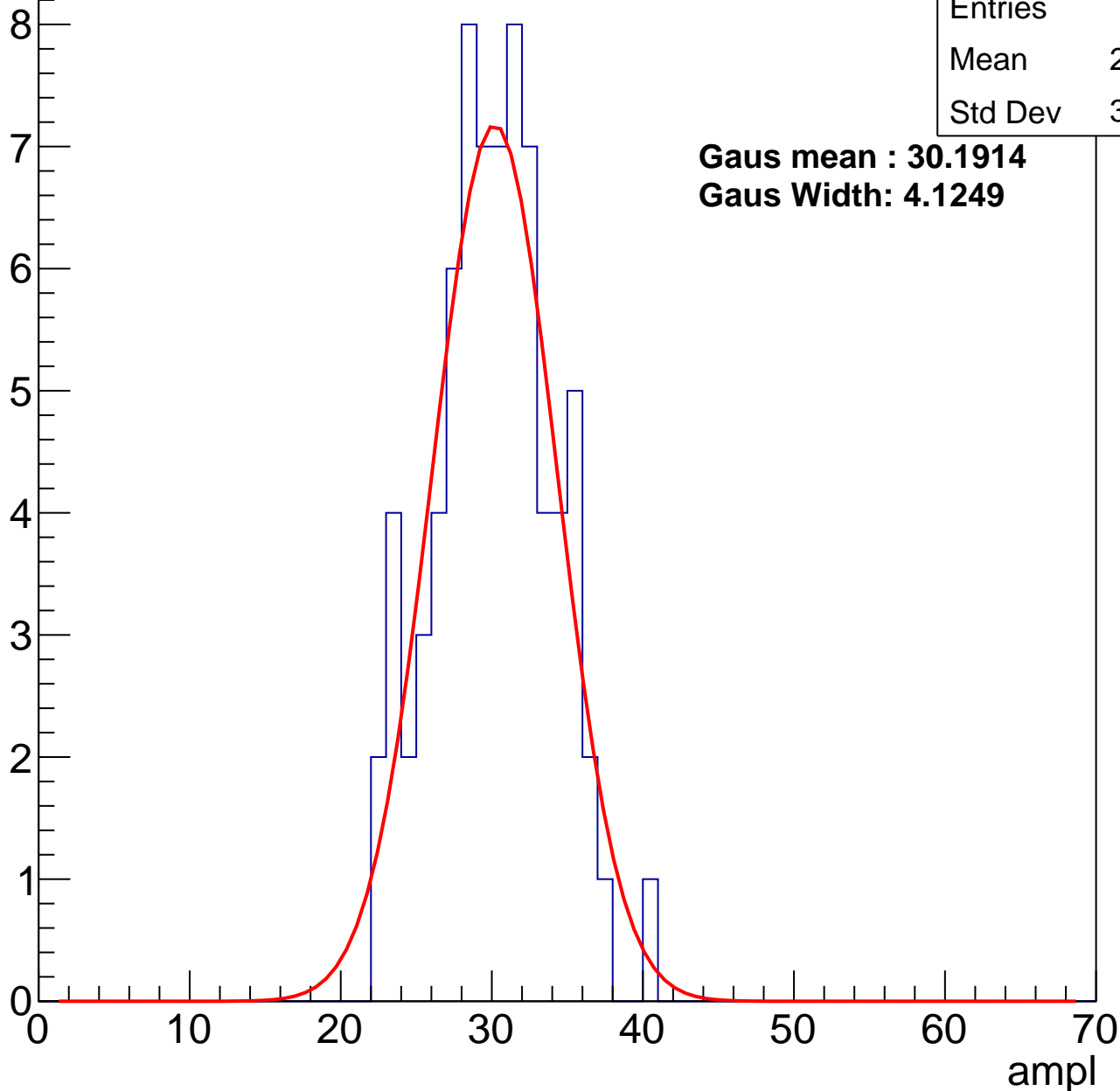
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.68
Std Dev	3.834

Gaus mean : 30.1914

Gaus Width: 4.1249



B1L100S, U15-ch40, adc1

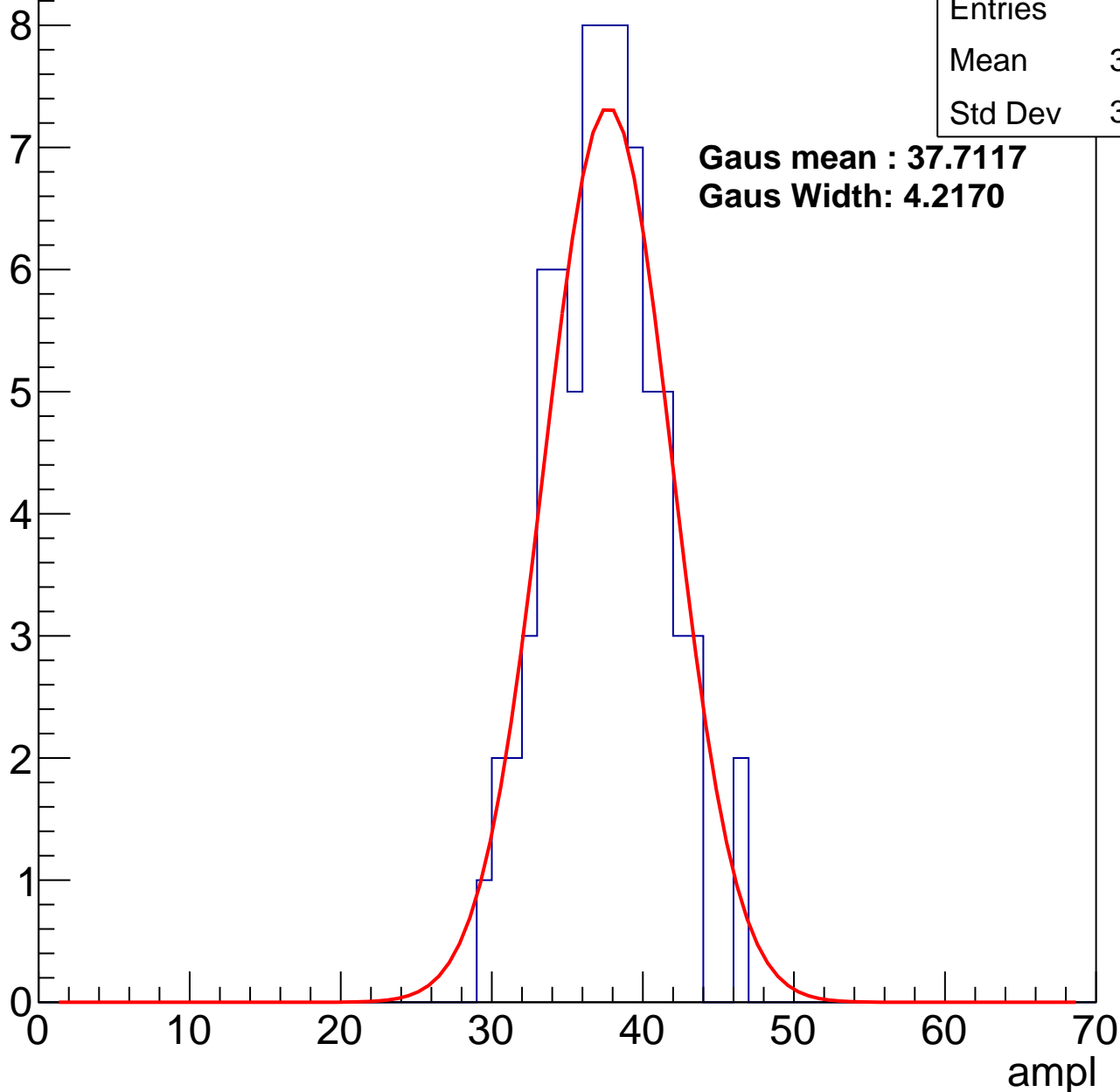
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.99
Std Dev	3.663

Gaus mean : 37.7117

Gaus Width: 4.2170



B1L100S, U15-ch40, adc2

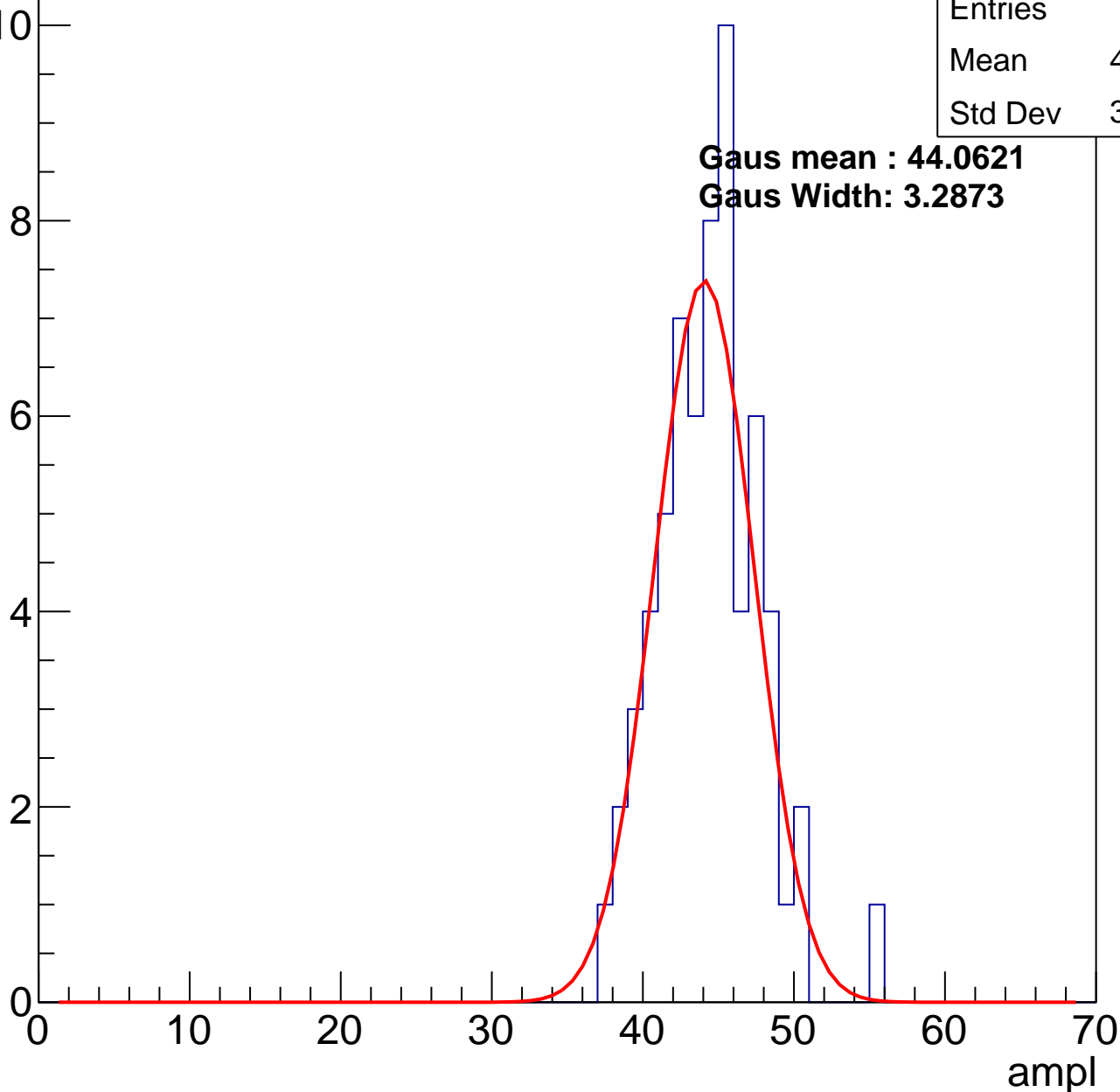
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.92
Std Dev	3.309

Gaus mean : 44.0621

Gaus Width: 3.2873

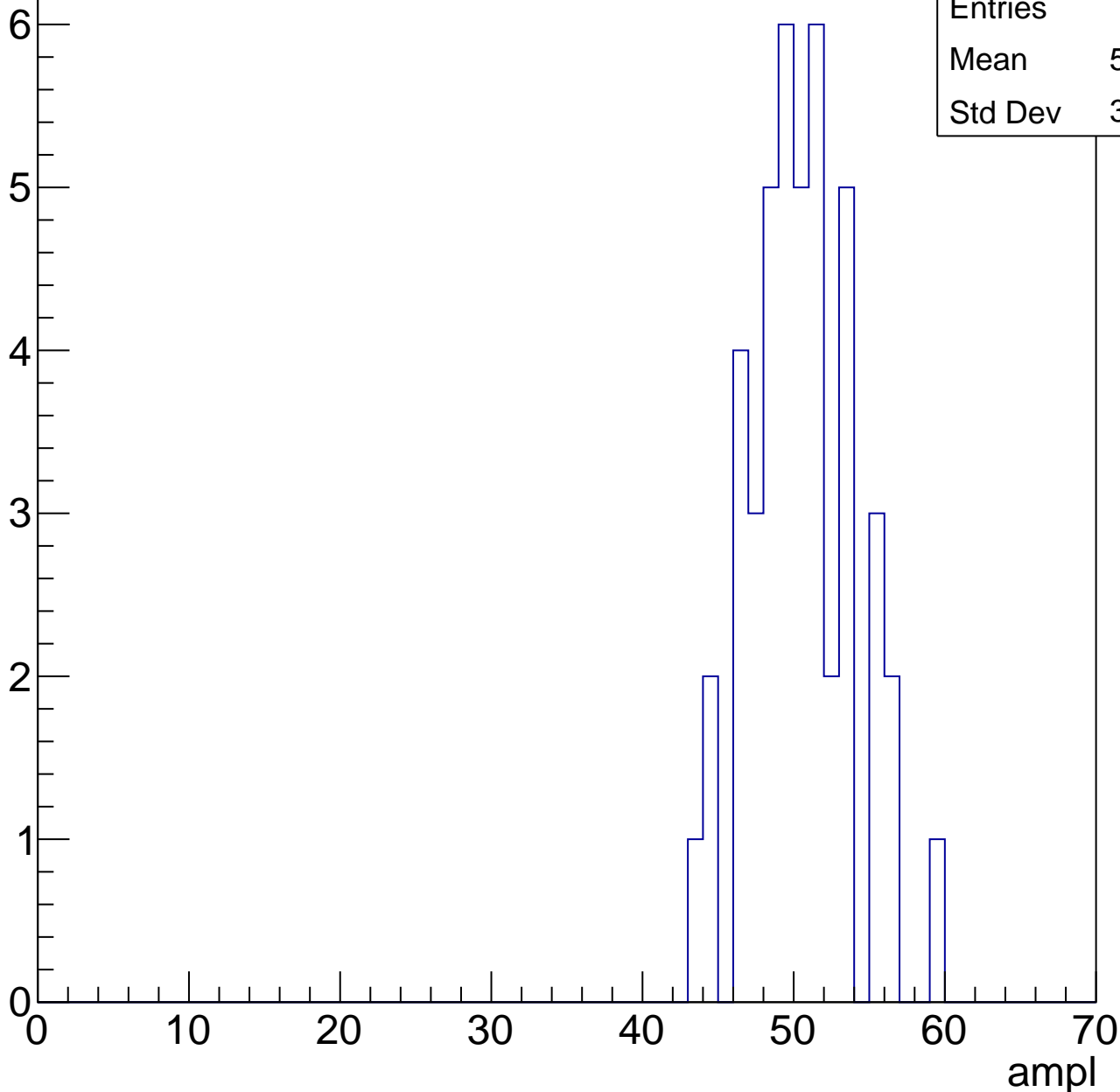


B1L100S, U15-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	50.02
Std Dev	3.416

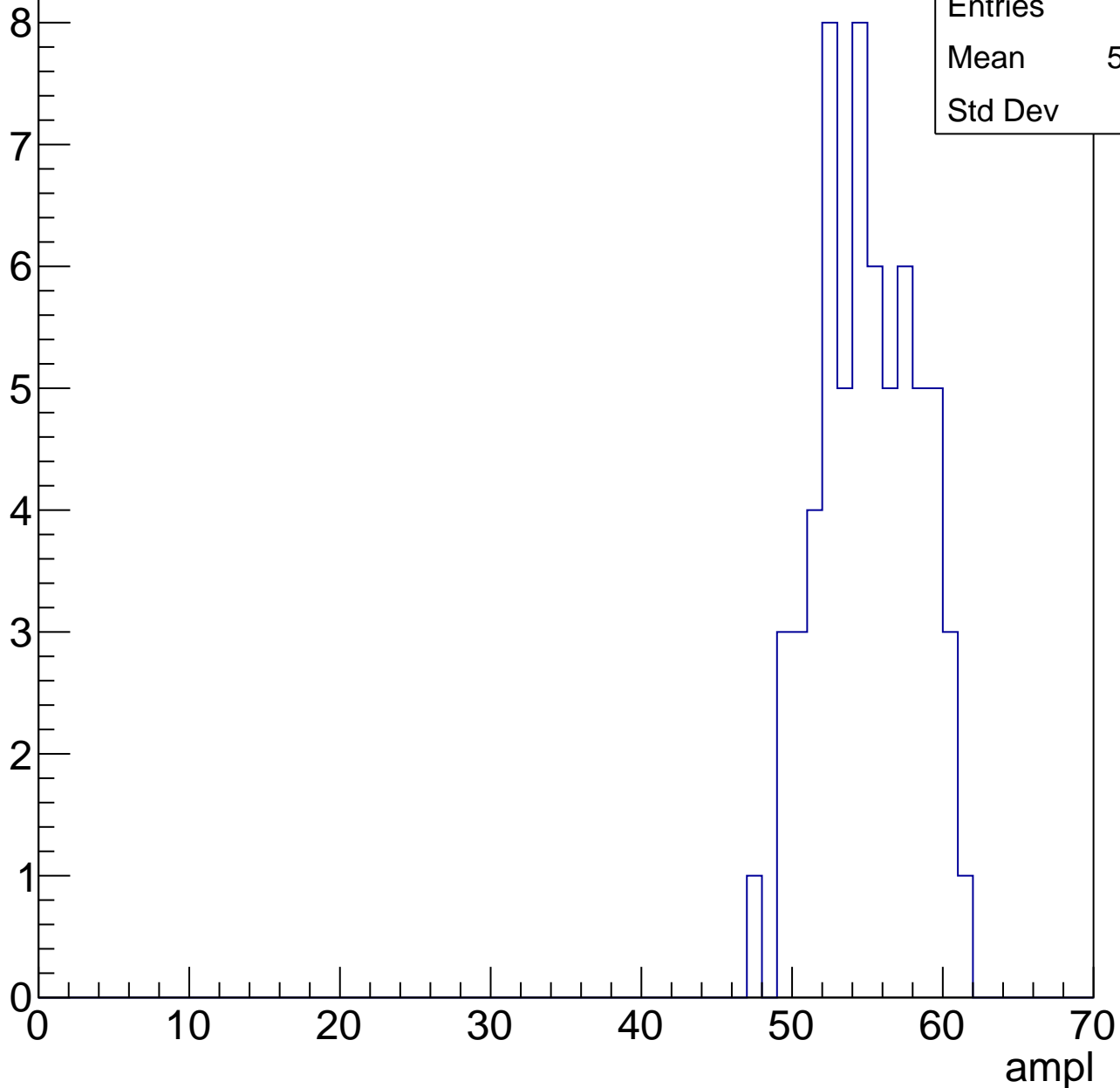


B1L100S, U15-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	54.59
Std Dev	3.25

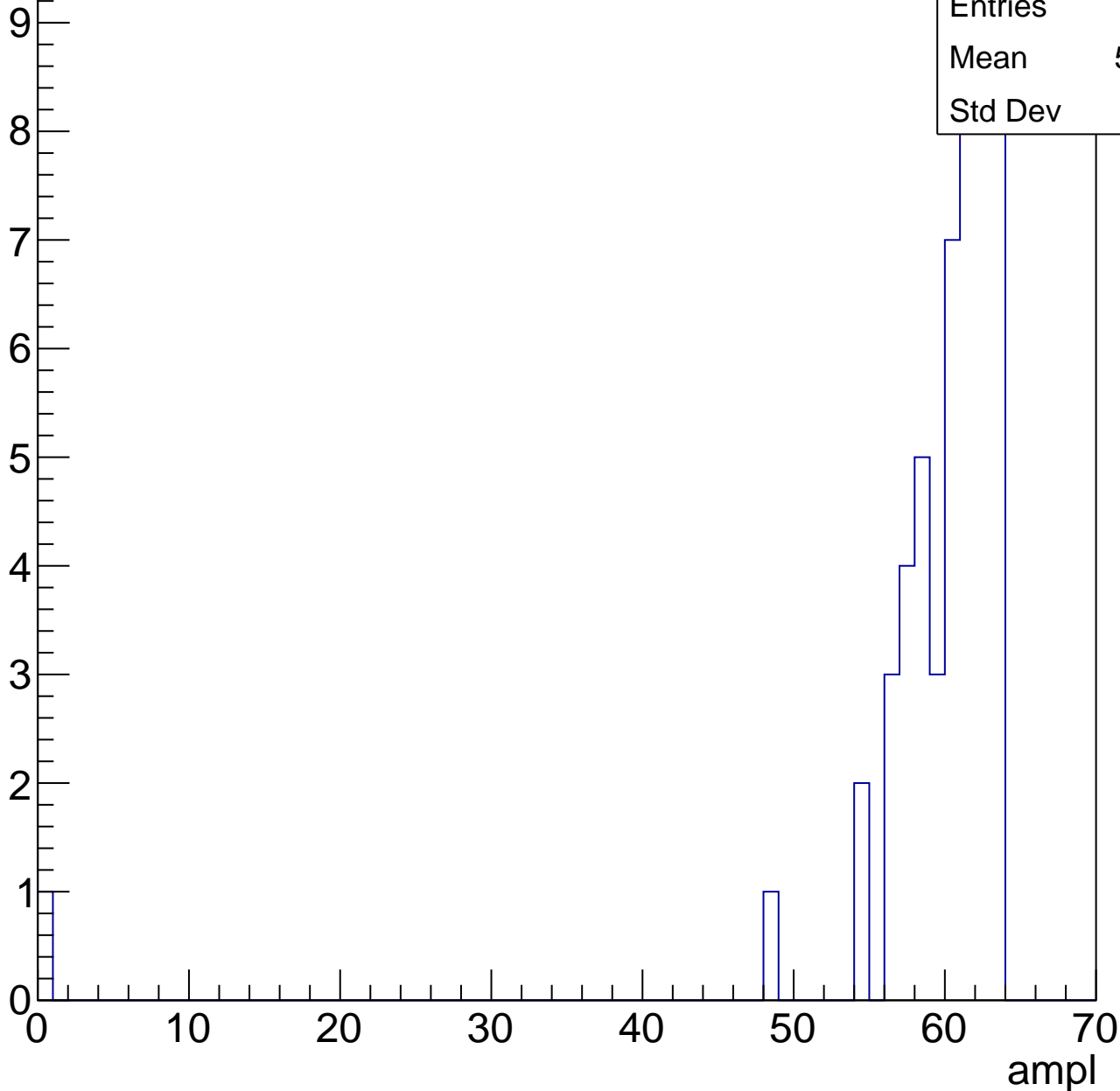


B1L100S, U15-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

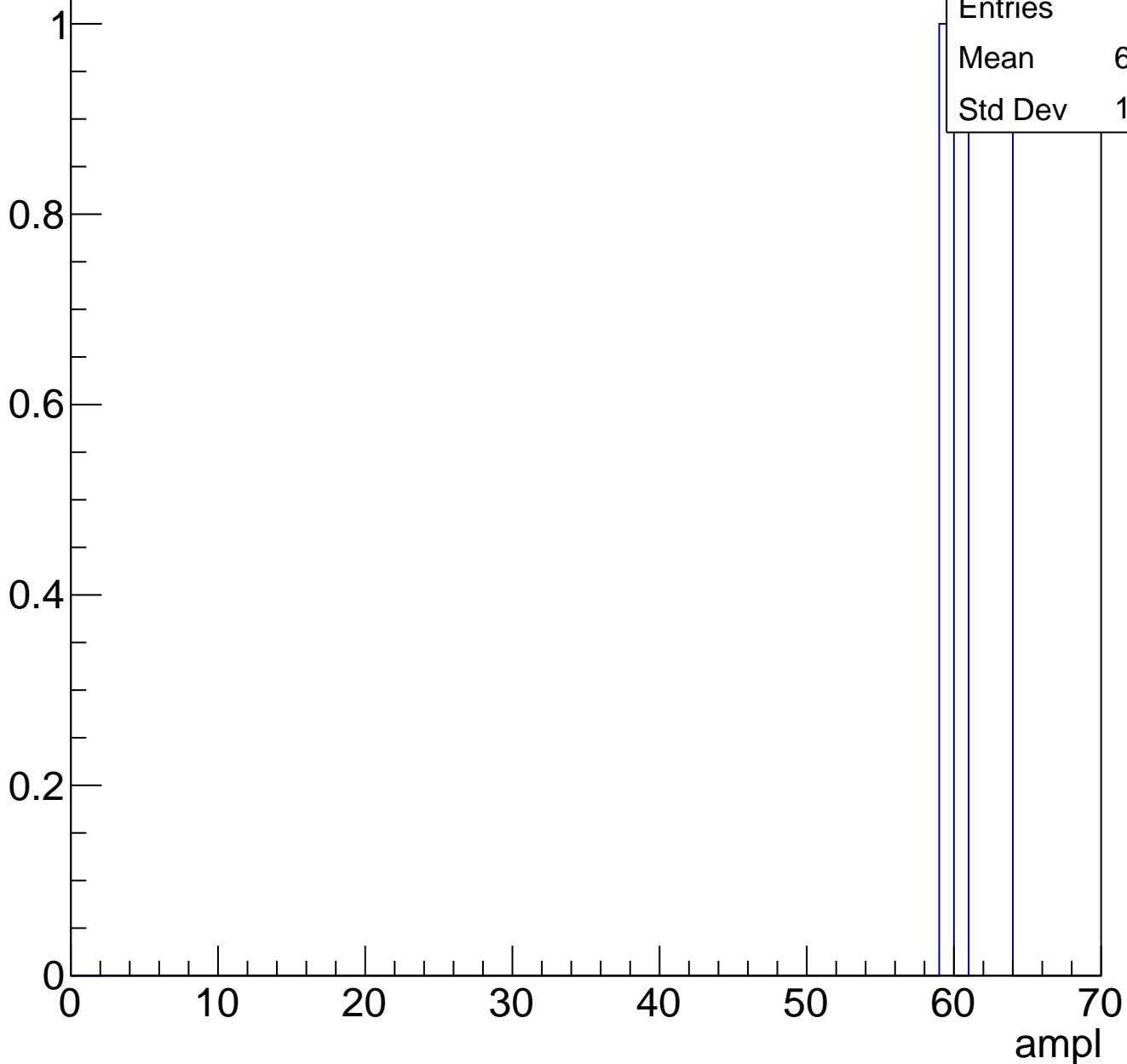
Entries	51
Mean	58.61
Std Dev	8.79



B1L100S, U15-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U15-ch41, adc0

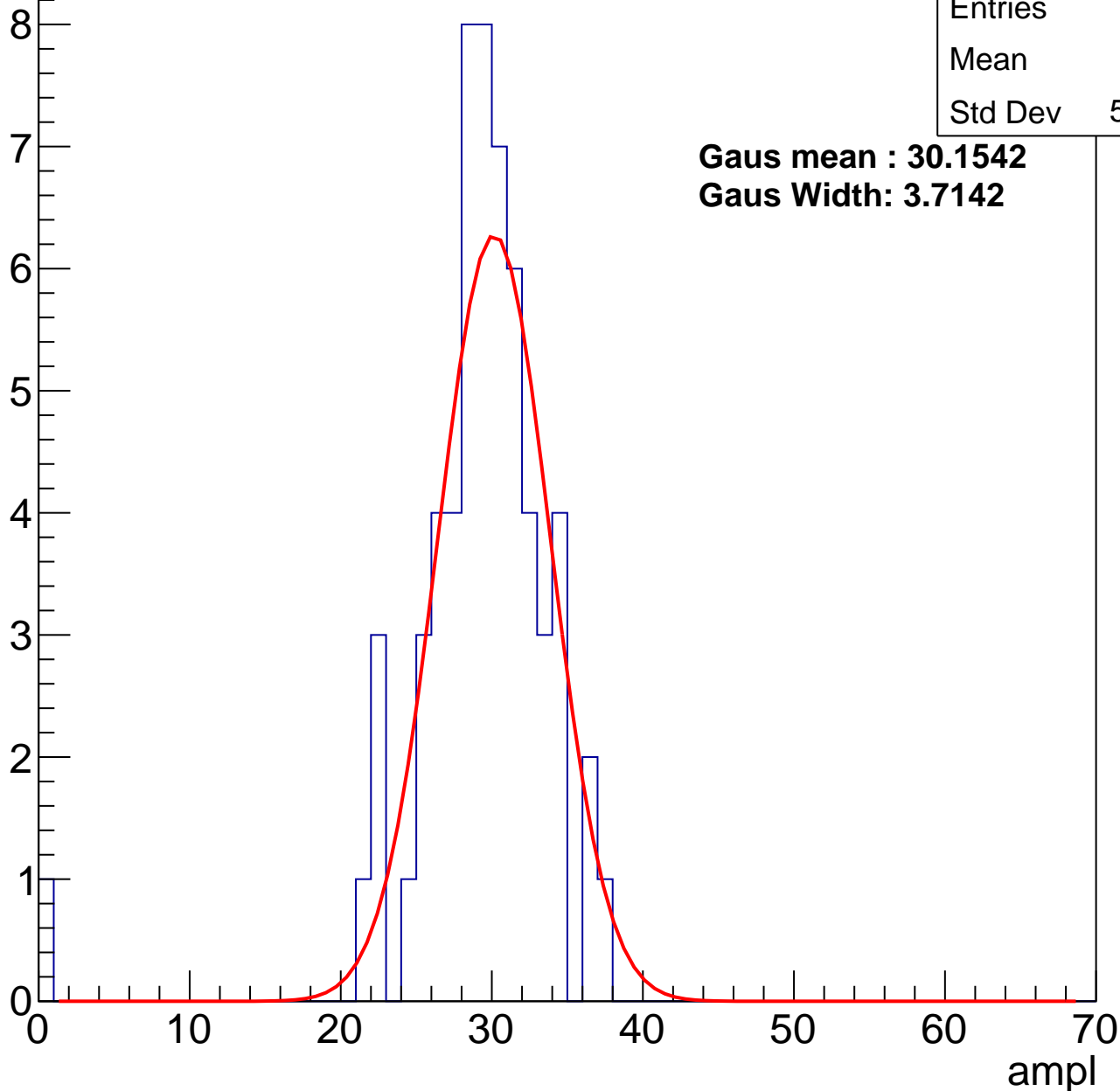
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	28.7
Std Dev	5.093

Gaus mean : 30.1542

Gaus Width: 3.7142



B1L100S, U15-ch41, adc1

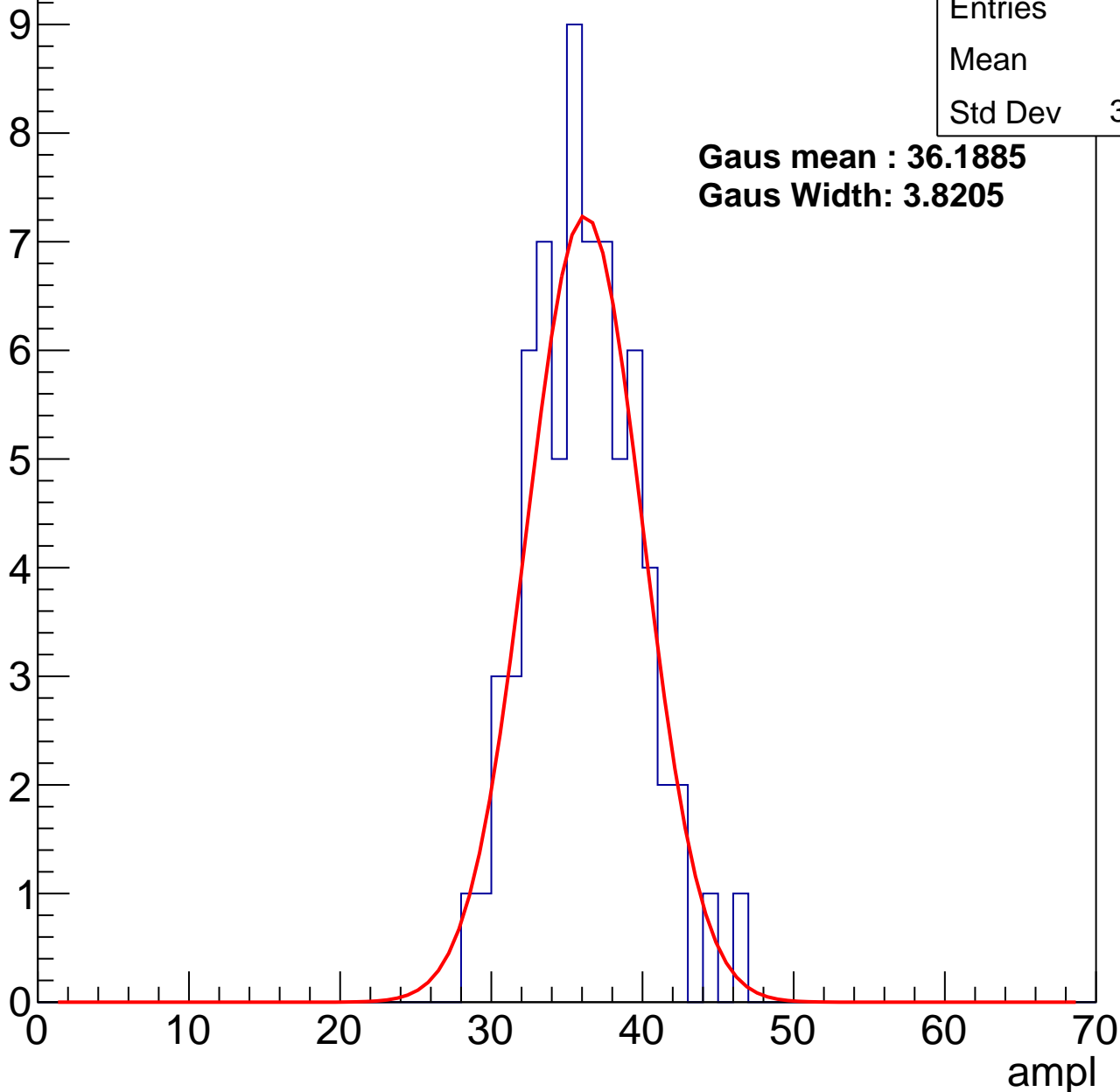
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.7
Std Dev	3.603

Gaus mean : 36.1885

Gaus Width: 3.8205



B1L100S, U15-ch41, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	42.42
Std Dev	3.443

Gaus mean : 42.7407

Gaus Width: 3.3451

10

8

6

4

2

0

0

10

20

30

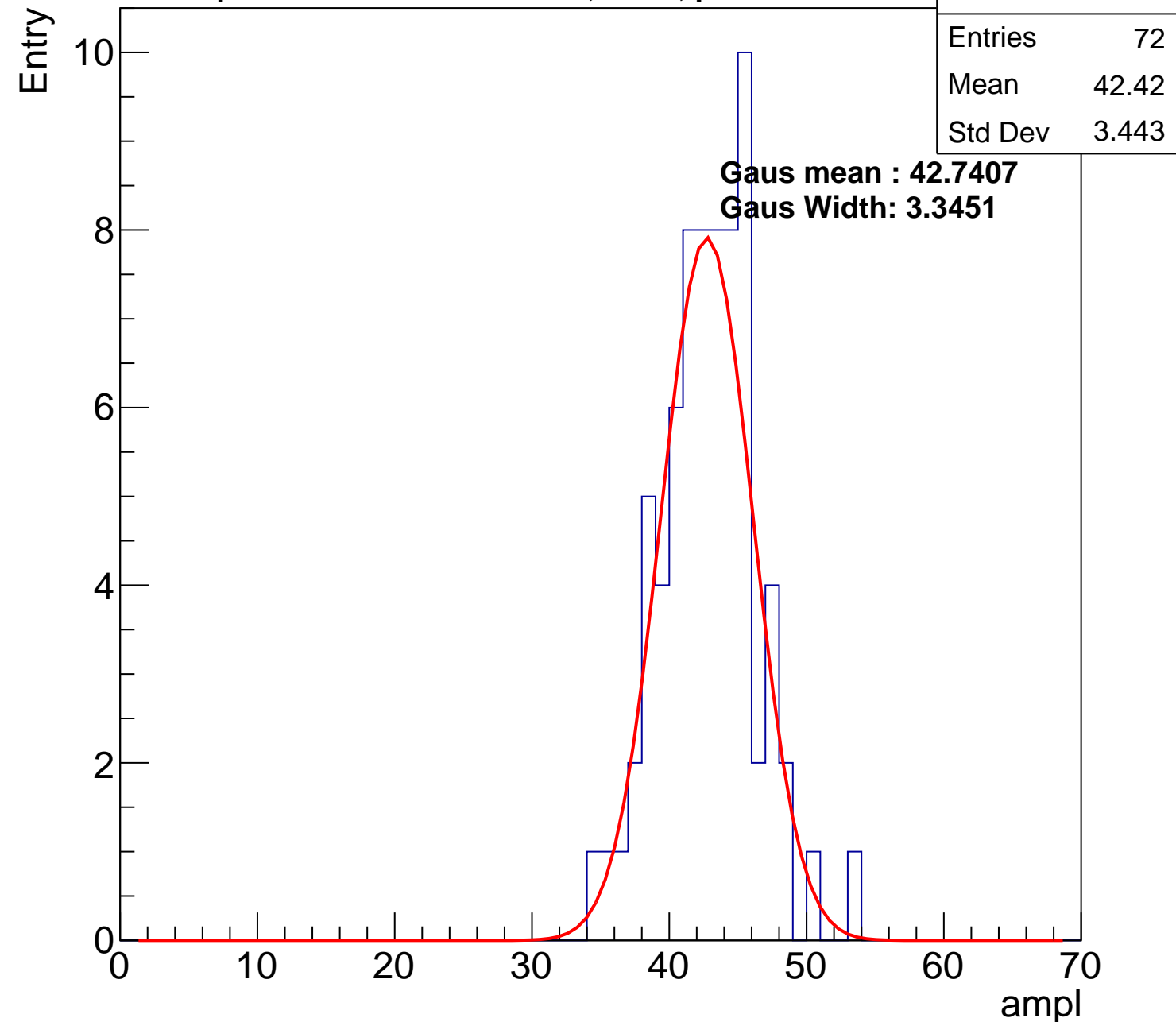
40

50

60

70

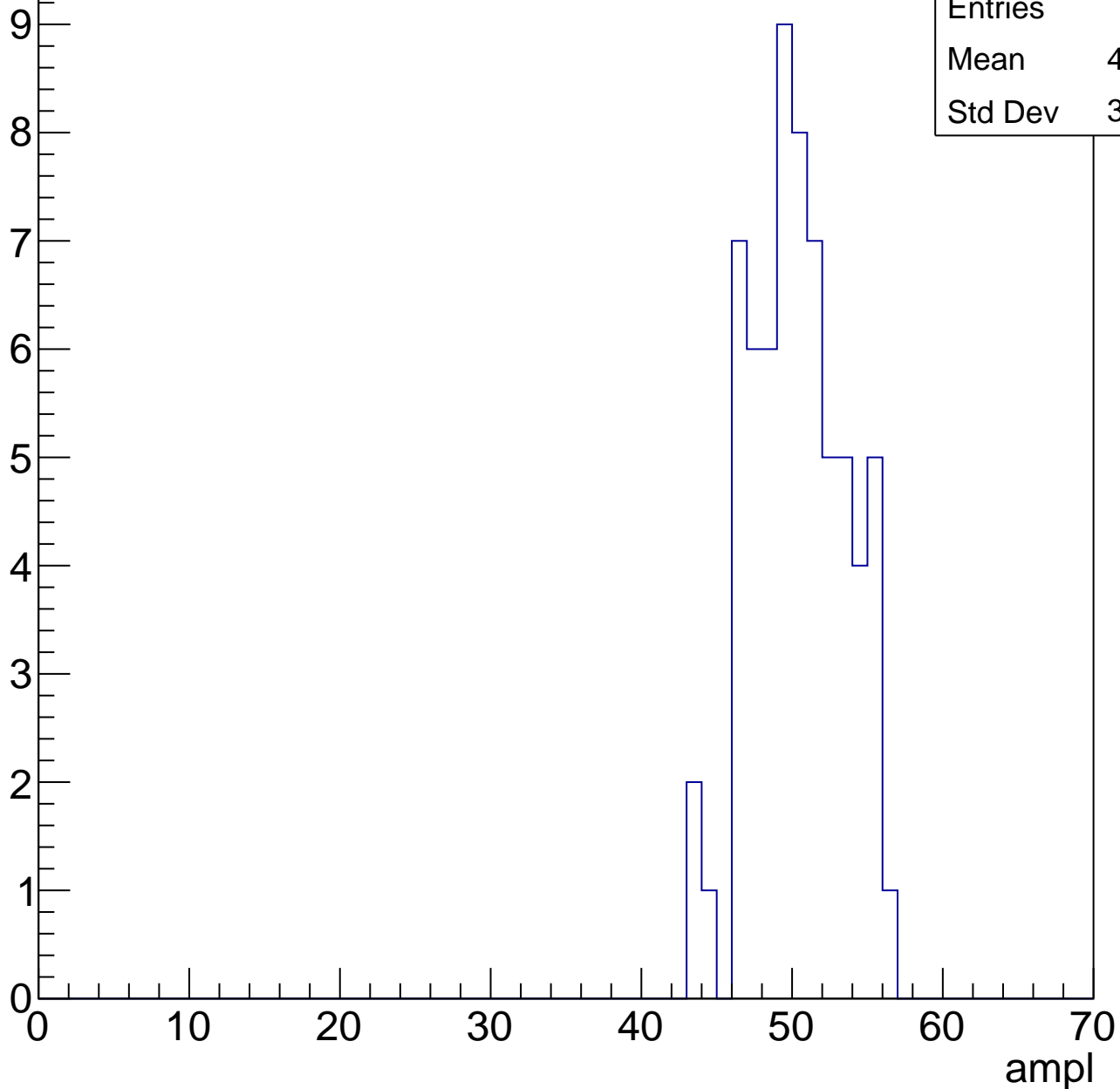
ampl



B1L100S, U15-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



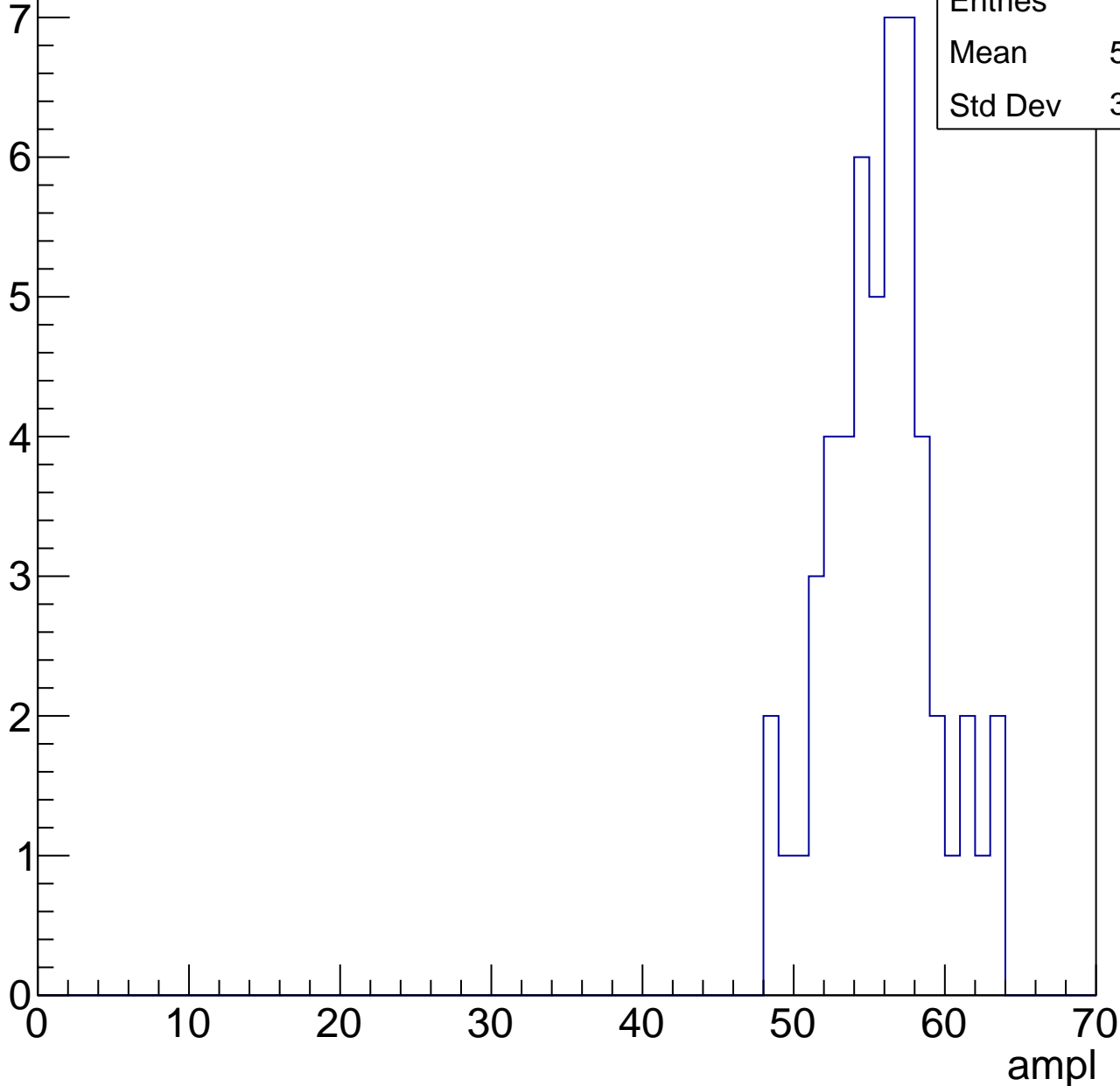
Entries	66
Mean	49.88
Std Dev	3.087

B1L100S, U15-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	55.35
Std Dev	3.497

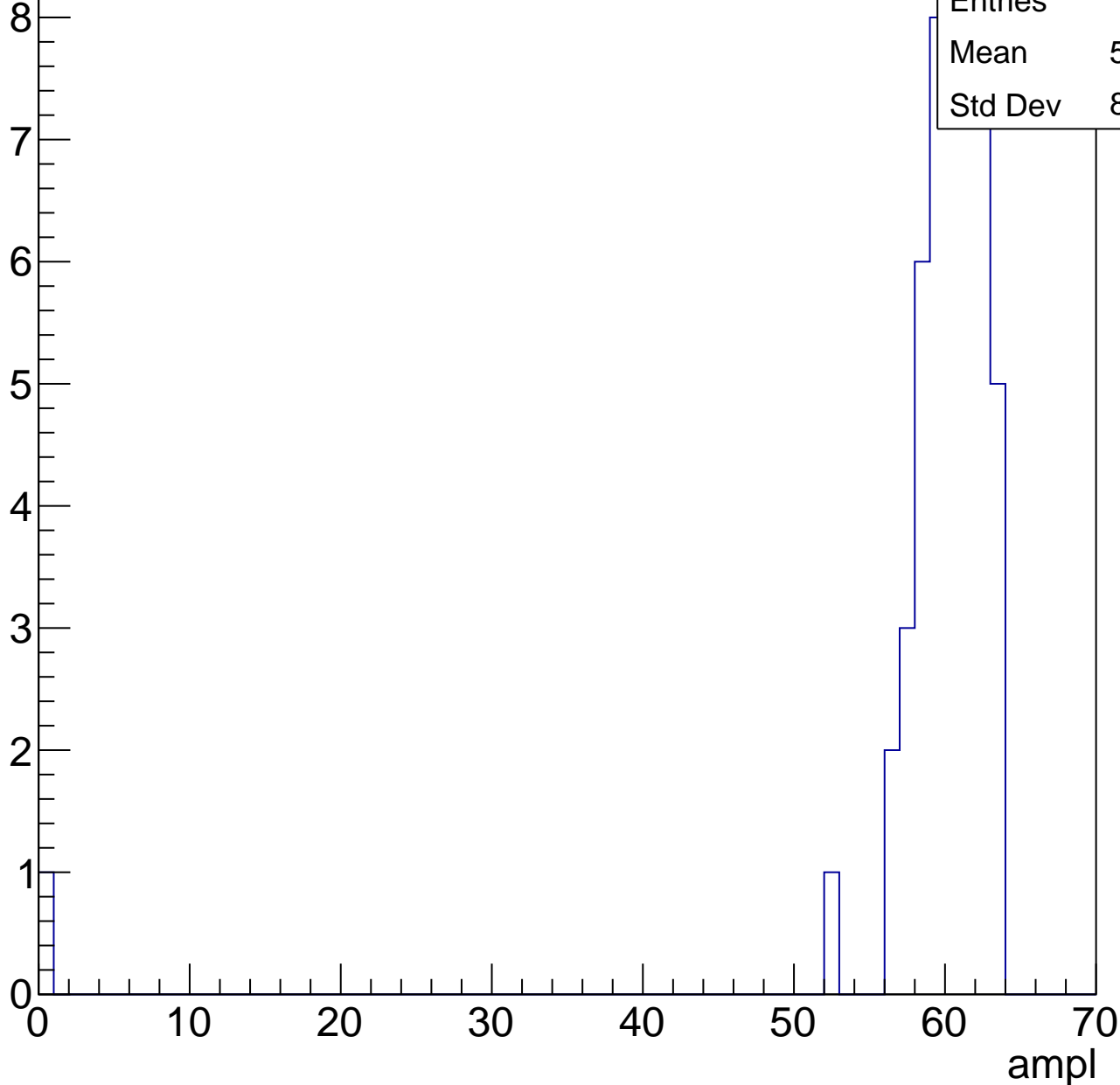


B1L100S, U15-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.68
Std Dev	8.664



B1L100S, U15-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch42, adc0

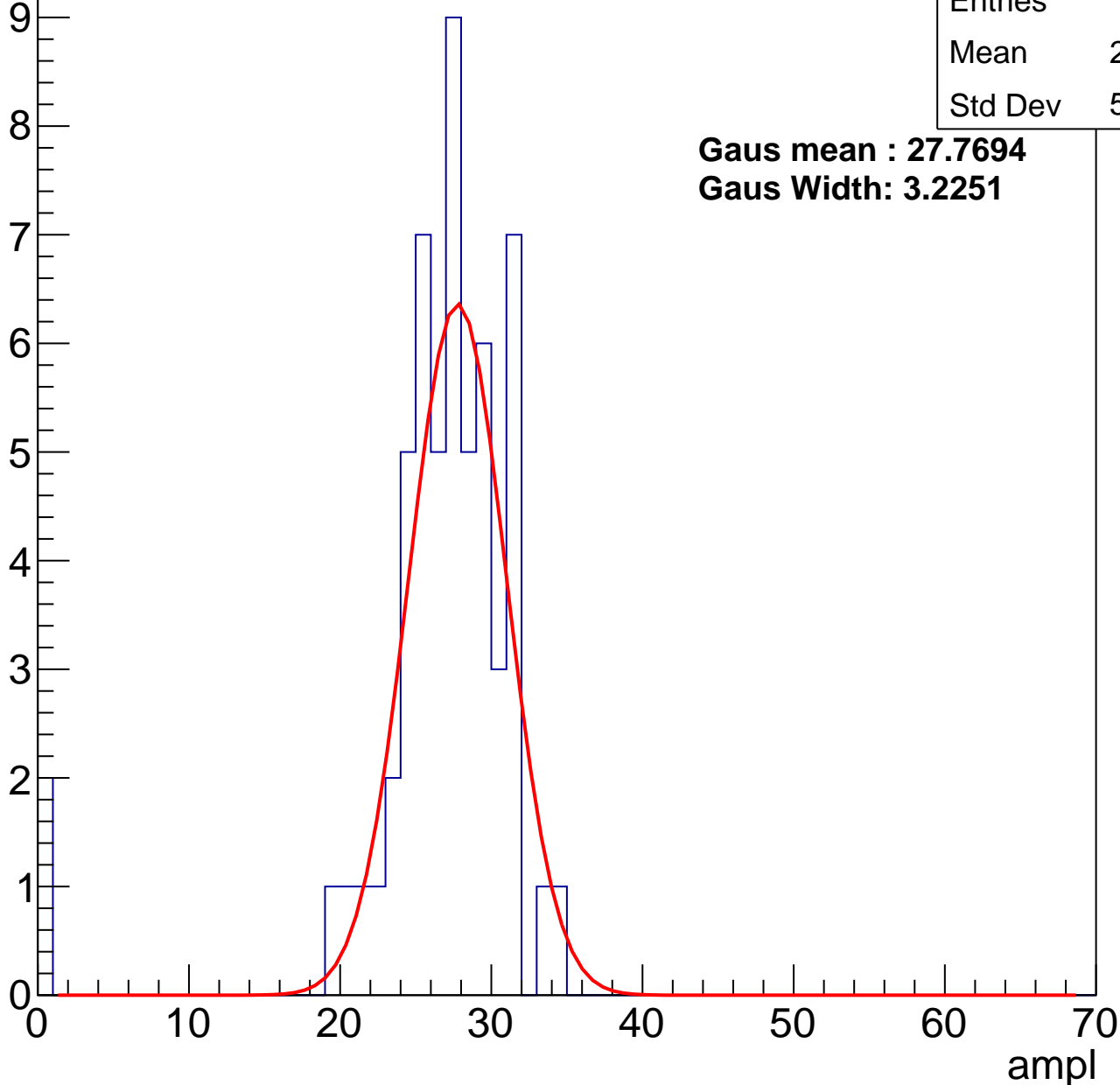
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	26.04
Std Dev	5.837

Gaus mean : 27.7694

Gaus Width: 3.2251



B1L100S, U15-ch42, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	33.53
Std Dev	3.684

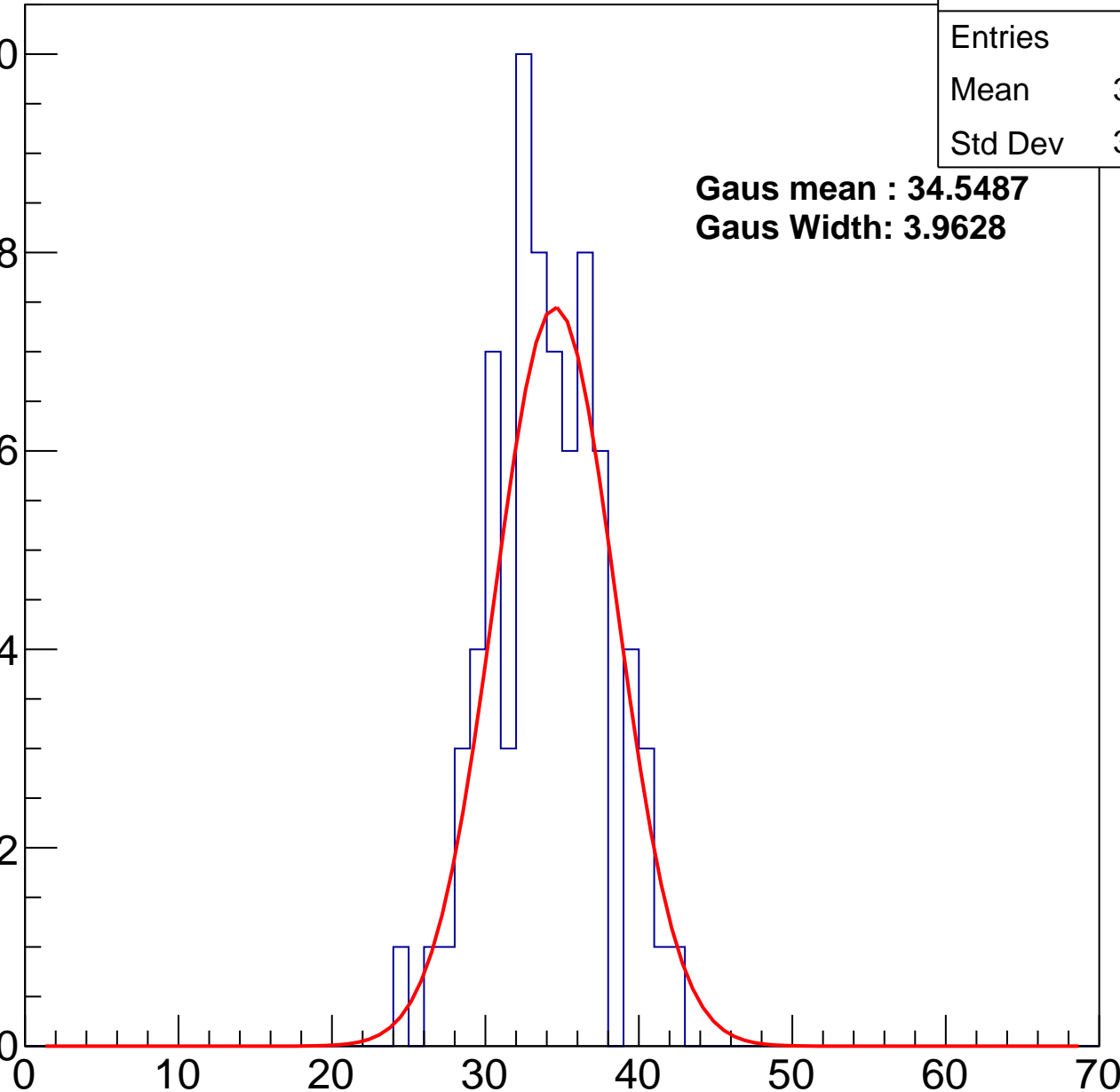
Gaus mean : 34.5487

Gaus Width: 3.9628

Entry

10
8
6
4
2
0

ampl



B1L100S, U15-ch42, adc2

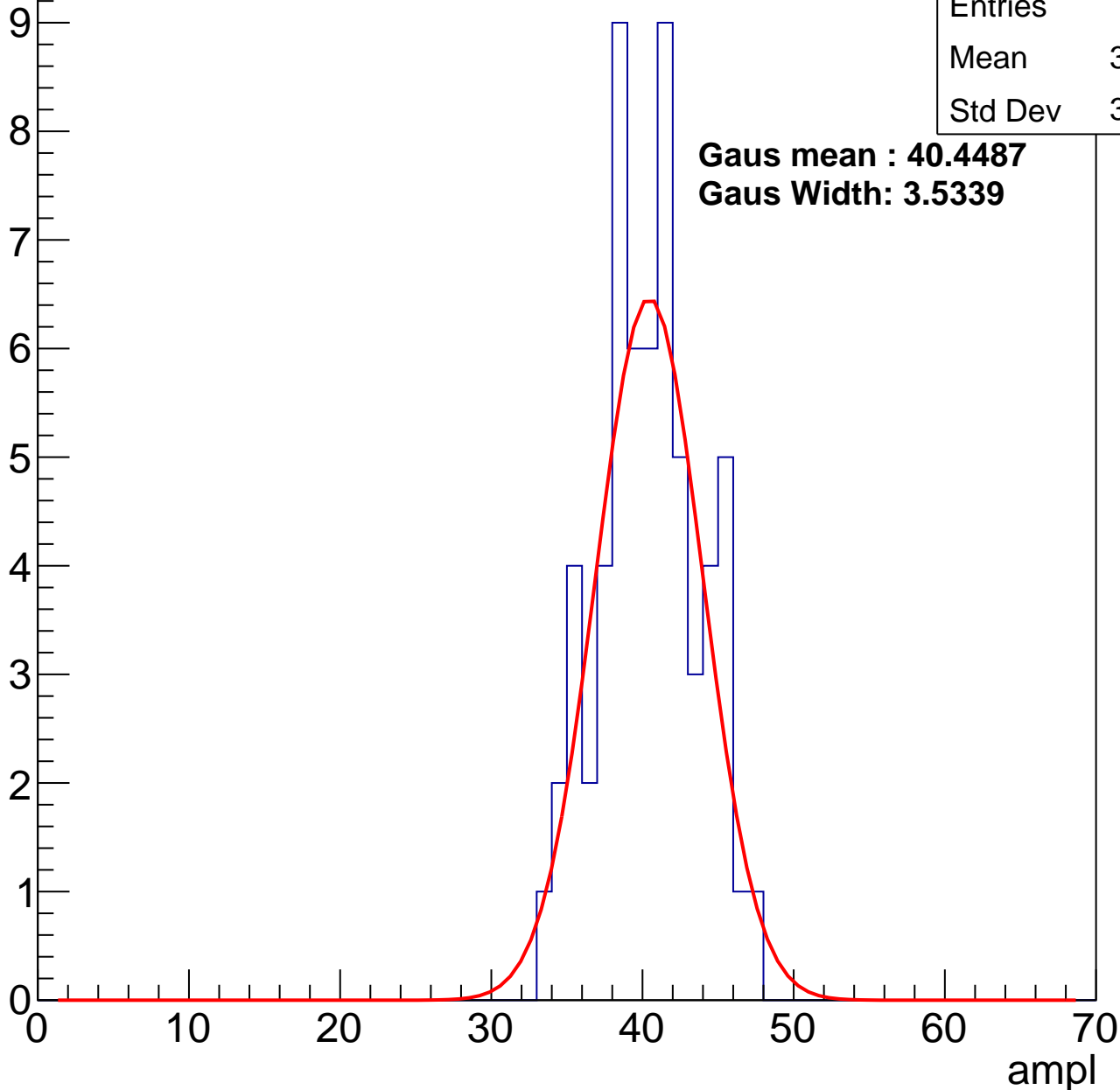
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	39.98
Std Dev	3.265

Gaus mean : 40.4487

Gaus Width: 3.5339

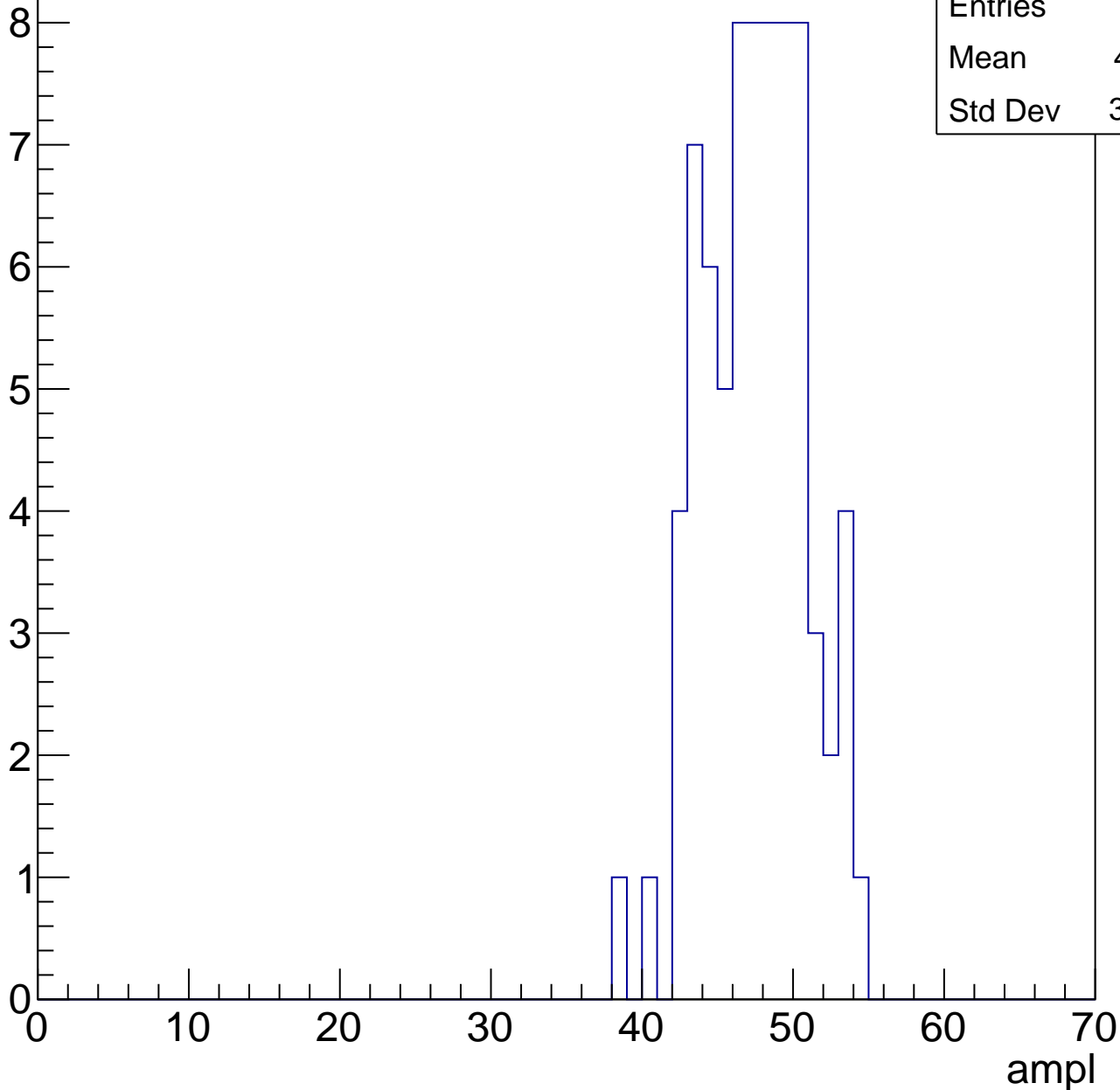


B1L100S, U15-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	47.01
Std Dev	3.343

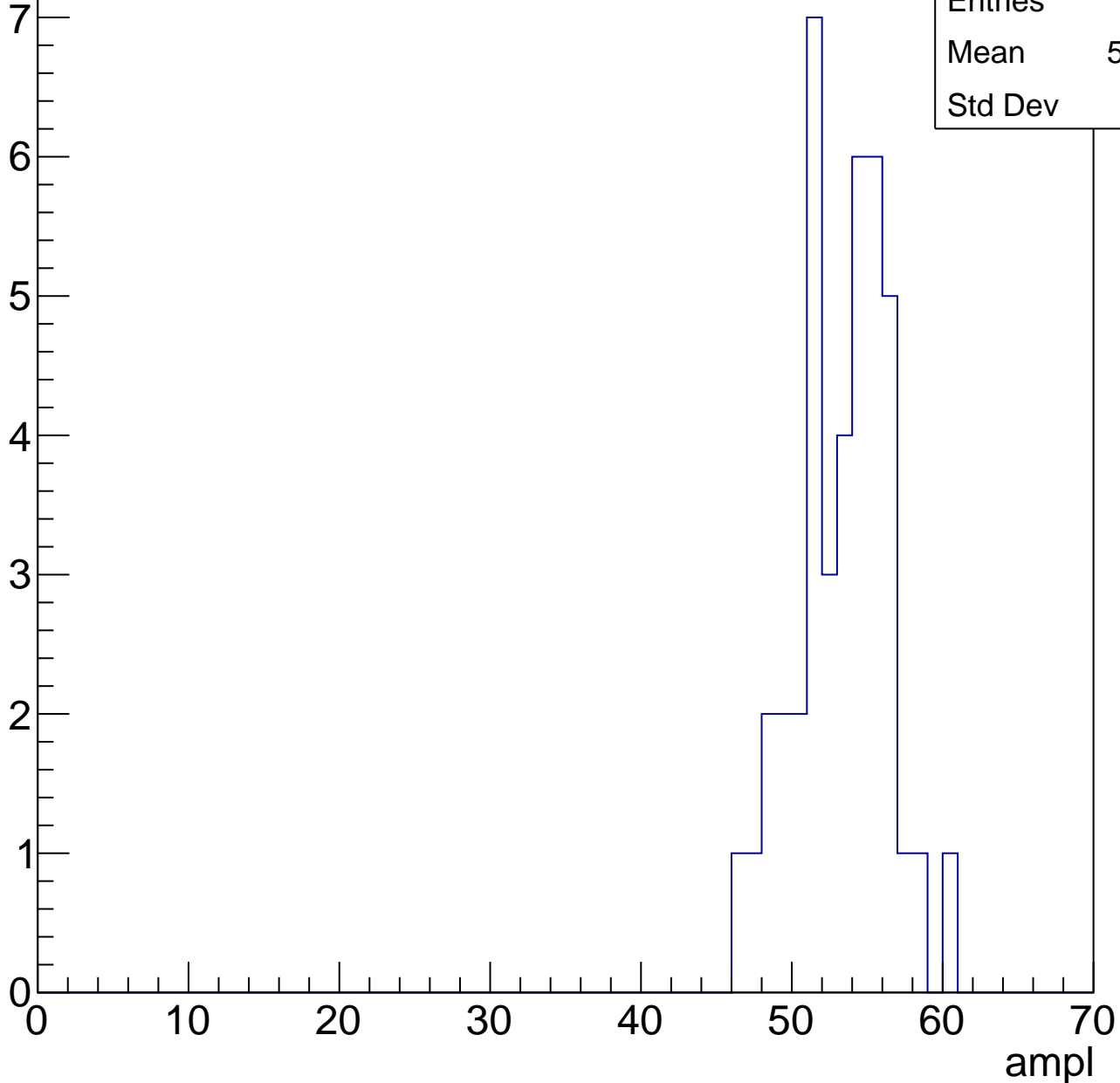


B1L100S, U15-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

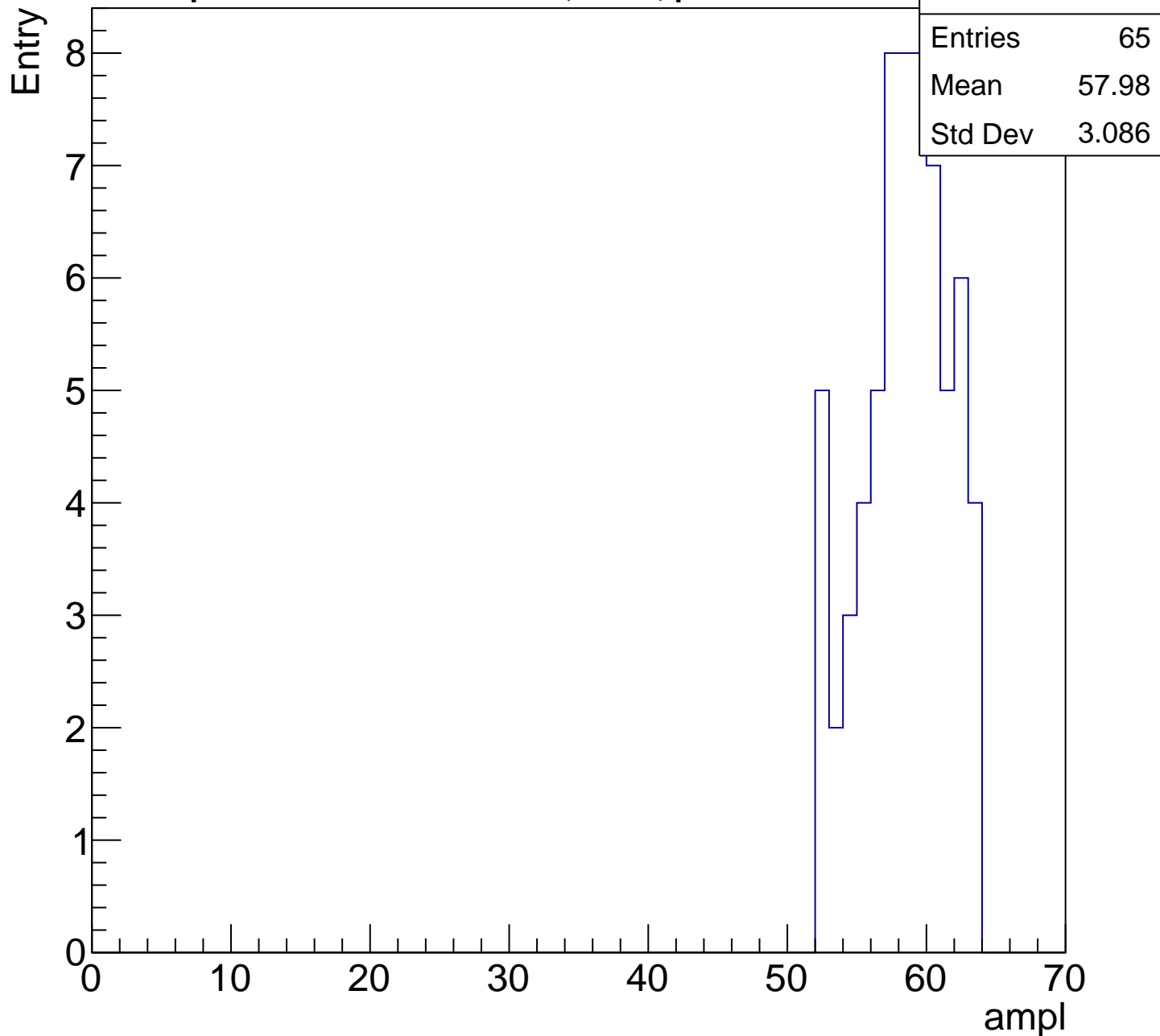
Entry

Entries	42
Mean	52.88
Std Dev	3.01



B1L100S, U15-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

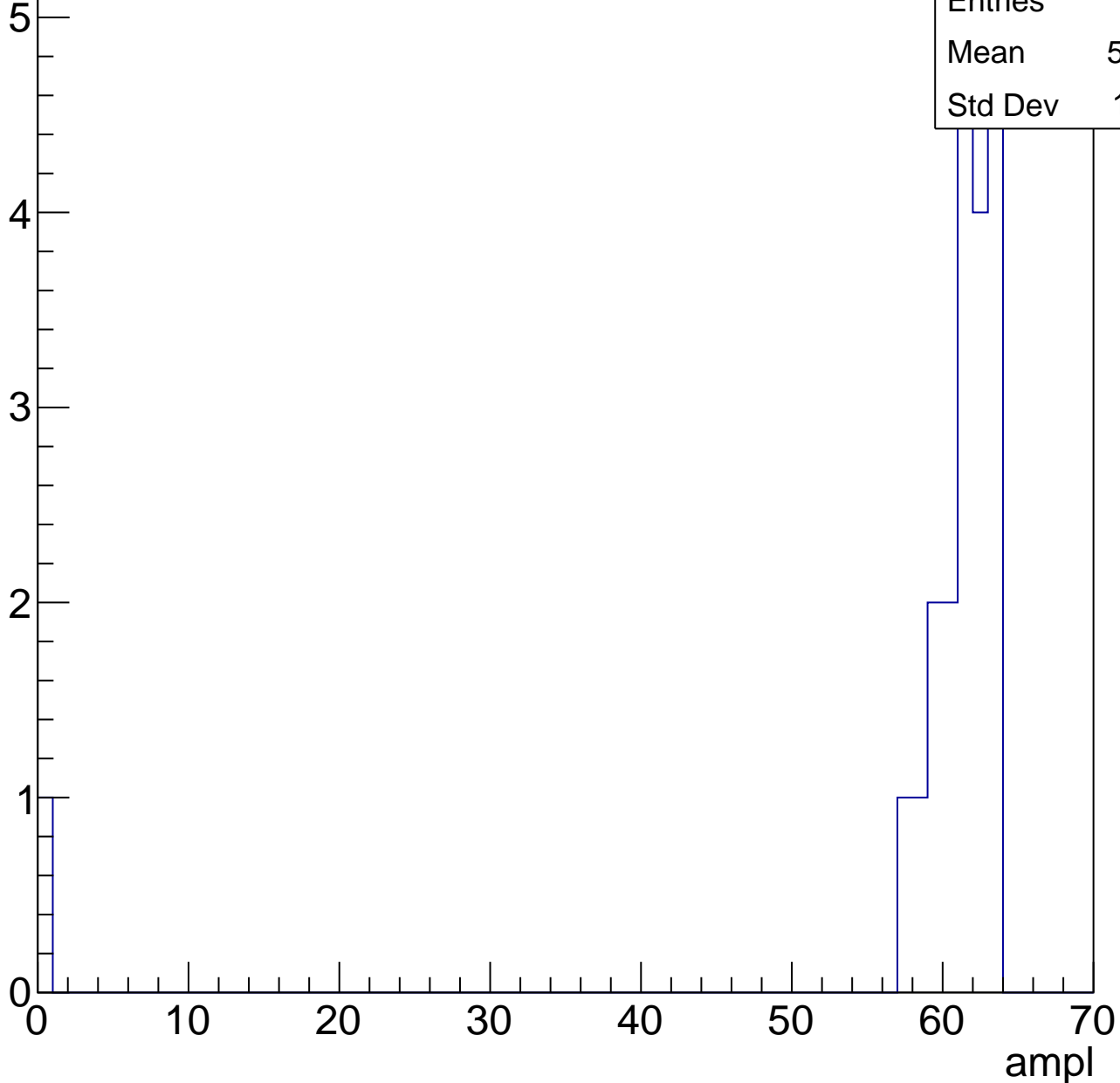


B1L100S, U15-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.14
Std Dev	13.11



B1L100S, U15-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U15-ch43, adc0

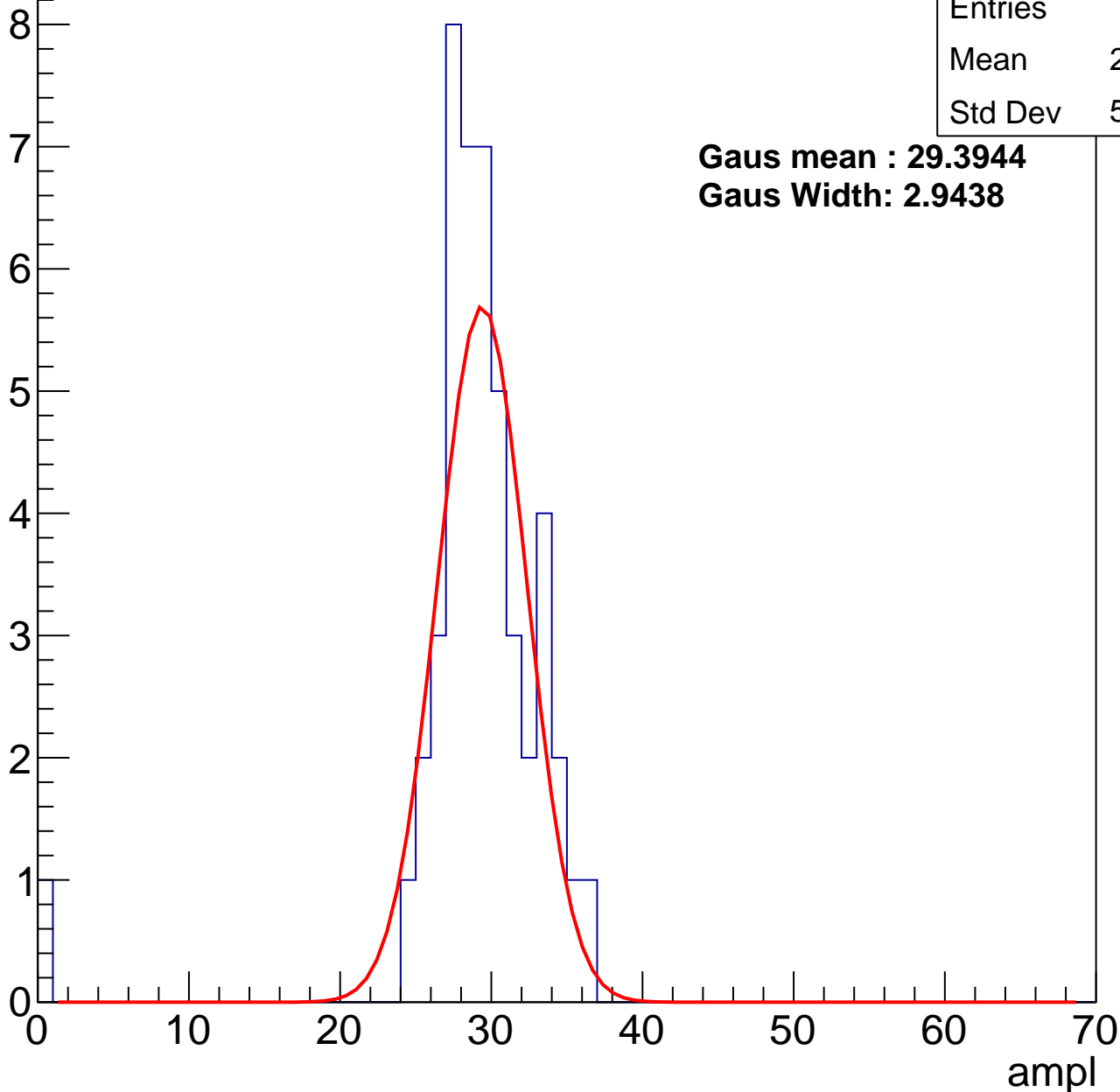
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	28.62
Std Dev	5.034

Gaus mean : 29.3944

Gaus Width: 2.9438



B1L100S, U15-ch43, adc1

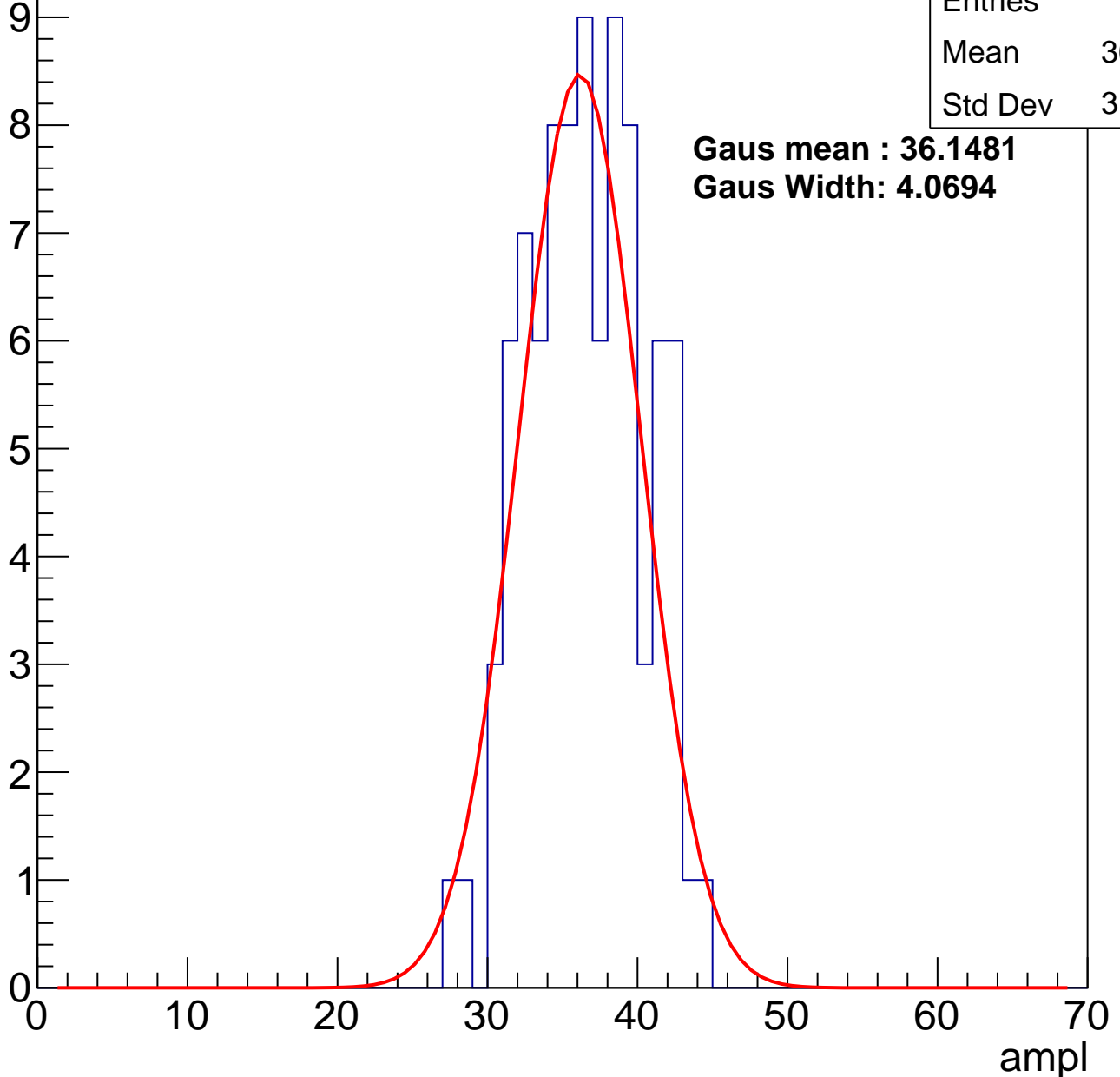
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	36.07
Std Dev	3.747

Gaus mean : 36.1481

Gaus Width: 4.0694



B1L100S, U15-ch43, adc2

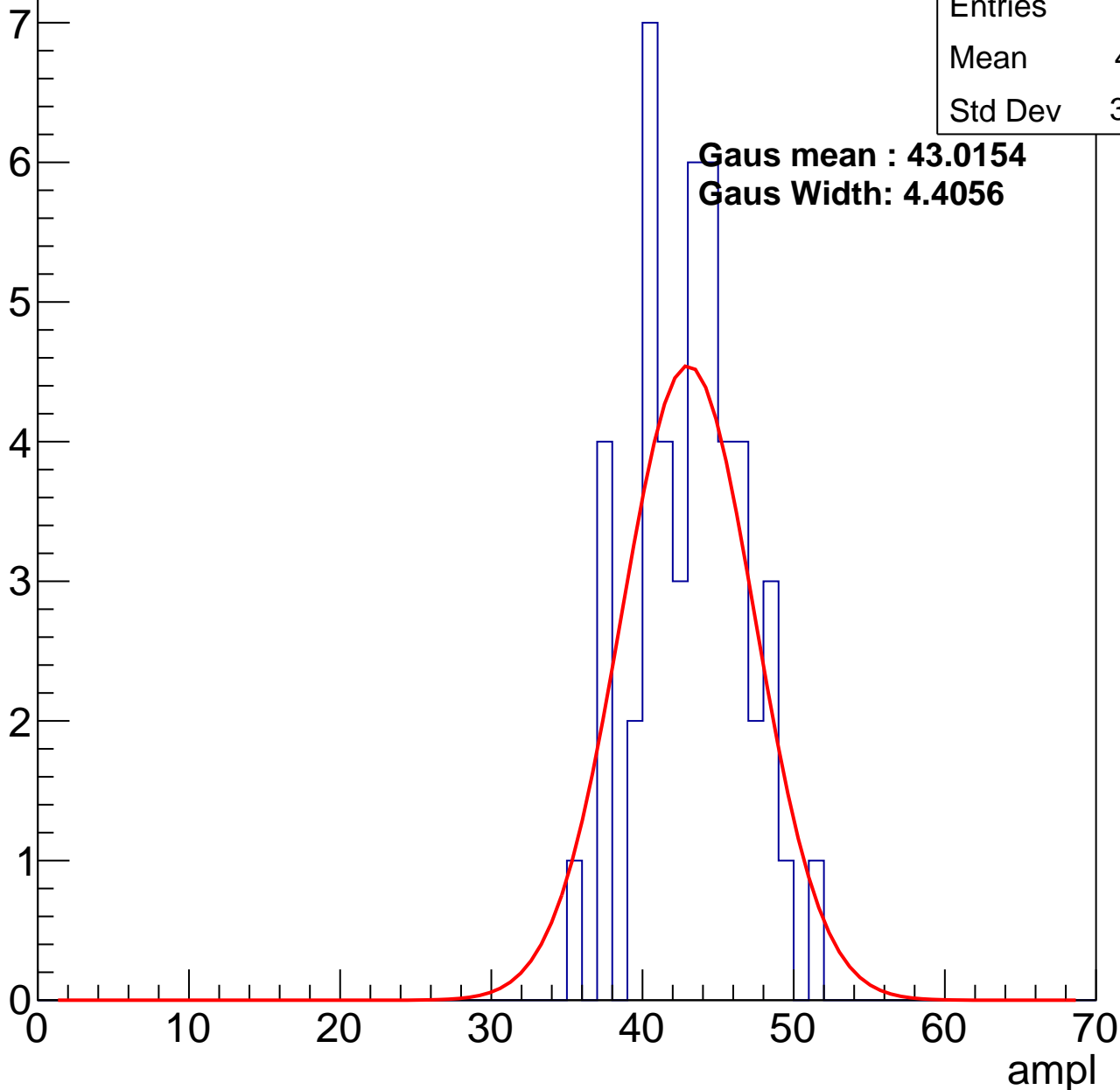
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	42.81
Std Dev	3.492

Gaus mean : 43.0154

Gaus Width: 4.4056



B1L100S, U15-ch43, adc3

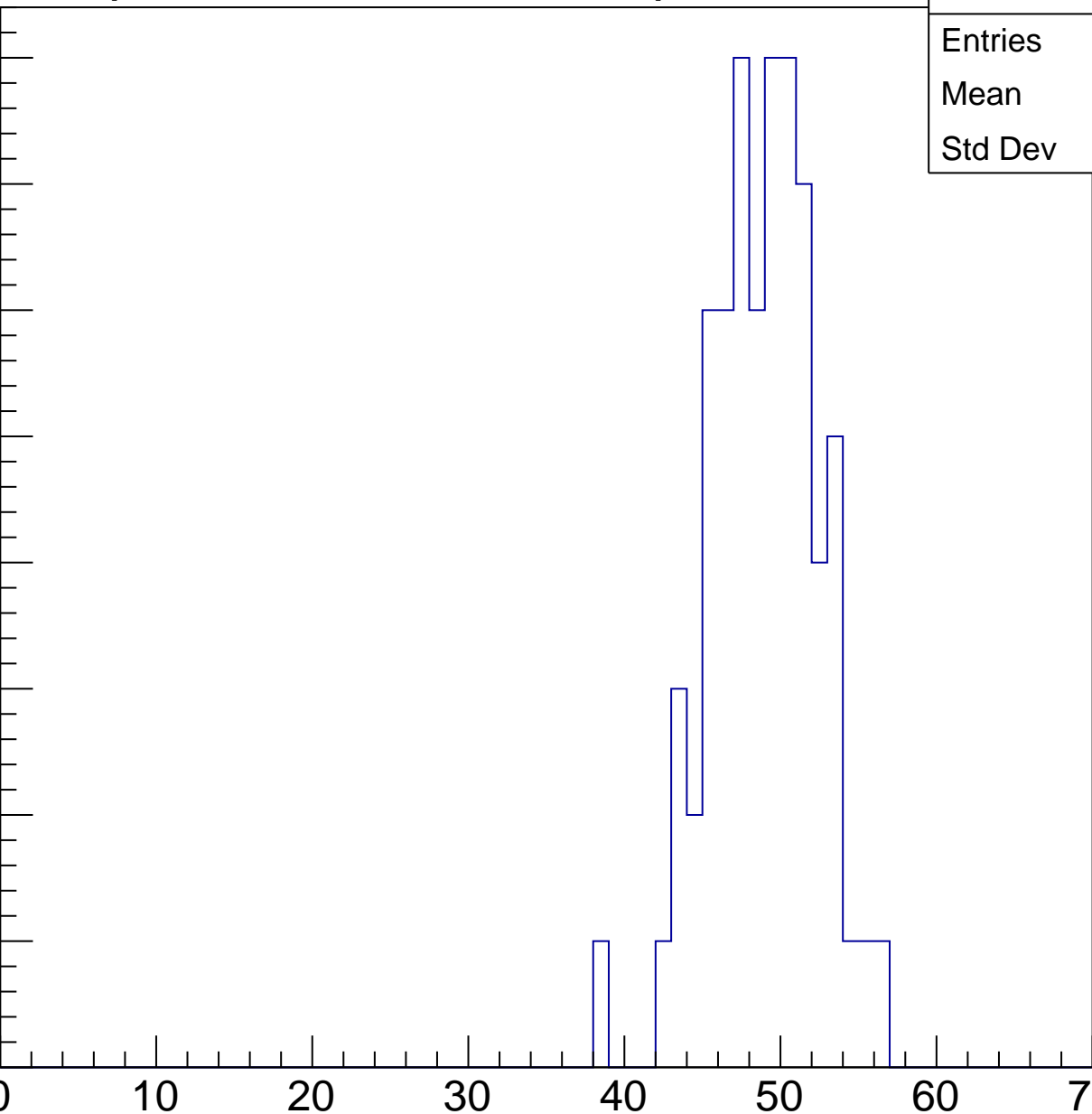
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	68
Mean	48.44
Std Dev	3.336

ampl

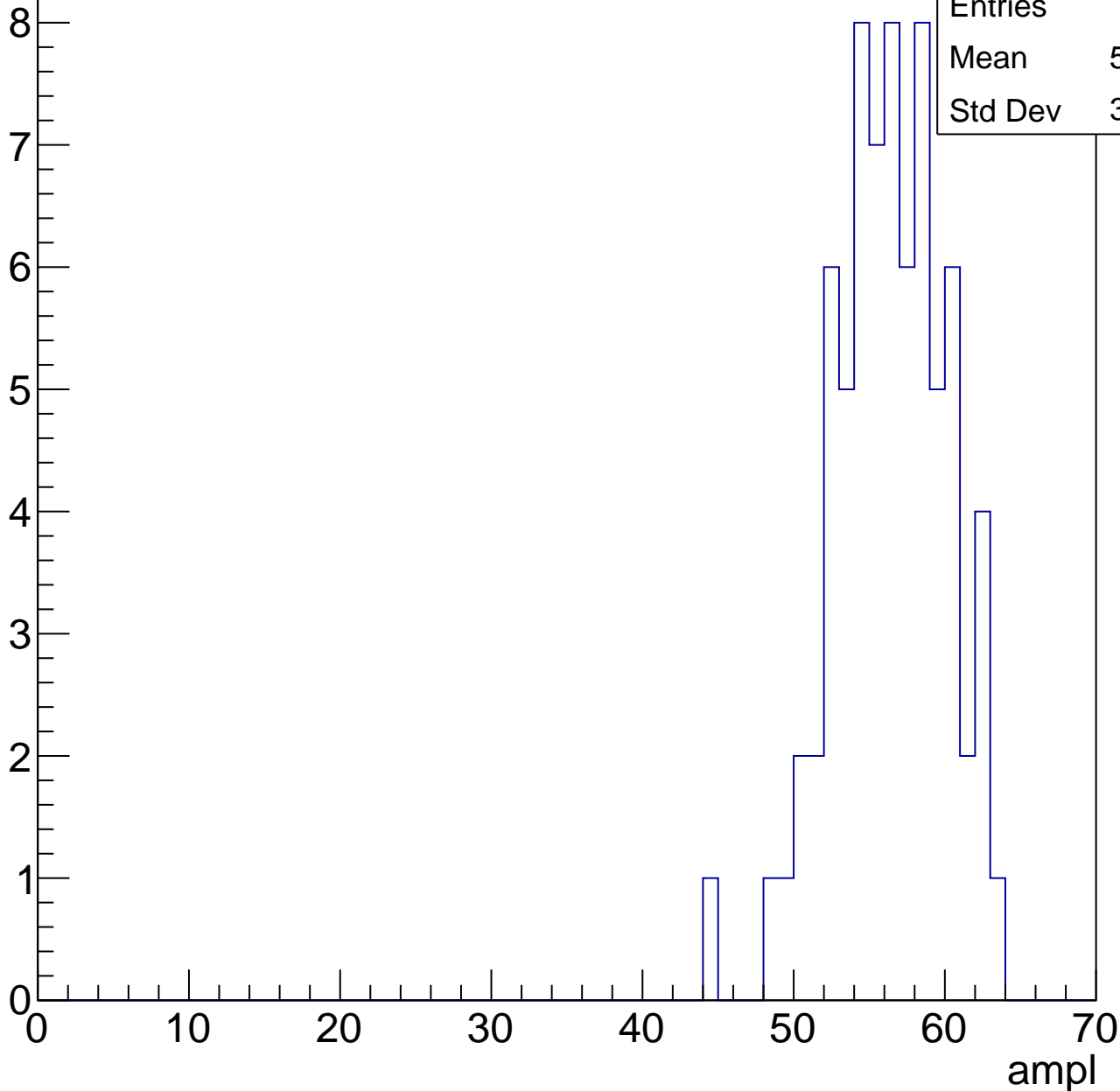


B1L100S, U15-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	55.88
Std Dev	3.664

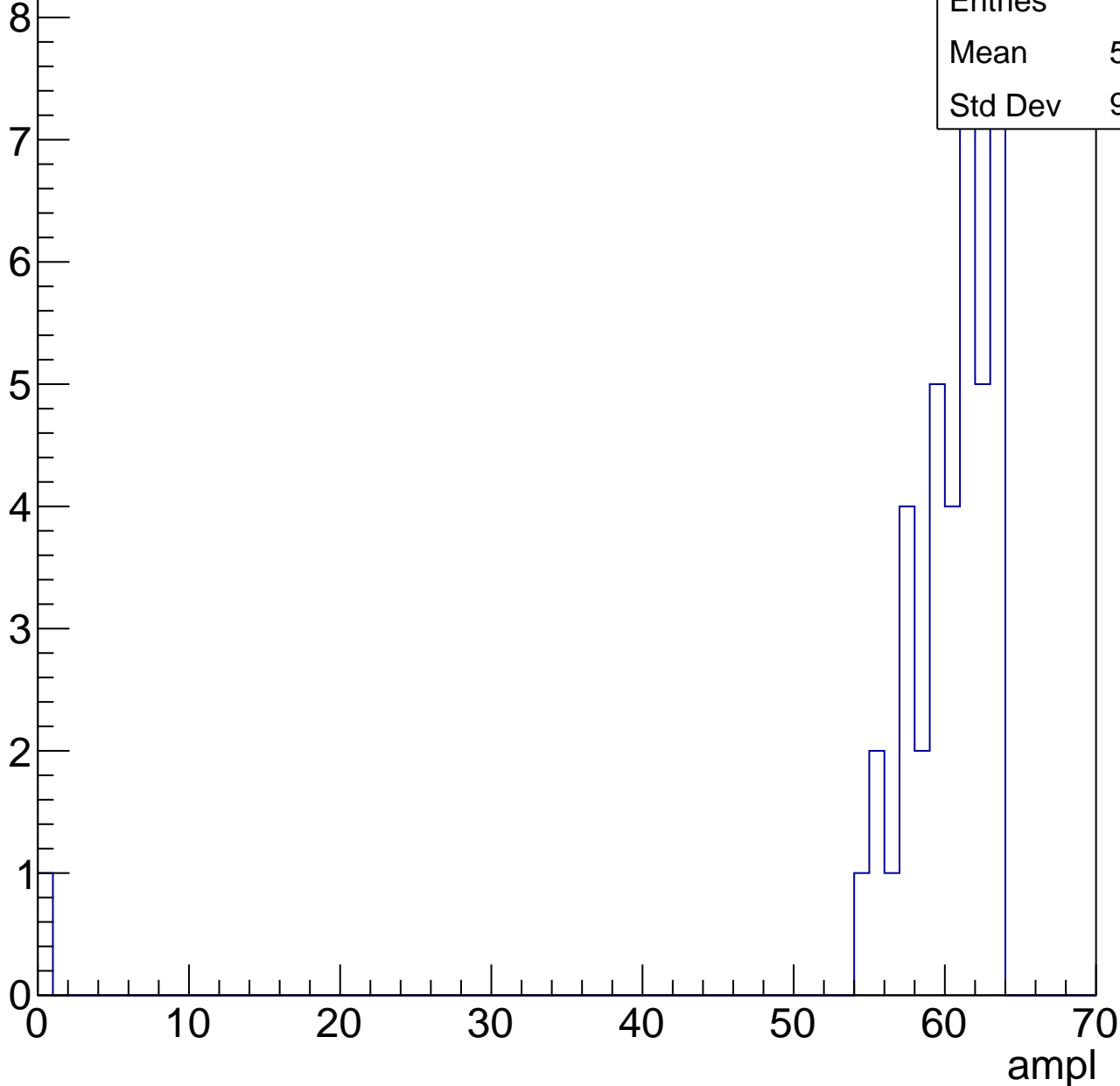


B1L100S, U15-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	58.56
Std Dev	9.584



B1L100S, U15-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch44, adc0

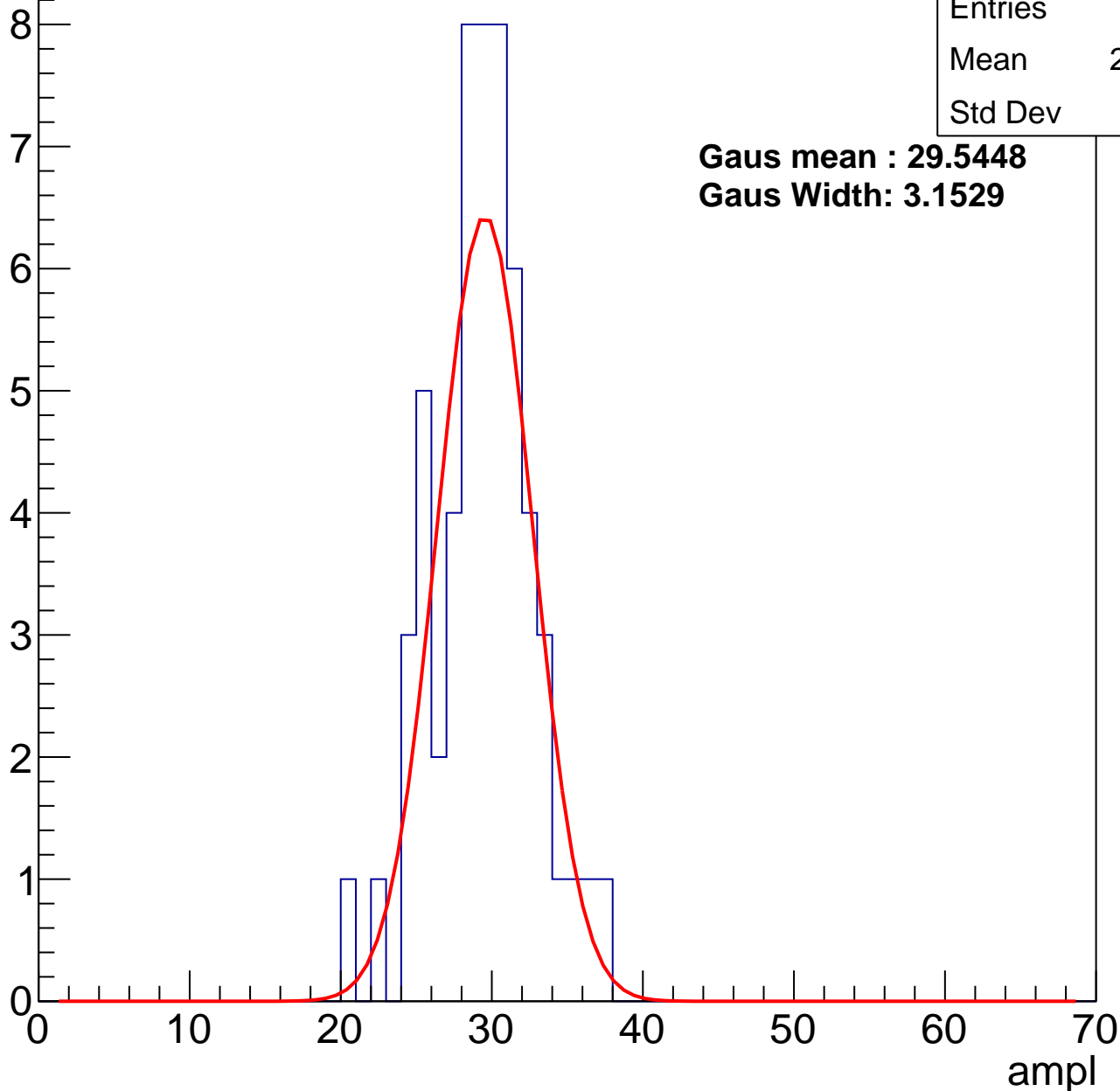
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	28.95
Std Dev	3.29

Gaus mean : 29.5448

Gaus Width: 3.1529



B1L100S, U15-ch44, adc1

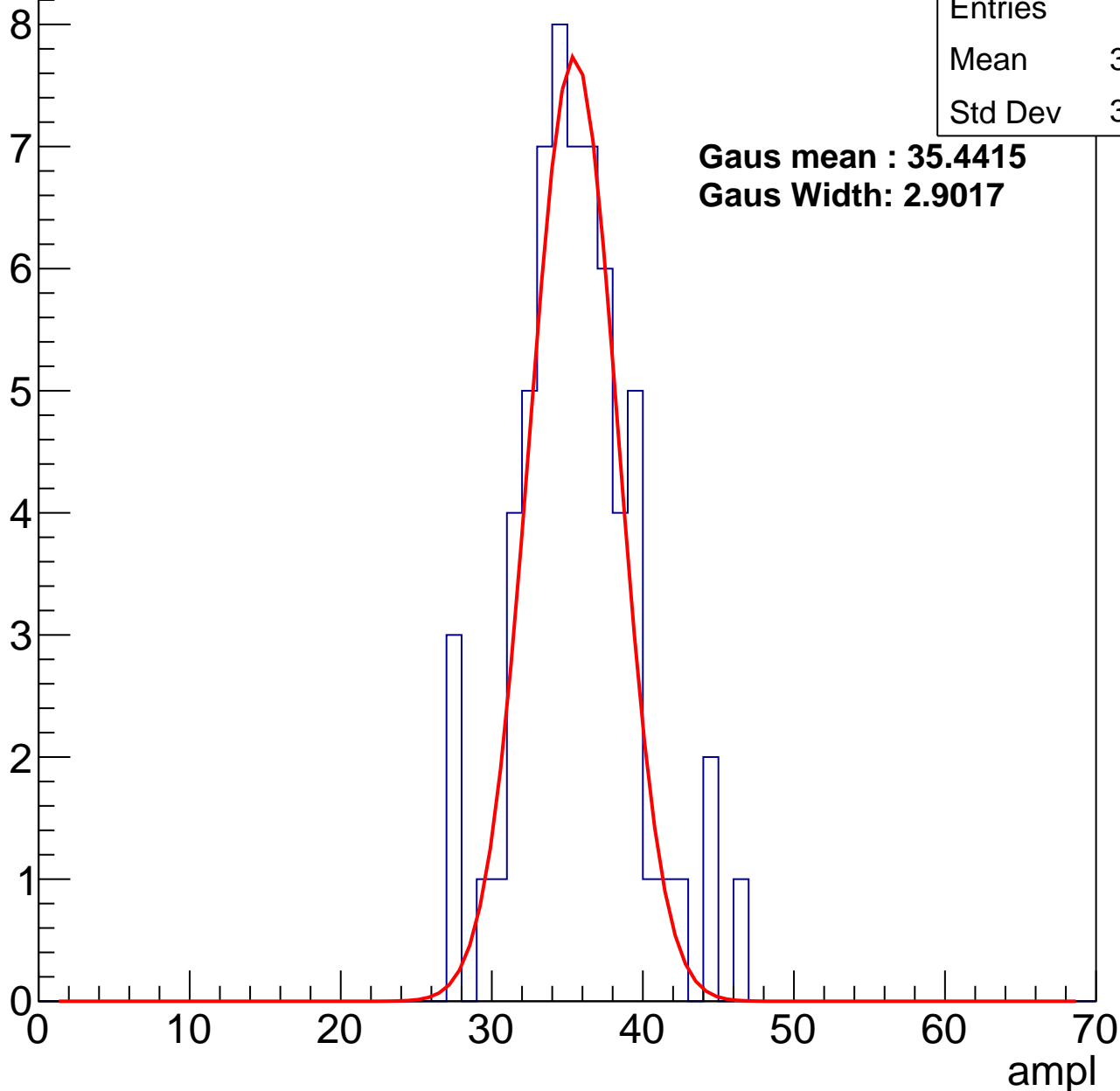
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	35.16
Std Dev	3.825

Gaus mean : 35.4415

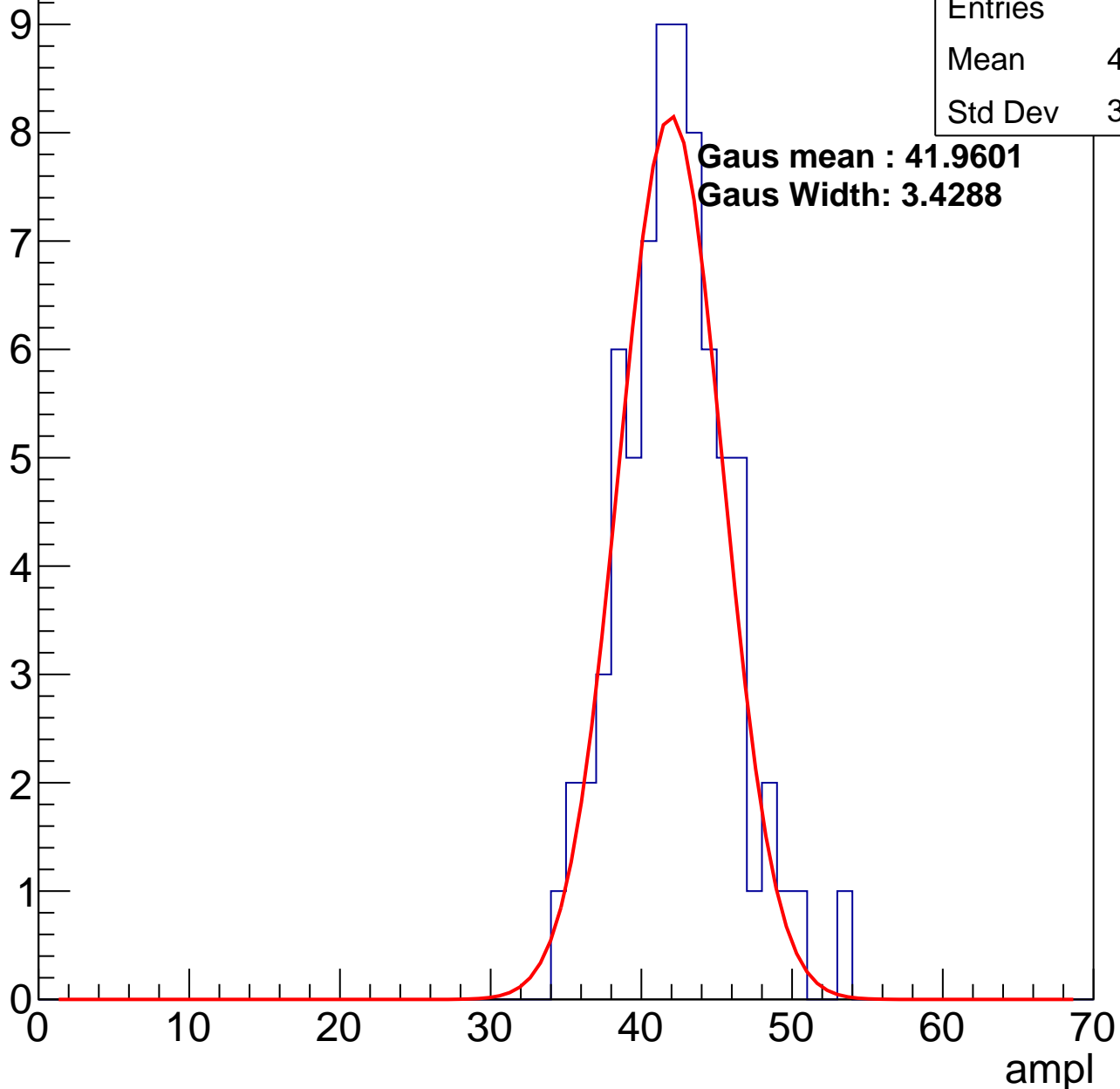
Gaus Width: 2.9017



B1L100S, U15-ch44, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	74
Mean	41.82
Std Dev	3.626

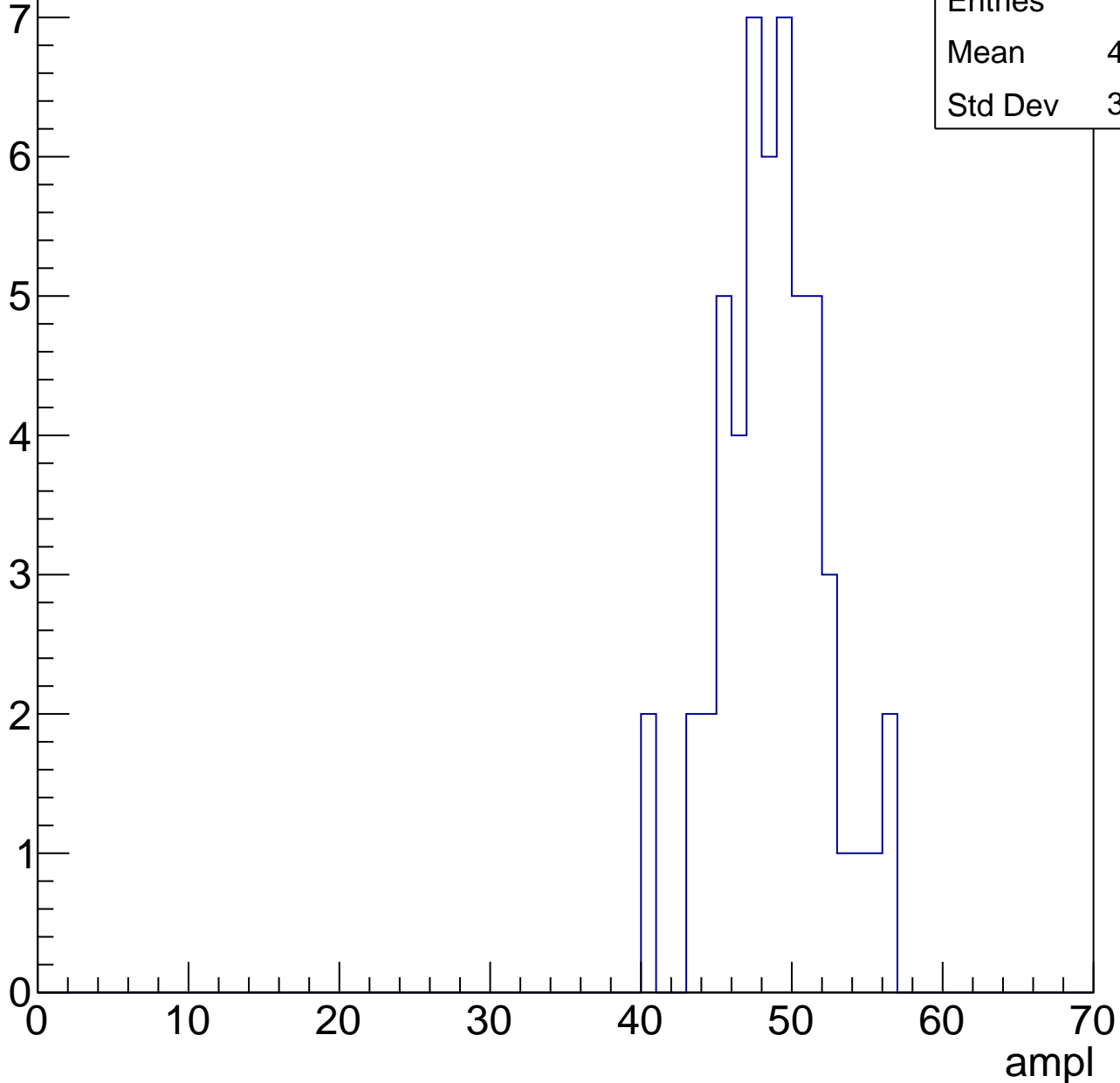
Gaus mean : 41.9601
Gaus Width: 3.4288

B1L100S, U15-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	48.26
Std Dev	3.454

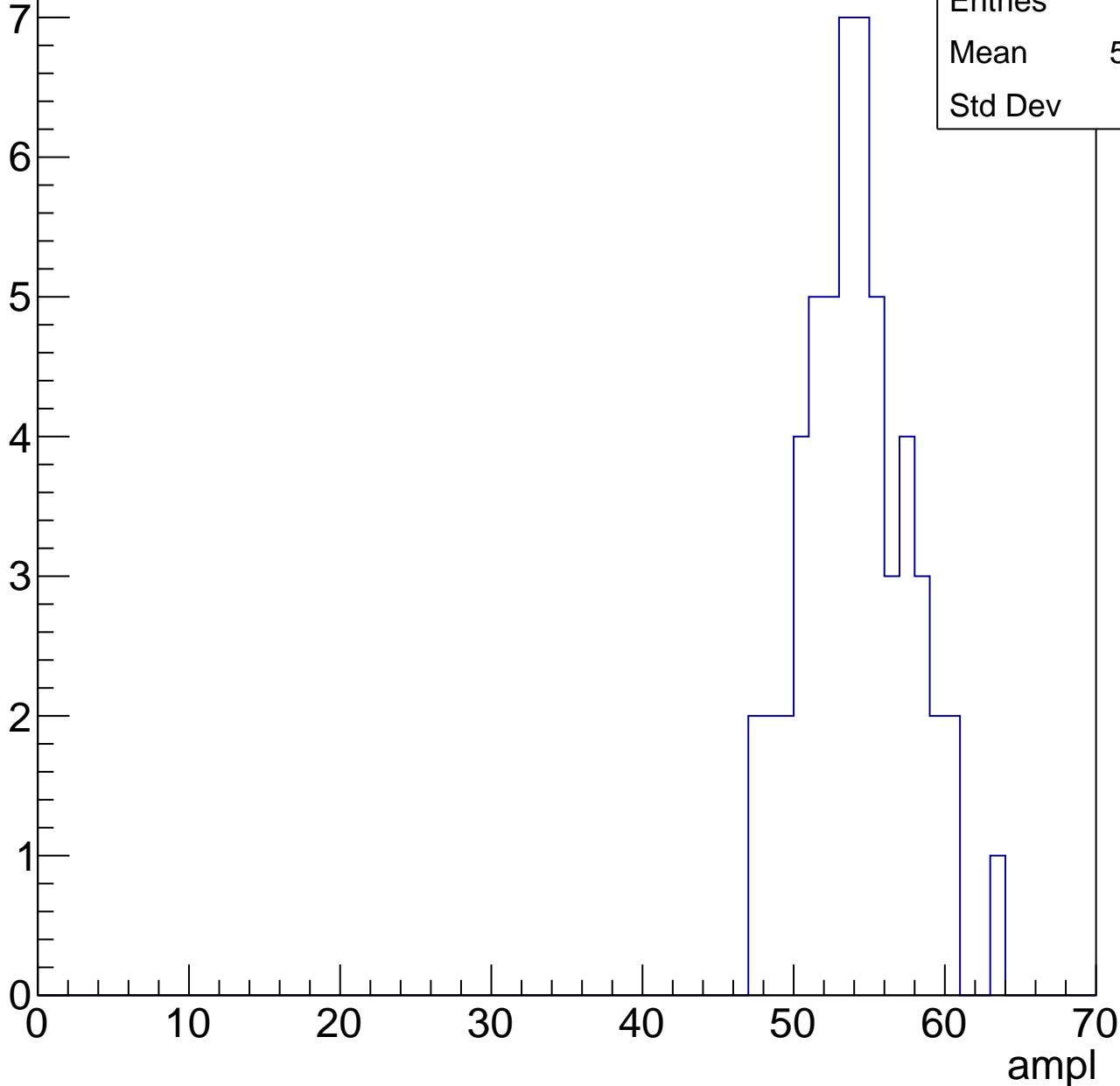


B1L100S, U15-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	53.67
Std Dev	3.48



B1L100S, U15-ch44, adc5

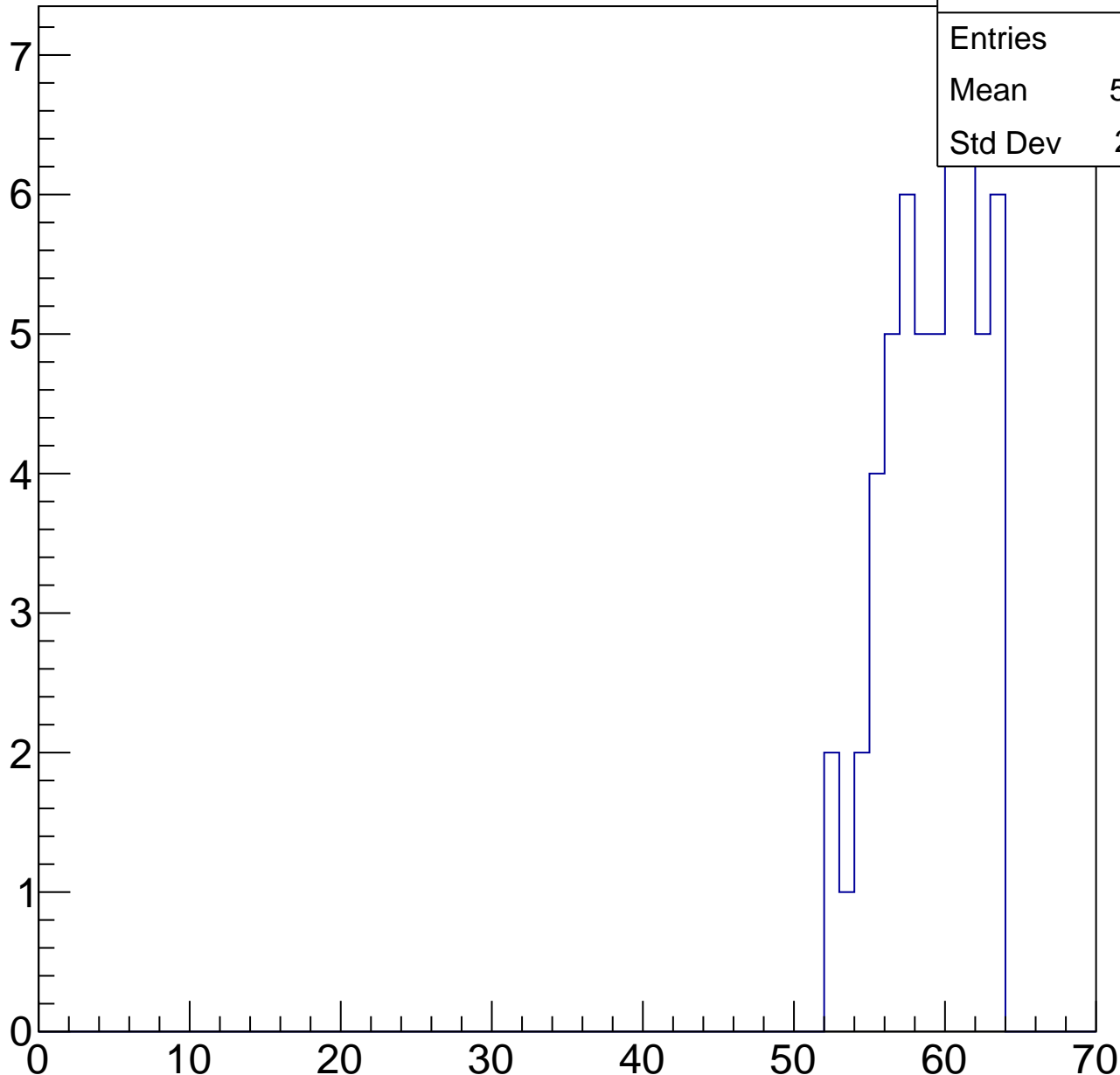
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	55
Mean	58.67
Std Dev	2.991

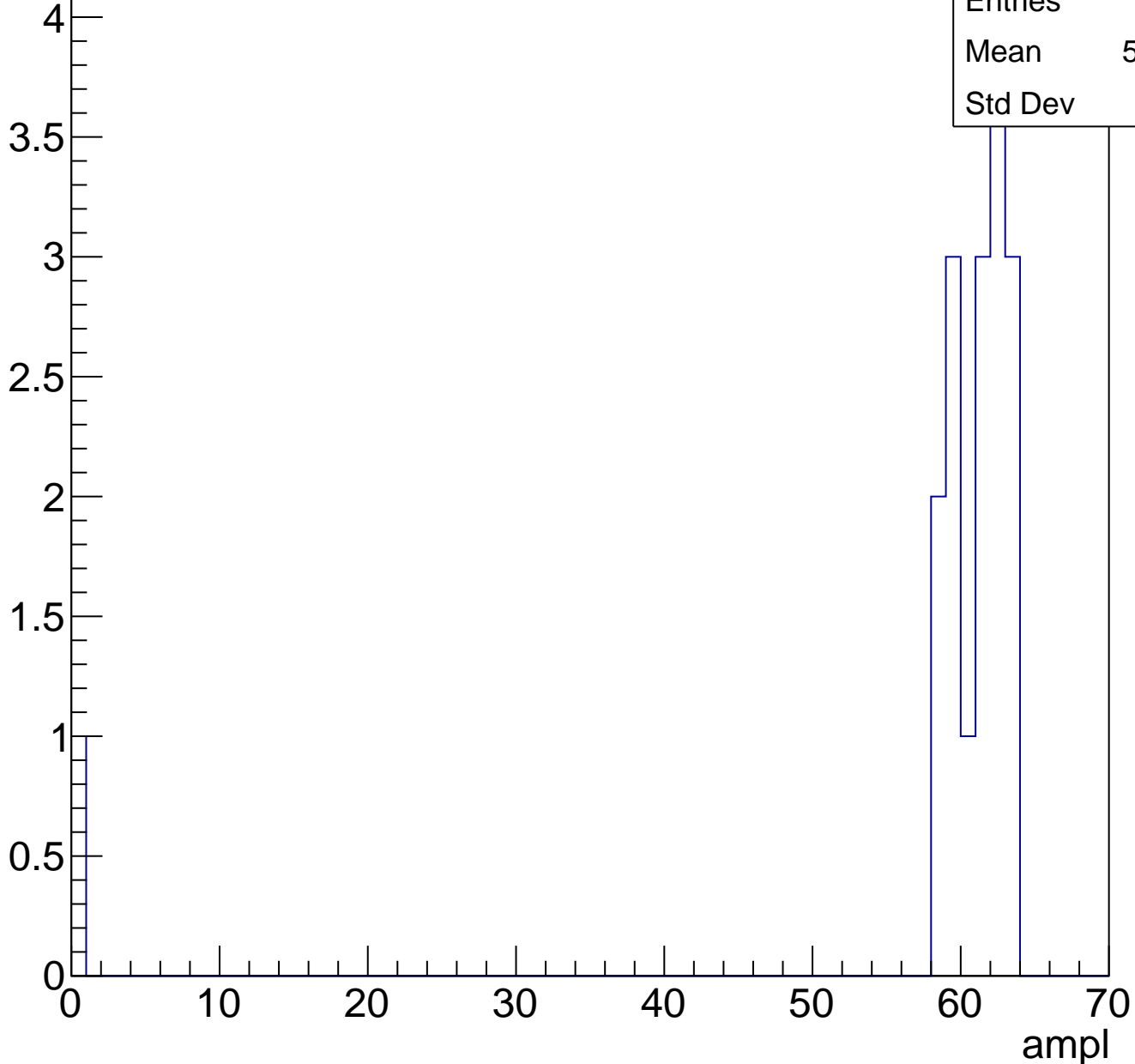
ampl



B1L100S, U15-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch45, adc0

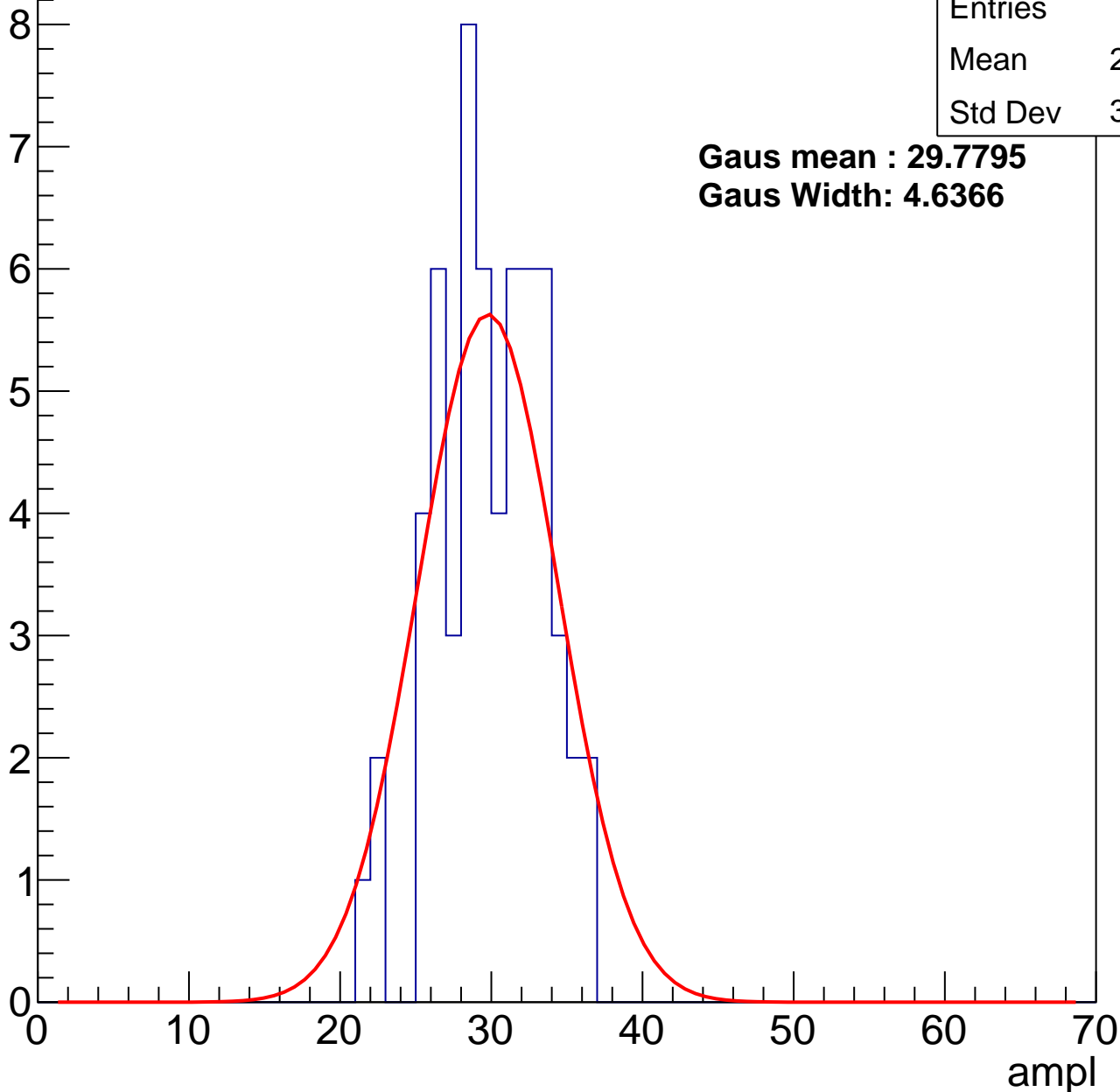
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.49
Std Dev	3.466

Gaus mean : 29.7795

Gaus Width: 4.6366



B1L100S, U15-ch45, adc1

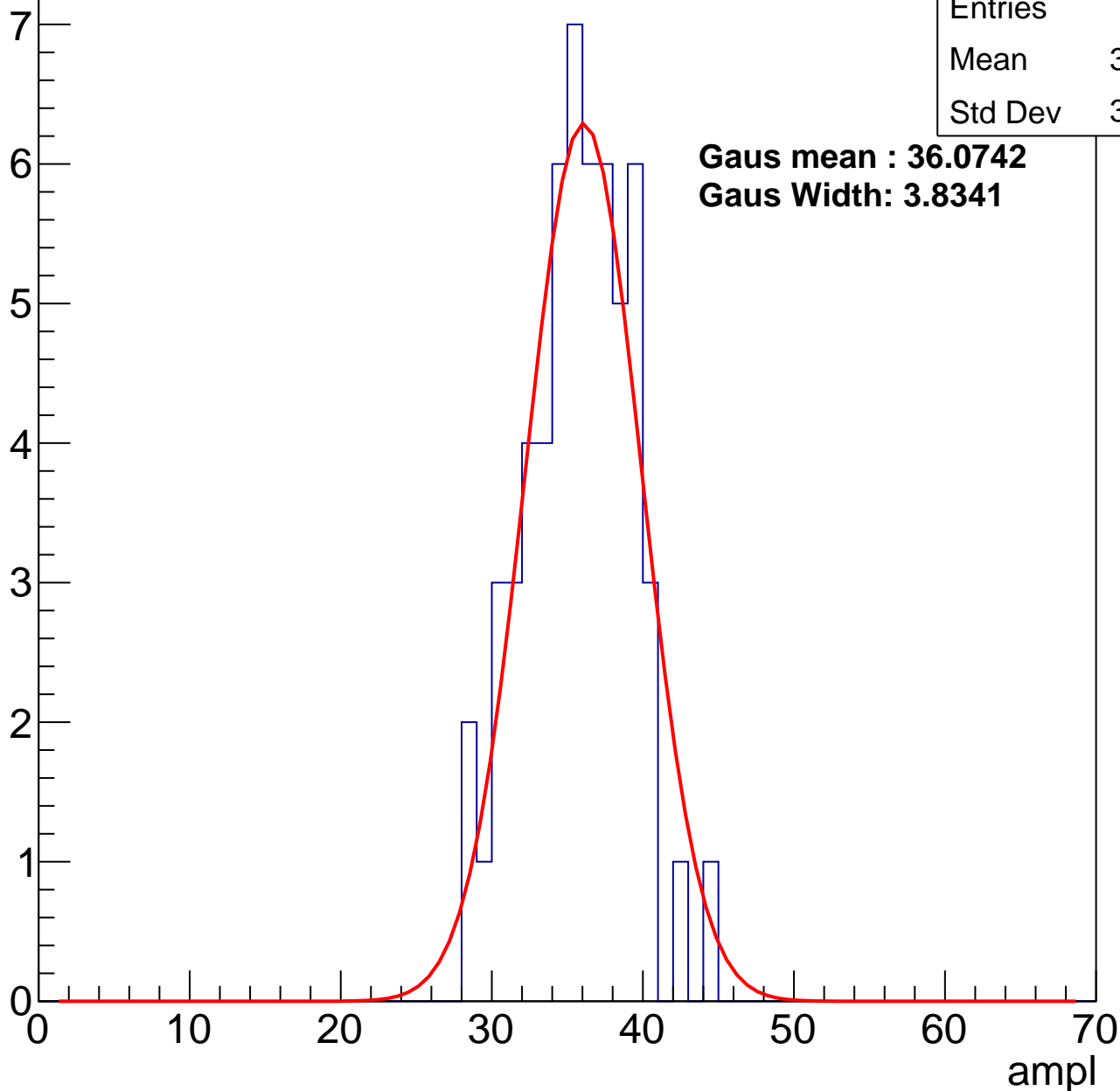
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	35.26
Std Dev	3.442

Gaus mean : 36.0742

Gaus Width: 3.8341



B1L100S, U15-ch45, adc2

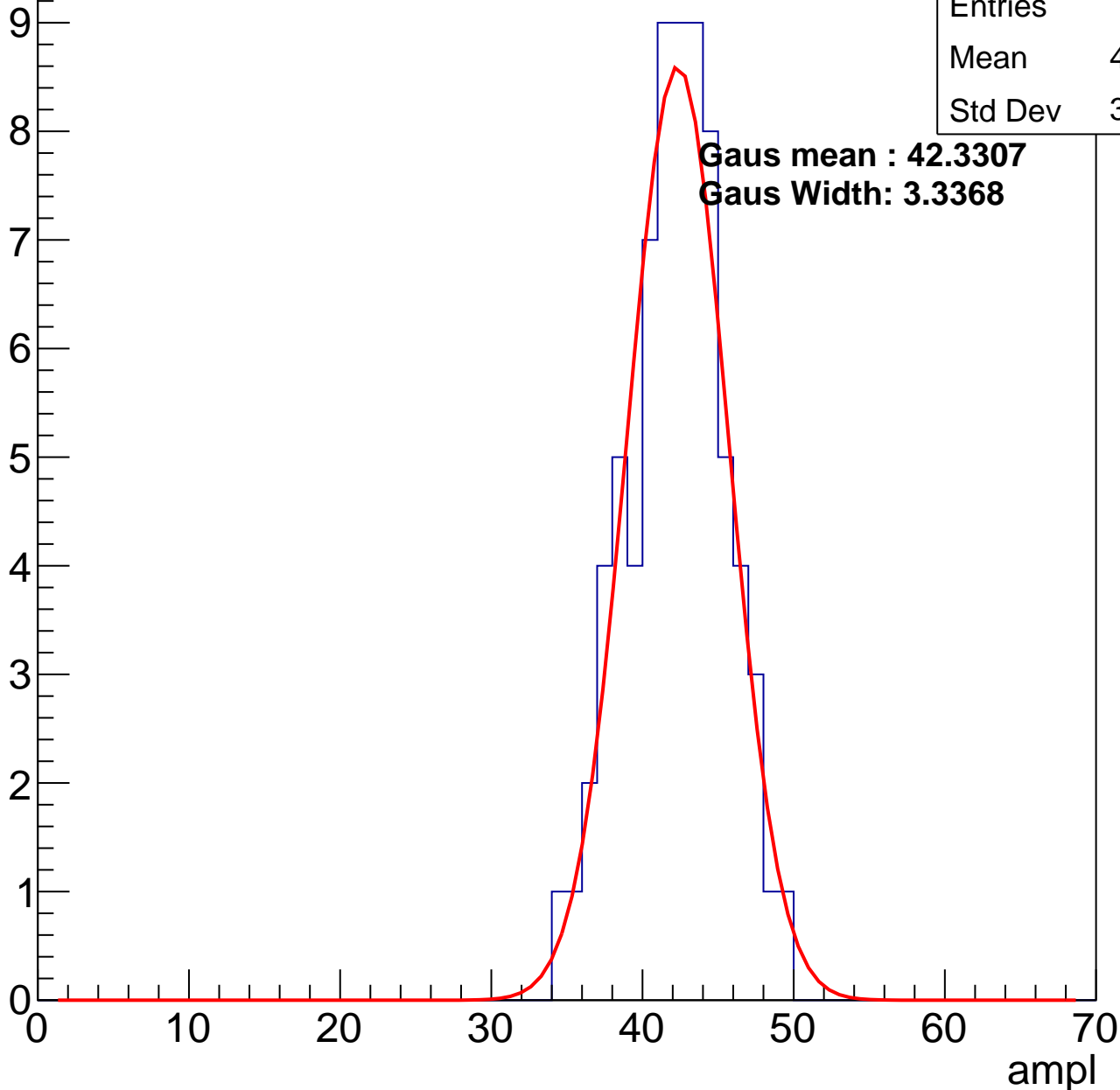
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	41.75
Std Dev	3.192

Gaus mean : 42.3307

Gaus Width: 3.3368

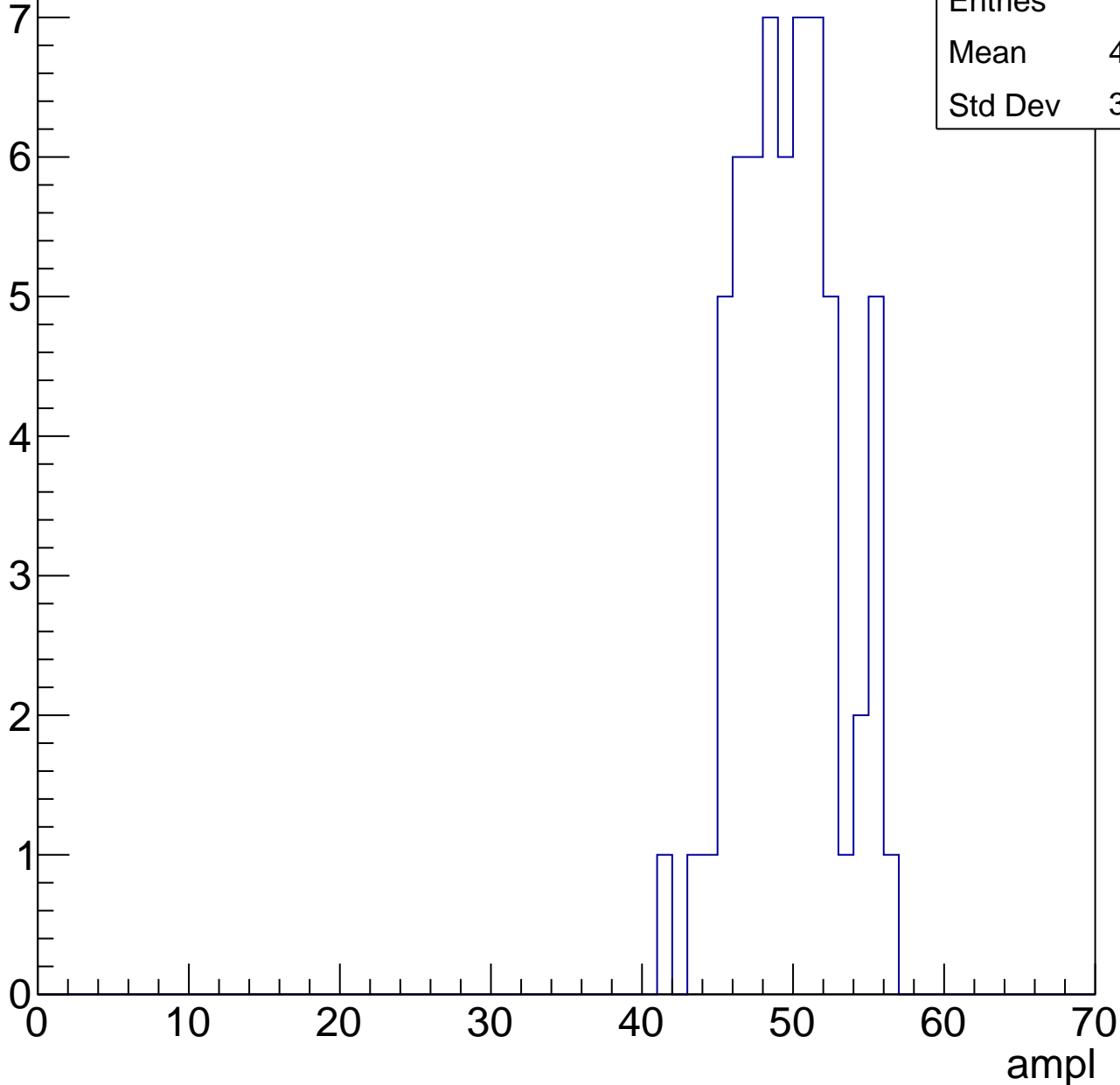


B1L100S, U15-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	49.18
Std Dev	3.297

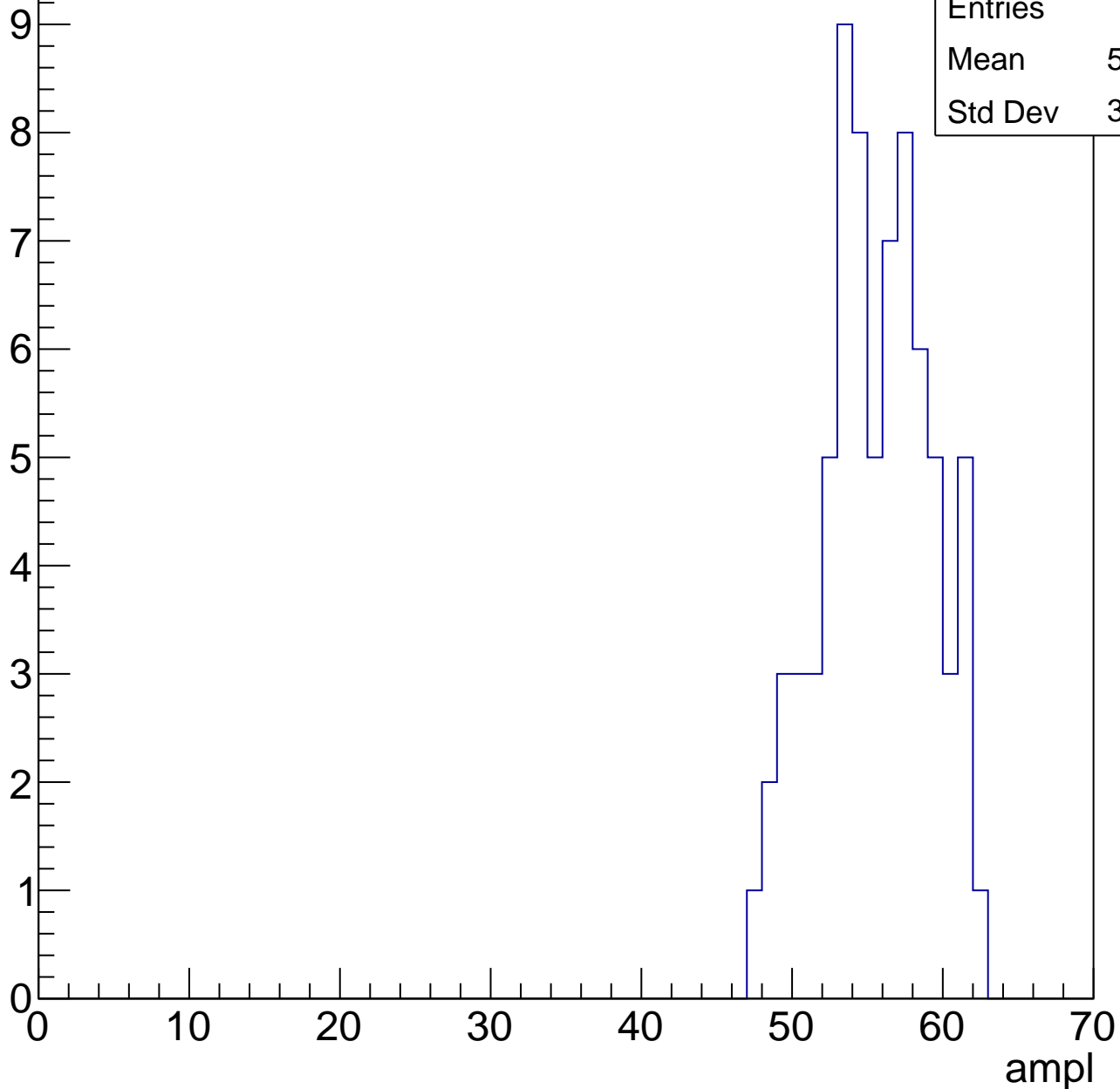


B1L100S, U15-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

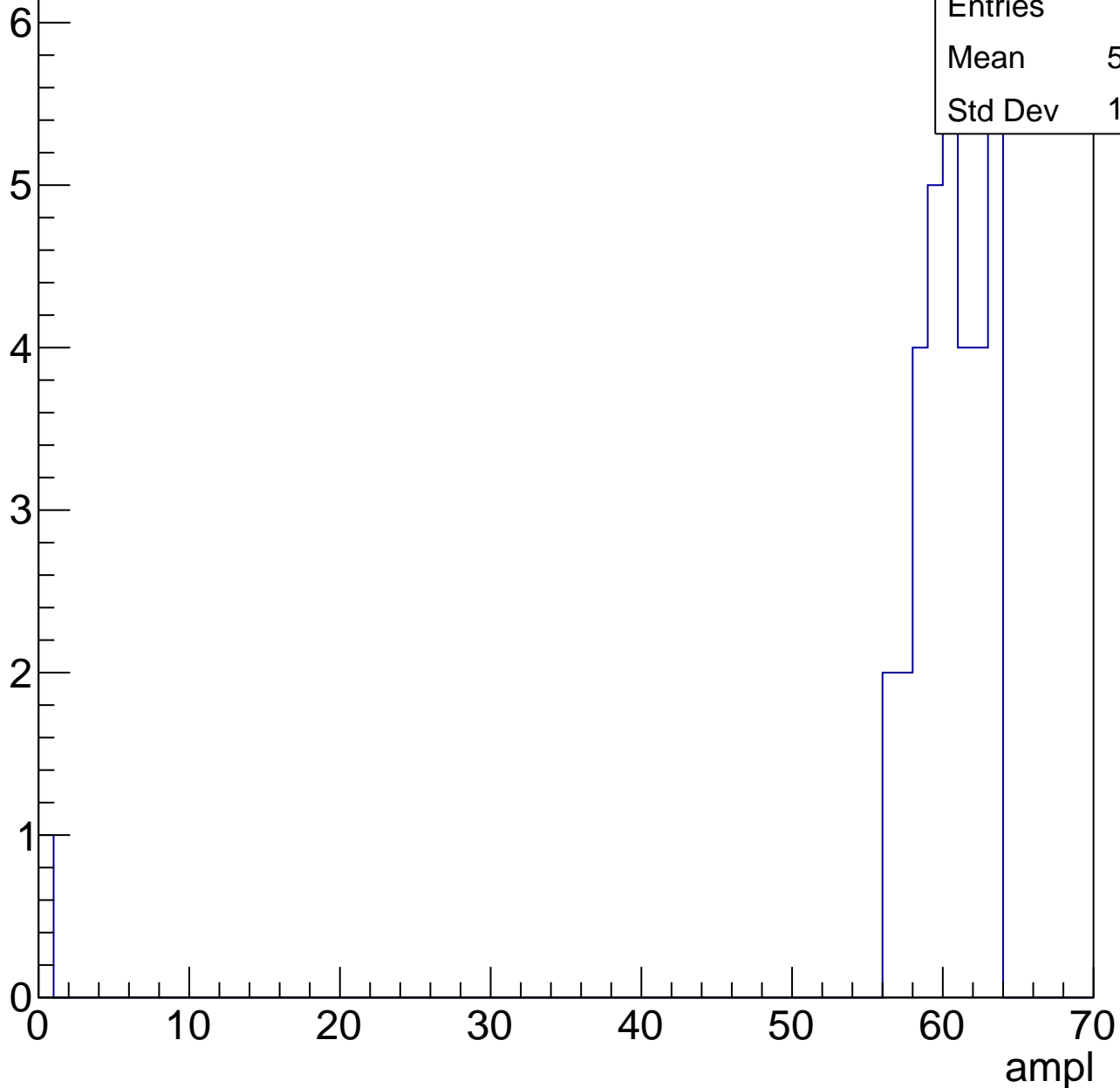
Entries	74
Mean	55.07
Std Dev	3.599



B1L100S, U15-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

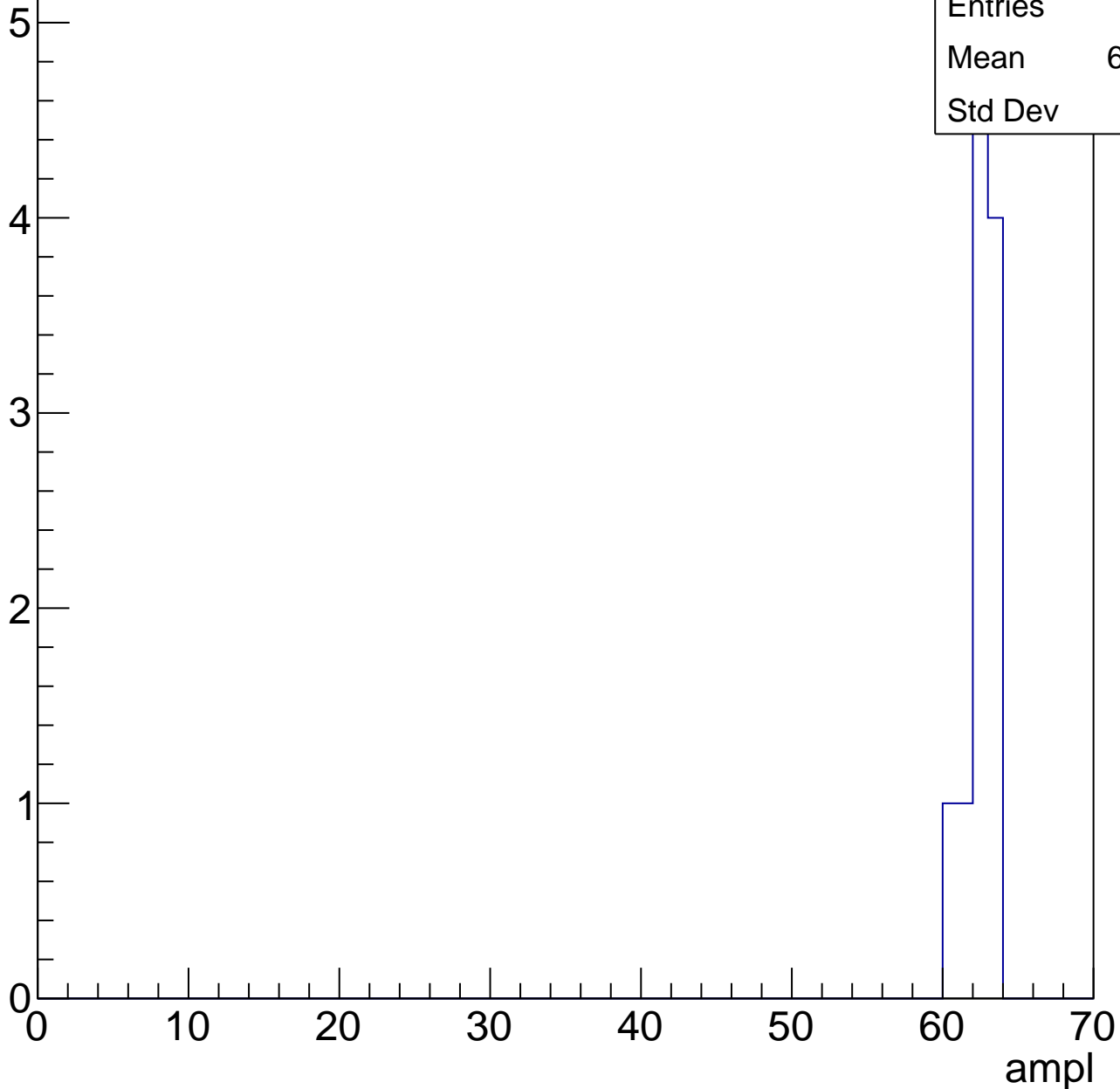


B1L100S, U15-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	62.09
Std Dev	0.9



B1L100S, U15-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch46, adc0

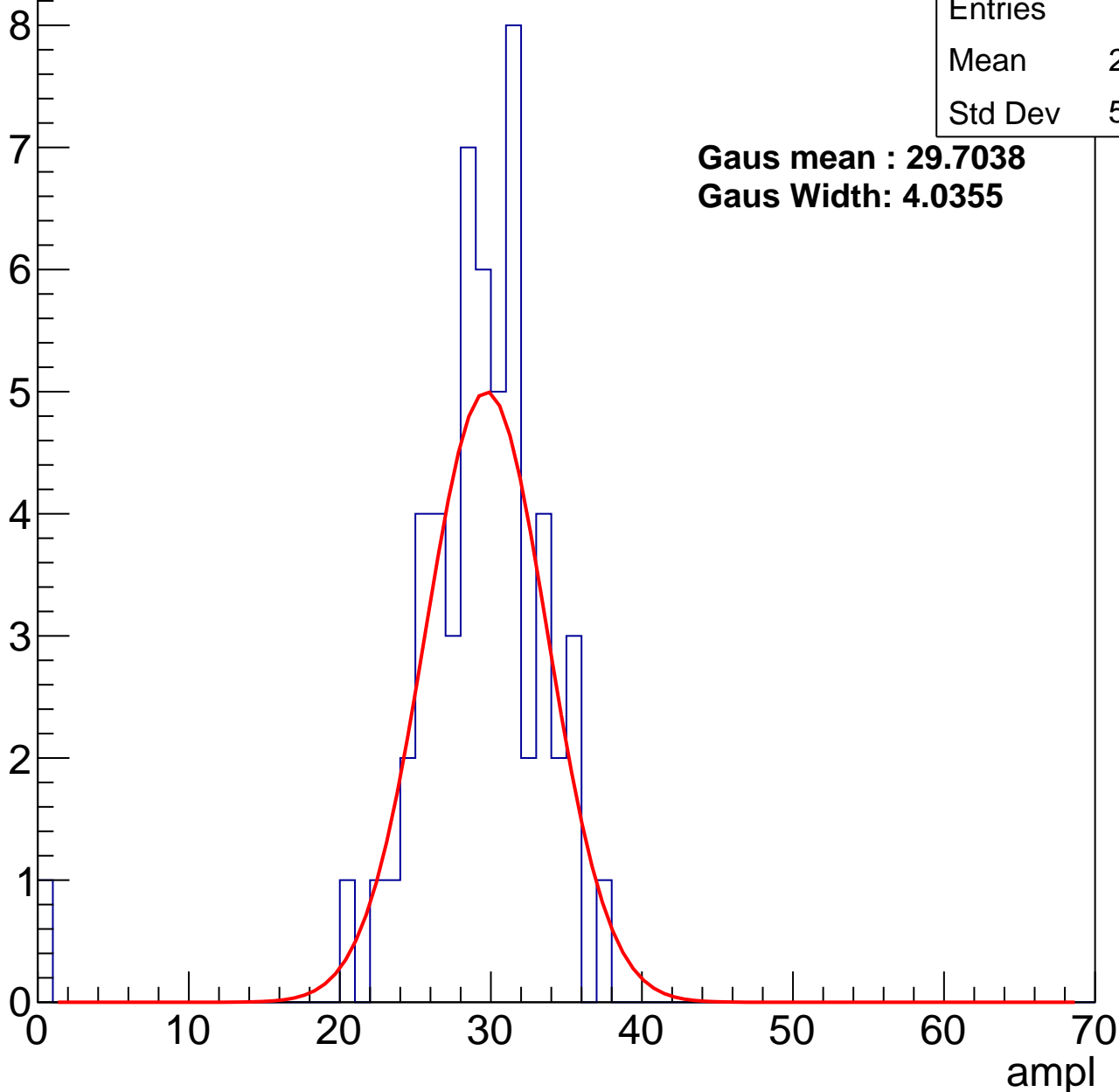
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	28.58
Std Dev	5.235

Gaus mean : 29.7038

Gaus Width: 4.0355



B1L100S, U15-ch46, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	35.93
Std Dev	3.864

Gaus mean : 36.5836

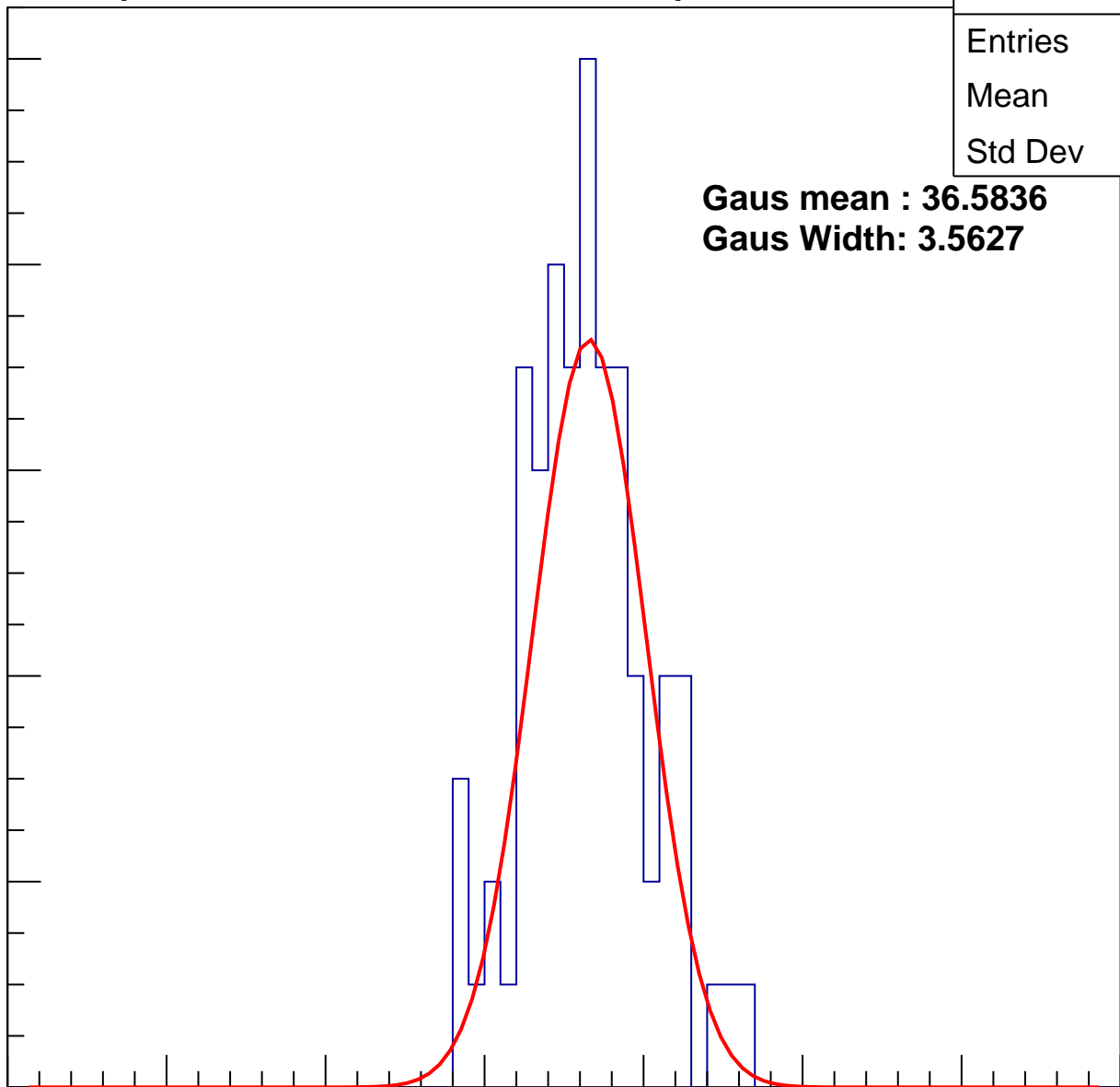
Gaus Width: 3.5627

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch46, adc2

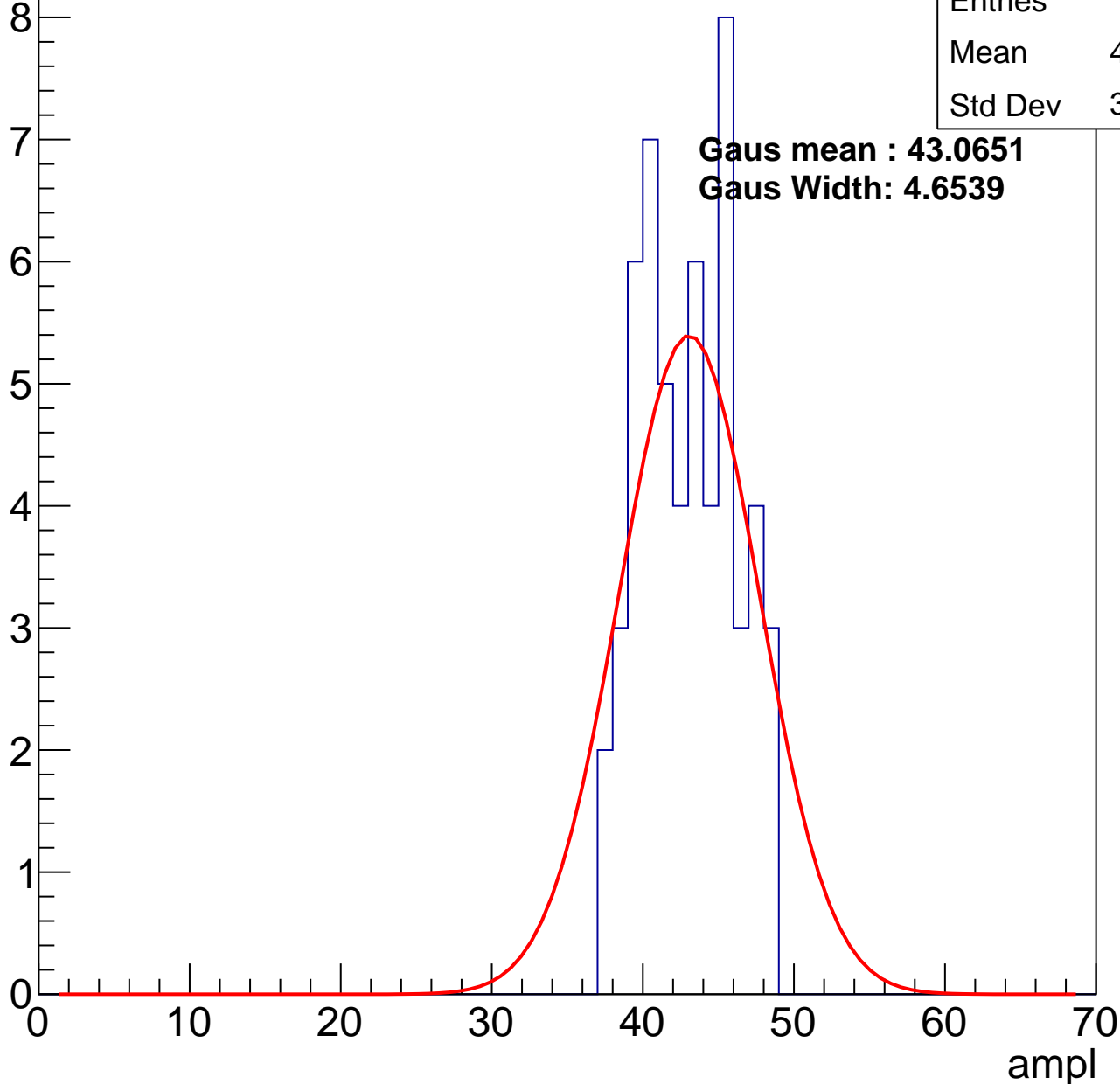
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	42.53
Std Dev	3.074

Gaus mean : 43.0651

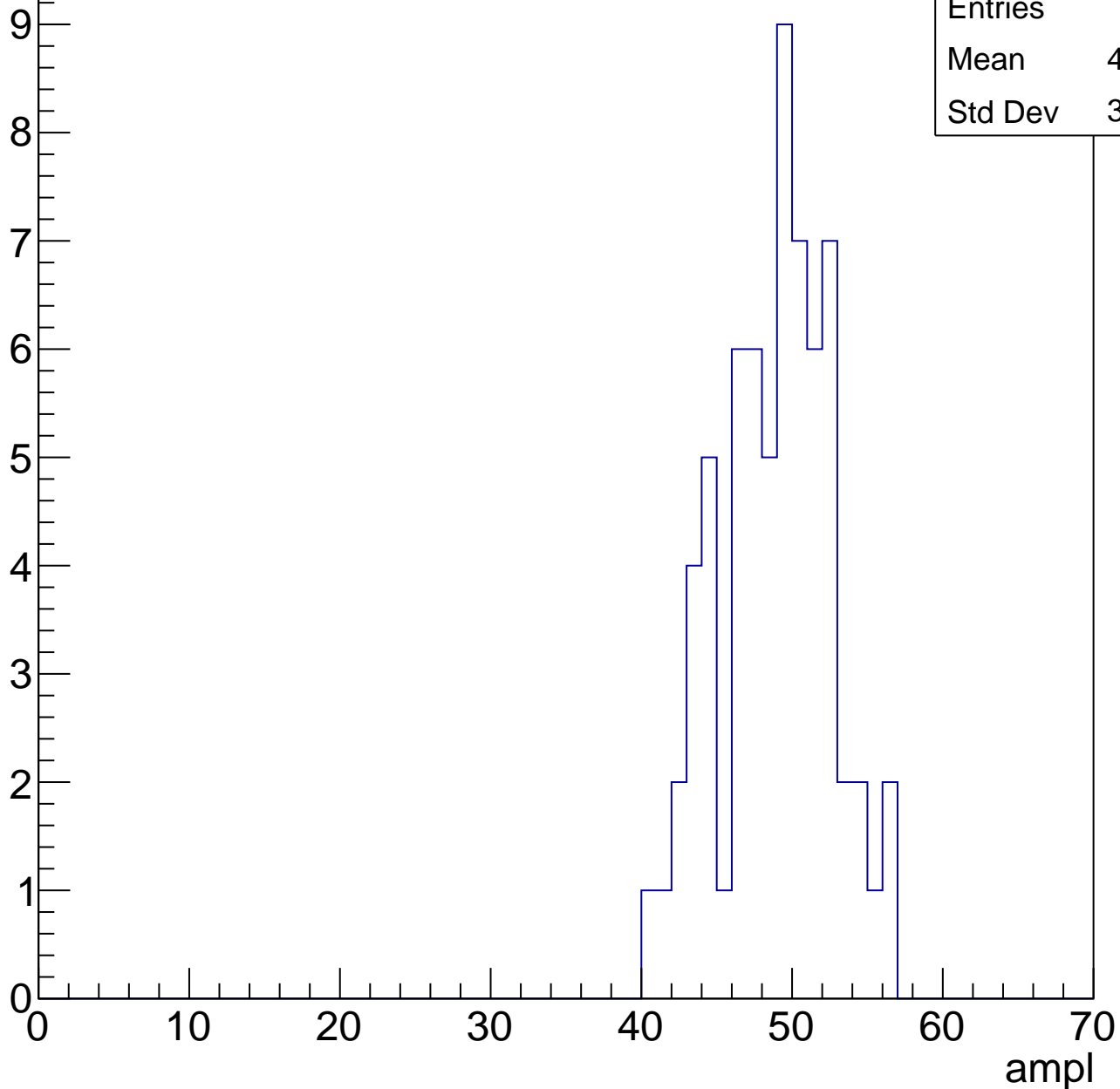
Gaus Width: 4.6539



B1L100S, U15-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

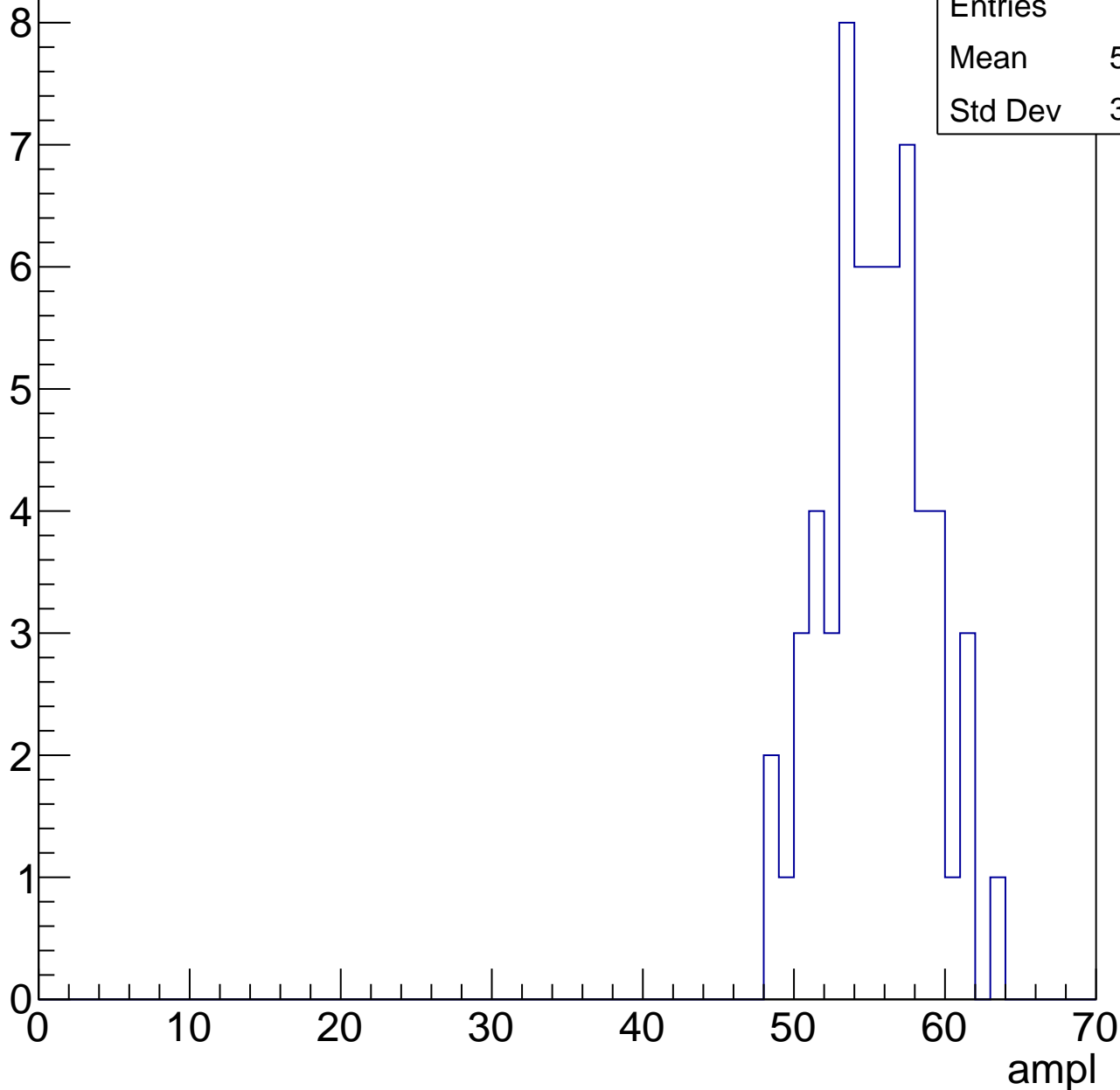


B1L100S, U15-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	54.95
Std Dev	3.367



B1L100S, U15-ch46, adc5

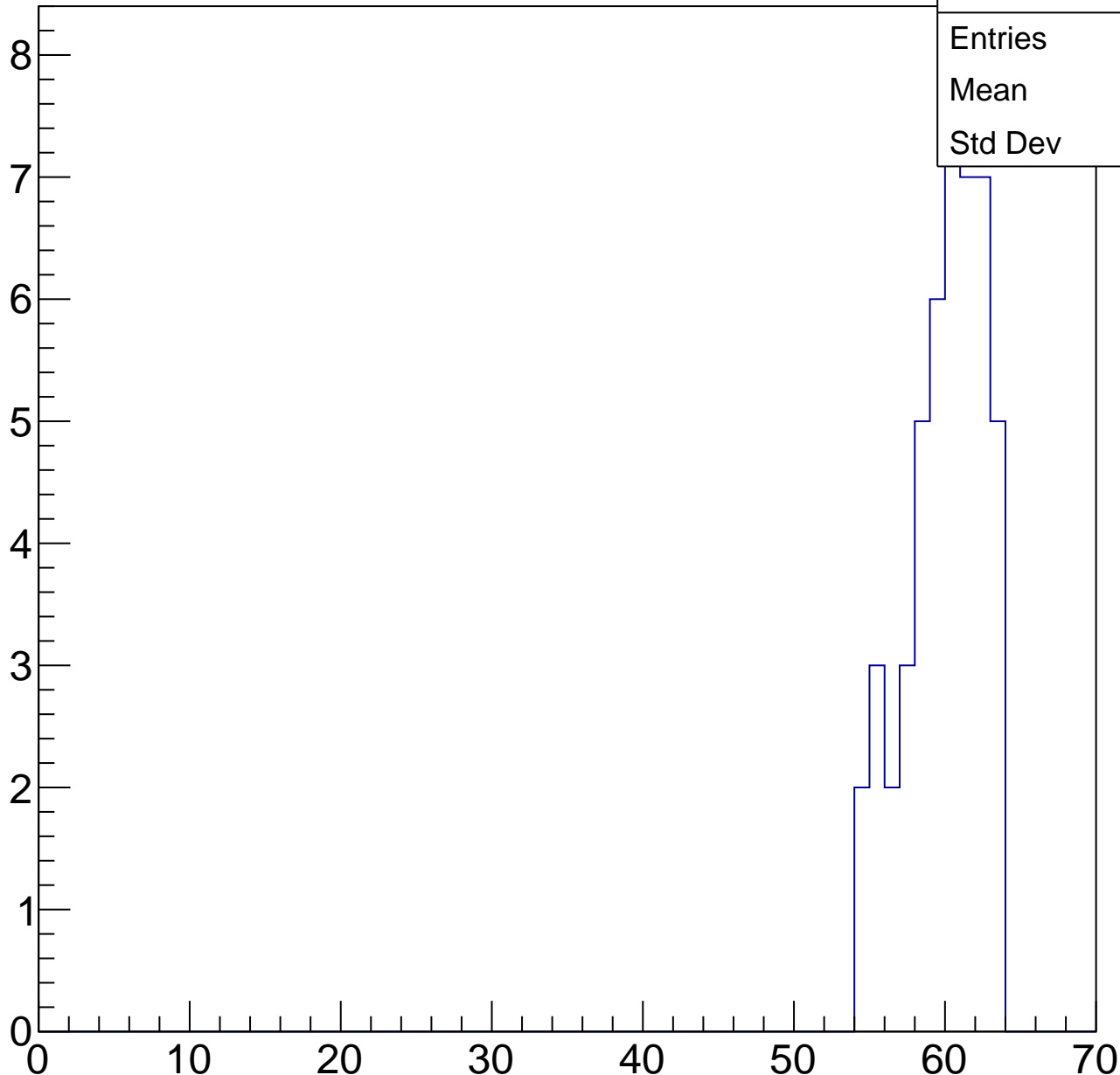
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.5
Std Dev	2.5

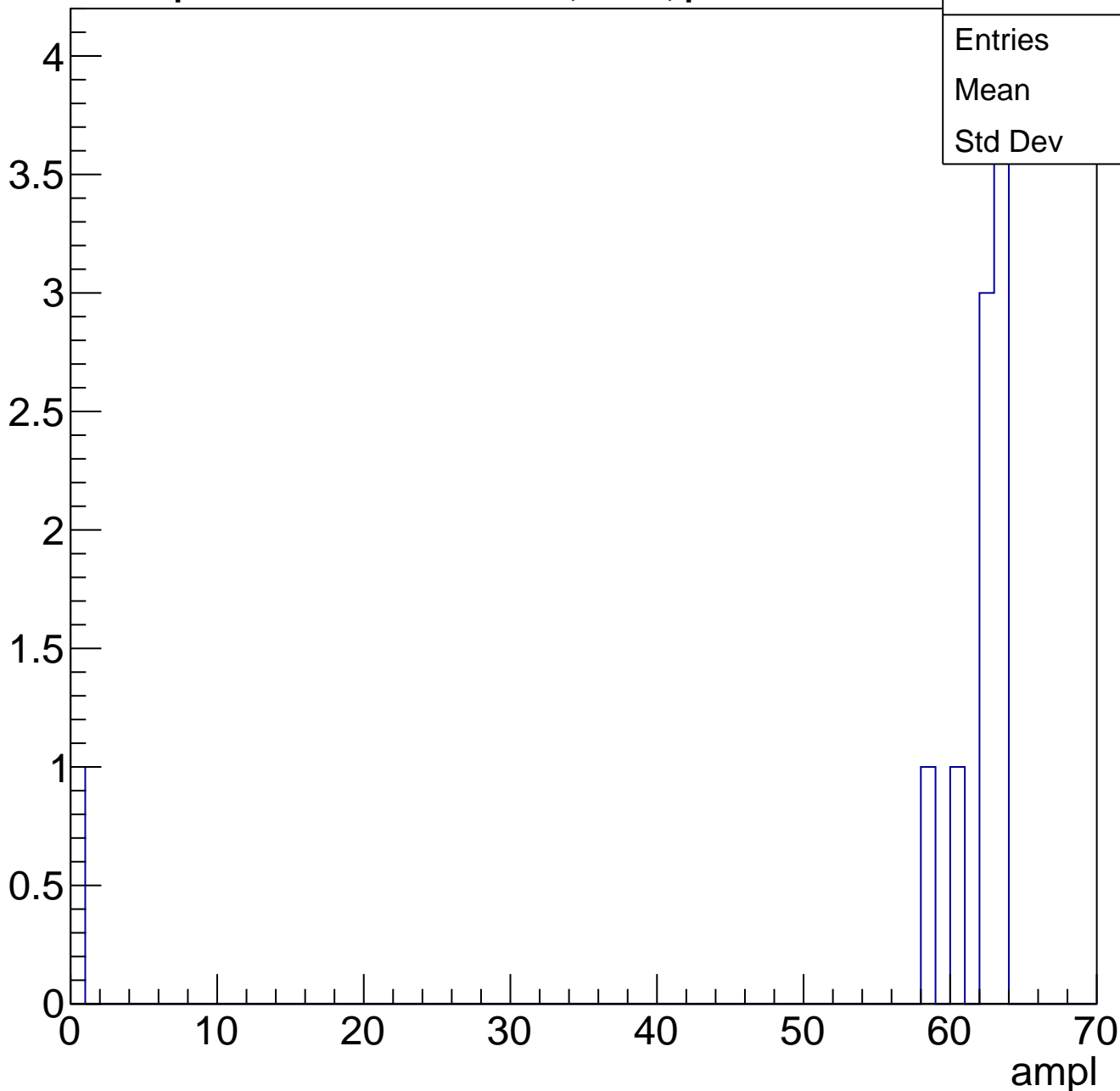
ampl



B1L100S, U15-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch47, adc0

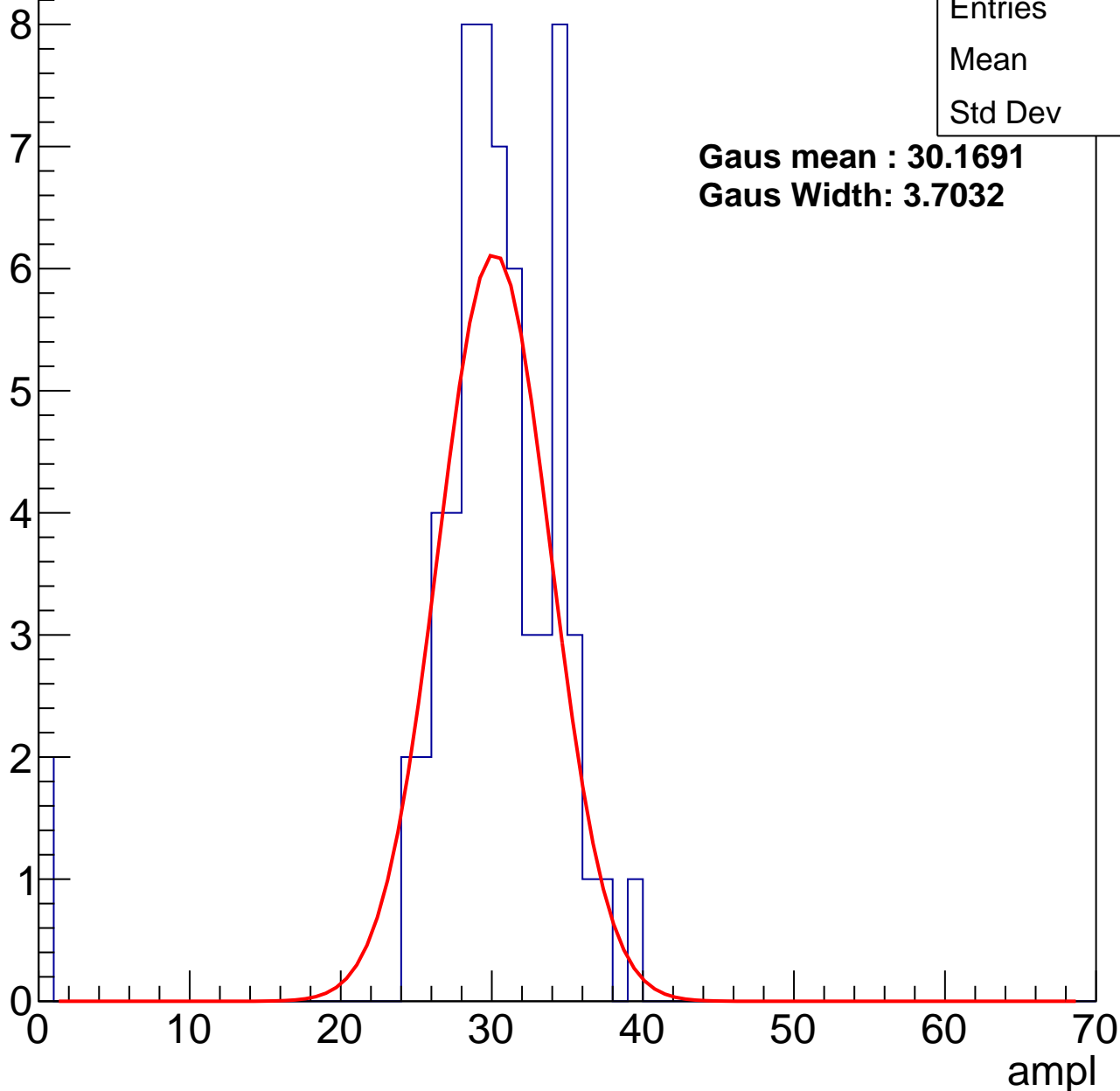
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	29.3
Std Dev	6.23

Gaus mean : 30.1691

Gaus Width: 3.7032



B1L100S, U15-ch47, adc1

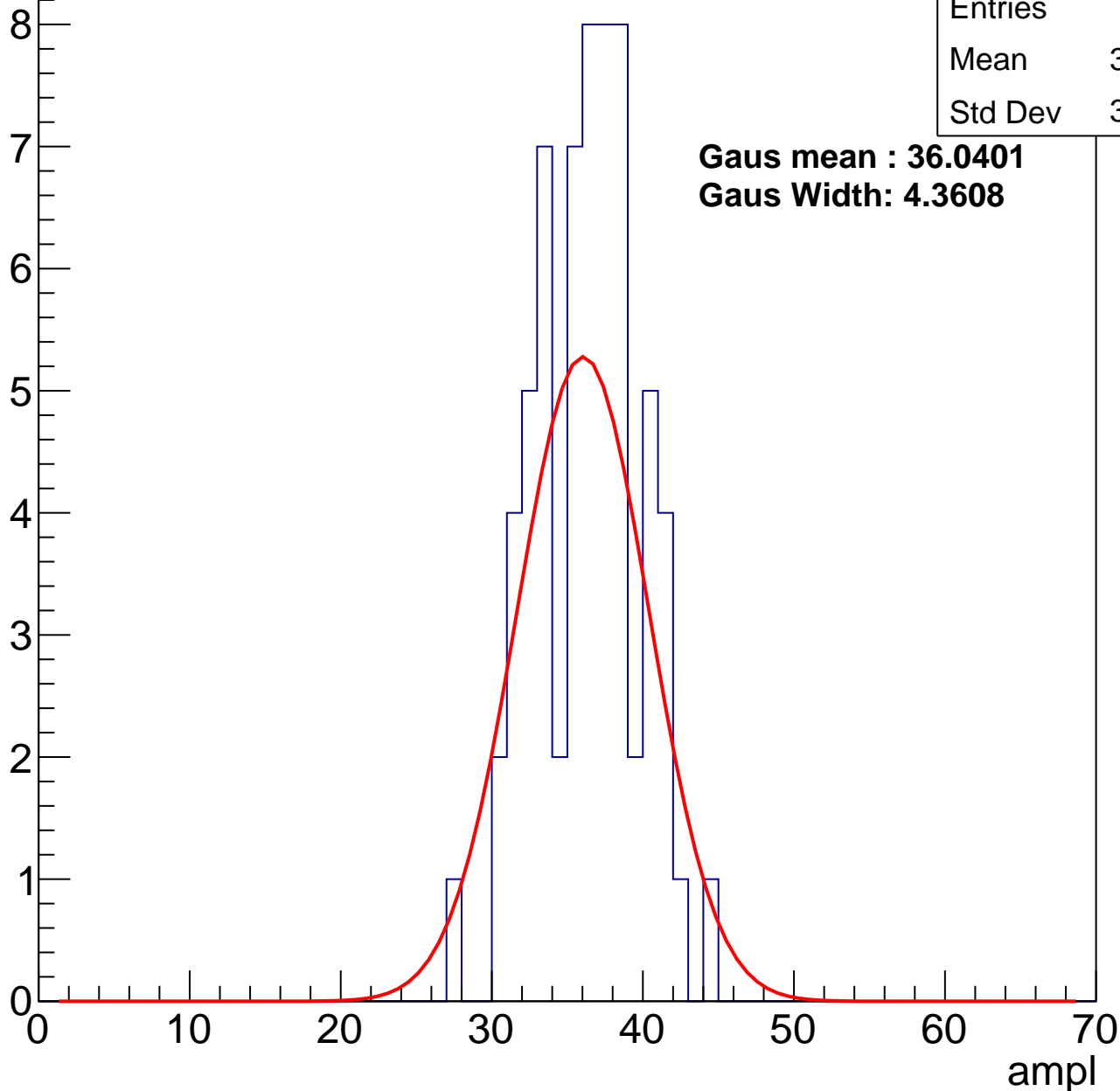
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.86
Std Dev	3.392

Gaus mean : 36.0401

Gaus Width: 4.3608



B1L100S, U15-ch47, adc2

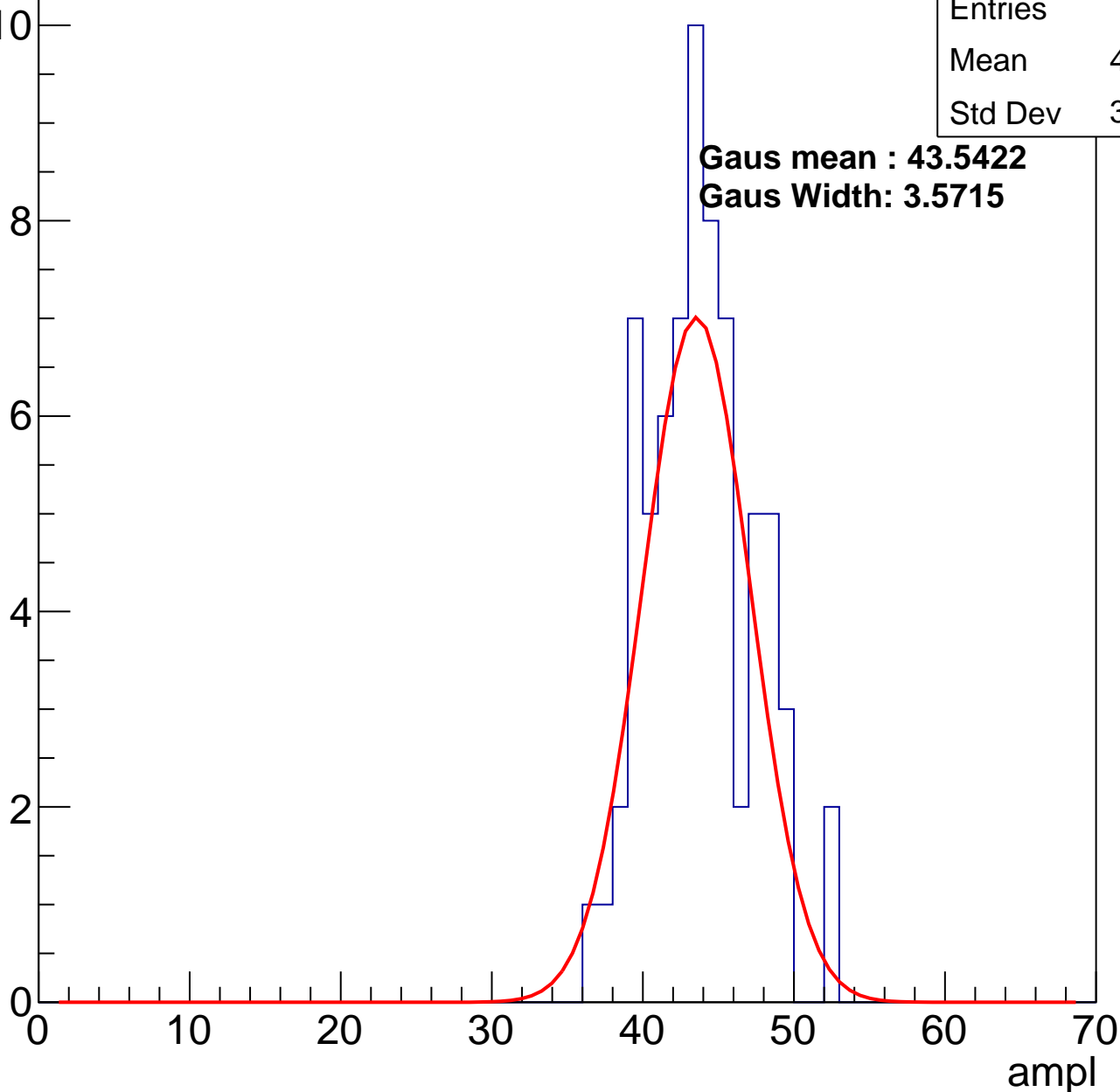
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.34
Std Dev	3.448

Gaus mean : 43.5422

Gaus Width: 3.5715



B1L100S, U15-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

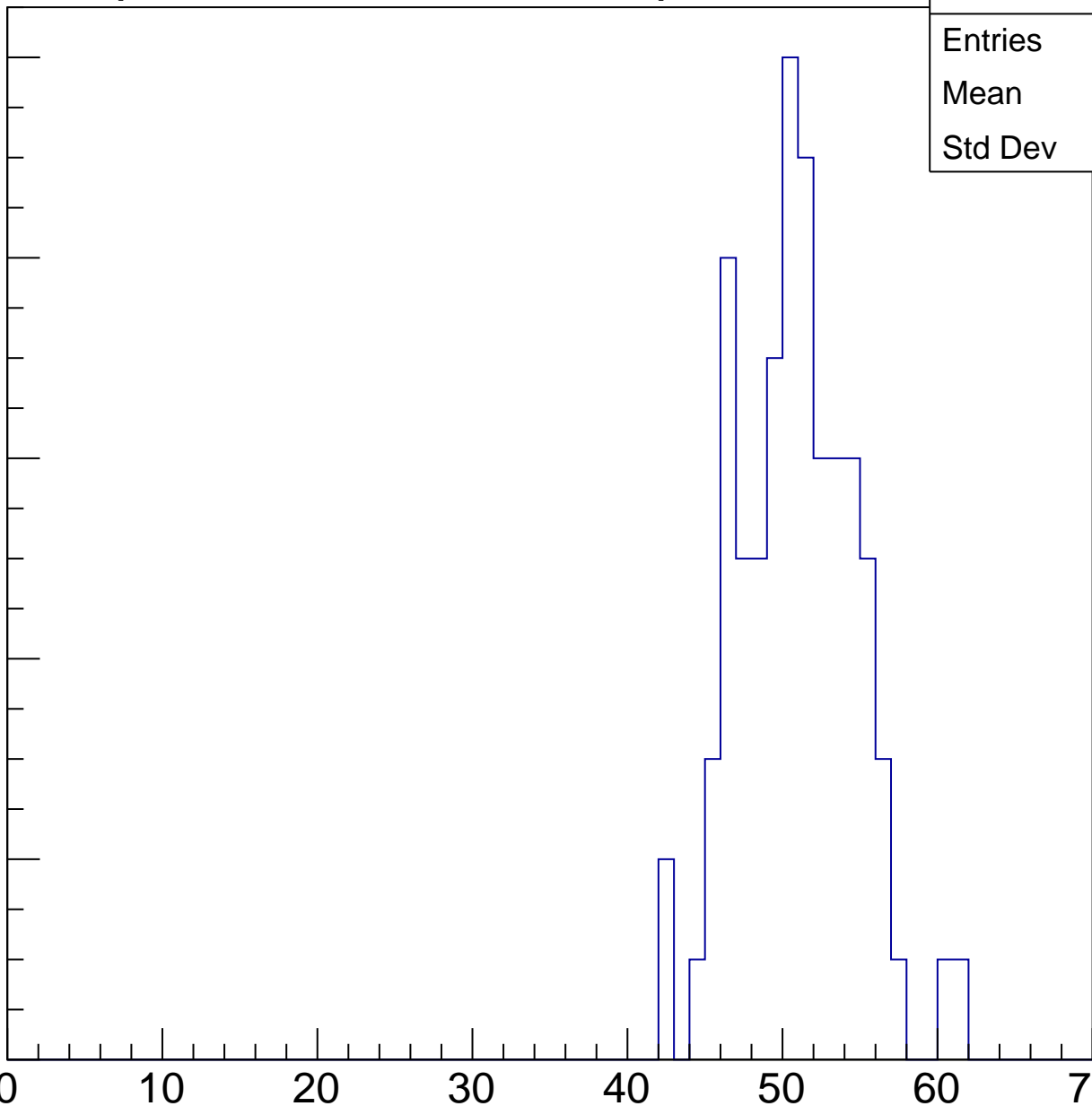
Entries	79
Mean	50.42
Std Dev	3.75

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

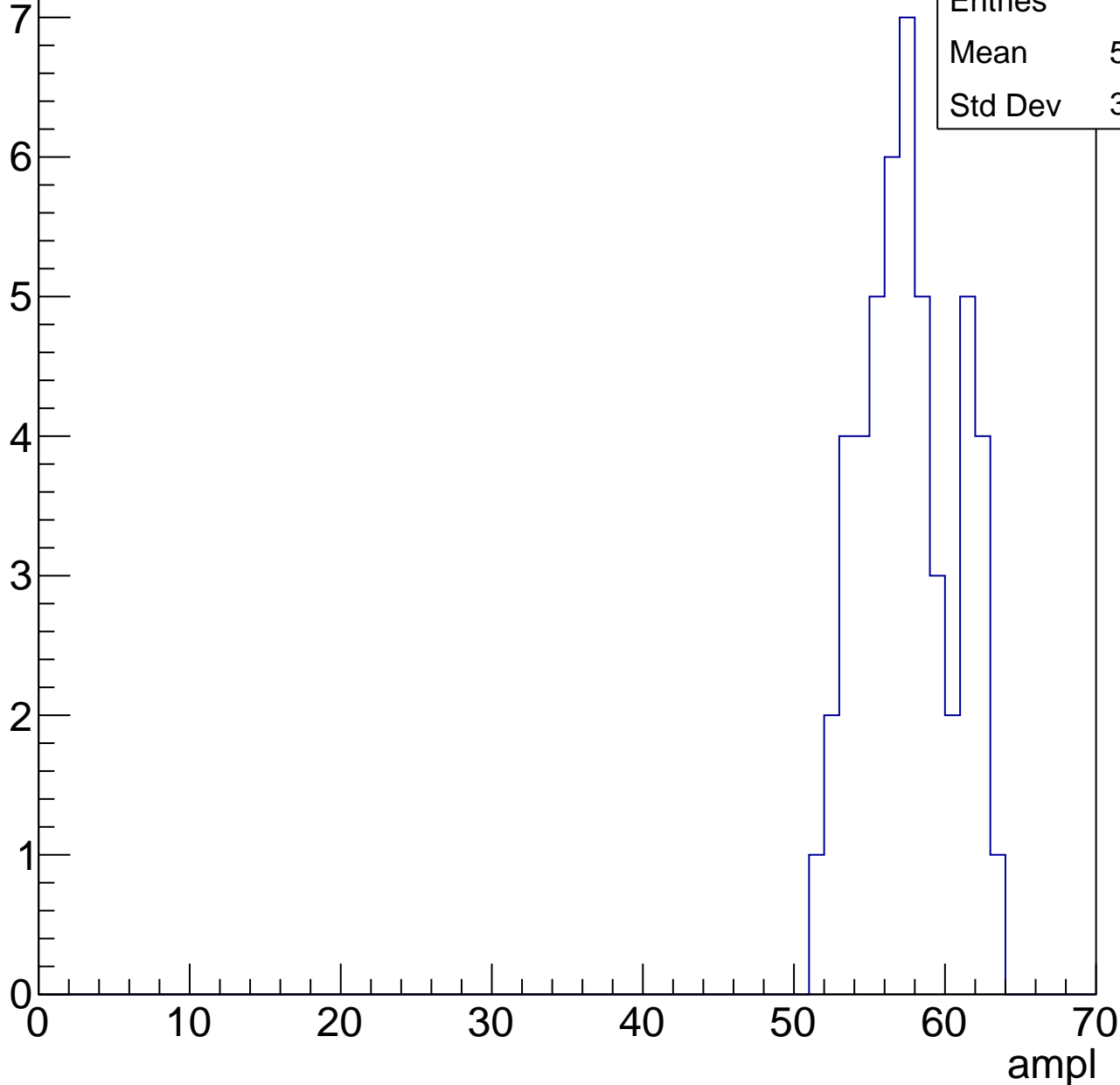
ampl



B1L100S, U15-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

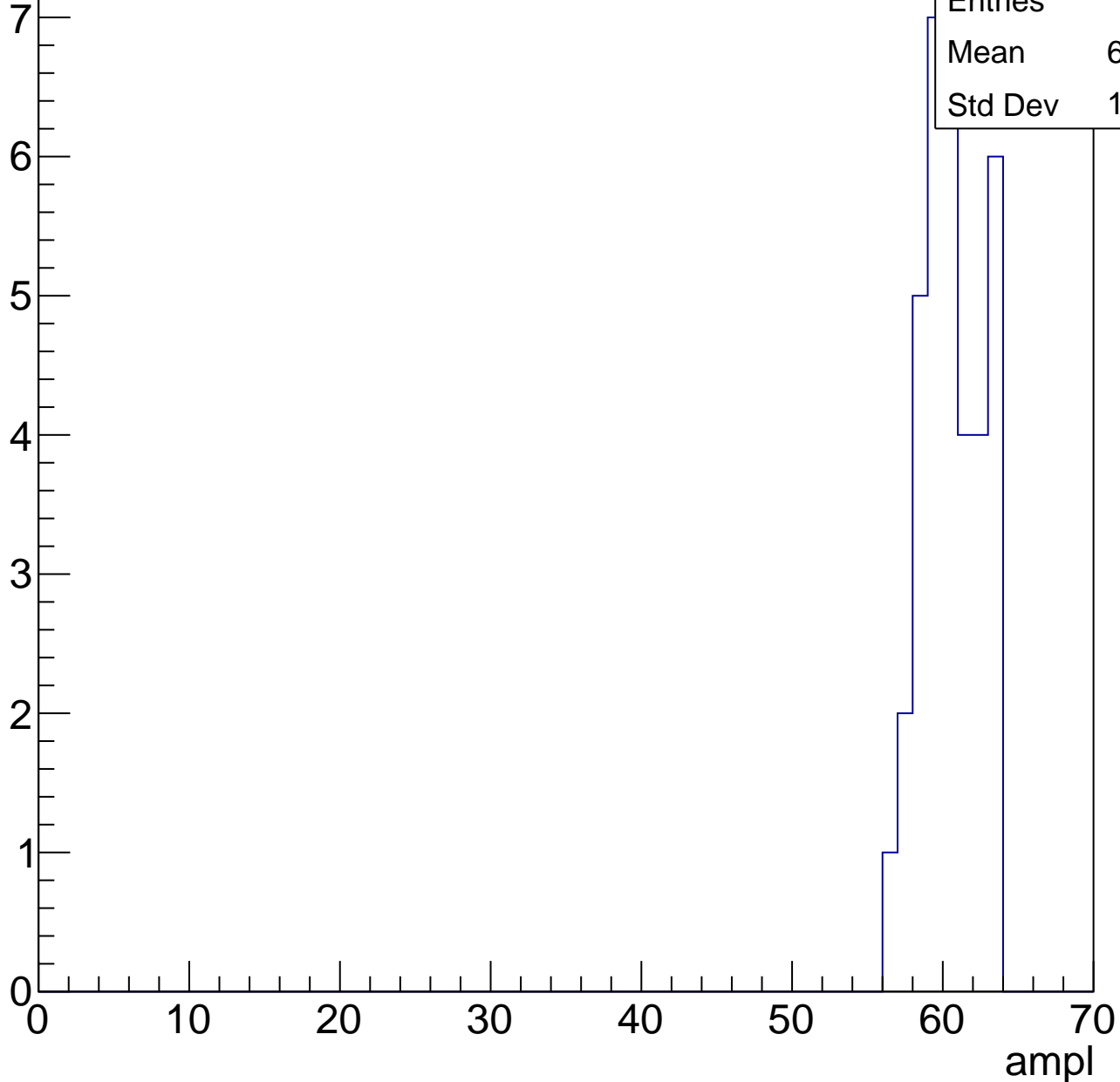


Entries	49
Mean	57.06
Std Dev	3.073

B1L100S, U15-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	36
Mean	60.08
Std Dev	1.935

B1L100S, U15-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U15-ch48, adc0

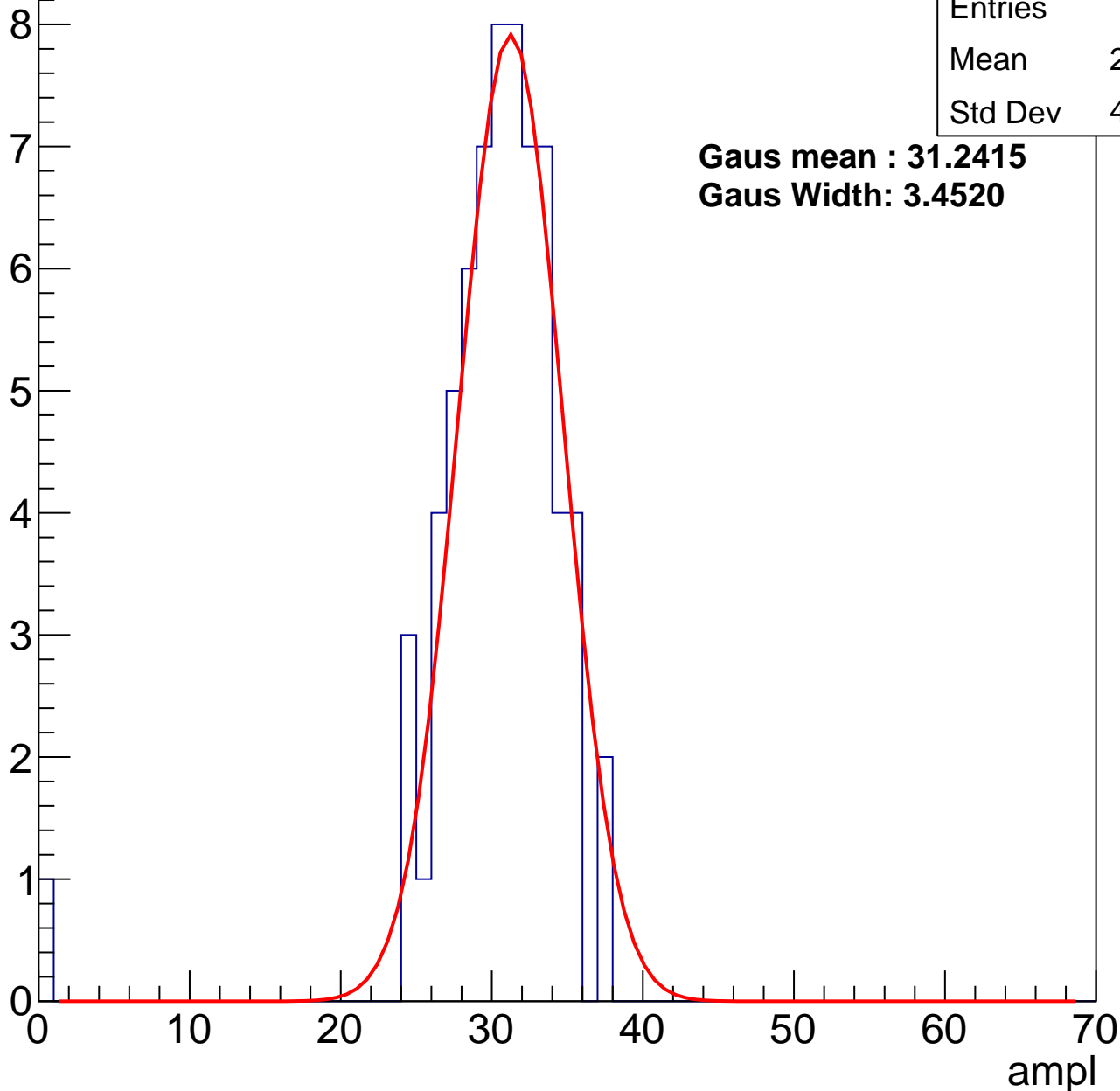
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	29.85
Std Dev	4.783

Gaus mean : 31.2415

Gaus Width: 3.4520



B1L100S, U15-ch48, adc1

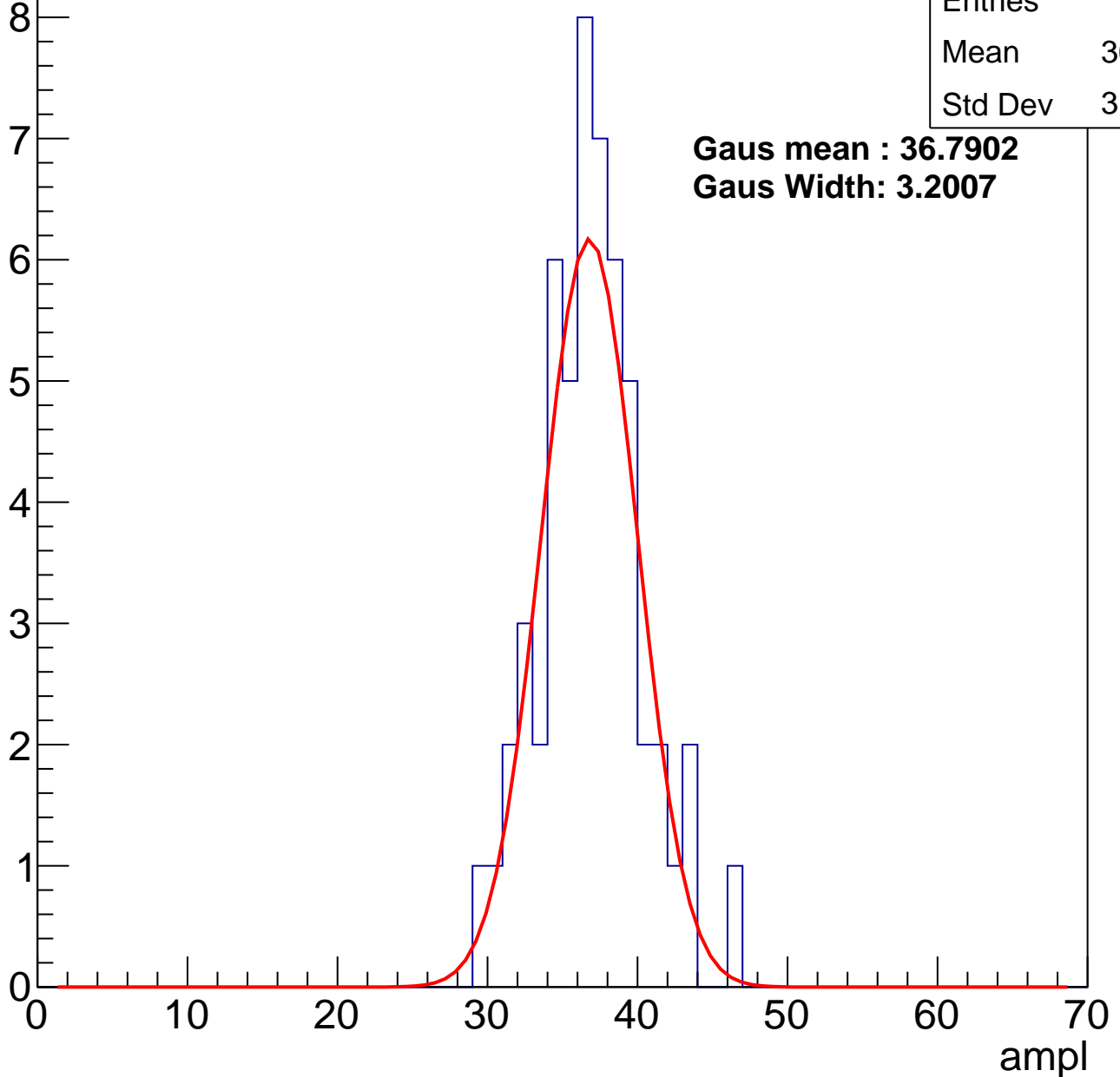
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	36.44
Std Dev	3.365

Gaus mean : 36.7902

Gaus Width: 3.2007



B1L100S, U15-ch48, adc2

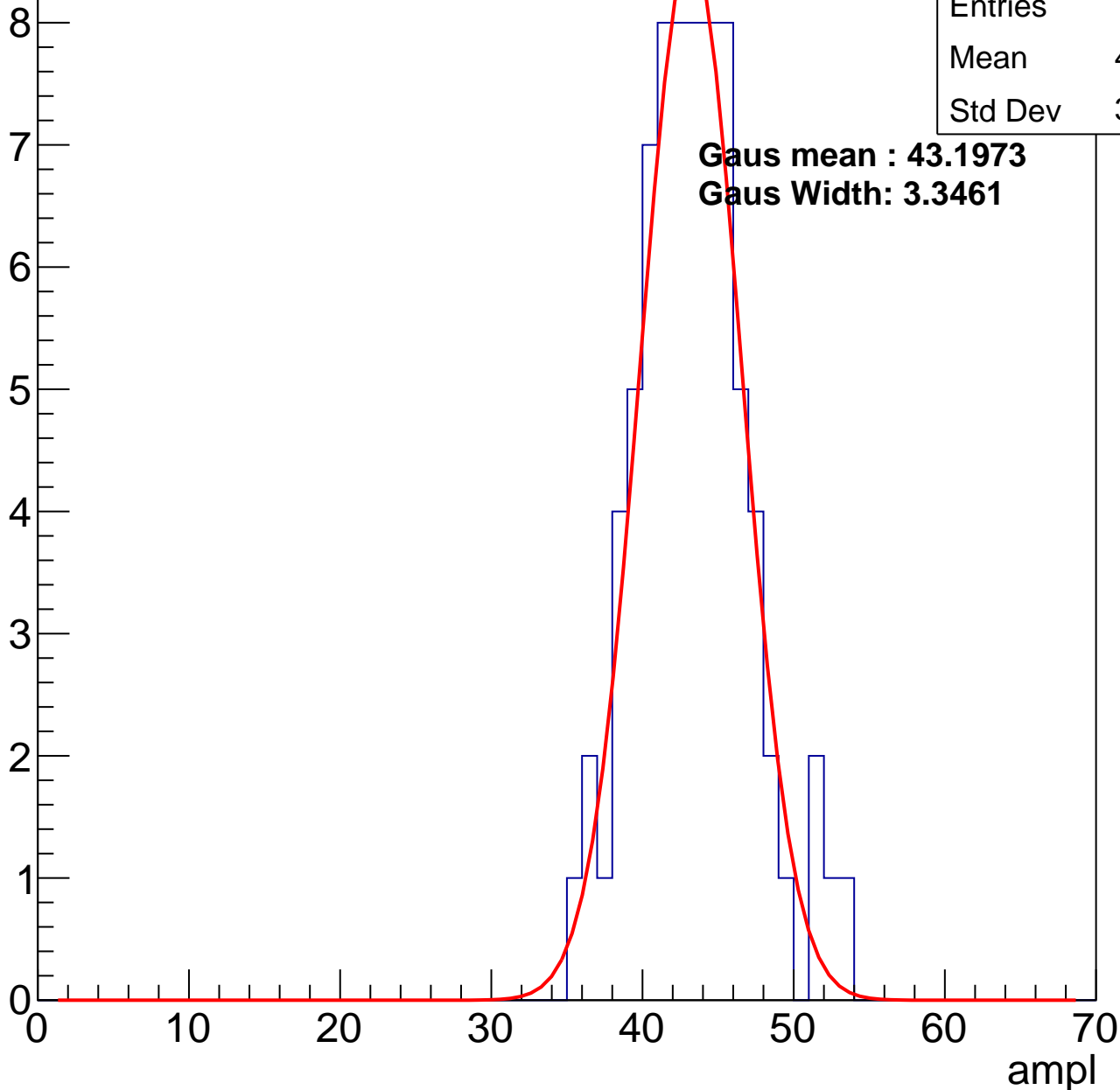
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	42.91
Std Dev	3.671

Gaus mean : 43.1973

Gaus Width: 3.3461

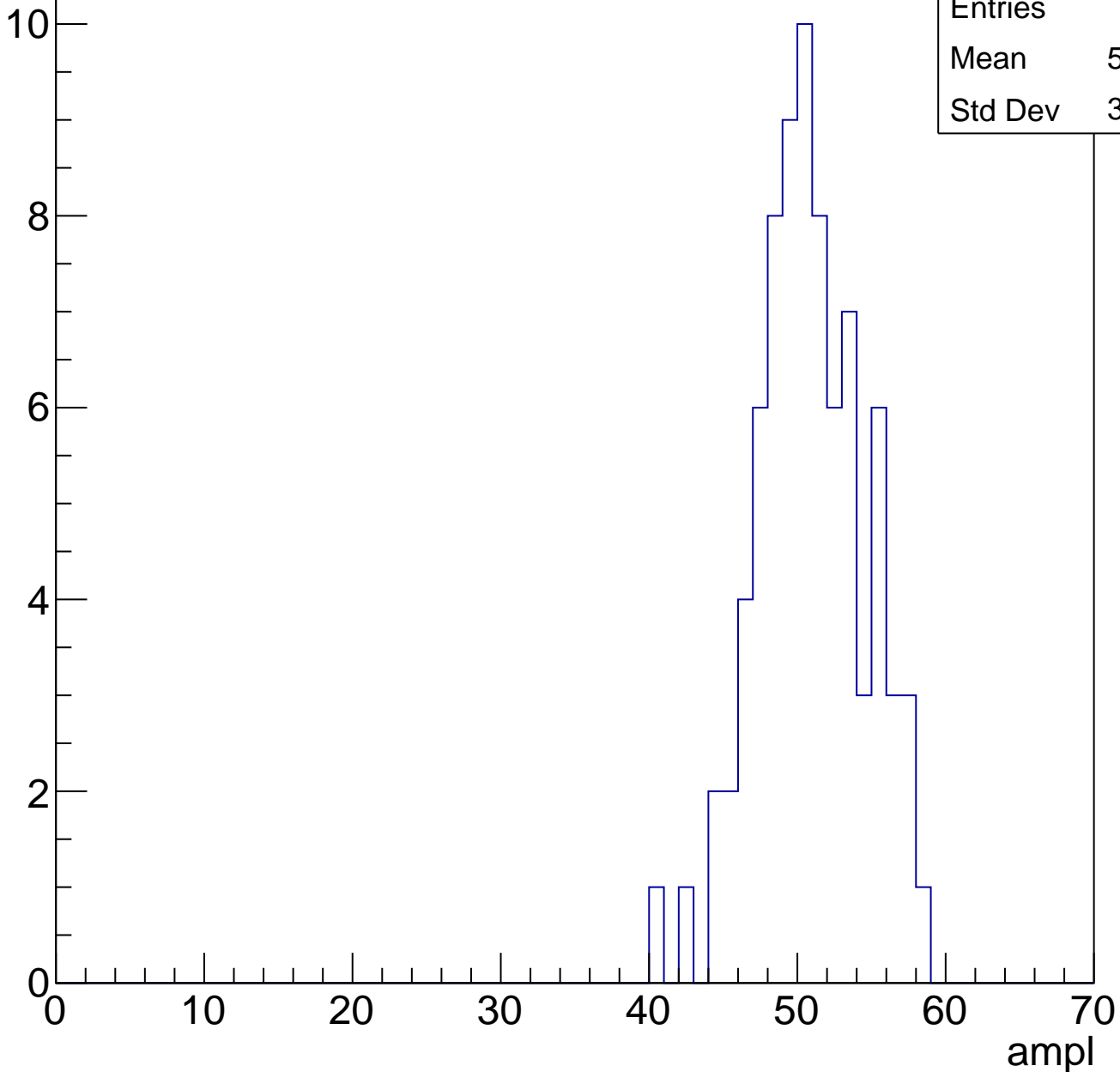


B1L100S, U15-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	50.39
Std Dev	3.614

Entry

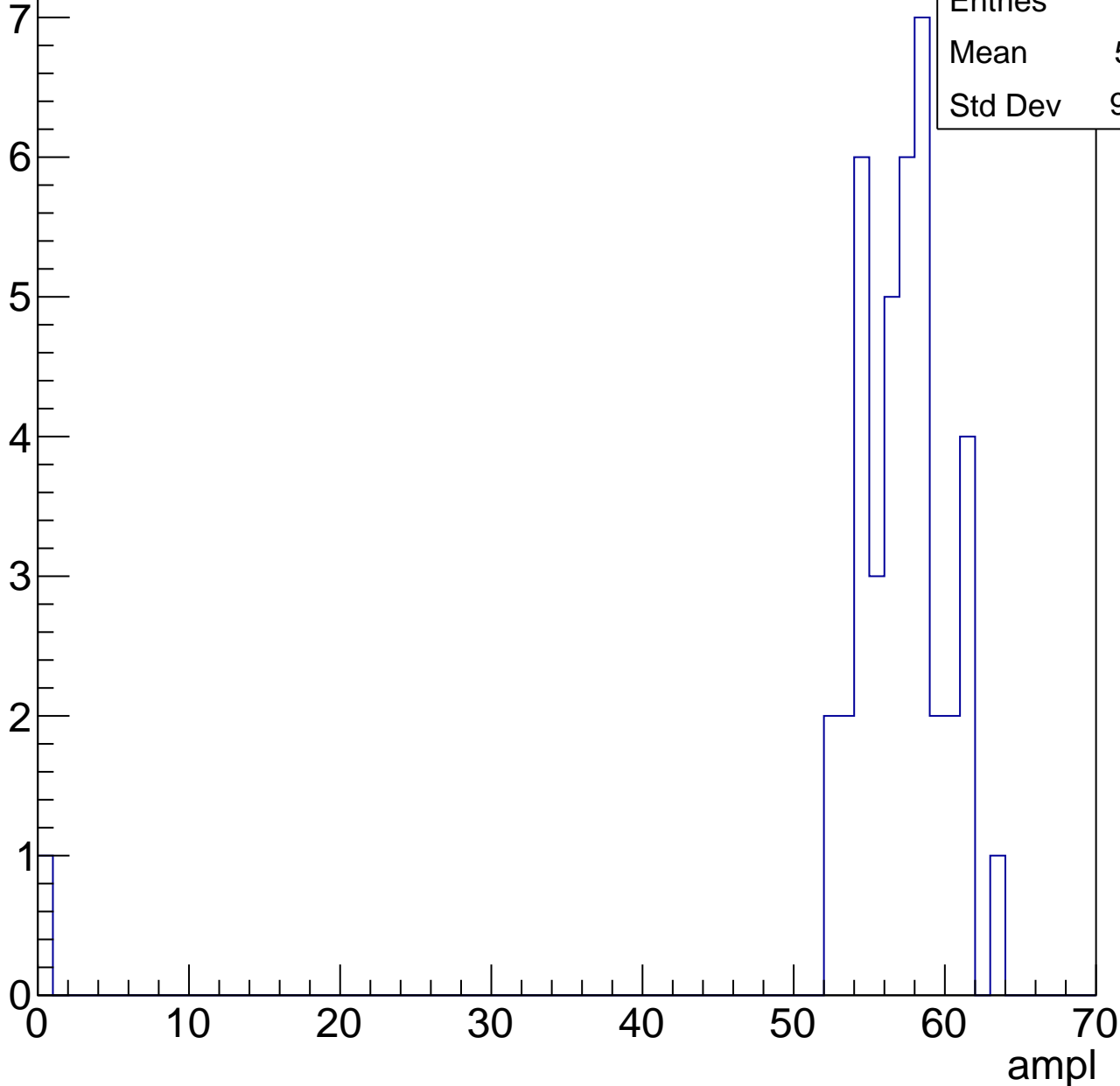


B1L100S, U15-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

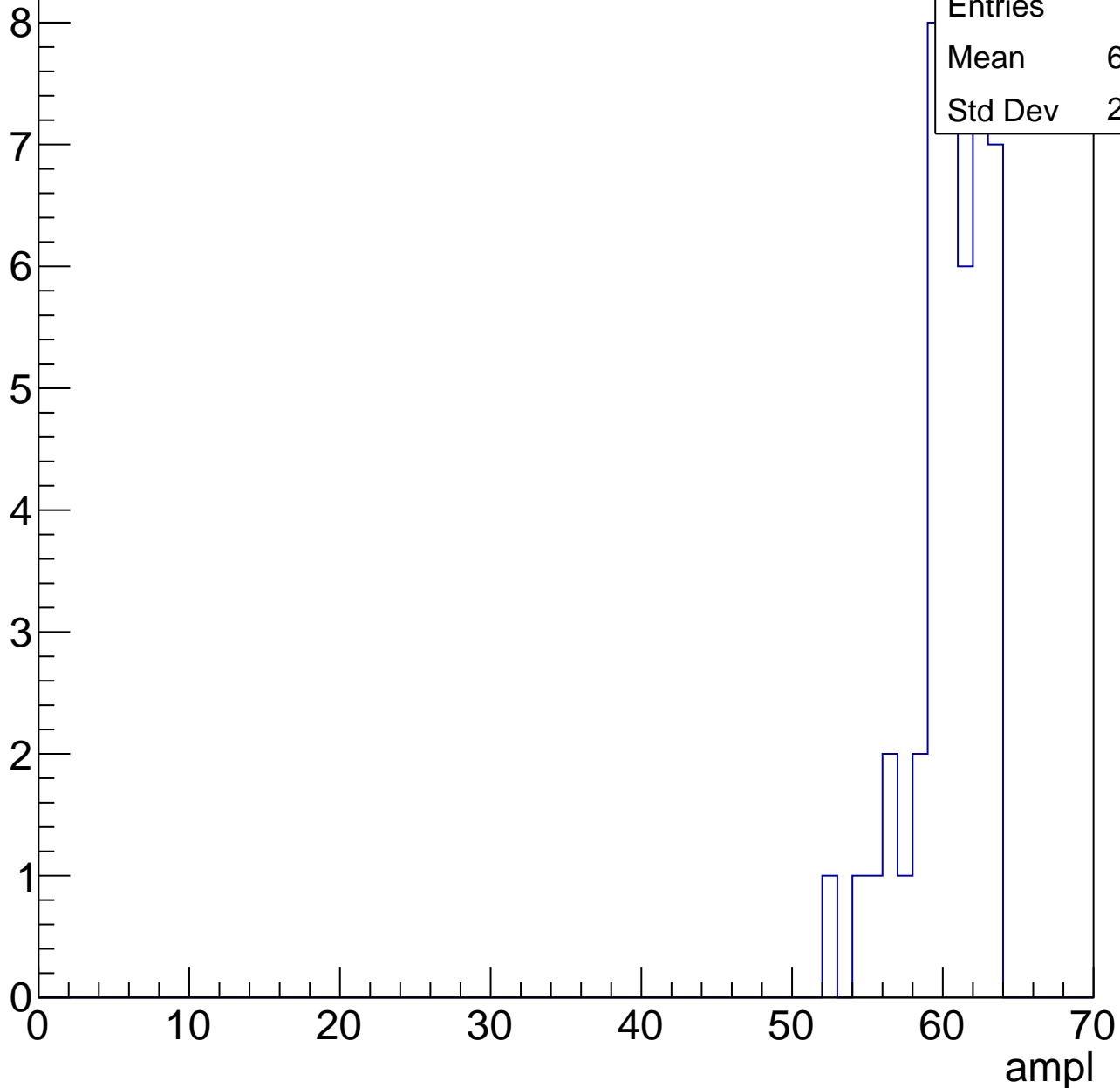
Entries	41
Mean	55.41
Std Dev	9.149



B1L100S, U15-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch49, adc0

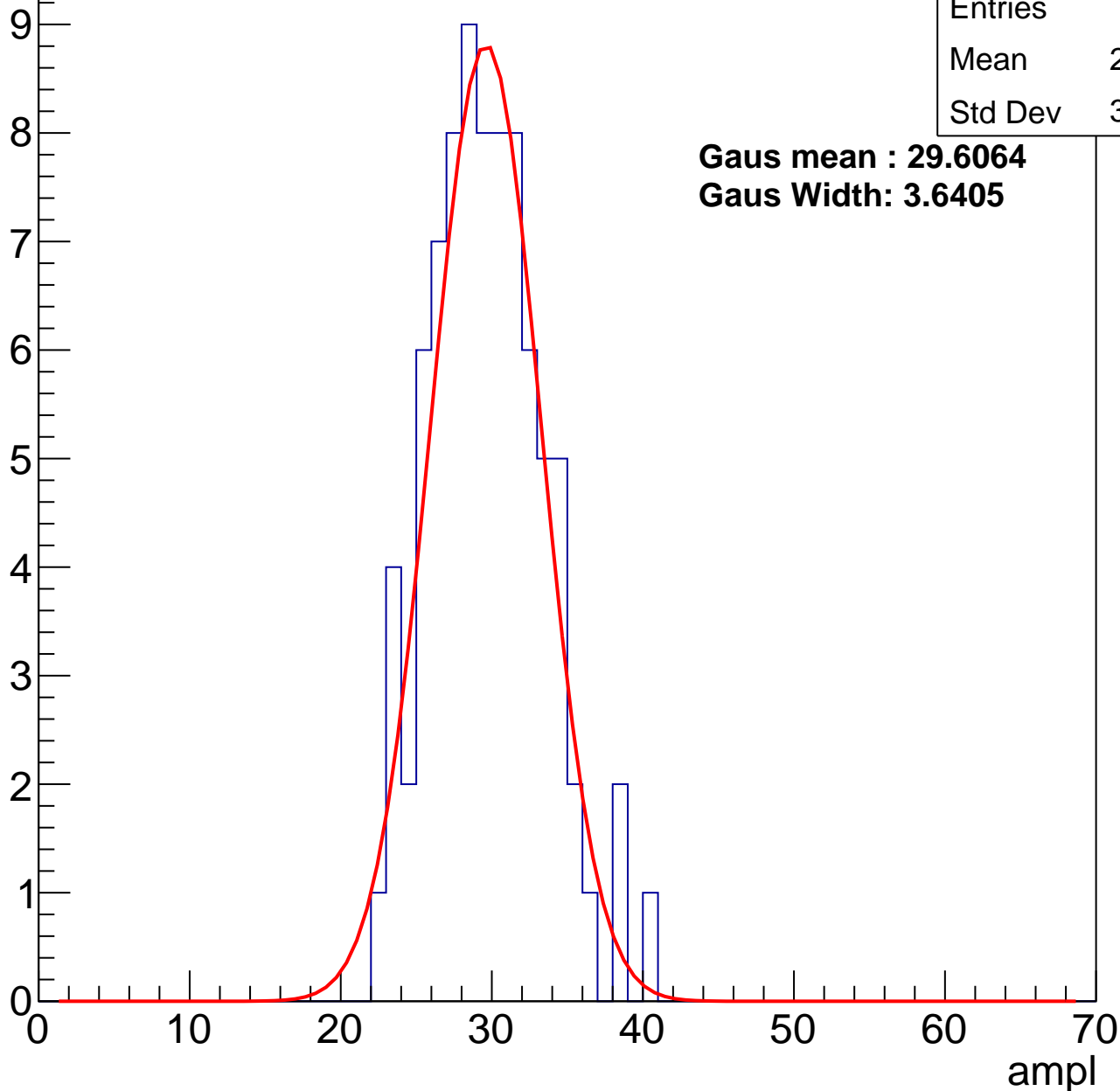
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	29.29
Std Dev	3.705

Gaus mean : 29.6064

Gaus Width: 3.6405



B1L100S, U15-ch49, adc1

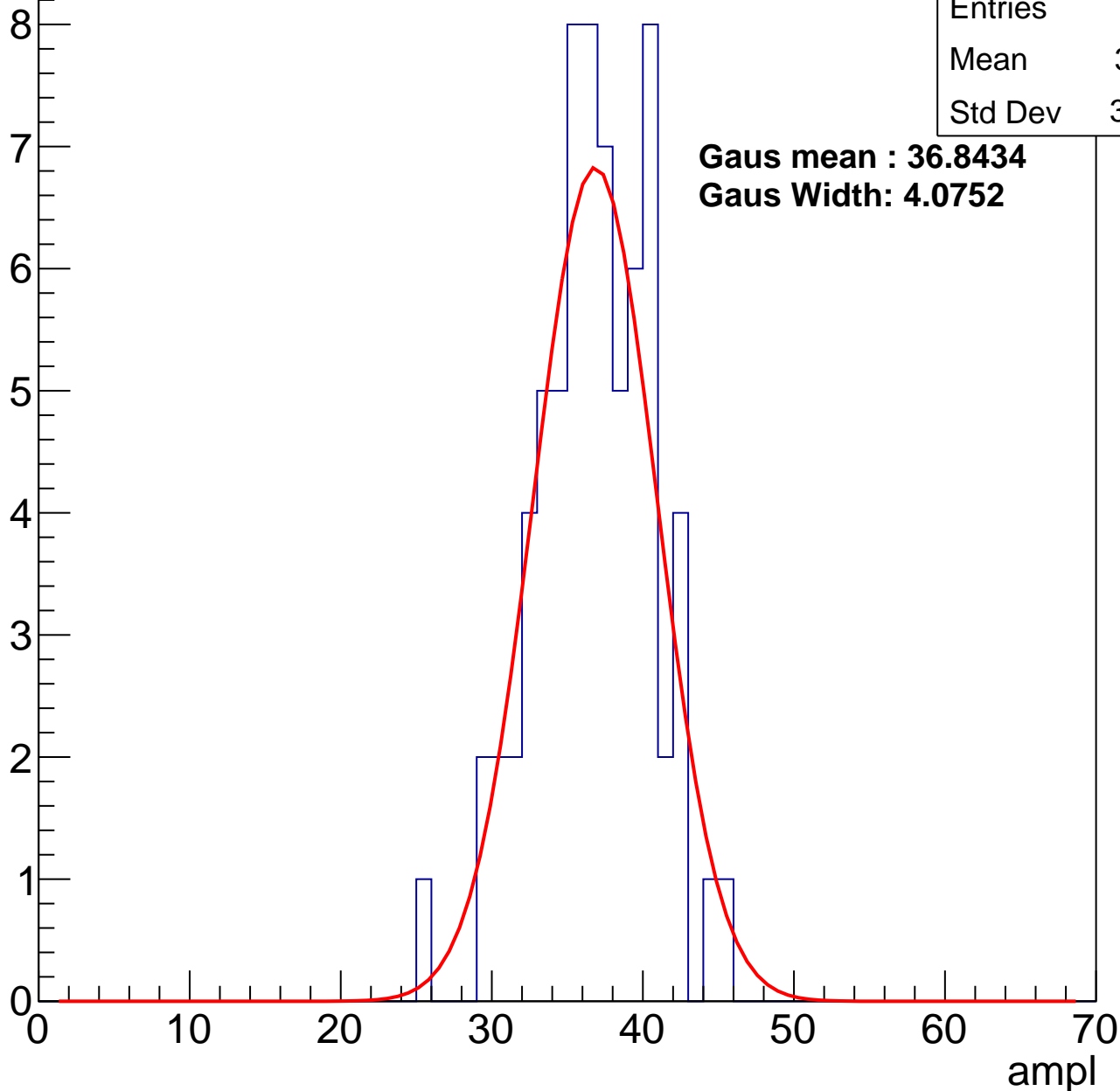
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	36.31
Std Dev	3.789

Gaus mean : 36.8434

Gaus Width: 4.0752



B1L100S, U15-ch49, adc2

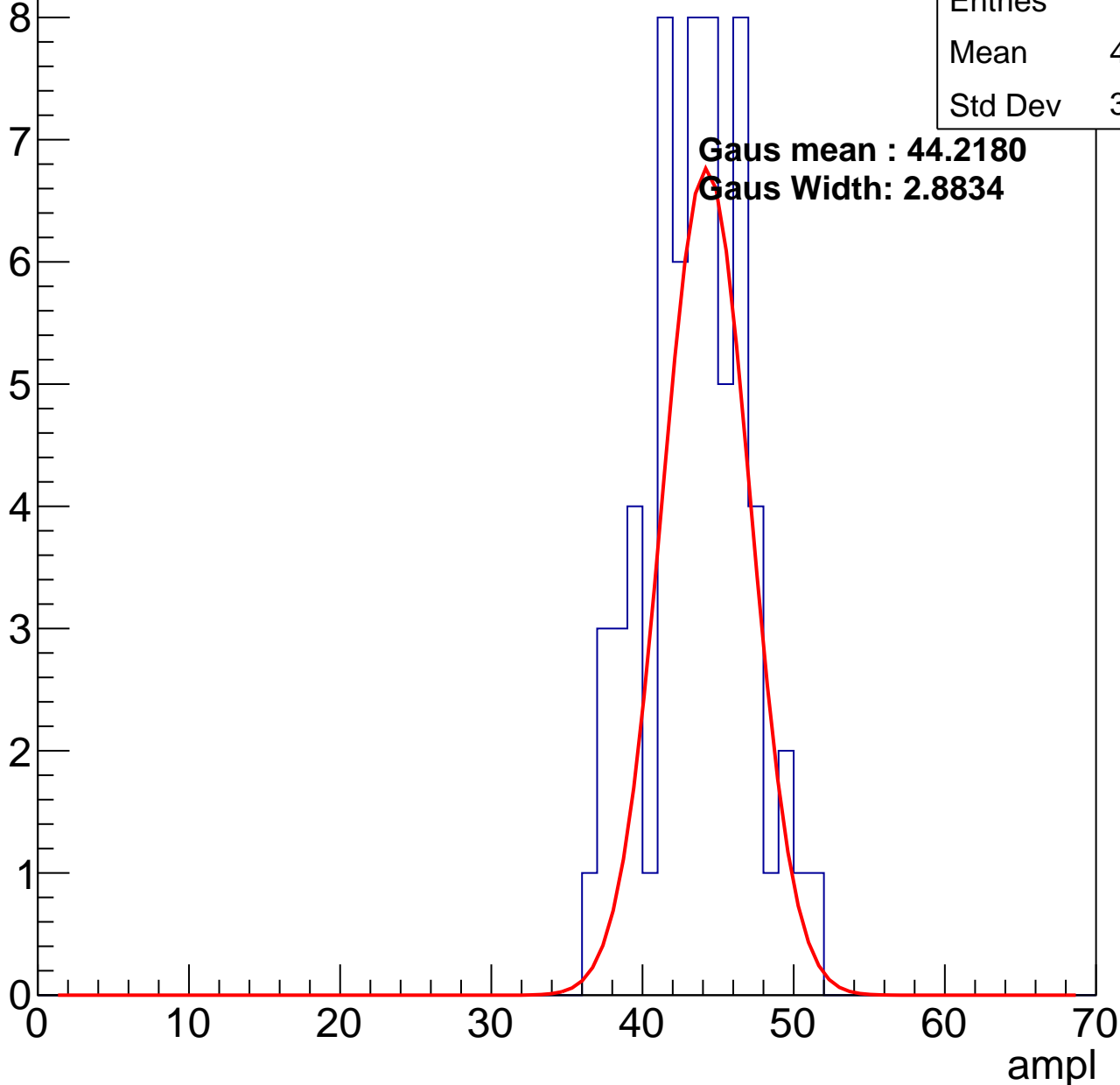
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.14
Std Dev	3.344

Gaus mean : 44.2180

Gaus Width: 2.8834

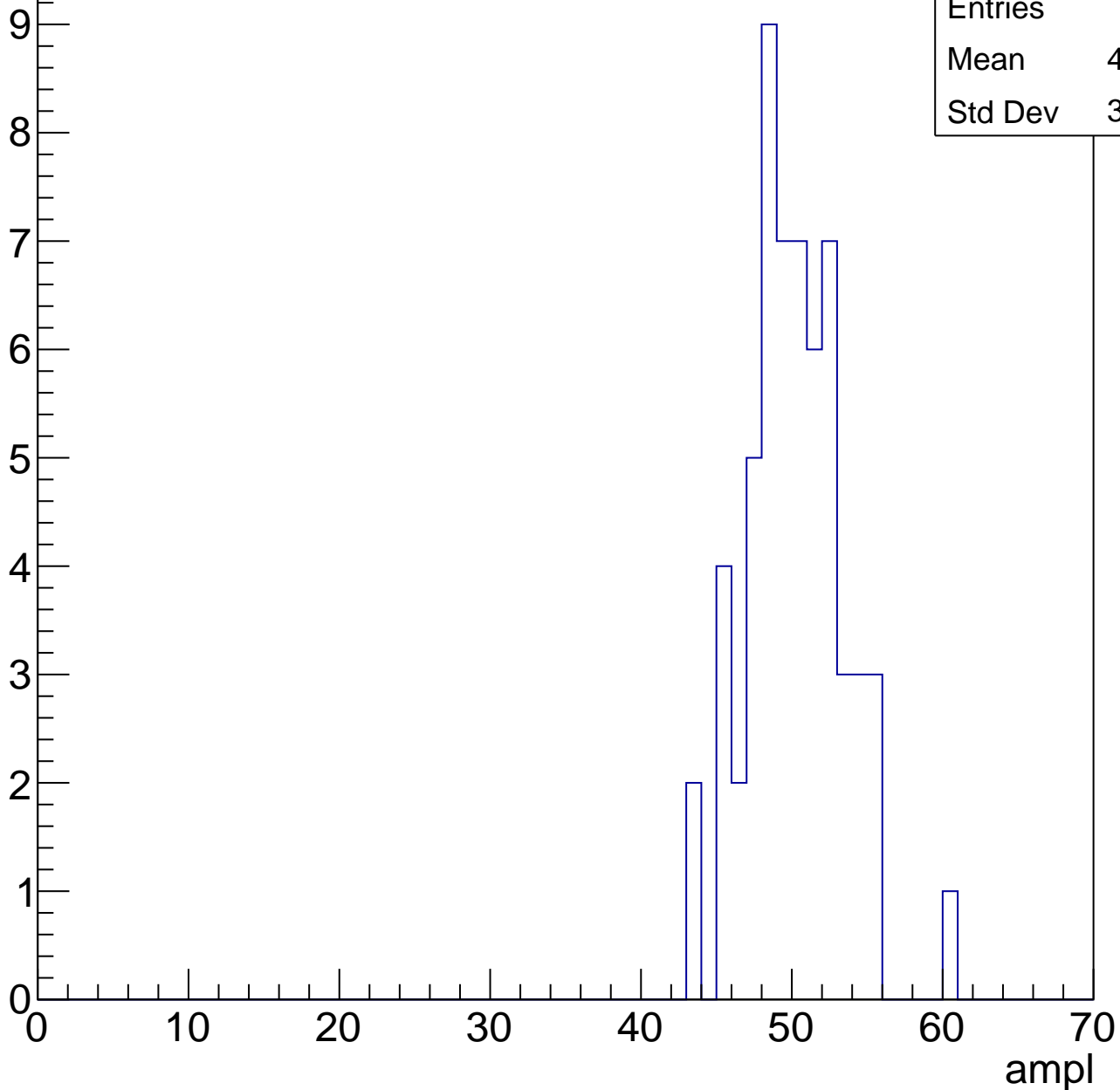


B1L100S, U15-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	49.73
Std Dev	3.183

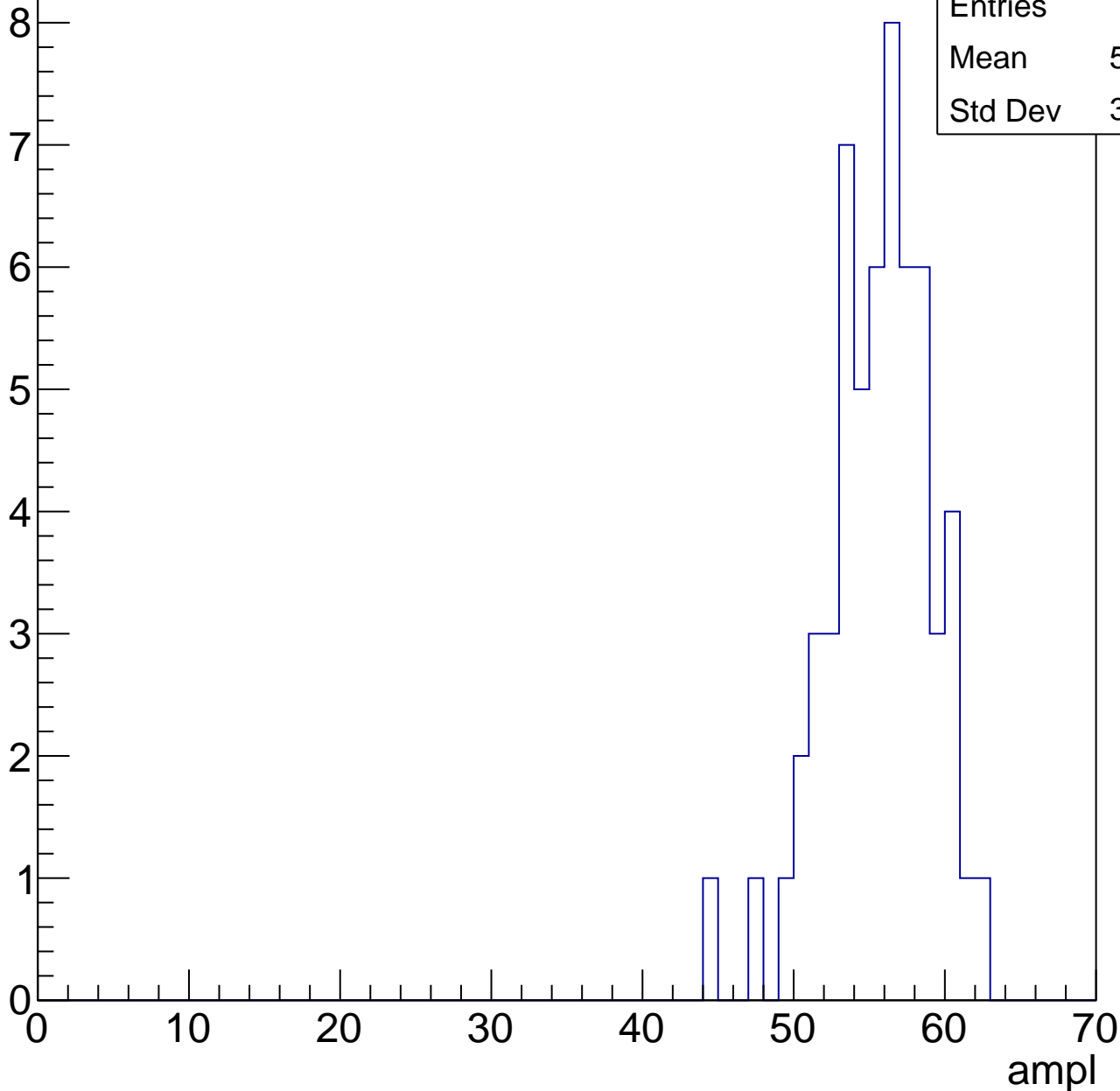


B1L100S, U15-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	55.14
Std Dev	3.466



B1L100S, U15-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

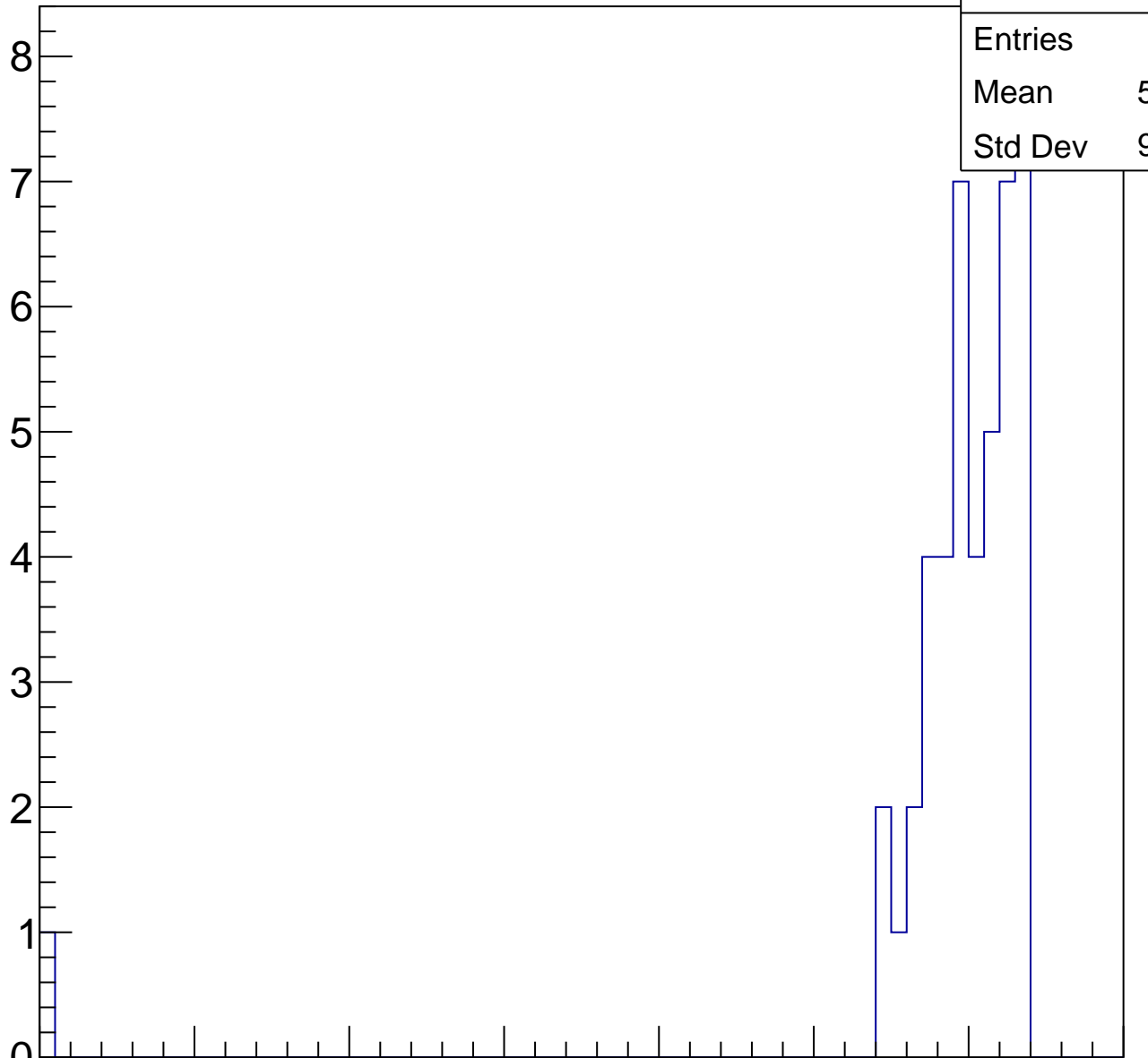
Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.47
Std Dev	9.174

ampl

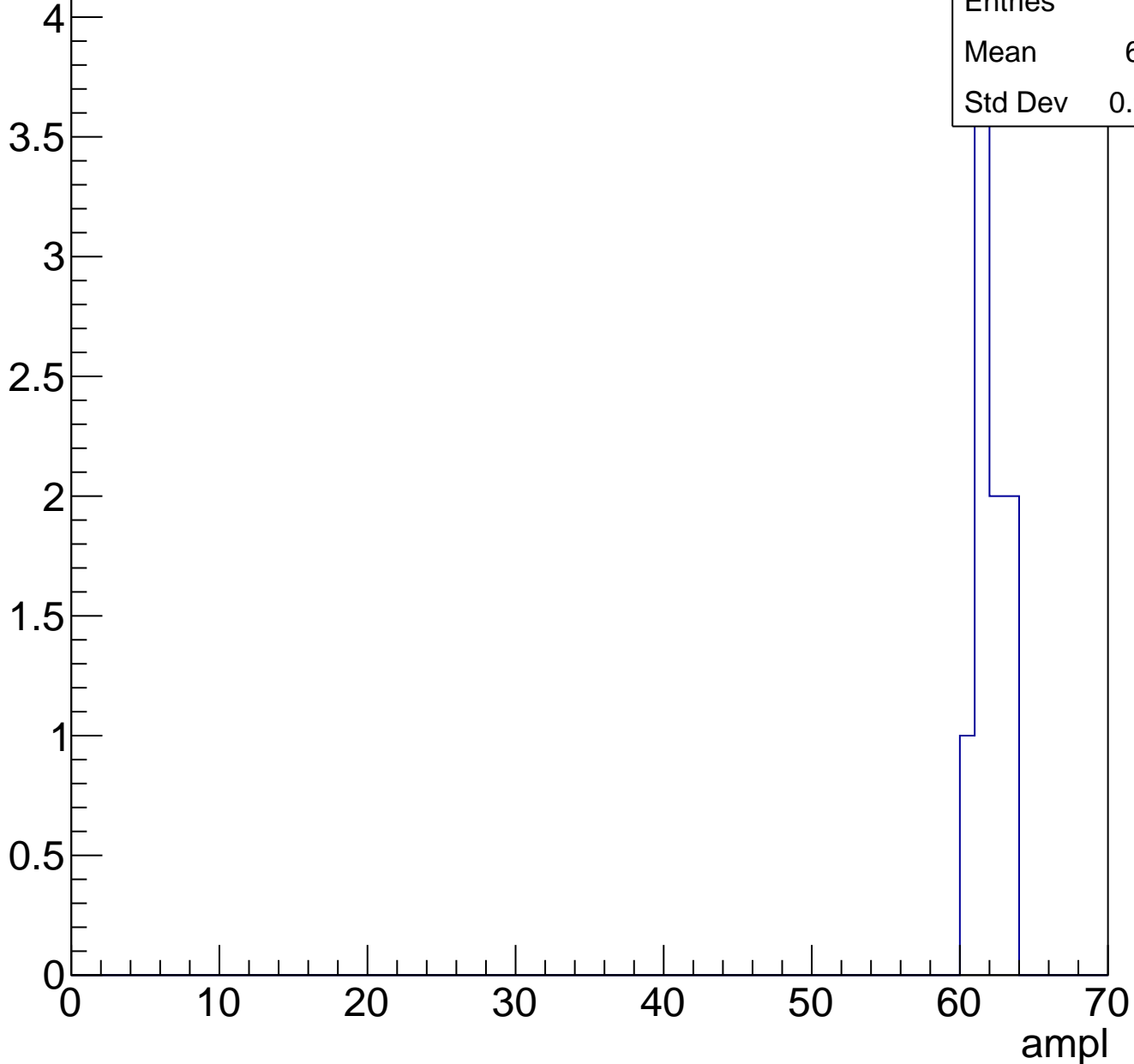
0 10 20 30 40 50 60 70



B1L100S, U15-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch50, adc0

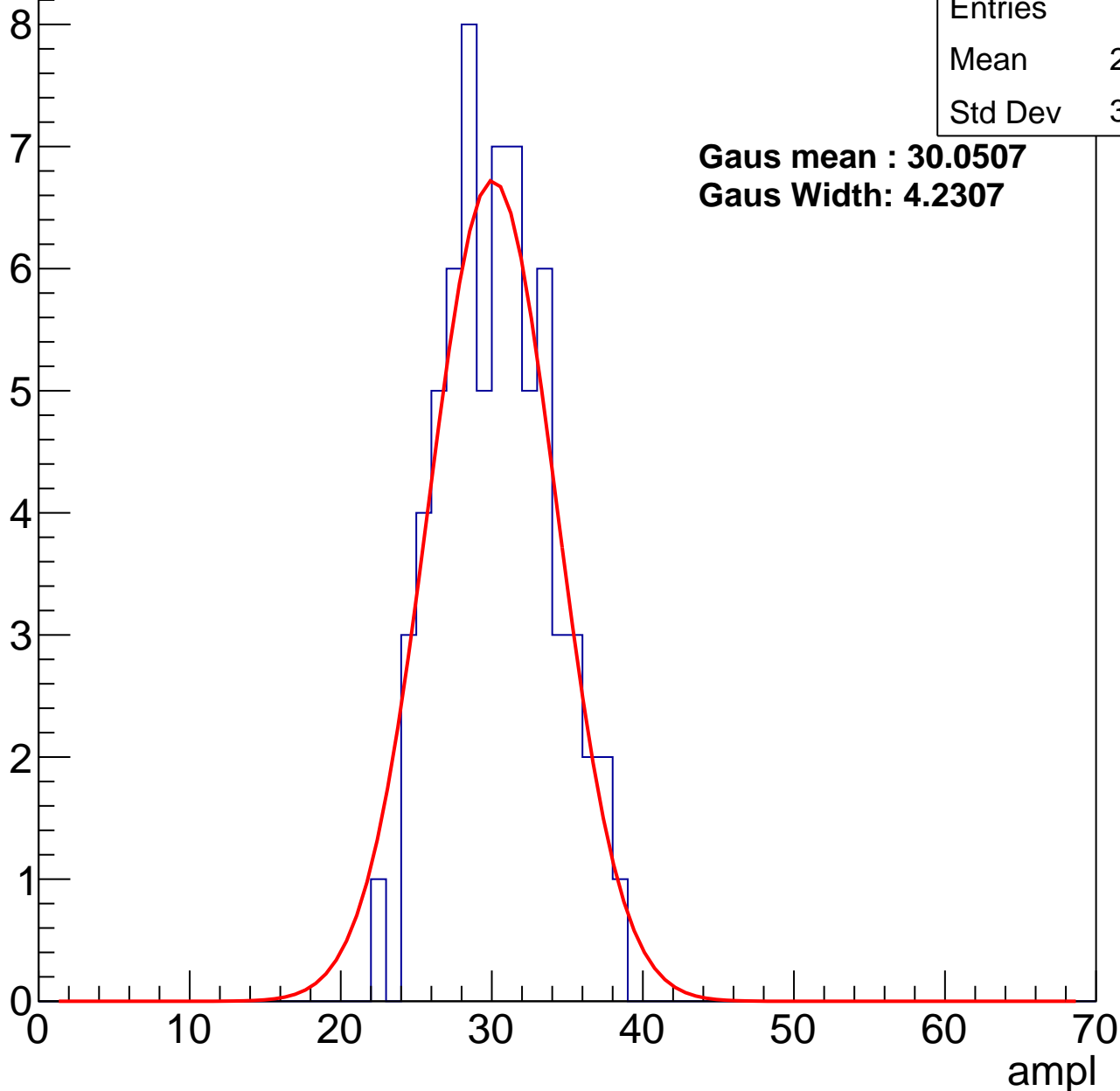
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.87
Std Dev	3.597

Gaus mean : 30.0507

Gaus Width: 4.2307



B1L100S, U15-ch50, adc1

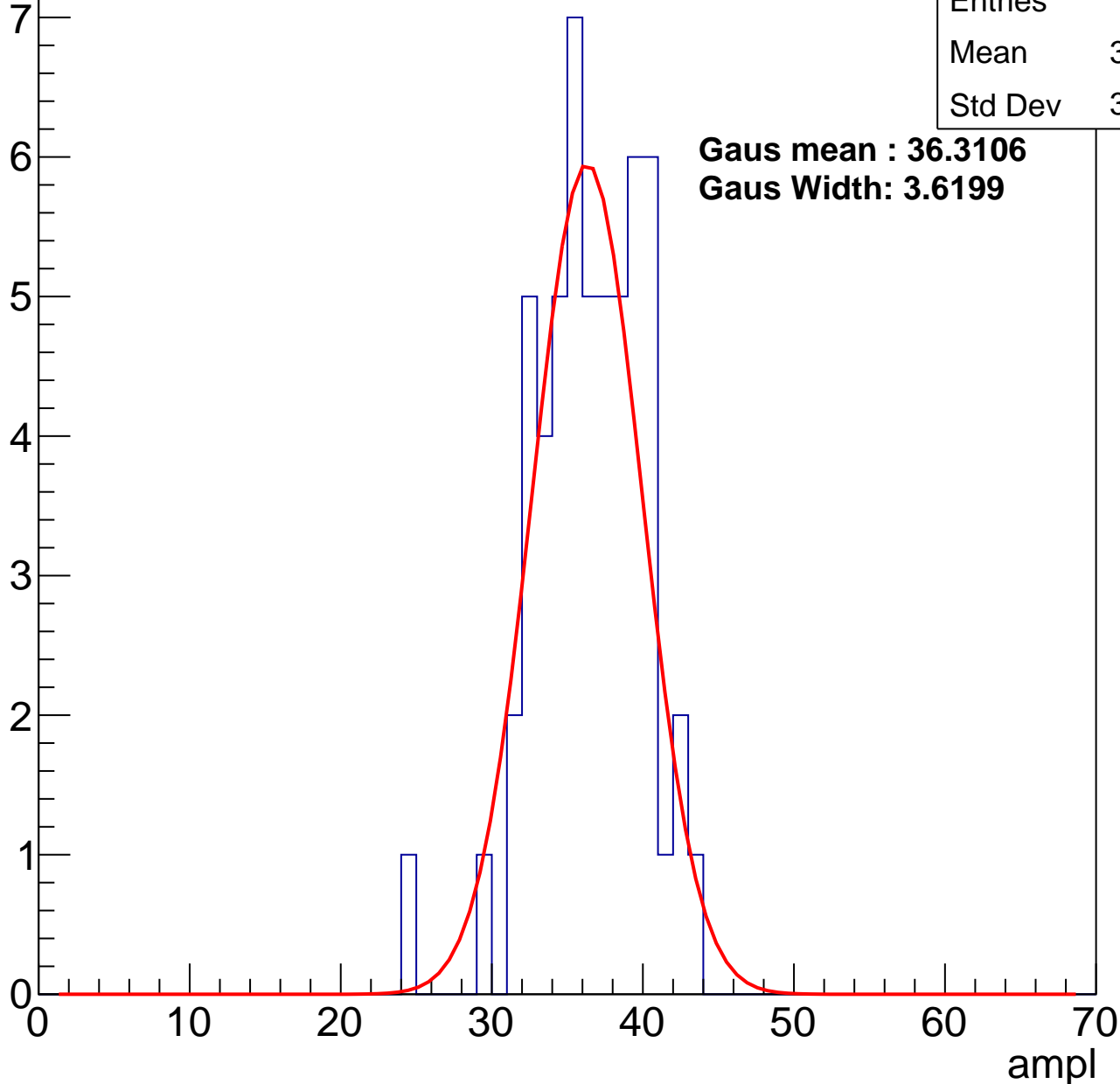
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	36.05
Std Dev	3.553

Gaus mean : 36.3106

Gaus Width: 3.6199



B1L100S, U15-ch50, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	42.72
Std Dev	3.981

Gaus mean : 42.7937

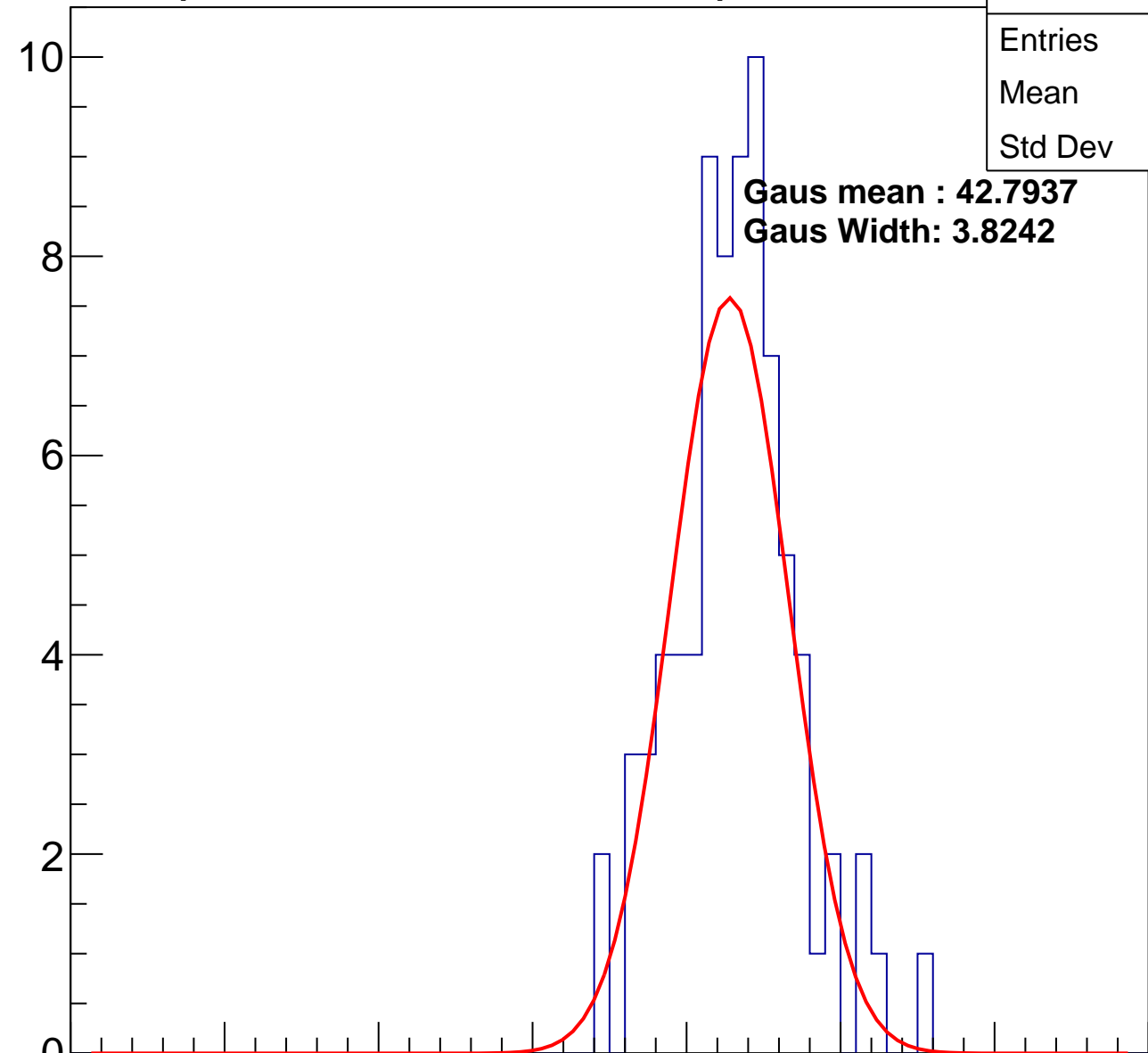
Gaus Width: 3.8242

Entry

10
8
6
4
2
0

ampl

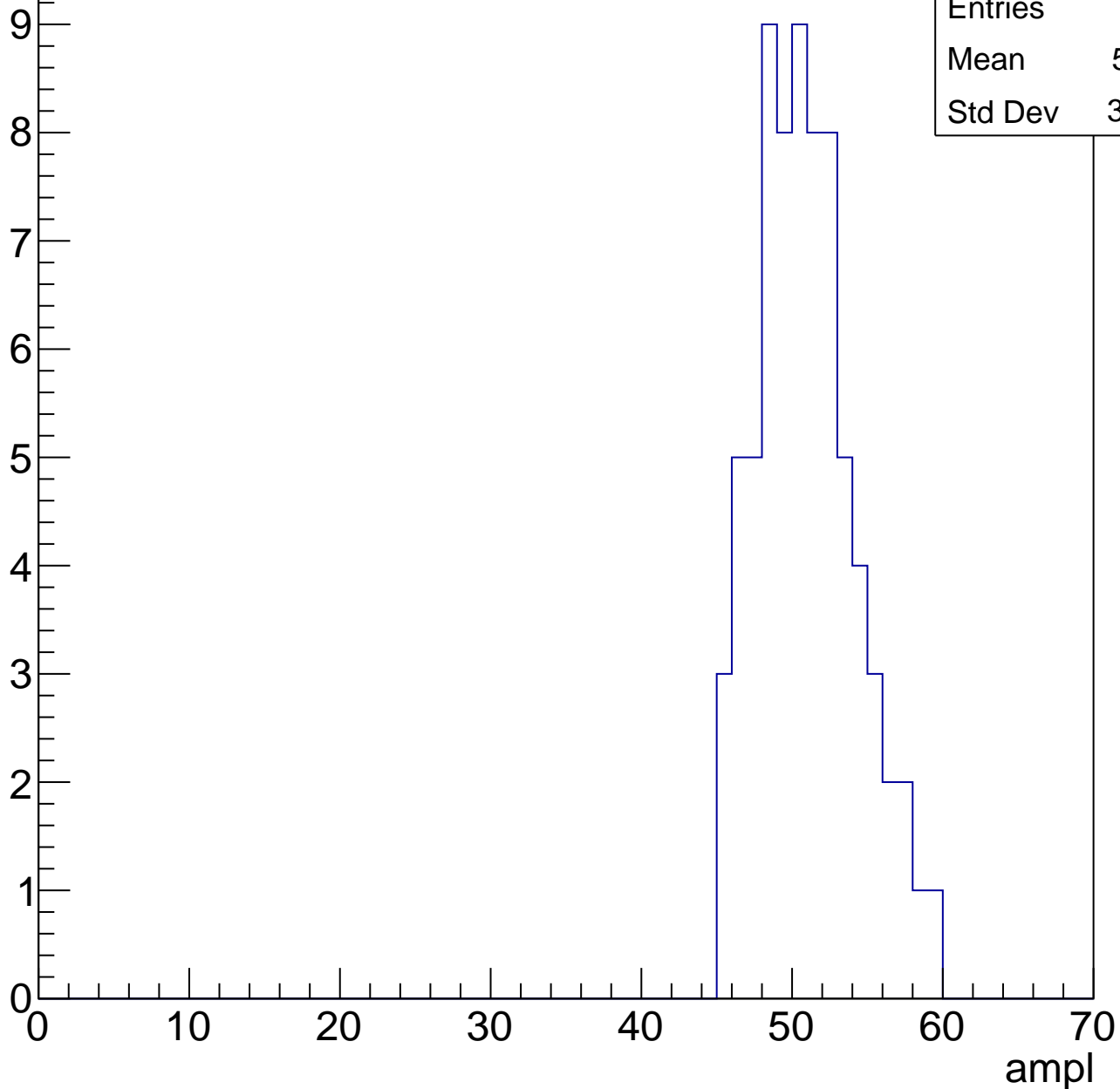
0 10 20 30 40 50 60 70



B1L100S, U15-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



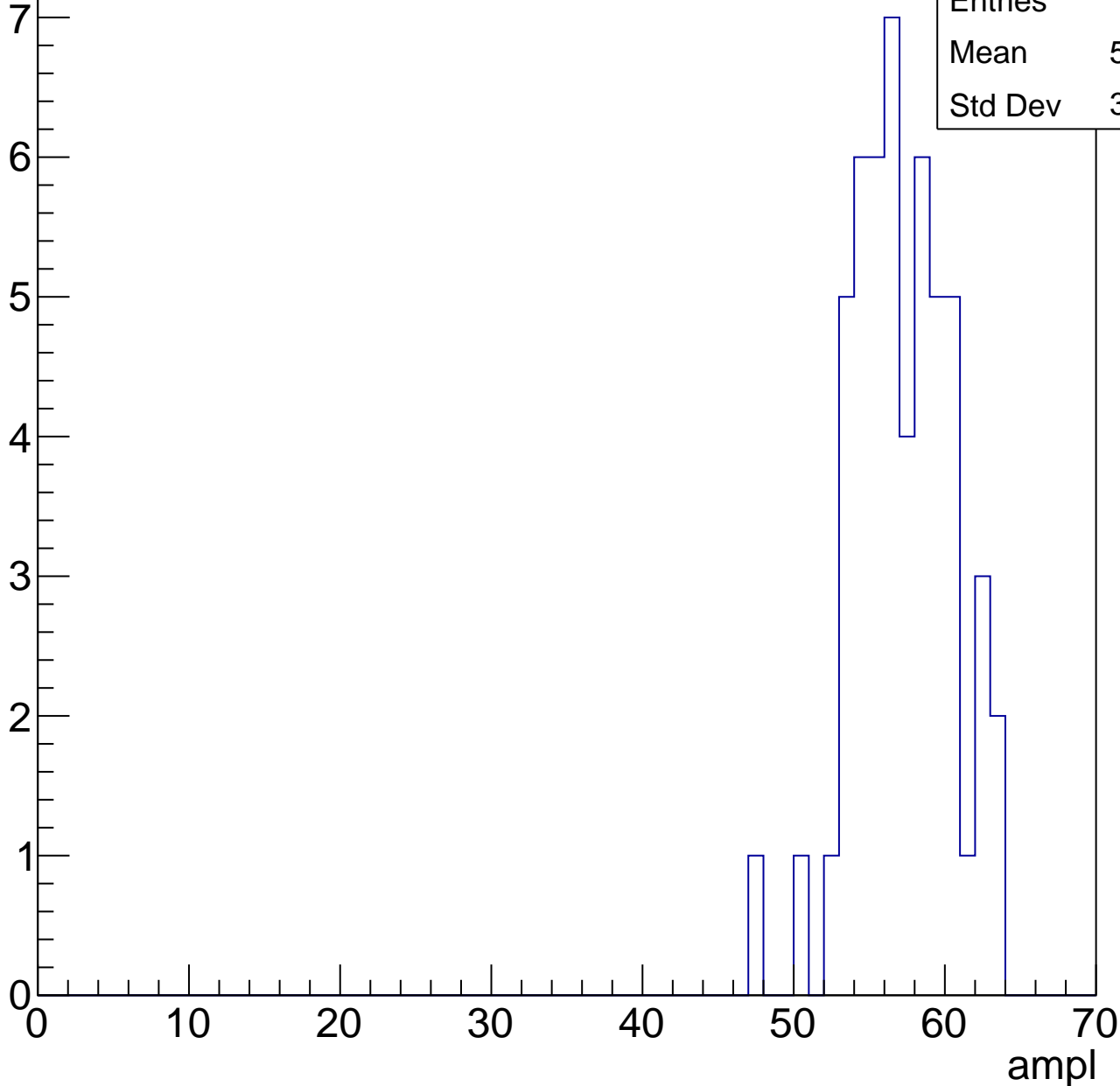
Entries	73
Mean	50.51
Std Dev	3.236

B1L100S, U15-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	56.68
Std Dev	3.272

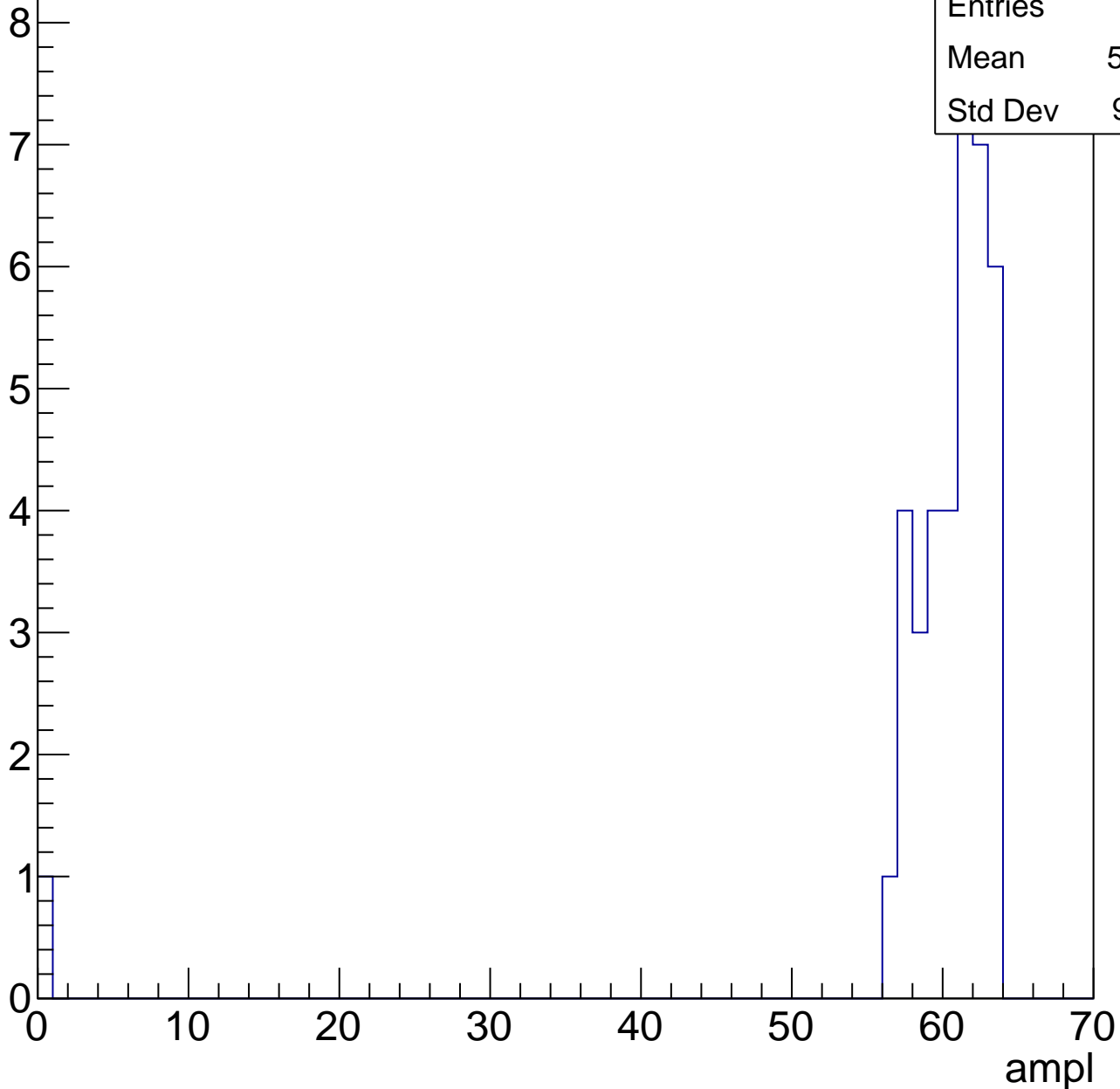


B1L100S, U15-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.79
Std Dev	9.871



B1L100S, U15-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch51, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	27.51
Std Dev	5.897

Gaus mean : 29.0859

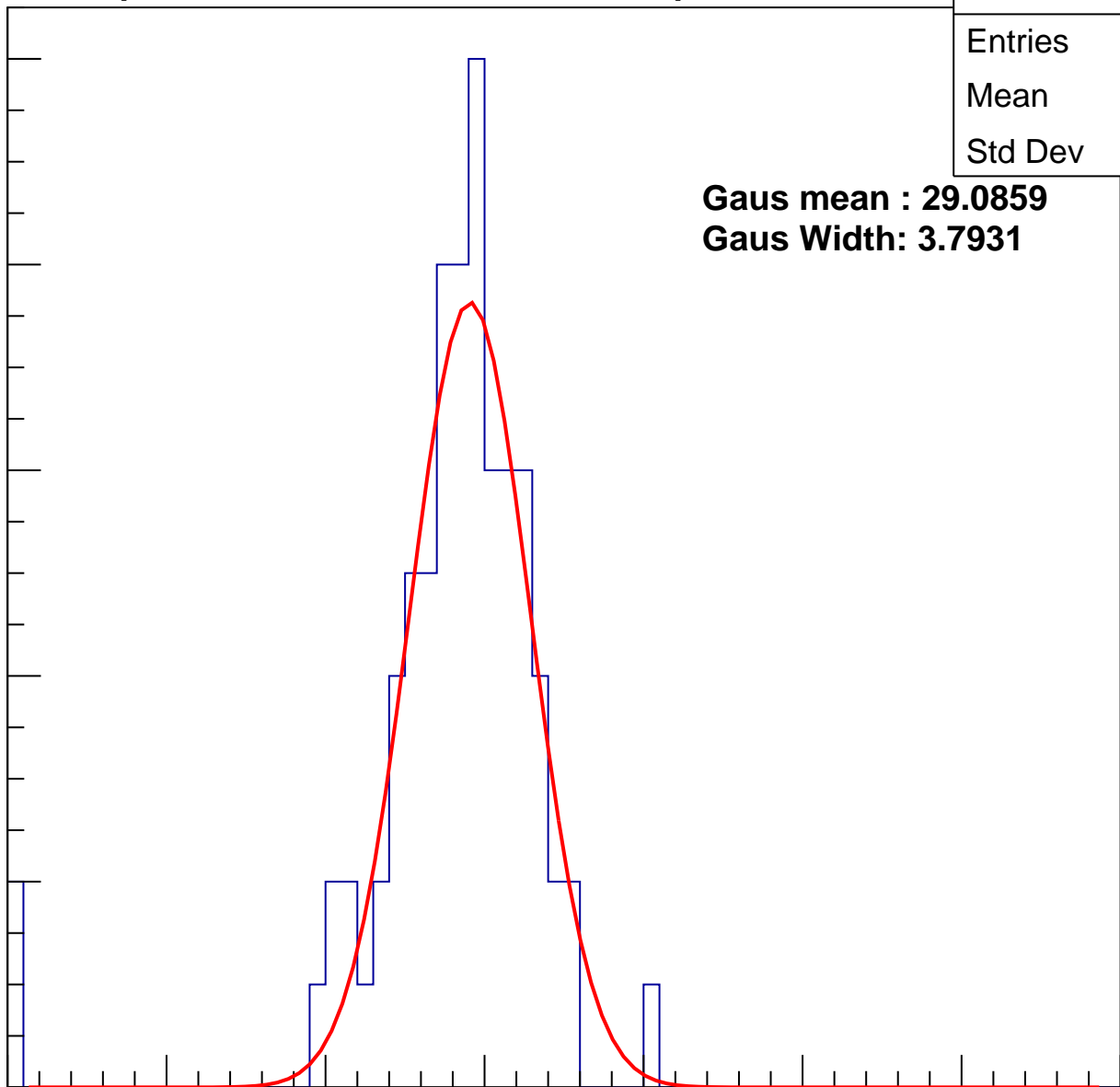
Gaus Width: 3.7931

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



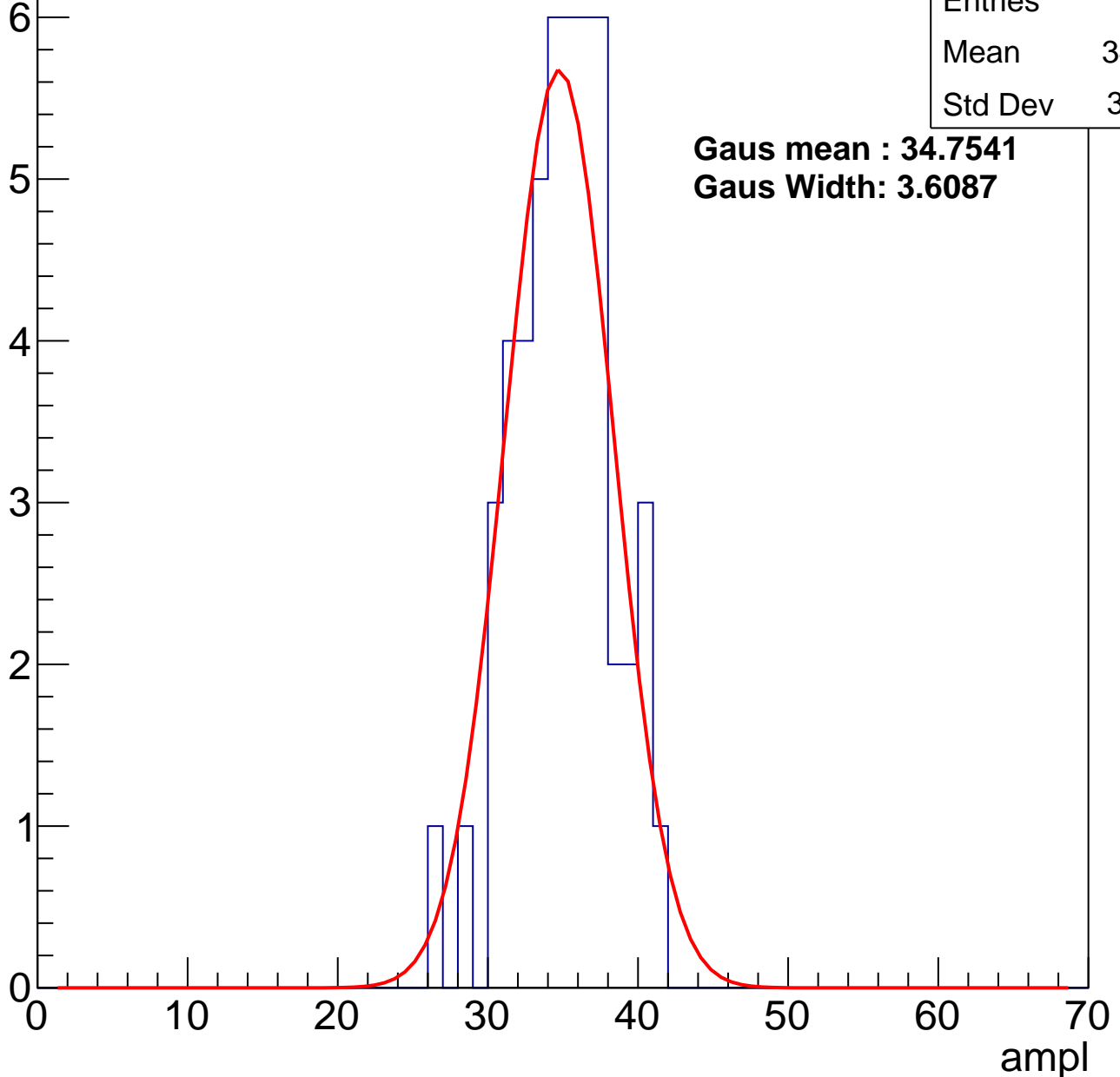
B1L100S, U15-ch51, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	34.56
Std Dev	3.201

Gaus mean : 34.7541
Gaus Width: 3.6087



B1L100S, U15-ch51, adc2

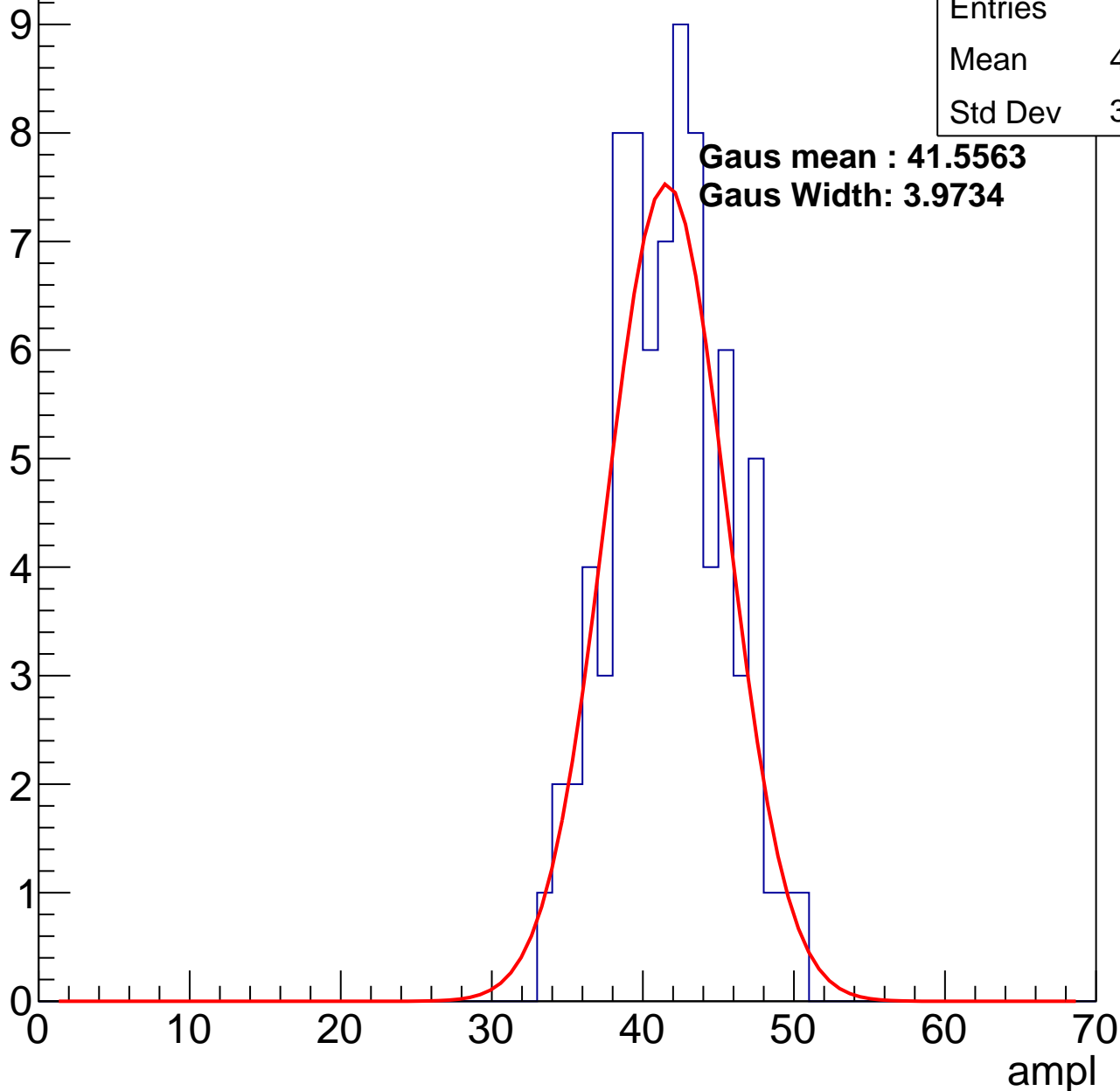
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	41.23
Std Dev	3.748

Gaus mean : 41.5563

Gaus Width: 3.9734

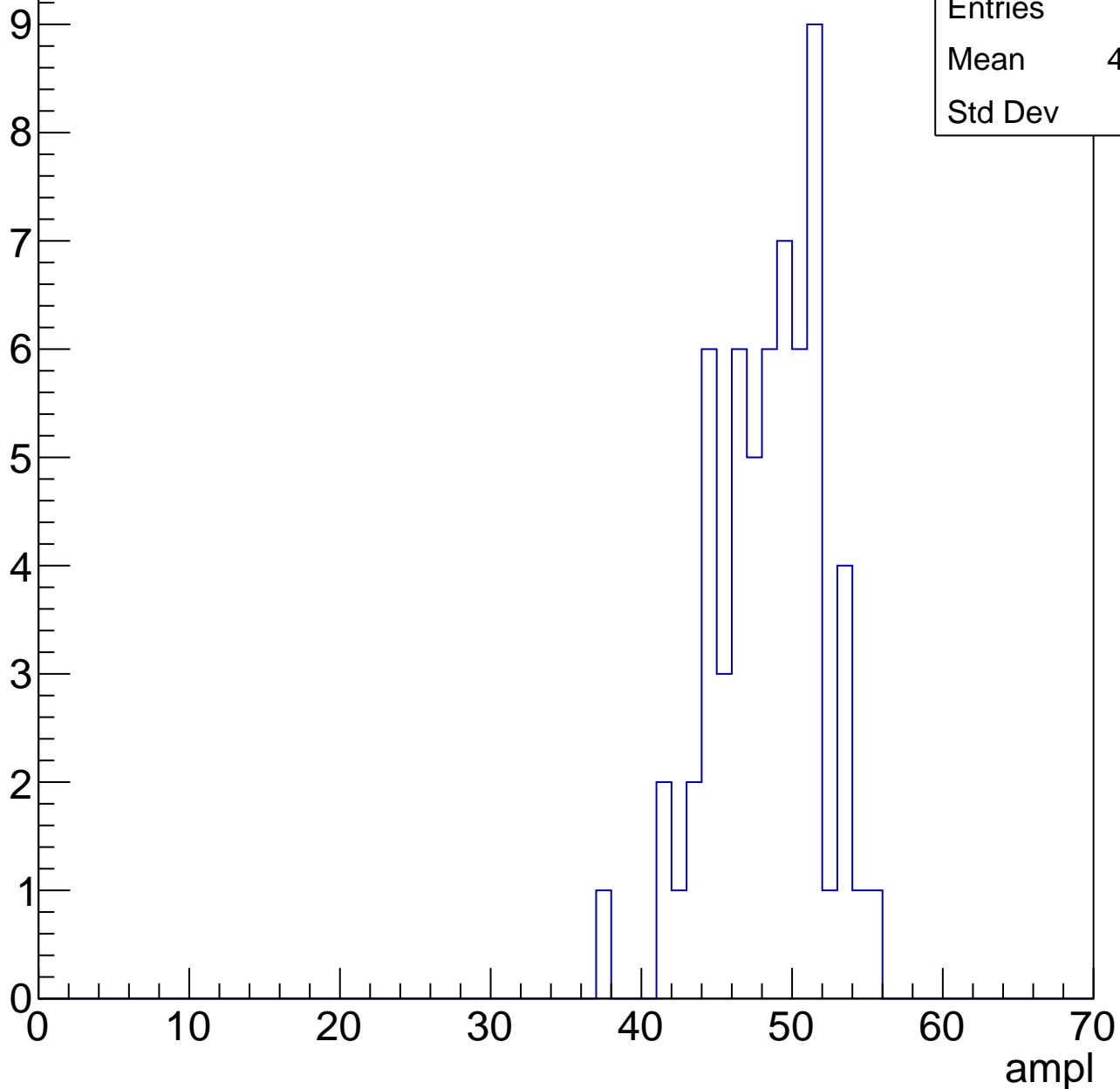


B1L100S, U15-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

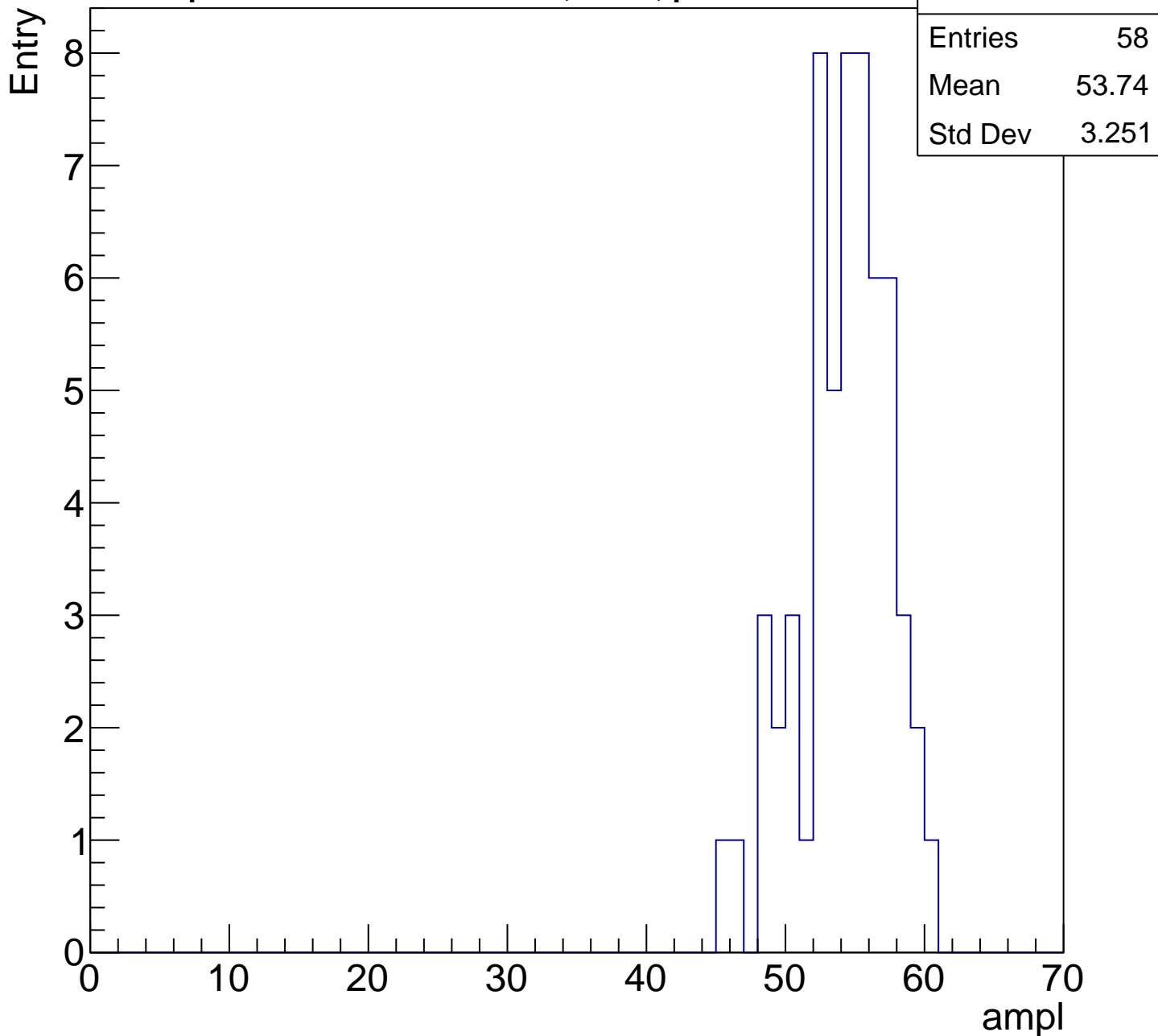
Entry

Entries	61
Mean	47.87
Std Dev	3.55



B1L100S, U15-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch51, adc5

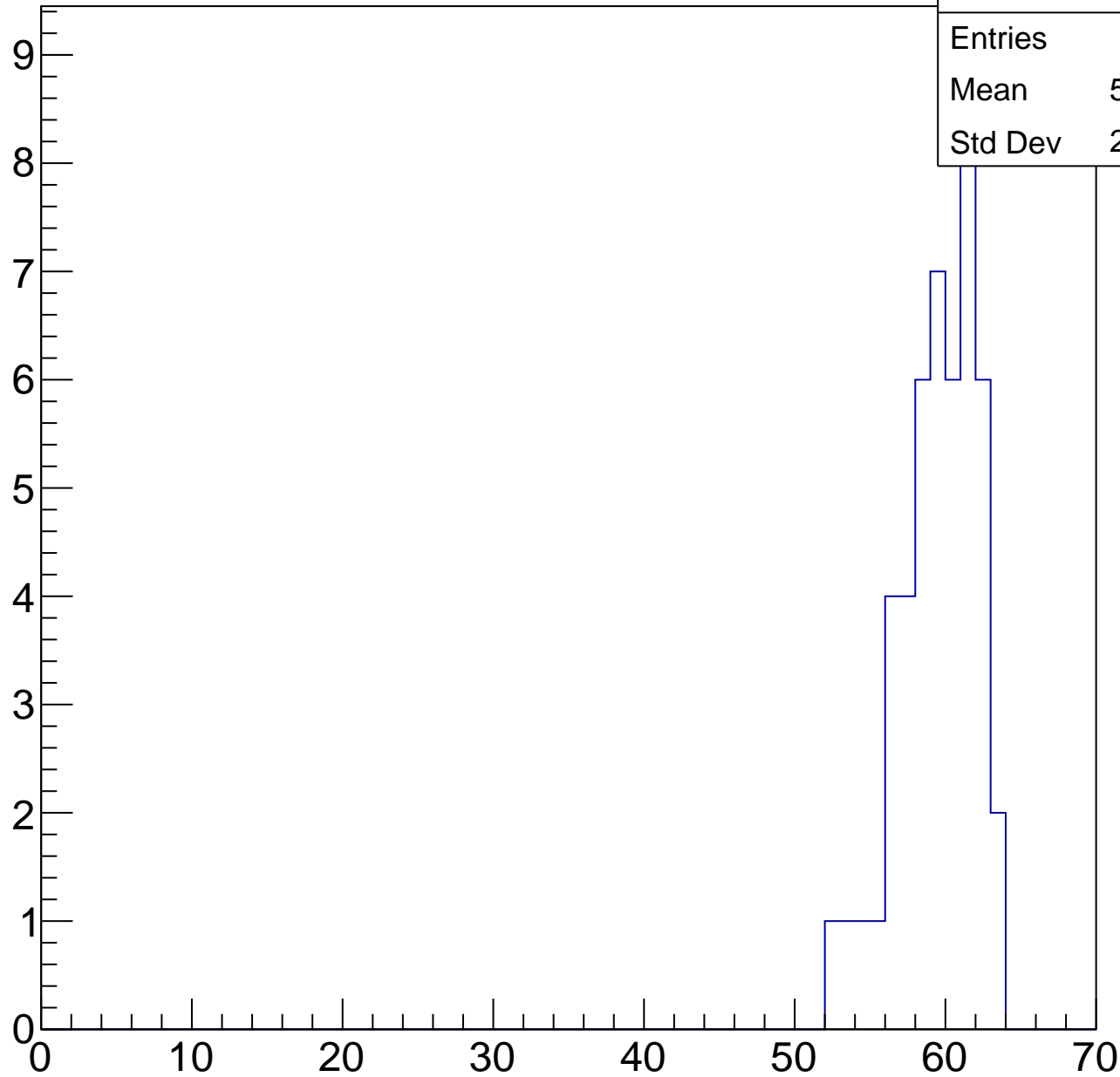
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.04
Std Dev	2.549

ampl



B1L100S, U15-ch51, adc6

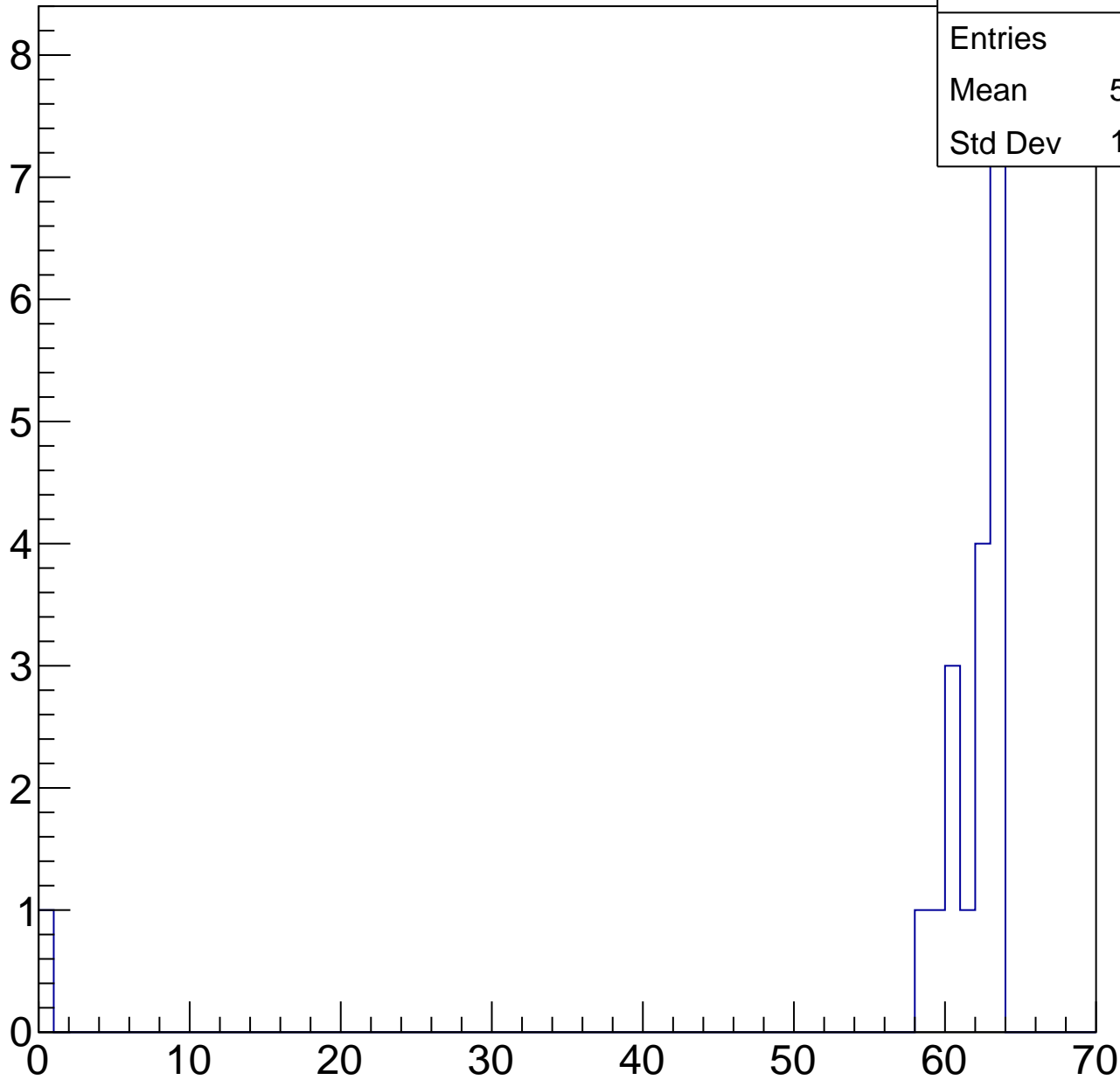
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	19
Mean	58.42
Std Dev	13.85

ampl



B1L100S, U15-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch52, adc0

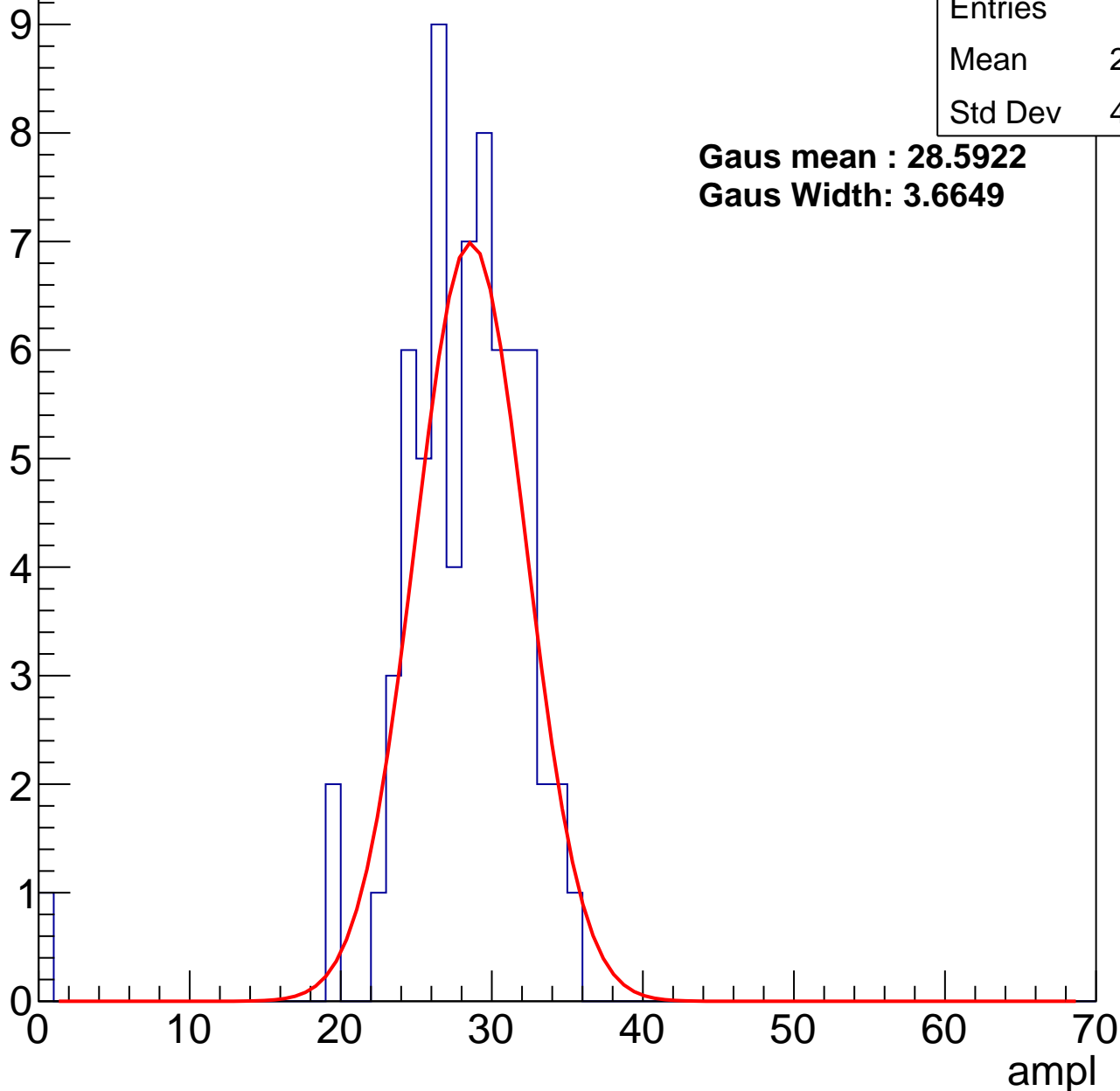
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	27.46
Std Dev	4.772

Gaus mean : 28.5922

Gaus Width: 3.6649



B1L100S, U15-ch52, adc1

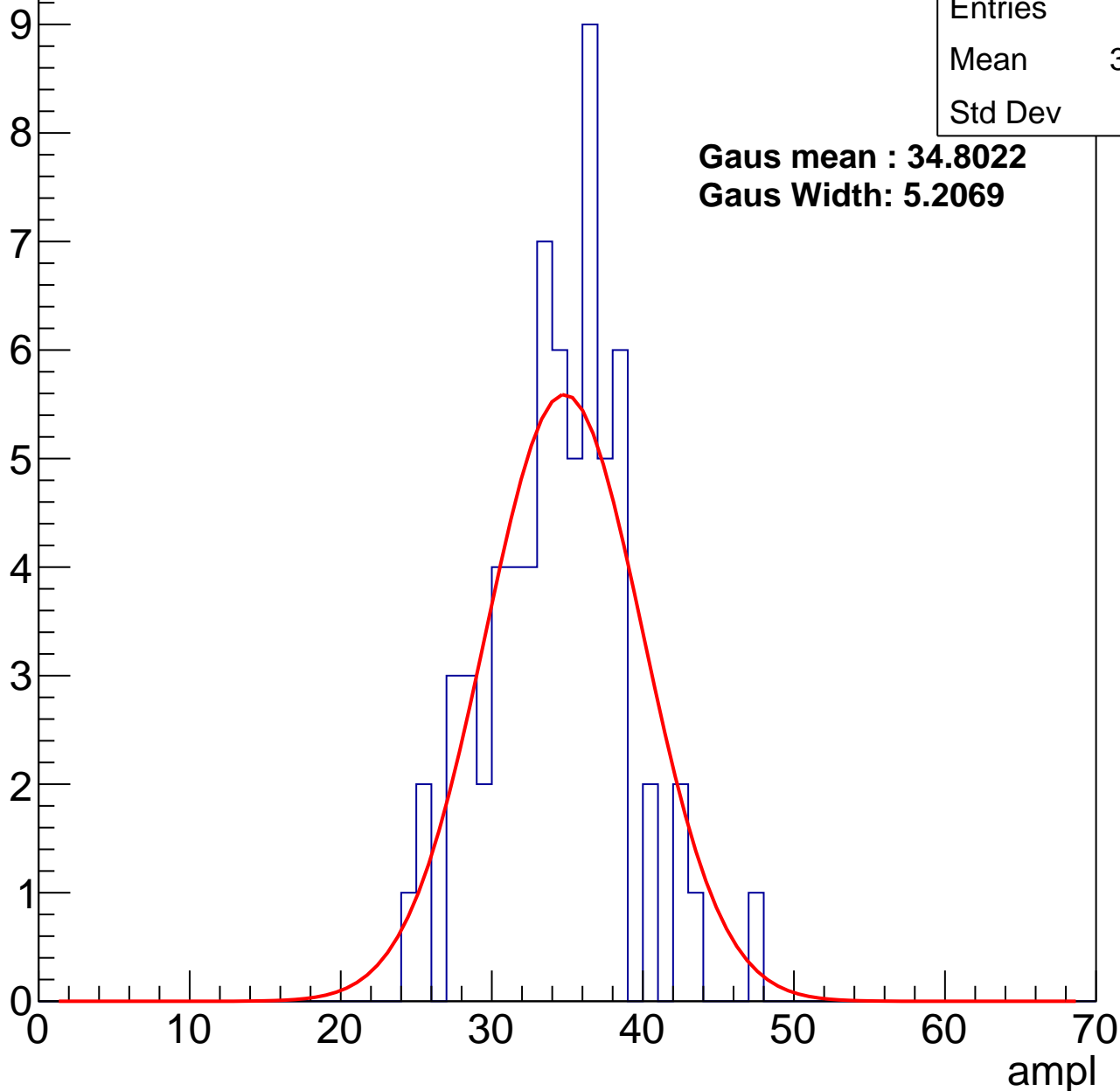
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	33.88
Std Dev	4.44

Gaus mean : 34.8022

Gaus Width: 5.2069



B1L100S, U15-ch52, adc2

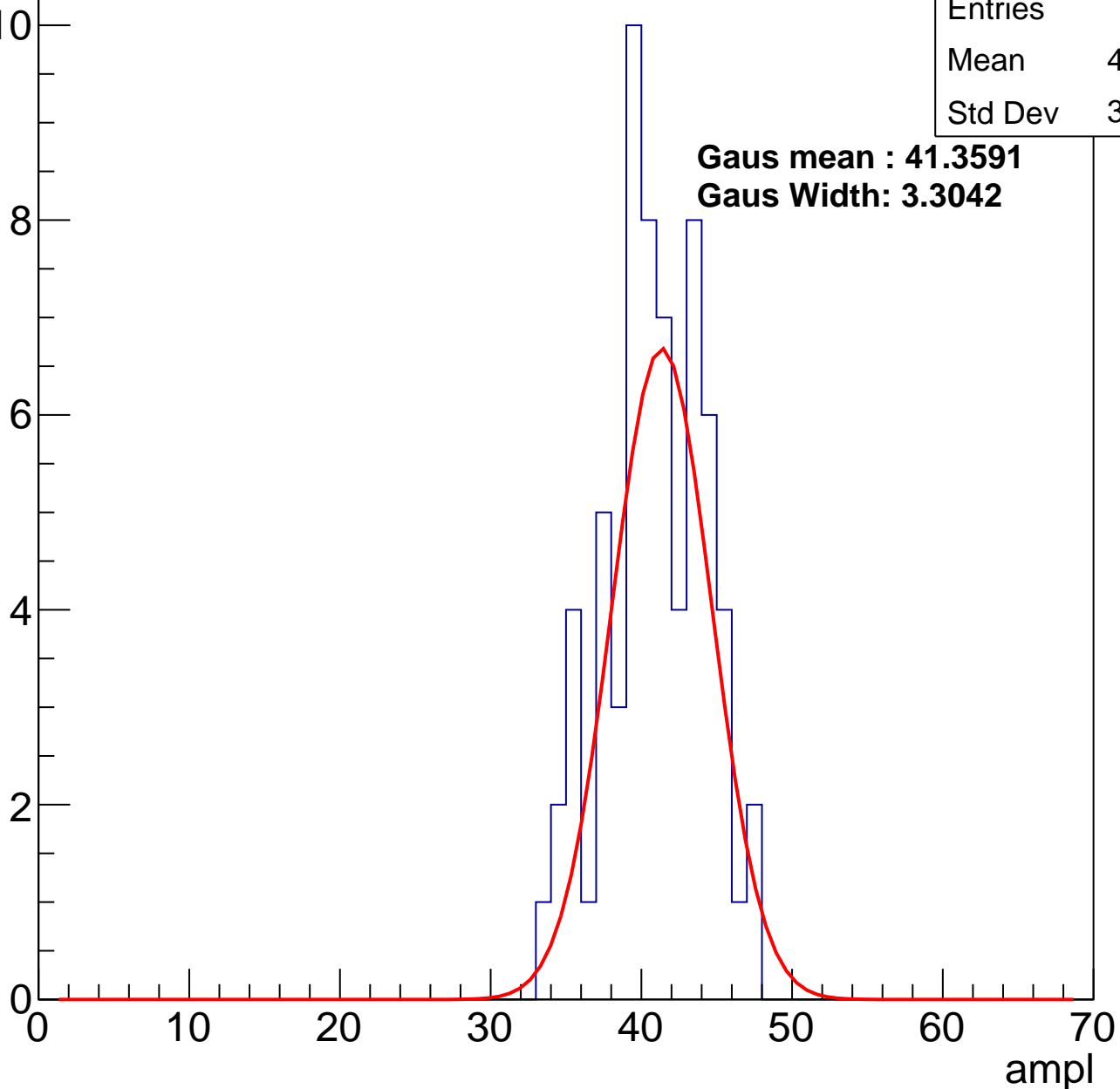
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	40.44
Std Dev	3.294

Gaus mean : 41.3591

Gaus Width: 3.3042

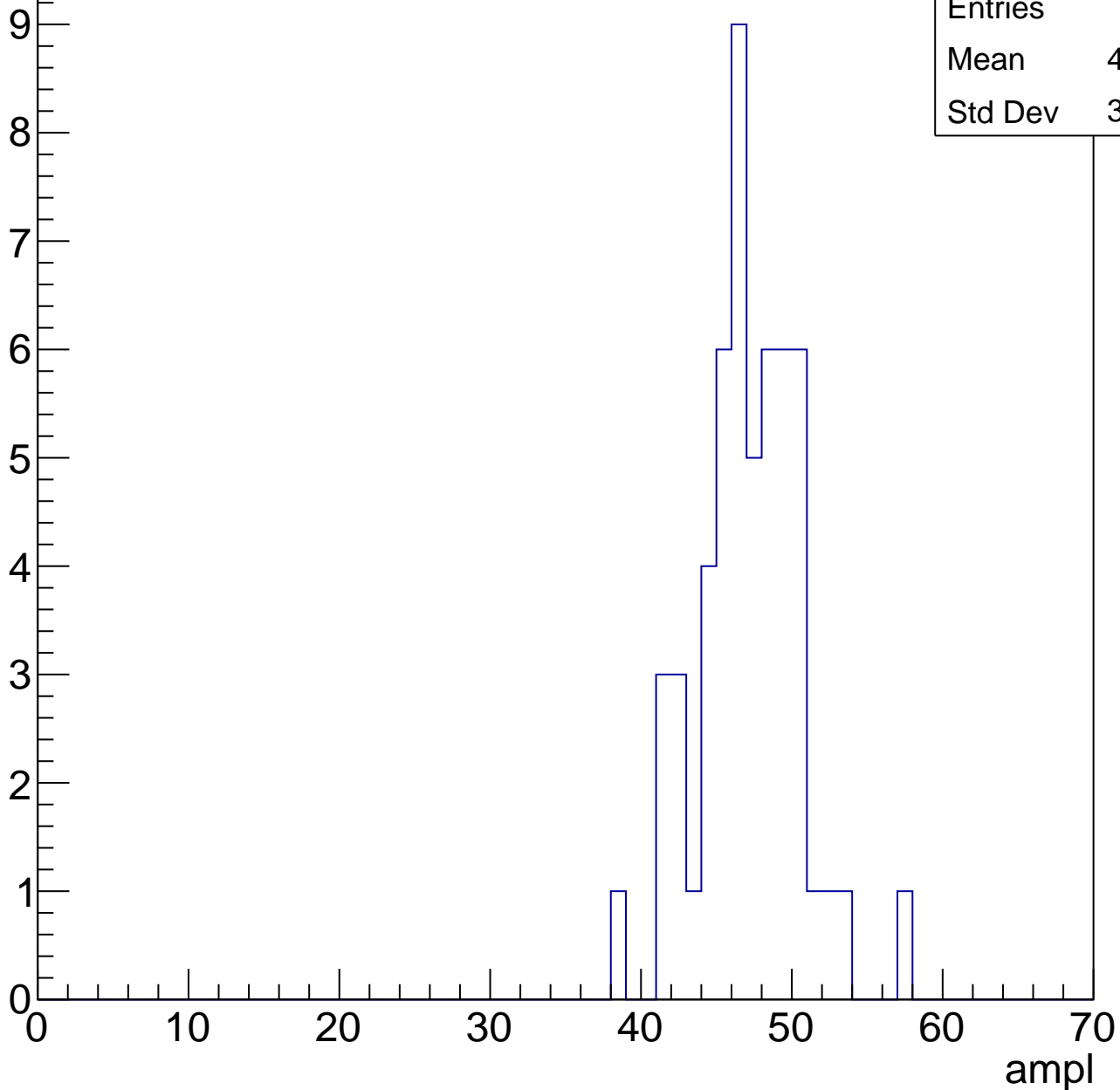


B1L100S, U15-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	46.67
Std Dev	3.344

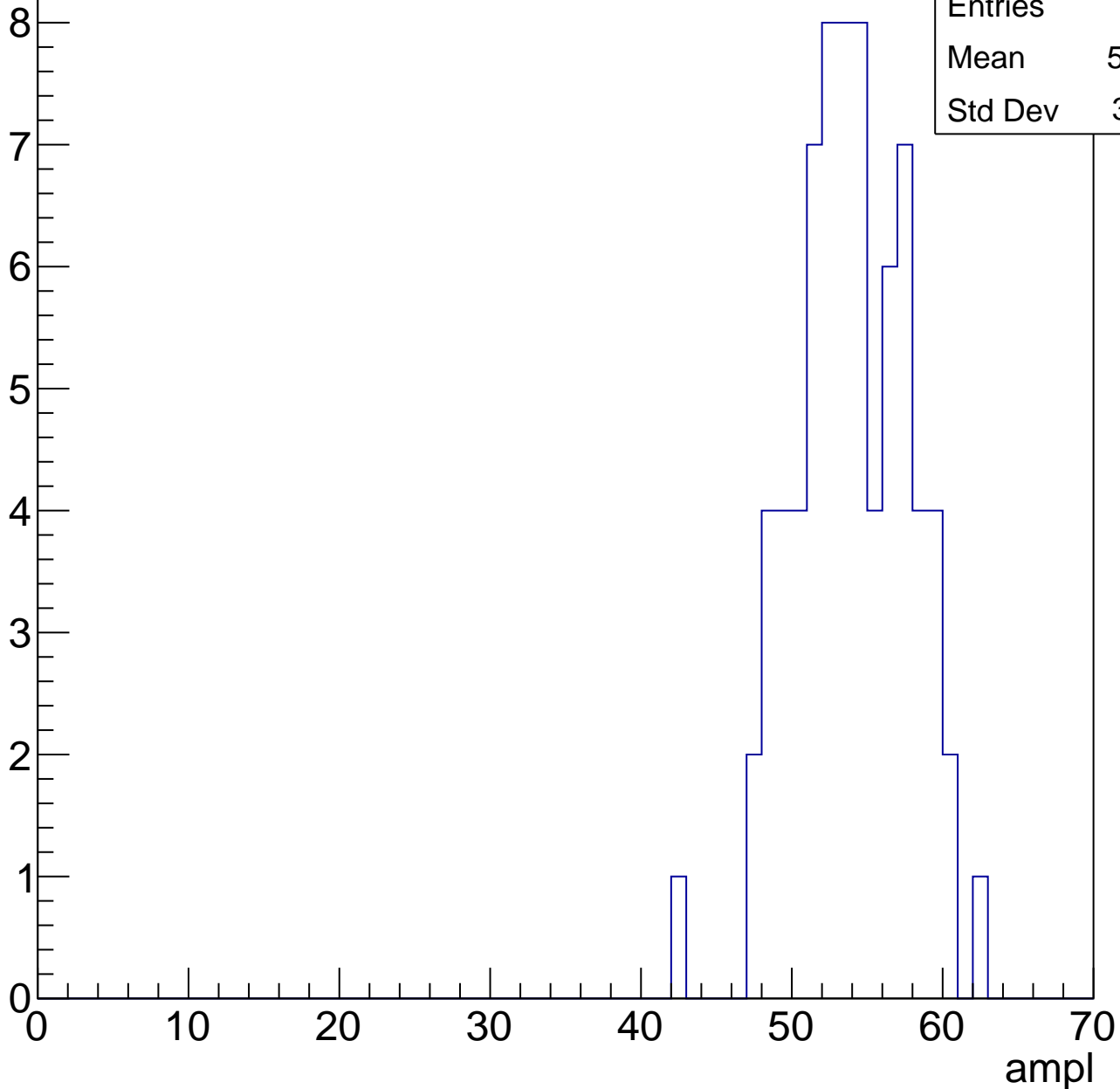


B1L100S, U15-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	53.49
Std Dev	3.721

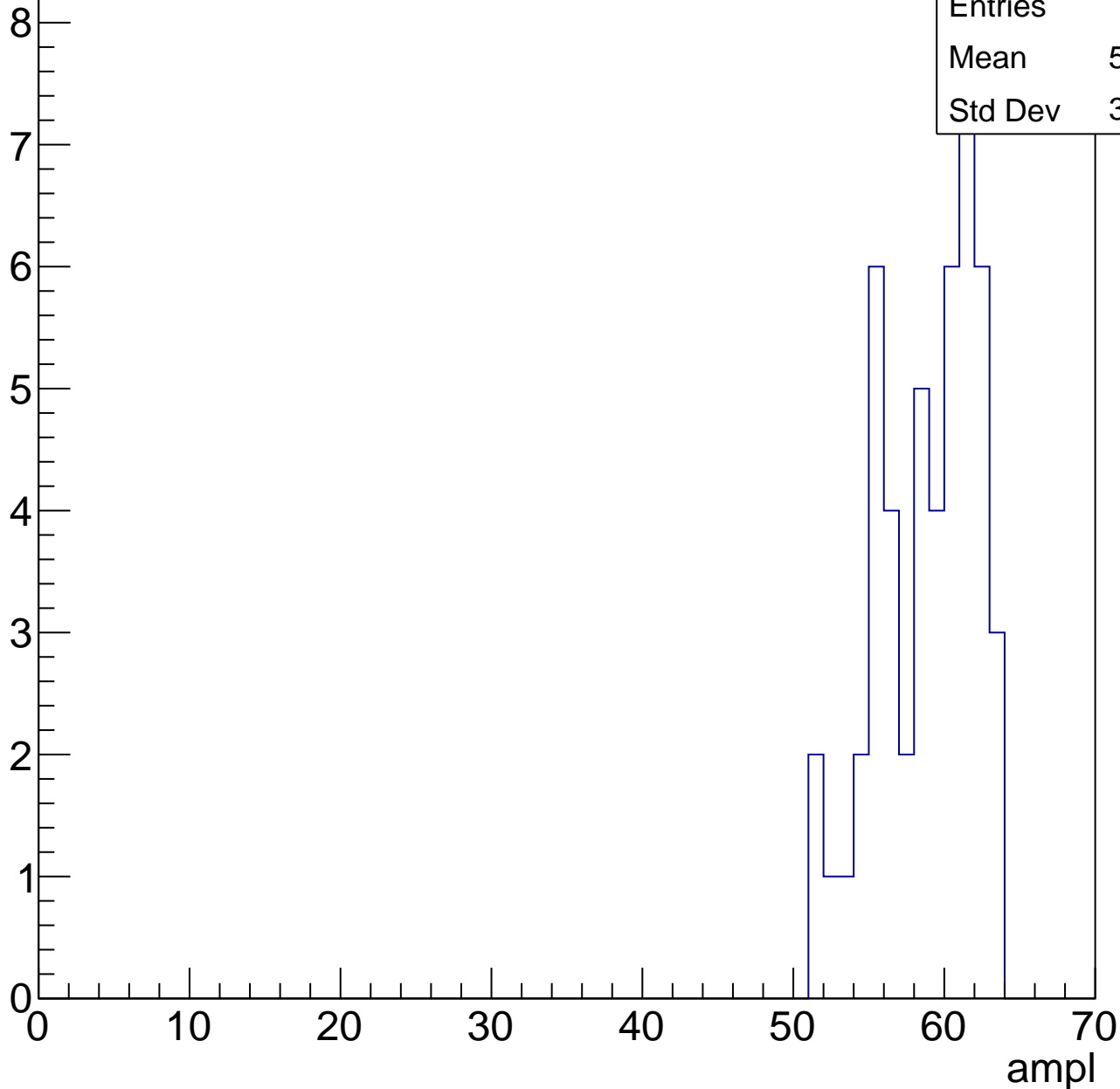


B1L100S, U15-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.36
Std Dev	3.248

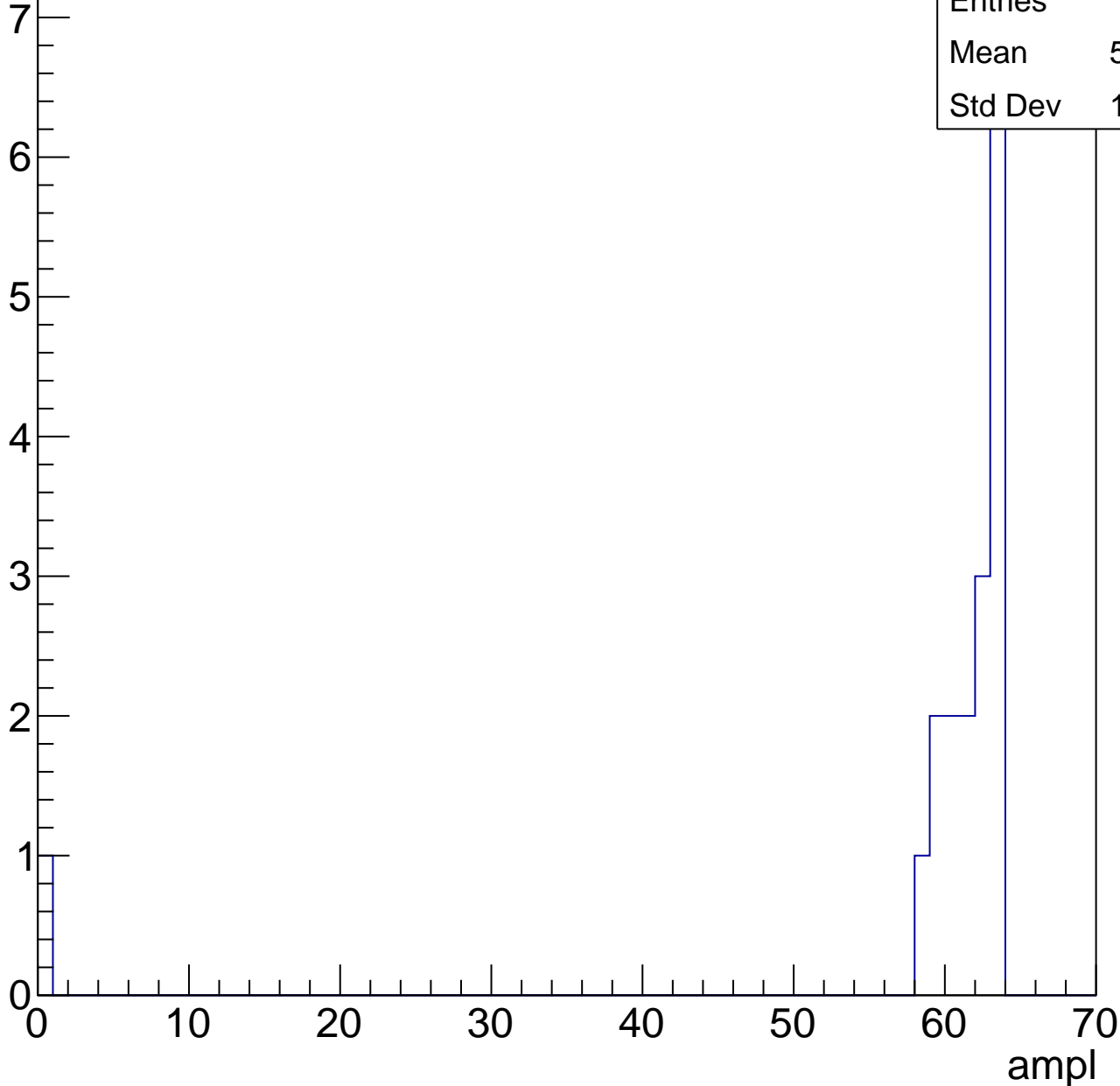


B1L100S, U15-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	58.06
Std Dev	14.17



B1L100S, U15-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch53, adc0

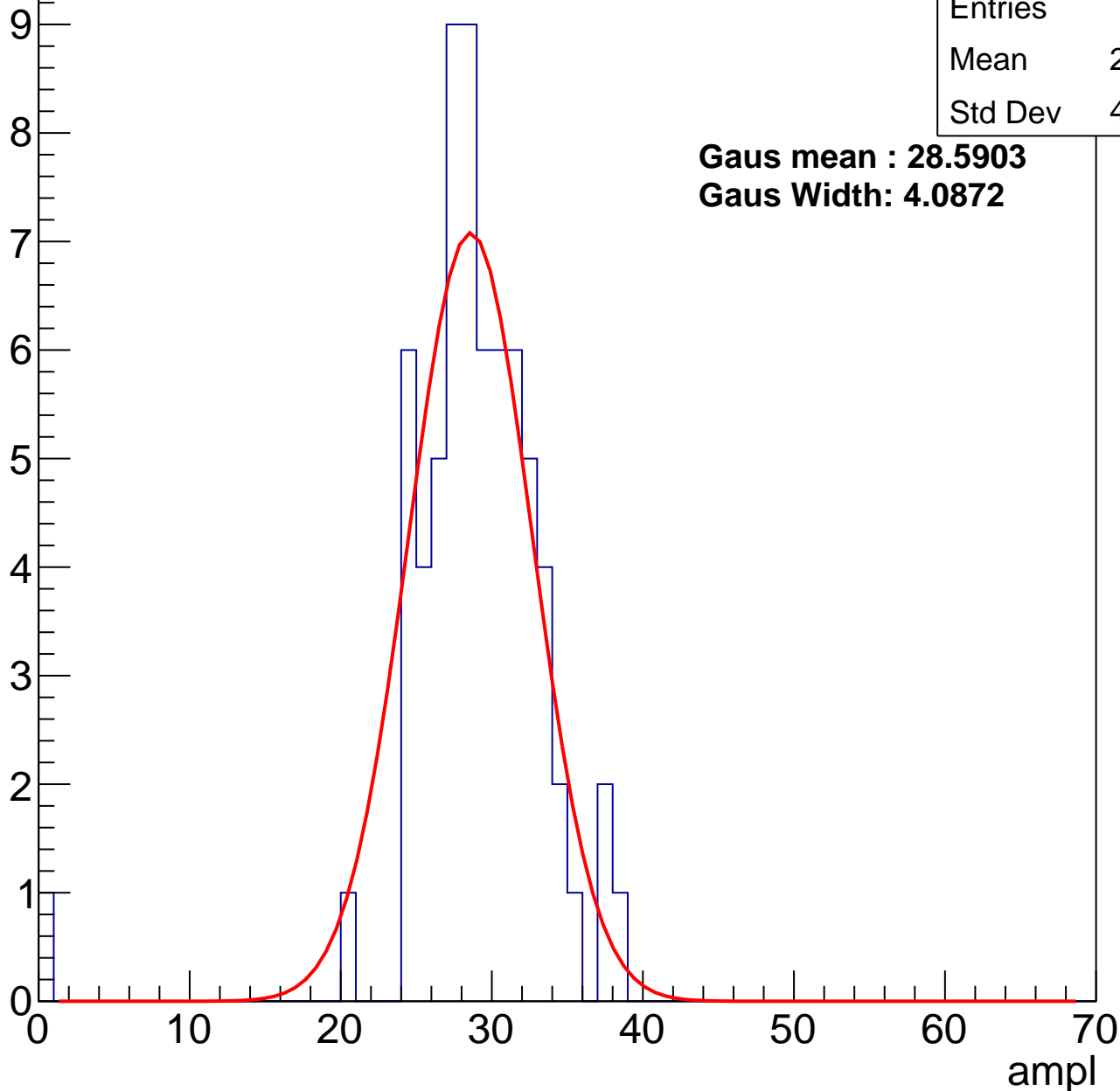
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.47
Std Dev	4.912

Gaus mean : 28.5903

Gaus Width: 4.0872



B1L100S, U15-ch53, adc1

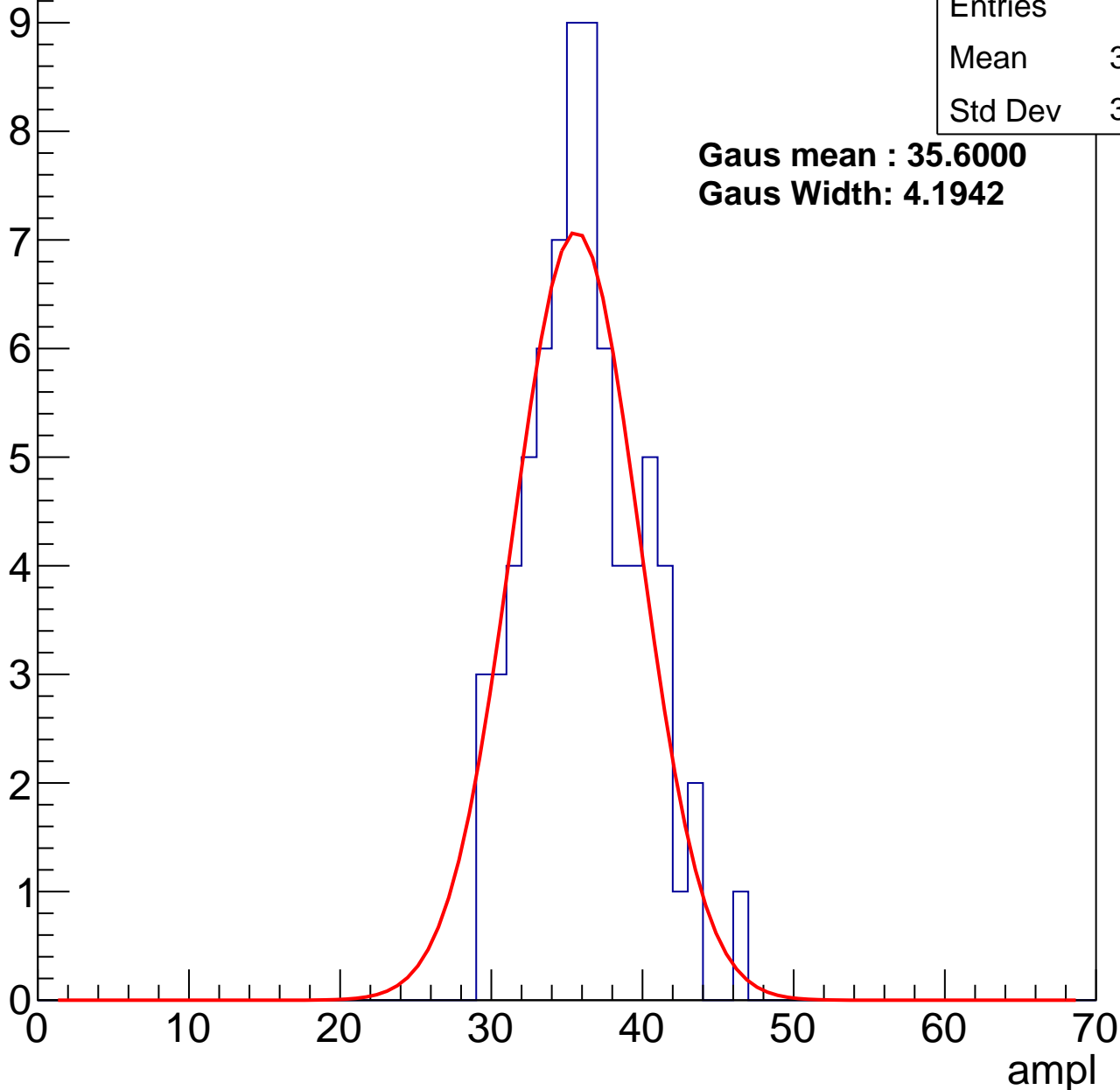
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	35.67
Std Dev	3.683

Gaus mean : 35.6000

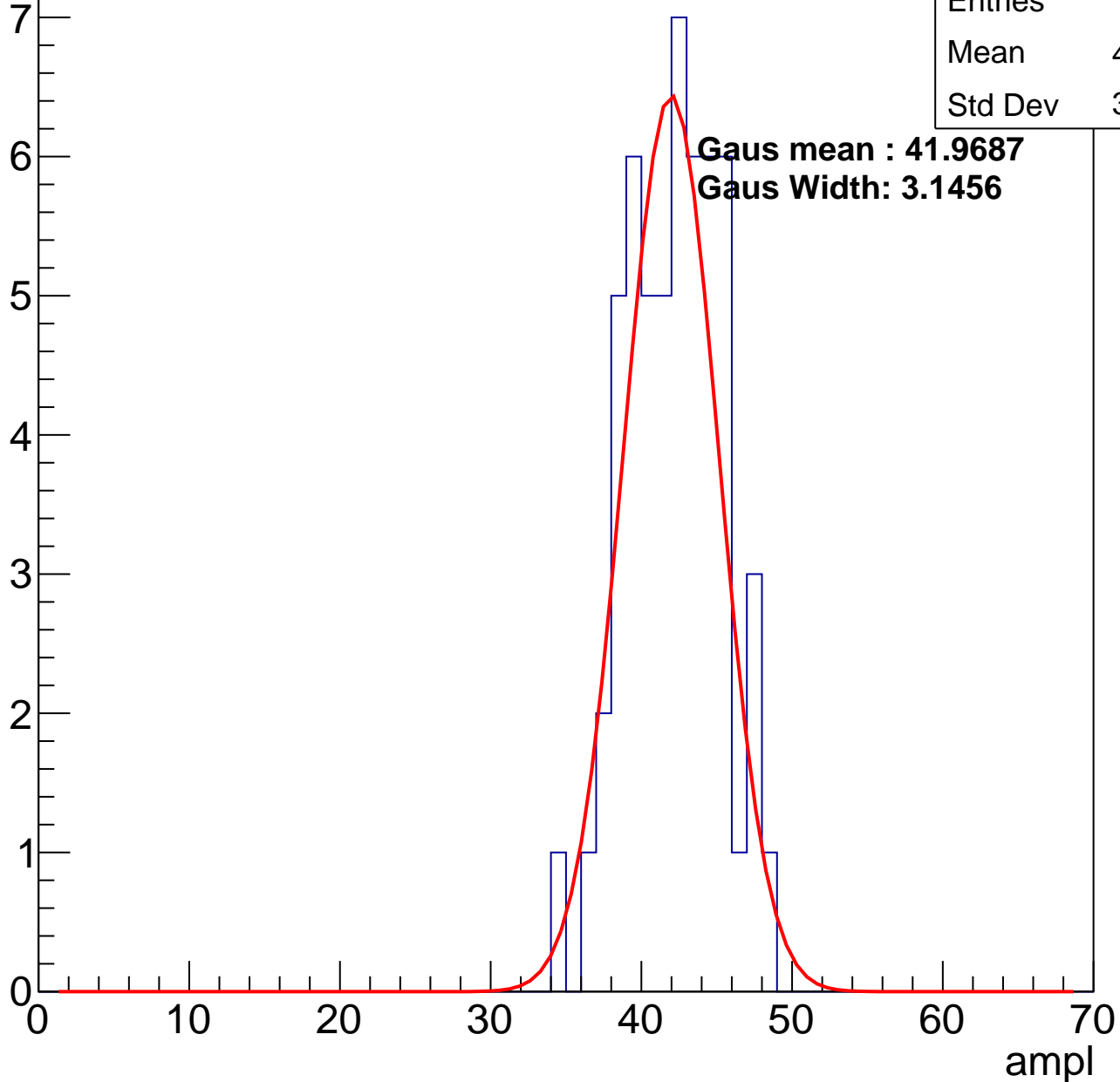
Gaus Width: 4.1942



B1L100S, U15-ch53, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	48.4
Std Dev	3.196

Entry

10

8

6

4

2

0

0

10

20

30

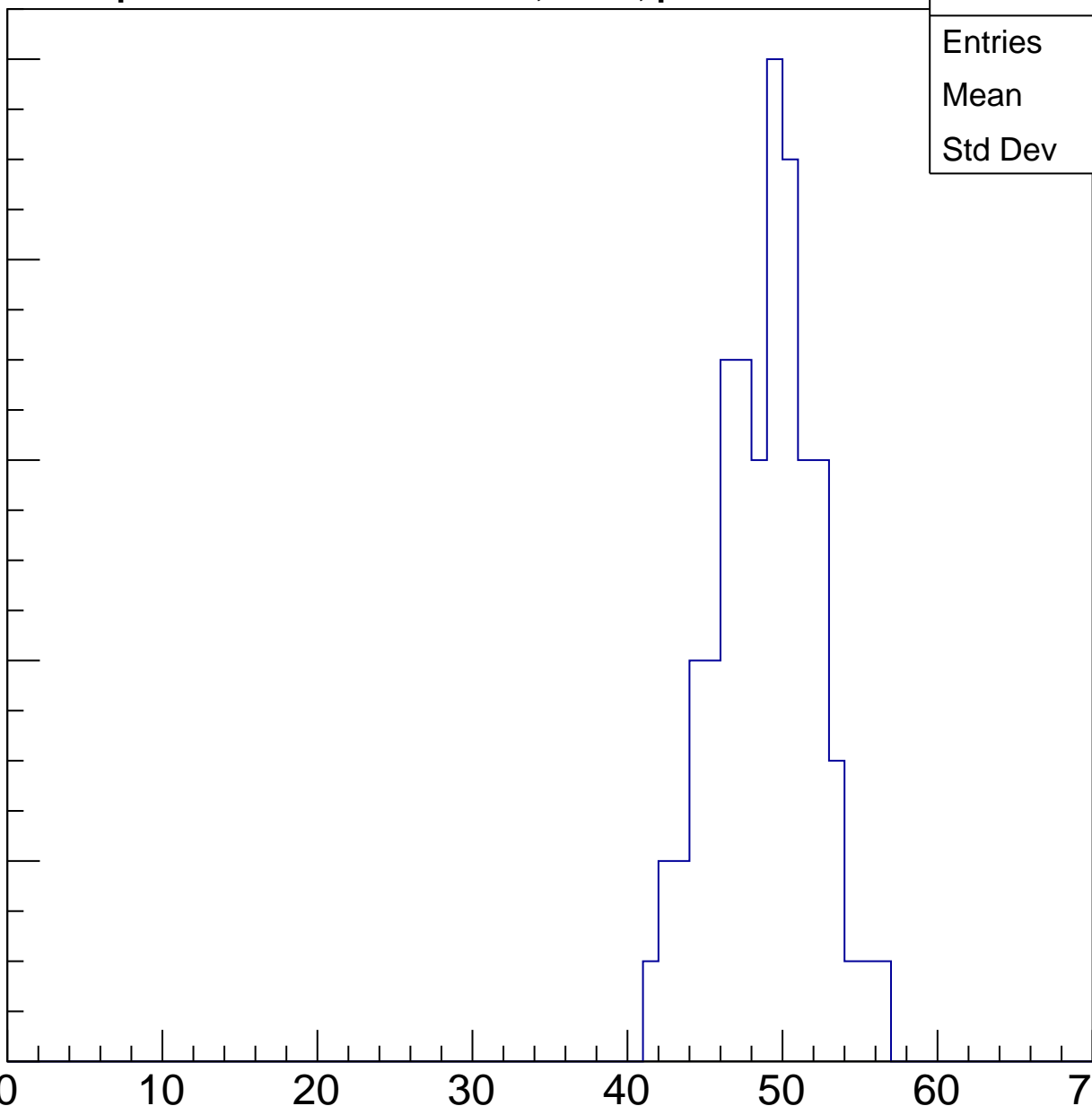
40

50

60

70

ampl

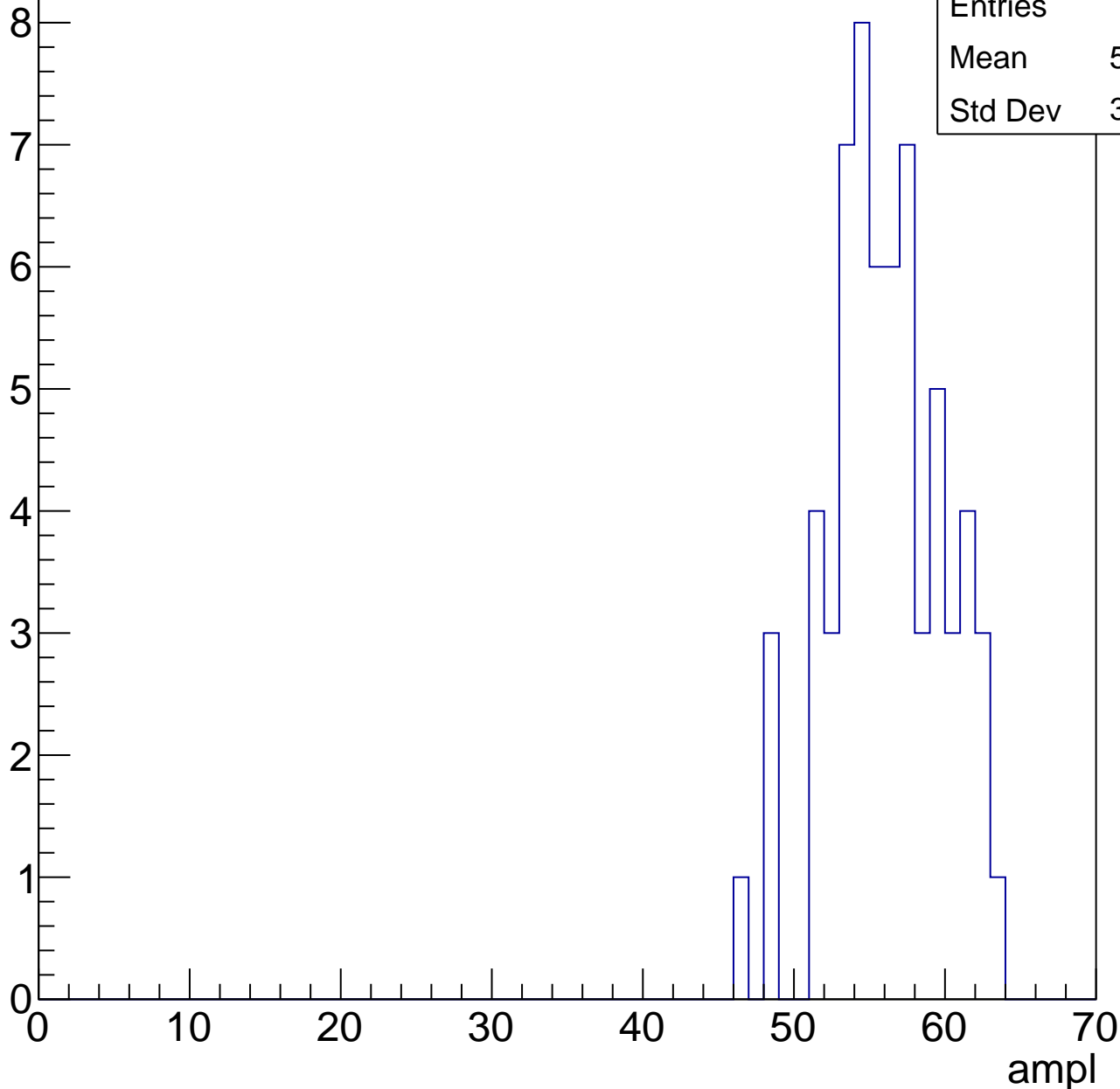


B1L100S, U15-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	55.62
Std Dev	3.752



B1L100S, U15-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 57.88

Std Dev 8.95

ampl

0

10

20

30

40

50

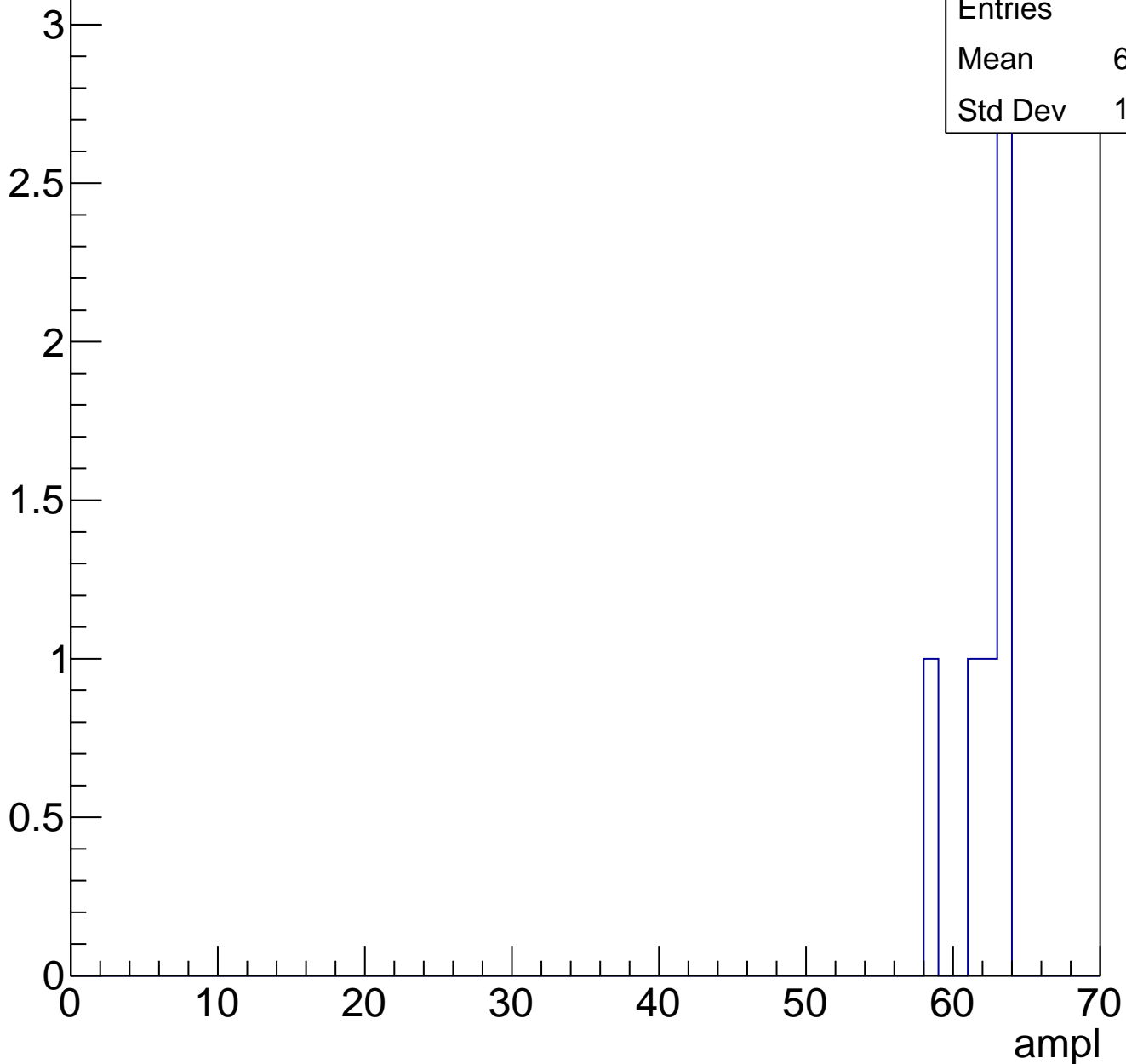
60

70

B1L100S, U15-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch54, adc0

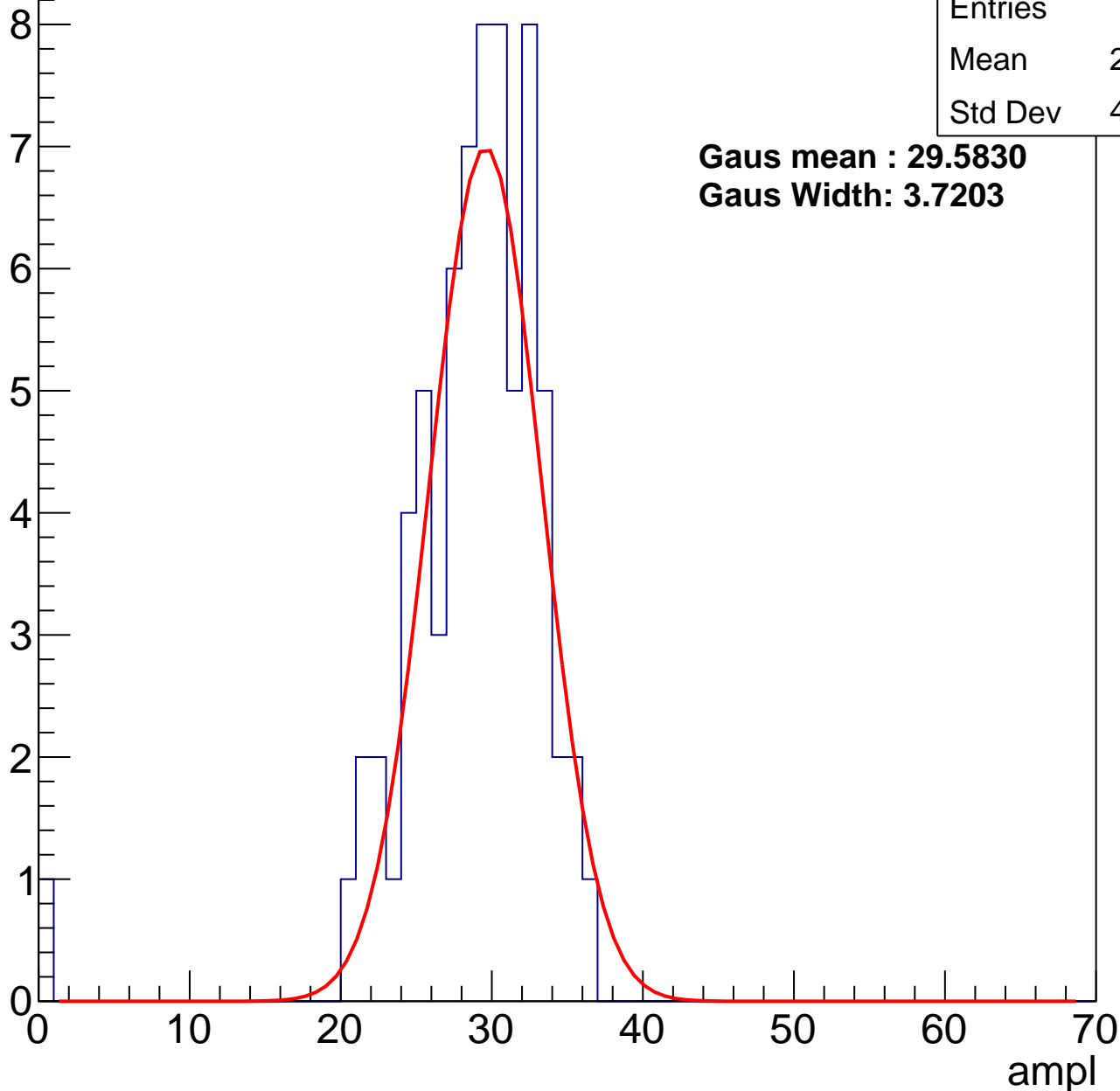
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	28.28
Std Dev	4.945

Gaus mean : 29.5830

Gaus Width: 3.7203



B1L100S, U15-ch54, adc1

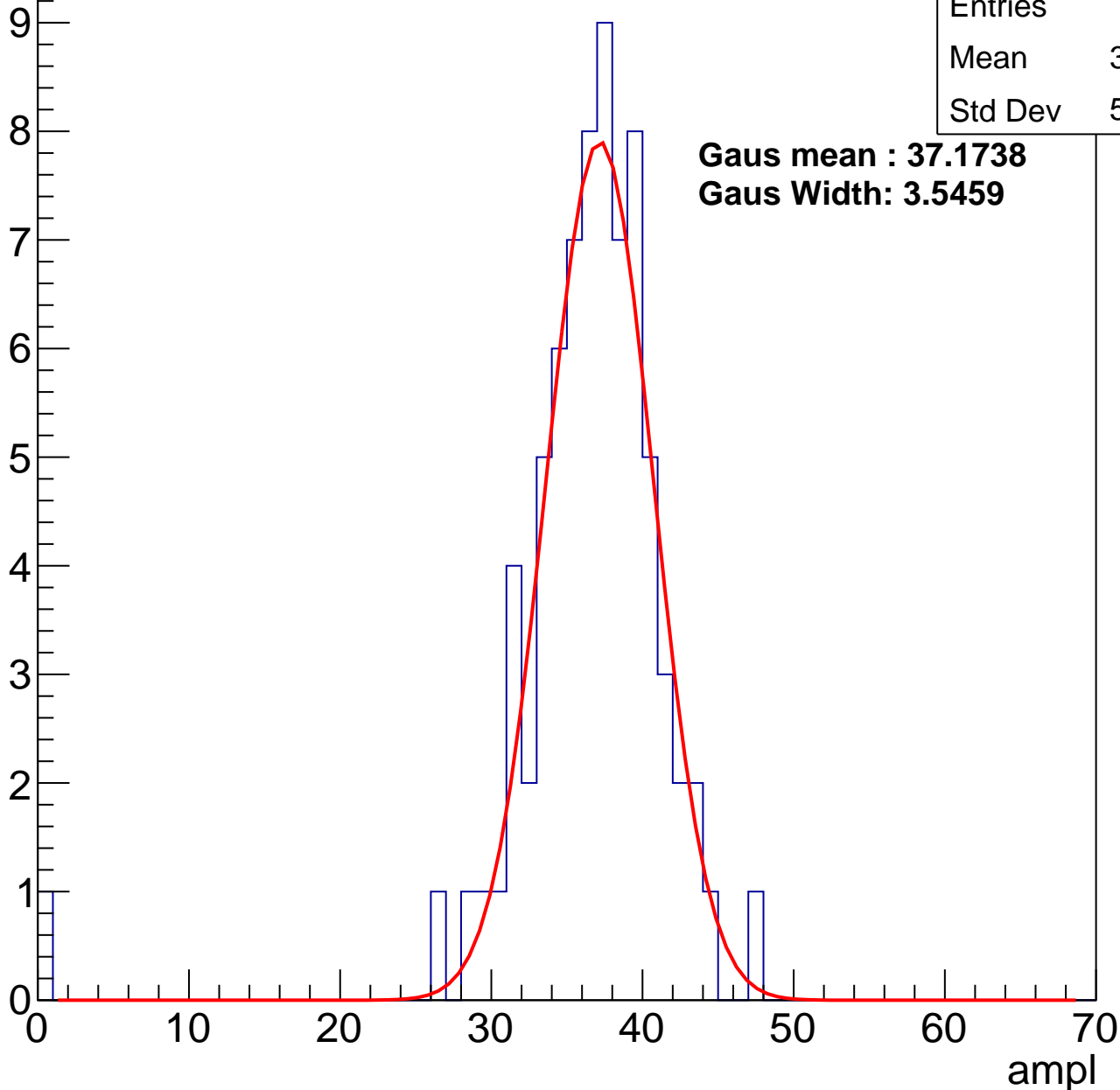
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	35.97
Std Dev	5.624

Gaus mean : 37.1738

Gaus Width: 3.5459



B1L100S, U15-ch54, adc2

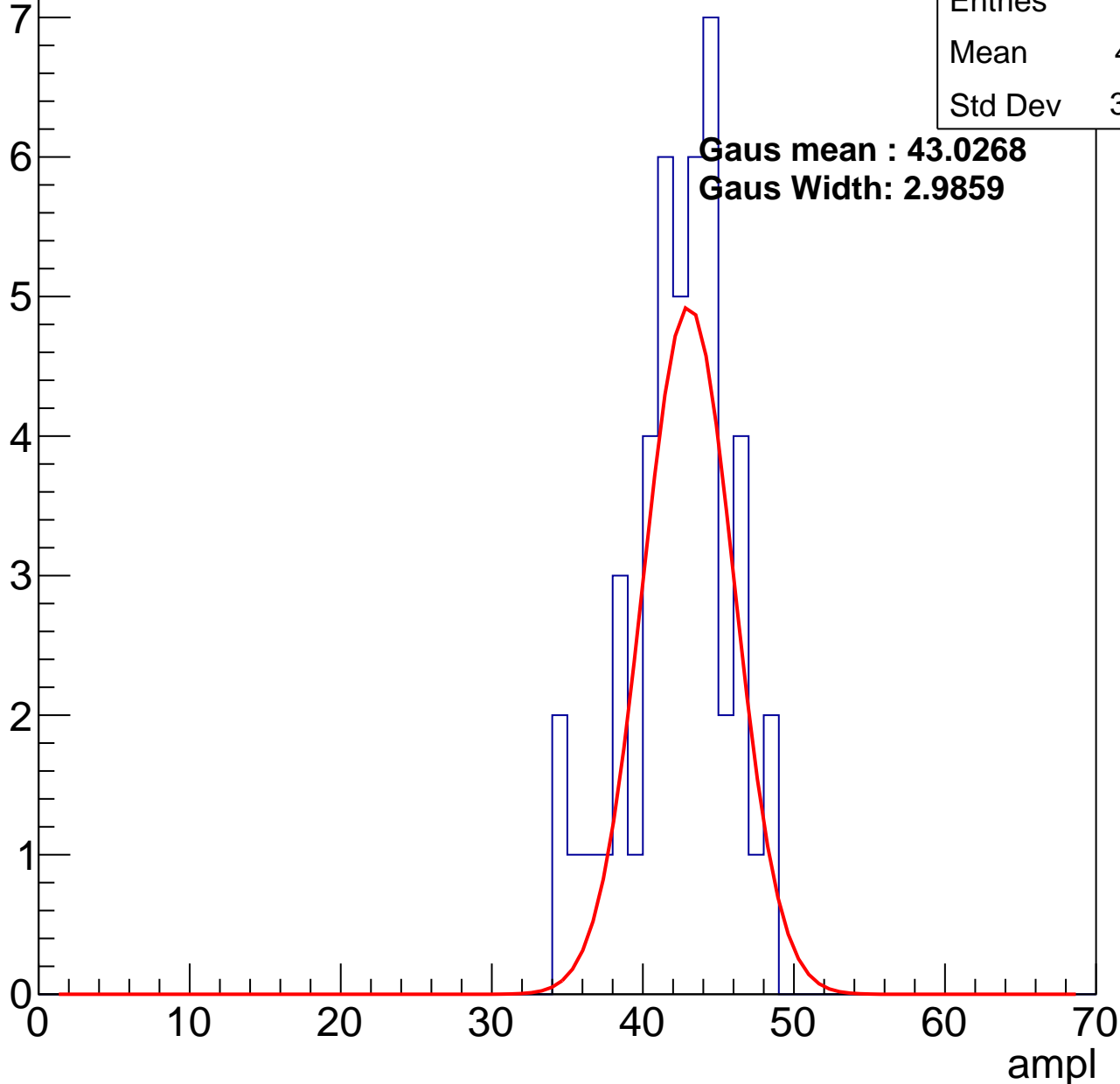
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	41.91
Std Dev	3.393

Gaus mean : 43.0268

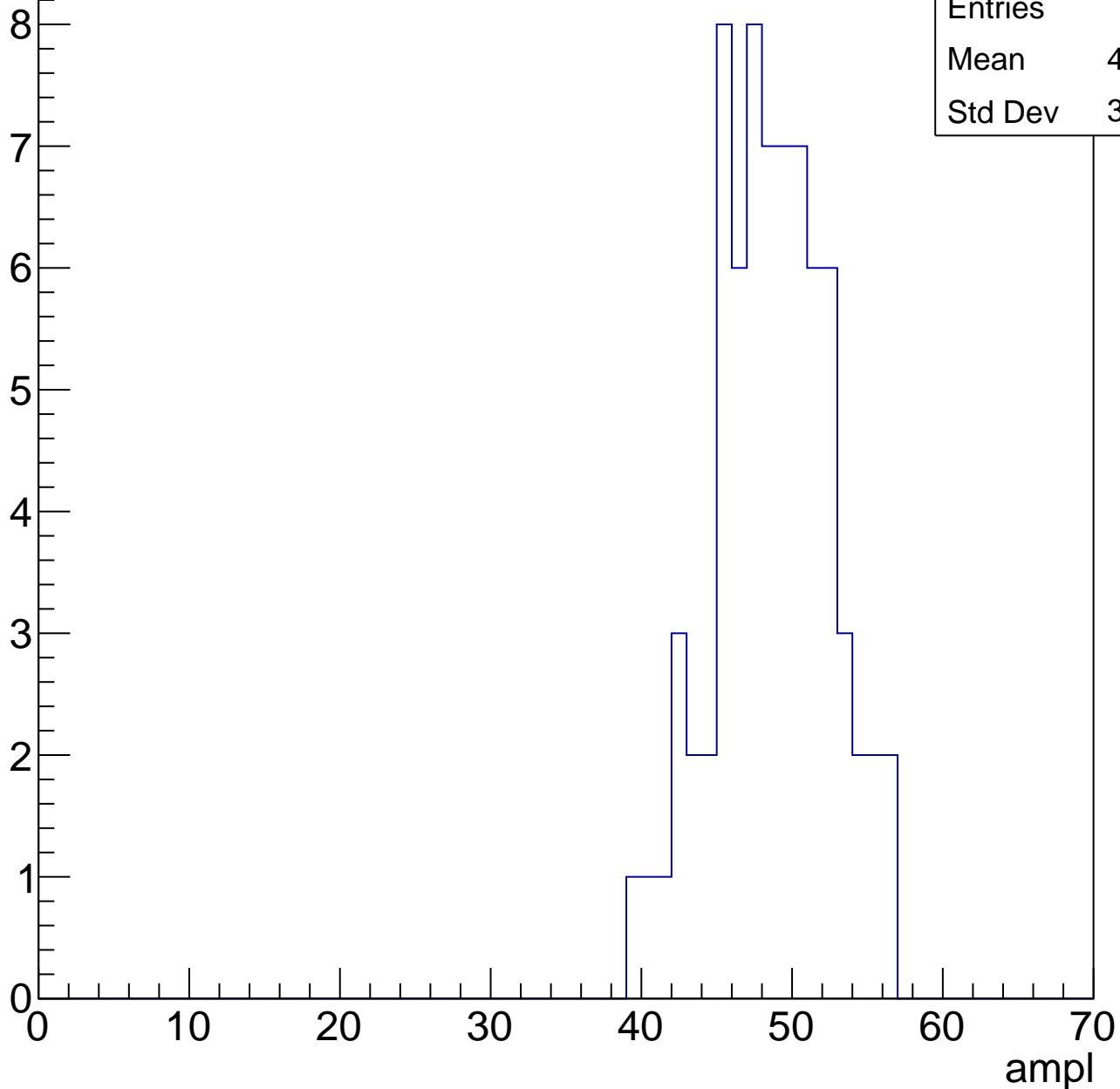
Gaus Width: 2.9859



B1L100S, U15-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



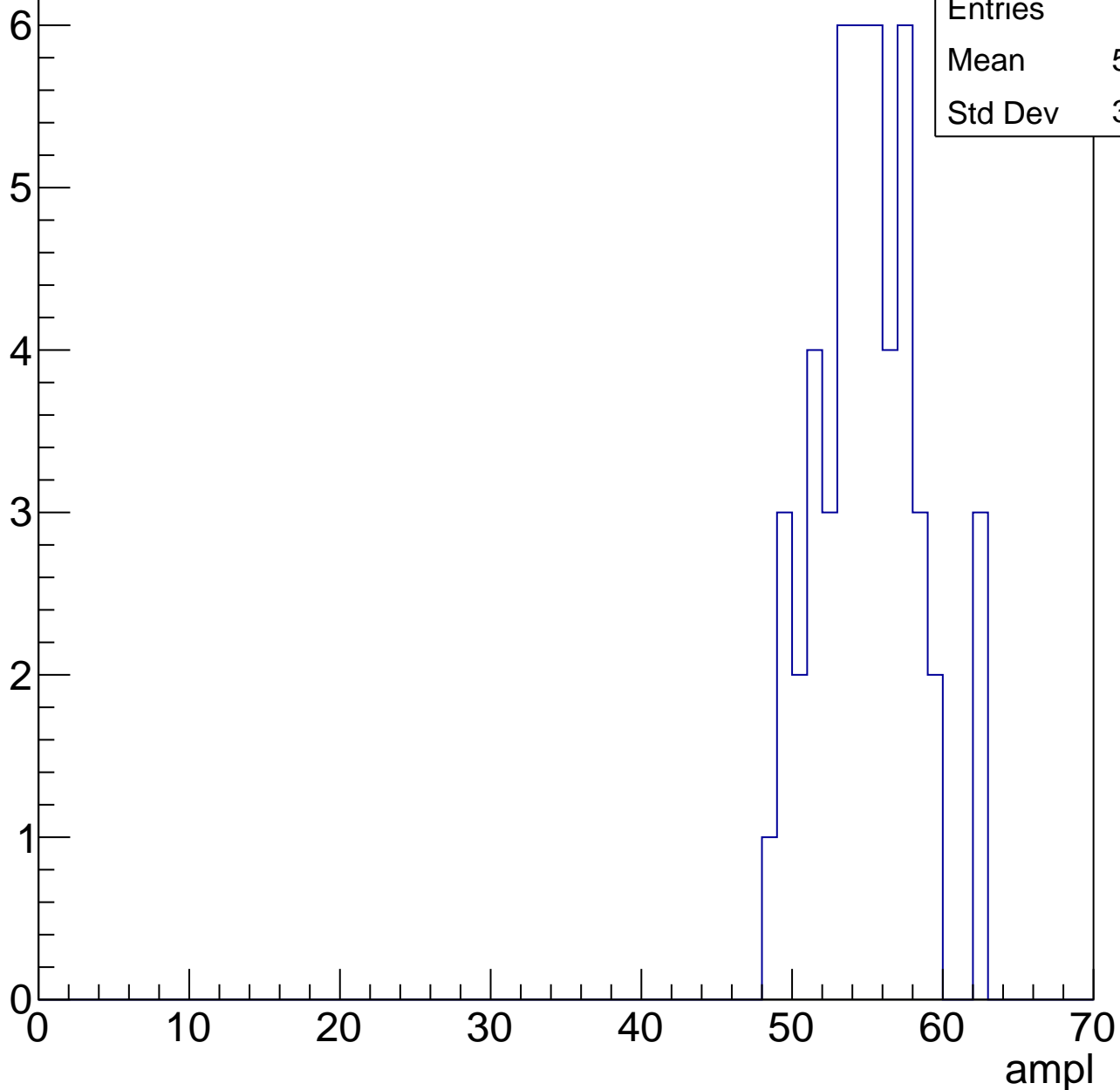
Entries	74
Mean	48.22
Std Dev	3.753

B1L100S, U15-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	54.51
Std Dev	3.351

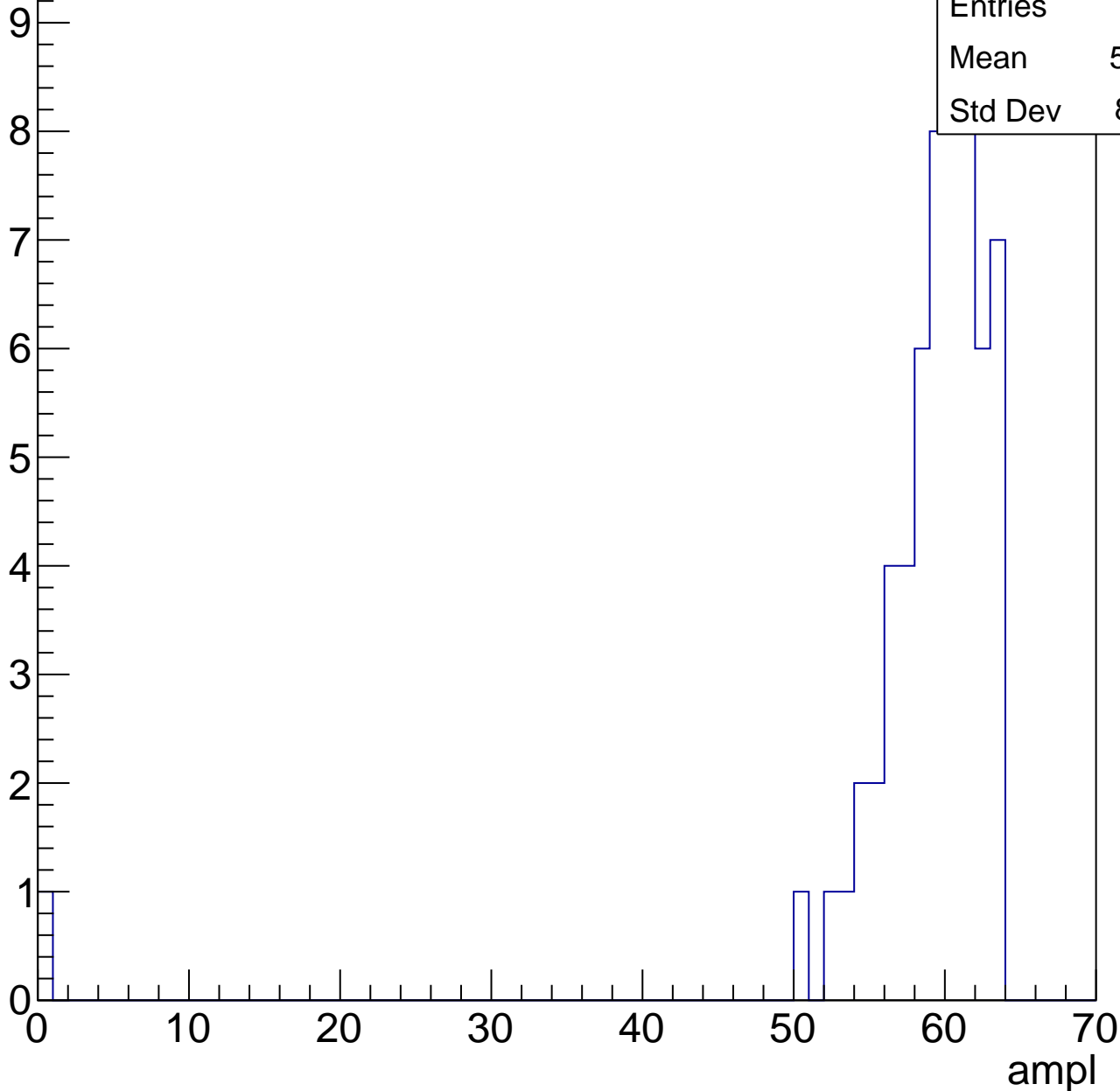


B1L100S, U15-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

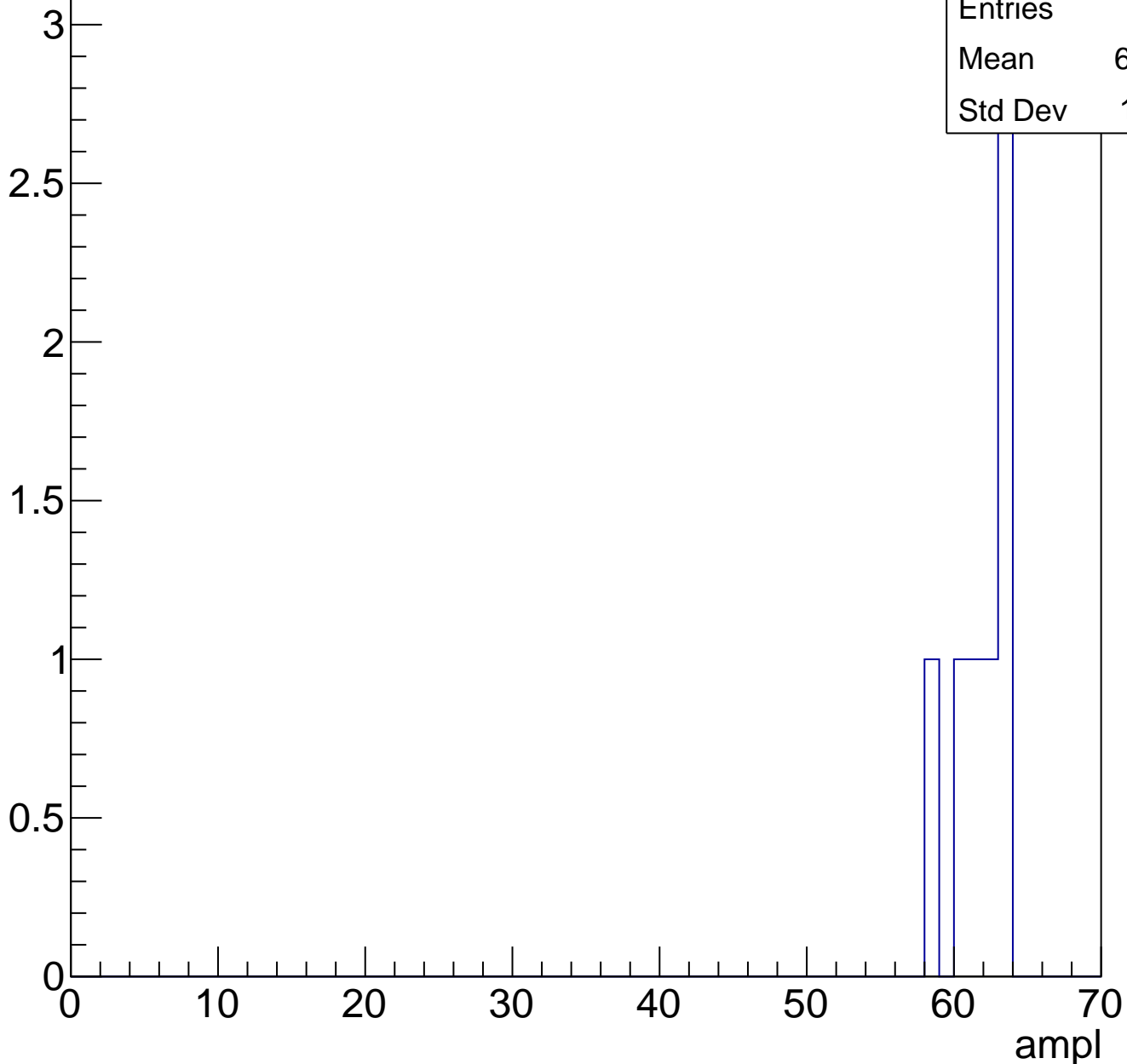
Entries	61
Mean	58.15
Std Dev	8.051



B1L100S, U15-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch55, adc0

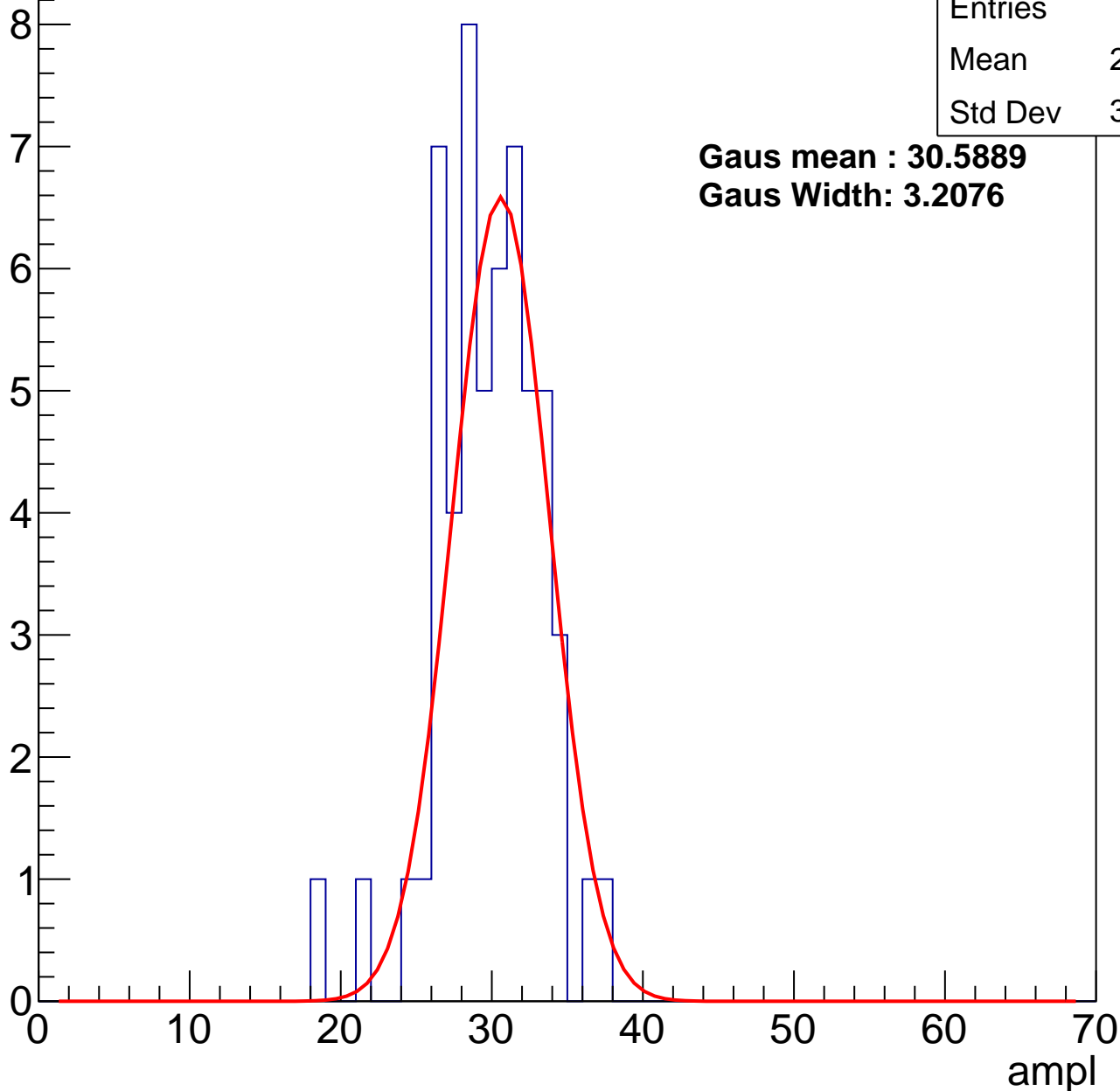
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.36
Std Dev	3.414

Gaus mean : 30.5889

Gaus Width: 3.2076



B1L100S, U15-ch55, adc1

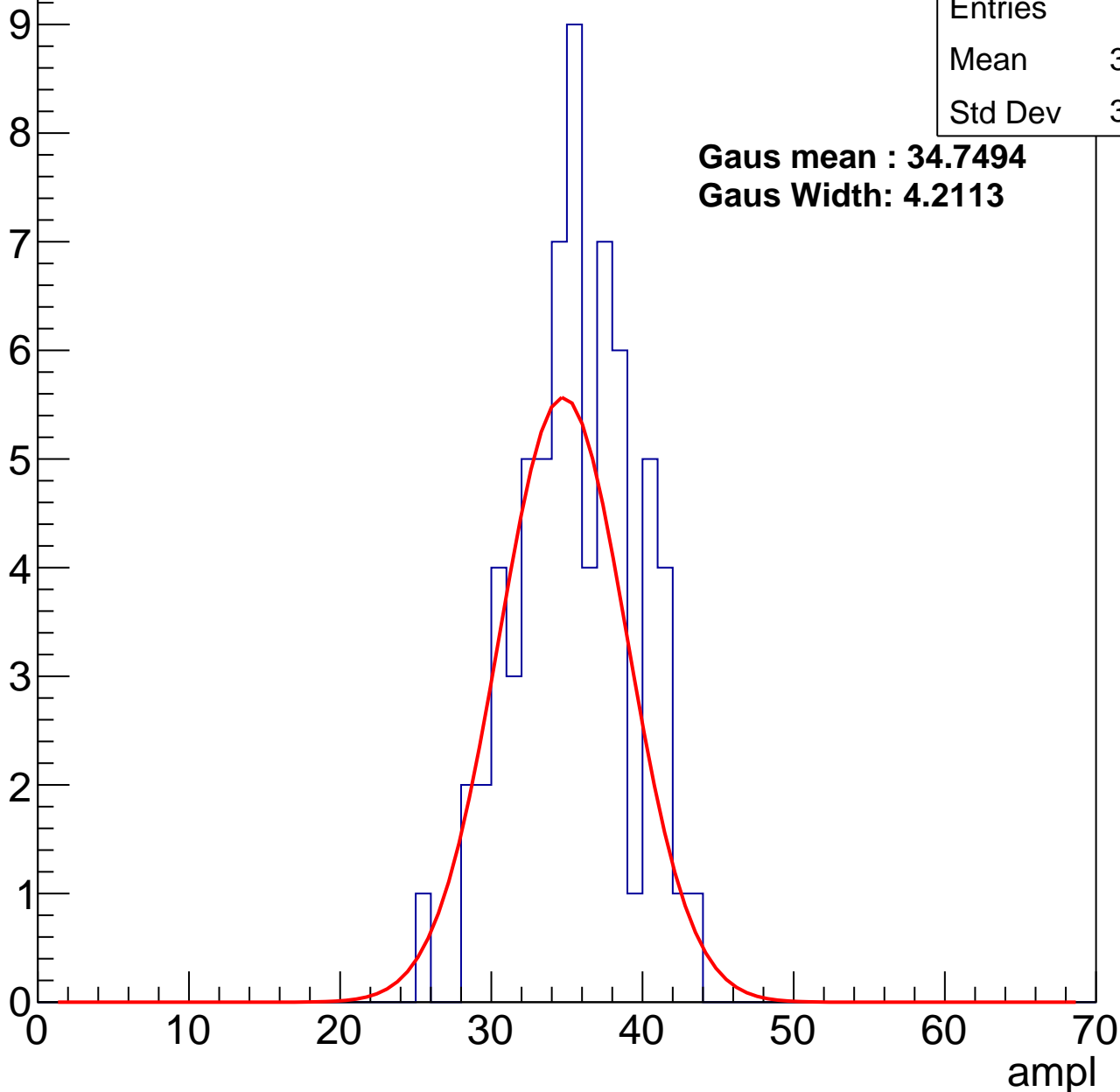
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.06
Std Dev	3.812

Gaus mean : 34.7494

Gaus Width: 4.2113



B1L100S, U15-ch55, adc2

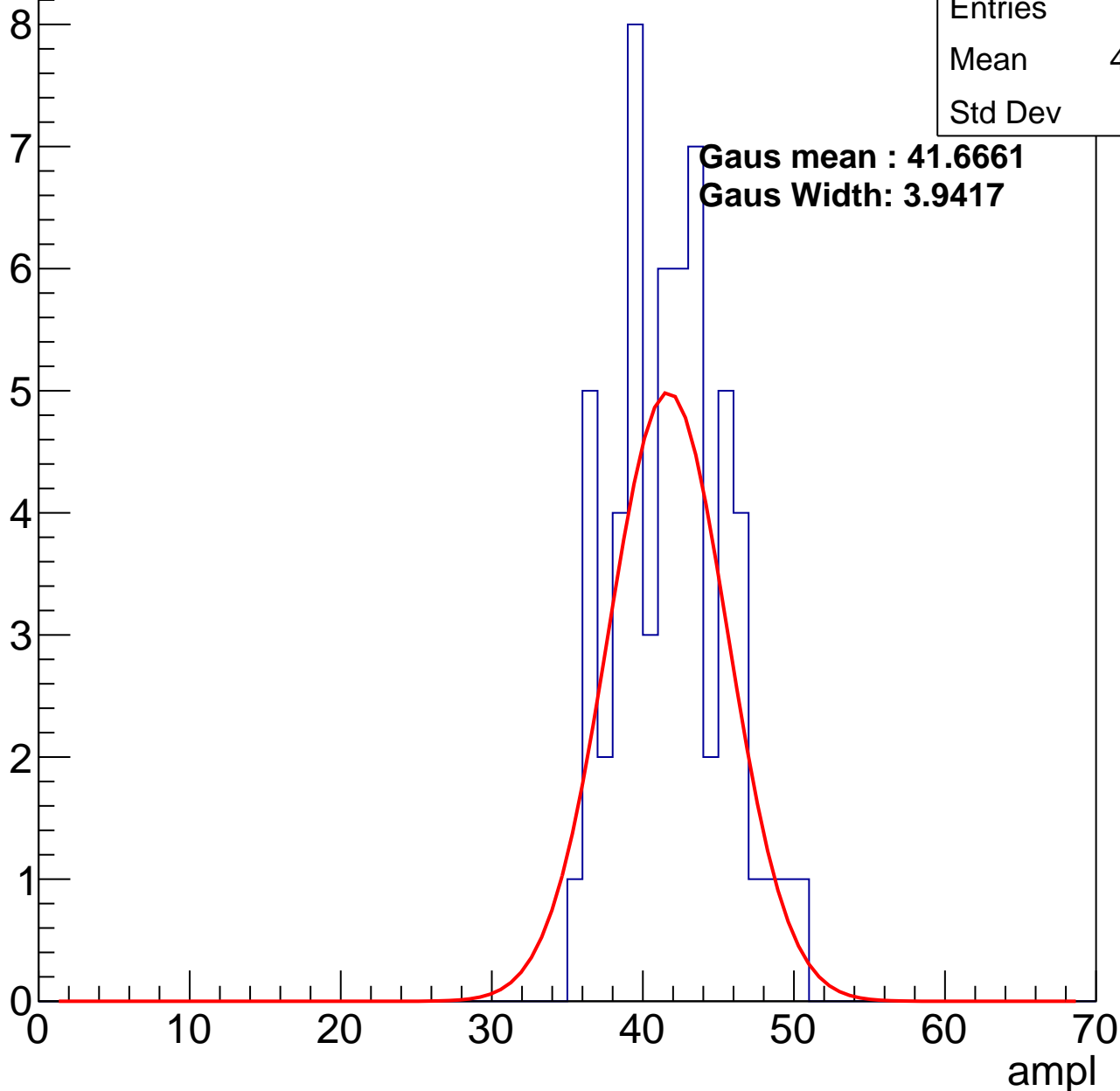
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	41.46
Std Dev	3.54

Gaus mean : 41.6661

Gaus Width: 3.9417



B1L100S, U15-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	48.17
Std Dev	3.811

Entry

10

8

6

4

2

0

0

10

20

30

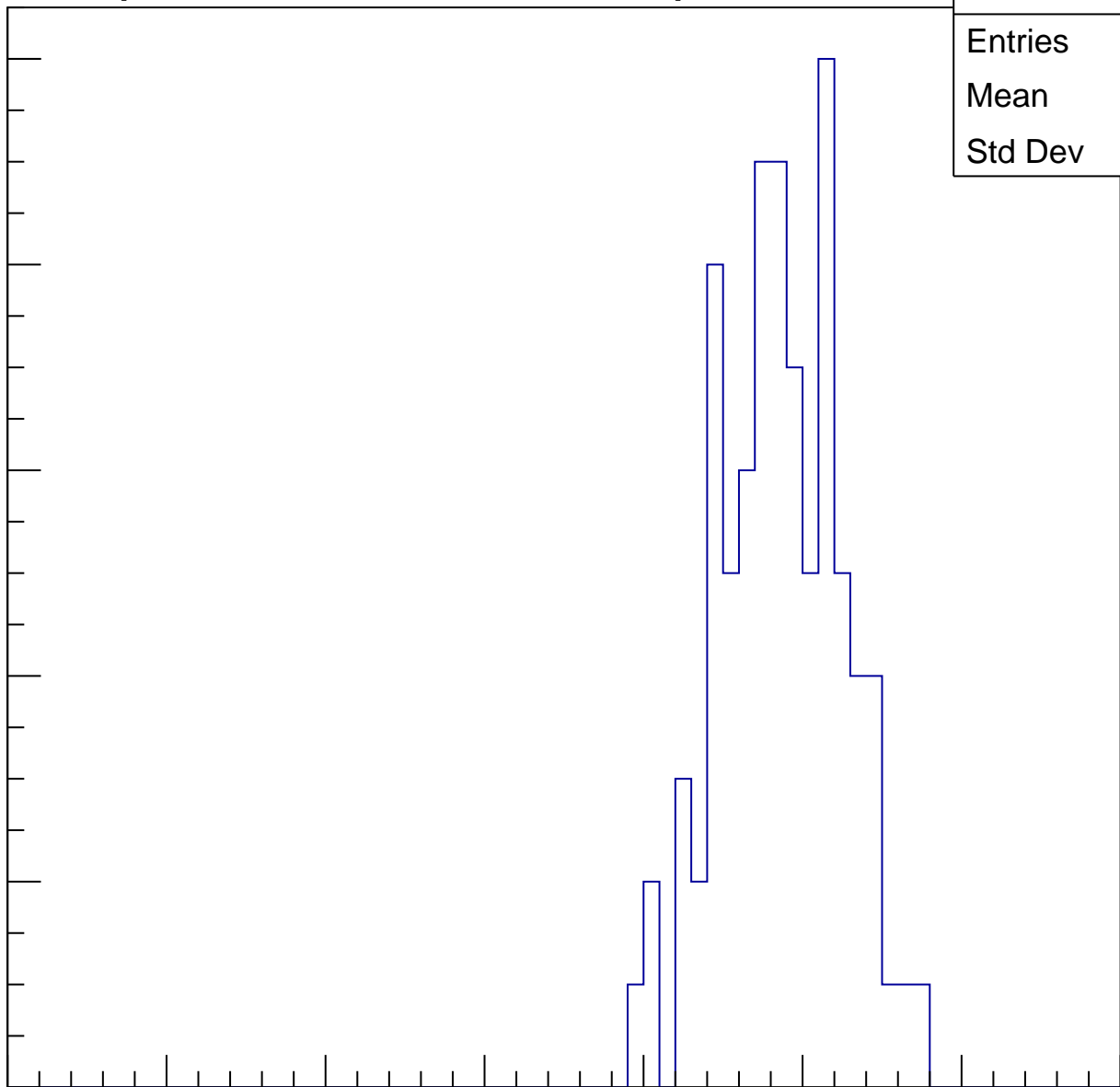
40

50

60

70

ampl

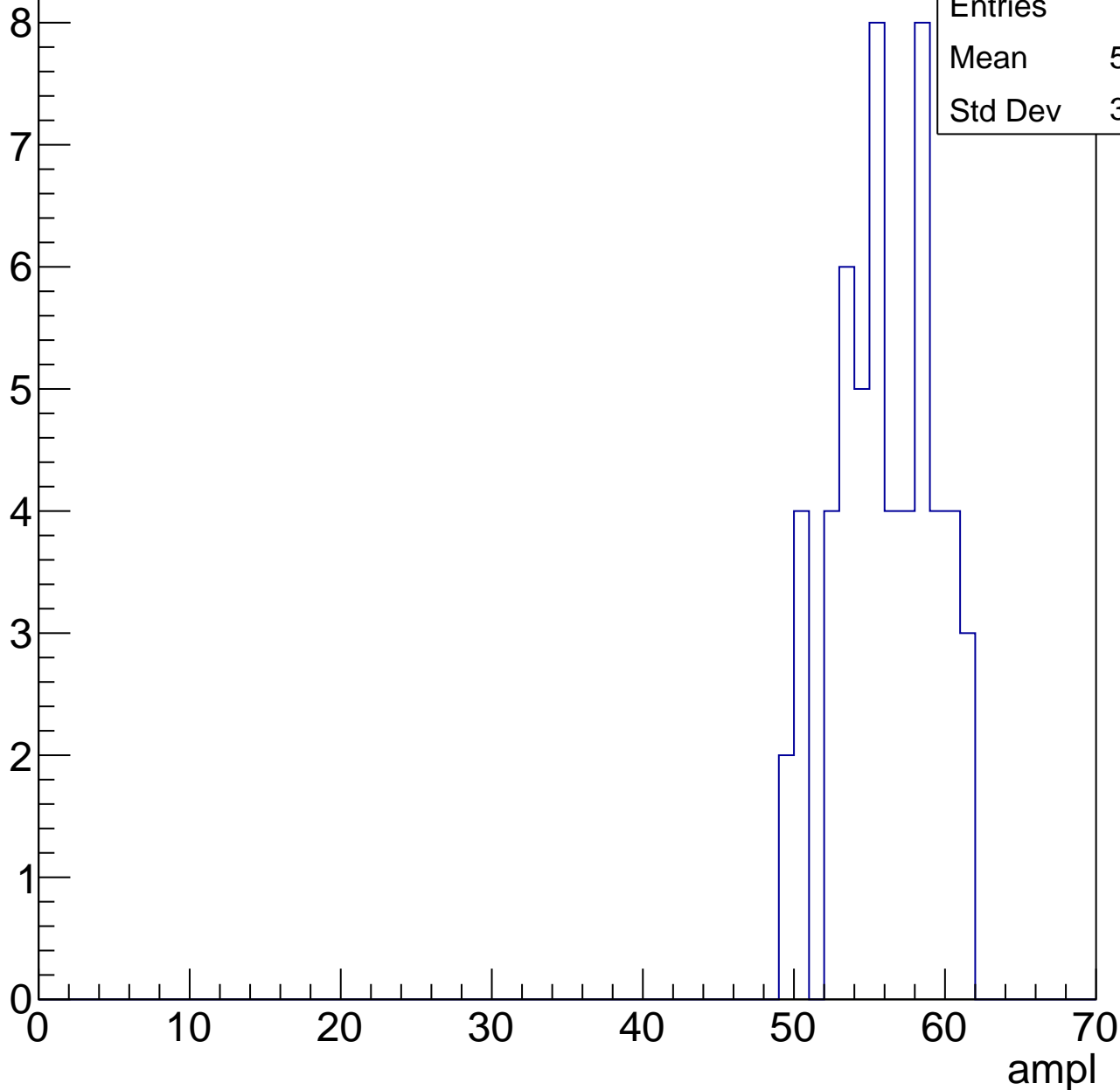


B1L100S, U15-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

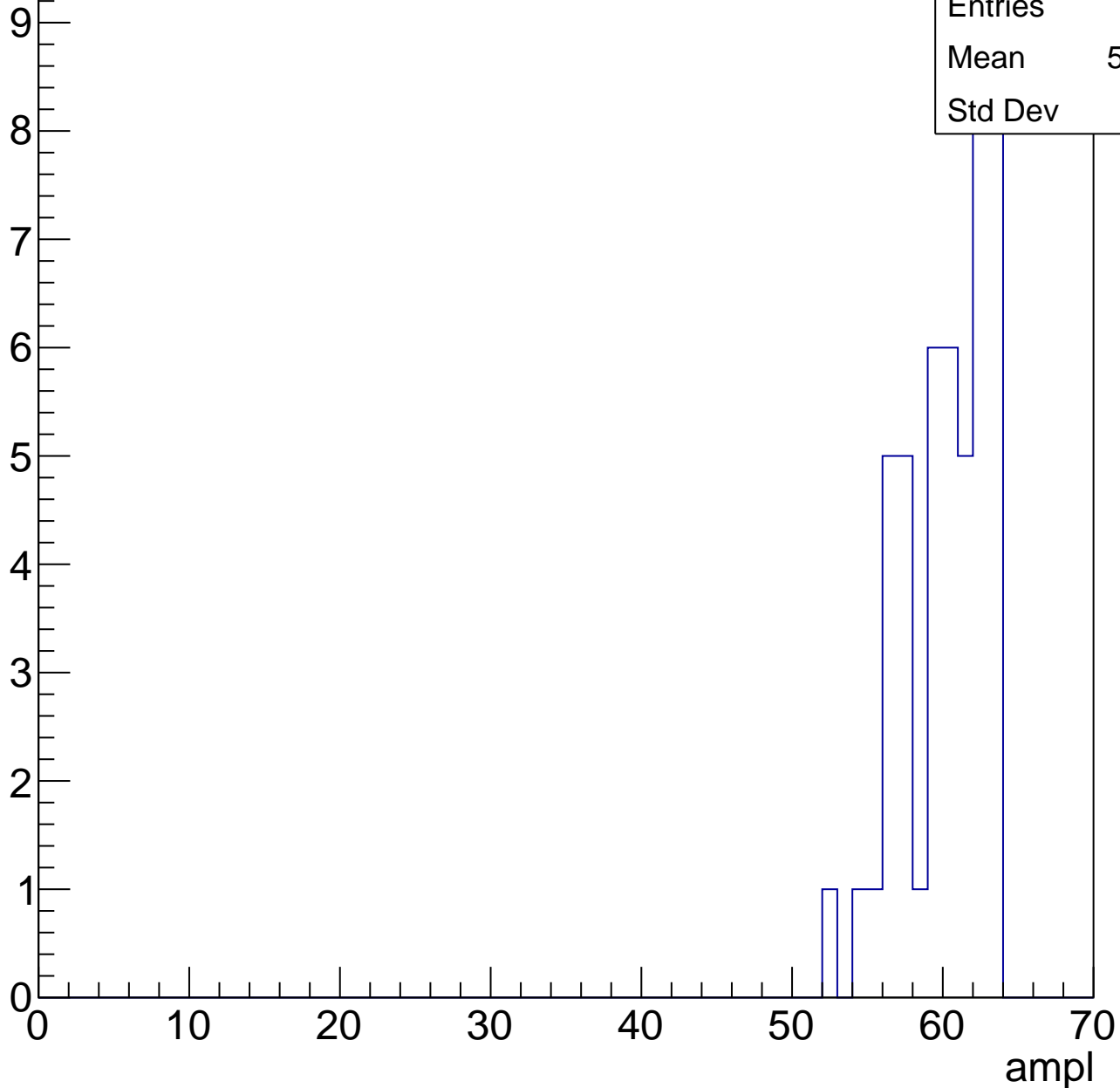
Entries	56
Mean	55.52
Std Dev	3.235



B1L100S, U15-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

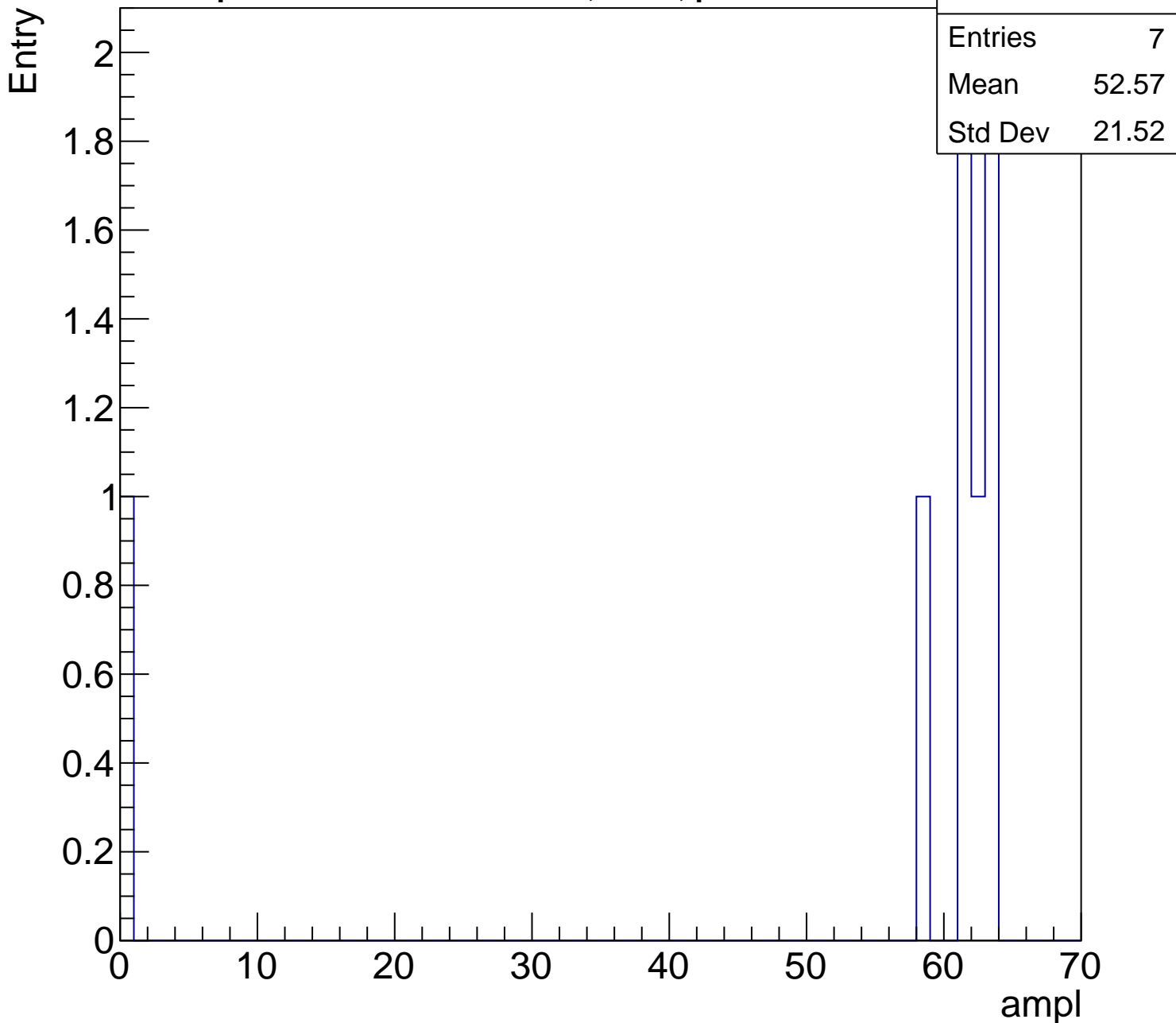
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.57
Std Dev	21.52

0 10 20 30 40 50 60 70

ampl



B1L100S, U15-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch56, adc0

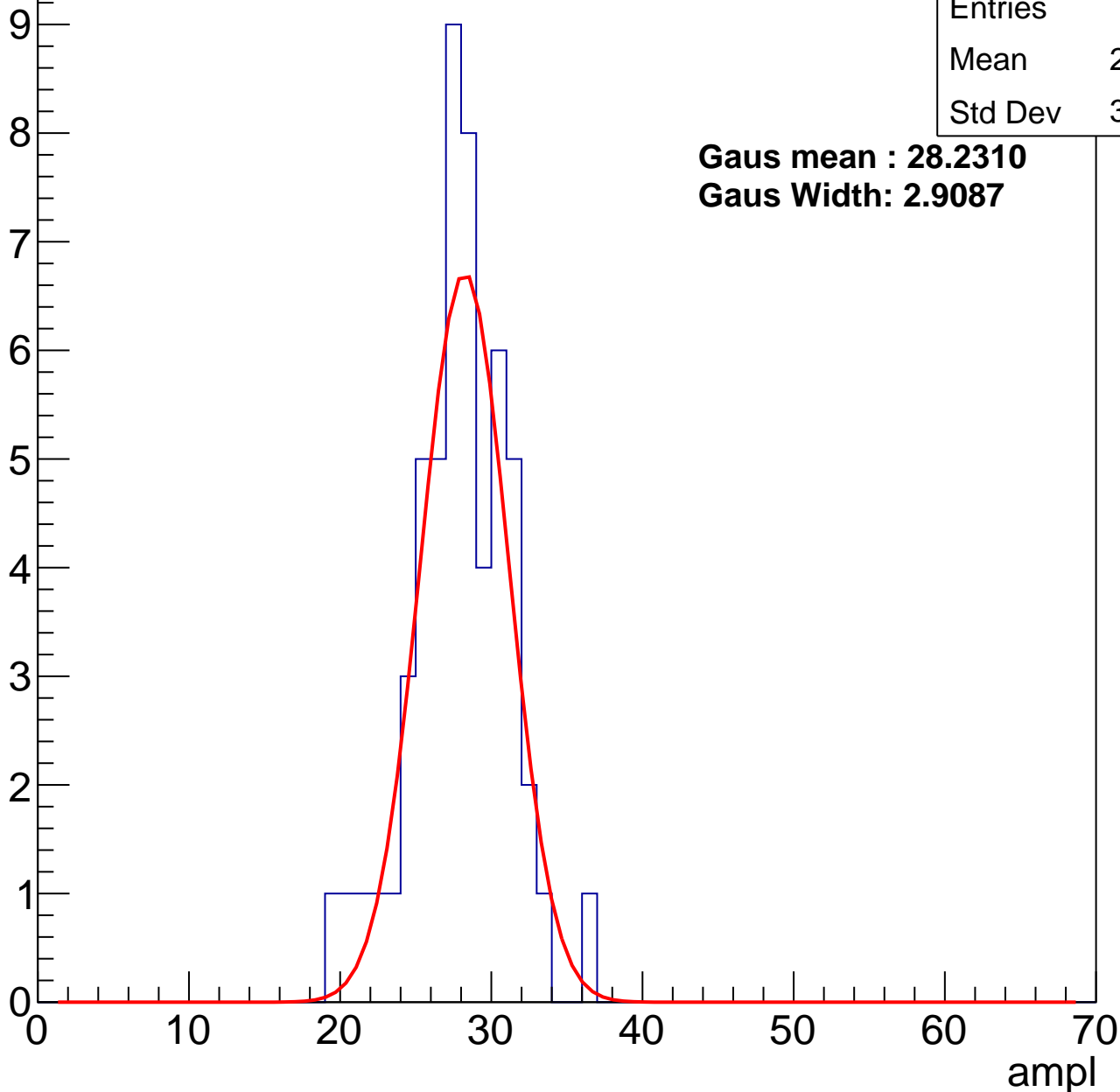
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	27.46
Std Dev	3.207

Gaus mean : 28.2310

Gaus Width: 2.9087



B1L100S, U15-ch56, adc1

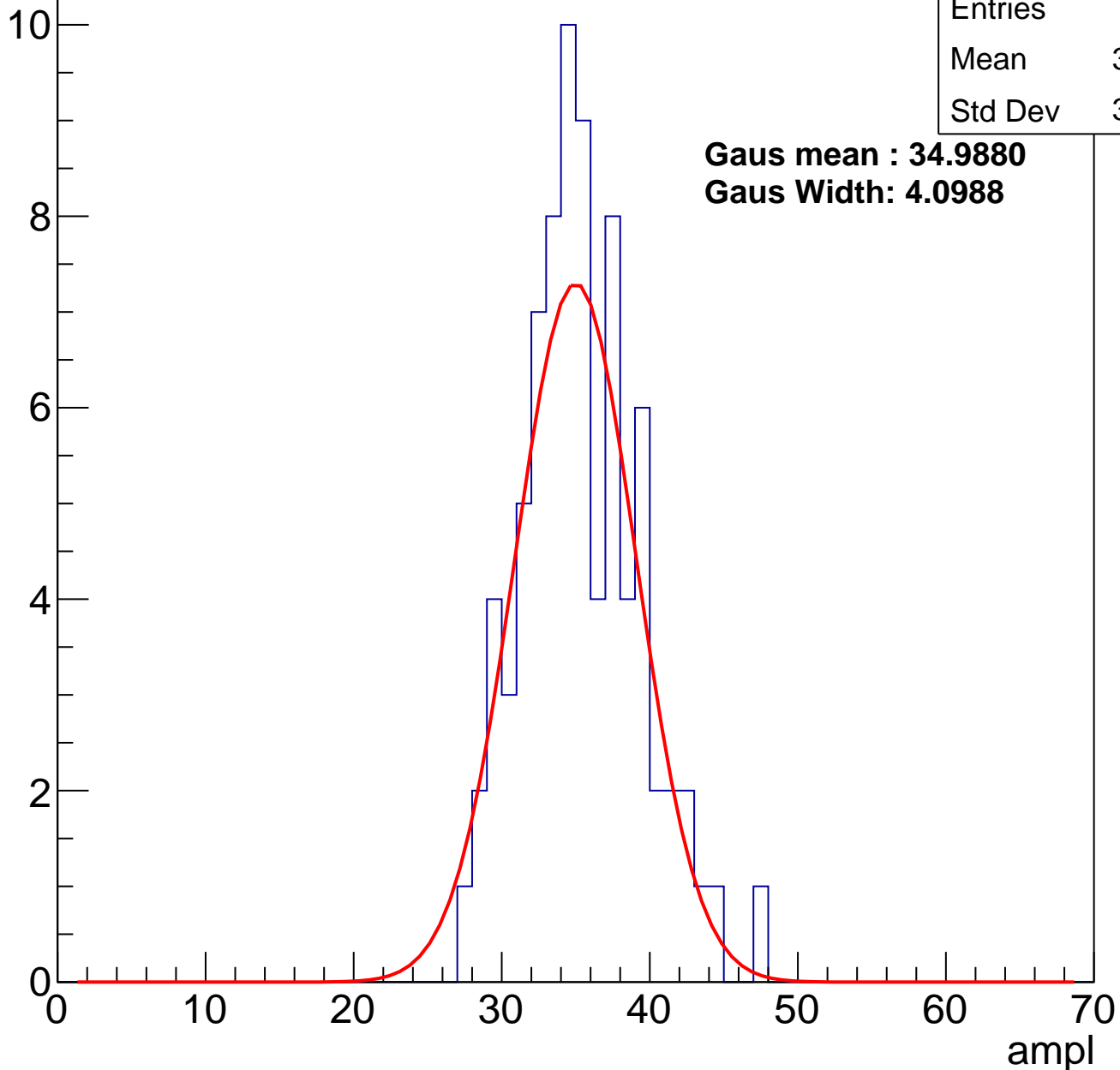
calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	34.91
Std Dev	3.941

Gaus mean : 34.9880

Gaus Width: 4.0988

Entry



B1L100S, U15-ch56, adc2

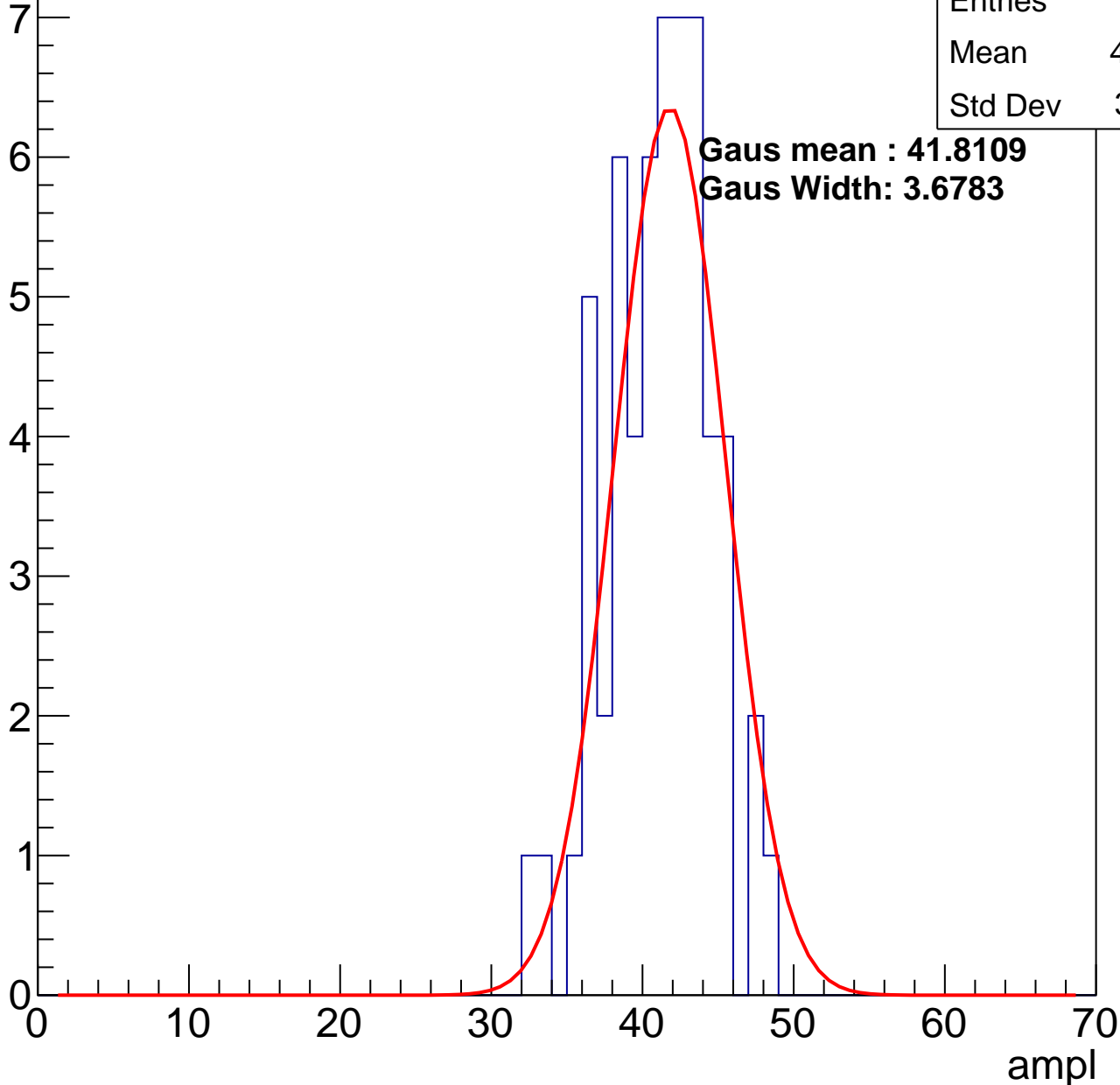
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	40.66
Std Dev	3.381

Gaus mean : 41.8109

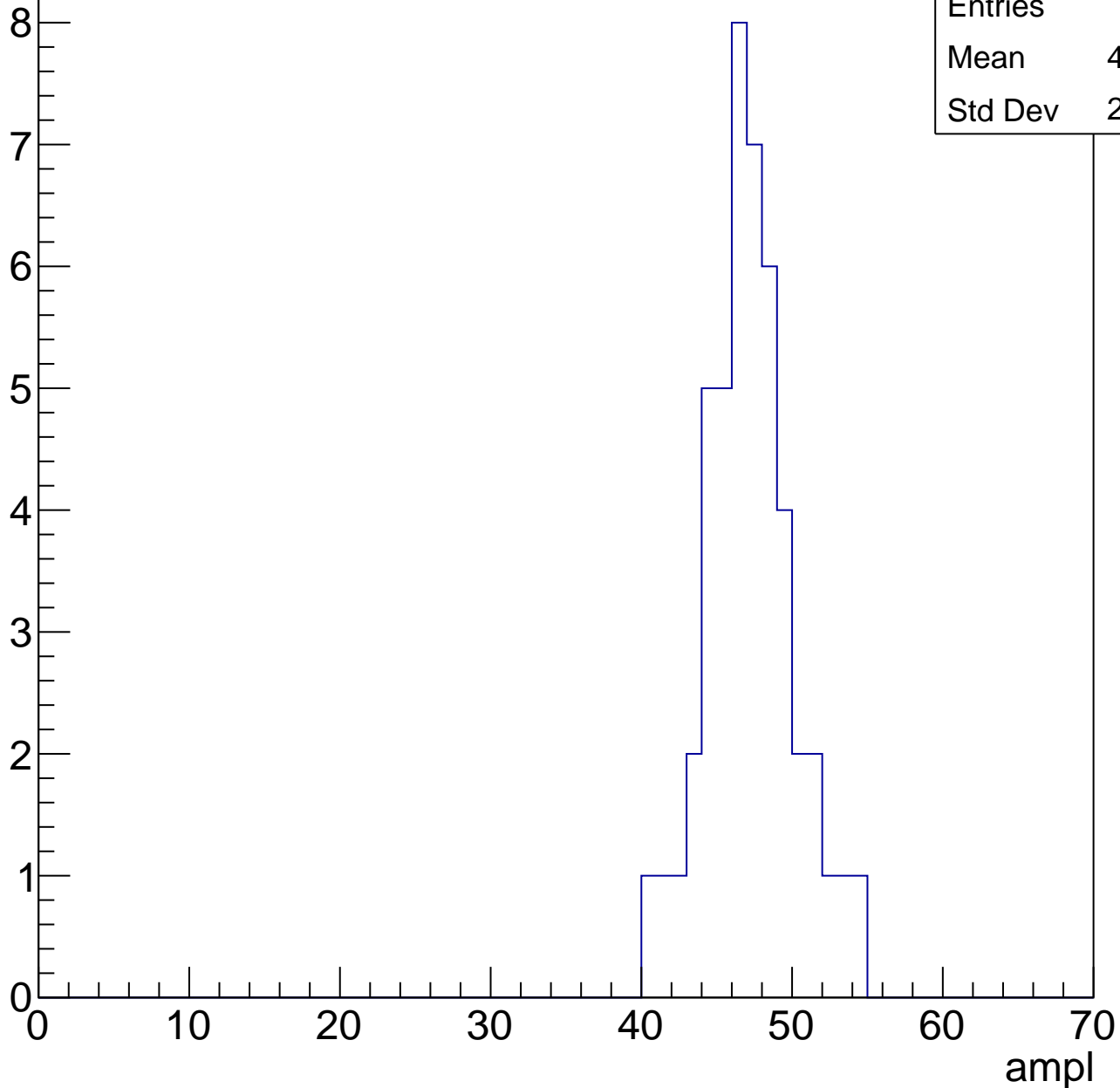
Gaus Width: 3.6783



B1L100S, U15-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



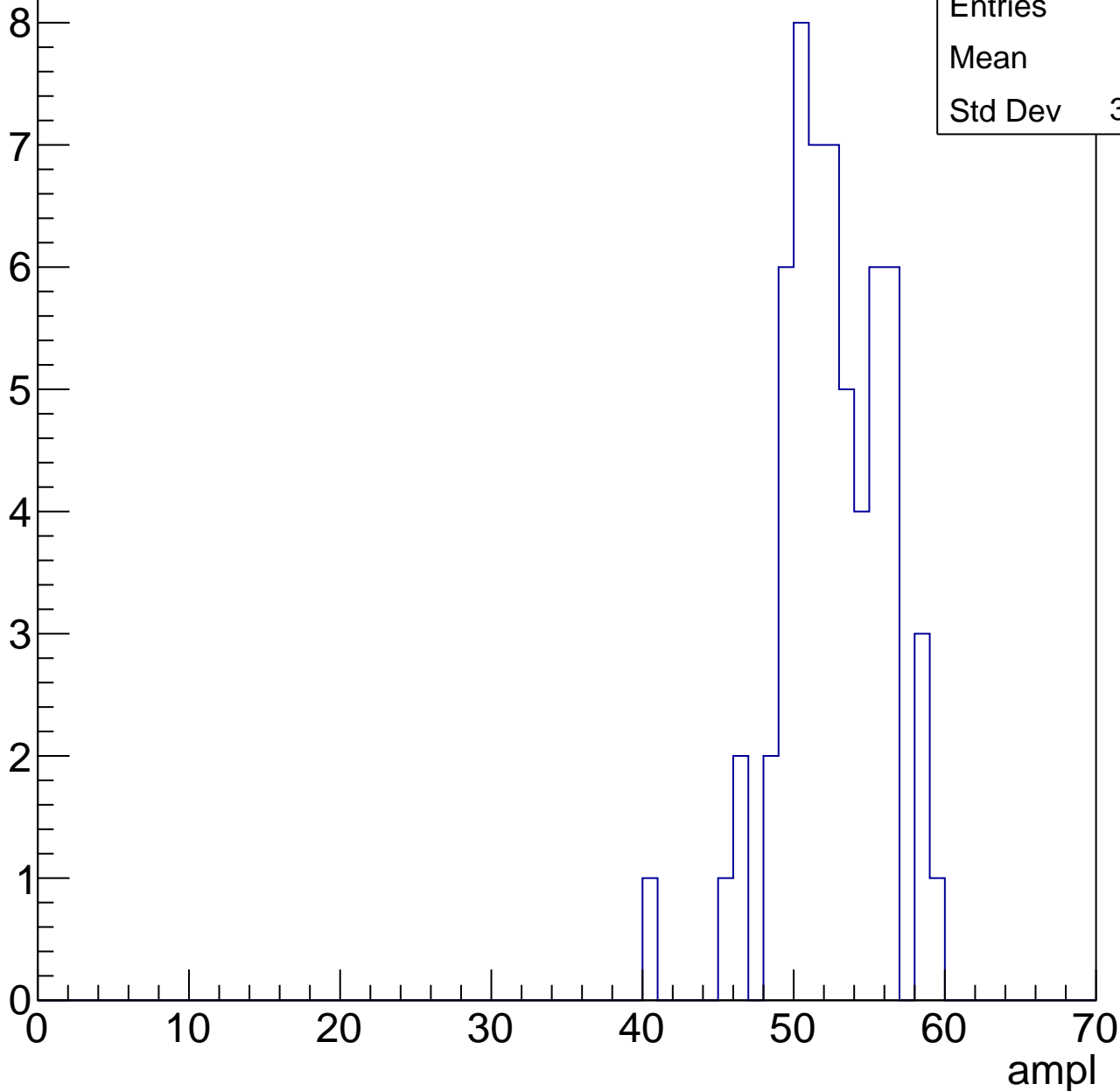
Entries	47
Mean	46.72
Std Dev	2.893

B1L100S, U15-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	52
Std Dev	3.488

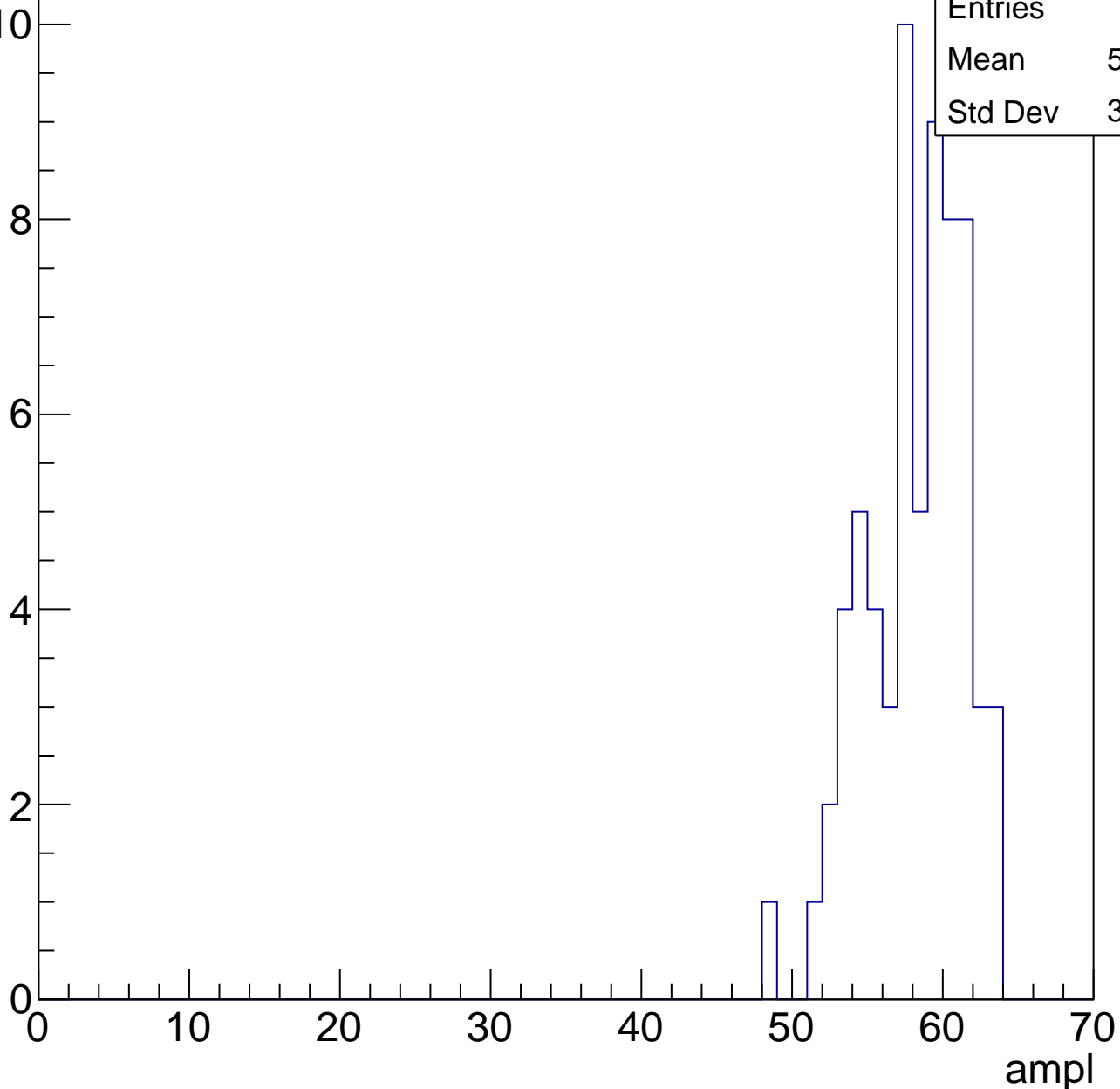


B1L100S, U15-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	57.68
Std Dev	3.229

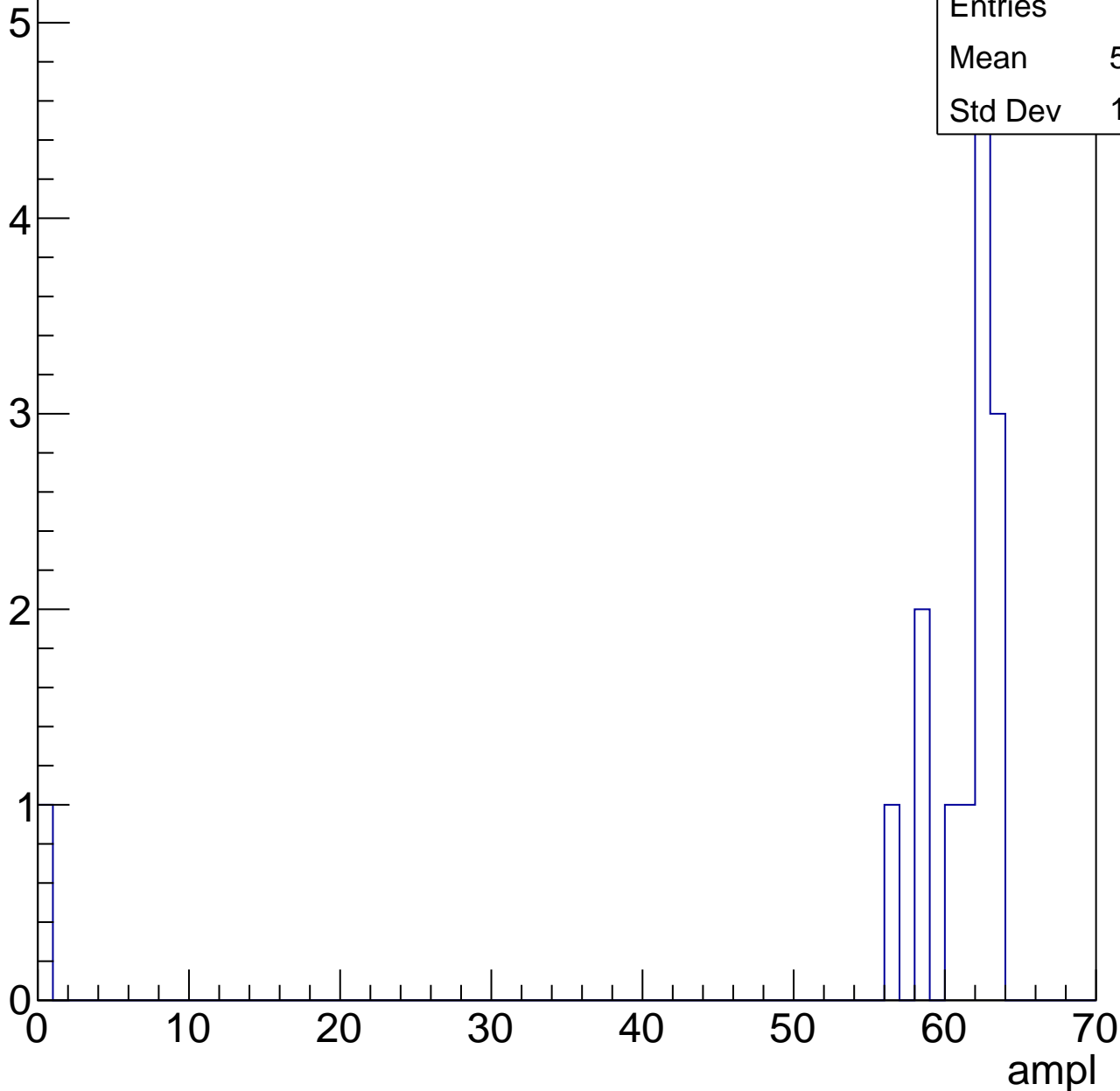


B1L100S, U15-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

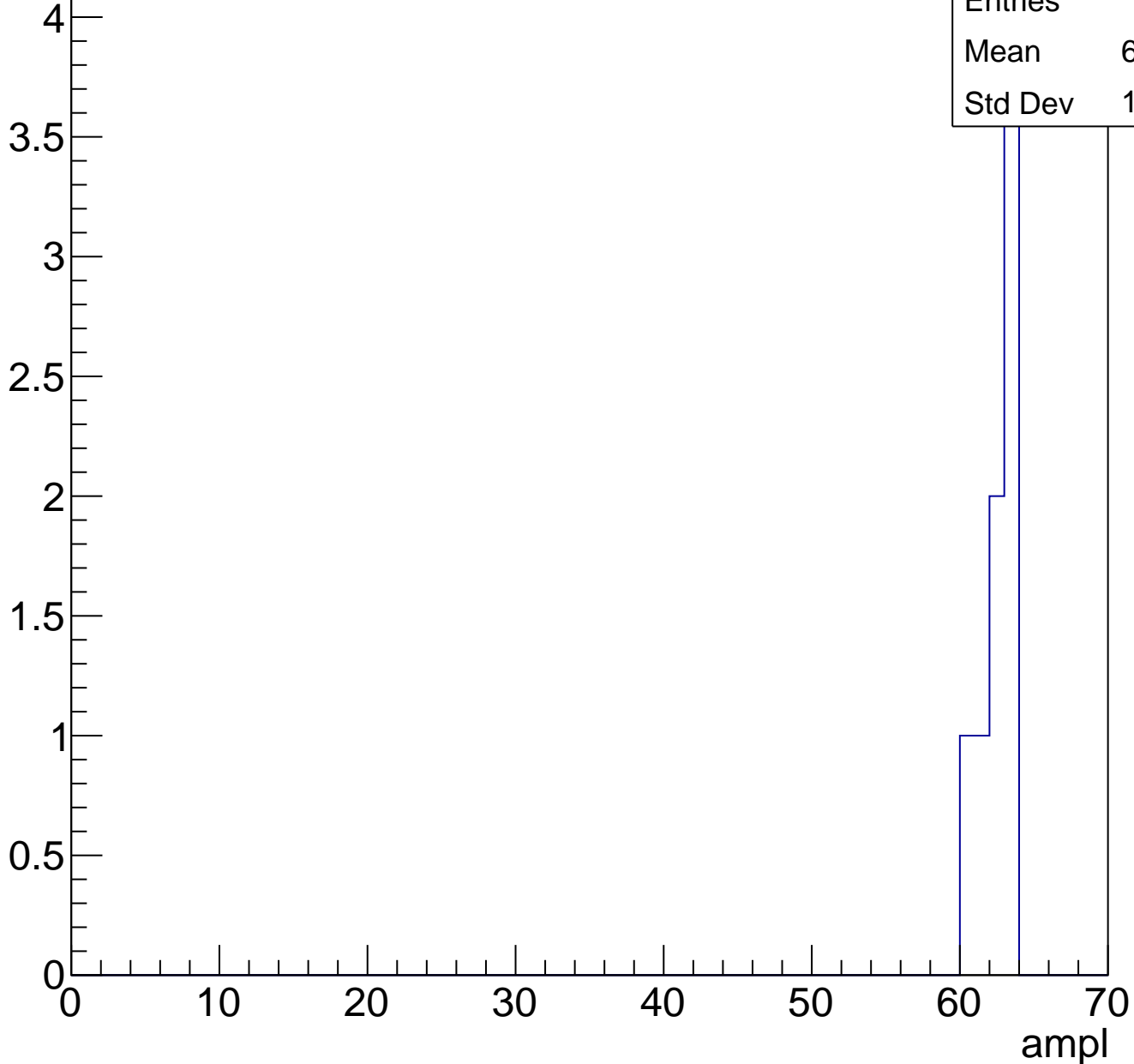
Entries	14
Mean	56.57
Std Dev	15.83



B1L100S, U15-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch57, adc0

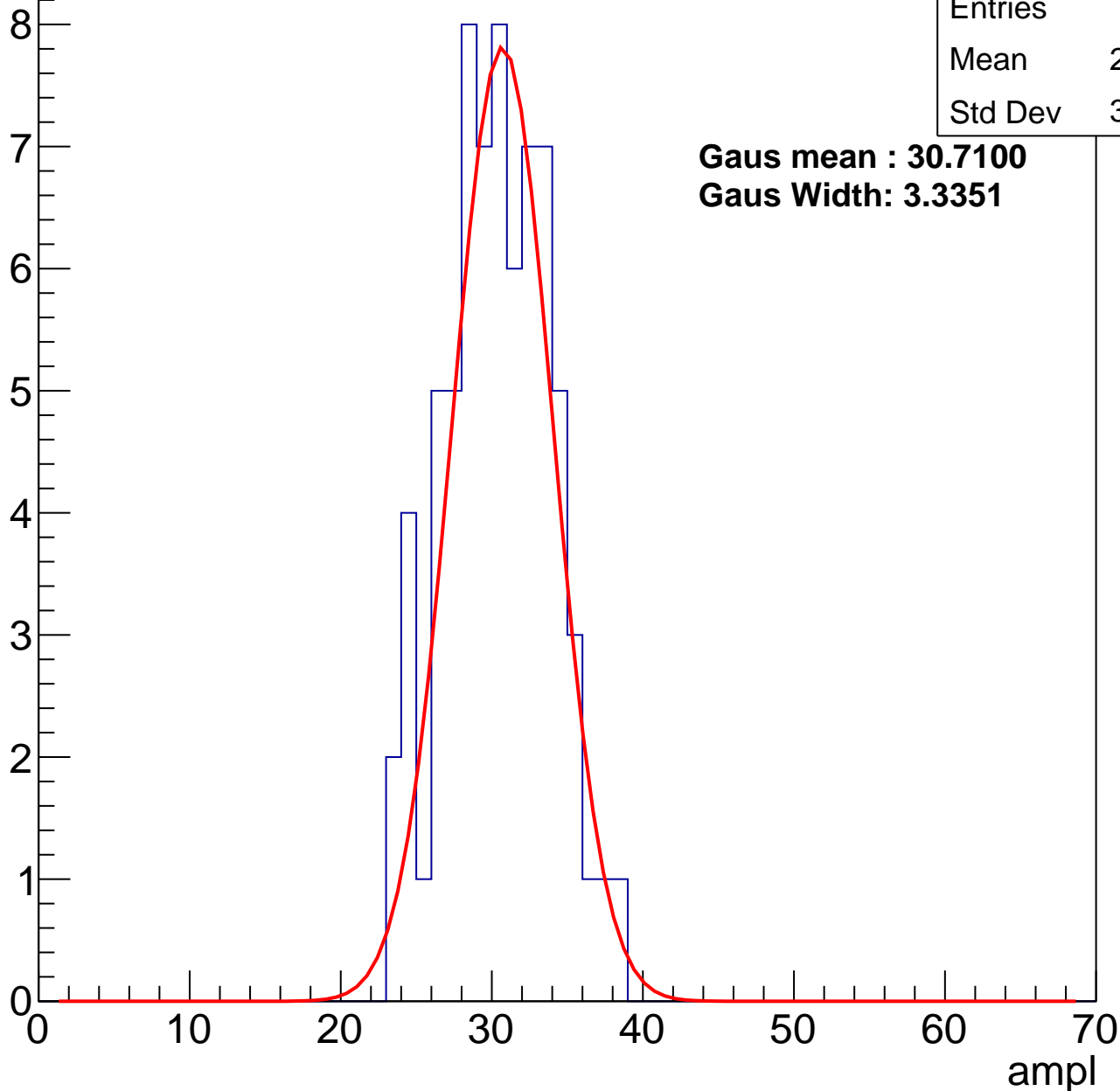
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.94
Std Dev	3.423

Gaus mean : 30.7100

Gaus Width: 3.3351



B1L100S, U15-ch57, adc1

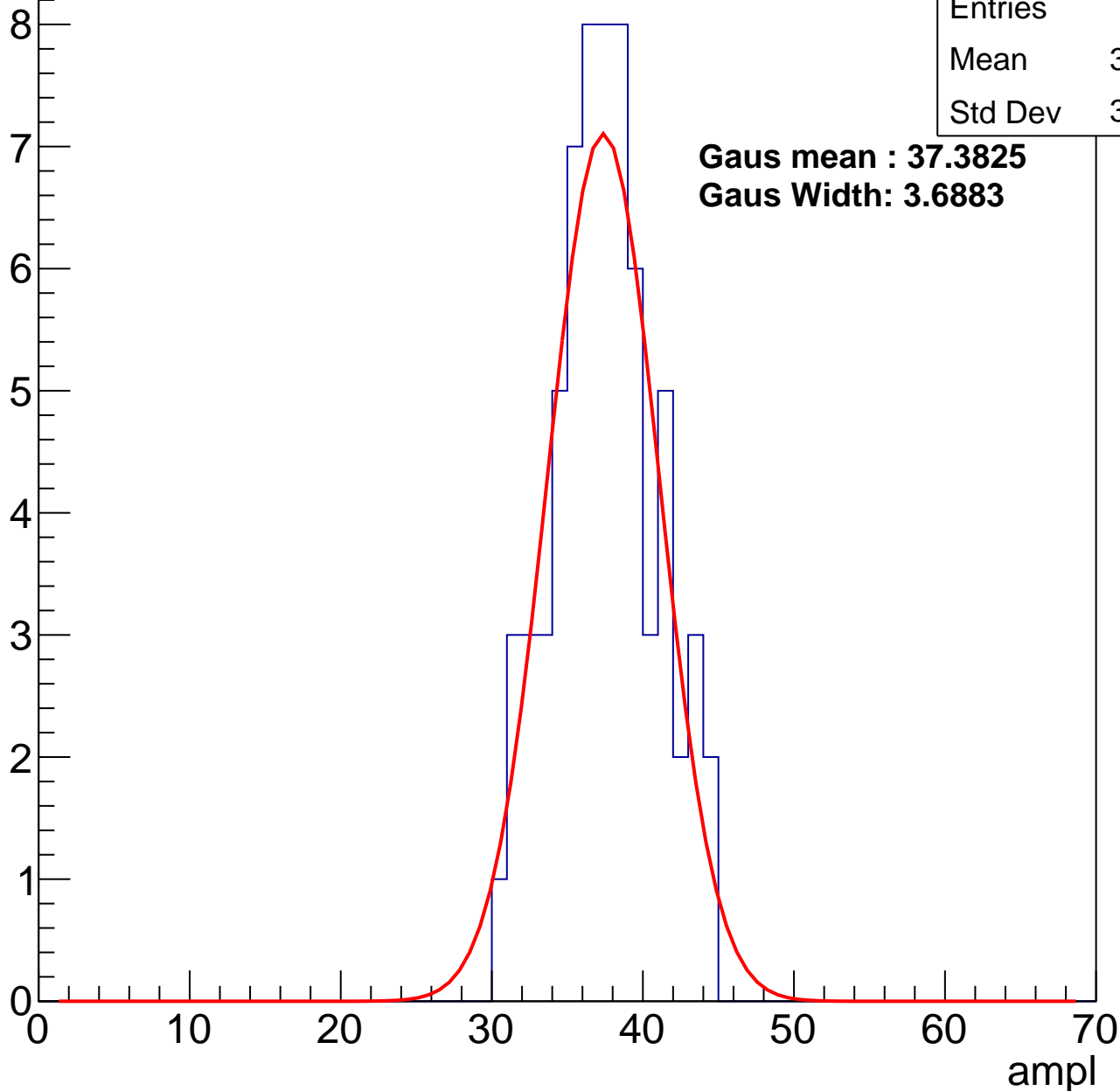
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.03
Std Dev	3.359

Gaus mean : 37.3825

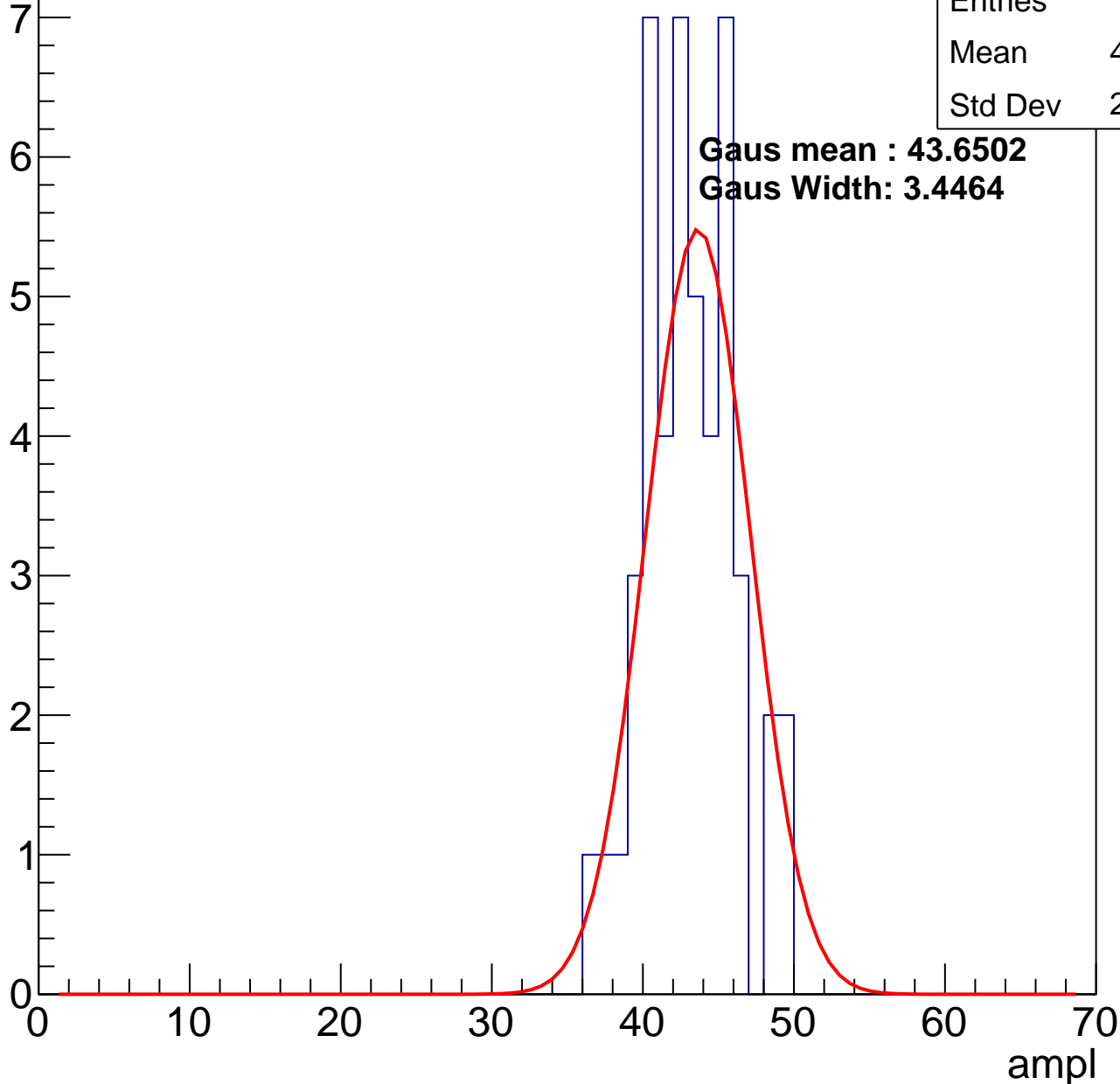
Gaus Width: 3.6883



B1L100S, U15-ch57, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

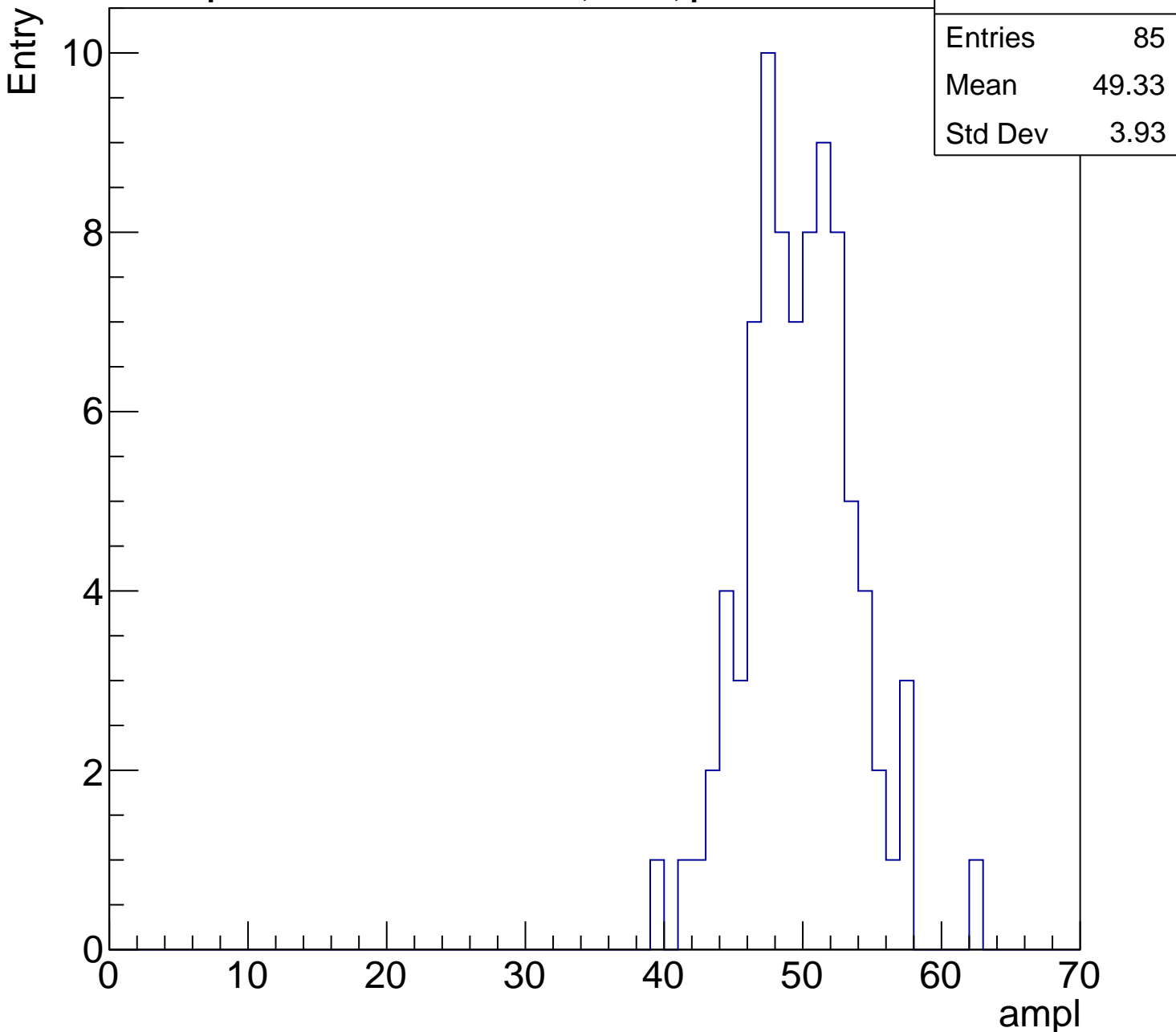
Entries	85
Mean	49.33
Std Dev	3.93

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

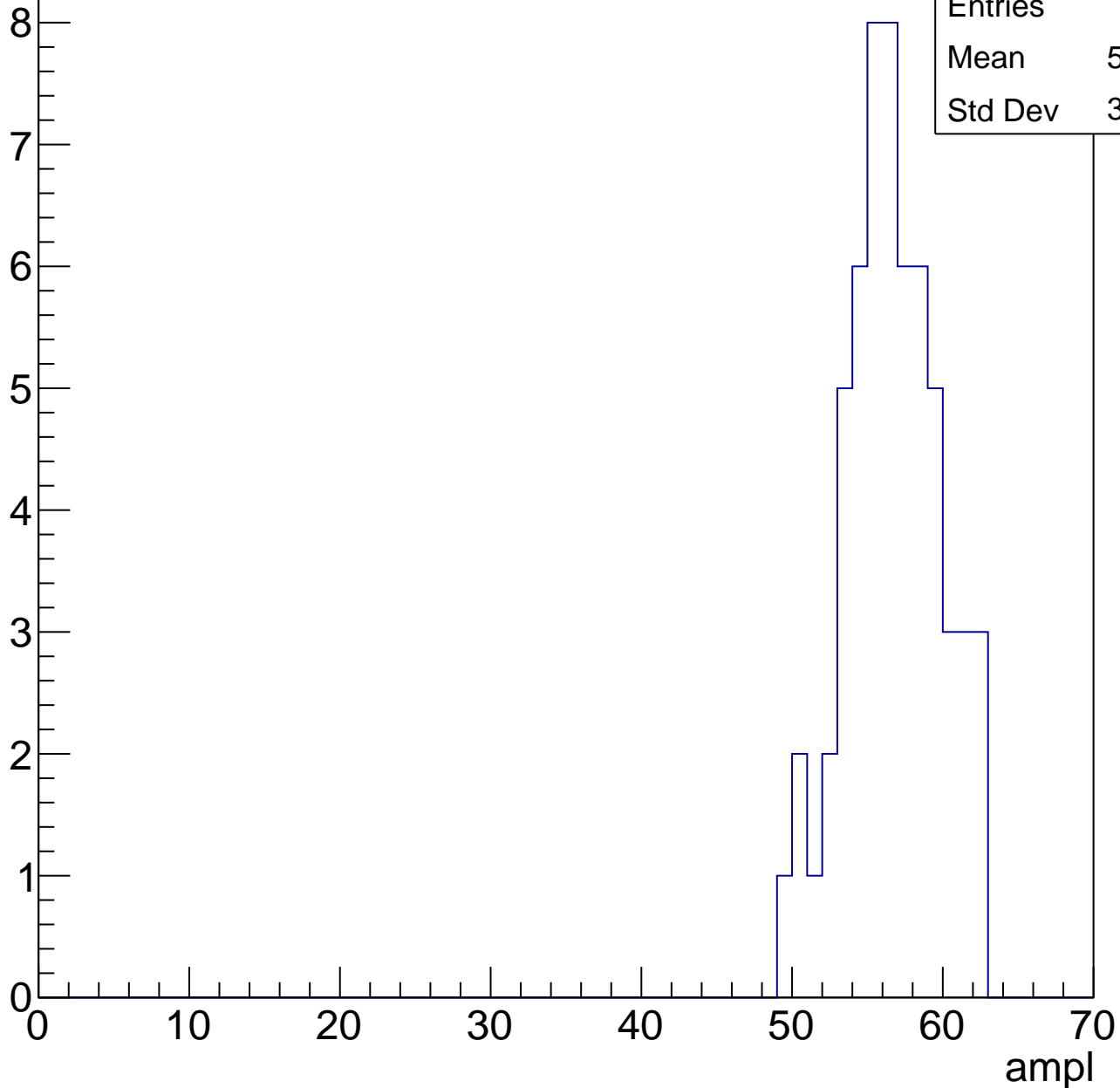
ampl



B1L100S, U15-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

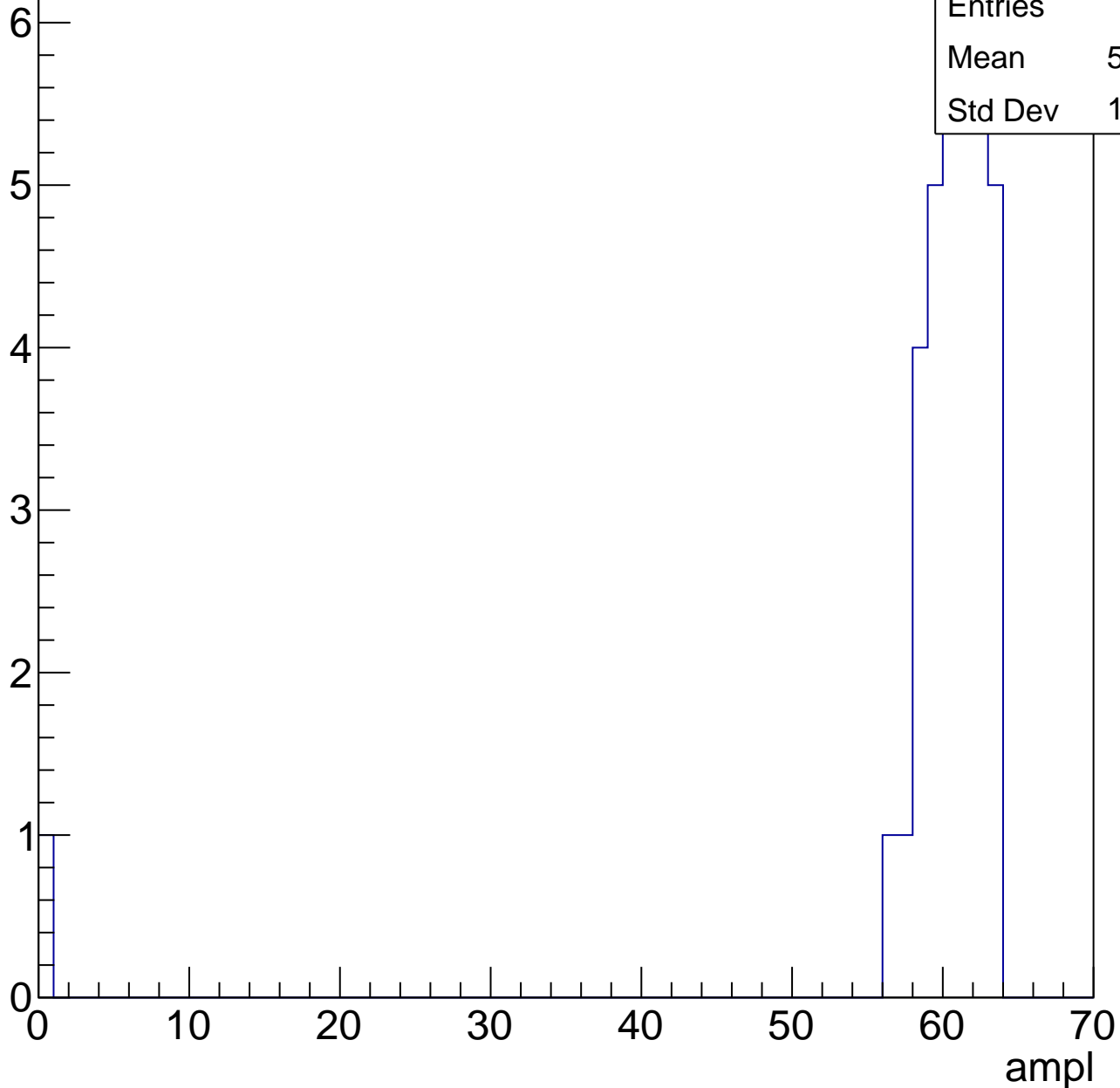


Entries	59
Mean	56.19
Std Dev	3.078

B1L100S, U15-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

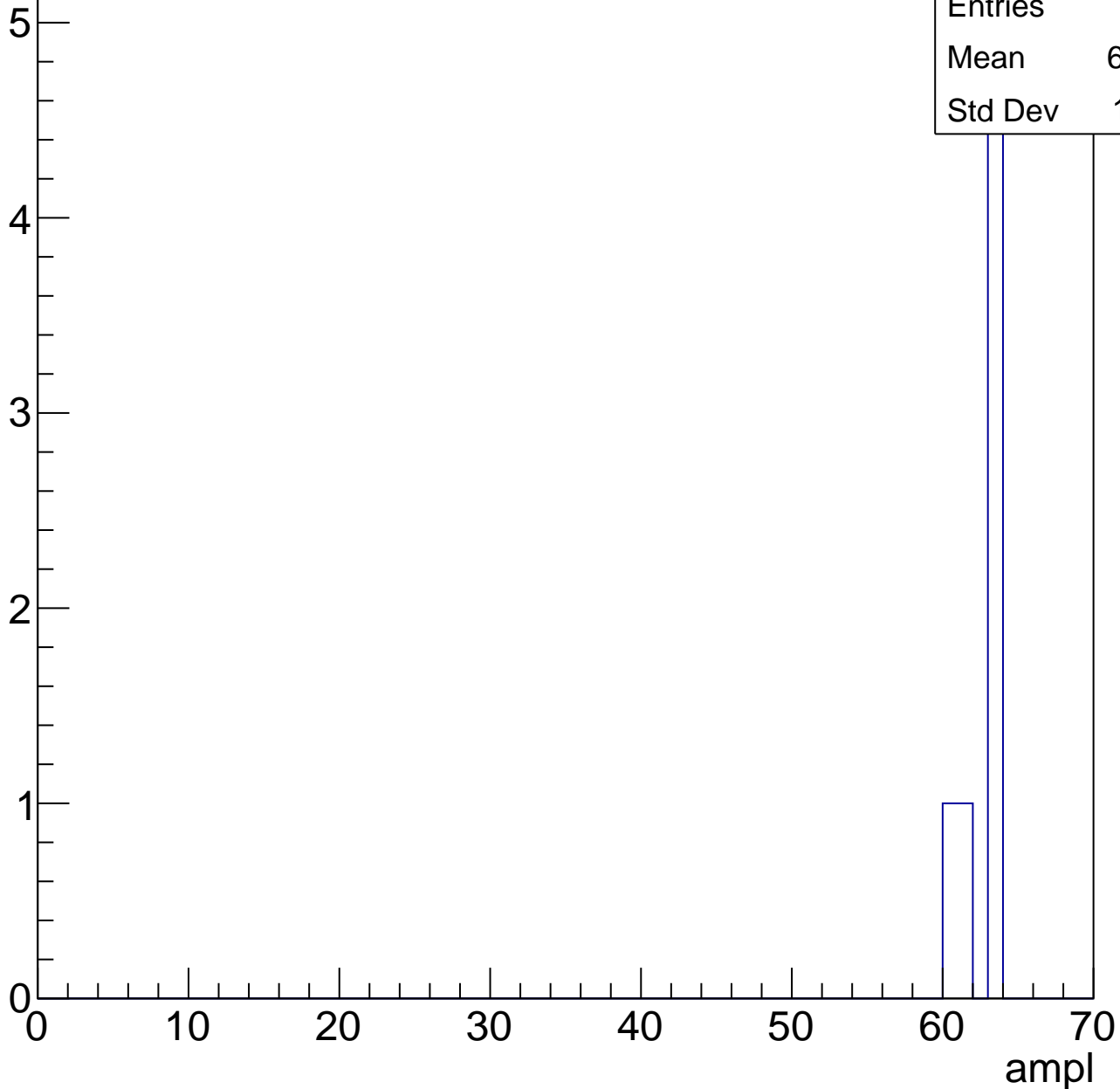


B1L100S, U15-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	7
Mean	62.29
Std Dev	1.161



B1L100S, U15-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	22
Std Dev	0

B1L100S, U15-ch58, adc0

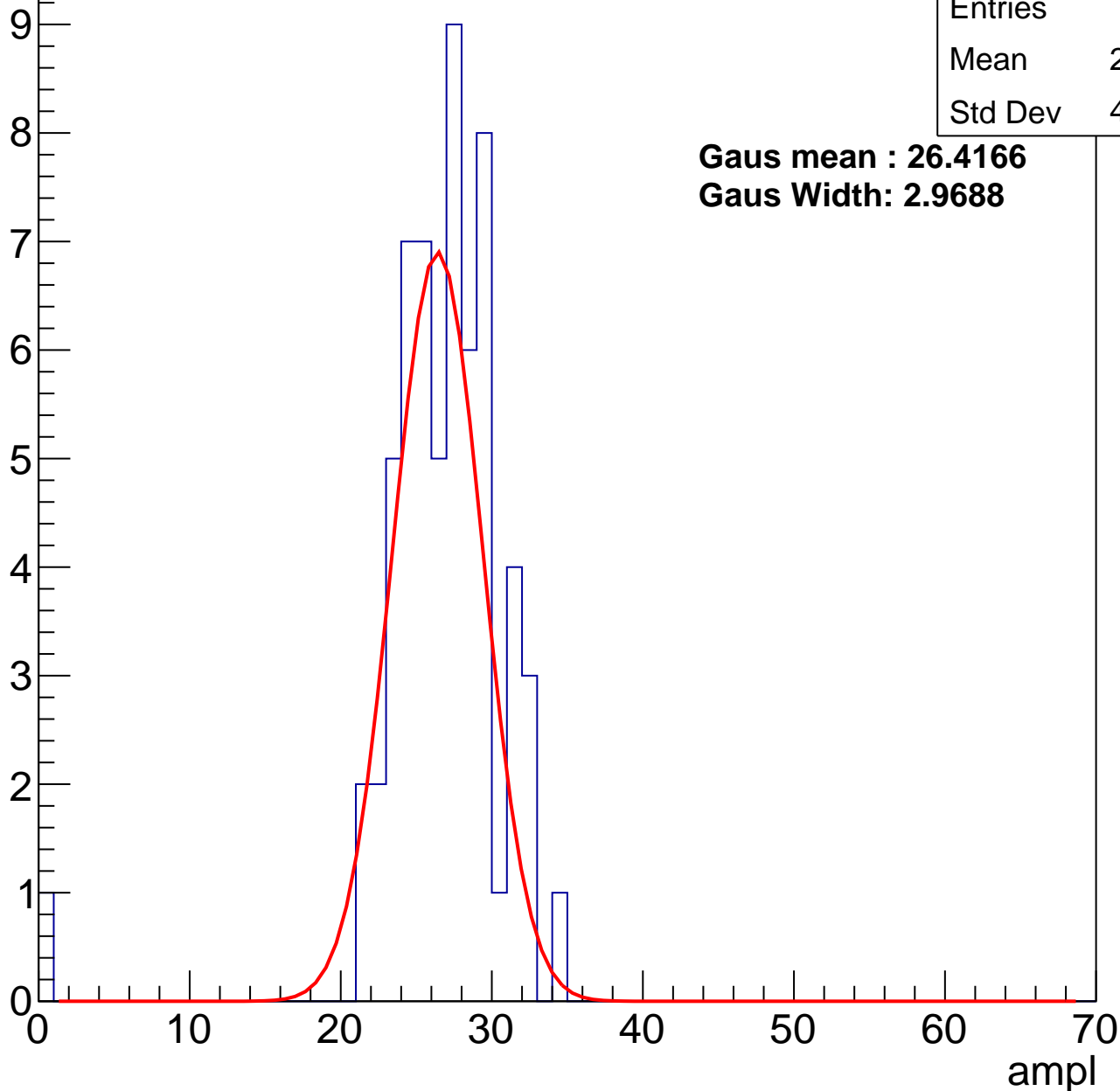
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	26.25
Std Dev	4.486

Gaus mean : 26.4166

Gaus Width: 2.9688



B1L100S, U15-ch58, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	94
Mean	34.22
Std Dev	4.938

Gaus mean : 35.0379

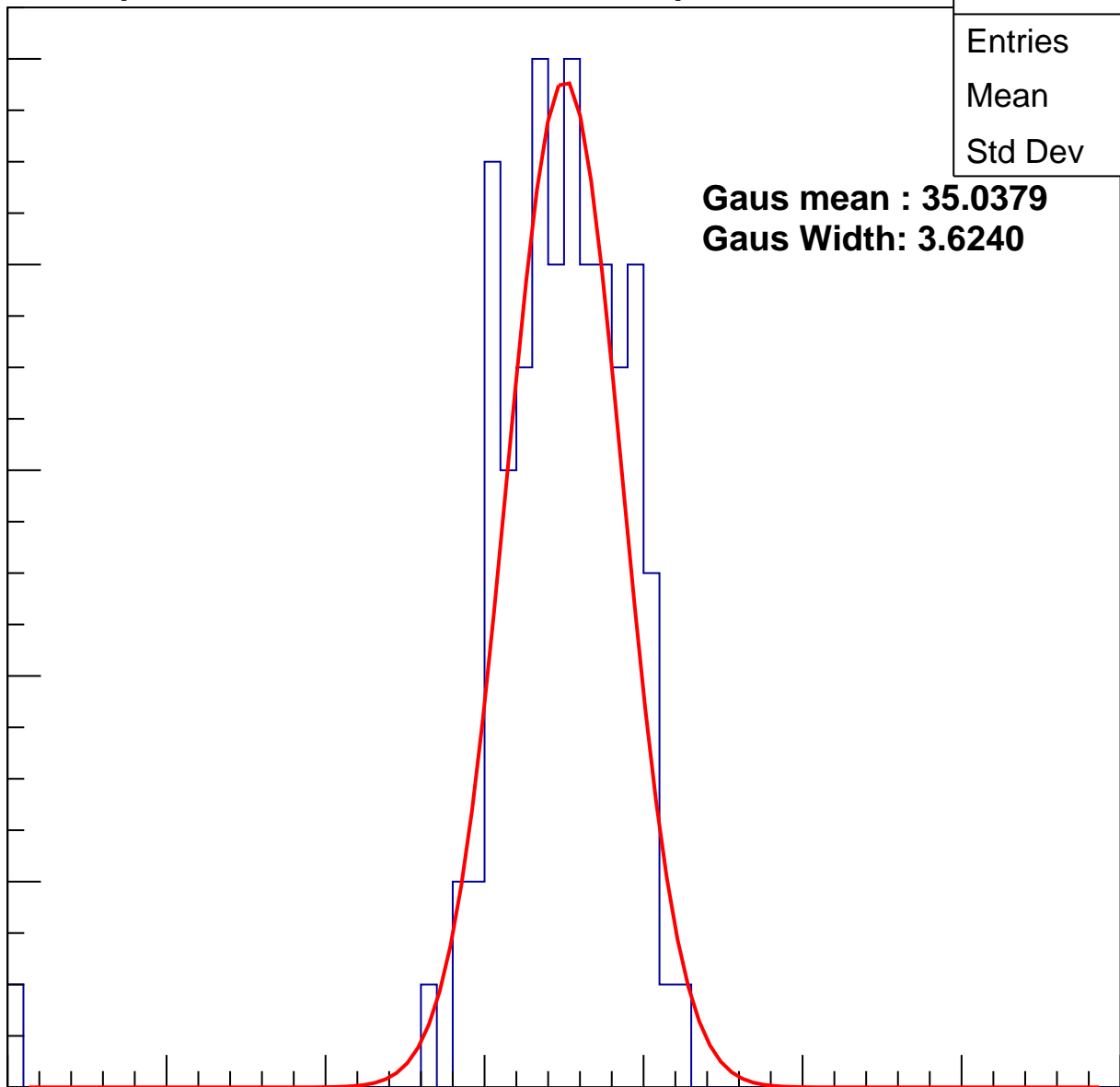
Gaus Width: 3.6240

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch58, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	54
Mean	41.94
Std Dev	3.033

Gaus mean : 42.7936

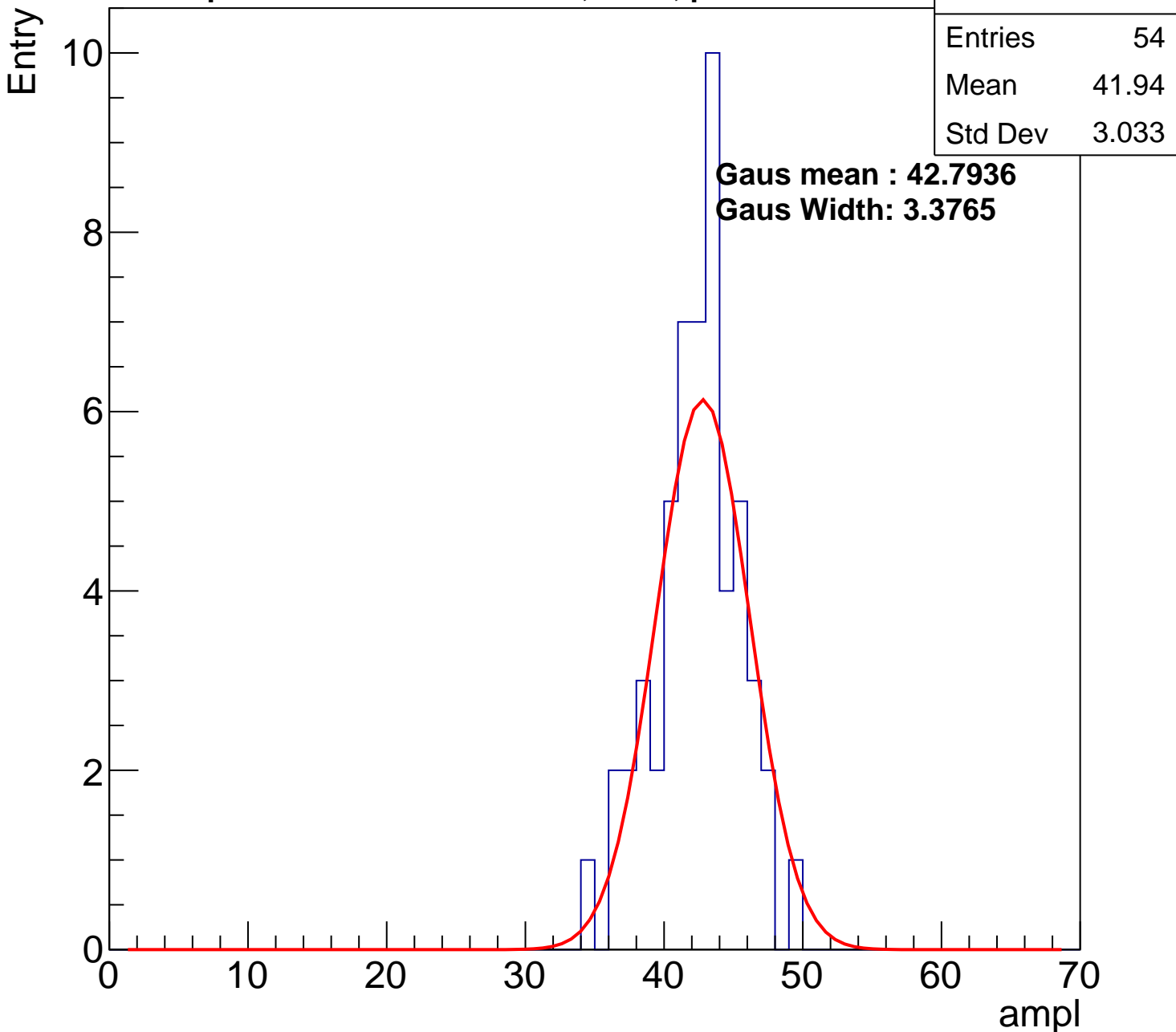
Gaus Width: 3.3765

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

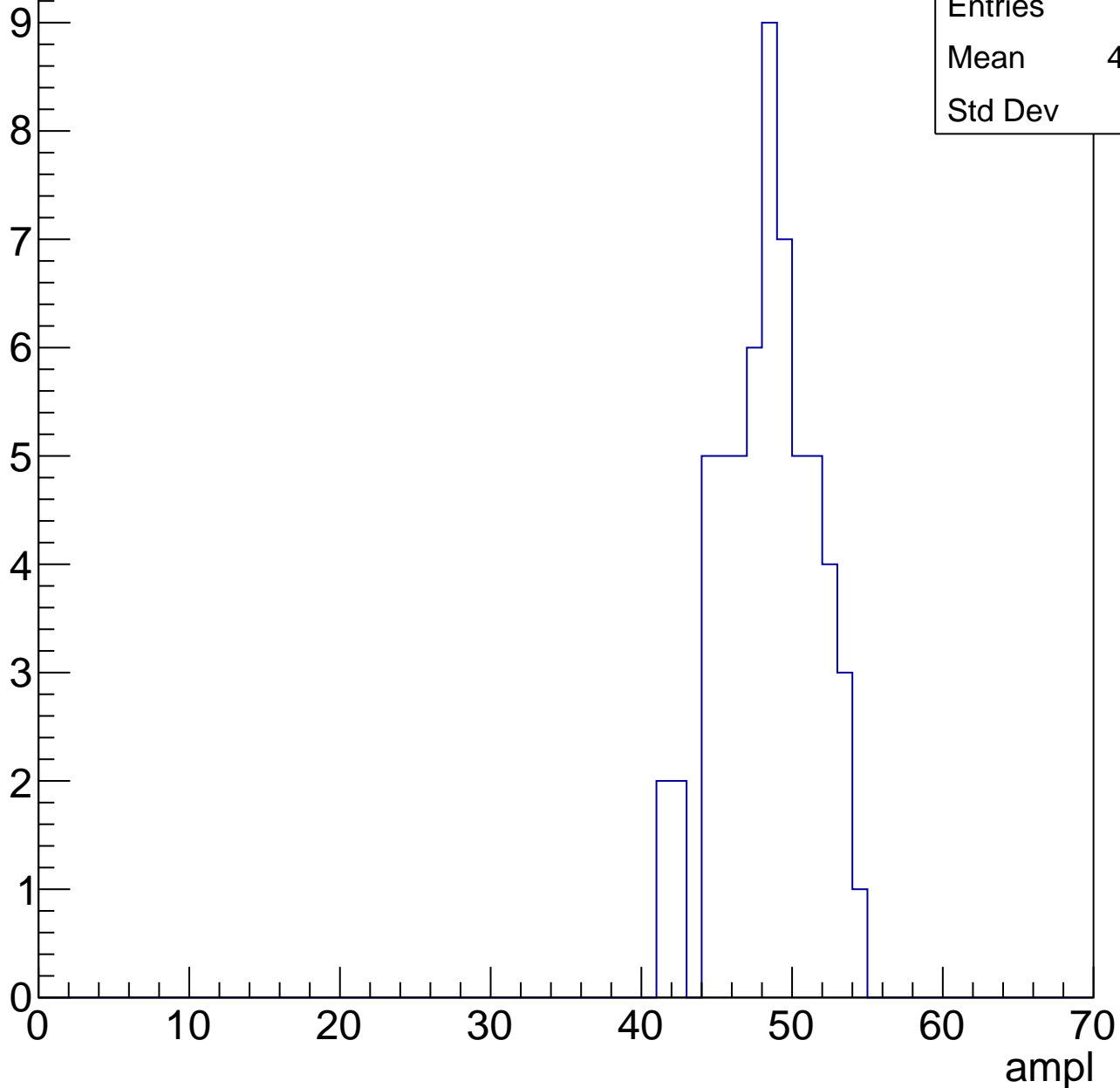
ampl



B1L100S, U15-ch58, adc3

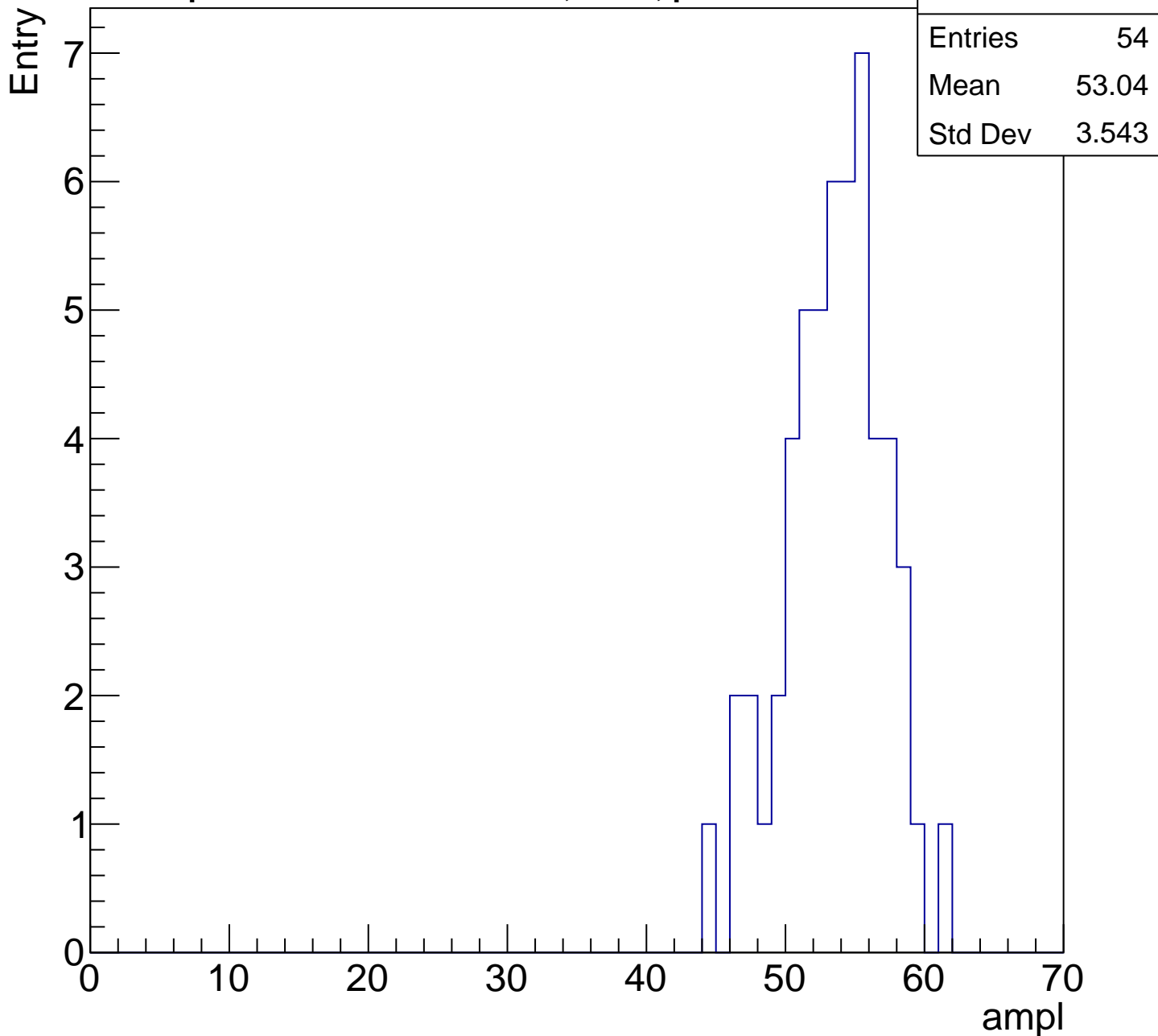
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

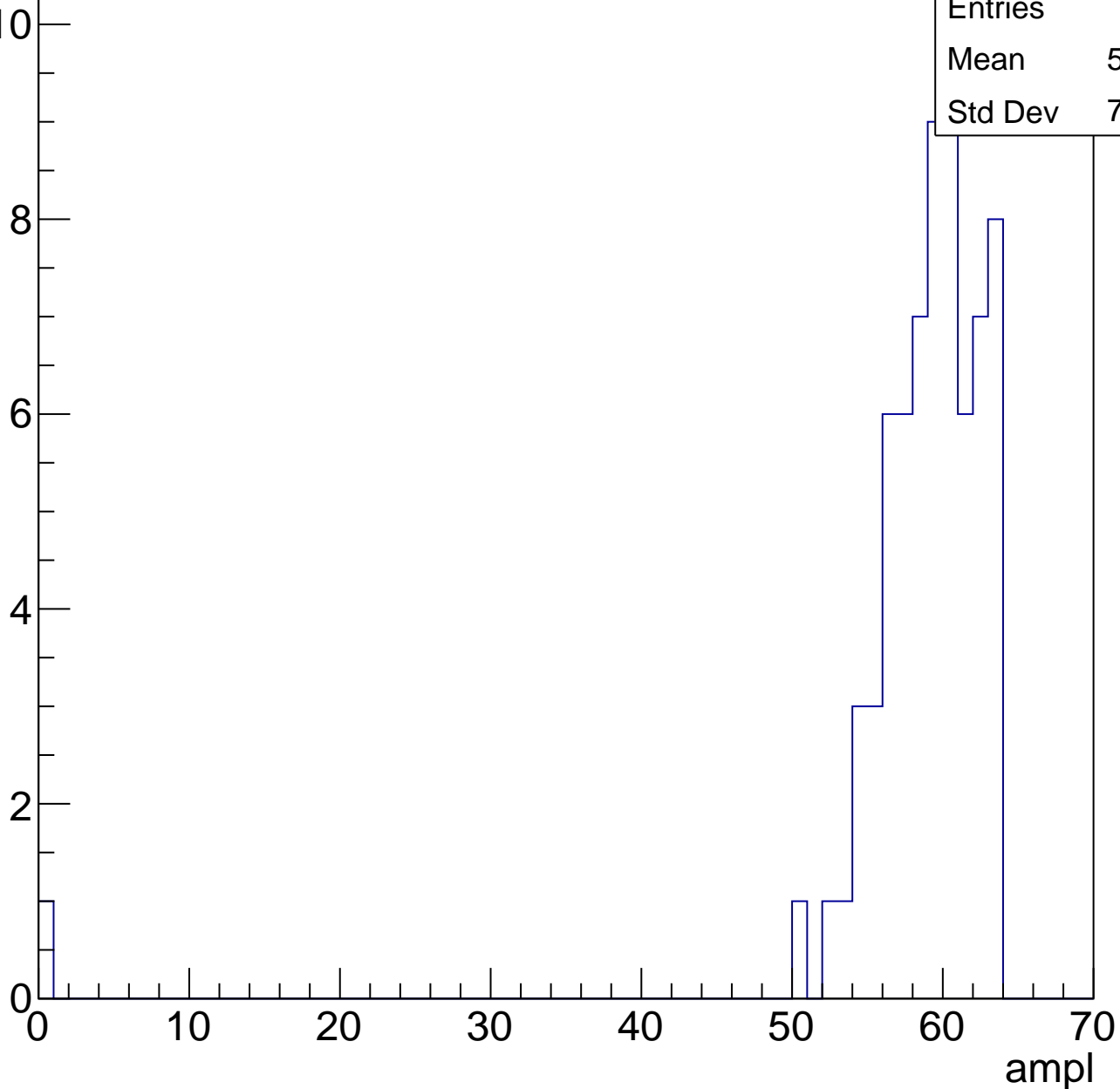


B1L100S, U15-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	57.99
Std Dev	7.622



B1L100S, U15-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch59, adc0

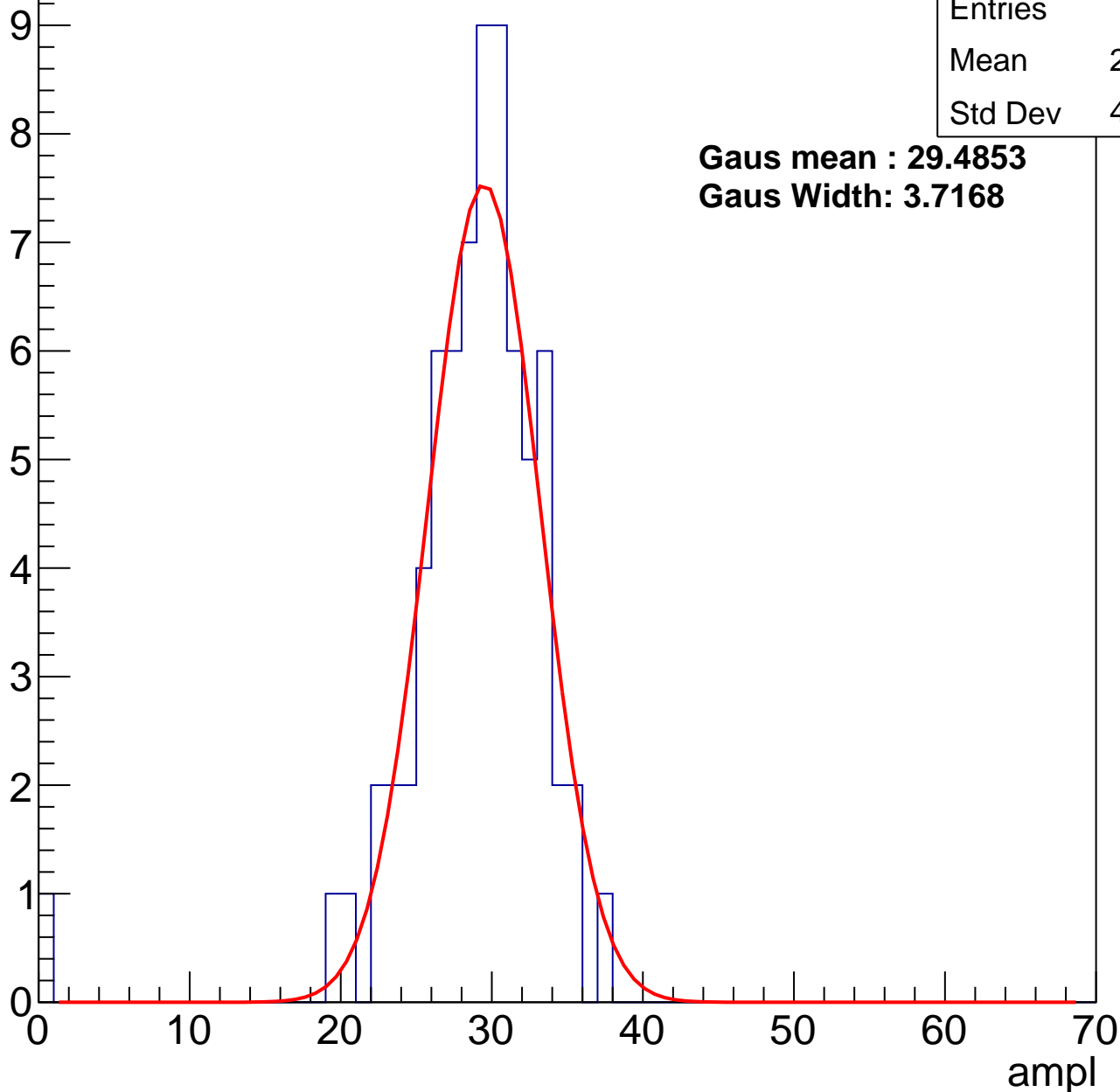
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.35
Std Dev	4.888

Gaus mean : 29.4853

Gaus Width: 3.7168



B1L100S, U15-ch59, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	36.32
Std Dev	3.139

Gaus mean : 36.5788

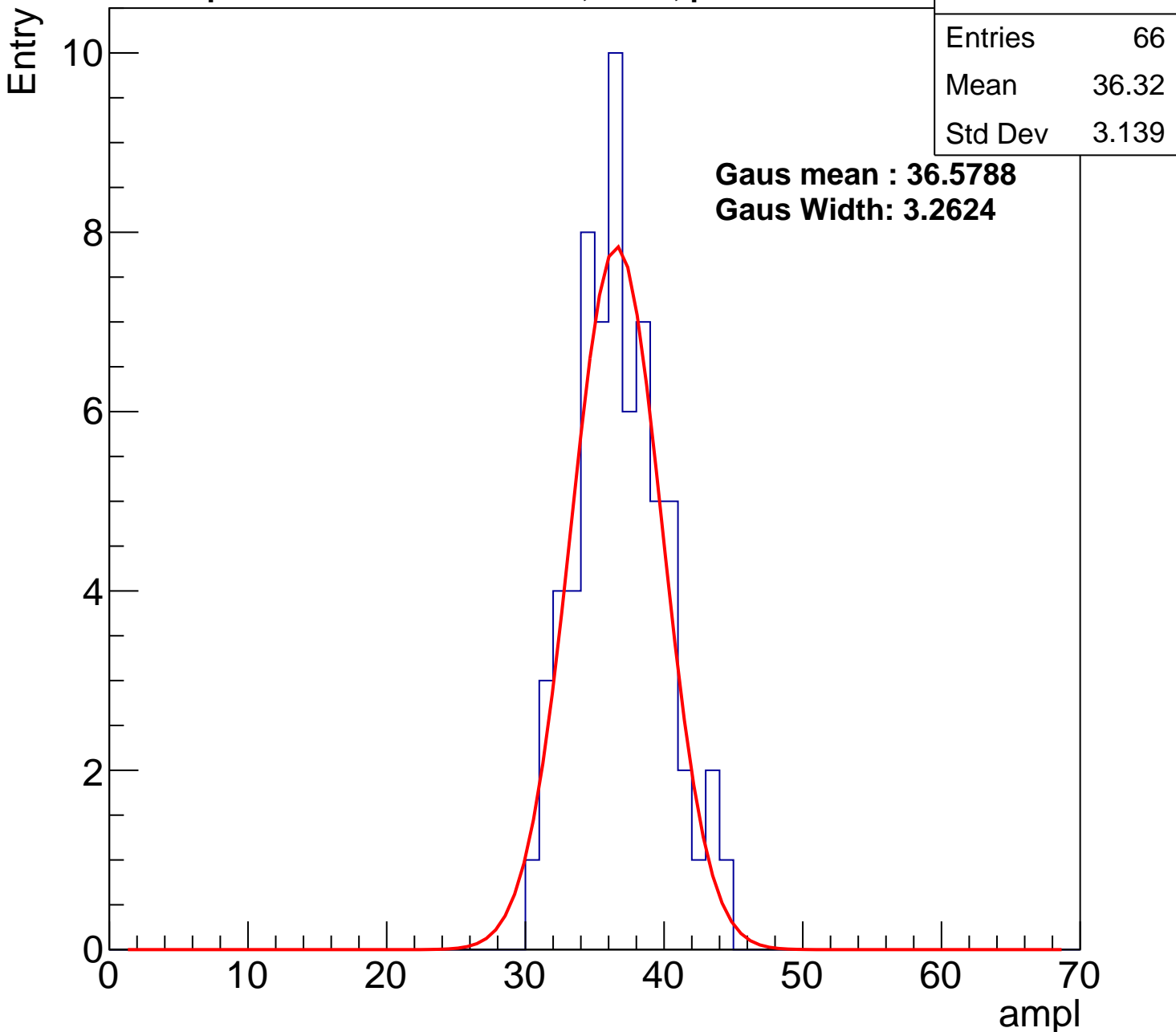
Gaus Width: 3.2624

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch59, adc2

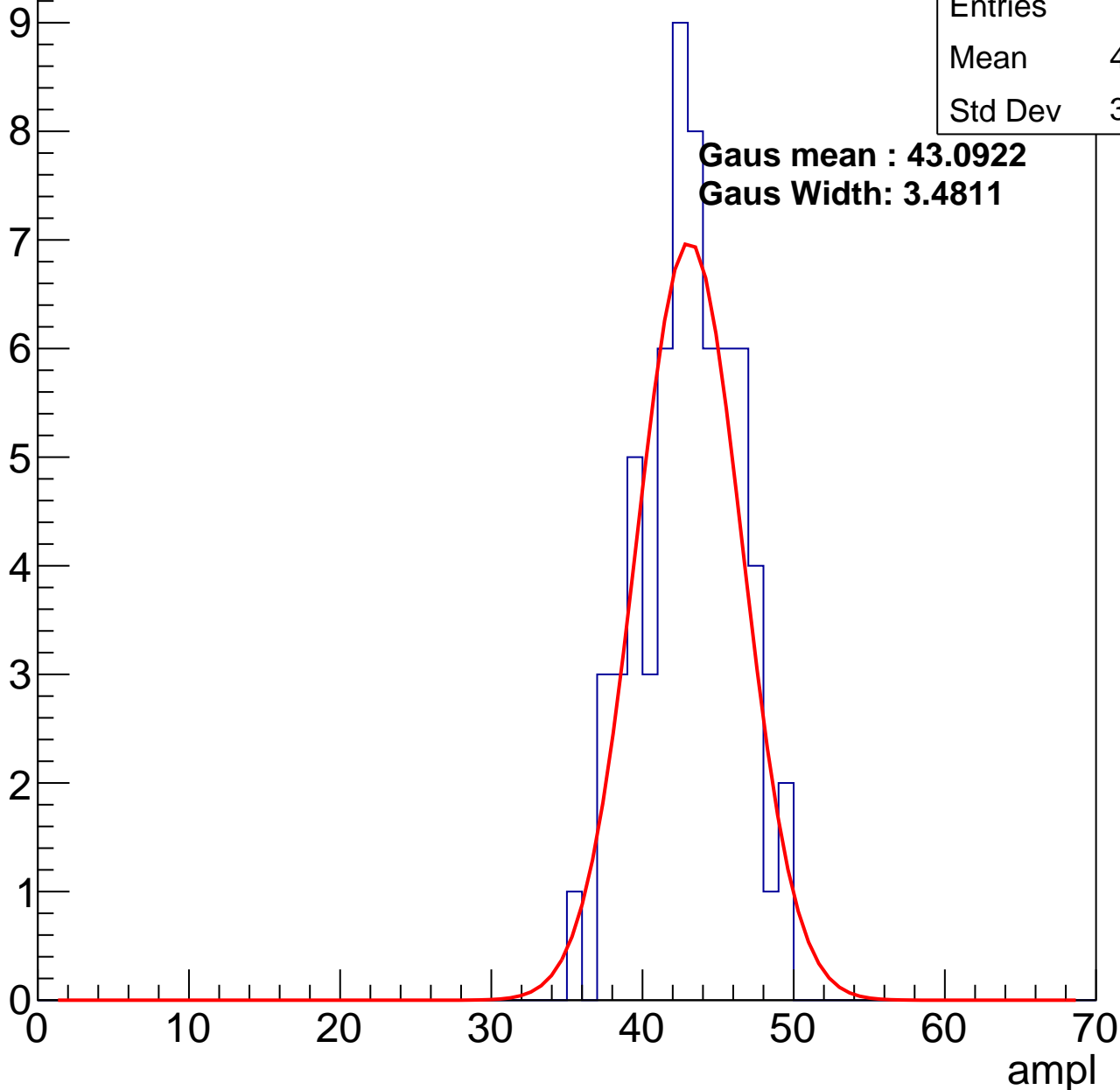
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.65
Std Dev	3.148

Gaus mean : 43.0922

Gaus Width: 3.4811

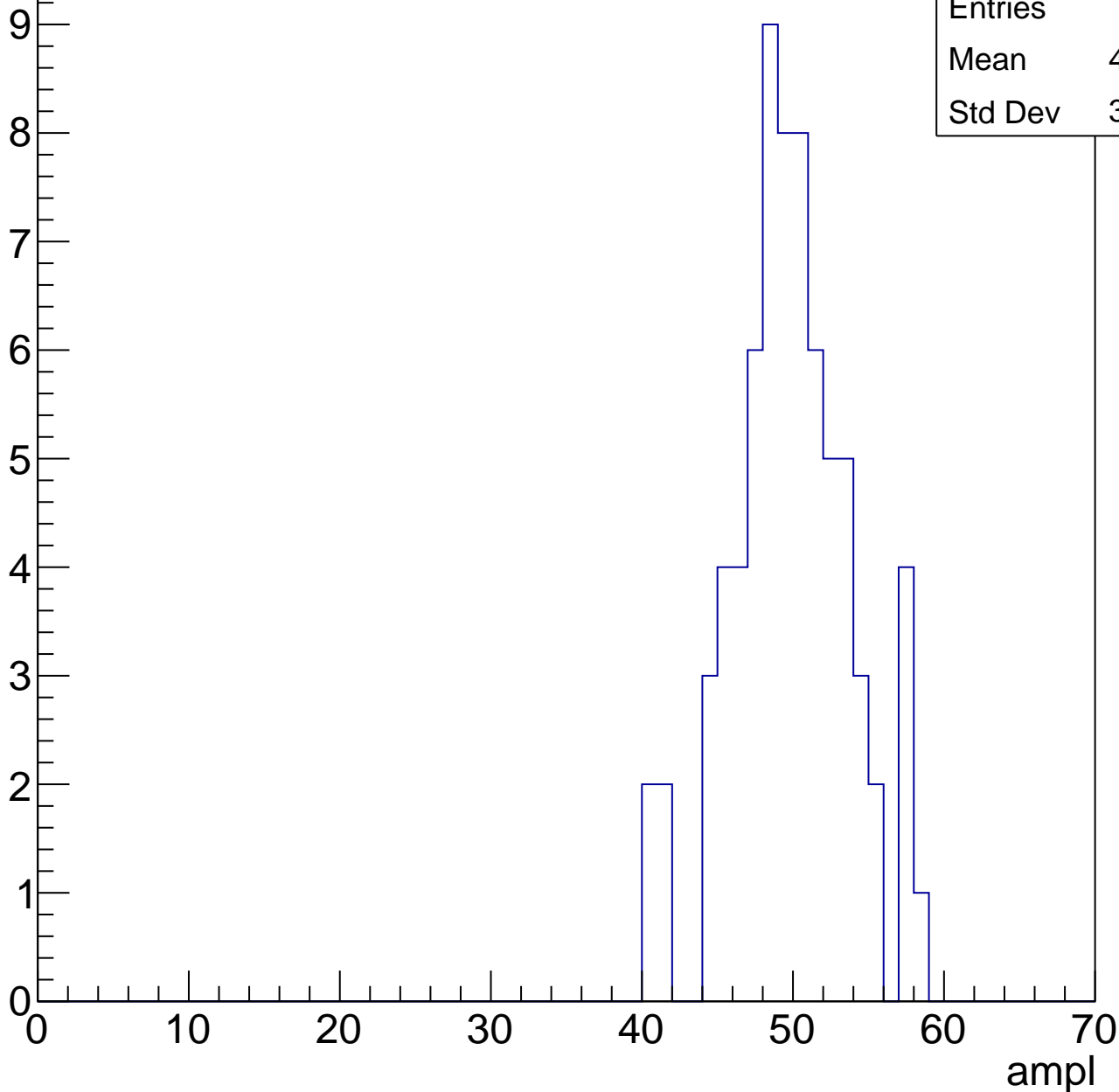


B1L100S, U15-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	49.35
Std Dev	3.962

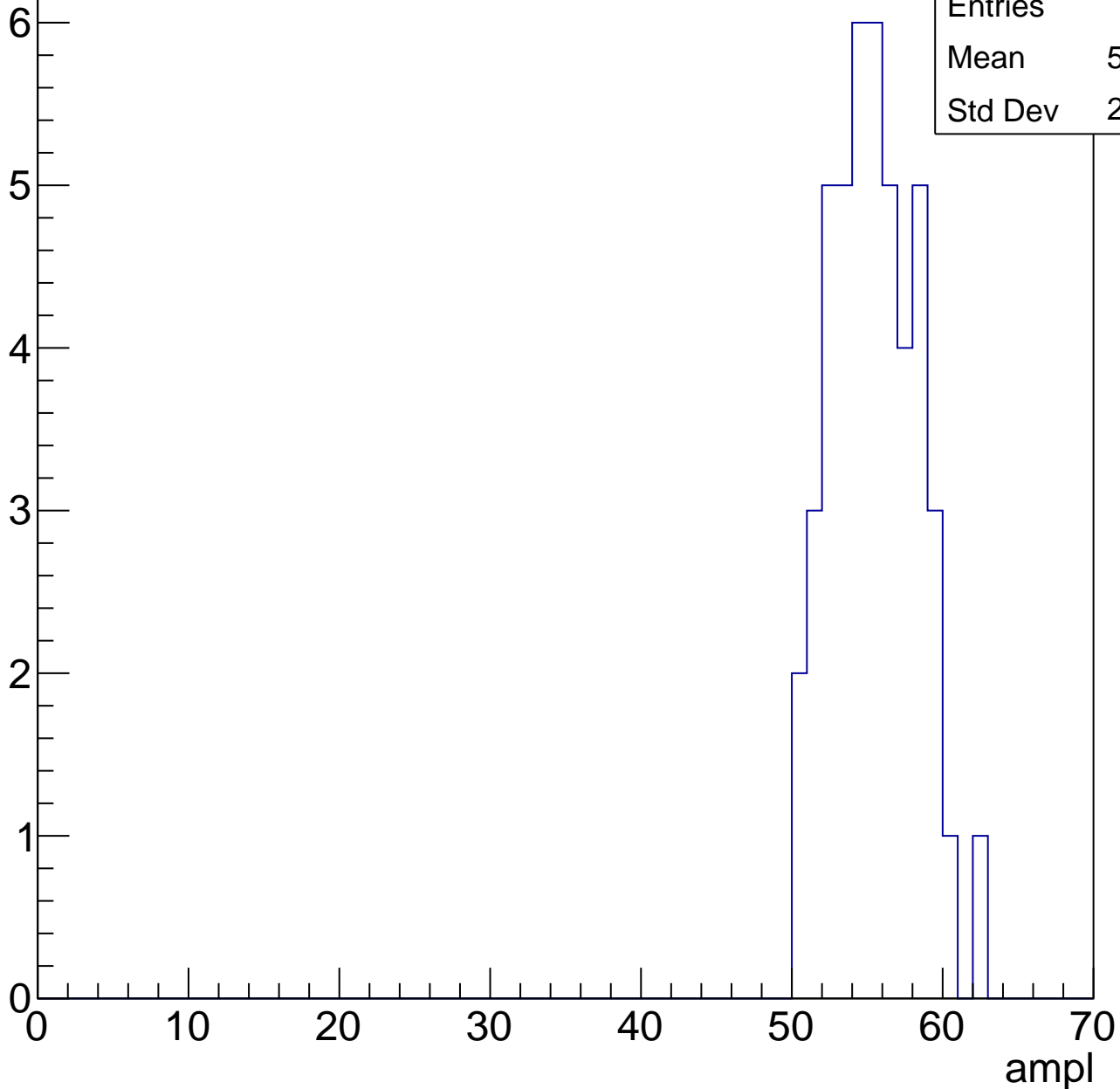


B1L100S, U15-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	54.98
Std Dev	2.786

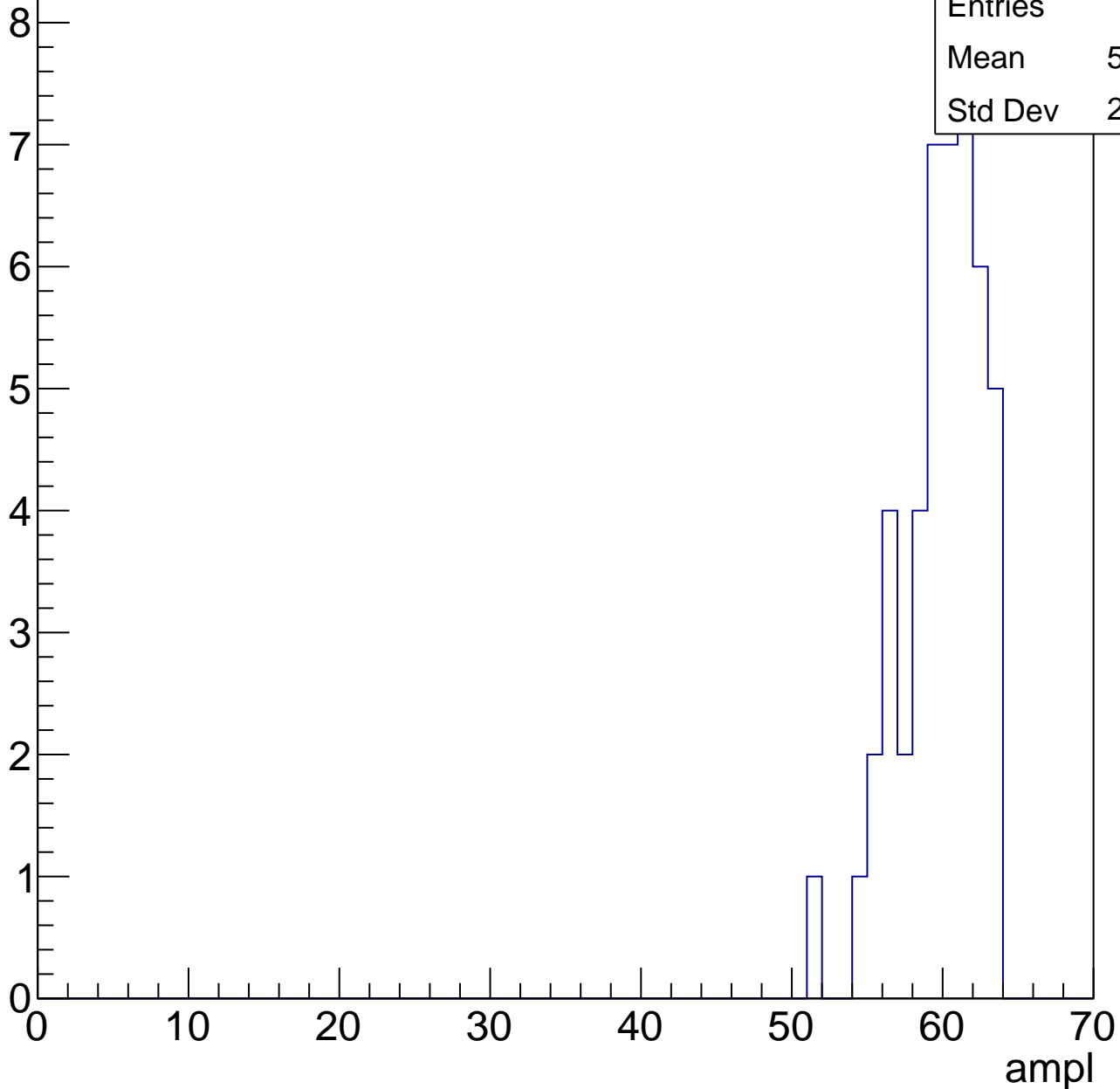


B1L100S, U15-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	59.43
Std Dev	2.664

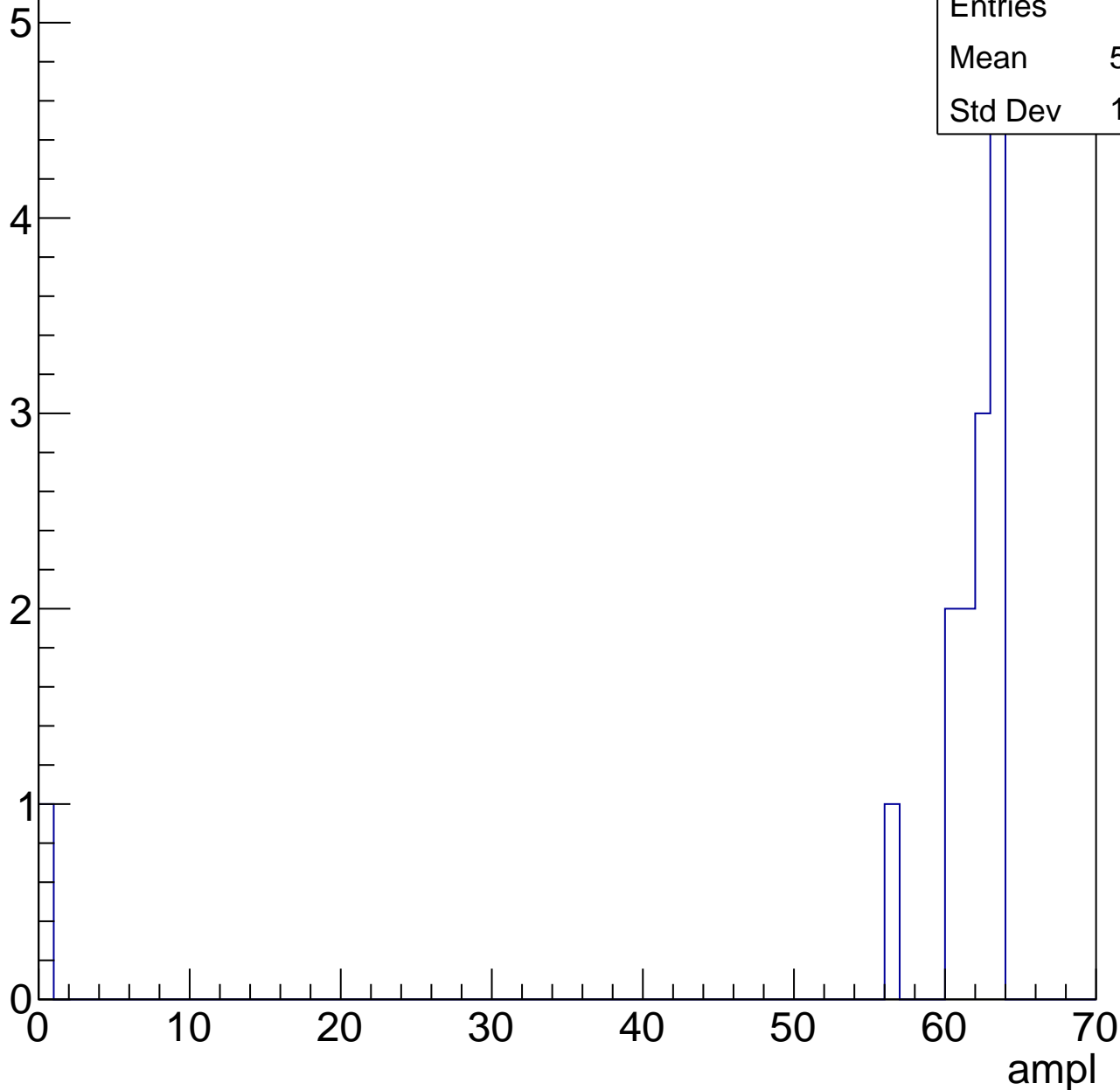


B1L100S, U15-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	57.07
Std Dev	15.93



B1L100S, U15-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch60, adc0

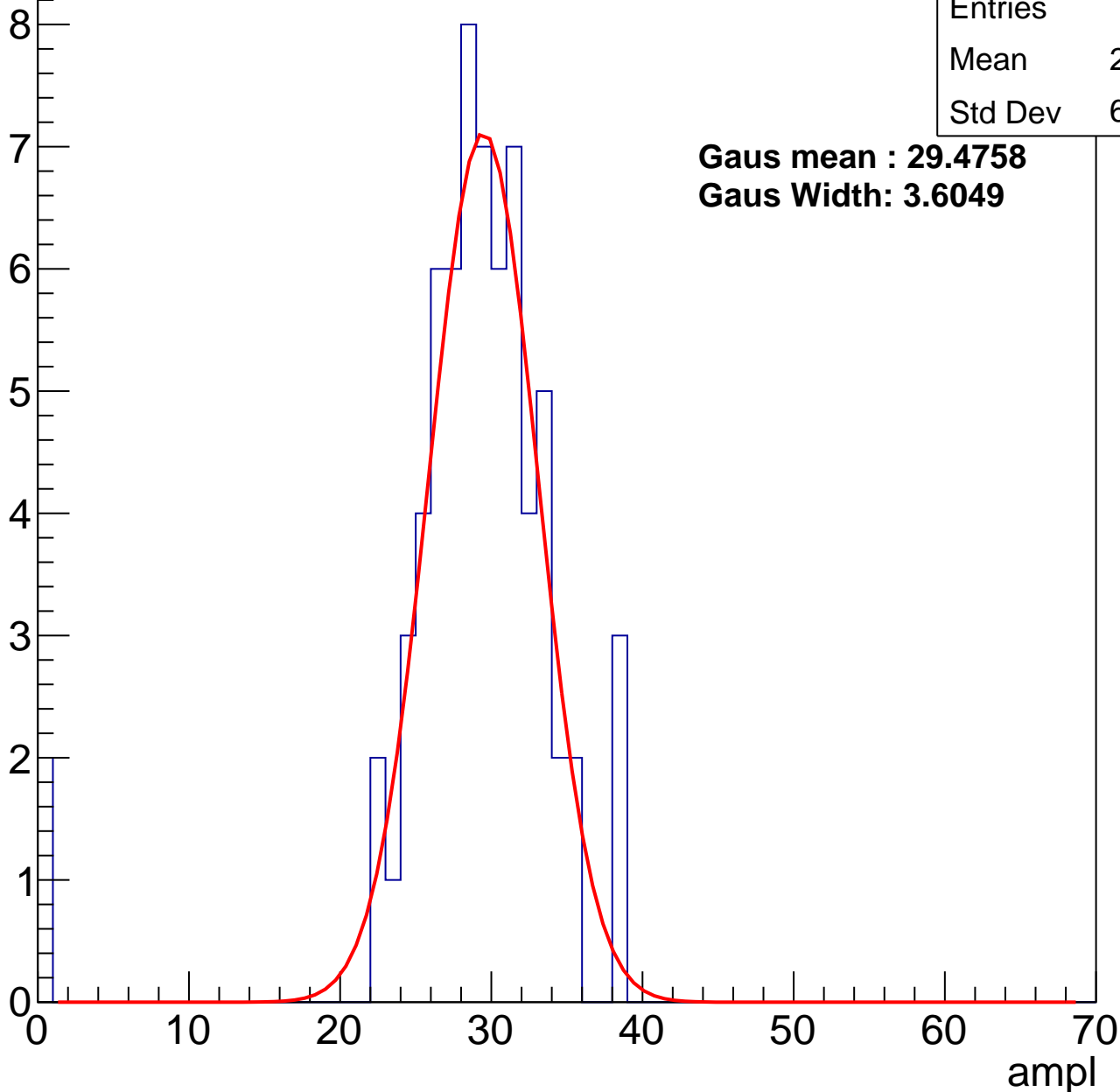
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.32
Std Dev	6.093

Gaus mean : 29.4758

Gaus Width: 3.6049



B1L100S, U15-ch60, adc1

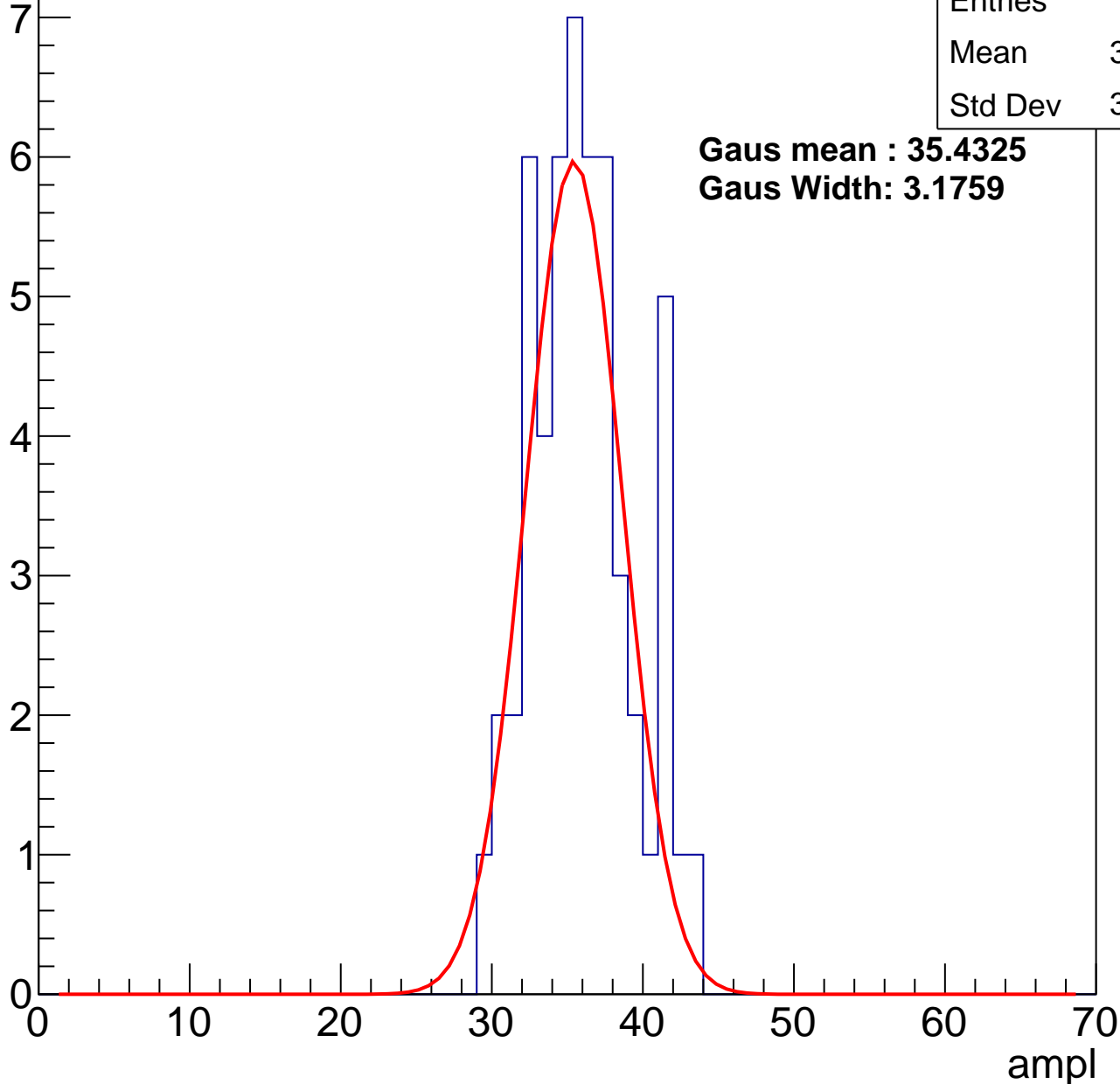
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	35.55
Std Dev	3.323

Gaus mean : 35.4325

Gaus Width: 3.1759



B1L100S, U15-ch60, adc2

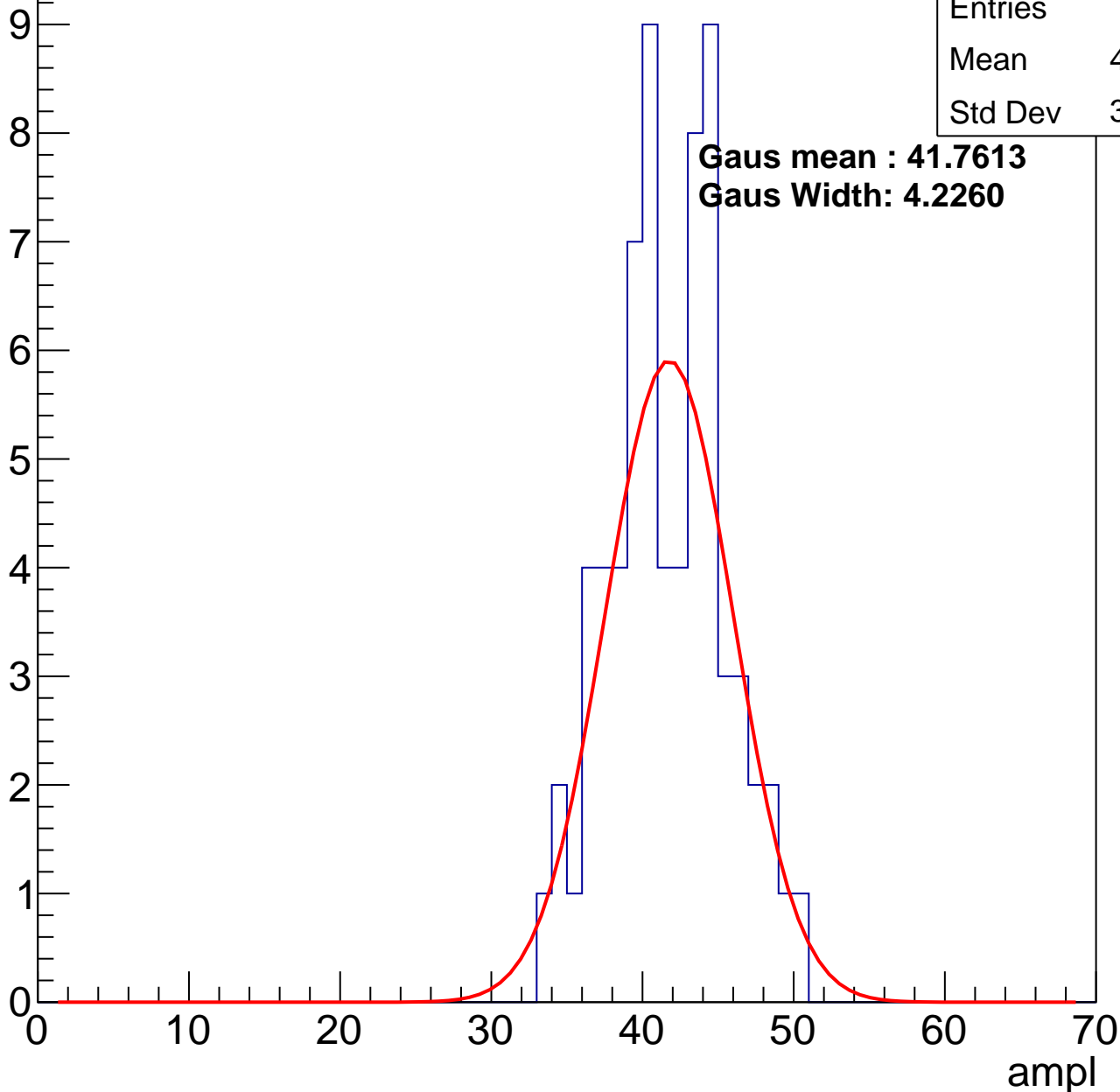
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	41.26
Std Dev	3.779

Gaus mean : 41.7613

Gaus Width: 4.2260

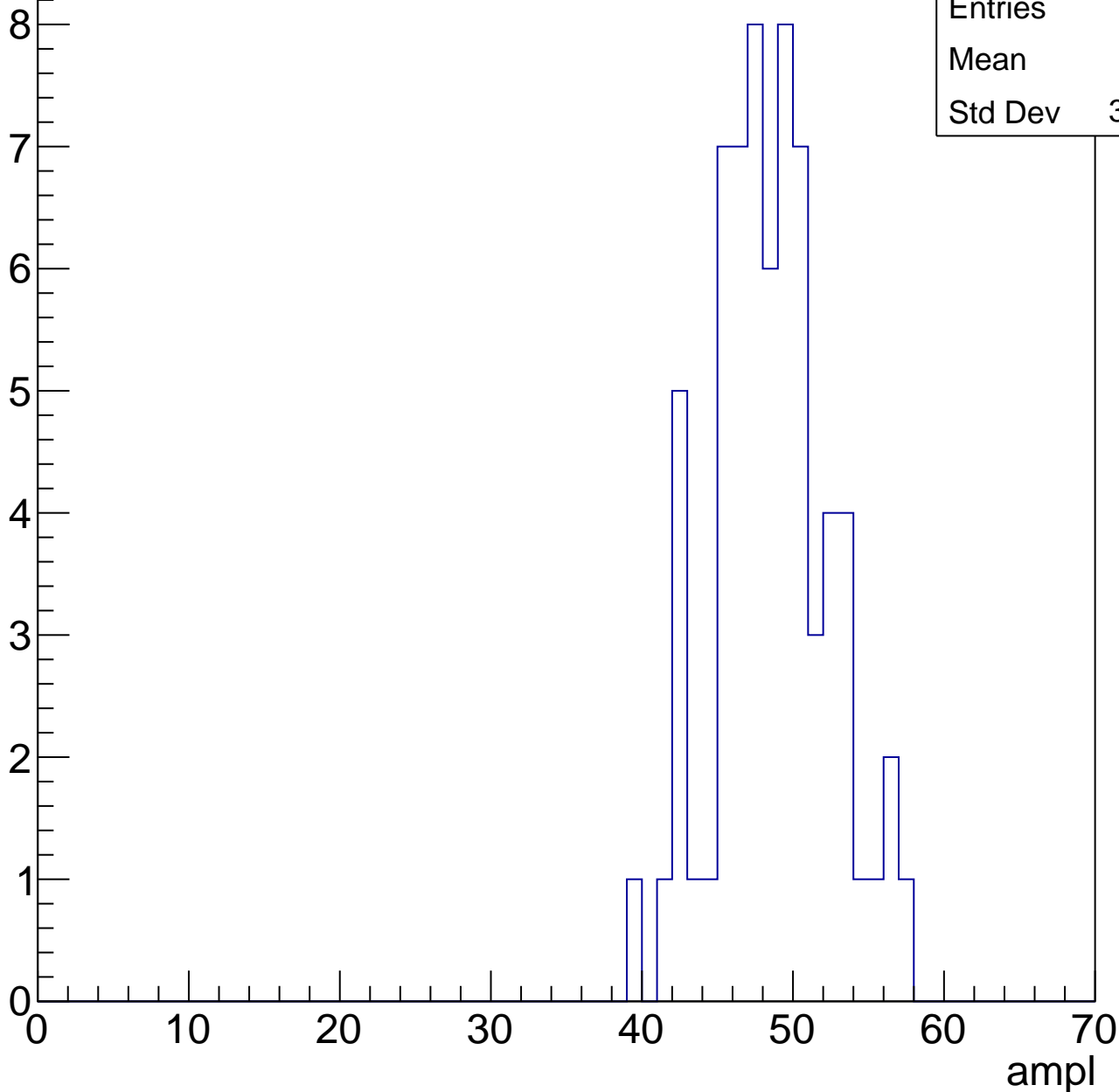


B1L100S, U15-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	48.1
Std Dev	3.777

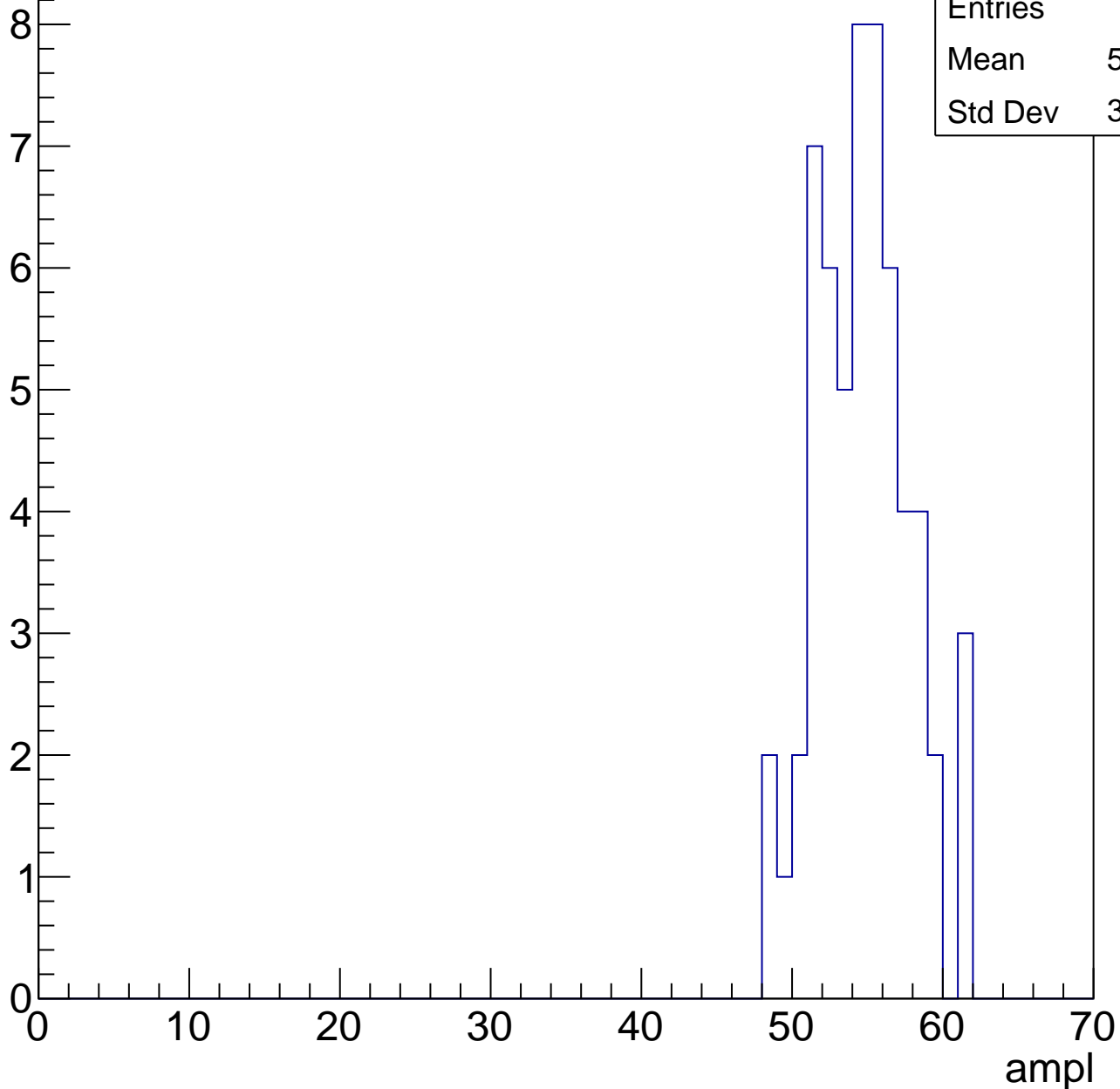


B1L100S, U15-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	54.28
Std Dev	3.067



B1L100S, U15-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.6
Std Dev	2.441

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

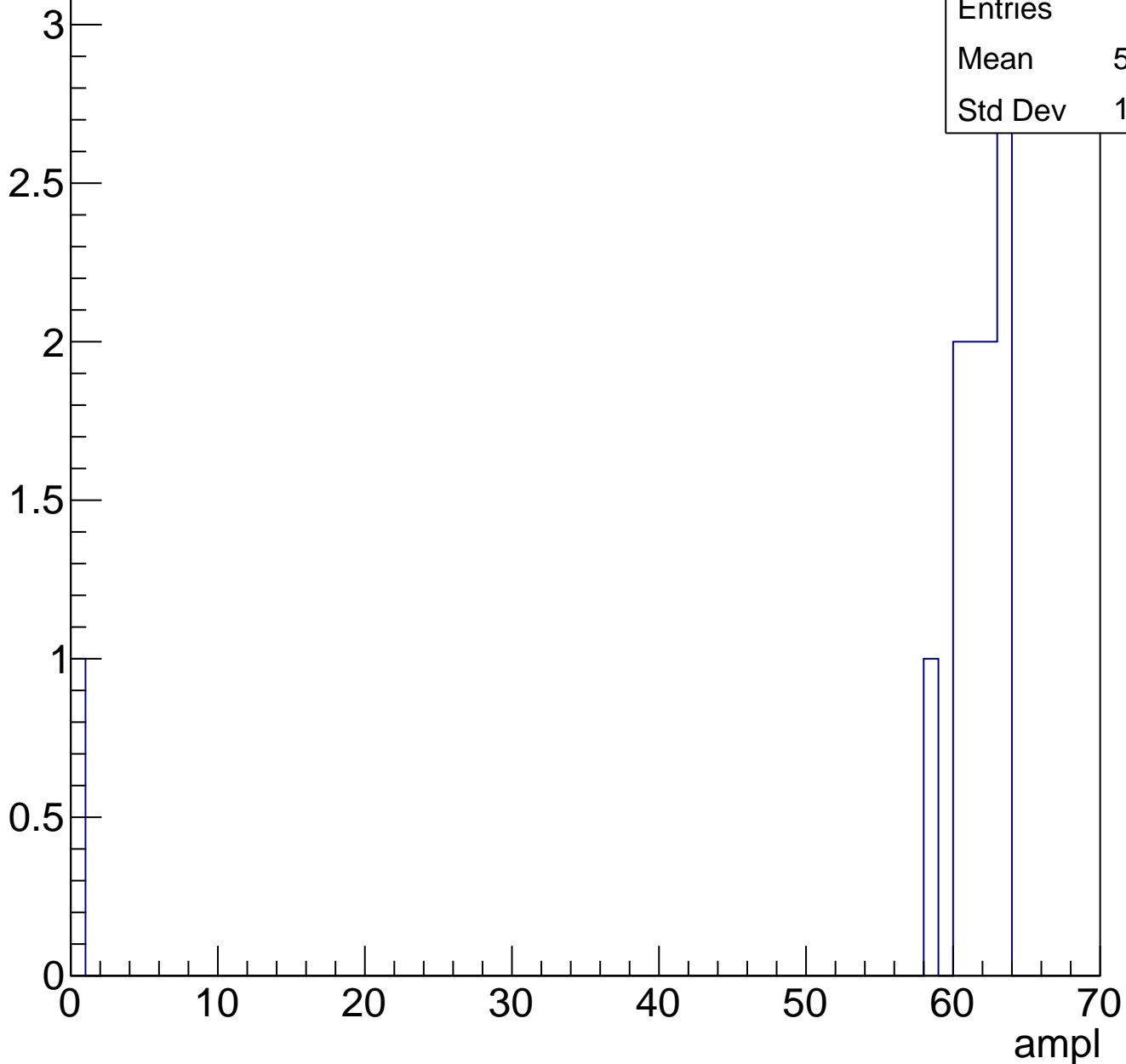
7

8

B1L100S, U15-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	11
Mean	55.73
Std Dev	17.68

B1L100S, U15-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch61, adc0

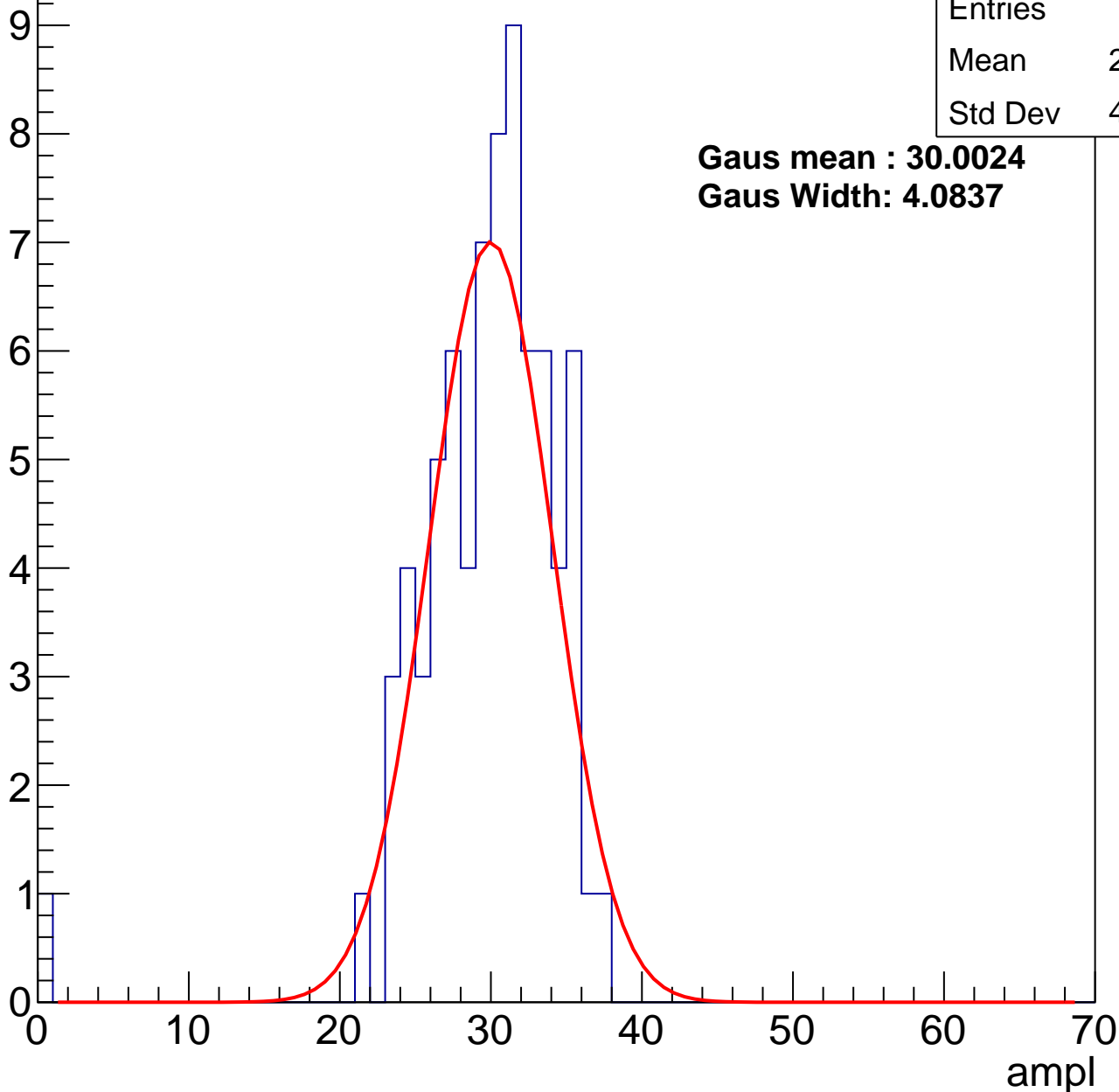
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.28
Std Dev	4.968

Gaus mean : 30.0024

Gaus Width: 4.0837



B1L100S, U15-ch61, adc1

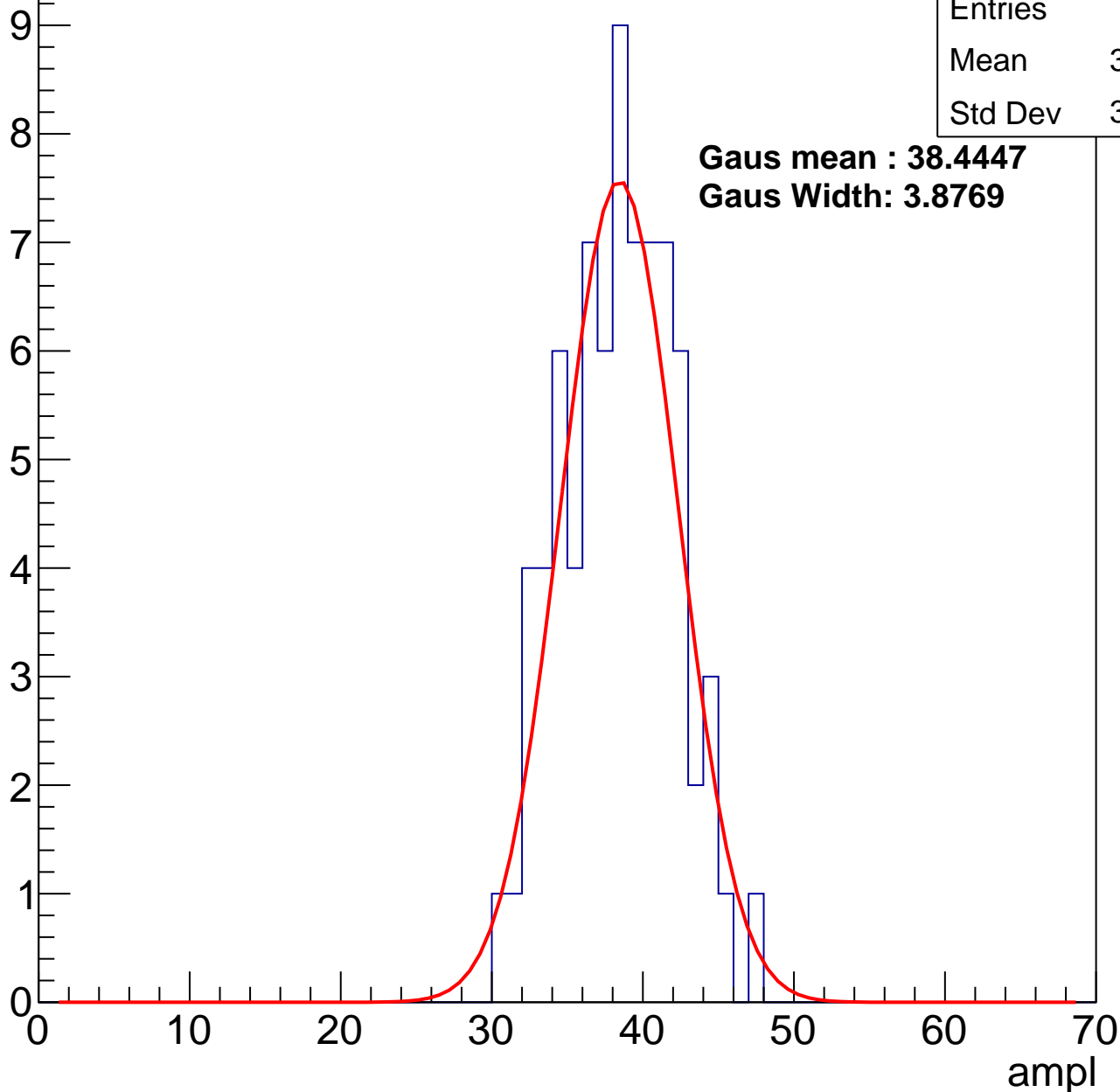
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	37.93
Std Dev	3.636

Gaus mean : 38.4447

Gaus Width: 3.8769



B1L100S, U15-ch61, adc2

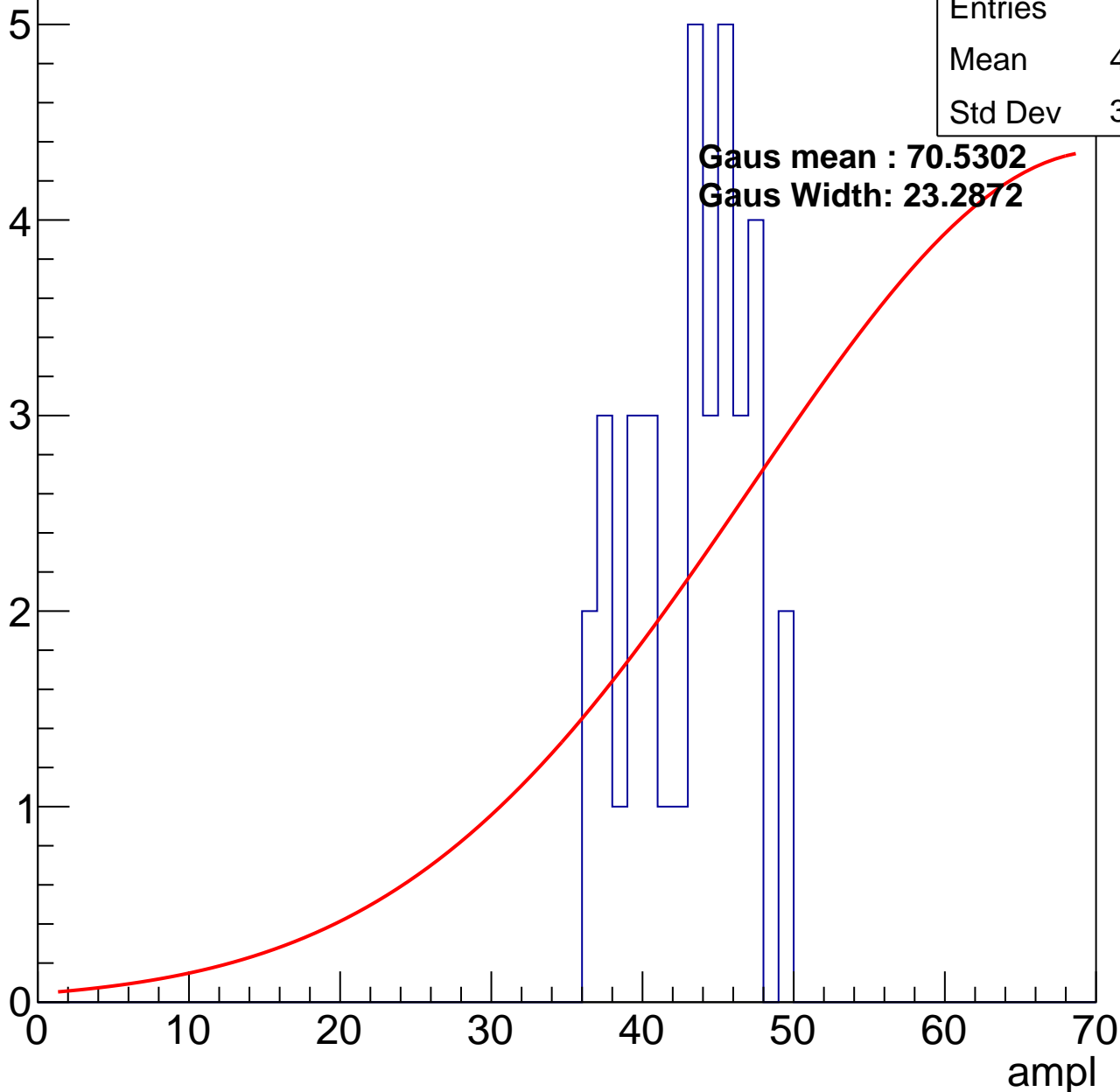
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	42.69
Std Dev	3.703

Gaus mean : 70.5302

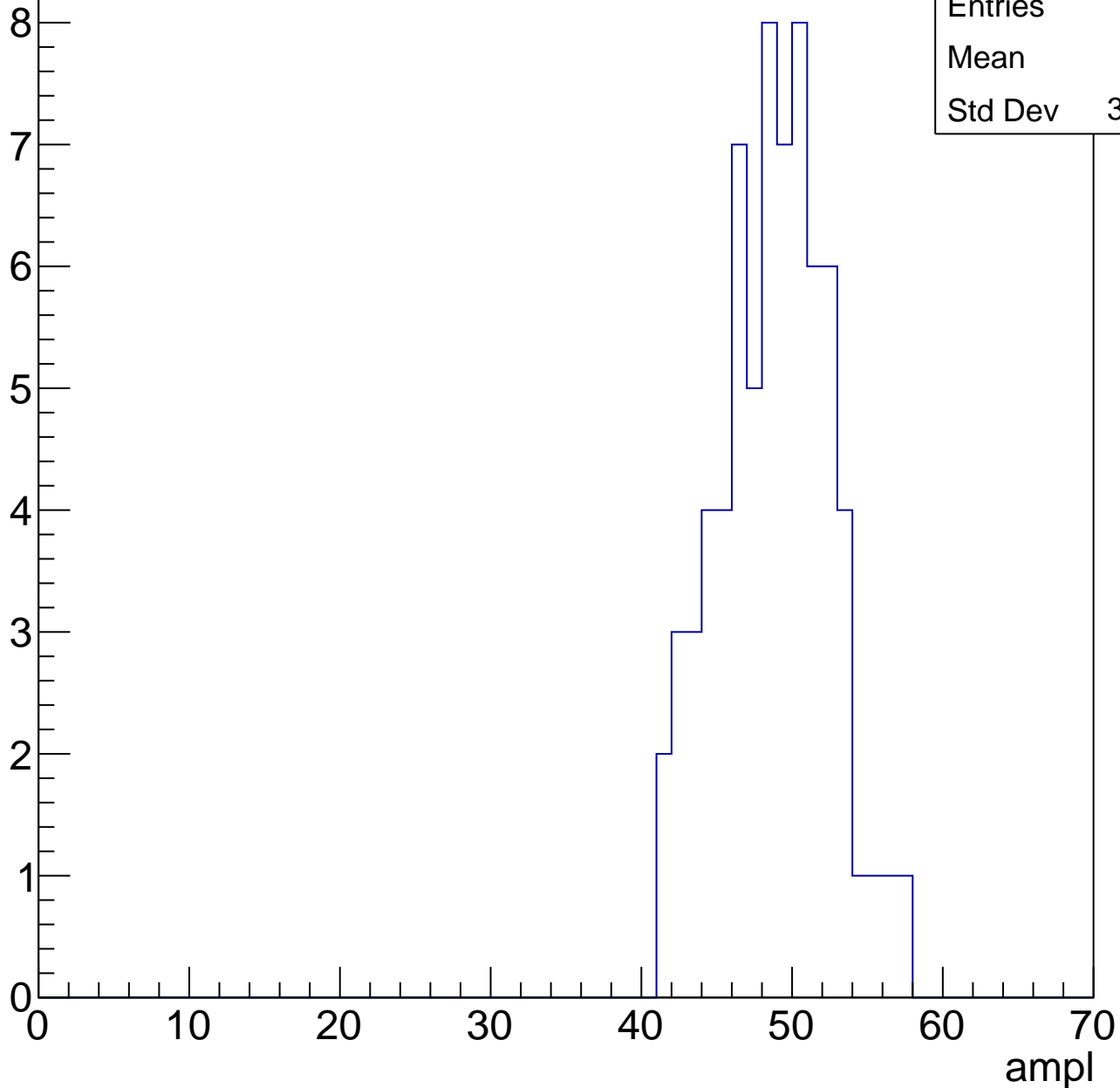
Gaus Width: 23.2872



B1L100S, U15-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

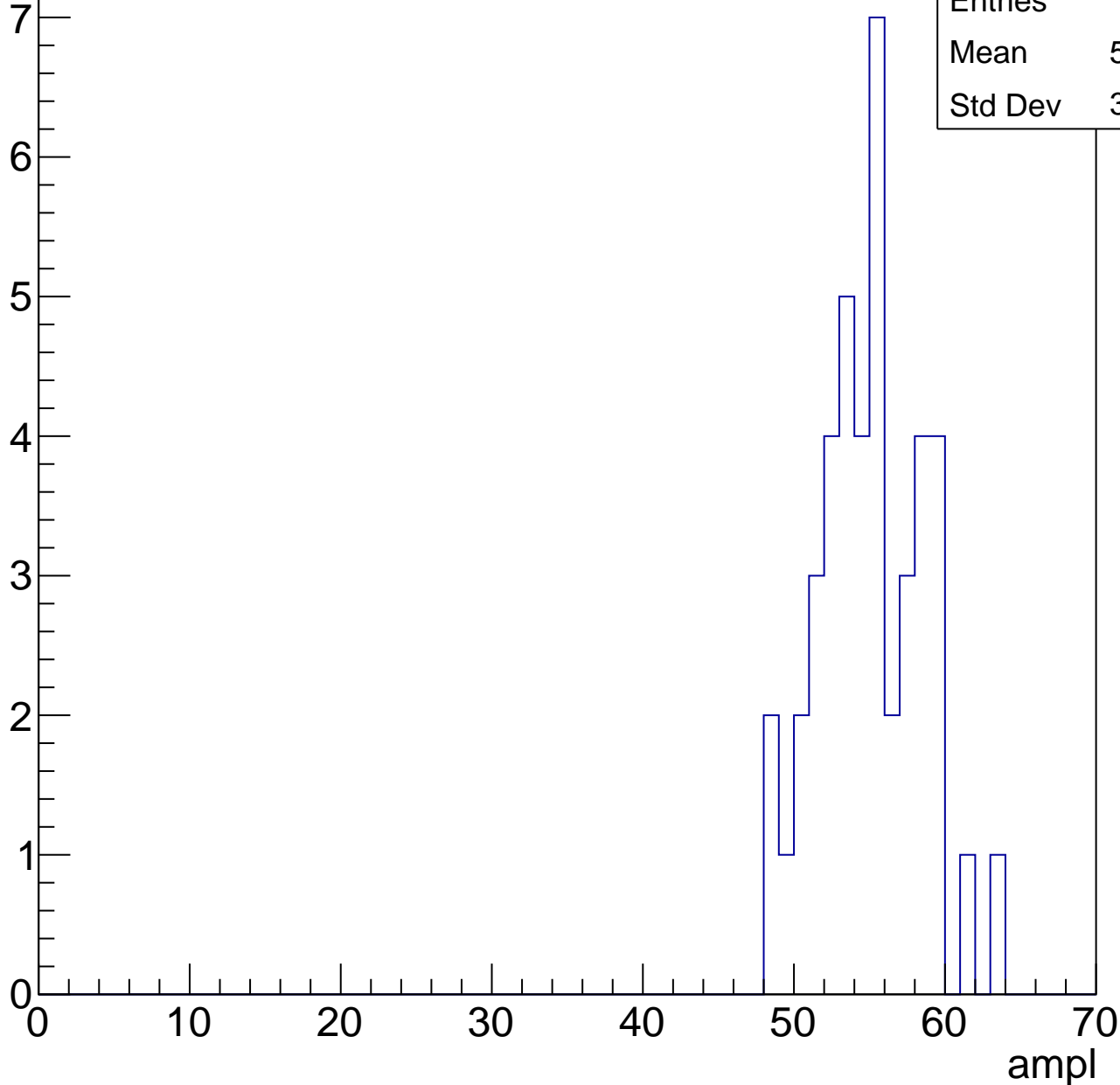


Entries	71
Mean	48.3
Std Dev	3.613

B1L100S, U15-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	43
Mean	54.58
Std Dev	3.405

B1L100S, U15-ch61, adc5

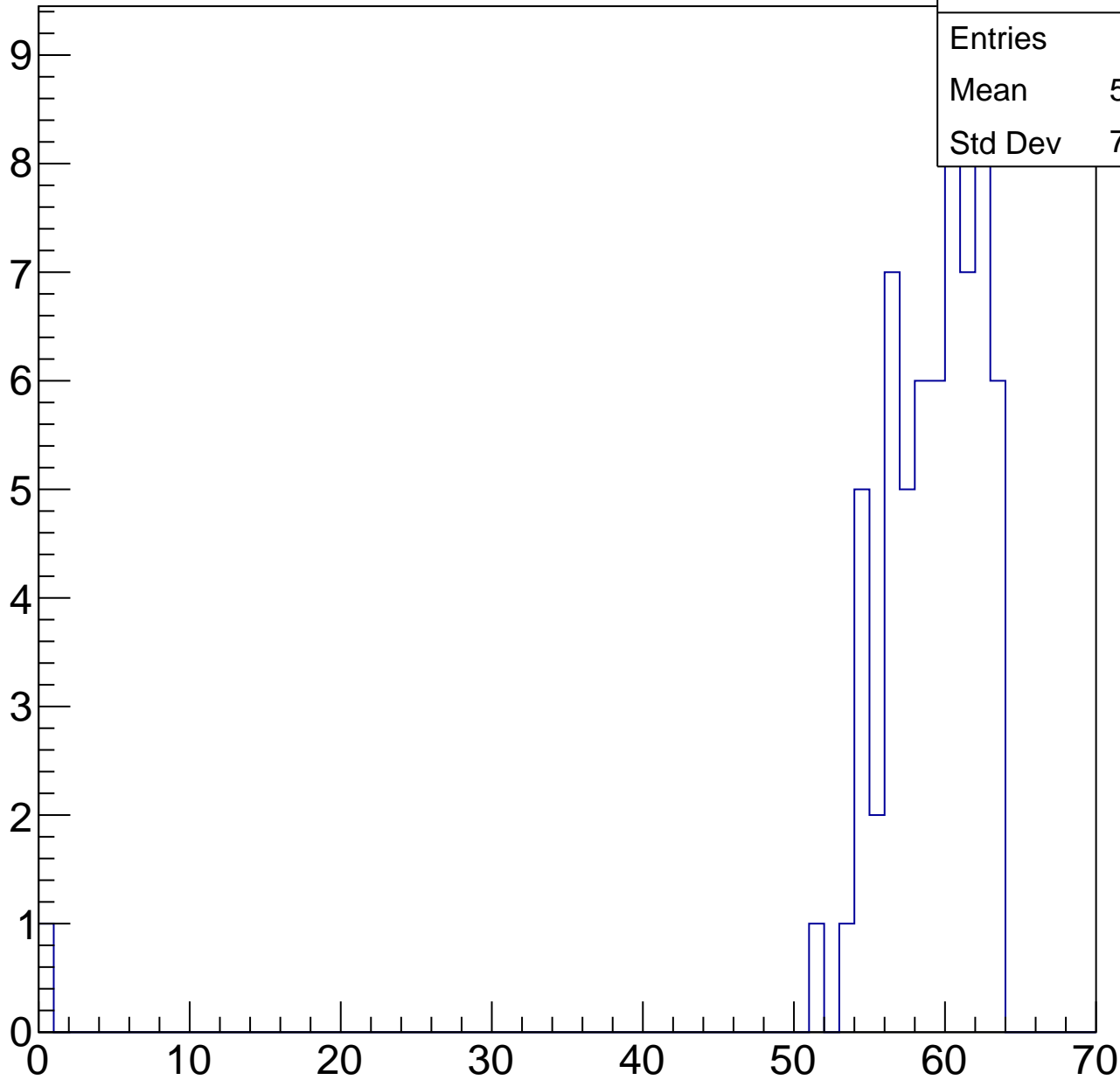
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	57.94
Std Dev	7.813

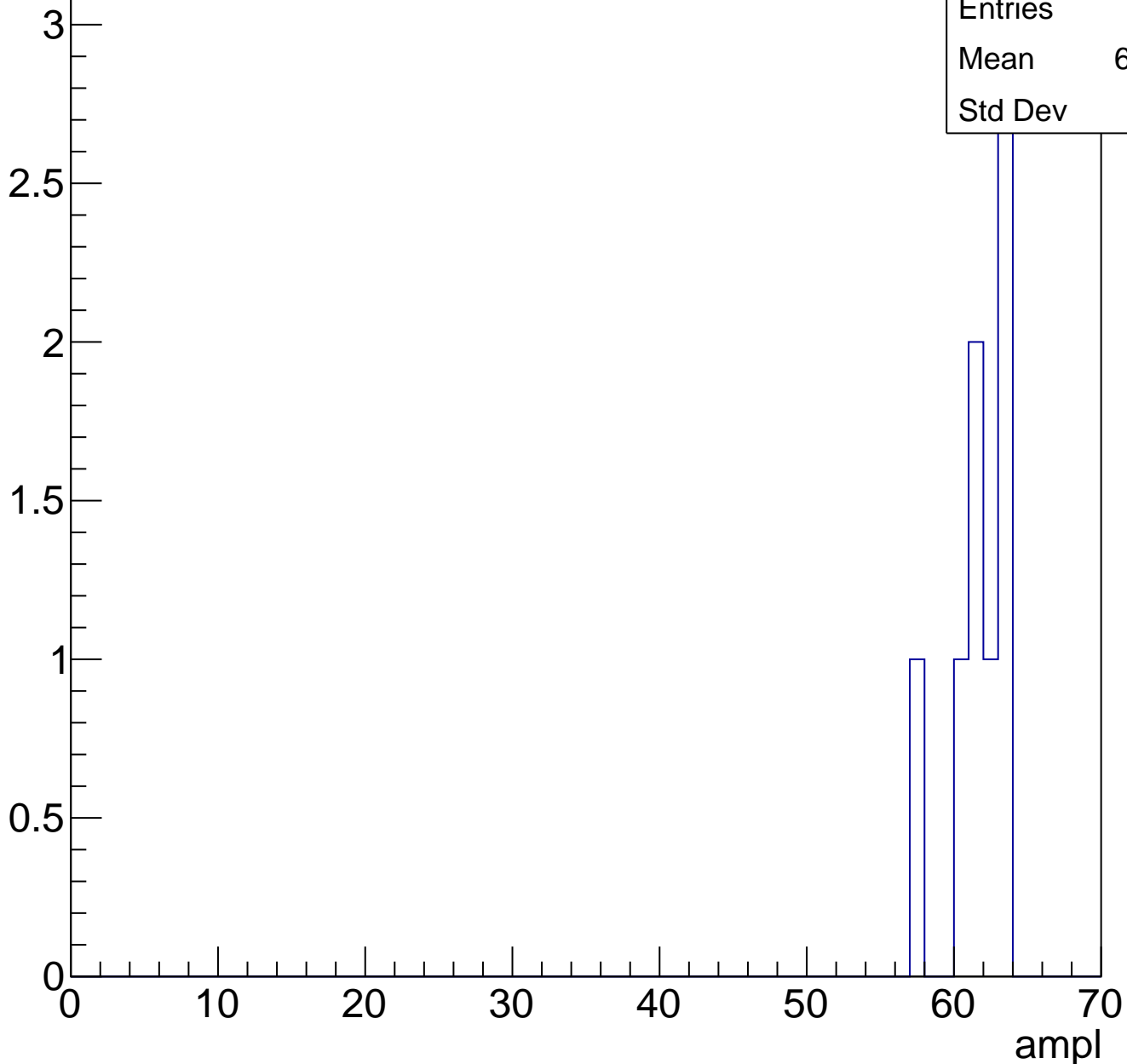
ampl



B1L100S, U15-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch62, adc0

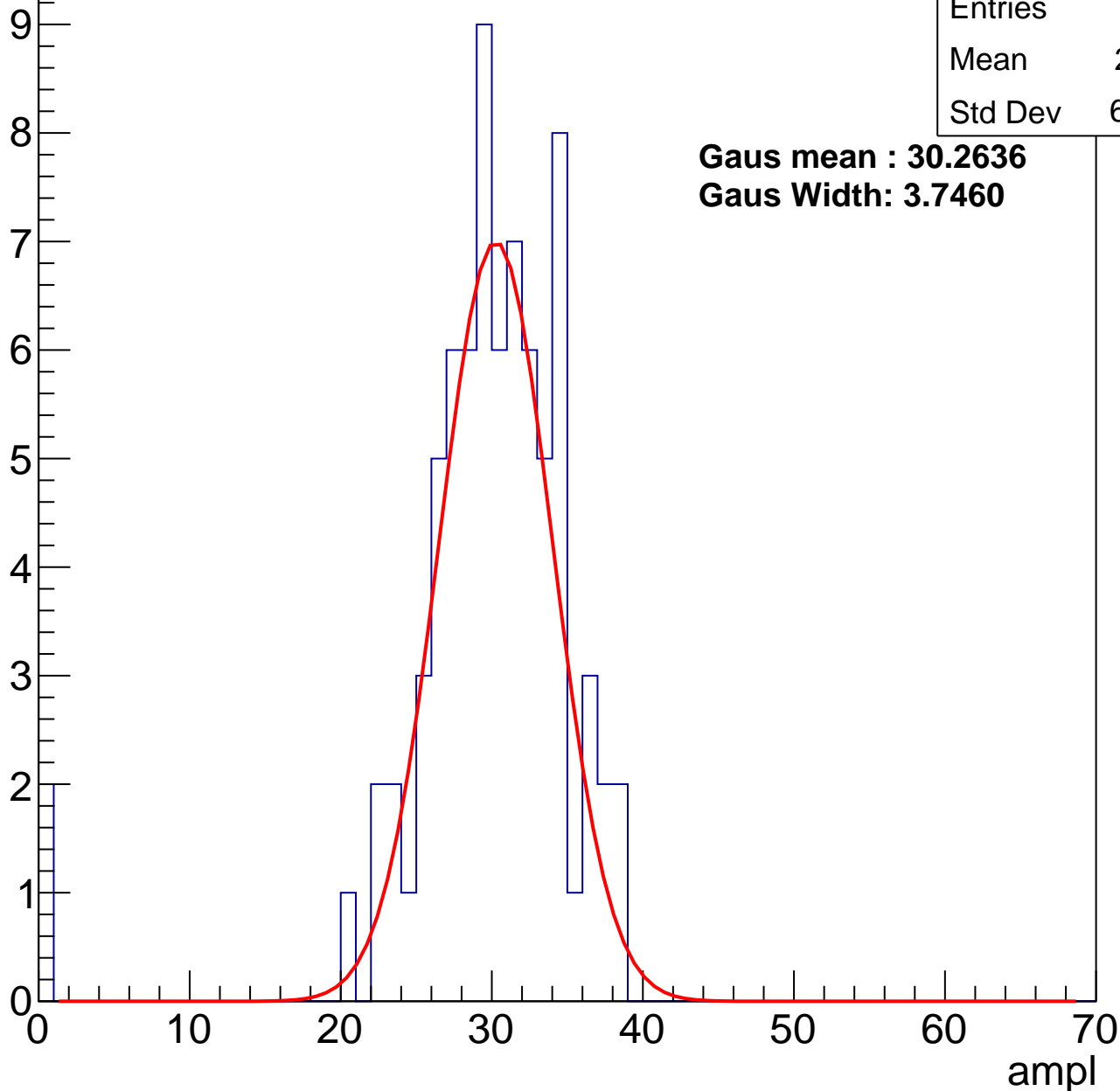
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.21
Std Dev	6.157

Gaus mean : 30.2636

Gaus Width: 3.7460



B1L100S, U15-ch62, adc1

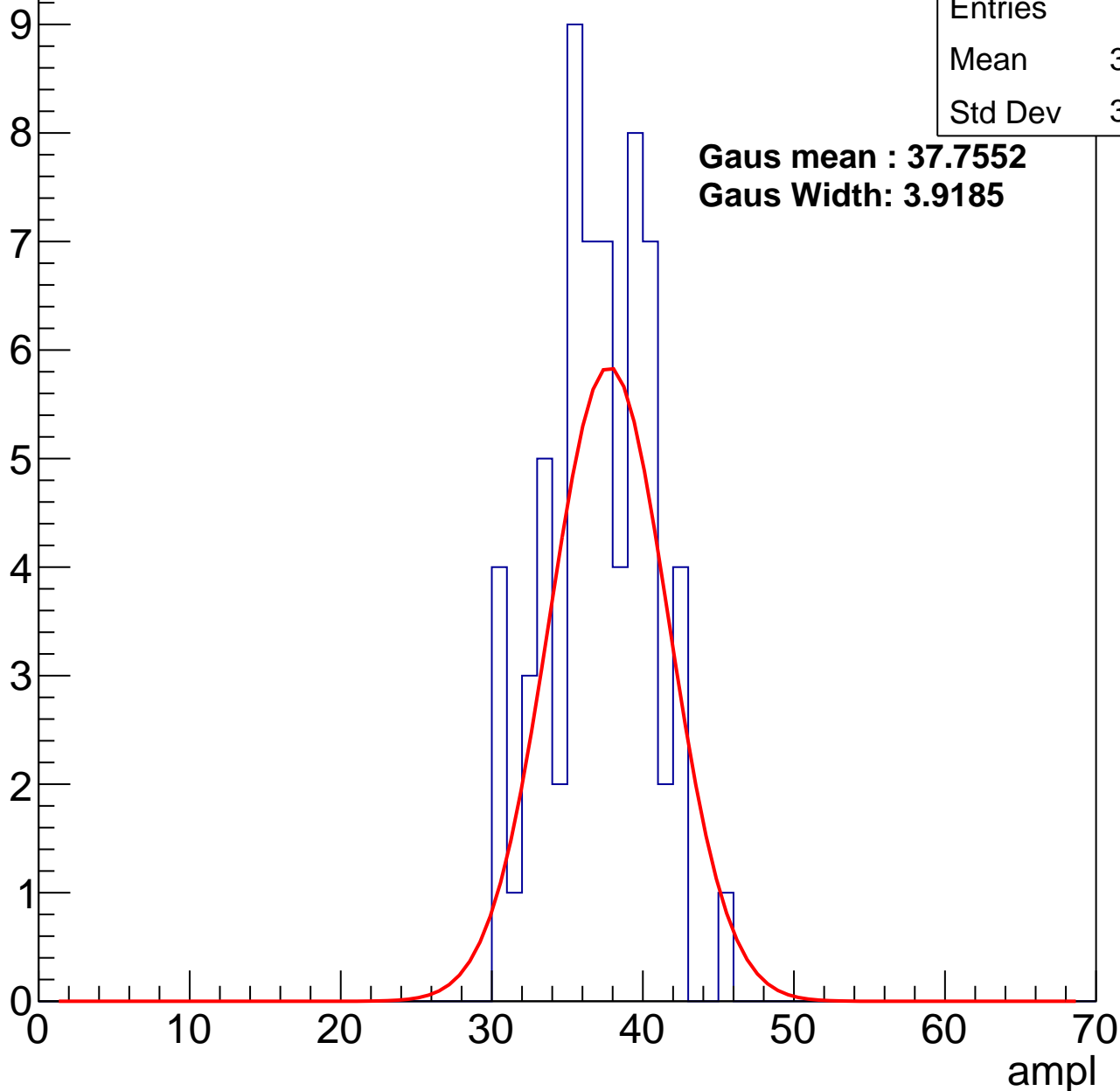
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.64
Std Dev	3.388

Gaus mean : 37.7552

Gaus Width: 3.9185



B1L100S, U15-ch62, adc2

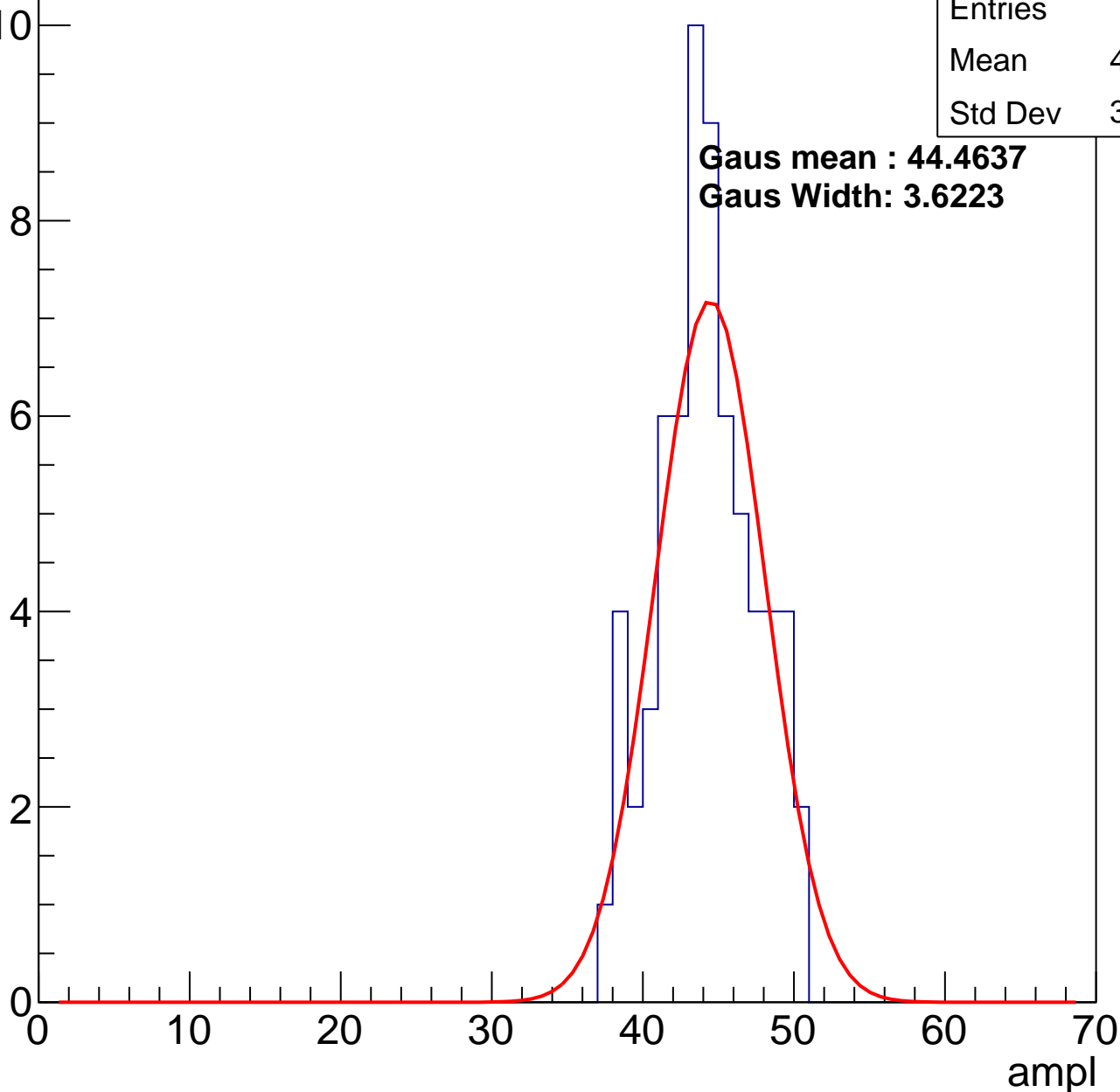
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.74
Std Dev	3.192

Gaus mean : 44.4637

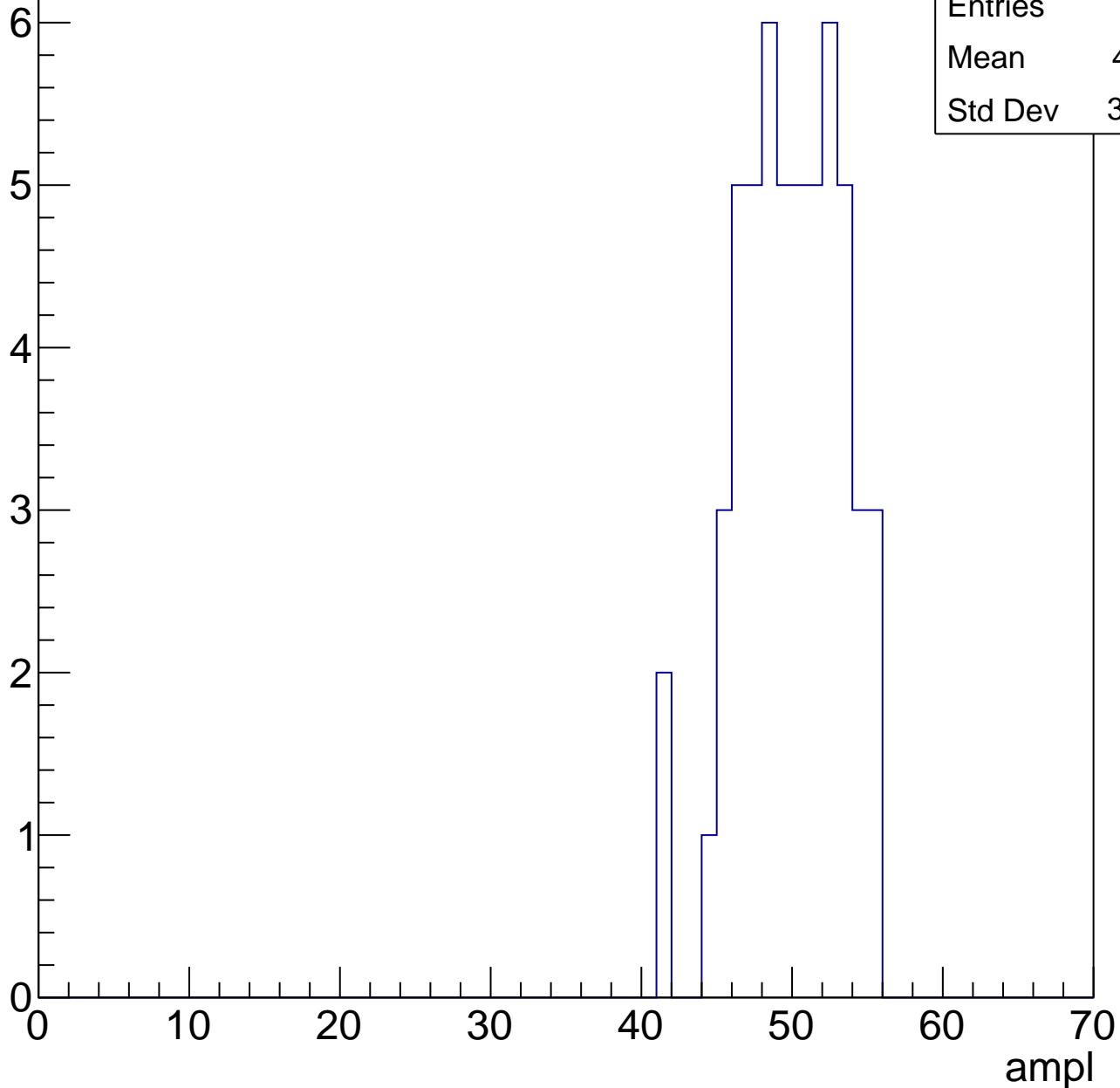
Gaus Width: 3.6223



B1L100S, U15-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



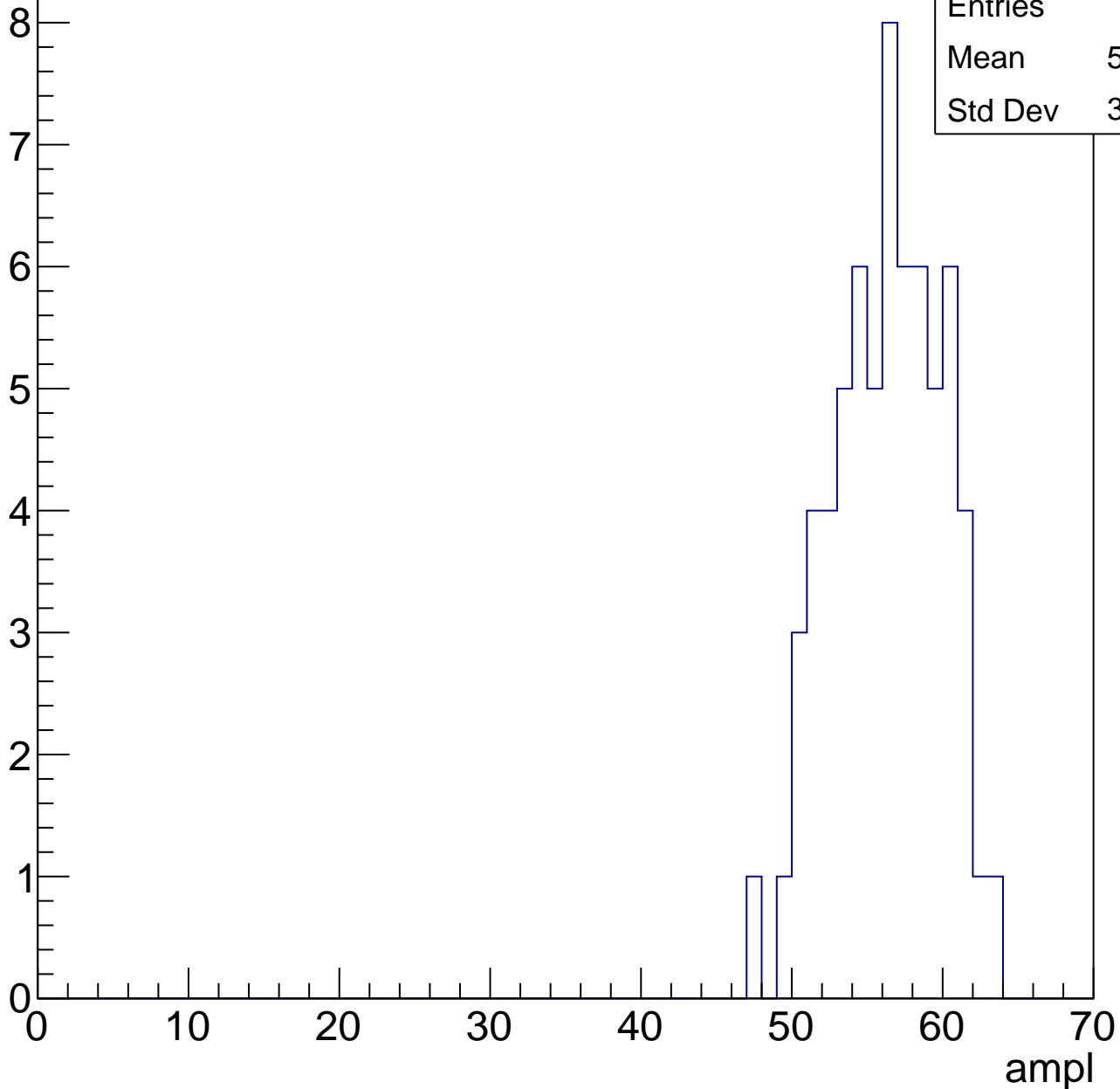
Entries	54
Mean	49.41
Std Dev	3.347

B1L100S, U15-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	55.82
Std Dev	3.546

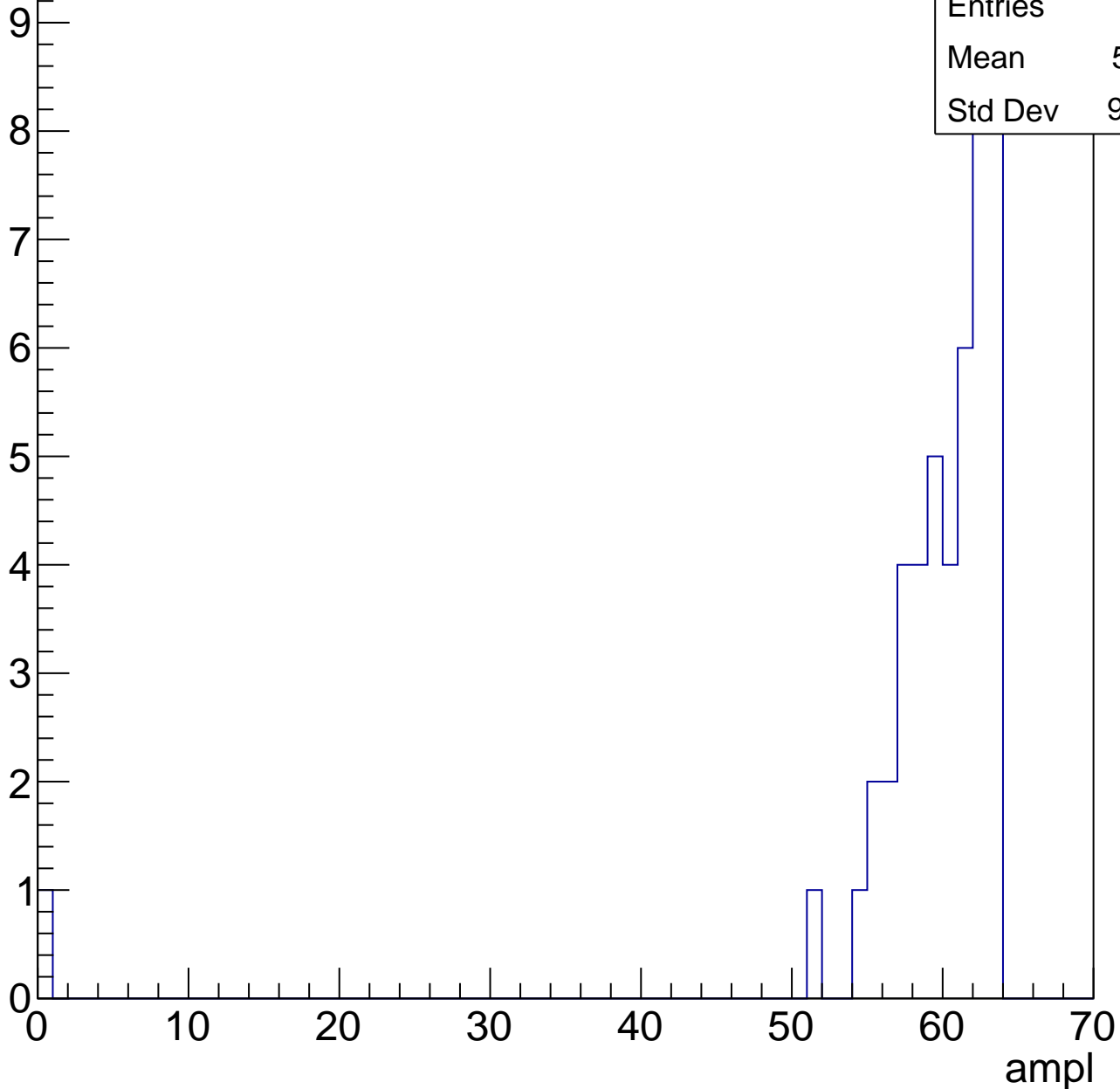


B1L100S, U15-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.51
Std Dev	9.069



B1L100S, U15-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U15-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch63, adc0

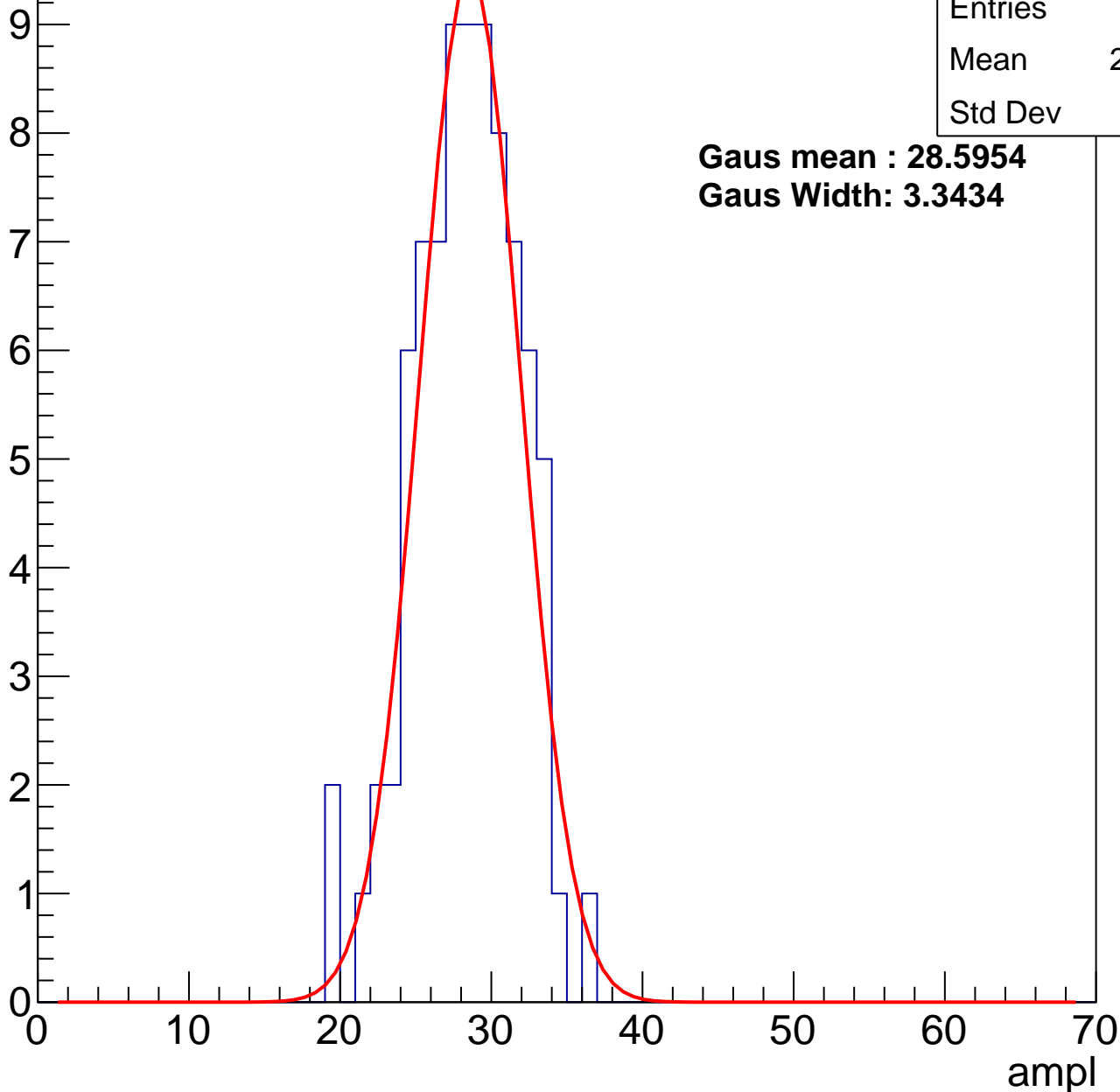
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	27.93
Std Dev	3.41

Gaus mean : 28.5954

Gaus Width: 3.3434



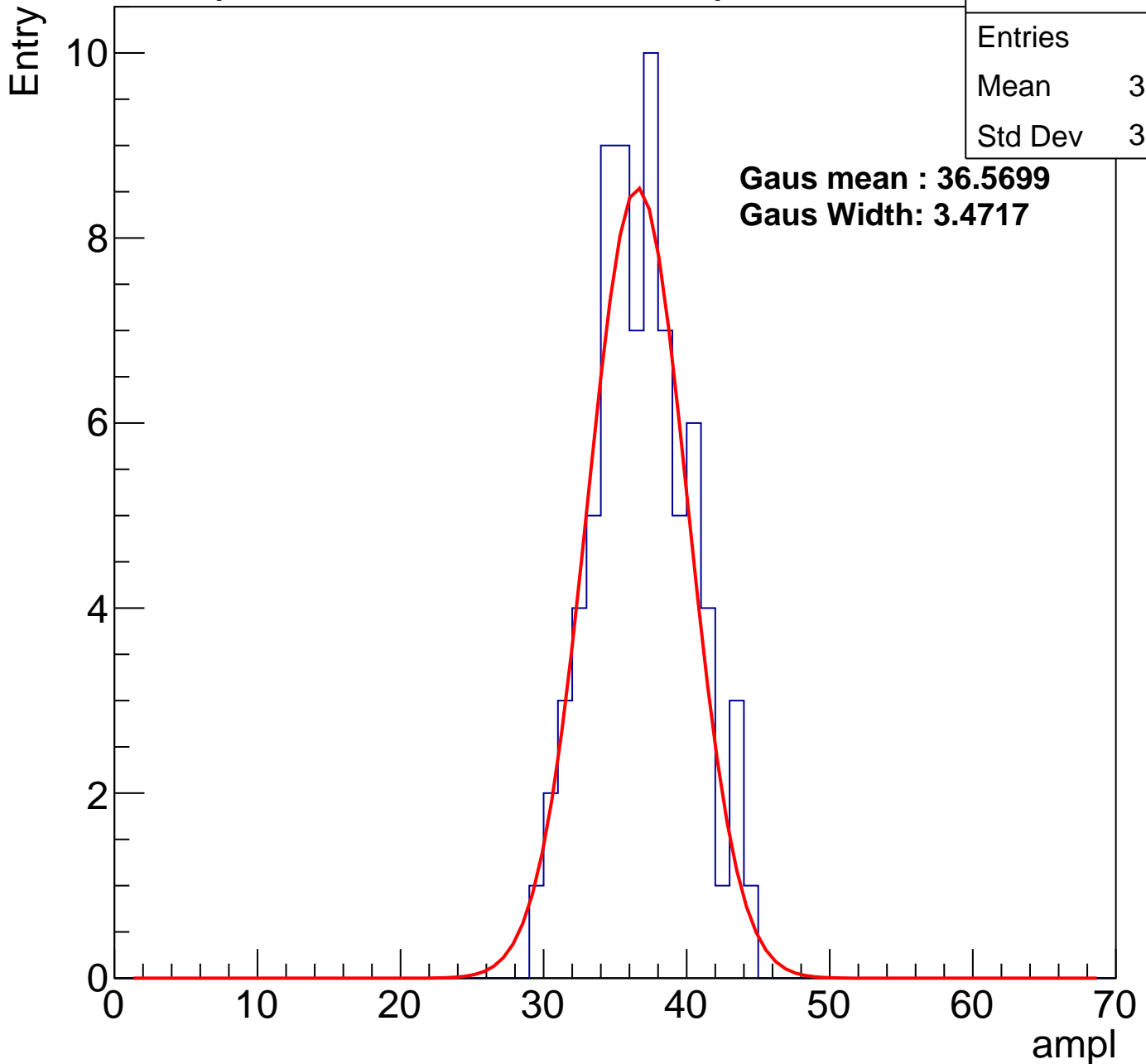
B1L100S, U15-ch63, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	36.34
Std Dev	3.356

Gaus mean : 36.5699

Gaus Width: 3.4717



B1L100S, U15-ch63, adc2

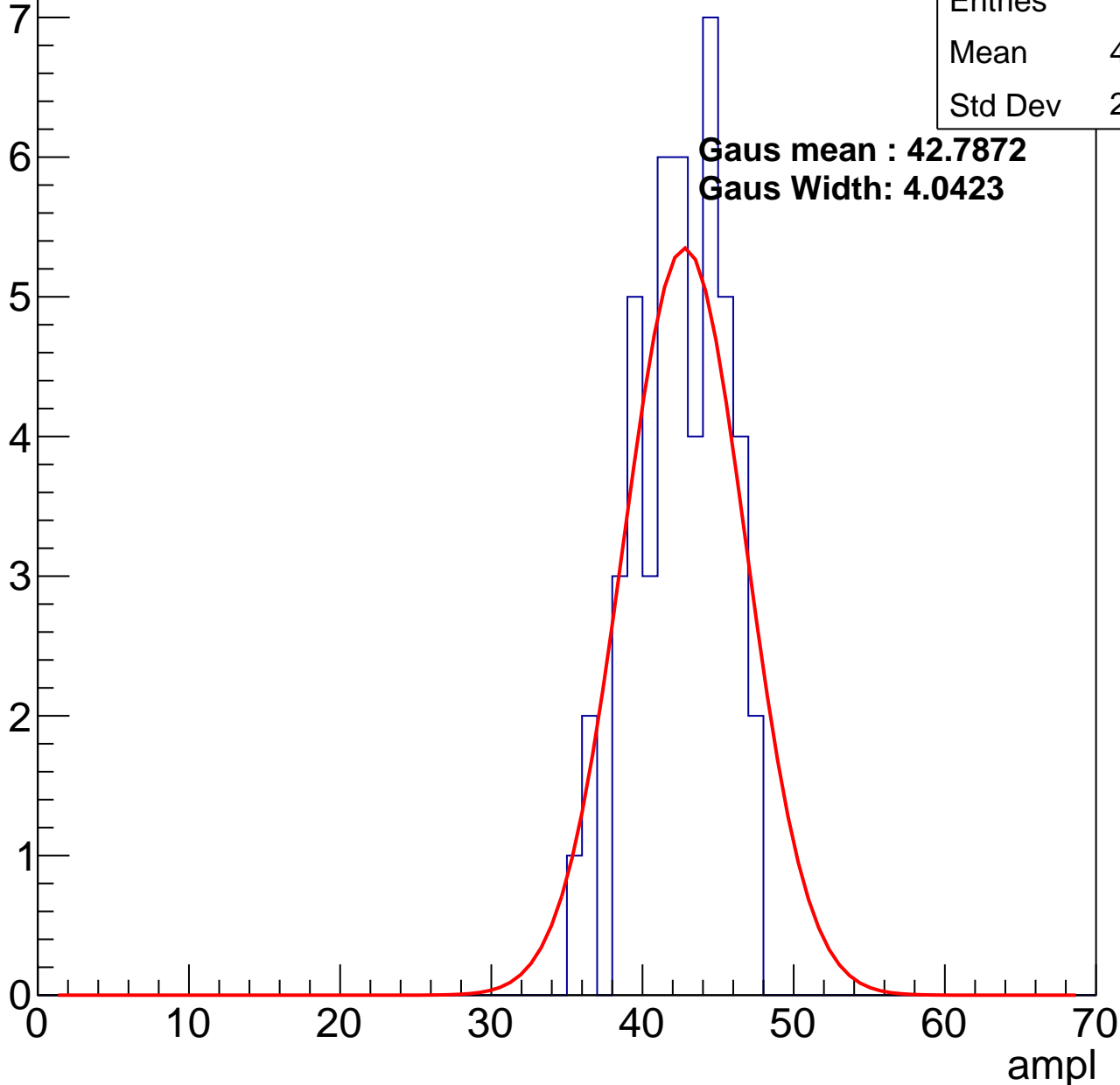
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	42.02
Std Dev	2.969

Gaus mean : 42.7872

Gaus Width: 4.0423



B1L100S, U15-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

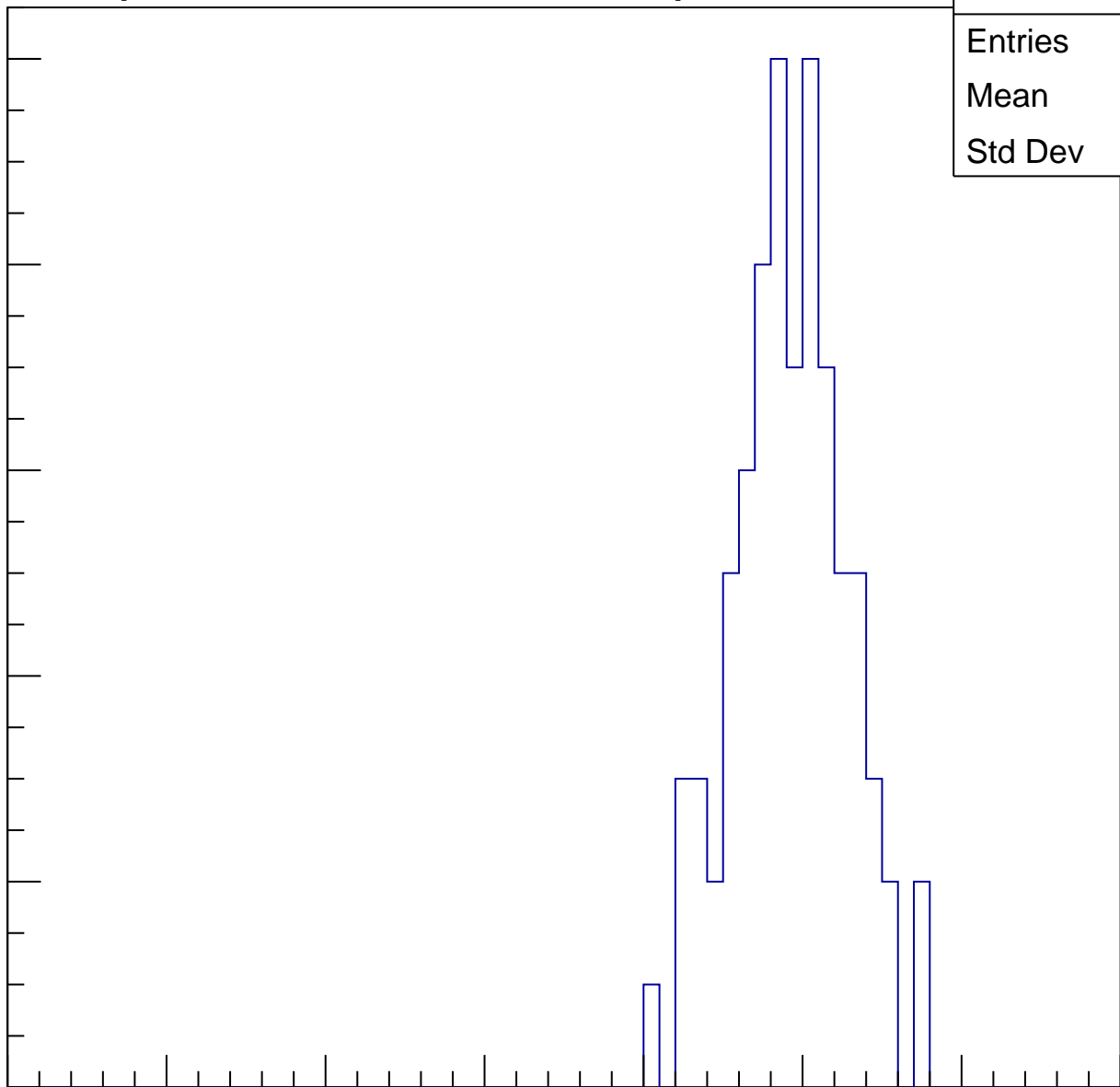
Entries	79
Mean	48.75
Std Dev	3.549

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

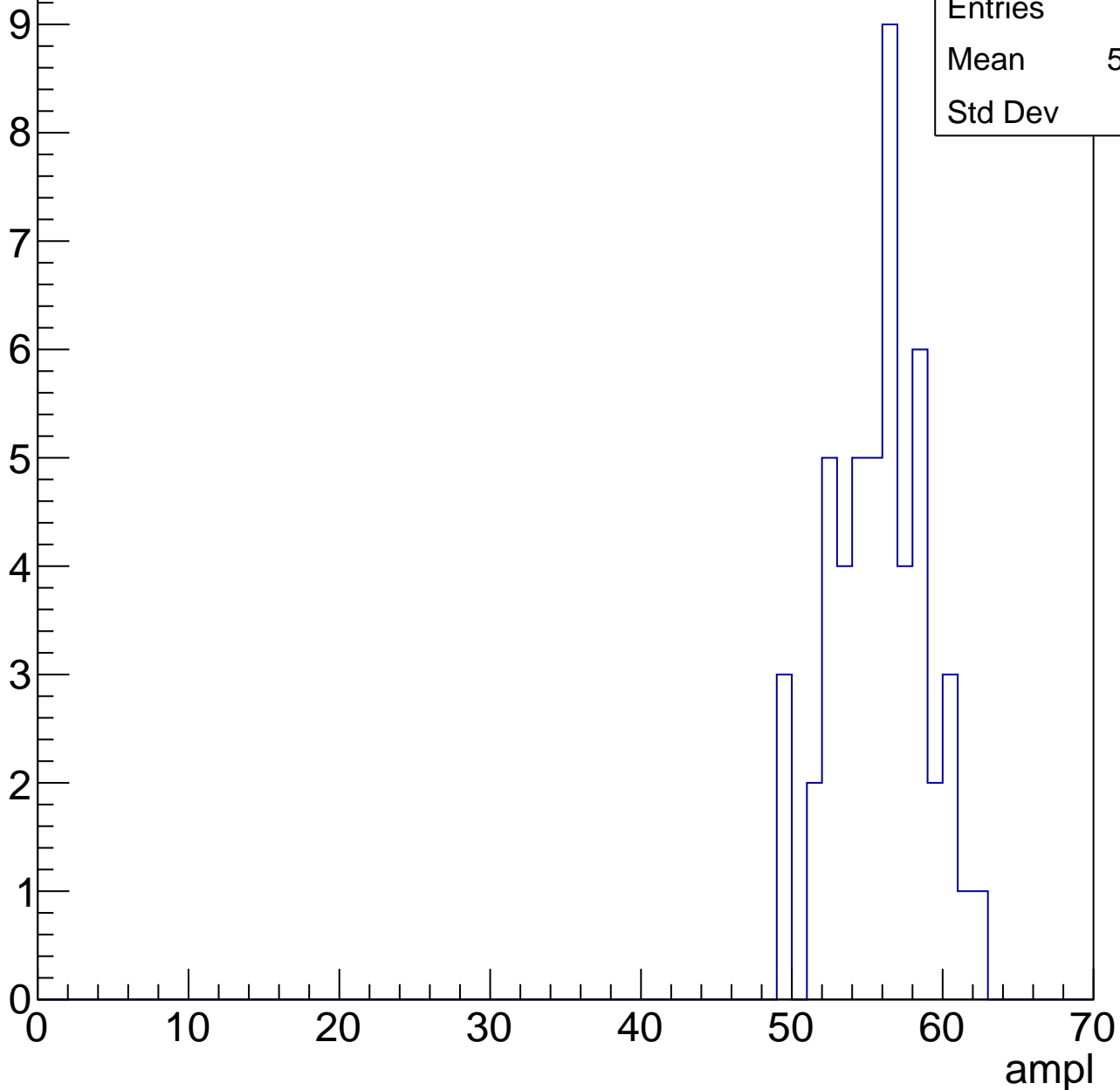


B1L100S, U15-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	55.34
Std Dev	3.07



B1L100S, U15-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

45

Mean

59

Std Dev

2.875

ampl

0

10

20

30

40

50

60

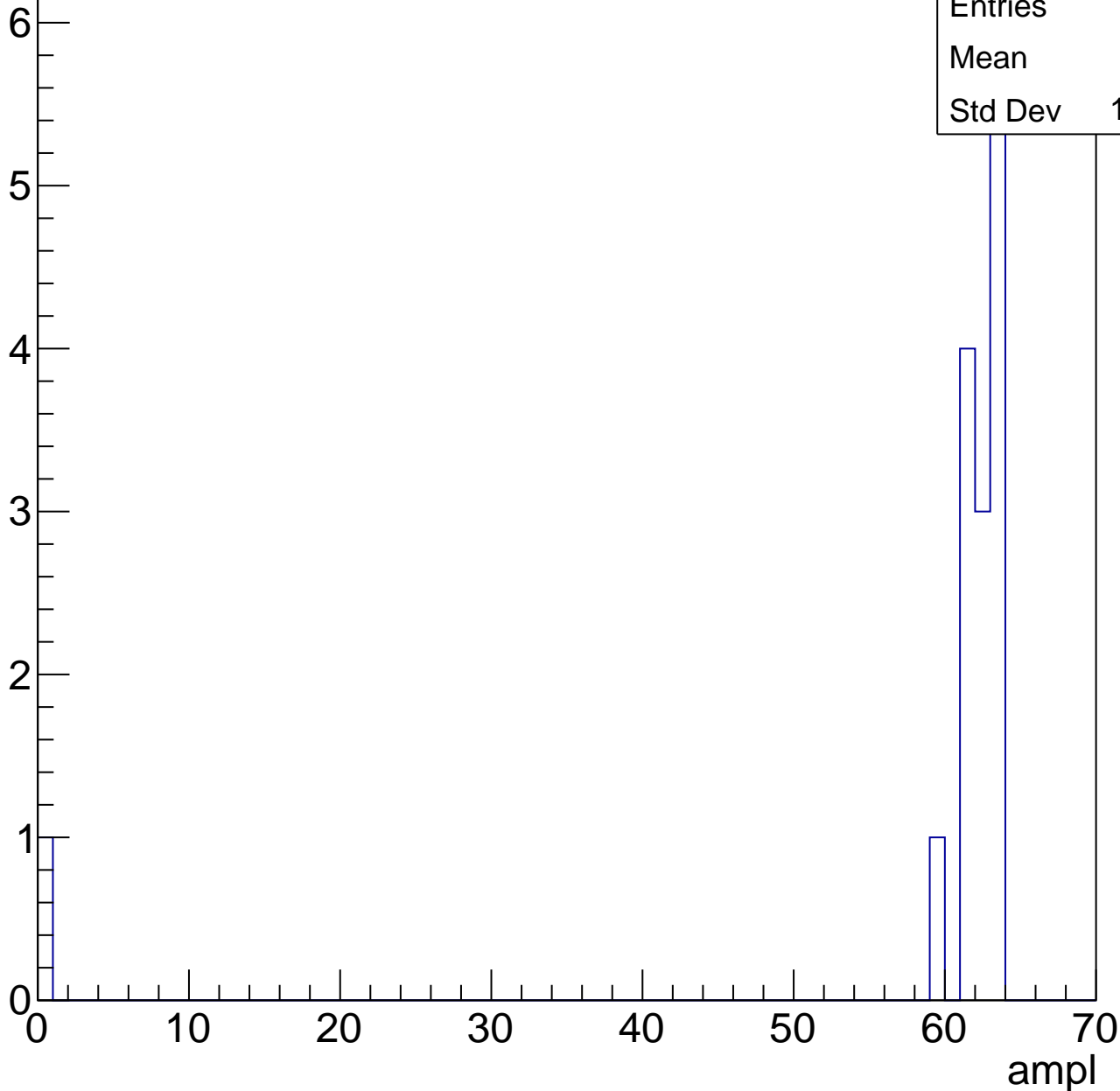
70

B1L100S, U15-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	57.8
Std Dev	15.49



B1L100S, U15-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch64, adc0

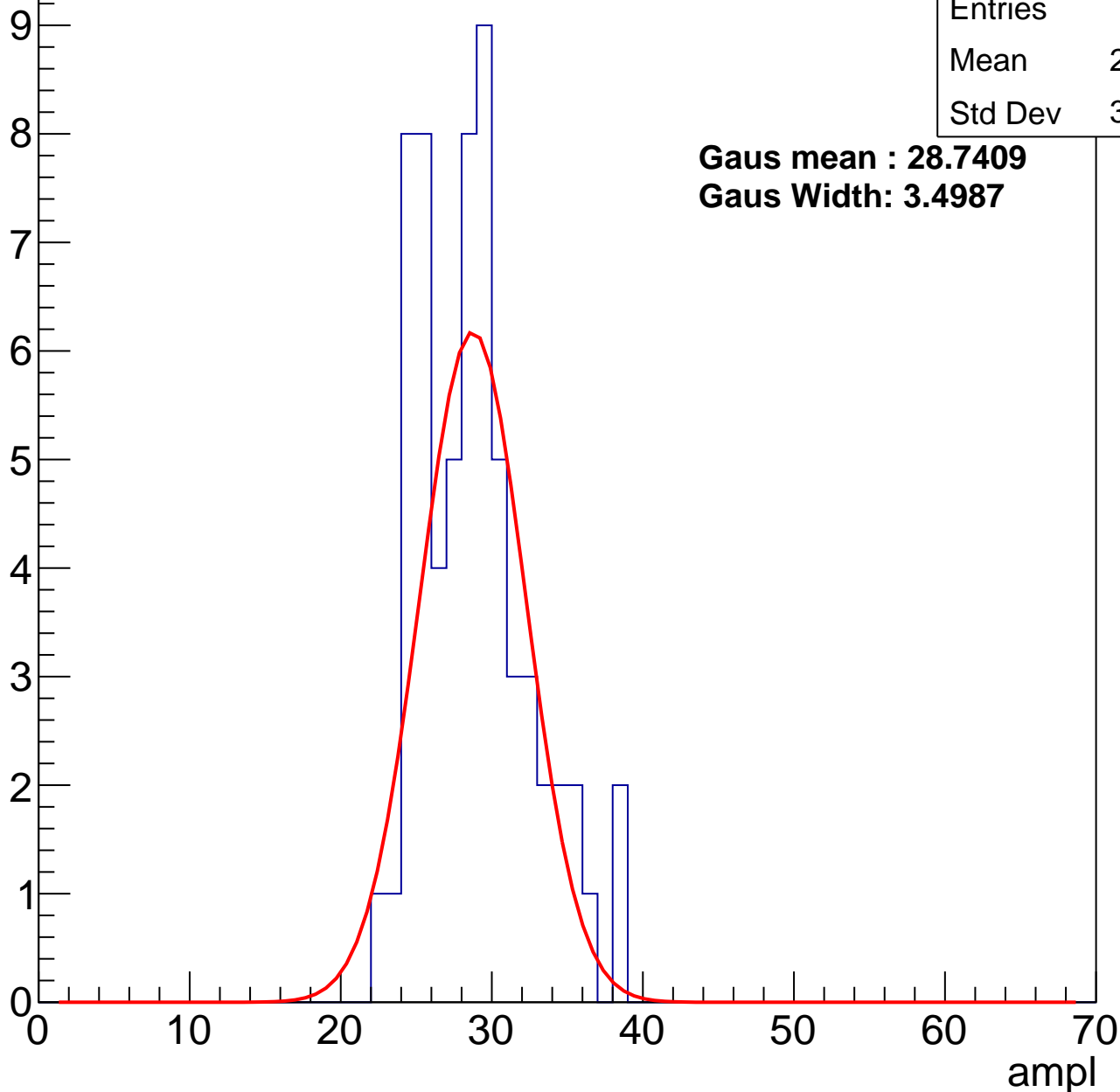
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	28.38
Std Dev	3.668

Gaus mean : 28.7409

Gaus Width: 3.4987



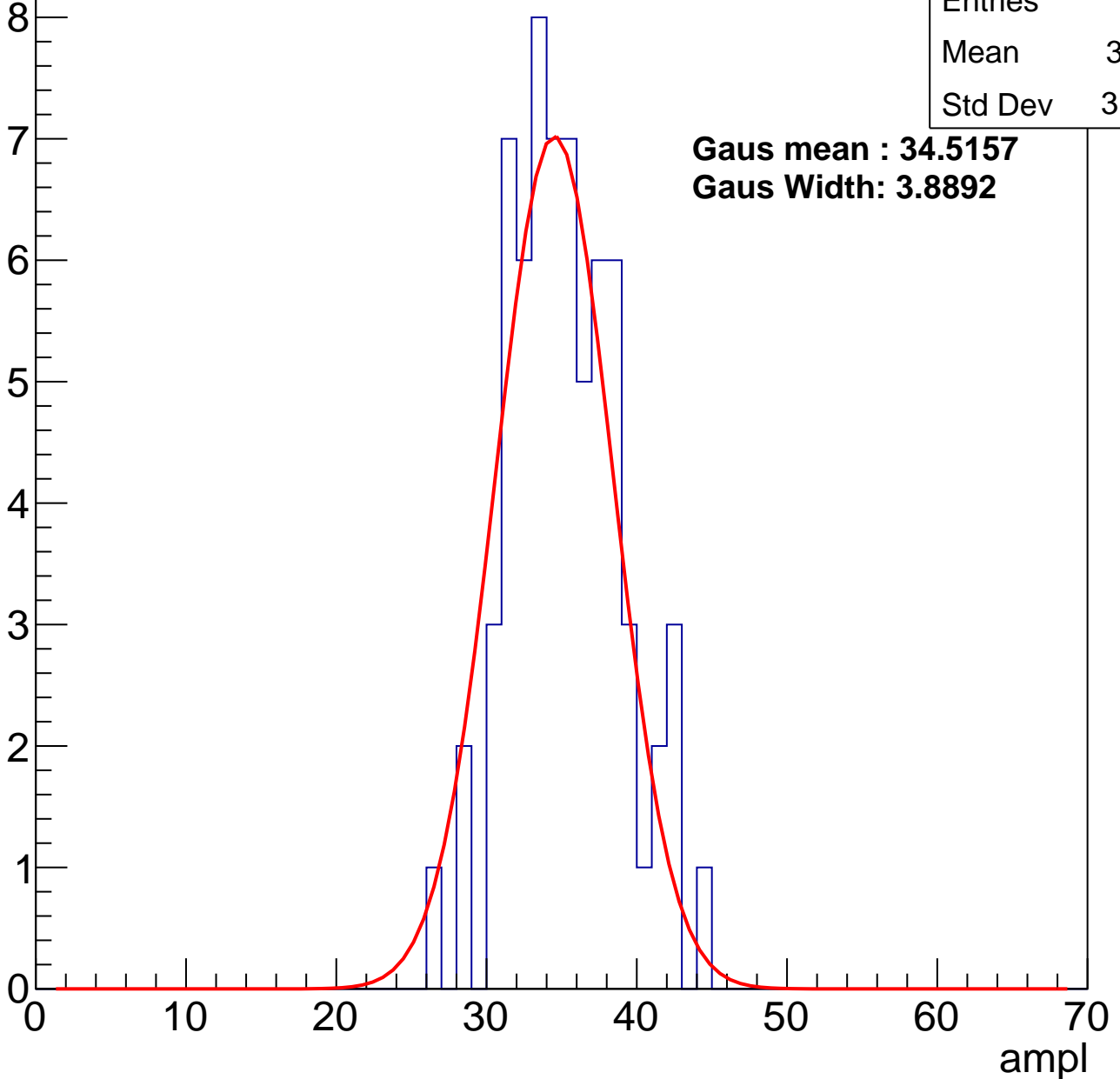
B1L100S, U15-ch64, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	34.81
Std Dev	3.663

Gaus mean : 34.5157
Gaus Width: 3.8892



B1L100S, U15-ch64, adc2

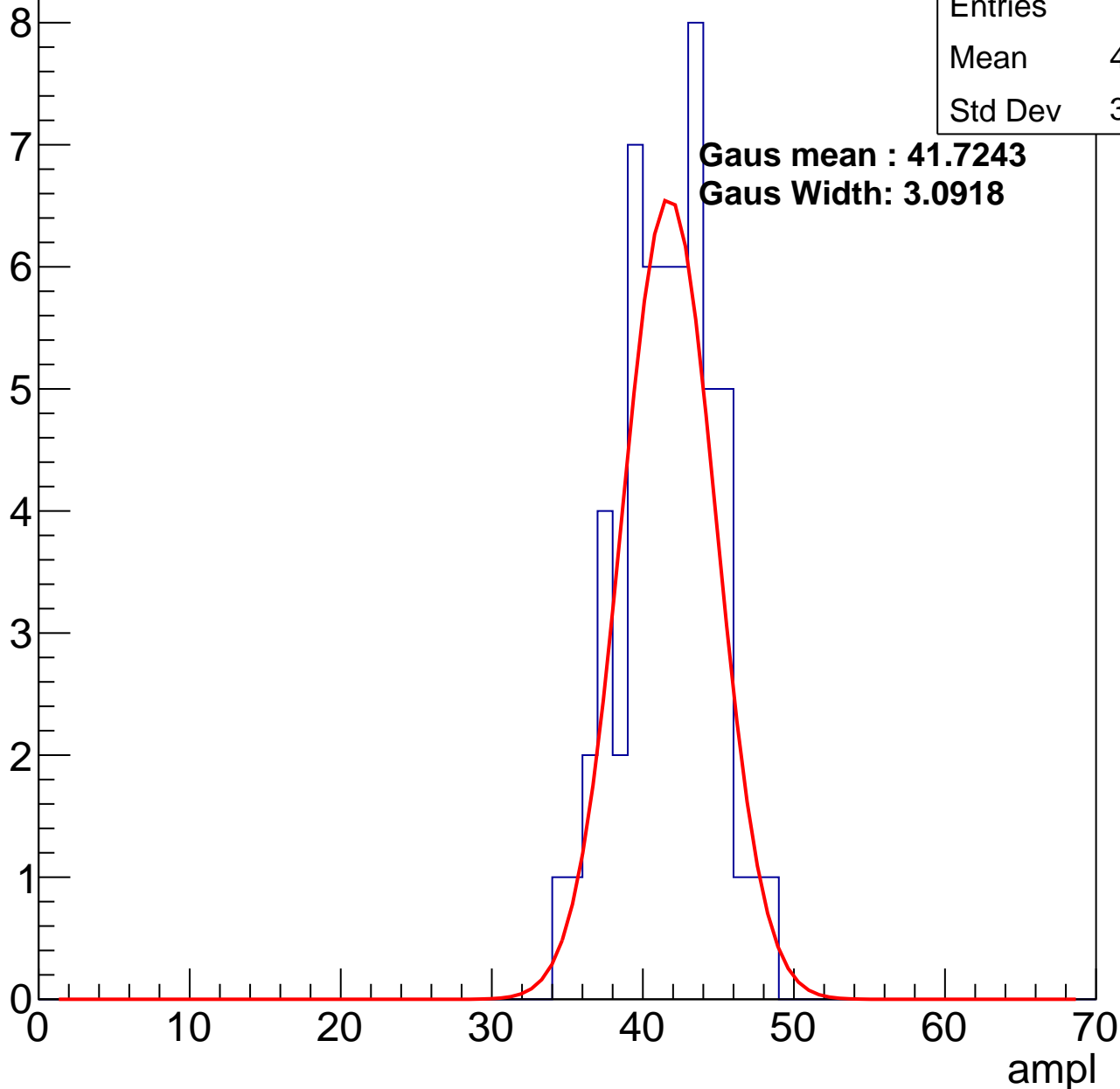
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	41.18
Std Dev	3.054

Gaus mean : 41.7243

Gaus Width: 3.0918

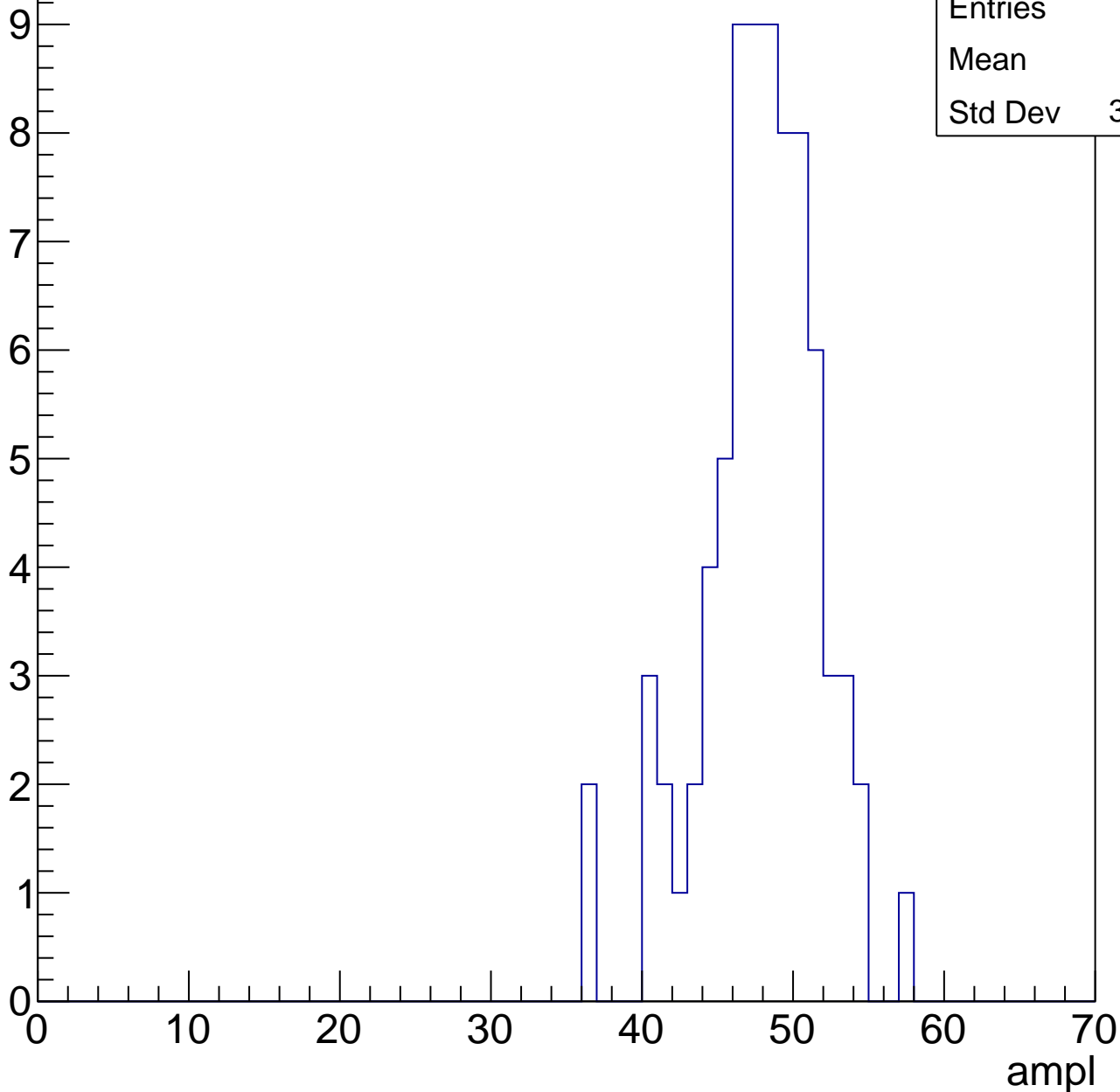


B1L100S, U15-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

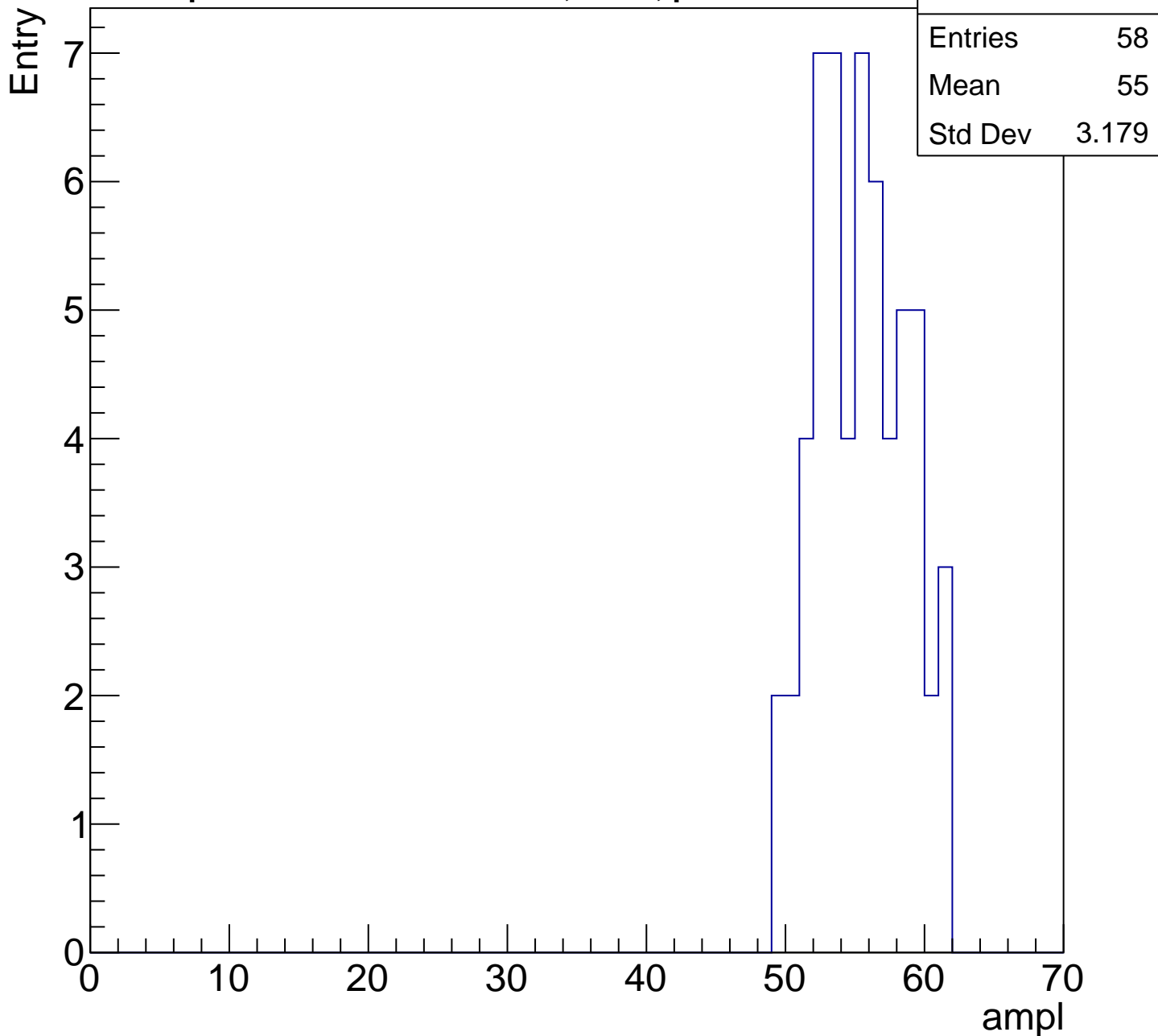
Entry

Entries	77
Mean	47.4
Std Dev	3.882



B1L100S, U15-ch64, adc4

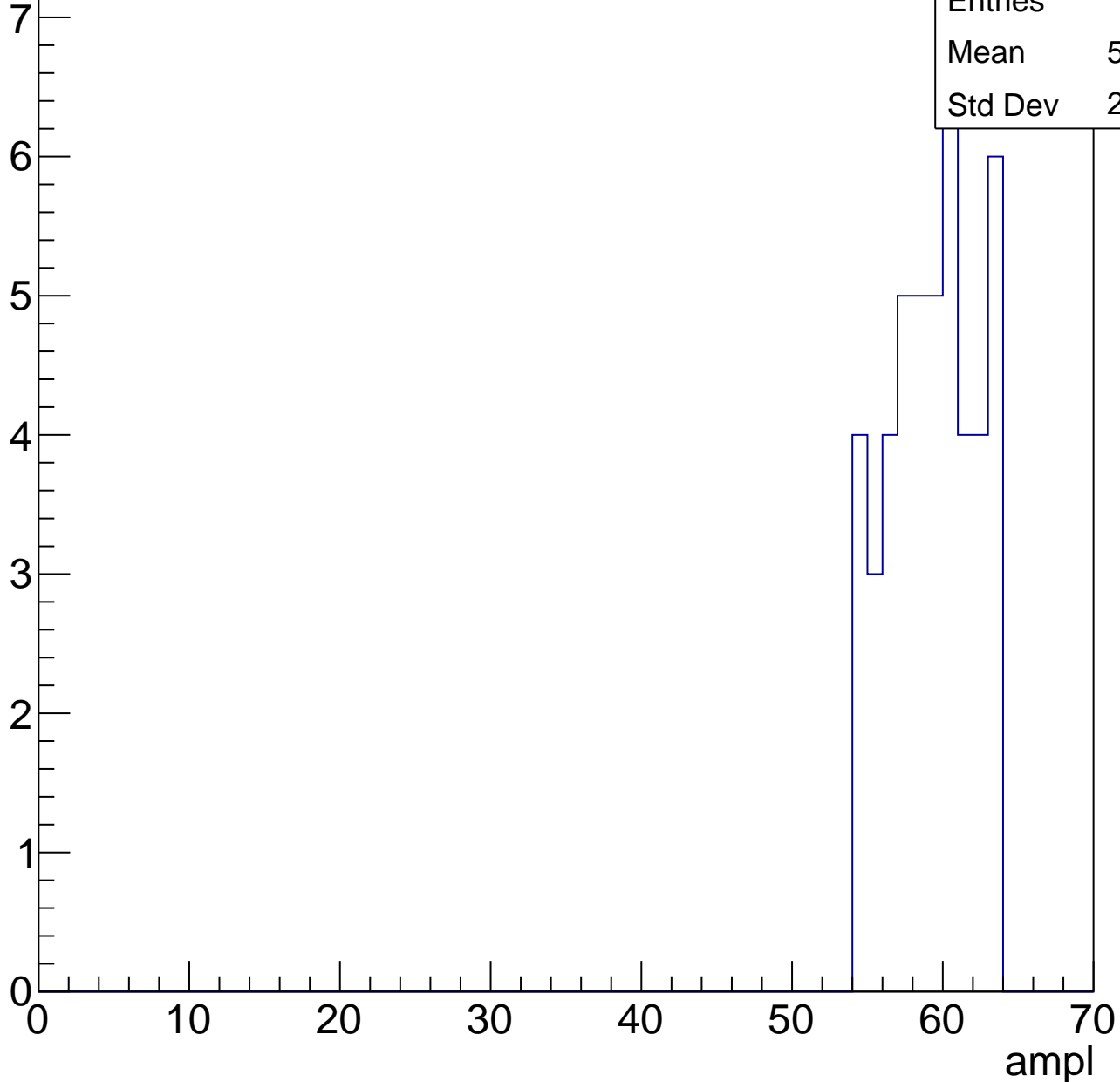
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



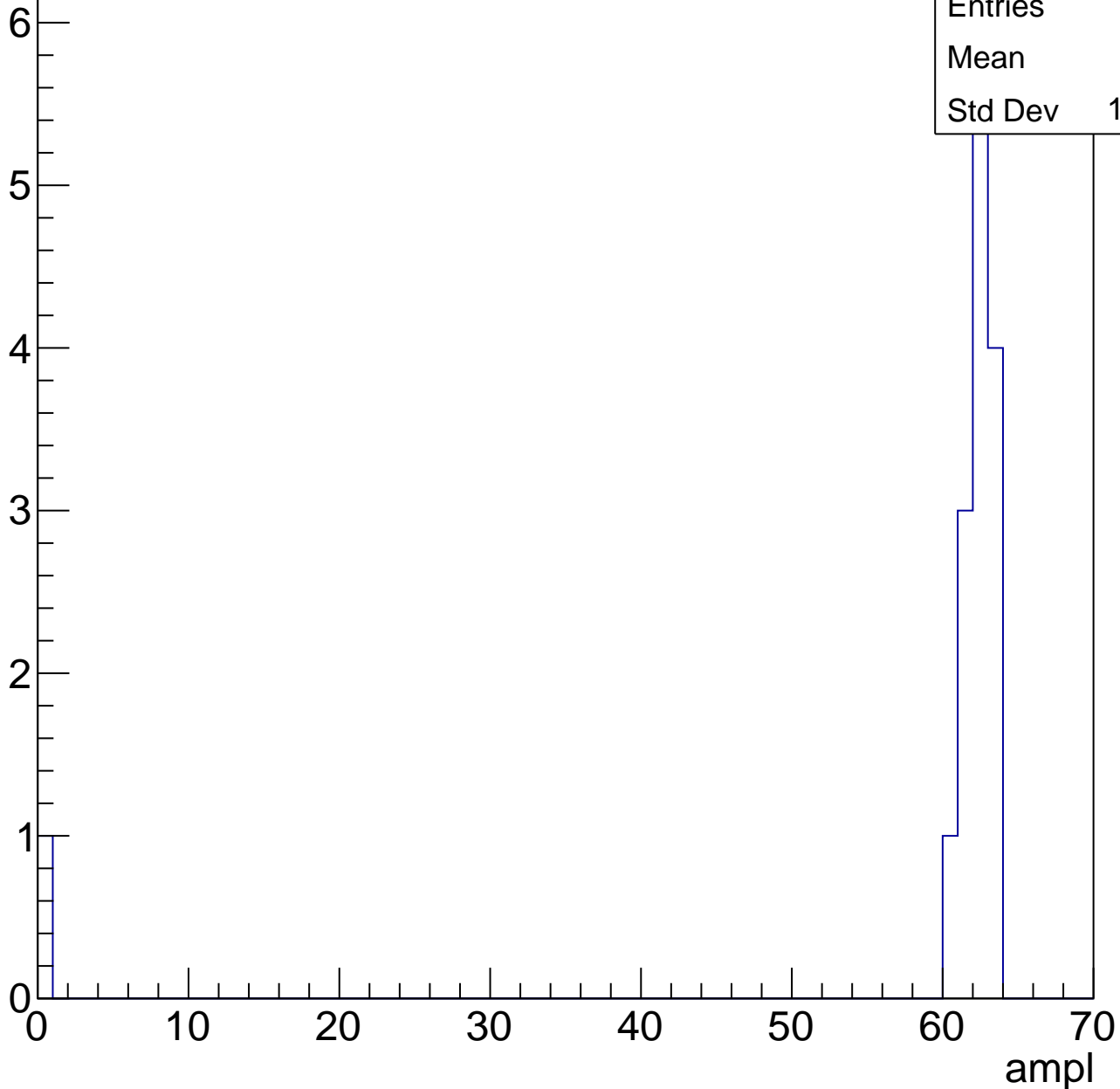
Entries	47
Mean	58.83
Std Dev	2.778

B1L100S, U15-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

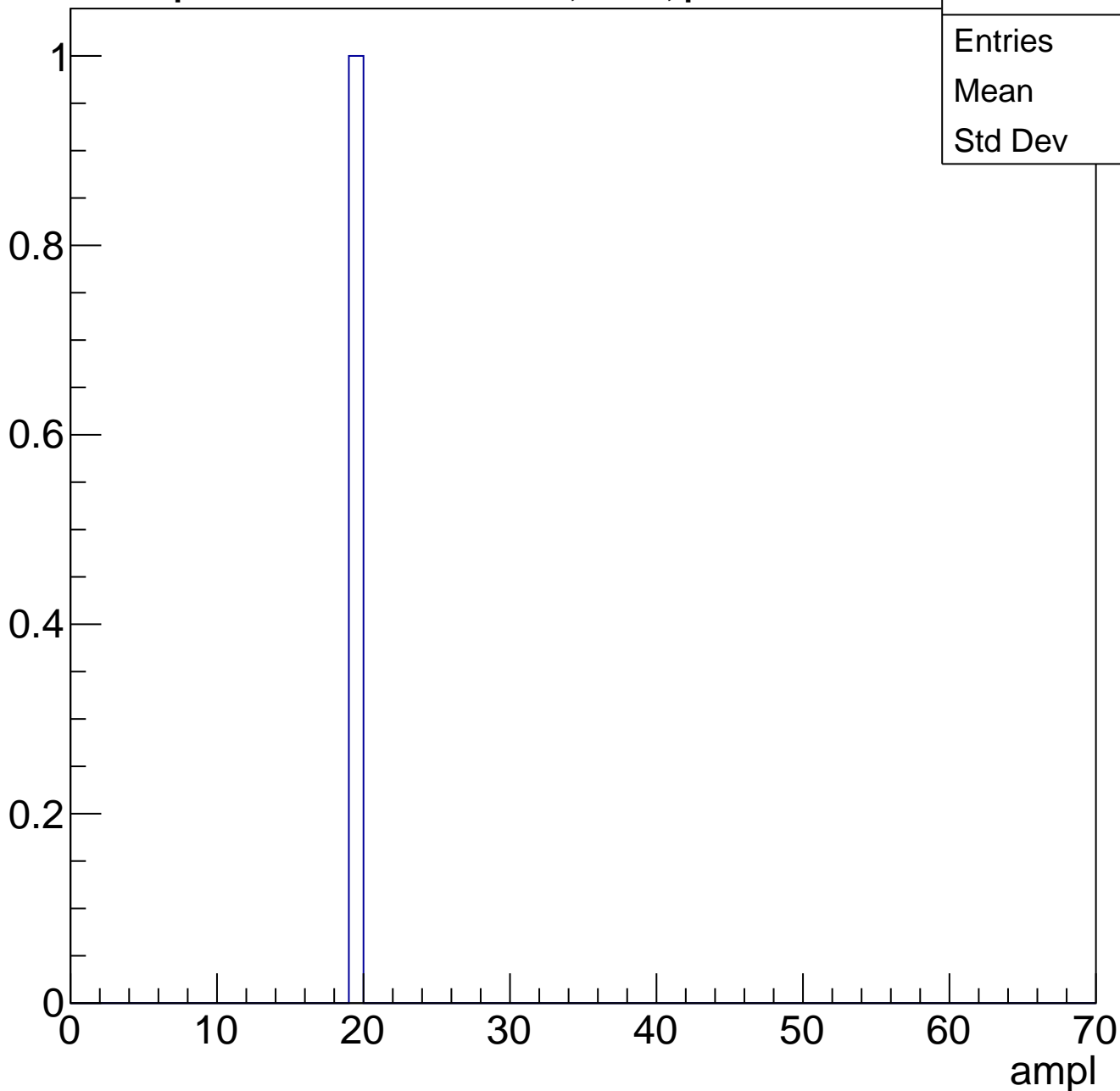
Entries	15
Mean	57.8
Std Dev	15.47



B1L100S, U15-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch65, adc0

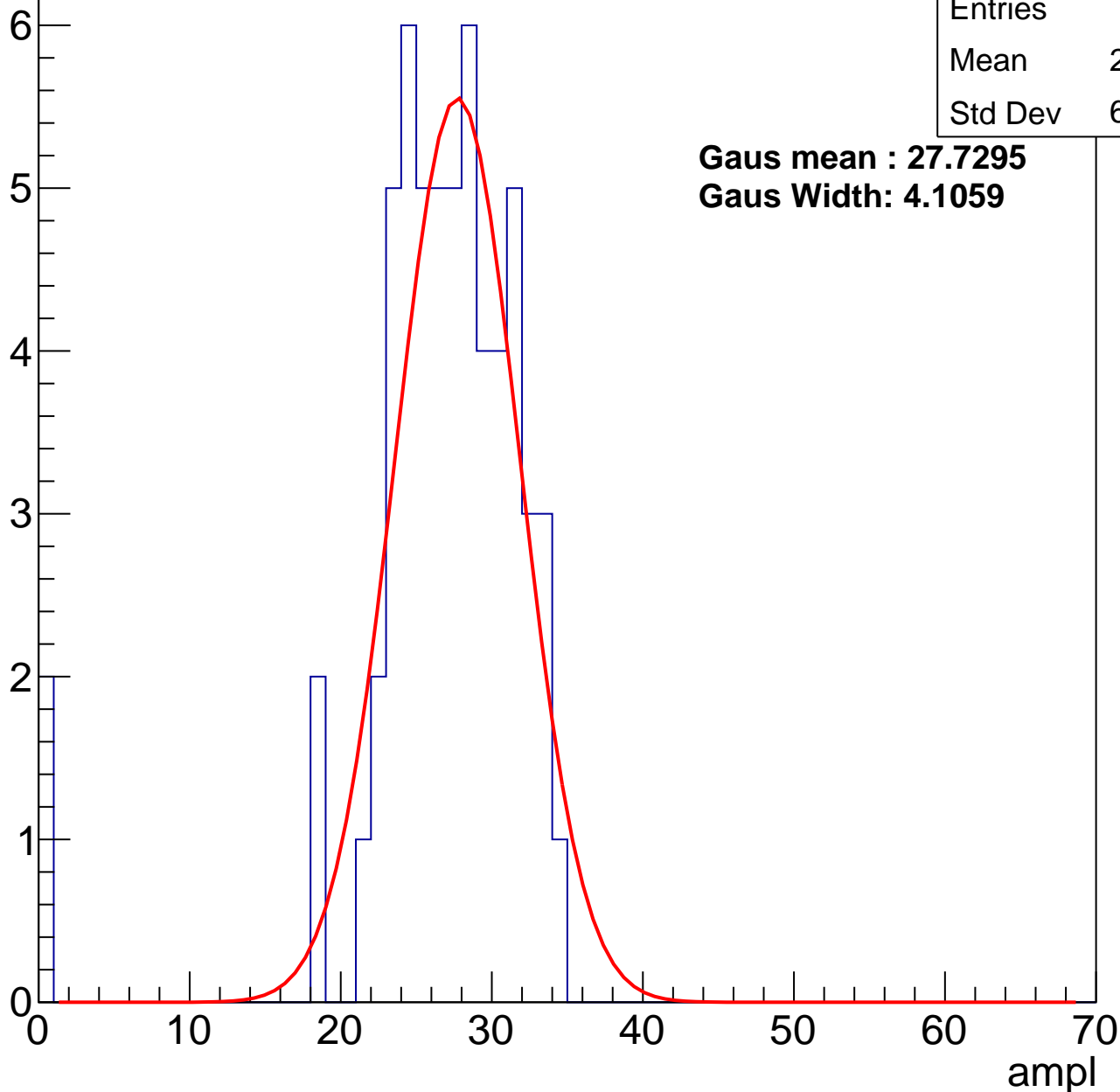
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	26.07
Std Dev	6.084

Gaus mean : 27.7295

Gaus Width: 4.1059



B1L100S, U15-ch65, adc1

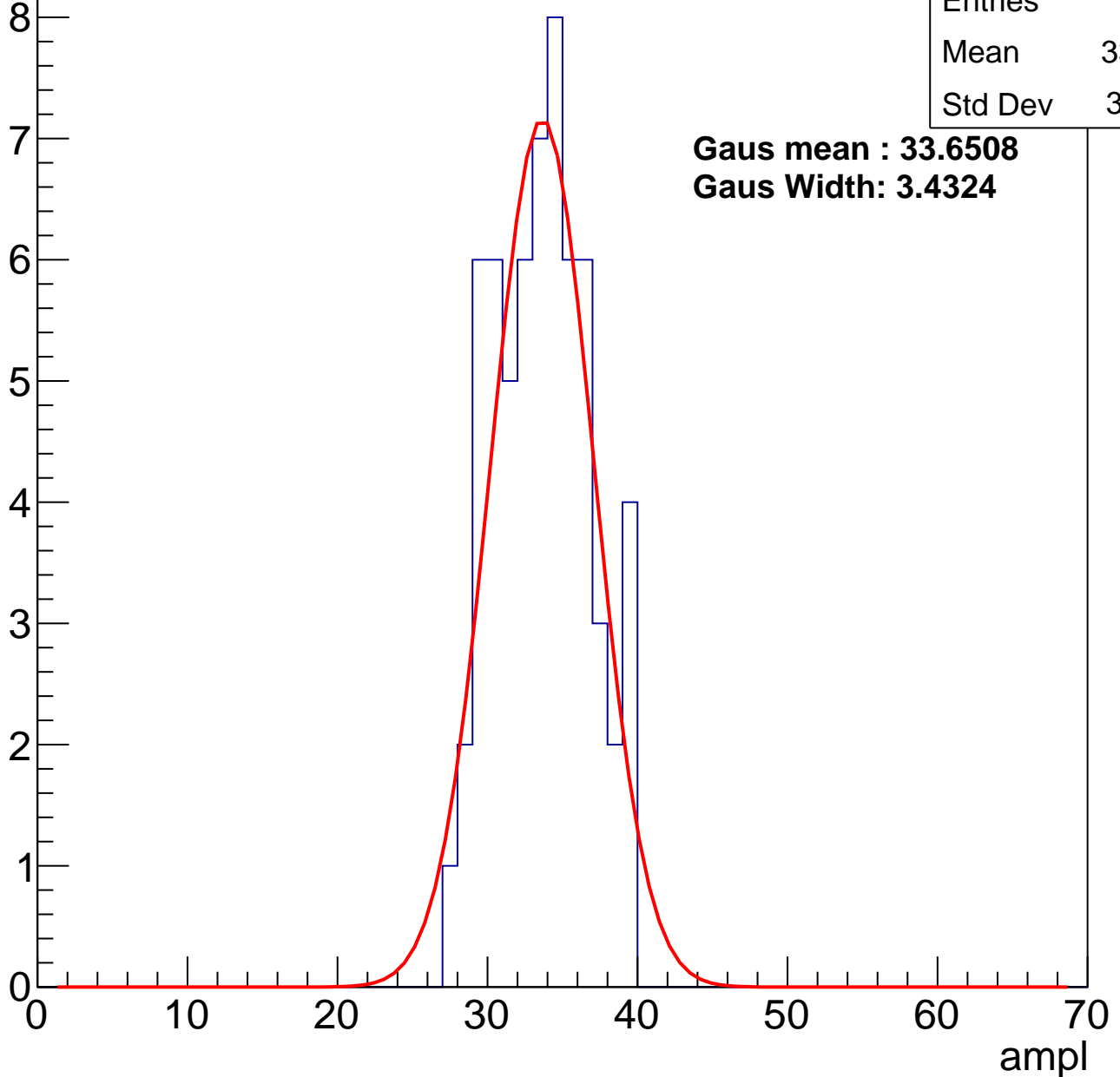
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	33.16
Std Dev	3.081

Gaus mean : 33.6508

Gaus Width: 3.4324



B1L100S, U15-ch65, adc2

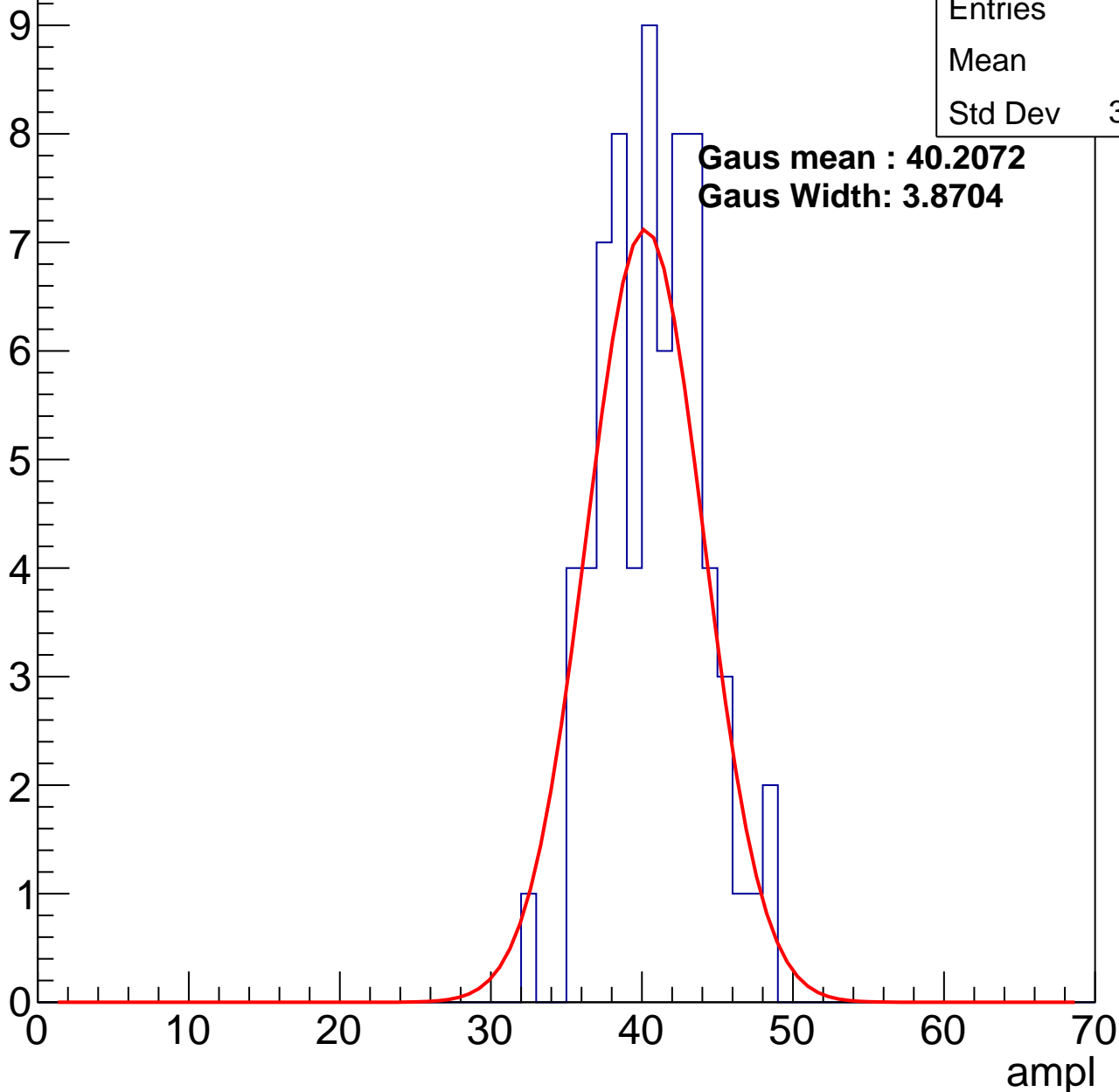
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	40.3
Std Dev	3.344

Gaus mean : 40.2072

Gaus Width: 3.8704

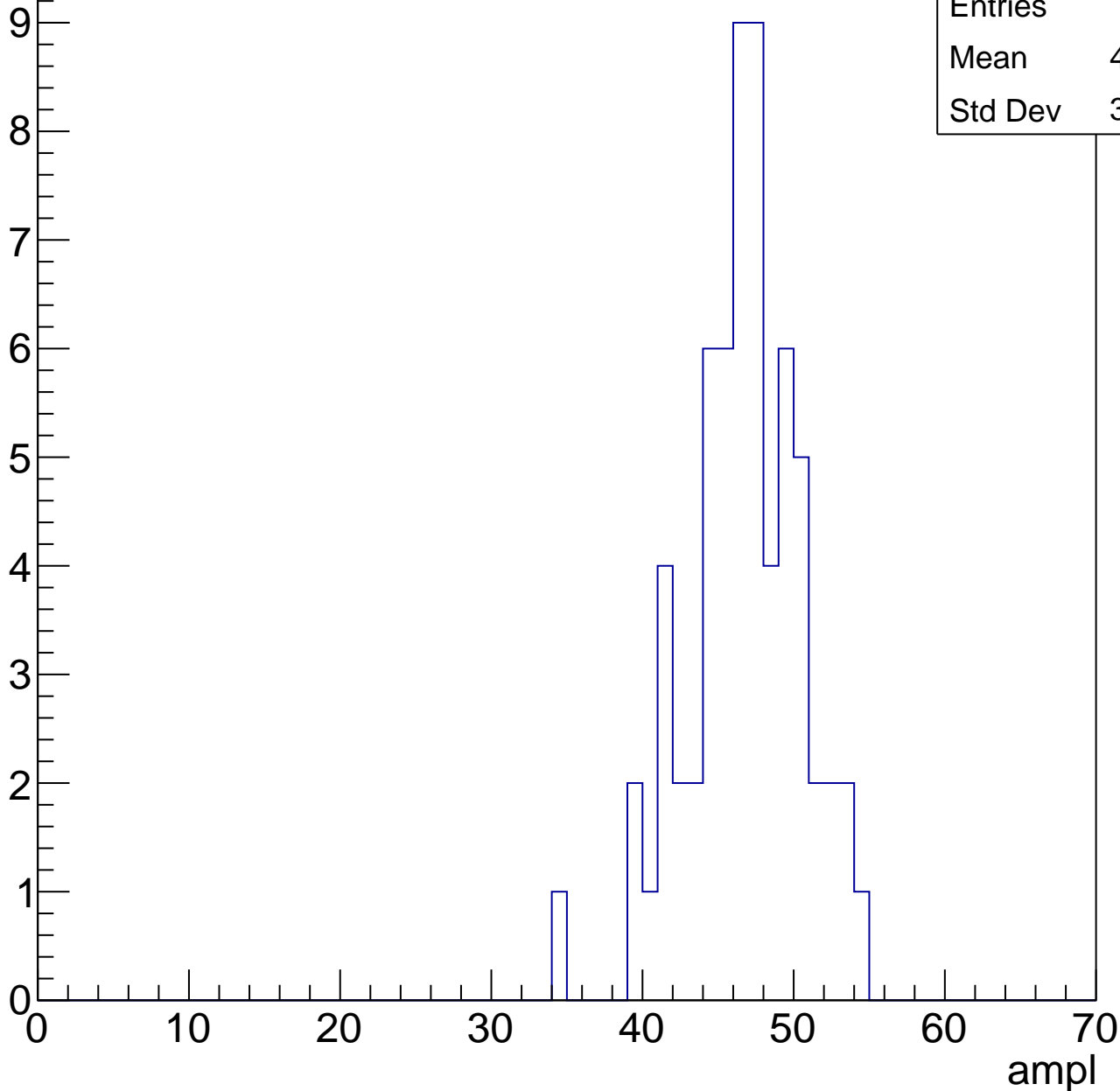


B1L100S, U15-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	46.23
Std Dev	3.745

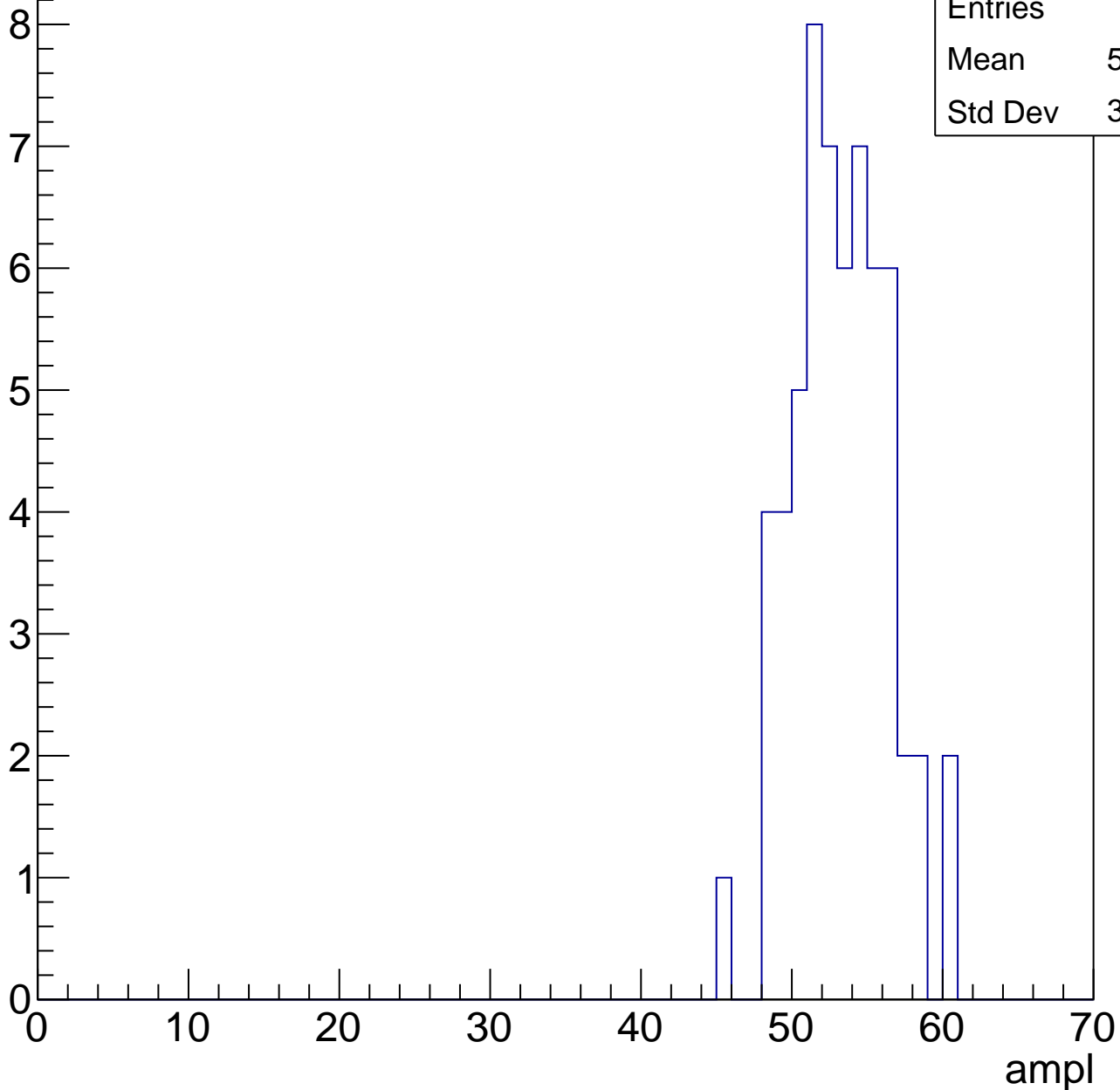


B1L100S, U15-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

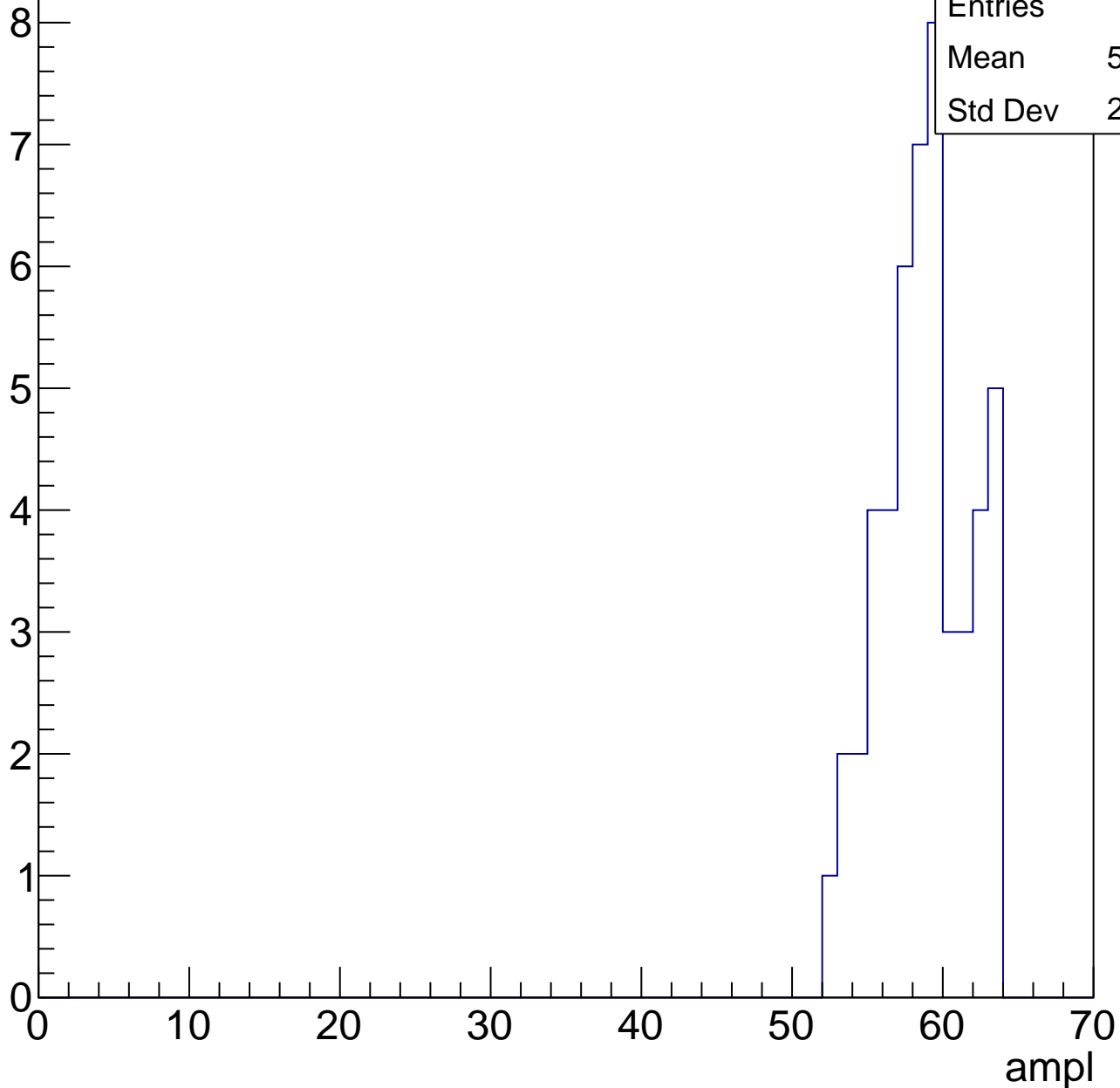
Entries	60
Mean	52.78
Std Dev	3.093



B1L100S, U15-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



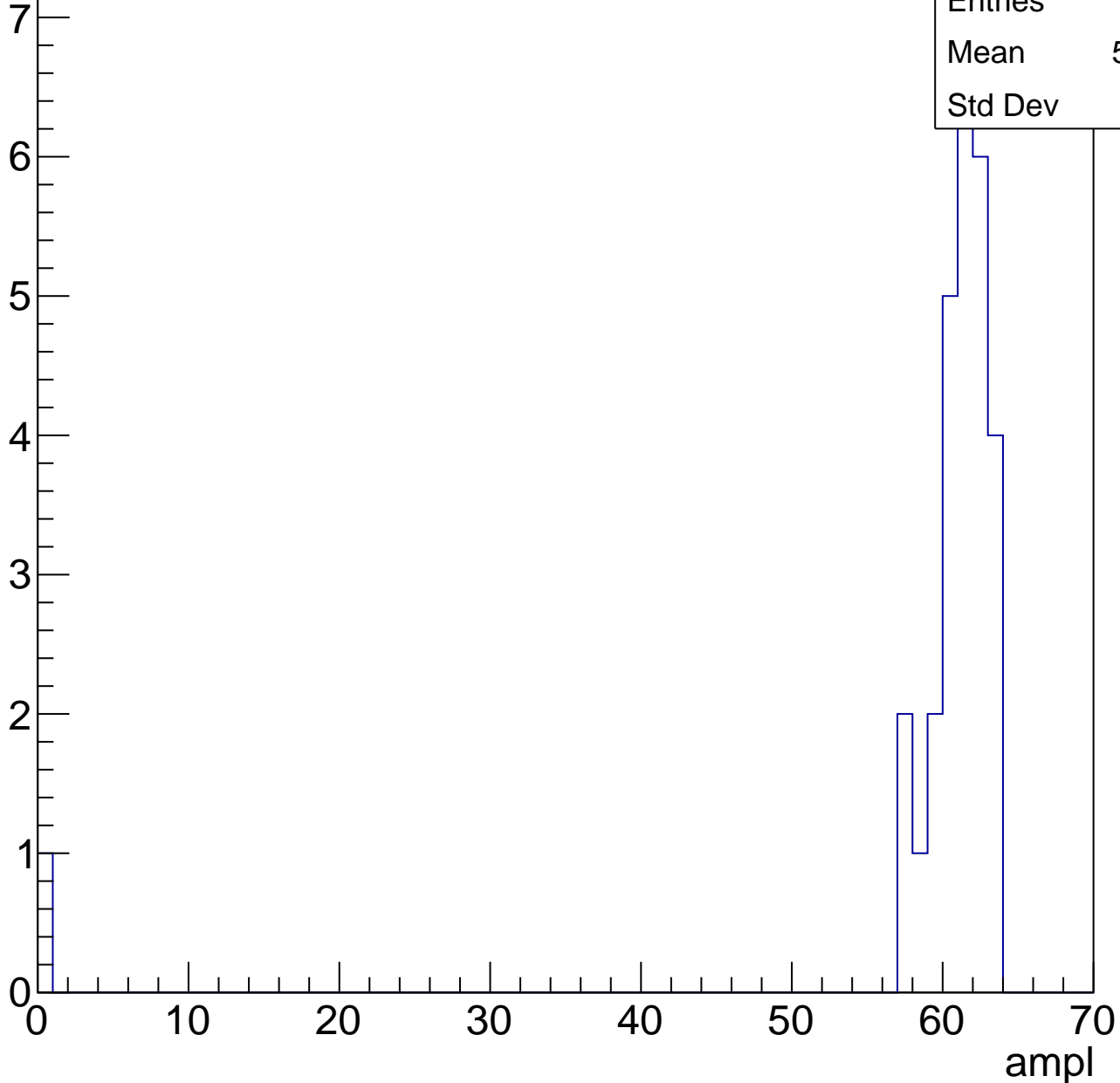
Entries	49
Mean	58.29
Std Dev	2.886

B1L100S, U15-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	58.61
Std Dev	11.4



B1L100S, U15-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch66, adc0

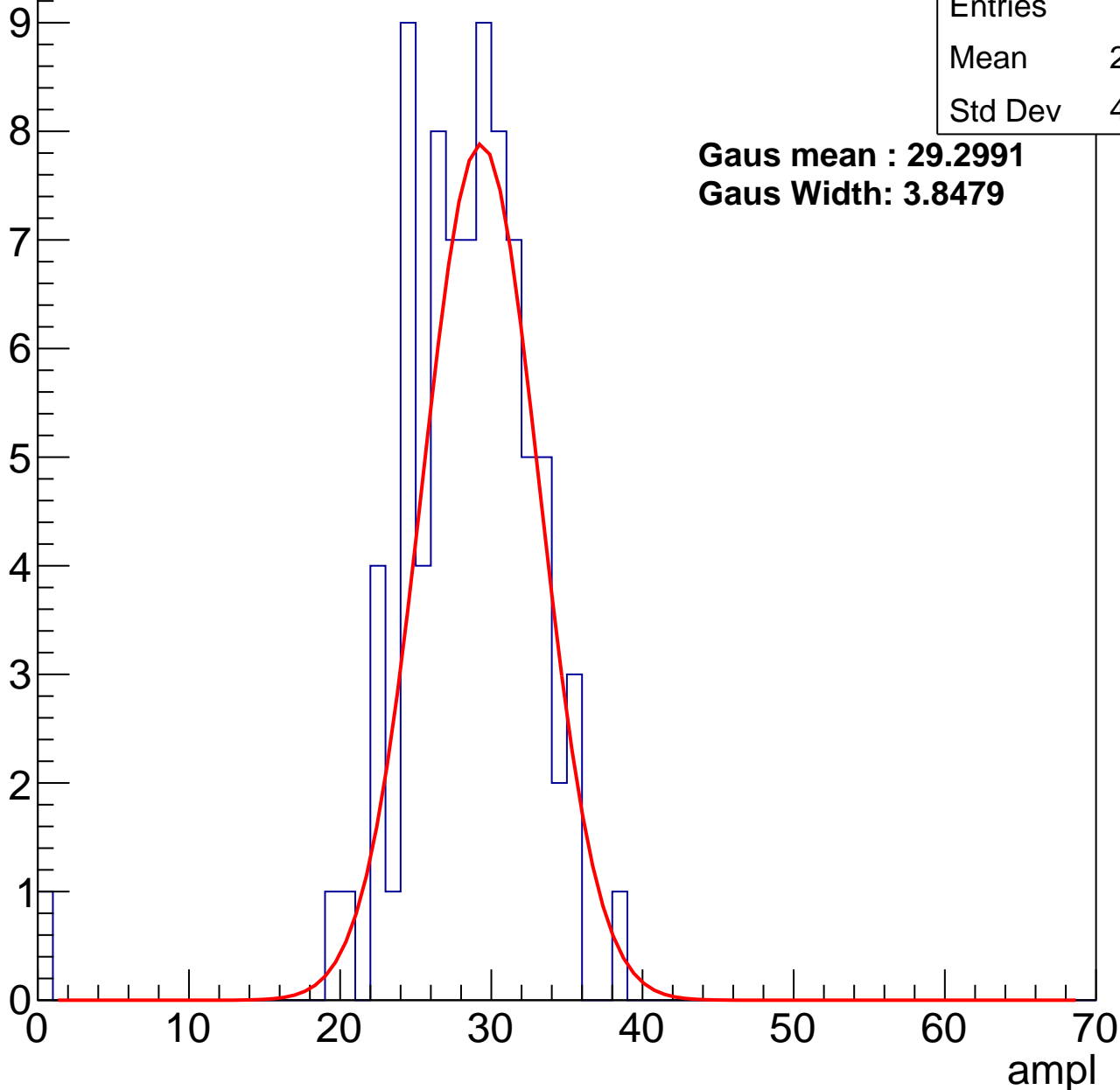
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	27.87
Std Dev	4.839

Gaus mean : 29.2991

Gaus Width: 3.8479



B1L100S, U15-ch66, adc1

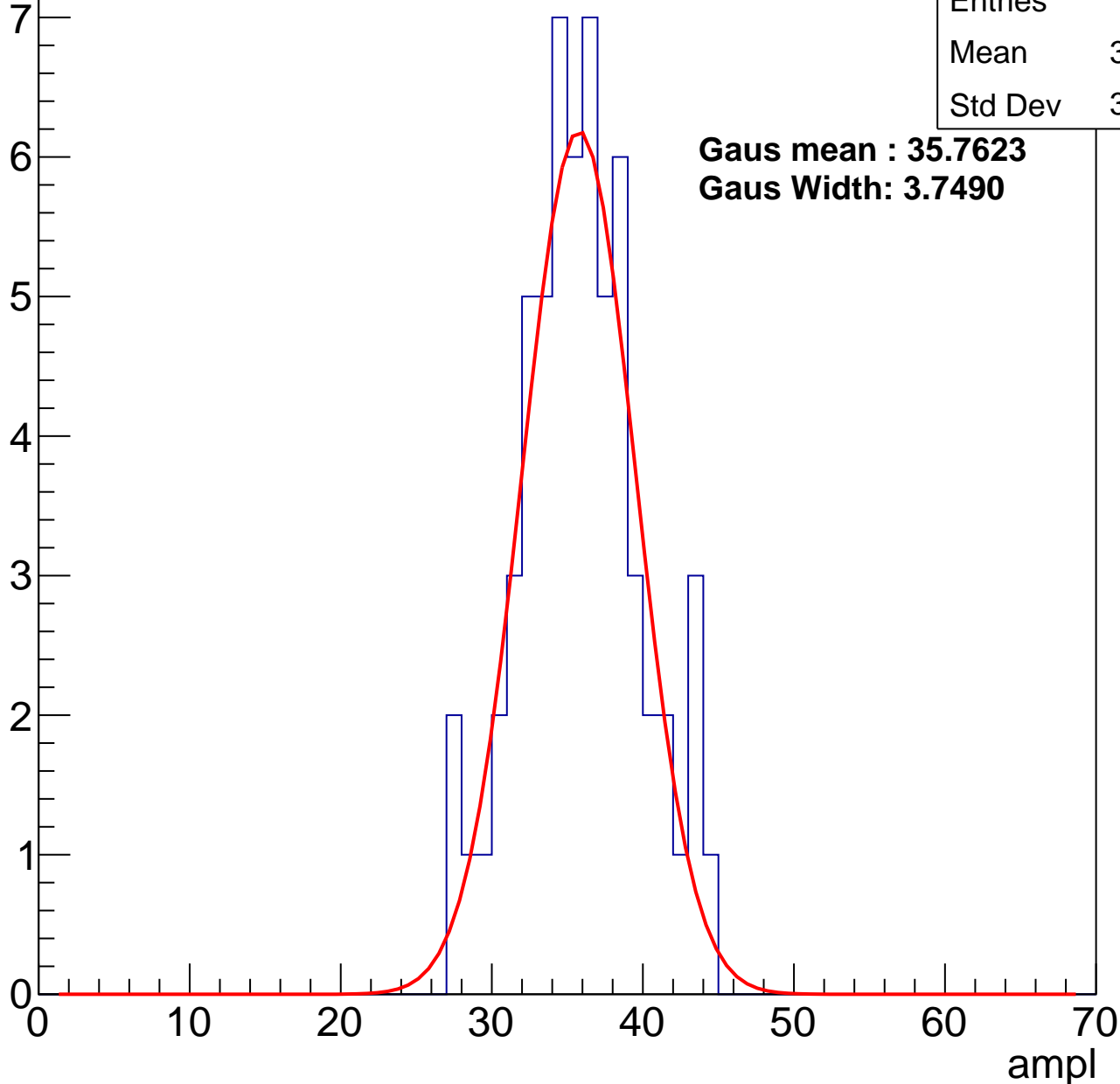
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	35.42
Std Dev	3.904

Gaus mean : 35.7623

Gaus Width: 3.7490



B1L100S, U15-ch66, adc2

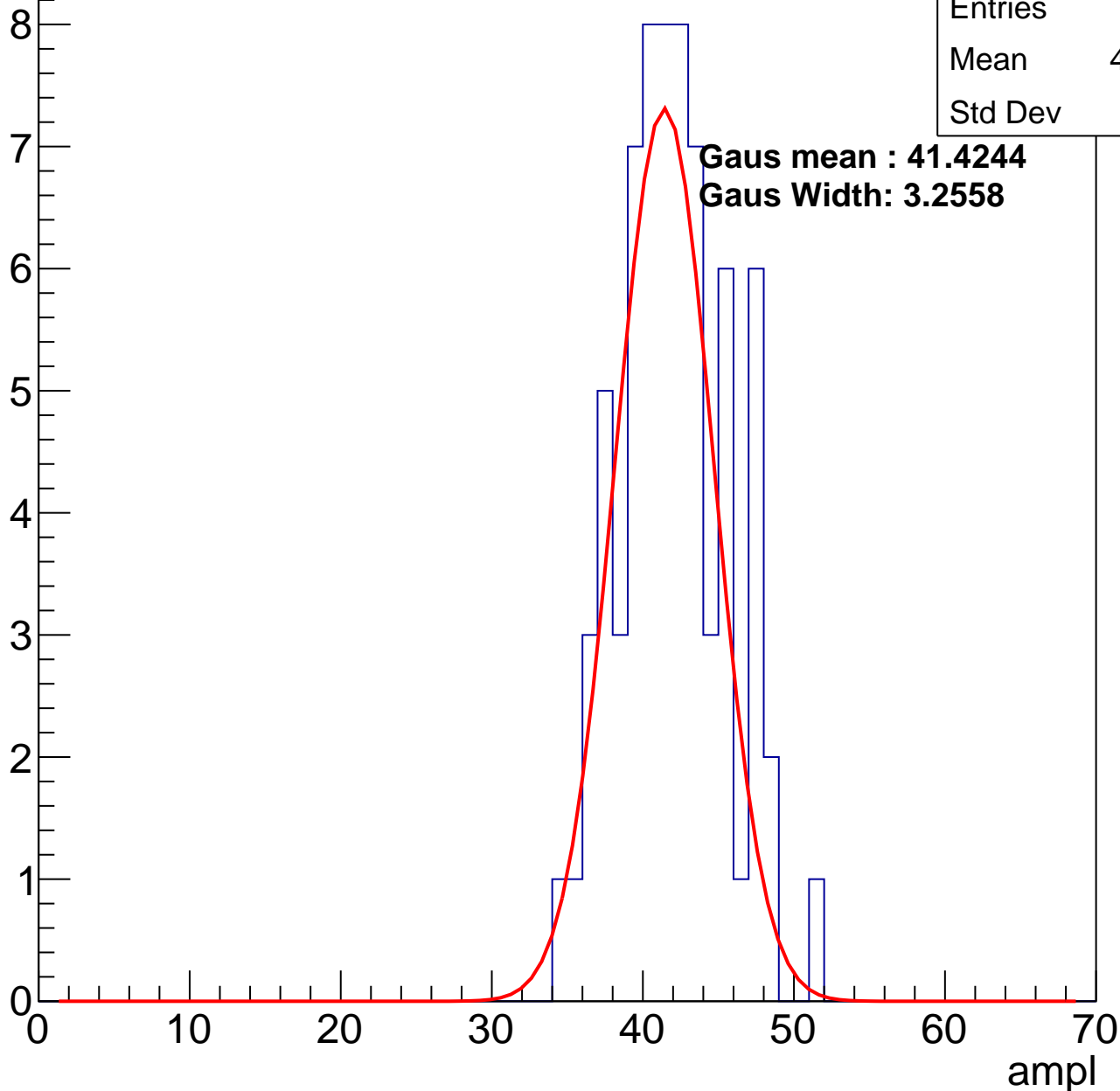
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	41.59
Std Dev	3.54

Gaus mean : 41.4244

Gaus Width: 3.2558

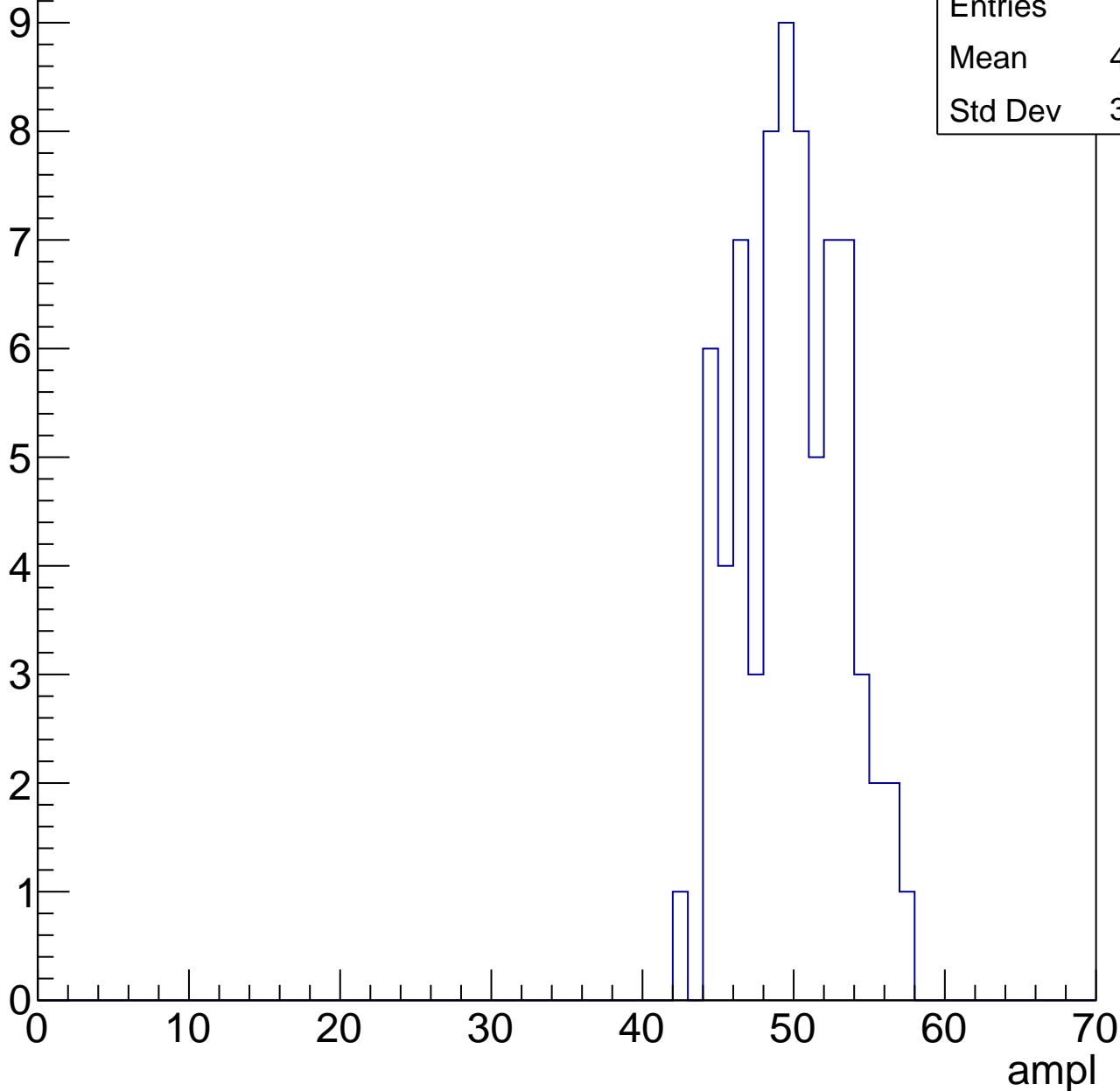


B1L100S, U15-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	49.38
Std Dev	3.407

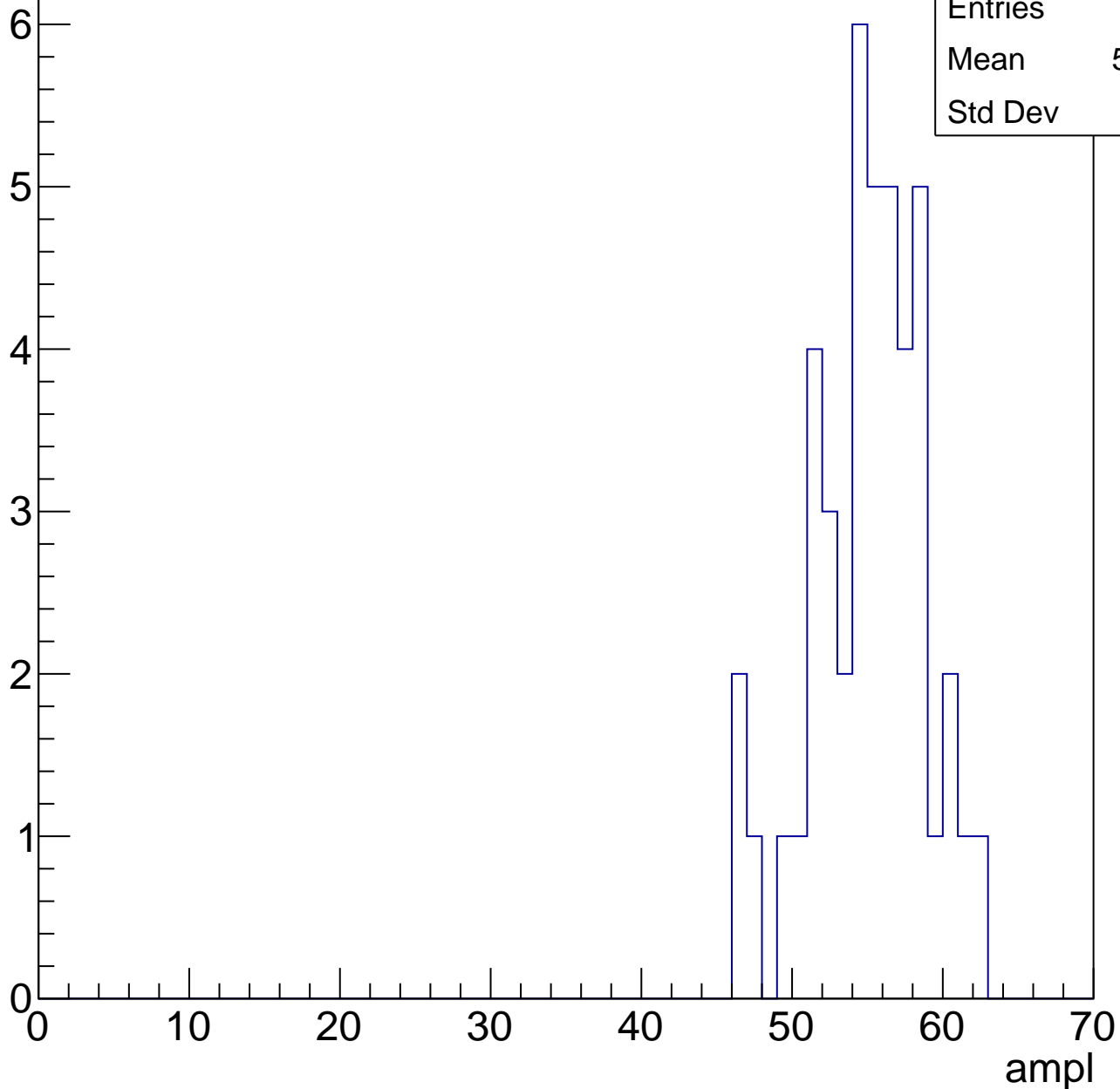


B1L100S, U15-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

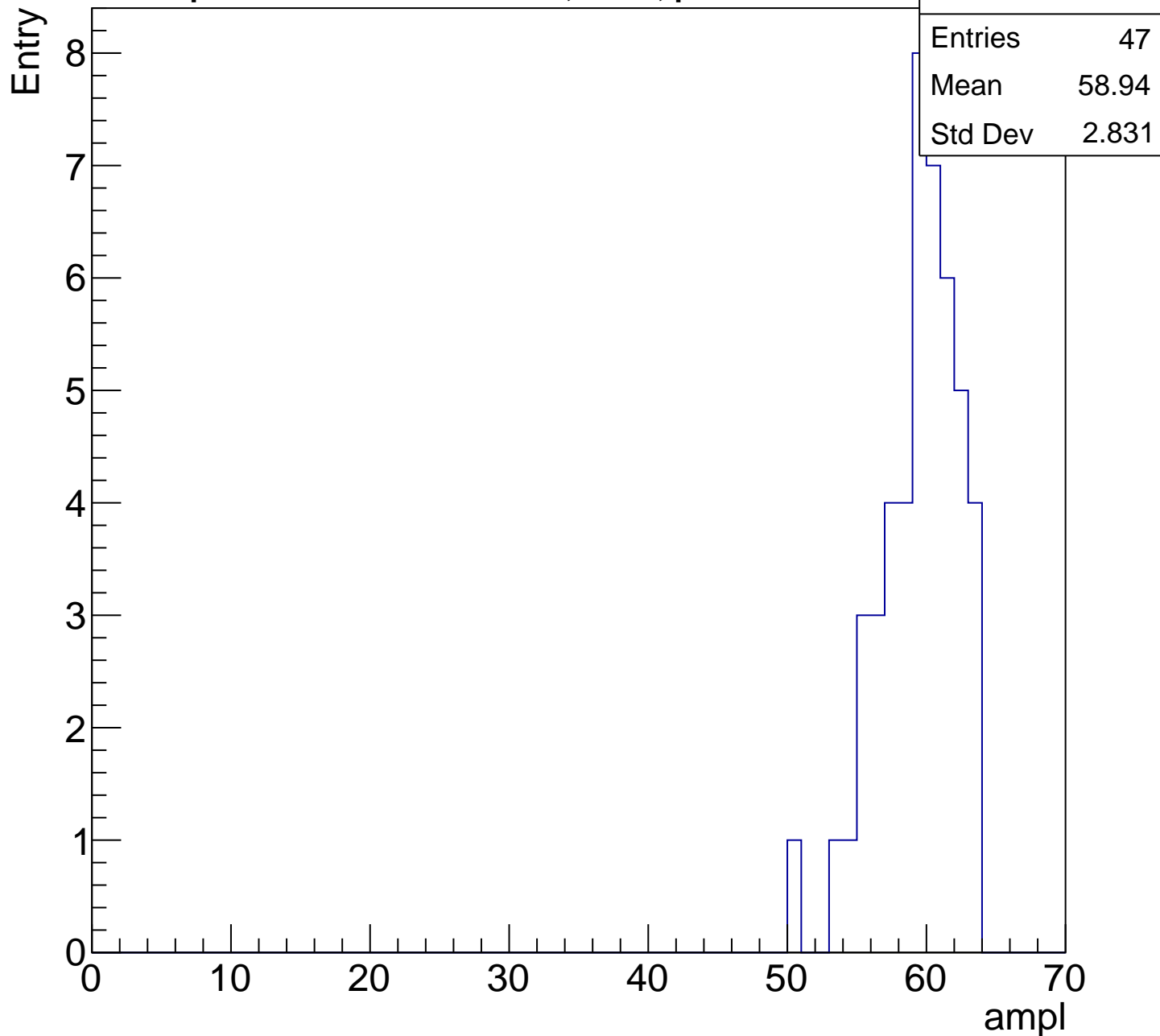
Entry

Entries	44
Mean	54.61
Std Dev	3.7



B1L100S, U15-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

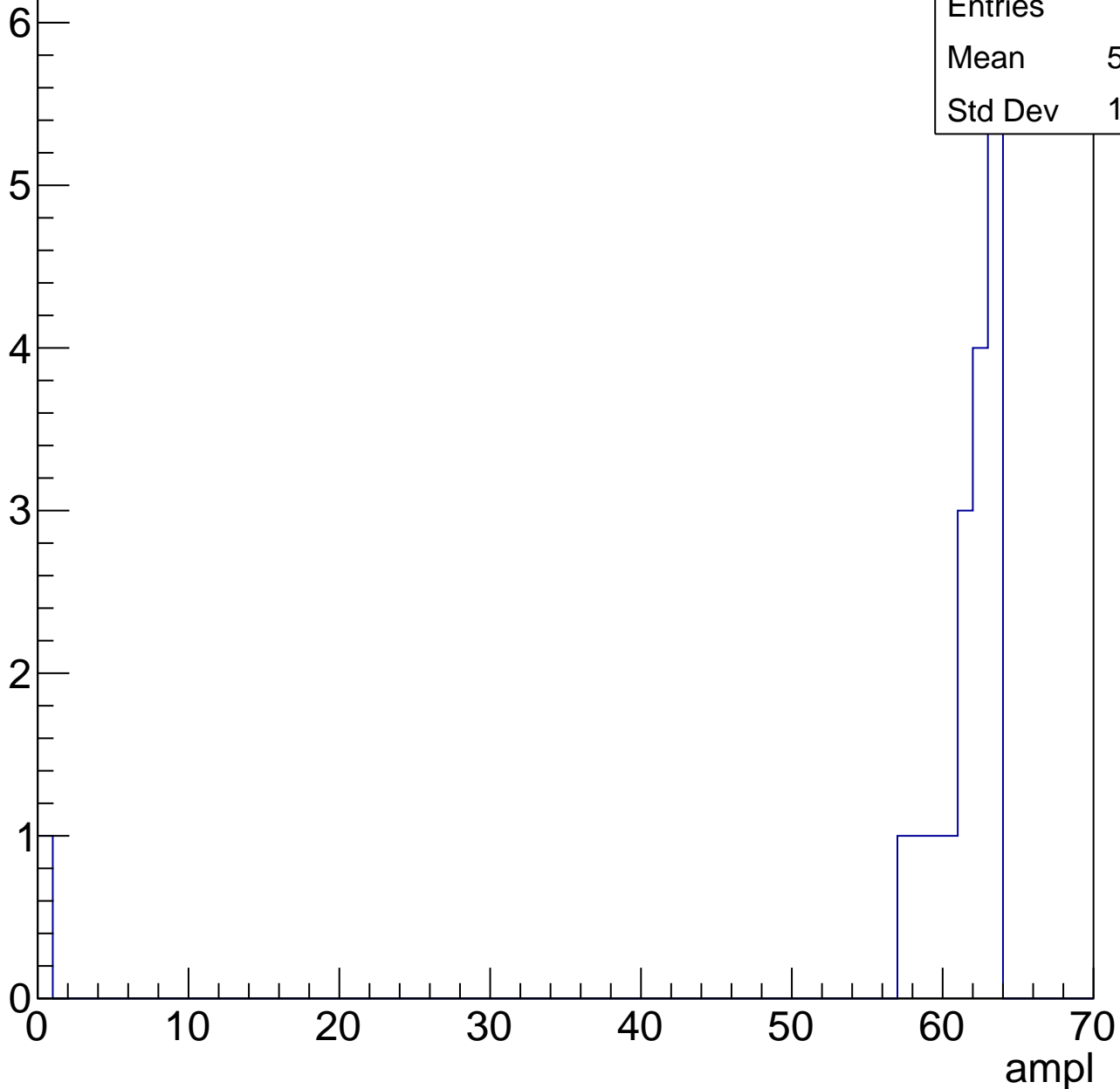


B1L100S, U15-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	57.94
Std Dev	14.16



B1L100S, U15-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch67, adc0

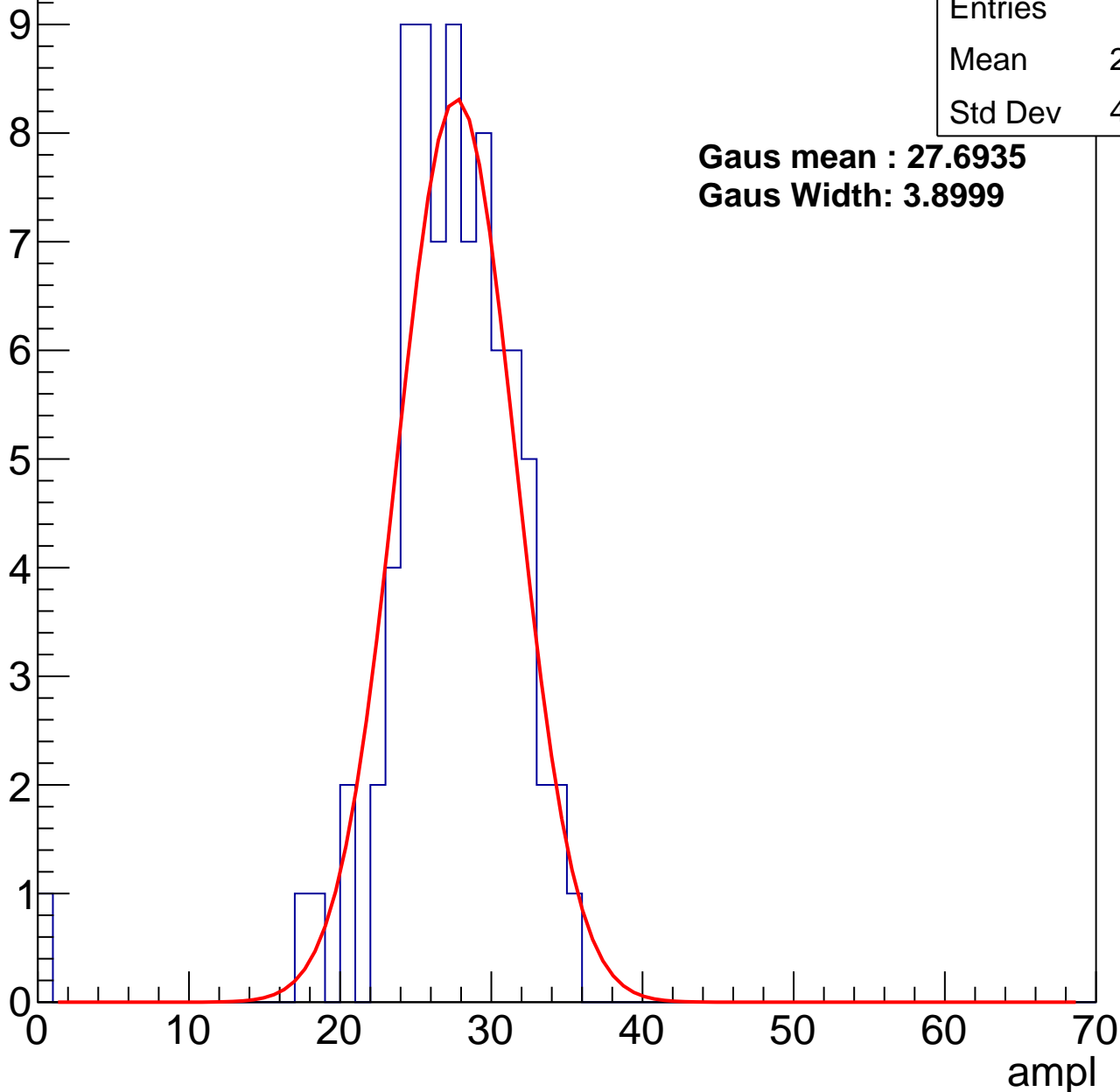
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	26.83
Std Dev	4.674

Gaus mean : 27.6935

Gaus Width: 3.8999



B1L100S, U15-ch67, adc1

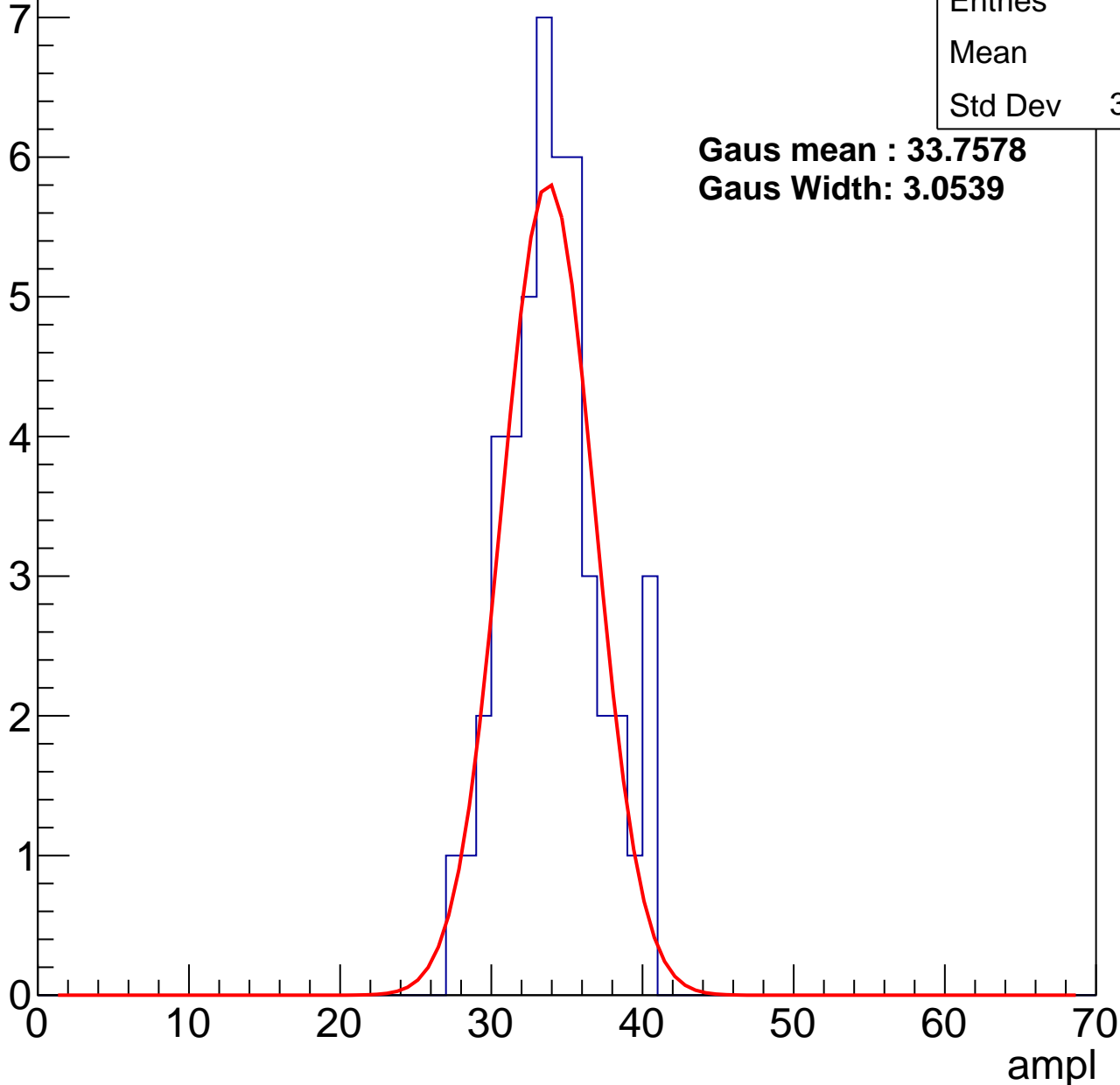
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	33.6
Std Dev	3.112

Gaus mean : 33.7578

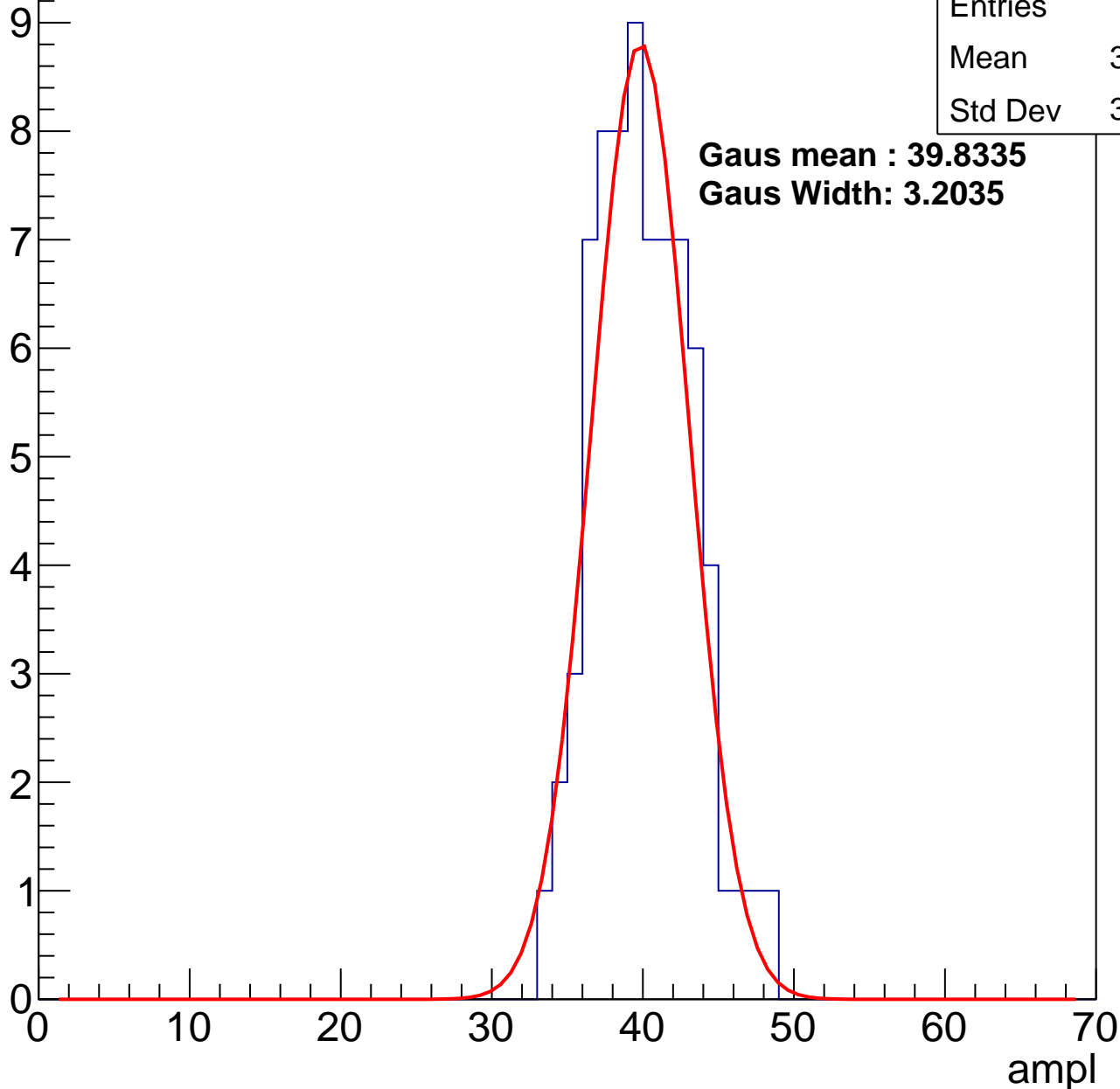
Gaus Width: 3.0539



B1L100S, U15-ch67, adc2

calib_packv5_042523_0143.root, FC#4, port A2

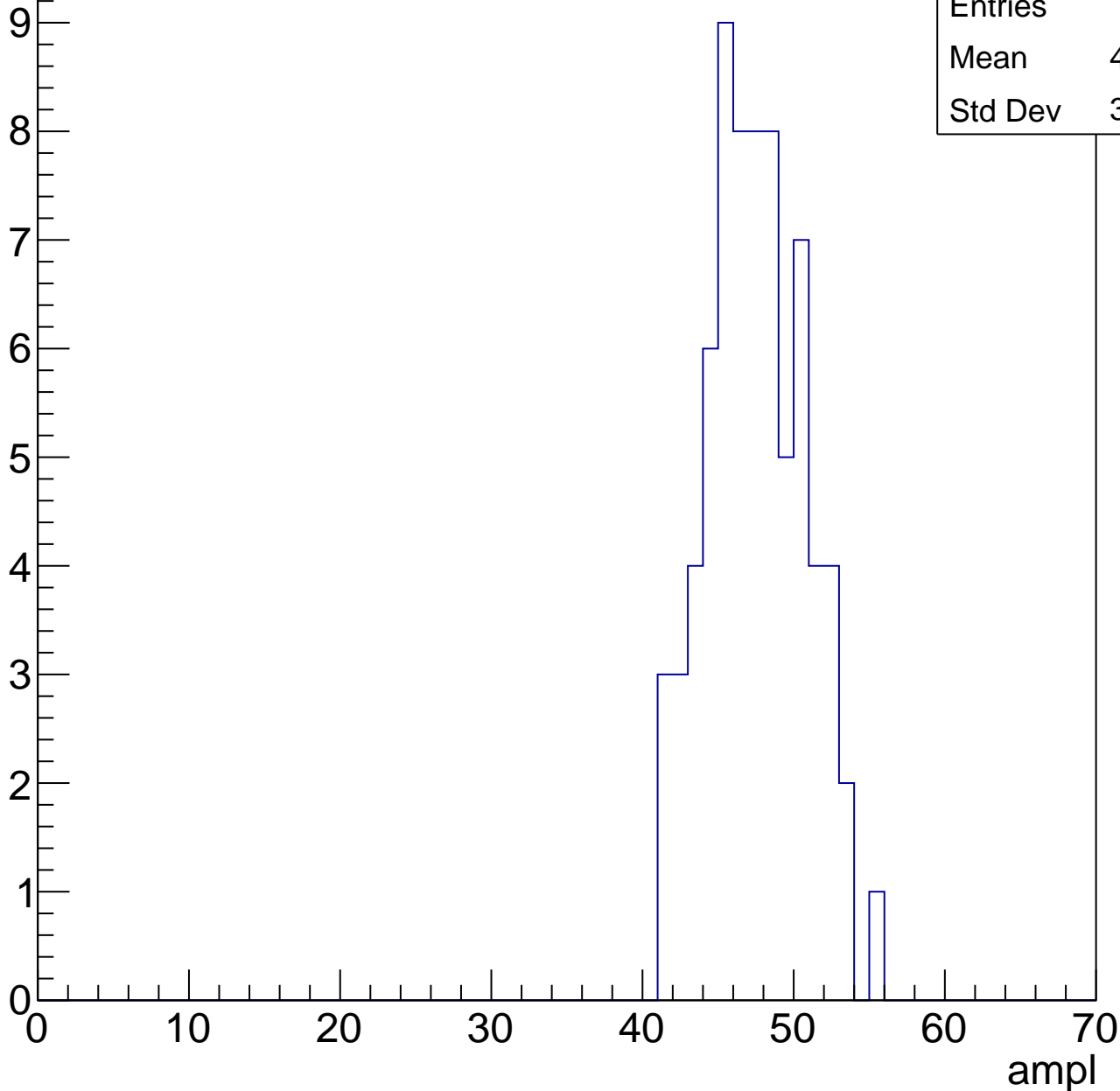
Entry



B1L100S, U15-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



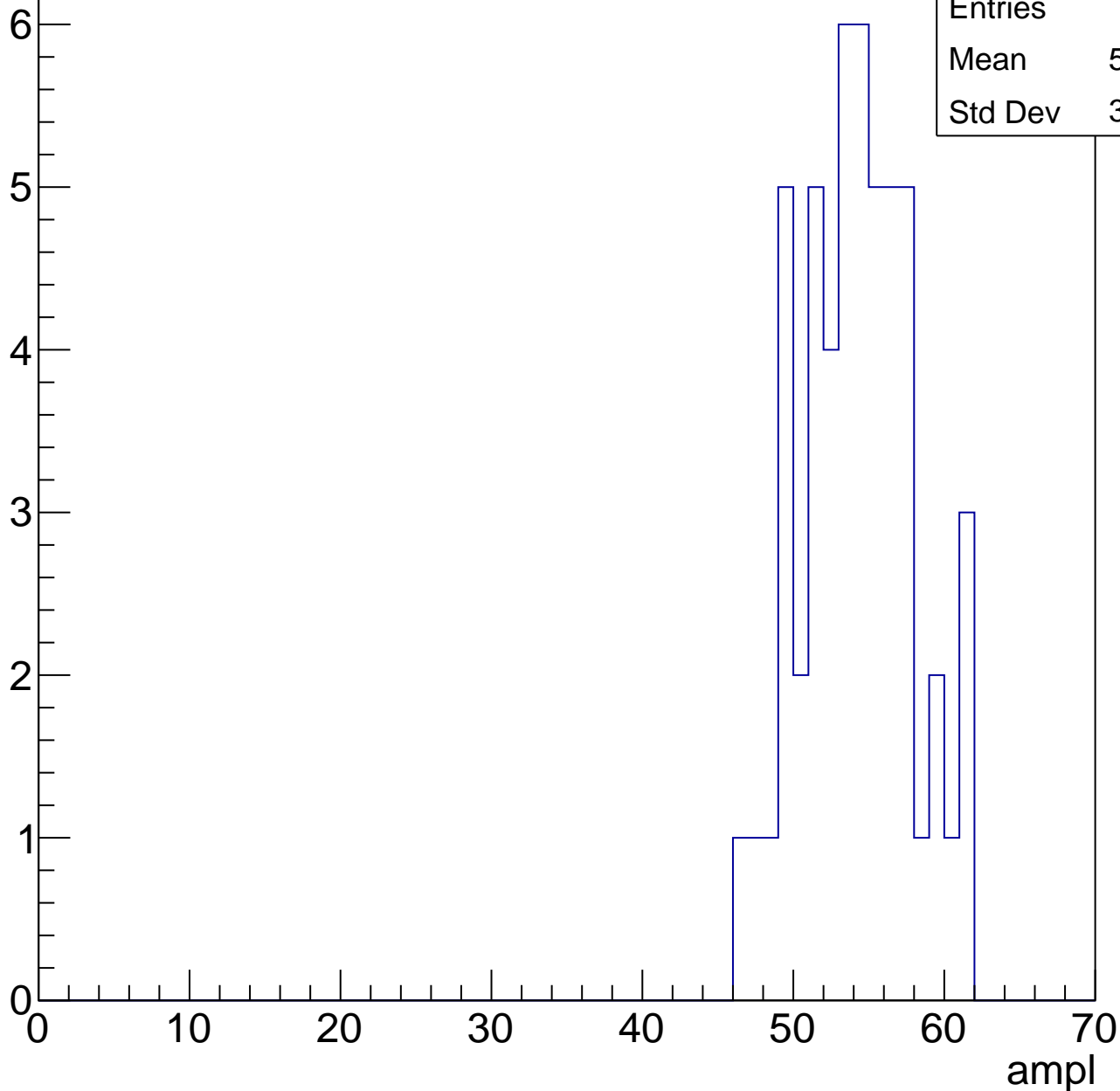
Entries	72
Mean	47.03
Std Dev	3.197

B1L100S, U15-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	53.77
Std Dev	3.606



B1L100S, U15-ch67, adc5

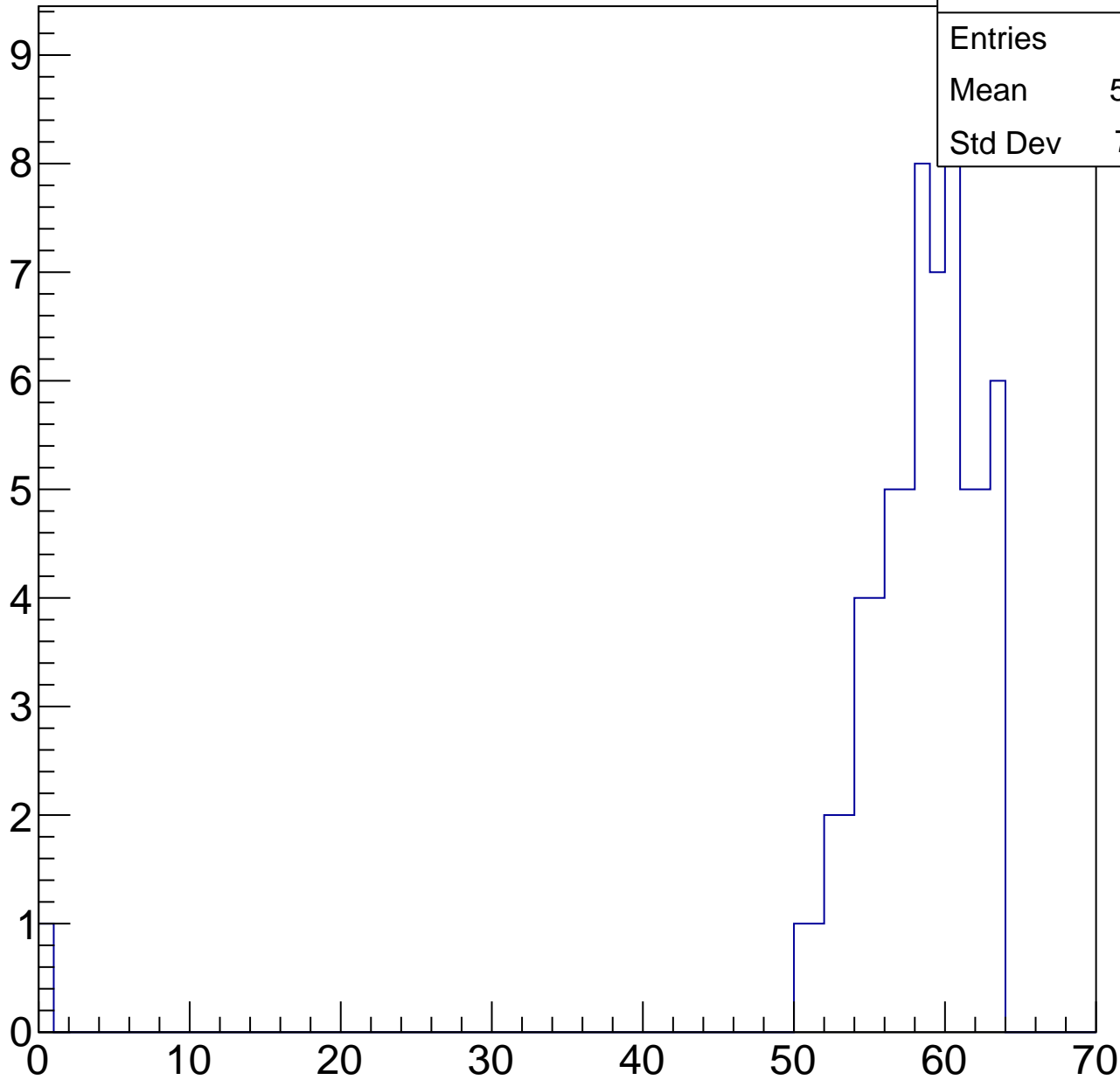
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	65
Mean	57.26
Std Dev	7.851

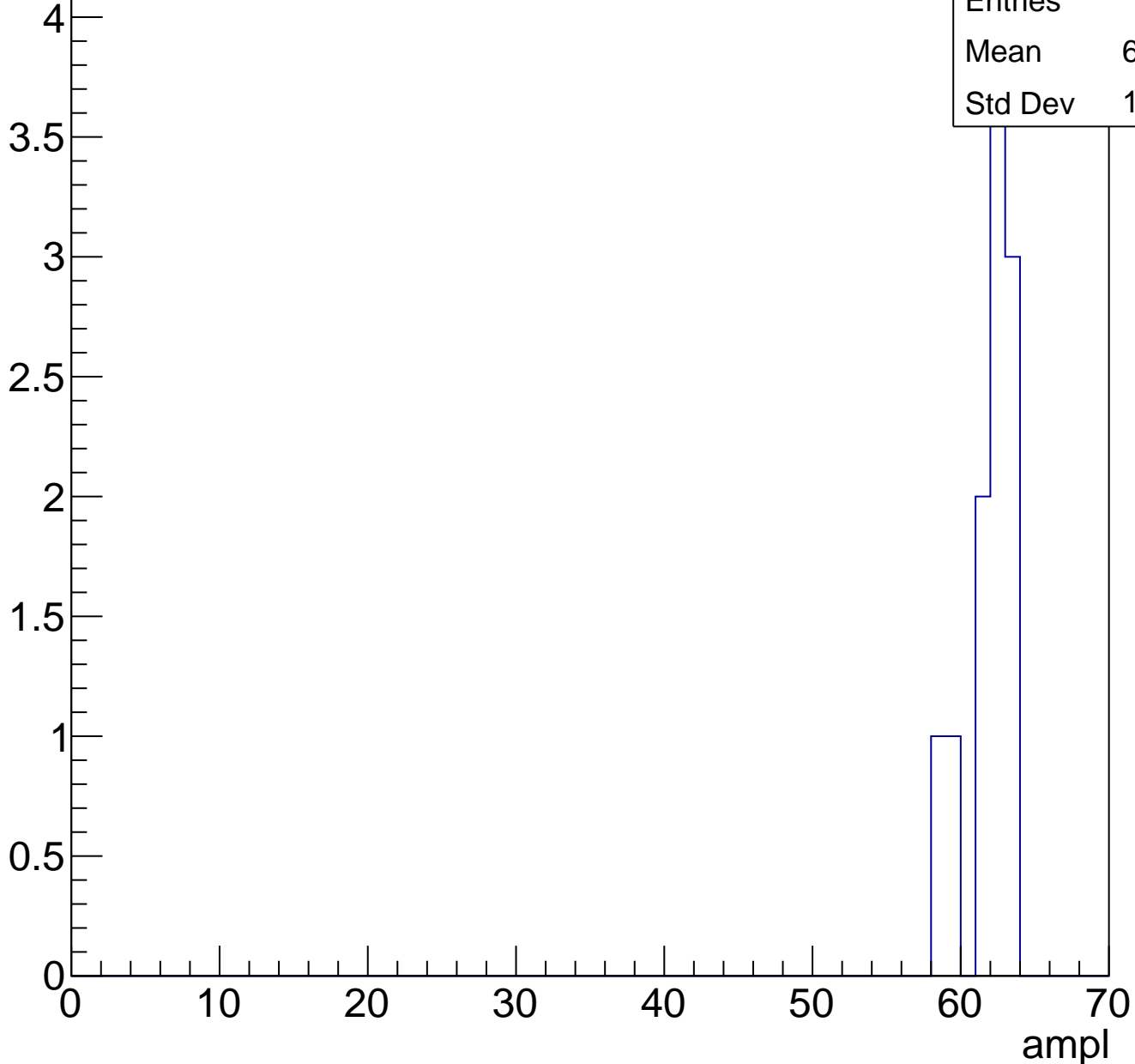
ampl



B1L100S, U15-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch68, adc0

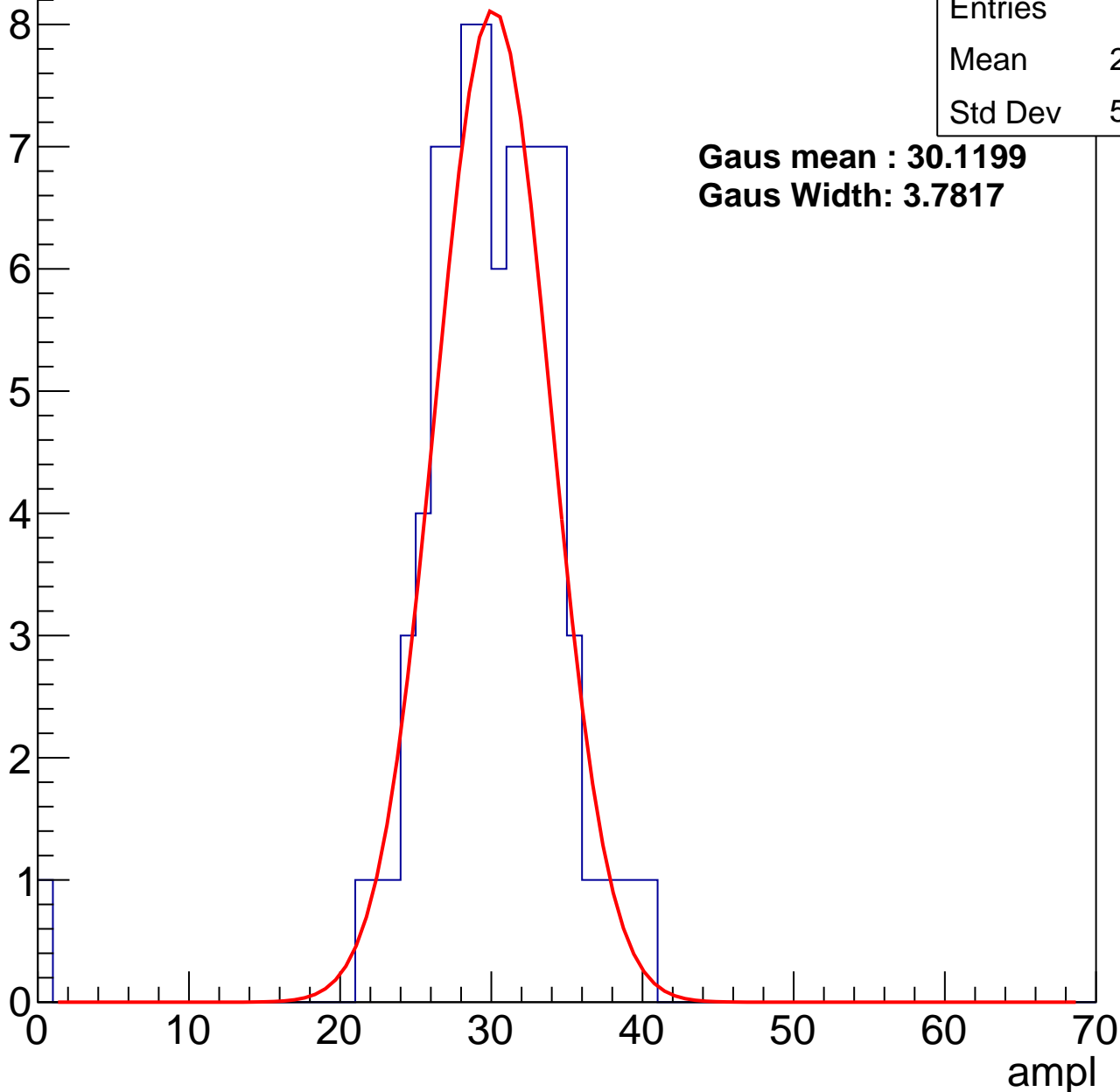
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	29.52
Std Dev	5.045

Gaus mean : 30.1199

Gaus Width: 3.7817



B1L100S, U15-ch68, adc1

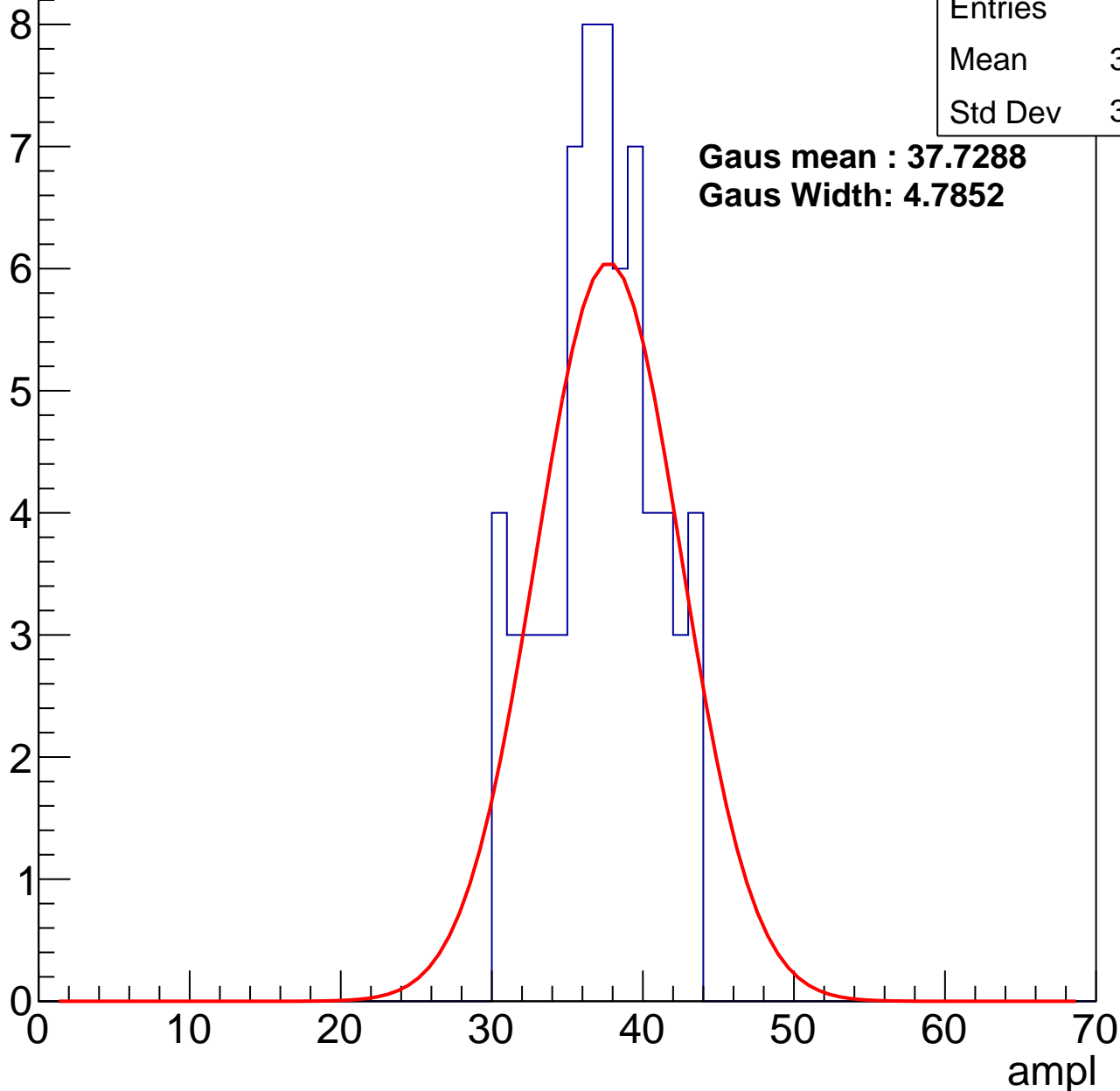
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	36.75
Std Dev	3.538

Gaus mean : 37.7288

Gaus Width: 4.7852



B1L100S, U15-ch68, adc2

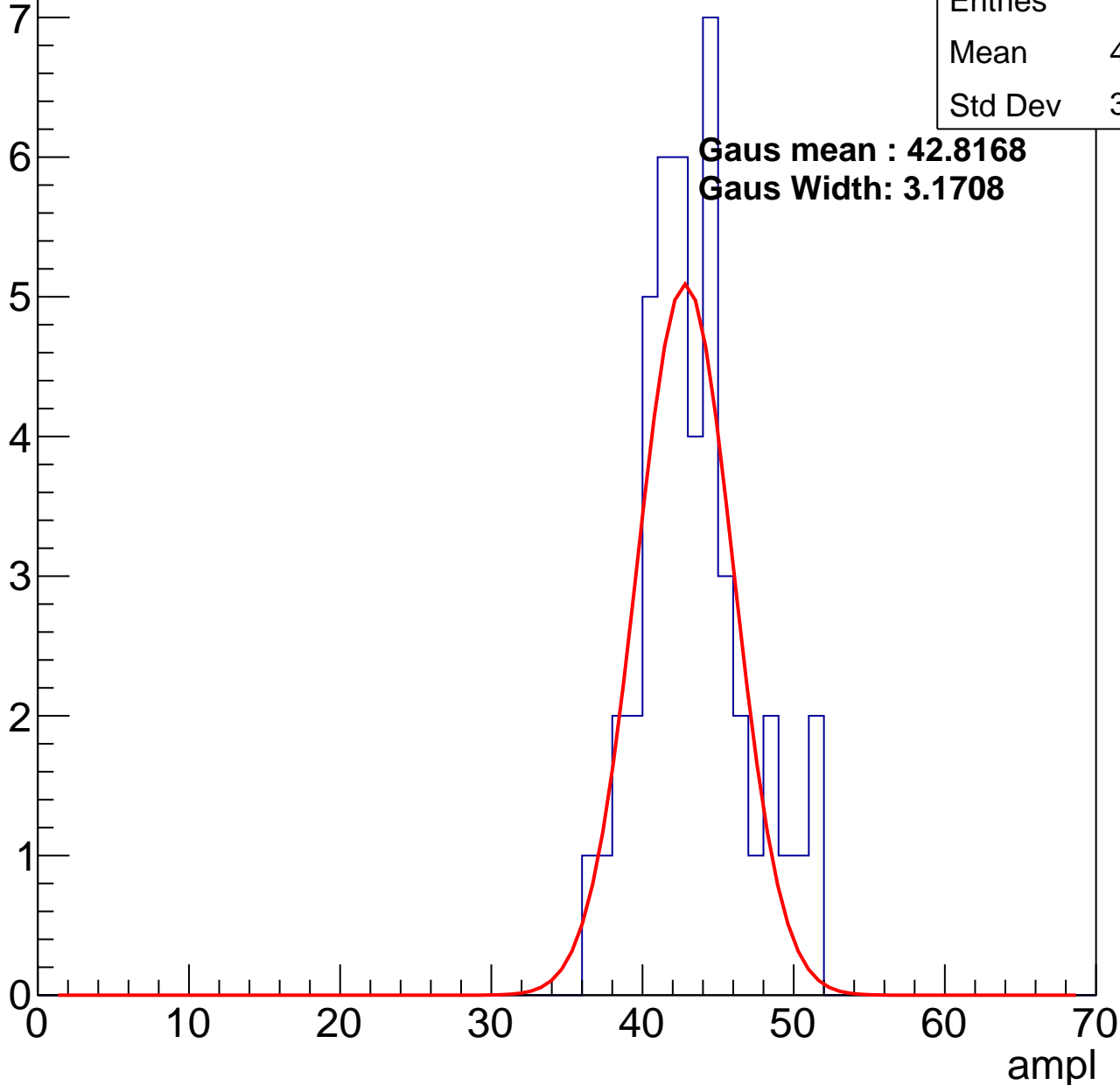
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	42.96
Std Dev	3.483

Gaus mean : 42.8168

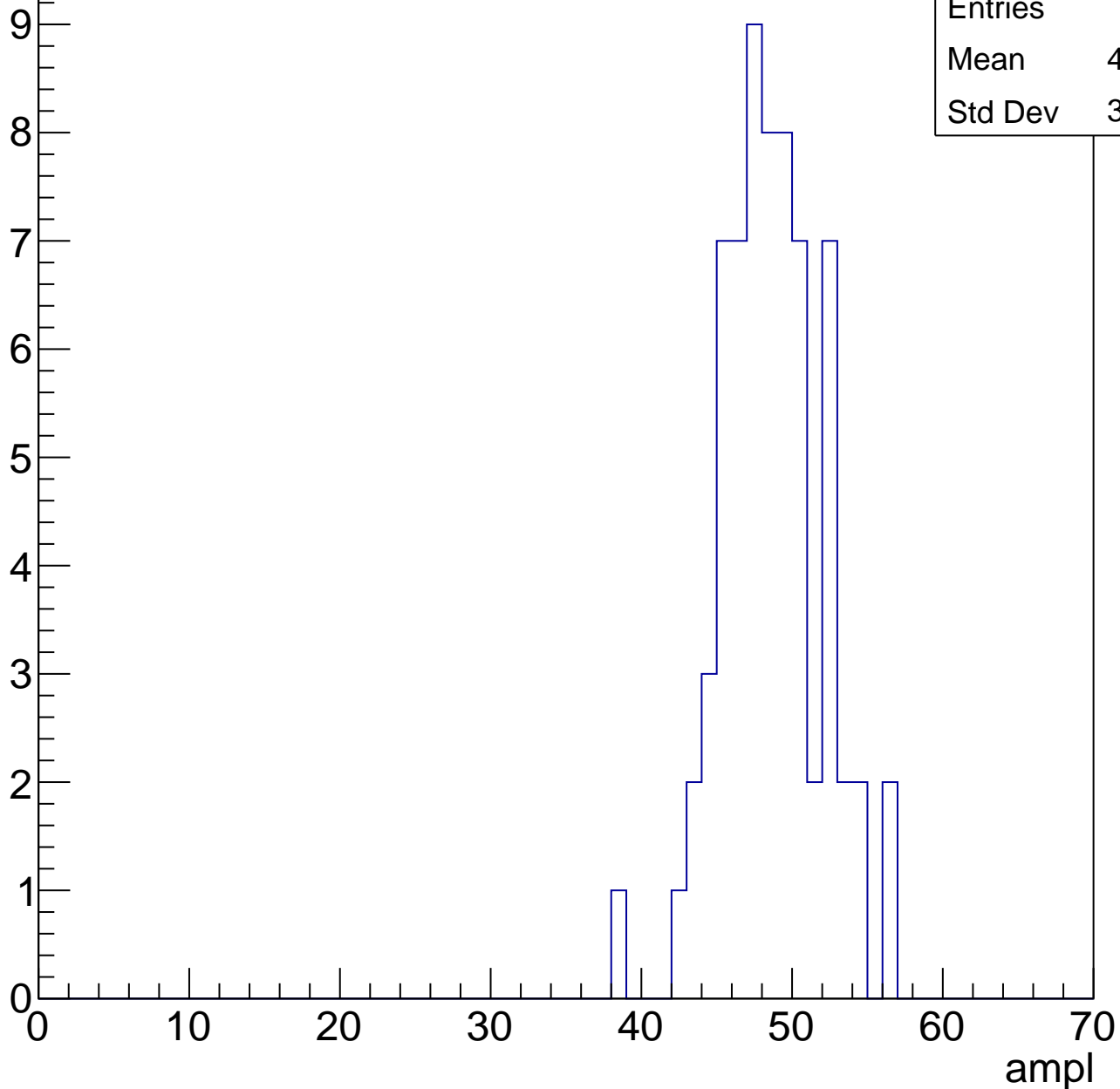
Gaus Width: 3.1708



B1L100S, U15-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

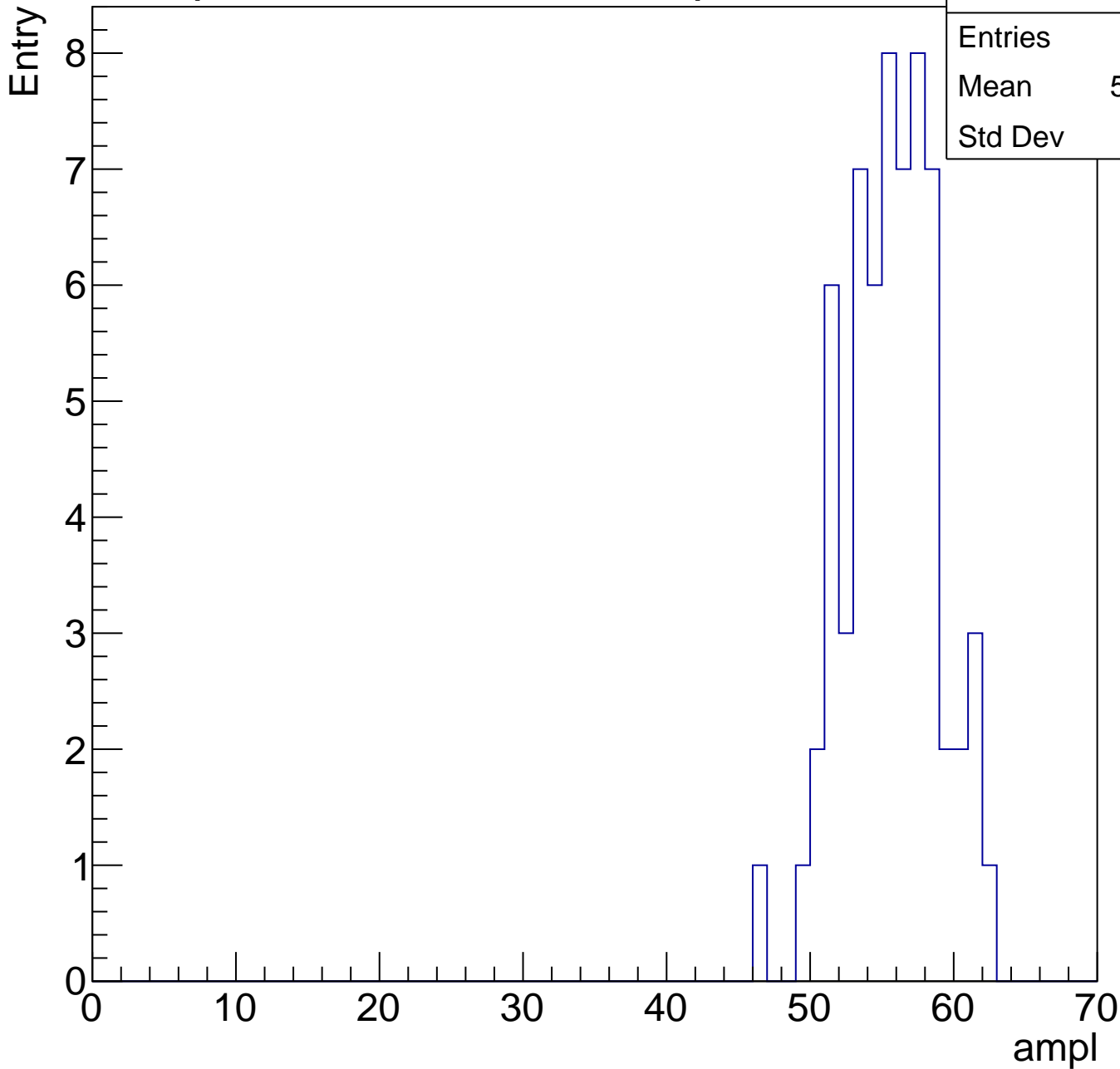


Entries	68
Mean	48.18
Std Dev	3.316

B1L100S, U15-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	55.14
Std Dev	3.22



B1L100S, U15-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.39
Std Dev	9.264

ampl

0

10

20

30

40

50

60

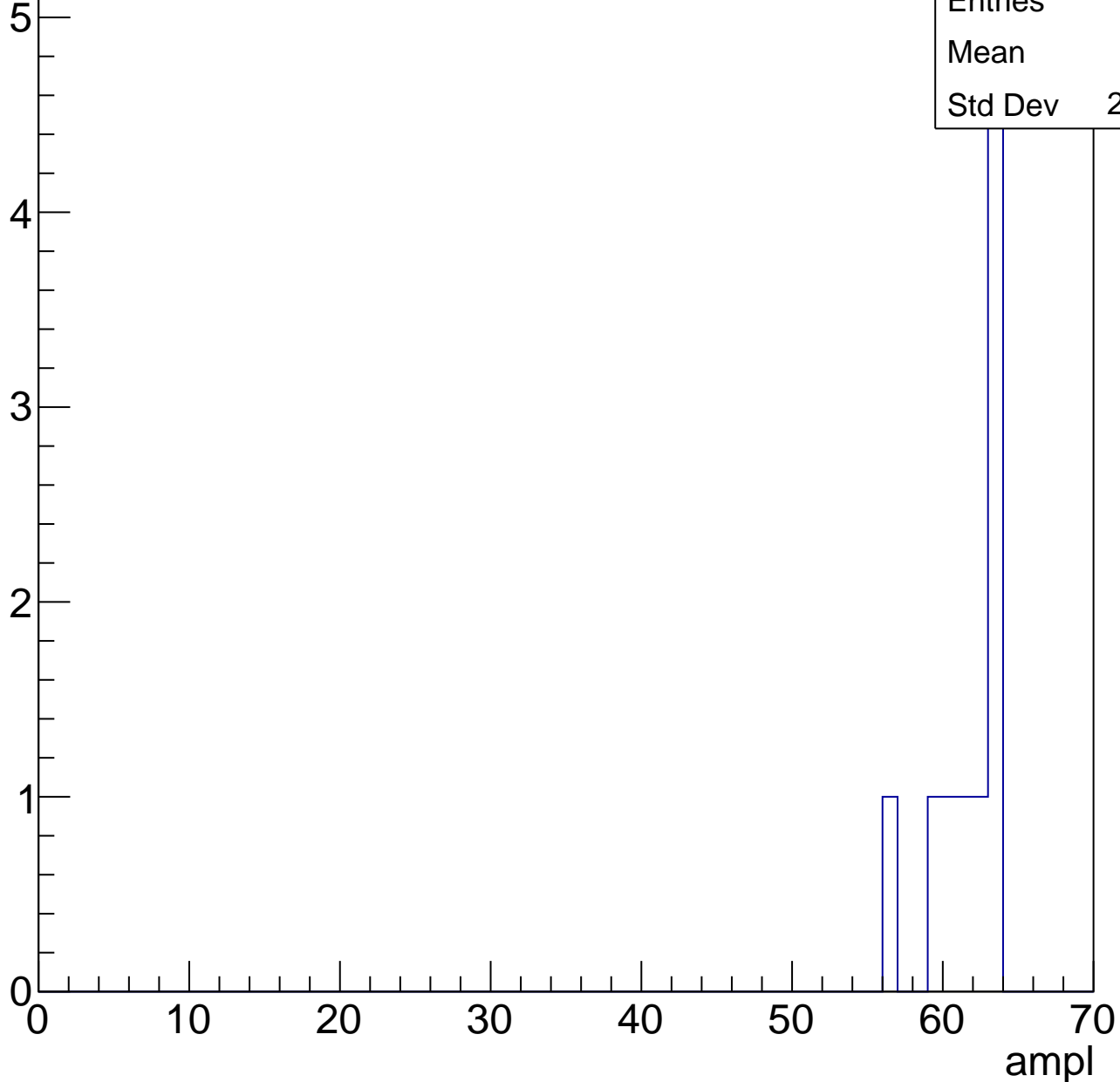
70

B1L100S, U15-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	61.3
Std Dev	2.238



B1L100S, U15-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch69, adc0

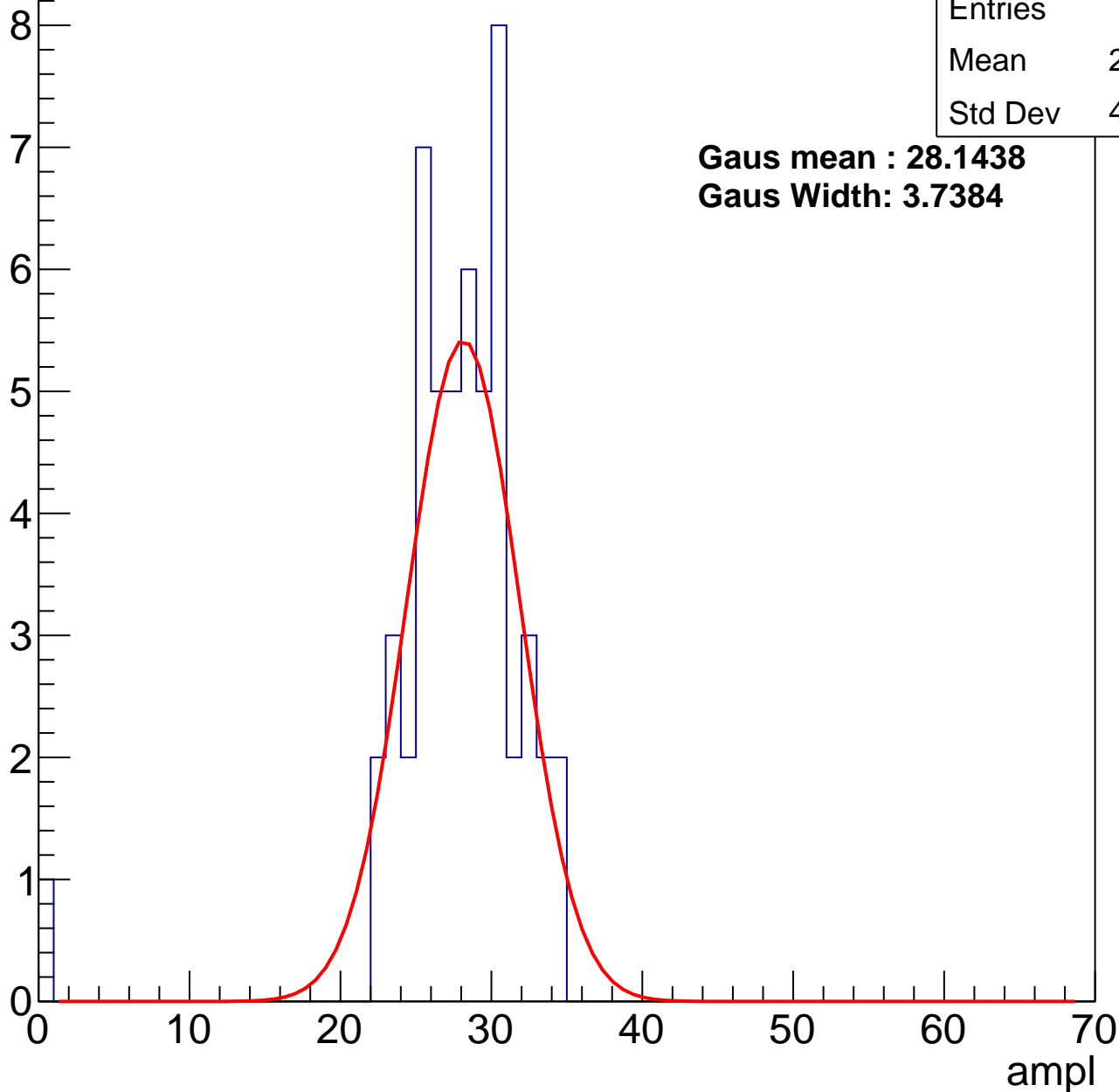
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	27.28
Std Dev	4.854

Gaus mean : 28.1438

Gaus Width: 3.7384



B1L100S, U15-ch69, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	33.61
Std Dev	5.143

Gaus mean : 34.9435

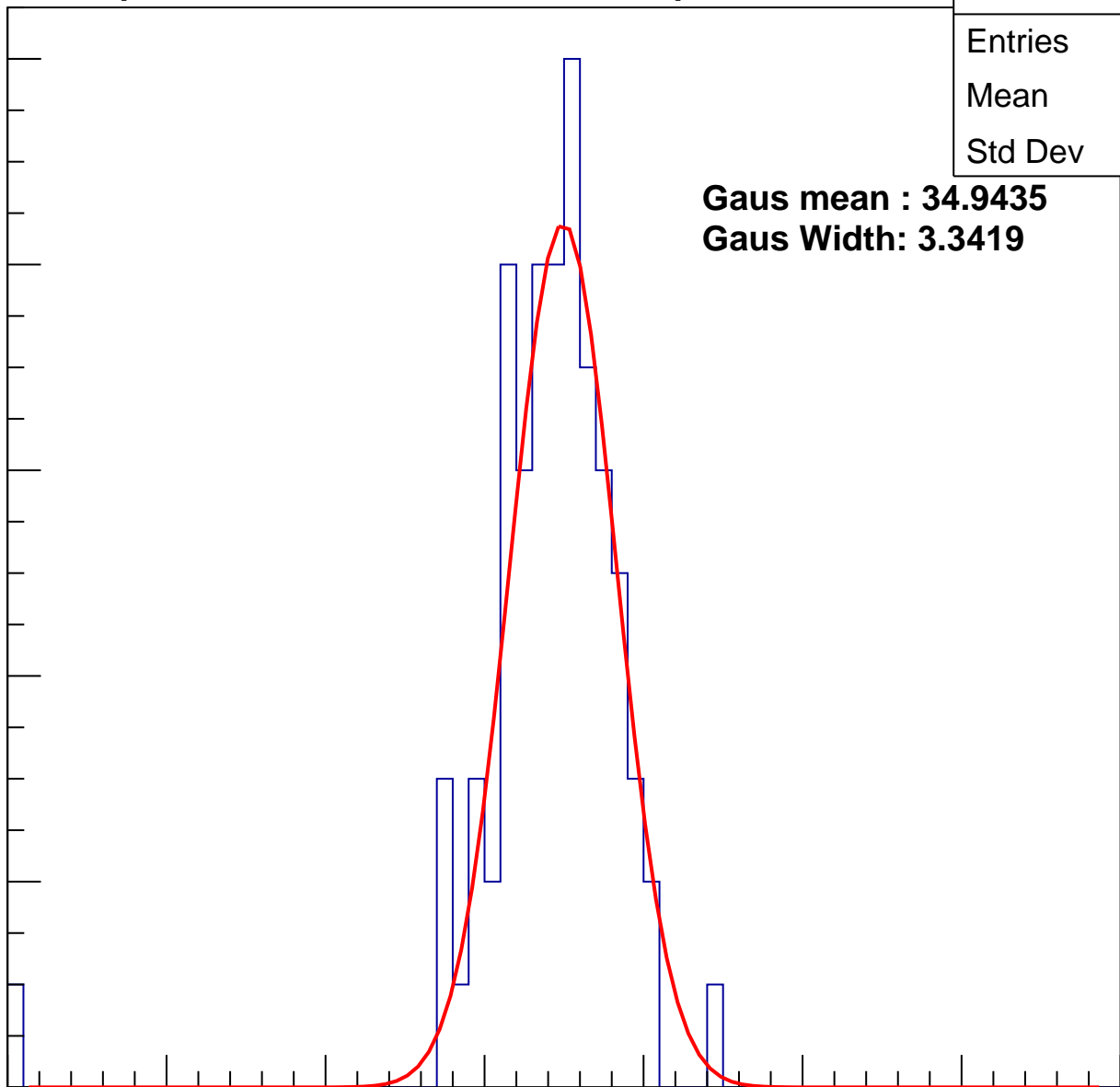
Gaus Width: 3.3419

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch69, adc2

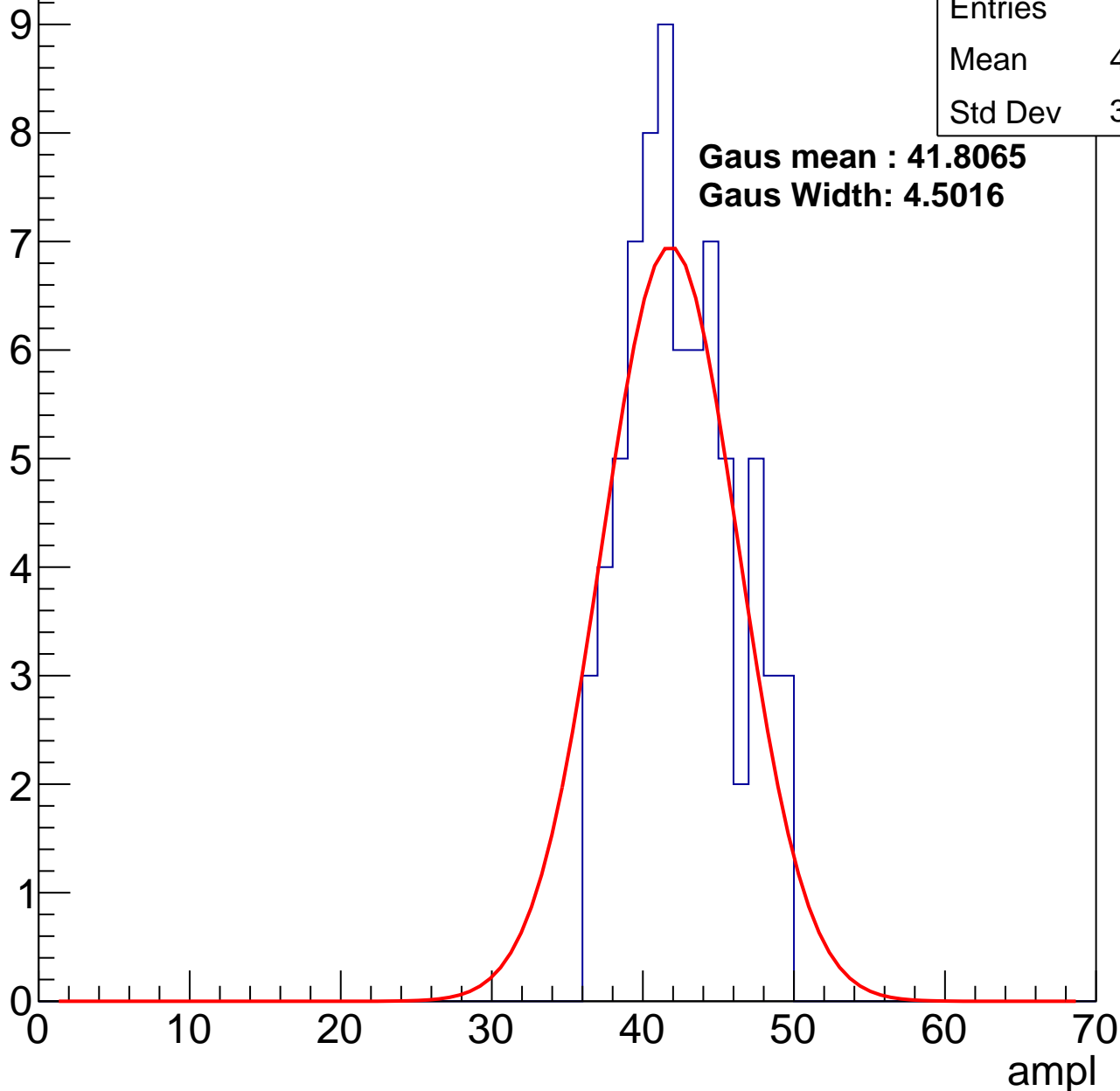
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	42.04
Std Dev	3.478

Gaus mean : 41.8065

Gaus Width: 4.5016

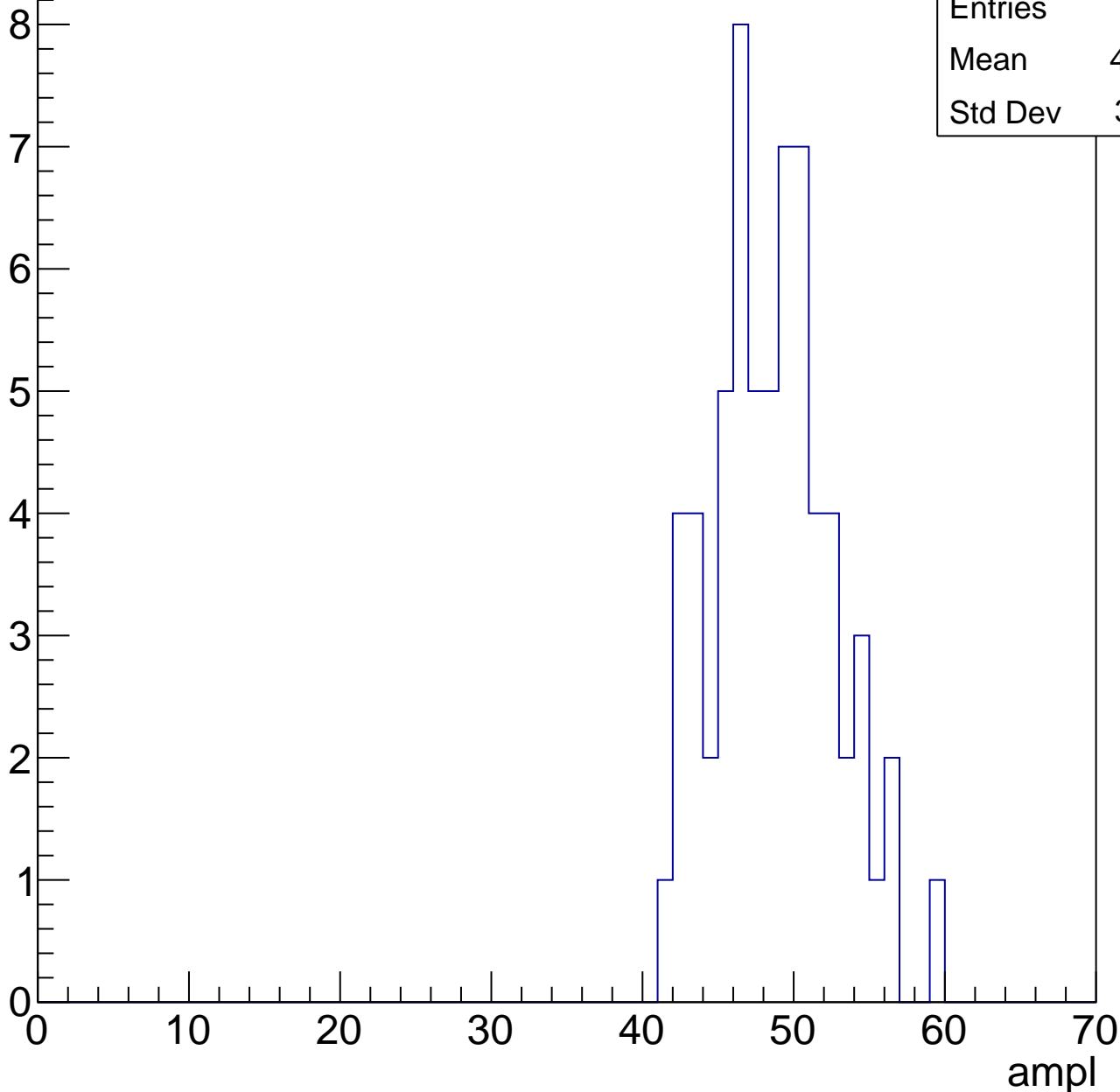


B1L100S, U15-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	48.25
Std Dev	3.911

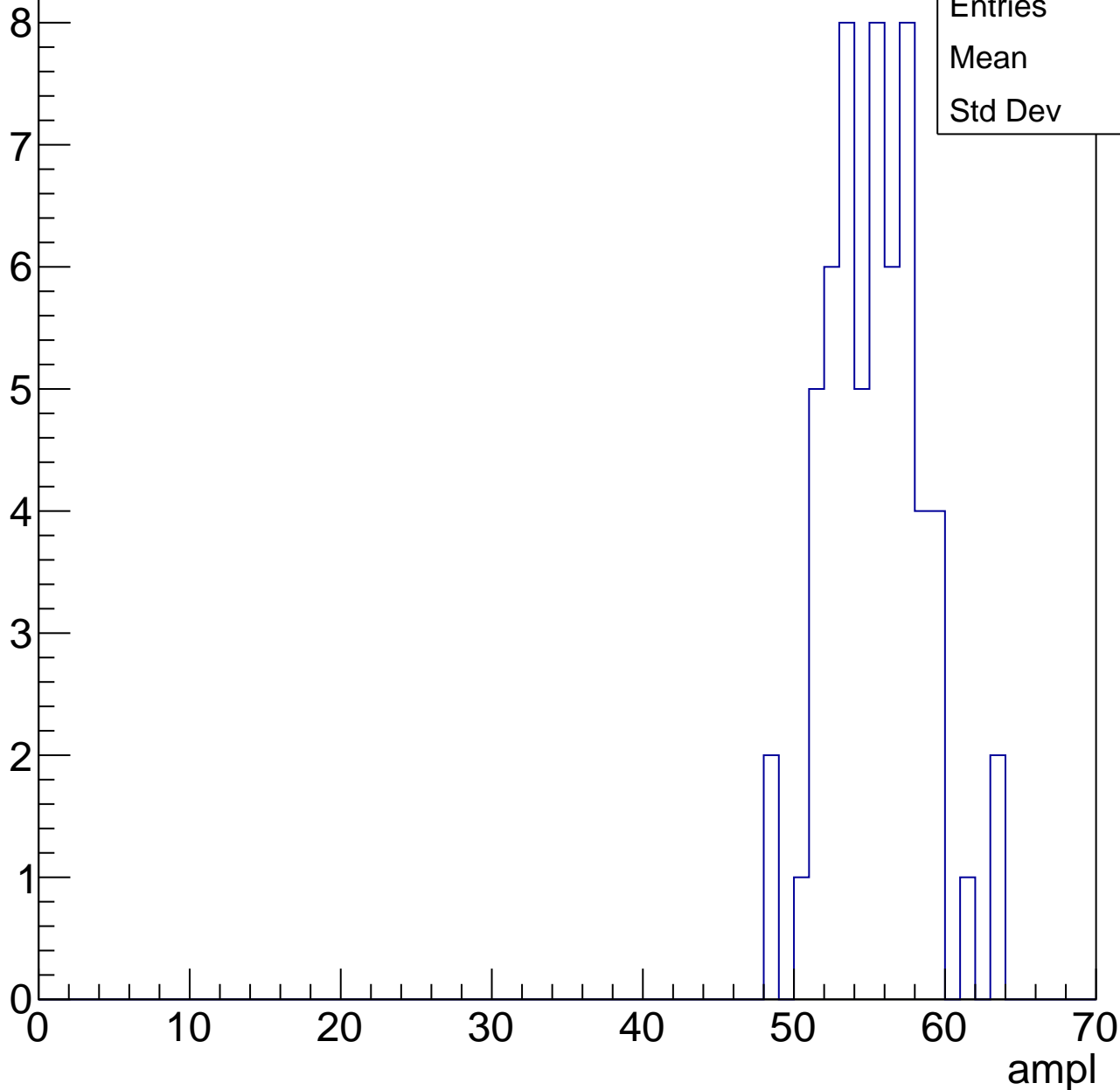


B1L100S, U15-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

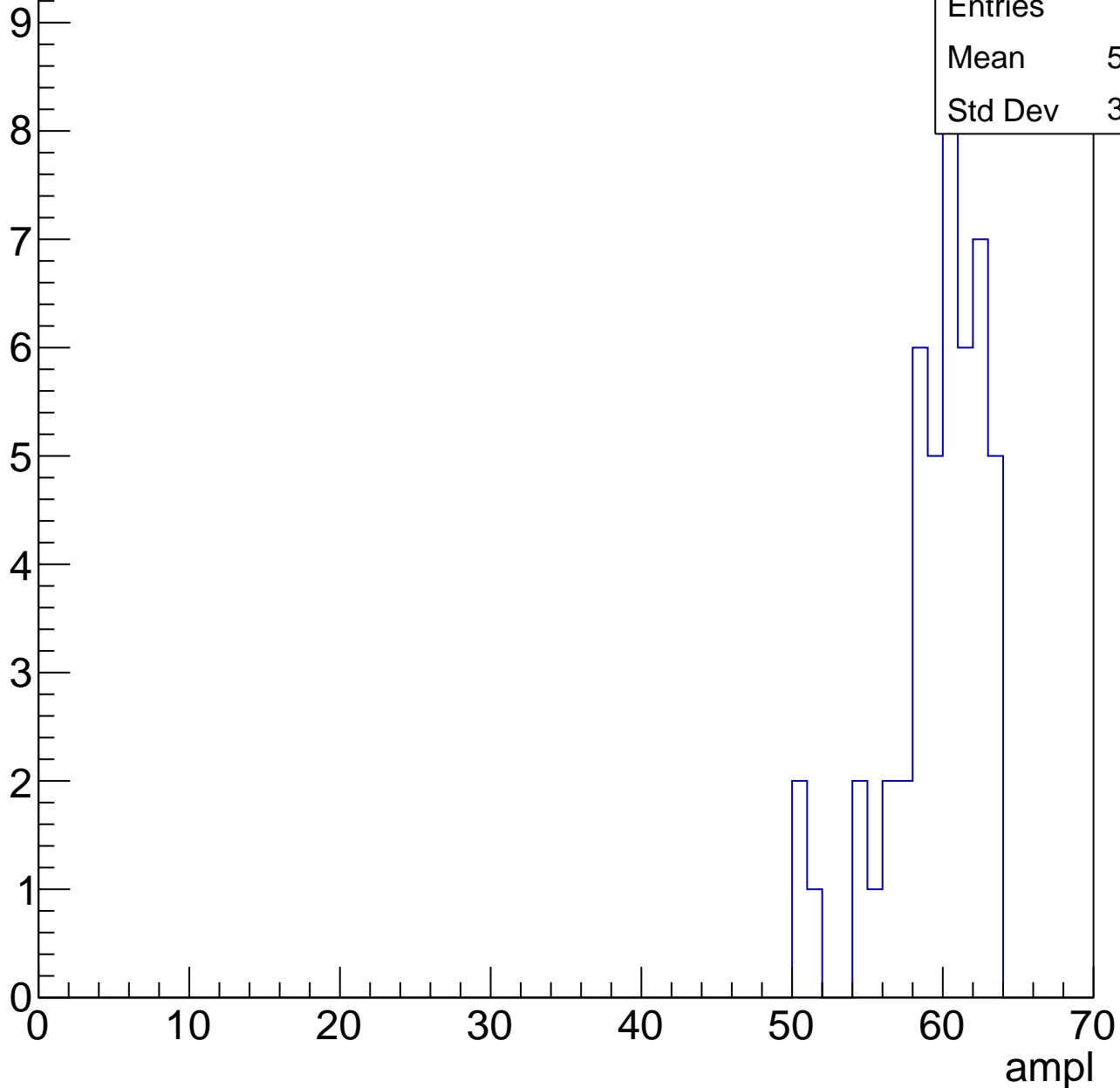
Entries	60
Mean	54.9
Std Dev	3.15



B1L100S, U15-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch69, adc6

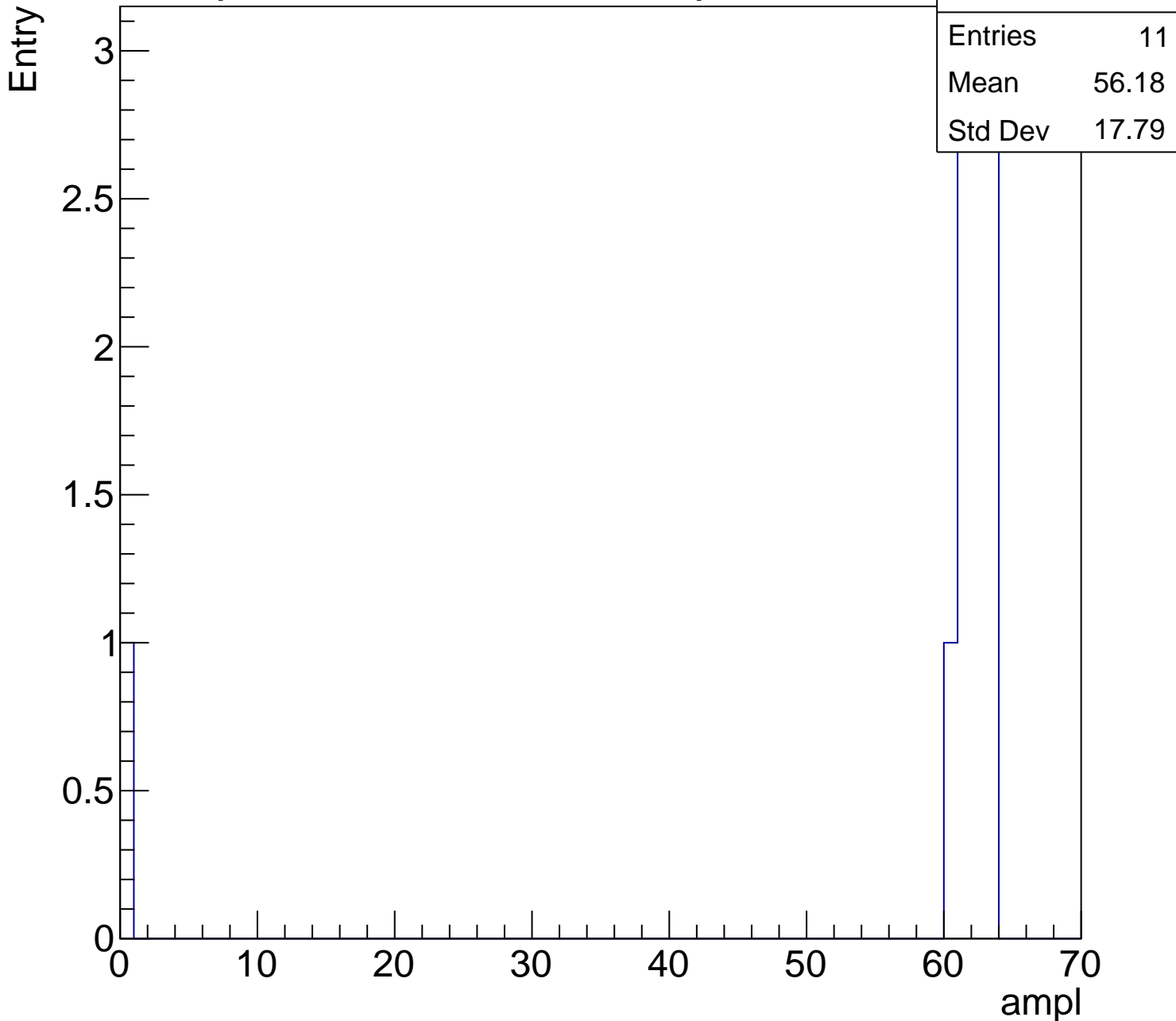
calib_packv5_042523_0143.root, FC#4, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	56.18
Std Dev	17.79

ampl



B1L100S, U15-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch70, adc0

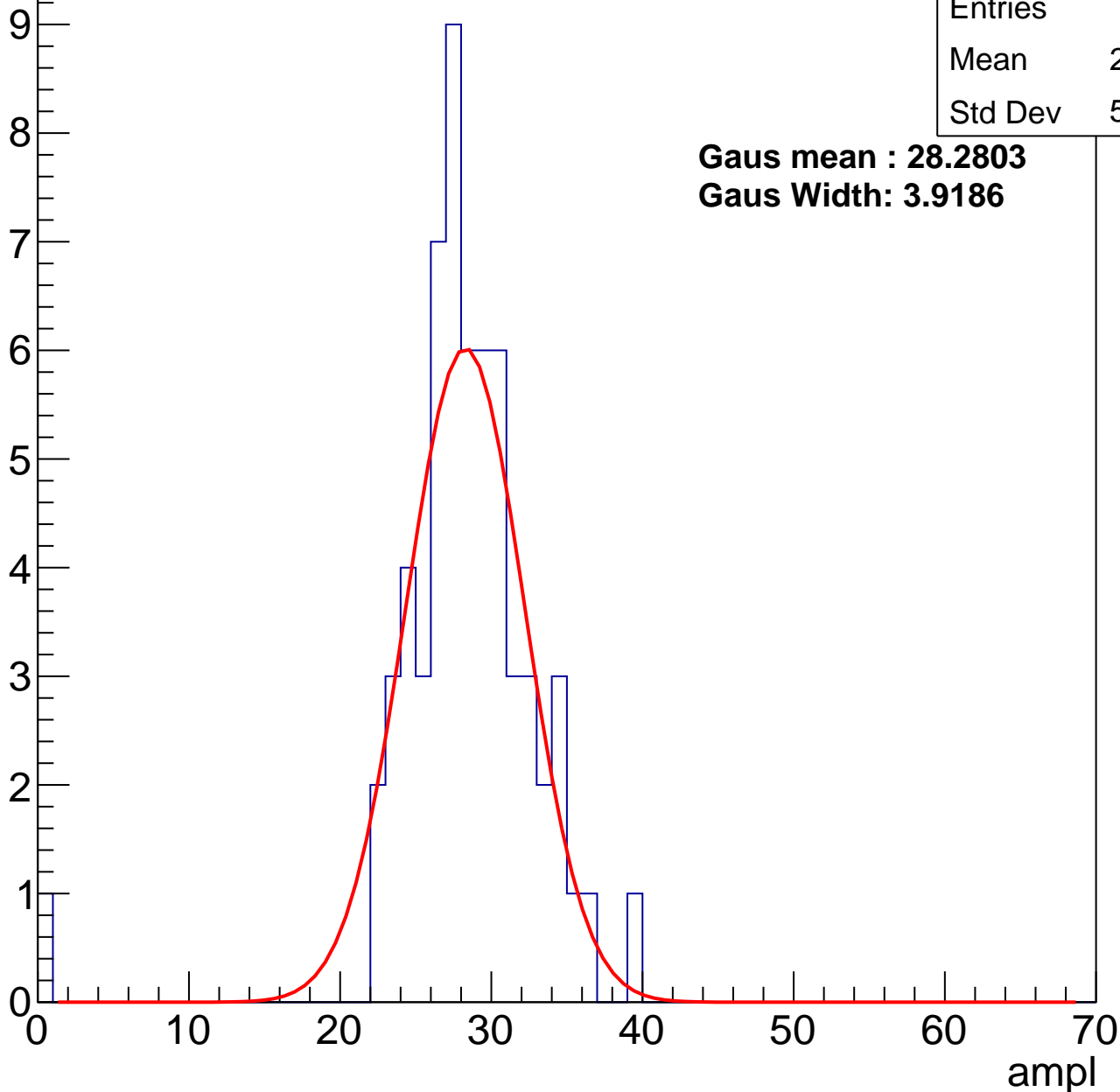
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	27.84
Std Dev	5.048

Gaus mean : 28.2803

Gaus Width: 3.9186



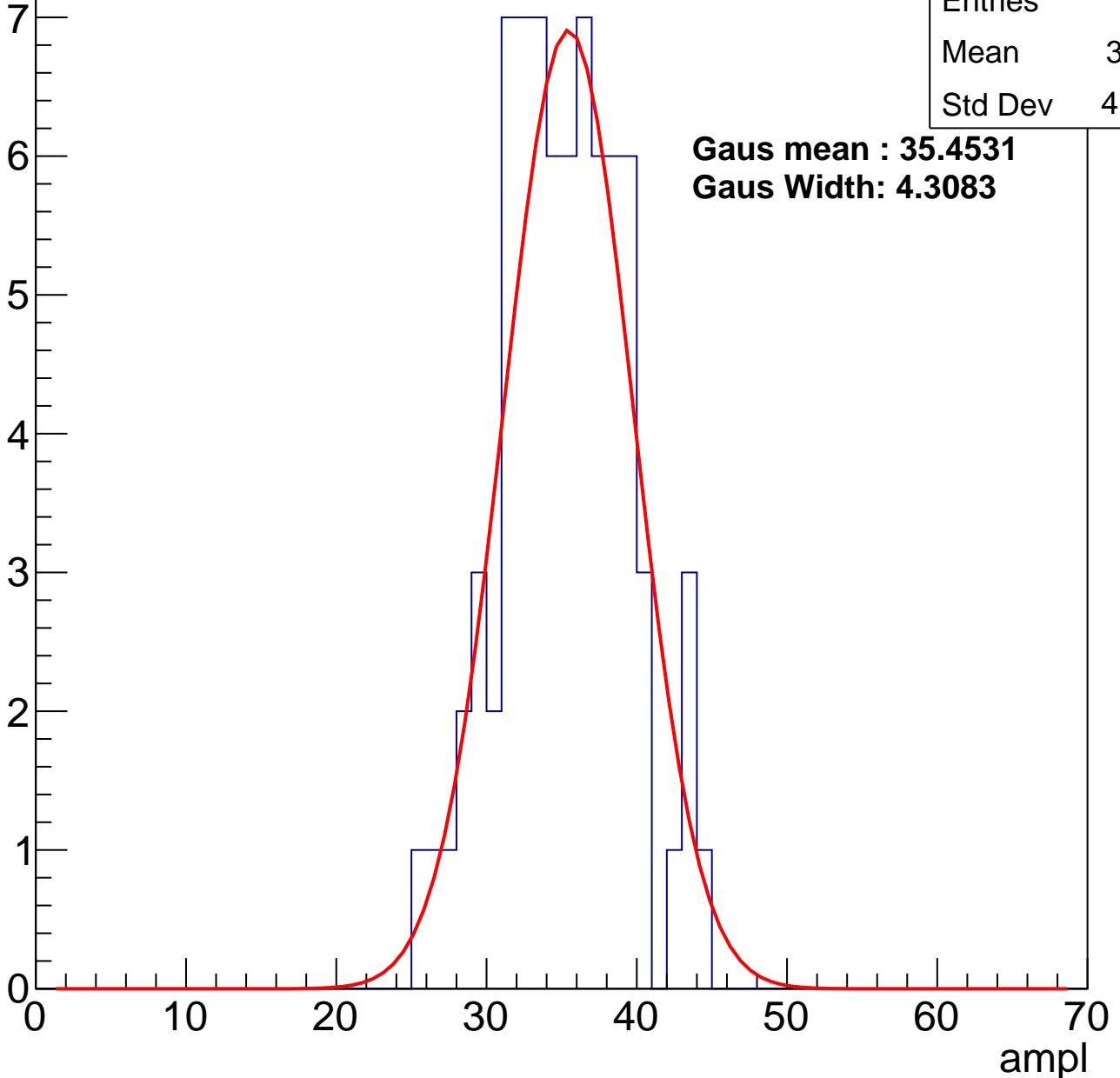
B1L100S, U15-ch70, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	34.71
Std Dev	4.107

Gaus mean : 35.4531
Gaus Width: 4.3083



B1L100S, U15-ch70, adc2

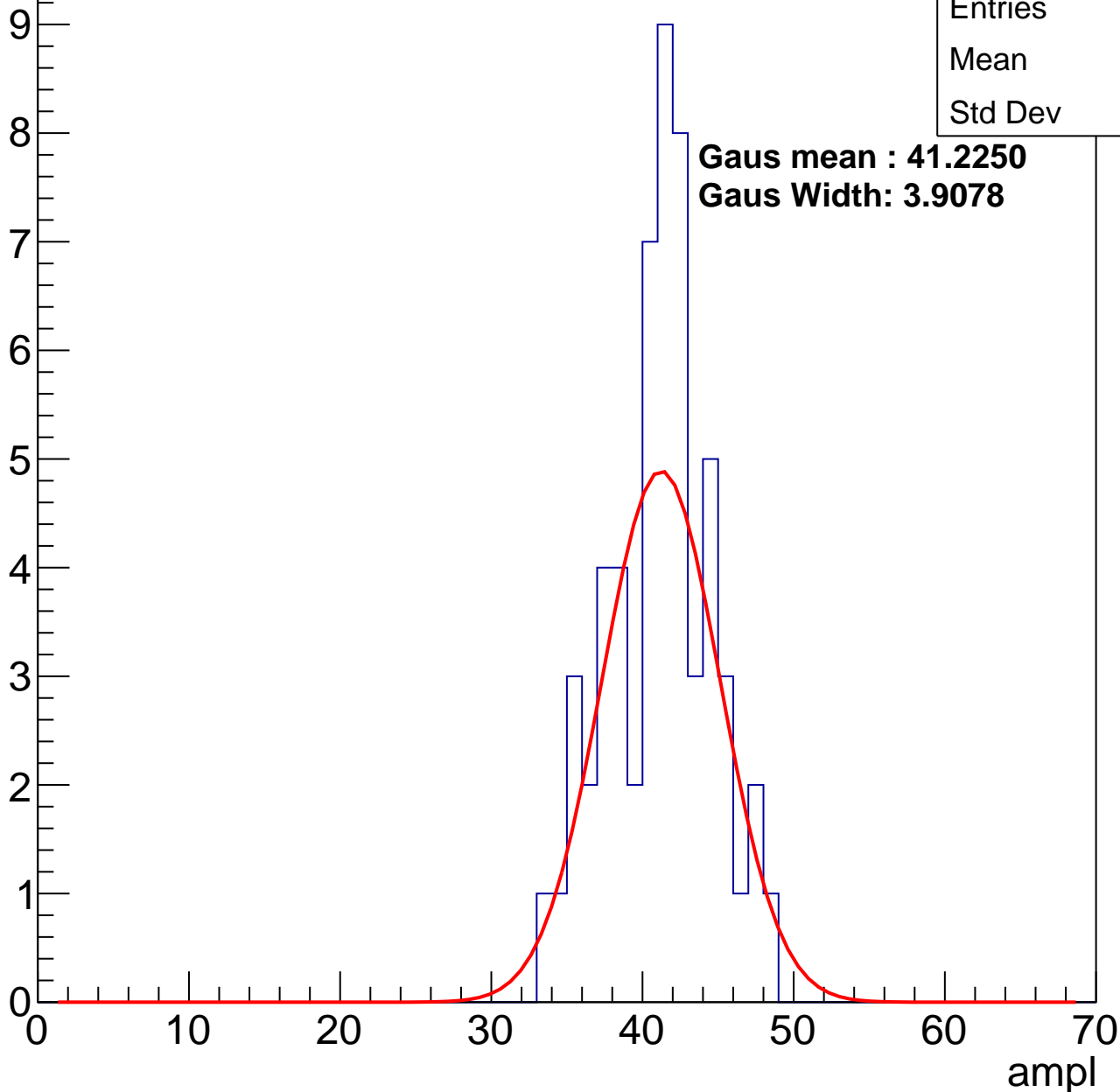
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	40.7
Std Dev	3.38

Gaus mean : 41.2250

Gaus Width: 3.9078

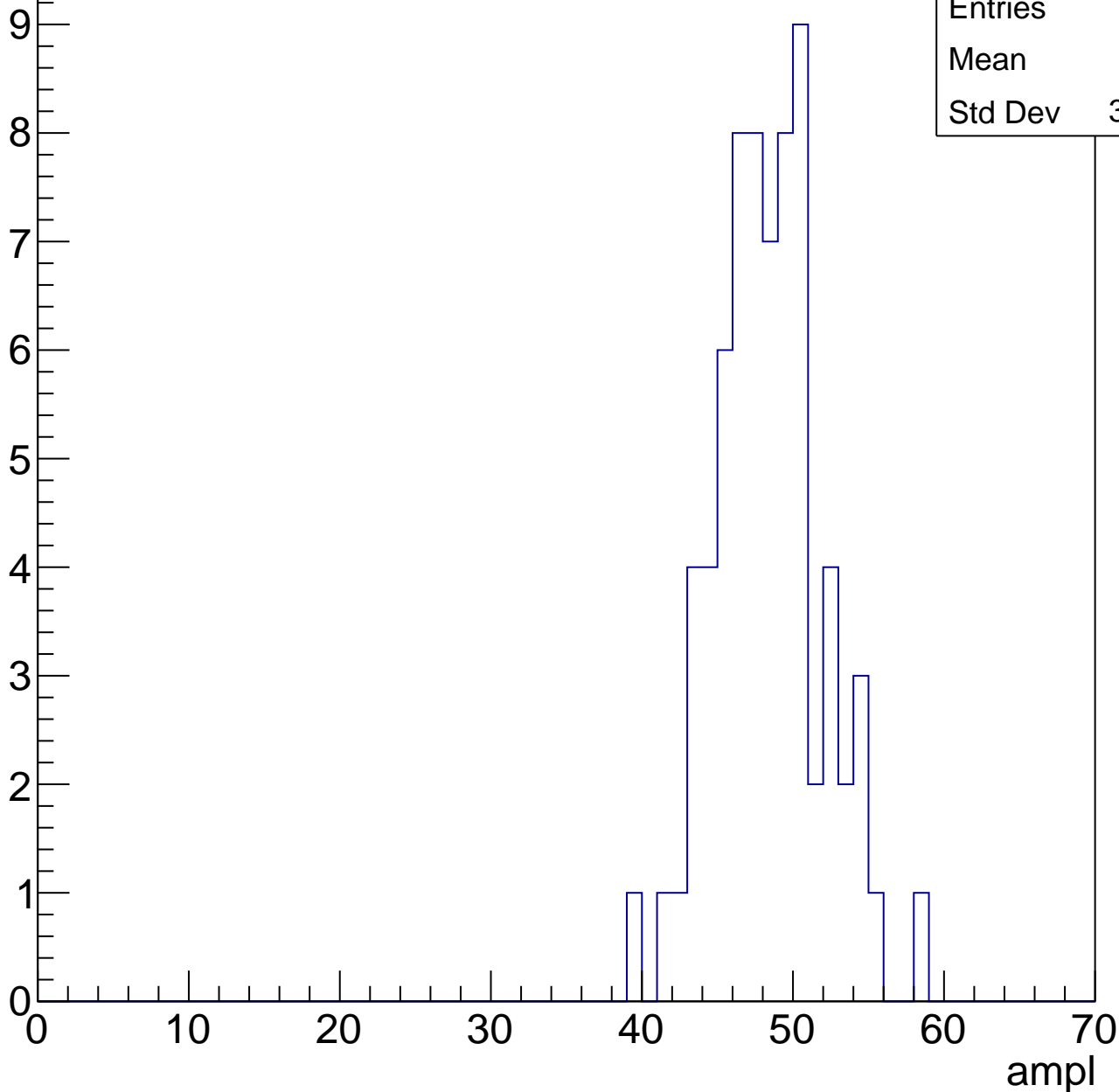


B1L100S, U15-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	47.9
Std Dev	3.498

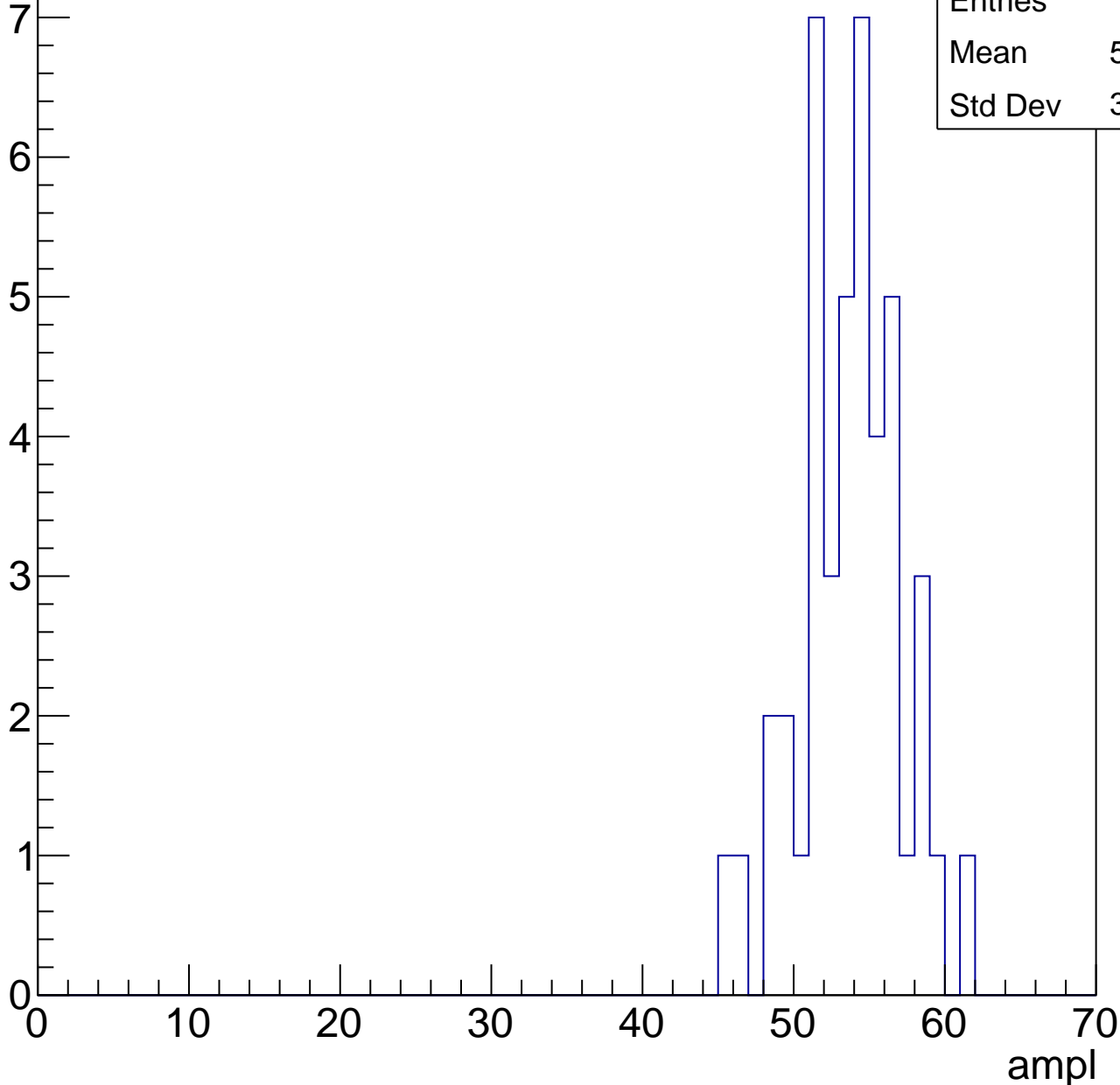


B1L100S, U15-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

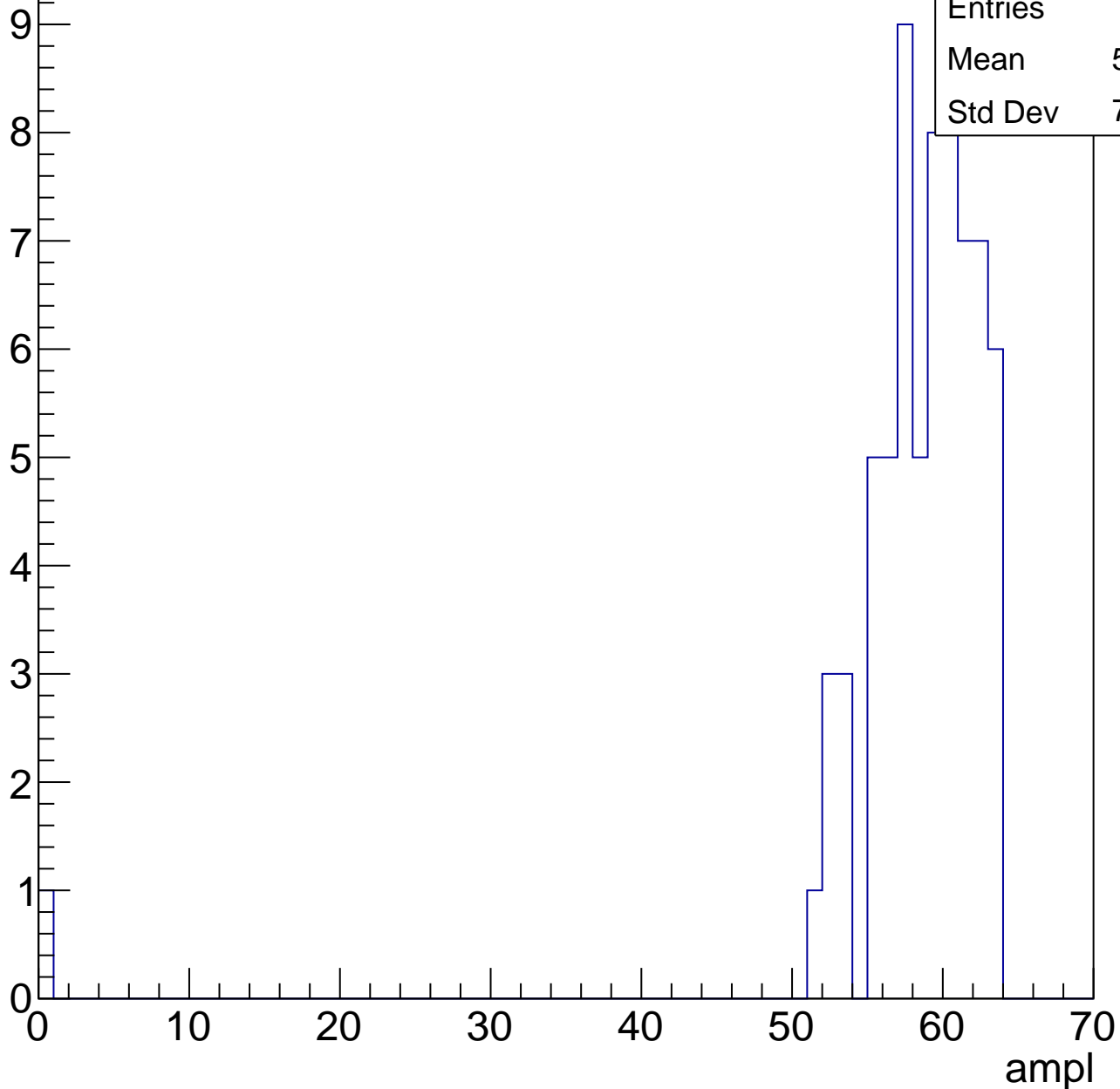
Entries	44
Mean	53.23
Std Dev	3.343



B1L100S, U15-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

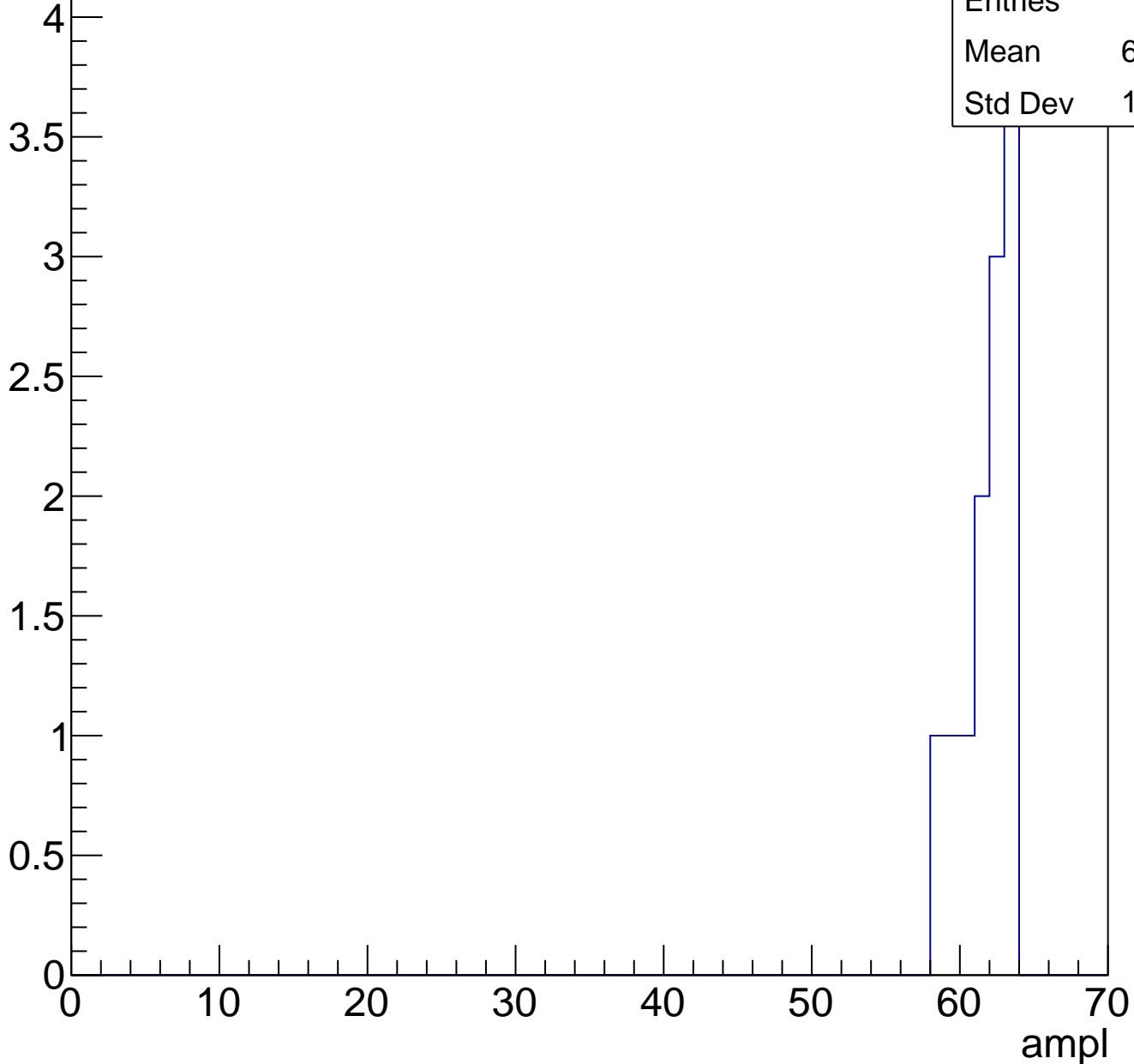
Entry



B1L100S, U15-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch71, adc0

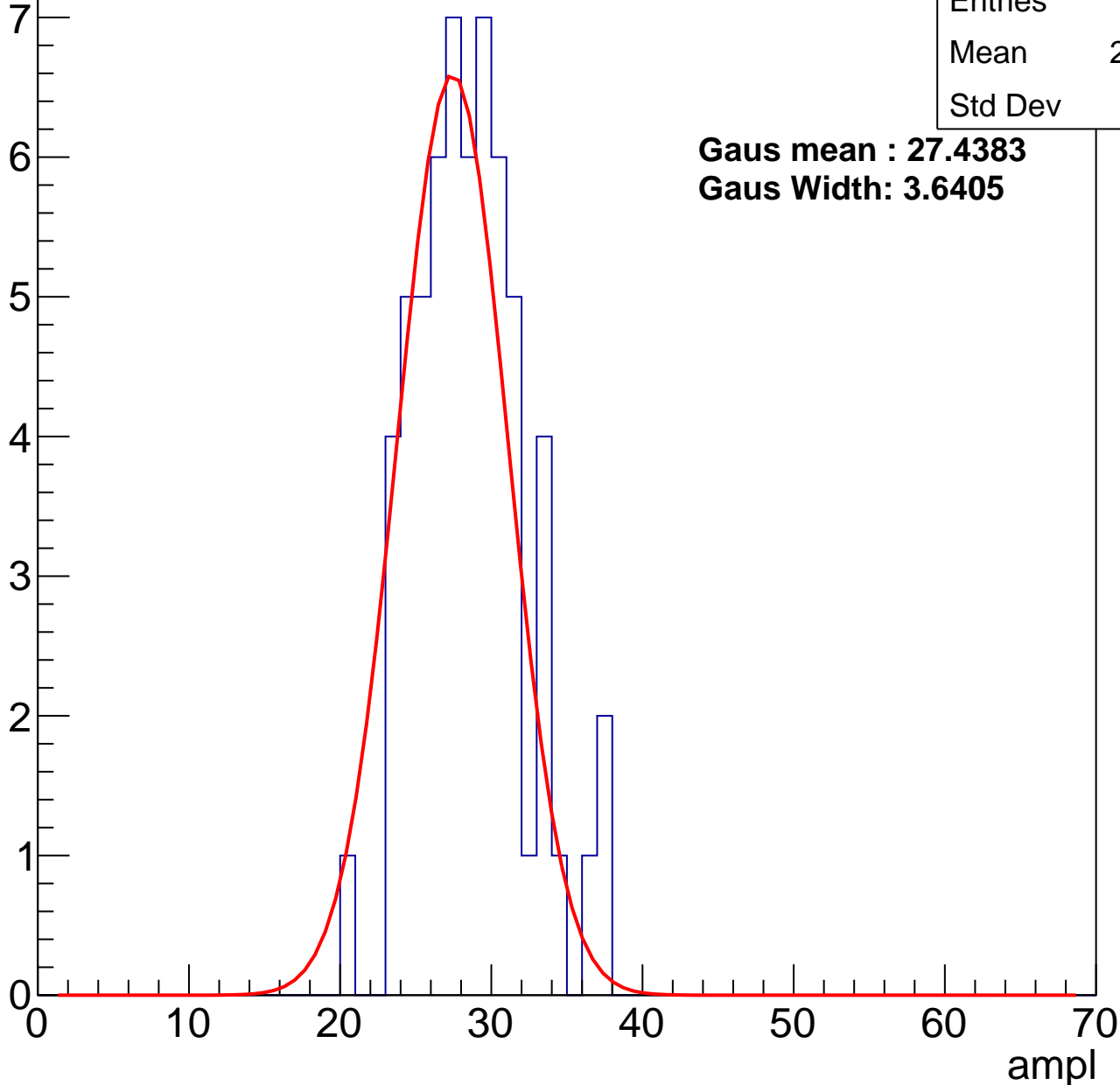
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	28.13
Std Dev	3.56

Gaus mean : 27.4383

Gaus Width: 3.6405



B1L100S, U15-ch71, adc1

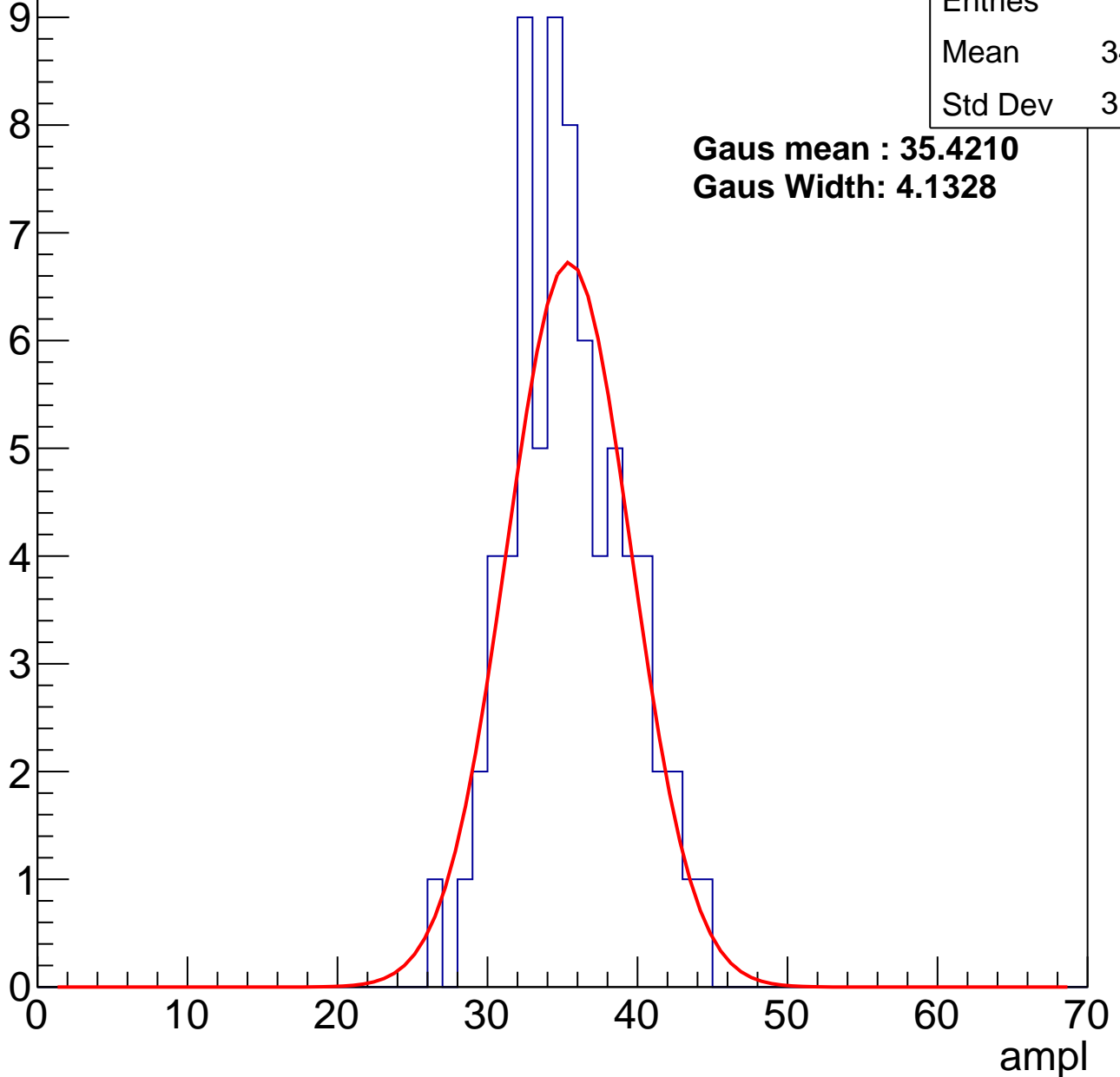
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	34.97
Std Dev	3.767

Gaus mean : 35.4210

Gaus Width: 4.1328



B1L100S, U15-ch71, adc2

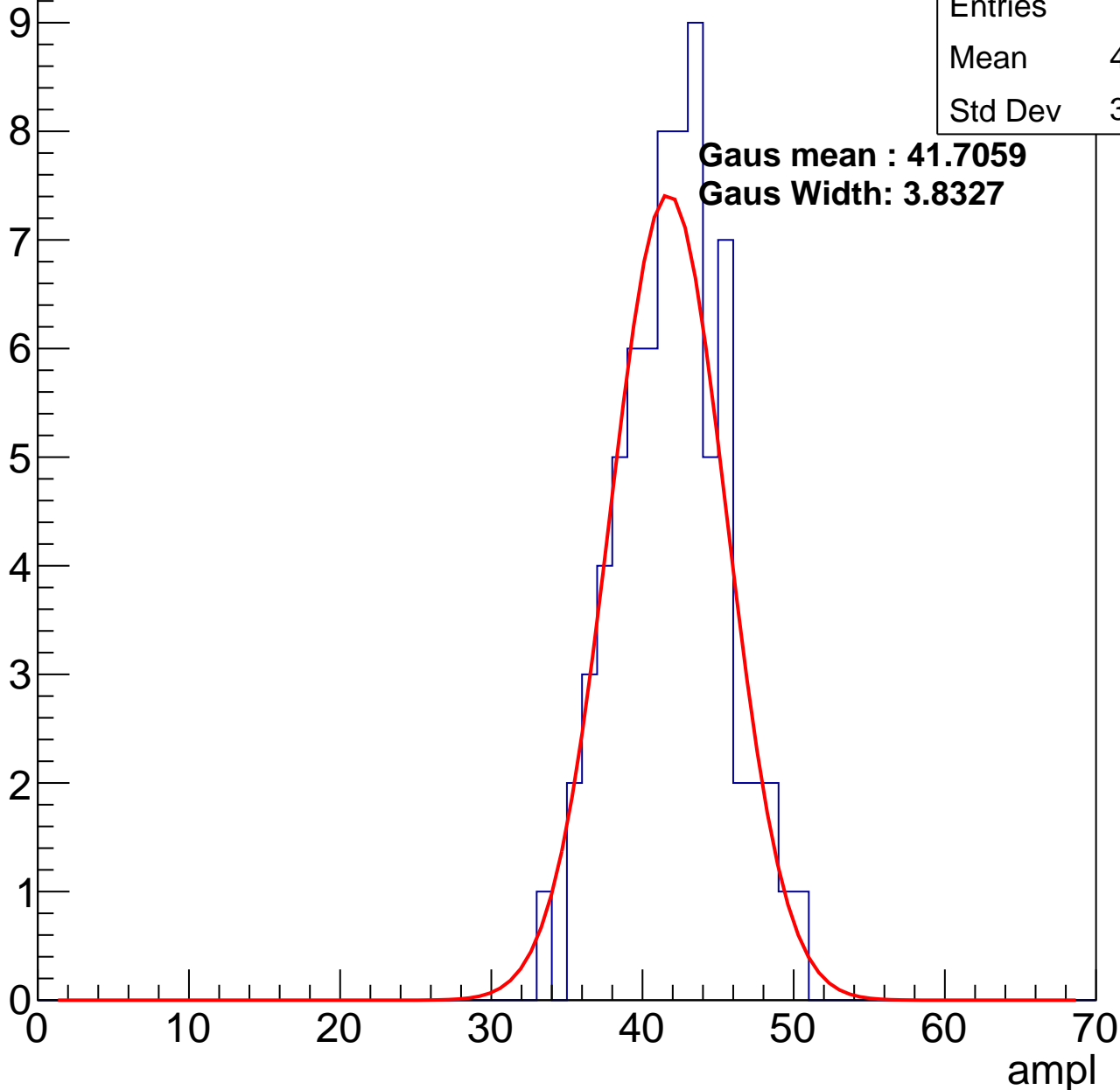
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	41.53
Std Dev	3.528

Gaus mean : 41.7059

Gaus Width: 3.8327

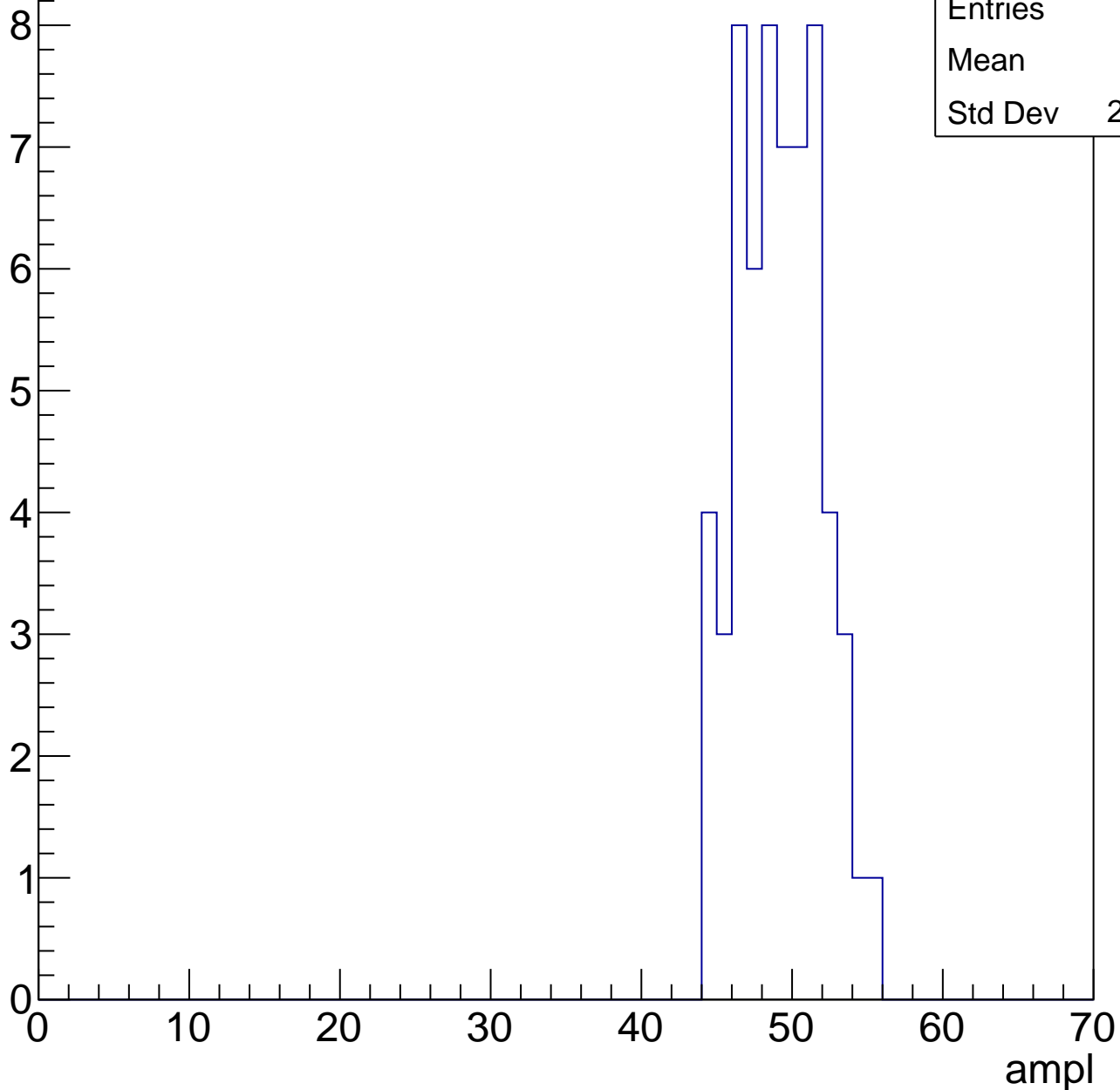


B1L100S, U15-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	48.7
Std Dev	2.679

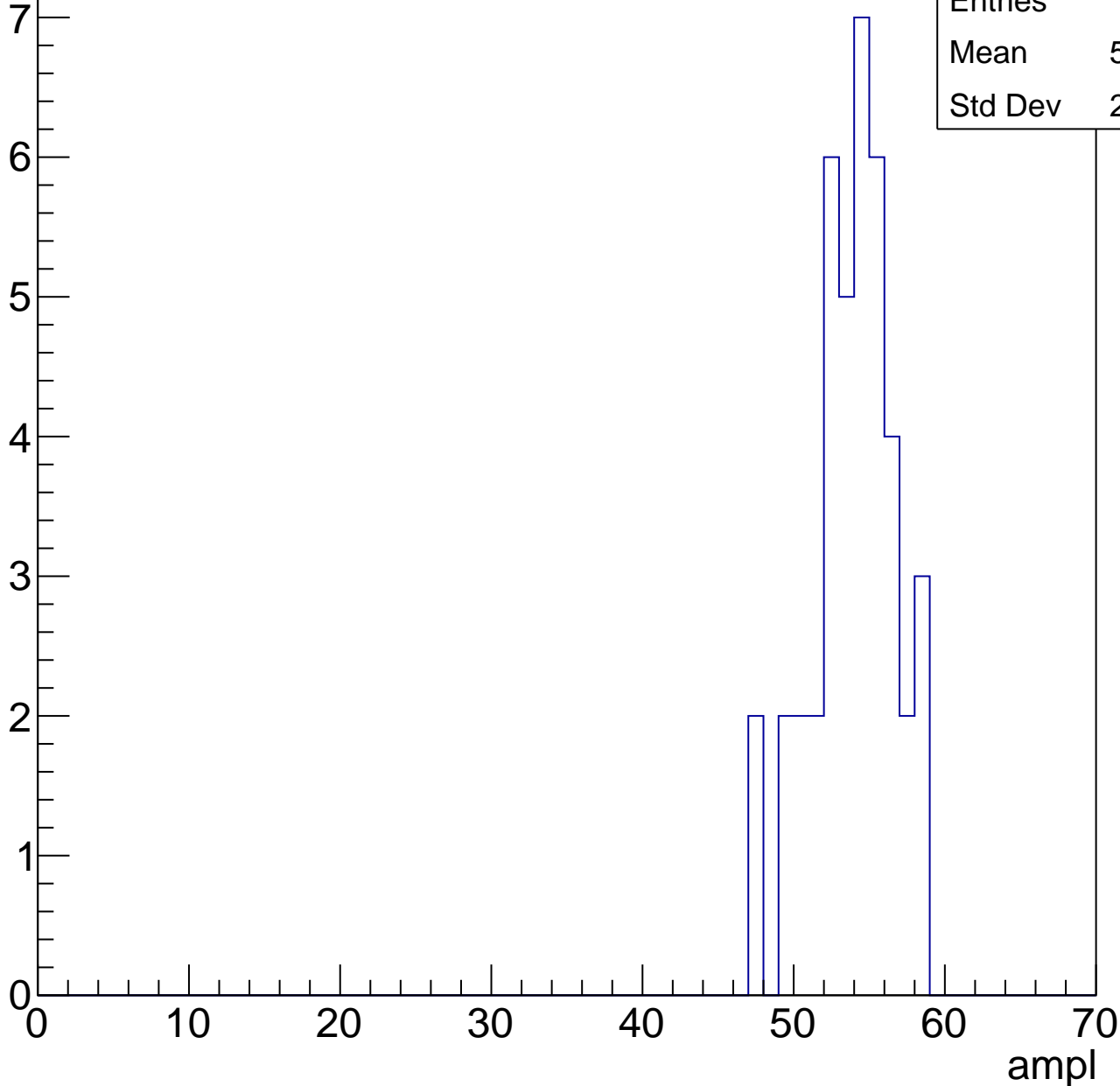


B1L100S, U15-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

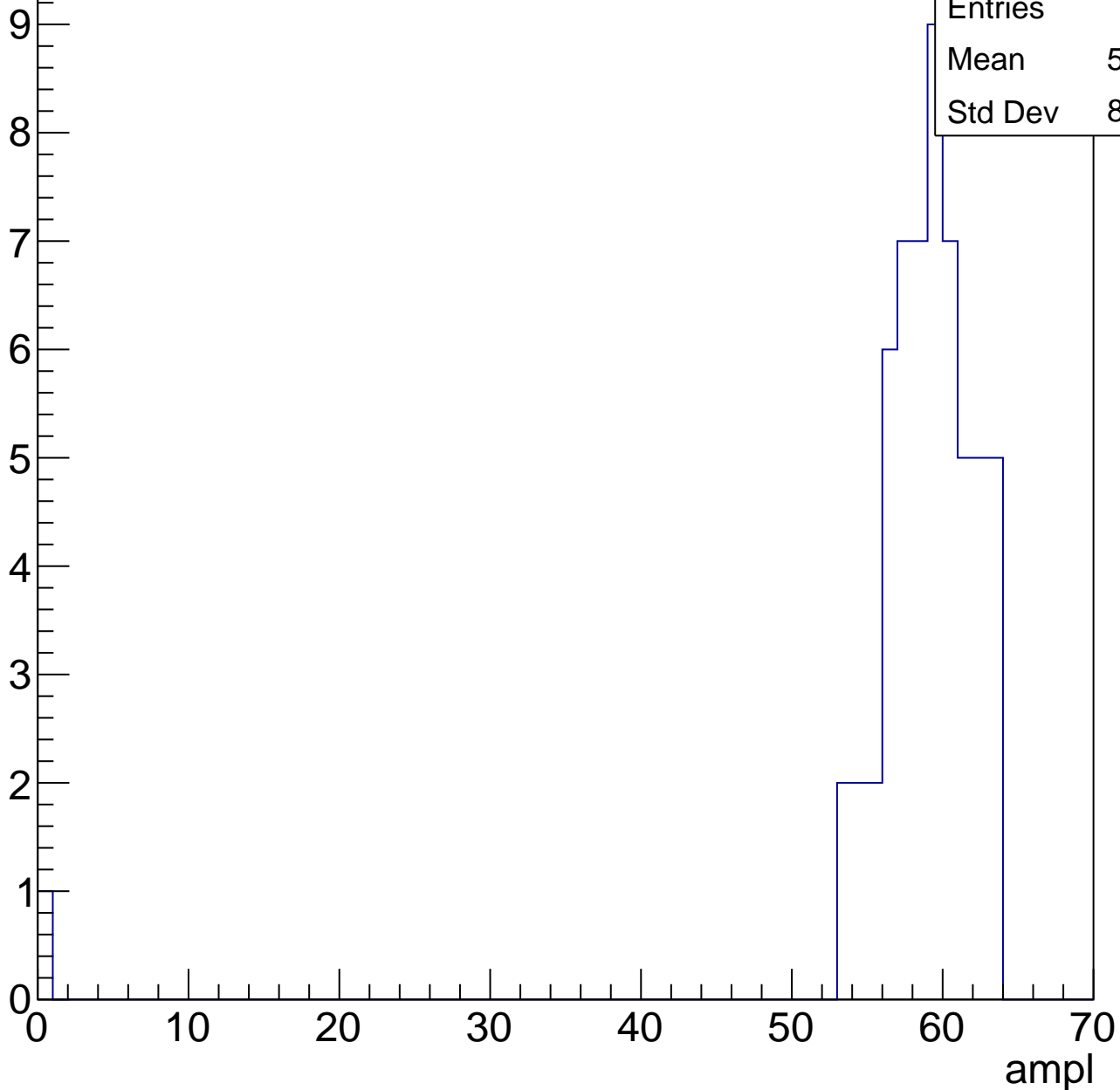
Entries	41
Mean	53.44
Std Dev	2.714



B1L100S, U15-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

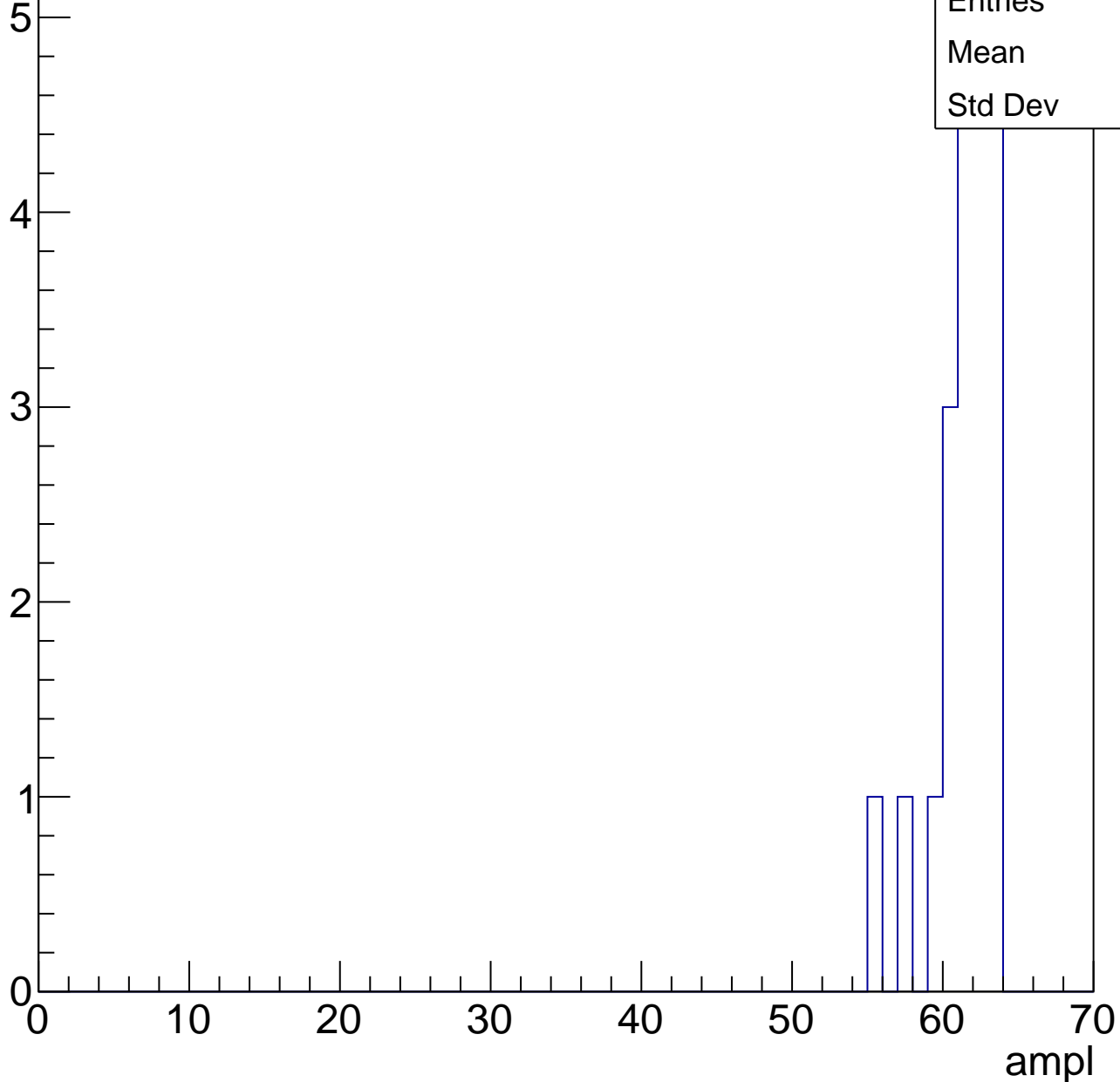
Entry



B1L100S, U15-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch72, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	27.88
Std Dev	6.761

Gaus mean : 29.9142

Gaus Width: 4.1033

Entry

10
8
6
4
2
0

ampl

0

10

20

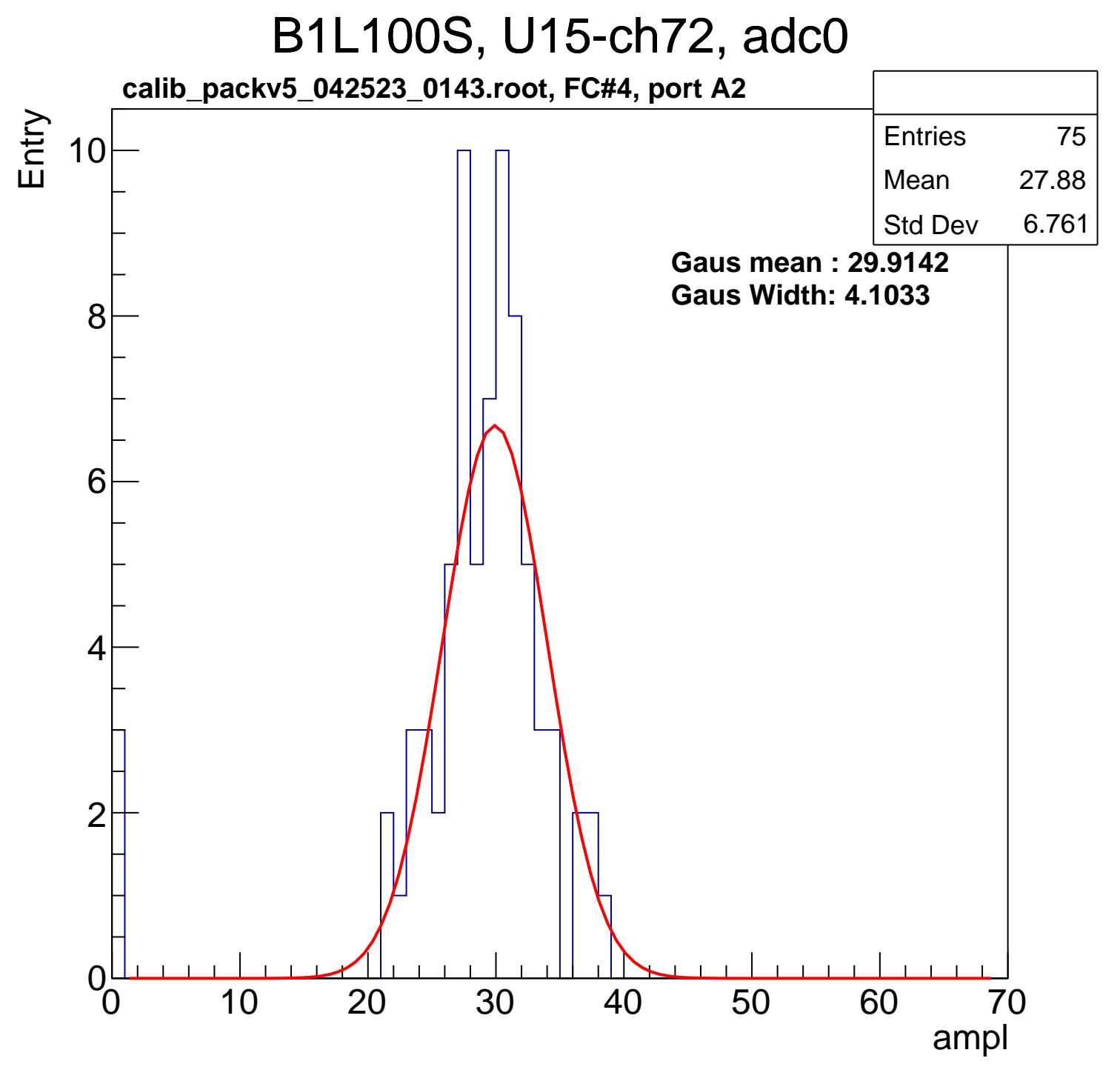
30

40

50

60

70



B1L100S, U15-ch72, adc1

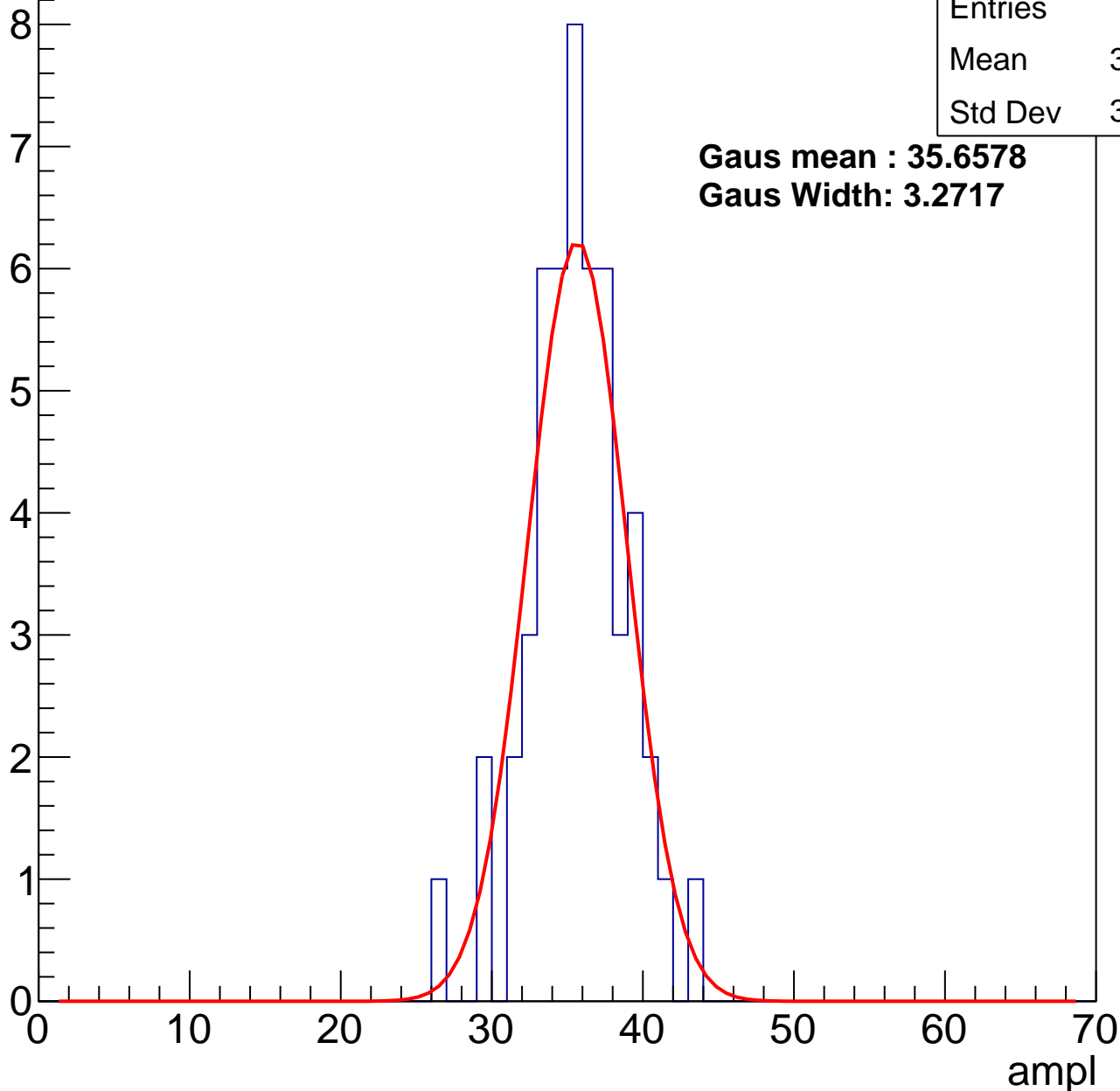
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	35.22
Std Dev	3.164

Gaus mean : 35.6578

Gaus Width: 3.2717



B1L100S, U15-ch72, adc2

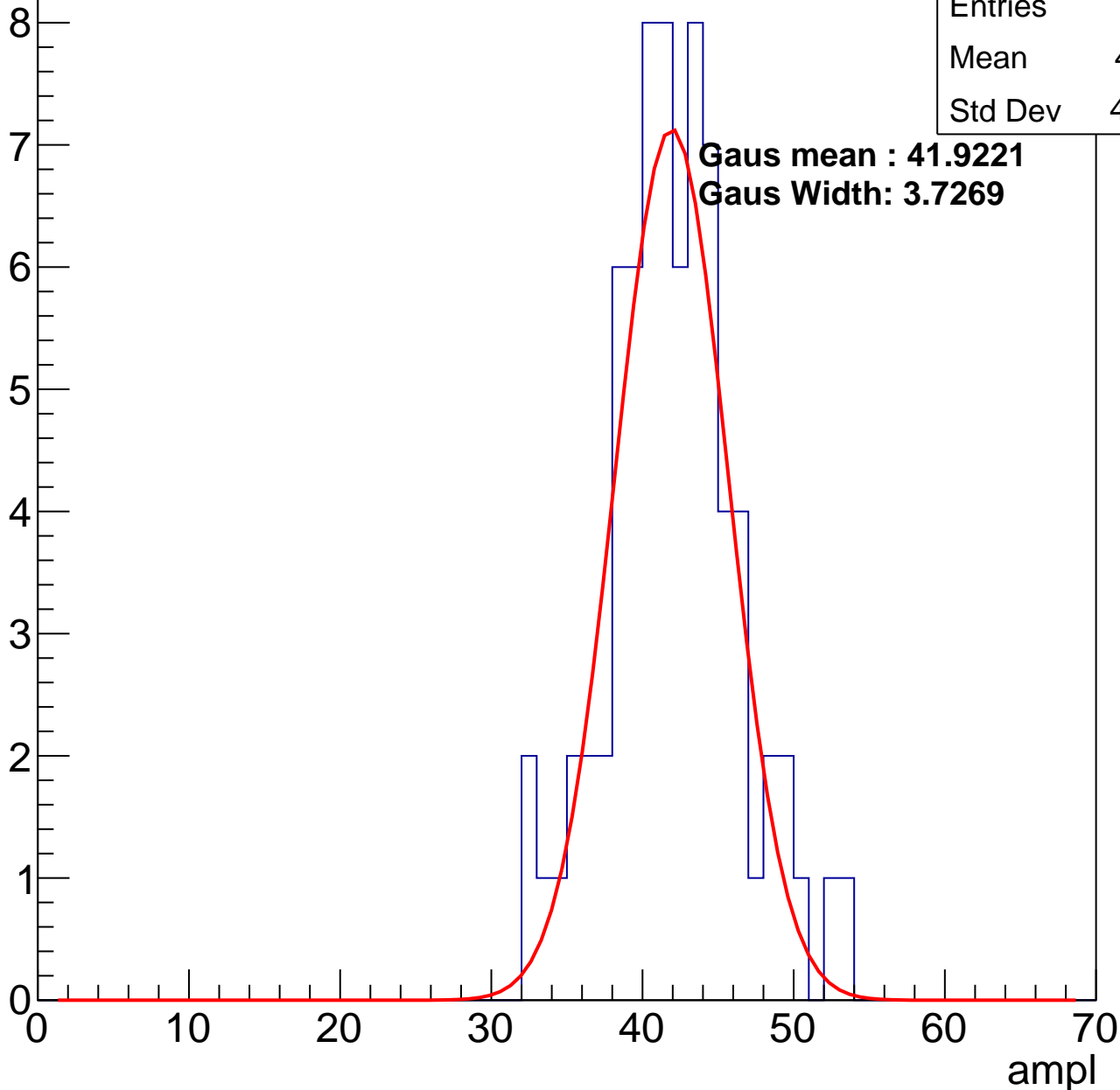
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	41.61
Std Dev	4.258

Gaus mean : 41.9221

Gaus Width: 3.7269

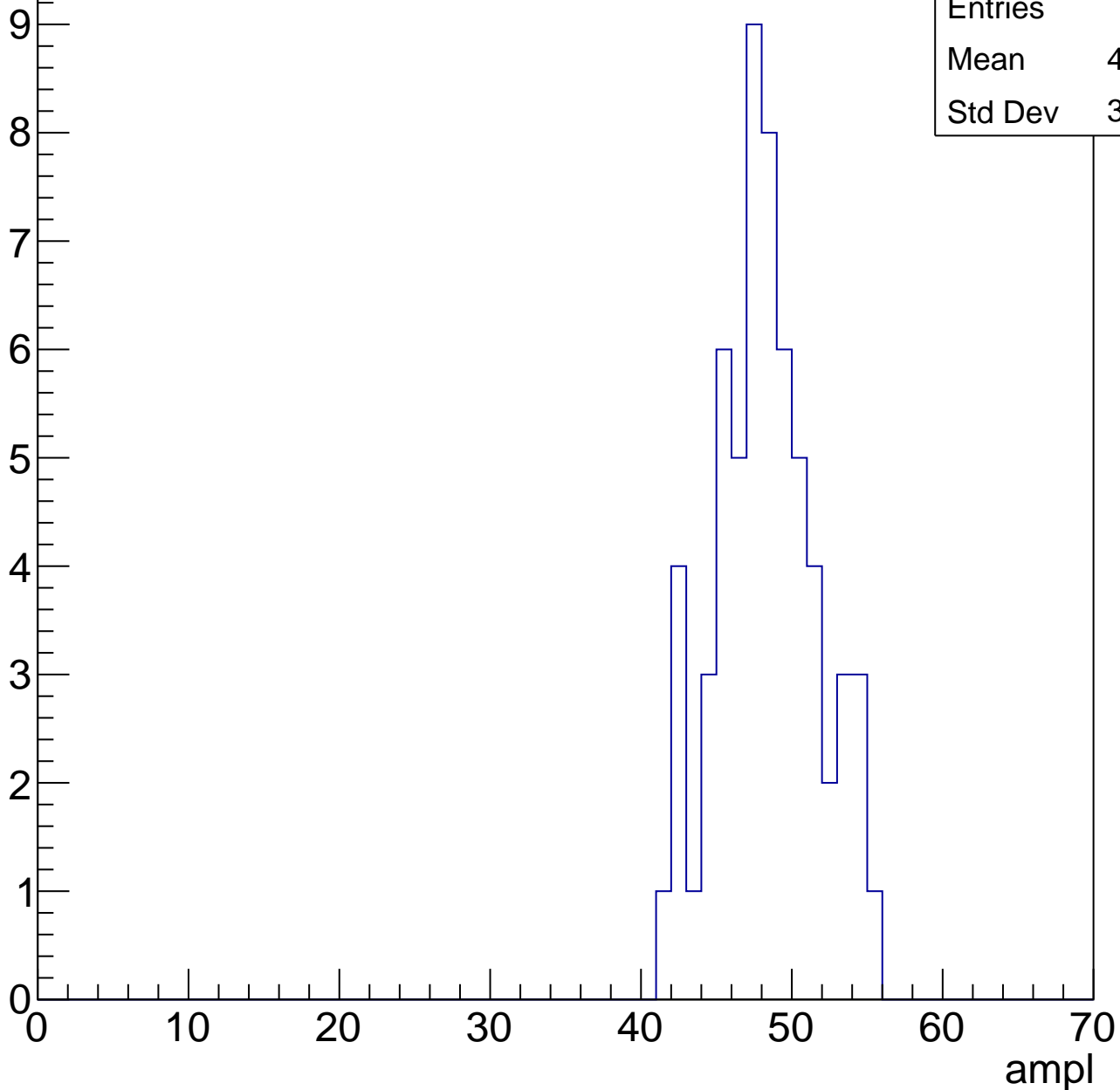


B1L100S, U15-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	47.85
Std Dev	3.323

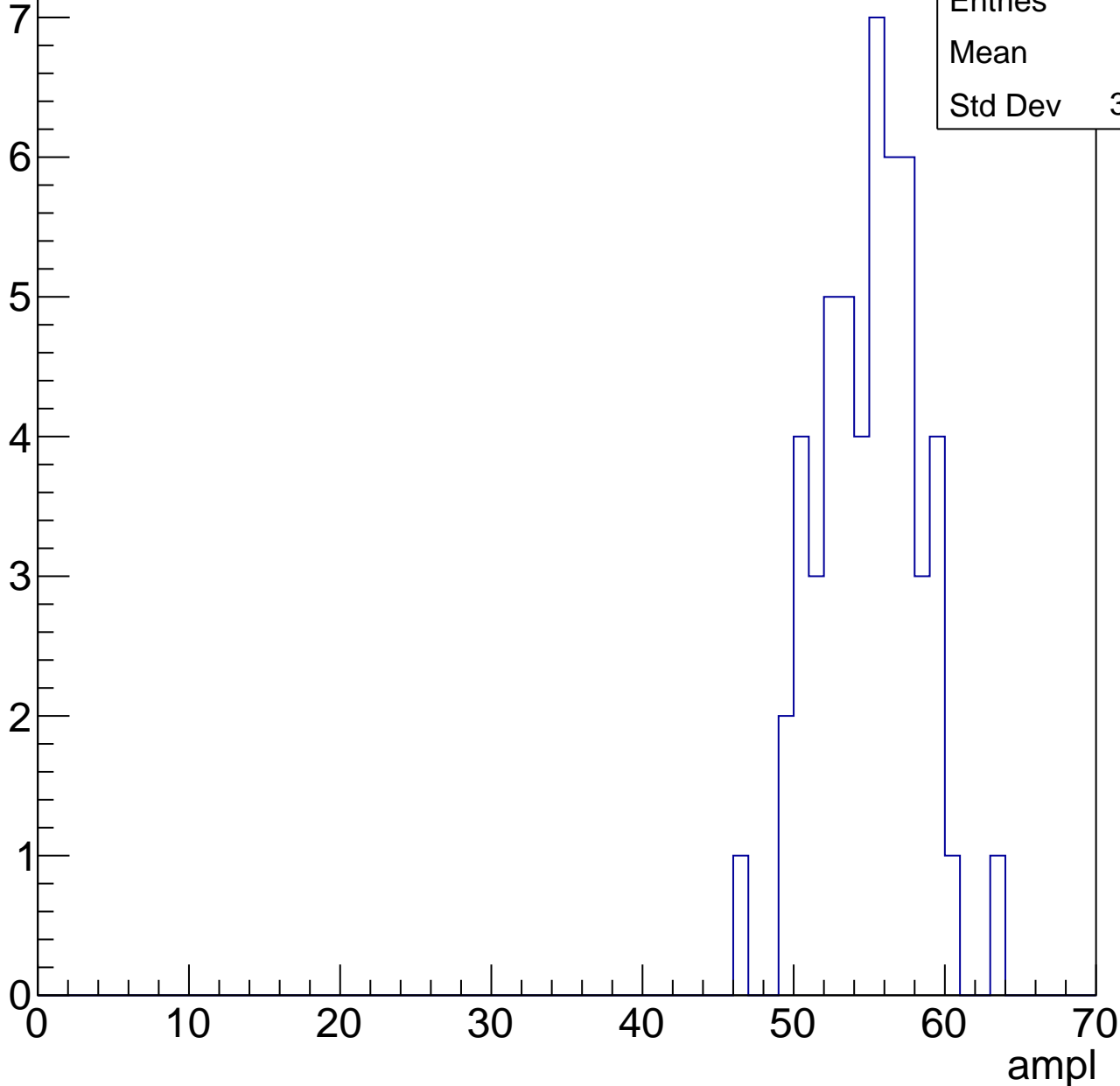


B1L100S, U15-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	54.5
Std Dev	3.302

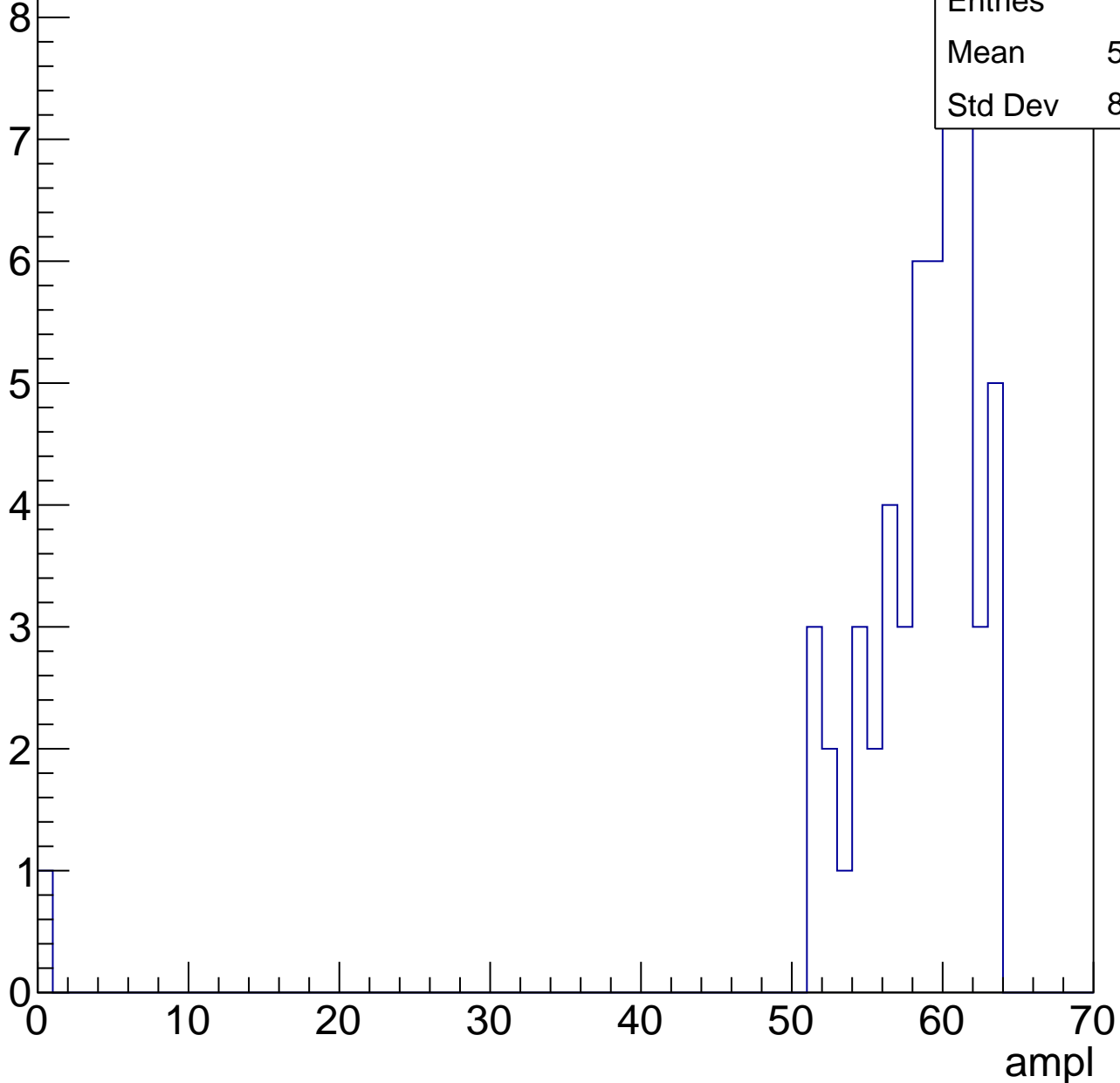


B1L100S, U15-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	57.24
Std Dev	8.468

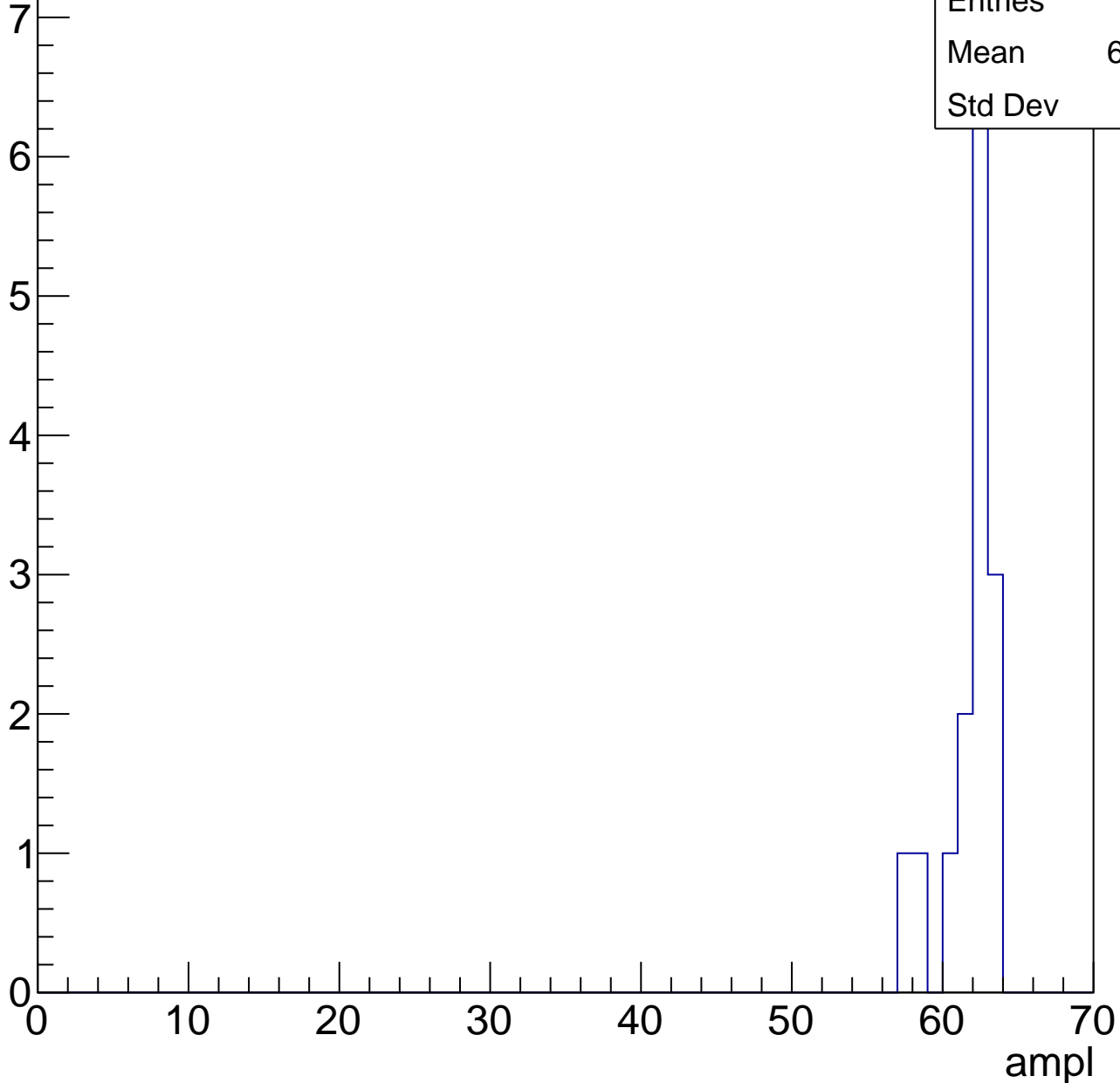


B1L100S, U15-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	61.33
Std Dev	1.7



B1L100S, U15-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U15-ch73, adc0

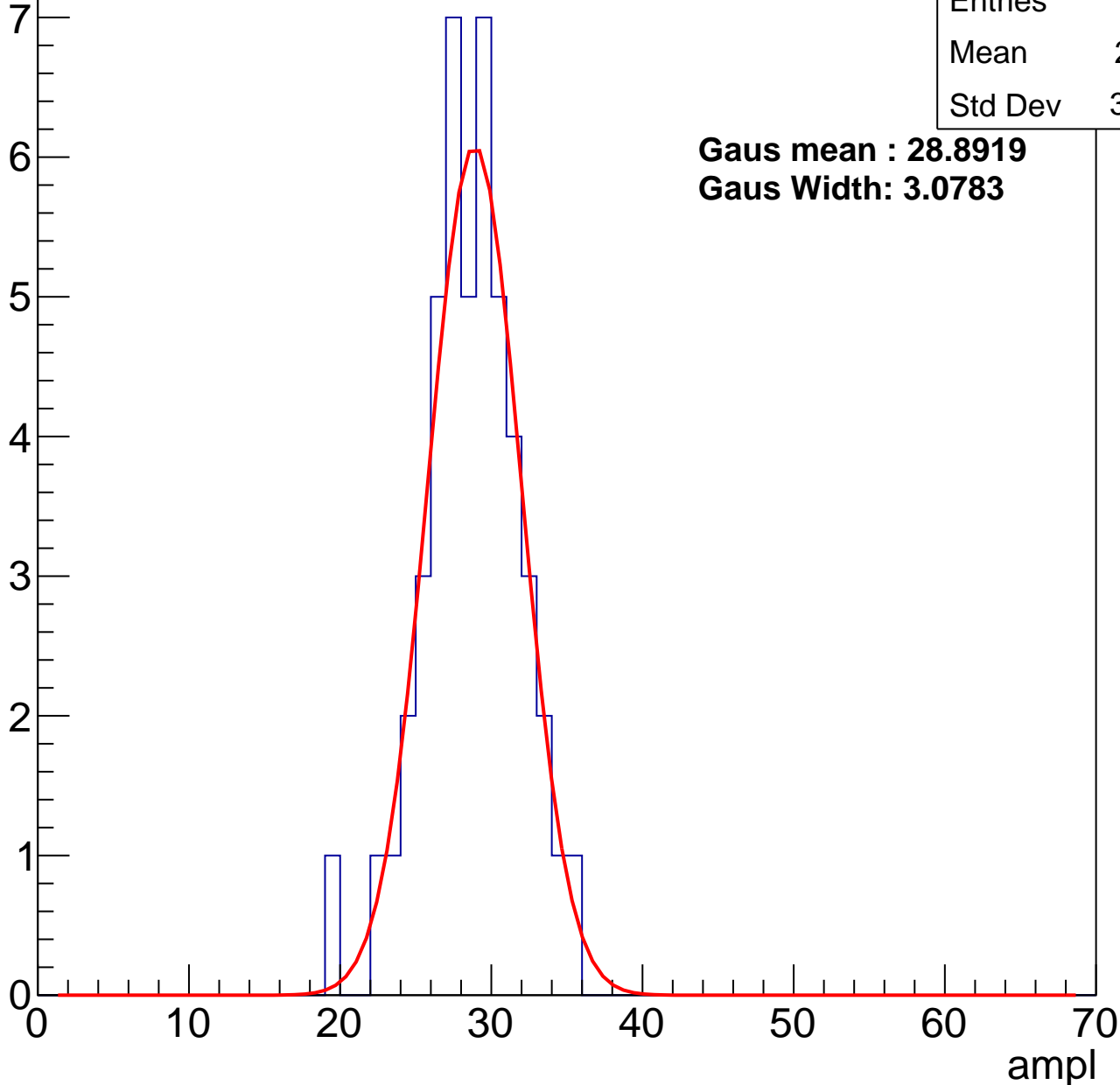
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	28.21
Std Dev	3.136

Gaus mean : 28.8919

Gaus Width: 3.0783



B1L100S, U15-ch73, adc1

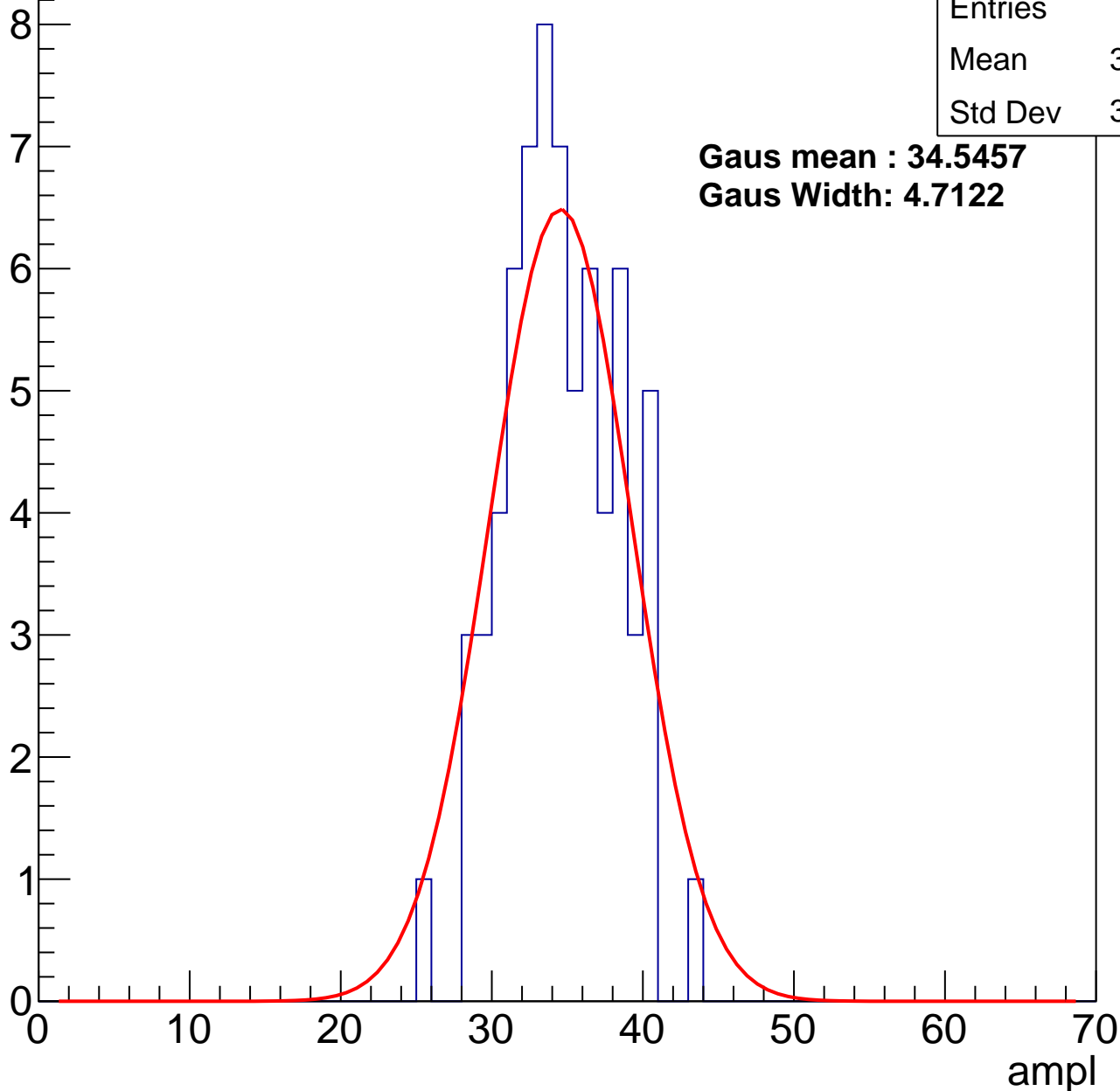
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	34.13
Std Dev	3.639

Gaus mean : 34.5457

Gaus Width: 4.7122



B1L100S, U15-ch73, adc2

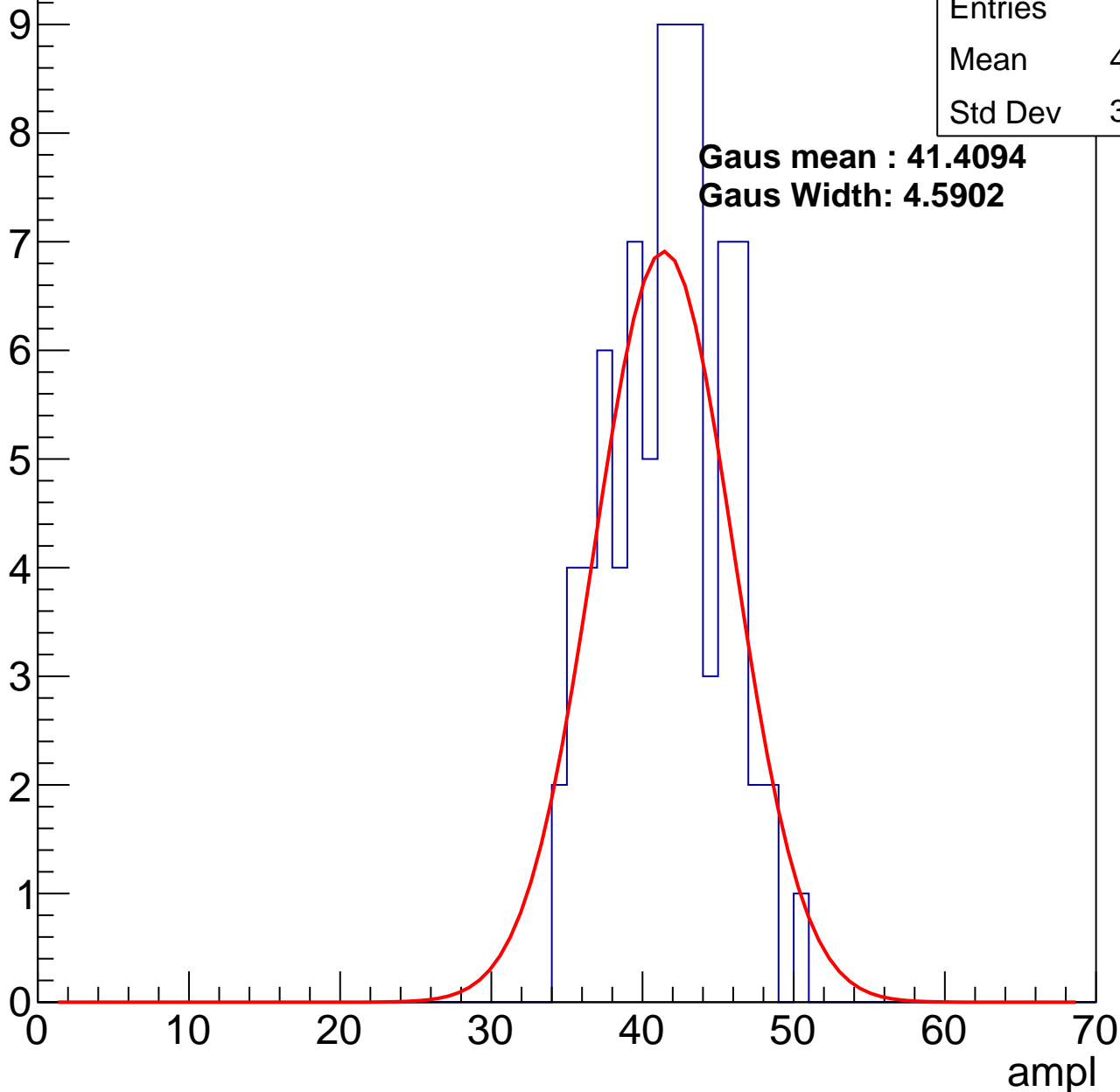
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	41.26
Std Dev	3.704

Gaus mean : 41.4094

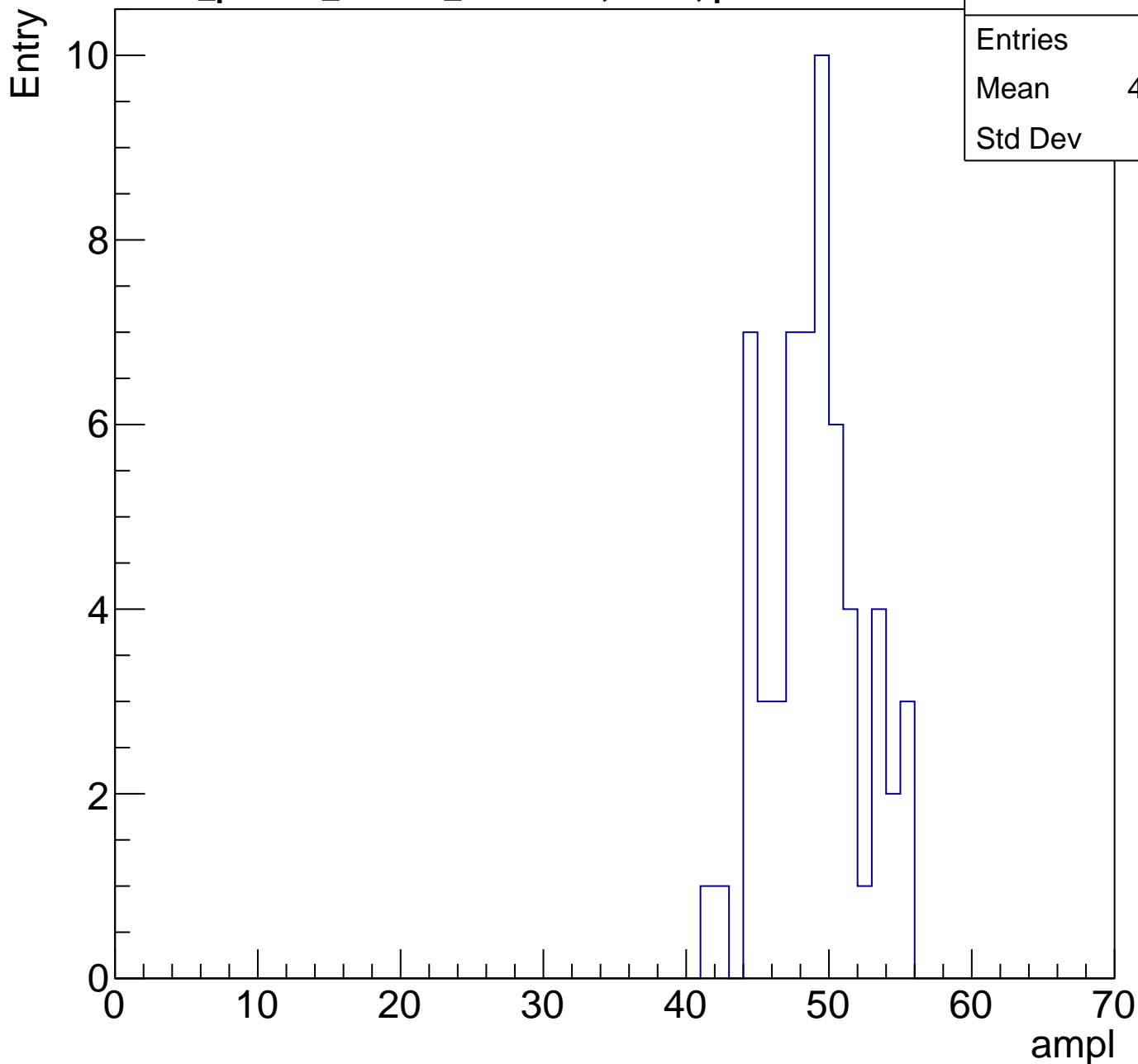
Gaus Width: 4.5902



B1L100S, U15-ch73, adc3

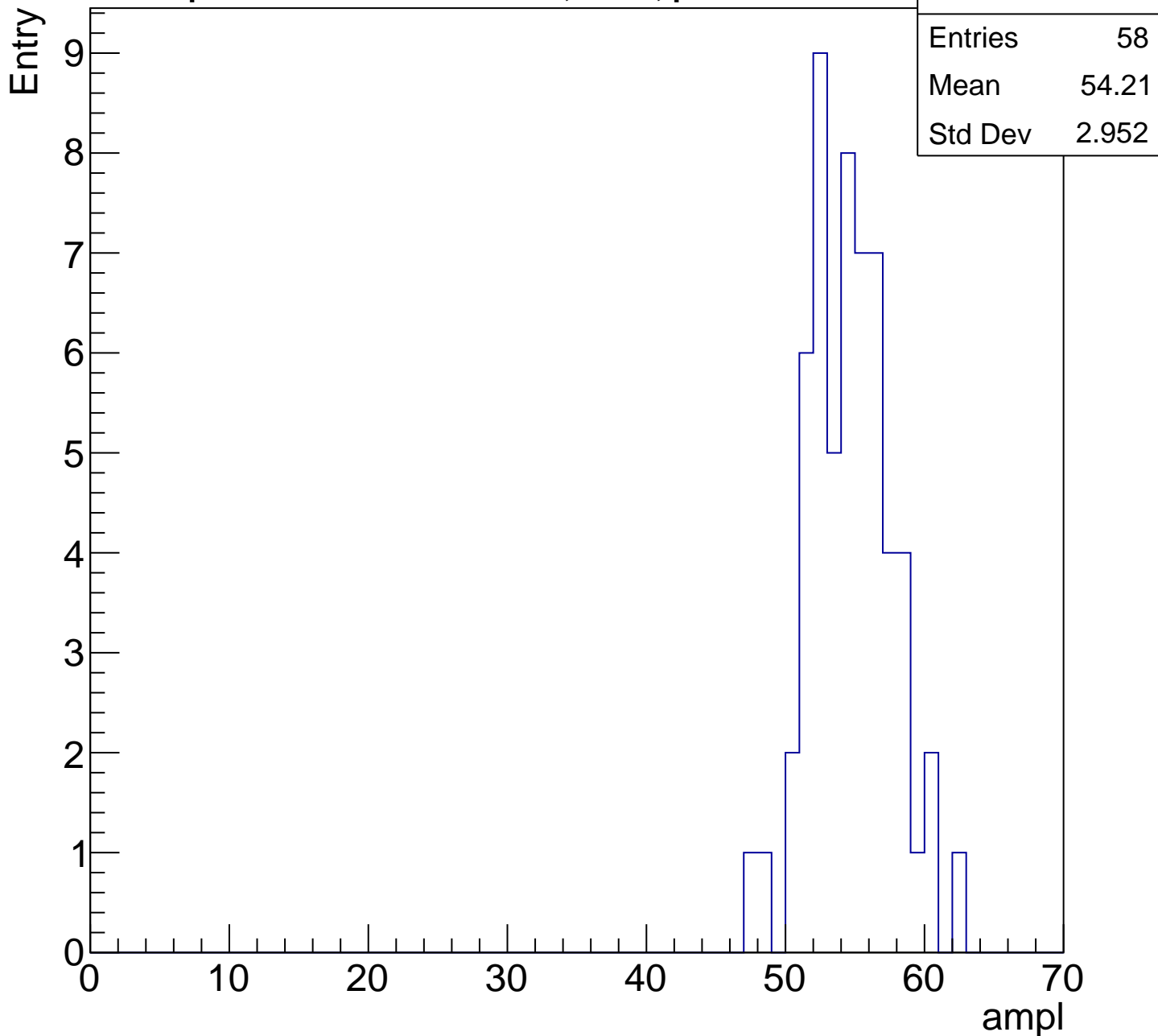
calib_packv5_042523_0143.root, FC#4, port A2

Entries	59
Mean	48.47
Std Dev	3.28



B1L100S, U15-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U15-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	49
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Mean	58.12
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Std Dev	8.792
---------	-------

ampl

0

10

20

30

40

50

60

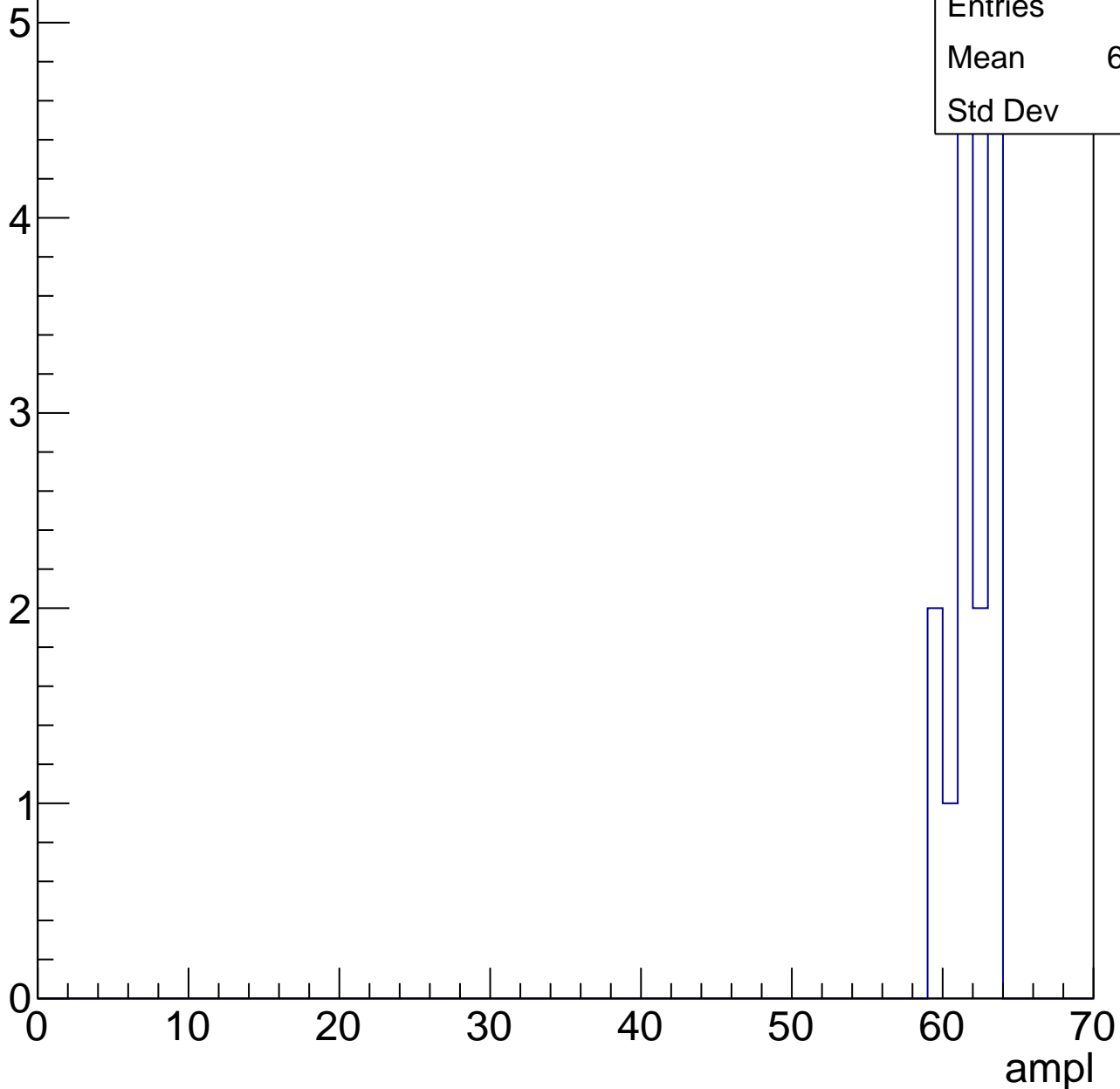
70

B1L100S, U15-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	61.47
Std Dev	1.36



B1L100S, U15-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch74, adc0

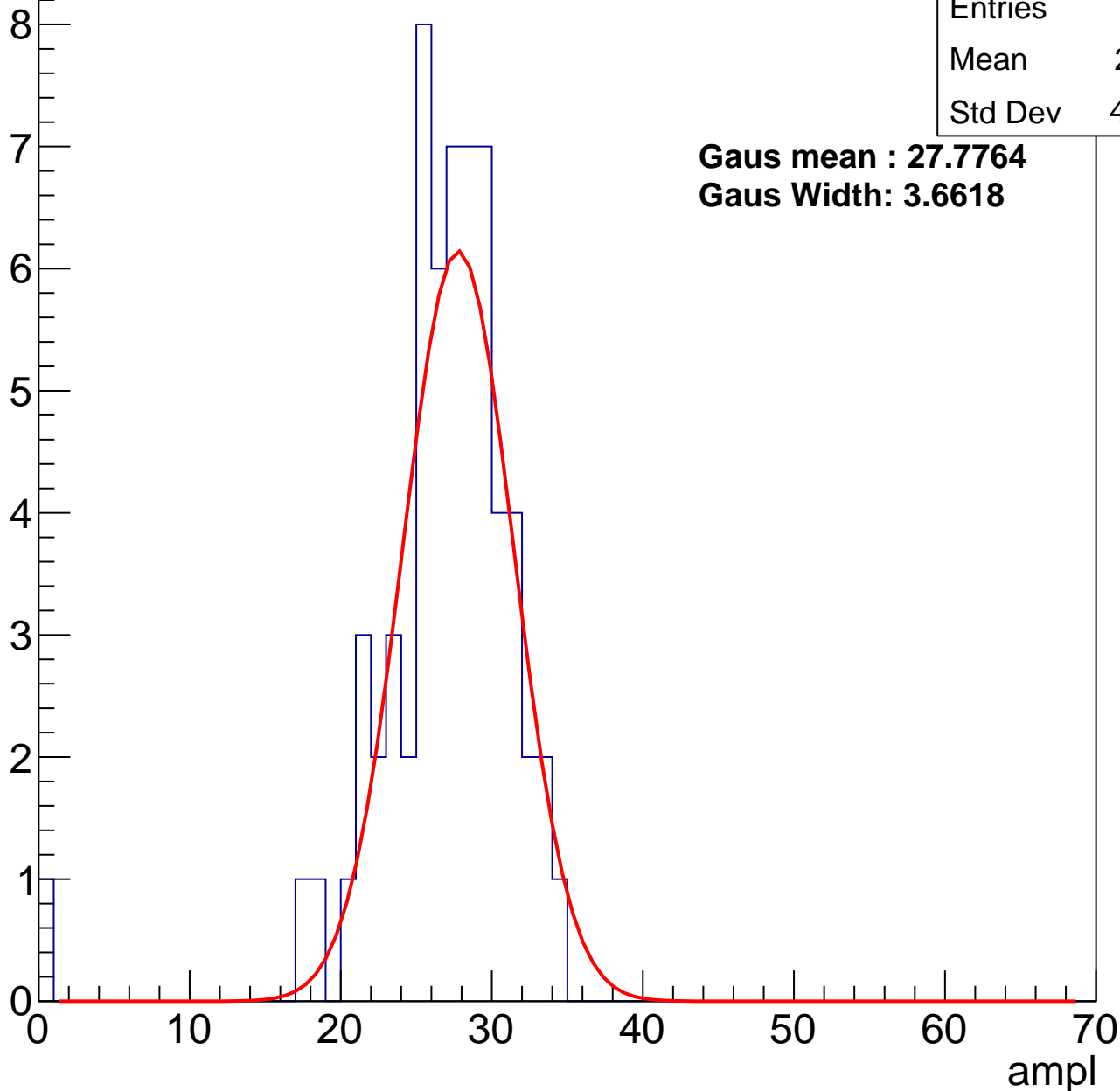
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	26.31
Std Dev	4.914

Gaus mean : 27.7764

Gaus Width: 3.6618



B1L100S, U15-ch74, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	33.46
Std Dev	3.778

Gaus mean : 33.6968

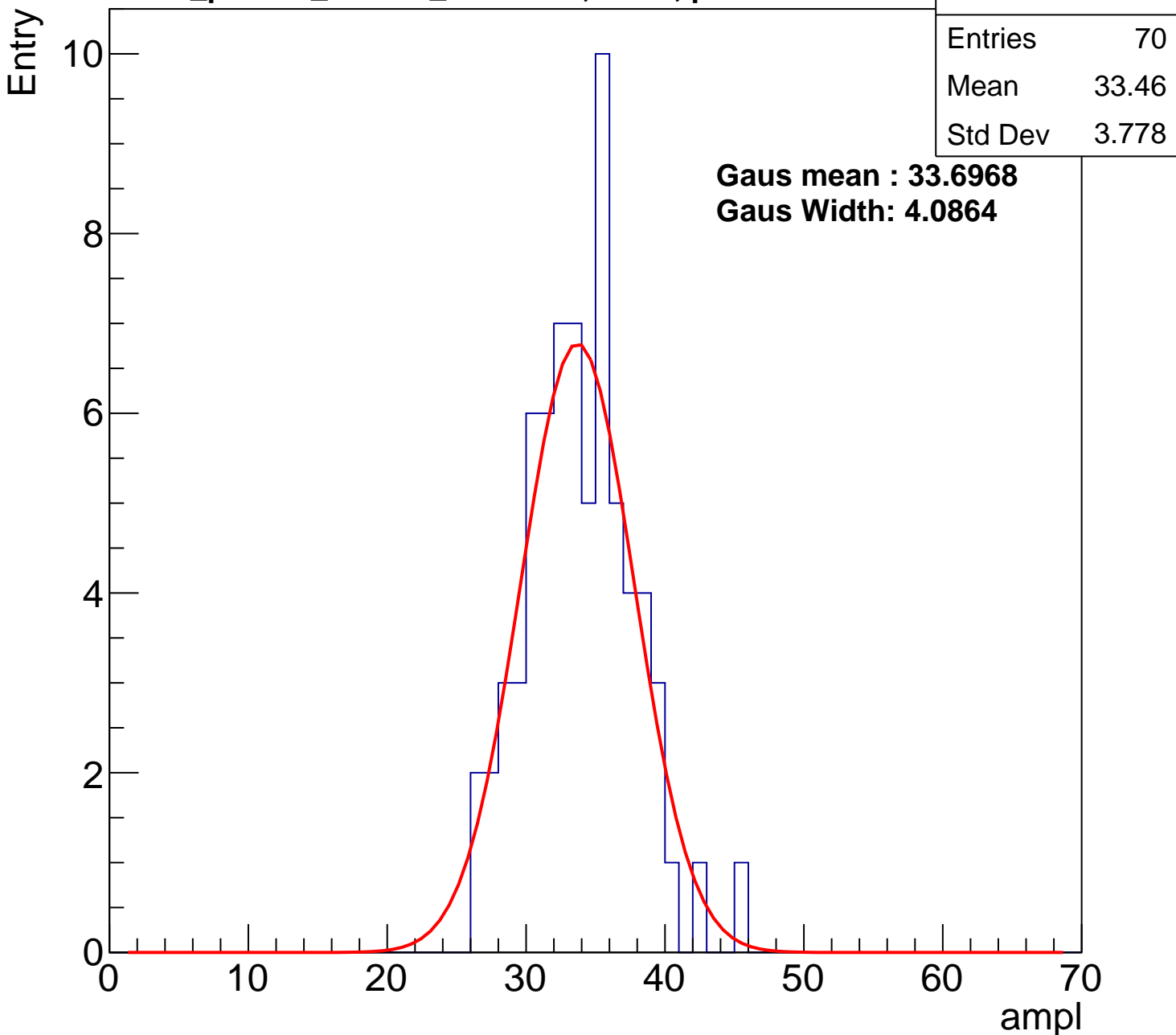
Gaus Width: 4.0864

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch74, adc2

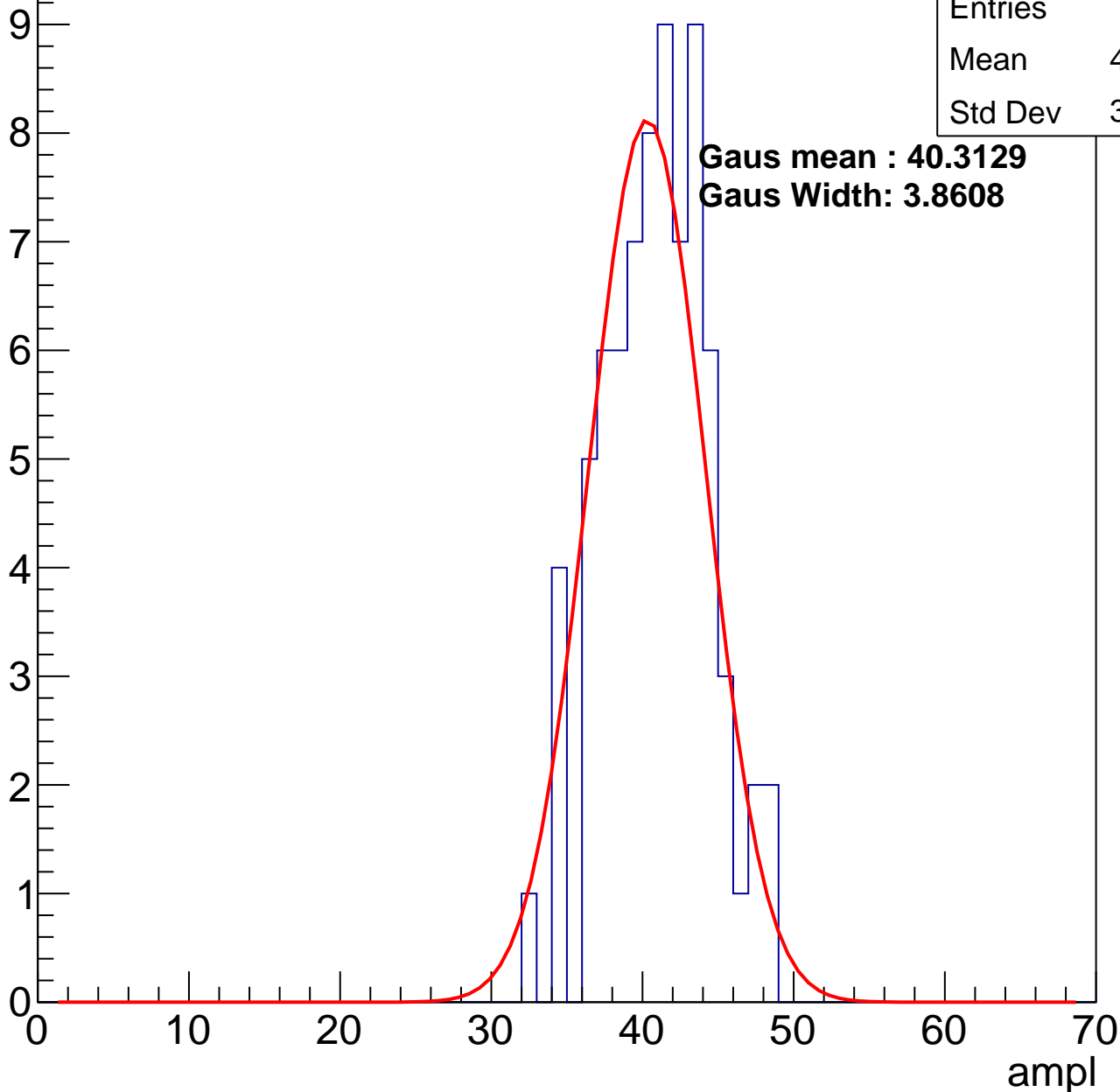
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	40.47
Std Dev	3.454

Gaus mean : 40.3129

Gaus Width: 3.8608

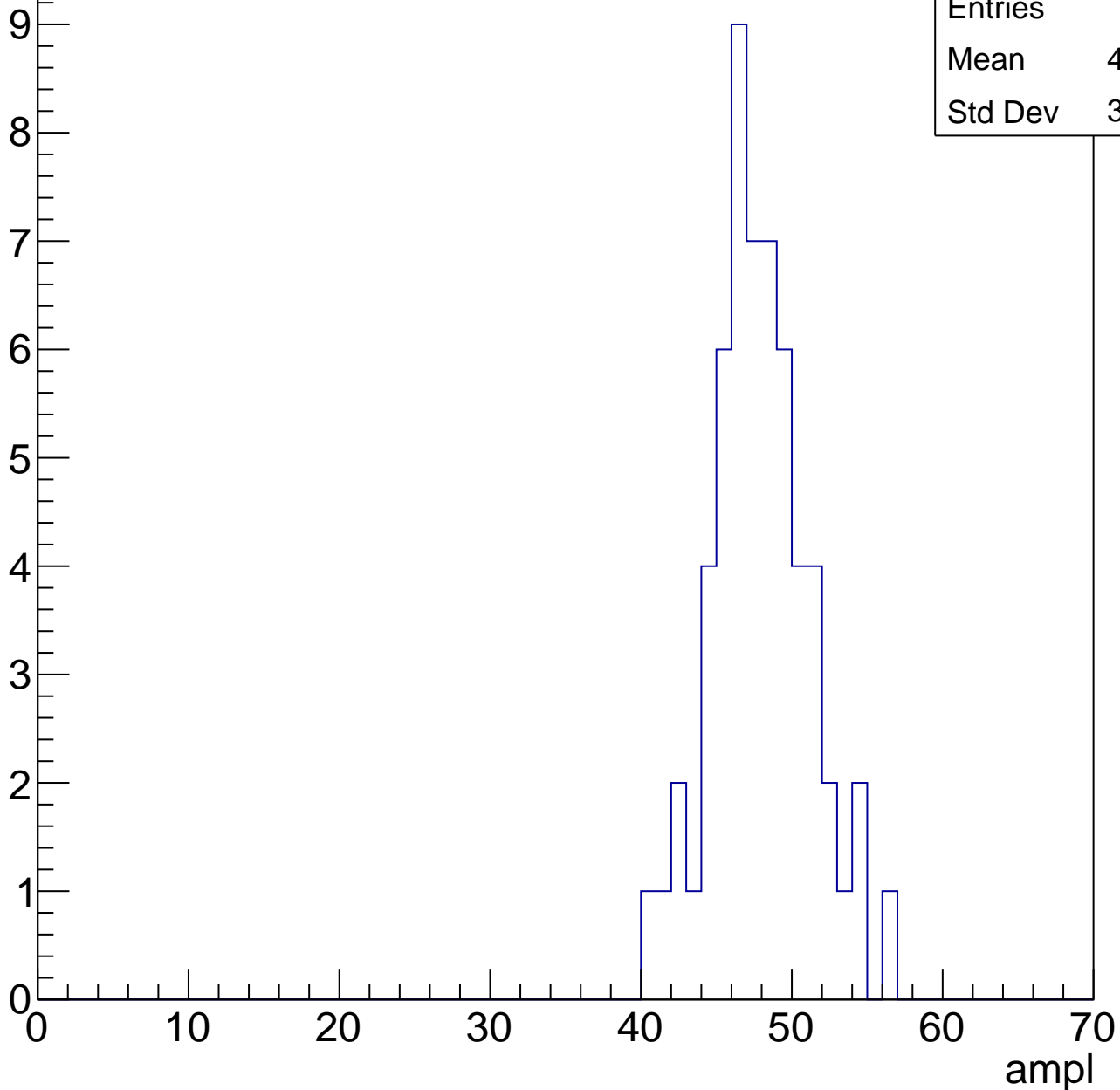


B1L100S, U15-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

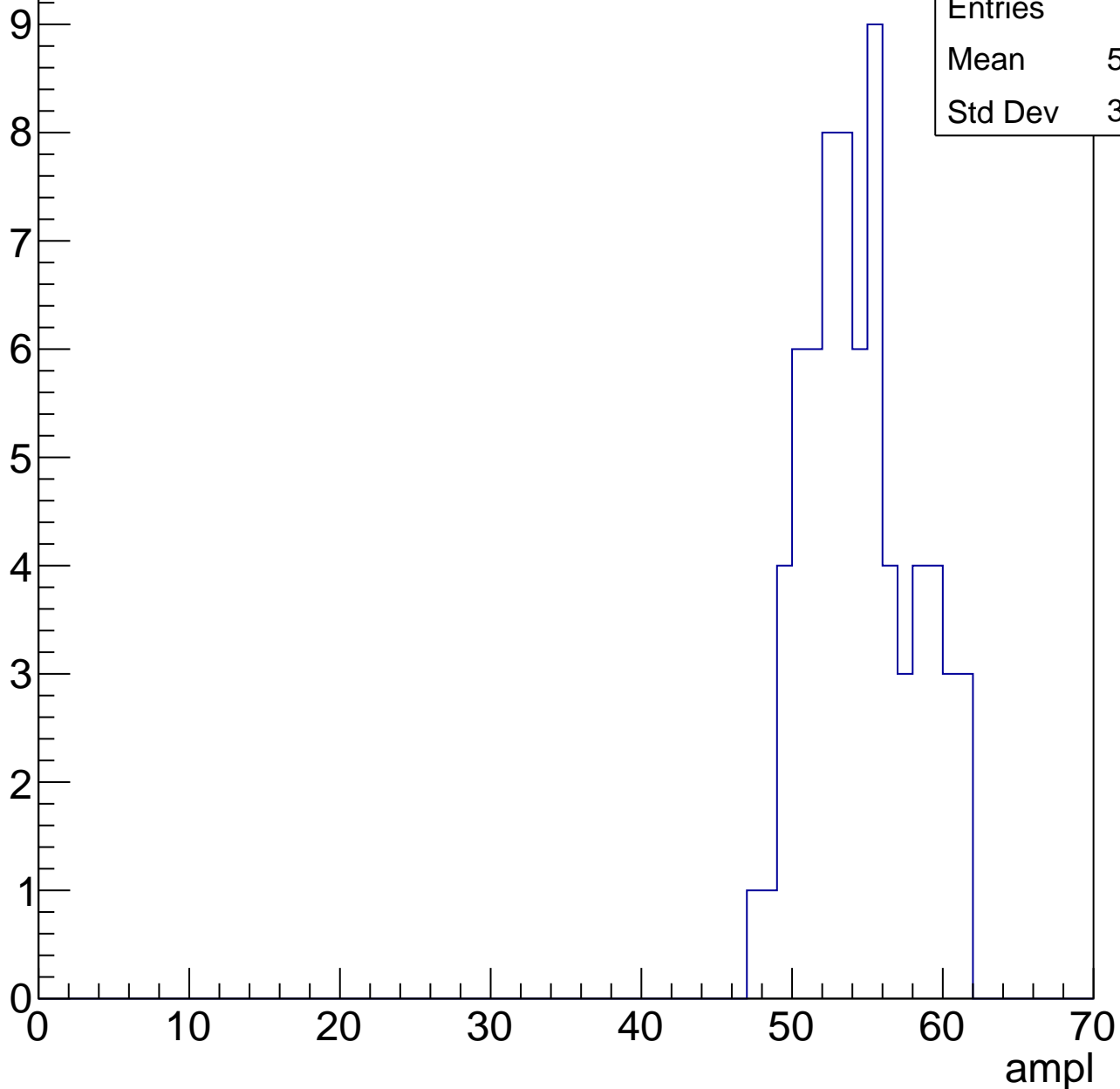
Entries	58
Mean	47.45
Std Dev	3.228



B1L100S, U15-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

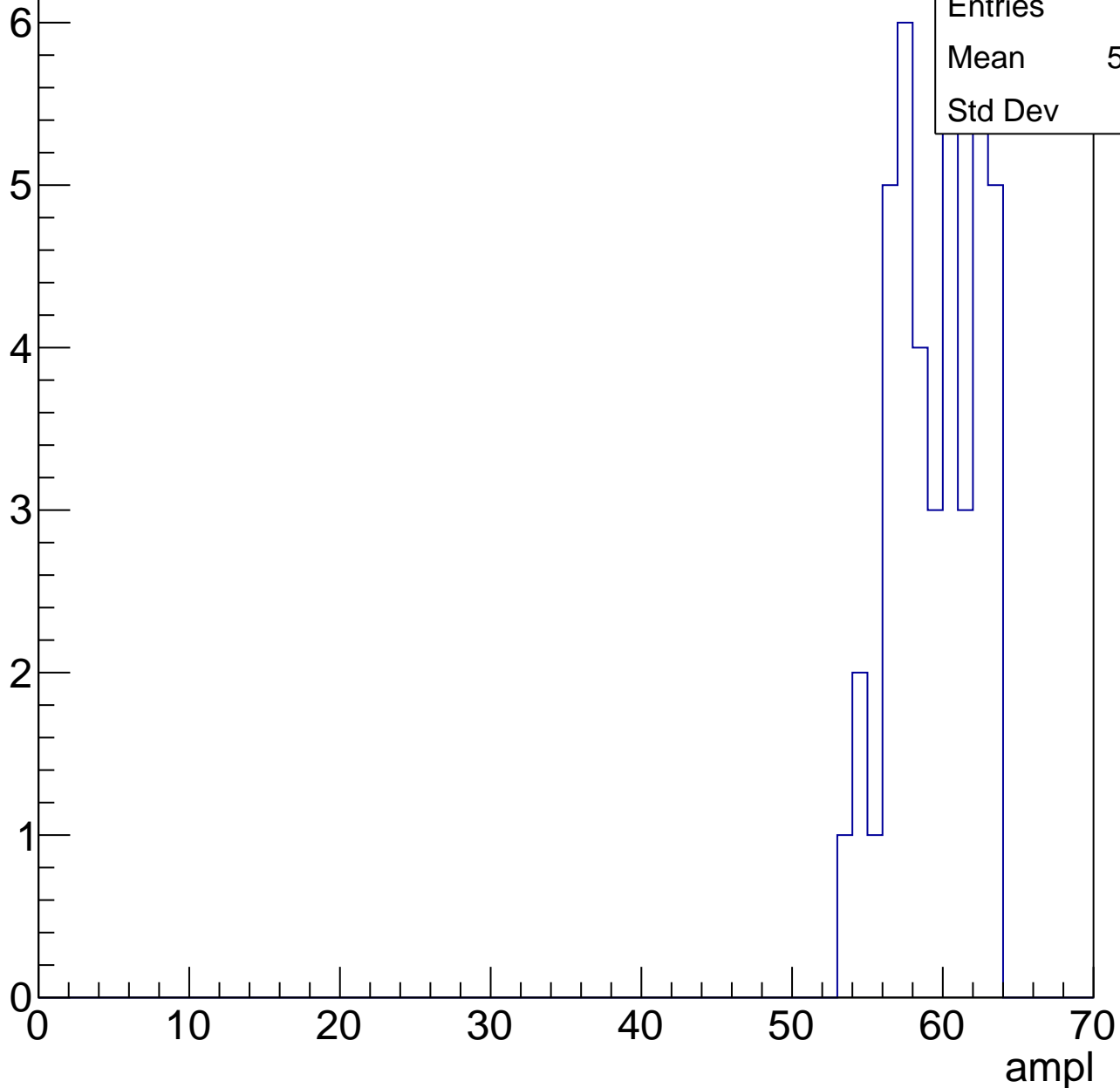
Entry



B1L100S, U15-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

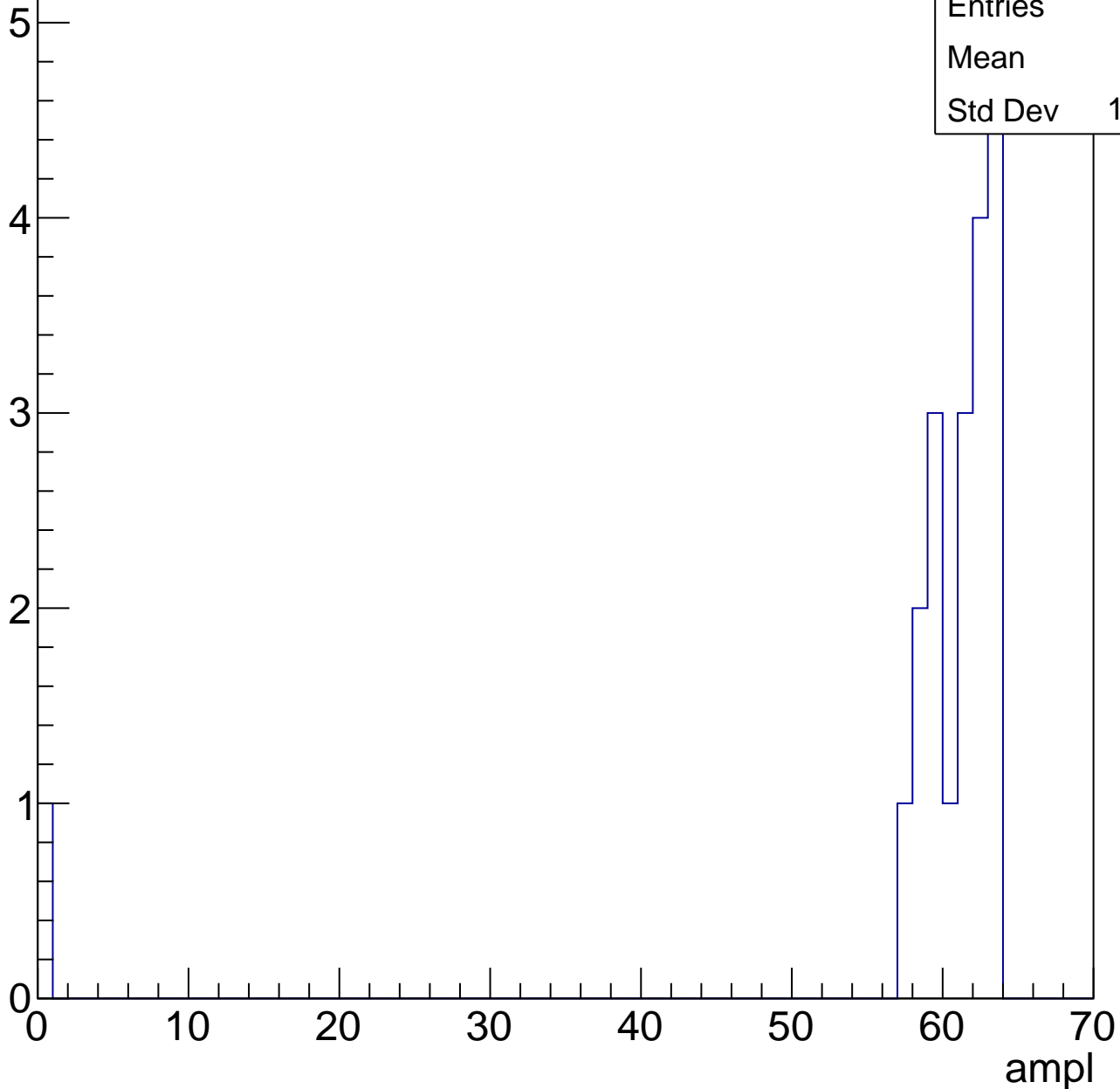


B1L100S, U15-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	57.8
Std Dev	13.39



B1L100S, U15-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L100S, U15-ch75, adc0

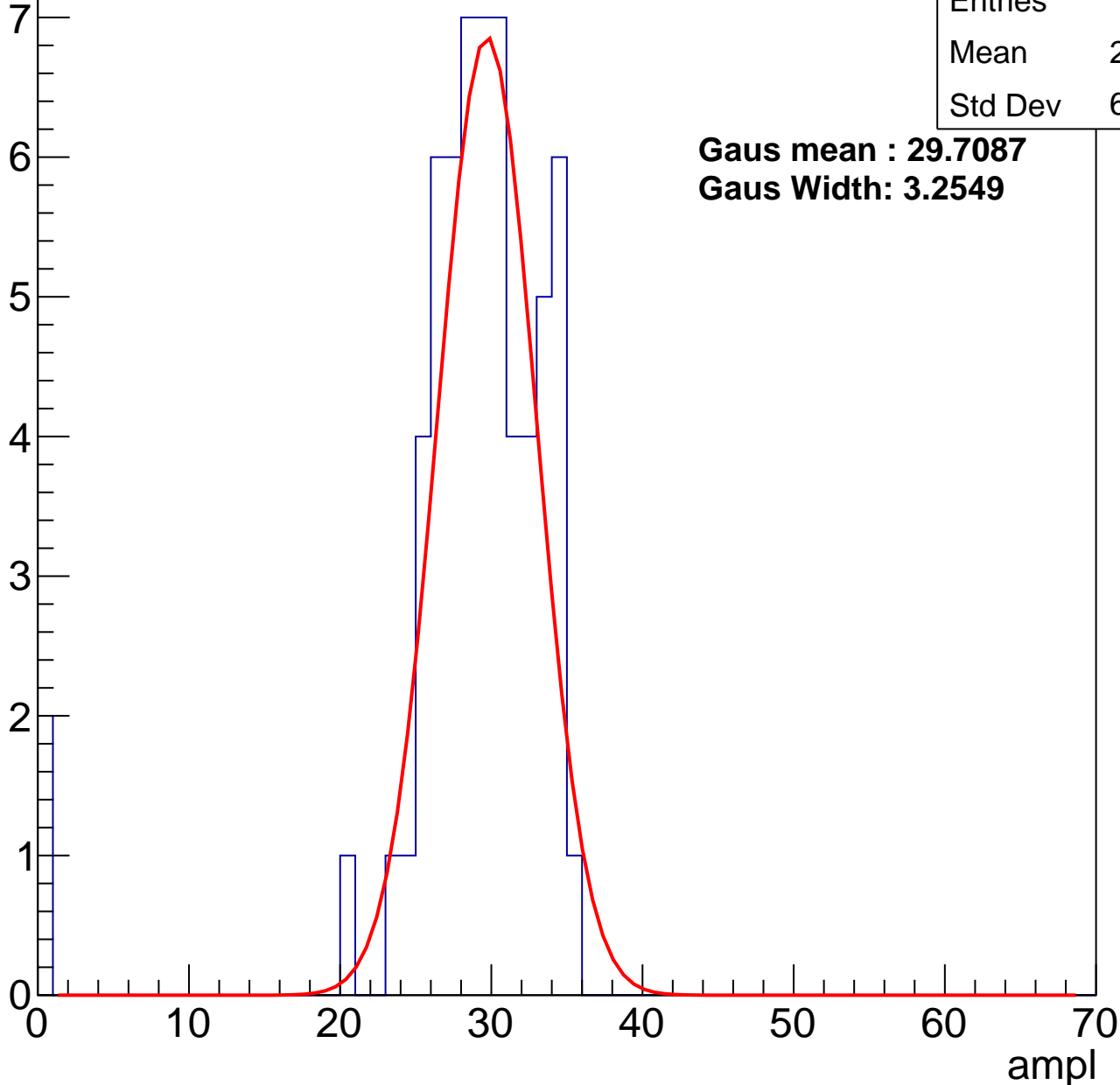
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.23
Std Dev	6.039

Gaus mean : 29.7087

Gaus Width: 3.2549



B1L100S, U15-ch75, adc1

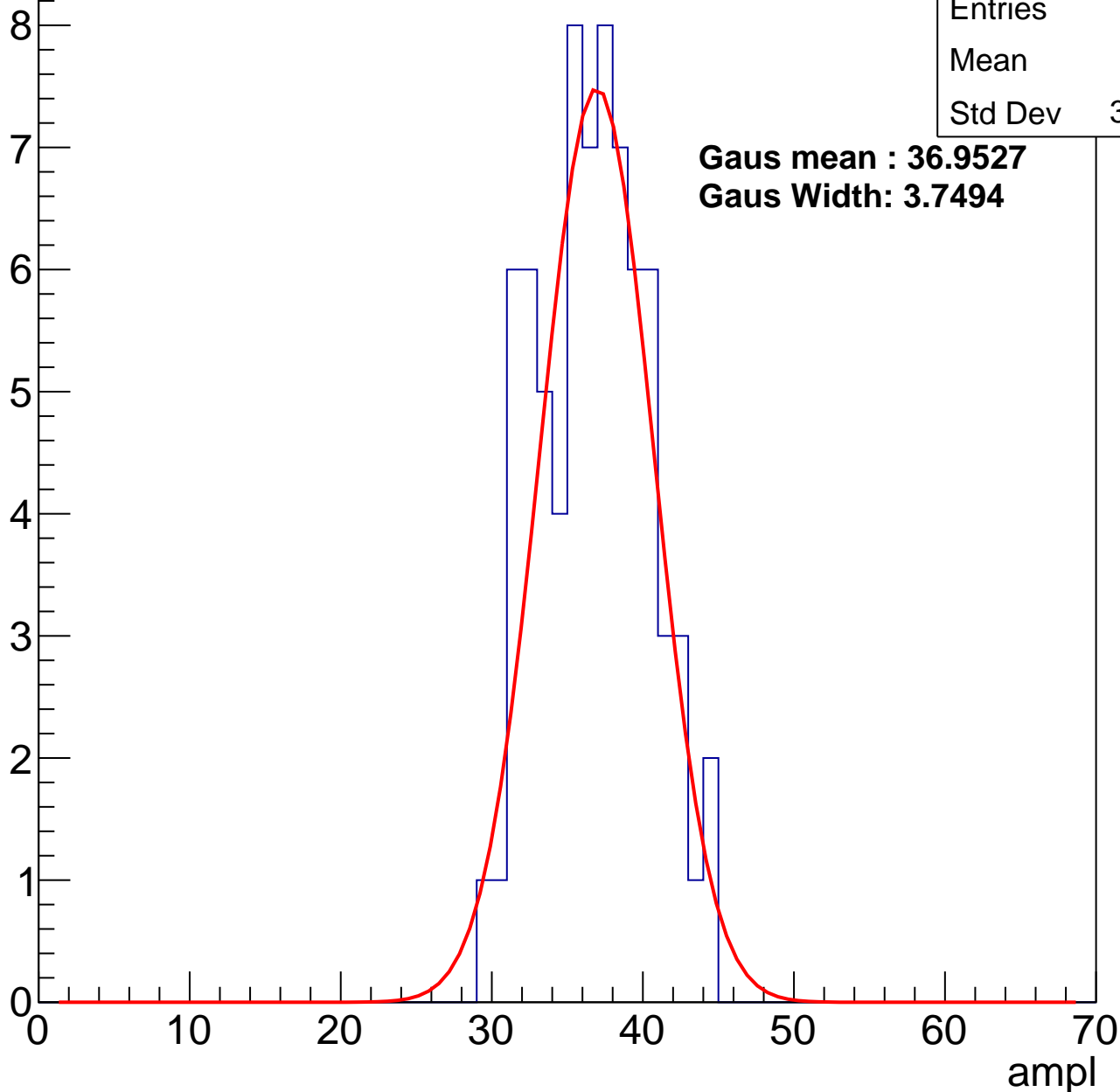
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.3
Std Dev	3.563

Gaus mean : 36.9527

Gaus Width: 3.7494



B1L100S, U15-ch75, adc2

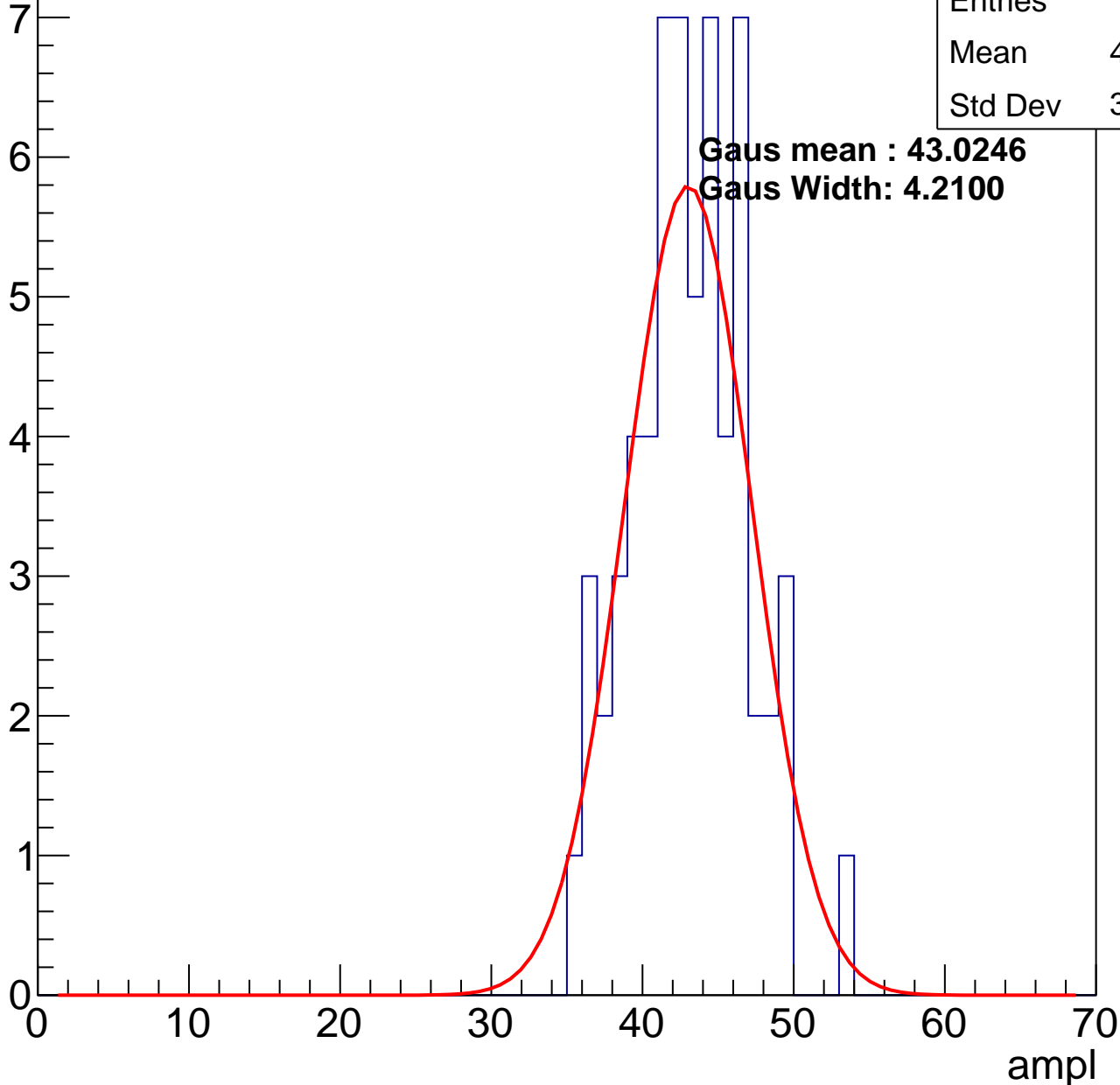
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.63
Std Dev	3.725

Gaus mean : 43.0246

Gaus Width: 4.2100

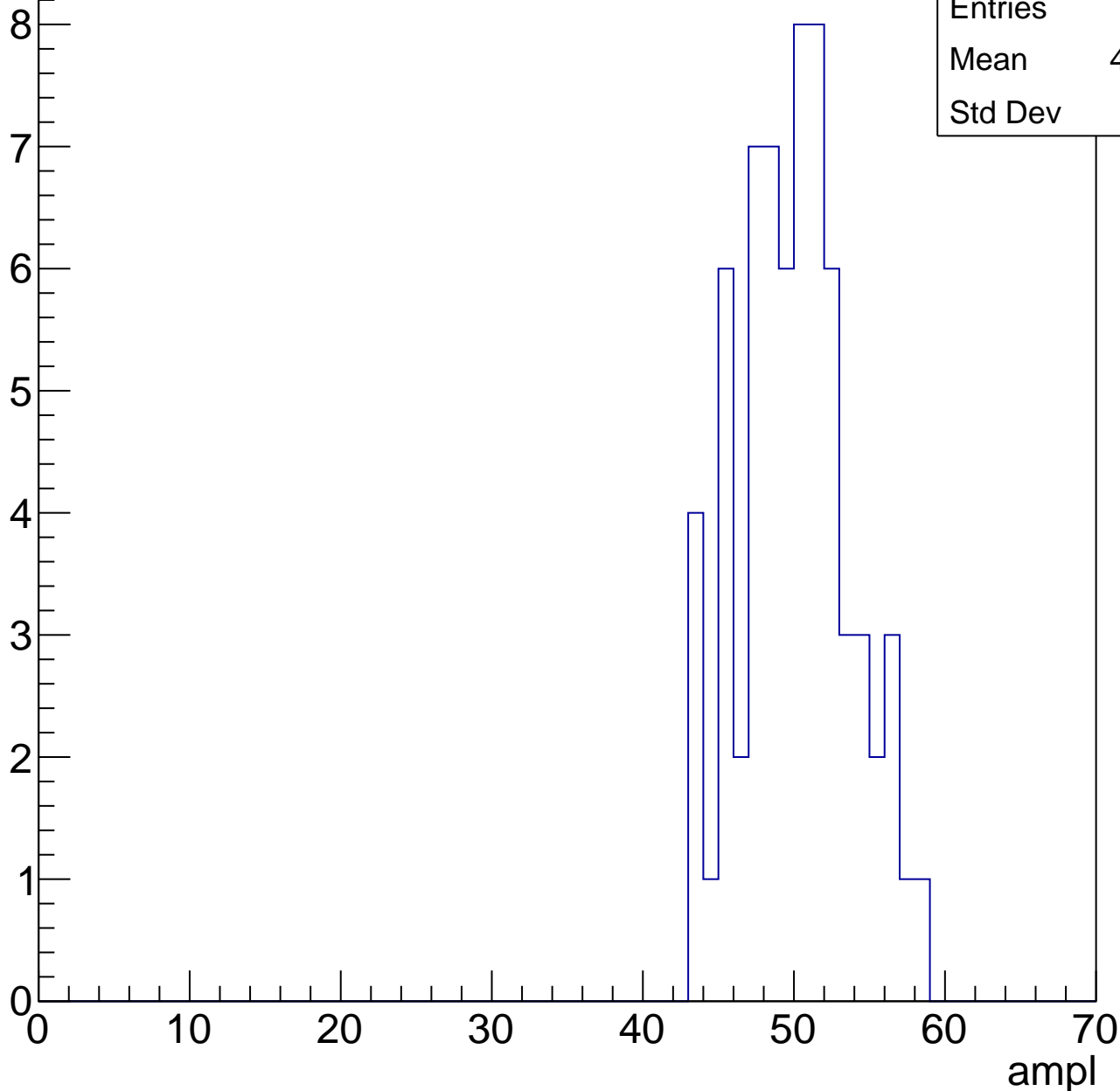


B1L100S, U15-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	49.57
Std Dev	3.59

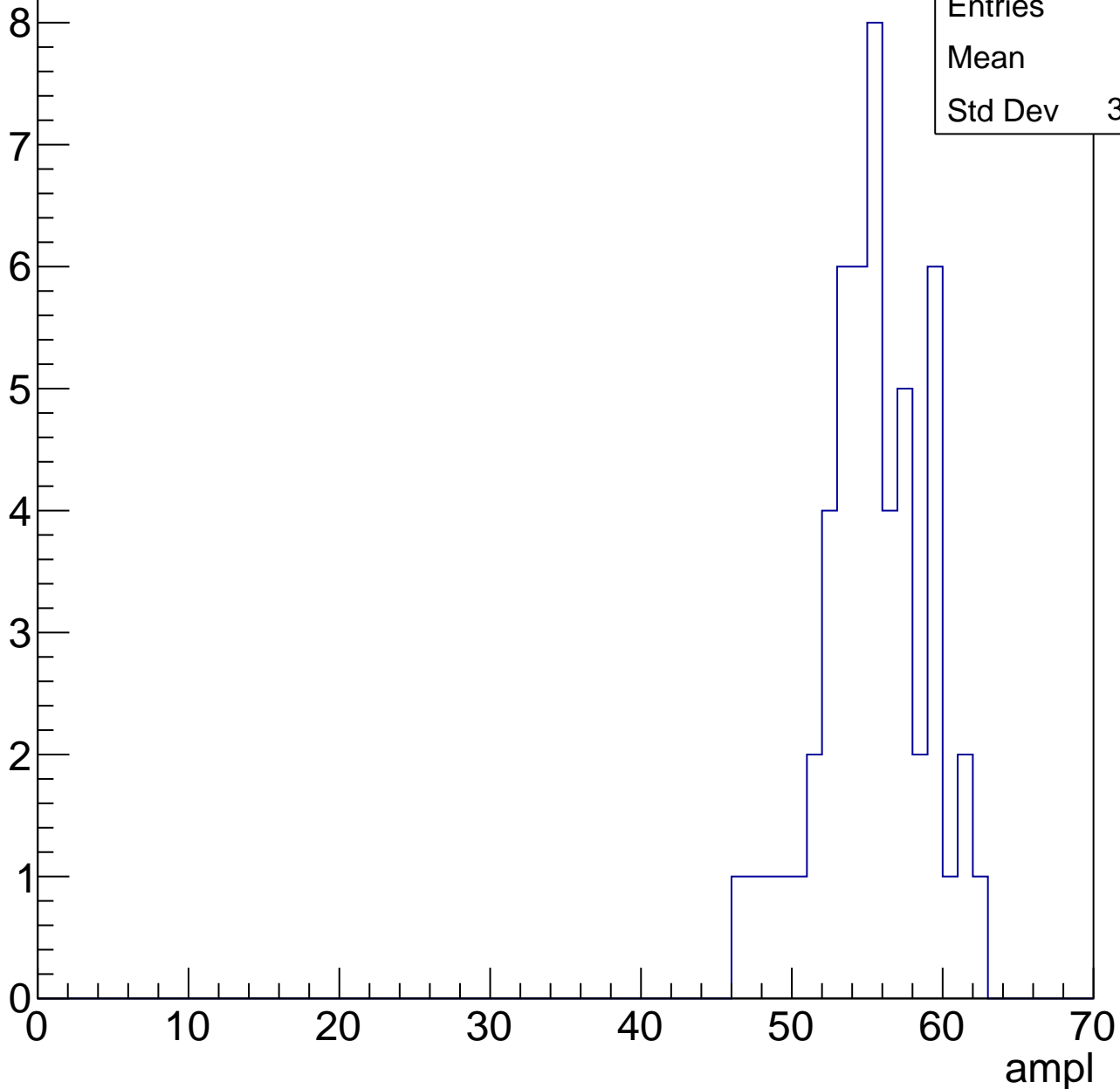


B1L100S, U15-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

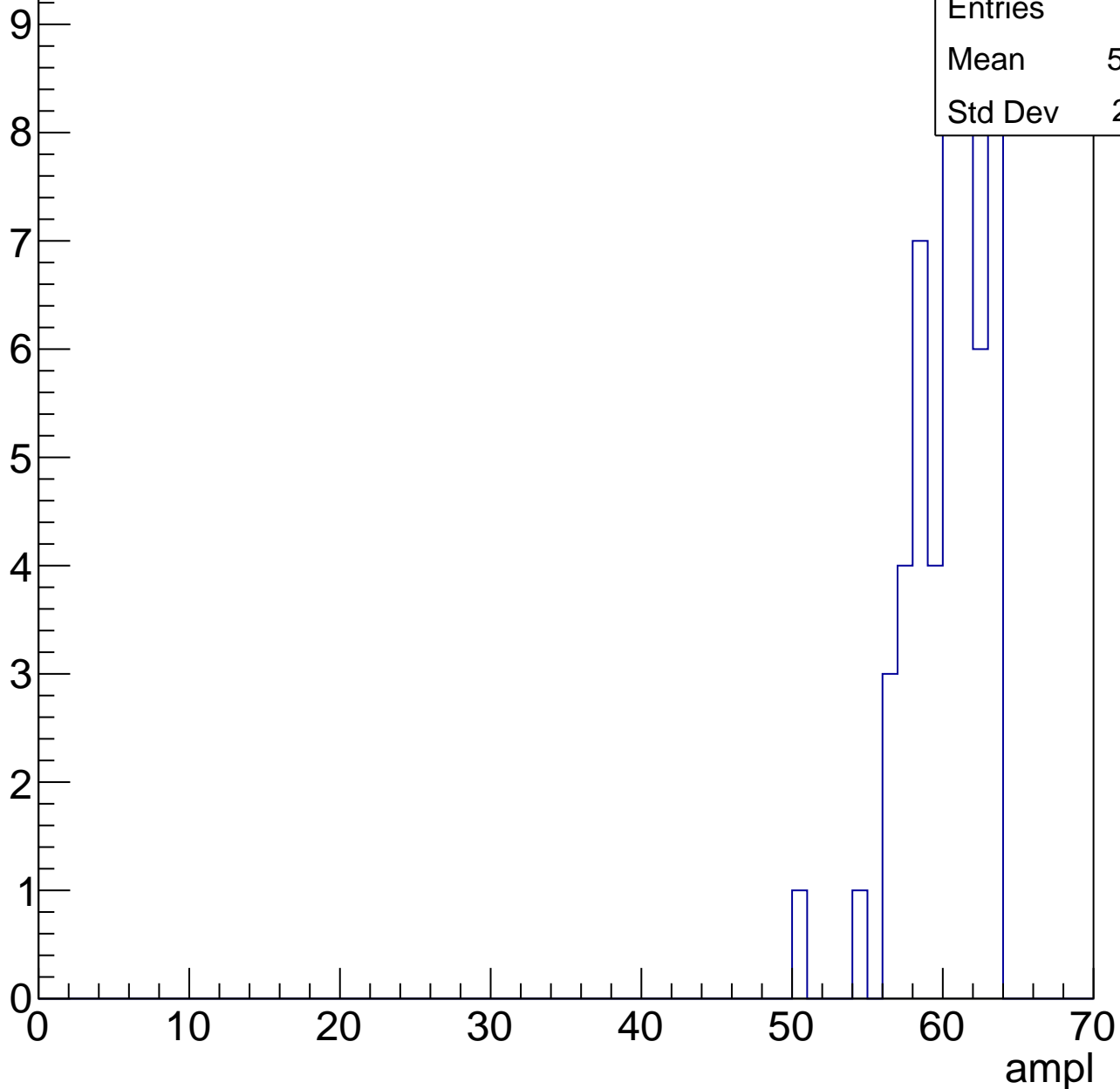
Entries	52
Mean	54.9
Std Dev	3.499



B1L100S, U15-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

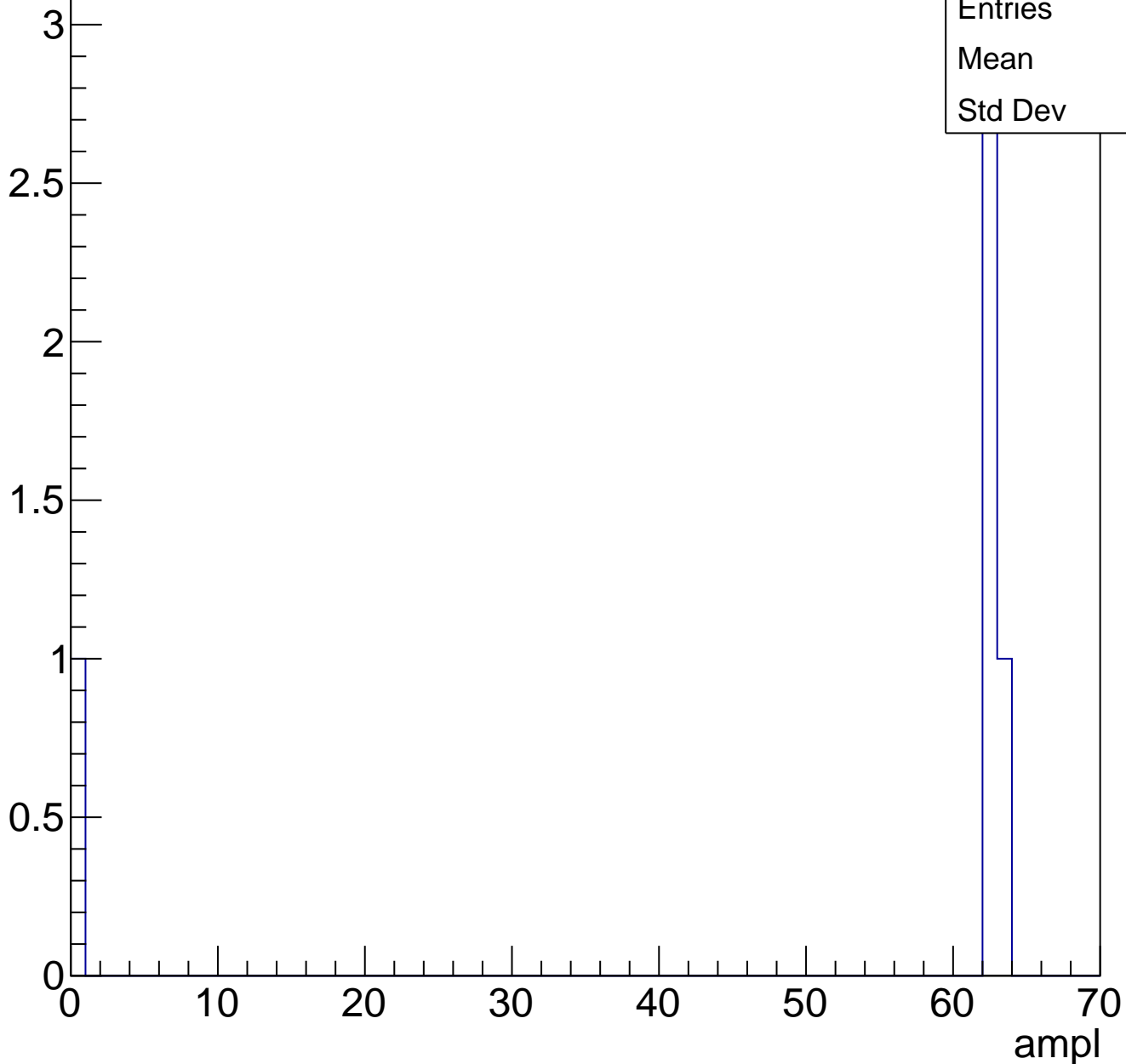
Entry



B1L100S, U15-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch76, adc0

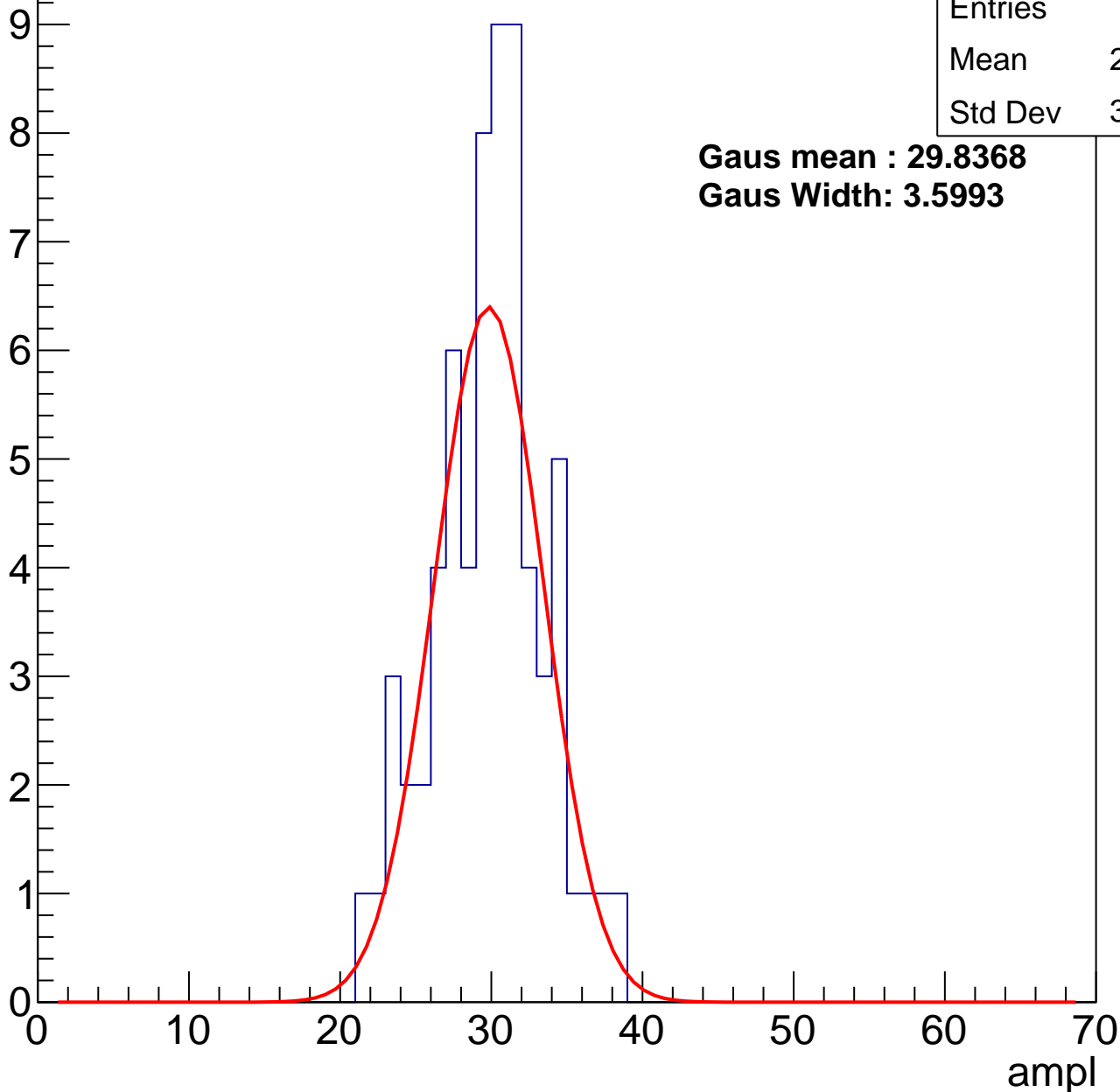
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.42
Std Dev	3.594

Gaus mean : 29.8368

Gaus Width: 3.5993



B1L100S, U15-ch76, adc1

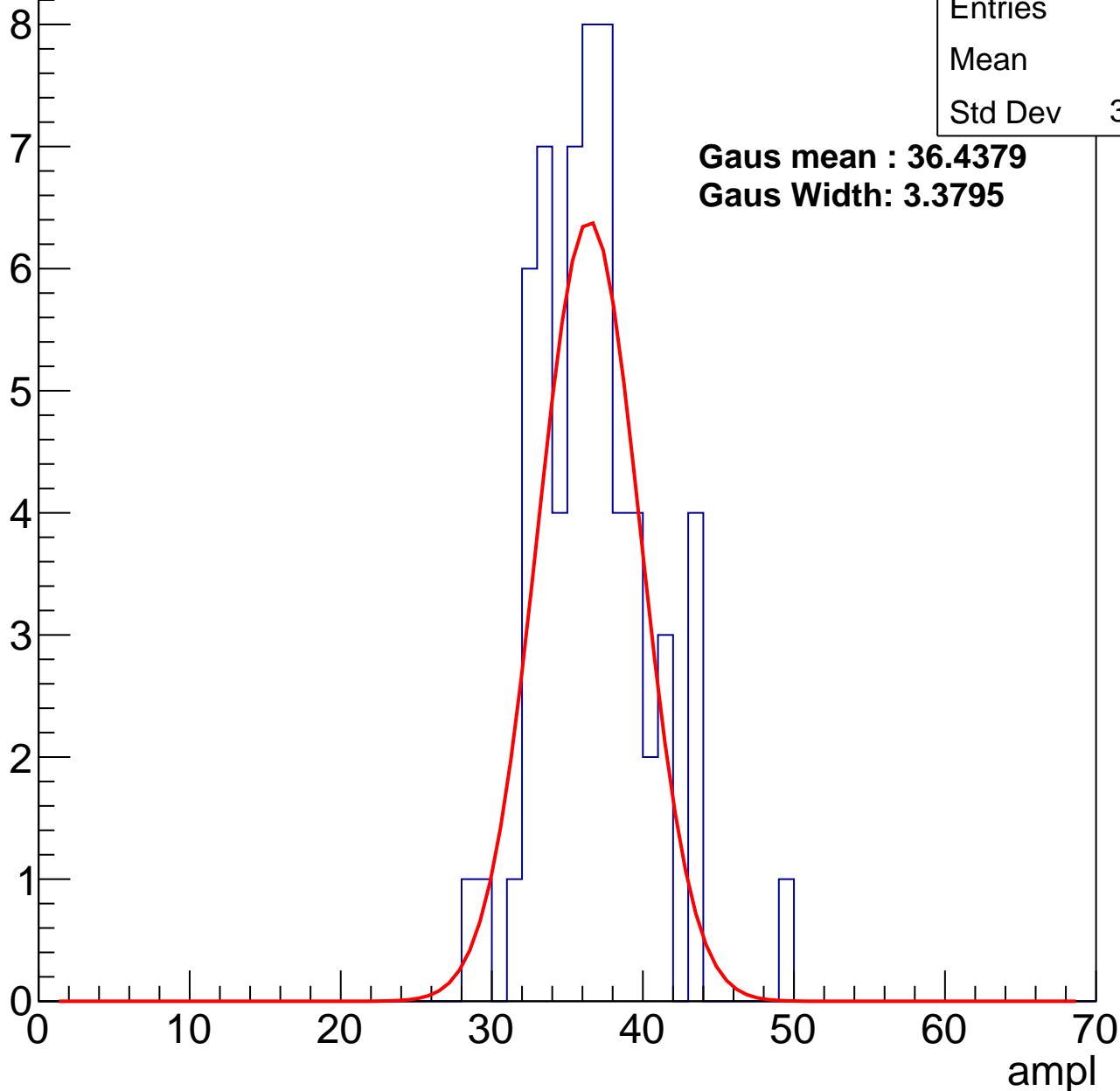
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.2
Std Dev	3.732

Gaus mean : 36.4379

Gaus Width: 3.3795



B1L100S, U15-ch76, adc2

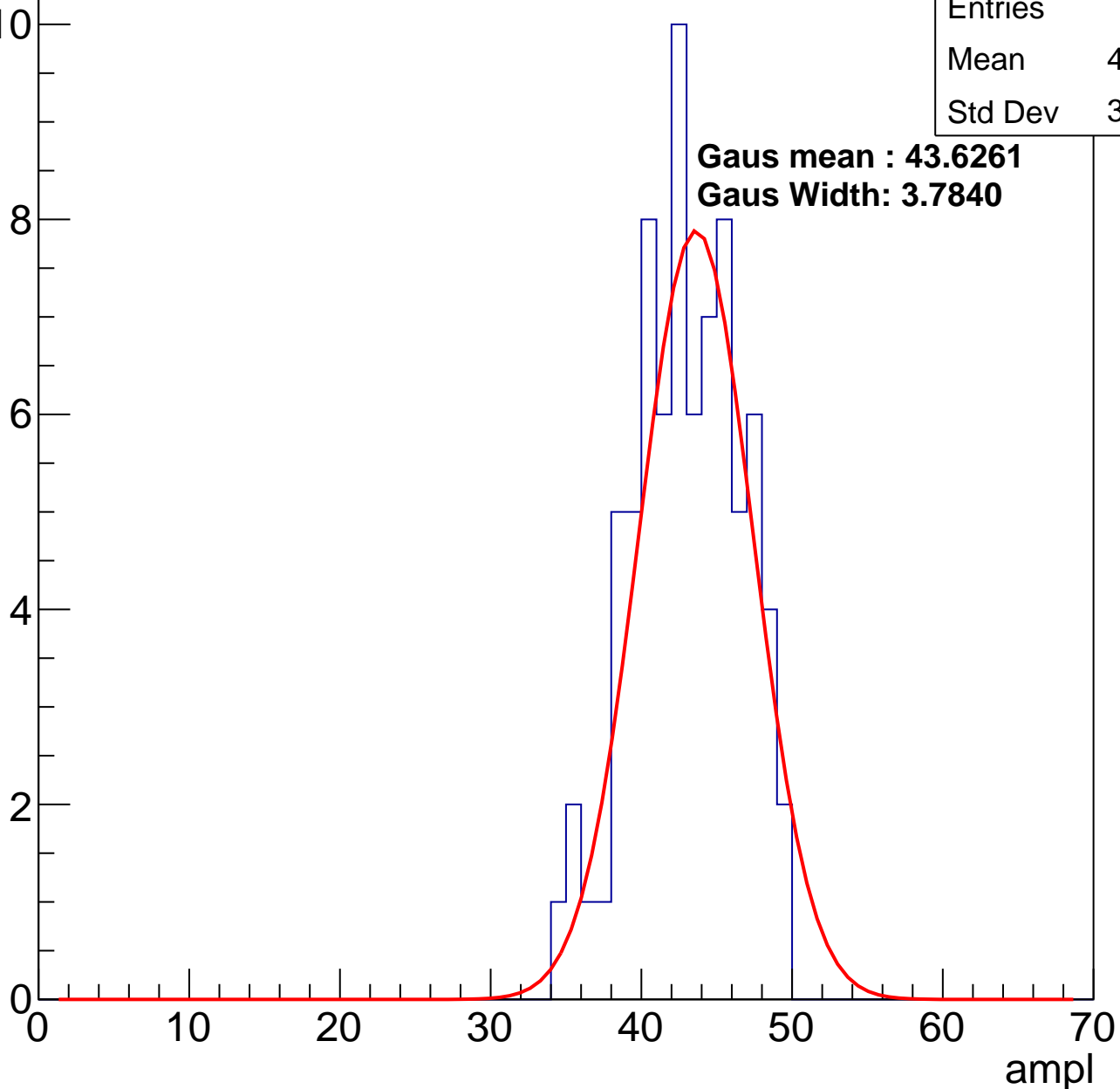
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	42.55
Std Dev	3.496

Gaus mean : 43.6261

Gaus Width: 3.7840



B1L100S, U15-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	58
Mean	49.4
Std Dev	3.503

Entry

10

8

6

4

2

0

0

10

20

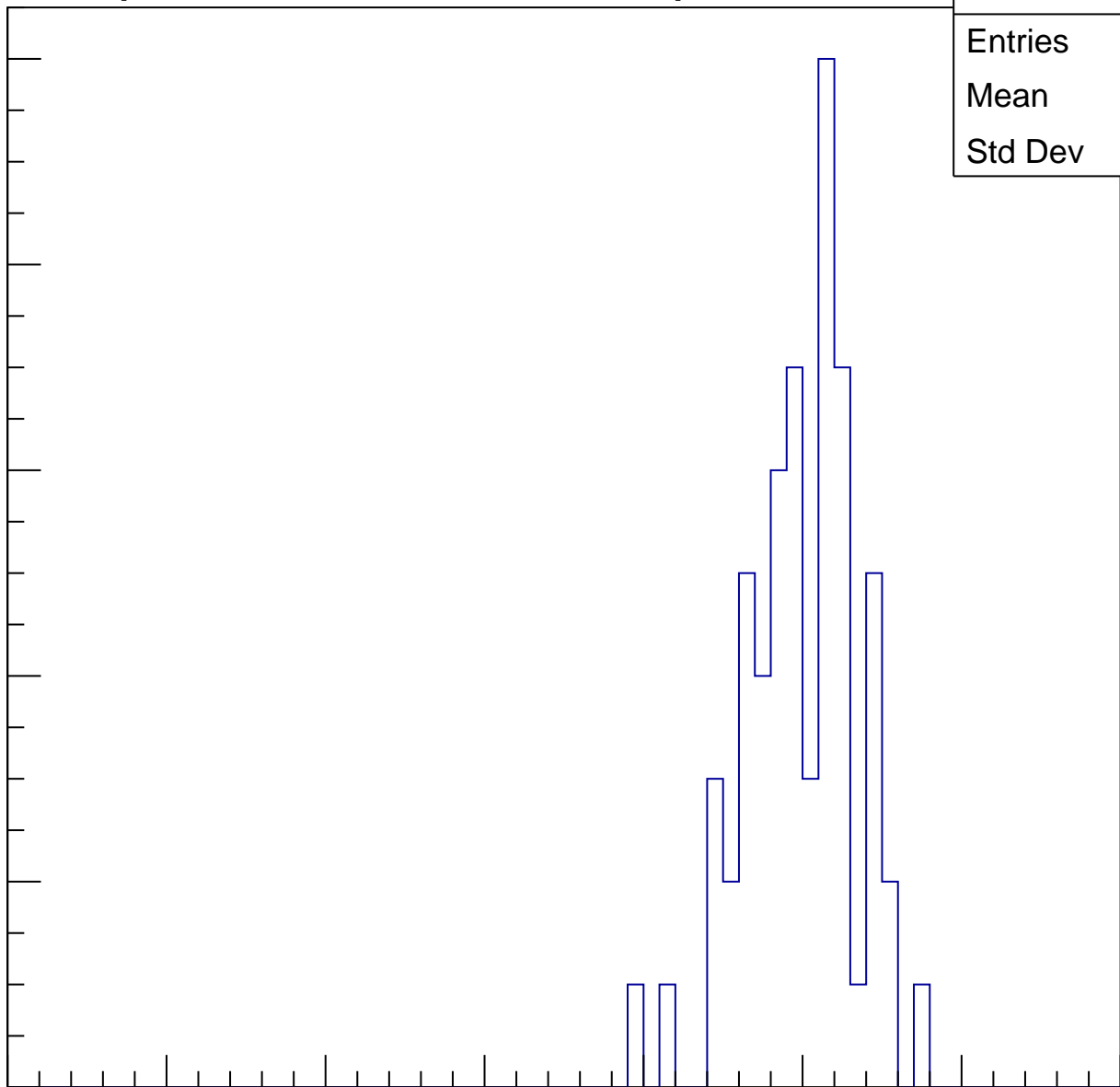
30

40

50

60

ampl

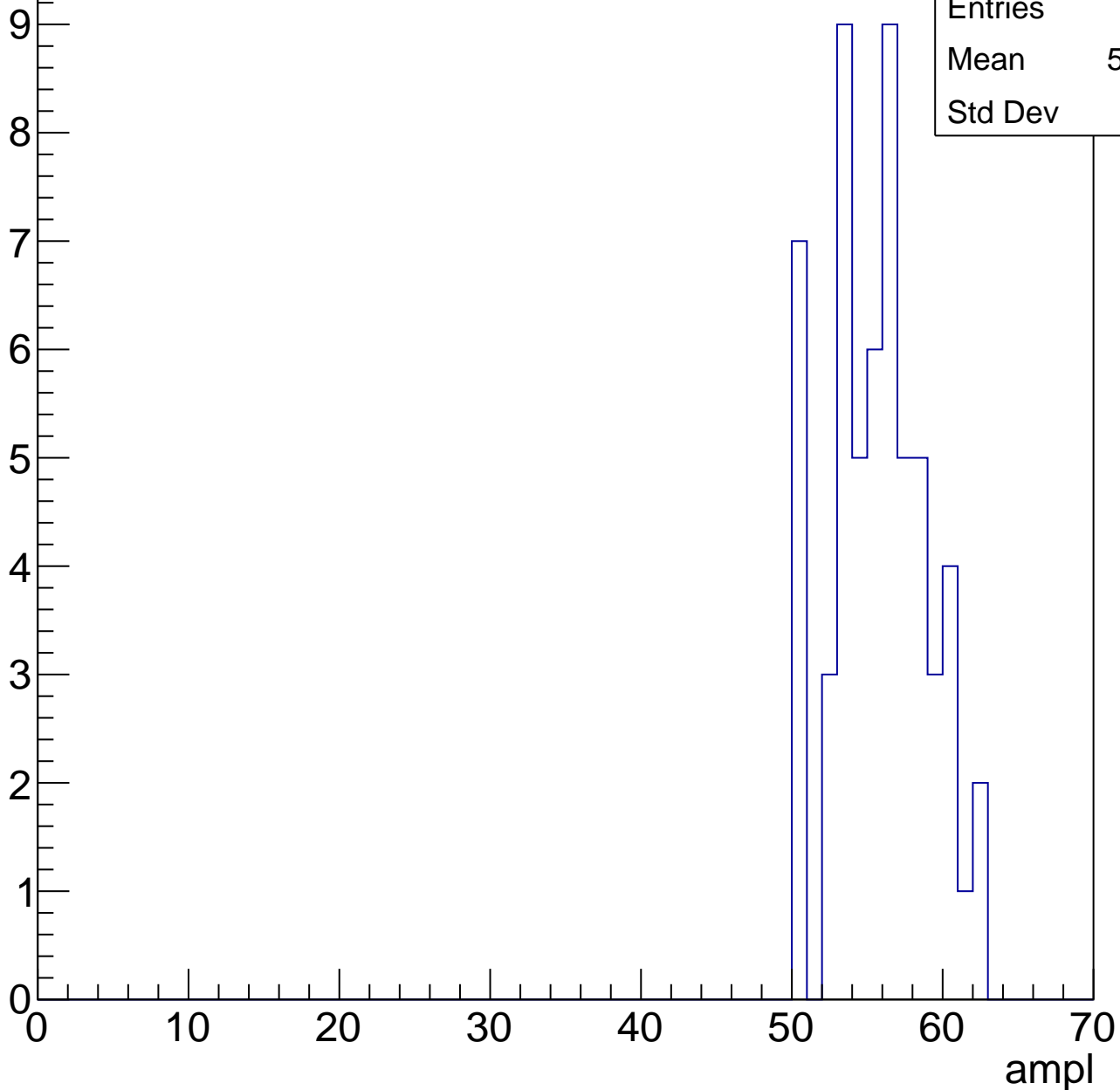


B1L100S, U15-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

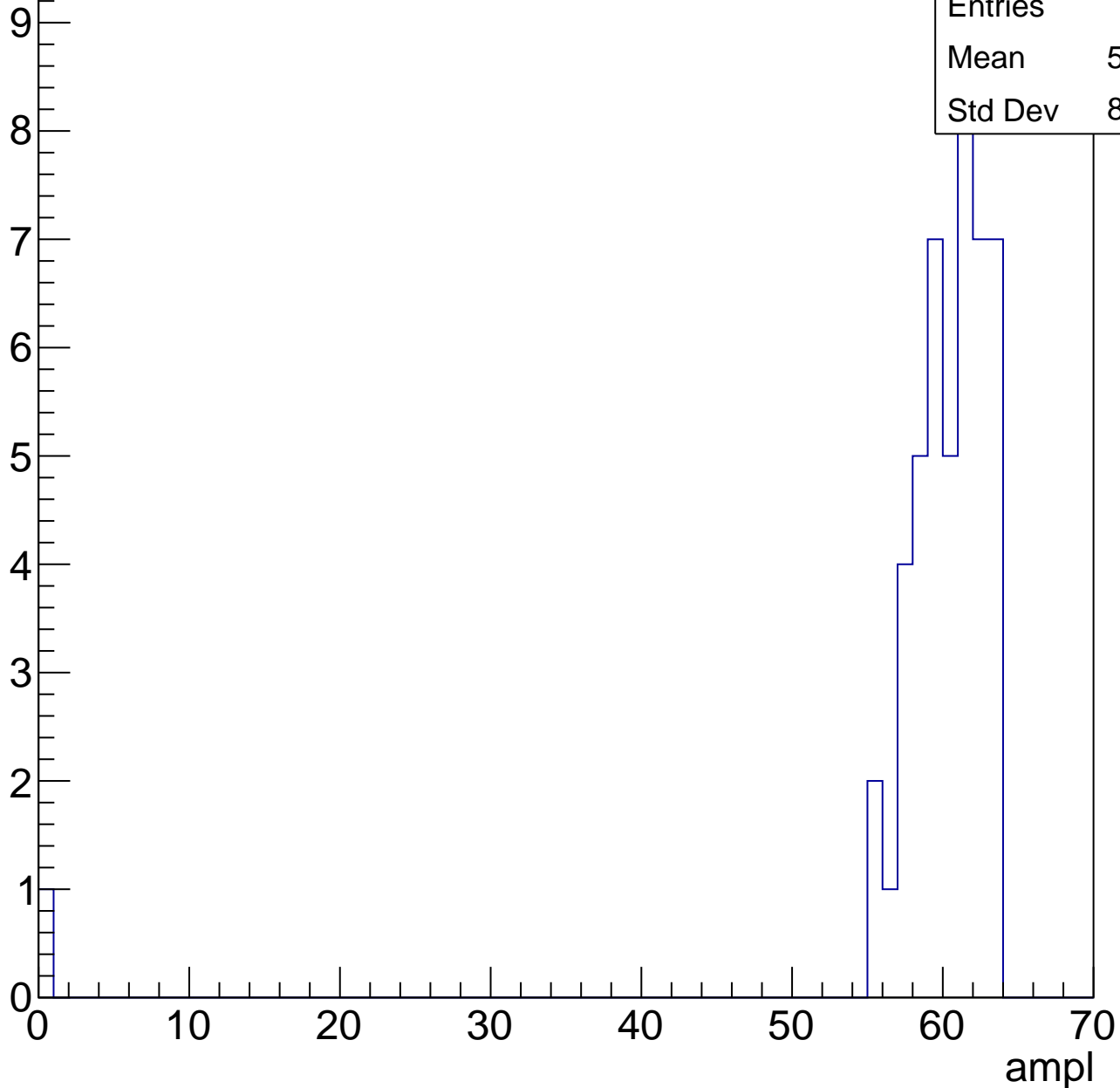
Entries	59
Mean	55.32
Std Dev	3.17



B1L100S, U15-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch77, adc0

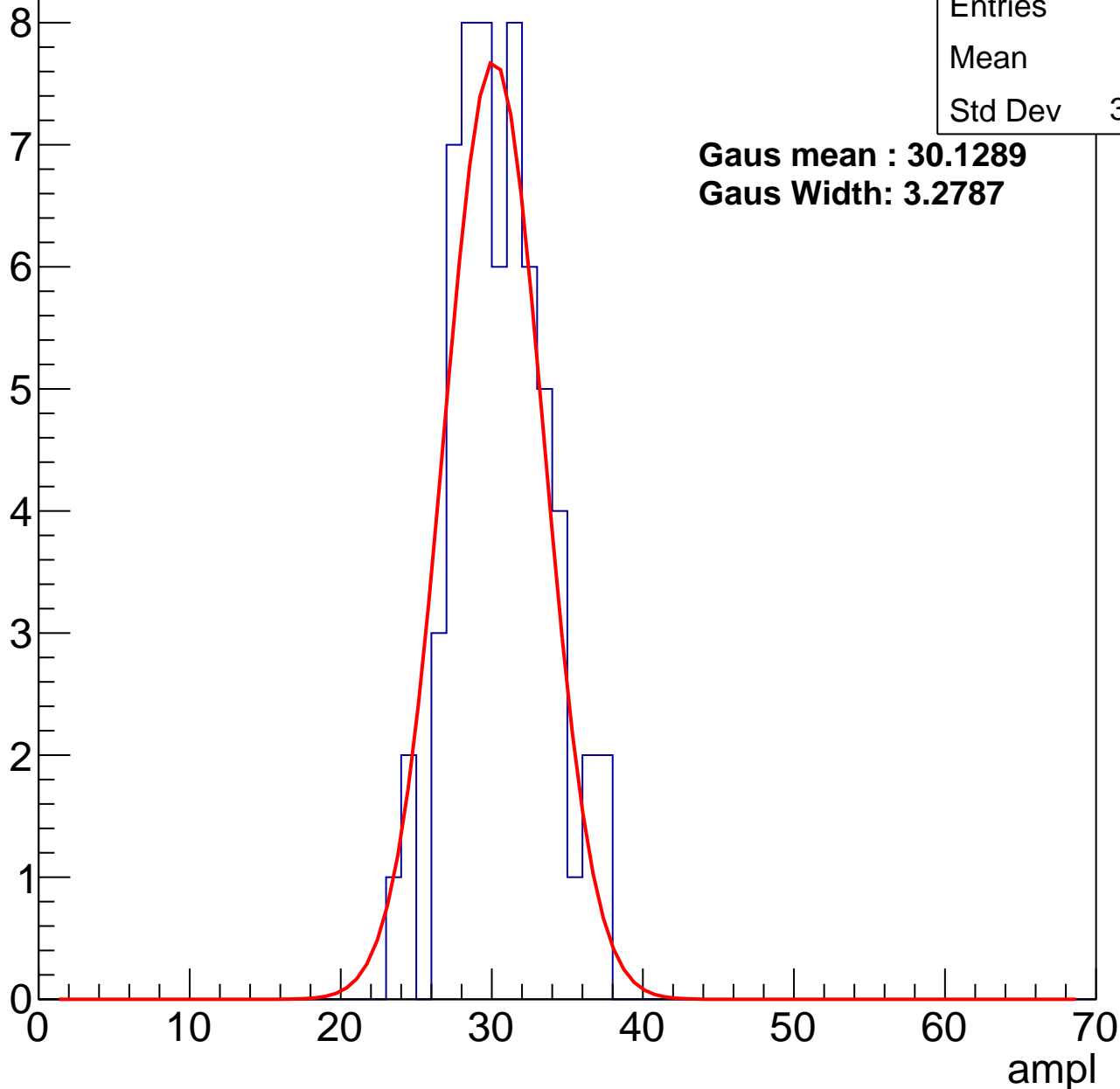
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	30.1
Std Dev	3.105

Gaus mean : 30.1289

Gaus Width: 3.2787



B1L100S, U15-ch77, adc1

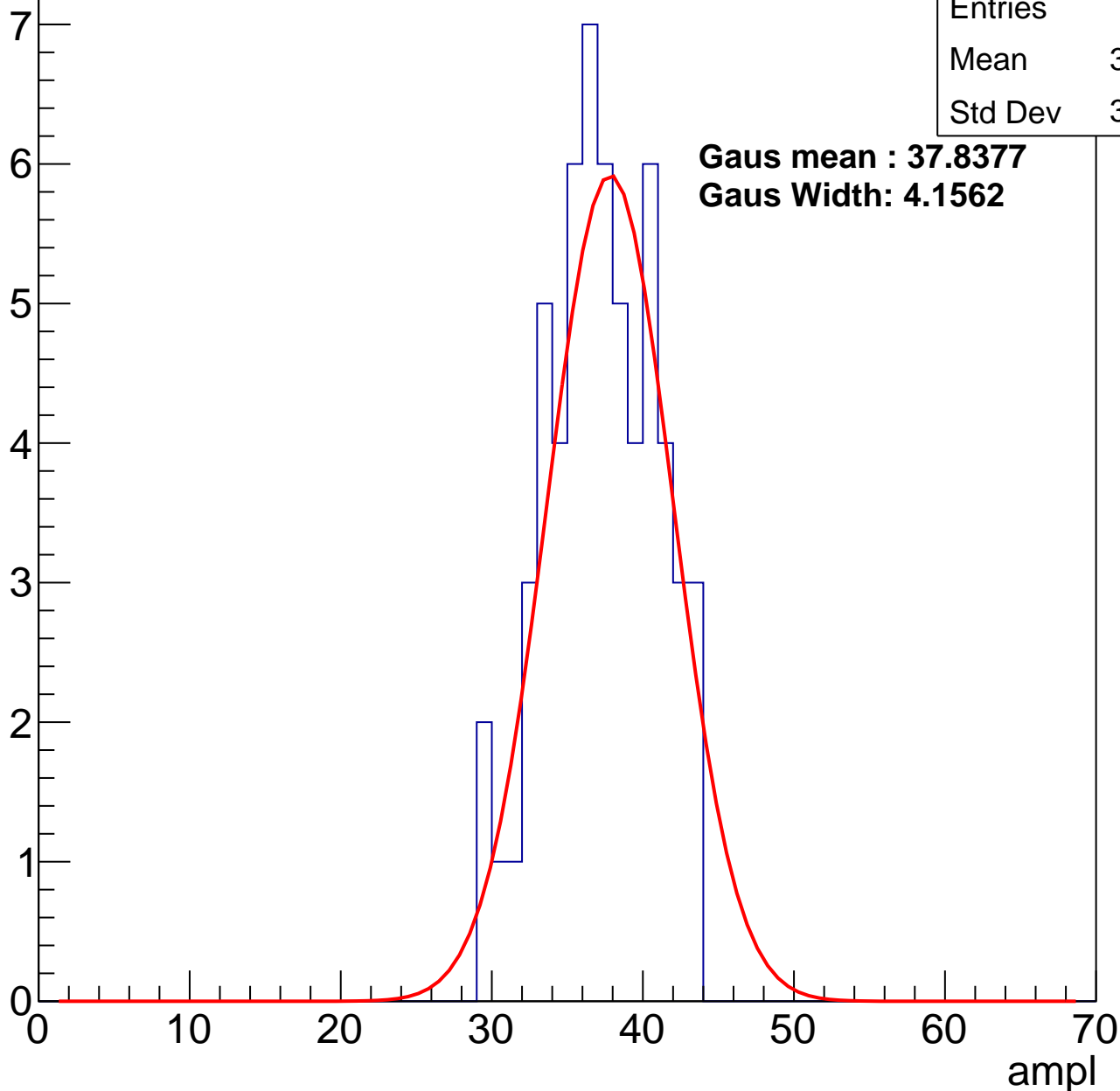
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	36.75
Std Dev	3.543

Gaus mean : 37.8377

Gaus Width: 4.1562



B1L100S, U15-ch77, adc2

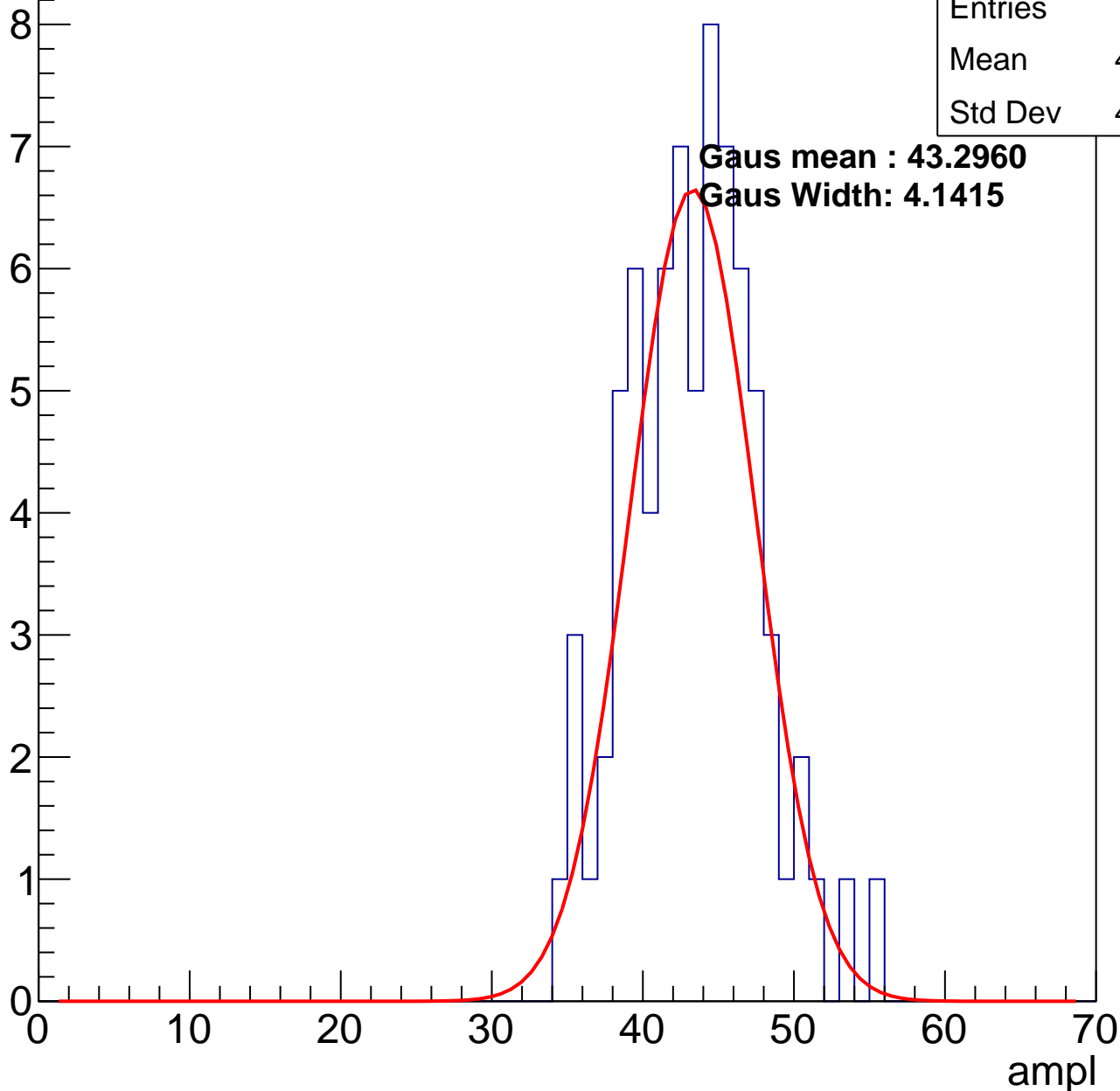
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	42.91
Std Dev	4.271

Gaus mean : 43.2960

Gaus Width: 4.1415

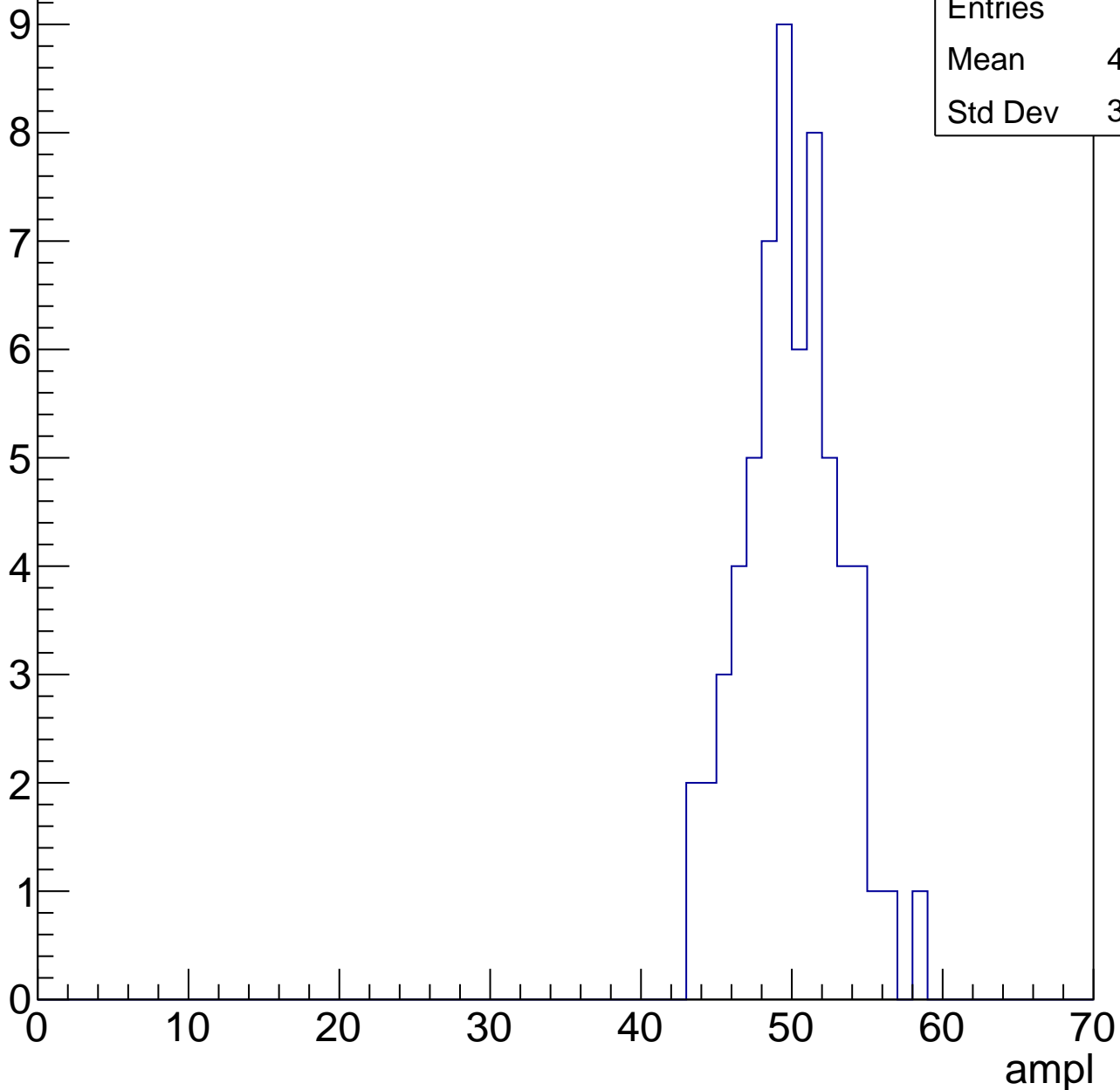


B1L100S, U15-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	49.52
Std Dev	3.186

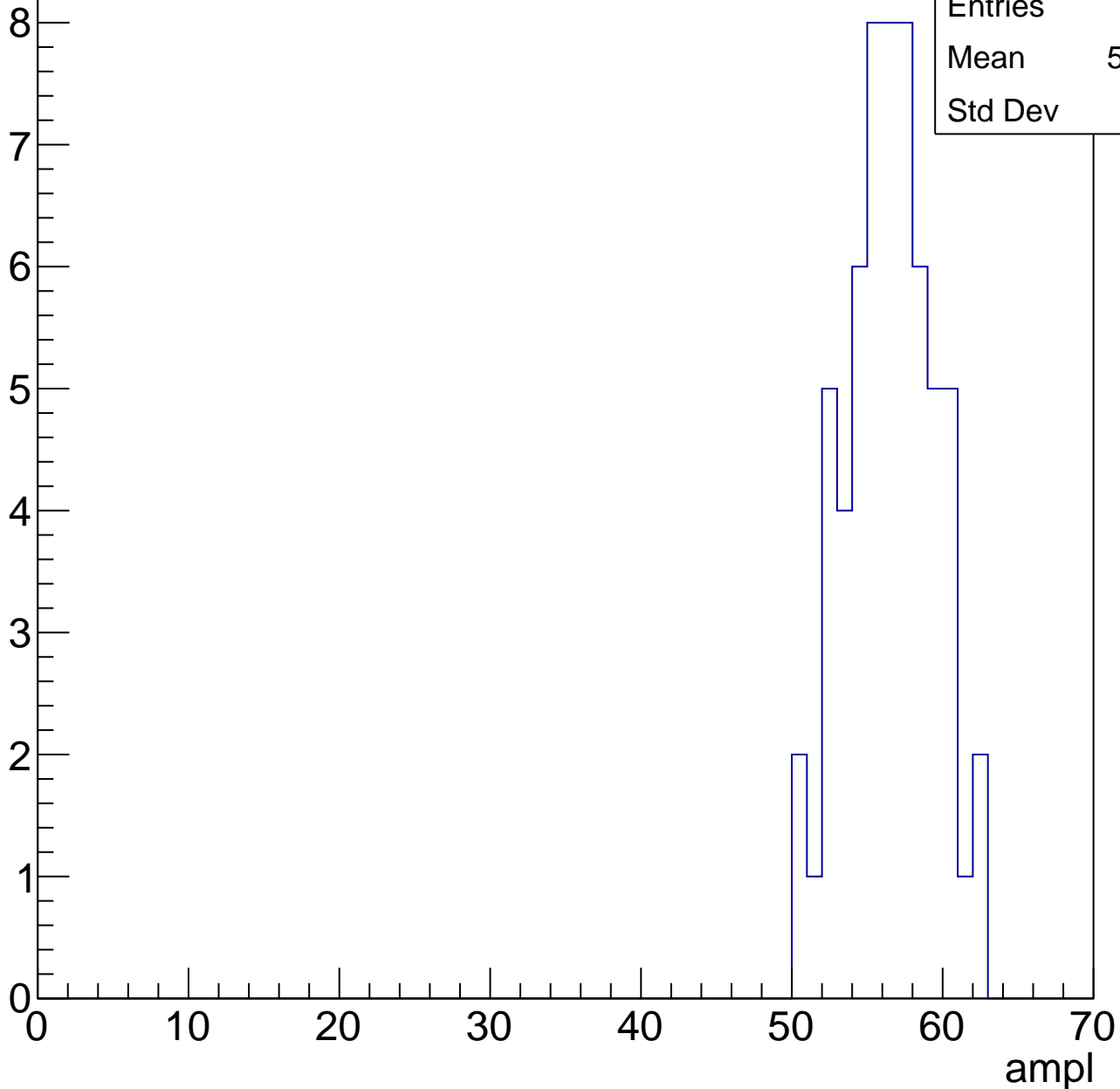


B1L100S, U15-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	56.05
Std Dev	2.86

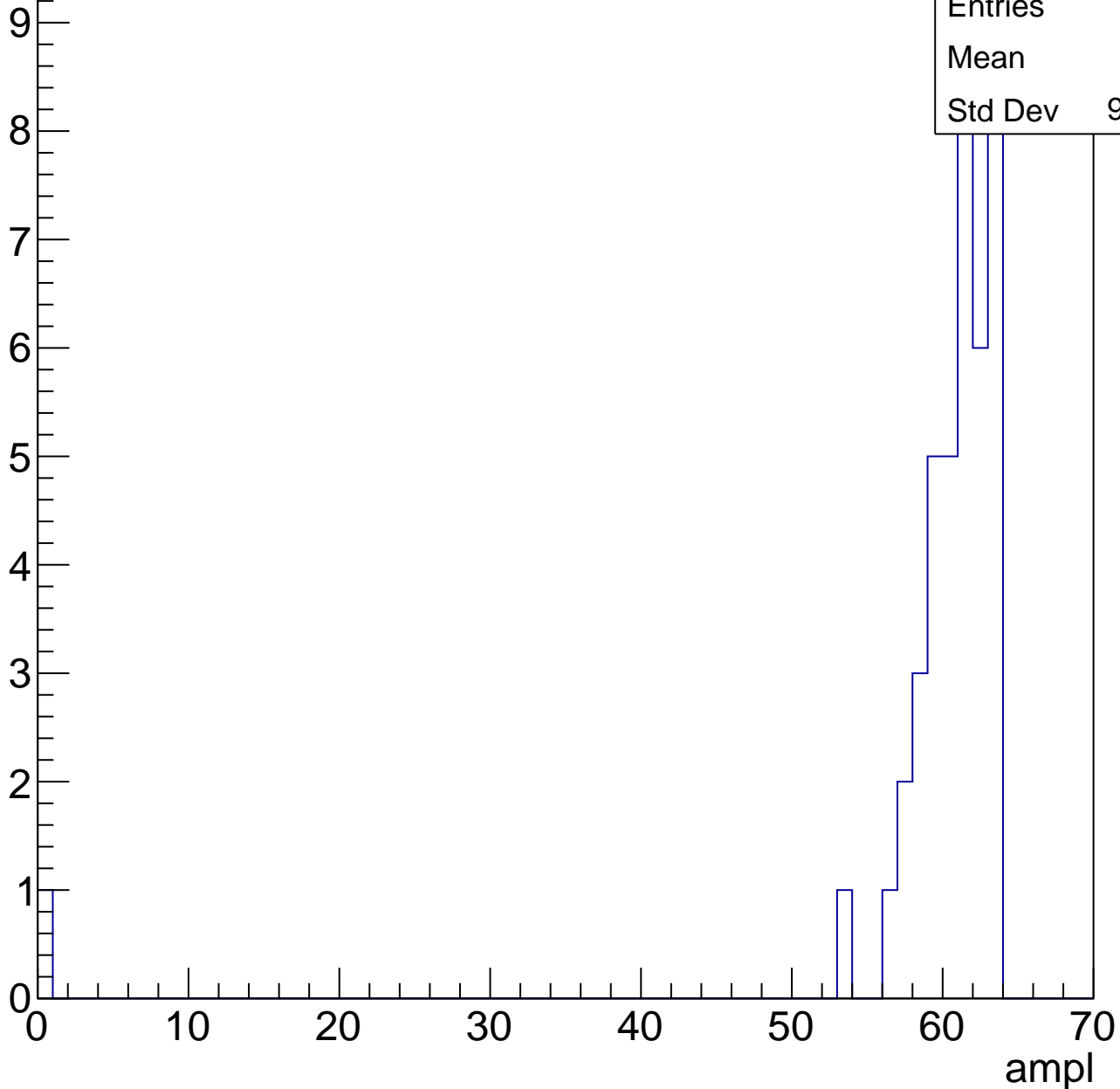


B1L100S, U15-ch77, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	59
Std Dev	9.592



B1L100S, U15-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch78, adc0

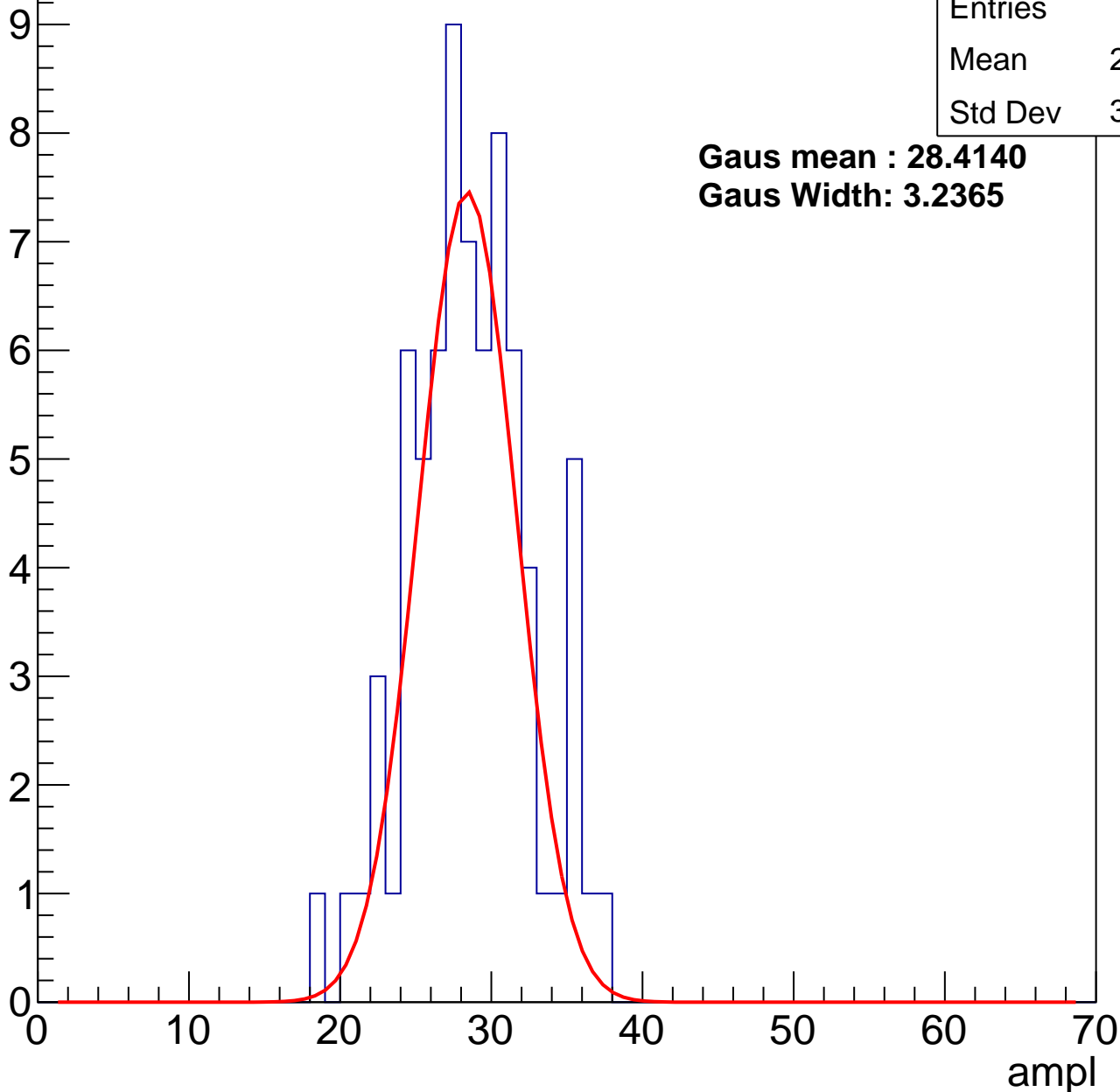
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.15
Std Dev	3.926

Gaus mean : 28.4140

Gaus Width: 3.2365



B1L100S, U15-ch78, adc1

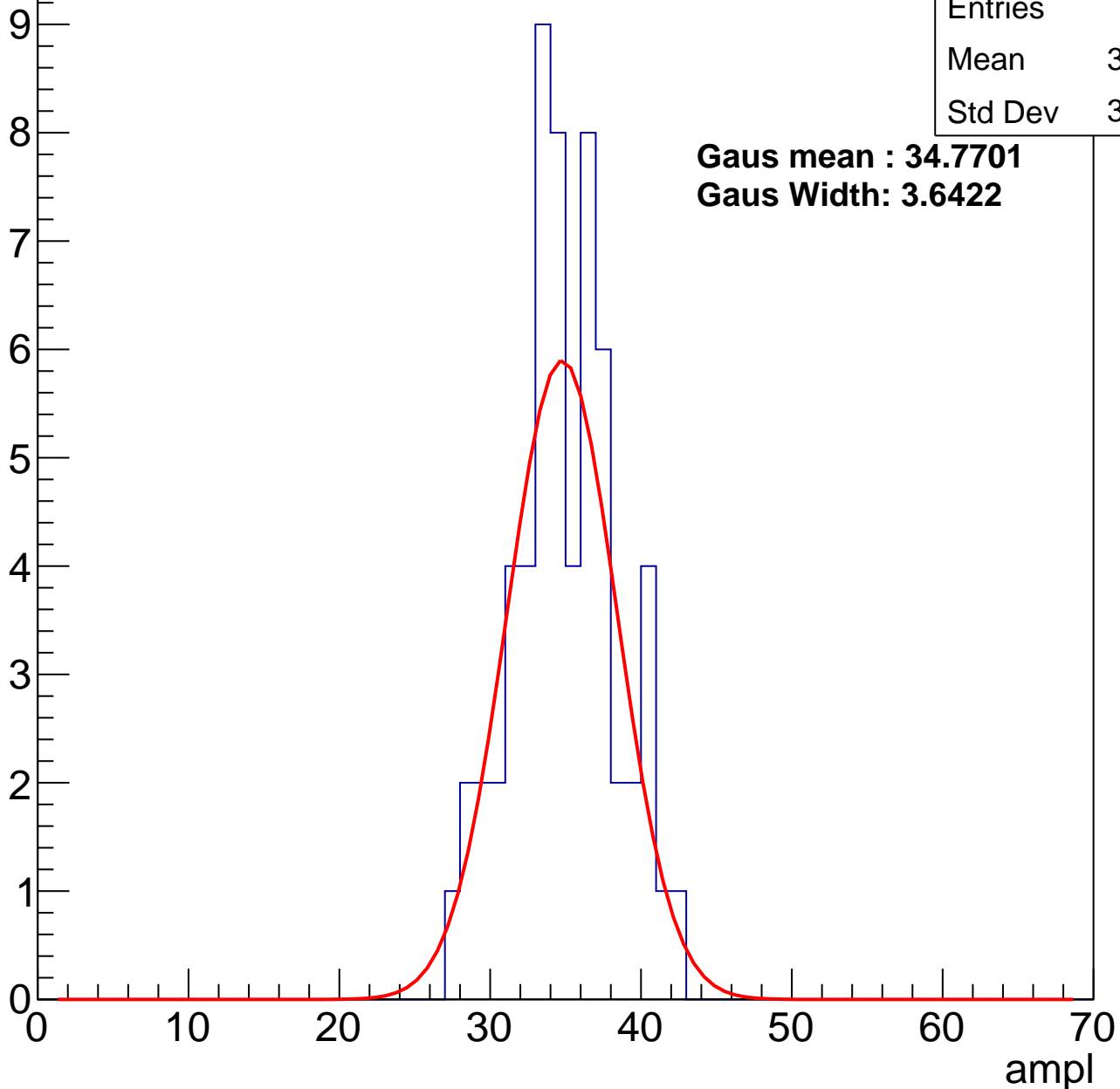
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	34.48
Std Dev	3.364

Gaus mean : 34.7701

Gaus Width: 3.6422



B1L100S, U15-ch78, adc2

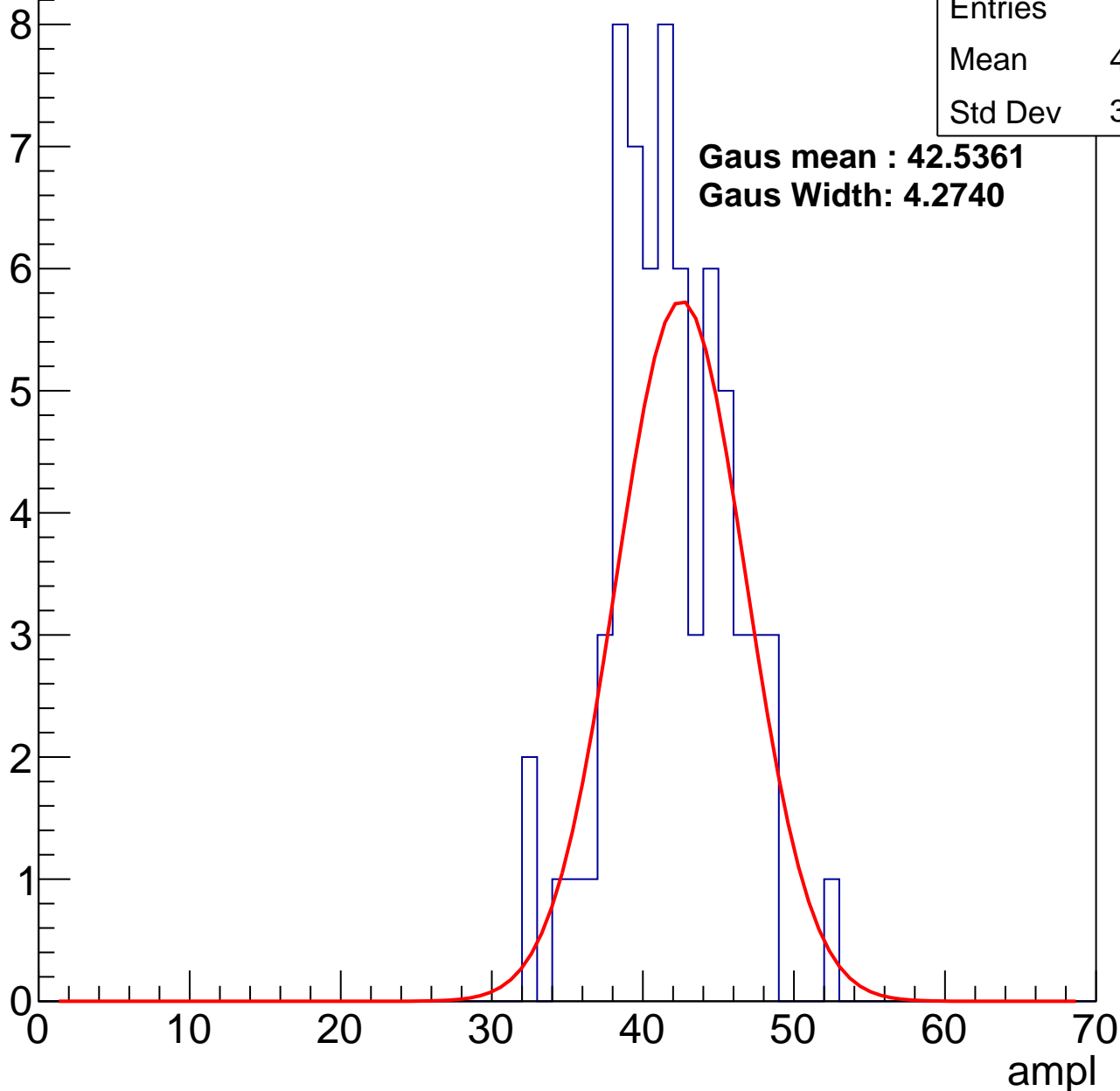
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	41.34
Std Dev	3.904

Gaus mean : 42.5361

Gaus Width: 4.2740

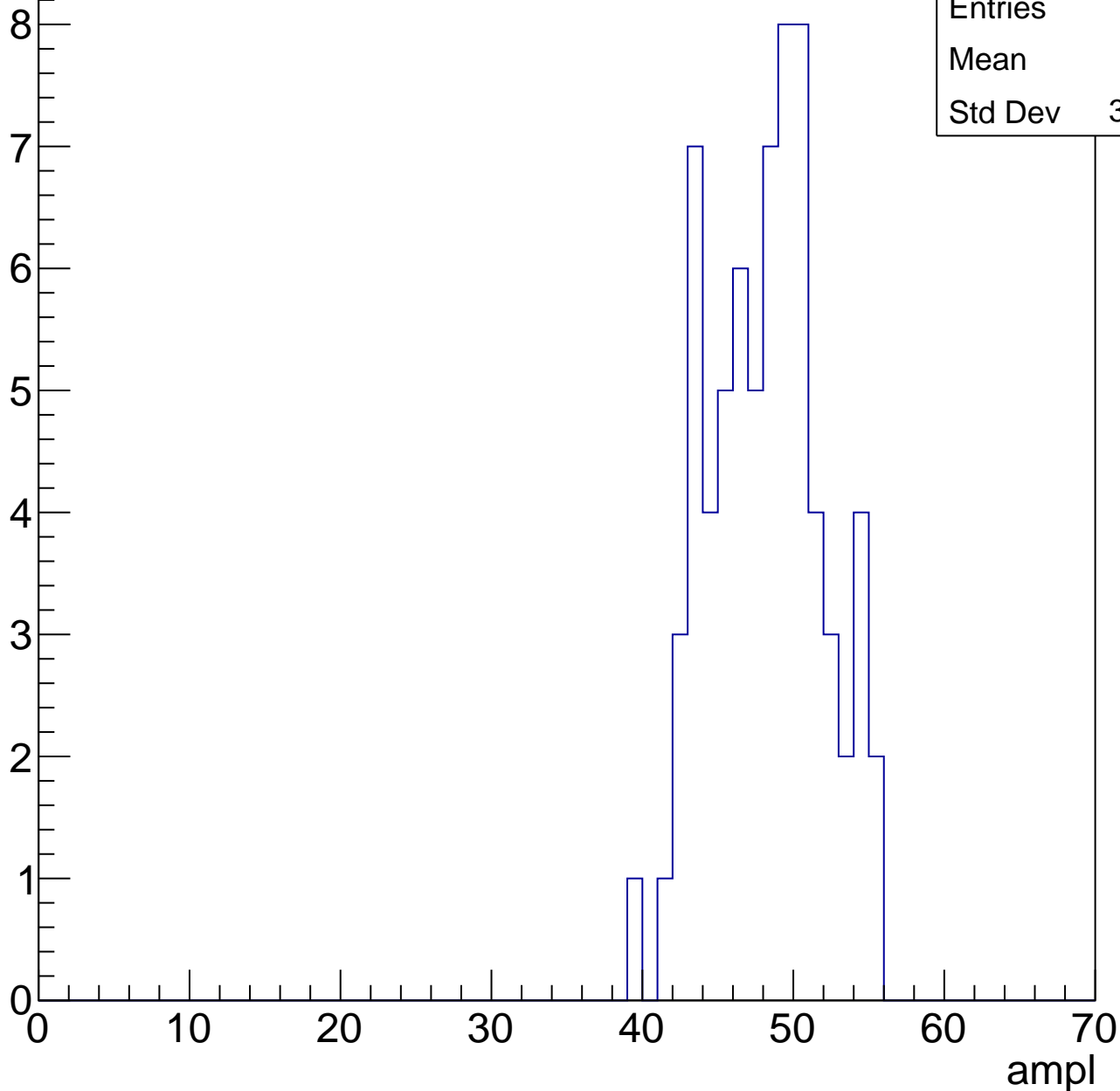


B1L100S, U15-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	47.7
Std Dev	3.705

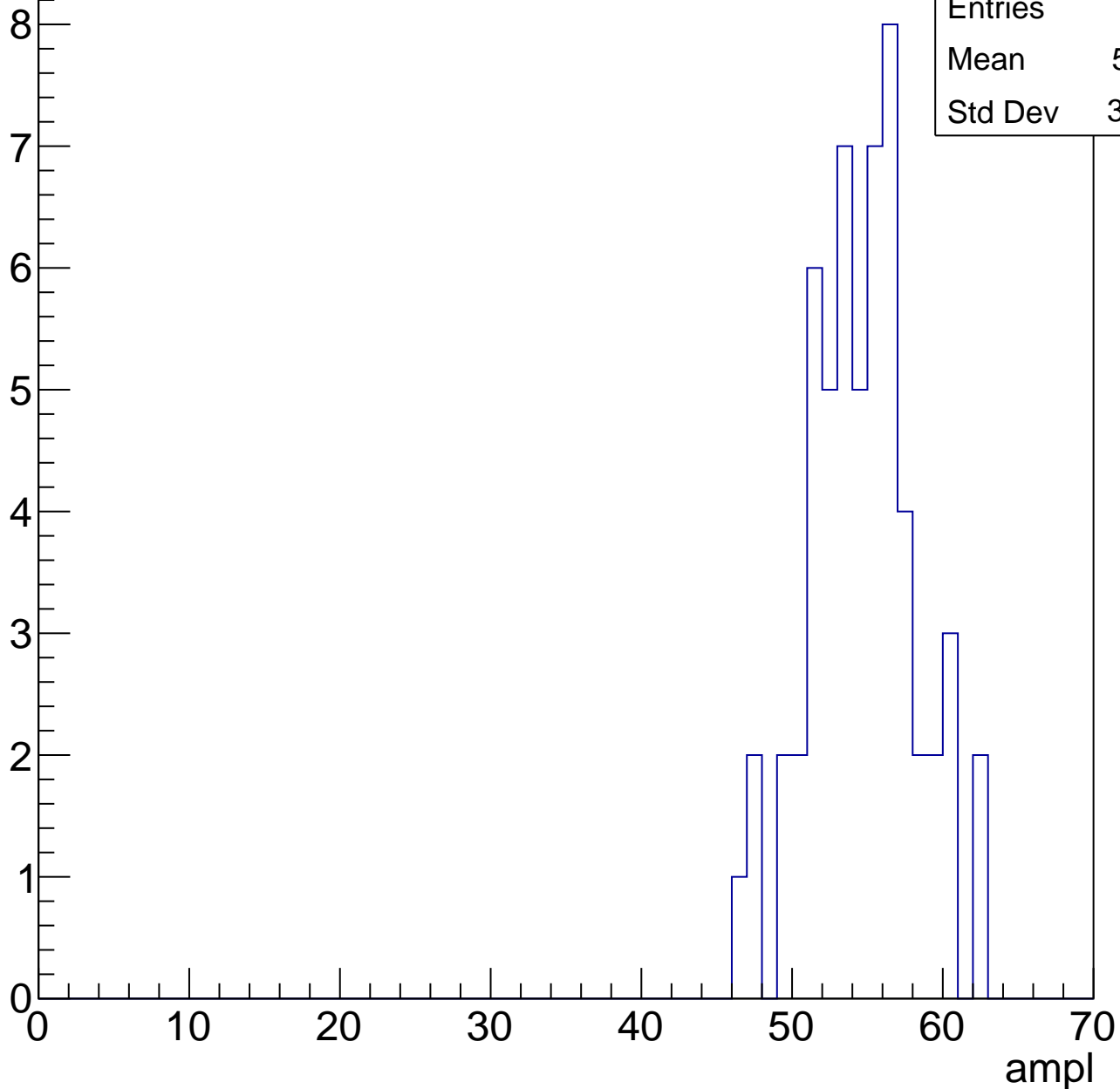


B1L100S, U15-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	54.21
Std Dev	3.512

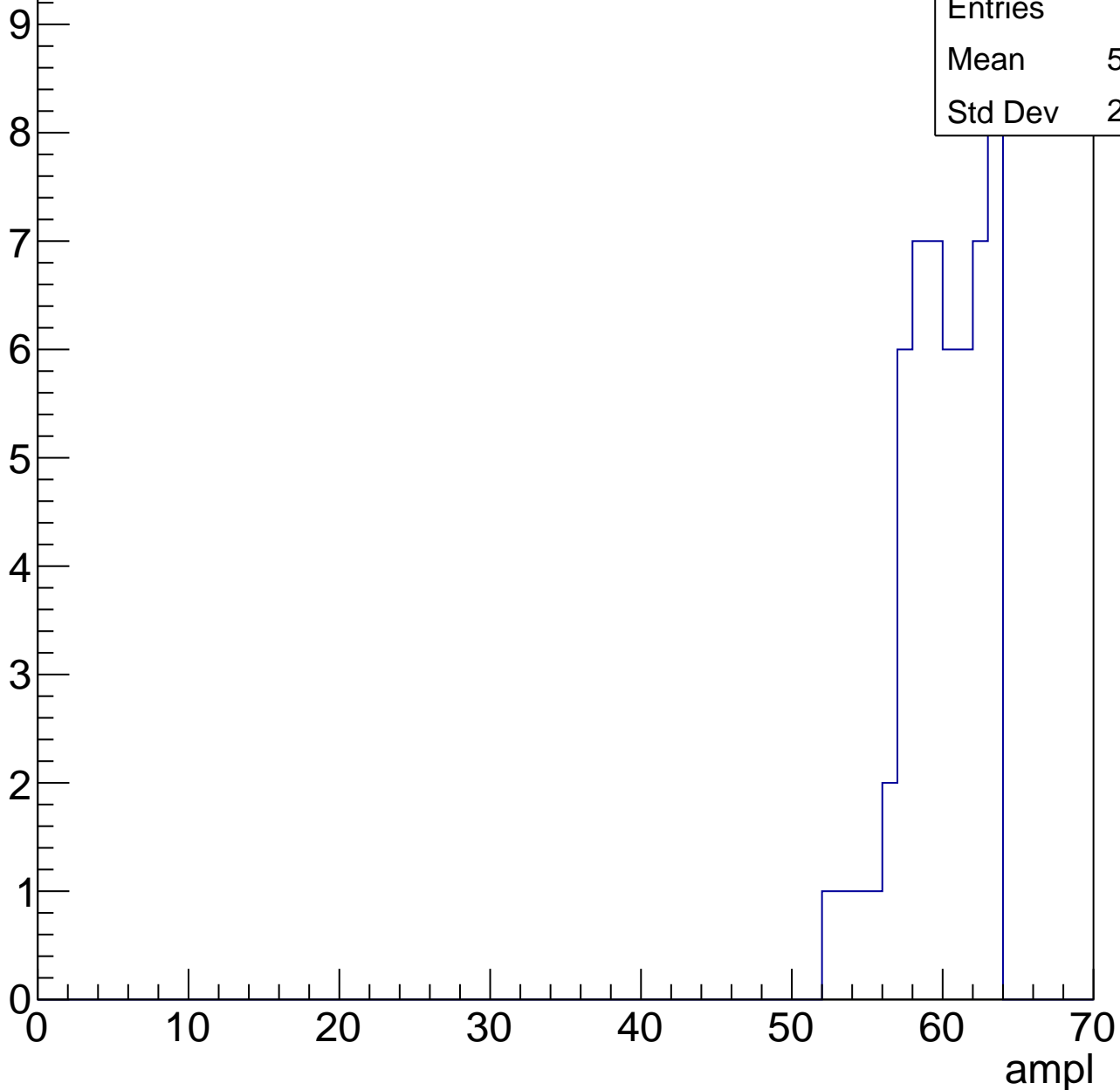


B1L100S, U15-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

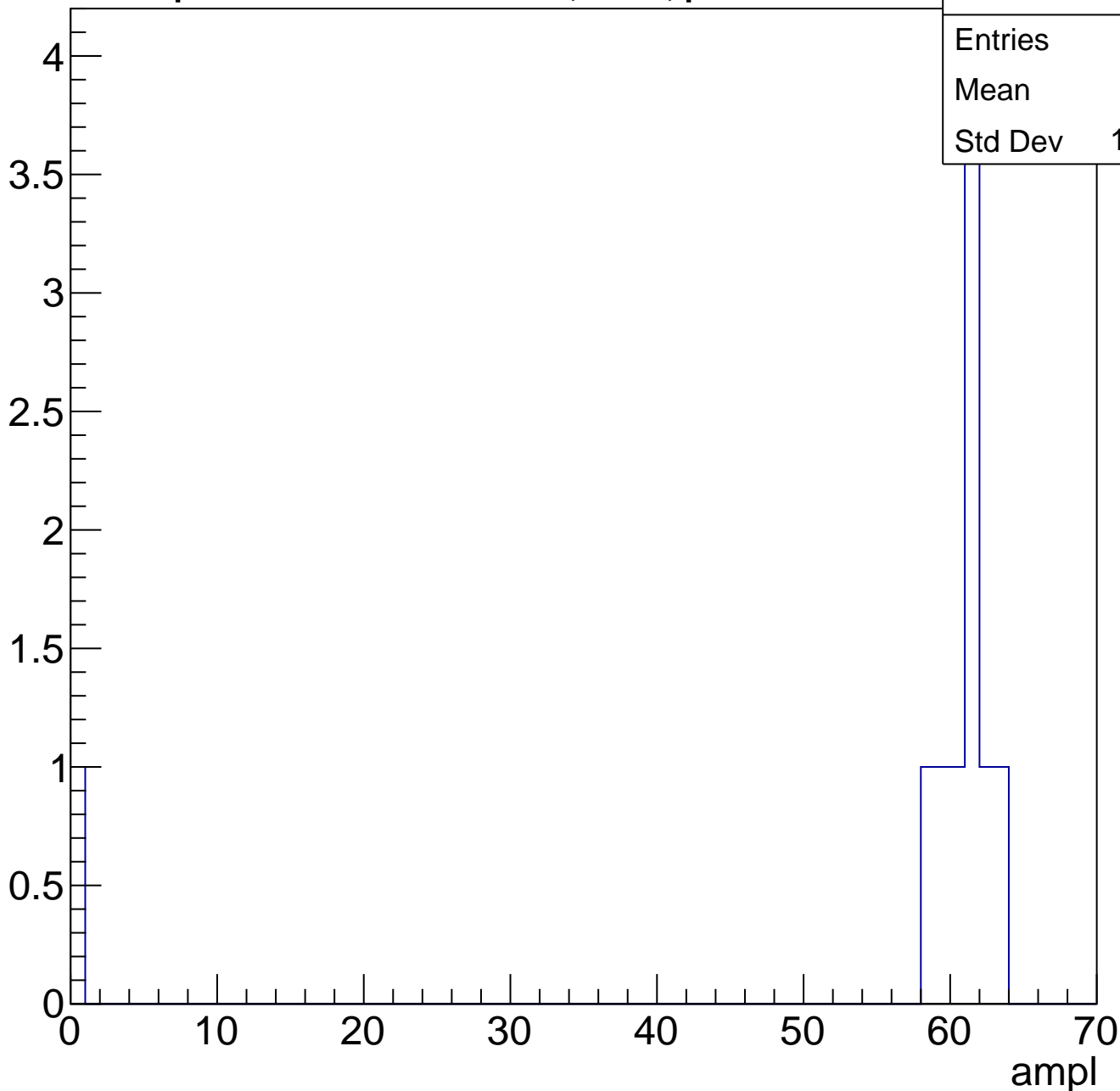
Entries	54
Mean	59.52
Std Dev	2.713



B1L100S, U15-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch79, adc0

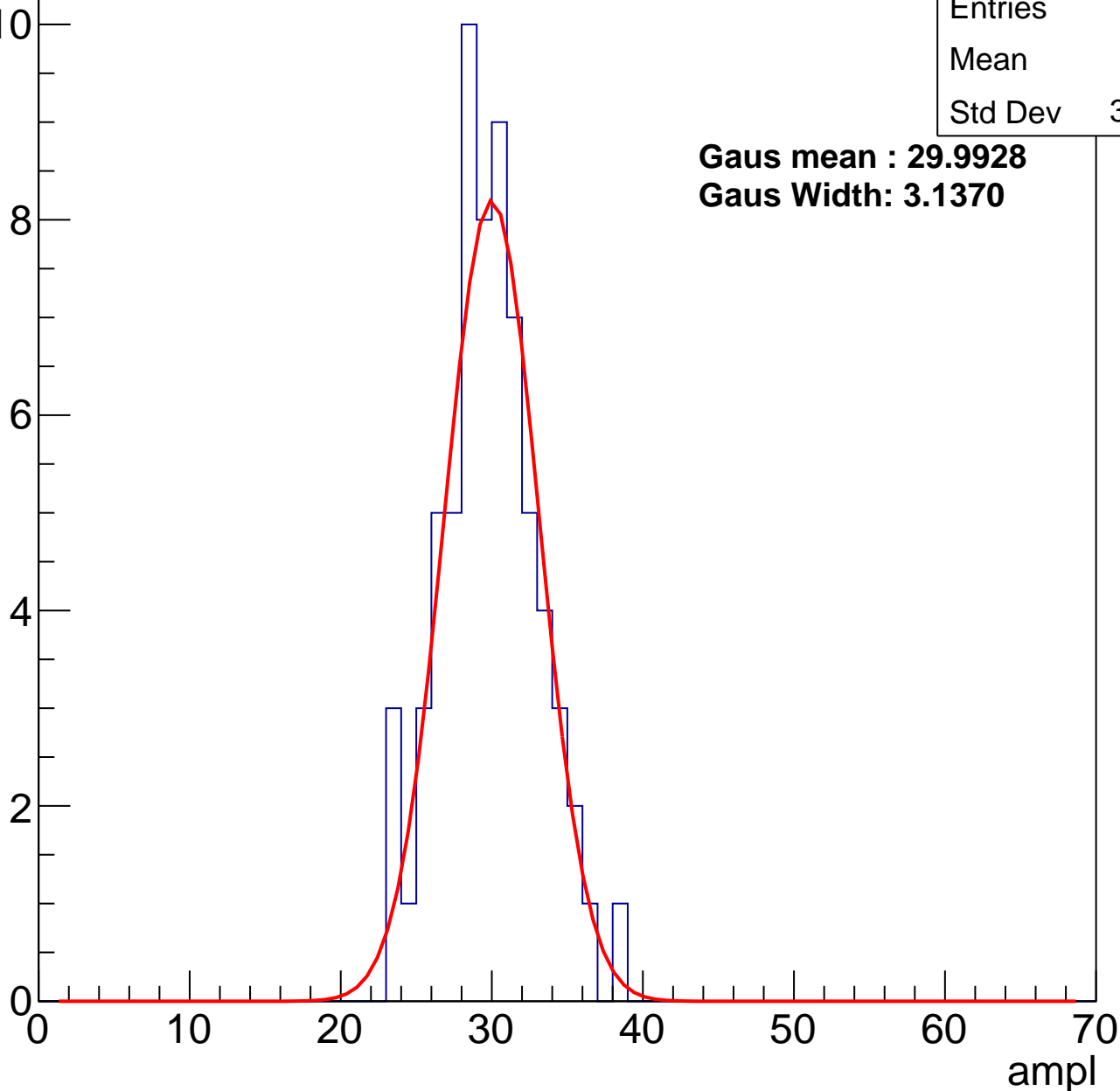
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	29.4
Std Dev	3.158

Gaus mean : 29.9928

Gaus Width: 3.1370



B1L100S, U15-ch79, adc1

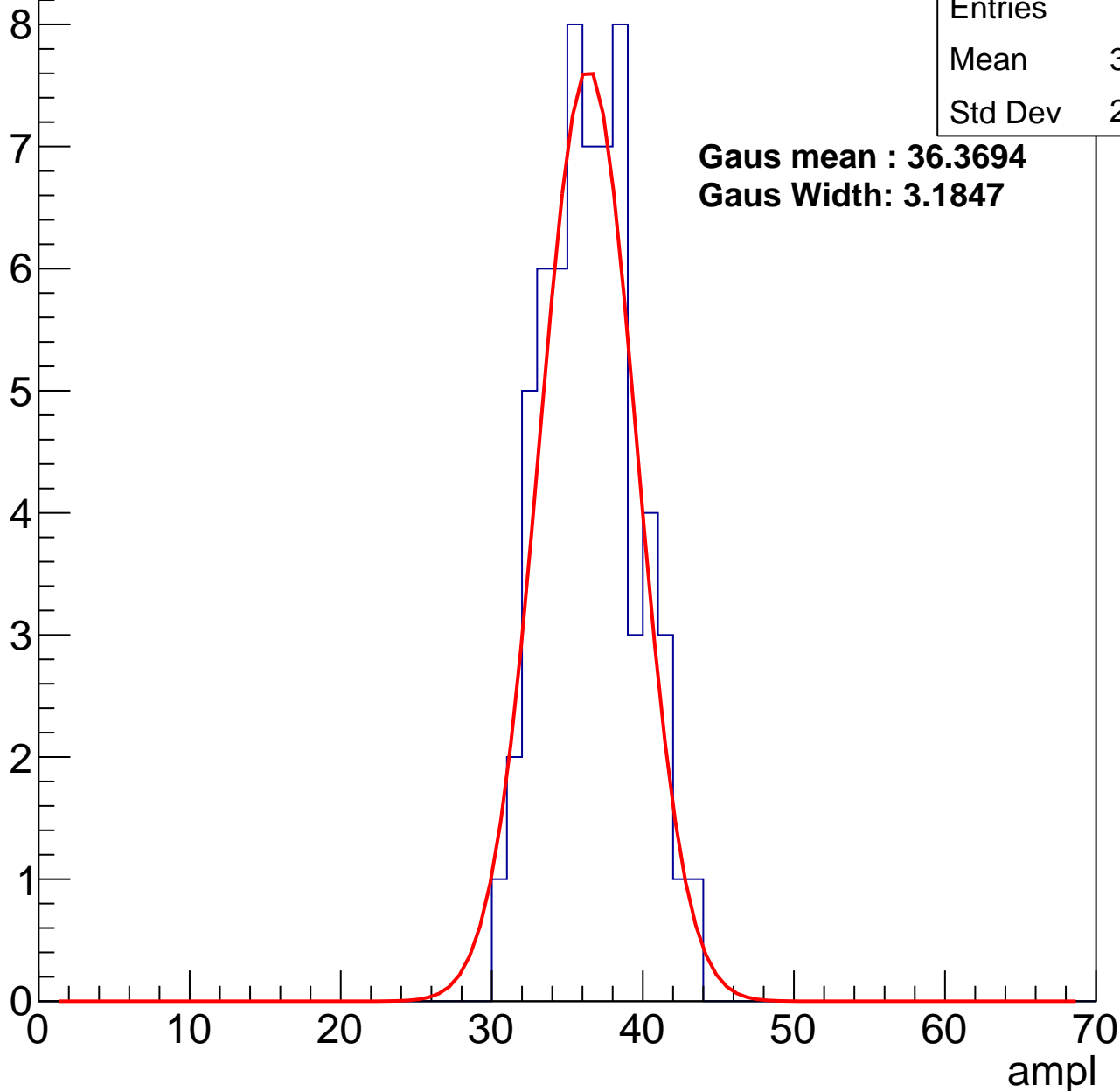
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.03
Std Dev	2.957

Gaus mean : 36.3694

Gaus Width: 3.1847



B1L100S, U15-ch79, adc2

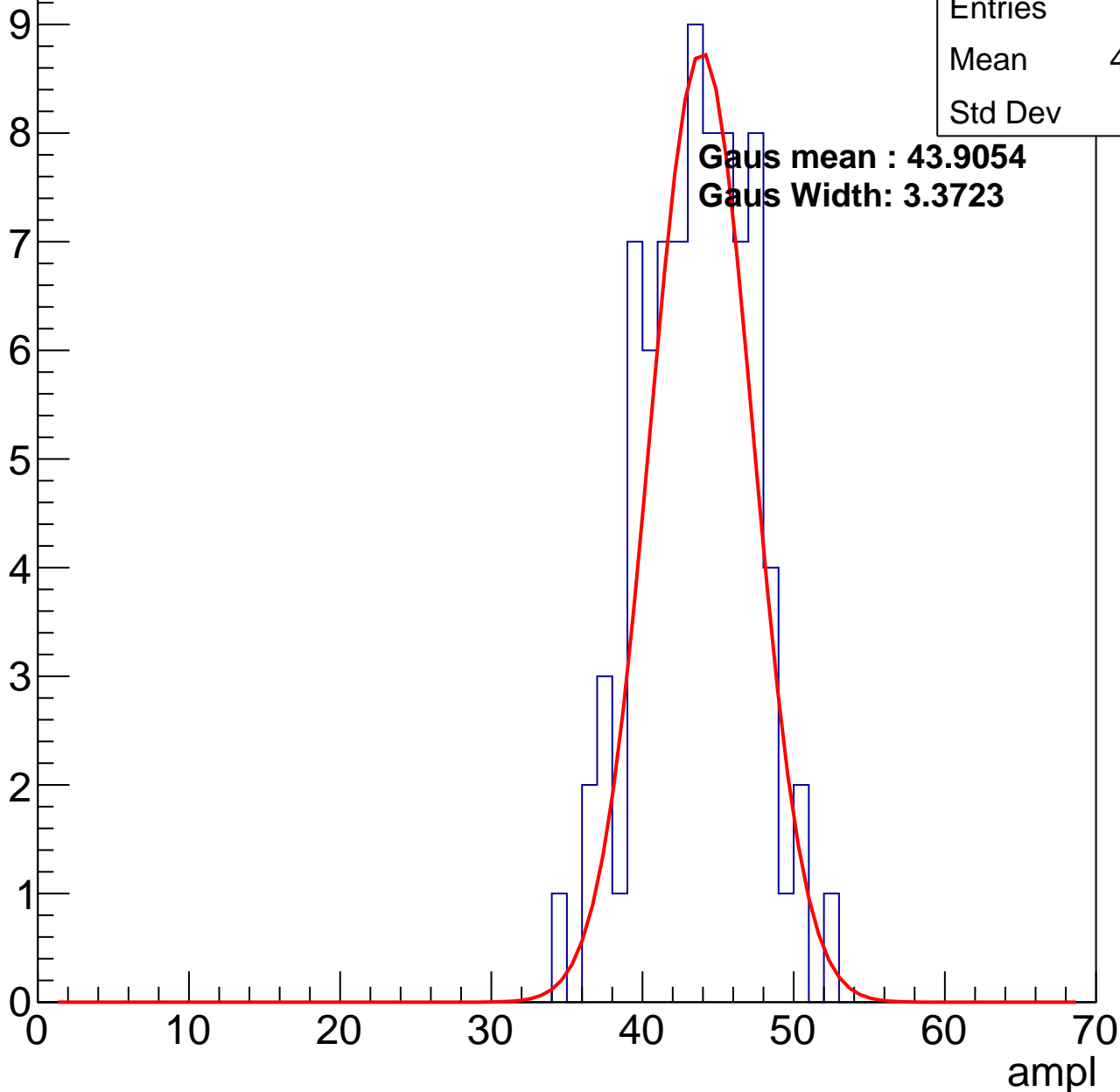
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	43.16
Std Dev	3.59

Gaus mean : 43.9054

Gaus Width: 3.3723

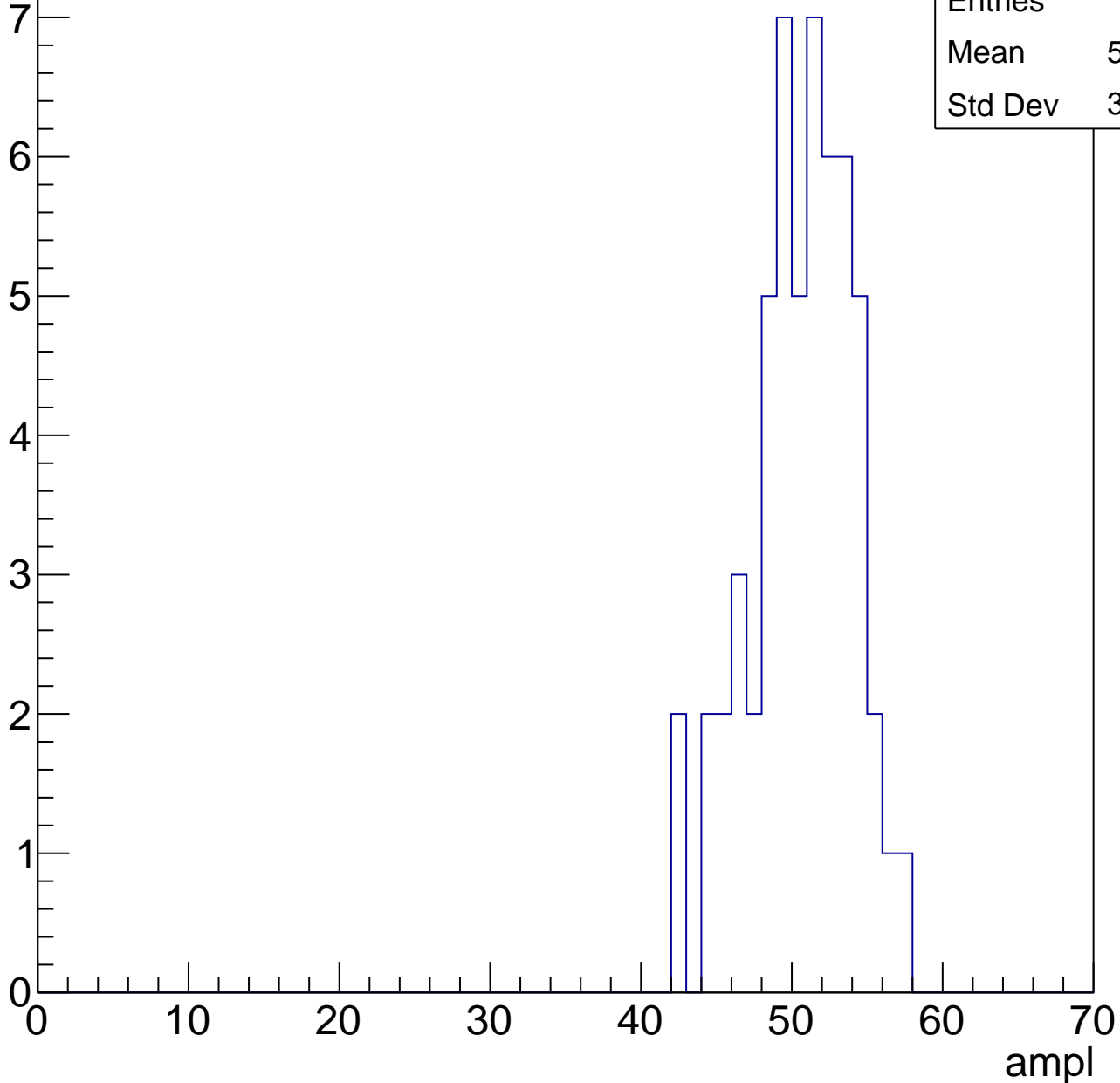


B1L100S, U15-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

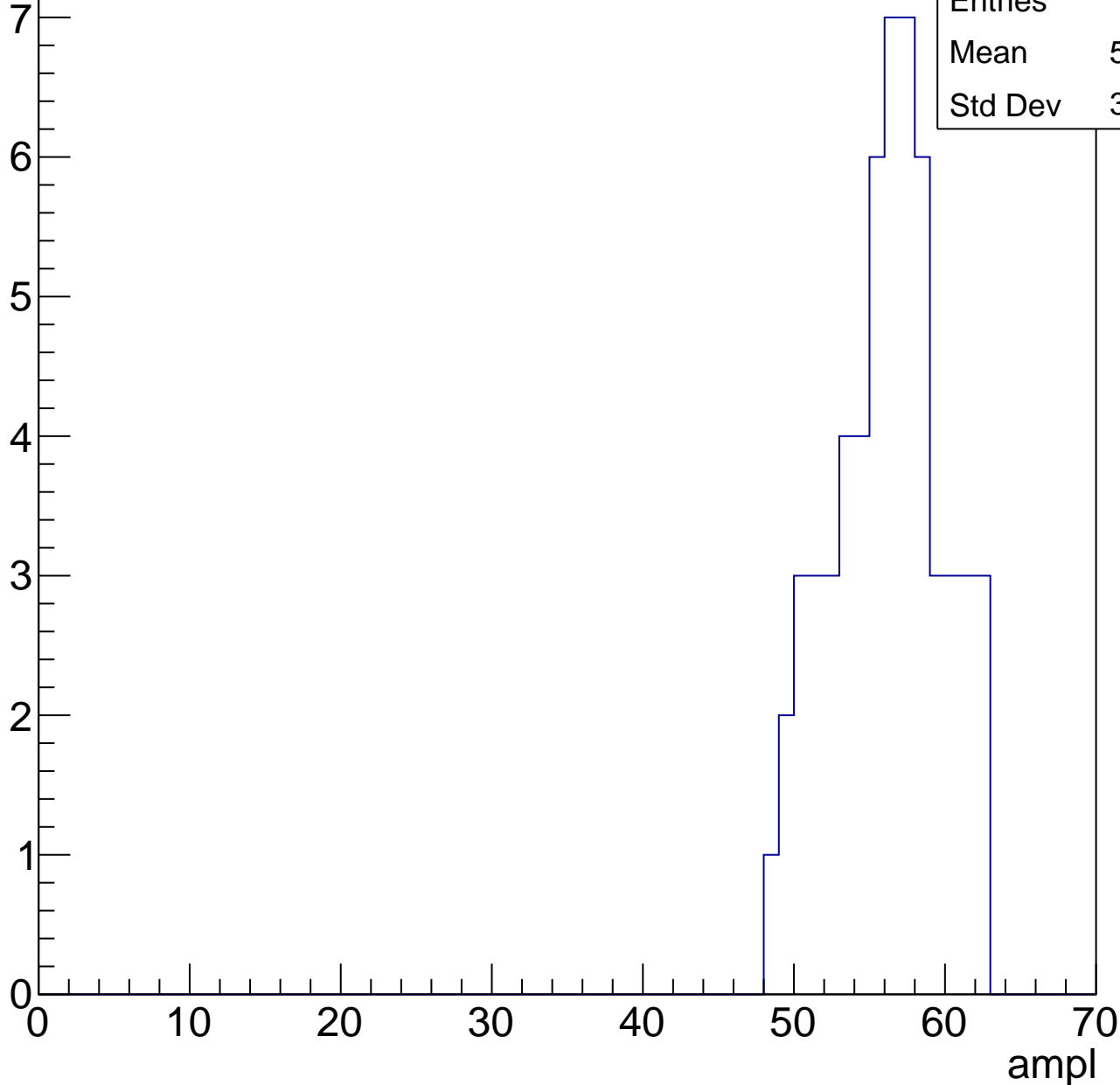
Entries	56
Mean	50.12
Std Dev	3.386



B1L100S, U15-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

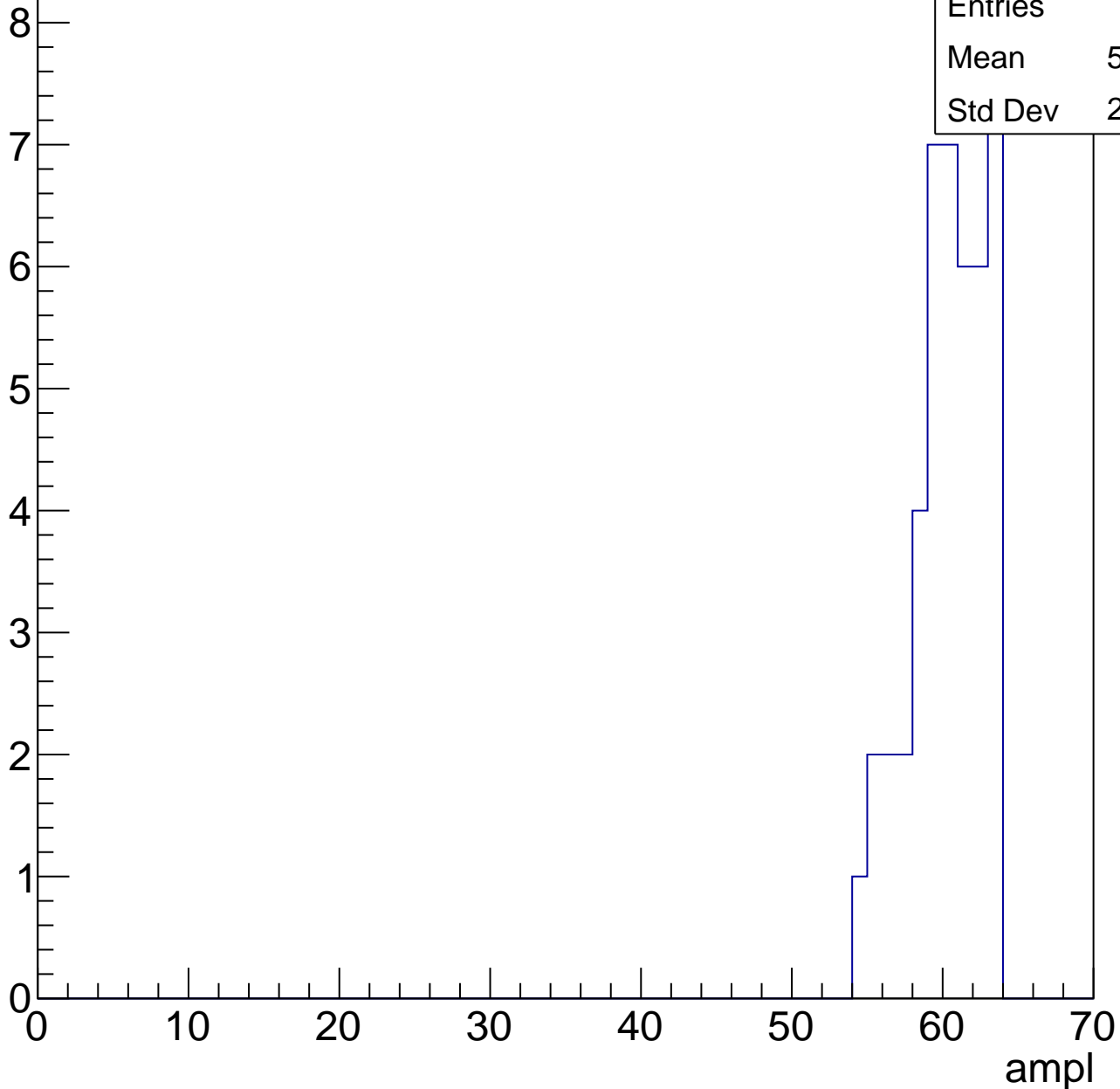


B1L100S, U15-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

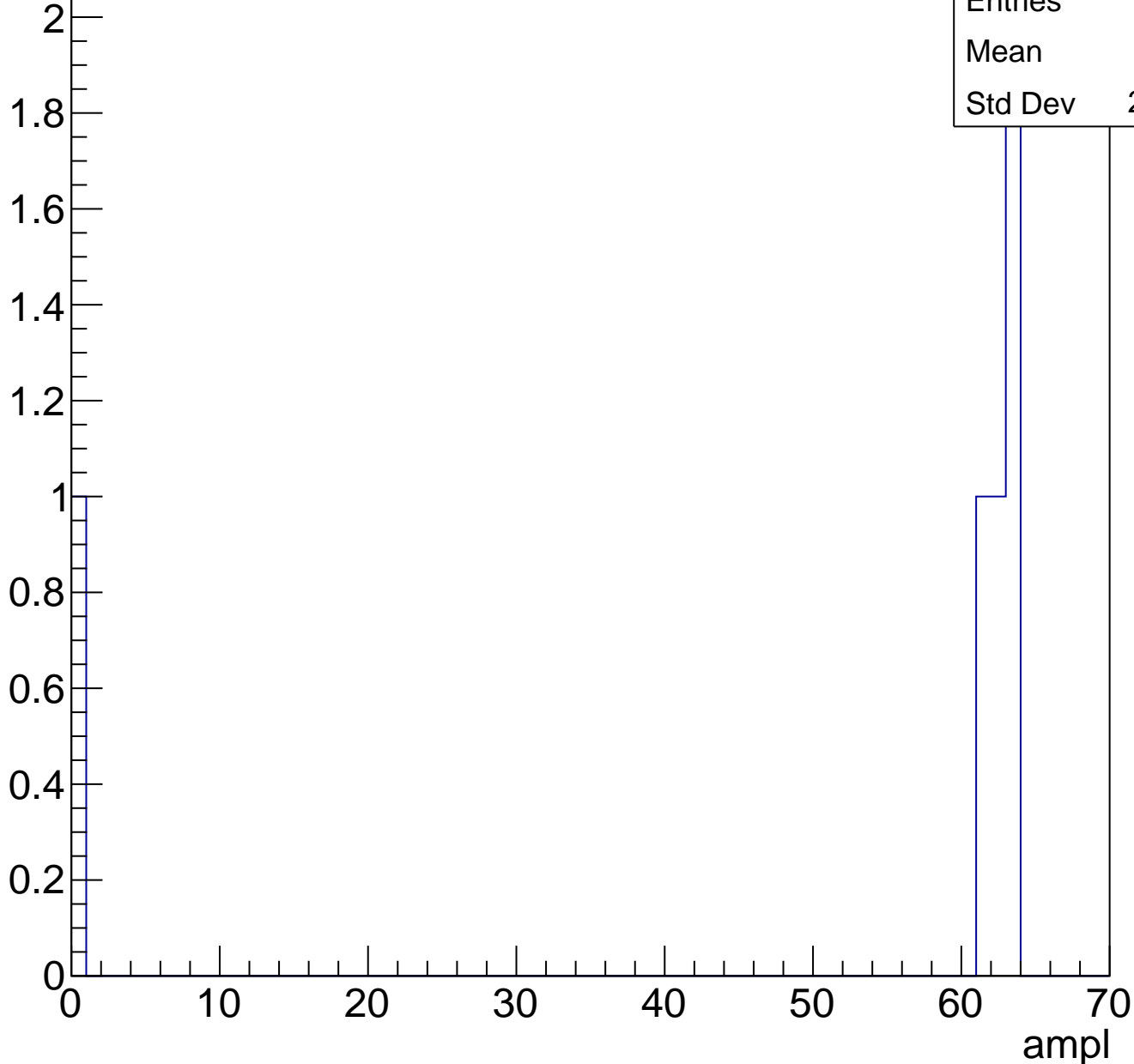
Entries	45
Mean	59.93
Std Dev	2.407



B1L100S, U15-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch80, adc0

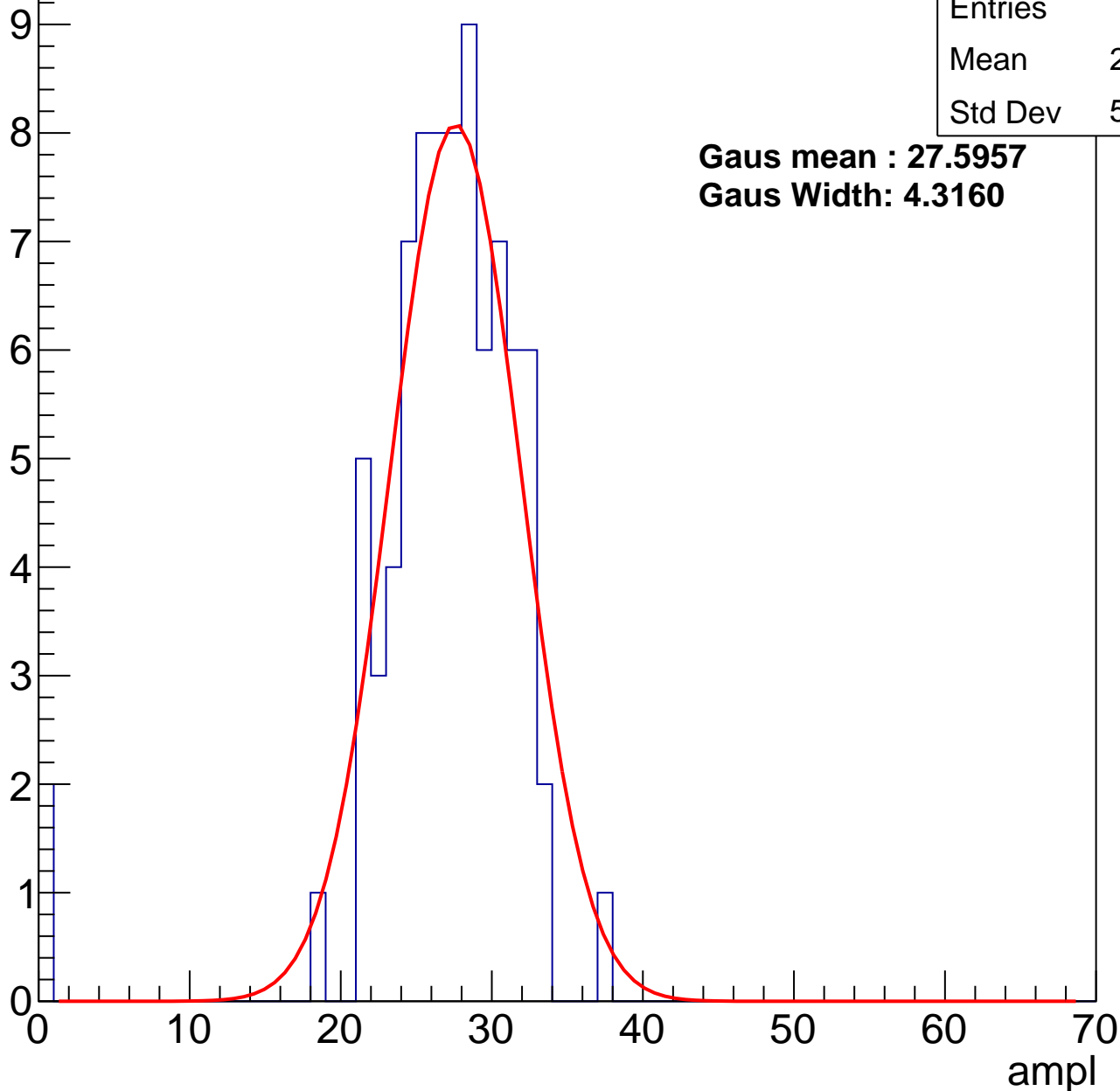
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	26.39
Std Dev	5.426

Gaus mean : 27.5957

Gaus Width: 4.3160



B1L100S, U15-ch80, adc1

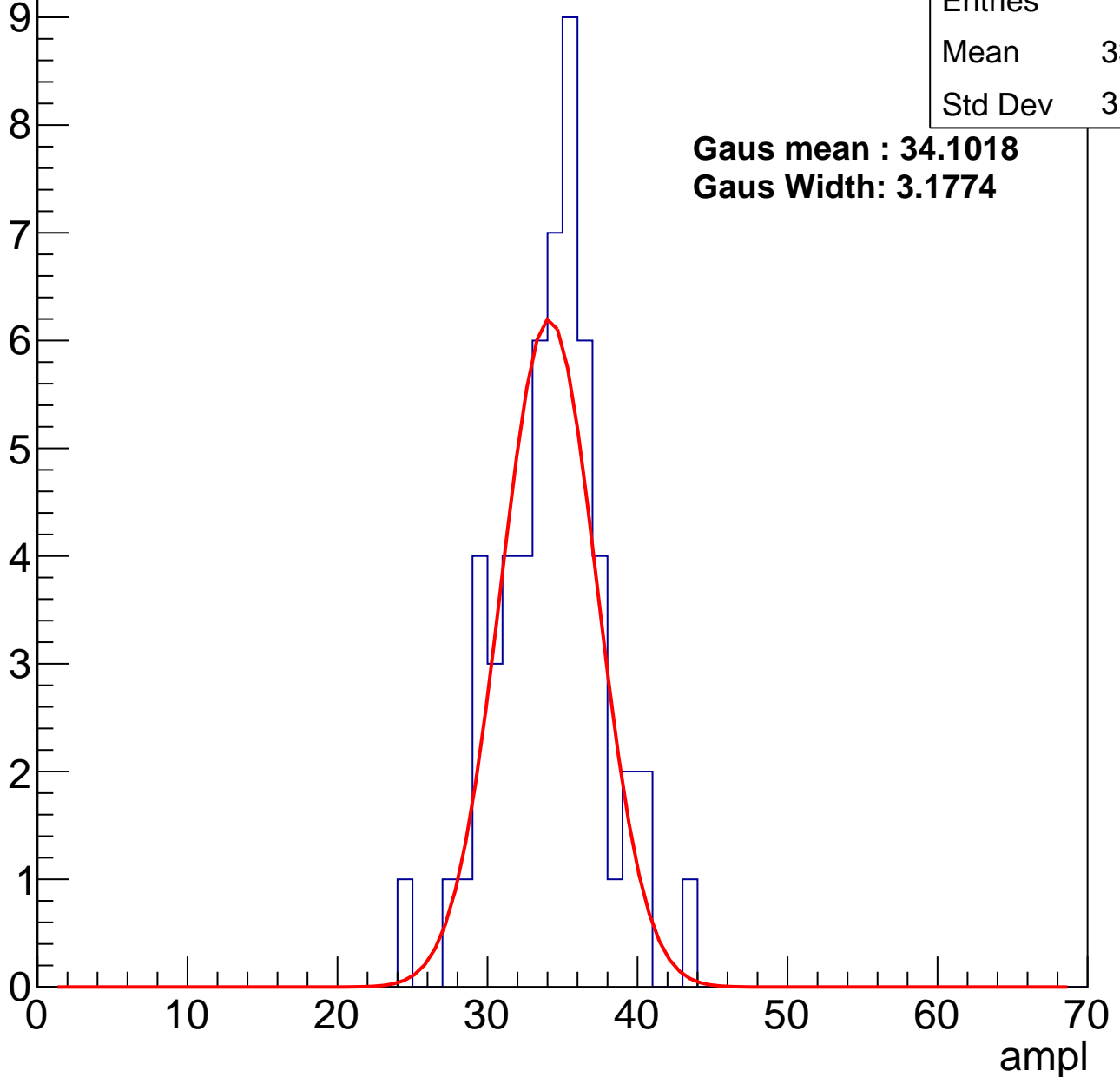
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	33.77
Std Dev	3.474

Gaus mean : 34.1018

Gaus Width: 3.1774



B1L100S, U15-ch80, adc2

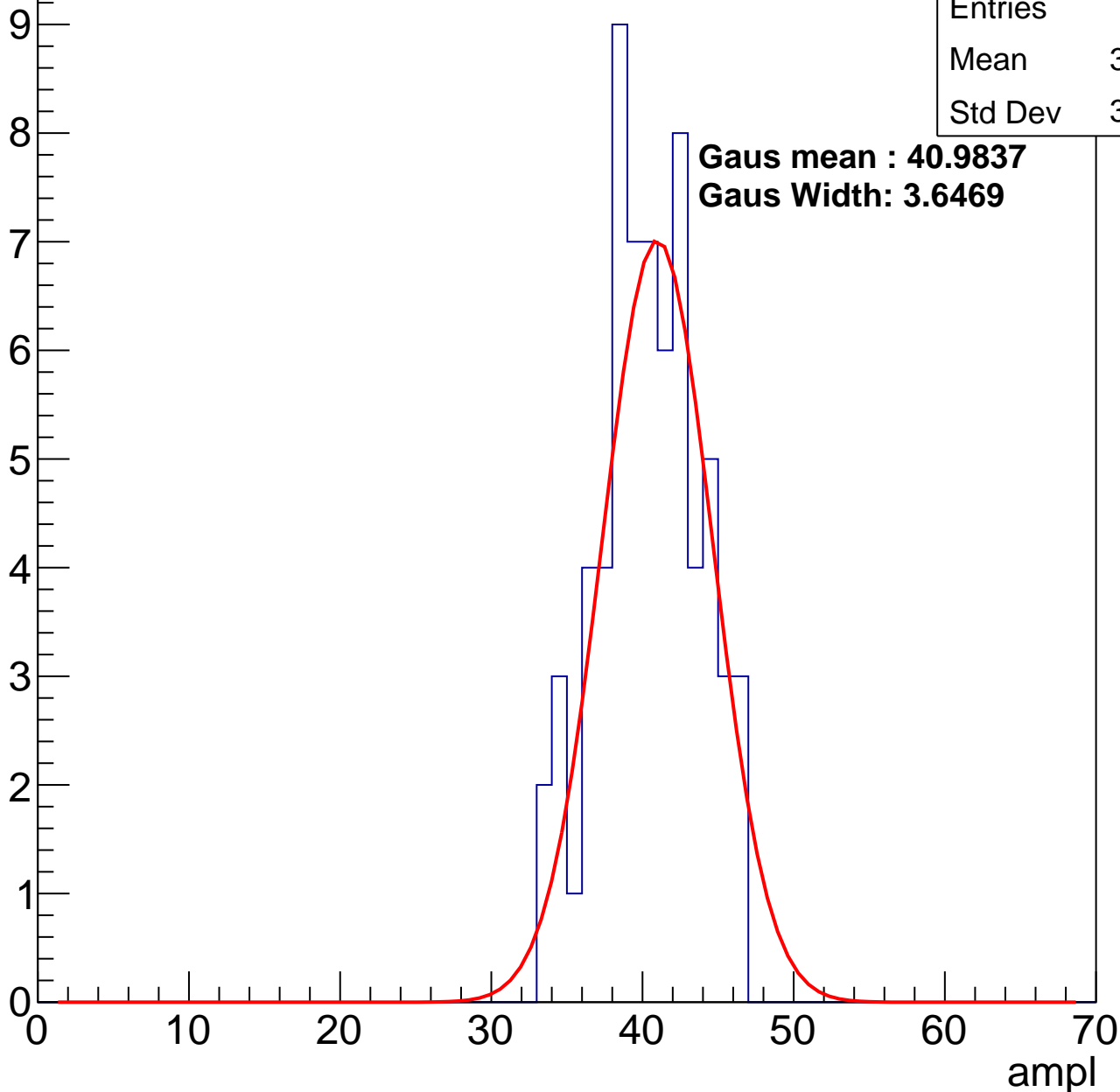
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	39.95
Std Dev	3.282

Gaus mean : 40.9837

Gaus Width: 3.6469

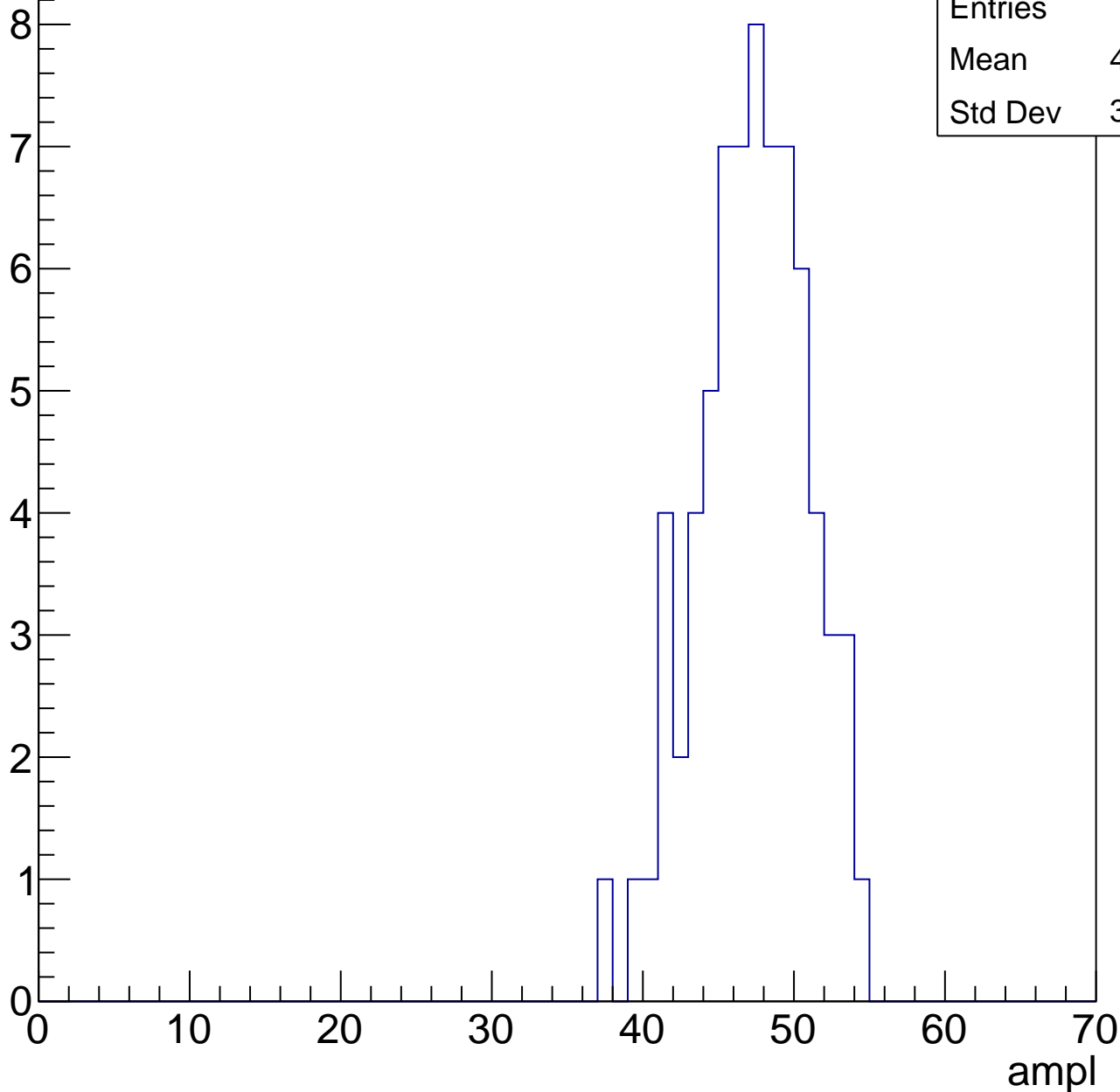


B1L100S, U15-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	46.77
Std Dev	3.624

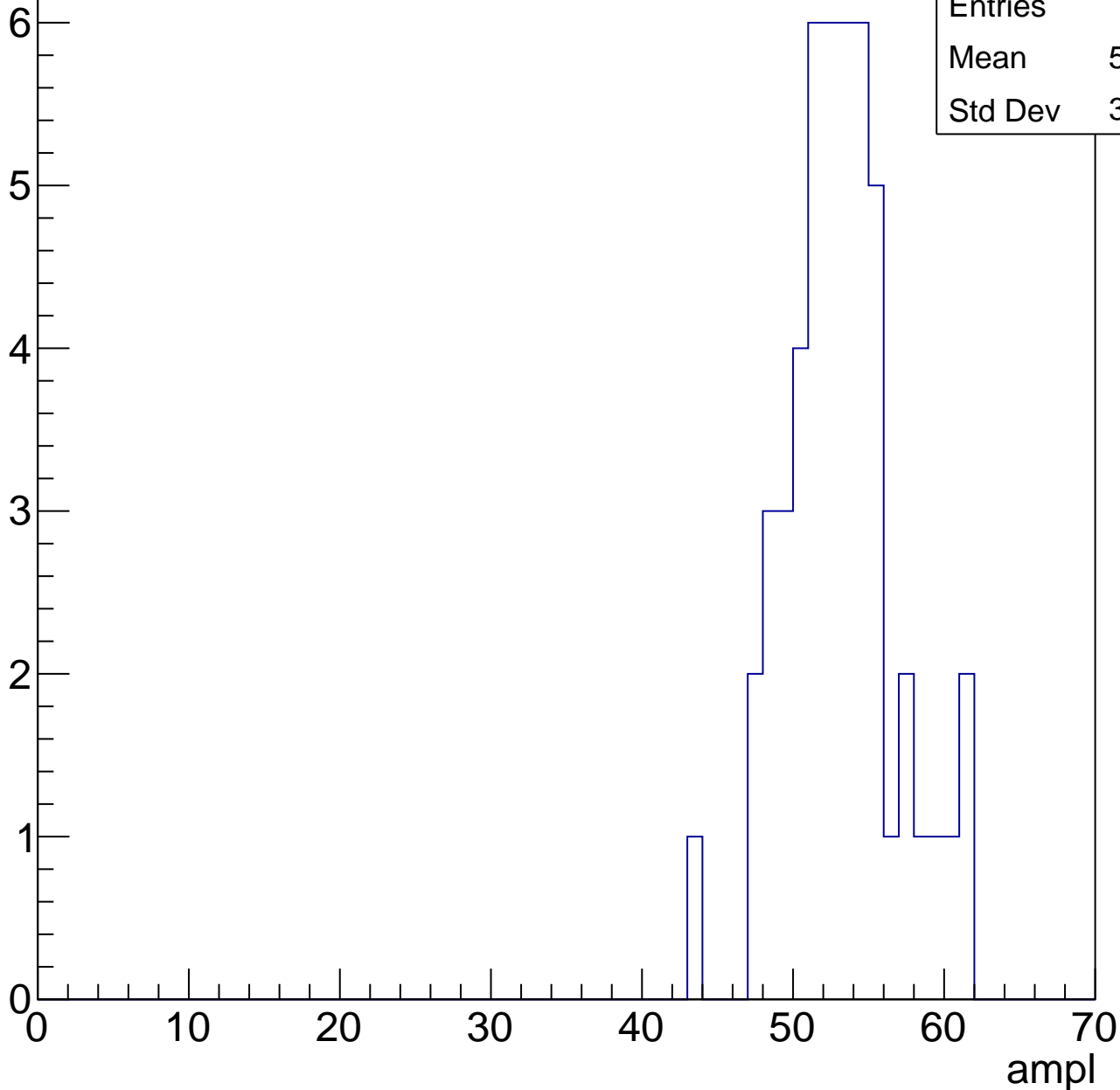


B1L100S, U15-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

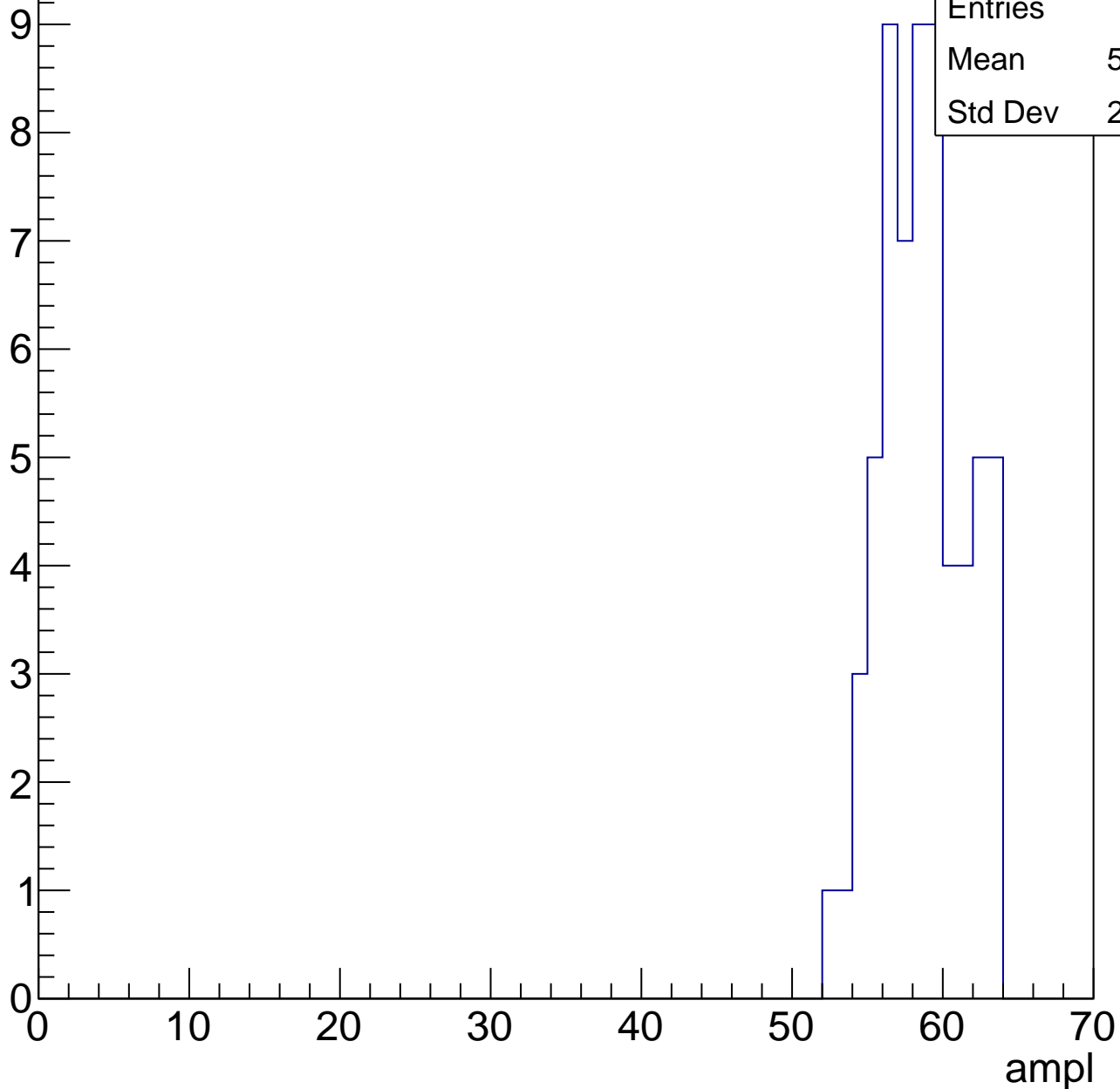
Entries	50
Mean	52.64
Std Dev	3.632



B1L100S, U15-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

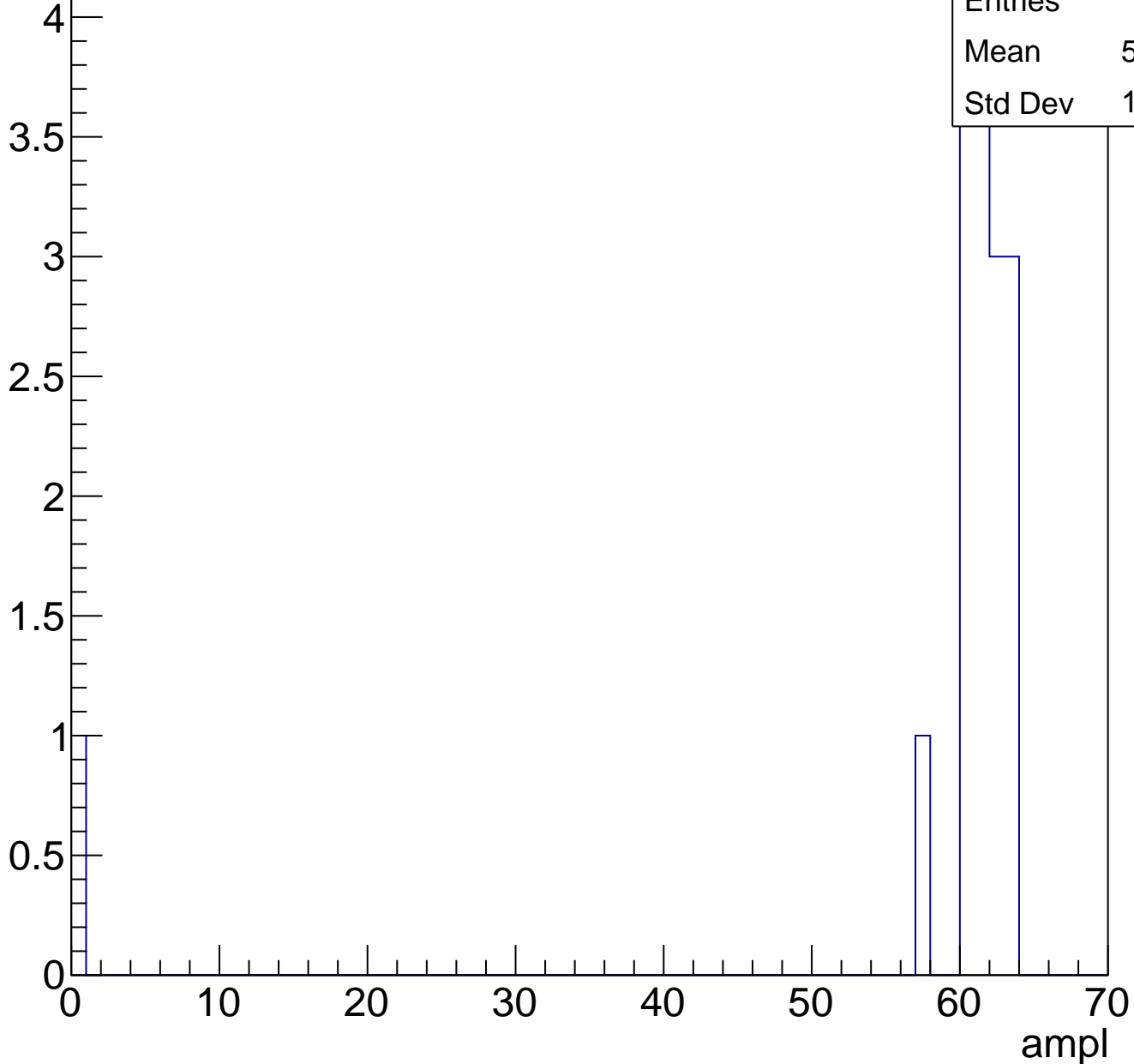


Entries	62
Mean	58.18
Std Dev	2.727

B1L100S, U15-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

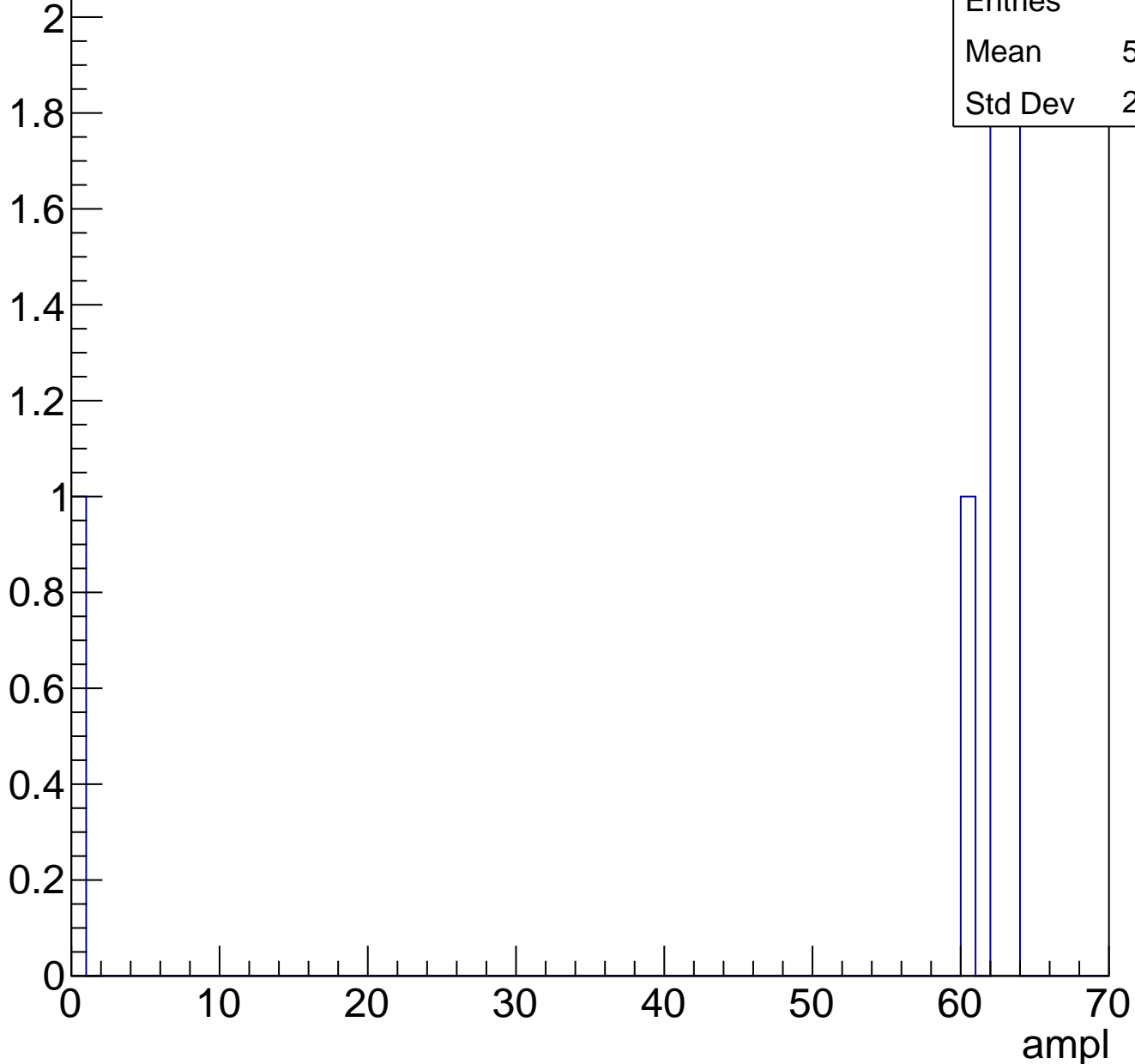
Entry



B1L100S, U15-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch81, adc0

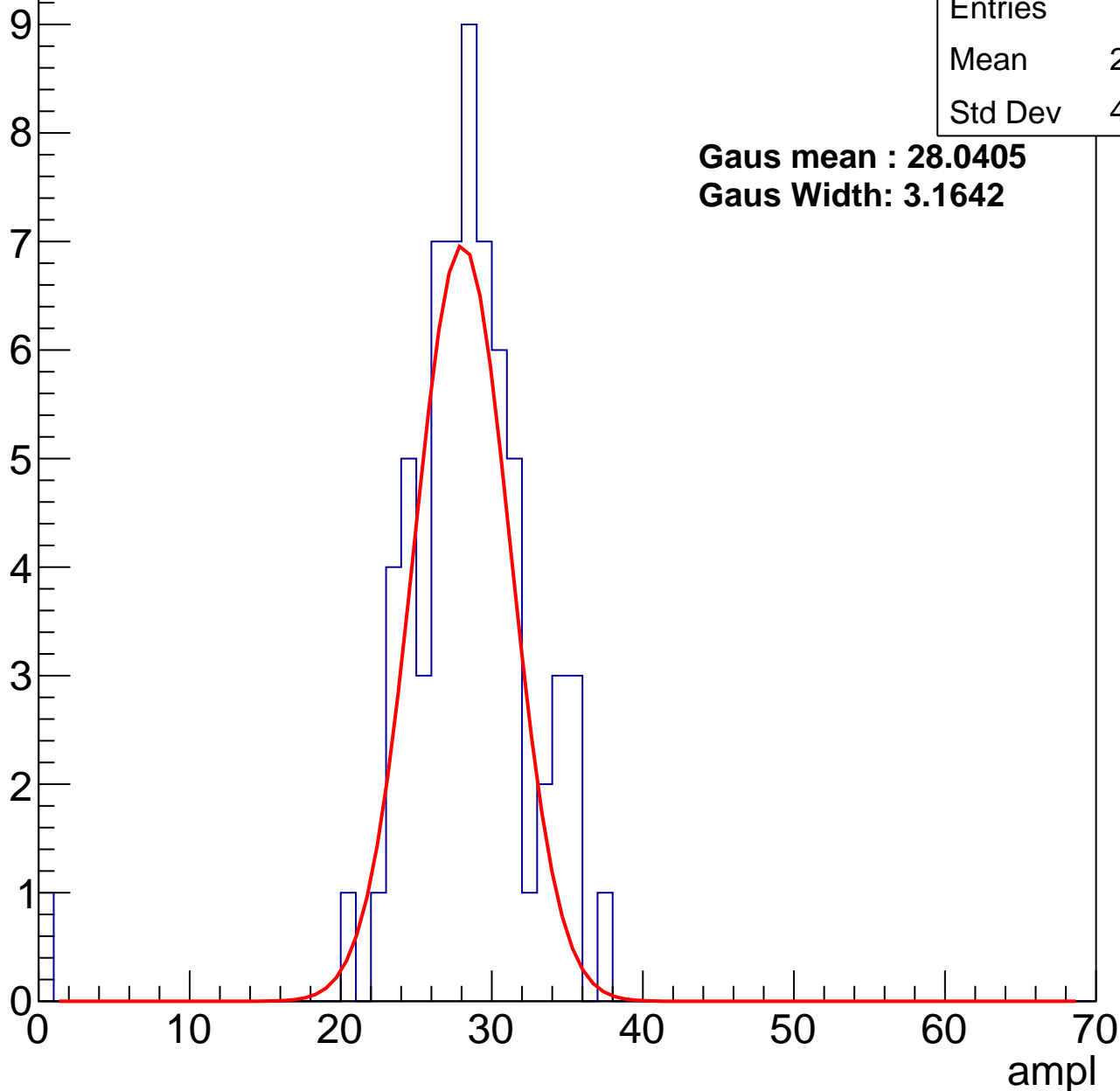
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	27.76
Std Dev	4.924

Gaus mean : 28.0405

Gaus Width: 3.1642



B1L100S, U15-ch81, adc1

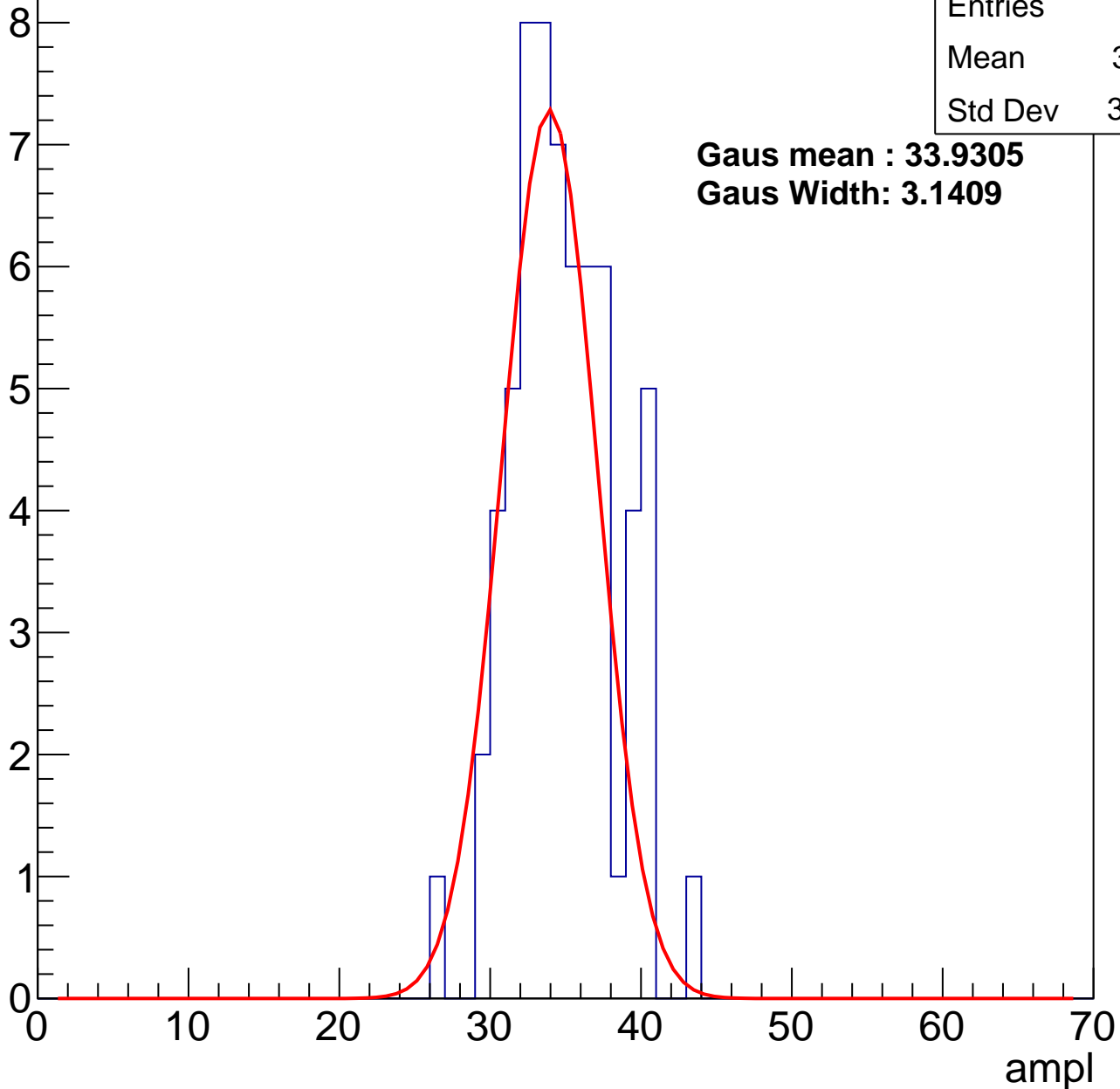
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	34.41
Std Dev	3.339

Gaus mean : 33.9305

Gaus Width: 3.1409



B1L100S, U15-ch81, adc2

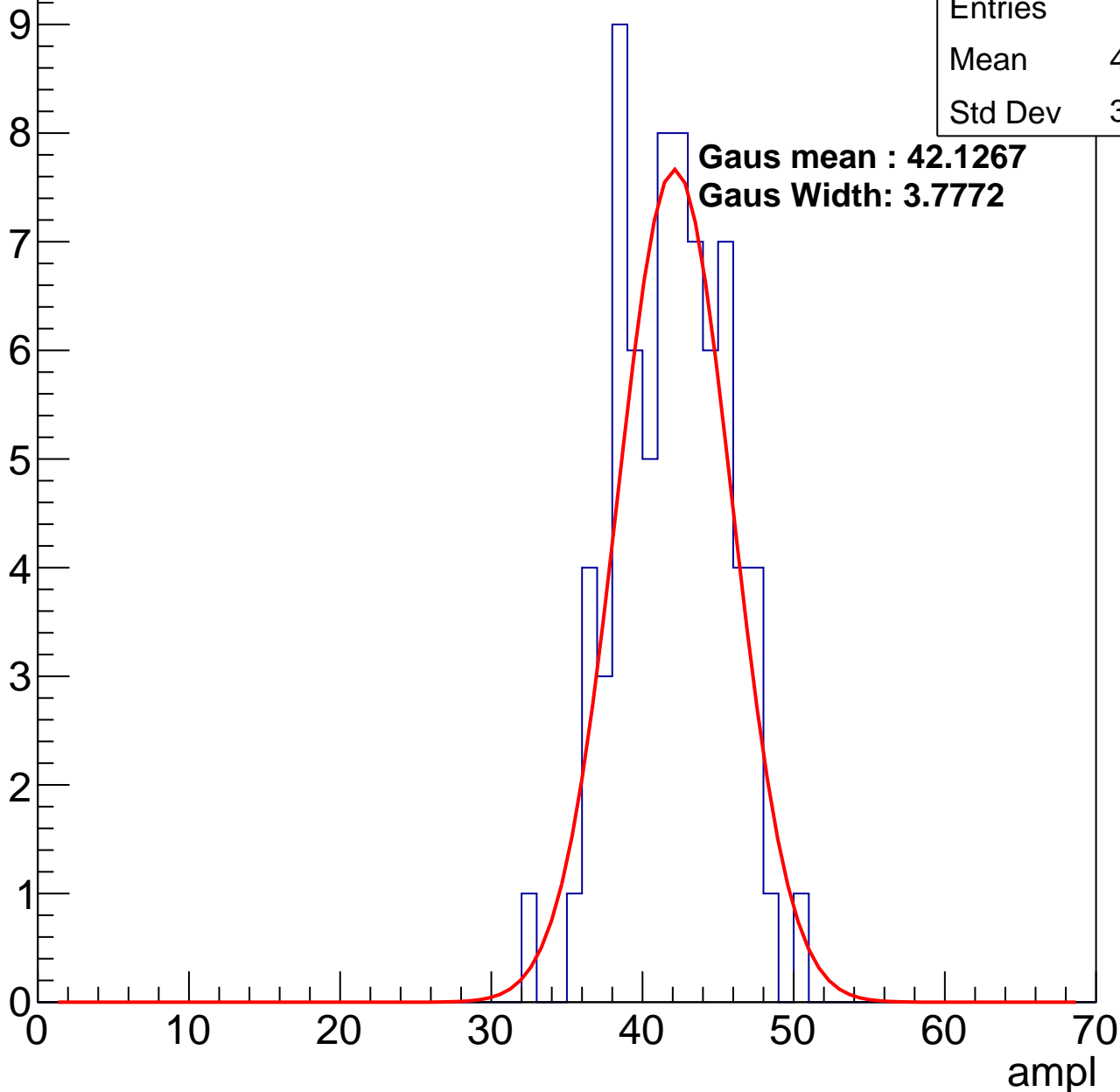
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	41.49
Std Dev	3.527

Gaus mean : 42.1267

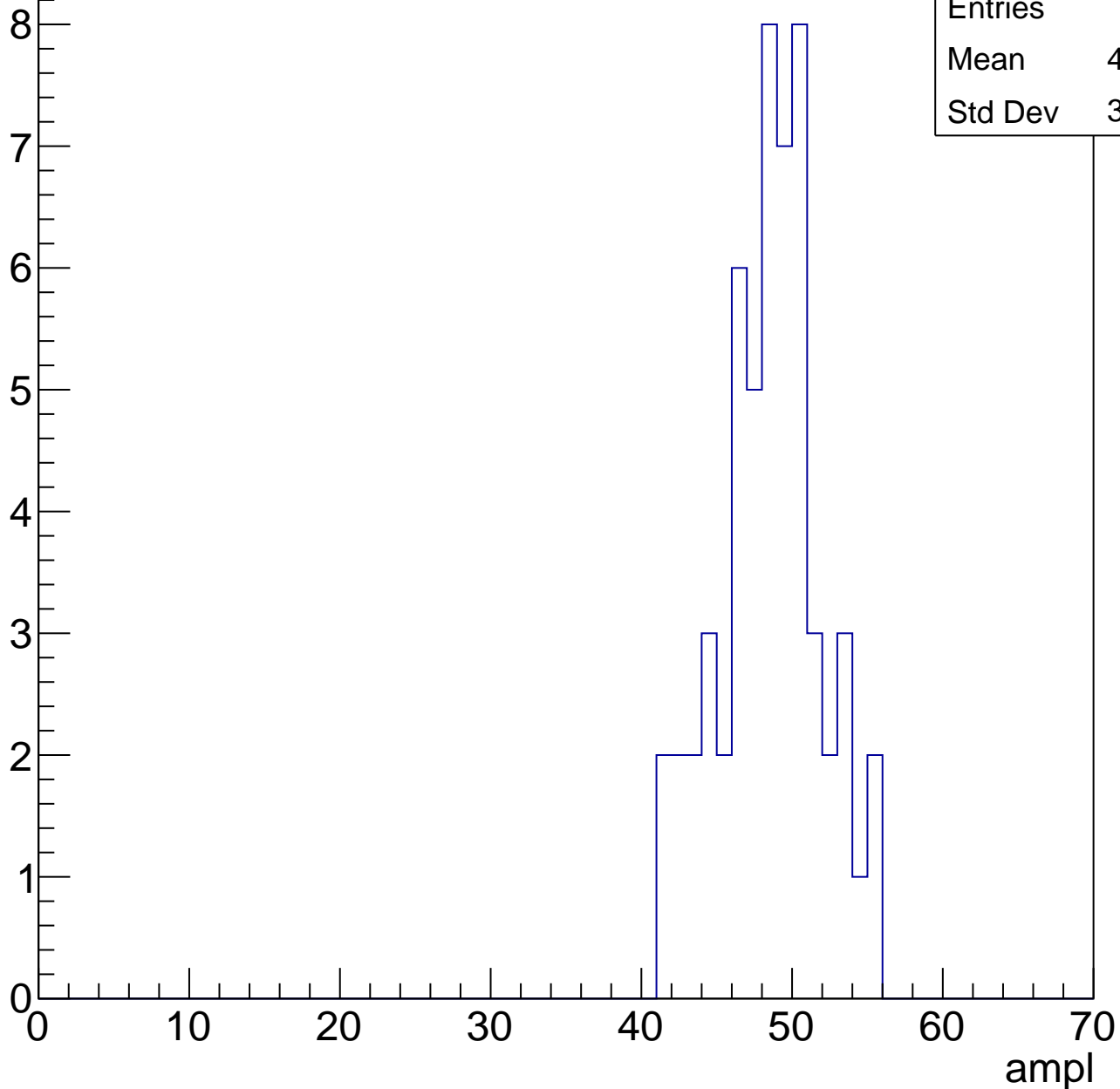
Gaus Width: 3.7772



B1L100S, U15-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

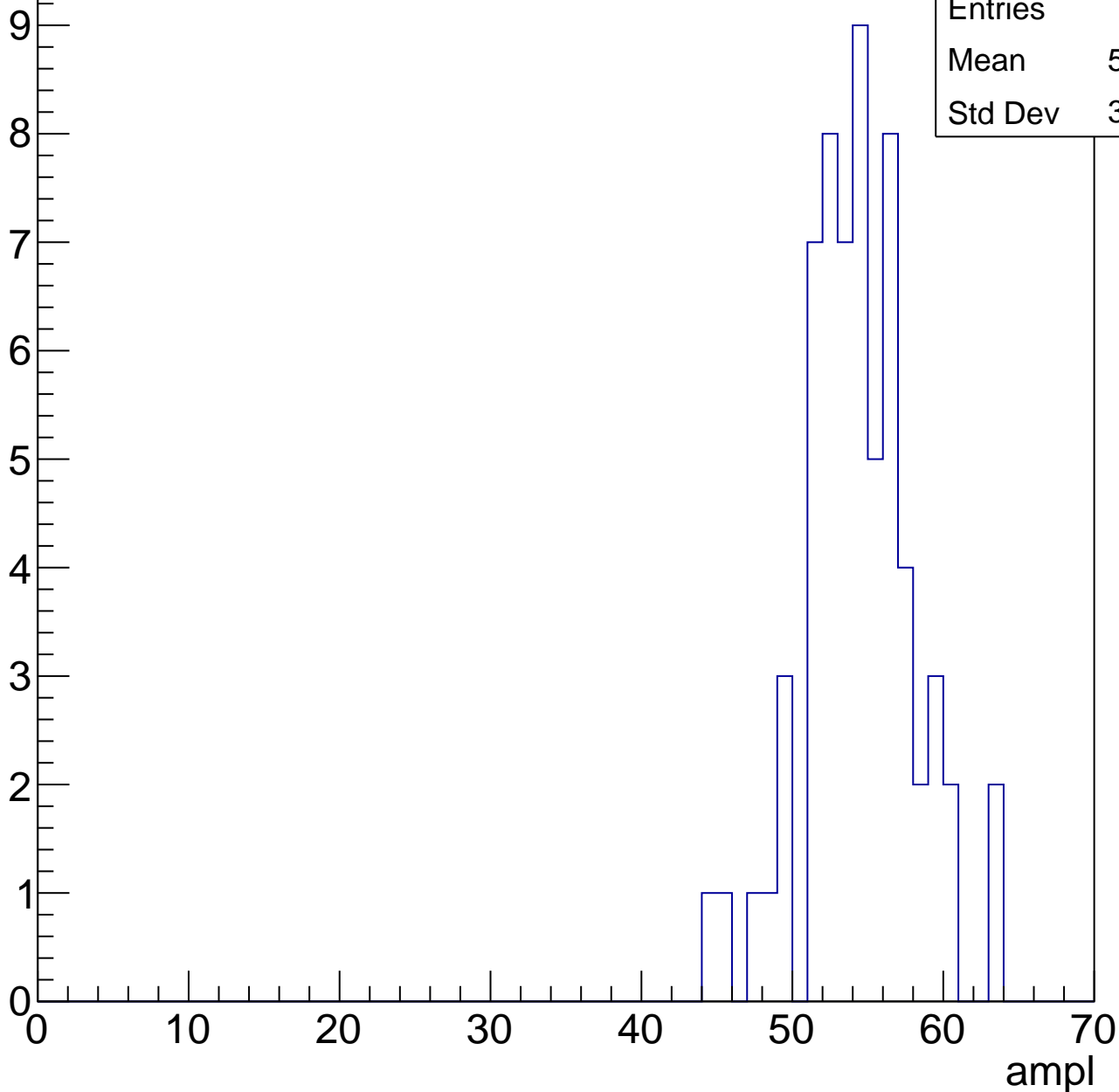


B1L100S, U15-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	53.92
Std Dev	3.658

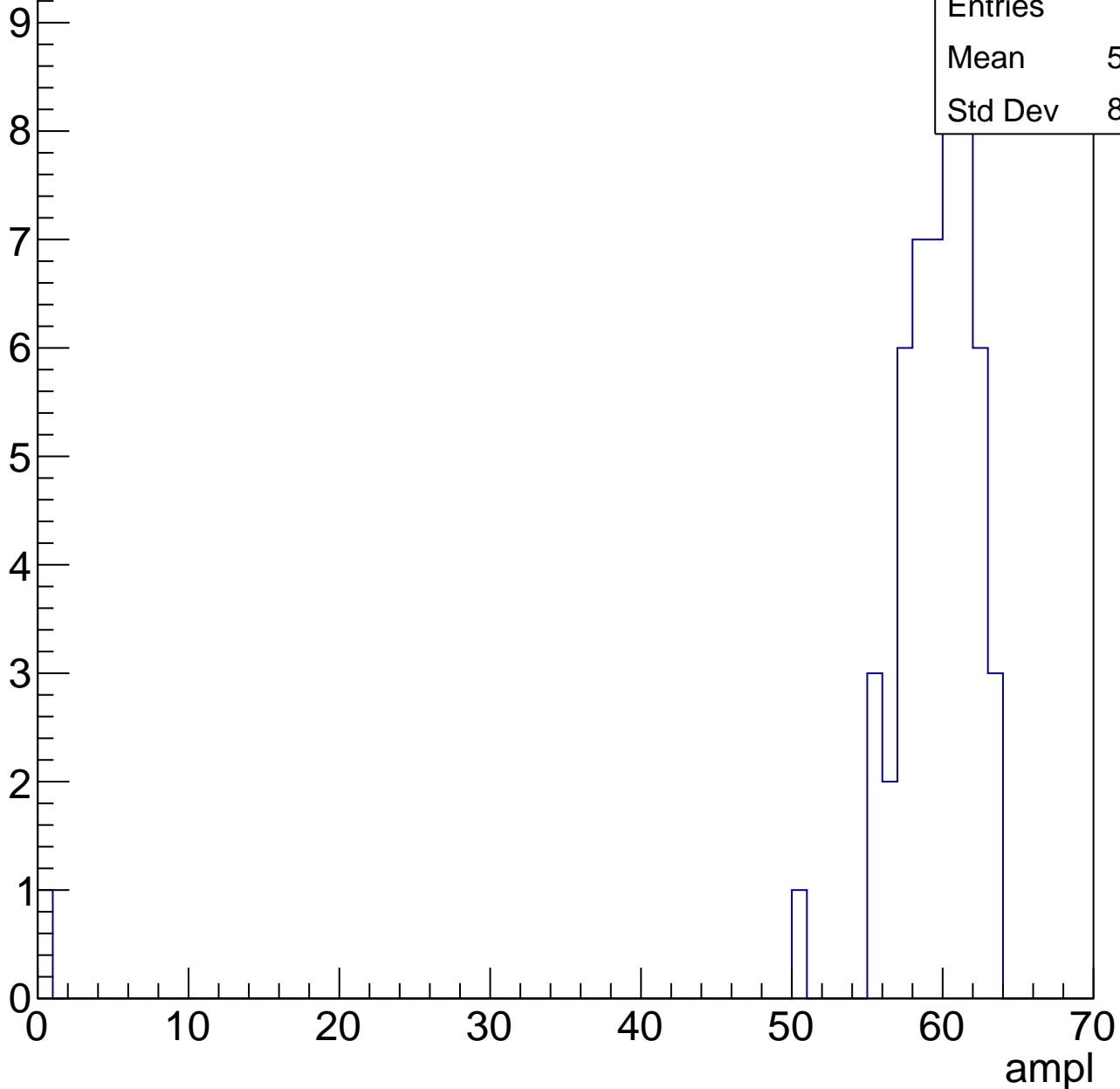


B1L100S, U15-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.08
Std Dev	8.422

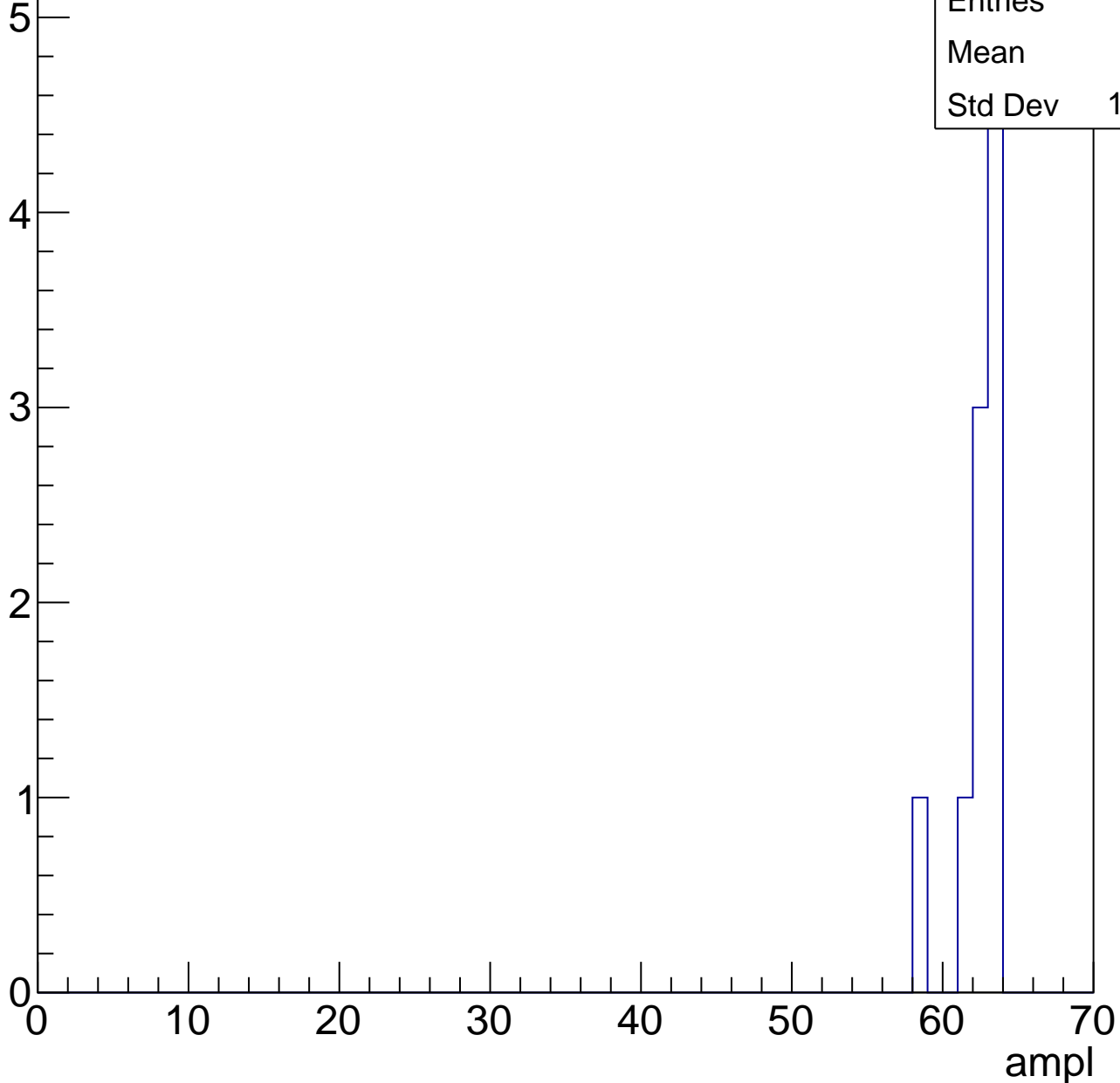


B1L100S, U15-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	62
Std Dev	1.483



B1L100S, U15-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch82, adc0

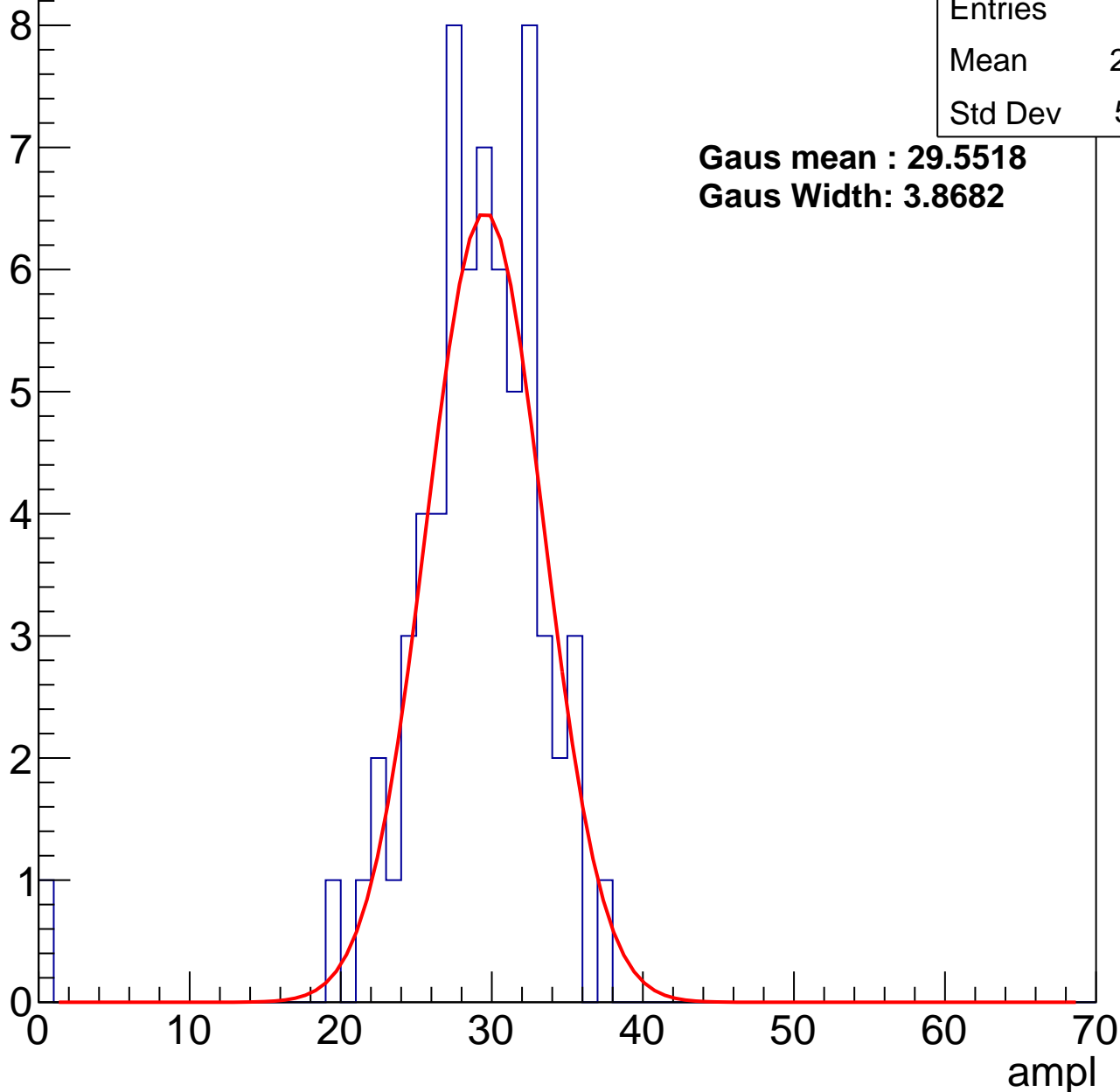
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	28.33
Std Dev	5.061

Gaus mean : 29.5518

Gaus Width: 3.8682



B1L100S, U15-ch82, adc1

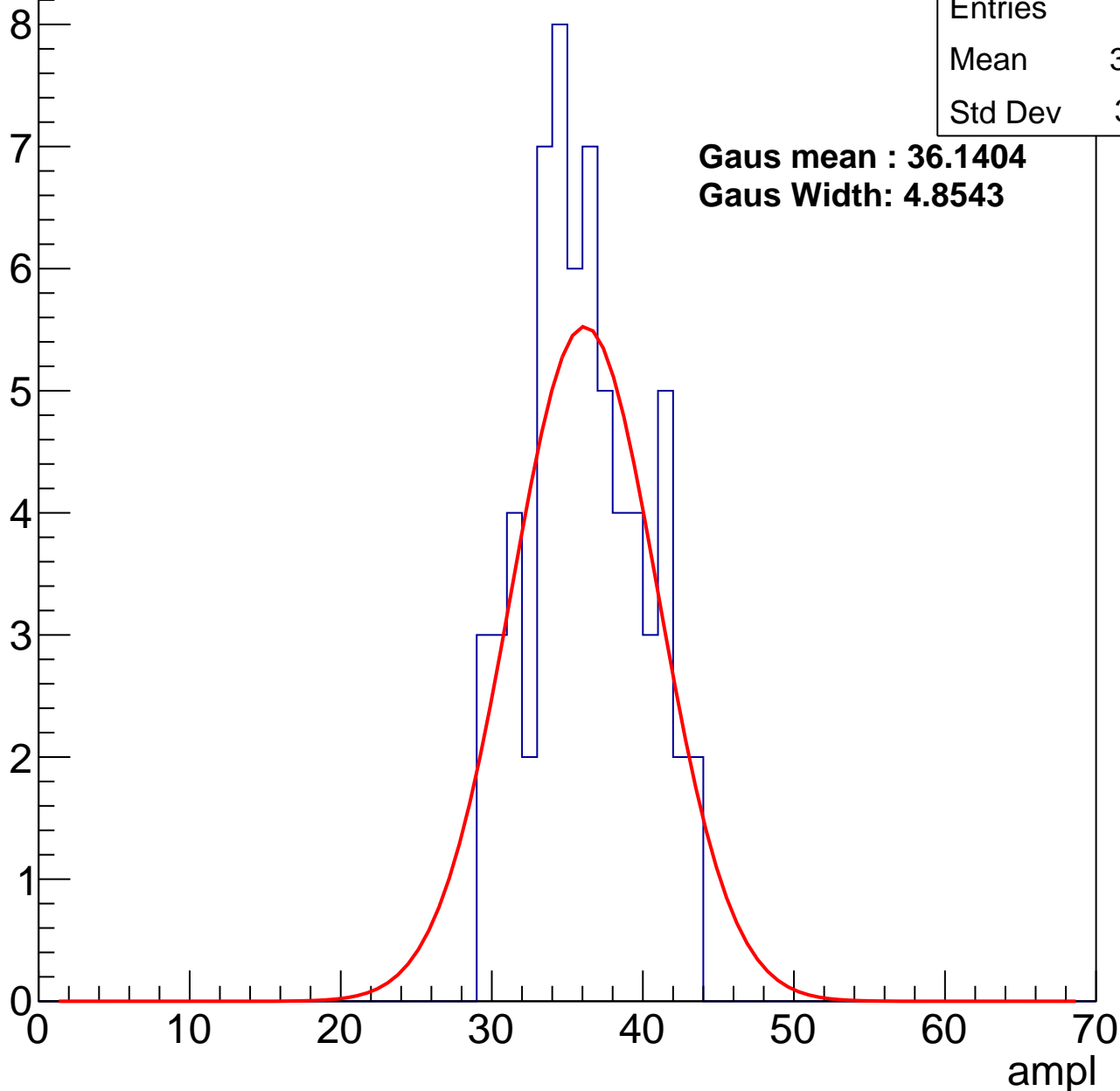
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.66
Std Dev	3.681

Gaus mean : 36.1404

Gaus Width: 4.8543



B1L100S, U15-ch82, adc2

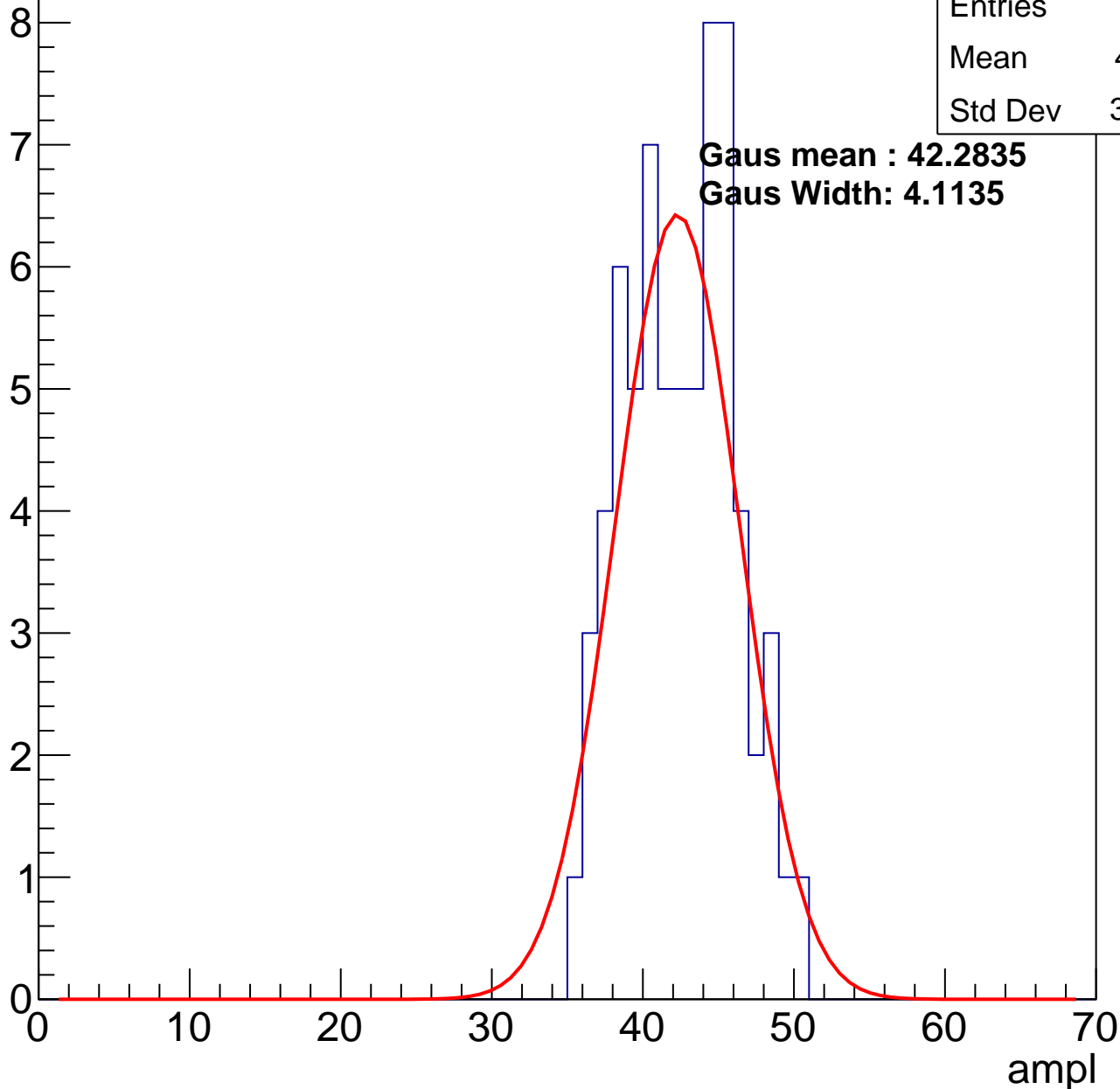
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.01
Std Dev	3.587

Gaus mean : 42.2835

Gaus Width: 4.1135

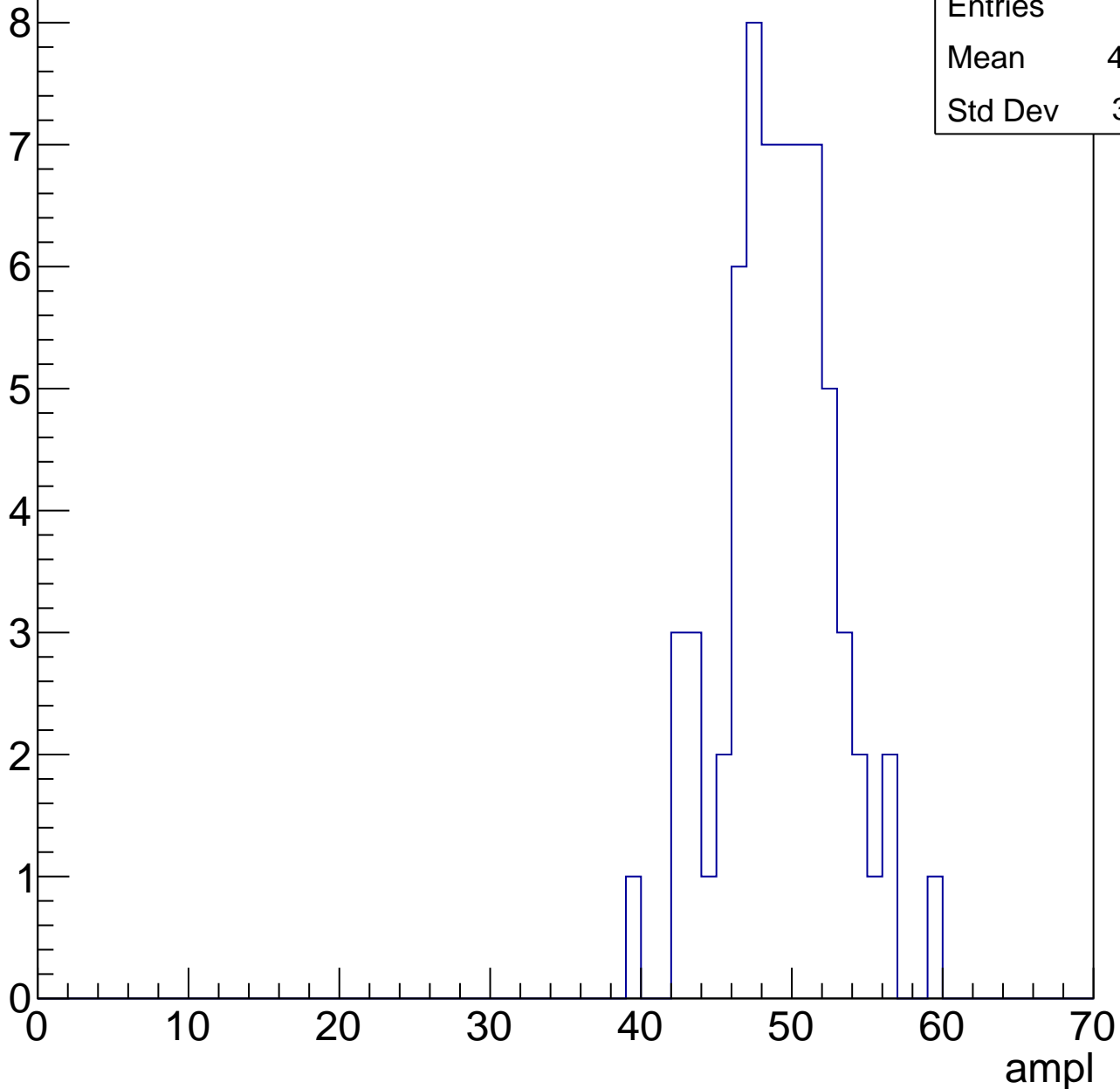


B1L100S, U15-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	48.77
Std Dev	3.721

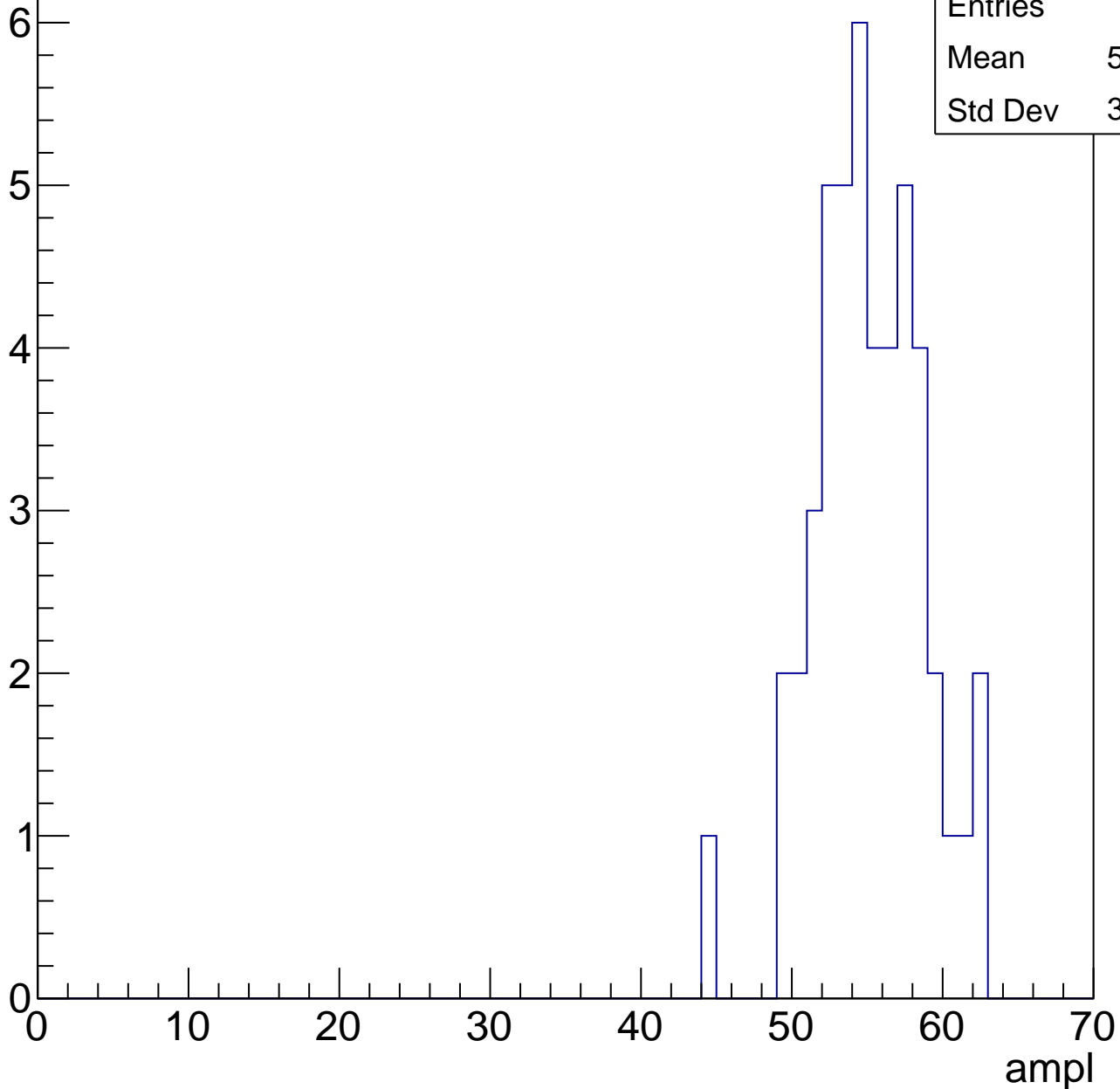


B1L100S, U15-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	54.64
Std Dev	3.593



B1L100S, U15-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

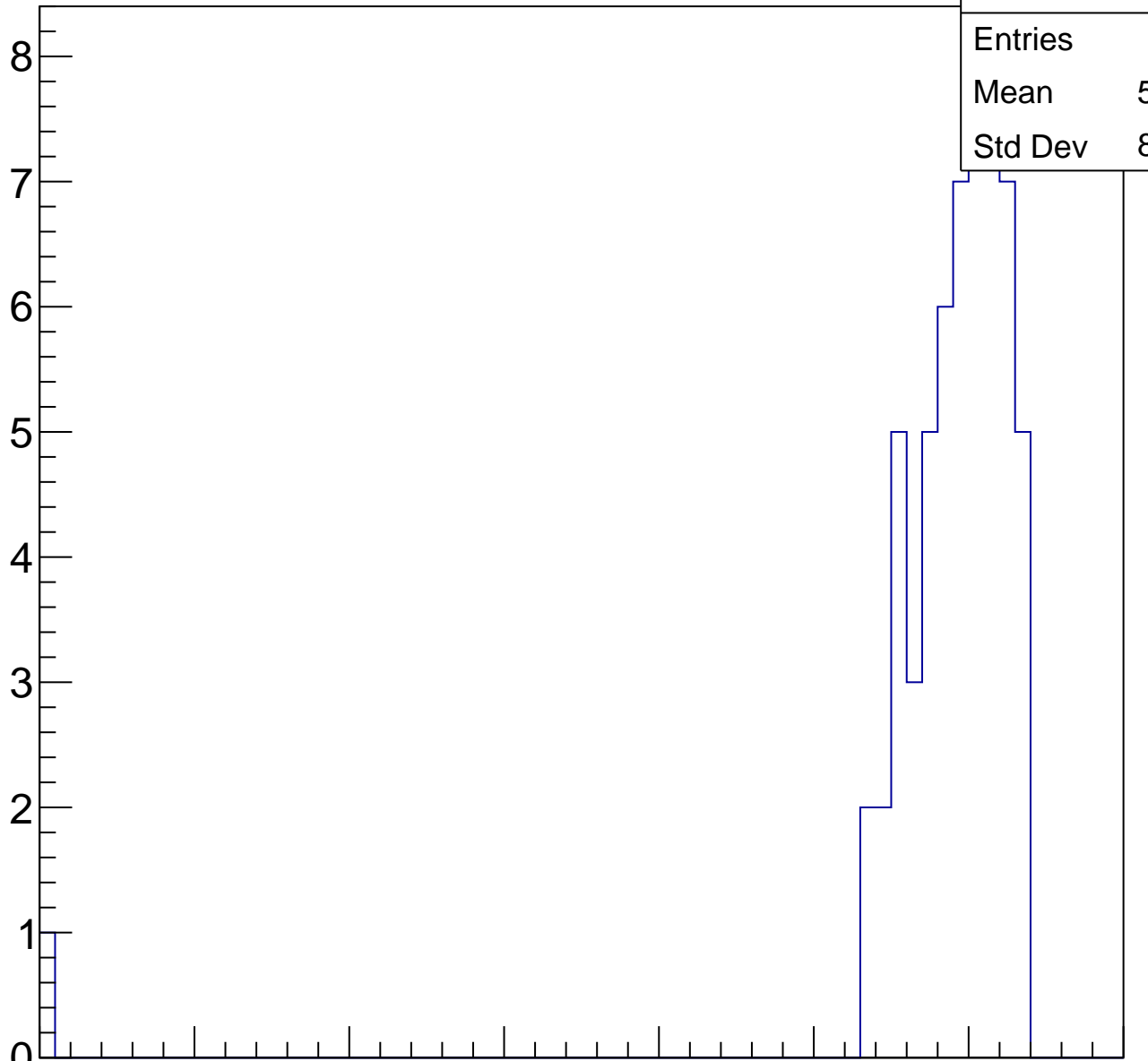
Entry

8
7
6
5
4
3
2
1
0

Entries	59
Mean	57.97
Std Dev	8.084

ampl

0 10 20 30 40 50 60 70

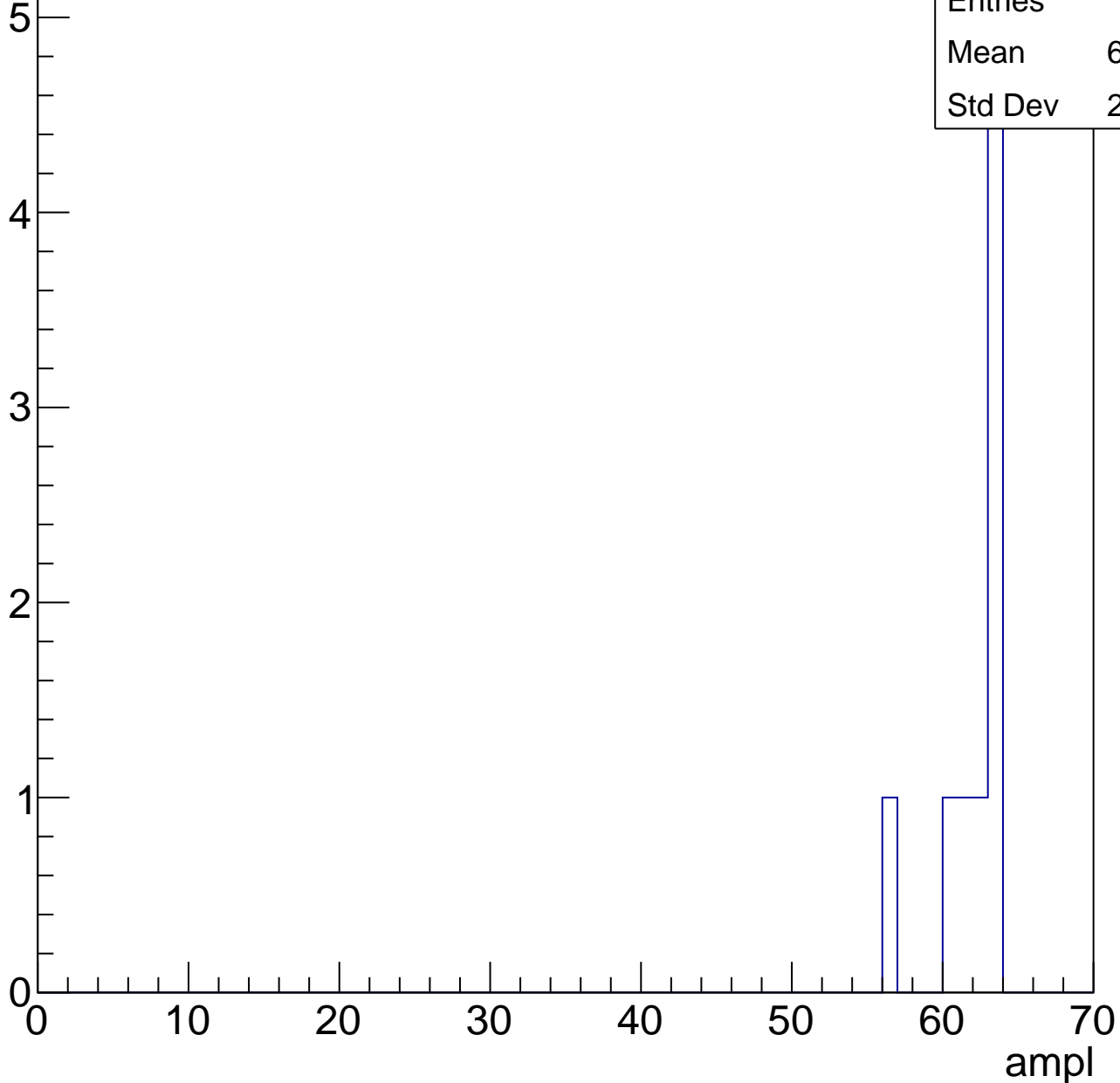


B1L100S, U15-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	9
Mean	61.56
Std Dev	2.217



B1L100S, U15-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch83, adc0

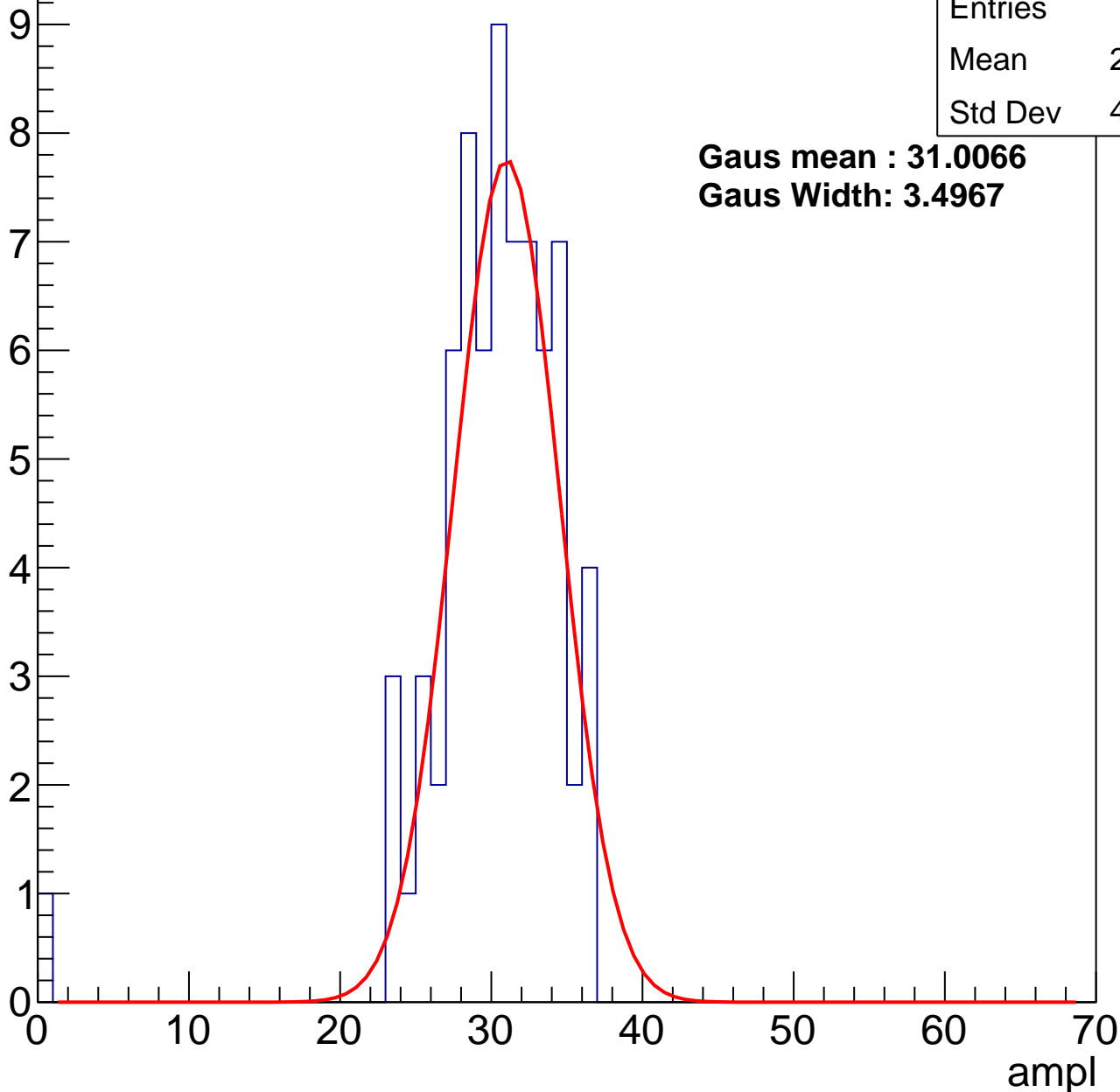
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	29.74
Std Dev	4.819

Gaus mean : 31.0066

Gaus Width: 3.4967



B1L100S, U15-ch83, adc1

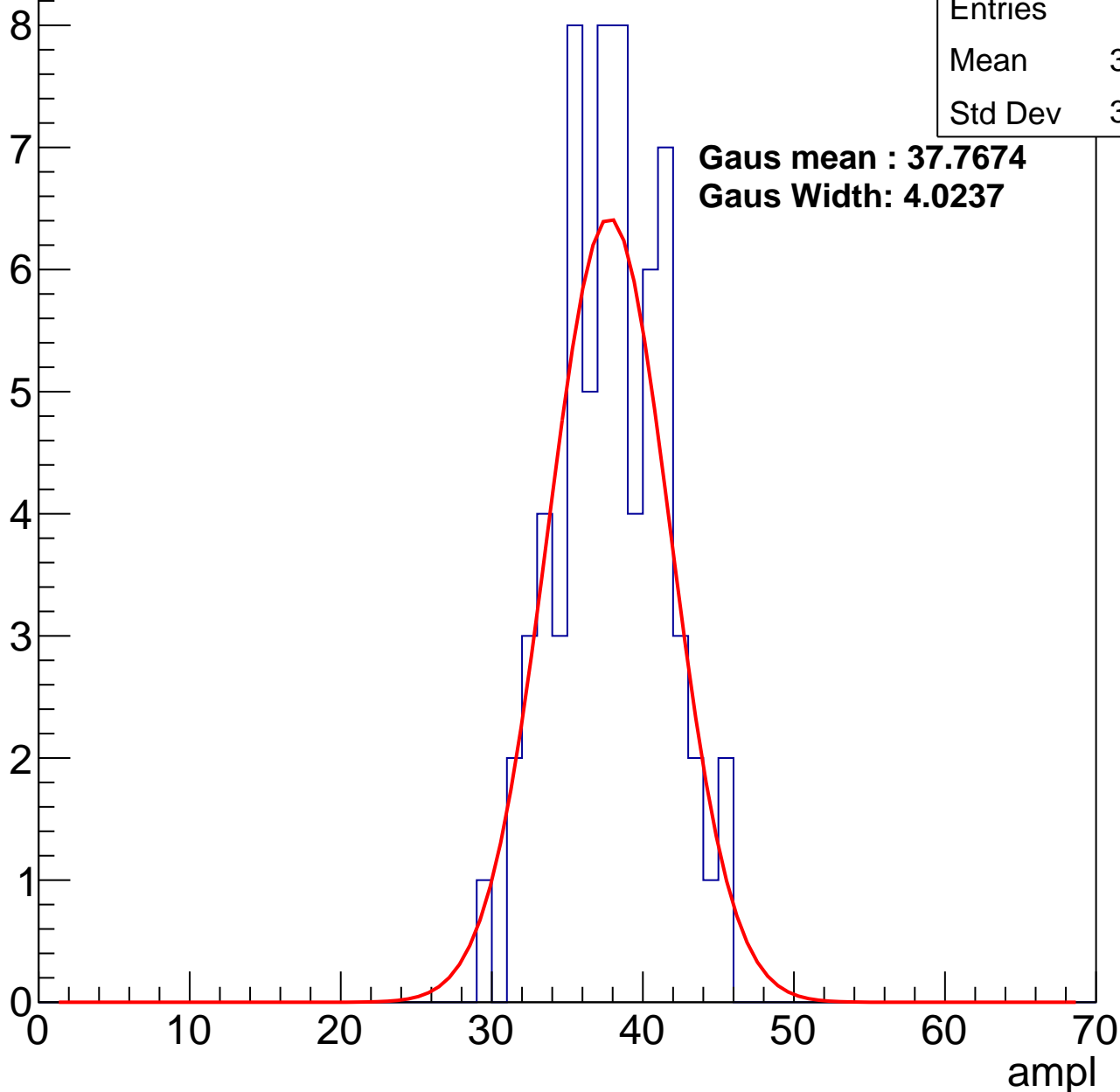
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.46
Std Dev	3.538

Gaus mean : 37.7674

Gaus Width: 4.0237



B1L100S, U15-ch83, adc2

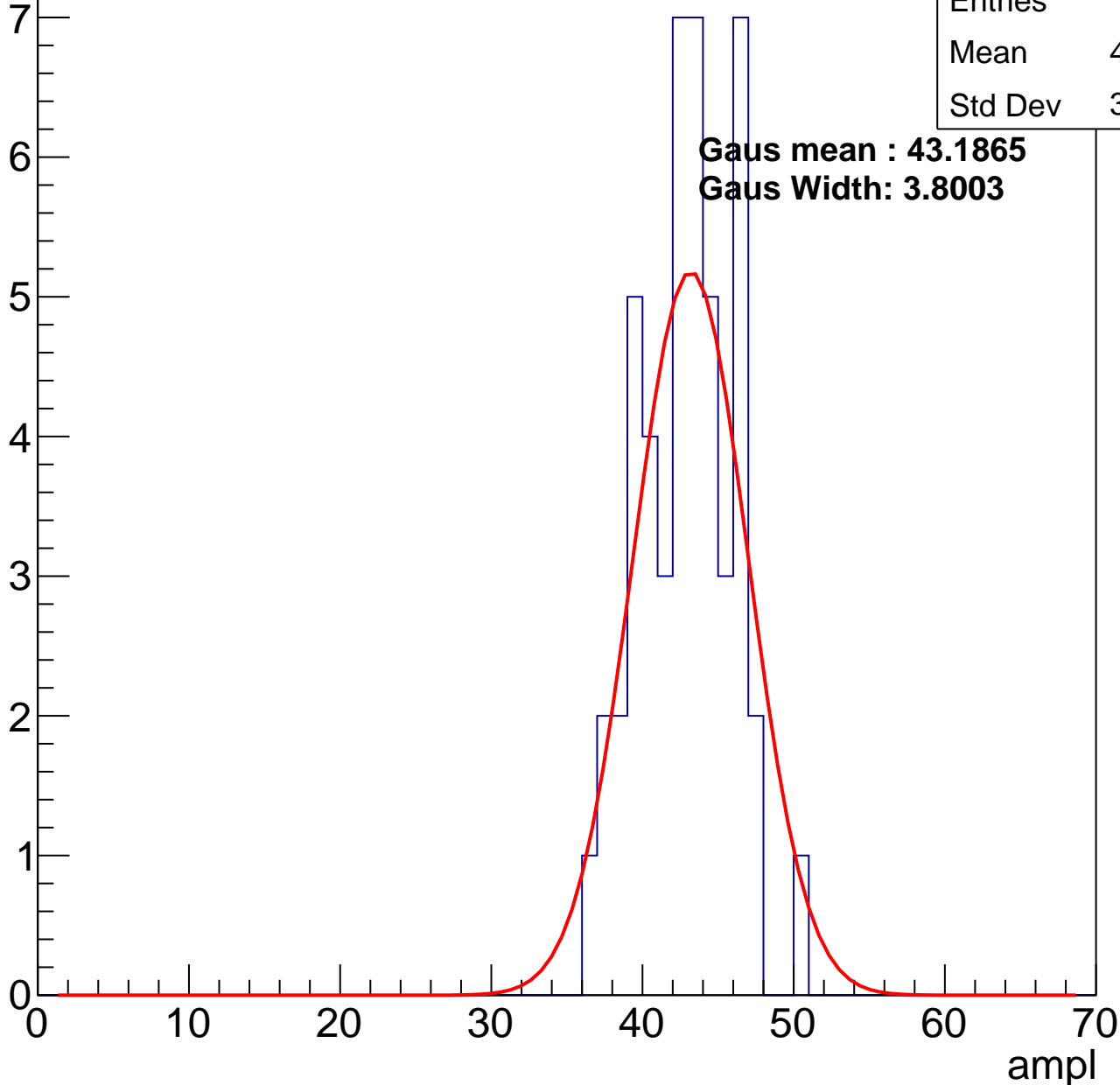
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	42.45
Std Dev	3.038

Gaus mean : 43.1865

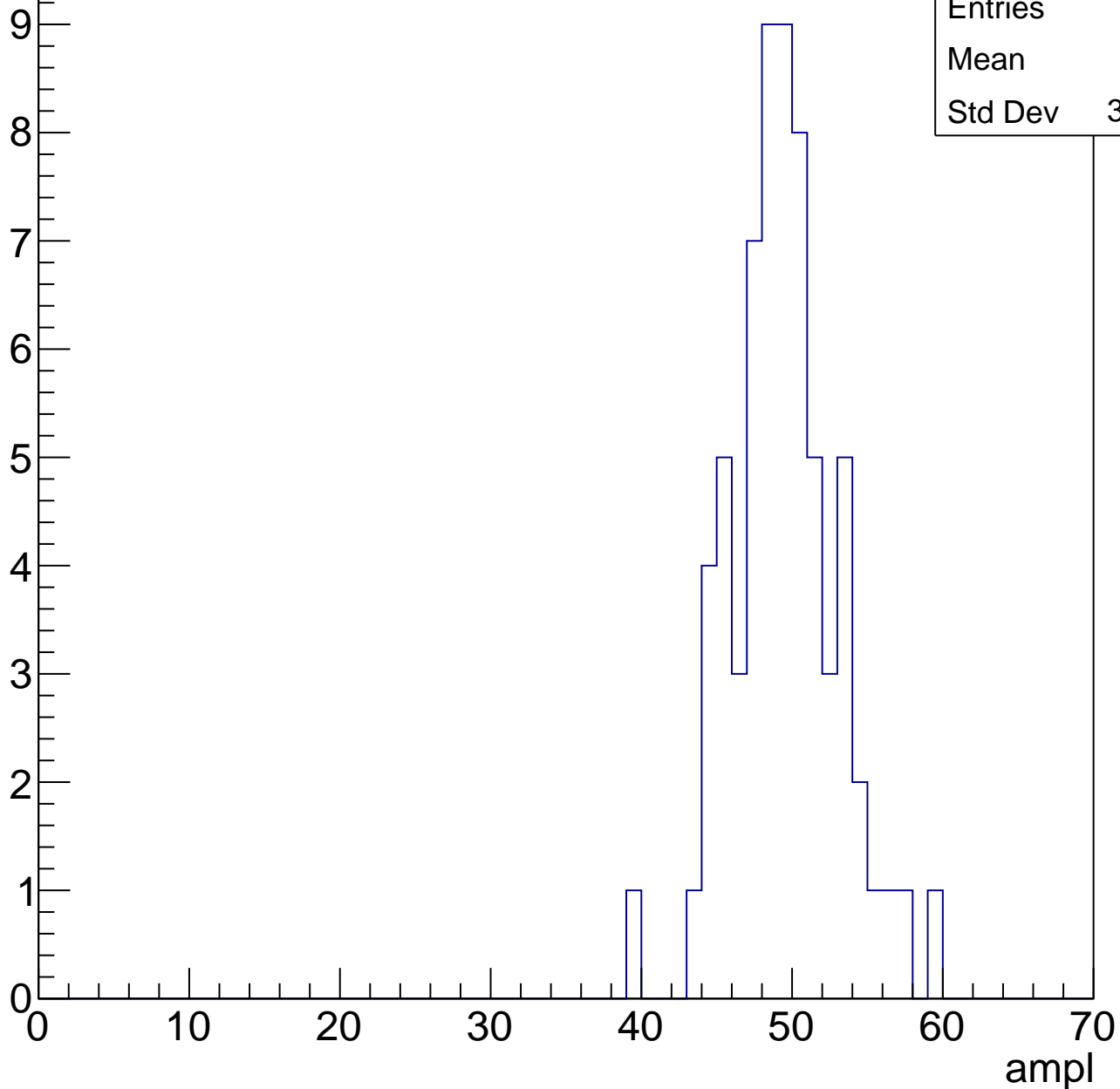
Gaus Width: 3.8003



B1L100S, U15-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

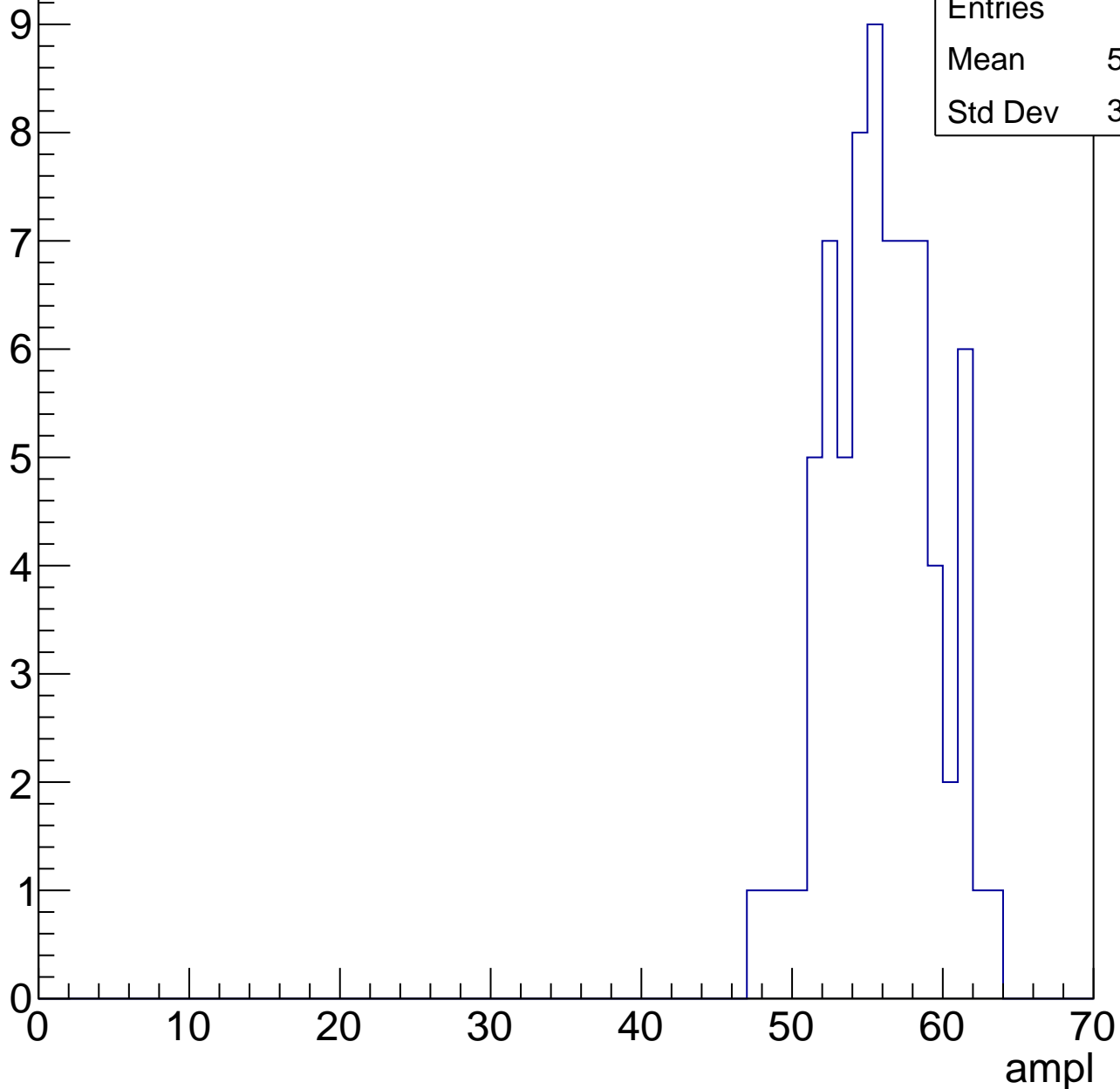
Entry



B1L100S, U15-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

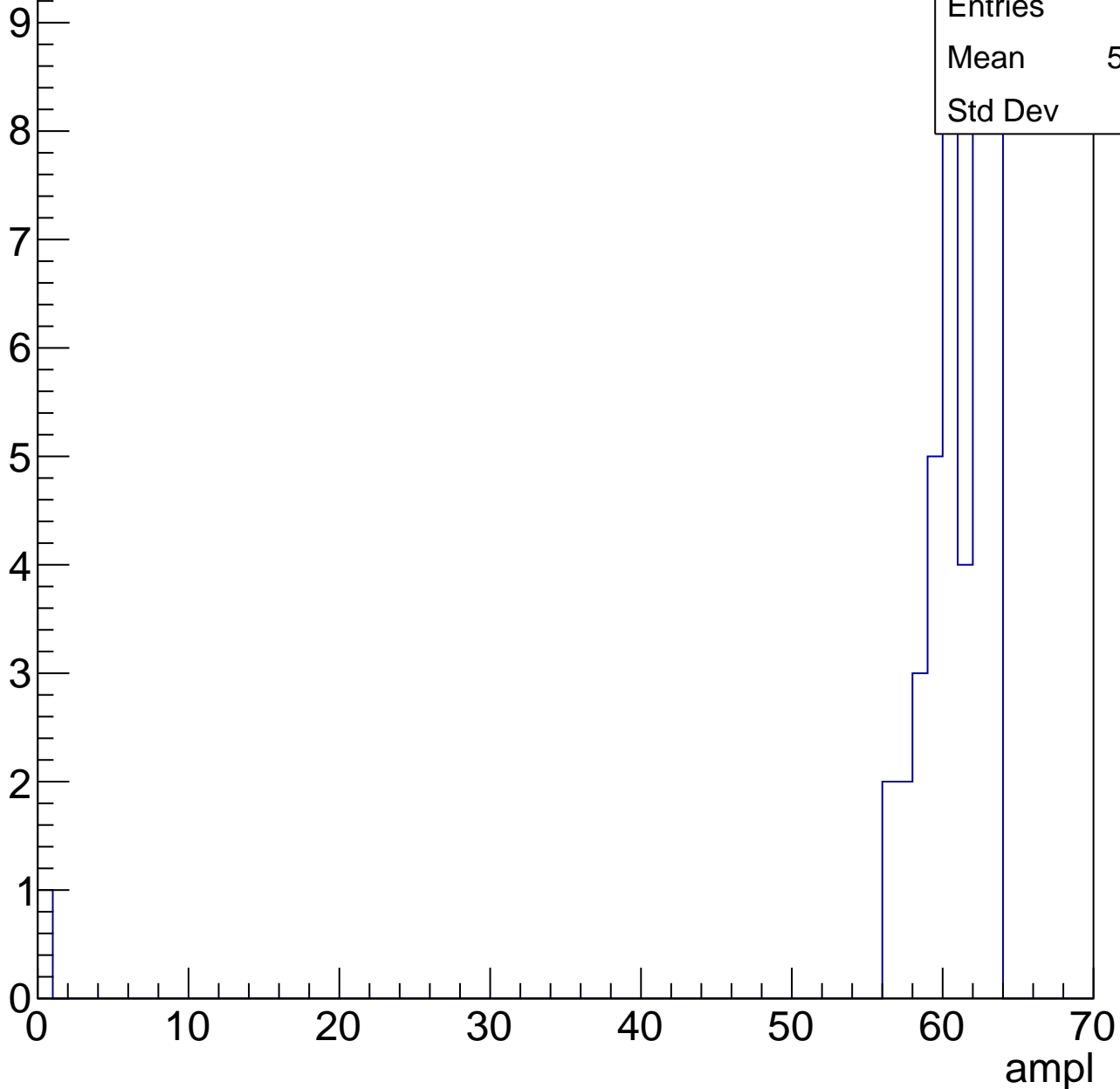


Entries	73
Mean	55.47
Std Dev	3.453

B1L100S, U15-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch84, adc0

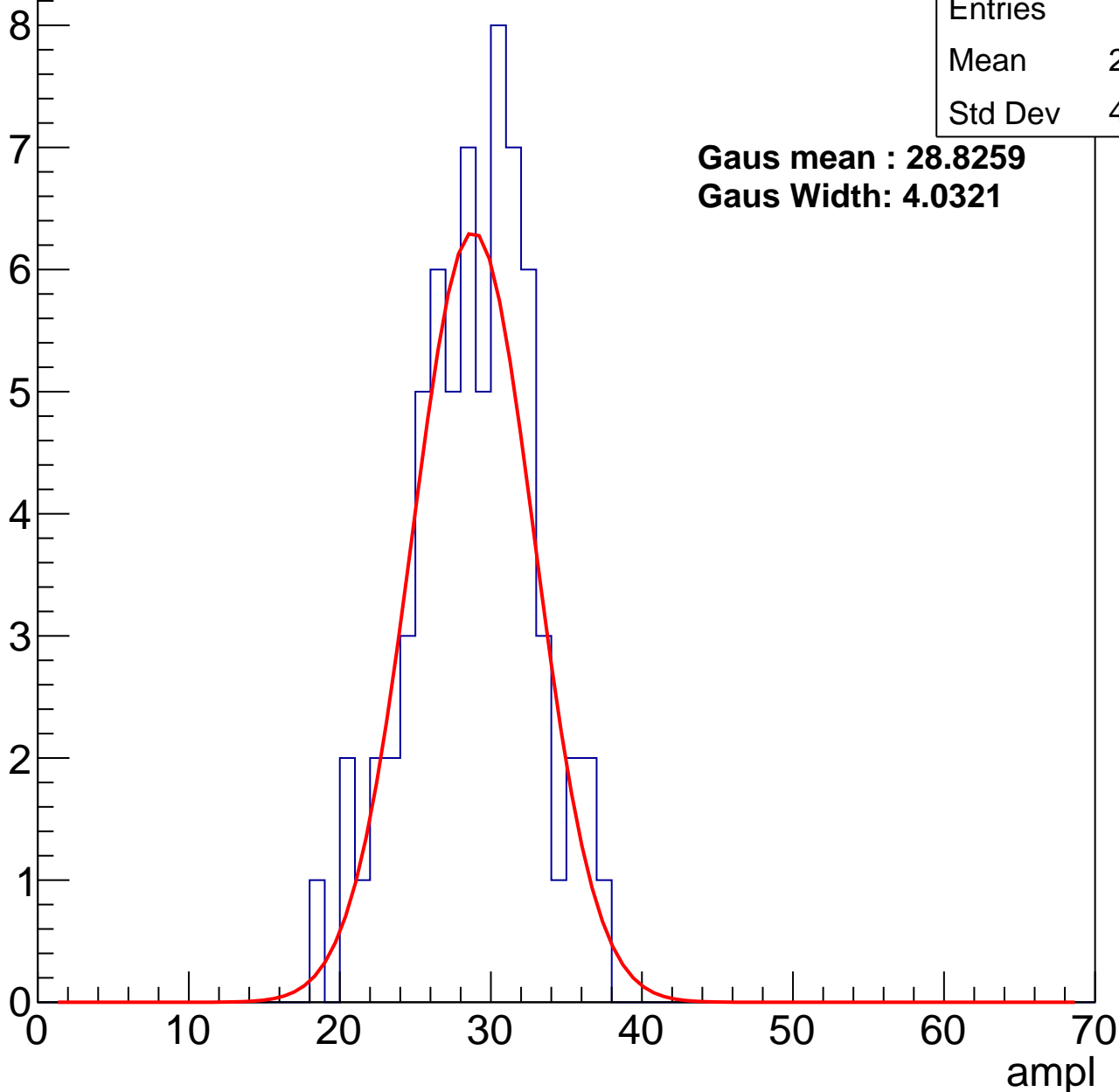
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	28.39
Std Dev	4.037

Gaus mean : 28.8259

Gaus Width: 4.0321



B1L100S, U15-ch84, adc1

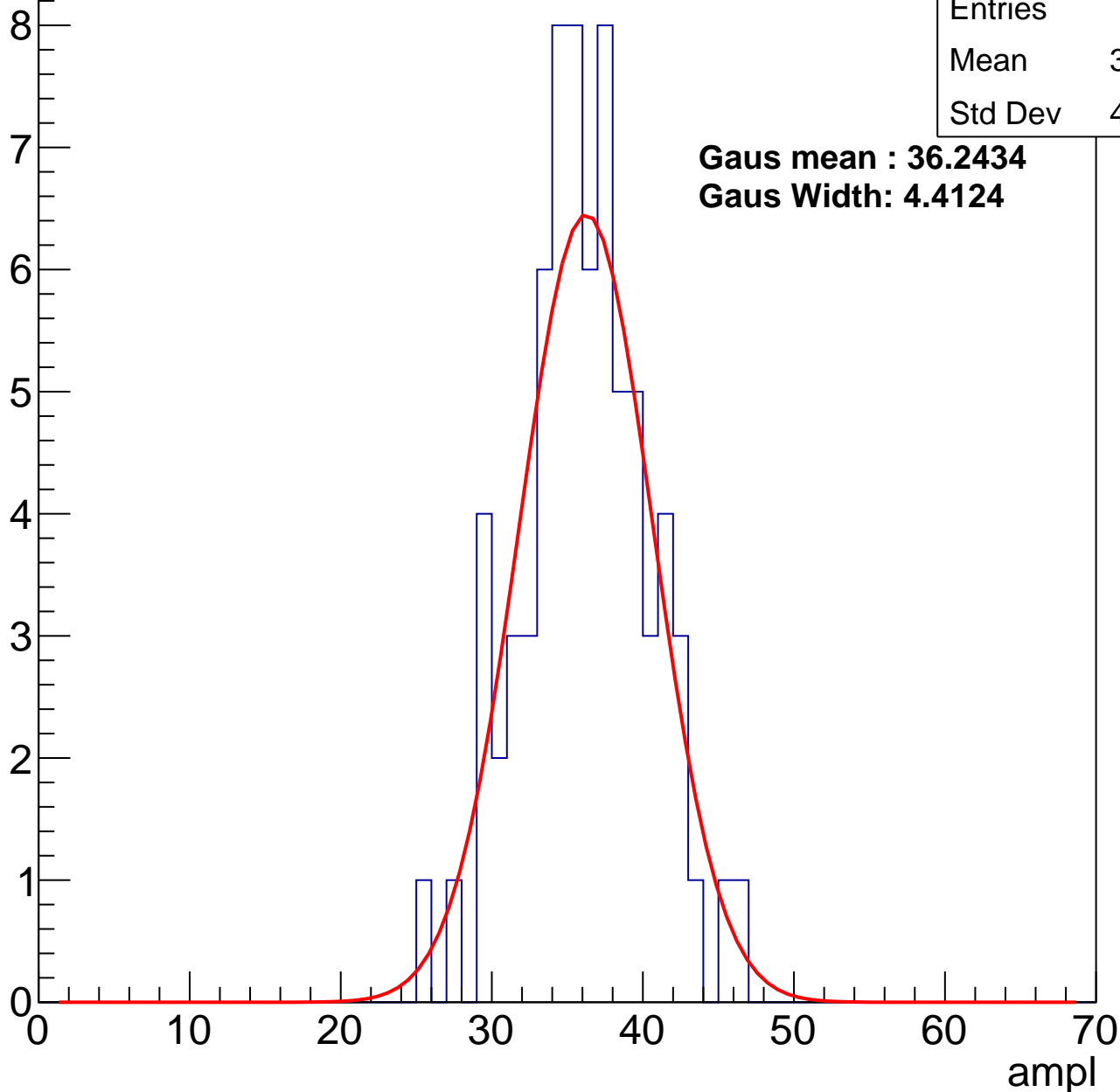
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	35.73
Std Dev	4.122

Gaus mean : 36.2434

Gaus Width: 4.4124



B1L100S, U15-ch84, adc2

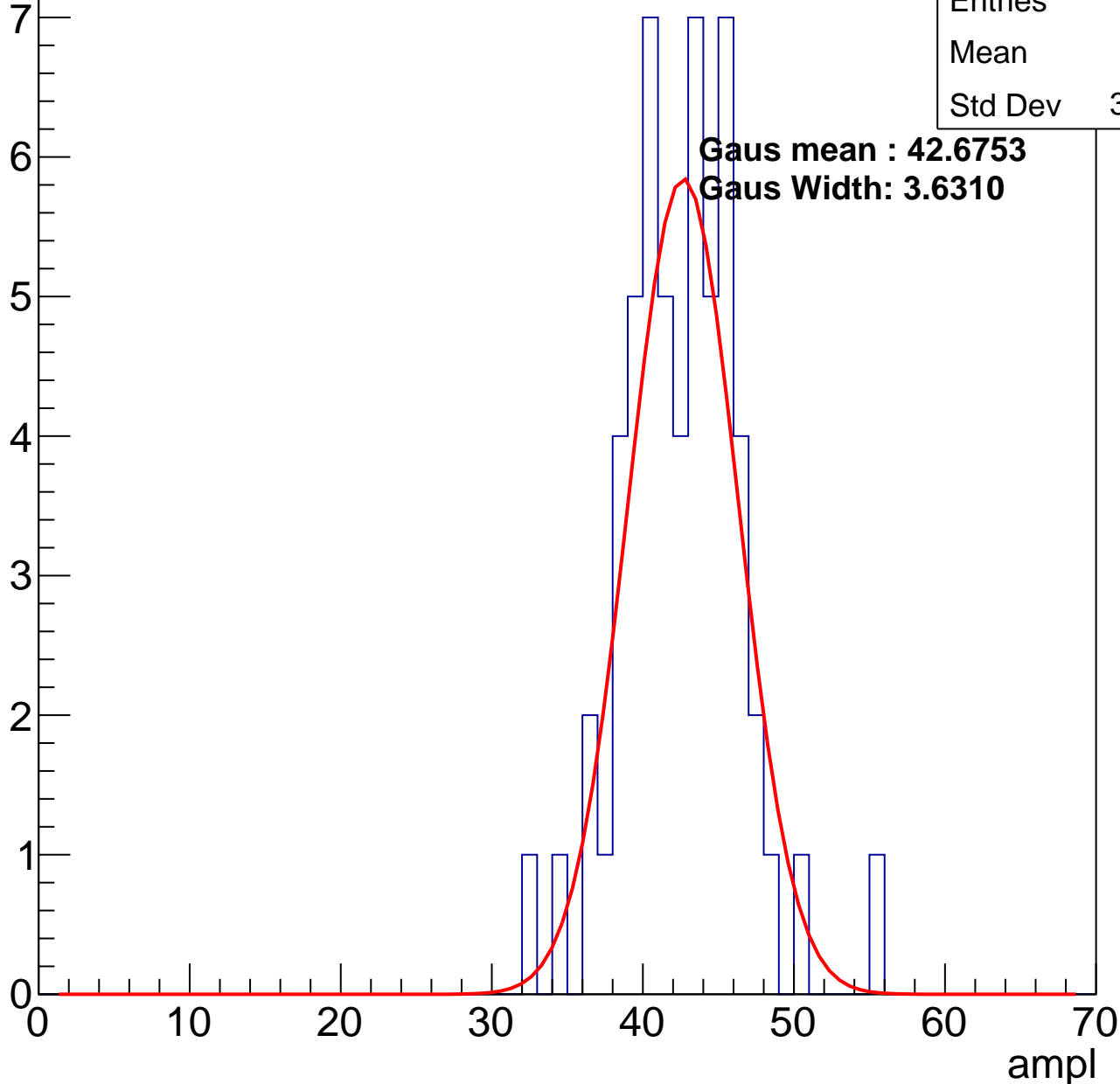
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	42.1
Std Dev	3.885

Gaus mean : 42.6753

Gaus Width: 3.6310

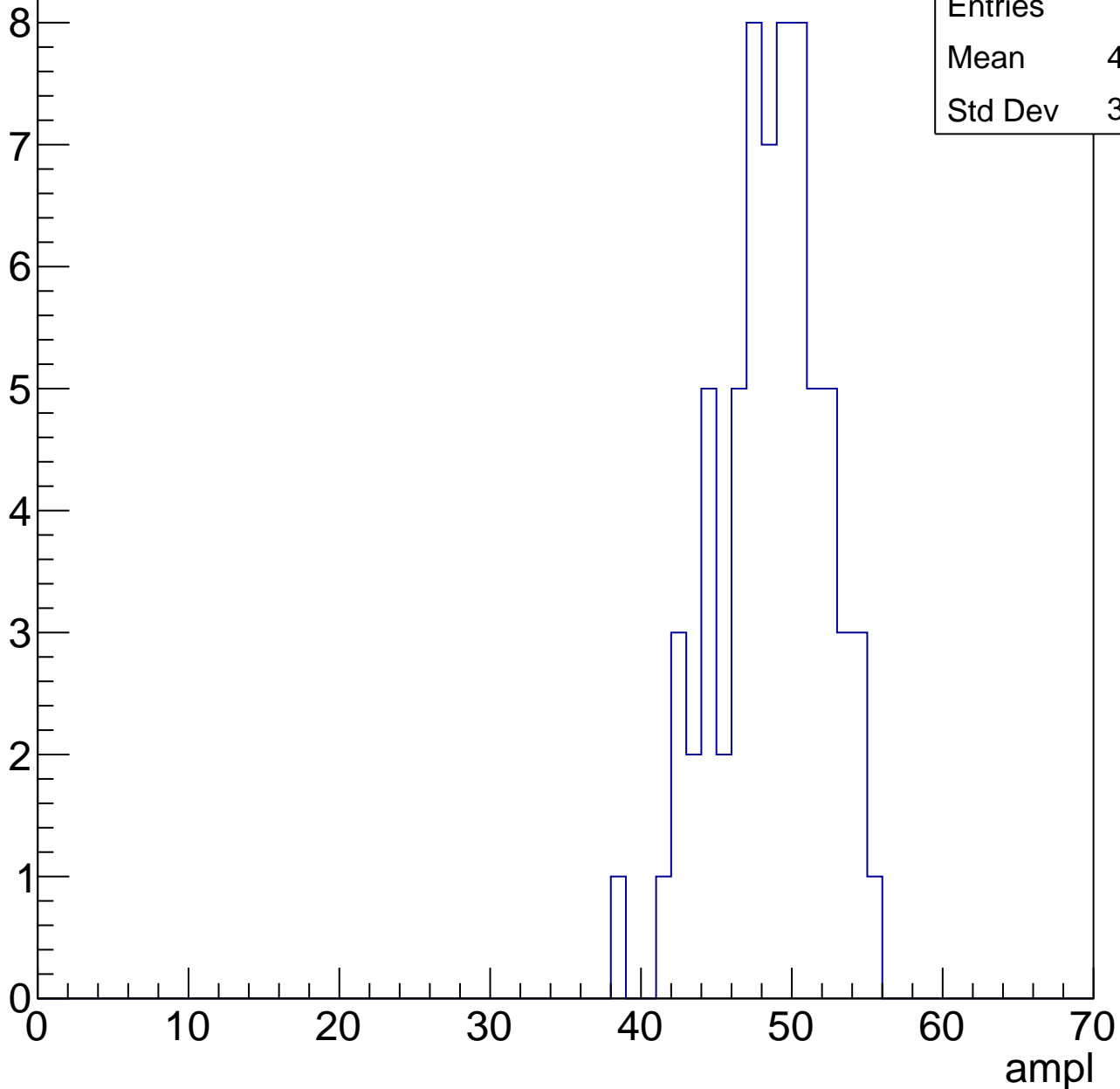


B1L100S, U15-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

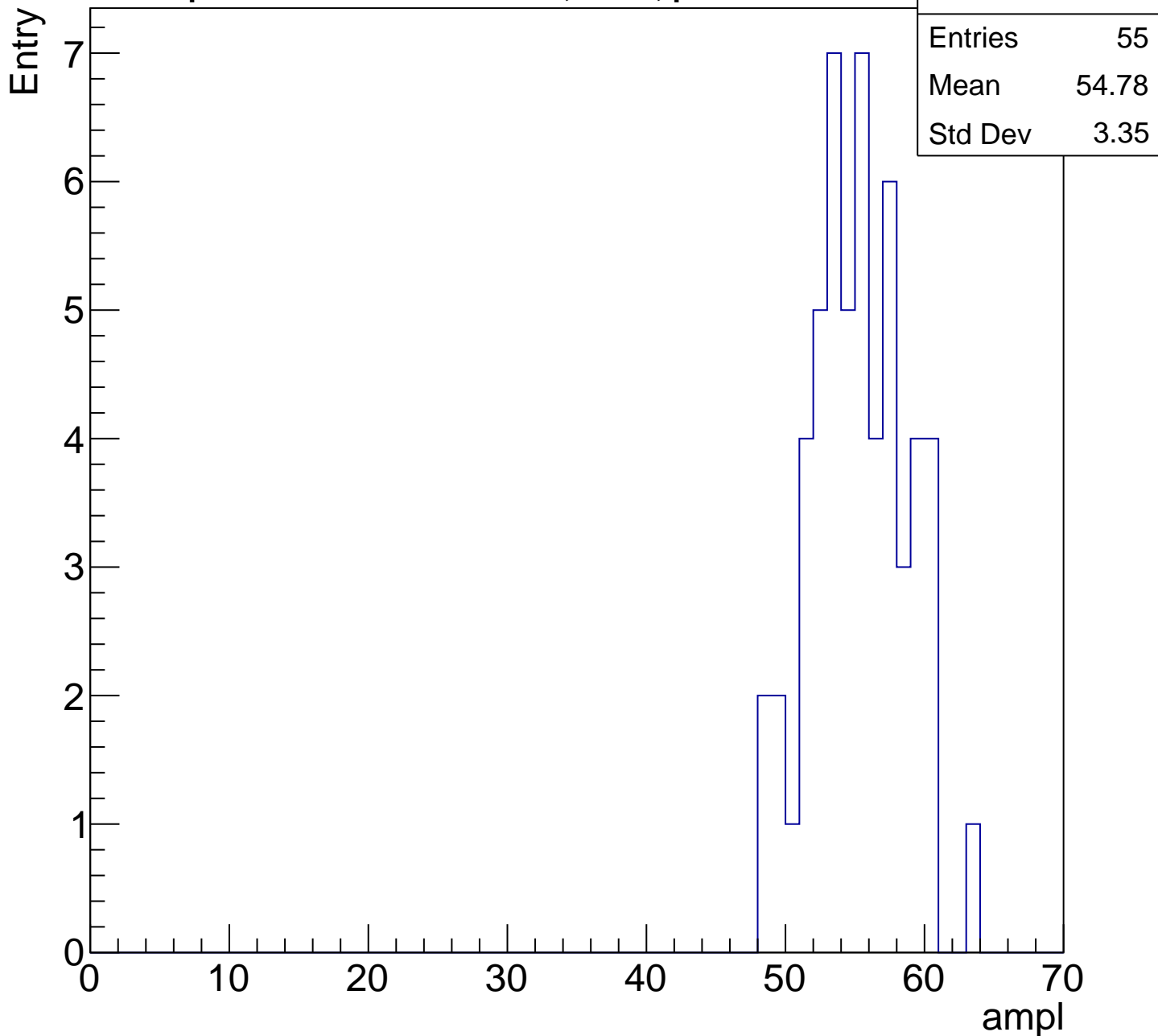
Entry

Entries	67
Mean	48.15
Std Dev	3.517



B1L100S, U15-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

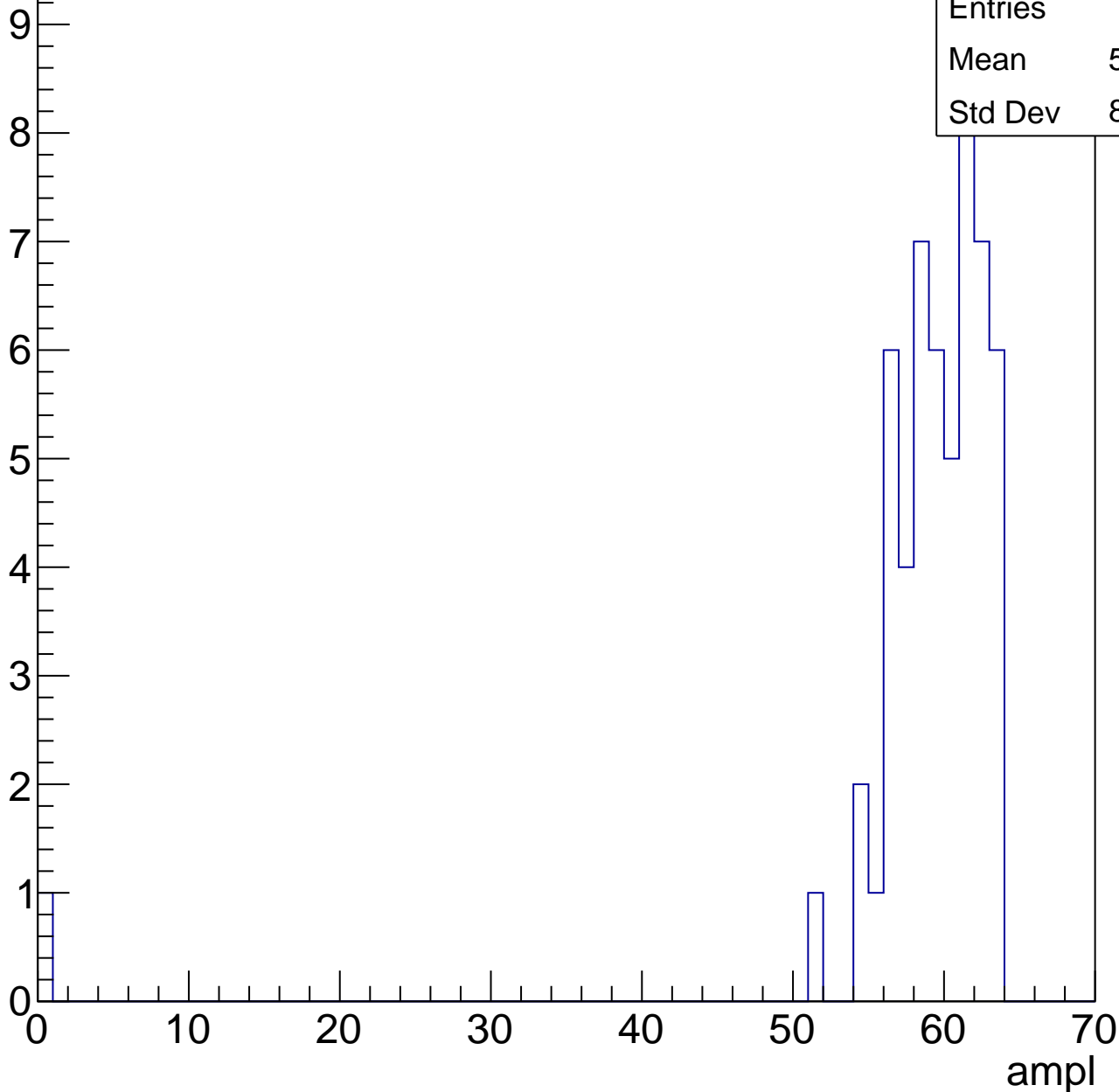


B1L100S, U15-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	58.16
Std Dev	8.366



B1L100S, U15-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

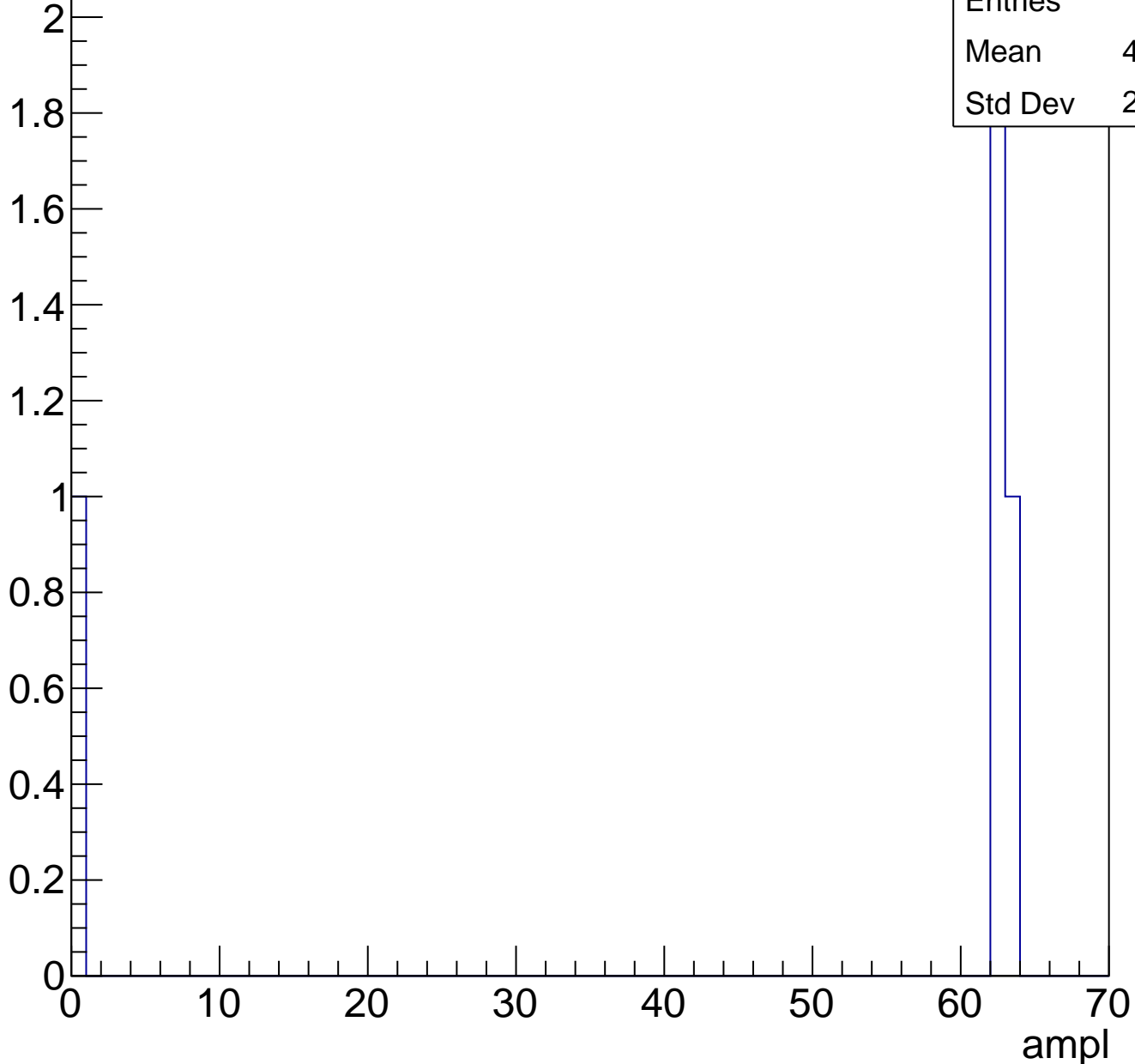
Entry



B1L100S, U15-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	46.75
Std Dev	26.99

B1L100S, U15-ch85, adc0

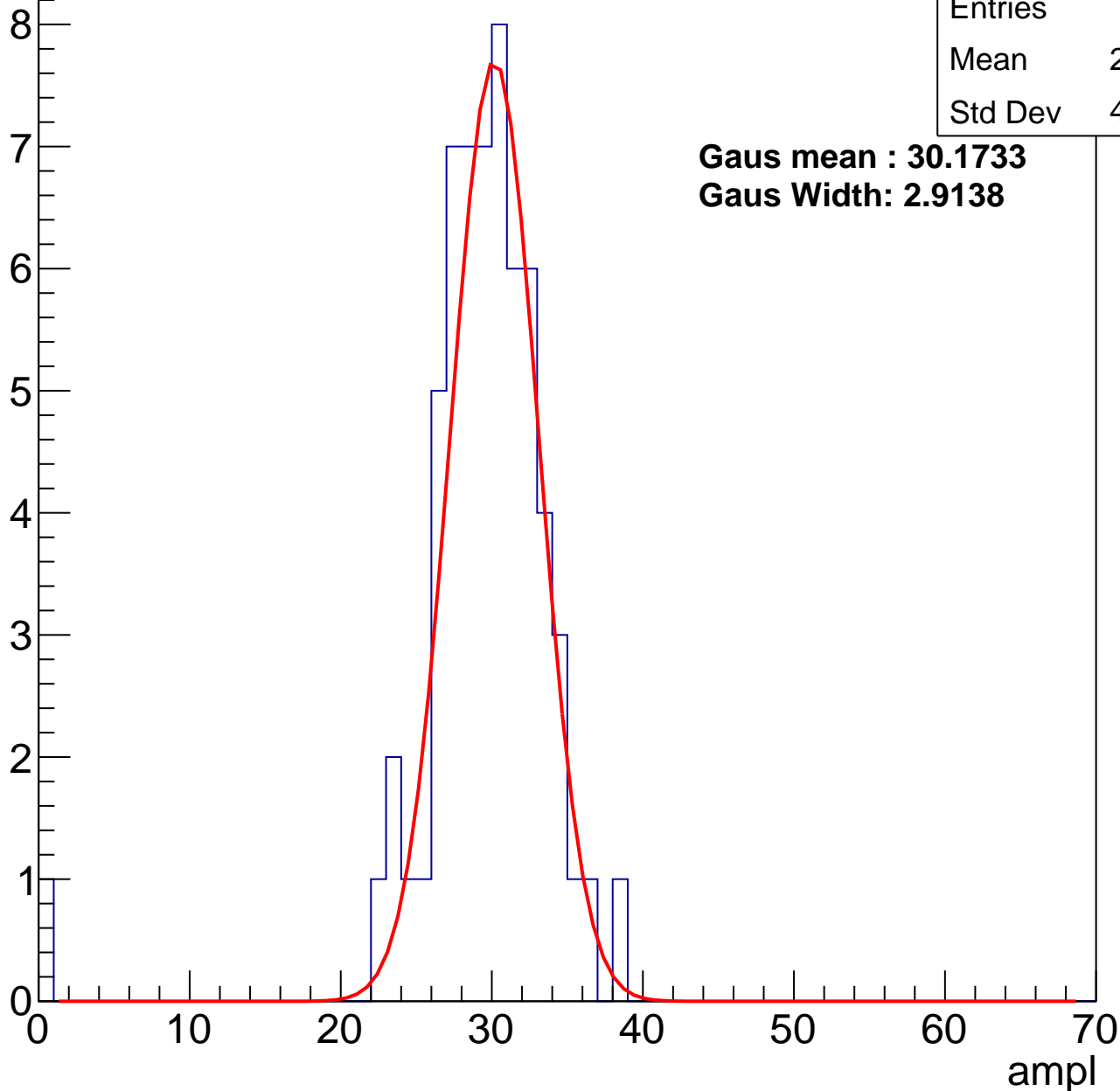
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.97
Std Dev	4.876

Gaus mean : 30.1733

Gaus Width: 2.9138



B1L100S, U15-ch85, adc1

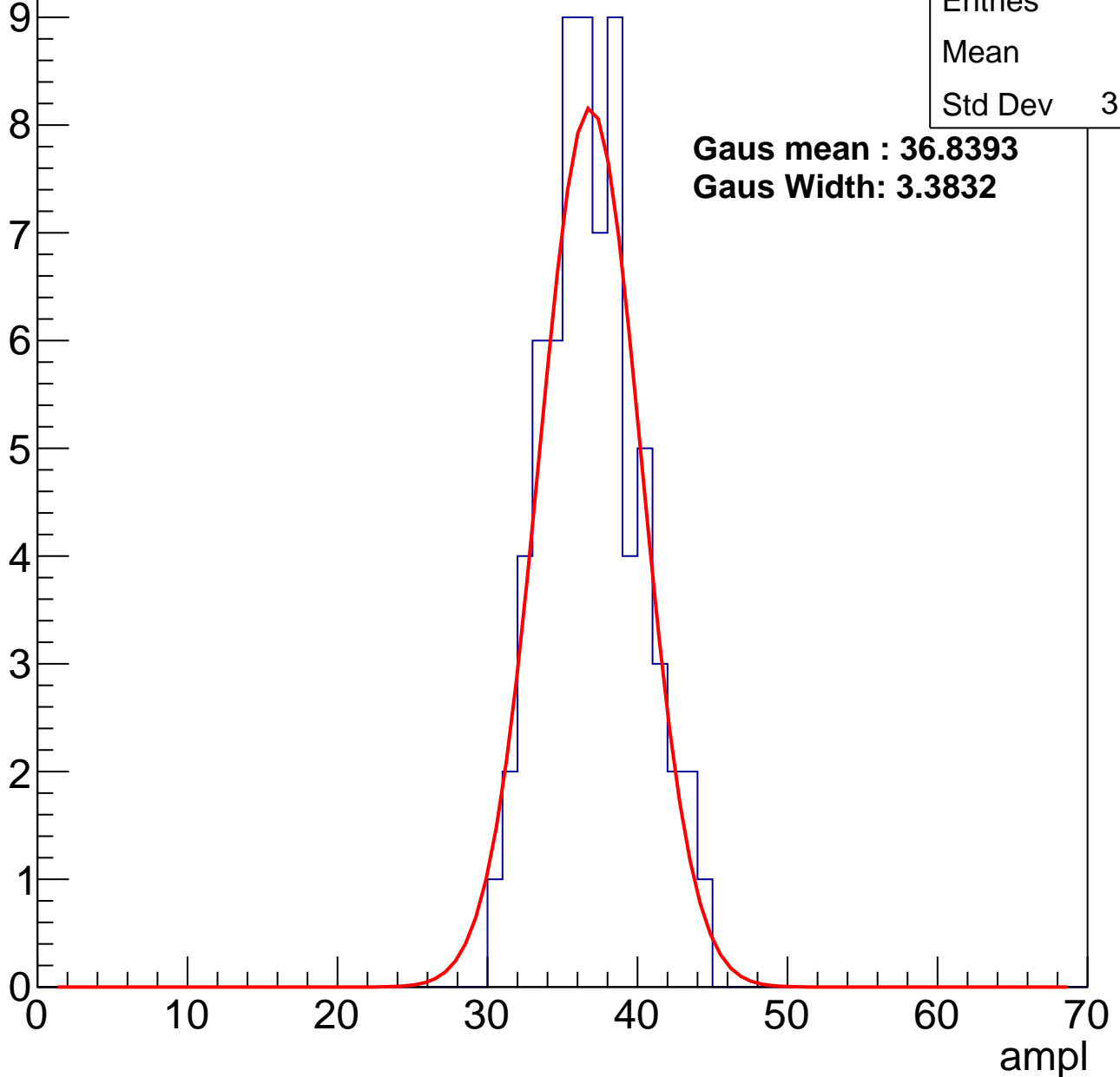
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.5
Std Dev	3.134

Gaus mean : 36.8393

Gaus Width: 3.3832



B1L100S, U15-ch85, adc2

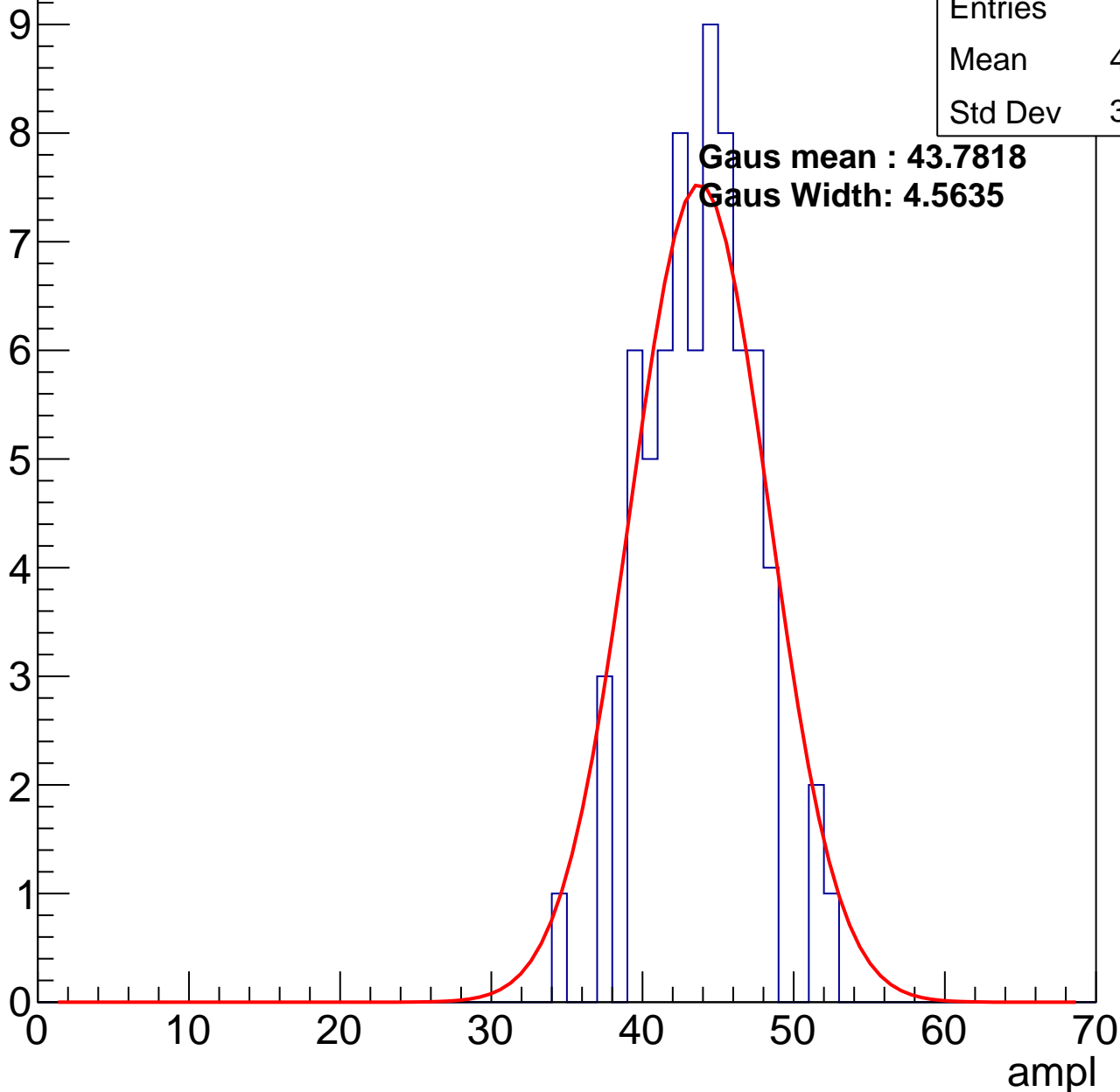
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.37
Std Dev	3.465

Gaus mean : 43.7818

Gaus Width: 4.5635



B1L100S, U15-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

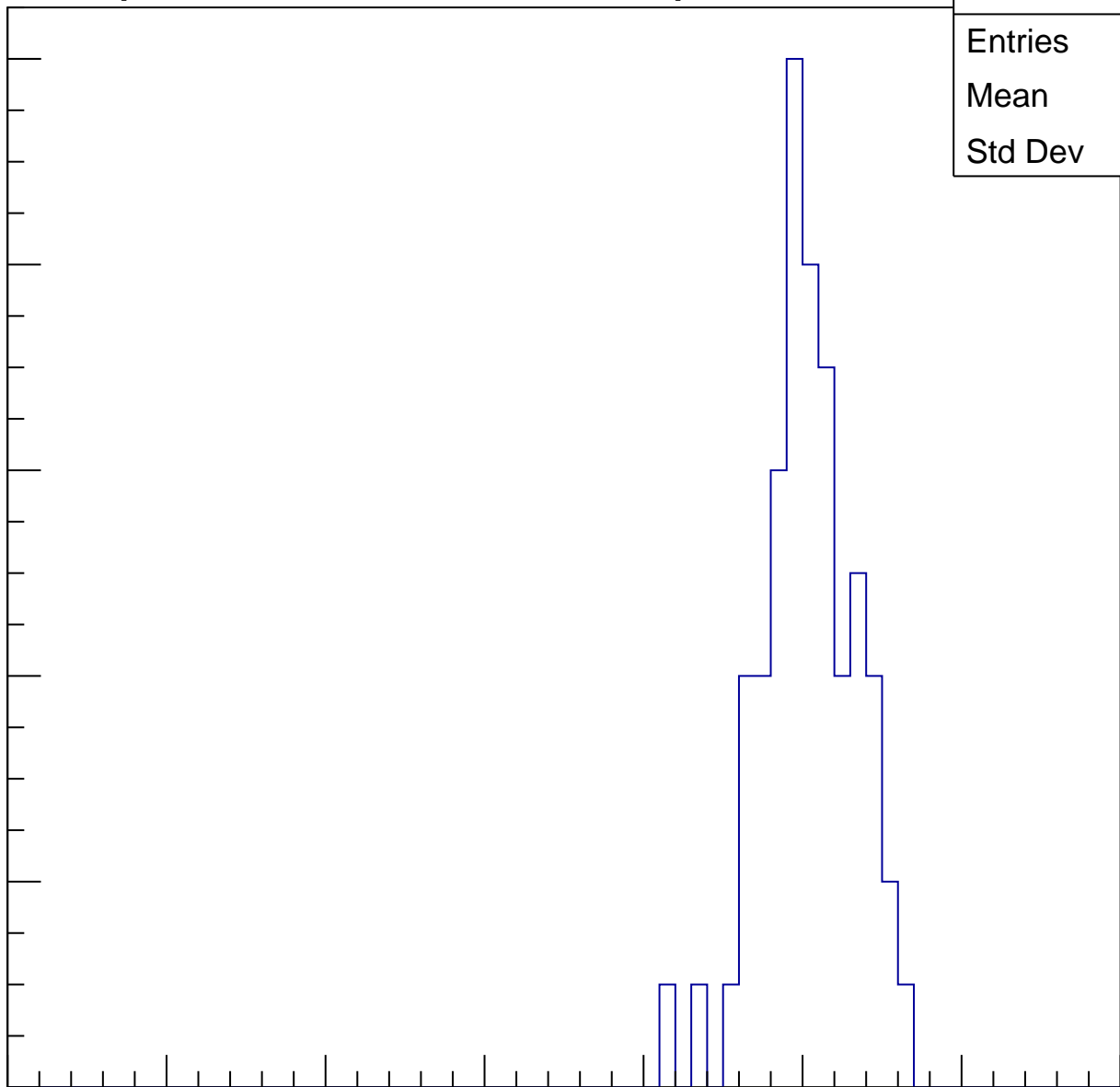
Entries	58
Mean	49.84
Std Dev	2.953

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

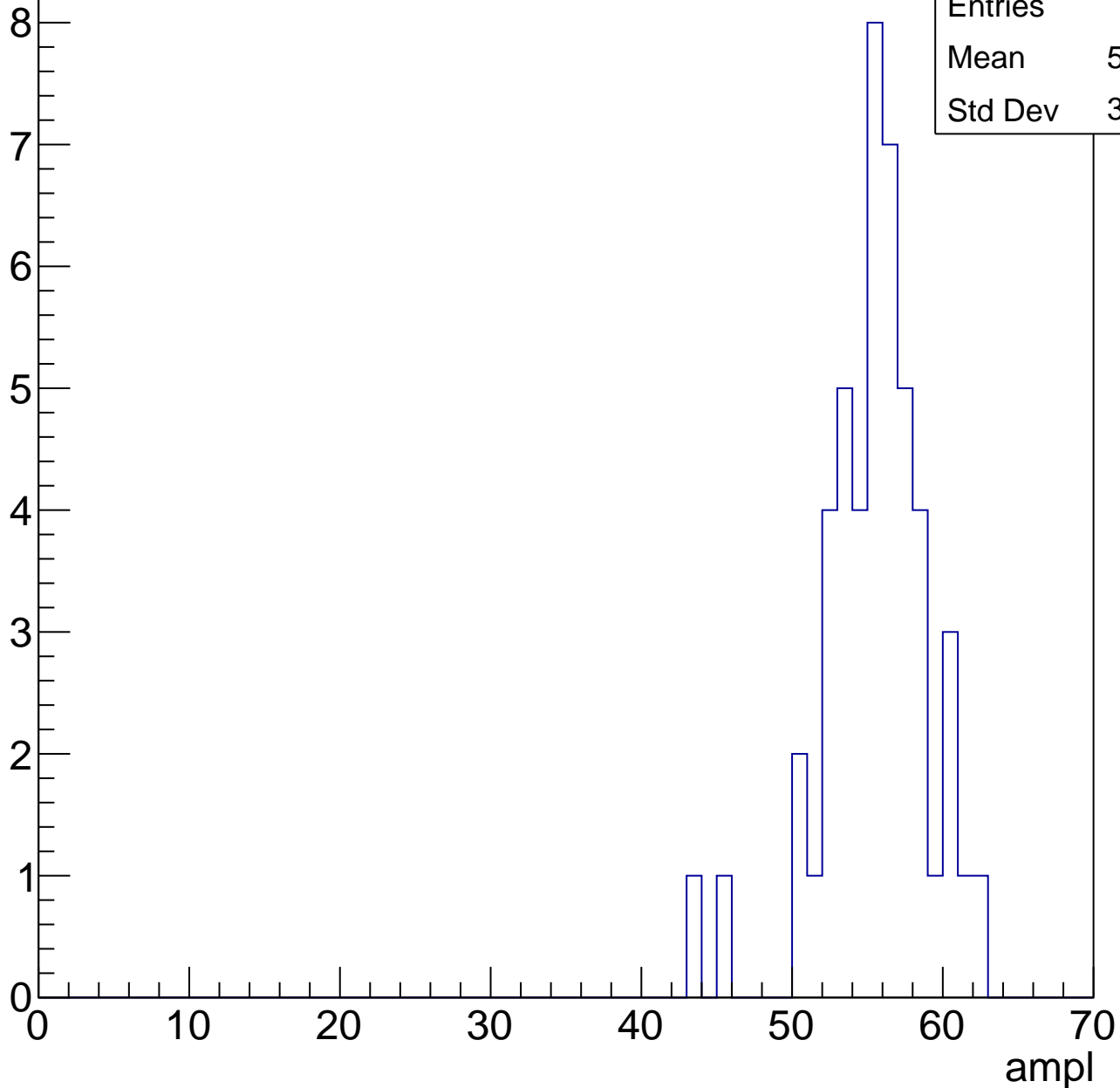
ampl



B1L100S, U15-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

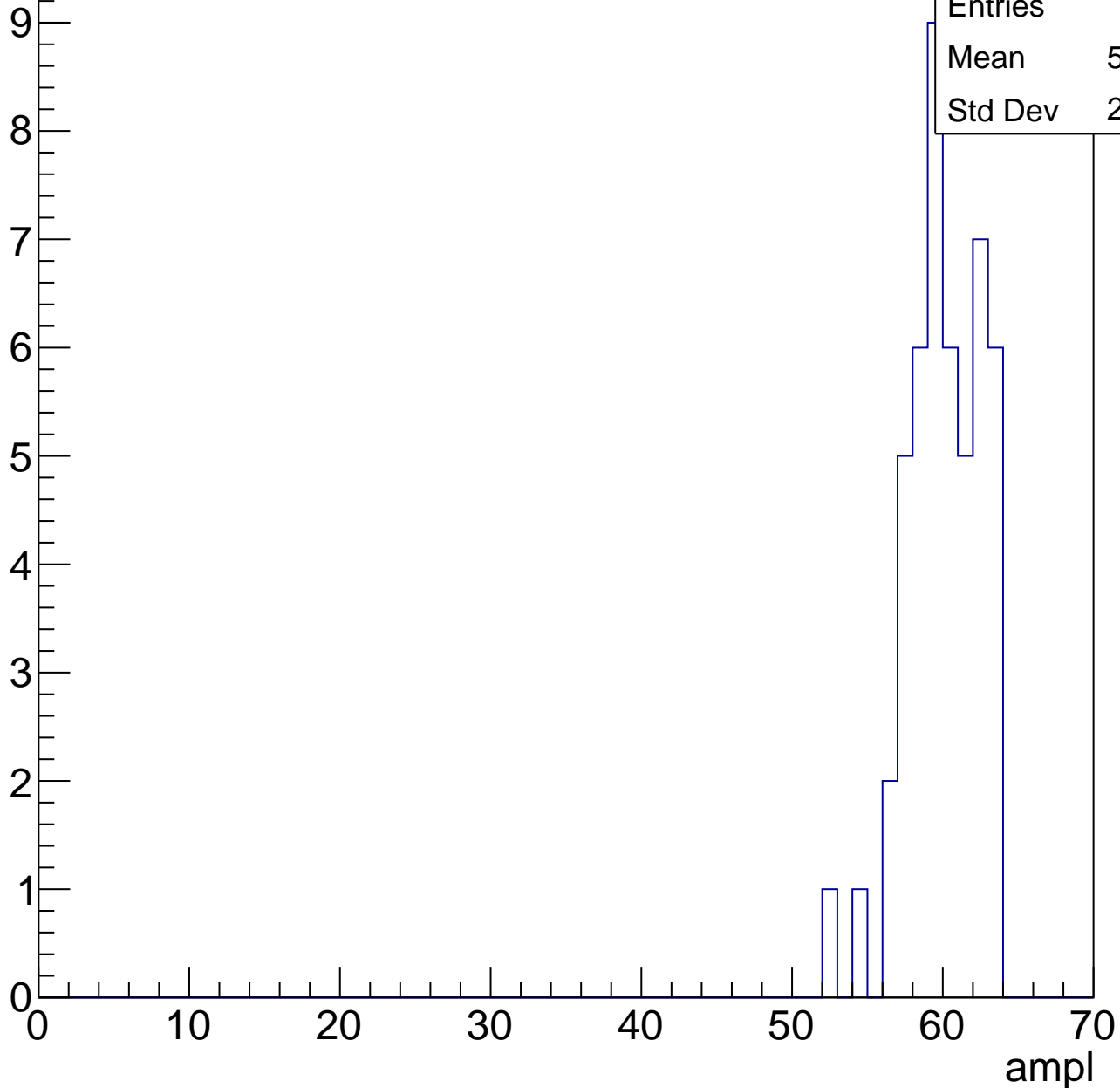
Entry



B1L100S, U15-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

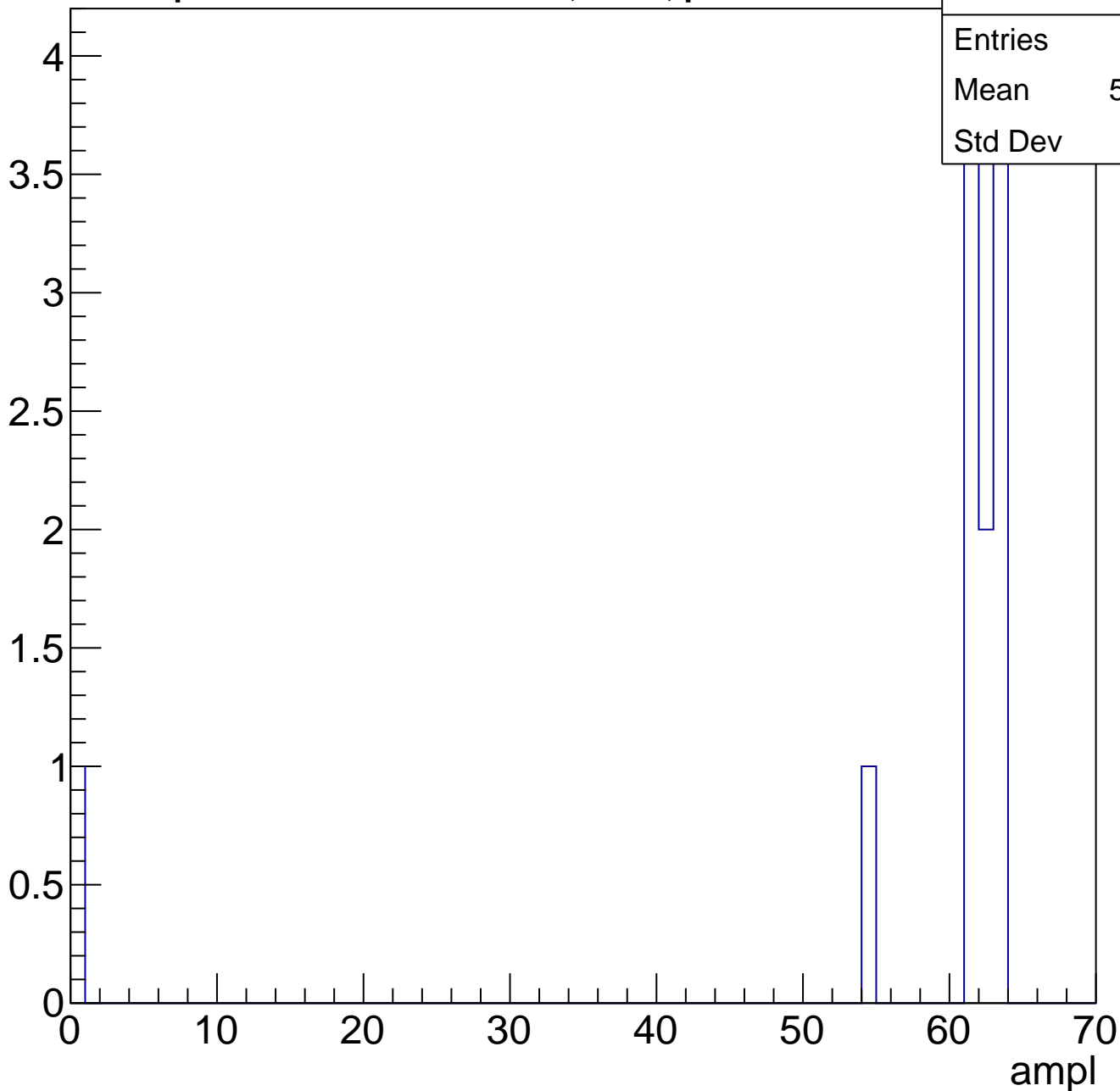
Entry



B1L100S, U15-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

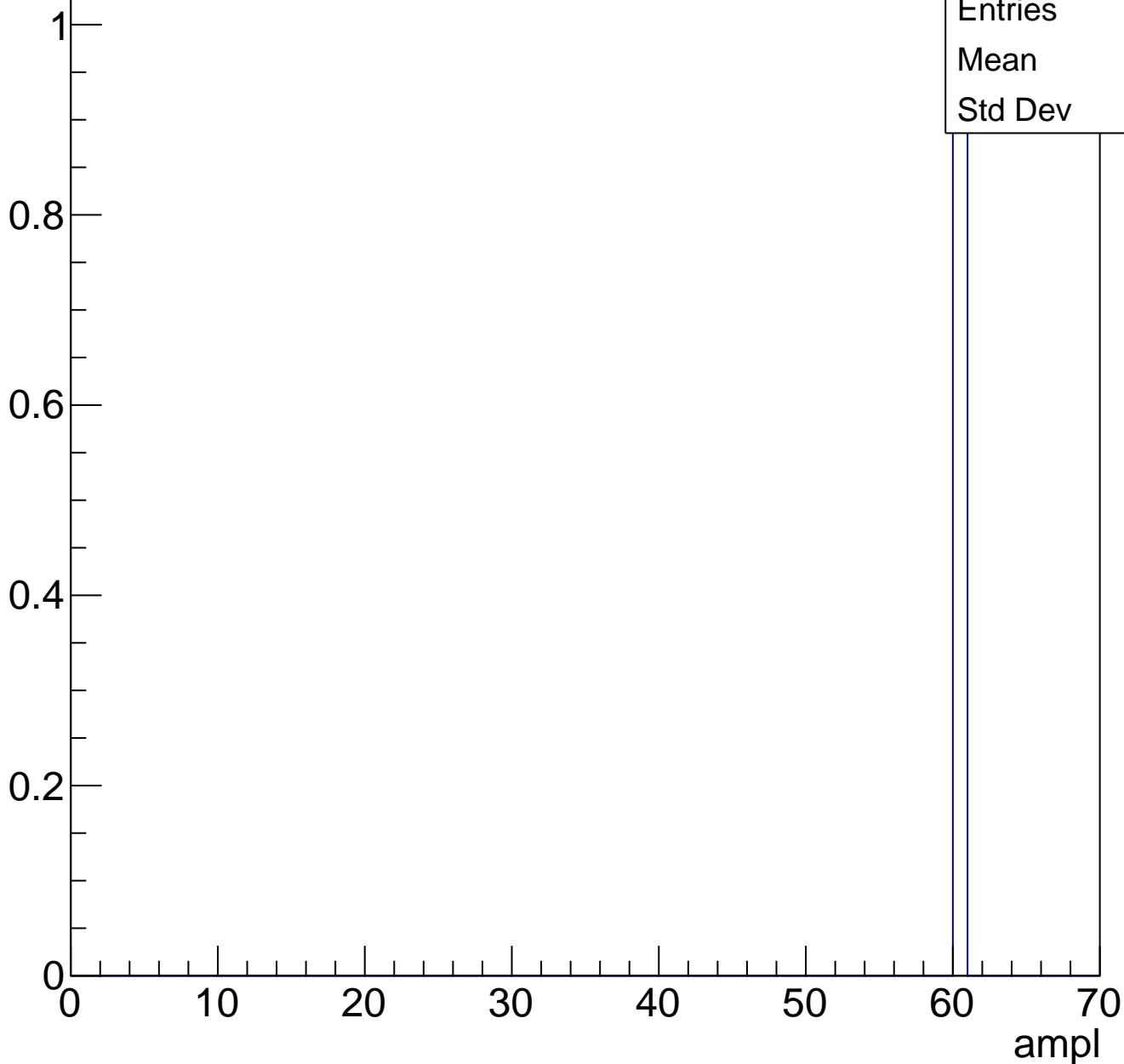
Entry



B1L100S, U15-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch86, adc0

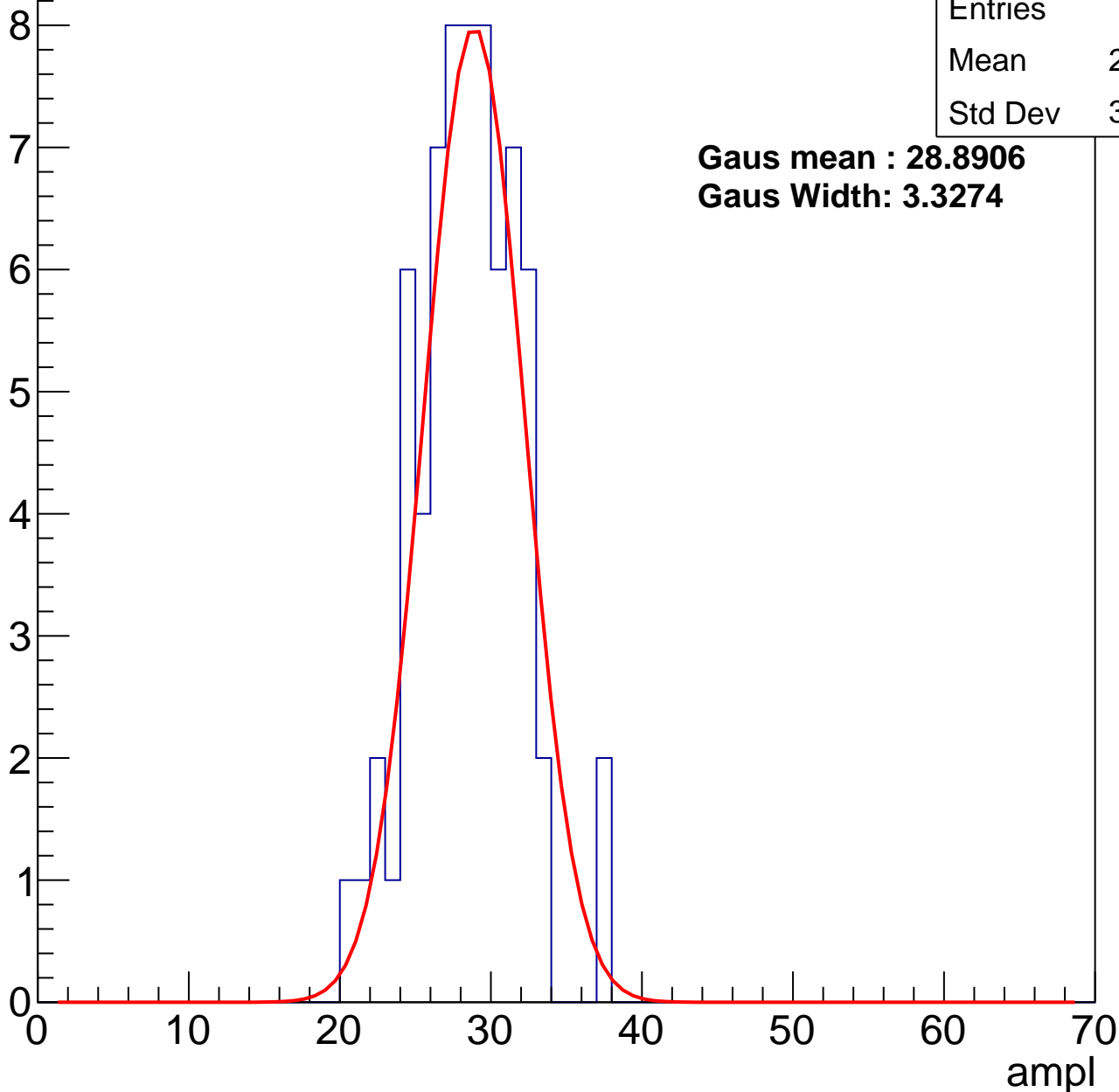
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	28.04
Std Dev	3.364

Gaus mean : 28.8906

Gaus Width: 3.3274



B1L100S, U15-ch86, adc1

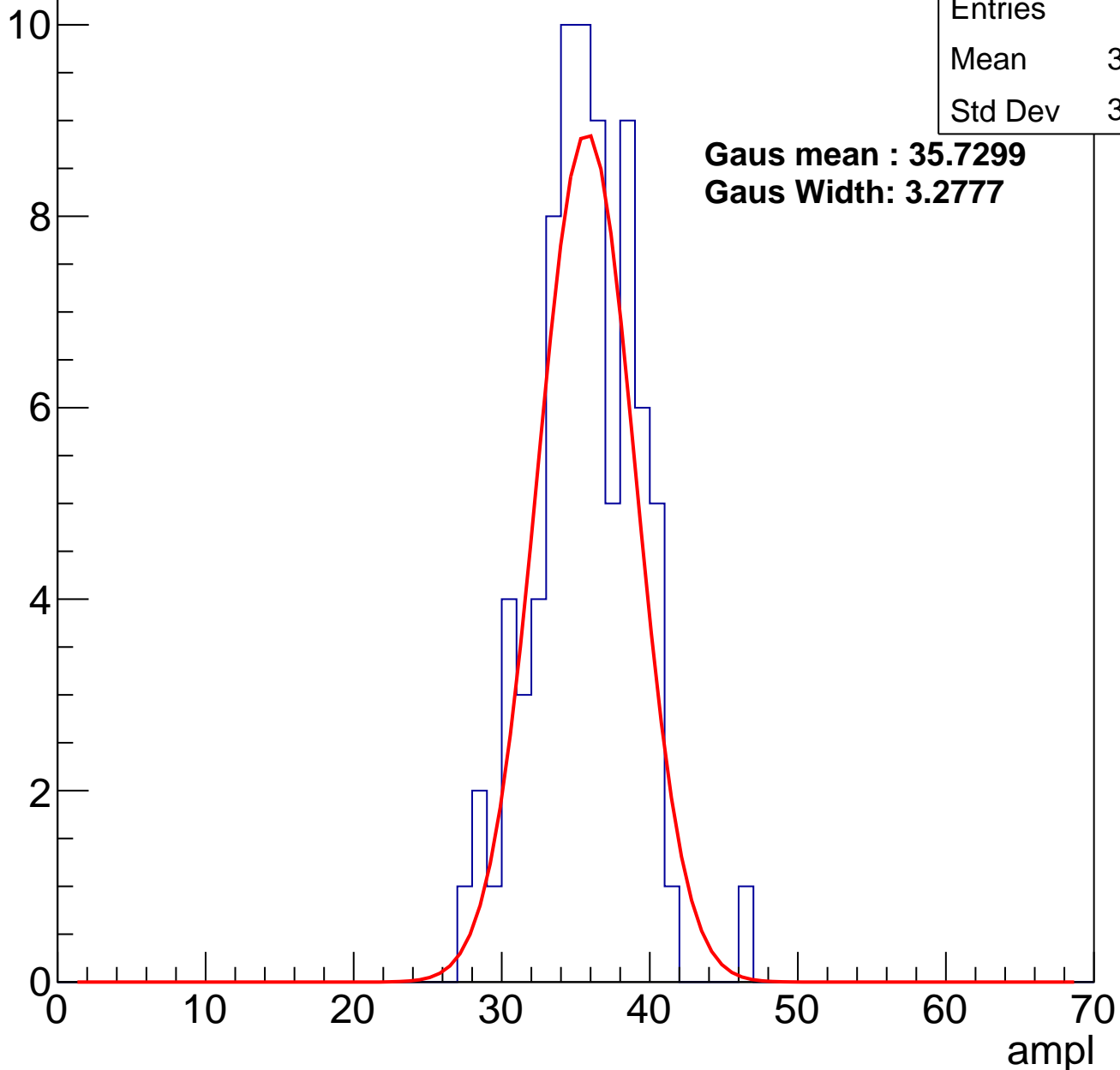
calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	35.18
Std Dev	3.393

Gaus mean : 35.7299

Gaus Width: 3.2777

Entry



B1L100S, U15-ch86, adc2

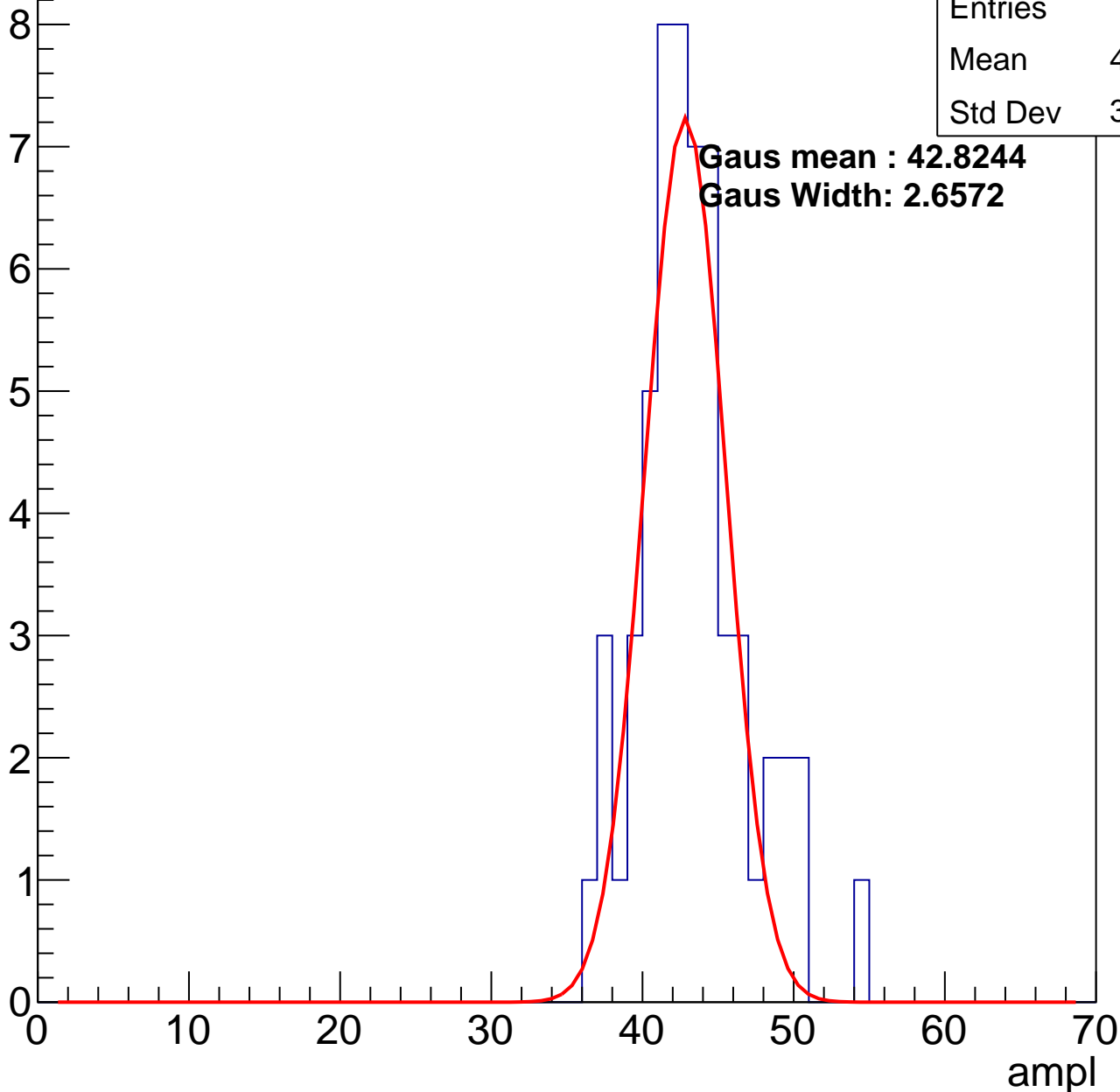
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.86
Std Dev	3.546

Gaus mean : 42.8244

Gaus Width: 2.6572

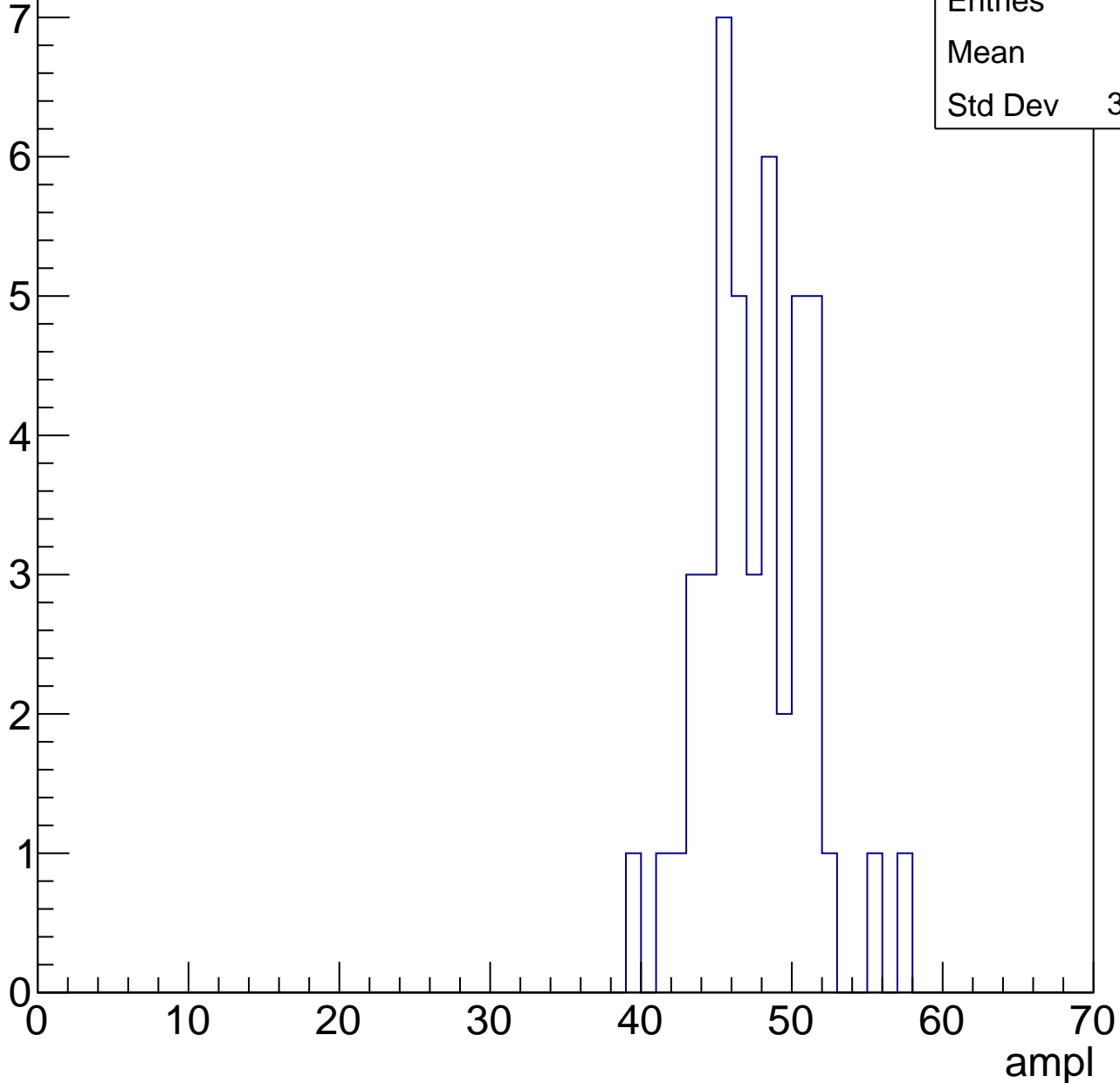


B1L100S, U15-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	47.2
Std Dev	3.525

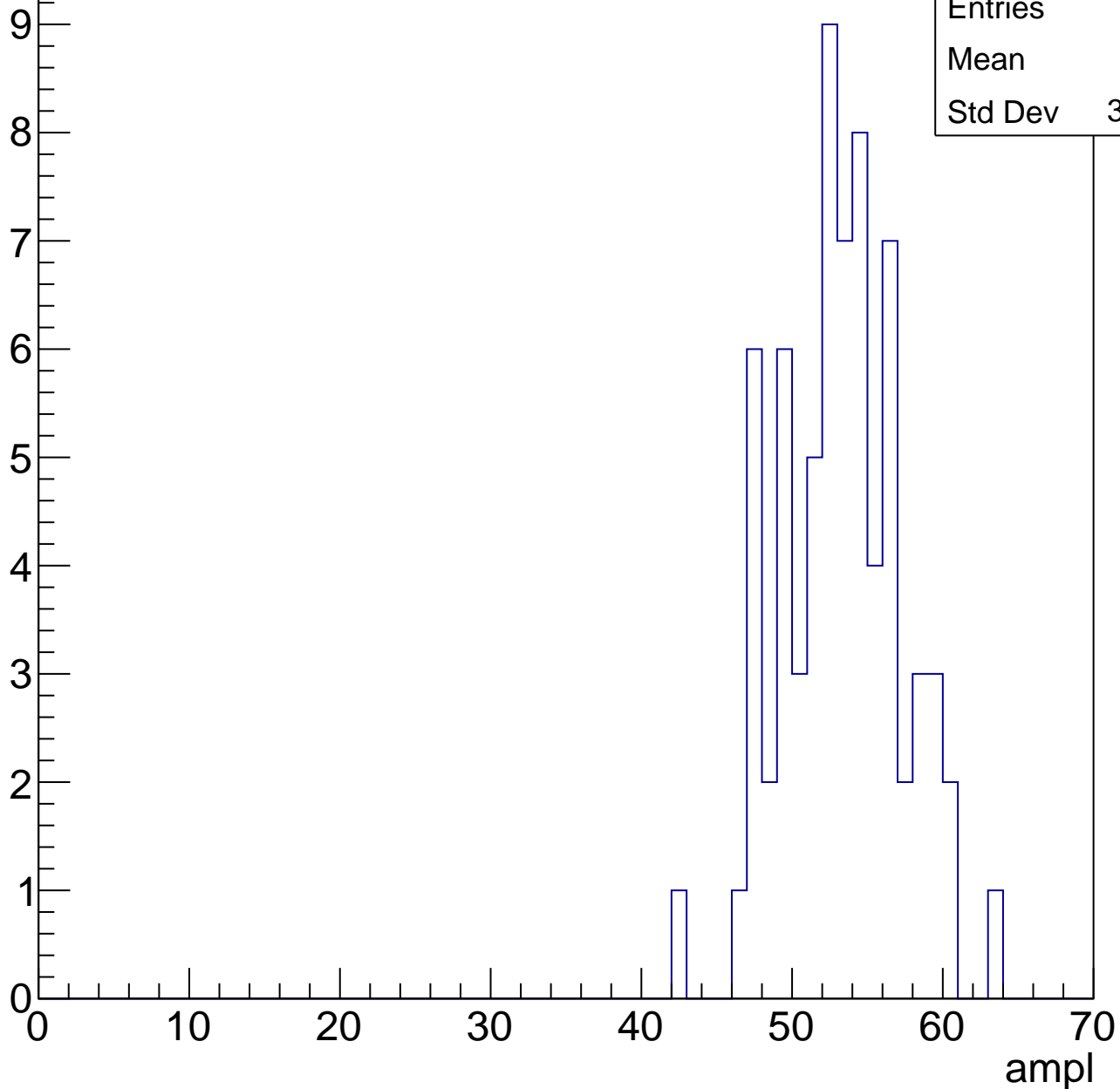


B1L100S, U15-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

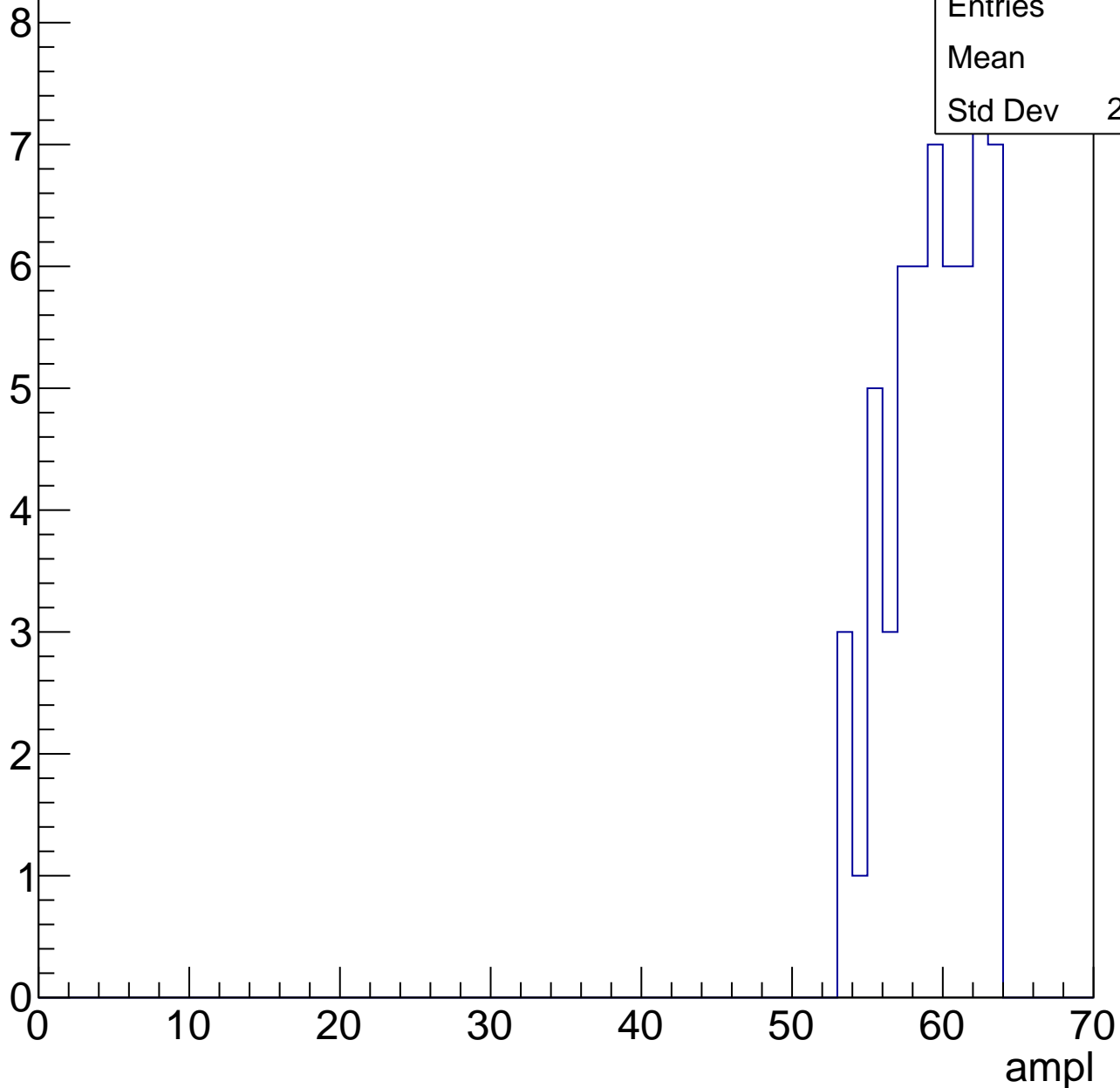
Entries	70
Mean	52.8
Std Dev	3.937



B1L100S, U15-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

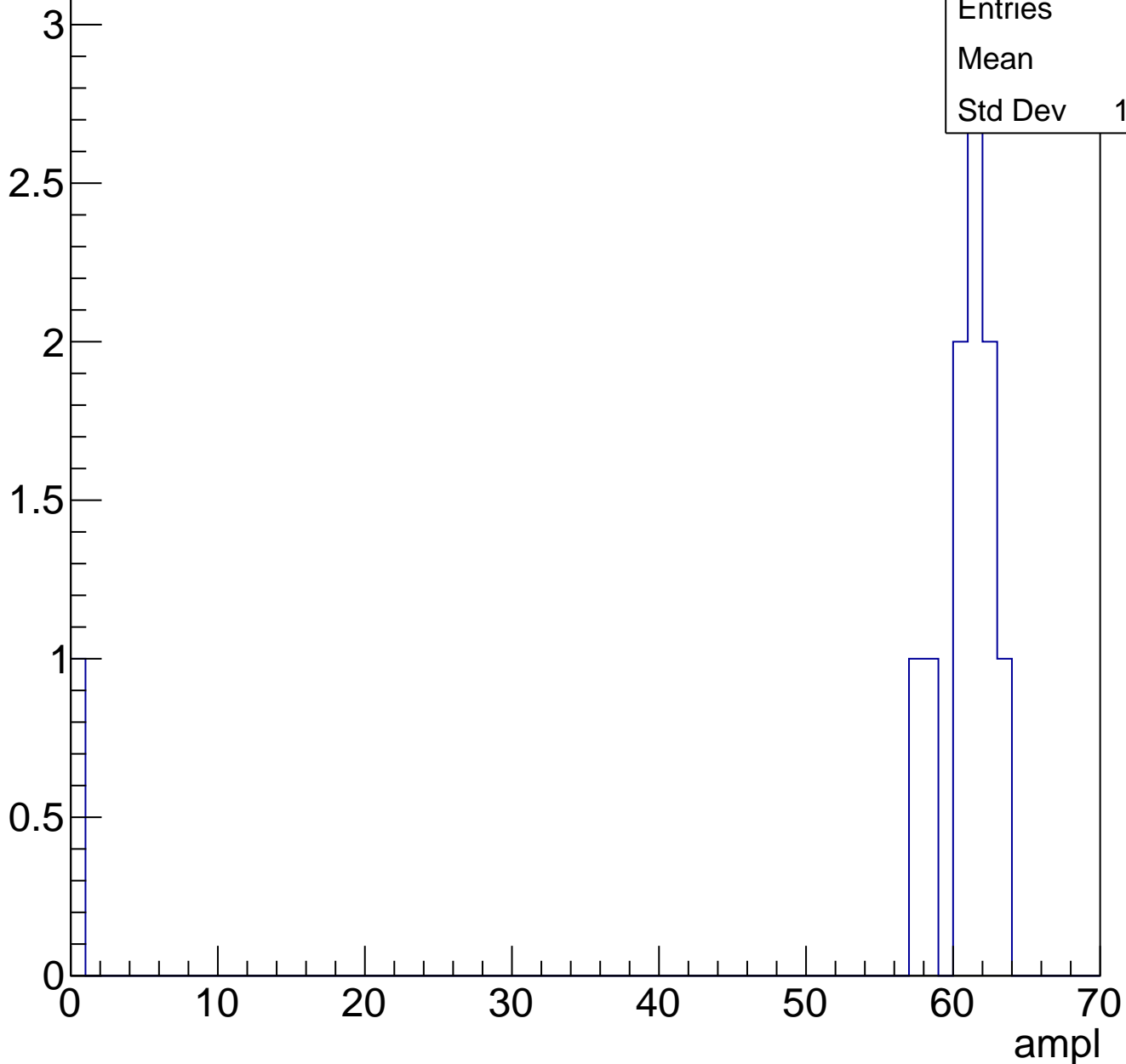
Entry



B1L100S, U15-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch87, adc0

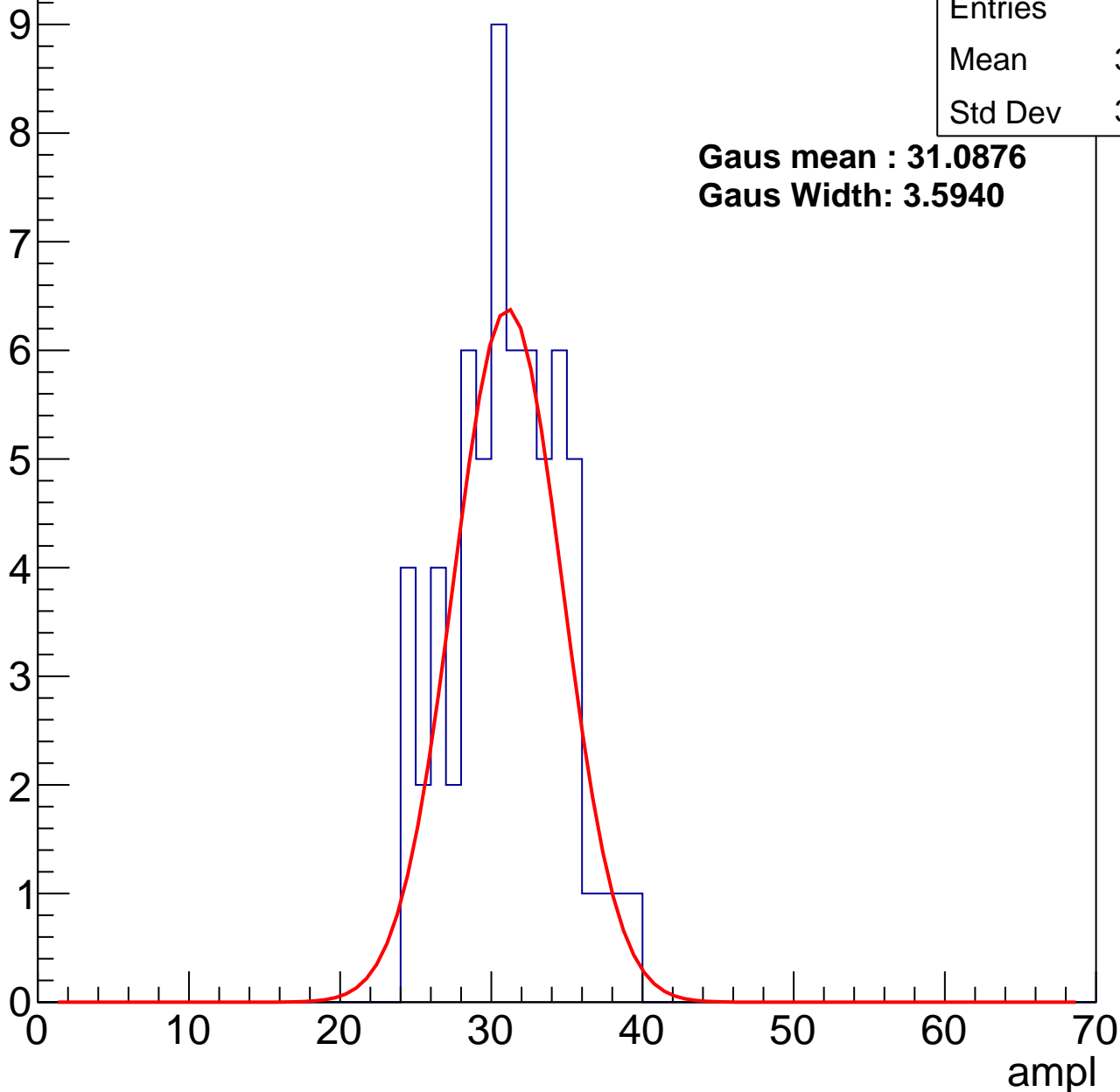
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.61
Std Dev	3.551

Gaus mean : 31.0876

Gaus Width: 3.5940



B1L100S, U15-ch87, adc1

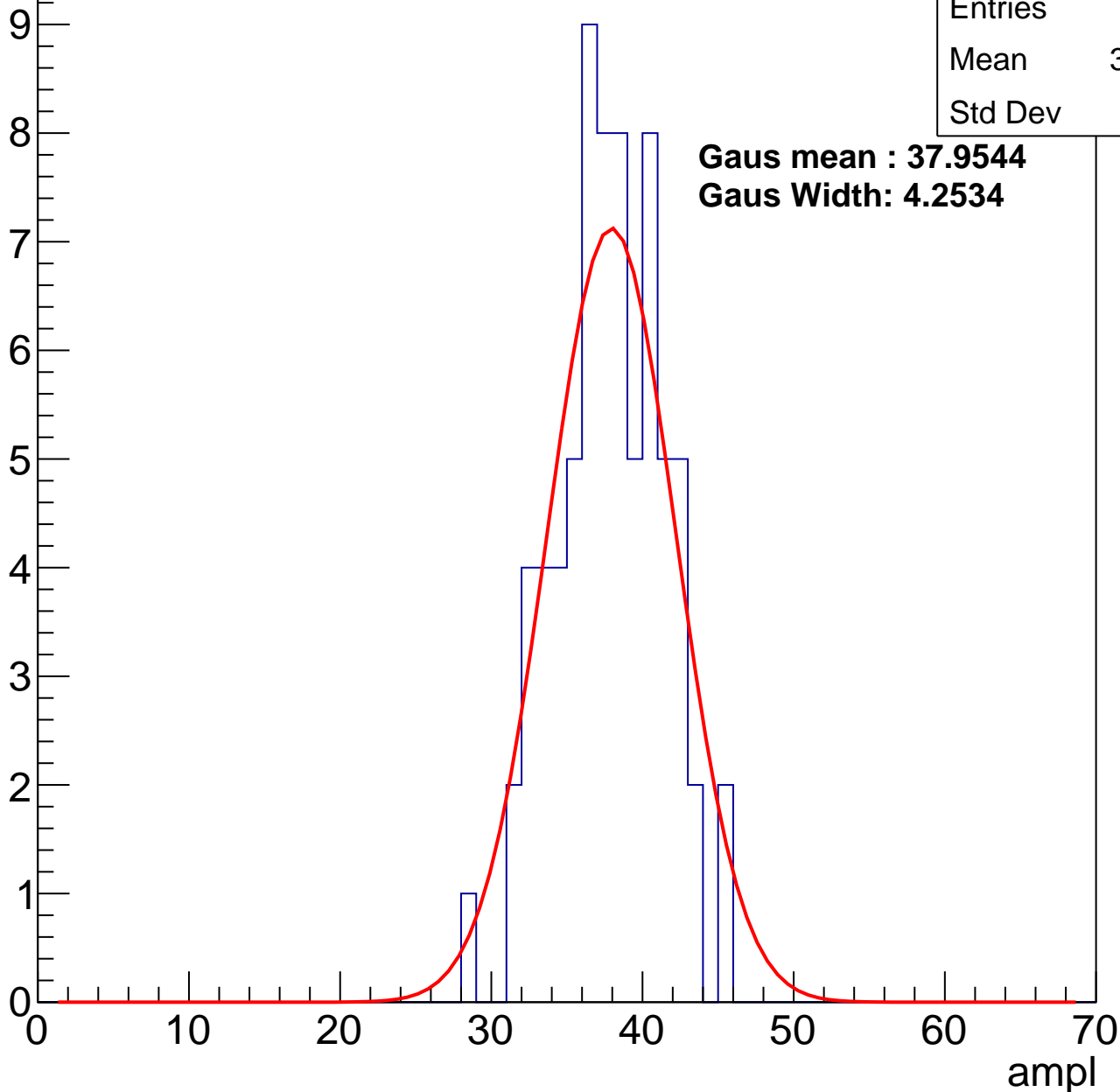
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.38
Std Dev	3.49

Gaus mean : 37.9544

Gaus Width: 4.2534



B1L100S, U15-ch87, adc2

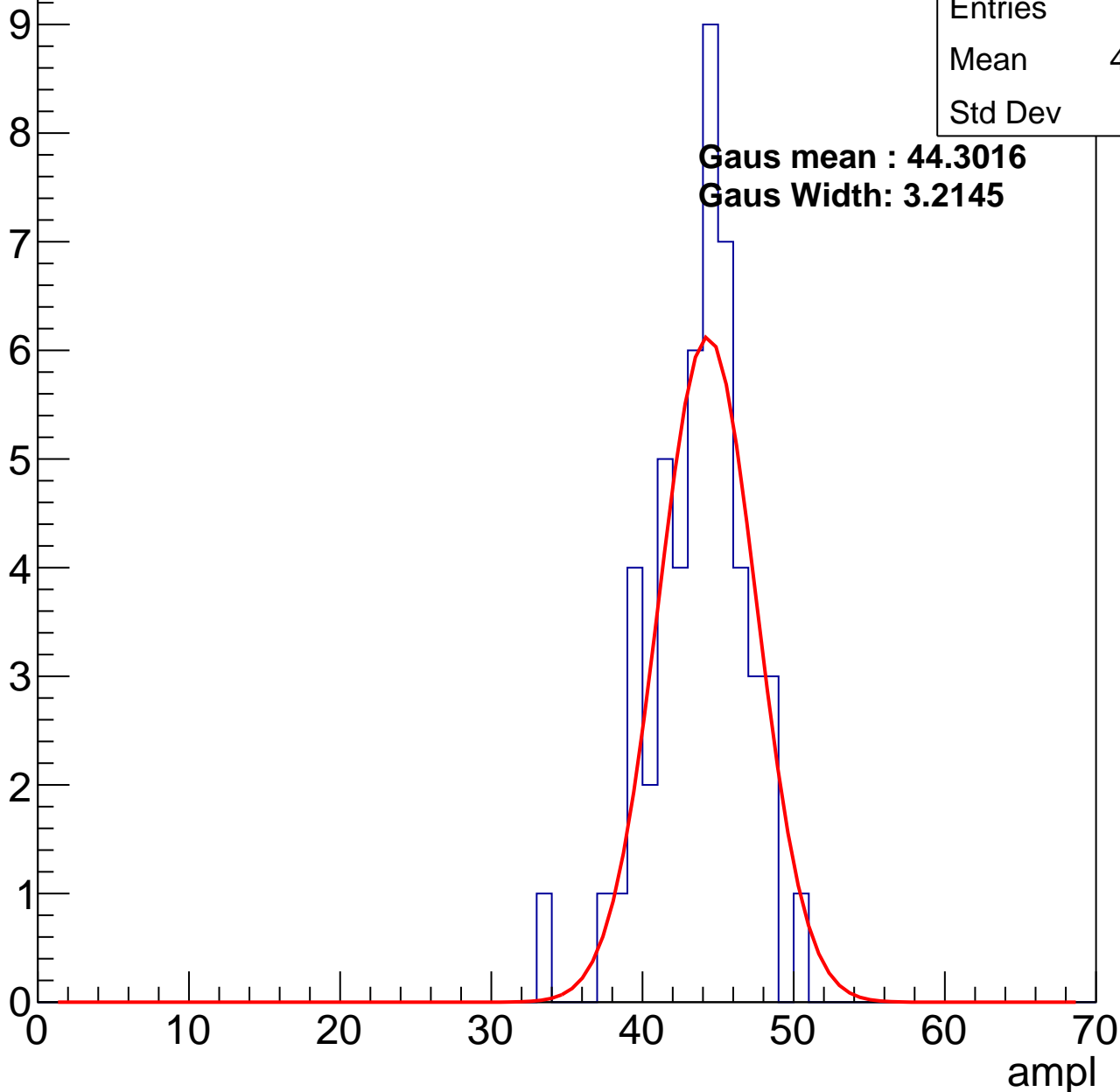
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	43.24
Std Dev	3.16

Gaus mean : 44.3016

Gaus Width: 3.2145



B1L100S, U15-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	49.71
Std Dev	3.136

Entry

10

8

6

4

2

0

0

10

20

30

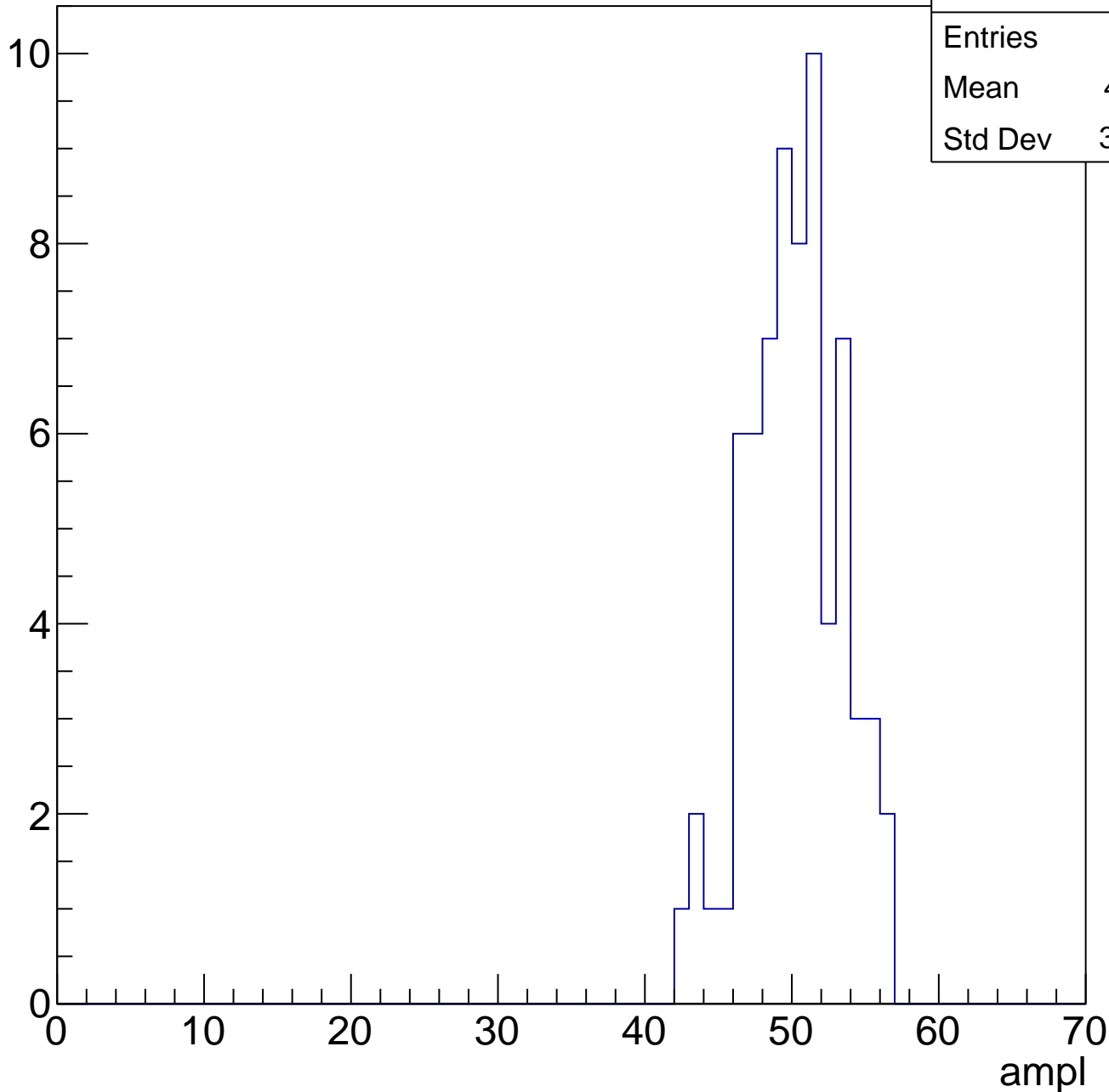
40

50

60

70

ampl

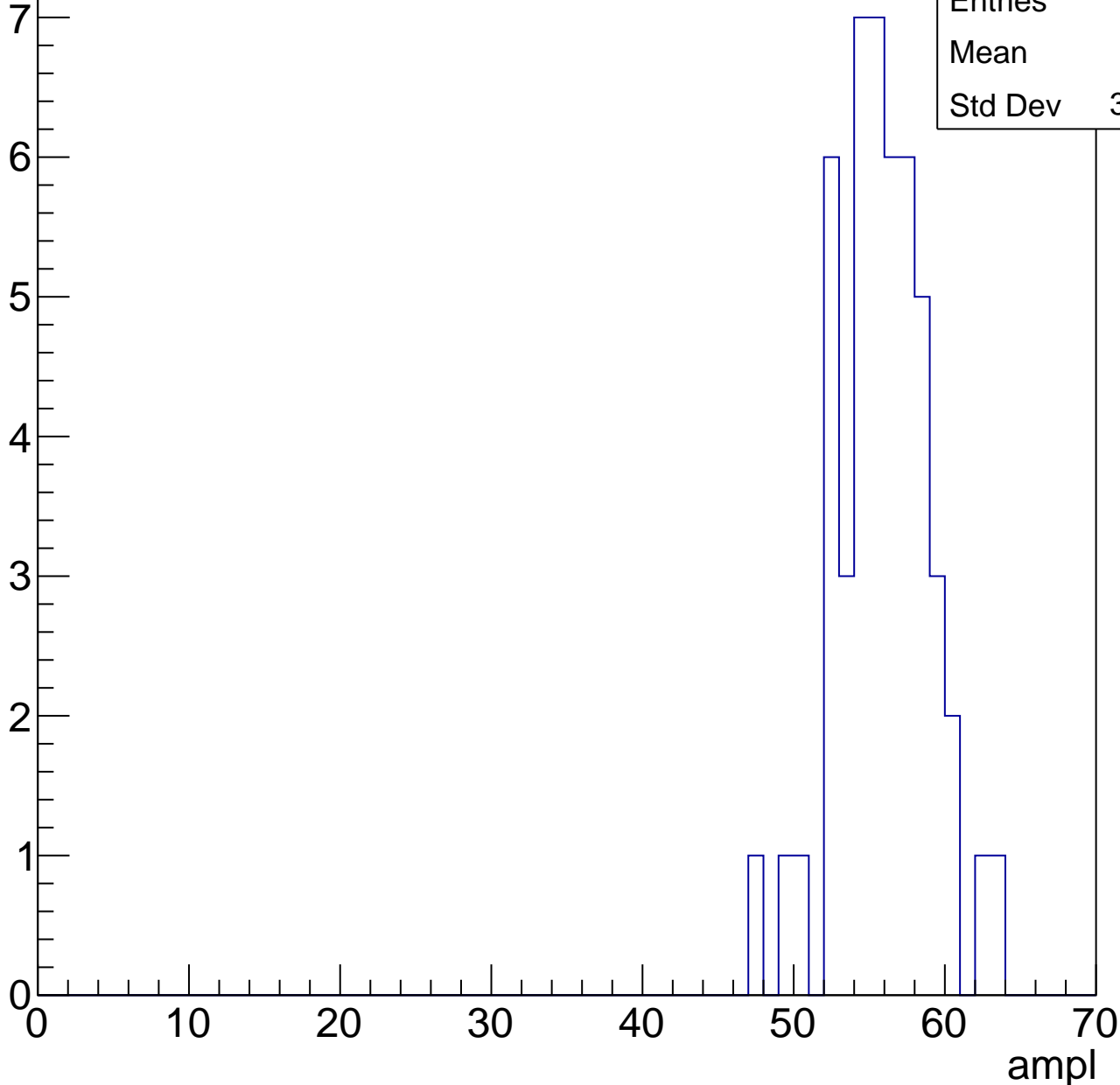


B1L100S, U15-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	55.4
Std Dev	3.085

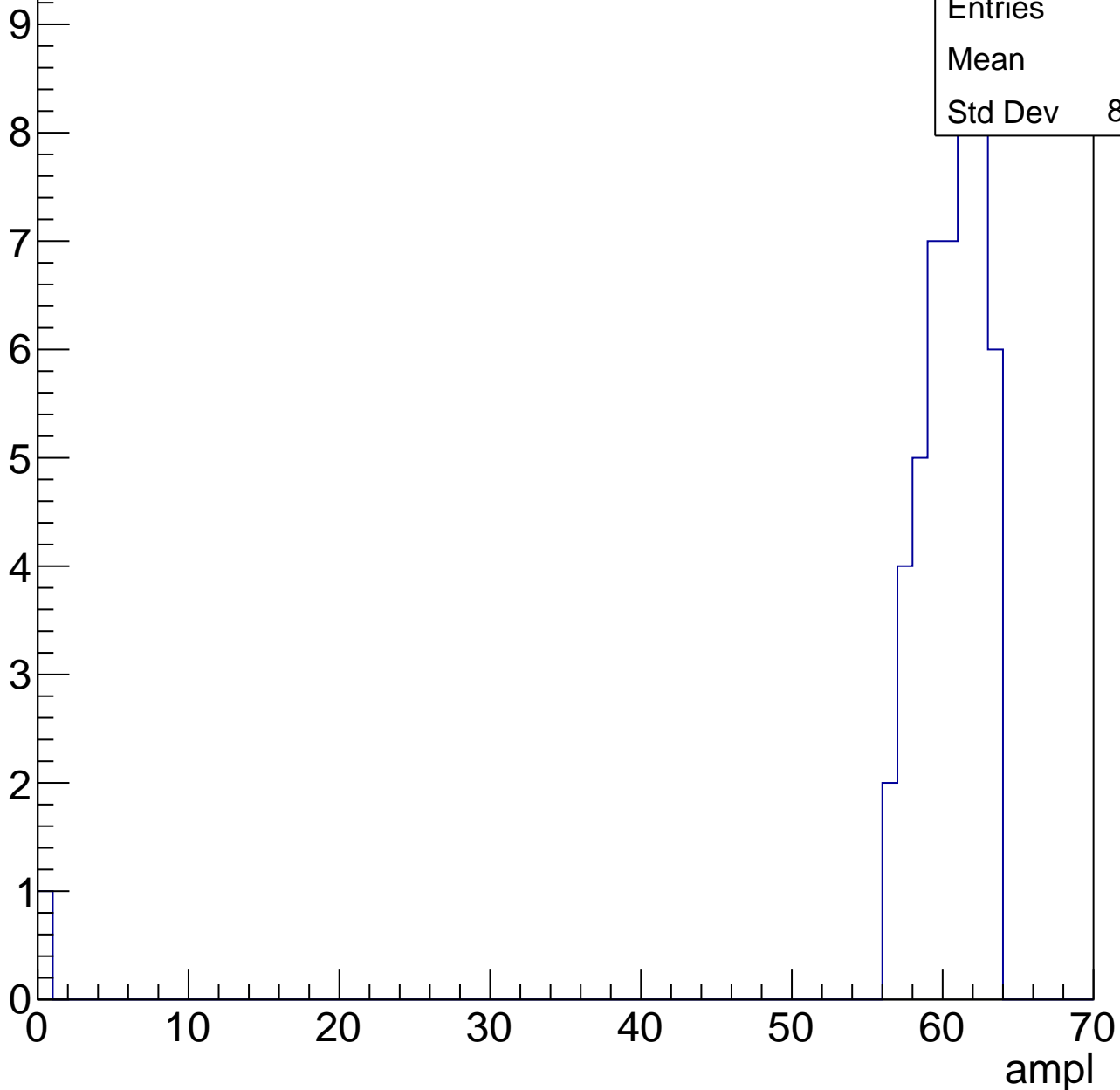


B1L100S, U15-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.9
Std Dev	8.725



B1L100S, U15-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U15-ch88, adc0

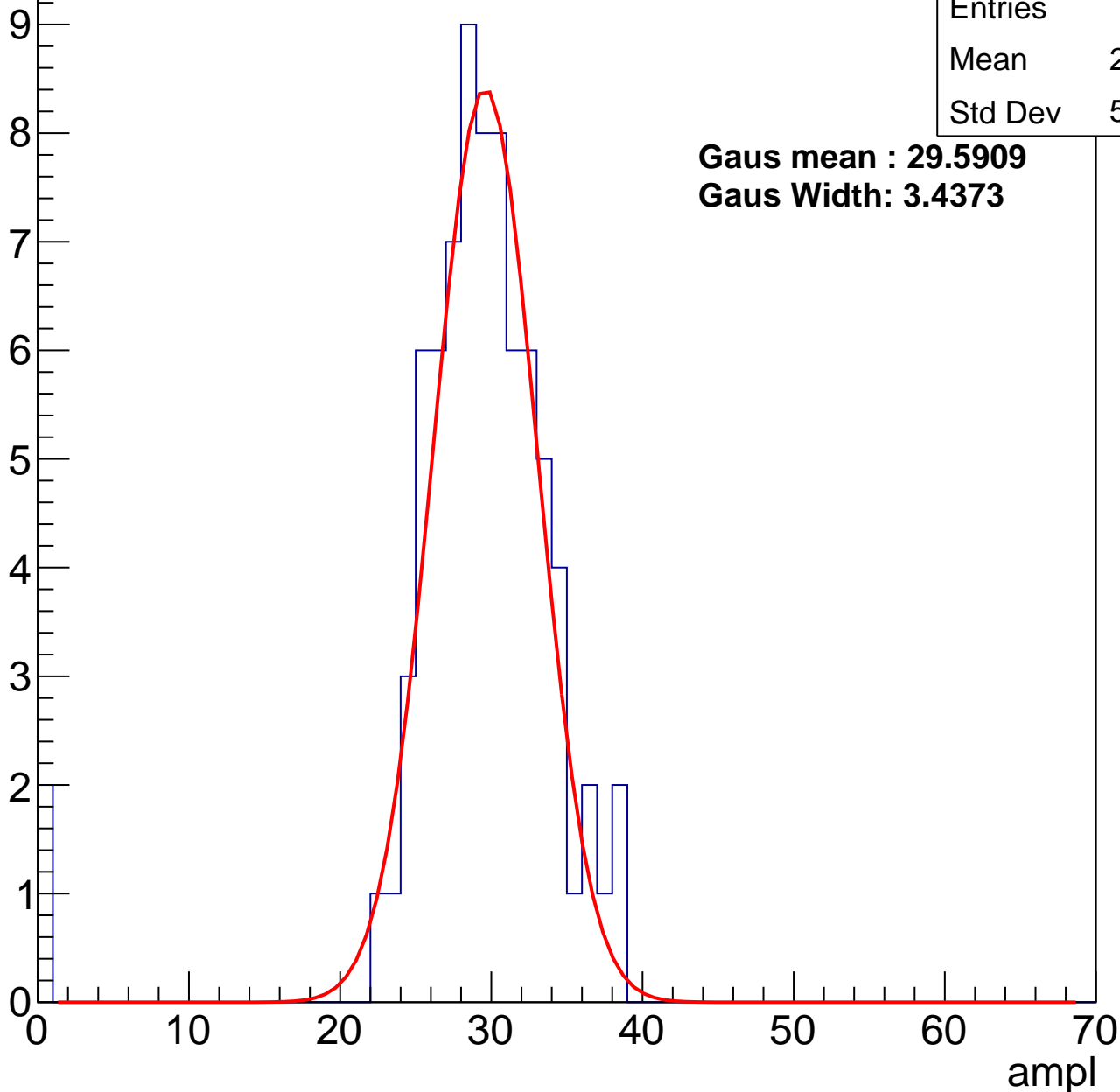
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.65
Std Dev	5.824

Gaus mean : 29.5909

Gaus Width: 3.4373



B1L100S, U15-ch88, adc1

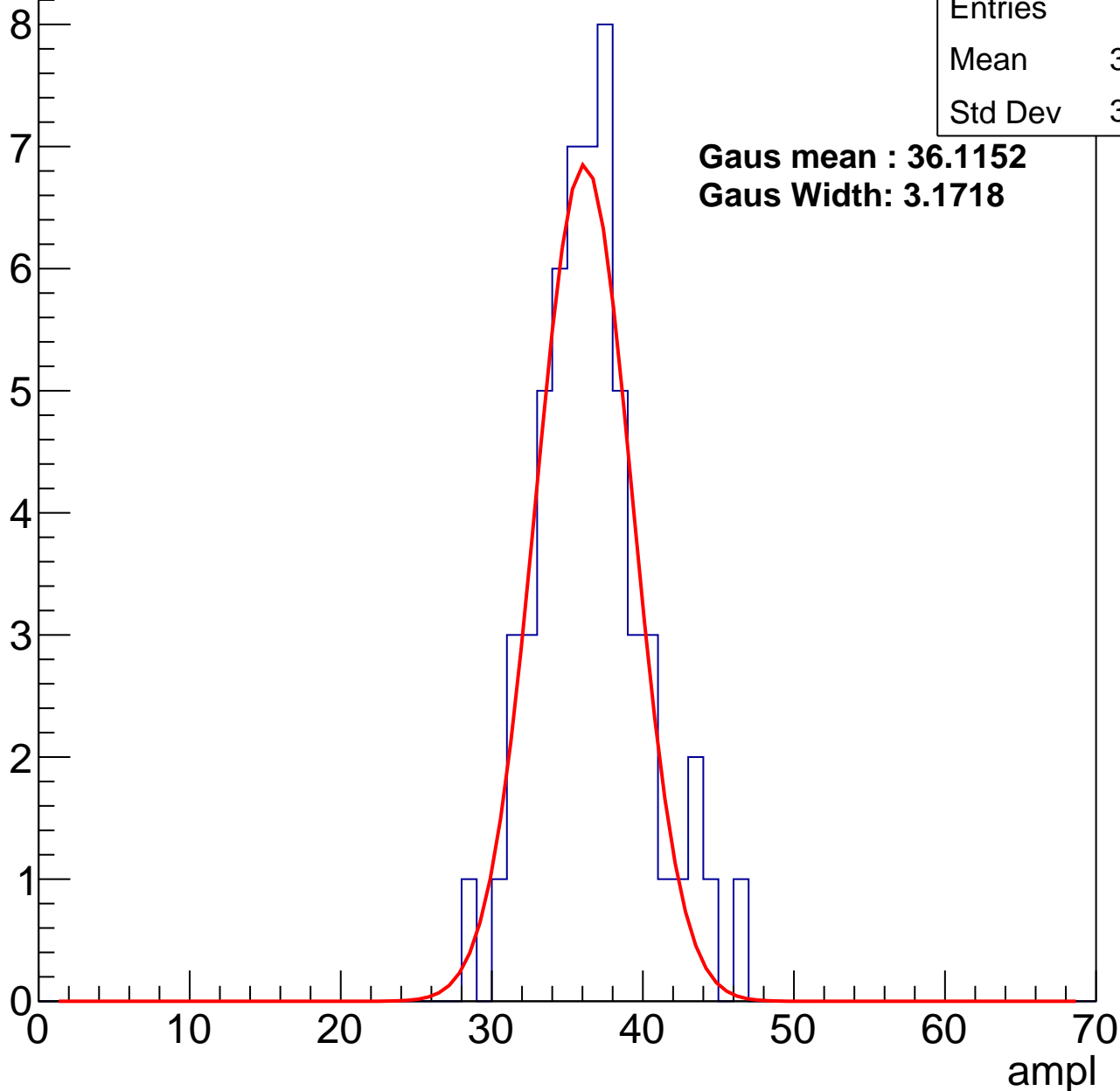
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	36.12
Std Dev	3.533

Gaus mean : 36.1152

Gaus Width: 3.1718



B1L100S, U15-ch88, adc2

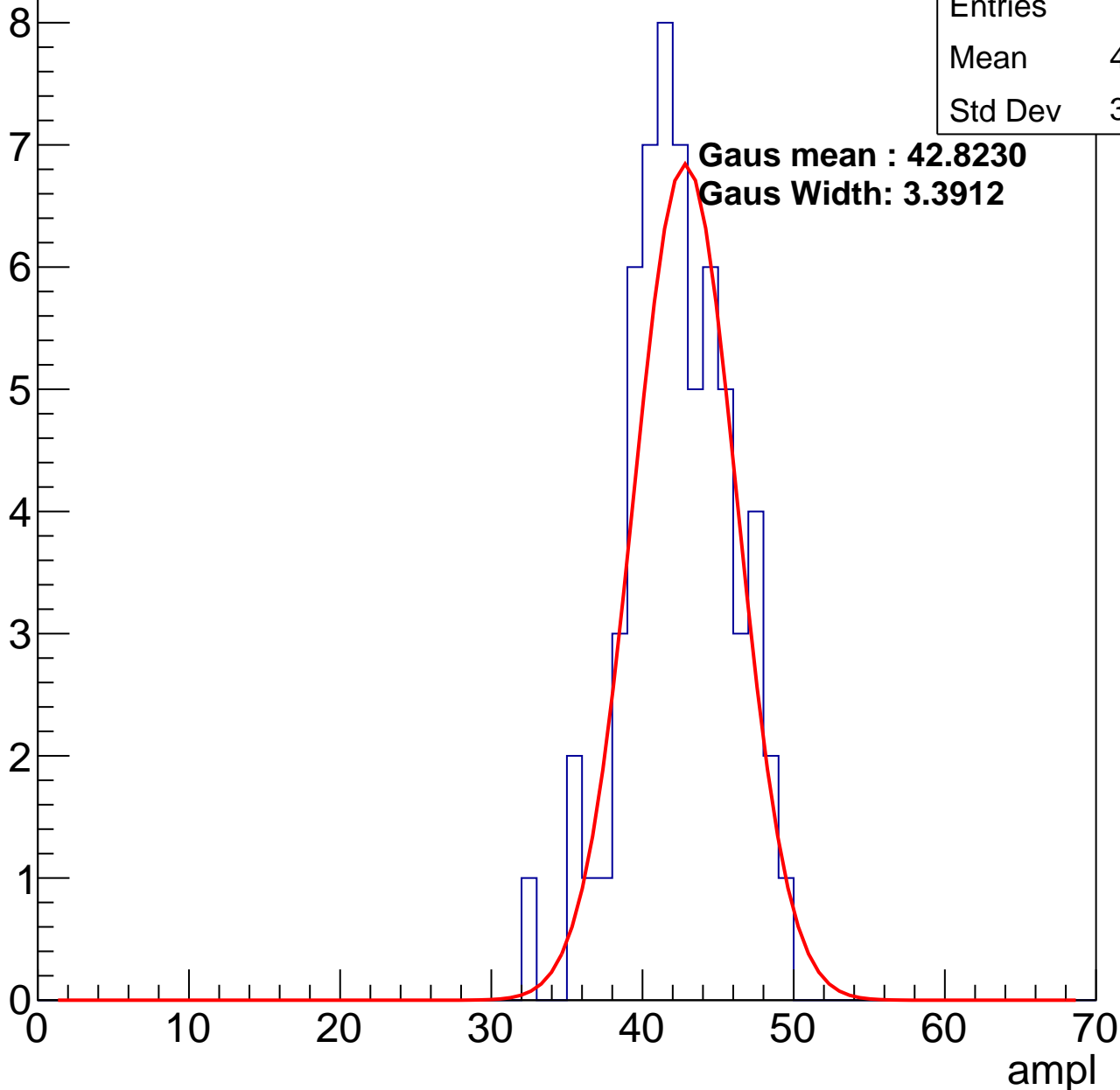
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	41.94
Std Dev	3.454

Gaus mean : 42.8230

Gaus Width: 3.3912

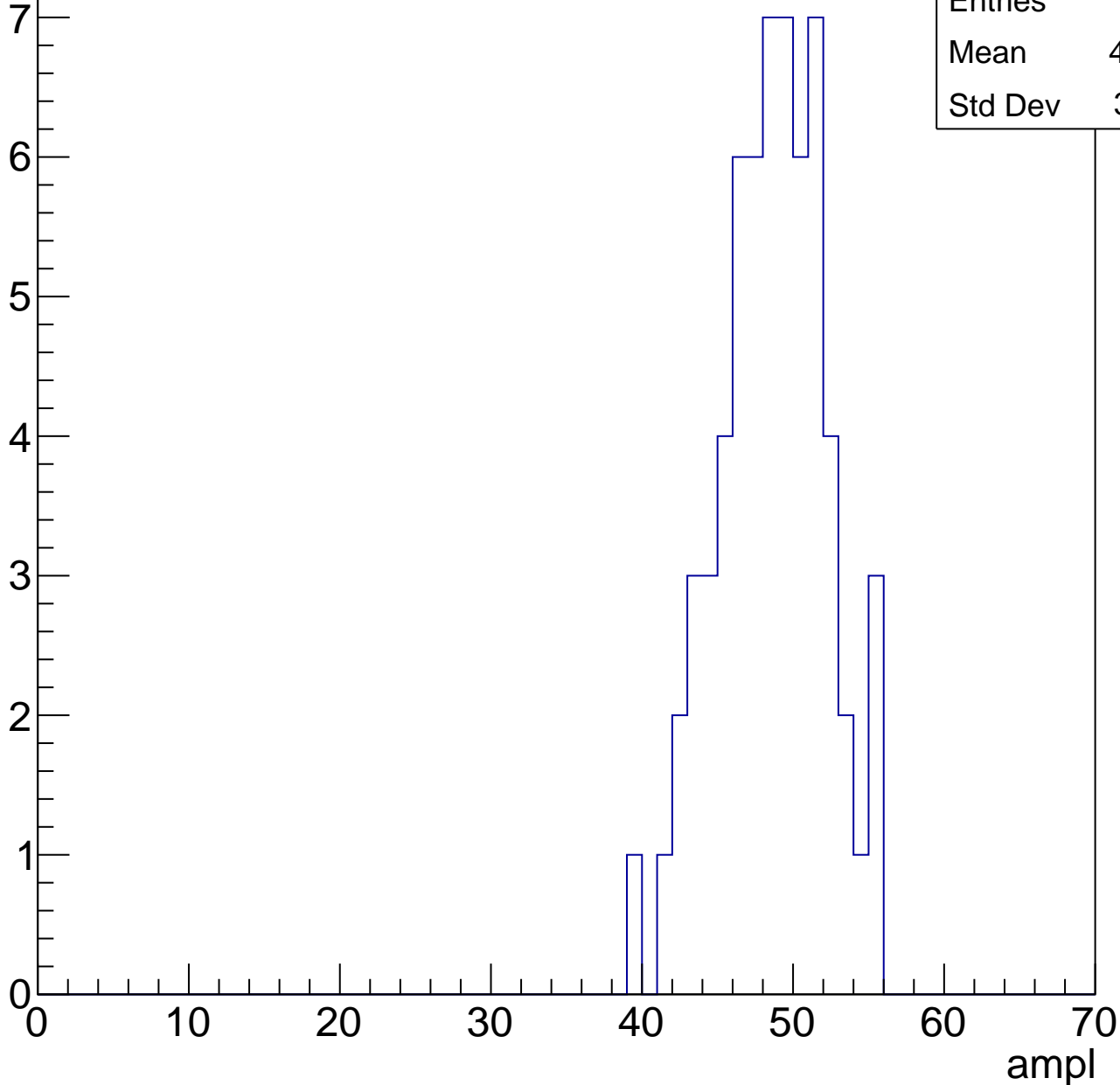


B1L100S, U15-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	48.13
Std Dev	3.521

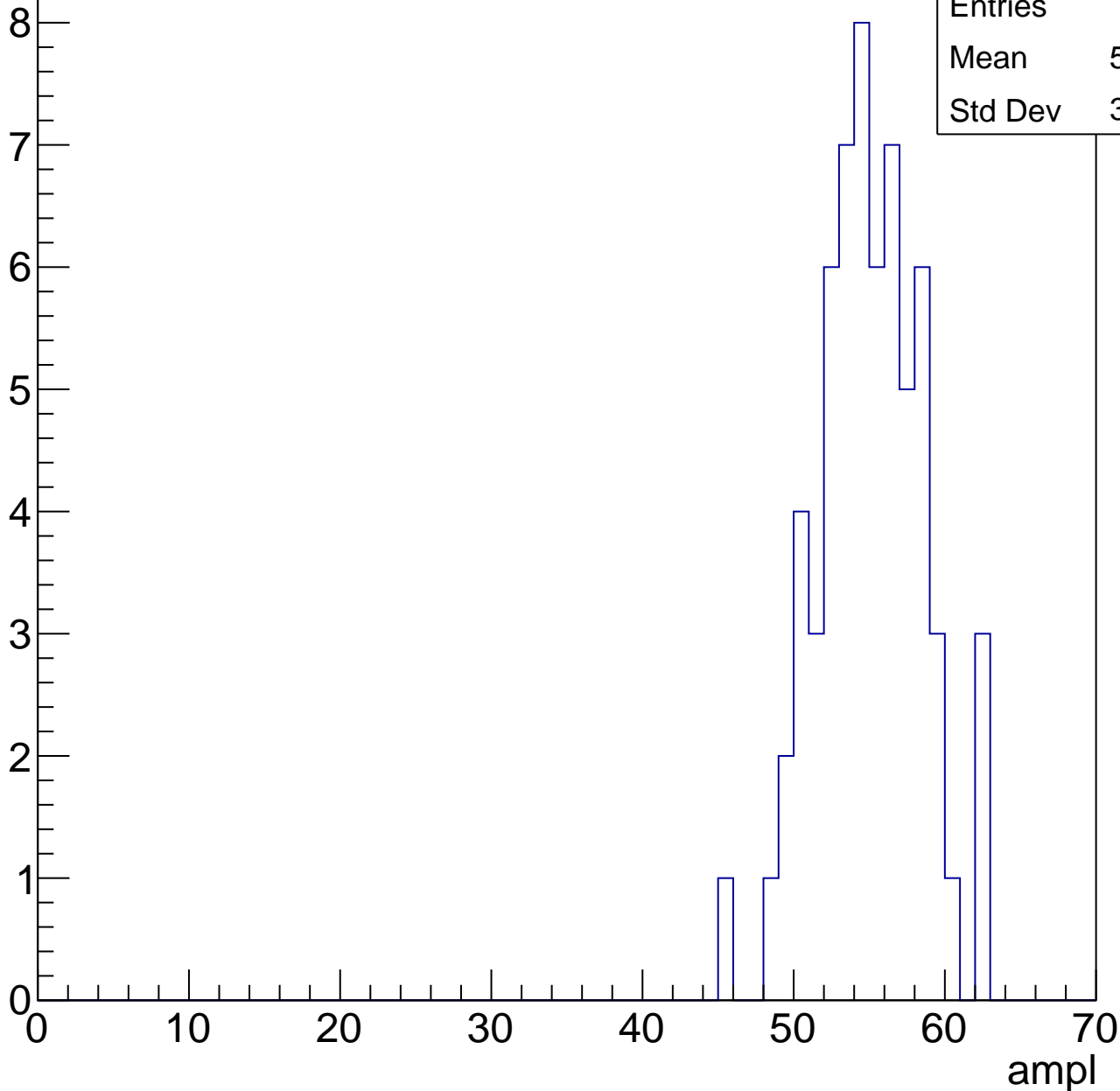


B1L100S, U15-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	54.56
Std Dev	3.445



B1L100S, U15-ch88, adc5

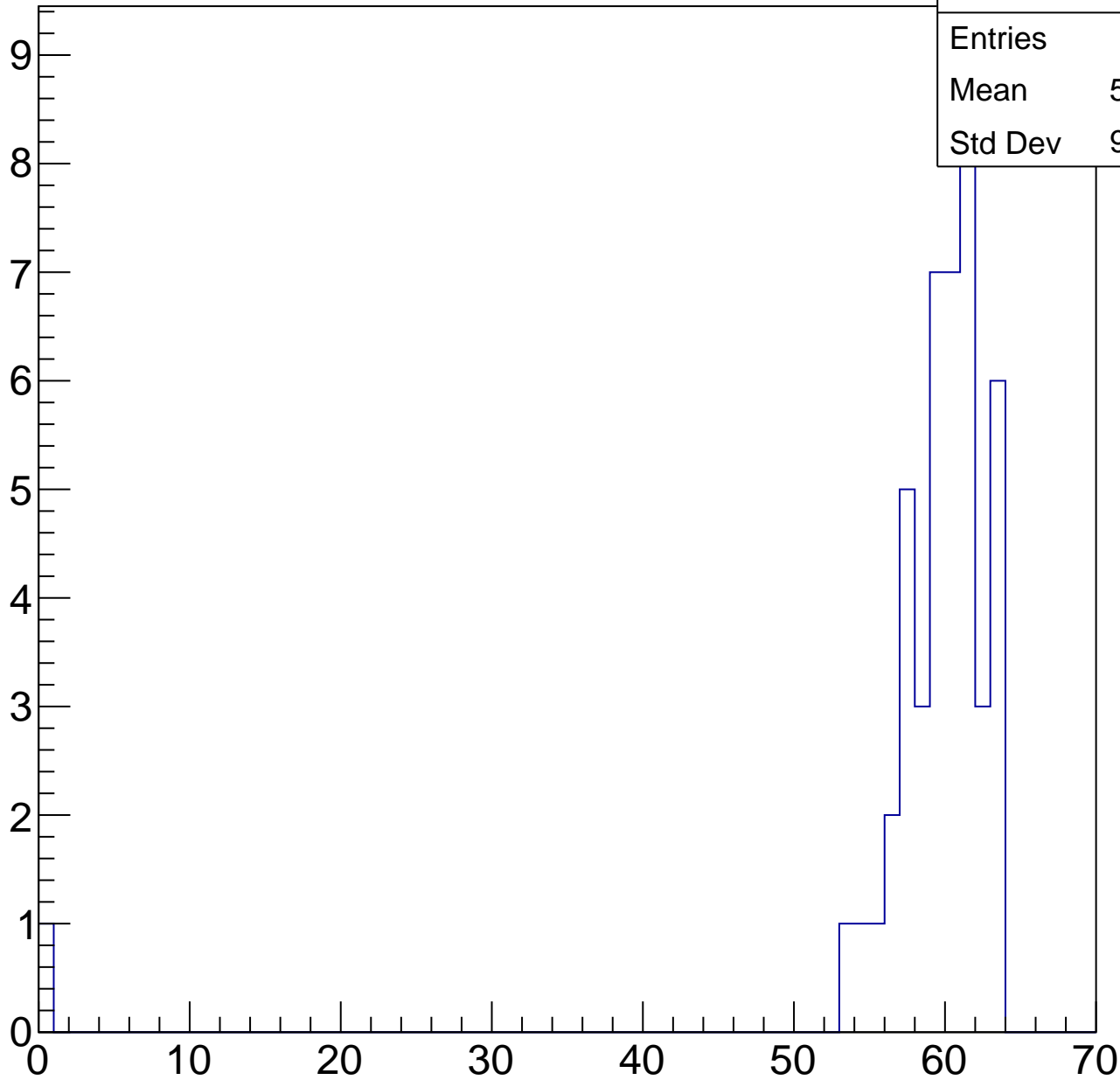
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.24
Std Dev	9.015

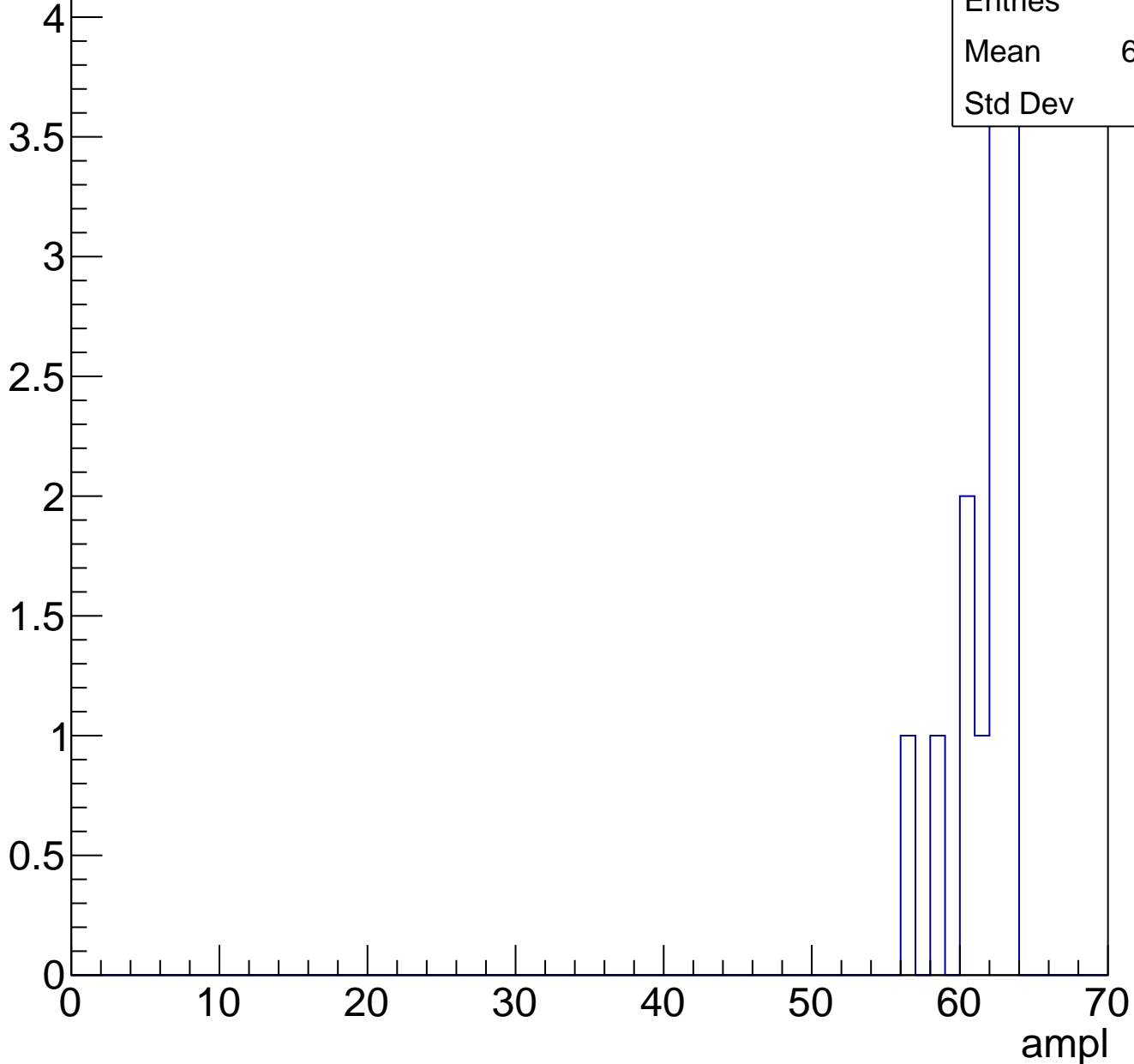
ampl



B1L100S, U15-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch89, adc0

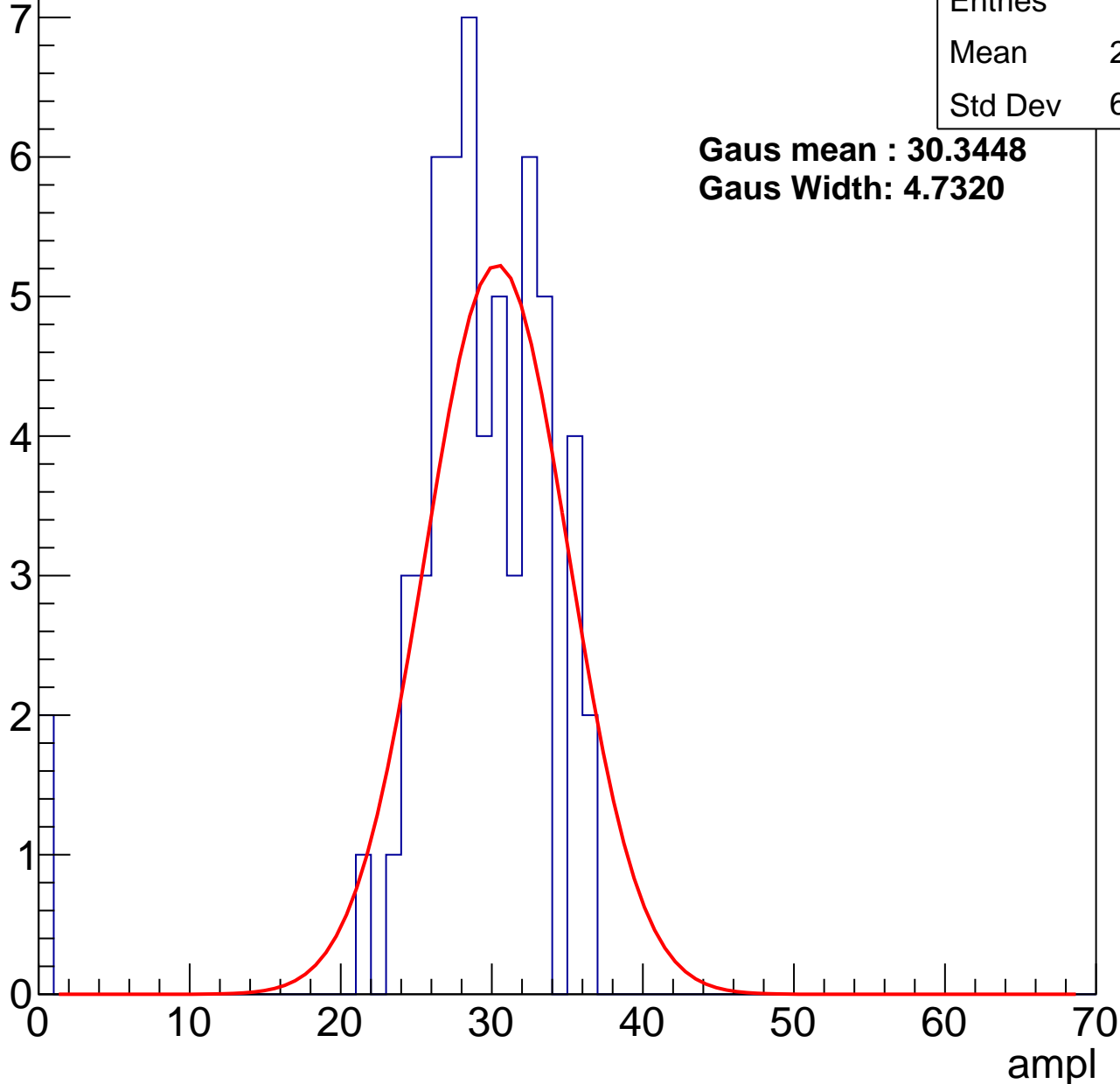
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	28.16
Std Dev	6.354

Gaus mean : 30.3448

Gaus Width: 4.7320



B1L100S, U15-ch89, adc1

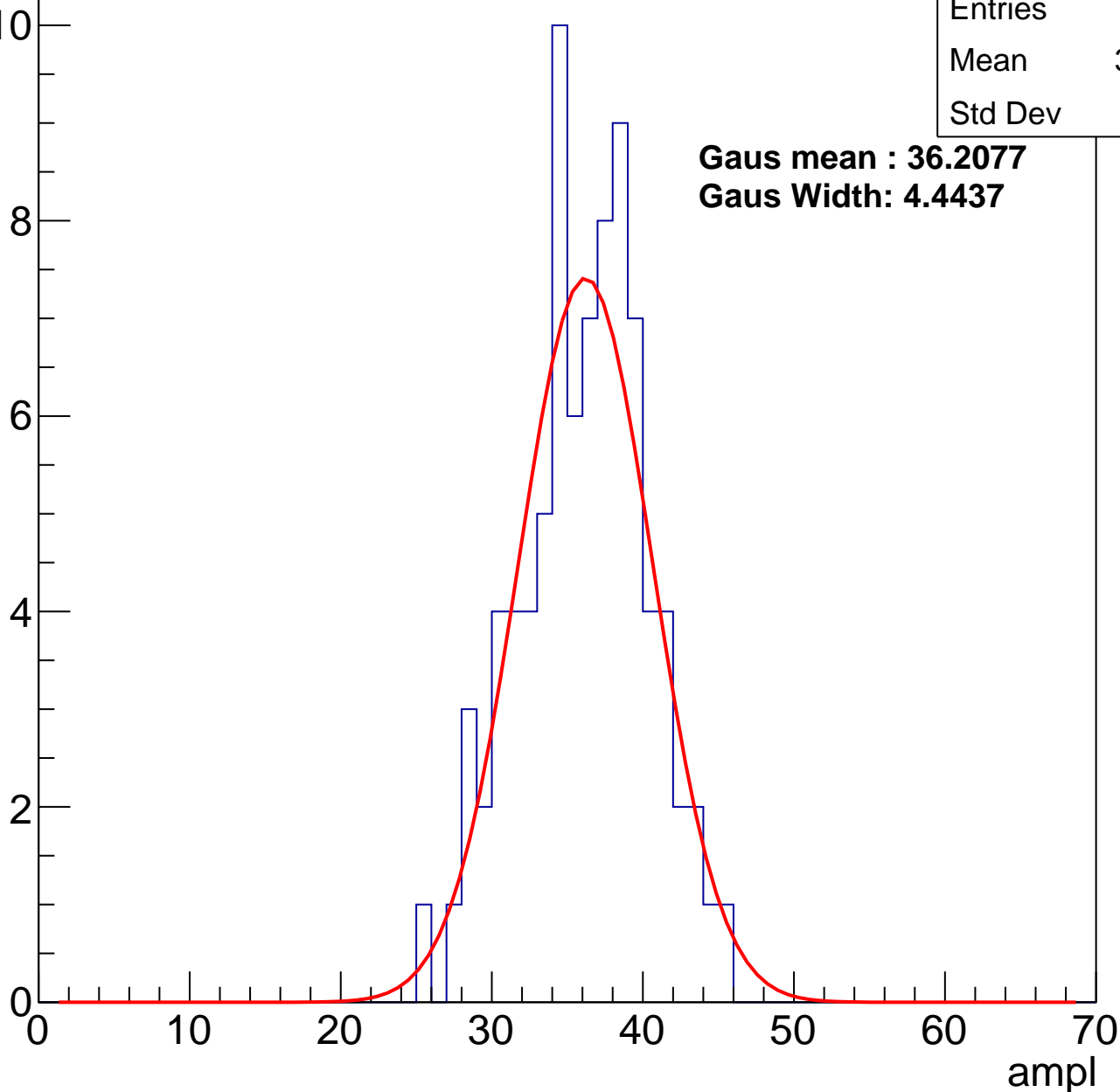
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	35.61
Std Dev	4.15

Gaus mean : 36.2077

Gaus Width: 4.4437



B1L100S, U15-ch89, adc2

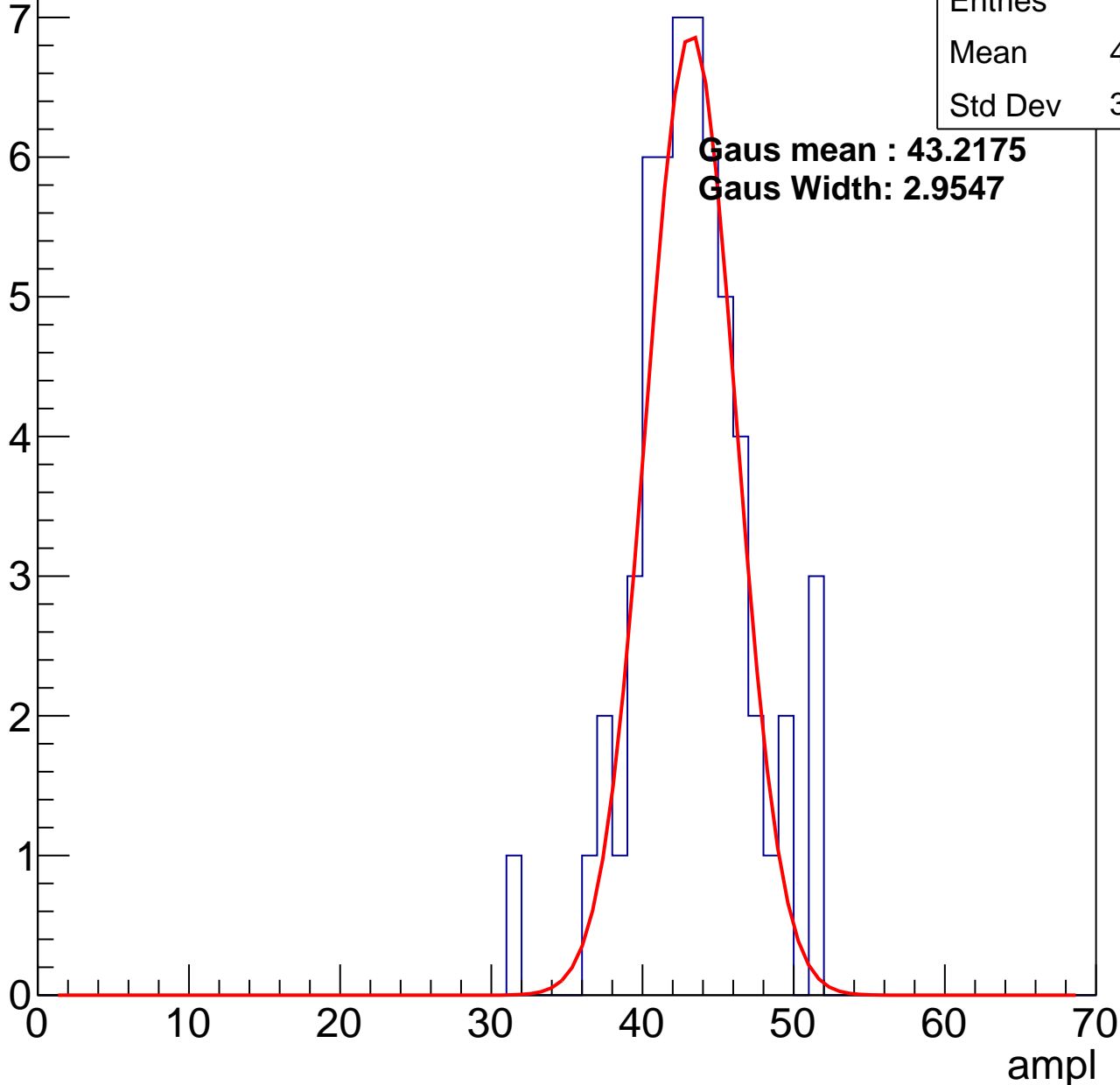
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.86
Std Dev	3.758

Gaus mean : 43.2175

Gaus Width: 2.9547

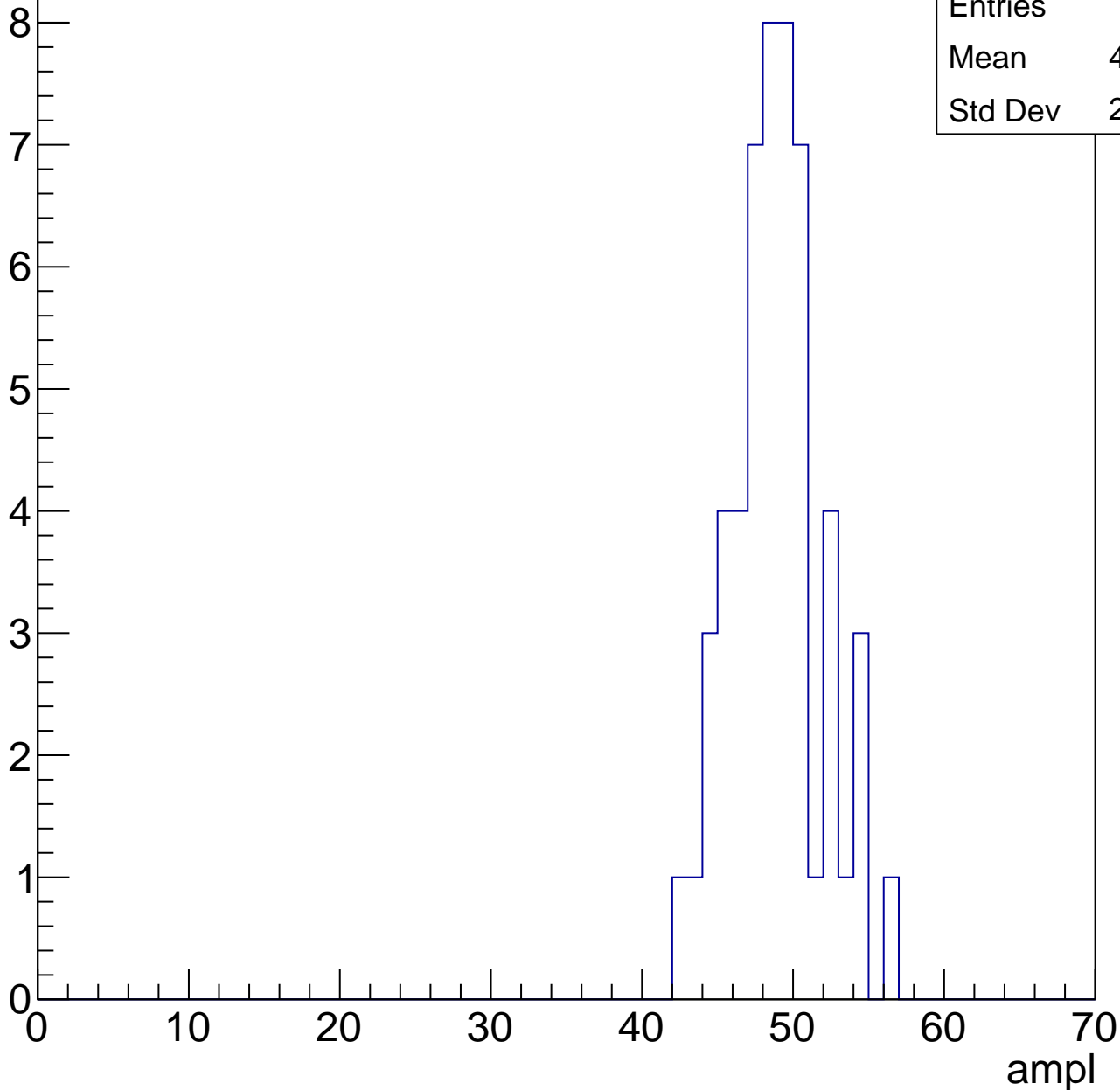


B1L100S, U15-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

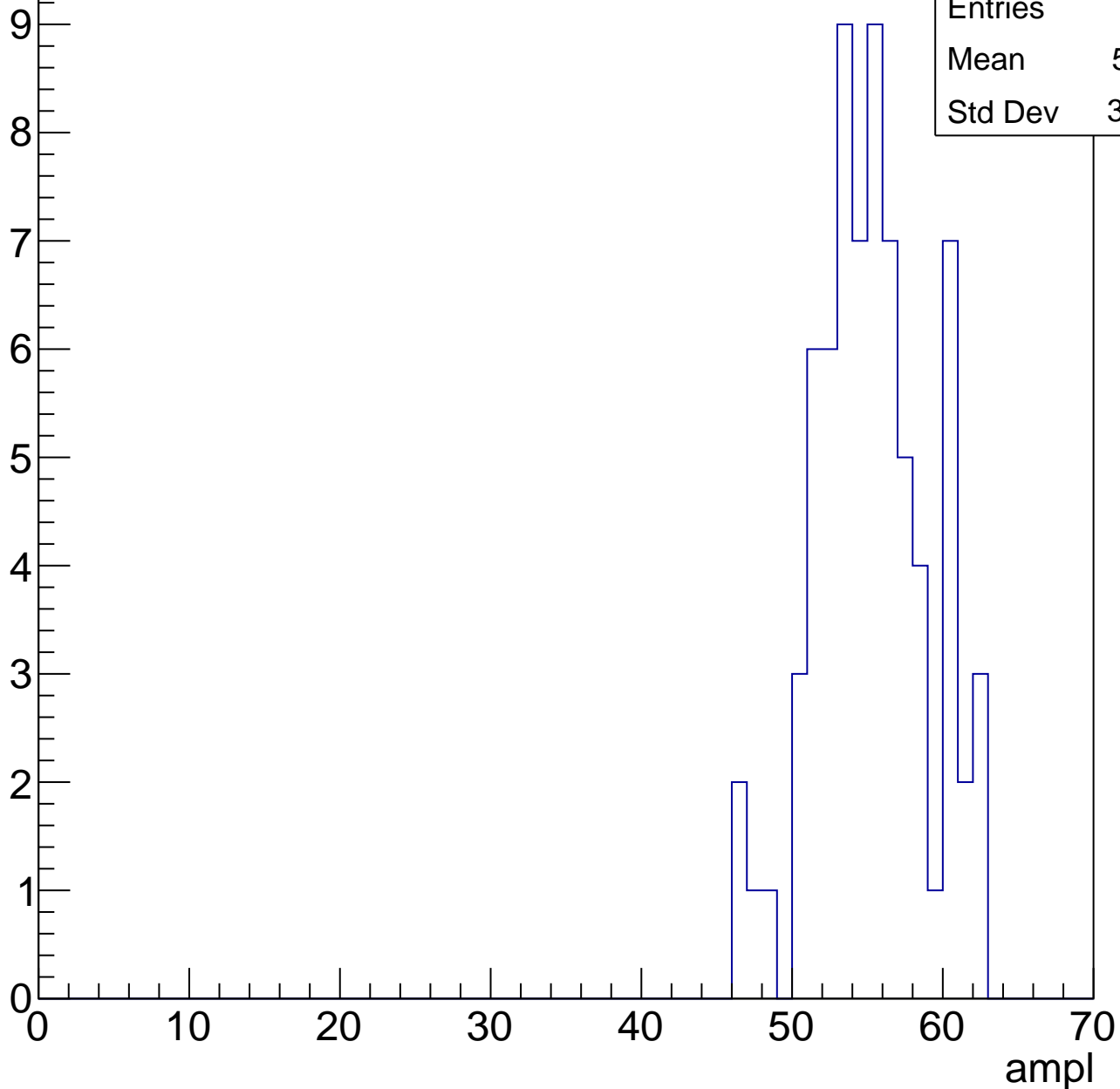
Entries	53
Mean	48.42
Std Dev	2.962



B1L100S, U15-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

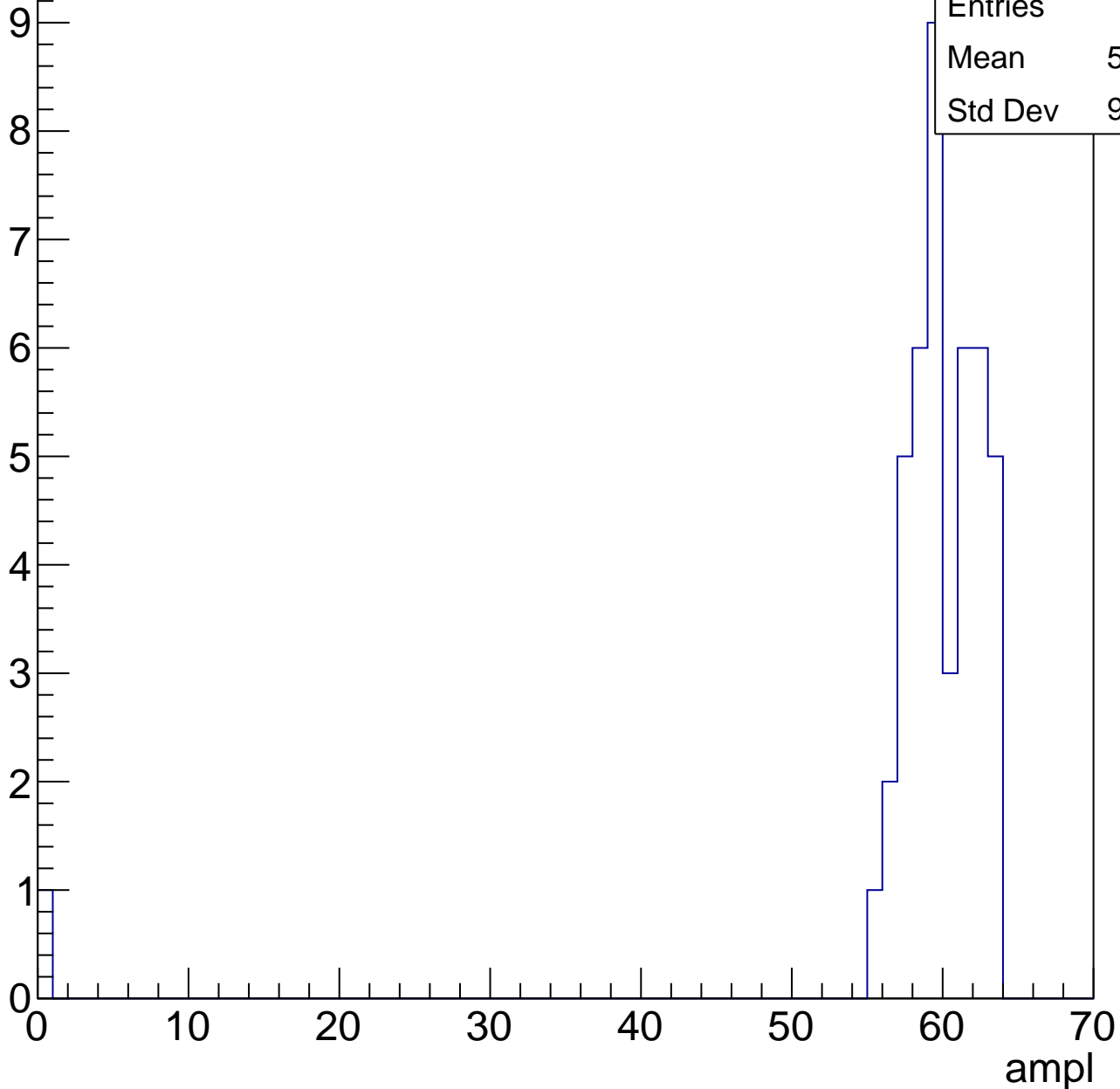
Entry



B1L100S, U15-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

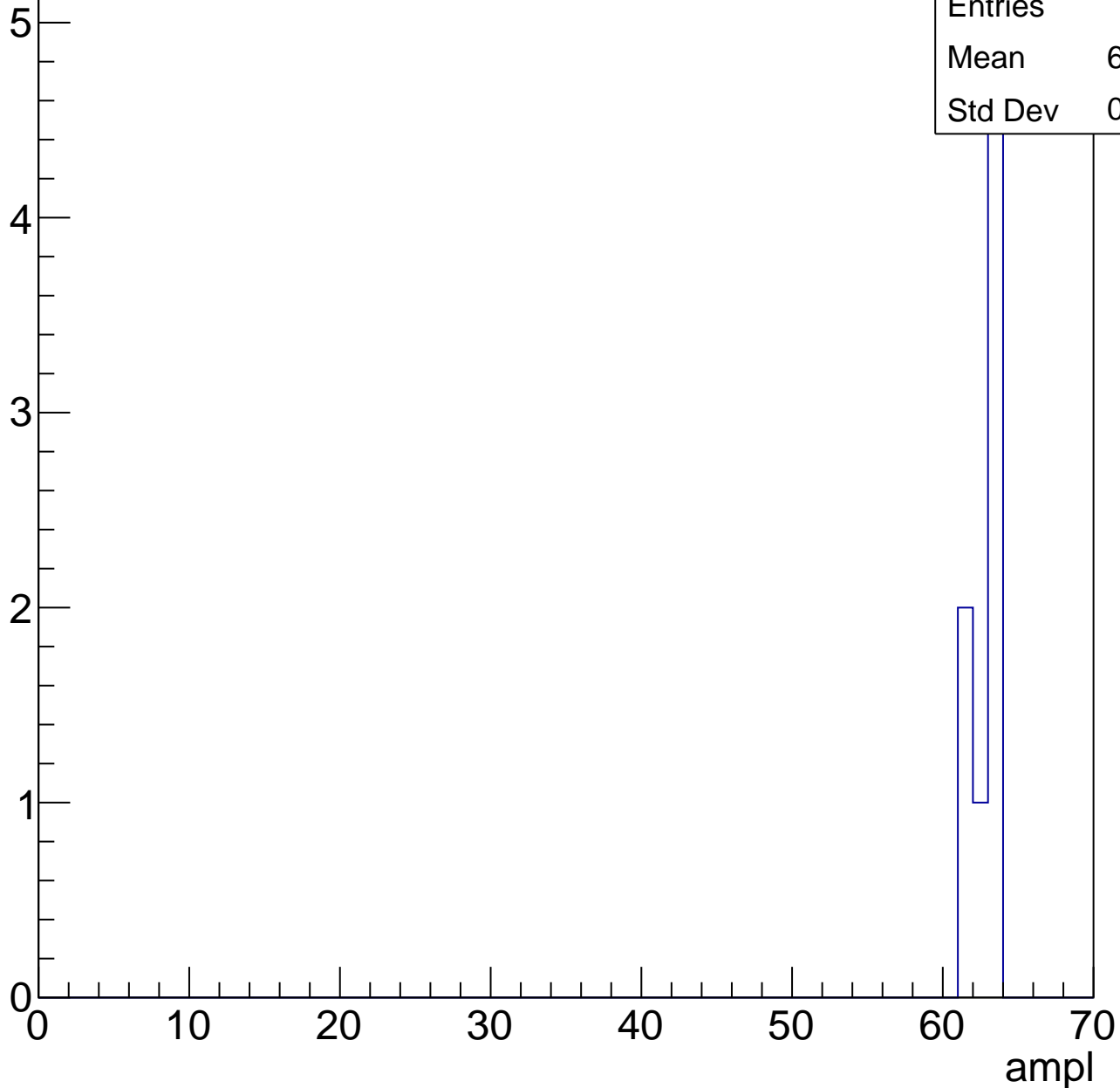
Entry



B1L100S, U15-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch90, adc0

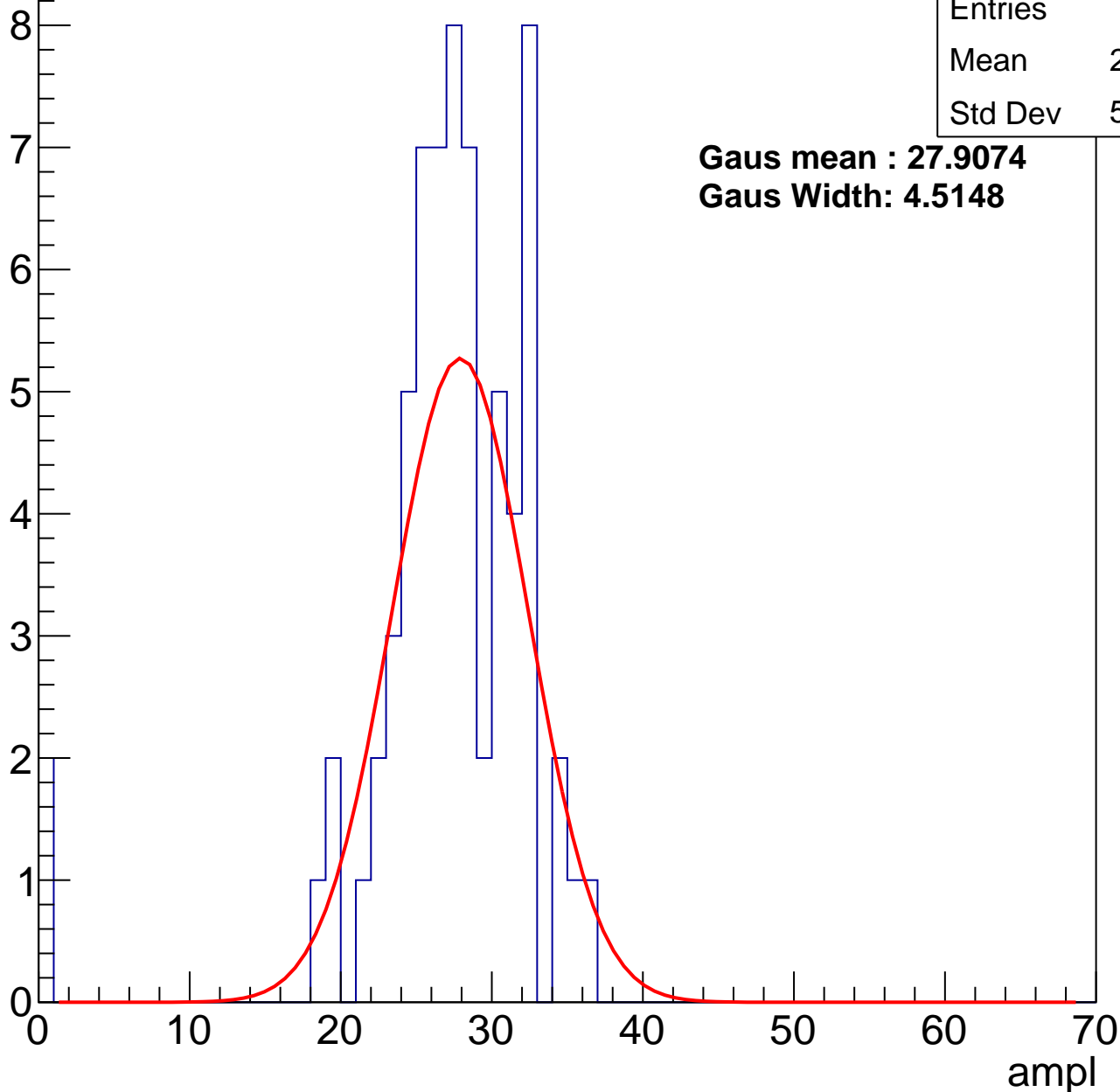
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	26.56
Std Dev	5.984

Gaus mean : 27.9074

Gaus Width: 4.5148



B1L100S, U15-ch90, adc1

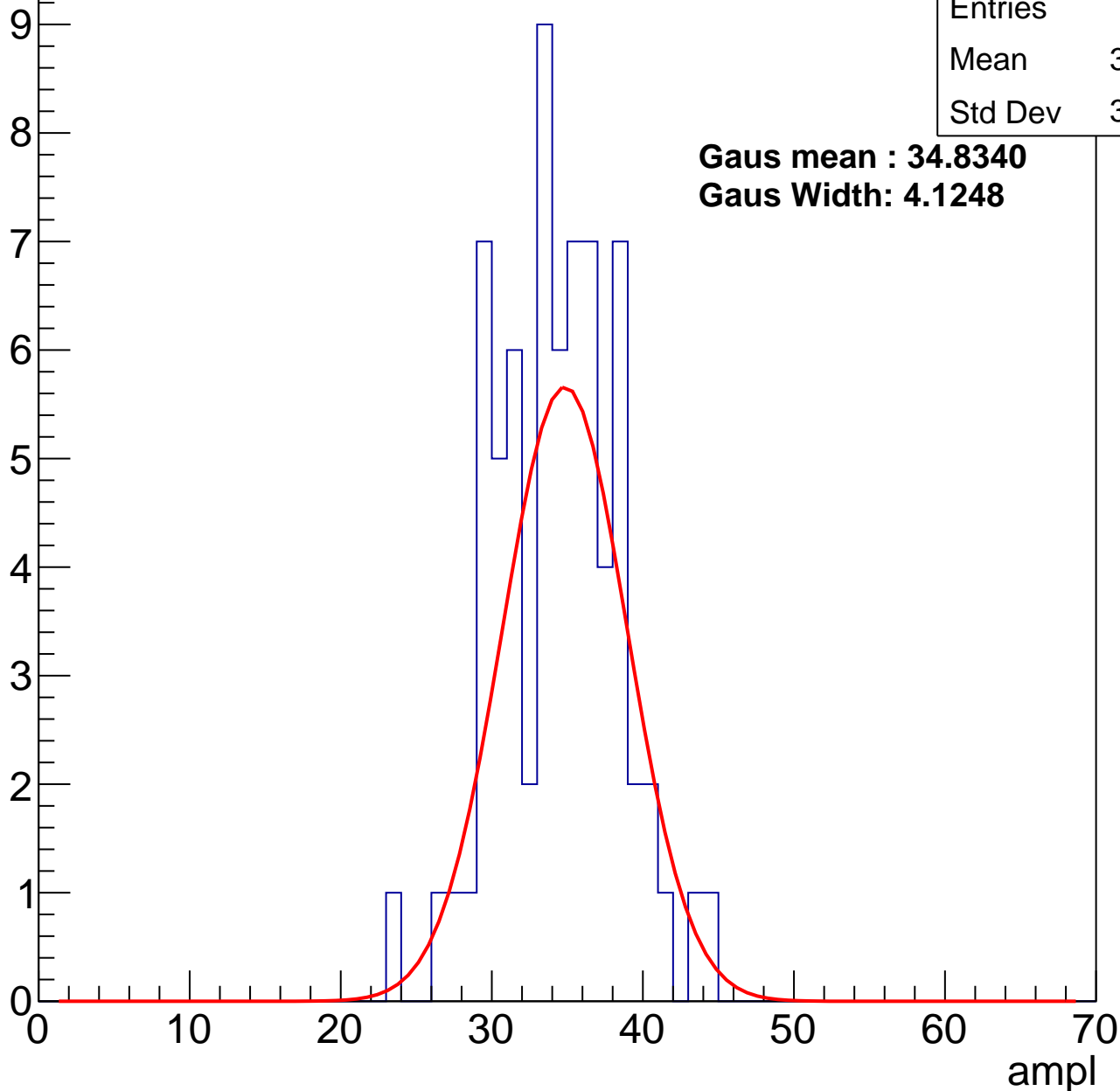
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	33.87
Std Dev	3.989

Gaus mean : 34.8340

Gaus Width: 4.1248



B1L100S, U15-ch90, adc2

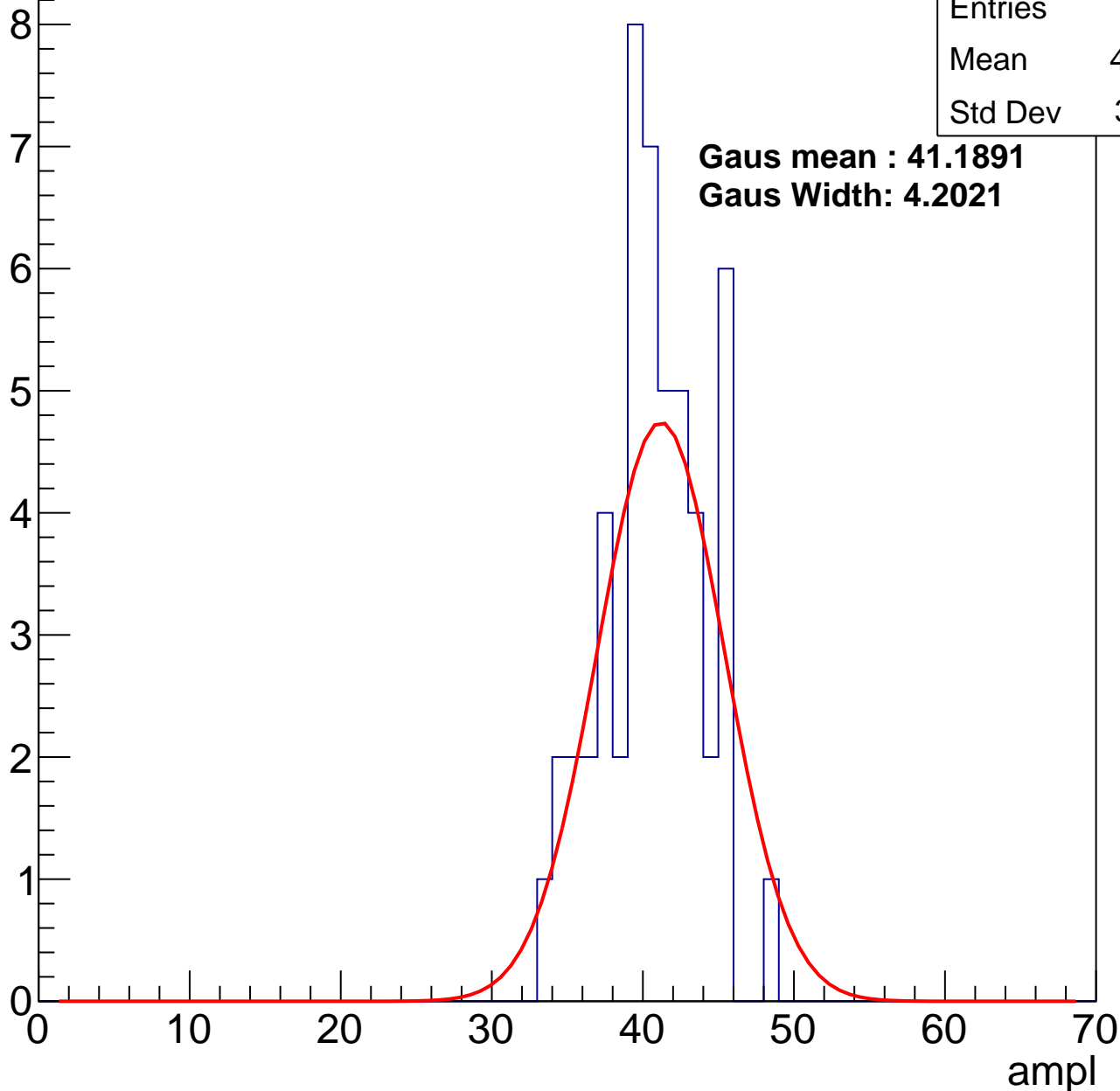
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	40.24
Std Dev	3.311

Gaus mean : 41.1891

Gaus Width: 4.2021

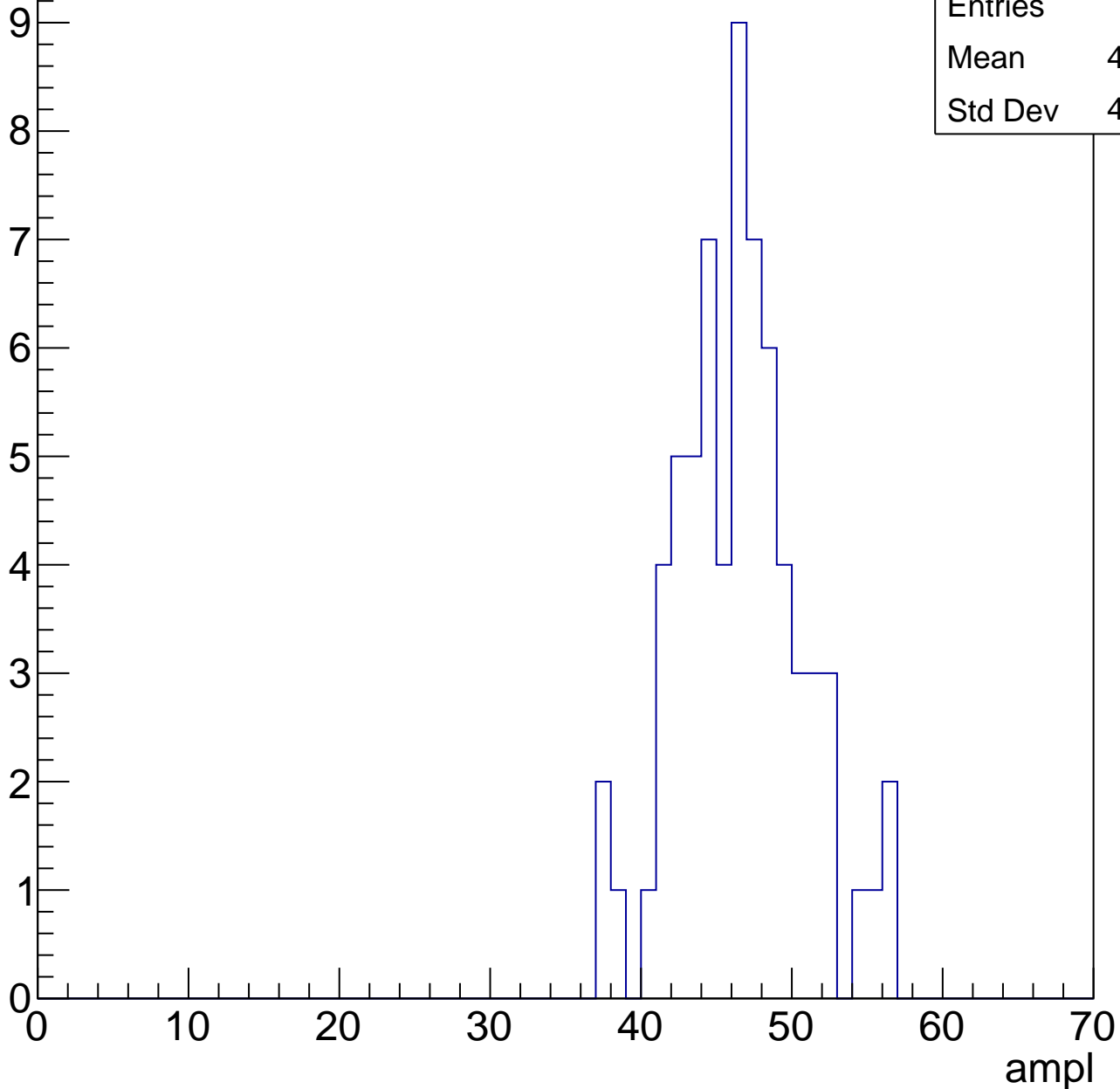


B1L100S, U15-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	46.12
Std Dev	4.132

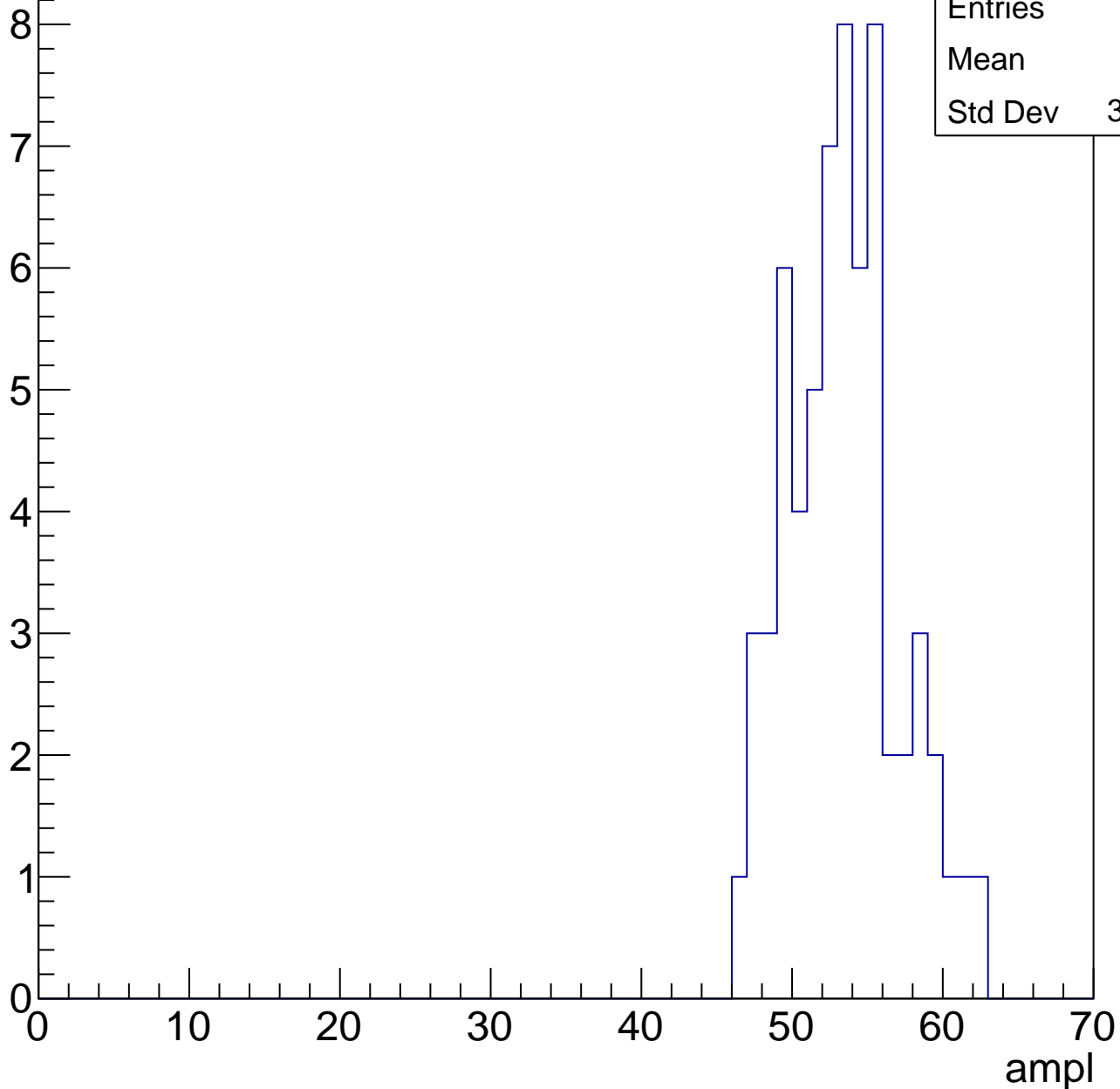


B1L100S, U15-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	52.9
Std Dev	3.606

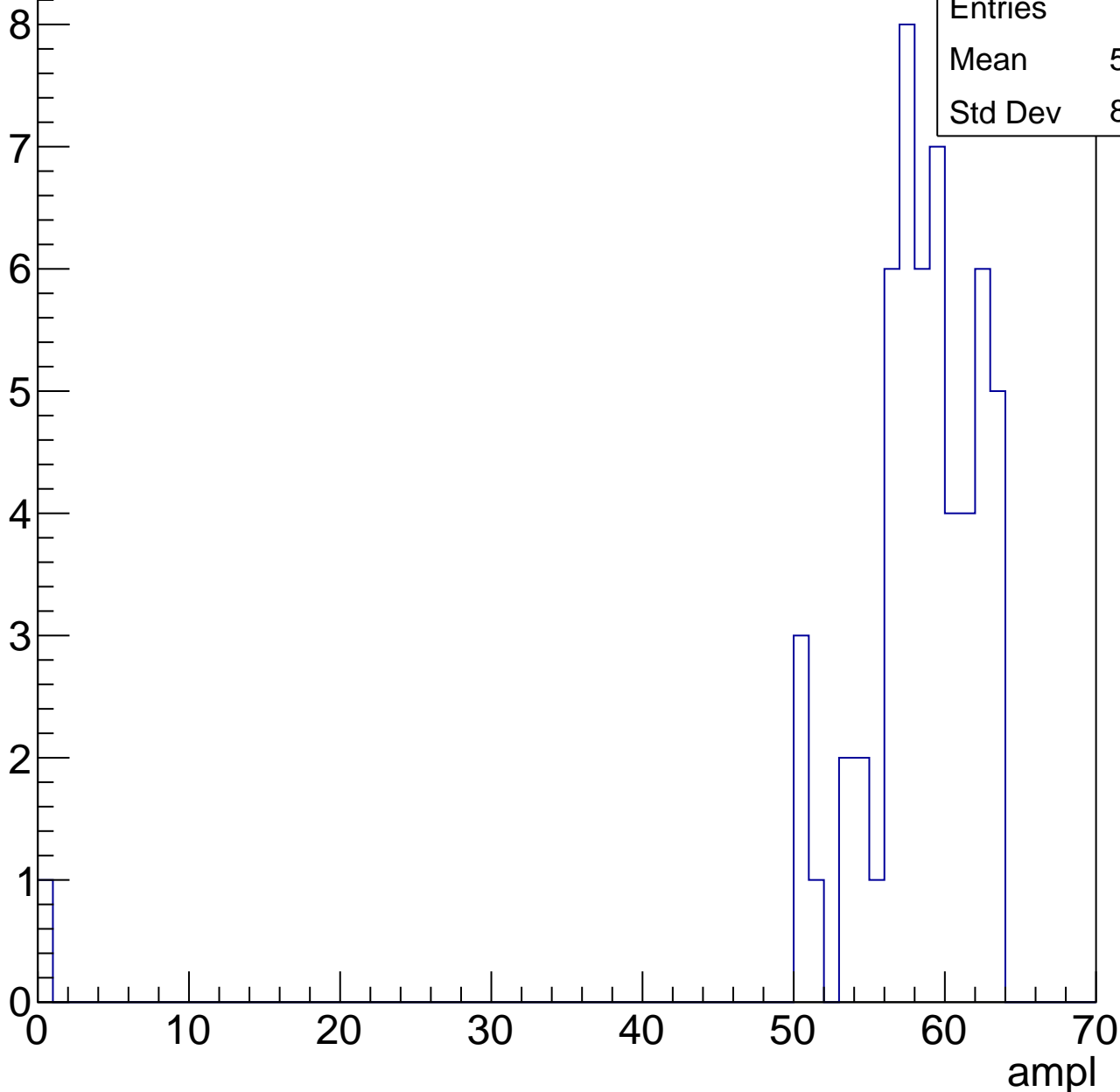


B1L100S, U15-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	57.04
Std Dev	8.405

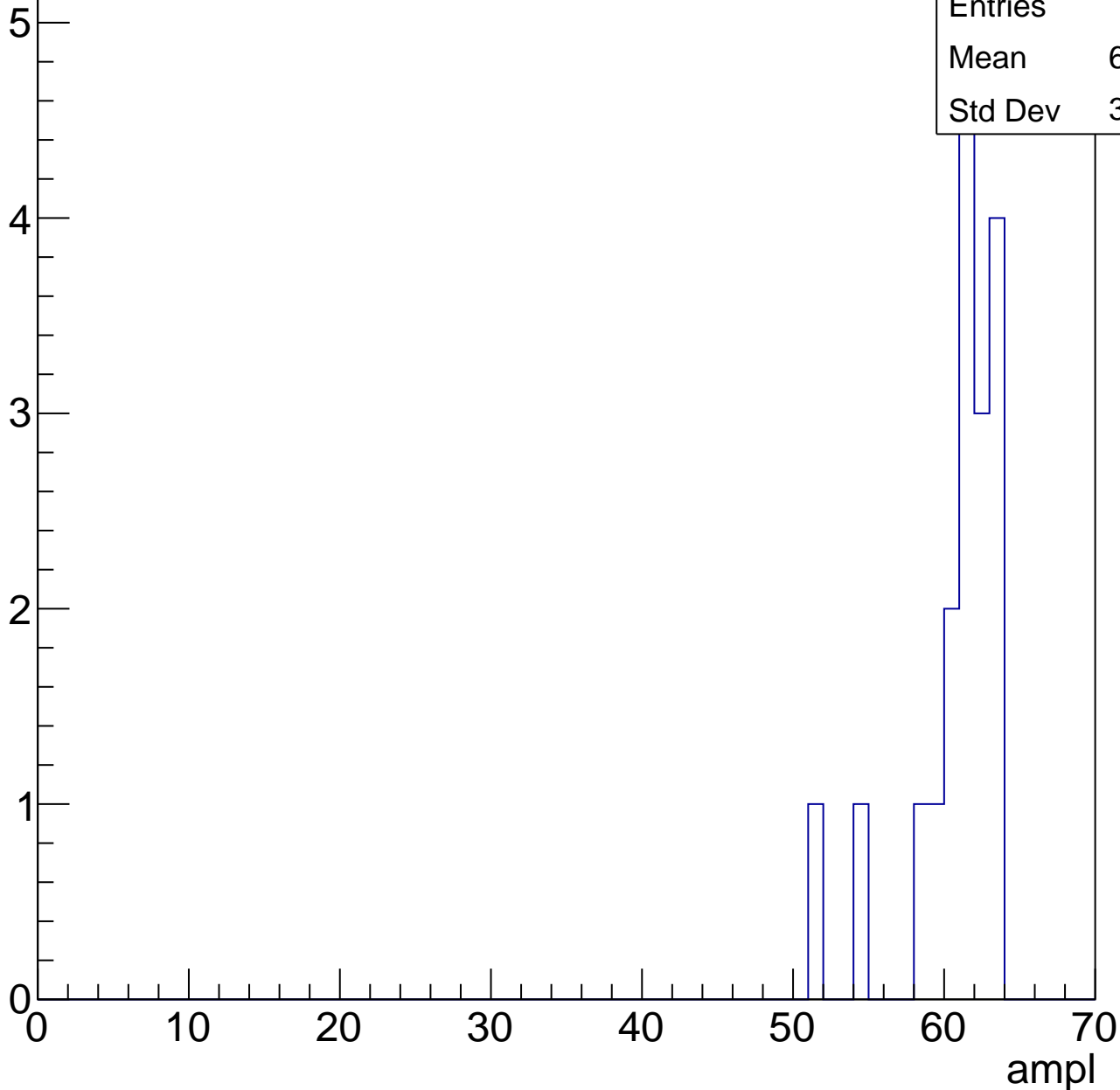


B1L100S, U15-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

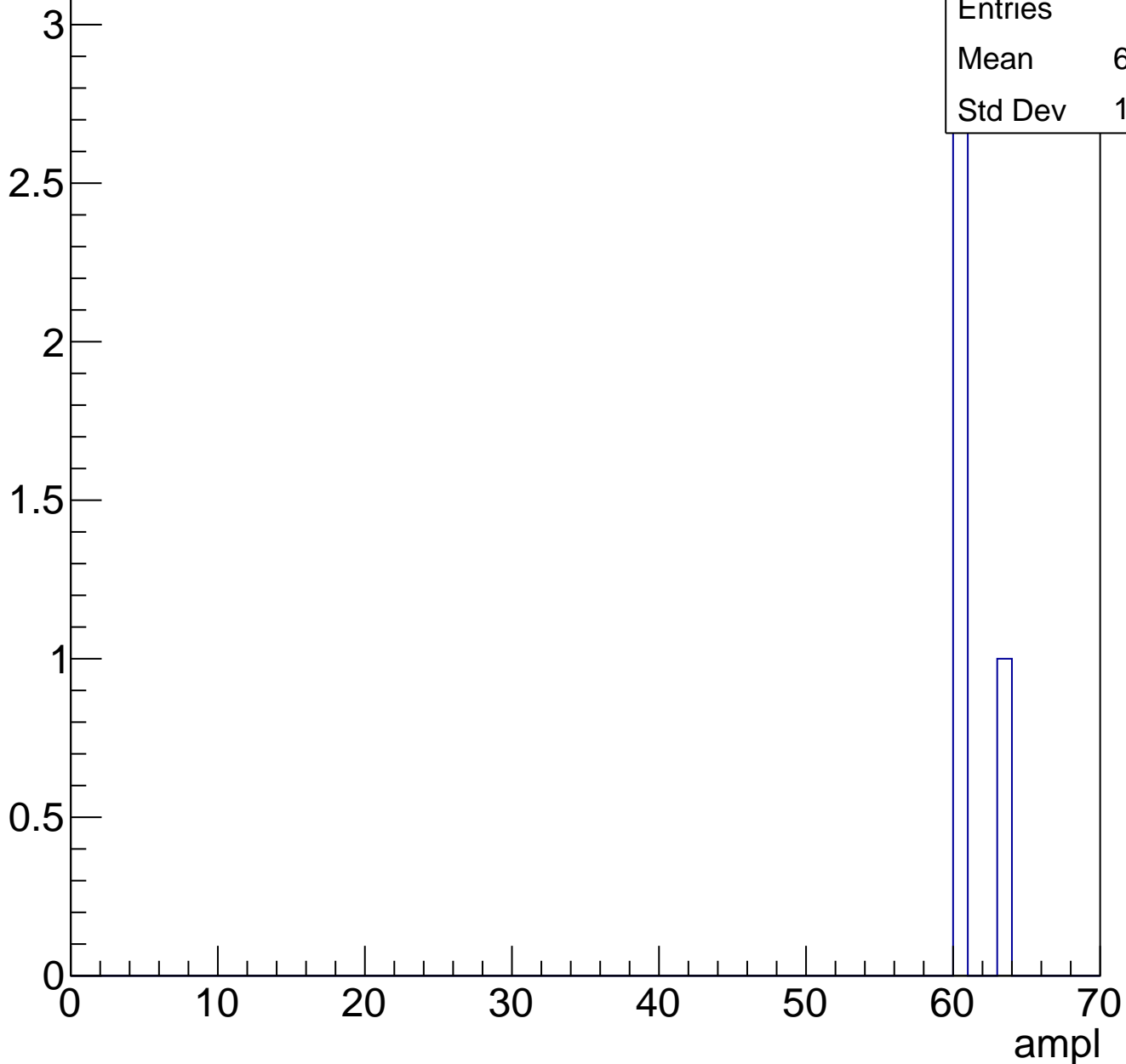
Entries	18
Mean	60.28
Std Dev	3.106



B1L100S, U15-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch91, adc0

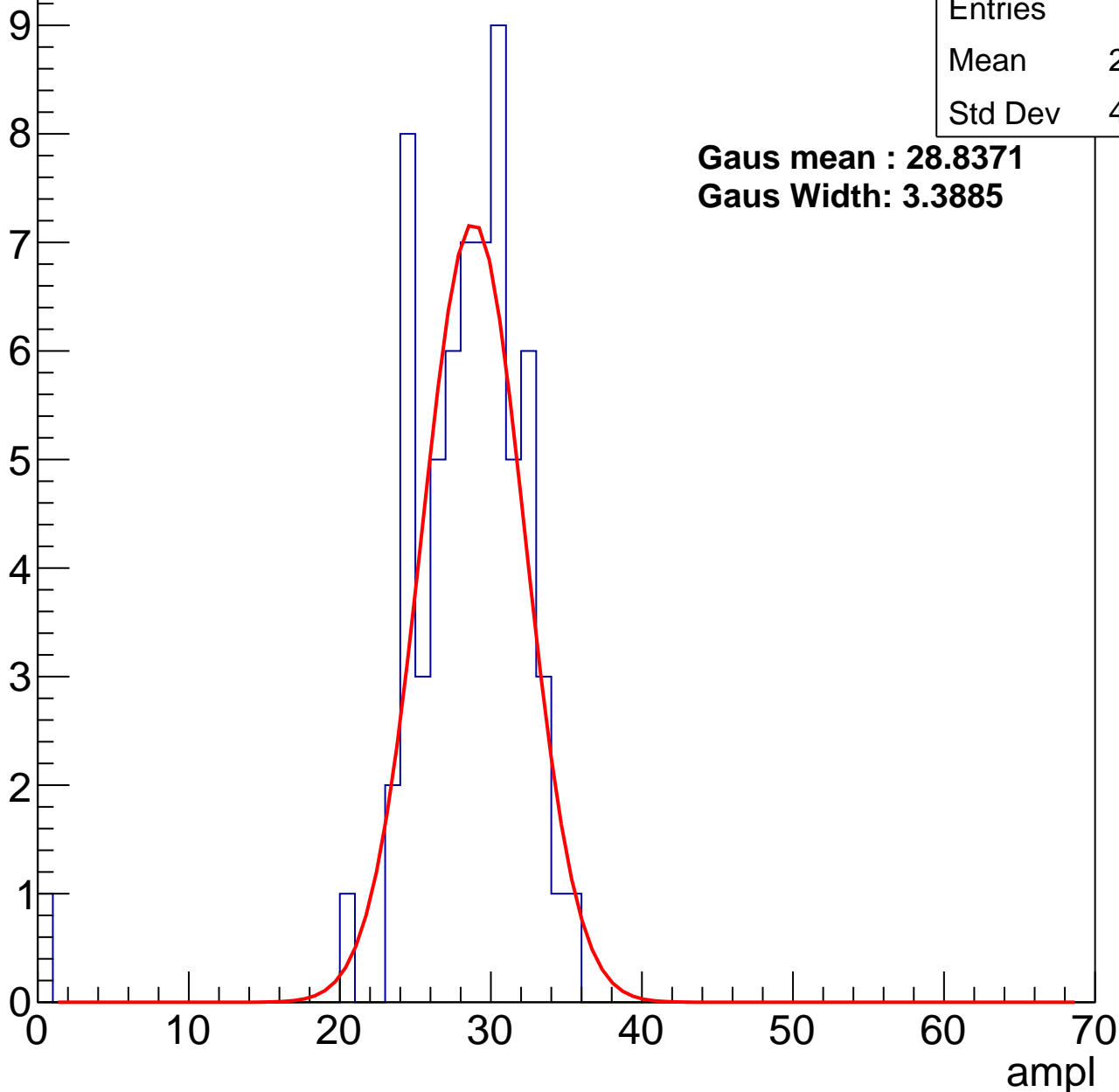
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.83
Std Dev	4.676

Gaus mean : 28.8371

Gaus Width: 3.3885



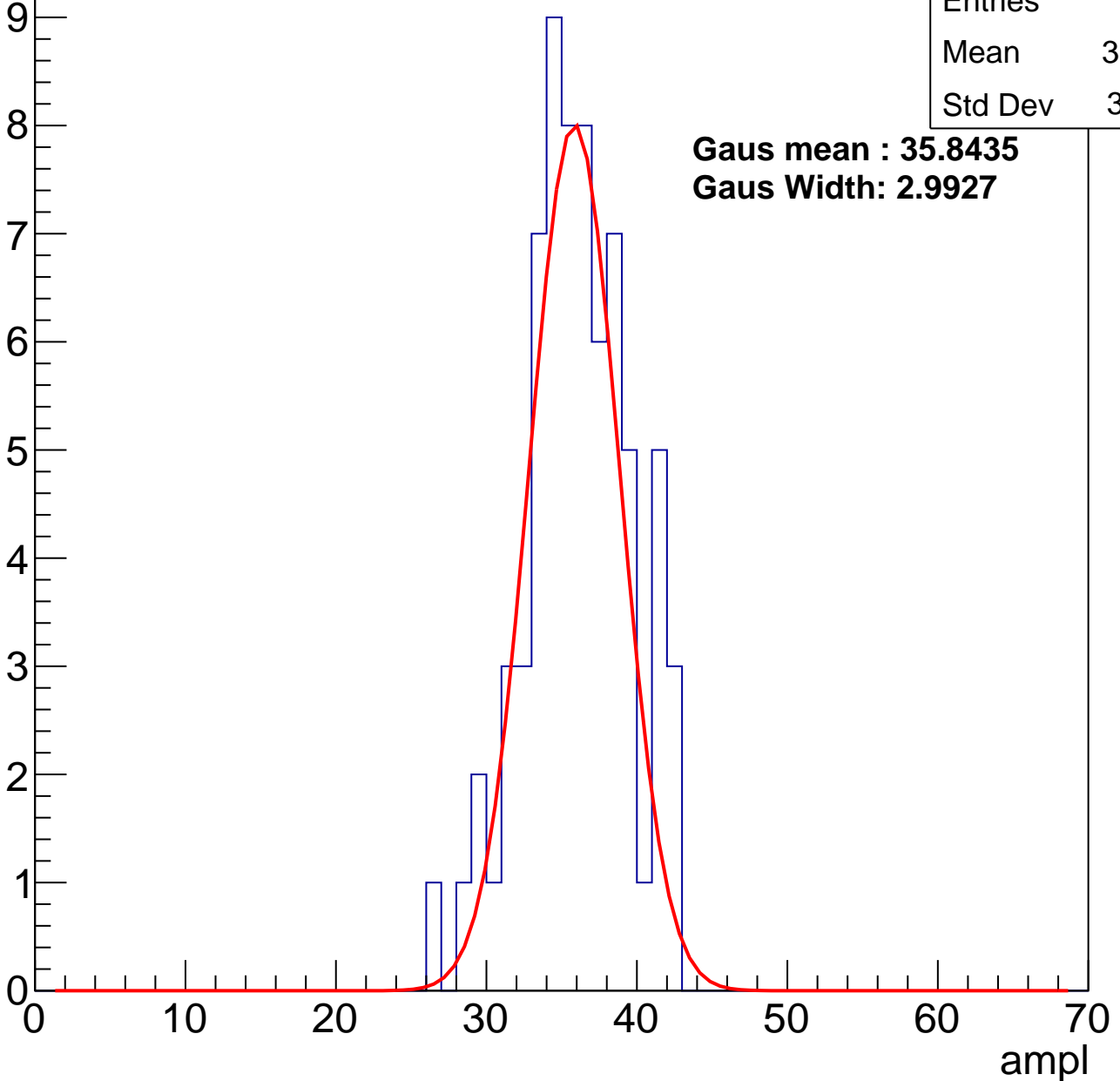
B1L100S, U15-ch91, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.57
Std Dev	3.471

Gaus mean : 35.8435
Gaus Width: 2.9927



B1L100S, U15-ch91, adc2

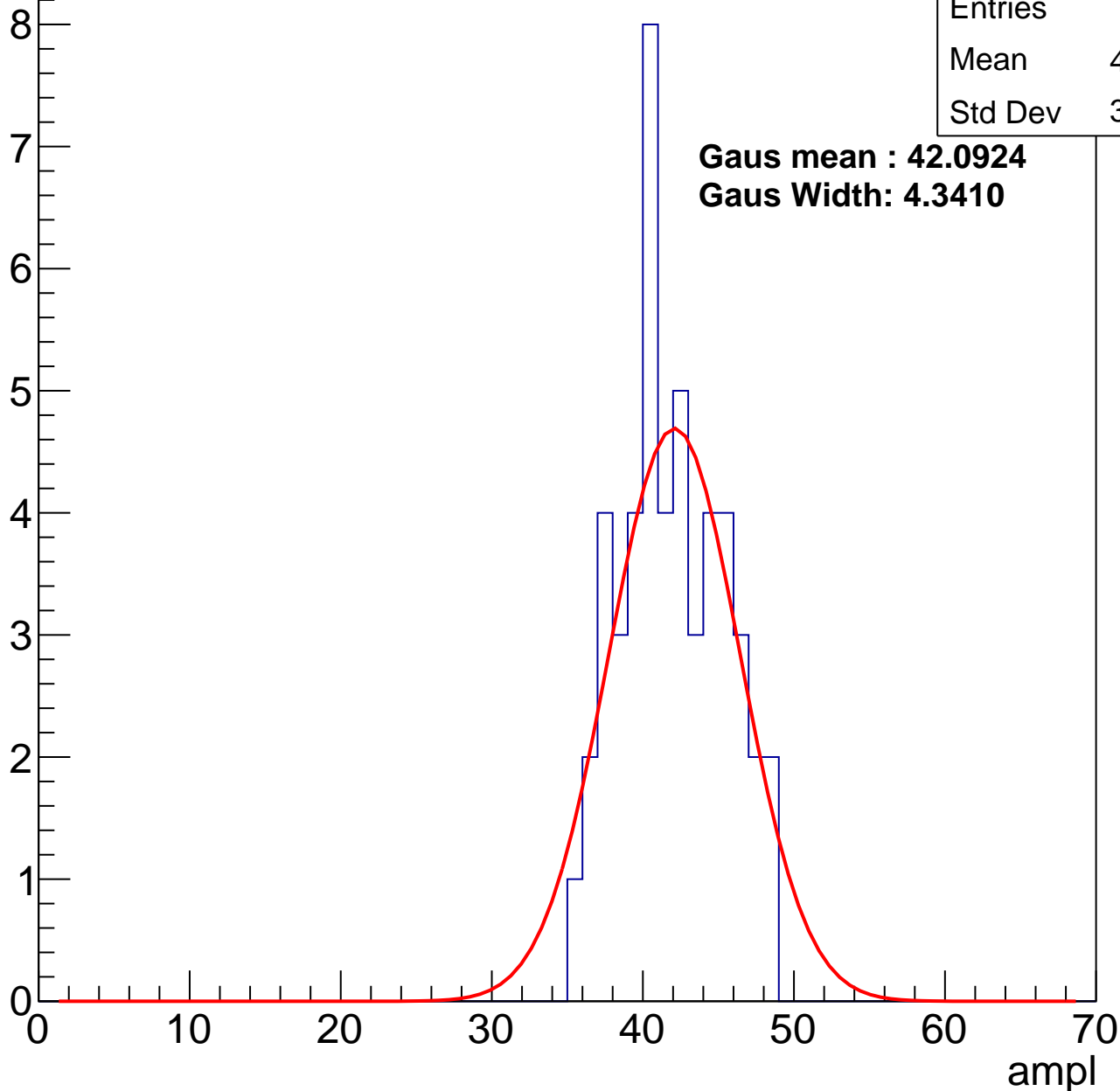
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	41.47
Std Dev	3.357

Gaus mean : 42.0924

Gaus Width: 4.3410

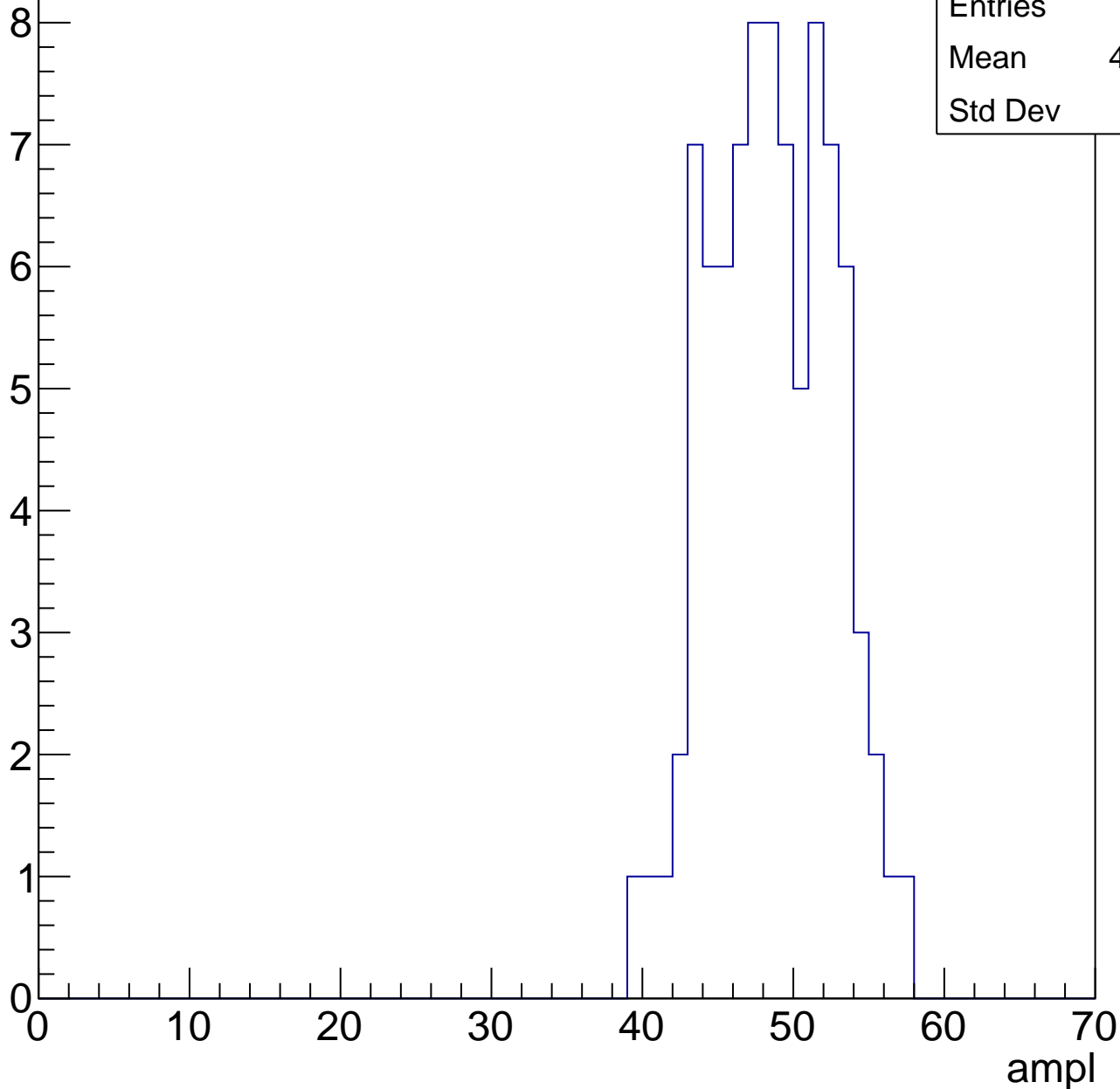


B1L100S, U15-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

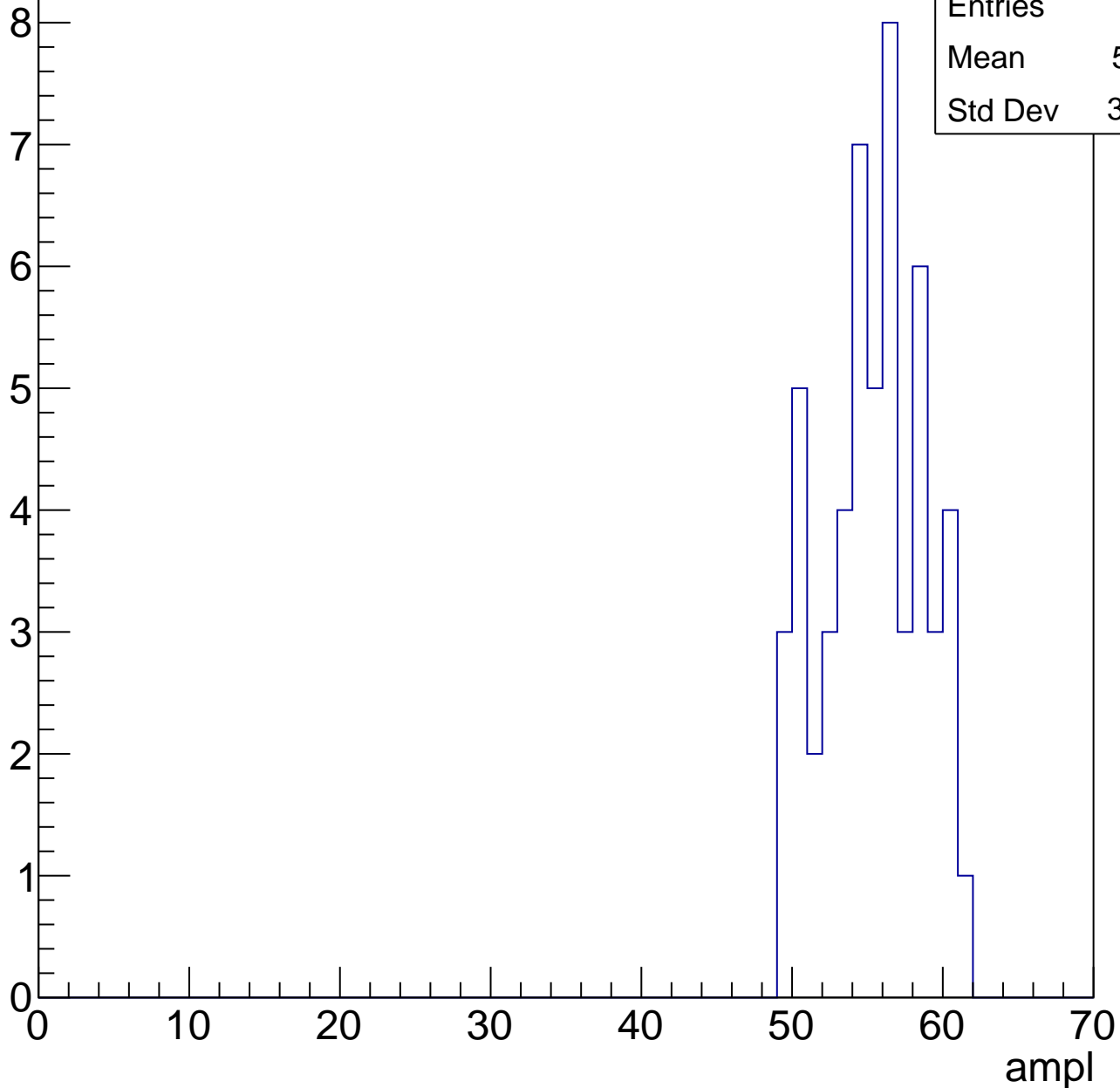
Entries	87
Mean	48.15
Std Dev	3.92



B1L100S, U15-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

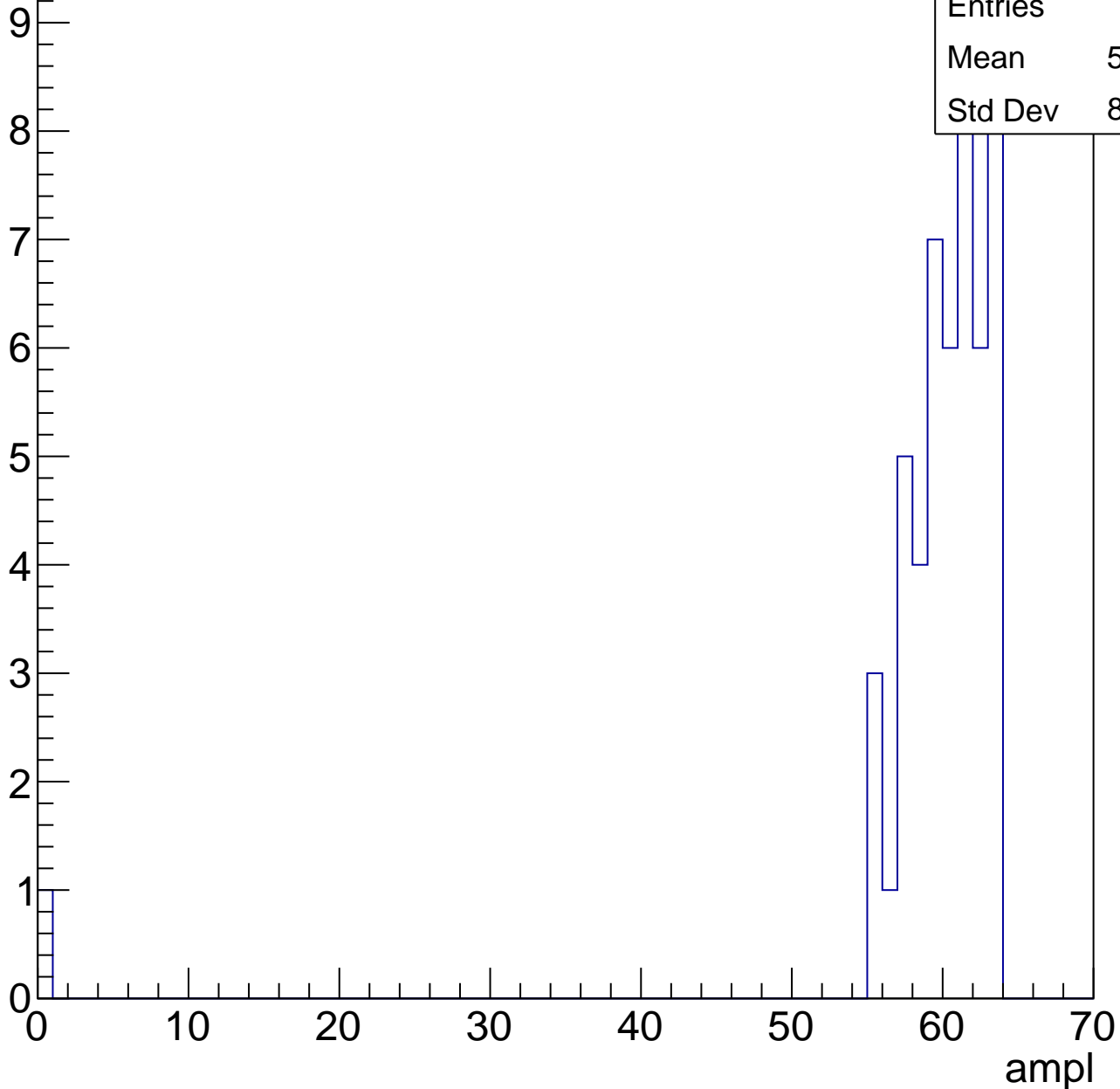


B1L100S, U15-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

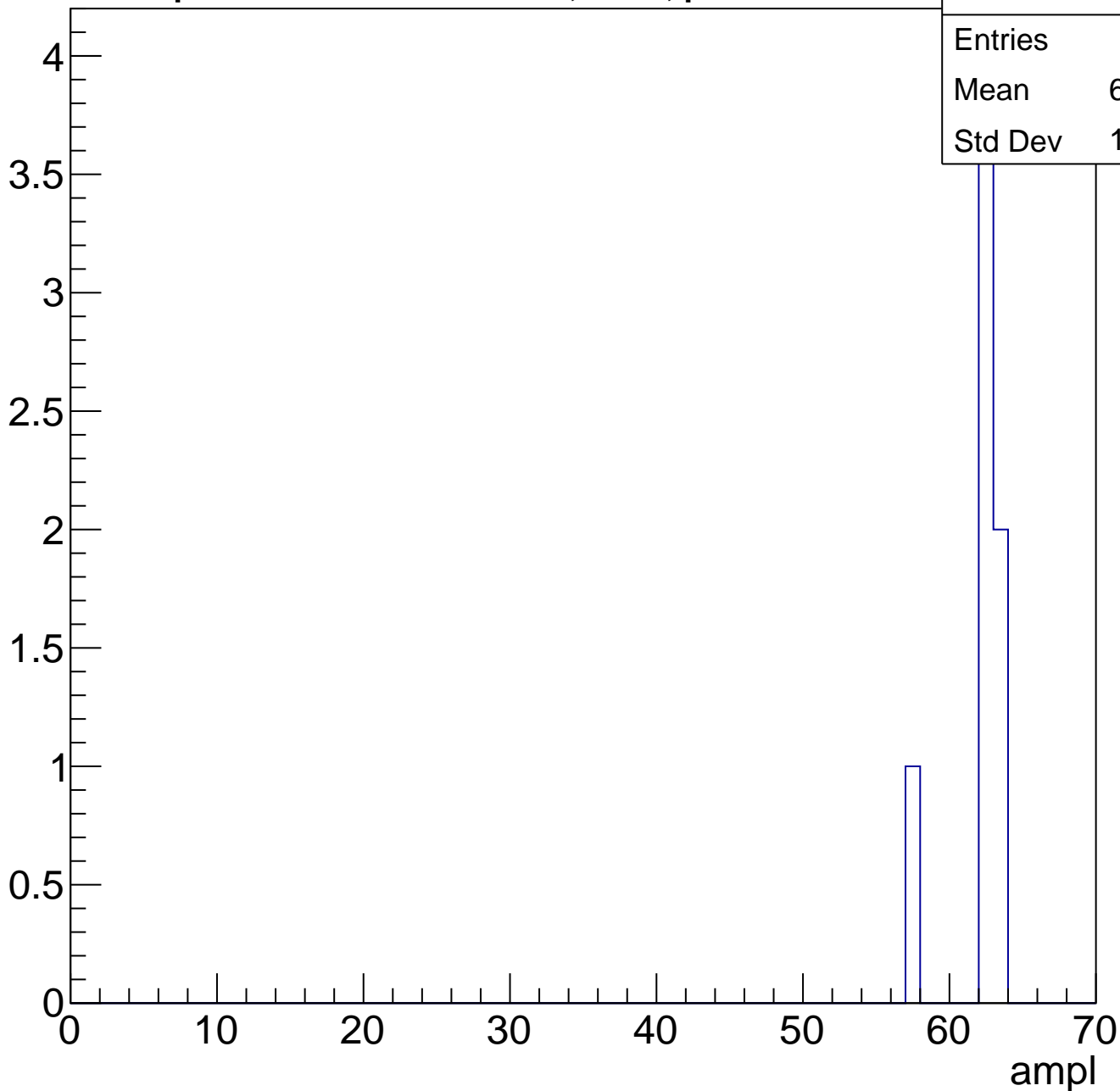
Entries	50
Mean	58.72
Std Dev	8.697



B1L100S, U15-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch92, adc0

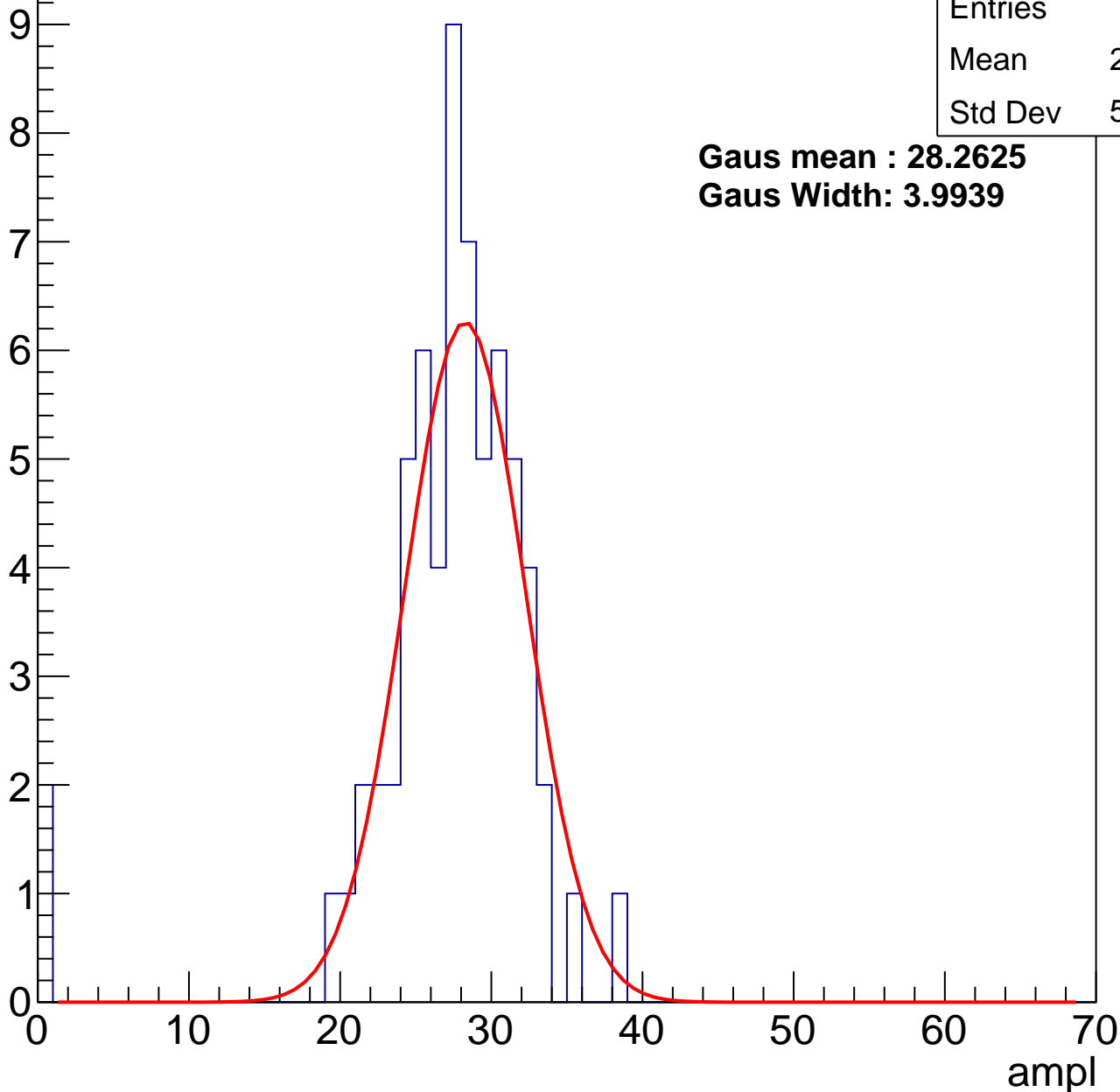
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	26.63
Std Dev	5.958

Gaus mean : 28.2625

Gaus Width: 3.9939



B1L100S, U15-ch92, adc1

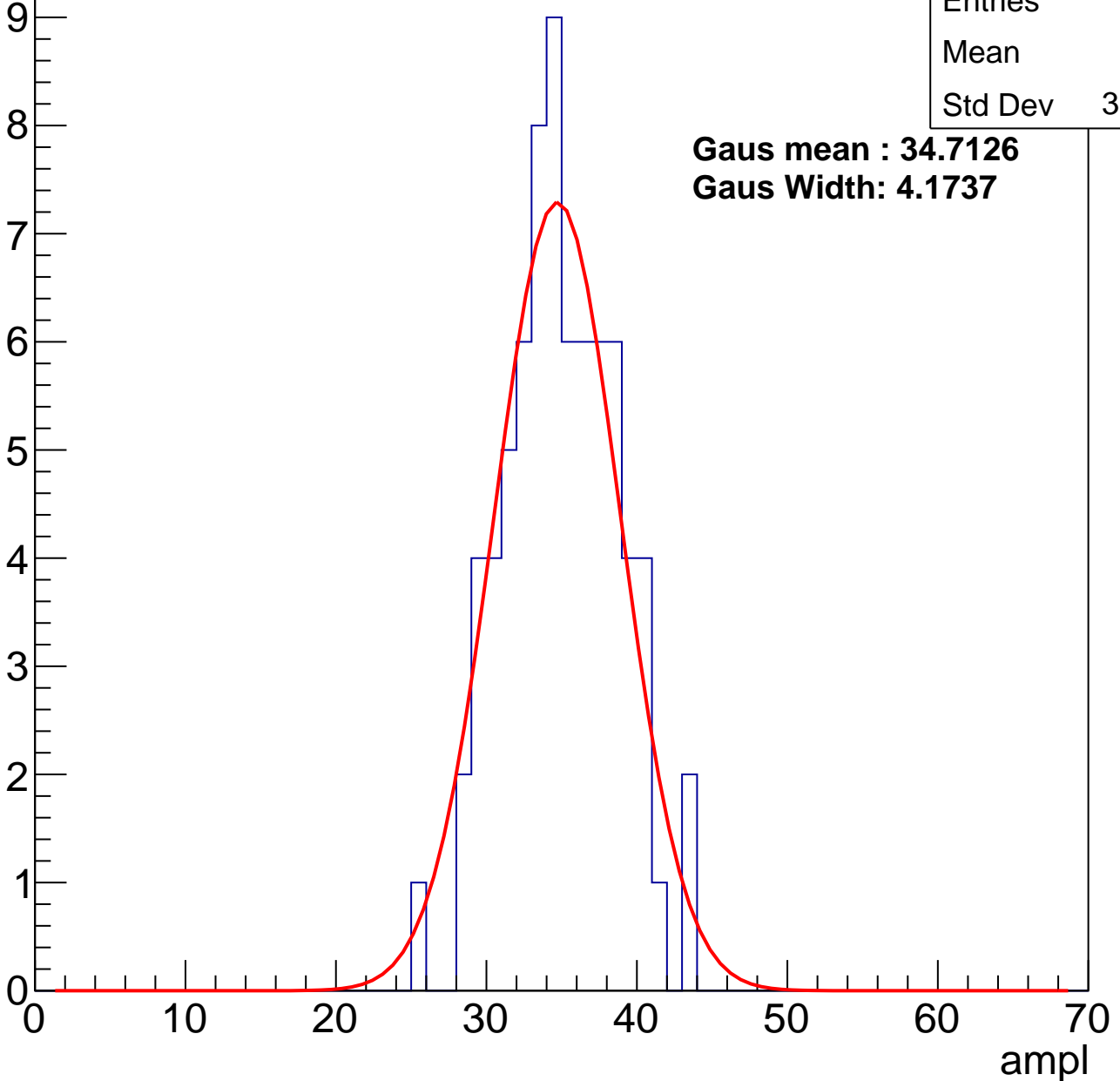
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	34.5
Std Dev	3.695

Gaus mean : 34.7126

Gaus Width: 4.1737



B1L100S, U15-ch92, adc2

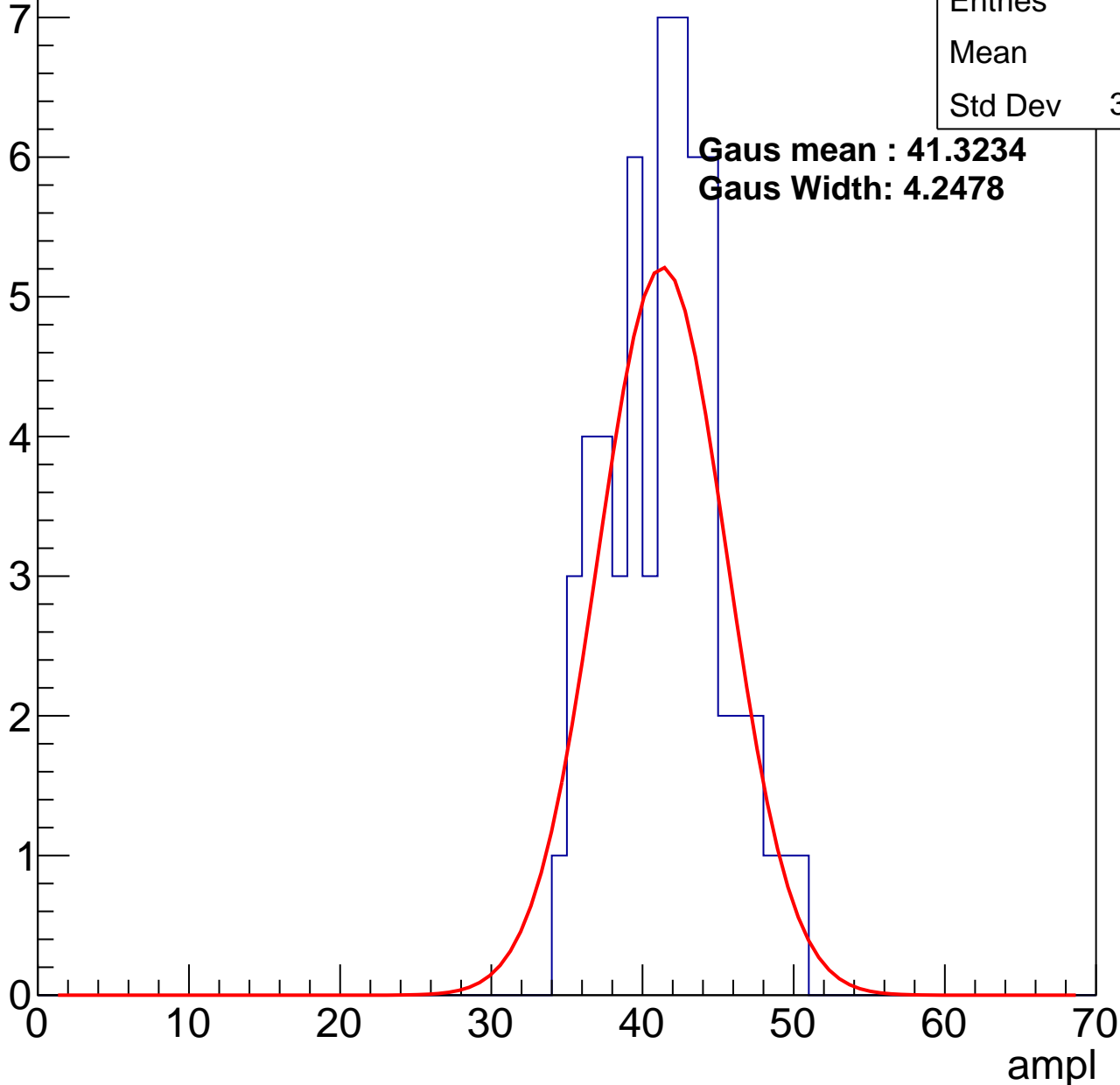
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	41.1
Std Dev	3.699

Gaus mean : 41.3234

Gaus Width: 4.2478

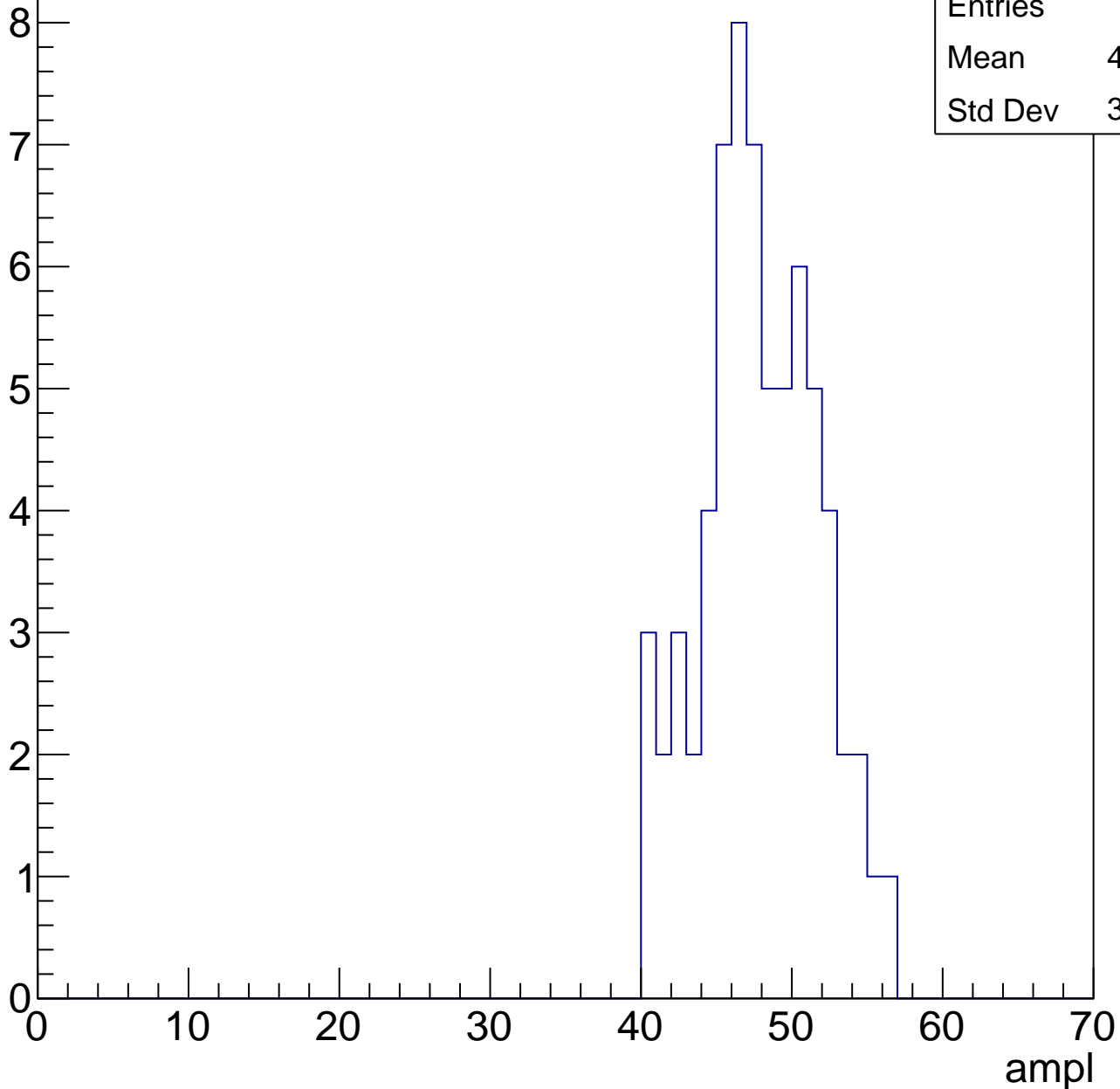


B1L100S, U15-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	47.39
Std Dev	3.789

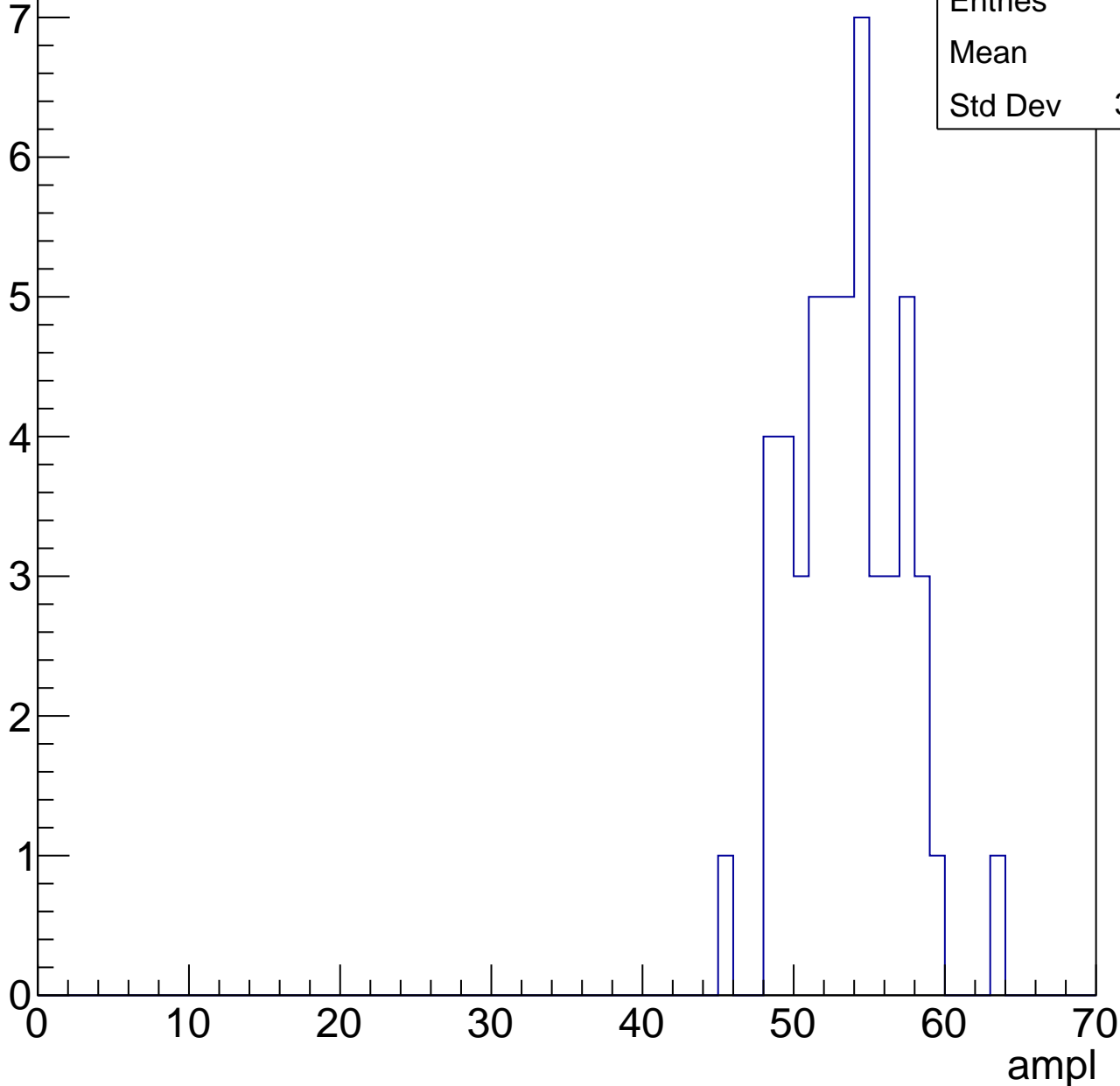


B1L100S, U15-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

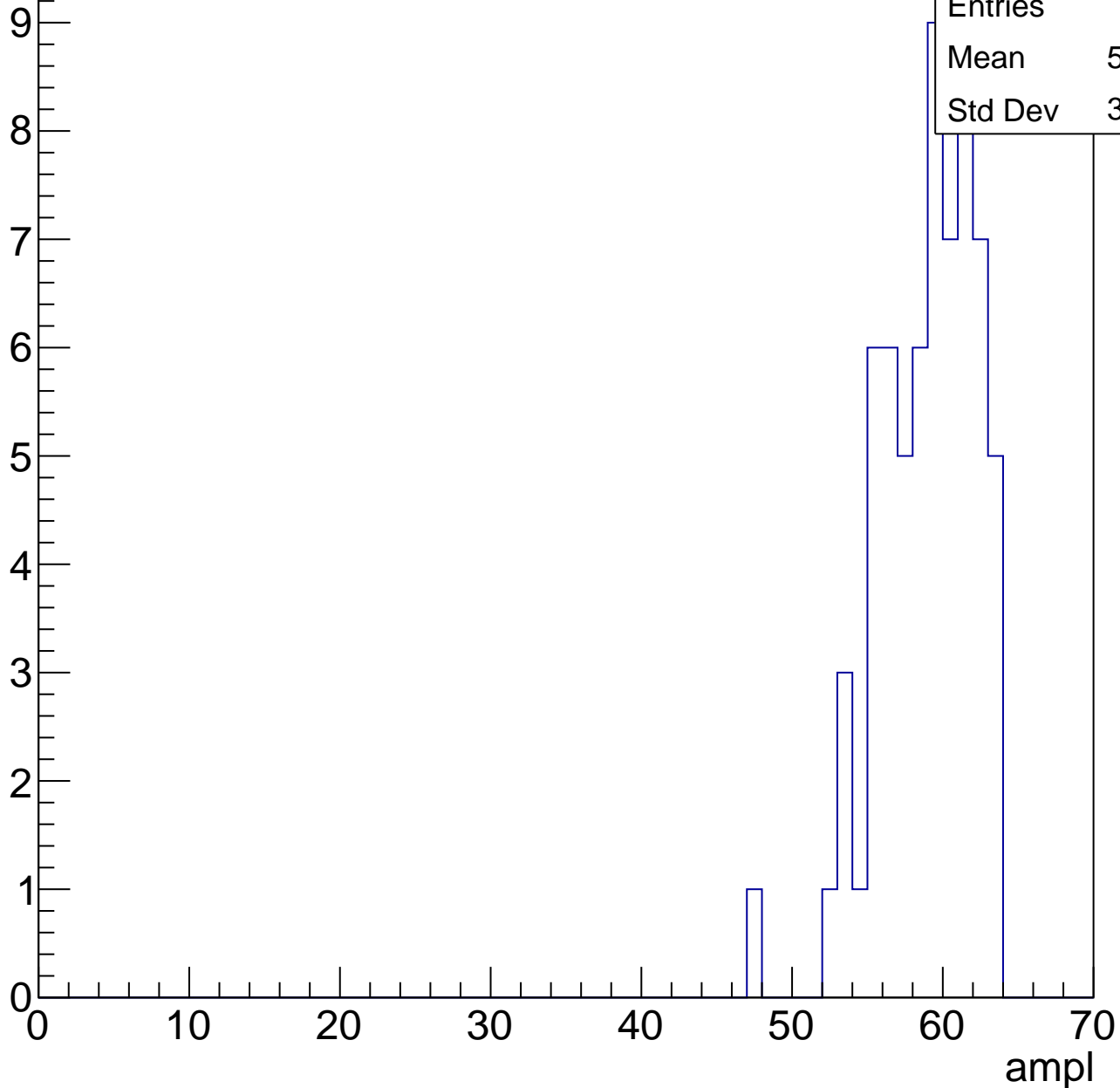
Entries	50
Mean	53.1
Std Dev	3.511



B1L100S, U15-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

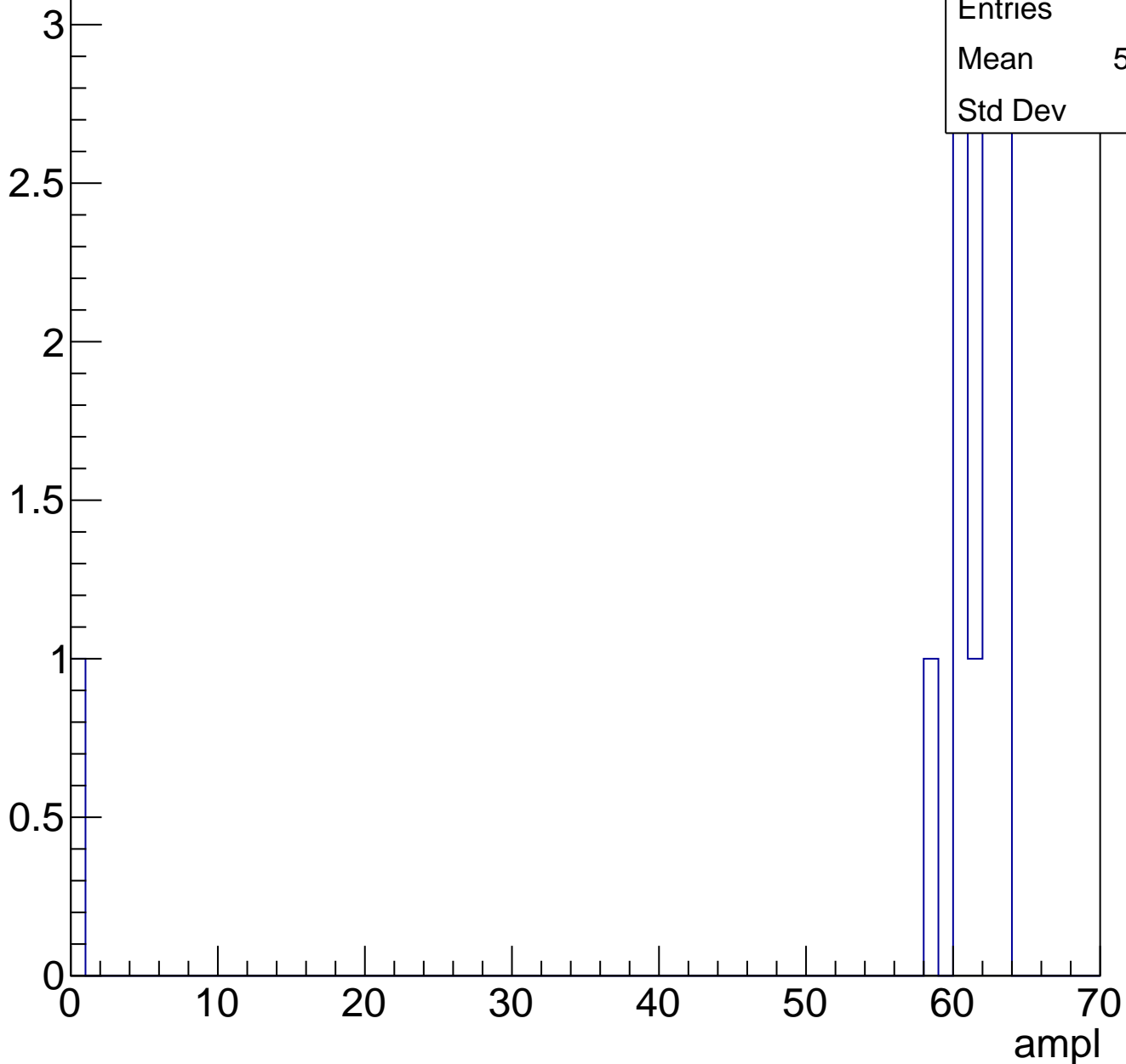
Entry



B1L100S, U15-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

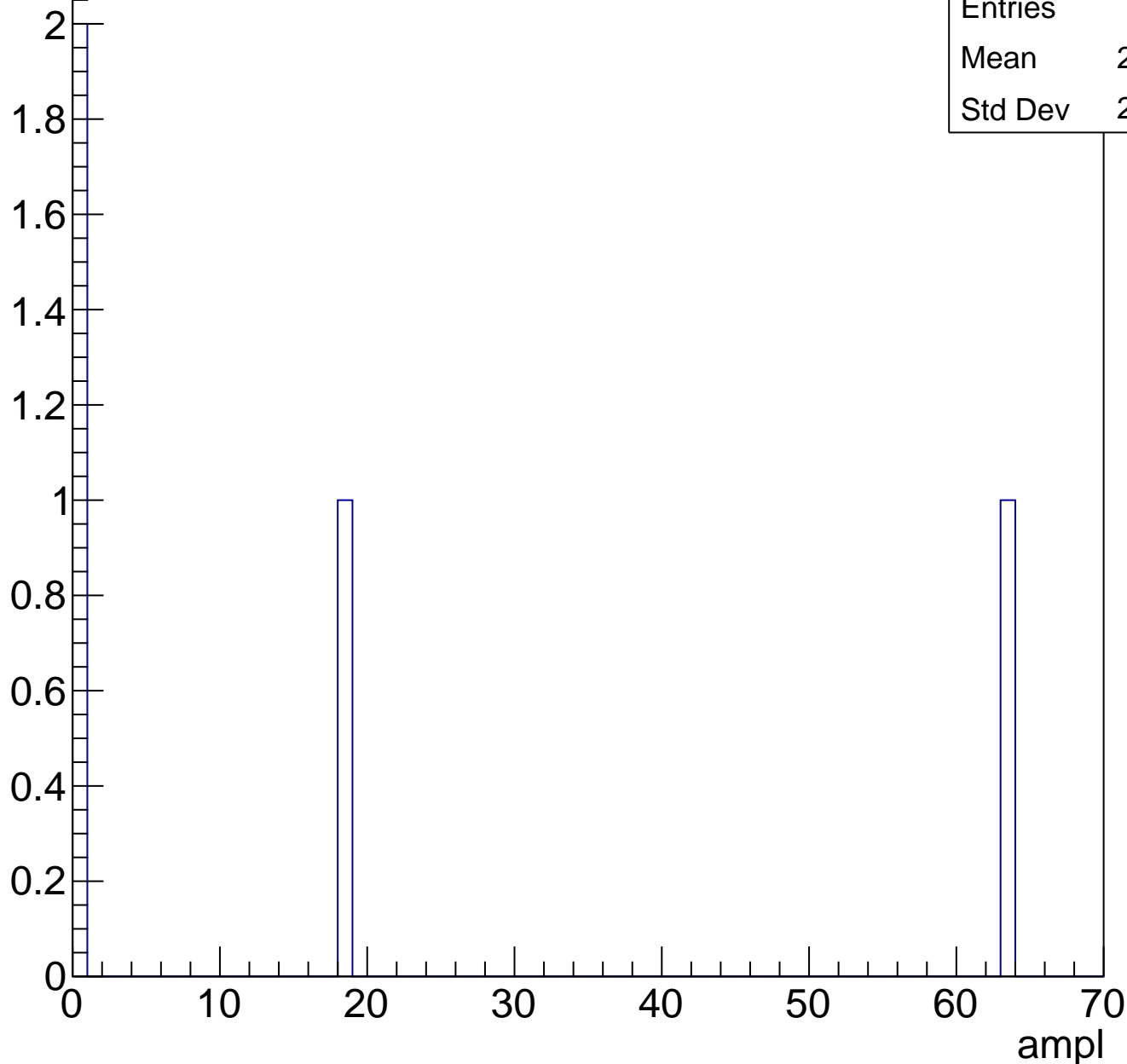
Entry



B1L100S, U15-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	20.25
Std Dev	25.75

B1L100S, U15-ch93, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	29.44
Std Dev	3.617

Gaus mean : 30.4117

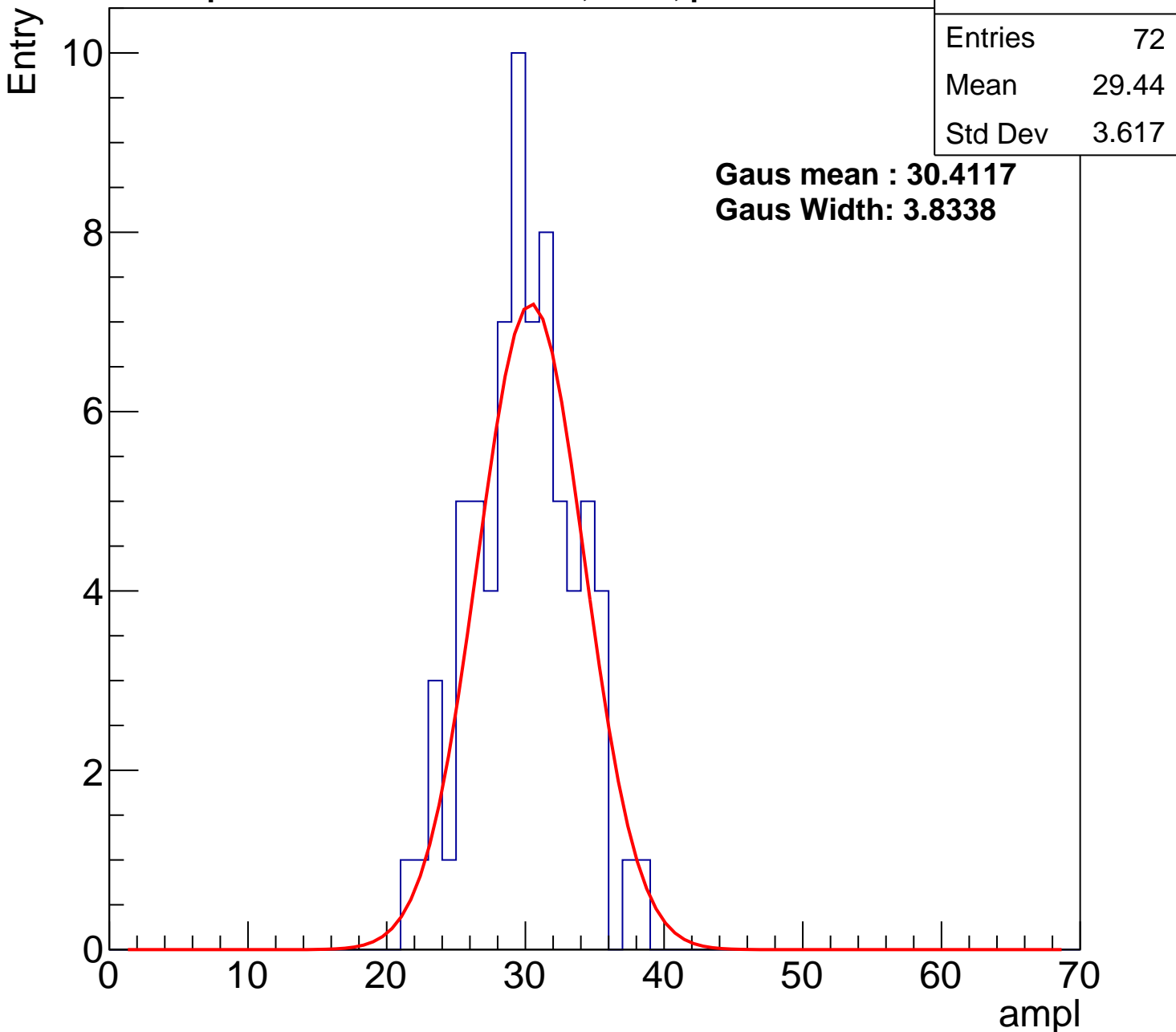
Gaus Width: 3.8338

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch93, adc1

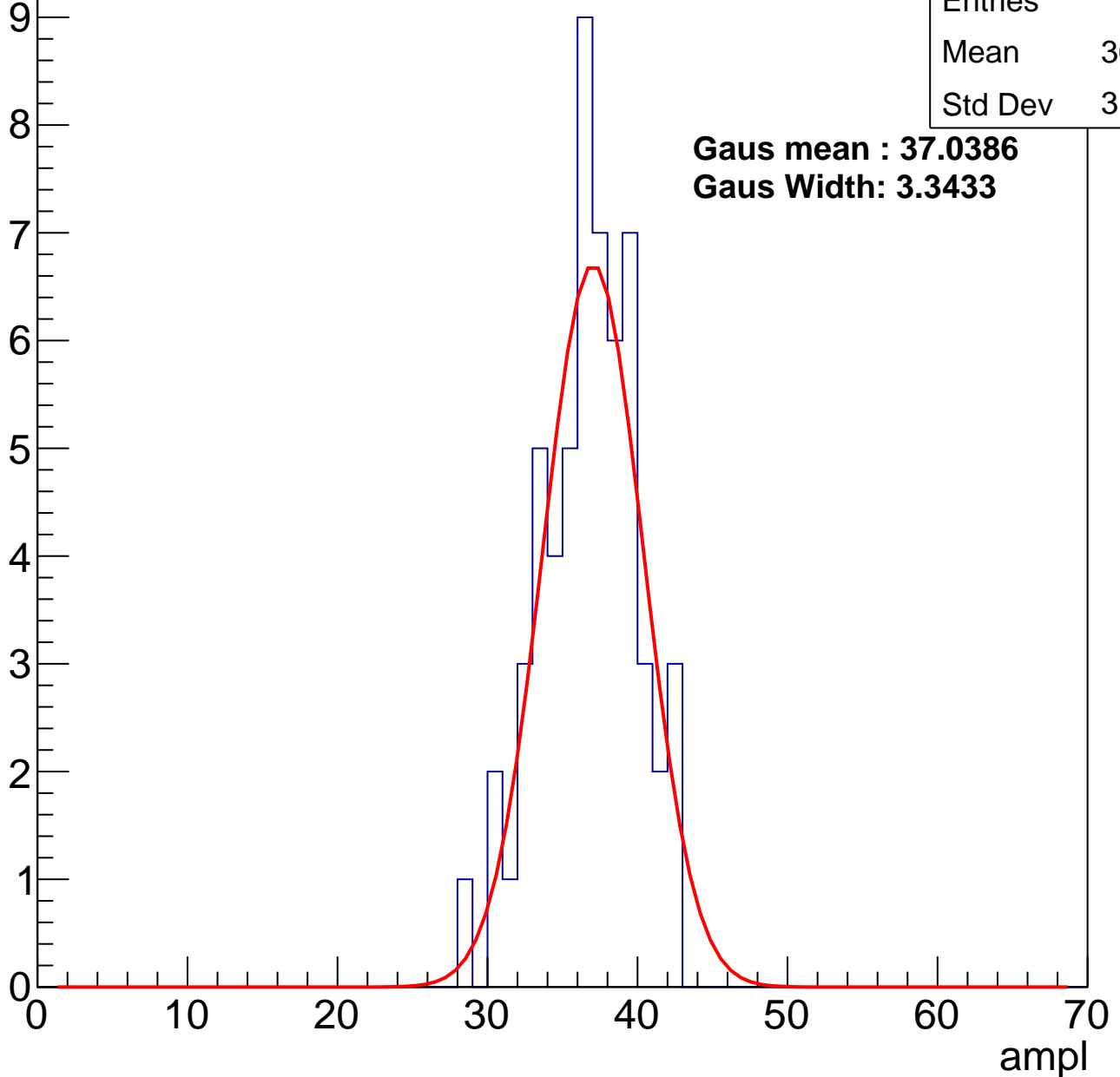
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	36.26
Std Dev	3.138

Gaus mean : 37.0386

Gaus Width: 3.3433



B1L100S, U15-ch93, adc2

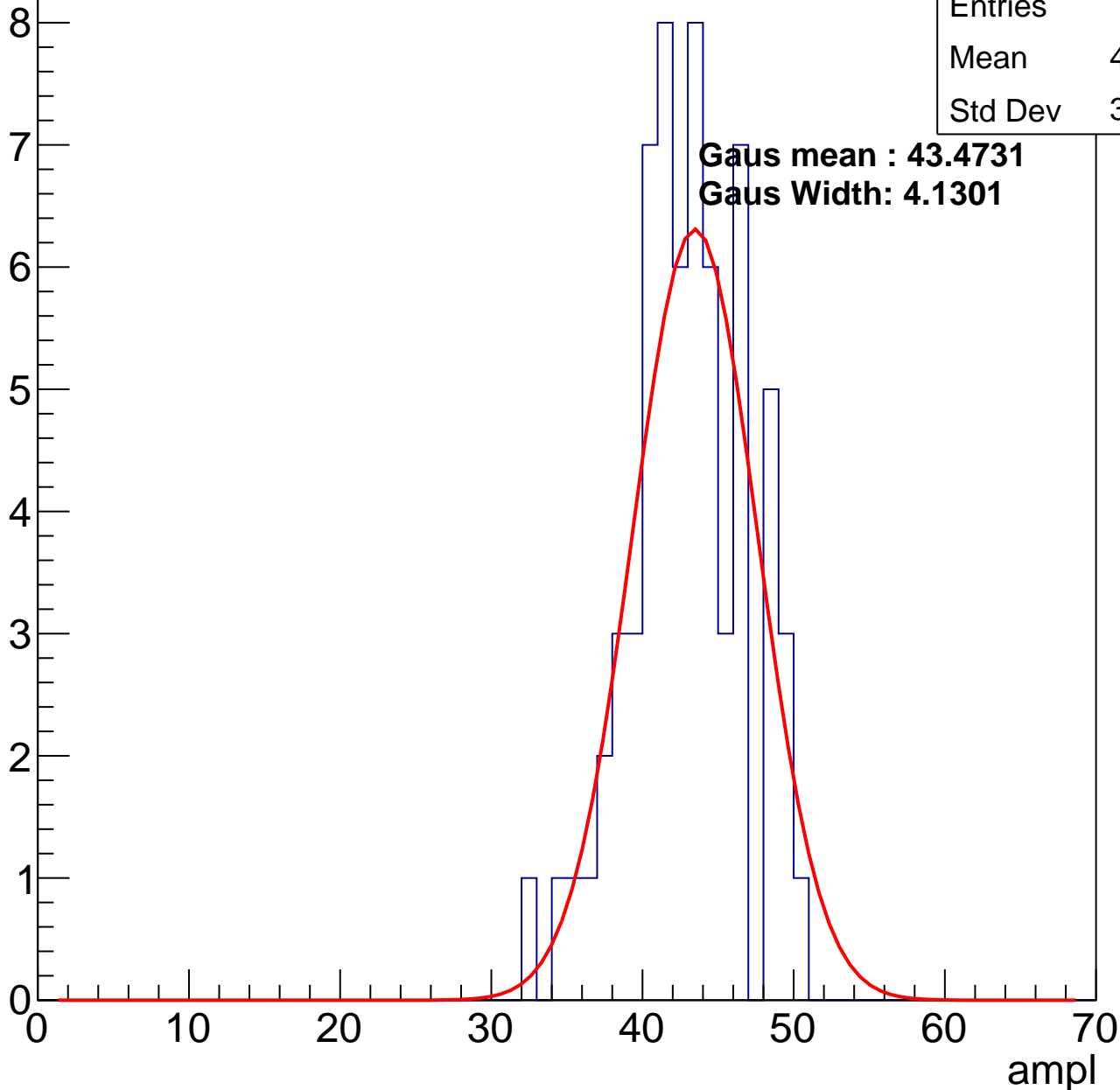
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.48
Std Dev	3.807

Gaus mean : 43.4731

Gaus Width: 4.1301



B1L100S, U15-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

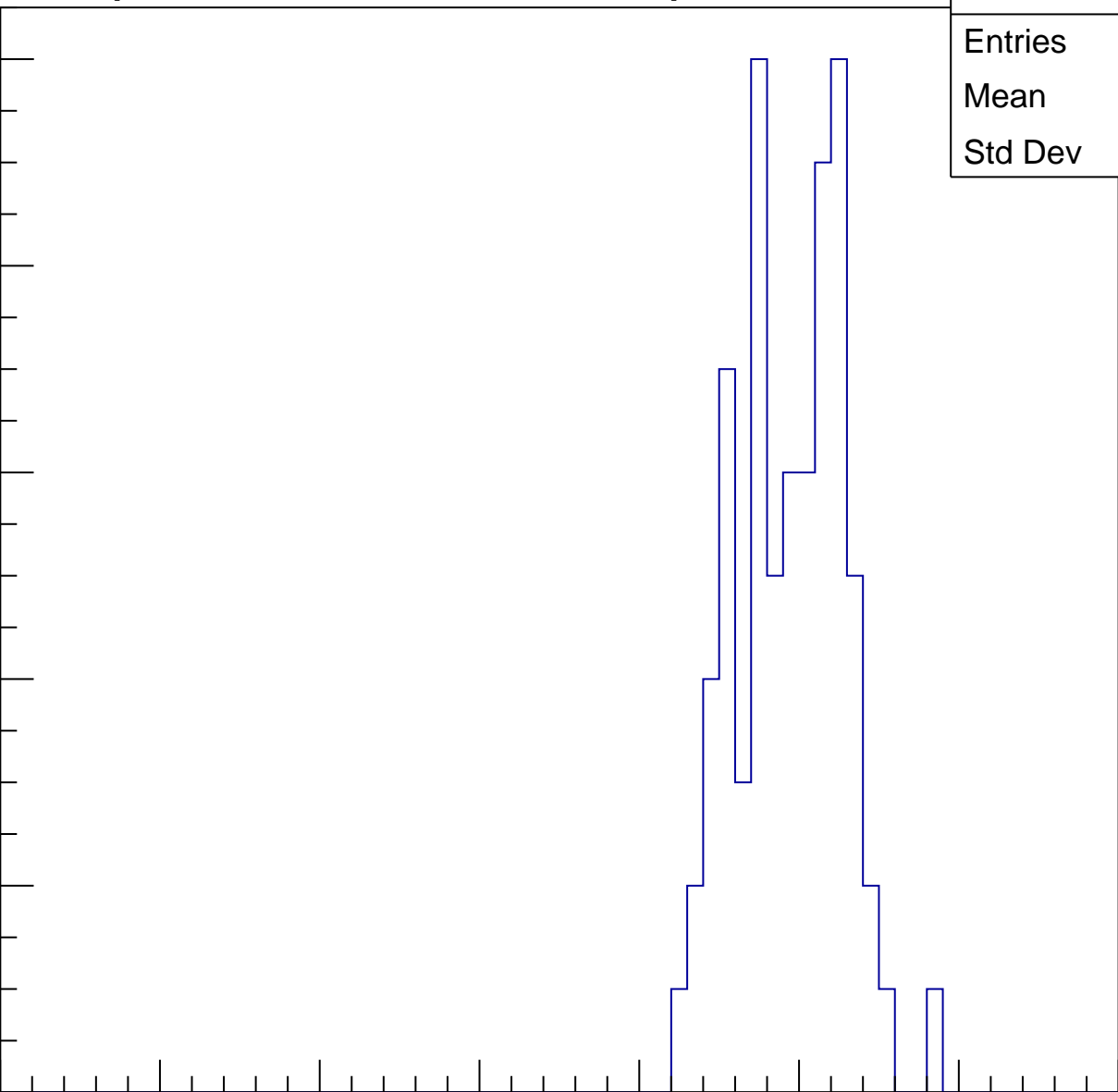
Entries	72
Mean	48.97
Std Dev	3.3

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

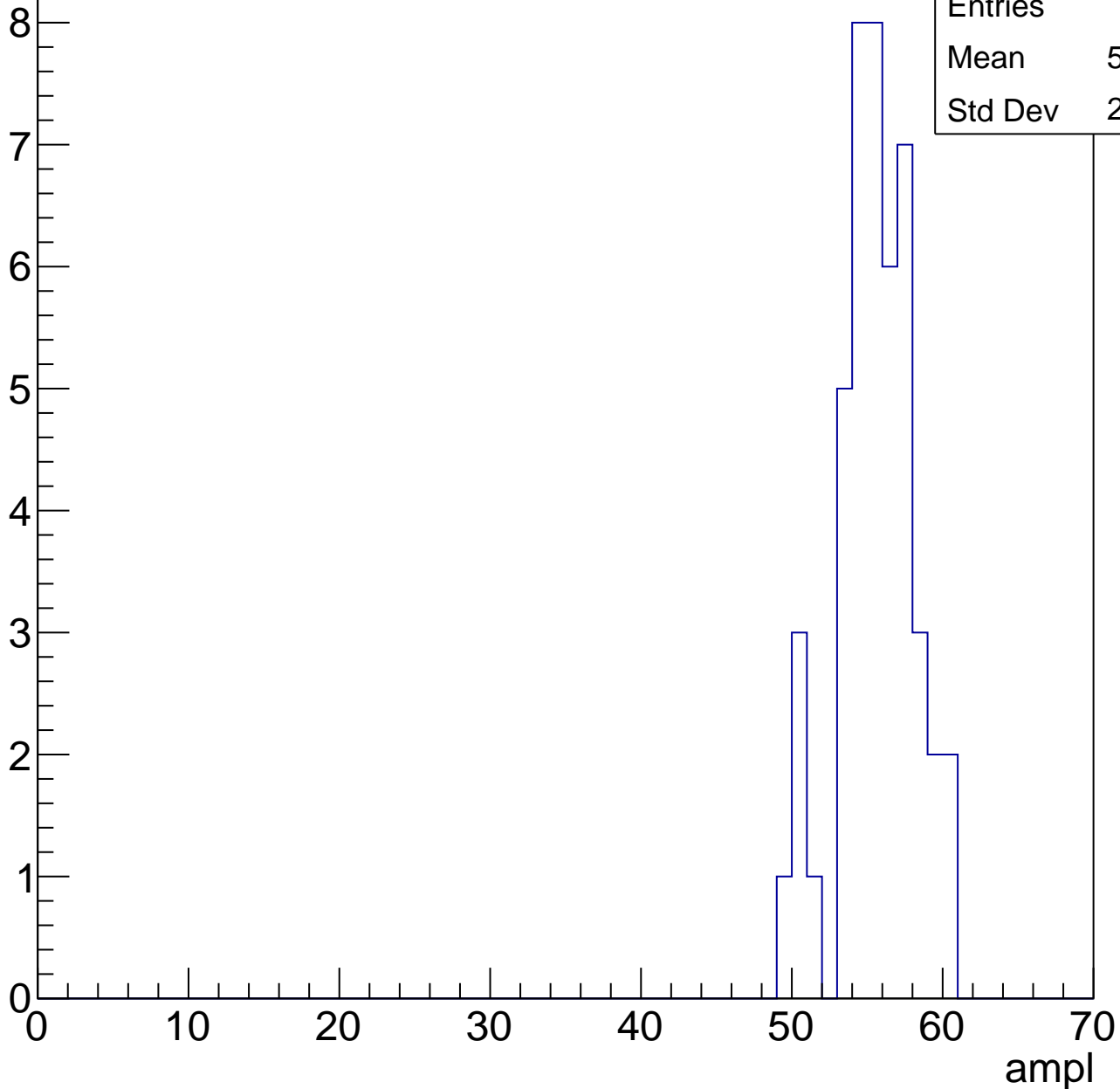


B1L100S, U15-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	55.09
Std Dev	2.544



B1L100S, U15-ch93, adc5

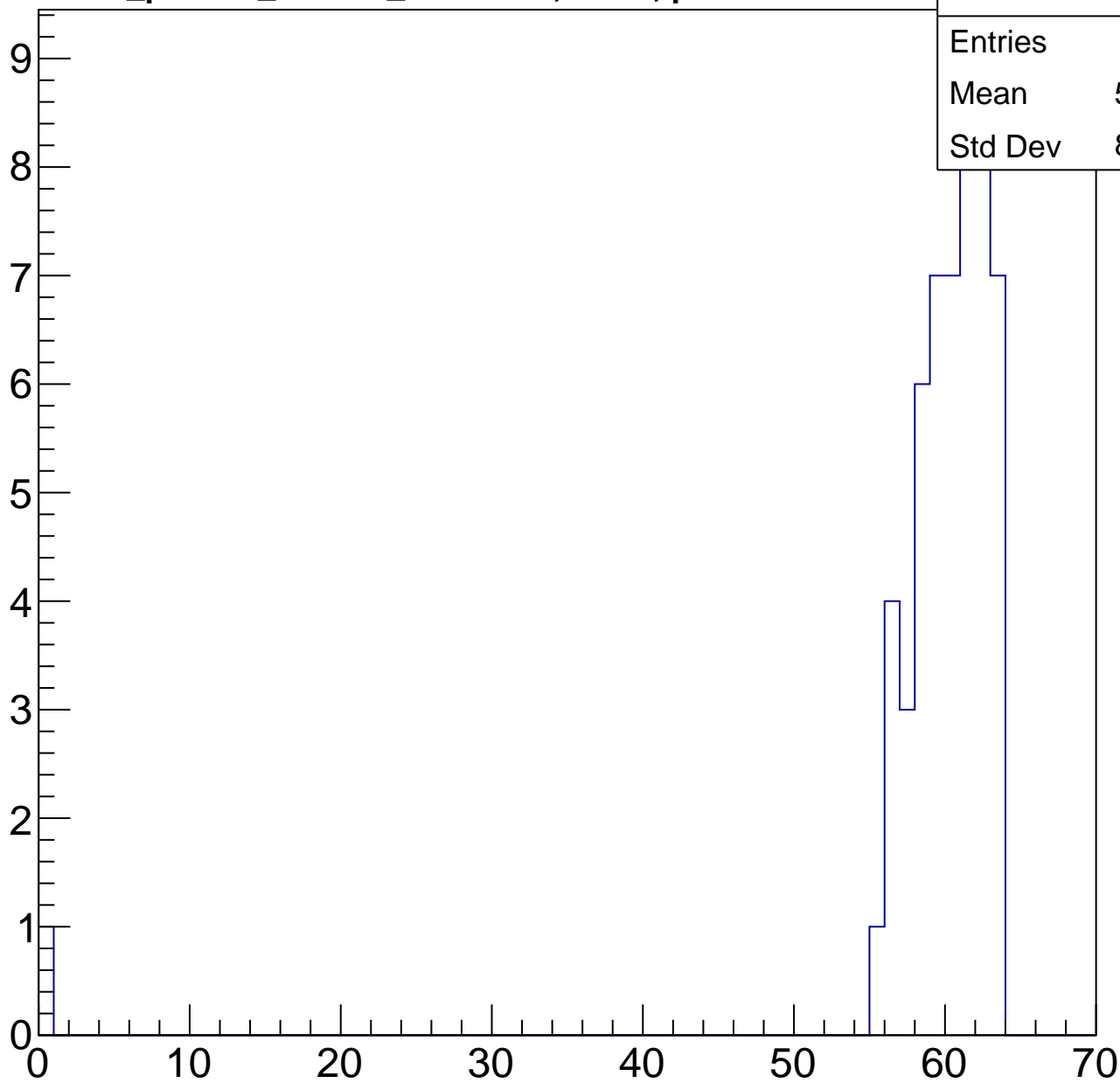
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.81
Std Dev	8.441

ampl



B1L100S, U15-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

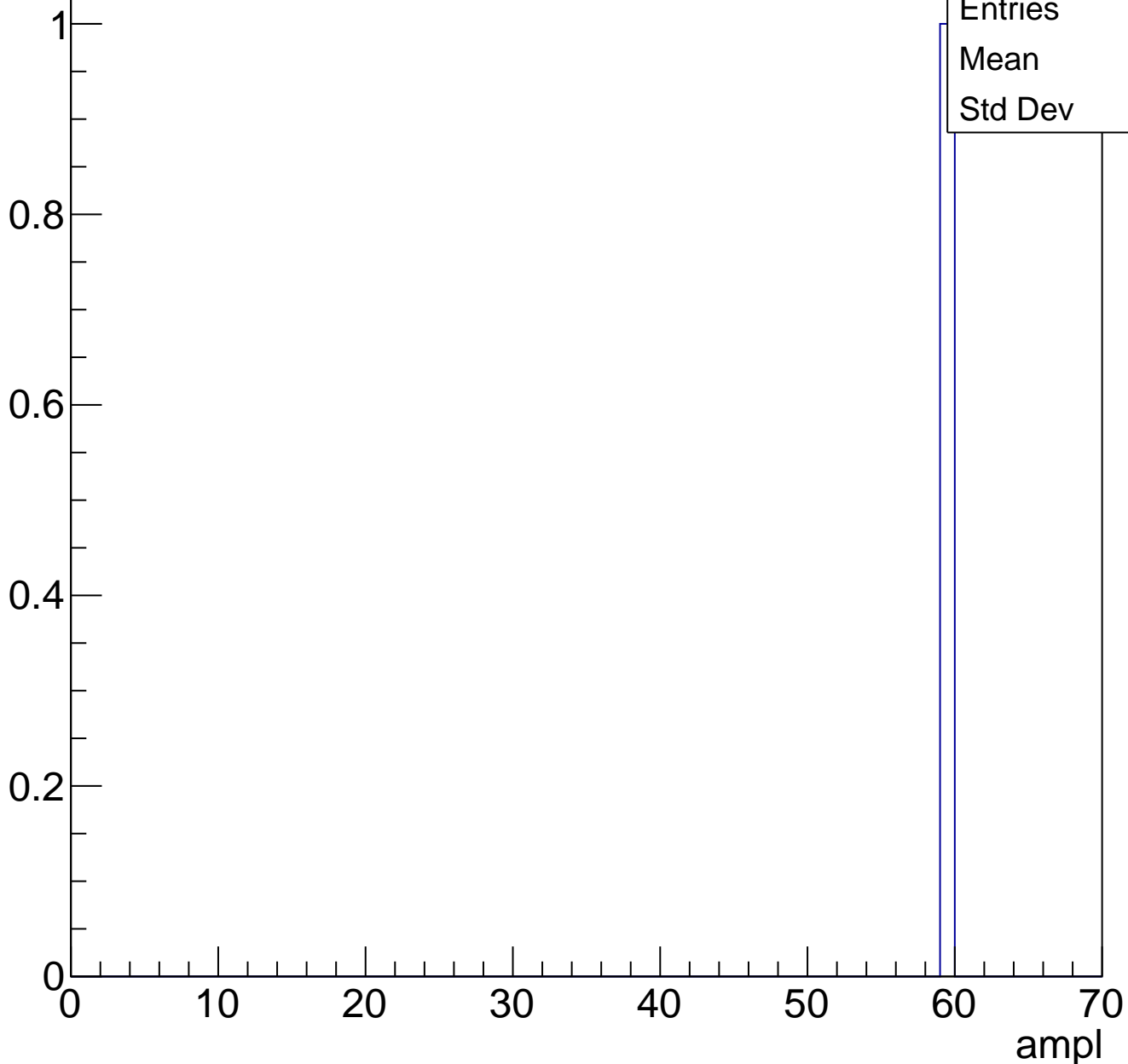
Entry



B1L100S, U15-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	59
Std Dev	0

B1L100S, U15-ch94, adc0

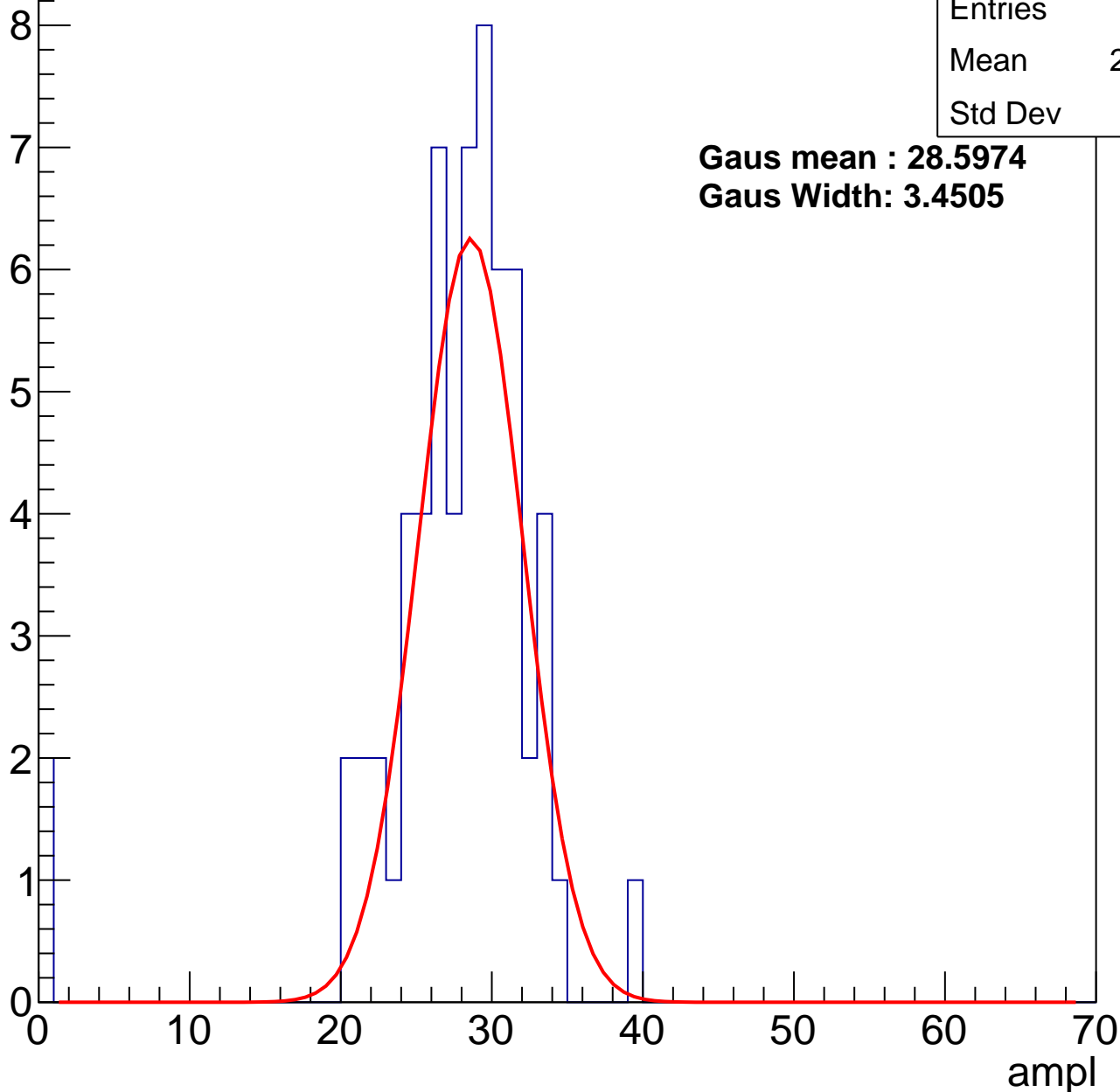
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	26.95
Std Dev	6.08

Gaus mean : 28.5974

Gaus Width: 3.4505



B1L100S, U15-ch94, adc1

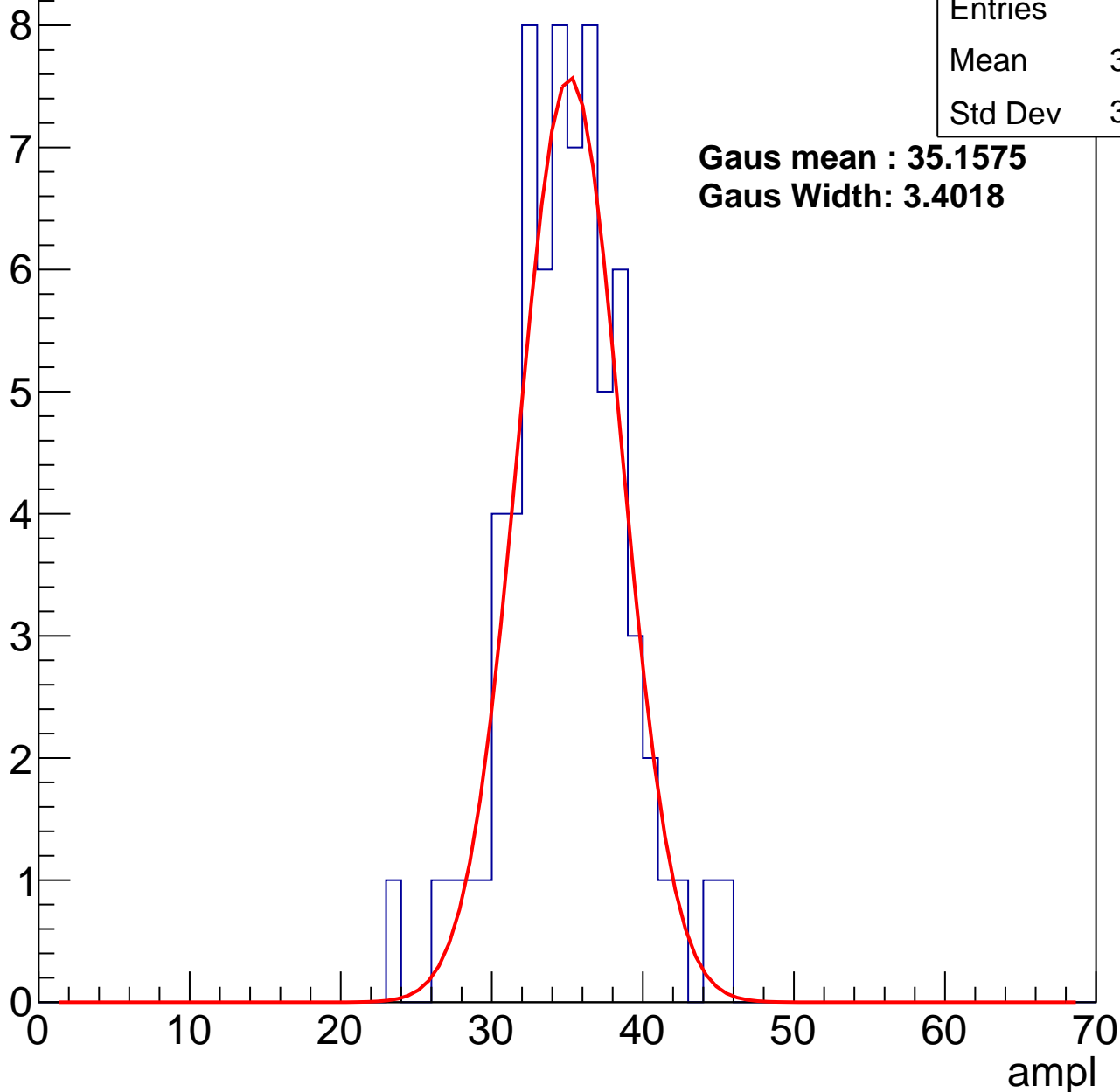
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	34.54
Std Dev	3.916

Gaus mean : 35.1575

Gaus Width: 3.4018



B1L100S, U15-ch94, adc2

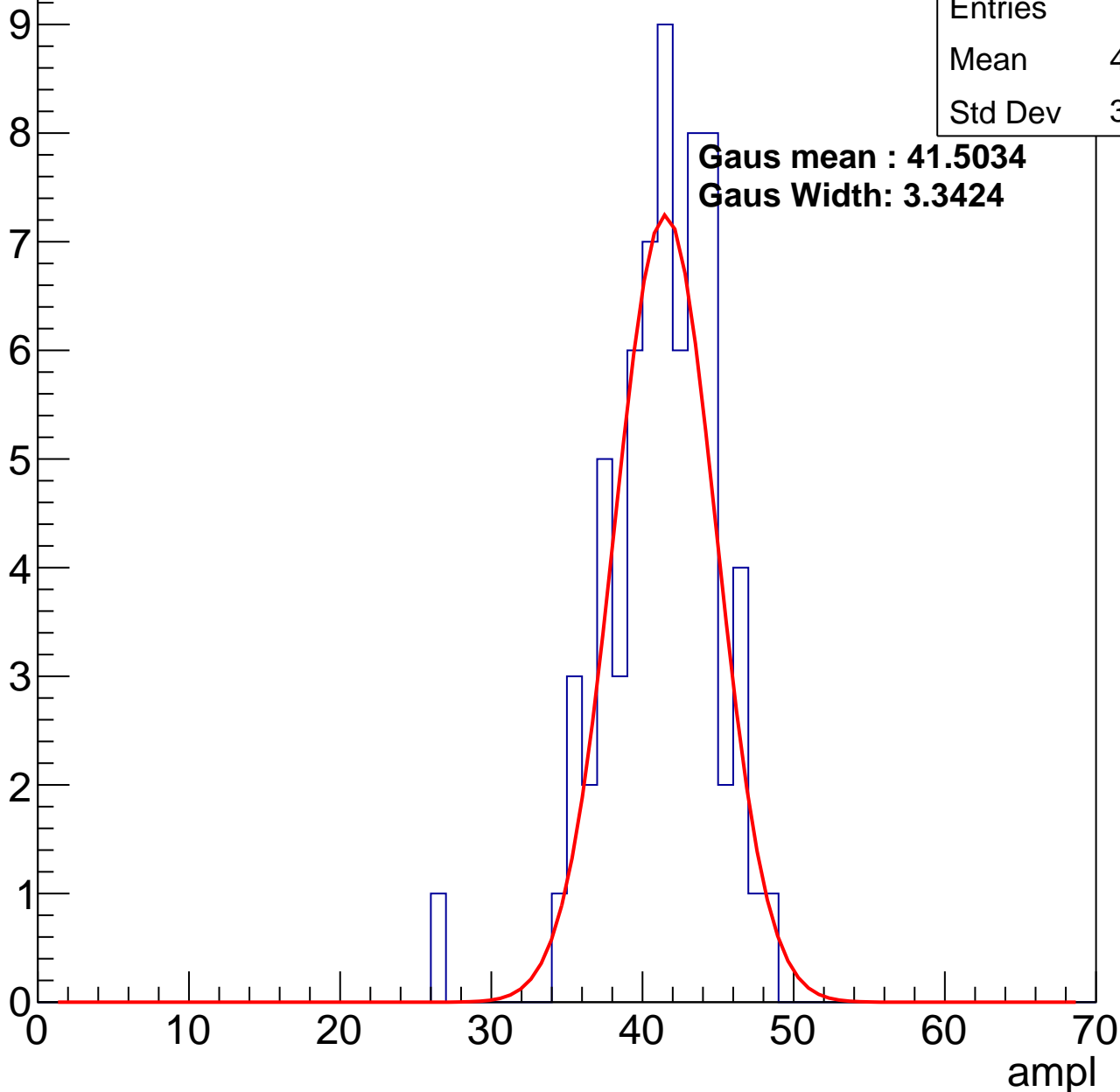
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	40.84
Std Dev	3.655

Gaus mean : 41.5034

Gaus Width: 3.3424

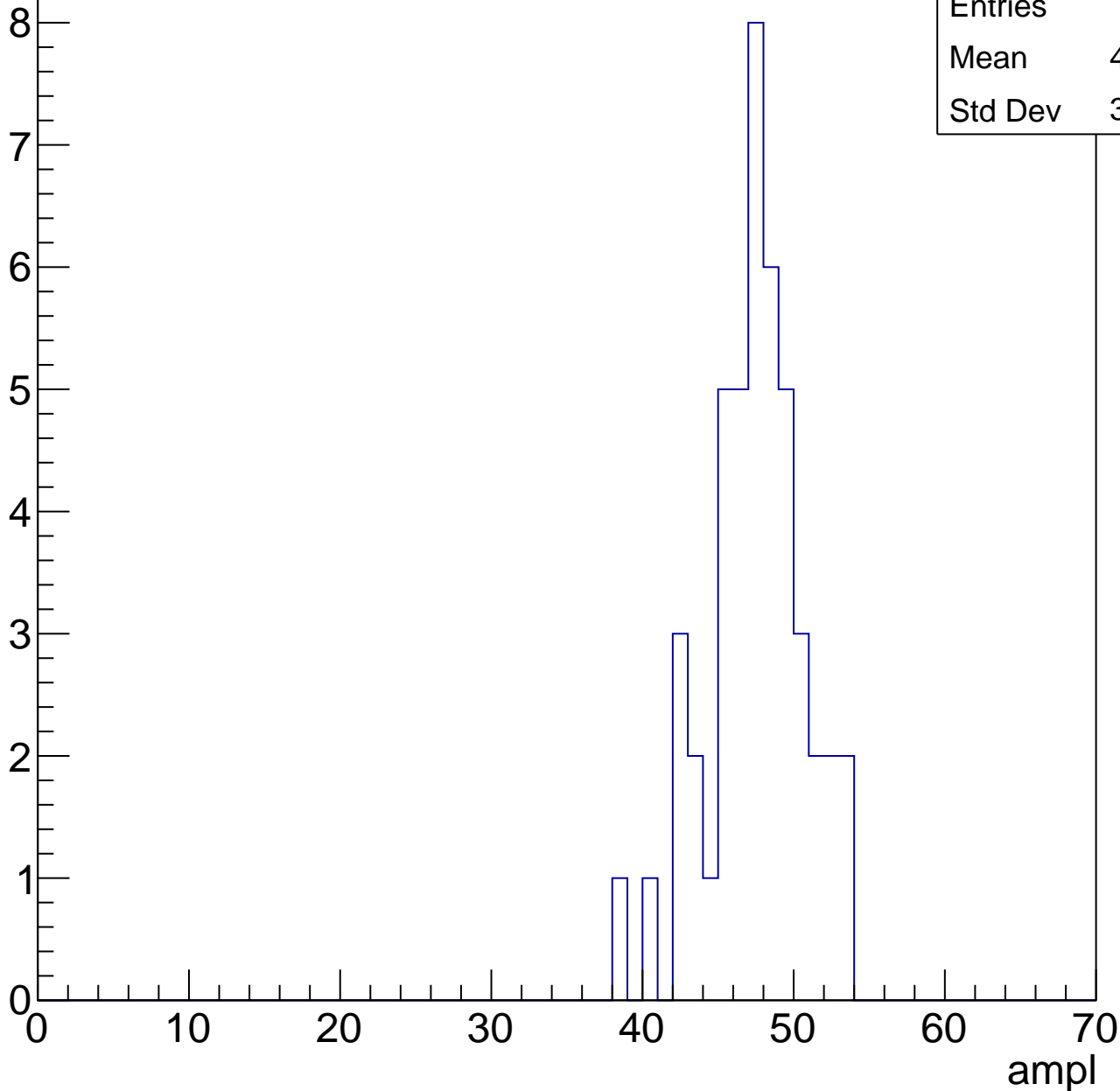


B1L100S, U15-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	46.96
Std Dev	3.223

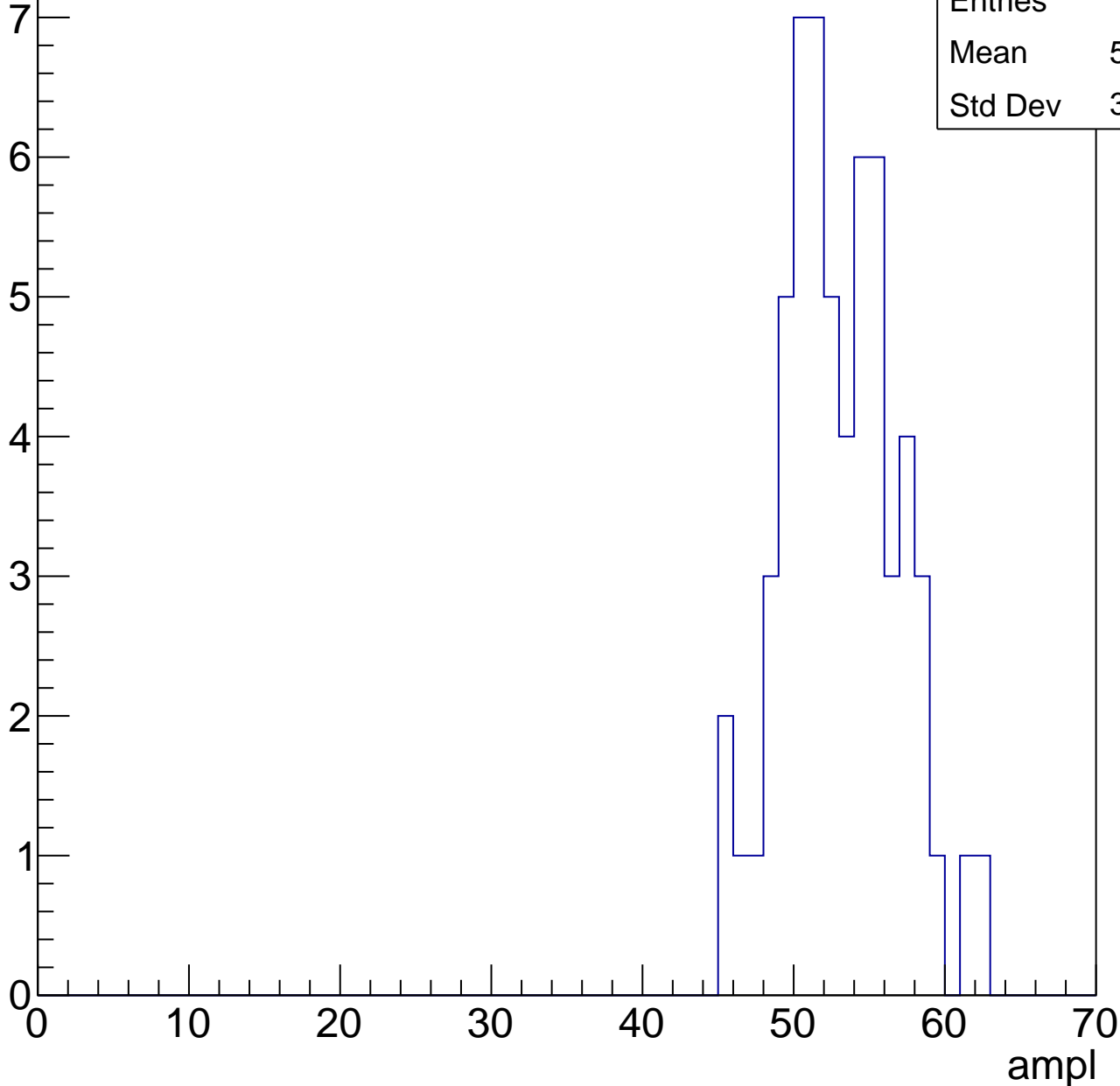


B1L100S, U15-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

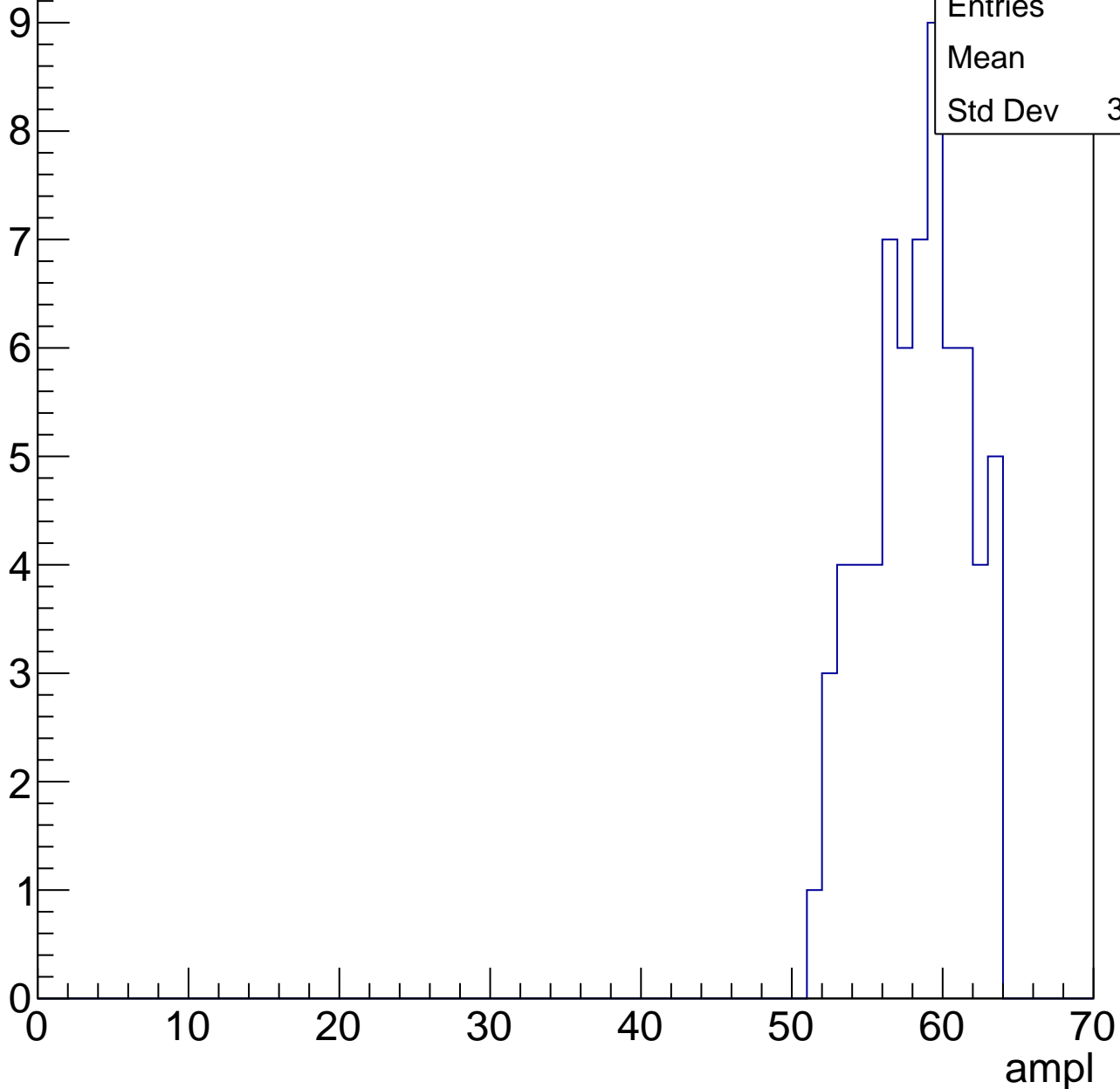
Entries	60
Mean	52.62
Std Dev	3.724



B1L100S, U15-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



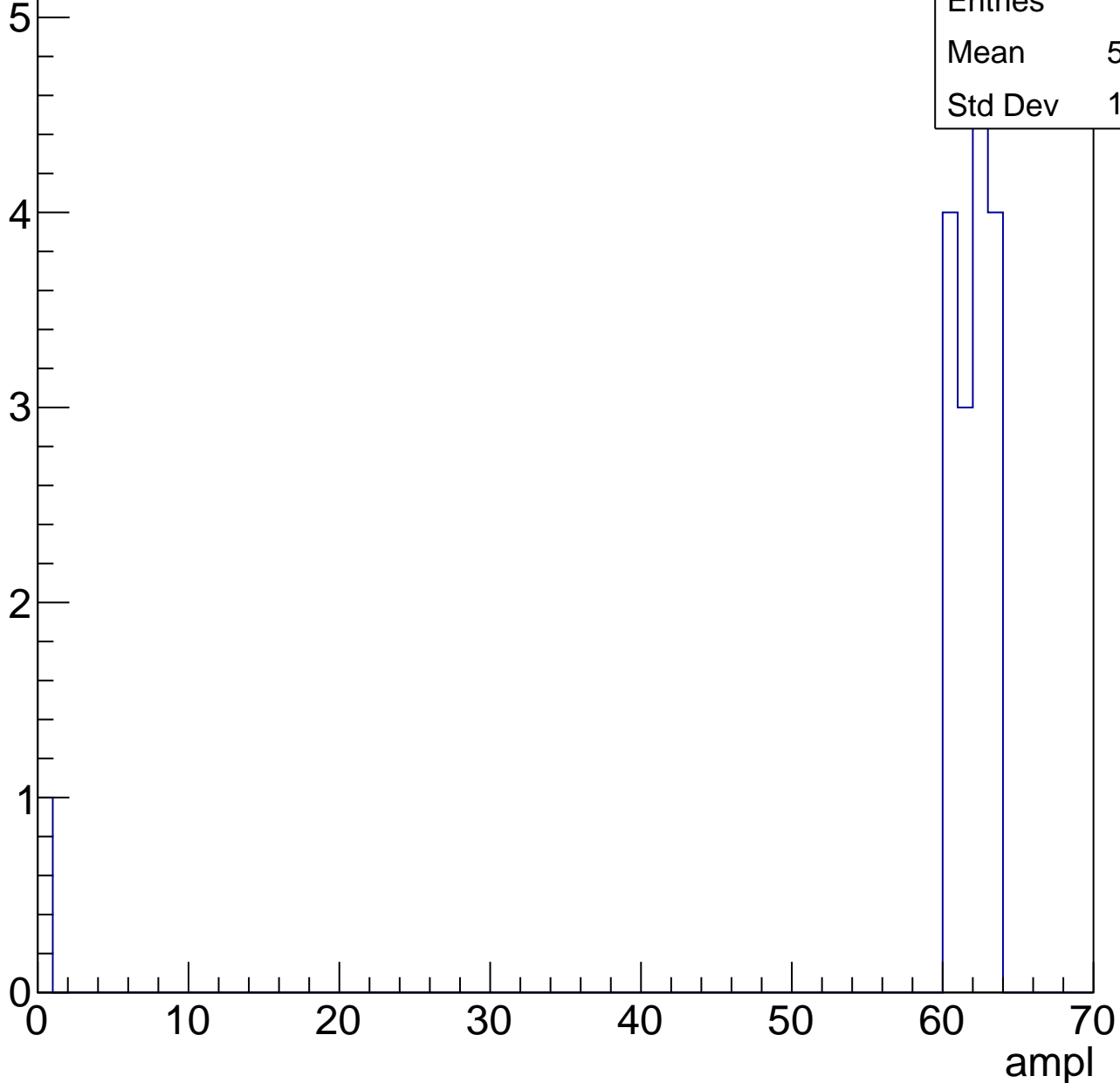
Entries	66
Mean	57.8
Std Dev	3.173

B1L100S, U15-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.94
Std Dev	14.53



B1L100S, U15-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch95, adc0

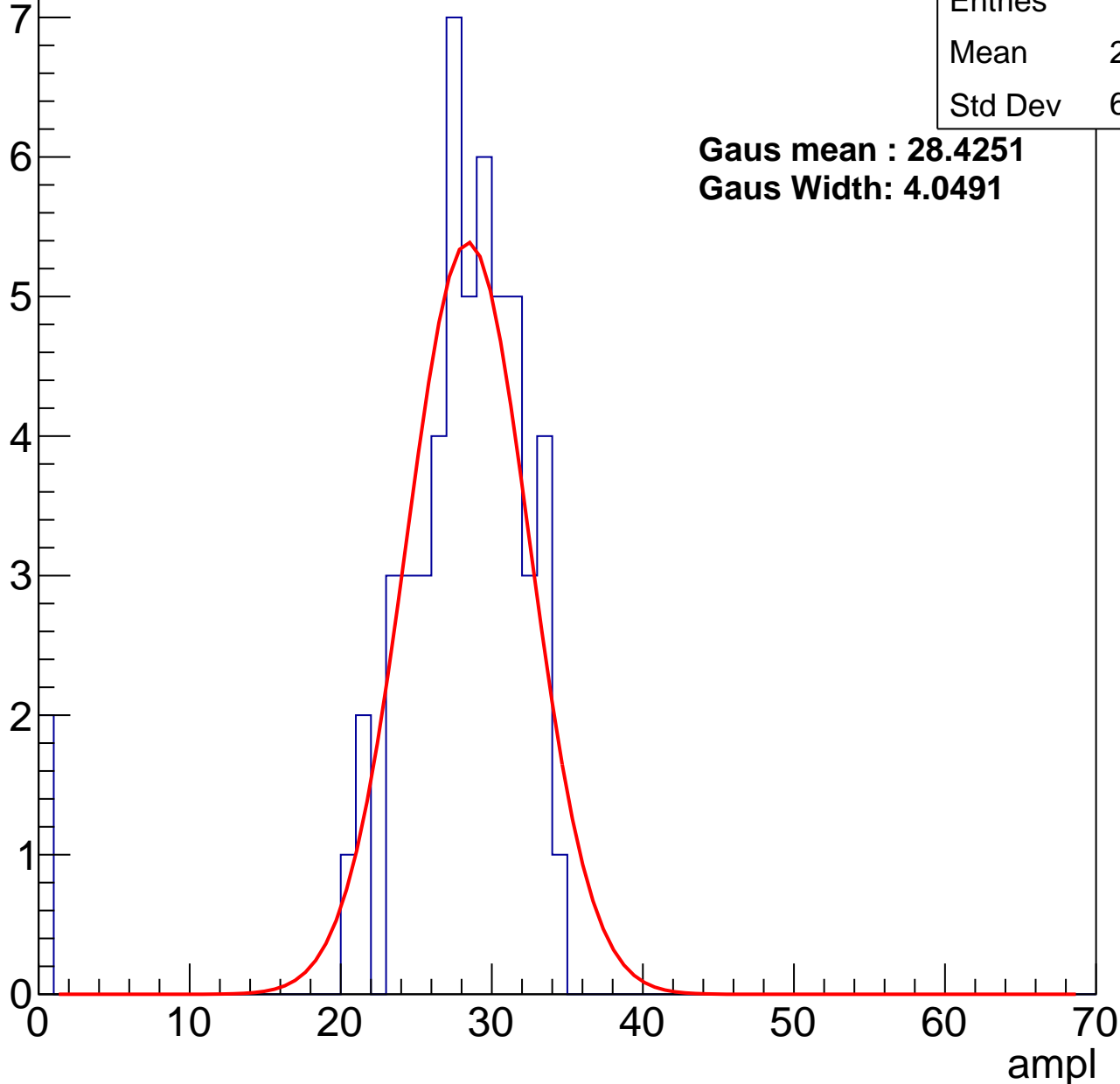
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	26.89
Std Dev	6.223

Gaus mean : 28.4251

Gaus Width: 4.0491



B1L100S, U15-ch95, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	35.35
Std Dev	3.772

Gaus mean : 35.4661

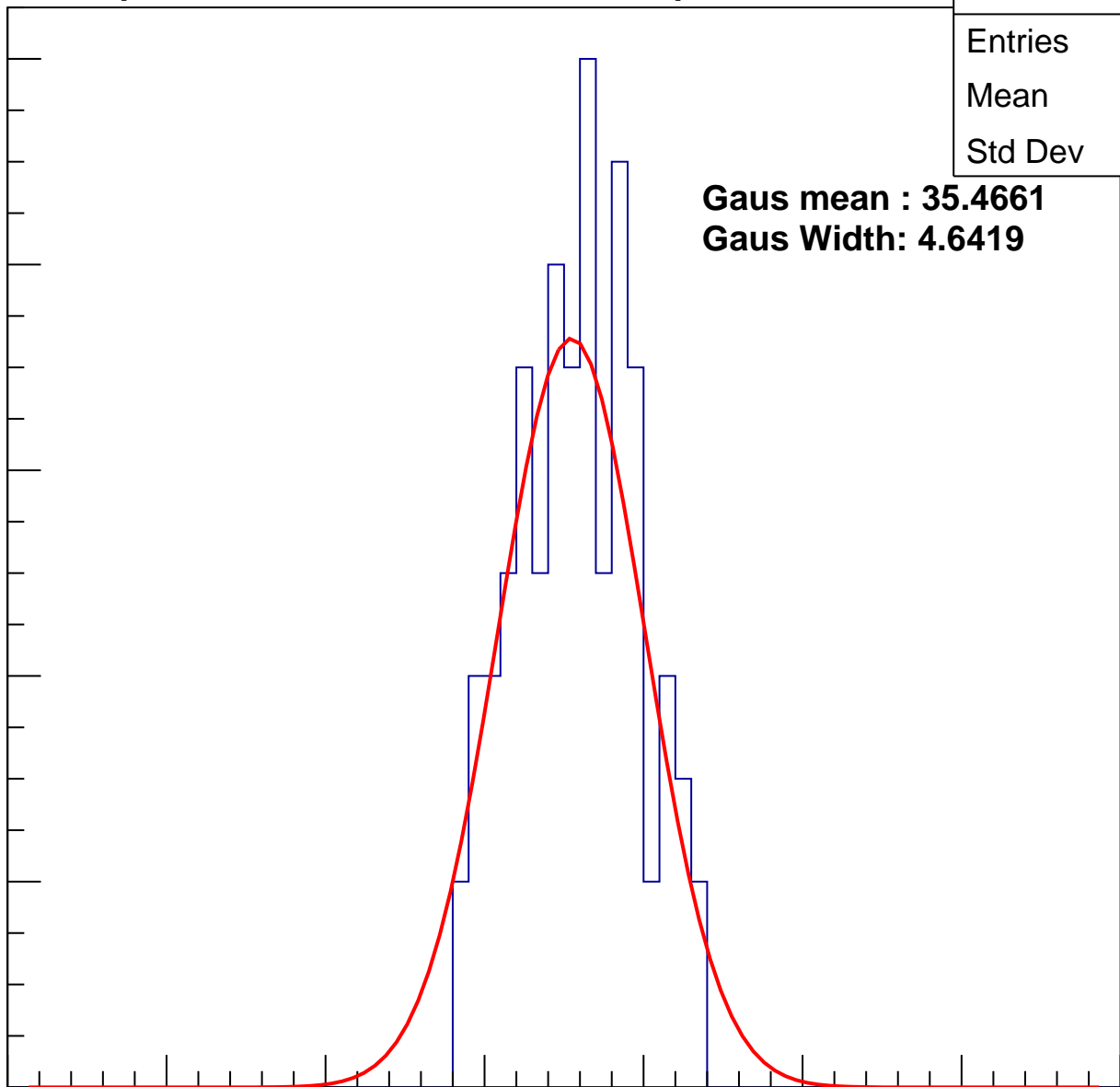
Gaus Width: 4.6419

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch95, adc2

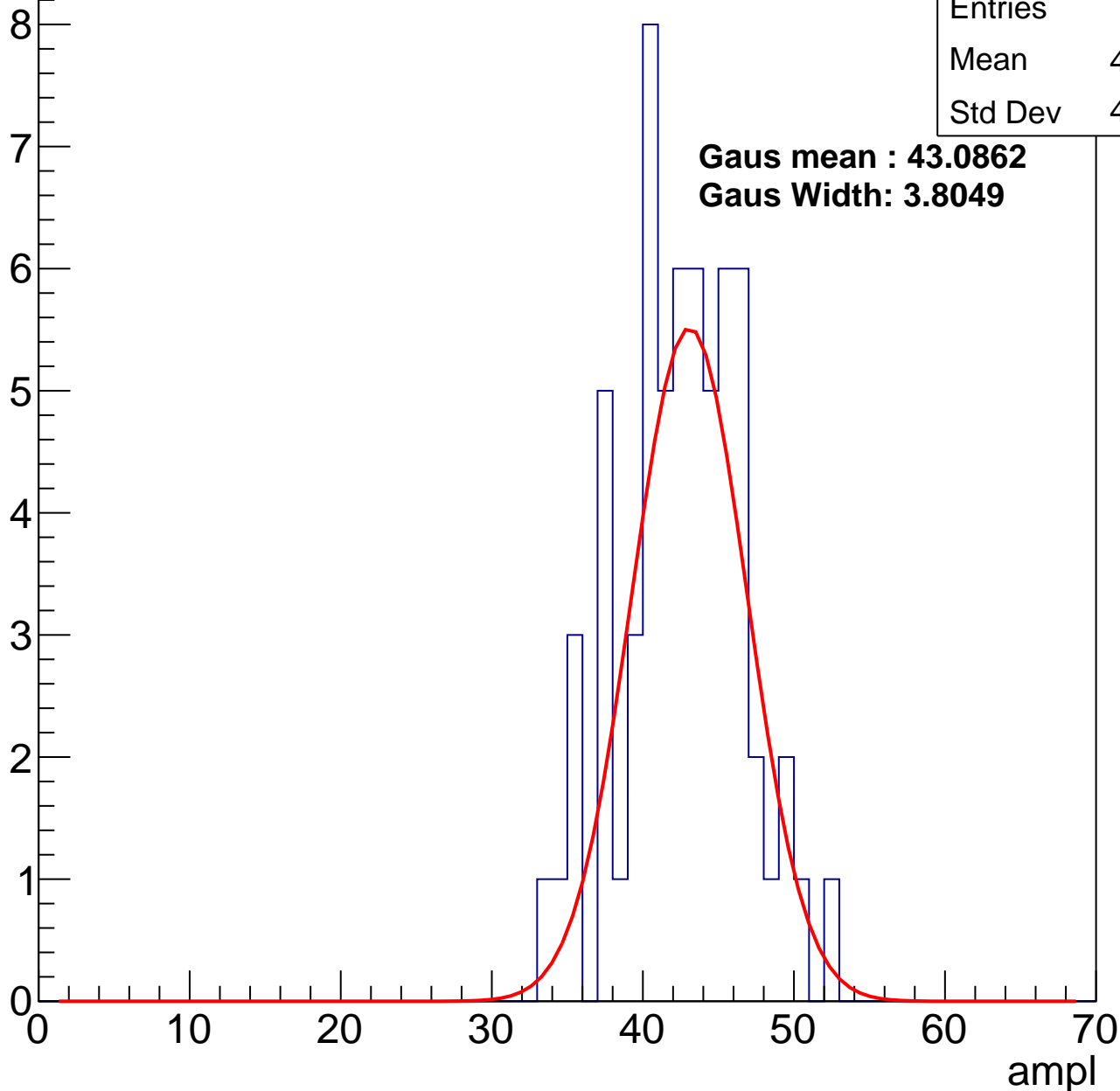
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.14
Std Dev	4.023

Gaus mean : 43.0862

Gaus Width: 3.8049

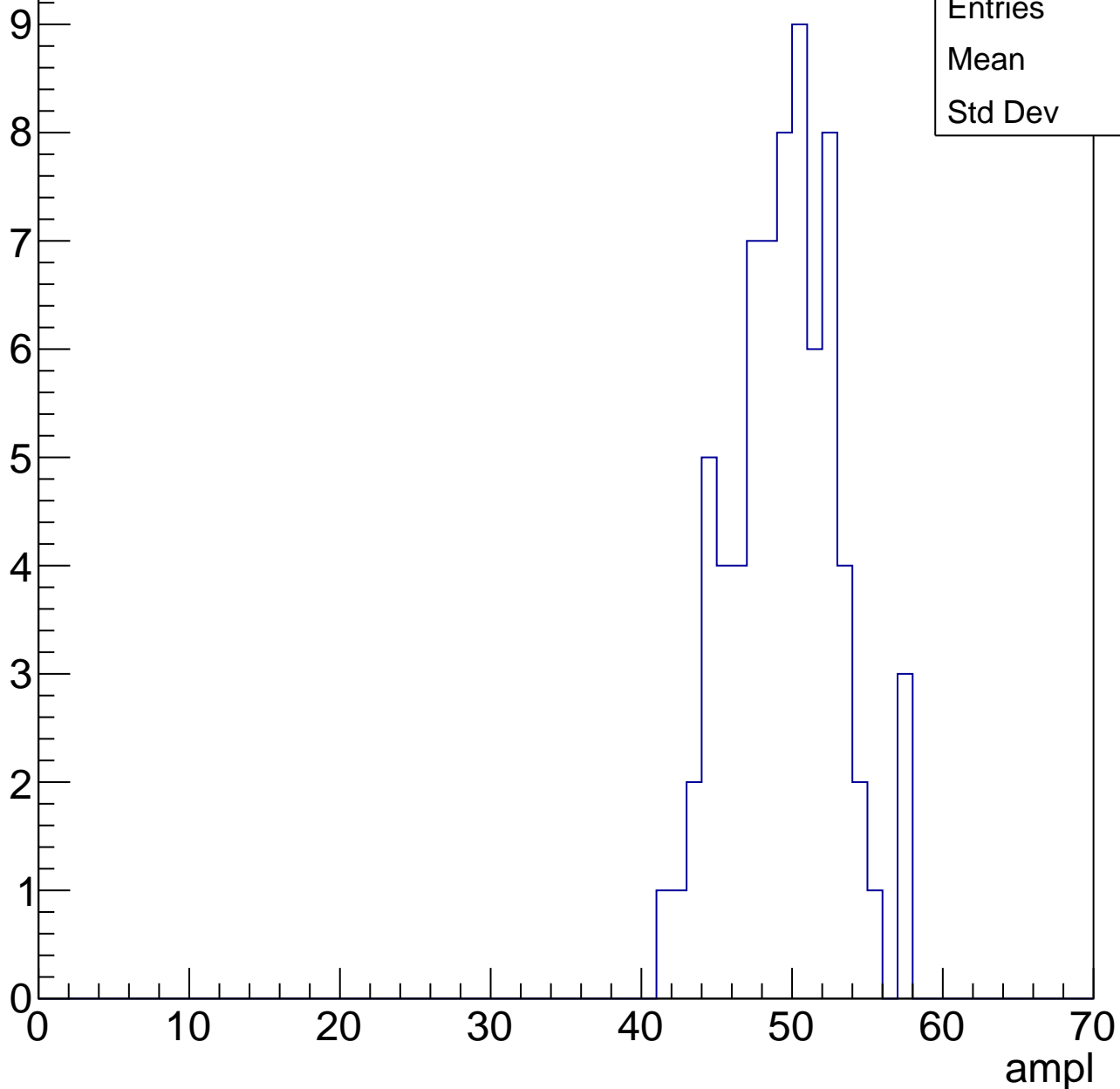


B1L100S, U15-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	49
Std Dev	3.52

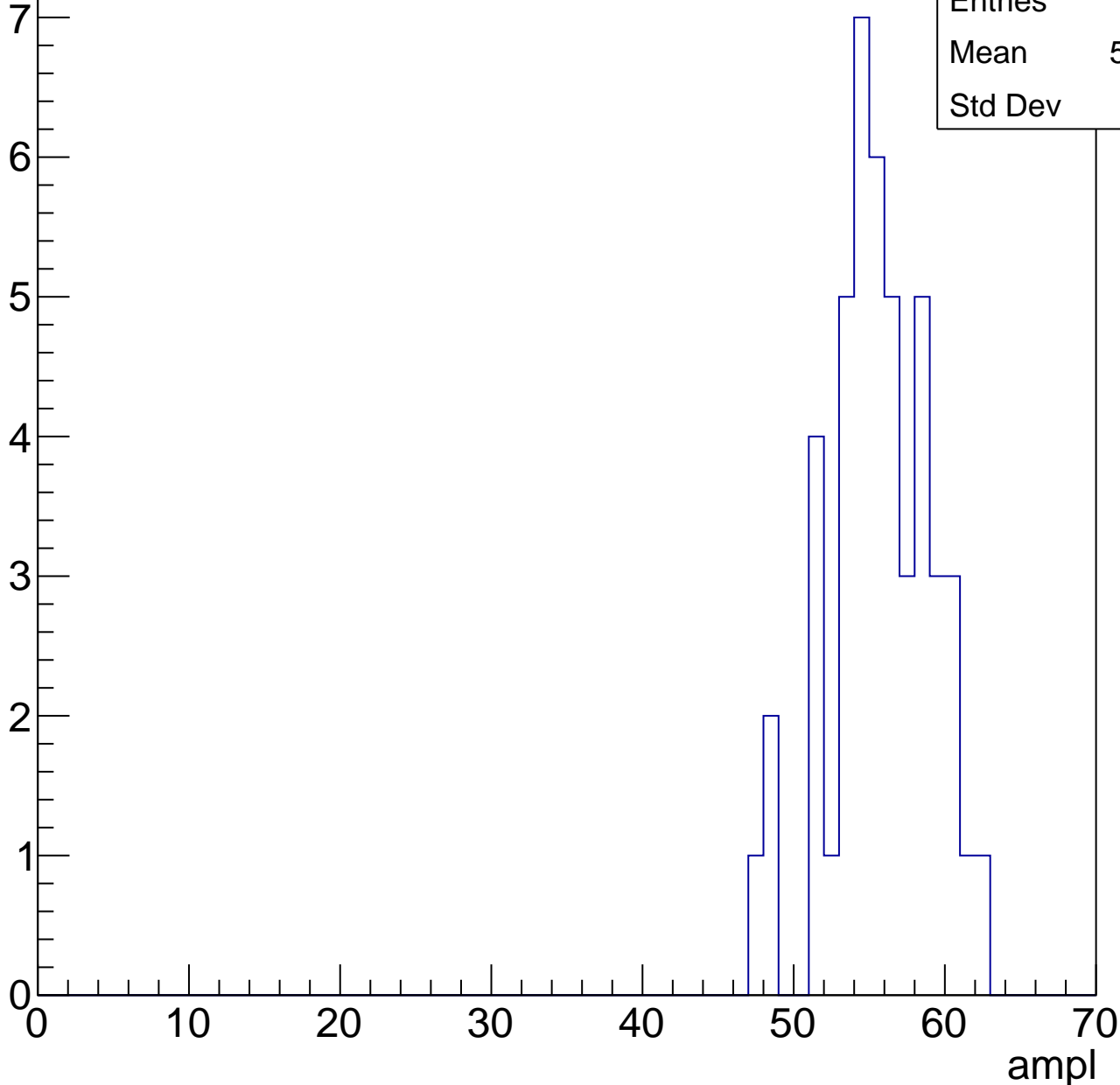


B1L100S, U15-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	55.17
Std Dev	3.36

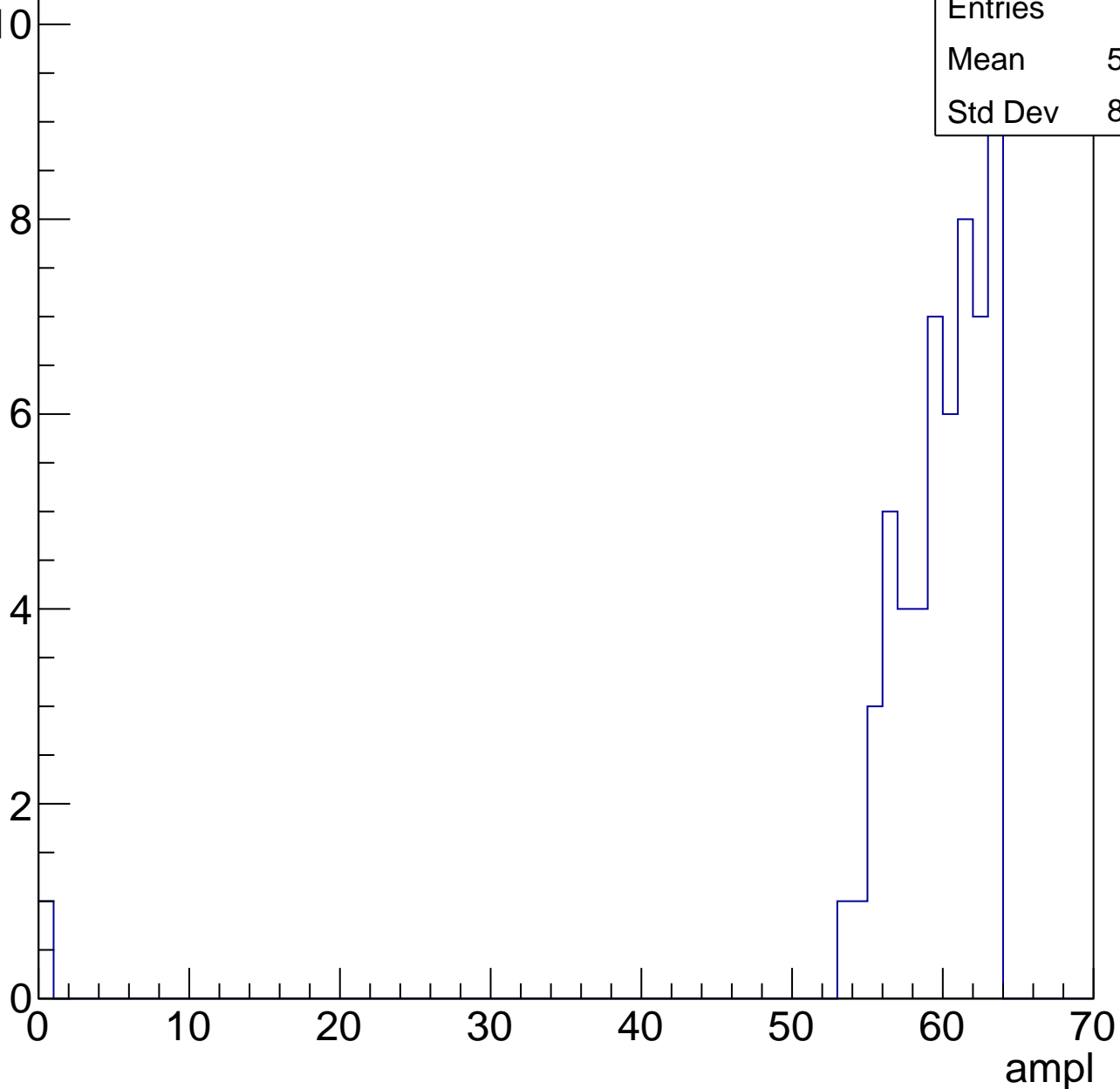


B1L100S, U15-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

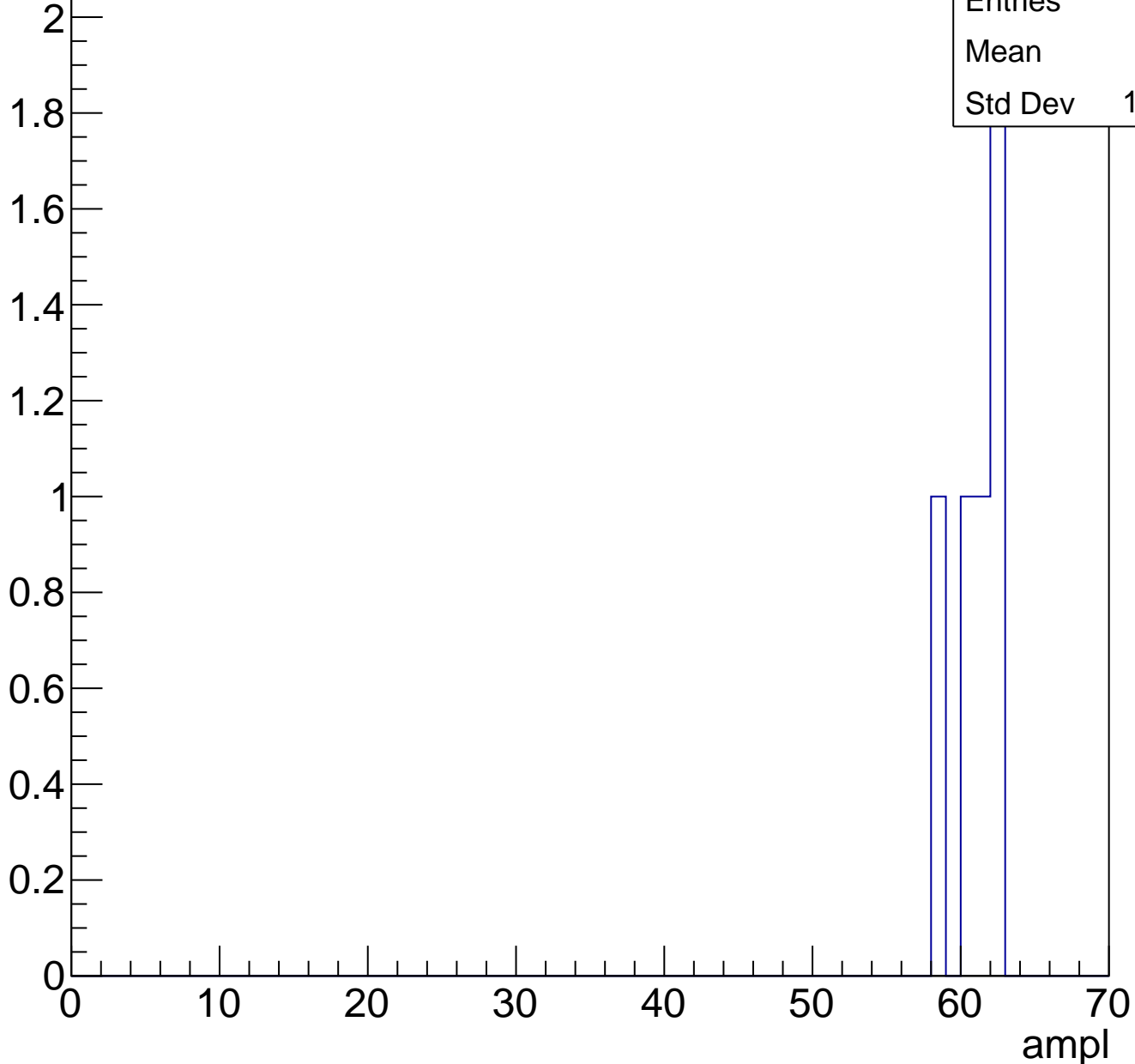
Entries	57
Mean	58.54
Std Dev	8.276



B1L100S, U15-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	60.6
Std Dev	1.497

B1L100S, U15-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch96, adc0

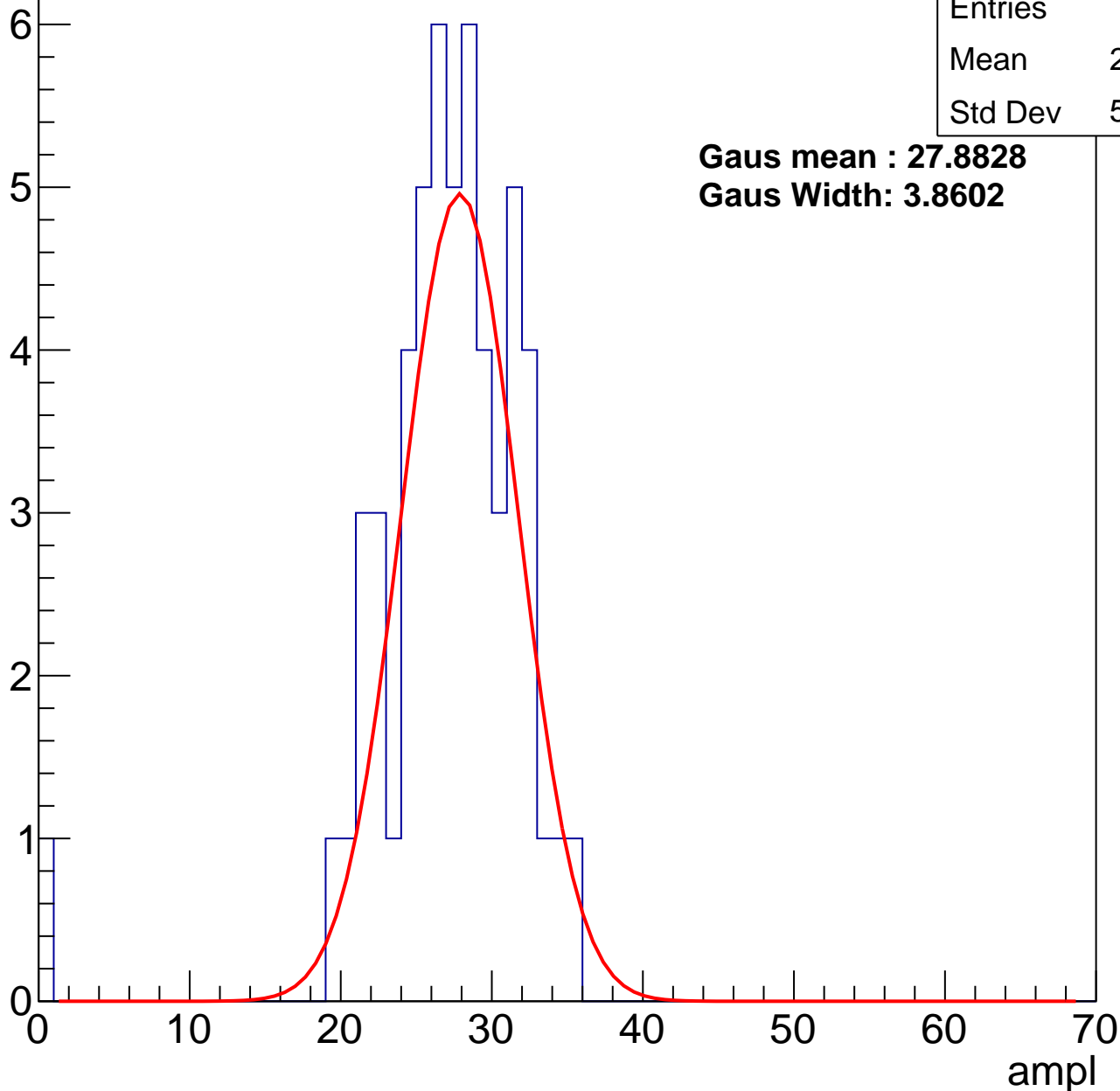
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	26.58
Std Dev	5.172

Gaus mean : 27.8828

Gaus Width: 3.8602



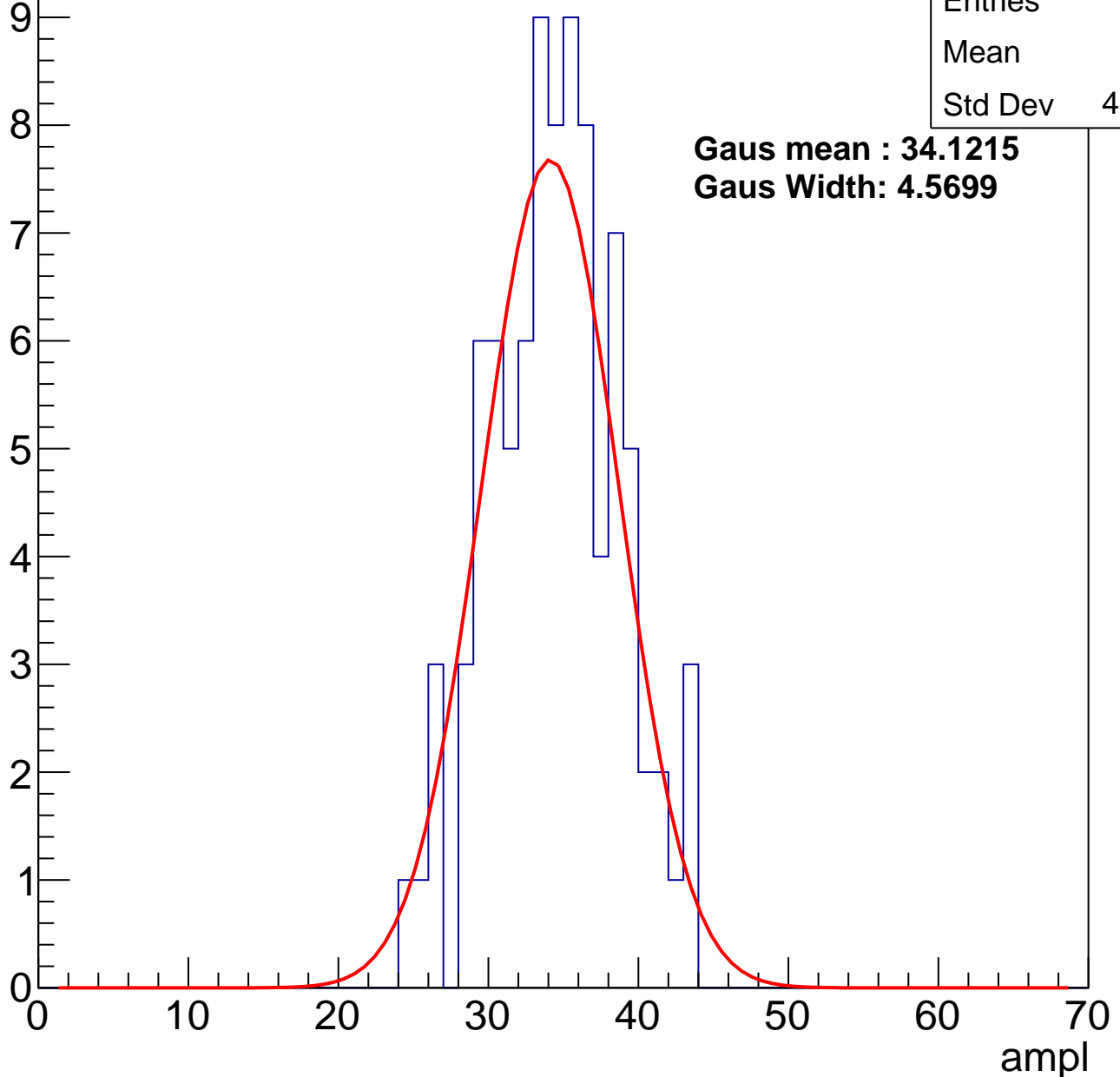
B1L100S, U15-ch96, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	89
Mean	34
Std Dev	4.235

Gaus mean : 34.1215
Gaus Width: 4.5699



B1L100S, U15-ch96, adc2

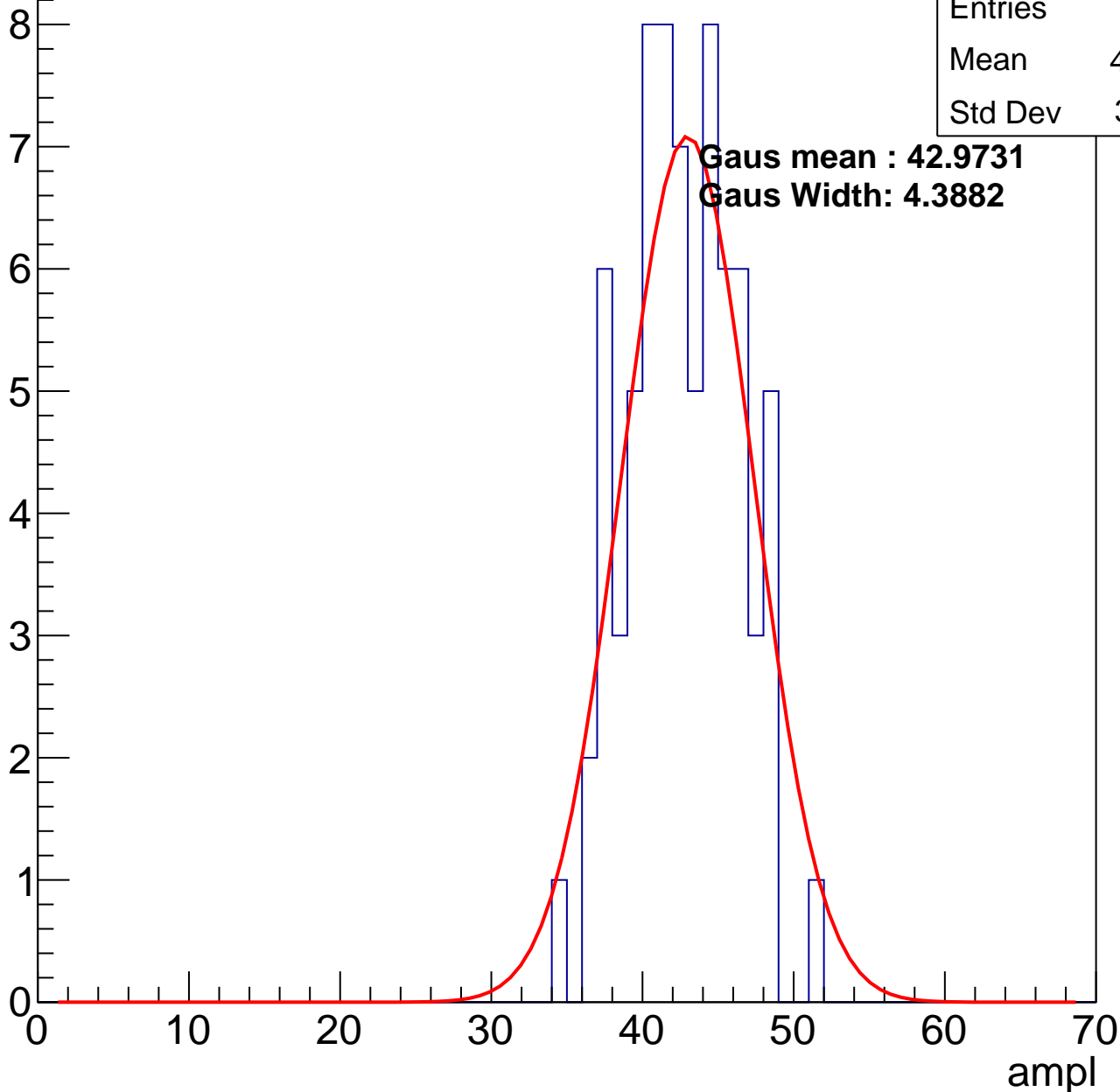
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	42.22
Std Dev	3.561

Gaus mean : 42.9731

Gaus Width: 4.3882

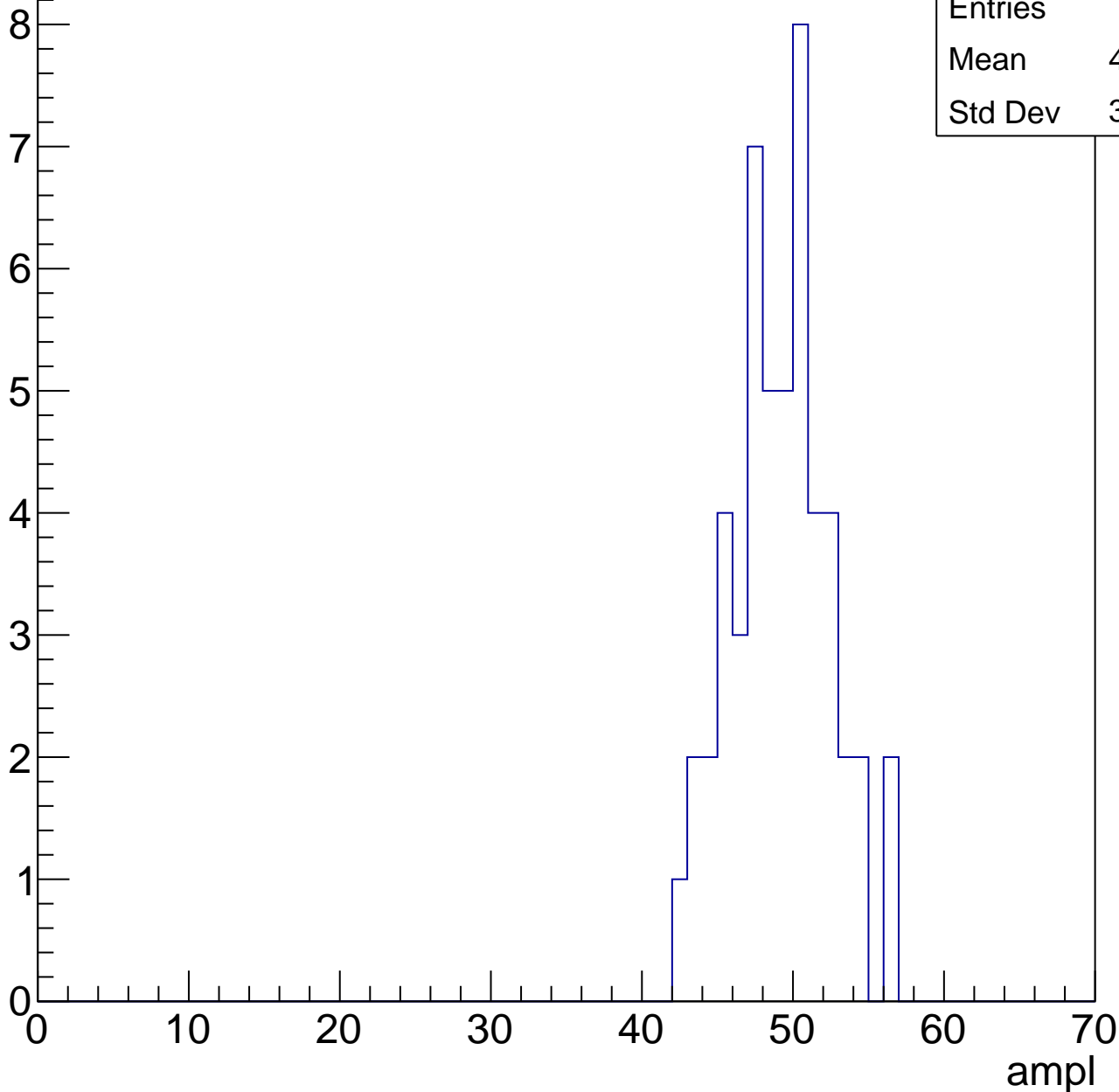


B1L100S, U15-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	48.75
Std Dev	3.229

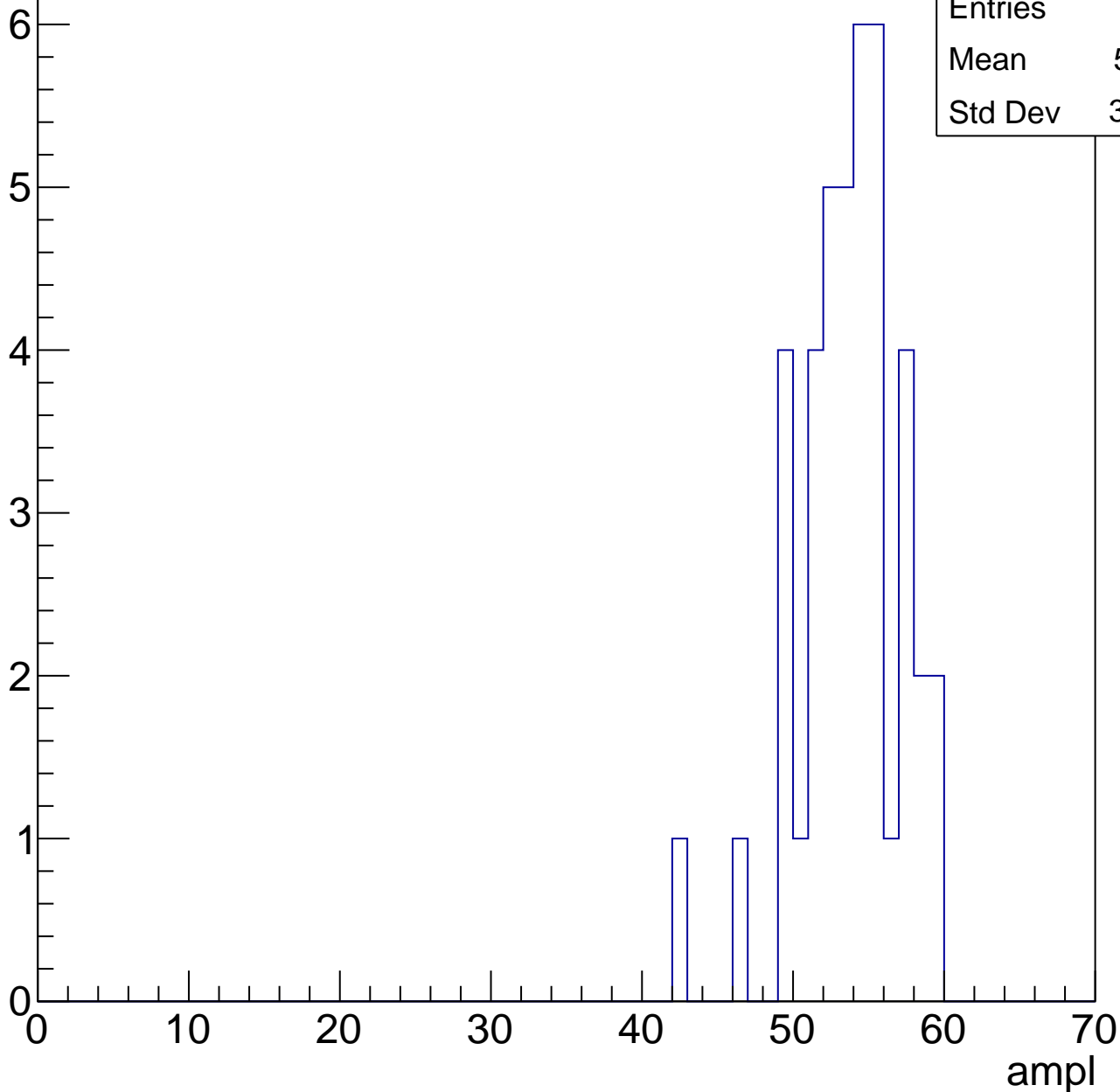


B1L100S, U15-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	53.21
Std Dev	3.405

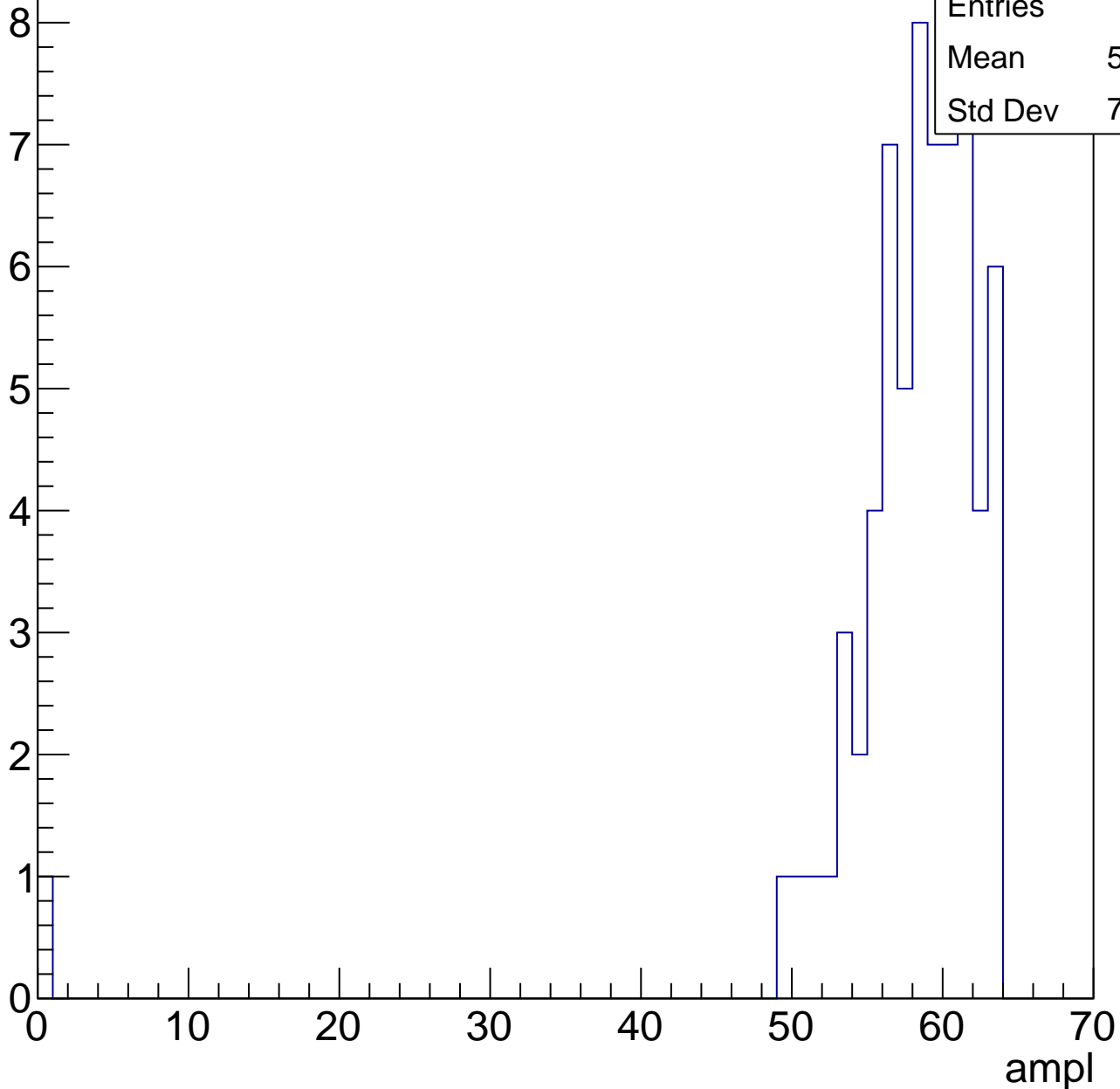


B1L100S, U15-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	57.23
Std Dev	7.835

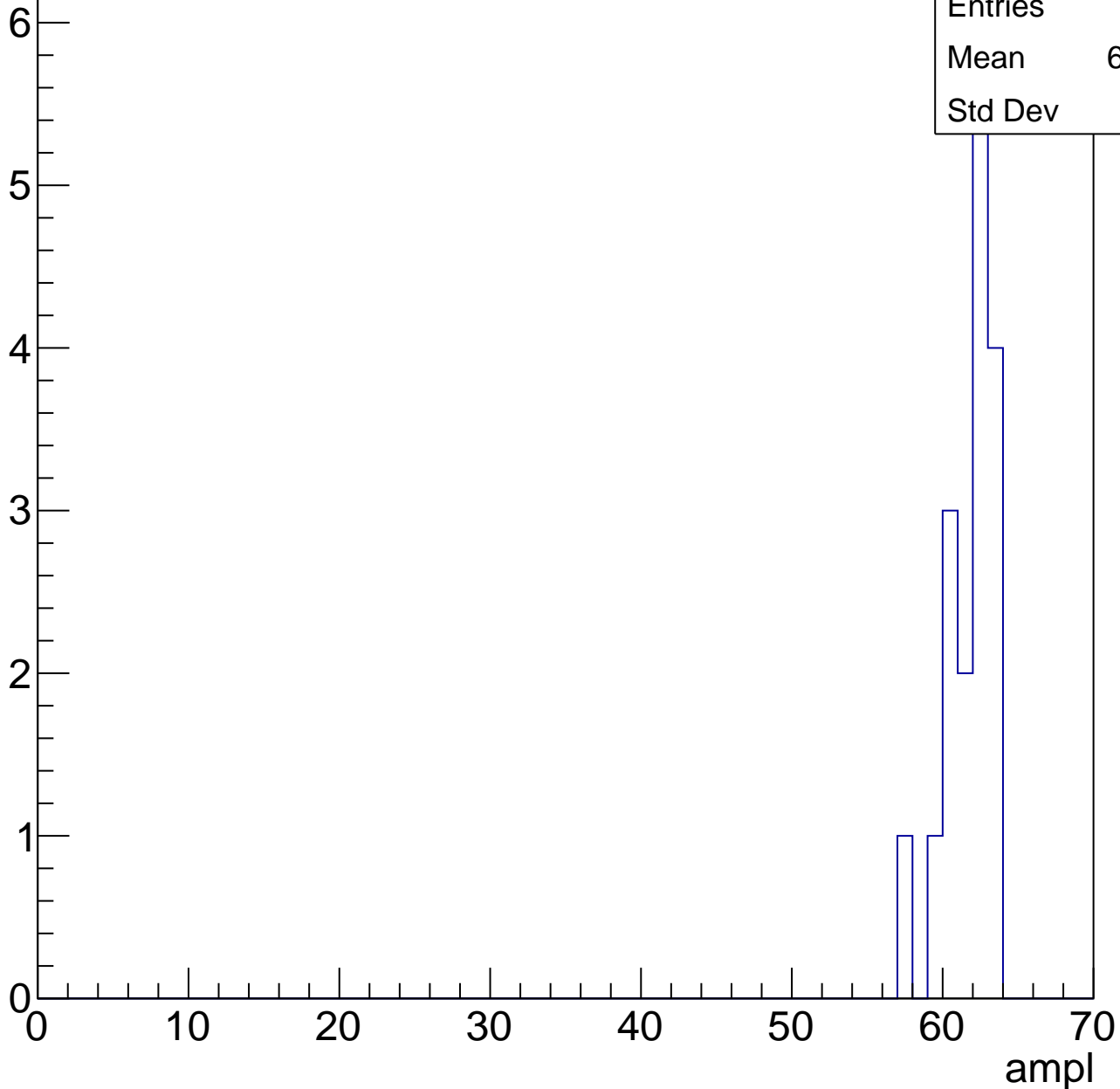


B1L100S, U15-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	61.29
Std Dev	1.6



B1L100S, U15-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch97, adc0

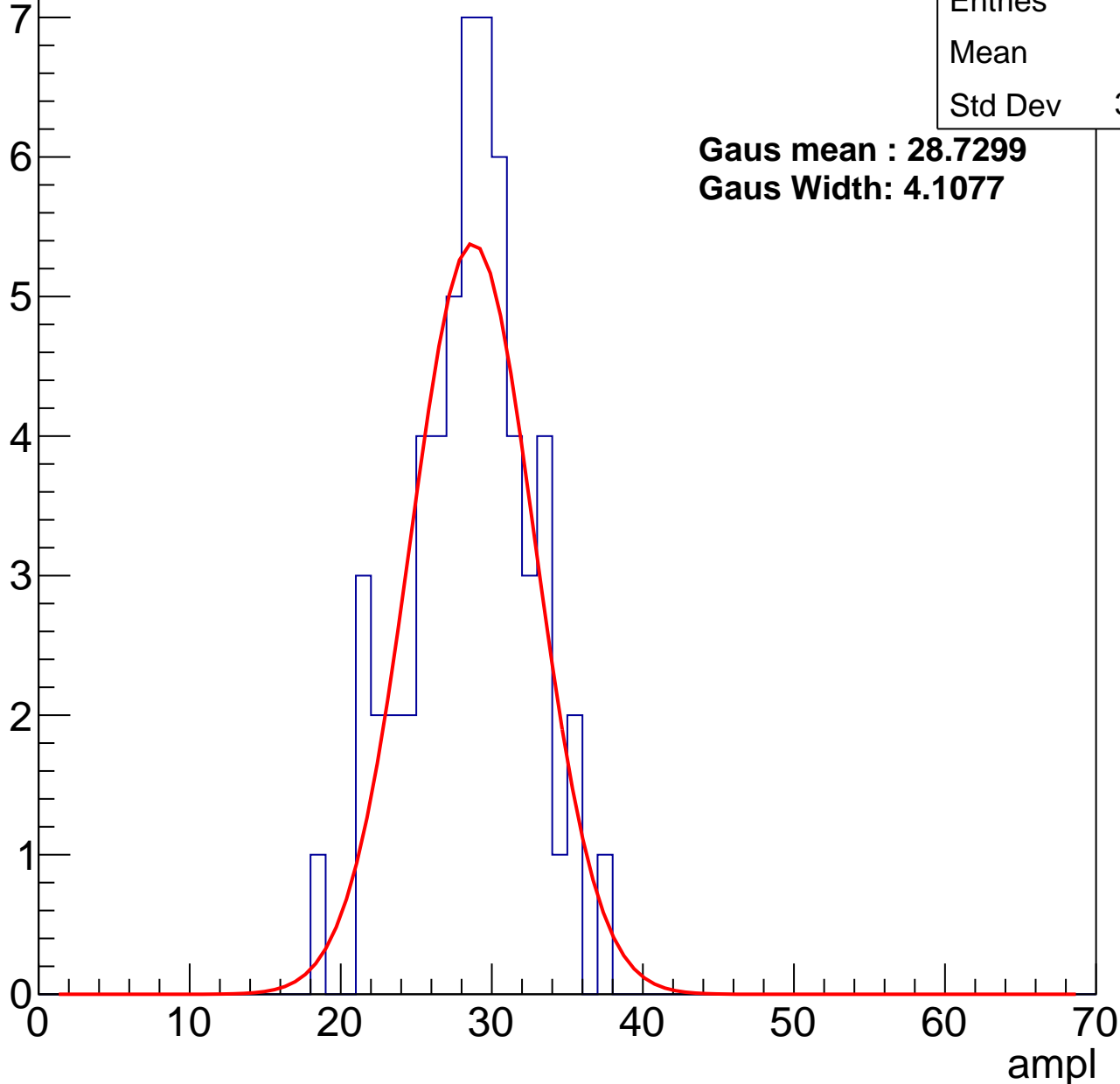
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	28.1
Std Dev	3.911

Gaus mean : 28.7299

Gaus Width: 4.1077



B1L100S, U15-ch97, adc1

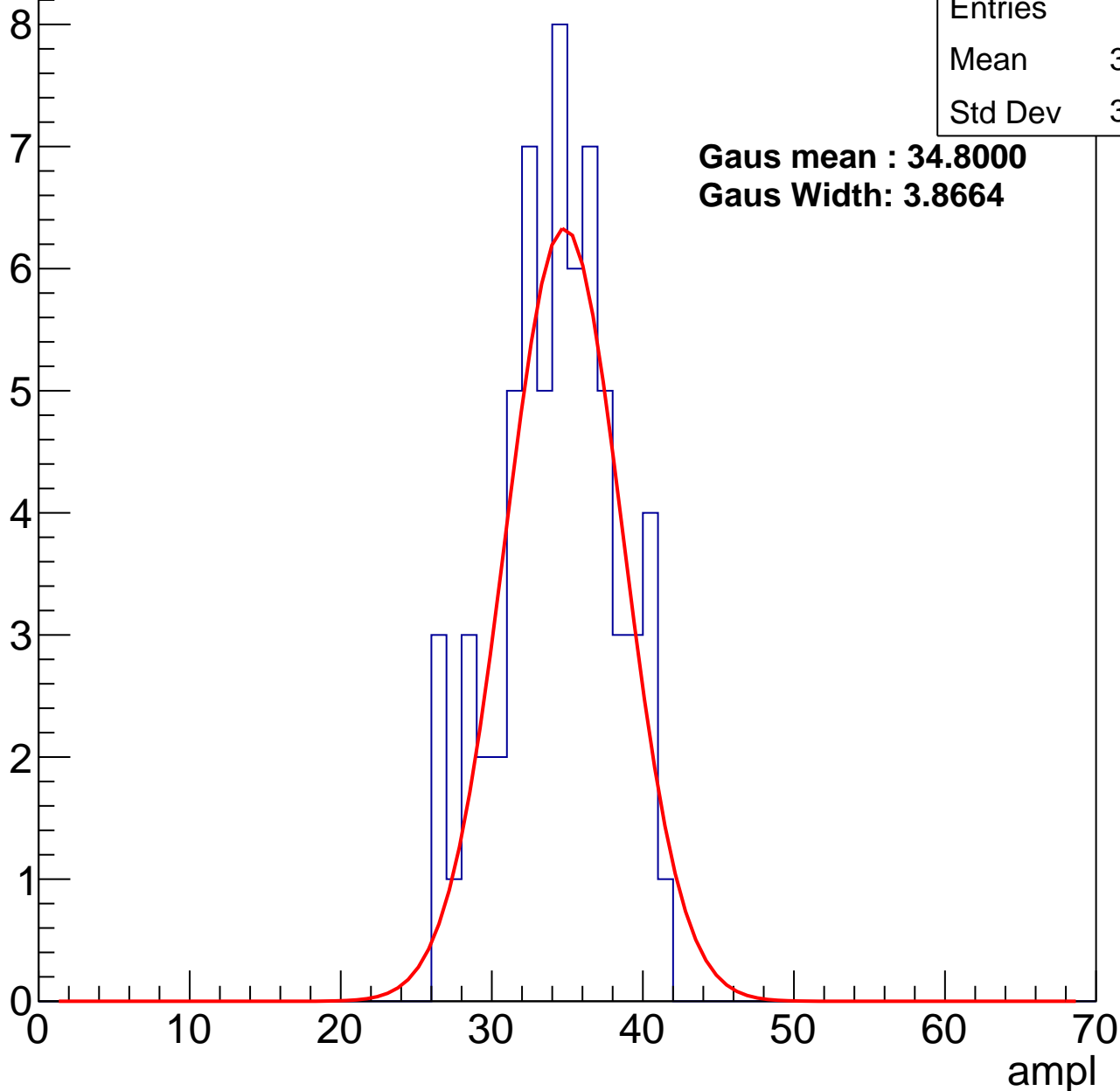
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	33.88
Std Dev	3.727

Gaus mean : 34.8000

Gaus Width: 3.8664



B1L100S, U15-ch97, adc2

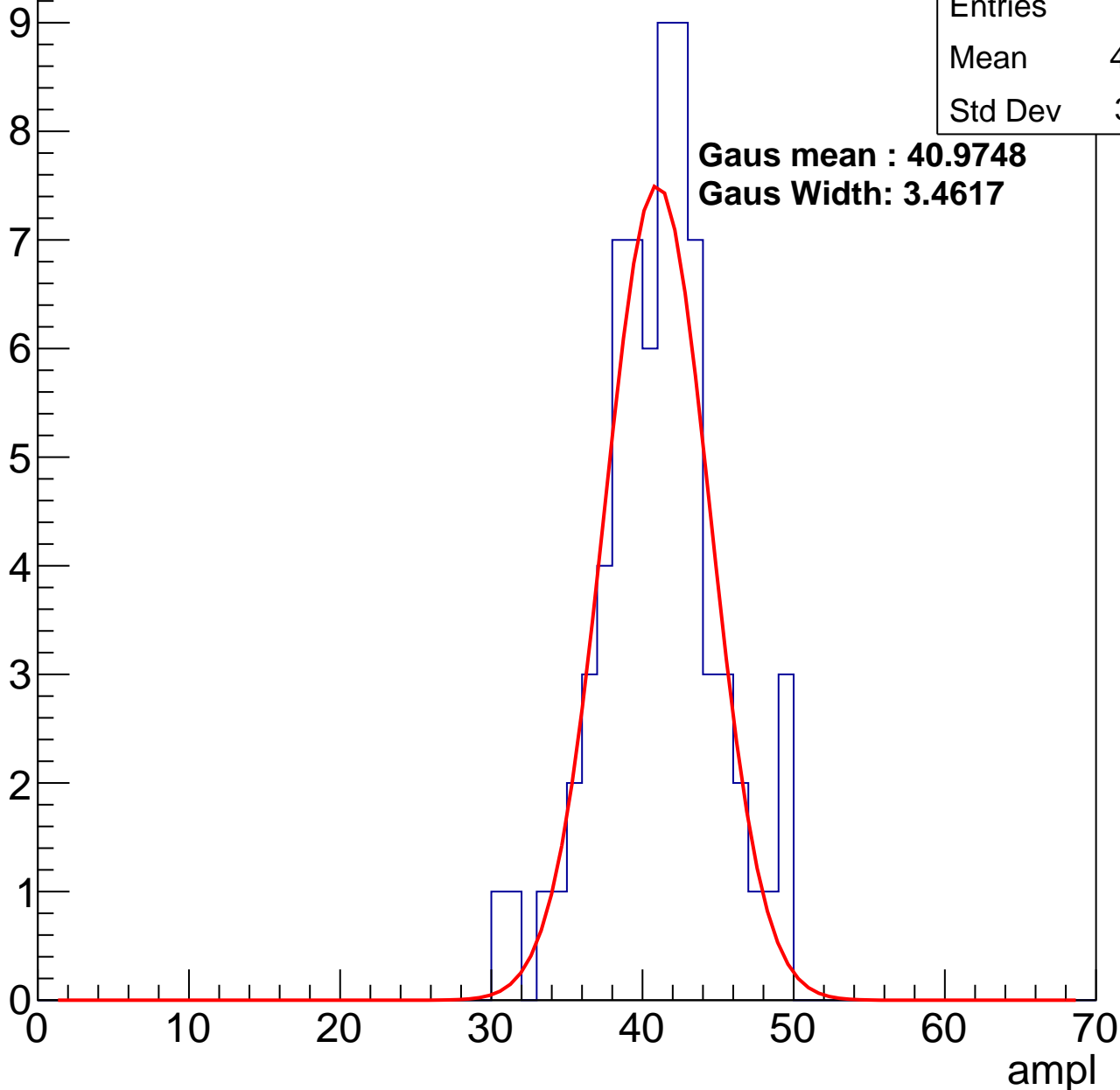
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	40.59
Std Dev	3.881

Gaus mean : 40.9748

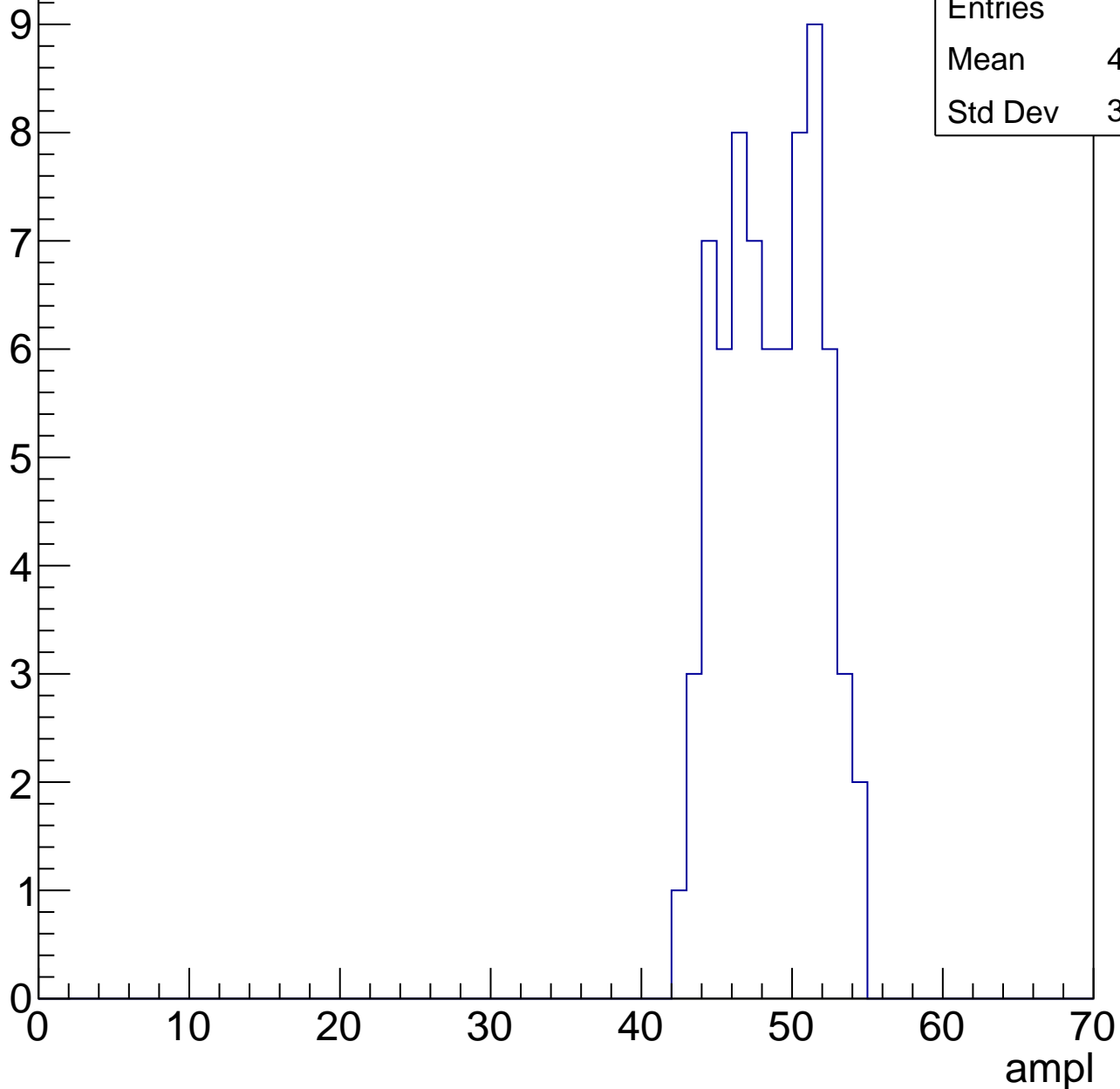
Gaus Width: 3.4617



B1L100S, U15-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

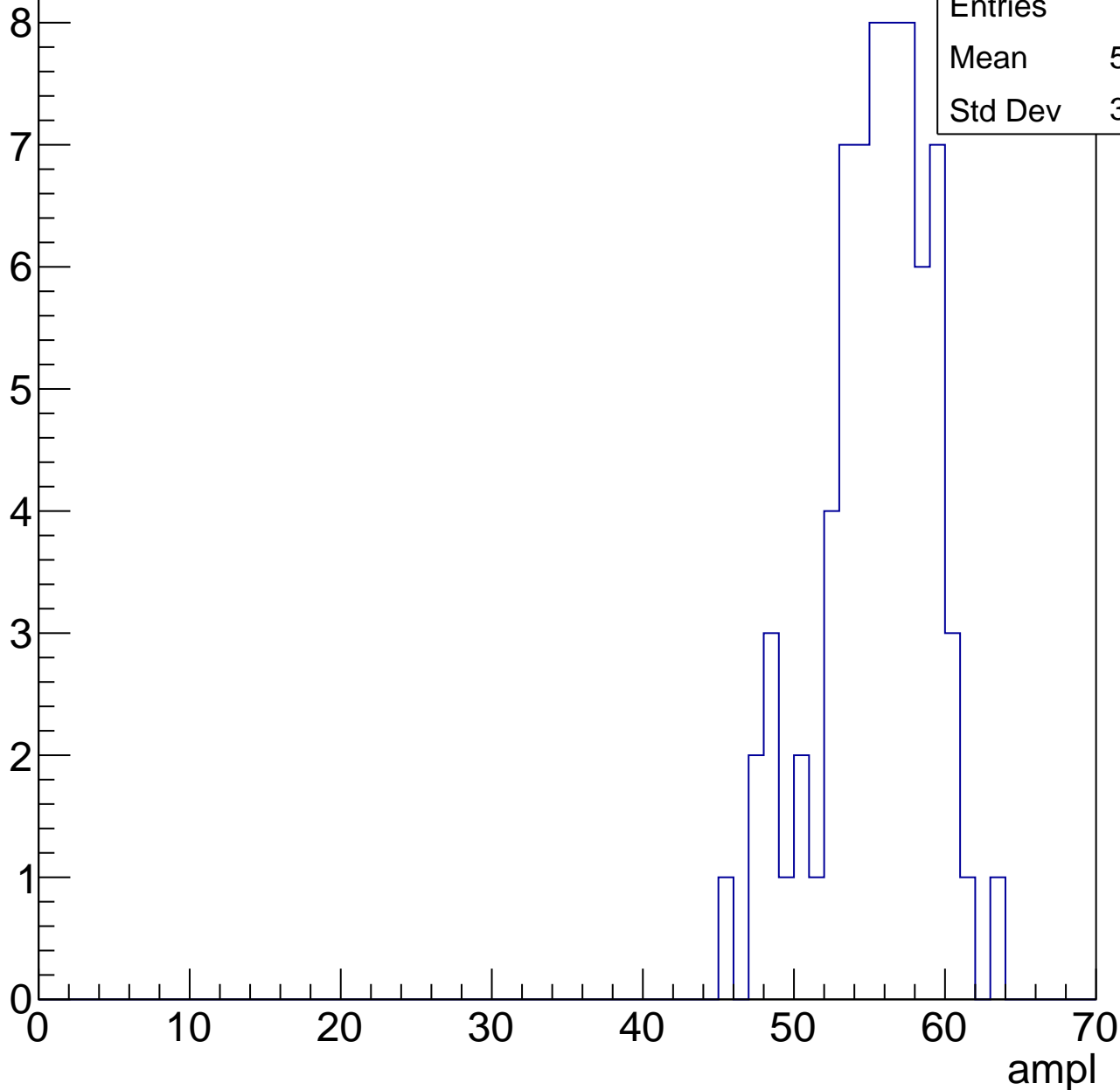


Entries	72
Mean	48.14
Std Dev	3.066

B1L100S, U15-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

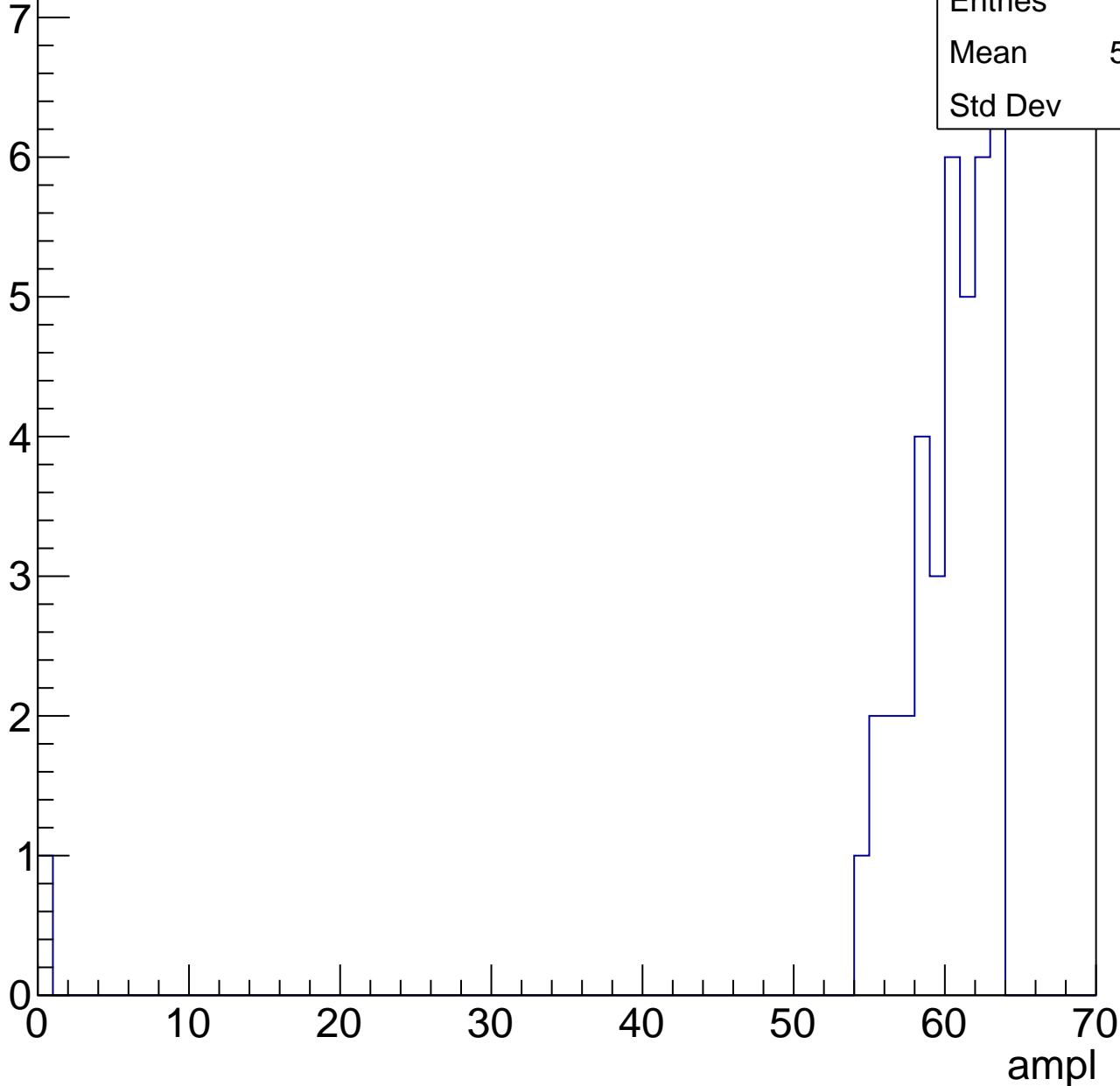


B1L100S, U15-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

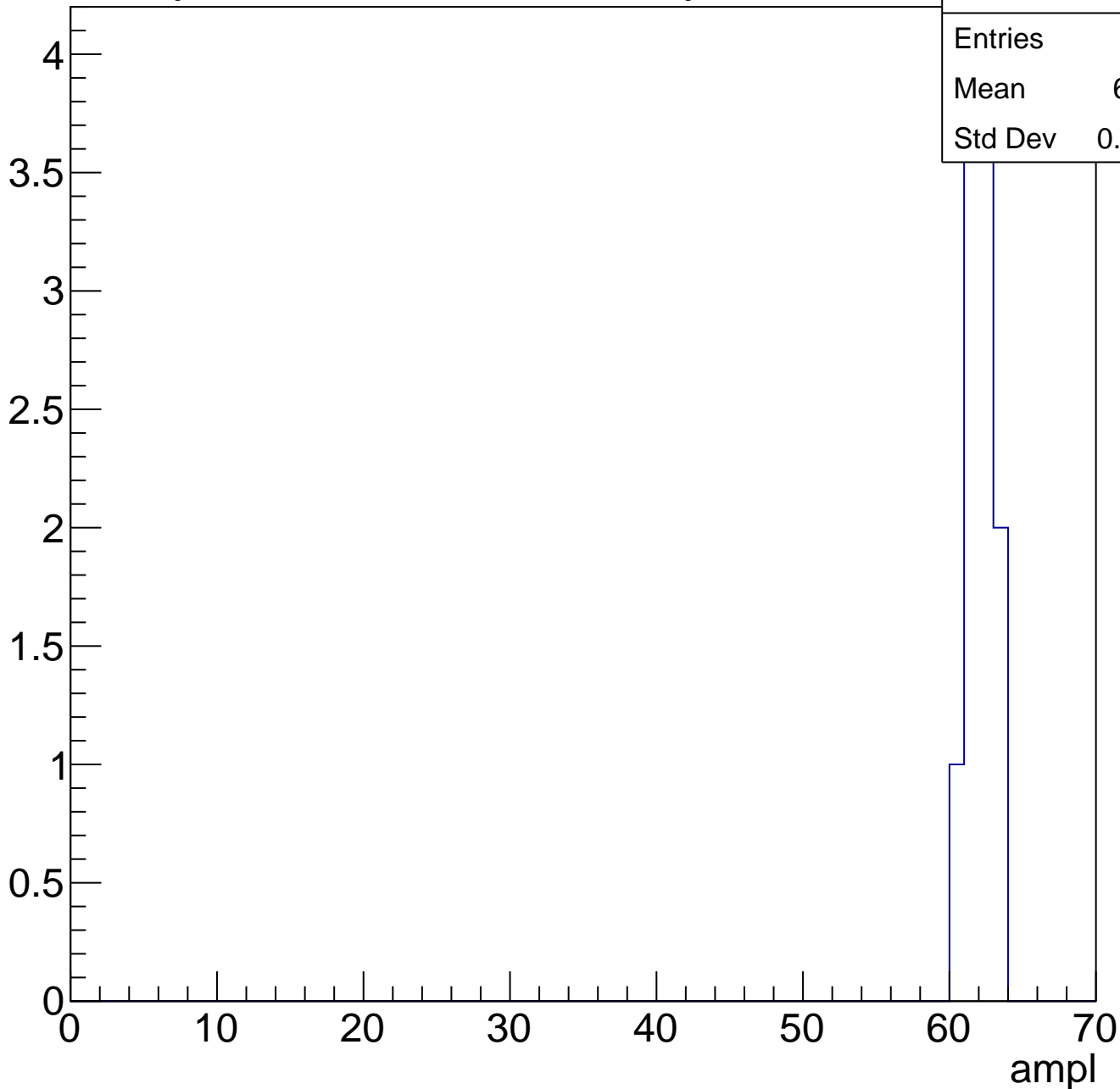
Entries	39
Mean	58.38
Std Dev	9.8



B1L100S, U15-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch98, adc0

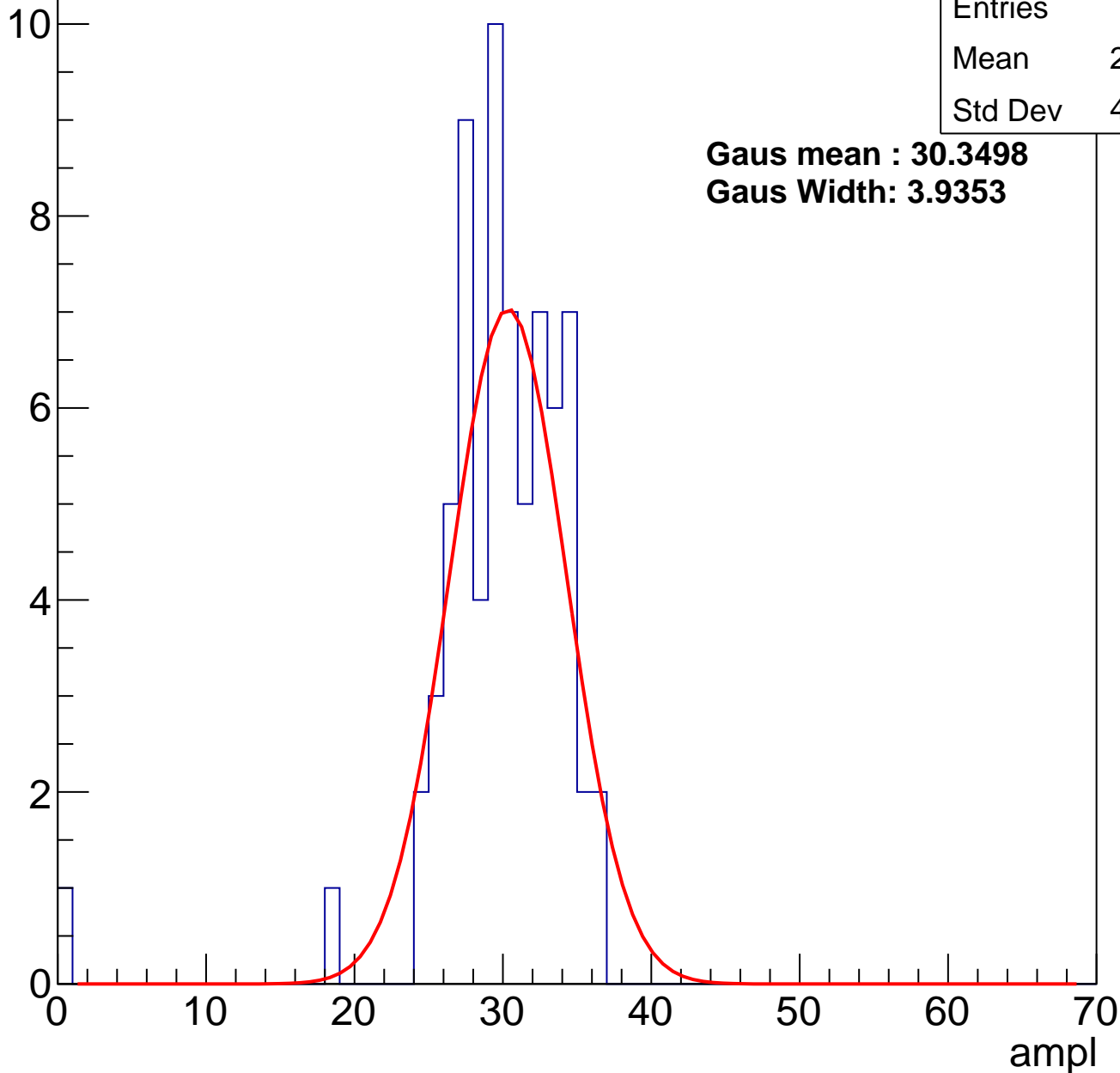
calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	29.34
Std Dev	4.847

Gaus mean : 30.3498

Gaus Width: 3.9353

Entry



B1L100S, U15-ch98, adc1

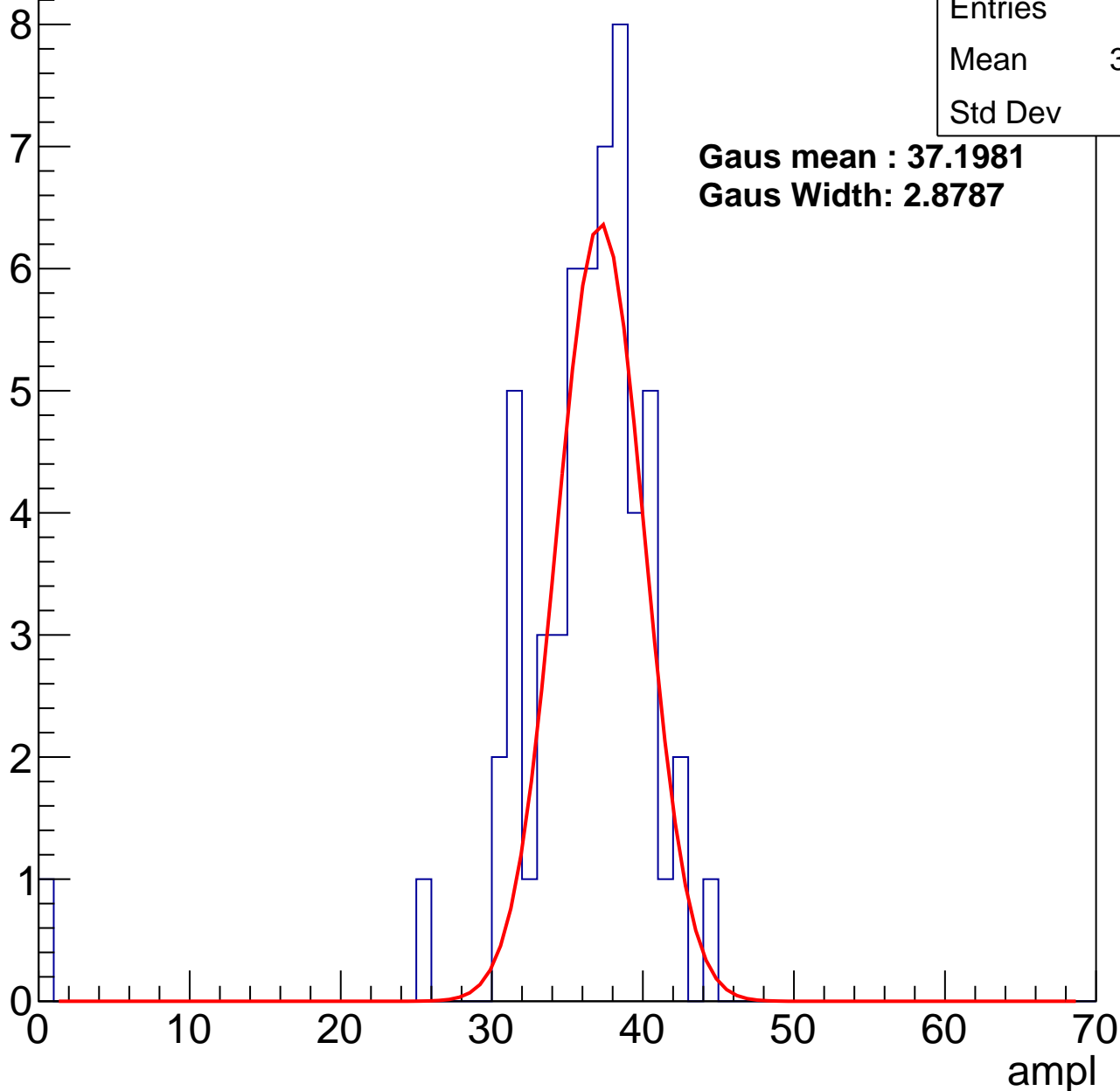
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	35.48
Std Dev	5.94

Gaus mean : 37.1981

Gaus Width: 2.8787



B1L100S, U15-ch98, adc2

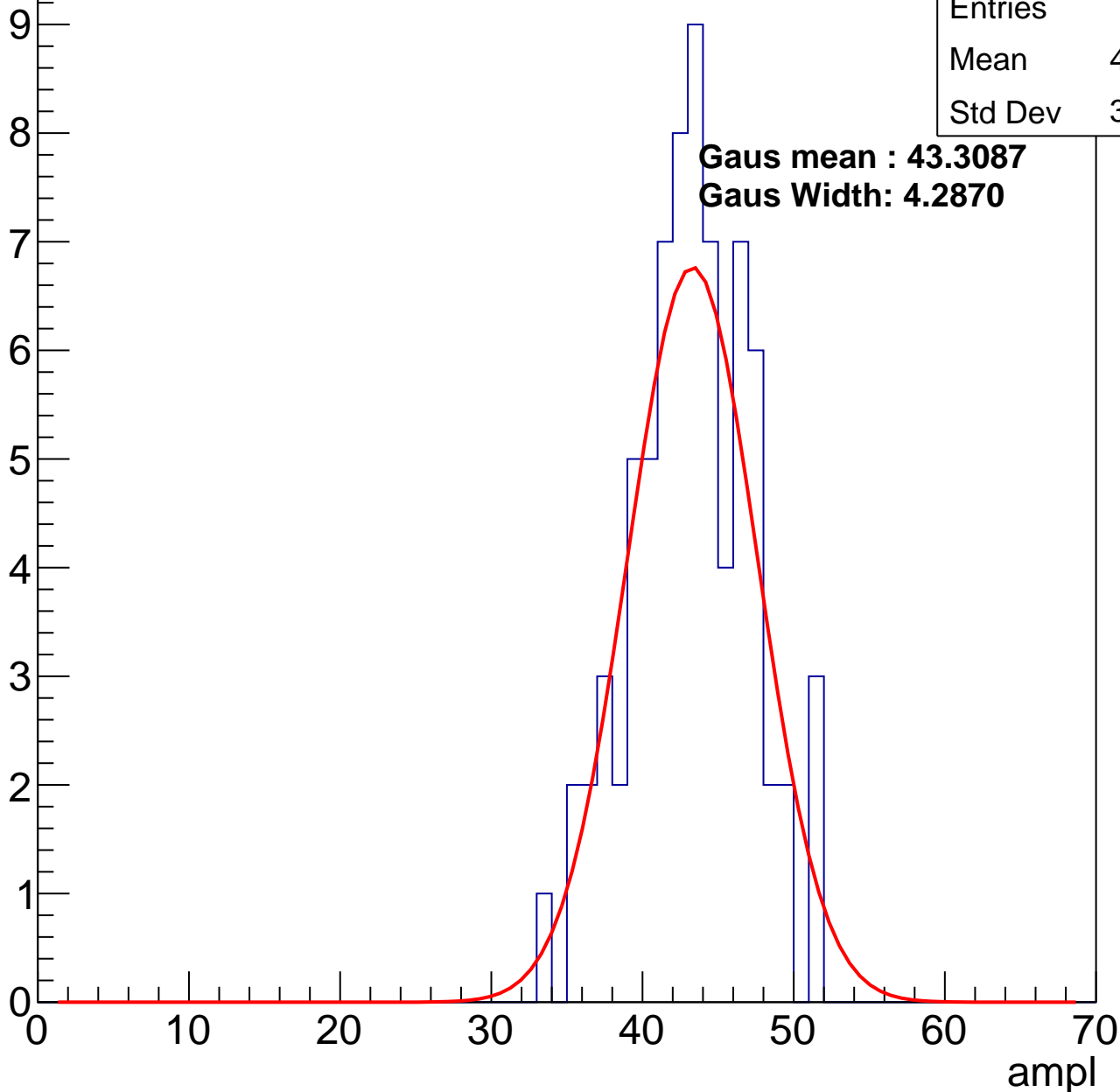
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	42.75
Std Dev	3.892

Gaus mean : 43.3087

Gaus Width: 4.2870

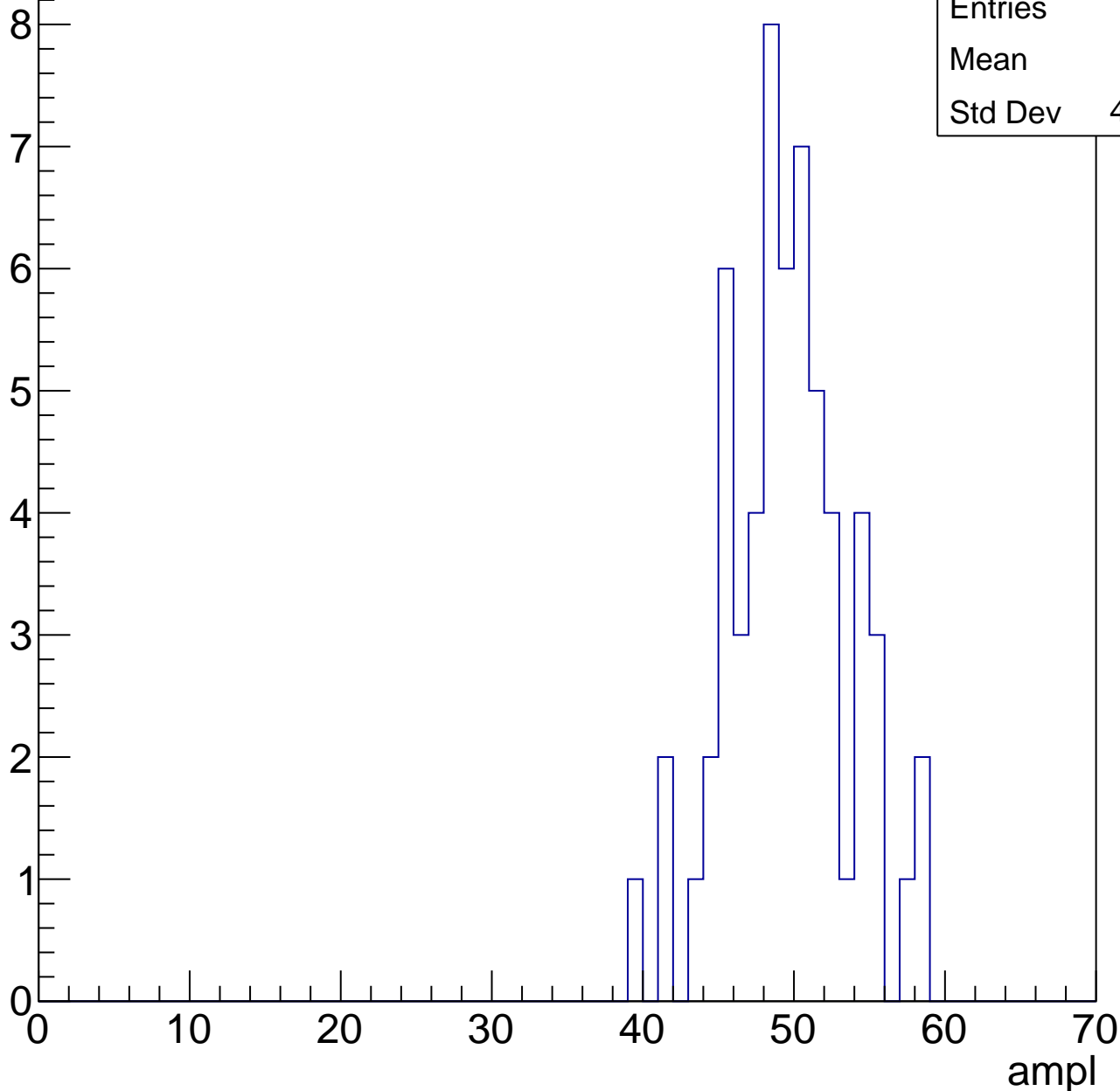


B1L100S, U15-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	49.1
Std Dev	4.028

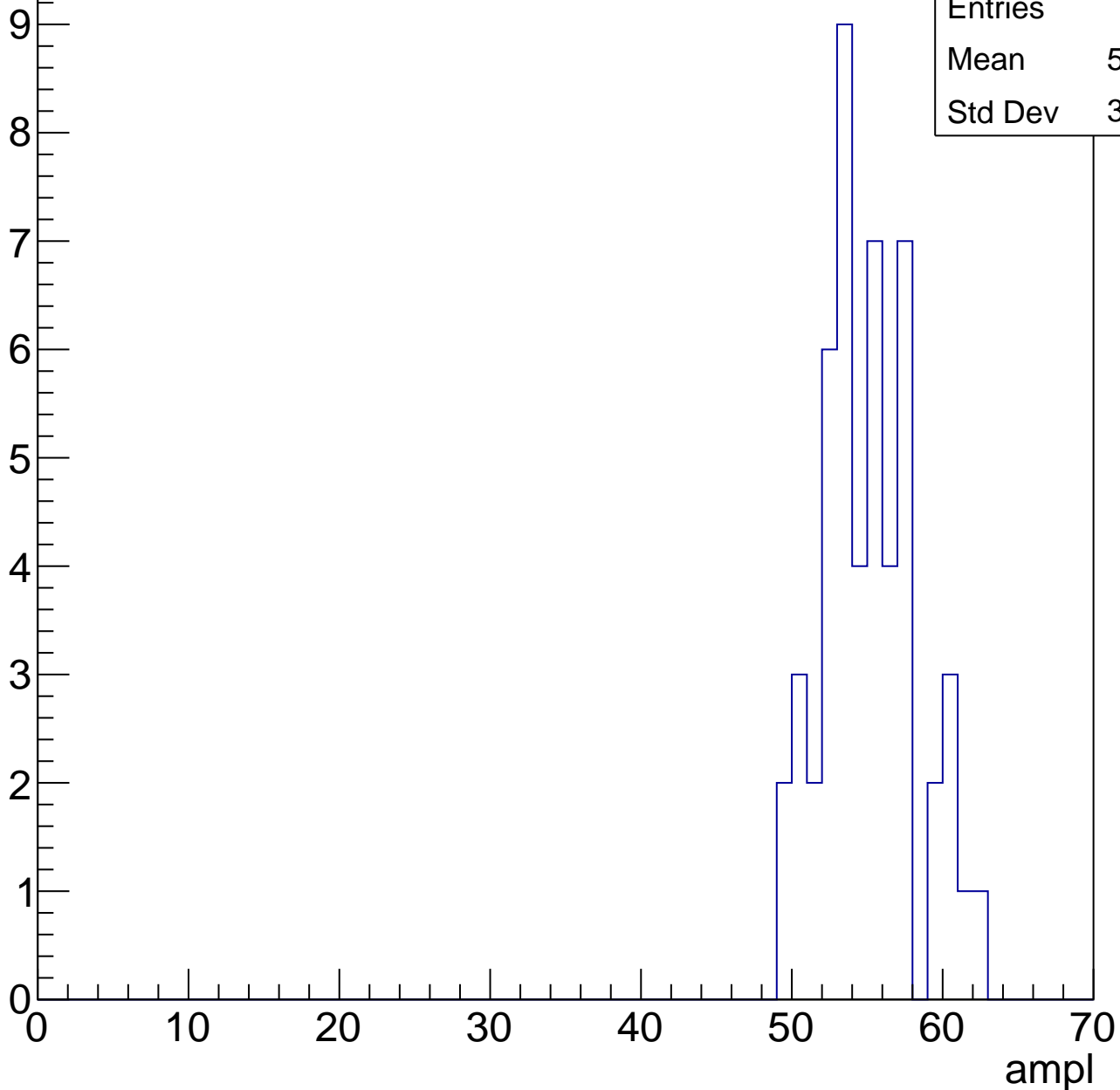


B1L100S, U15-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

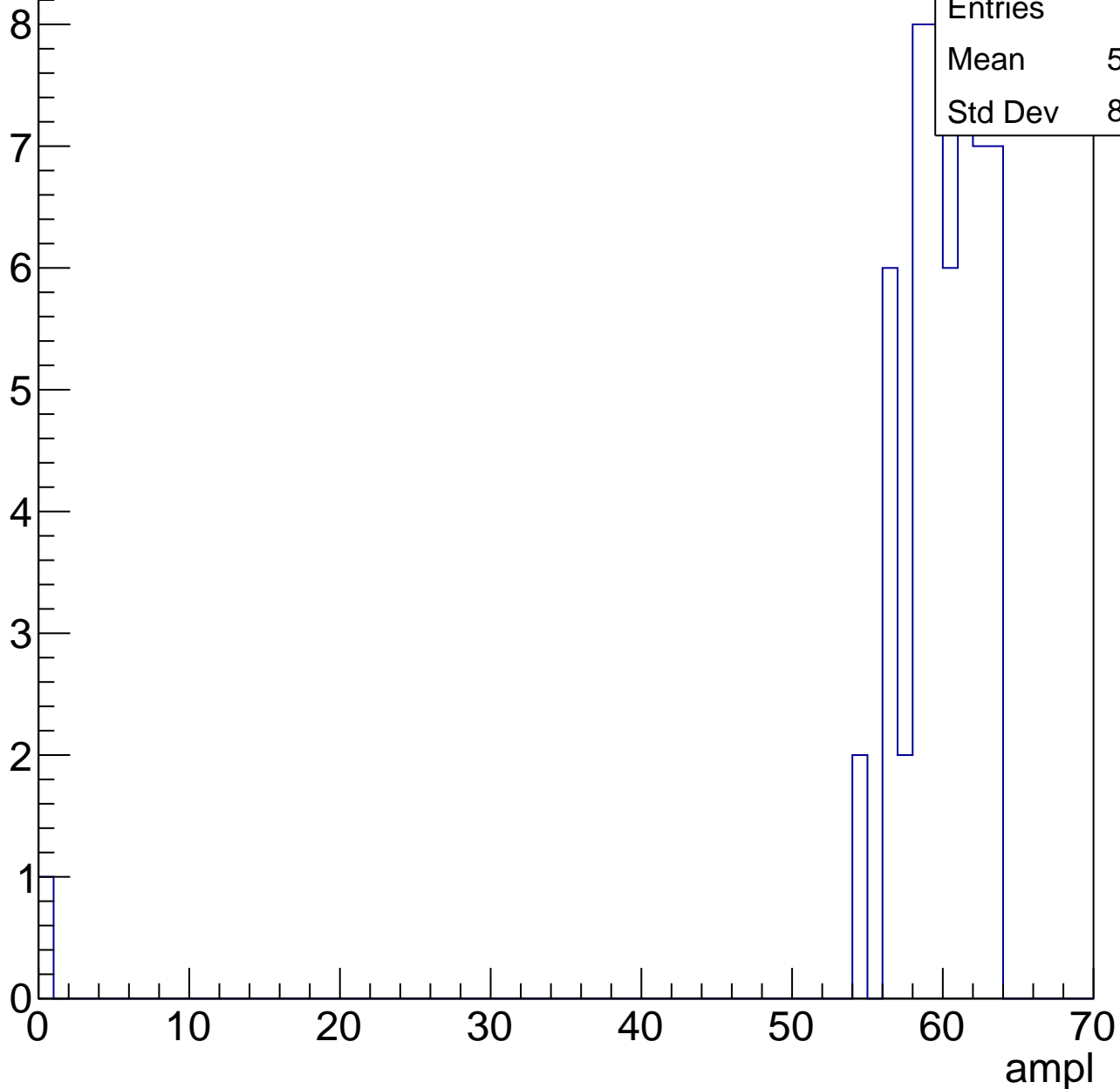
Entries	51
Mean	54.59
Std Dev	3.094



B1L100S, U15-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch99, adc0

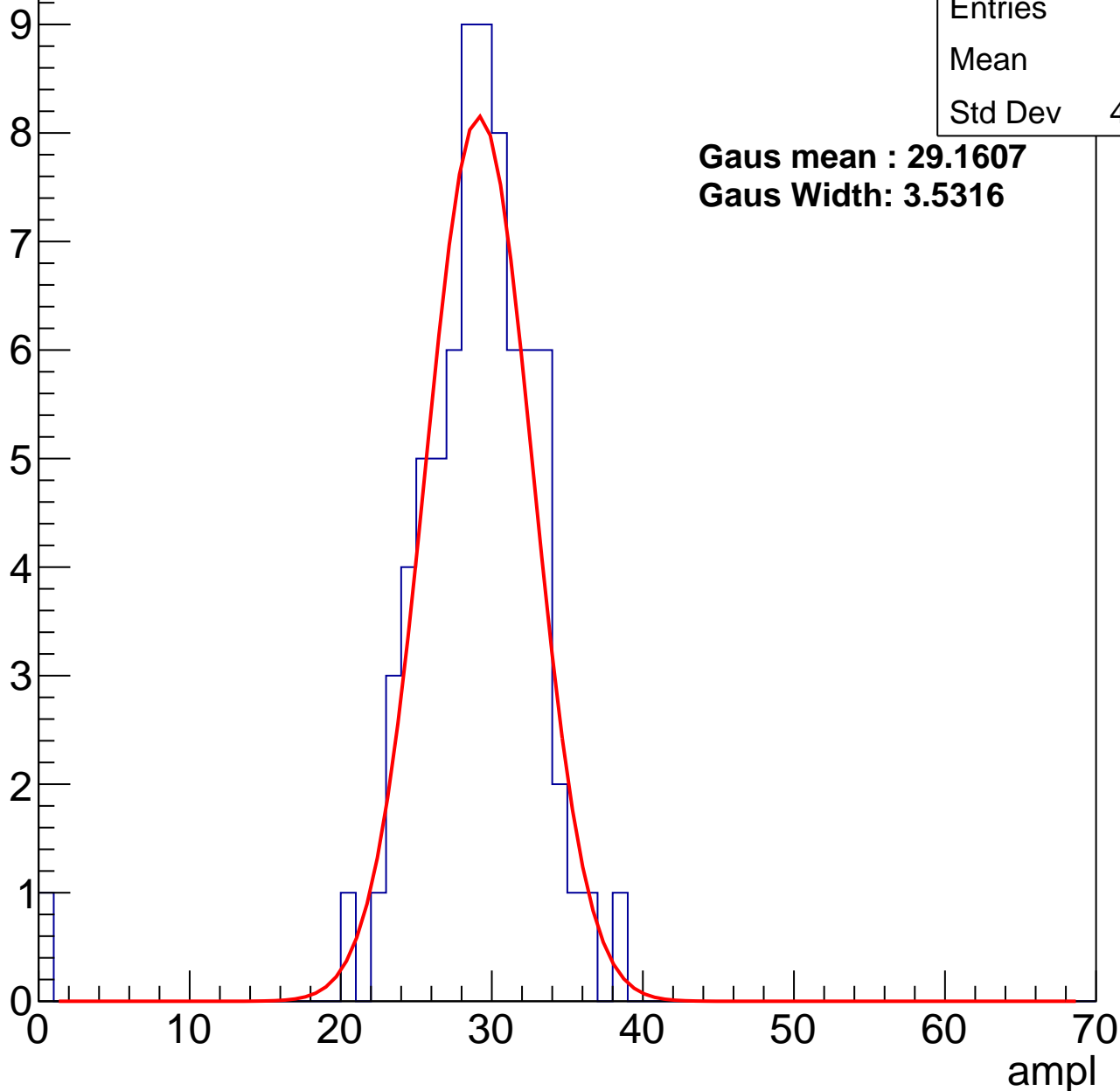
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	28.4
Std Dev	4.772

Gaus mean : 29.1607

Gaus Width: 3.5316



B1L100S, U15-ch99, adc1

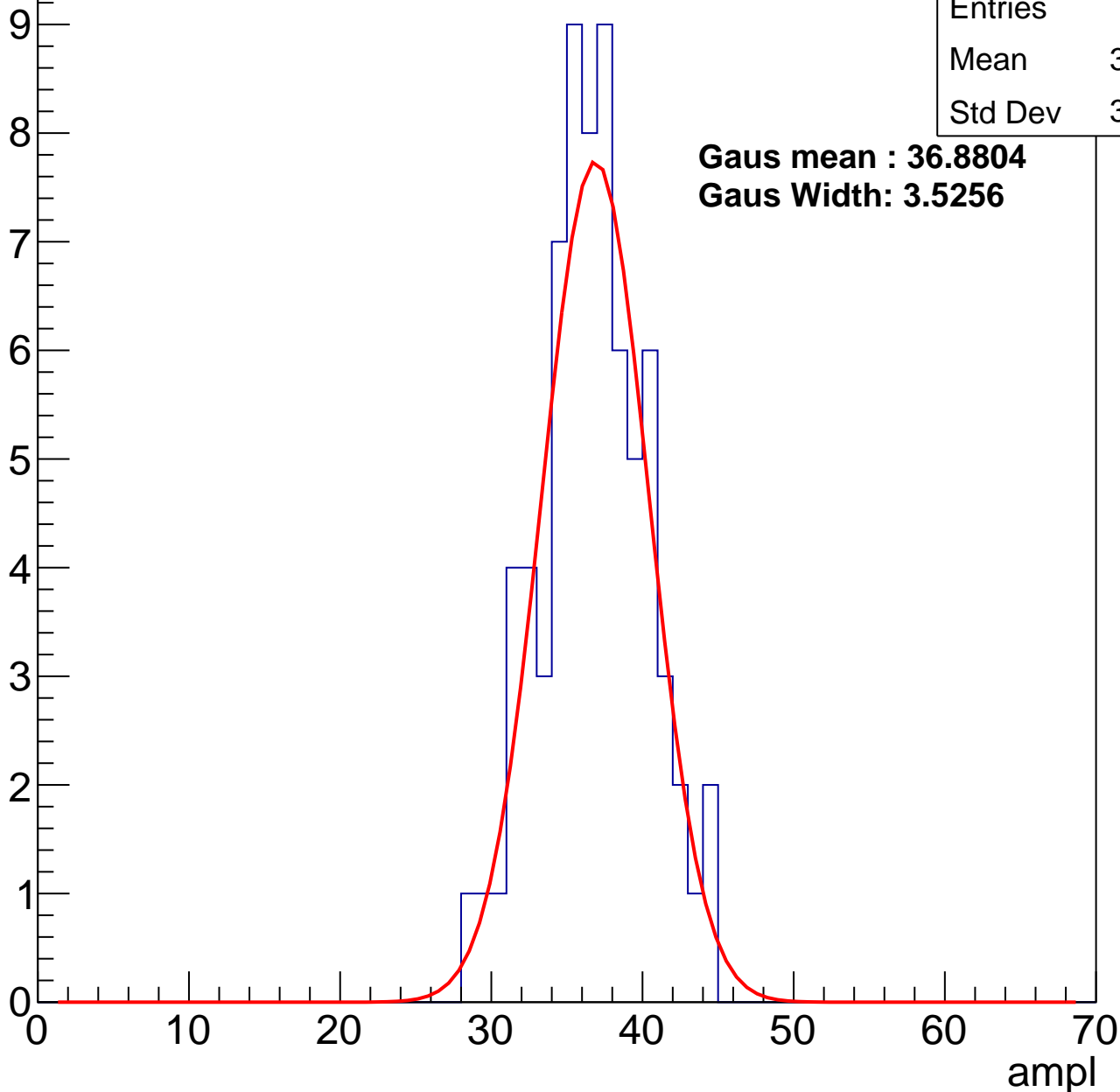
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	36.29
Std Dev	3.474

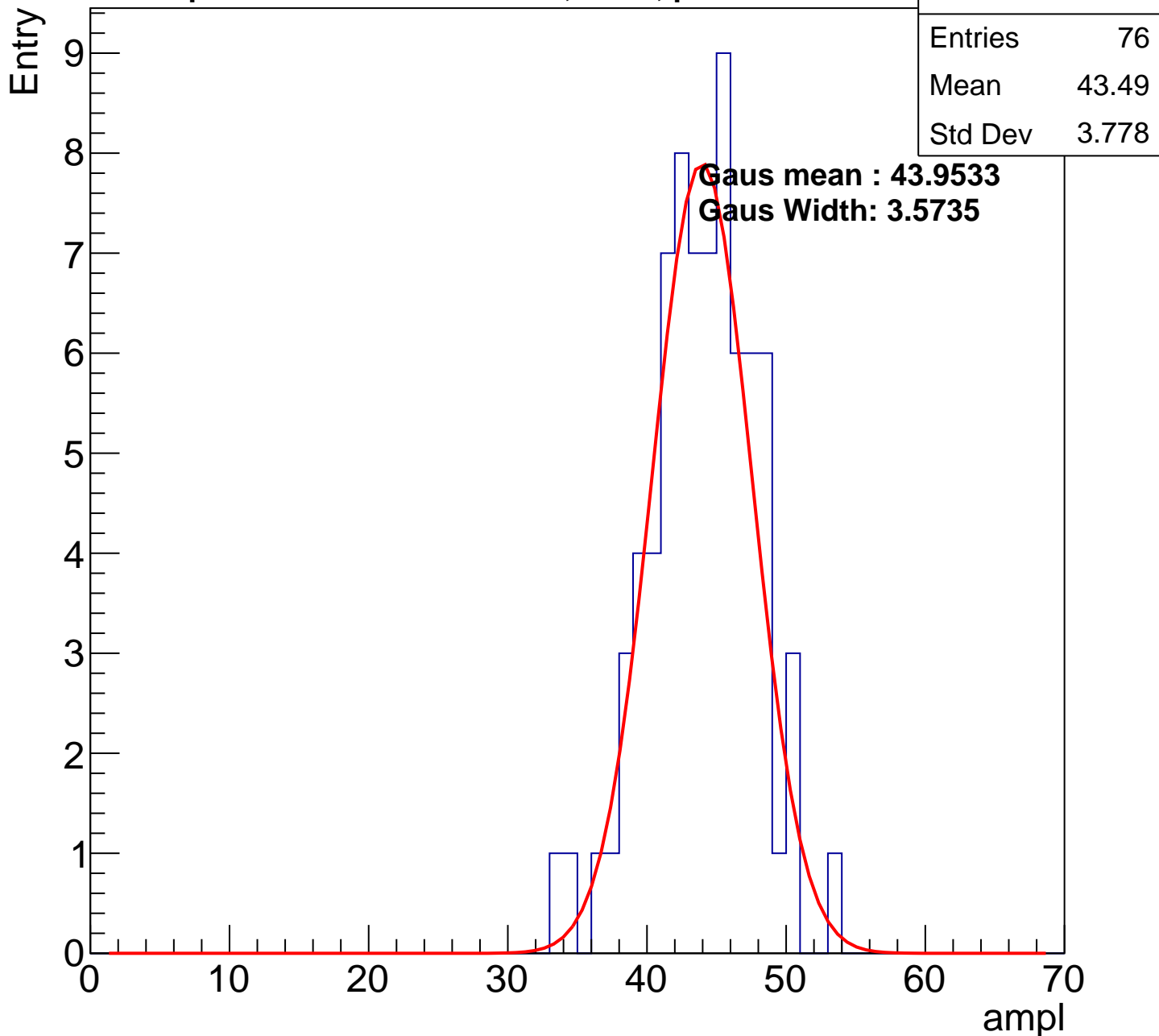
Gaus mean : 36.8804

Gaus Width: 3.5256



B1L100S, U15-ch99, adc2

calib_packv5_042523_0143.root, FC#4, port A2

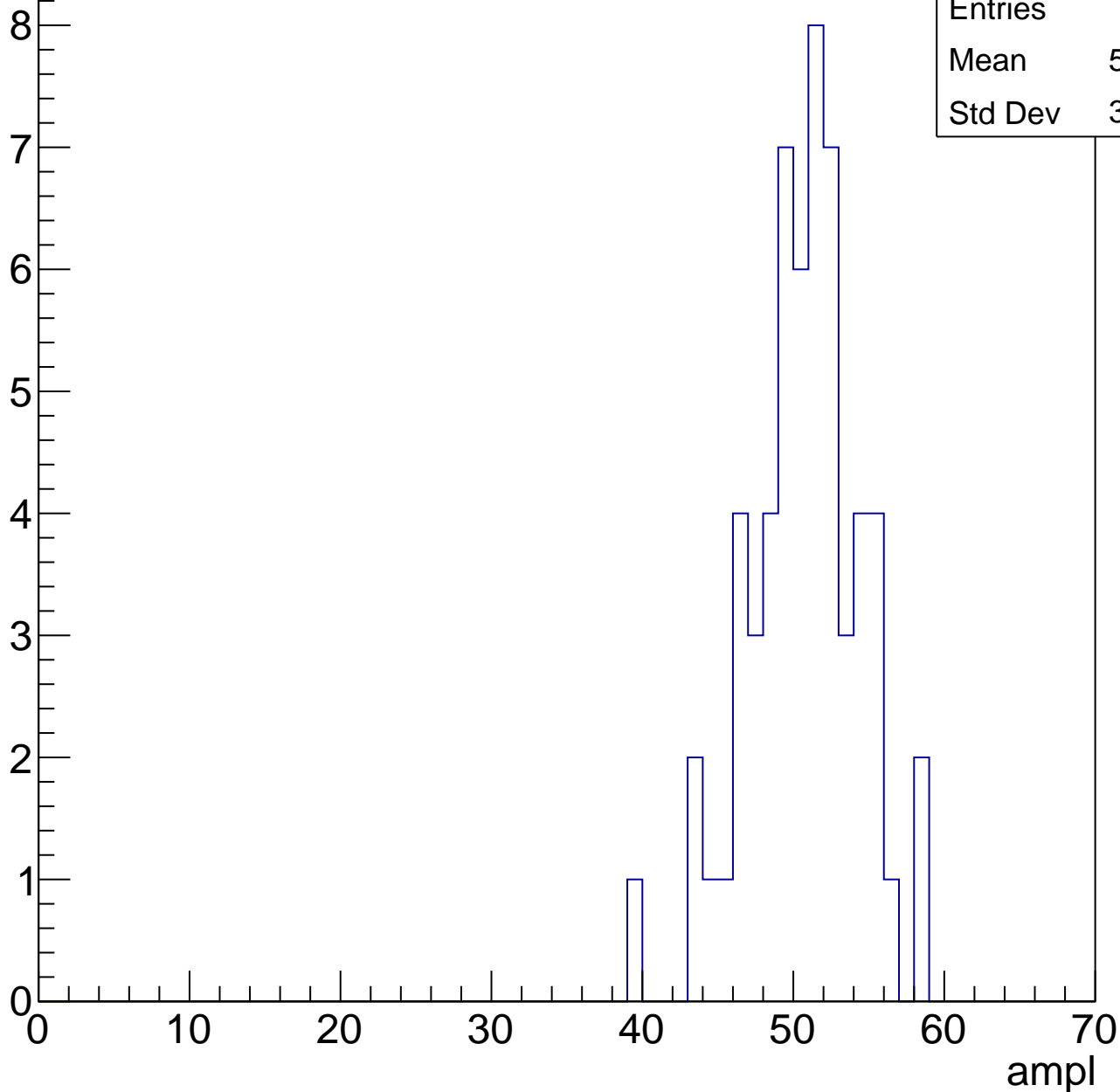


B1L100S, U15-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	50.22
Std Dev	3.672

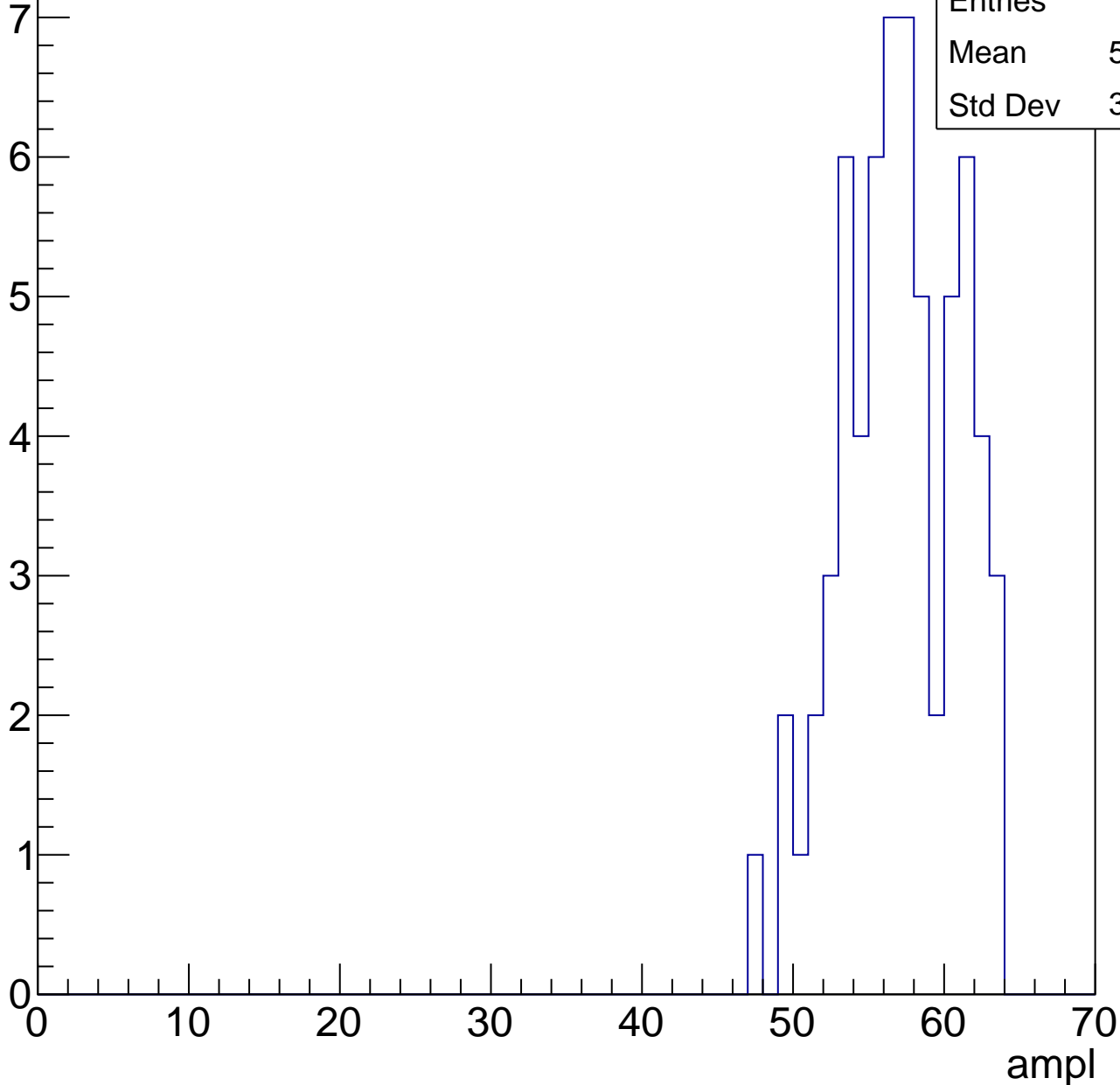


B1L100S, U15-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

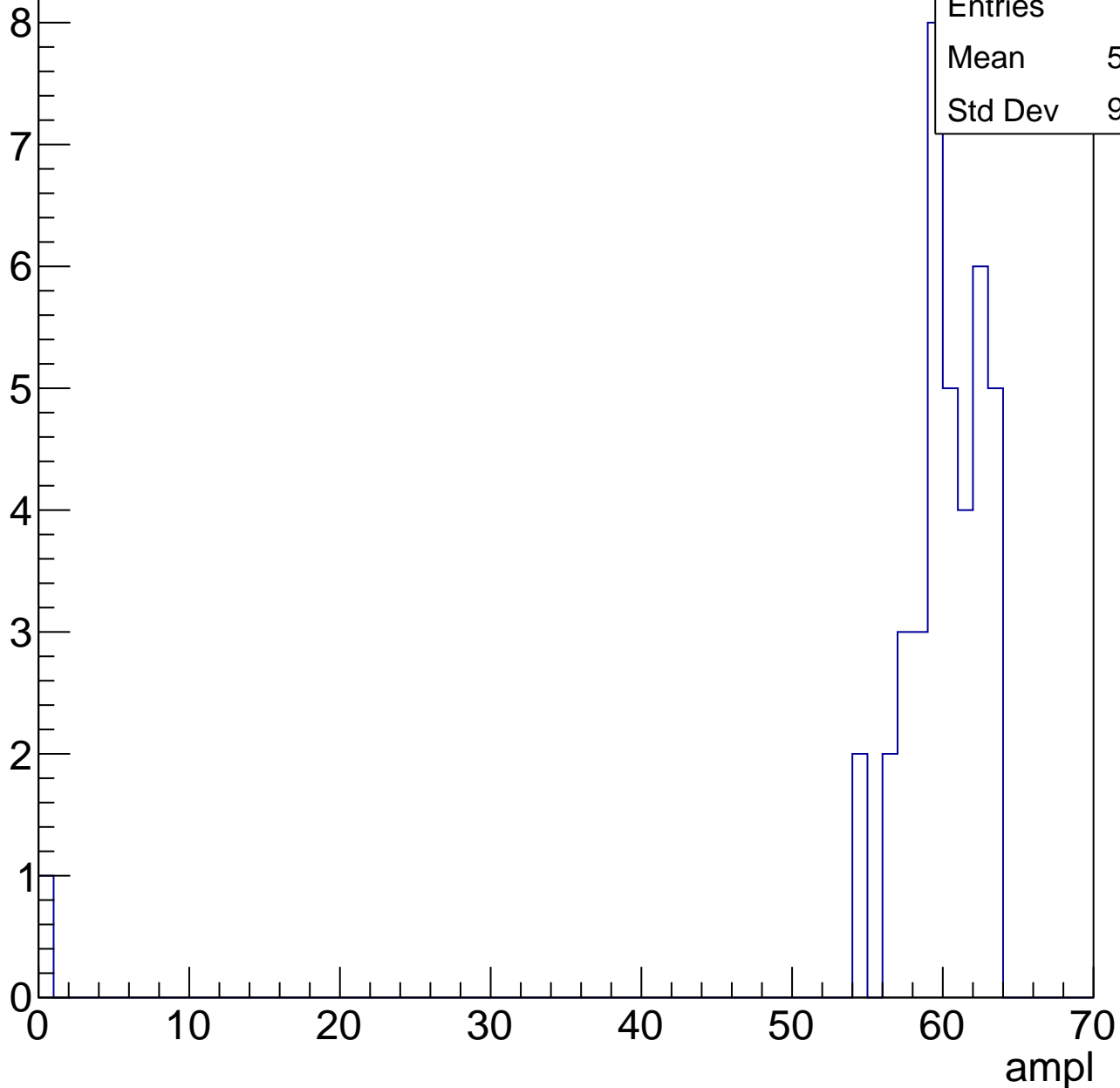
Entries	64
Mean	56.55
Std Dev	3.824



B1L100S, U15-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch100, adc0

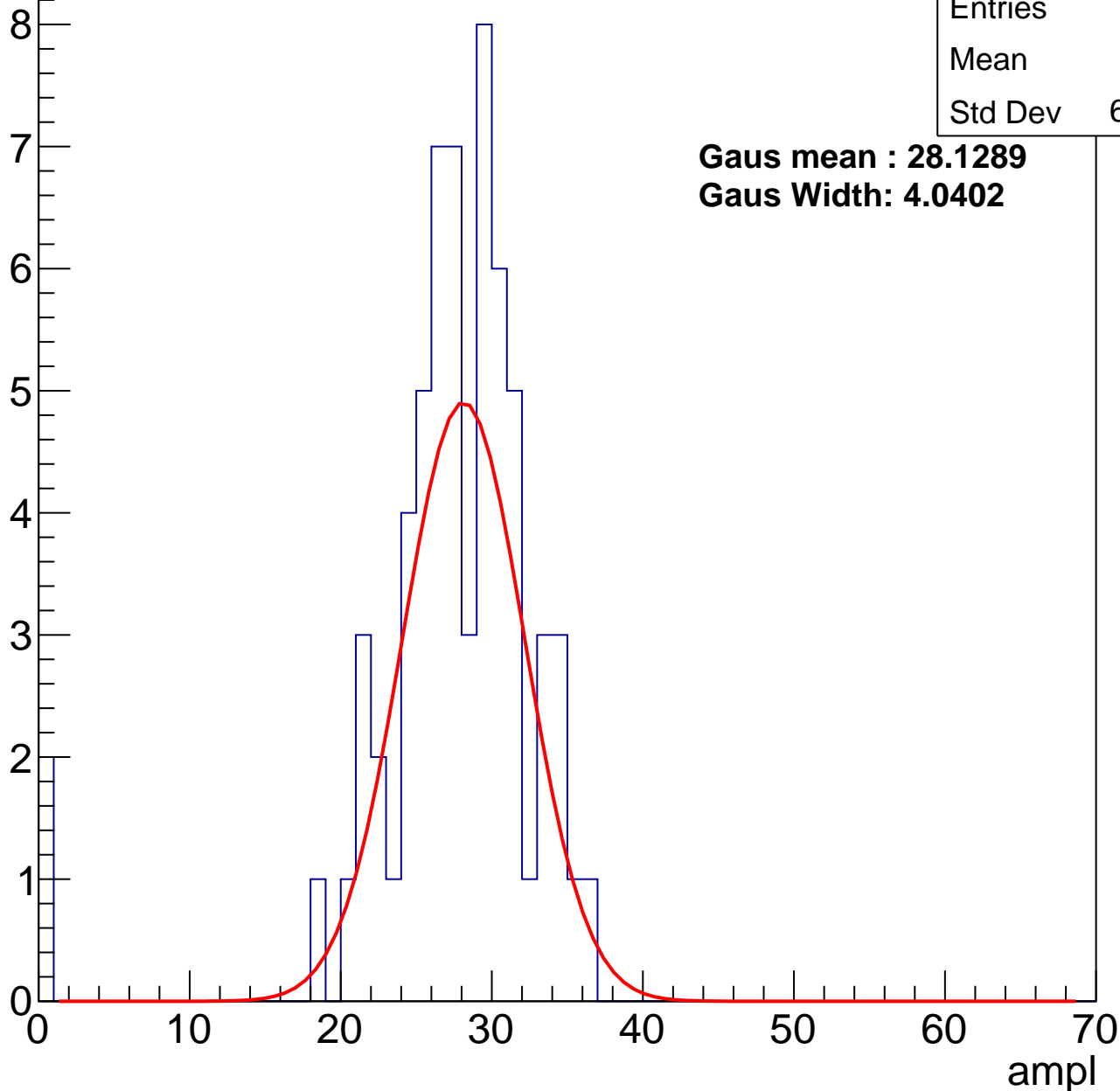
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	26.8
Std Dev	6.145

Gaus mean : 28.1289

Gaus Width: 4.0402



B1L100S, U15-ch100, adc1

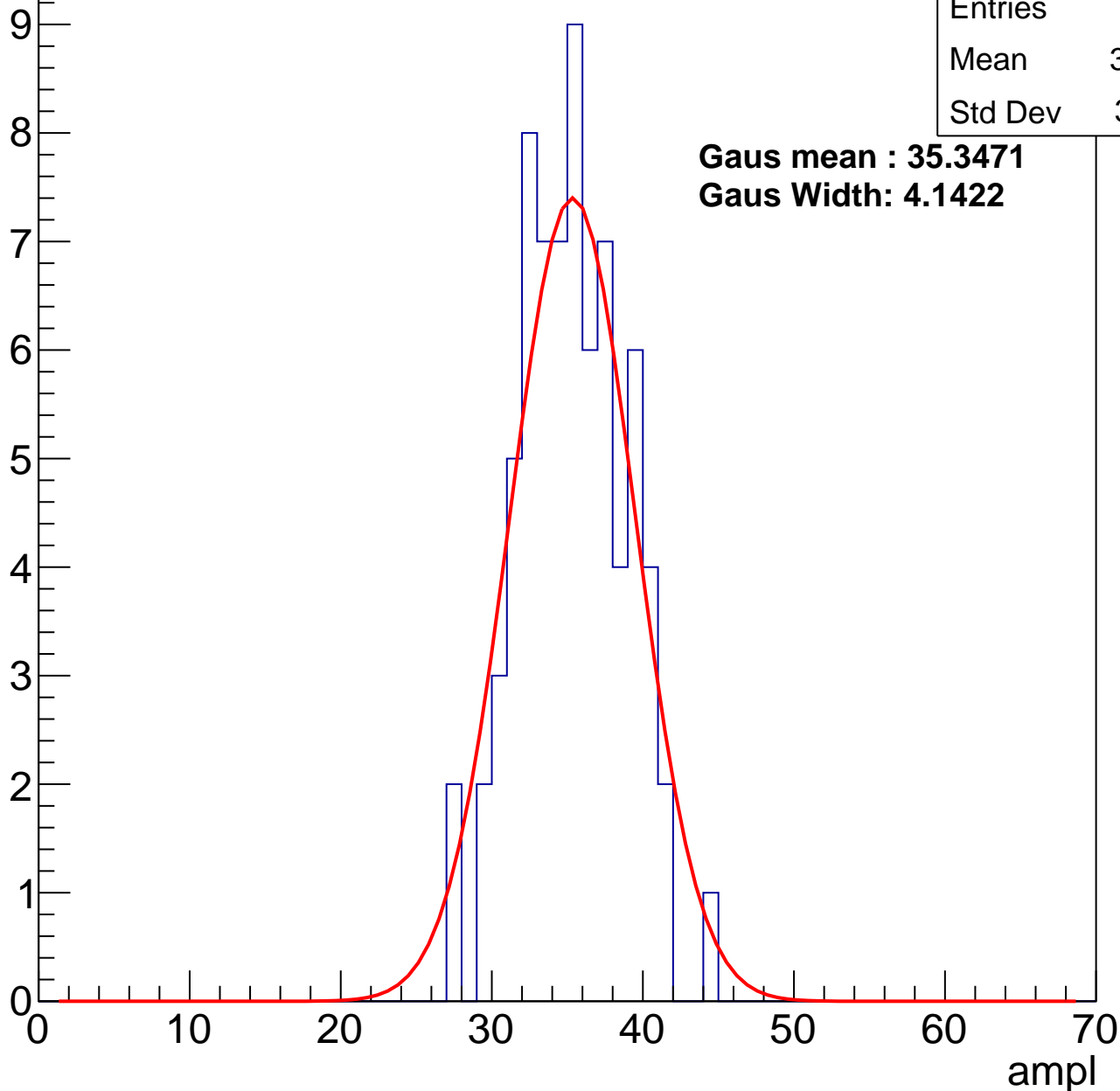
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	34.85
Std Dev	3.471

Gaus mean : 35.3471

Gaus Width: 4.1422



B1L100S, U15-ch100, adc2

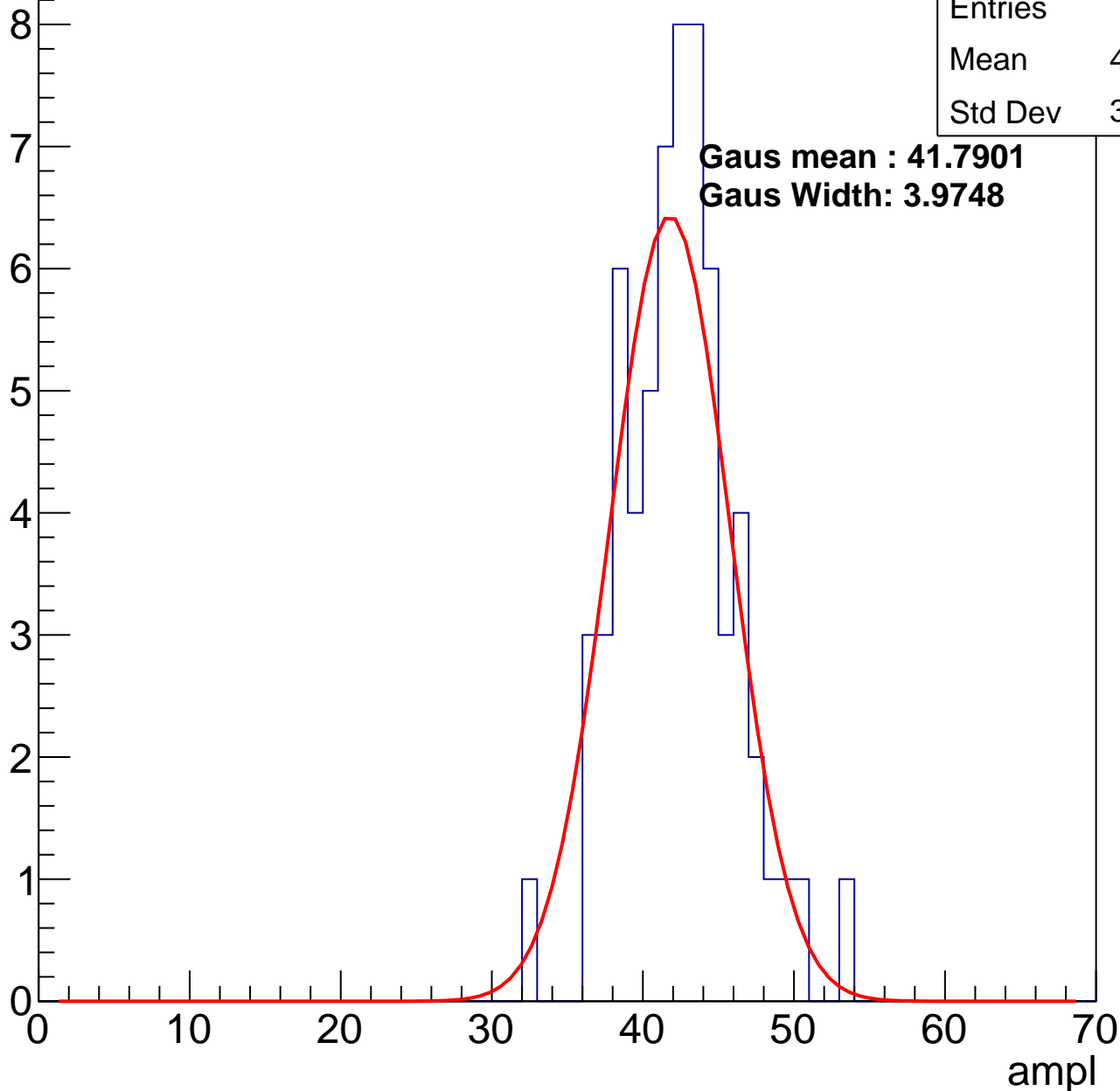
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.86
Std Dev	3.716

Gaus mean : 41.7901

Gaus Width: 3.9748

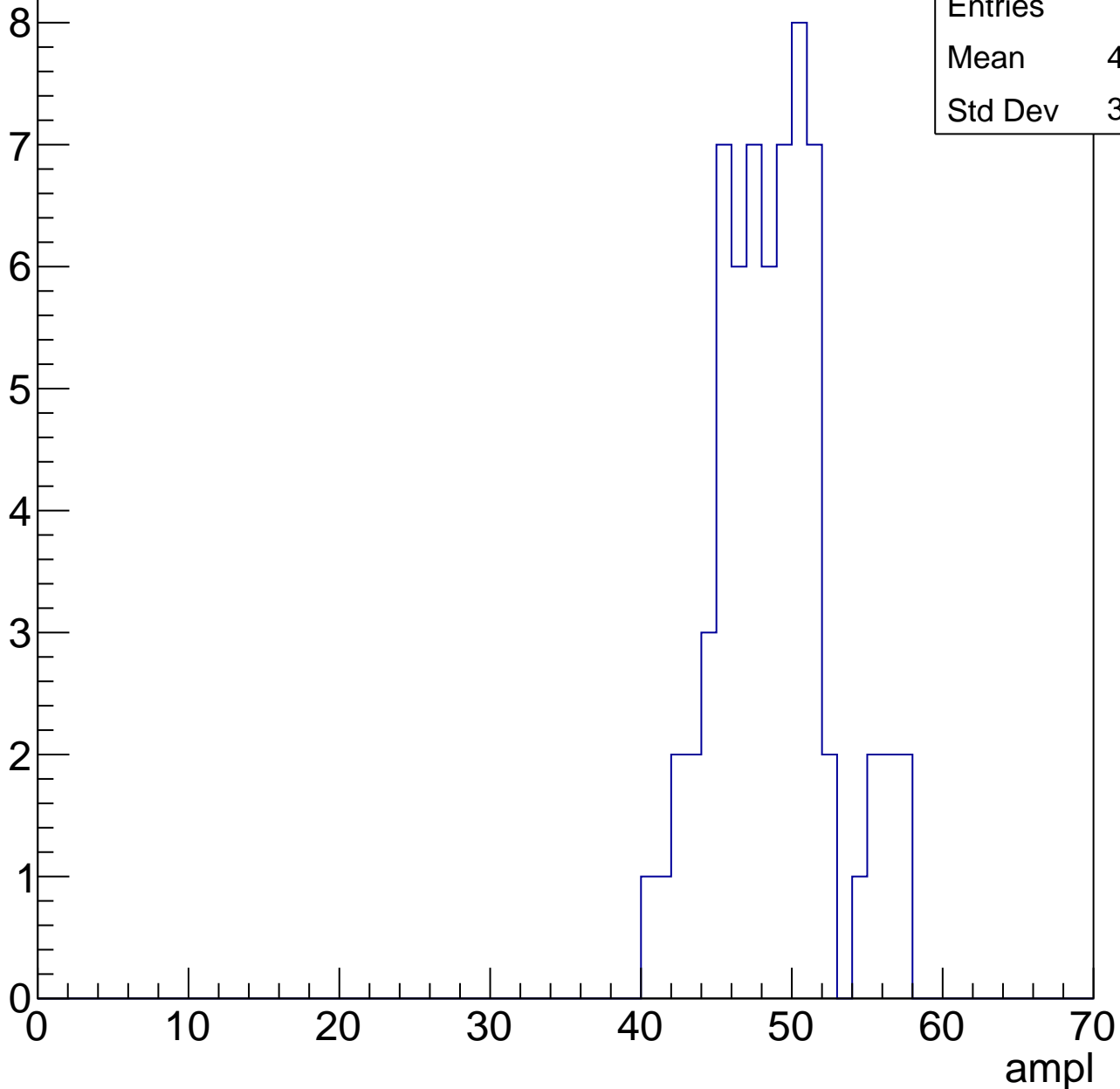


B1L100S, U15-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	48.26
Std Dev	3.755

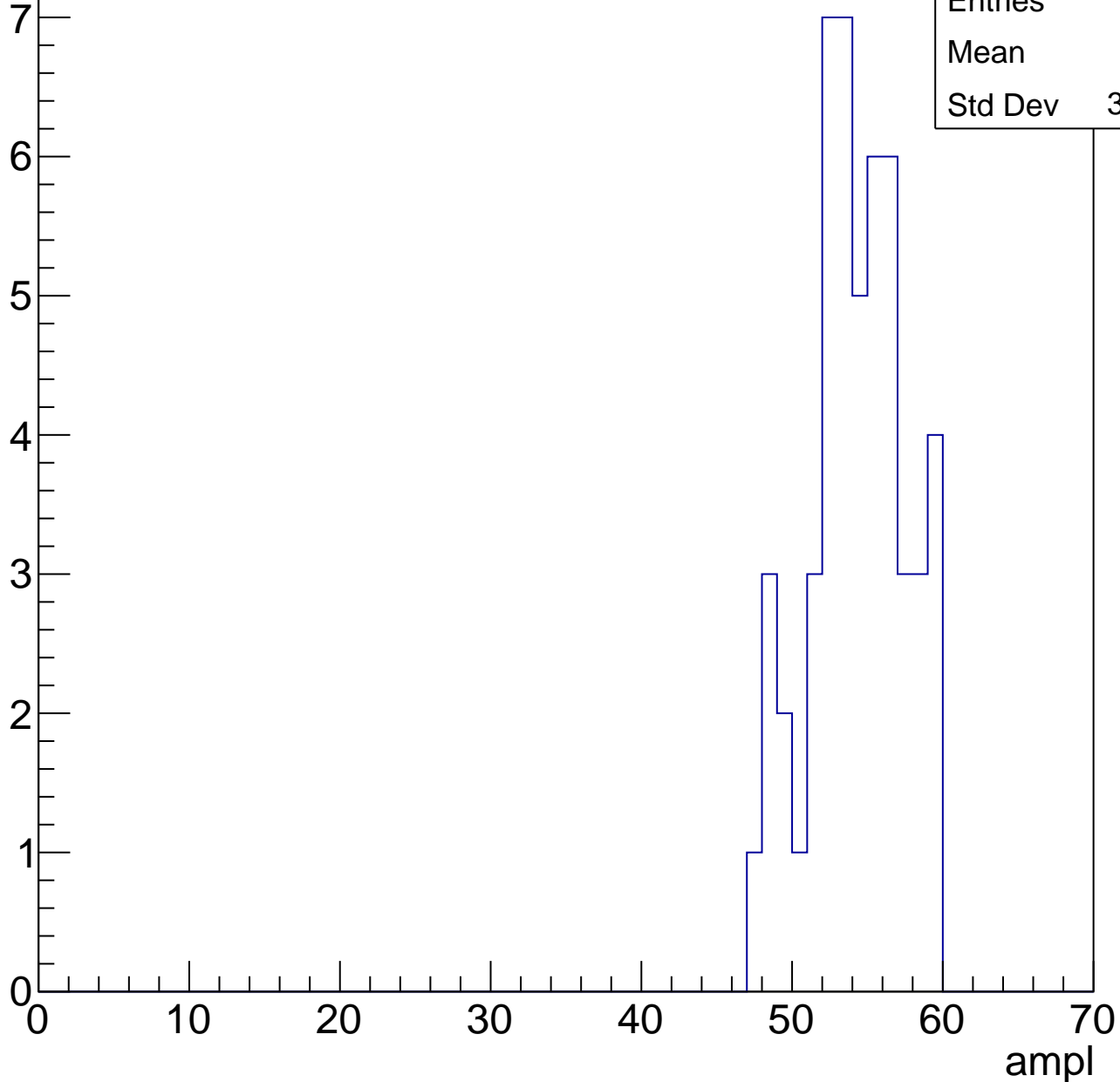


B1L100S, U15-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	53.8
Std Dev	3.093

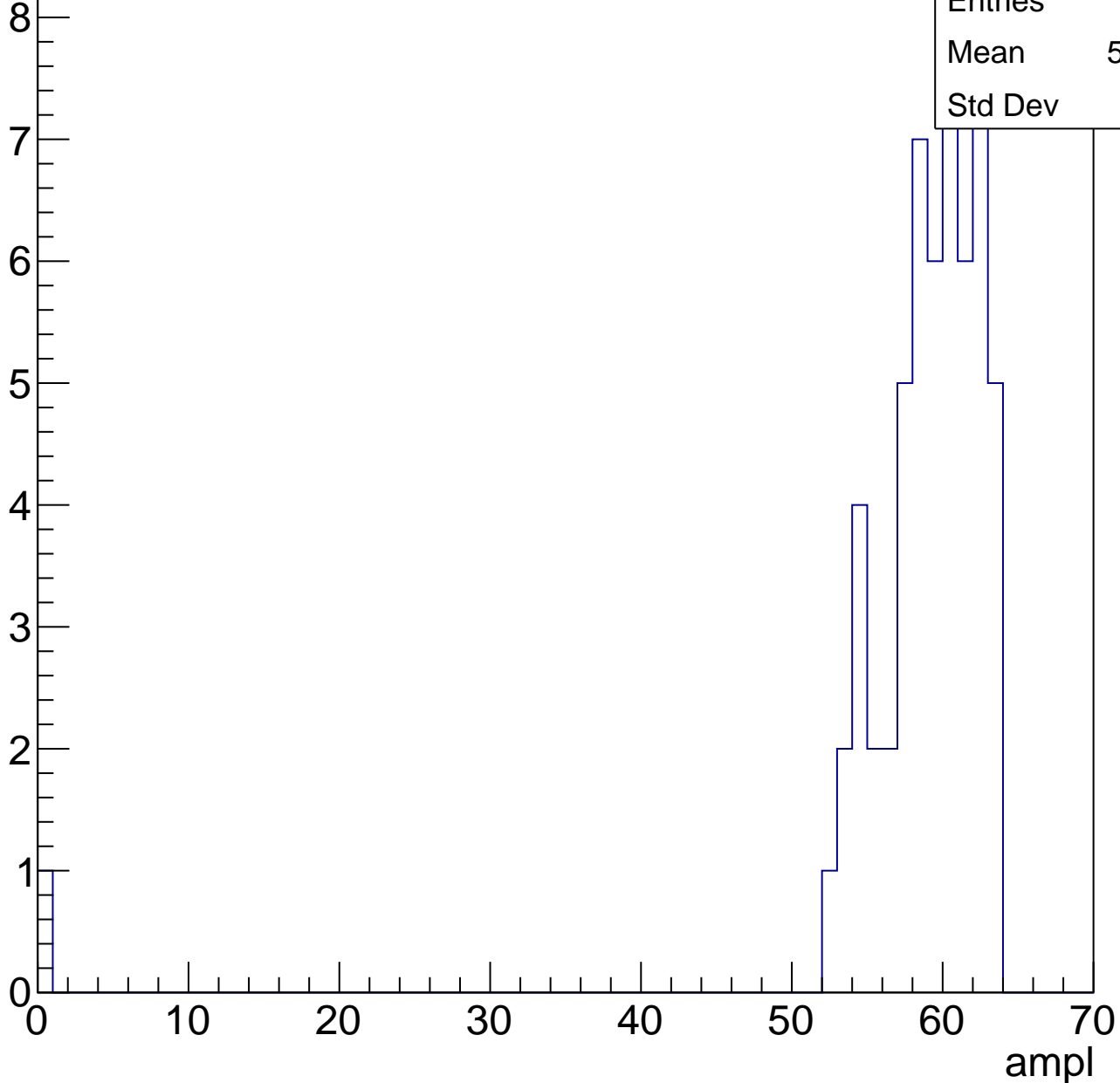


B1L100S, U15-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	57.86
Std Dev	8.26

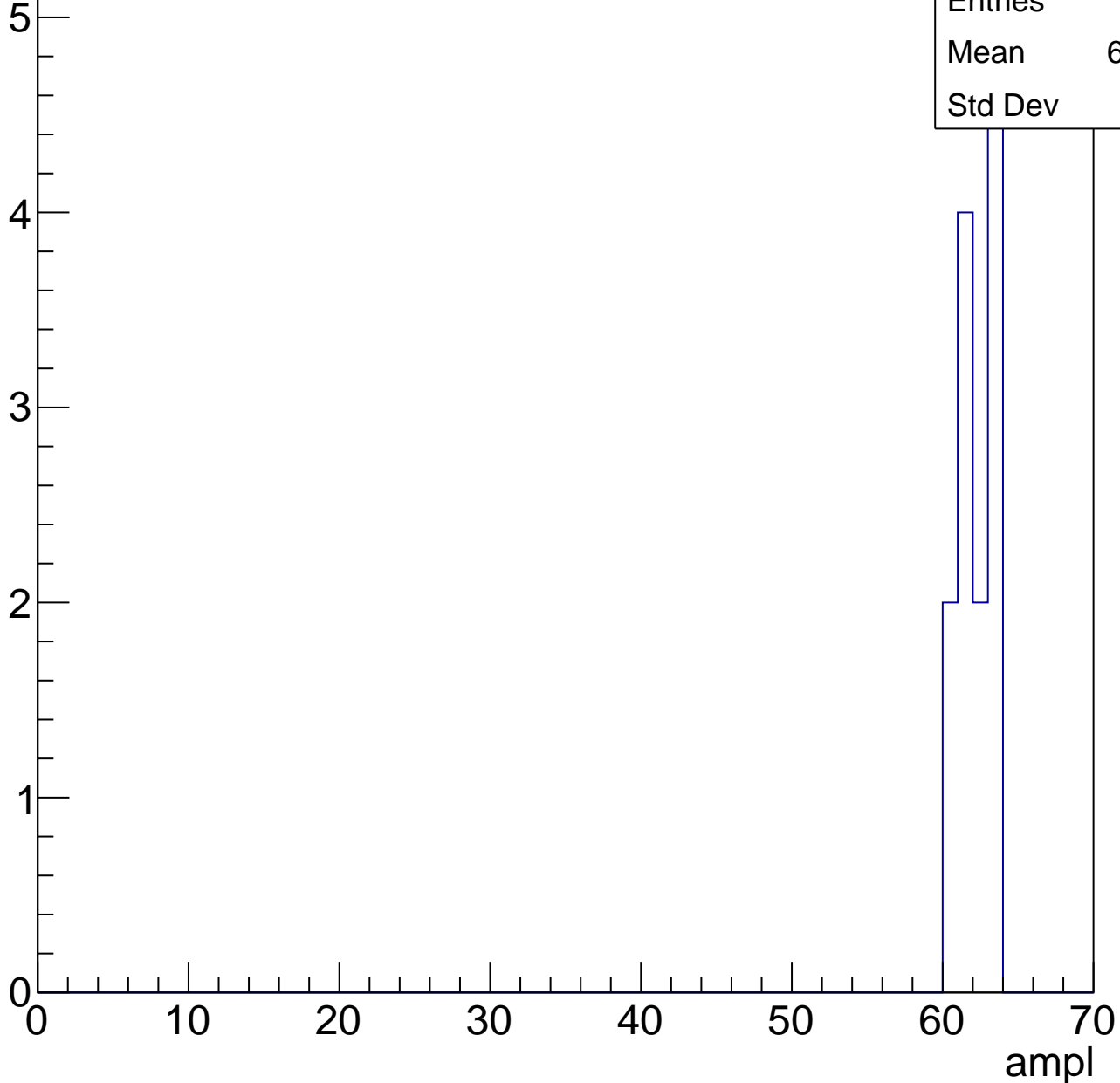


B1L100S, U15-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	13
Mean	61.77
Std Dev	1.12



B1L100S, U15-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch101, adc0

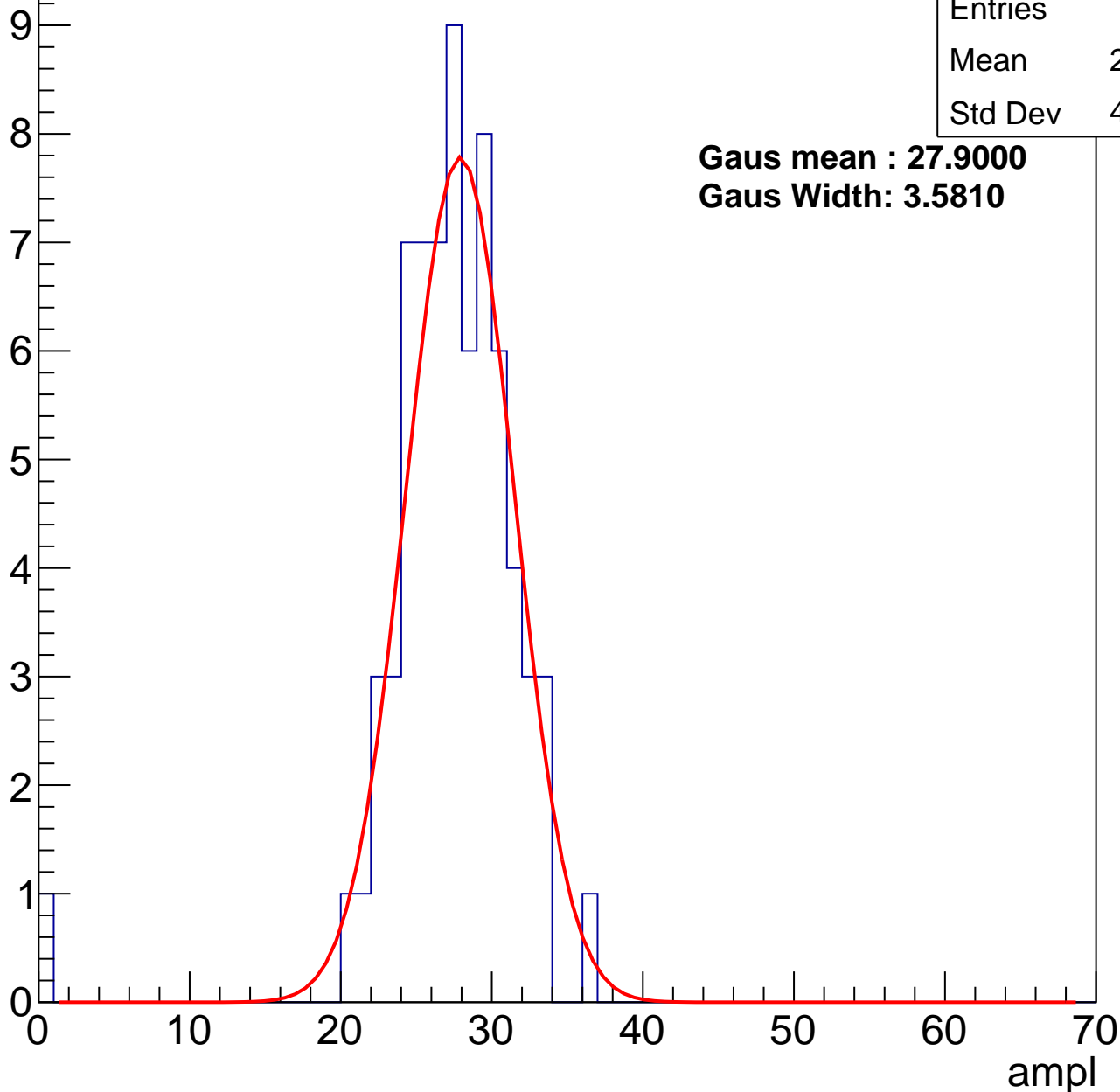
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	26.84
Std Dev	4.556

Gaus mean : 27.9000

Gaus Width: 3.5810



B1L100S, U15-ch101, adc1

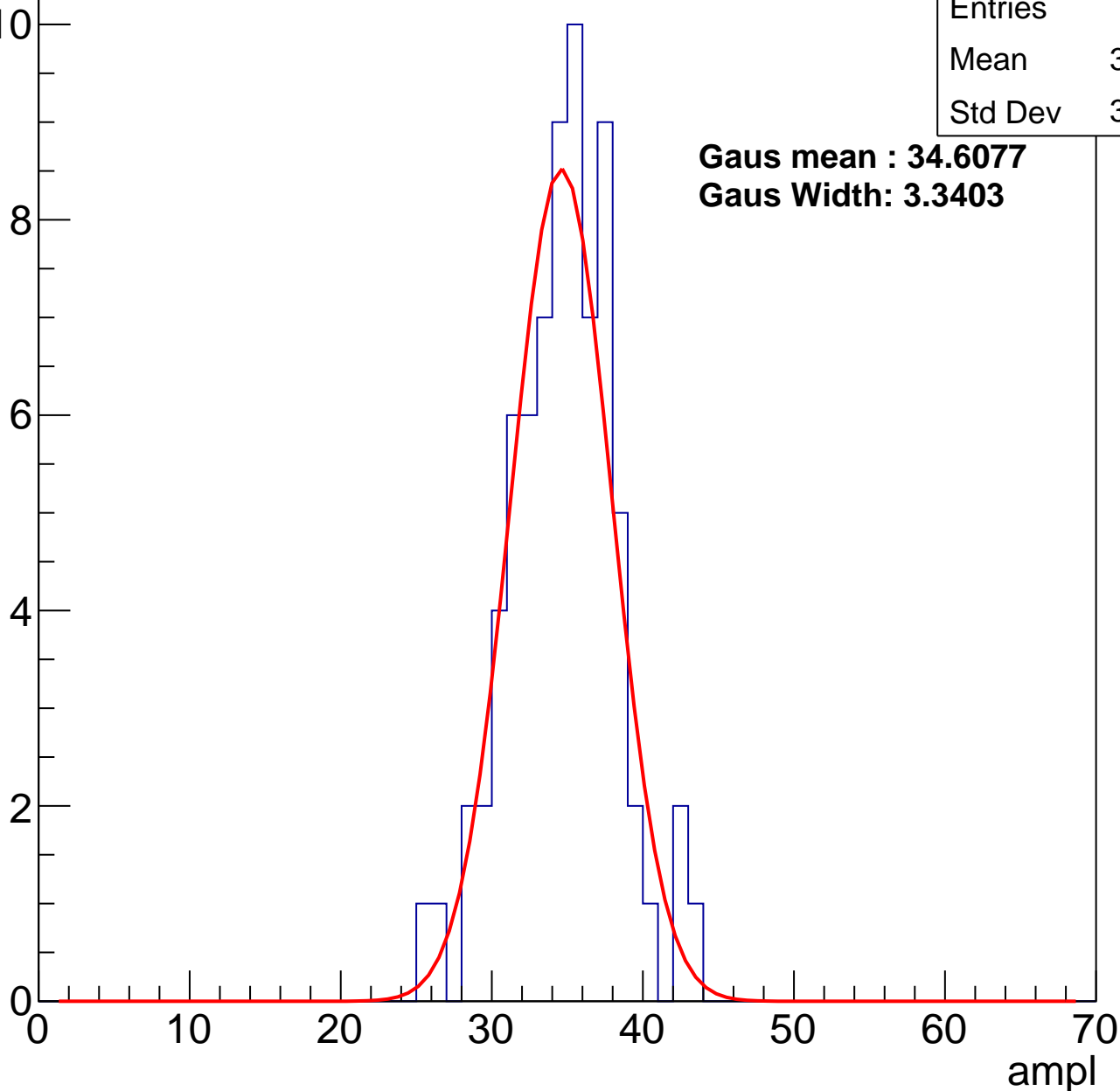
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	34.27
Std Dev	3.473

Gaus mean : 34.6077

Gaus Width: 3.3403



B1L100S, U15-ch101, adc2

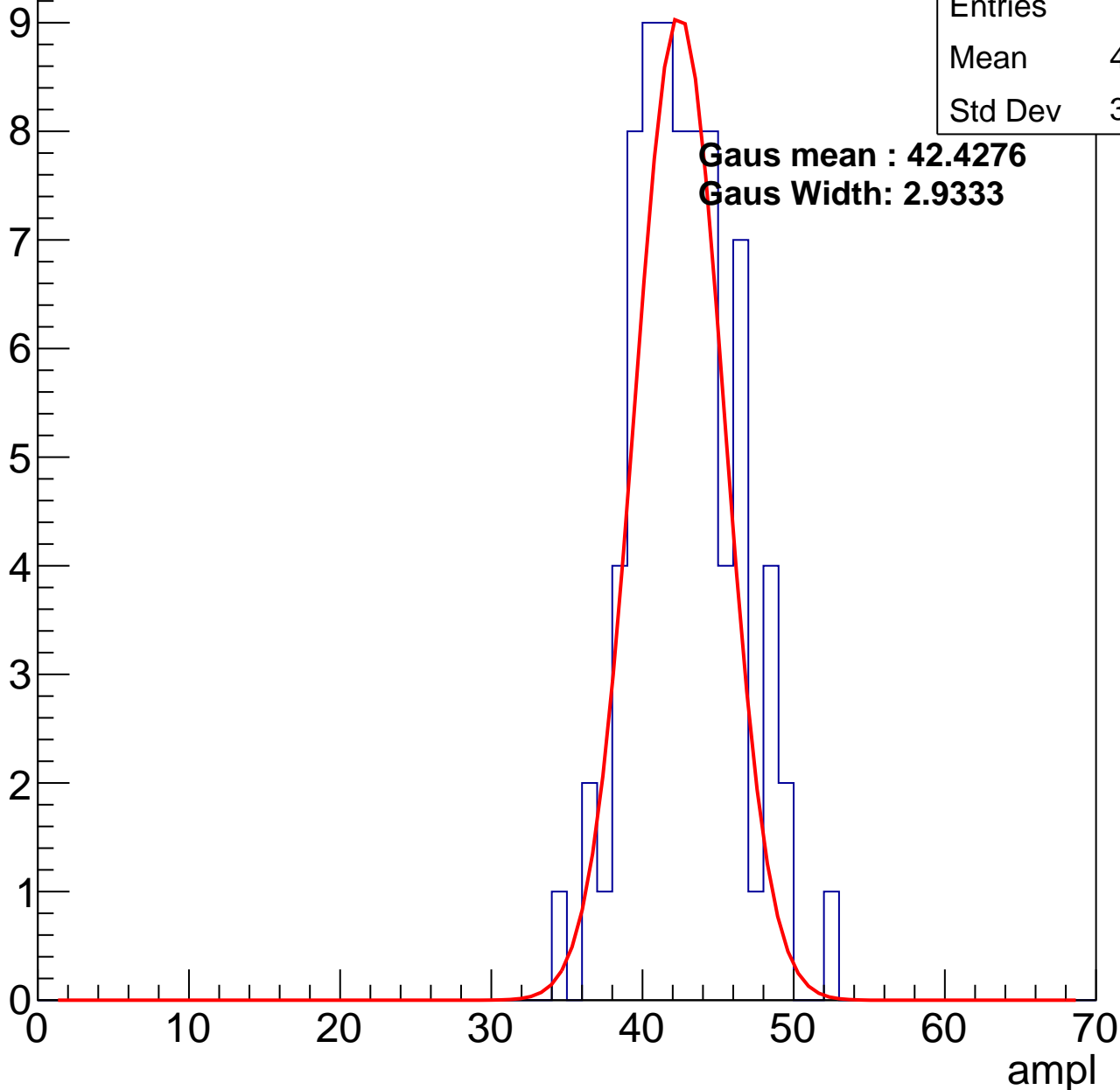
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	42.32
Std Dev	3.398

Gaus mean : 42.4276

Gaus Width: 2.9333

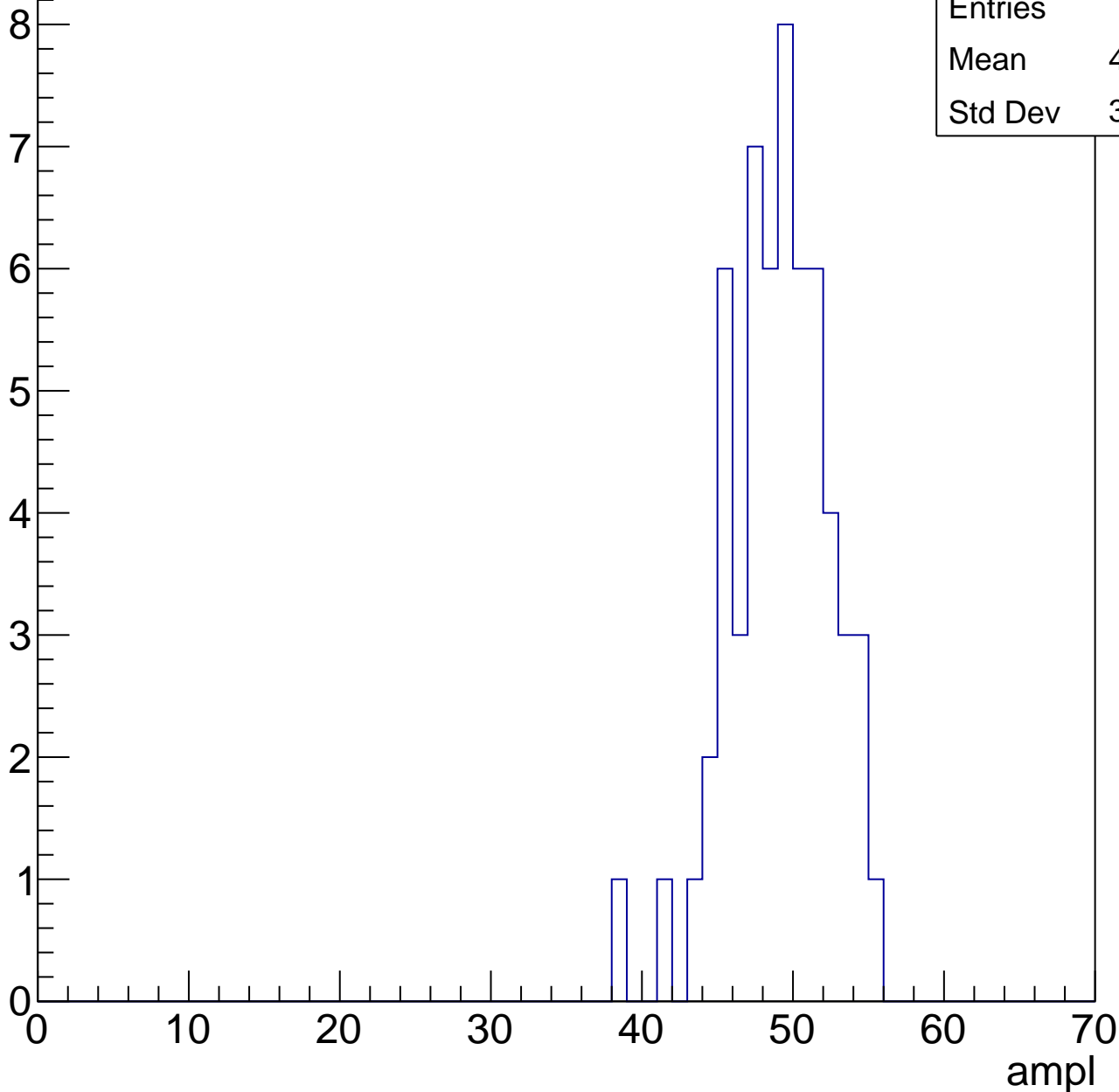


B1L100S, U15-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	48.57
Std Dev	3.328

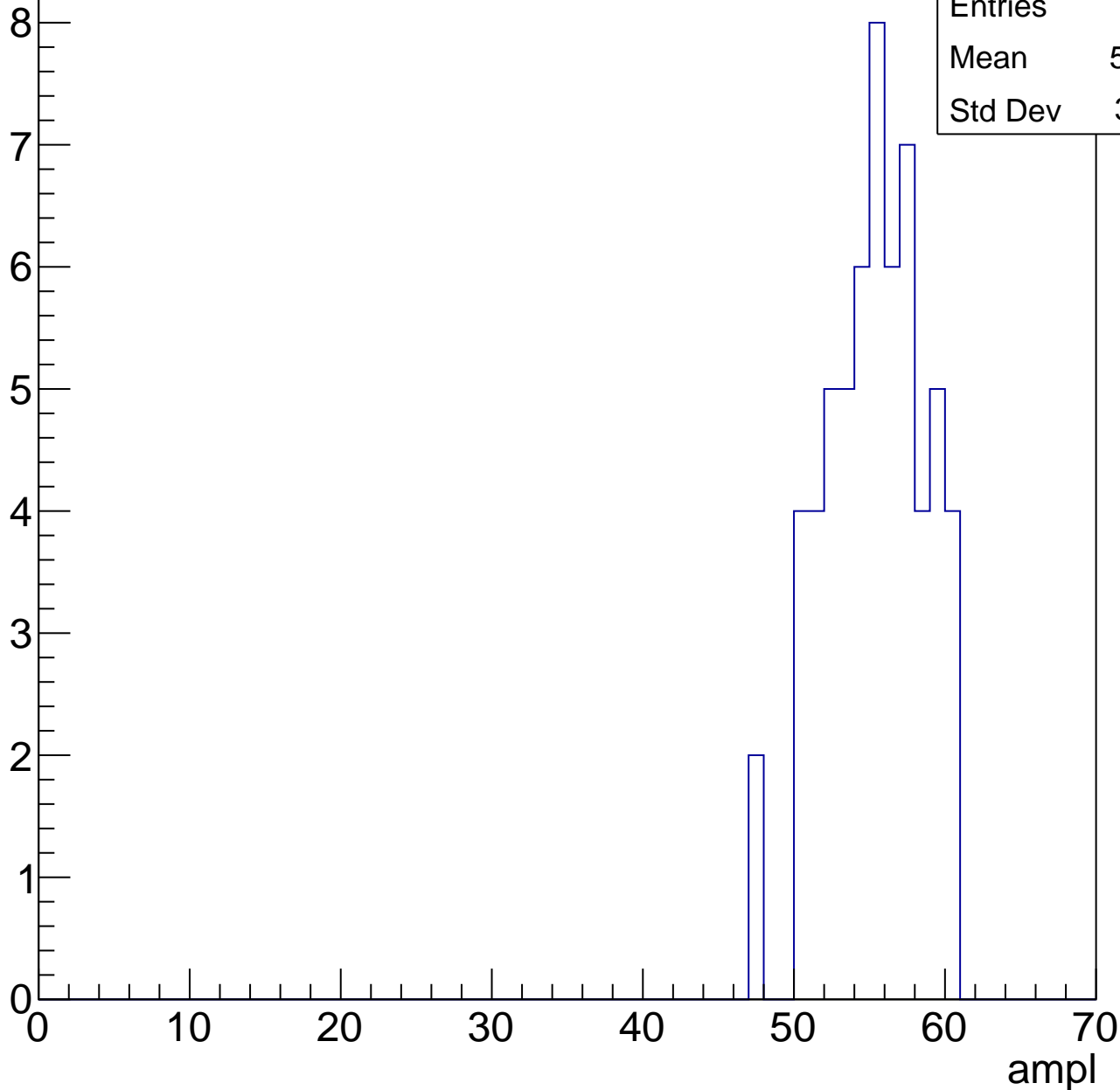


B1L100S, U15-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	54.82
Std Dev	3.191

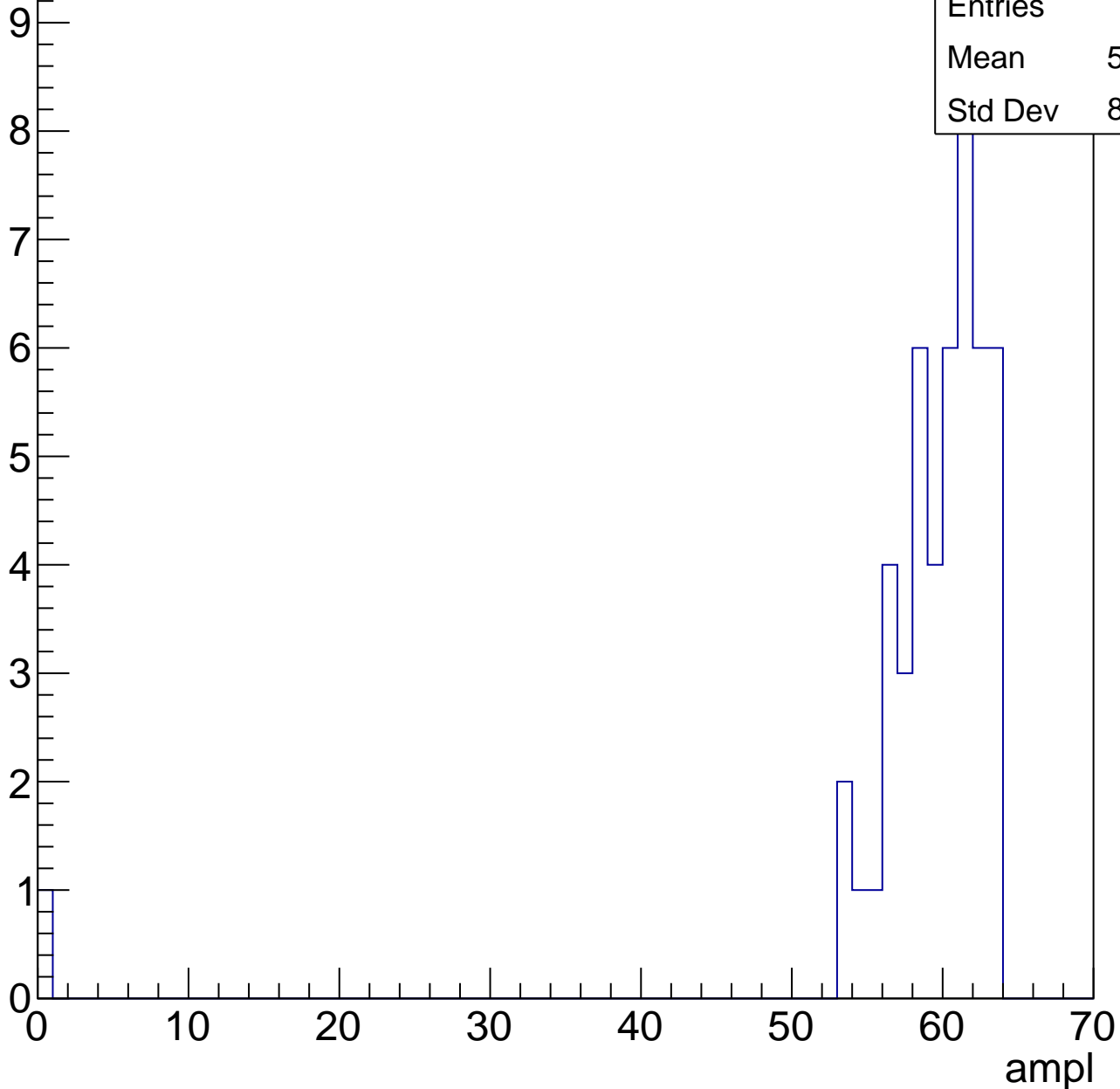


B1L100S, U15-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

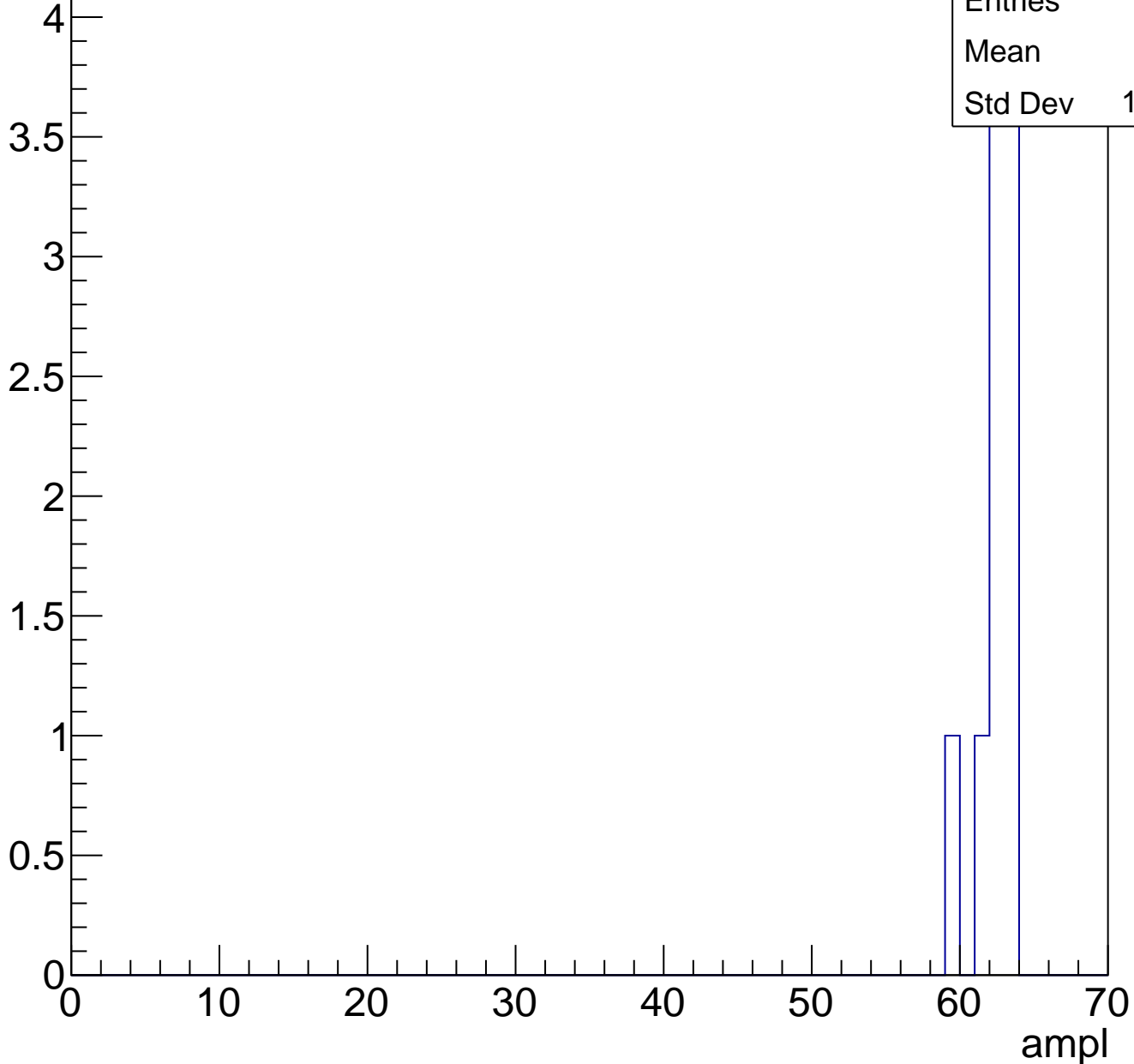
Entries	49
Mean	58.22
Std Dev	8.818



B1L100S, U15-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch102, adc0

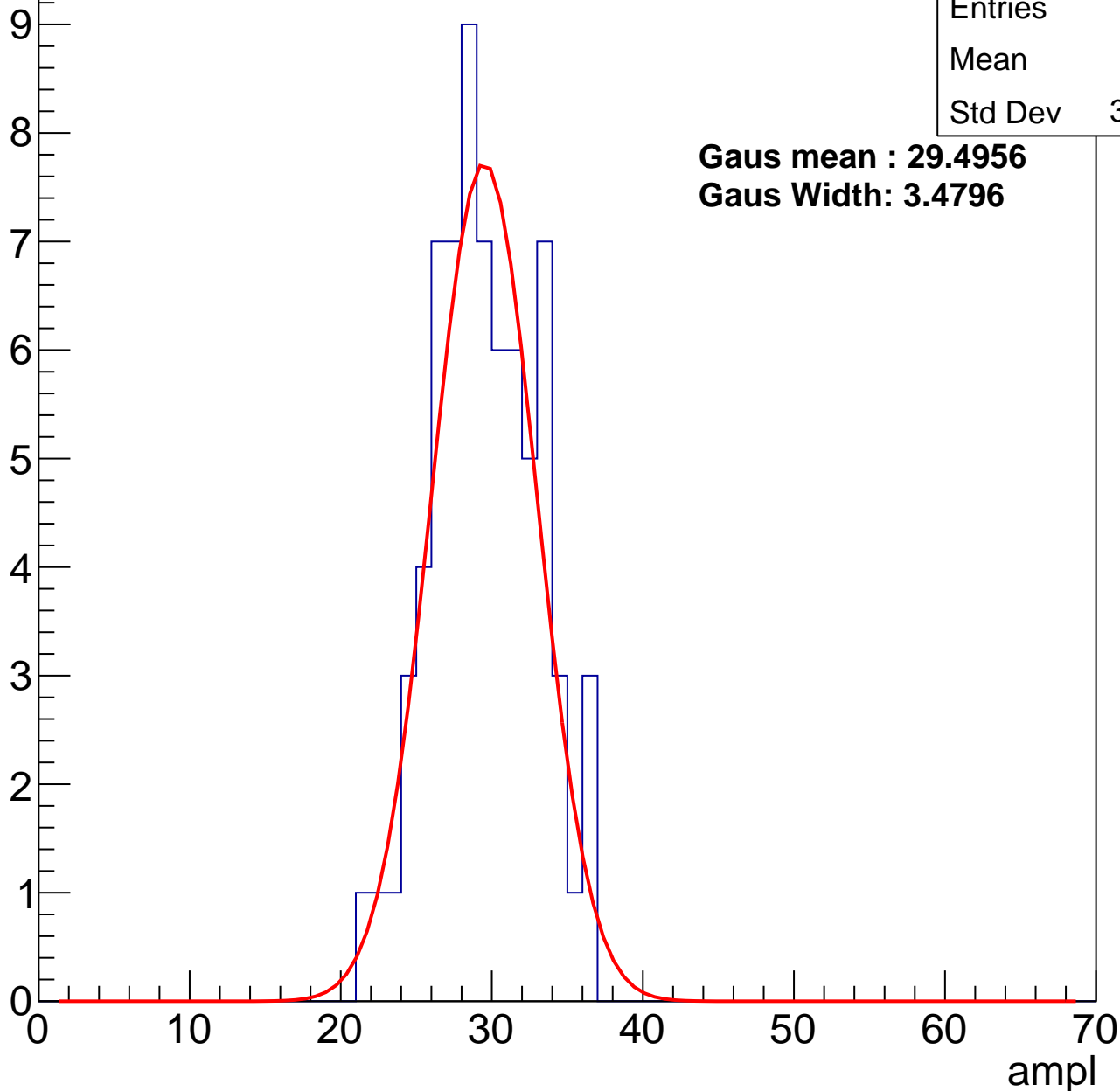
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.1
Std Dev	3.424

Gaus mean : 29.4956

Gaus Width: 3.4796



B1L100S, U15-ch102, adc1

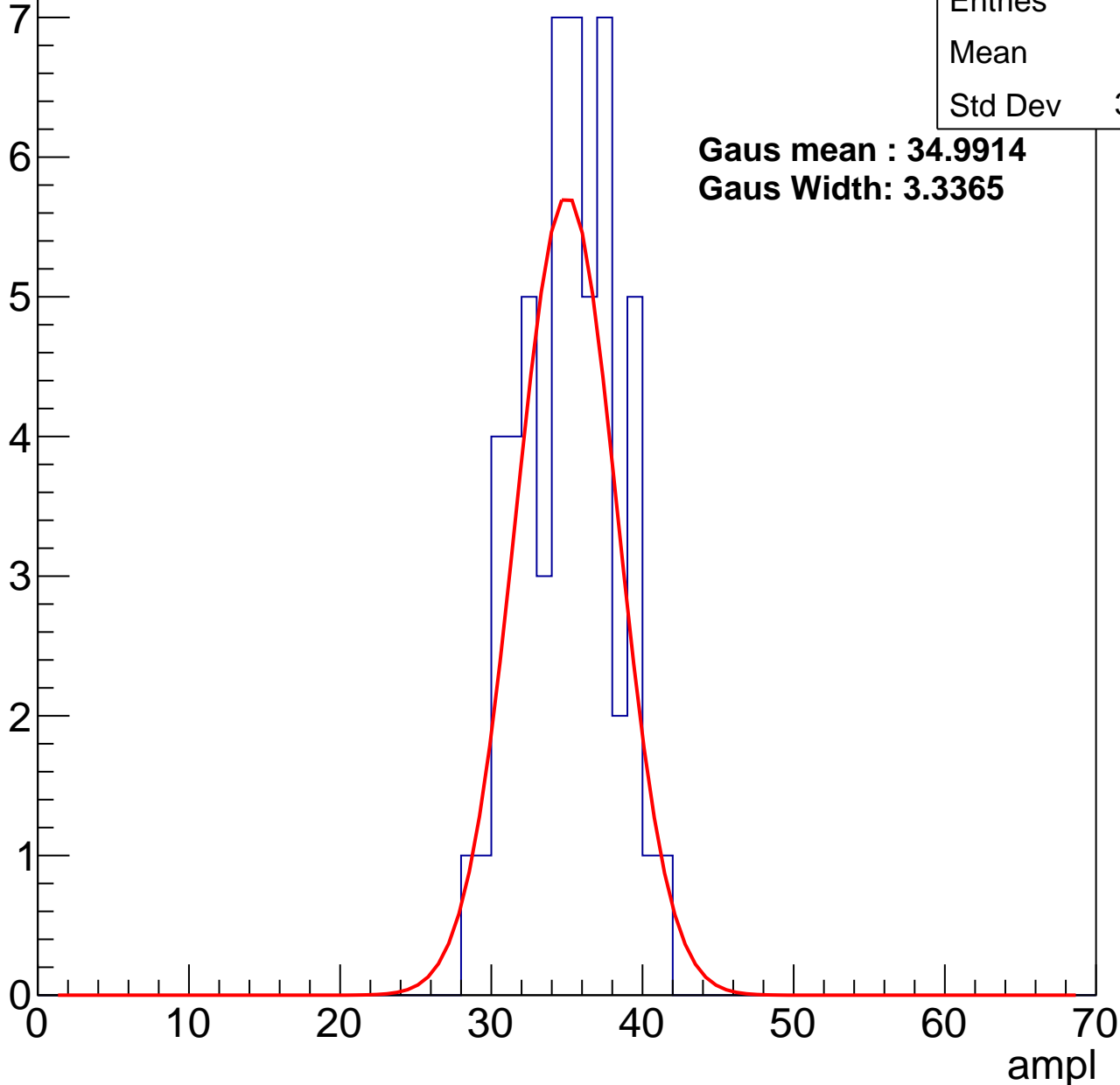
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	34.6
Std Dev	3.061

Gaus mean : 34.9914

Gaus Width: 3.3365



B1L100S, U15-ch102, adc2

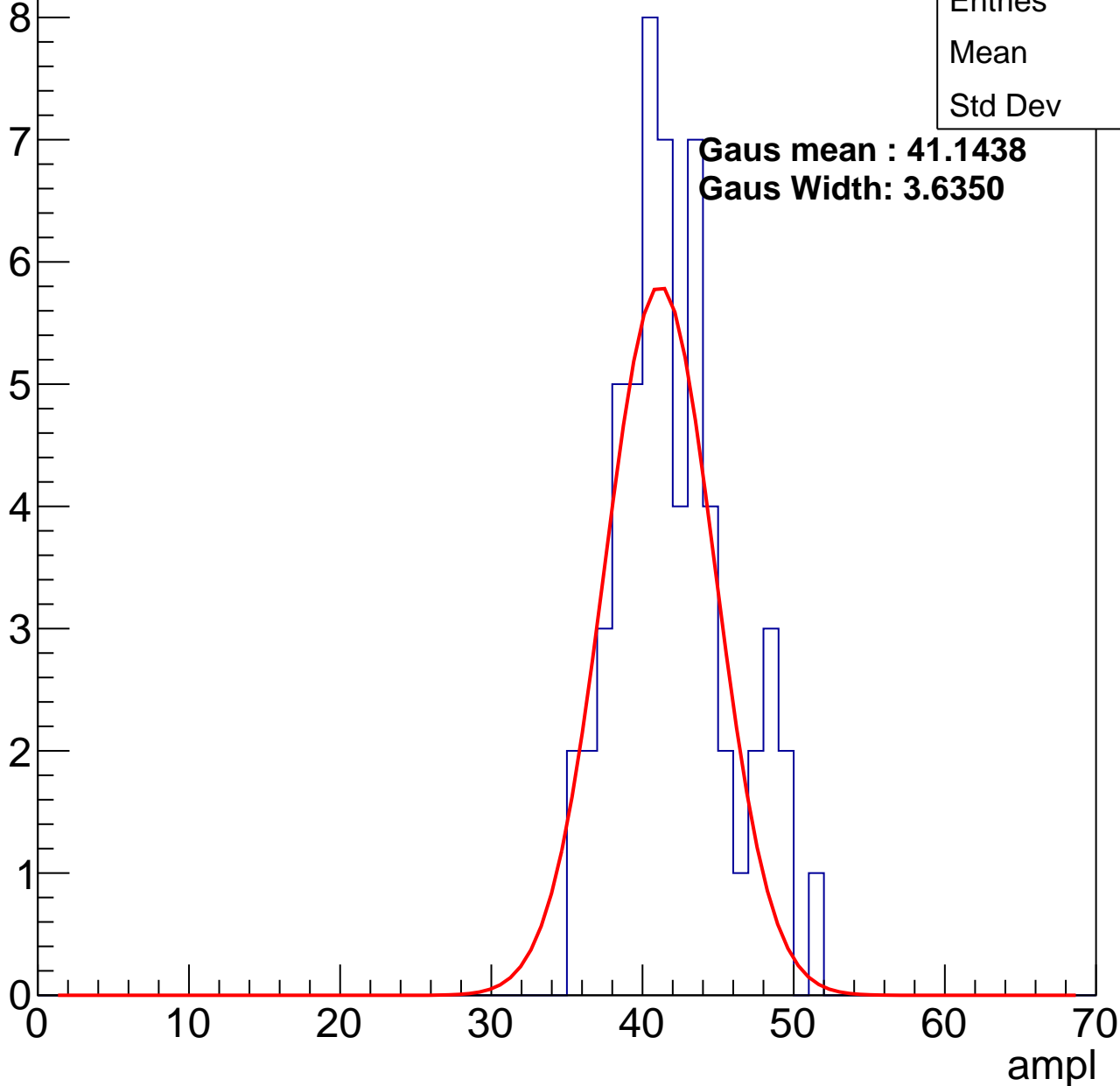
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	41.6
Std Dev	3.7

Gaus mean : 41.1438

Gaus Width: 3.6350

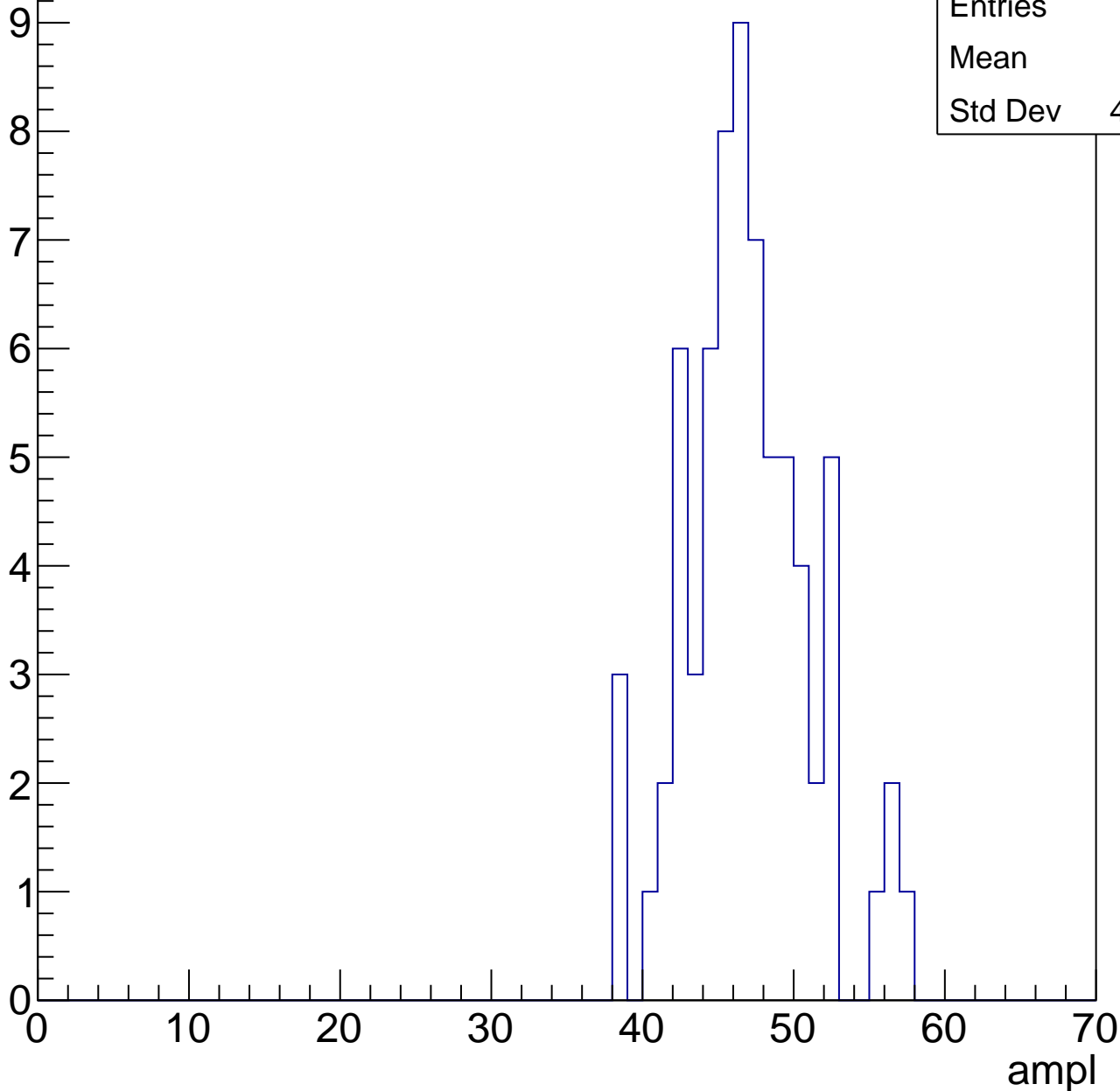


B1L100S, U15-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	46.5
Std Dev	4.112

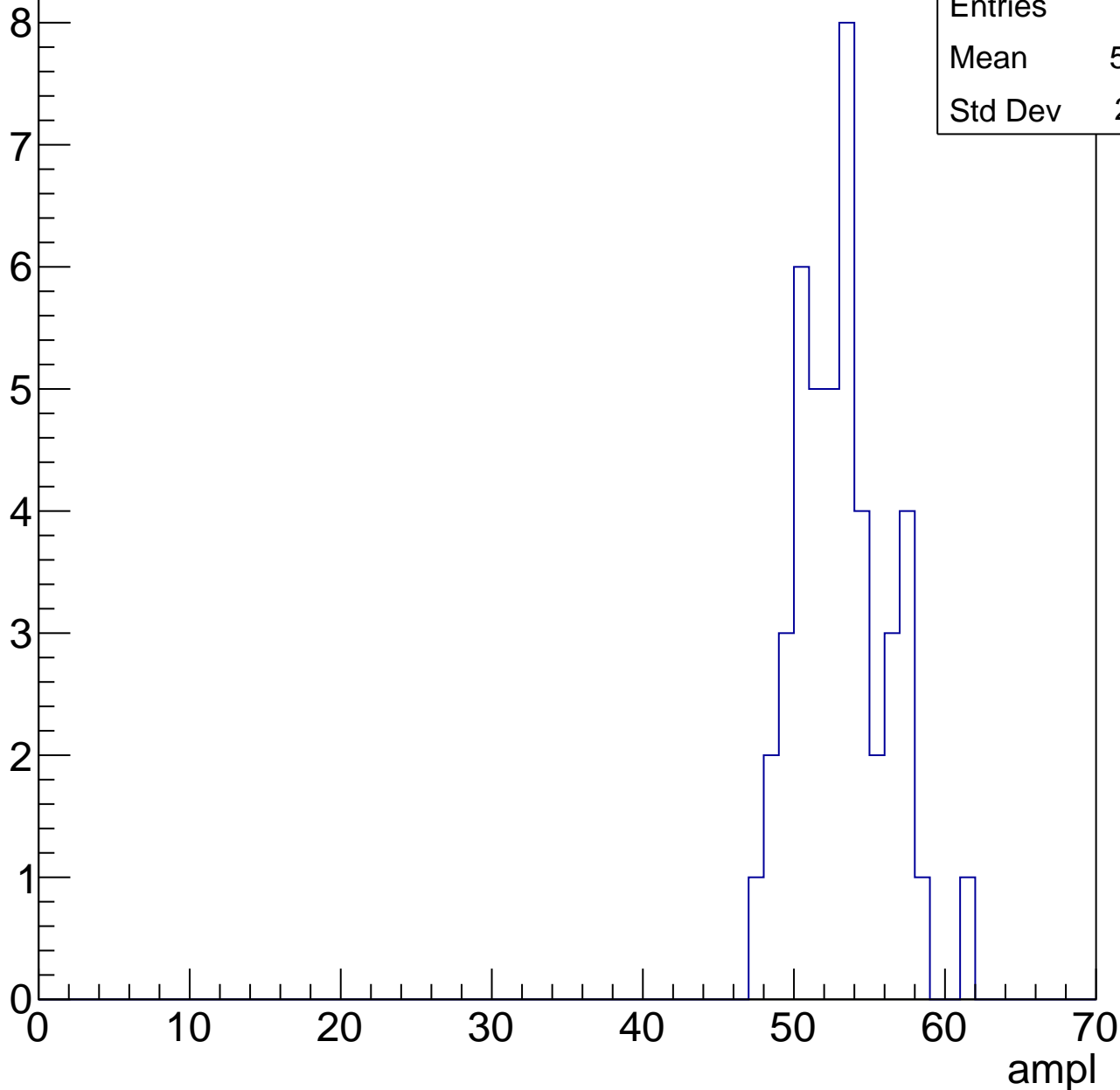


B1L100S, U15-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	52.67
Std Dev	2.981

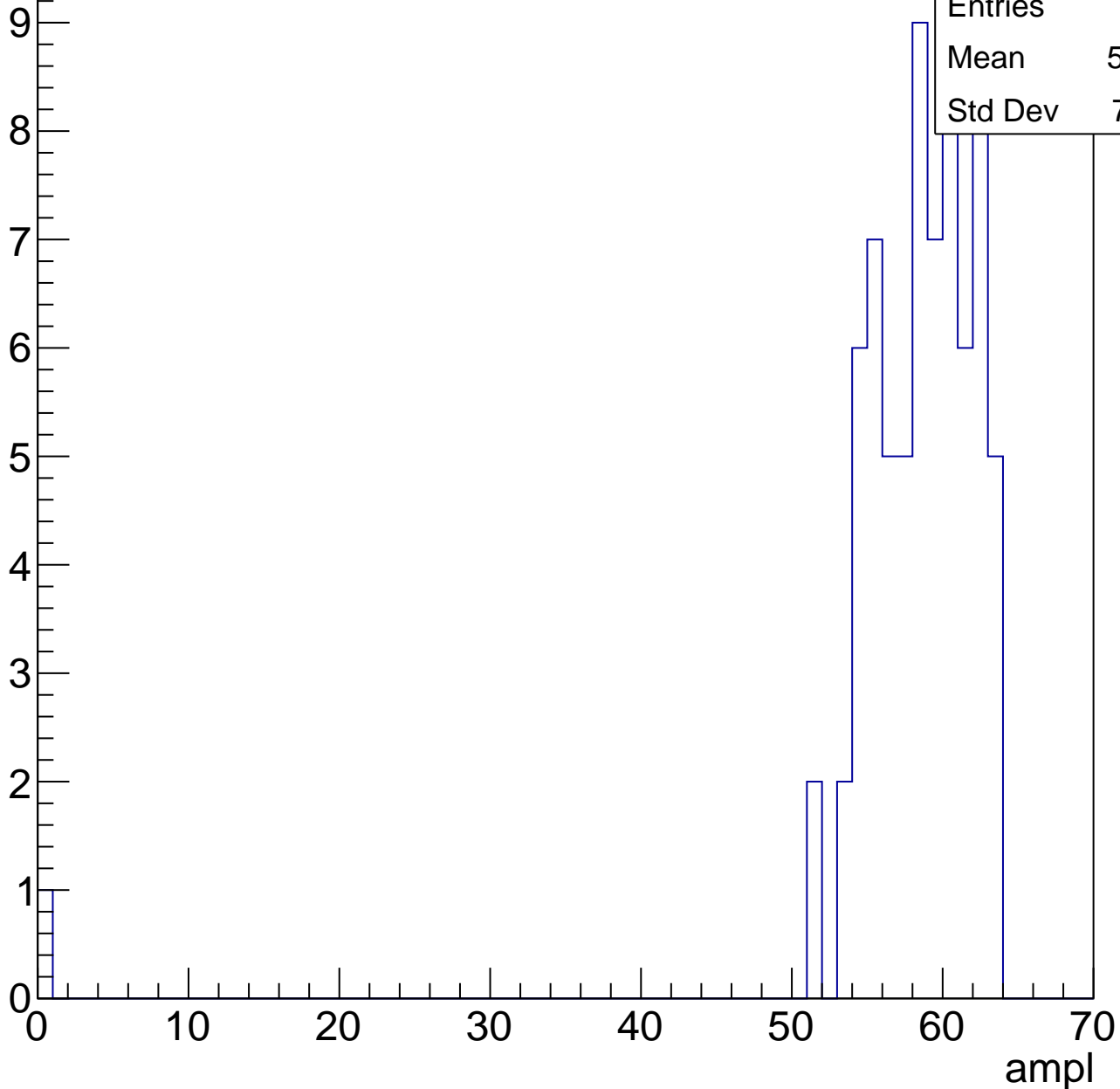


B1L100S, U15-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

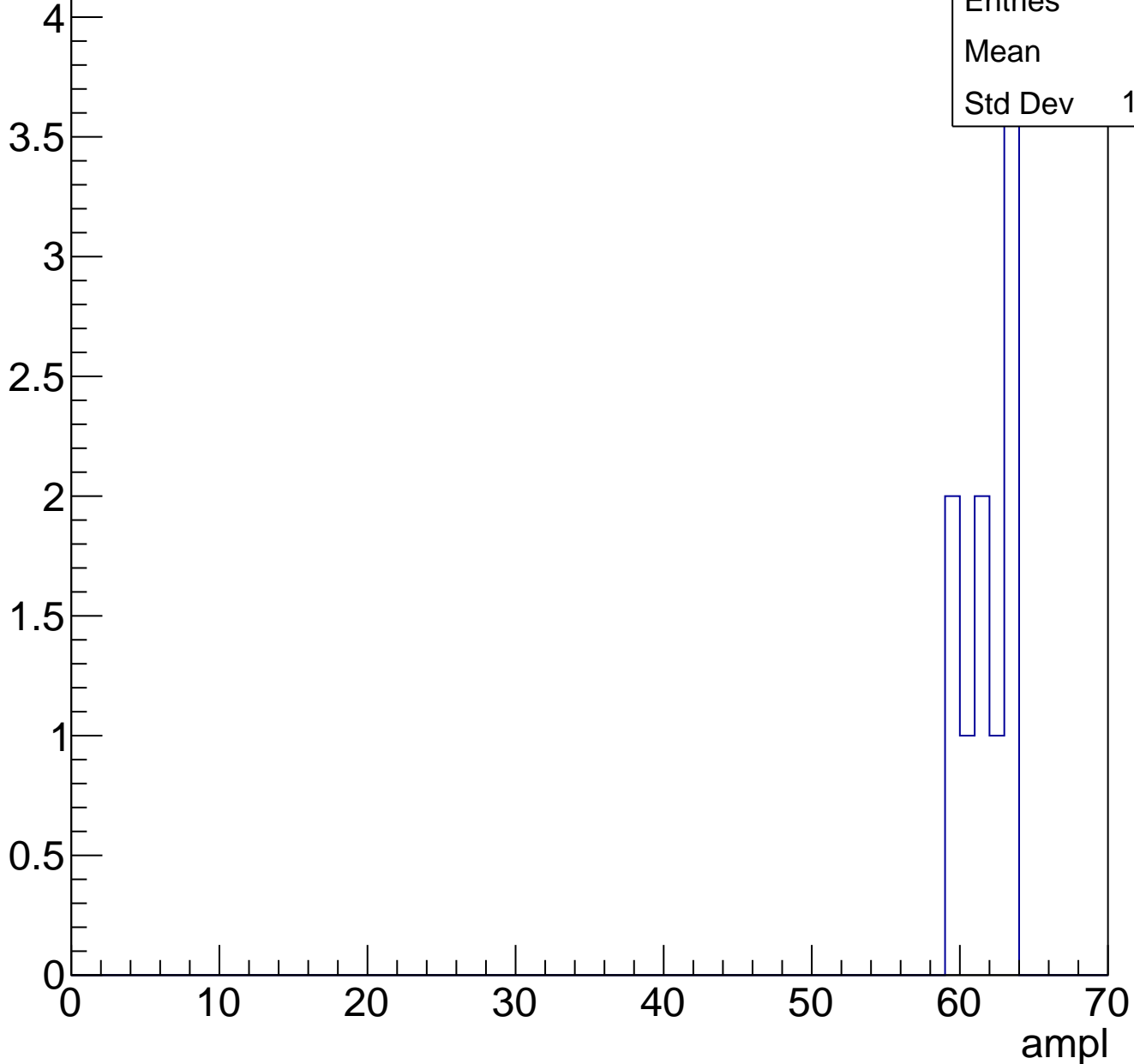
Entries	73
Mean	57.48
Std Dev	7.441



B1L100S, U15-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

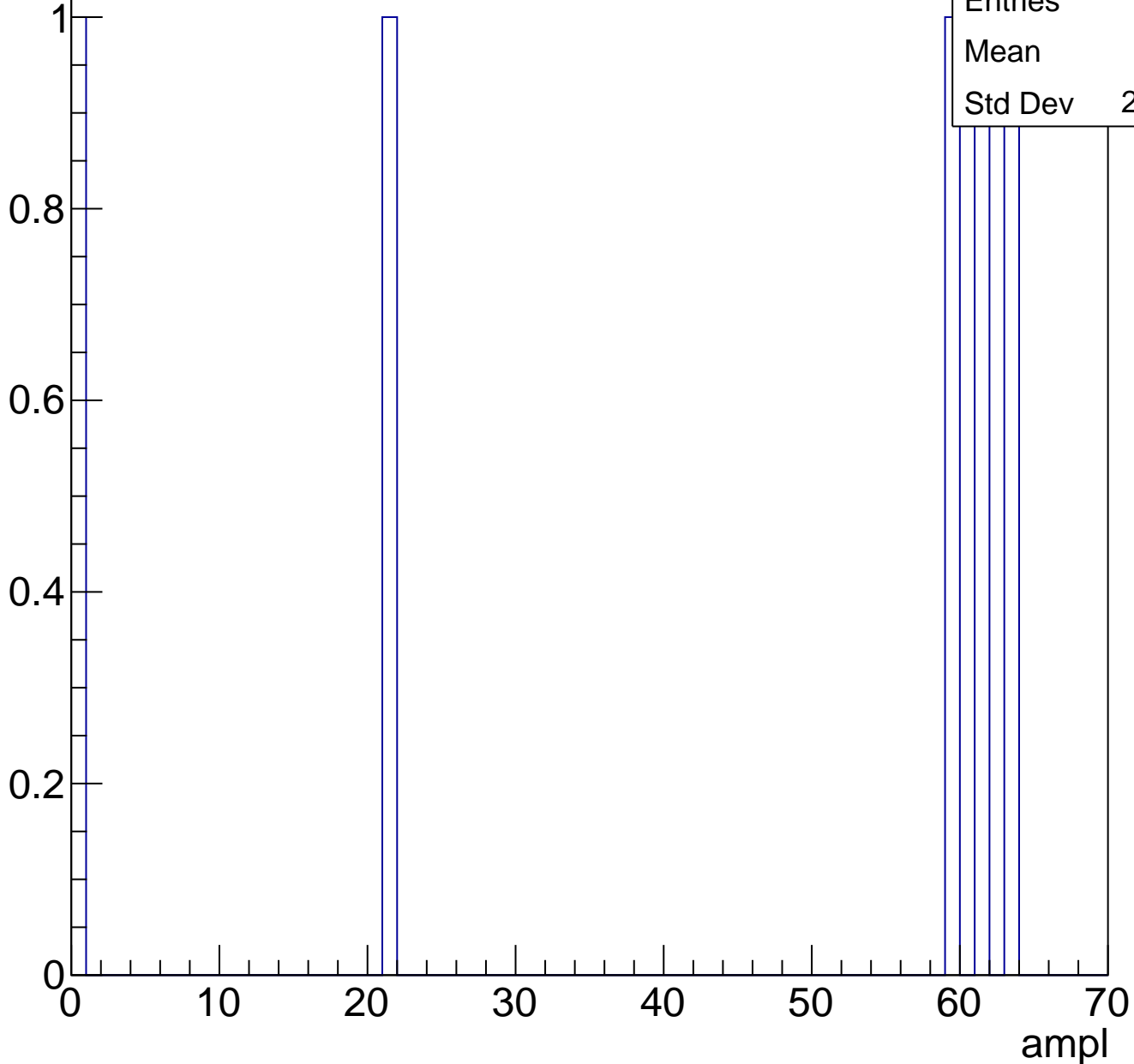
Entry



B1L100S, U15-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch103, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	30.03
Std Dev	3.708

Gaus mean : 30.2024

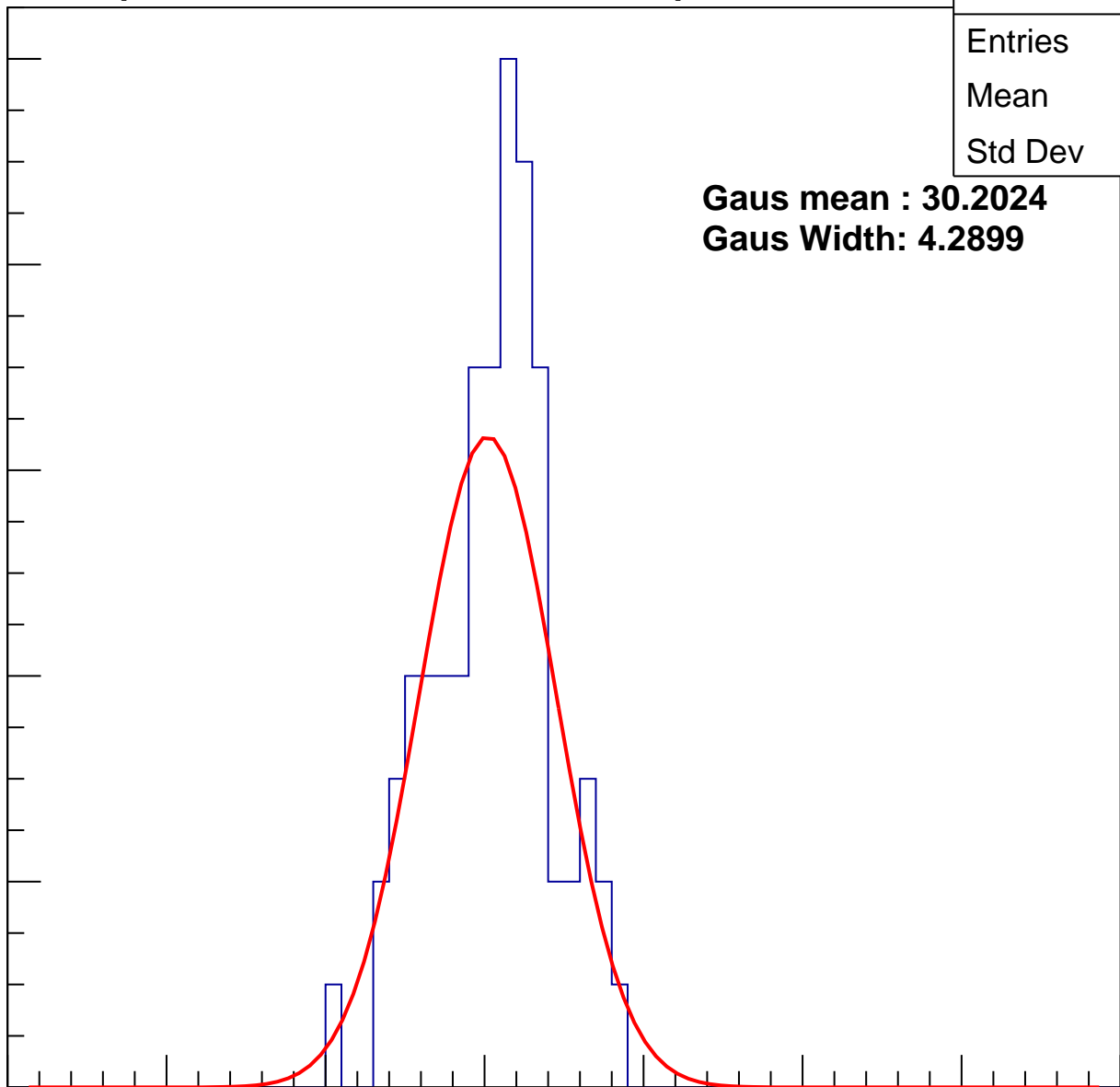
Gaus Width: 4.2899

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch103, adc1

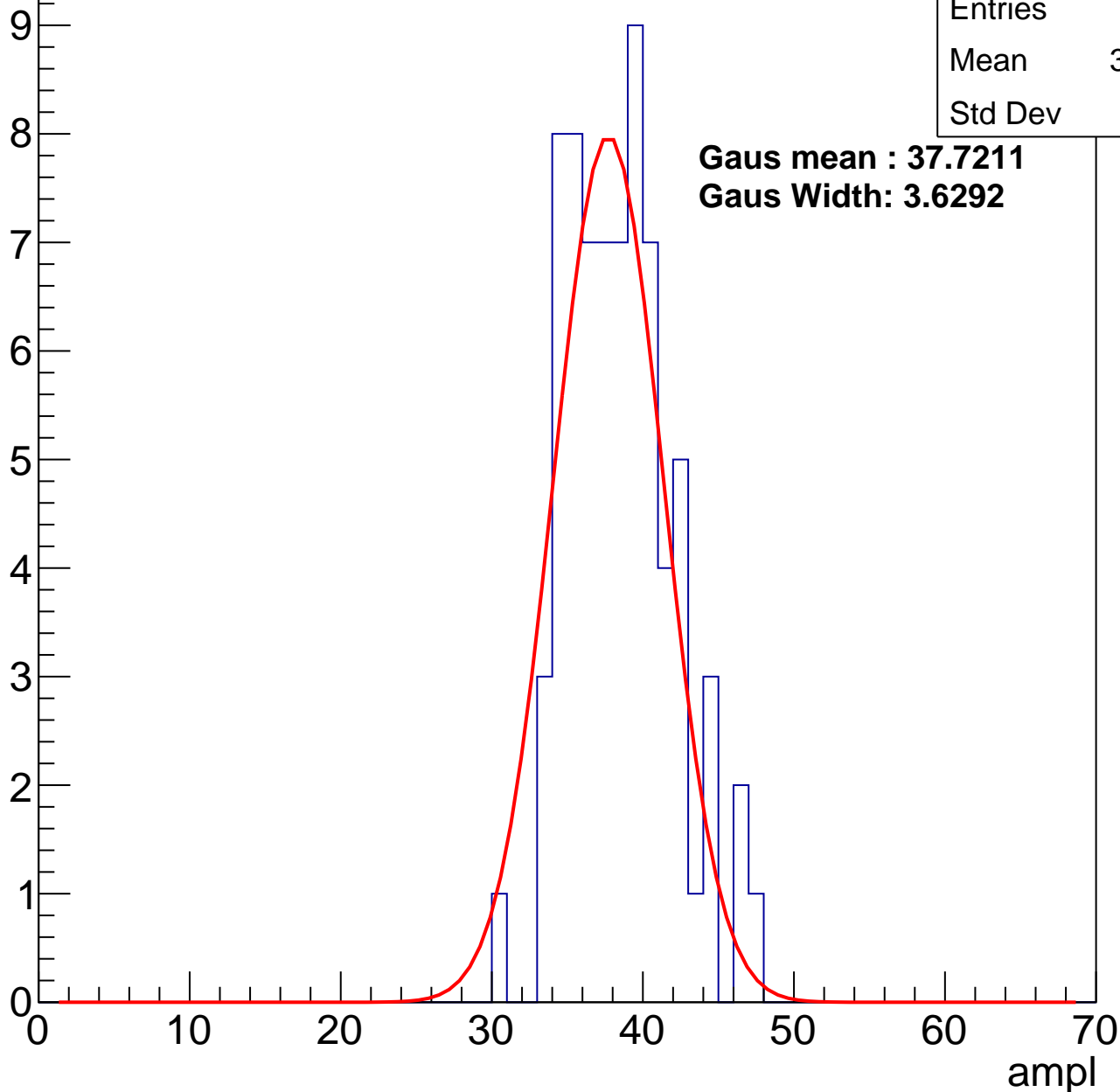
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	38.04
Std Dev	3.45

Gaus mean : 37.7211

Gaus Width: 3.6292



B1L100S, U15-ch103, adc2

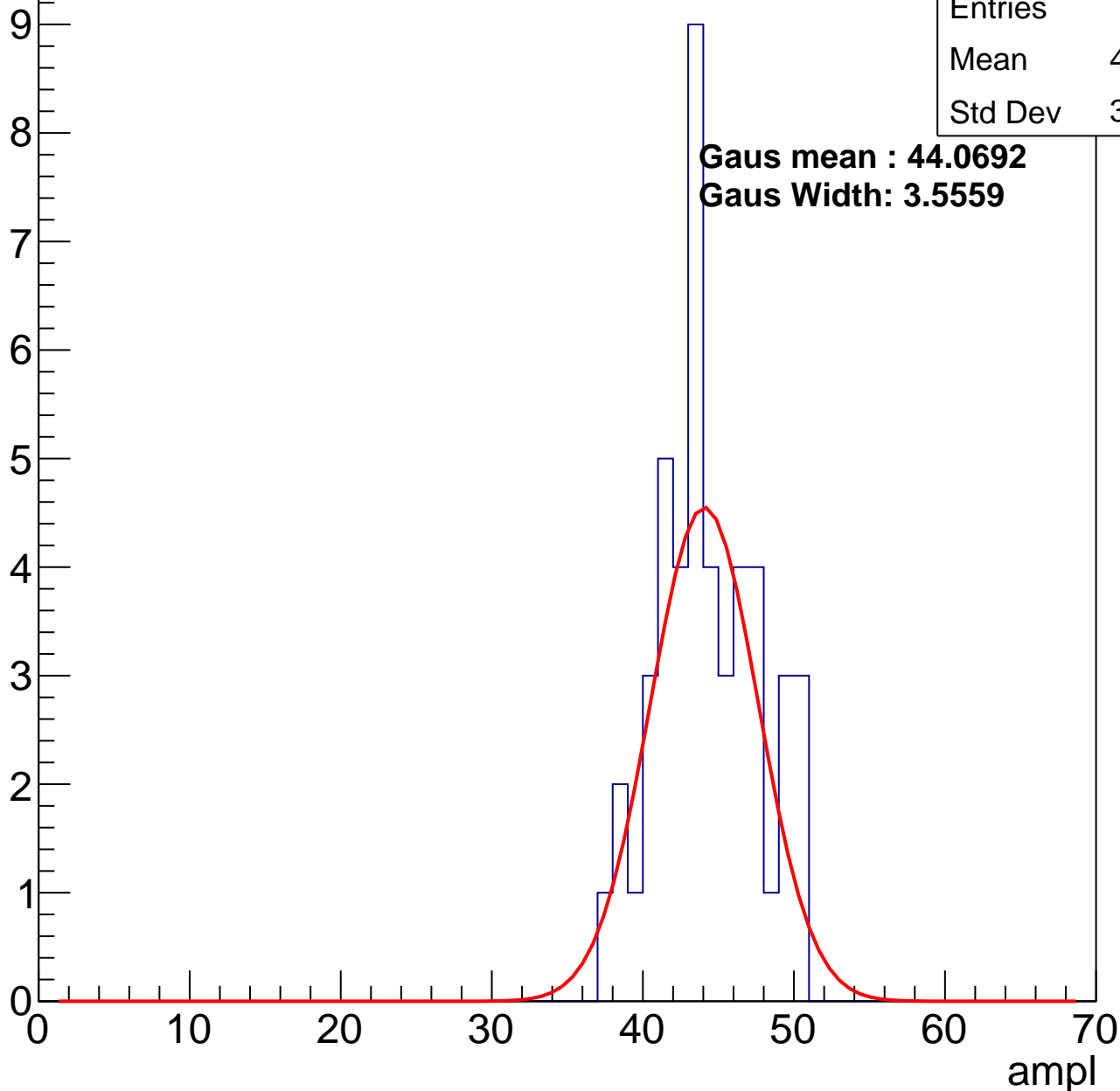
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	43.83
Std Dev	3.315

Gaus mean : 44.0692

Gaus Width: 3.5559

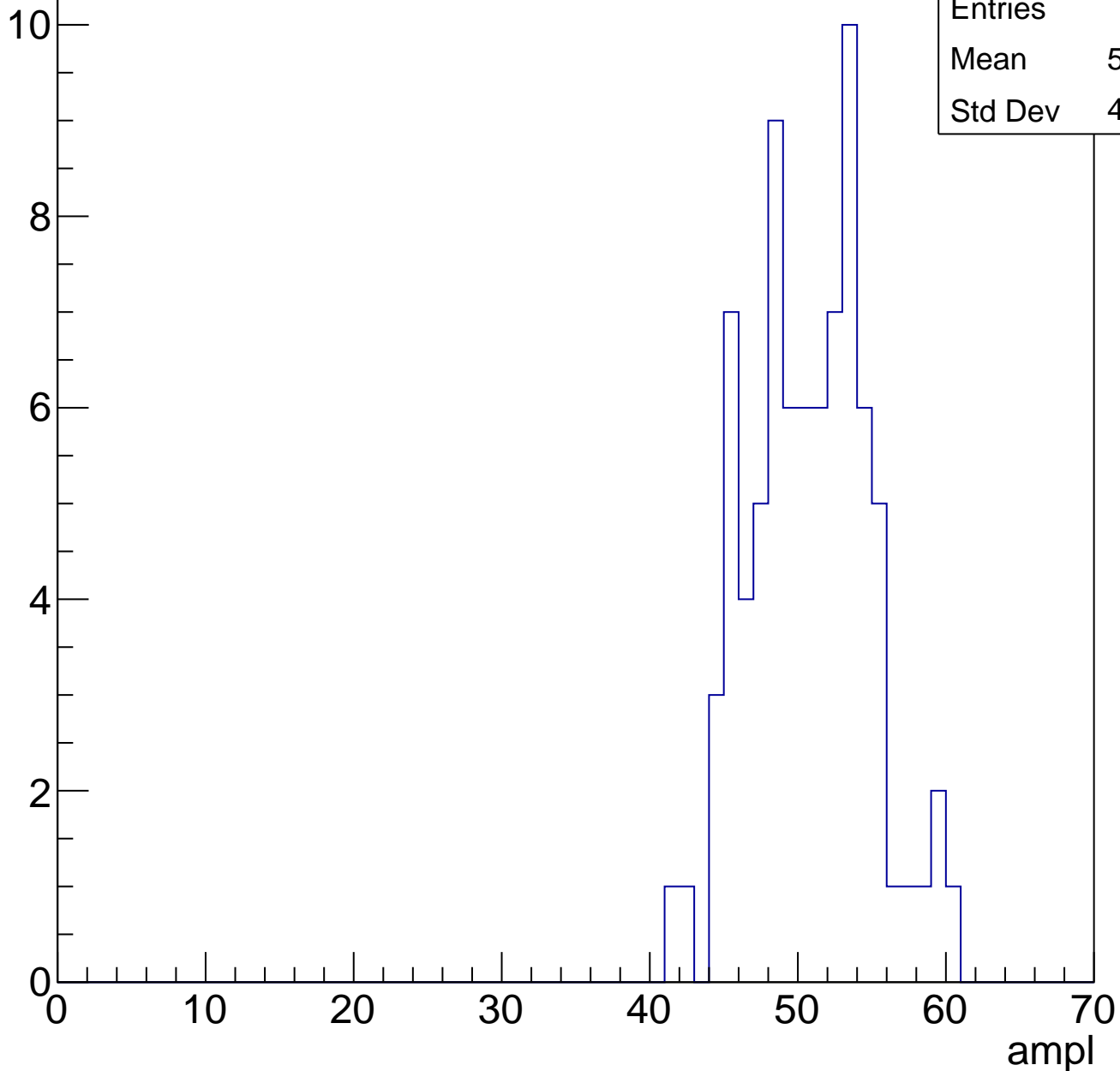


B1L100S, U15-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	50.28
Std Dev	4.025

Entry

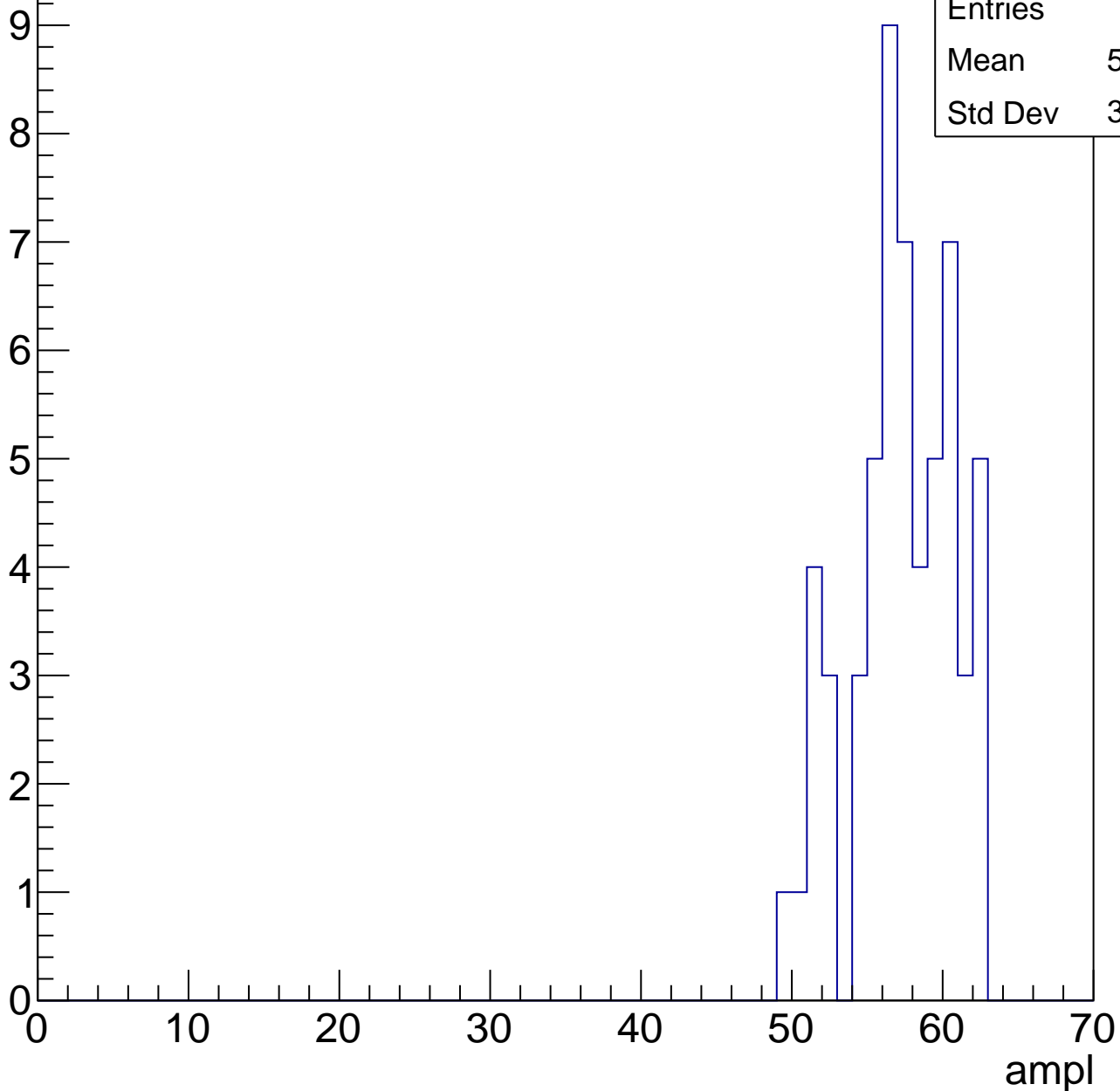


B1L100S, U15-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	56.82
Std Dev	3.367

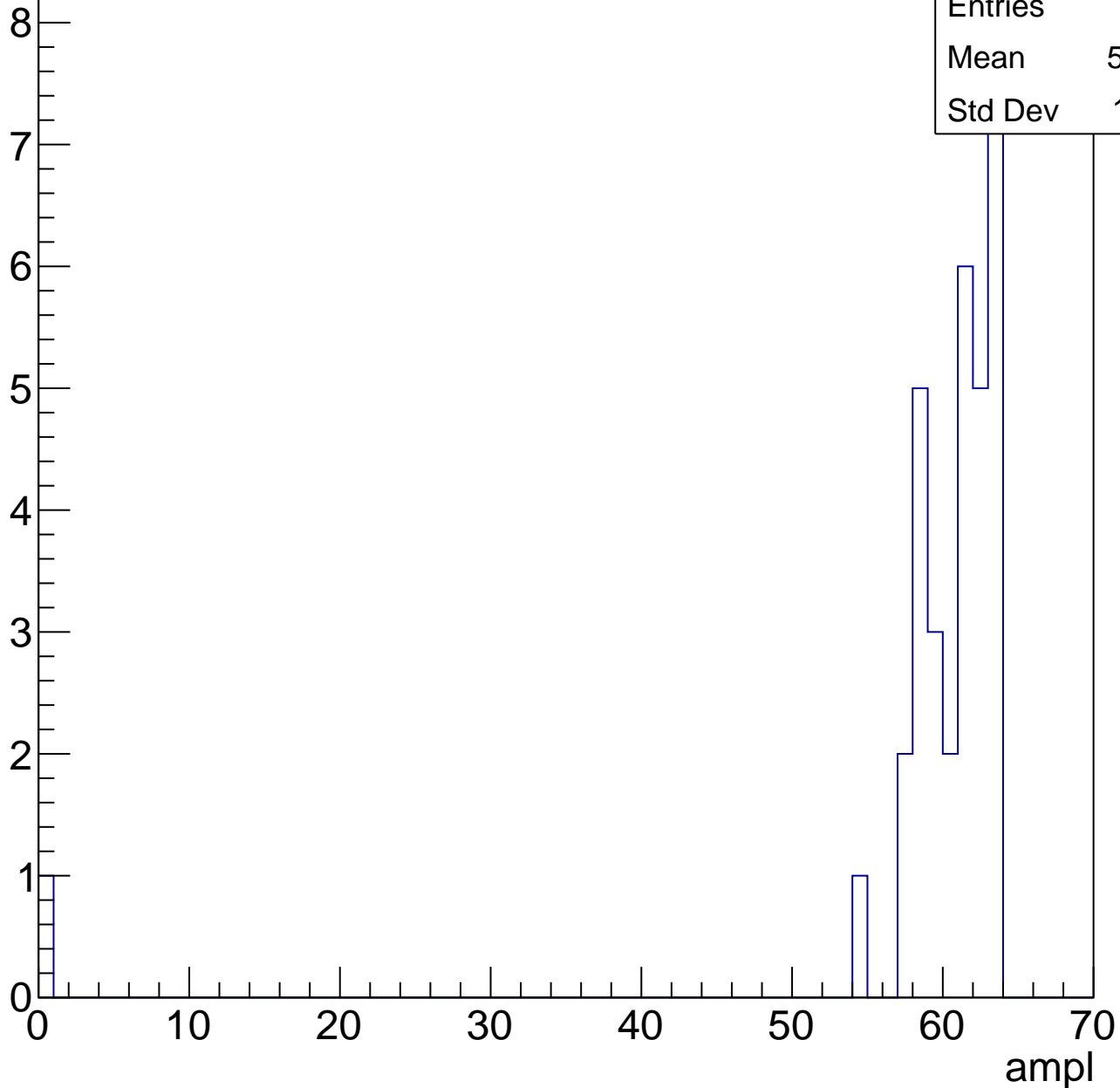


B1L100S, U15-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.64
Std Dev	10.61



B1L100S, U15-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

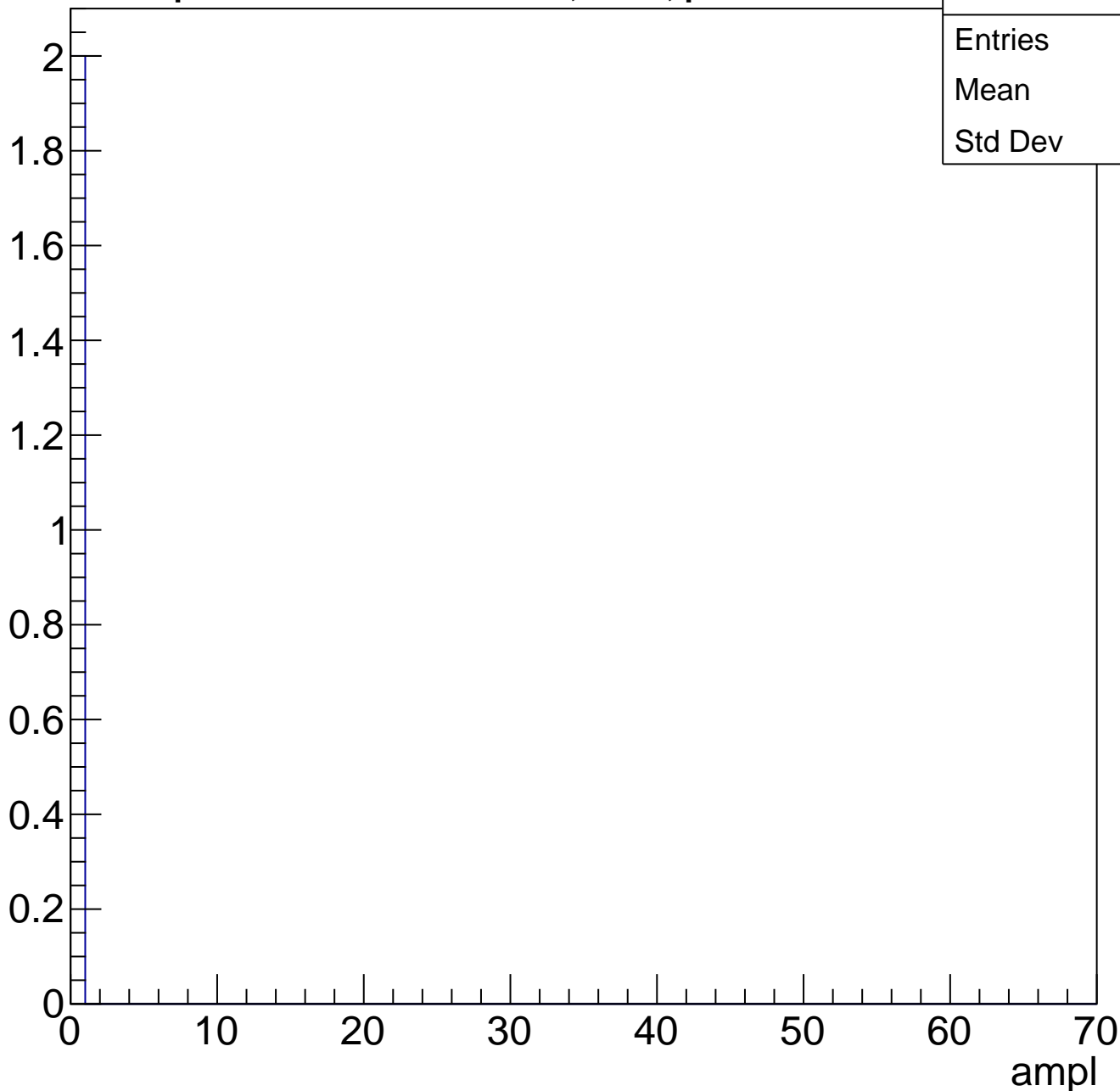
Entry



B1L100S, U15-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U15-ch104, adc0

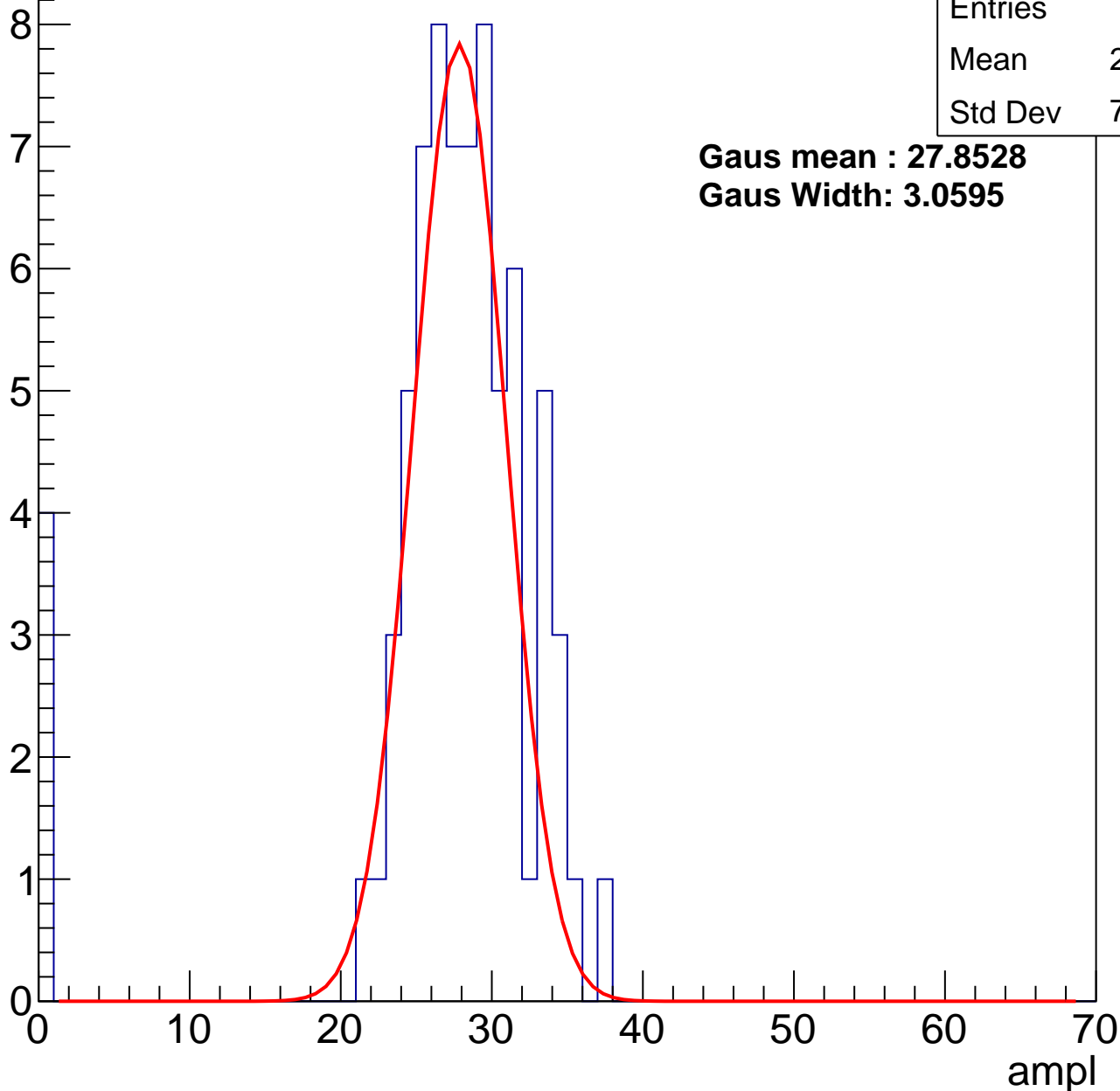
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	26.56
Std Dev	7.203

Gaus mean : 27.8528

Gaus Width: 3.0595



B1L100S, U15-ch104, adc1

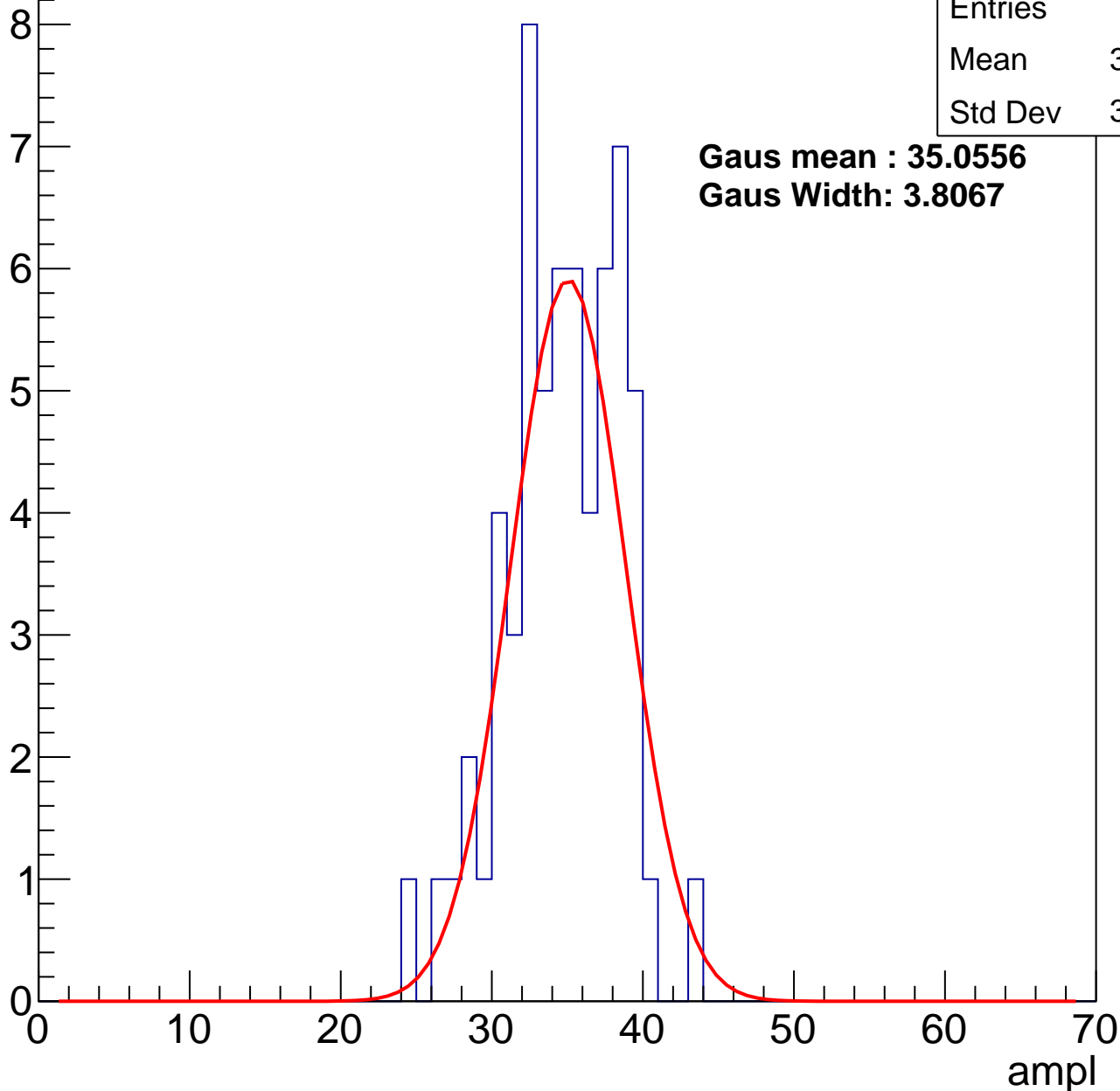
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	34.19
Std Dev	3.737

Gaus mean : 35.0556

Gaus Width: 3.8067



B1L100S, U15-ch104, adc2

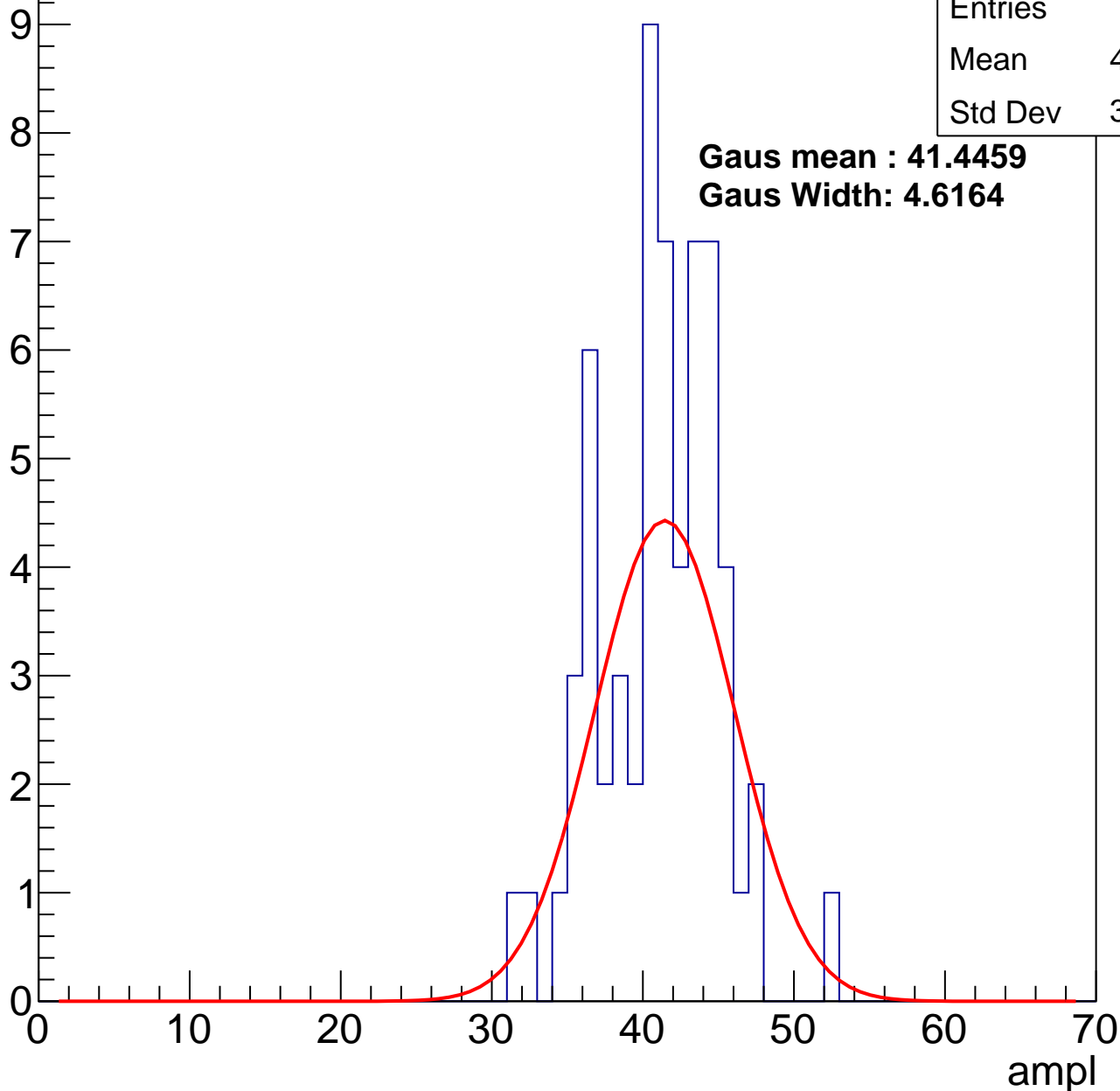
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	40.66
Std Dev	3.913

Gaus mean : 41.4459

Gaus Width: 4.6164

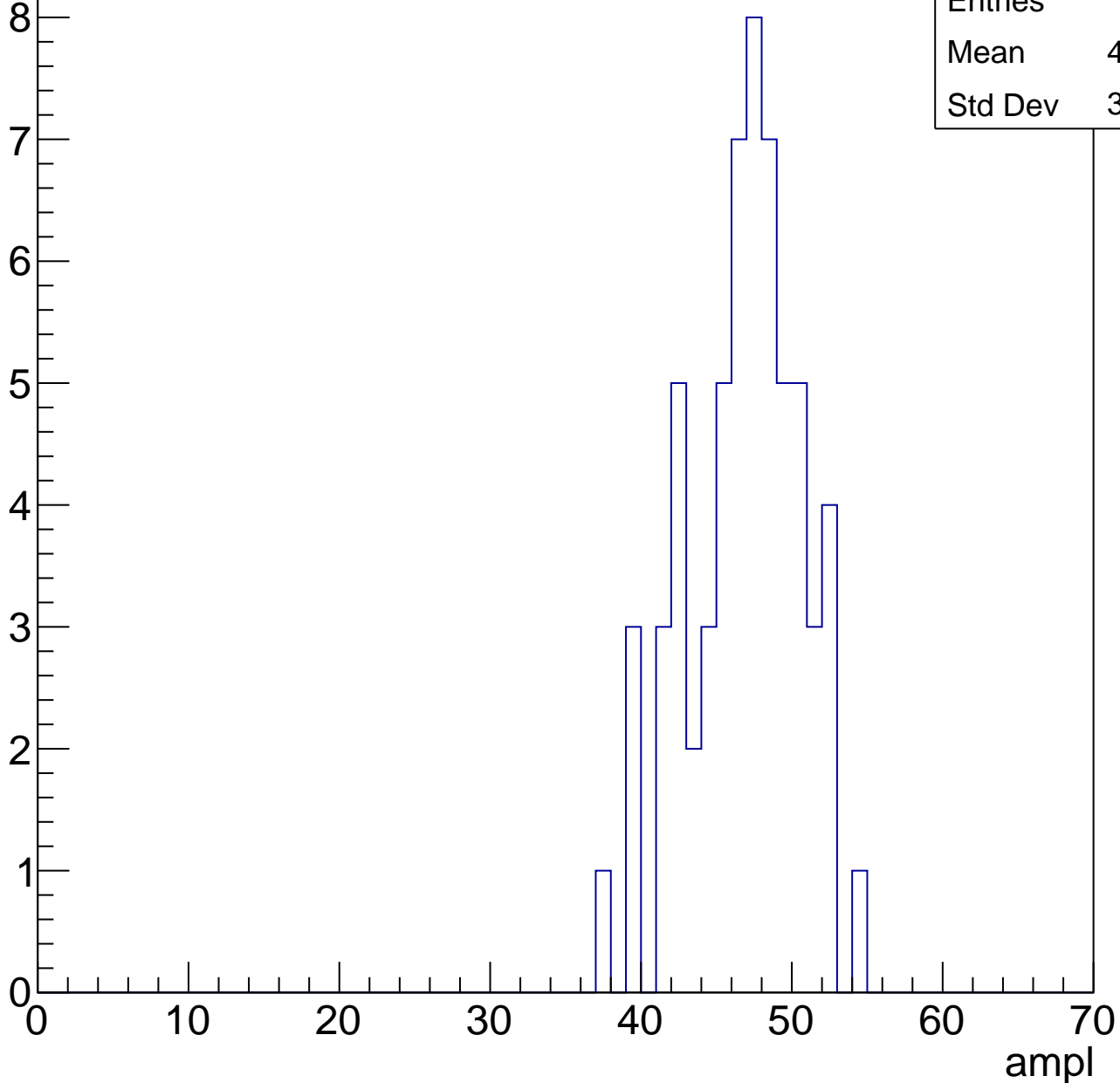


B1L100S, U15-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	46.35
Std Dev	3.712

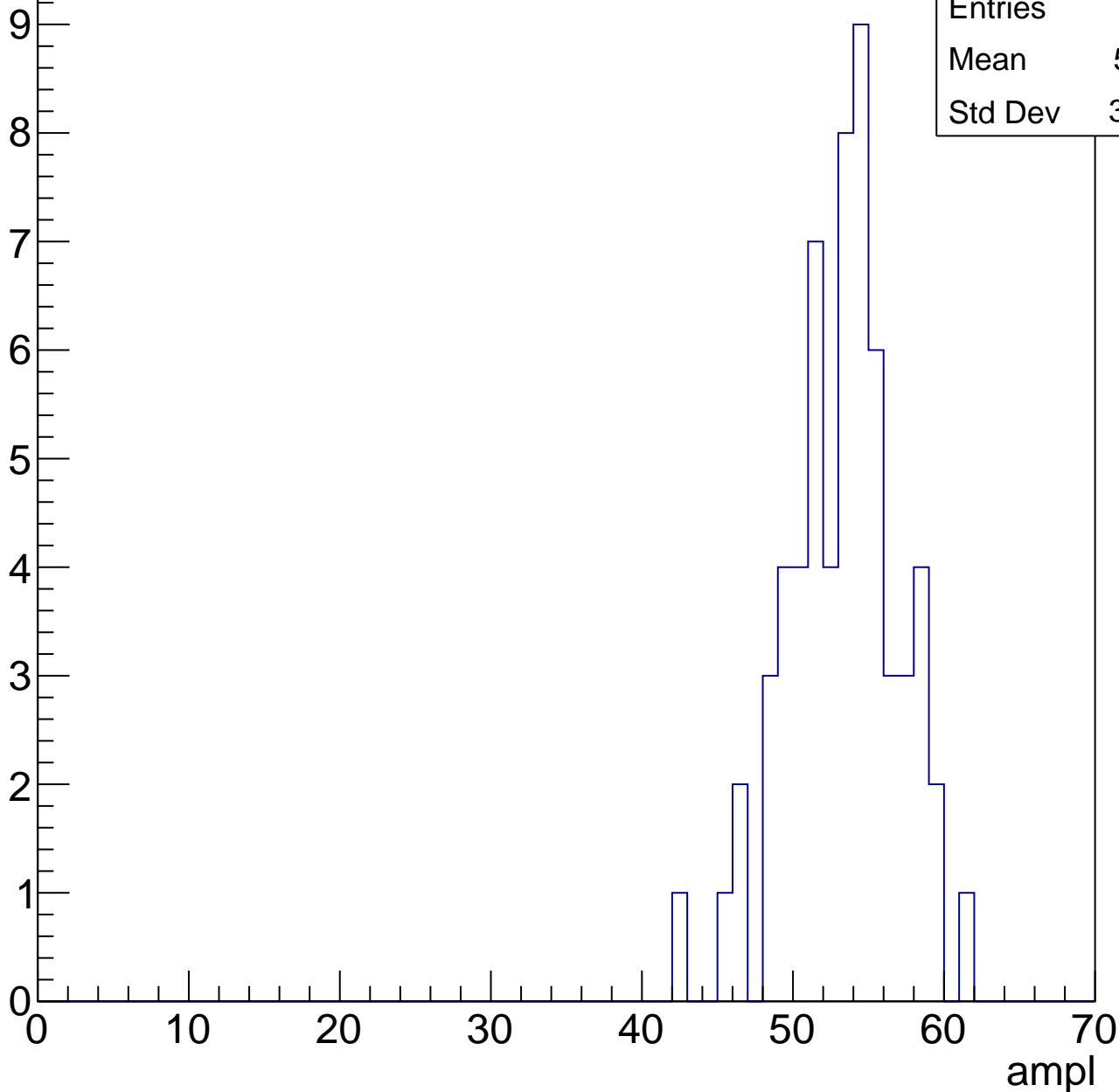


B1L100S, U15-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	52.81
Std Dev	3.663

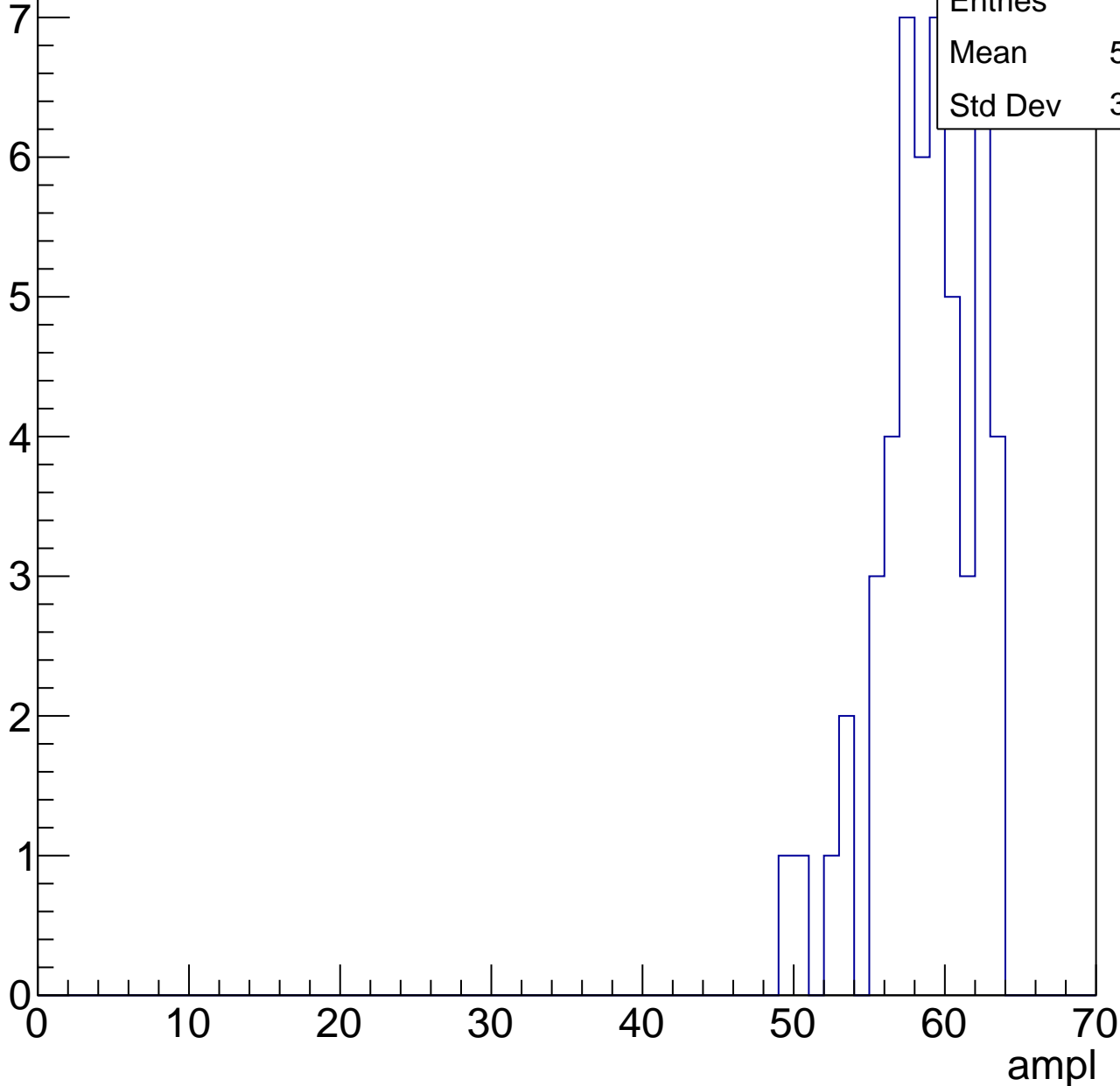


B1L100S, U15-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	58.33
Std Dev	3.258

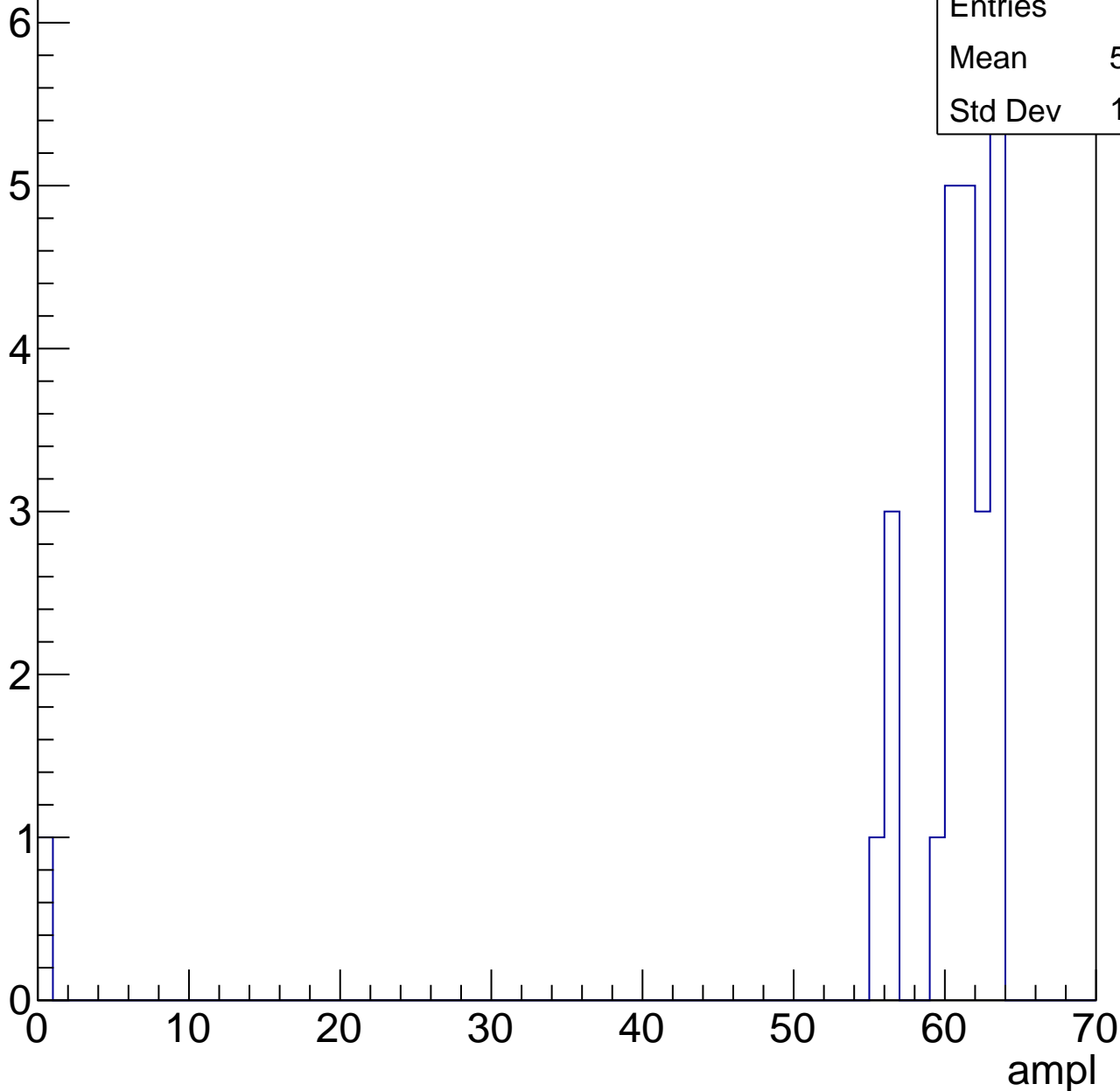


B1L100S, U15-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

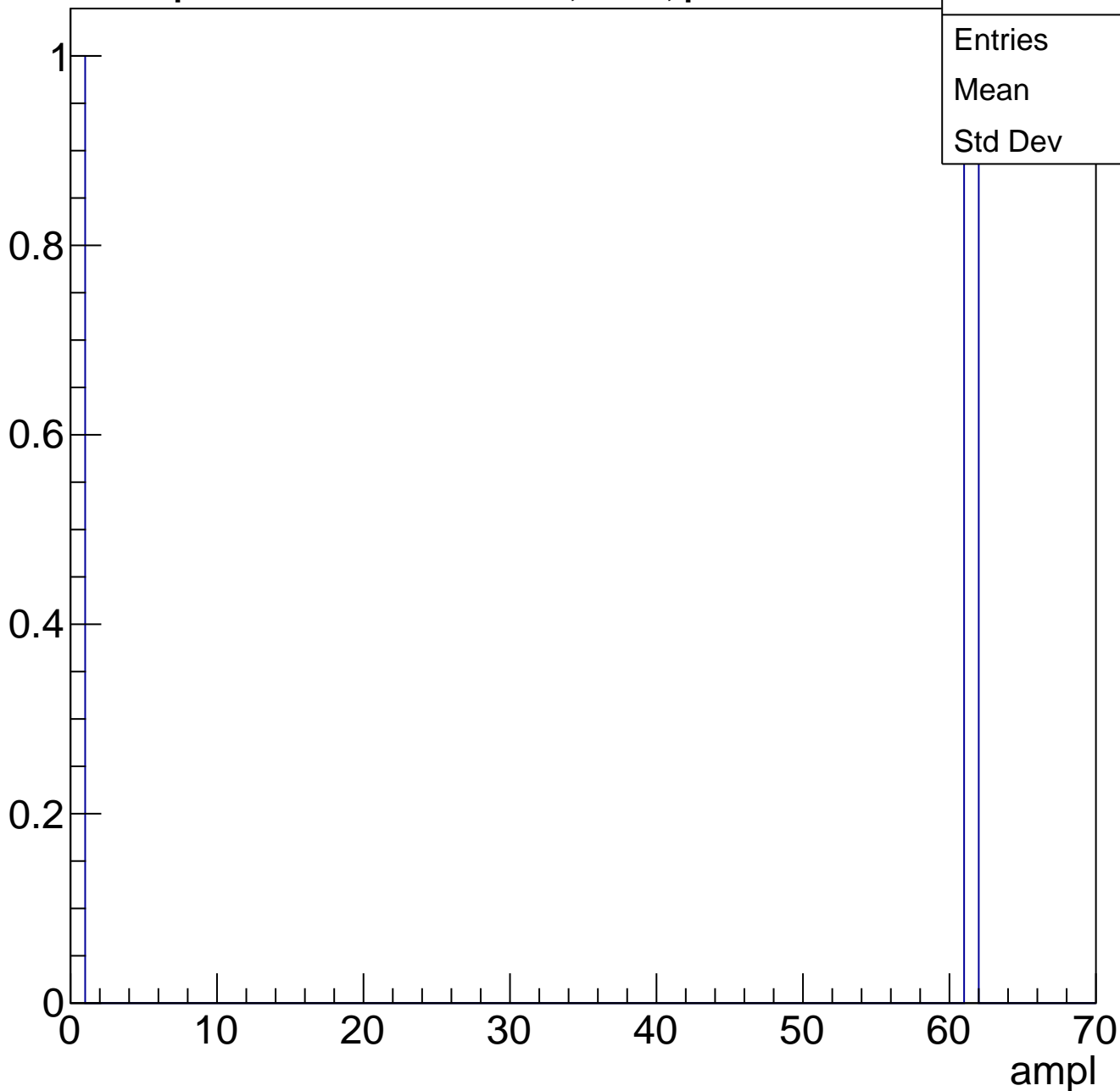
Entries	25
Mean	58.04
Std Dev	12.08



B1L100S, U15-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch105, adc0

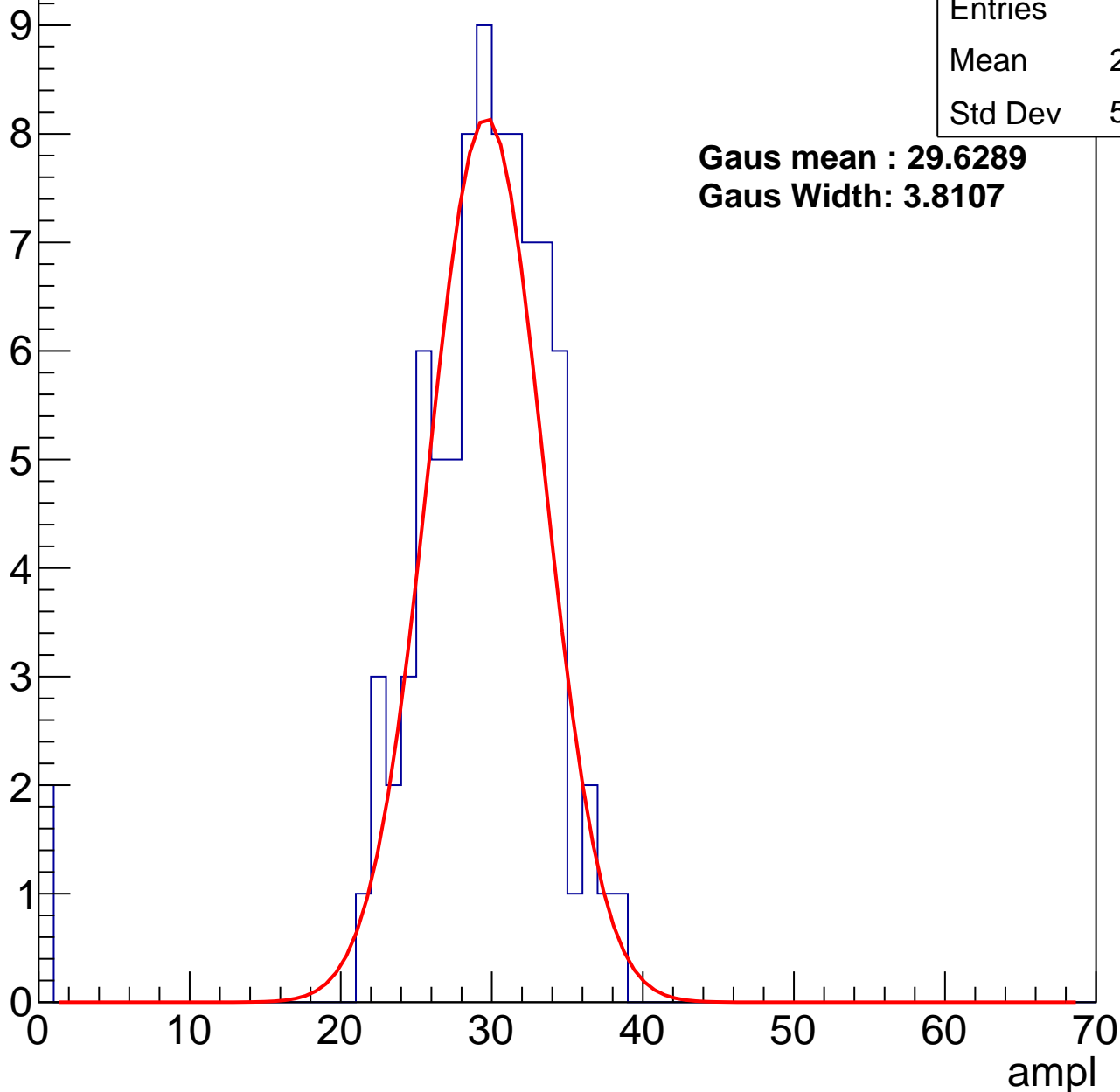
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	28.64
Std Dev	5.782

Gaus mean : 29.6289

Gaus Width: 3.8107



B1L100S, U15-ch105, adc1

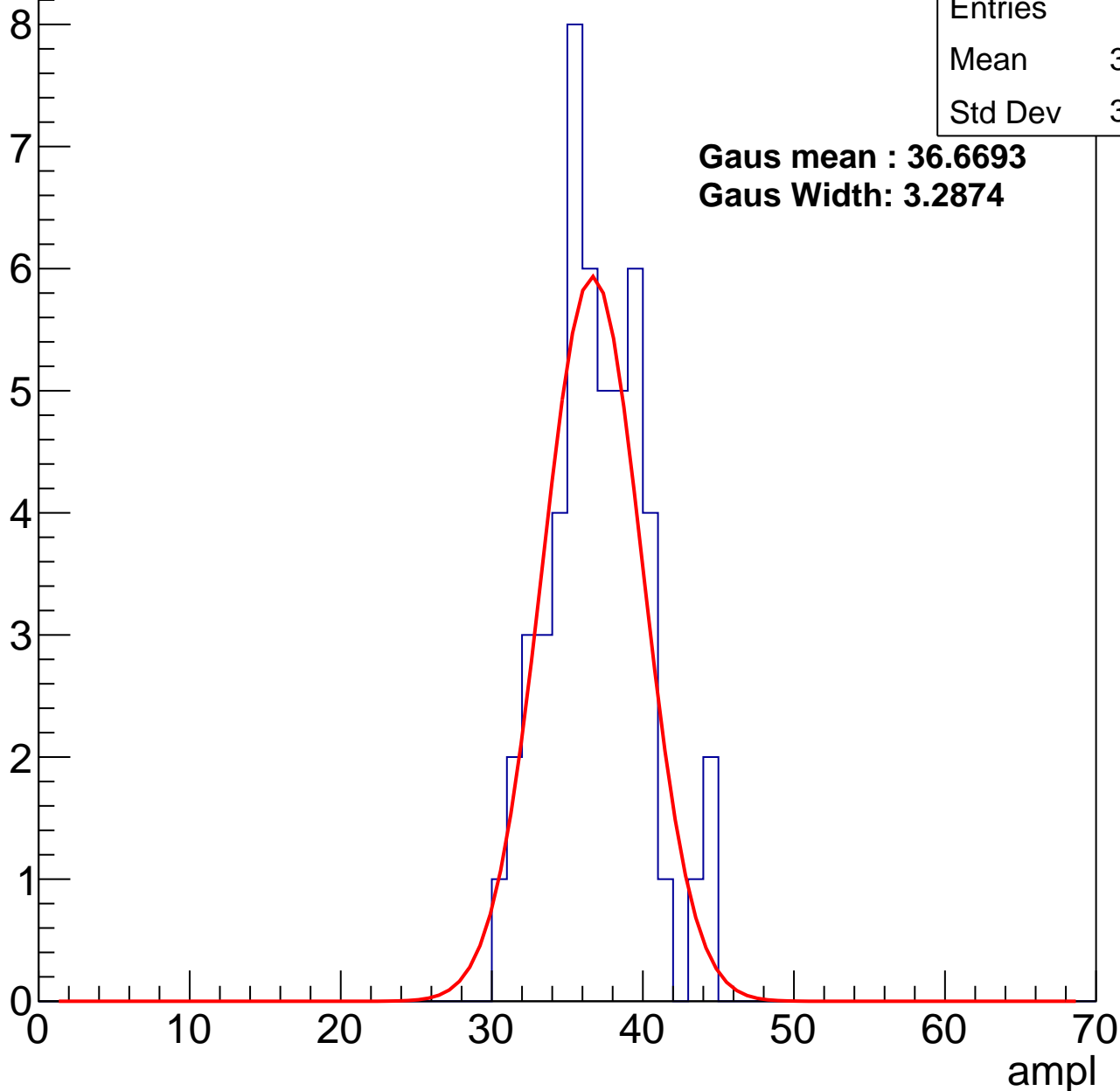
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	36.47
Std Dev	3.189

Gaus mean : 36.6693

Gaus Width: 3.2874



B1L100S, U15-ch105, adc2

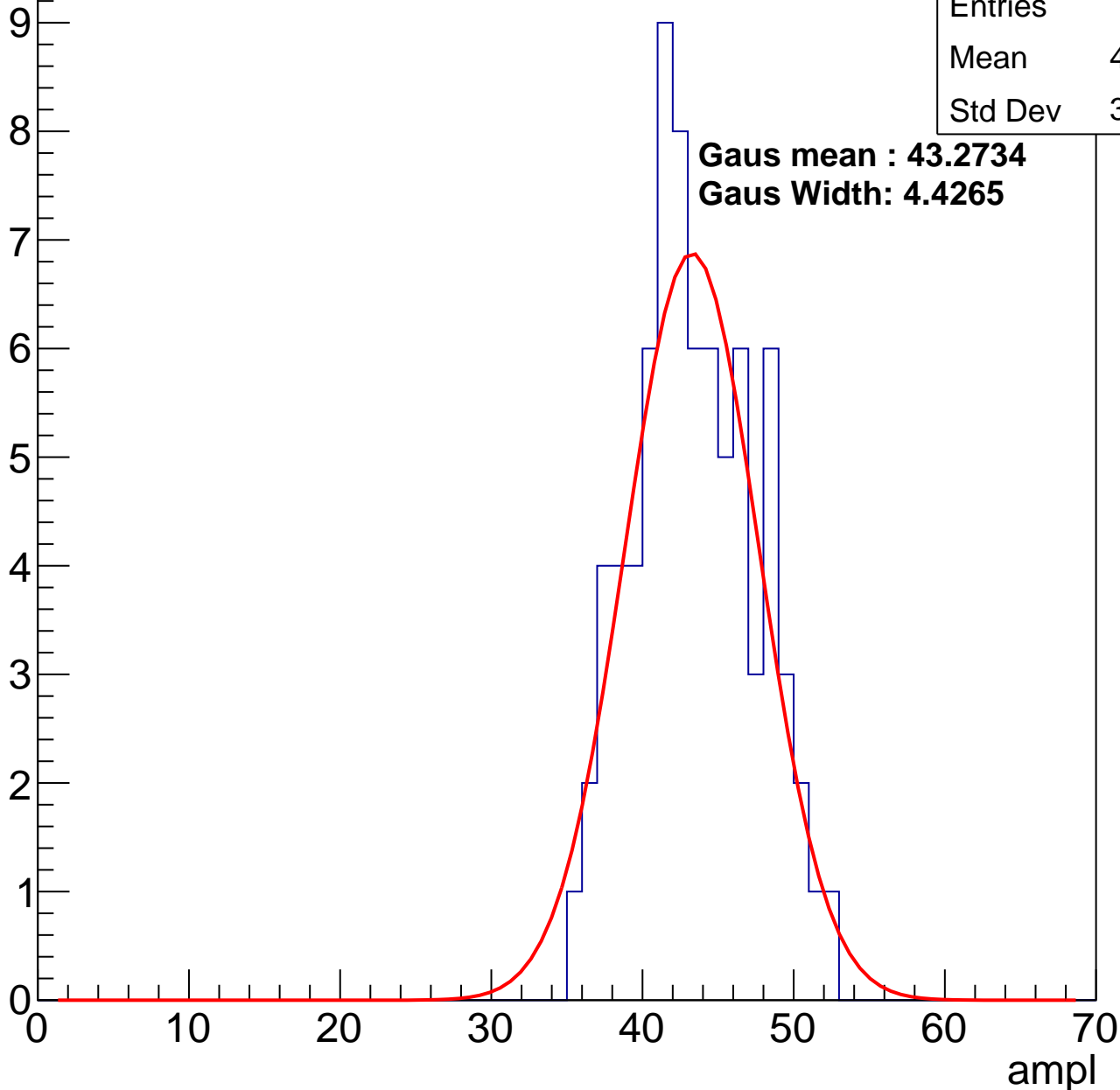
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	42.99
Std Dev	3.943

Gaus mean : 43.2734

Gaus Width: 4.4265

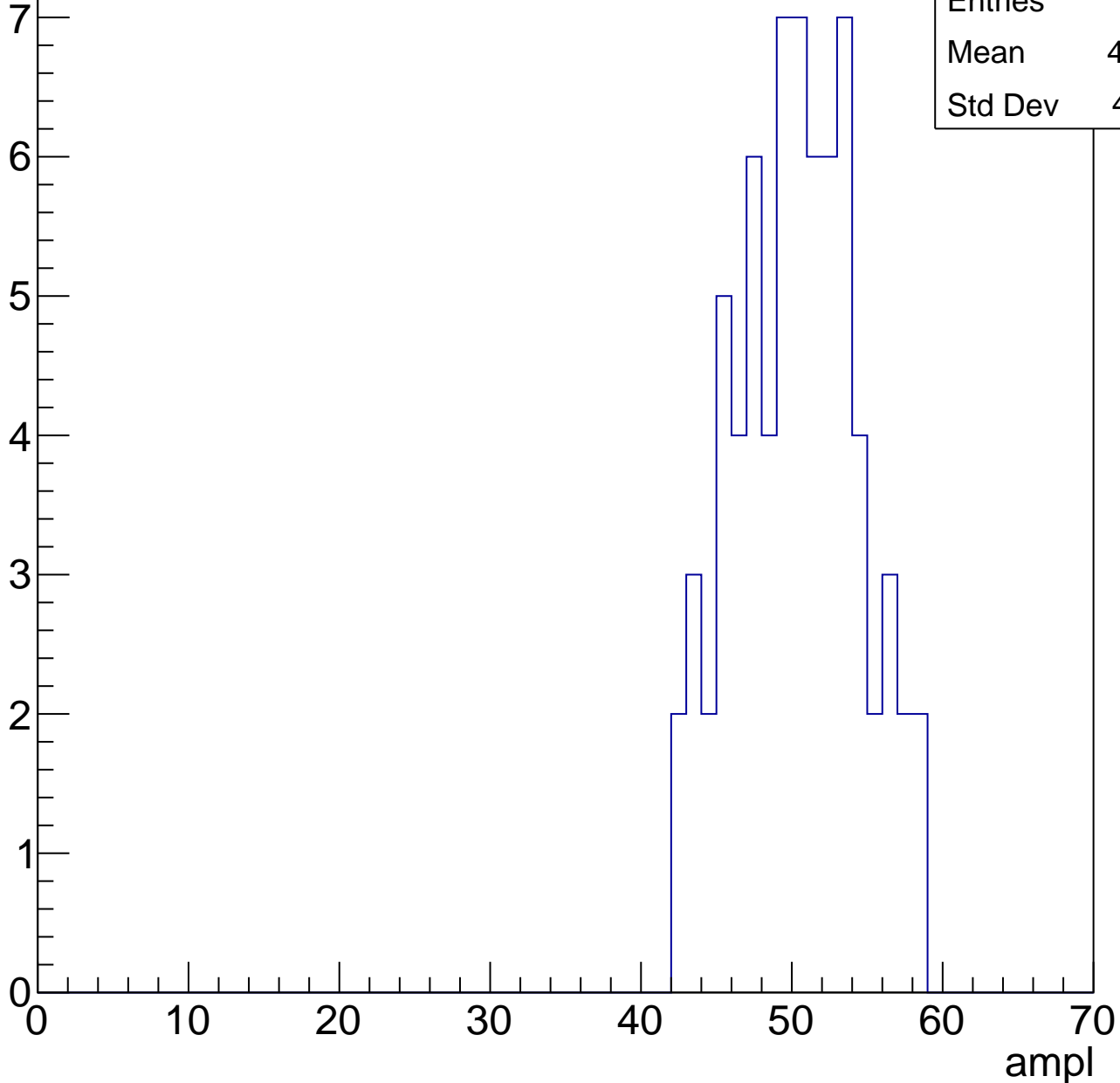


B1L100S, U15-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	49.86
Std Dev	4.001

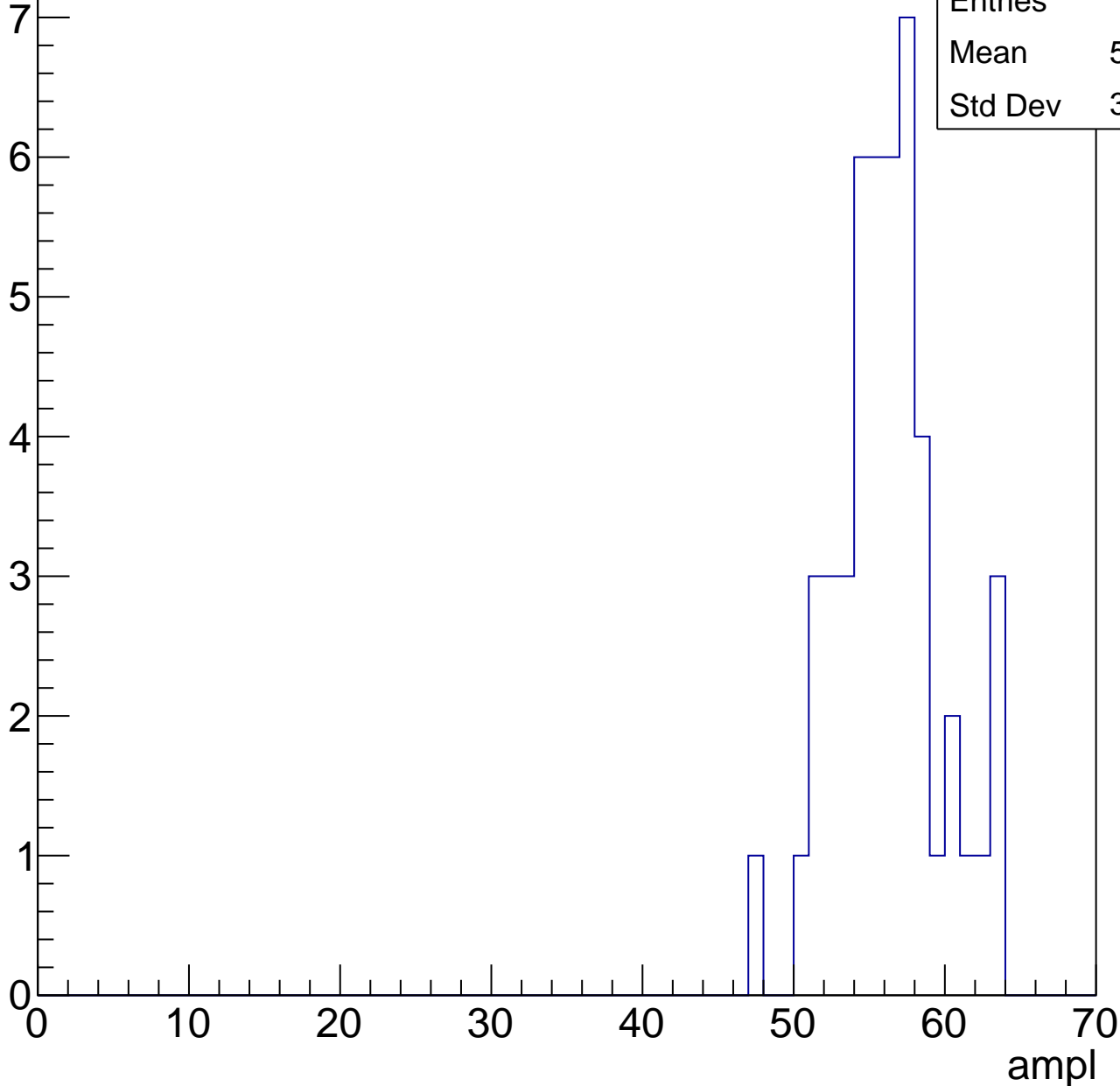


B1L100S, U15-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

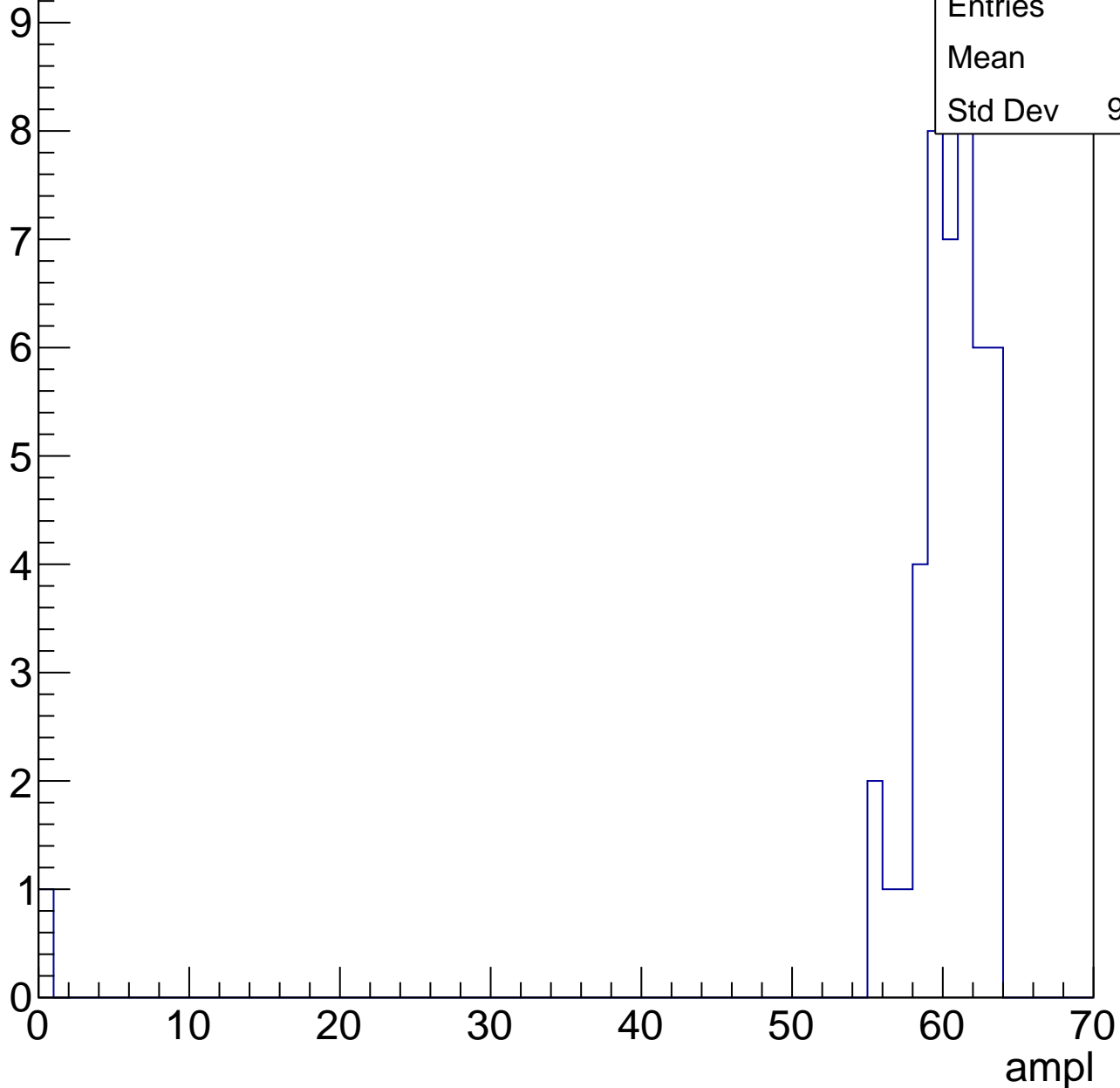
Entries	48
Mean	55.77
Std Dev	3.435



B1L100S, U15-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

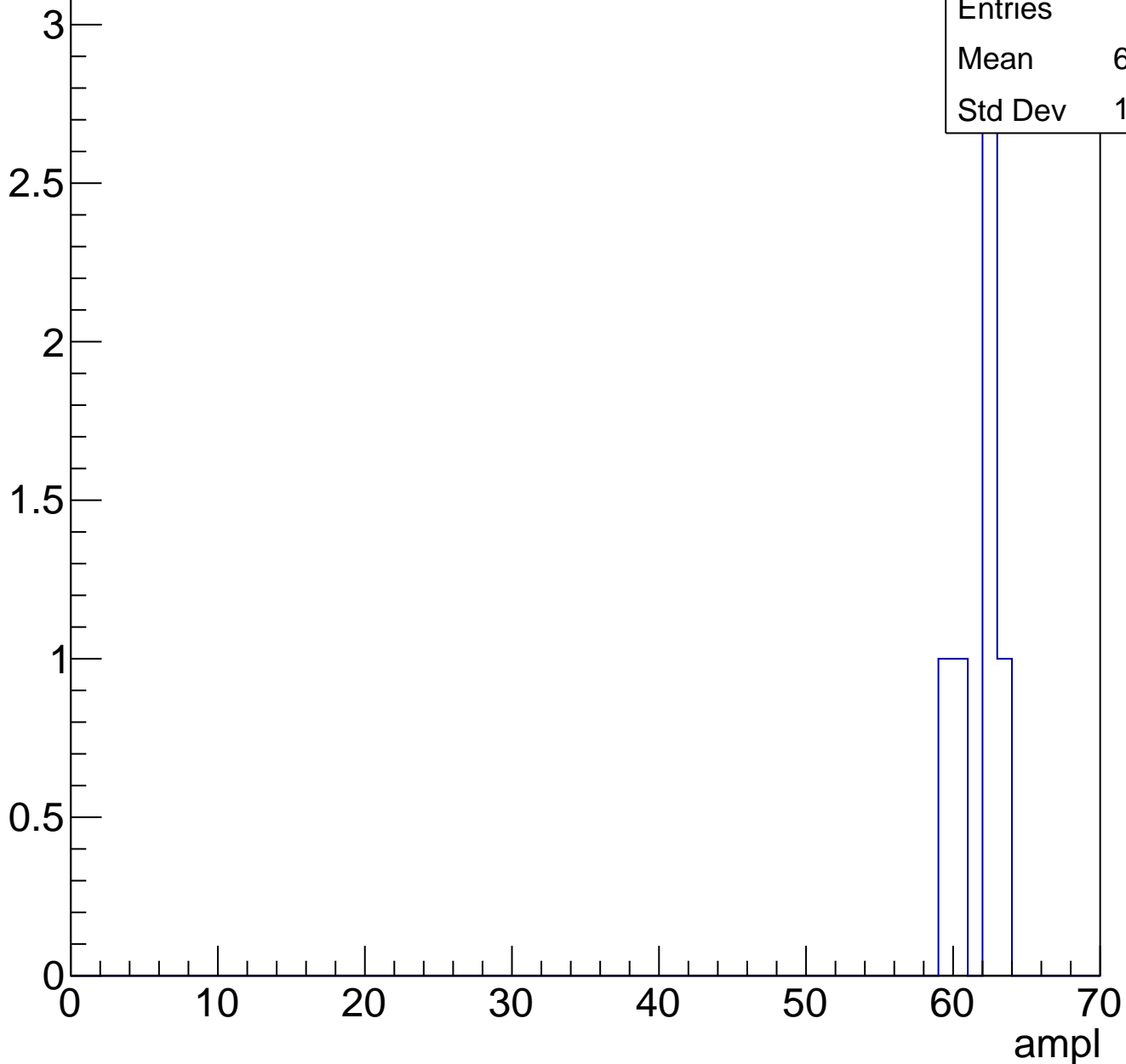
Entry



B1L100S, U15-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch106, adc0

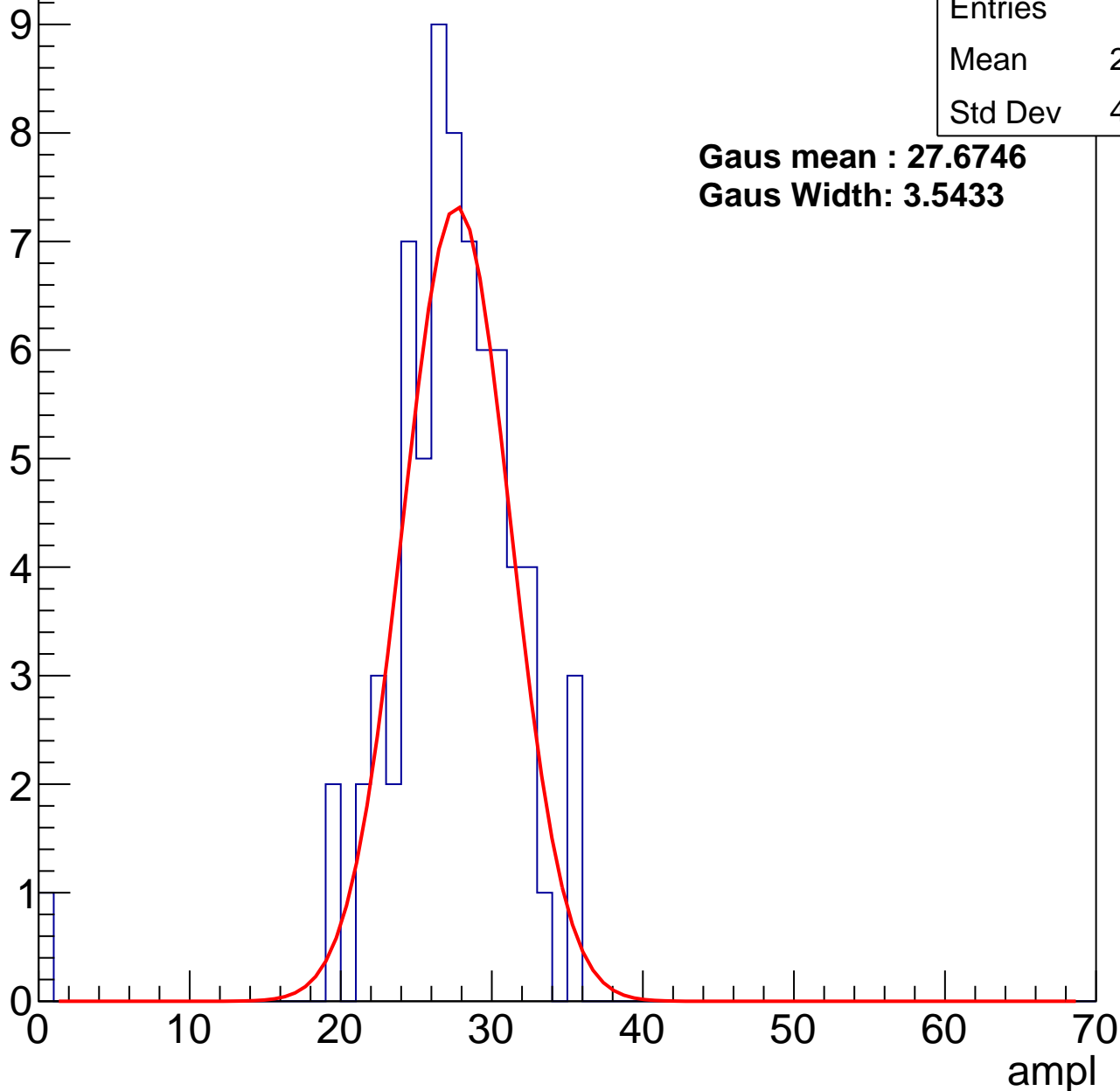
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	26.79
Std Dev	4.784

Gaus mean : 27.6746

Gaus Width: 3.5433



B1L100S, U15-ch106, adc1

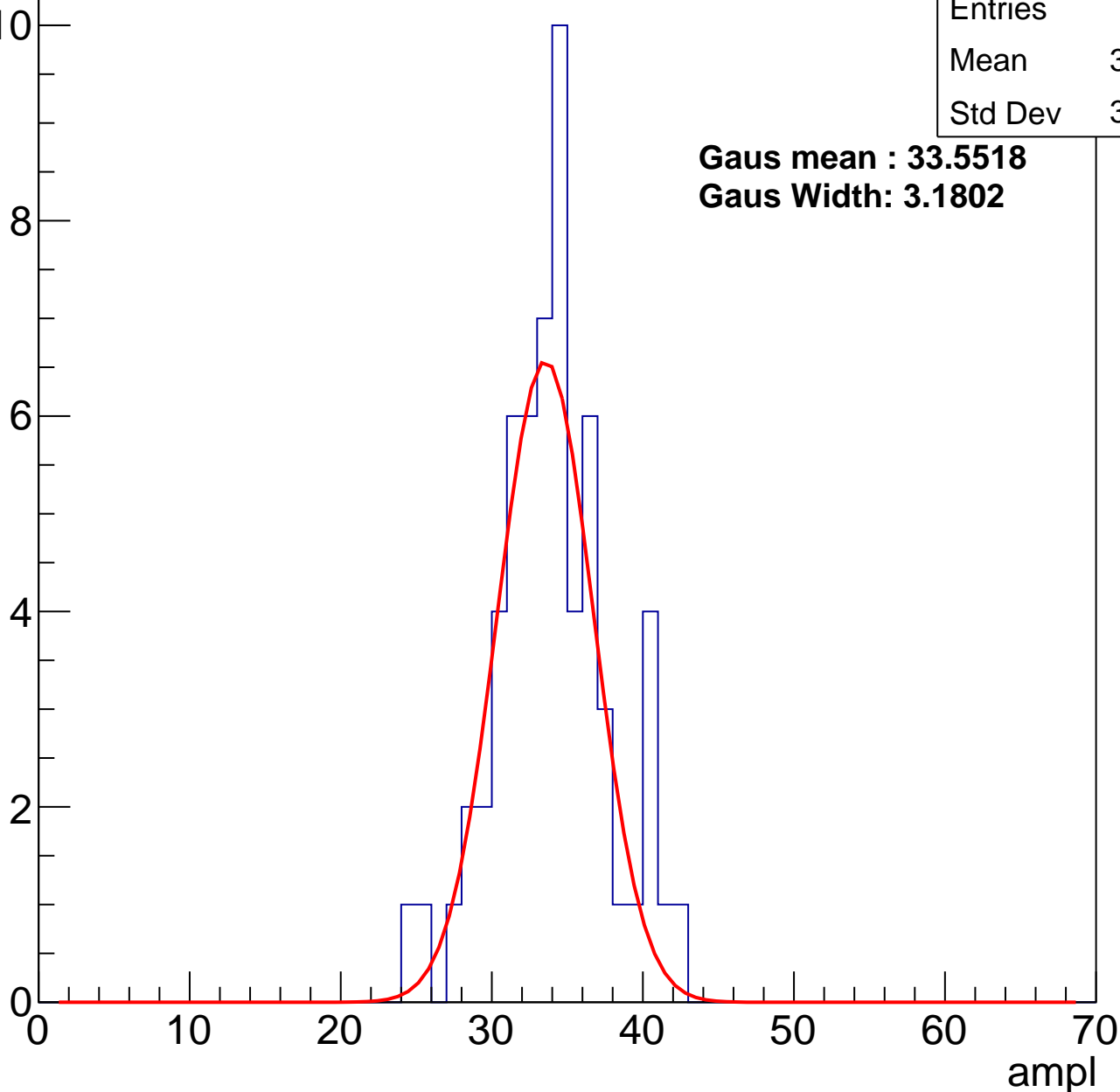
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	33.54
Std Dev	3.713

Gaus mean : 33.5518

Gaus Width: 3.1802



B1L100S, U15-ch106, adc2

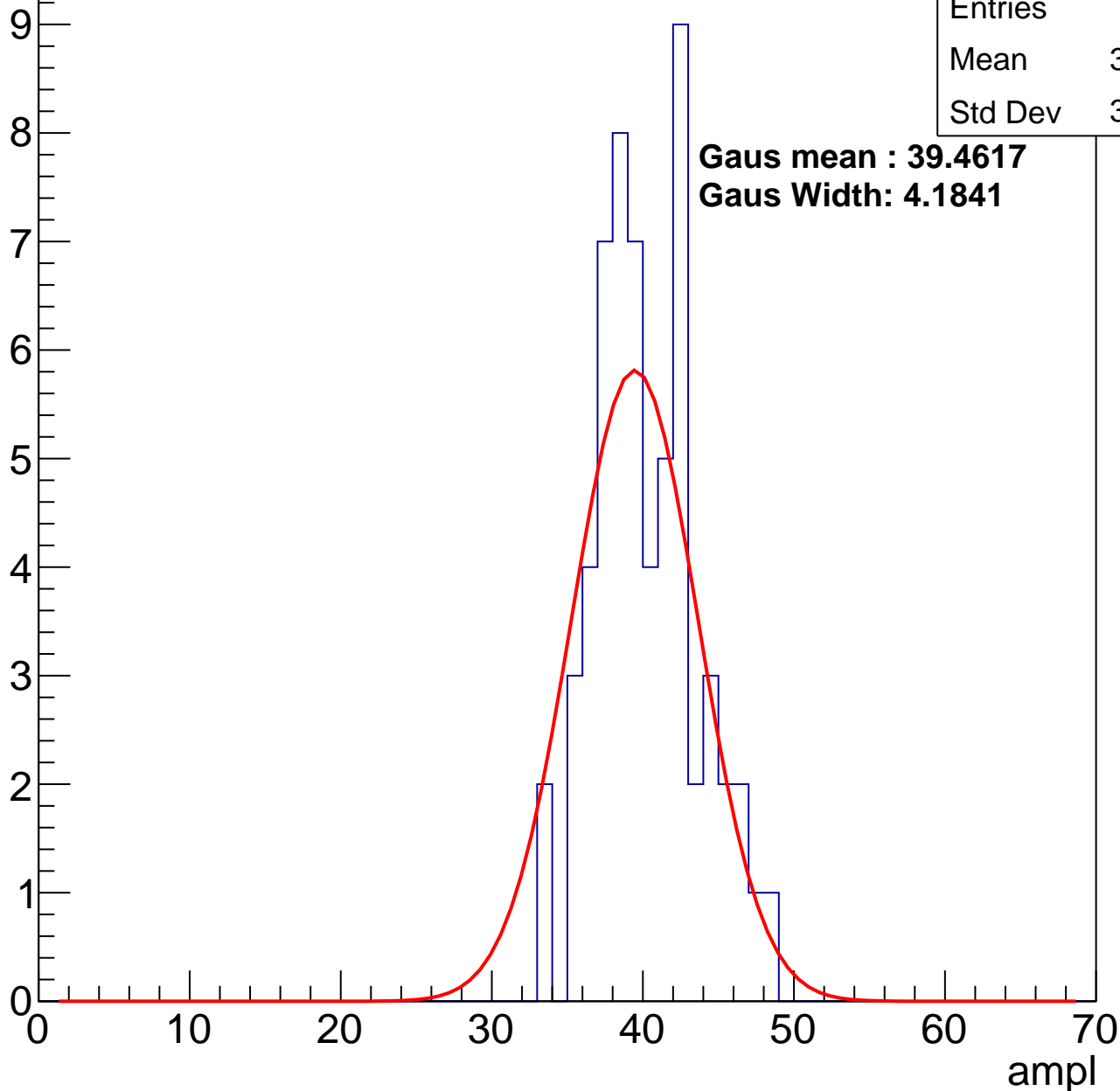
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	39.82
Std Dev	3.364

Gaus mean : 39.4617

Gaus Width: 4.1841

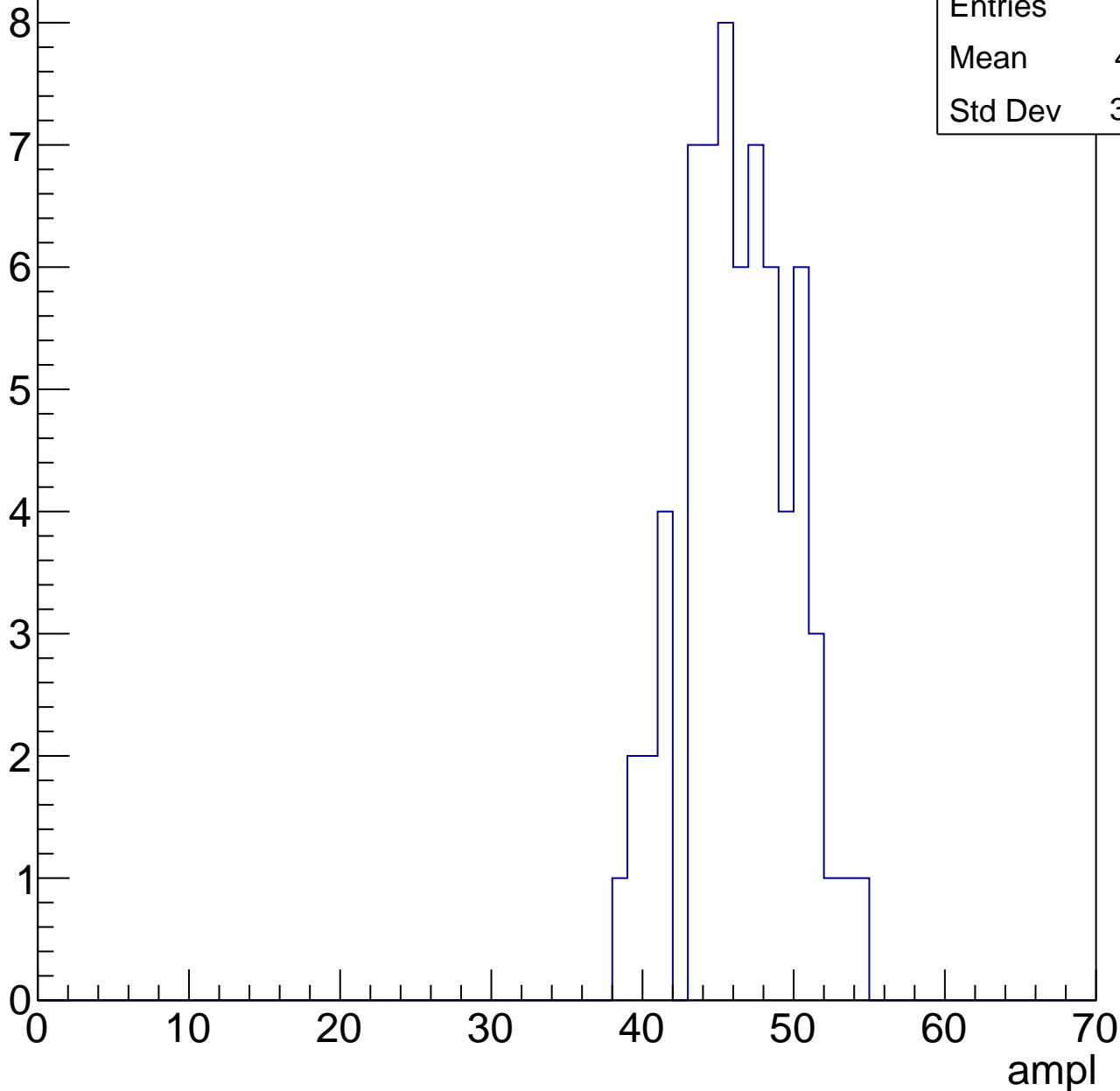


B1L100S, U15-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	45.91
Std Dev	3.524

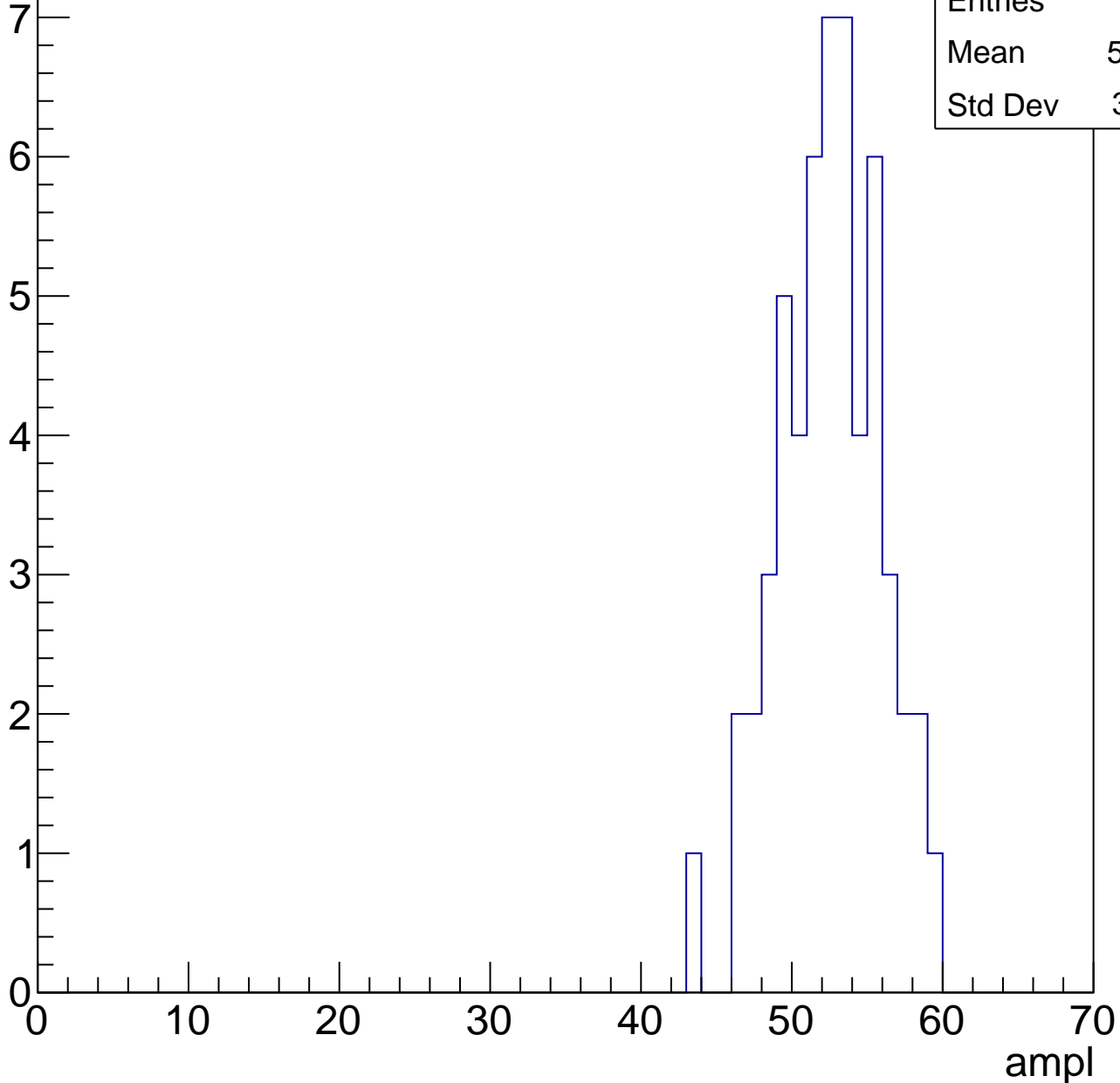


B1L100S, U15-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	52.04
Std Dev	3.341

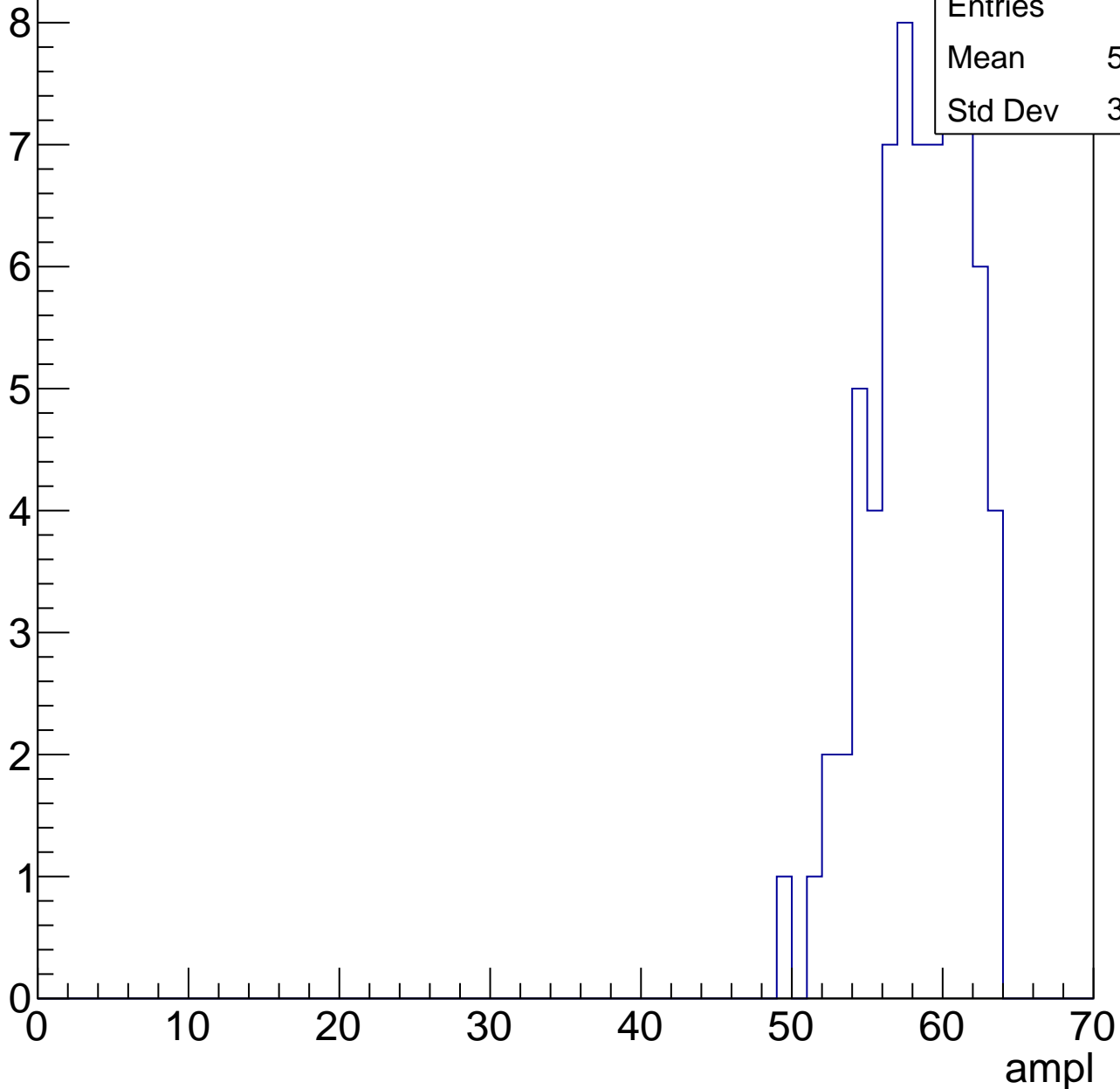


B1L100S, U15-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

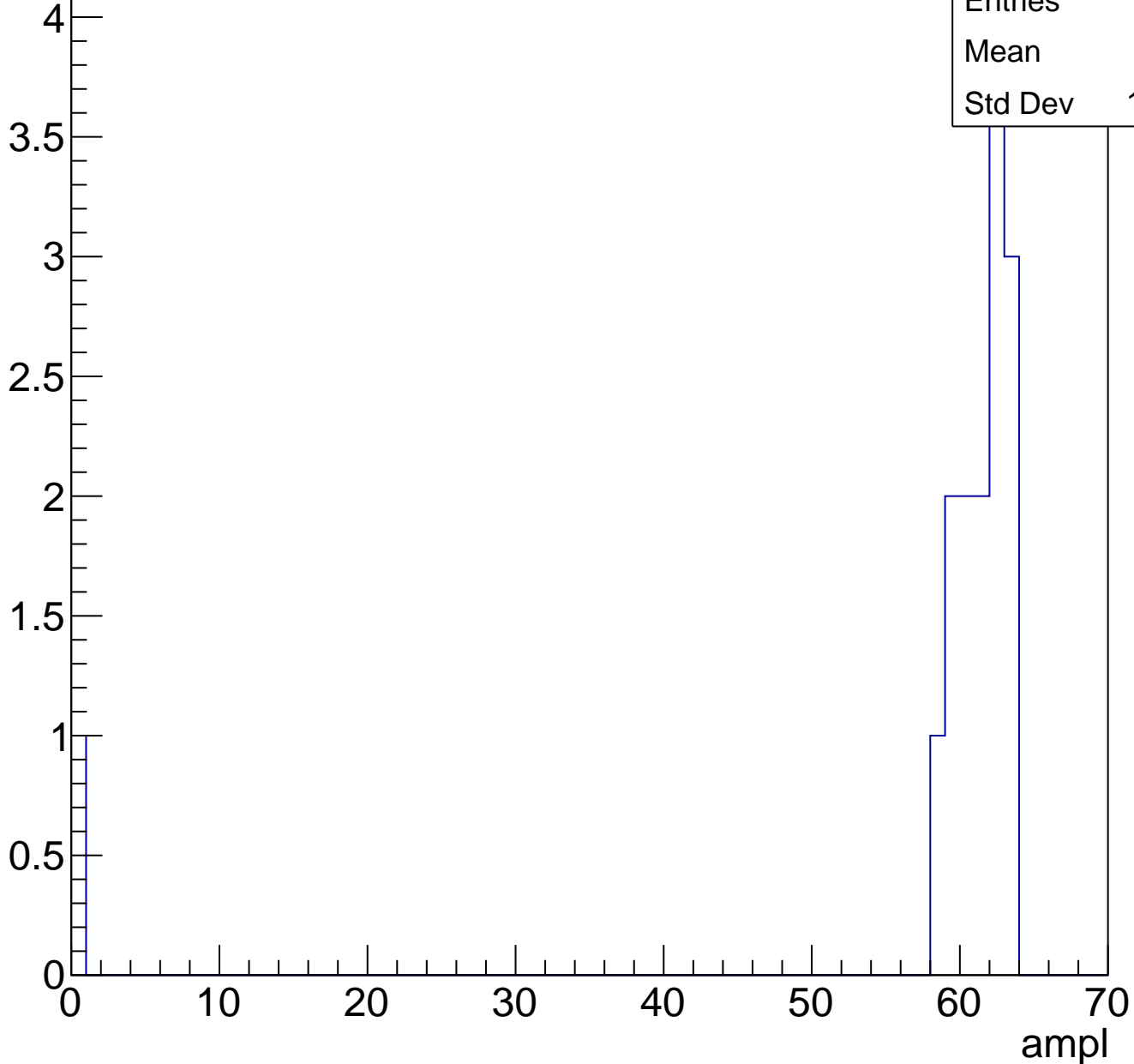
Entries	70
Mean	57.99
Std Dev	3.187



B1L100S, U15-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

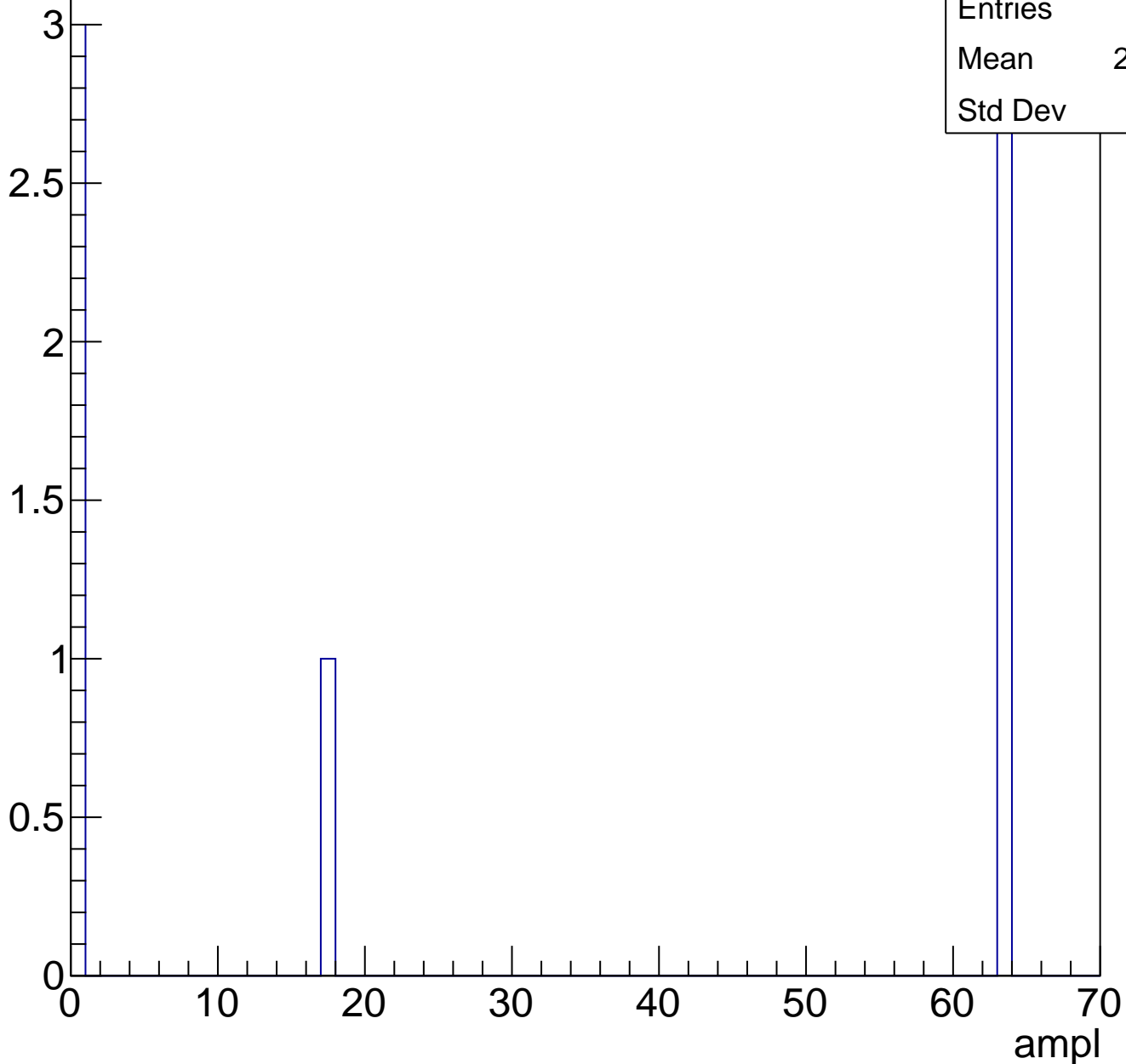
Entry



B1L100S, U15-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch107, adc0

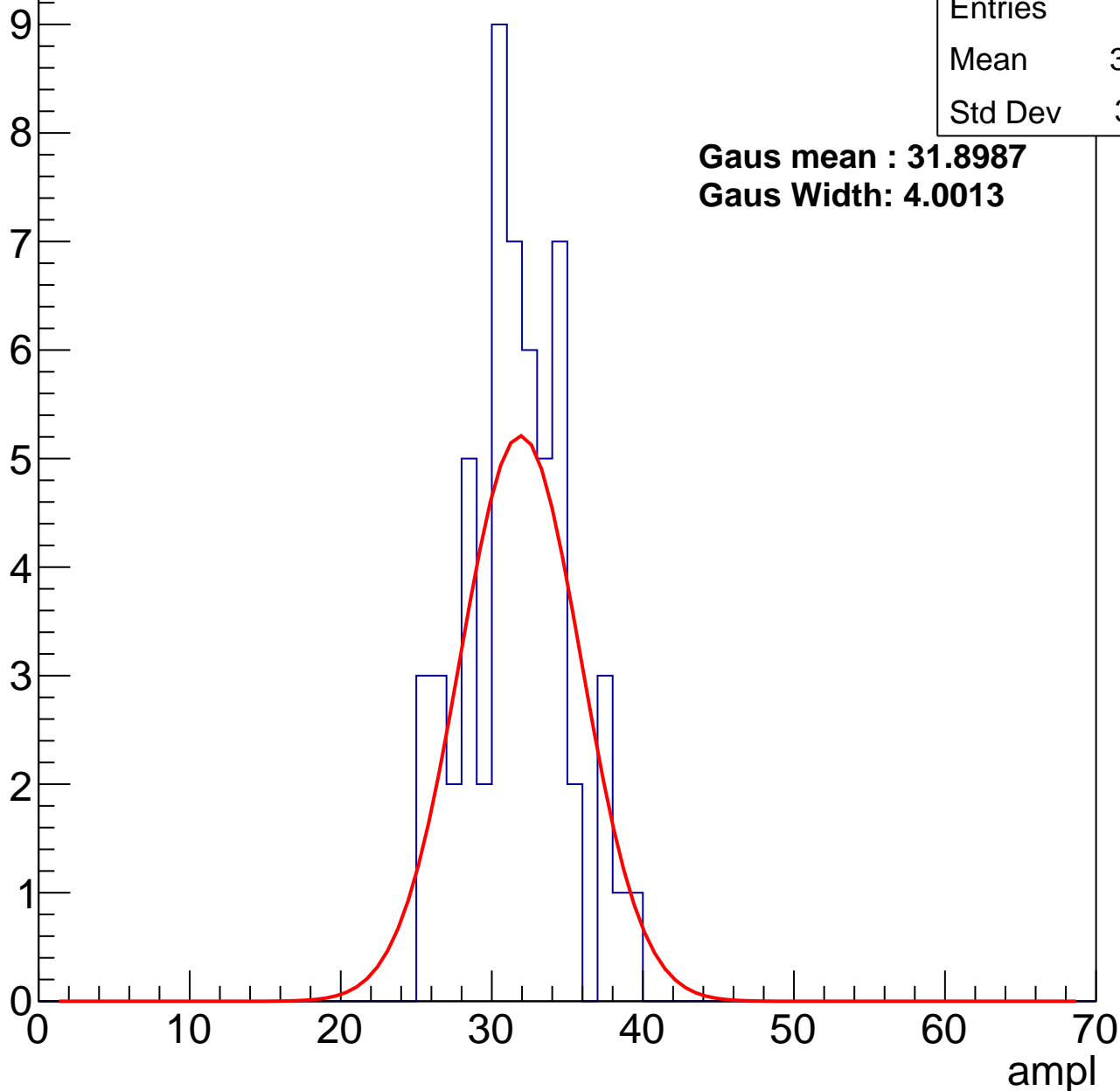
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	31.16
Std Dev	3.321

Gaus mean : 31.8987

Gaus Width: 4.0013



B1L100S, U15-ch107, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	38.01
Std Dev	3.319

Gaus mean : 39.0750

Gaus Width: 3.3503

10

8

6

4

2

0

0

10

20

30

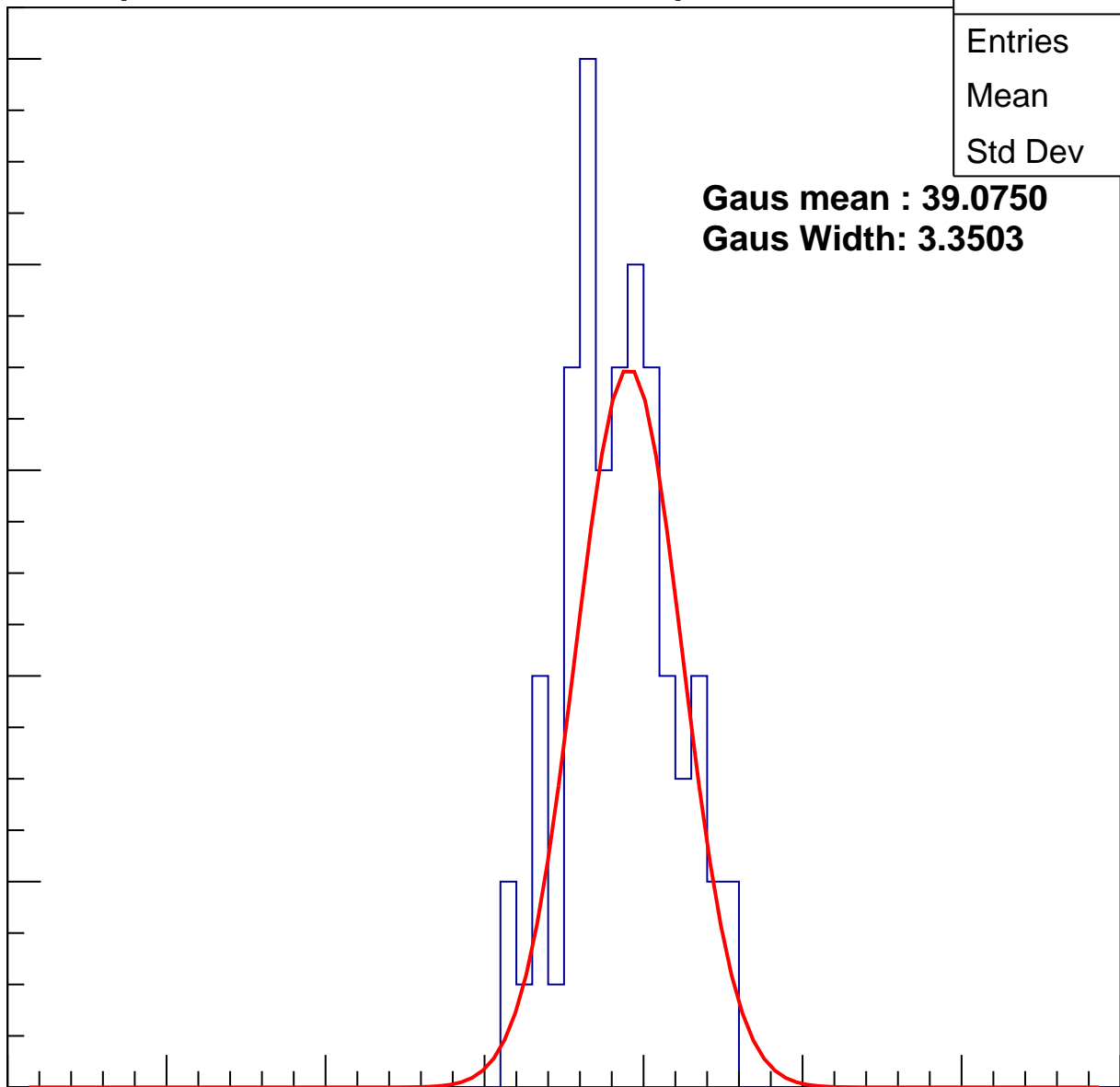
40

50

60

70

ampl



B1L100S, U15-ch107, adc2

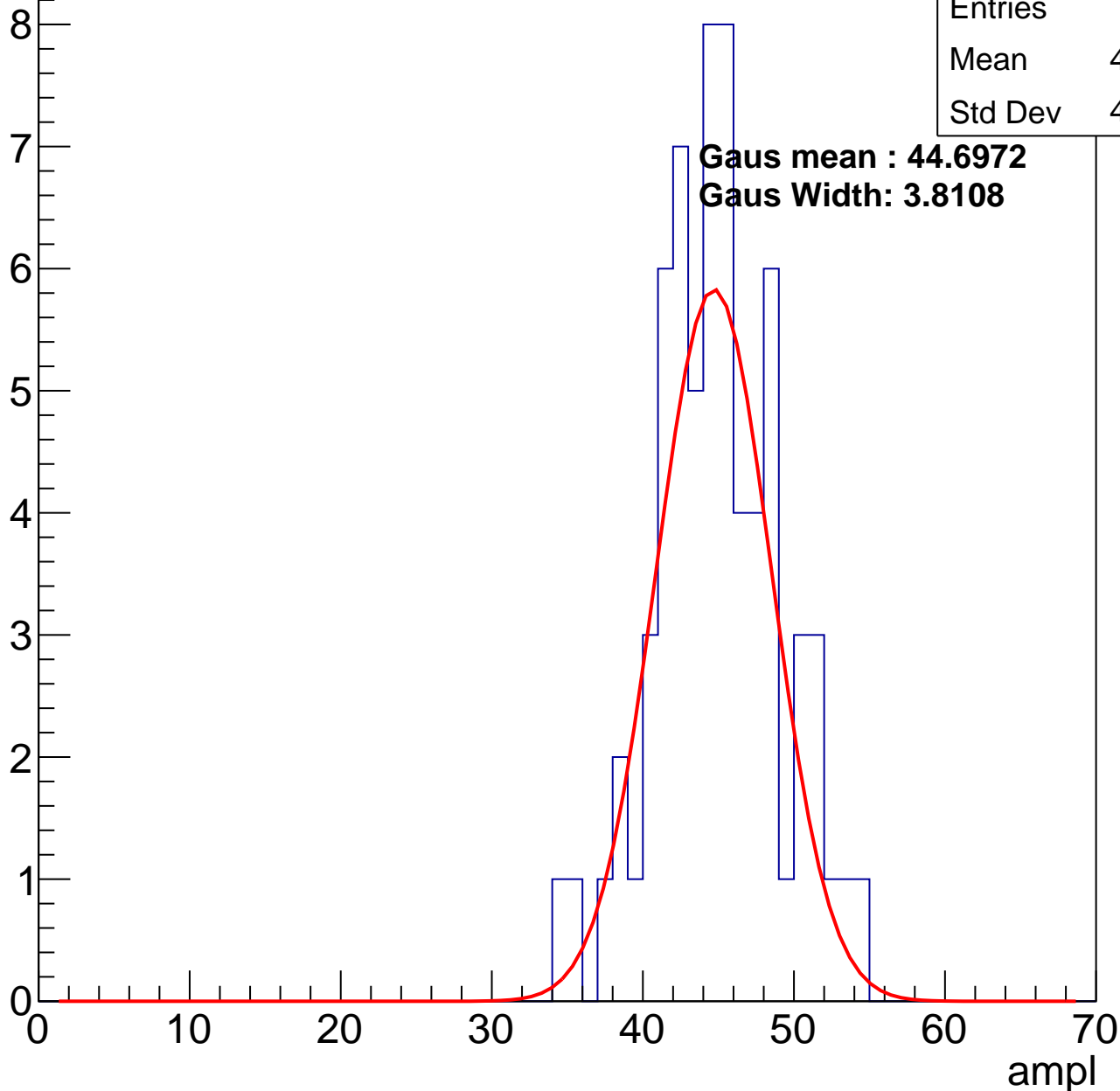
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.46
Std Dev	4.104

Gaus mean : 44.6972

Gaus Width: 3.8108

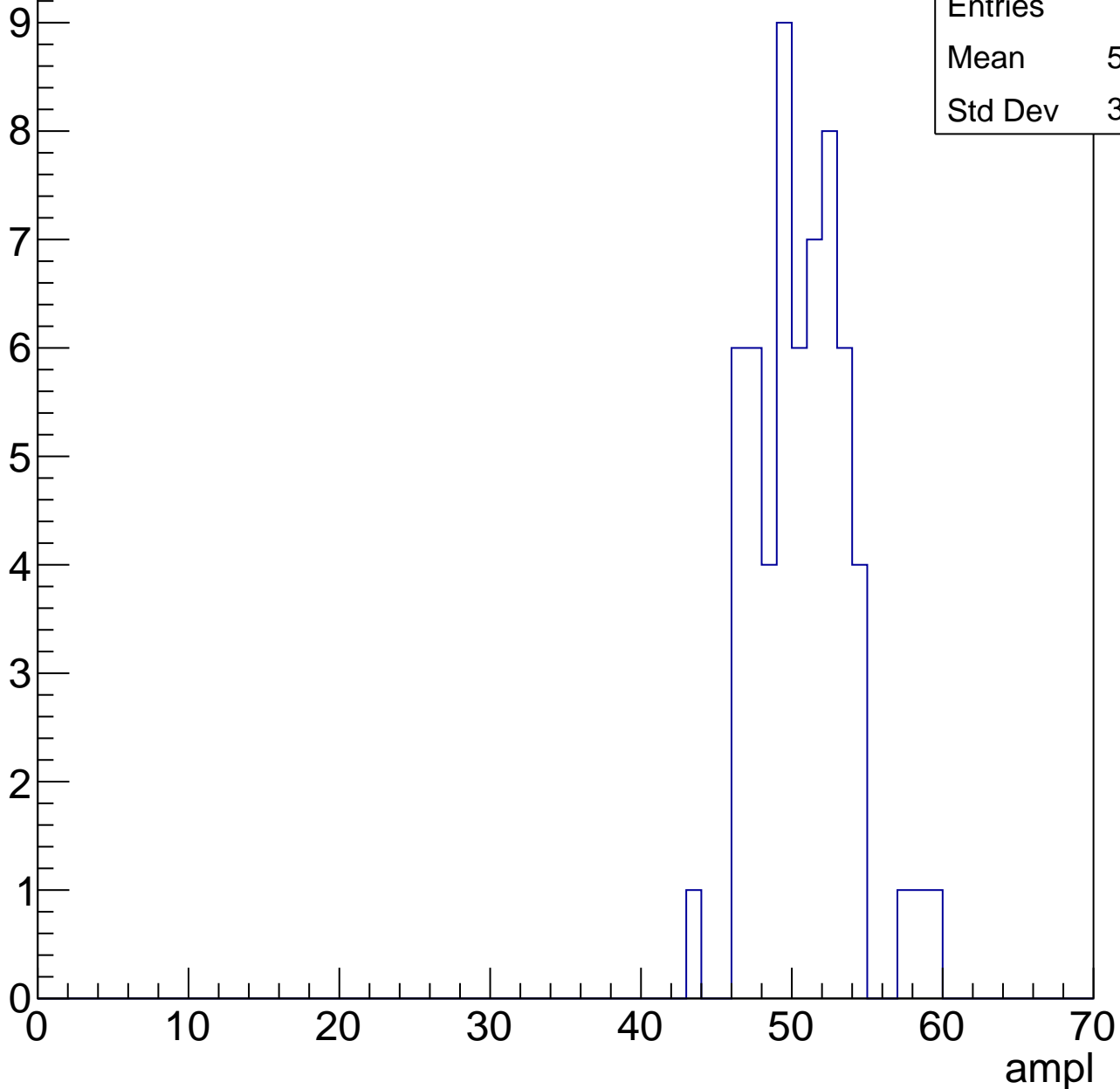


B1L100S, U15-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	50.25
Std Dev	3.086

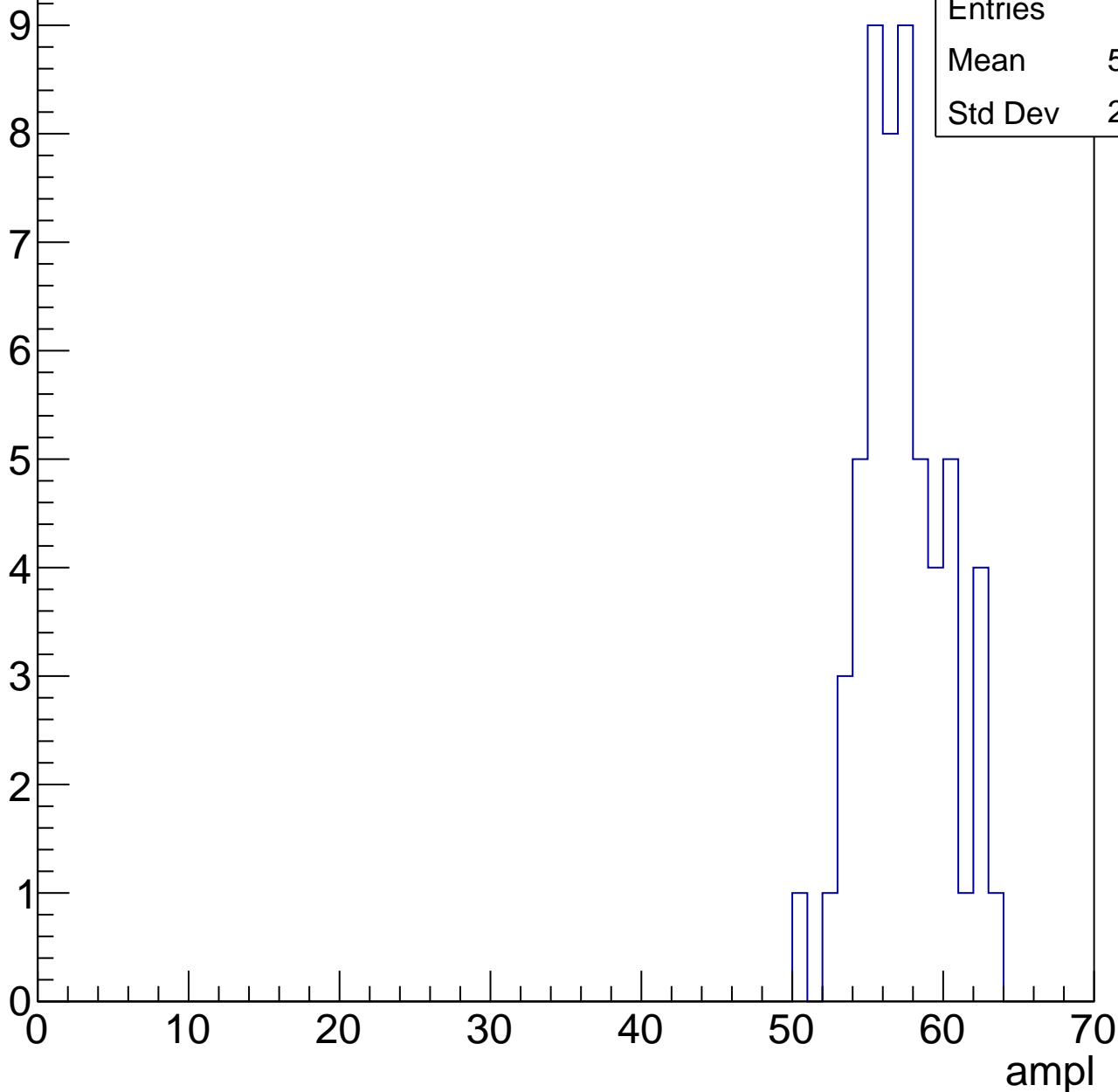


B1L100S, U15-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	56.88
Std Dev	2.765

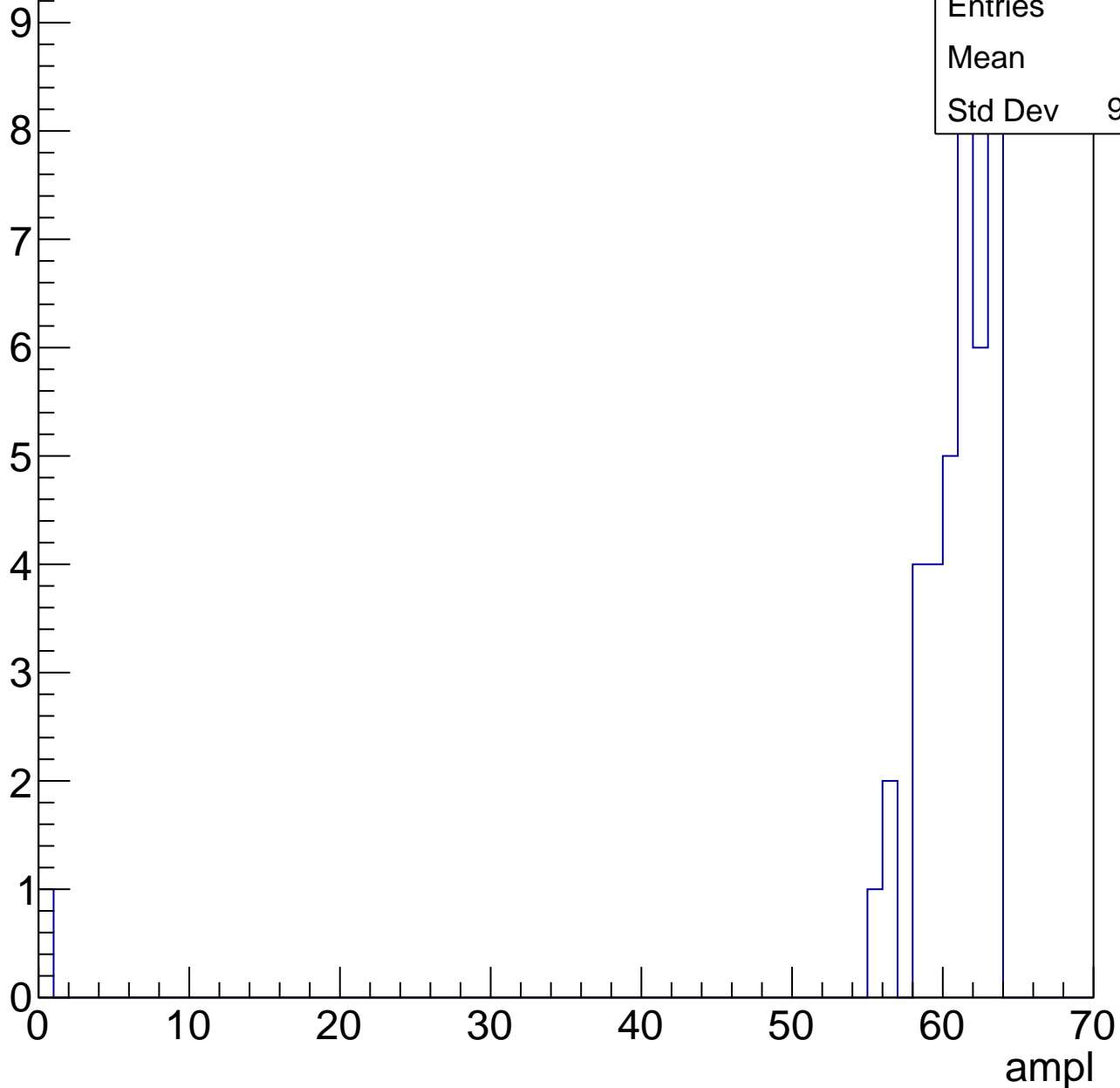


B1L100S, U15-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	59
Std Dev	9.672



B1L100S, U15-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch108, adc0

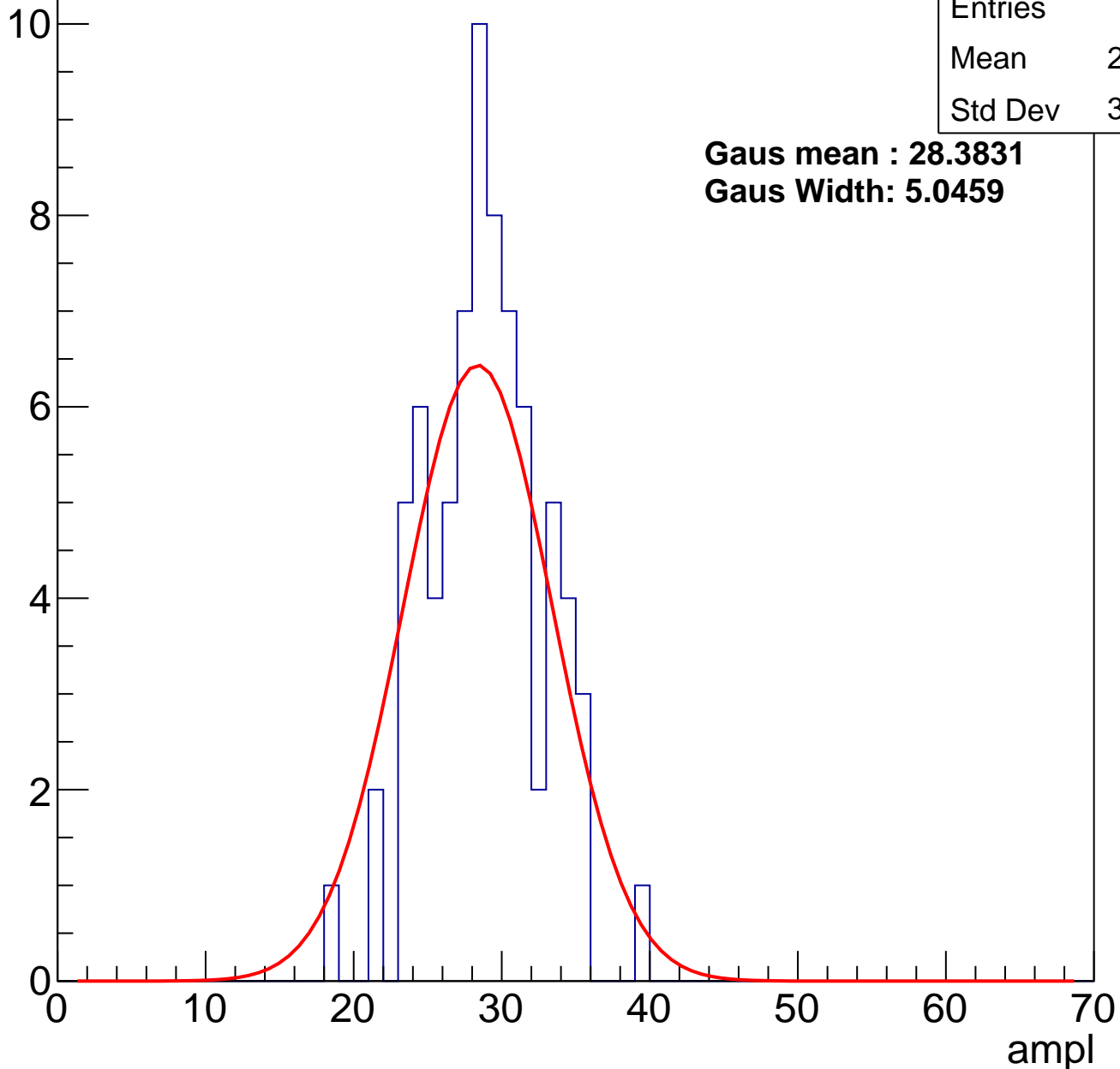
calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	28.36
Std Dev	3.858

Gaus mean : 28.3831

Gaus Width: 5.0459

Entry



B1L100S, U15-ch108, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	62
Mean	35.45
Std Dev	3.568

Gaus mean : 35.7533

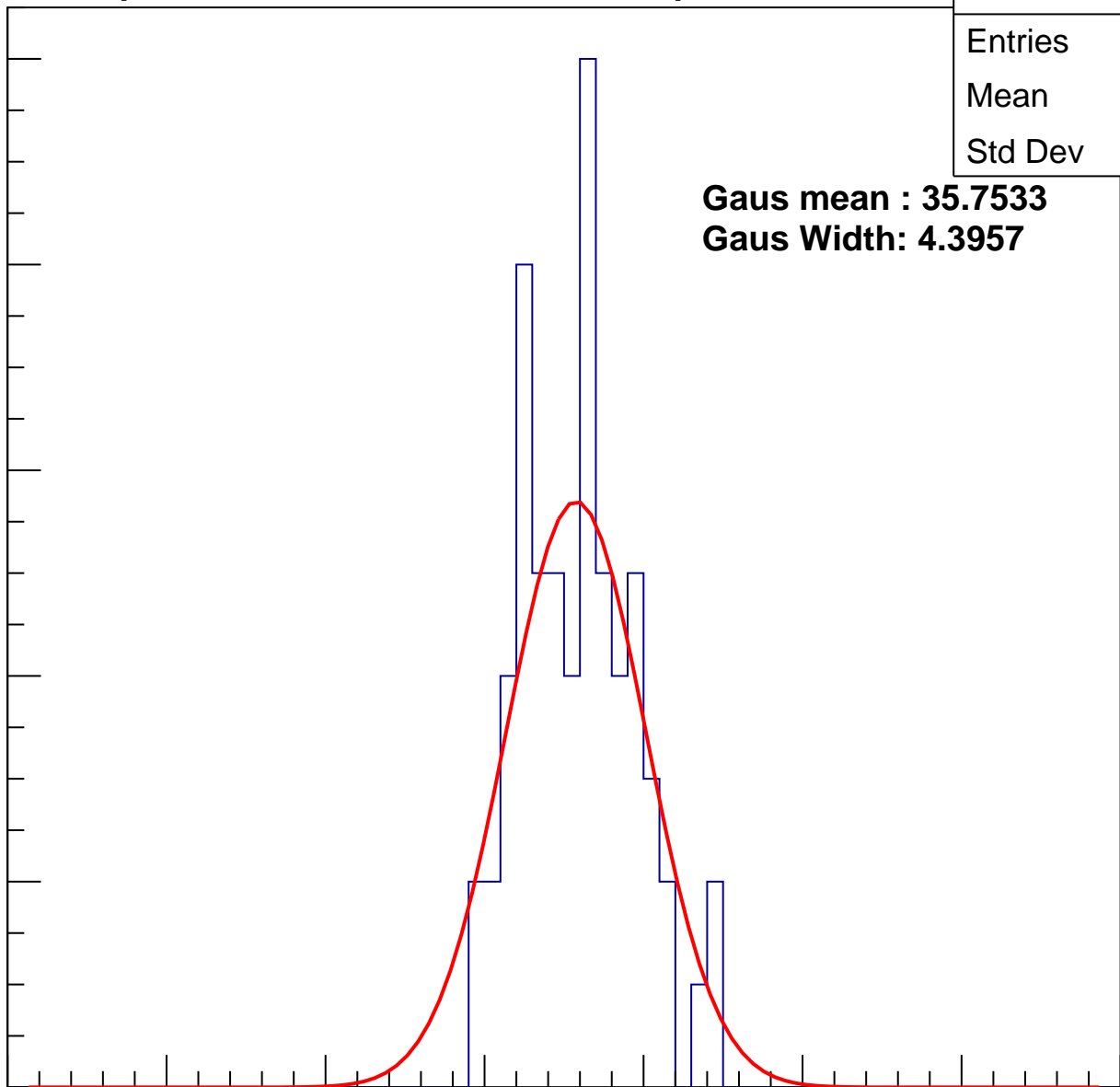
Gaus Width: 4.3957

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U15-ch108, adc2

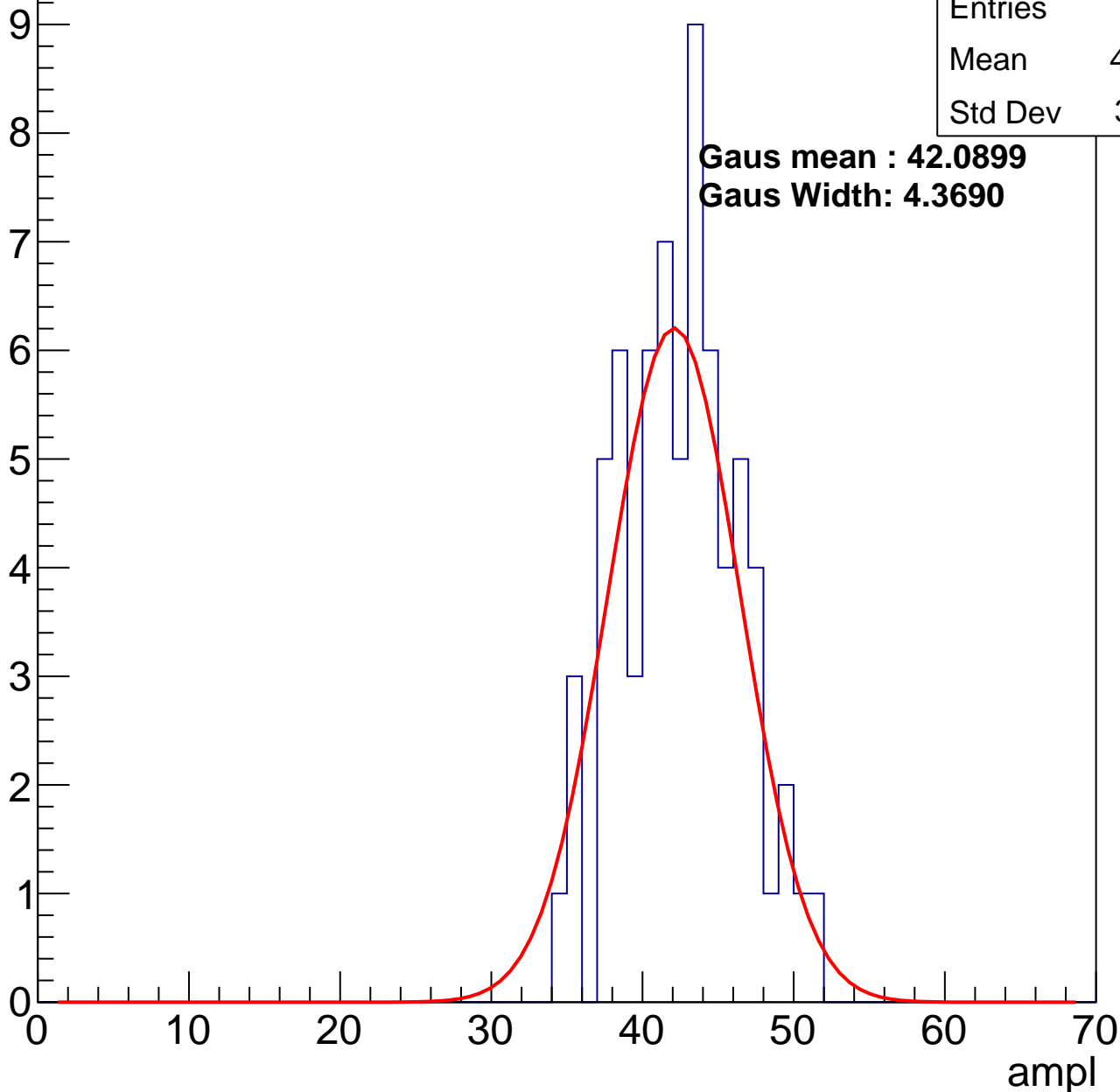
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.06
Std Dev	3.841

Gaus mean : 42.0899

Gaus Width: 4.3690

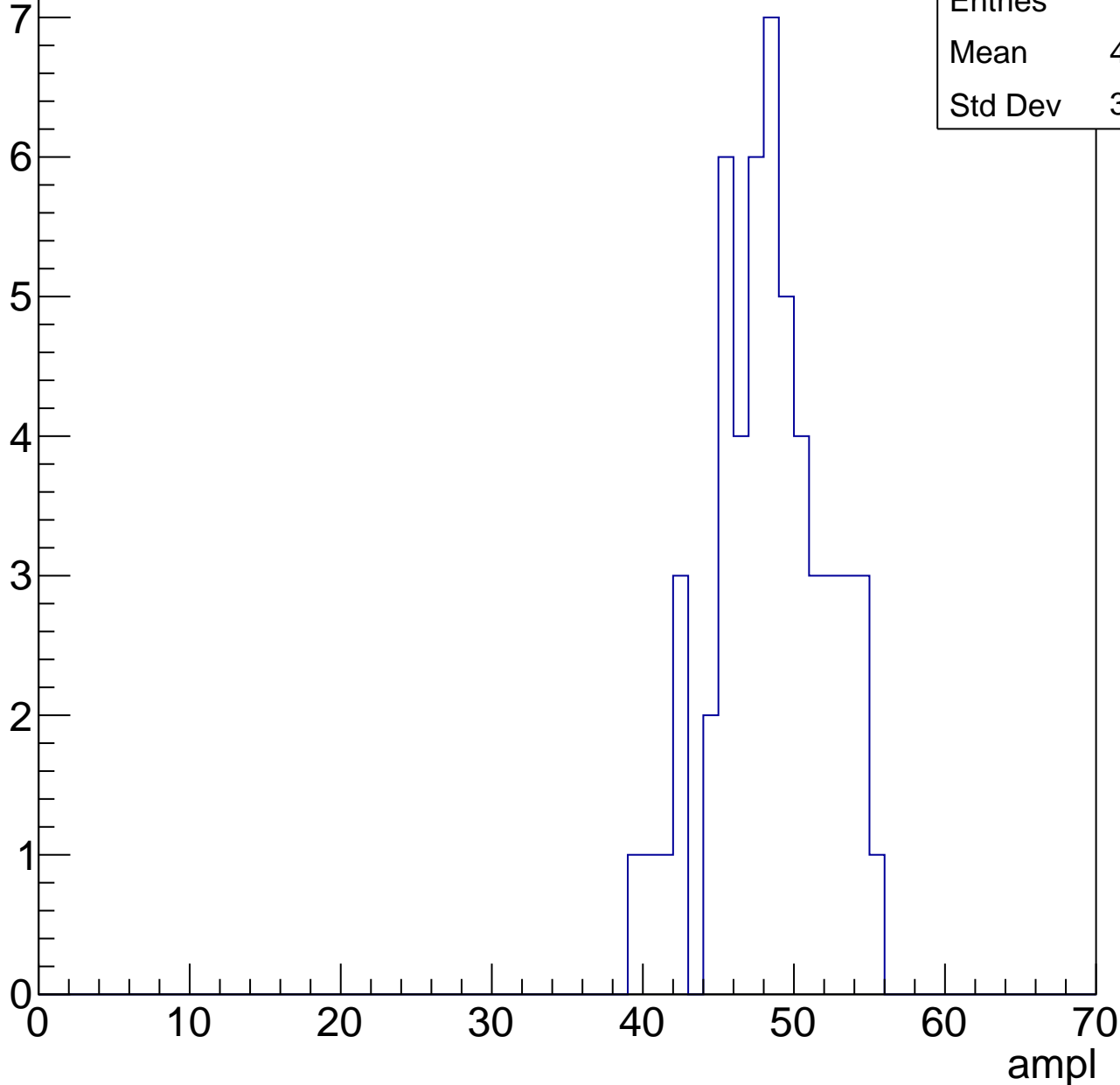


B1L100S, U15-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	47.85
Std Dev	3.729

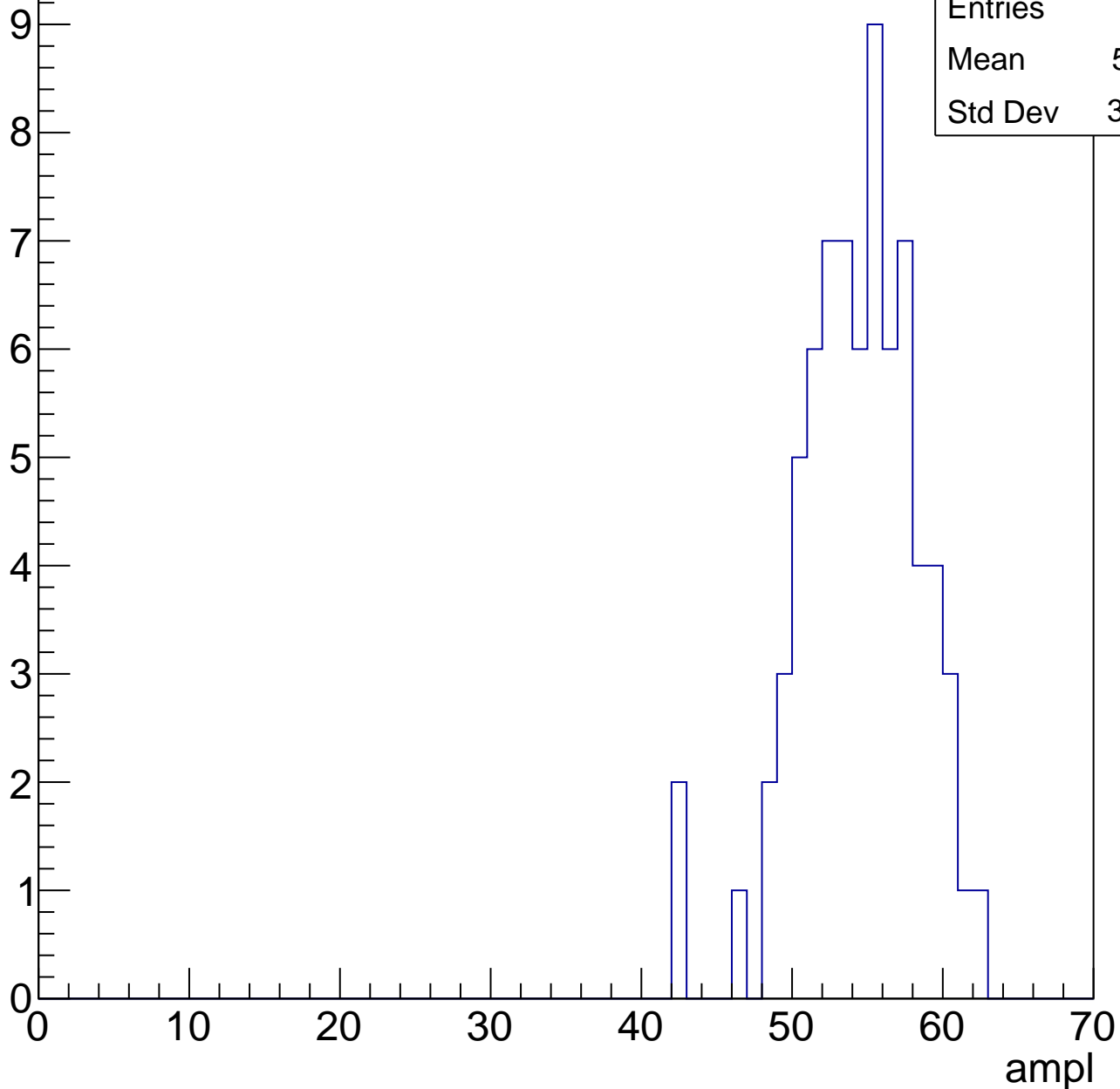


B1L100S, U15-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	53.91
Std Dev	3.939

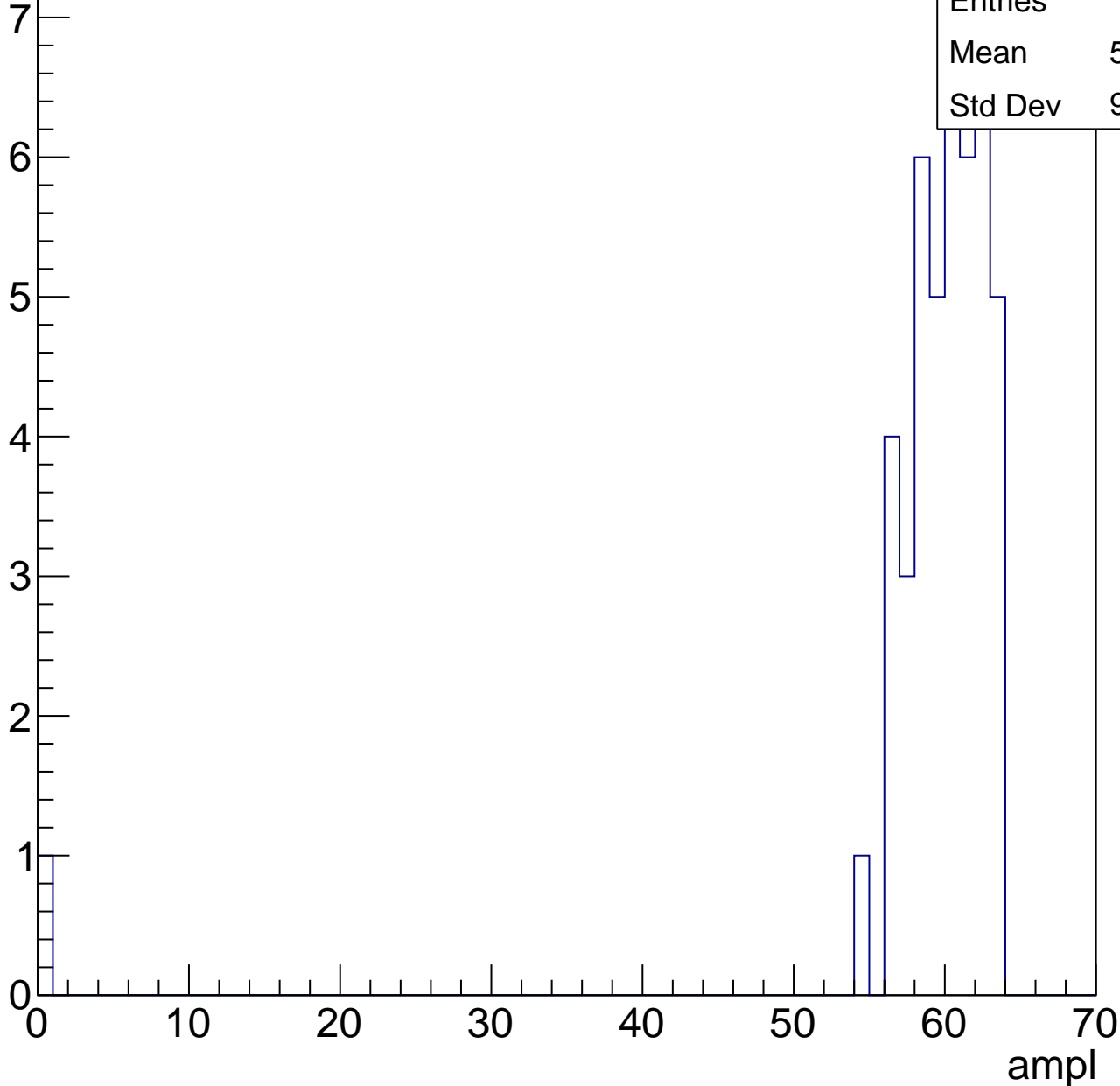


B1L100S, U15-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

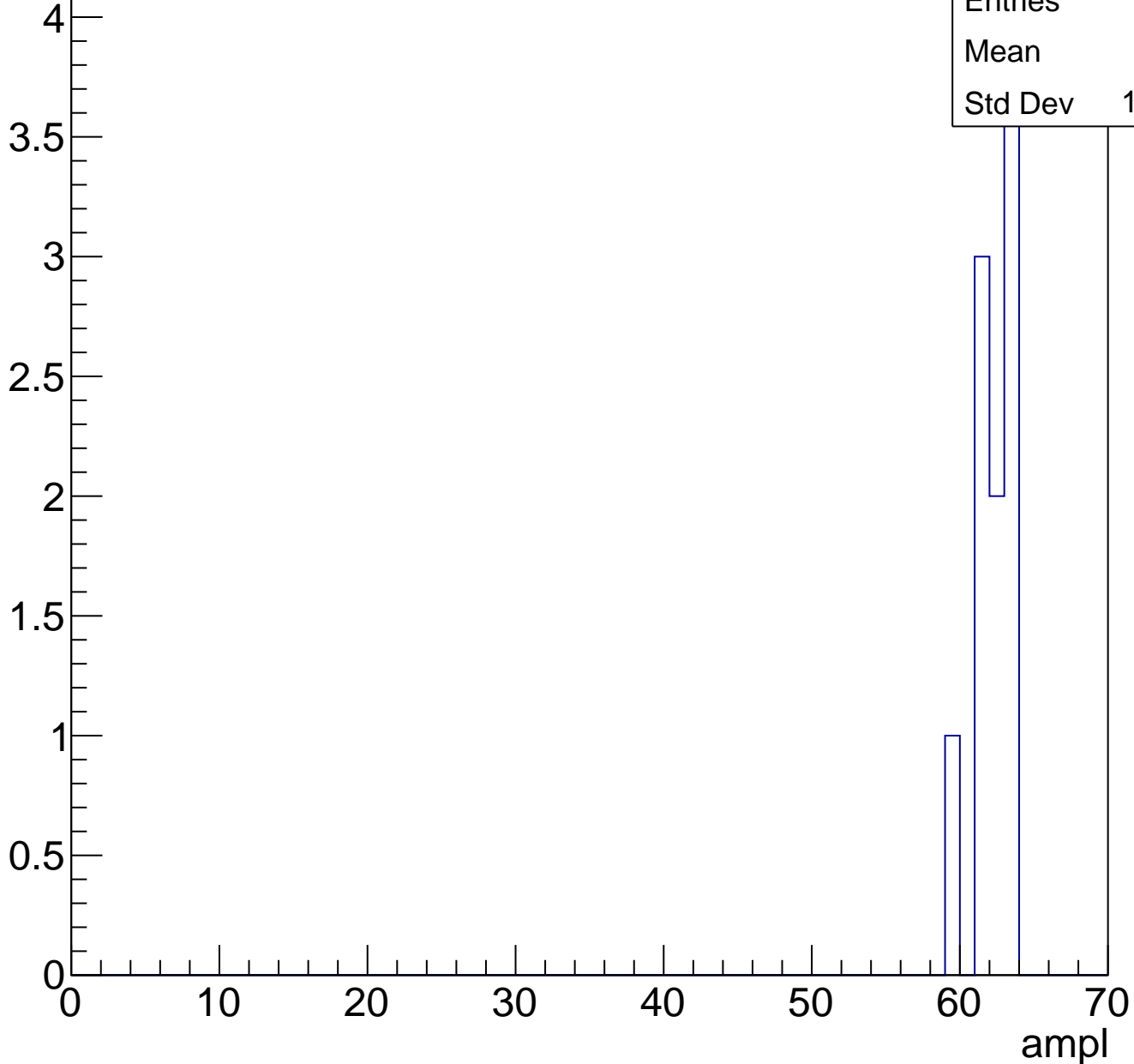
Entries	45
Mean	58.38
Std Dev	9.088



B1L100S, U15-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch109, adc0

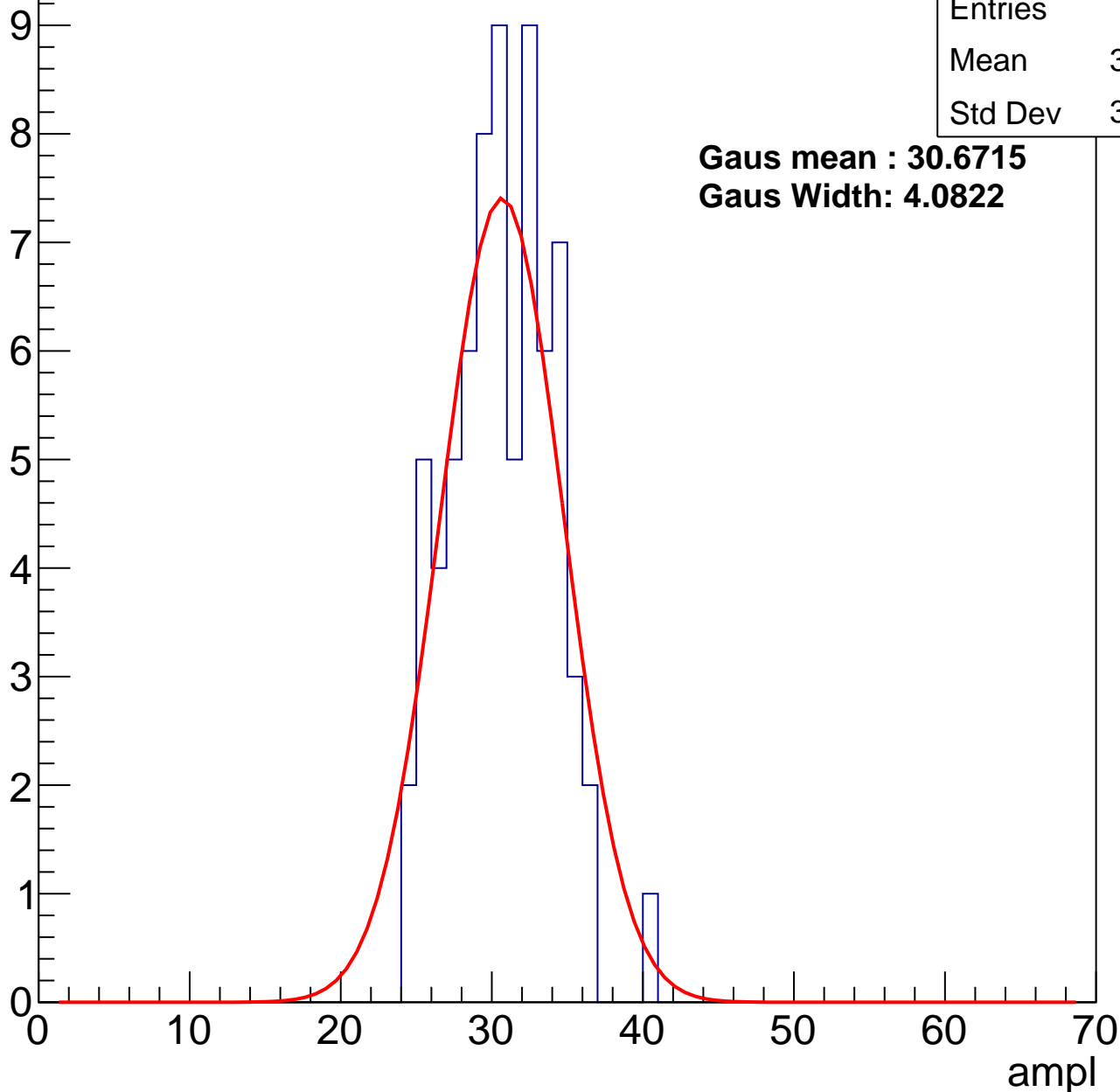
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.25
Std Dev	3.307

Gaus mean : 30.6715

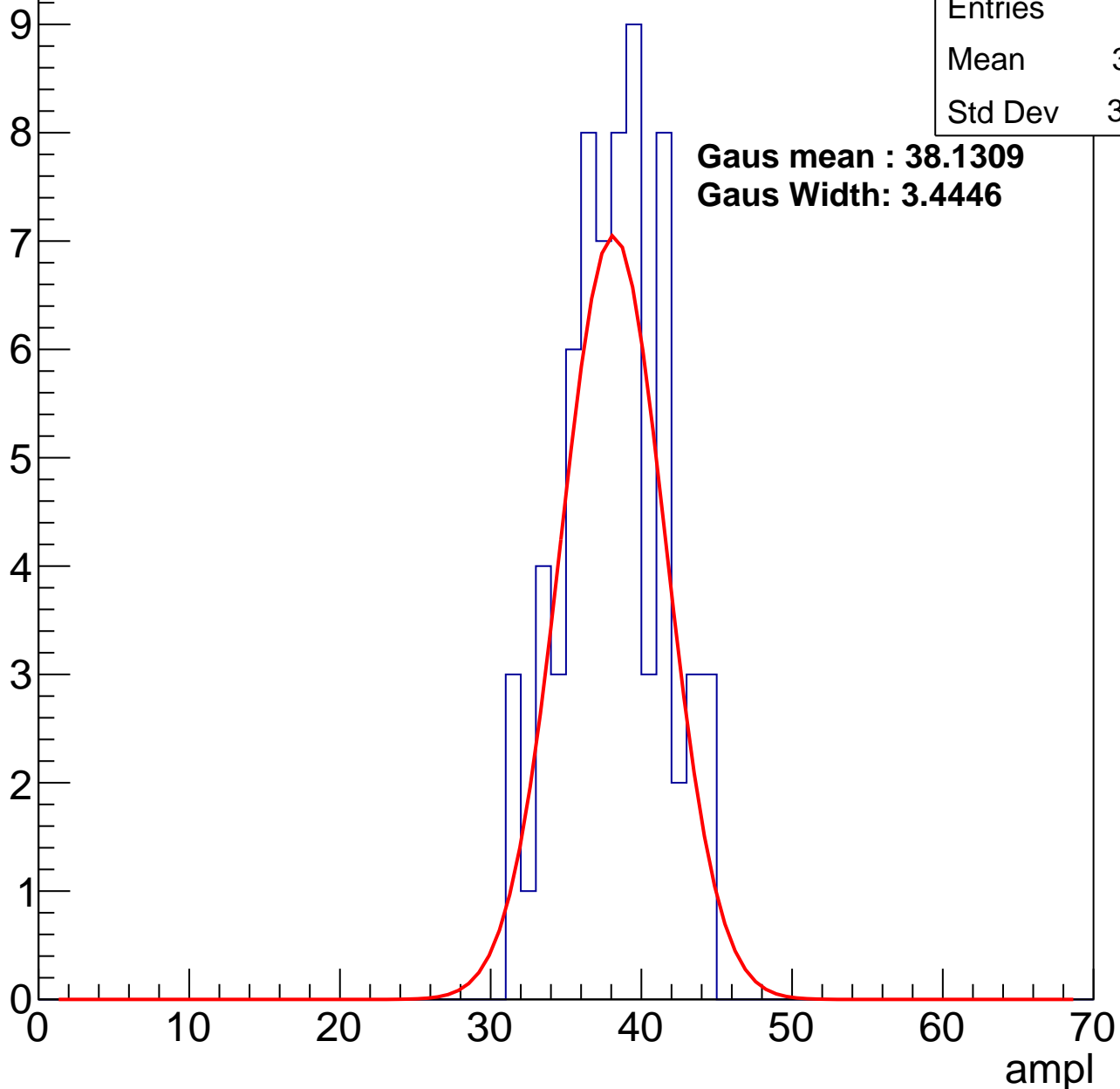
Gaus Width: 4.0822



B1L100S, U15-ch109, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	68
Mean	37.71
Std Dev	3.268

Gaus mean : 38.1309

Gaus Width: 3.4446

B1L100S, U15-ch109, adc2

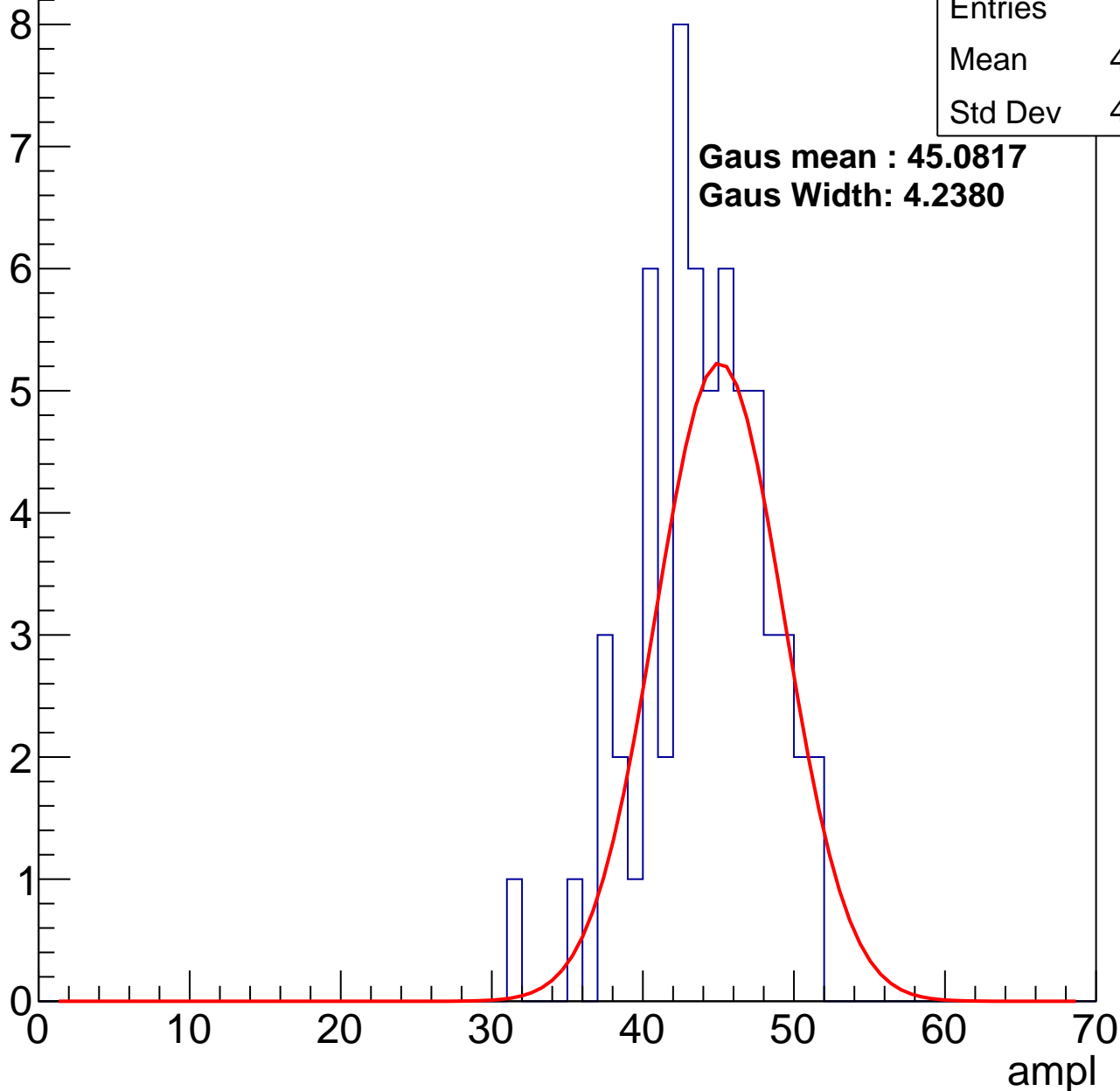
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.54
Std Dev	4.043

Gaus mean : 45.0817

Gaus Width: 4.2380

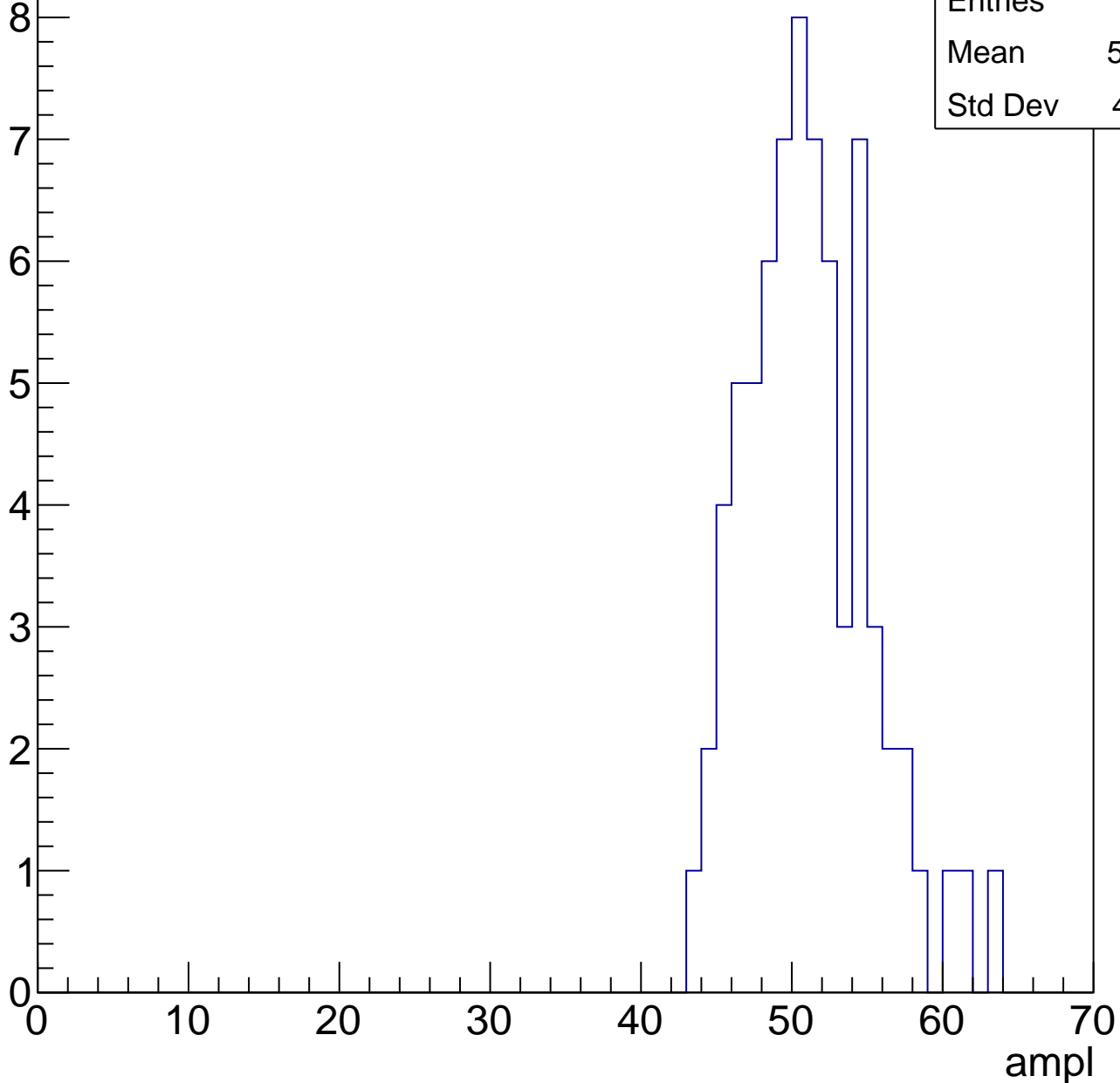


B1L100S, U15-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	50.64
Std Dev	4.111

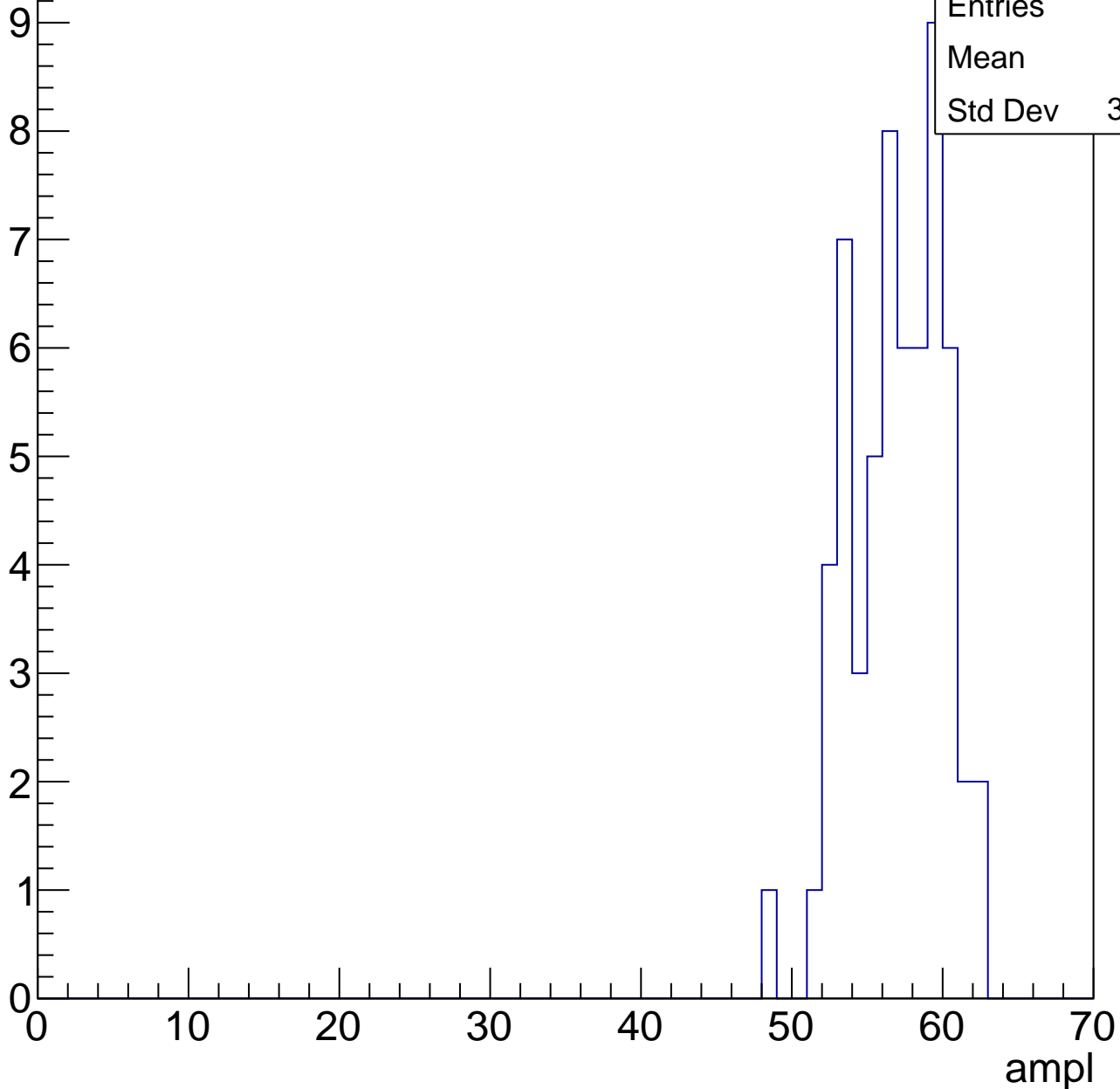


B1L100S, U15-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	56.5
Std Dev	3.019

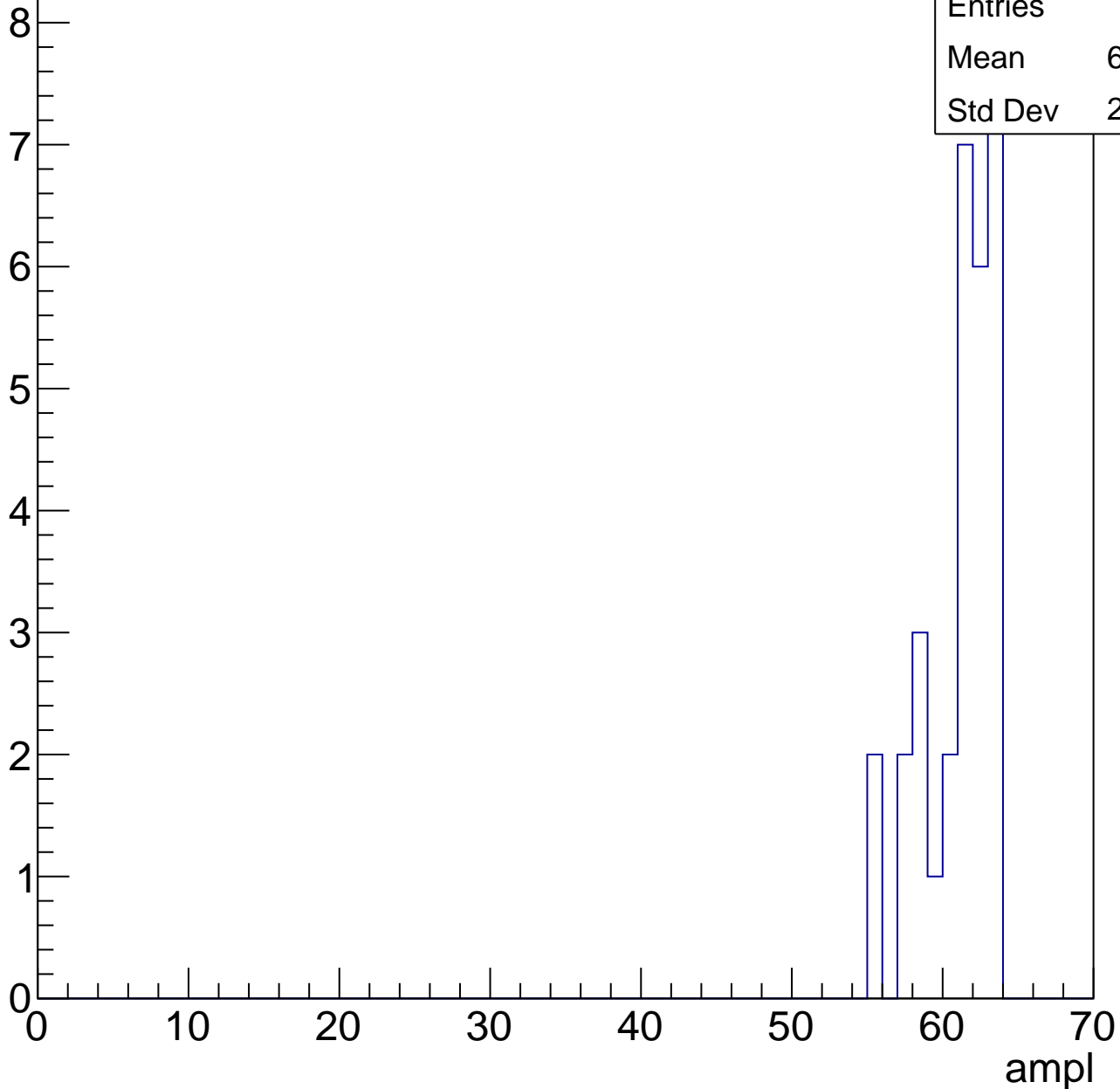


B1L100S, U15-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	60.65
Std Dev	2.349



B1L100S, U15-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

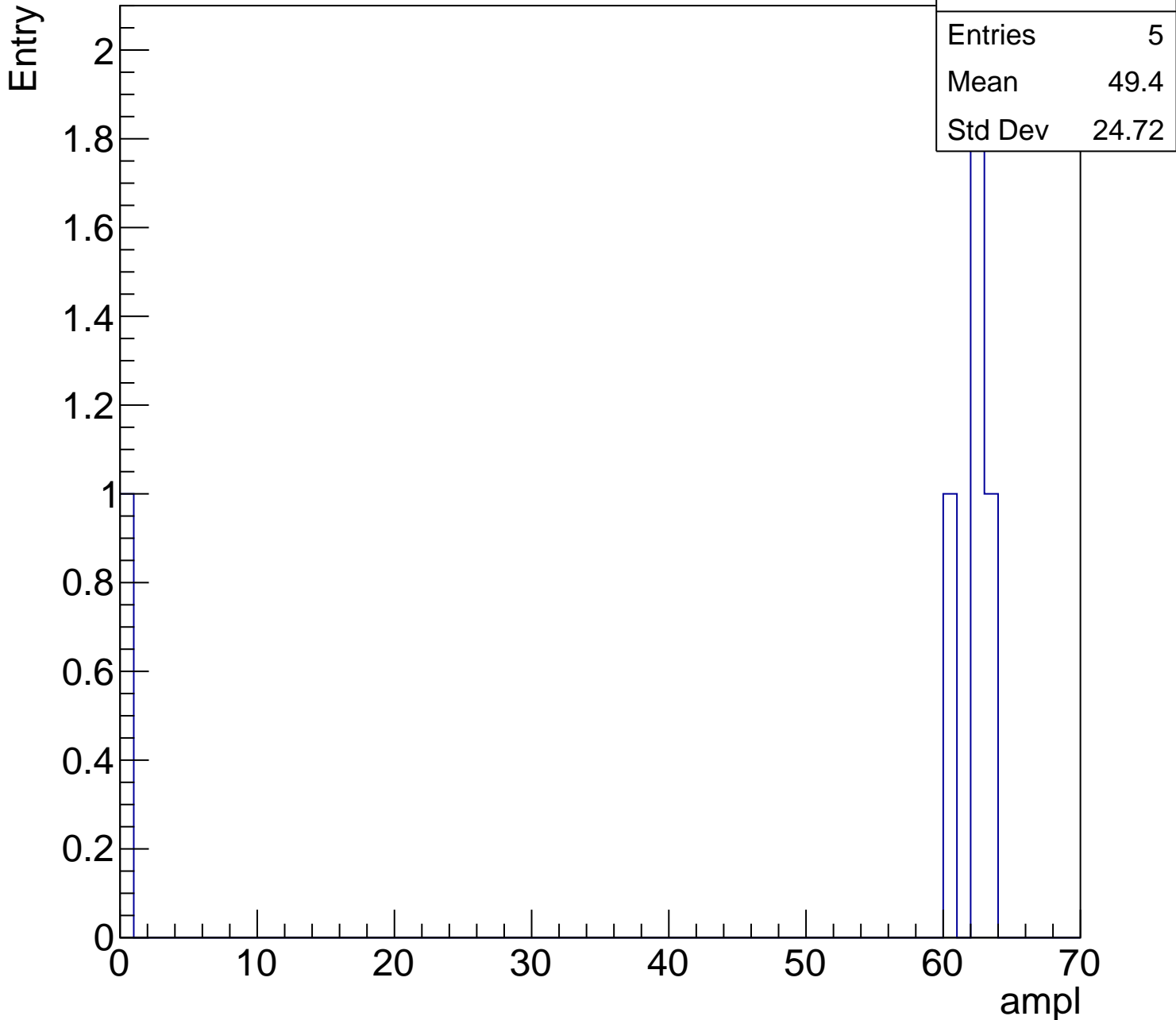
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.4
Std Dev	24.72

0 10 20 30 40 50 60 70

ampl



B1L100S, U15-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch110, adc0

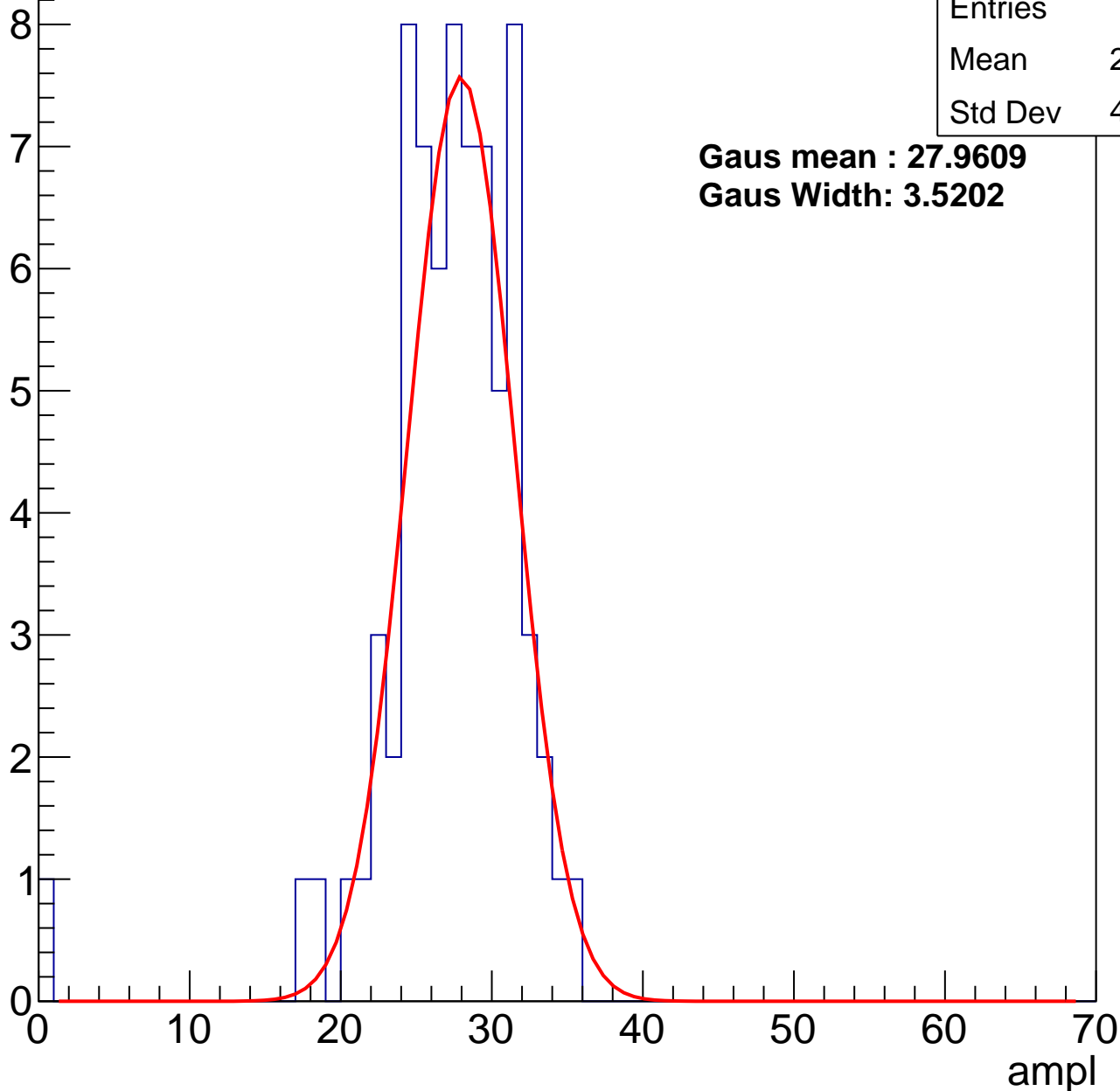
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	26.78
Std Dev	4.787

Gaus mean : 27.9609

Gaus Width: 3.5202



B1L100S, U15-ch110, adc1

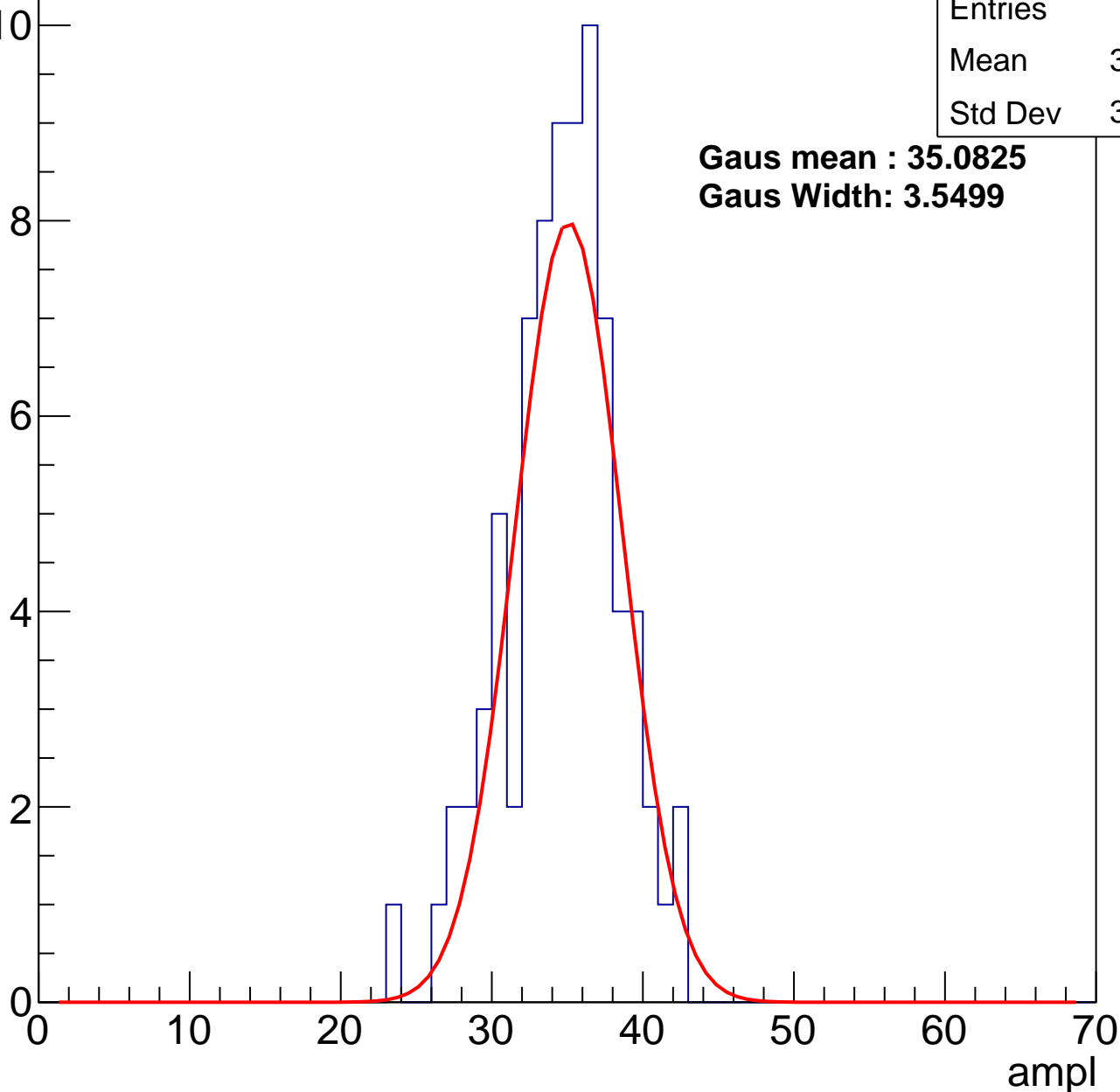
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	34.16
Std Dev	3.709

Gaus mean : 35.0825

Gaus Width: 3.5499



B1L100S, U15-ch110, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	42.26
Std Dev	3.624

Gaus mean : 42.4268

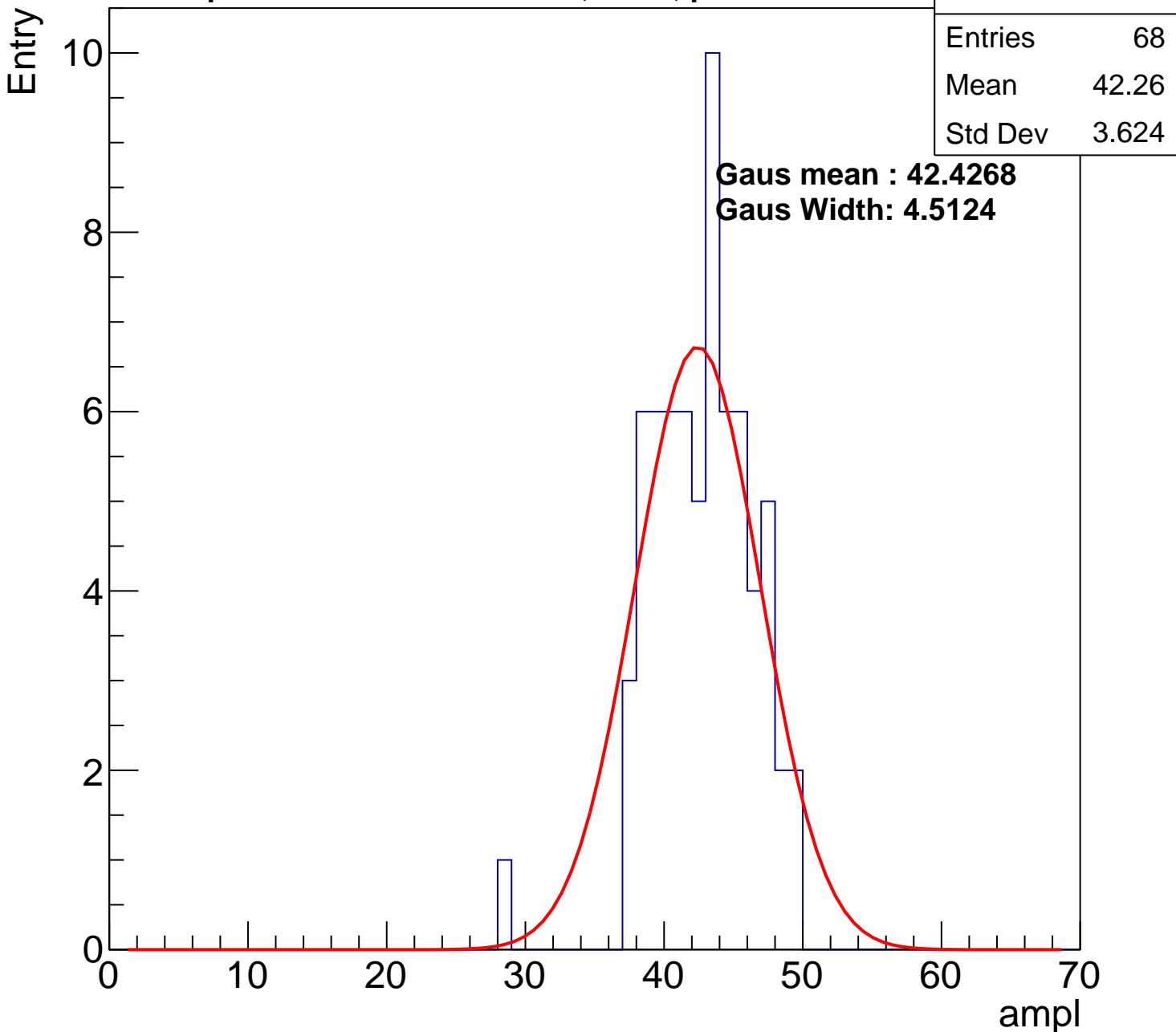
Gaus Width: 4.5124

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

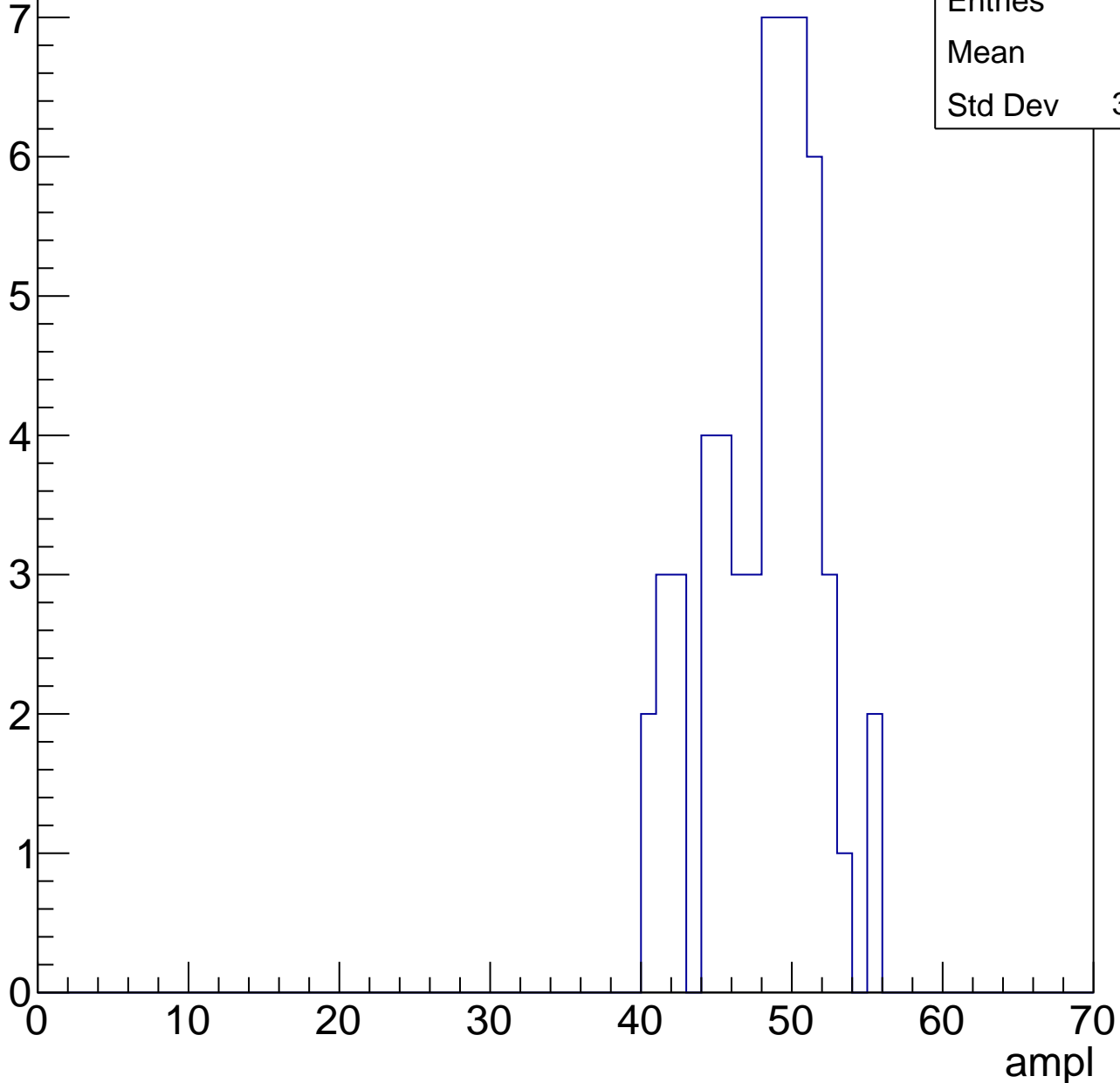


B1L100S, U15-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	47.6
Std Dev	3.691

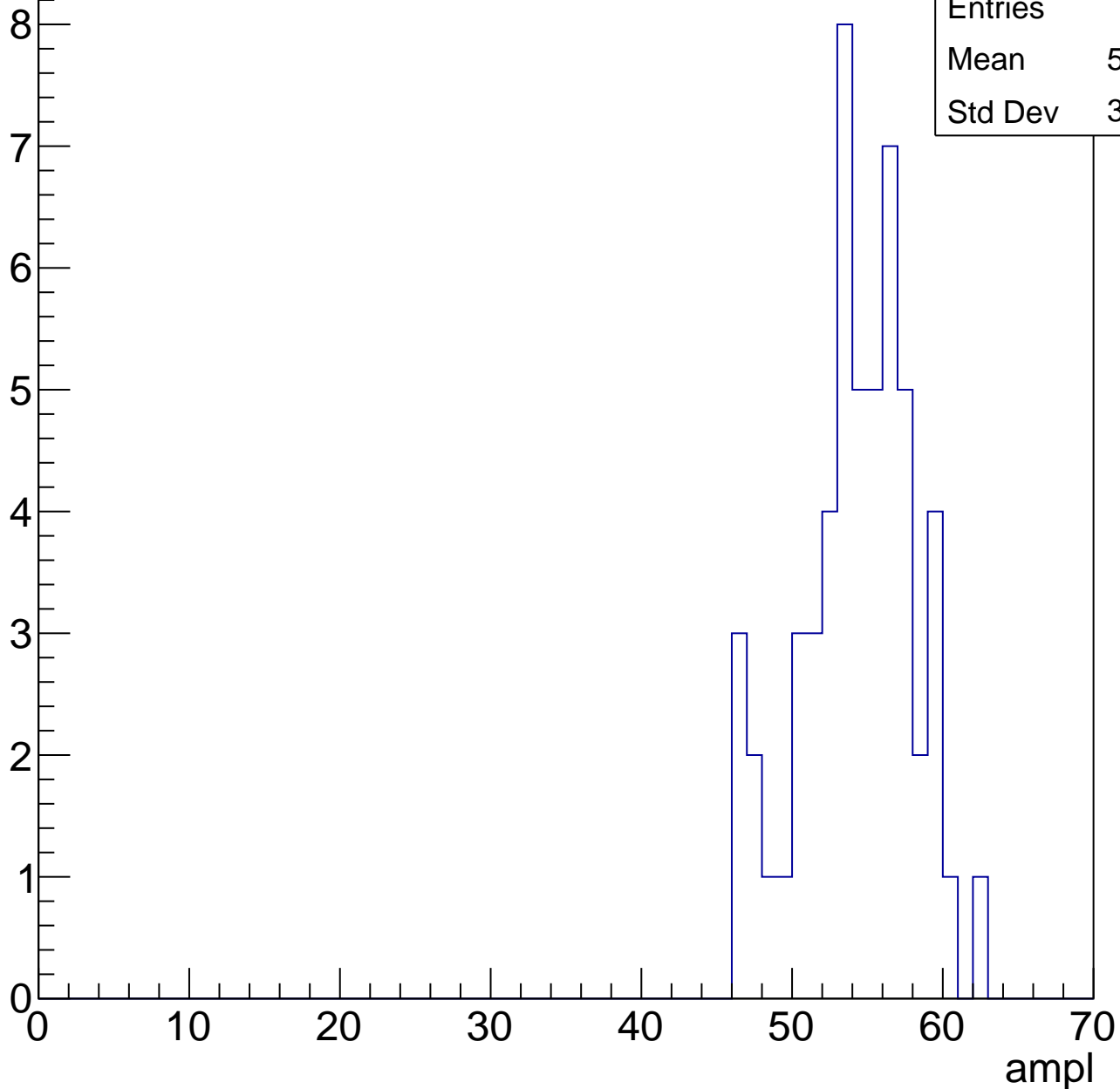


B1L100S, U15-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	53.82
Std Dev	3.713

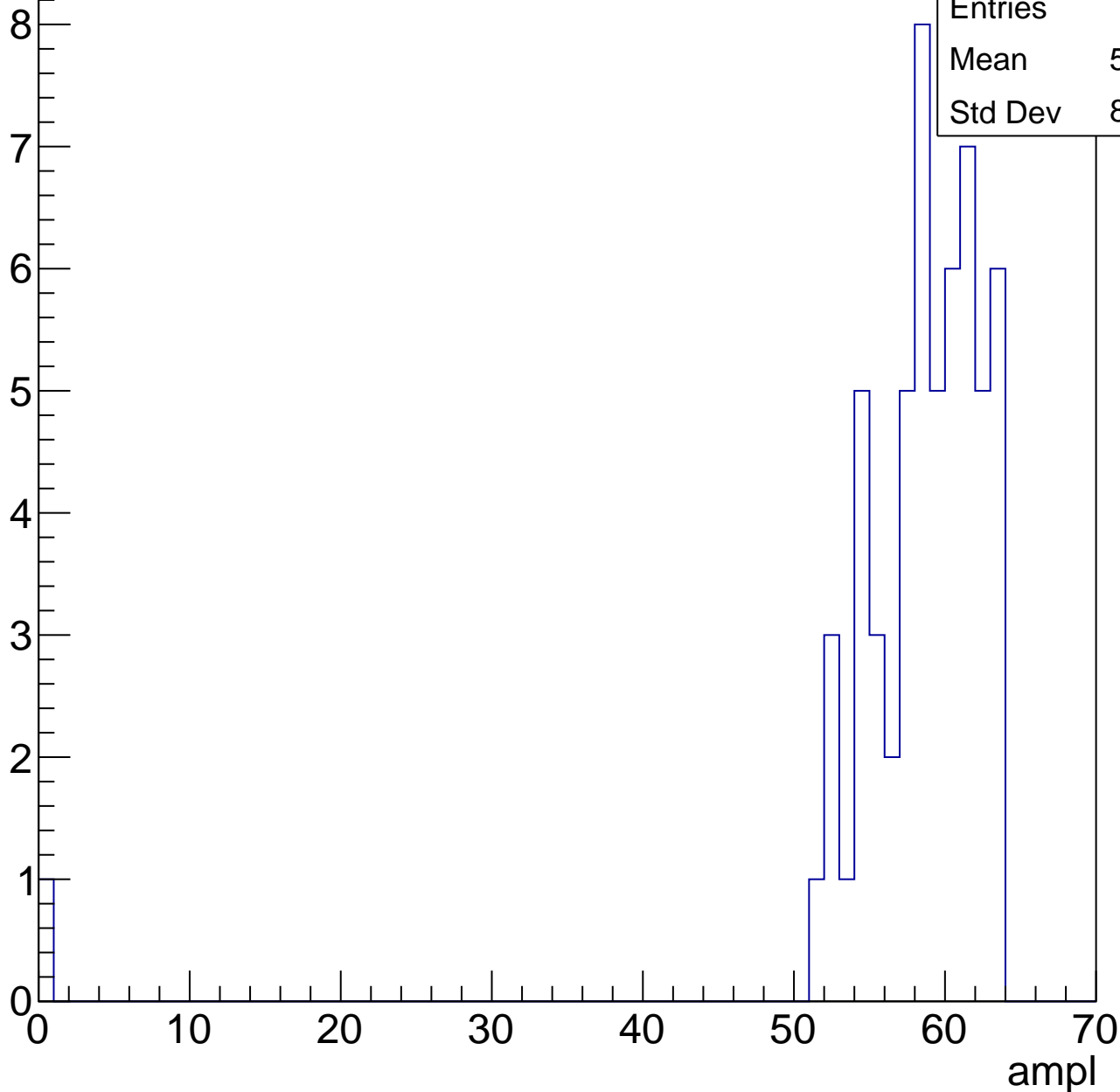


B1L100S, U15-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	57.34
Std Dev	8.266



B1L100S, U15-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

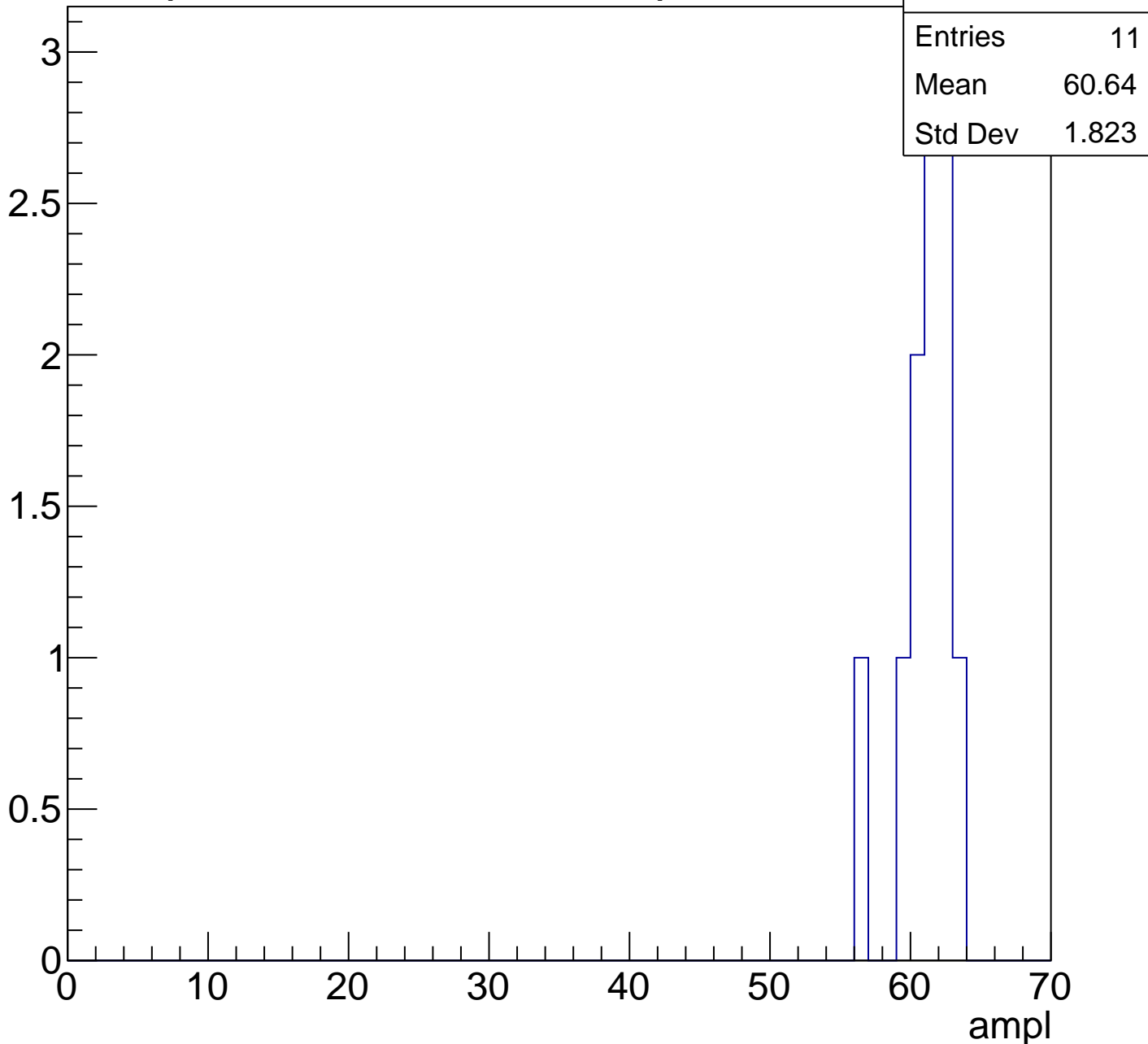
50

60

70

ampl

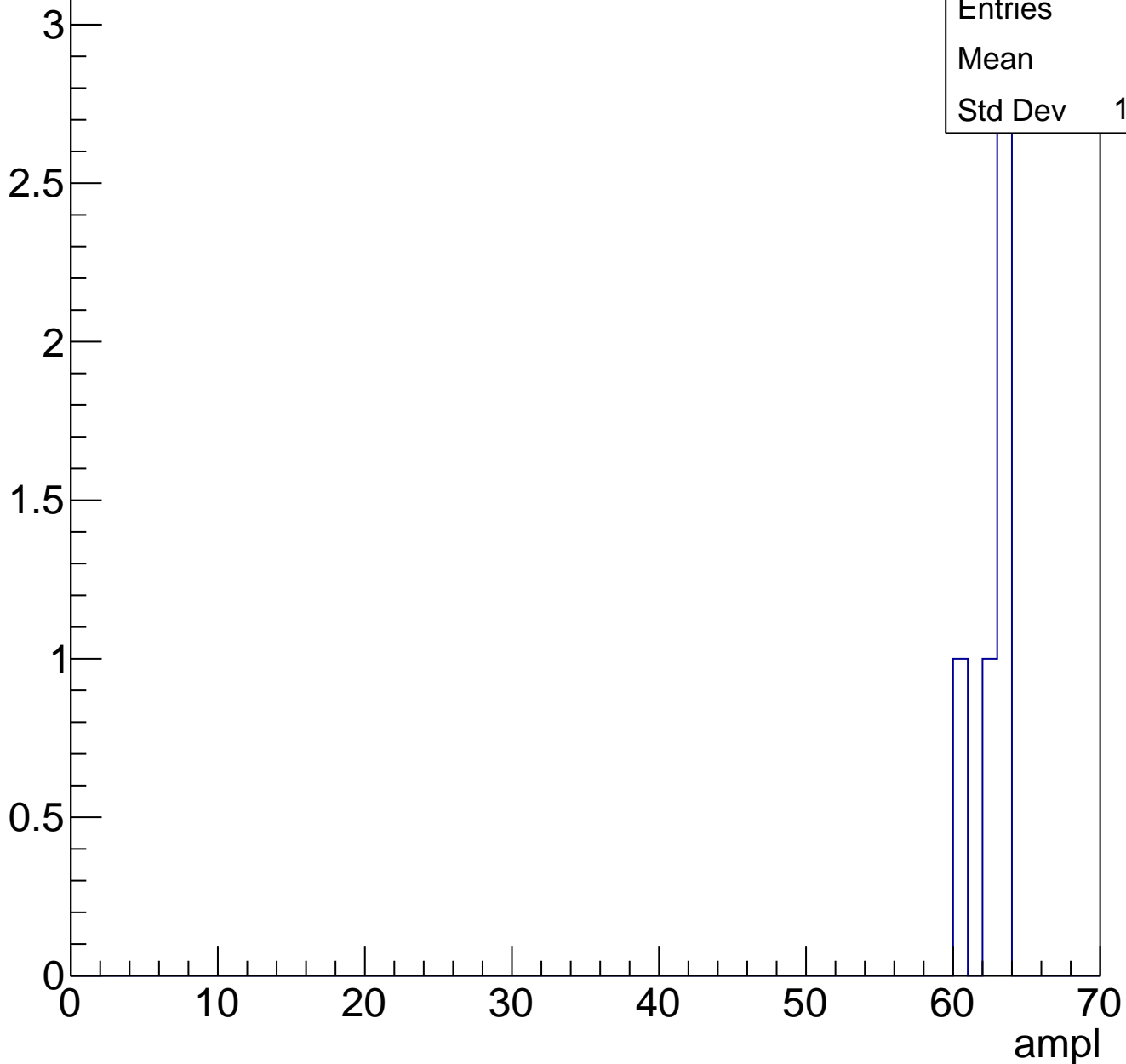
Entries	11
Mean	60.64
Std Dev	1.823



B1L100S, U15-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch111, adc0

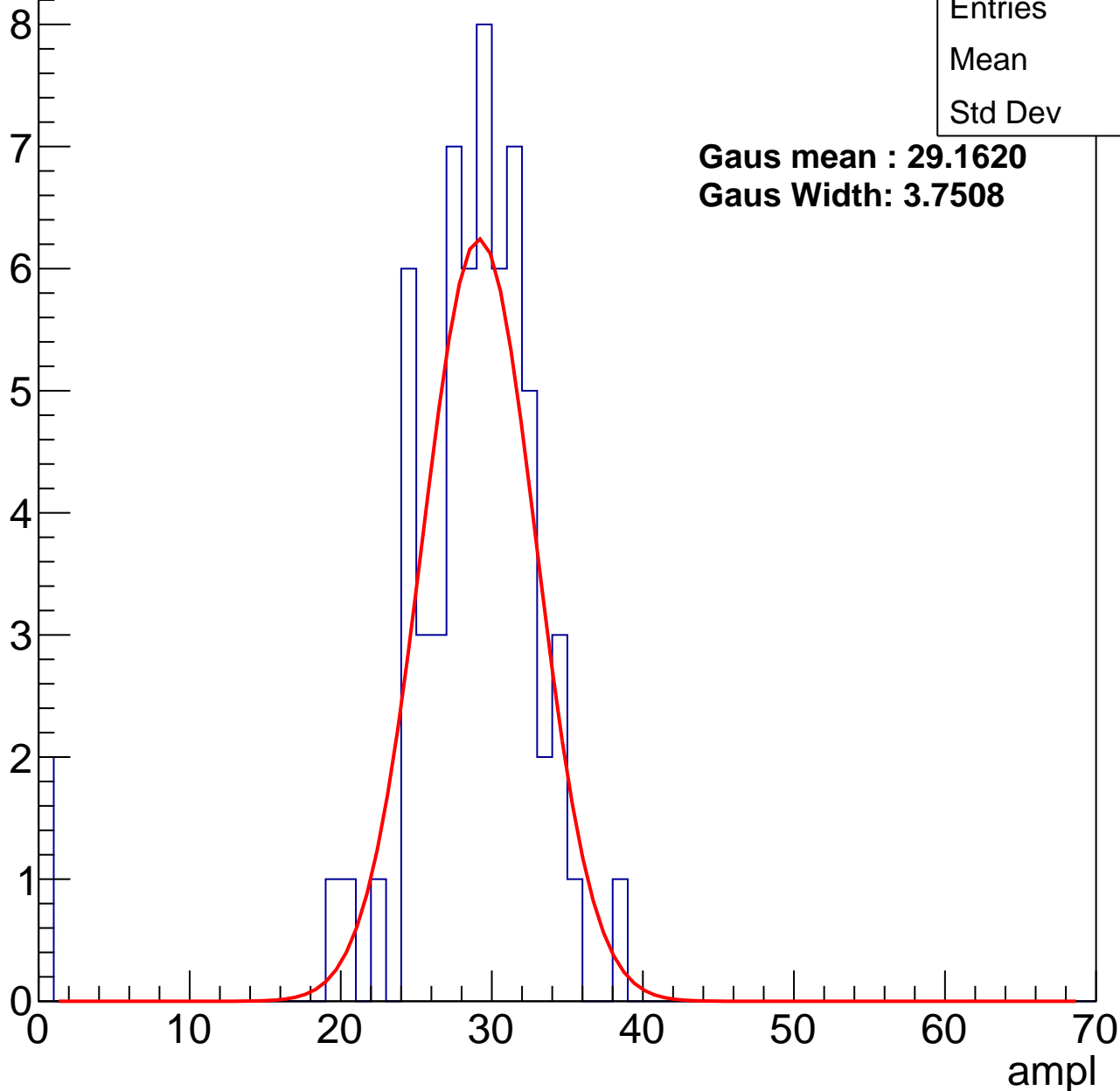
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.7
Std Dev	6.13

Gaus mean : 29.1620

Gaus Width: 3.7508



B1L100S, U15-ch111, adc1

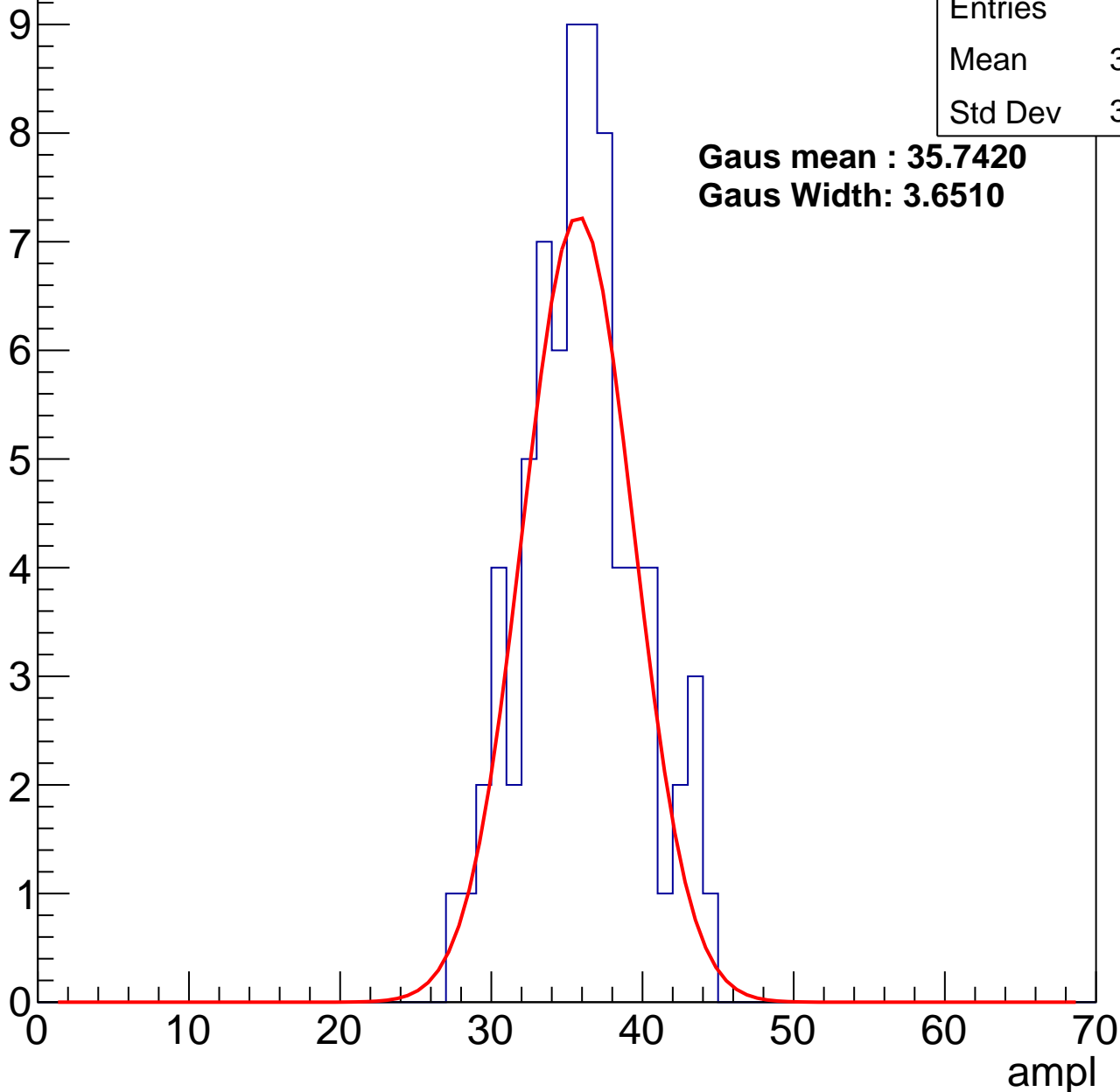
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	35.49
Std Dev	3.749

Gaus mean : 35.7420

Gaus Width: 3.6510



B1L100S, U15-ch111, adc2

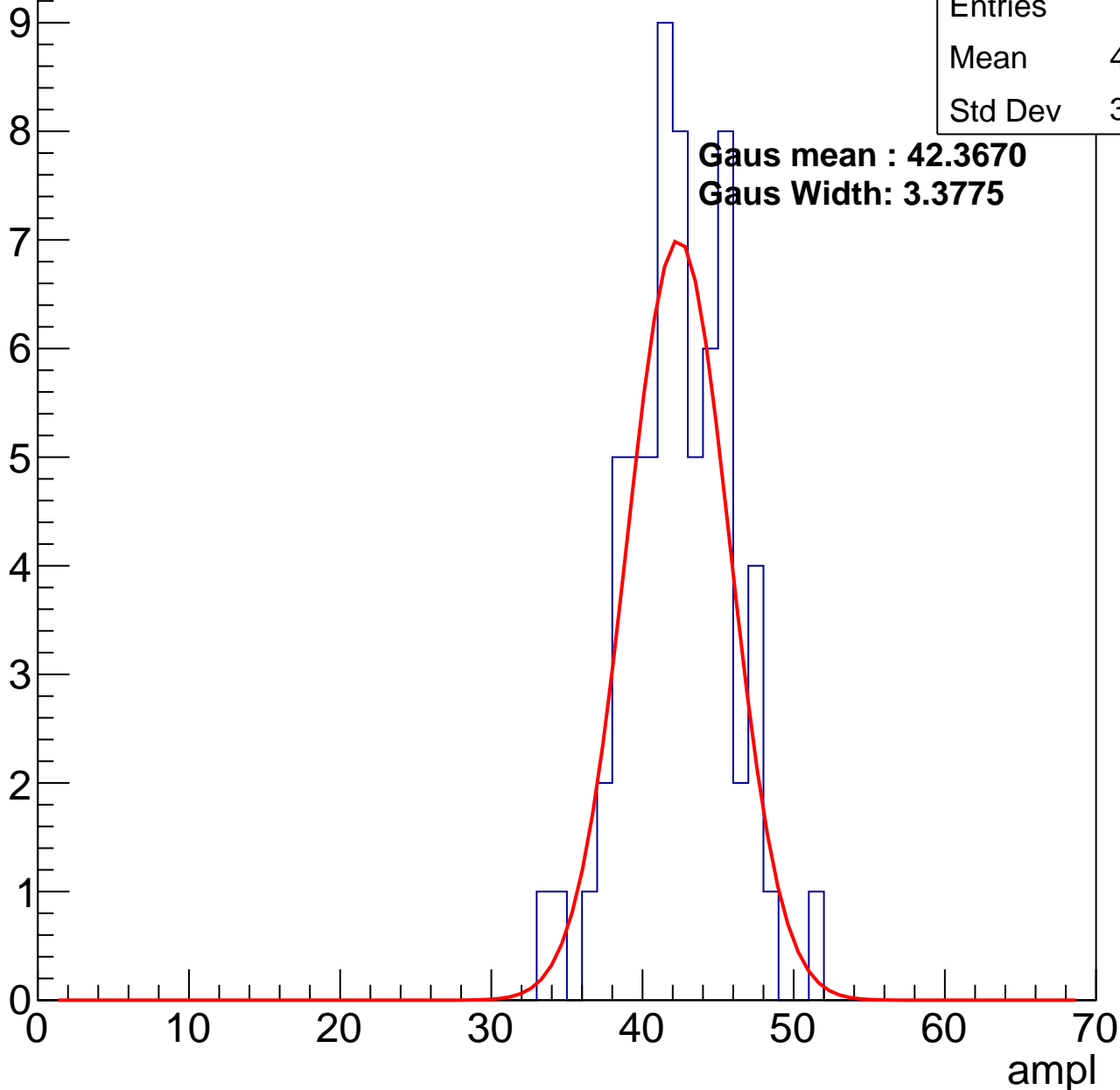
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.95
Std Dev	3.389

Gaus mean : 42.3670

Gaus Width: 3.3775

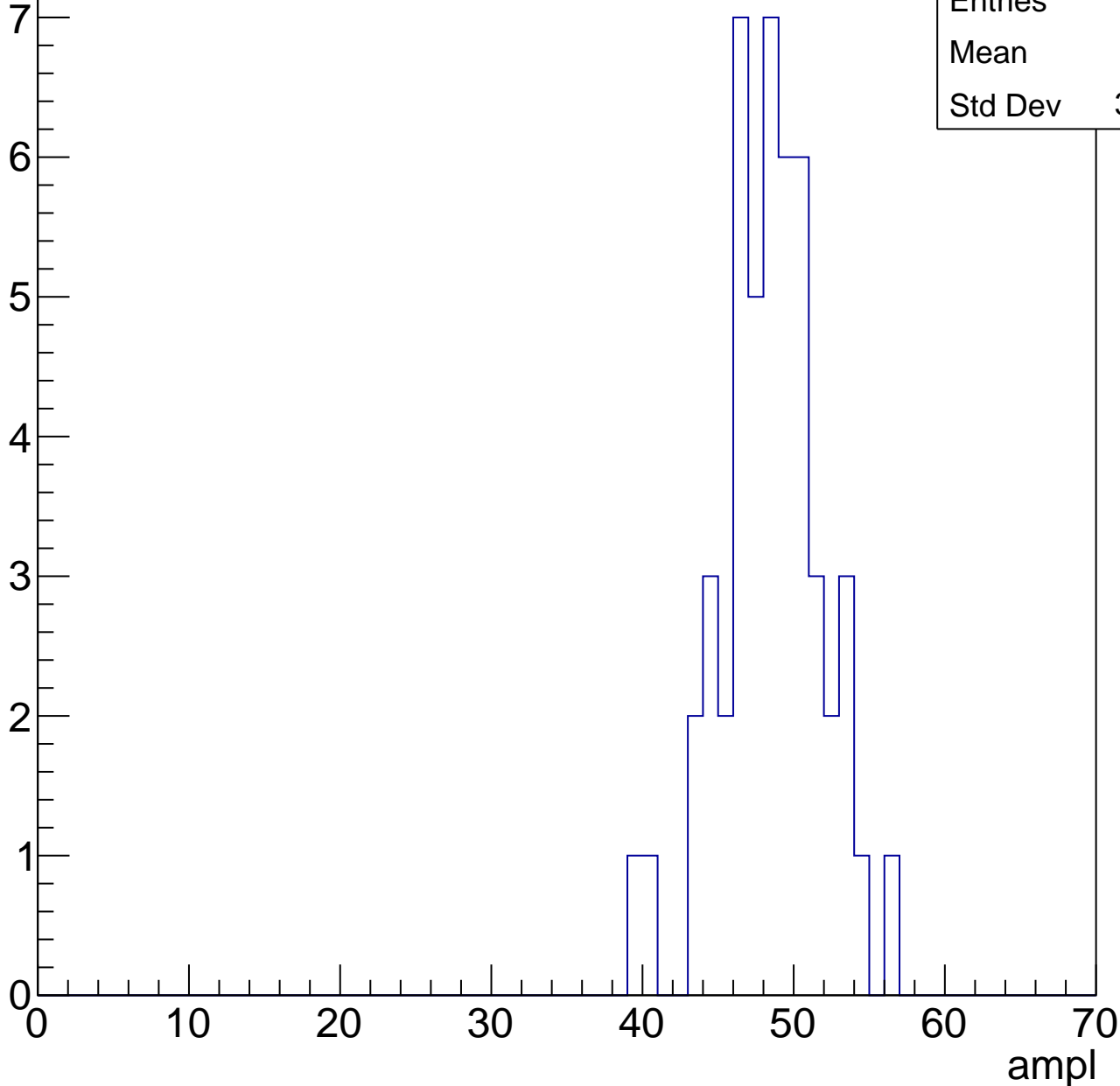


B1L100S, U15-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	48
Std Dev	3.341

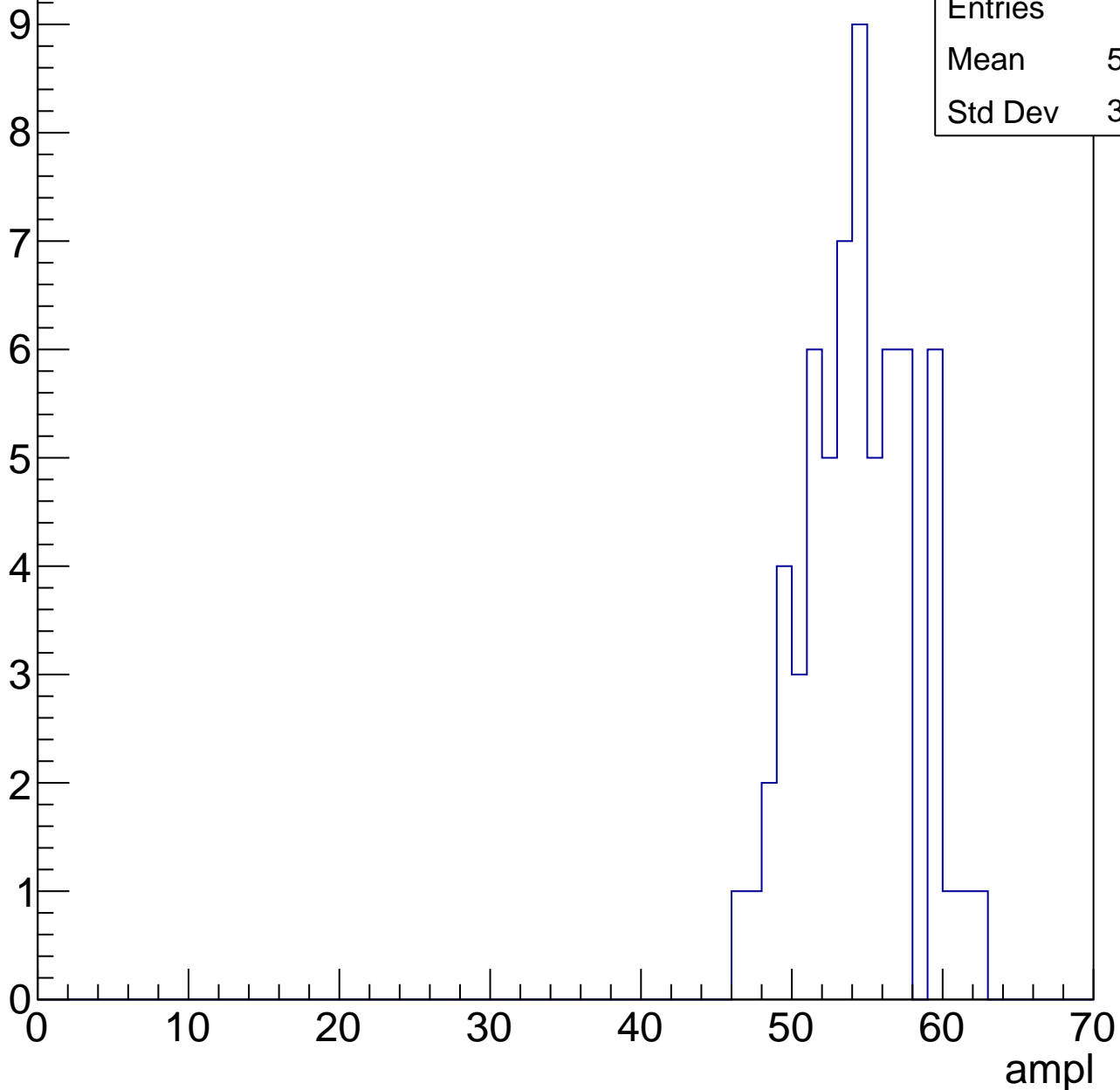


B1L100S, U15-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	53.88
Std Dev	3.524

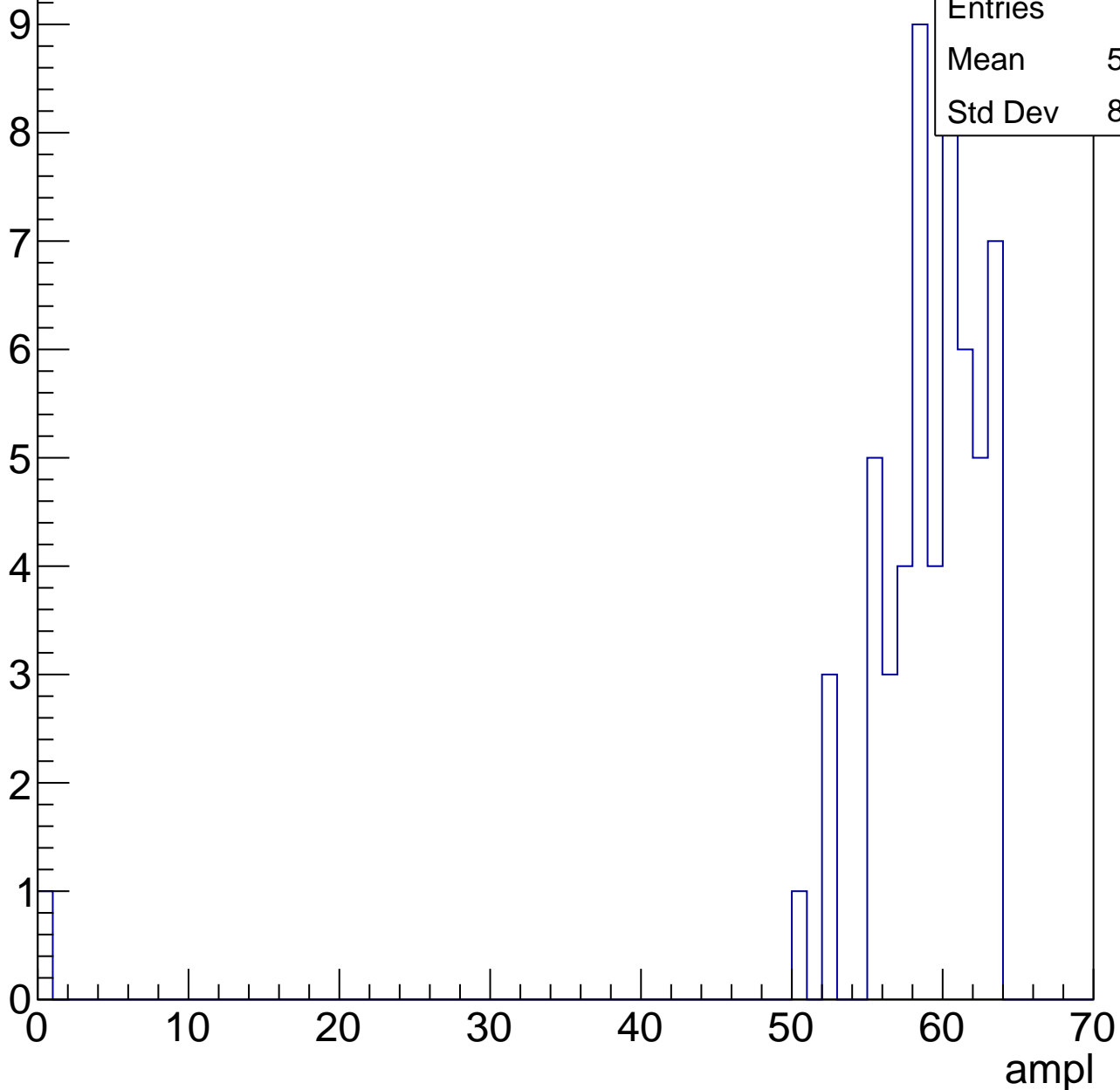


B1L100S, U15-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

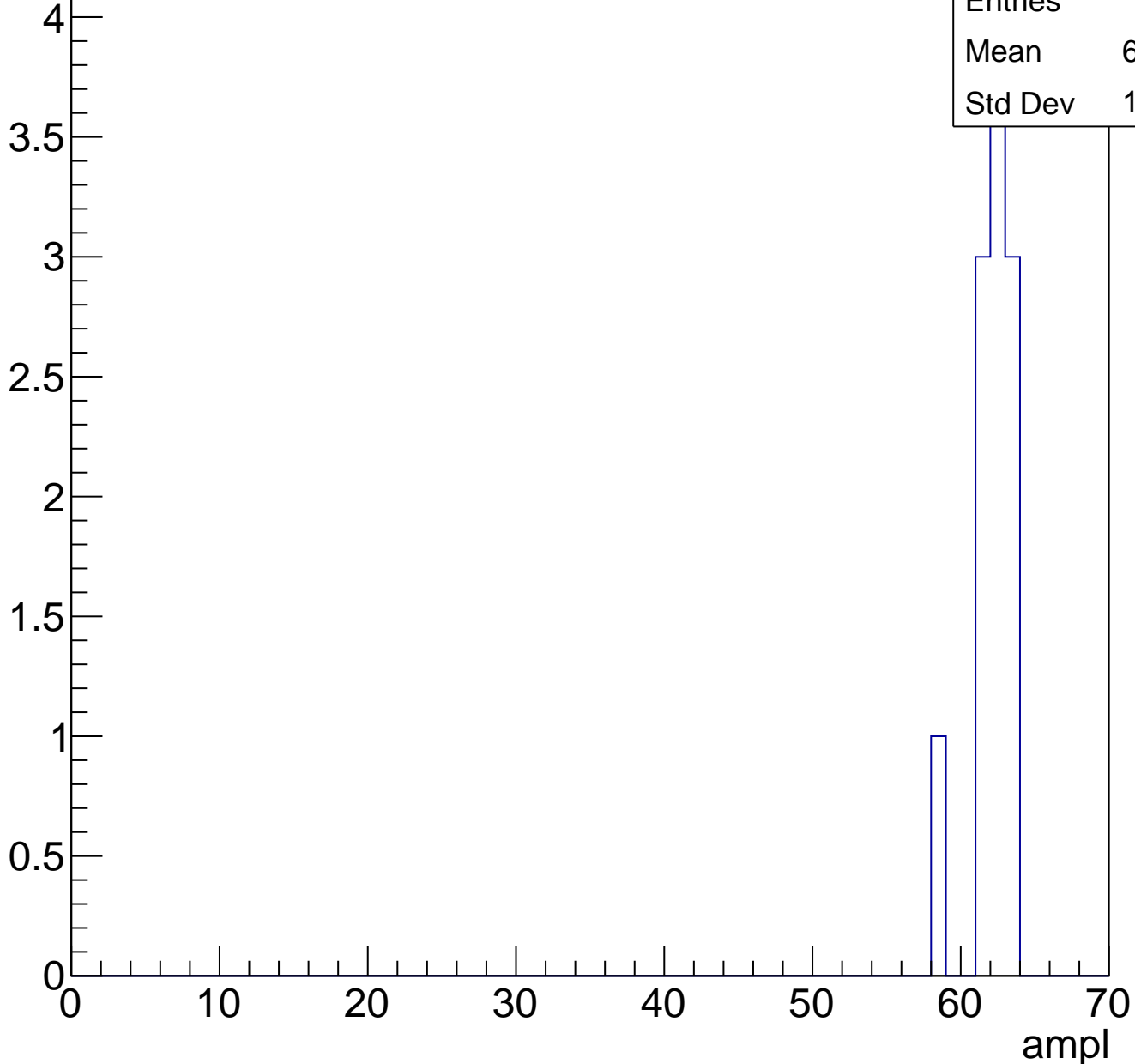
Entries	57
Mean	57.75
Std Dev	8.317



B1L100S, U15-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U15-ch112, adc0

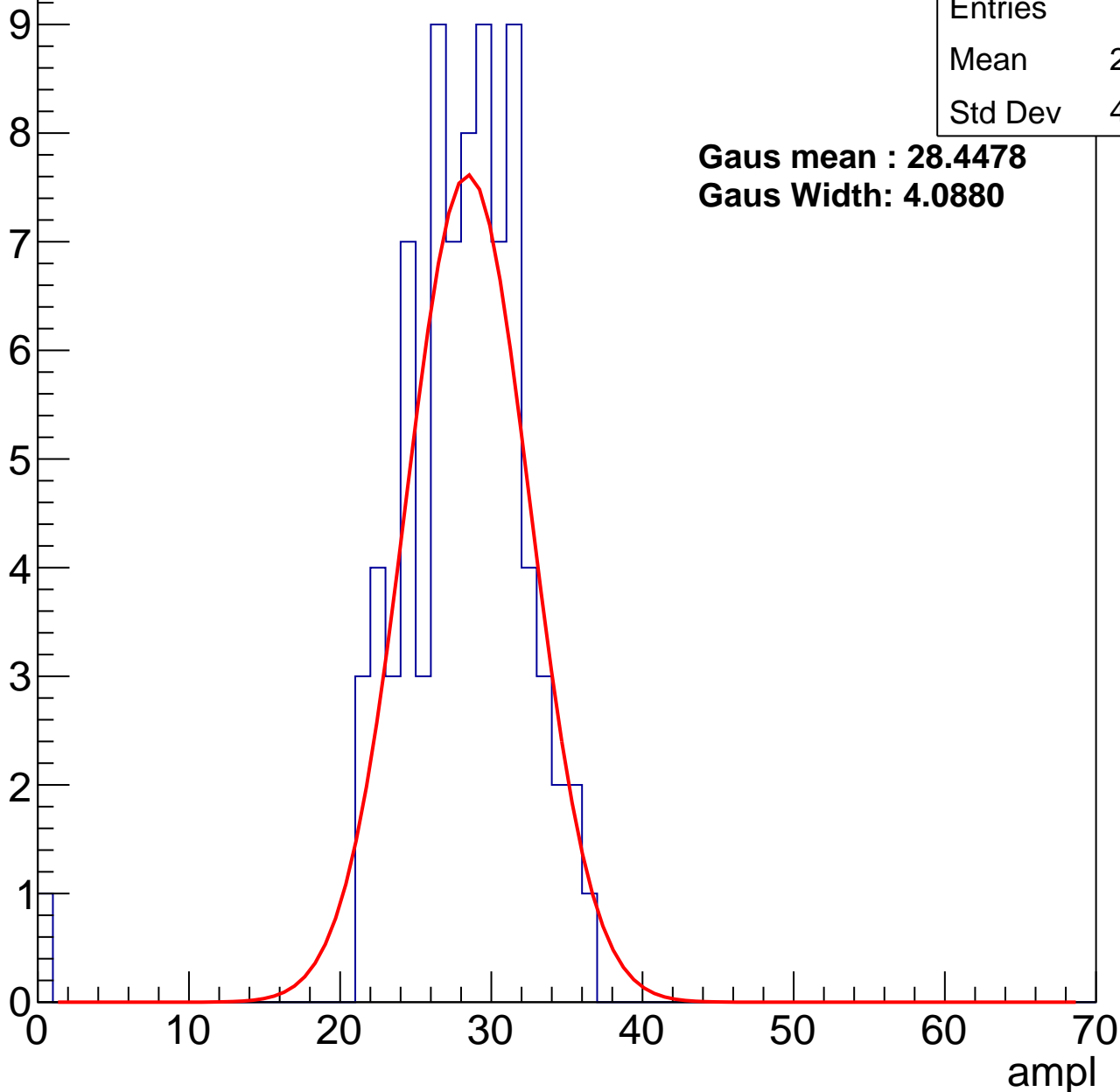
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	27.57
Std Dev	4.696

Gaus mean : 28.4478

Gaus Width: 4.0880



B1L100S, U15-ch112, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

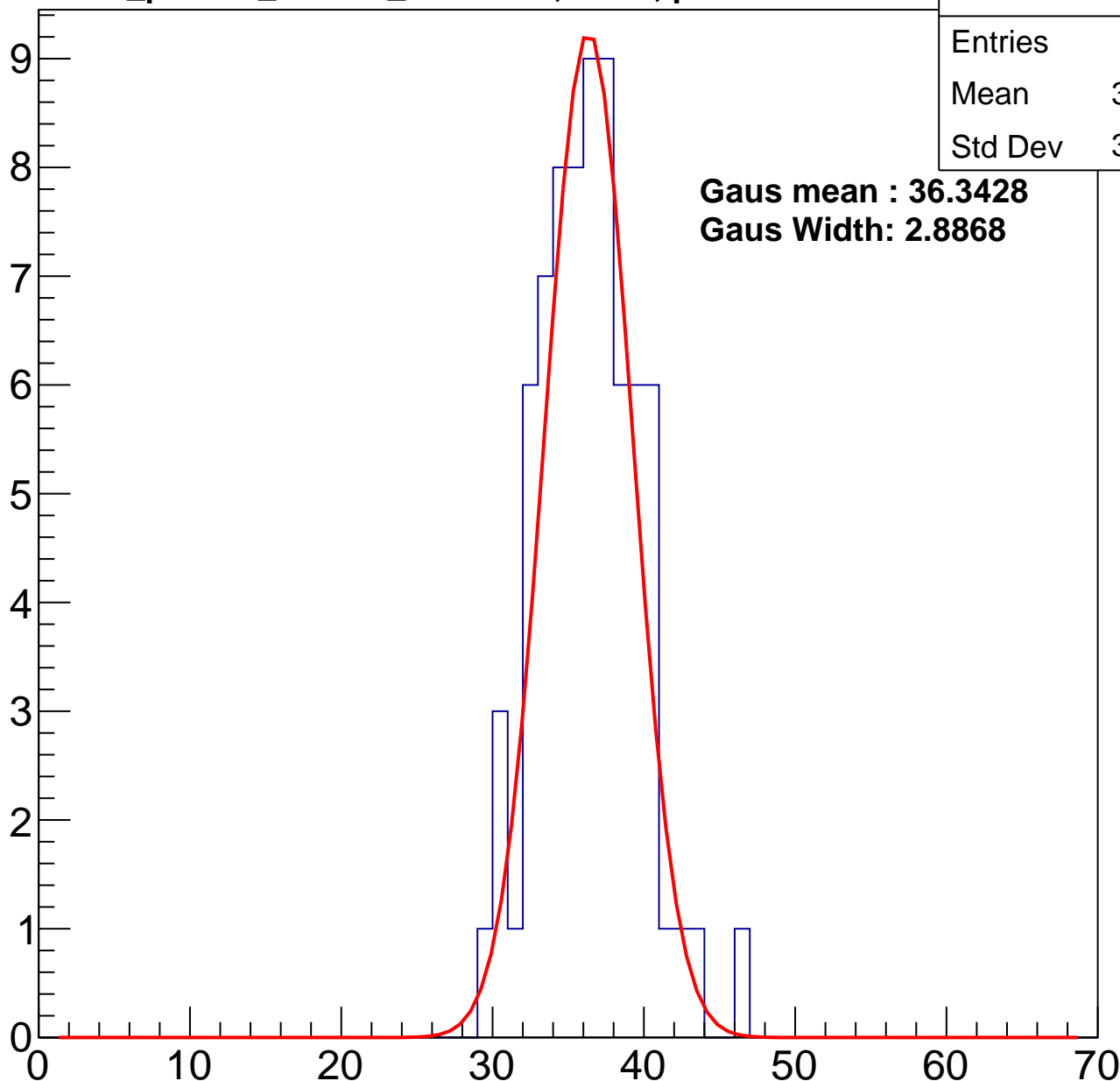
9
8
7
6
5
4
3
2
1
0

Entries	74
Mean	35.89
Std Dev	3.232

Gaus mean : 36.3428

Gaus Width: 2.8868

ampl



B1L100S, U15-ch112, adc2

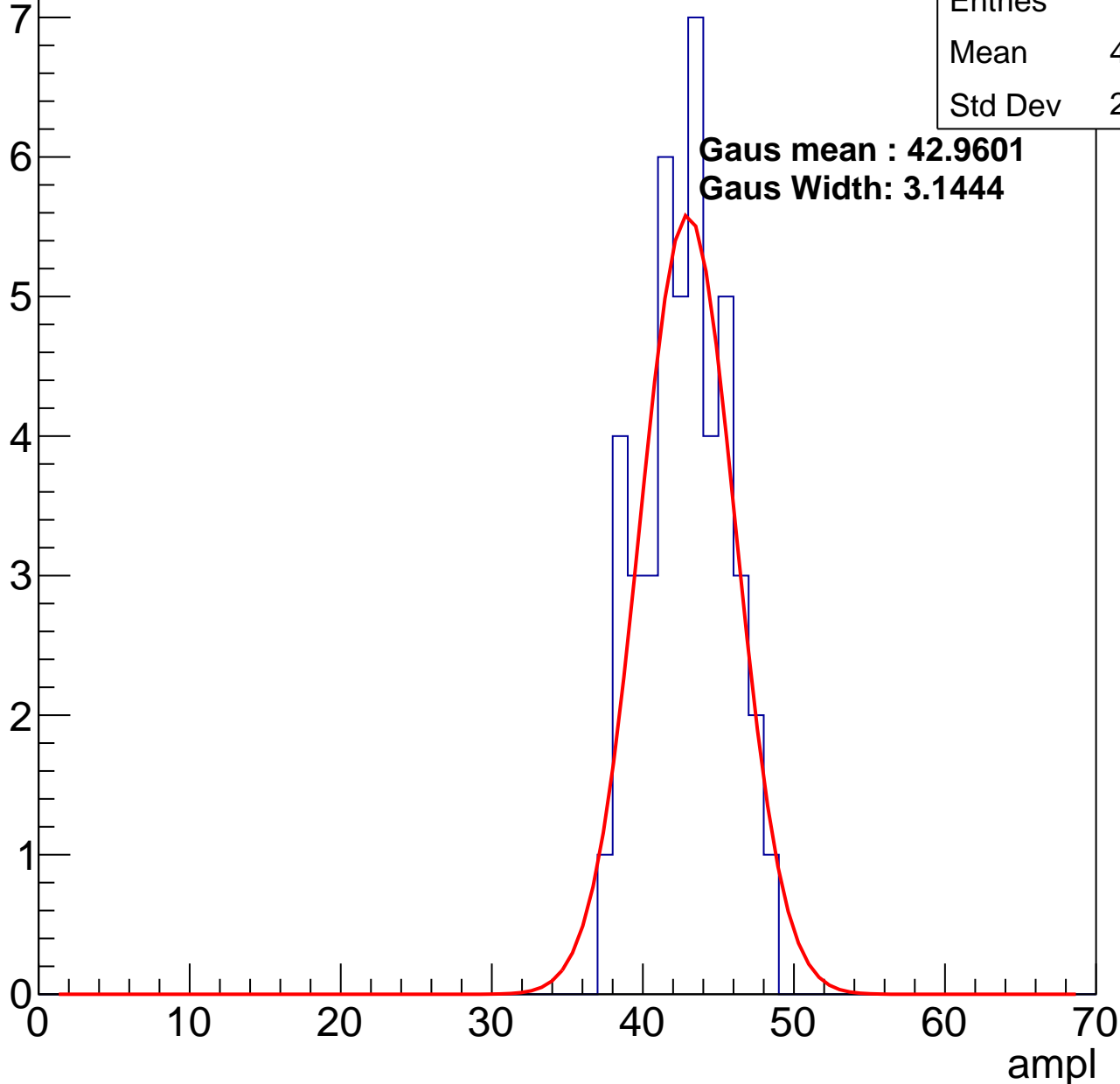
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	42.36
Std Dev	2.739

Gaus mean : 42.9601

Gaus Width: 3.1444

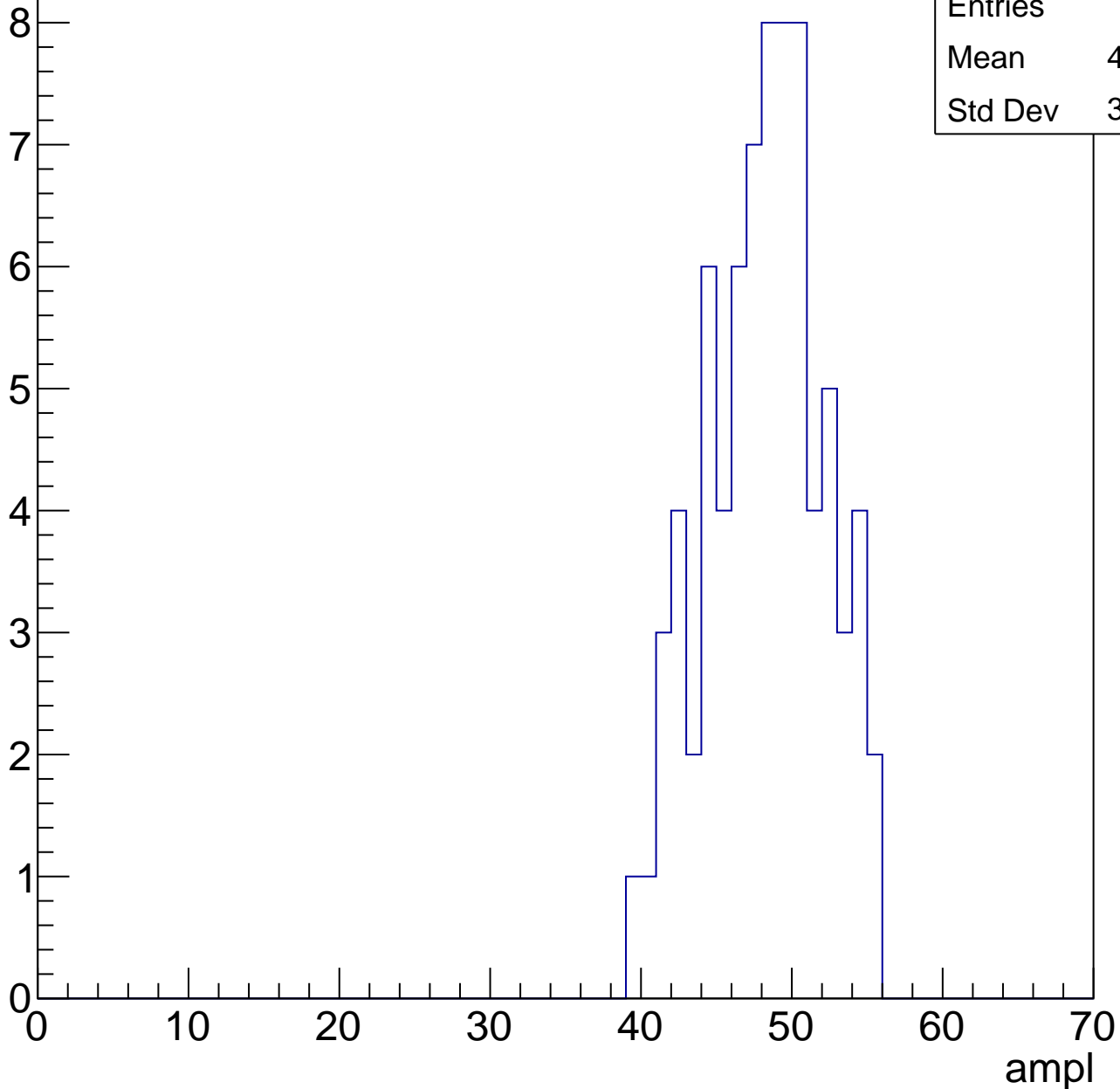


B1L100S, U15-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	47.76
Std Dev	3.835

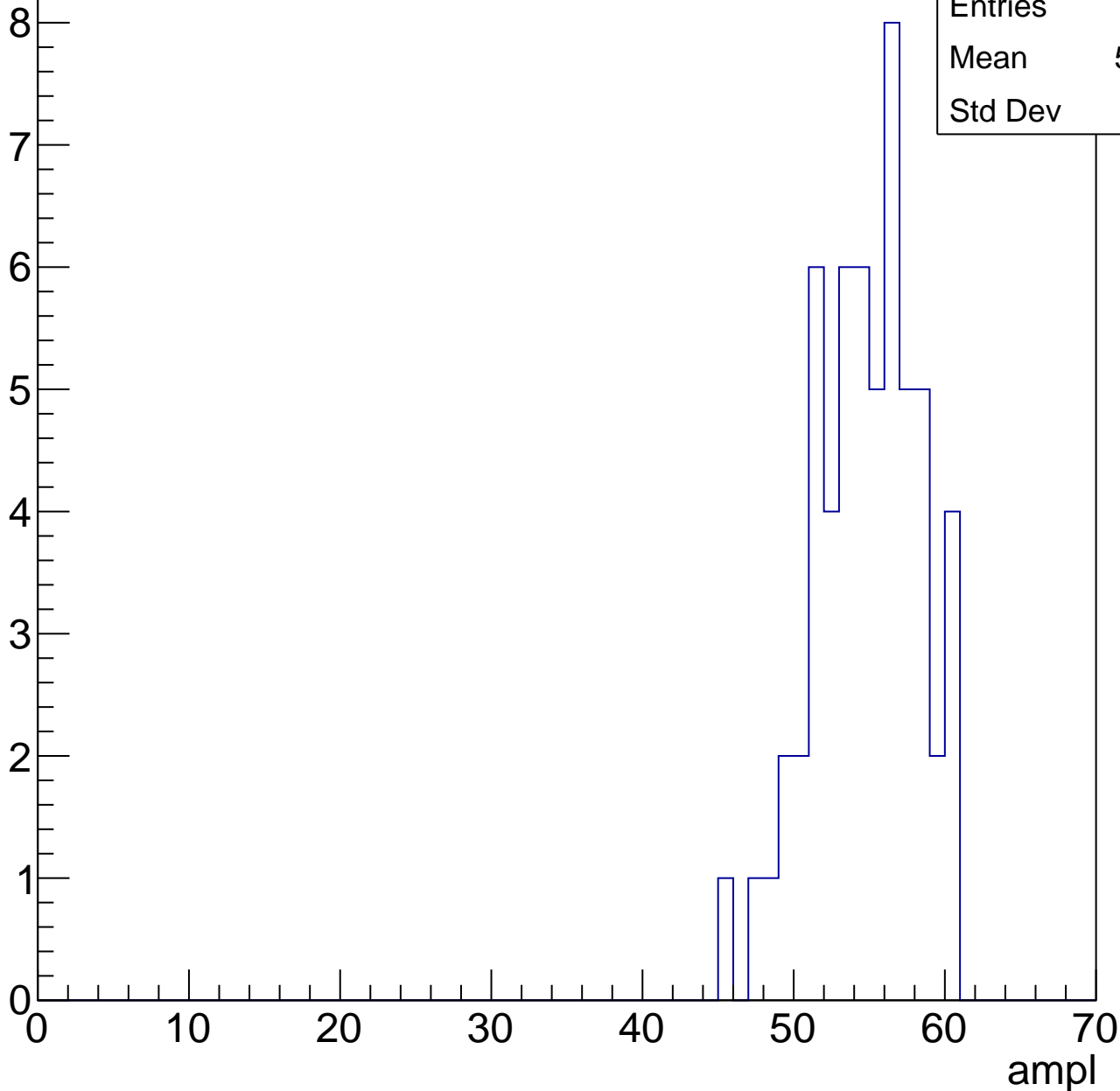


B1L100S, U15-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	54.31
Std Dev	3.41

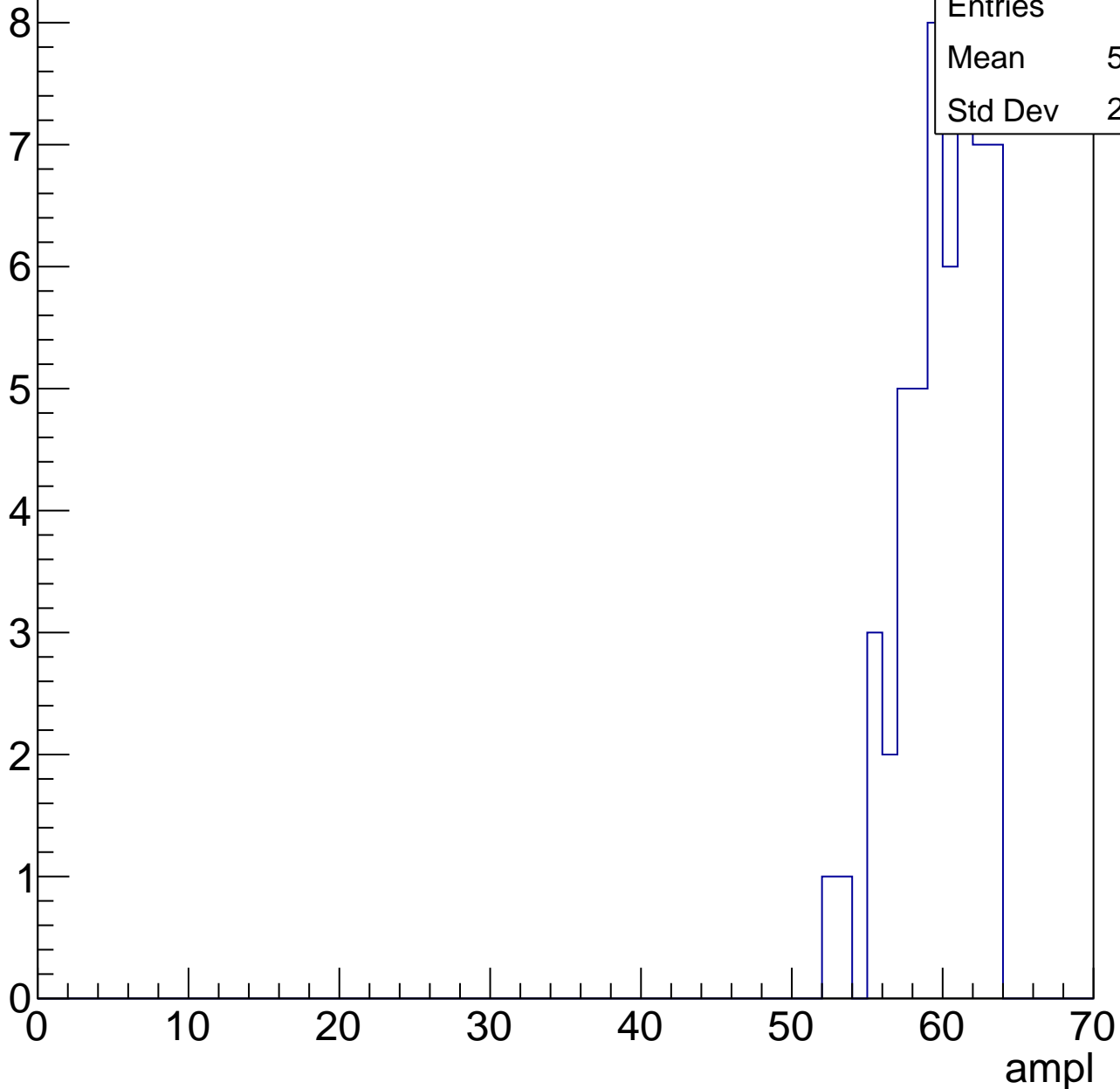


B1L100S, U15-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

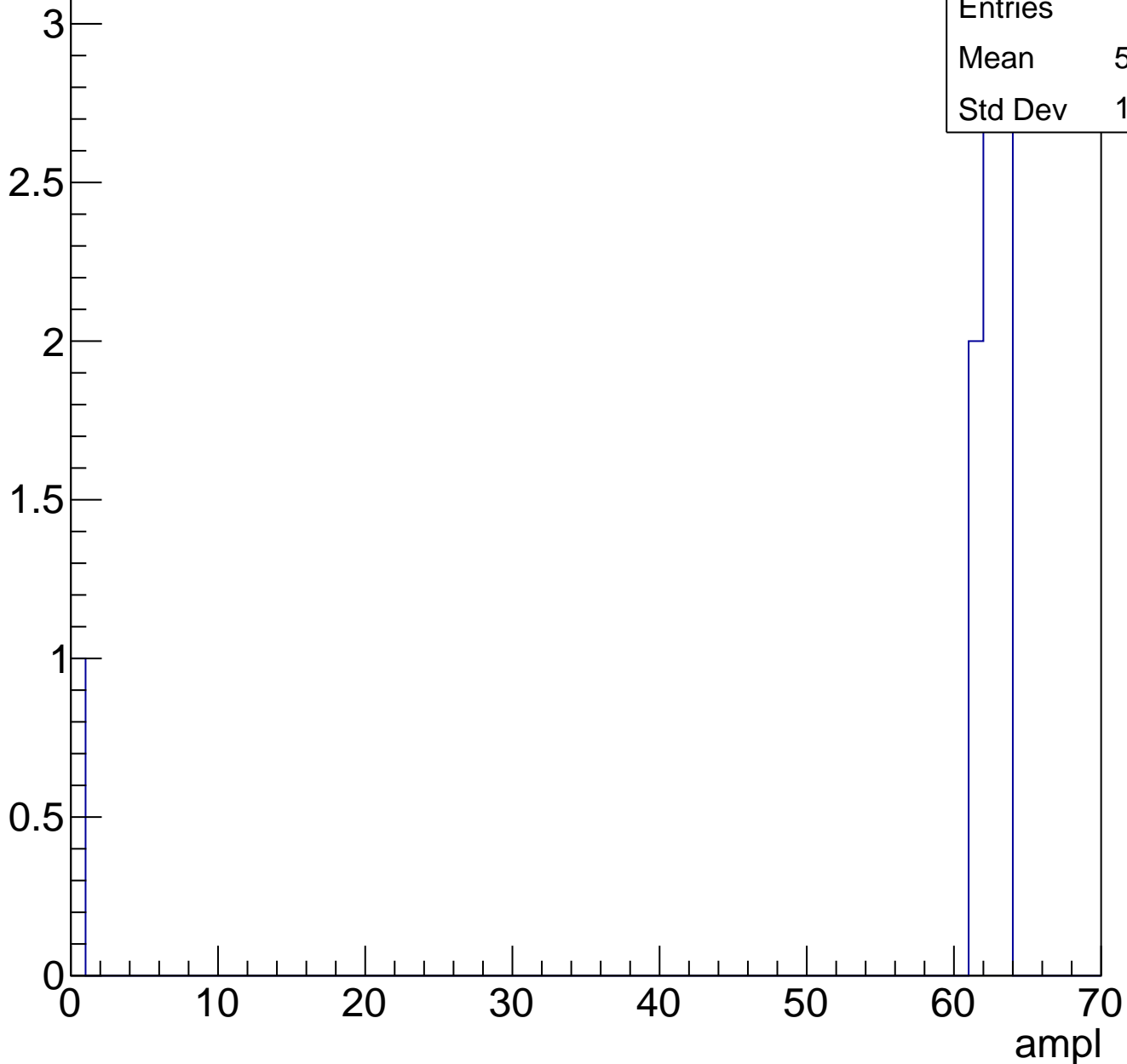
Entries	53
Mean	59.47
Std Dev	2.668



B1L100S, U15-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch113, adc0

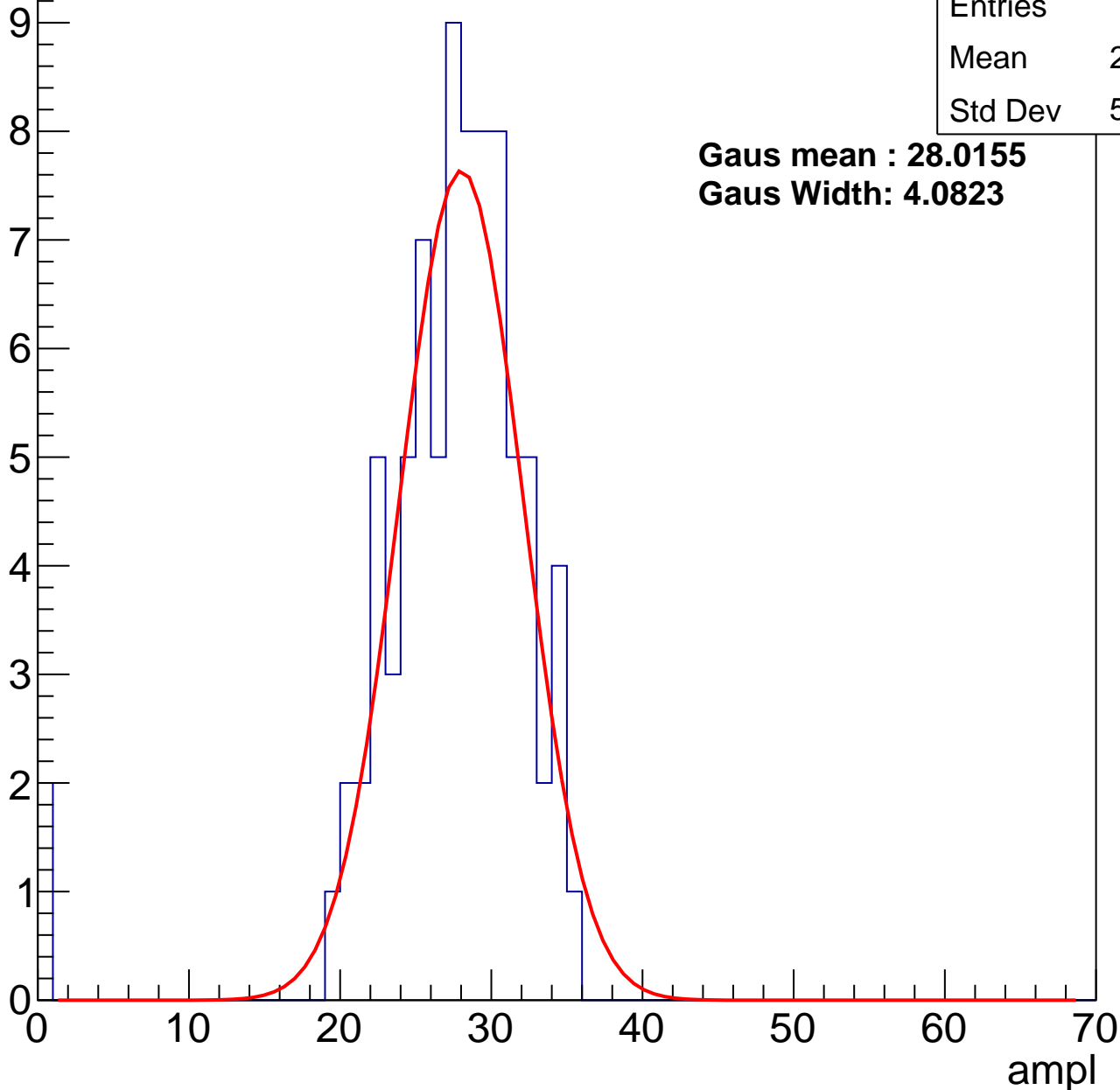
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	26.78
Std Dev	5.618

Gaus mean : 28.0155

Gaus Width: 4.0823



B1L100S, U15-ch113, adc1

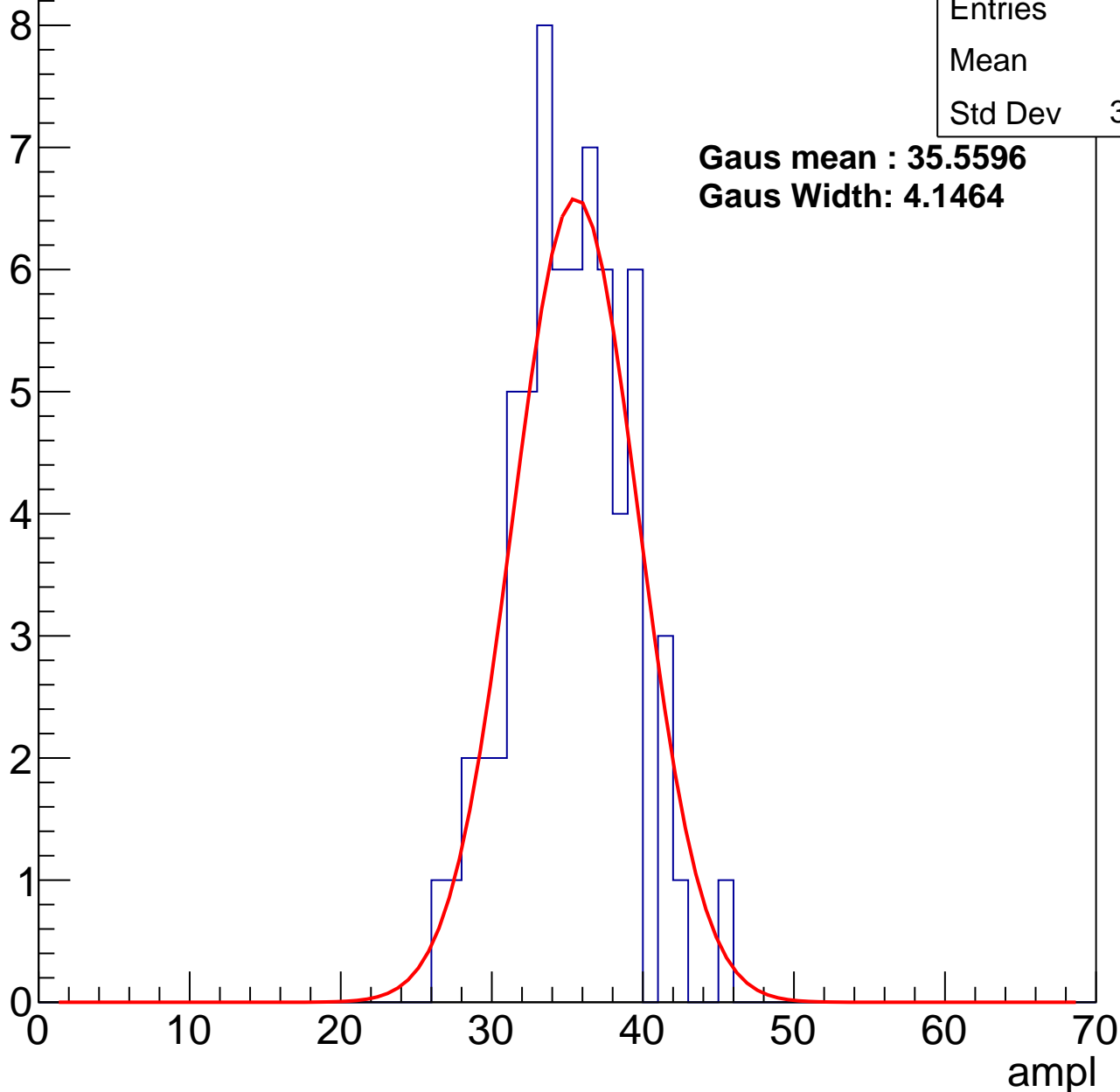
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	34.7
Std Dev	3.778

Gaus mean : 35.5596

Gaus Width: 4.1464



B1L100S, U15-ch113, adc2

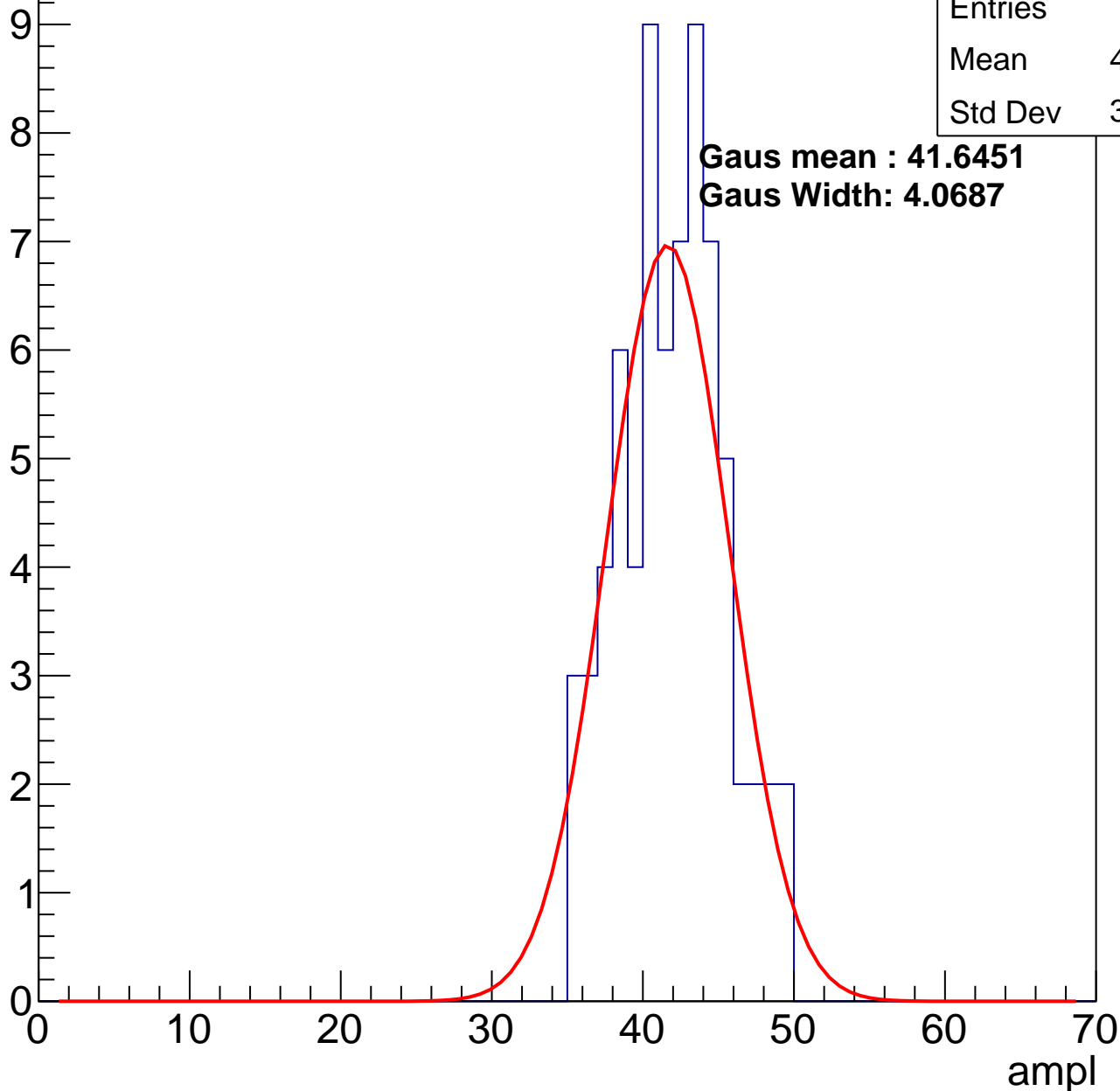
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	41.48
Std Dev	3.447

Gaus mean : 41.6451

Gaus Width: 4.0687

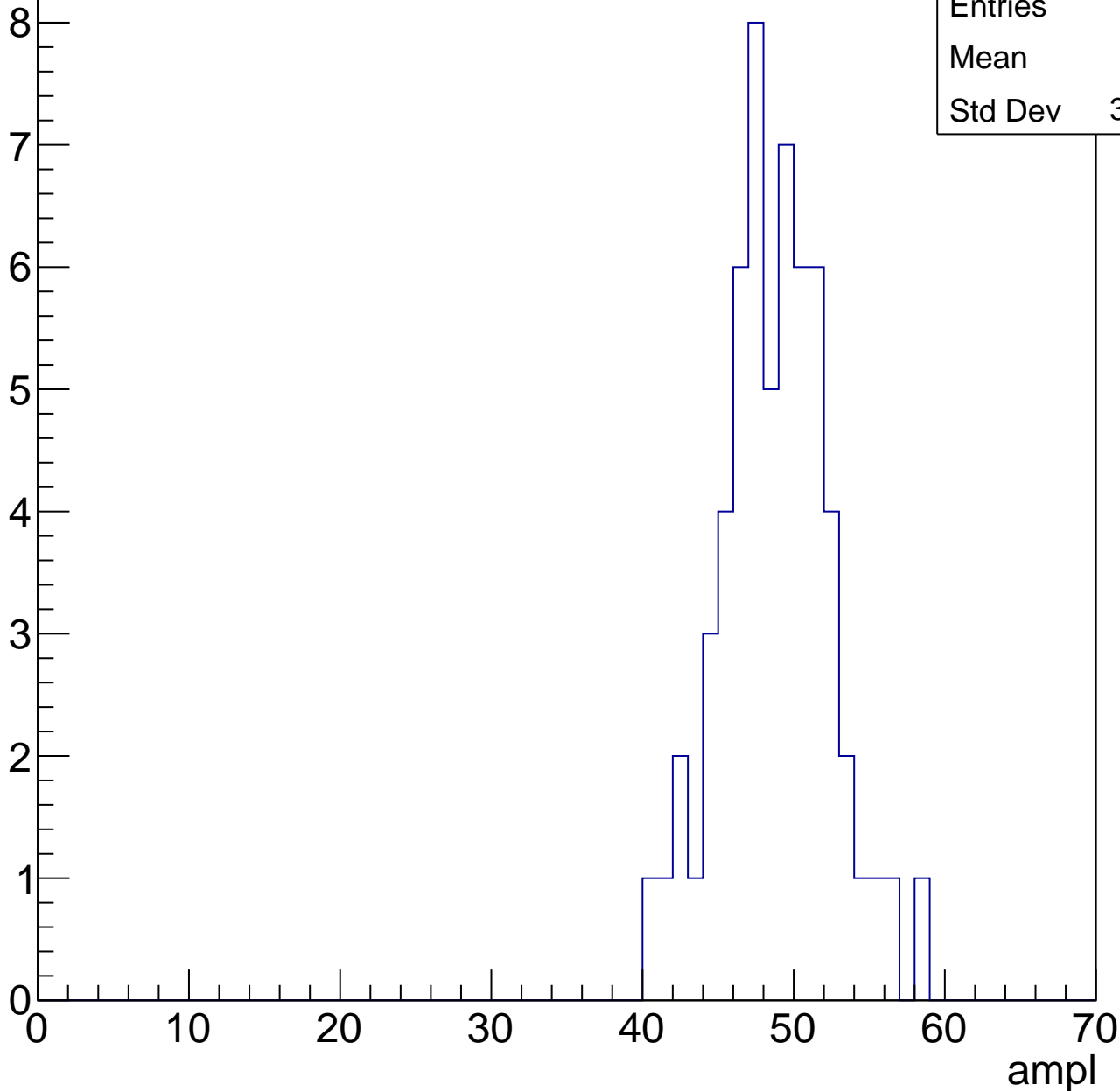


B1L100S, U15-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	48.3
Std Dev	3.579

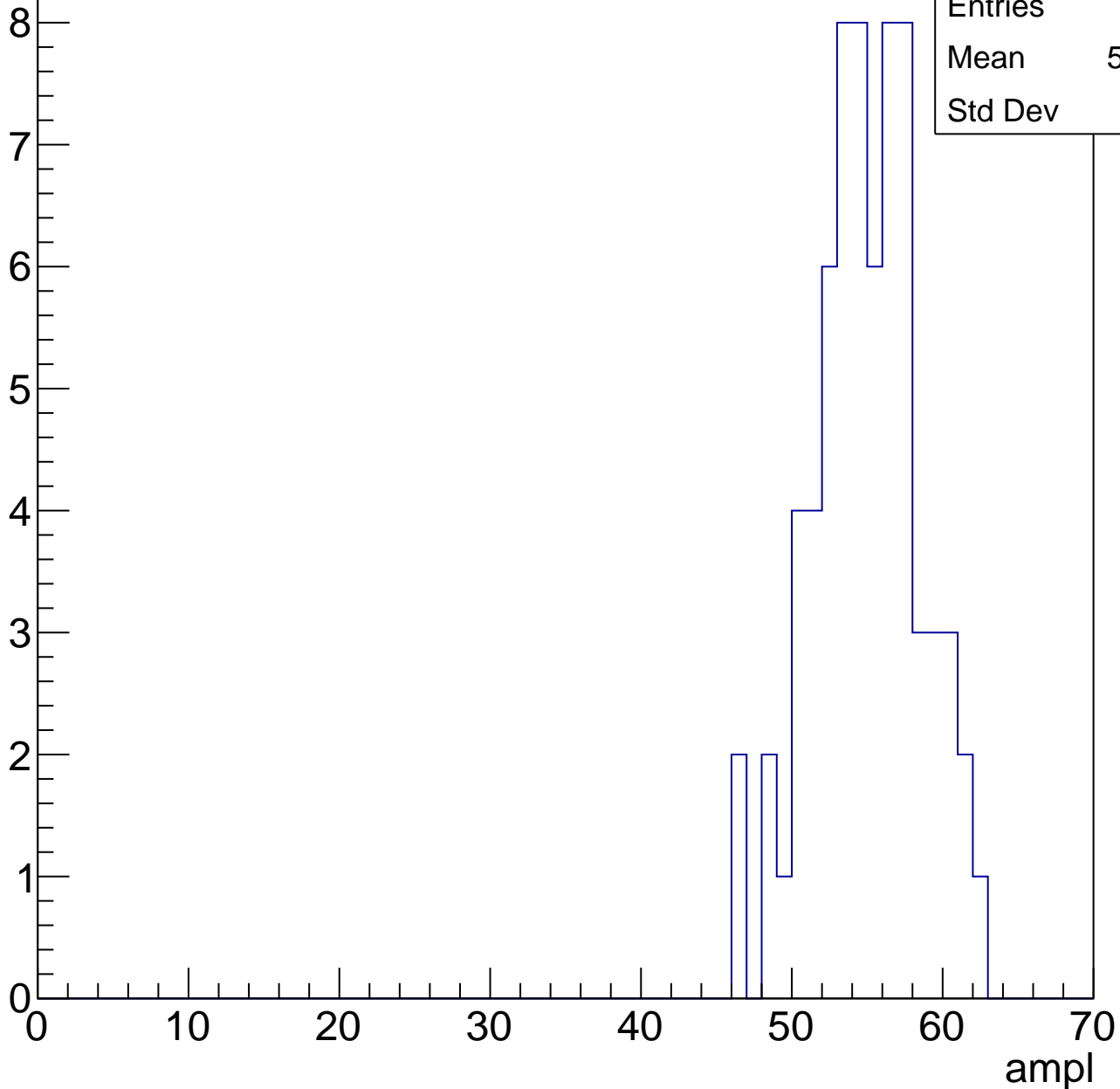


B1L100S, U15-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	54.46
Std Dev	3.5

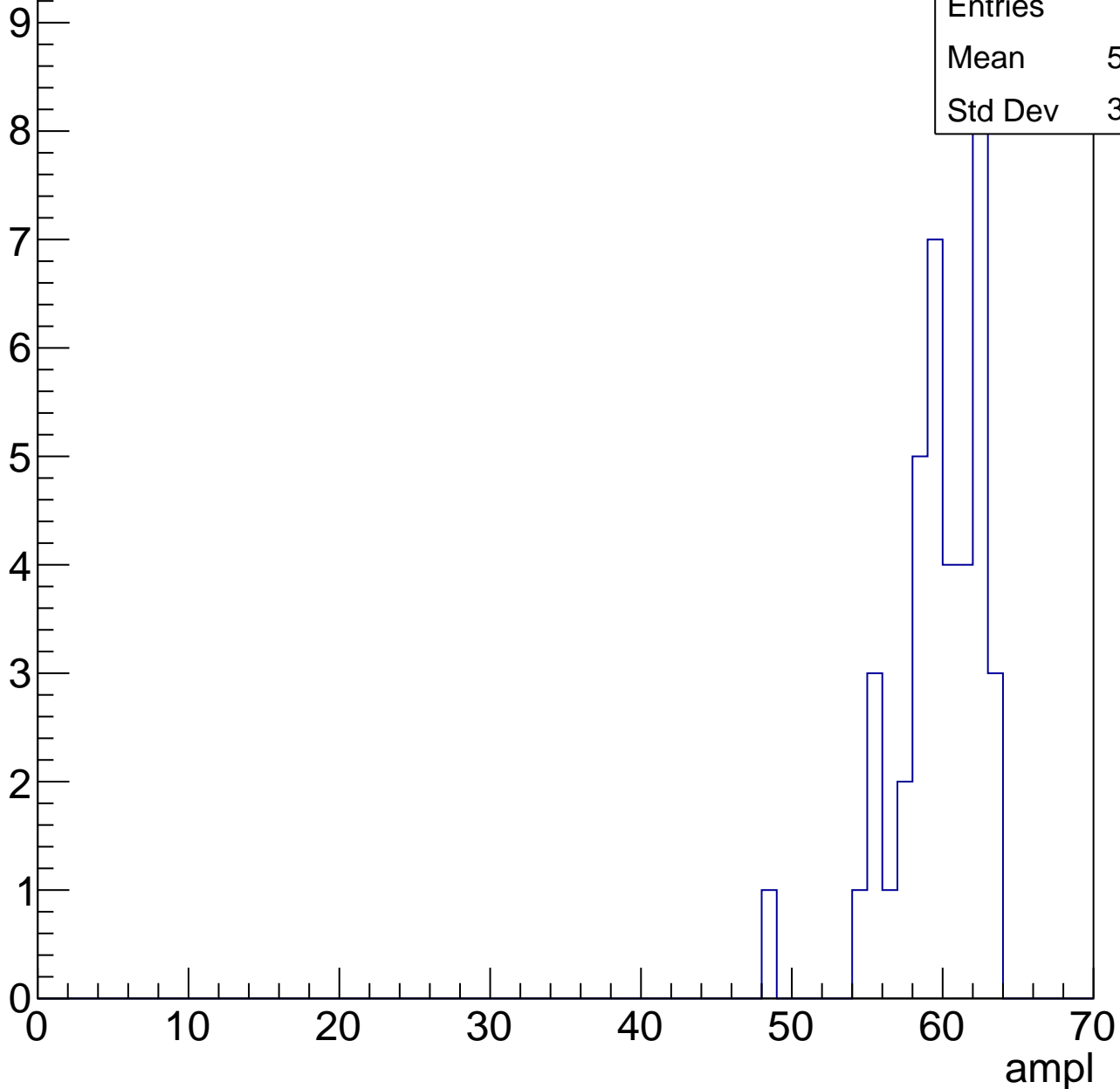


B1L100S, U15-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	59.27
Std Dev	3.008

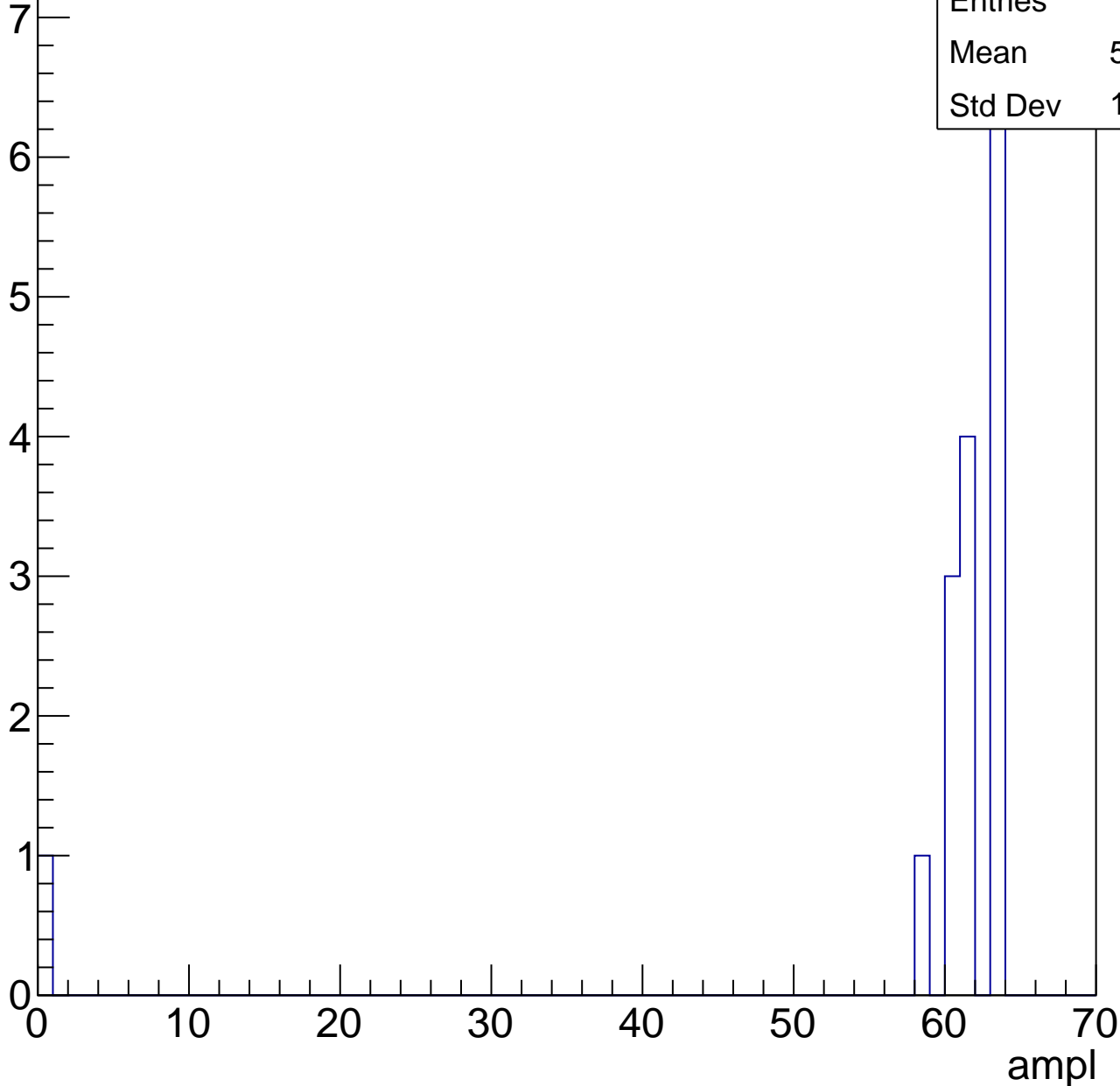


B1L100S, U15-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

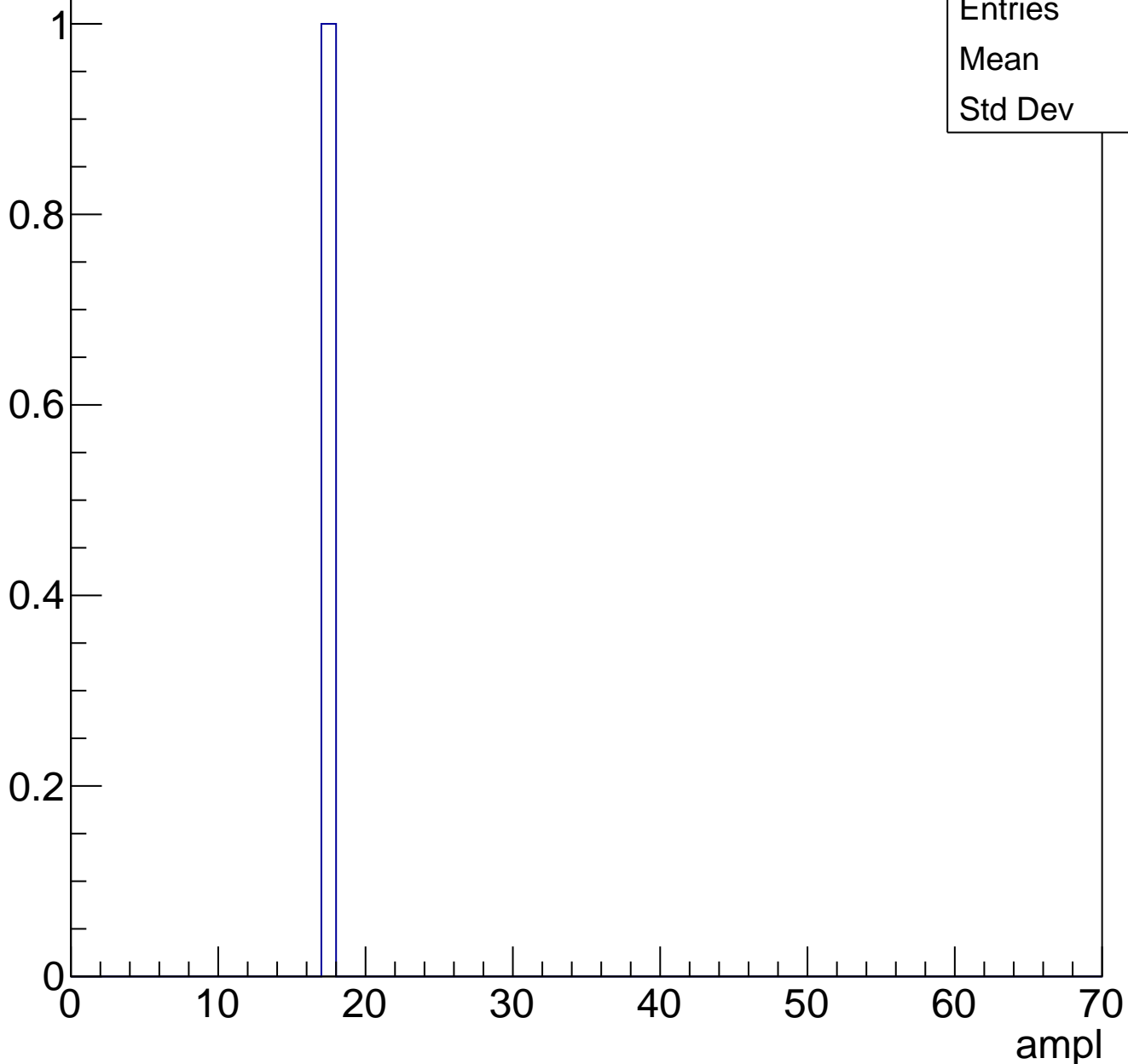
Entries	16
Mean	57.69
Std Dev	14.97



B1L100S, U15-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	17
Std Dev	0

B1L100S, U15-ch114, adc0

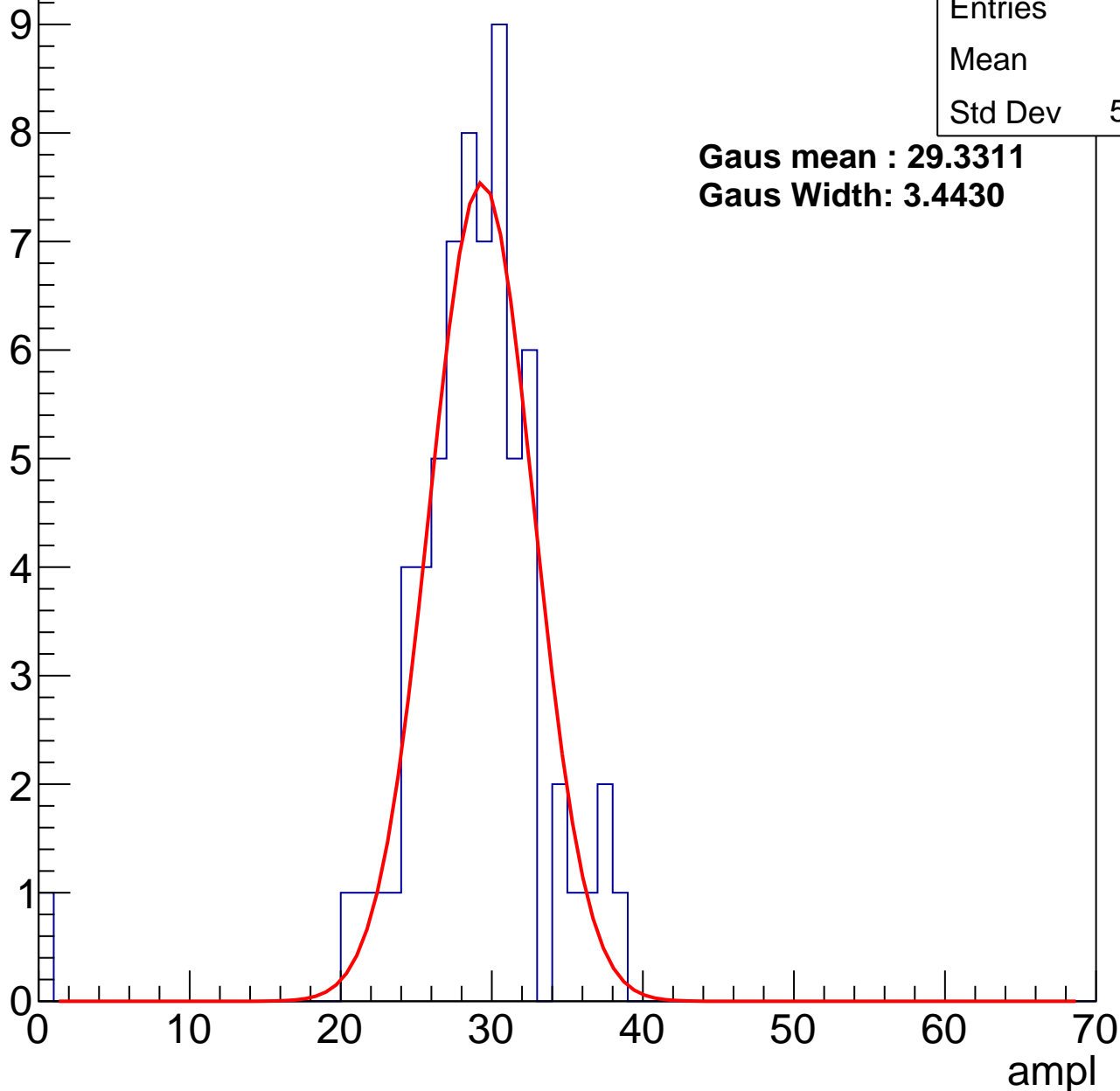
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.3
Std Dev	5.052

Gaus mean : 29.3311

Gaus Width: 3.4430



B1L100S, U15-ch114, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	35.98
Std Dev	4.111

Gaus mean : 36.7639

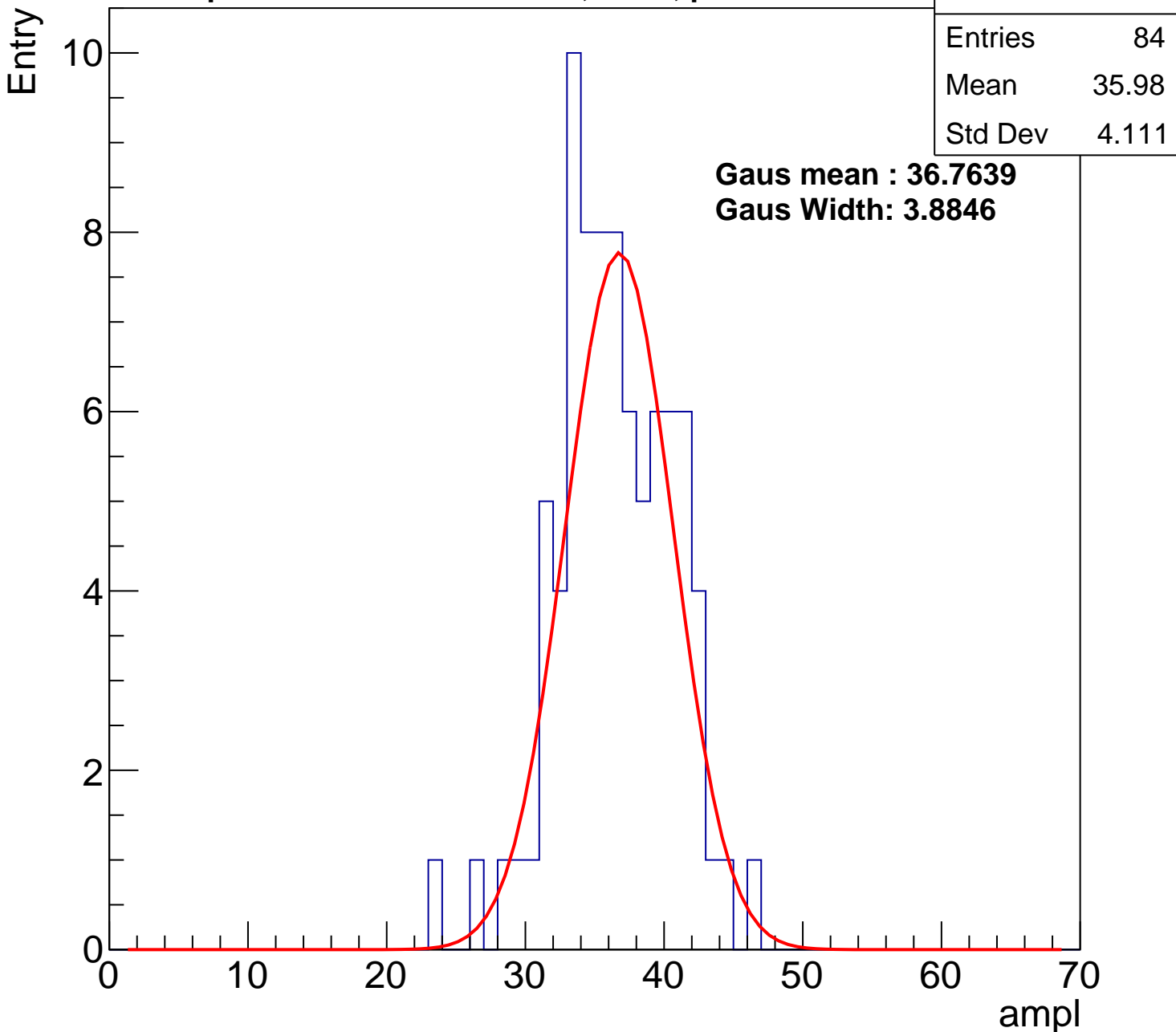
Gaus Width: 3.8846

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U15-ch114, adc2

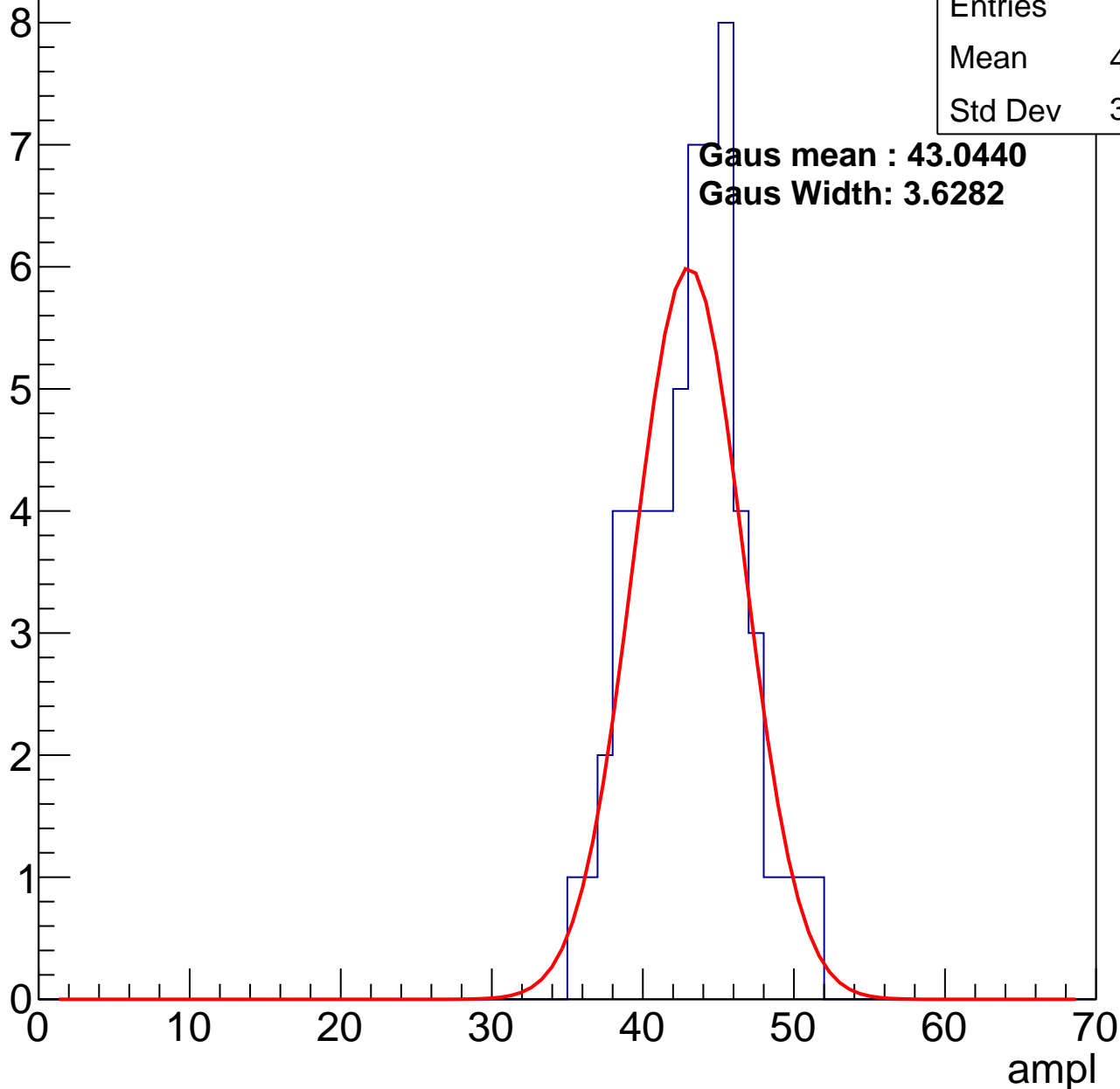
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	42.74
Std Dev	3.472

Gaus mean : 43.0440

Gaus Width: 3.6282

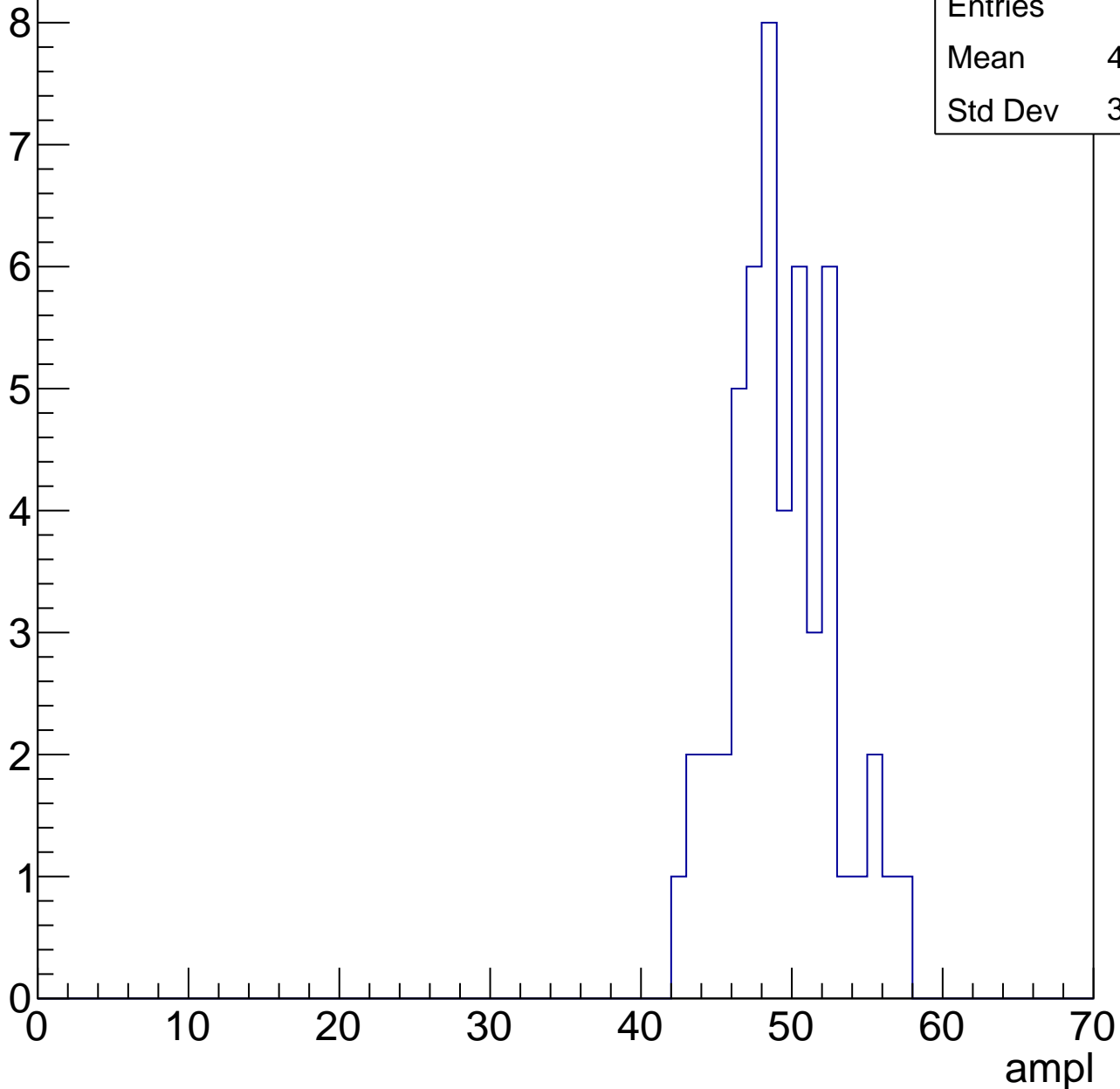


B1L100S, U15-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	48.88
Std Dev	3.364

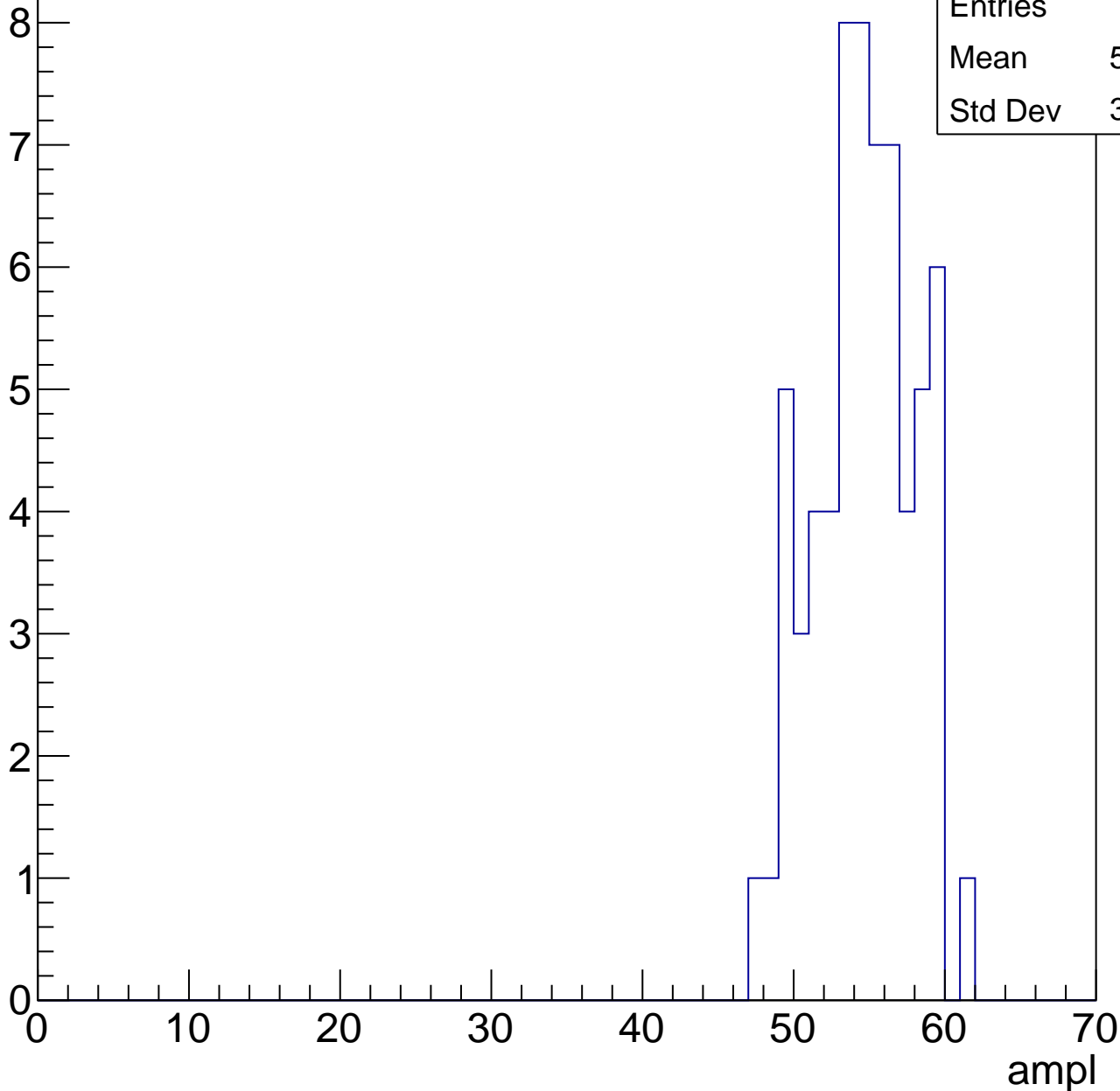


B1L100S, U15-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	54.19
Std Dev	3.225

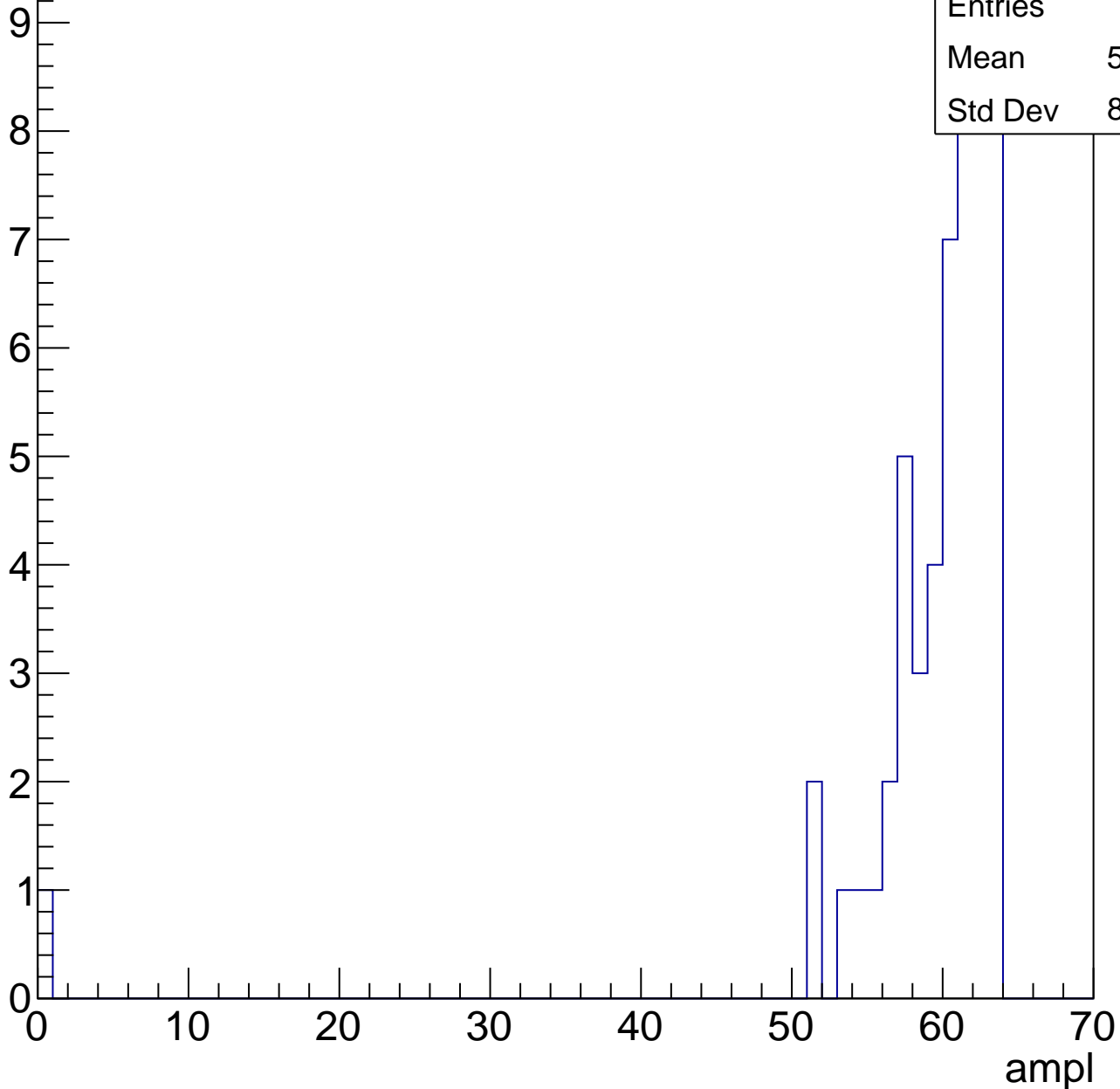


B1L100S, U15-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

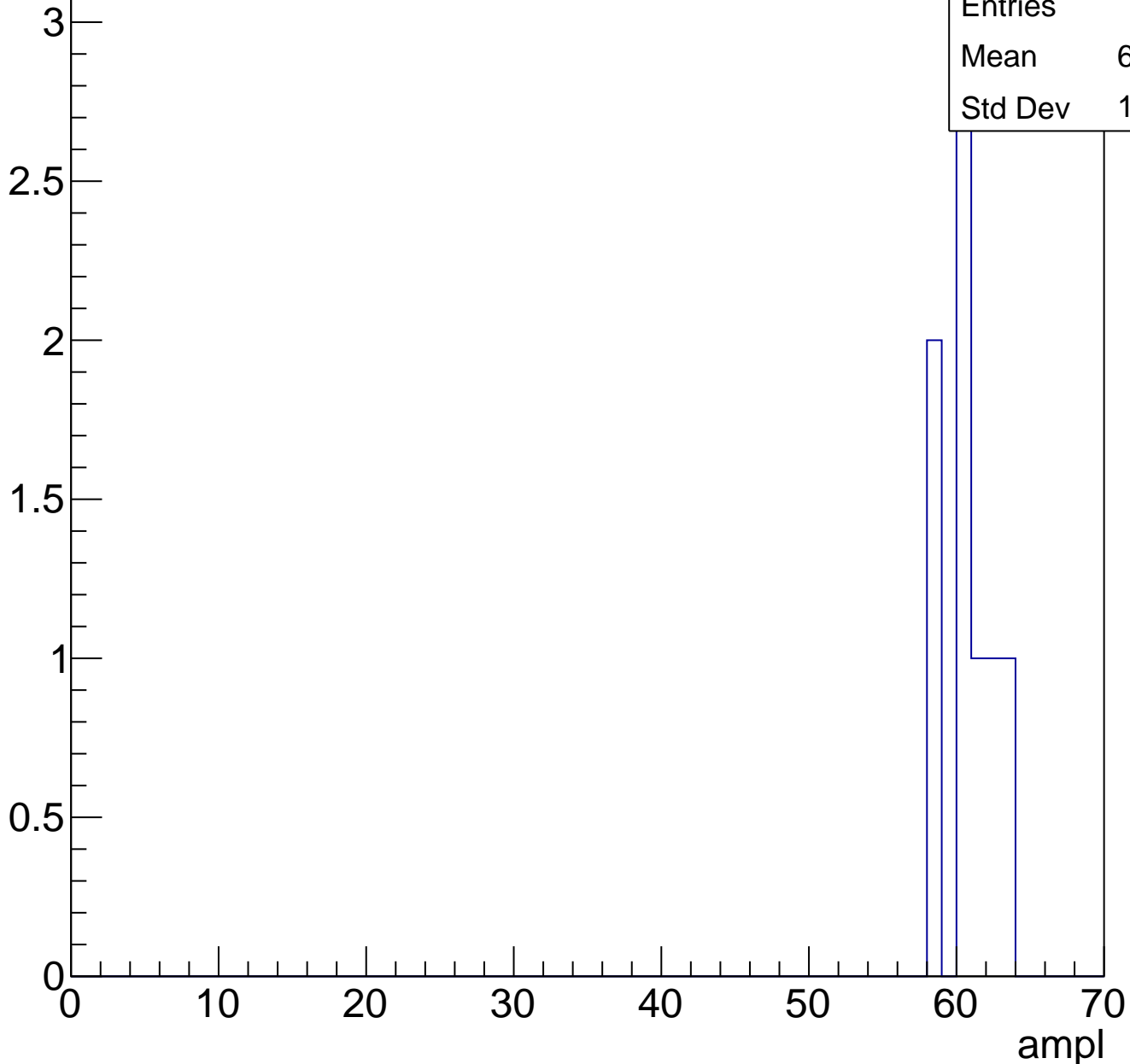
Entries	52
Mean	58.48
Std Dev	8.728



B1L100S, U15-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

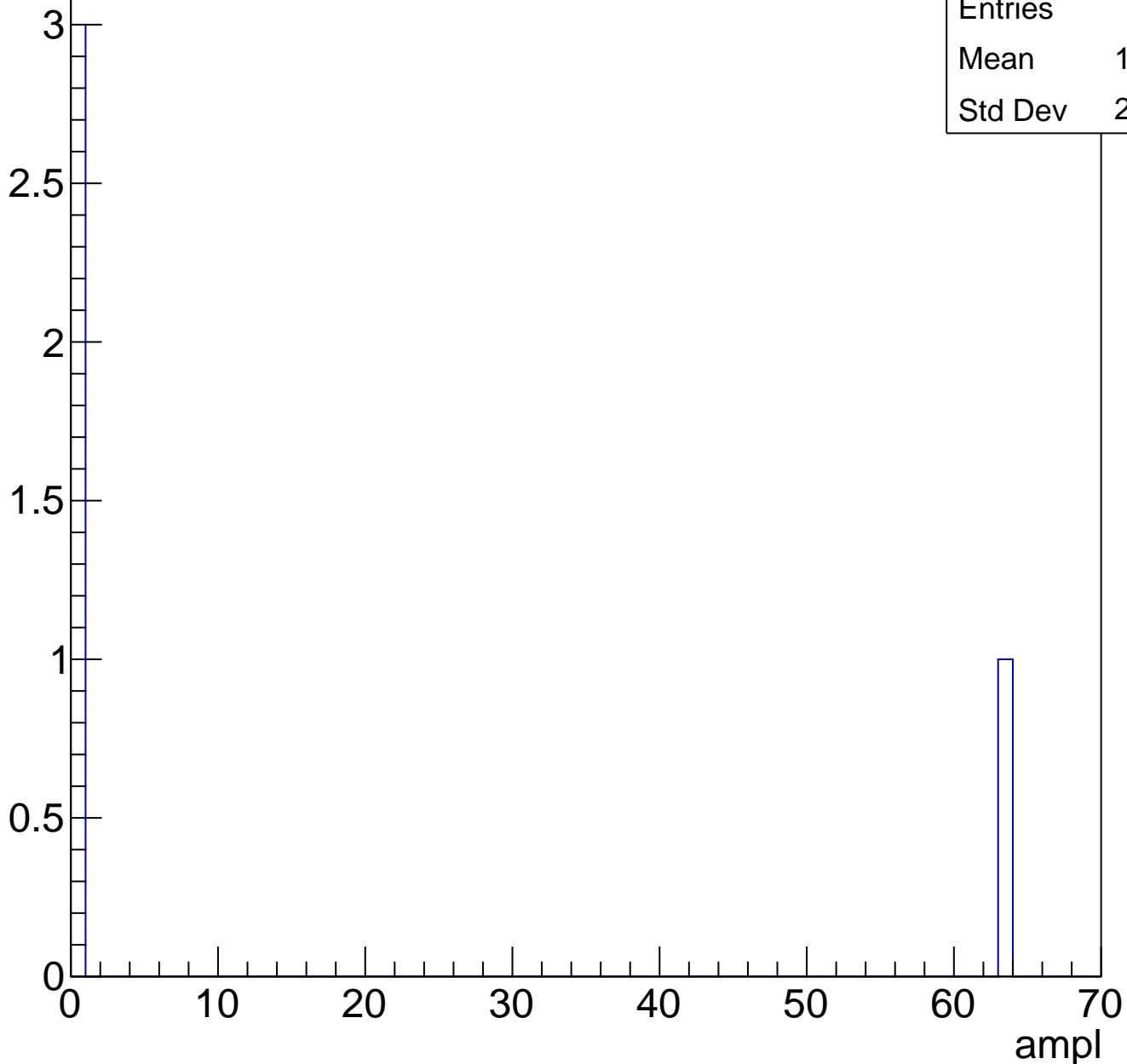
Entry



B1L100S, U15-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch115, adc0

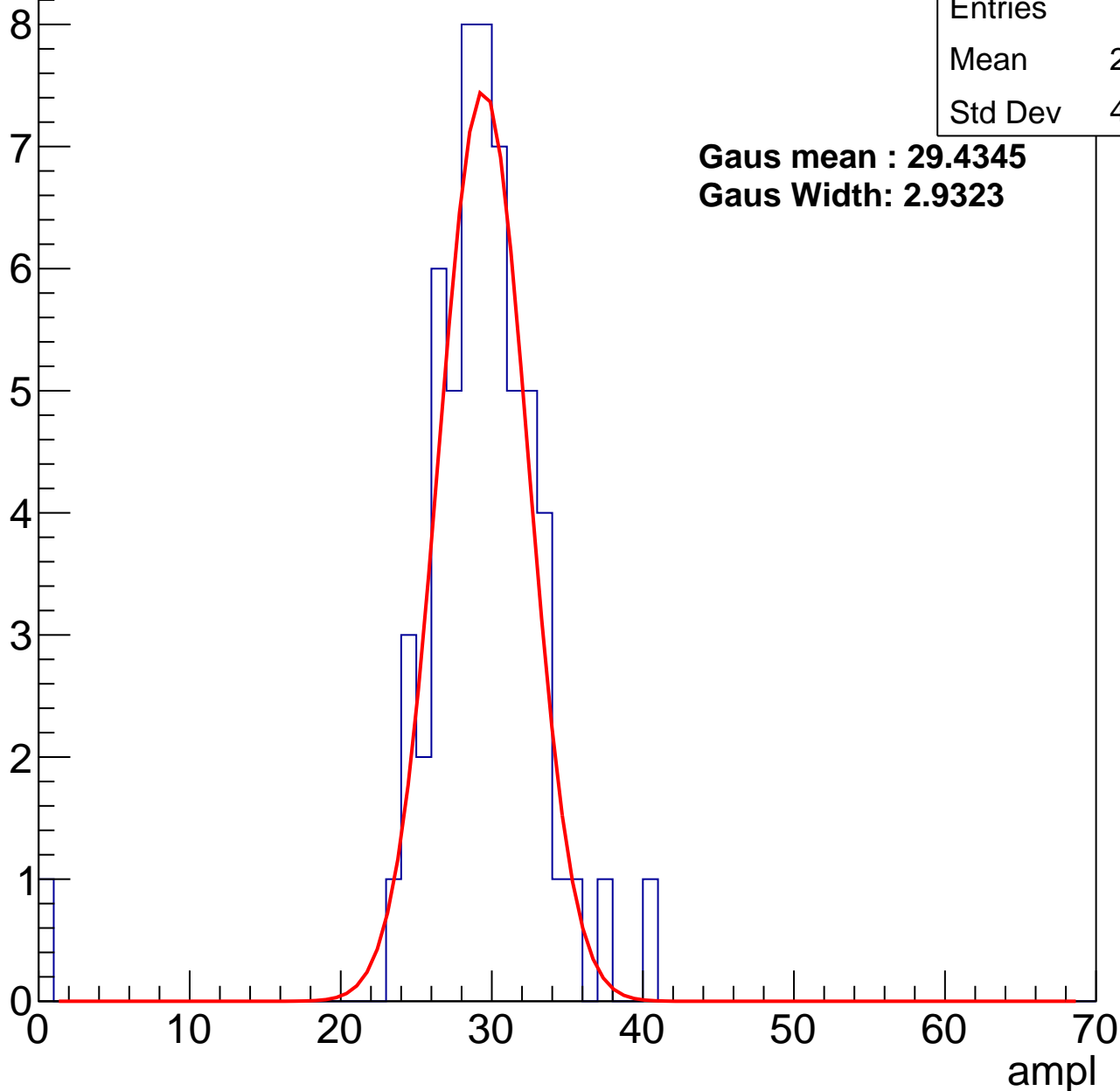
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	28.73
Std Dev	4.947

Gaus mean : 29.4345

Gaus Width: 2.9323



B1L100S, U15-ch115, adc1

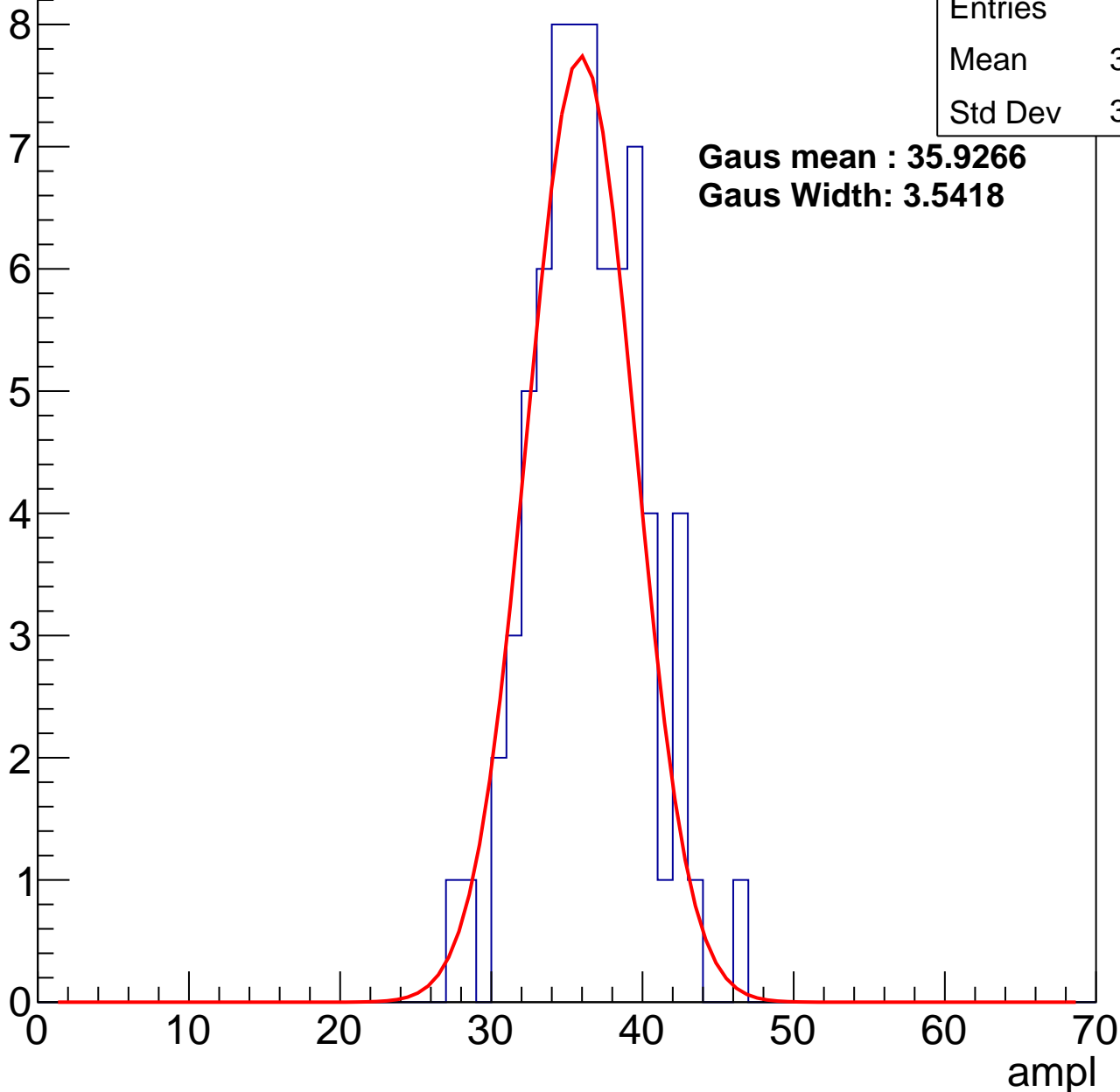
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	35.93
Std Dev	3.615

Gaus mean : 35.9266

Gaus Width: 3.5418



B1L100S, U15-ch115, adc2

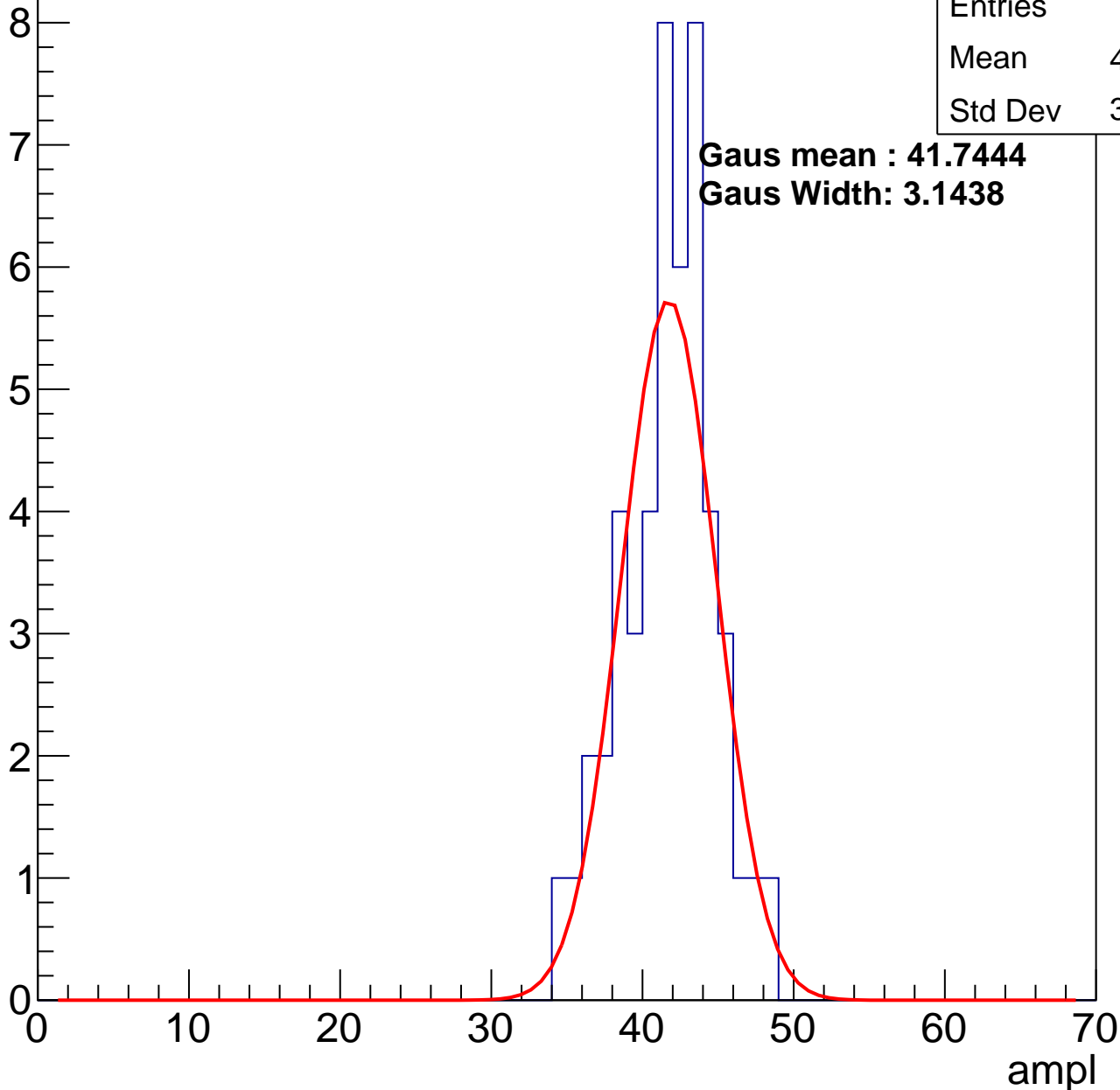
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	41.22
Std Dev	3.026

Gaus mean : 41.7444

Gaus Width: 3.1438

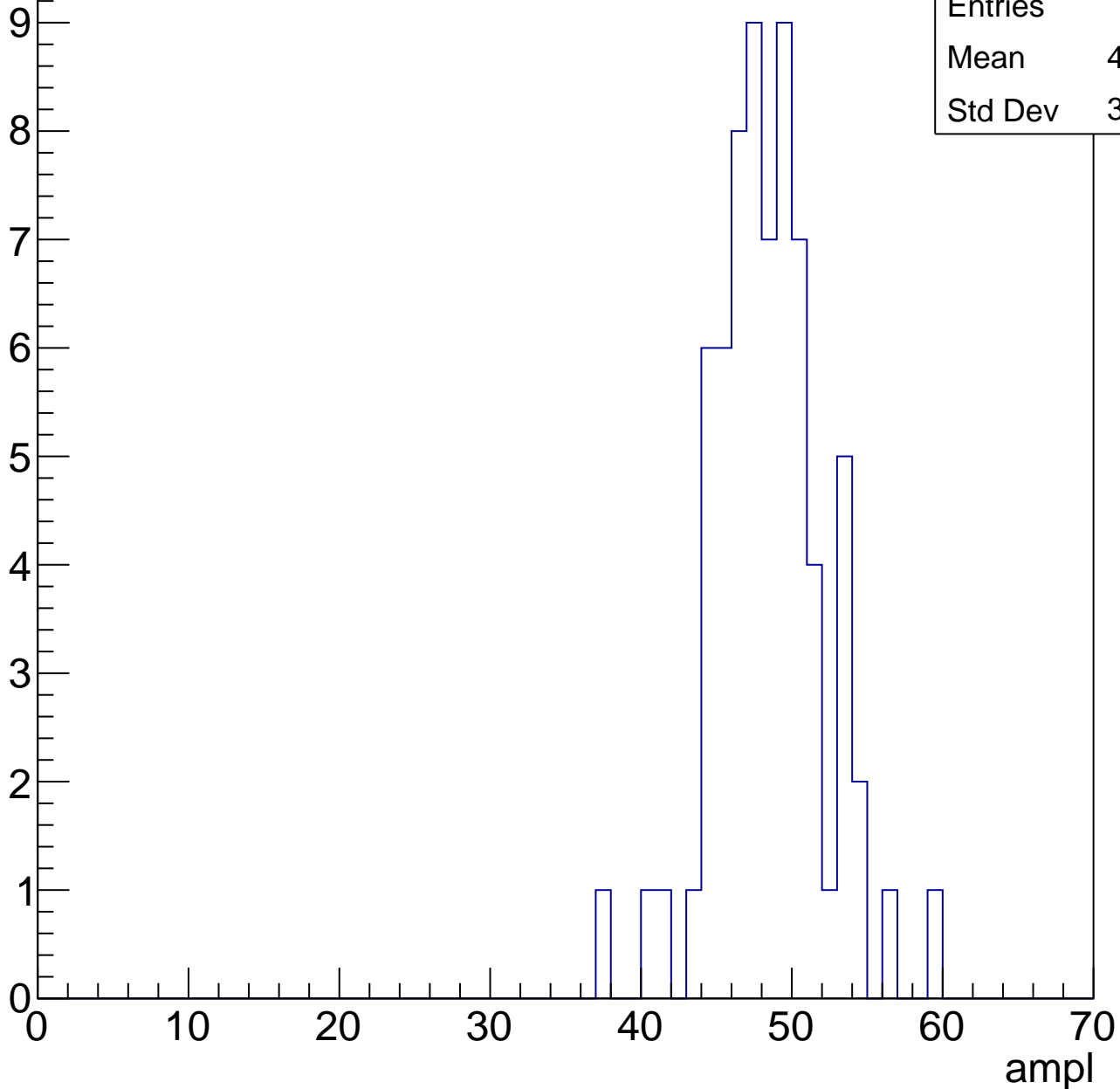


B1L100S, U15-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	47.96
Std Dev	3.627

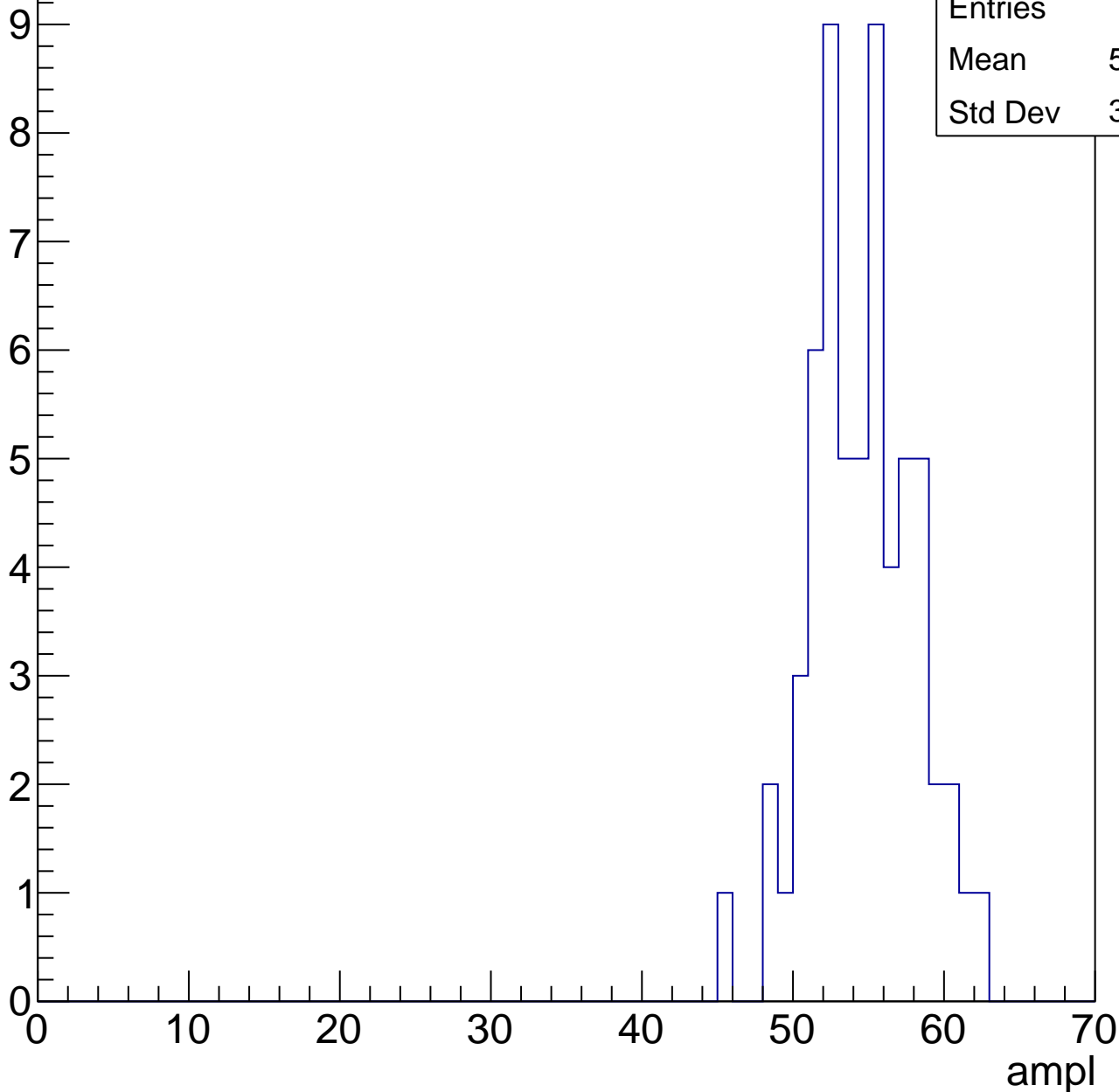


B1L100S, U15-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	54.16
Std Dev	3.398



B1L100S, U15-ch115, adc5

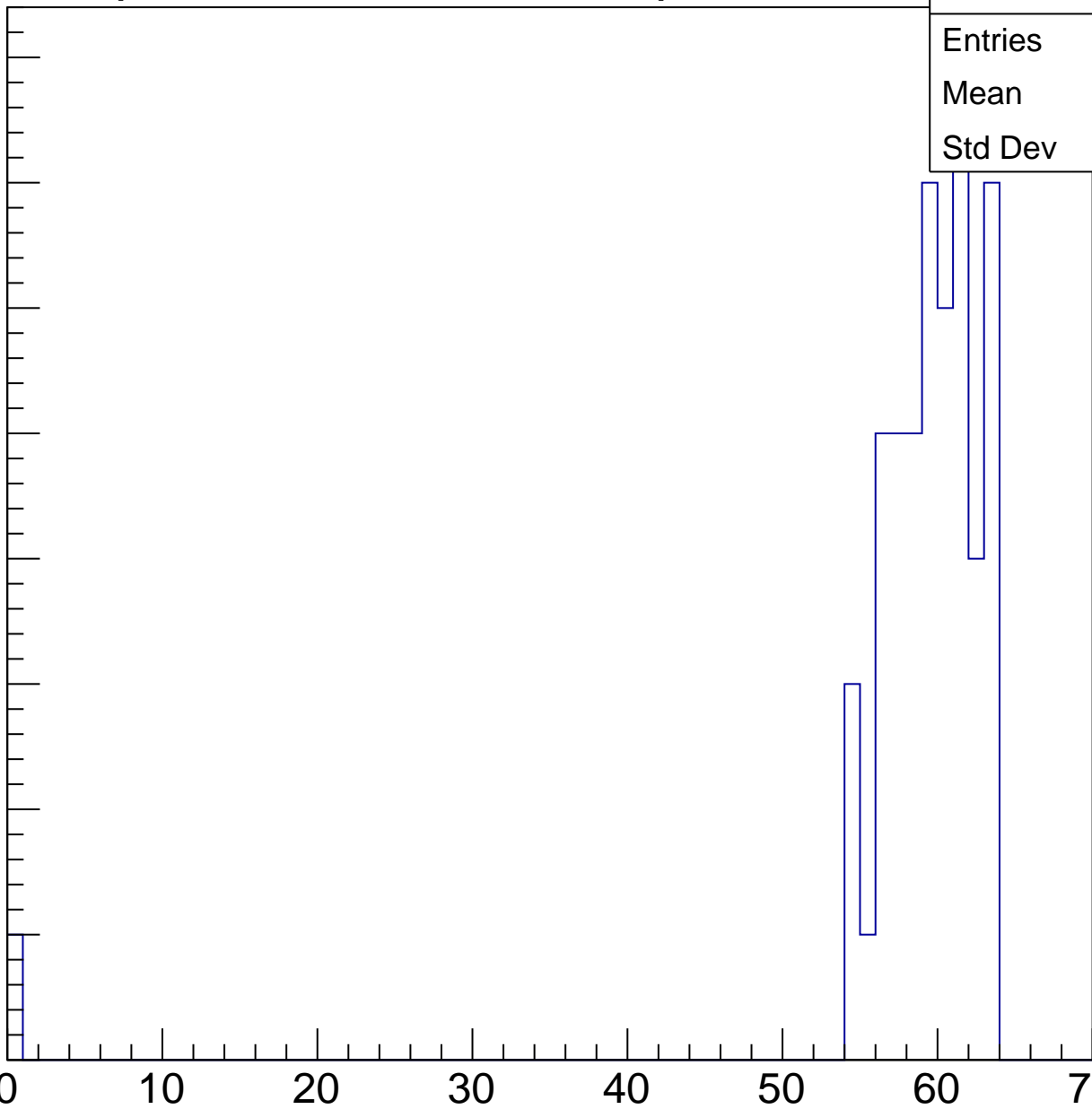
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.12
Std Dev	8.534

ampl

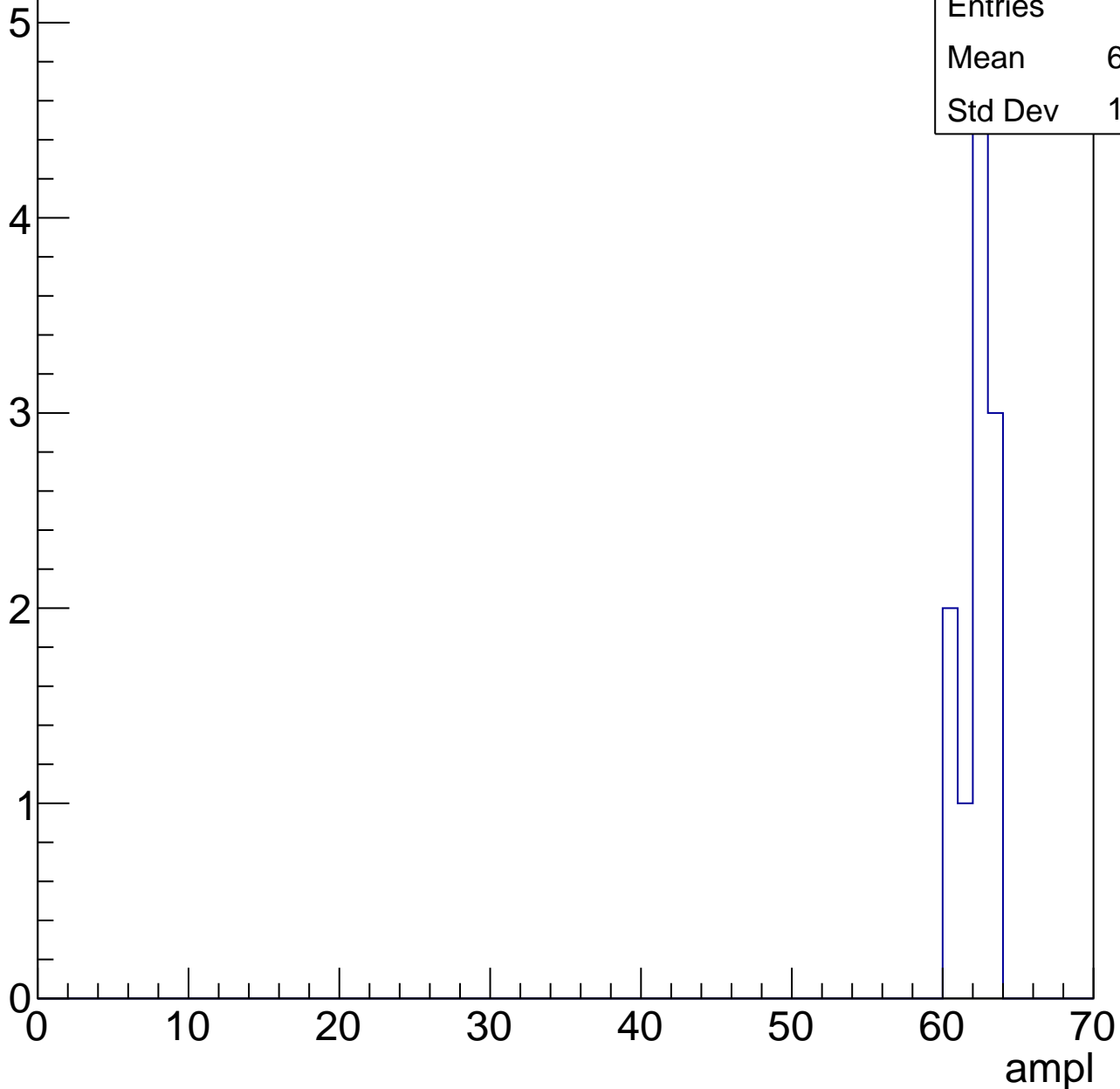


B1L100S, U15-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	61.82
Std Dev	1.029



B1L100S, U15-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch116, adc0

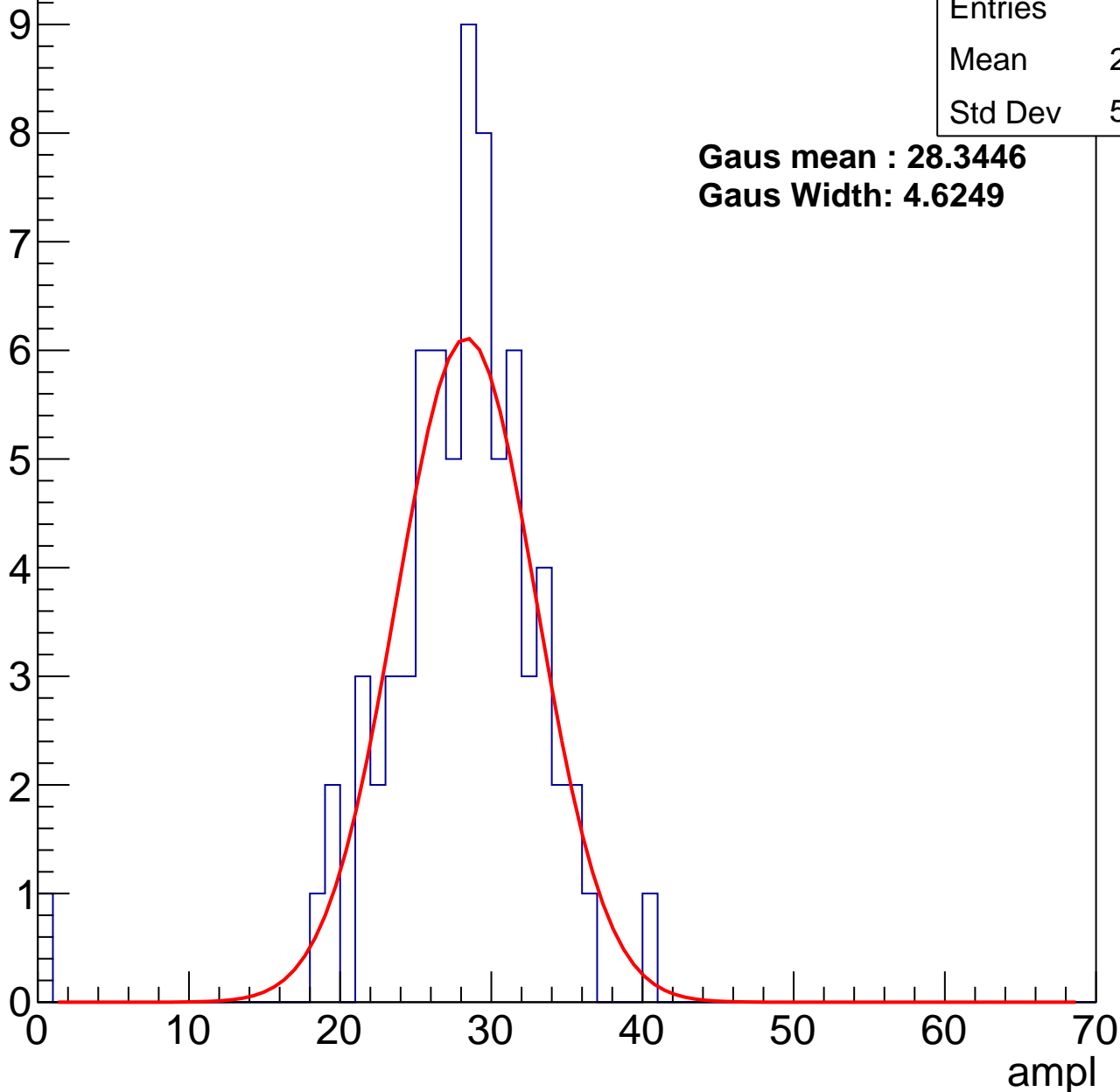
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	27.49
Std Dev	5.312

Gaus mean : 28.3446

Gaus Width: 4.6249



B1L100S, U15-ch116, adc1

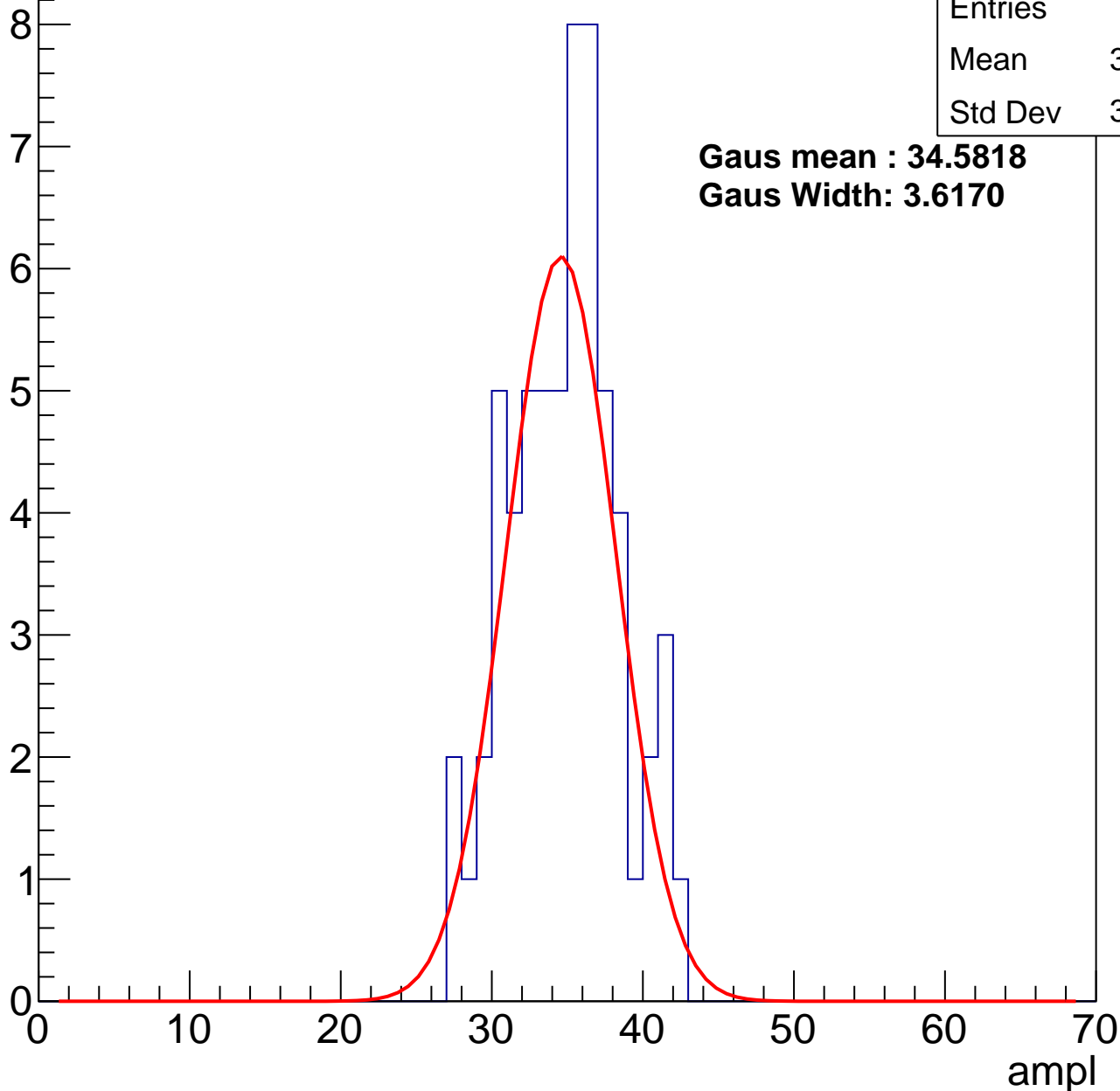
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	34.39
Std Dev	3.559

Gaus mean : 34.5818

Gaus Width: 3.6170



B1L100S, U15-ch116, adc2

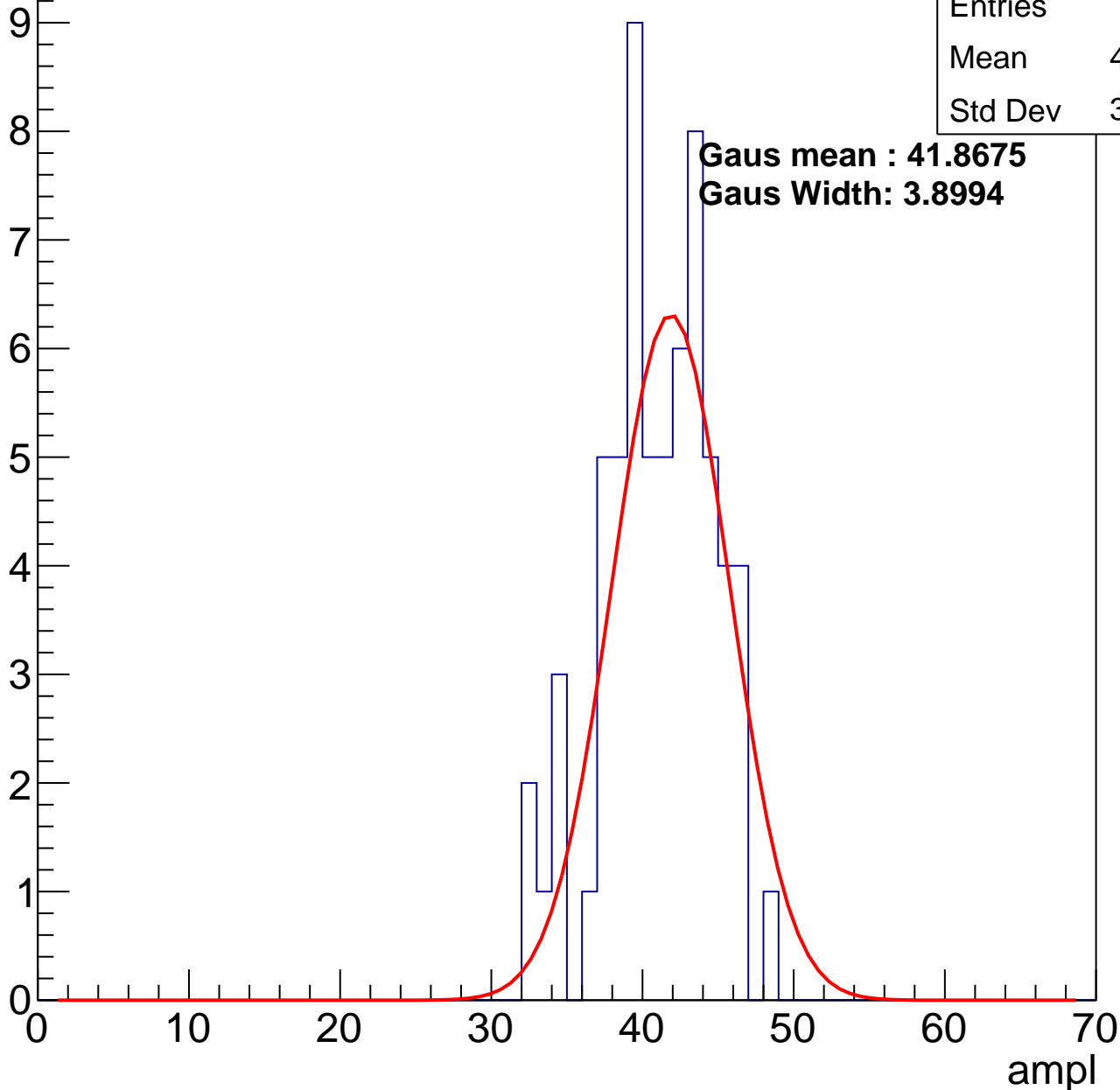
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	40.53
Std Dev	3.636

Gaus mean : 41.8675

Gaus Width: 3.8994

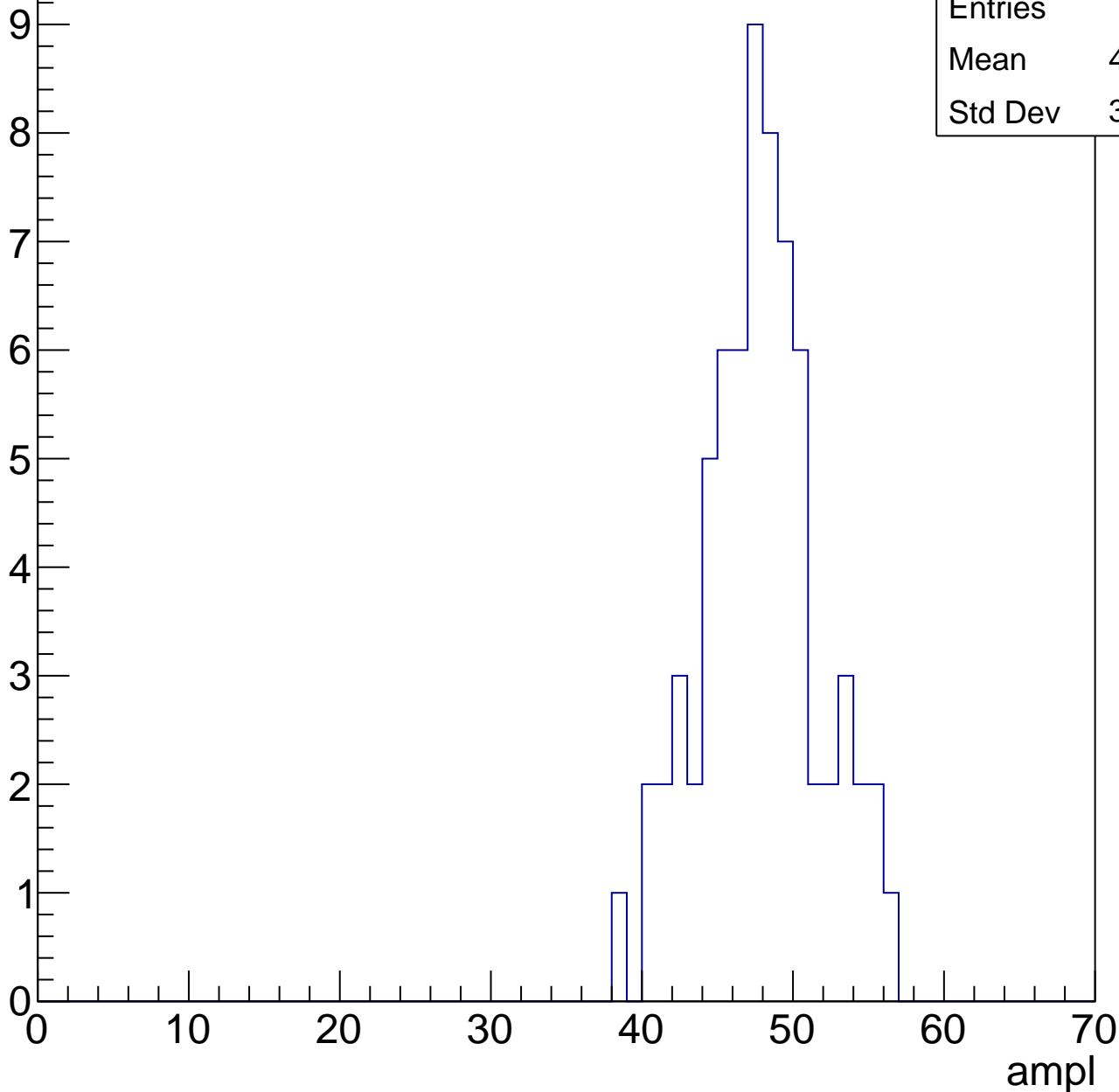


B1L100S, U15-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	47.35
Std Dev	3.829

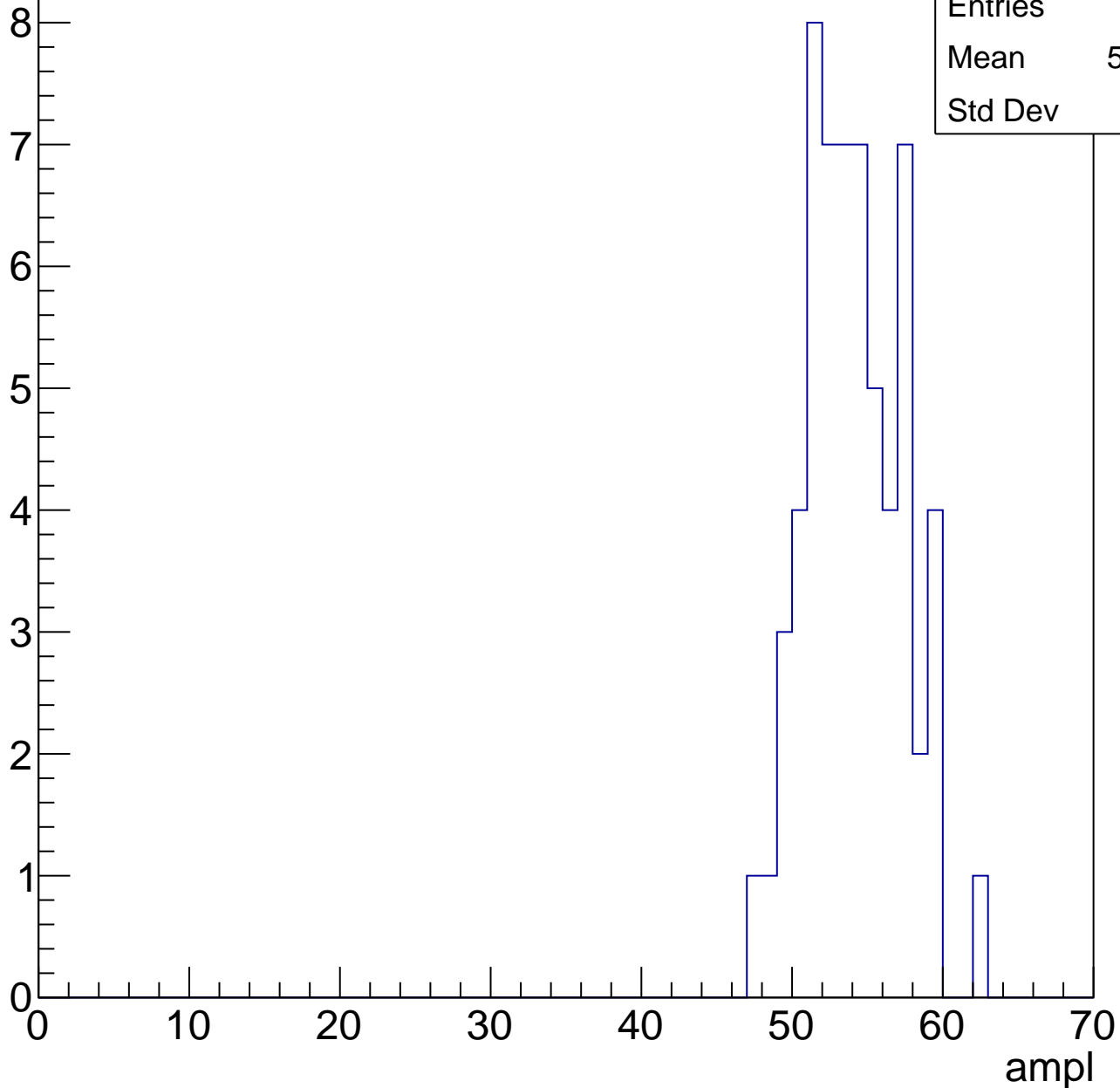


B1L100S, U15-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	53.69
Std Dev	3.15

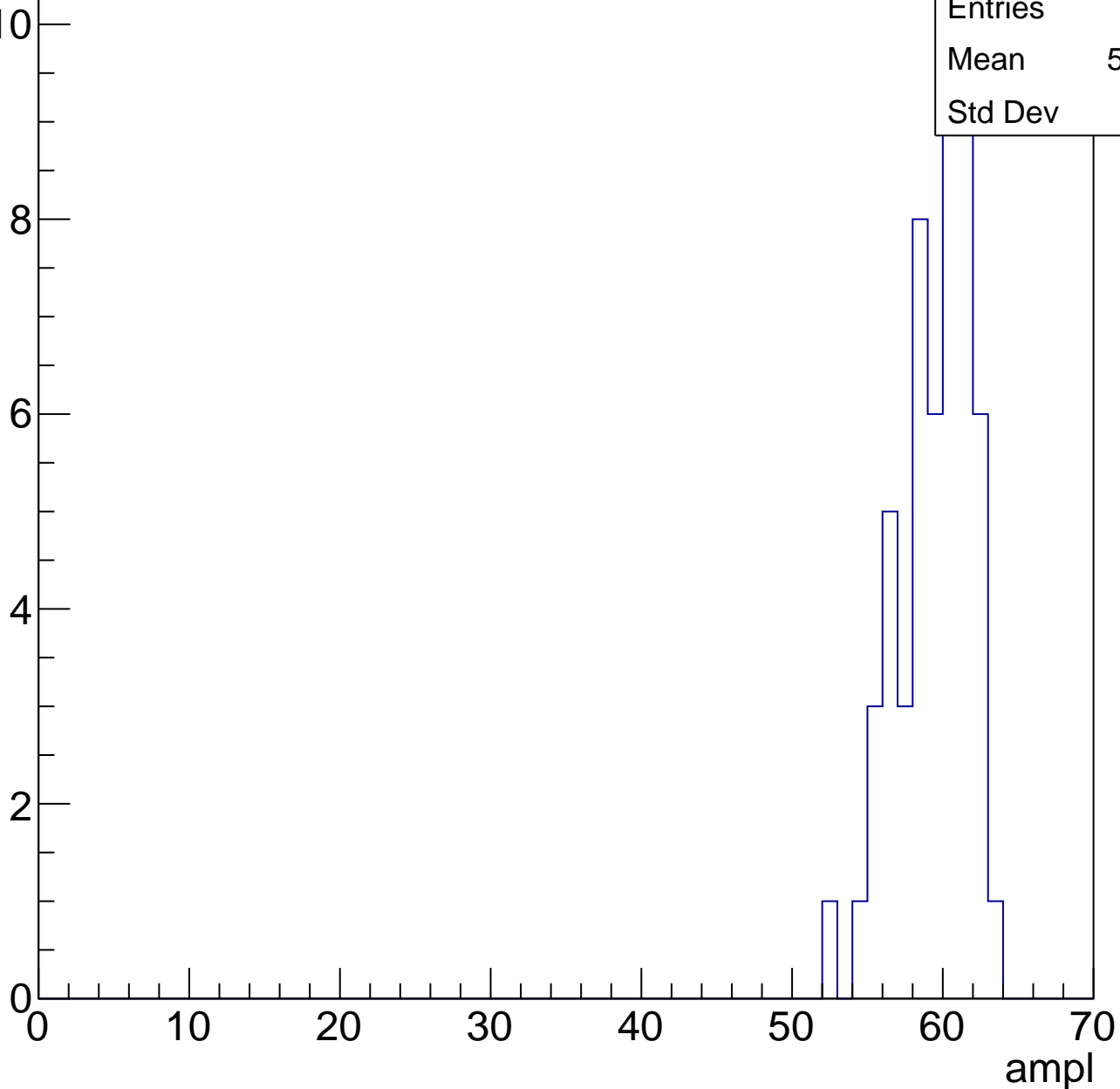


B1L100S, U15-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	58.94
Std Dev	2.39

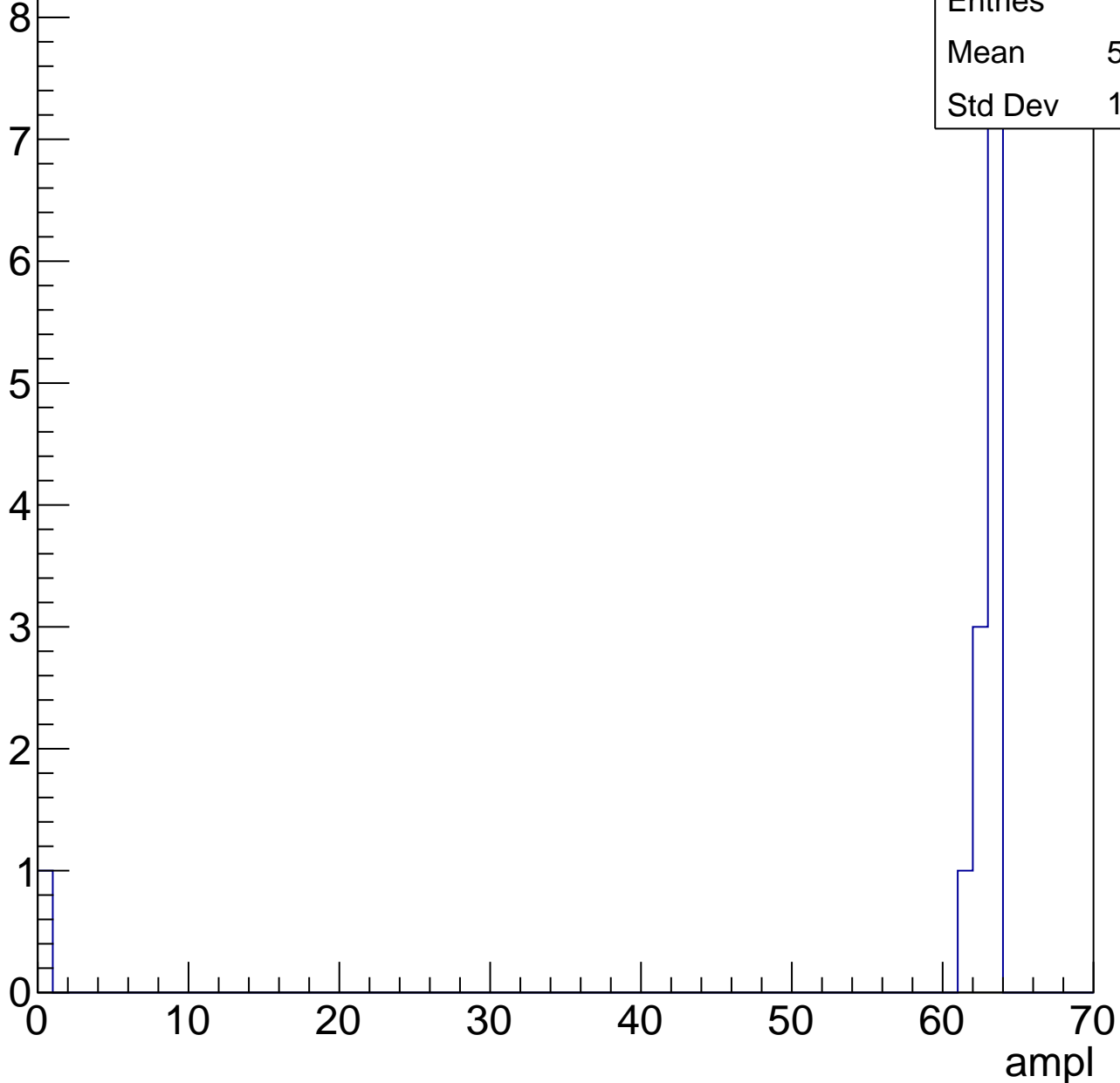


B1L100S, U15-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	13
Mean	57.77
Std Dev	16.69



B1L100S, U15-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U15-ch117, adc0

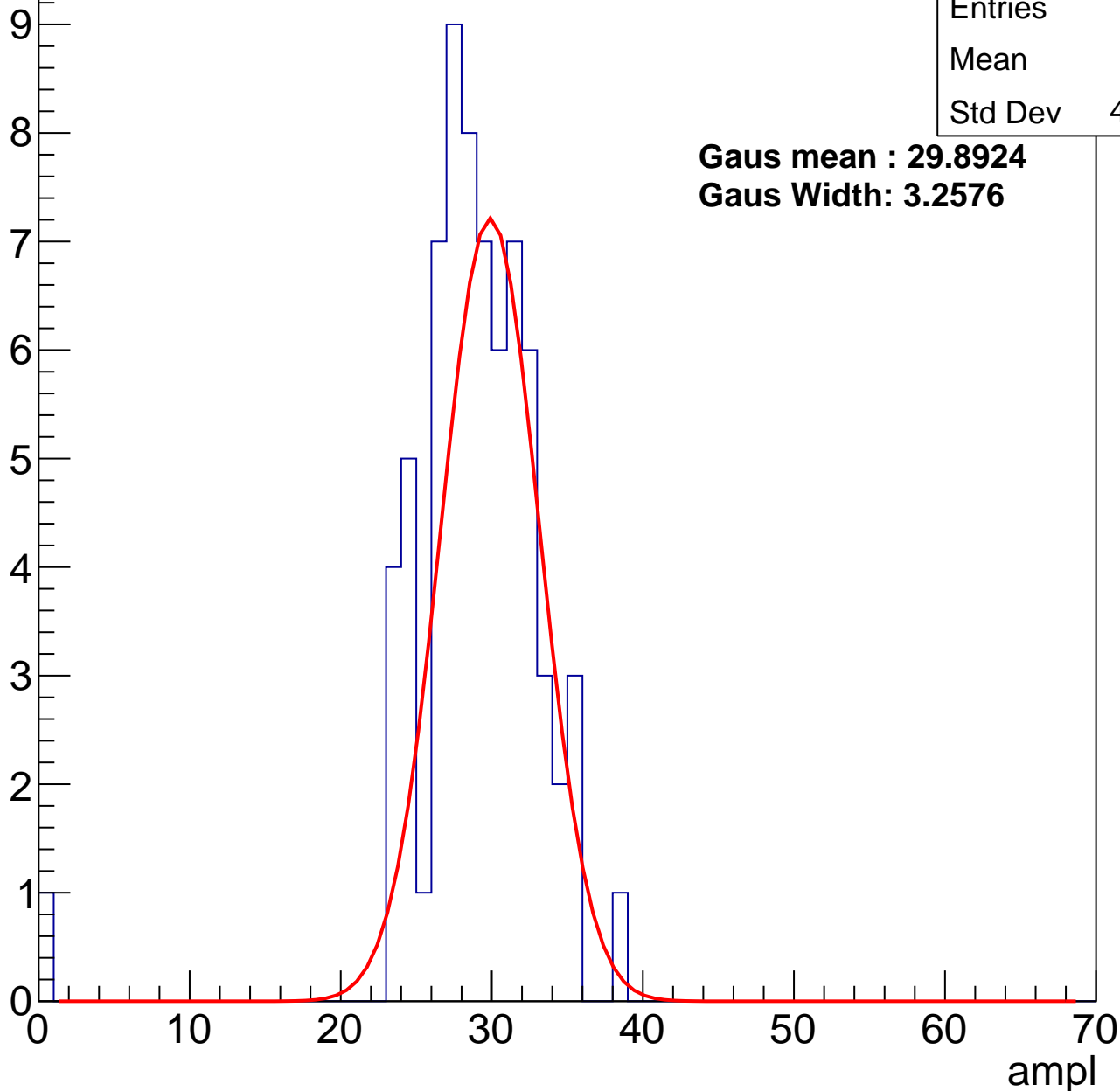
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	28.4
Std Dev	4.755

Gaus mean : 29.8924

Gaus Width: 3.2576



B1L100S, U15-ch117, adc1

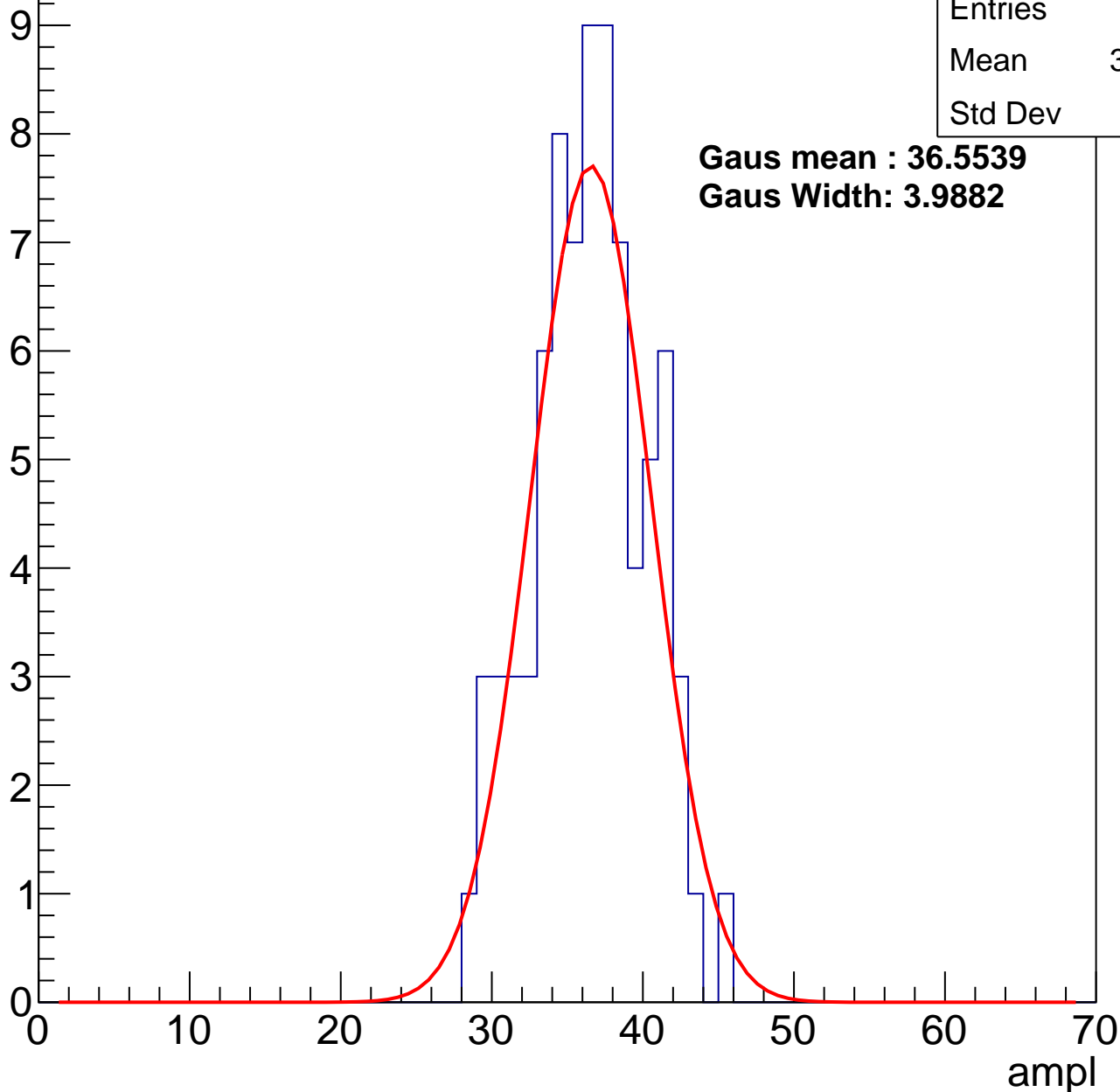
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	36.05
Std Dev	3.69

Gaus mean : 36.5539

Gaus Width: 3.9882



B1L100S, U15-ch117, adc2

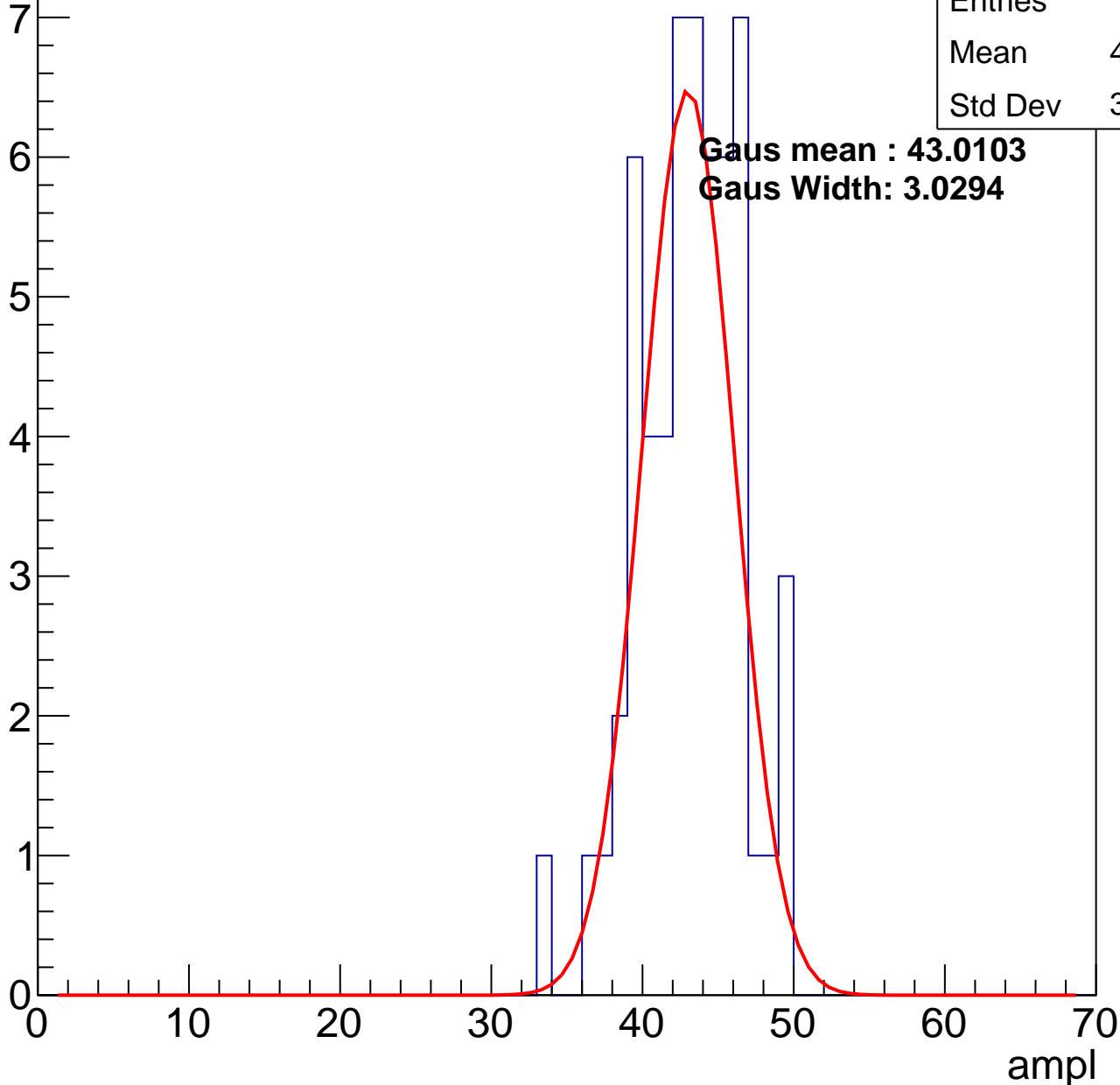
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.68
Std Dev	3.315

Gaus mean : 43.0103

Gaus Width: 3.0294

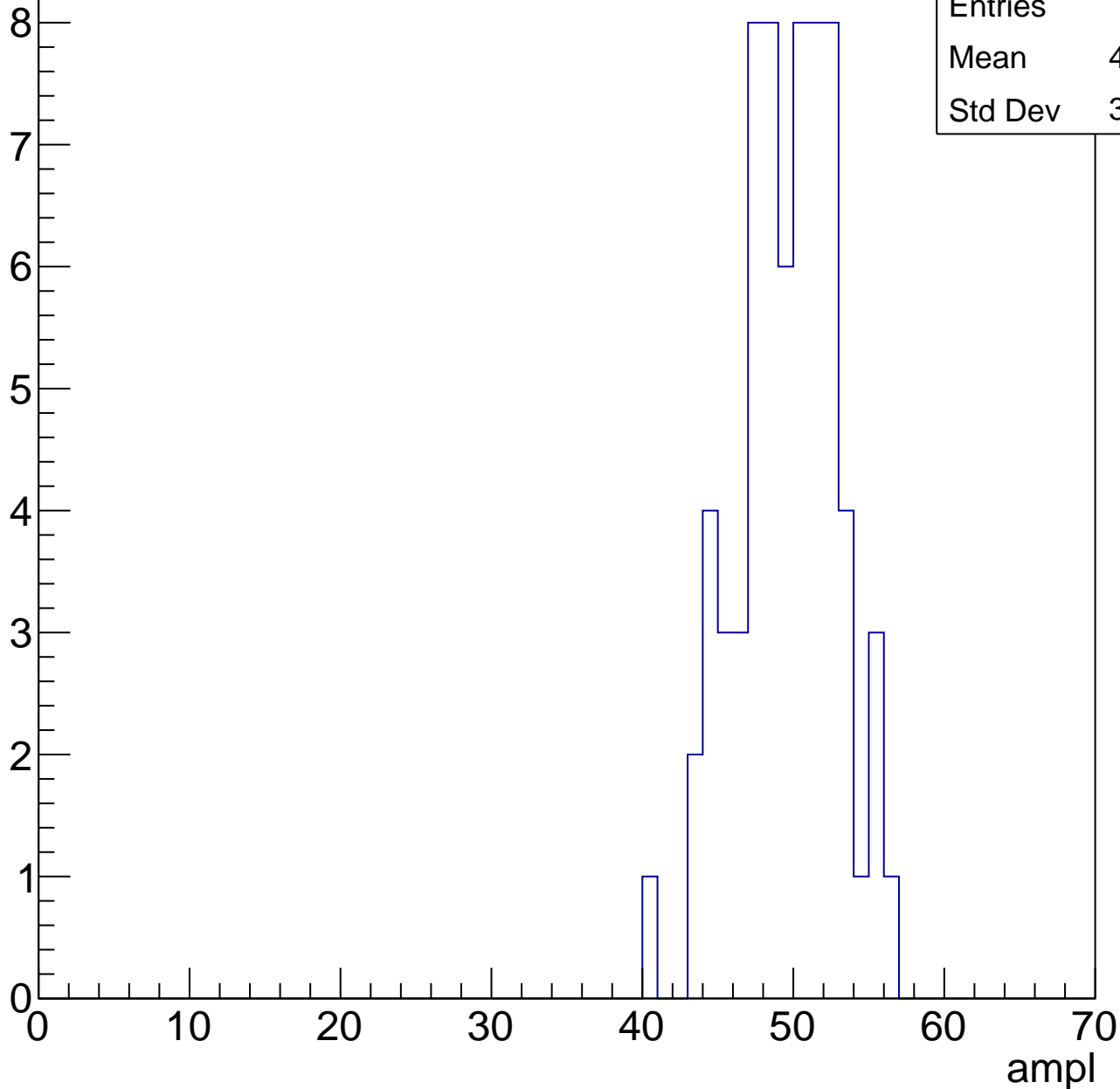


B1L100S, U15-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	49.12
Std Dev	3.265

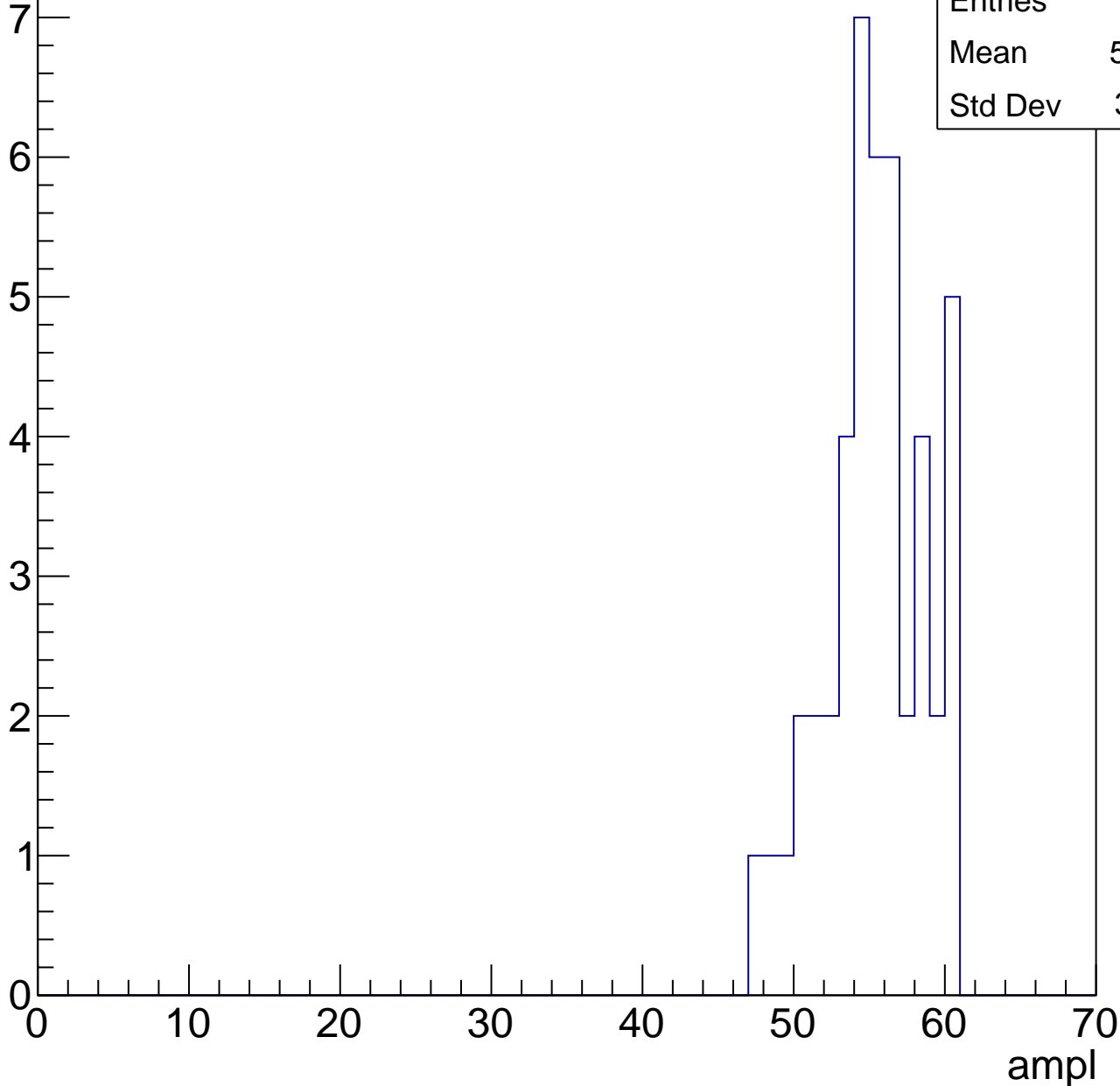


B1L100S, U15-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	54.89
Std Dev	3.261

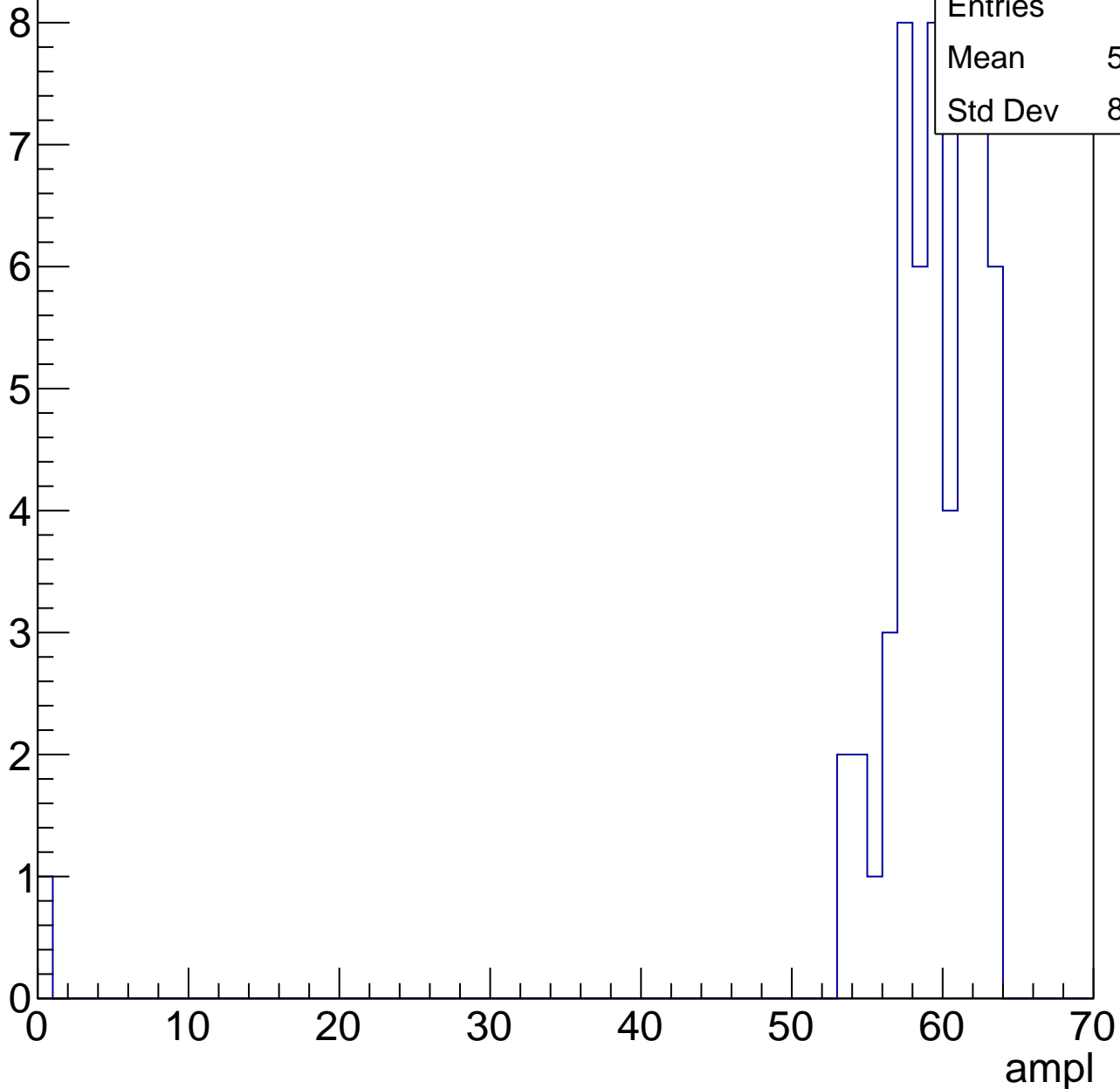


B1L100S, U15-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	58.16
Std Dev	8.216



B1L100S, U15-ch117, adc6

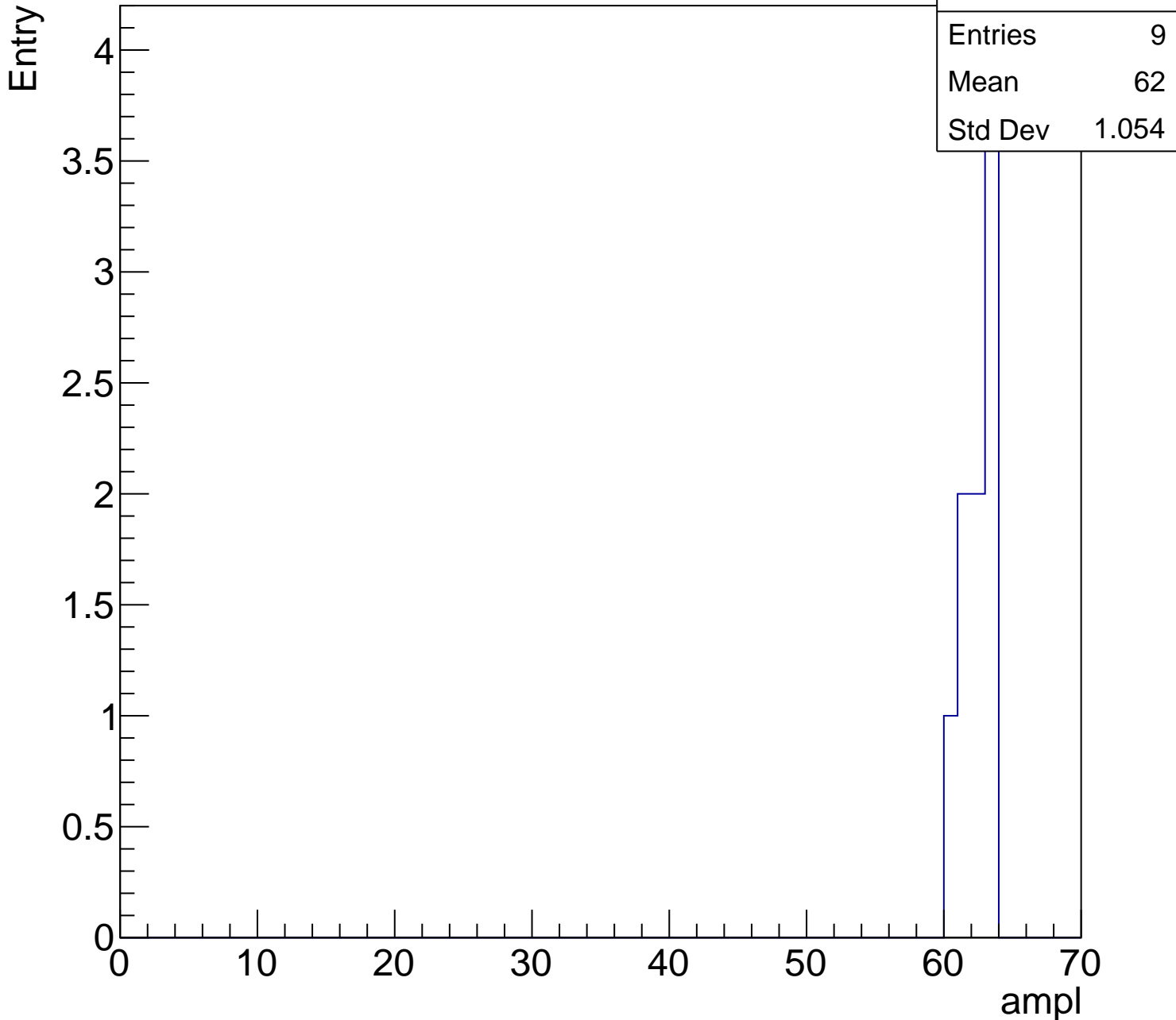
calib_packv5_042523_0143.root, FC#4, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	62
Std Dev	1.054

ampl



B1L100S, U15-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch118, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	29.94
Std Dev	5.09

Gaus mean : 30.0040

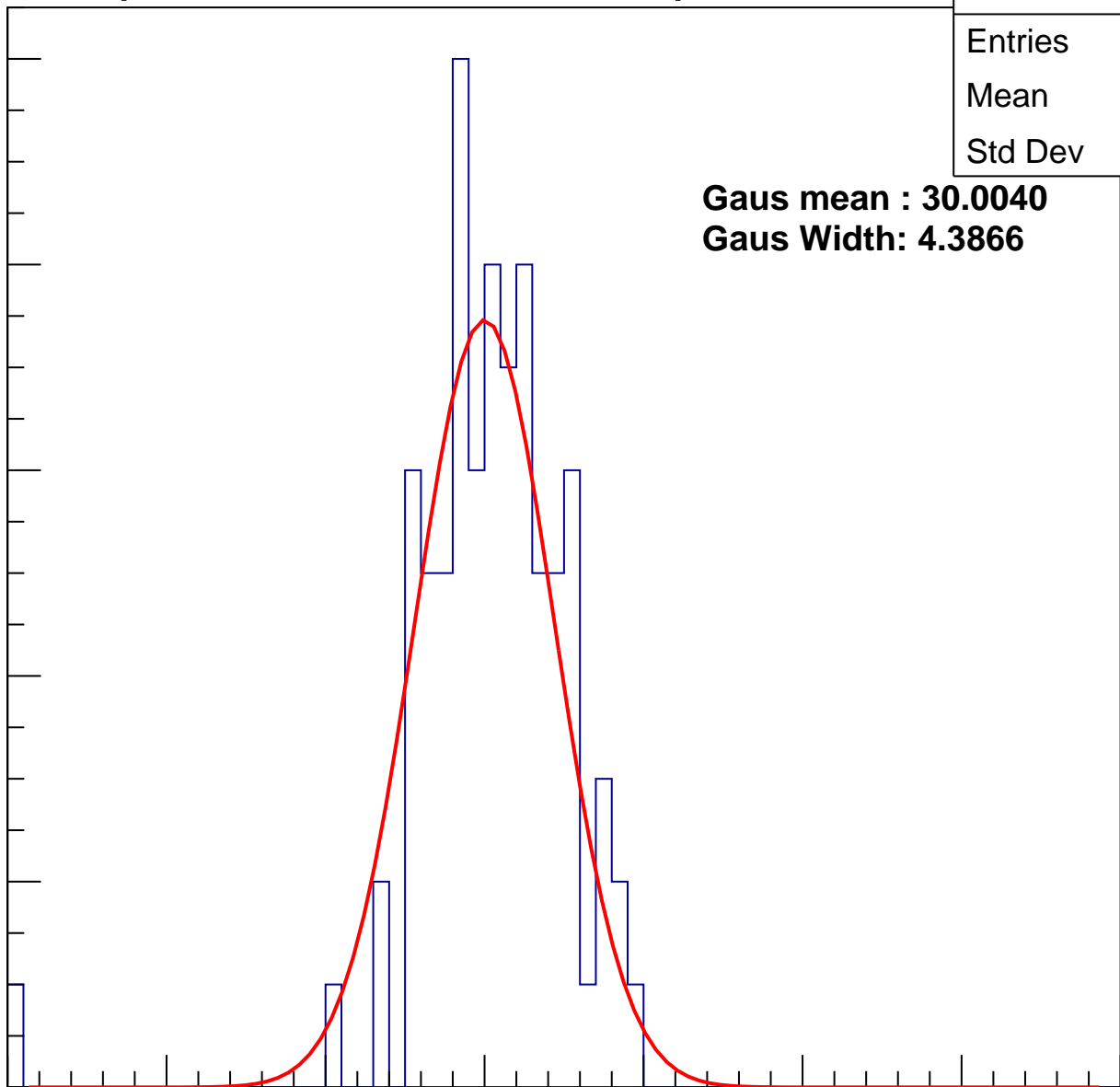
Gaus Width: 4.3866

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch118, adc1

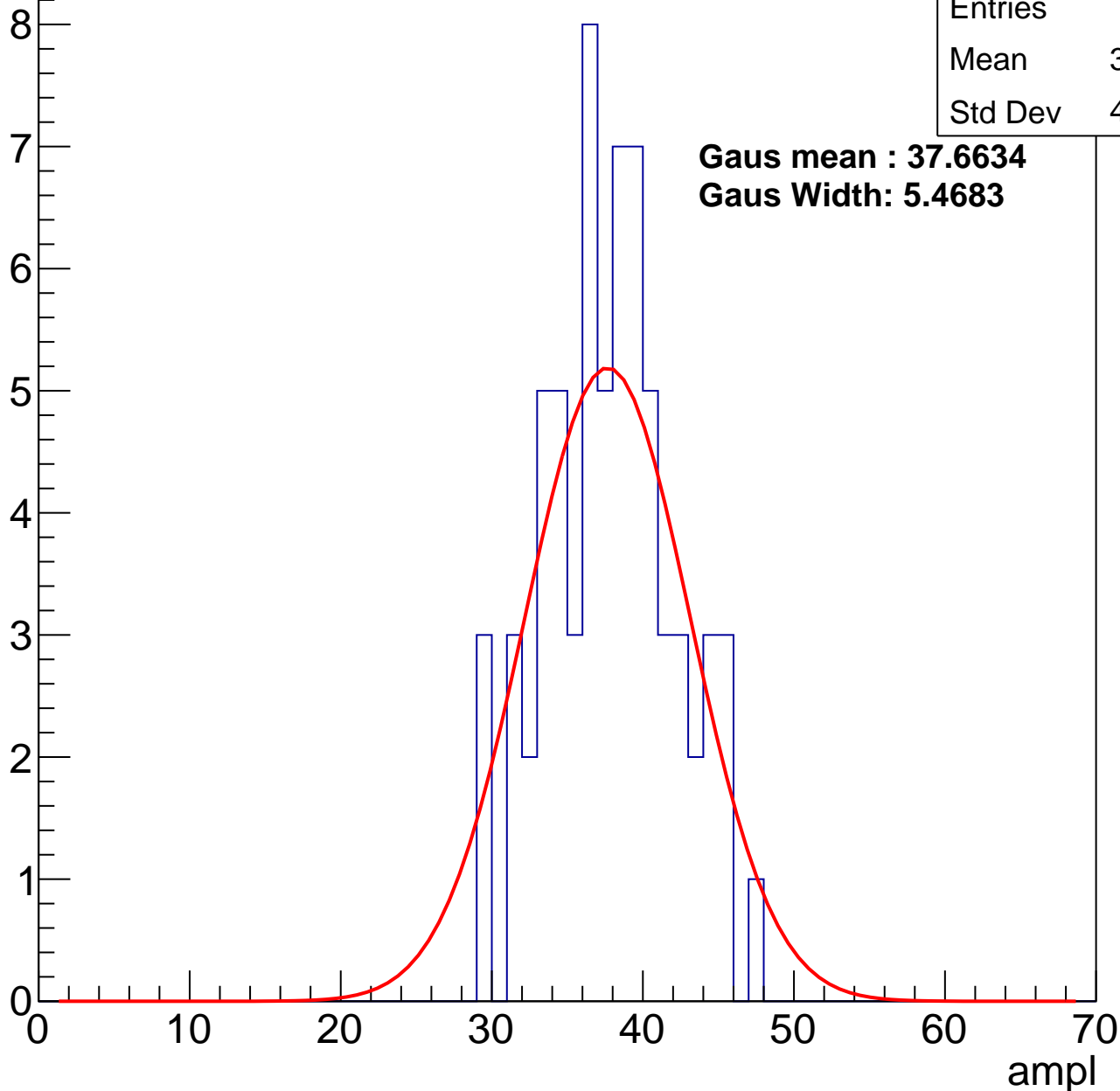
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.43
Std Dev	4.195

Gaus mean : 37.6634

Gaus Width: 5.4683



B1L100S, U15-ch118, adc2

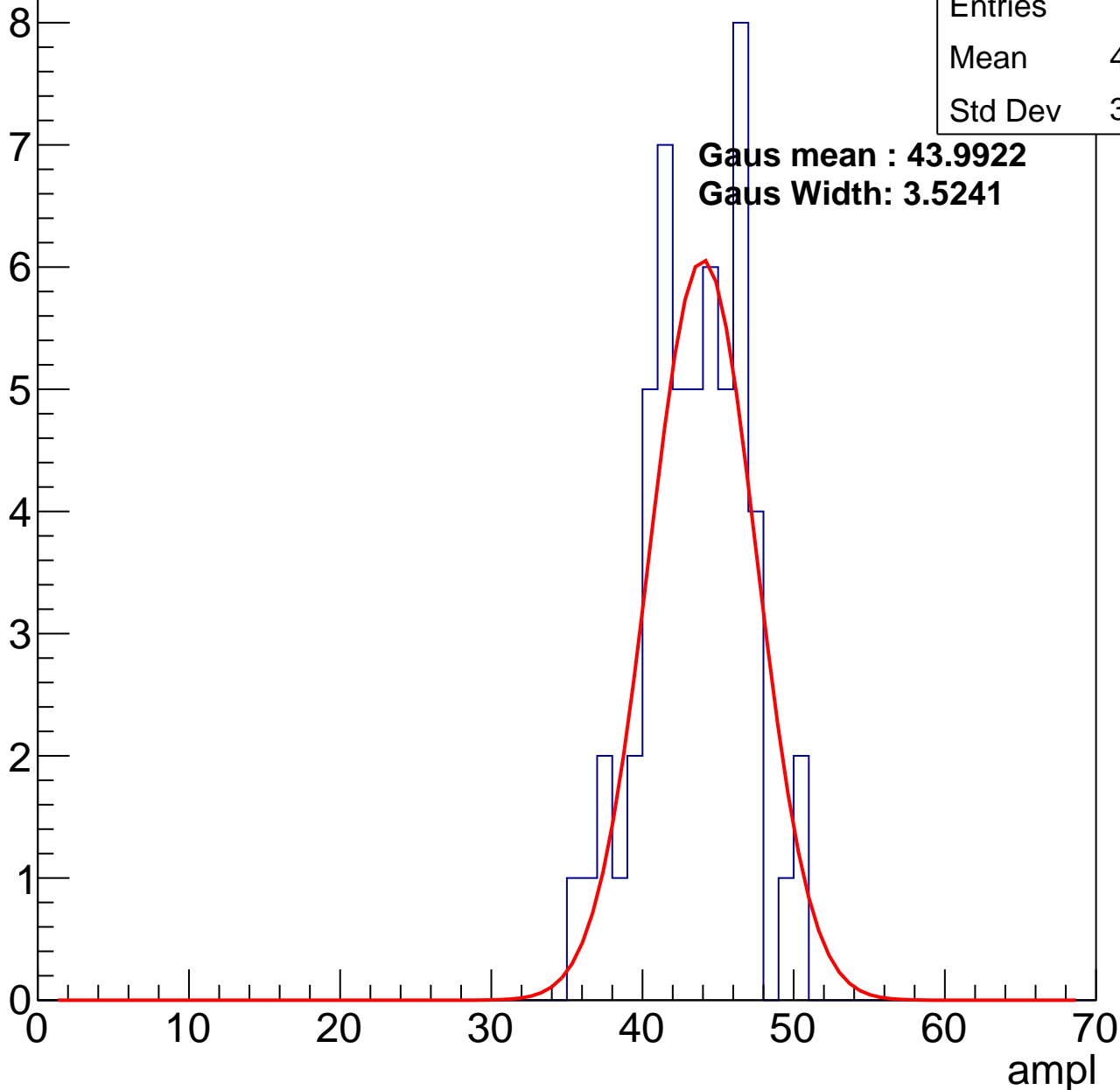
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	43.04
Std Dev	3.346

Gaus mean : 43.9922

Gaus Width: 3.5241



B1L100S, U15-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

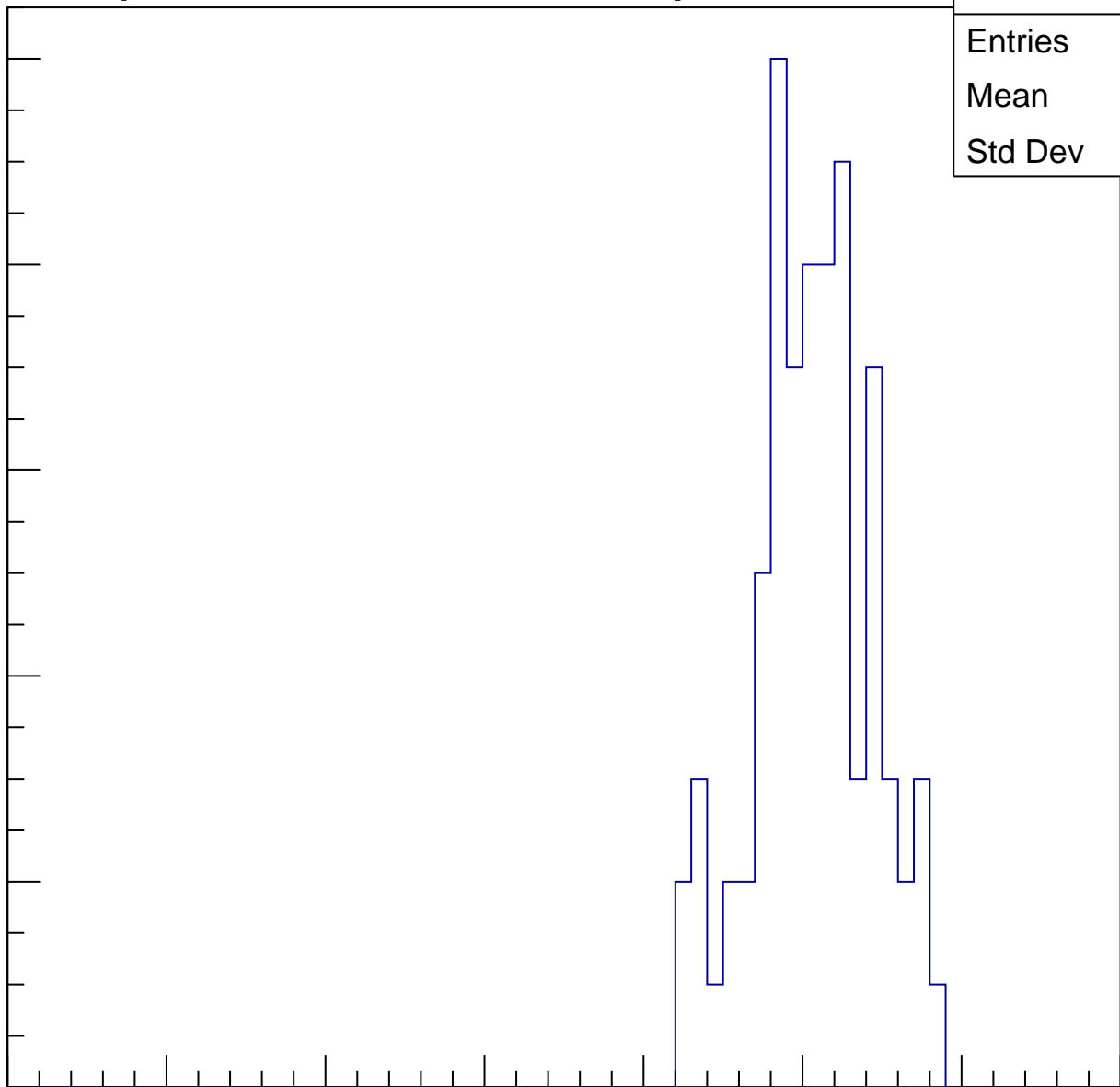
Entries	76
Mean	50.21
Std Dev	3.668

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

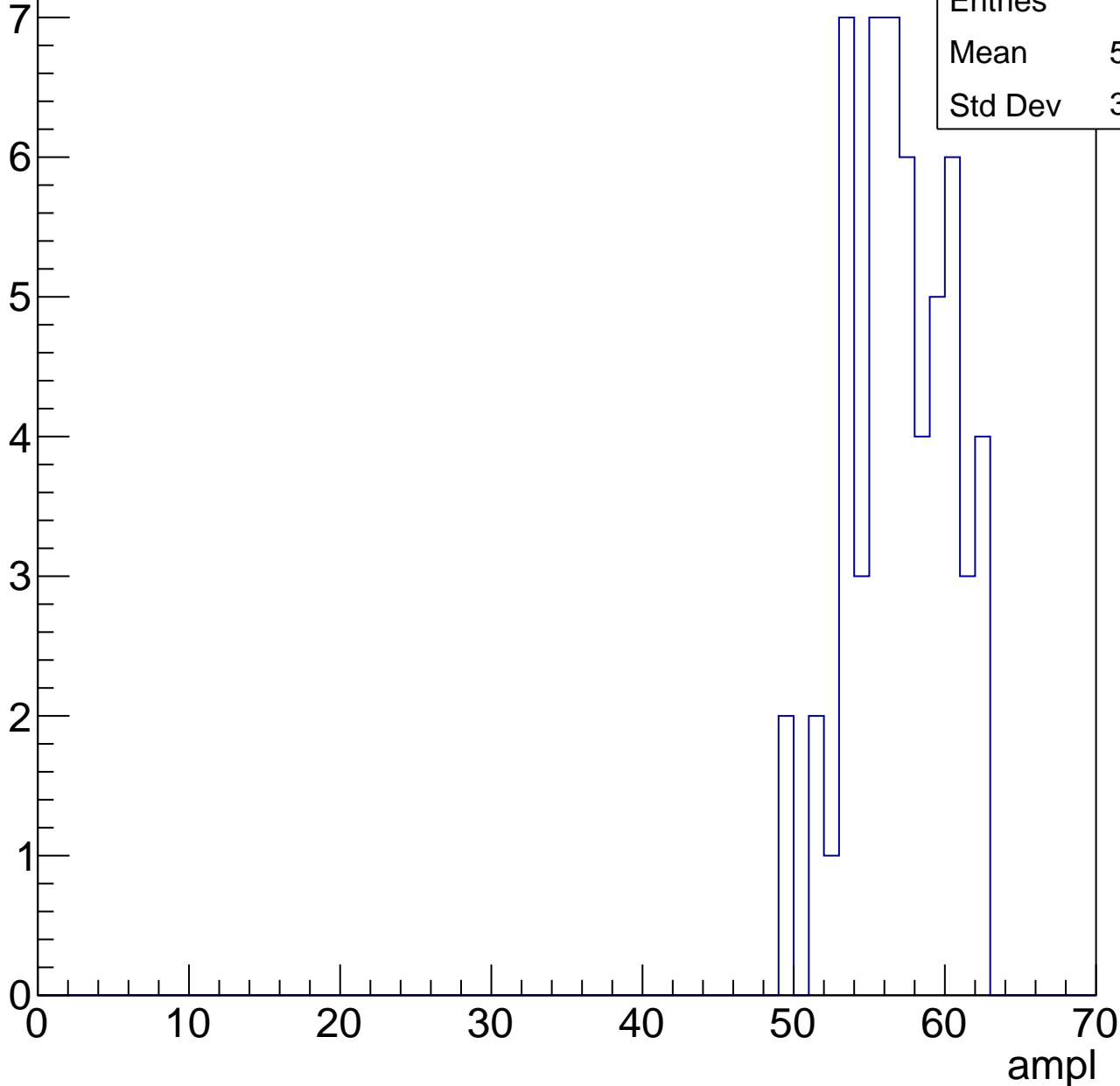


B1L100S, U15-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	56.53
Std Dev	3.277

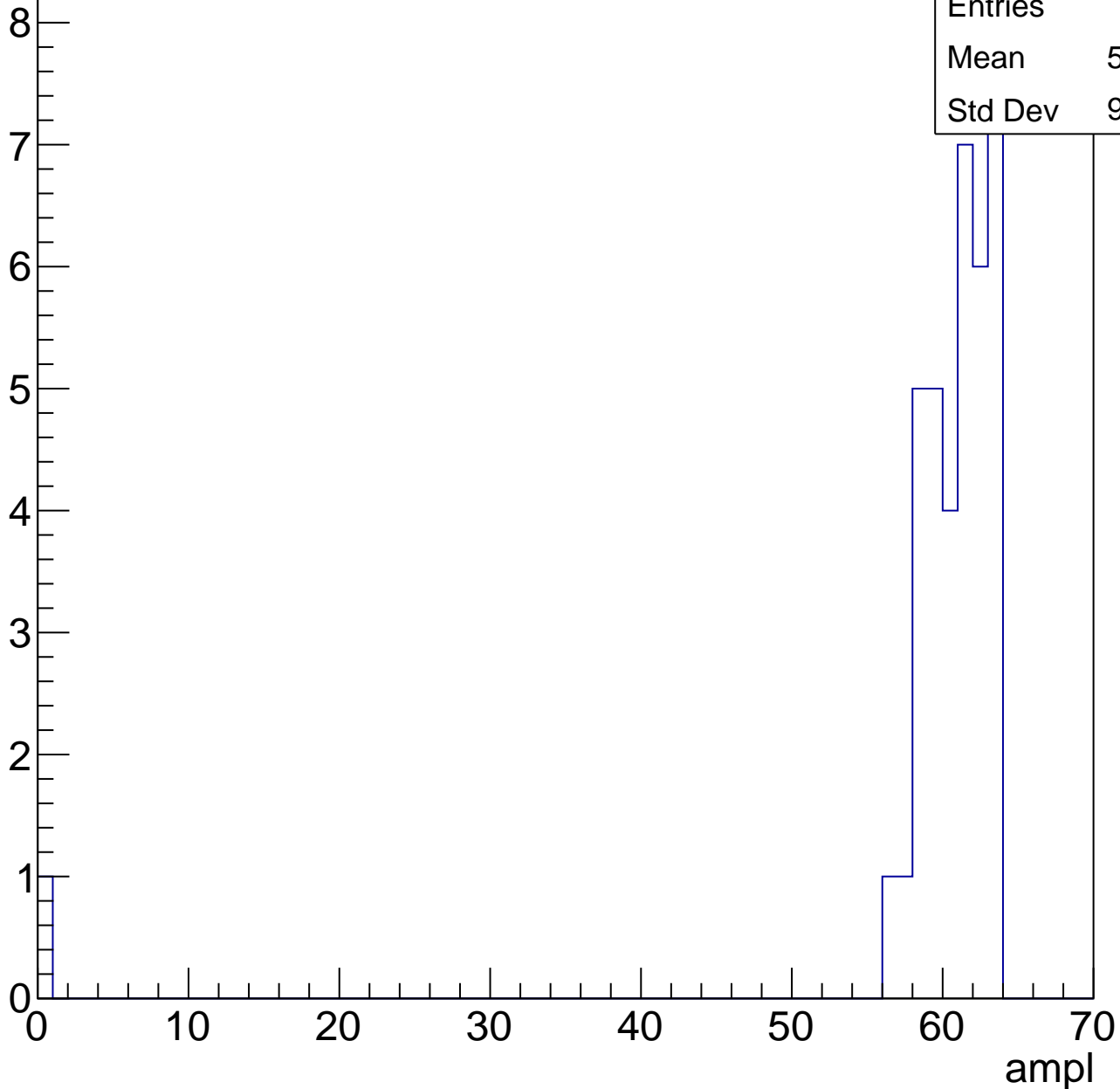


B1L100S, U15-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.97
Std Dev	9.885



B1L100S, U15-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L100S, U15-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch119, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	29.53
Std Dev	5.102

Gaus mean : 30.3572

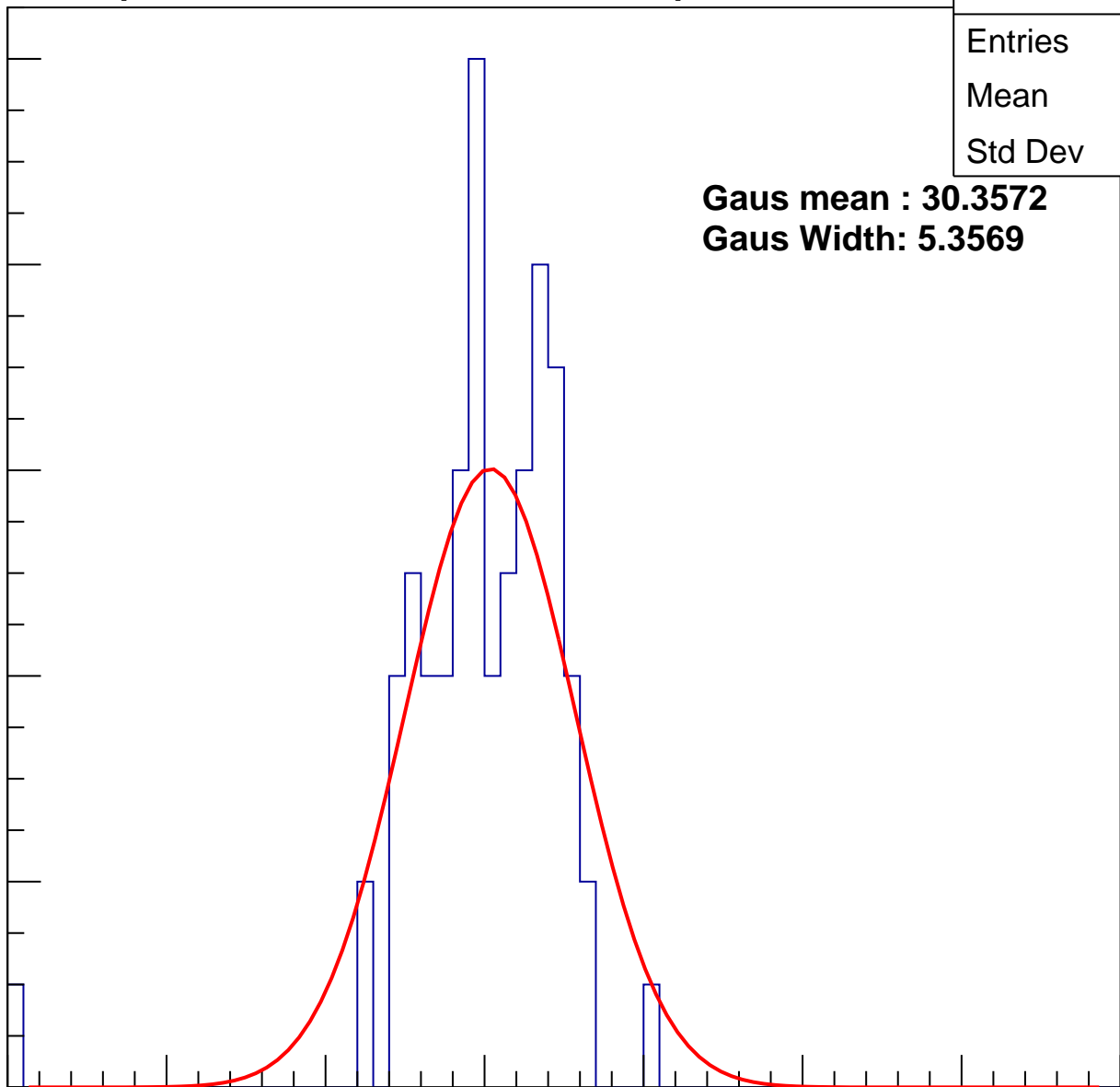
Gaus Width: 5.3569

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch119, adc1

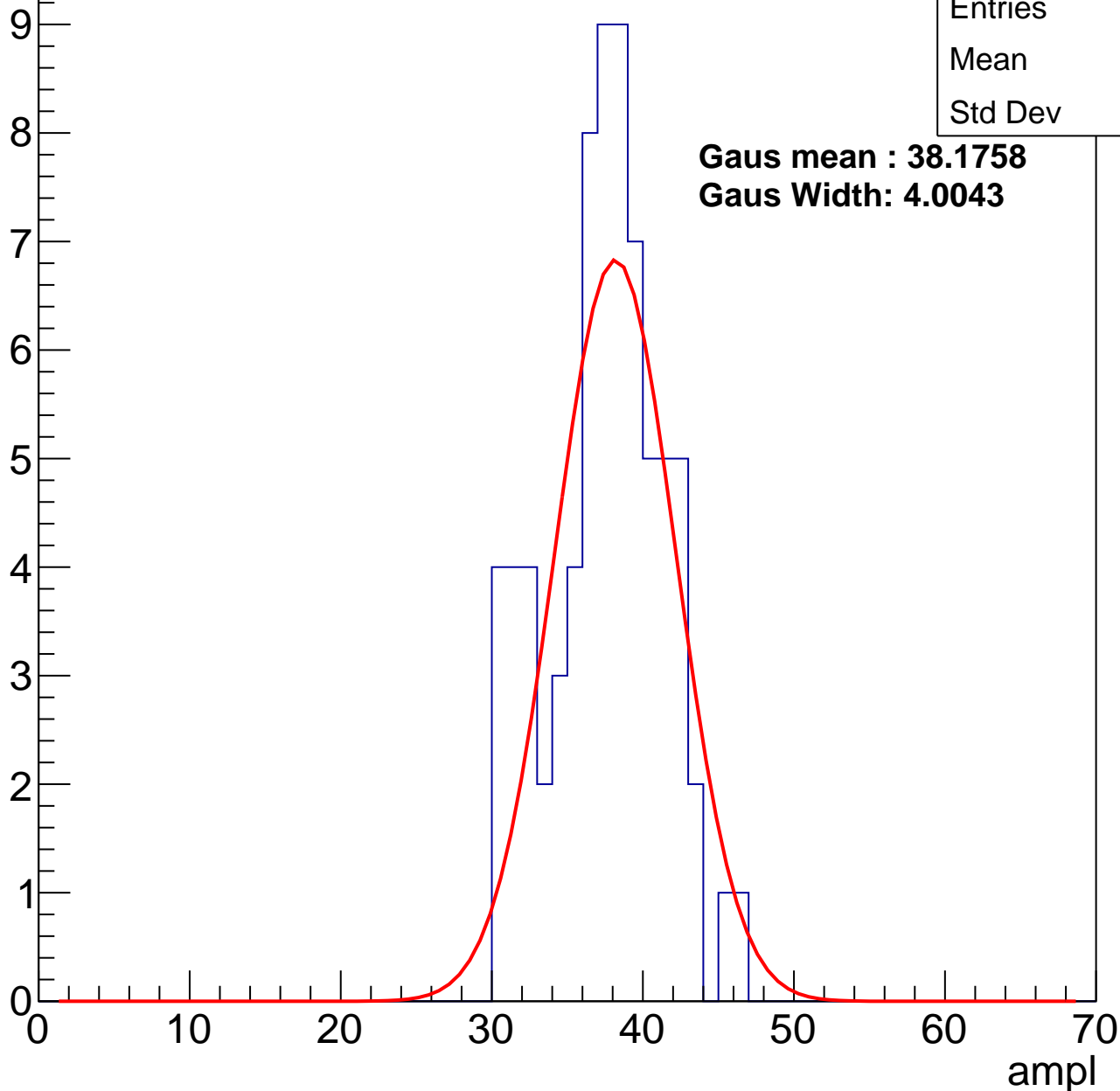
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.1
Std Dev	3.75

Gaus mean : 38.1758

Gaus Width: 4.0043



B1L100S, U15-ch119, adc2

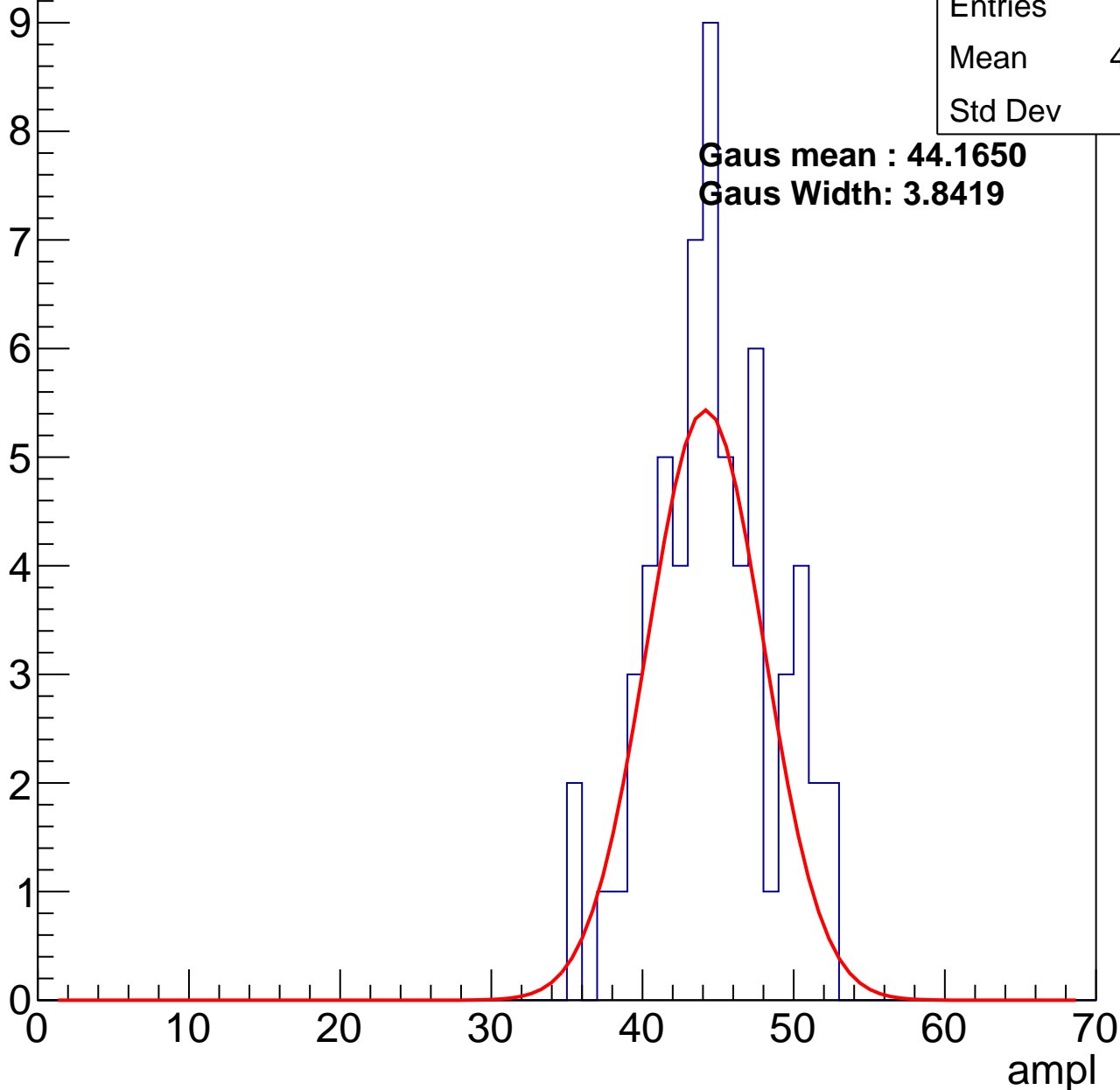
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.19
Std Dev	3.96

Gaus mean : 44.1650

Gaus Width: 3.8419

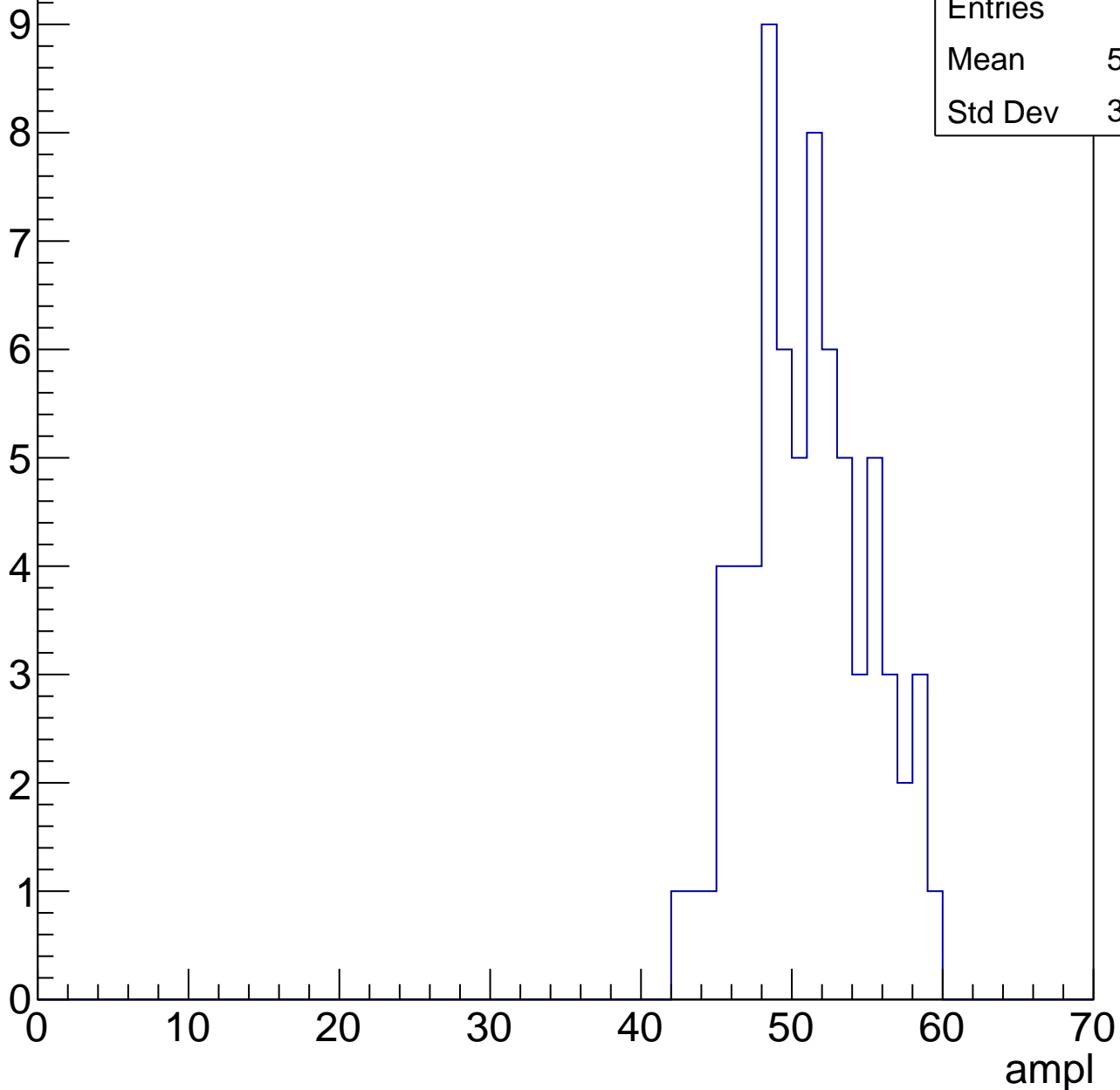


B1L100S, U15-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	50.62
Std Dev	3.923

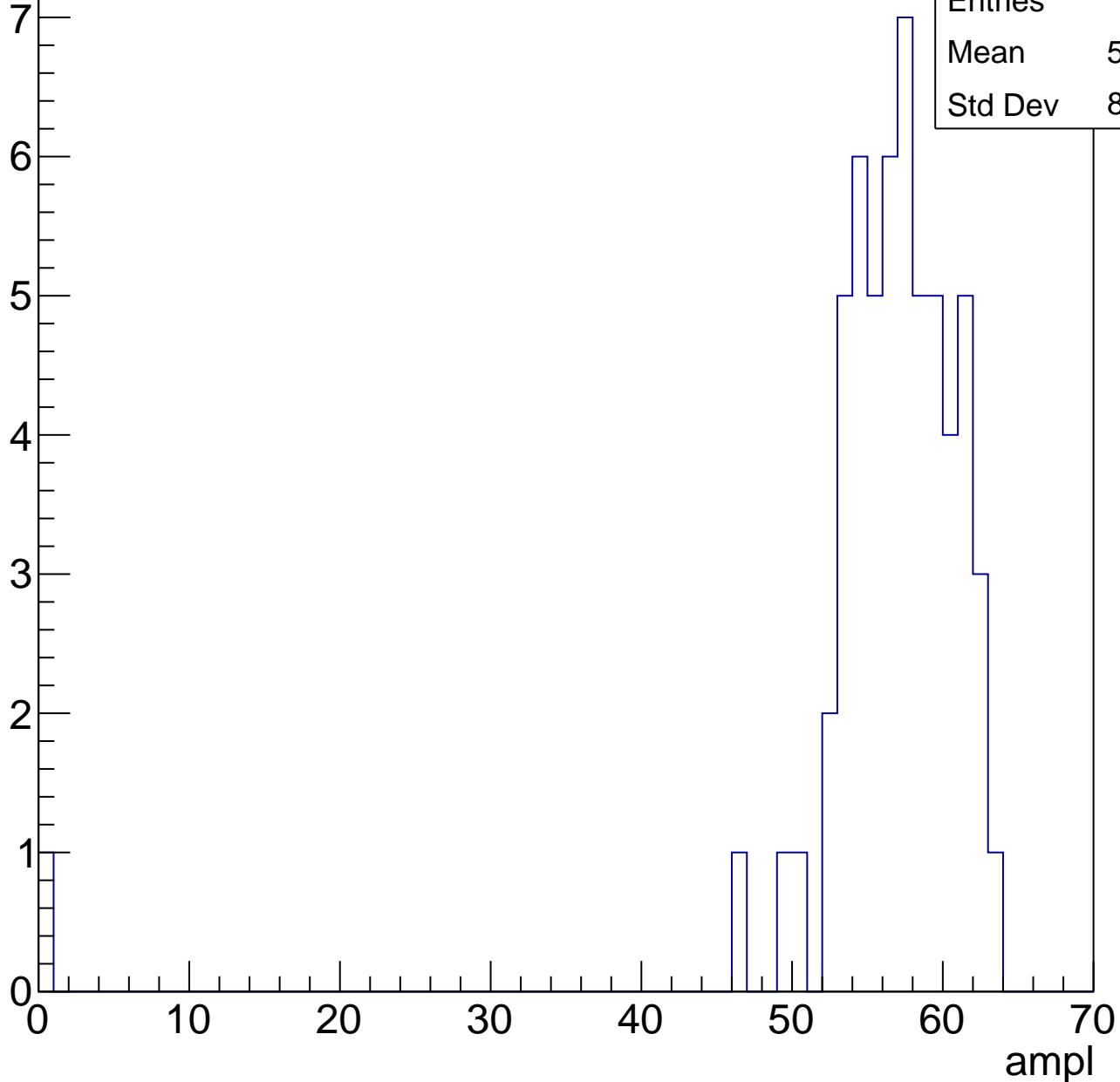


B1L100S, U15-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	55.64
Std Dev	8.134

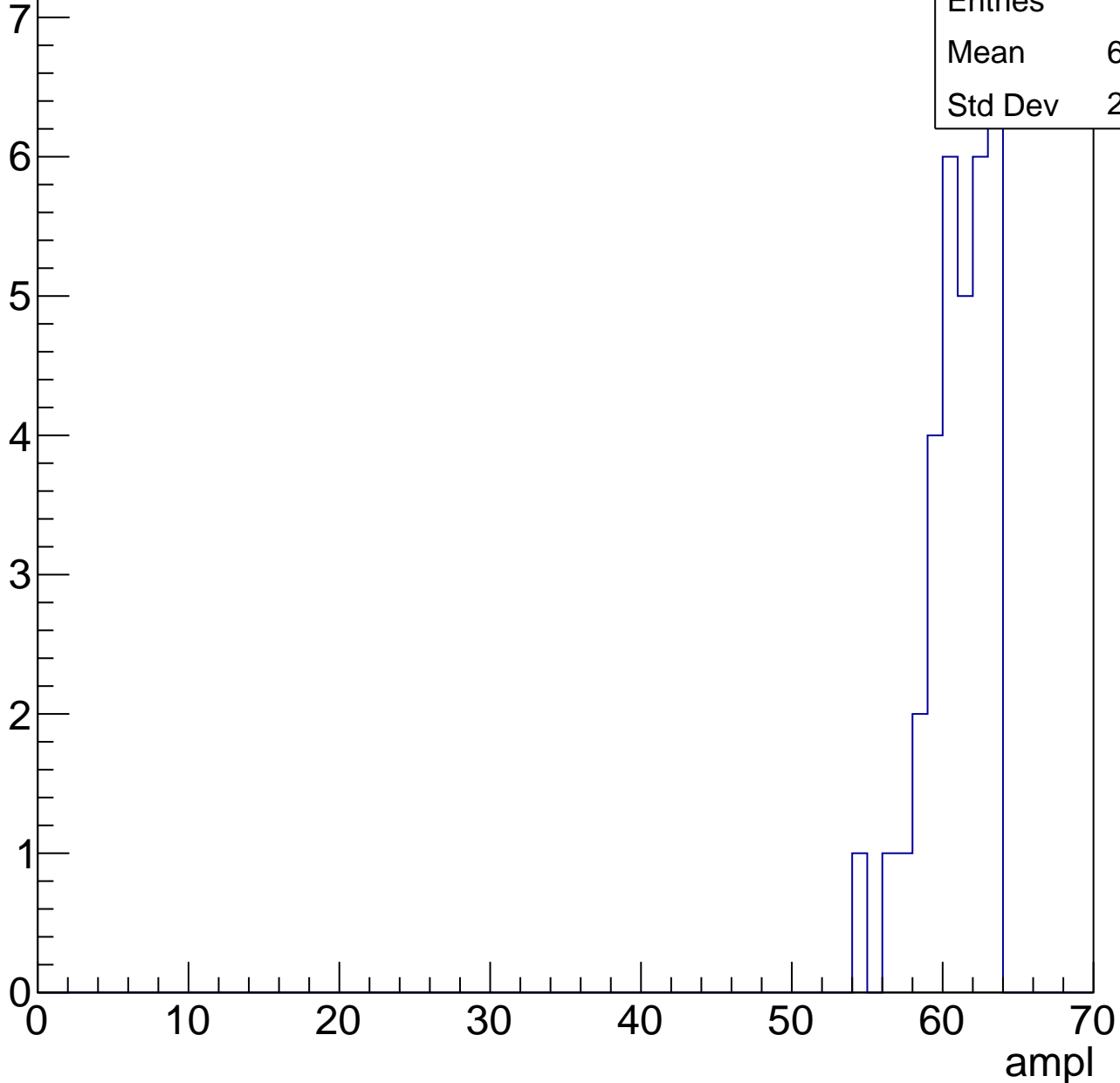


B1L100S, U15-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	60.52
Std Dev	2.176



B1L100S, U15-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U15-ch120, adc0

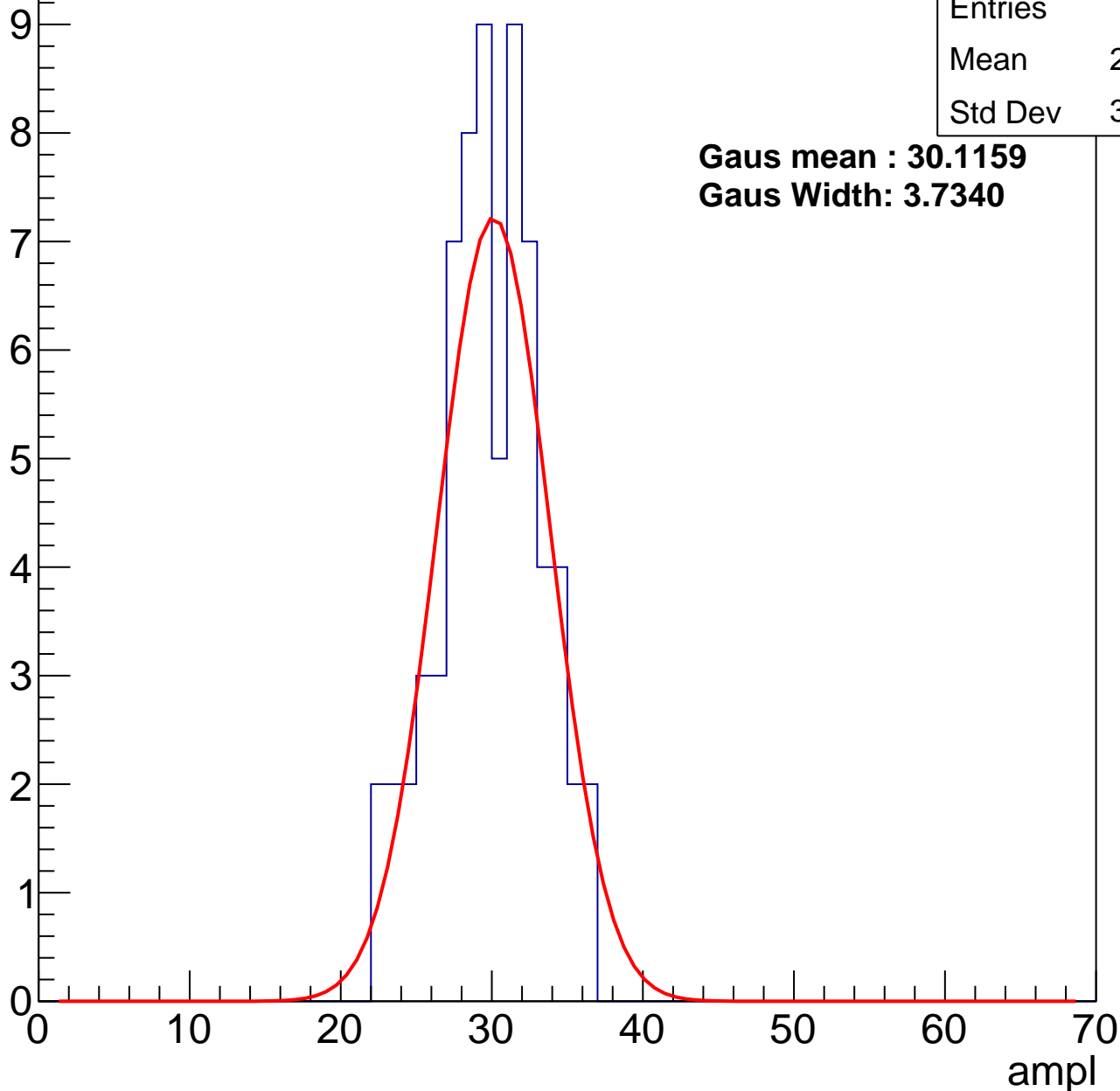
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	29.39
Std Dev	3.315

Gaus mean : 30.1159

Gaus Width: 3.7340



B1L100S, U15-ch120, adc1

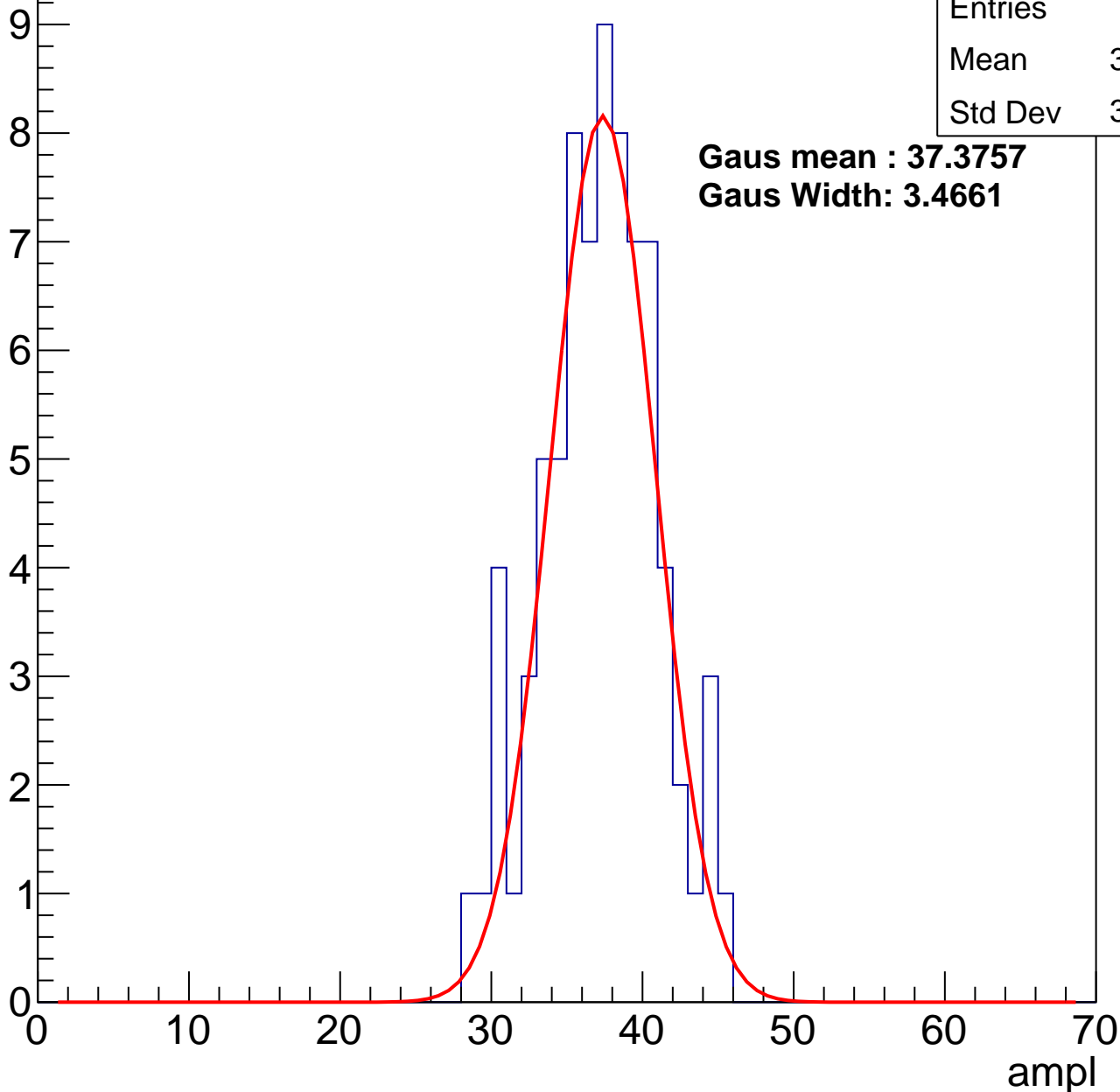
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	36.74
Std Dev	3.729

Gaus mean : 37.3757

Gaus Width: 3.4661



B1L100S, U15-ch120, adc2

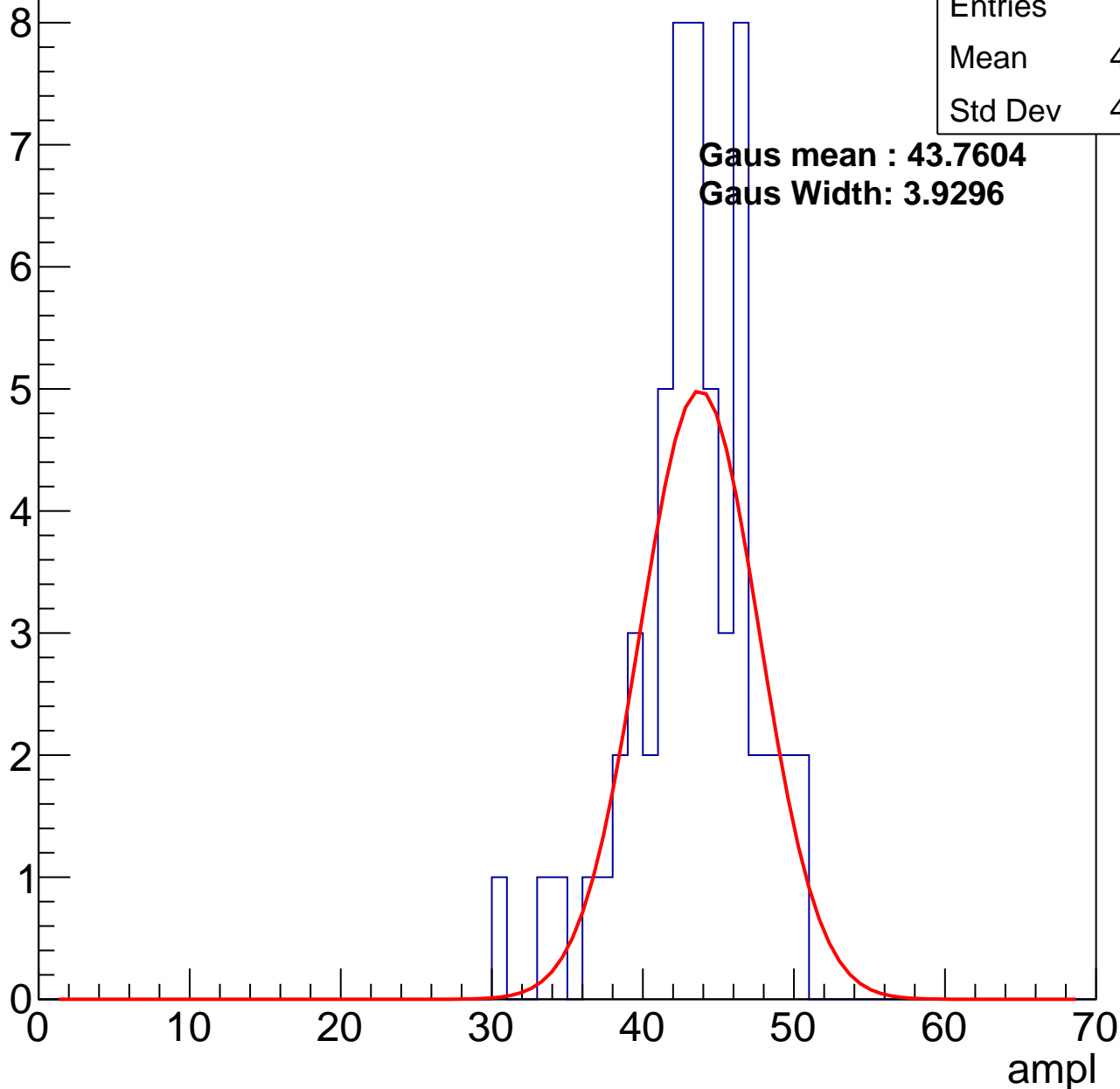
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.79
Std Dev	4.012

Gaus mean : 43.7604

Gaus Width: 3.9296

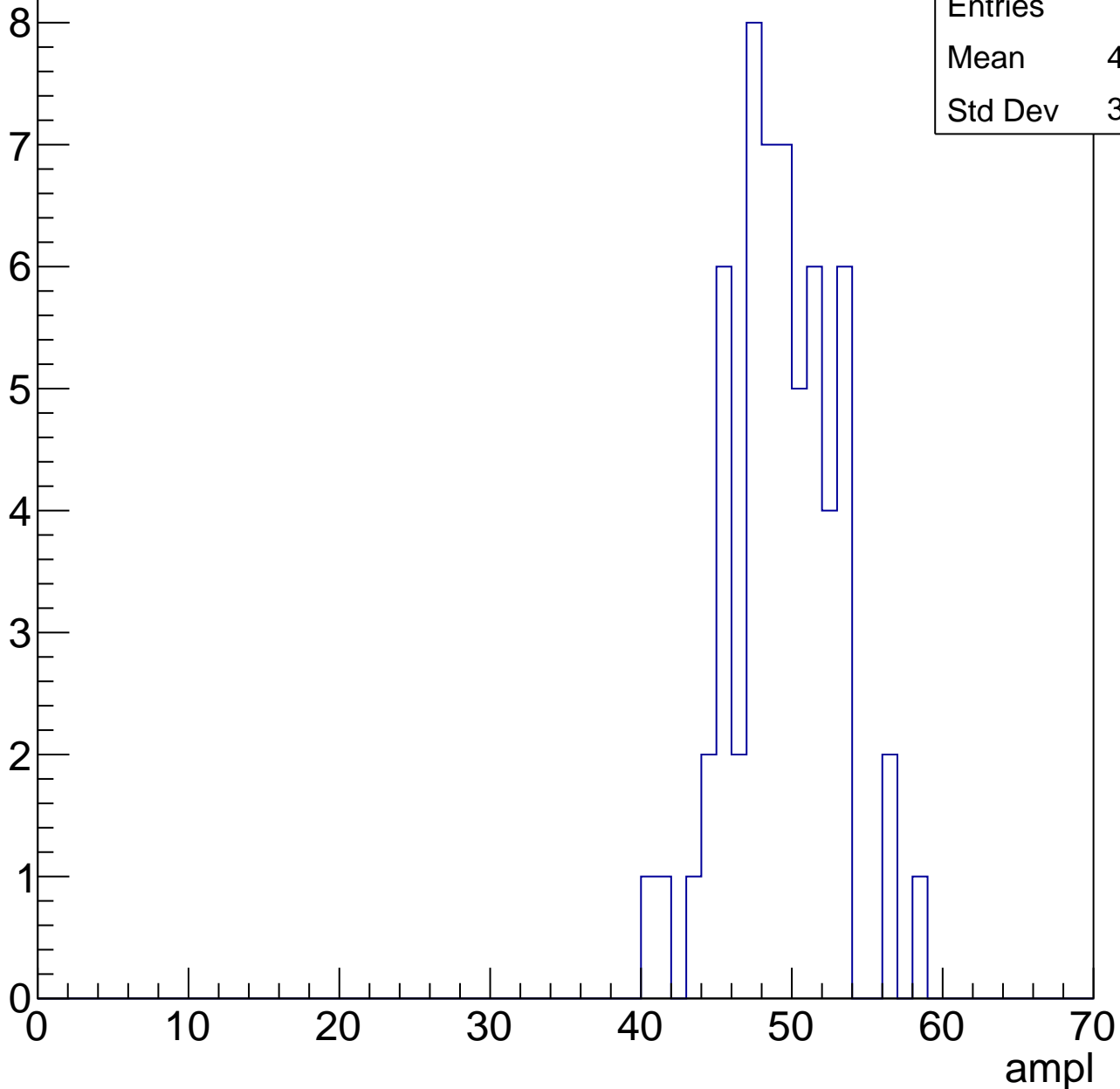


B1L100S, U15-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

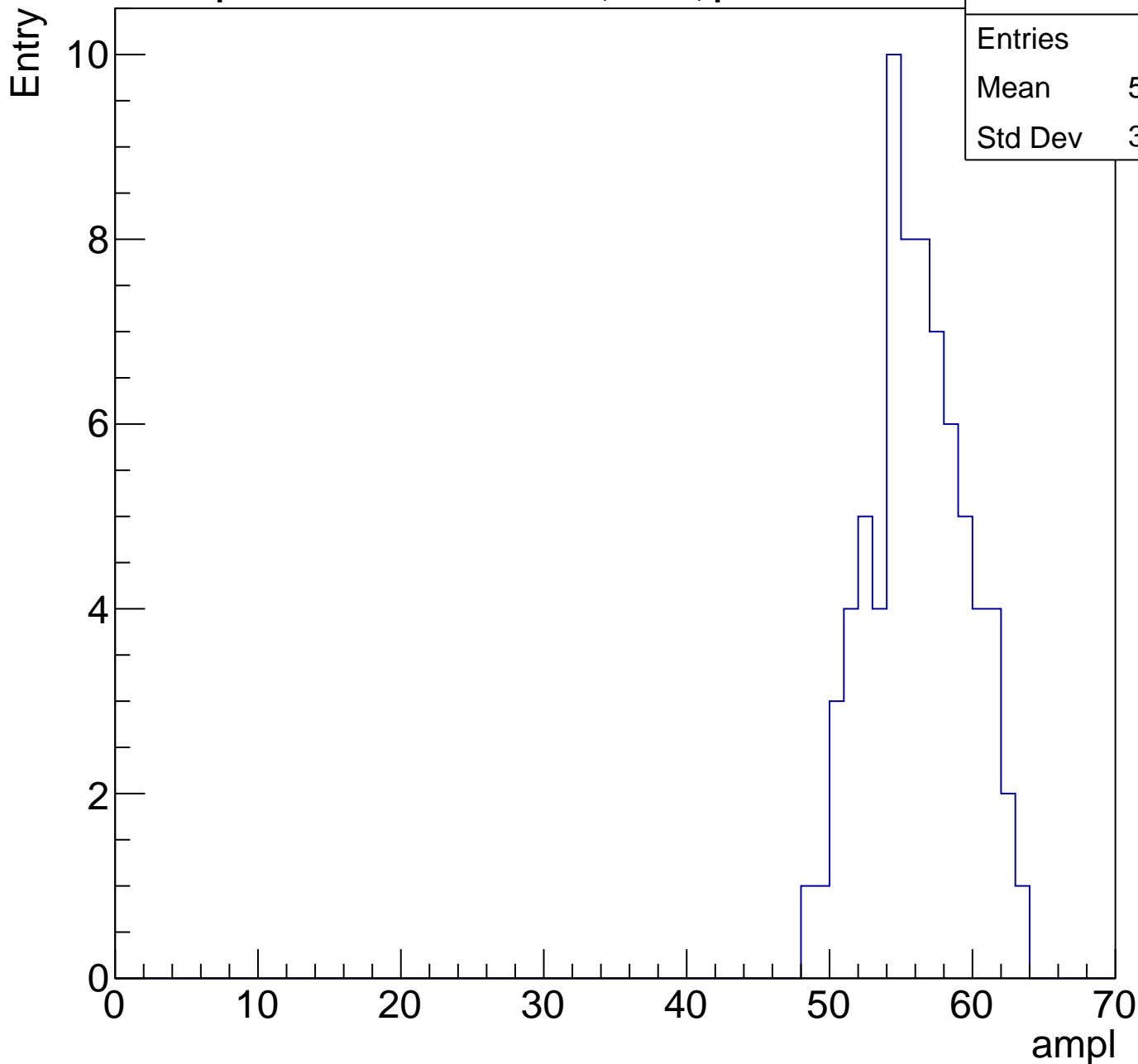
Entries	59
Mean	48.83
Std Dev	3.499



B1L100S, U15-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	55.67
Std Dev	3.388

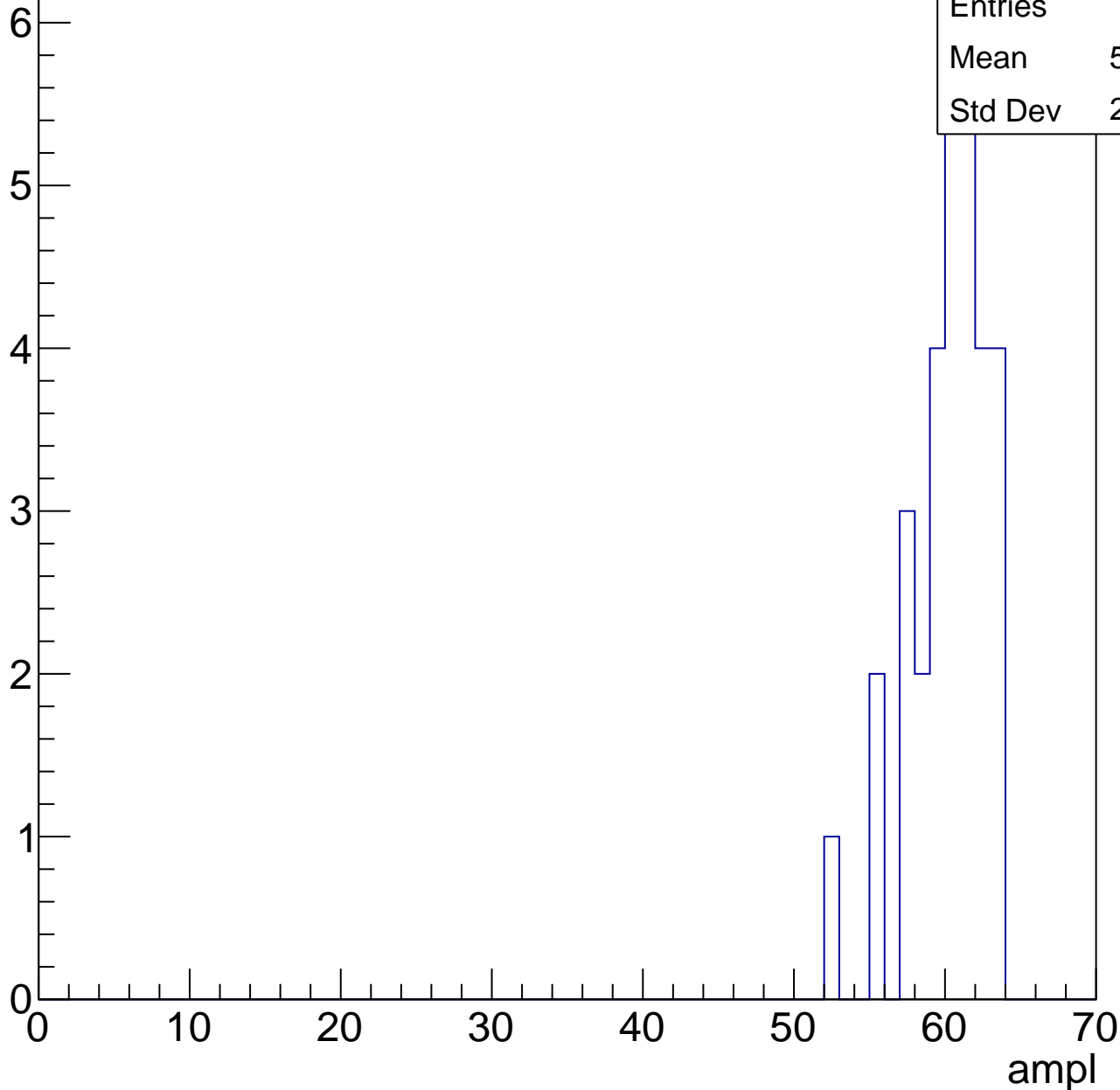


B1L100S, U15-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

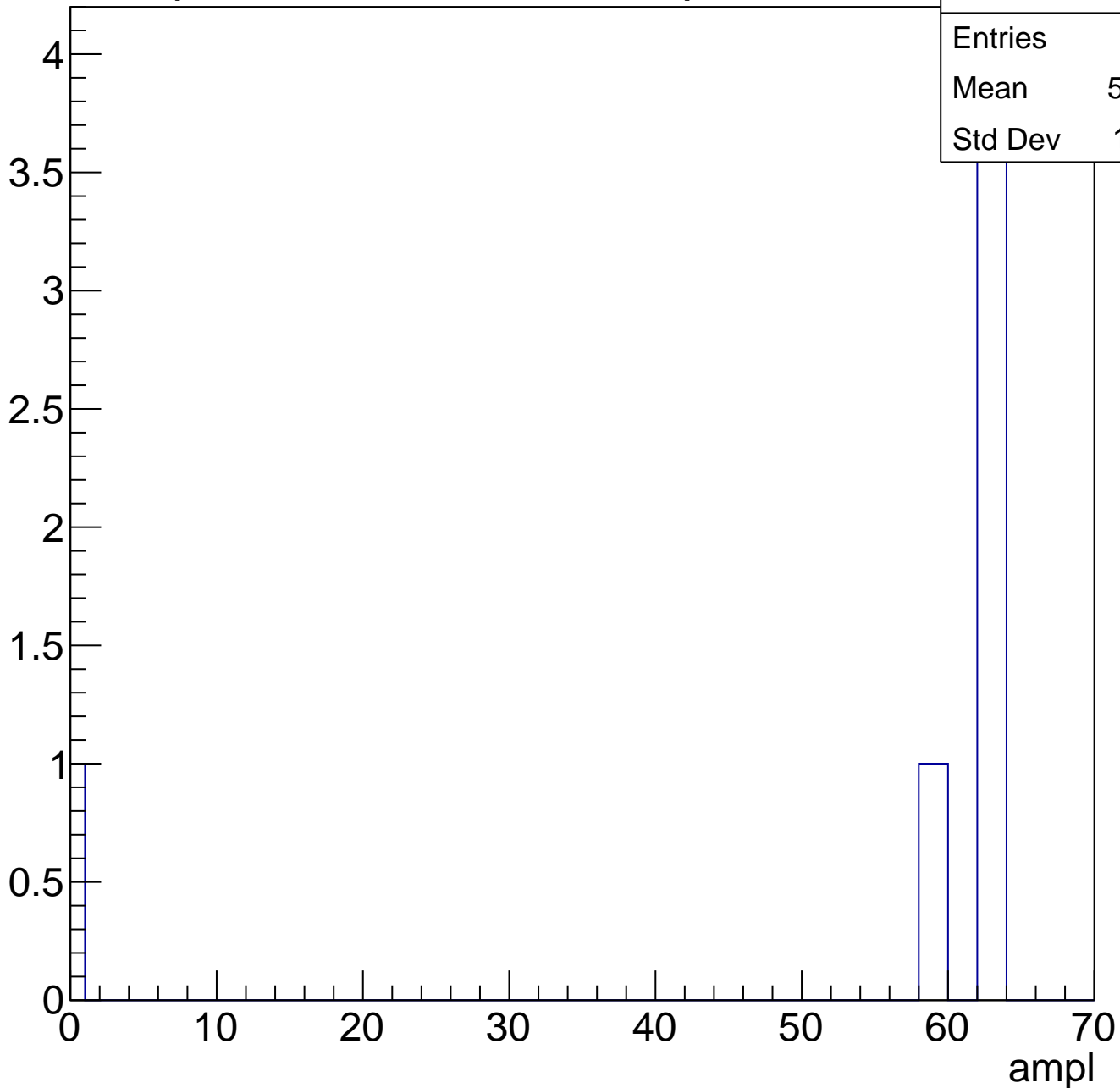
Entries	32
Mean	59.72
Std Dev	2.552



B1L100S, U15-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch121, adc0

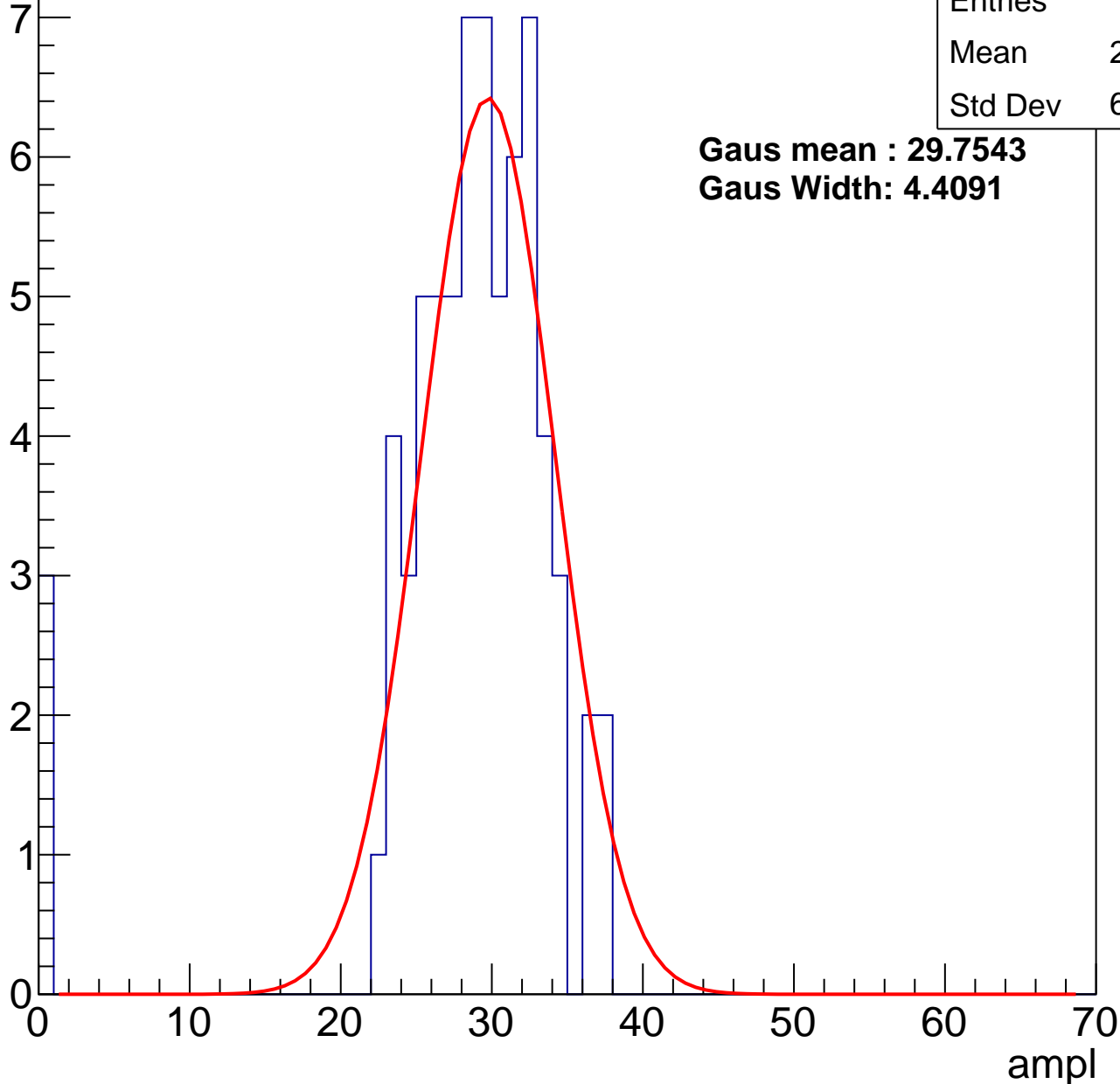
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	27.75
Std Dev	6.908

Gaus mean : 29.7543

Gaus Width: 4.4091



B1L100S, U15-ch121, adc1

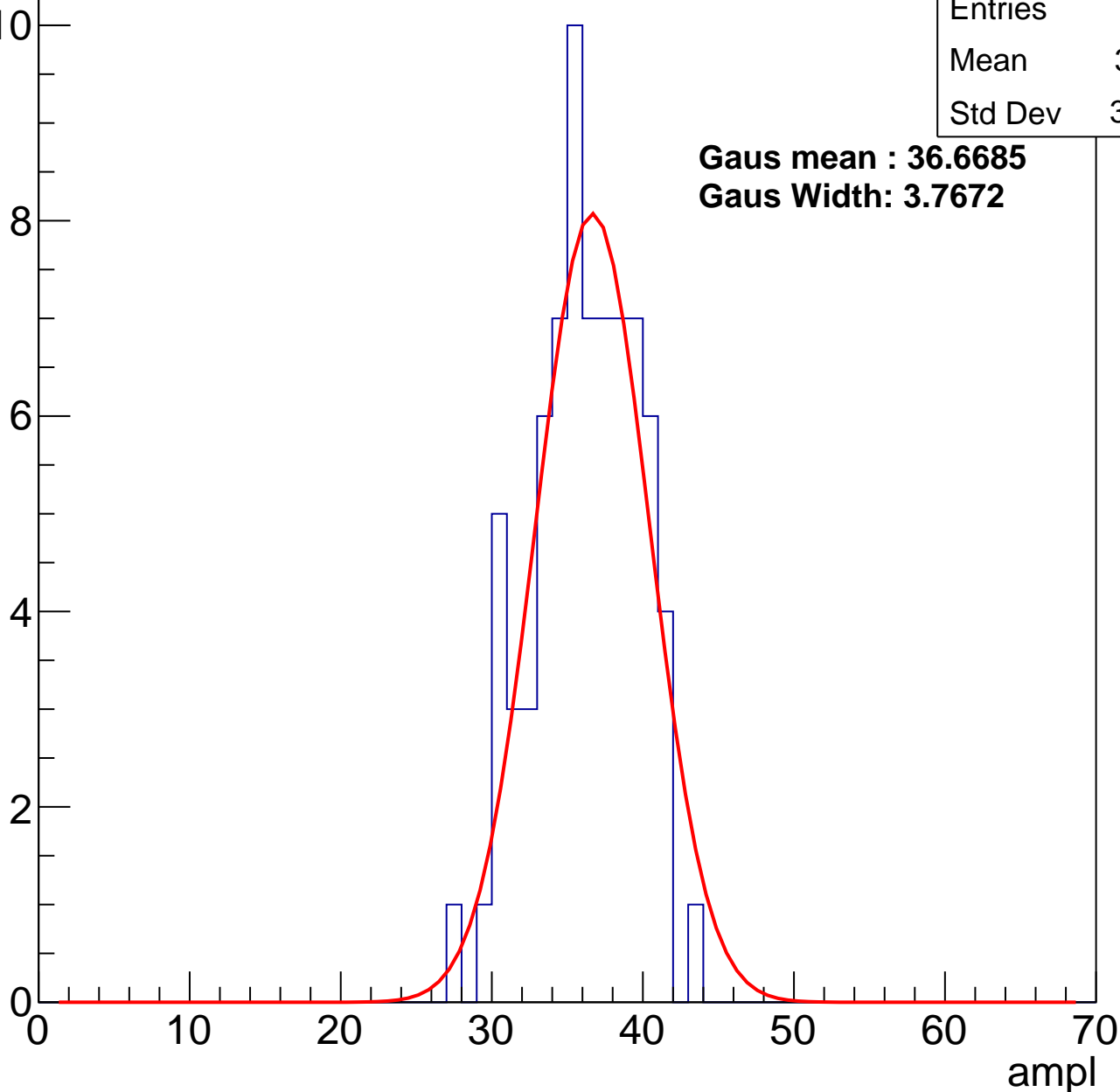
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	35.71
Std Dev	3.389

Gaus mean : 36.6685

Gaus Width: 3.7672



B1L100S, U15-ch121, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	43.66
Std Dev	3.286

Gaus mean : 44.2023

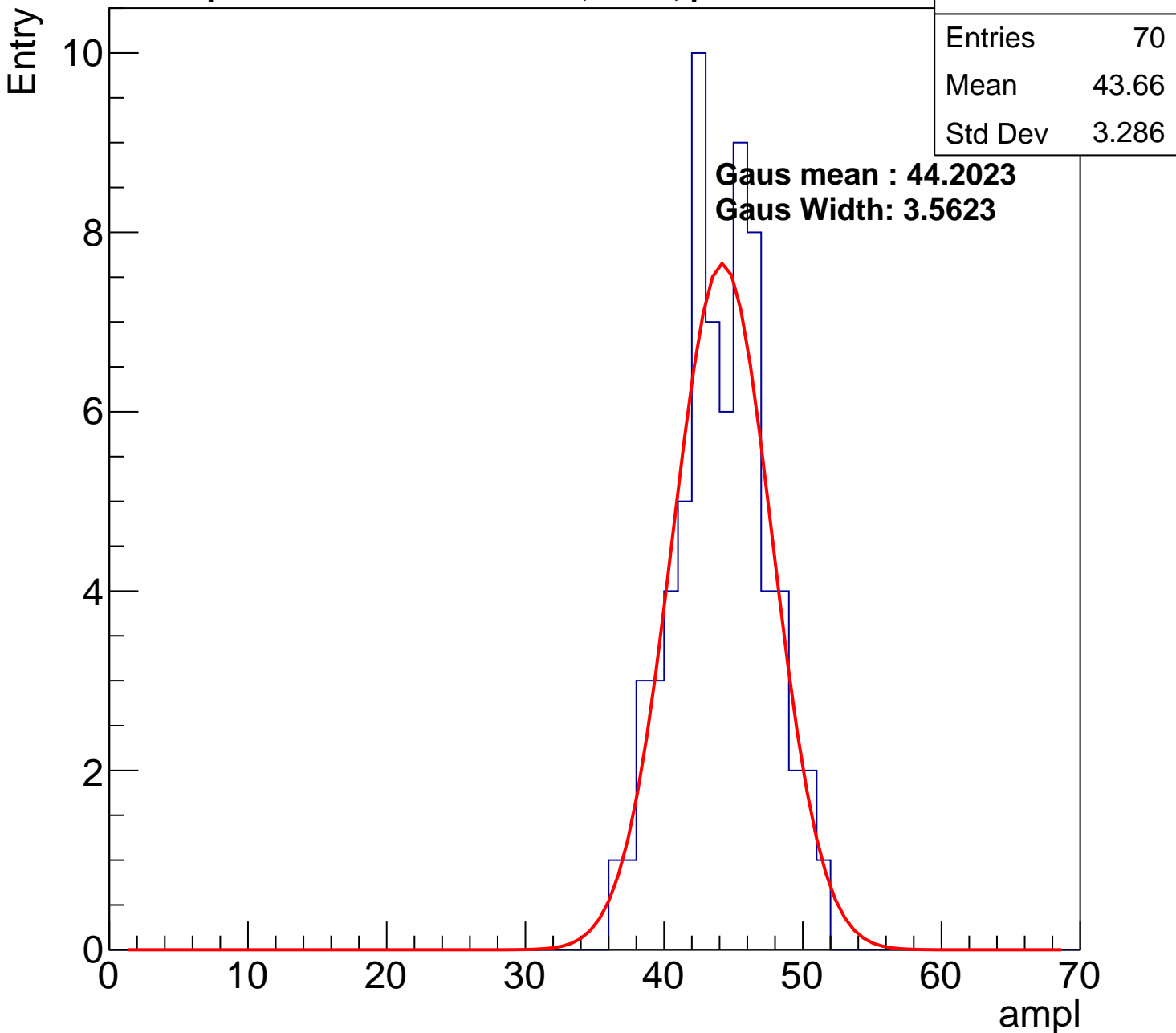
Gaus Width: 3.5623

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

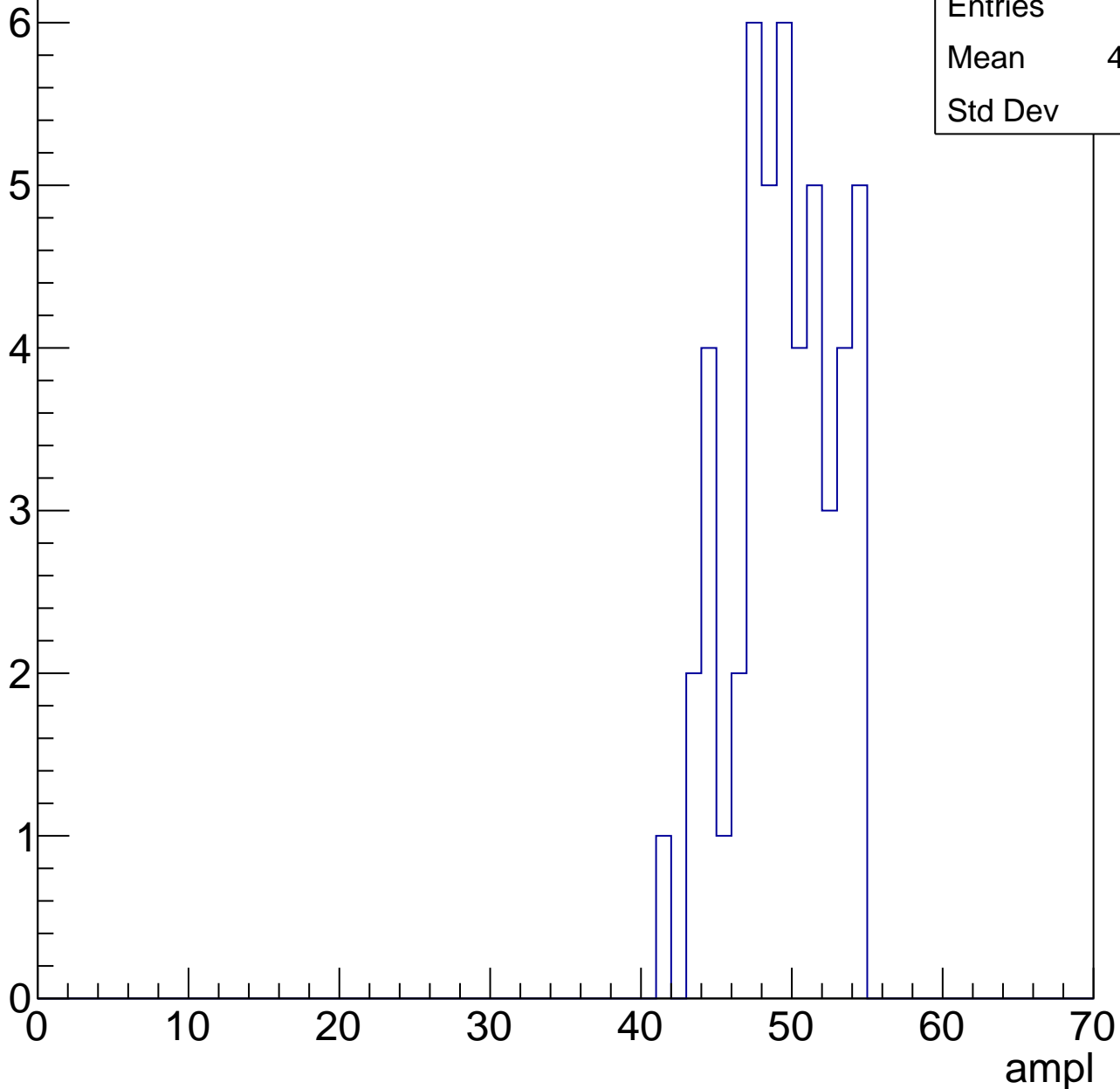


B1L100S, U15-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	48.94
Std Dev	3.35

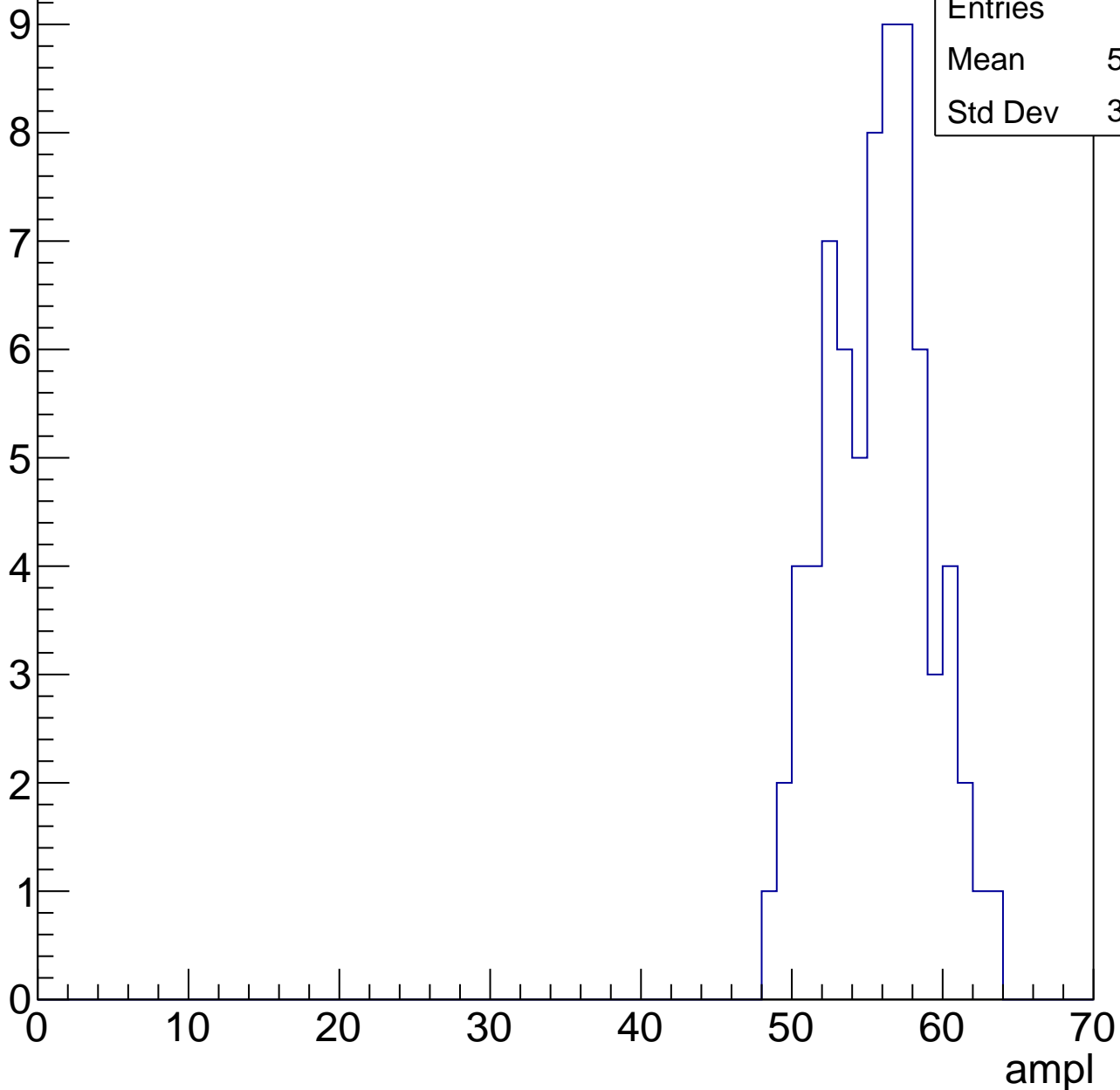


B1L100S, U15-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

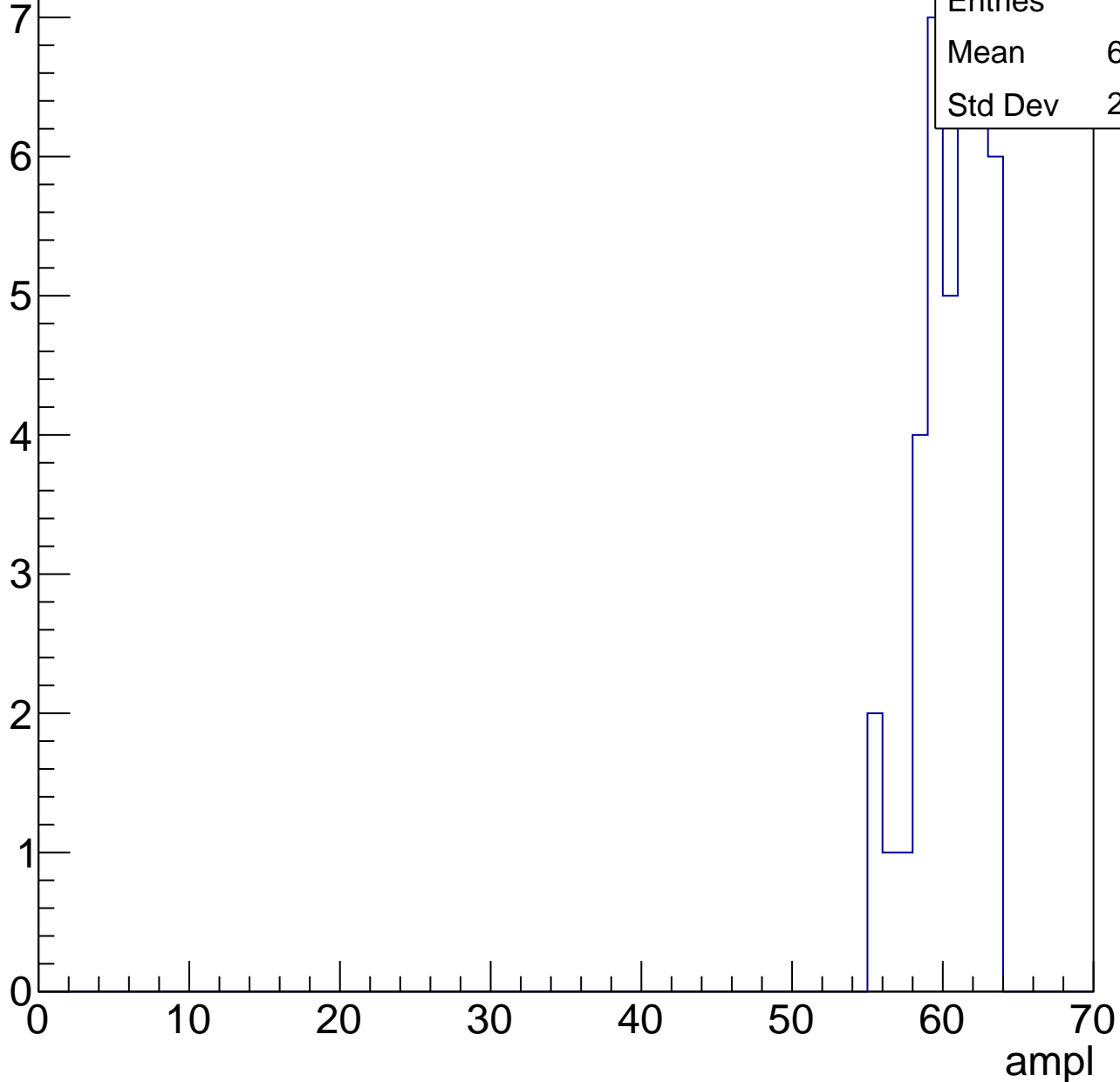
Entries	72
Mean	55.15
Std Dev	3.349



B1L100S, U15-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

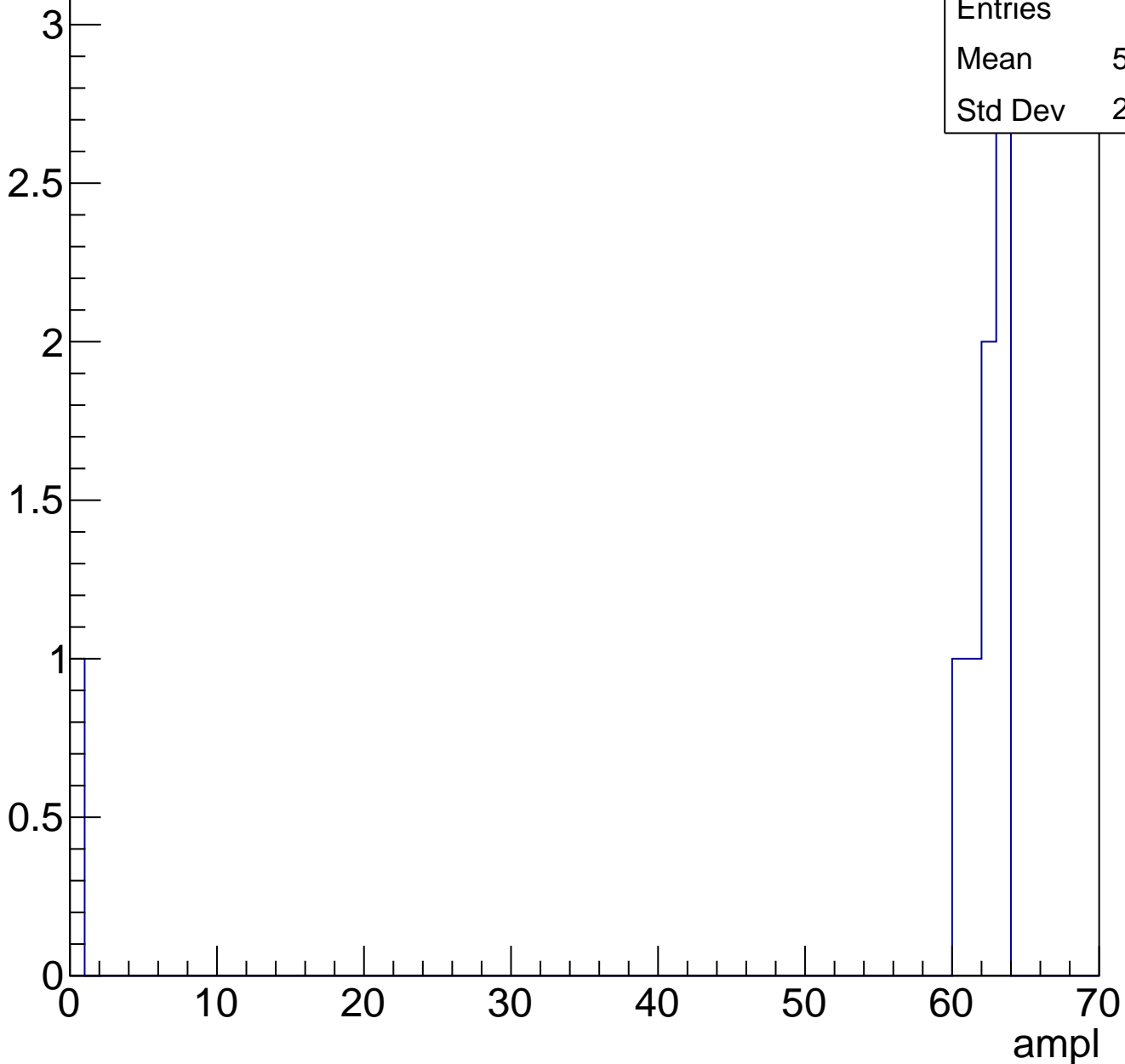
Entry



B1L100S, U15-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L100S, U15-ch122, adc0

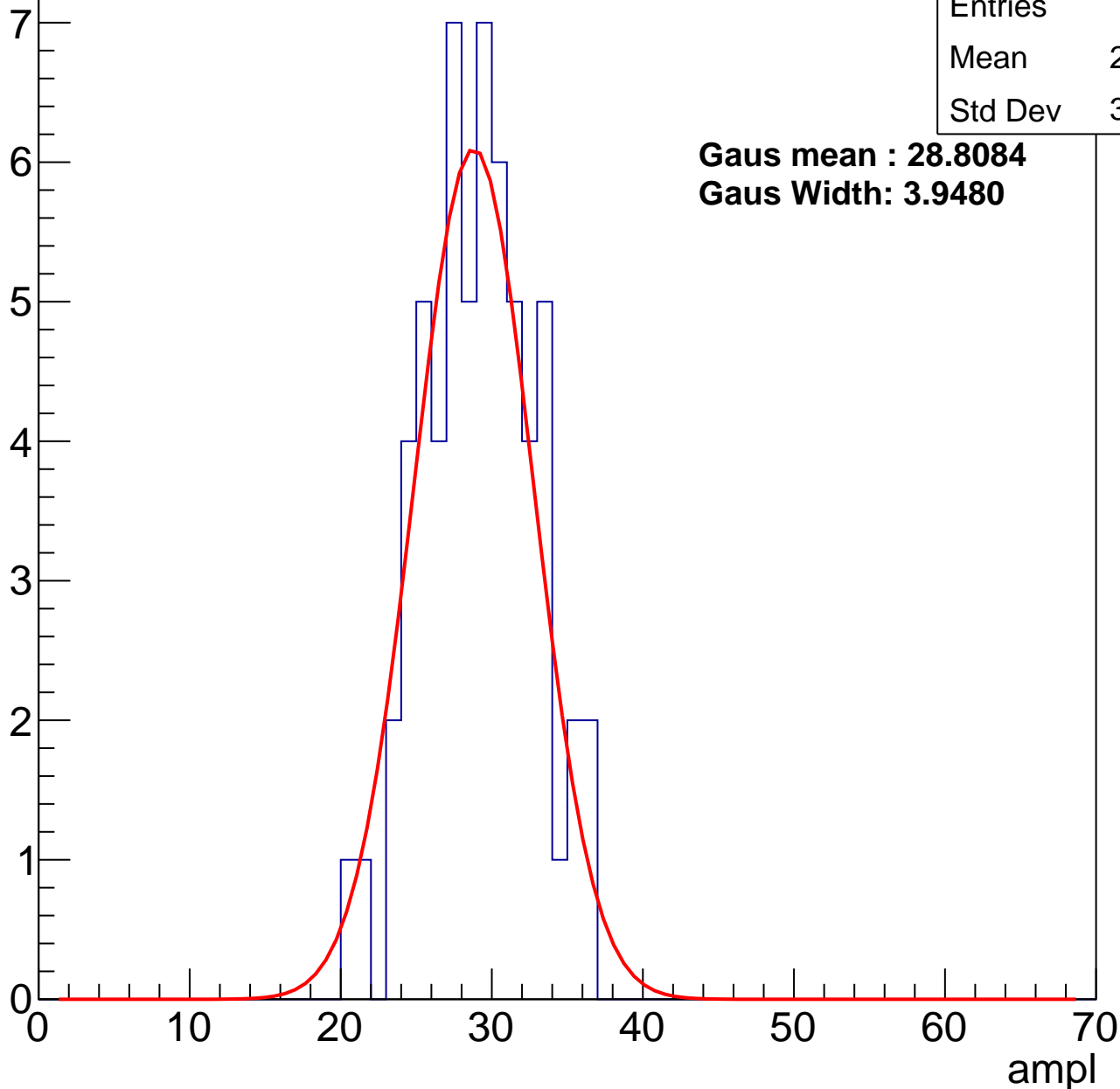
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	28.66
Std Dev	3.612

Gaus mean : 28.8084

Gaus Width: 3.9480



B1L100S, U15-ch122, adc1

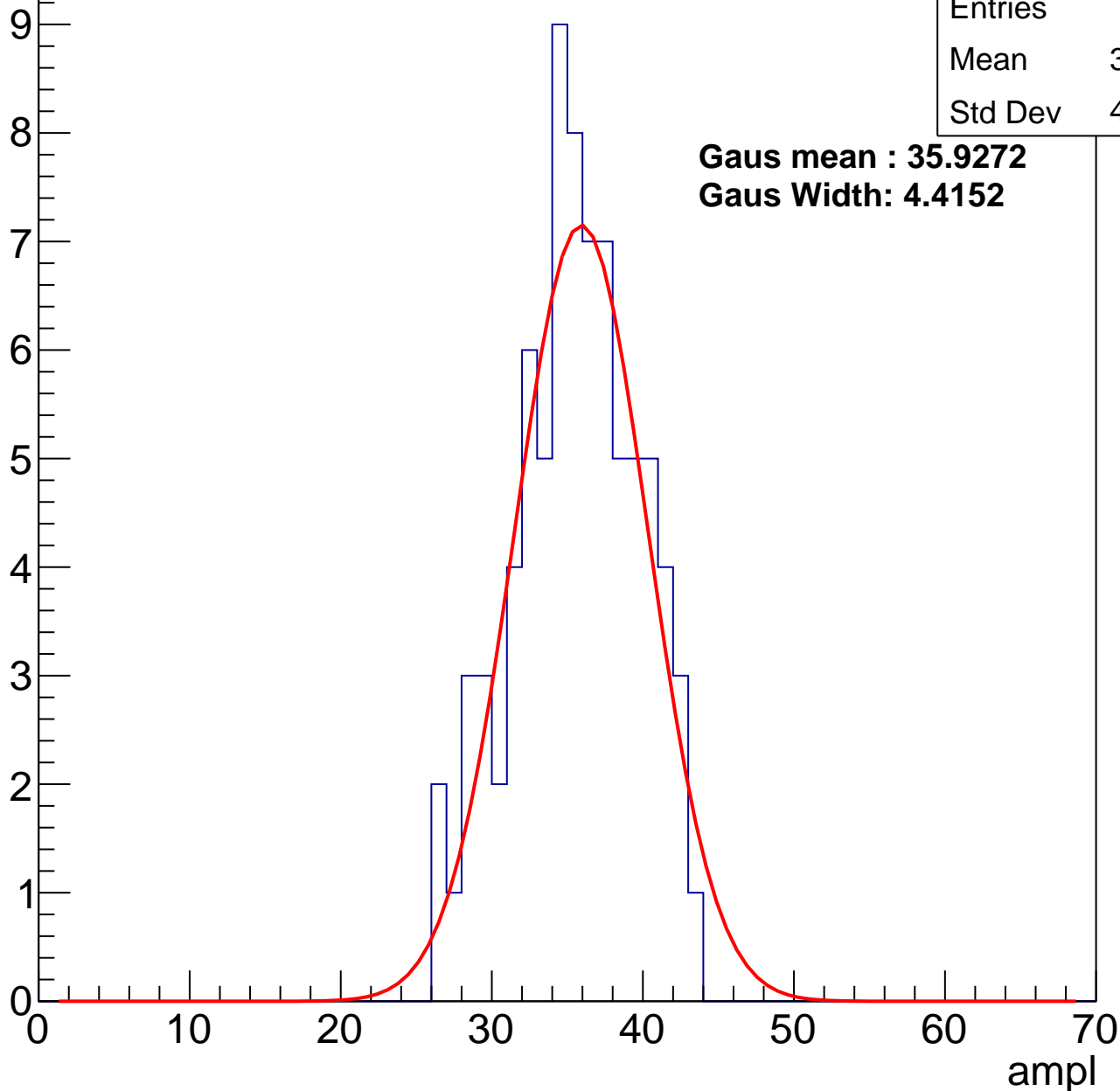
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	35.08
Std Dev	4.058

Gaus mean : 35.9272

Gaus Width: 4.4152



B1L100S, U15-ch122, adc2

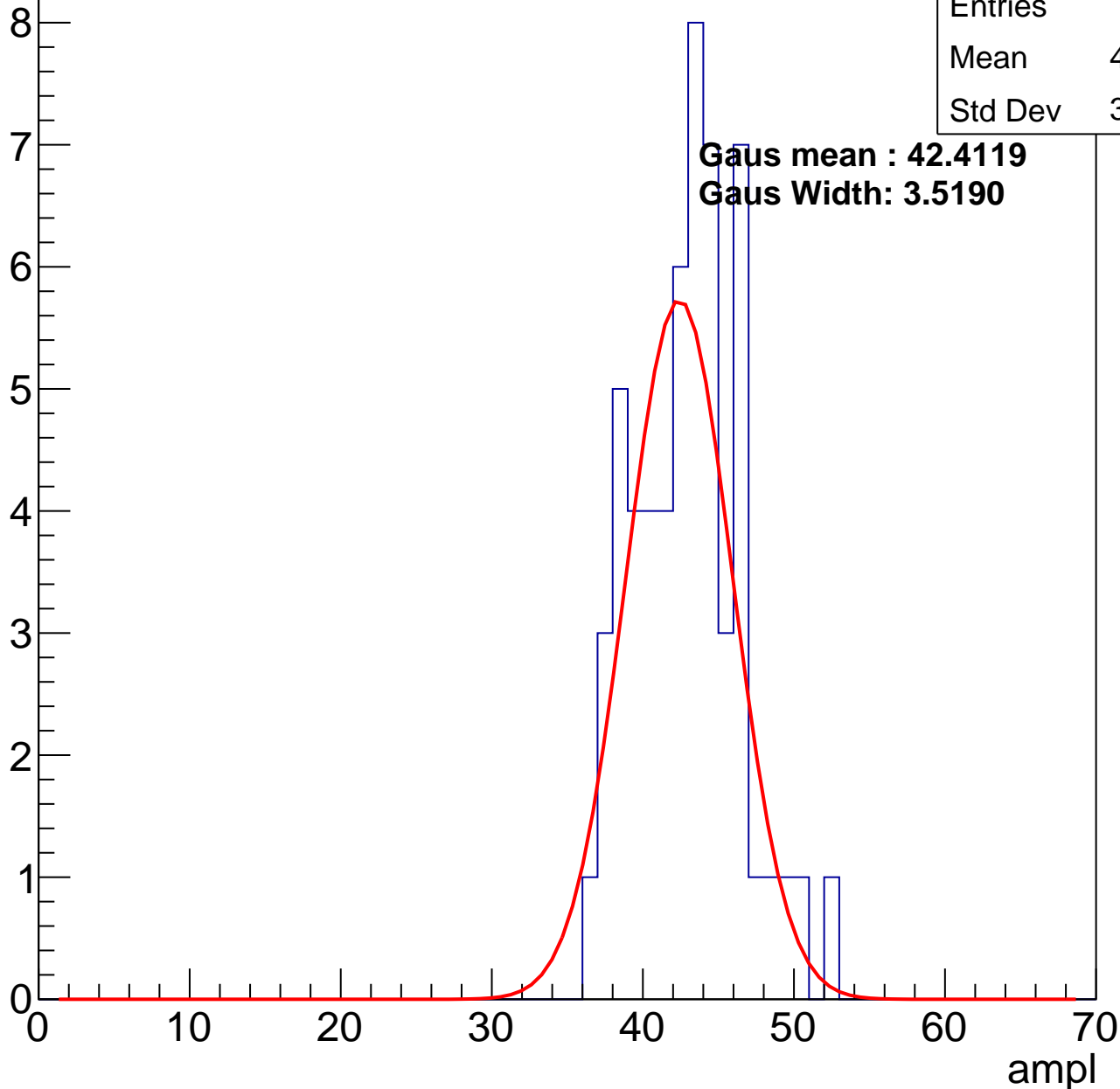
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.53
Std Dev	3.454

Gaus mean : 42.4119

Gaus Width: 3.5190

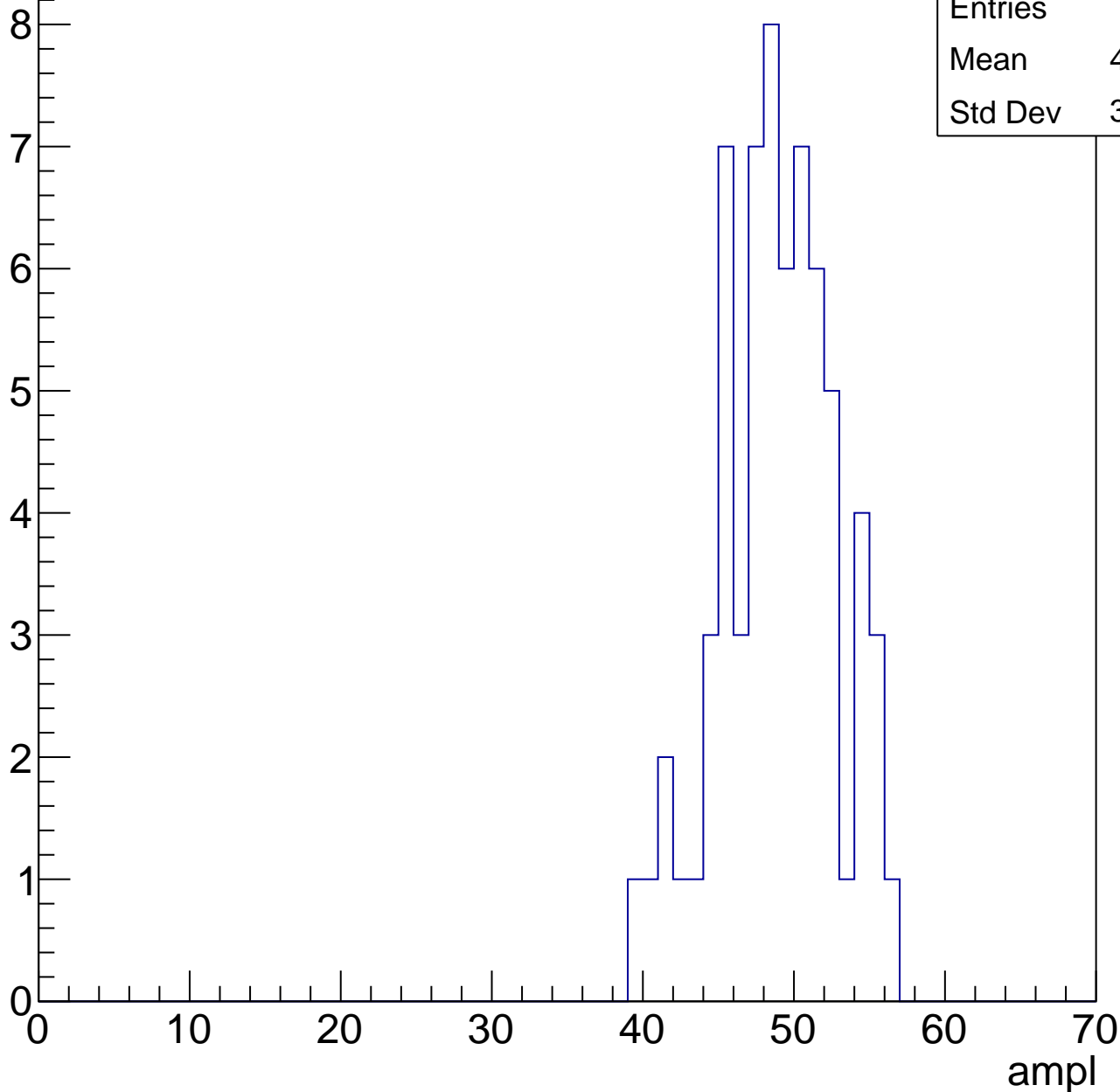


B1L100S, U15-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

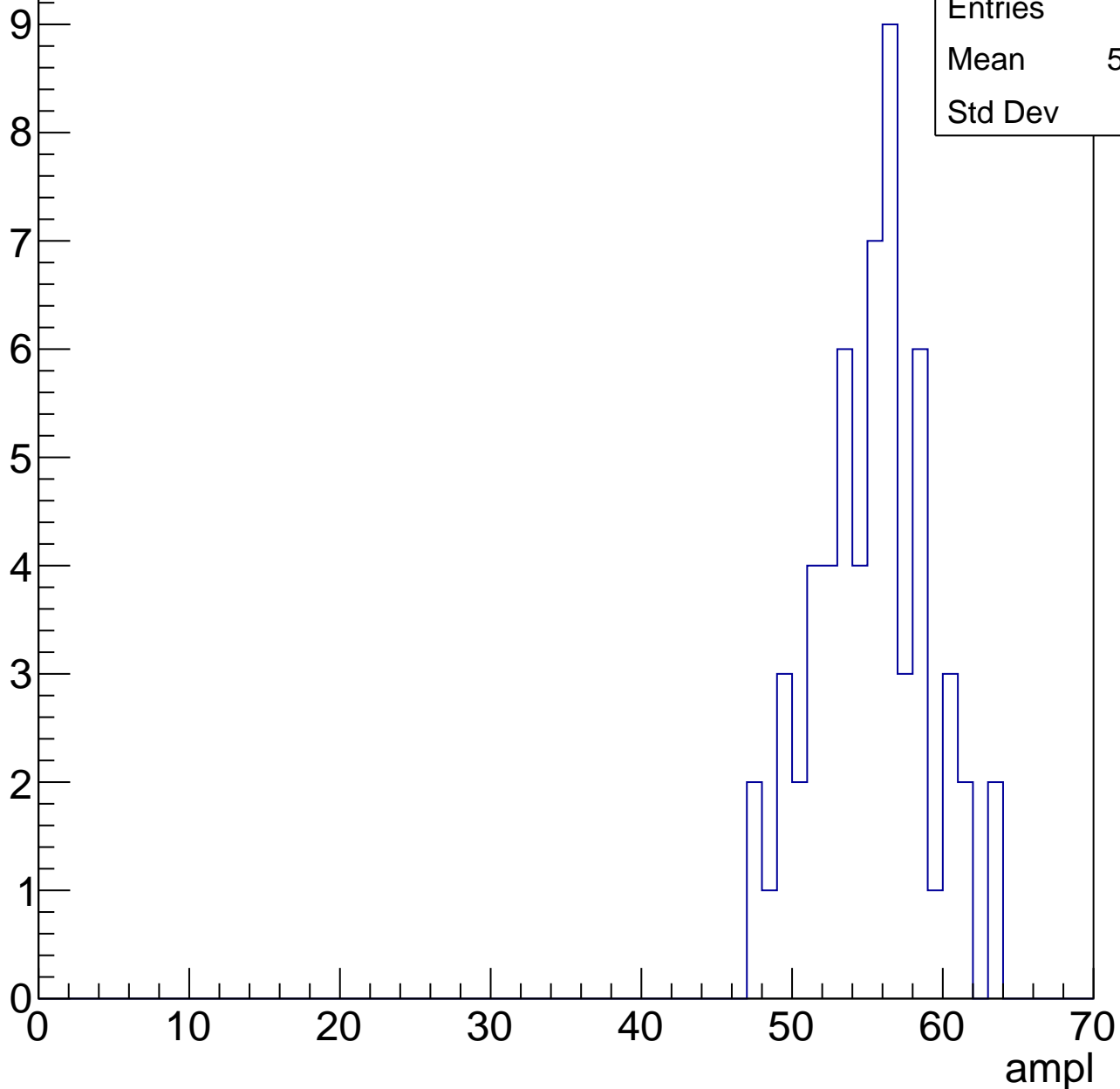
Entries	67
Mean	48.42
Std Dev	3.806



B1L100S, U15-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



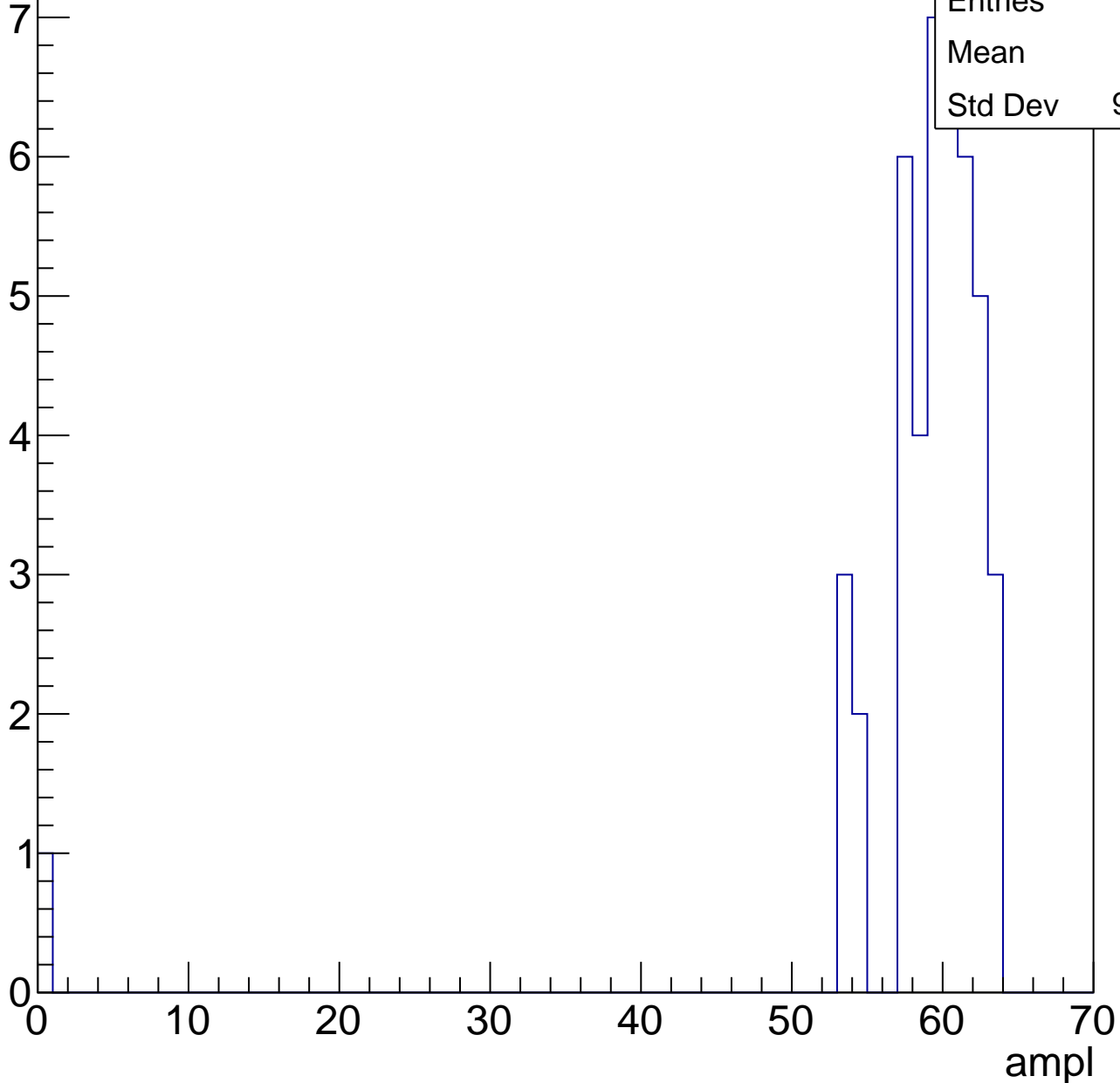
Entries	59
Mean	54.75
Std Dev	3.74

B1L100S, U15-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	57.7
Std Dev	9.191

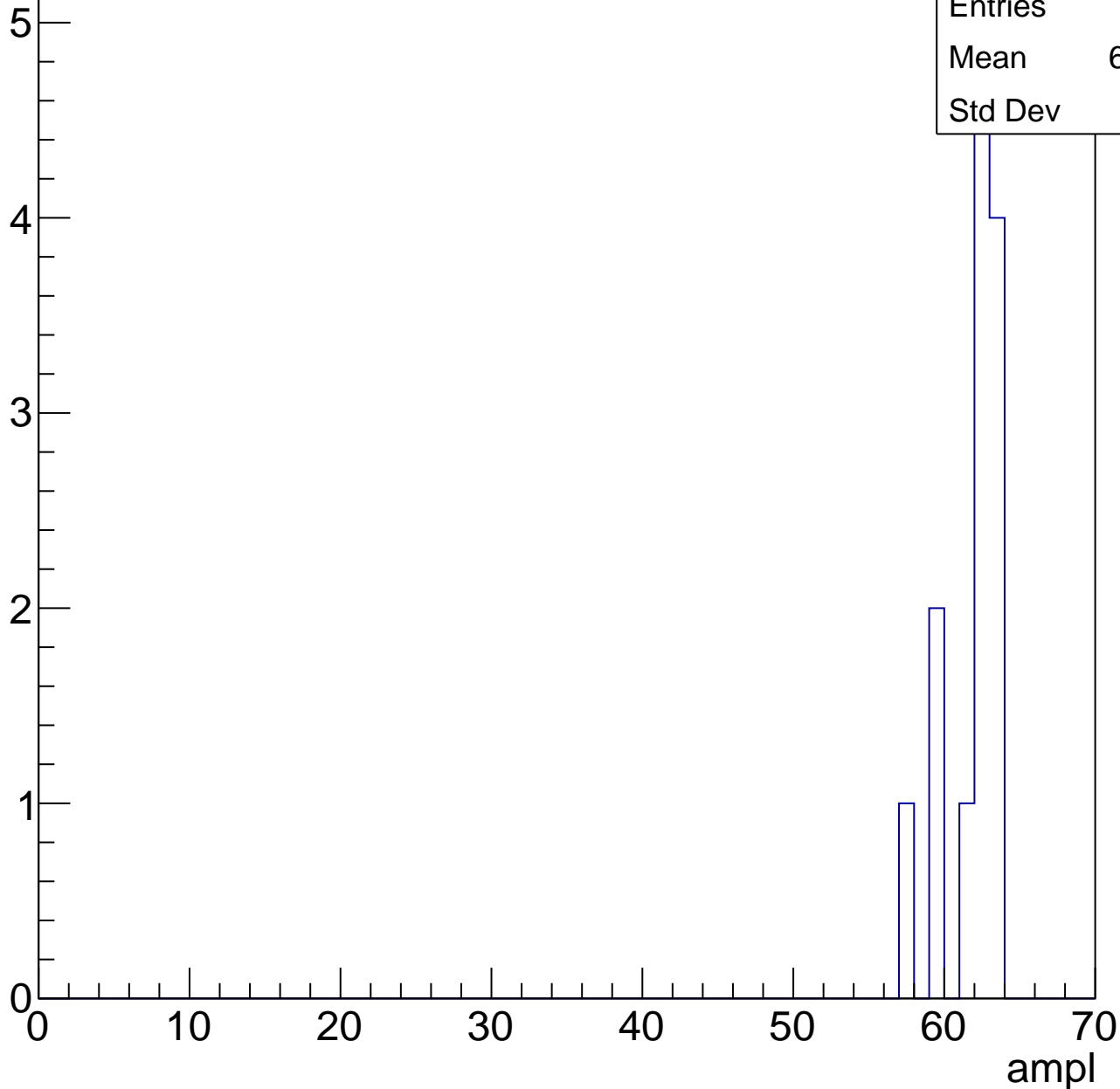


B1L100S, U15-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

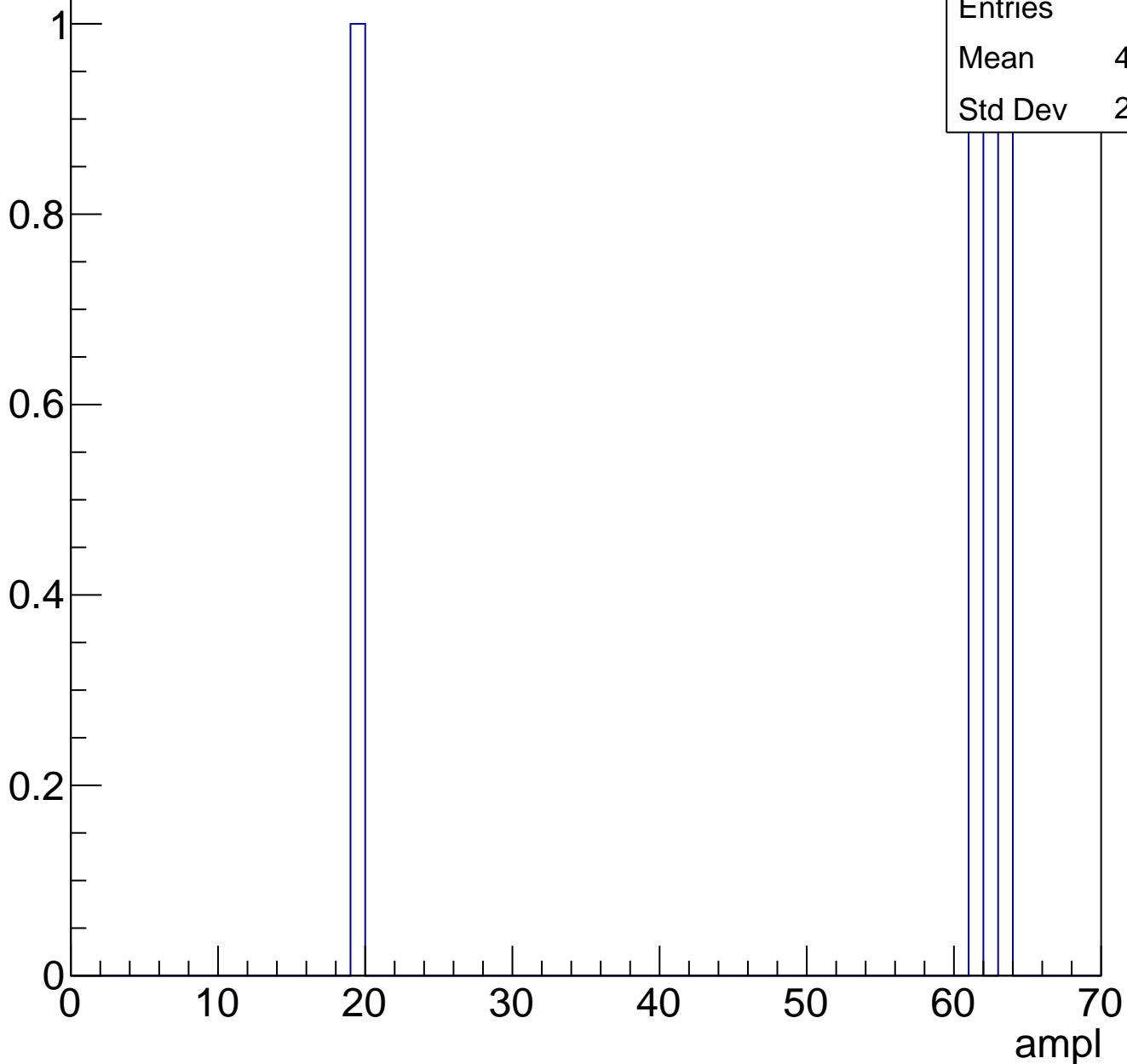
Entries	13
Mean	61.38
Std Dev	1.82



B1L100S, U15-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch123, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	28.71
Std Dev	3.597

Gaus mean : 29.2181

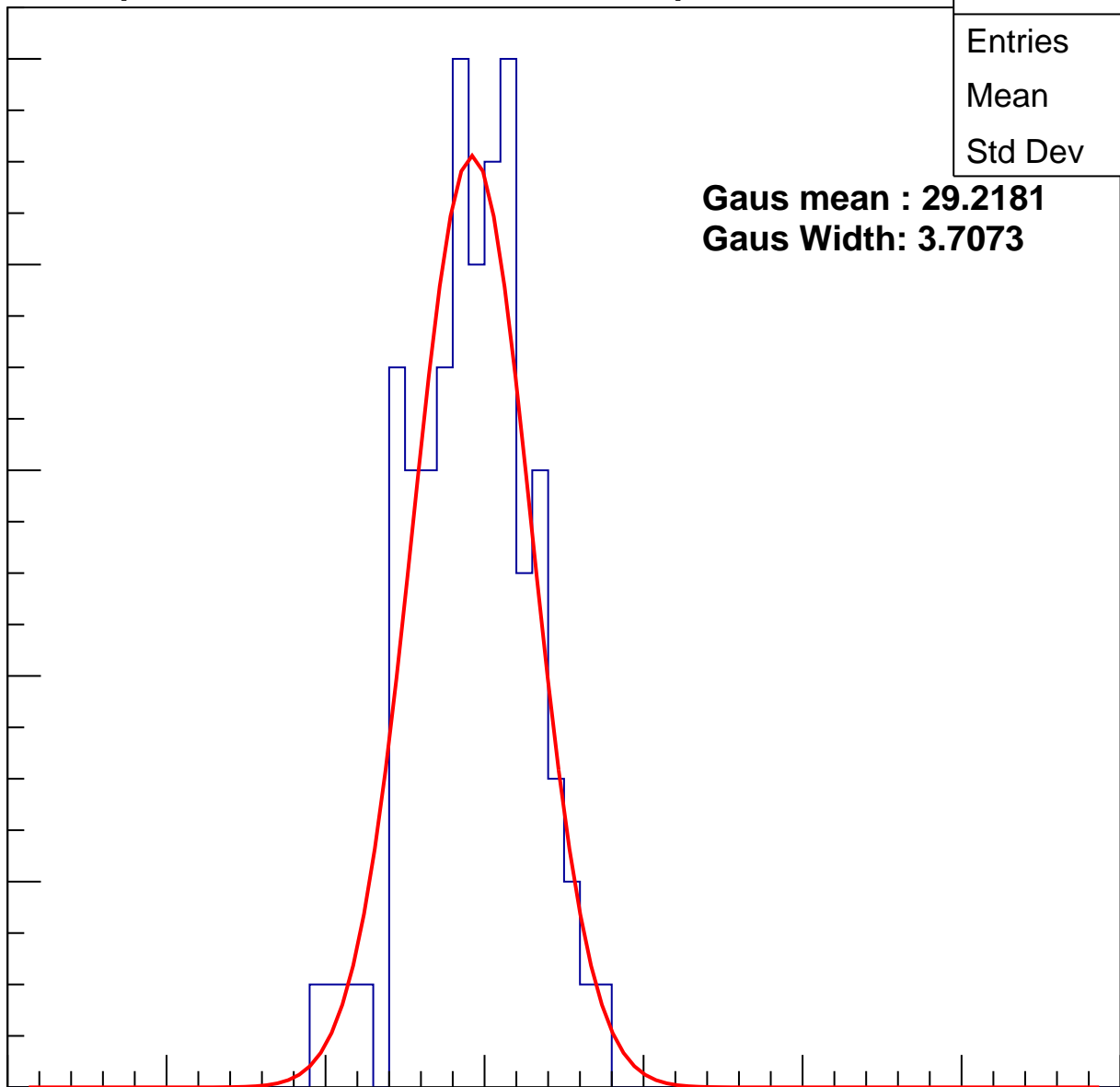
Gaus Width: 3.7073

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch123, adc1

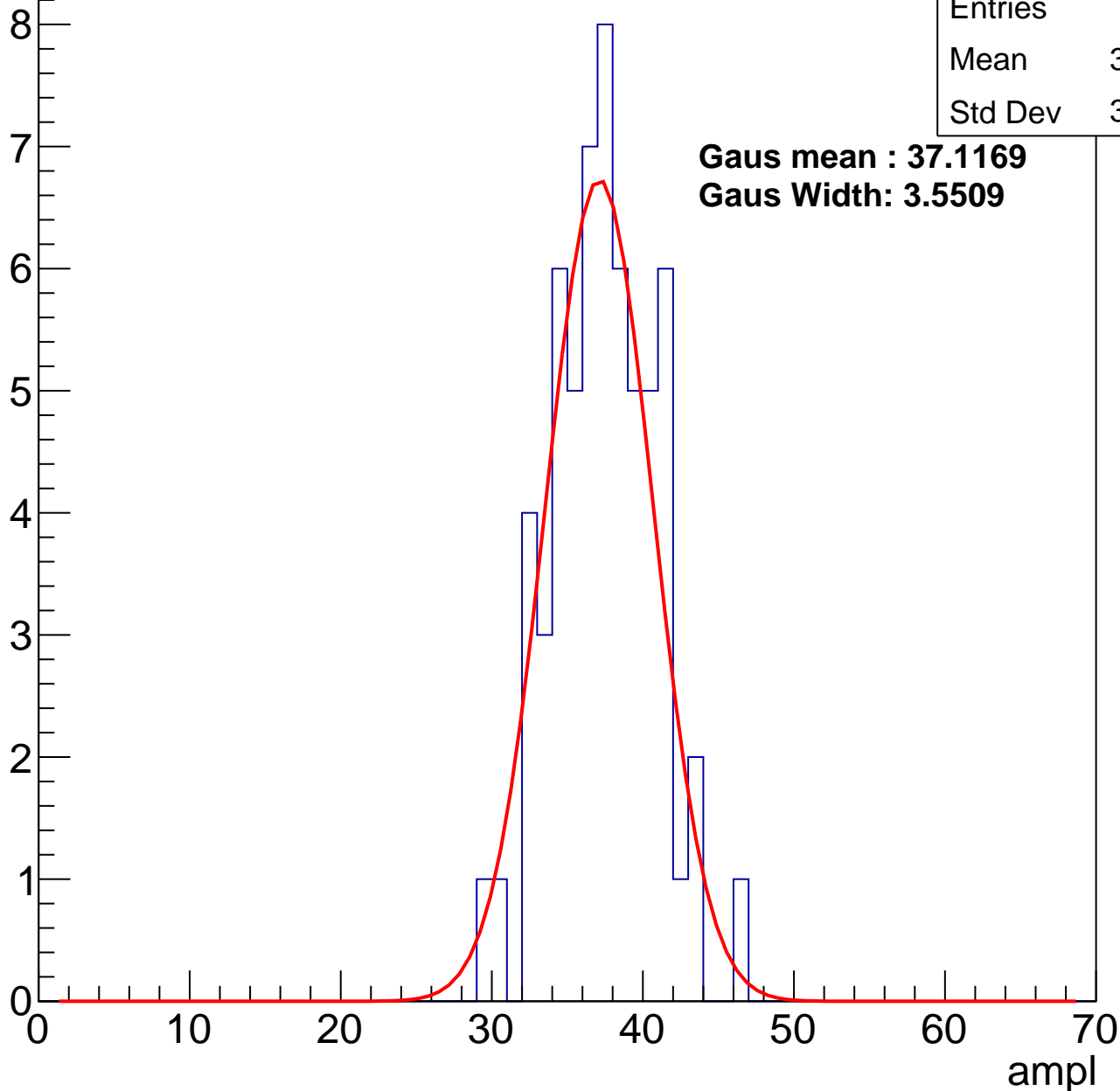
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.98
Std Dev	3.356

Gaus mean : 37.1169

Gaus Width: 3.5509



B1L100S, U15-ch123, adc2

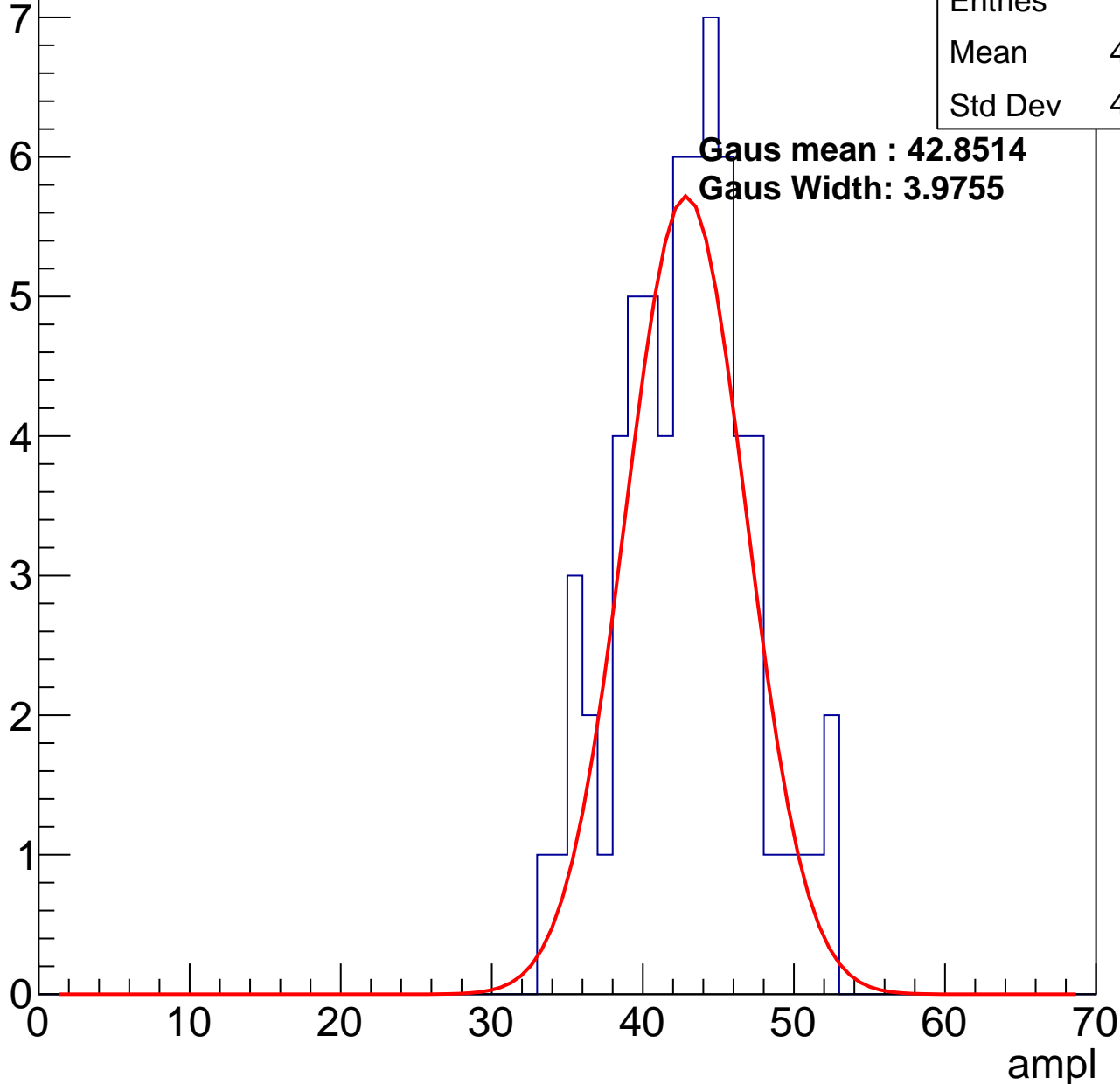
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.37
Std Dev	4.288

Gaus mean : 42.8514

Gaus Width: 3.9755

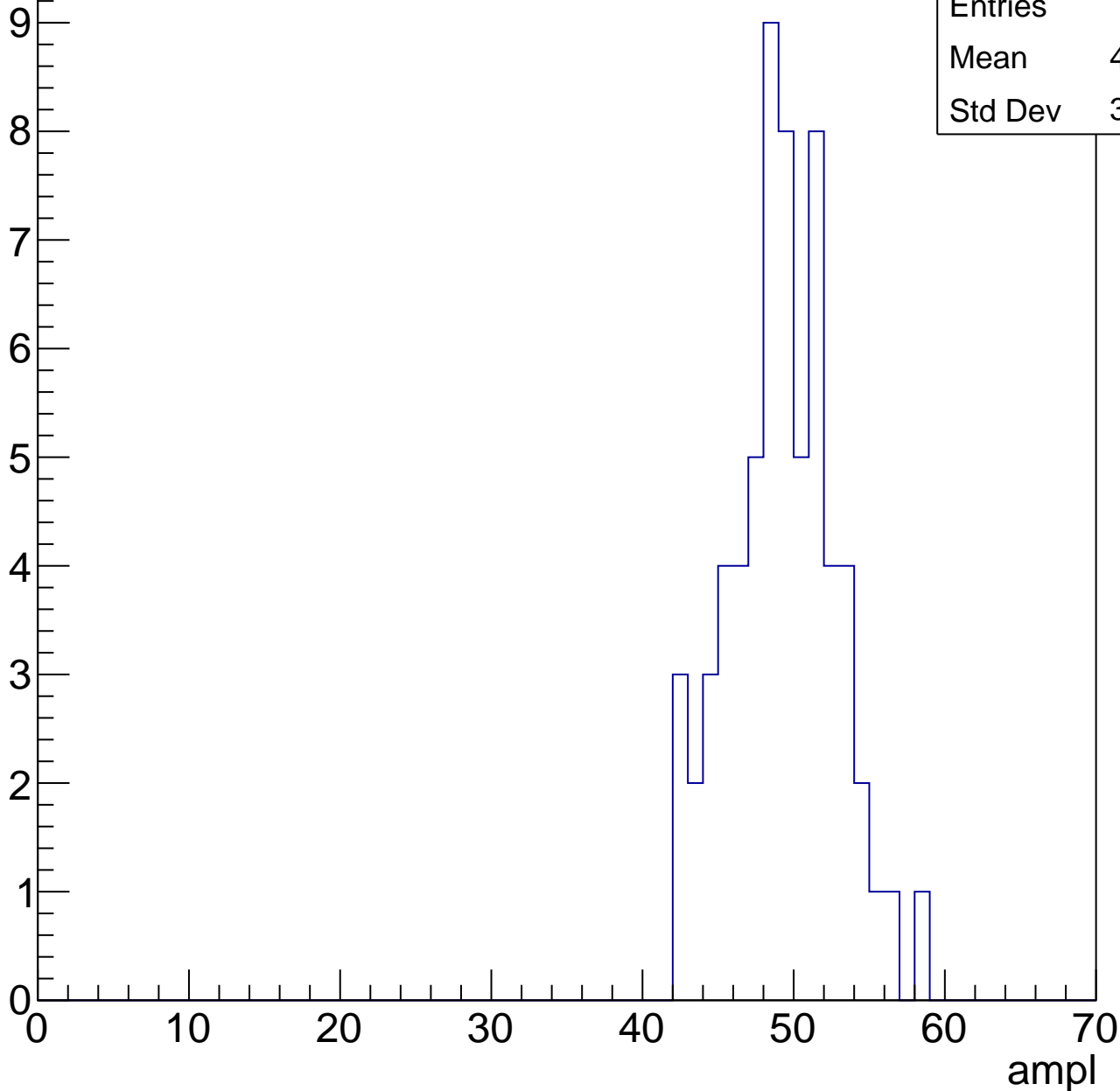


B1L100S, U15-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	48.78
Std Dev	3.475

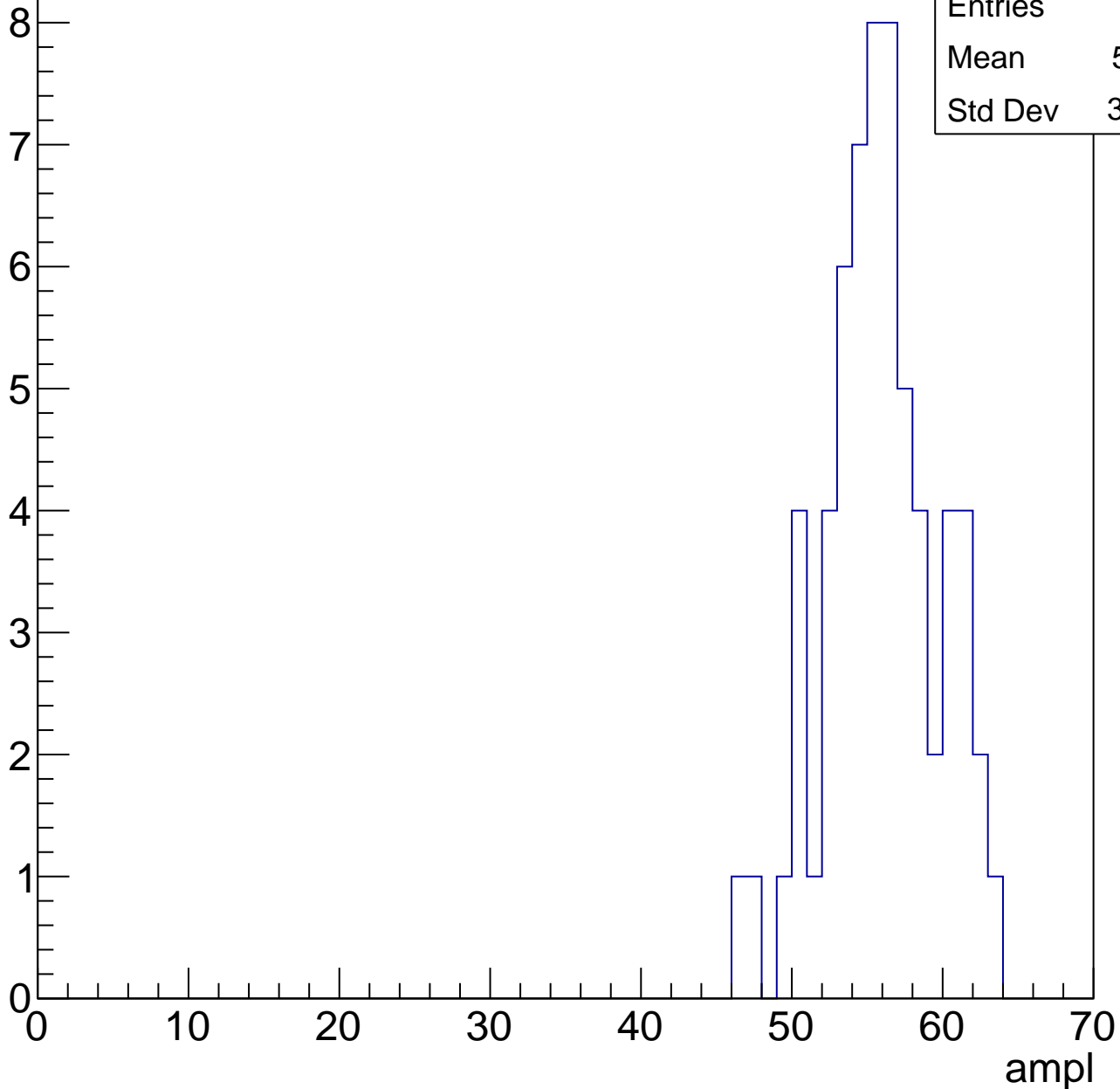


B1L100S, U15-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	55.41
Std Dev	3.685

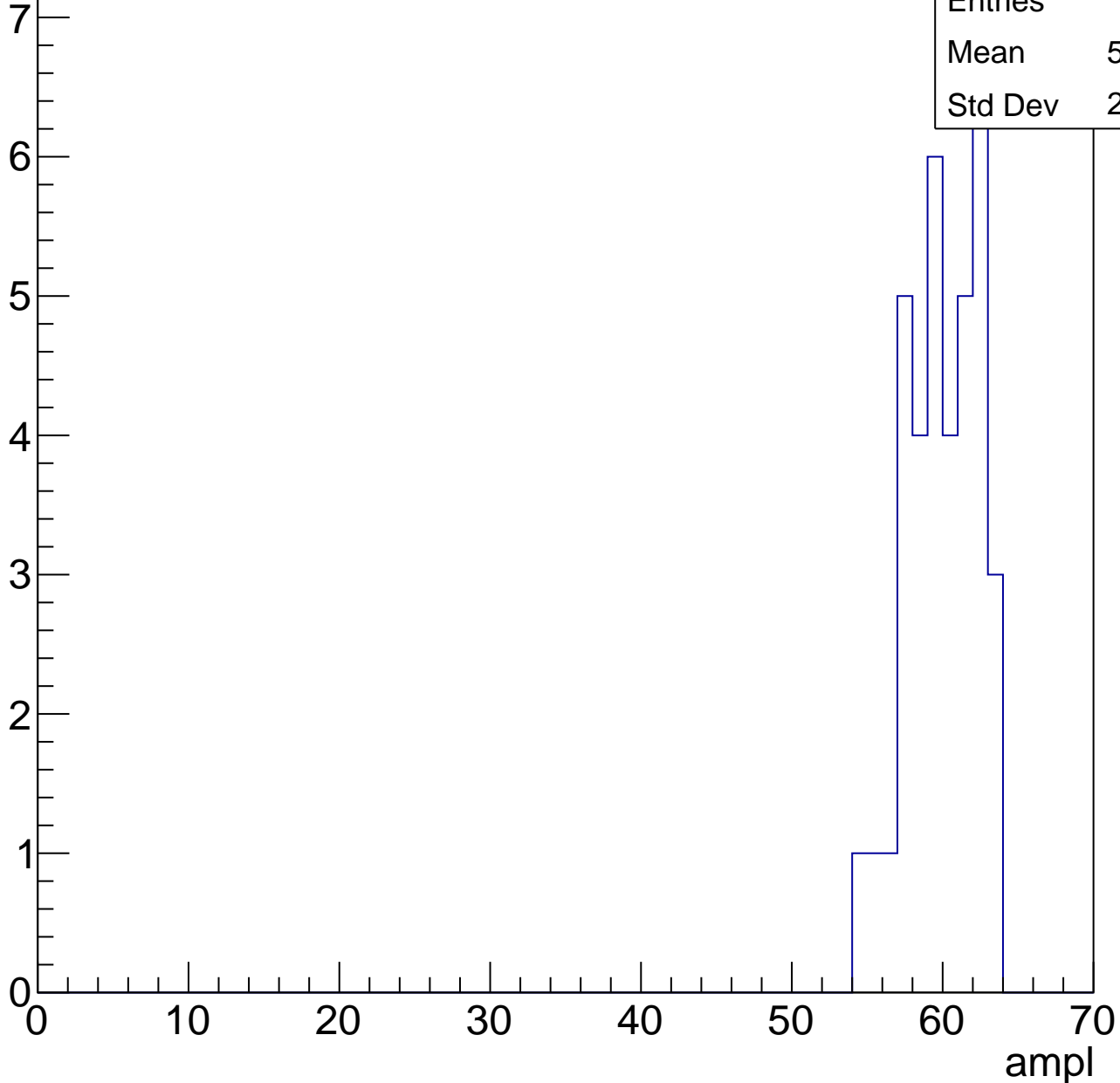


B1L100S, U15-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	59.57
Std Dev	2.308

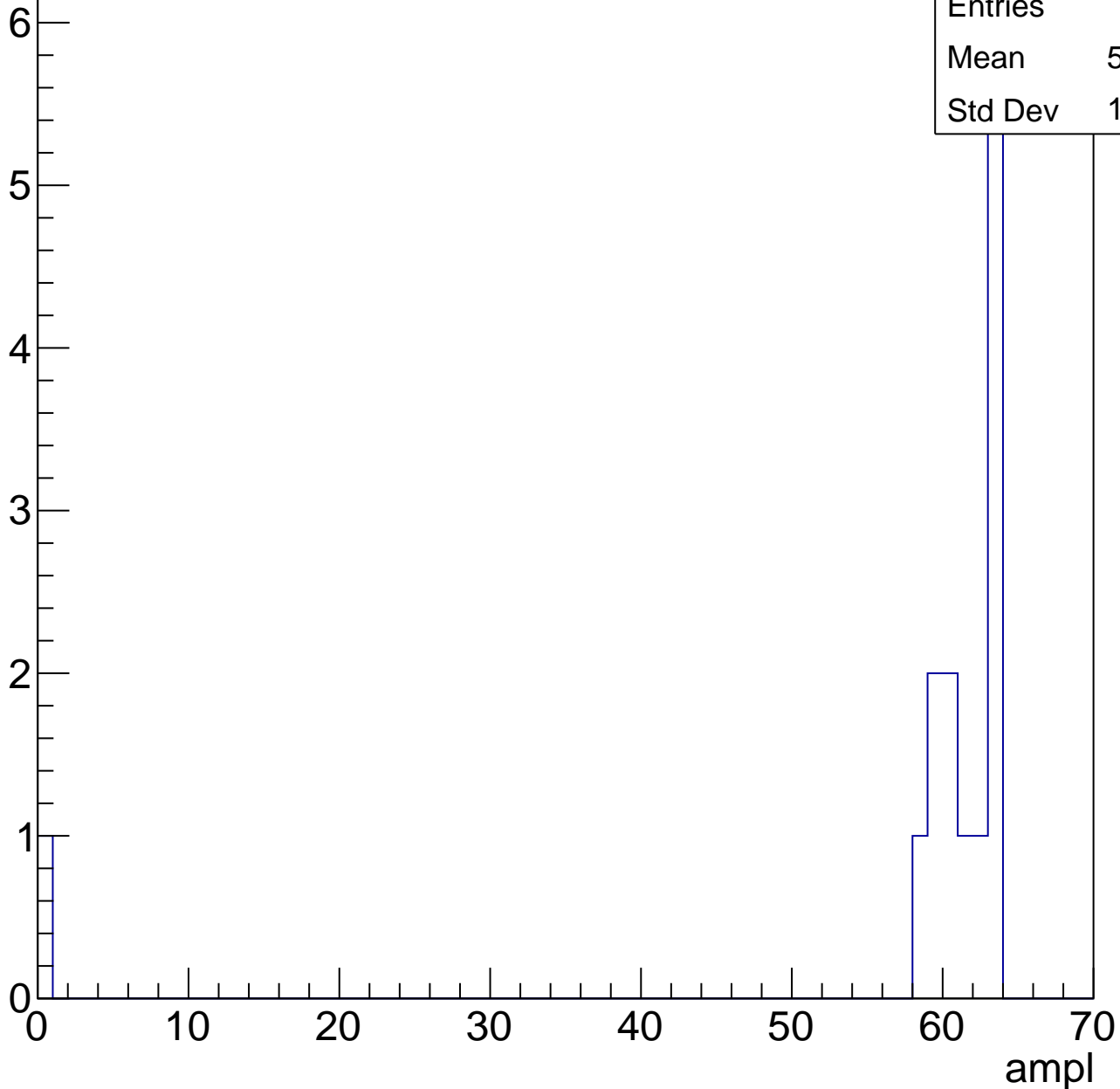


B1L100S, U15-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	56.93
Std Dev	15.89



B1L100S, U15-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch124, adc0

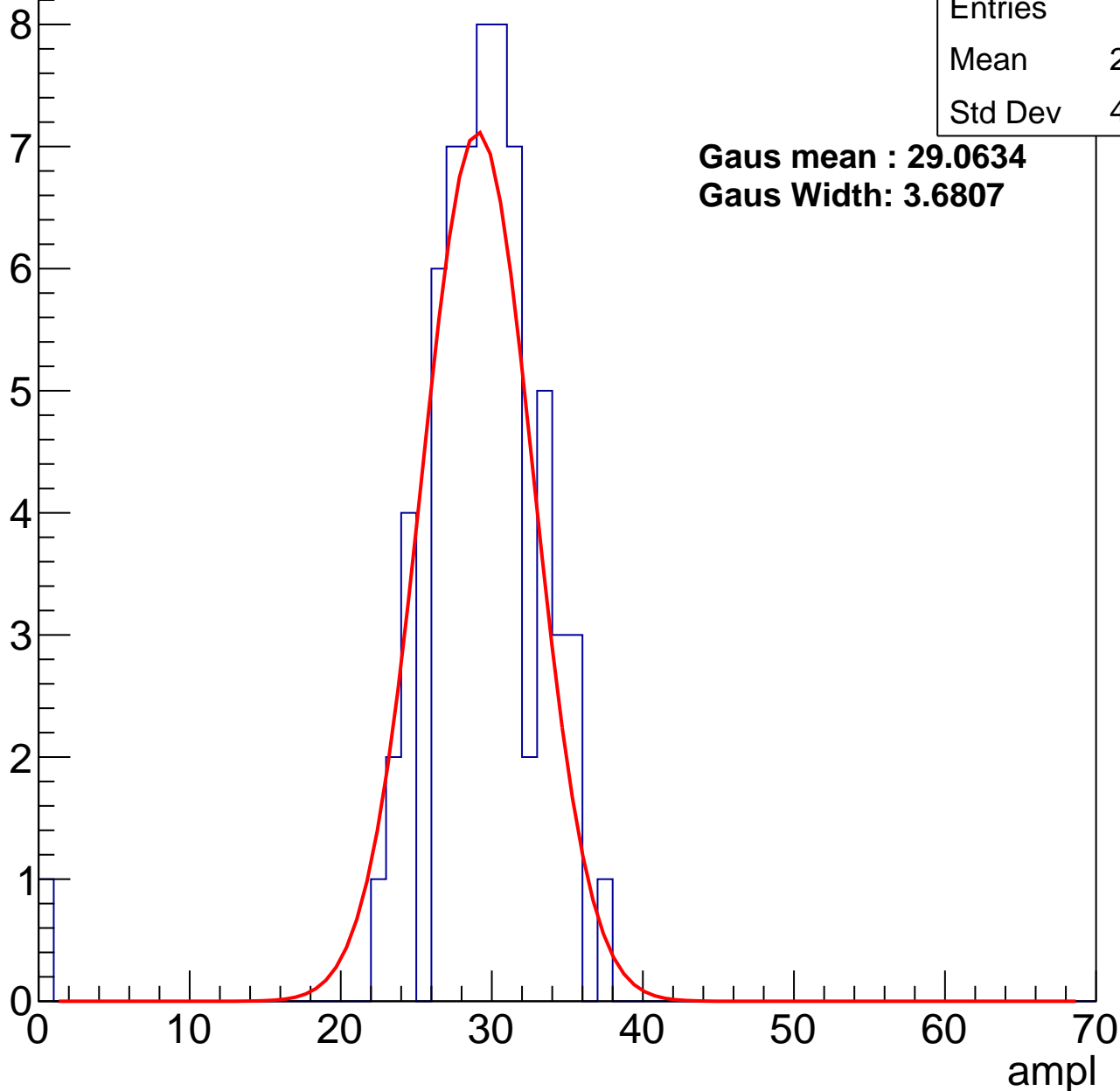
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.72
Std Dev	4.847

Gaus mean : 29.0634

Gaus Width: 3.6807



B1L100S, U15-ch124, adc1

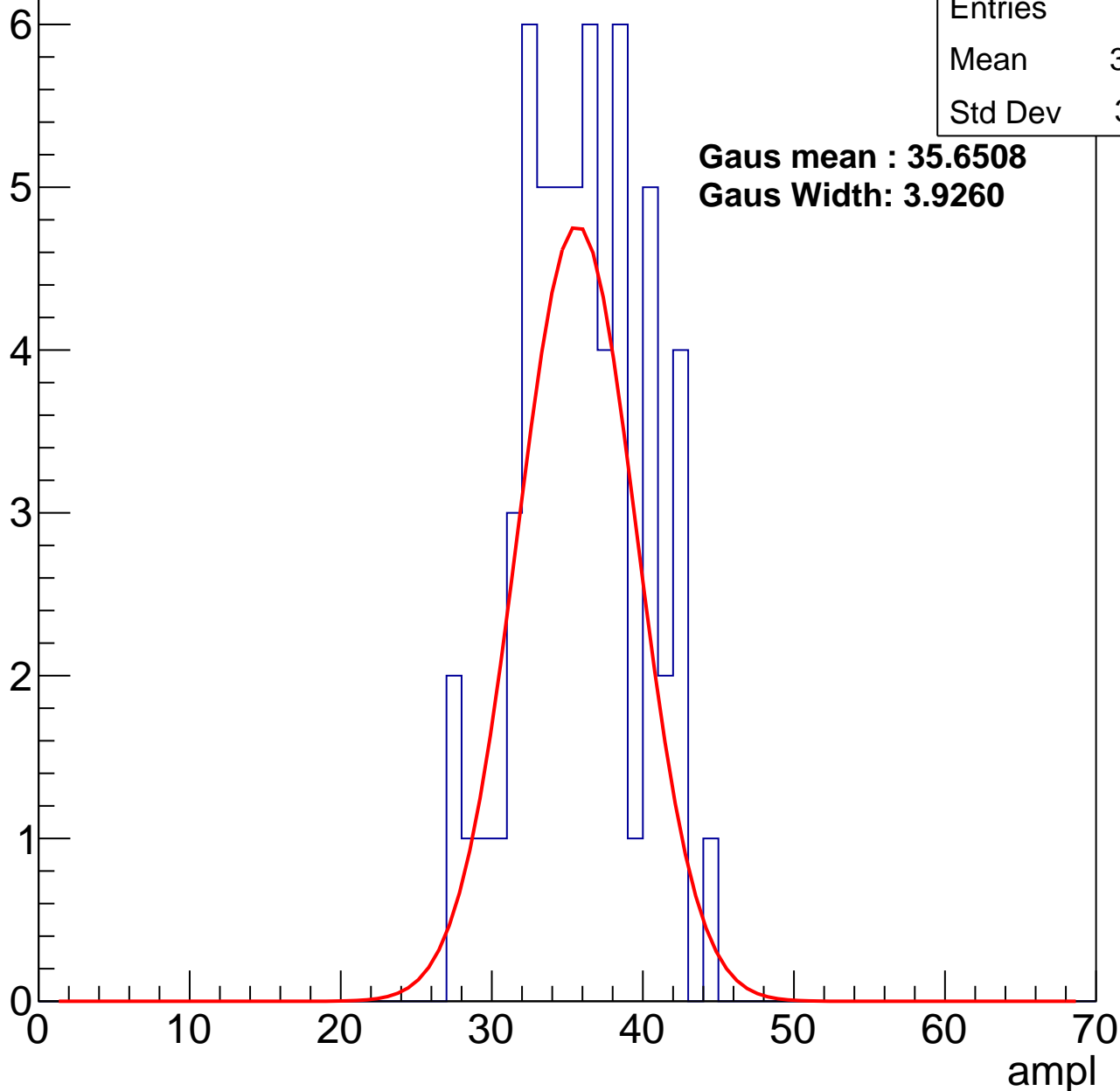
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	35.53
Std Dev	3.971

Gaus mean : 35.6508

Gaus Width: 3.9260



B1L100S, U15-ch124, adc2

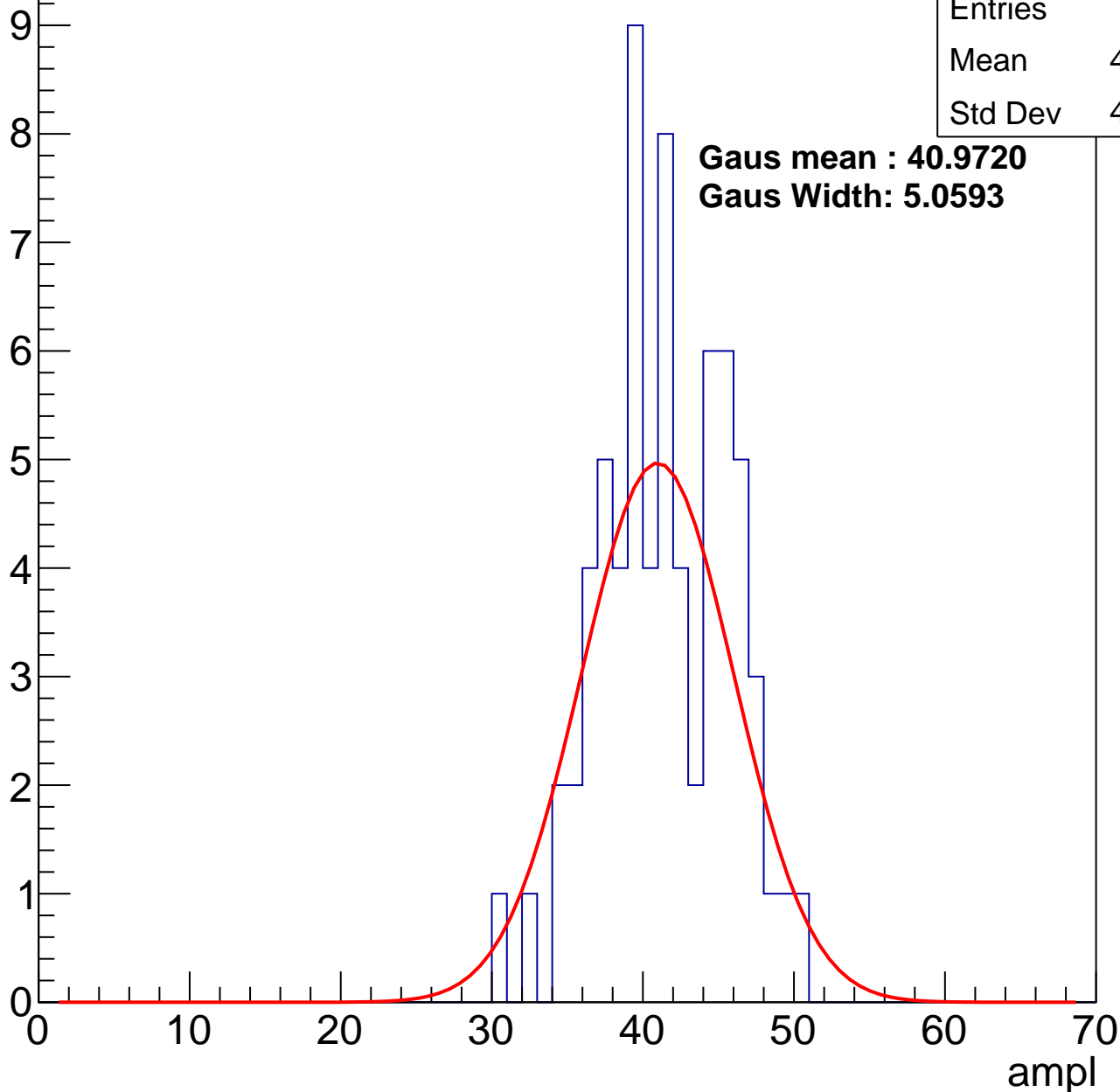
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	40.96
Std Dev	4.199

Gaus mean : 40.9720

Gaus Width: 5.0593

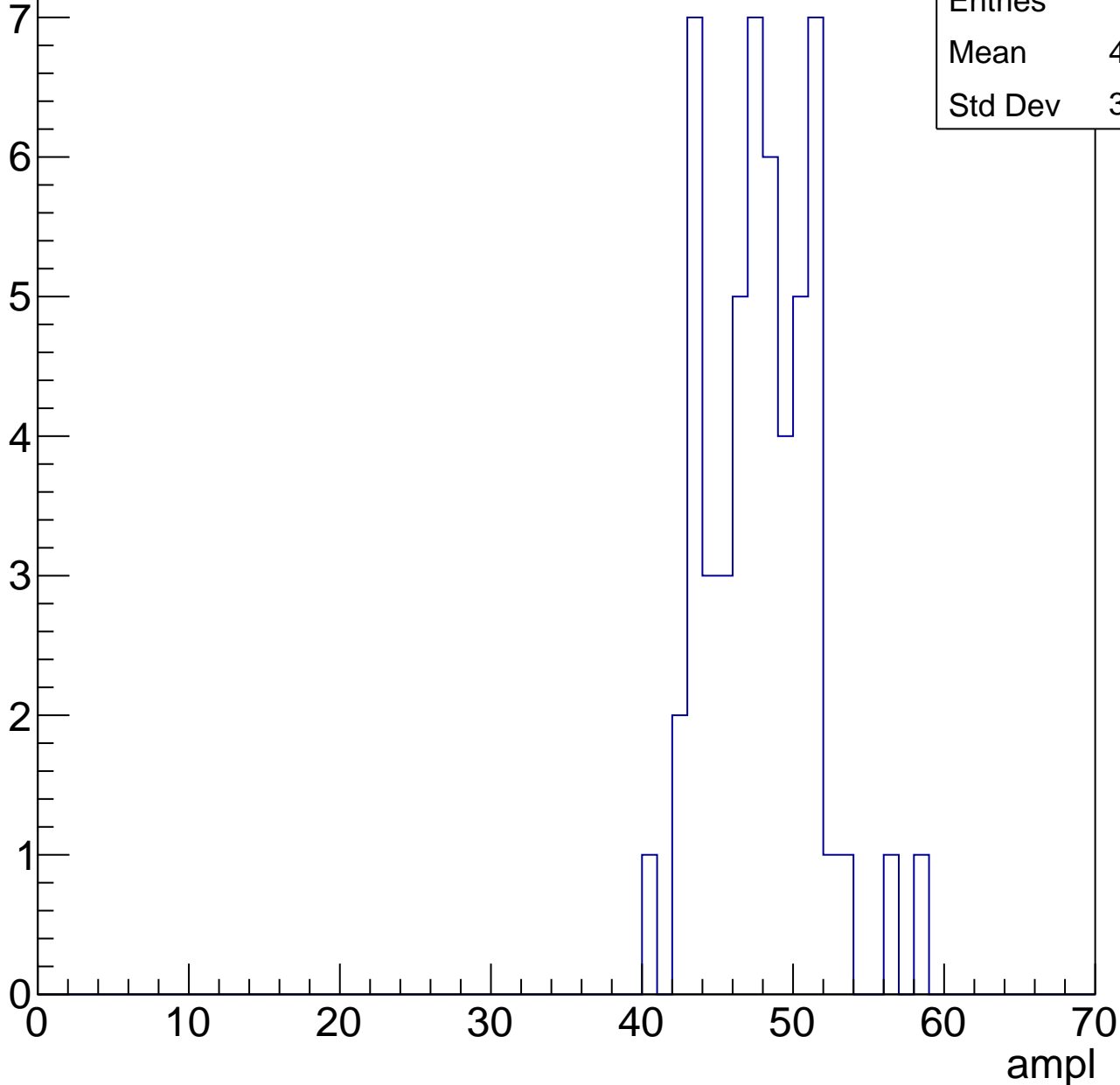


B1L100S, U15-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	47.43
Std Dev	3.567

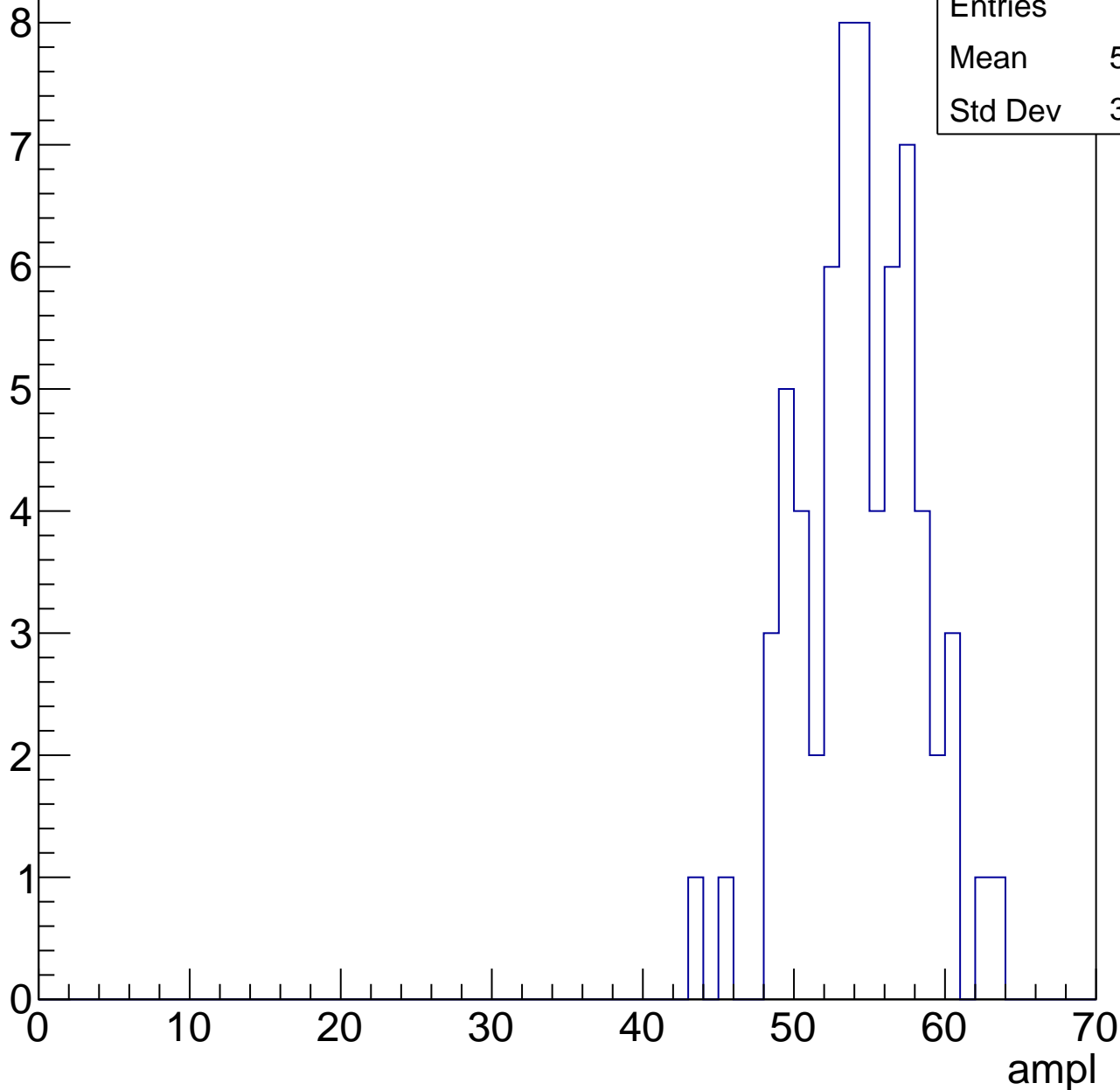


B1L100S, U15-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	53.89
Std Dev	3.905

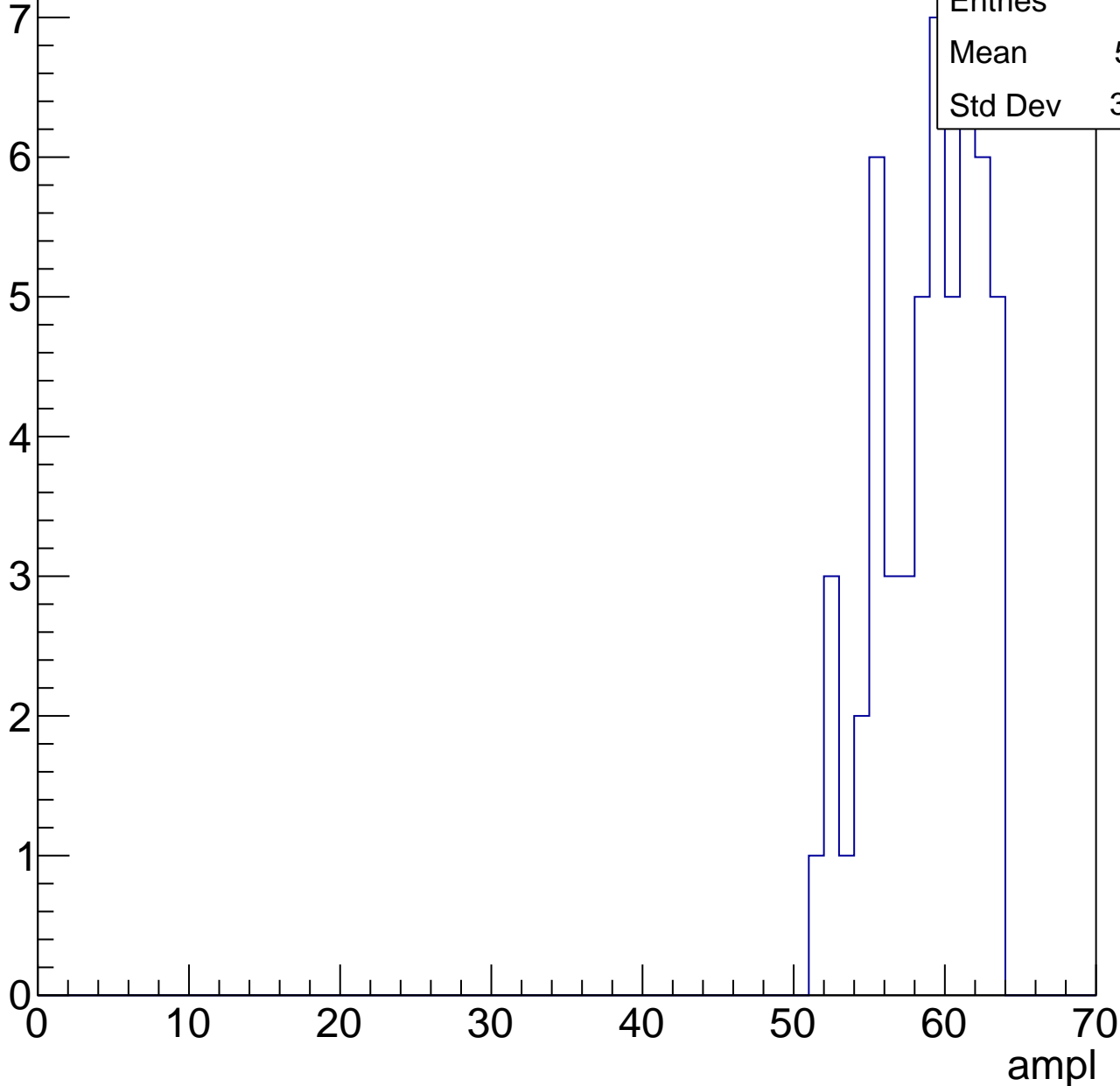


B1L100S, U15-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

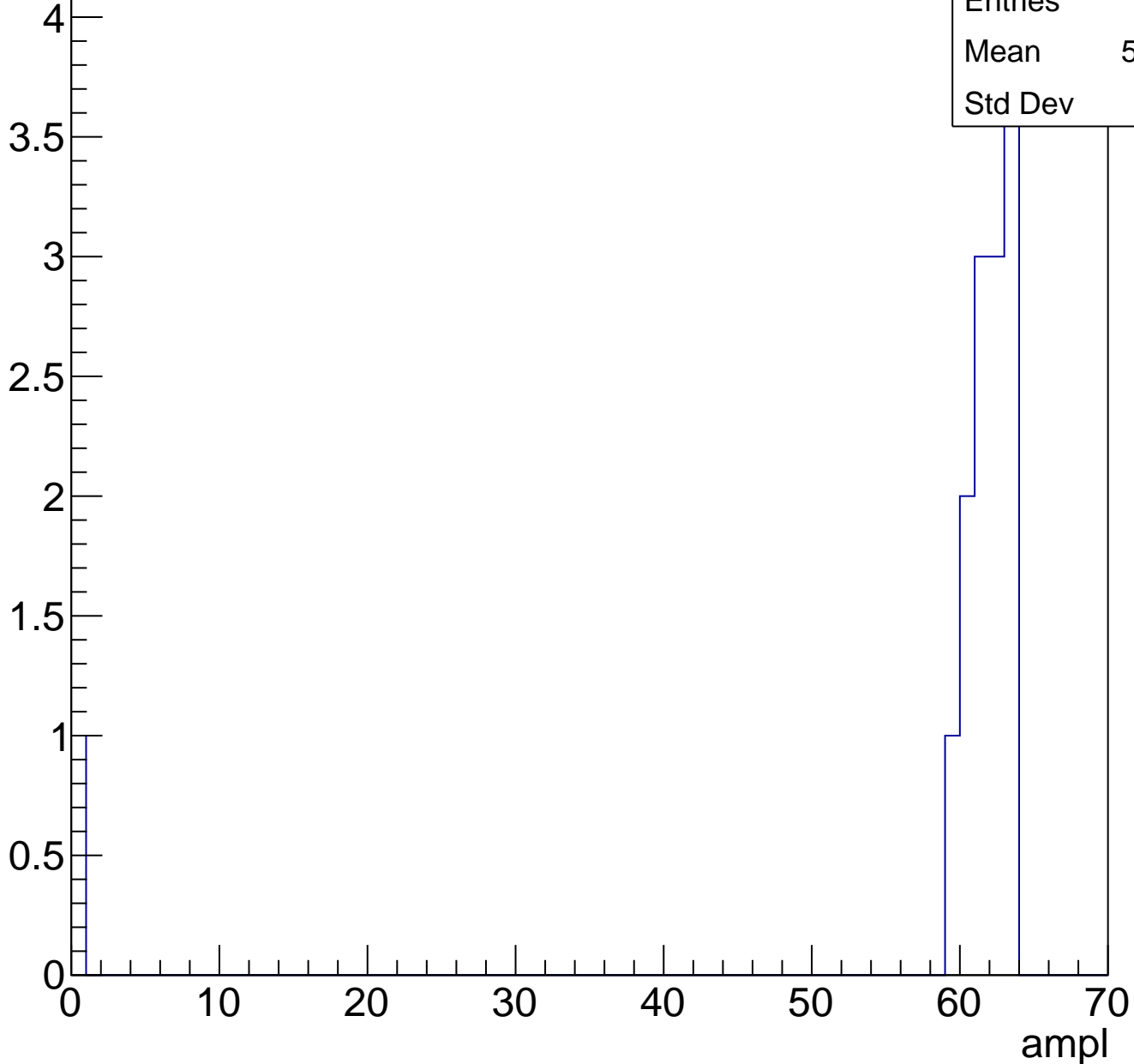
Entries	54
Mean	58.41
Std Dev	3.292



B1L100S, U15-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U15-ch125, adc0

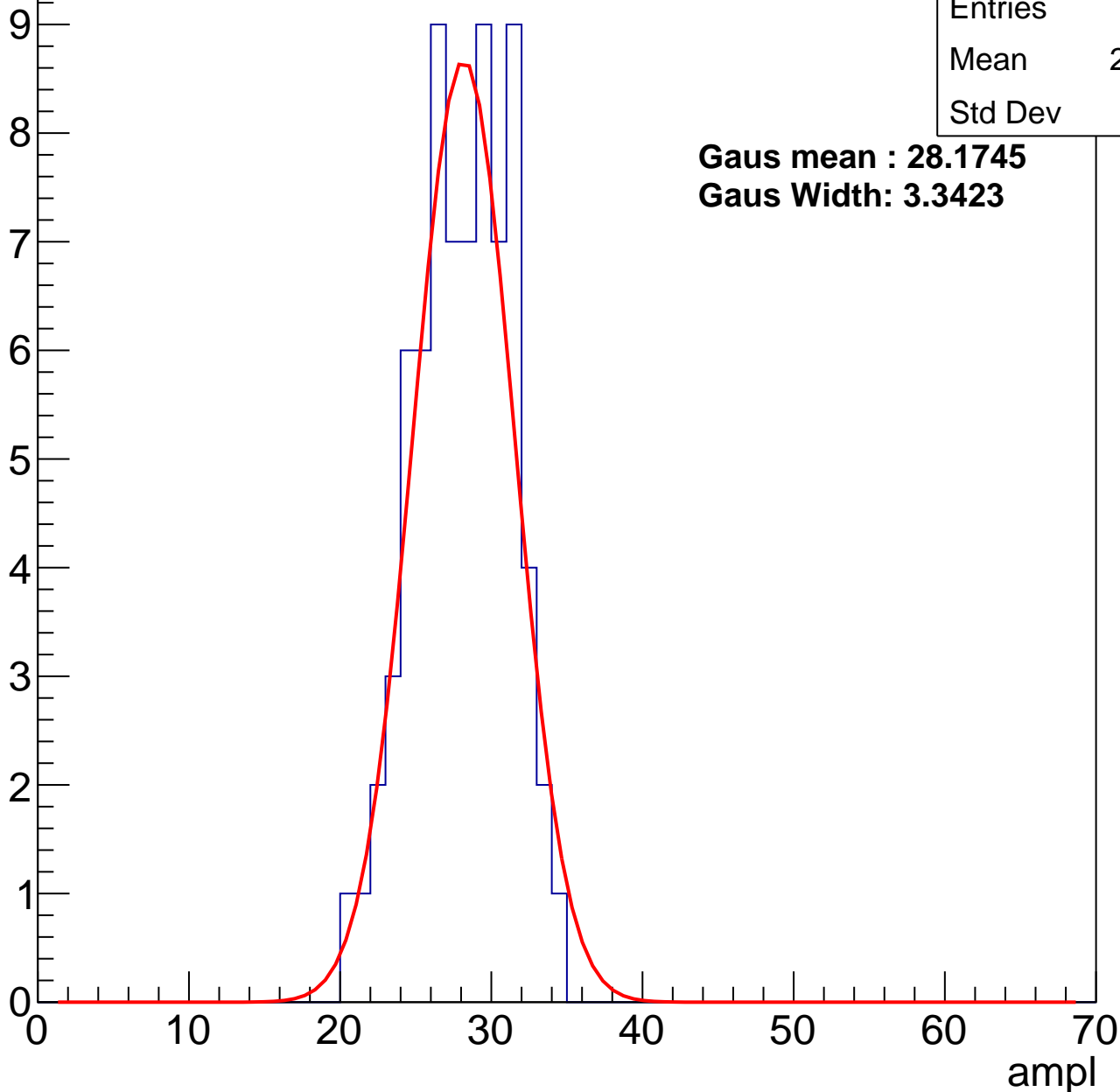
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	27.64
Std Dev	3.1

Gaus mean : 28.1745

Gaus Width: 3.3423



B1L100S, U15-ch125, adc1

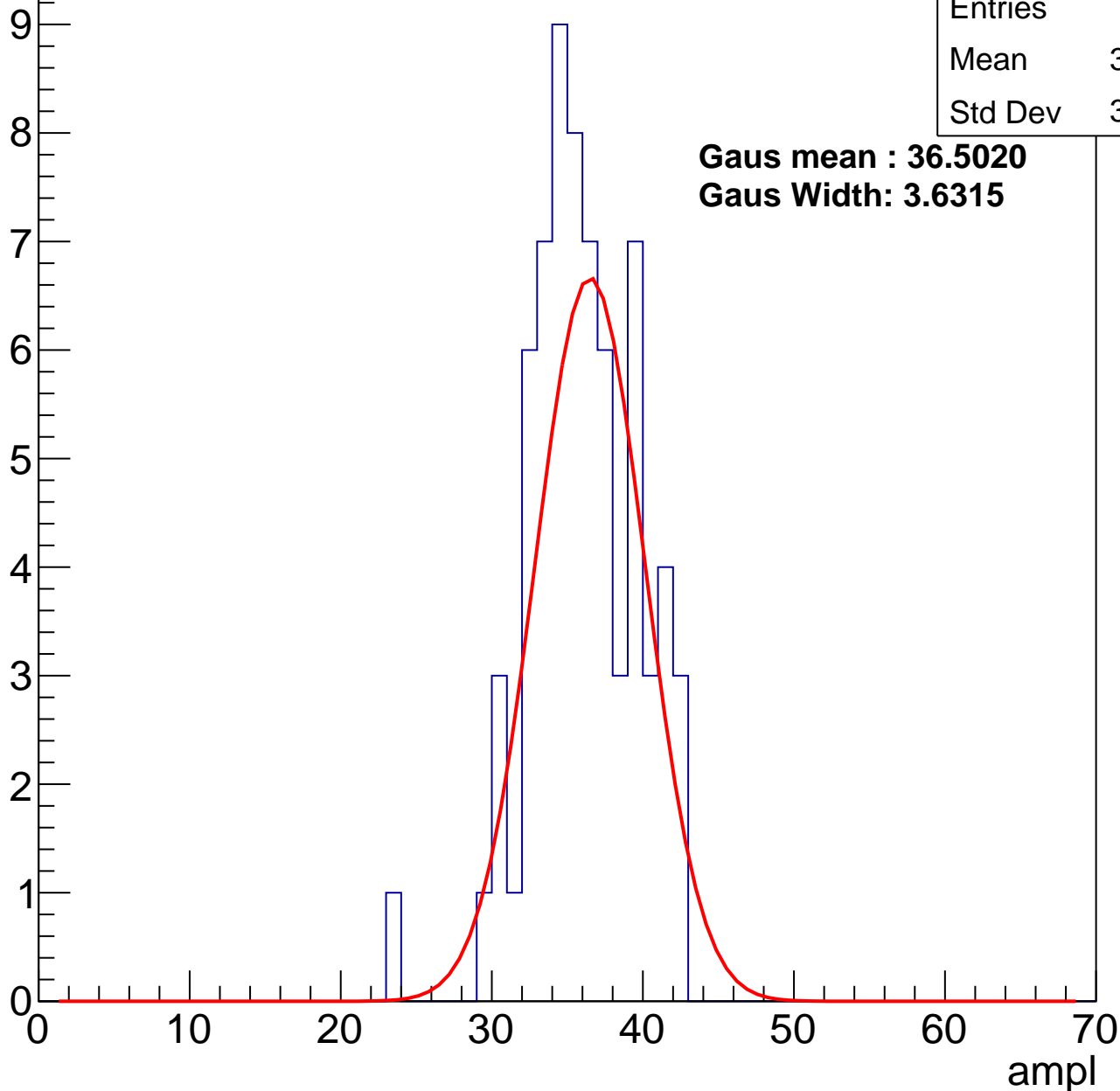
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	35.55
Std Dev	3.565

Gaus mean : 36.5020

Gaus Width: 3.6315

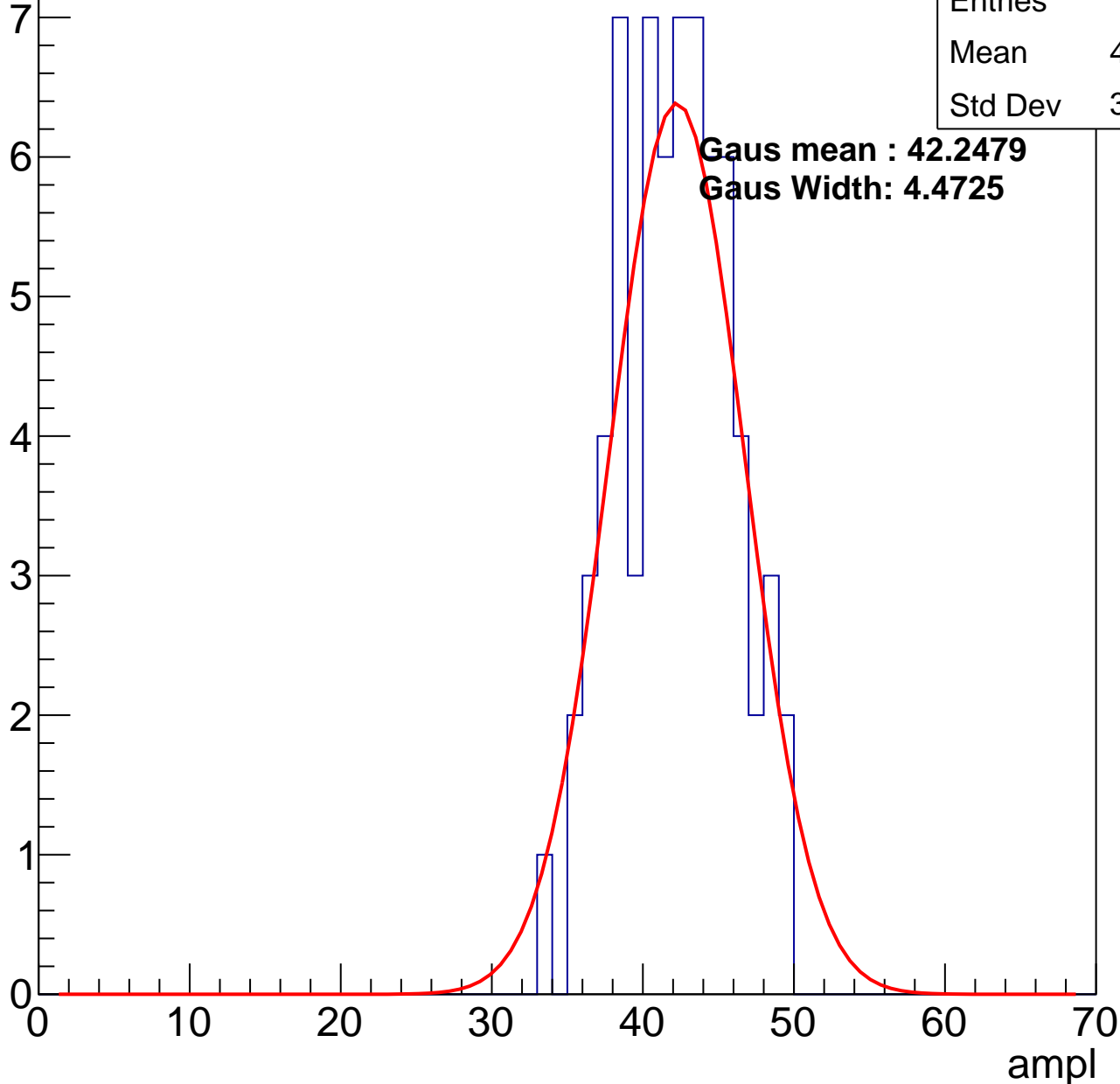


B1L100S, U15-ch125, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	41.67
Std Dev	3.698

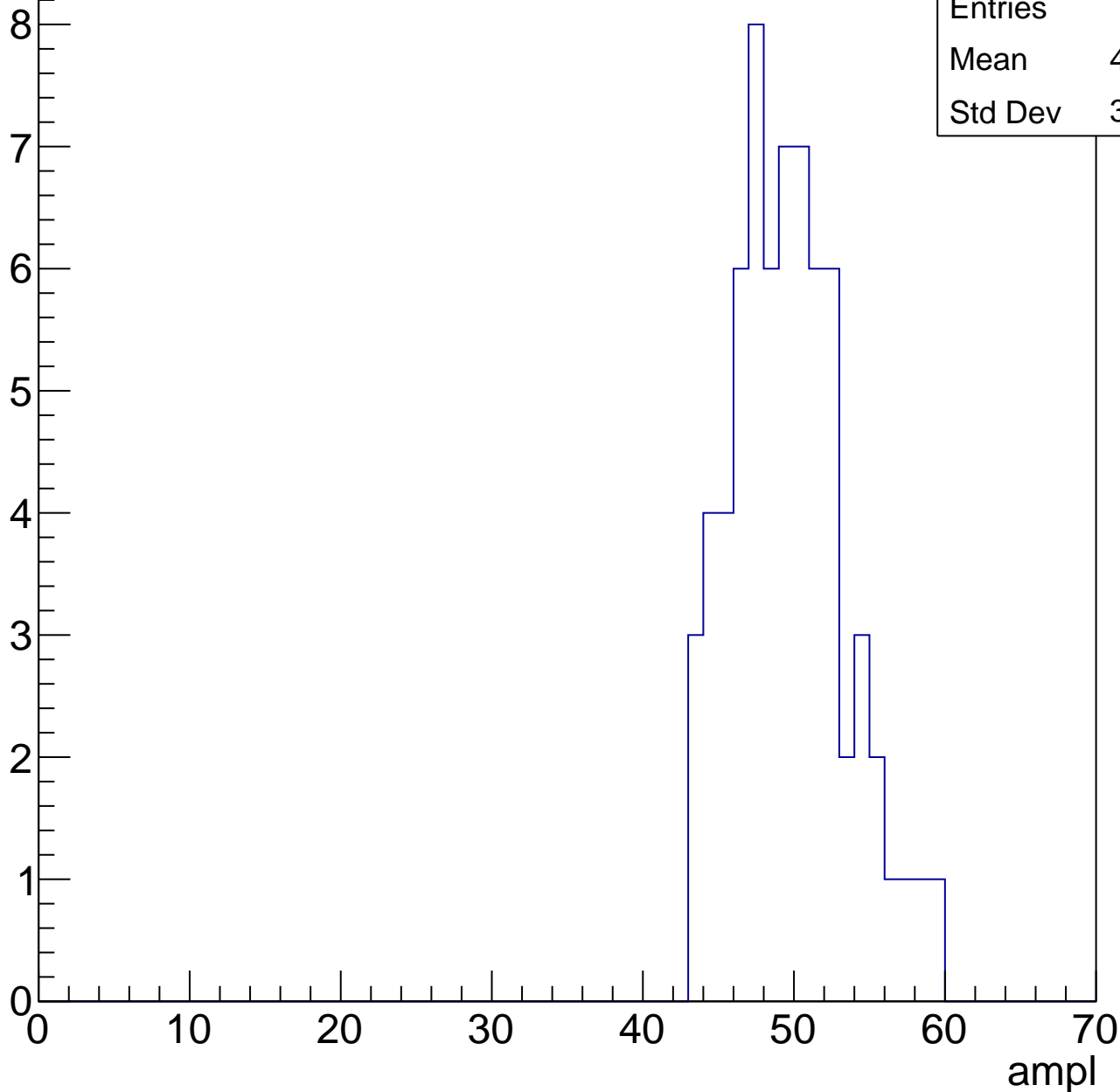


B1L100S, U15-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	49.18
Std Dev	3.678

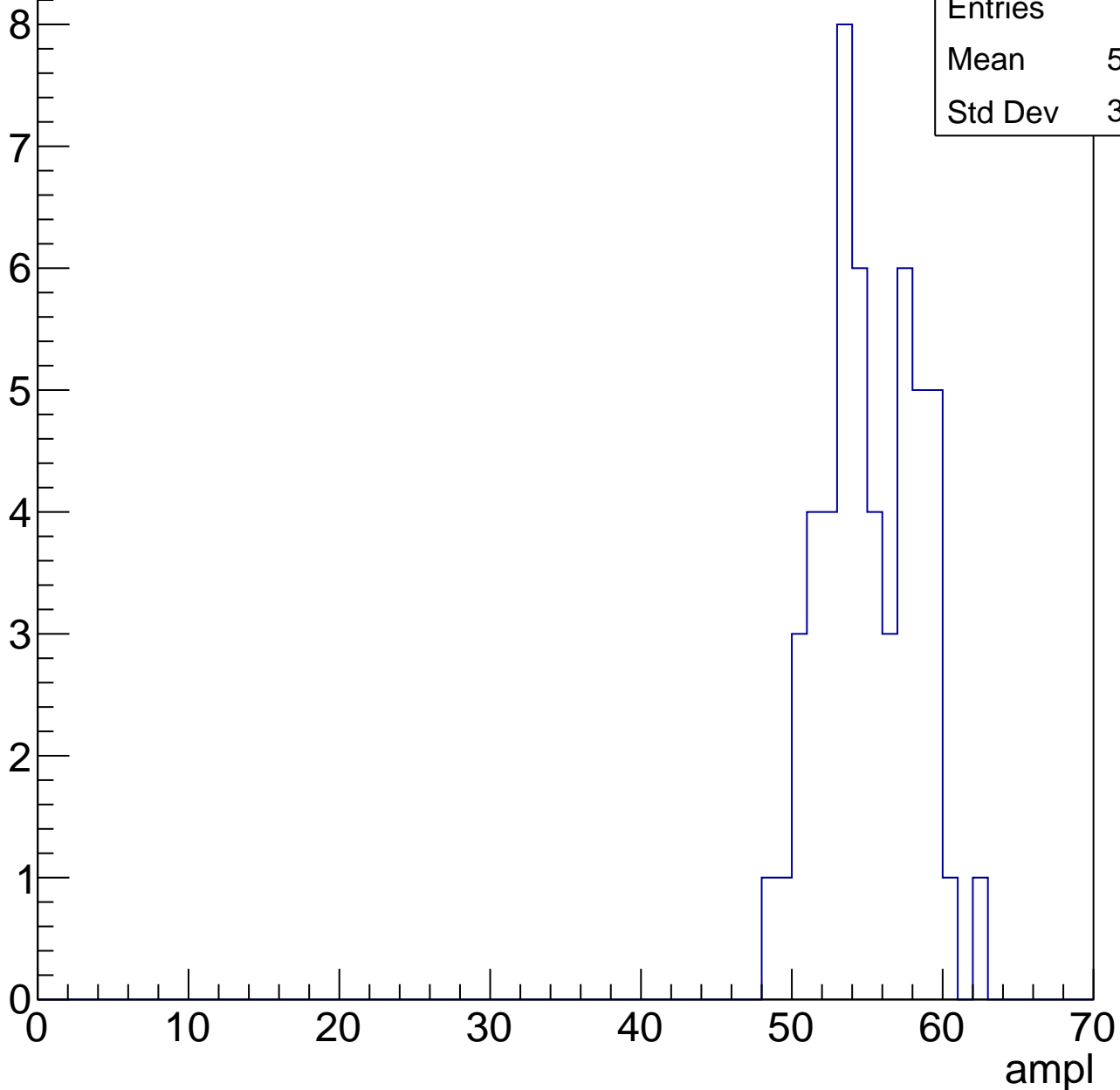


B1L100S, U15-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	54.69
Std Dev	3.159

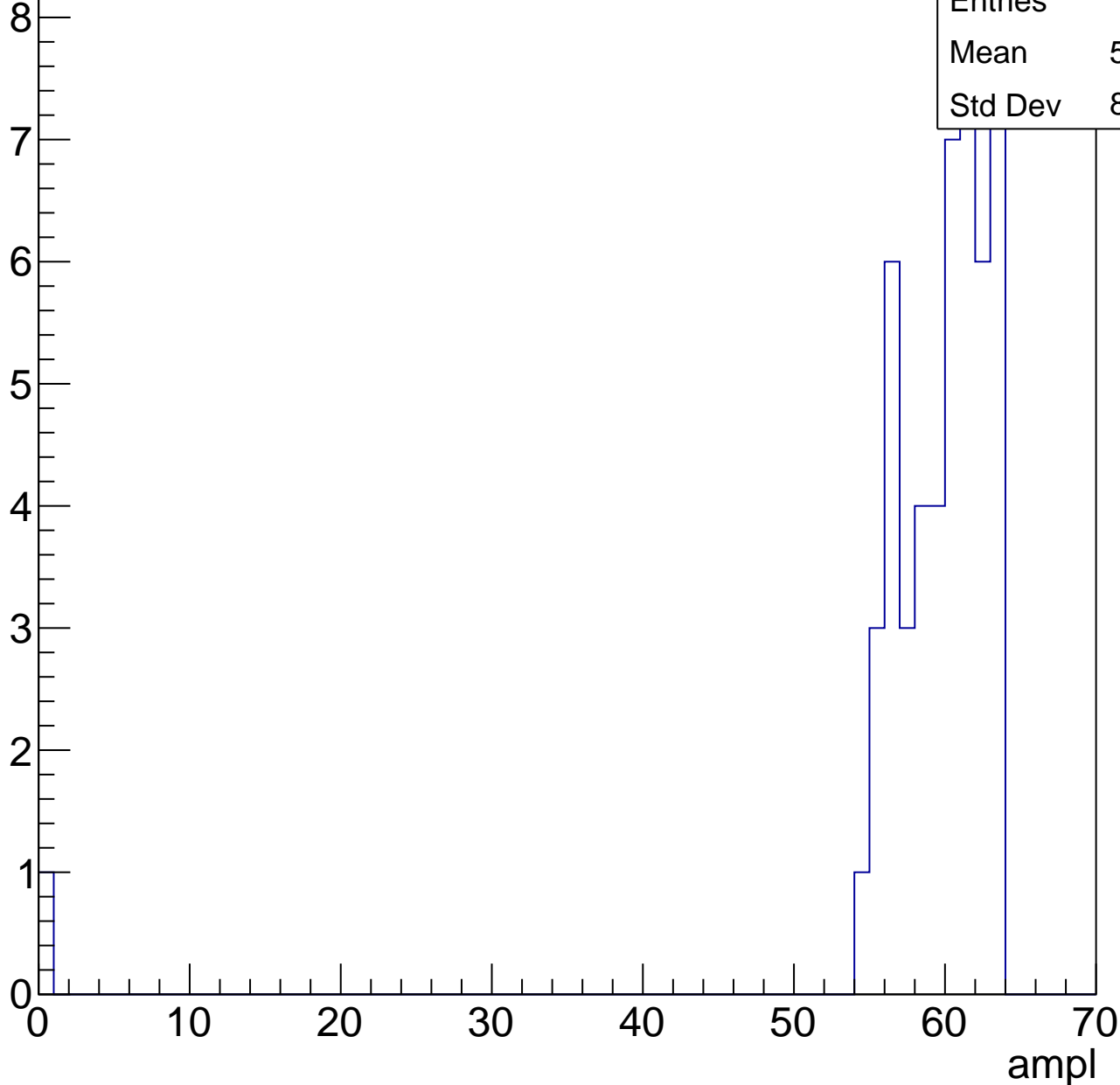


B1L100S, U15-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

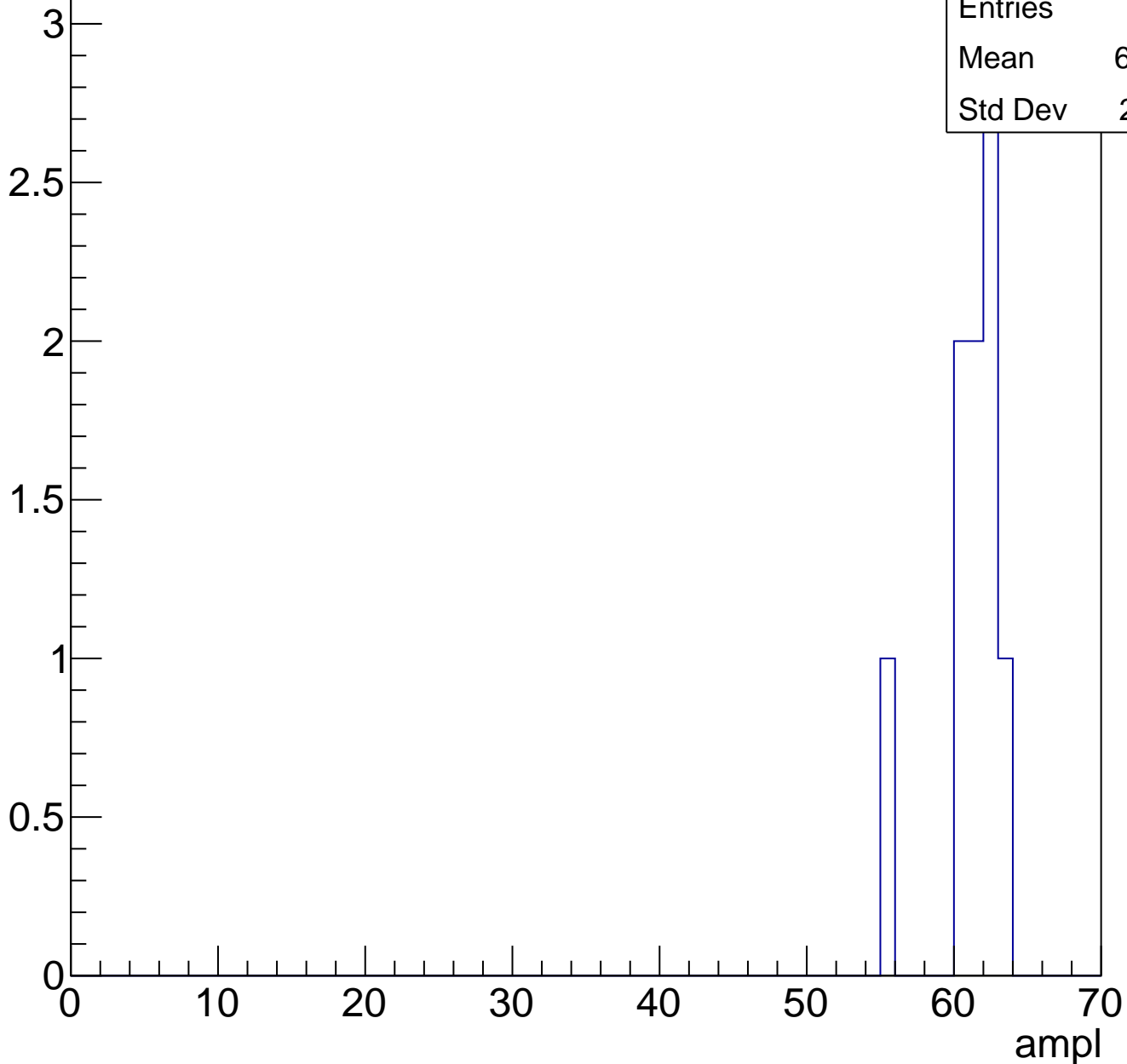
Entries	51
Mean	58.39
Std Dev	8.662



B1L100S, U15-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch126, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	103
Mean	28.18
Std Dev	6.441

Gaus mean : 29.7539

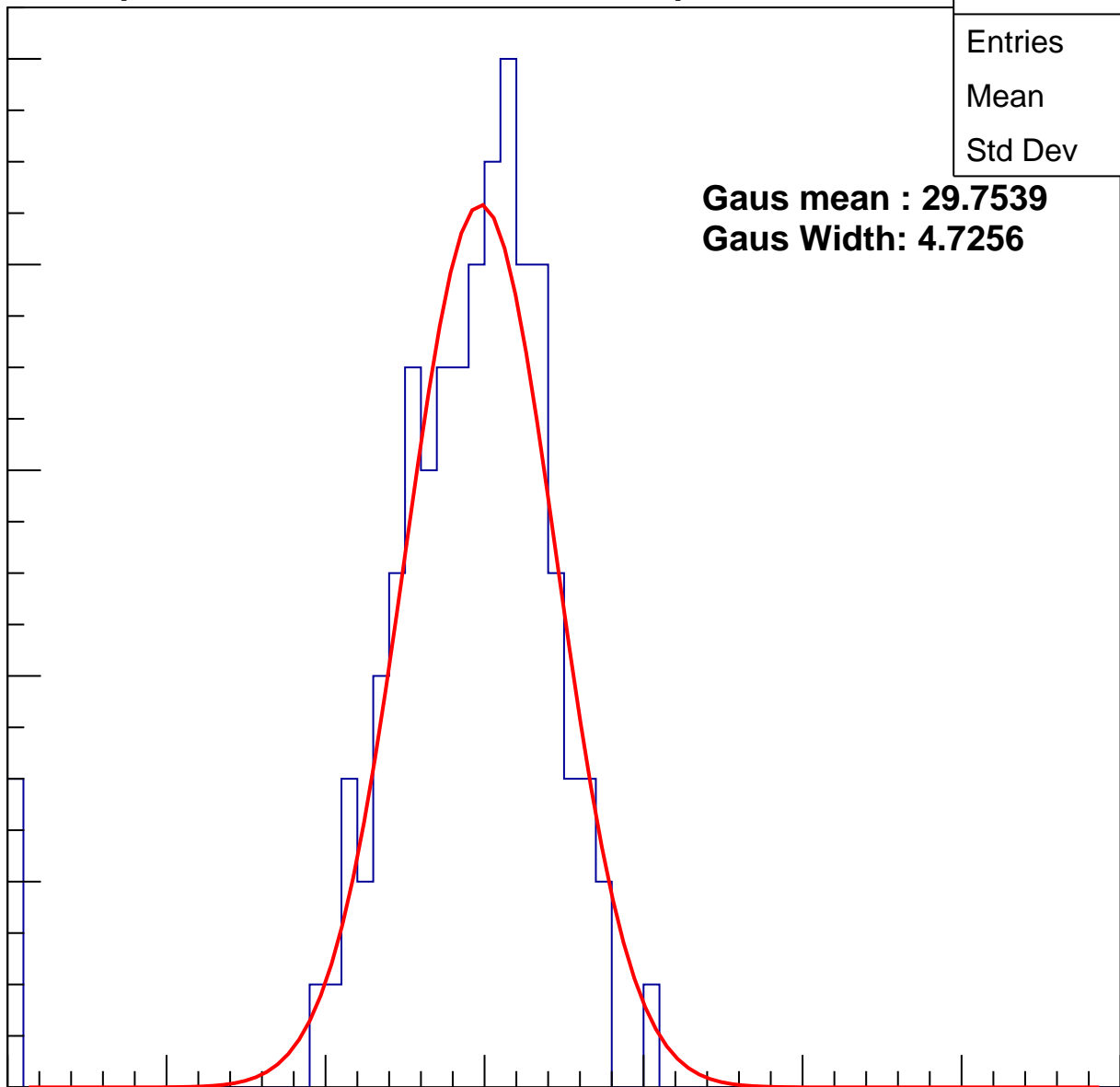
Gaus Width: 4.7256

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U15-ch126, adc1

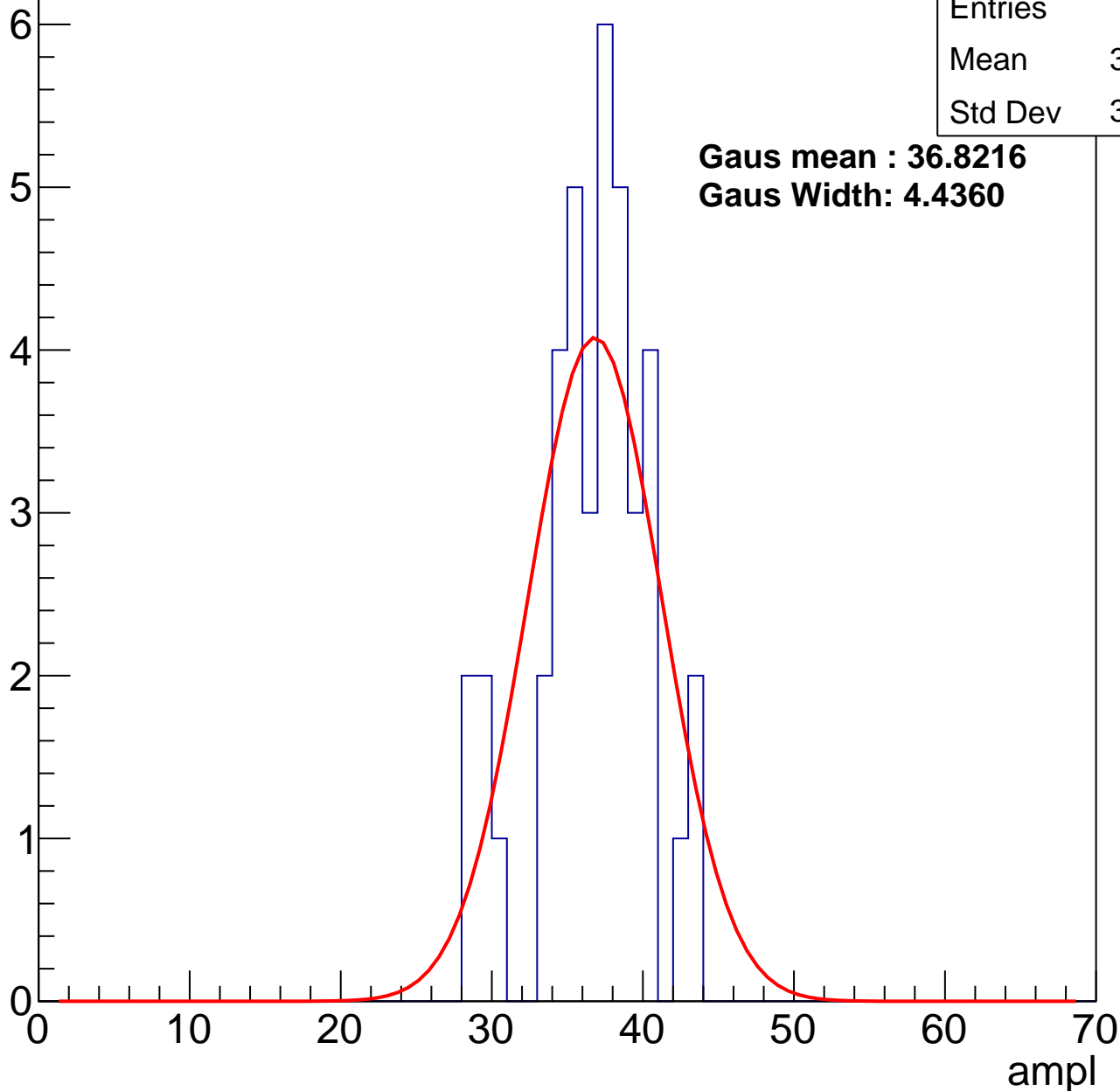
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	40
Mean	36.15
Std Dev	3.712

Gaus mean : 36.8216

Gaus Width: 4.4360



B1L100S, U15-ch126, adc2

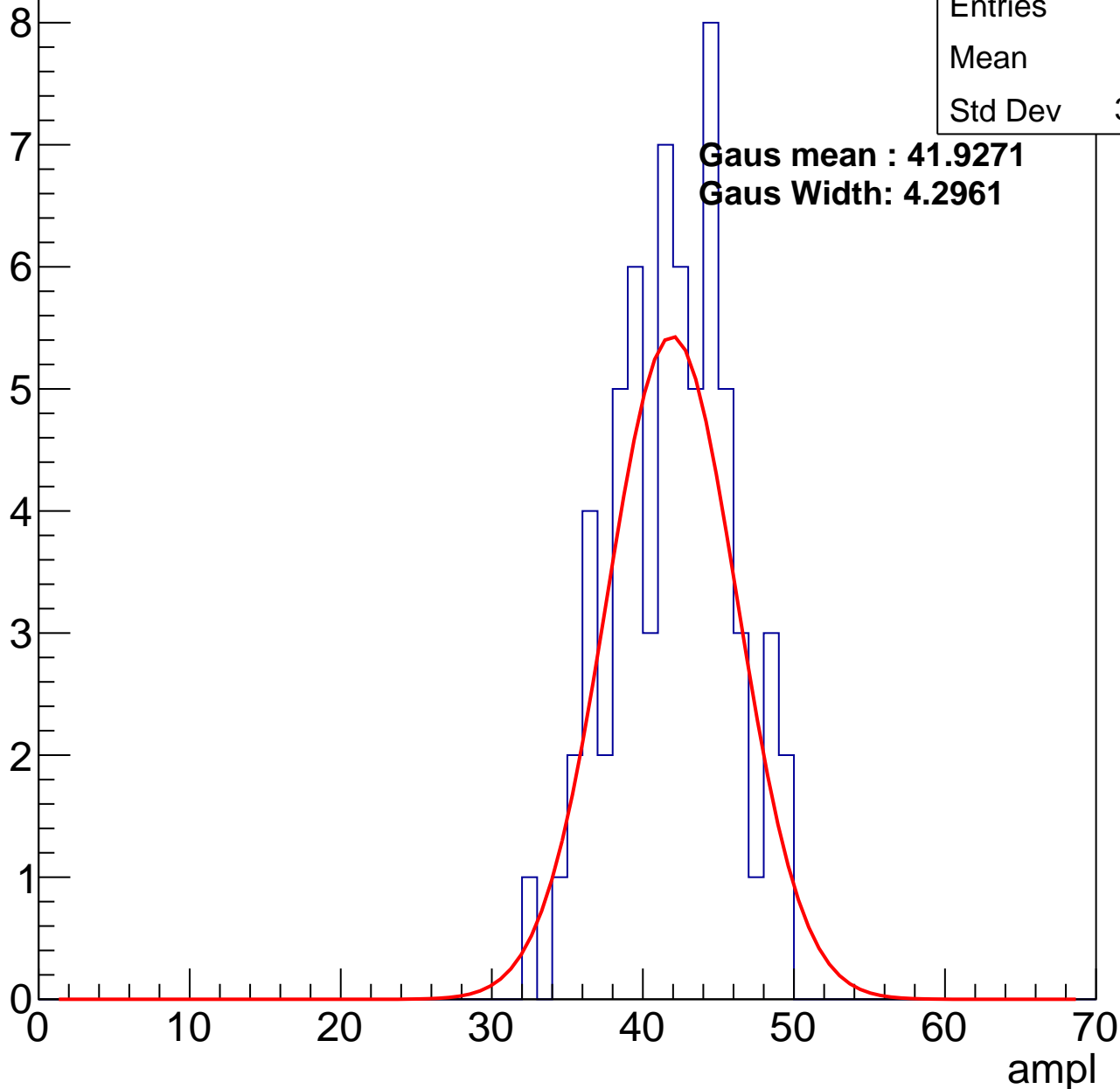
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.5
Std Dev	3.861

Gaus mean : 41.9271

Gaus Width: 4.2961

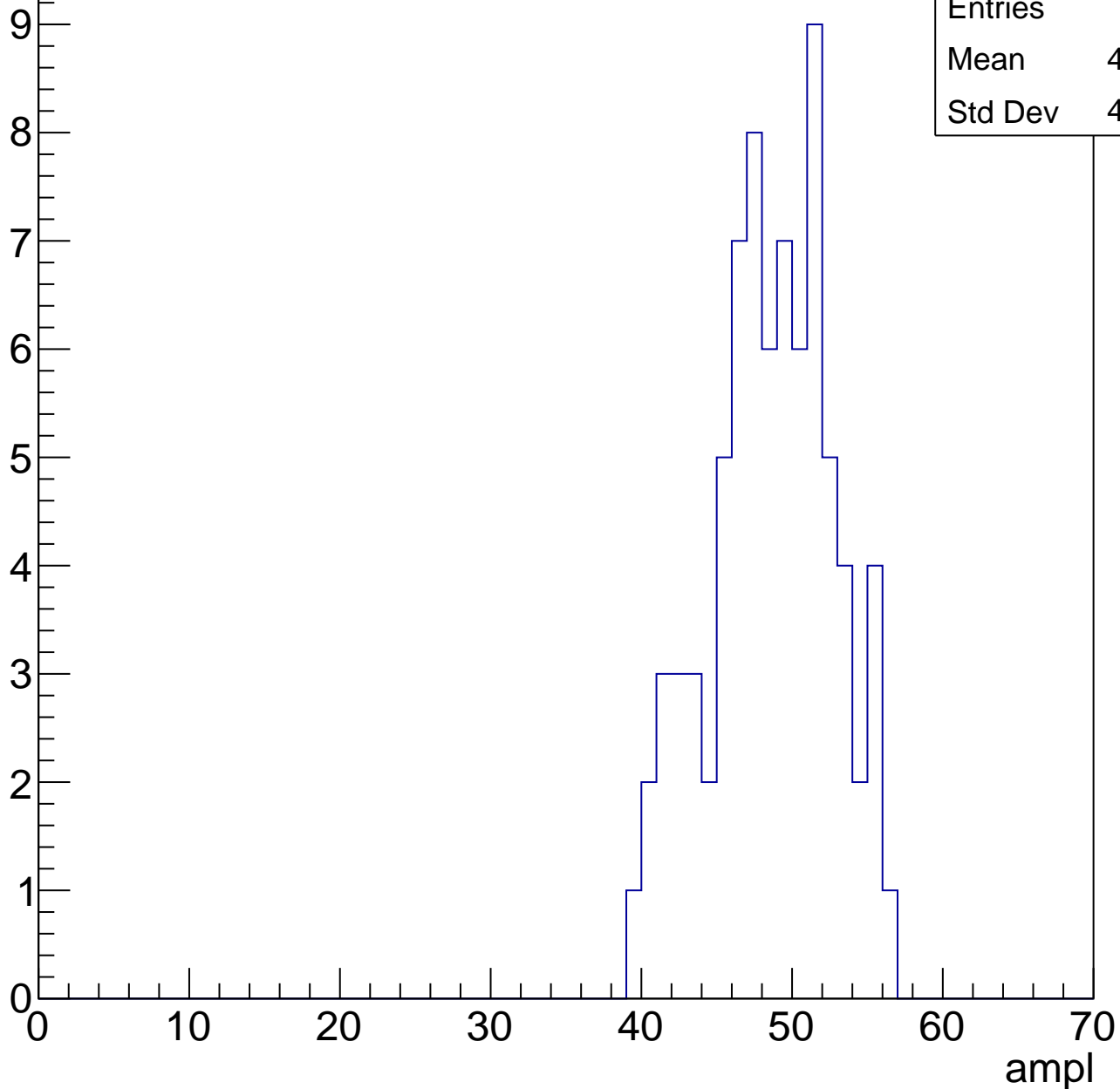


B1L100S, U15-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	48.13
Std Dev	4.043

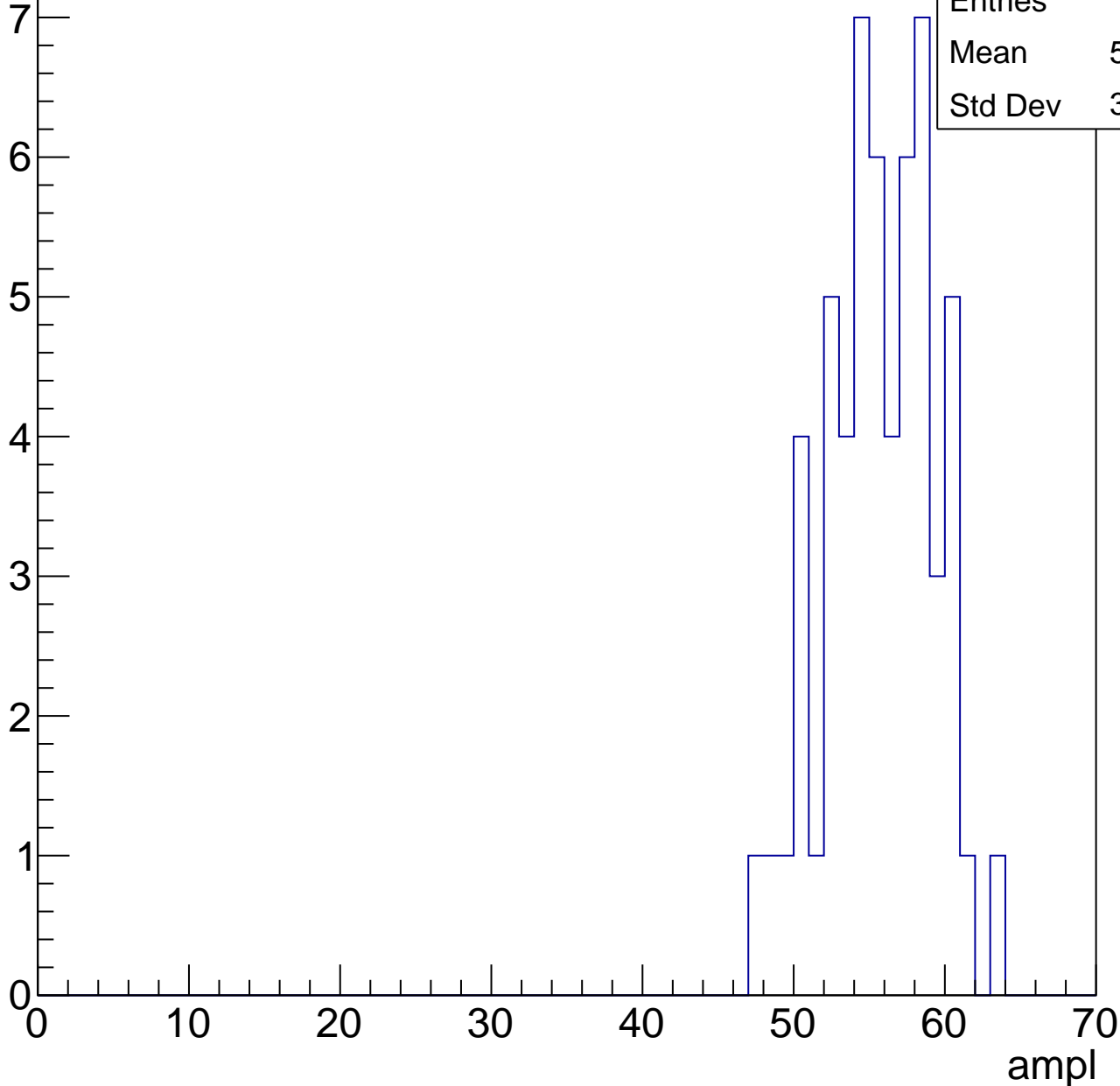


B1L100S, U15-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	55.23
Std Dev	3.489

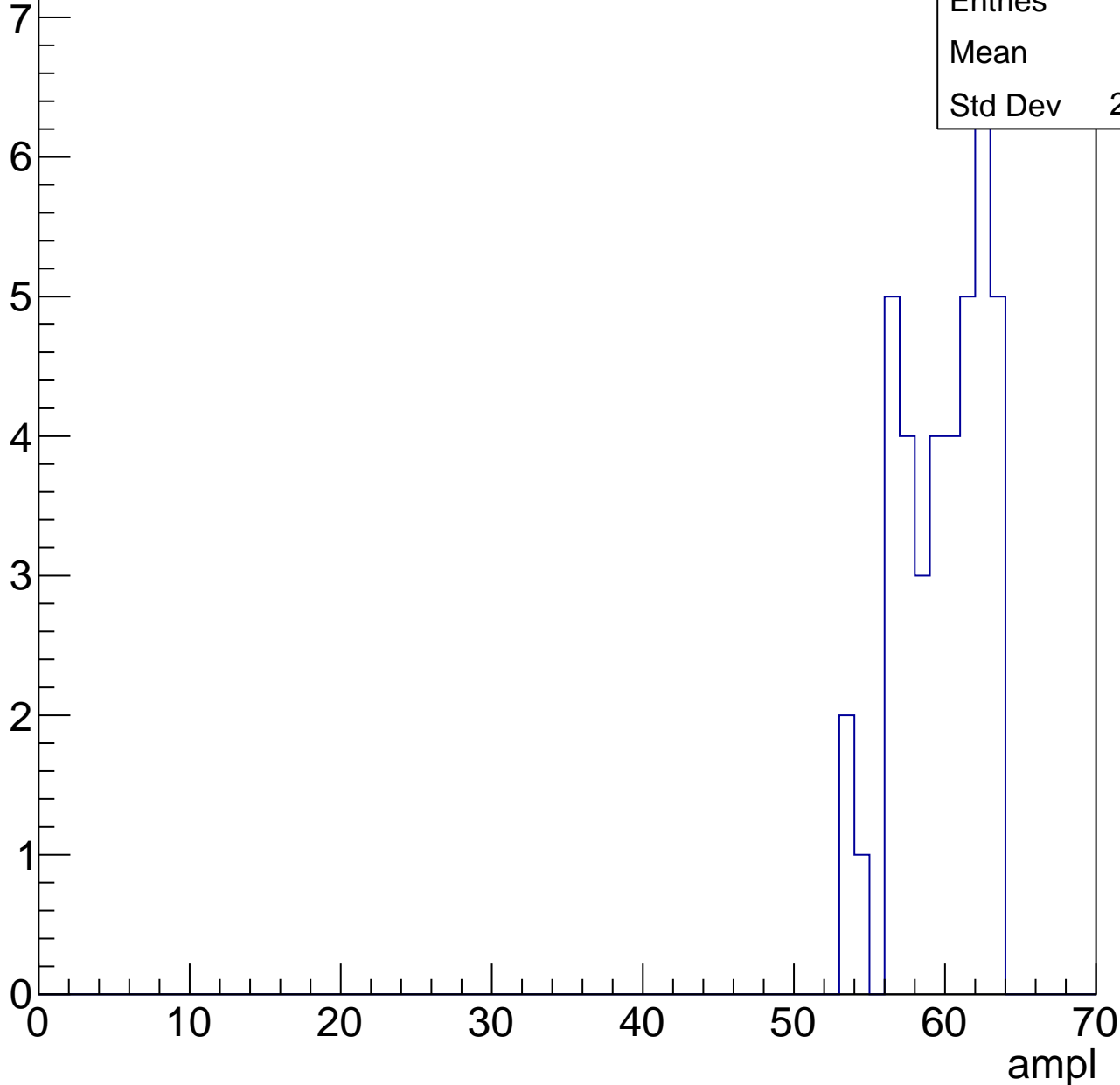


B1L100S, U15-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

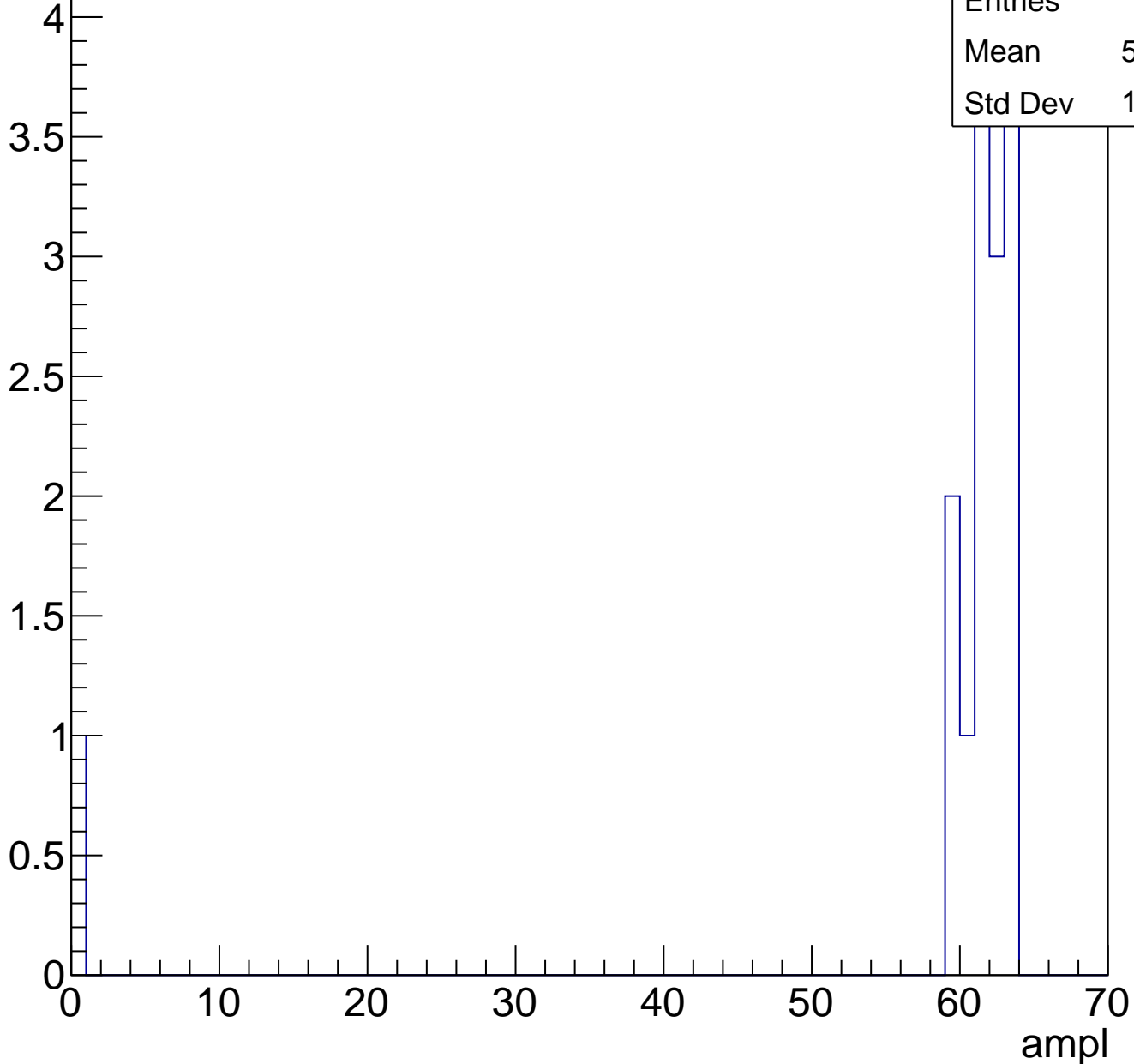
Entries	40
Mean	59.3
Std Dev	2.848



B1L100S, U15-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

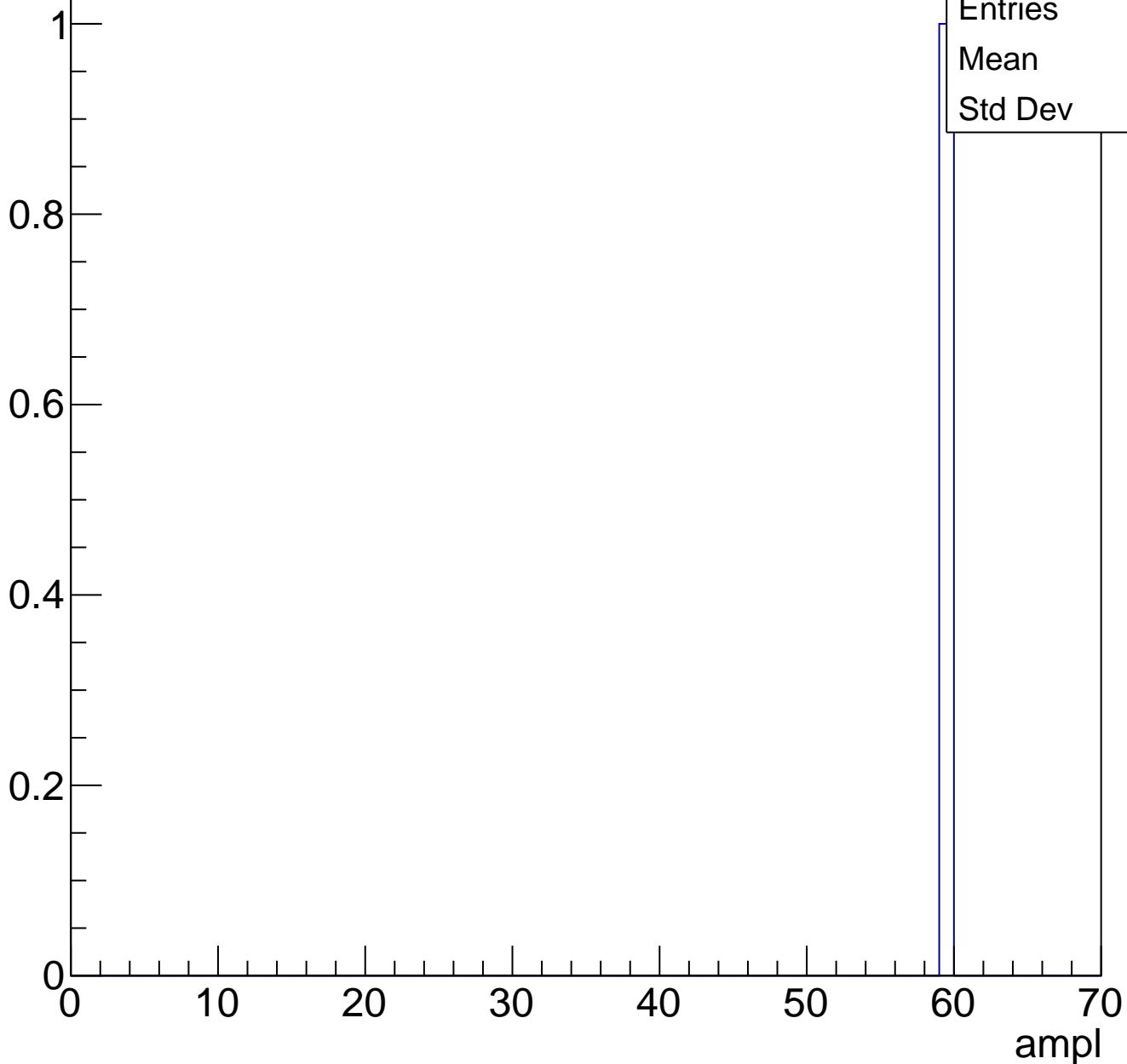
Entry



B1L100S, U15-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	59
Std Dev	0

B1L100S, U15-ch127, adc0

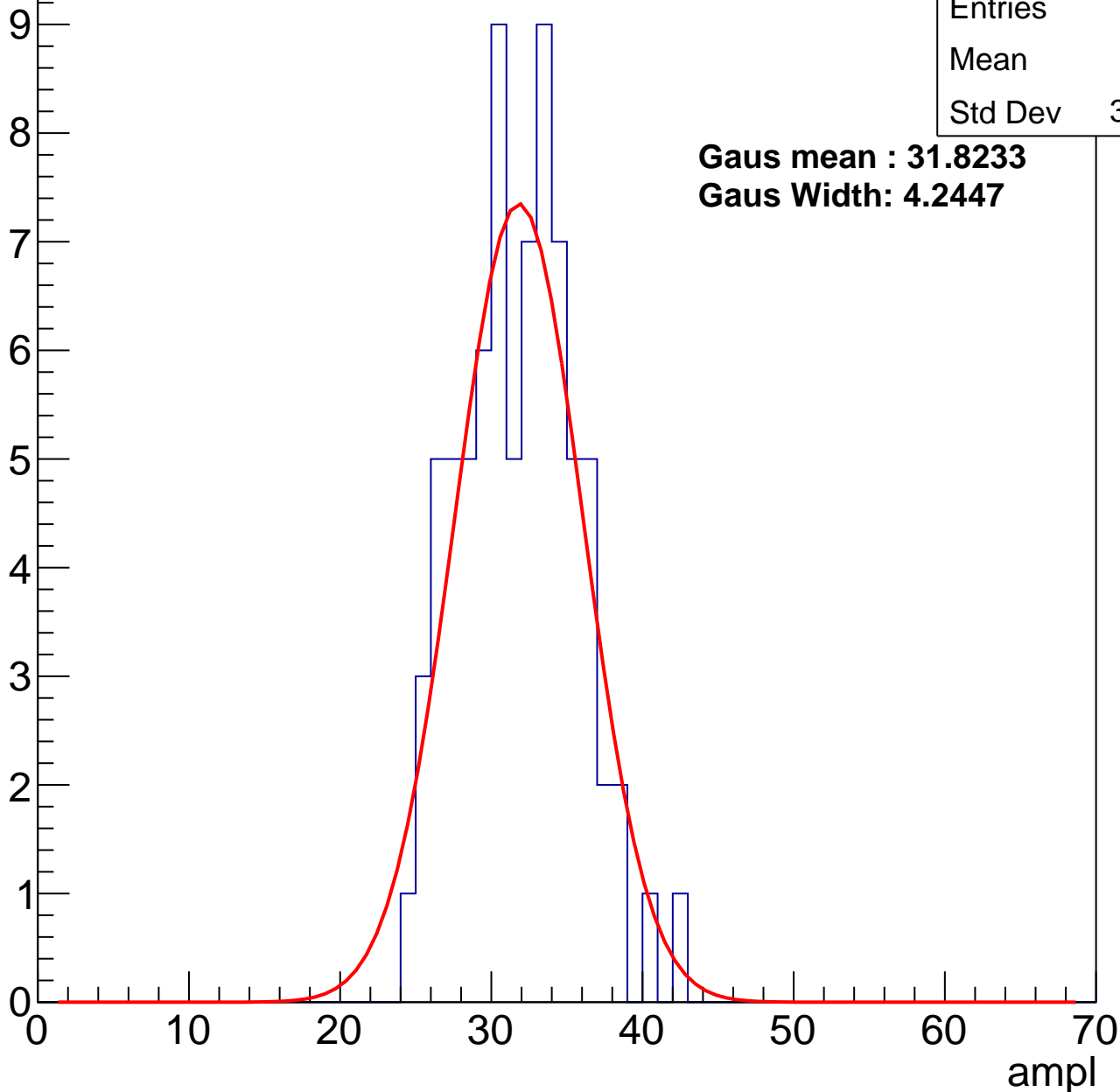
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	31.4
Std Dev	3.774

Gaus mean : 31.8233

Gaus Width: 4.2447



B1L100S, U15-ch127, adc1

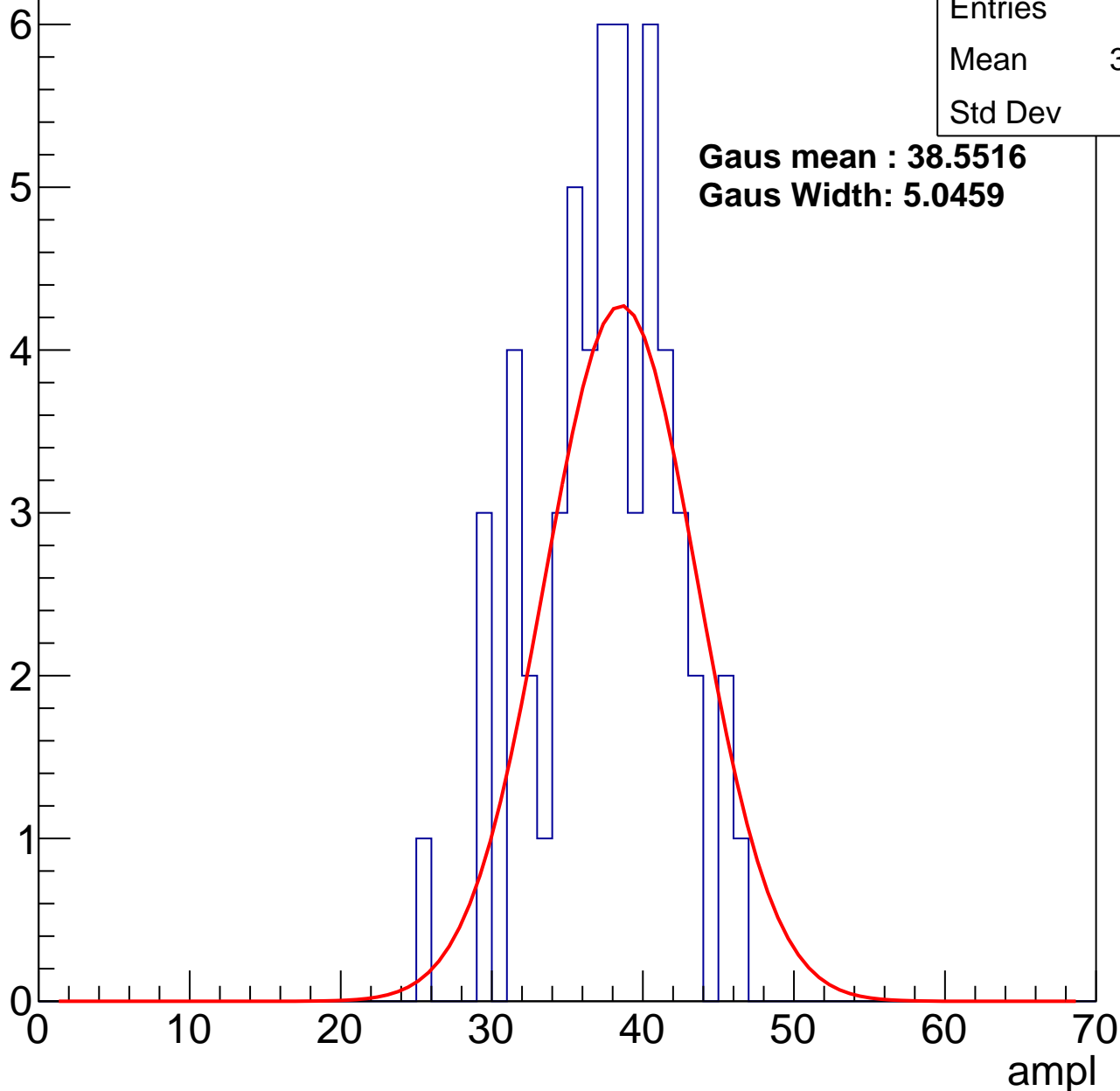
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	37.02
Std Dev	4.41

Gaus mean : 38.5516

Gaus Width: 5.0459

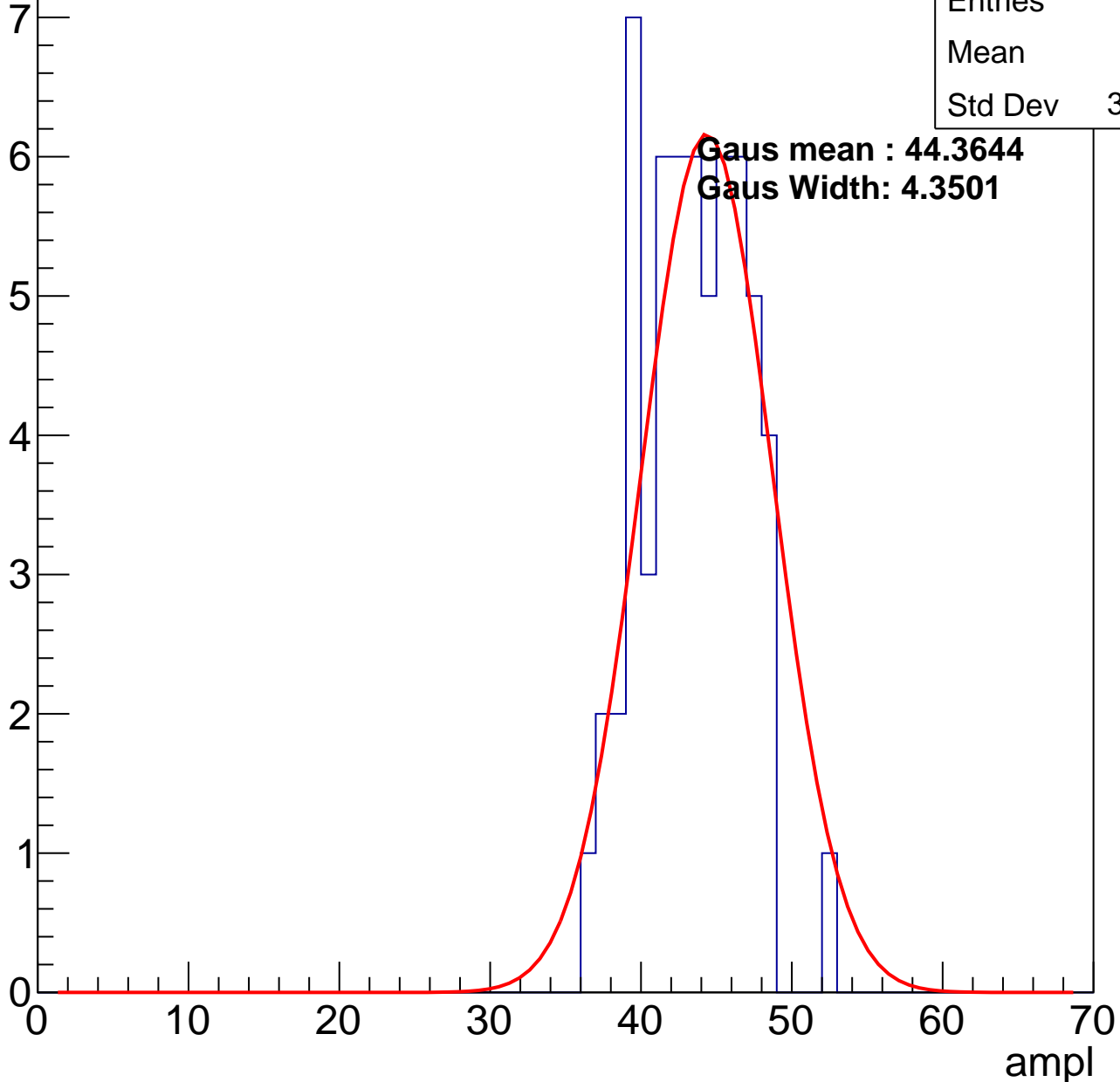


B1L100S, U15-ch127, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	43
Std Dev	3.376

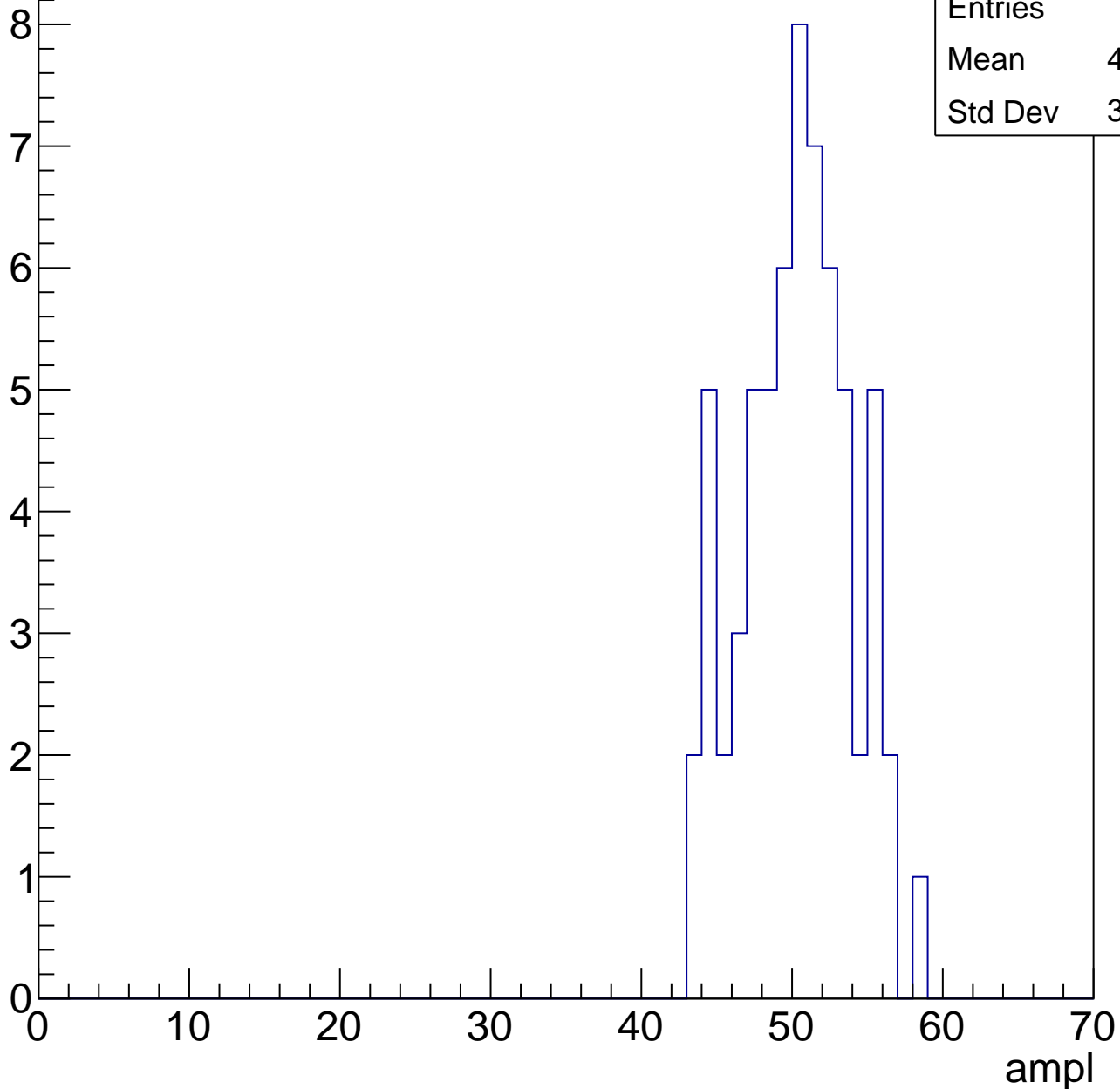


B1L100S, U15-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.84
Std Dev	3.567

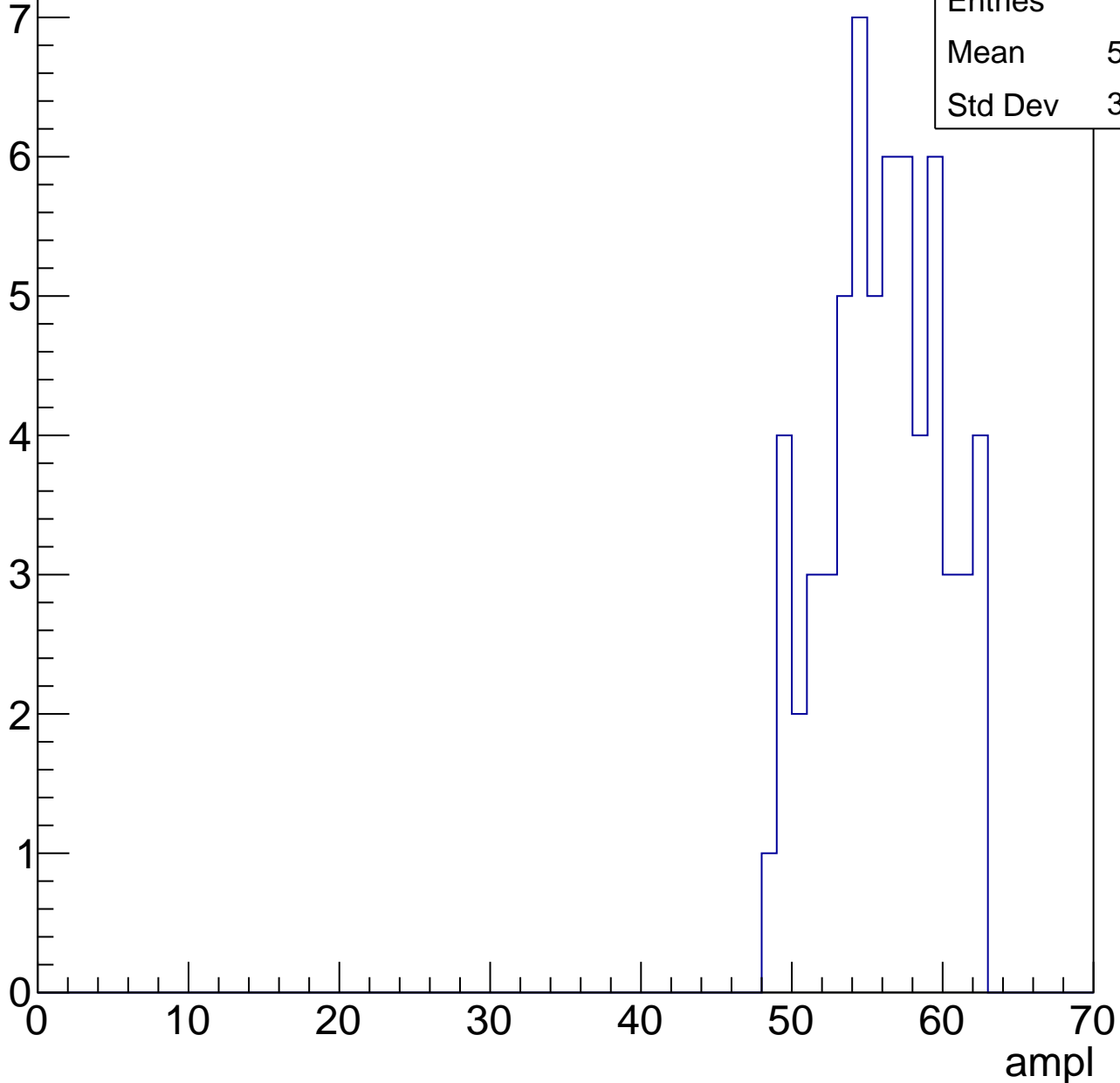


B1L100S, U15-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	55.58
Std Dev	3.735

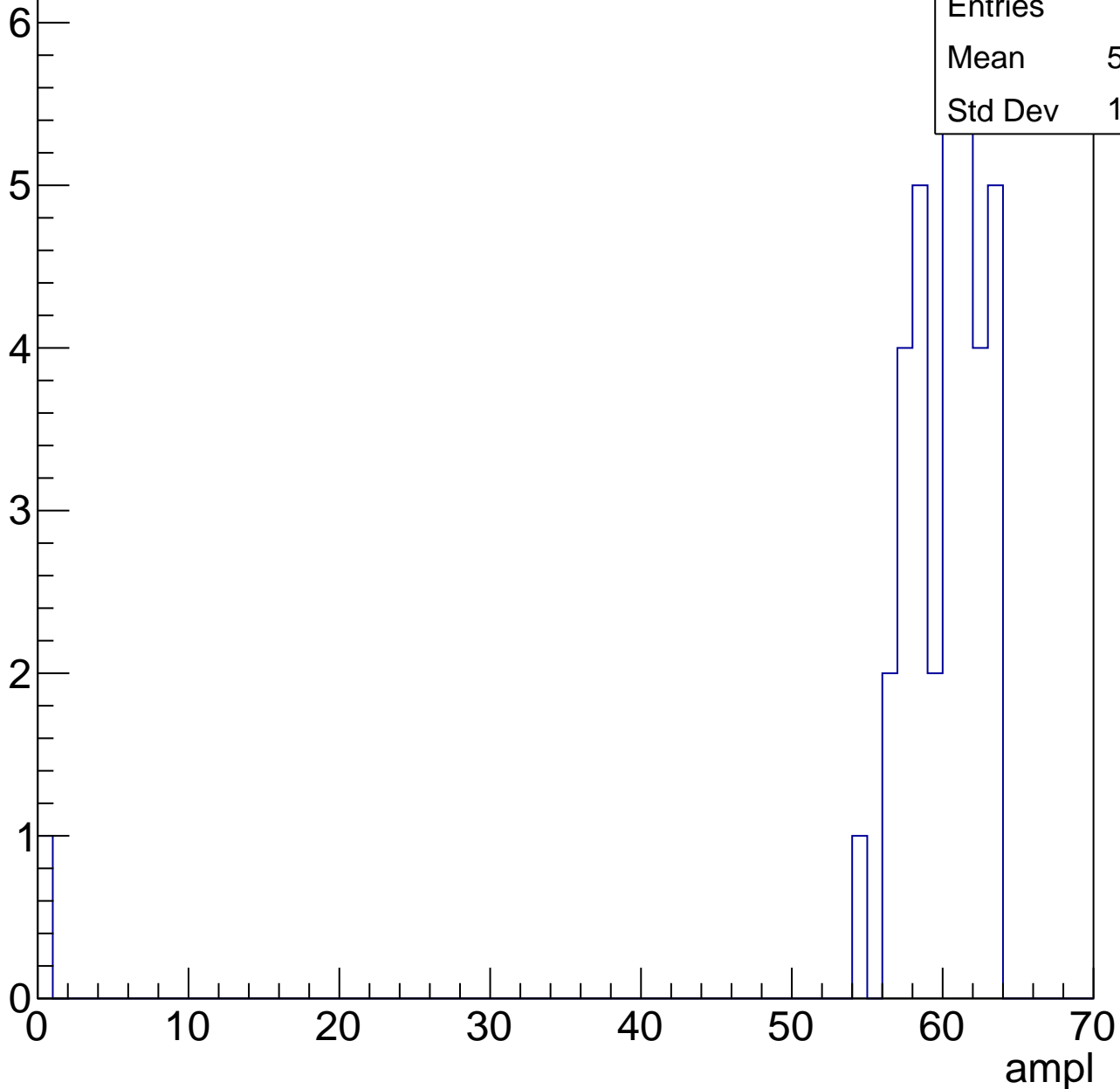


B1L100S, U15-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.08
Std Dev	10.08

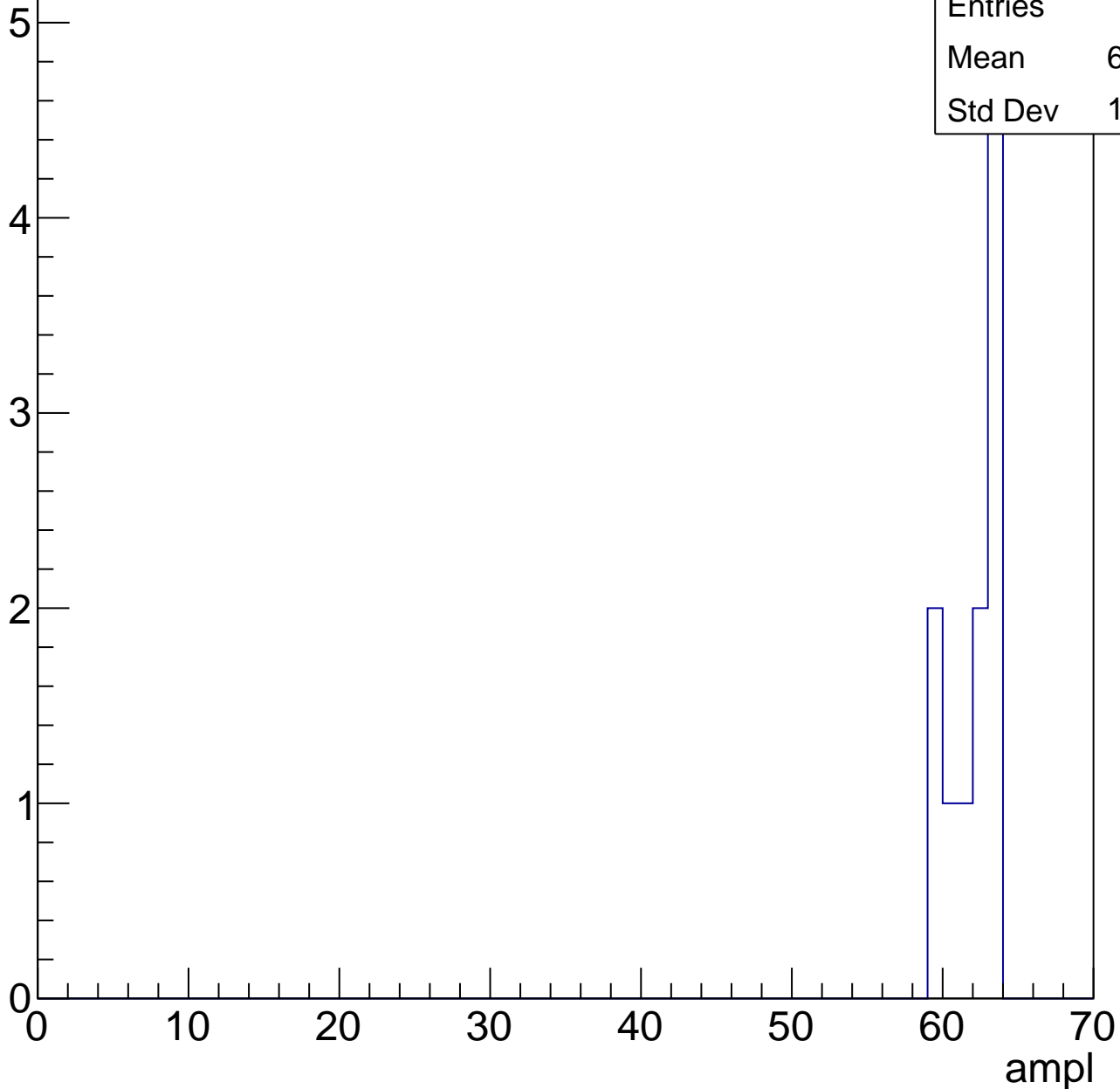


B1L100S, U15-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	61.64
Std Dev	1.553



B1L100S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

