



# B1L002S, U11-ch0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	375
Mean	44.22
Std Dev	12.14

Turn on : 27.4984

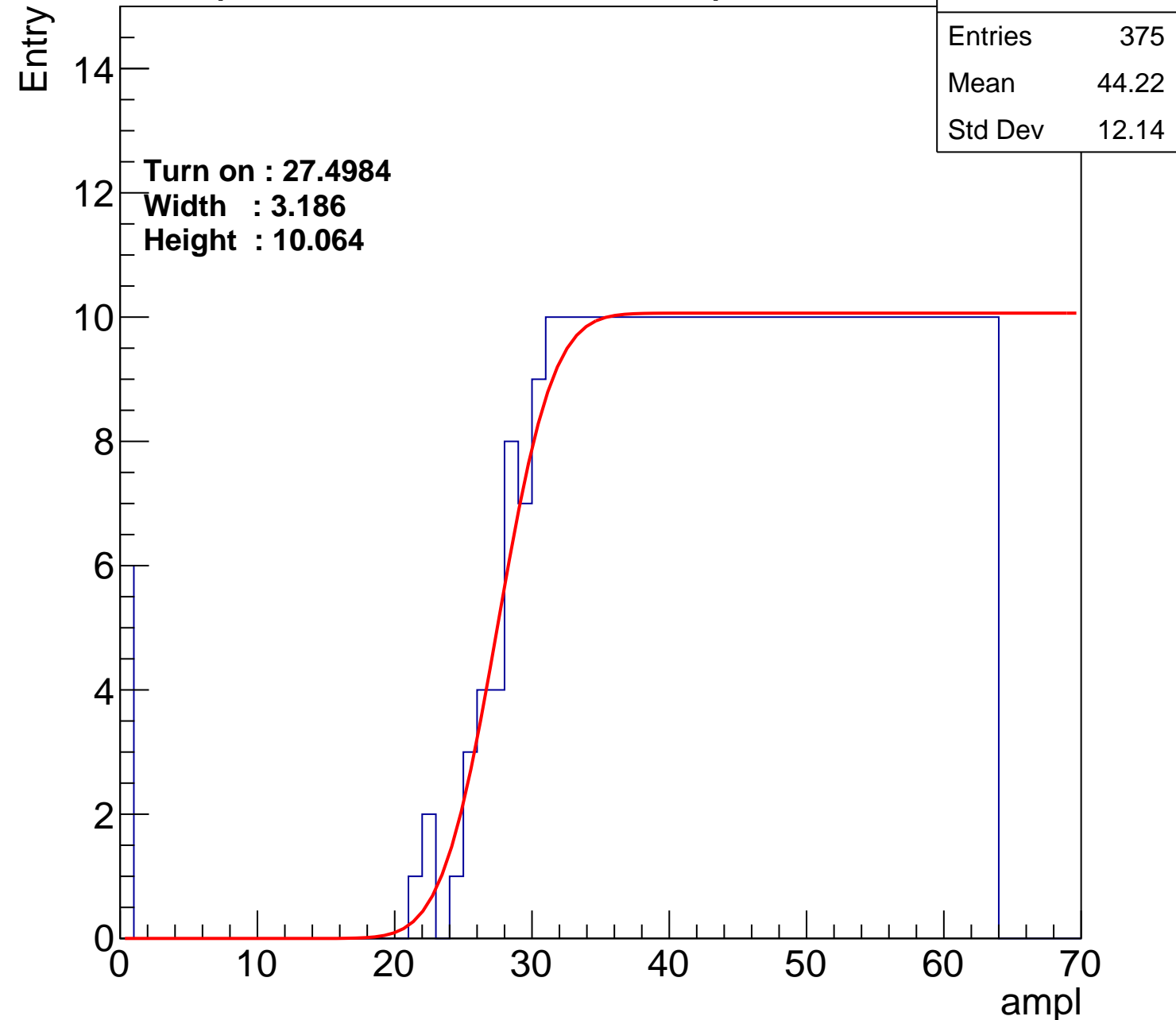
Width : 3.186

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	350
Mean	45.77
Std Dev	10.71

Turn on : 29.5246

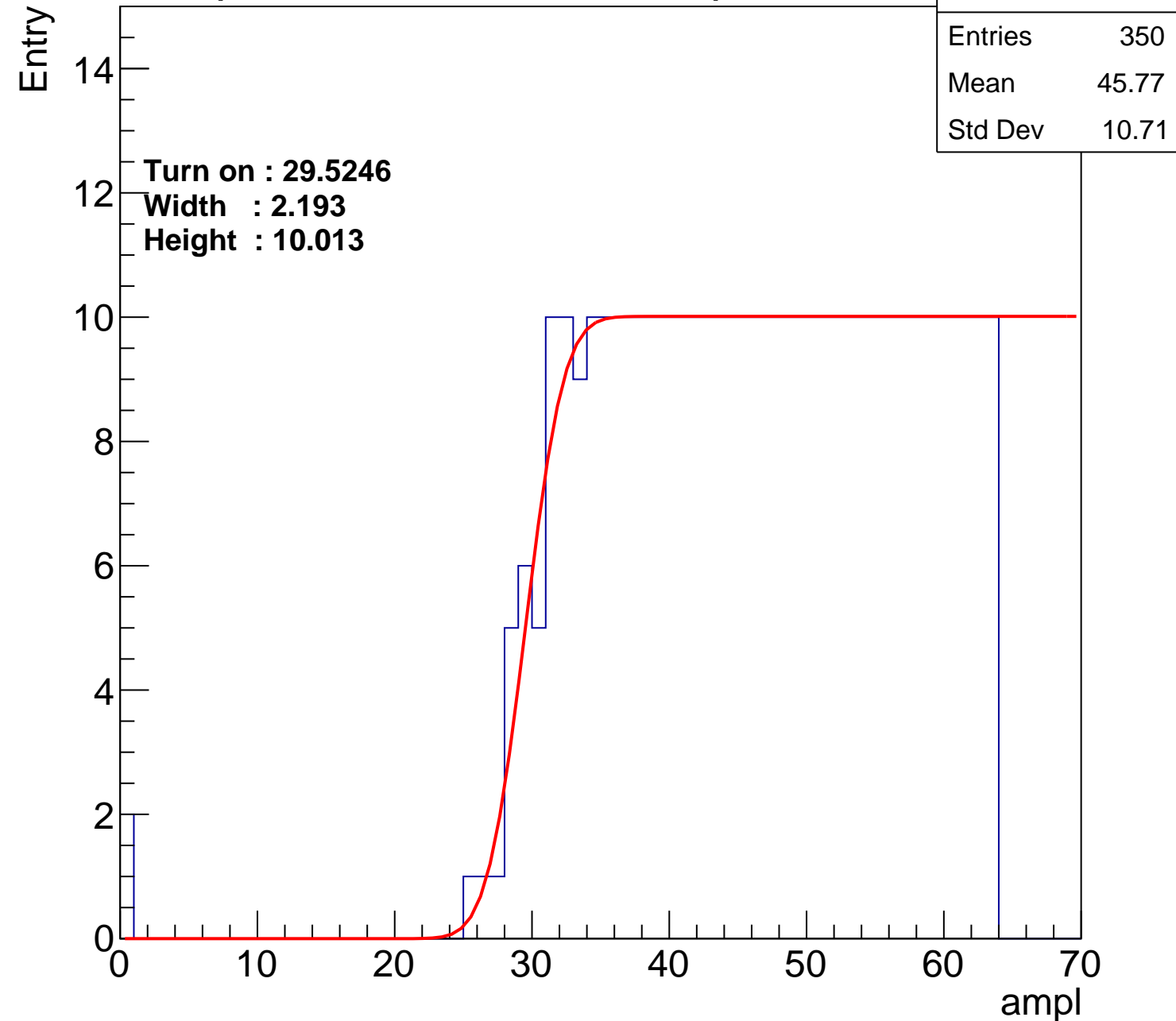
Width : 2.193

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.56

Turn on : 26.0997

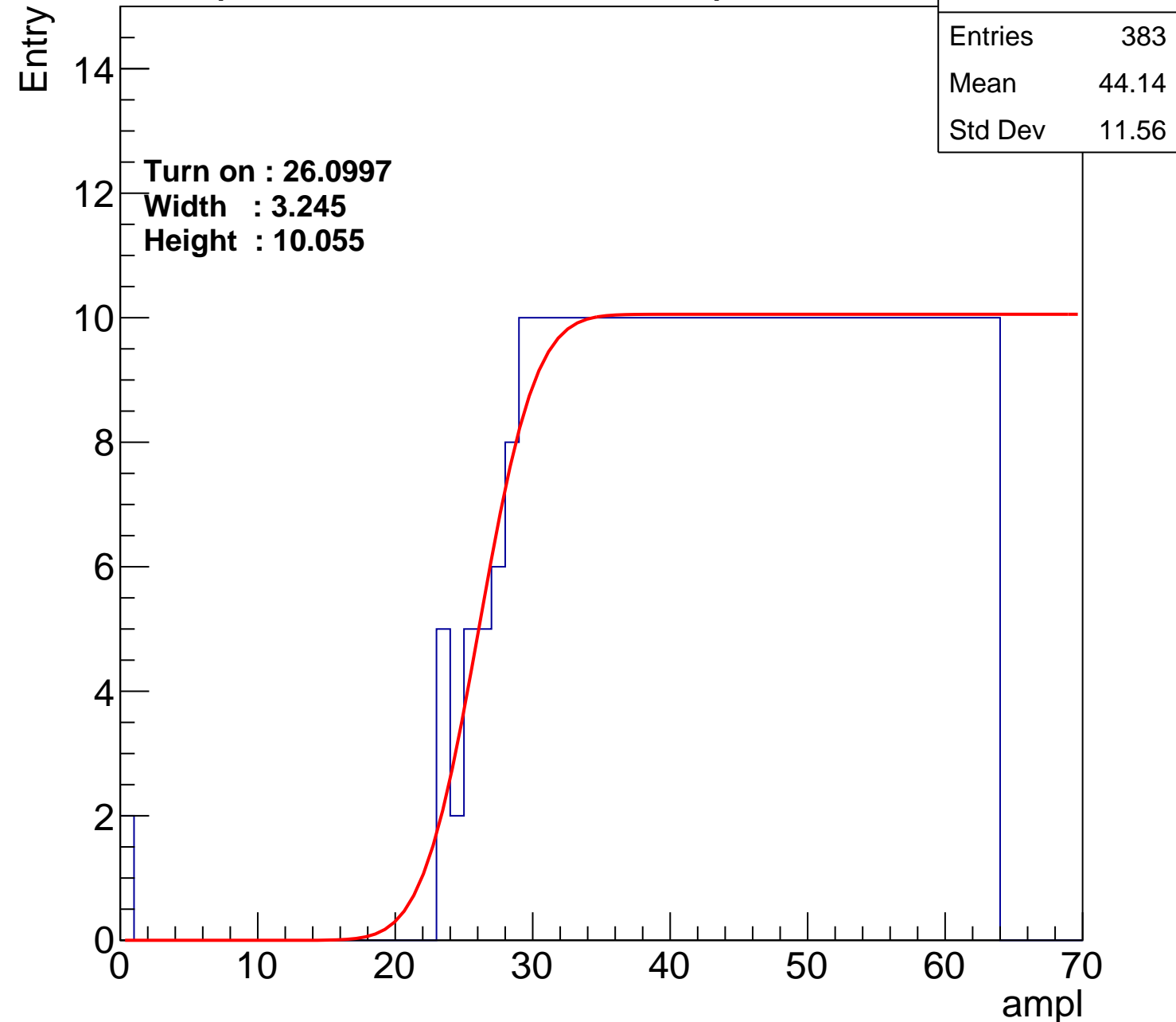
Width : 3.245

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch3

calib\_packv5\_042523\_0143.root, FC#10, port B3

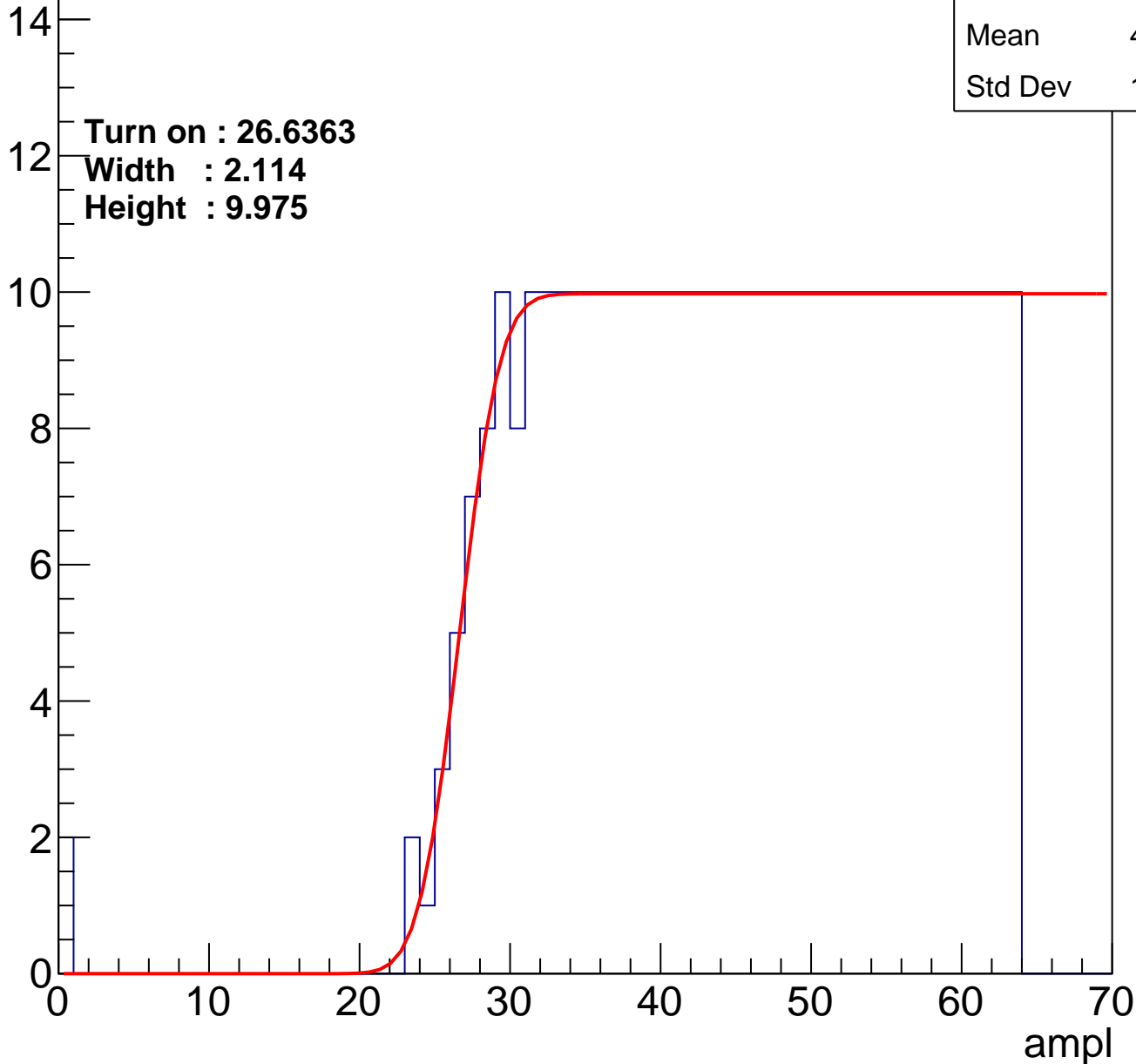
Entry

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.6363

Width : 2.114

Height : 9.975



# B1L002S, U11-ch4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.79
Std Dev	11.73

Turn on : 24.9407

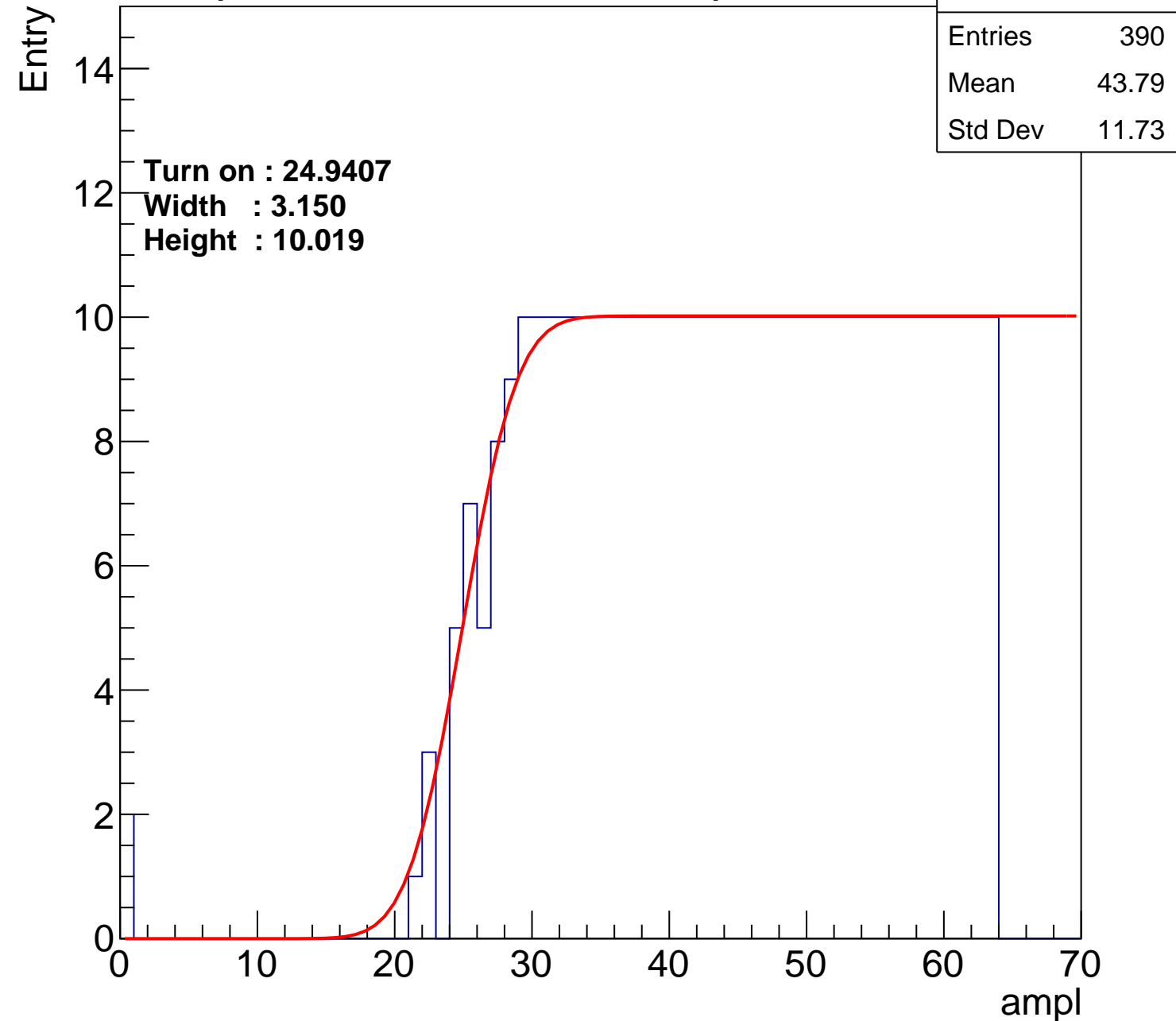
Width : 3.150

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	44.03
Std Dev	11.45

Turn on : 25.3450

Width : 1.712

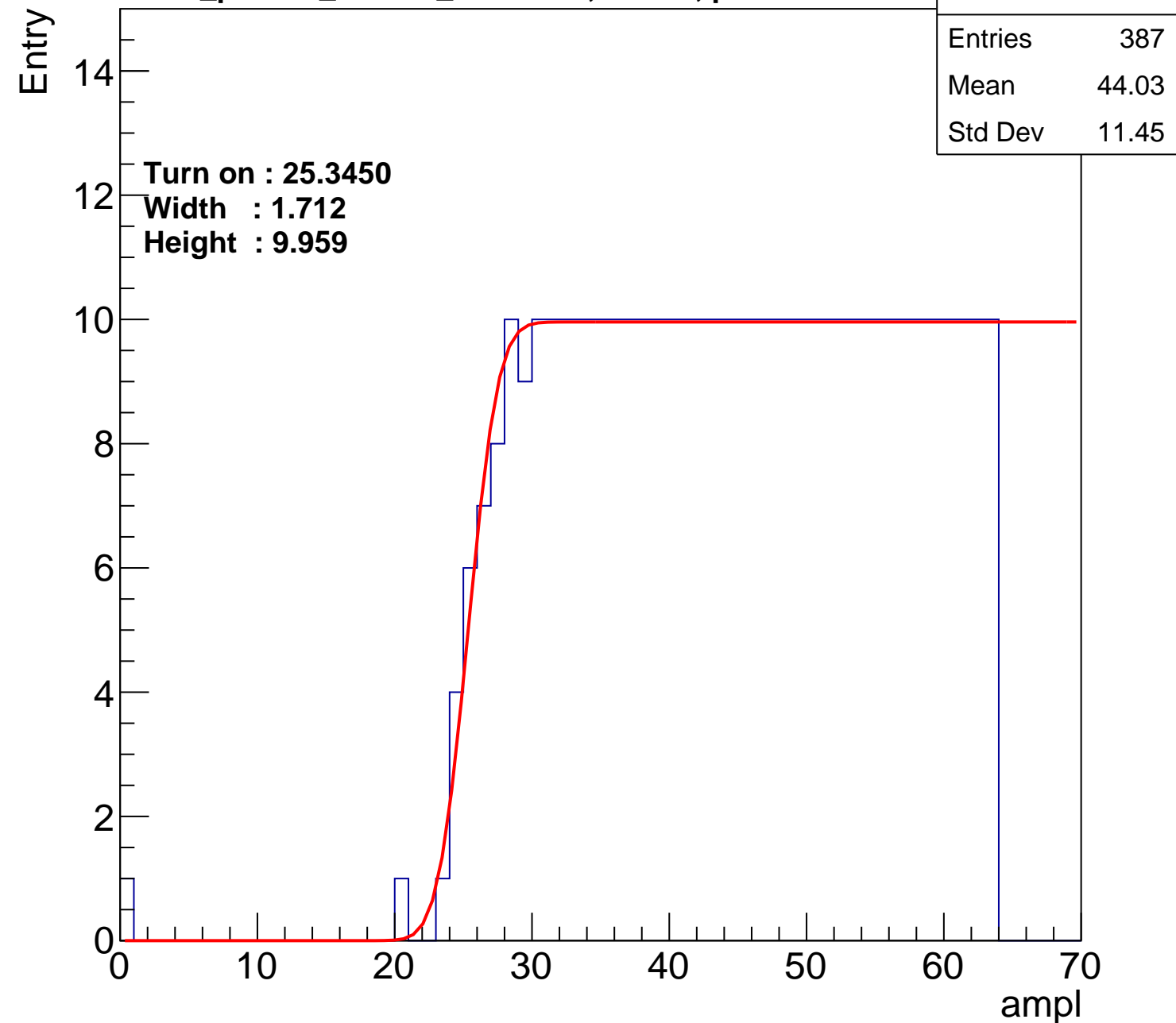
Height : 9.959

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U11-ch6

calib\_packv5\_042523\_0143.root, FC#10, port B3

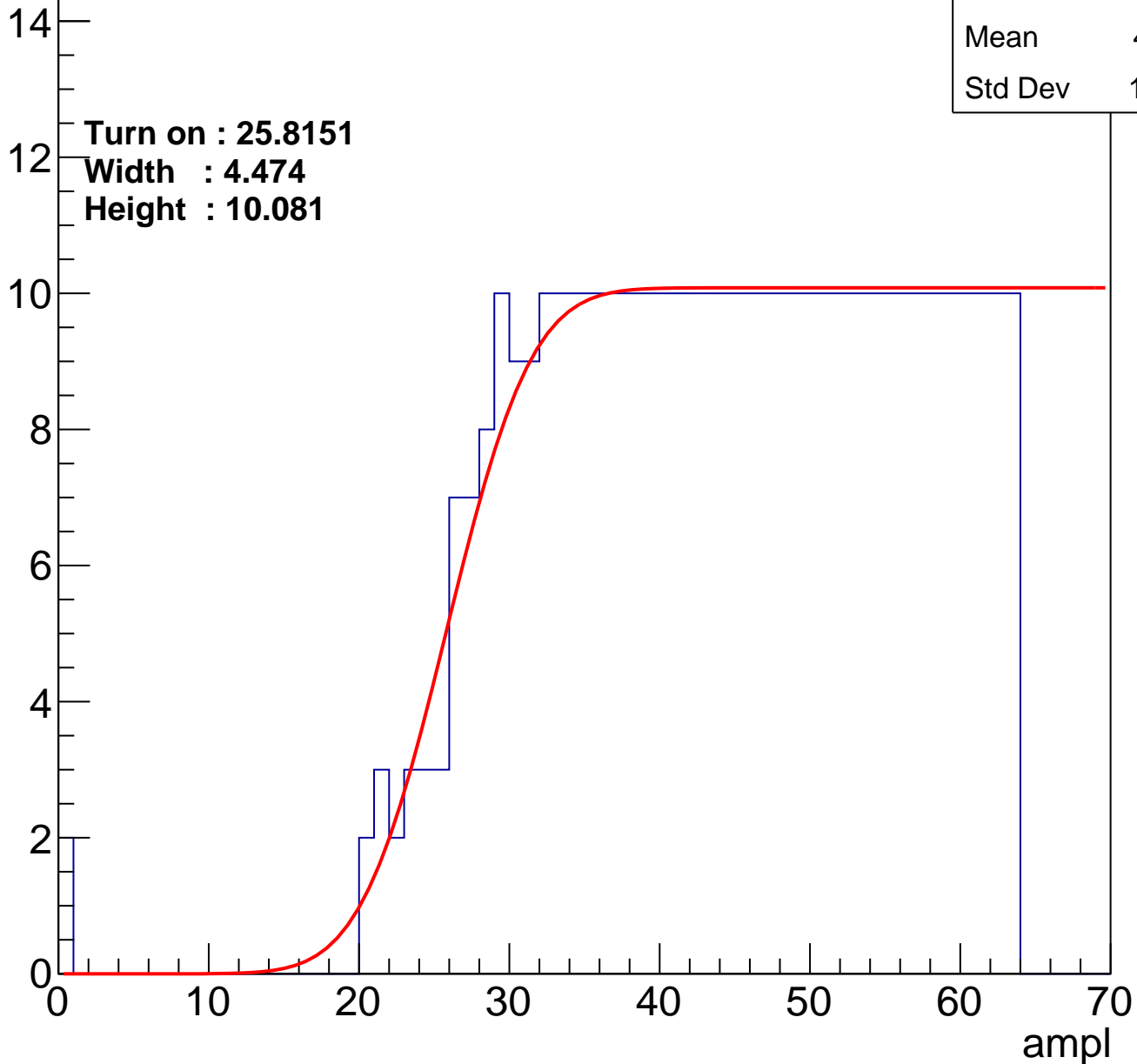
Entries	388
Mean	43.81
Std Dev	11.82

Turn on : 25.8151

Width : 4.474

Height : 10.081

Entry





# B1L002S, U11-ch7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.41
Std Dev	11.25

Turn on : 26.6447

Width : 2.856

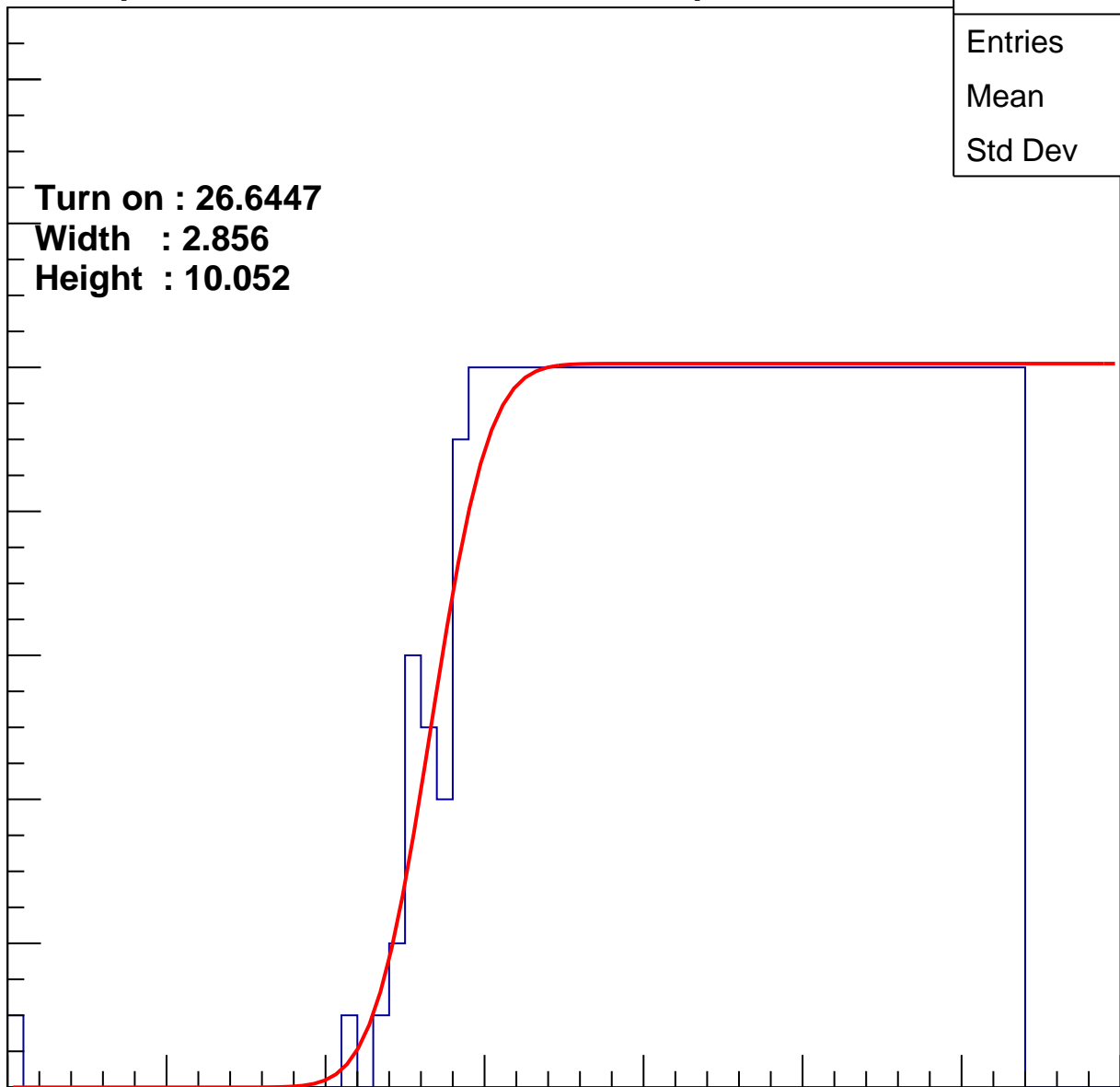
Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U11-ch8

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.53
Std Dev	11.49

Turn on : 27.1107

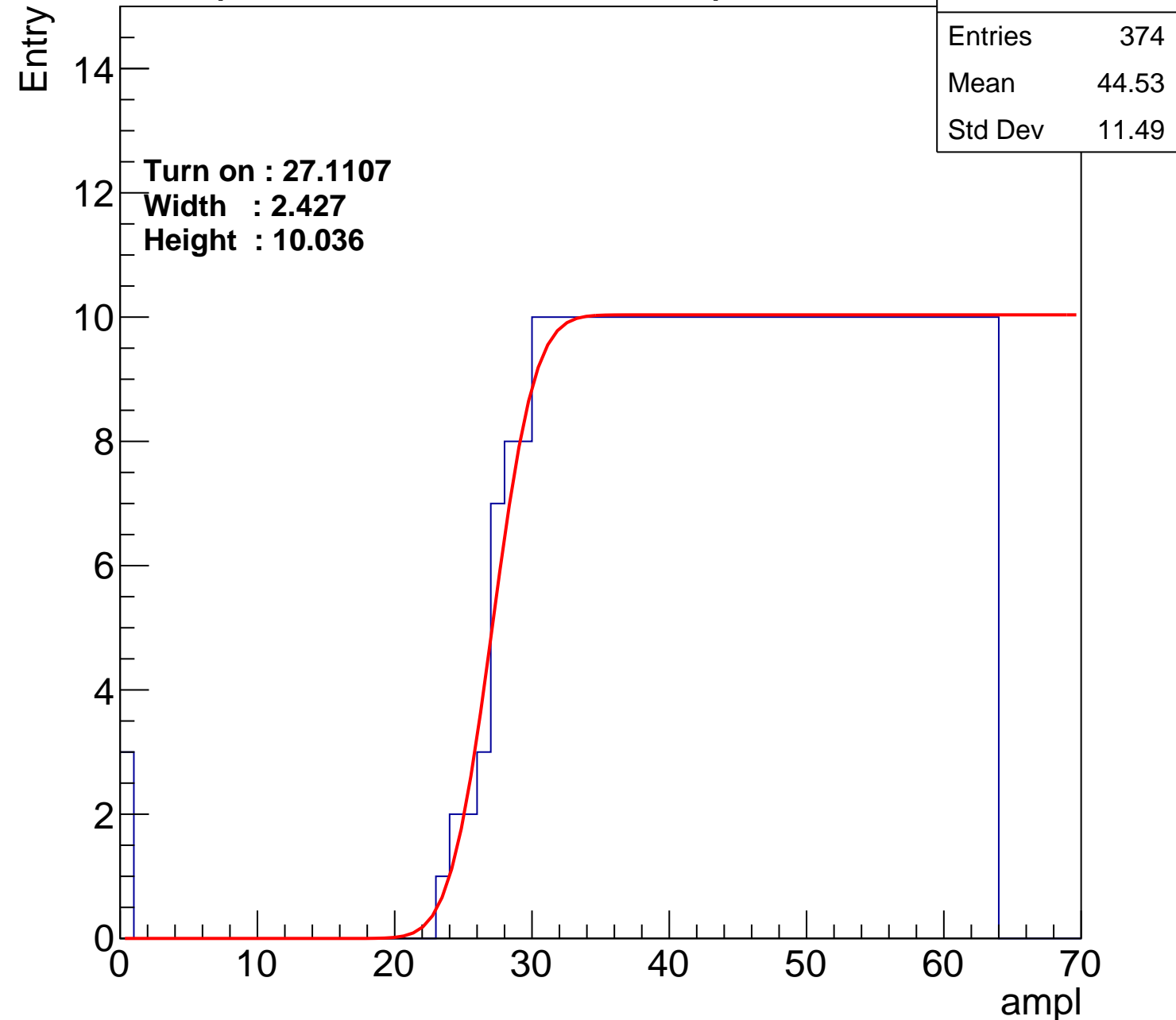
Width : 2.427

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch9

calib\_packv5\_042523\_0143.root, FC#10, port B3

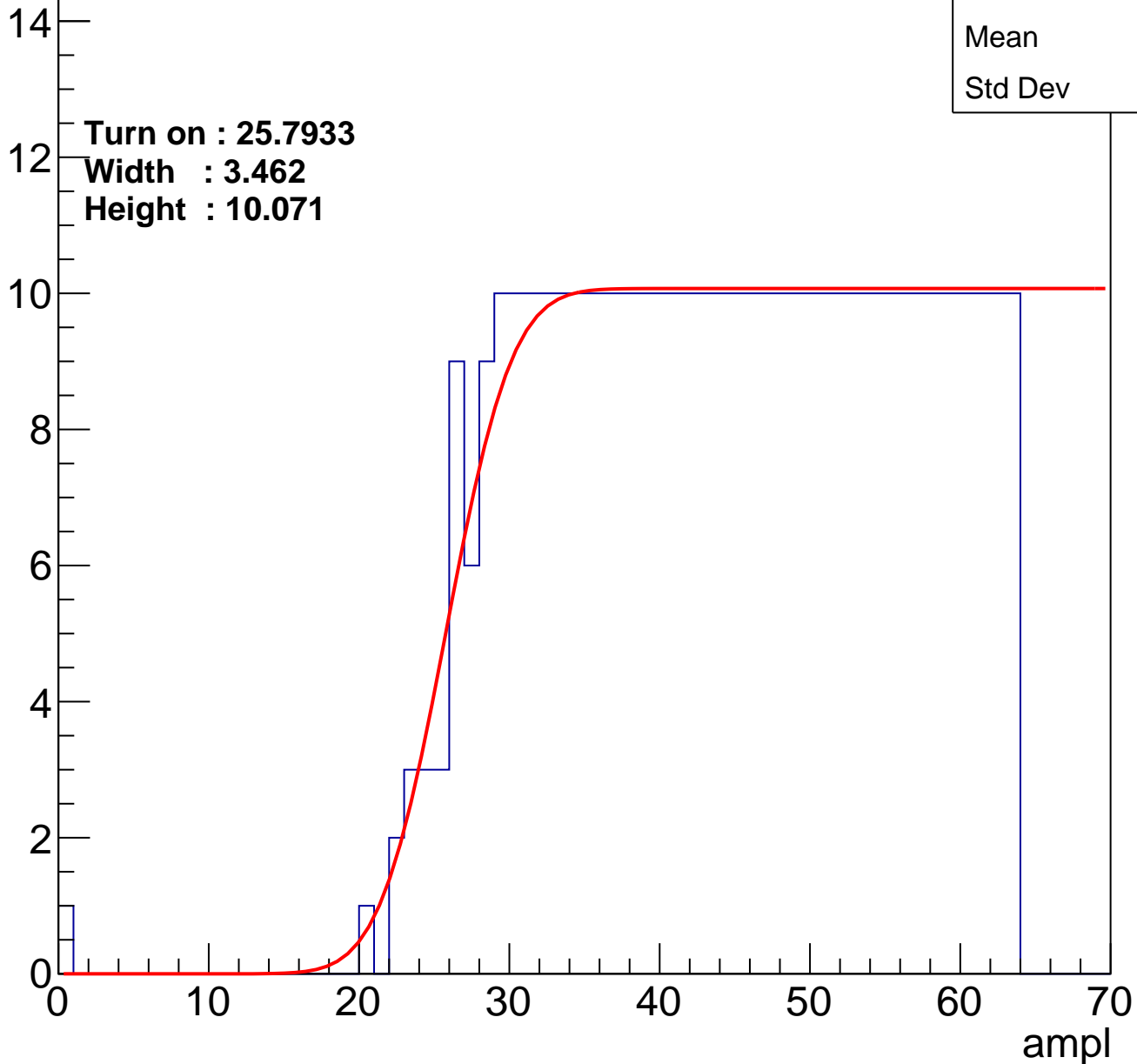
Entries	387
Mean	44
Std Dev	11.5

Turn on : 25.7933

Width : 3.462

Height : 10.071

Entry



# B1L002S, U11-ch10

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.58
Std Dev	12.09

Turn on : 25.4527

Width : 2.686

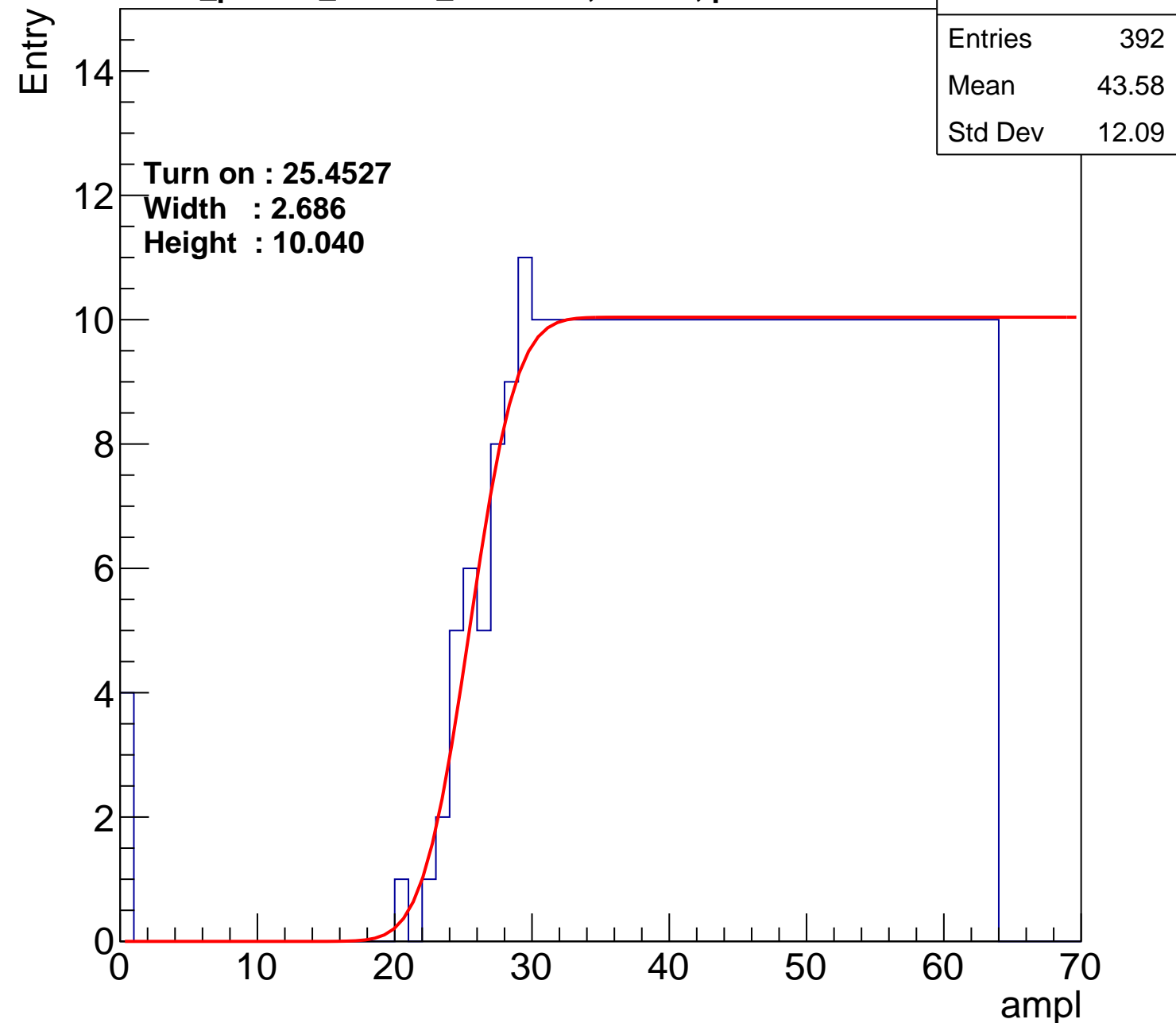
Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U11-ch11

calib\_packv5\_042523\_0143.root, FC#10, port B3

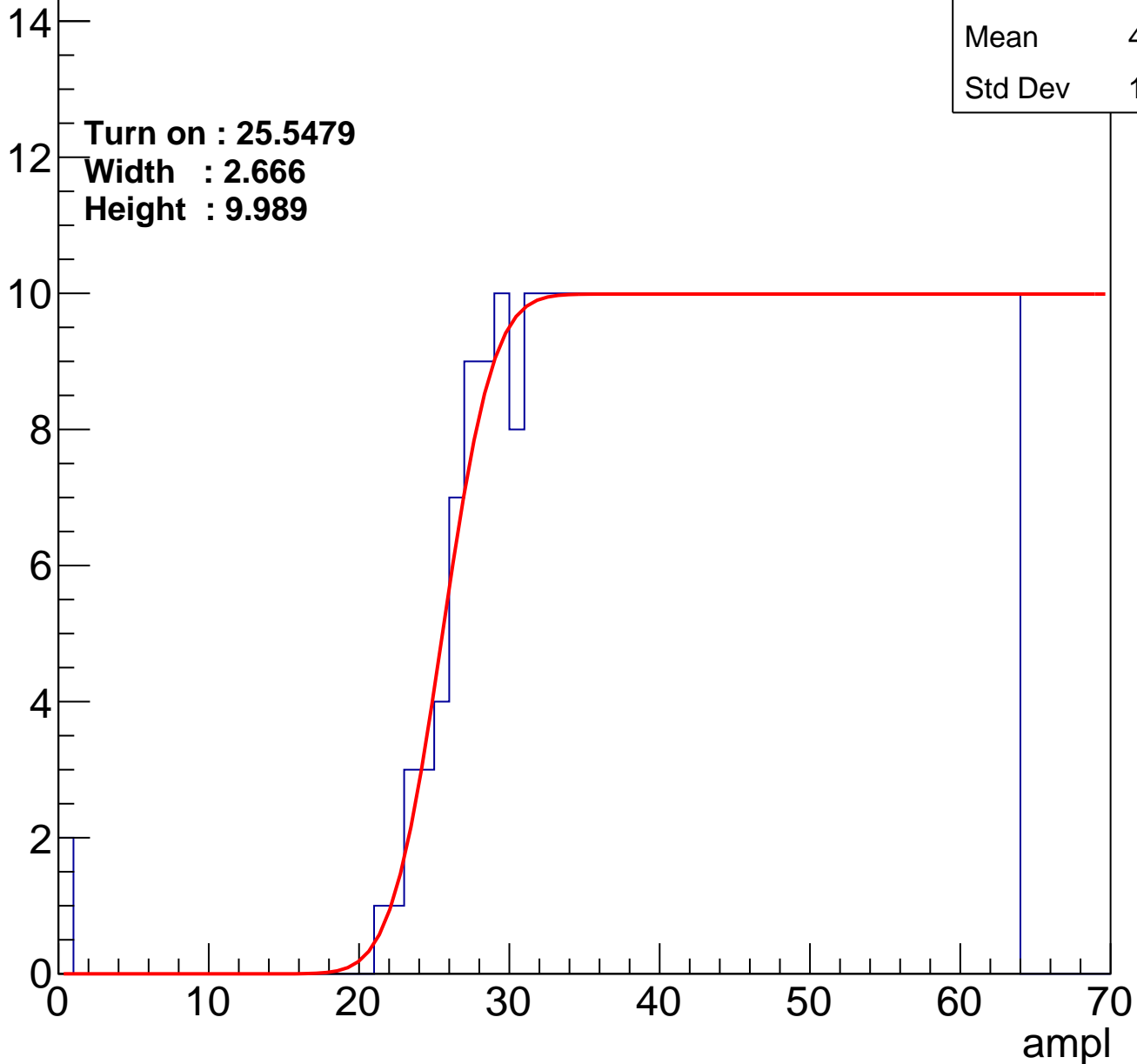
Entries	387
Mean	43.93
Std Dev	11.67

Turn on : 25.5479

Width : 2.666

Height : 9.989

Entry



# B1L002S, U11-ch12

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.47
Std Dev	11.26

**Turn on : 26.5048**

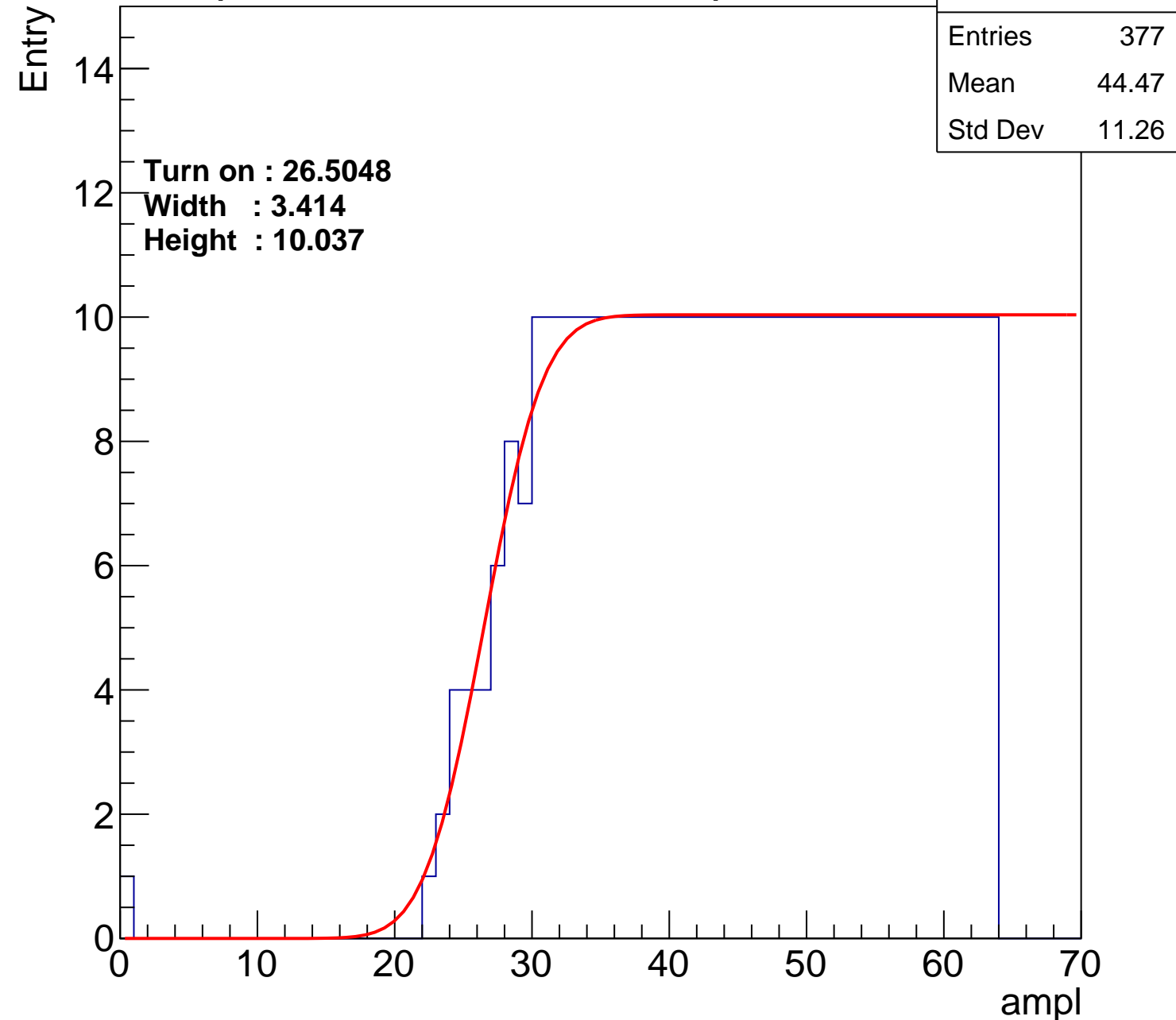
**Width : 3.414**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch13

calib\_packv5\_042523\_0143.root, FC#10, port B3

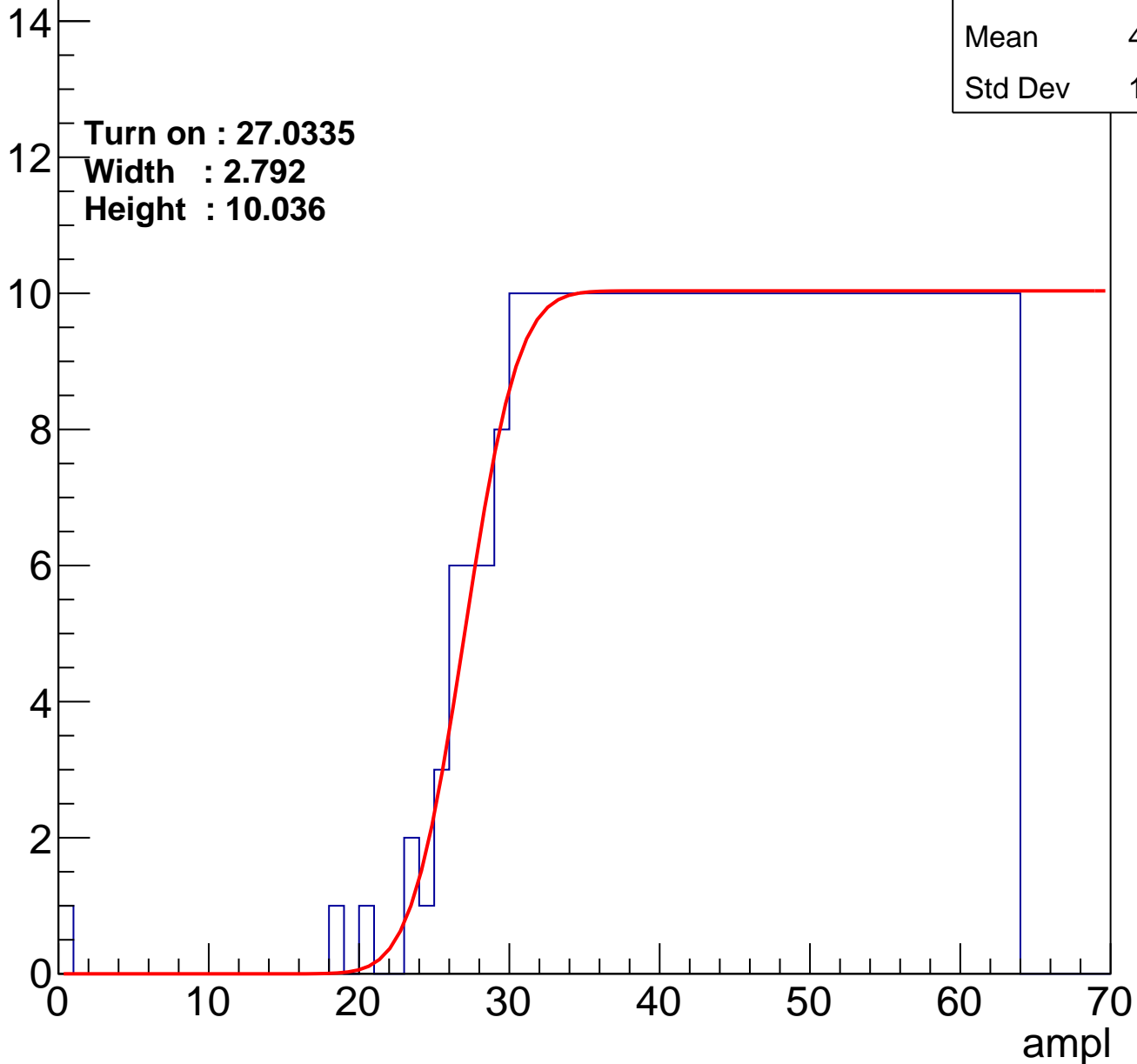
Entries	375
Mean	44.56
Std Dev	11.23

Turn on : 27.0335

Width : 2.792

Height : 10.036

Entry



# B1L002S, U11-ch14

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44.08
Std Dev	11.48

Turn on : 25.8624

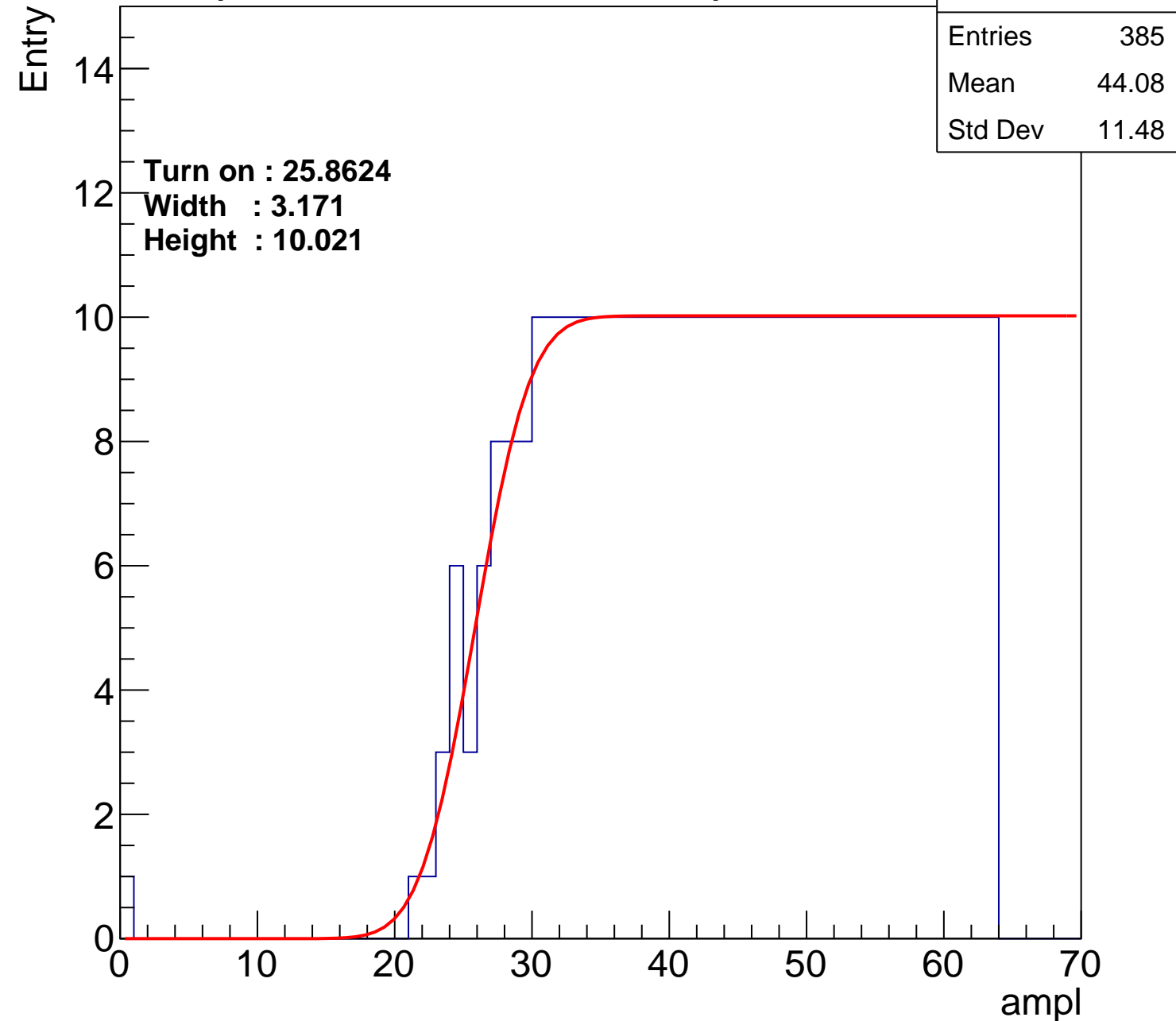
Width : 3.171

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





**calib\_packv5\_042523\_0143.root, FC#10, port B3**

**calib\_packv5\_042523\_0143.root, FC#10, port B3**

Turn on : 28.7147  
Width : 2.412  
Height : 9.977



# B1L002S, U11-ch16

calib\_packv5\_042523\_0143.root, FC#10, port B3

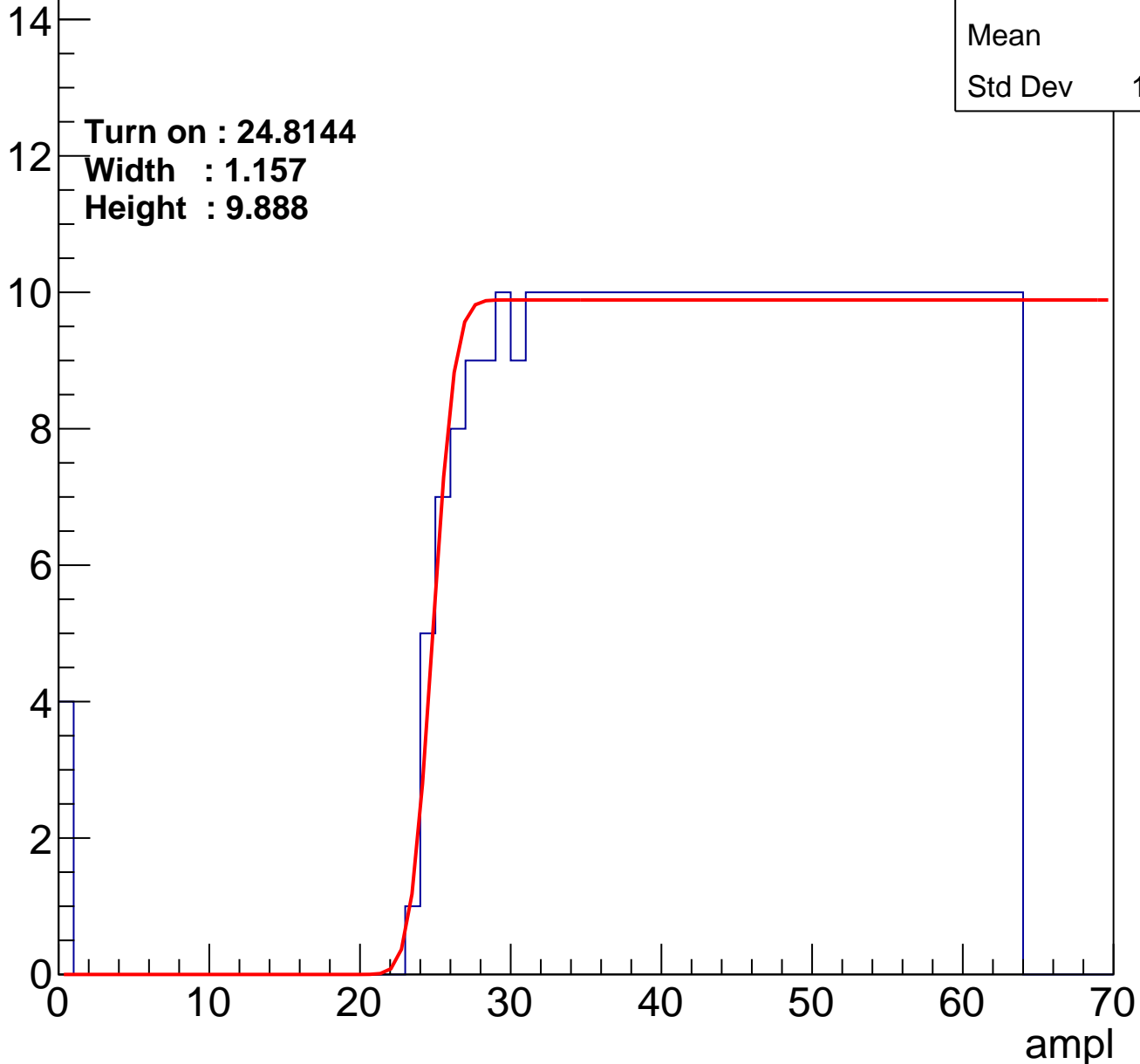
Entries	392
Mean	43.6
Std Dev	12.06

Turn on : 24.8144

Width : 1.157

Height : 9.888

Entry



# B1L002S, U11-ch17

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.85
Std Dev	11.52

**Turn on : 24.9486**

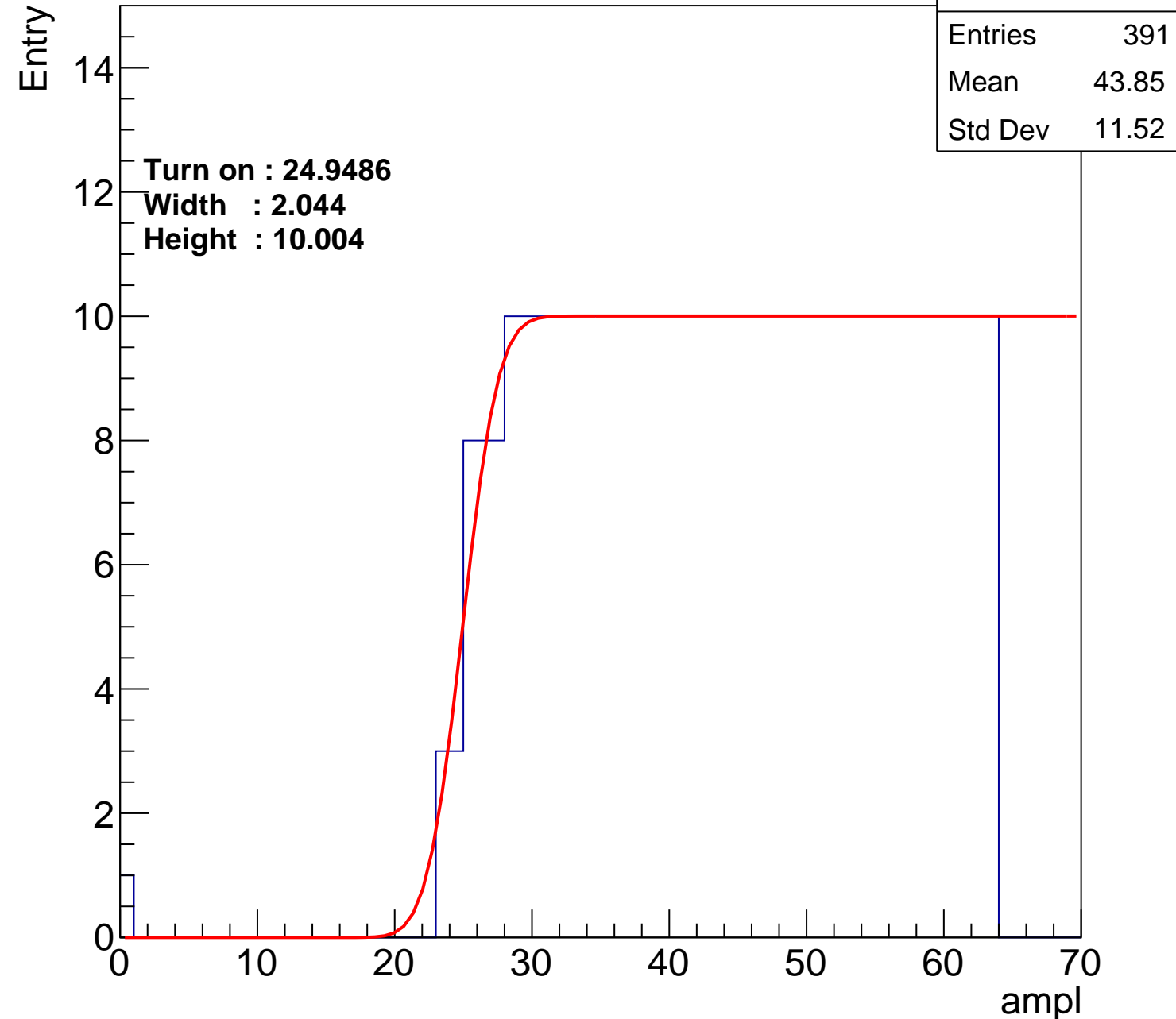
**Width : 2.044**

**Height : 10.004**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch18

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.5543

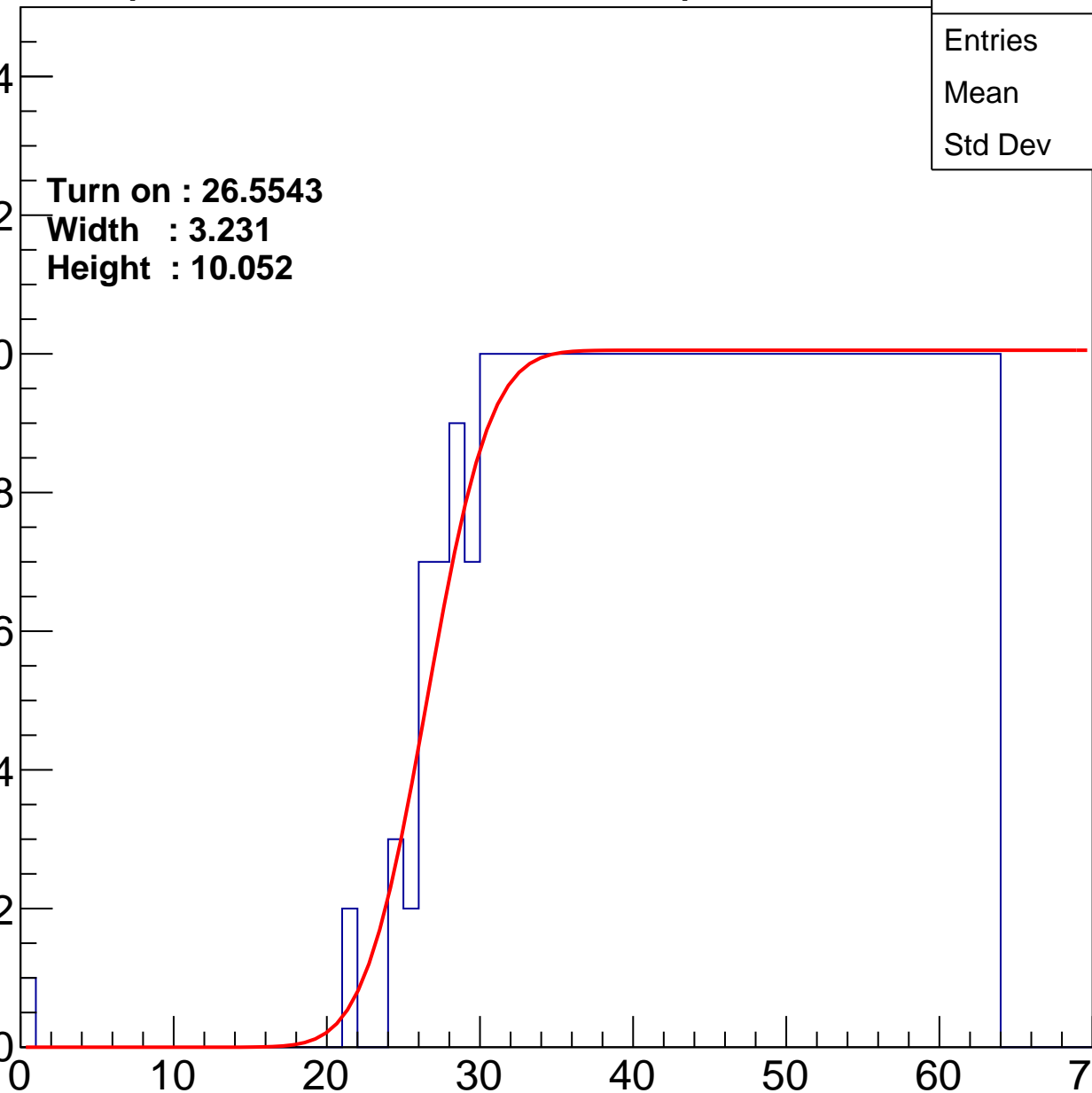
Width : 3.231

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch19

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.14
Std Dev	11.42

Turn on : 25.8072

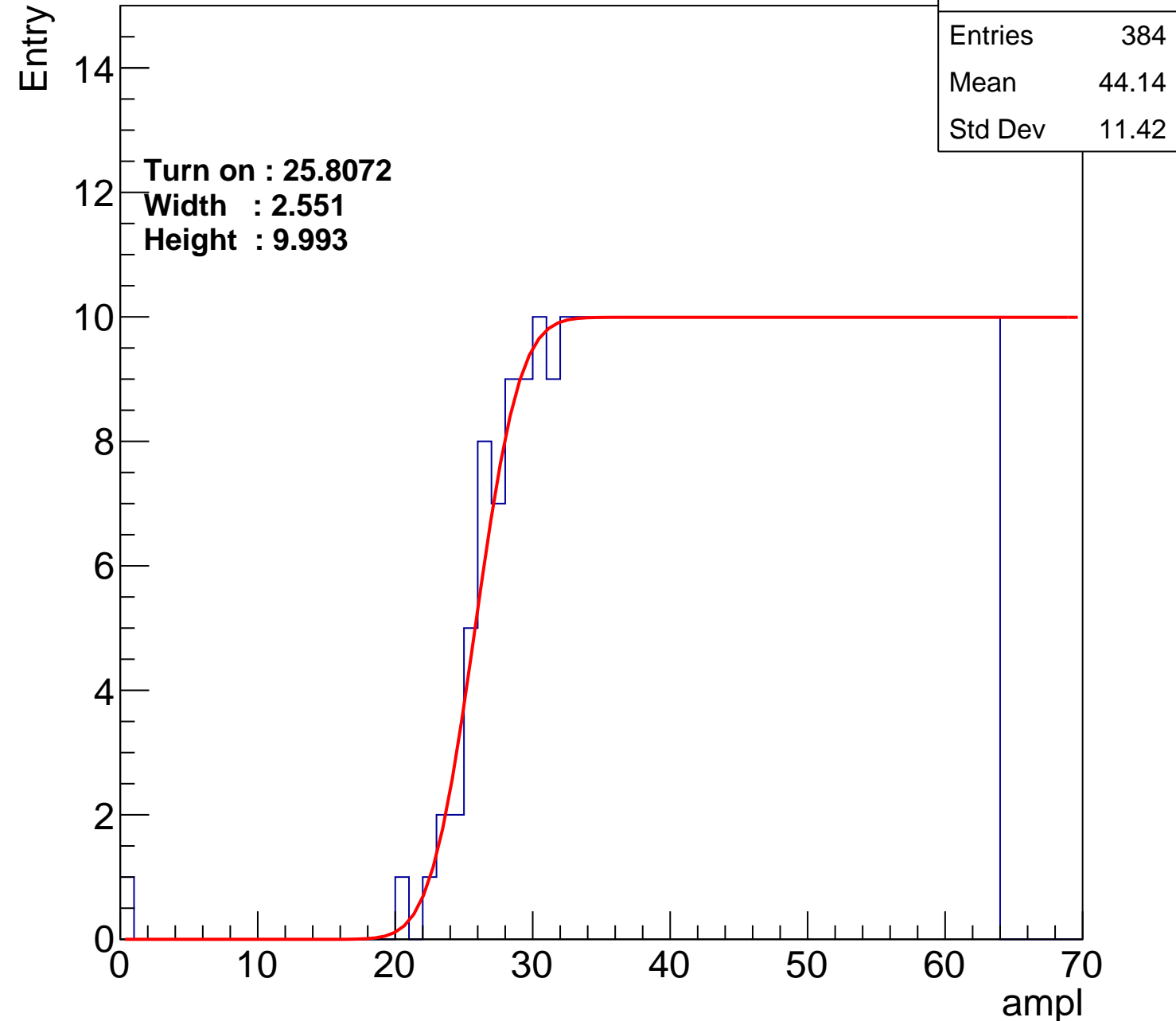
Width : 2.551

Height : 9.993

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch20

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.65

Turn on : 25.6951

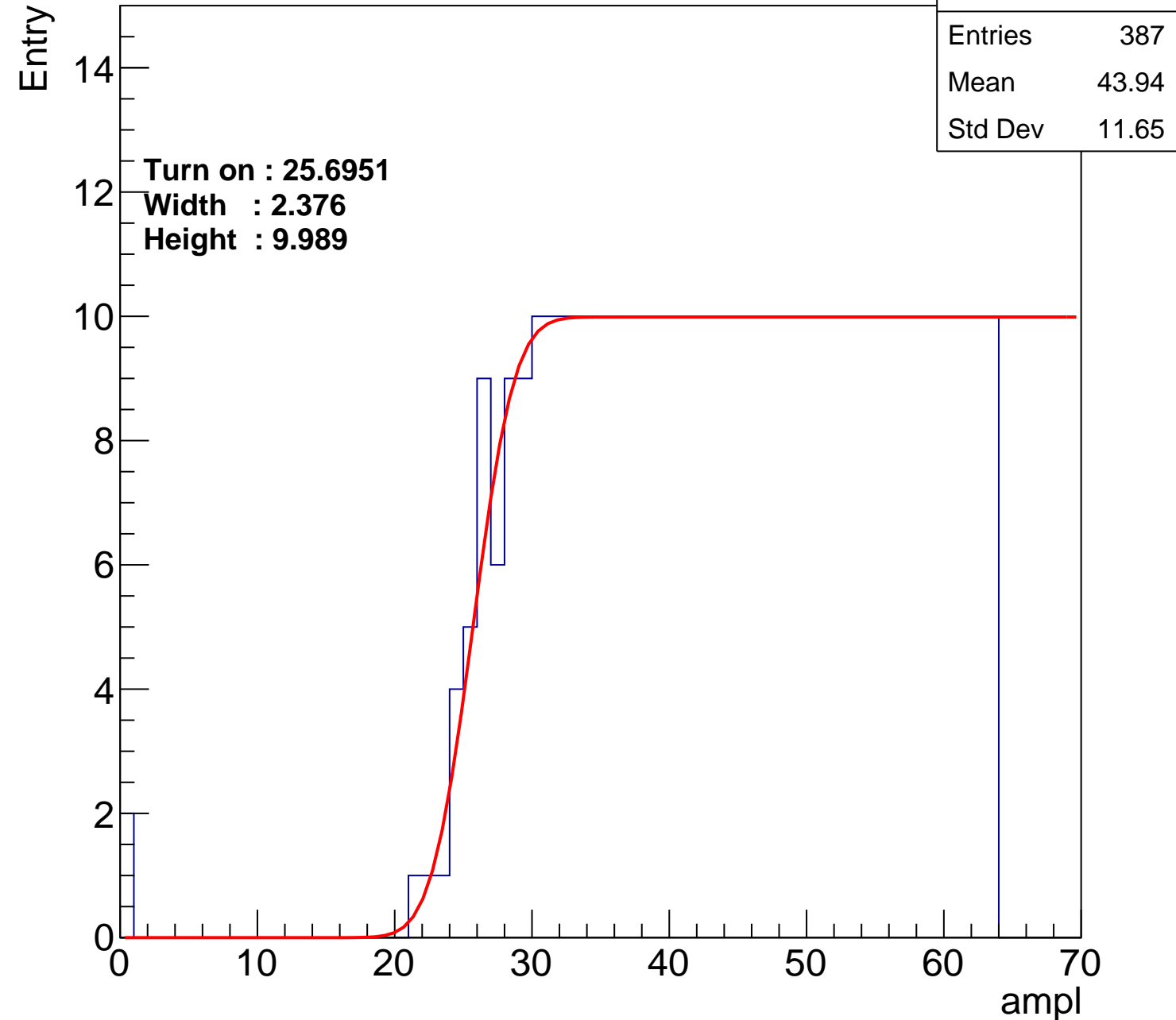
Width : 2.376

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch21

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	376
Mean	44.57
Std Dev	11.16

Turn on : 27.0185

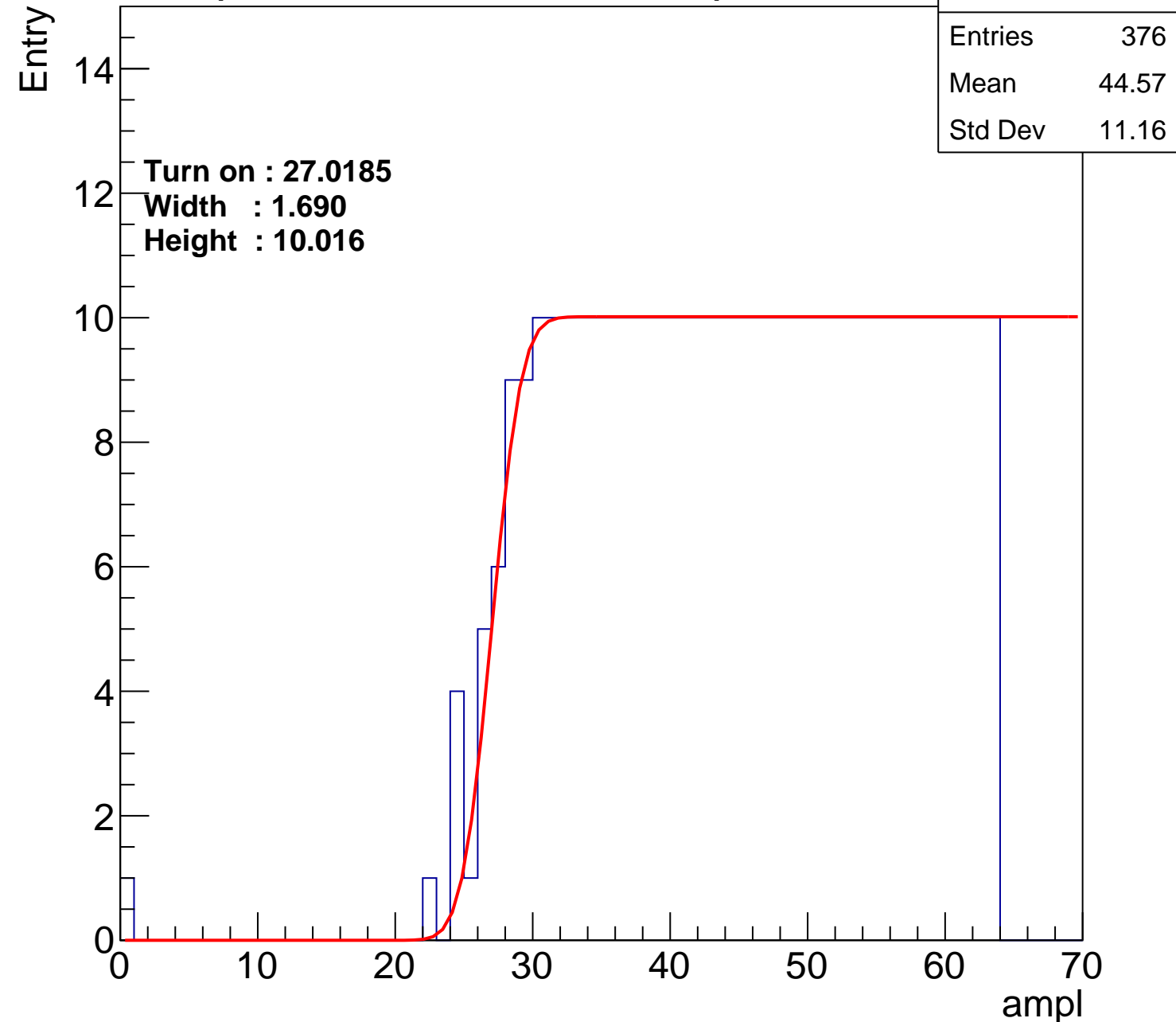
Width : 1.690

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch22

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.51
Std Dev	11.69

Turn on : 27.4622

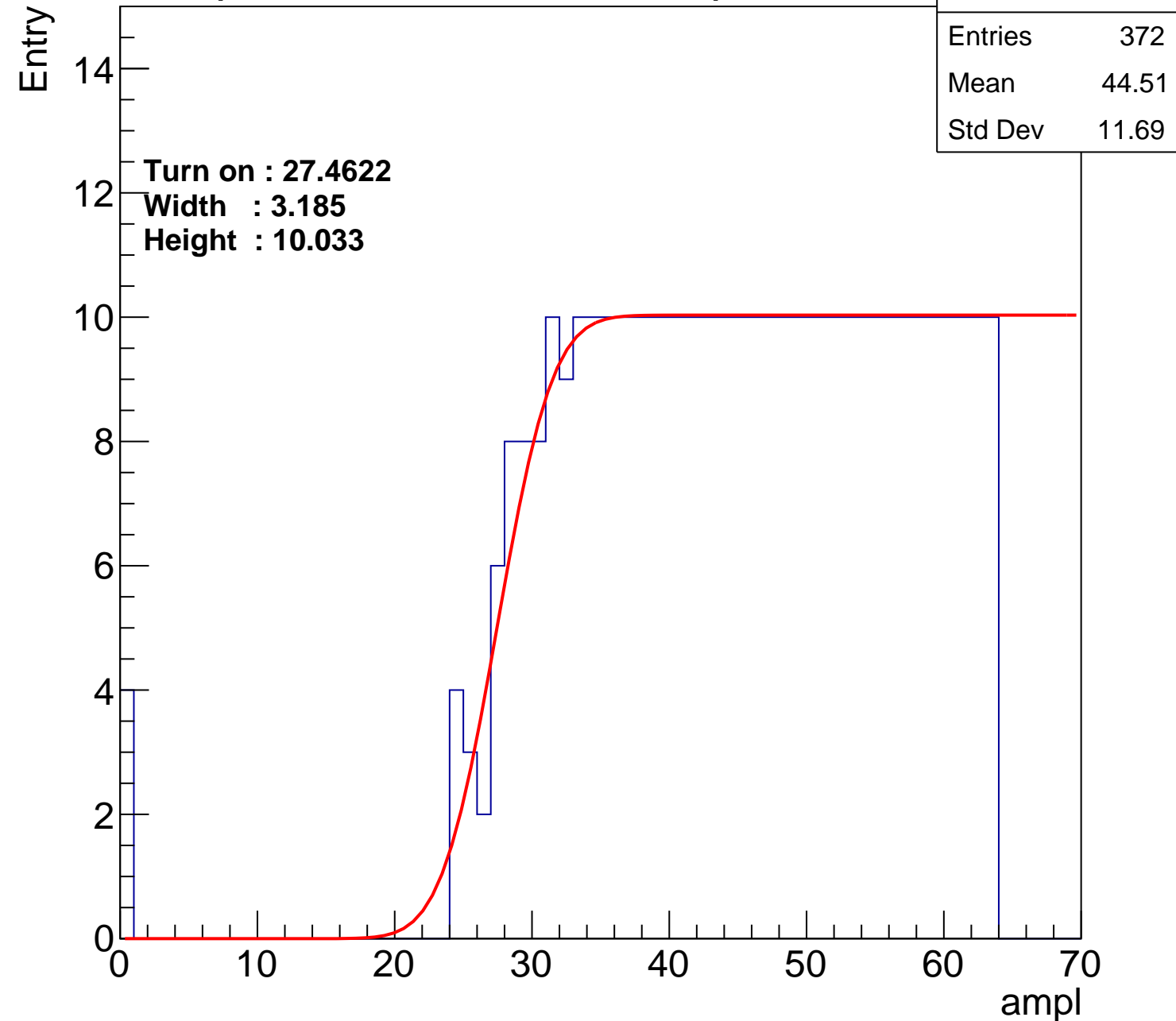
Width : 3.185

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch23

calib\_packv5\_042523\_0143.root, FC#10, port B3

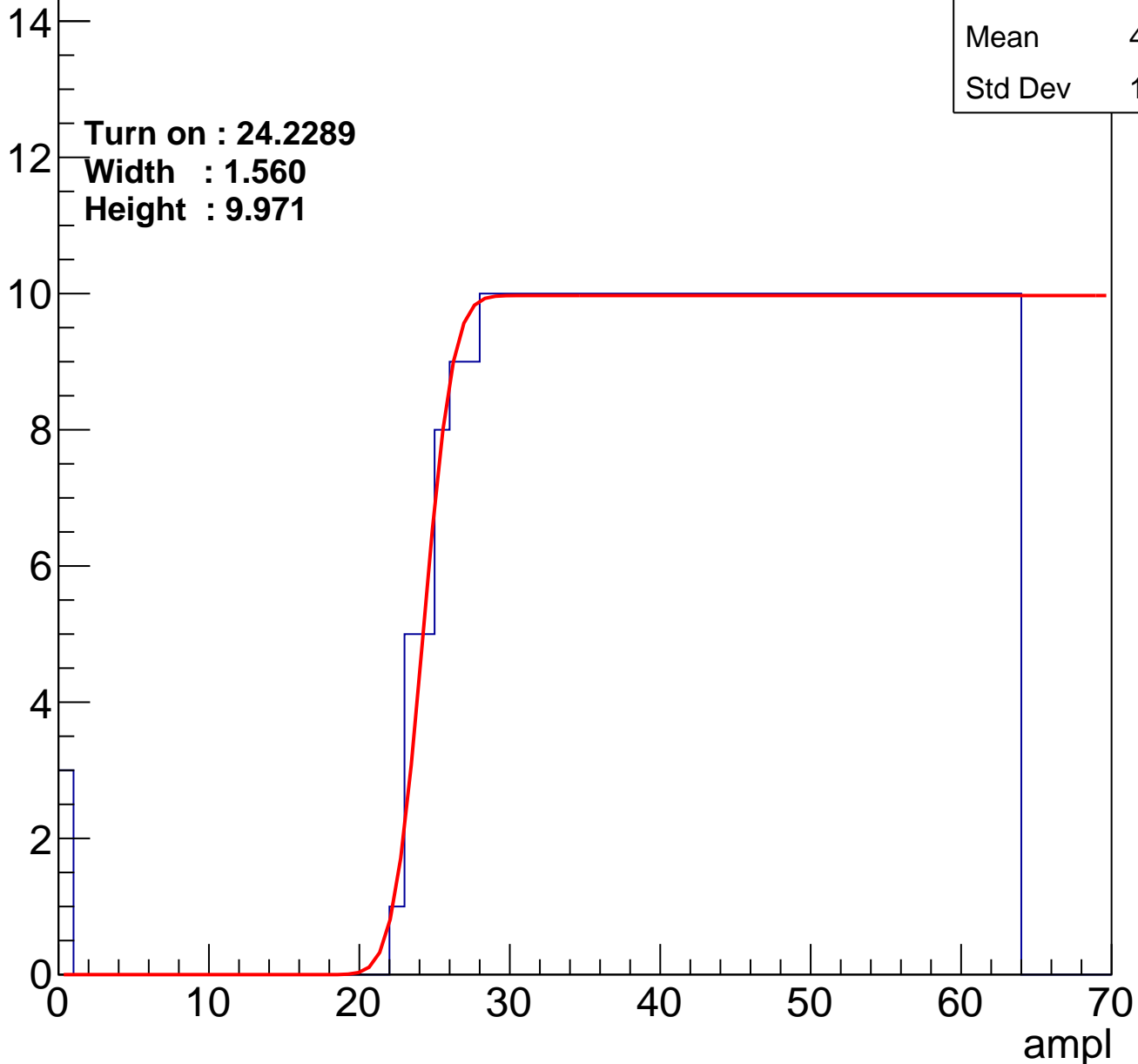
Entries	400
Mean	43.28
Std Dev	12.08

Turn on : 24.2289

Width : 1.560

Height : 9.971

Entry



# B1L002S, U11-ch24

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.9427

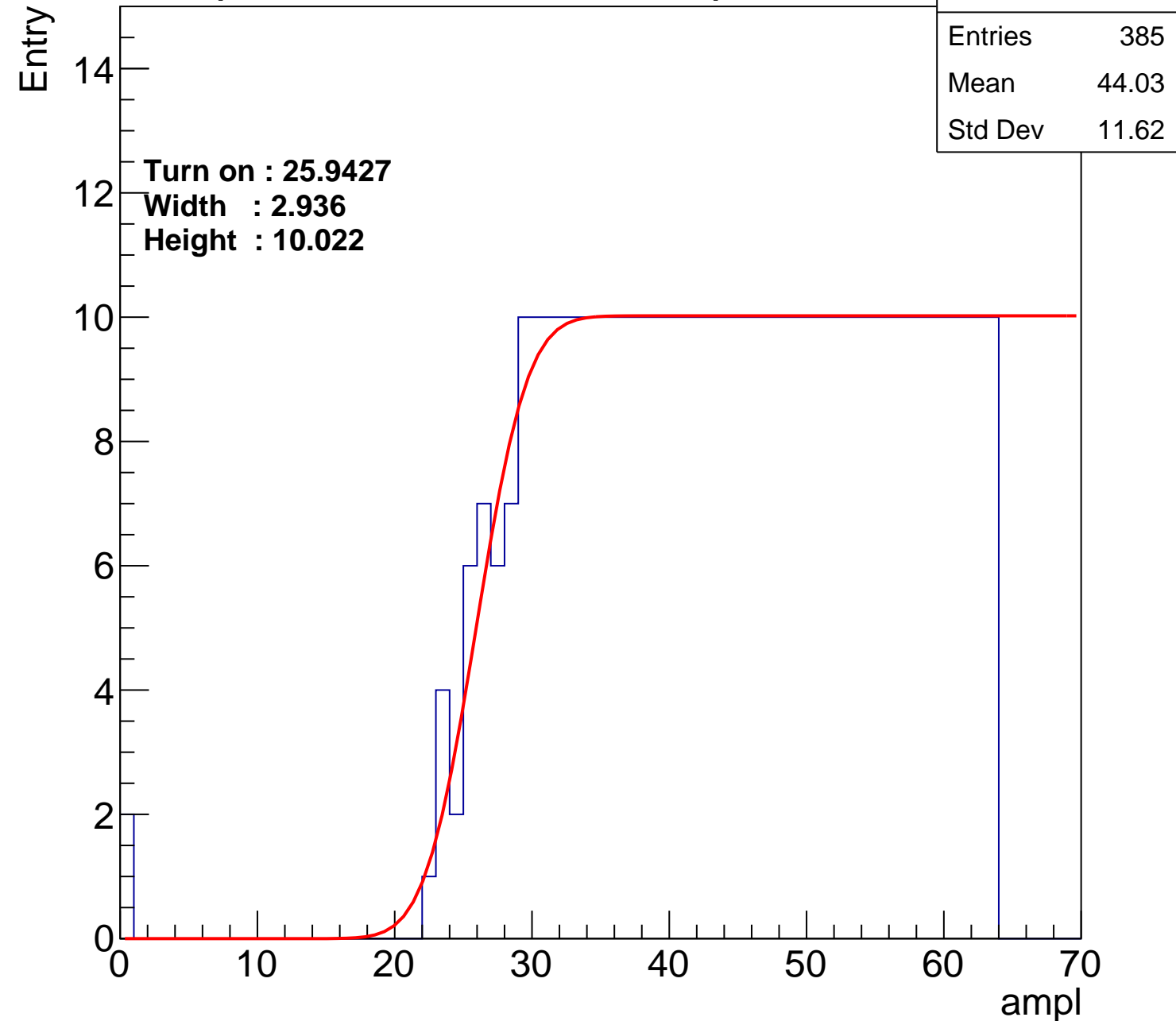
Width : 2.936

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch25

calib\_packv5\_042523\_0143.root, FC#10, port B3

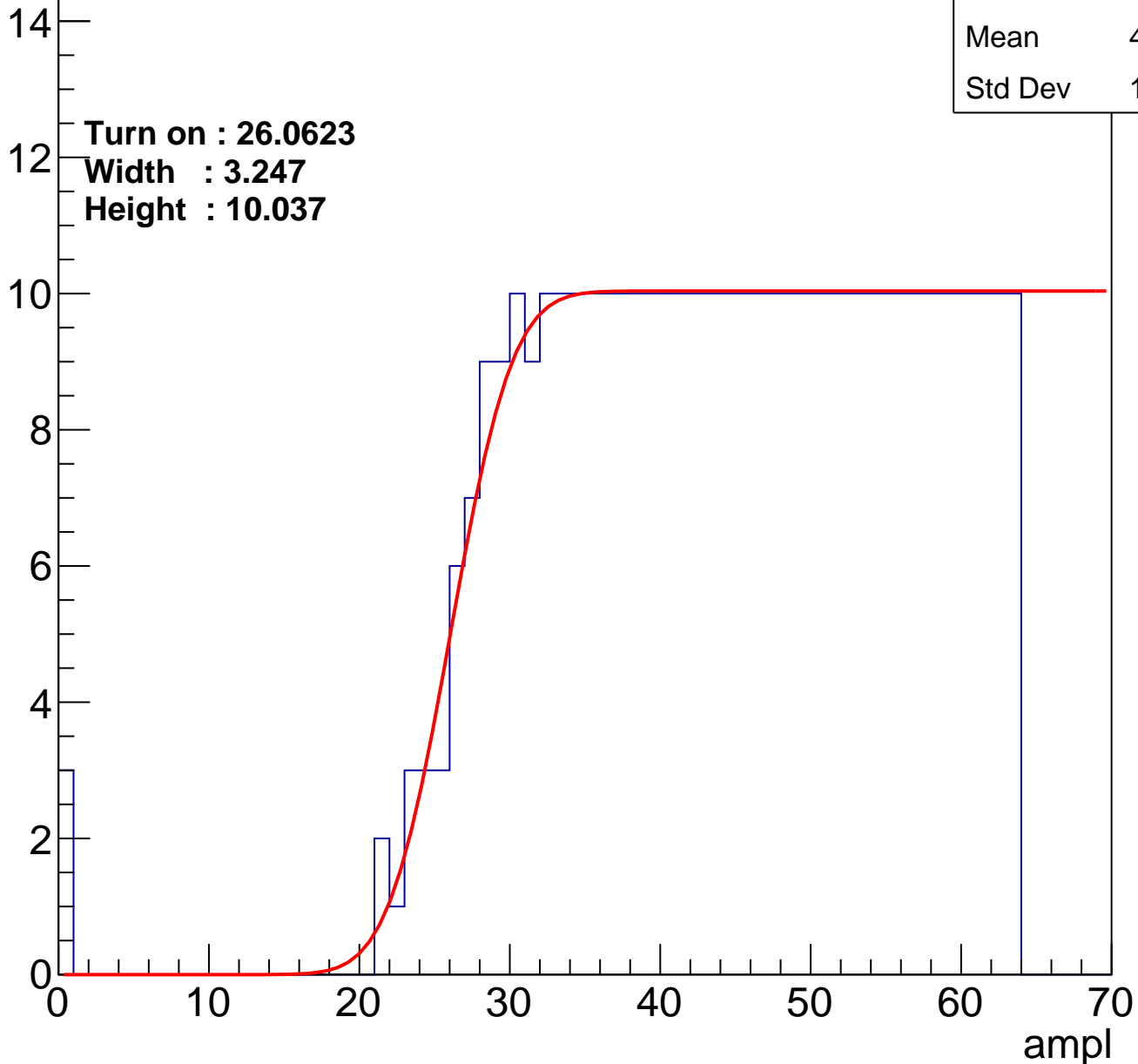
Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 26.0623

Width : 3.247

Height : 10.037

Entry



# B1L002S, U11-ch26

calib\_packv5\_042523\_0143.root, FC#10, port B3

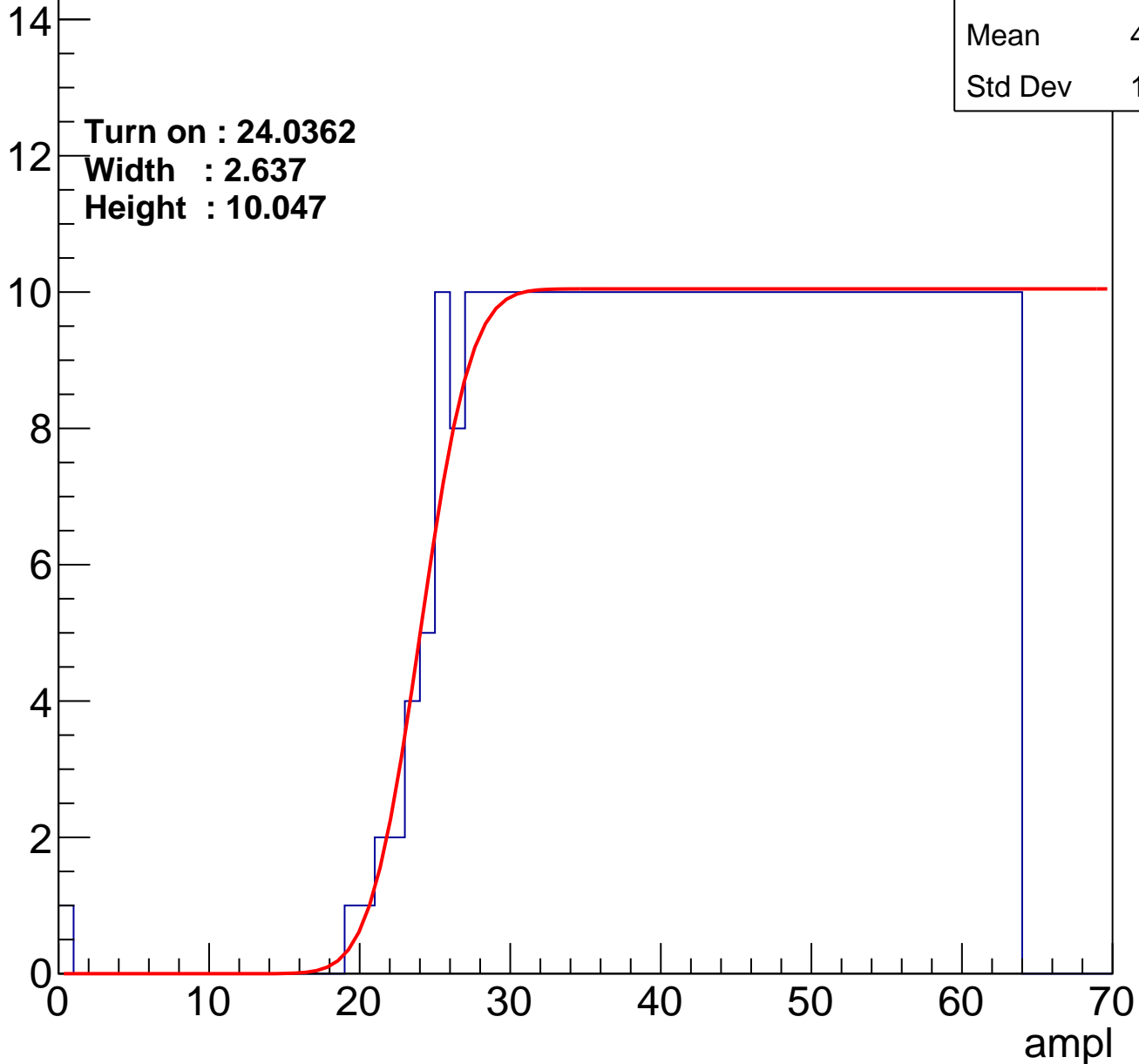
Entries	404
Mean	43.18
Std Dev	11.92

Turn on : 24.0362

Width : 2.637

Height : 10.047

Entry



# B1L002S, U11-ch27

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.24
Std Dev	11.56

Turn on : 26.7608

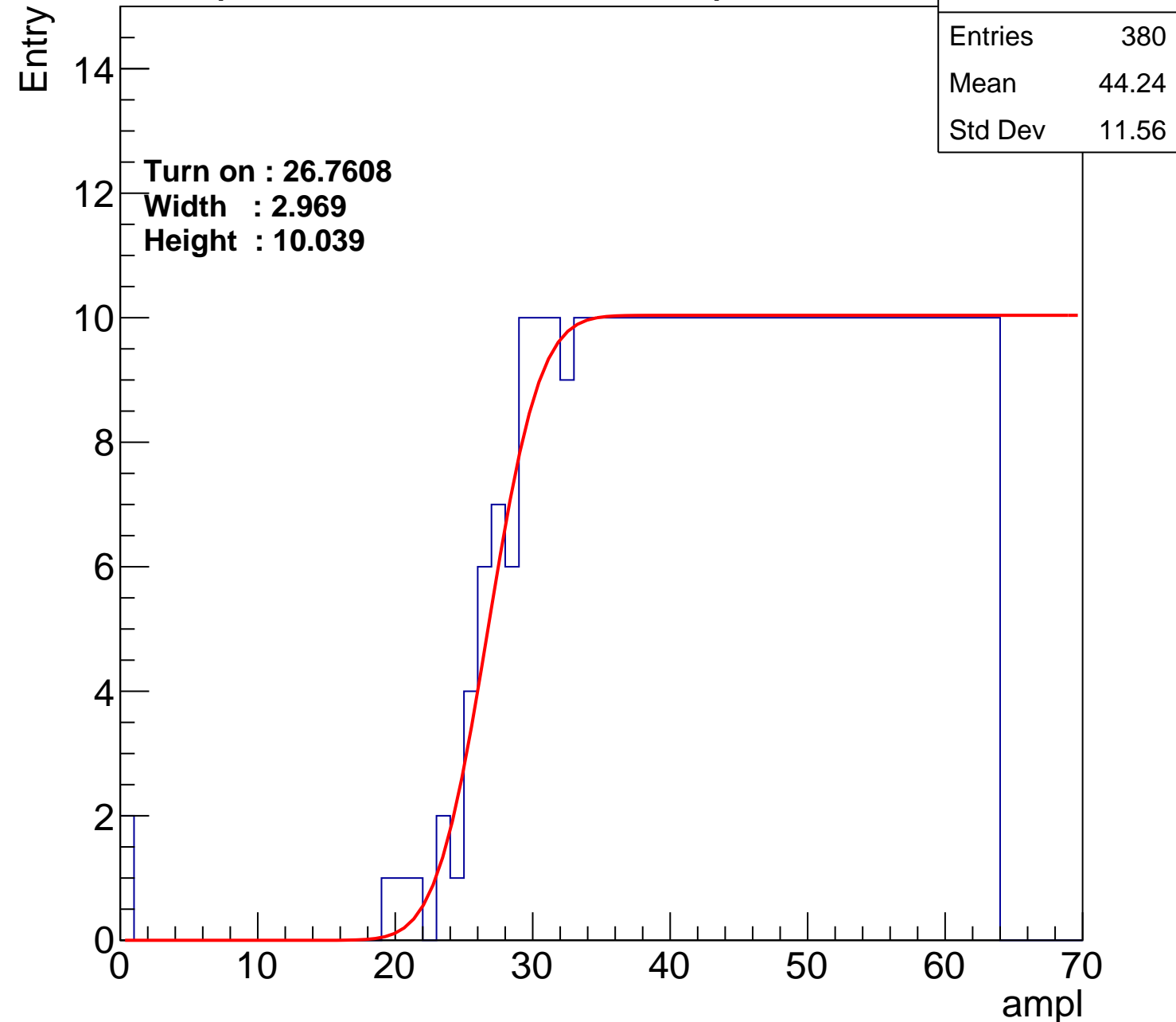
Width : 2.969

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch28

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.6255

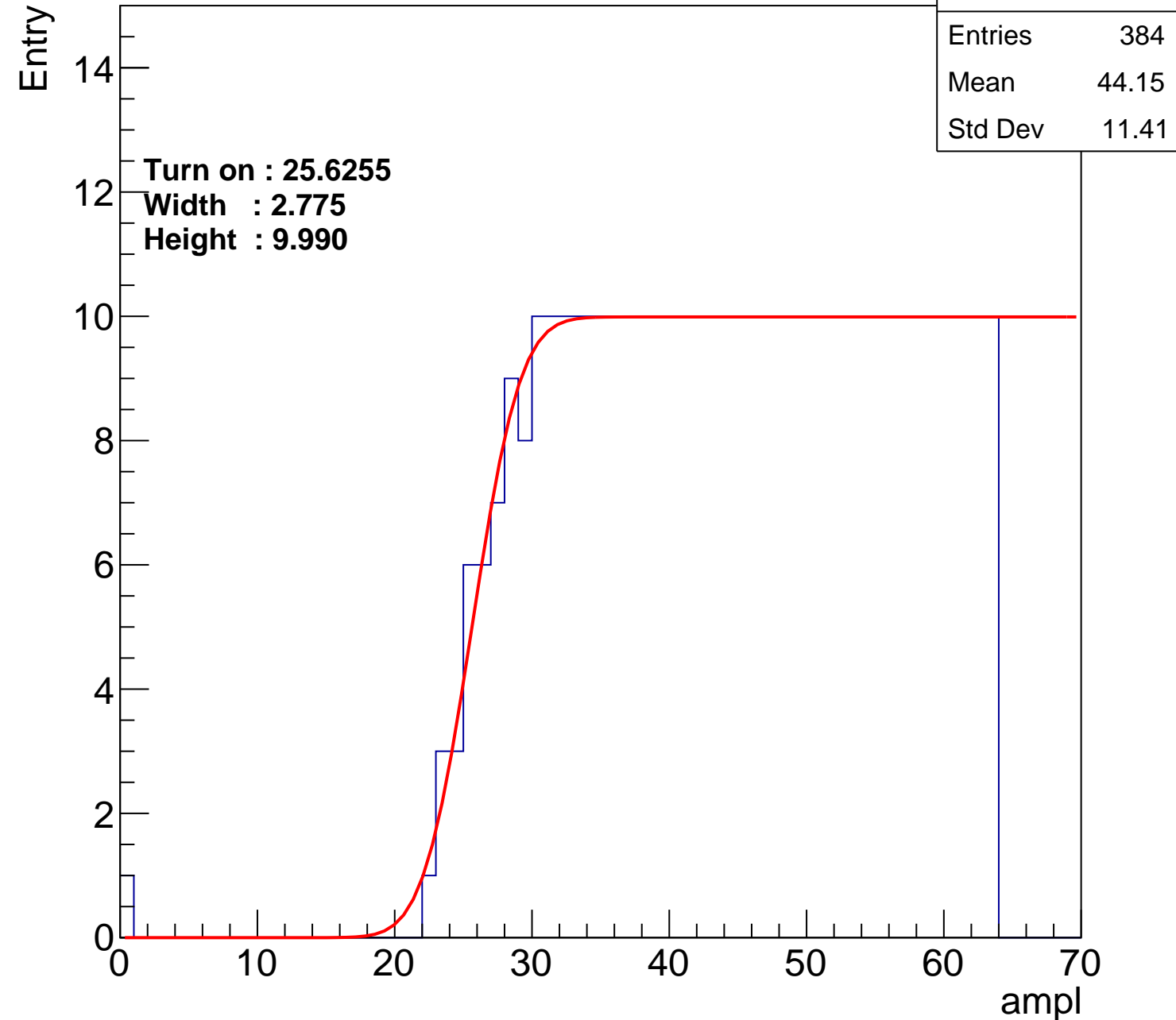
Width : 2.775

Height : 9.990

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch29

calib\_packv5\_042523\_0143.root, FC#10, port B3

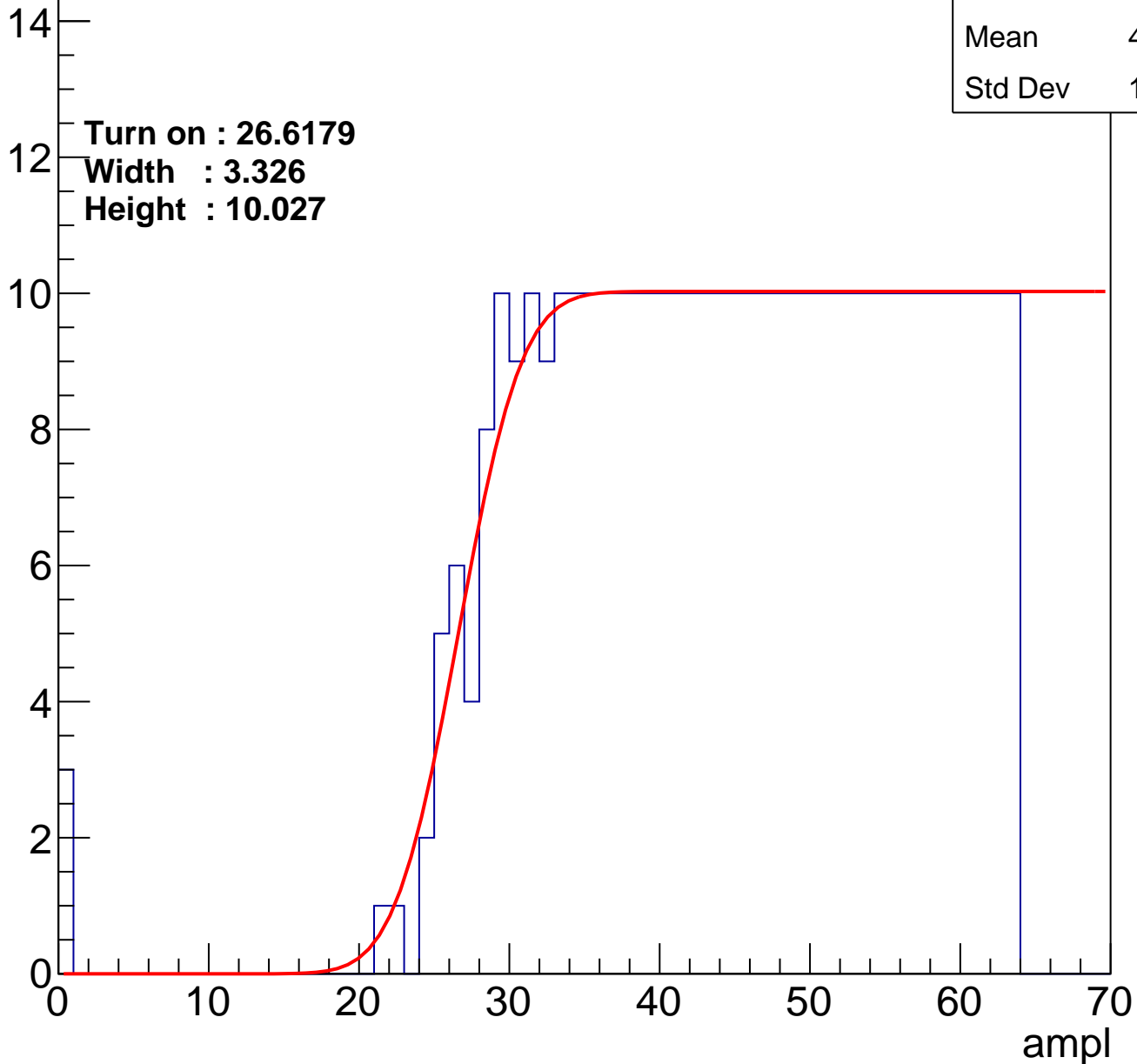
Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.6179

Width : 3.326

Height : 10.027

Entry



# B1L002S, U11-ch30

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.7183

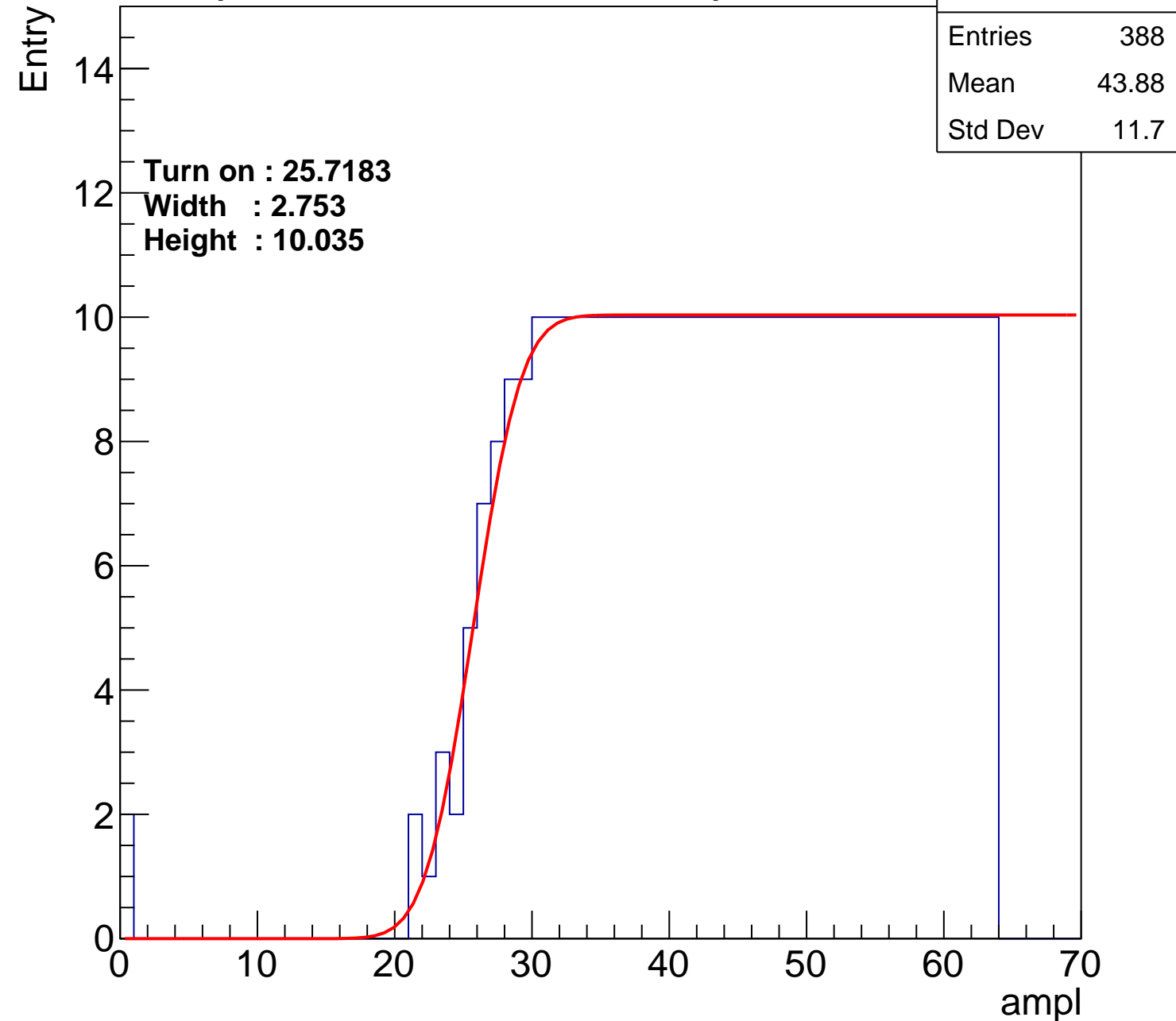
Width : 2.753

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch31

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.82
Std Dev	11.08

Turn on : 27.2391

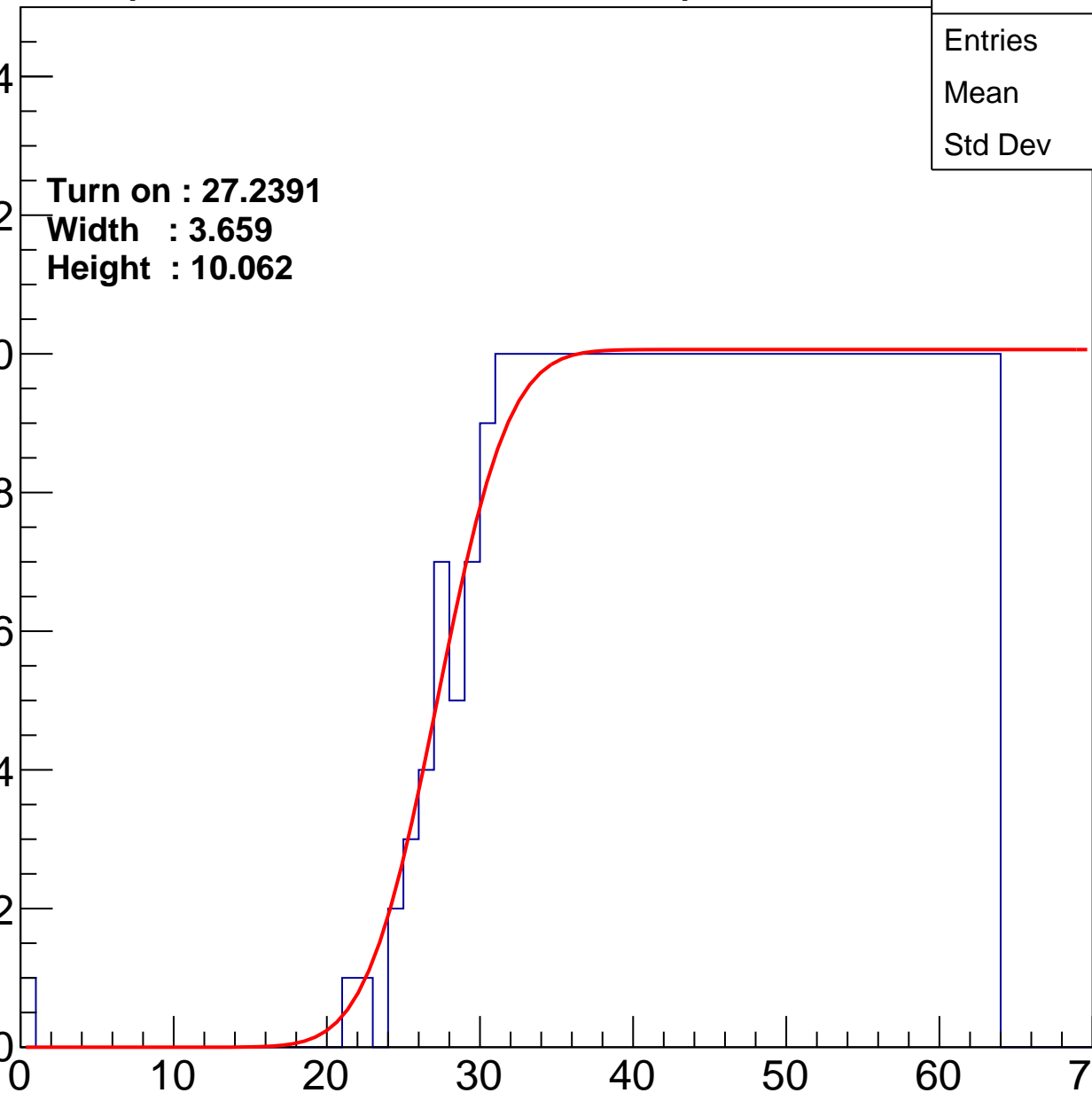
Width : 3.659

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch32

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 25.6793

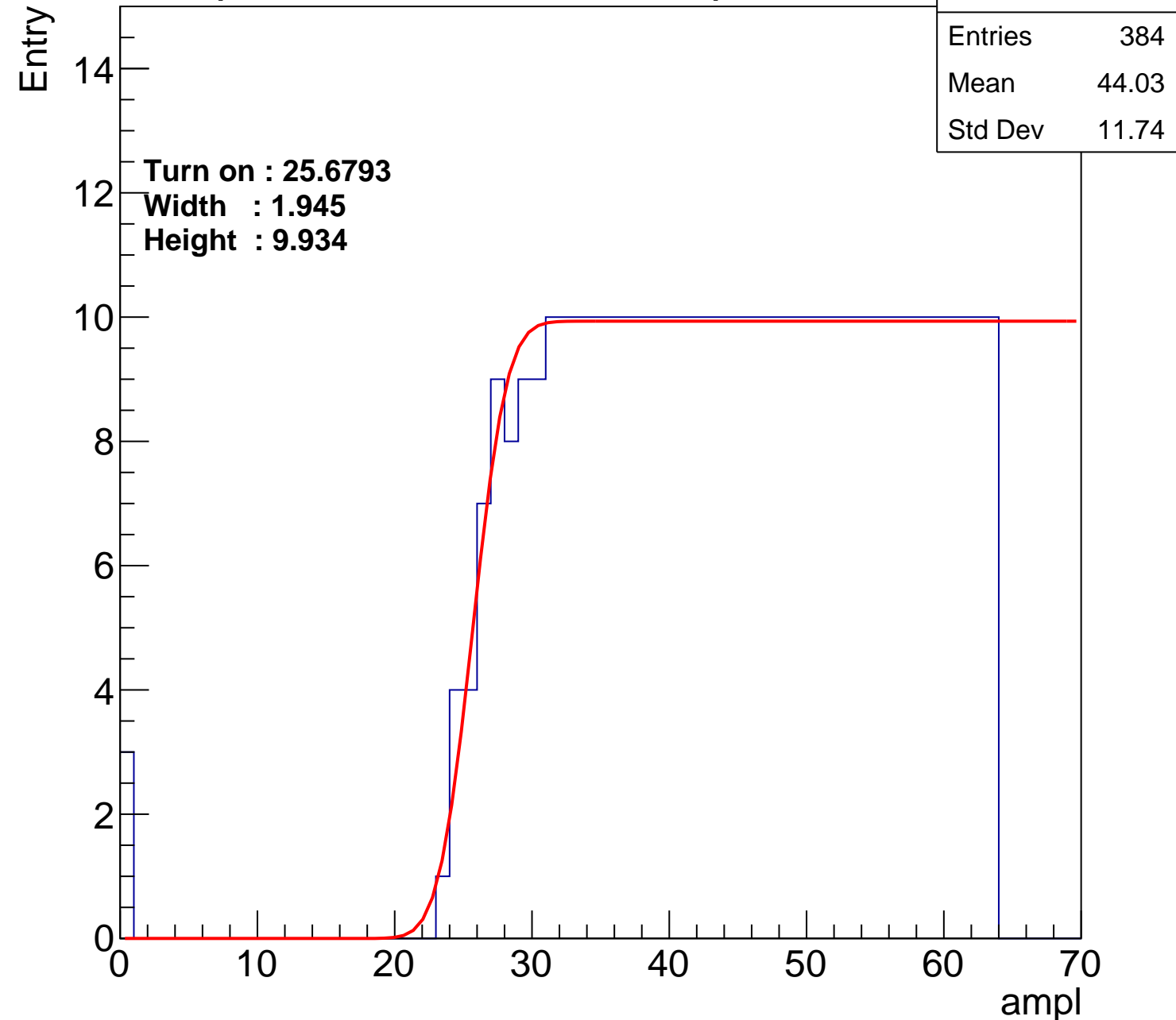
Width : 1.945

Height : 9.934

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch33

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	376
Mean	44.41
Std Dev	11.56

Turn on : 26.5793

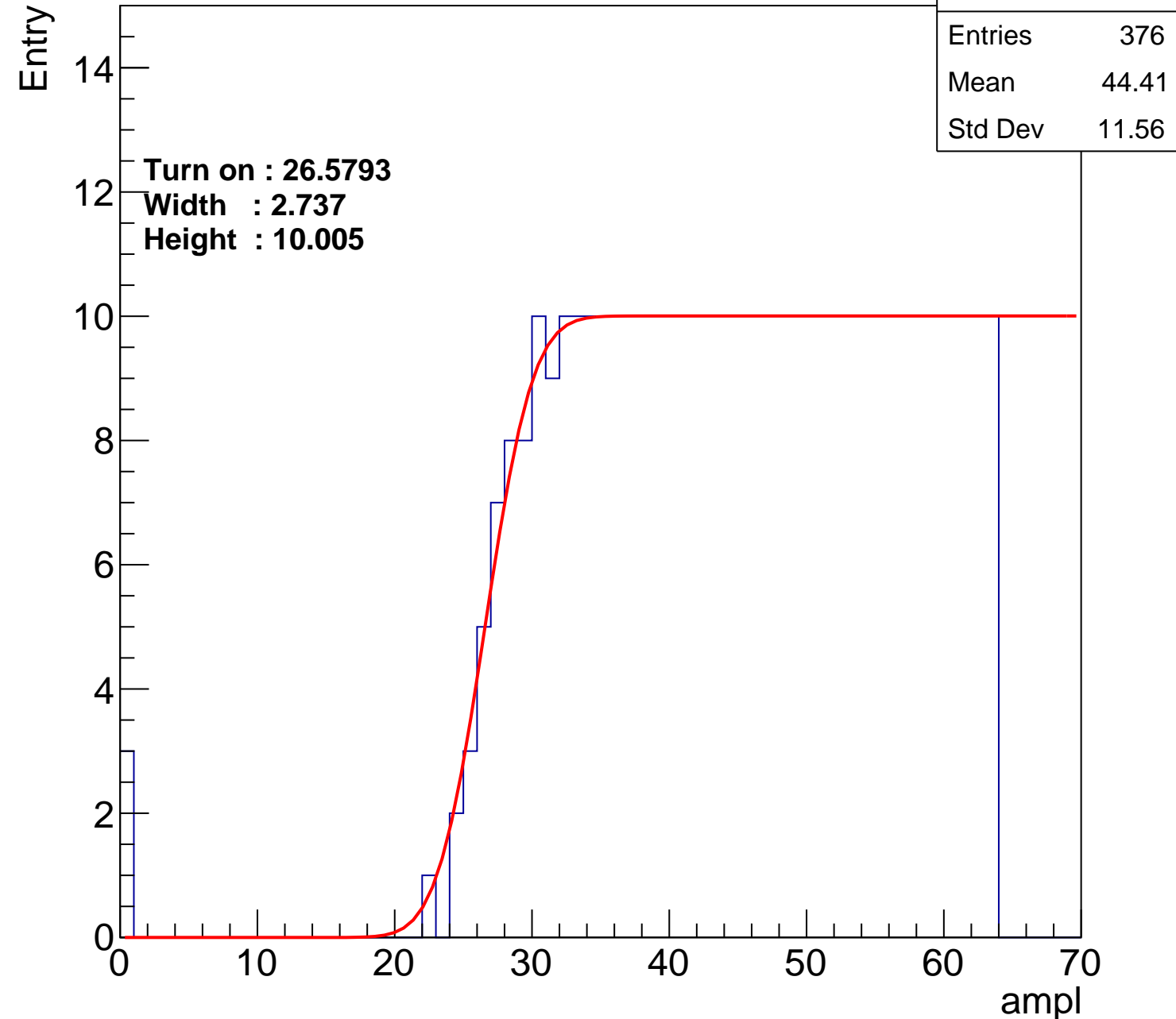
Width : 2.737

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch34

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.5292

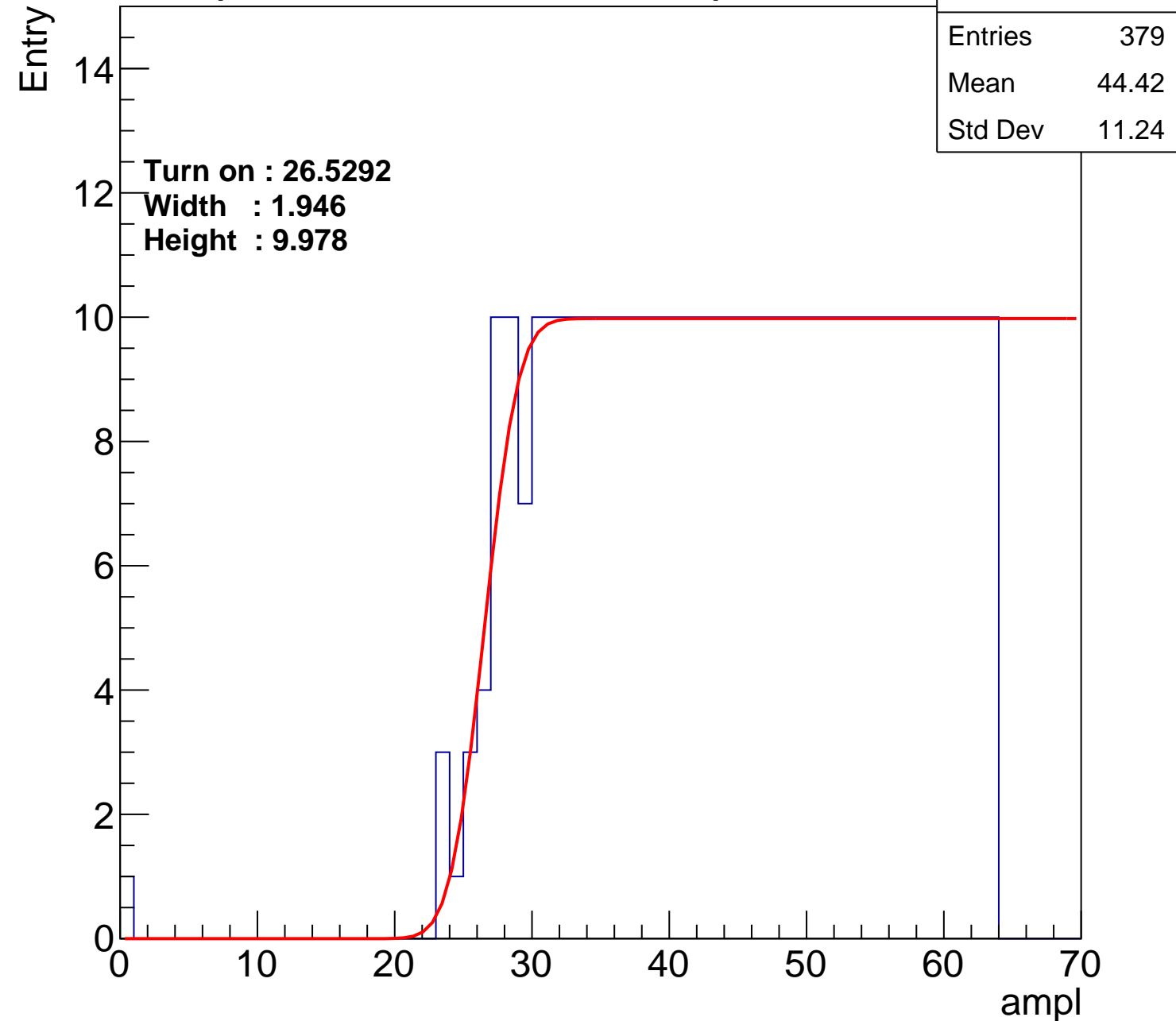
Width : 1.946

Height : 9.978

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch35

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	44.03
Std Dev	11.56

Turn on : 25.2864

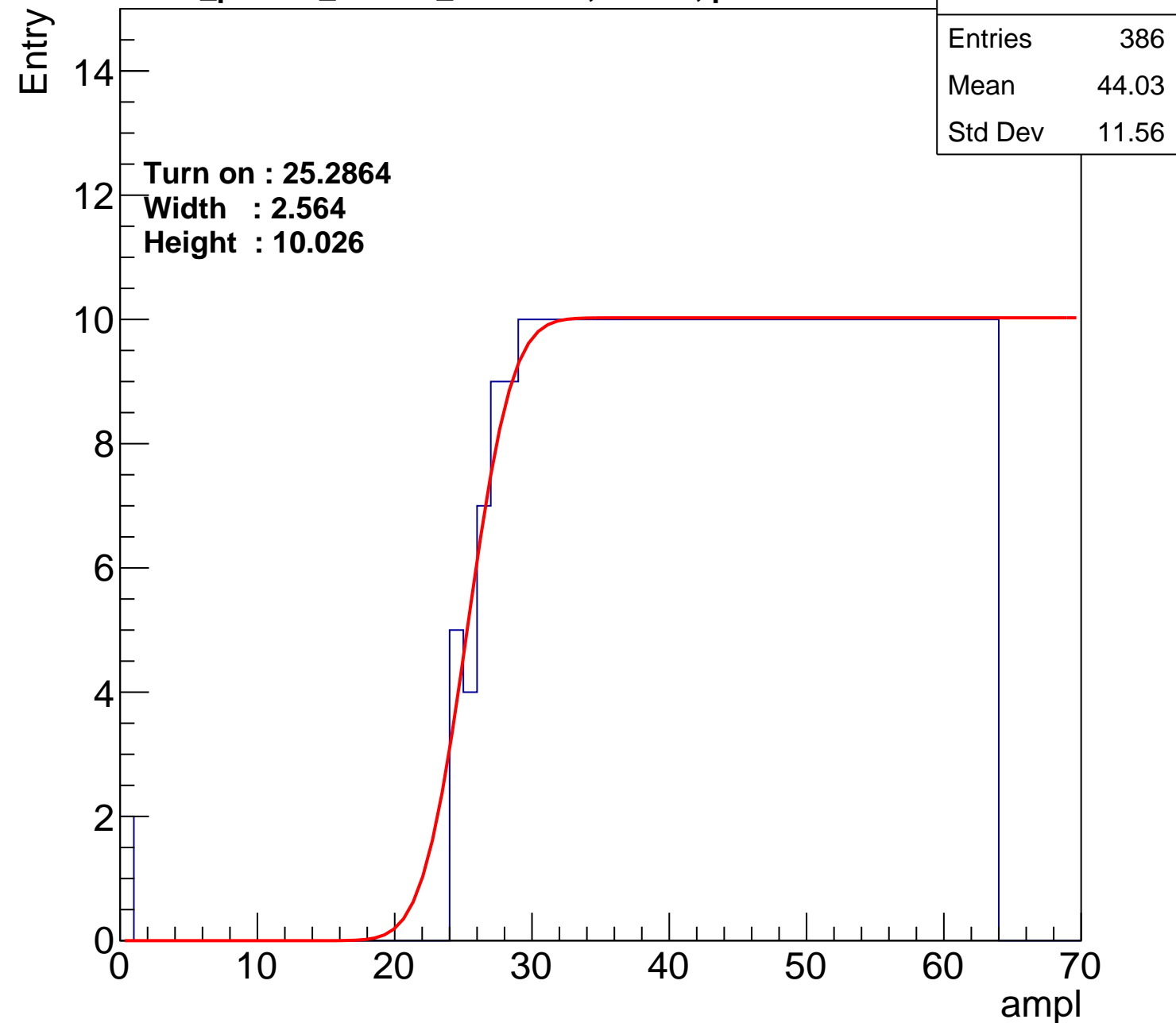
Width : 2.564

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch36

calib\_packv5\_042523\_0143.root, FC#10, port B3

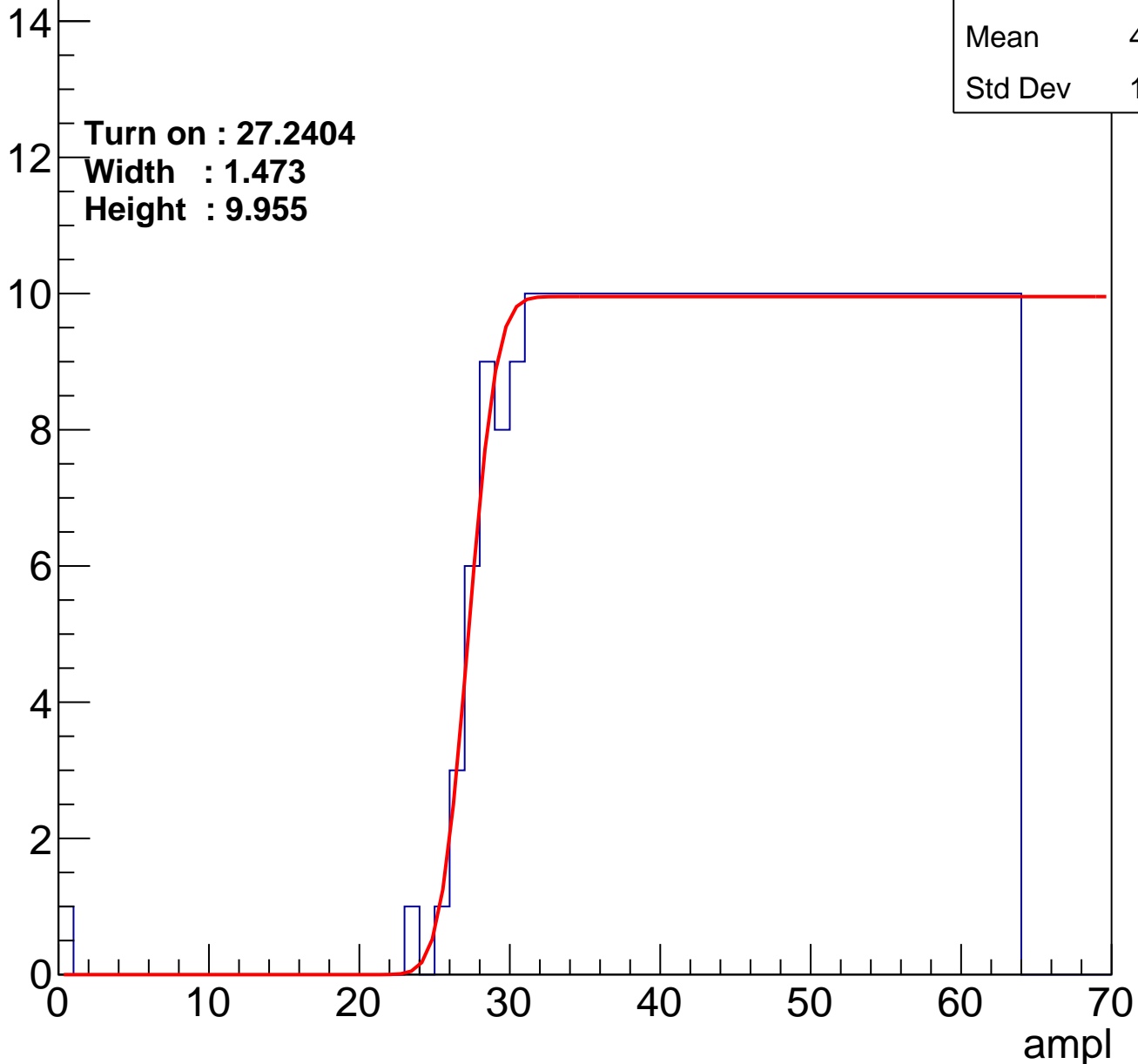
Entries	368
Mean	44.98
Std Dev	10.92

Turn on : 27.2404

Width : 1.473

Height : 9.955

Entry



# B1L002S, U11-ch37

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.82
Std Dev	11.06

Turn on : 27.0910

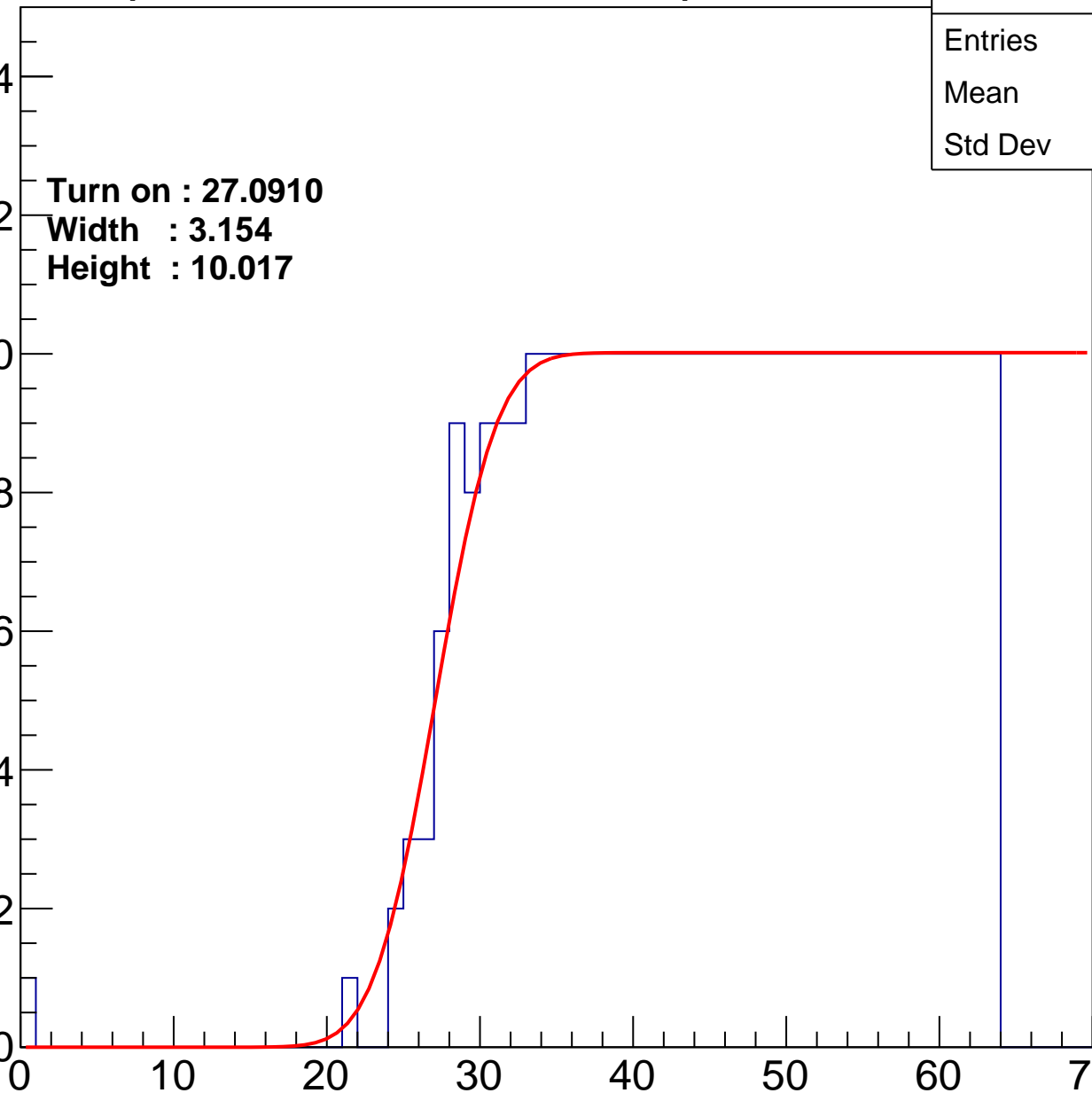
Width : 3.154

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch38

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.69
Std Dev	12.09

Turn on : 25.7988

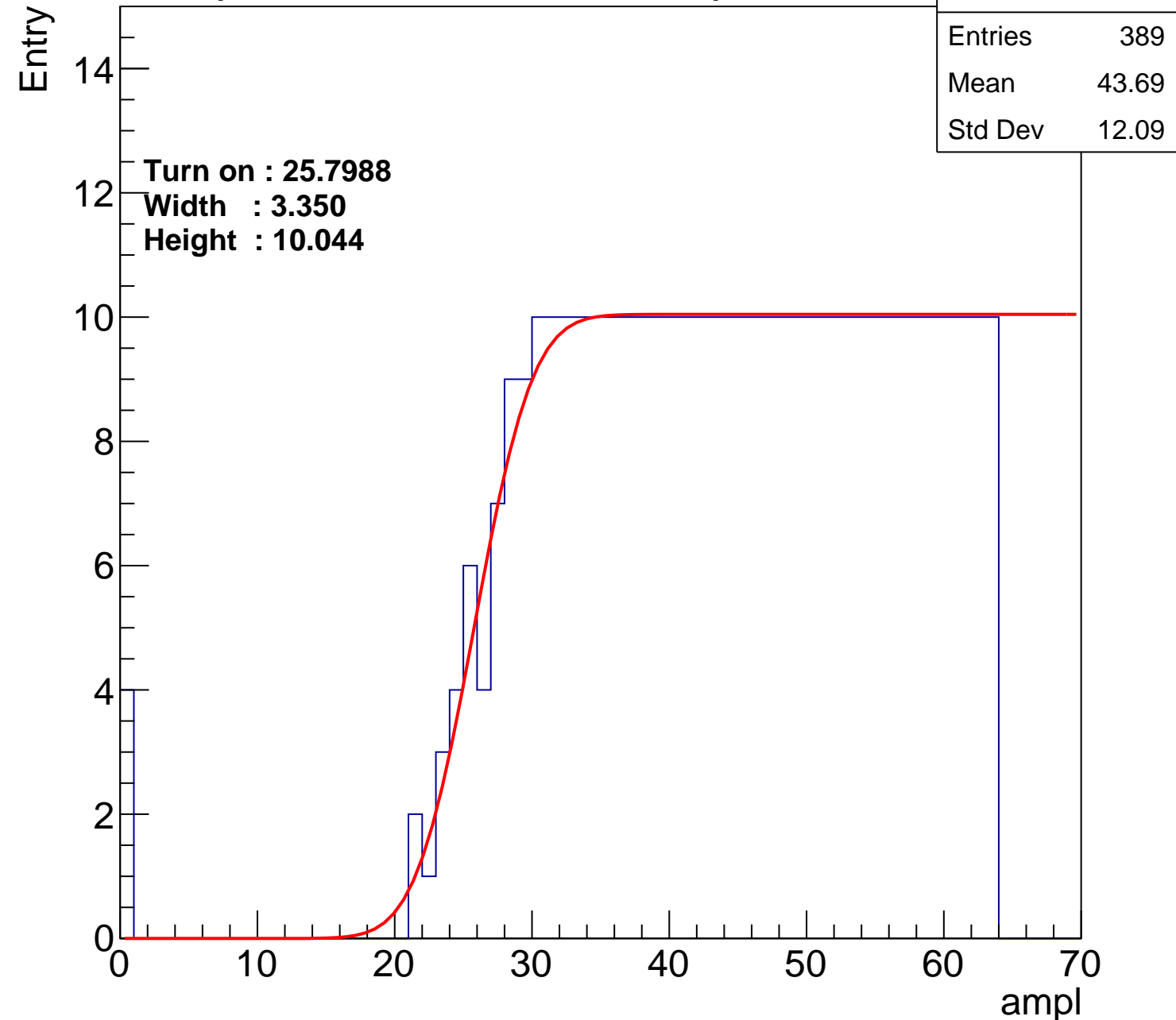
Width : 3.350

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch39

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	367
Mean	45.01
Std Dev	10.91

Turn on : 27.3737

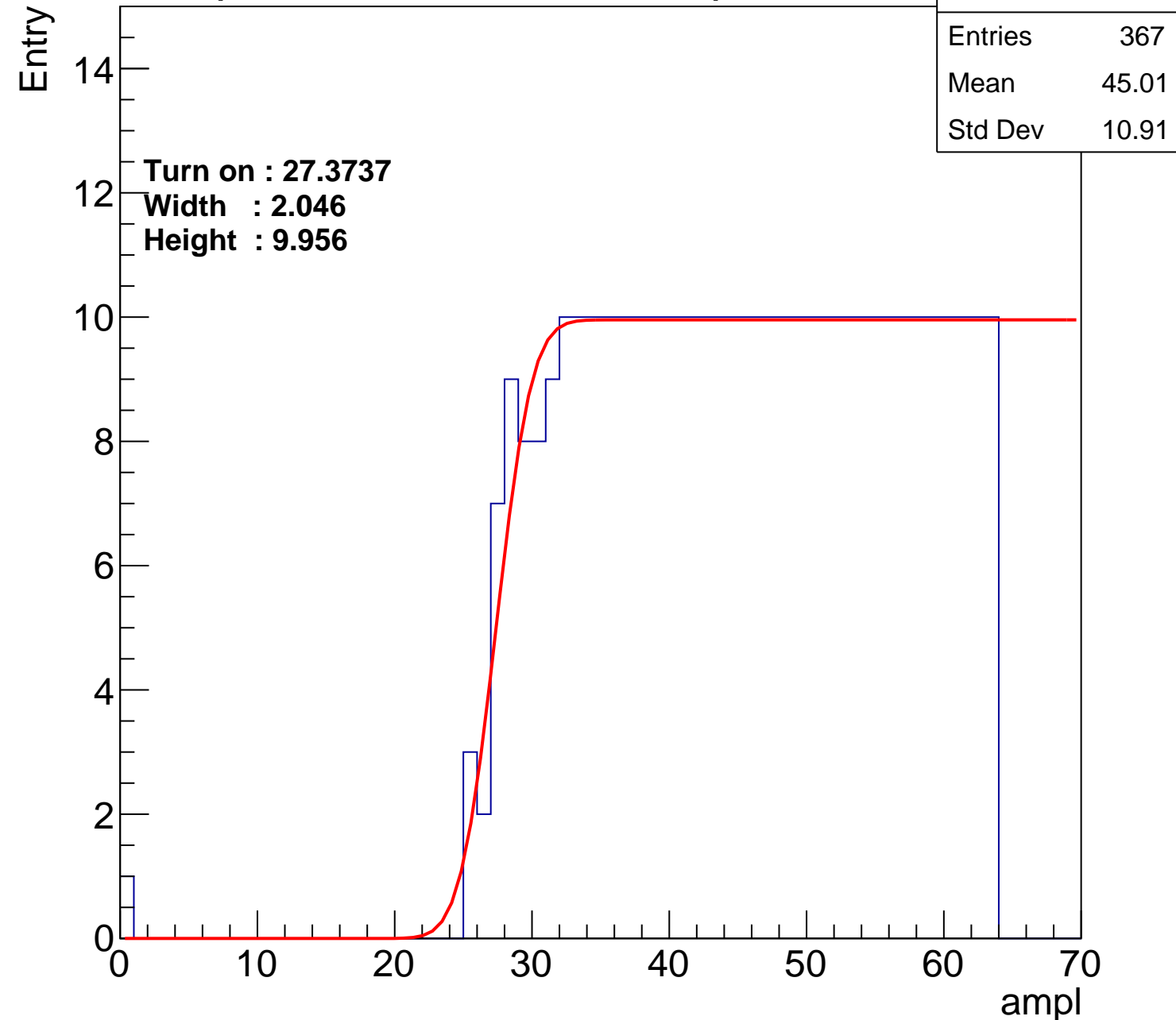
Width : 2.046

Height : 9.956

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch40

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.8405

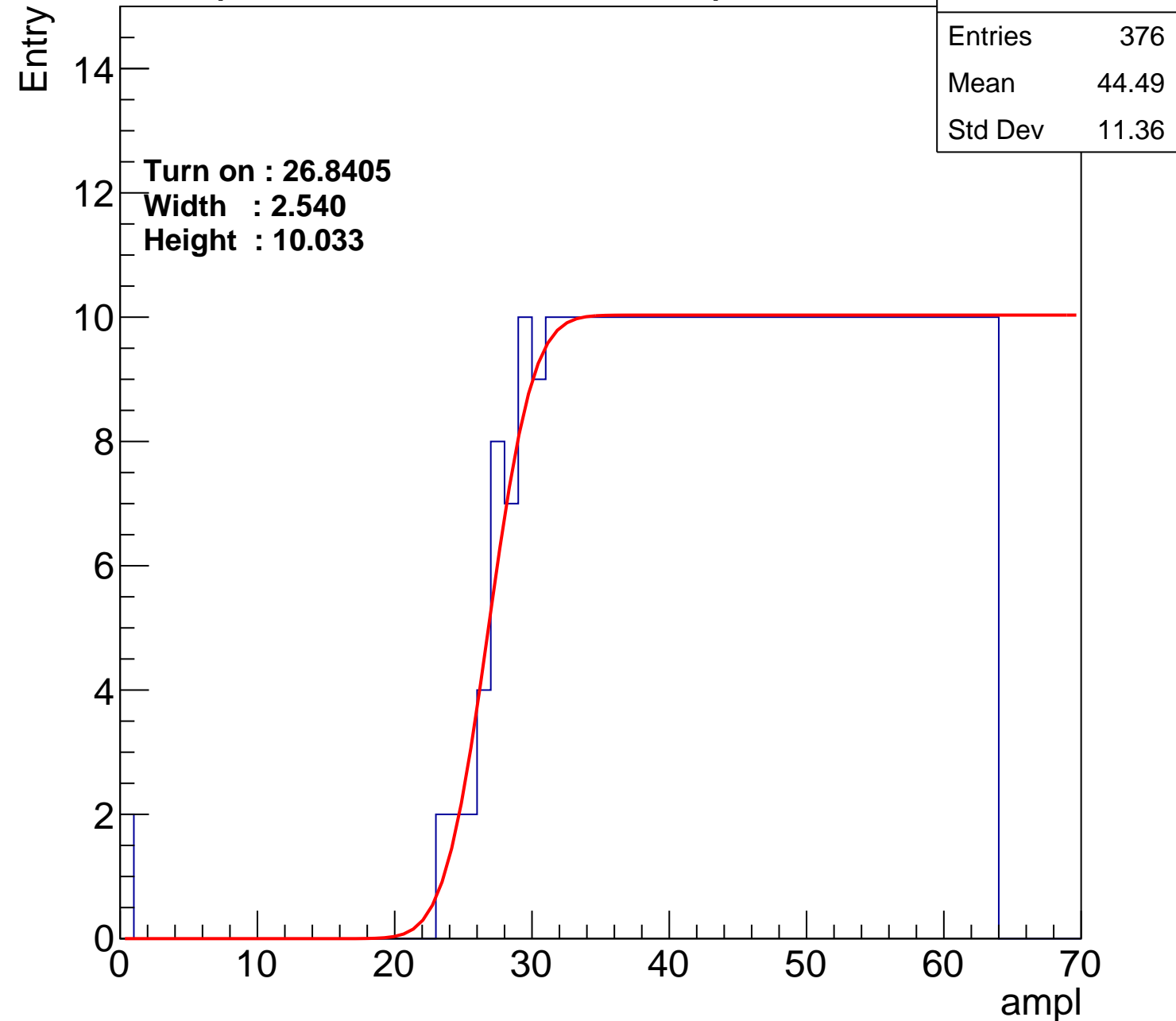
Width : 2.540

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch41

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.76
Std Dev	11.78

Turn on : 25.6098

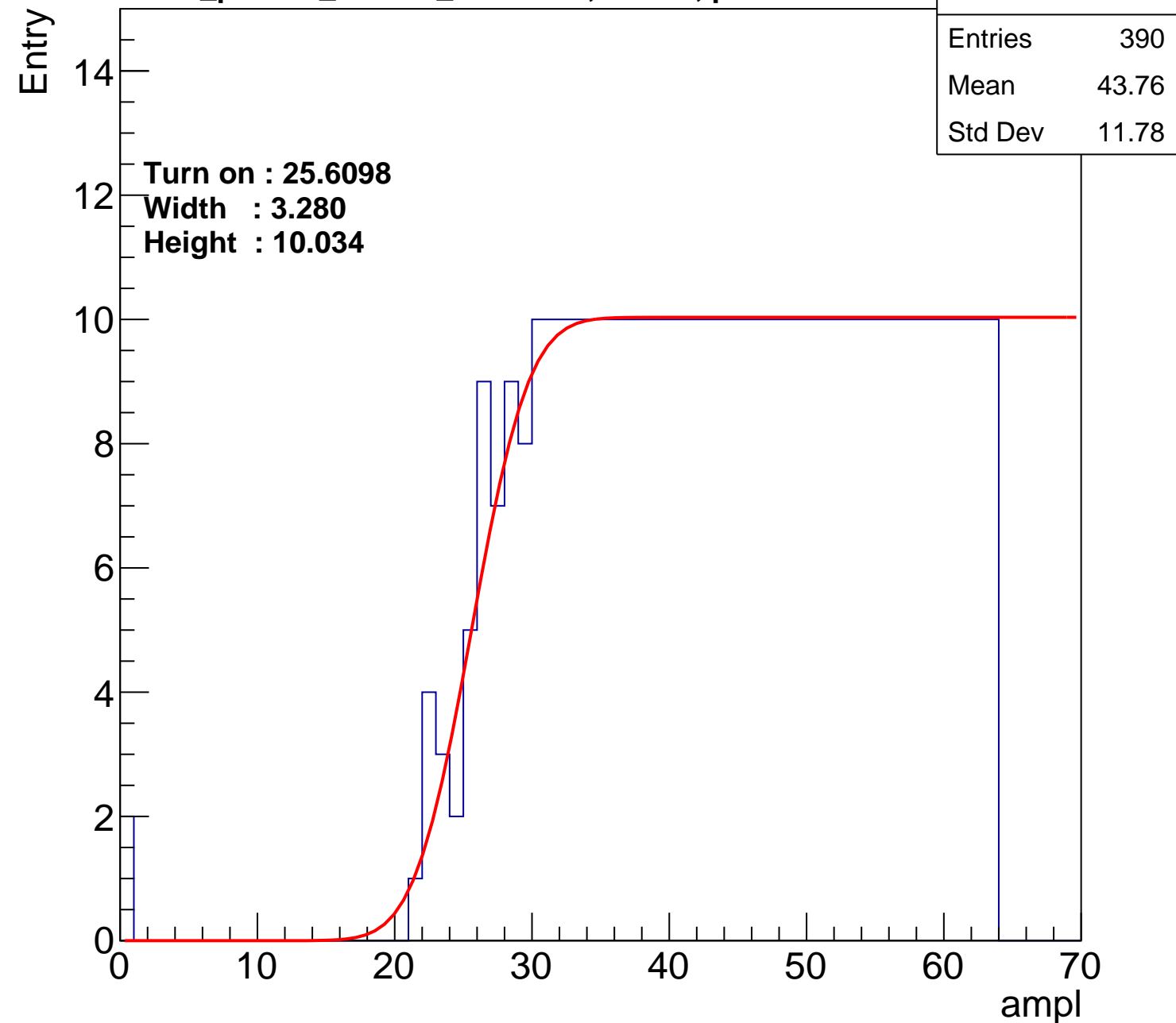
Width : 3.280

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch42

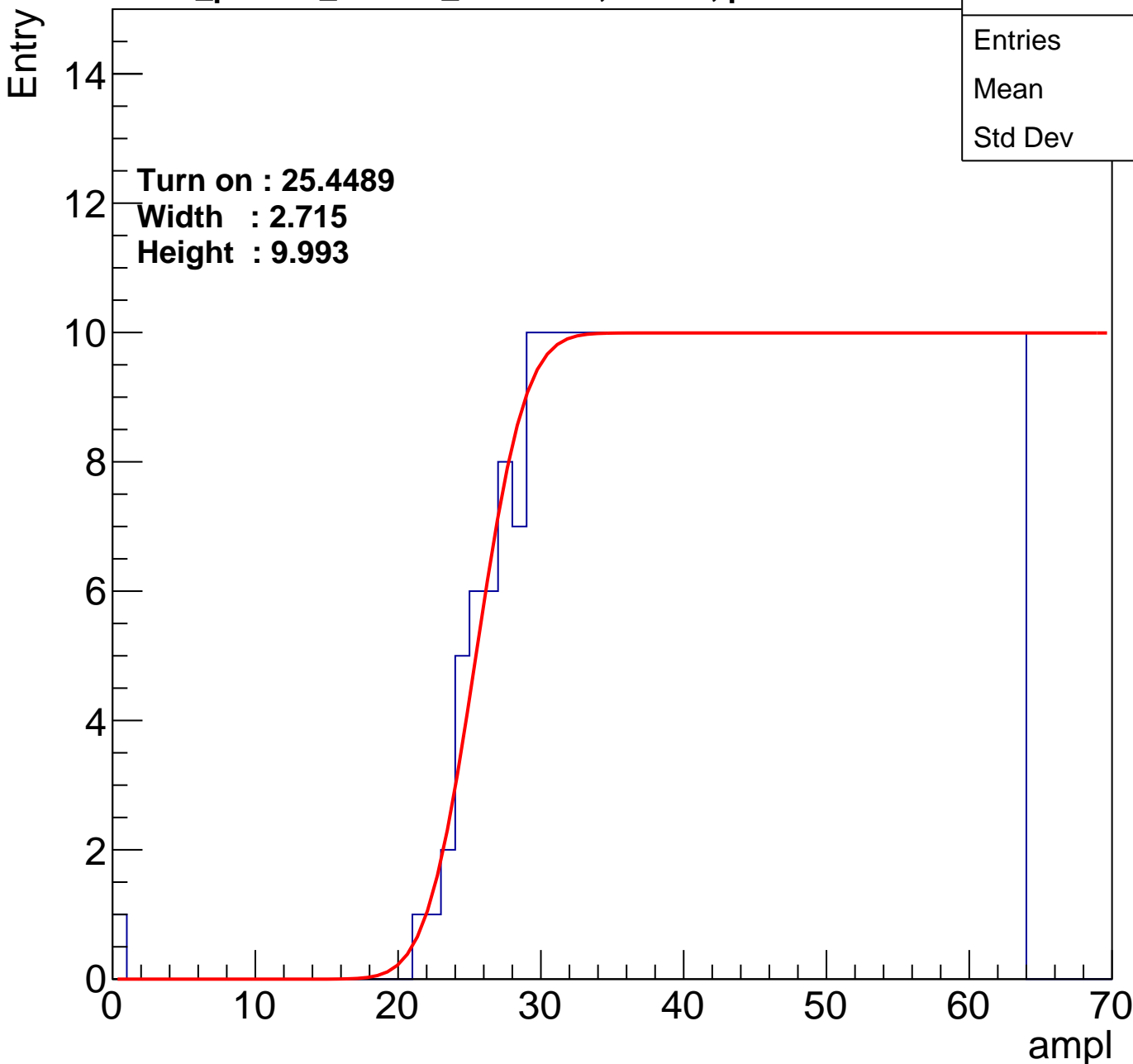
**calib\_packv5\_042523\_0143.root, FC#10, port B3**

Entries	387
Mean	44
Std Dev	11.5

**Turn on : 25.4489**

**Width : 2.715**

**Height : 9.993**



# B1L002S, U11-ch43

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 27.4474

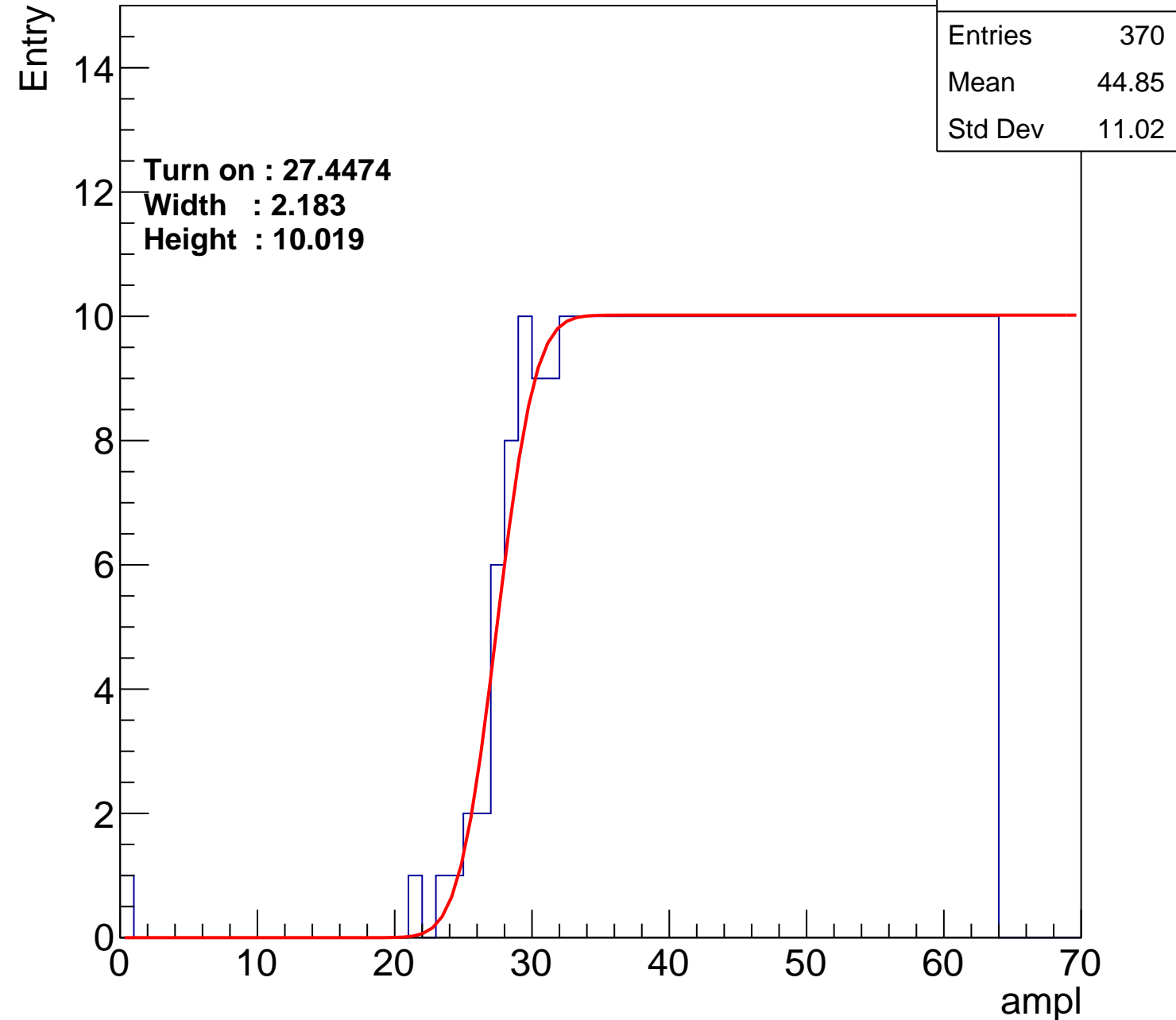
Width : 2.183

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch44

calib\_packv5\_042523\_0143.root, FC#10, port B3

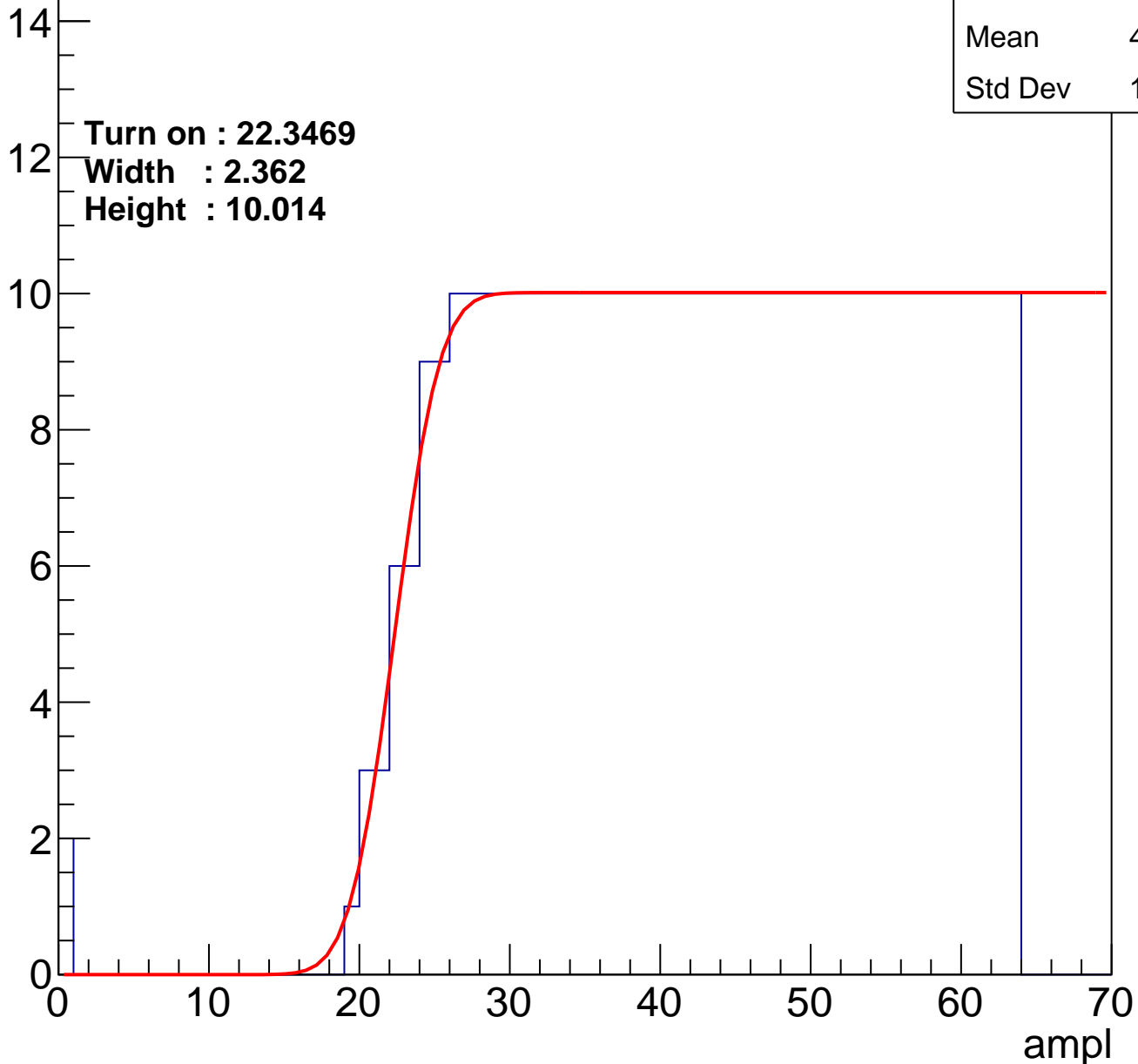
Entries	419
Mean	42.39
Std Dev	12.45

Turn on : 22.3469

Width : 2.362

Height : 10.014

Entry



# B1L002S, U11-ch45

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	357
Mean	45.44
Std Dev	10.86

Turn on : 28.6620

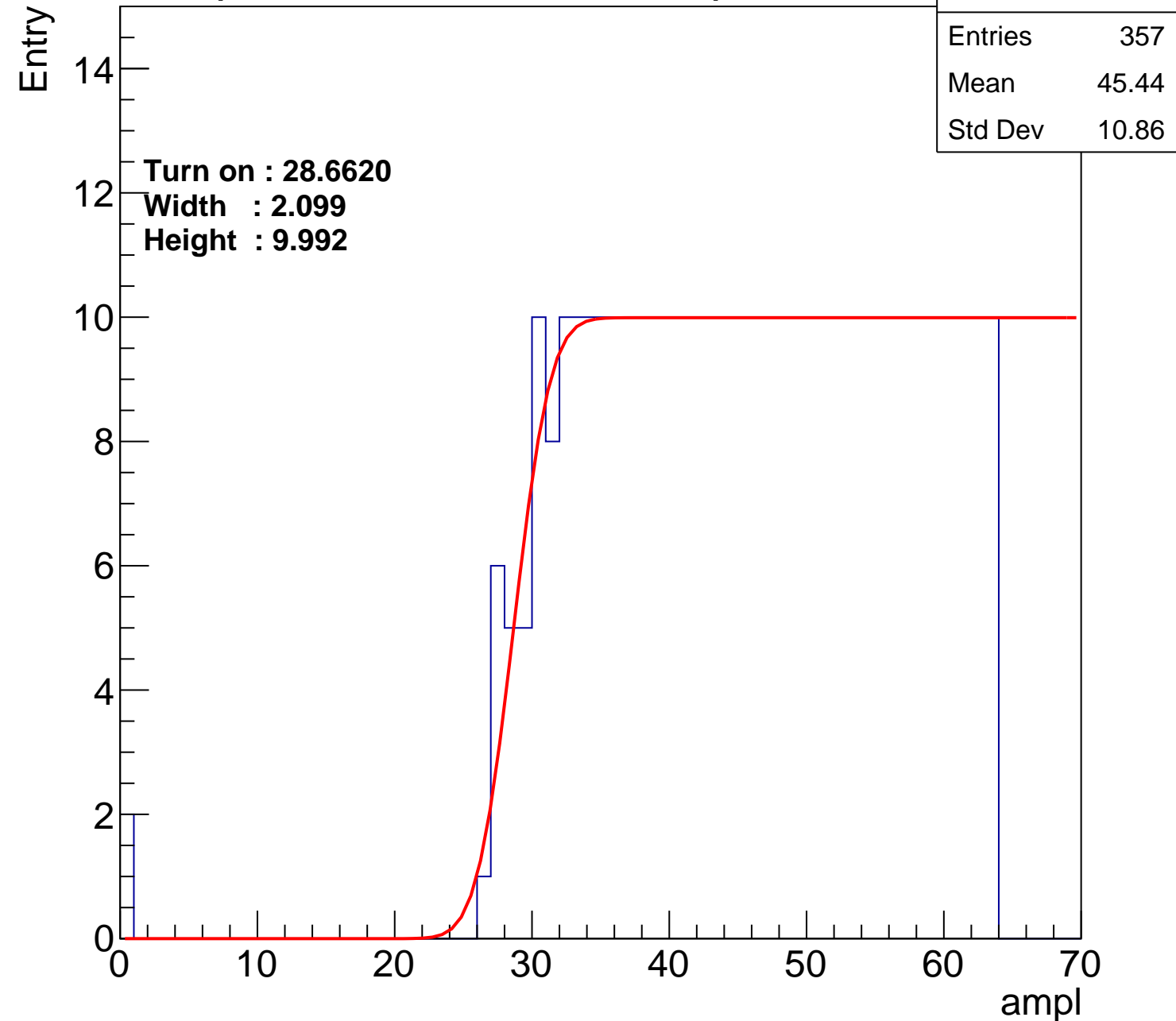
Width : 2.099

Height : 9.992

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch46

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.51
Std Dev	11.2

Turn on : 26.6094

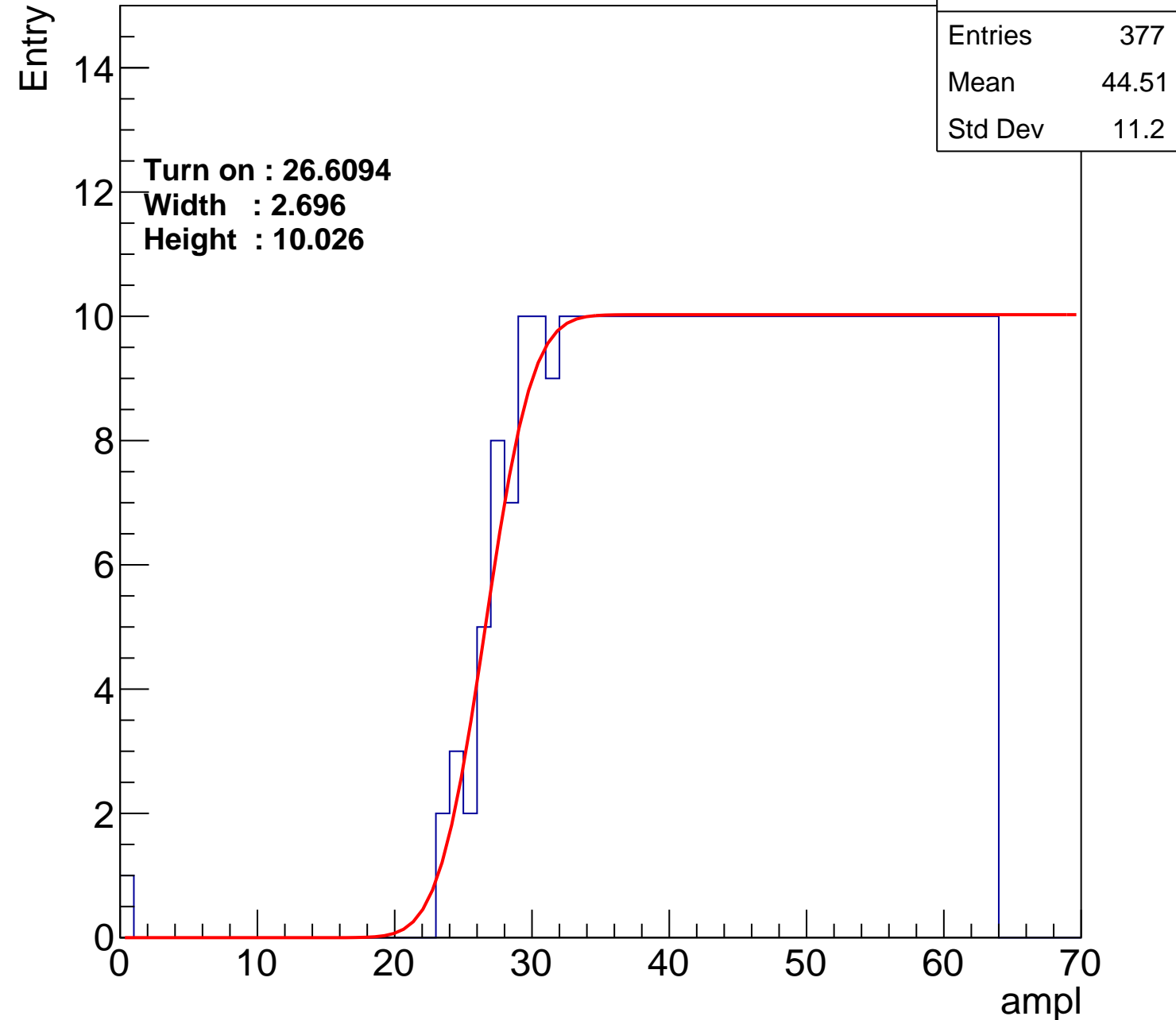
Width : 2.696

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch47

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.52
Std Dev	12.14

Turn on : 25.5342

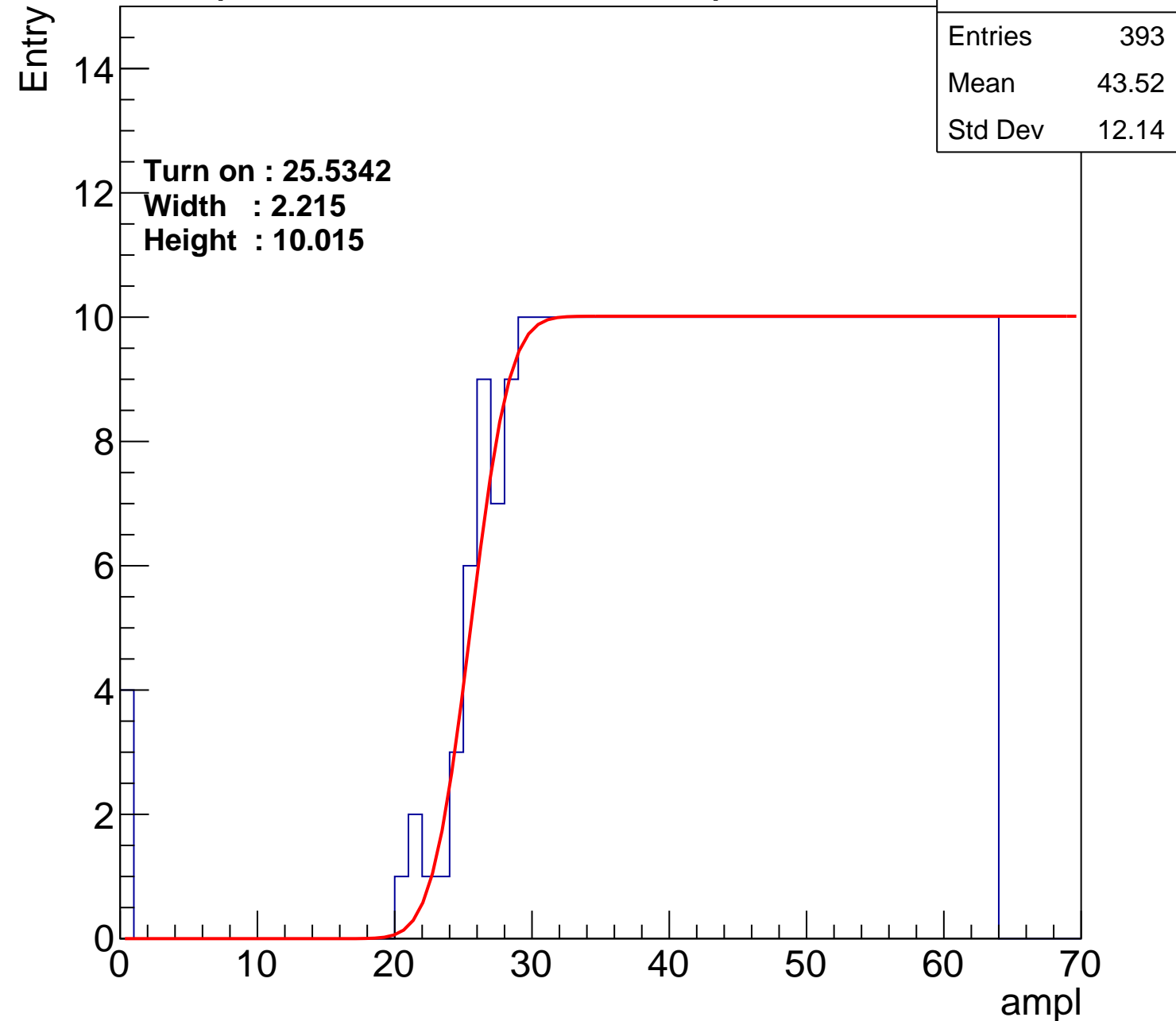
Width : 2.215

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch48

calib\_packv5\_042523\_0143.root, FC#10, port B3

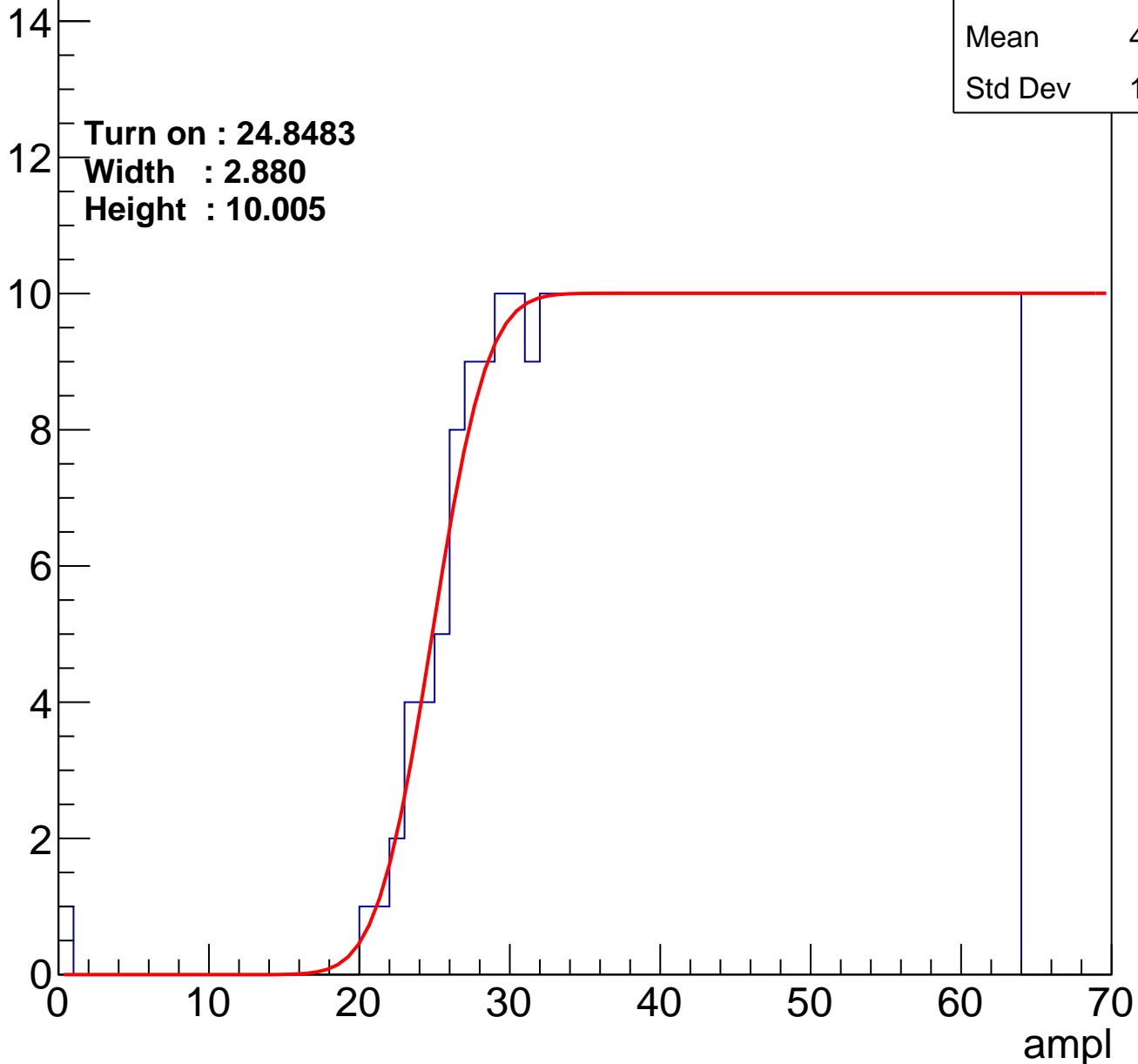
Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 24.8483

Width : 2.880

Height : 10.005

Entry



# B1L002S, U11-ch49

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.8043

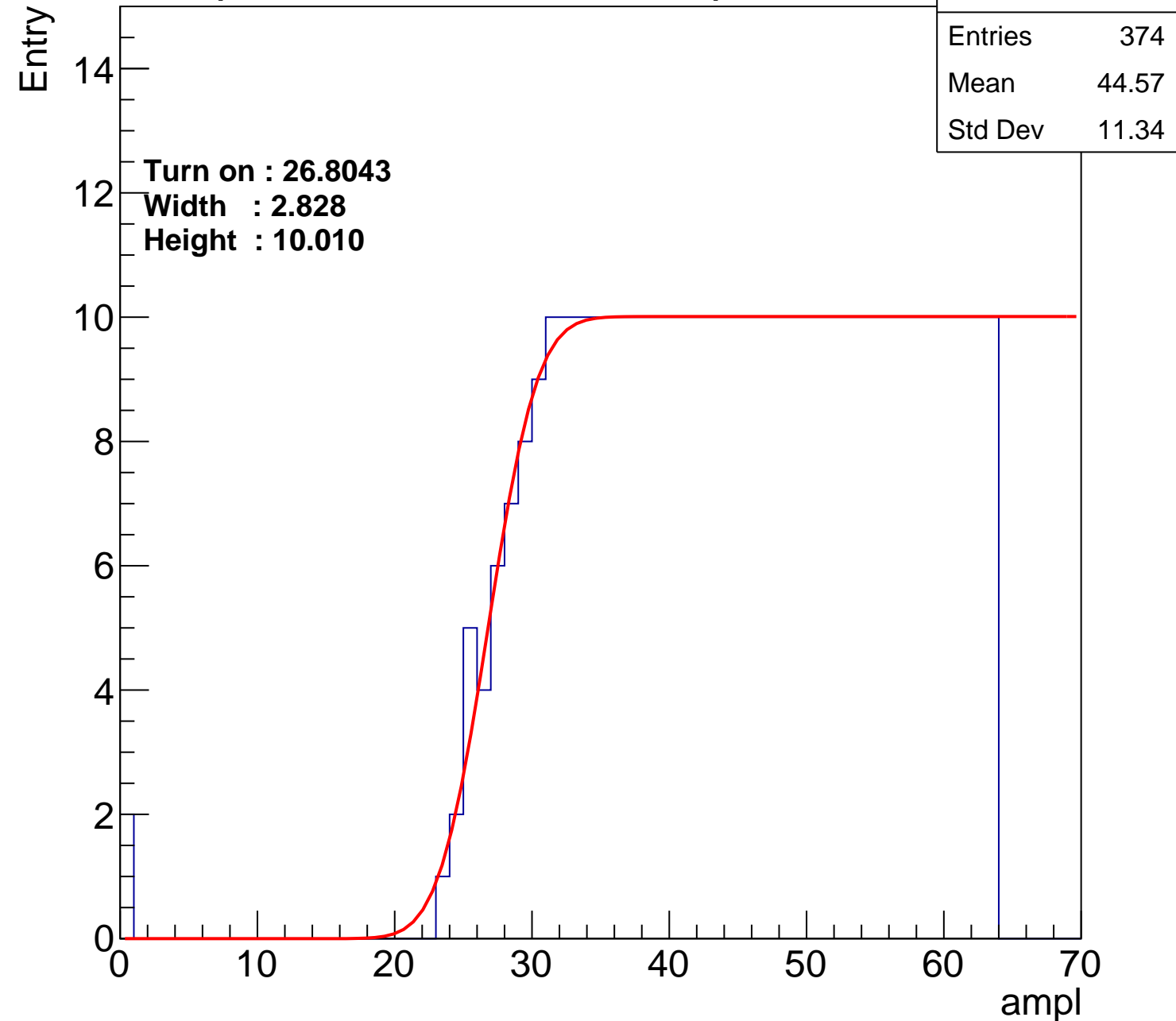
Width : 2.828

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch50

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	395
Mean	43.46
Std Dev	12.07

Turn on : 25.0960

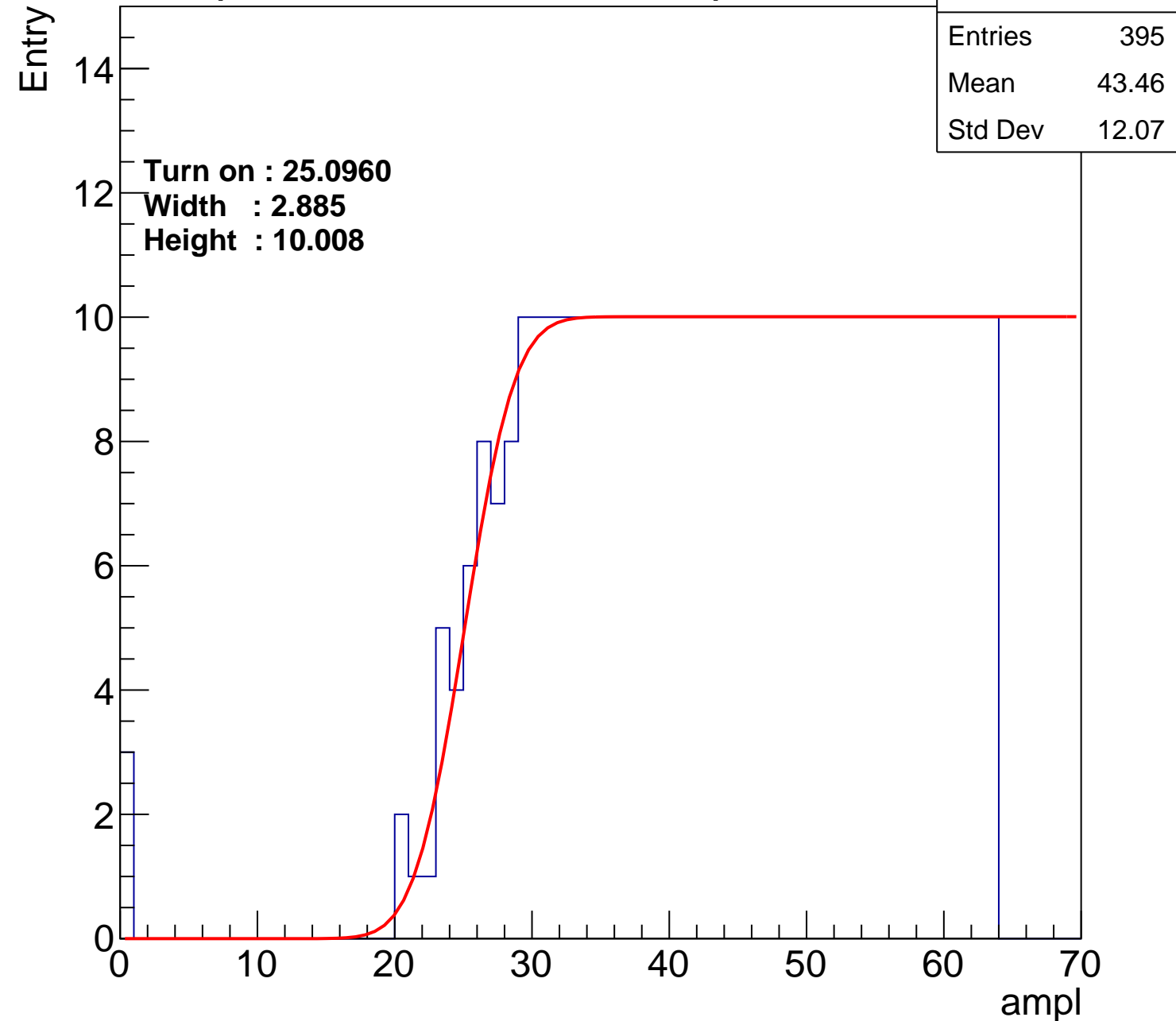
Width : 2.885

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch51

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	397
Mean	43.4
Std Dev	12.06

Turn on : 24.7744

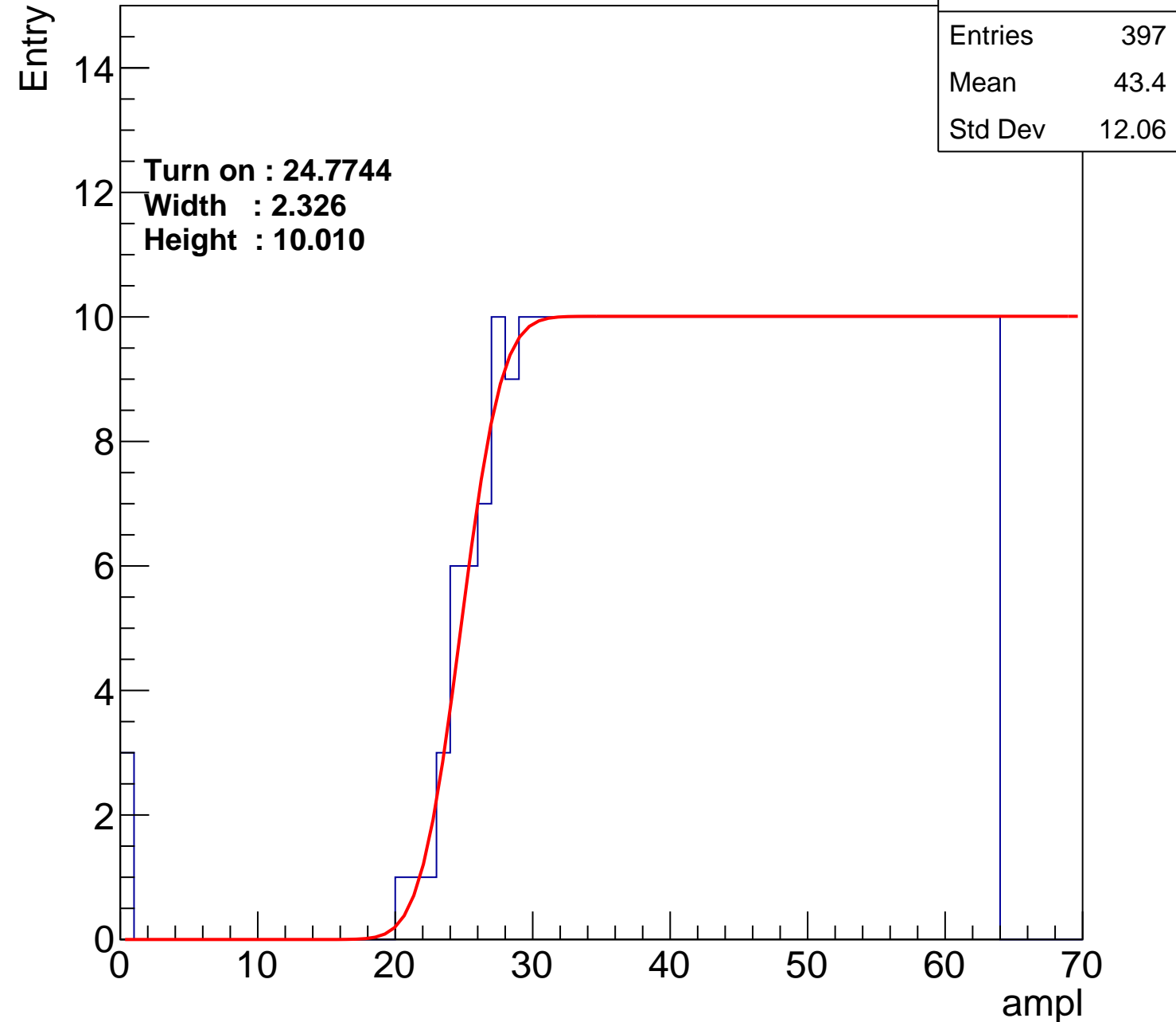
Width : 2.326

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch52

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	366
Mean	44.95
Std Dev	11.17

Turn on : 28.0548

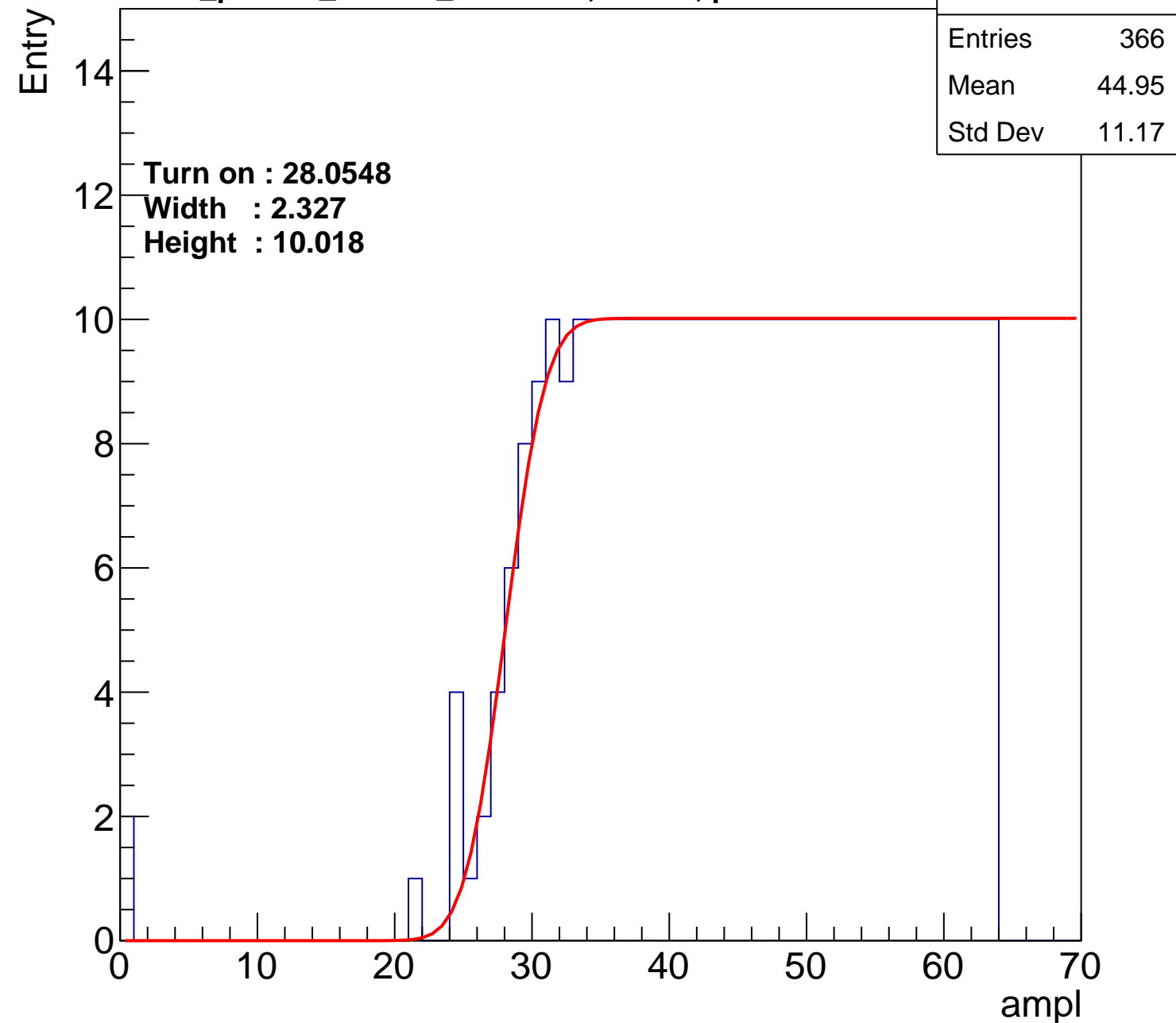
Width : 2.327

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch53

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.3245

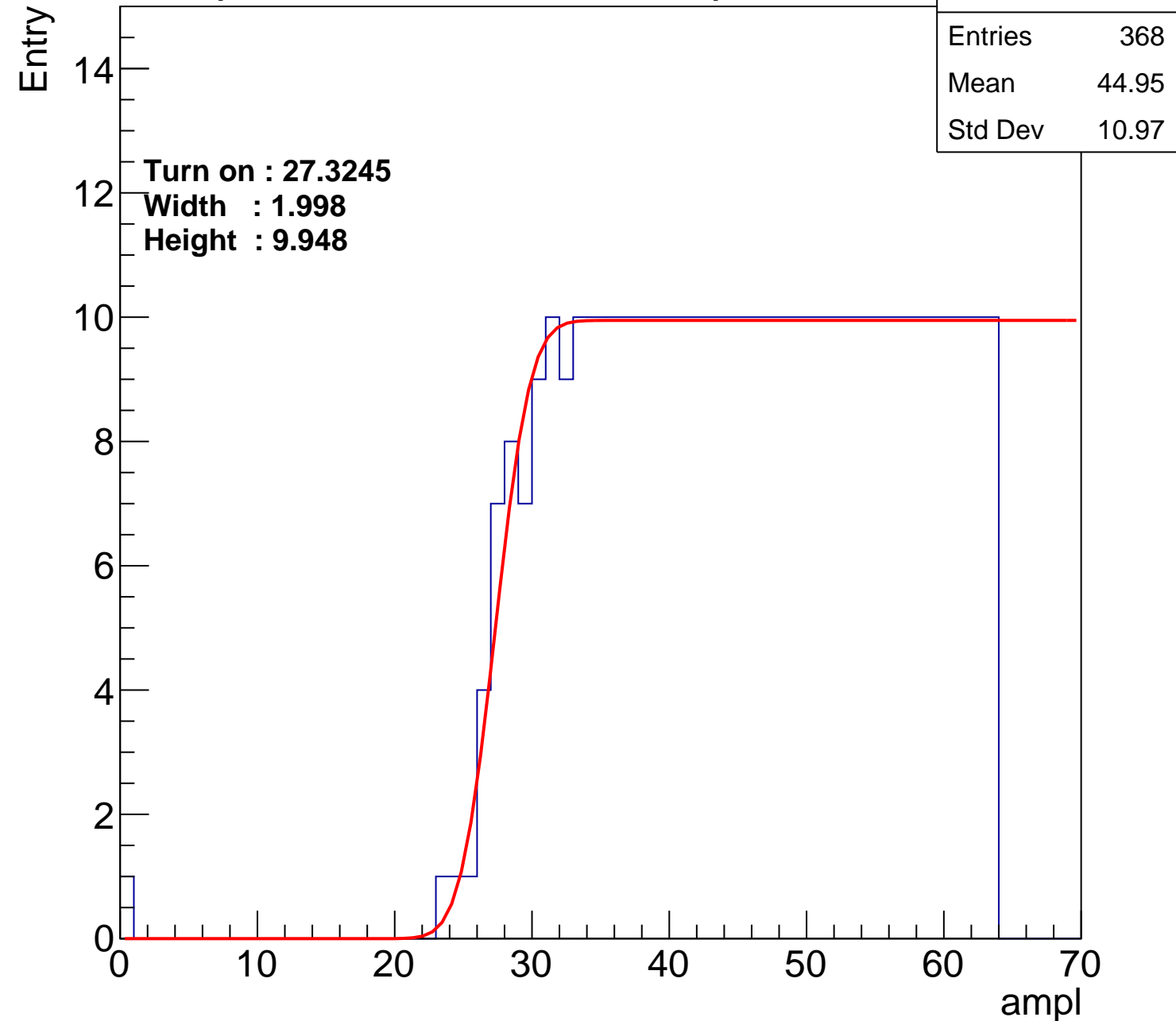
Width : 1.998

Height : 9.948

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch54

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.67
Std Dev	12.06

Turn on : 25.6515

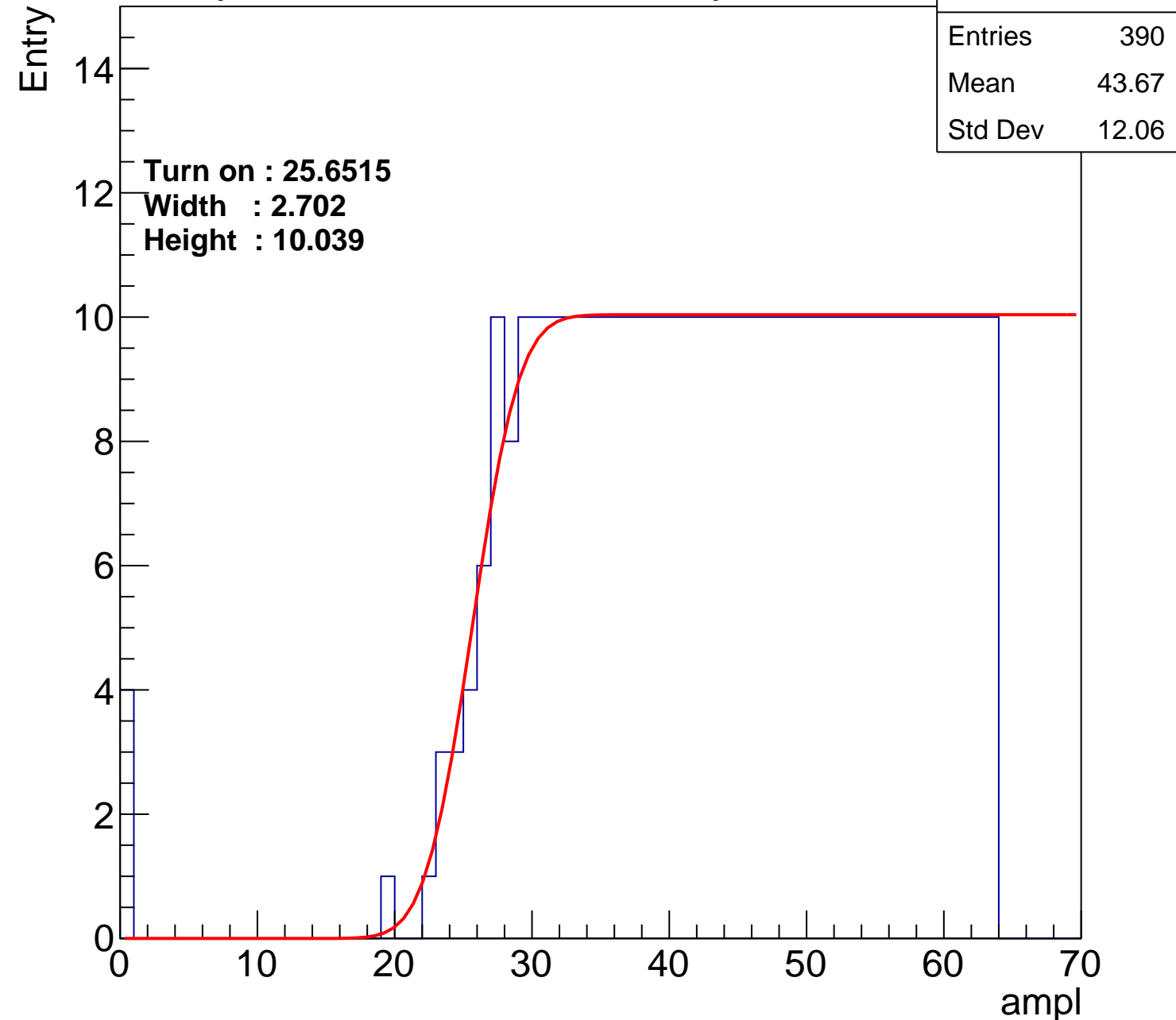
Width : 2.702

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch55

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.13
Std Dev	11.58

Turn on : 26.1757

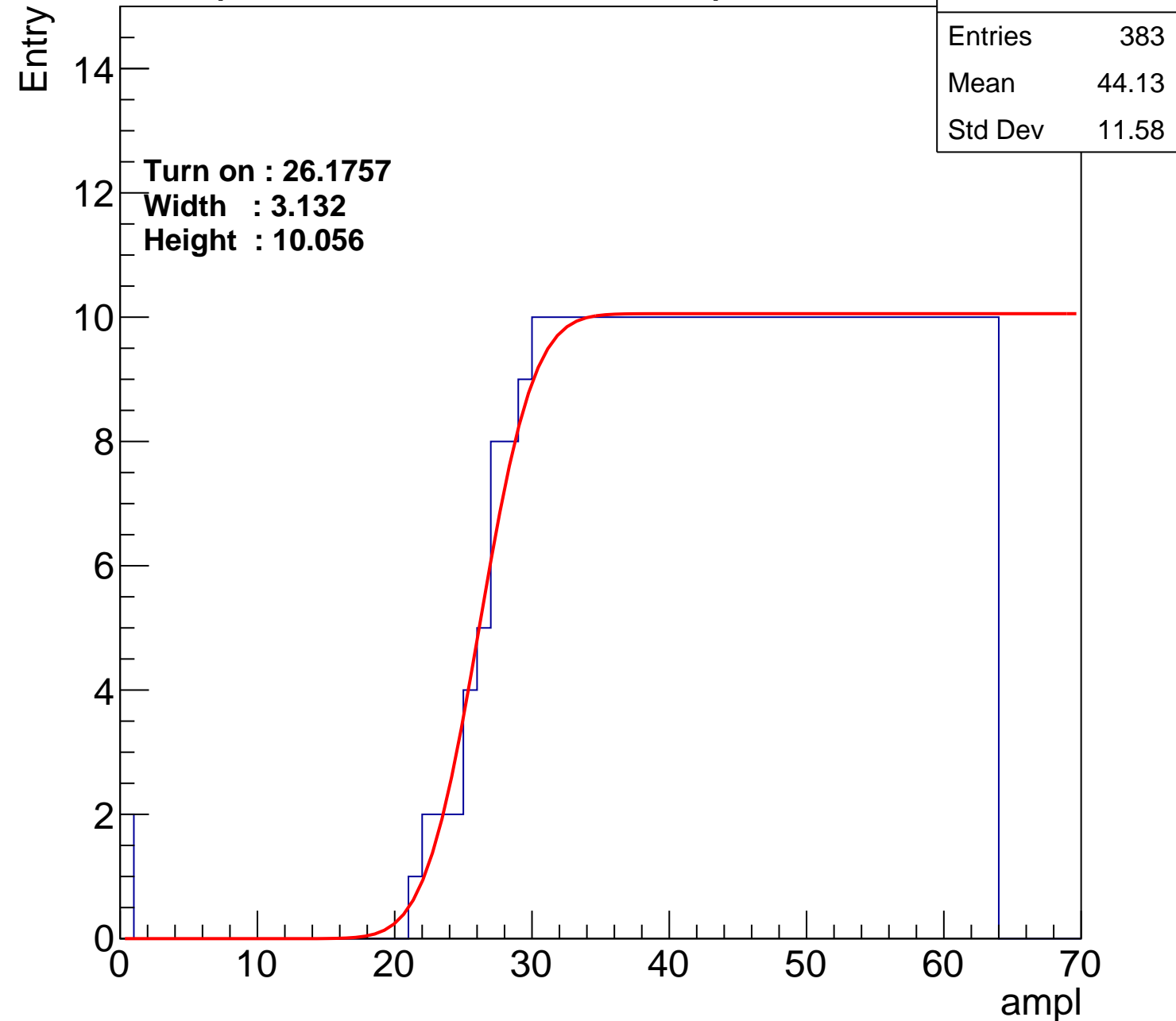
Width : 3.132

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch56

calib\_packv5\_042523\_0143.root, FC#10, port B3

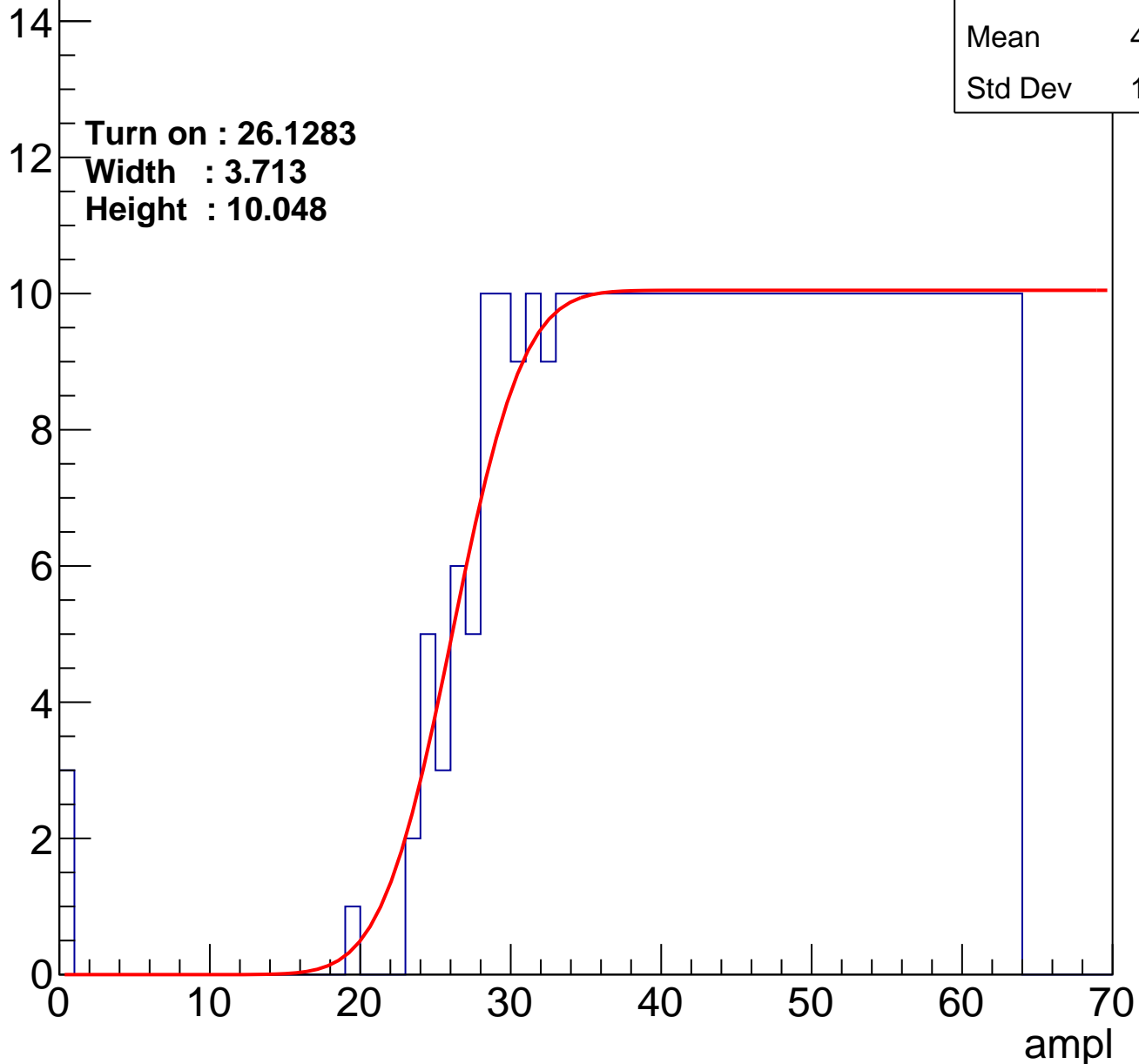
Entries	383
Mean	44.04
Std Dev	11.78

Turn on : 26.1283

Width : 3.713

Height : 10.048

Entry



# B1L002S, U11-ch57

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.0622

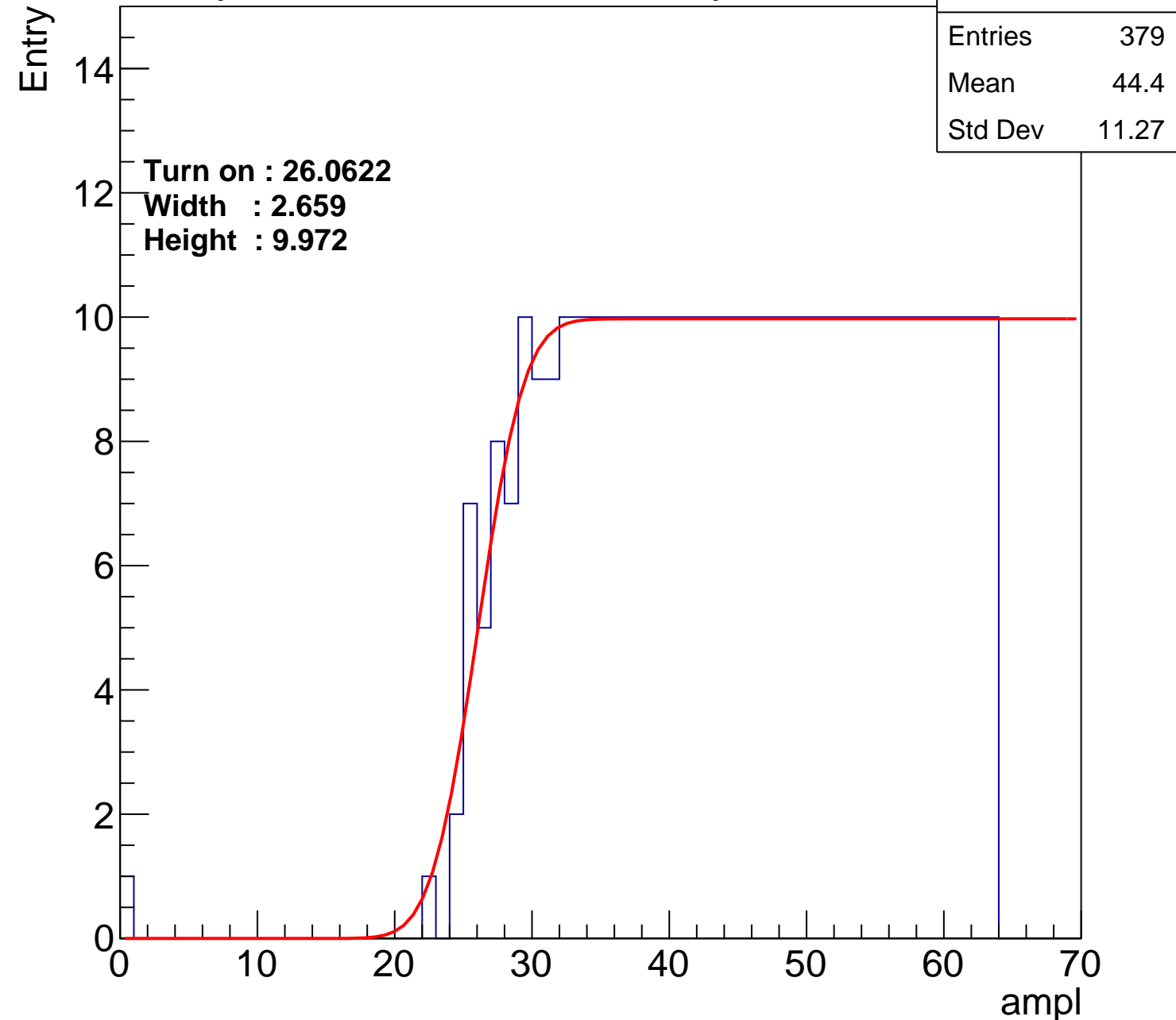
Width : 2.659

Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch58

calib\_packv5\_042523\_0143.root, FC#10, port B3

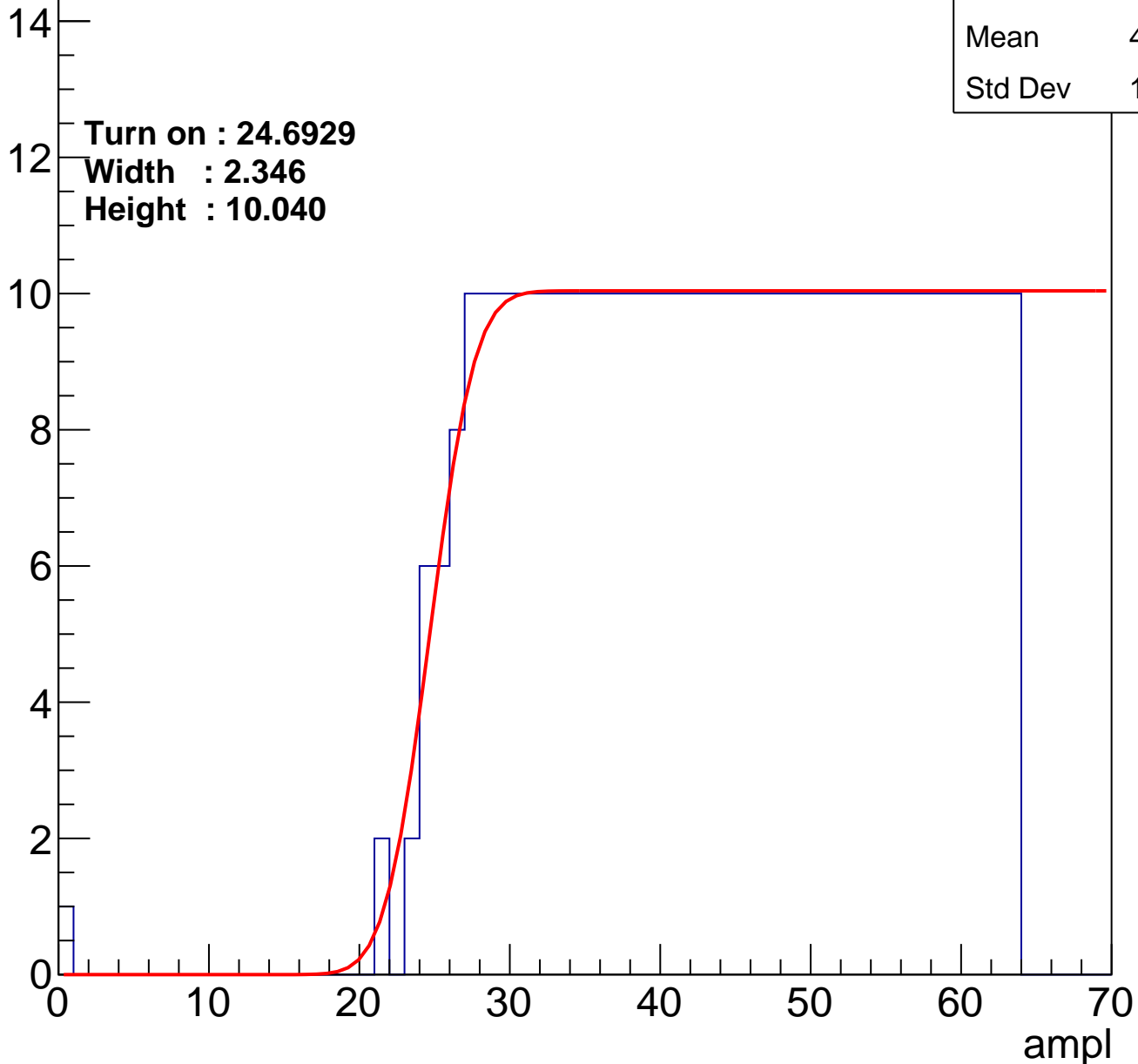
Entries	395
Mean	43.65
Std Dev	11.64

Turn on : 24.6929

Width : 2.346

Height : 10.040

Entry



# B1L002S, U11-ch59

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.53
Std Dev	12

Turn on : 24.9206

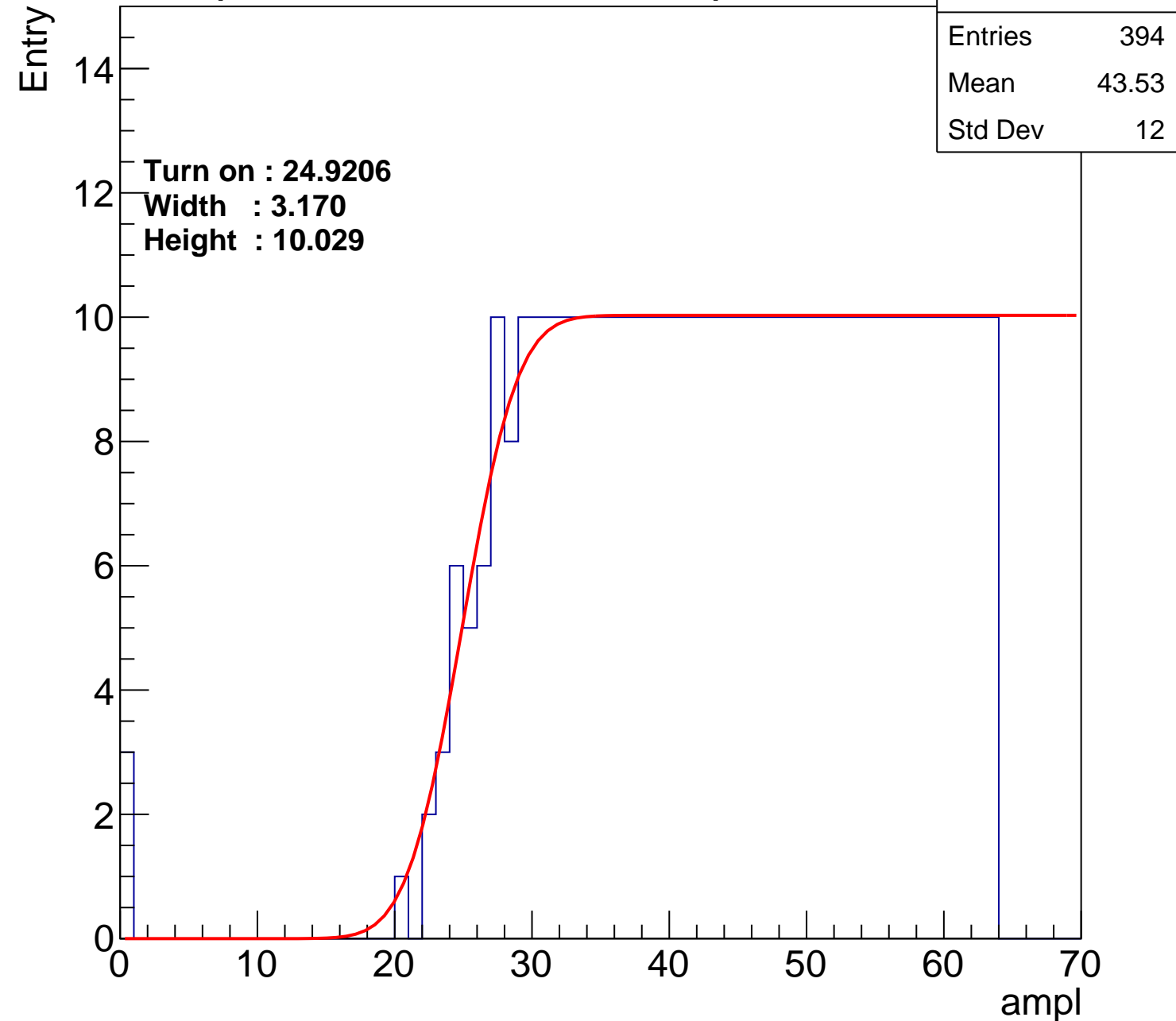
Width : 3.170

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch60

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	407
Mean	42.94
Std Dev	12.21

Turn on : 23.6588

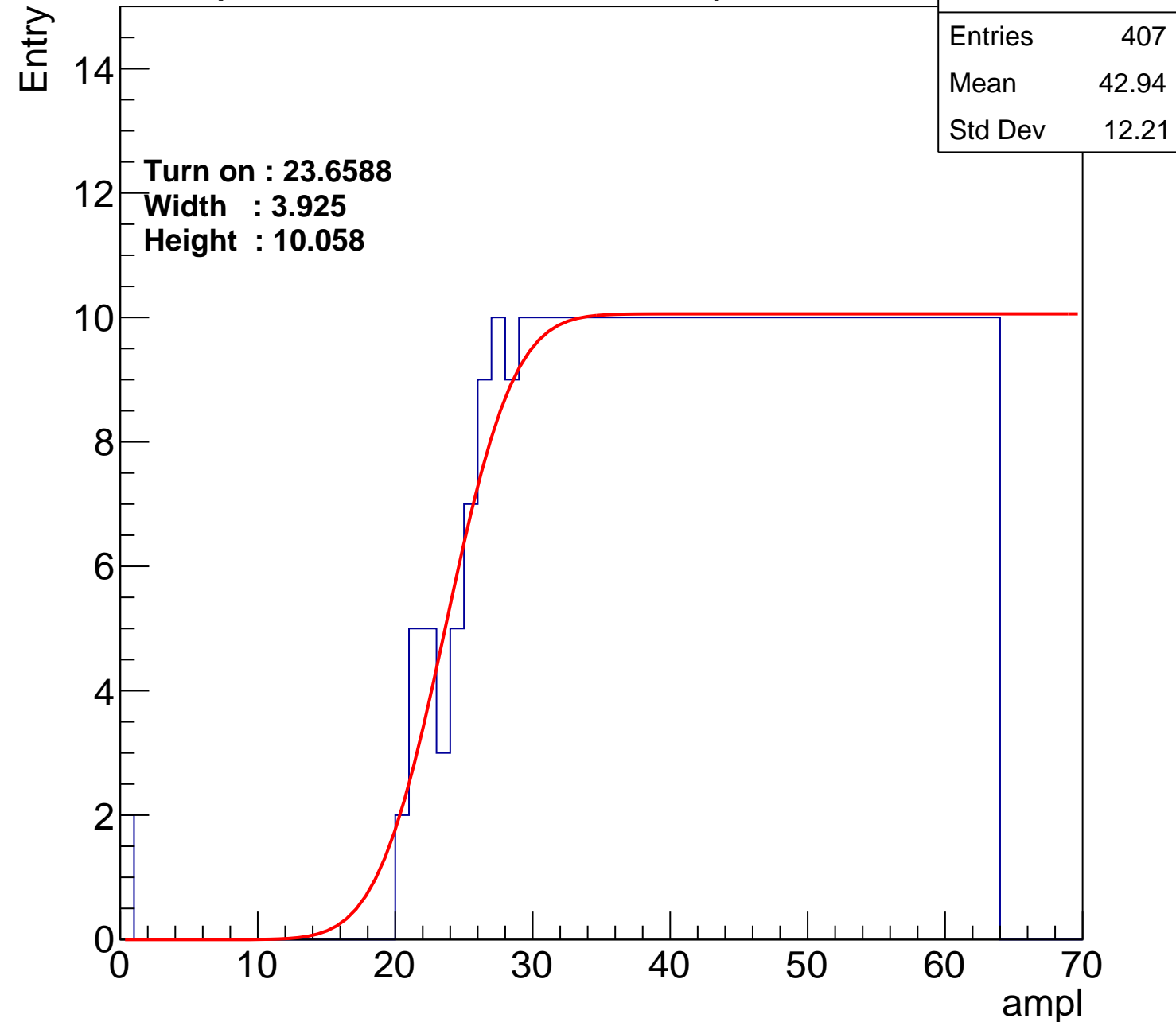
Width : 3.925

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch61

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.12
Std Dev	11.47

Turn on : 25.5936

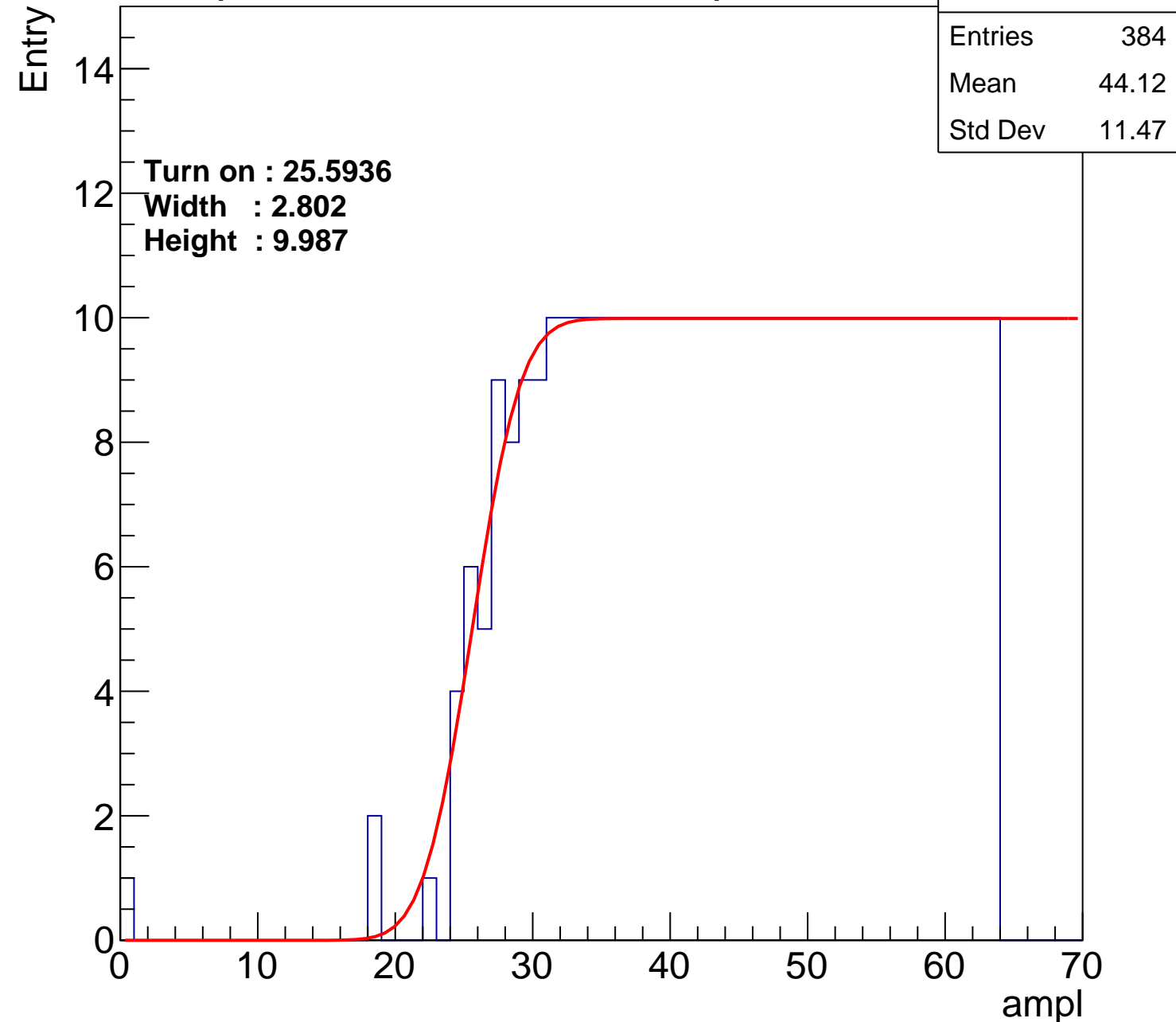
Width : 2.802

Height : 9.987

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch62

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.44
Std Dev	11.82

Turn on : 24.1372

Width : 2.776

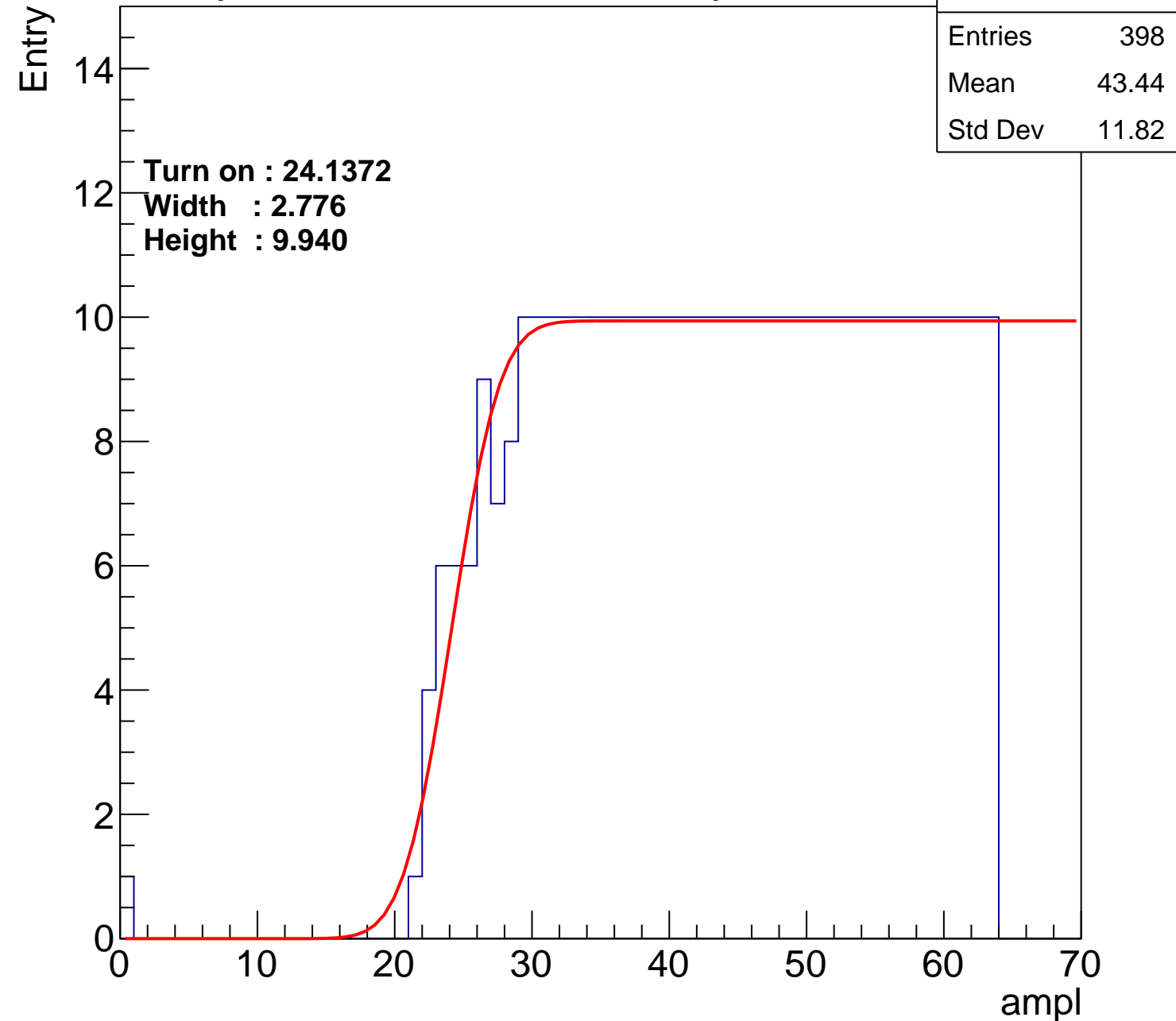
Height : 9.940

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B1L002S, U11-ch63

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.62
Std Dev	11.17

Turn on : 26.8985

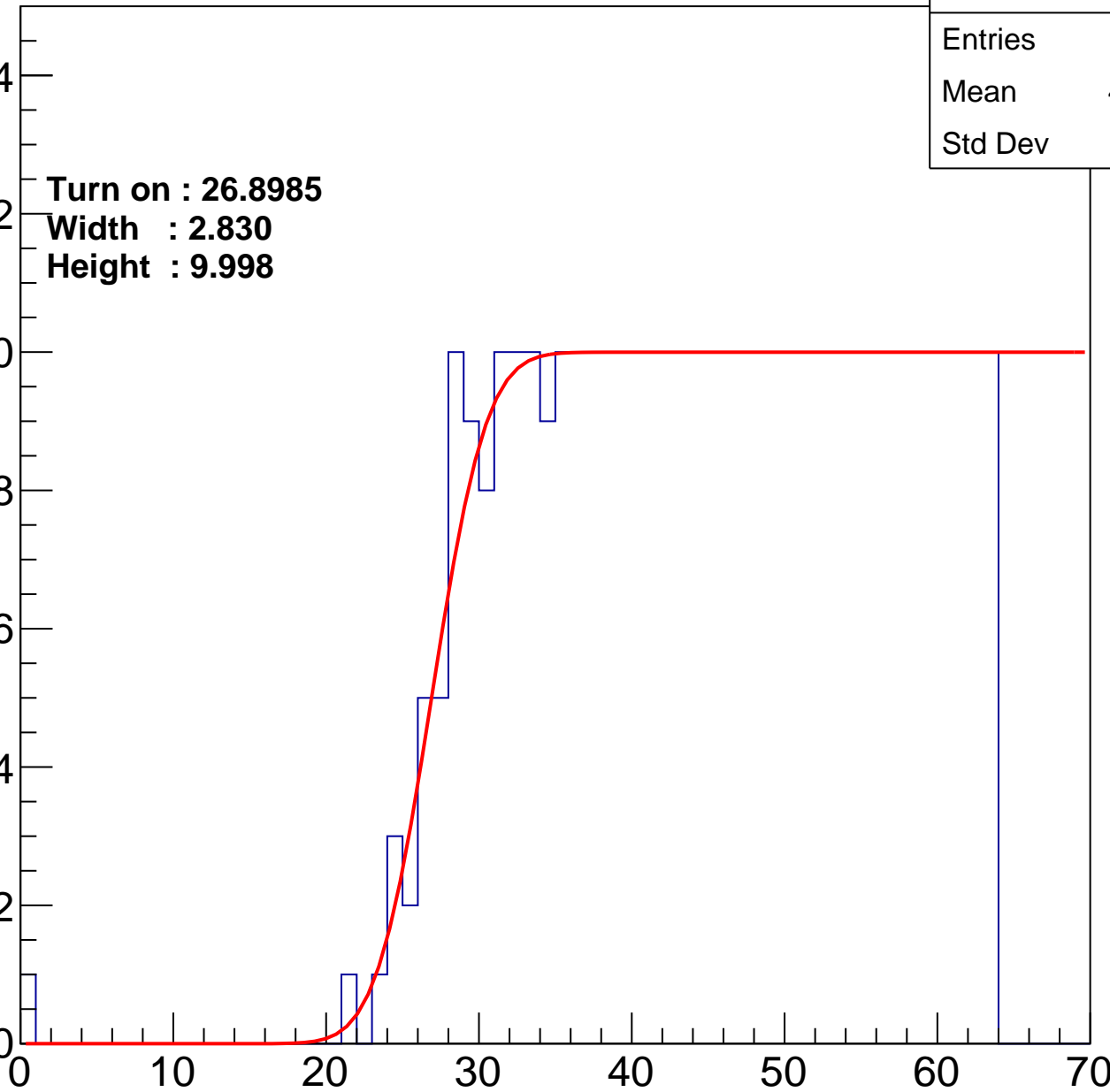
Width : 2.830

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch64

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 27.0674

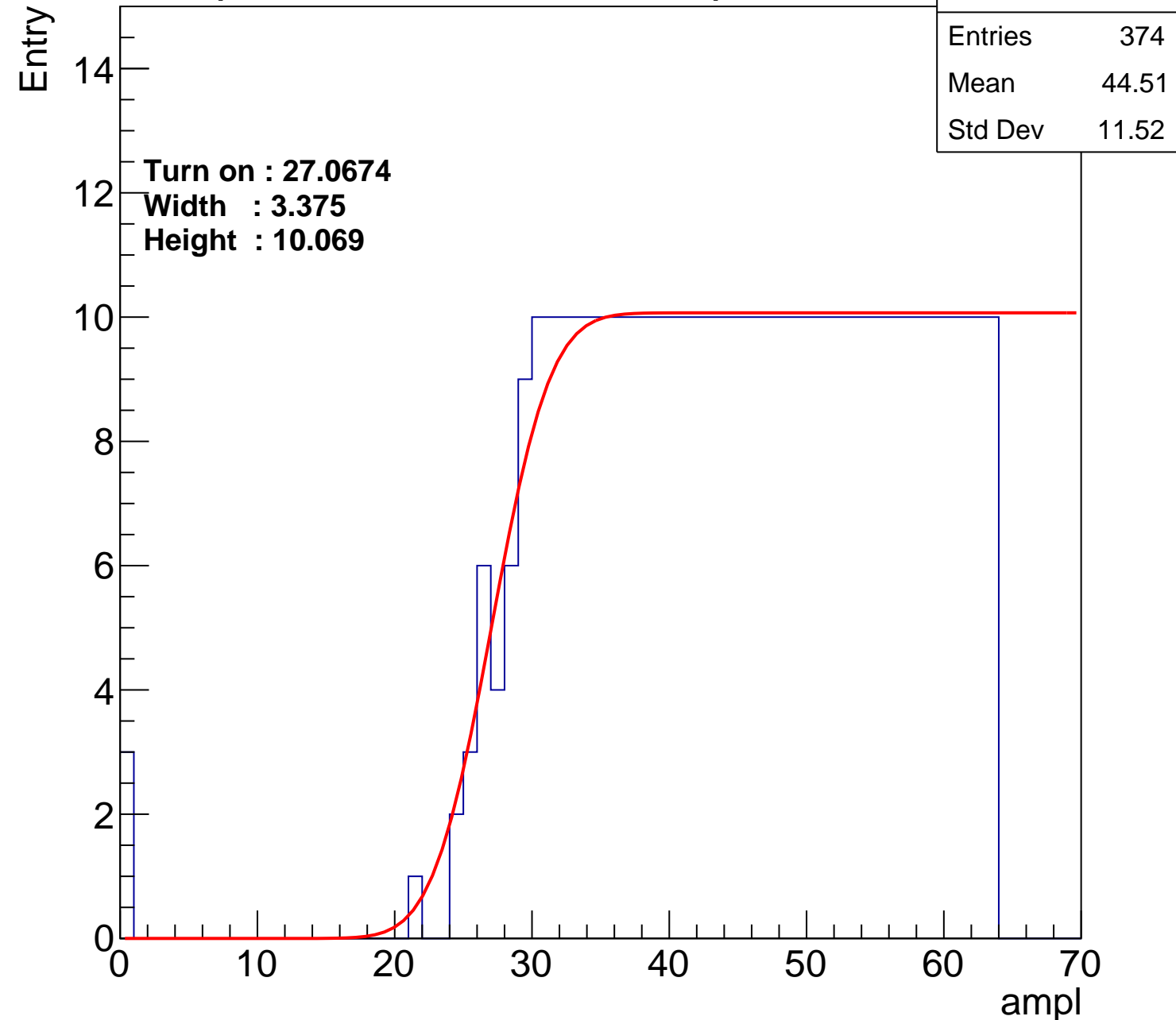
Width : 3.375

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch65

calib\_packv5\_042523\_0143.root, FC#10, port B3

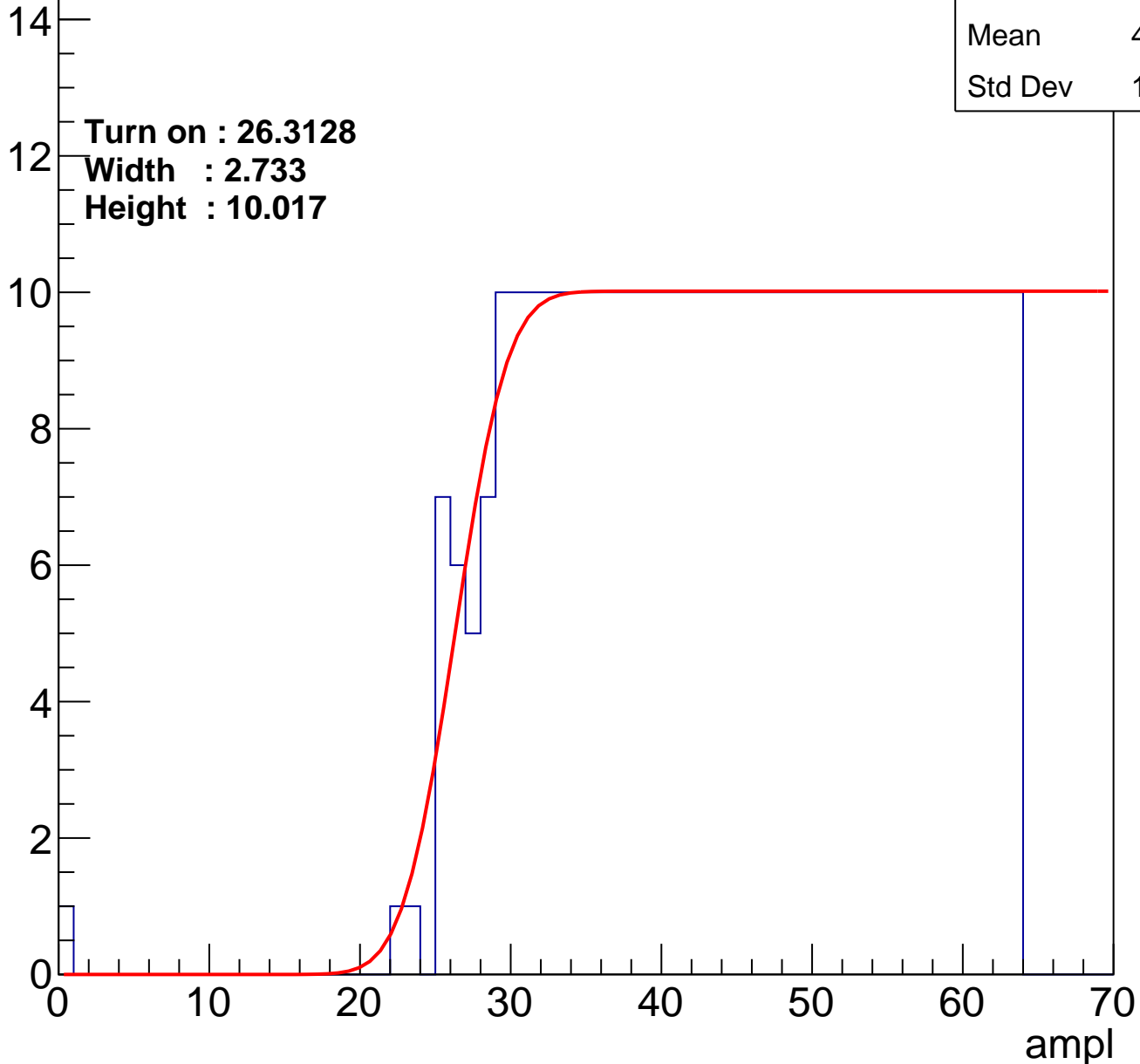
Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 26.3128

Width : 2.733

Height : 10.017

Entry



# B1L002S, U11-ch66

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.18
Std Dev	11.49

**Turn on : 25.8824**

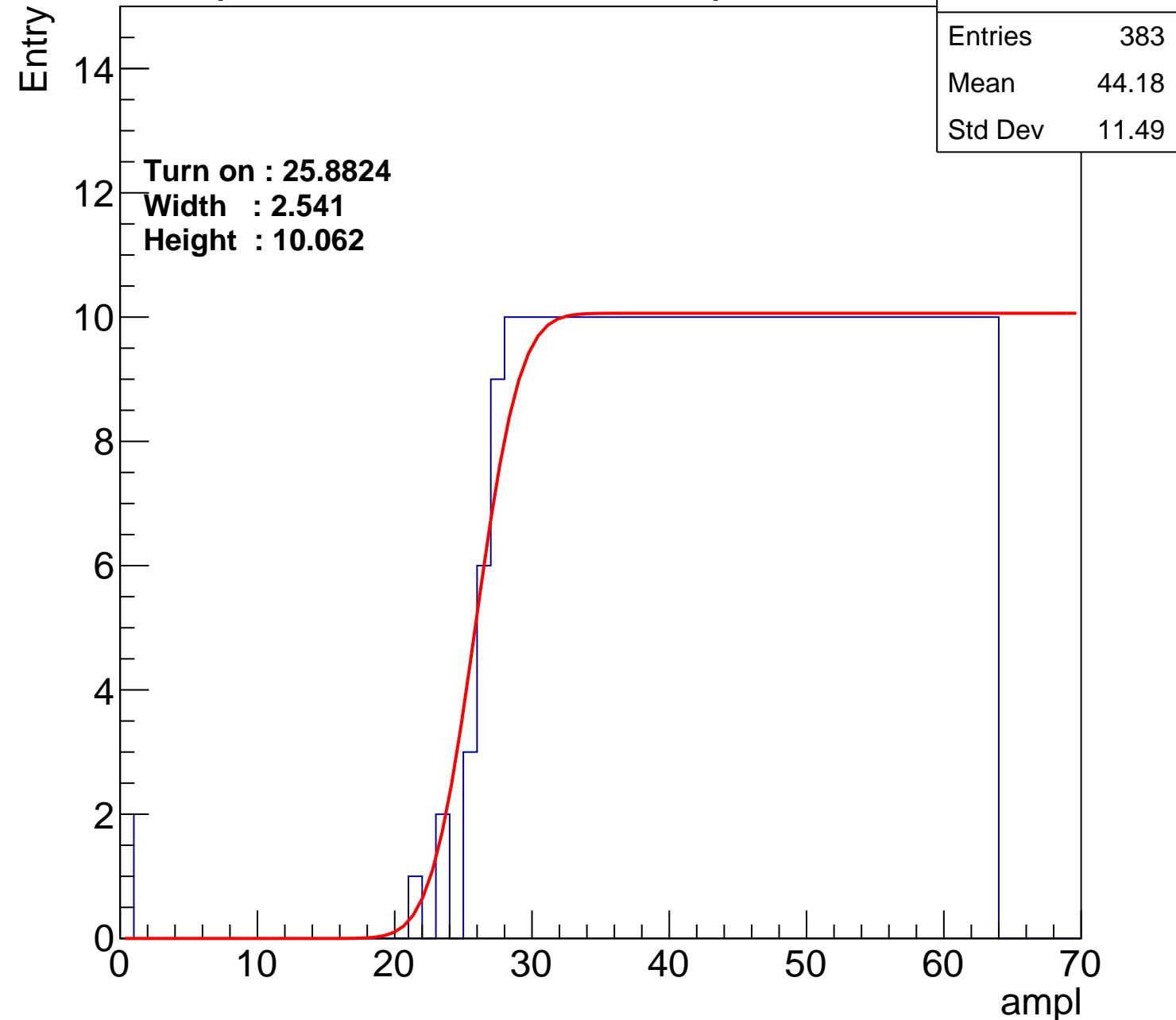
**Width : 2.541**

**Height : 10.062**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch67

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.02
Std Dev	11.76

Turn on : 26.2491

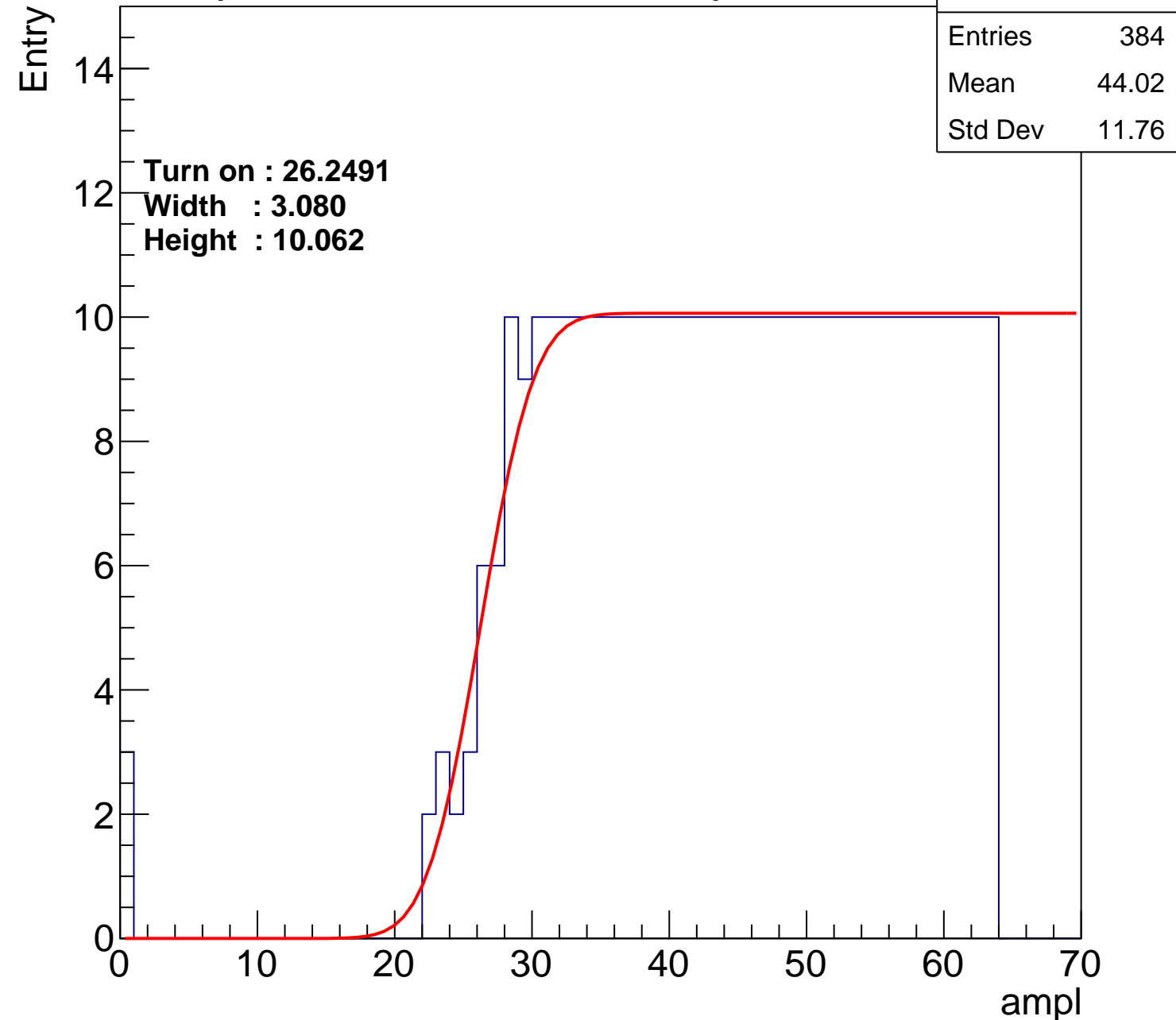
Width : 3.080

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch68

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 25.8749

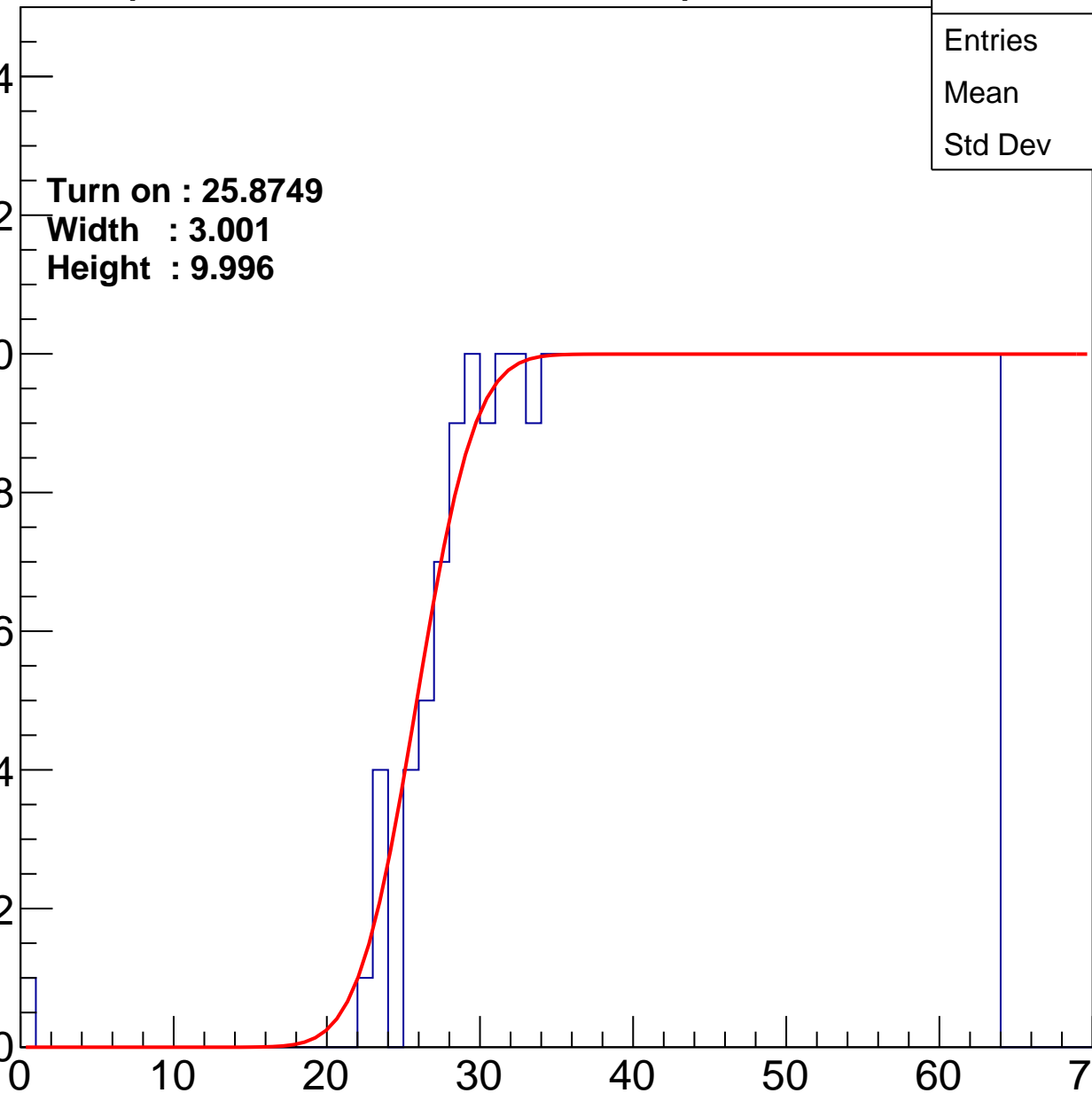
Width : 3.001

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch69

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 27.0872

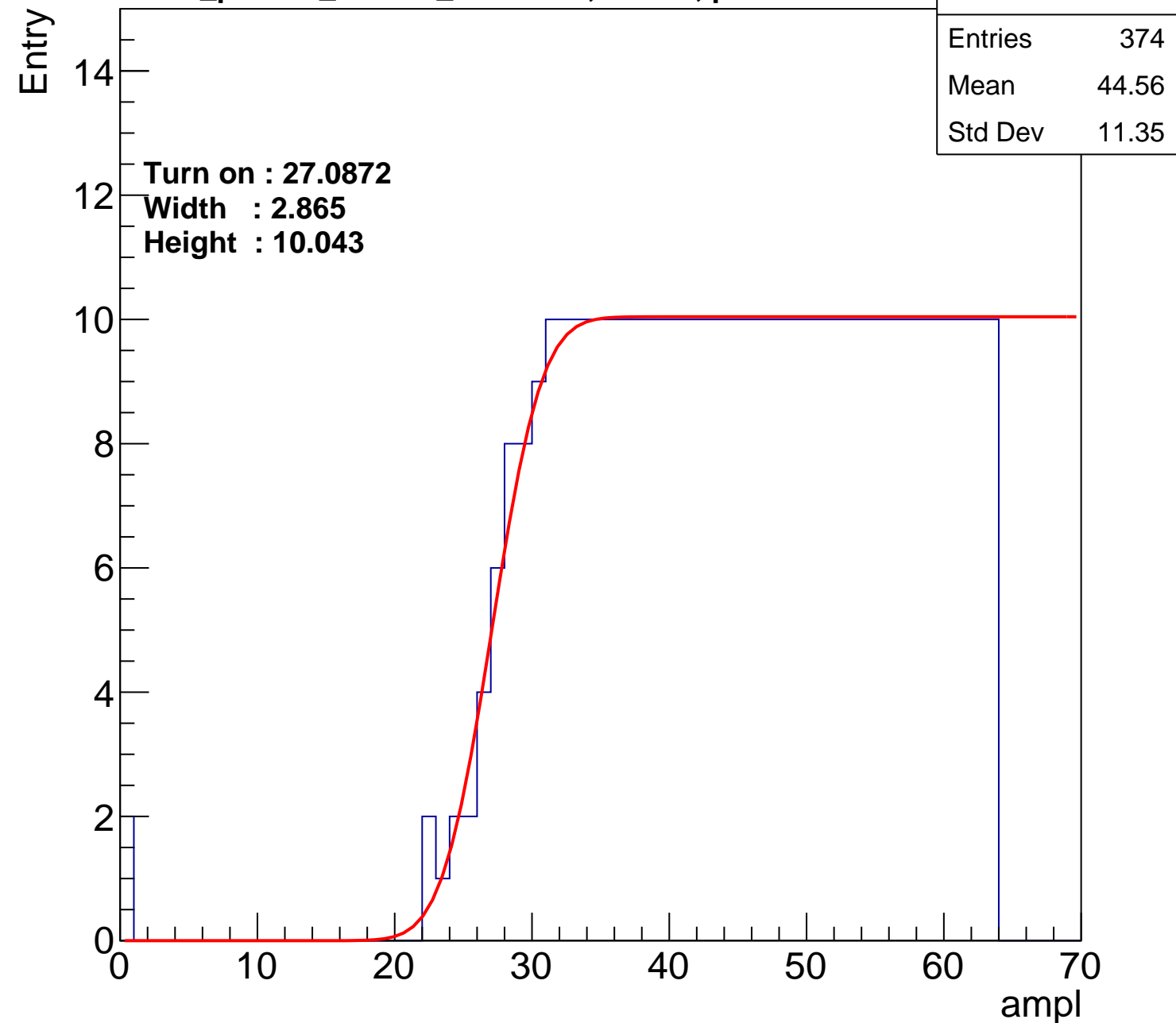
Width : 2.865

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch70

calib\_packv5\_042523\_0143.root, FC#10, port B3

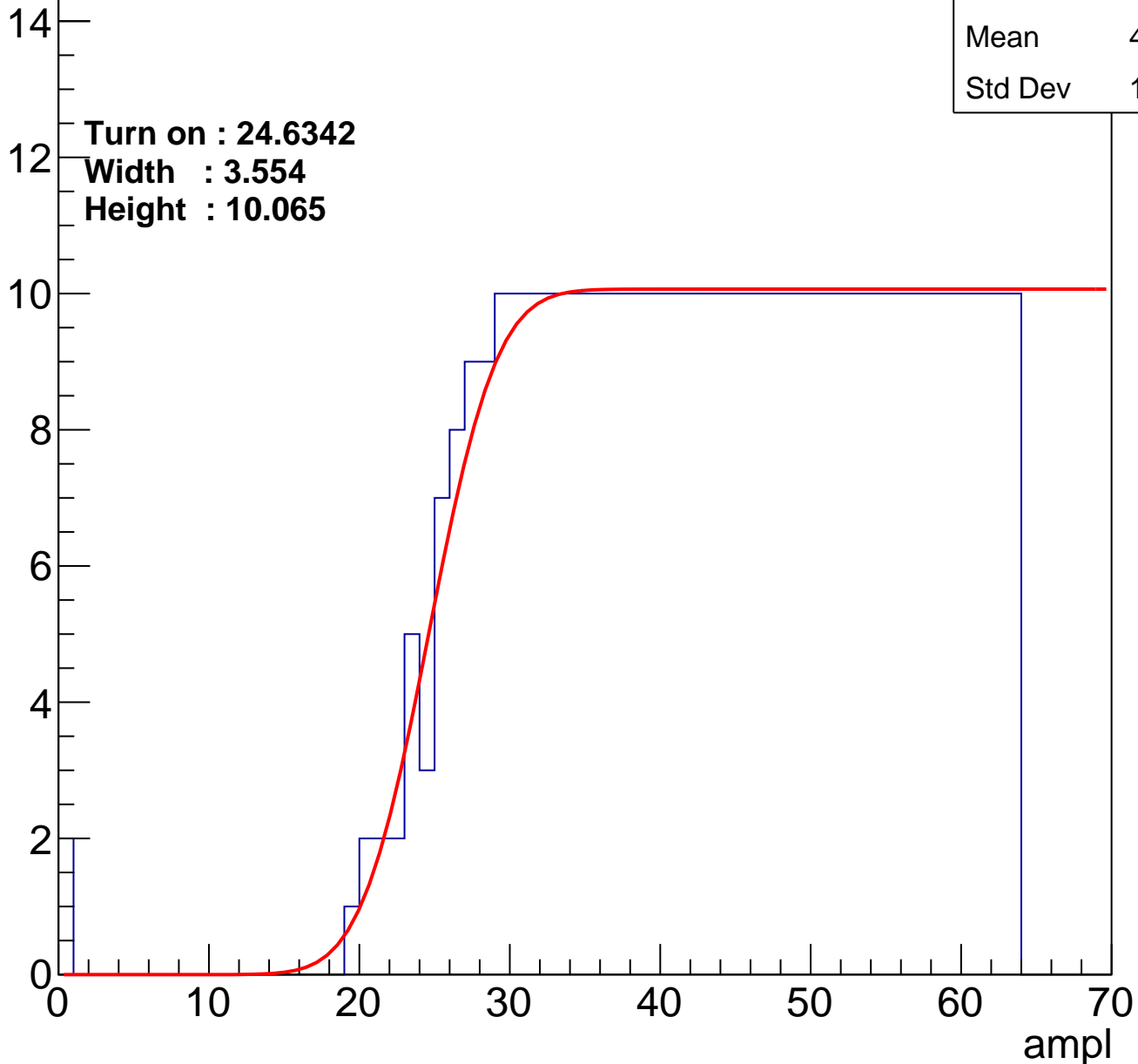
Entries	400
Mean	43.27
Std Dev	12.04

Turn on : 24.6342

Width : 3.554

Height : 10.065

Entry





# B1L002S, U11-ch71

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.41
Std Dev	11.93

Turn on : 24.6681

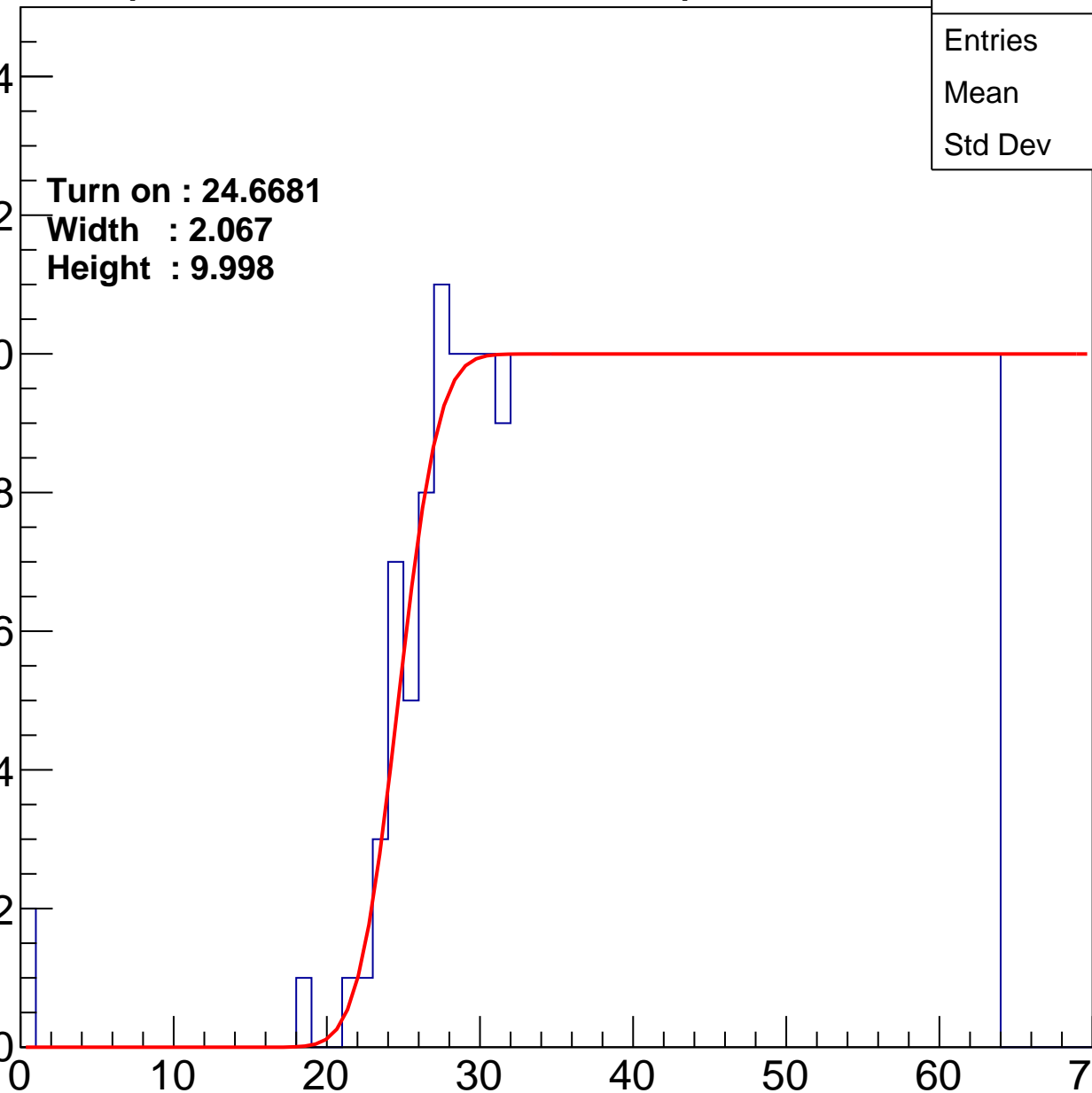
Width : 2.067

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch72

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.21
Std Dev	11.36

Turn on : 25.5371

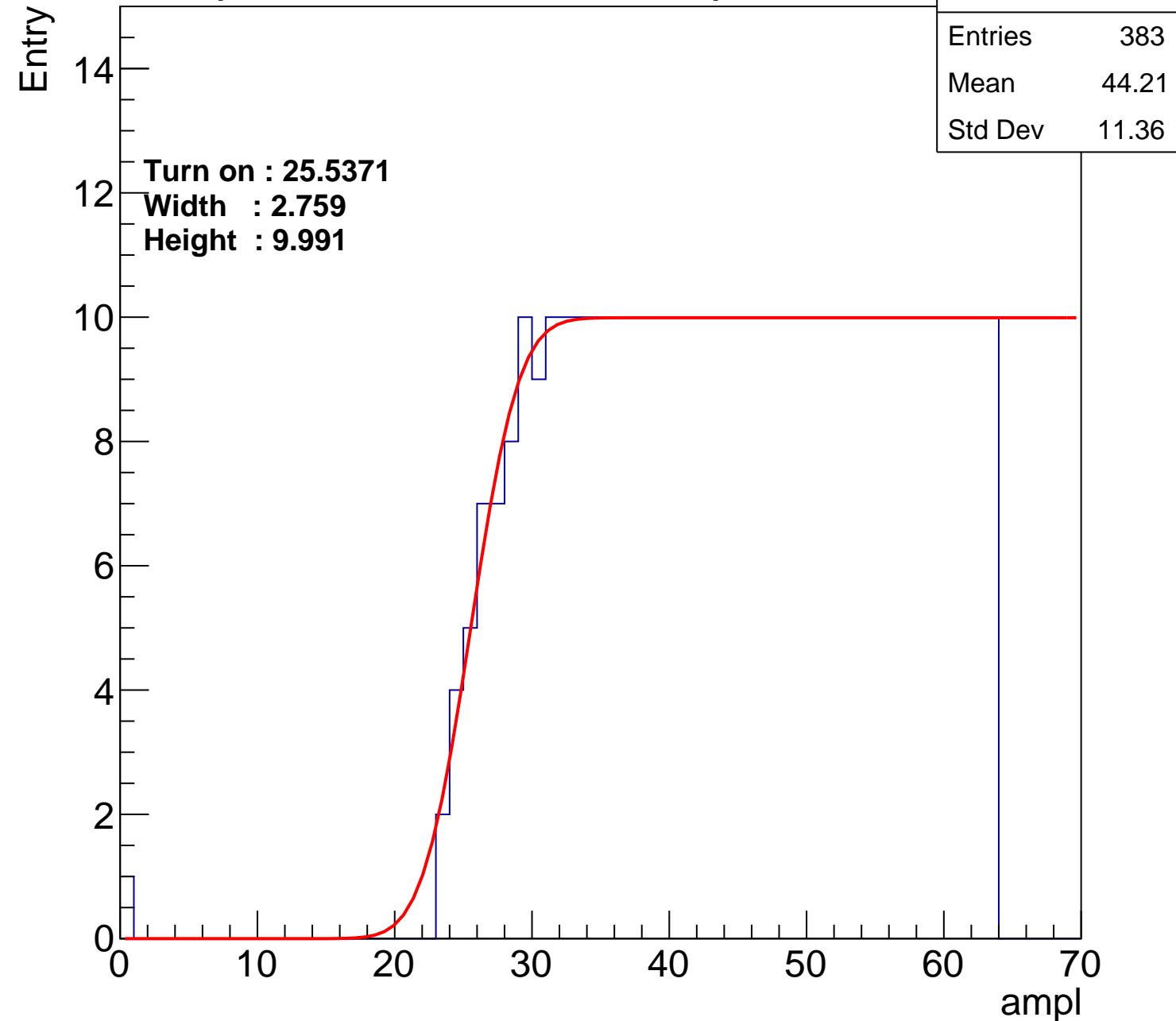
Width : 2.759

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch73

calib\_packv5\_042523\_0143.root, FC#10, port B3

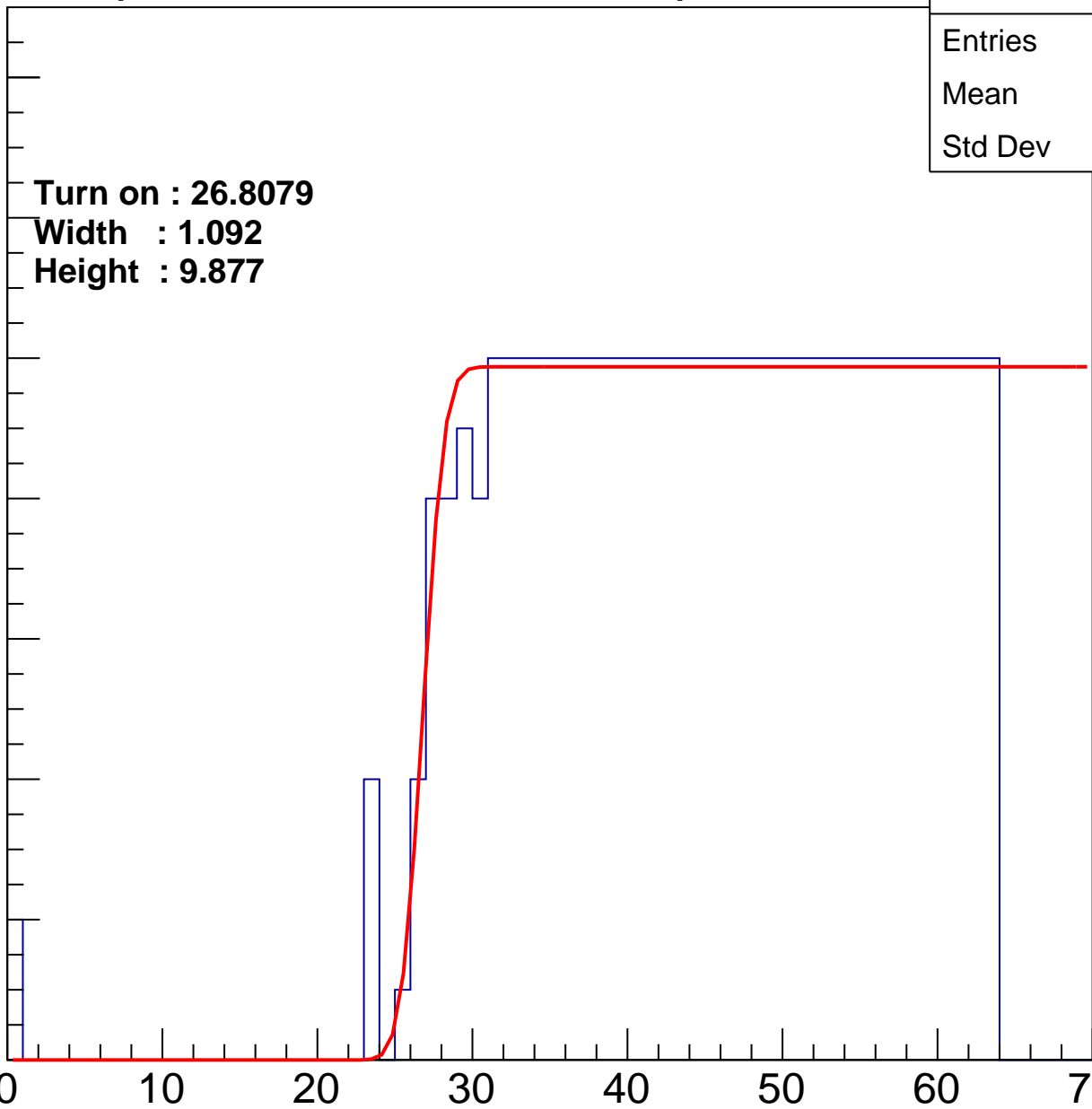
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.8079  
Width : 1.092  
Height : 9.877

Entries	374
Mean	44.58
Std Dev	11.33

ampl



# B1L002S, U11-ch74

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.69
Std Dev	12.03

Turn on : 25.2624

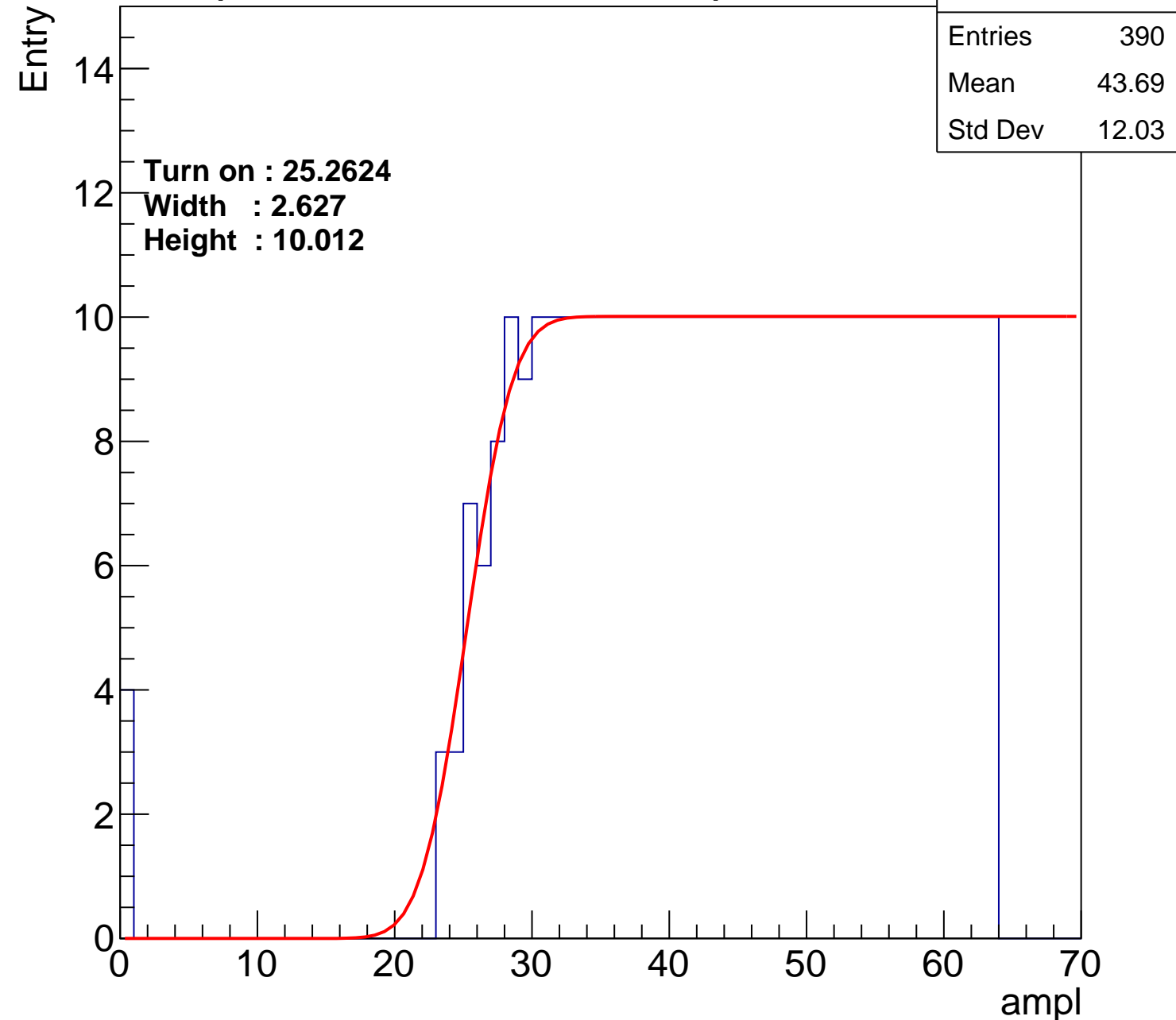
Width : 2.627

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch75

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.97
Std Dev	11.66

Turn on : 25.7238

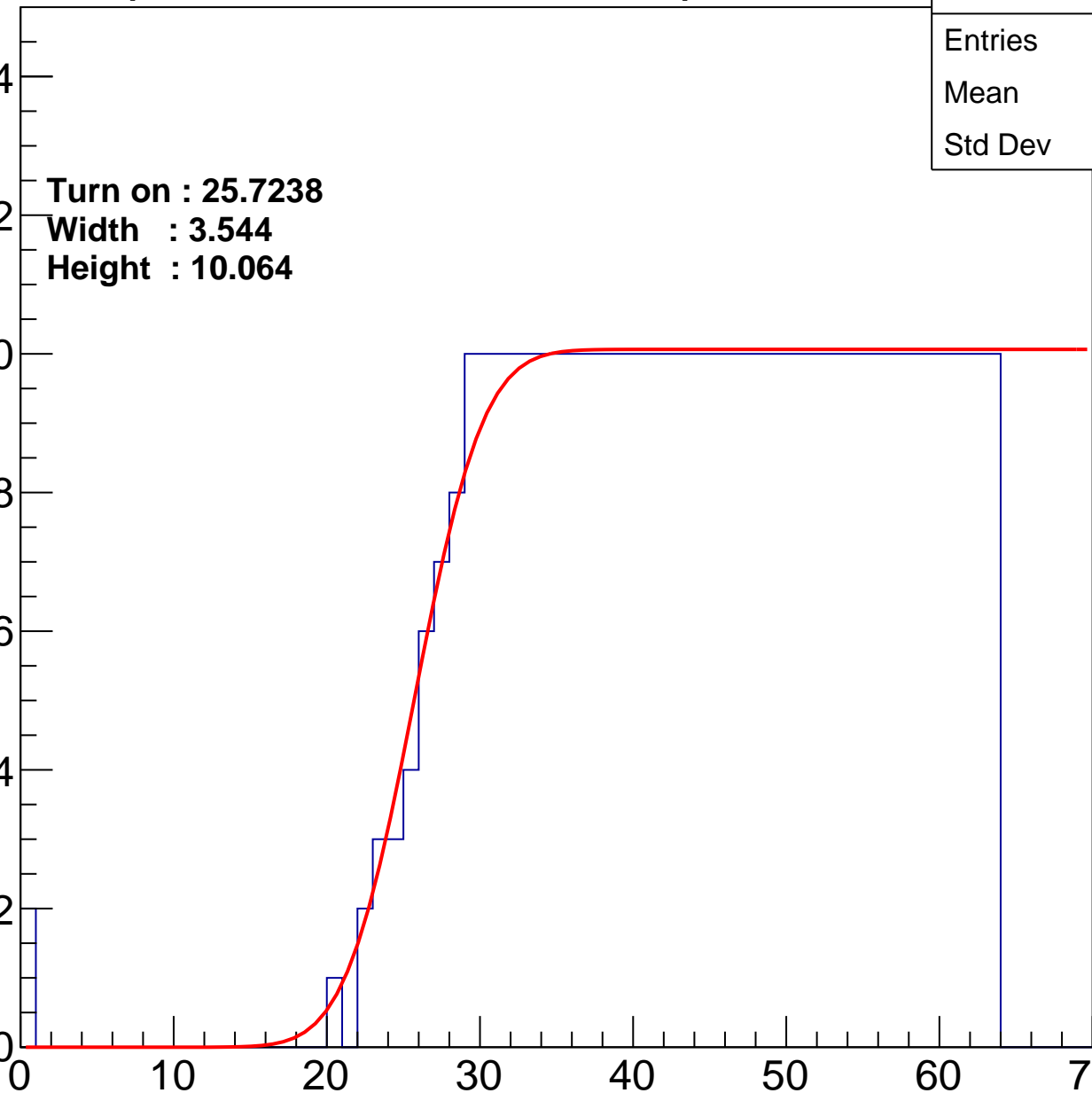
Width : 3.544

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch76

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.4
Std Dev	11.94

Turn on : 24.3065

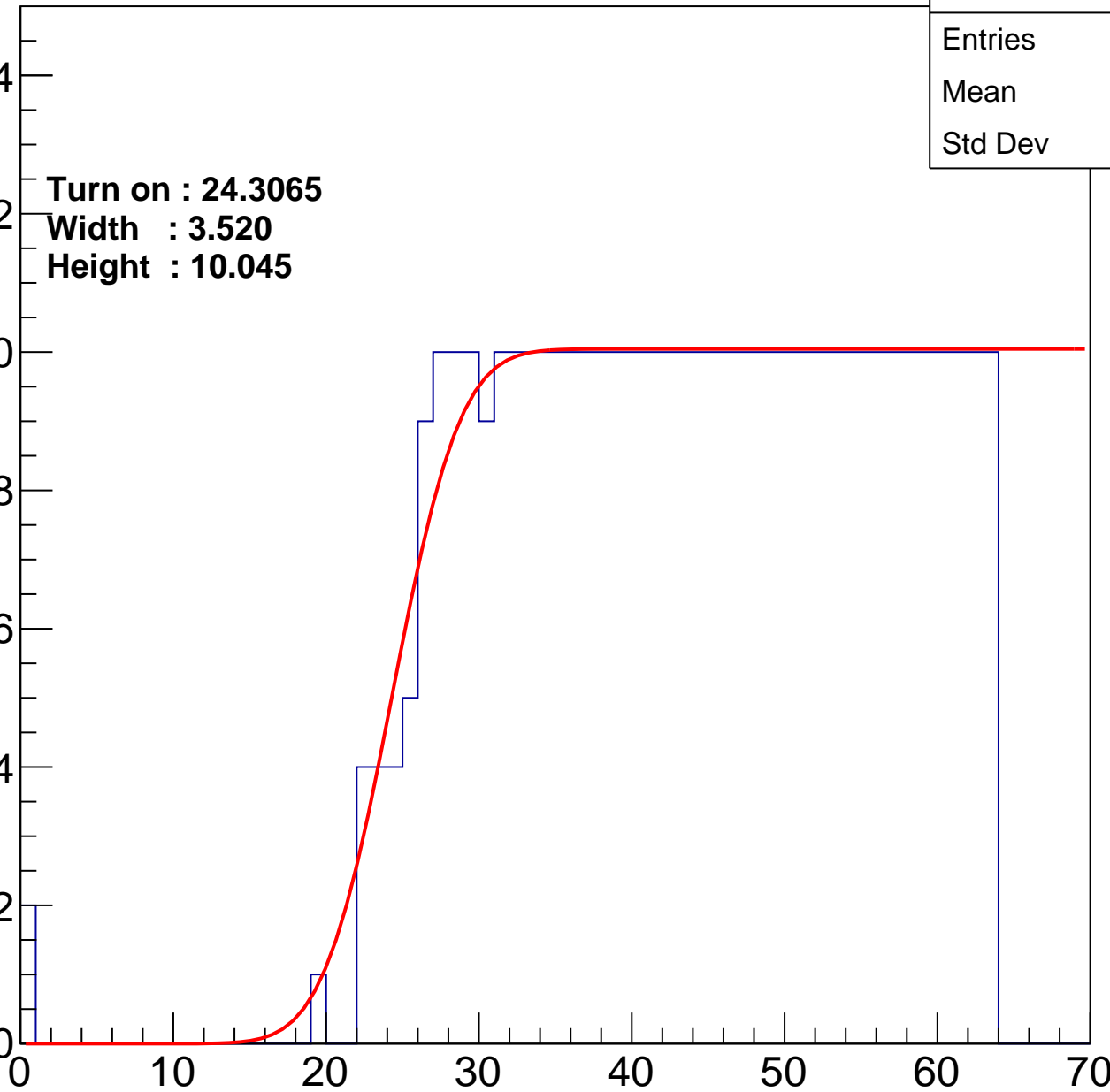
Width : 3.520

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch77

calib\_packv5\_042523\_0143.root, FC#10, port B3

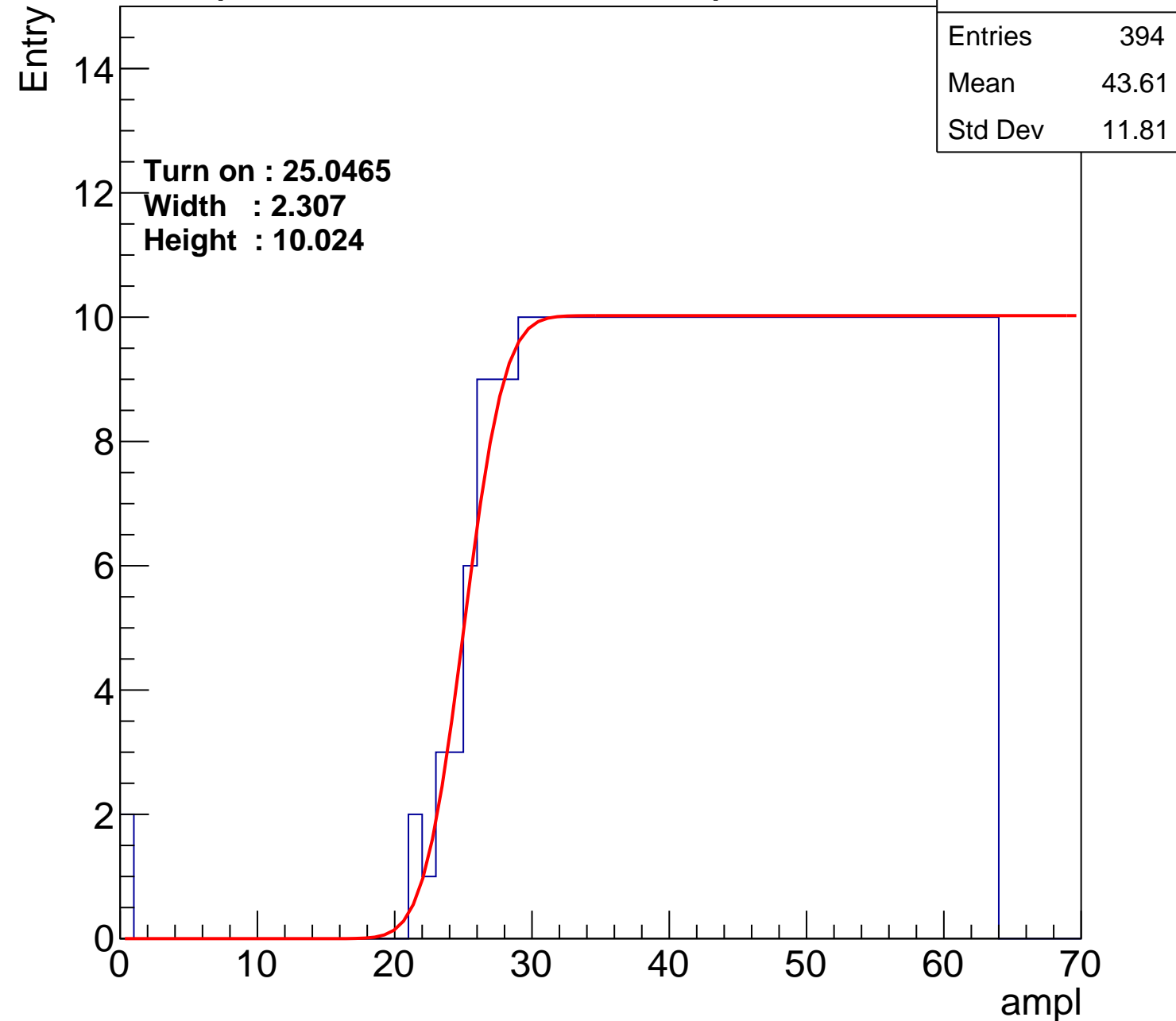
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.0465  
Width : 2.307  
Height : 10.024

Entries	394
Mean	43.61
Std Dev	11.81

ampl



# B1L002S, U11-ch78

calib\_packv5\_042523\_0143.root, FC#10, port B3

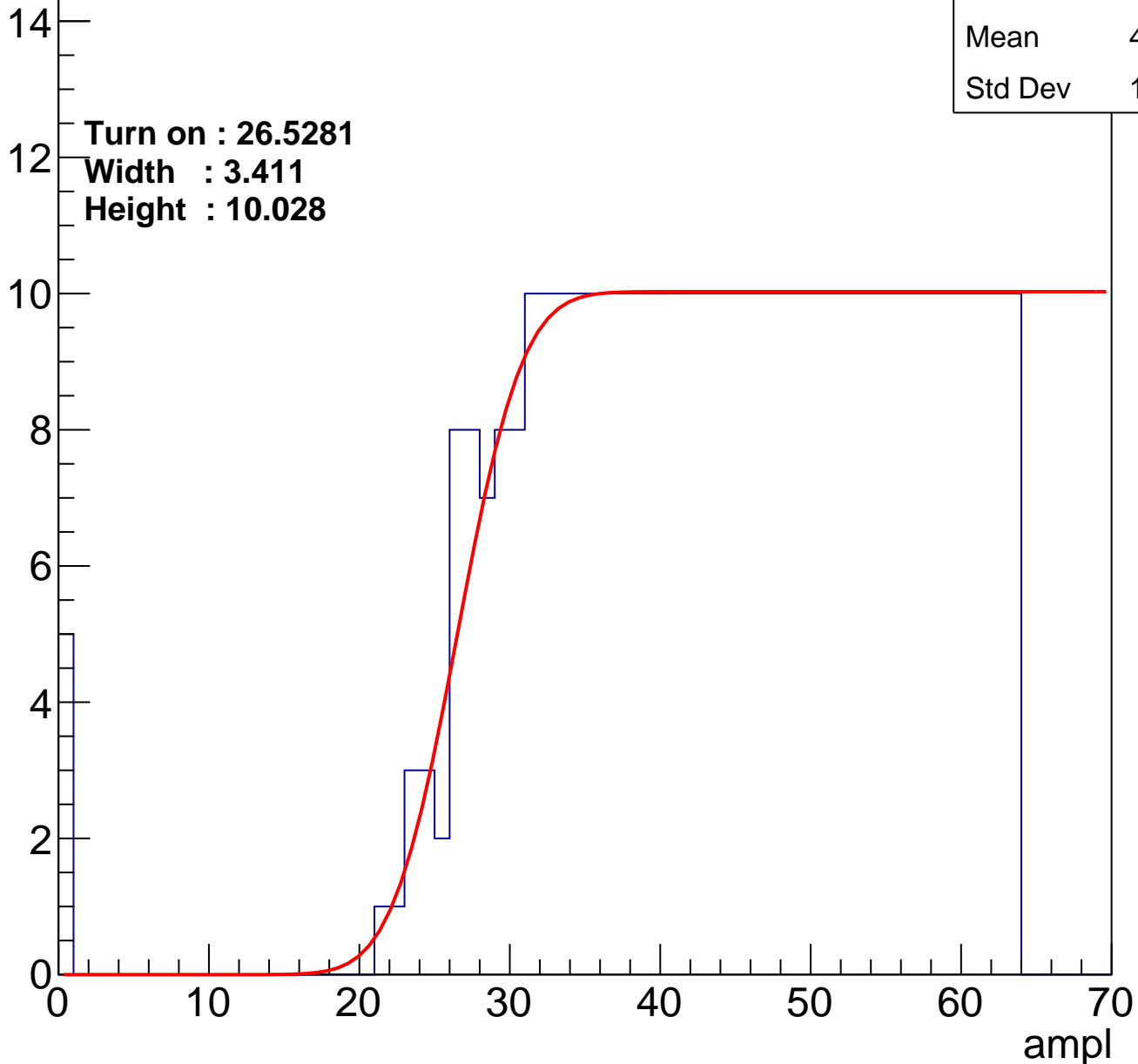
Entries	384
Mean	43.84
Std Dev	12.17

Turn on : 26.5281

Width : 3.411

Height : 10.028

Entry





# B1L002S, U11-ch79

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 26.6227

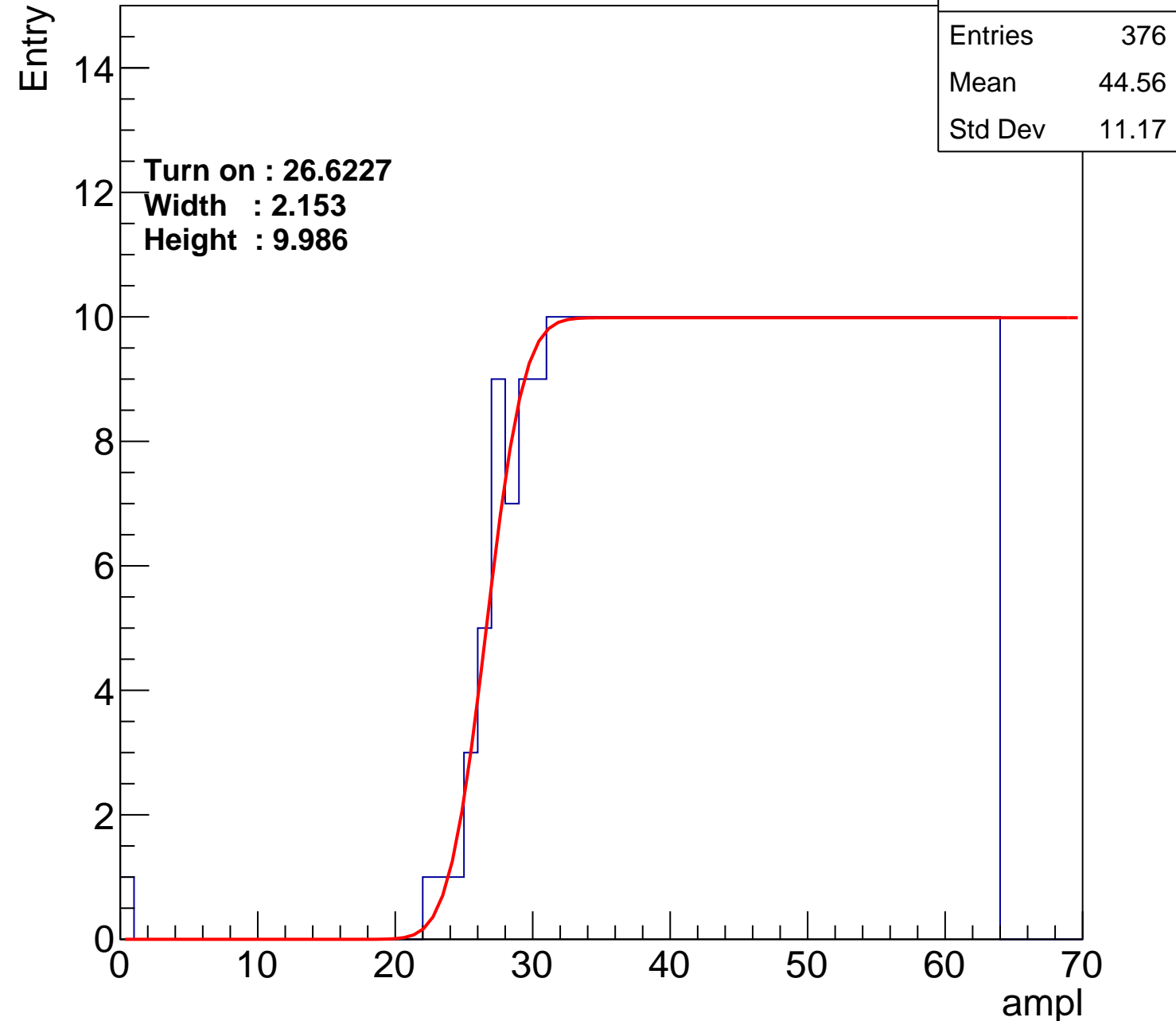
Width : 2.153

Height : 9.986

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch80

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 26.8914

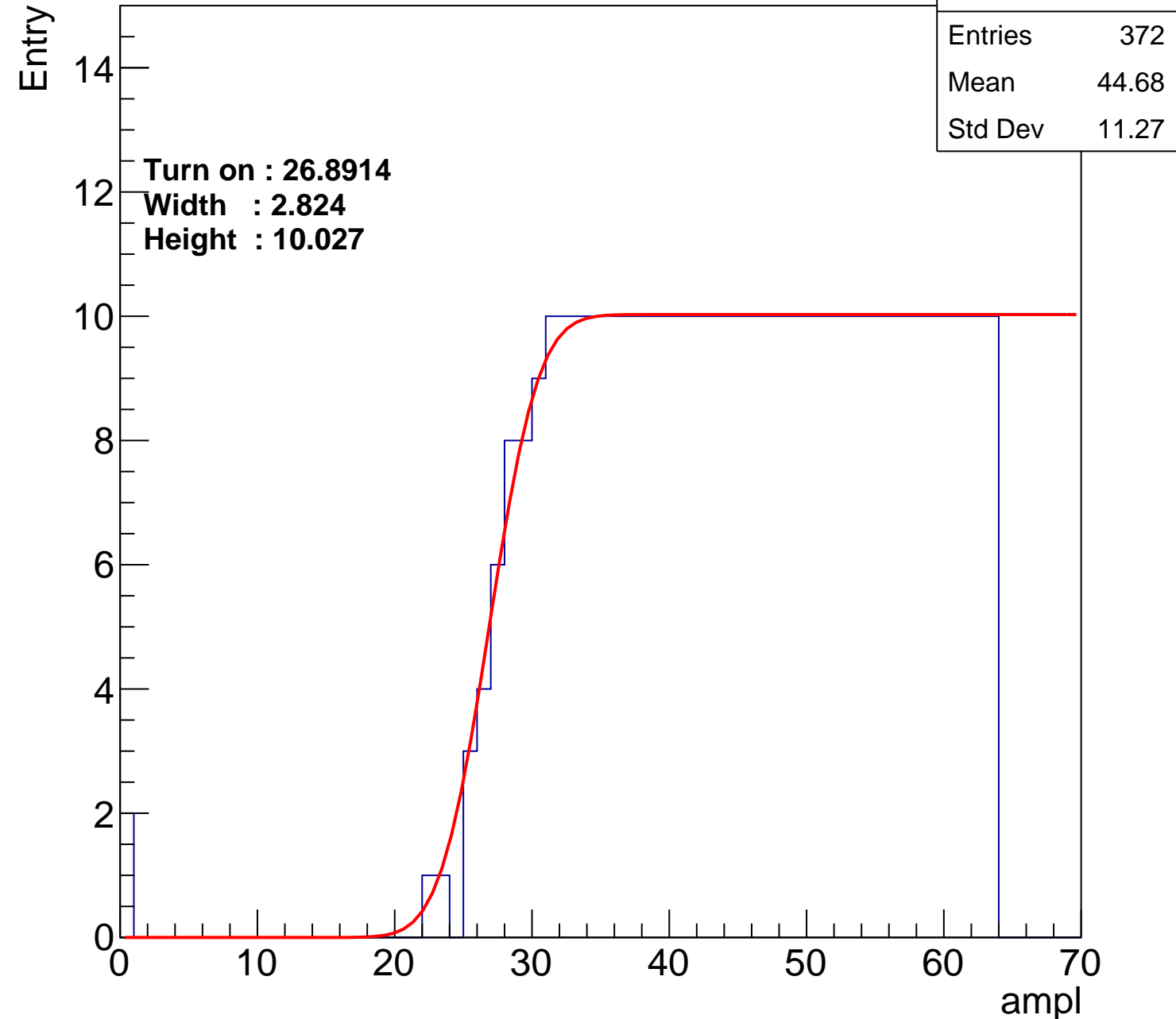
Width : 2.824

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch81

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.75
Std Dev	11.88

Turn on : 25.4488

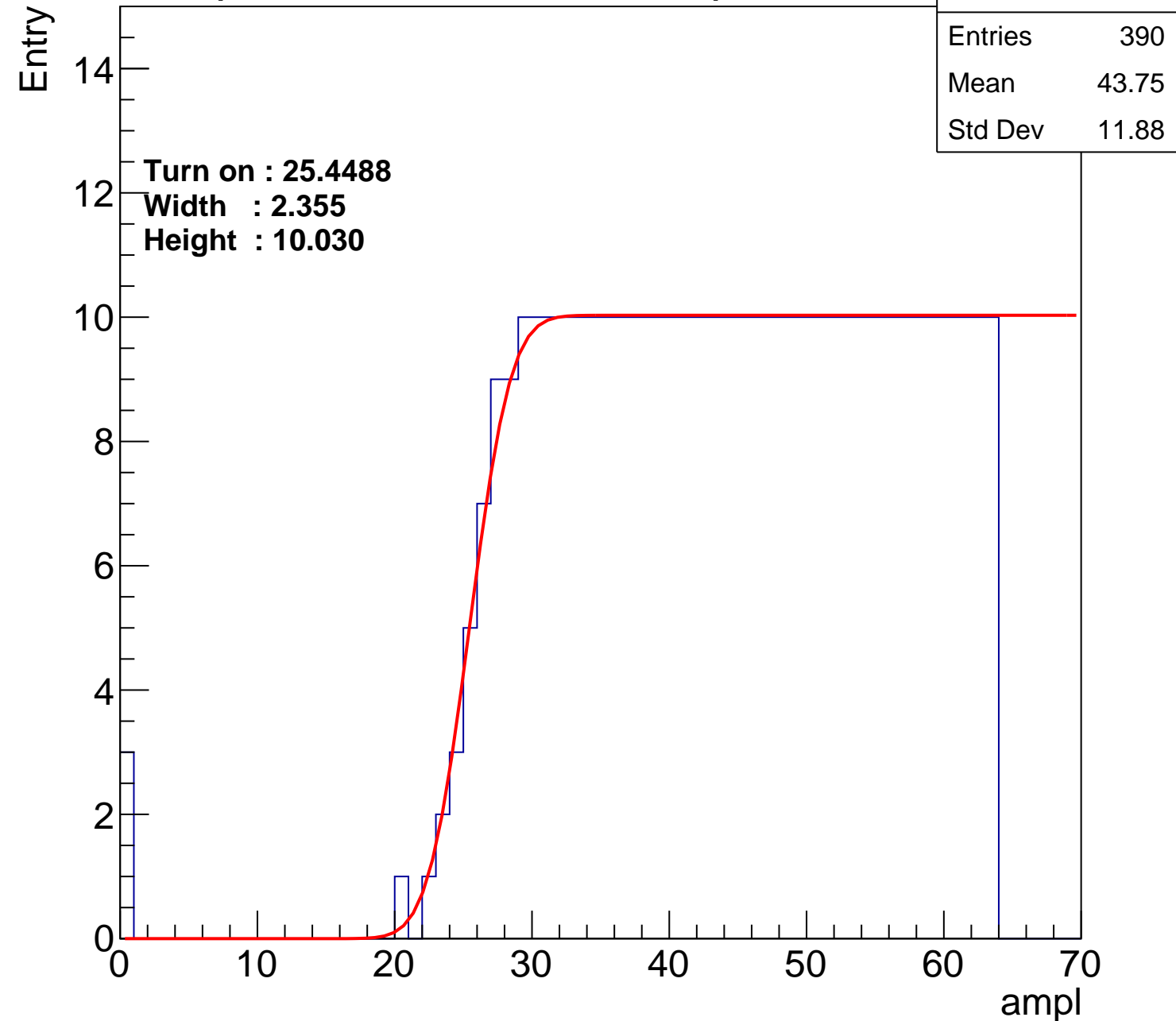
Width : 2.355

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch82

calib\_packv5\_042523\_0143.root, FC#10, port B3

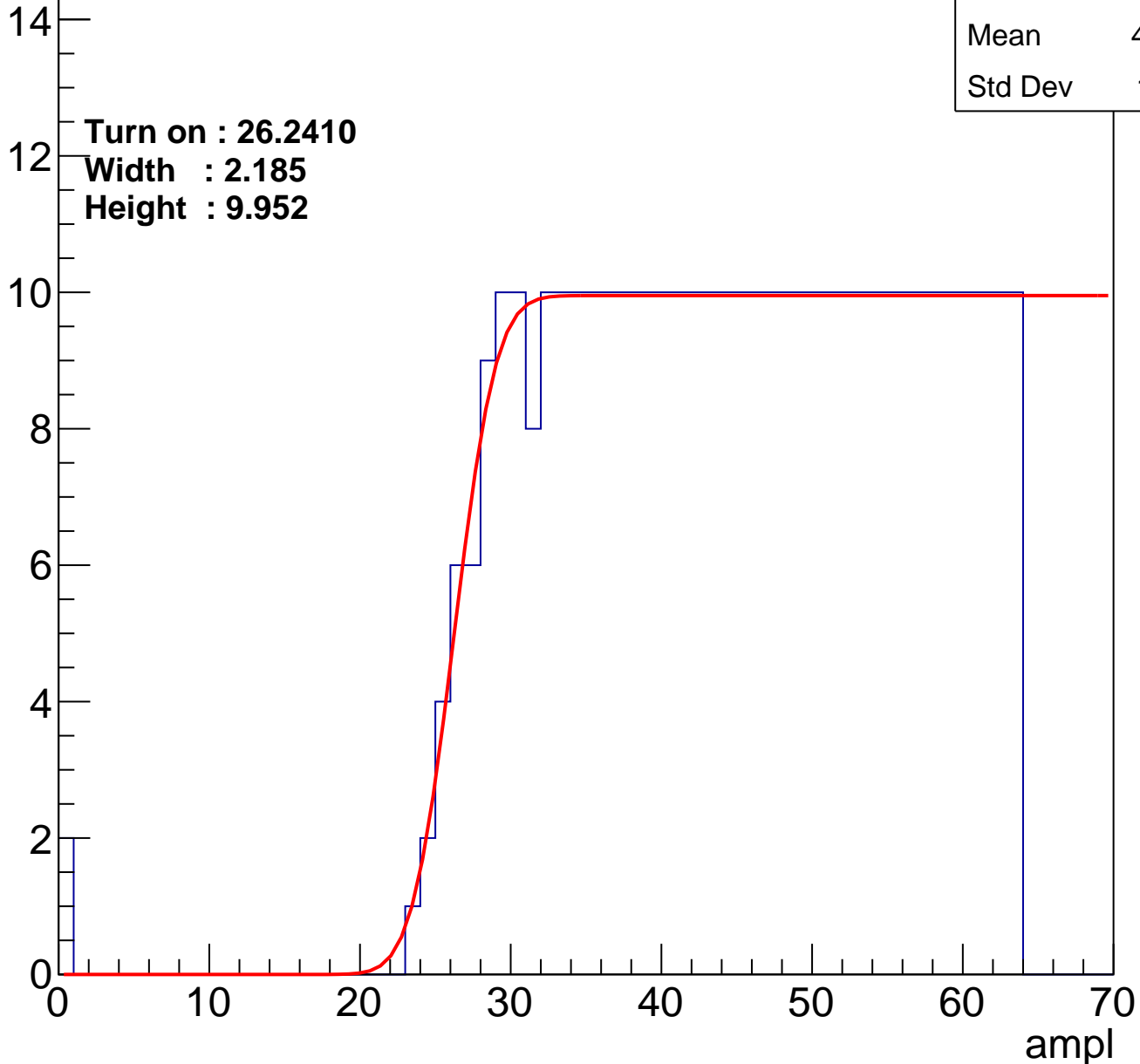
Entries	378
Mean	44.39
Std Dev	11.41

Turn on : 26.2410

Width : 2.185

Height : 9.952

Entry



# B1L002S, U11-ch83

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.41
Std Dev	11.93

Turn on : 27.4203

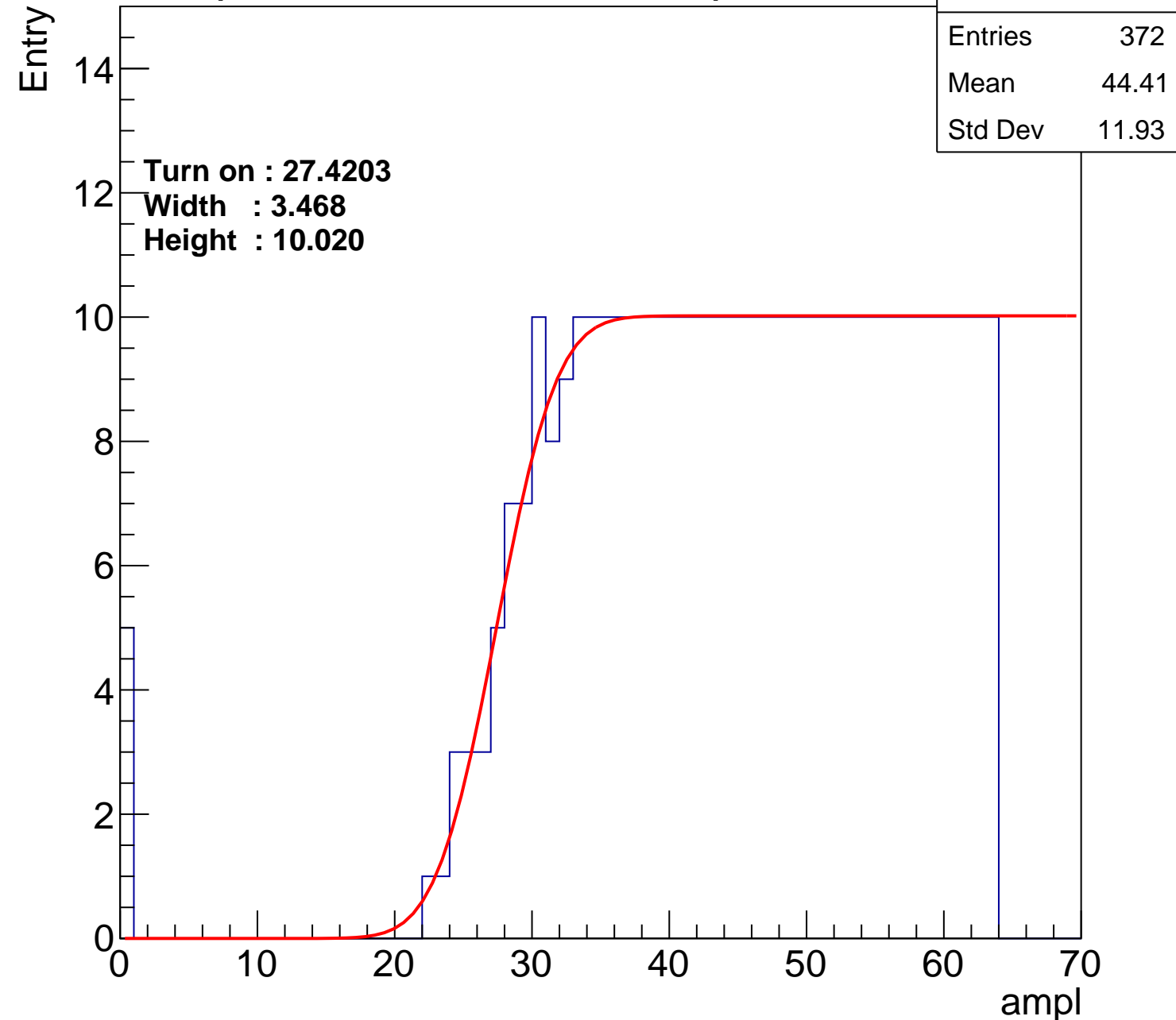
Width : 3.468

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch84

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.2
Std Dev	11.52

Turn on : 26.2462

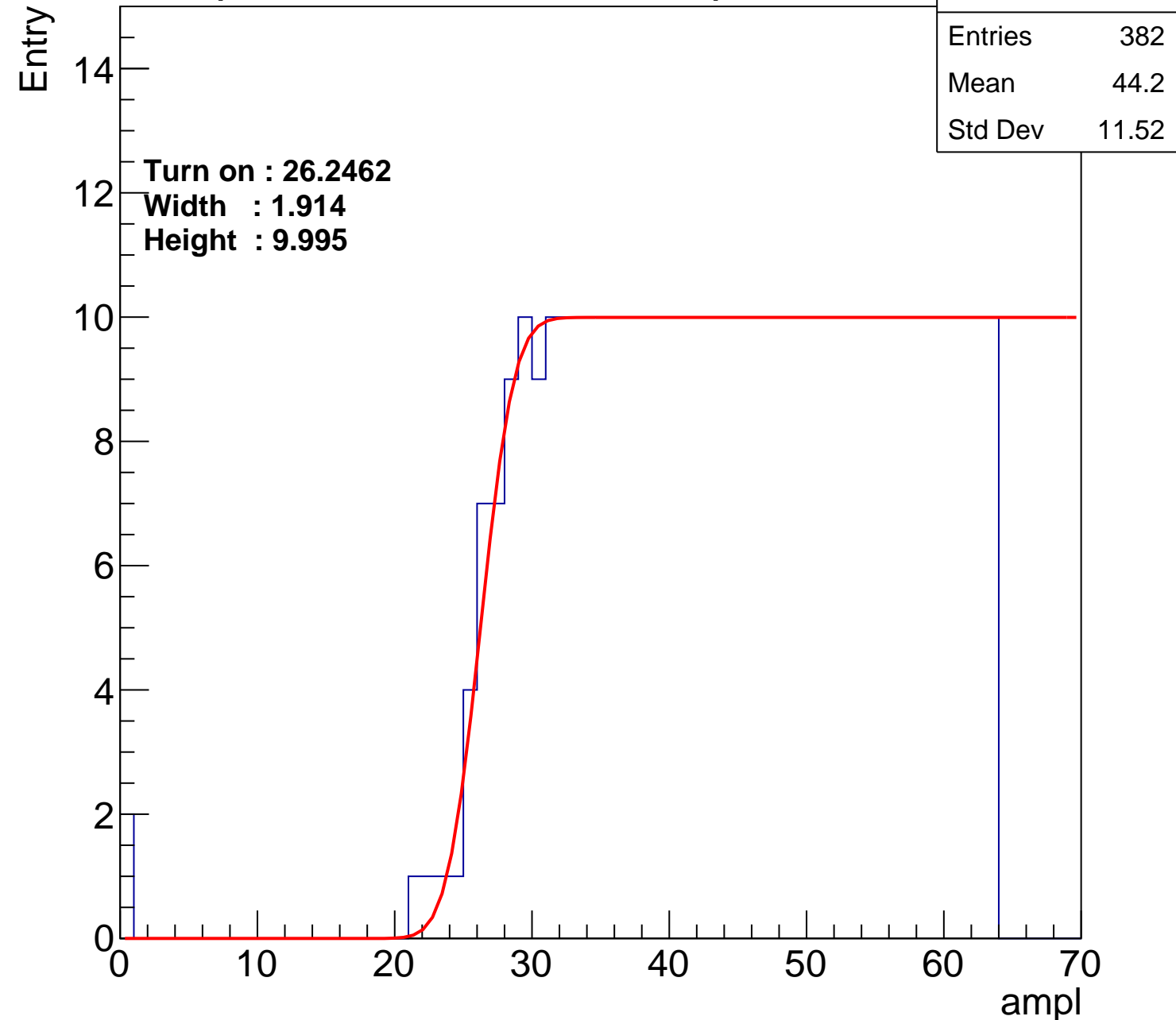
Width : 1.914

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch85

calib\_packv5\_042523\_0143.root, FC#10, port B3

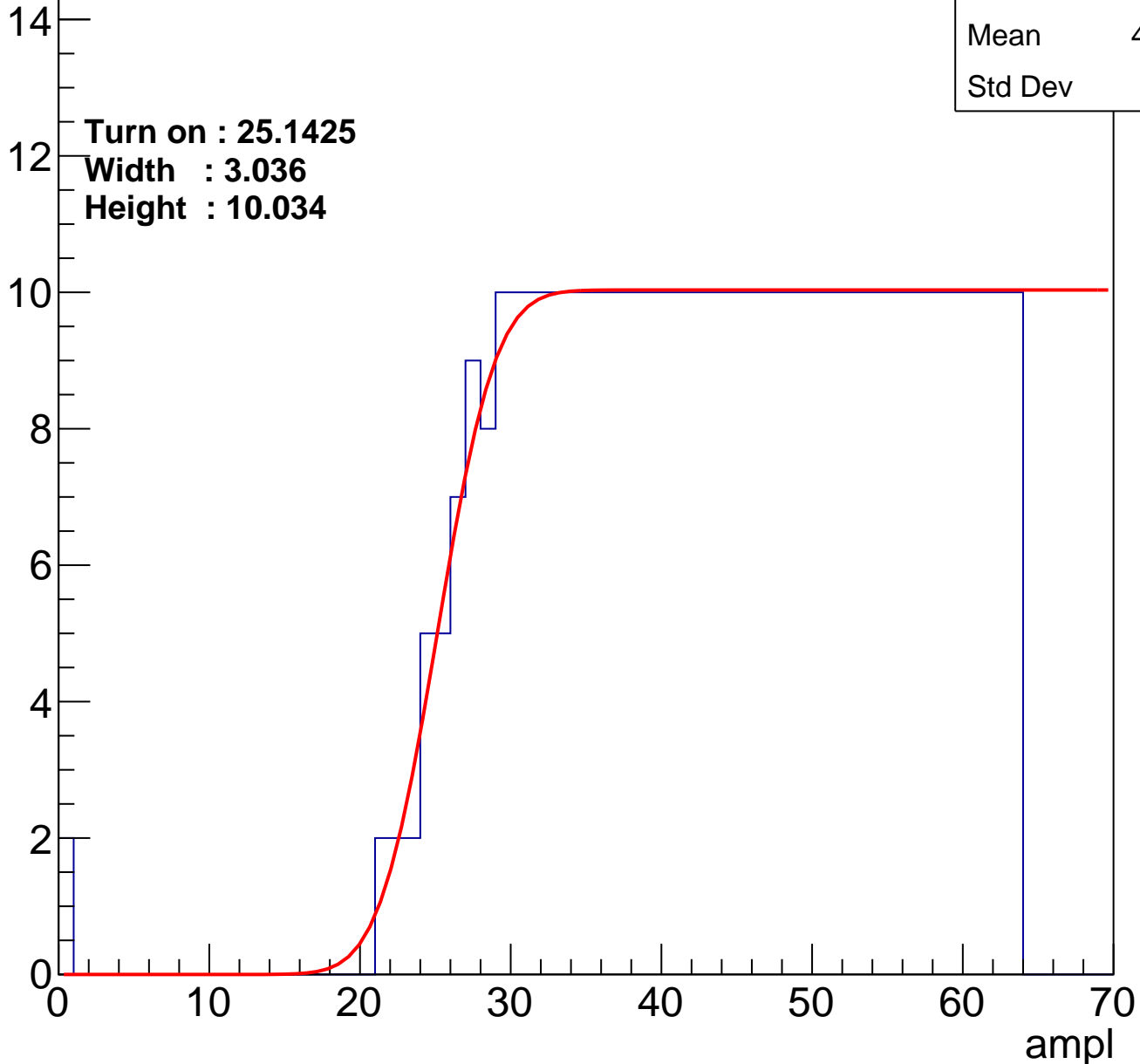
Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.1425

Width : 3.036

Height : 10.034

Entry



# B1L002S, U11-ch86

calib\_packv5\_042523\_0143.root, FC#10, port B3

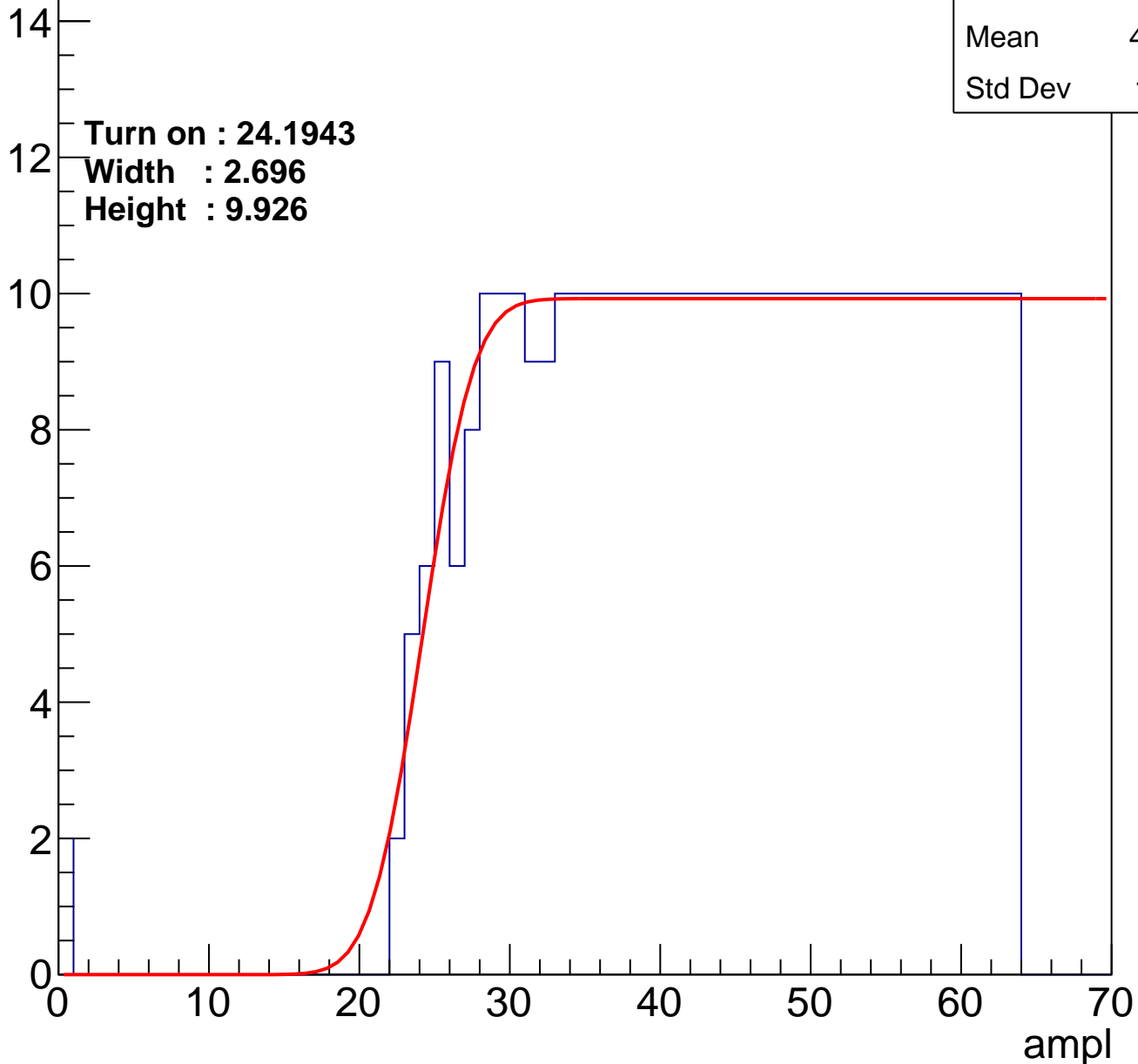
Entries	396
Mean	43.48
Std Dev	11.91

Turn on : 24.1943

Width : 2.696

Height : 9.926

Entry





# B1L002S, U11-ch87

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.25
Std Dev	11.79

Turn on : 26.6718

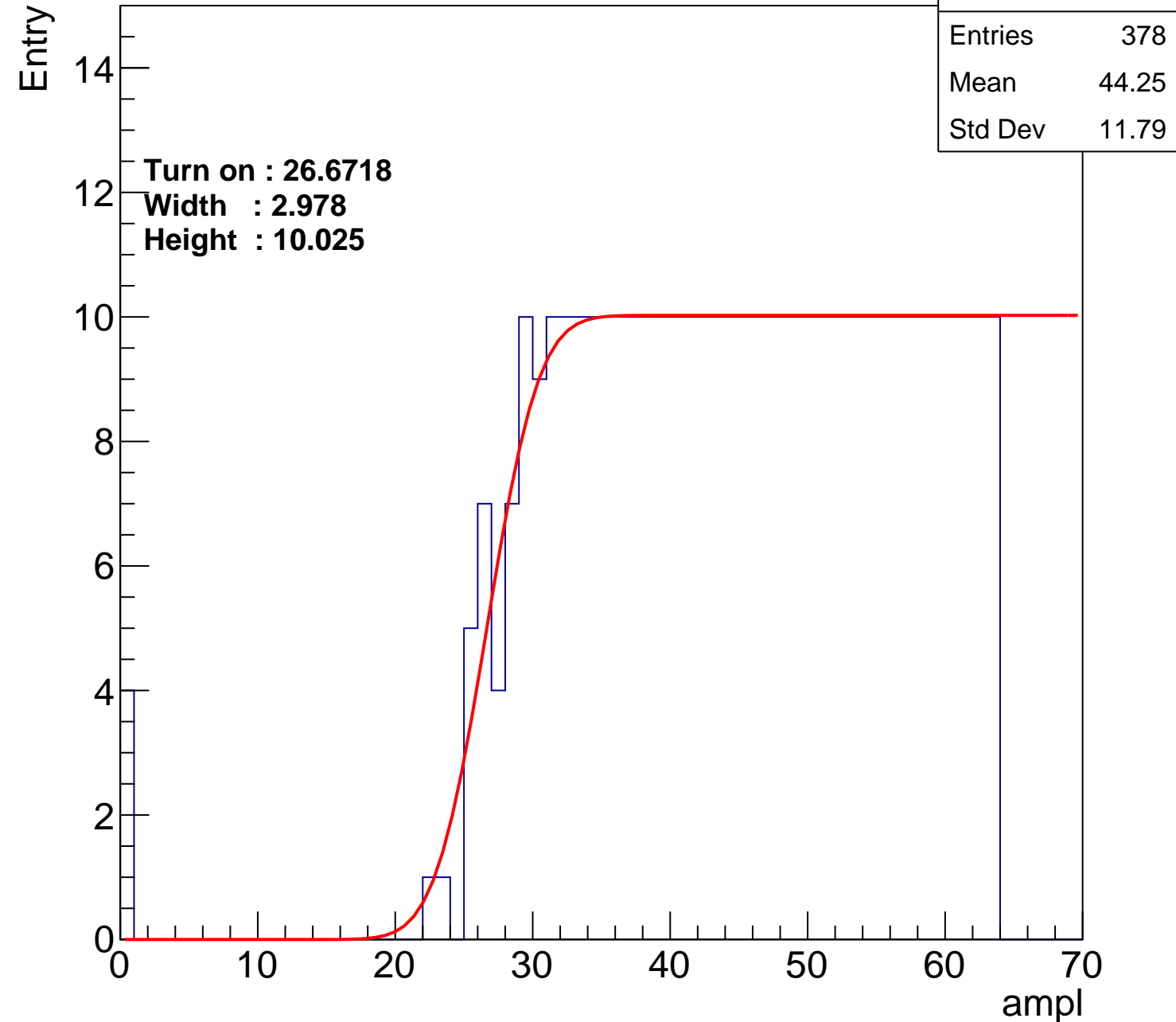
Width : 2.978

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch88

calib\_packv5\_042523\_0143.root, FC#10, port B3

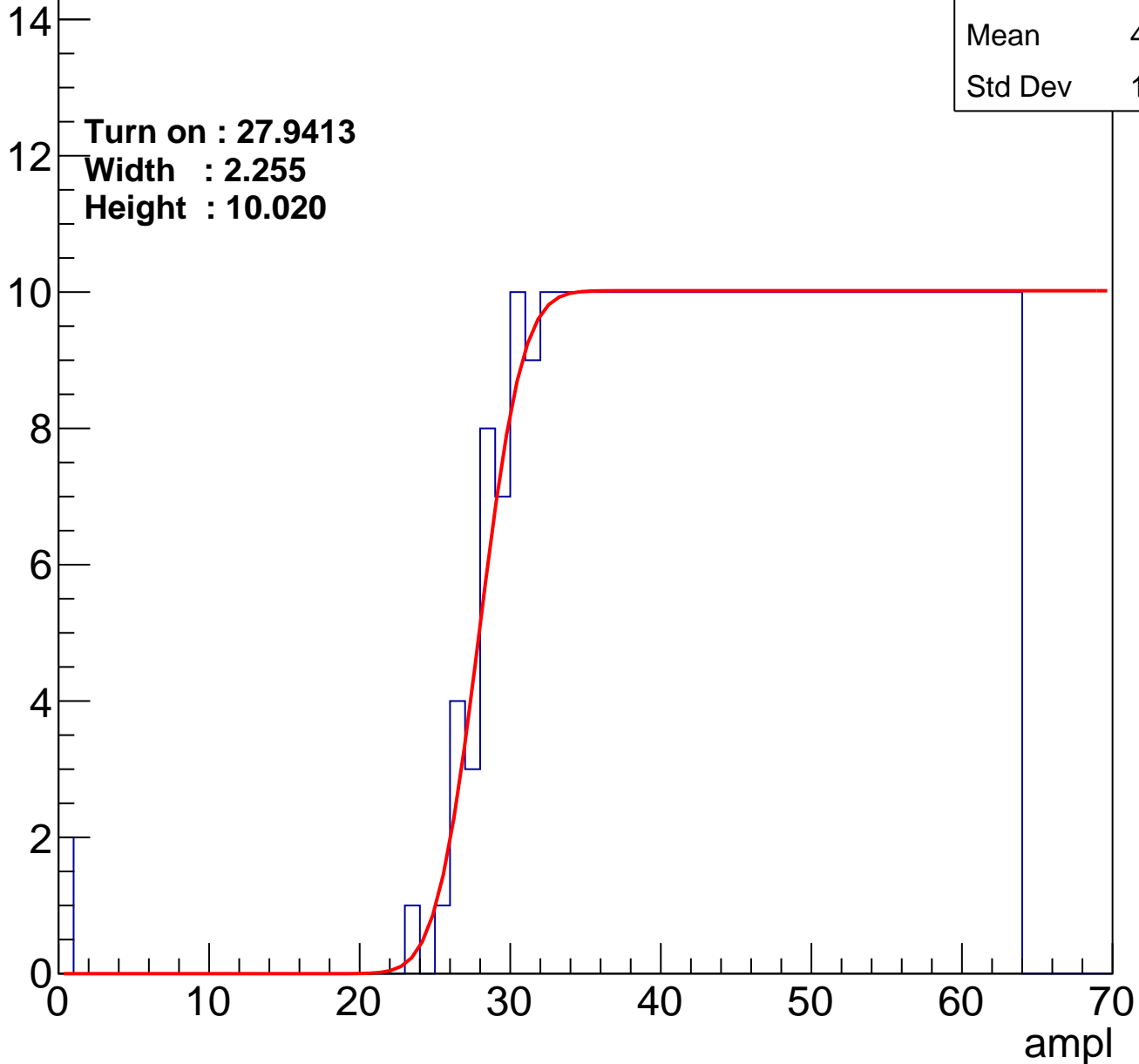
Entries	365
Mean	45.04
Std Dev	11.07

Turn on : 27.9413

Width : 2.255

Height : 10.020

Entry



# B1L002S, U11-ch89

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	408
Mean	42.84
Std Dev	12.37

Turn on : 23.4169

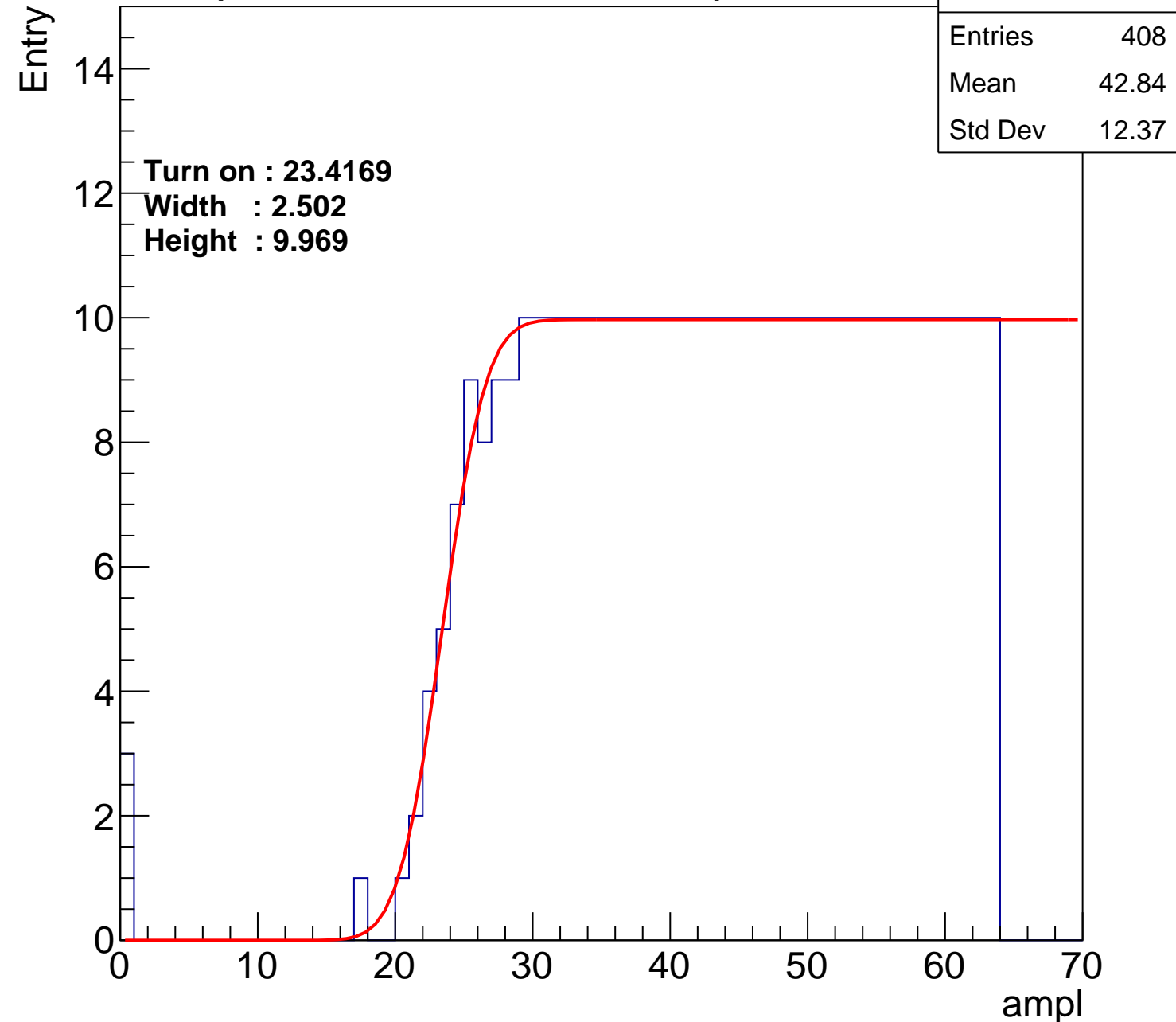
Width : 2.502

Height : 9.969

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch90

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.4389

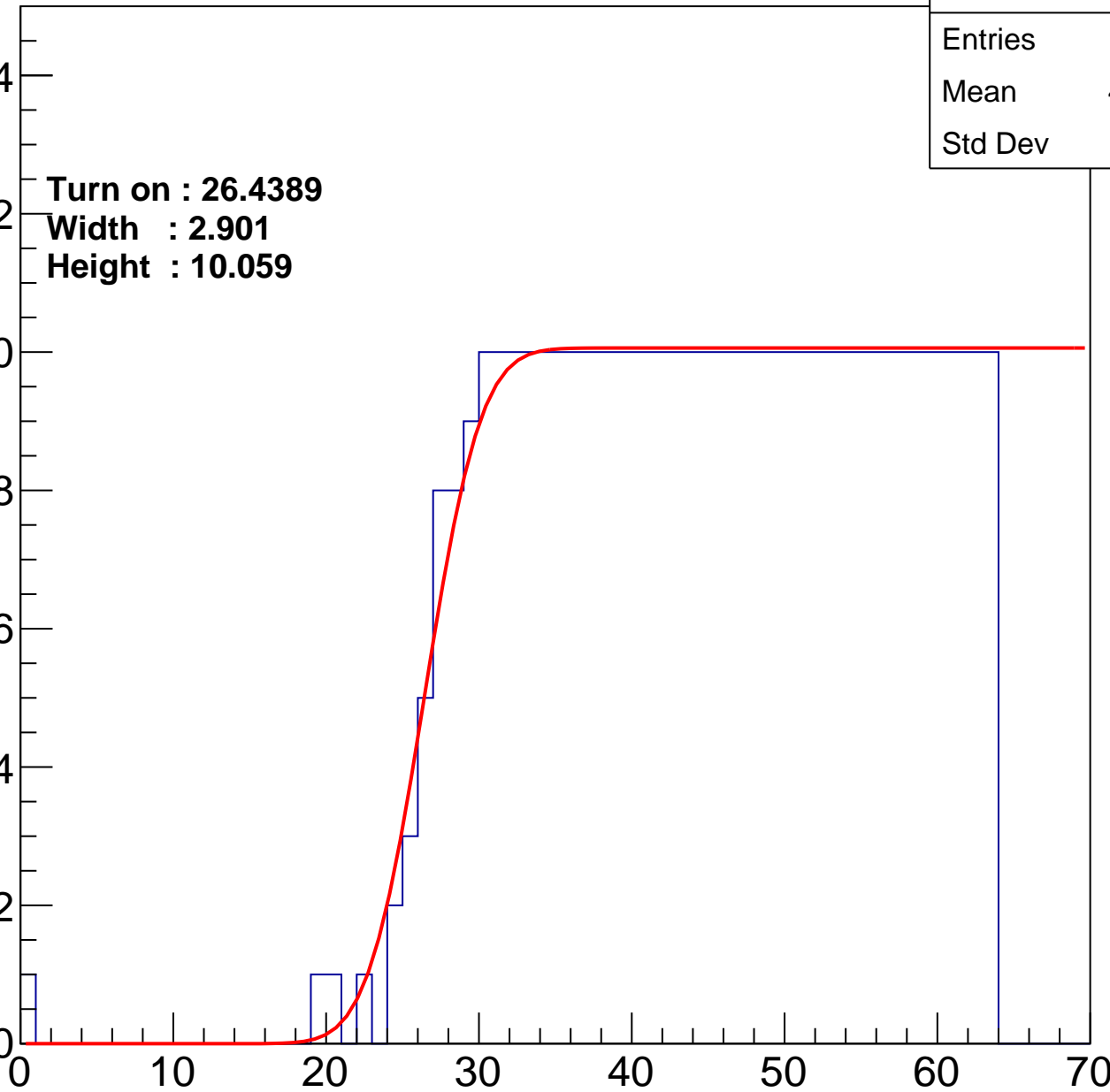
Width : 2.901

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch91

calib\_packv5\_042523\_0143.root, FC#10, port B3

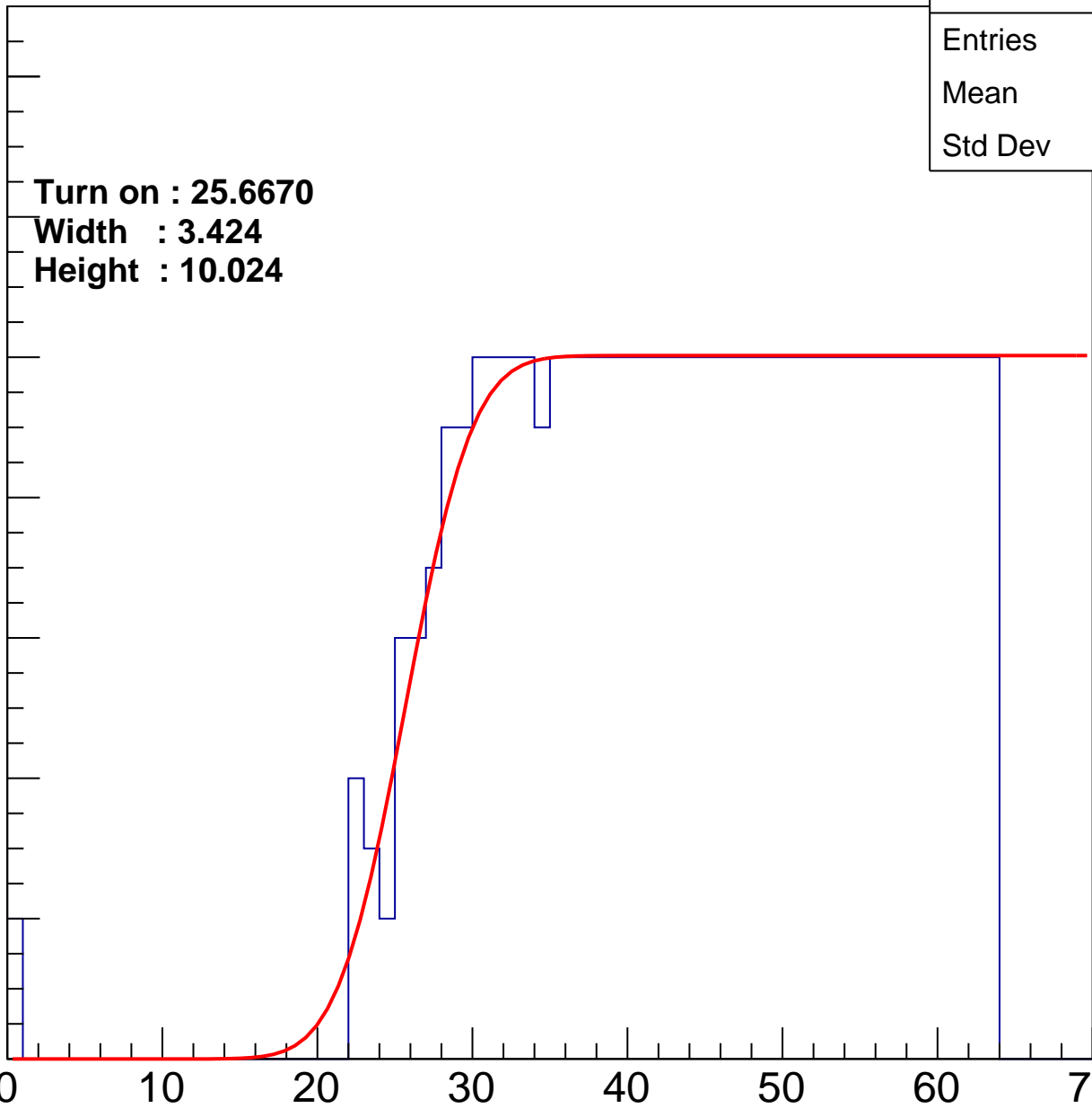
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.6670  
Width : 3.424  
Height : 10.024

Entries	387
Mean	43.9
Std Dev	11.72

ampl



# B1L002S, U11-ch92

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.64
Std Dev	12.11

Turn on : 25.6252

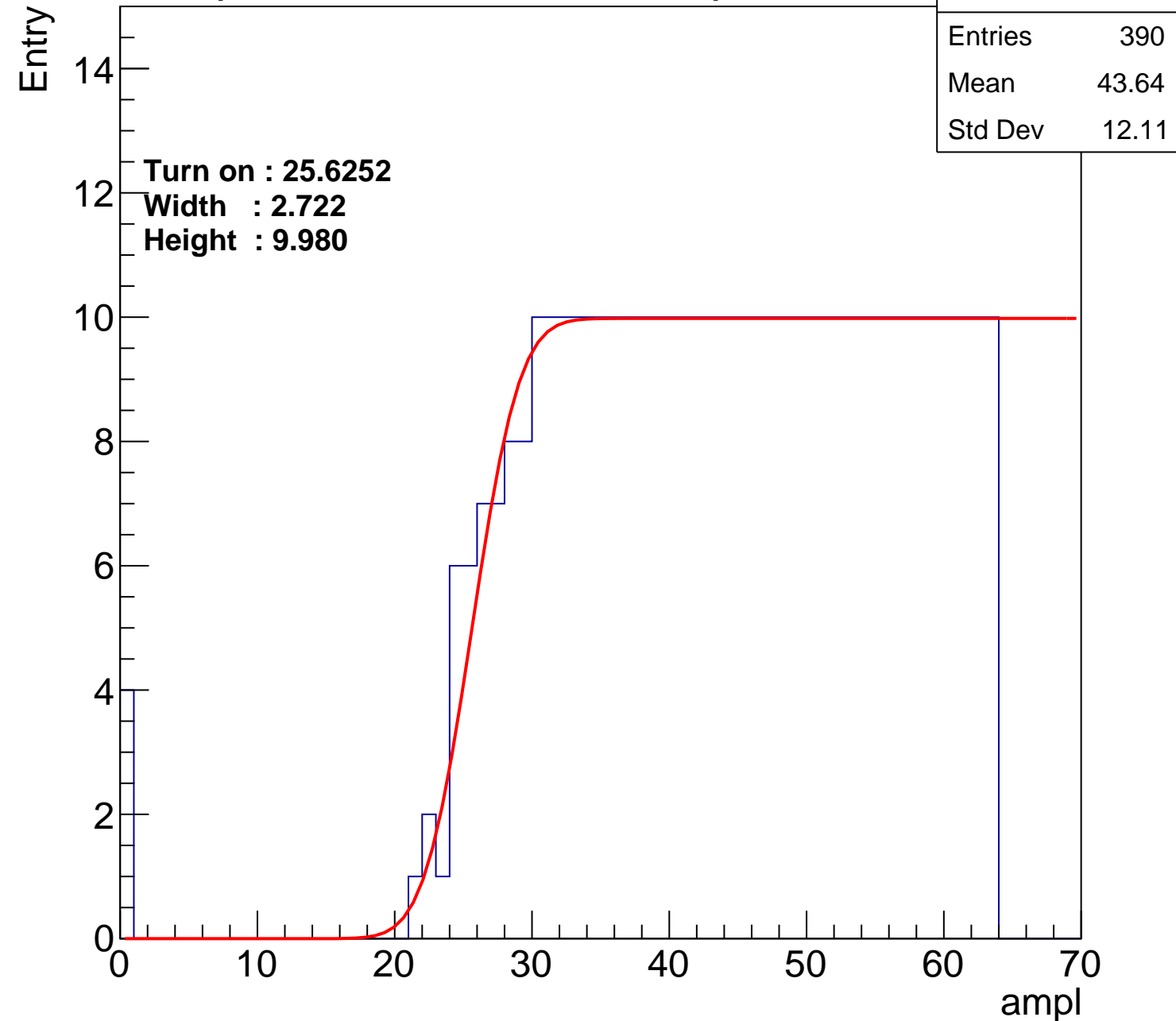
Width : 2.722

Height : 9.980

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch93

calib\_packv5\_042523\_0143.root, FC#10, port B3

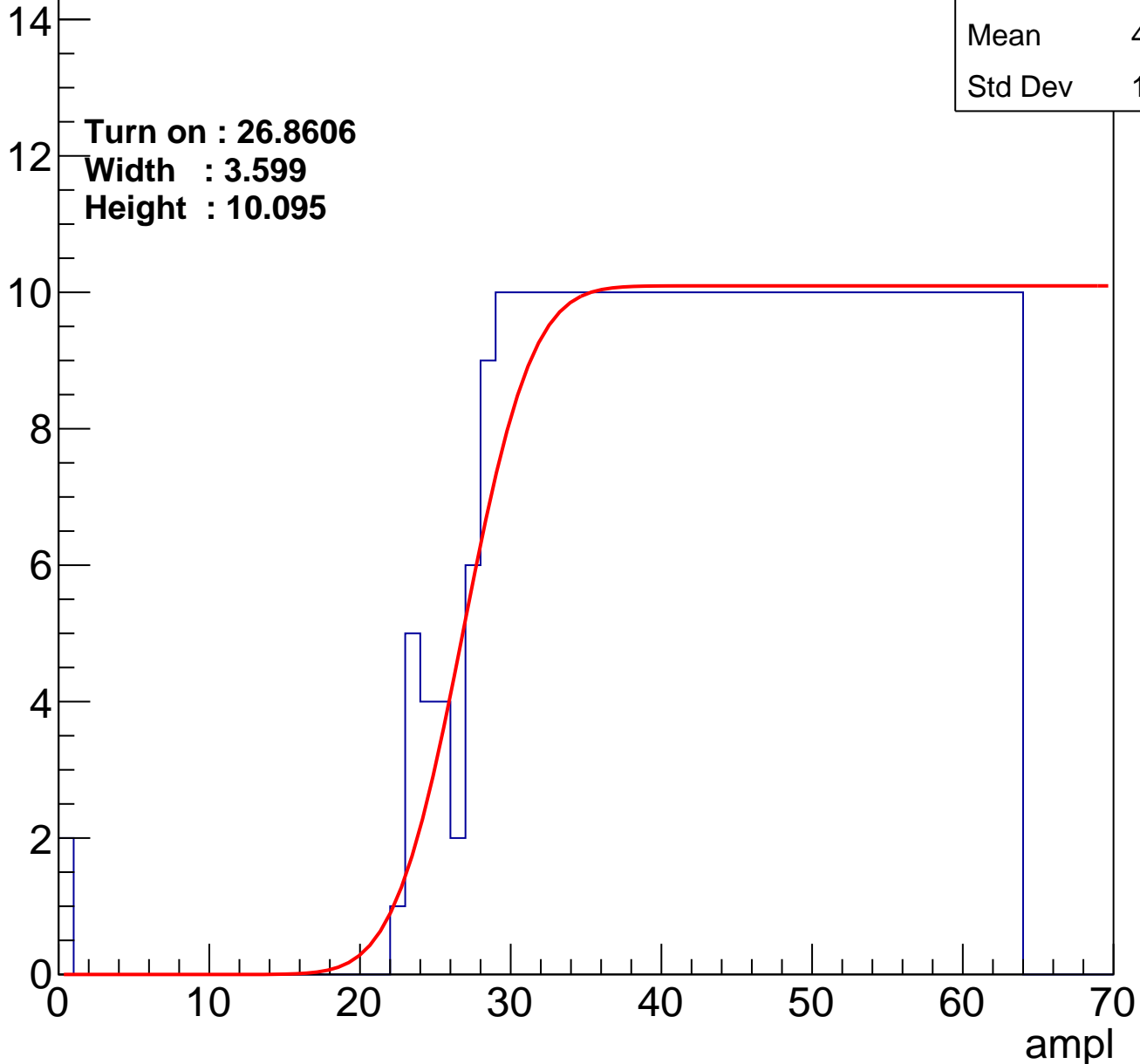
Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.8606

Width : 3.599

Height : 10.095

Entry



# B1L002S, U11-ch94

calib\_packv5\_042523\_0143.root, FC#10, port B3

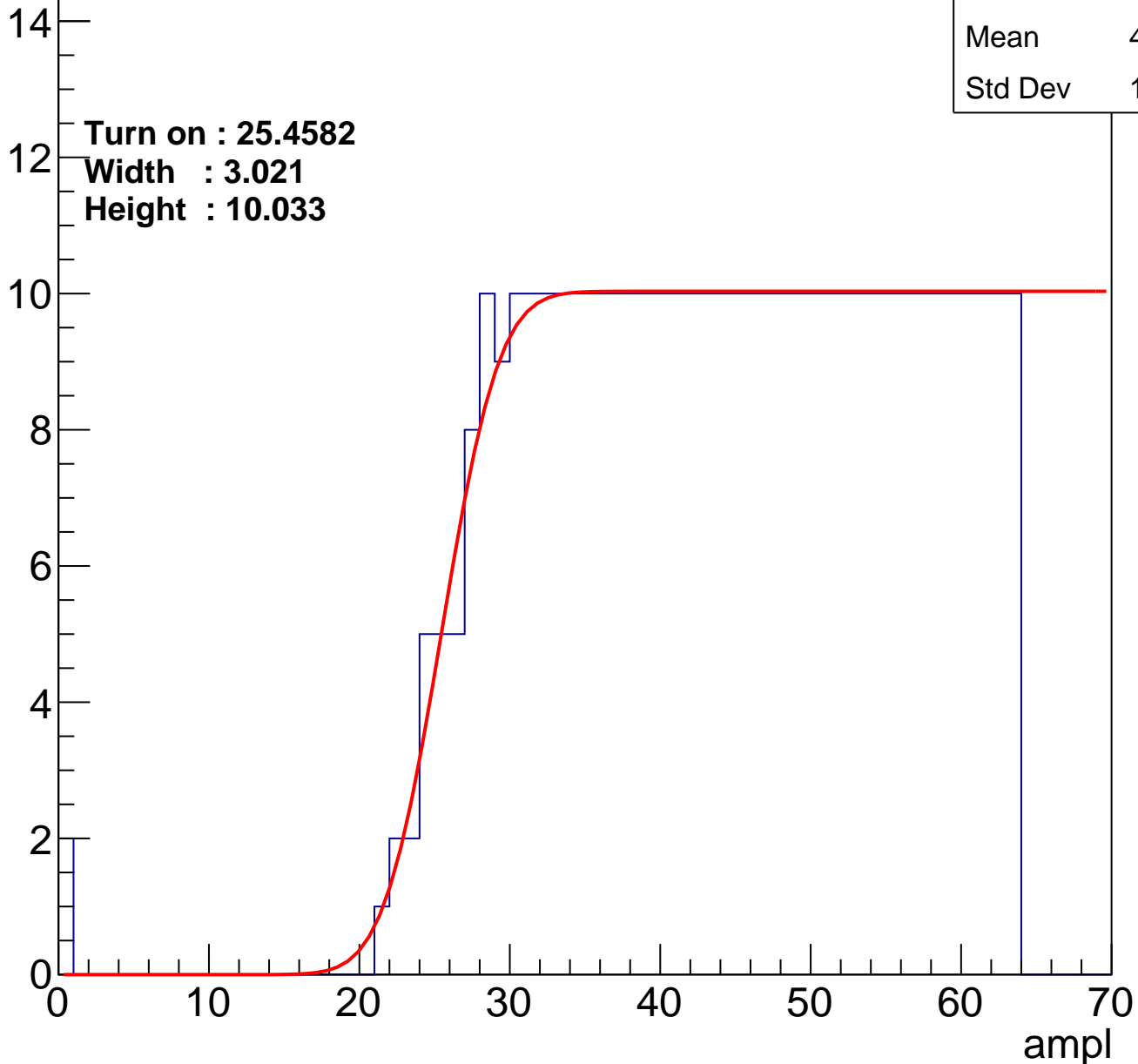
Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.4582

Width : 3.021

Height : 10.033

Entry





# B1L002S, U11-ch95

calib\_packv5\_042523\_0143.root, FC#10, port B3

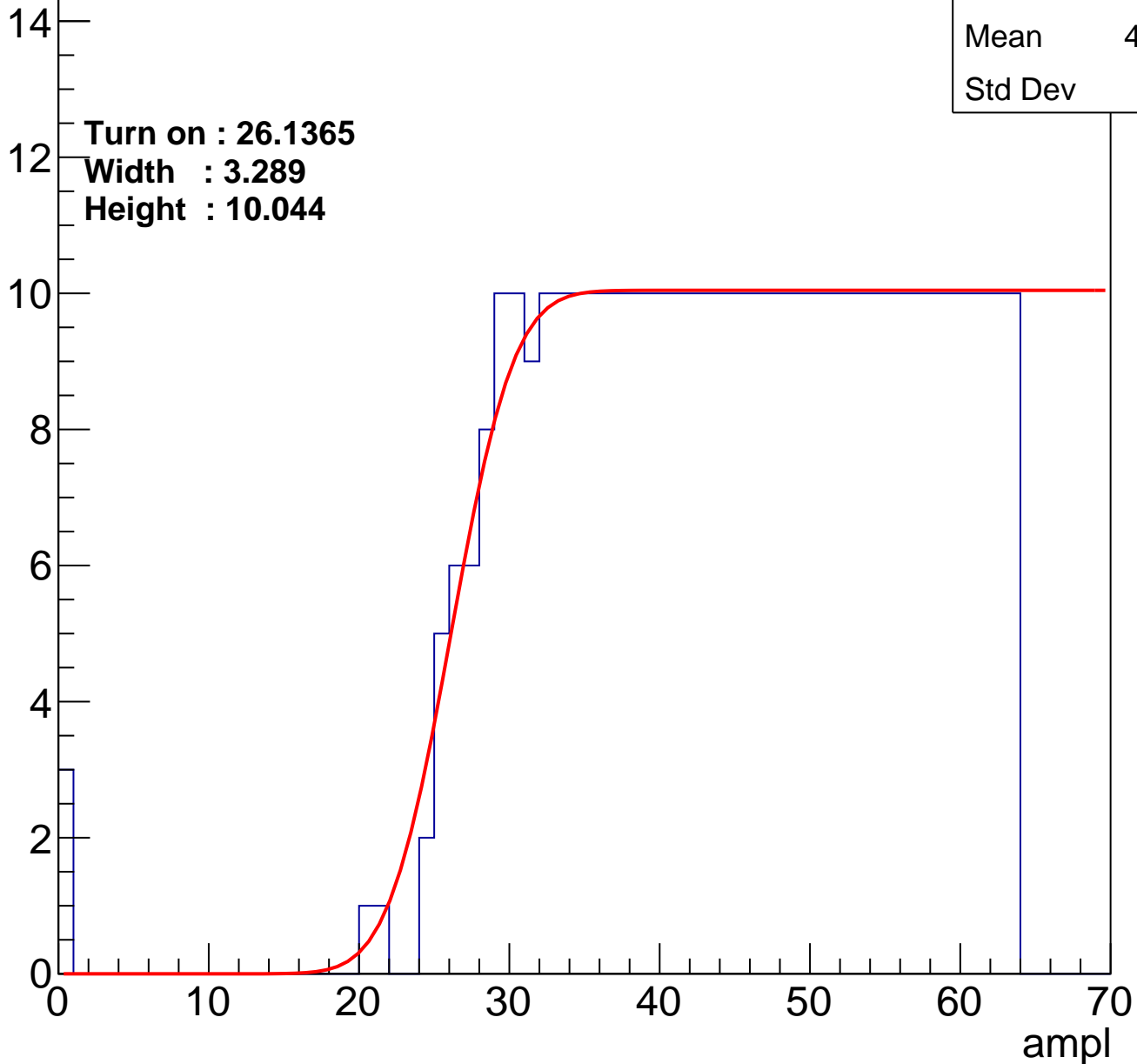
Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.1365

Width : 3.289

Height : 10.044

Entry



# B1L002S, U11-ch96

calib\_packv5\_042523\_0143.root, FC#10, port B3

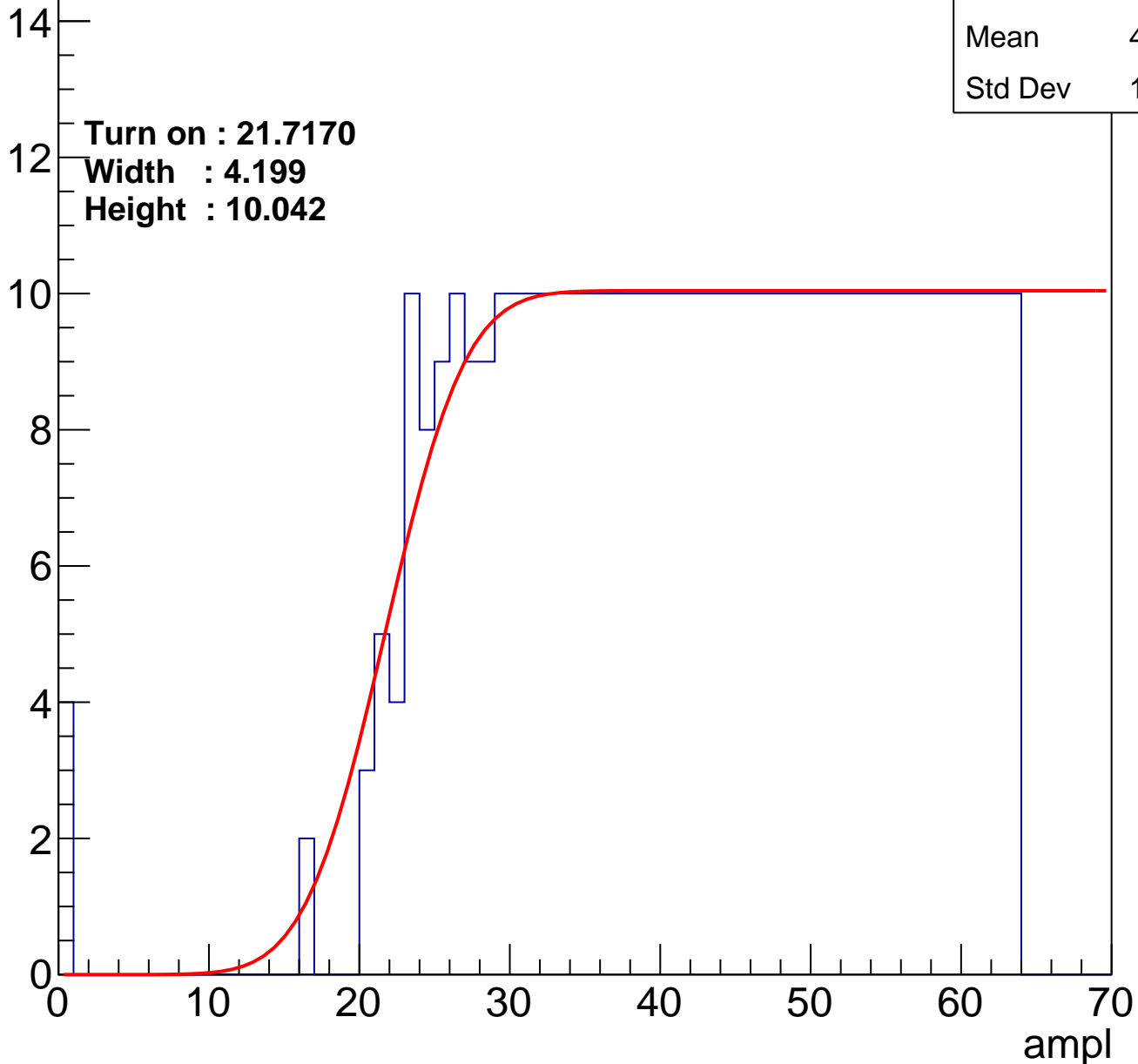
Entries	423
Mean	42.05
Std Dev	12.87

Turn on : 21.7170

Width : 4.199

Height : 10.042

Entry



# B1L002S, U11-ch97

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.89
Std Dev	11.86

Turn on : 25.0722

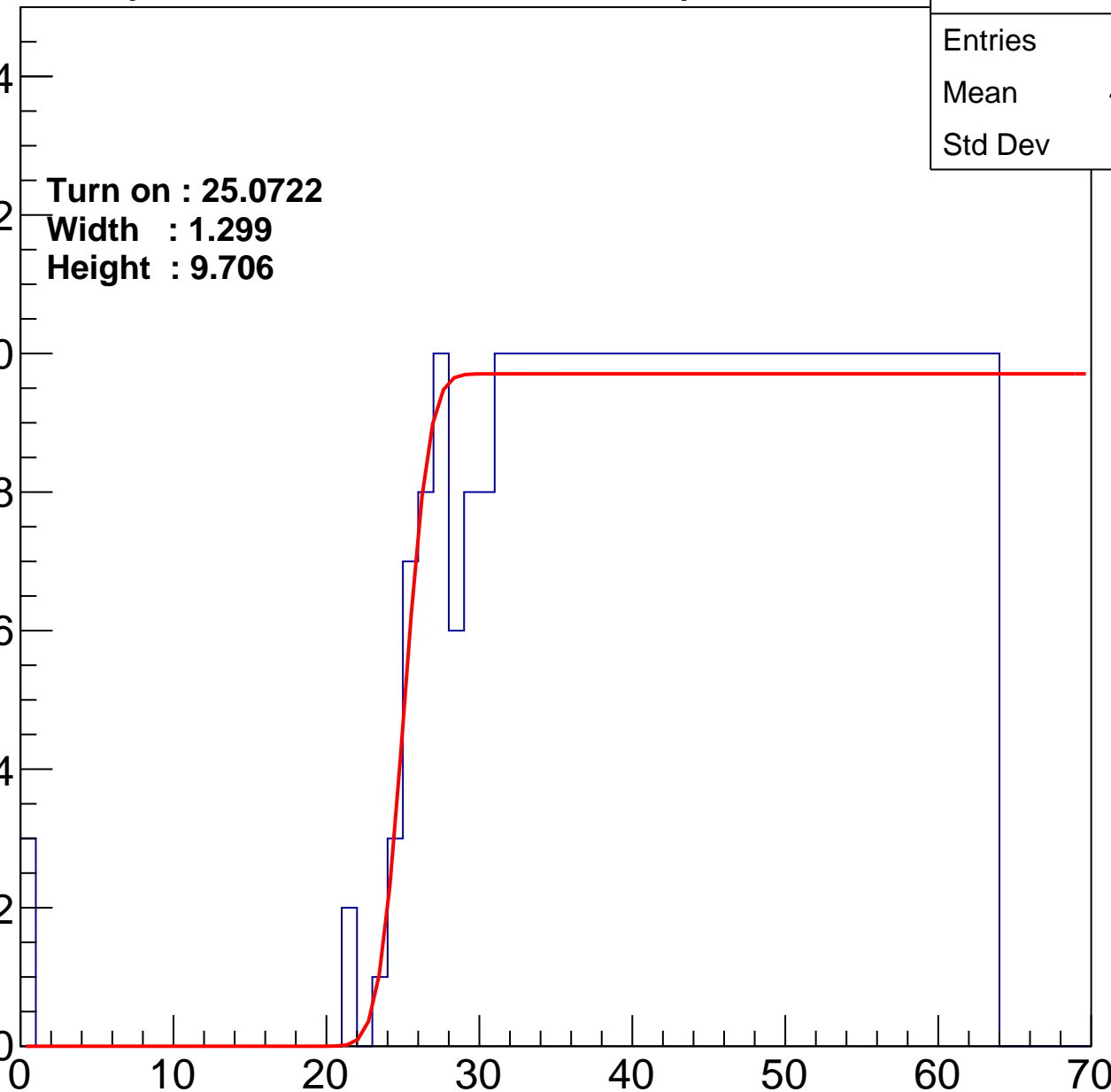
Width : 1.299

Height : 9.706

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch98

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.17
Std Dev	11.55

Turn on : 25.7910

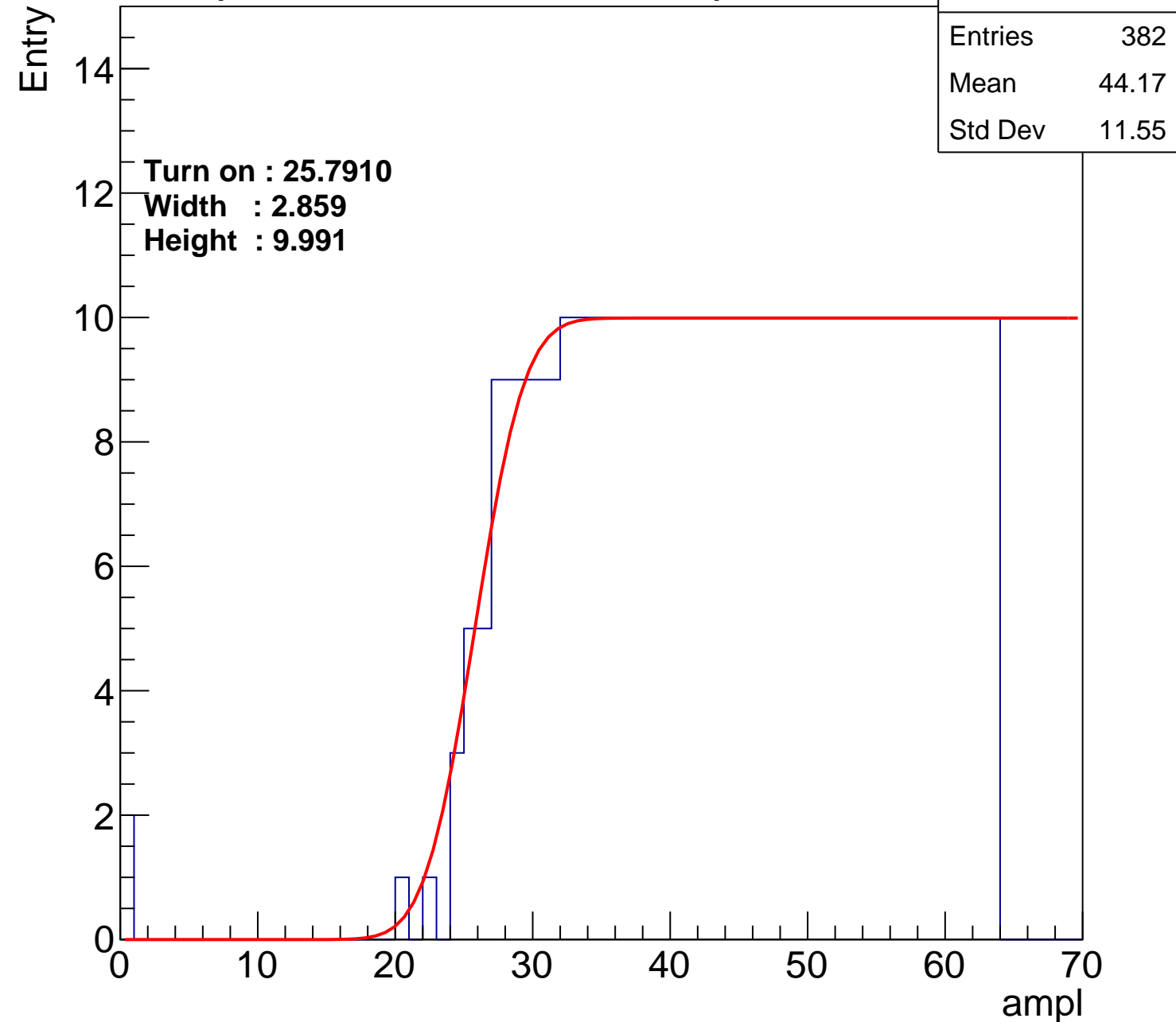
Width : 2.859

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch99

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.8
Std Dev	12.02

Turn on : 26.2305

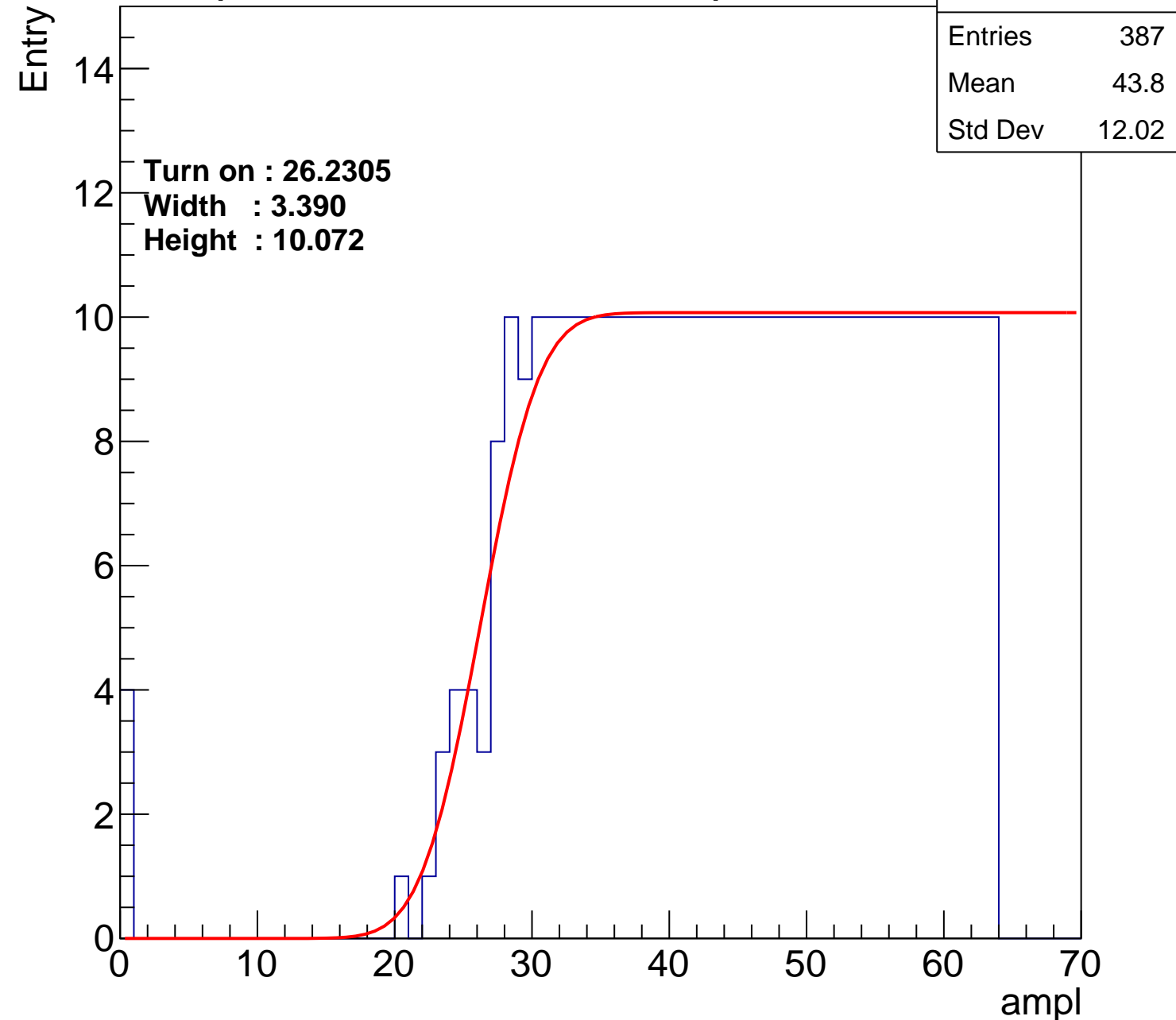
Width : 3.390

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch100

calib\_packv5\_042523\_0143.root, FC#10, port B3

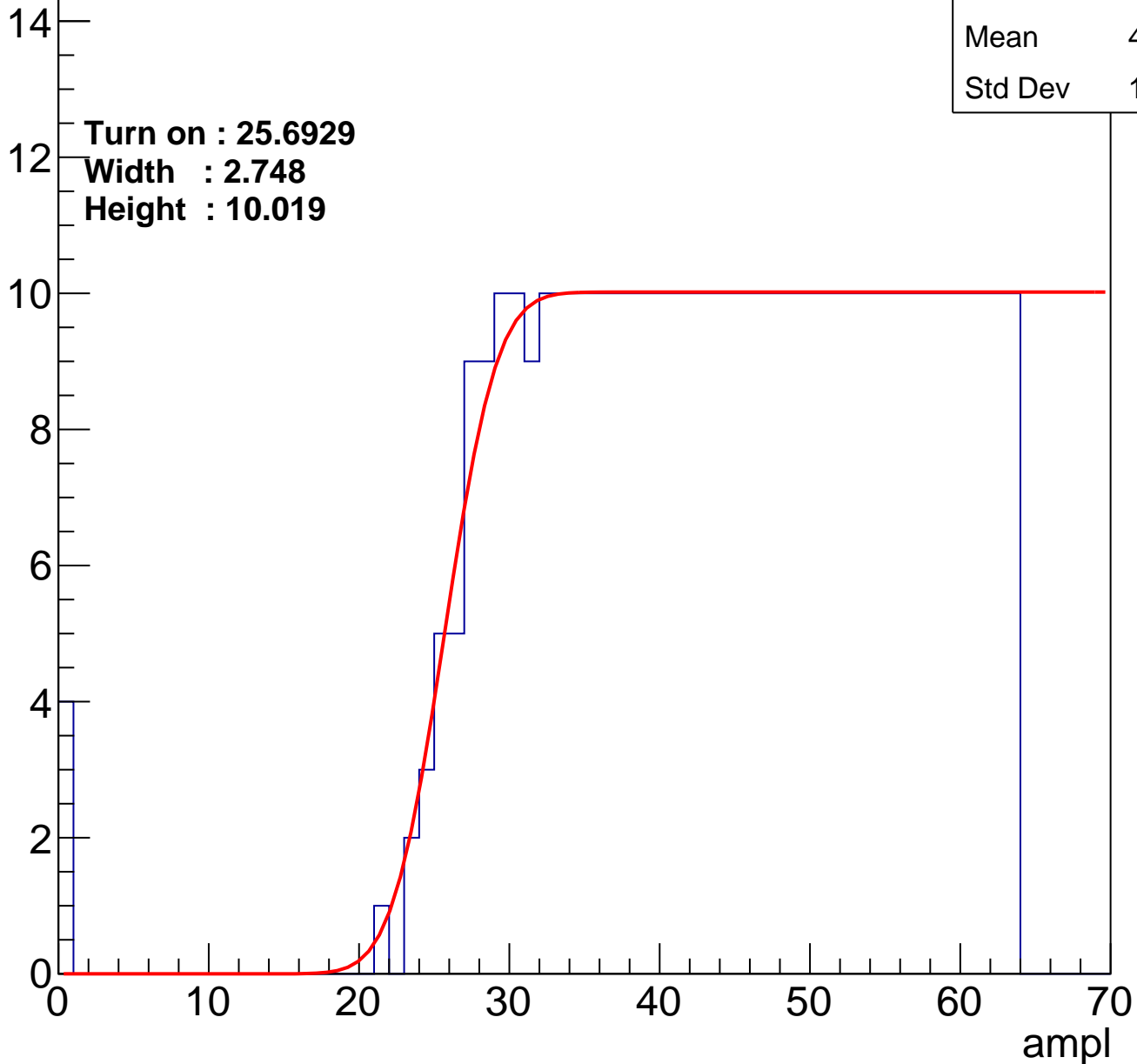
Entries	387
Mean	43.82
Std Dev	11.99

Turn on : 25.6929

Width : 2.748

Height : 10.019

Entry



# B1L002S, U11-ch101

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.4259

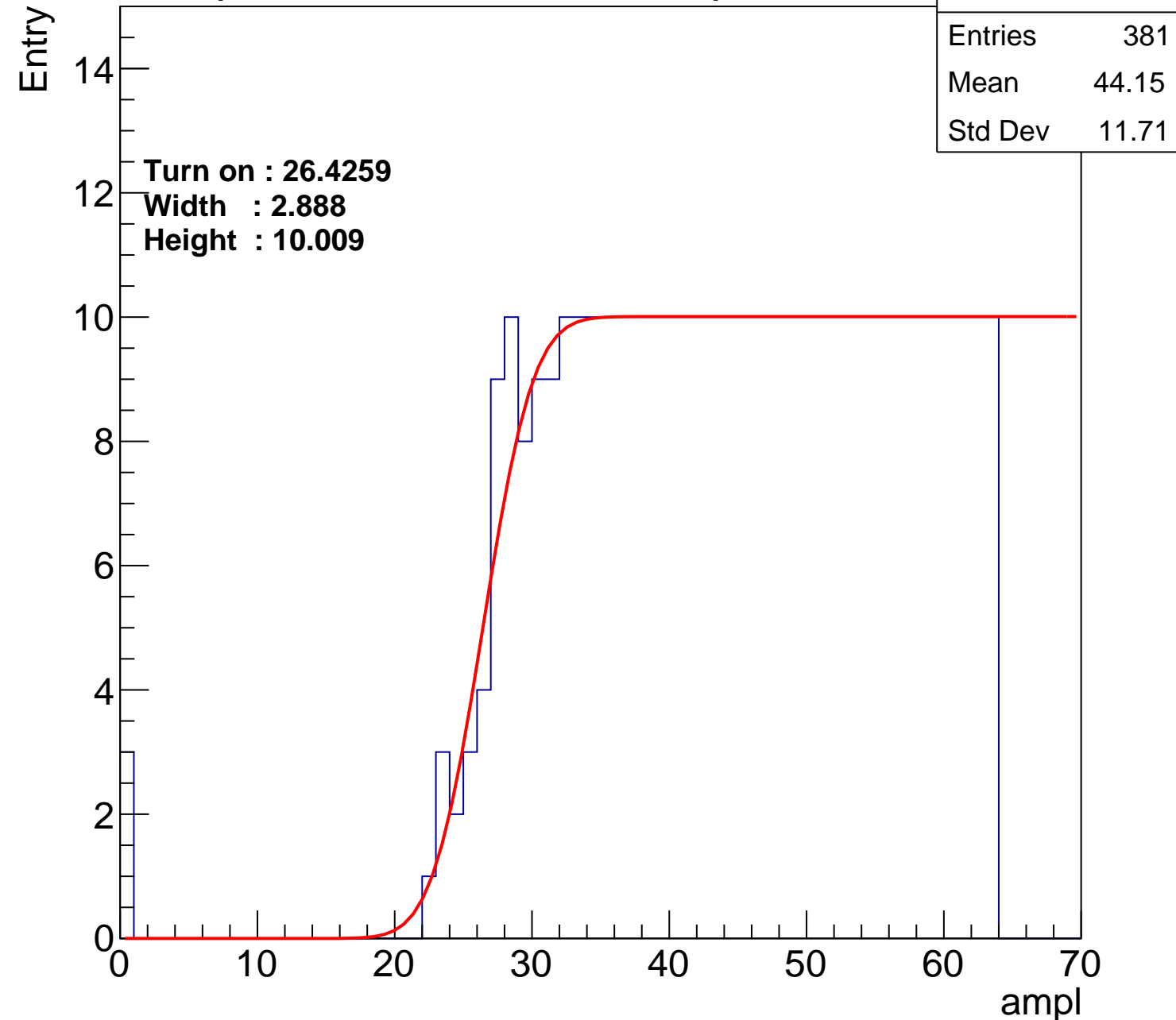
Width : 2.888

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch102

calib\_packv5\_042523\_0143.root, FC#10, port B3

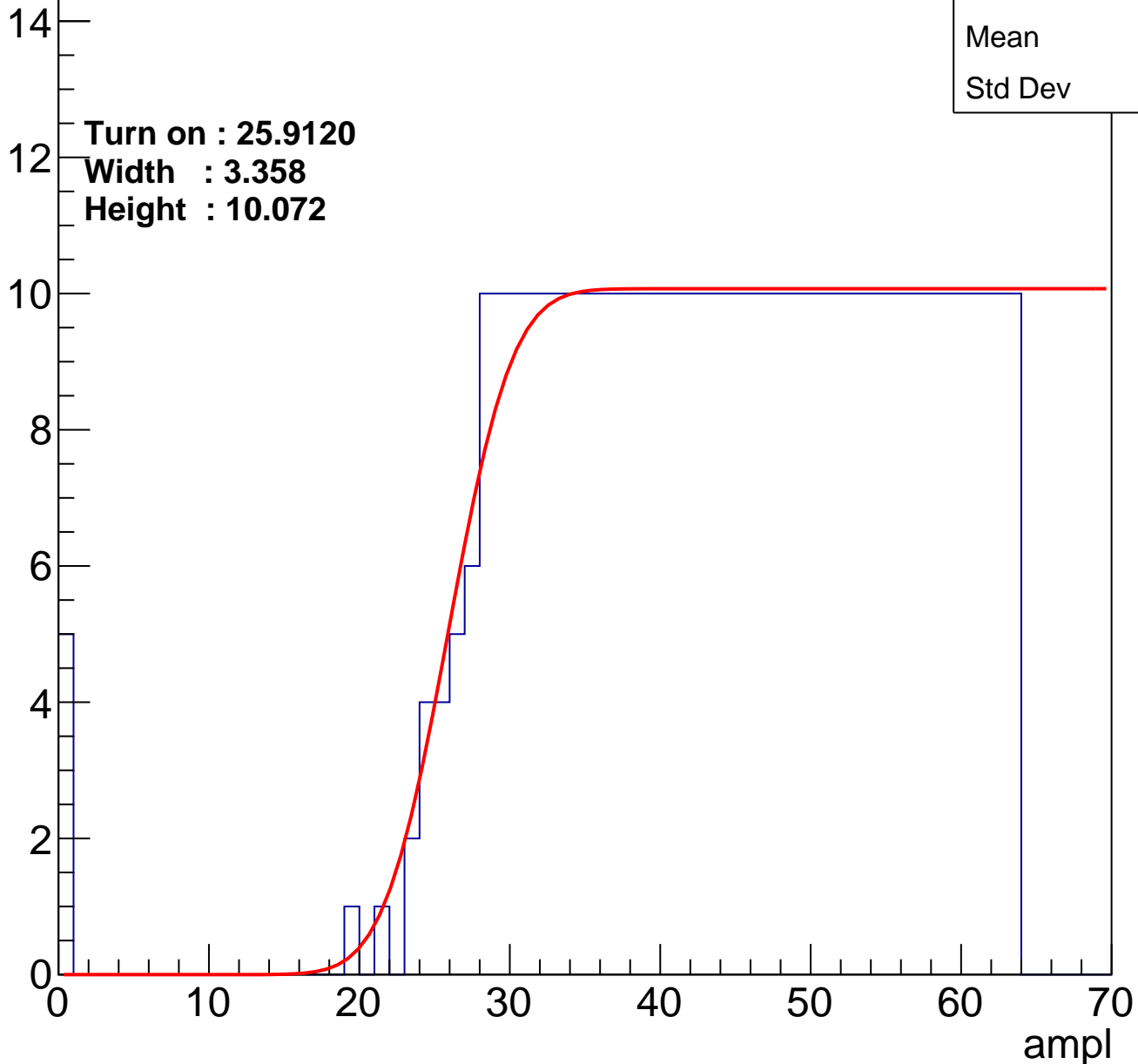
Entries	388
Mean	43.7
Std Dev	12.2

Turn on : 25.9120

Width : 3.358

Height : 10.072

Entry





# B1L002S, U11-ch103

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	43.88
Std Dev	12.12

Turn on : 26.1926

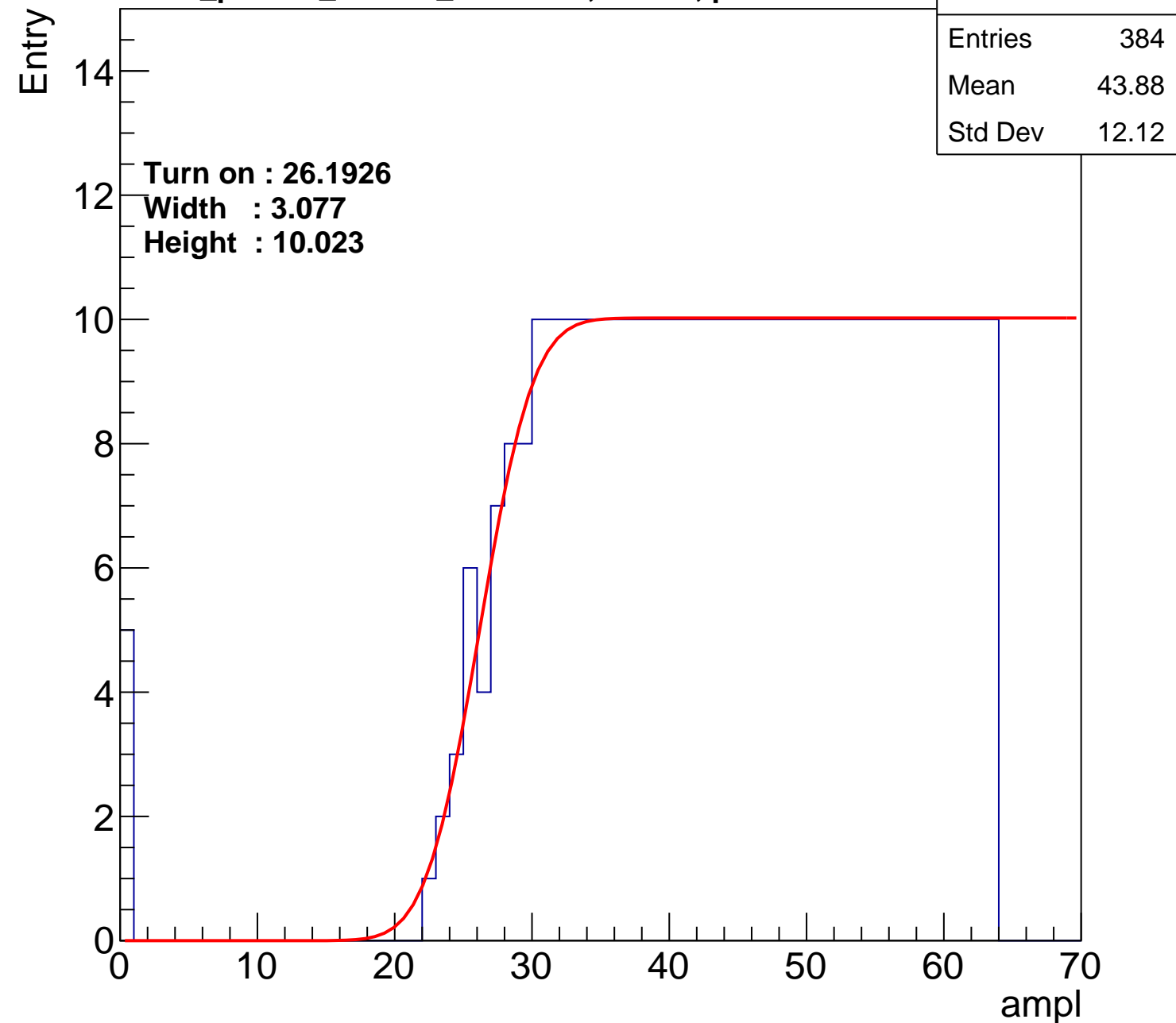
Width : 3.077

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch104

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.9121

Width : 2.789

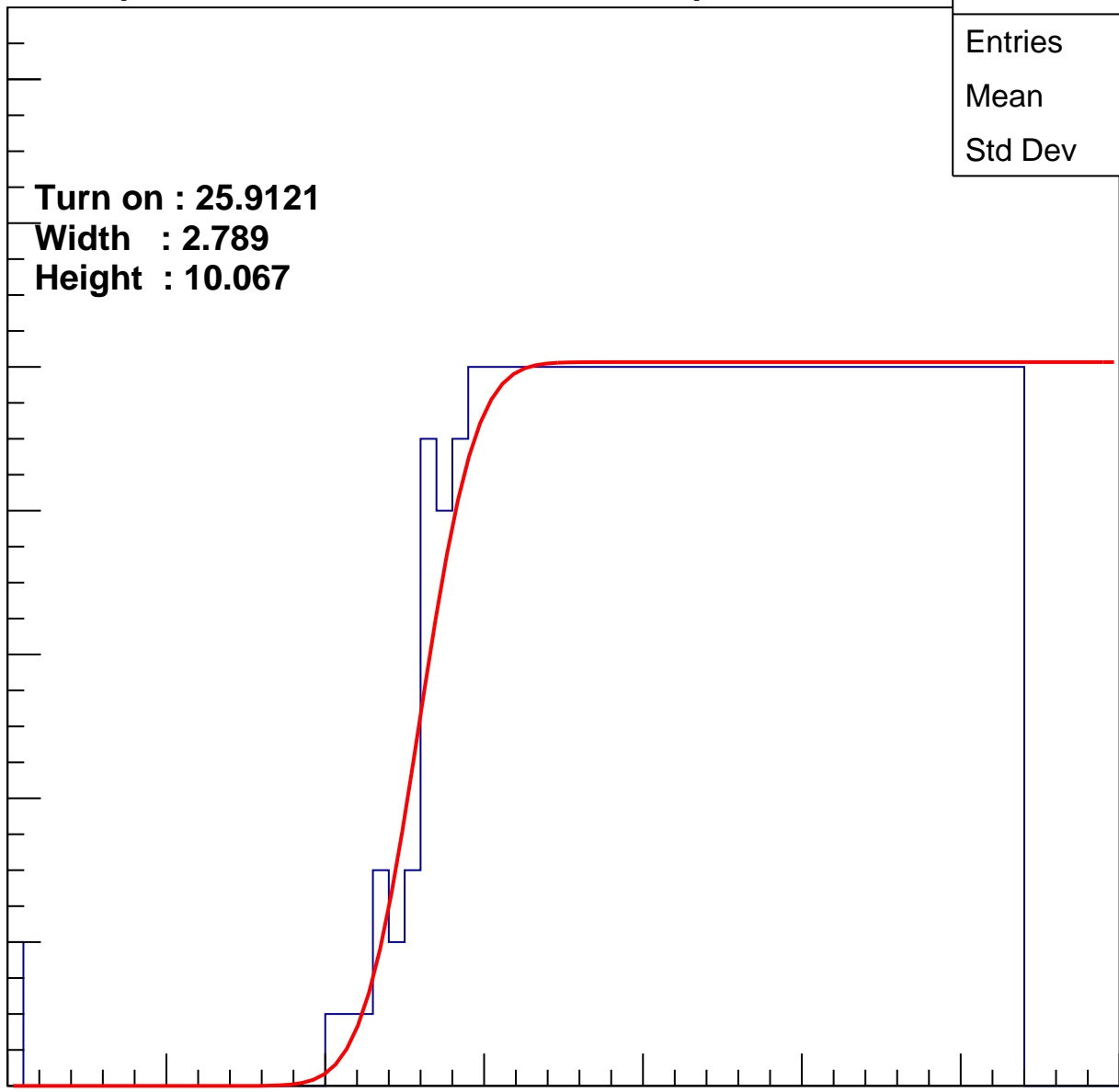
Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U11-ch105

calib\_packv5\_042523\_0143.root, FC#10, port B3

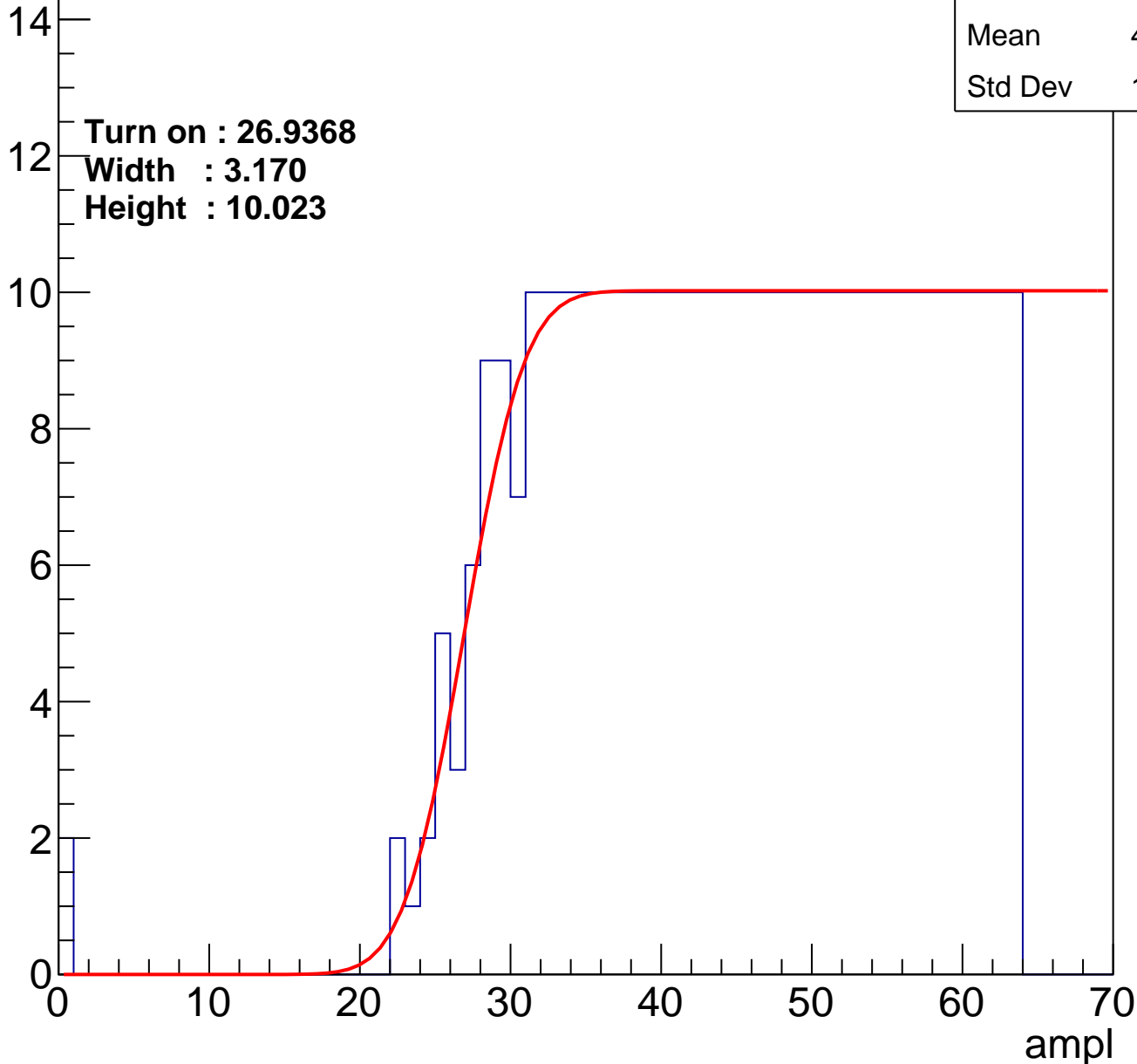
Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 26.9368

Width : 3.170

Height : 10.023

Entry



# B1L002S, U11-ch106

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.87
Std Dev	11.97

Turn on : 26.1317

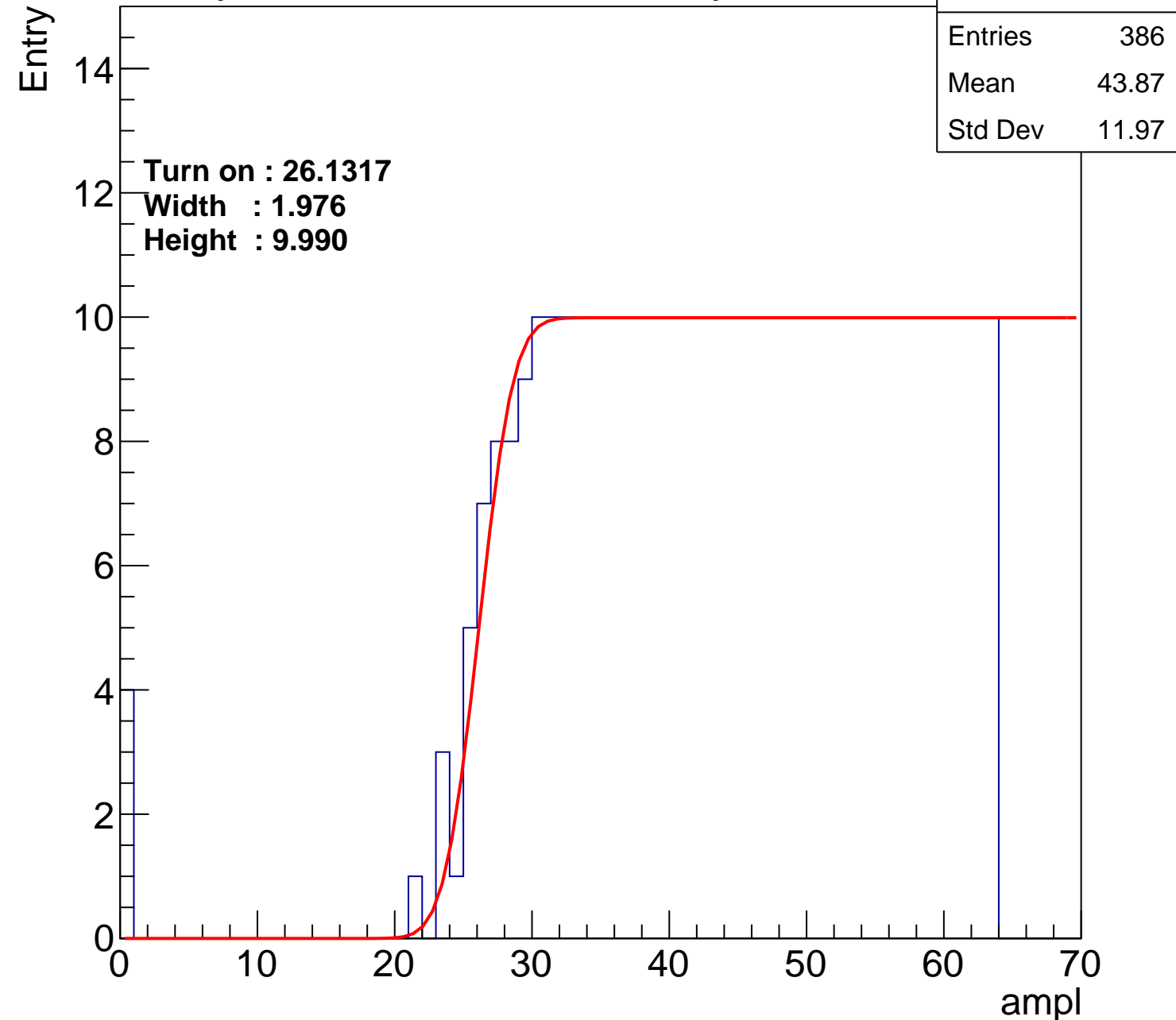
Width : 1.976

Height : 9.990

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch107

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	43.99
Std Dev	11.8

Turn on : 25.8077

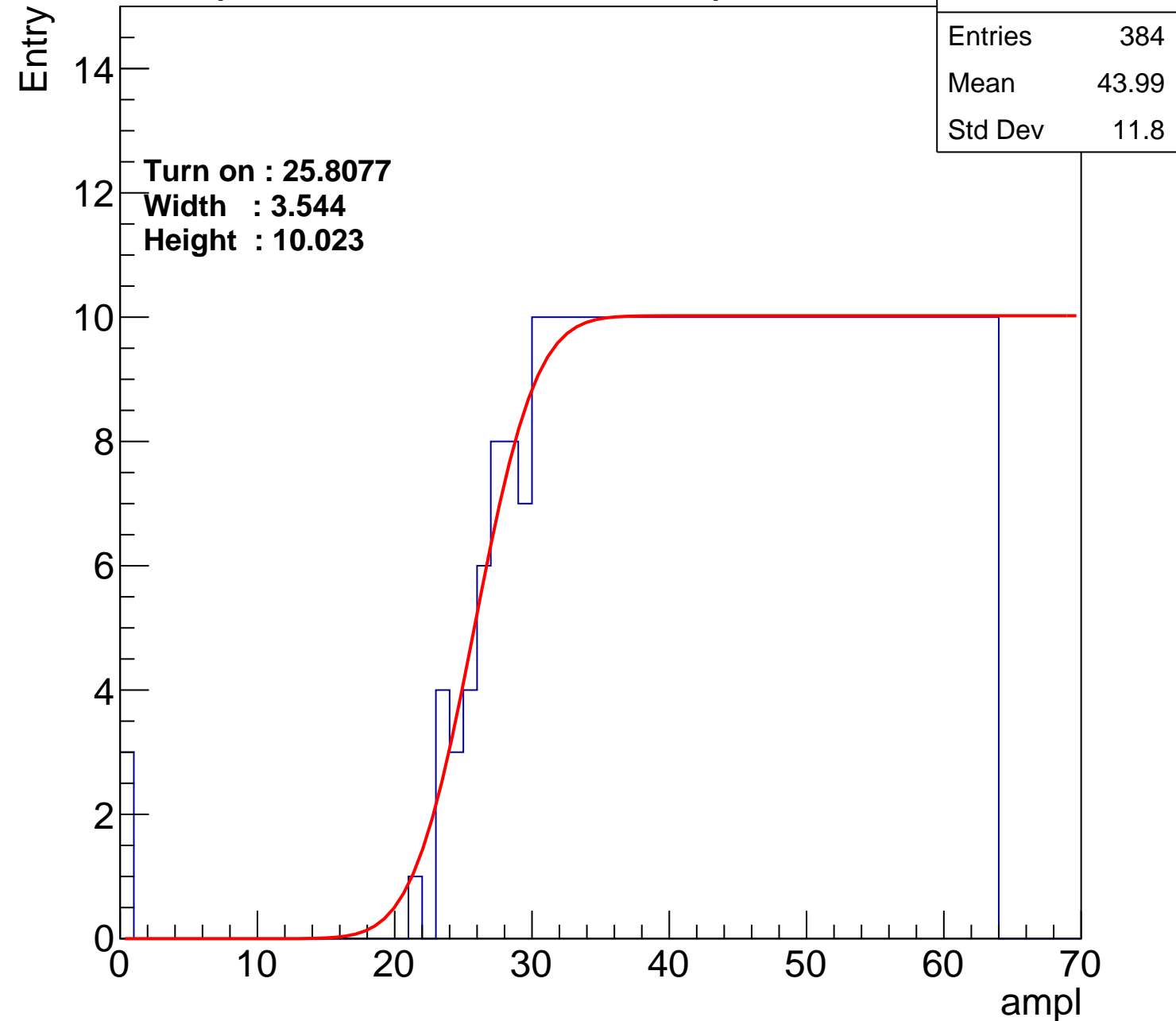
Width : 3.544

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch108

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	366
Mean	44.99
Std Dev	11.01

Turn on : 27.9980

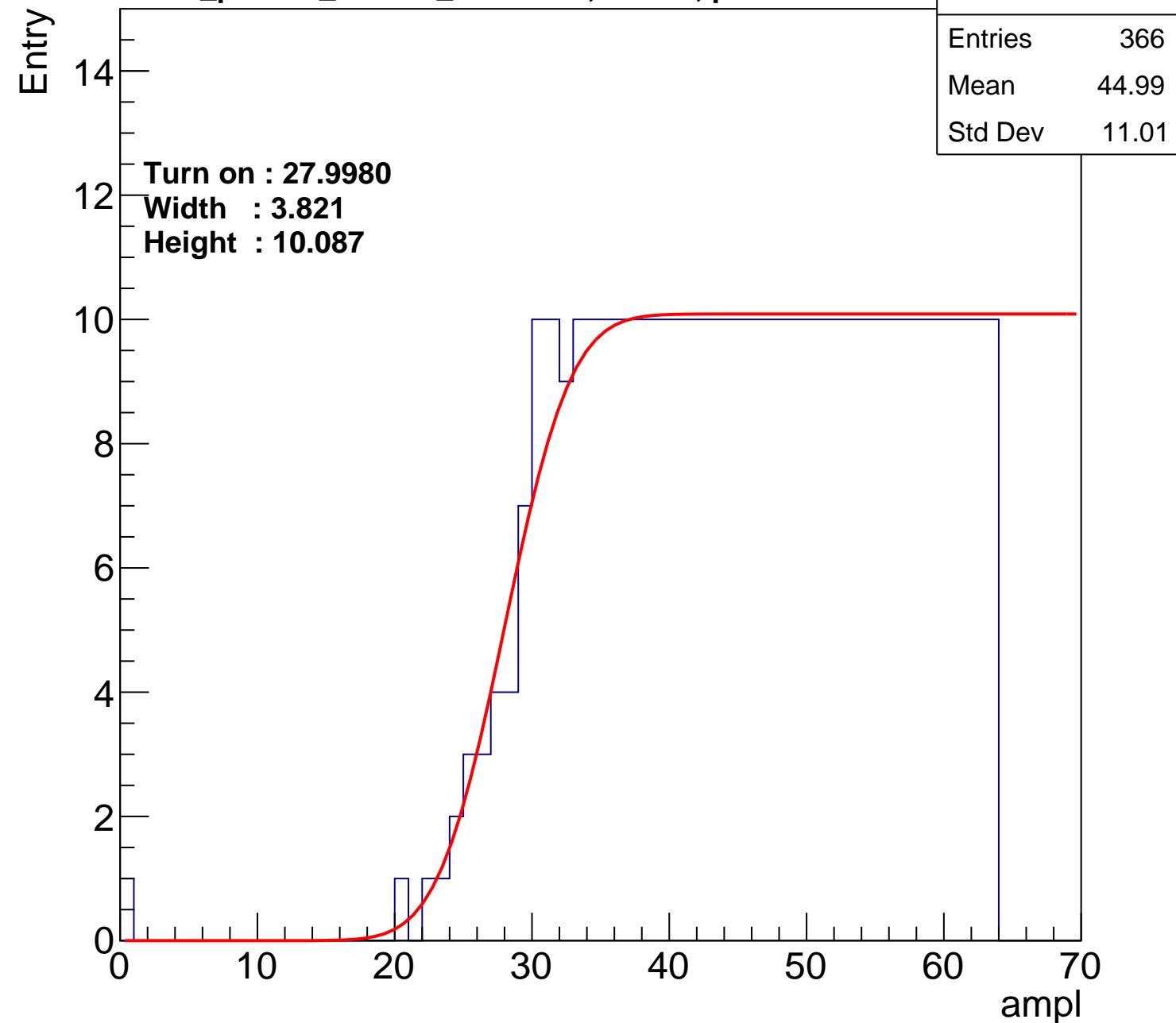
Width : 3.821

Height : 10.087

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch109

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.94
Std Dev	11.54

Turn on : 25.5195

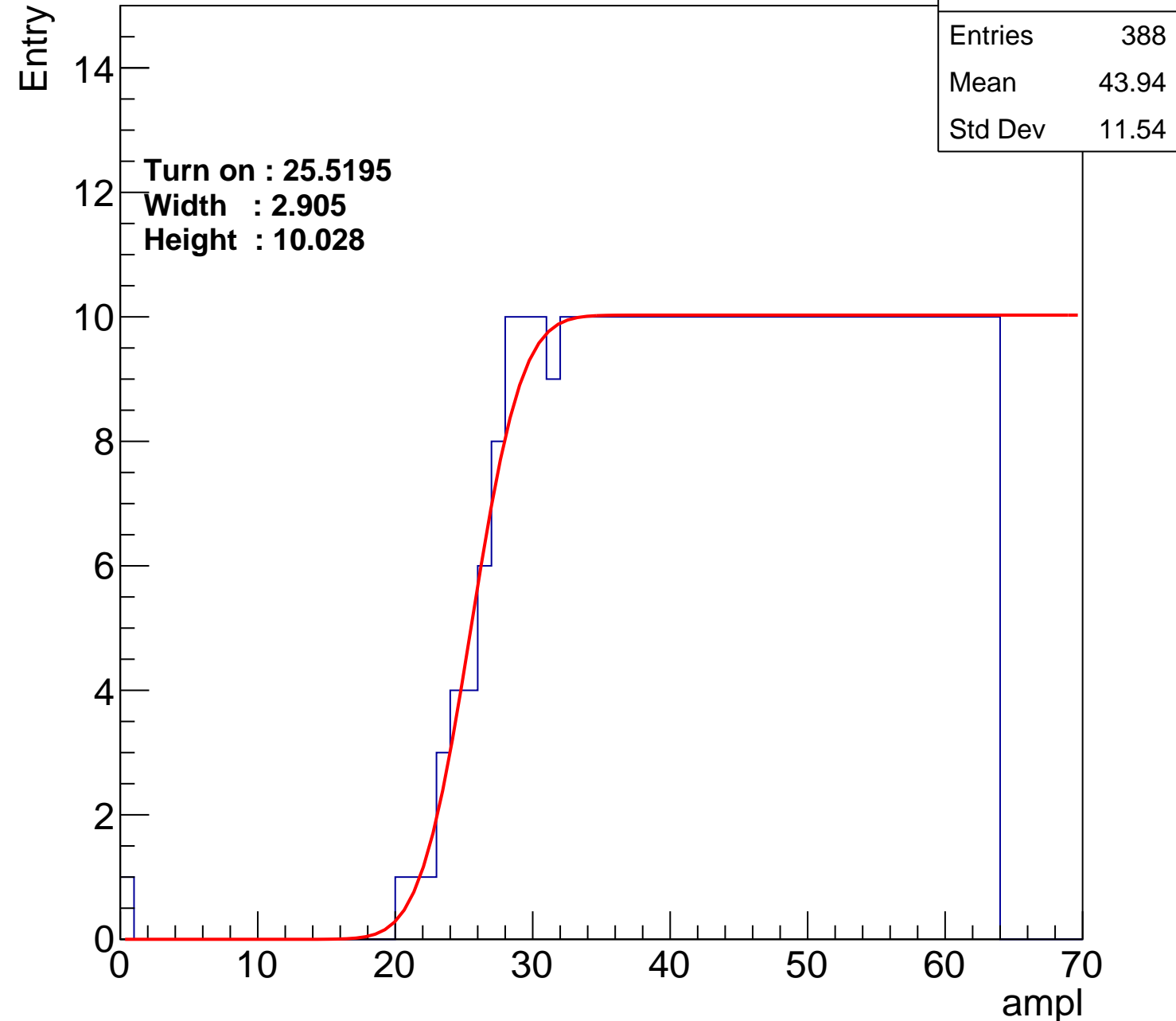
Width : 2.905

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch110

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.98
Std Dev	11.51

Turn on : 25.2902

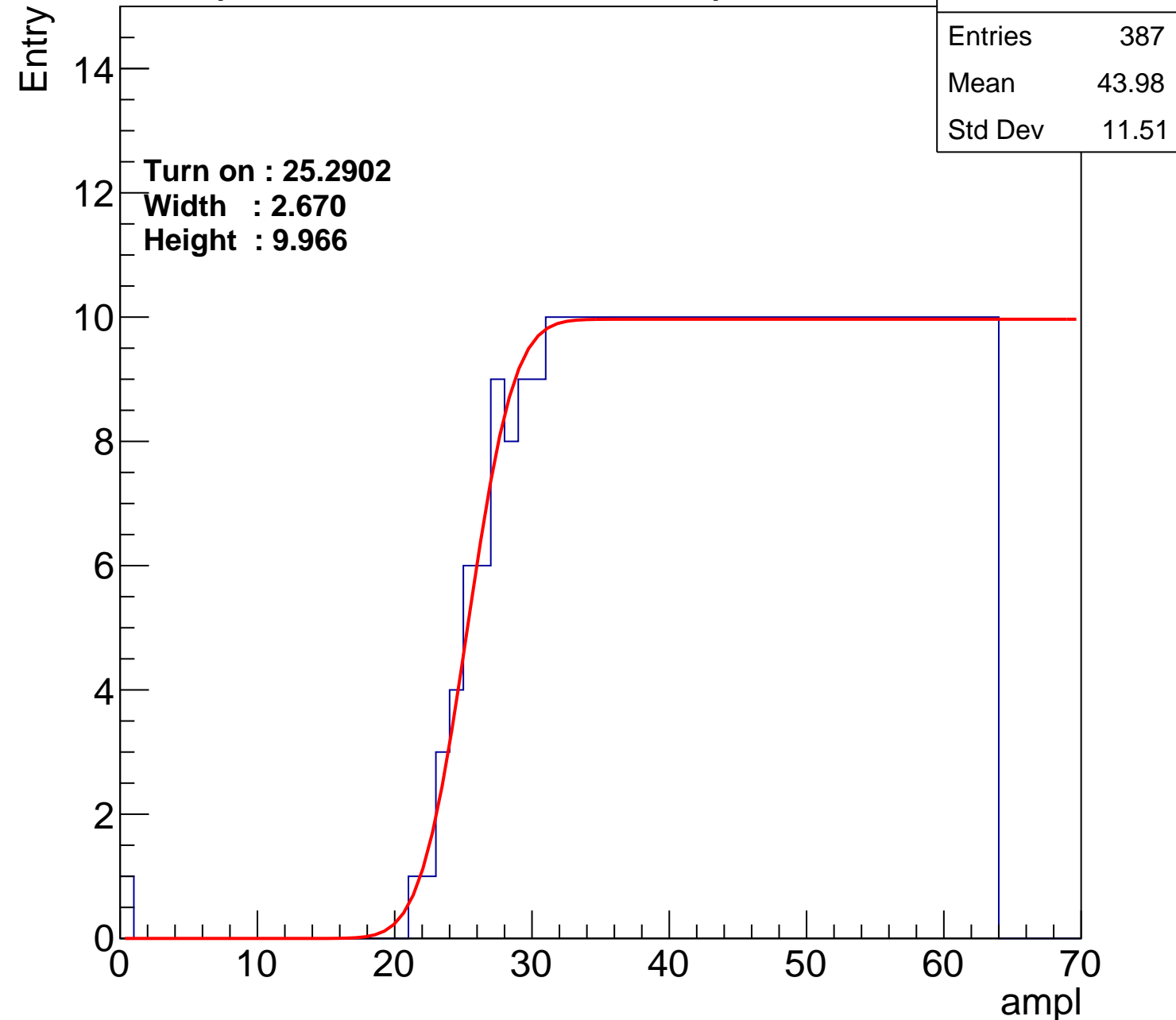
Width : 2.670

Height : 9.966

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch111

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.09
Std Dev	11.59

Turn on : 26.6324

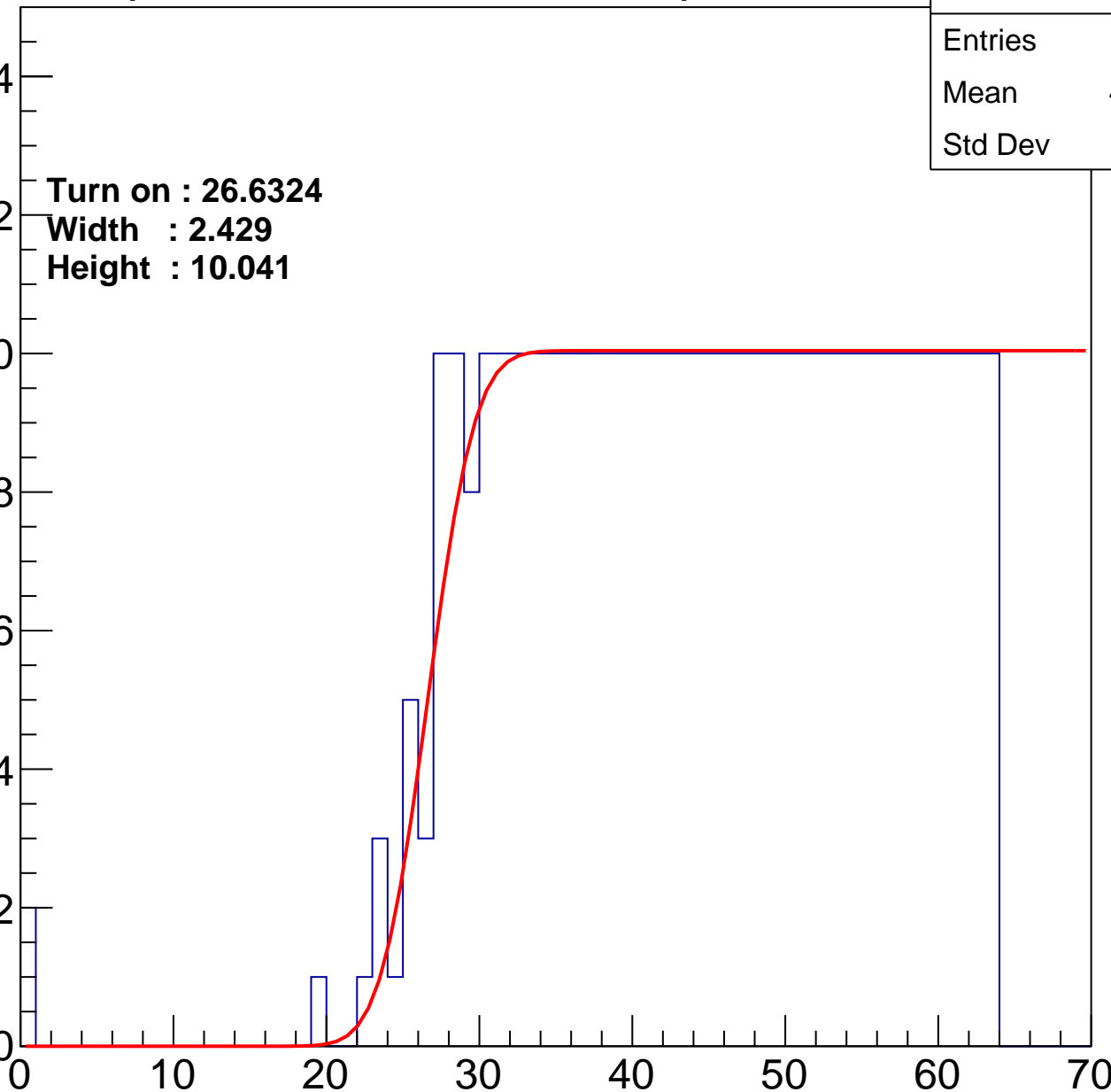
Width : 2.429

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch112

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.27
Std Dev	11.44

Turn on : 26.2619

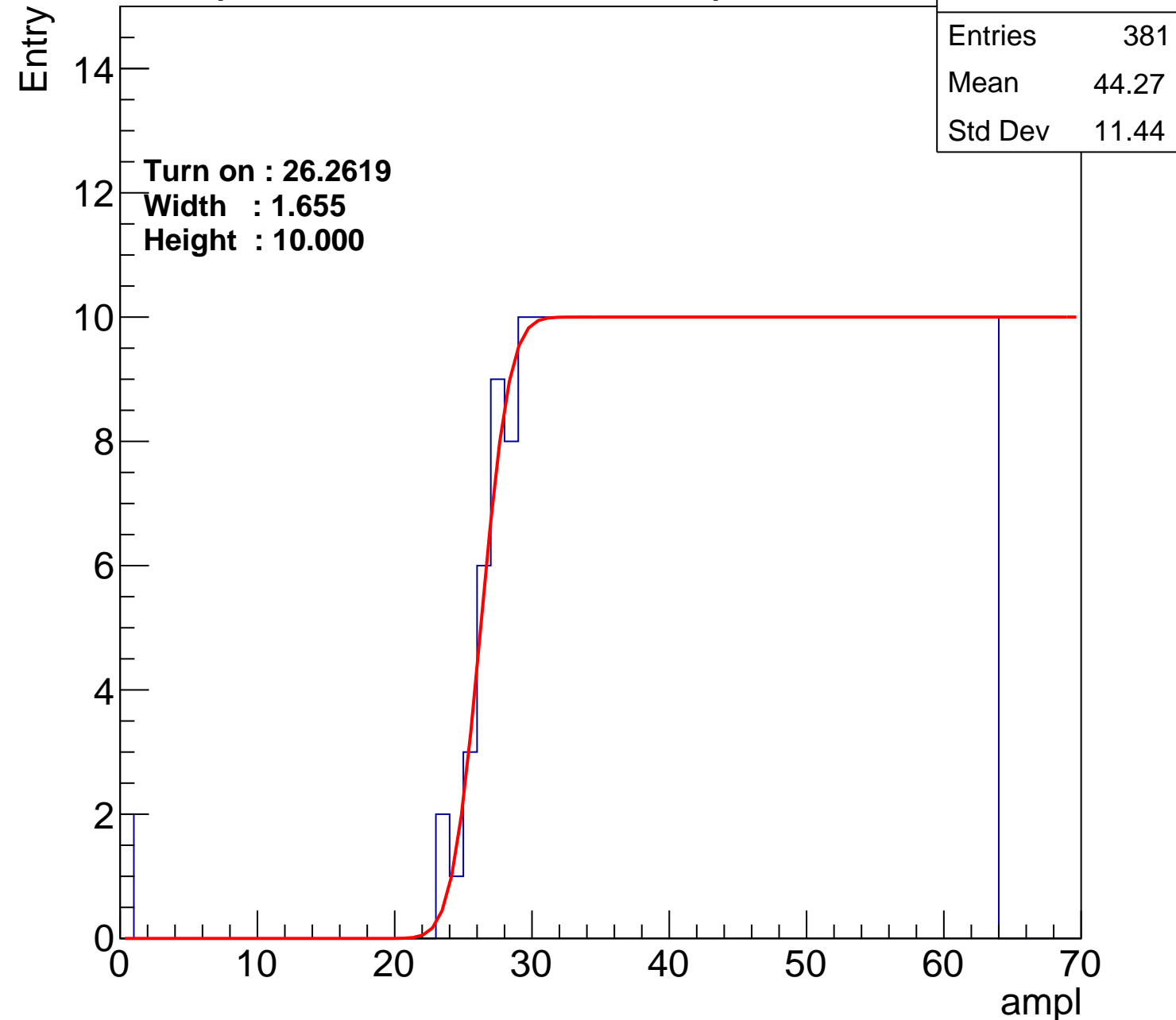
Width : 1.655

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch113

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	44.02
Std Dev	11.51

Turn on : 25.6760

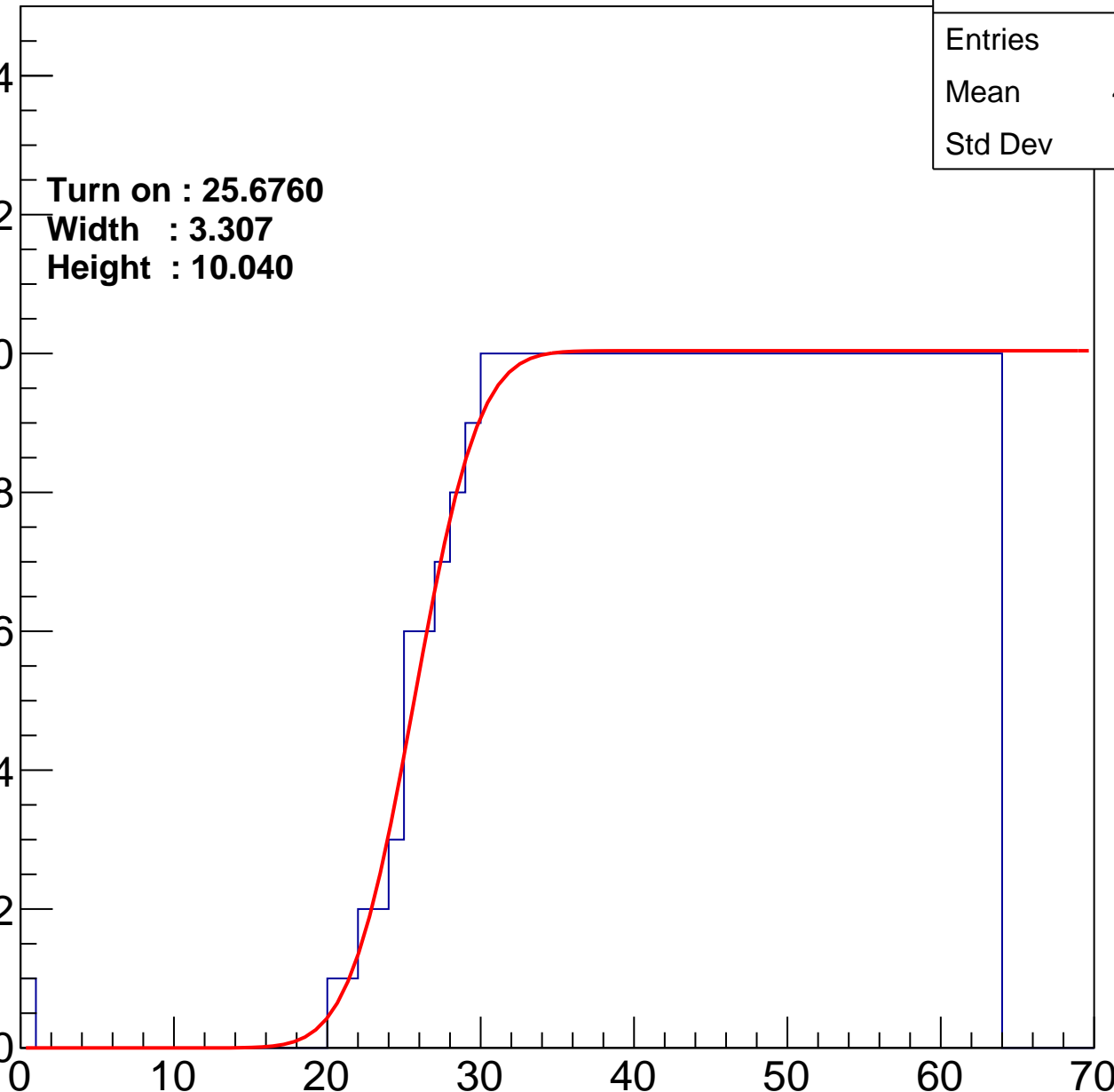
Width : 3.307

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch114

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 25.2030

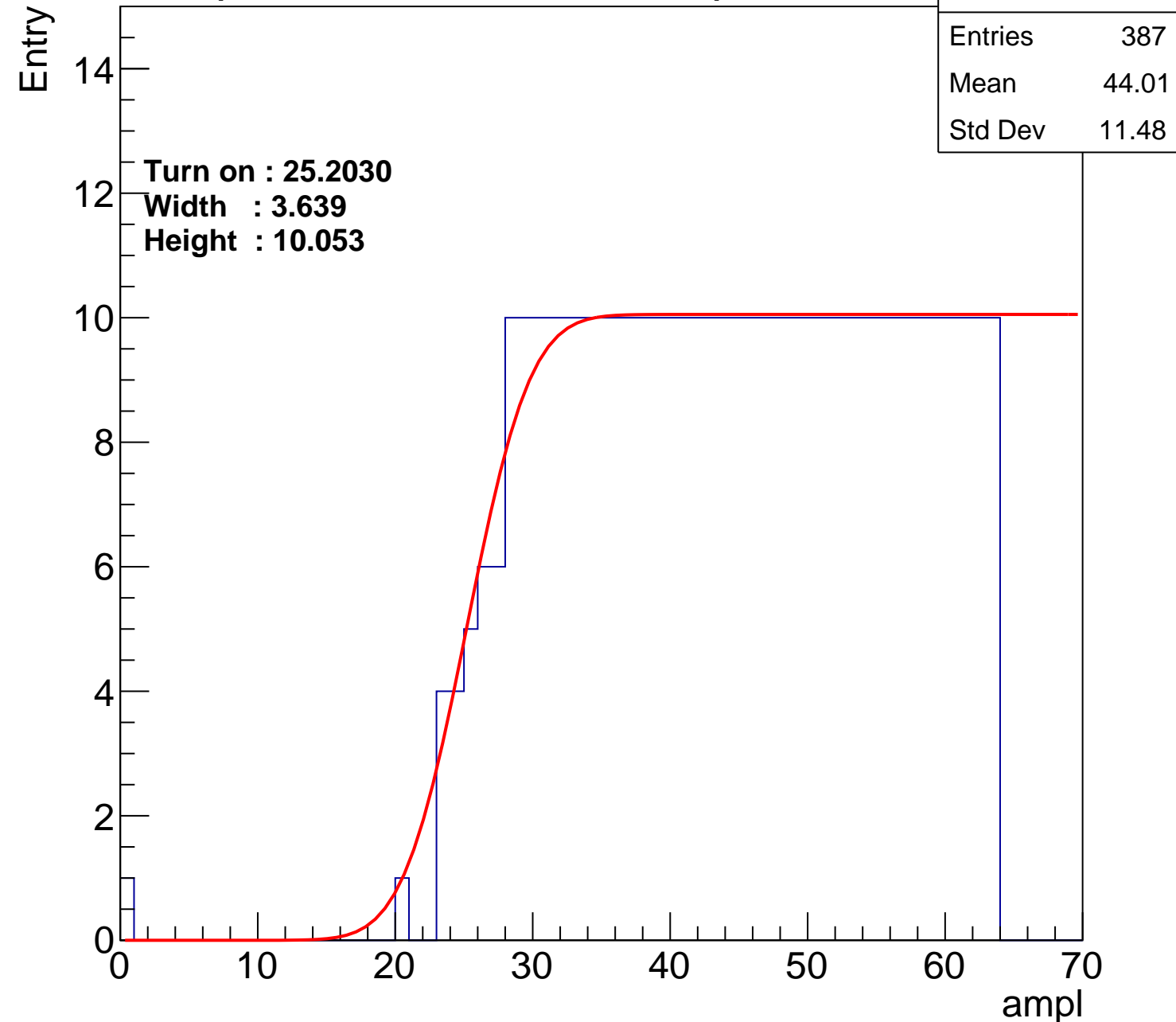
Width : 3.639

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch115

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	406
Mean	42.92
Std Dev	12.35

Turn on : 23.9460

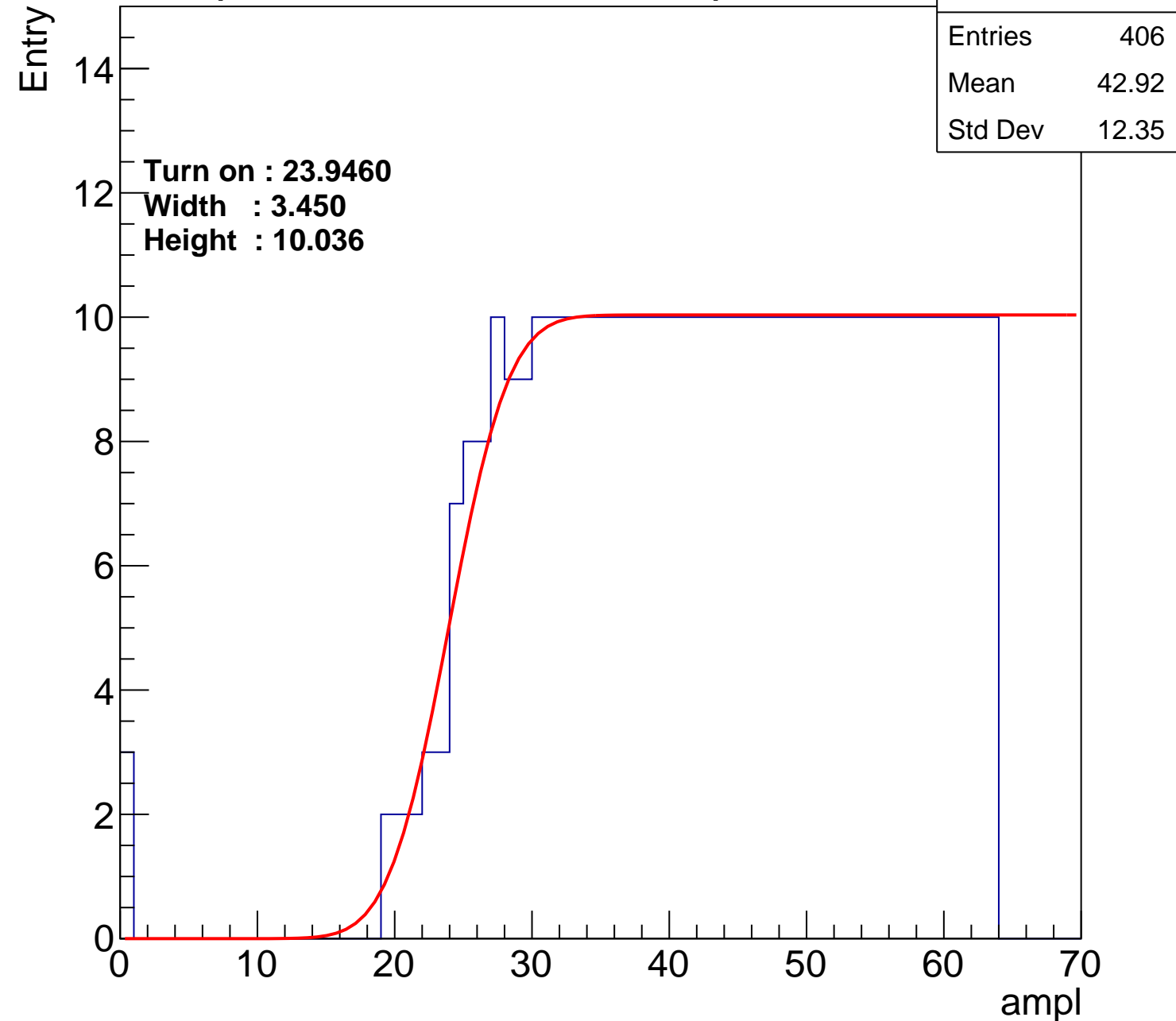
Width : 3.450

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch116

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.82
Std Dev	11.74

Turn on : 25.4798

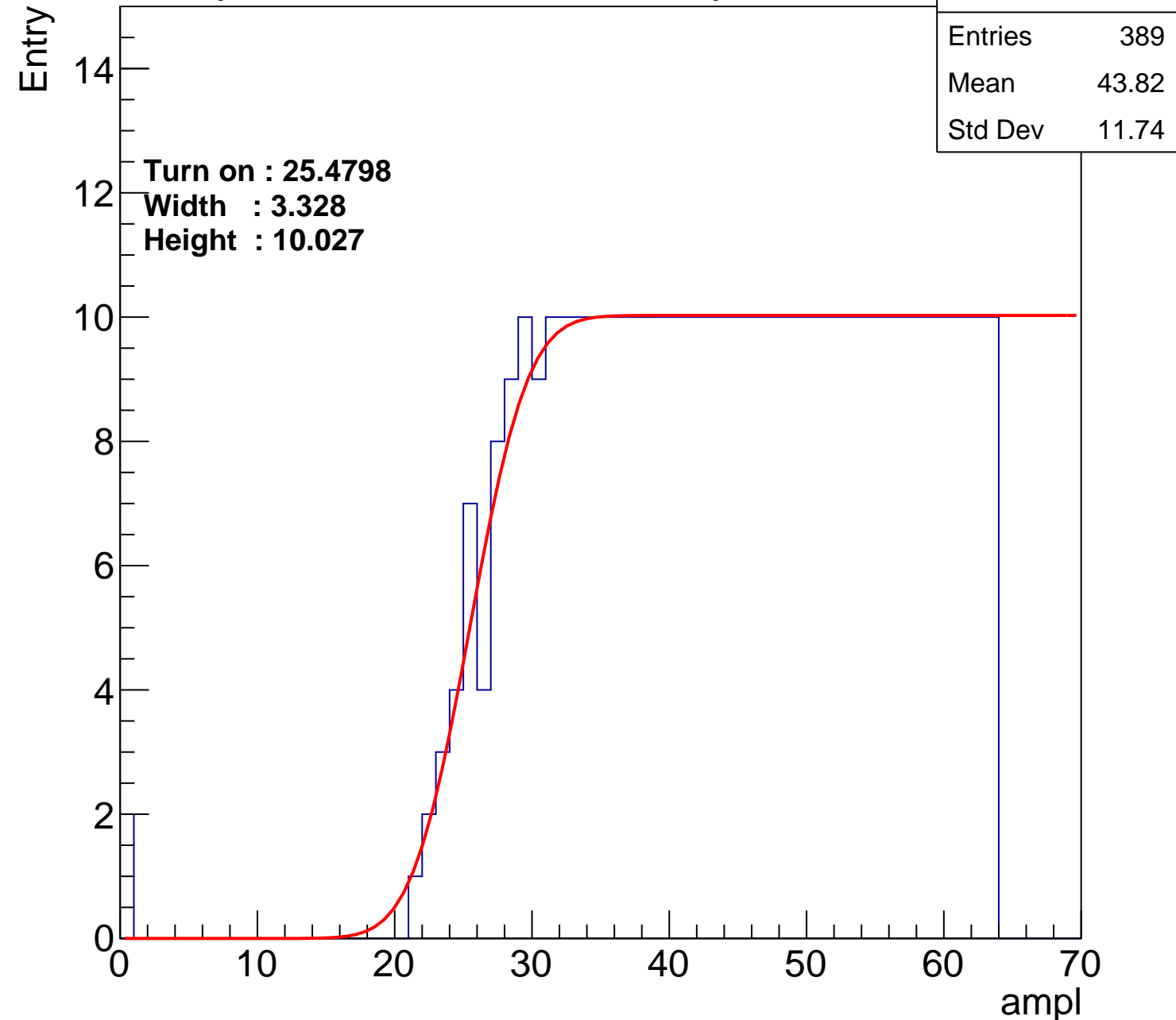
Width : 3.328

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch117

calib\_packv5\_042523\_0143.root, FC#10, port B3

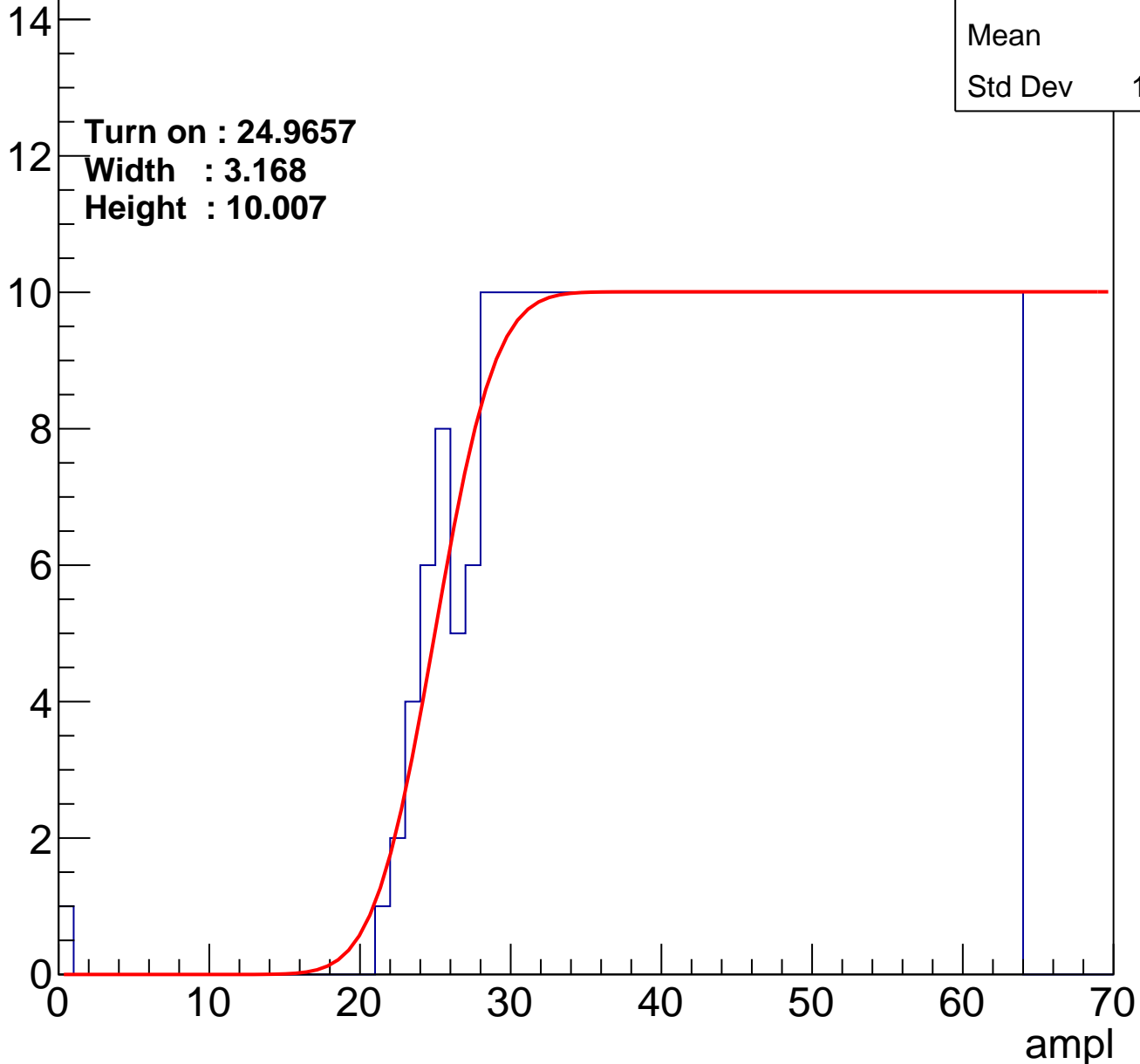
Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 24.9657

Width : 3.168

Height : 10.007

Entry



# B1L002S, U11-ch118

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 25.5687

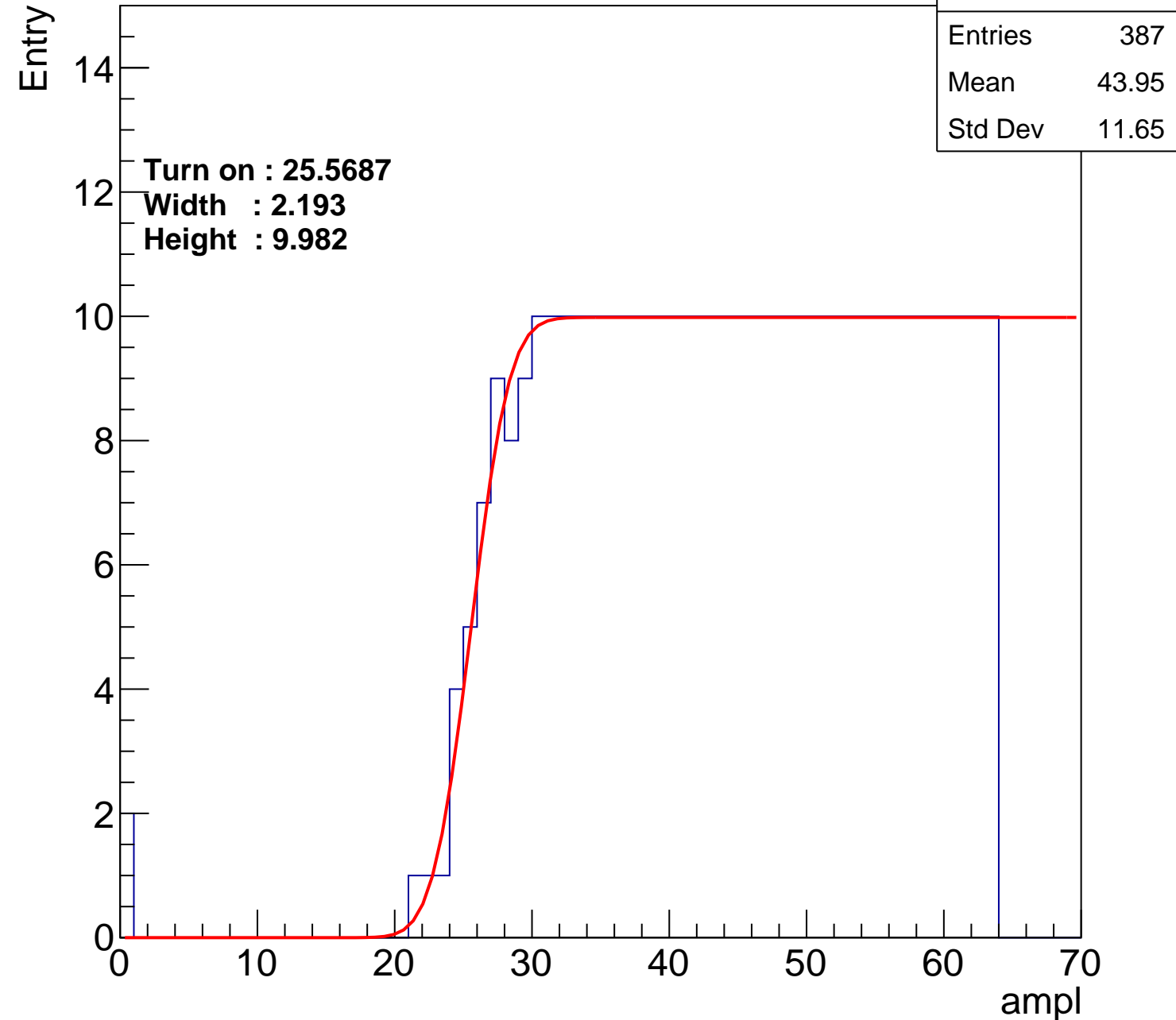
Width : 2.193

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch119

calib\_packv5\_042523\_0143.root, FC#10, port B3

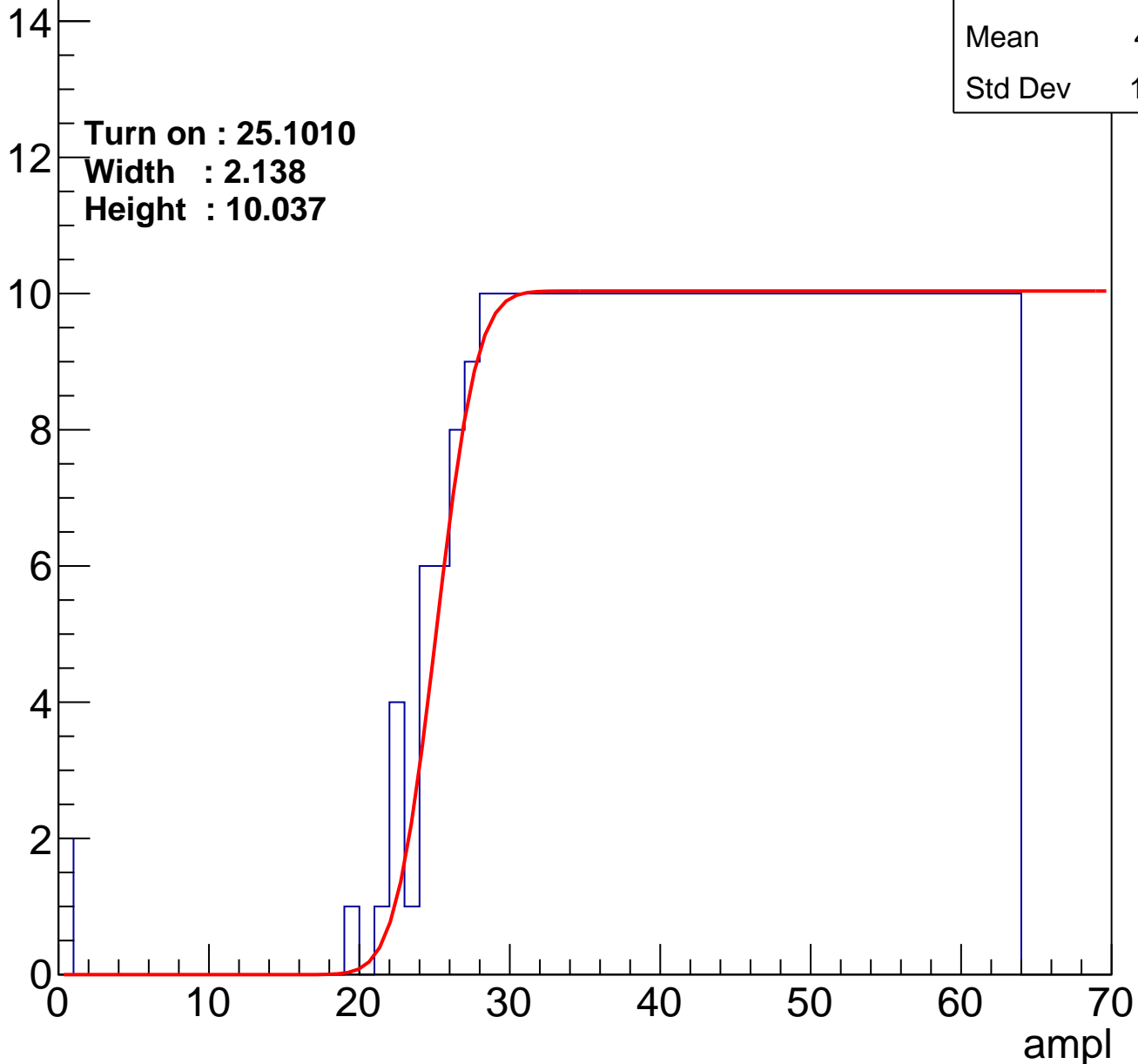
Entries	398
Mean	43.41
Std Dev	11.93

Turn on : 25.1010

Width : 2.138

Height : 10.037

Entry



# B1L002S, U11-ch120

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.78
Std Dev	11.87

Turn on : 25.5599

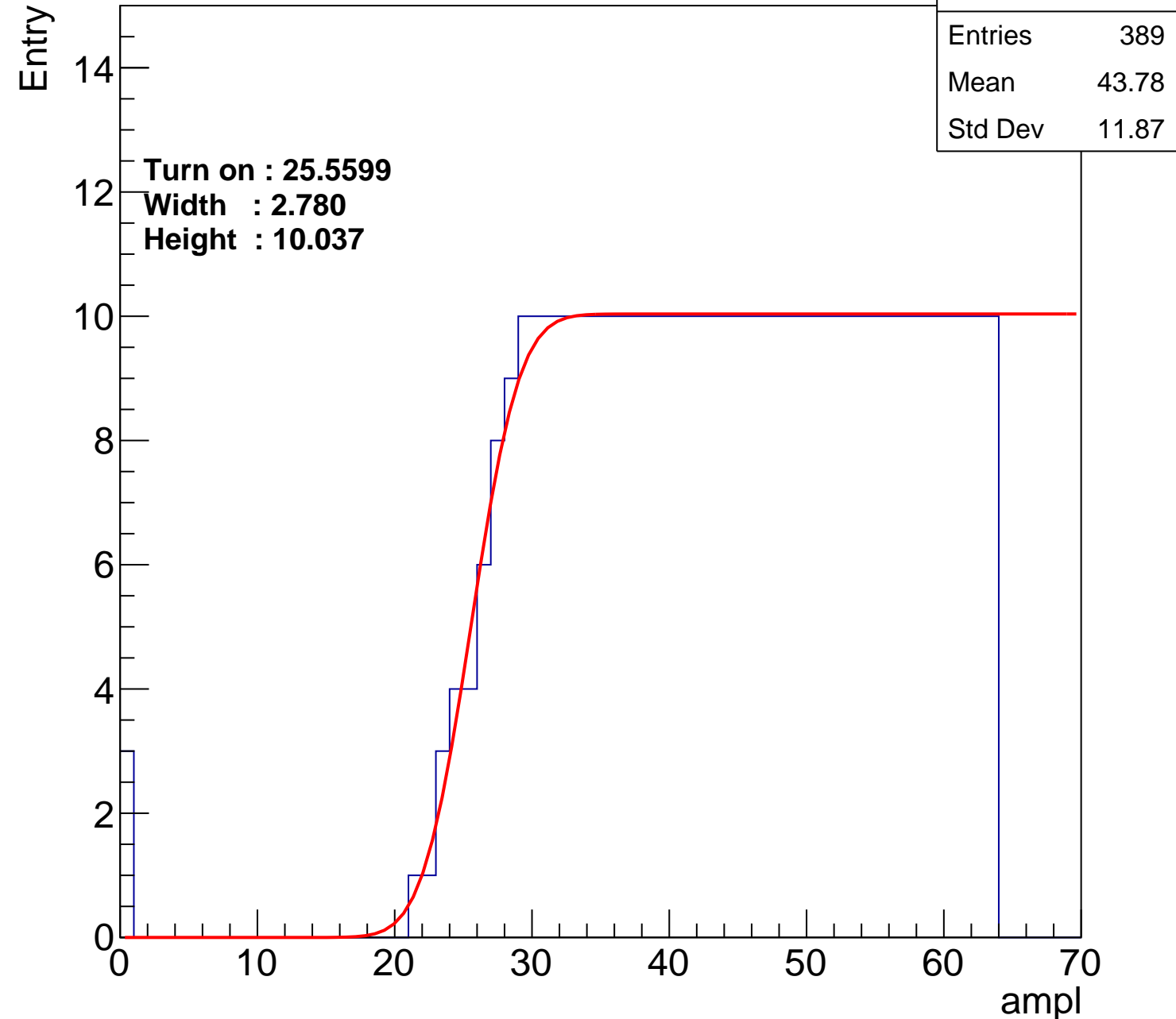
Width : 2.780

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch121

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.23
Std Dev	11.69

Turn on : 26.3124

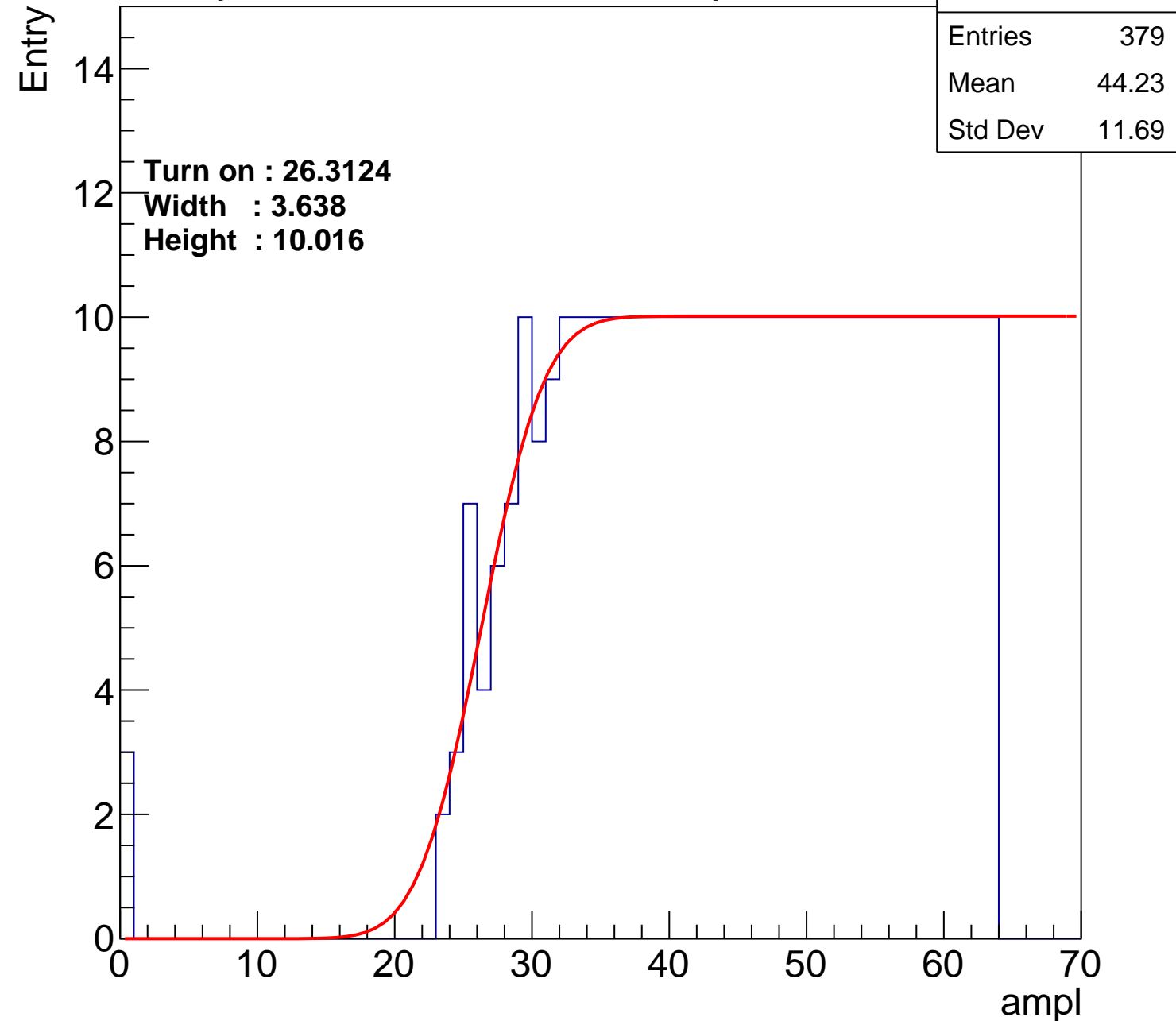
Width : 3.638

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch122

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 26.3577

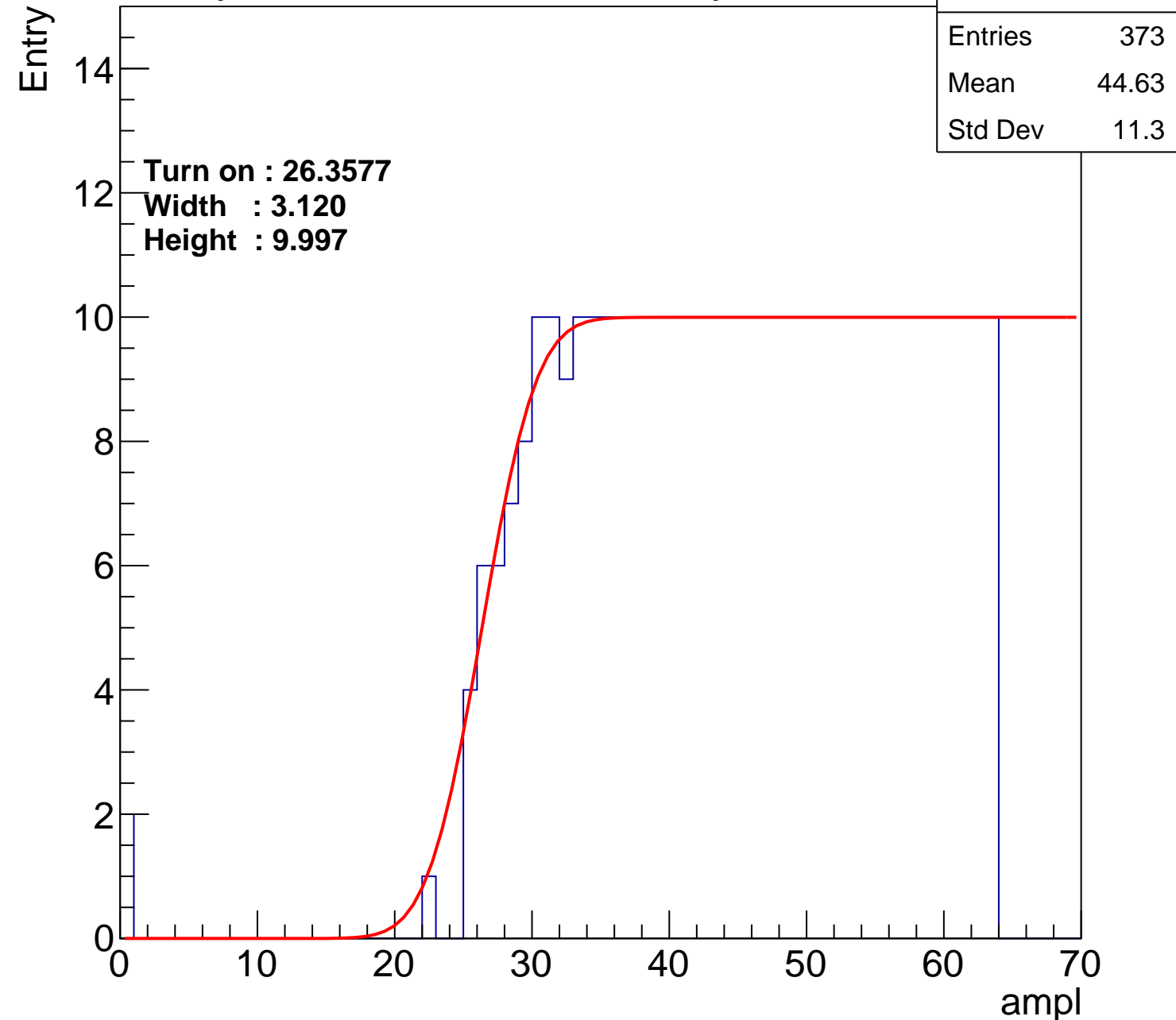
Width : 3.120

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch123

calib\_packv5\_042523\_0143.root, FC#10, port B3

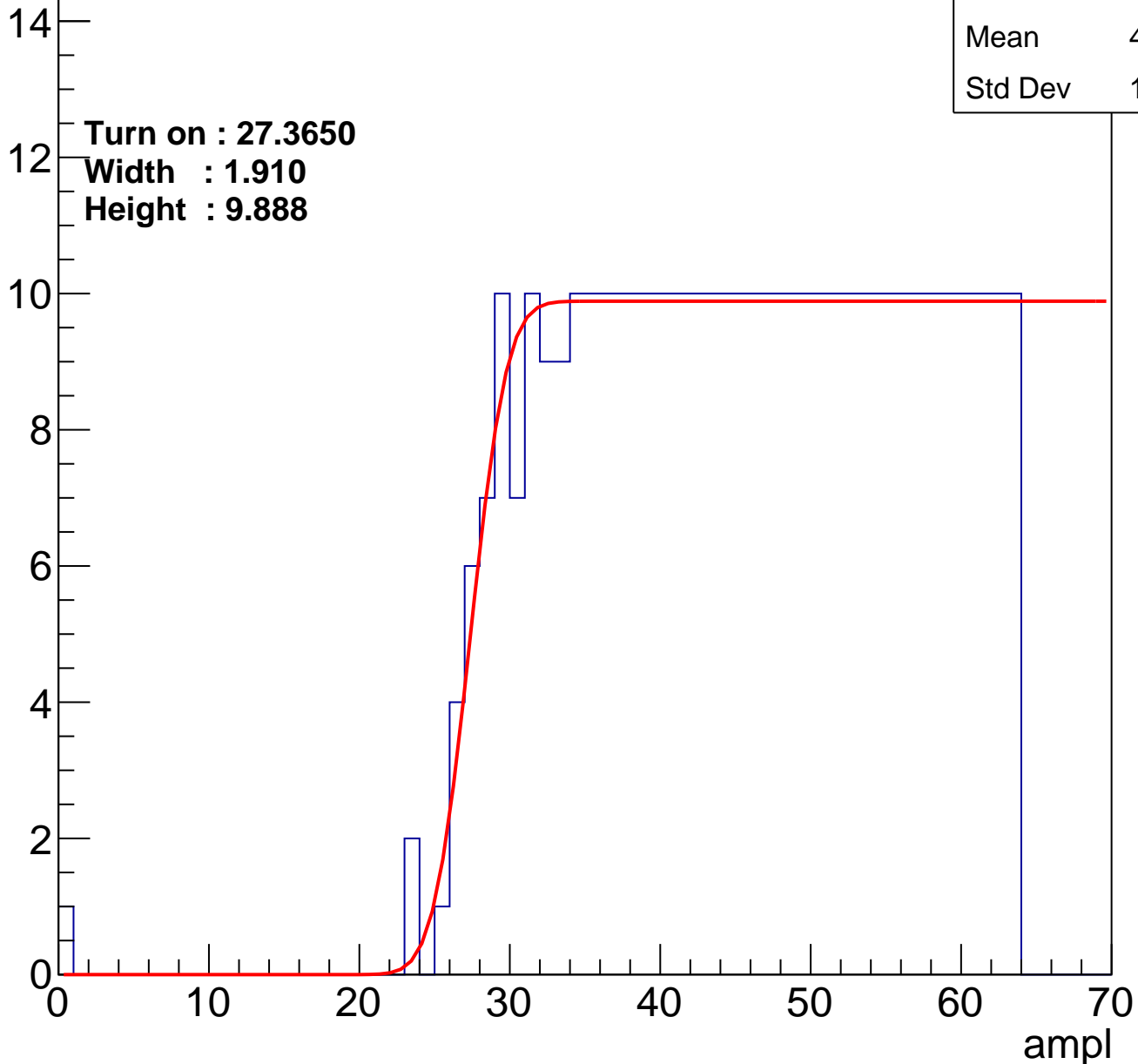
Entries	366
Mean	45.02
Std Dev	10.95

Turn on : 27.3650

Width : 1.910

Height : 9.888

Entry



# B1L002S, U11-ch124

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	353
Mean	45.66
Std Dev	10.62

Turn on : 28.9812

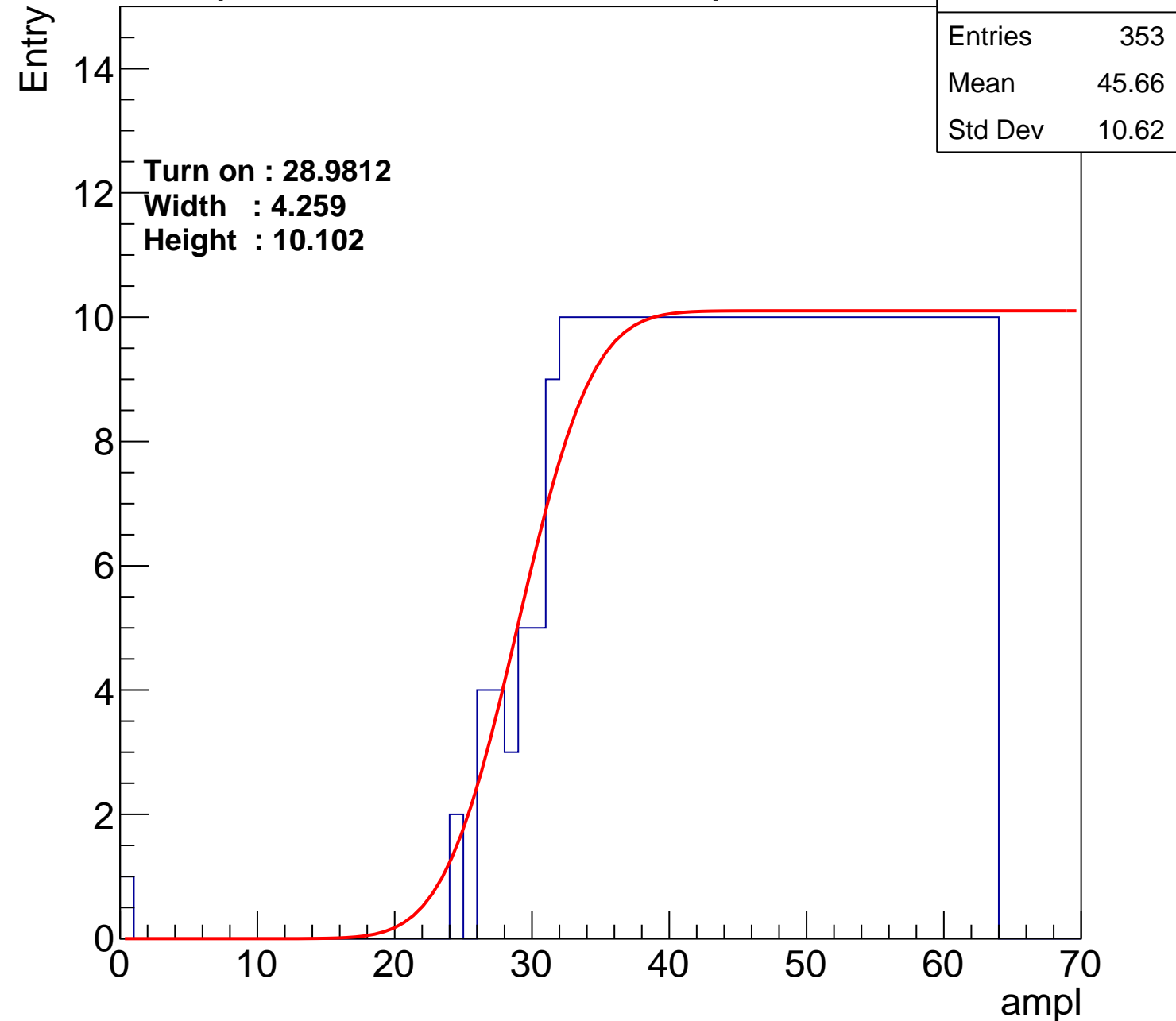
Width : 4.259

Height : 10.102

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch125

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.14
Std Dev	11.67

Turn on : 26.2124

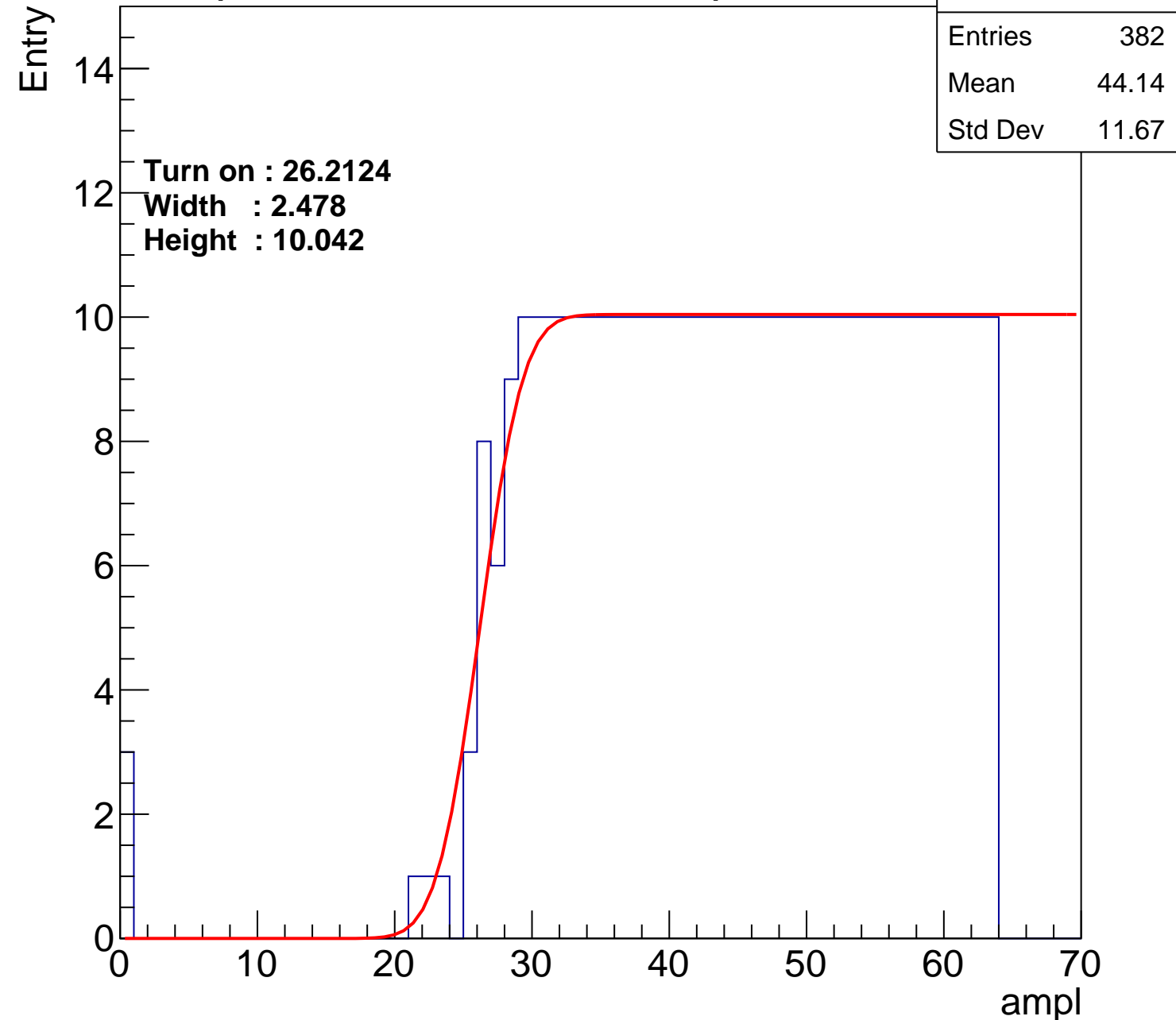
Width : 2.478

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U11-ch126

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	369
Mean	44.79
Std Dev	11.26

Turn on : 27.1078

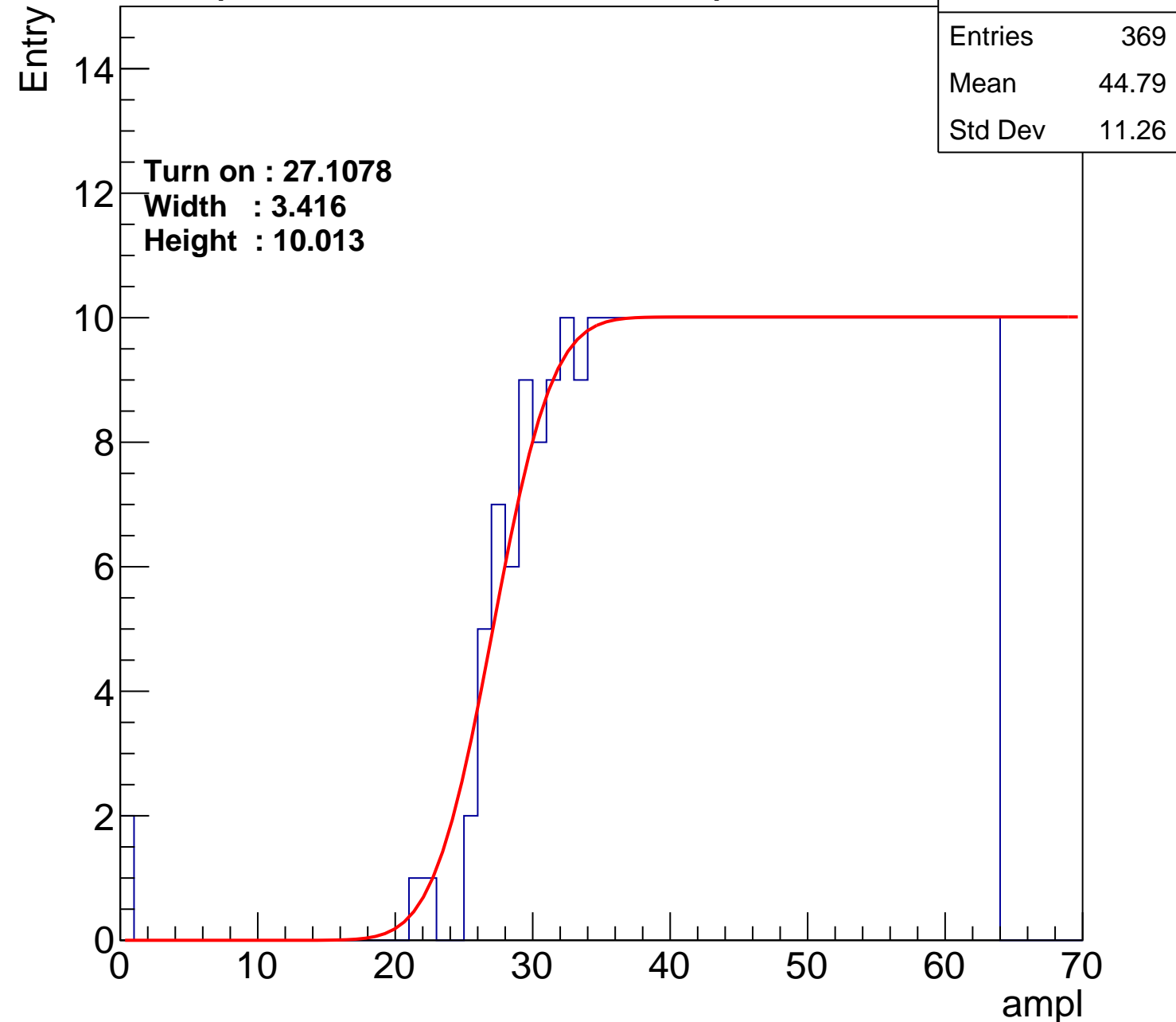
Width : 3.416

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U11-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

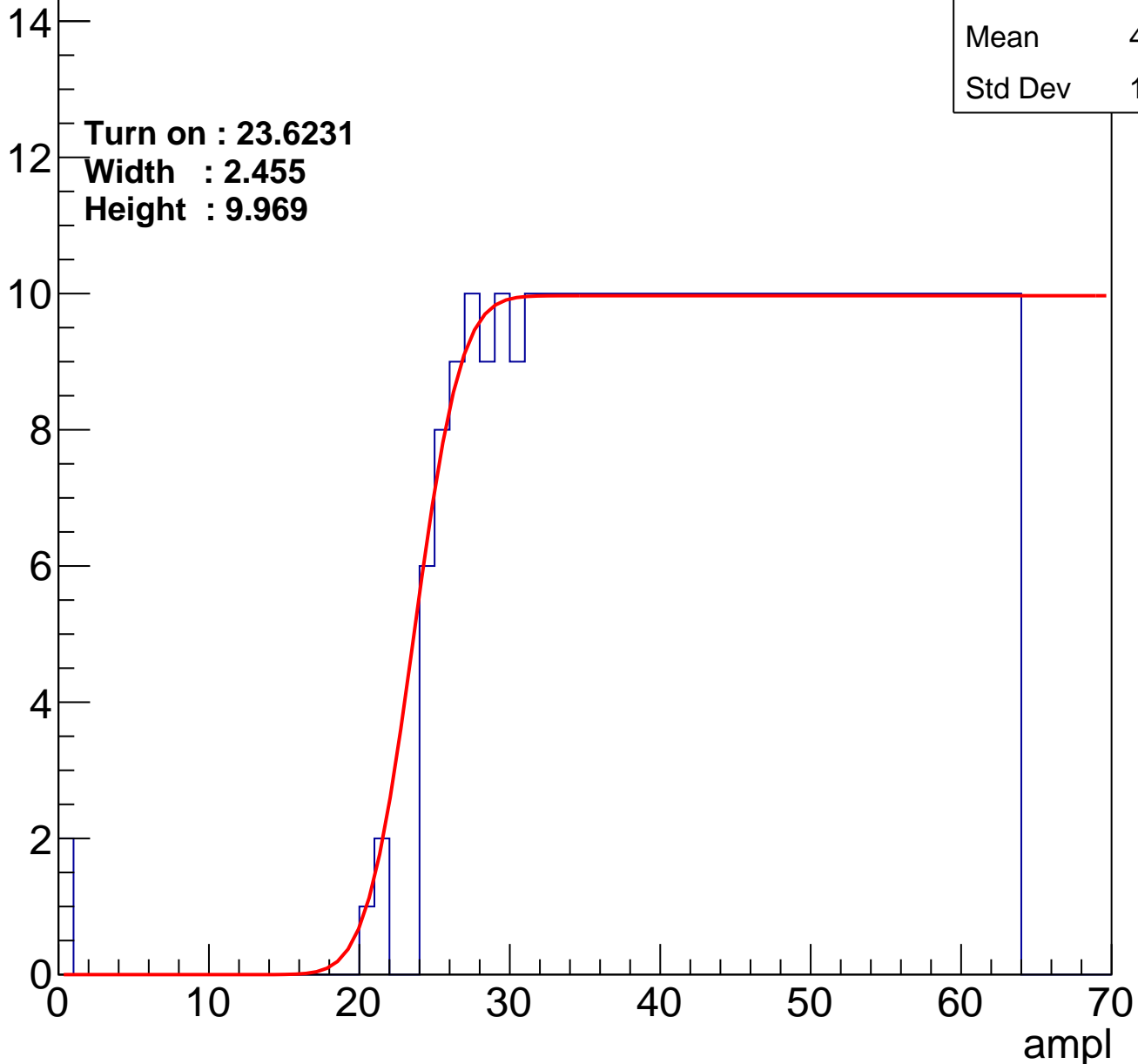
Entries	396
Mean	43.52
Std Dev	11.86

Turn on : 23.6231

Width : 2.455

Height : 9.969

Entry



# B1L002S, U11-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.52
Std Dev	11.86

Turn on : 23.6231

Width : 2.455

Height : 9.969

Entry

