

B1L104S, U6-ch0

calib_packv5_033123_0516.root, FC#4, port A1

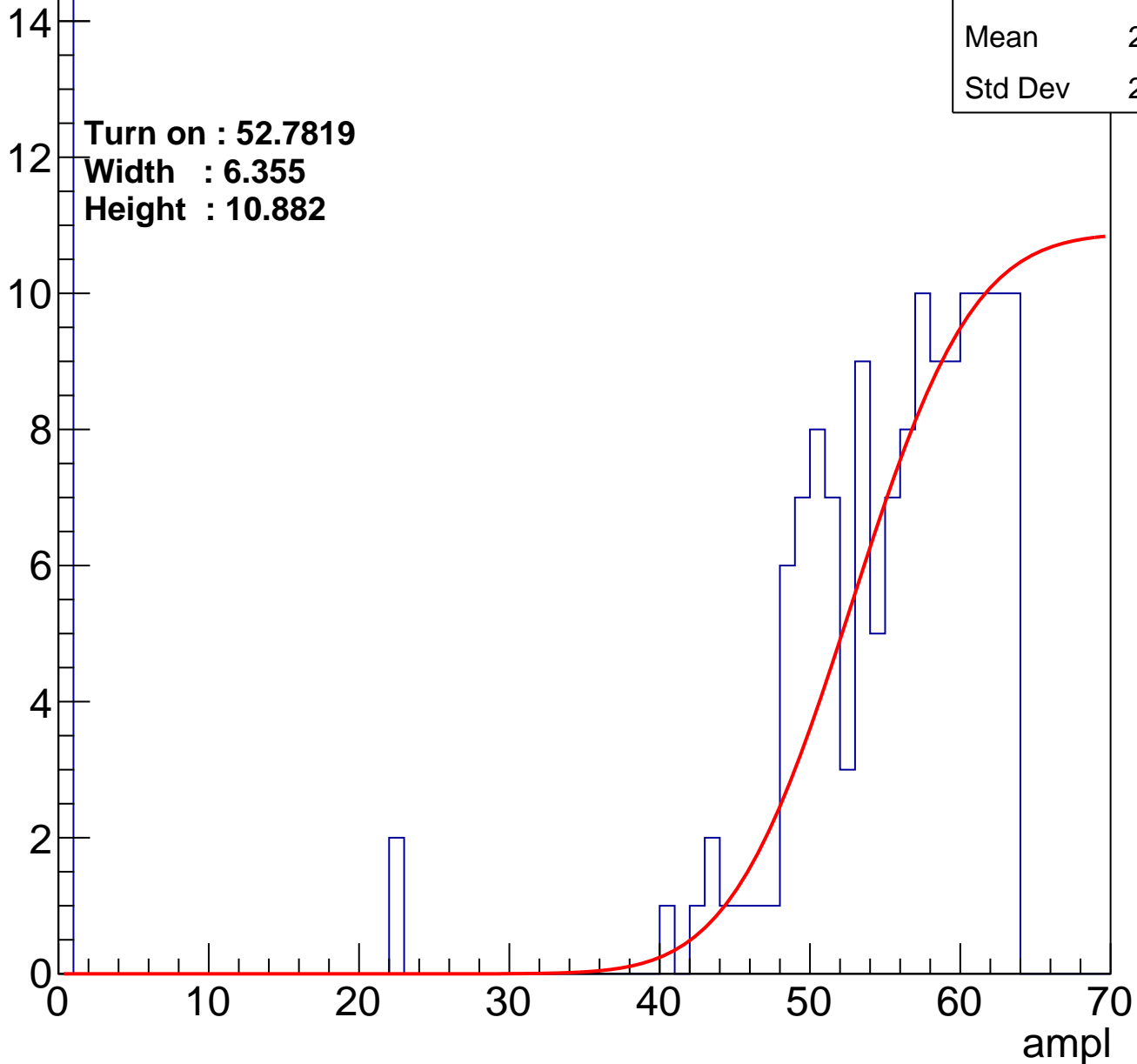
Entry

| | |
|---------|-------|
| Entries | 285 |
| Mean | 26.67 |
| Std Dev | 27.92 |

Turn on : 52.7819

Width : 6.355

Height : 10.882



B1L104S, U6-ch1

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 30.36 |
| Std Dev | 28.99 |

Turn on : 54.5135

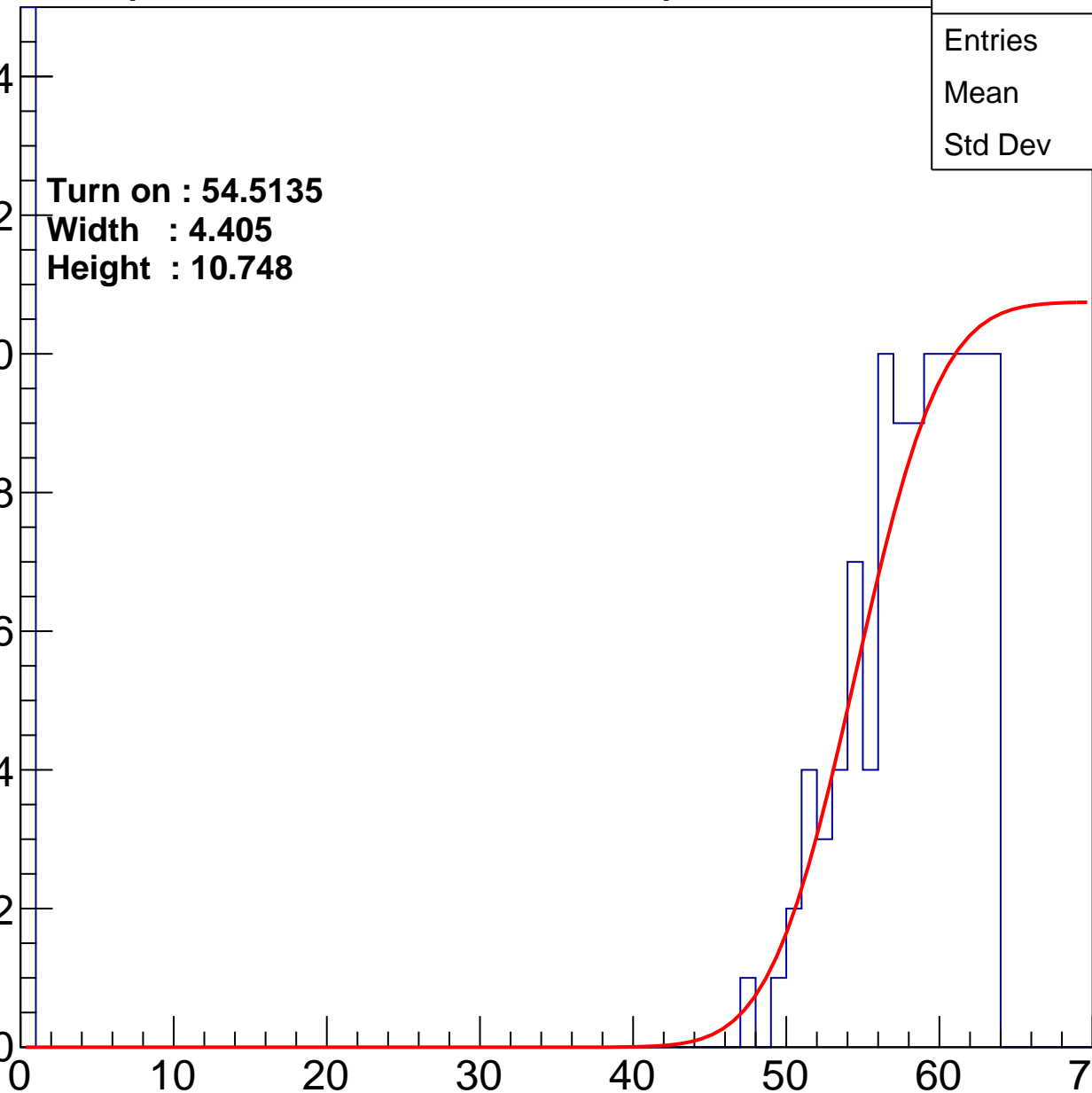
Width : 4.405

Height : 10.748

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch2

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 186 |
| Mean | 31.96 |
| Std Dev | 28.84 |

Turn on : 54.3229

Width : 5.598

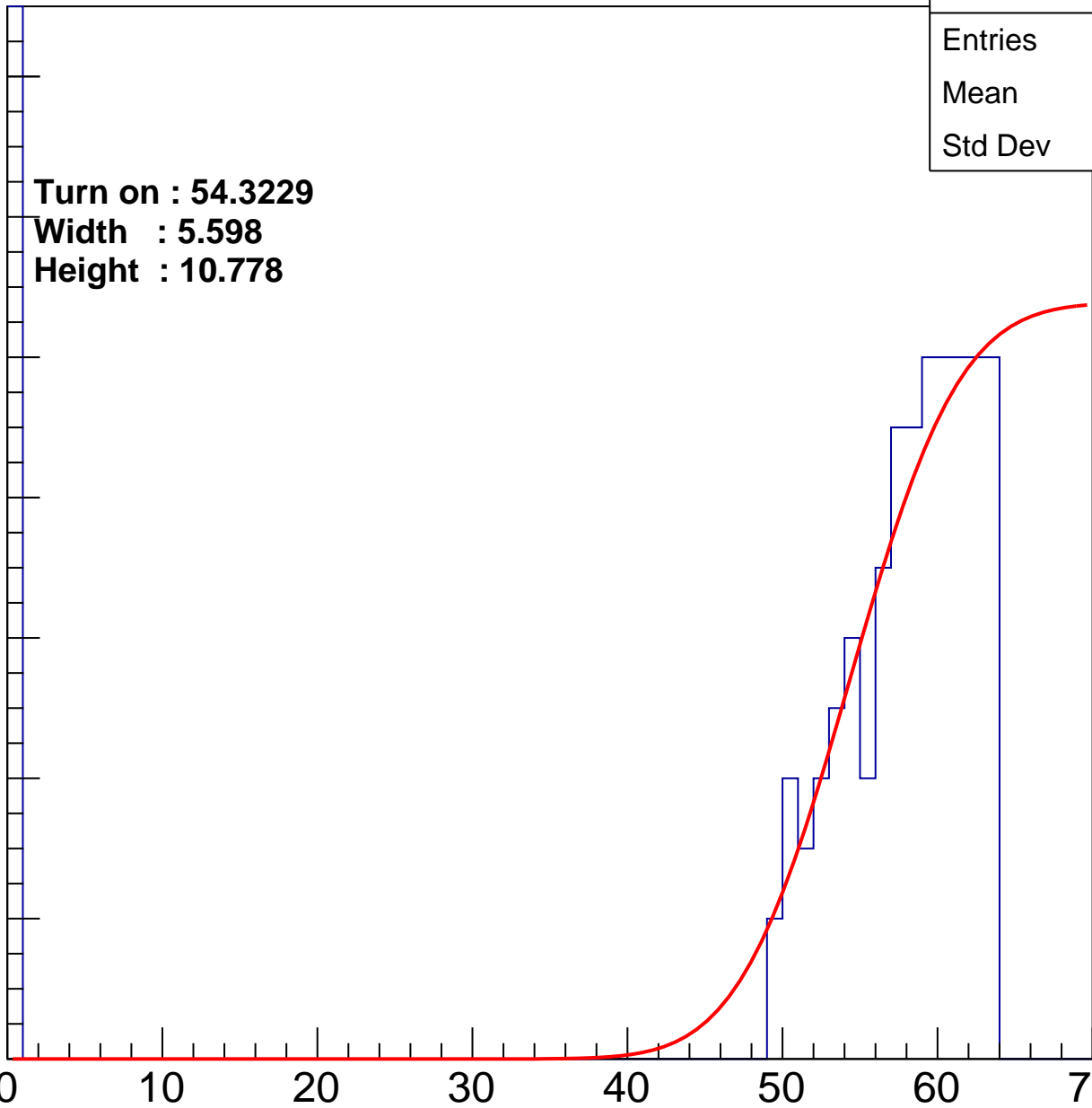
Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch3

calib_packv5_033123_0516.root, FC#4, port A1

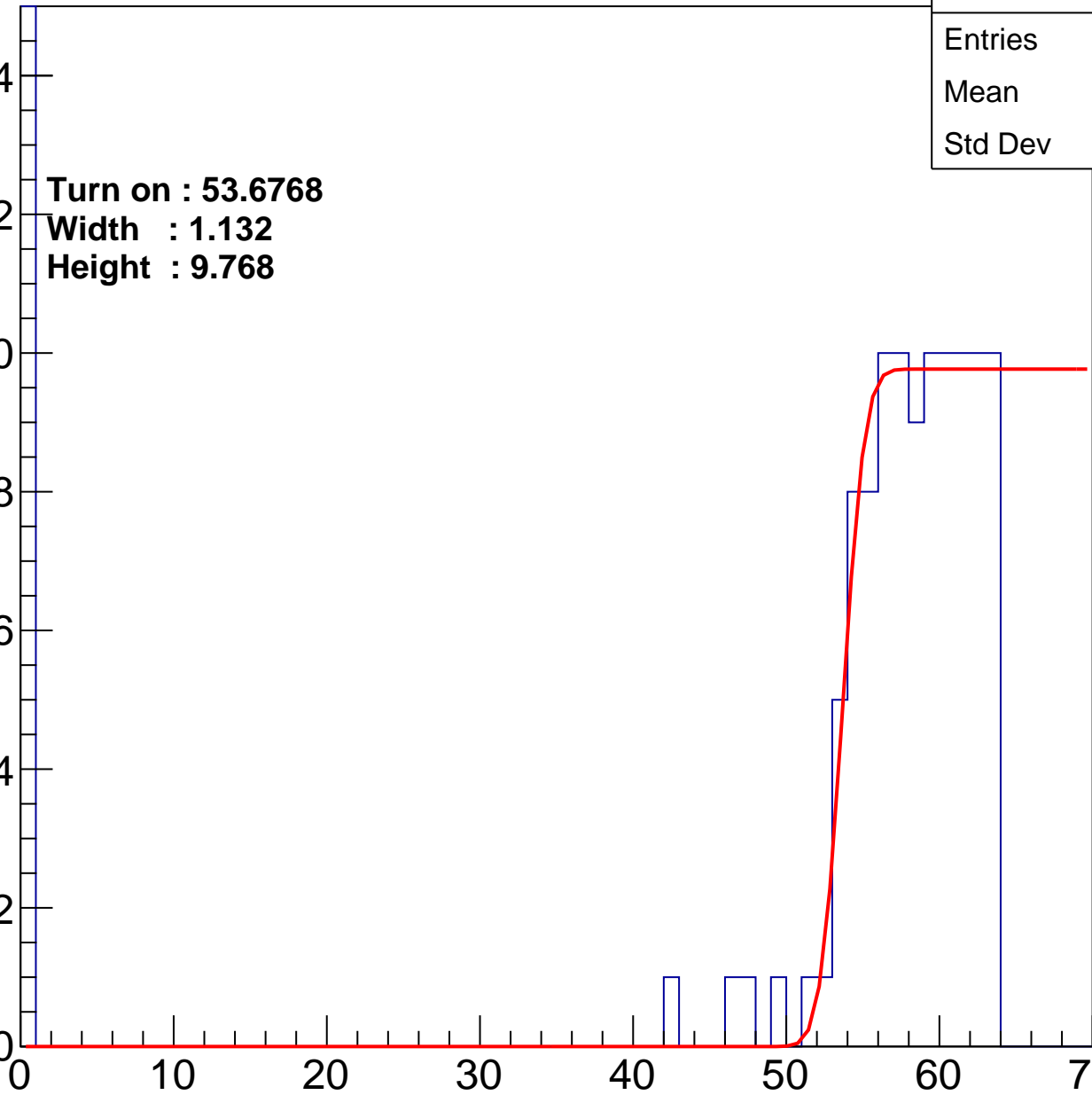
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6768
Width : 1.132
Height : 9.768

| | |
|---------|-------|
| Entries | 192 |
| Mean | 31.91 |
| Std Dev | 28.88 |

ampl



B1L104S, U6-ch4

calib_packv5_033123_0516.root, FC#4, port A1

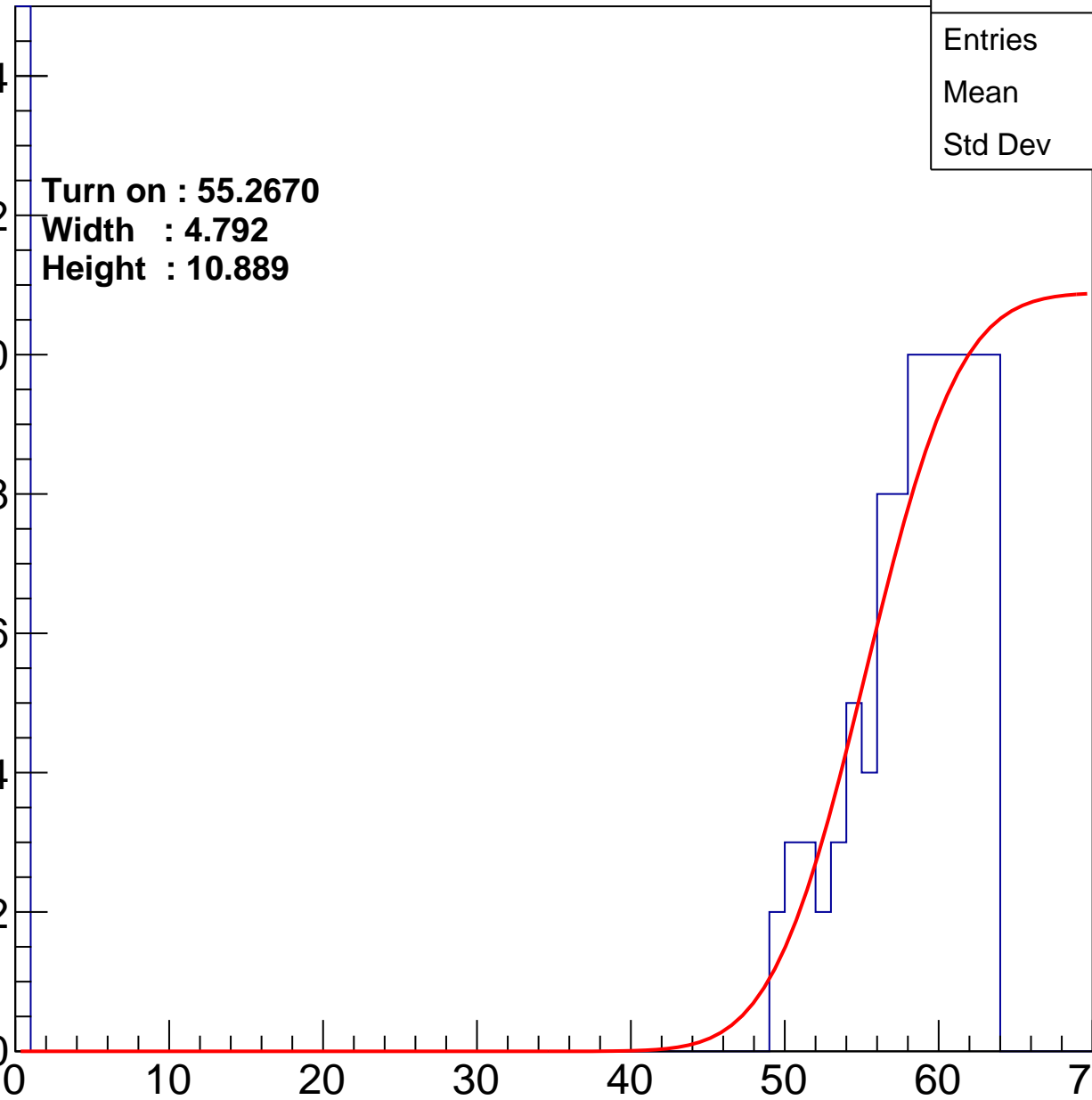
Entry

14
12
10
8
6
4
2
0

Turn on : 55.2670
Width : 4.792
Height : 10.889

| | |
|---------|-------|
| Entries | 197 |
| Mean | 28.87 |
| Std Dev | 29.14 |

ampl



B1L104S, U6-ch5

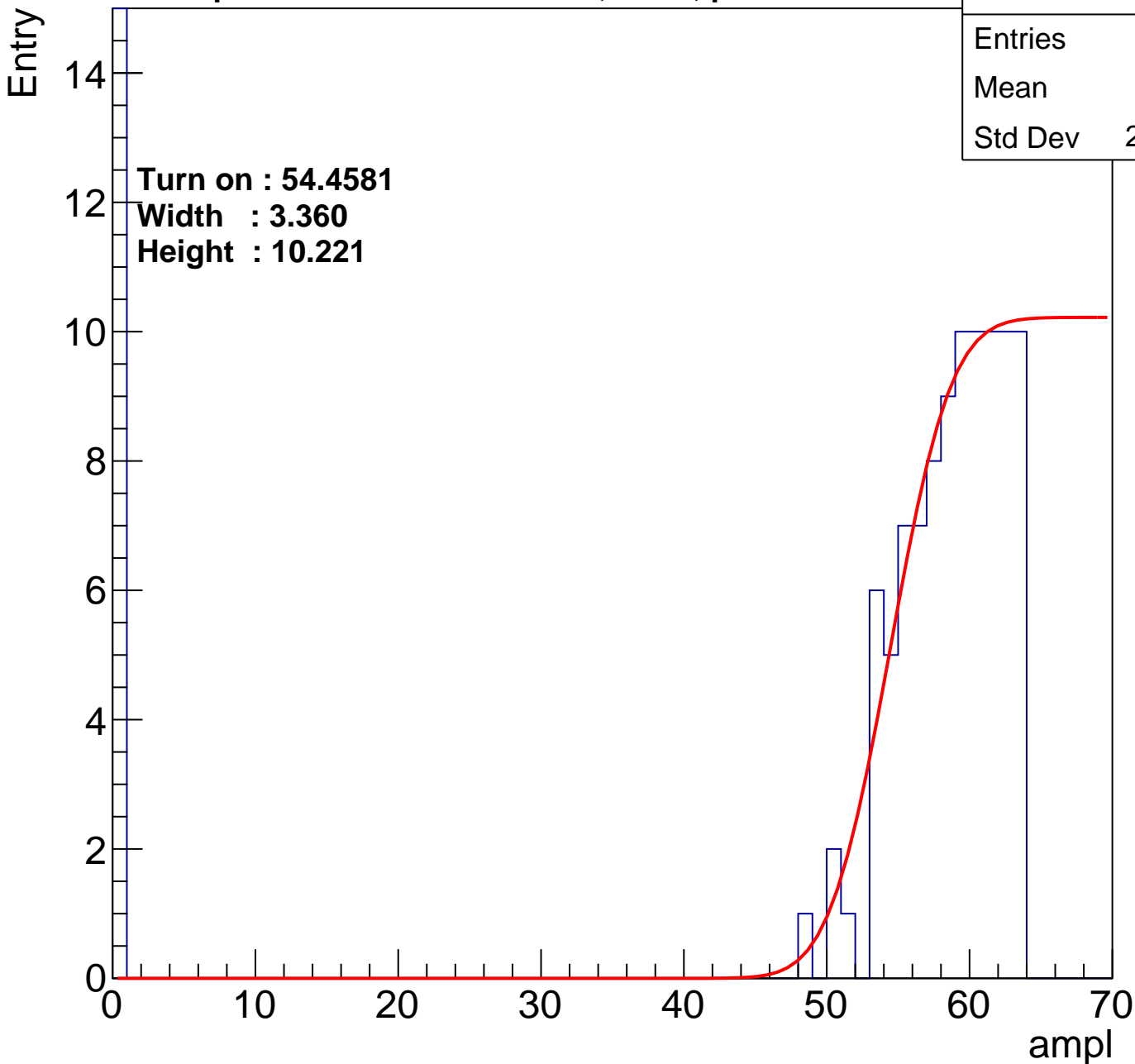
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 171 |
| Mean | 32.7 |
| Std Dev | 29.02 |

Turn on : 54.4581

Width : 3.360

Height : 10.221



B1L104S, U6-ch6

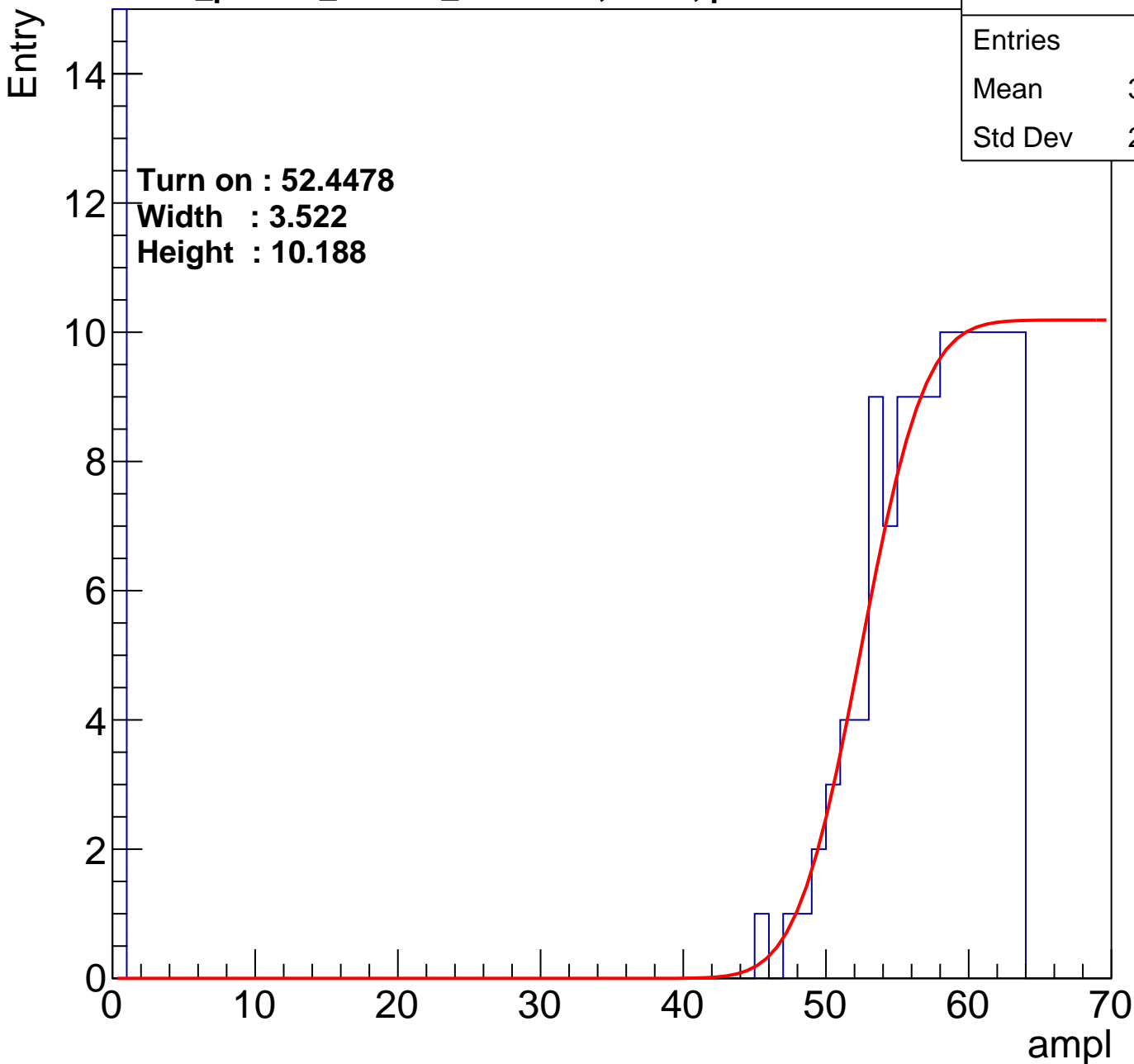
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 190 |
| Mean | 35.77 |
| Std Dev | 27.82 |

Turn on : 52.4478

Width : 3.522

Height : 10.188



B1L104S, U6-ch7

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 189 |
| Mean | 32.15 |
| Std Dev | 28.88 |

Turn on : 53.4437

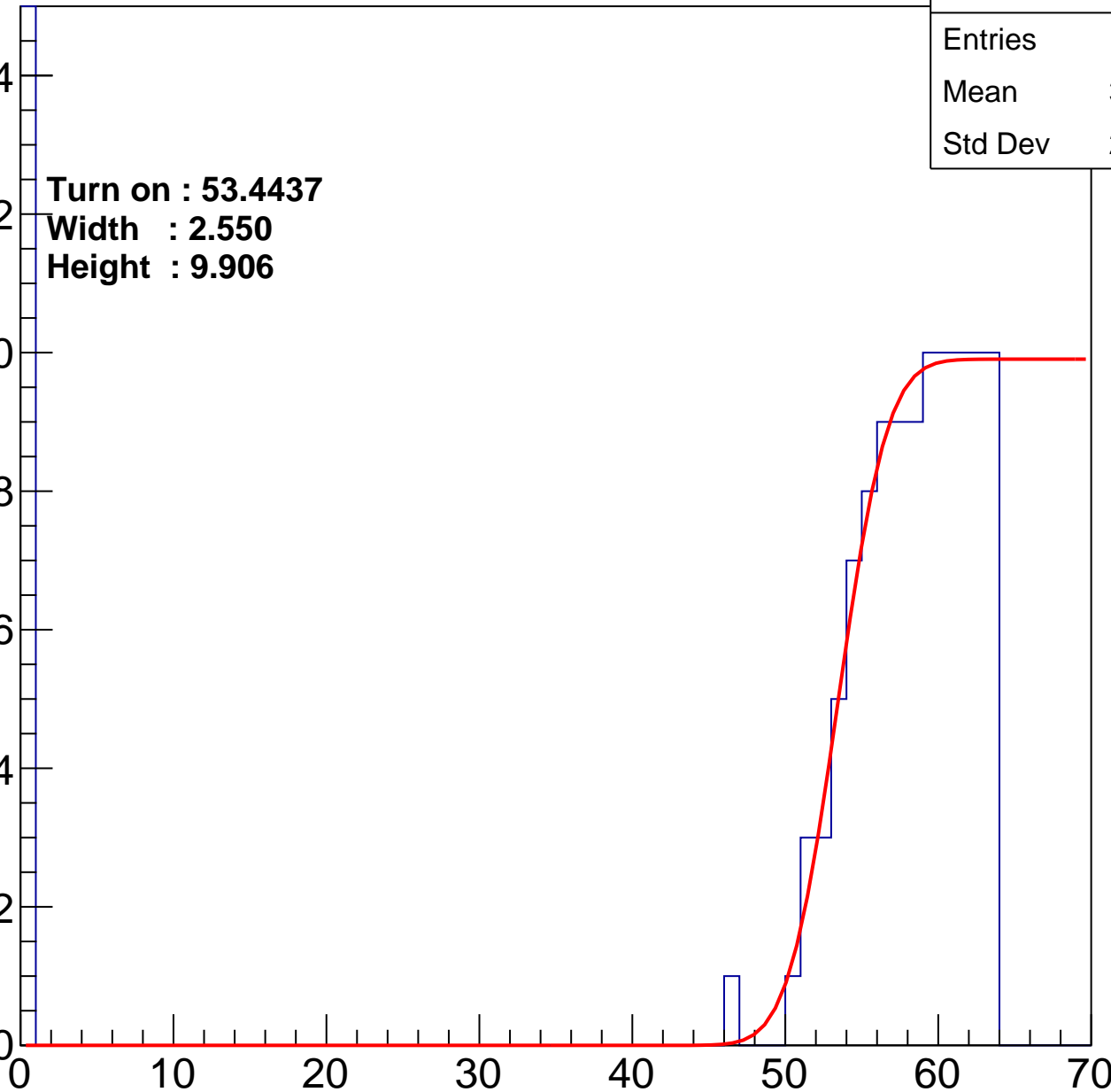
Width : 2.550

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch8

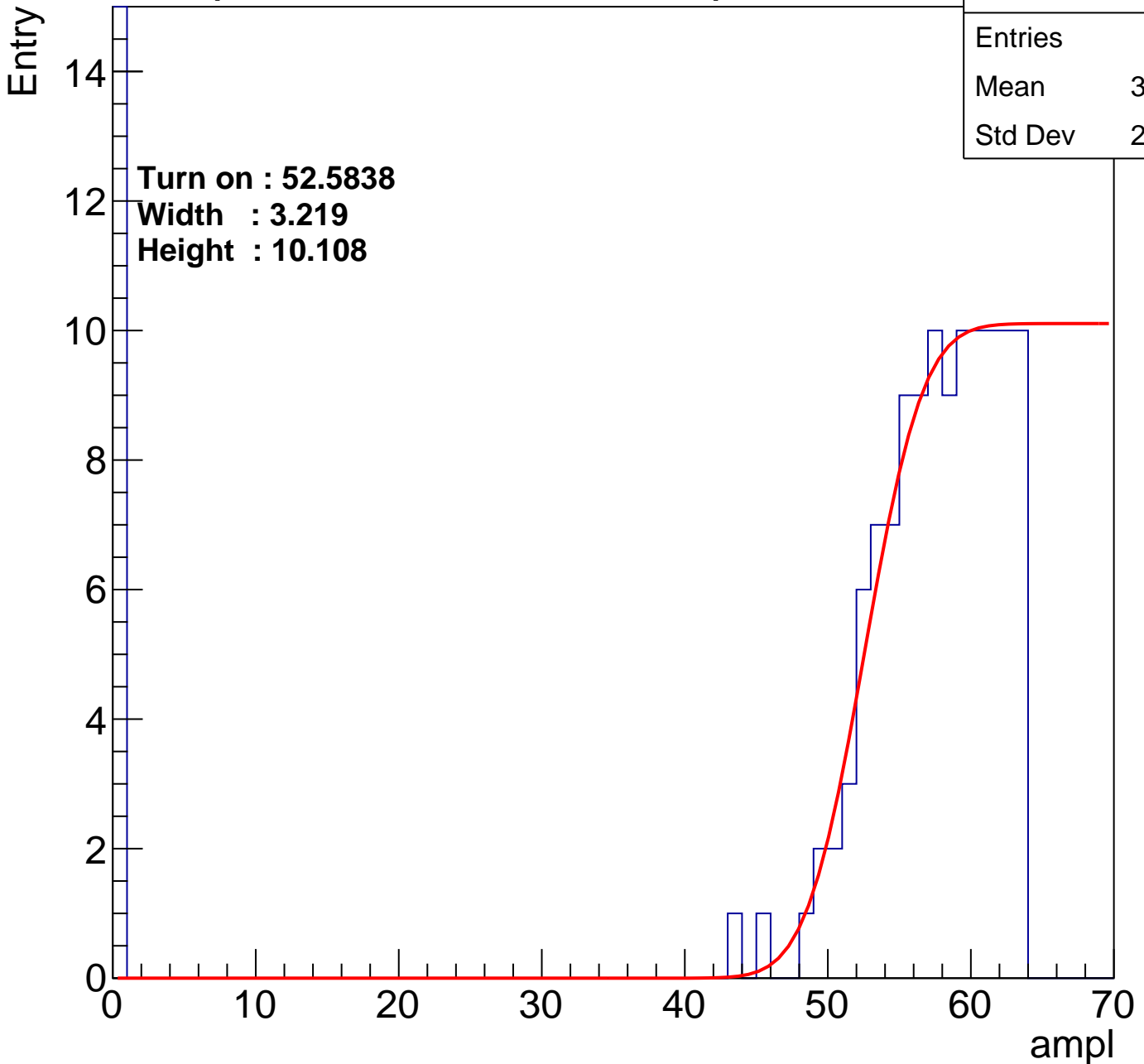
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 180 |
| Mean | 37.16 |
| Std Dev | 27.47 |

Turn on : 52.5838

Width : 3.219

Height : 10.108



B1L104S, U6-ch9

calib_packv5_033123_0516.root, FC#4, port A1

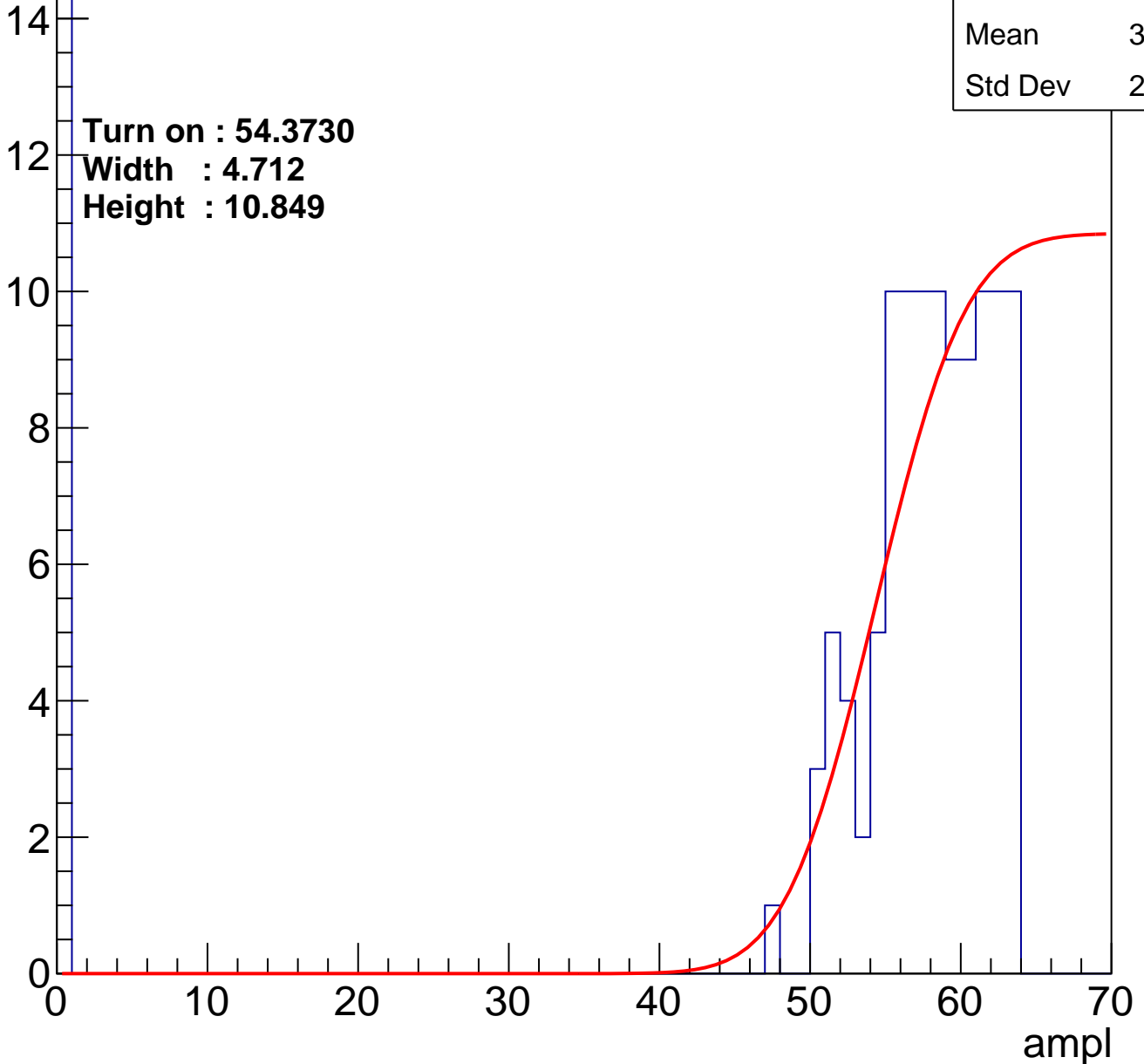
Entry

| | |
|---------|-------|
| Entries | 175 |
| Mean | 35.58 |
| Std Dev | 28.18 |

Turn on : 54.3730

Width : 4.712

Height : 10.849



B1L104S, U6-ch10

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 32.5 |
| Std Dev | 28.63 |

Turn on : 52.8996

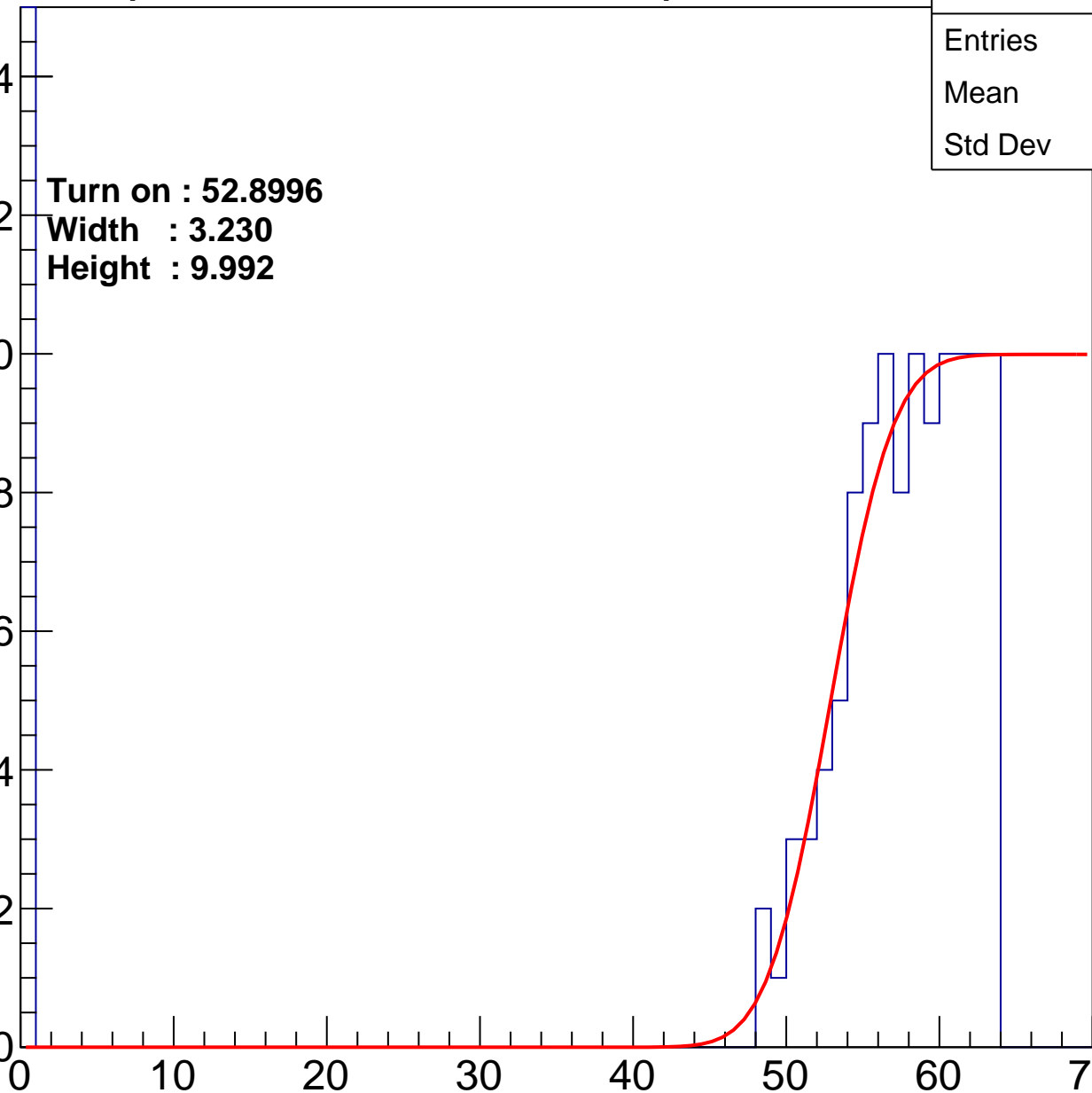
Width : 3.230

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch11

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 31.71 |
| Std Dev | 28.99 |

Turn on : 55.0186

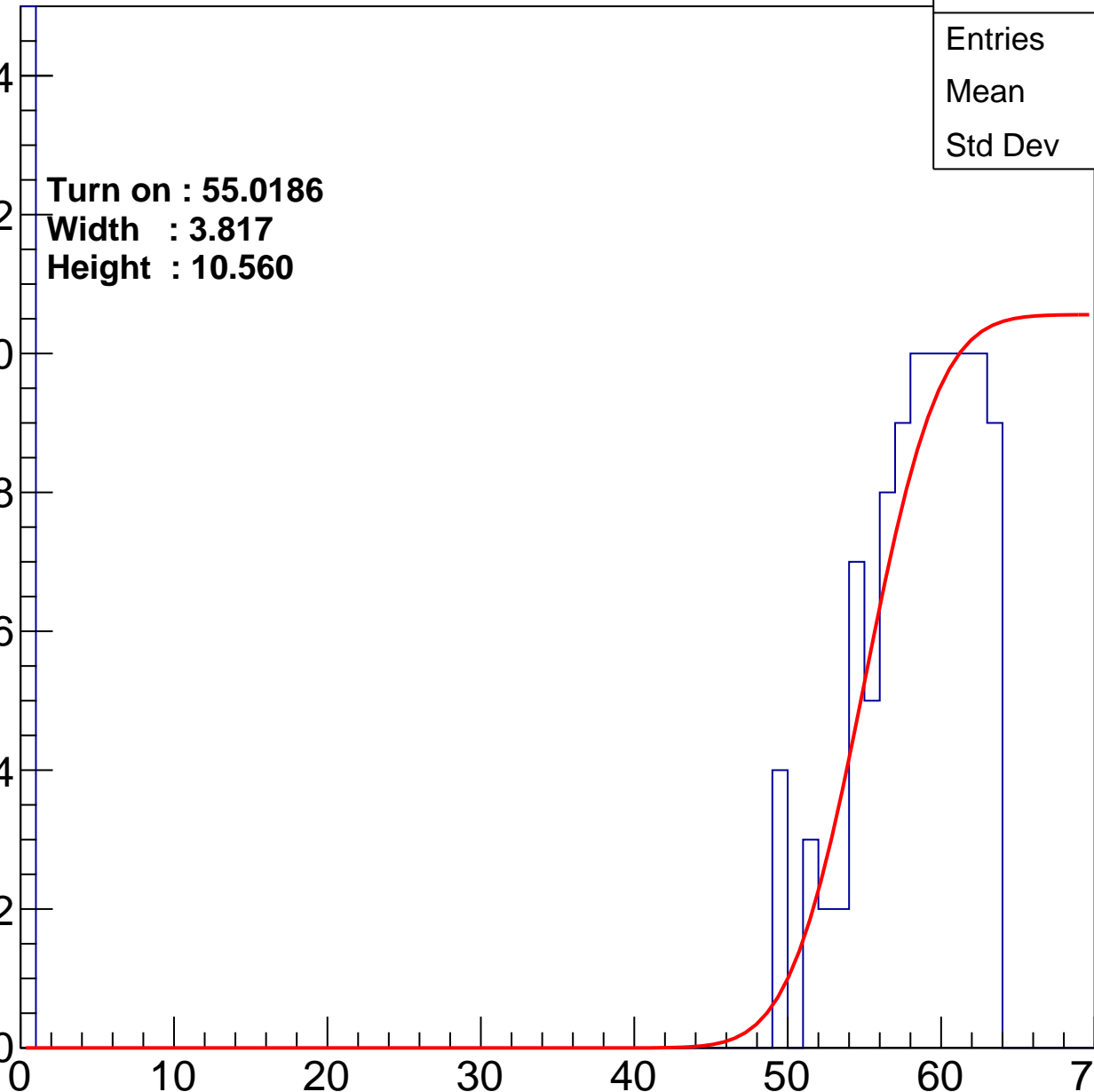
Width : 3.817

Height : 10.560

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch12

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 31.19 |
| Std Dev | 28.89 |

Turn on : 53.1036

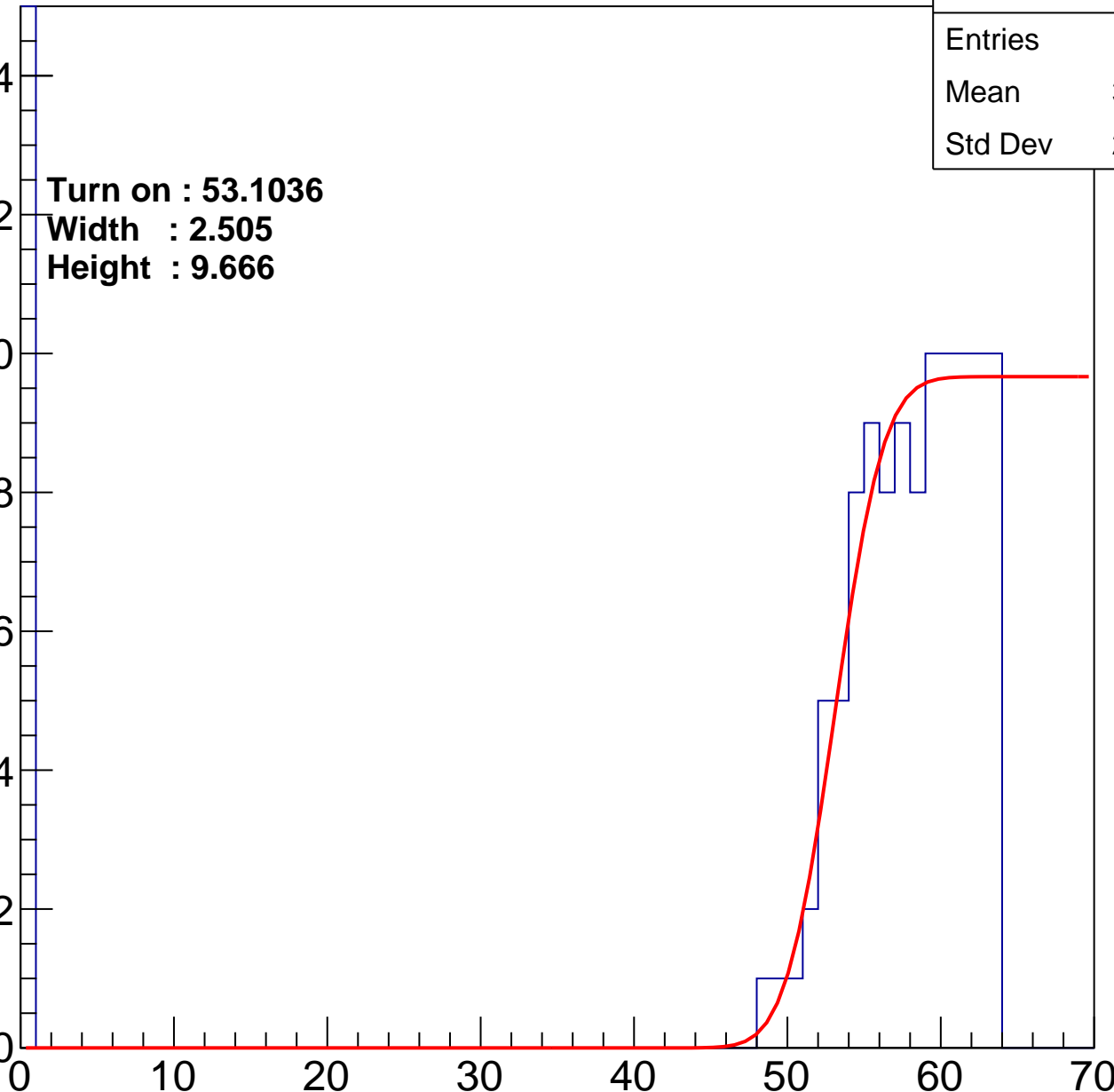
Width : 2.505

Height : 9.666

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch13

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 161 |
| Mean | 36.42 |
| Std Dev | 28.2 |

Turn on : 54.2403

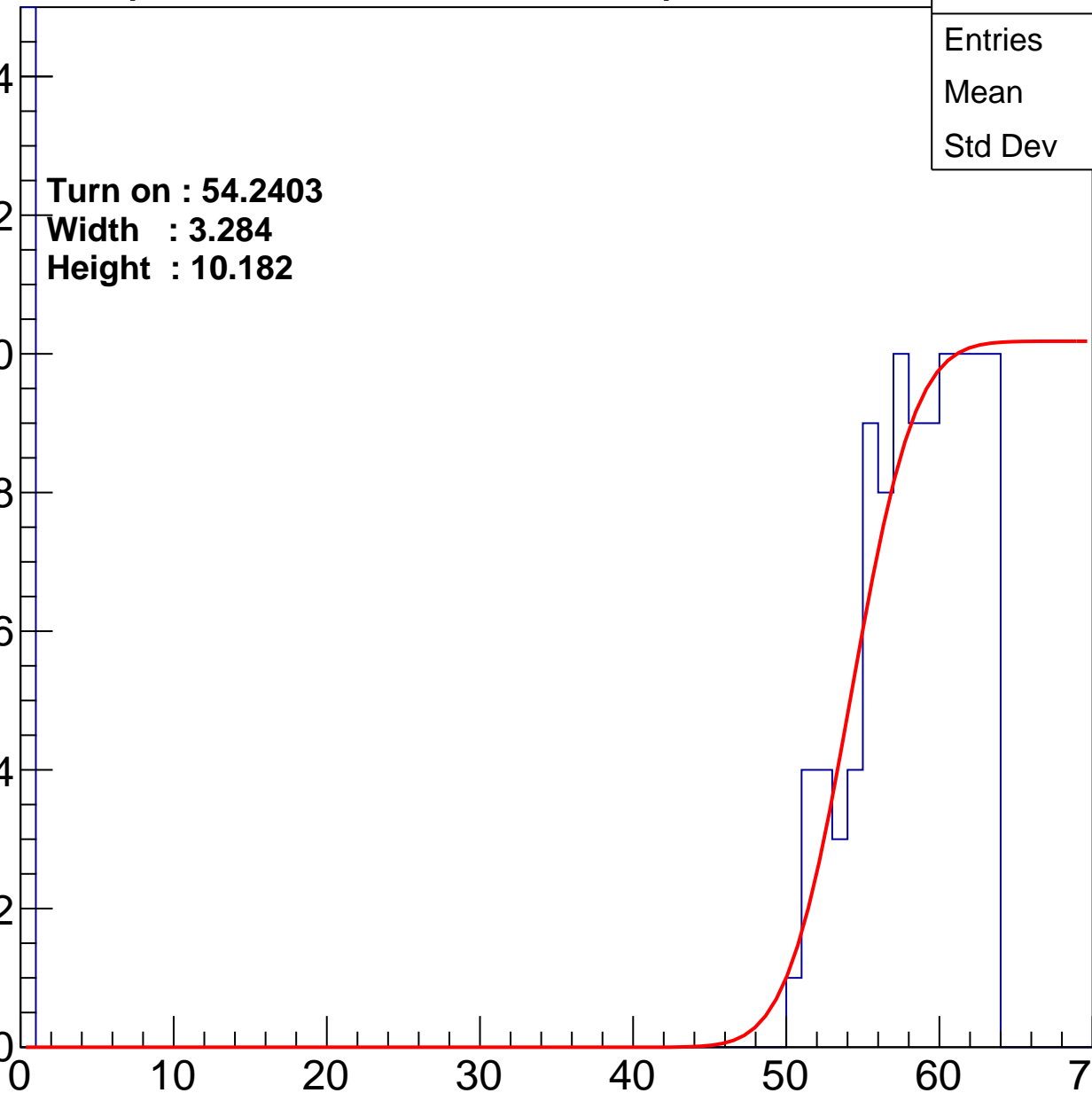
Width : 3.284

Height : 10.182

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch14

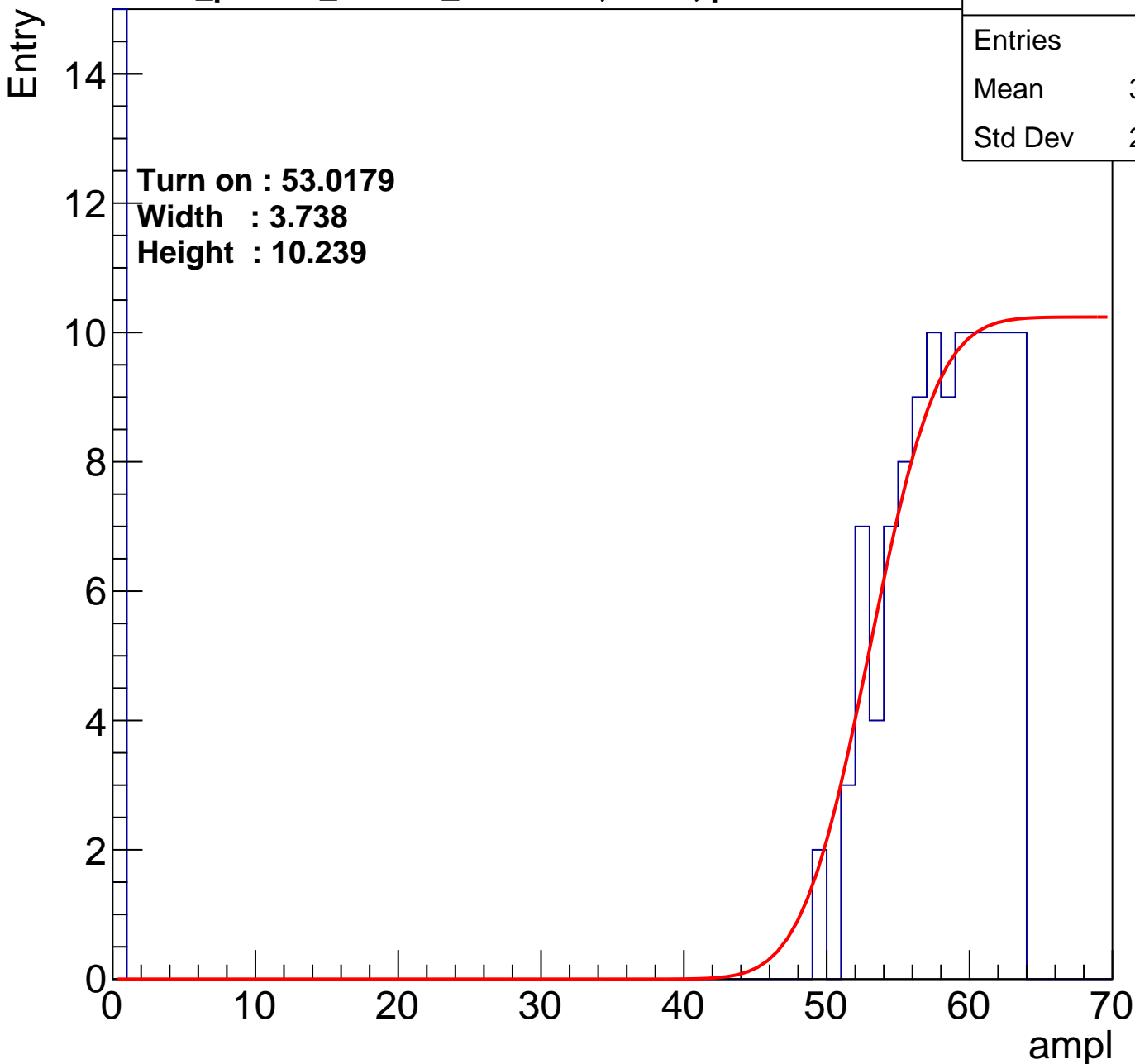
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 162 |
| Mean | 38.83 |
| Std Dev | 27.24 |

Turn on : 53.0179

Width : 3.738

Height : 10.239



B1L104S, U6-ch15

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 174 |
| Mean | 36.71 |
| Std Dev | 27.83 |

Turn on : 53.6078

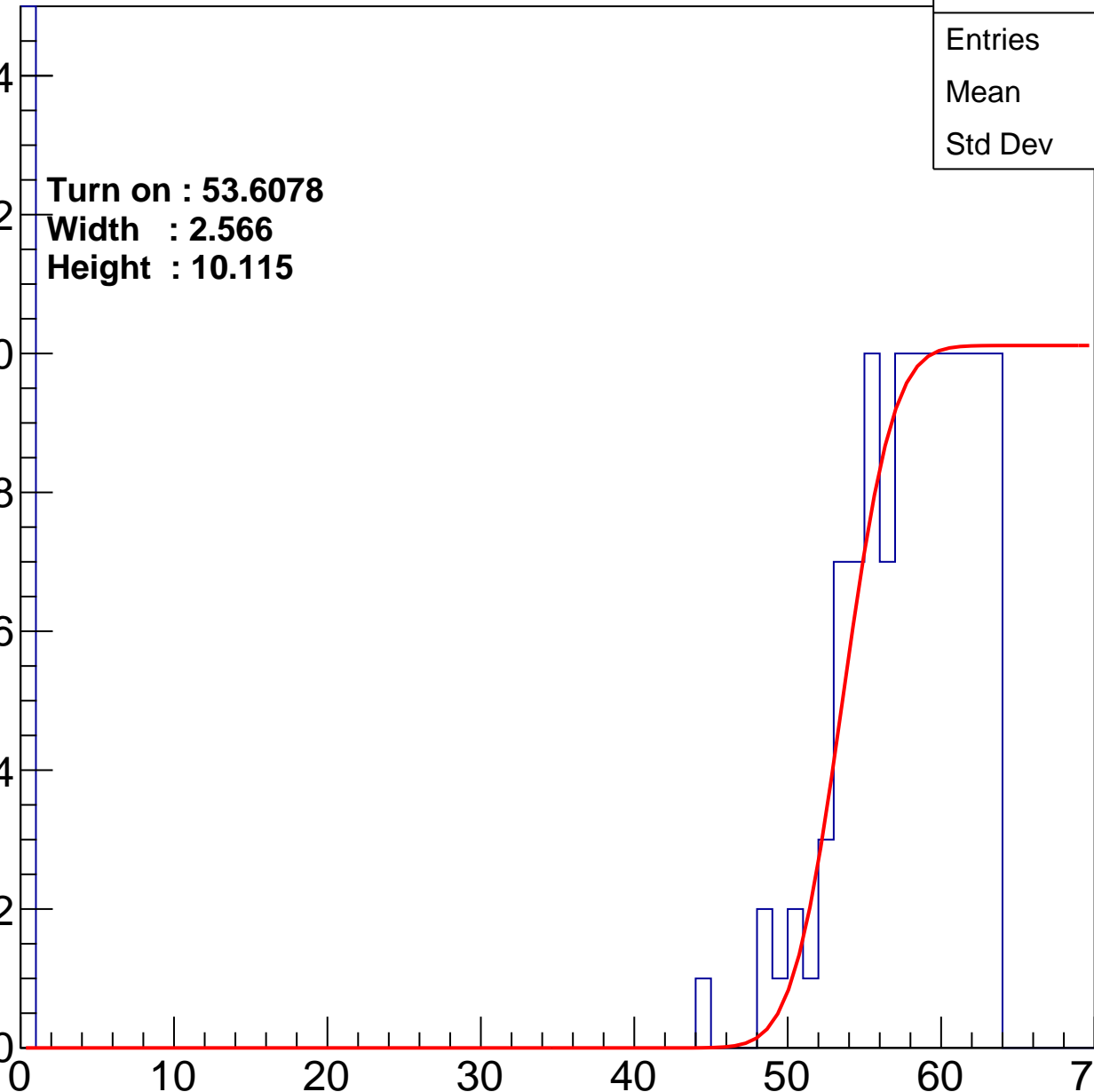
Width : 2.566

Height : 10.115

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch16

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 224 |
| Mean | 33.83 |
| Std Dev | 27.51 |

Turn on : 52.6583

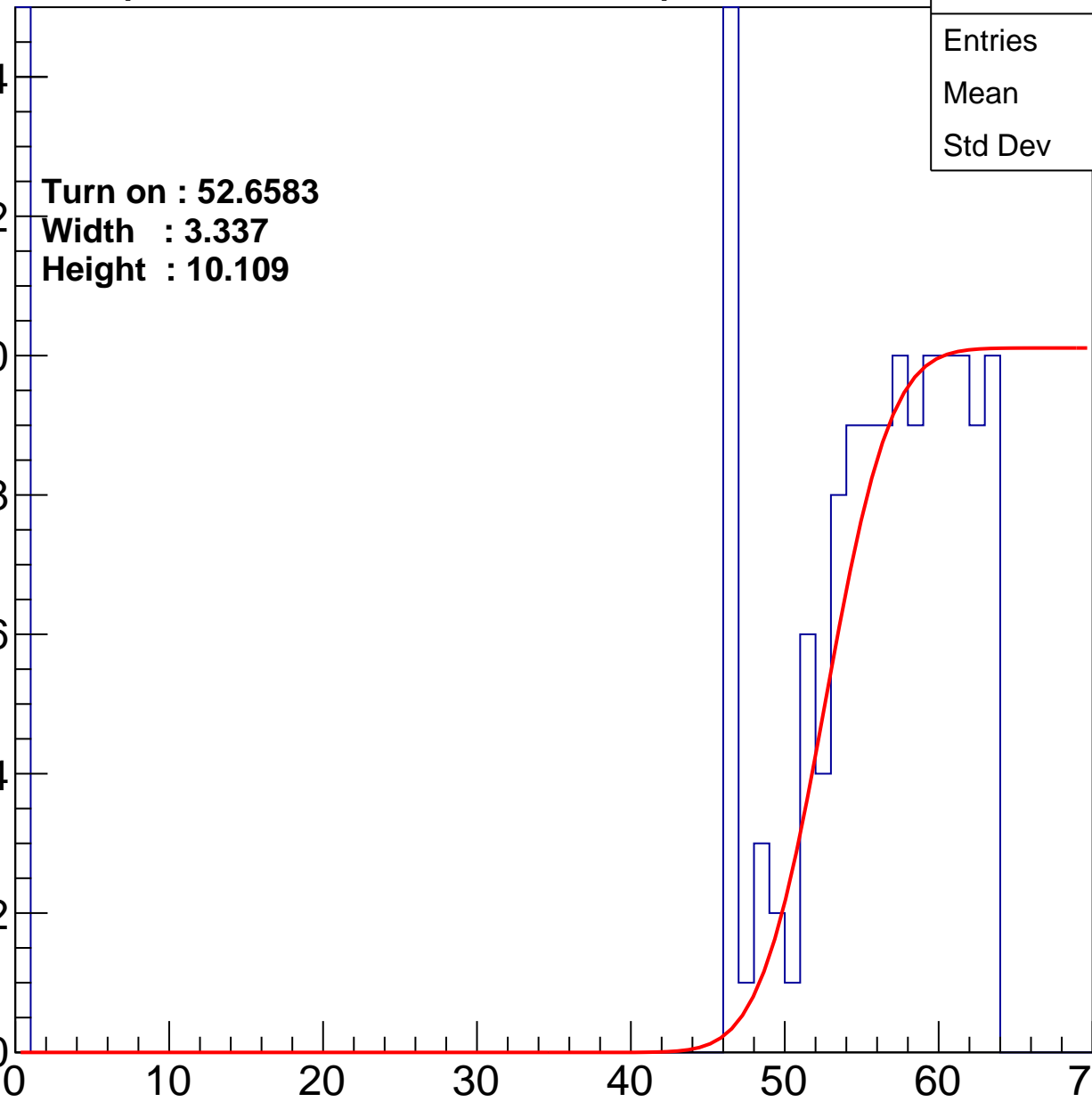
Width : 3.337

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch17

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 32.72 |
| Std Dev | 28.91 |

Turn on : 54.0240

Width : 2.669

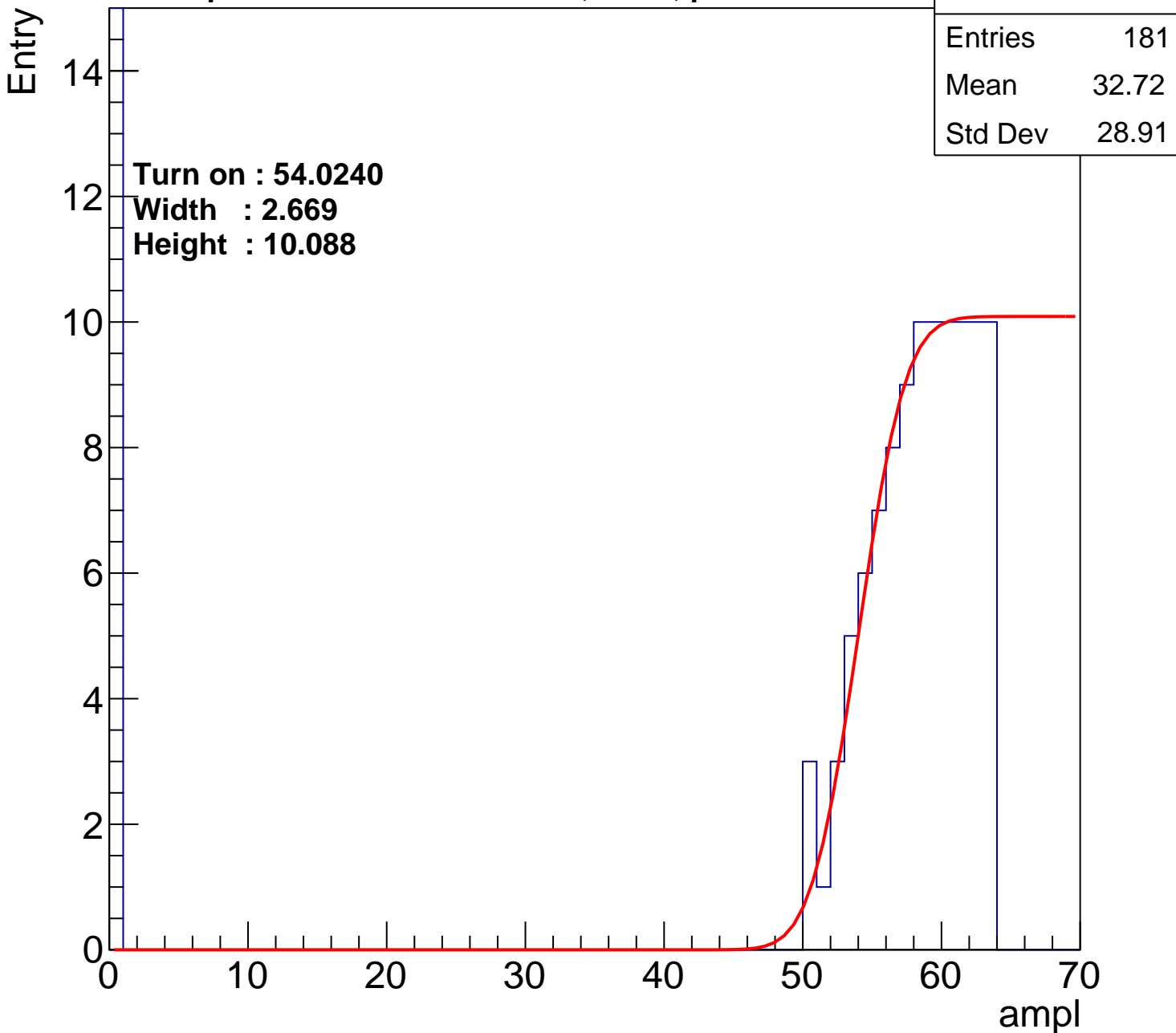
Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch18

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 32.39 |
| Std Dev | 28.96 |

Turn on : 54.4502

Width : 3.548

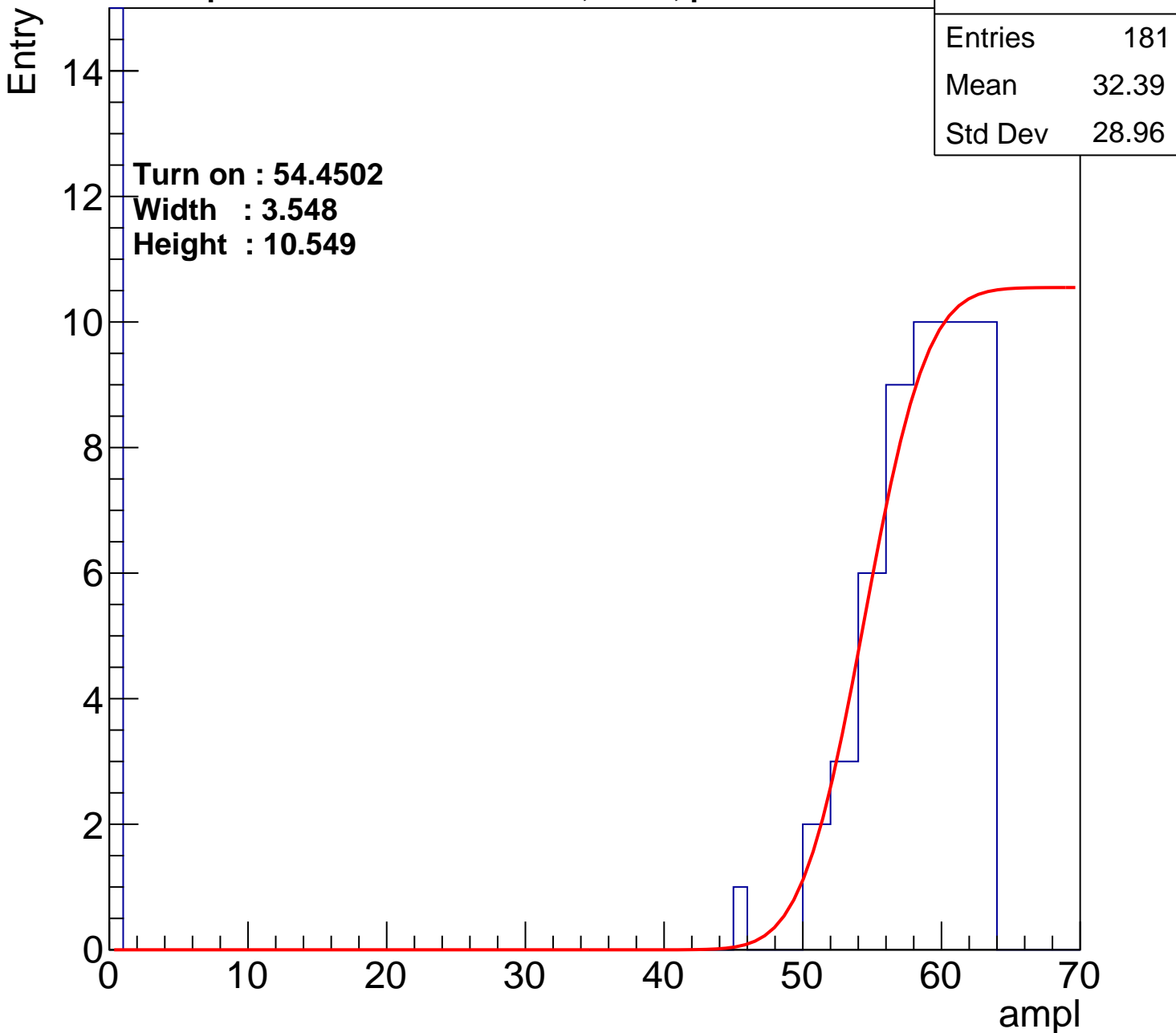
Height : 10.549

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch19

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 168 |
| Mean | 37.15 |
| Std Dev | 27.84 |

Turn on : 53.6859

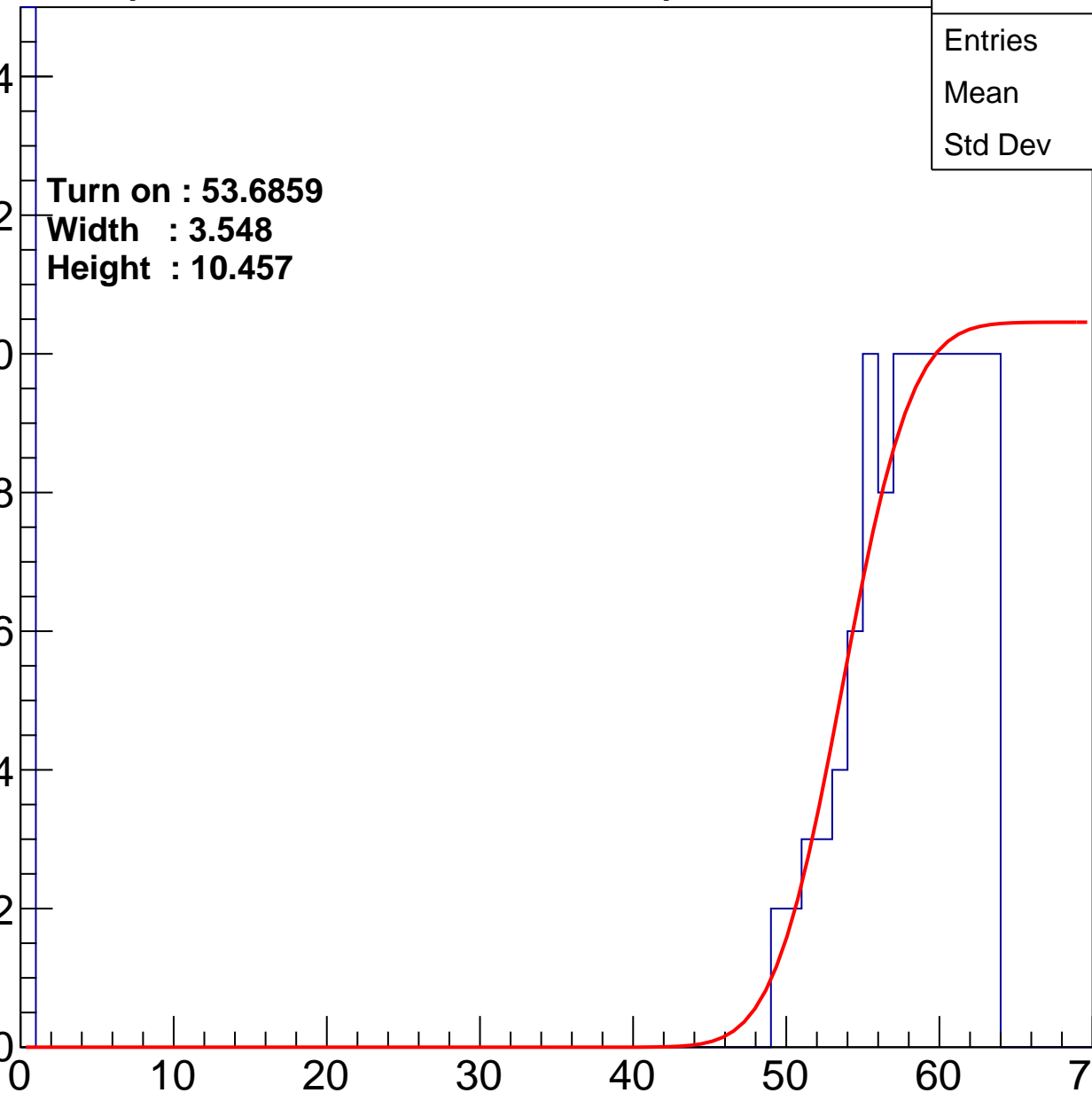
Width : 3.548

Height : 10.457

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch20

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 204 |
| Mean | 32.1 |
| Std Dev | 28.67 |

Turn on : 53.1569

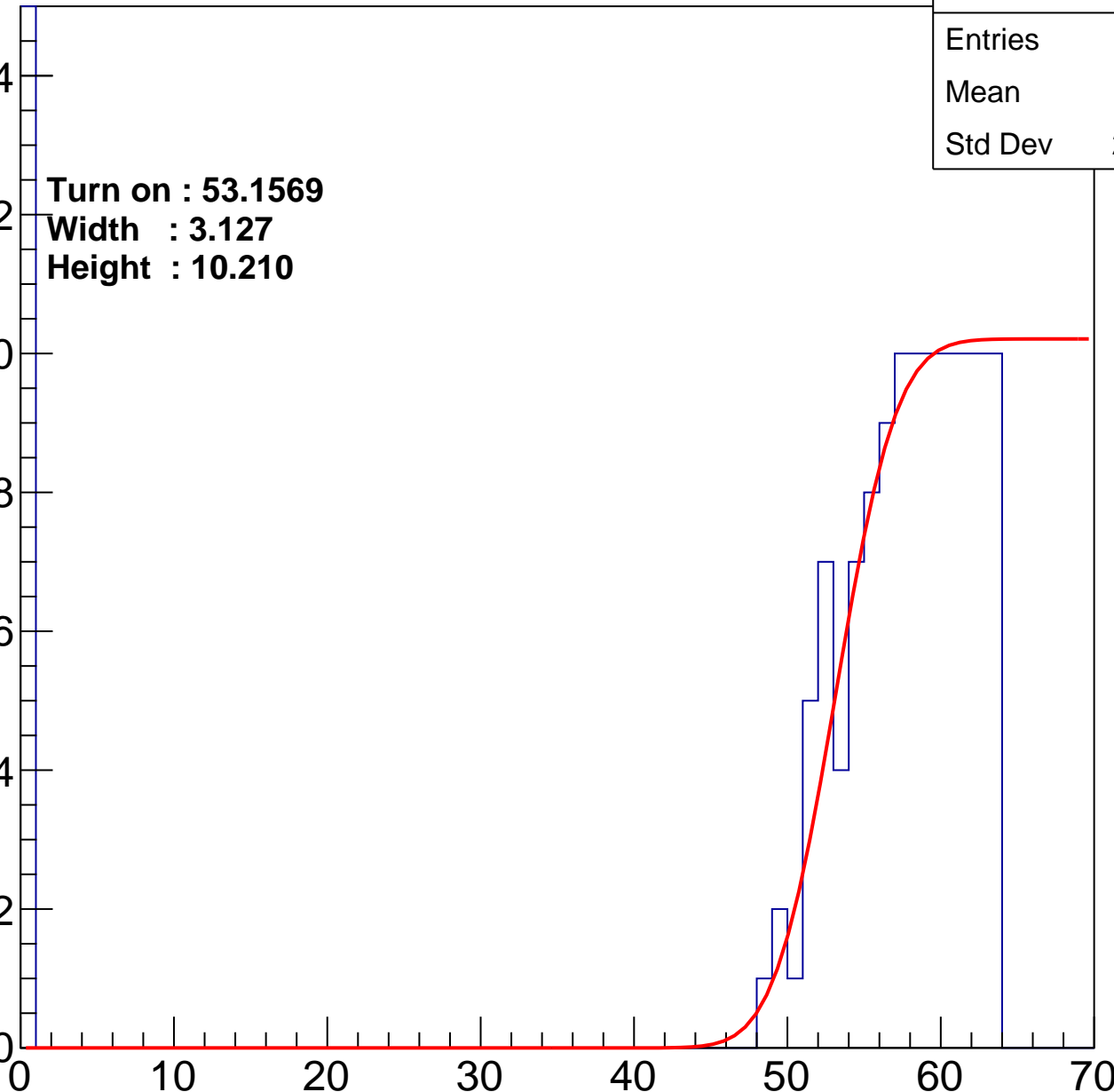
Width : 3.127

Height : 10.210

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch21

calib_packv5_033123_0516.root, FC#4, port A1

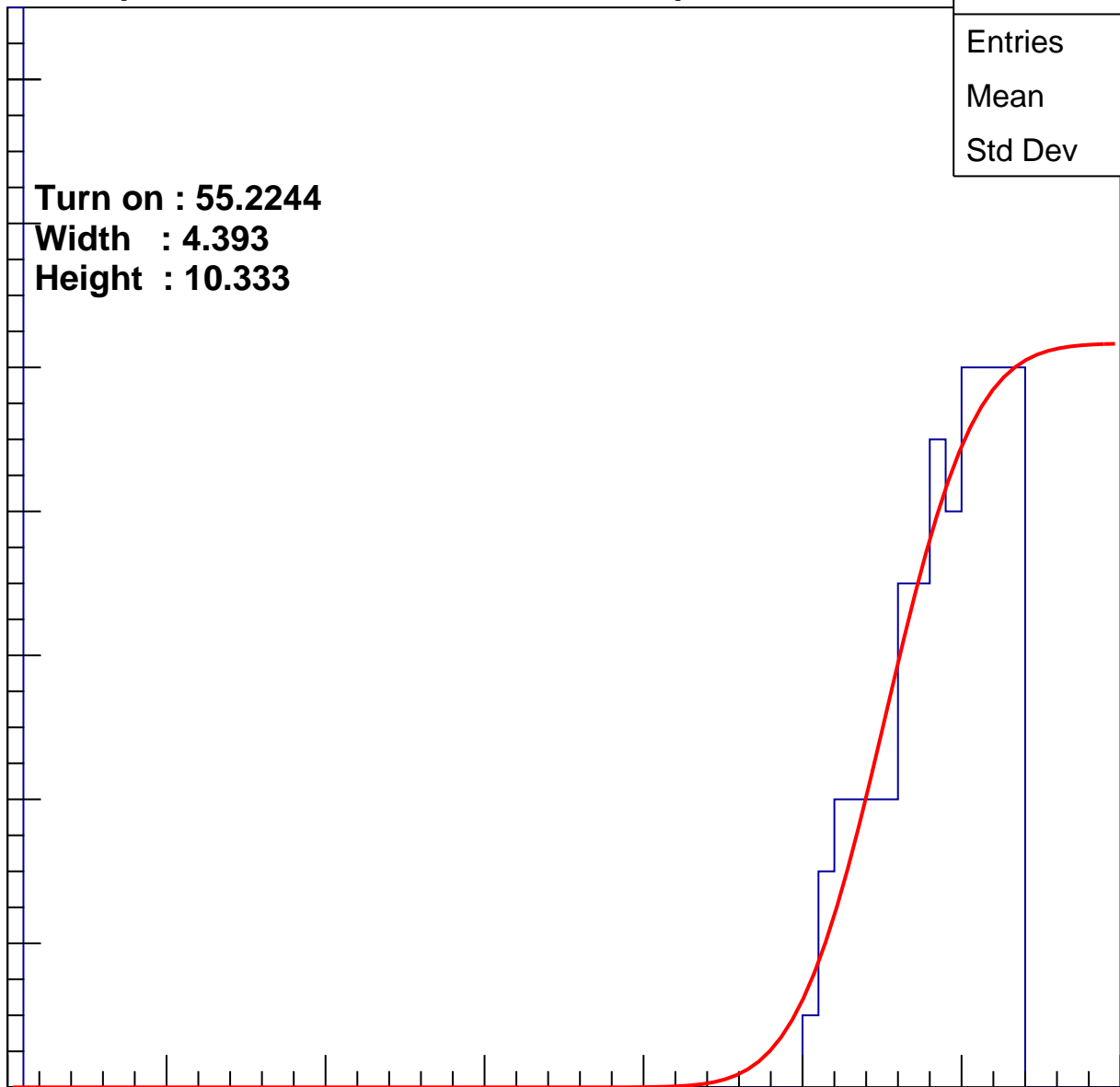
Entry

14
12
10
8
6
4
2
0

Turn on : 55.2244
Width : 4.393
Height : 10.333

| | |
|---------|-------|
| Entries | 150 |
| Mean | 35.36 |
| Std Dev | 28.6 |

ampl



B1L104S, U6-ch22

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 189 |
| Mean | 34.53 |
| Std Dev | 28.2 |

Turn on : 53.5221

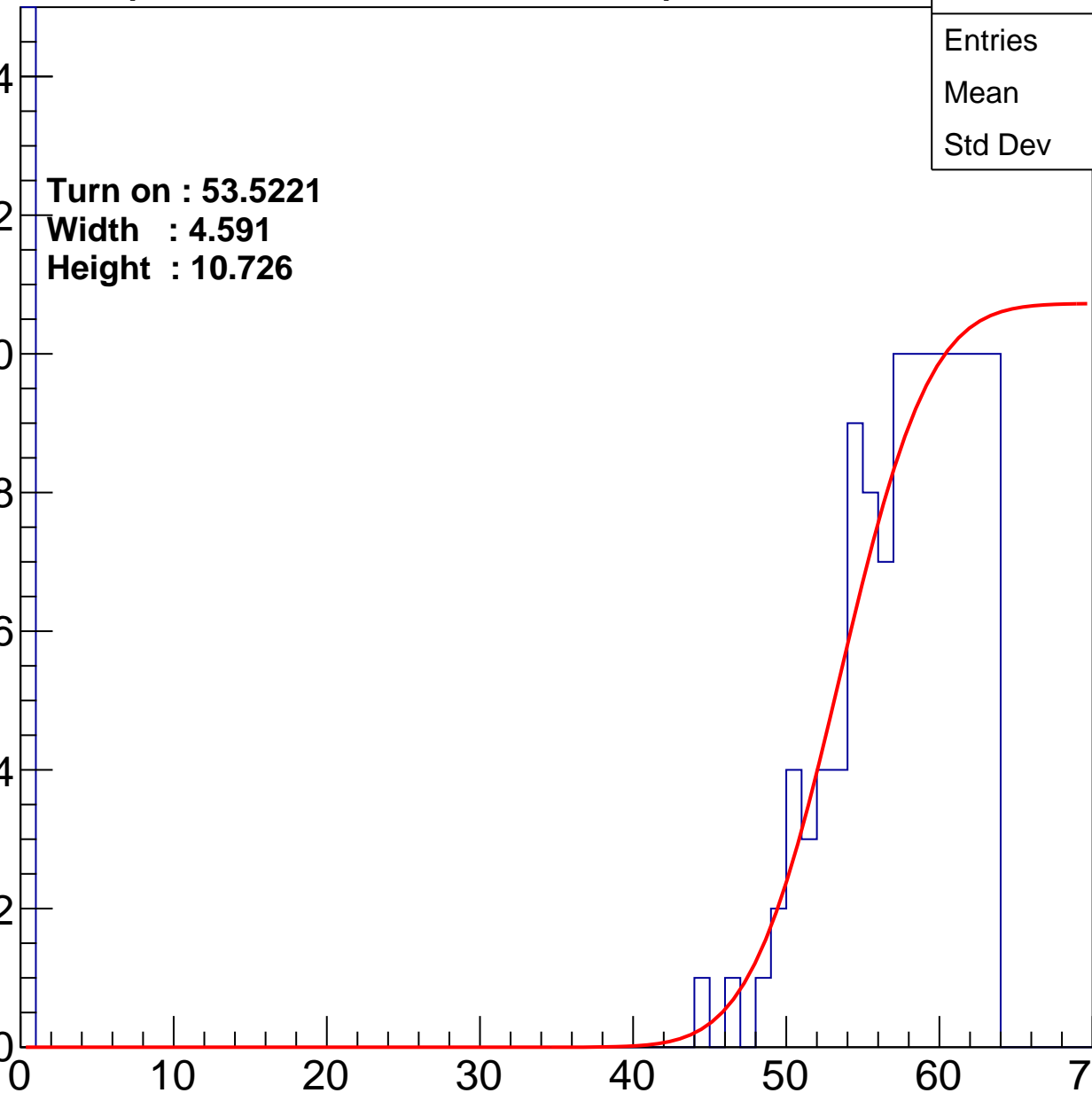
Width : 4.591

Height : 10.726

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch23

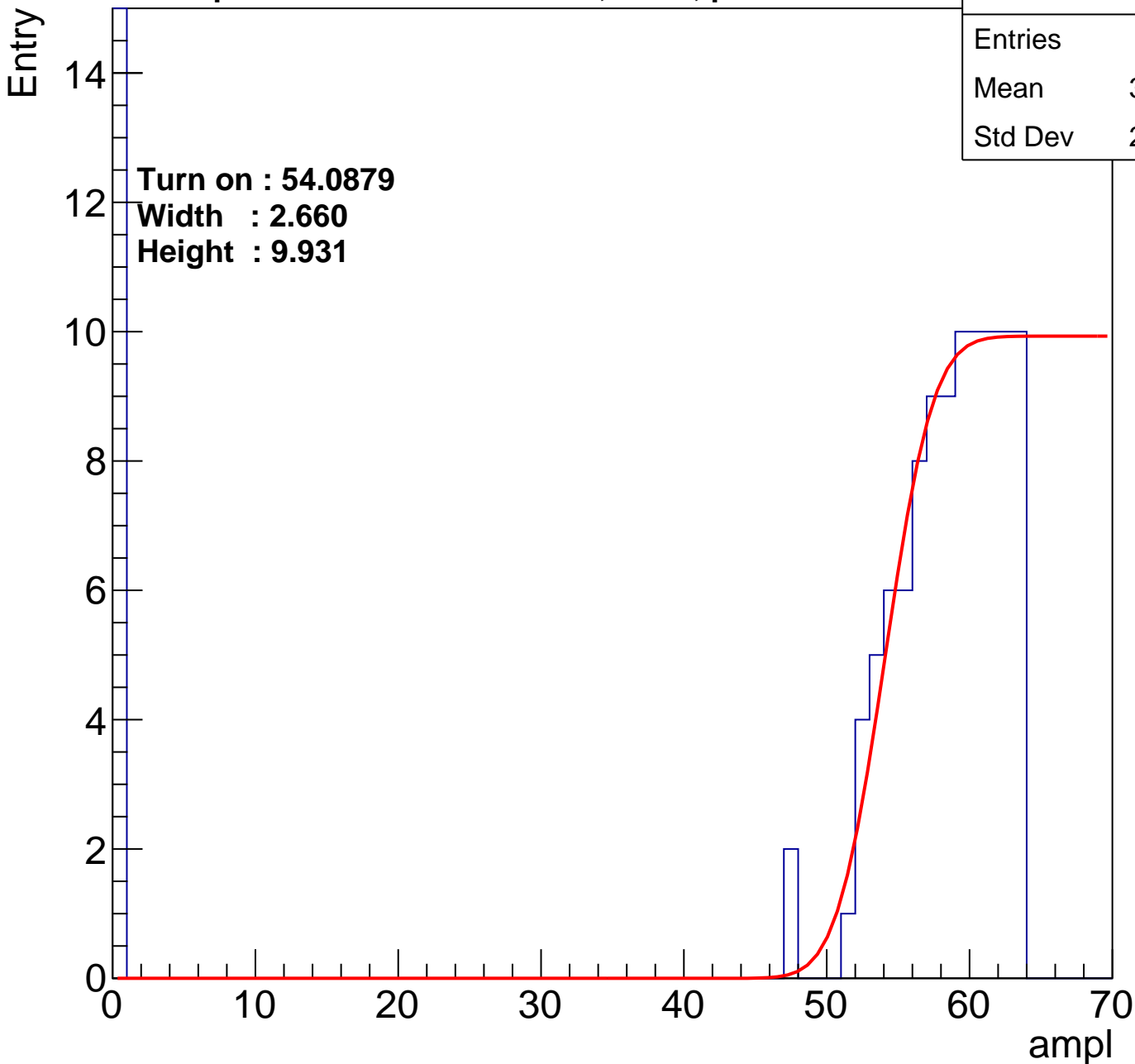
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 183 |
| Mean | 31.72 |
| Std Dev | 29.02 |

Turn on : 54.0879

Width : 2.660

Height : 9.931



B1L104S, U6-ch24

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 210 |
| Mean | 32.04 |
| Std Dev | 28.48 |

Turn on : 52.9736

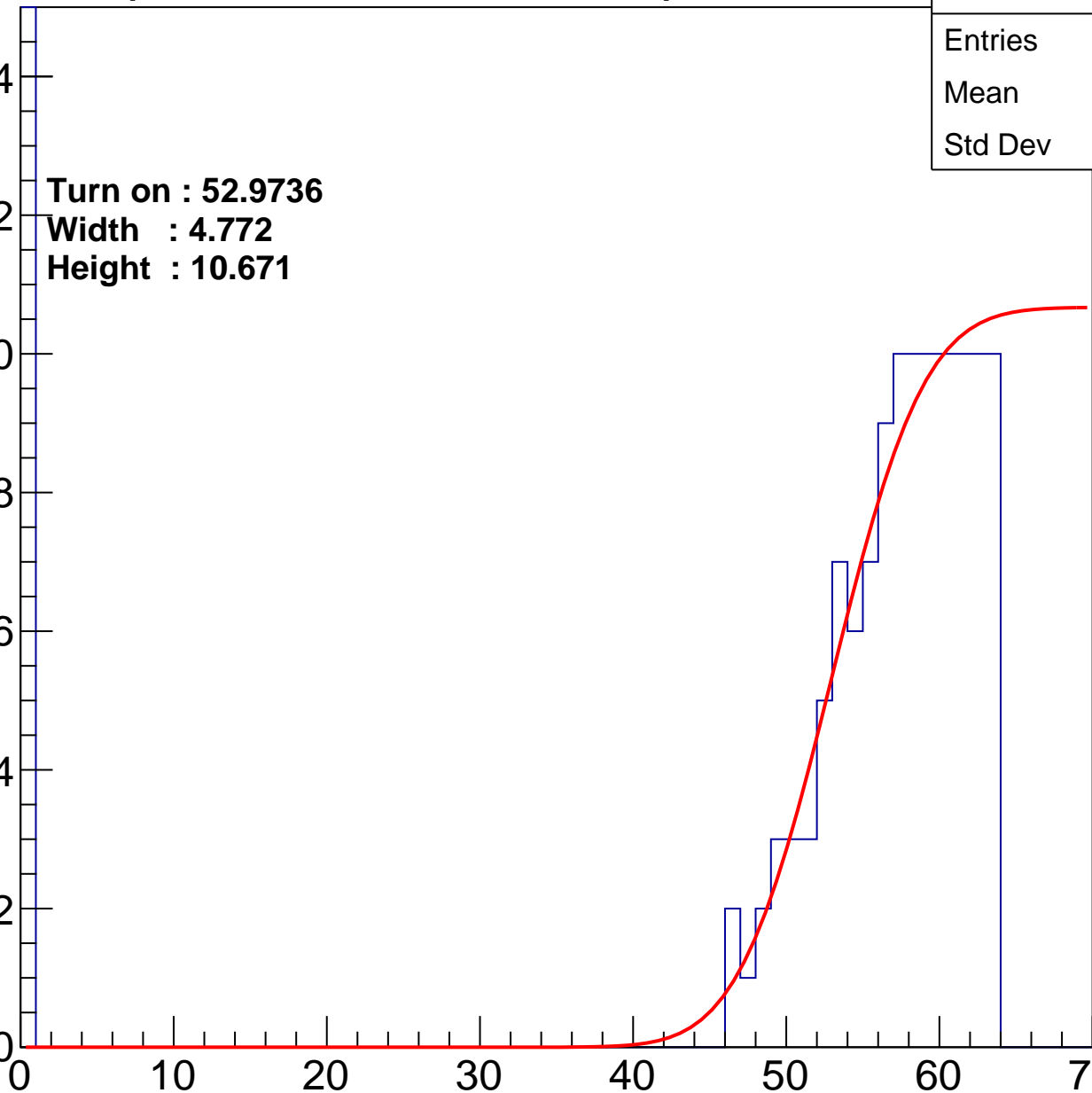
Width : 4.772

Height : 10.671

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch25

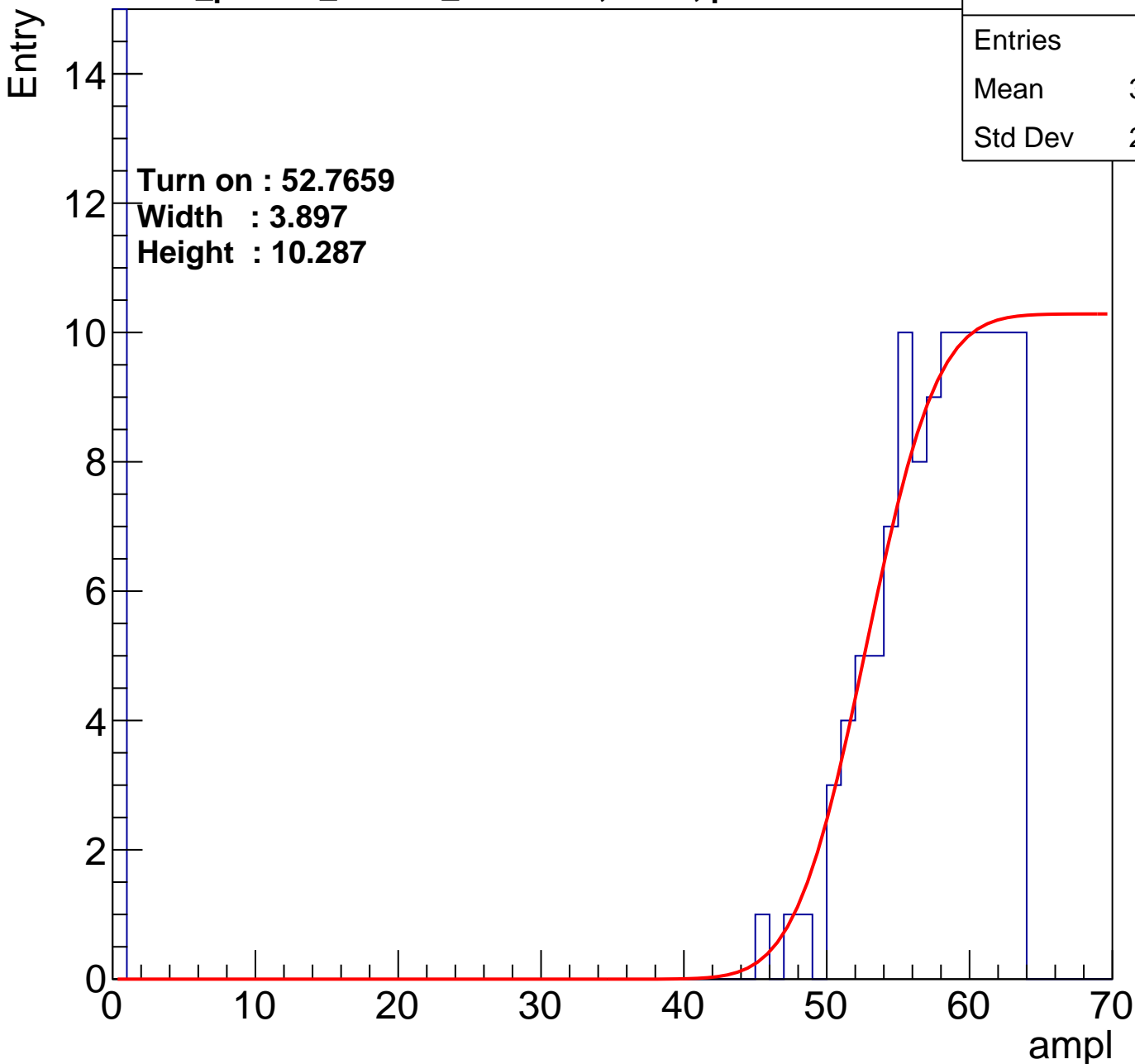
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 183 |
| Mean | 35.73 |
| Std Dev | 27.97 |

Turn on : 52.7659

Width : 3.897

Height : 10.287



B1L104S, U6-ch26

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 183 |
| Mean | 33.85 |
| Std Dev | 28.39 |

Turn on : 52.9011

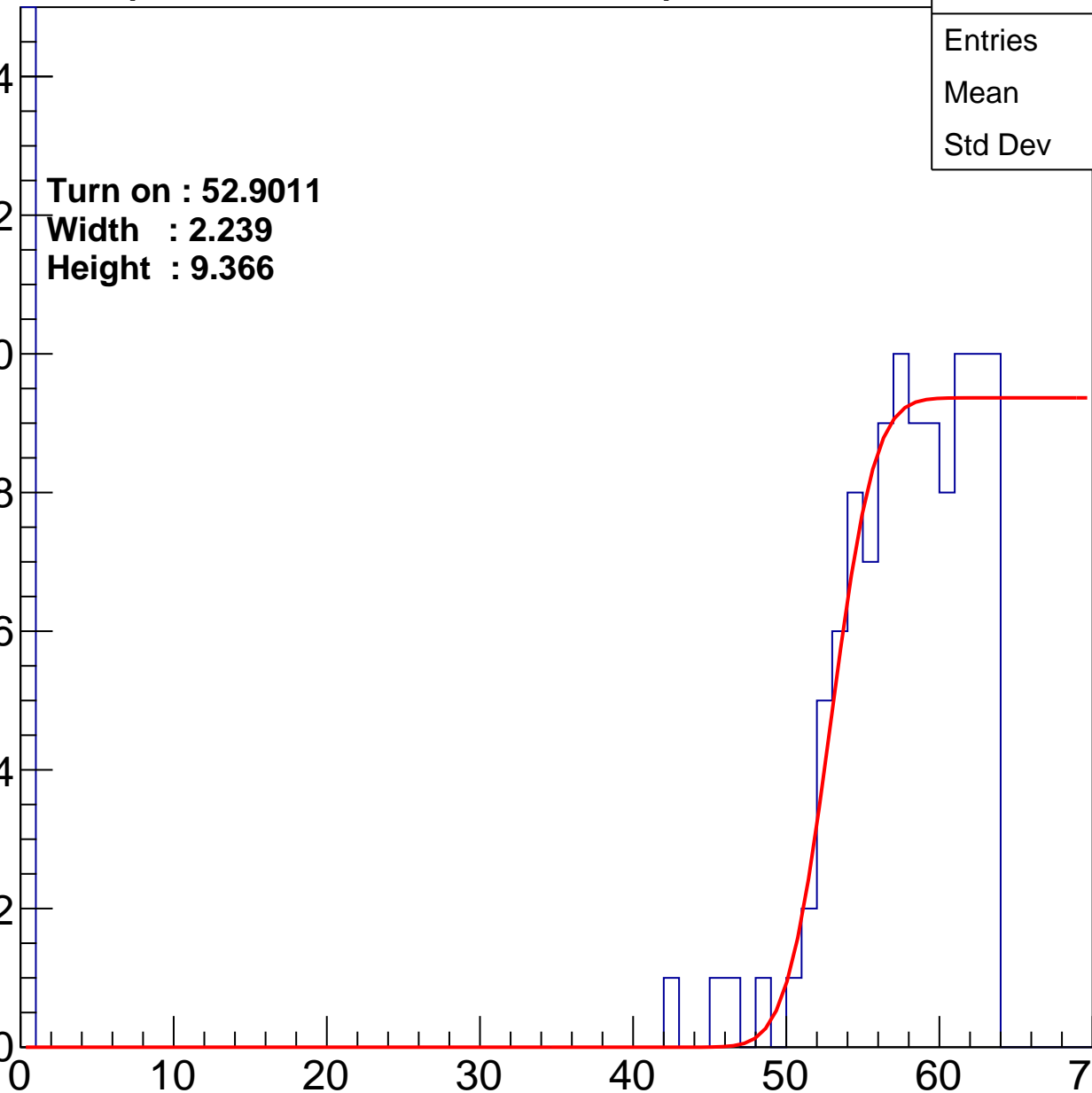
Width : 2.239

Height : 9.366

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch27

calib_packv5_033123_0516.root, FC#4, port A1

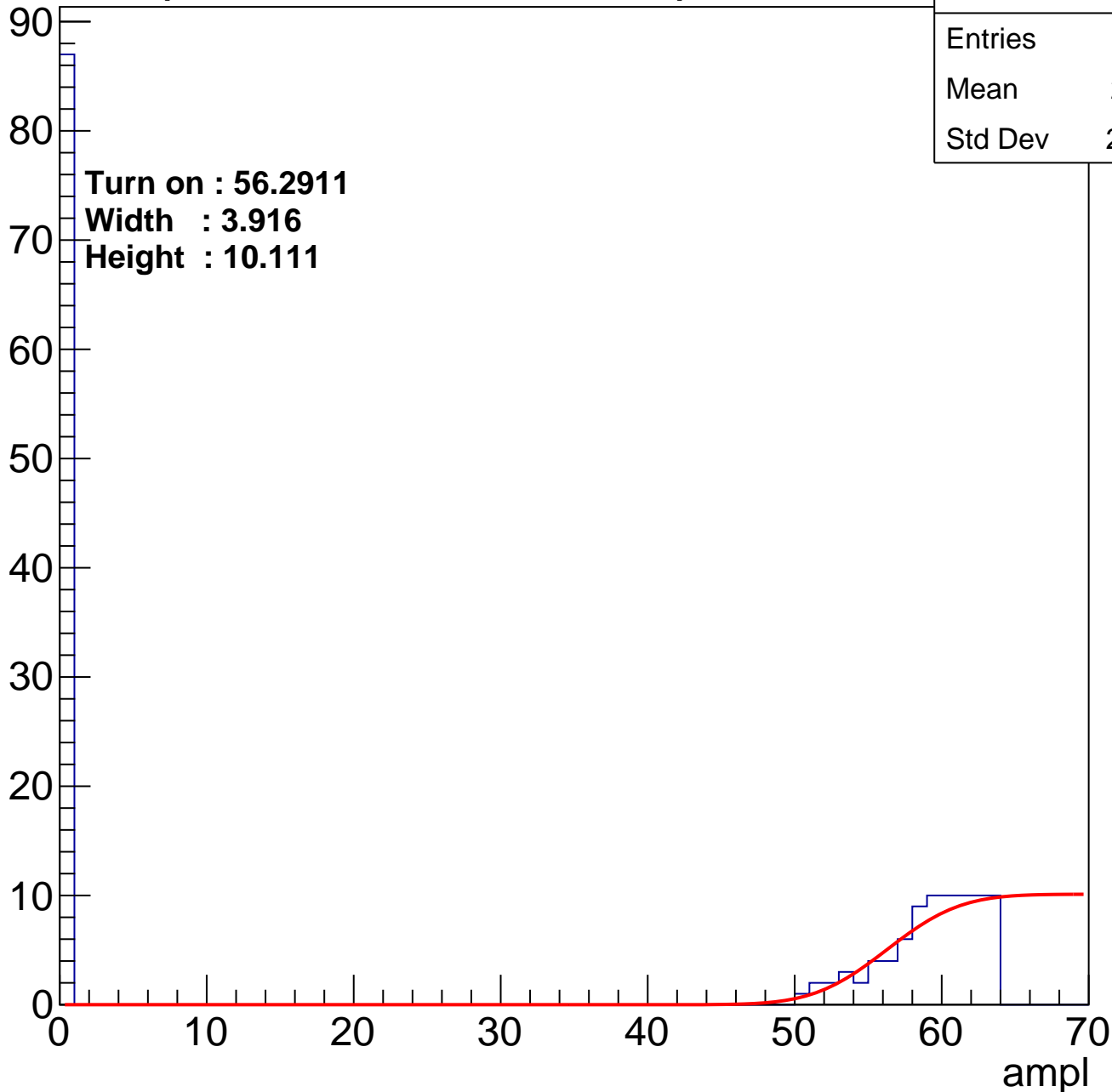
| | |
|---------|-------|
| Entries | 170 |
| Mean | 28.71 |
| Std Dev | 29.48 |

Turn on : 56.2911

Width : 3.916

Height : 10.111

Entry



B1L104S, U6-ch28

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 32.45 |
| Std Dev | 28.59 |

Turn on : 53.1129

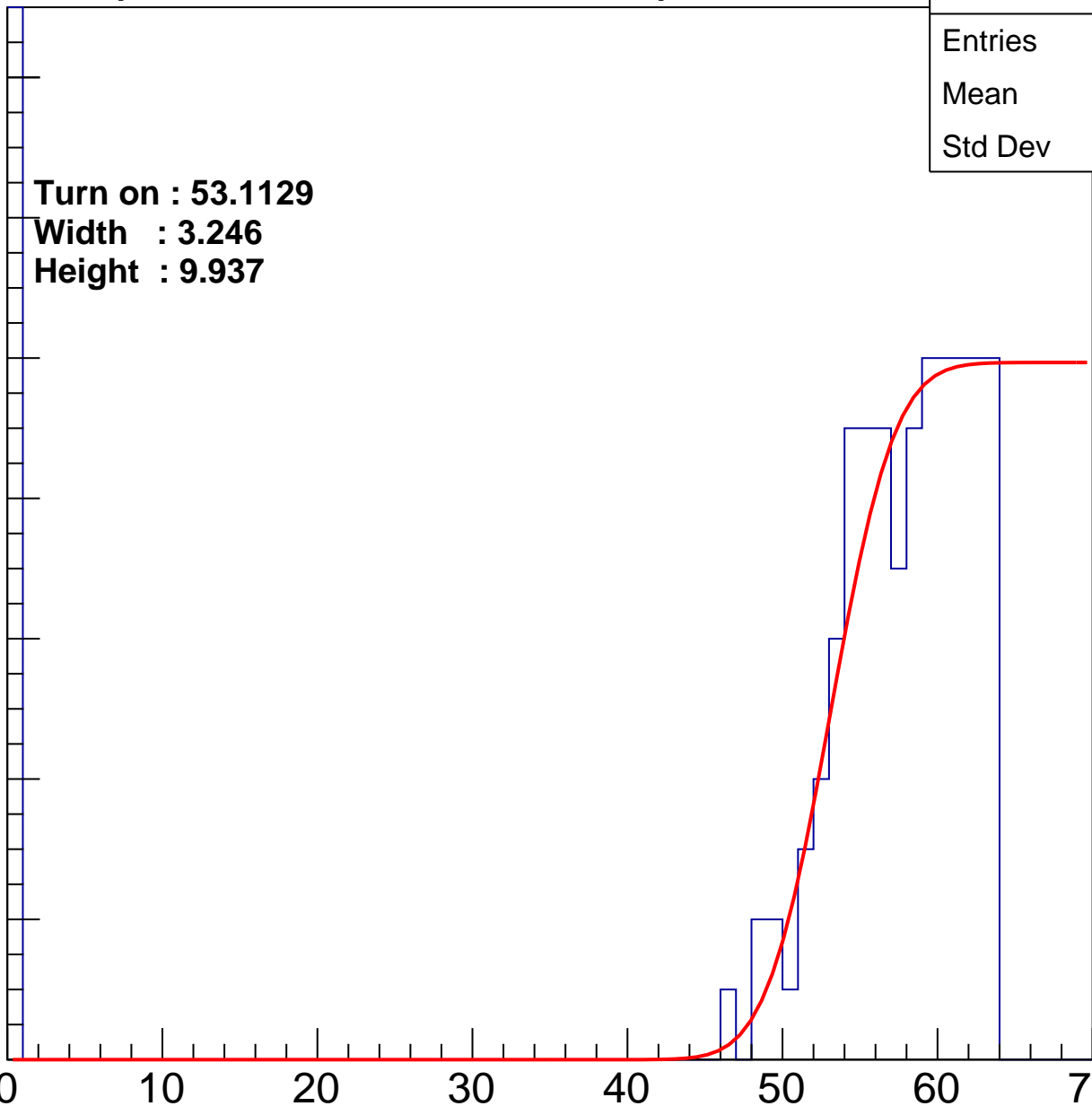
Width : 3.246

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch29

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 166 |
| Mean | 31.09 |
| Std Dev | 29.37 |

Turn on : 55.9447

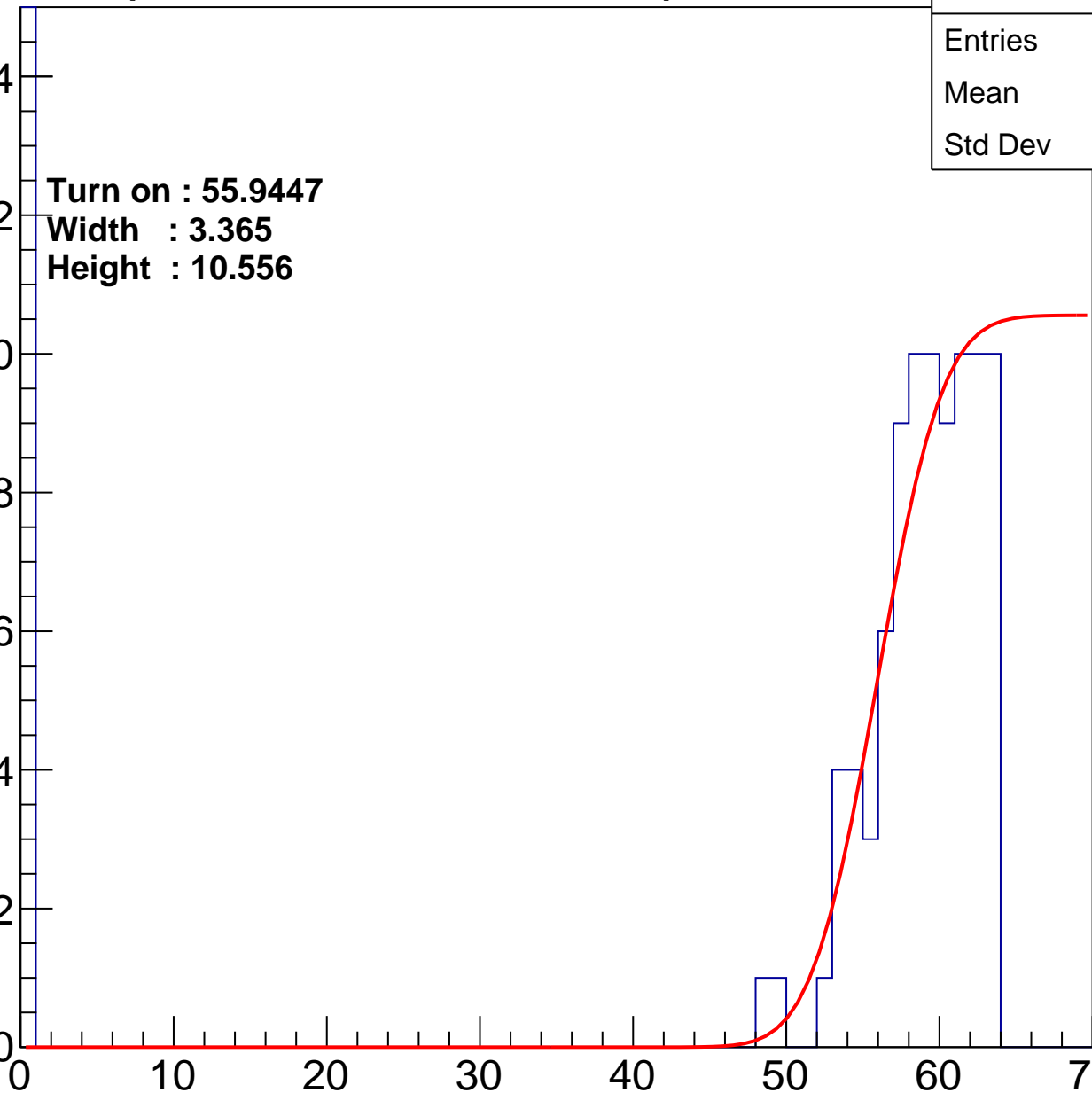
Width : 3.365

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch30

calib_packv5_033123_0516.root, FC#4, port A1

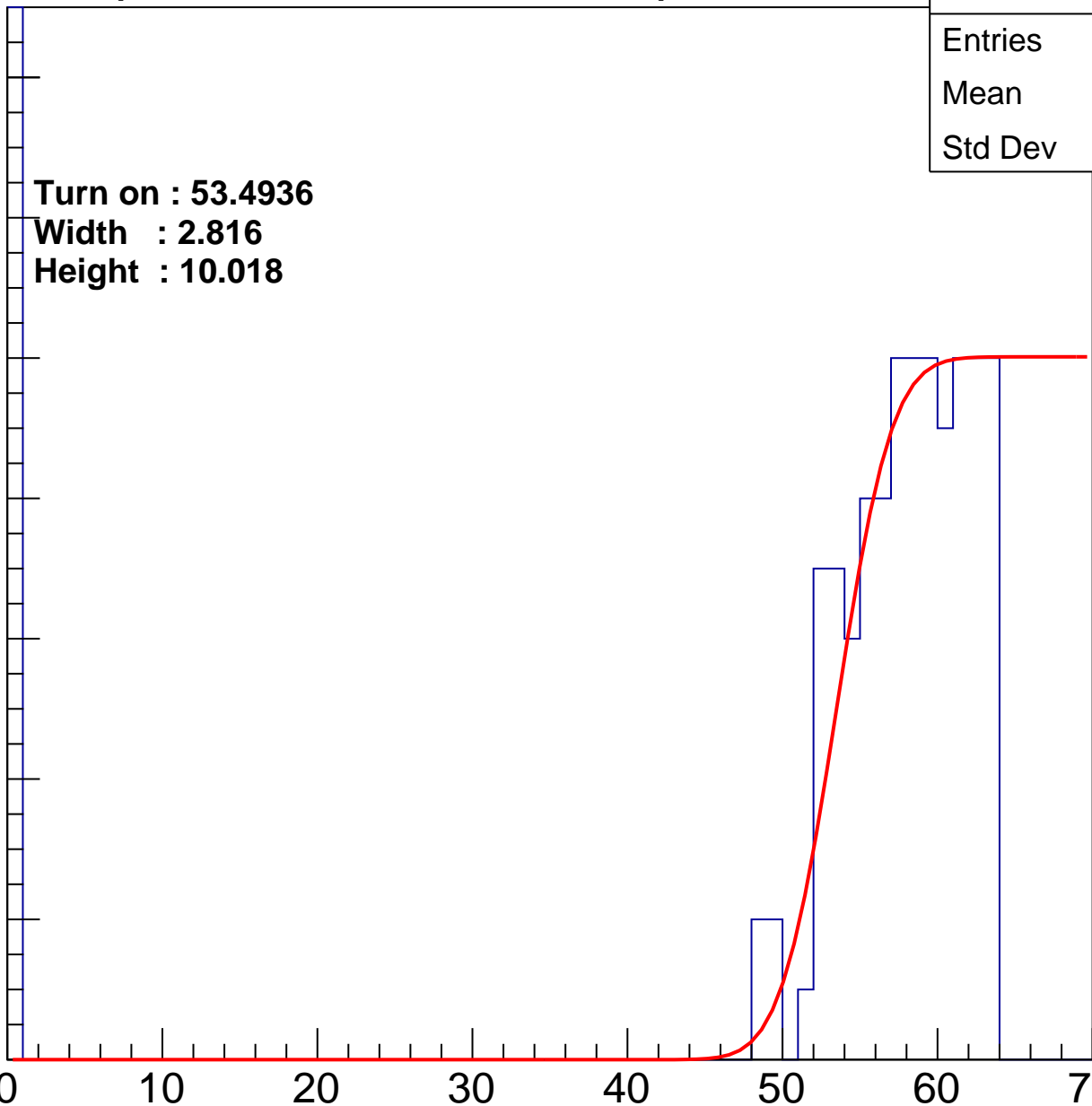
Entry

14
12
10
8
6
4
2
0

Turn on : 53.4936
Width : 2.816
Height : 10.018

| | |
|---------|-------|
| Entries | 221 |
| Mean | 28.65 |
| Std Dev | 28.9 |

ampl



B1L104S, U6-ch31

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 179 |
| Mean | 35.42 |
| Std Dev | 28.21 |

Turn on : 53.8408

Width : 2.825

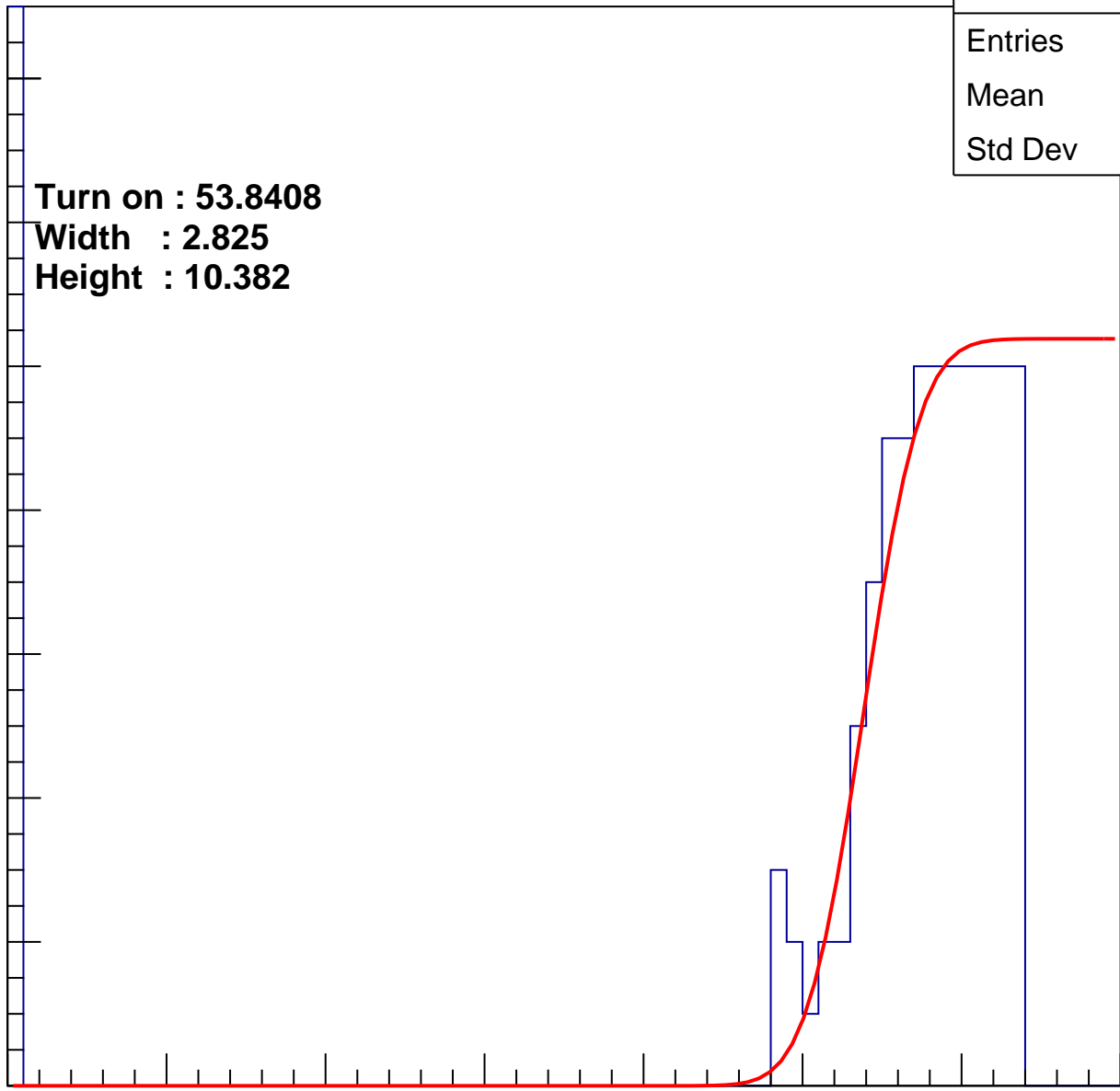
Height : 10.382

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch32

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 209 |
| Mean | 32.51 |
| Std Dev | 28.44 |

Turn on : 53.2638

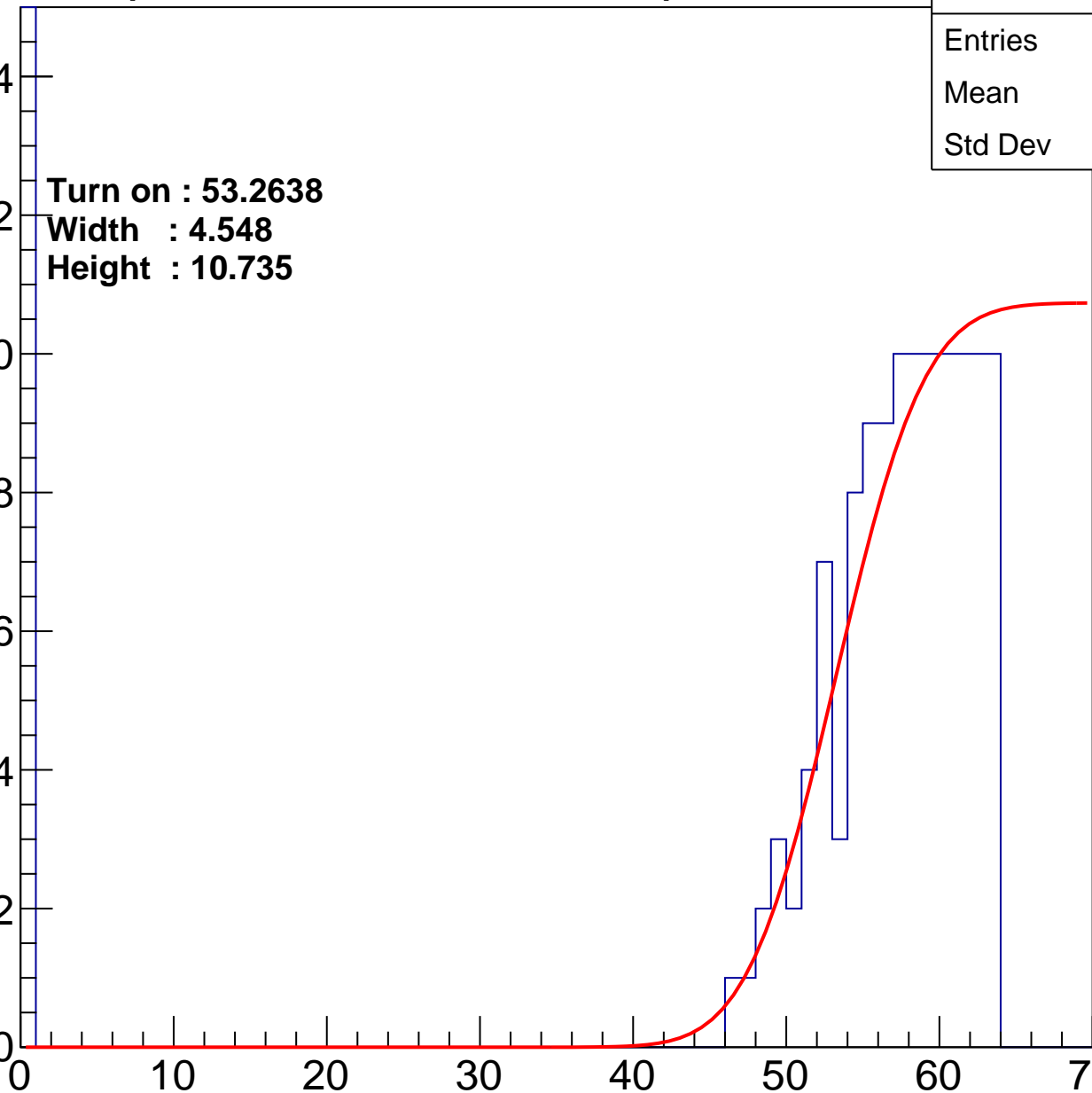
Width : 4.548

Height : 10.735

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch33

calib_packv5_033123_0516.root, FC#4, port A1

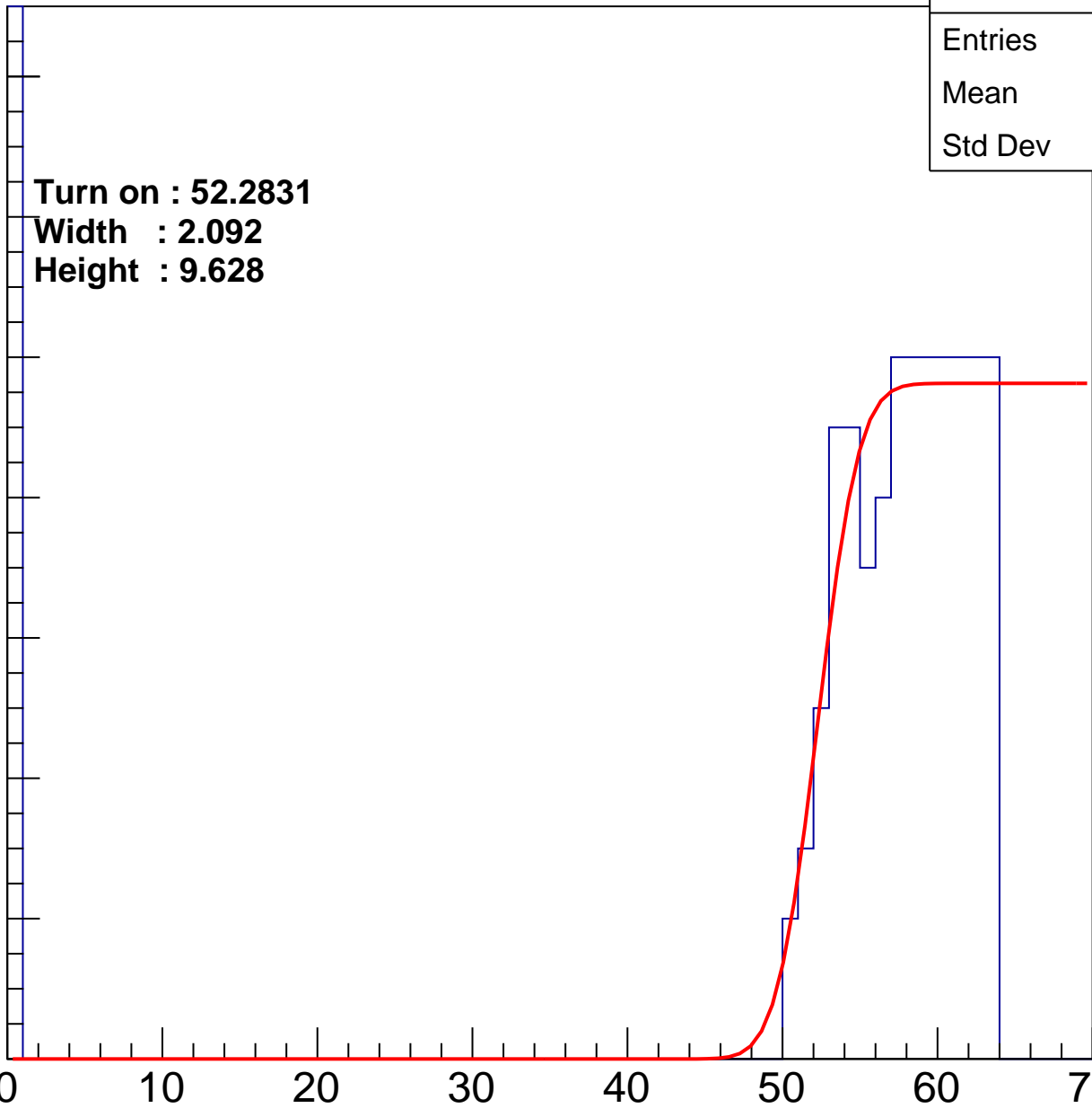
Entry

14
12
10
8
6
4
2
0

Turn on : 52.2831
Width : 2.092
Height : 9.628

| | |
|---------|-------|
| Entries | 210 |
| Mean | 31 |
| Std Dev | 28.84 |

ampl



B1L104S, U6-ch34

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 201 |
| Mean | 35.34 |
| Std Dev | 27.76 |

Turn on : 52.5089

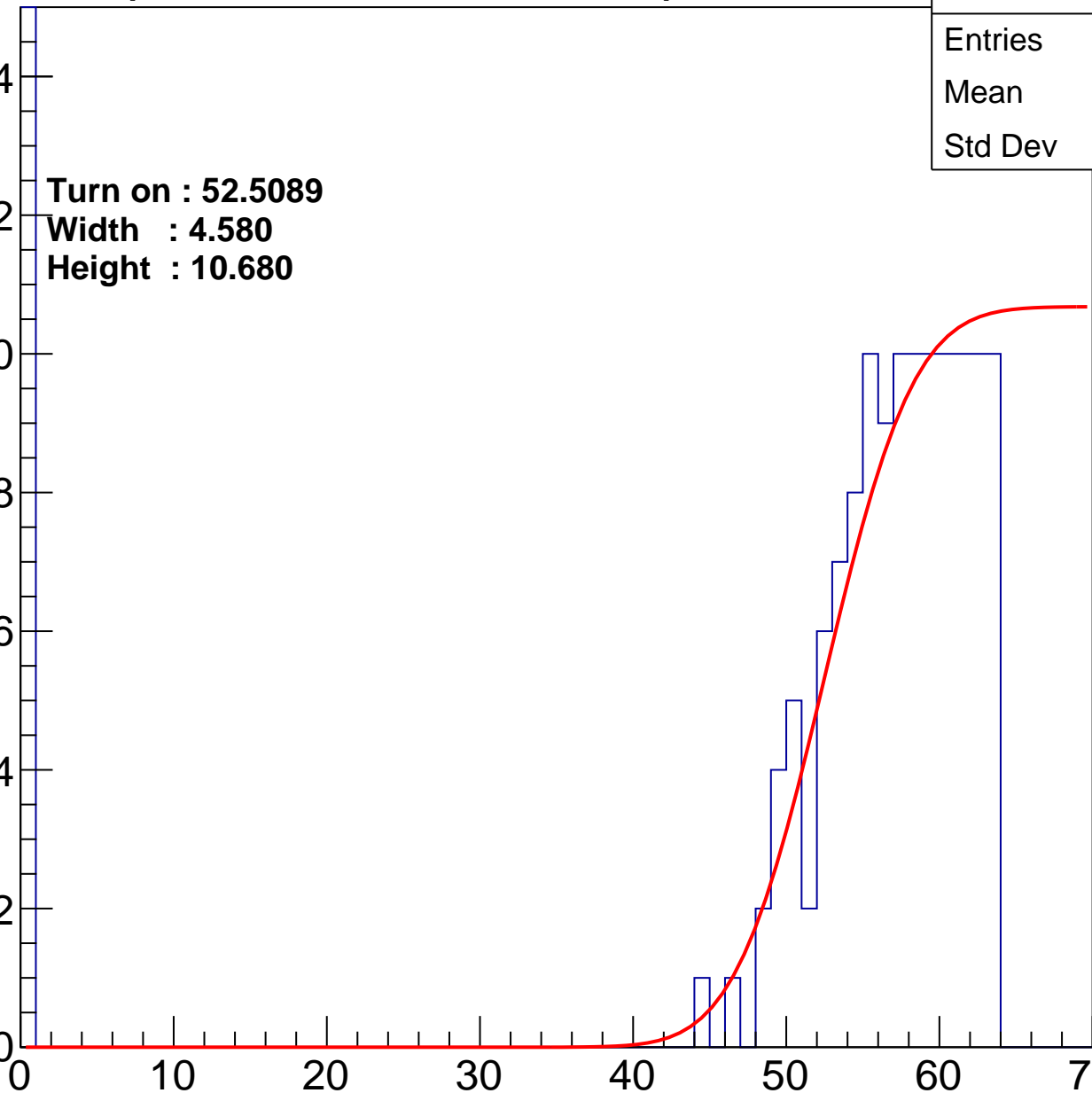
Width : 4.580

Height : 10.680

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch35

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 169 |
| Mean | 31.2 |
| Std Dev | 29.32 |

Turn on : 55.5740

Width : 3.749

Height : 10.667

Entry

14

12

10

8

6

4

2

0

0

10

20

30

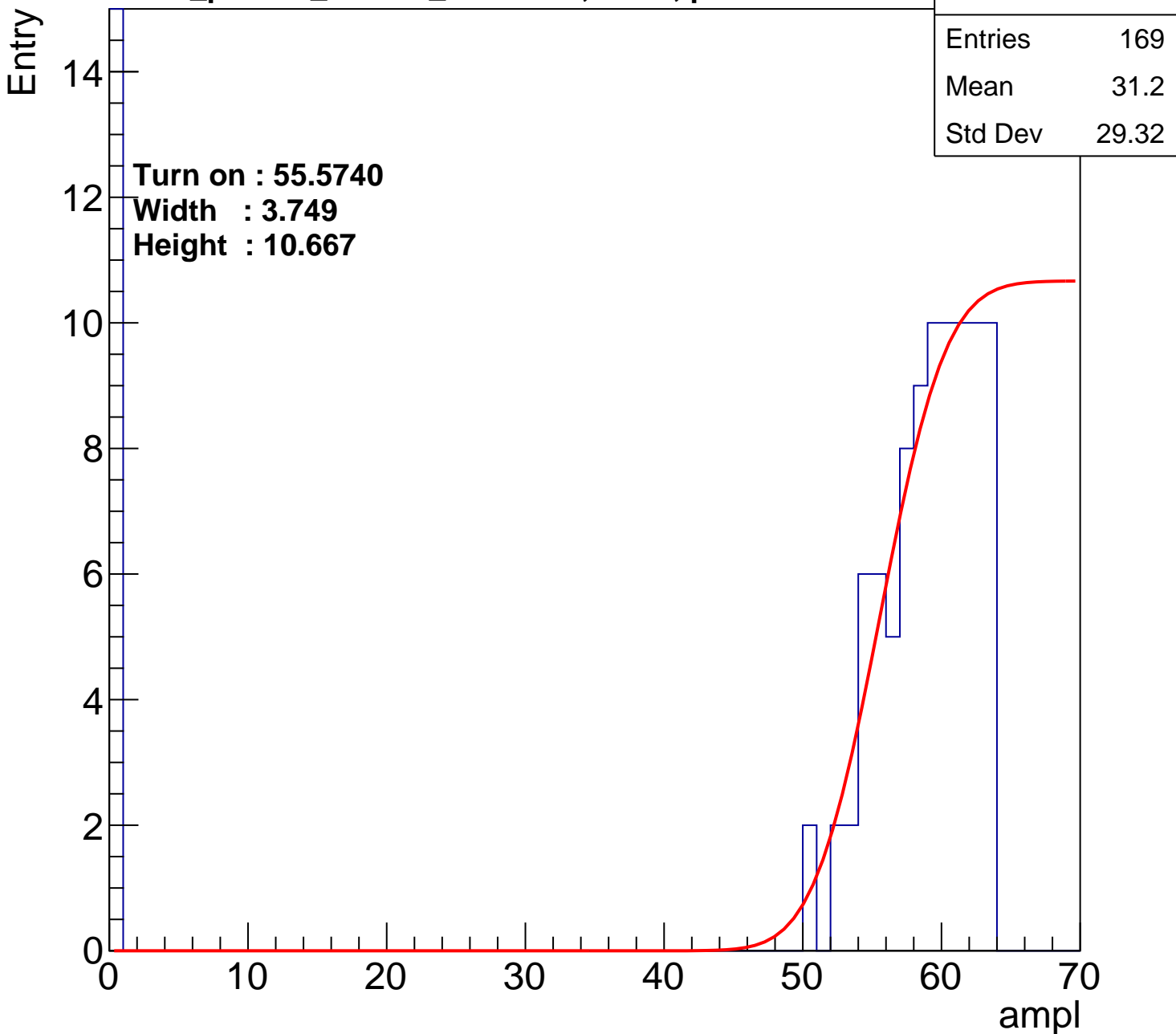
40

50

60

70

ampl



B1L104S, U6-ch36

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 197 |
| Mean | 35.99 |
| Std Dev | 27.54 |

Turn on : 51.4649

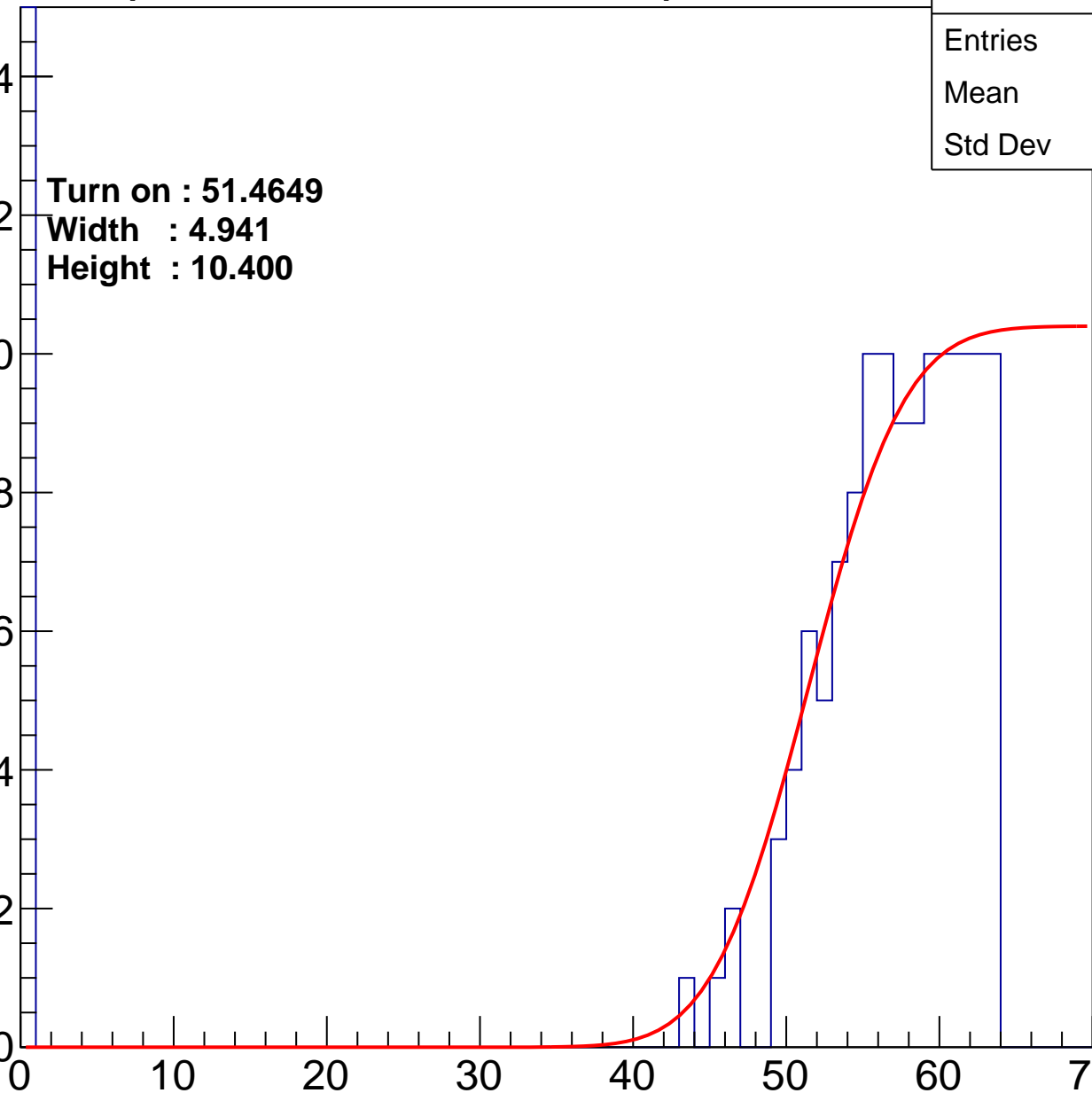
Width : 4.941

Height : 10.400

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch37

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 202 |
| Mean | 29.39 |
| Std Dev | 29.19 |

Turn on : 54.1990

Width : 1.806

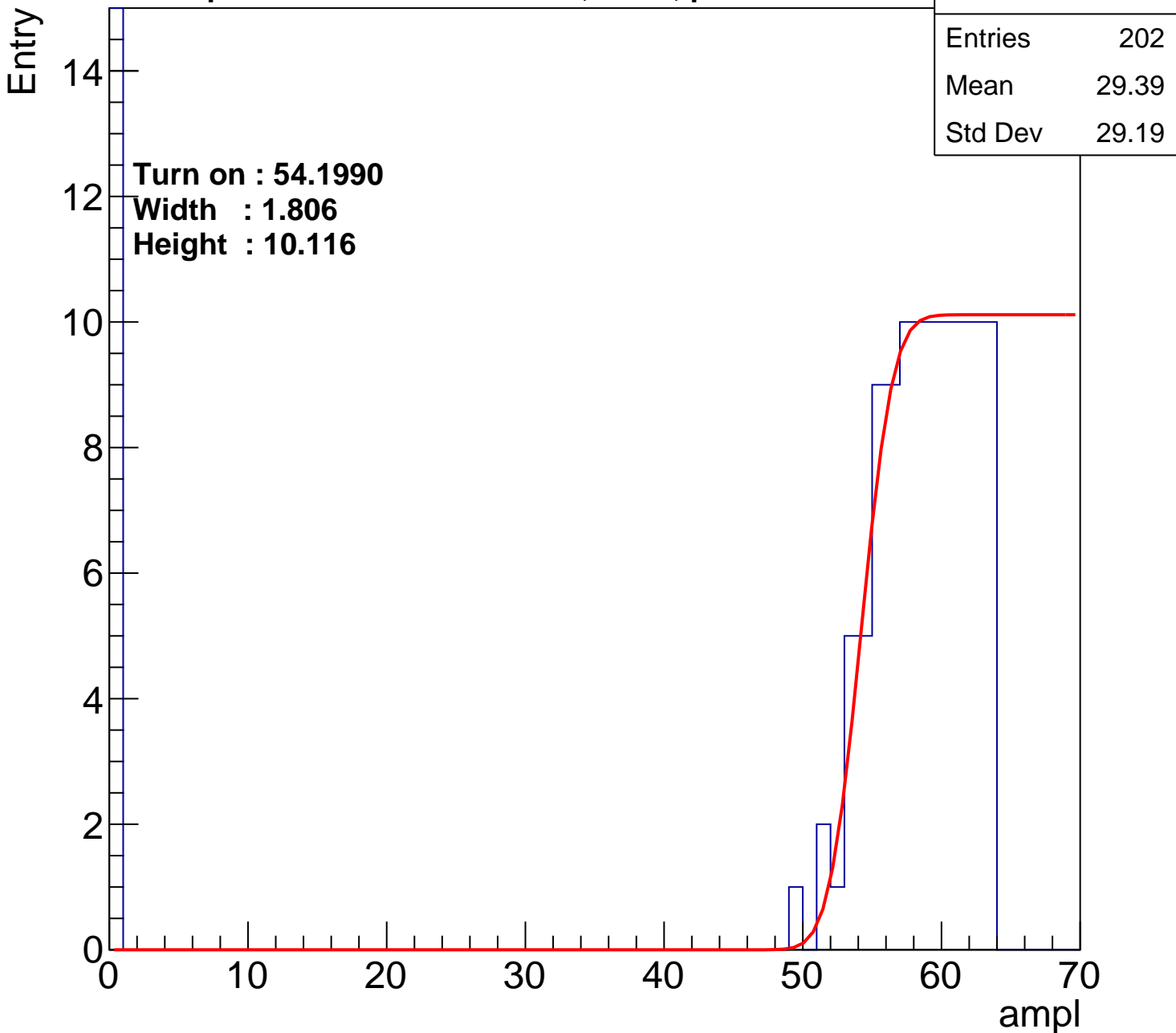
Height : 10.116

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch38

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 177 |
| Mean | 39.11 |
| Std Dev | 26.51 |

Turn on : 51.9708

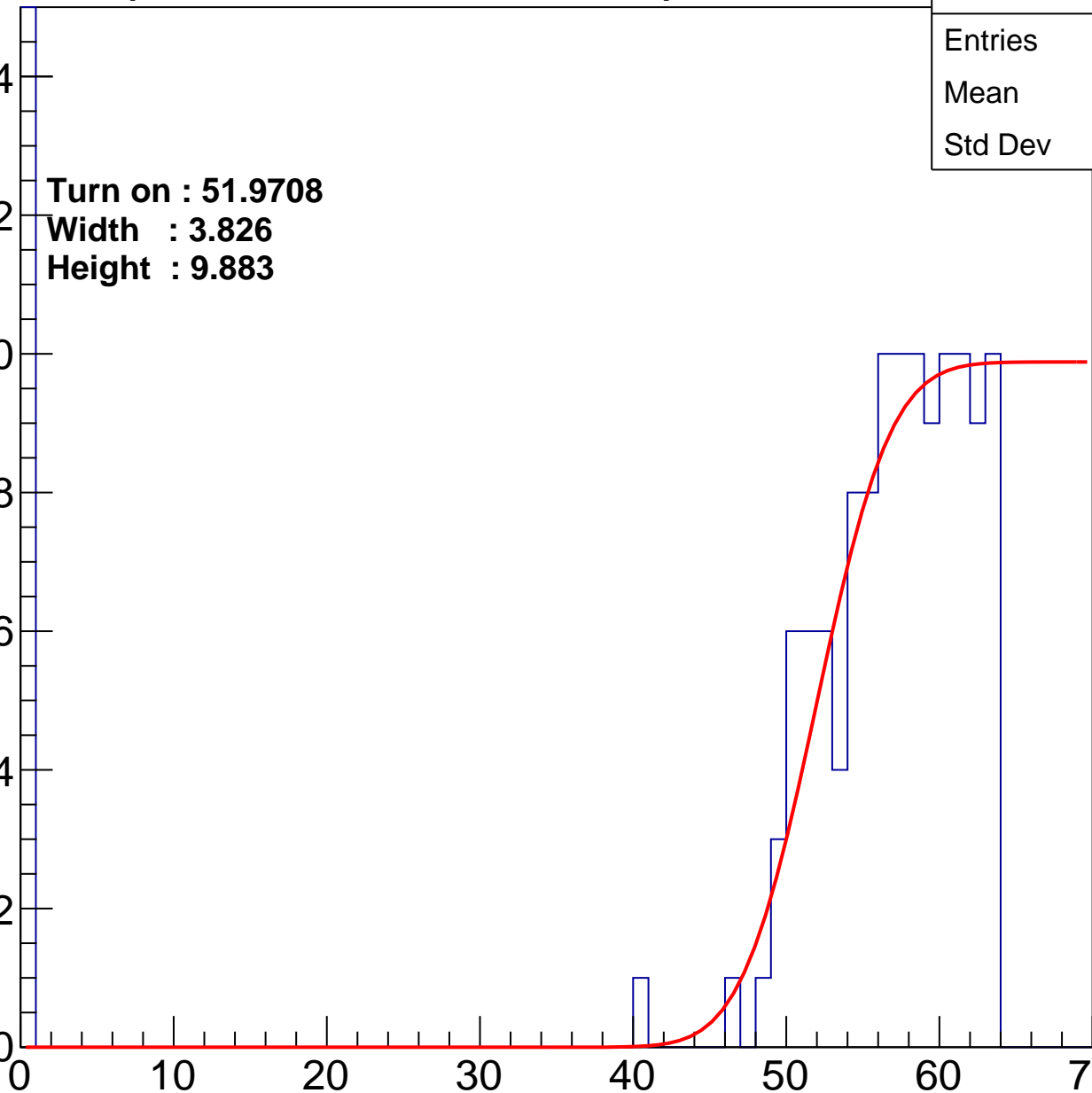
Width : 3.826

Height : 9.883

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch39

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 203 |
| Mean | 33.11 |
| Std Dev | 28.28 |

Turn on : 52.8826

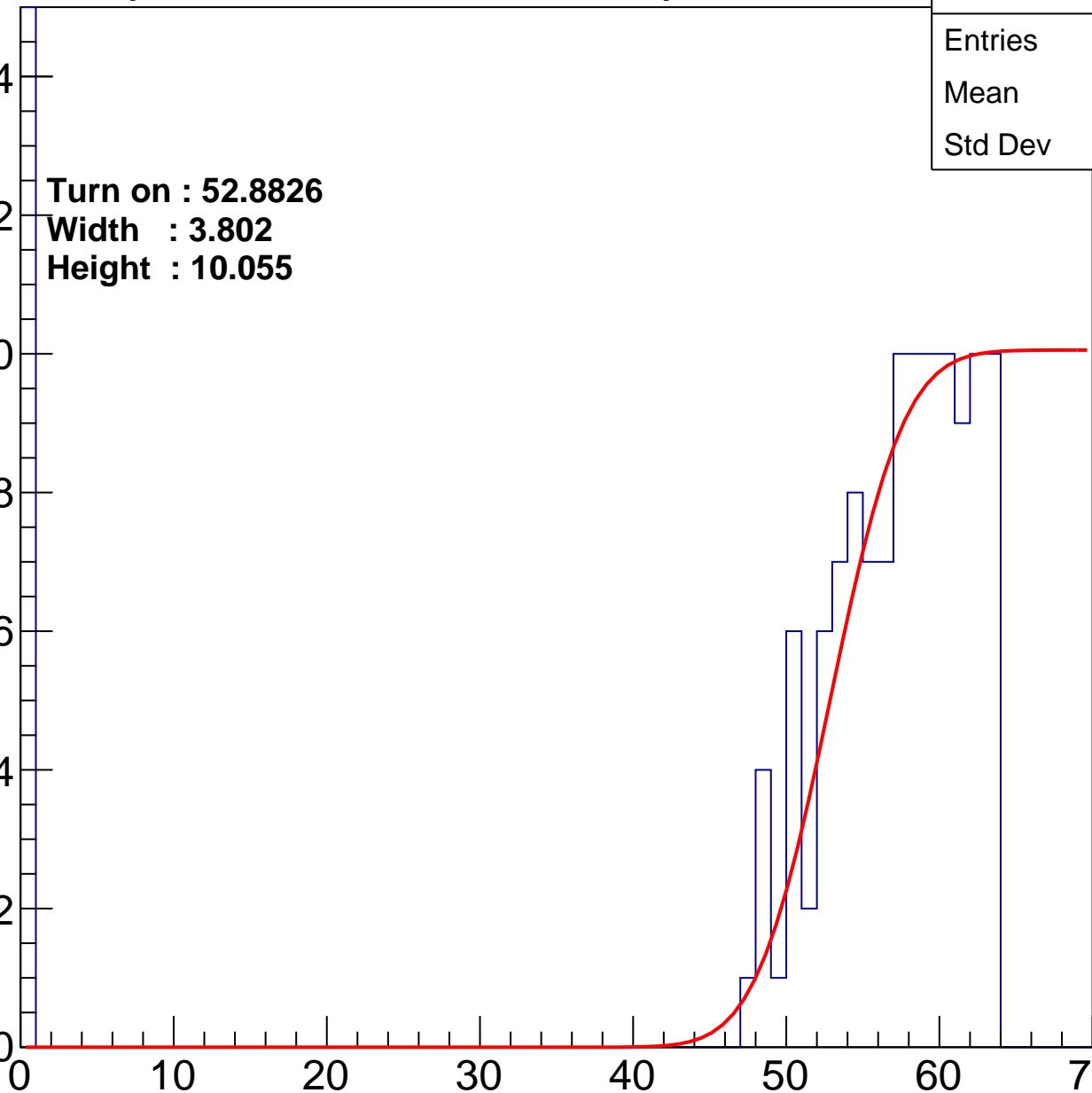
Width : 3.802

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch40

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 180 |
| Mean | 36.52 |
| Std Dev | 27.66 |

Turn on : 52.4807

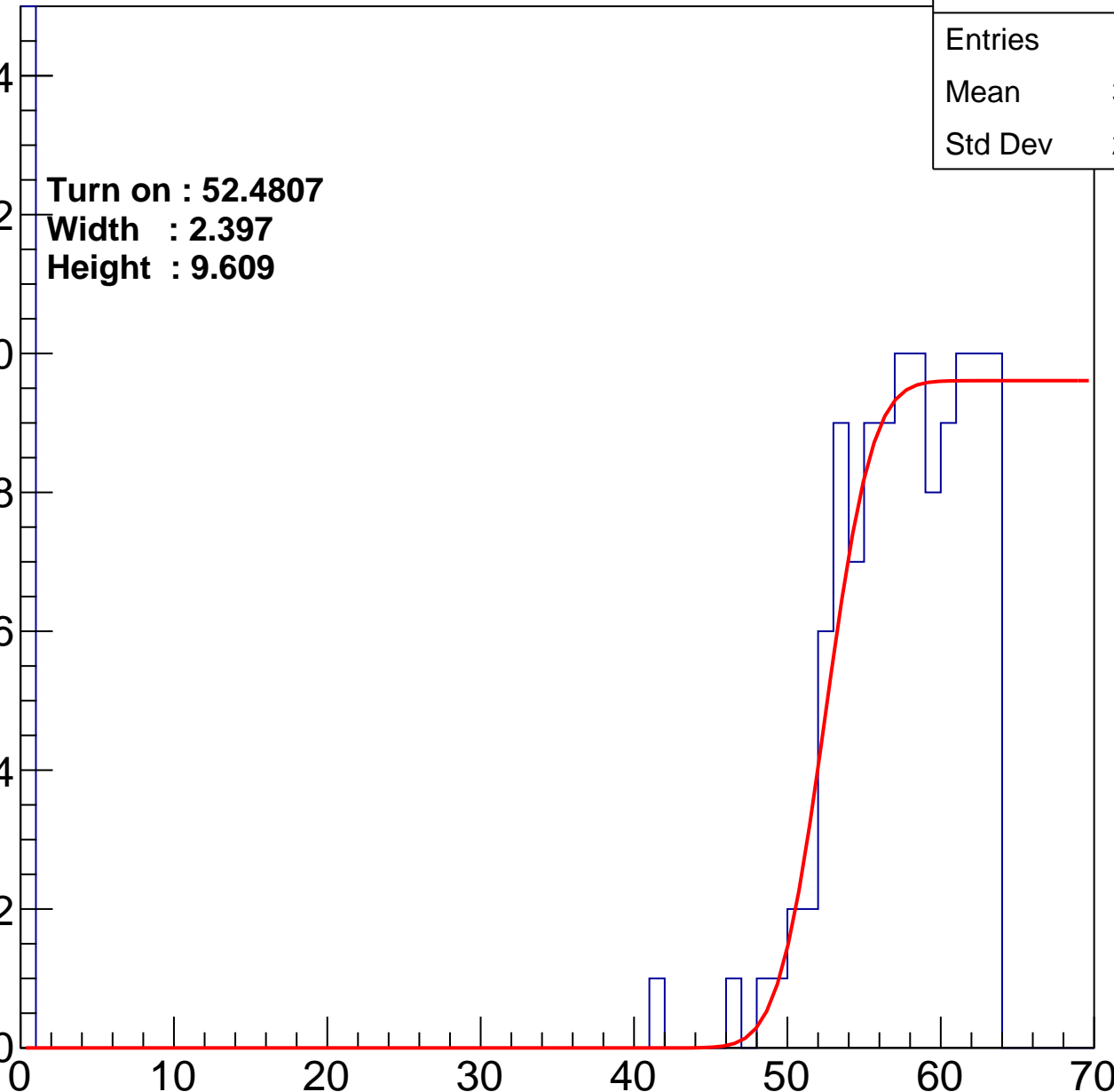
Width : 2.397

Height : 9.609

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch41

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 158 |
| Mean | 31.96 |
| Std Dev | 29.33 |

Turn on : 55.8906

Width : 3.504

Height : 10.222

Entry

70

60

50

40

30

20

10

0

0

10

20

30

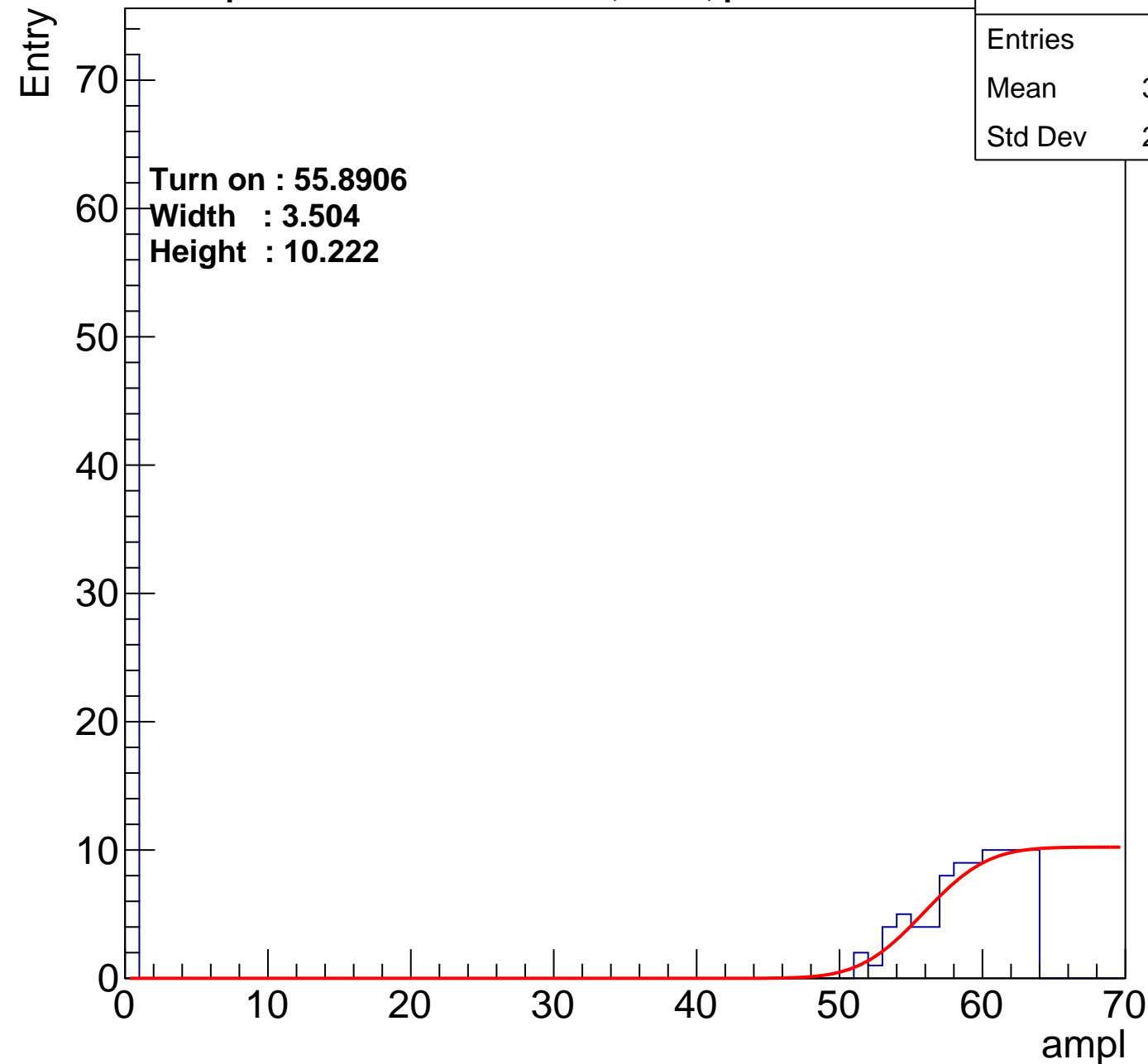
40

50

60

70

ampl



B1L104S, U6-ch42

calib_packv5_033123_0516.root, FC#4, port A1

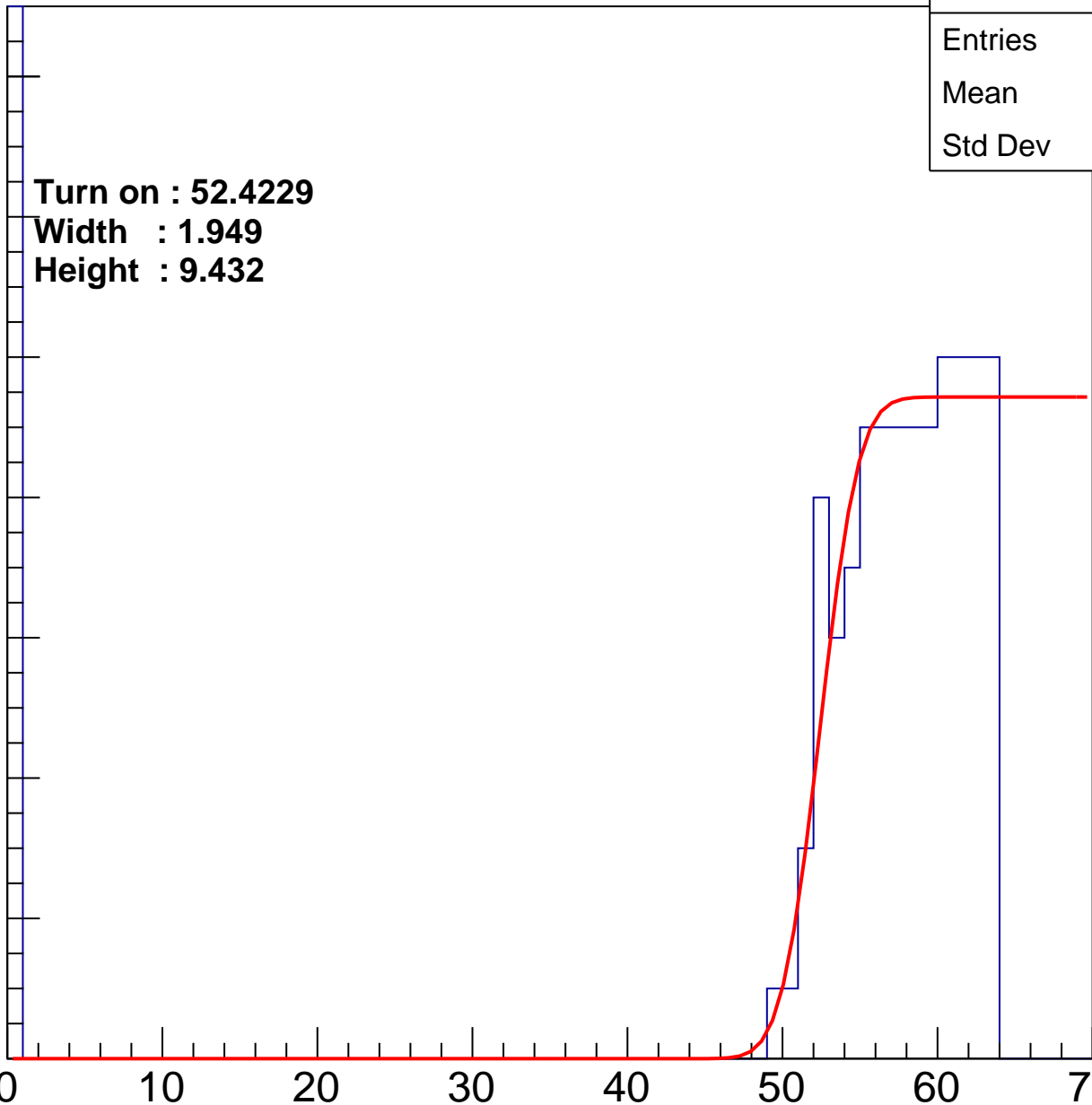
Entry

14
12
10
8
6
4
2
0

Turn on : 52.4229
Width : 1.949
Height : 9.432

| | |
|---------|-------|
| Entries | 213 |
| Mean | 30 |
| Std Dev | 28.88 |

ampl



B1L104S, U6-ch43

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 167 |
| Mean | 37.7 |
| Std Dev | 27.66 |

Turn on : 53.7643

Width : 3.406

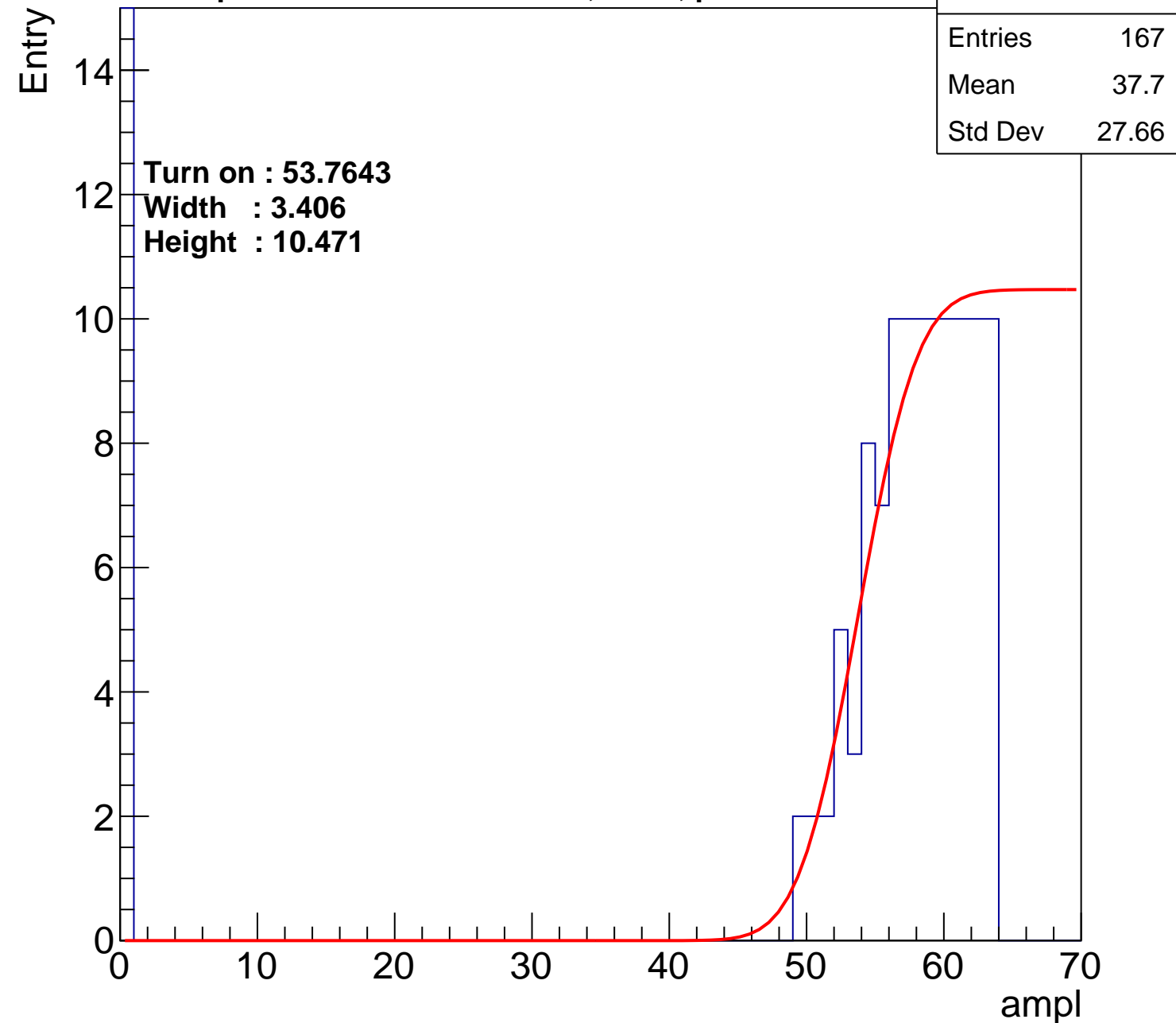
Height : 10.471

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch44

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 218 |
| Mean | 37.19 |
| Std Dev | 26.66 |

Turn on : 49.7825

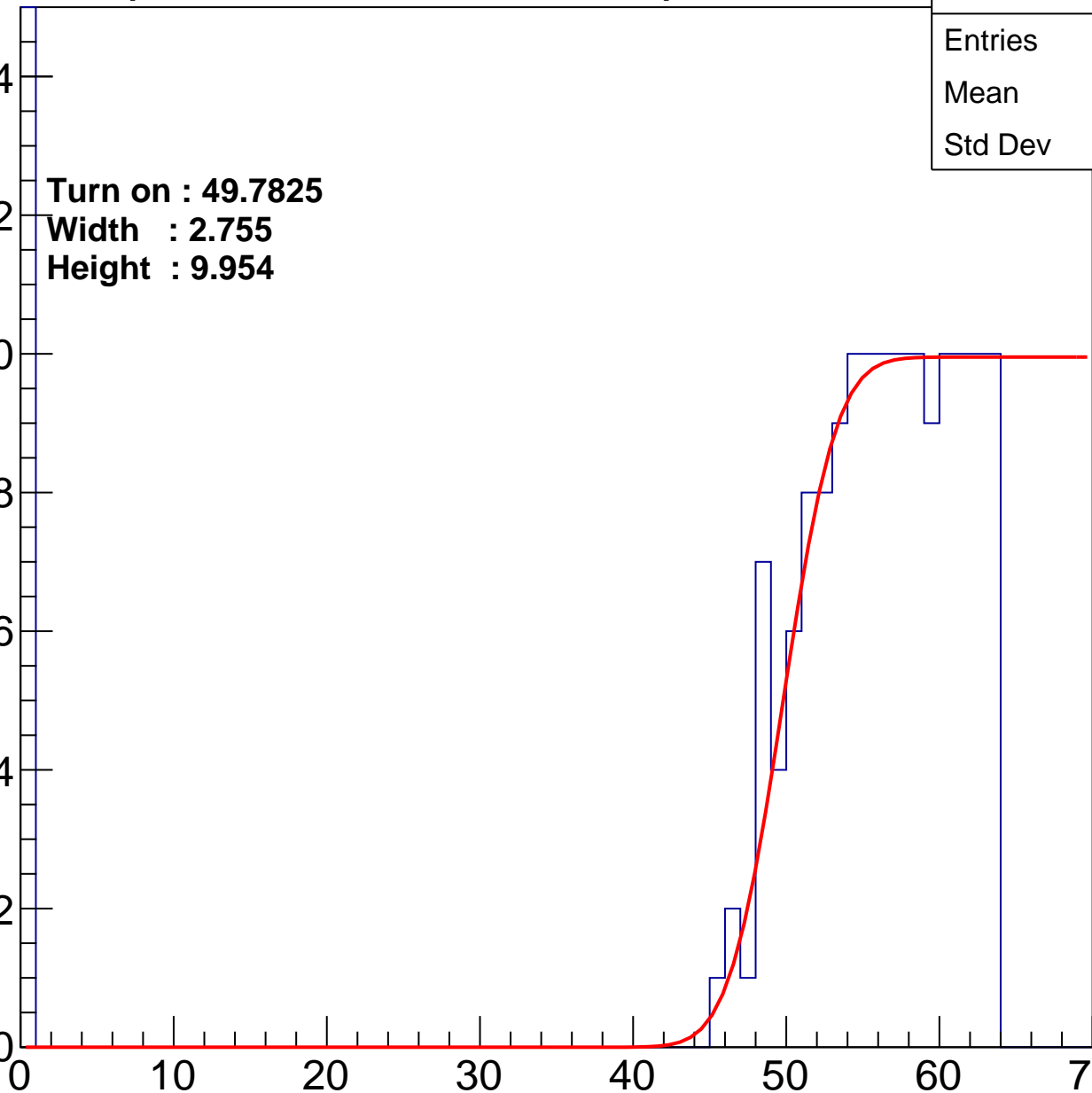
Width : 2.755

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch45

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 174 |
| Mean | 34.57 |
| Std Dev | 28.5 |

Turn on : 53.2657

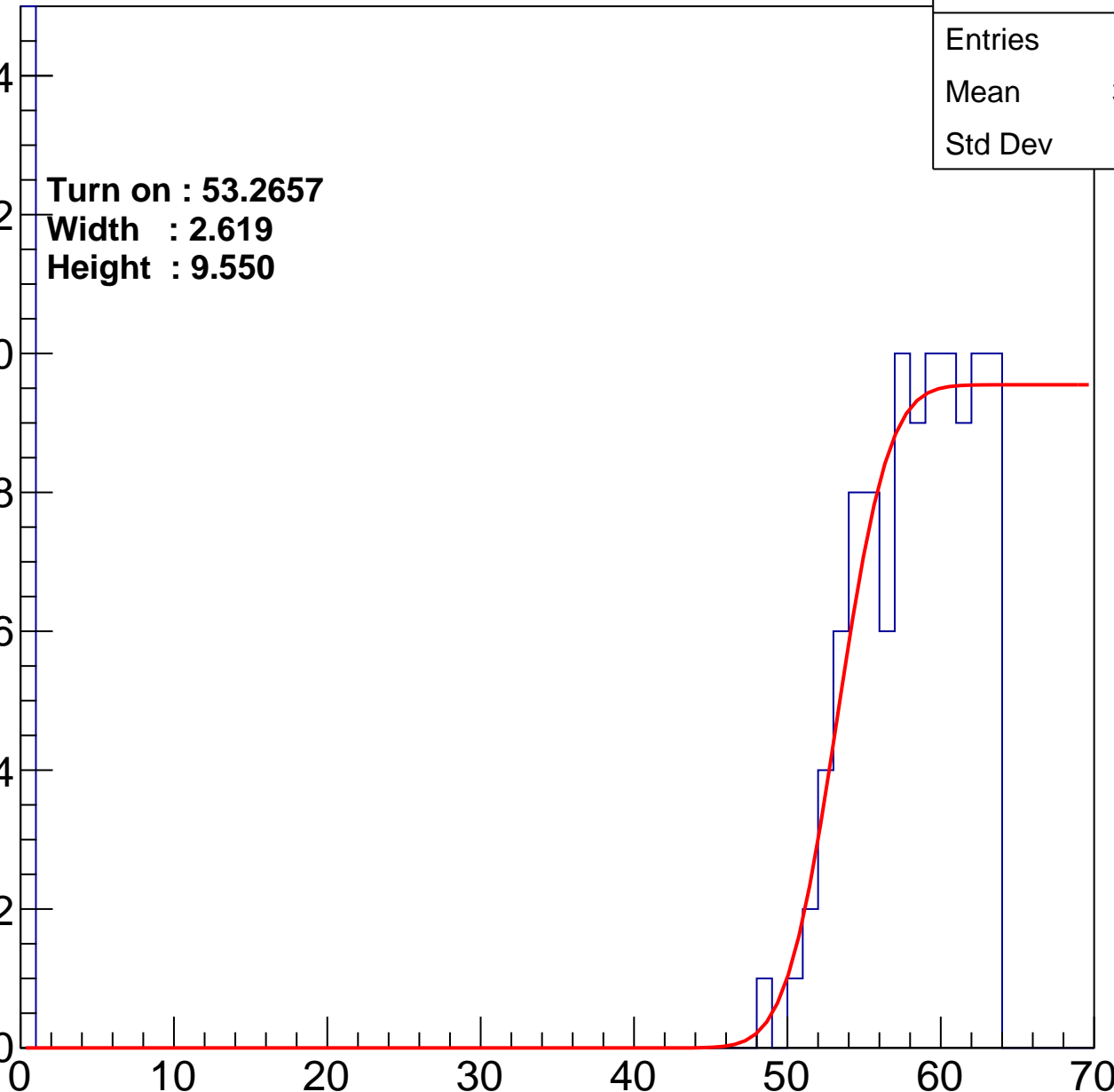
Width : 2.619

Height : 9.550

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch46

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 174 |
| Mean | 31.68 |
| Std Dev | 29.02 |

Turn on : 55.6195

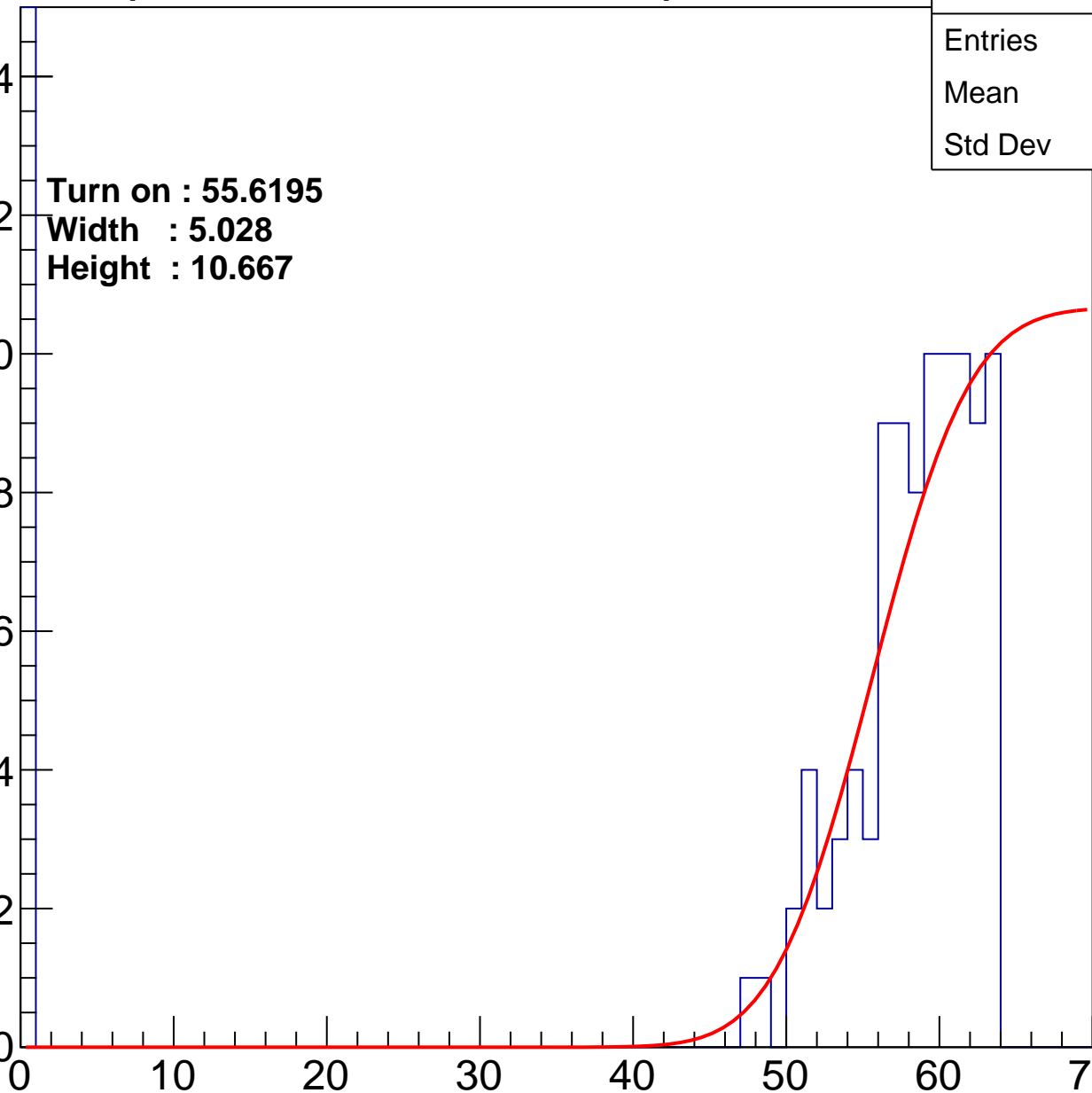
Width : 5.028

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch47

calib_packv5_033123_0516.root, FC#4, port A1

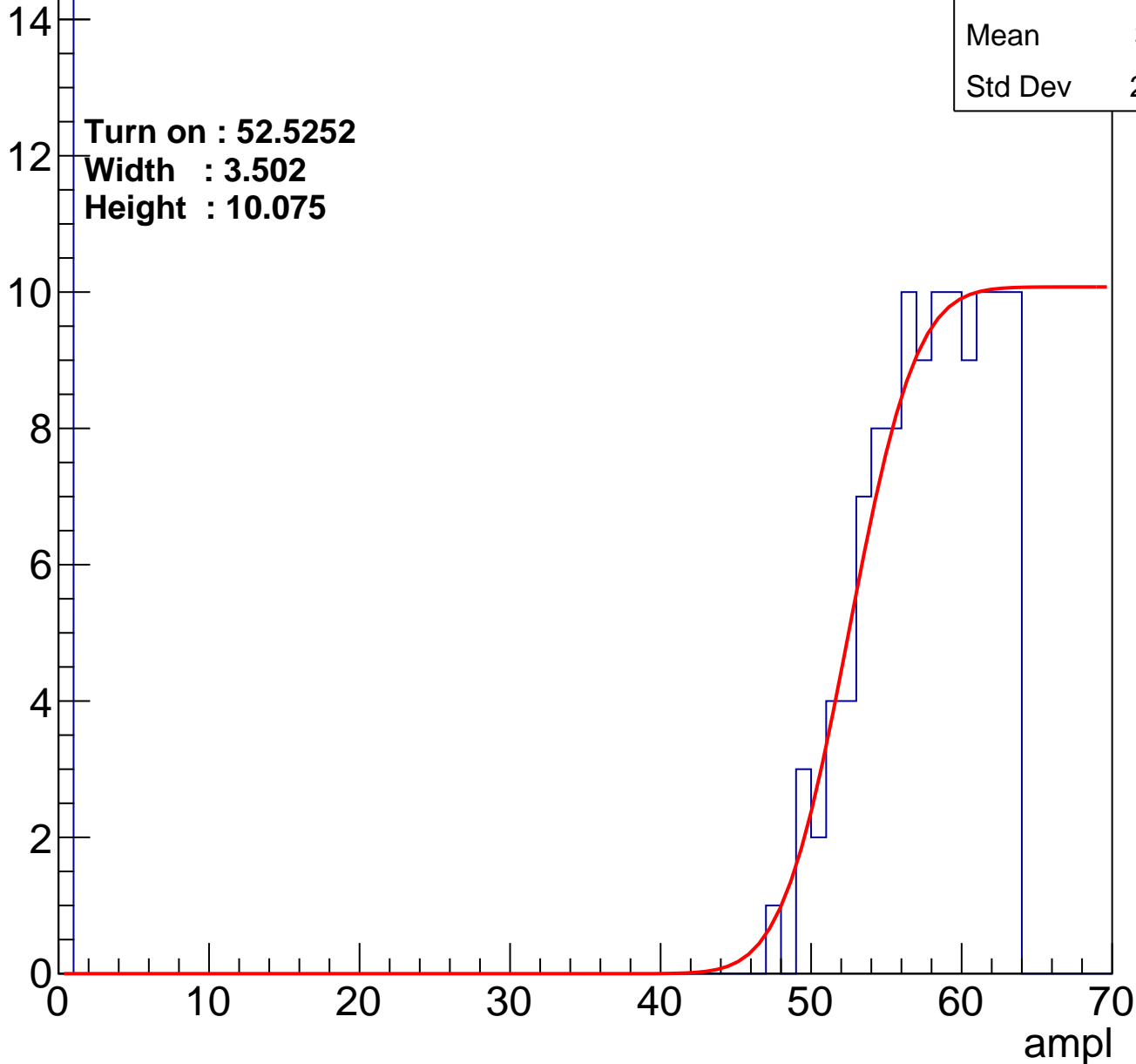
Entry

| | |
|---------|-------|
| Entries | 195 |
| Mean | 33.81 |
| Std Dev | 28.35 |

Turn on : 52.5252

Width : 3.502

Height : 10.075



B1L104S, U6-ch48

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 205 |
| Mean | 29.05 |
| Std Dev | 29.03 |

Turn on : 54.0771

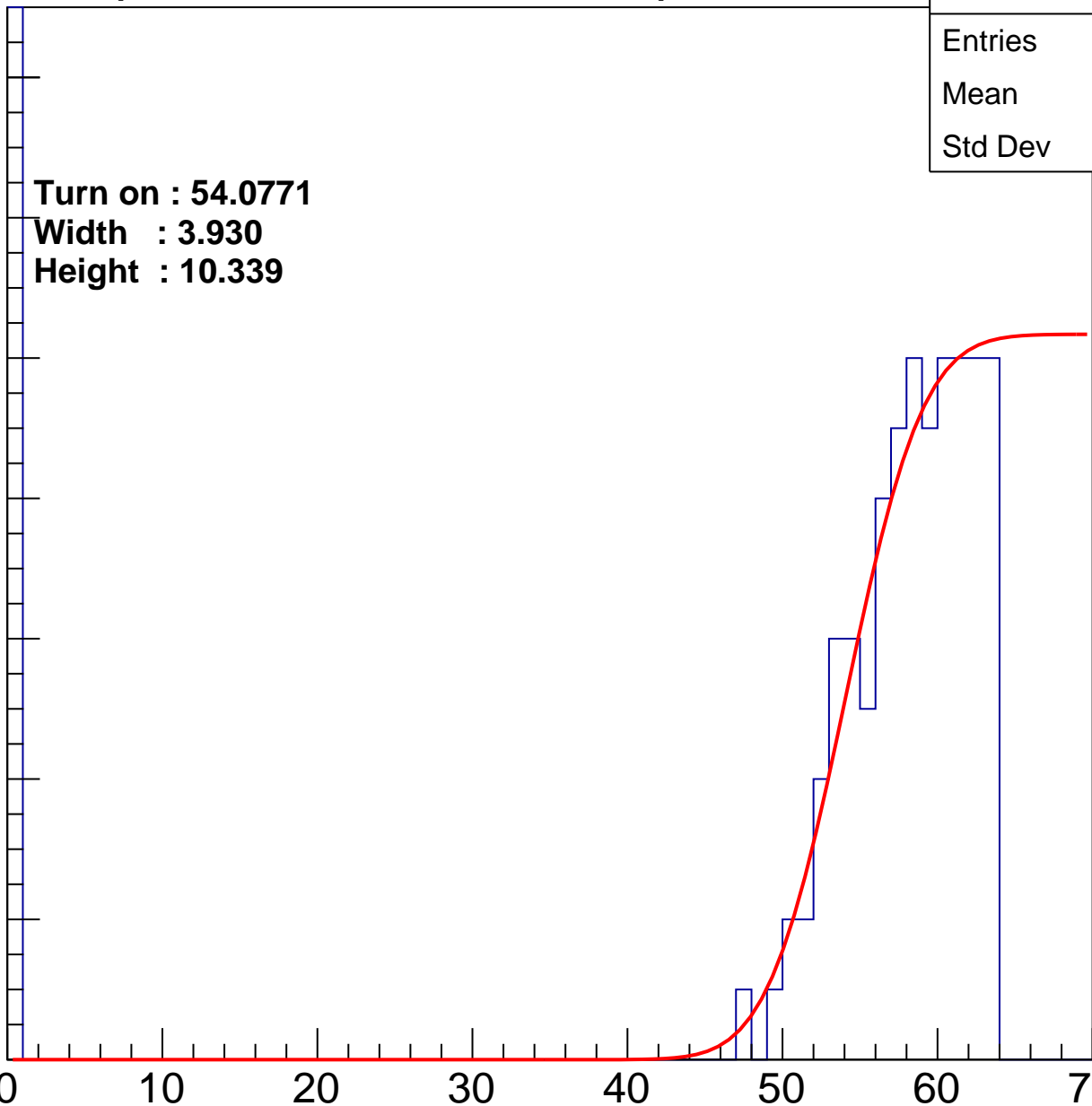
Width : 3.930

Height : 10.339

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch49

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 35.47 |
| Std Dev | 27.87 |

Turn on : 51.8093

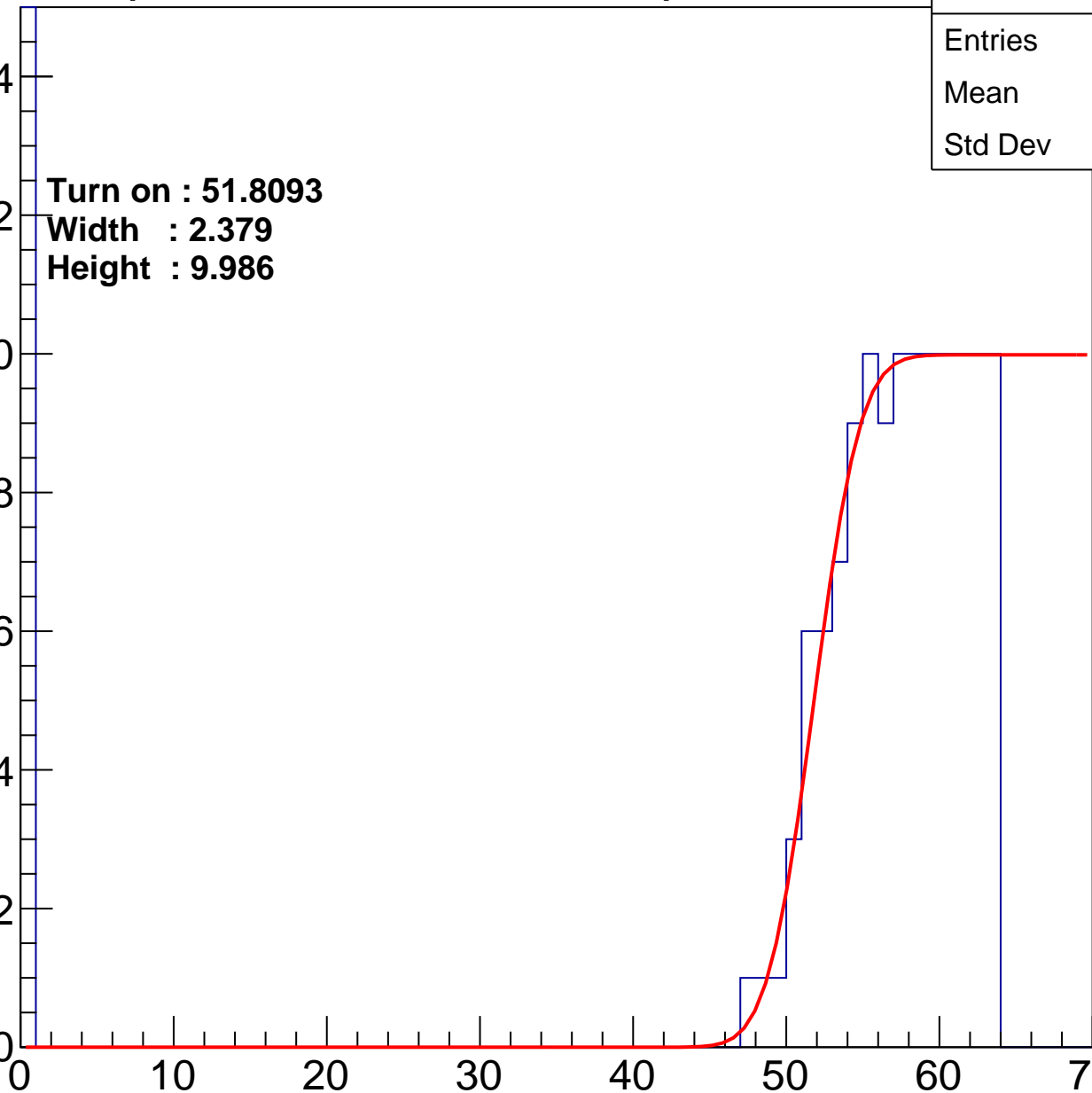
Width : 2.379

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch50

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 206 |
| Mean | 31.12 |
| Std Dev | 28.69 |

Turn on : 54.0518

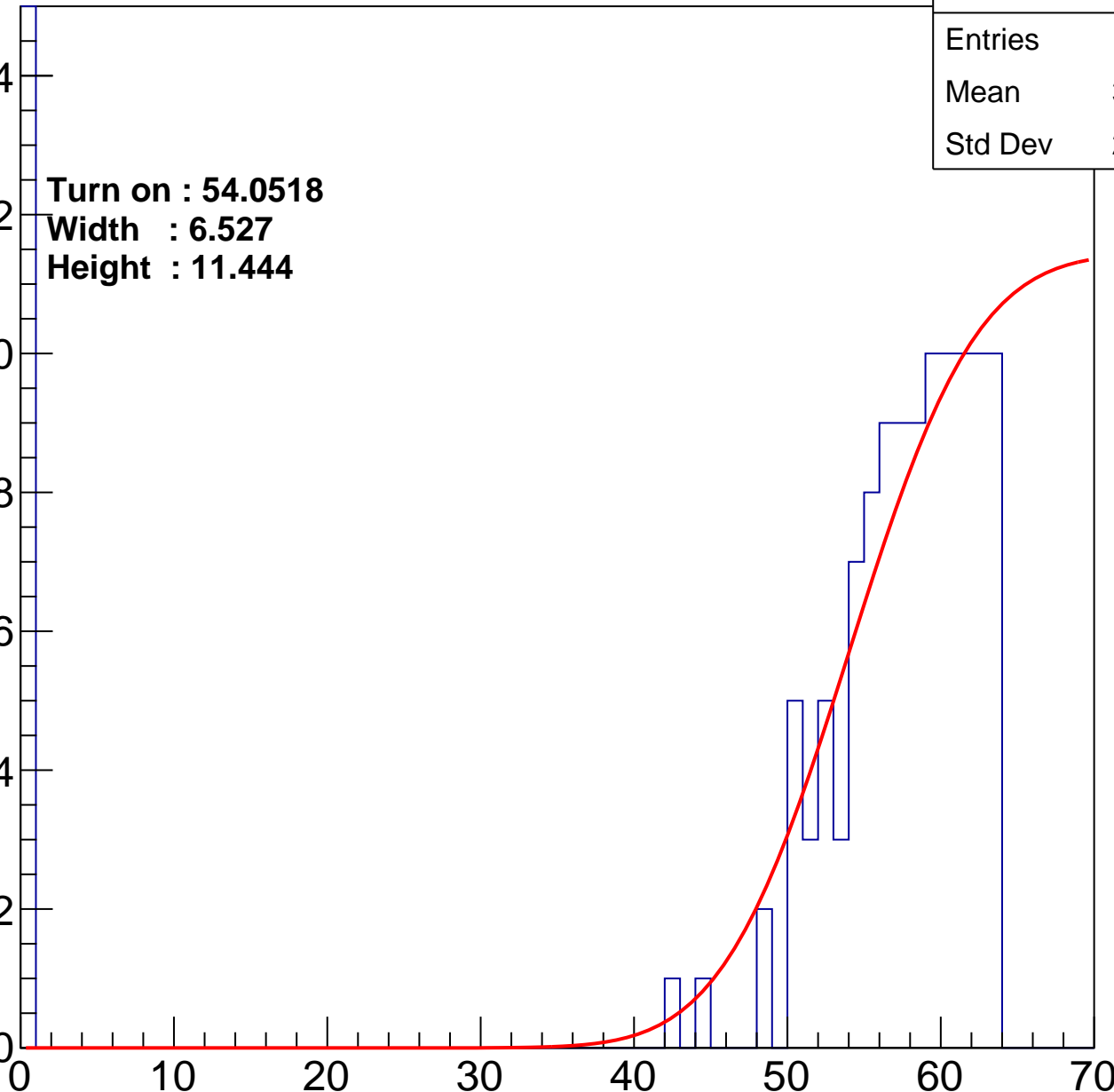
Width : 6.527

Height : 11.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch51

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 225 |
| Mean | 31 |
| Std Dev | 28.62 |

Turn on : 52.0238

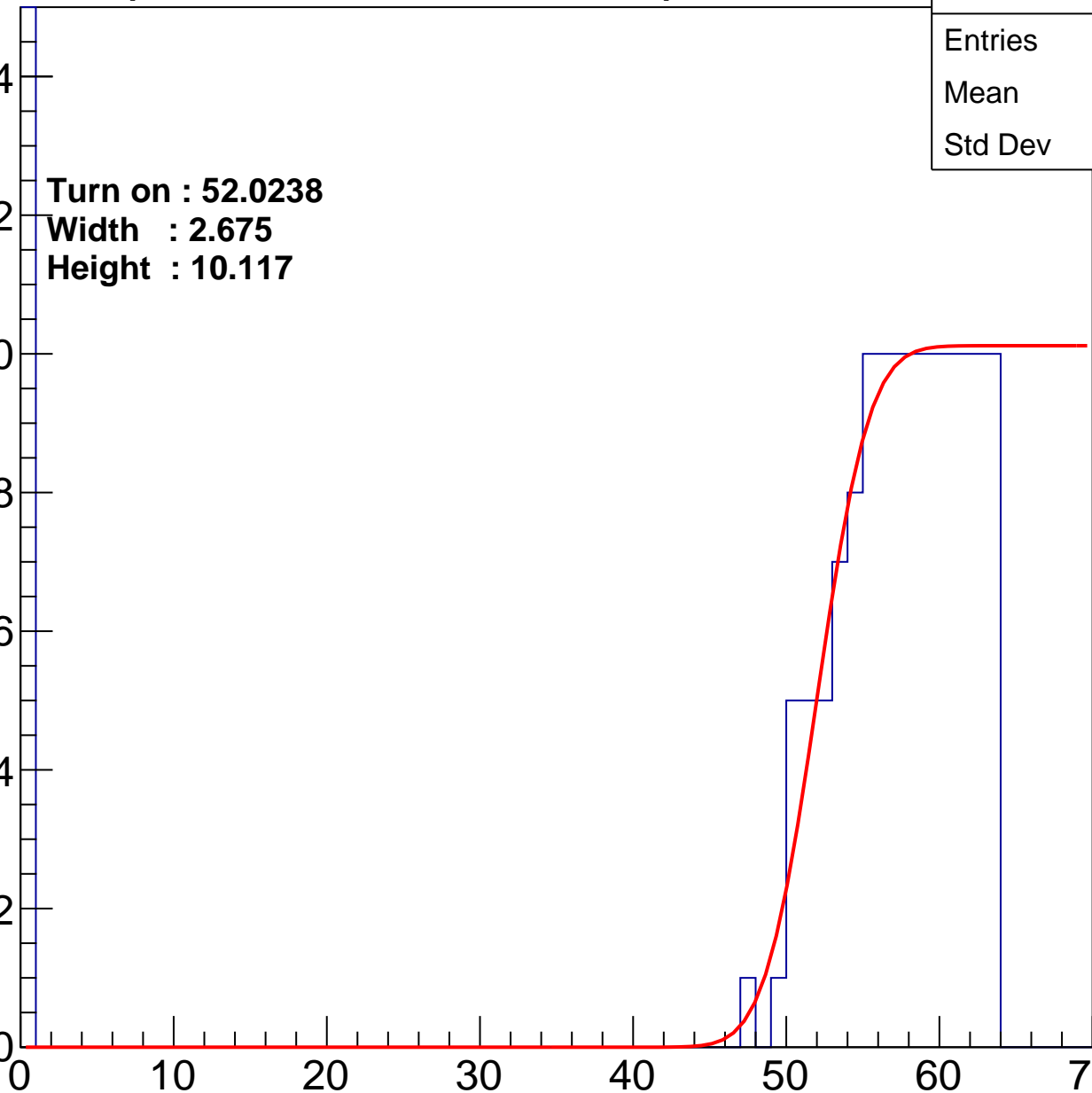
Width : 2.675

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch52

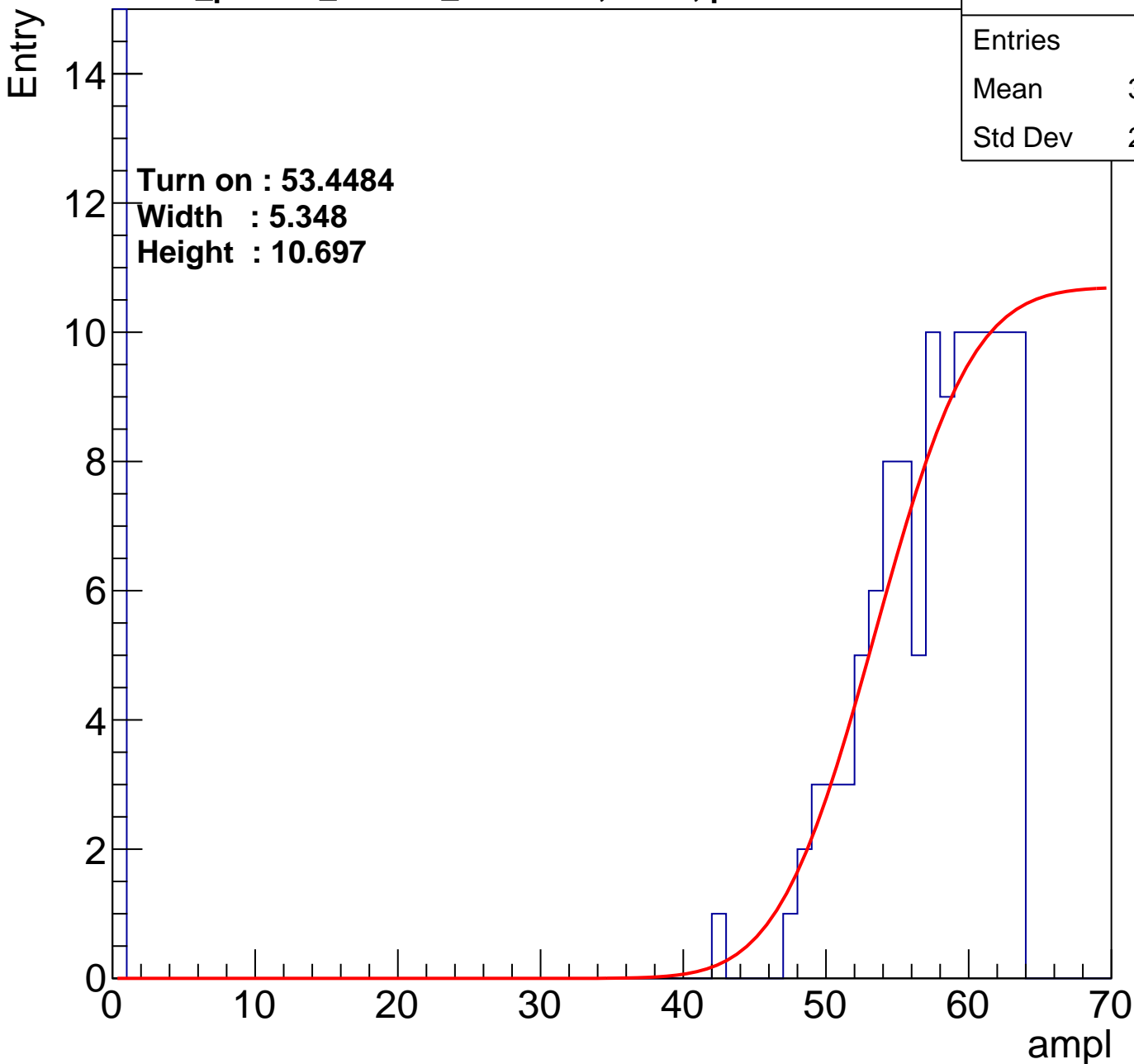
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 195 |
| Mean | 33.37 |
| Std Dev | 28.33 |

Turn on : 53.4484

Width : 5.348

Height : 10.697



B1L104S, U6-ch53

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 199 |
| Mean | 35.22 |
| Std Dev | 27.87 |

Turn on : 52.0214

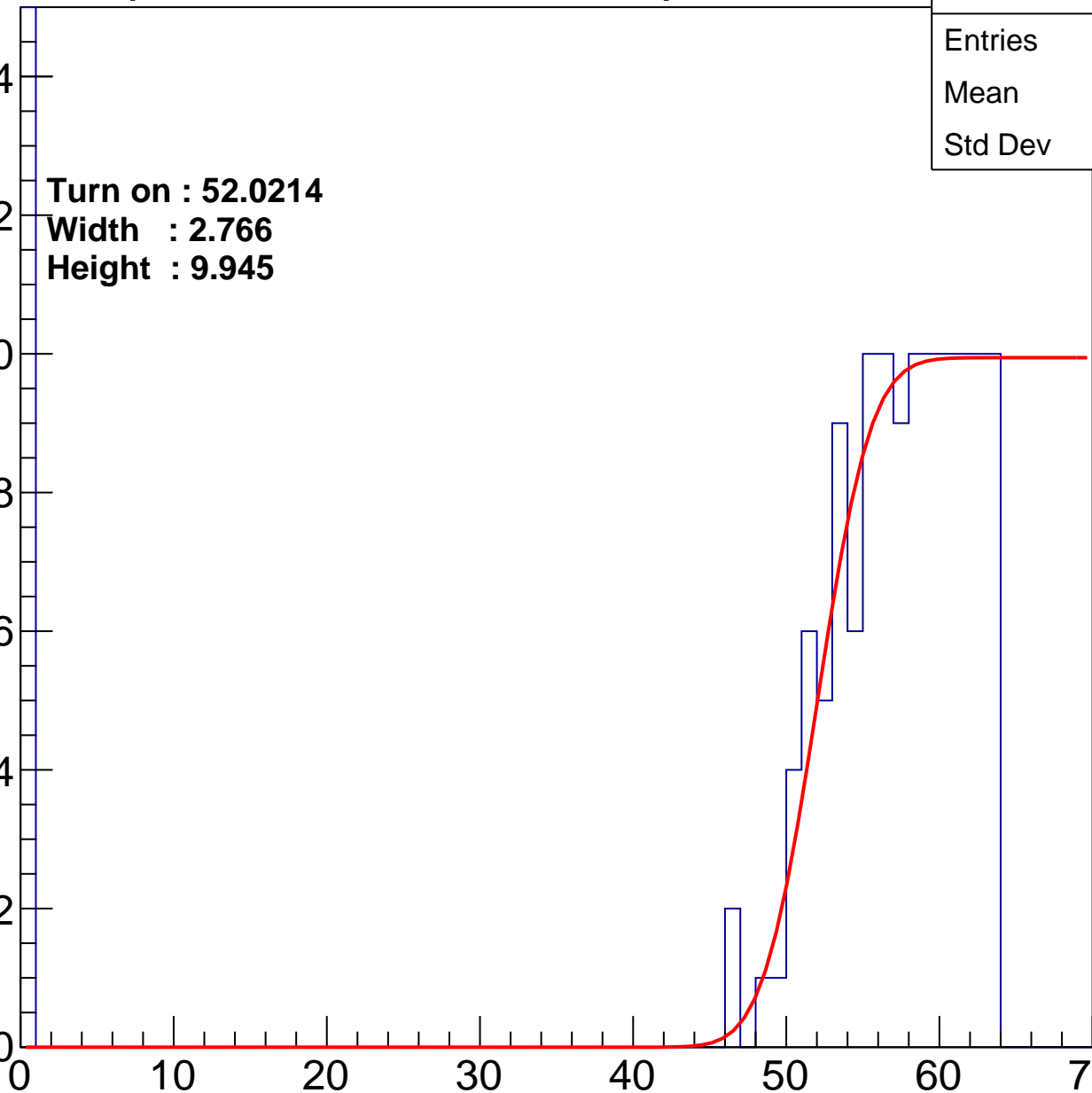
Width : 2.766

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch54

calib_packv5_033123_0516.root, FC#4, port A1

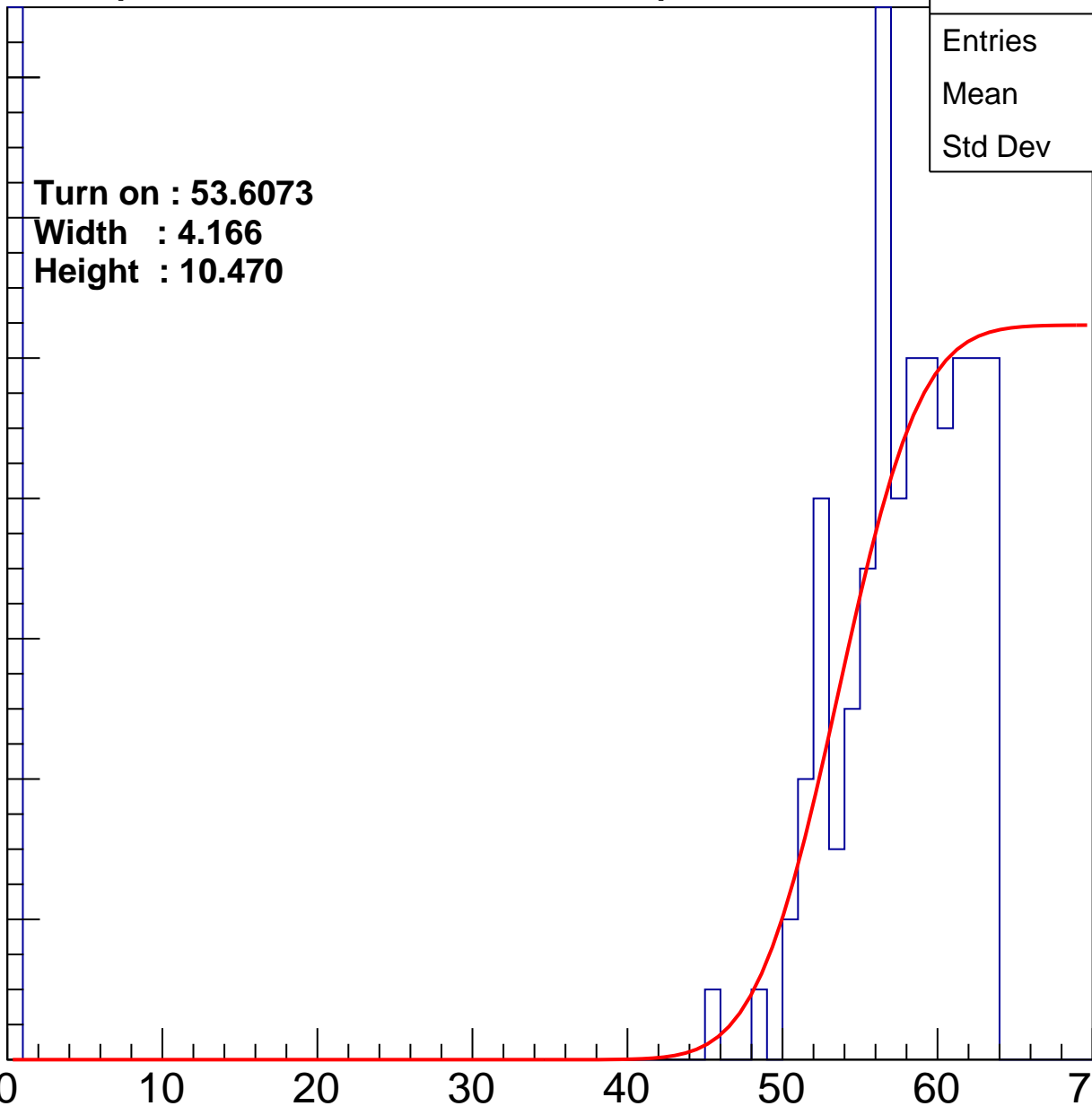
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6073
Width : 4.166
Height : 10.470

| | |
|---------|-------|
| Entries | 209 |
| Mean | 31.87 |
| Std Dev | 28.68 |

ampl



B1L104S, U6-ch55

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 182 |
| Mean | 35.63 |
| Std Dev | 28.01 |

Turn on : 53.8867

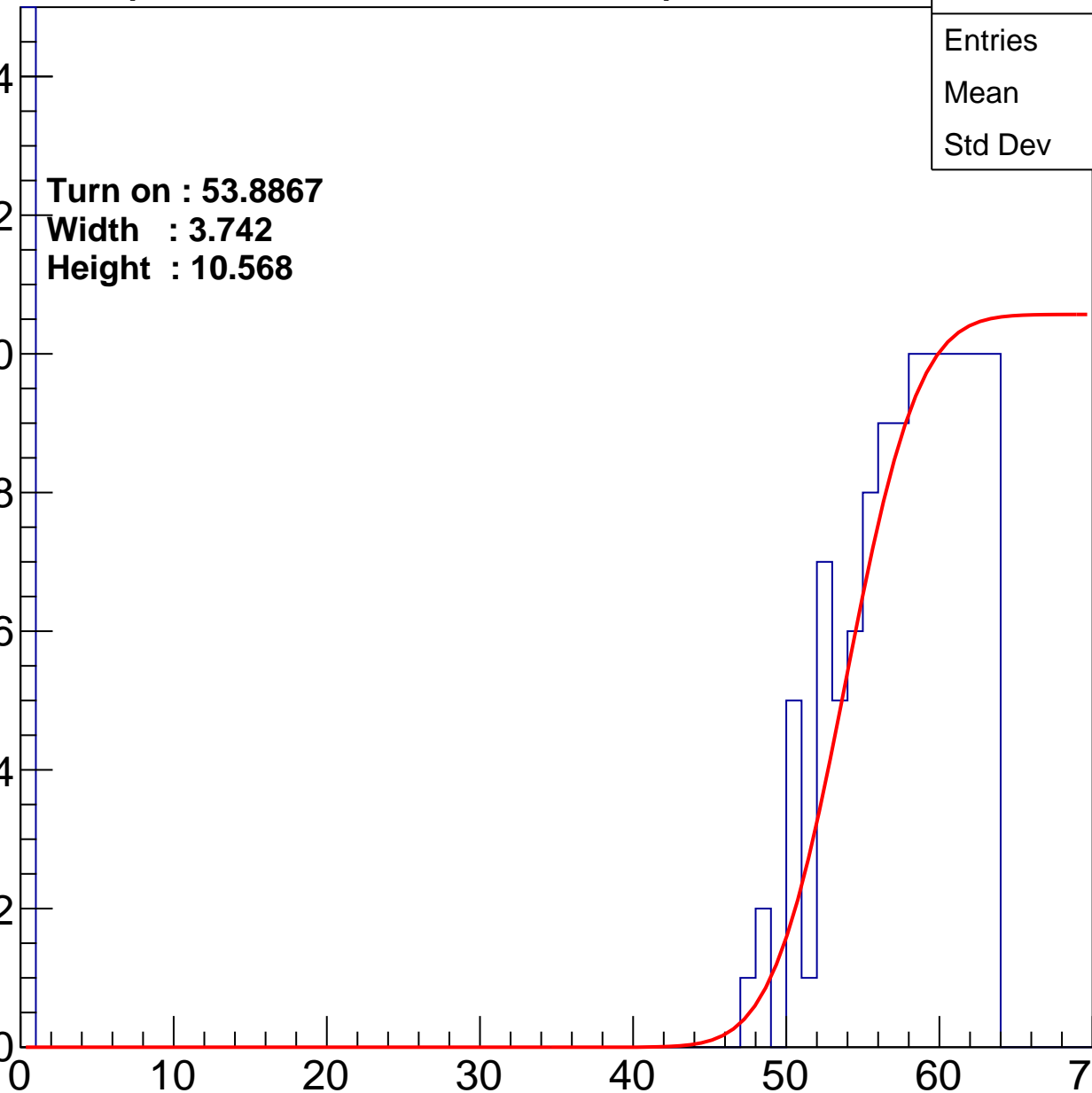
Width : 3.742

Height : 10.568

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch56

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 237 |
| Mean | 33.79 |
| Std Dev | 27.63 |

Turn on : 50.4625

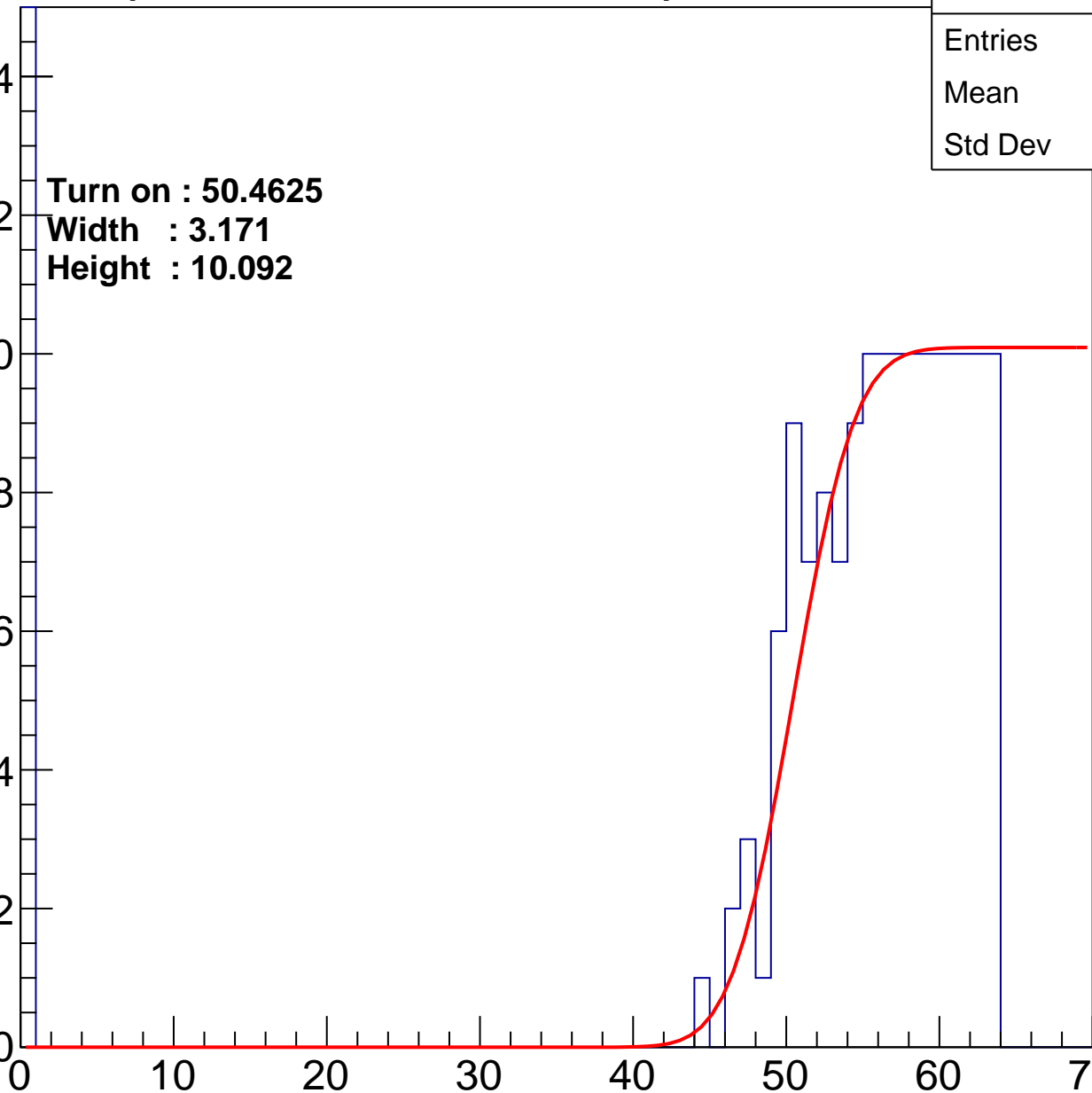
Width : 3.171

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch57

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 187 |
| Mean | 35.55 |
| Std Dev | 27.99 |

Turn on : 52.6413

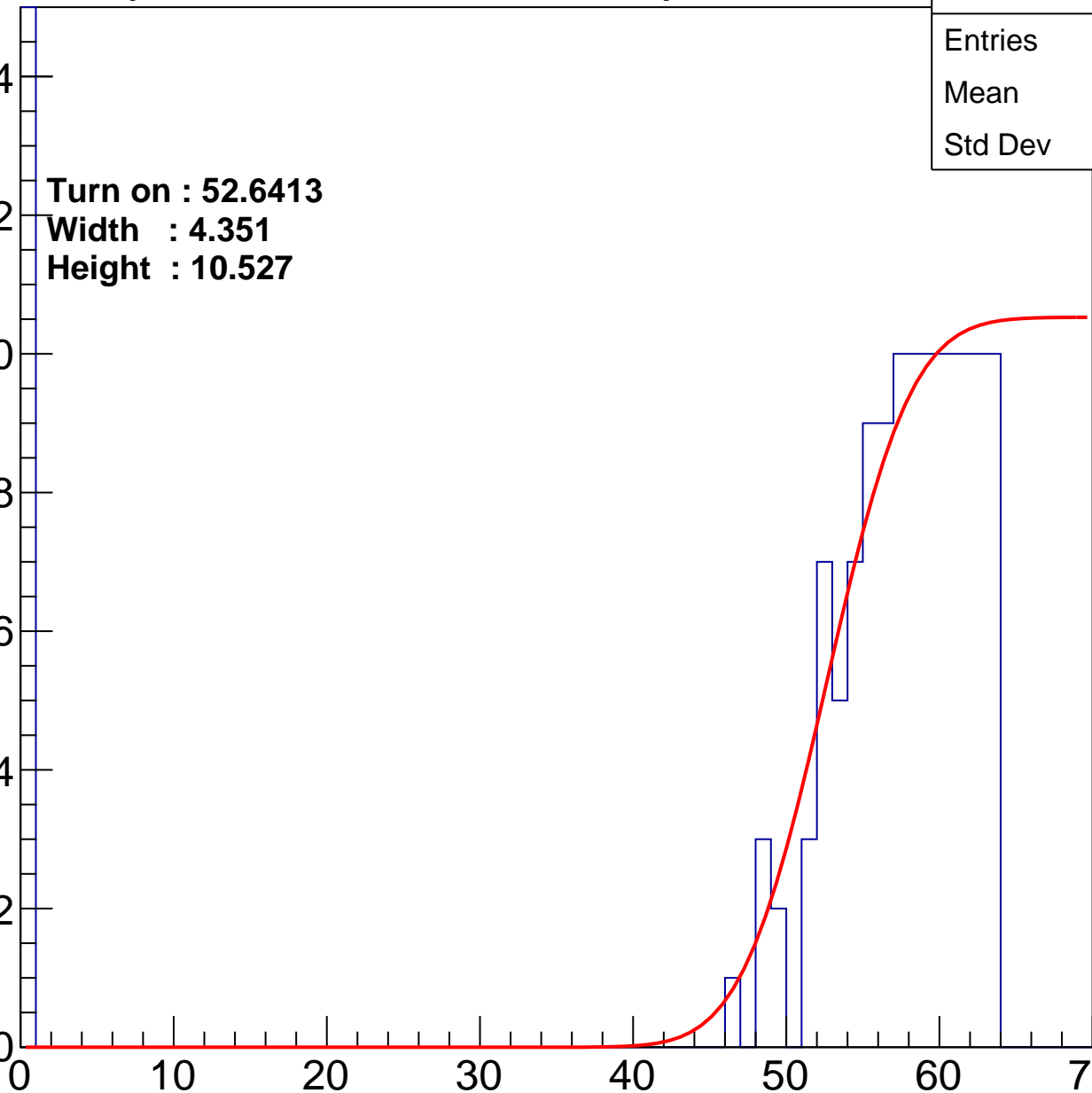
Width : 4.351

Height : 10.527

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch58

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 31.41 |
| Std Dev | 28.81 |

Turn on : 52.6521

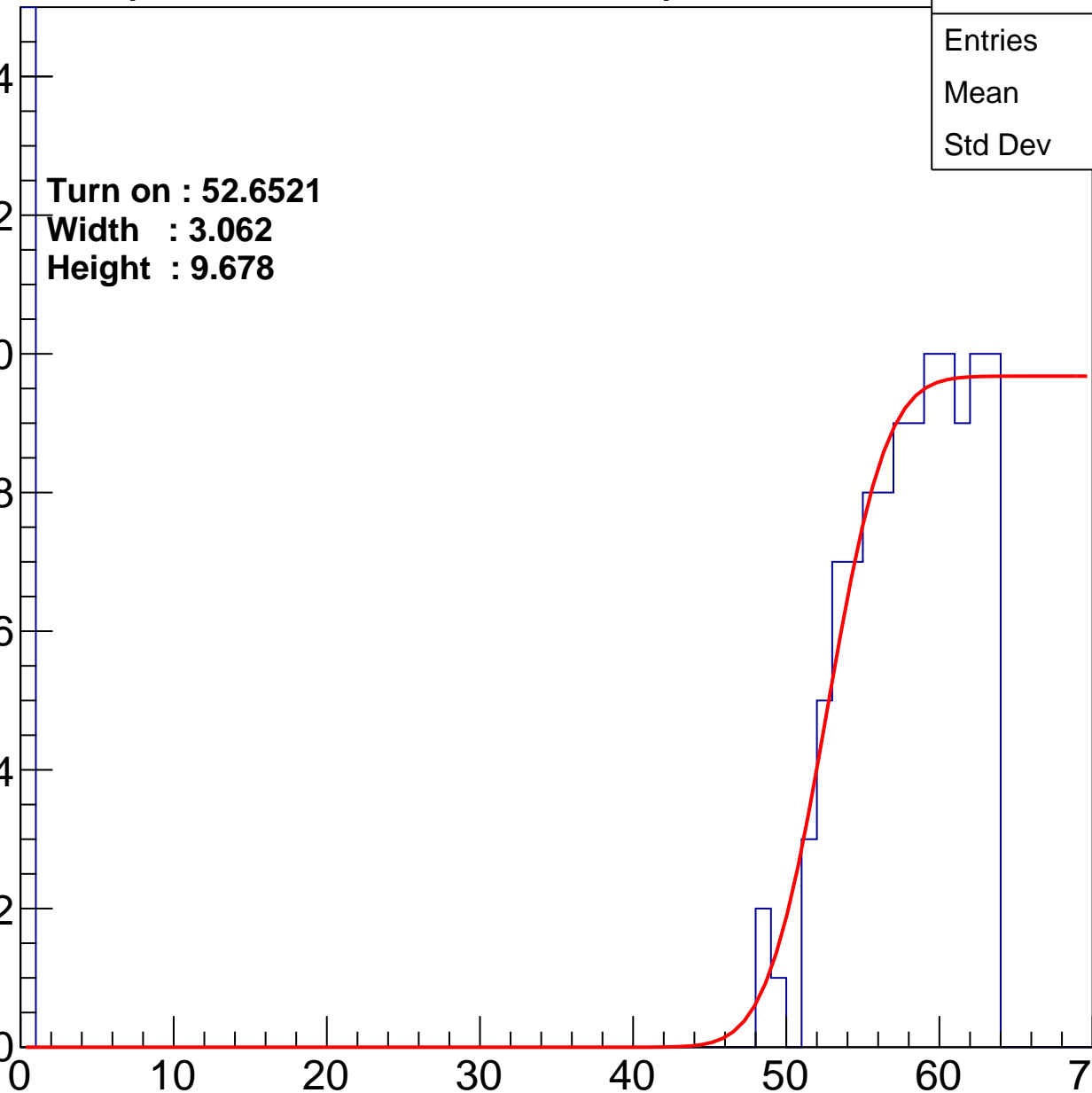
Width : 3.062

Height : 9.678

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch59

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 165 |
| Mean | 28.75 |
| Std Dev | 29.38 |

Turn on : 55.7767

Width : 4.430

Height : 9.889

Entry

80
70
60
50
40
30
20
10
0

ampl

0 10 20 30 40 50 60 70

B1L104S, U6-ch60

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 33.15 |
| Std Dev | 28.36 |

Turn on : 53.5635

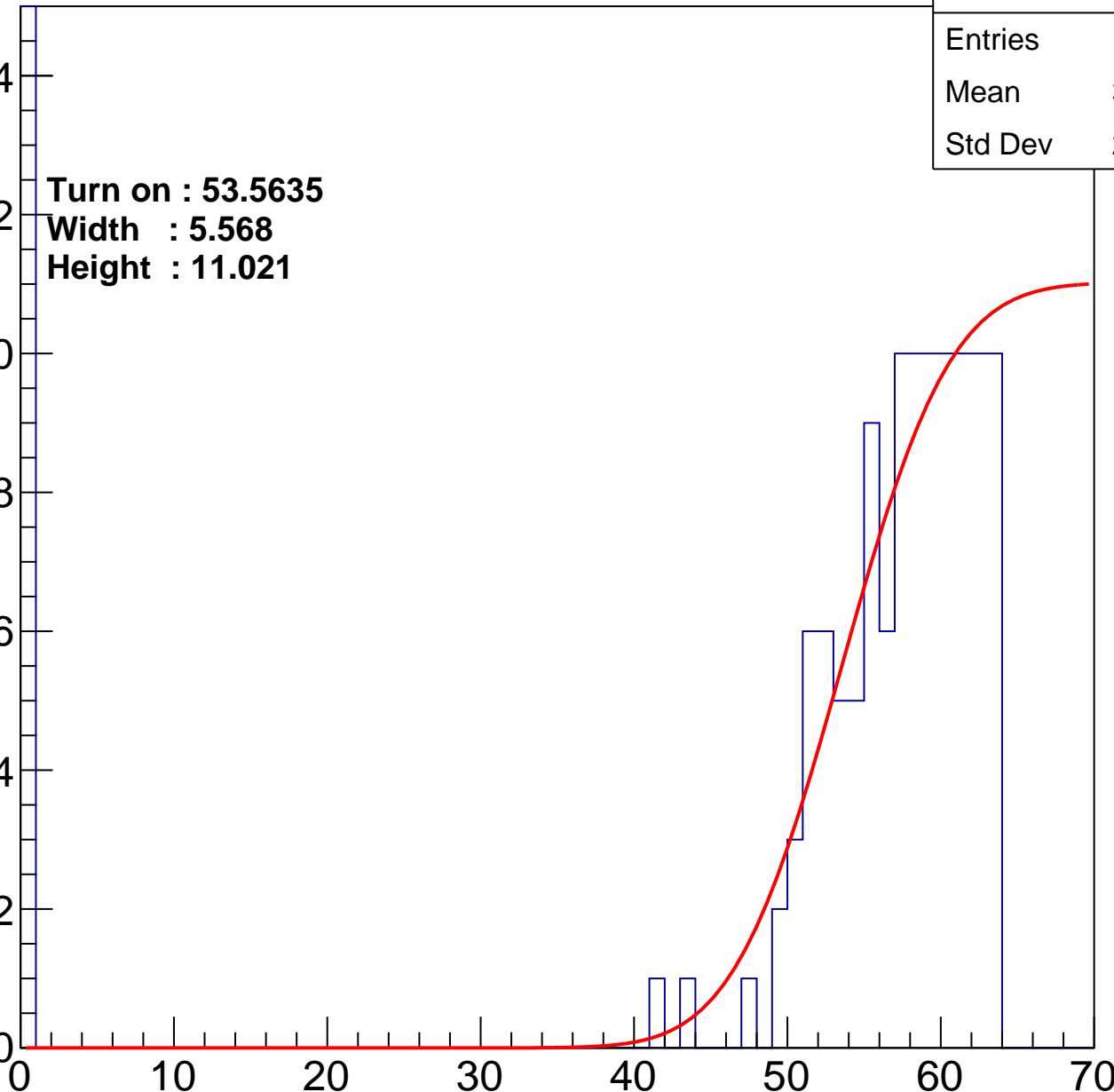
Width : 5.568

Height : 11.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch61

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 175 |
| Mean | 31.05 |
| Std Dev | 29.25 |

Turn on : 55.2729

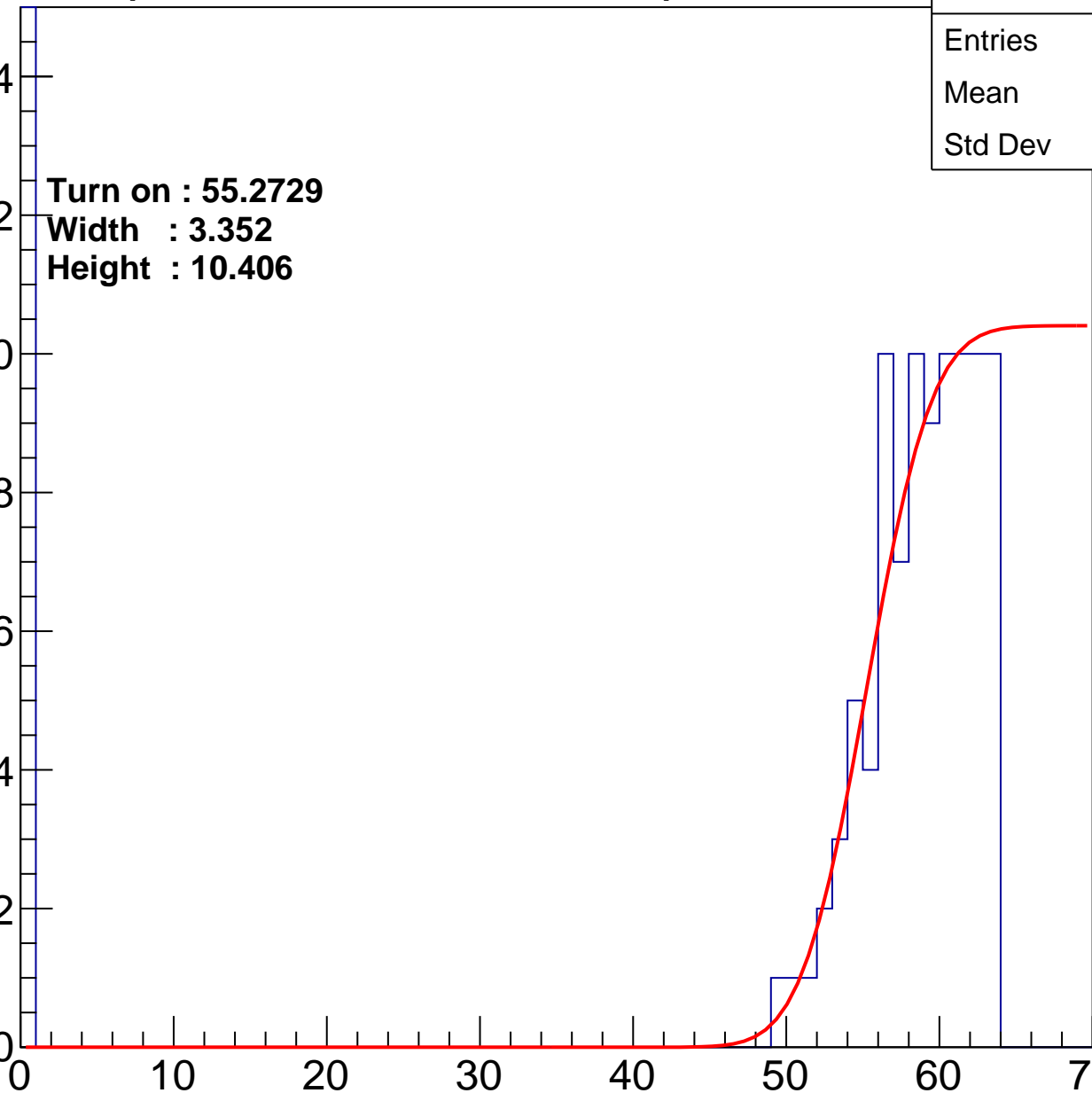
Width : 3.352

Height : 10.406

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch62

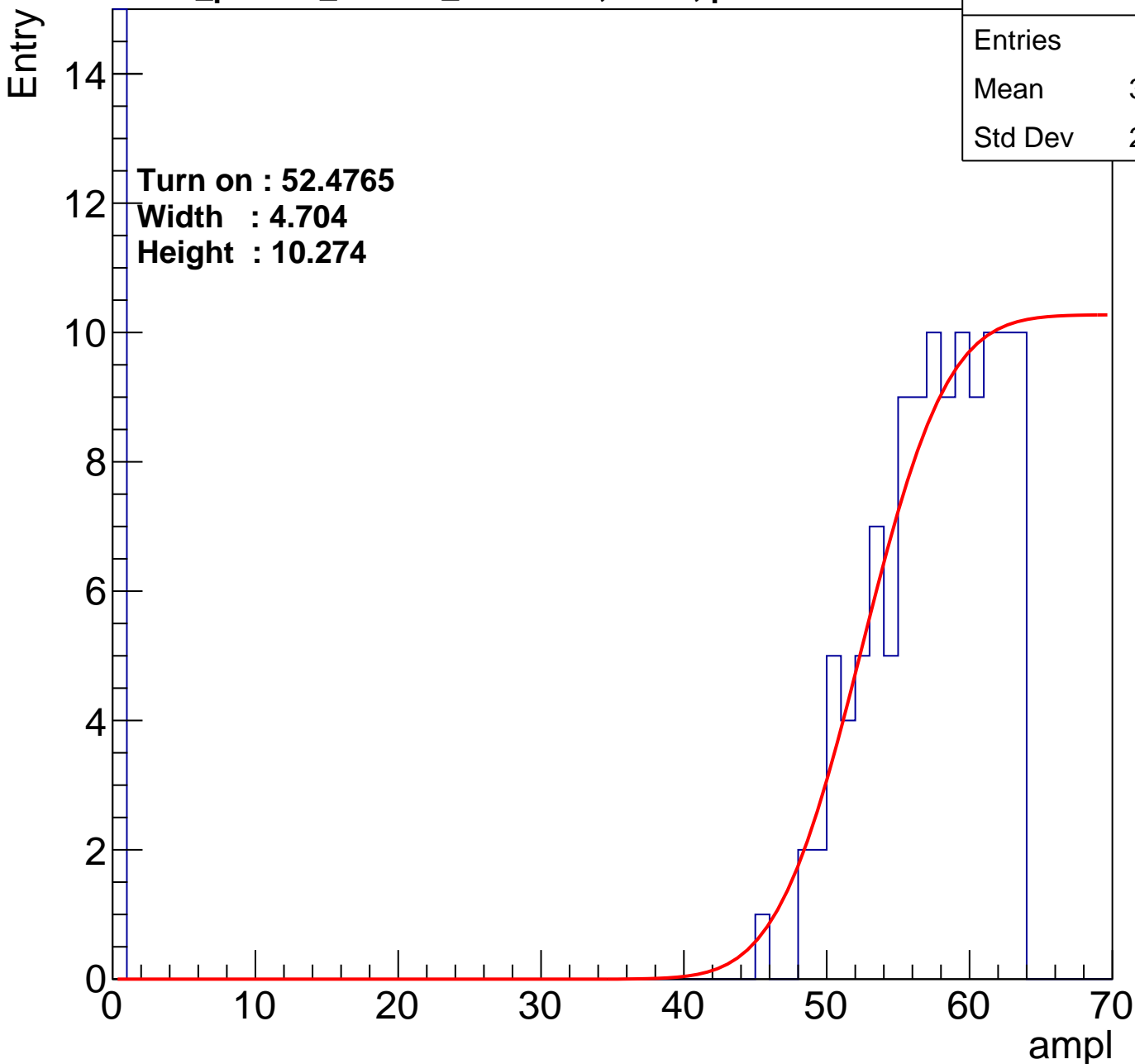
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 190 |
| Mean | 35.13 |
| Std Dev | 27.94 |

Turn on : 52.4765

Width : 4.704

Height : 10.274



B1L104S, U6-ch63

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 174 |
| Mean | 33.4 |
| Std Dev | 28.85 |

Turn on : 53.8201

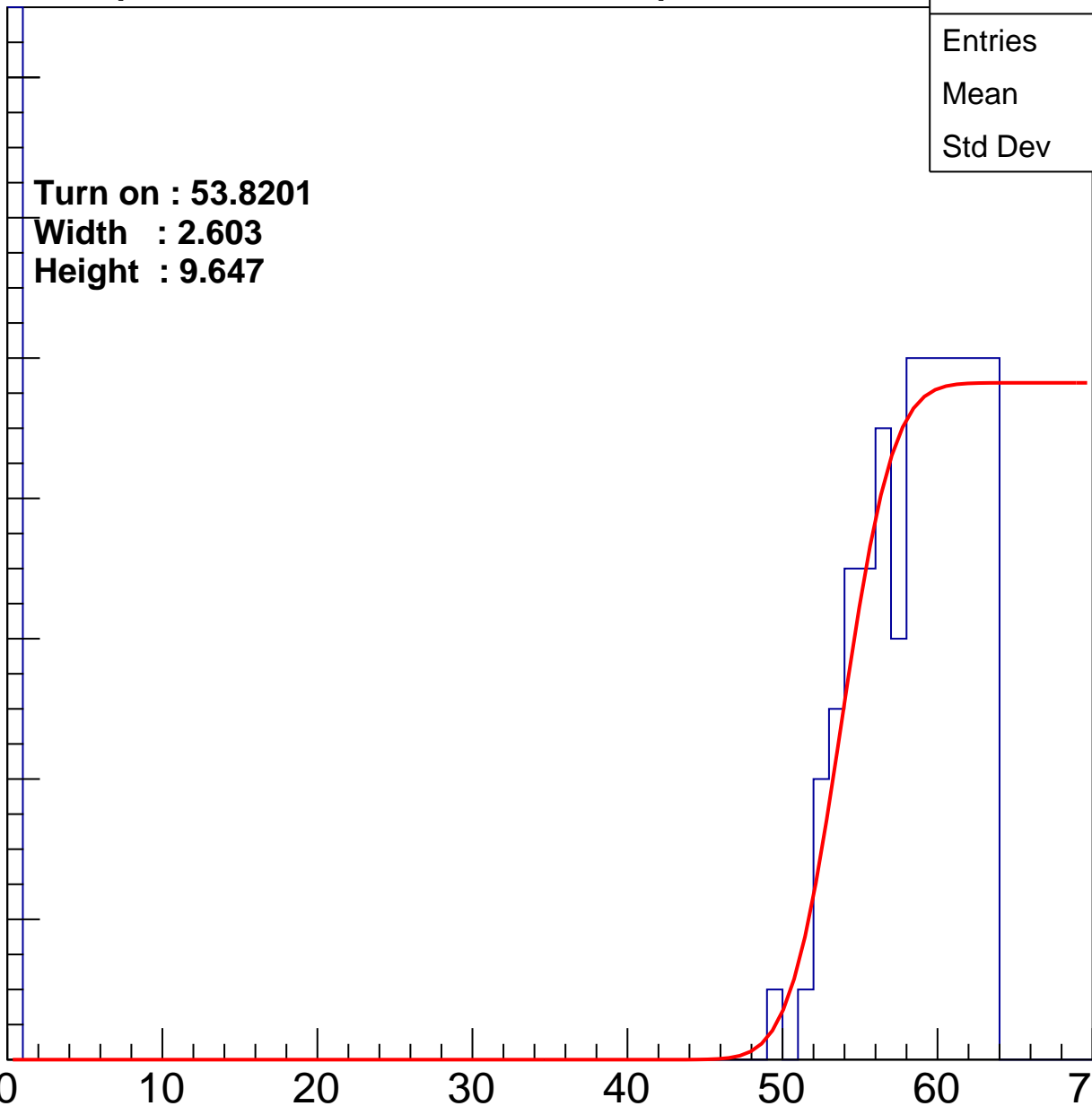
Width : 2.603

Height : 9.647

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch64

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 192 |
| Mean | 32.27 |
| Std Dev | 28.64 |

Turn on : 54.7091

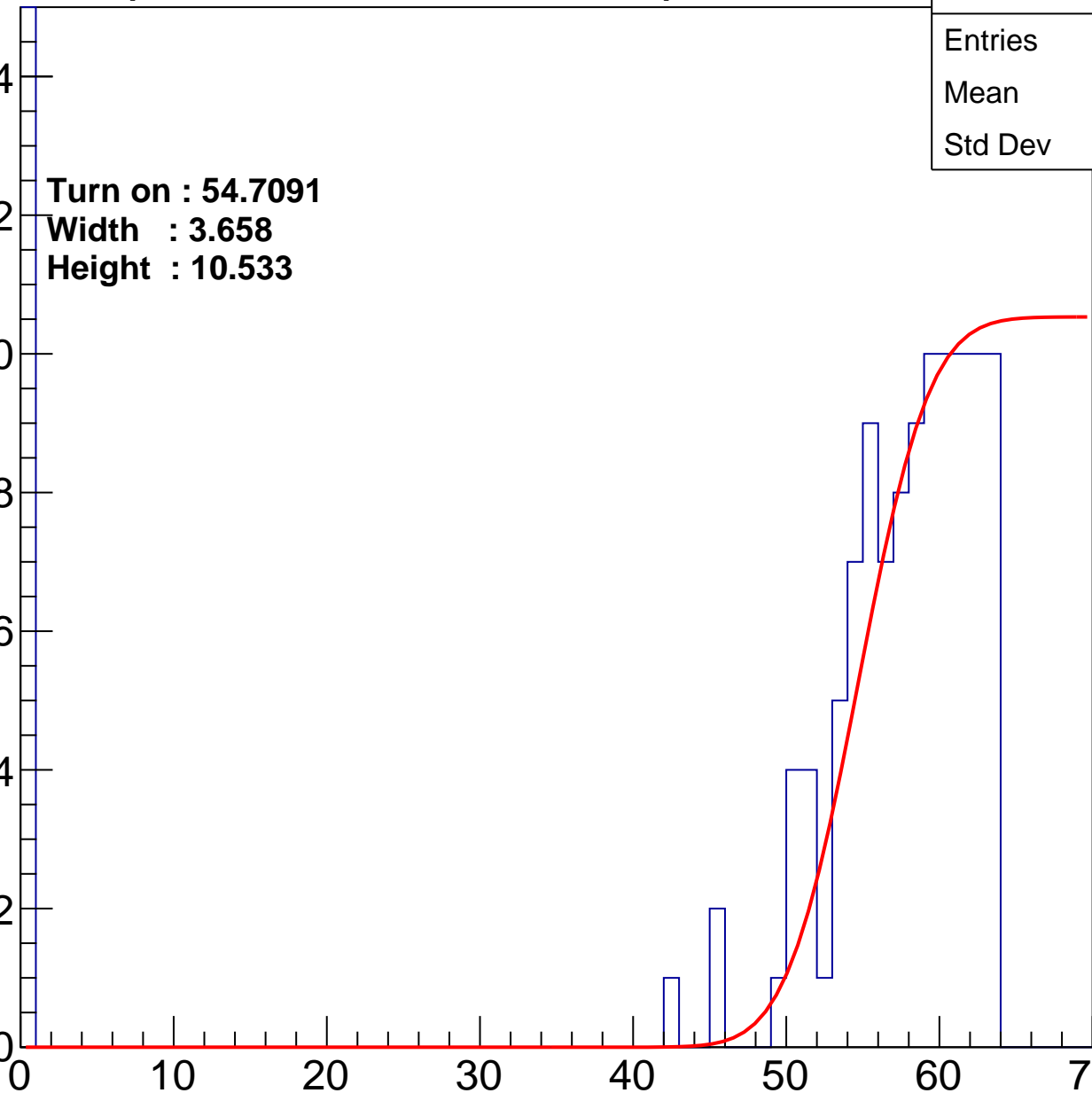
Width : 3.658

Height : 10.533

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch65

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 170 |
| Mean | 32.82 |
| Std Dev | 28.94 |

Turn on : 54.8132

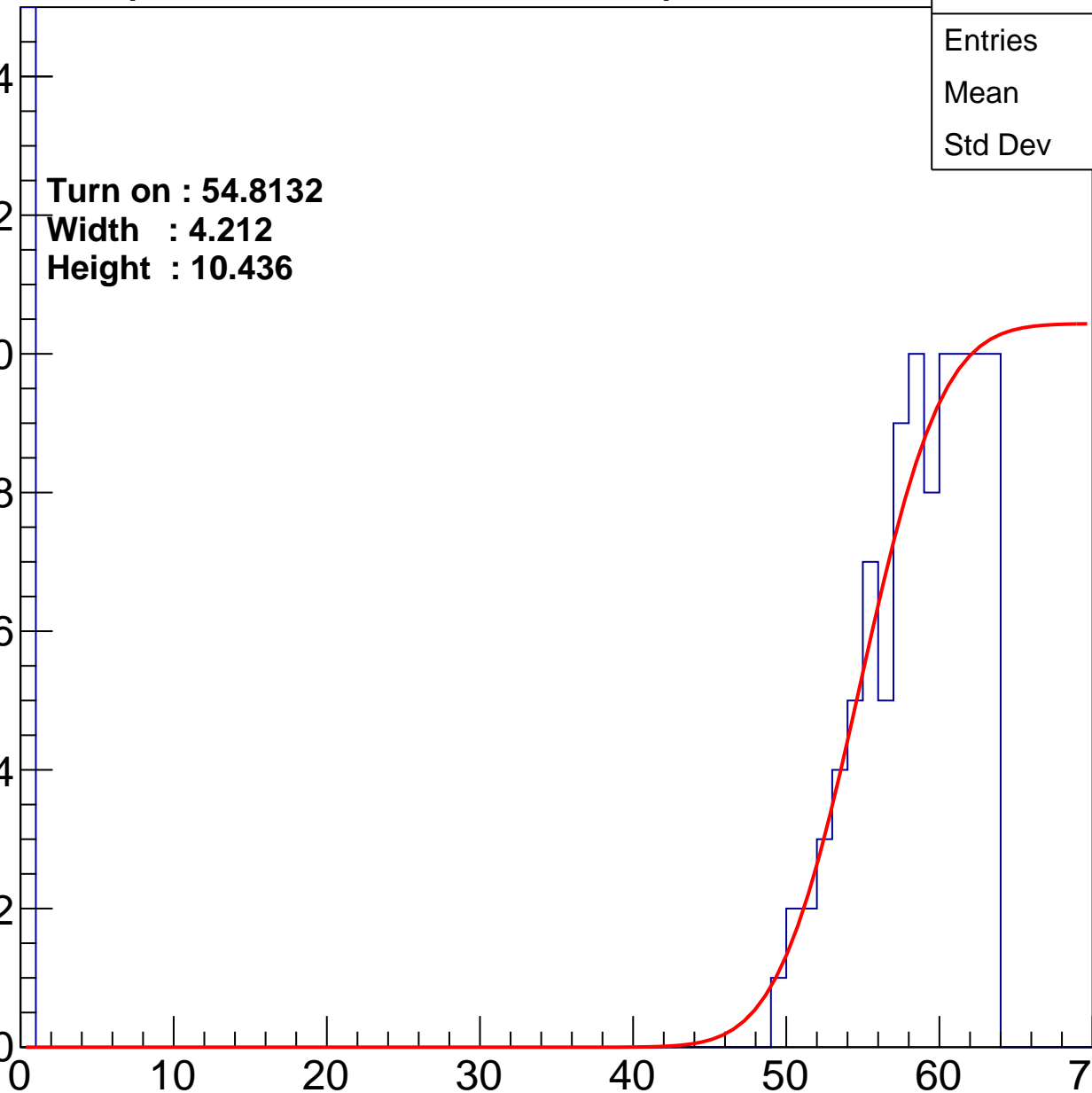
Width : 4.212

Height : 10.436

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch66

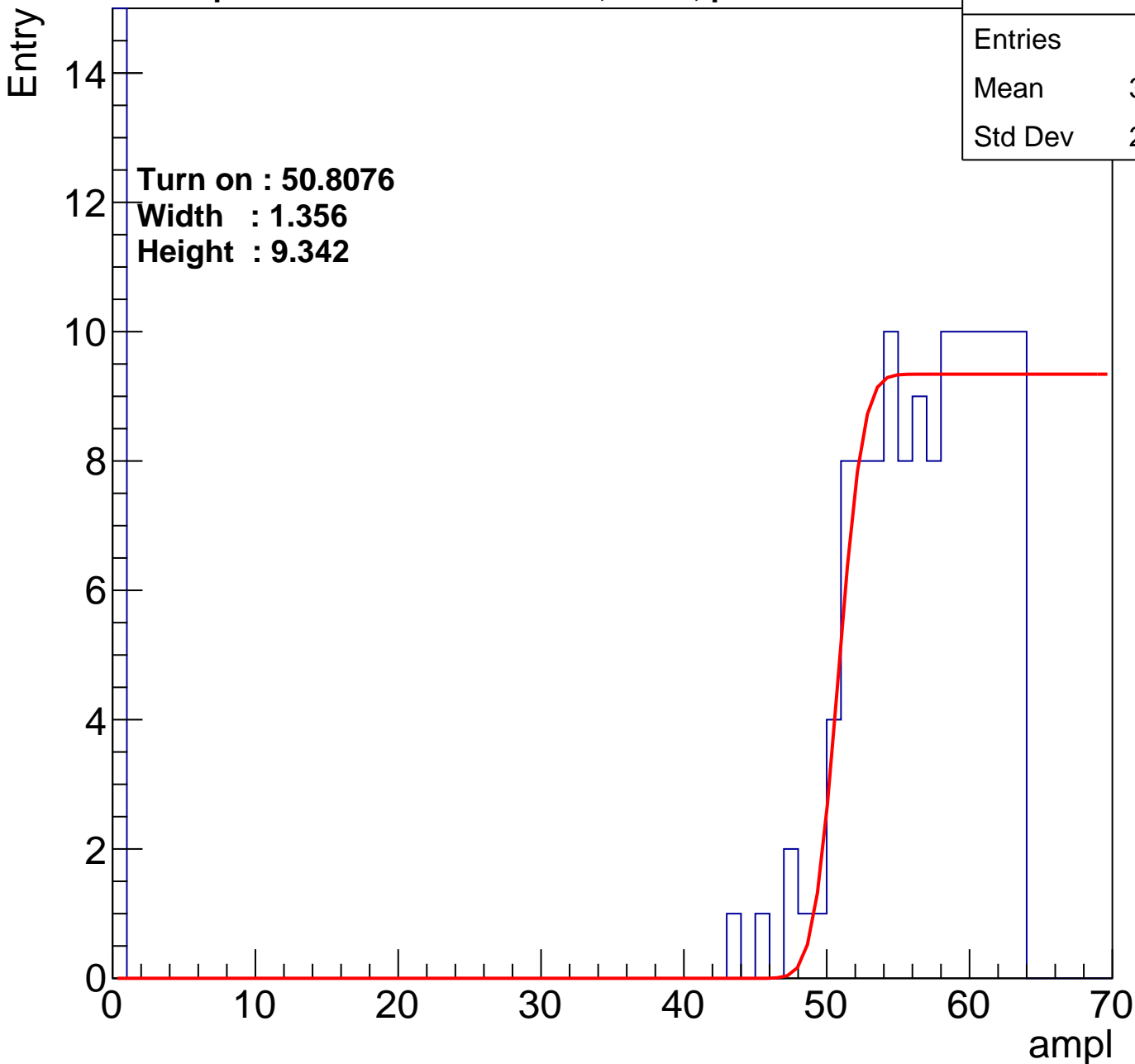
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 199 |
| Mean | 36.67 |
| Std Dev | 27.24 |

Turn on : 50.8076

Width : 1.356

Height : 9.342



B1L104S, U6-ch67

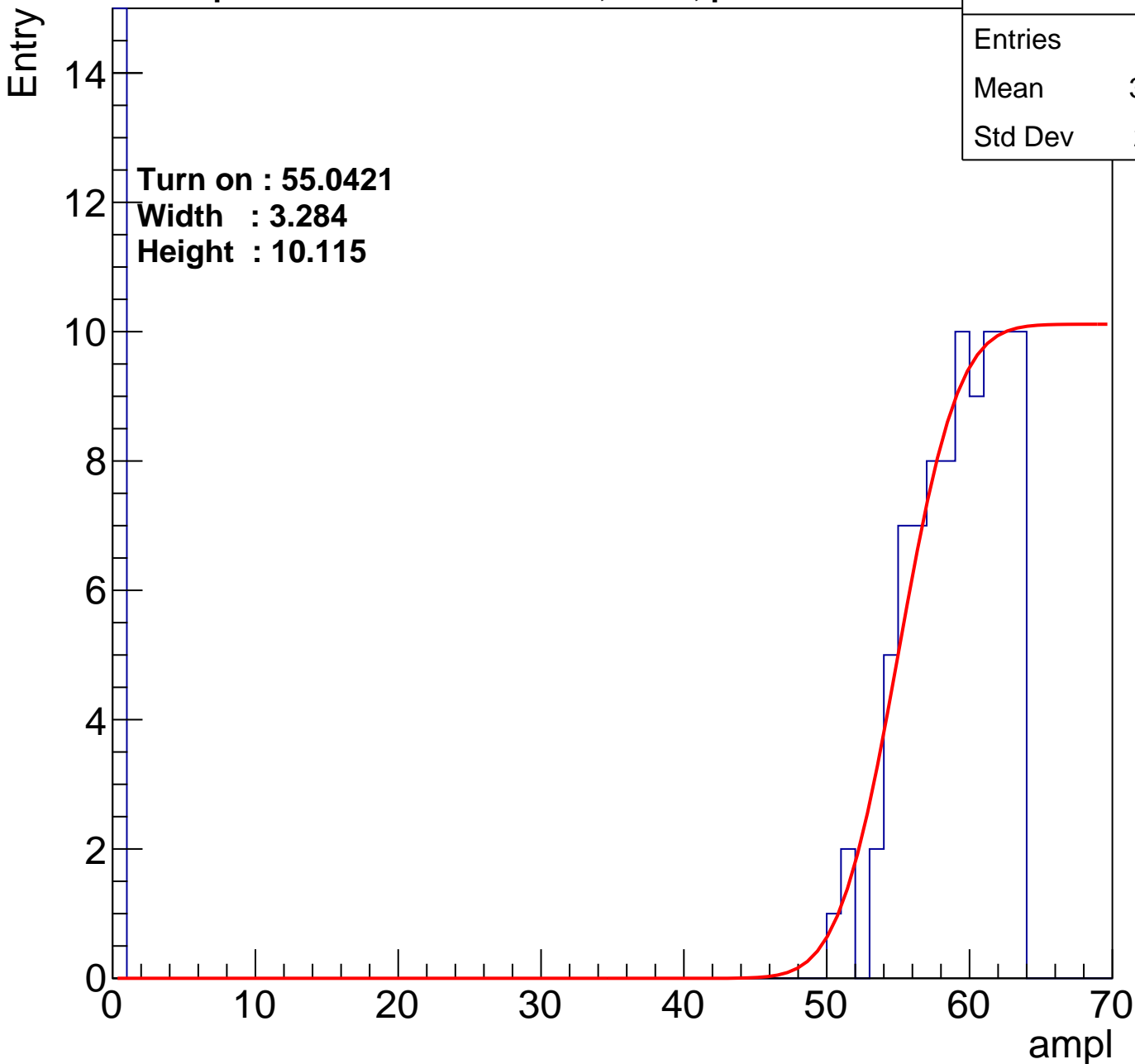
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 166 |
| Mean | 31.42 |
| Std Dev | 29.31 |

Turn on : 55.0421

Width : 3.284

Height : 10.115



B1L104S, U6-ch68

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 200 |
| Mean | 28.91 |
| Std Dev | 29.03 |

Turn on : 54.4476

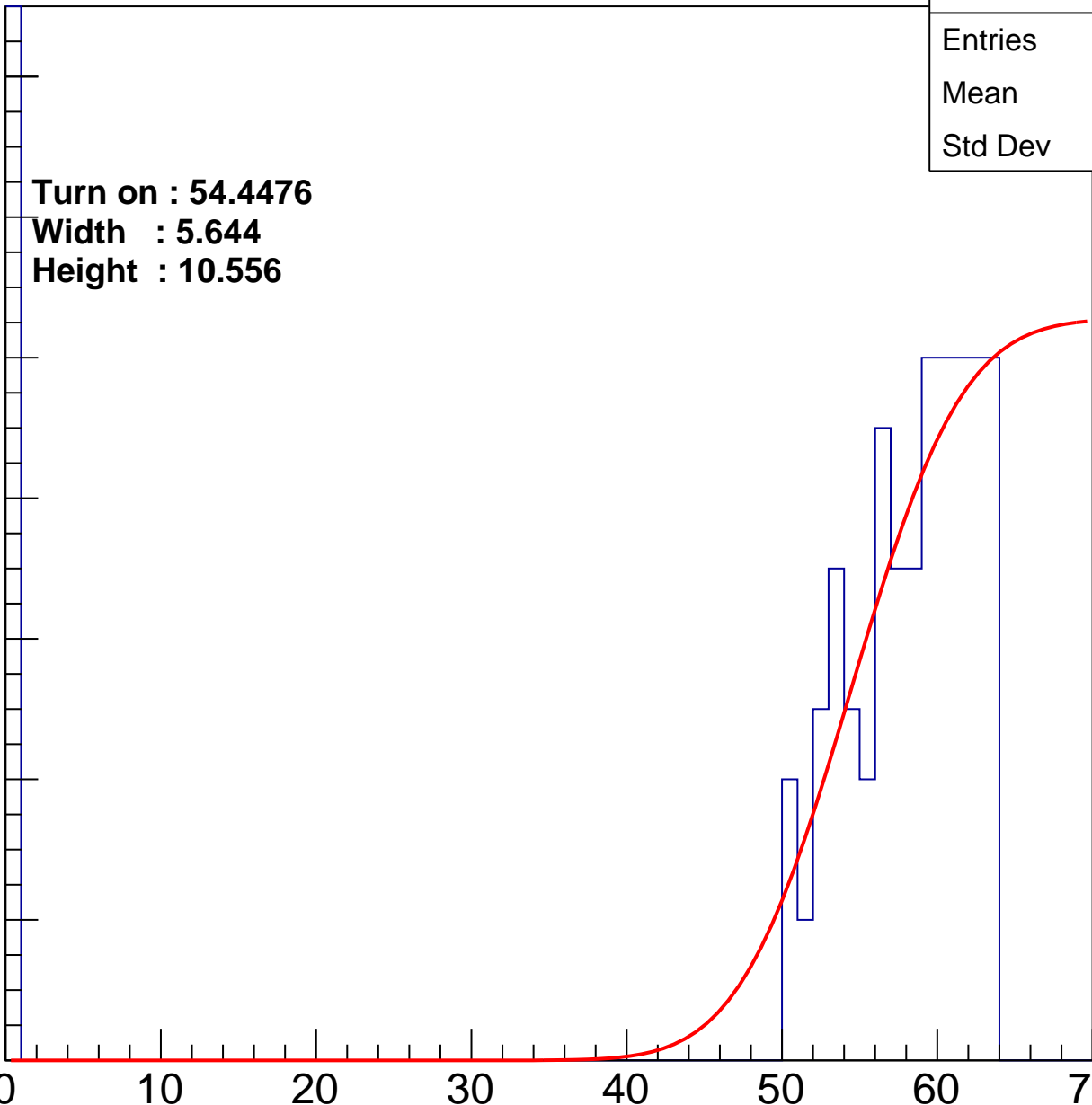
Width : 5.644

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch69

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 176 |
| Mean | 30.43 |
| Std Dev | 29.19 |

Turn on : 55.6385

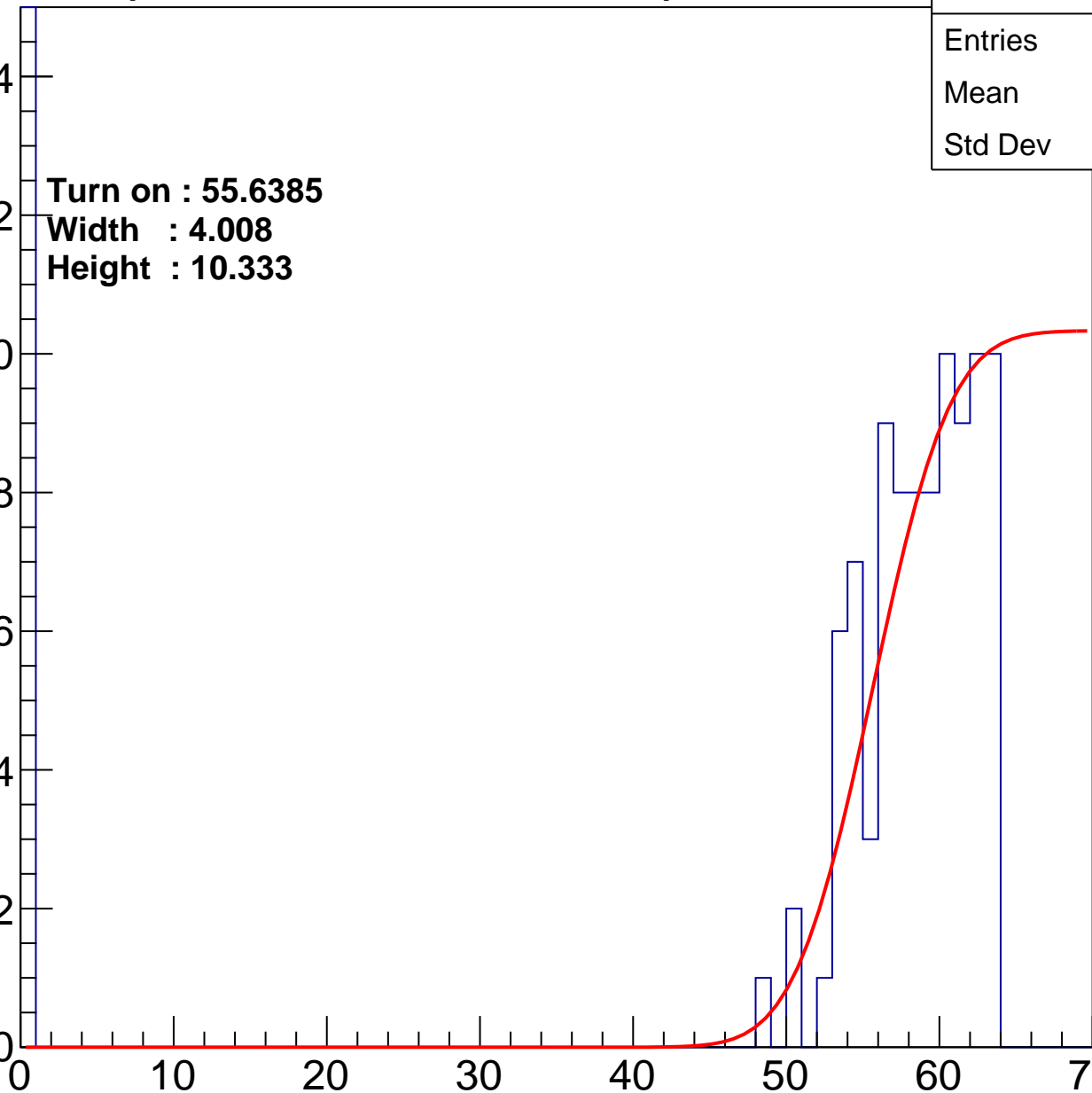
Width : 4.008

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch70

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 226 |
| Mean | 28.65 |
| Std Dev | 28.79 |

Turn on : 53.8592

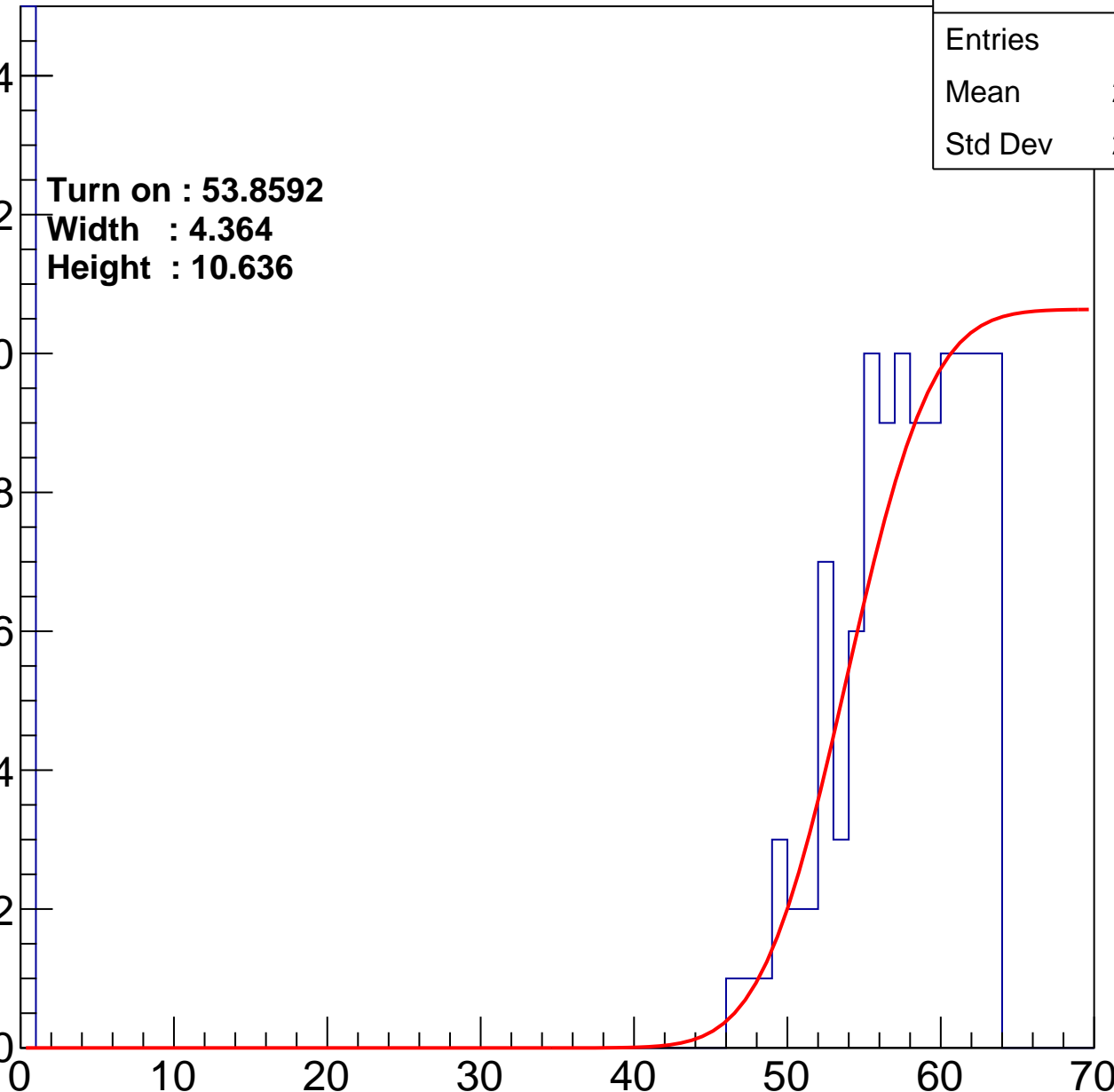
Width : 4.364

Height : 10.636

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch71

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 178 |
| Mean | 29.96 |
| Std Dev | 29.37 |

Turn on : 54.5180

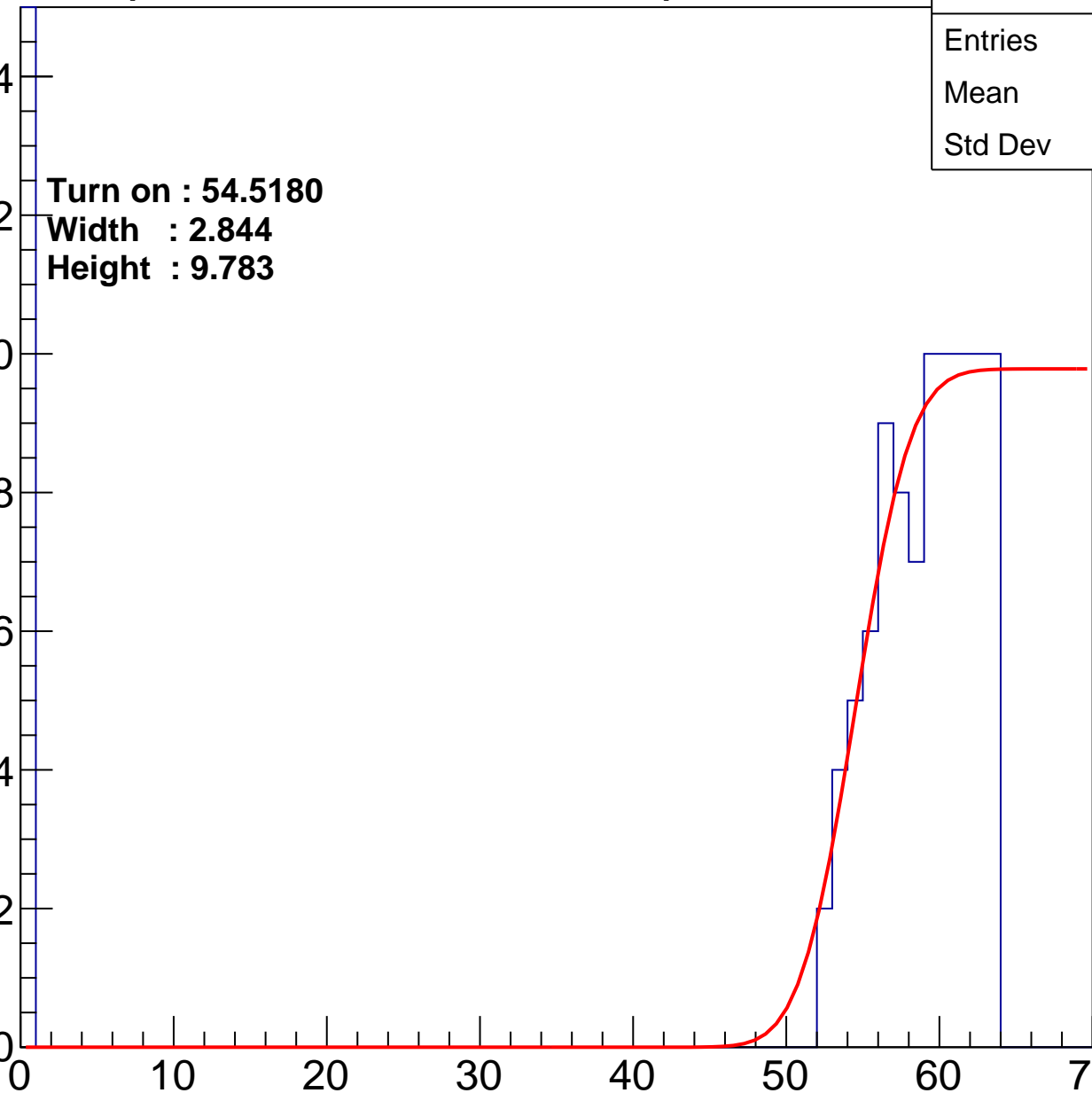
Width : 2.844

Height : 9.783

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch72

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 182 |
| Mean | 29.05 |
| Std Dev | 29.17 |

Turn on : 54.6226

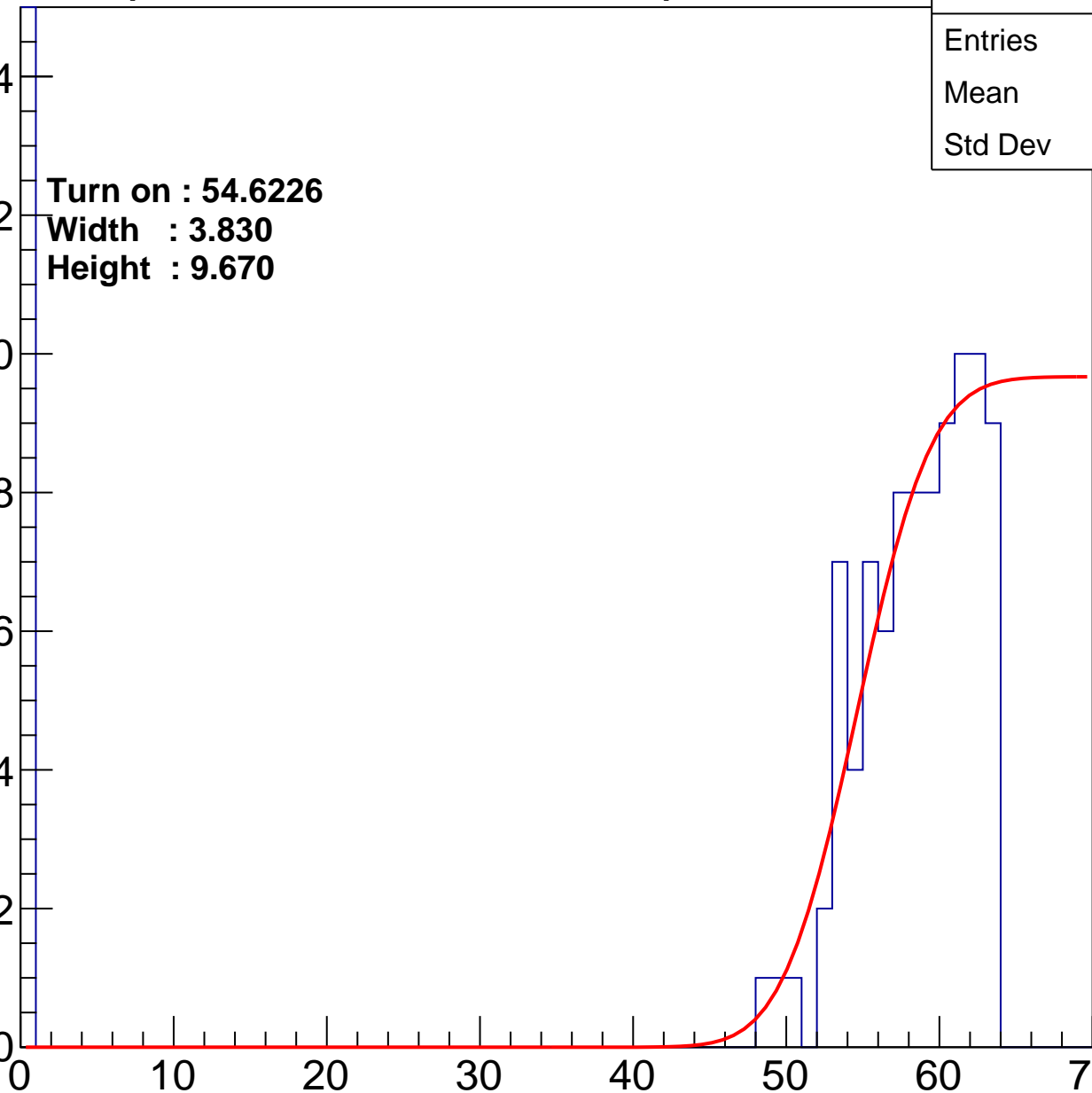
Width : 3.830

Height : 9.670

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch73

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 206 |
| Mean | 34.73 |
| Std Dev | 27.87 |

Turn on : 51.3988

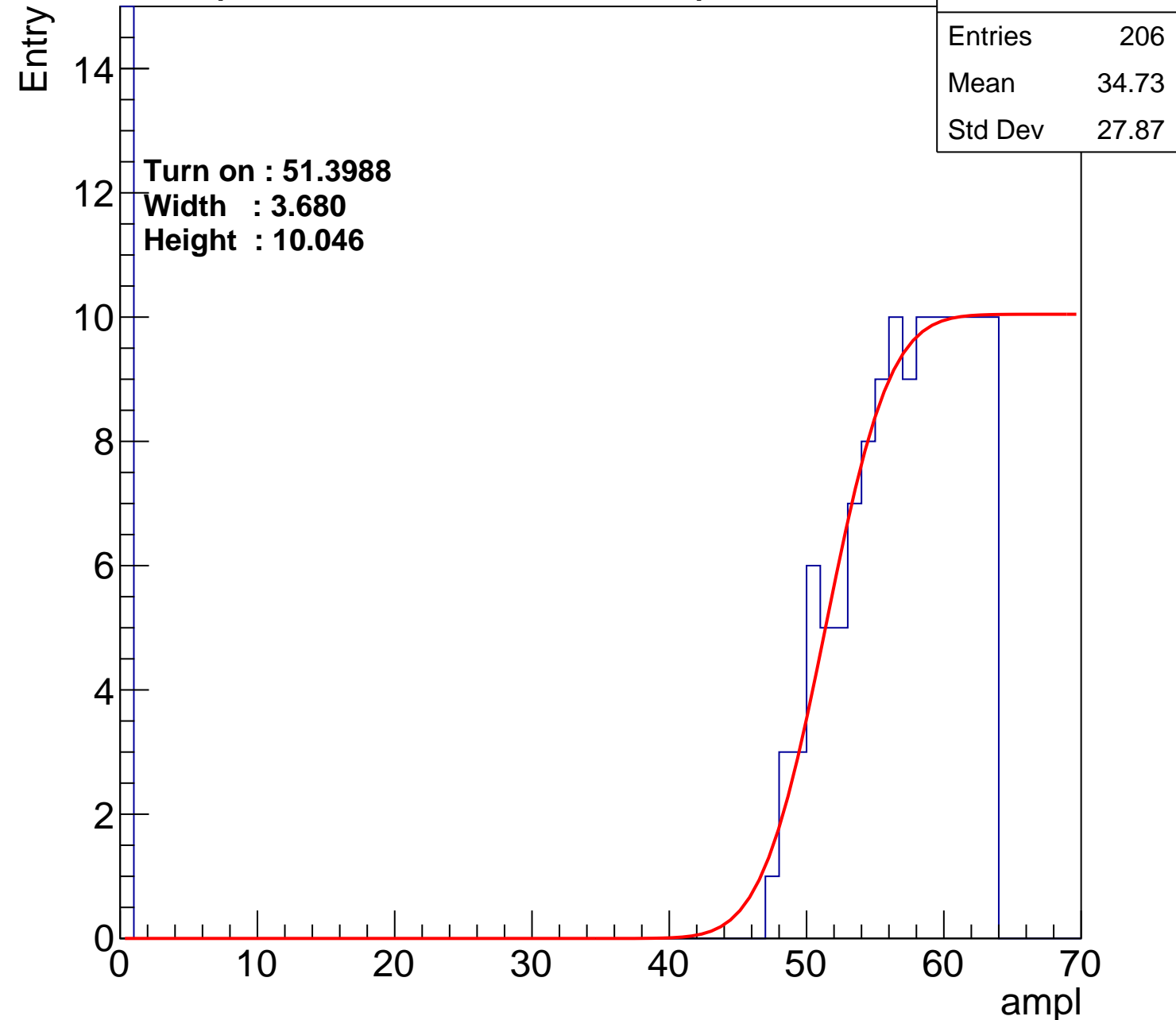
Width : 3.680

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch74

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 201 |
| Mean | 28.53 |
| Std Dev | 29.09 |

Turn on : 55.2282

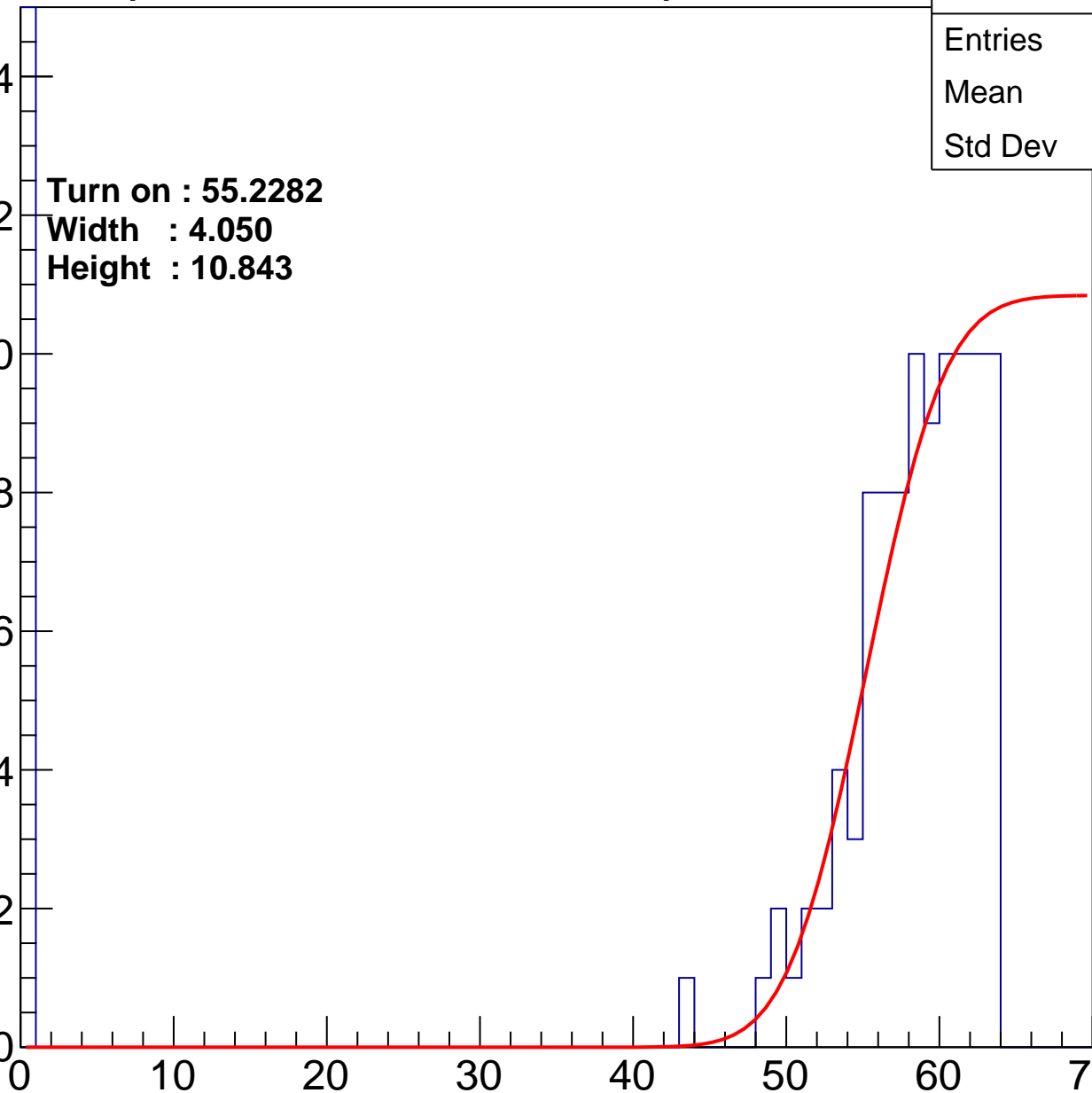
Width : 4.050

Height : 10.843

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch75

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 185 |
| Mean | 31.39 |
| Std Dev | 29.06 |

Turn on : 54.5920

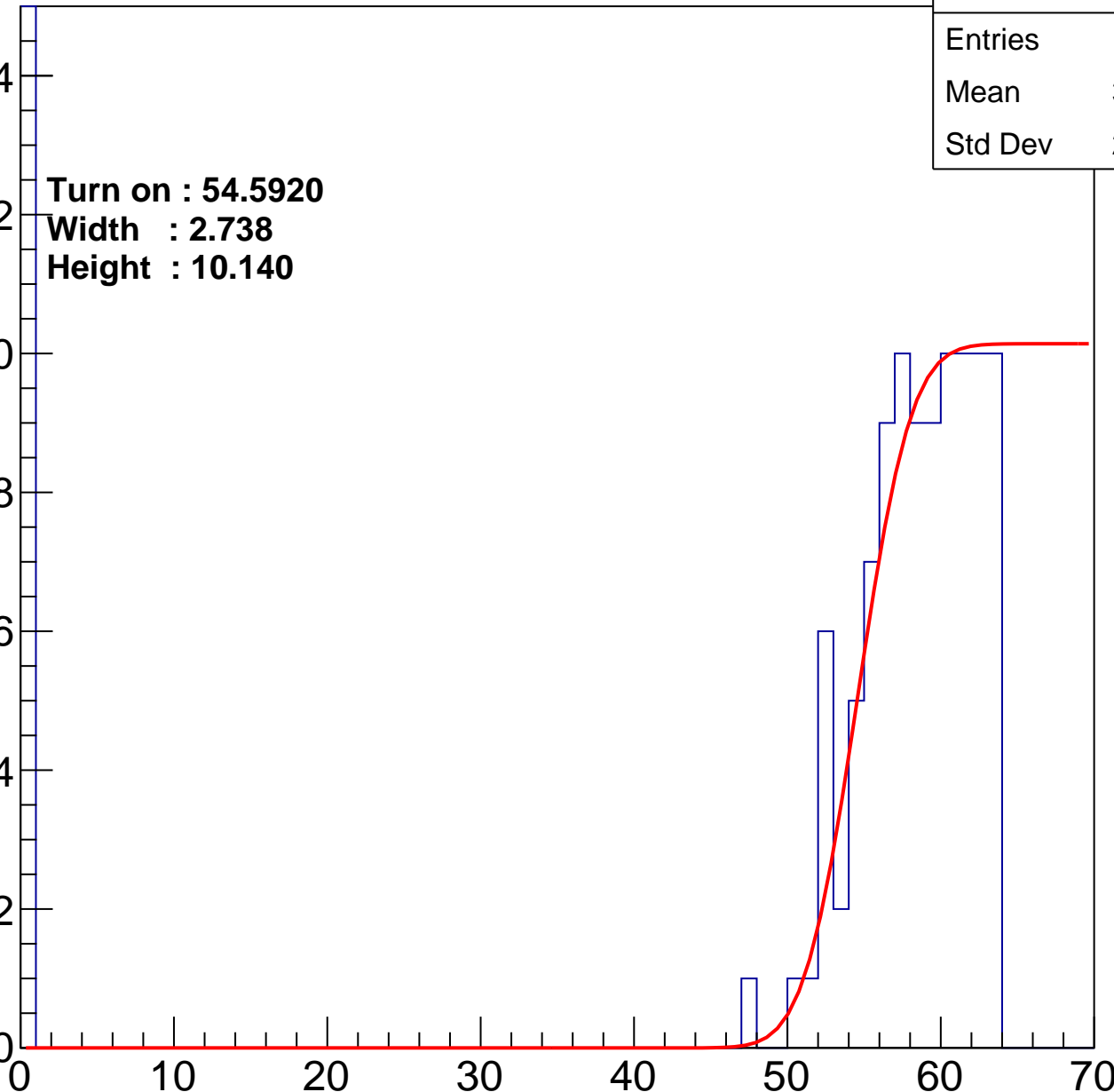
Width : 2.738

Height : 10.140

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch76

calib_packv5_033123_0516.root, FC#4, port A1

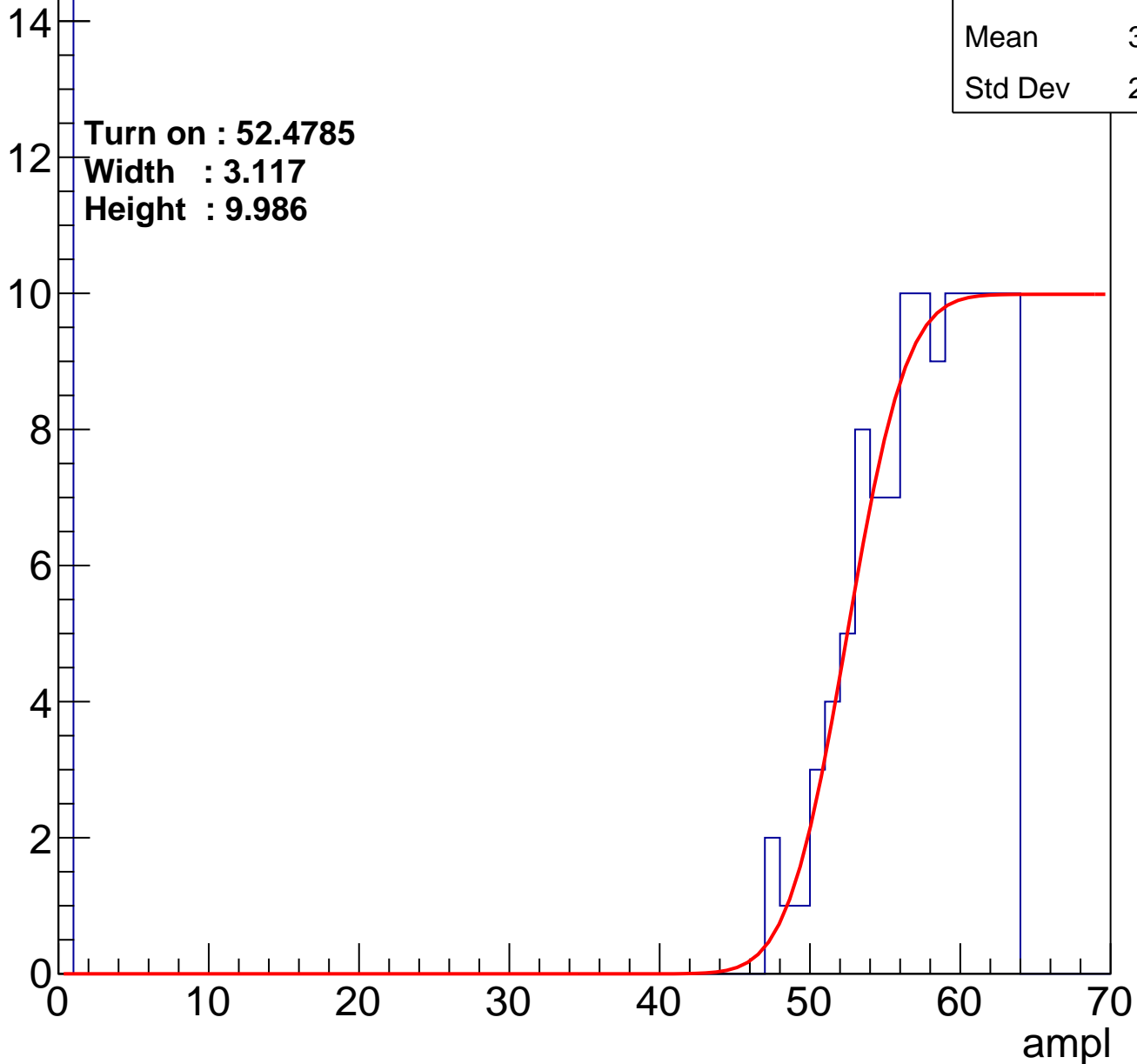
Entry

| | |
|---------|-------|
| Entries | 193 |
| Mean | 34.68 |
| Std Dev | 28.13 |

Turn on : 52.4785

Width : 3.117

Height : 9.986



B1L104S, U6-ch77

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 188 |
| Mean | 32.85 |
| Std Dev | 28.72 |

Turn on : 53.8640

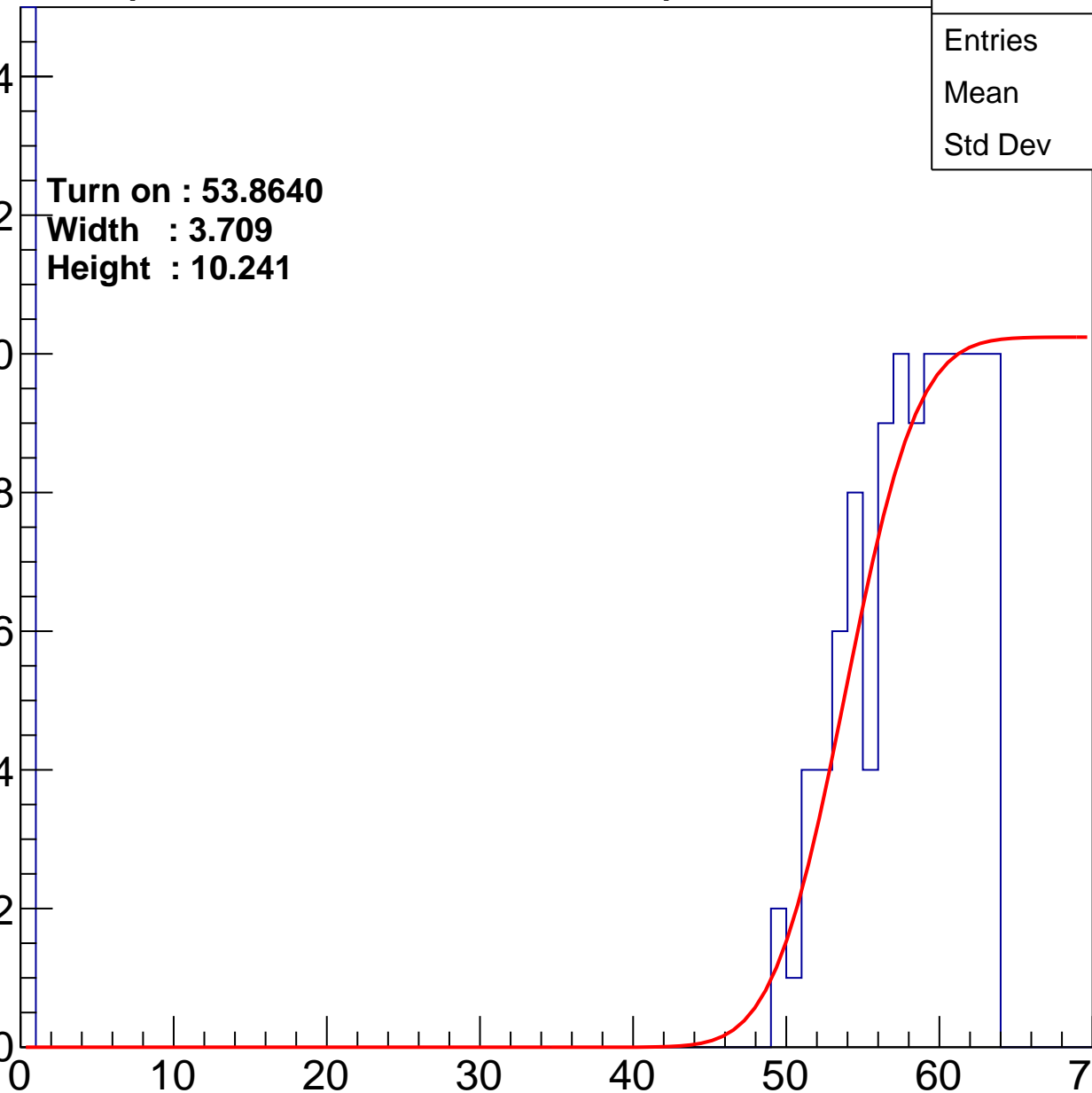
Width : 3.709

Height : 10.241

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch78

calib_packv5_033123_0516.root, FC#4, port A1

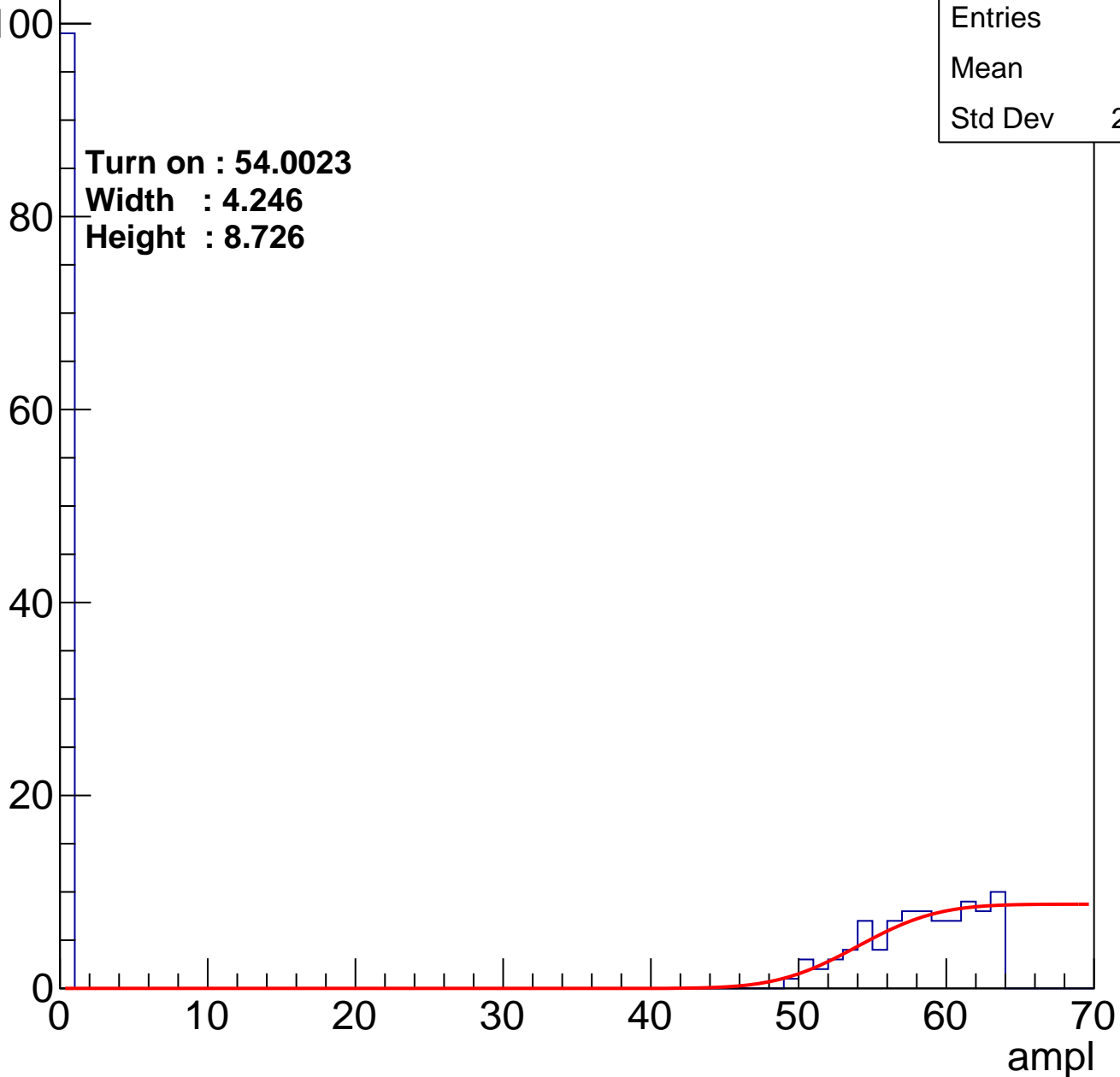
Entry

| | |
|---------|-------|
| Entries | 187 |
| Mean | 27.2 |
| Std Dev | 28.97 |

Turn on : 54.0023

Width : 4.246

Height : 8.726



B1L104S, U6-ch79

calib_packv5_033123_0516.root, FC#4, port A1

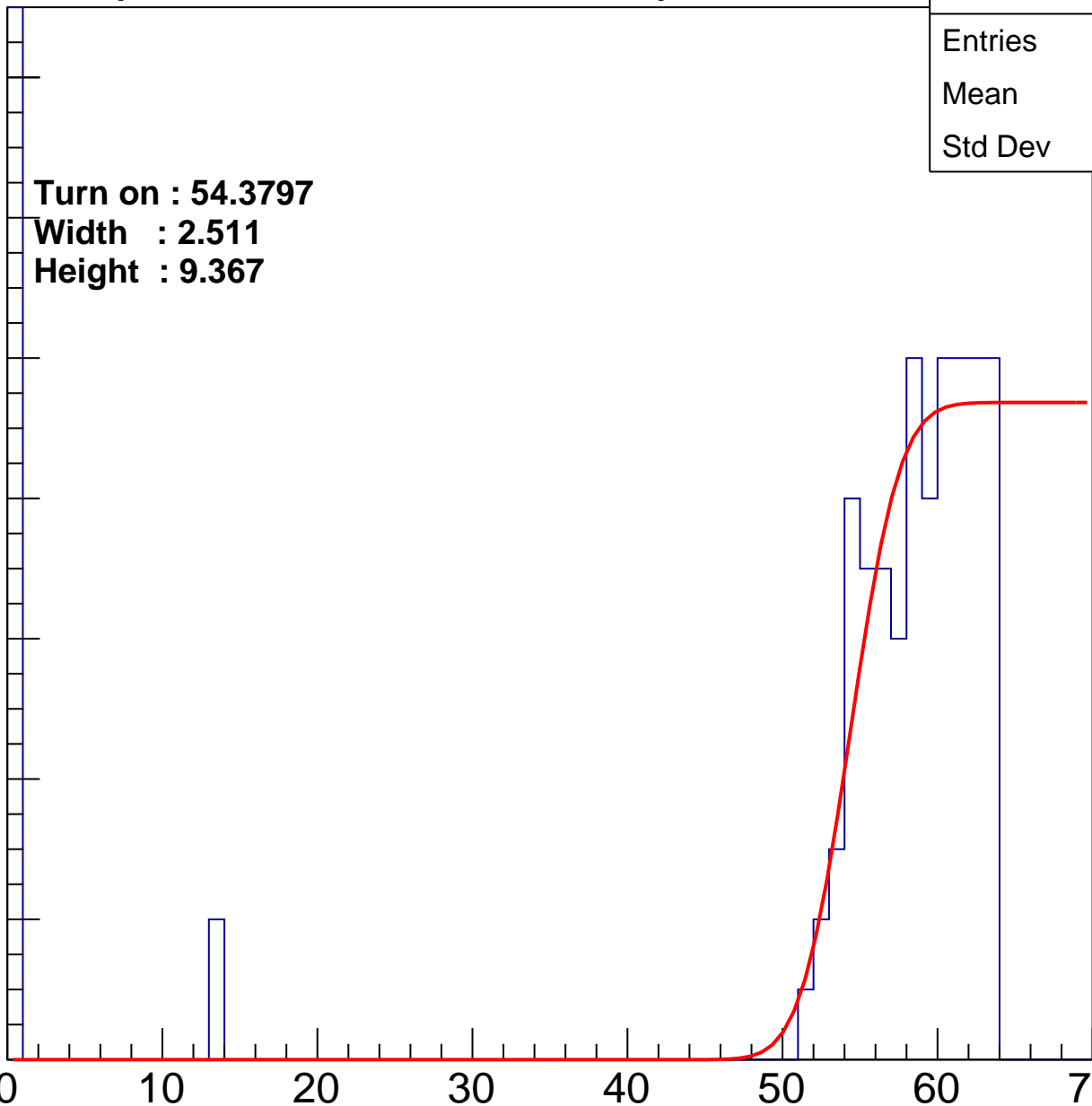
Entry

14
12
10
8
6
4
2
0

Turn on : 54.3797
Width : 2.511
Height : 9.367

| | |
|---------|-------|
| Entries | 164 |
| Mean | 32.95 |
| Std Dev | 28.96 |

ampl



B1L104S, U6-ch80

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 204 |
| Mean | 31.59 |
| Std Dev | 28.77 |

Turn on : 53.0484

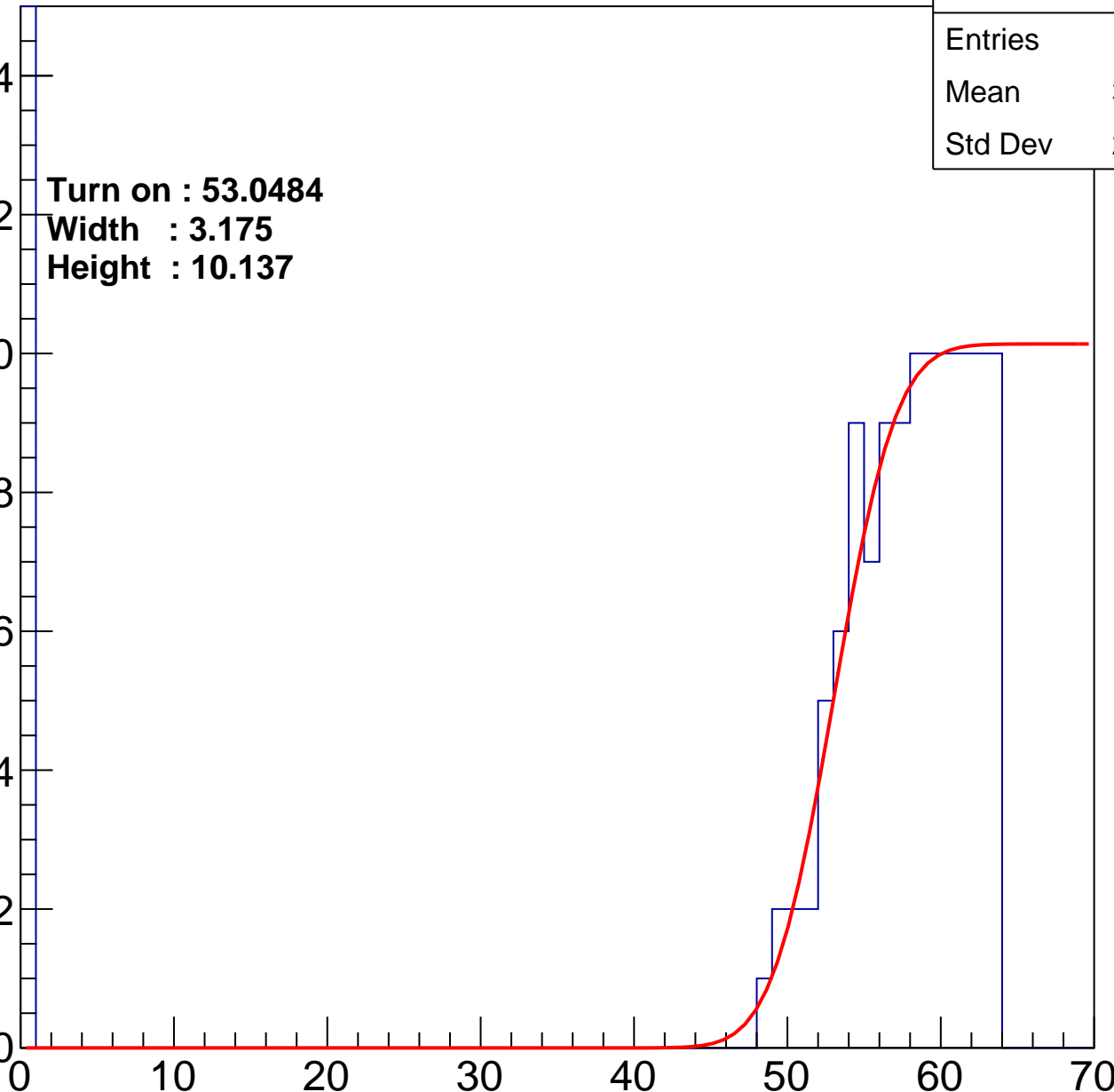
Width : 3.175

Height : 10.137

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch81

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 170 |
| Mean | 40.82 |
| Std Dev | 25.86 |

Turn on : 52.2684

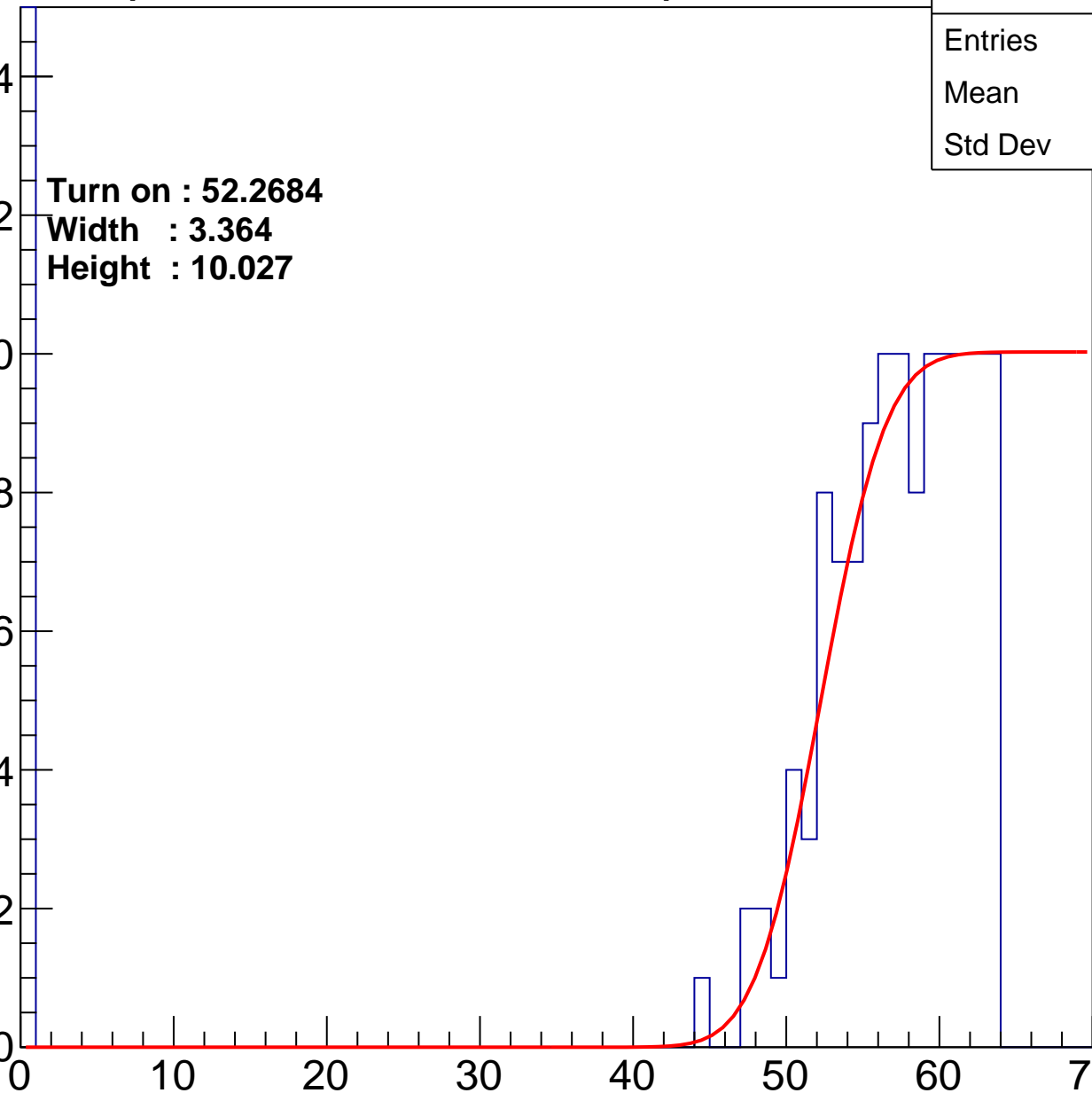
Width : 3.364

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch82

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 195 |
| Mean | 31.44 |
| Std Dev | 28.94 |

Turn on : 53.5043

Width : 2.449

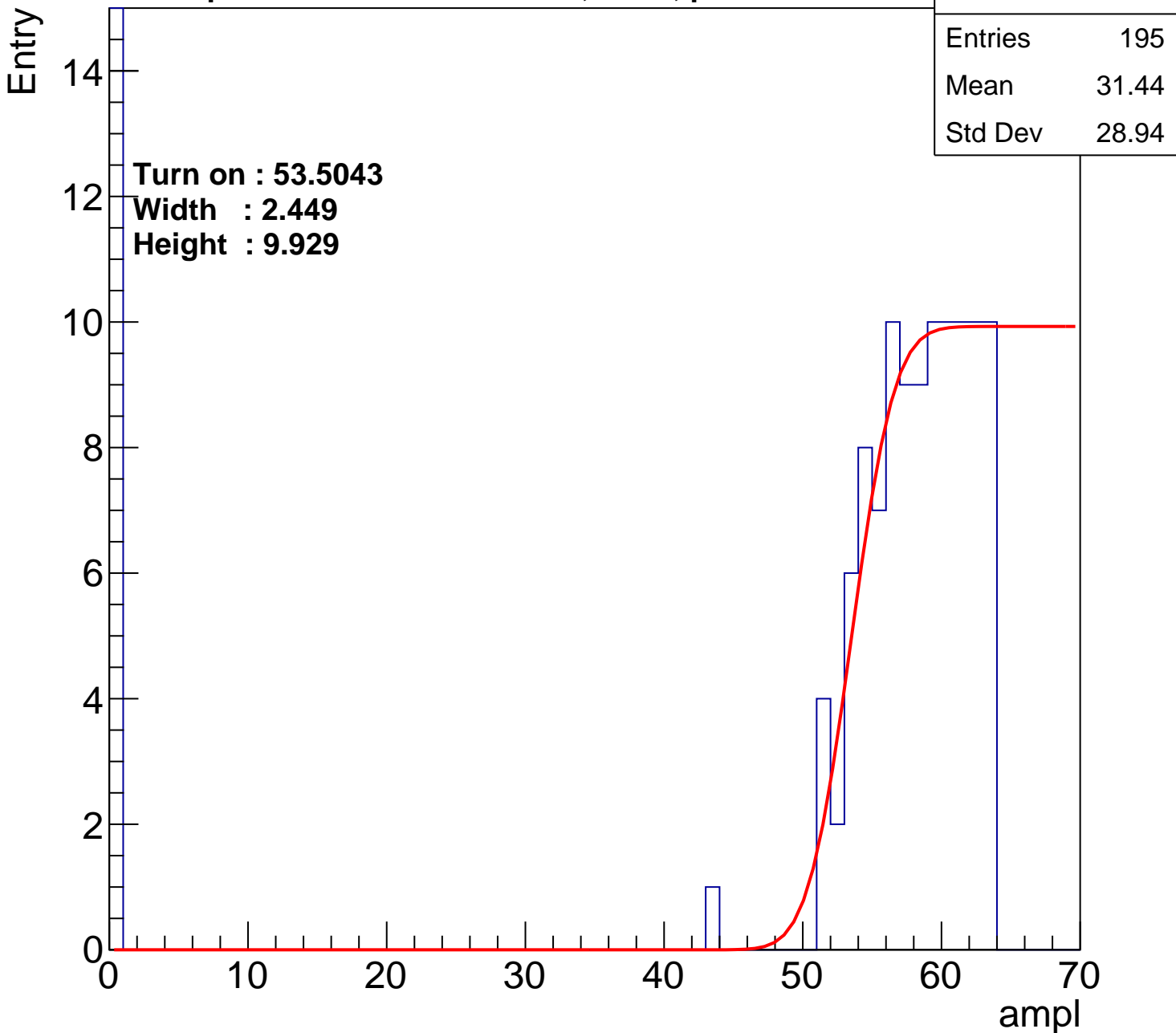
Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch83

calib_packv5_033123_0516.root, FC#4, port A1

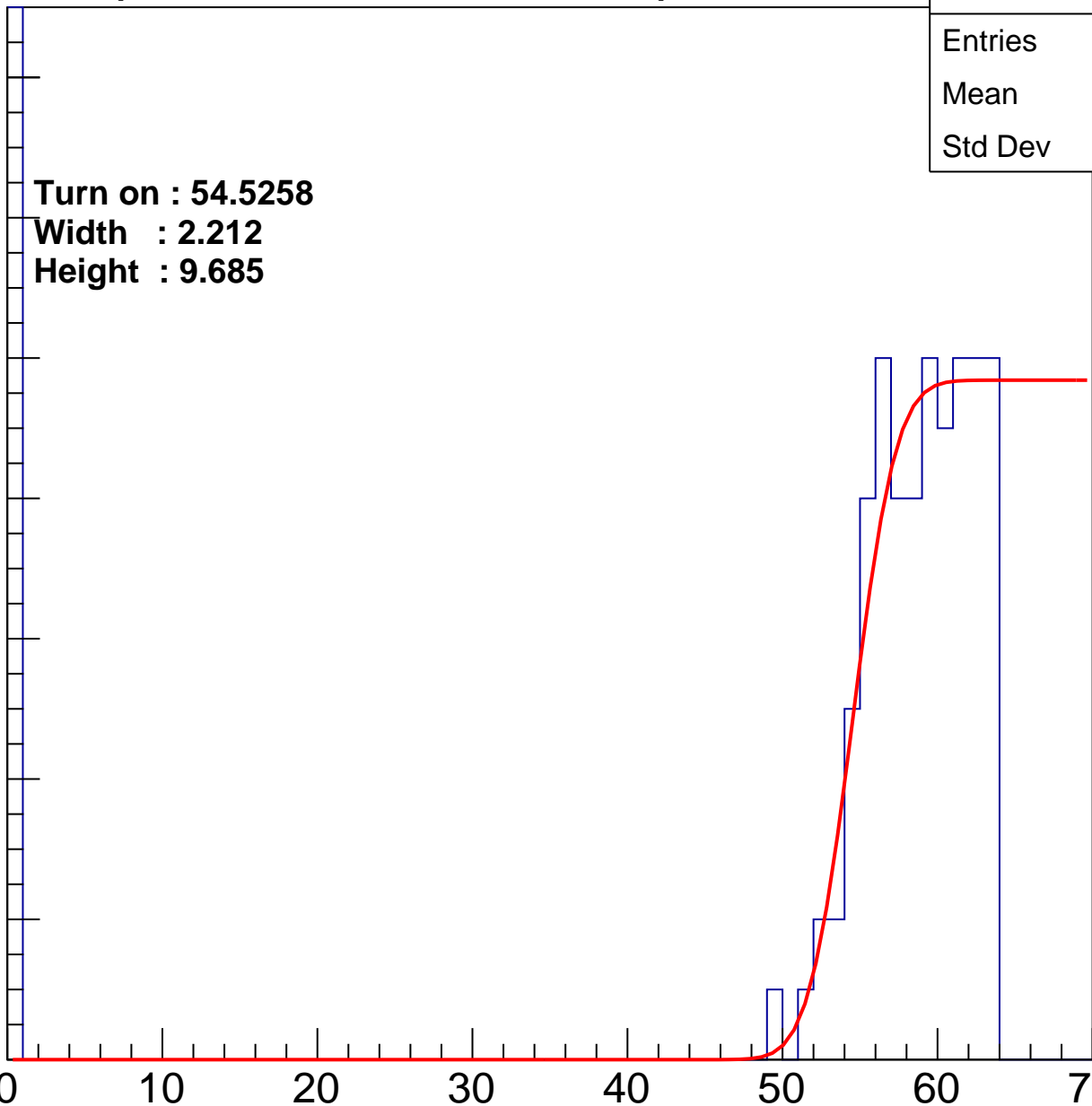
Entry

14
12
10
8
6
4
2
0

Turn on : 54.5258
Width : 2.212
Height : 9.685

| | |
|---------|-------|
| Entries | 183 |
| Mean | 30 |
| Std Dev | 29.28 |

ampl



B1L104S, U6-ch84

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 217 |
| Mean | 28.47 |
| Std Dev | 28.78 |

Turn on : 54.7236

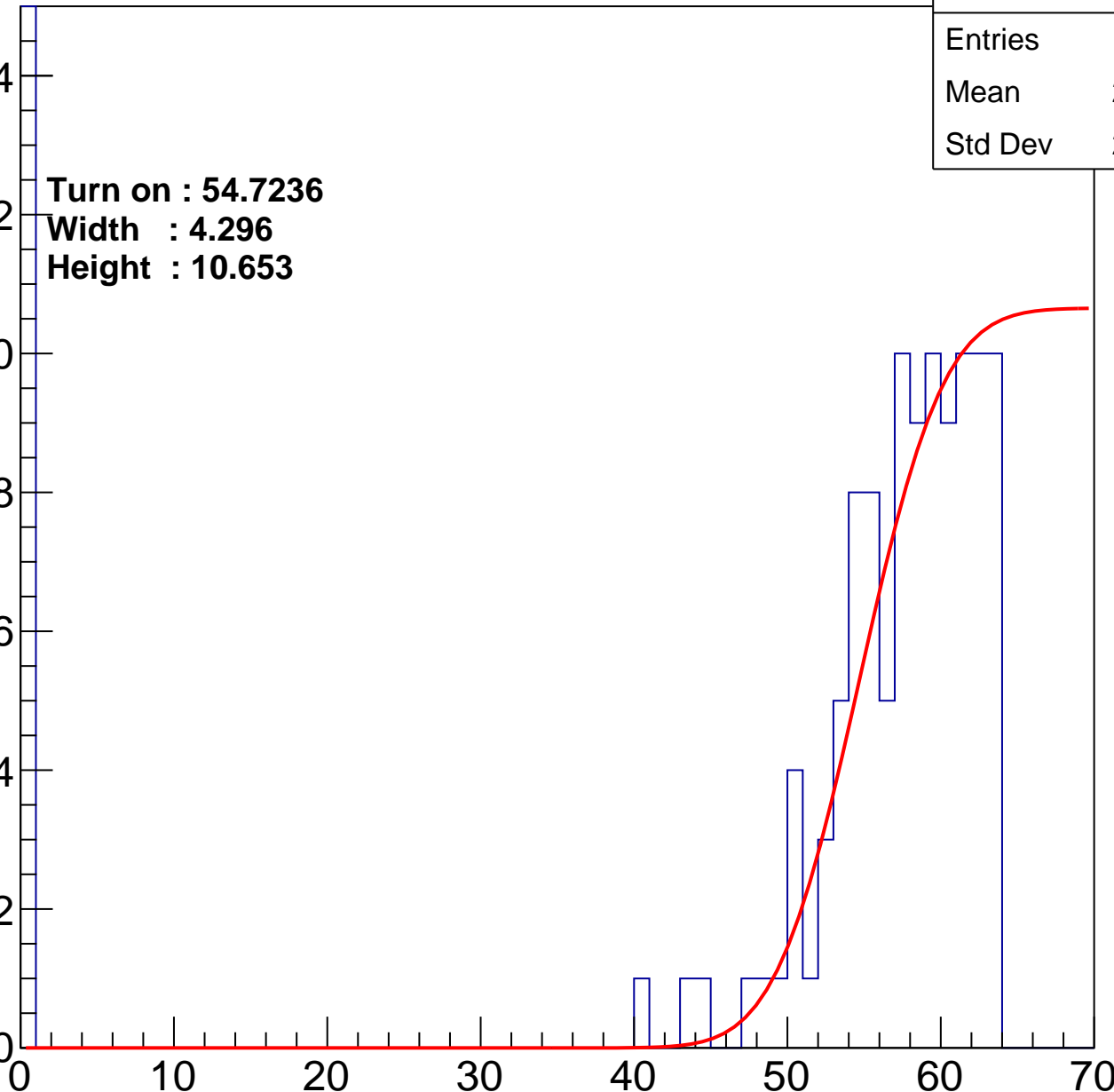
Width : 4.296

Height : 10.653

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch85

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 203 |
| Mean | 28.82 |
| Std Dev | 29.07 |

Turn on : 53.2329

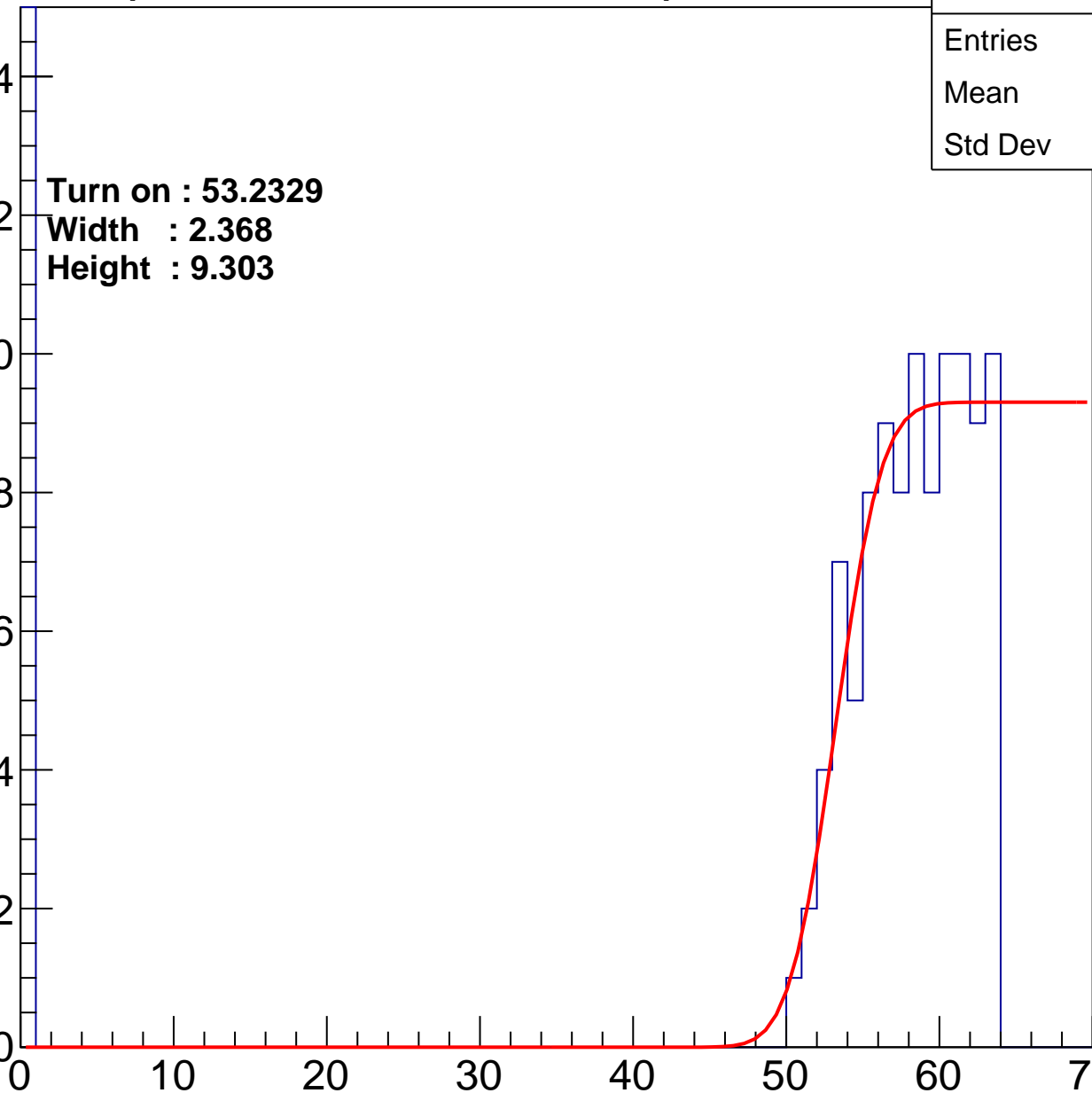
Width : 2.368

Height : 9.303

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch86

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 215 |
| Mean | 32.77 |
| Std Dev | 28.26 |

Turn on : 52.0989

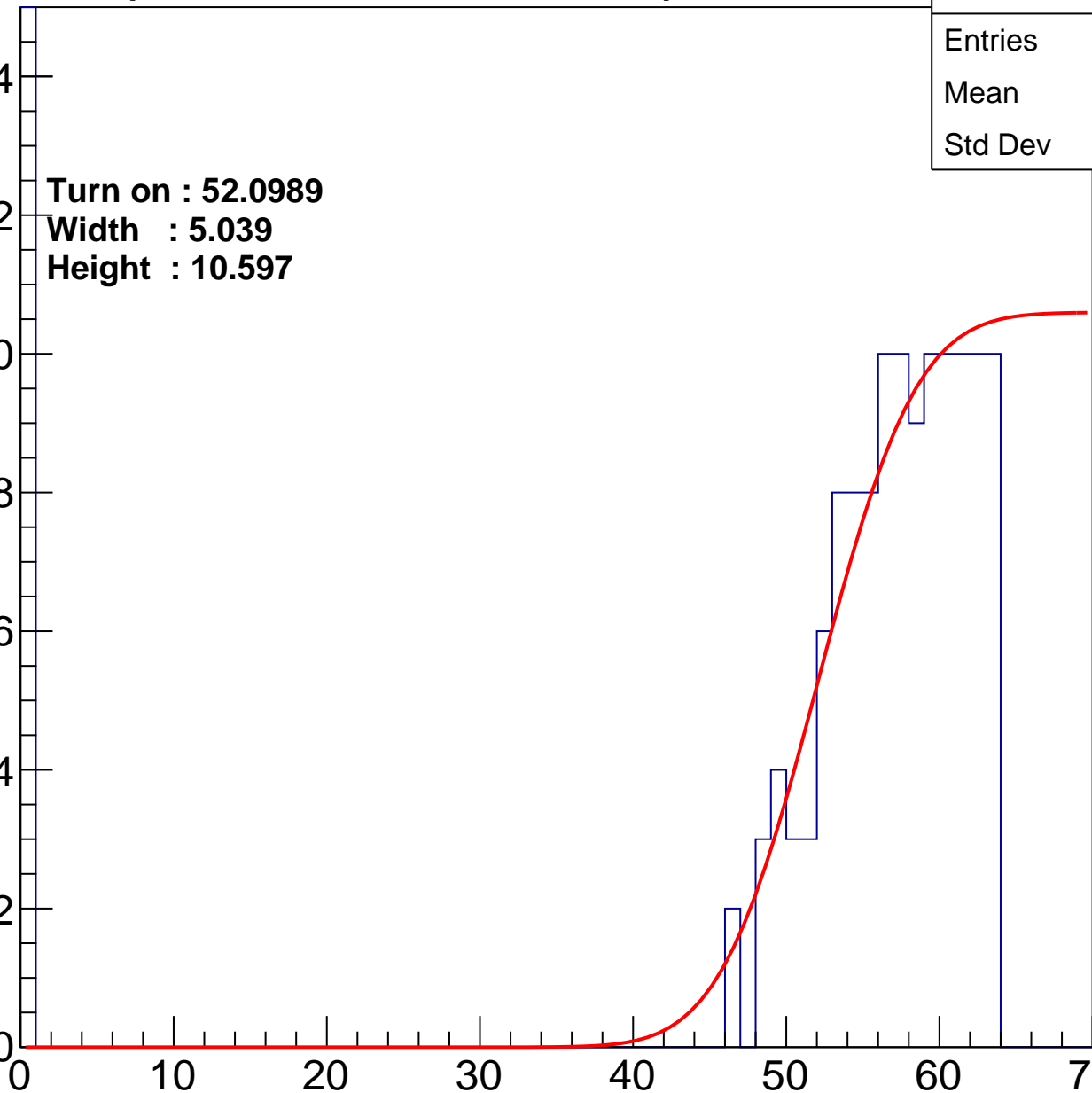
Width : 5.039

Height : 10.597

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch87

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 202 |
| Mean | 33.57 |
| Std Dev | 28.24 |

Turn on : 52.6252

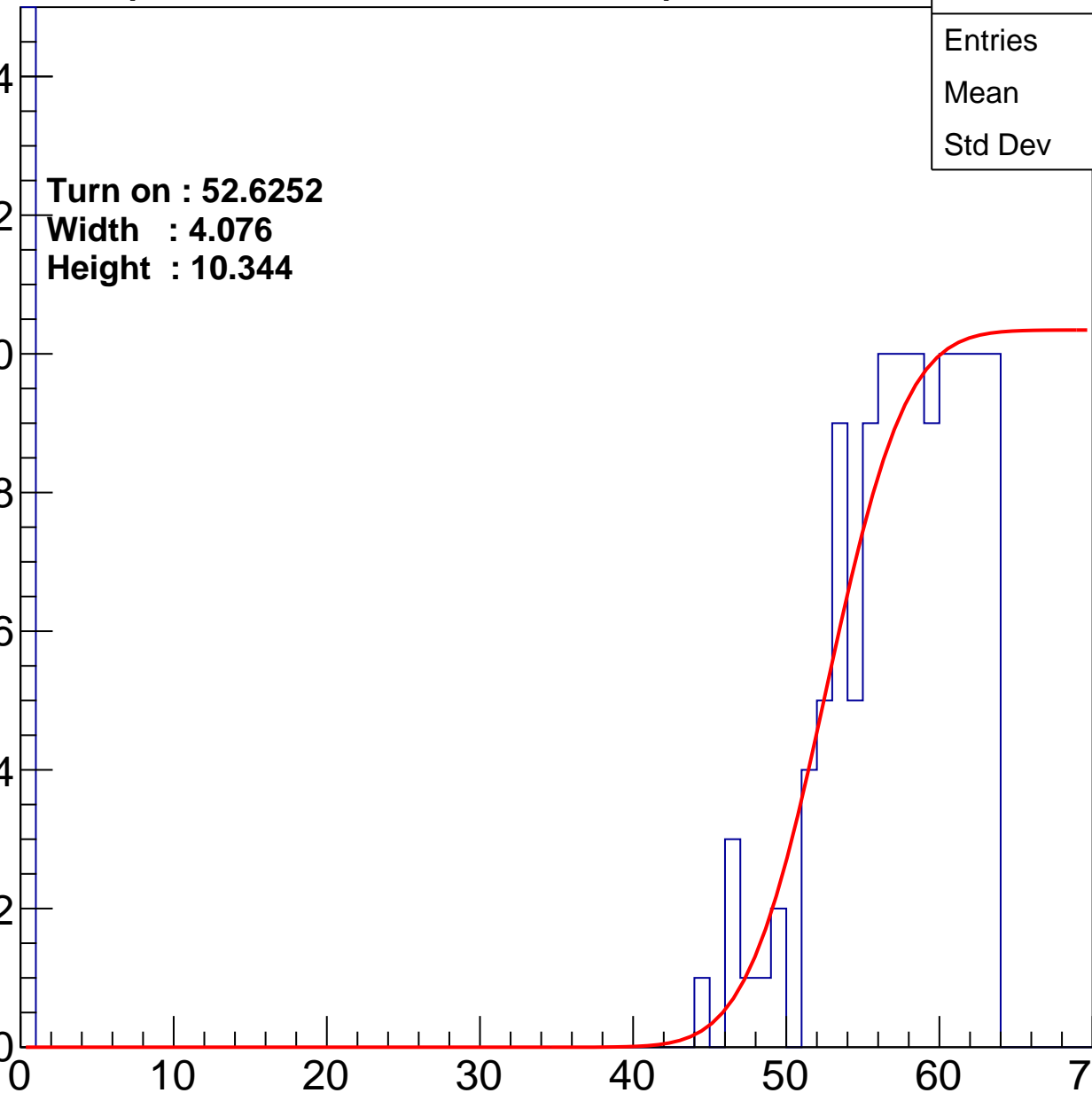
Width : 4.076

Height : 10.344

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch88

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 196 |
| Mean | 31.97 |
| Std Dev | 28.72 |

Turn on : 53.6501

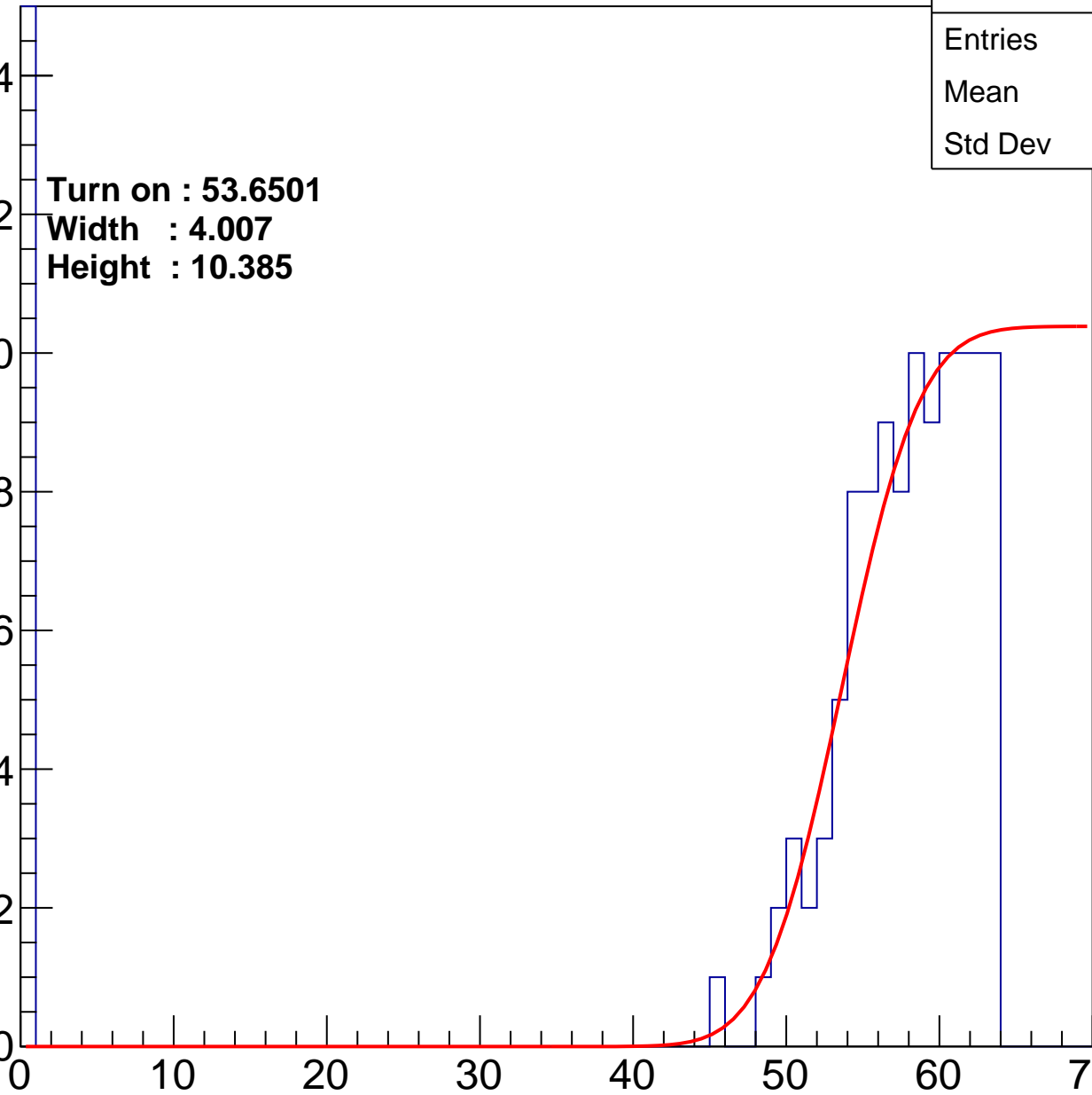
Width : 4.007

Height : 10.385

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch89

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 206 |
| Mean | 28.34 |
| Std Dev | 29.04 |

Turn on : 54.4377

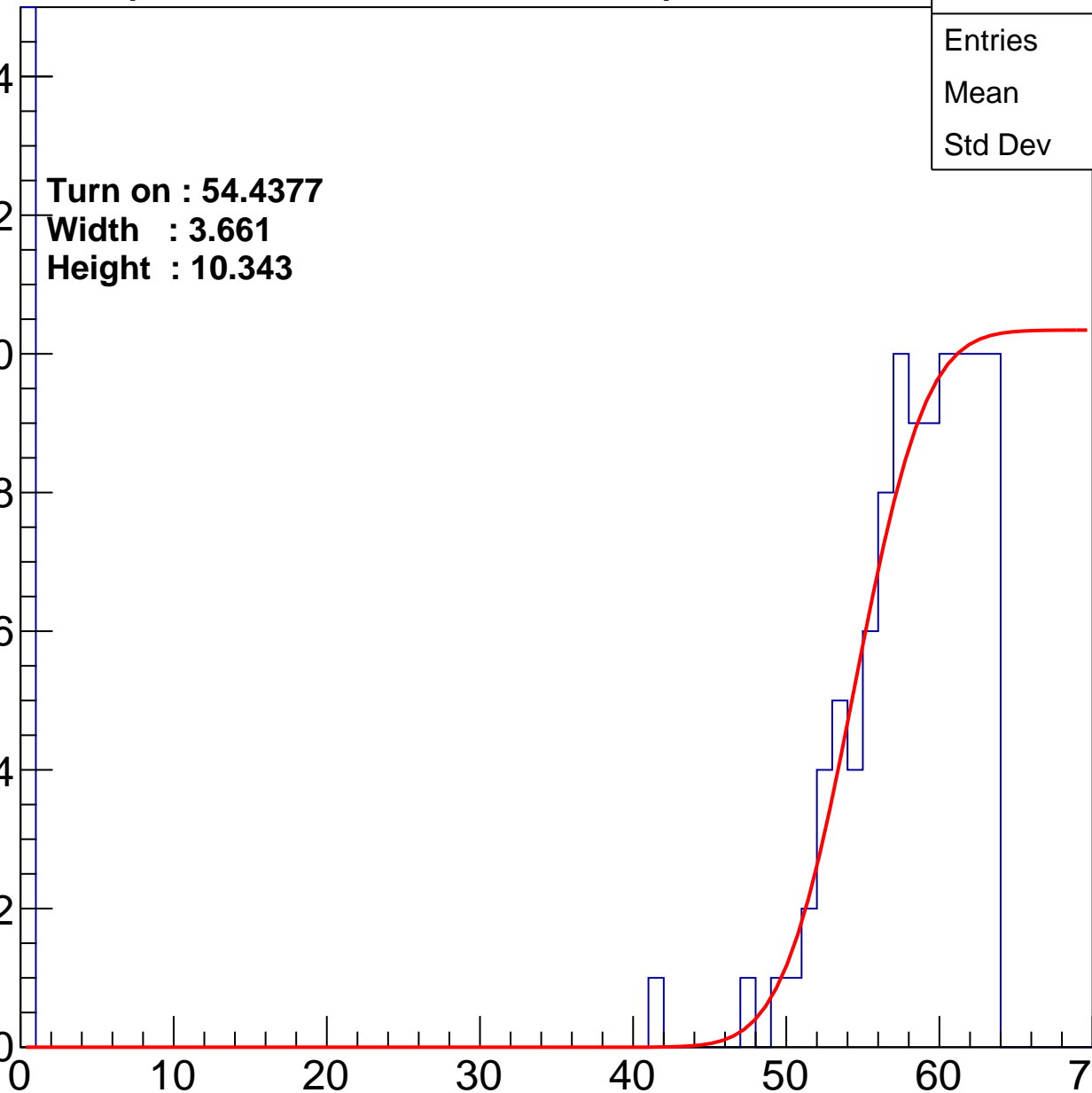
Width : 3.661

Height : 10.343

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch90

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 192 |
| Mean | 31.7 |
| Std Dev | 28.74 |

Turn on : 54.3946

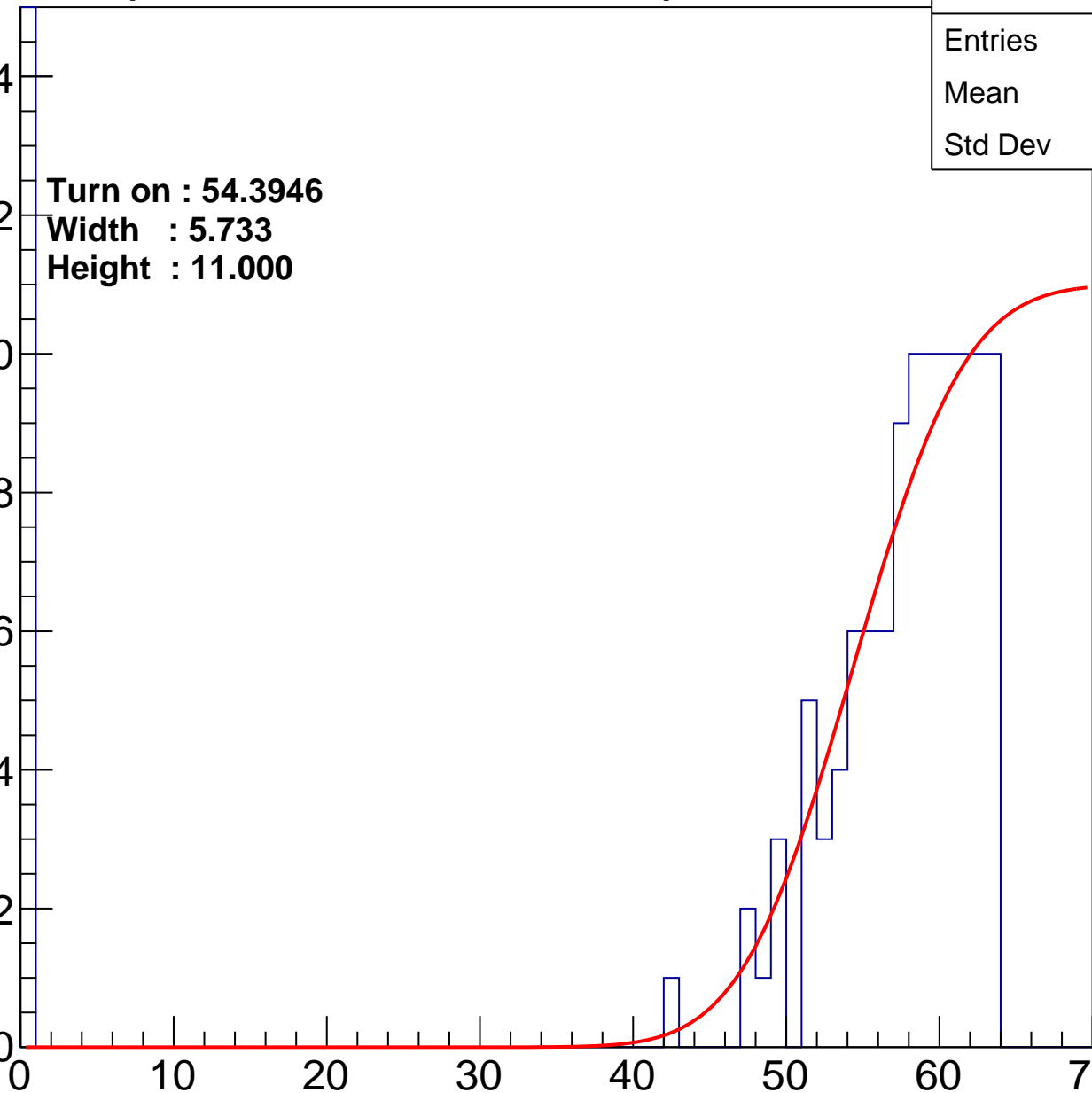
Width : 5.733

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch91

calib_packv5_033123_0516.root, FC#4, port A1

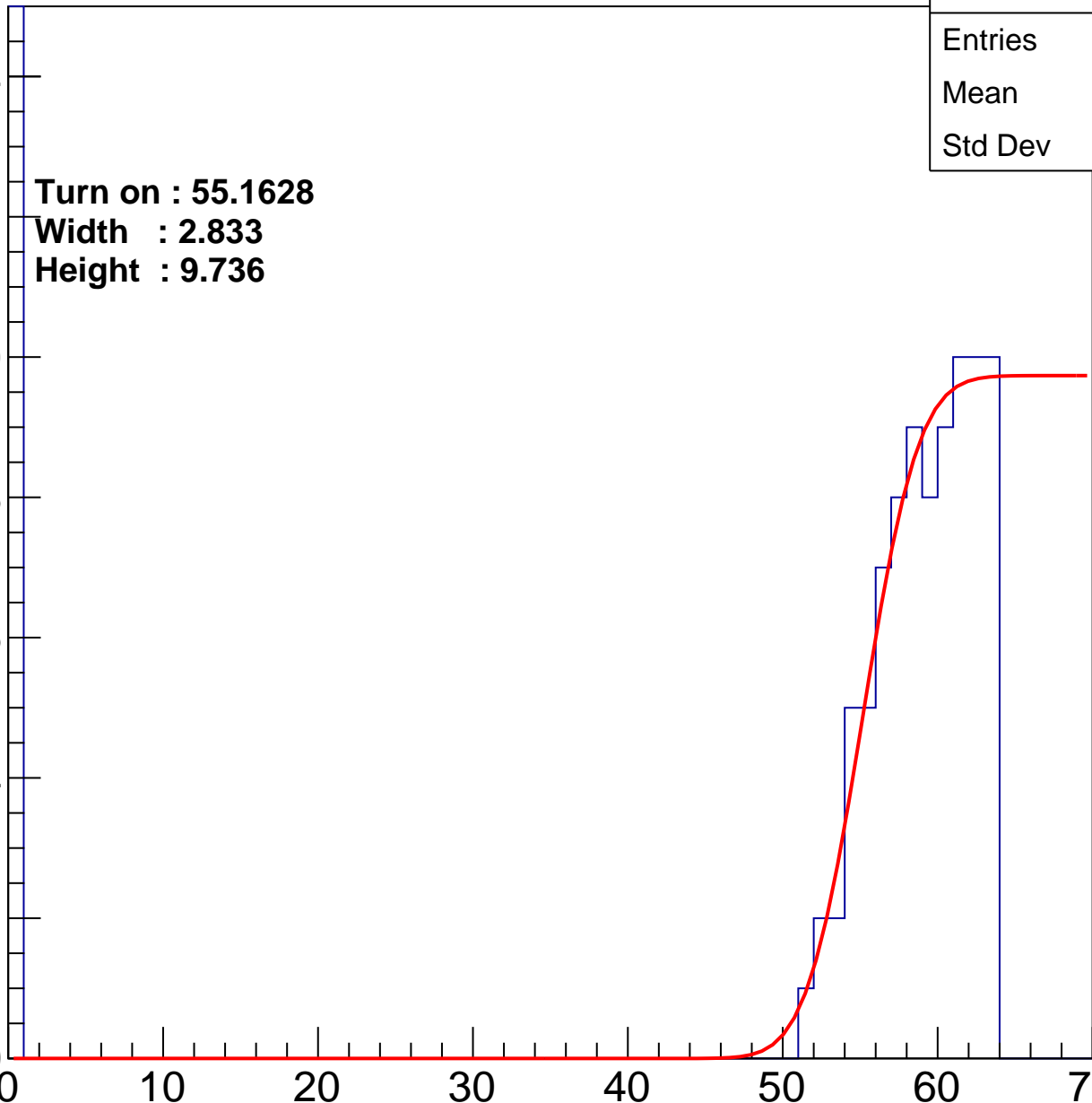
Entry

14
12
10
8
6
4
2
0

Turn on : 55.1628
Width : 2.833
Height : 9.736

| | |
|---------|-------|
| Entries | 154 |
| Mean | 32.78 |
| Std Dev | 29.24 |

ampl



B1L104S, U6-ch92

calib_packv5_033123_0516.root, FC#4, port A1

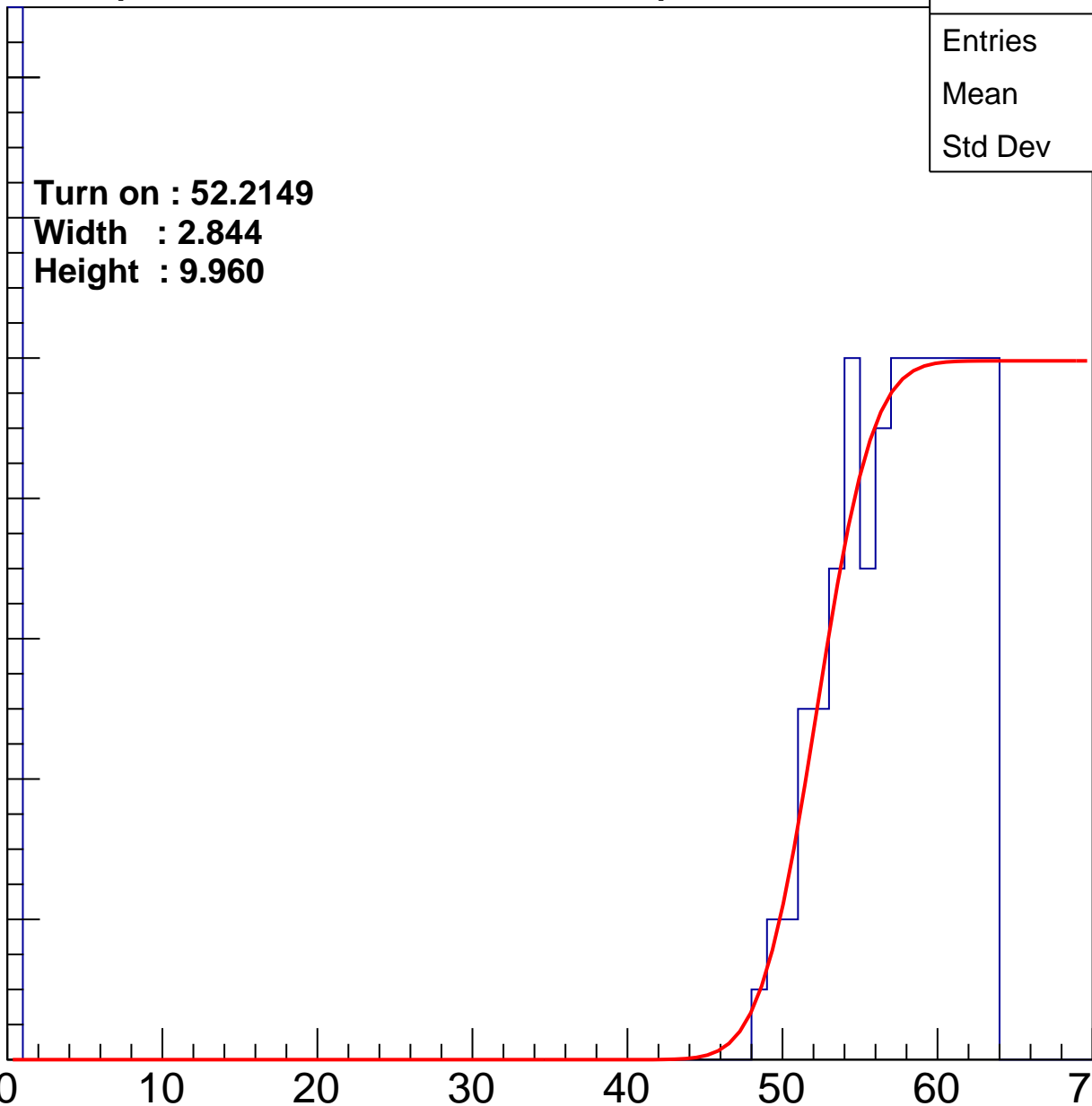
Entry

14
12
10
8
6
4
2
0

Turn on : 52.2149
Width : 2.844
Height : 9.960

| | |
|---------|-------|
| Entries | 233 |
| Mean | 29.02 |
| Std Dev | 28.78 |

ampl



B1L104S, U6-ch93

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 201 |
| Mean | 31.71 |
| Std Dev | 28.7 |

Turn on : 53.5537

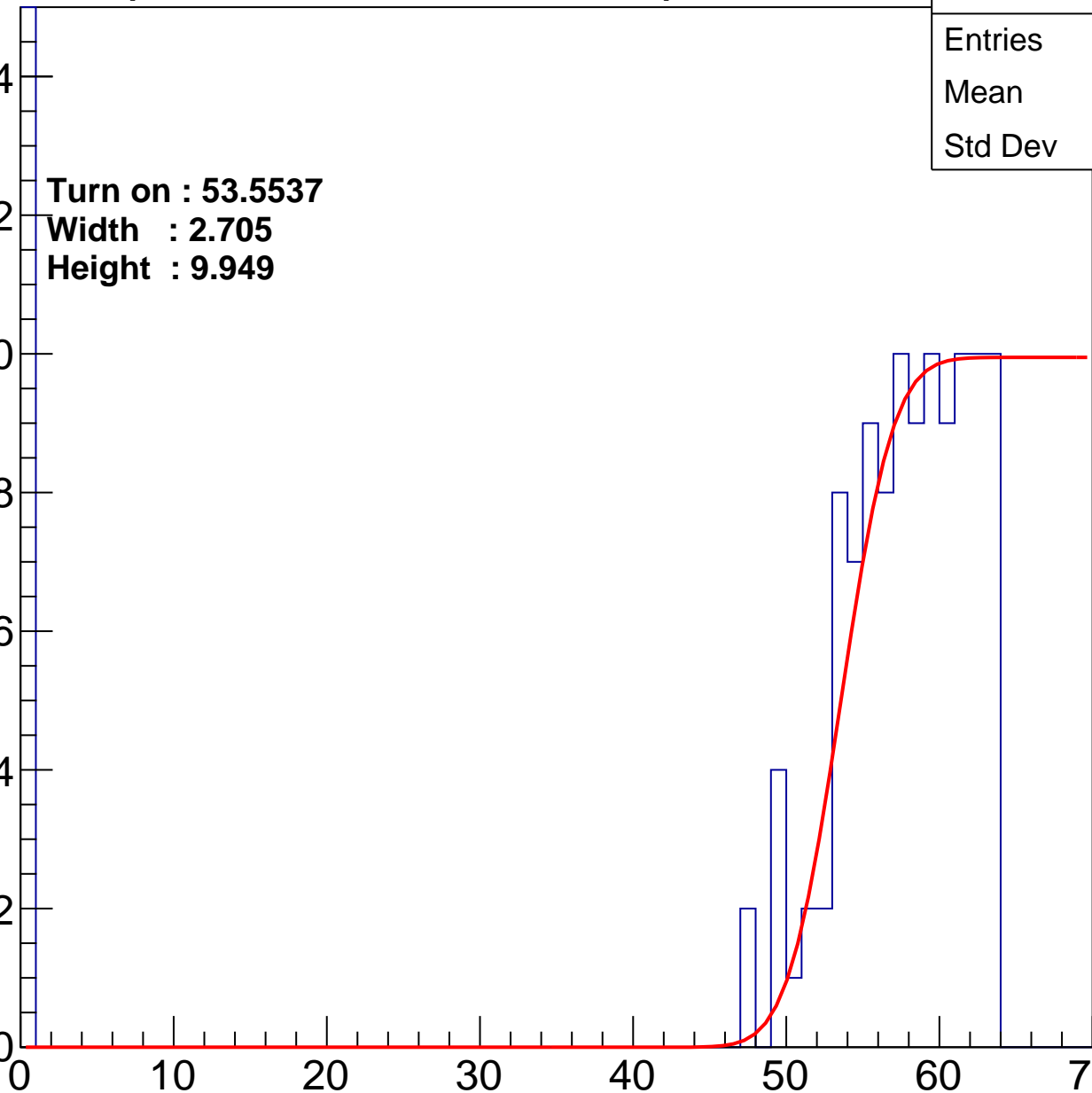
Width : 2.705

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch94

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 203 |
| Mean | 29.46 |
| Std Dev | 28.88 |

Turn on : 52.7904

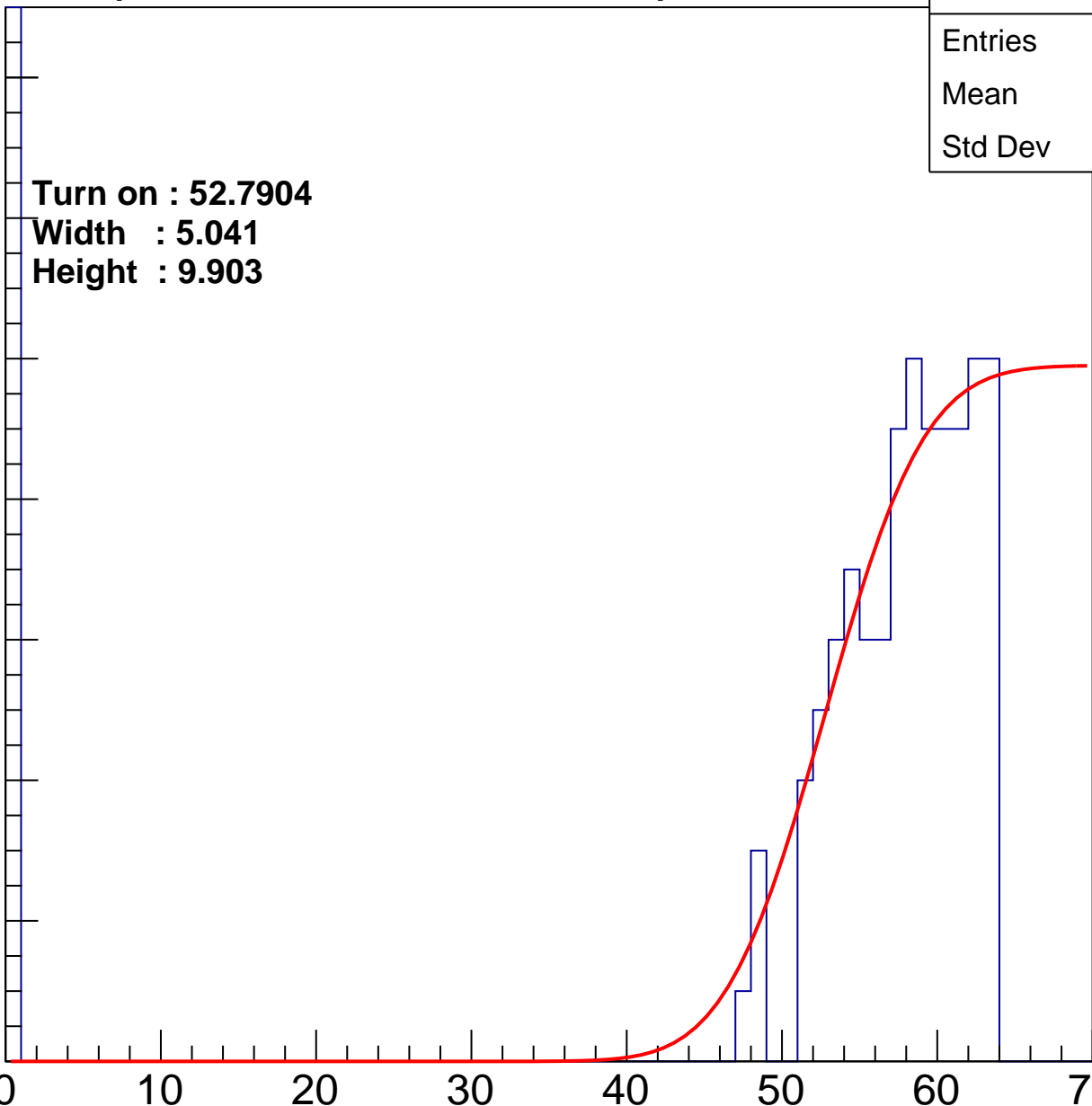
Width : 5.041

Height : 9.903

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch95

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 32.07 |
| Std Dev | 28.99 |

Turn on : 54.6811

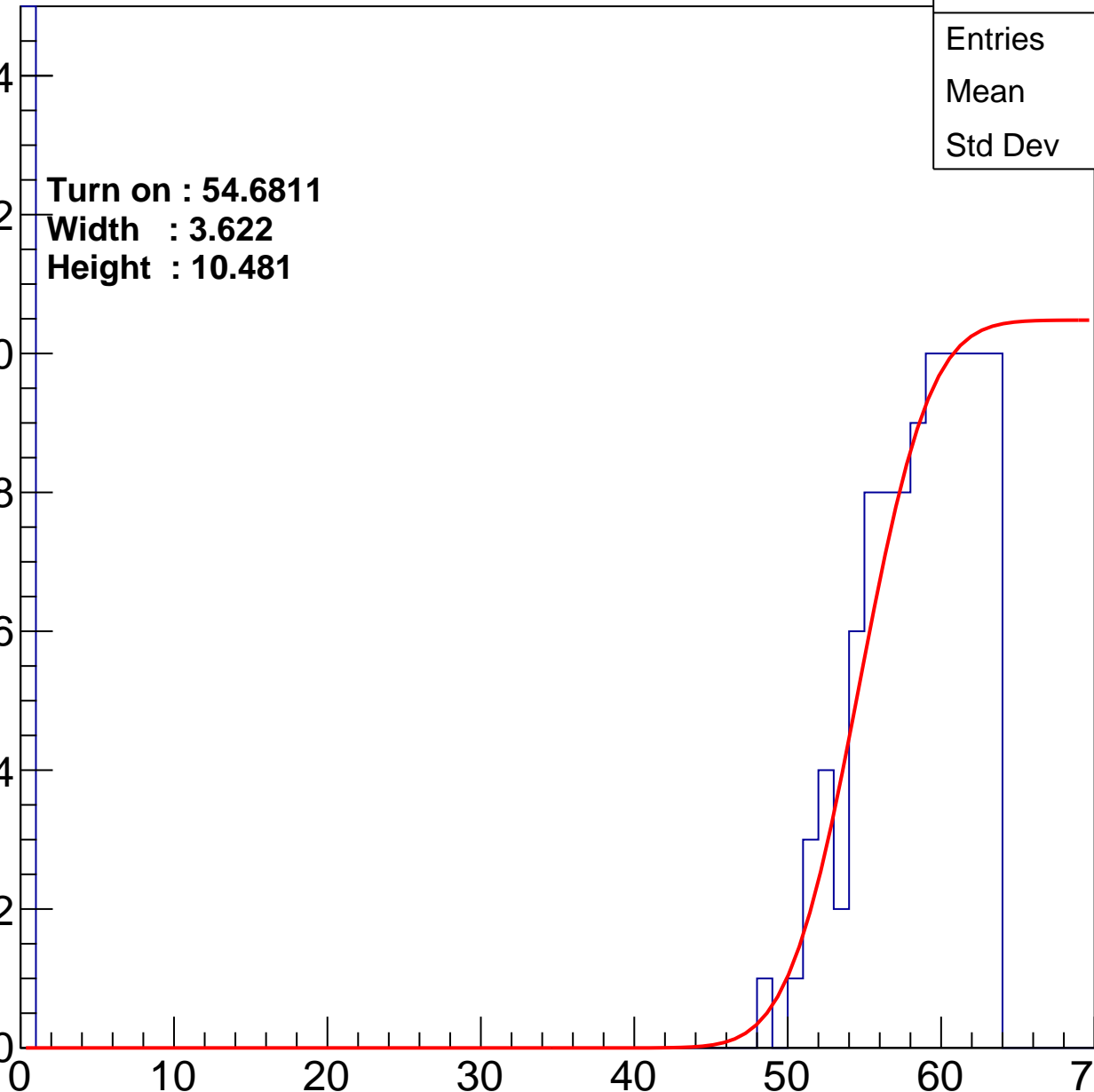
Width : 3.622

Height : 10.481

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch96

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 228 |
| Mean | 31.59 |
| Std Dev | 28.36 |

Turn on : 51.3557

Width : 2.788

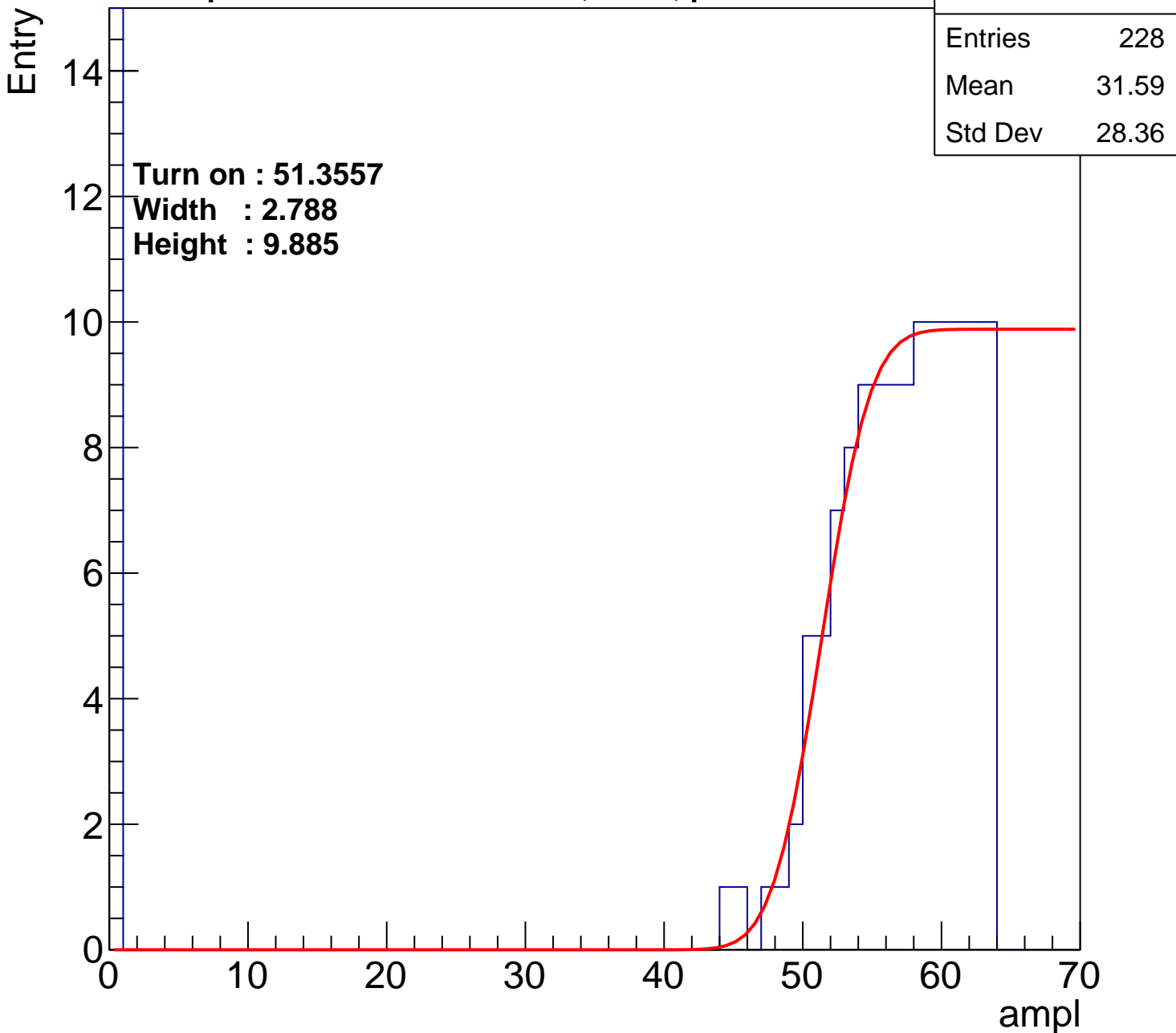
Height : 9.885

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U6-ch97

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 200 |
| Mean | 32.35 |
| Std Dev | 28.56 |

Turn on : 53.6088

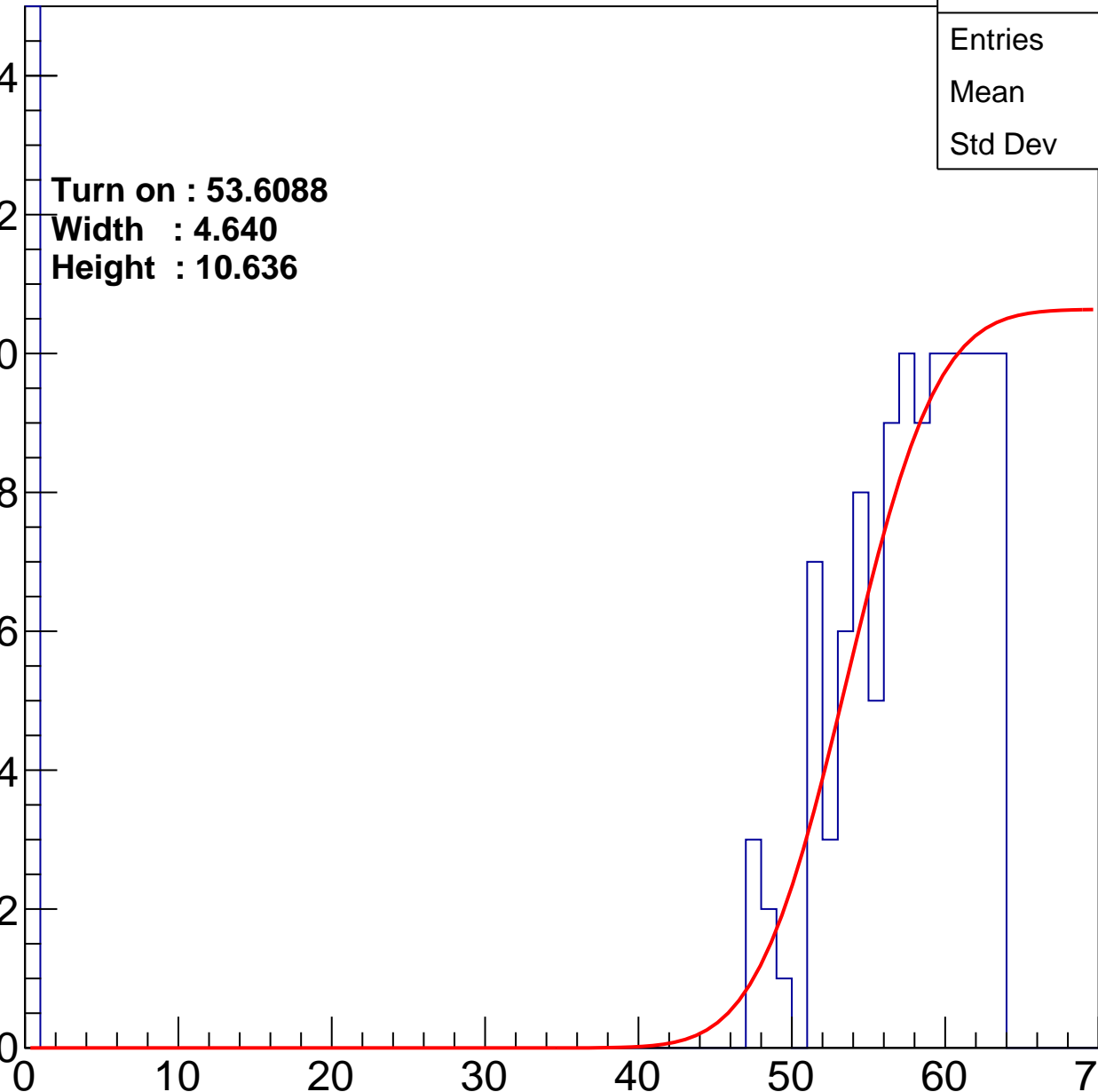
Width : 4.640

Height : 10.636

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch98

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 219 |
| Mean | 31.56 |
| Std Dev | 28.36 |

Turn on : 52.8843

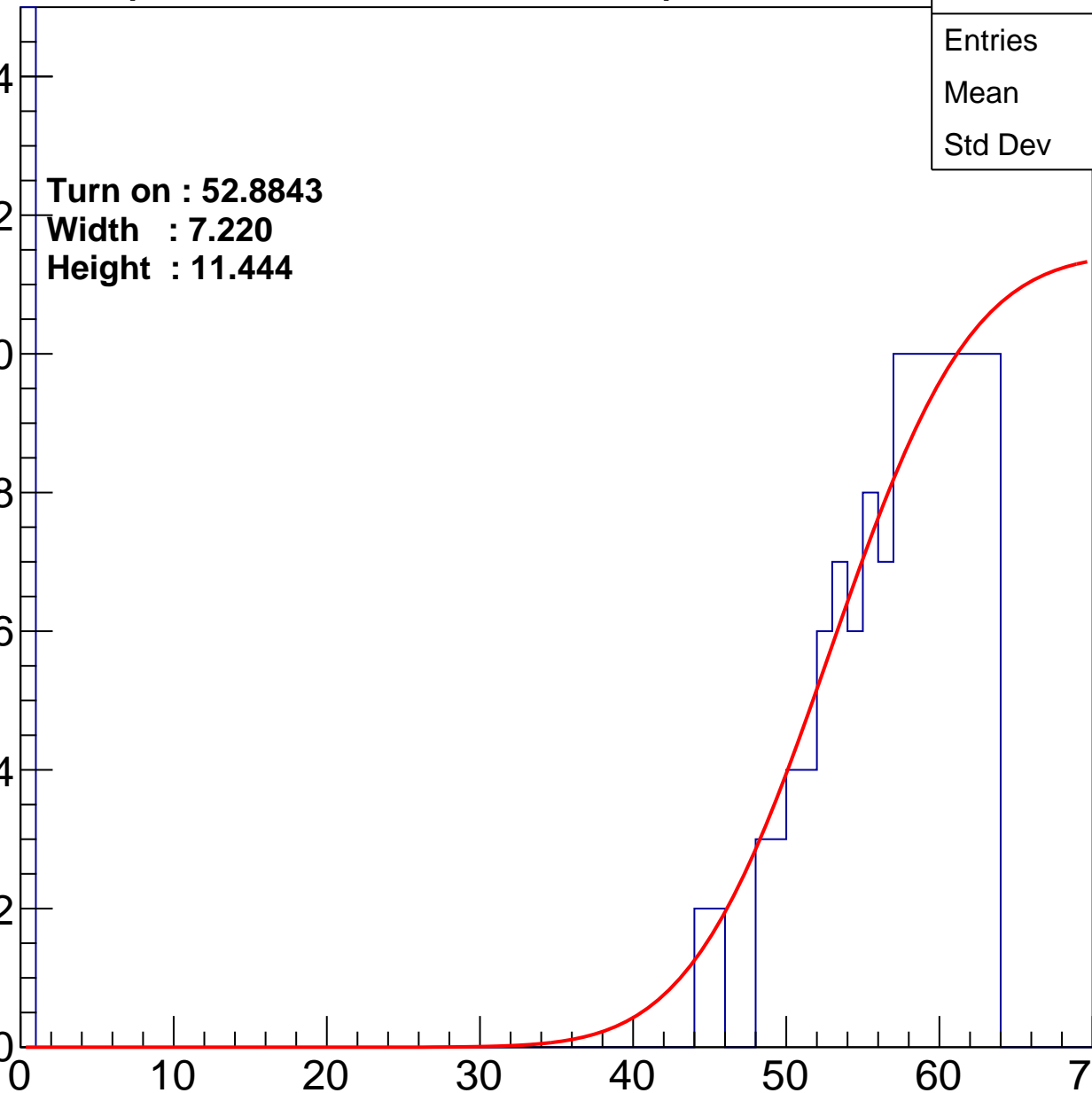
Width : 7.220

Height : 11.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch99

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 210 |
| Mean | 29.85 |
| Std Dev | 28.88 |

Turn on : 55.1186

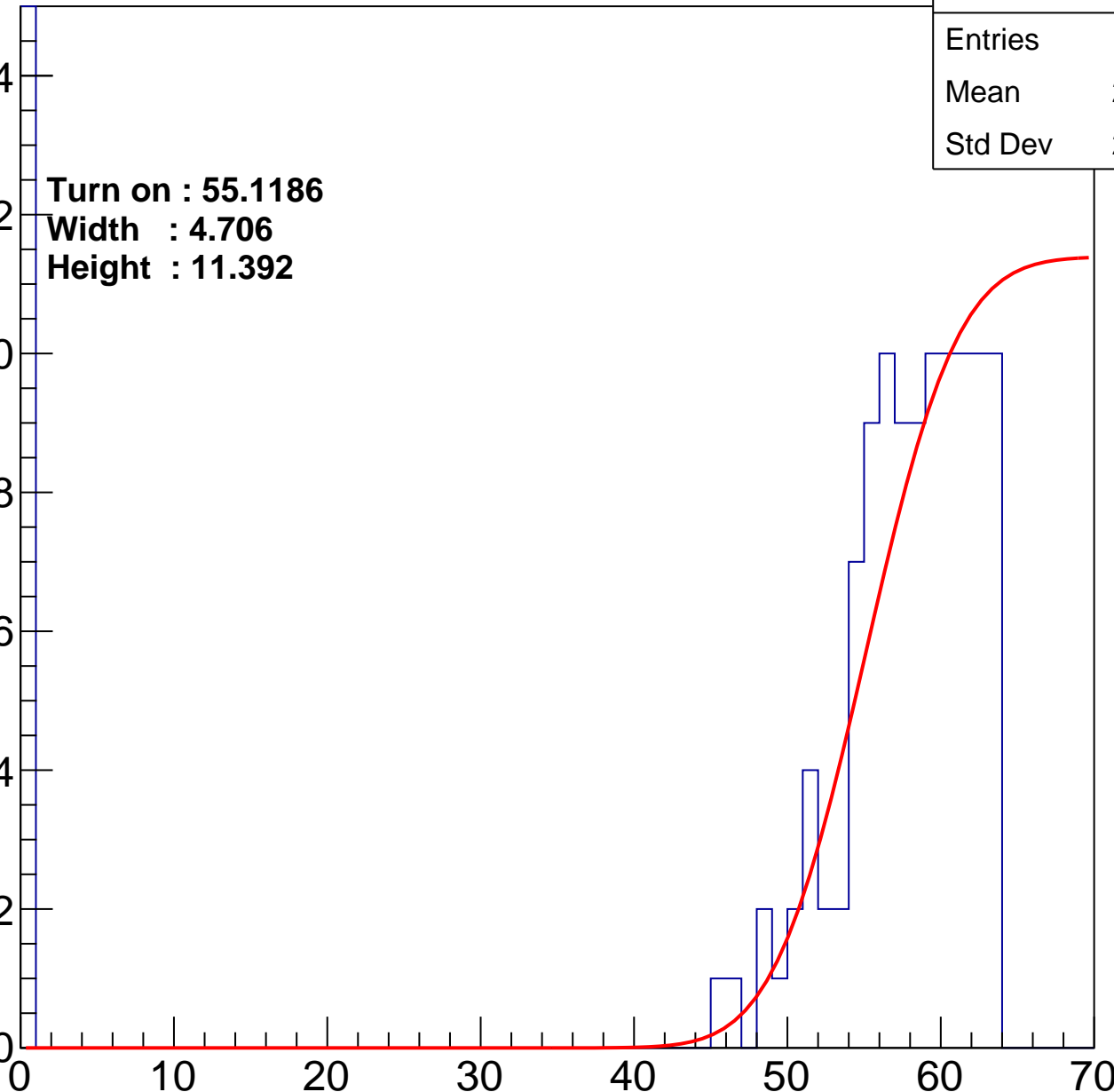
Width : 4.706

Height : 11.392

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch100

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 221 |
| Mean | 33.84 |
| Std Dev | 27.77 |

Turn on : 50.9673

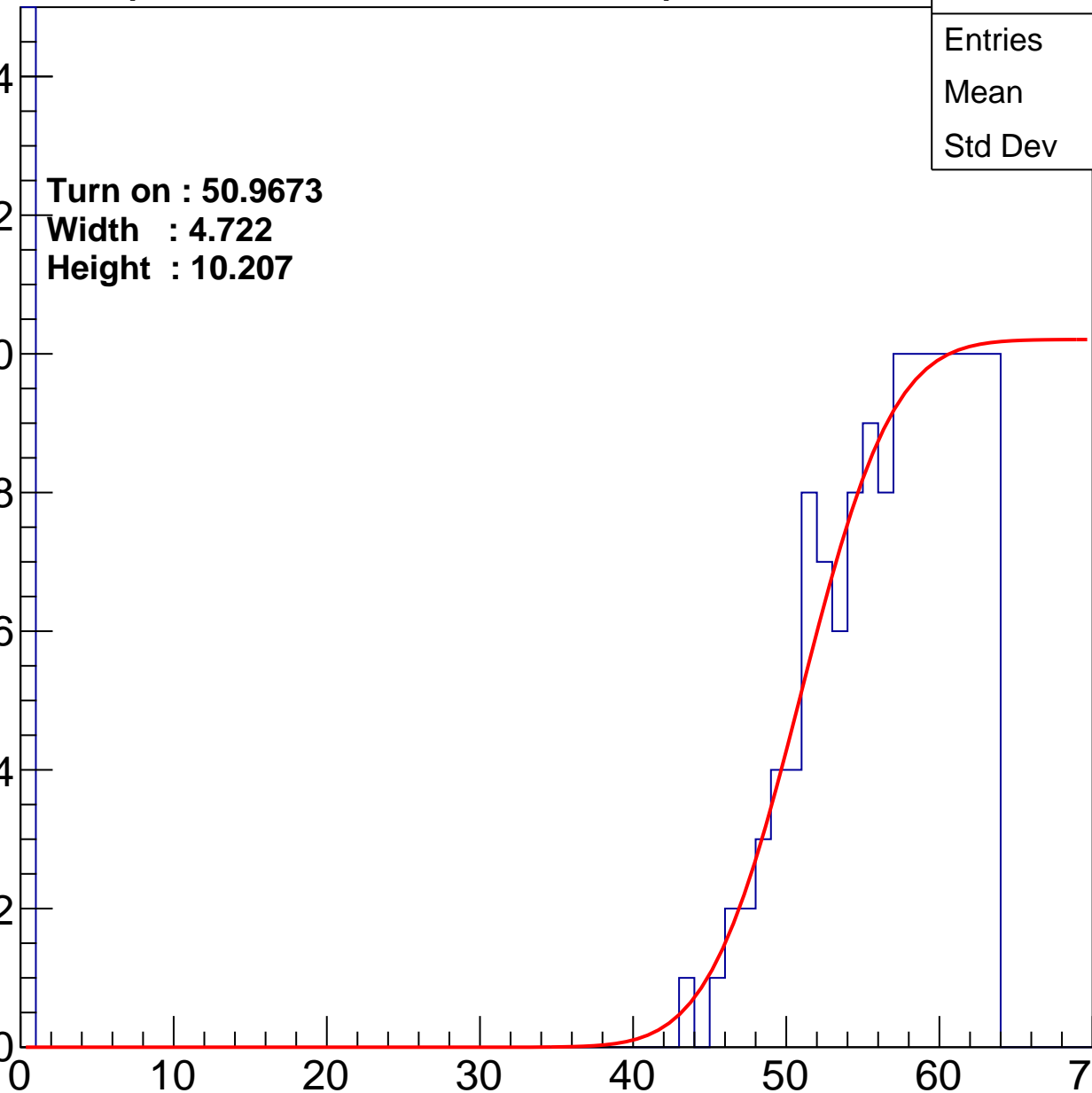
Width : 4.722

Height : 10.207

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch101

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 192 |
| Mean | 35.31 |
| Std Dev | 27.87 |

Turn on : 52.7329

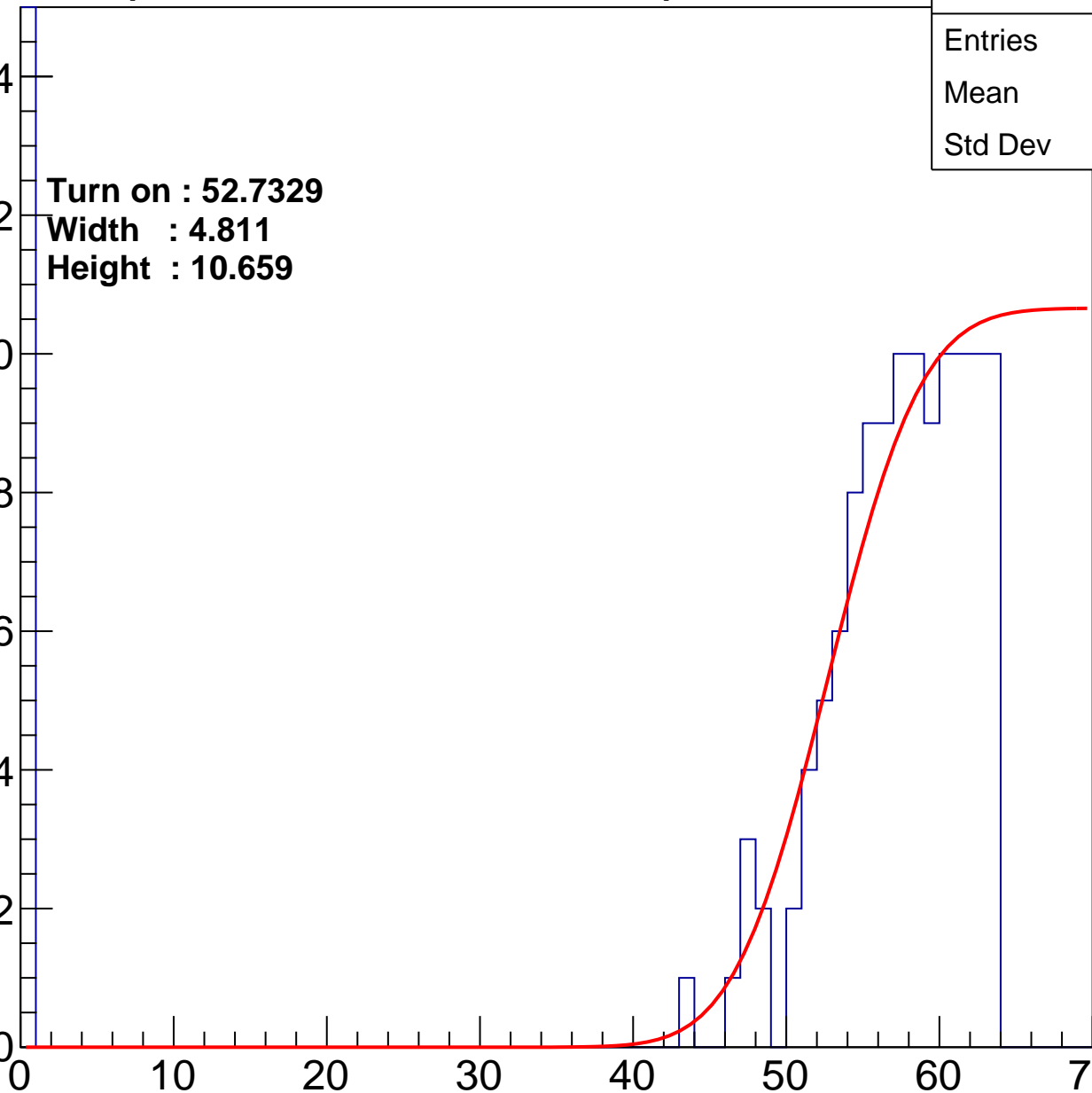
Width : 4.811

Height : 10.659

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch102

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 212 |
| Mean | 30.77 |
| Std Dev | 28.7 |

Turn on : 54.3719

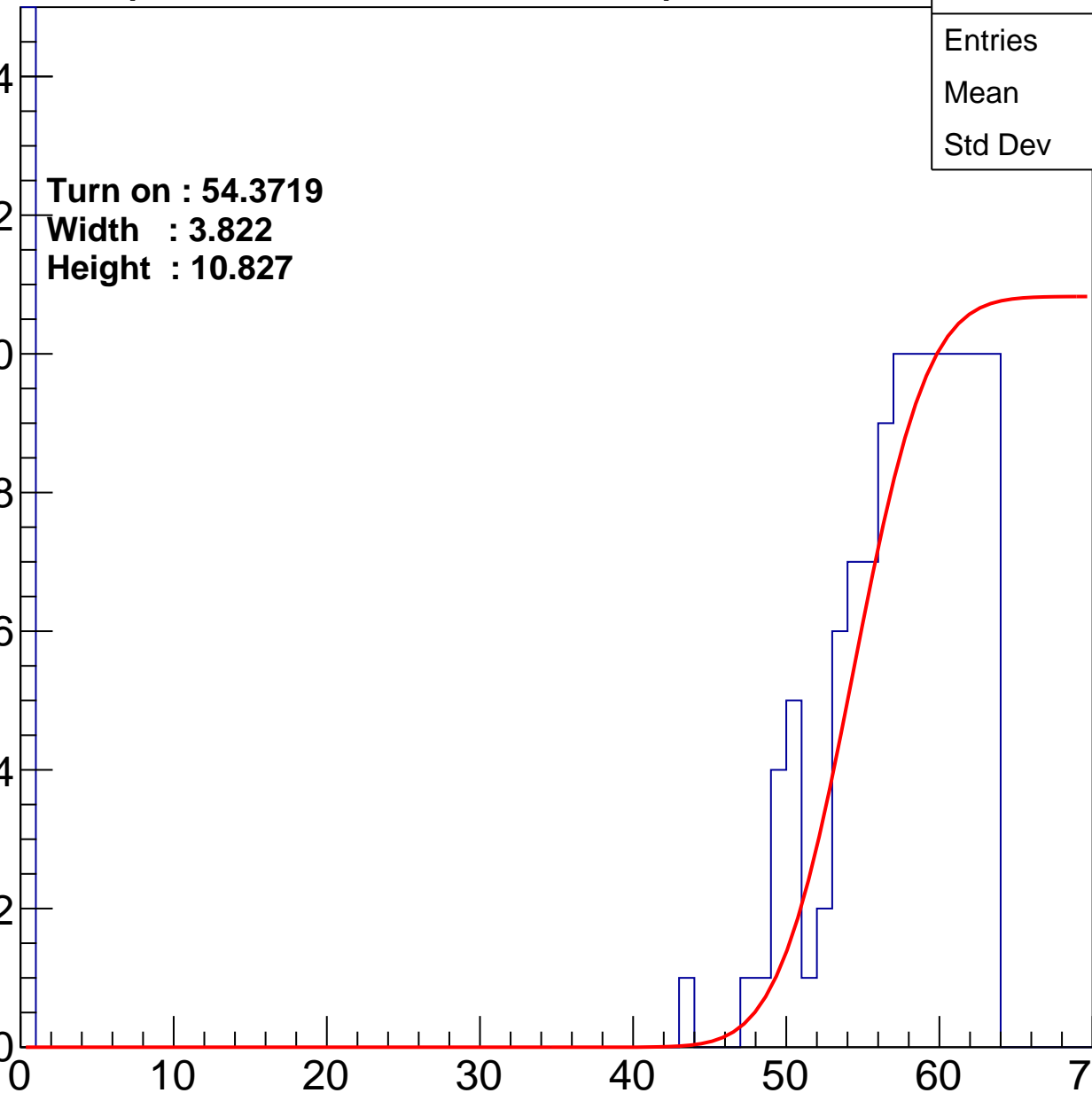
Width : 3.822

Height : 10.827

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch103

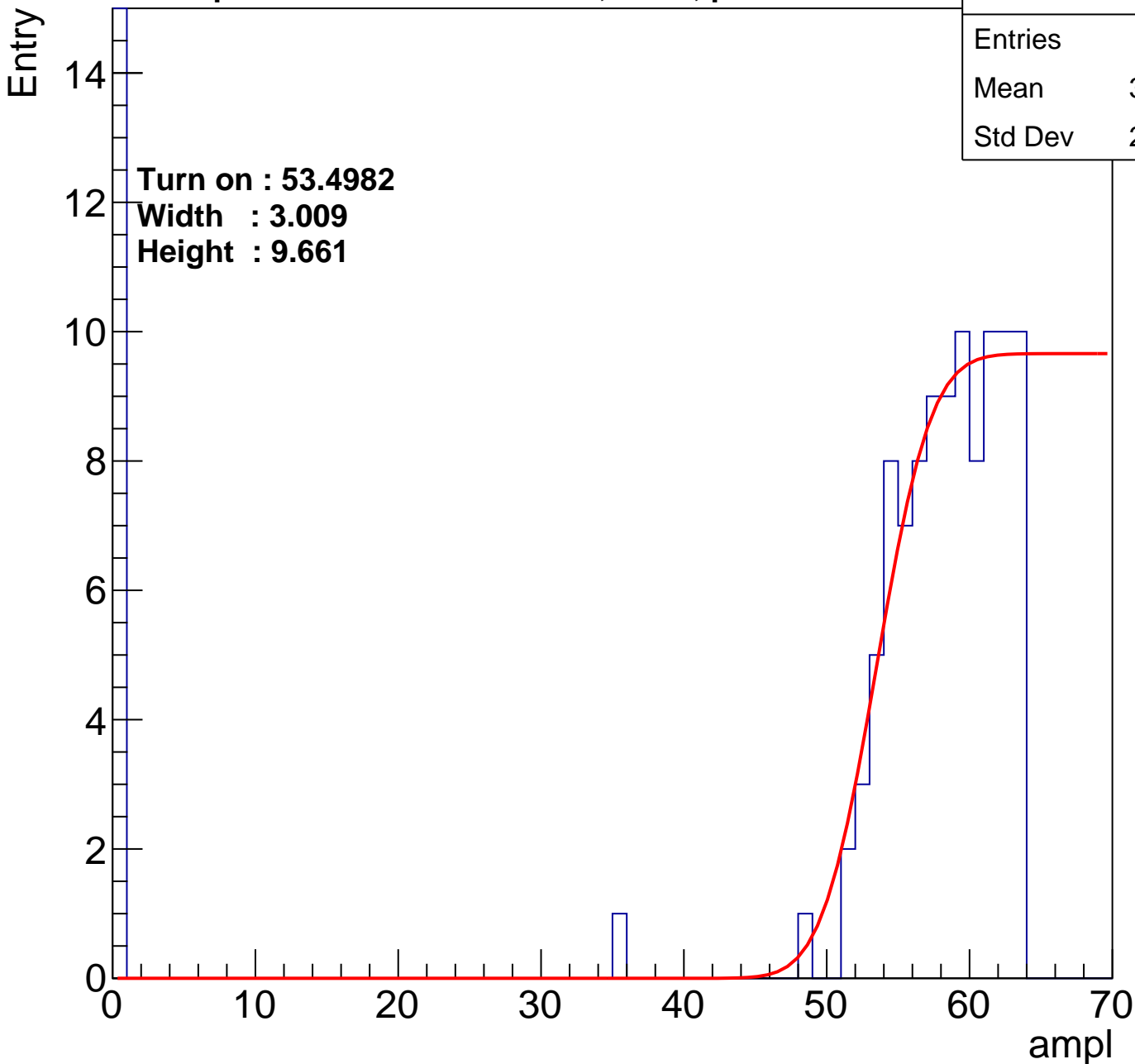
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 173 |
| Mean | 33.73 |
| Std Dev | 28.66 |

Turn on : 53.4982

Width : 3.009

Height : 9.661



B1L104S, U6-ch104

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 203 |
| Mean | 33.95 |
| Std Dev | 28.12 |

Turn on : 52.5986

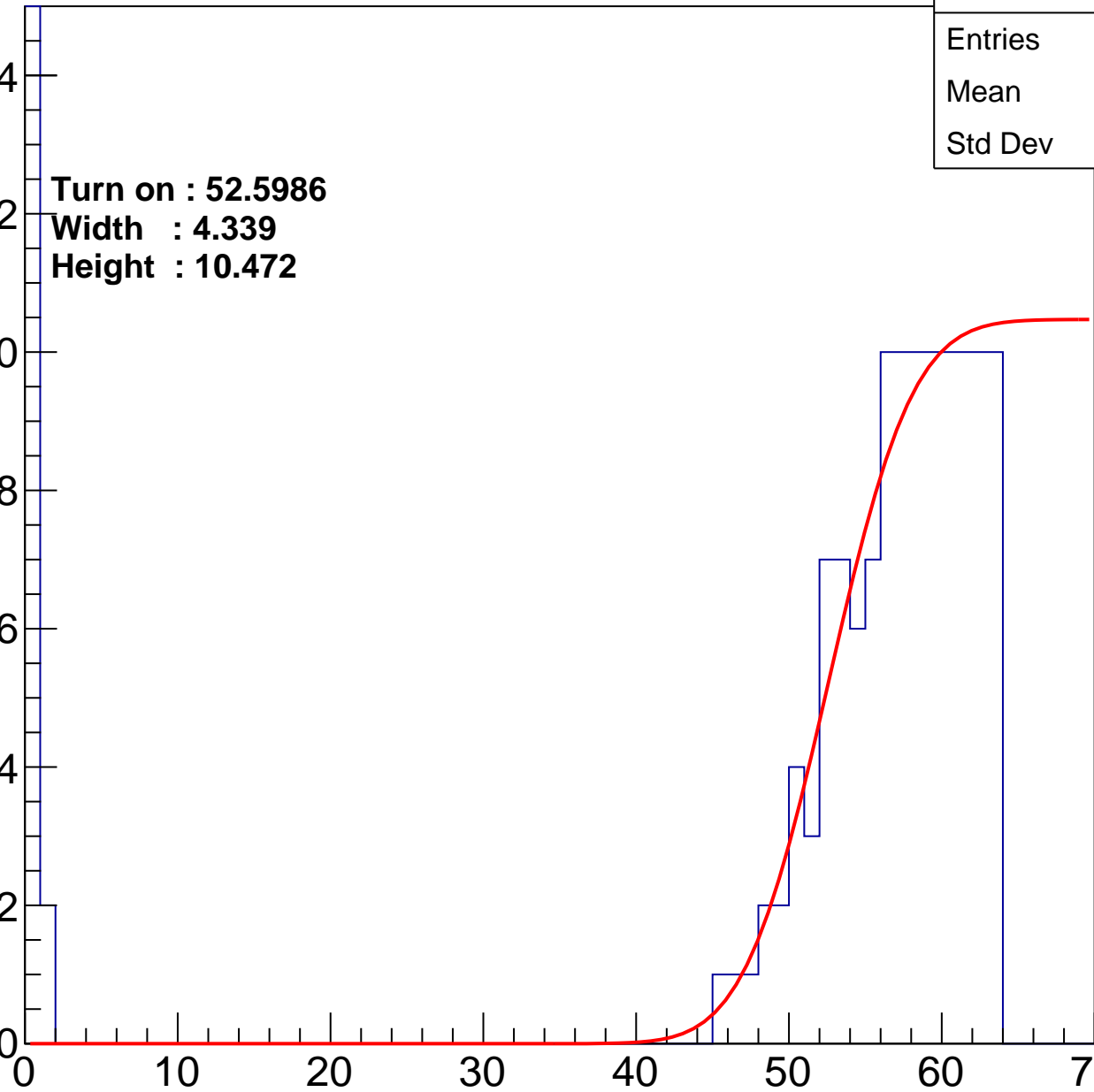
Width : 4.339

Height : 10.472

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch105

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 181 |
| Mean | 32.27 |
| Std Dev | 28.86 |

Turn on : 54.1188

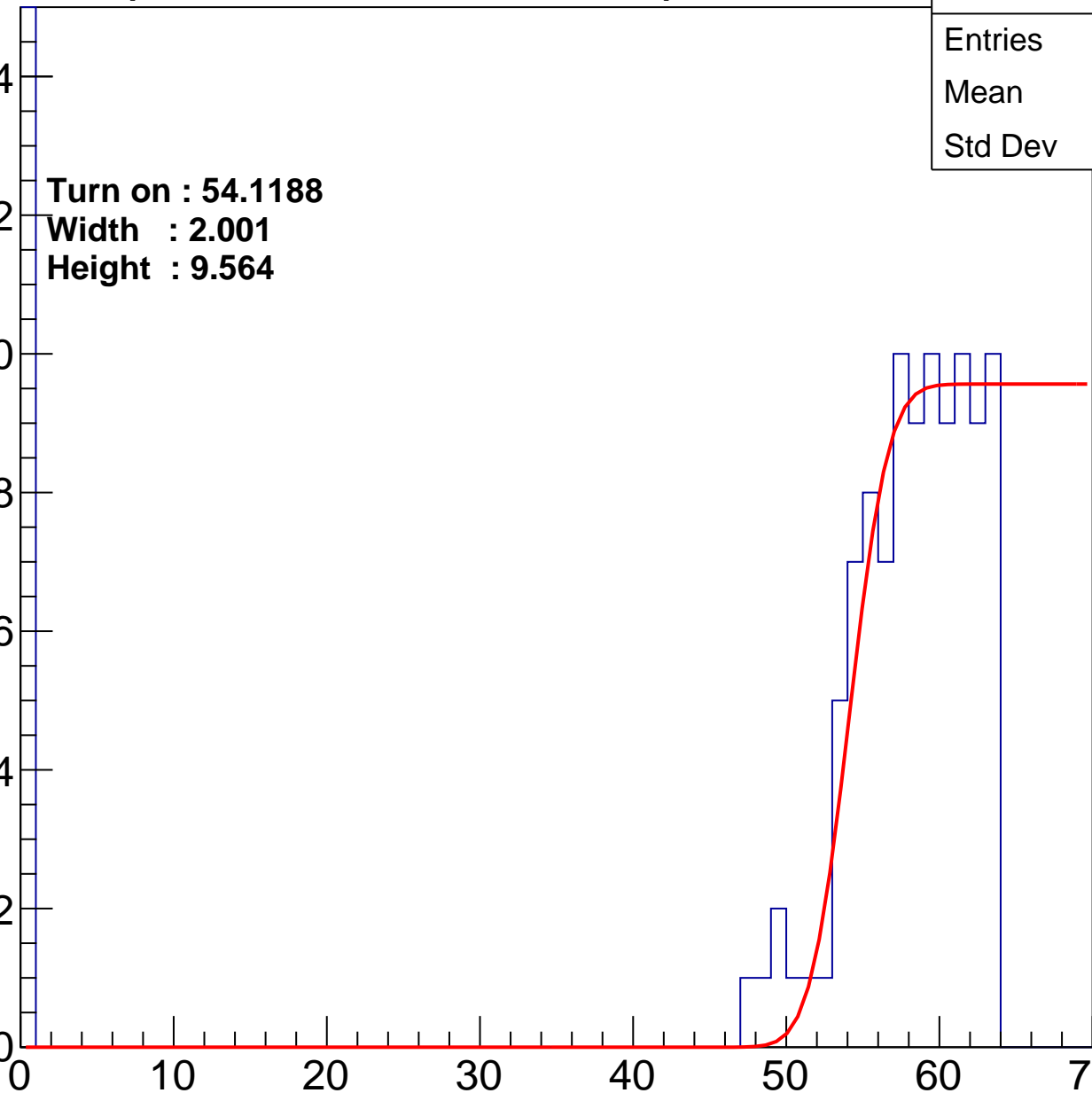
Width : 2.001

Height : 9.564

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch106

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 189 |
| Mean | 32.79 |
| Std Dev | 28.57 |

Turn on : 53.6898

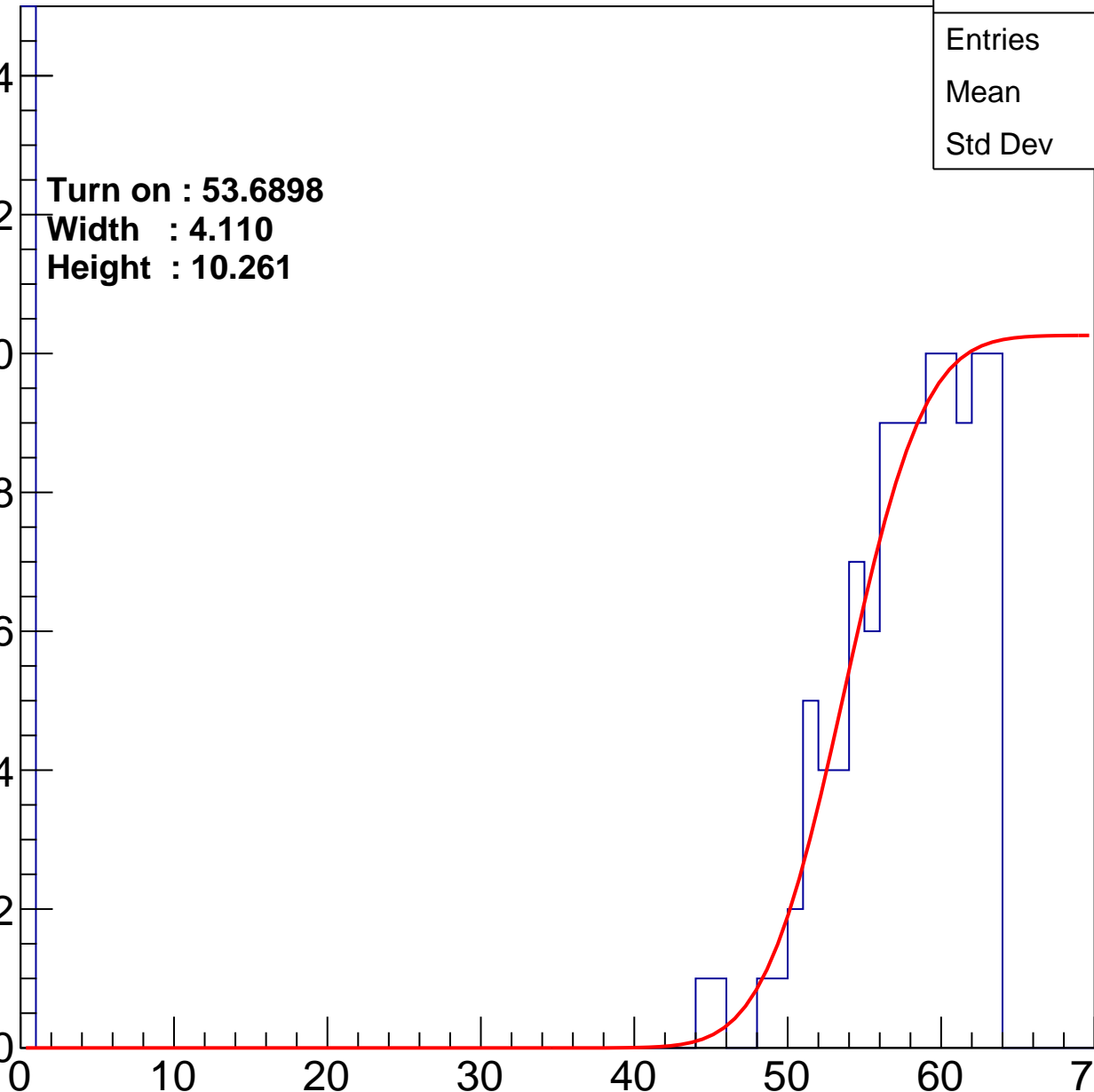
Width : 4.110

Height : 10.261

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch107

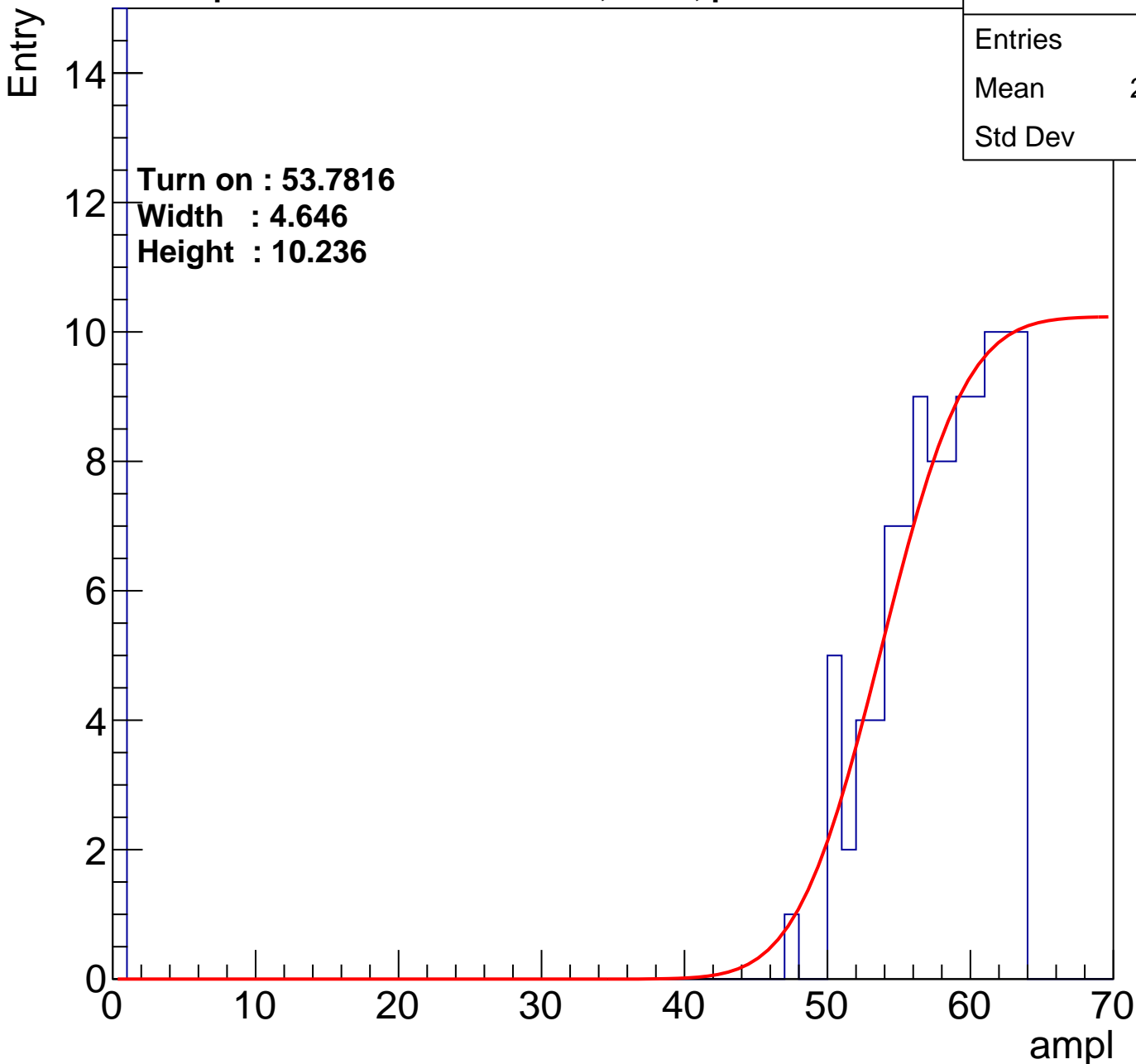
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 218 |
| Mean | 27.23 |
| Std Dev | 28.9 |

Turn on : 53.7816

Width : 4.646

Height : 10.236



B1L104S, U6-ch108

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 190 |
| Mean | 32.43 |
| Std Dev | 28.71 |

Turn on : 54.1009

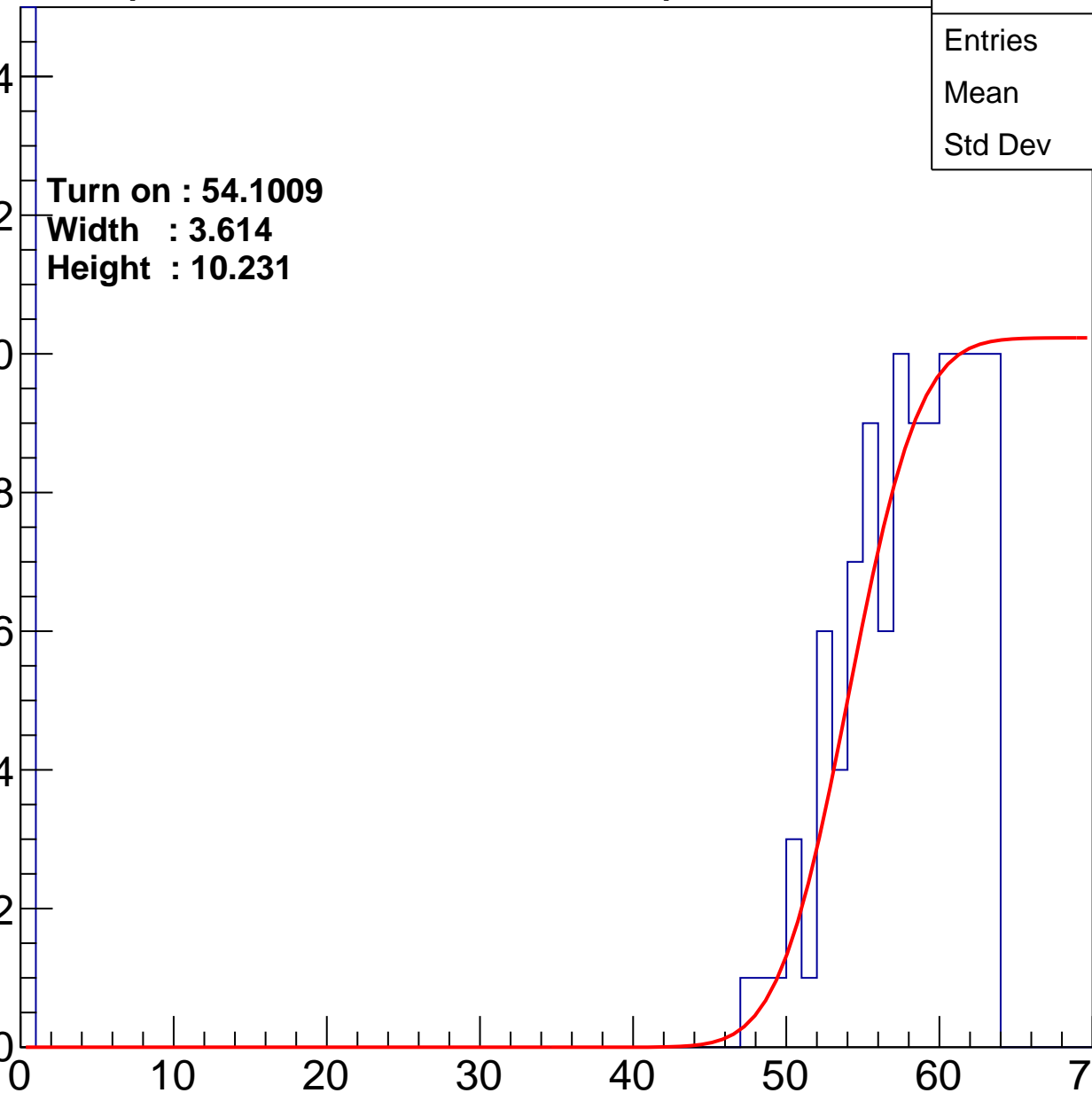
Width : 3.614

Height : 10.231

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch109

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 210 |
| Mean | 29.38 |
| Std Dev | 28.95 |

Turn on : 53.6691

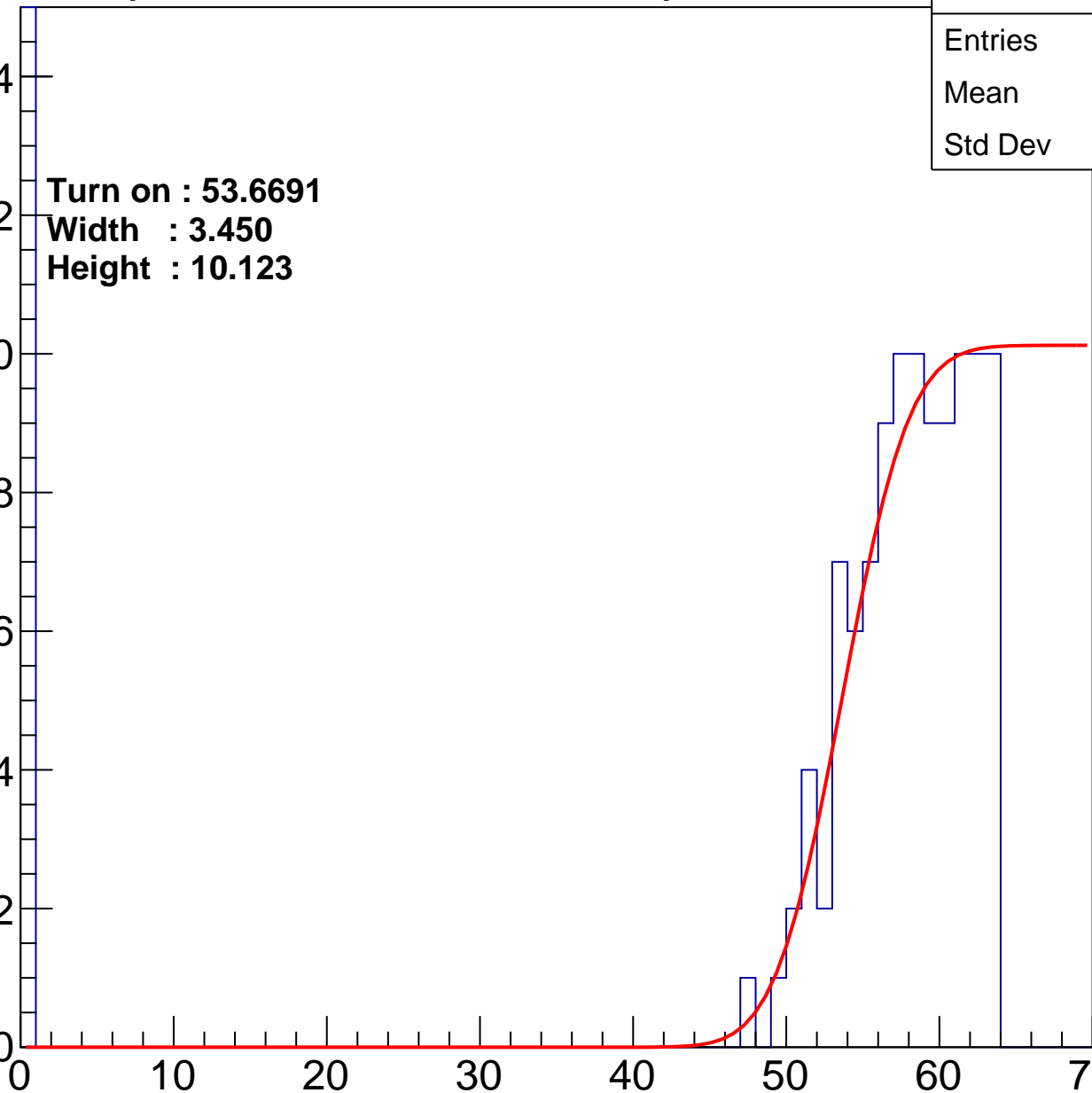
Width : 3.450

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch110

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 171 |
| Mean | 39.67 |
| Std Dev | 26.19 |

Turn on : 53.9391

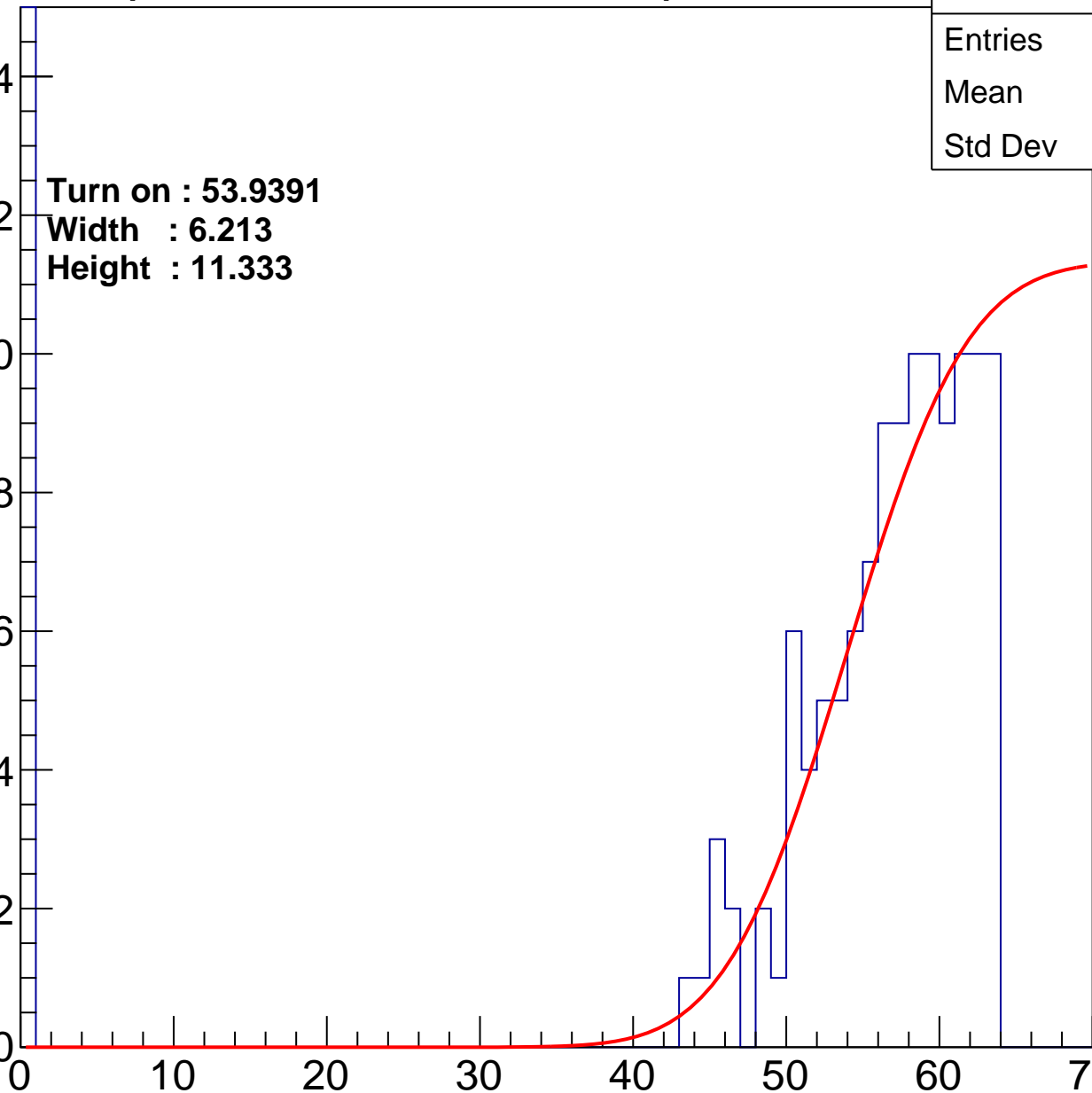
Width : 6.213

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch111

calib_packv5_033123_0516.root, FC#4, port A1

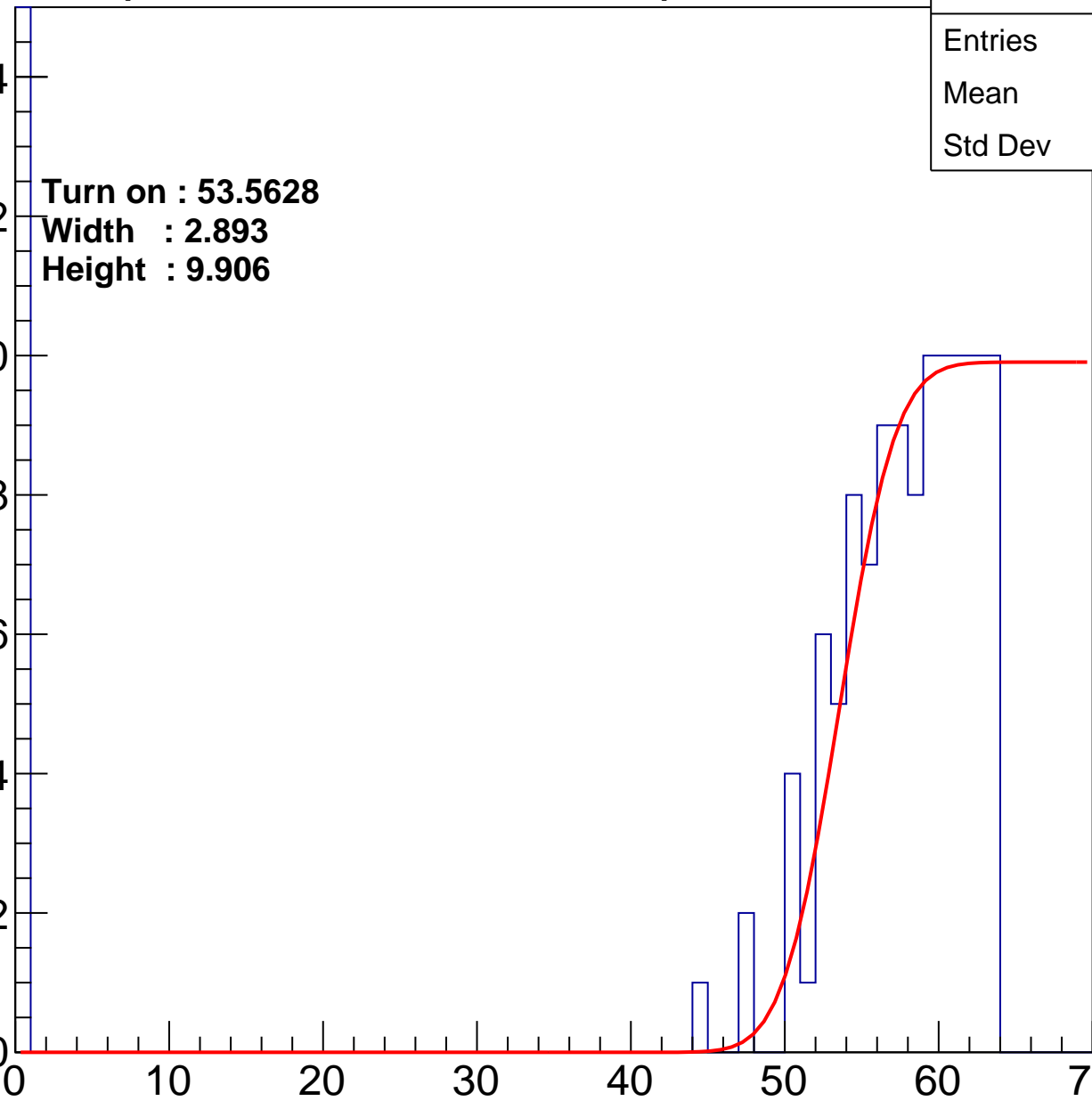
Entry

14
12
10
8
6
4
2
0

Turn on : 53.5628
Width : 2.893
Height : 9.906

| | |
|---------|-------|
| Entries | 208 |
| Mean | 30.36 |
| Std Dev | 28.81 |

ampl



B1L104S, U6-ch112

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 201 |
| Mean | 31.75 |
| Std Dev | 28.51 |

Turn on : 54.2581

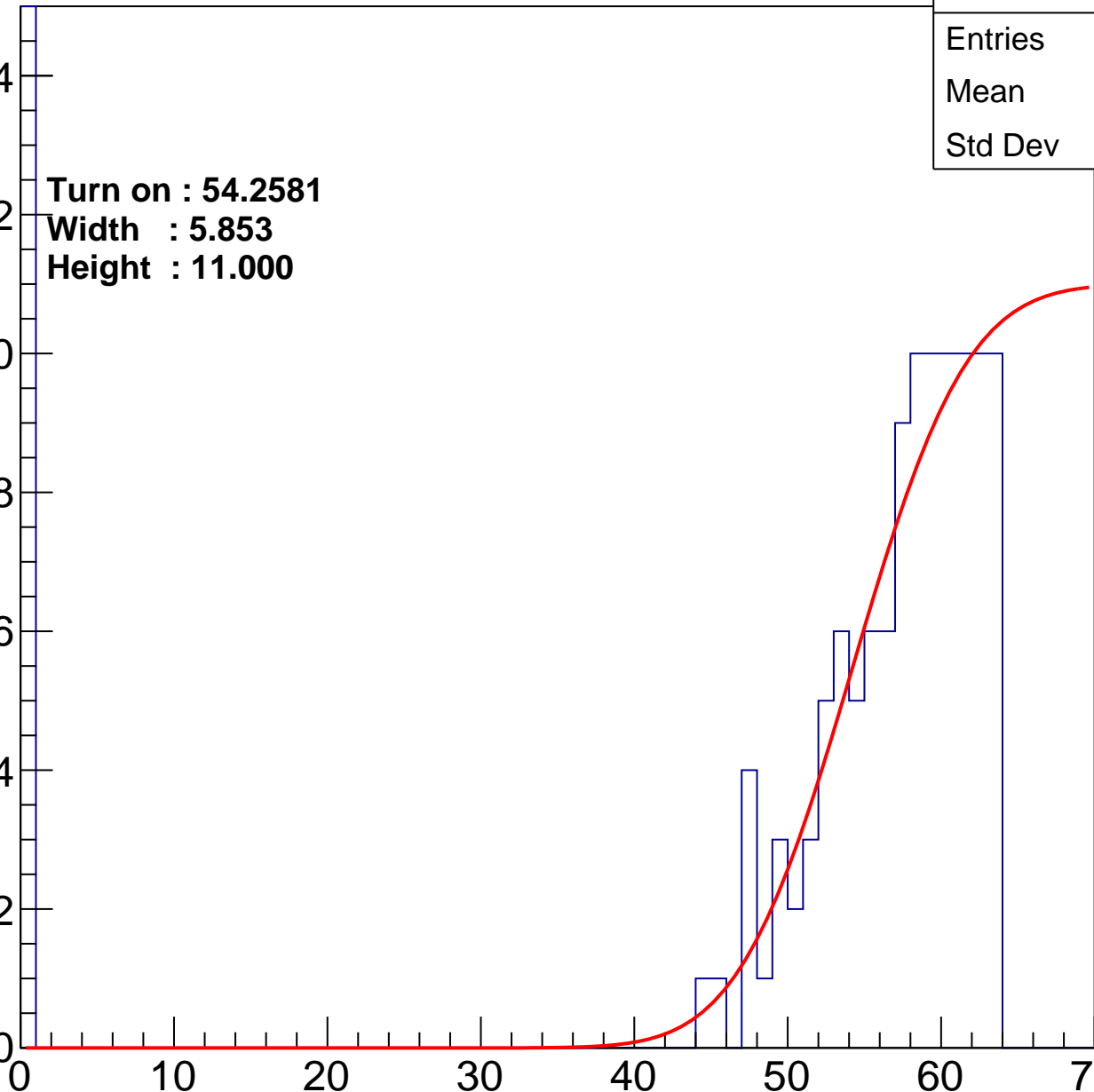
Width : 5.853

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch113

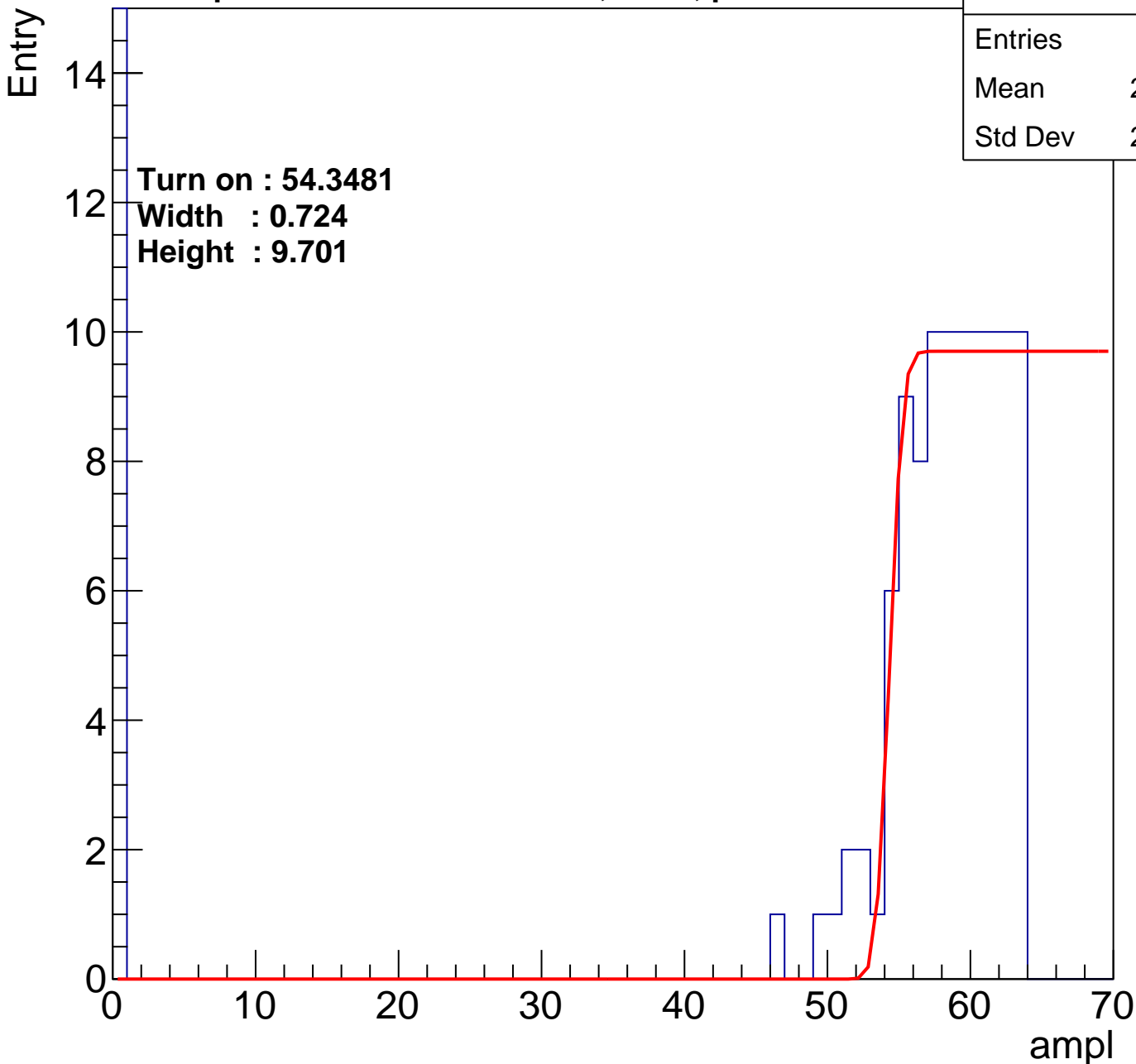
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 218 |
| Mean | 26.93 |
| Std Dev | 29.08 |

Turn on : 54.3481

Width : 0.724

Height : 9.701



B1L104S, U6-ch114

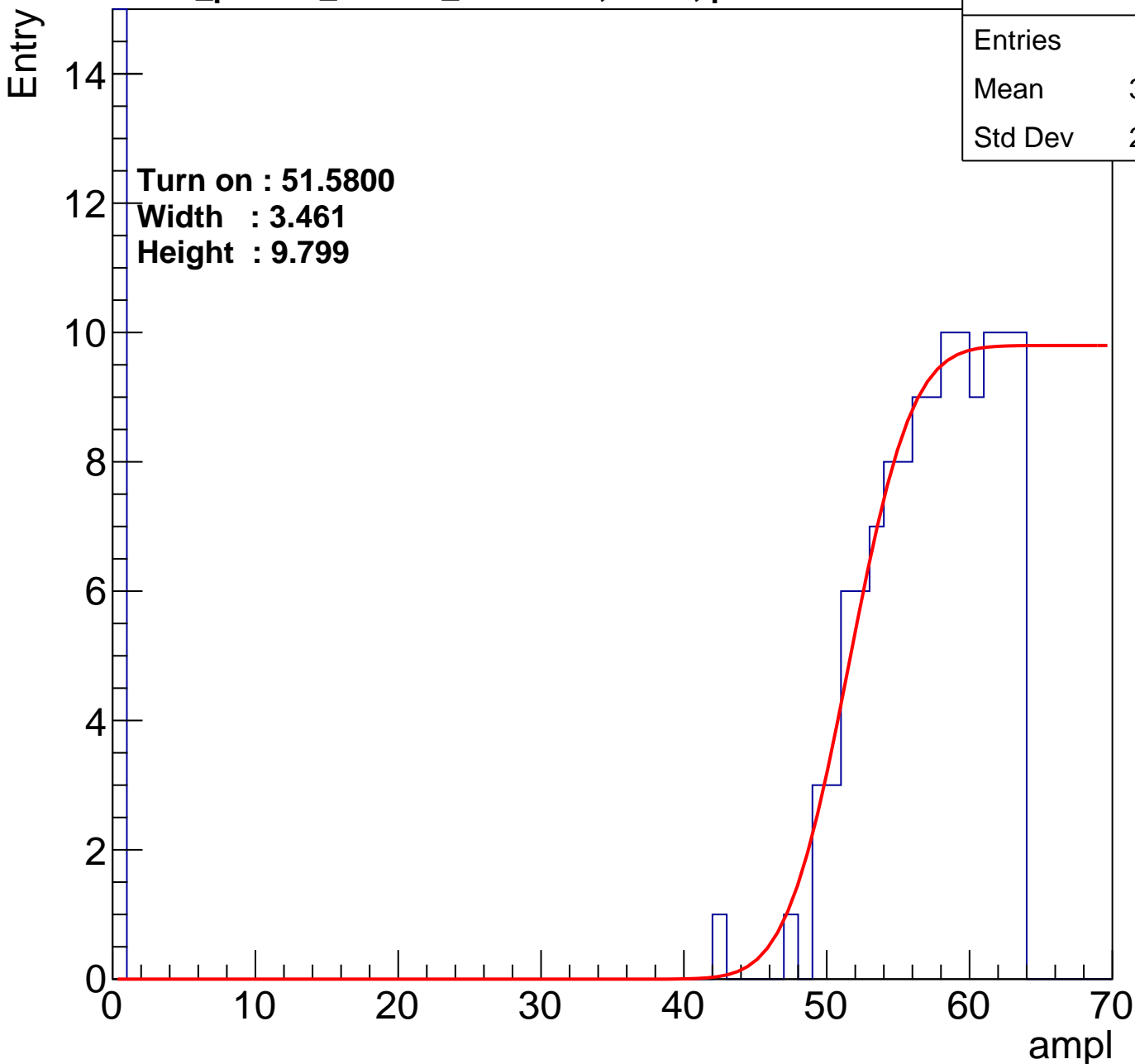
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 198 |
| Mean | 34.52 |
| Std Dev | 28.02 |

Turn on : 51.5800

Width : 3.461

Height : 9.799



B1L104S, U6-ch115

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 177 |
| Mean | 38.41 |
| Std Dev | 27.02 |

Turn on : 52.5441

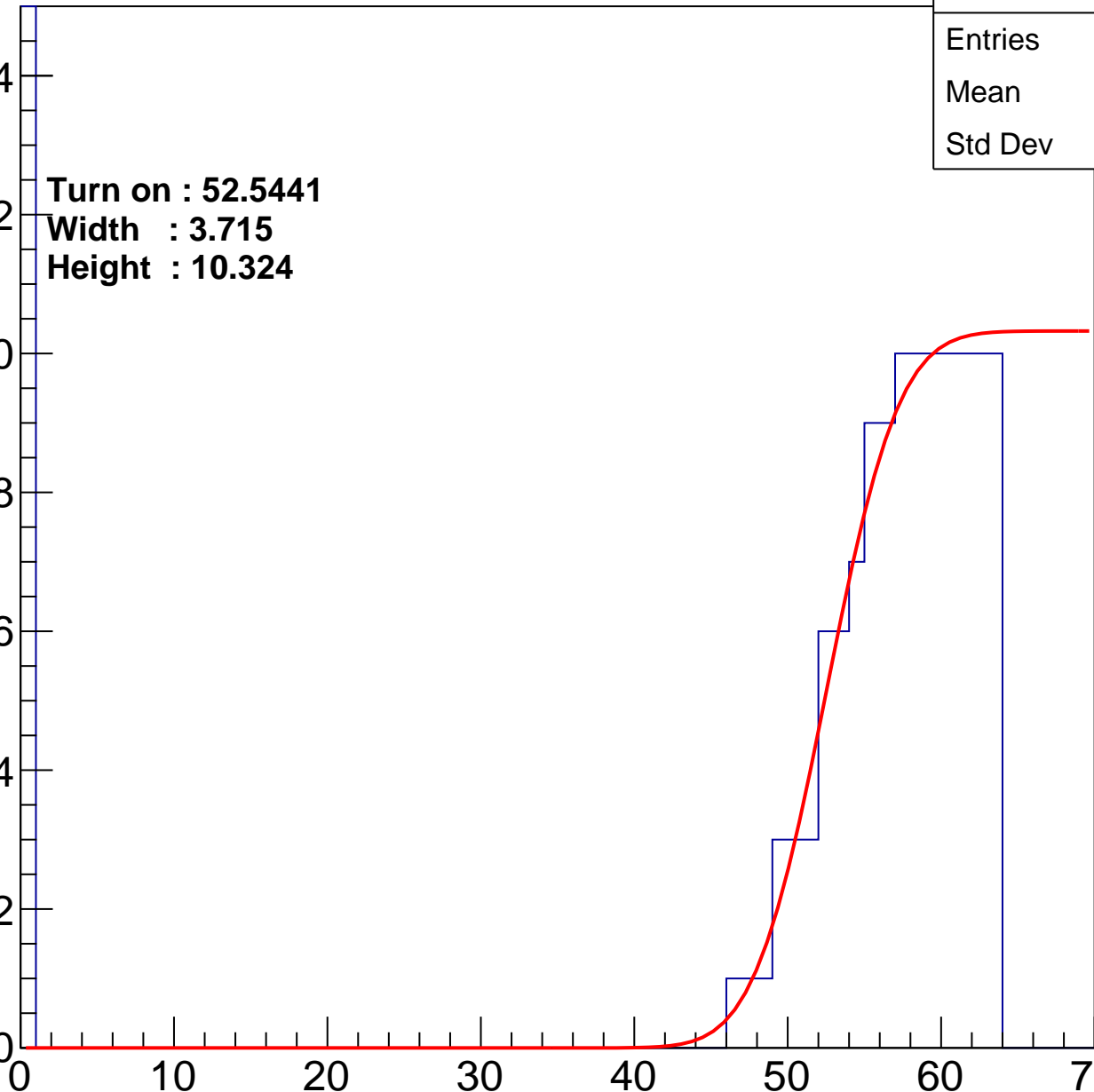
Width : 3.715

Height : 10.324

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch116

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 180 |
| Mean | 32.36 |
| Std Dev | 28.79 |

Turn on : 55.9176

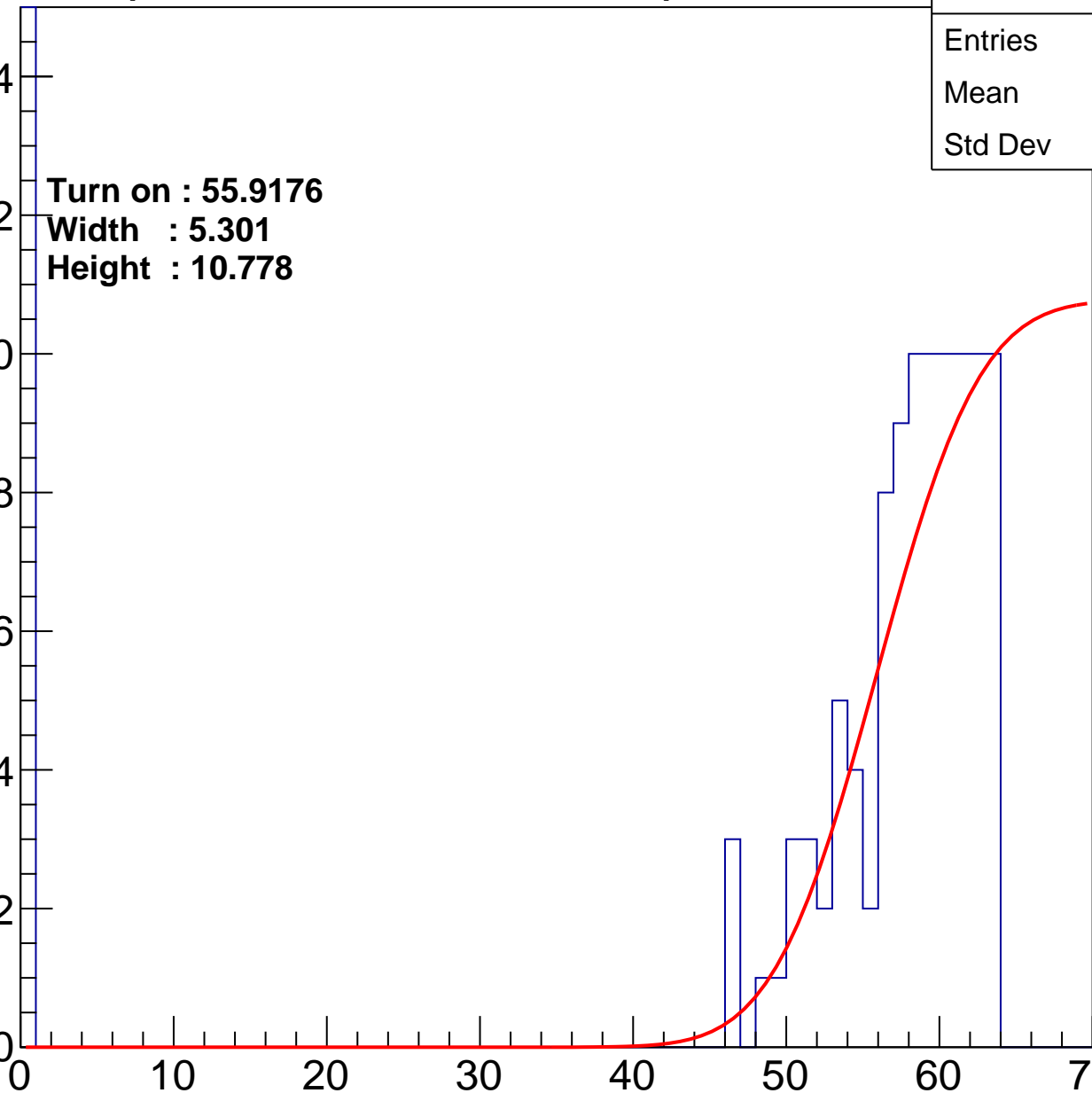
Width : 5.301

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch117

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 173 |
| Mean | 39.57 |
| Std Dev | 26.52 |

Turn on : 52.4544

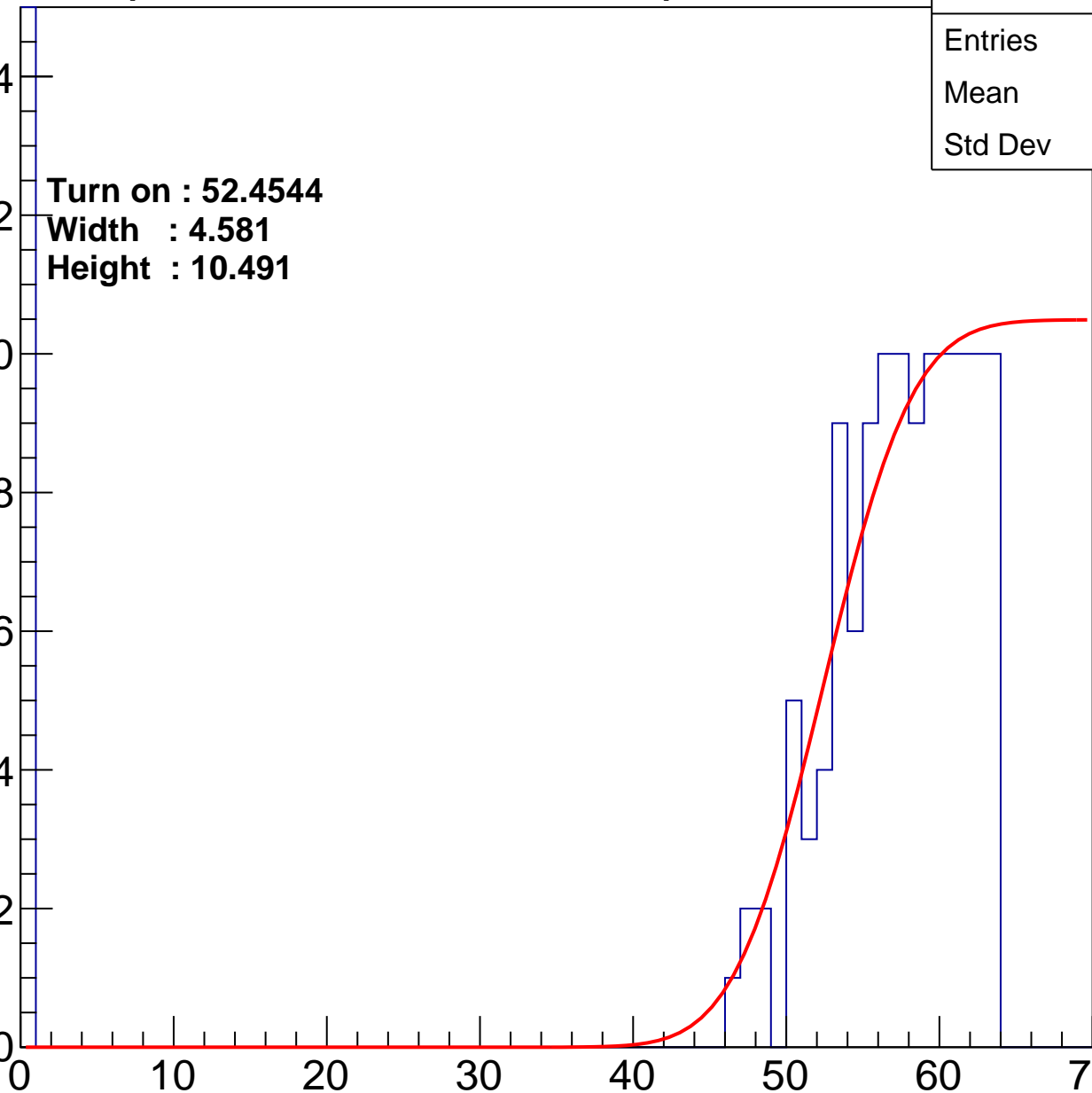
Width : 4.581

Height : 10.491

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch118

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 219 |
| Mean | 33.7 |
| Std Dev | 27.86 |

Turn on : 52.2553

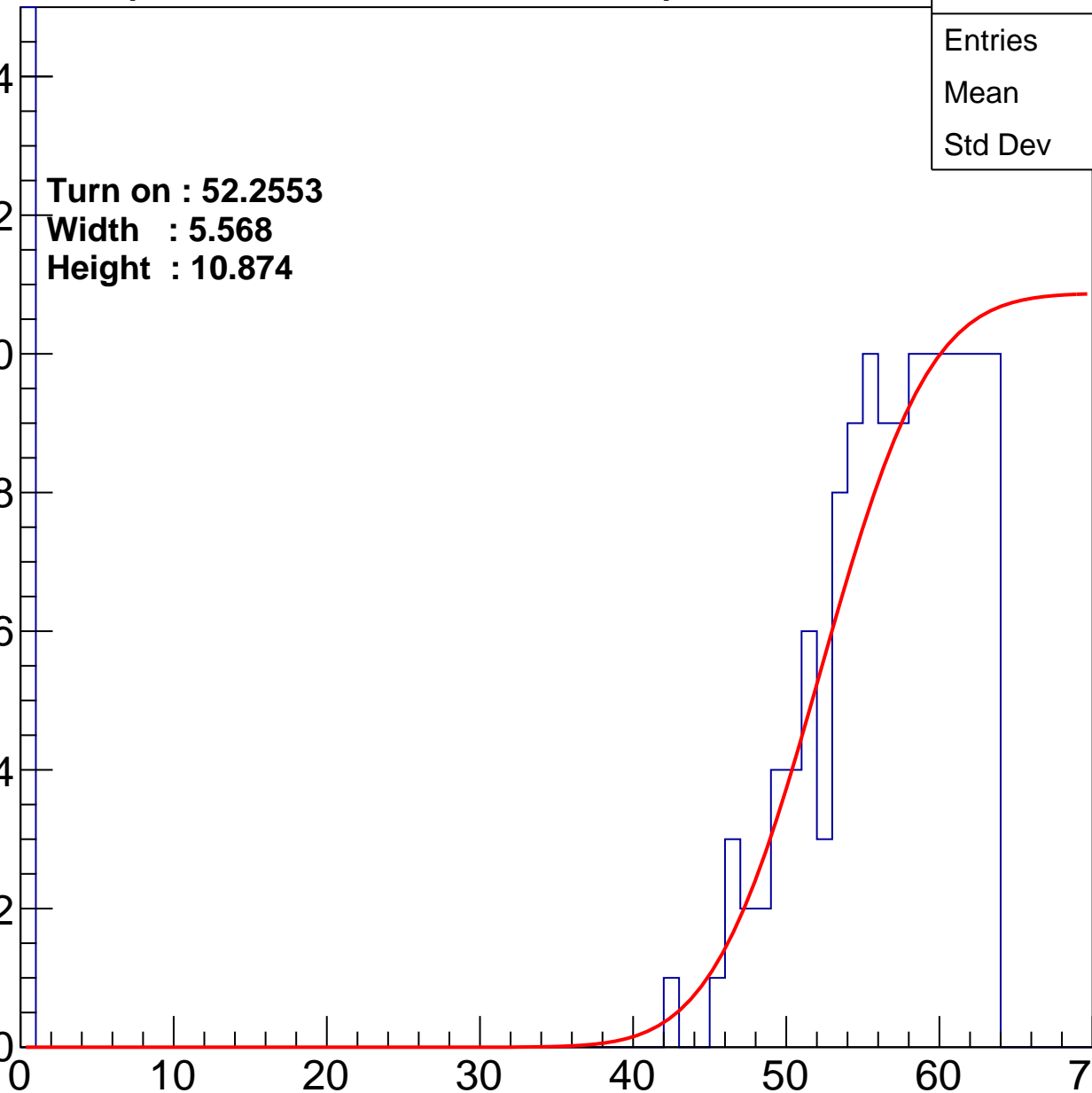
Width : 5.568

Height : 10.874

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch119

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 186 |
| Mean | 35.12 |
| Std Dev | 28.1 |

Turn on : 53.4509

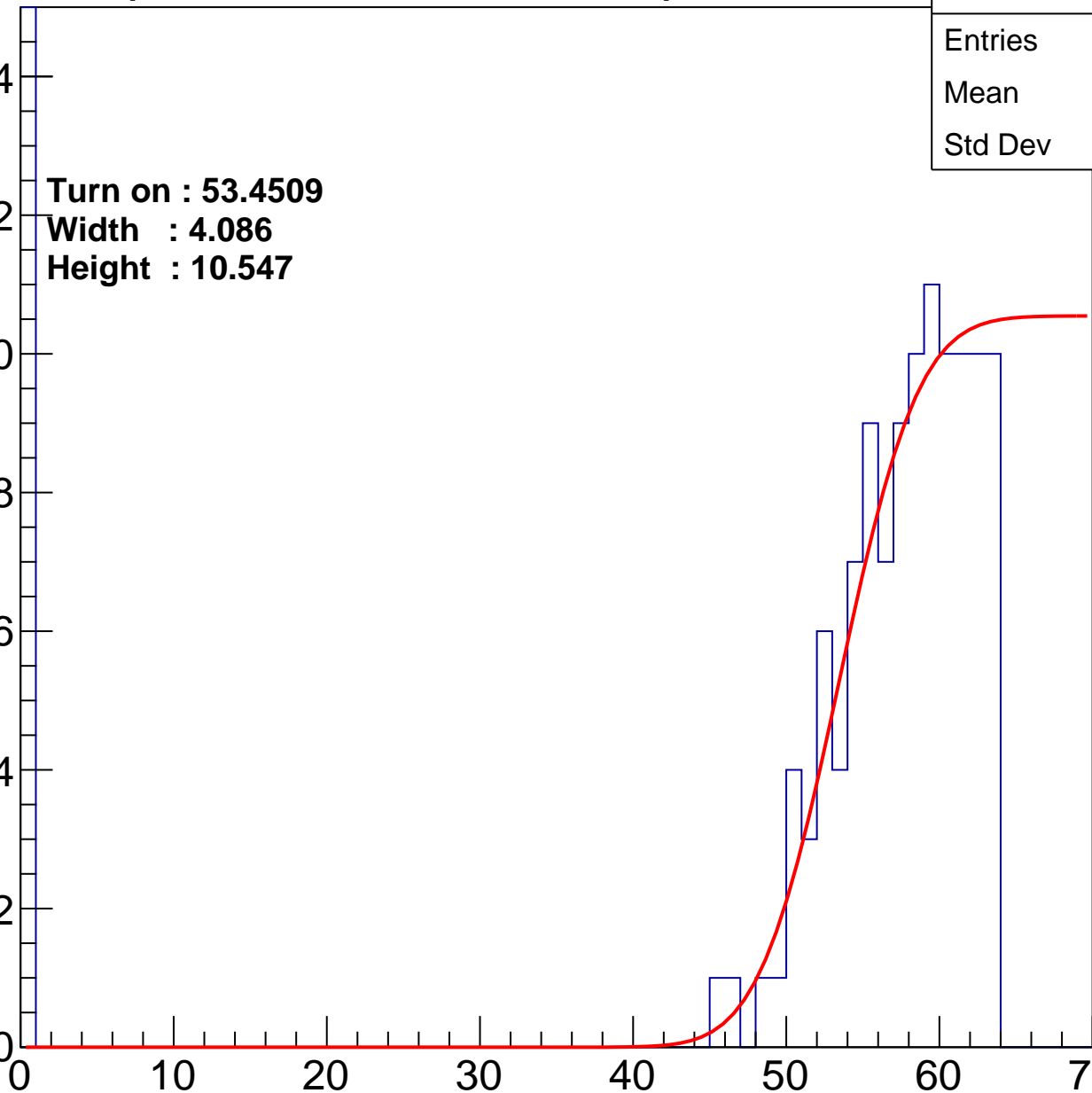
Width : 4.086

Height : 10.547

Entry

14
12
10
8
6
4
2
0

ampl

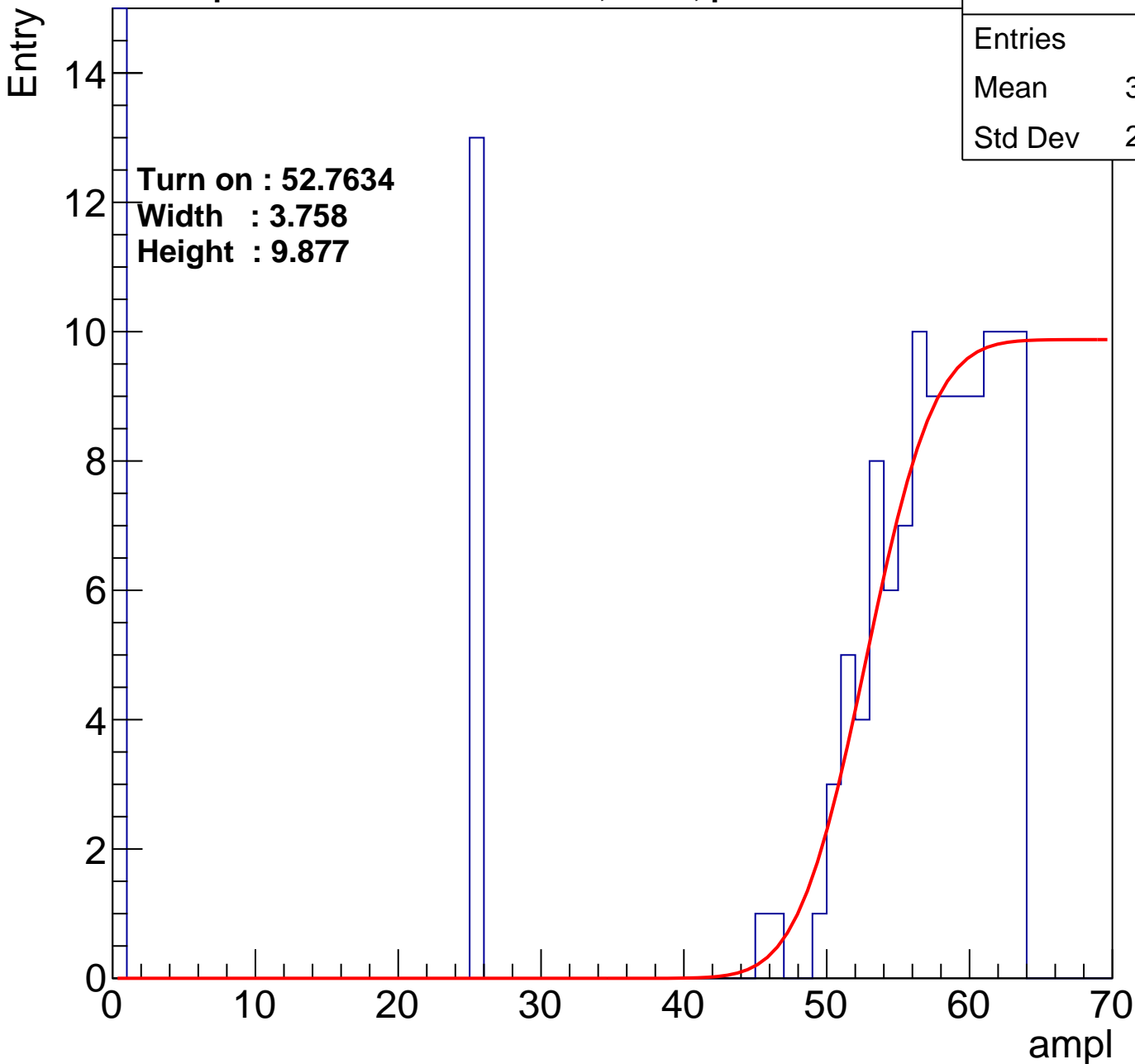


B1L104S, U6-ch120

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 201 |
| Mean | 33.52 |
| Std Dev | 27.43 |

Turn on : 52.7634
Width : 3.758
Height : 9.877



B1L104S, U6-ch121

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 182 |
| Mean | 28.57 |
| Std Dev | 29.3 |

Turn on : 55.3730

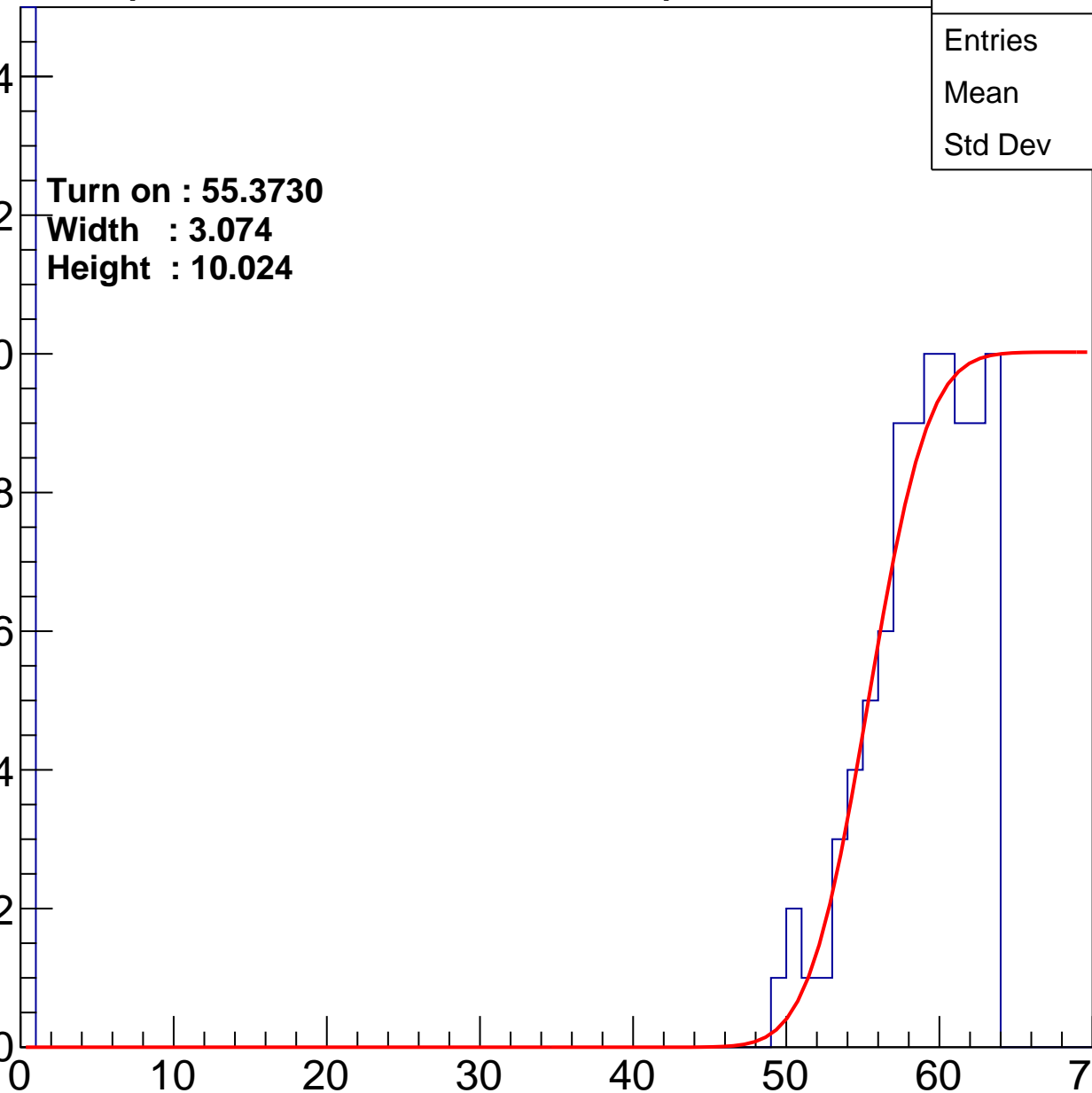
Width : 3.074

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch122

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 187 |
| Mean | 32.33 |
| Std Dev | 28.72 |

Turn on : 53.3476

Width : 4.819

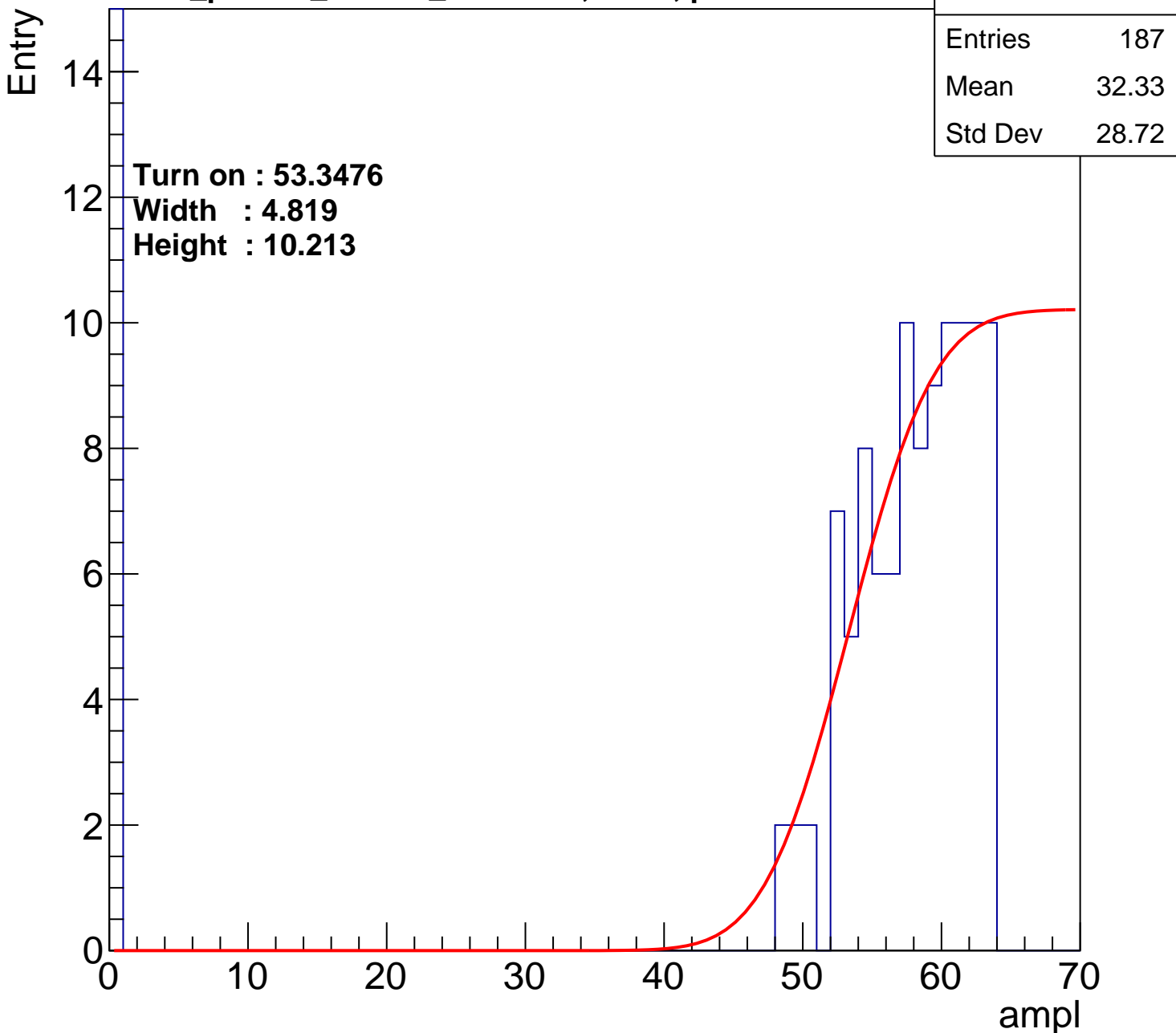
Height : 10.213

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L104S, U6-ch123

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 172 |
| Mean | 36.93 |
| Std Dev | 27.58 |

Turn on : 53.5763

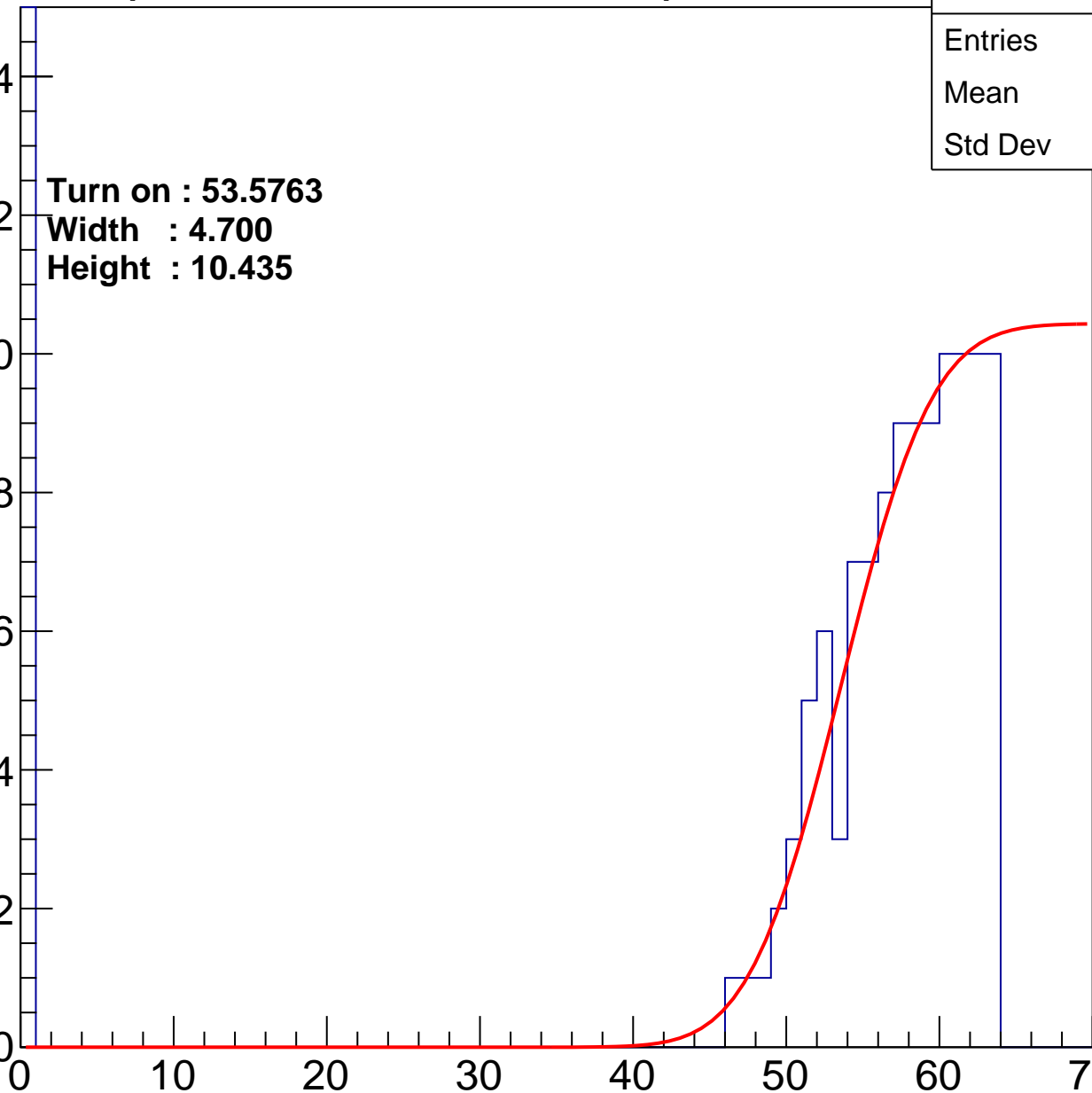
Width : 4.700

Height : 10.435

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch124

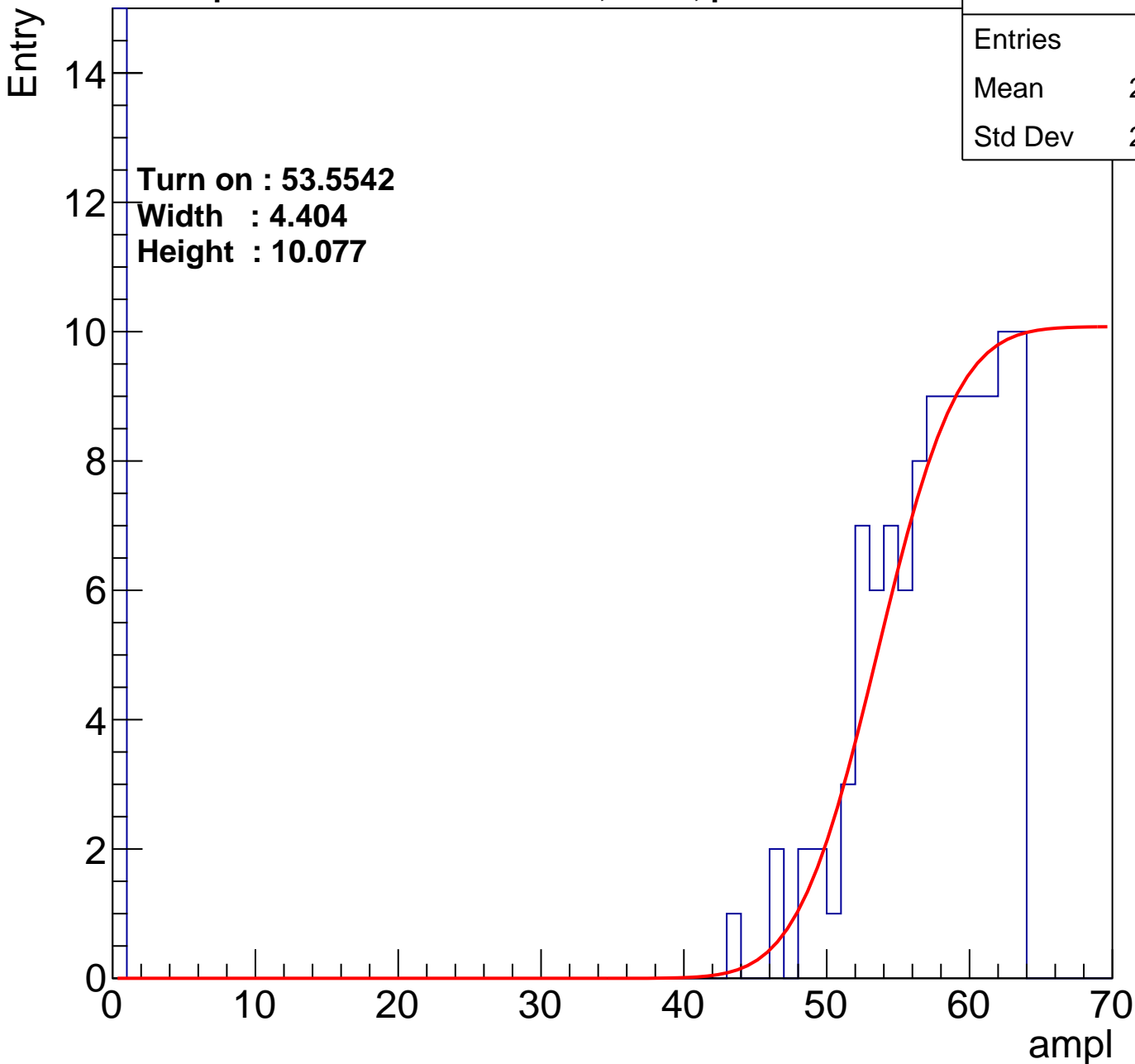
calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 218 |
| Mean | 28.78 |
| Std Dev | 28.69 |

Turn on : 53.5542

Width : 4.404

Height : 10.077



B1L104S, U6-ch125

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 232 |
| Mean | 31.31 |
| Std Dev | 28.39 |

Turn on : 50.9524

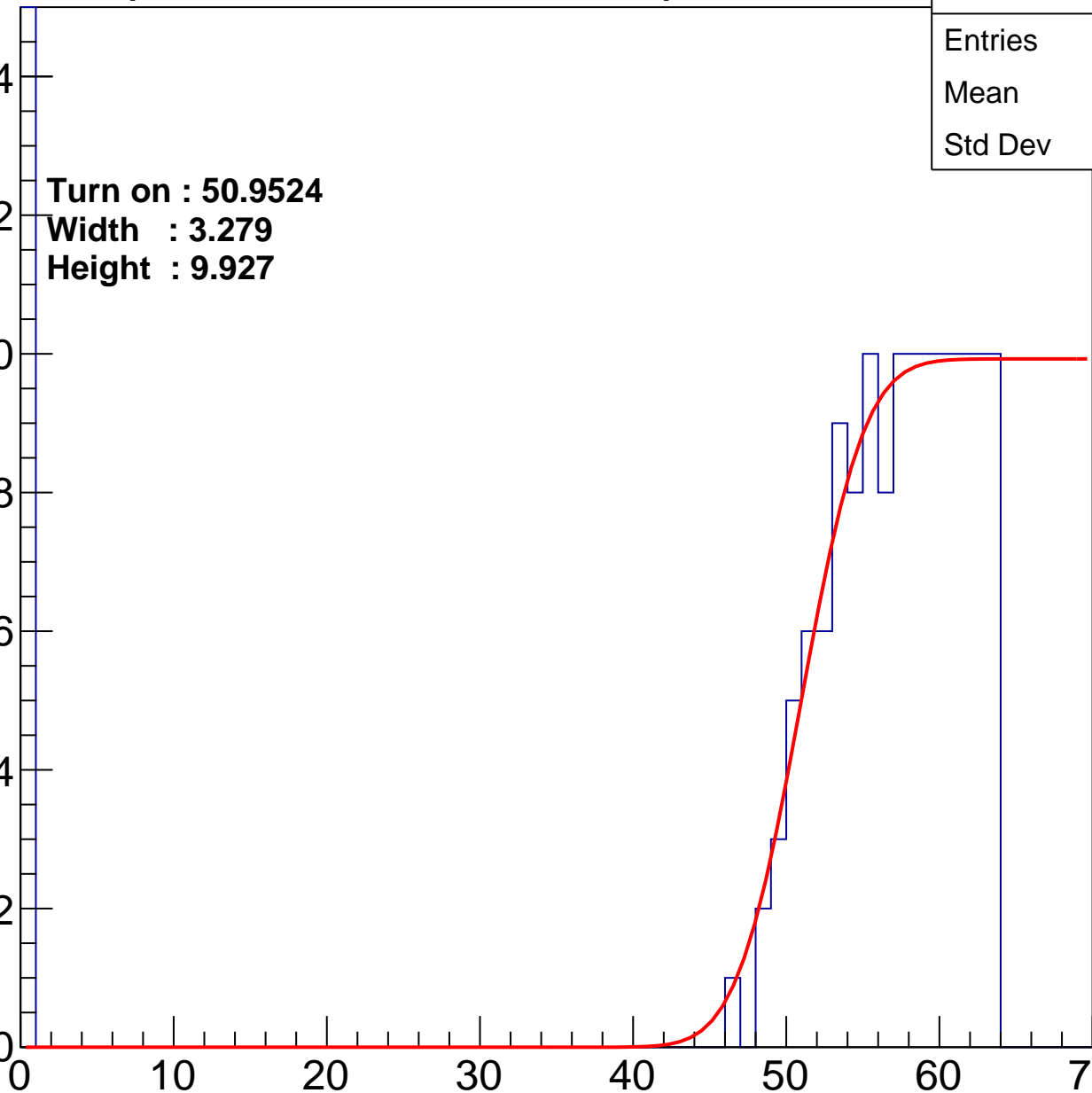
Width : 3.279

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch126

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 220 |
| Mean | 32.81 |
| Std Dev | 28.02 |

Turn on : 50.3029

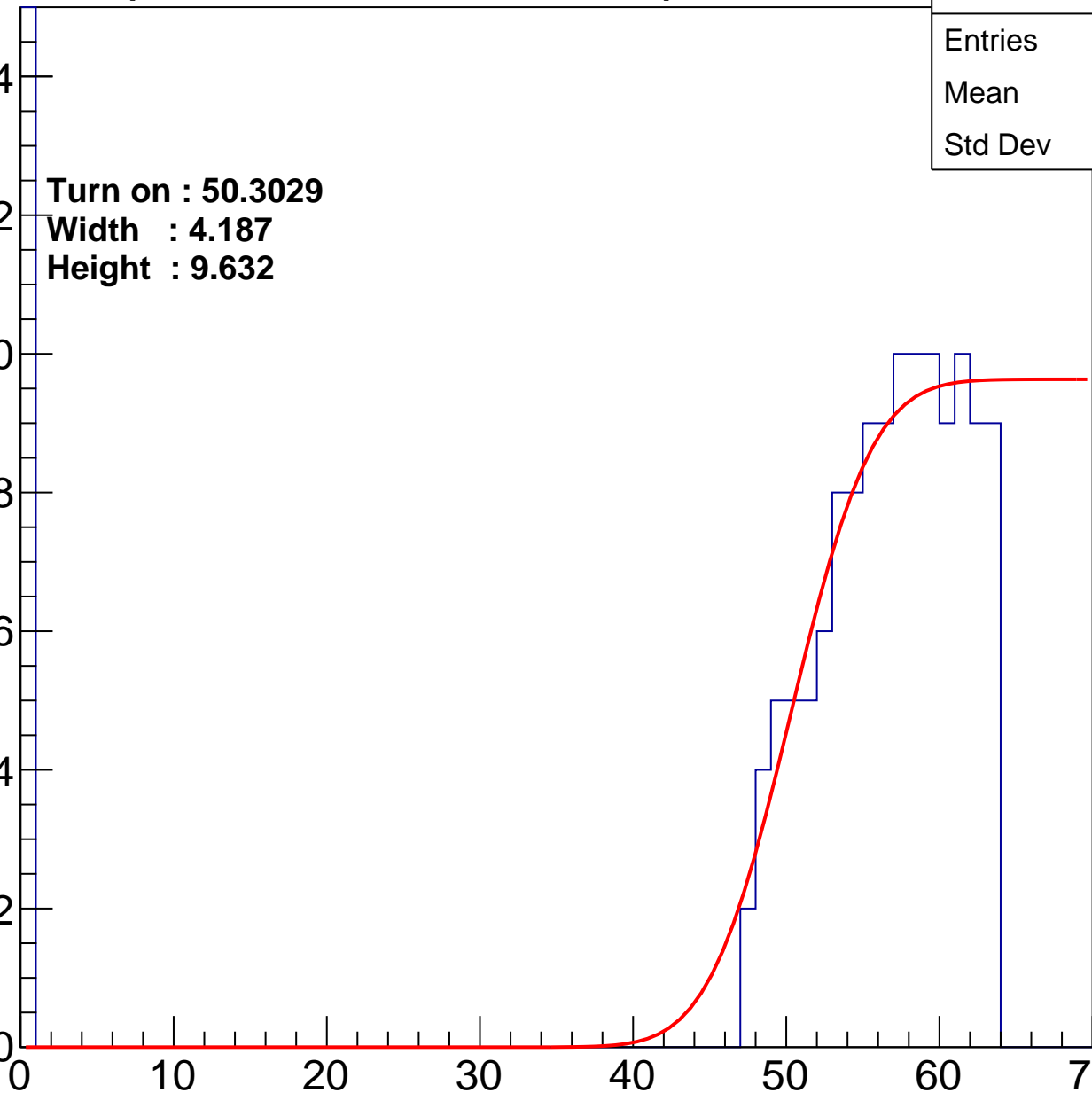
Width : 4.187

Height : 9.632

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch127

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 214 |
| Mean | 34.53 |
| Std Dev | 27.72 |

Turn on : 51.8900

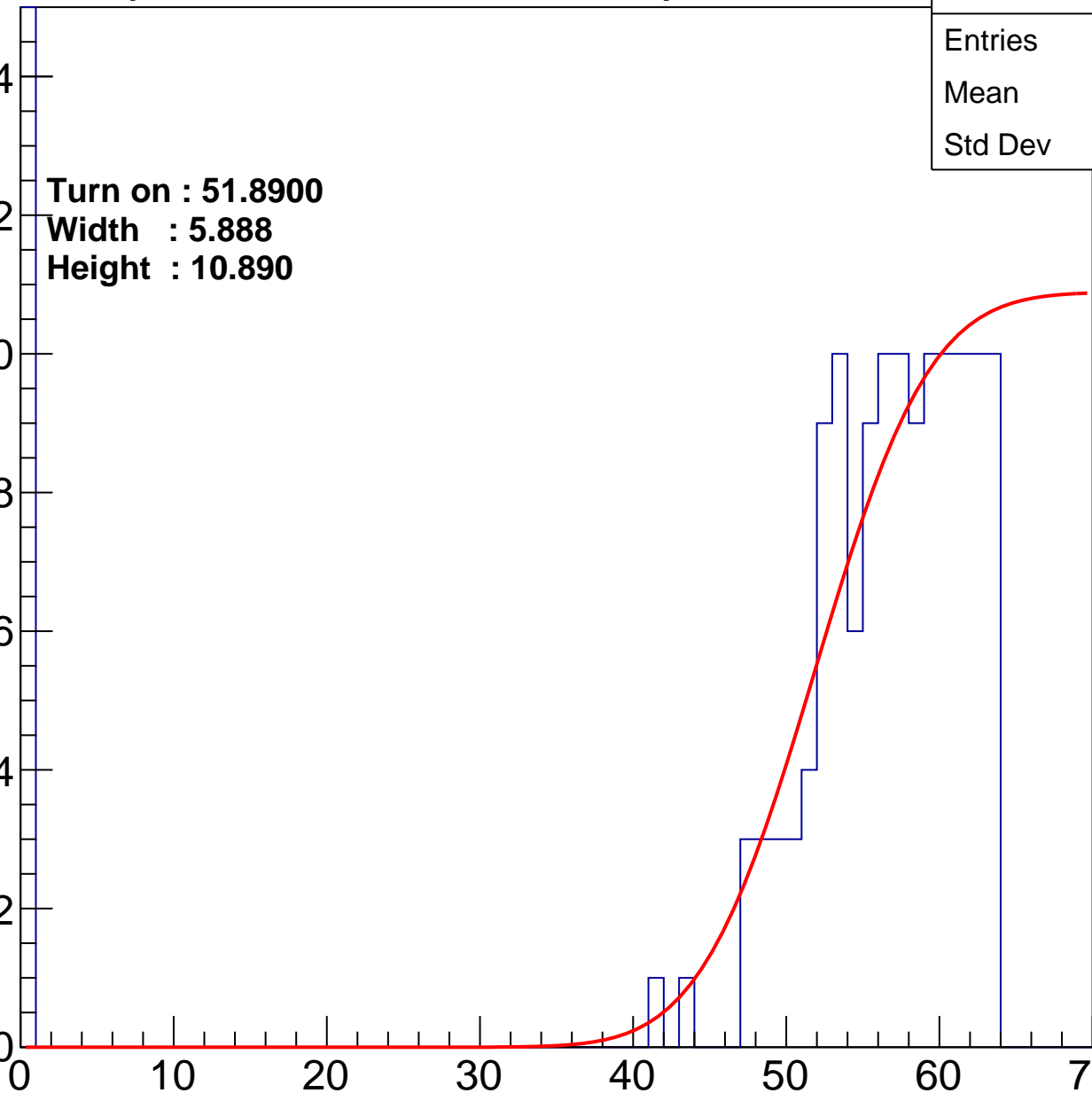
Width : 5.888

Height : 10.890

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U6-ch127

calib_packv5_033123_0516.root, FC#4, port A1

| | |
|---------|-------|
| Entries | 214 |
| Mean | 34.53 |
| Std Dev | 27.72 |

Turn on : 51.8900

Width : 5.888

Height : 10.890

Entry

14
12
10
8
6
4
2
0

ampl

