

B1L000S, U25-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 26.0326

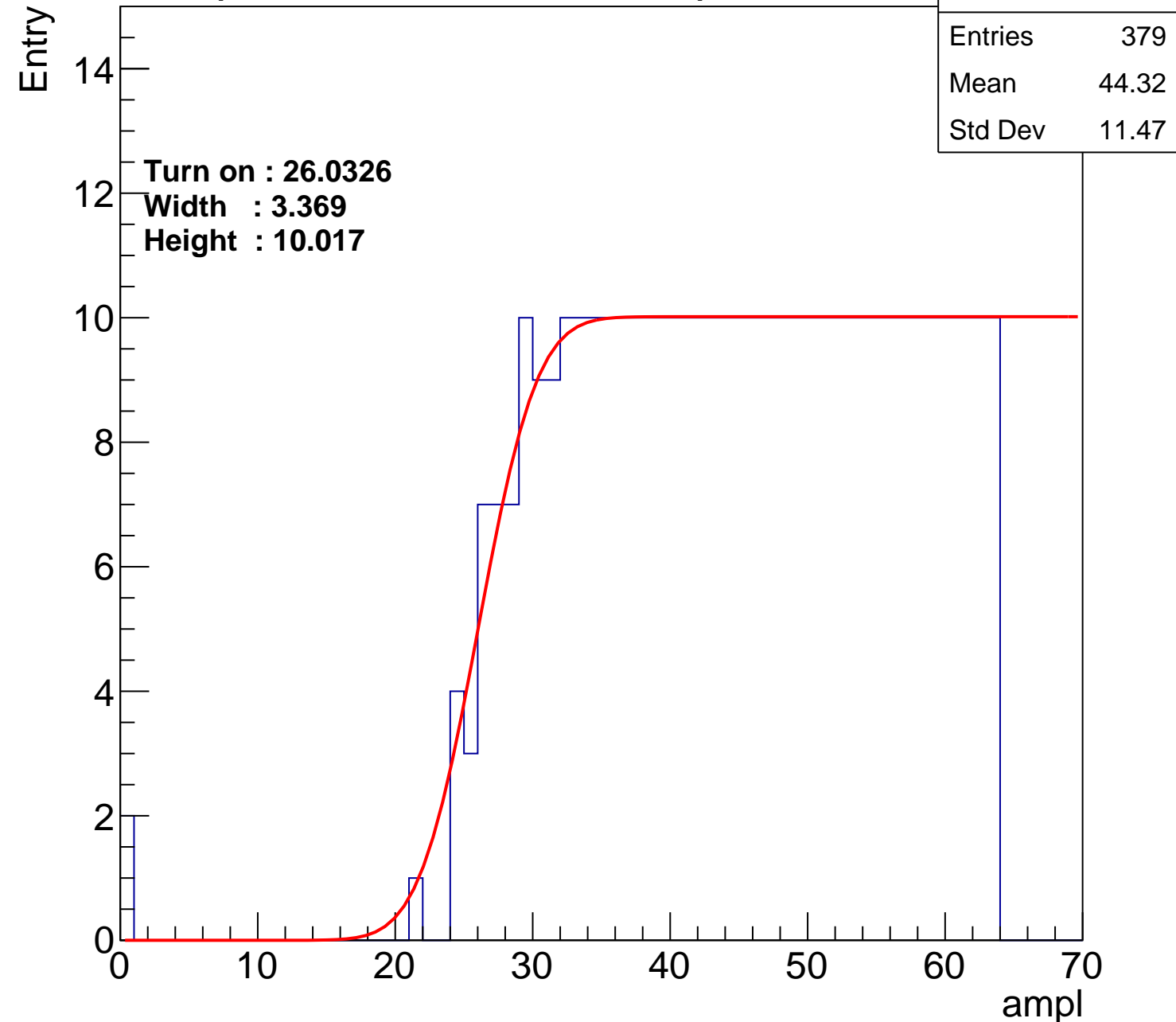
Width : 3.369

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.77
Std Dev	11.23

Turn on : 27.3083

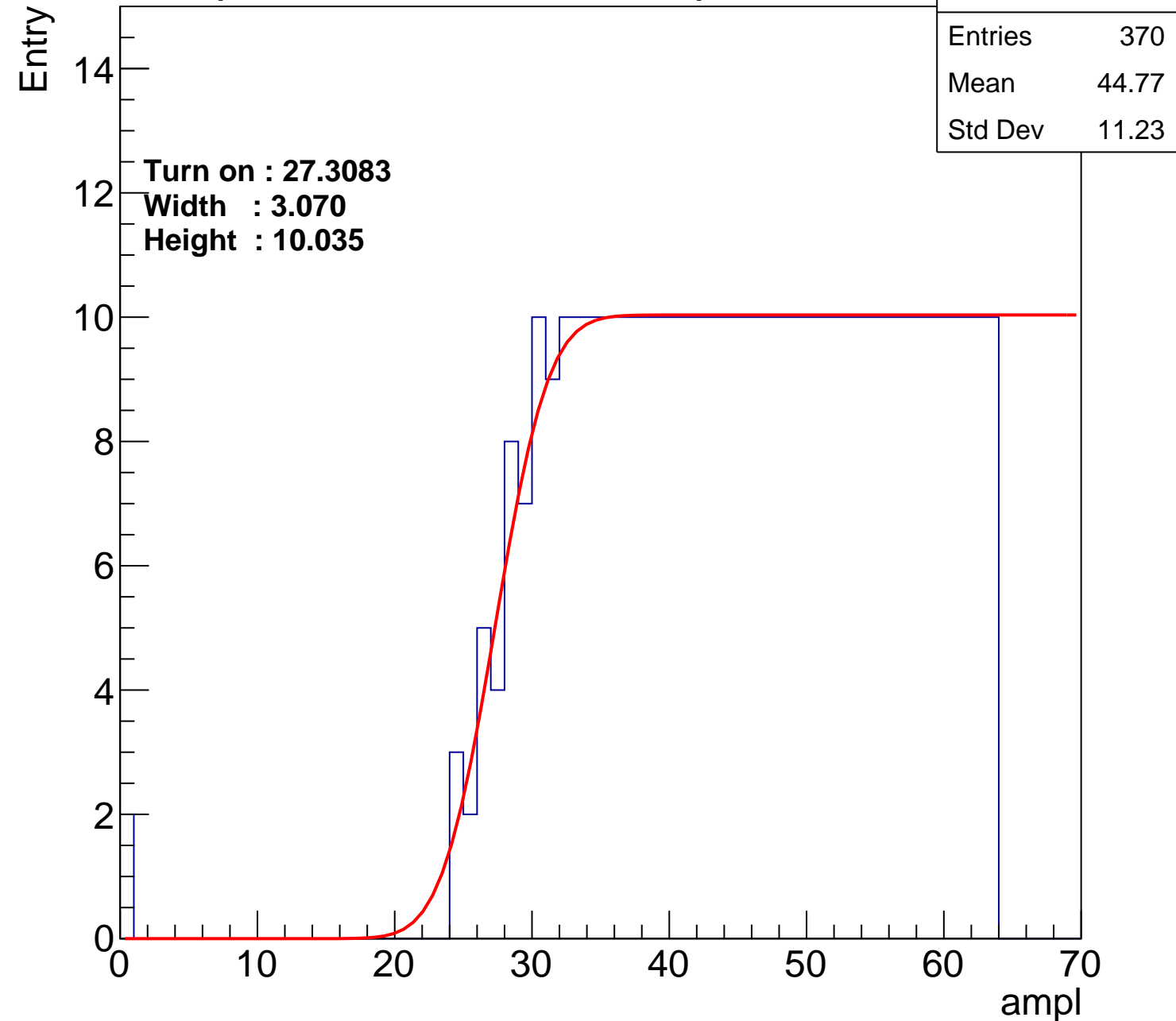
Width : 3.070

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch2

calib_packv5_042523_0143.root, FC#3, port B3

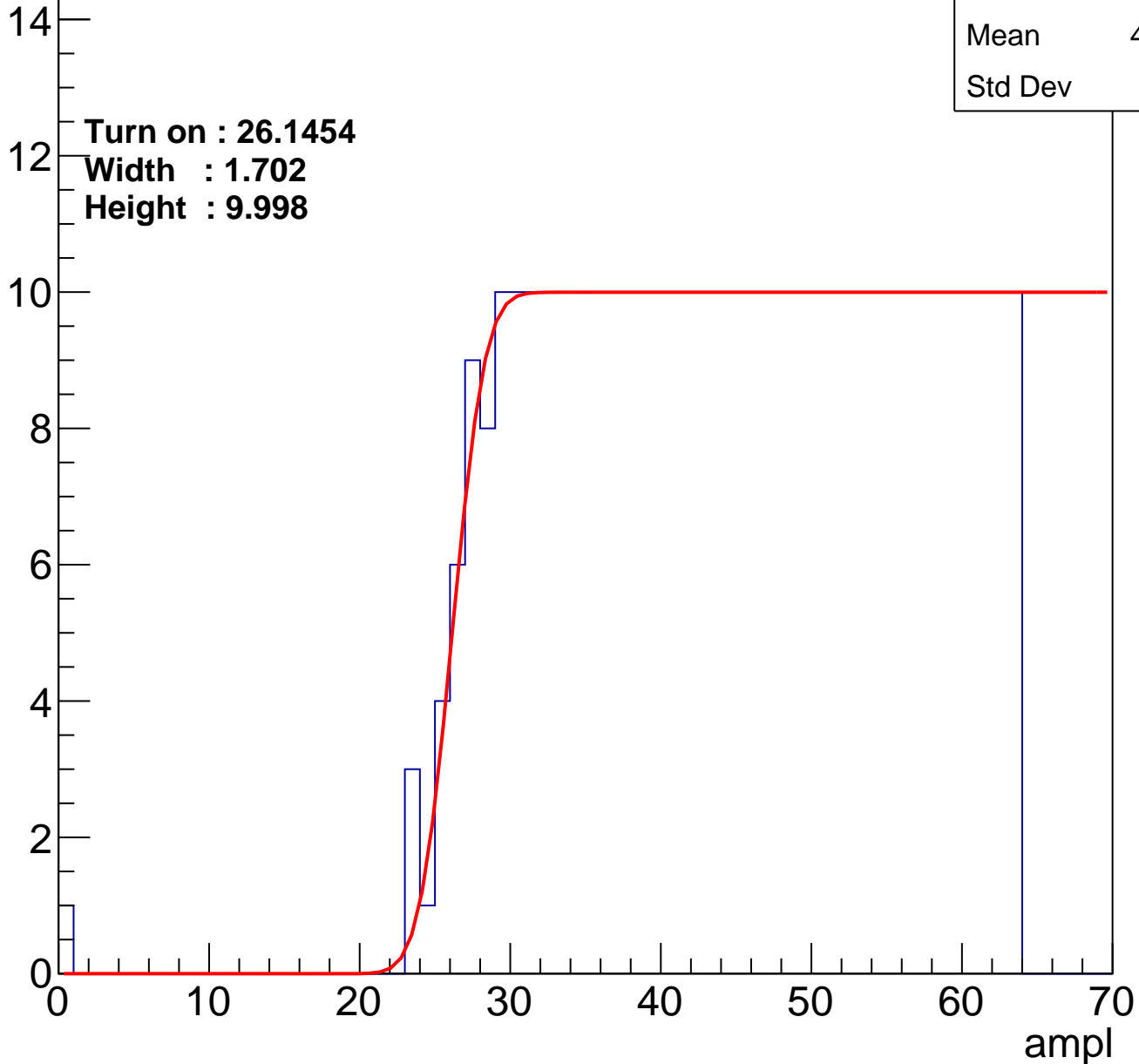
Entries	382
Mean	44.28
Std Dev	11.3

Turn on : 26.1454

Width : 1.702

Height : 9.998

Entry



B1L000S, U25-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.99
Std Dev	11.82

Turn on : 26.5074

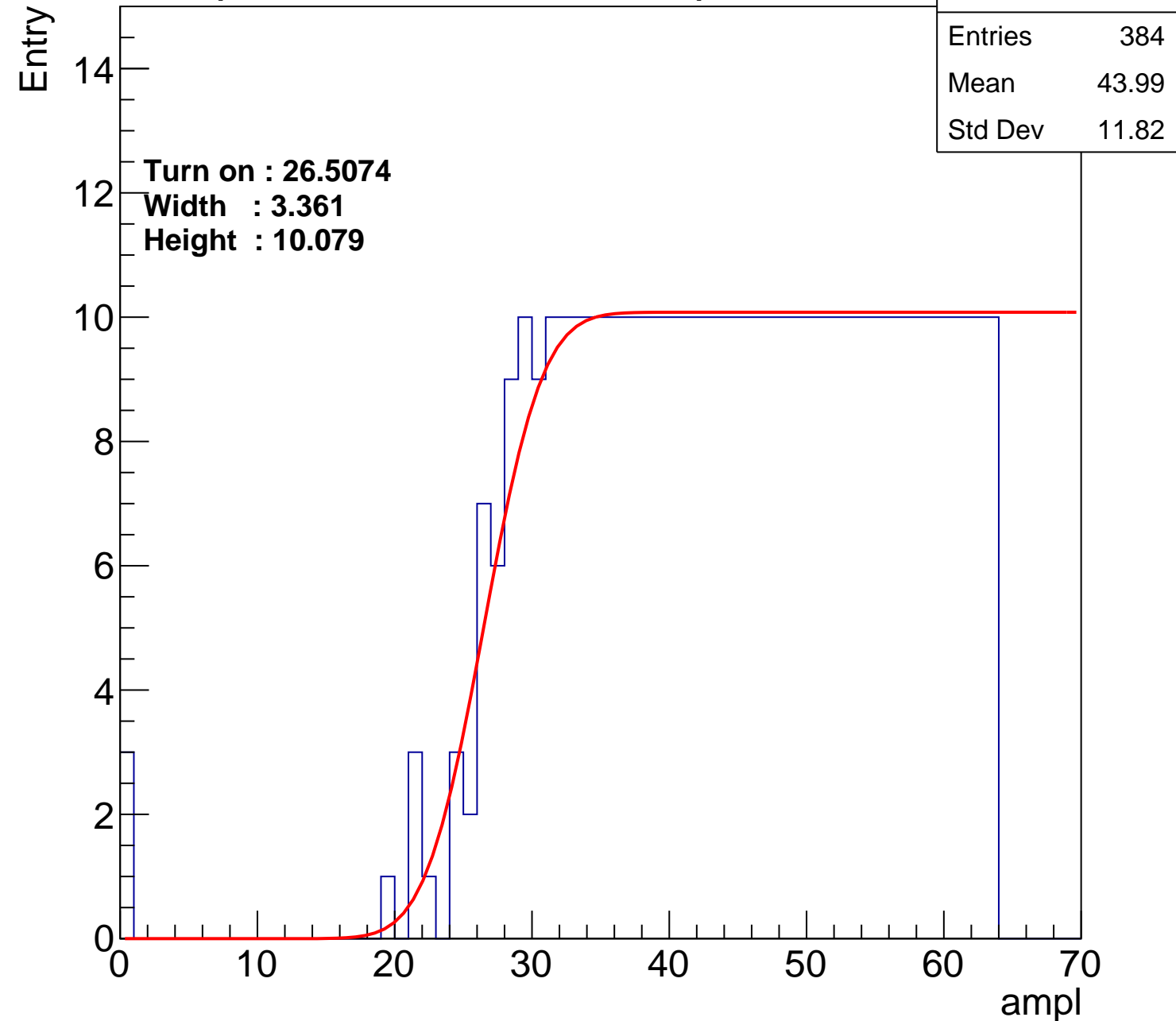
Width : 3.361

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 27.3515

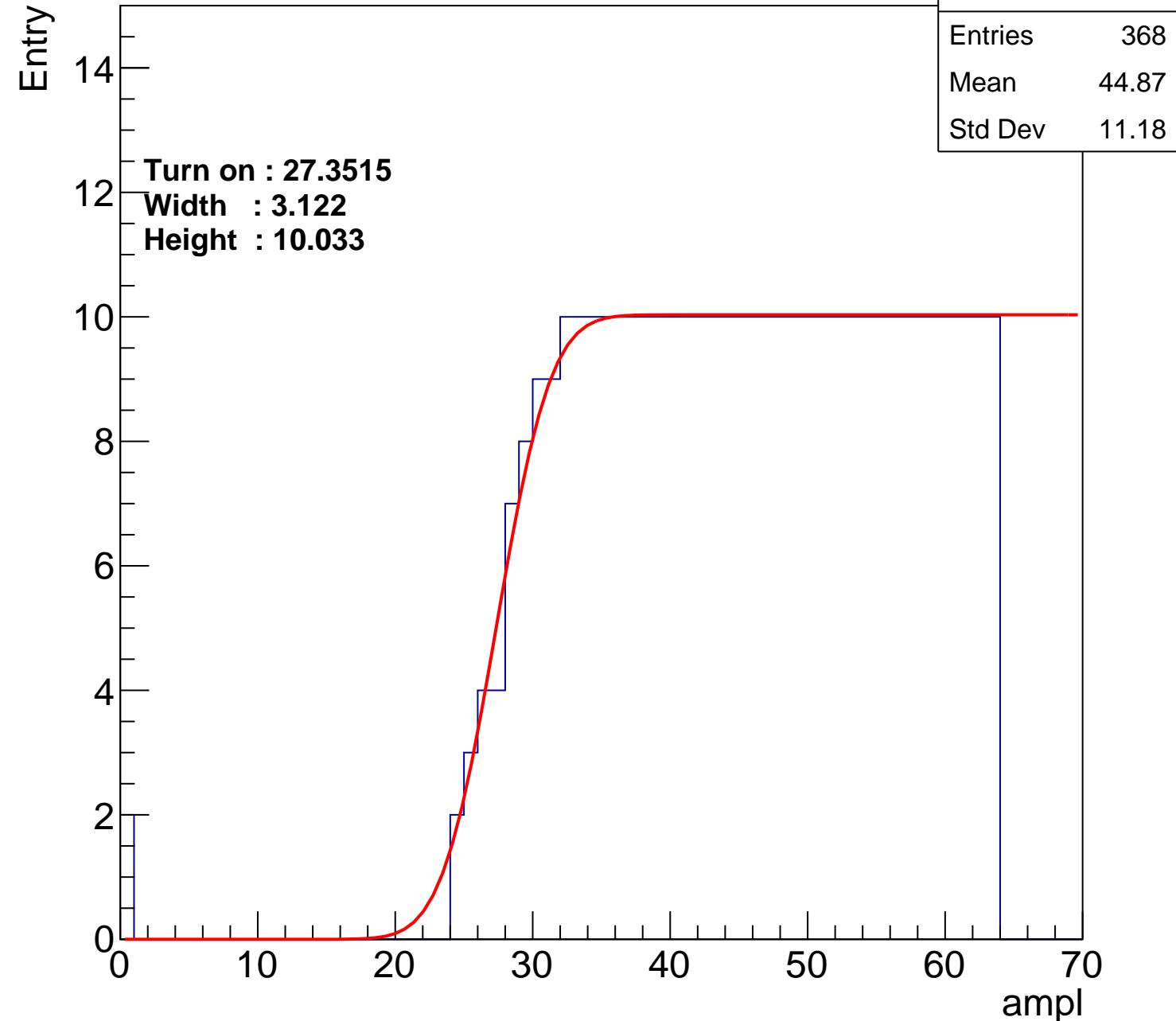
Width : 3.122

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 27.2777

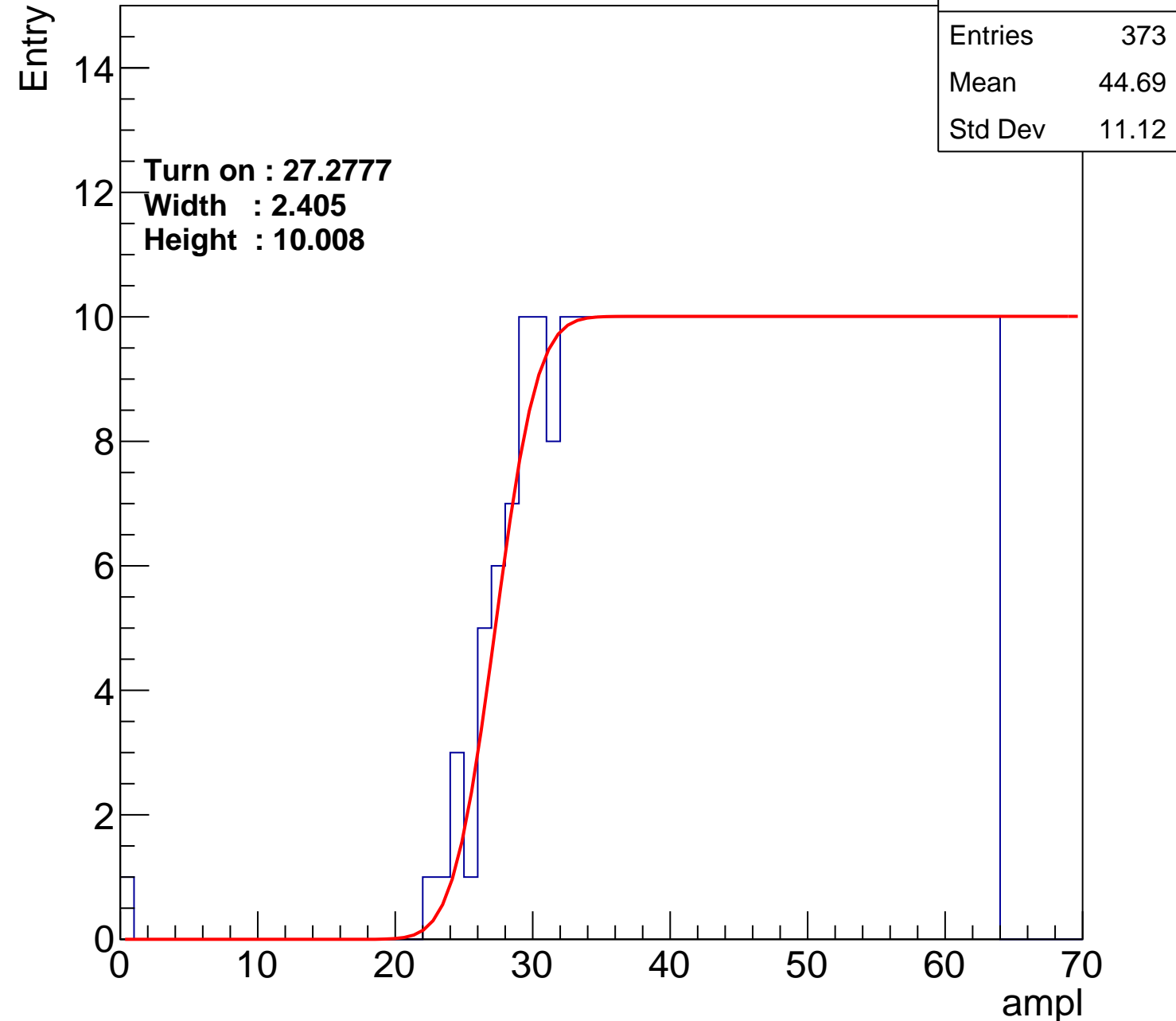
Width : 2.405

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch6

calib_packv5_042523_0143.root, FC#3, port B3

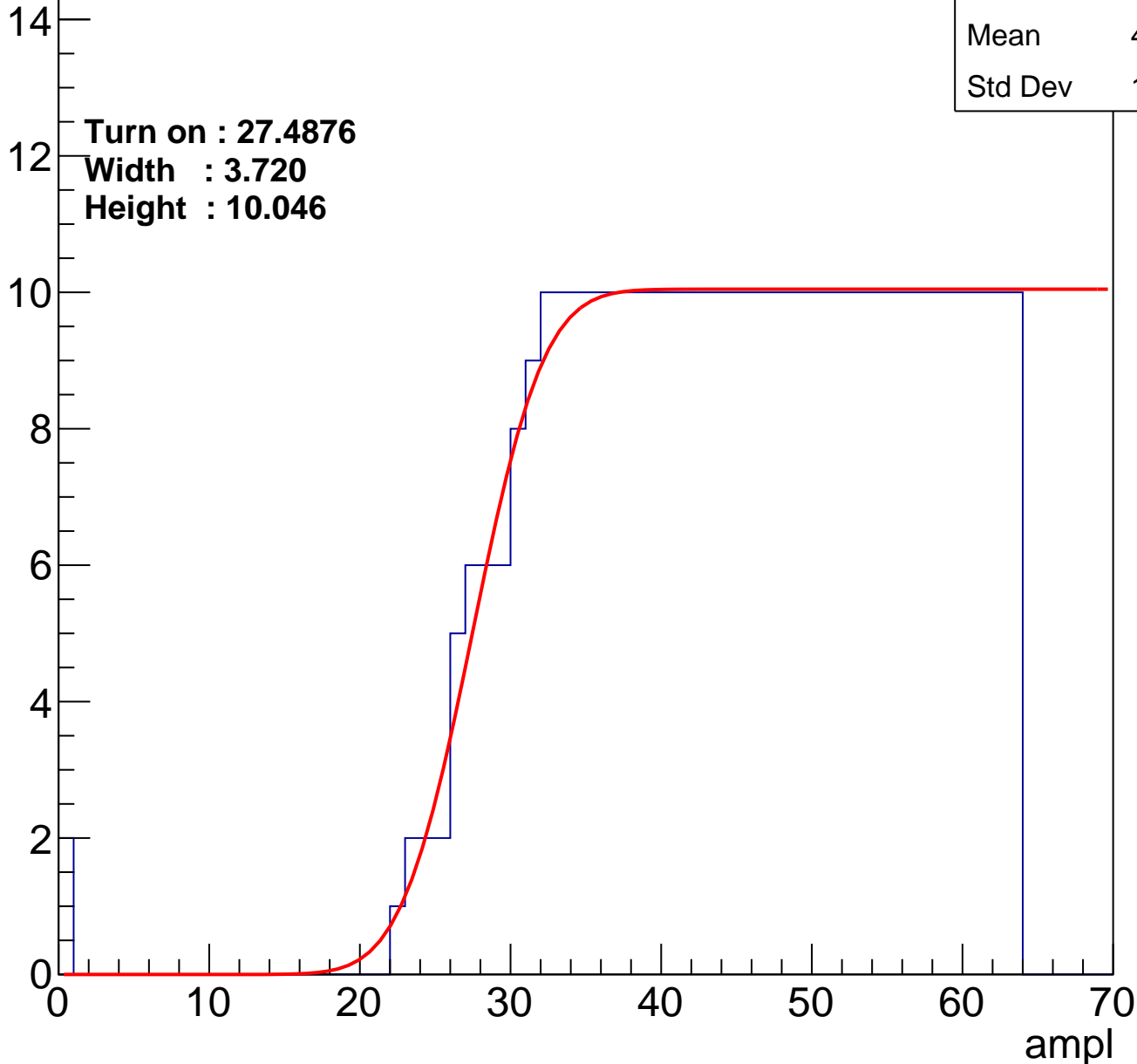
Entries	369
Mean	44.77
Std Dev	11.29

Turn on : 27.4876

Width : 3.720

Height : 10.046

Entry



B1L000S, U25-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.6901

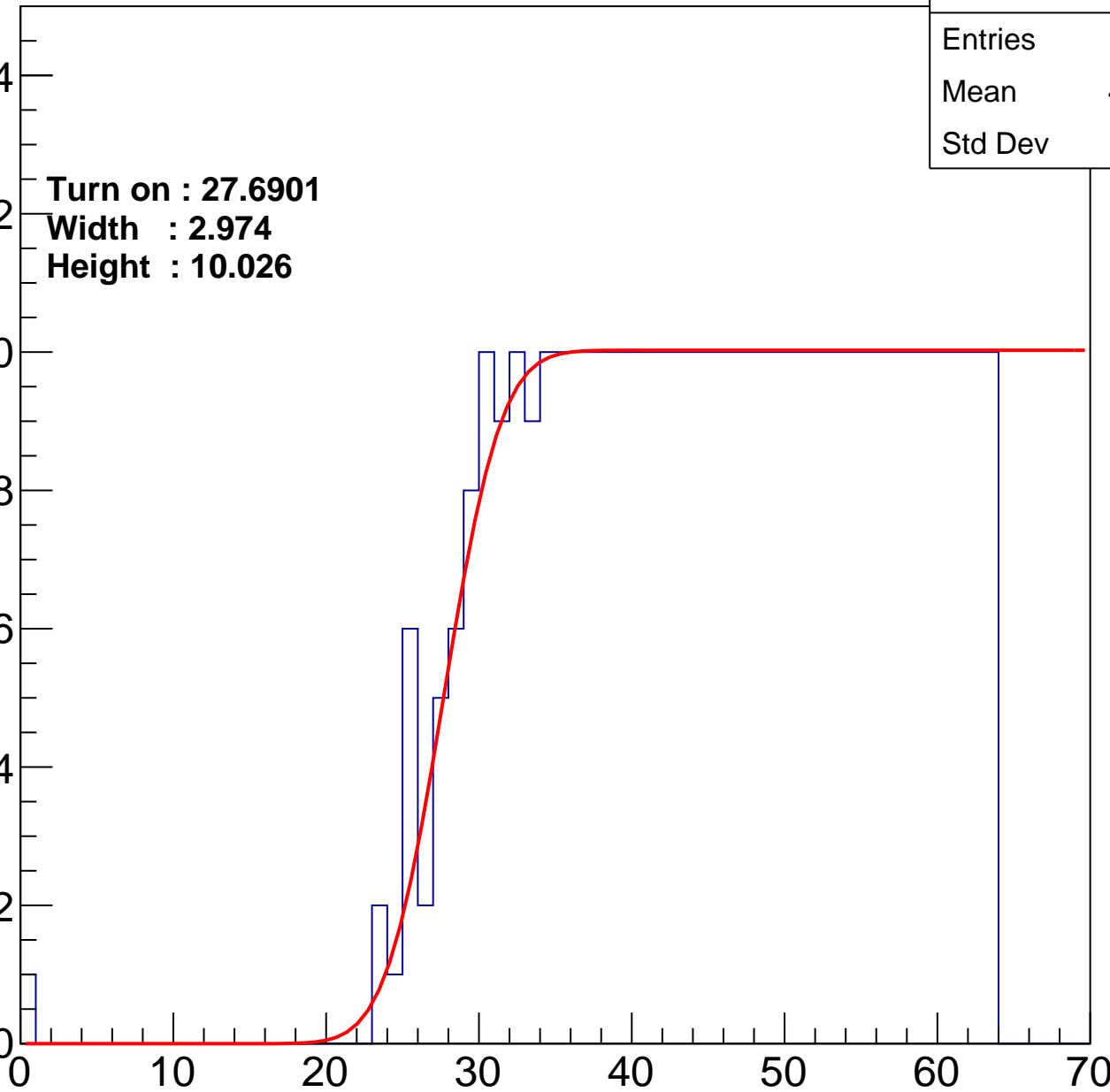
Width : 2.974

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.79
Std Dev	12.15

Turn on : 26.3671

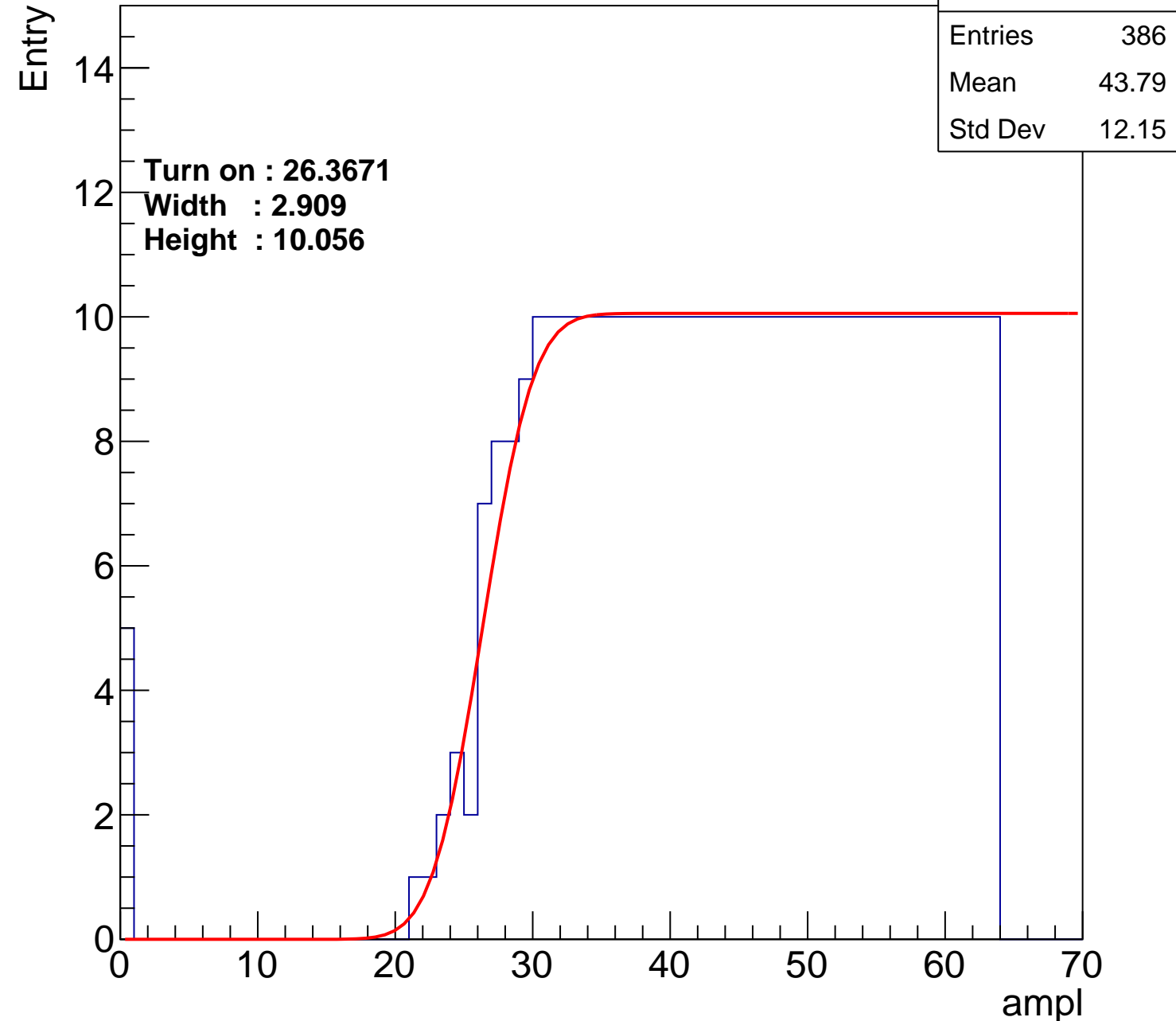
Width : 2.909

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.11
Std Dev	10.93

Turn on : 27.9287

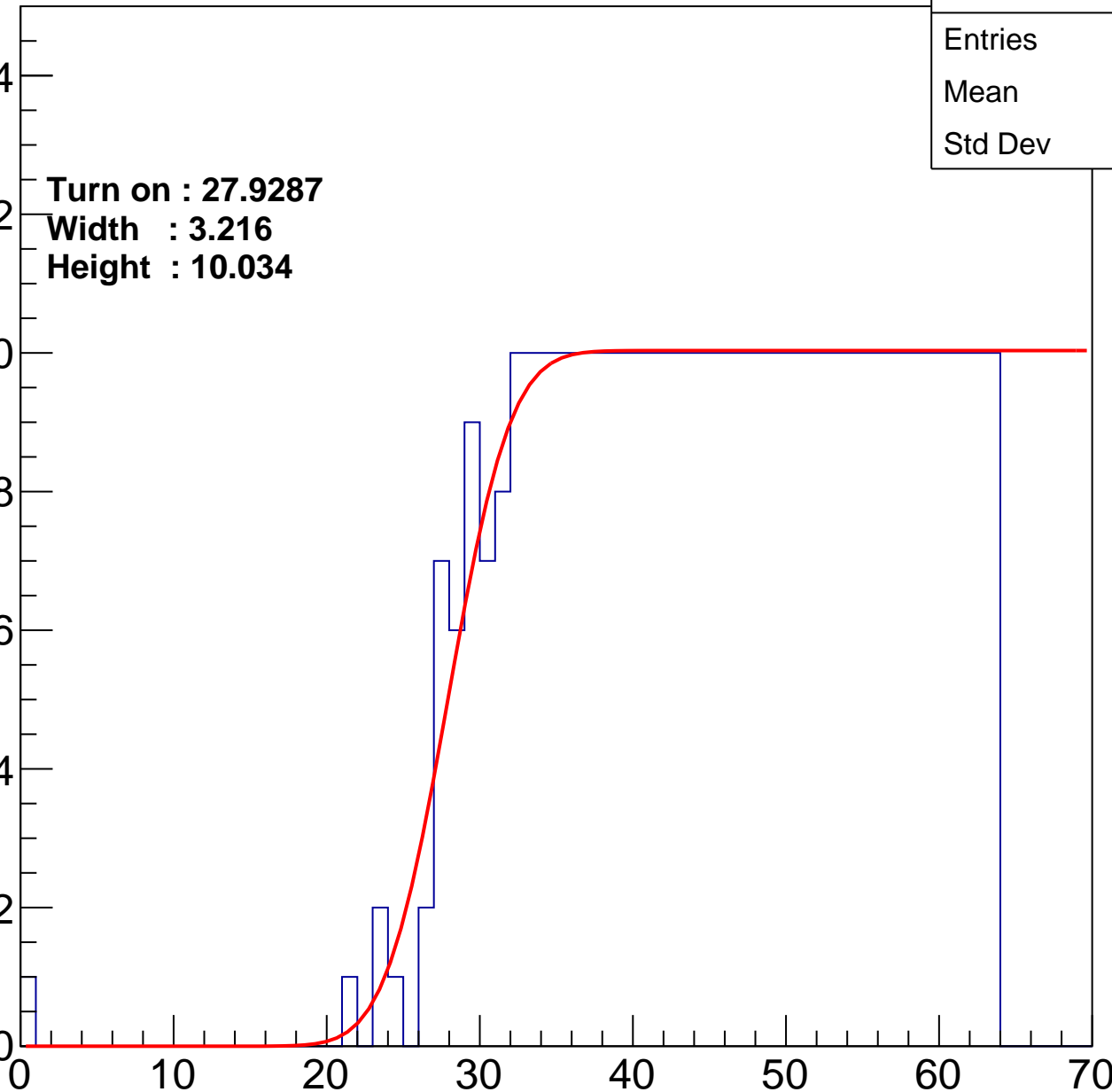
Width : 3.216

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch10

calib_packv5_042523_0143.root, FC#3, port B3

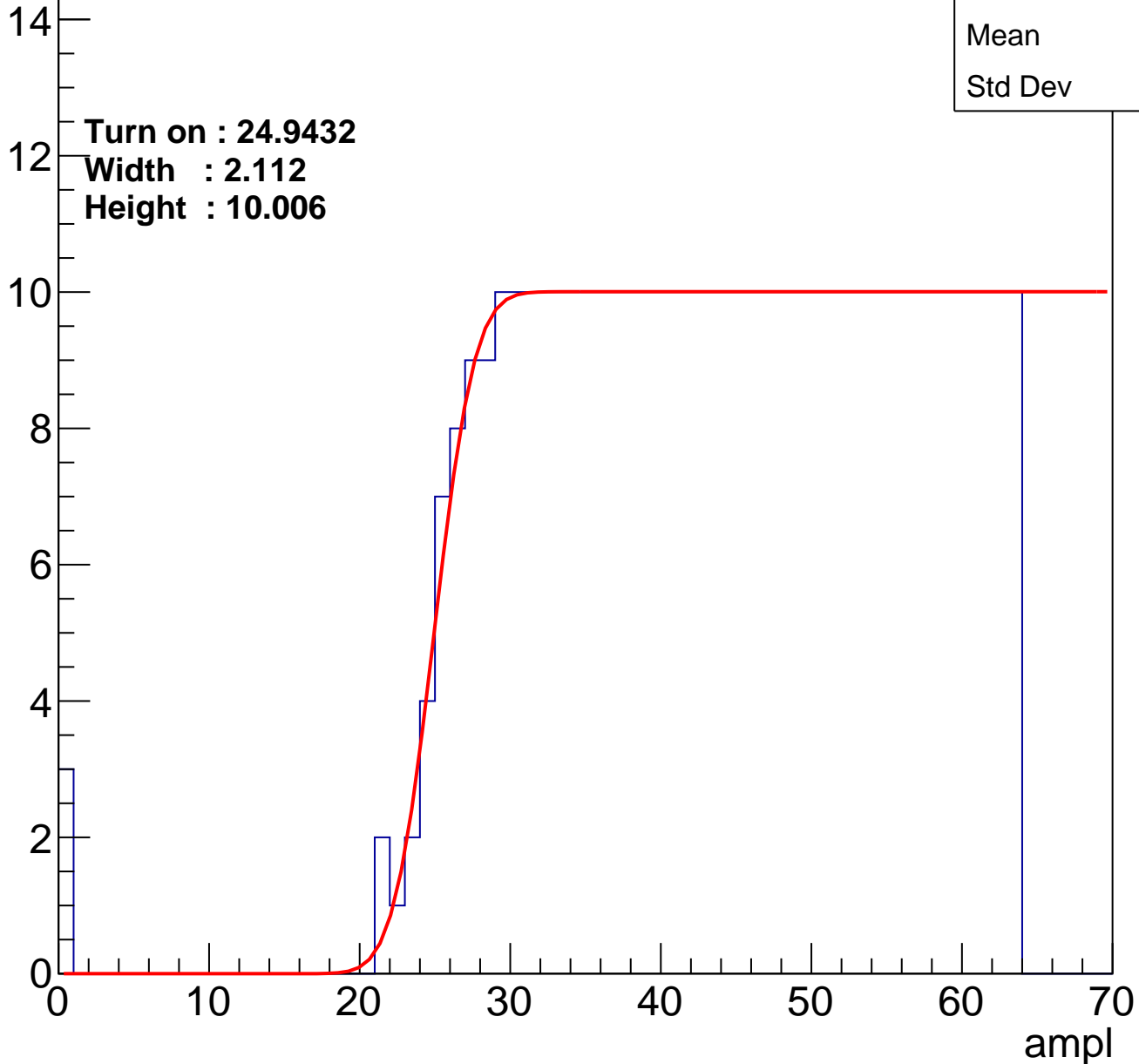
Entries	395
Mean	43.5
Std Dev	12

Turn on : 24.9432

Width : 2.112

Height : 10.006

Entry



B1L000S, U25-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.7337

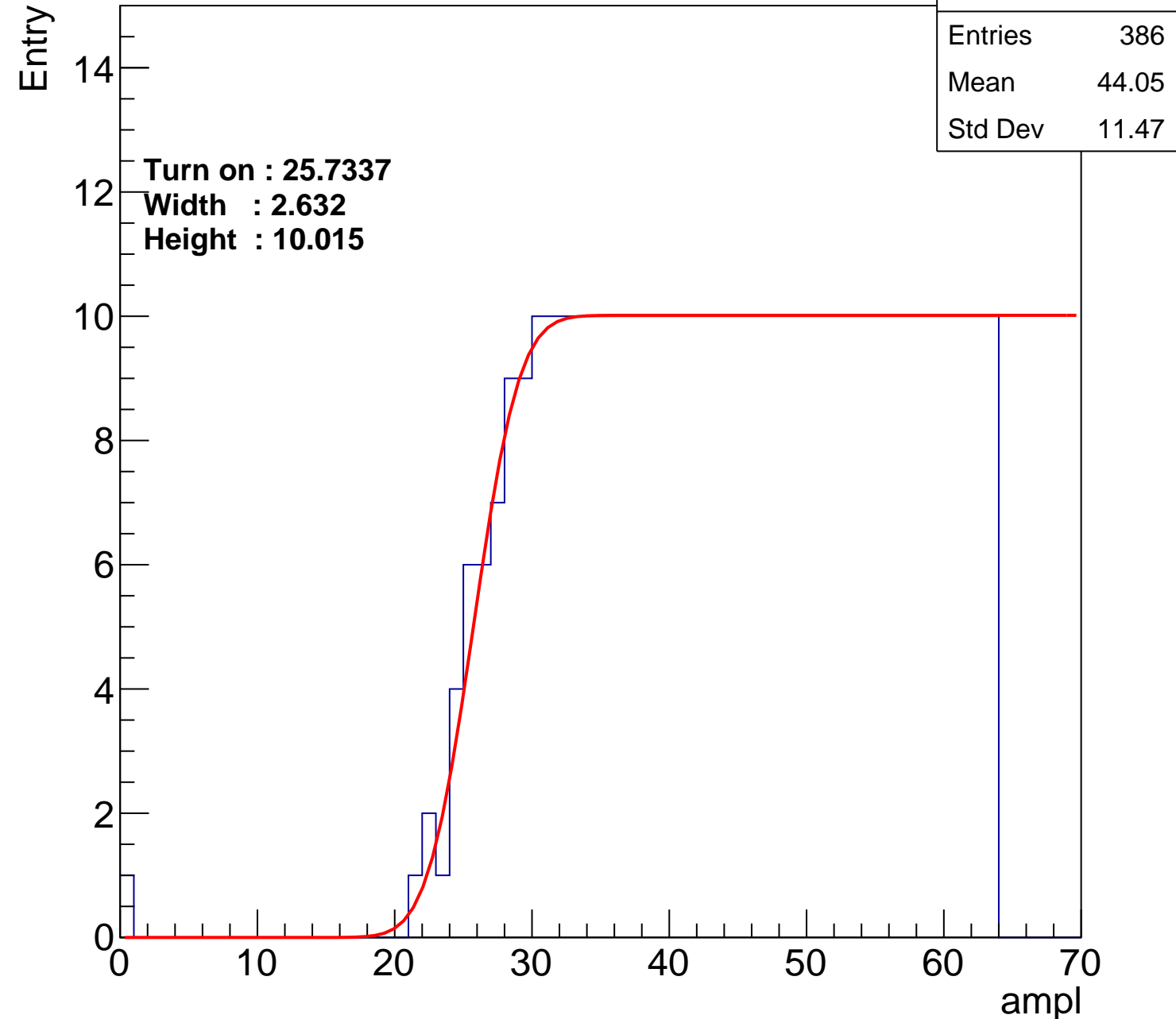
Width : 2.632

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 26.2061

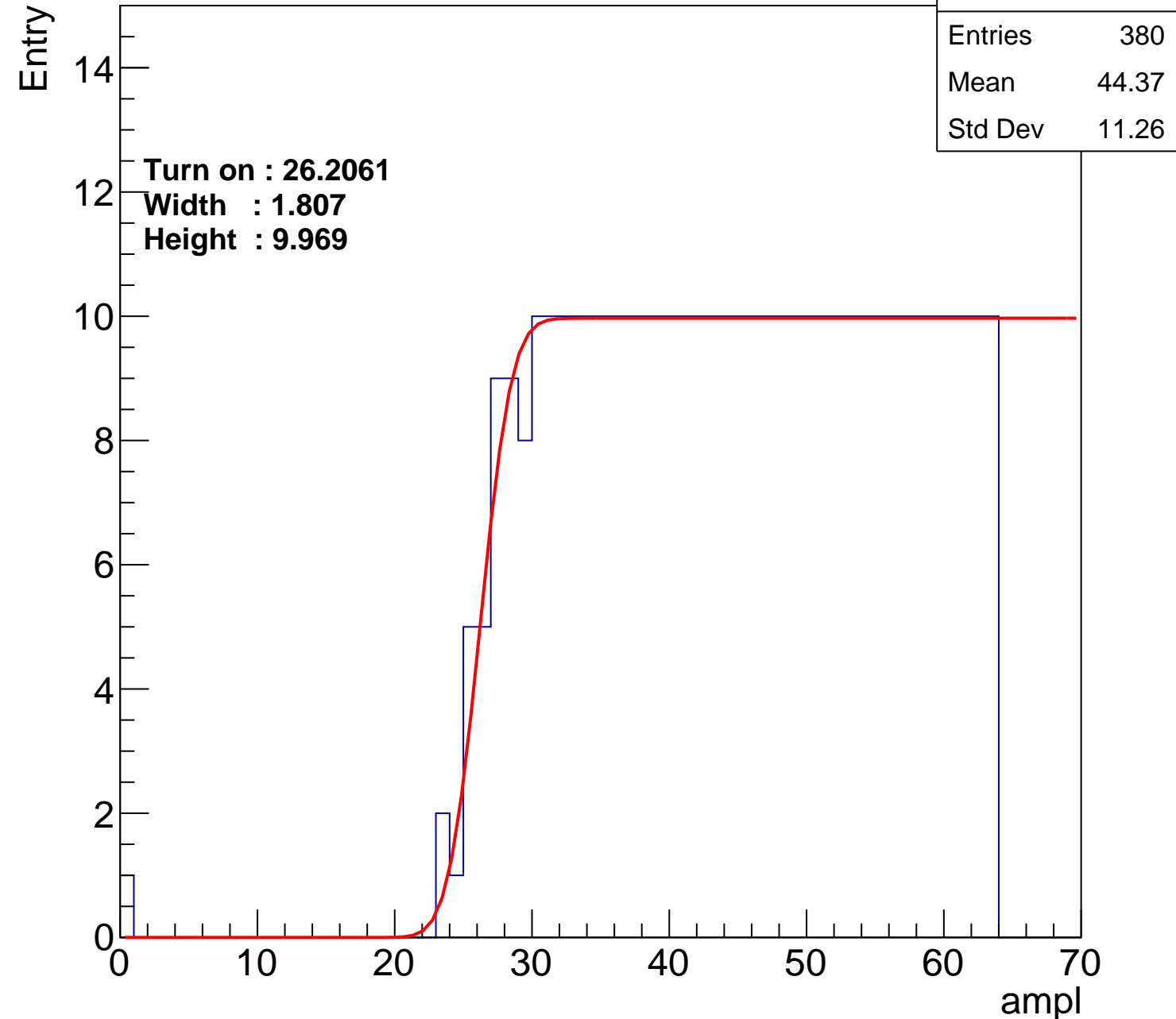
Width : 1.807

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.4722

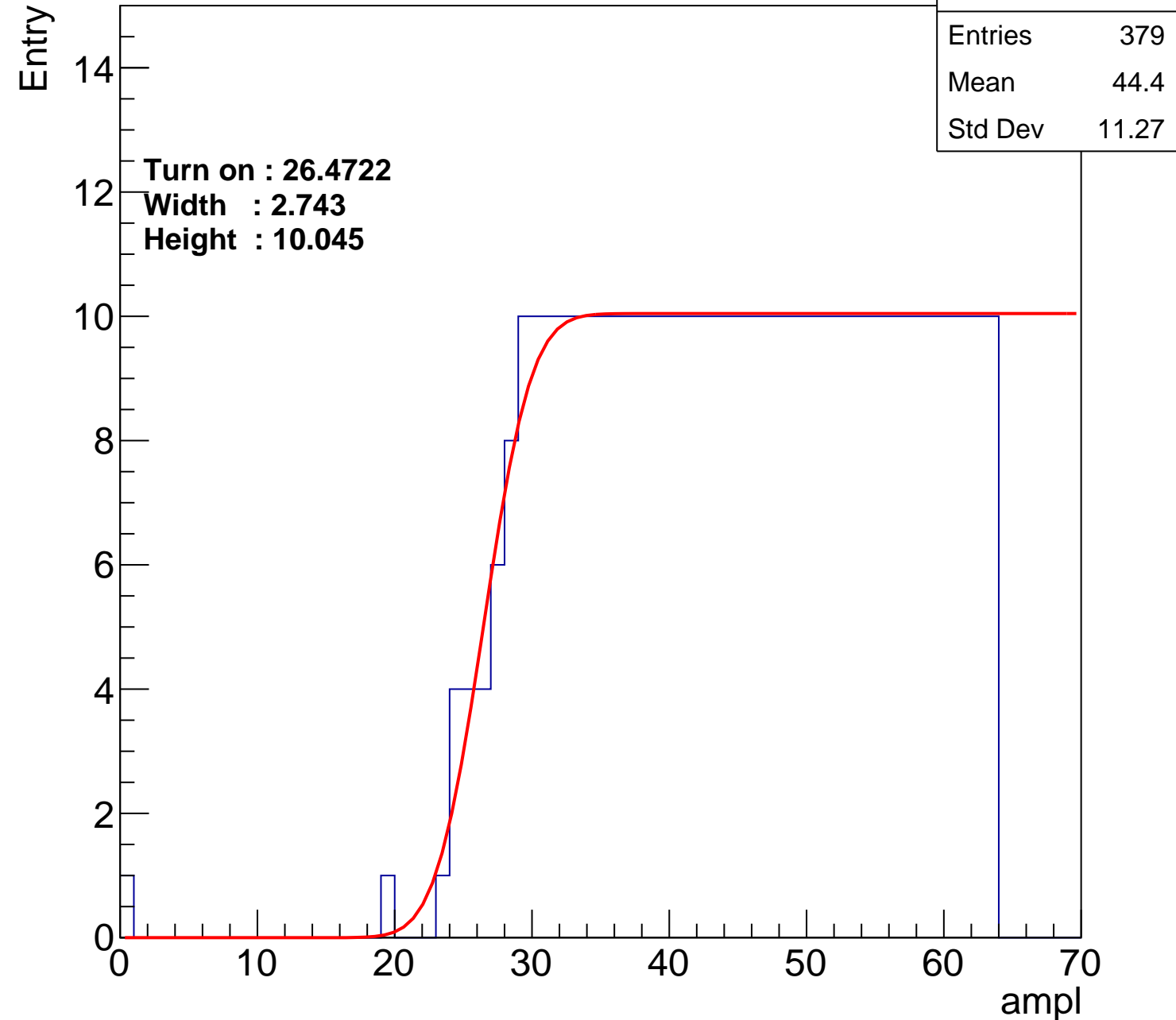
Width : 2.743

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch14

calib_packv5_042523_0143.root, FC#3, port B3

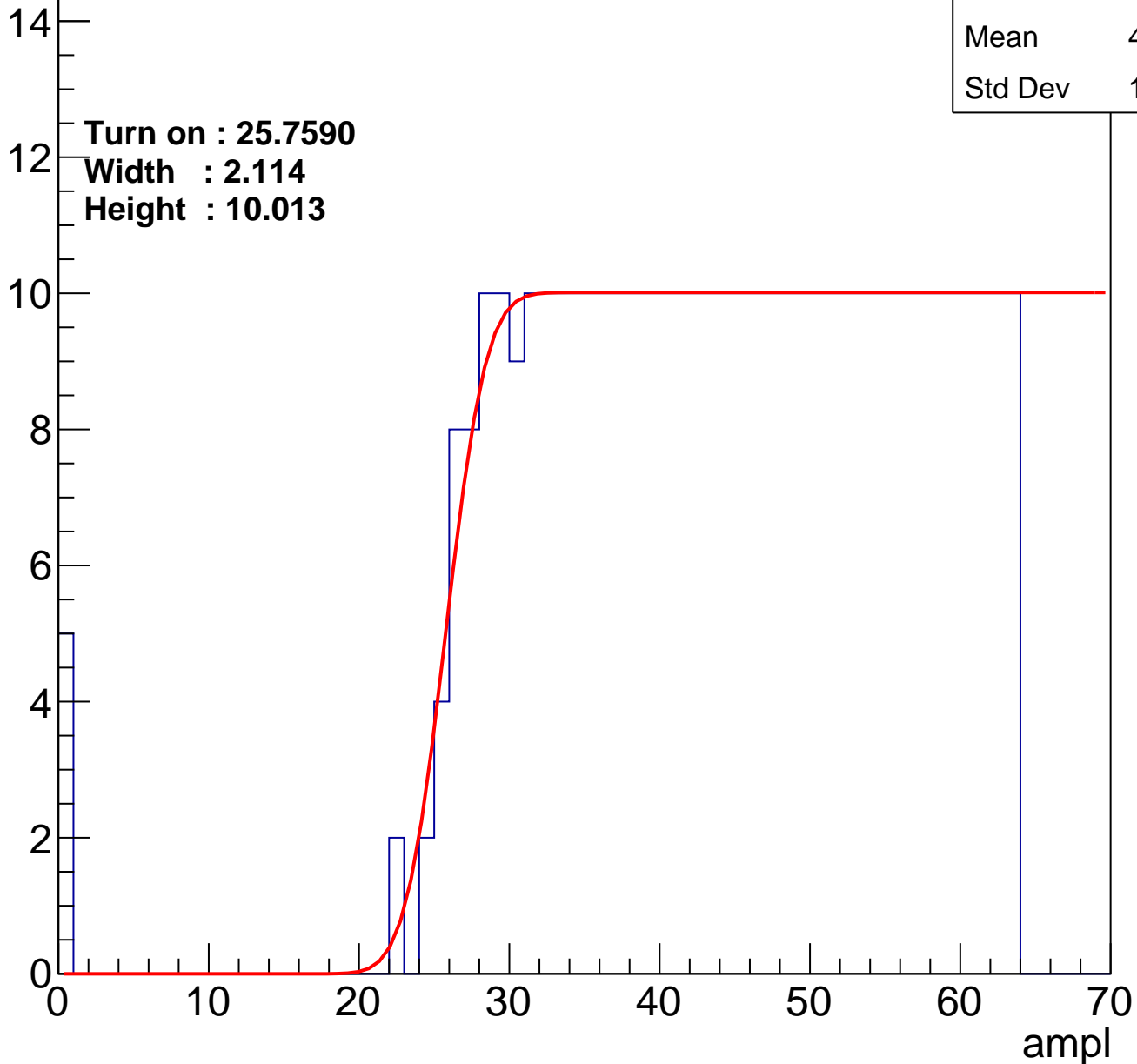
Entries	388
Mean	43.73
Std Dev	12.15

Turn on : 25.7590

Width : 2.114

Height : 10.013

Entry



B1L000S, U25-ch15

calib_packv5_042523_0143.root, FC#3, port B3

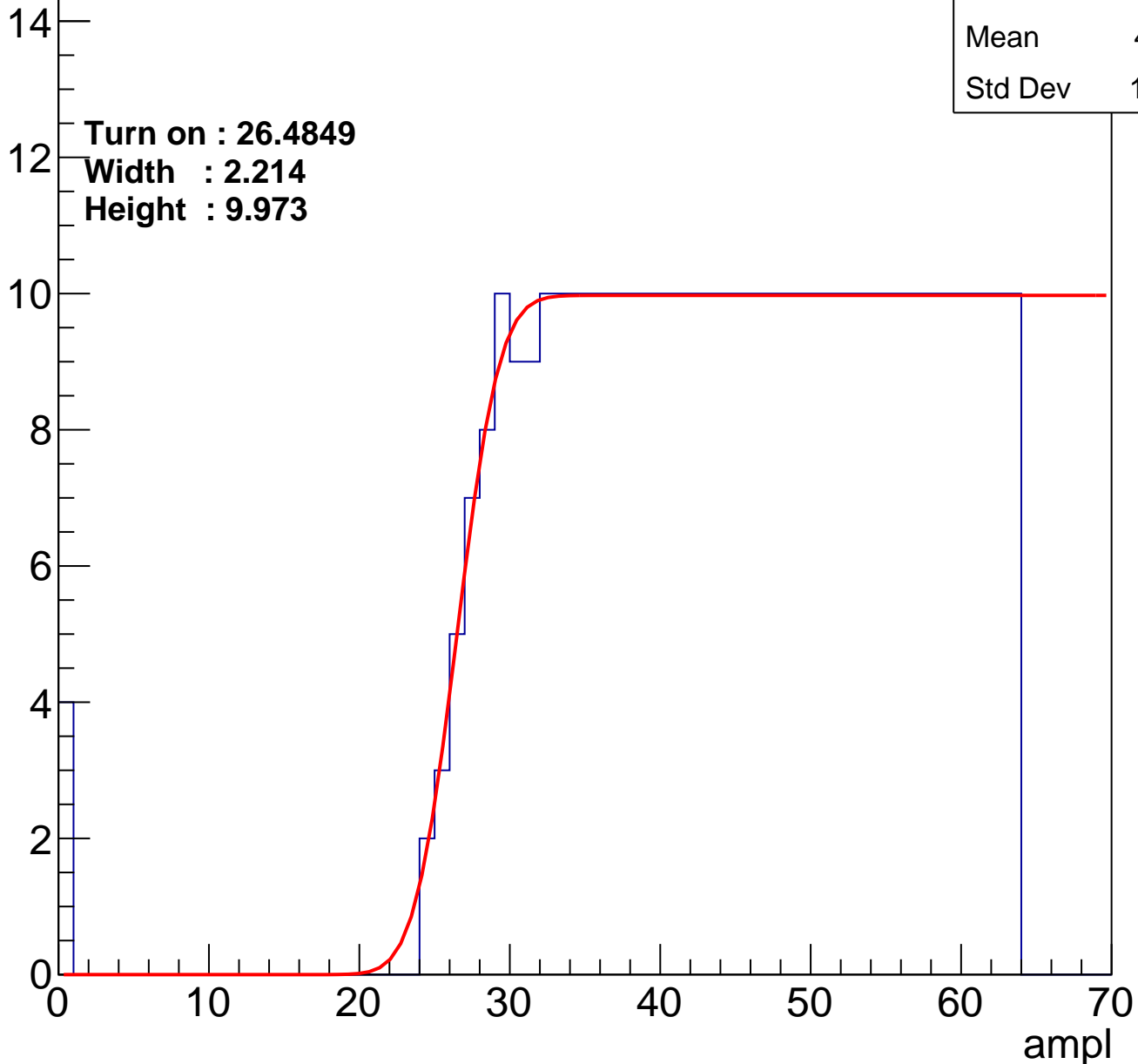
Entries	377
Mean	44.31
Std Dev	11.75

Turn on : 26.4849

Width : 2.214

Height : 9.973

Entry



B1L000S, U25-ch16

calib_packv5_042523_0143.root, FC#3, port B3

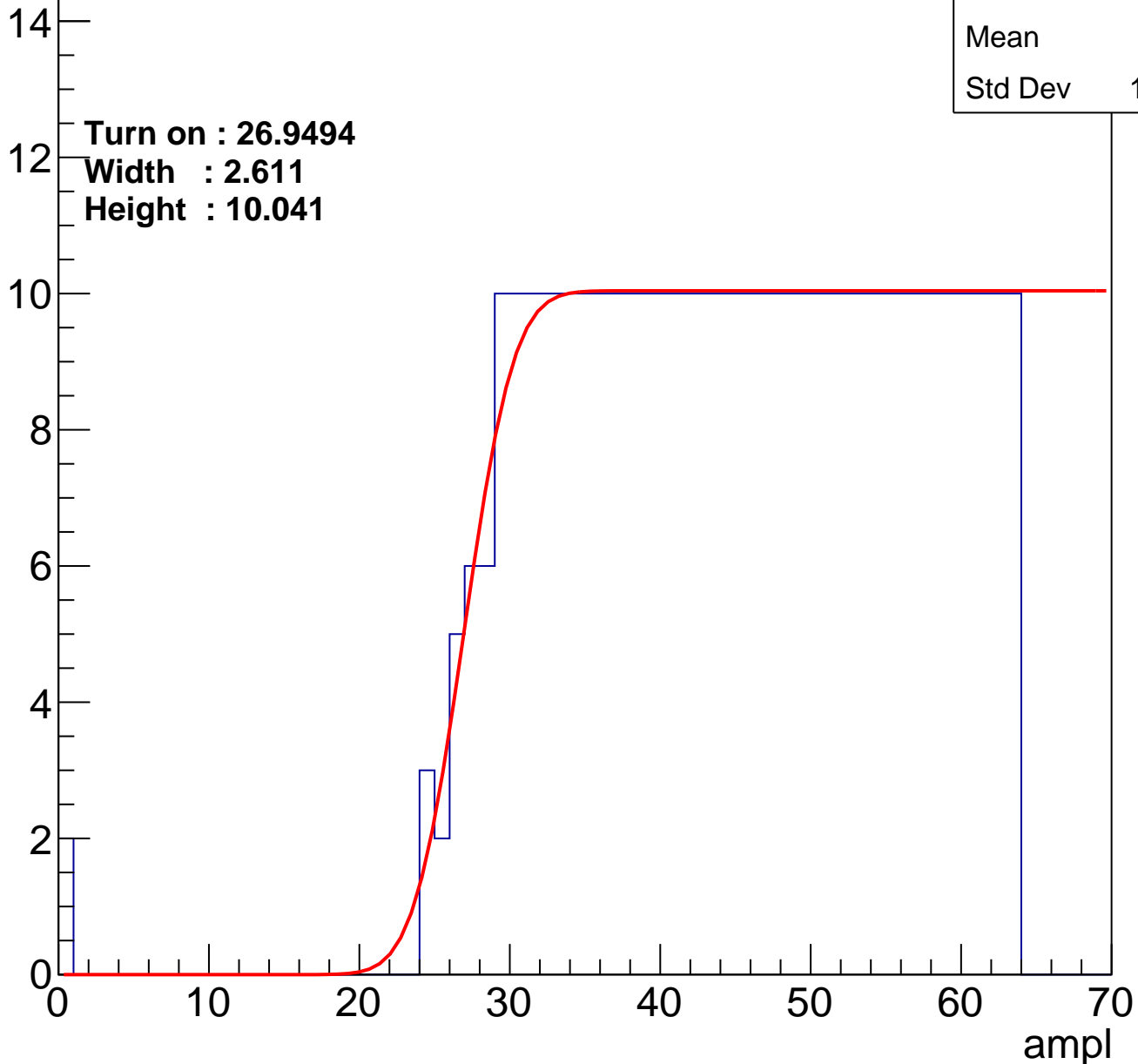
Entries	374
Mean	44.6
Std Dev	11.29

Turn on : 26.9494

Width : 2.611

Height : 10.041

Entry



B1L000S, U25-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.95
Std Dev	11.02

Turn on : 27.8410

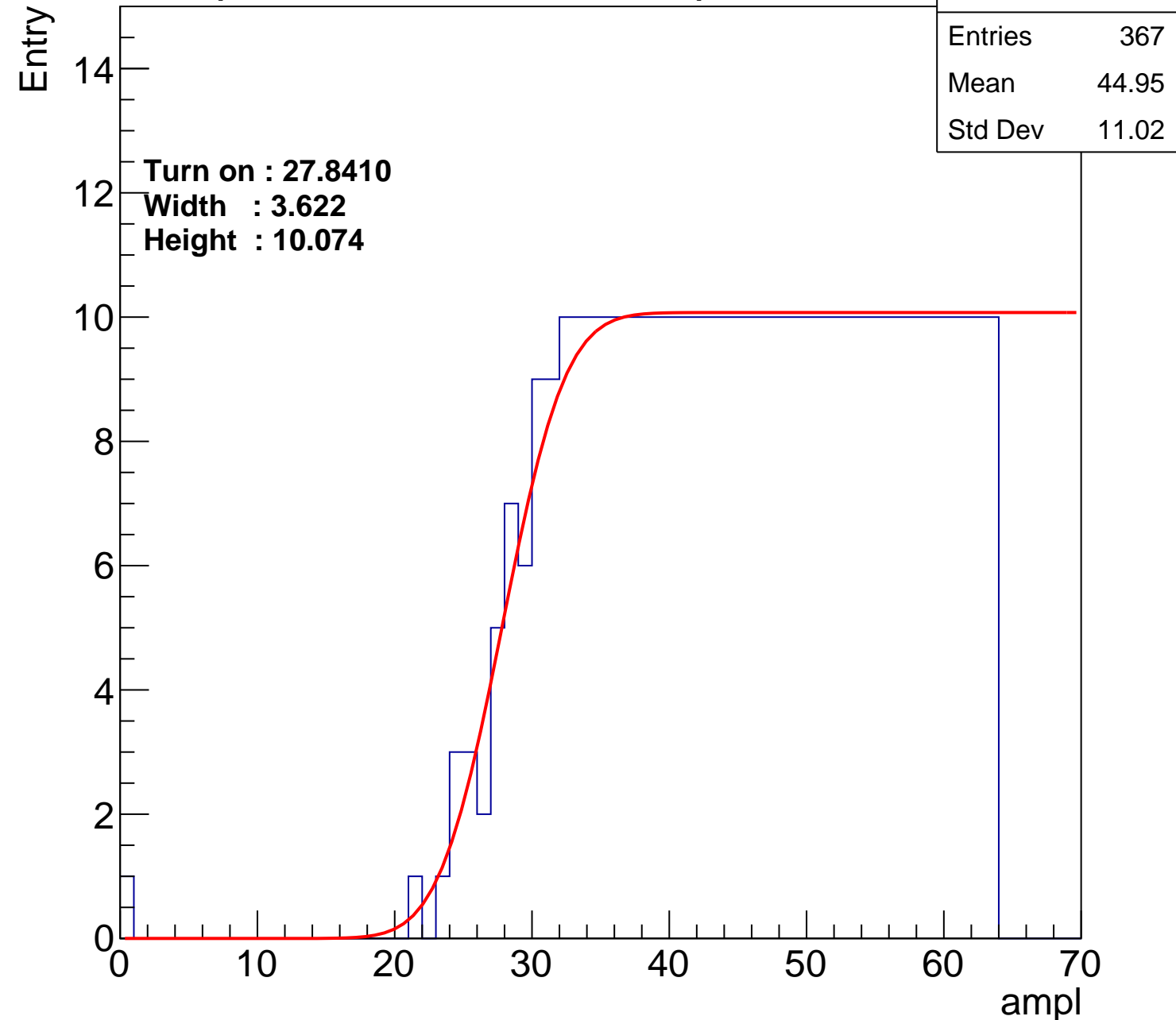
Width : 3.622

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 27.9460

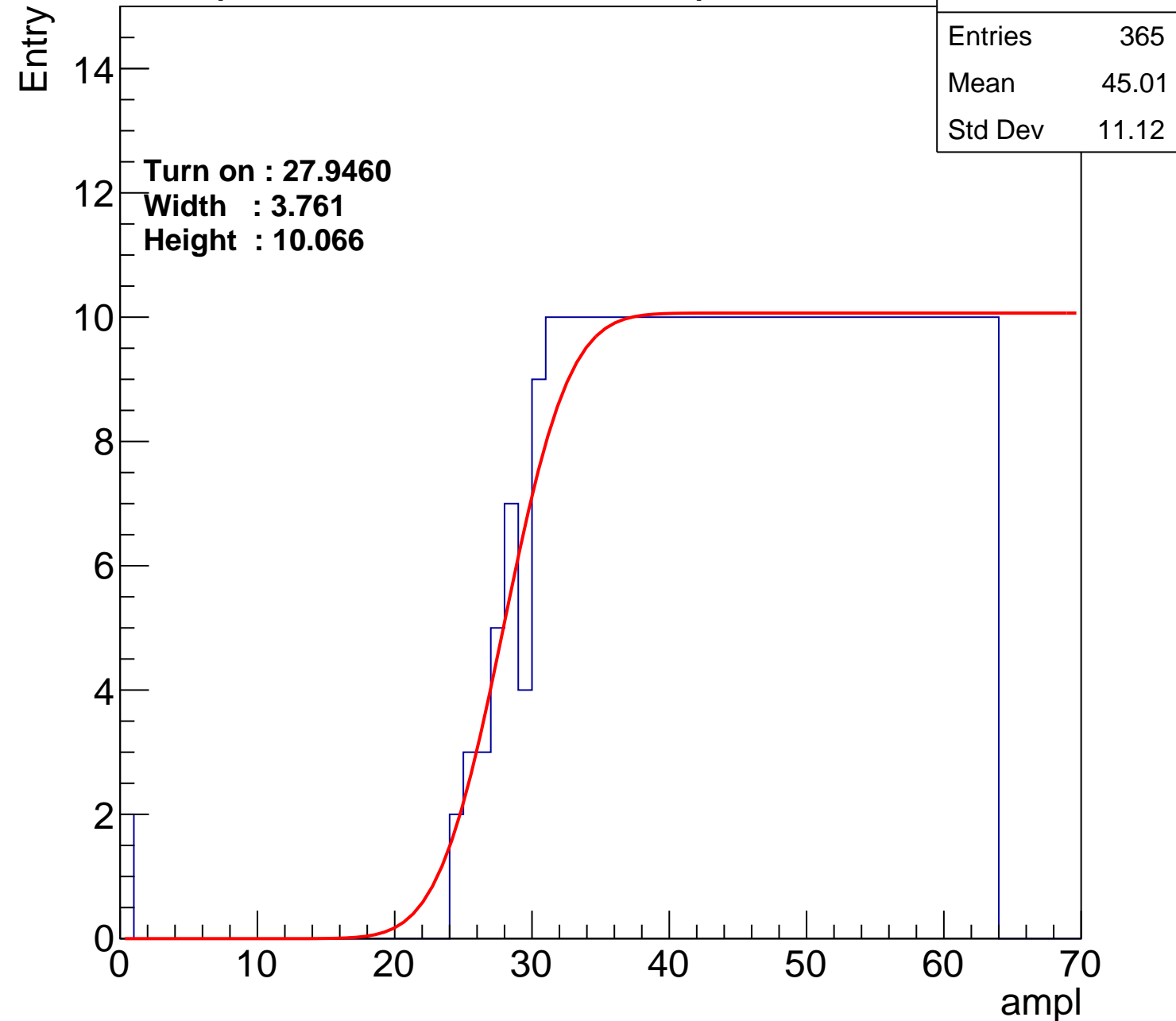
Width : 3.761

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.6
Std Dev	11.63

Turn on : 27.3334

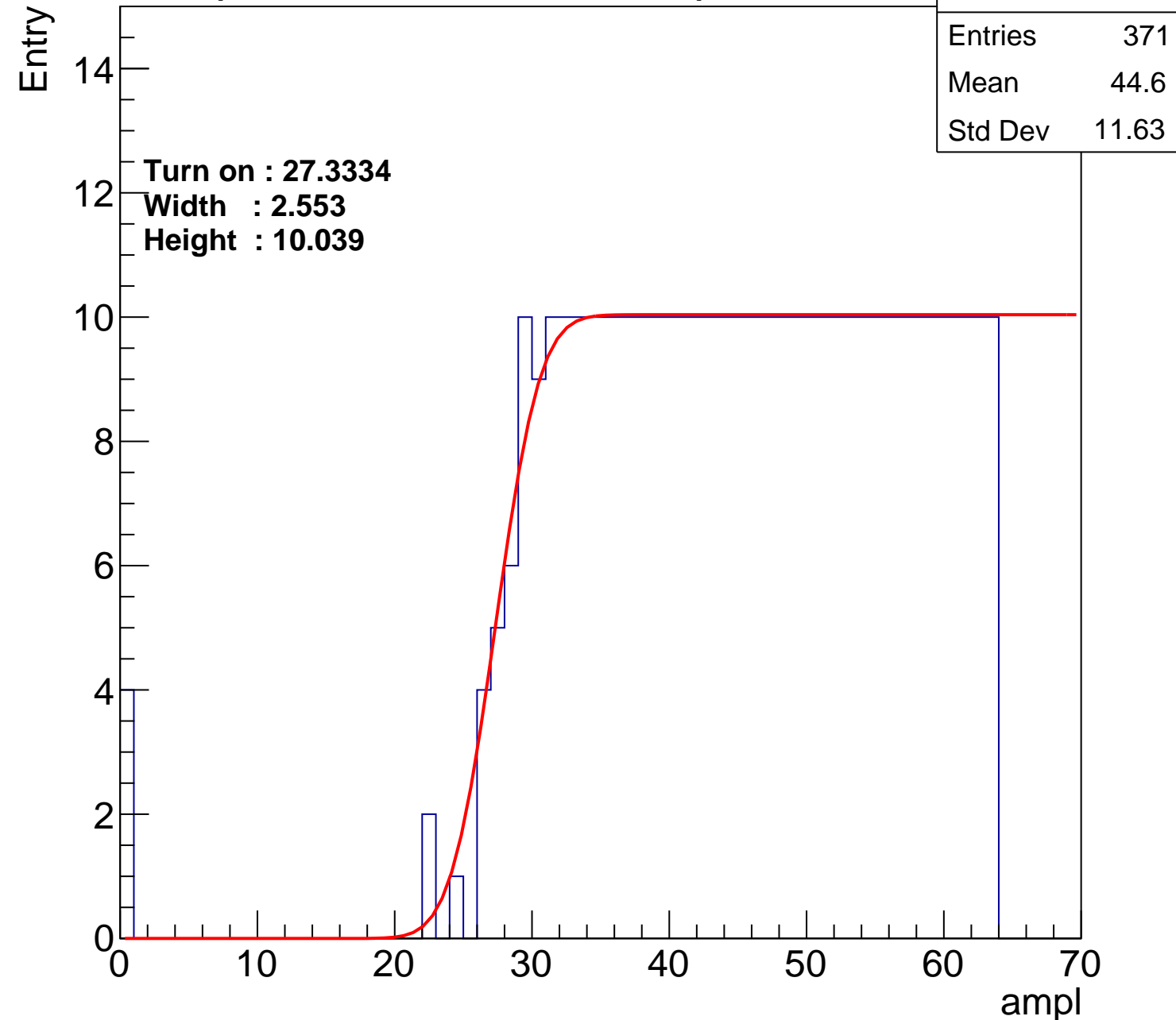
Width : 2.553

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch20

calib_packv5_042523_0143.root, FC#3, port B3

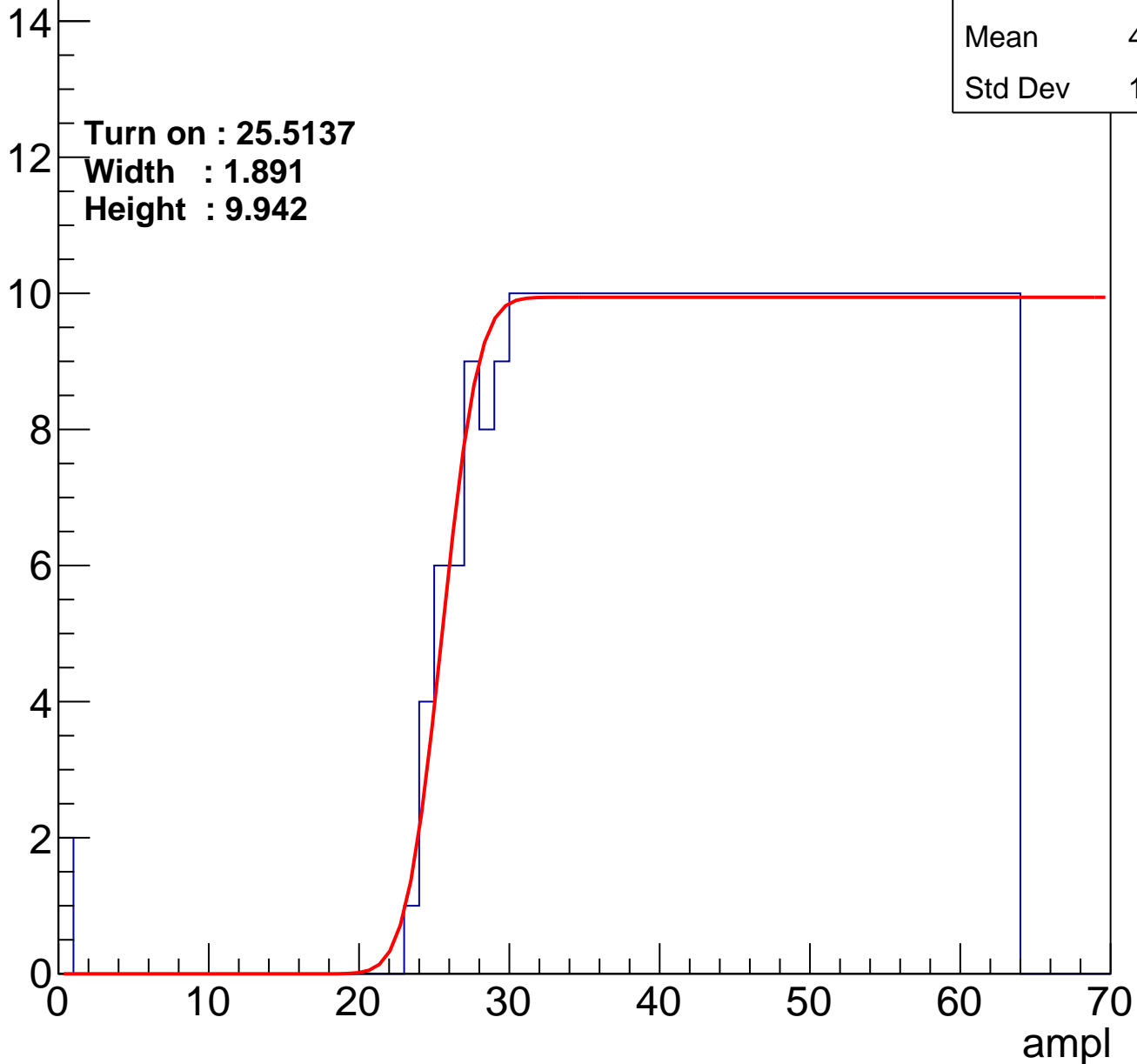
Entries	385
Mean	44.06
Std Dev	11.57

Turn on : 25.5137

Width : 1.891

Height : 9.942

Entry



B1L000S, U25-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
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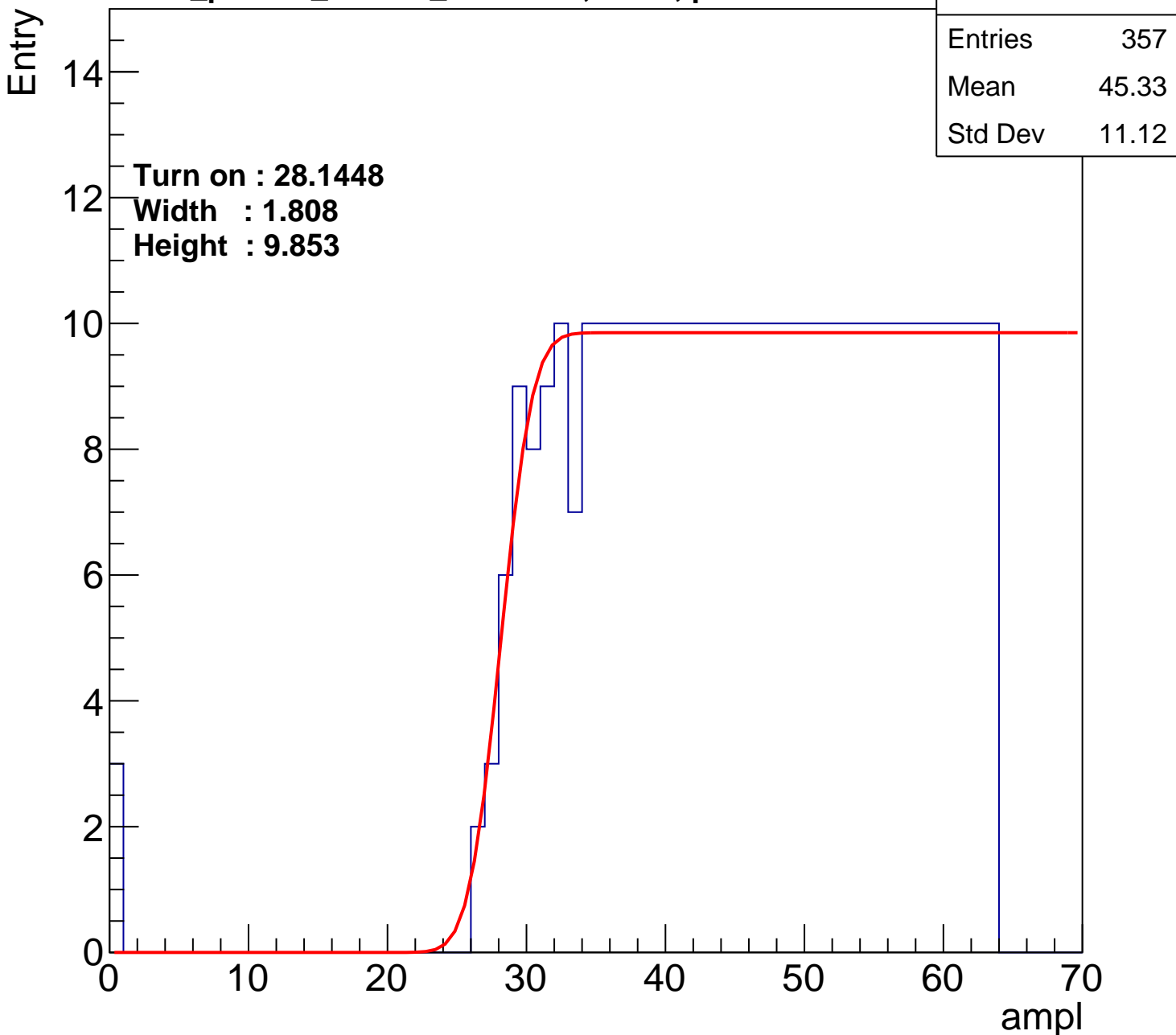
Mean	45.33
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Std Dev	11.12
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Turn on : 28.1448

Width : 1.808

Height : 9.853



B1L000S, U25-ch22

calib_packv5_042523_0143.root, FC#3, port B3

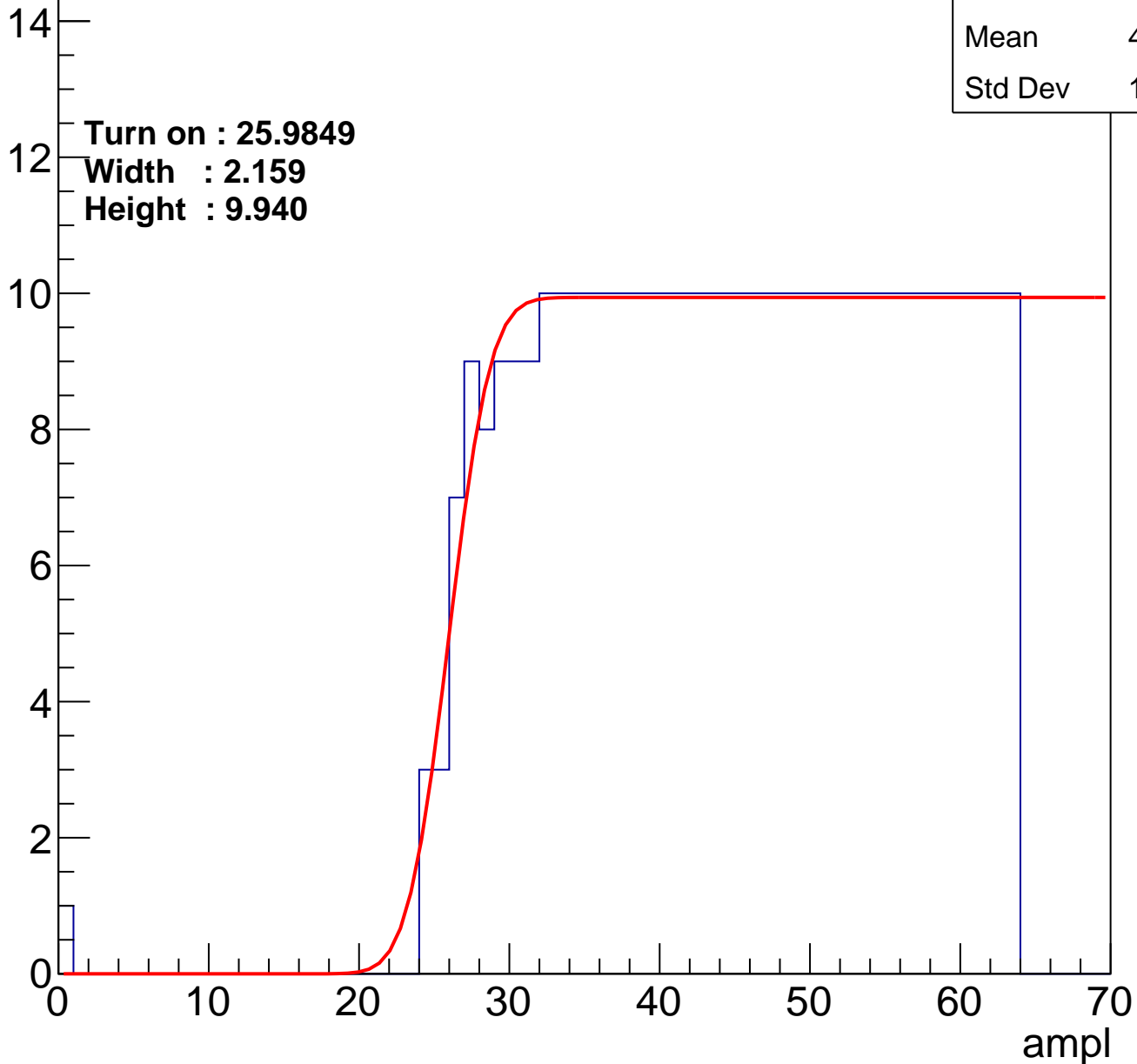
Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 25.9849

Width : 2.159

Height : 9.940

Entry



B1L000S, U25-ch23

calib_packv5_042523_0143.root, FC#3, port B3

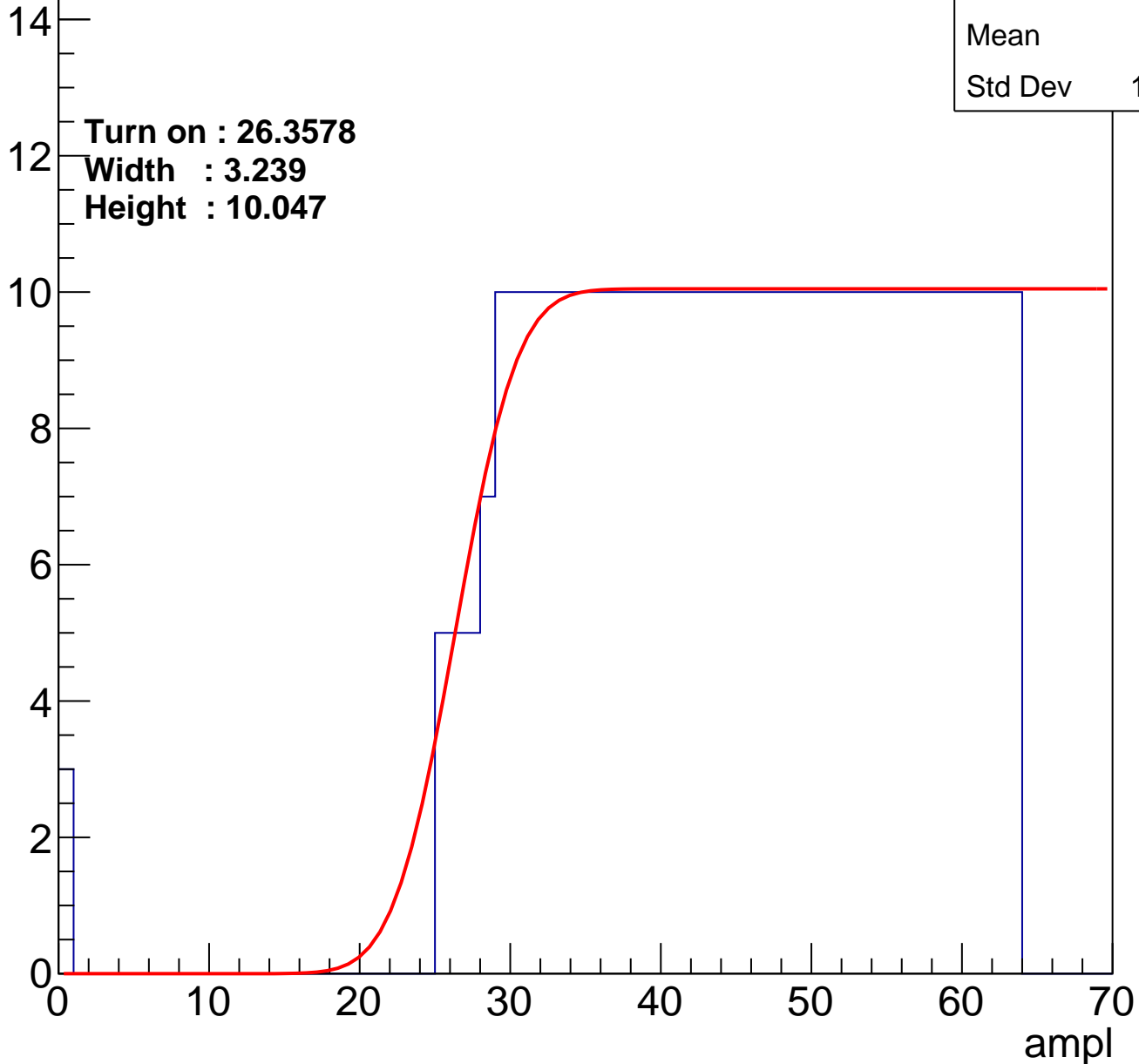
Entries	375
Mean	44.5
Std Dev	11.49

Turn on : 26.3578

Width : 3.239

Height : 10.047

Entry



B1L000S, U25-ch24

calib_packv5_042523_0143.root, FC#3, port B3

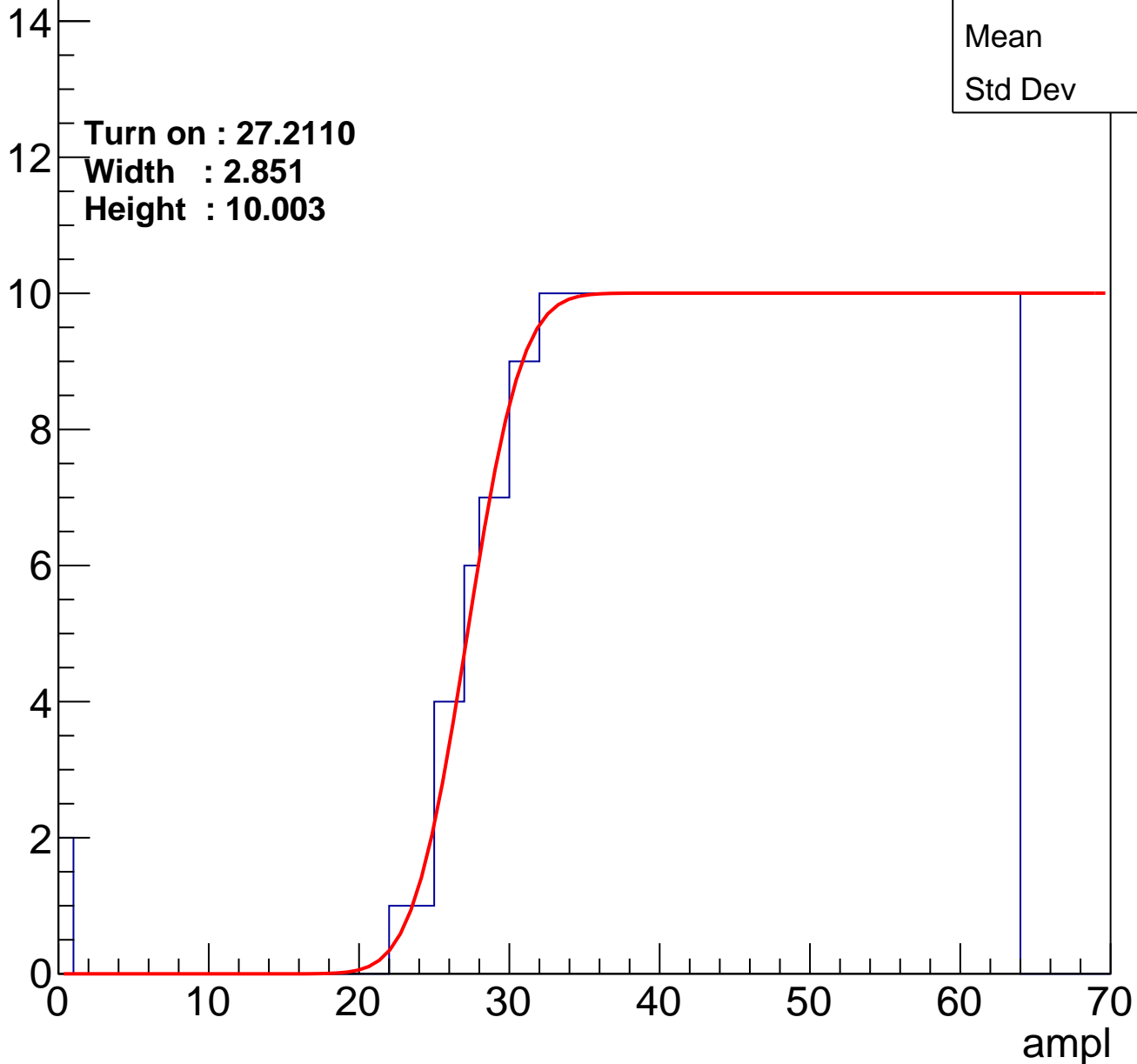
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.2110

Width : 2.851

Height : 10.003

Entry



B1L000S, U25-ch25

calib_packv5_042523_0143.root, FC#3, port B3

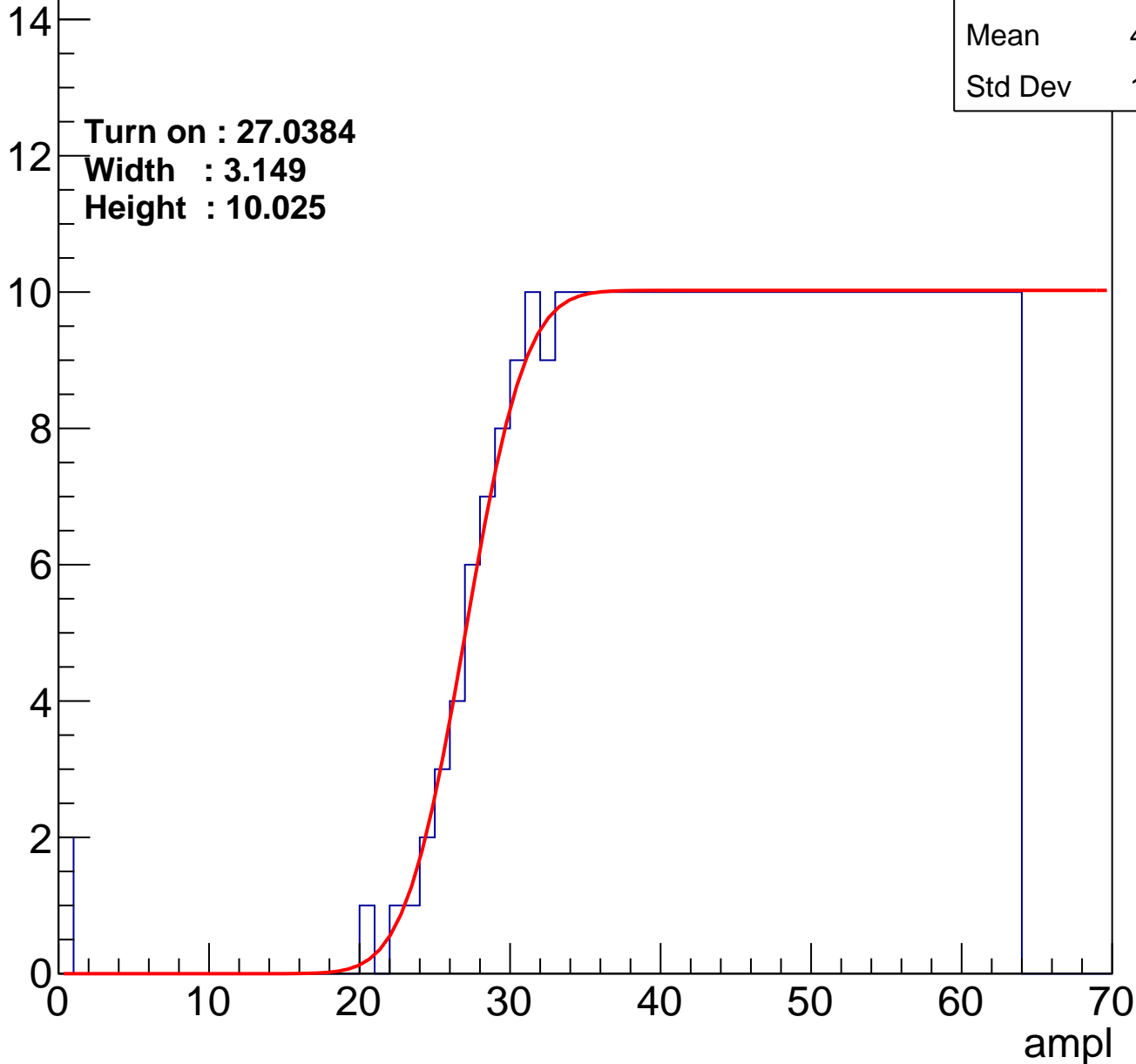
Entries	373
Mean	44.58
Std Dev	11.37

Turn on : 27.0384

Width : 3.149

Height : 10.025

Entry



B1L000S, U25-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.98
Std Dev	10.92

Turn on : 27.3086

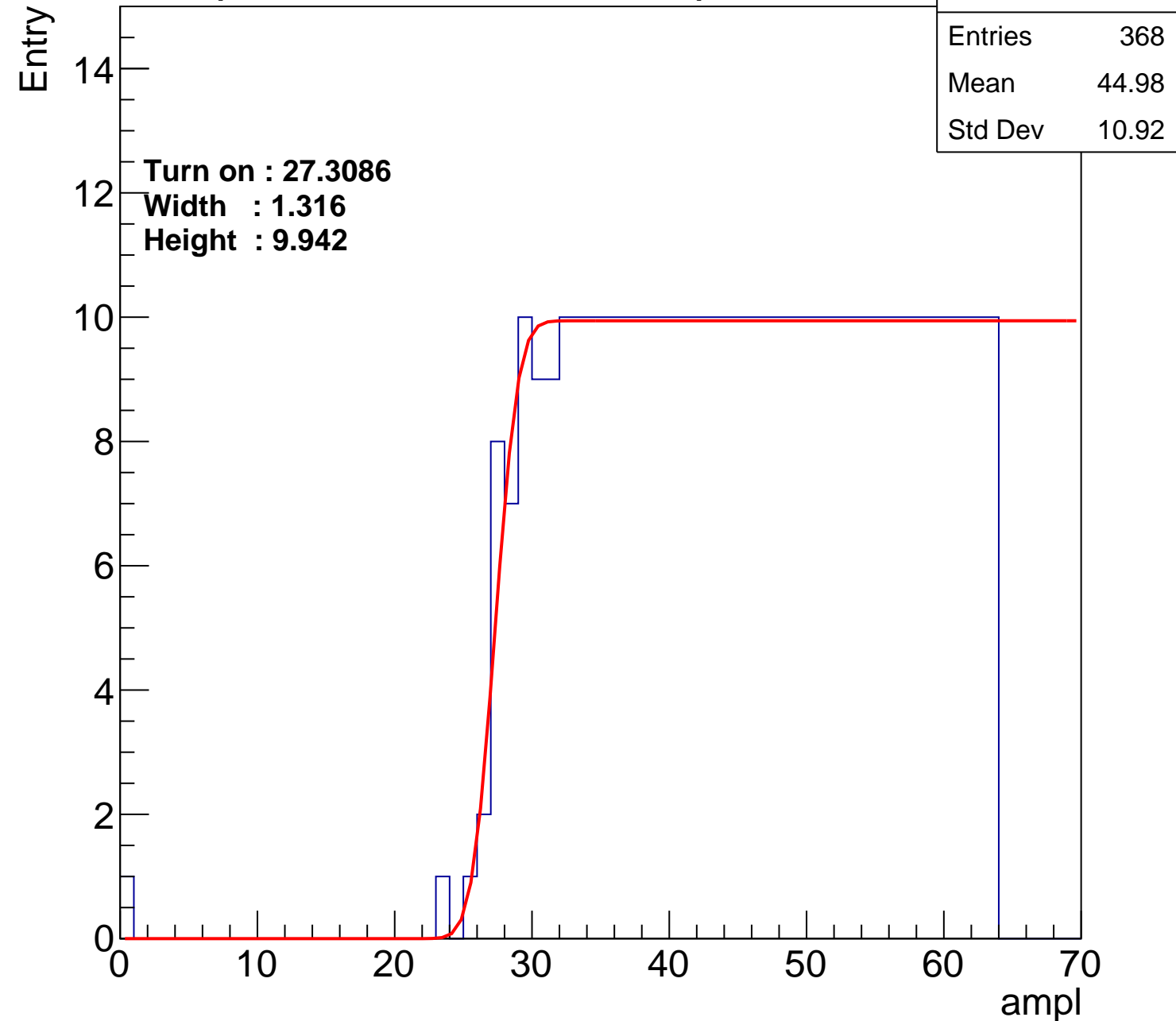
Width : 1.316

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.0703

Width : 1.597

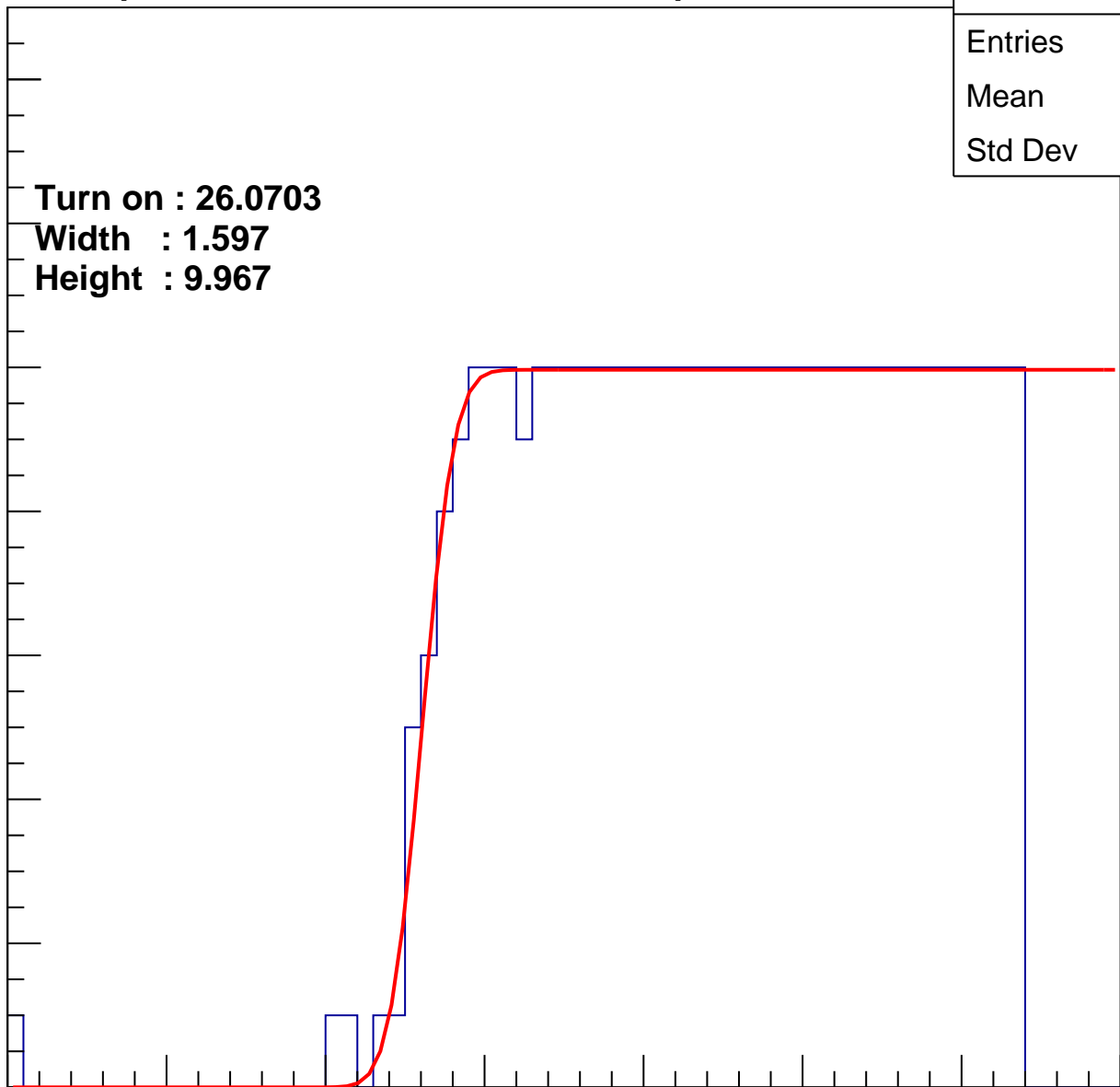
Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U25-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.45
Std Dev	11.24

Turn on : 26.6487

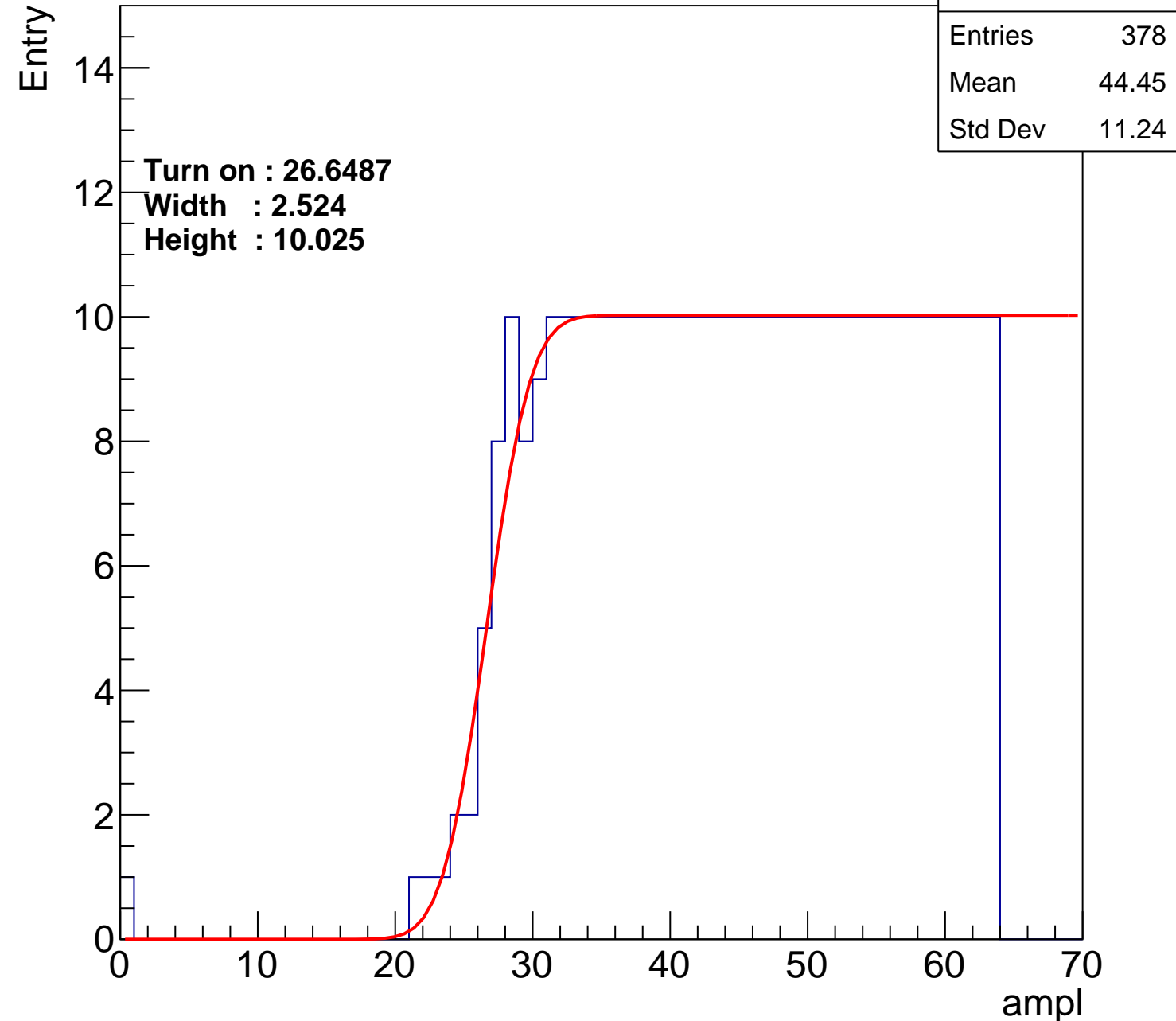
Width : 2.524

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.07
Std Dev	11.44

Turn on : 25.7886

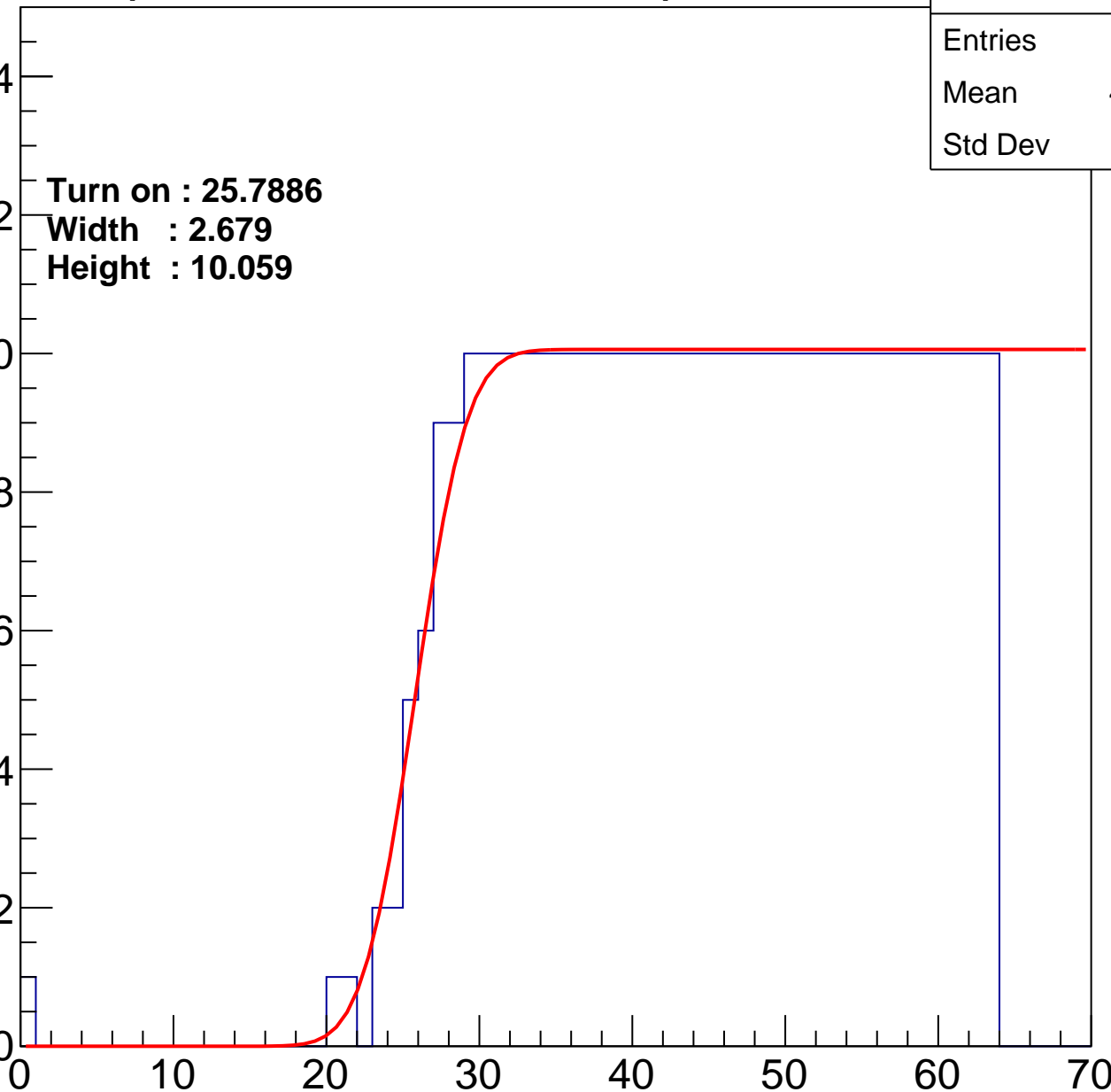
Width : 2.679

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.8
Std Dev	11.85

Turn on : 25.6995

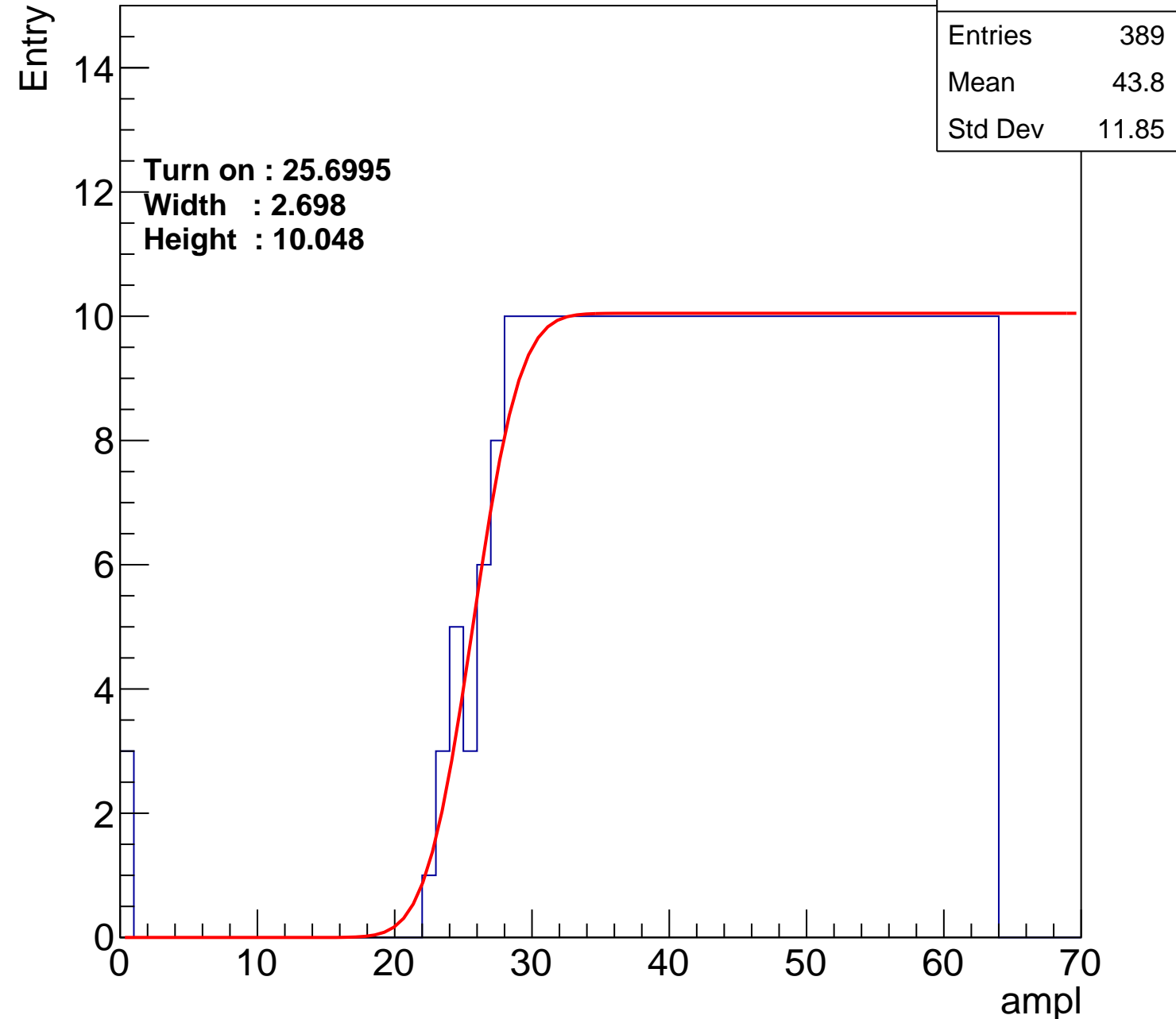
Width : 2.698

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 26.3792

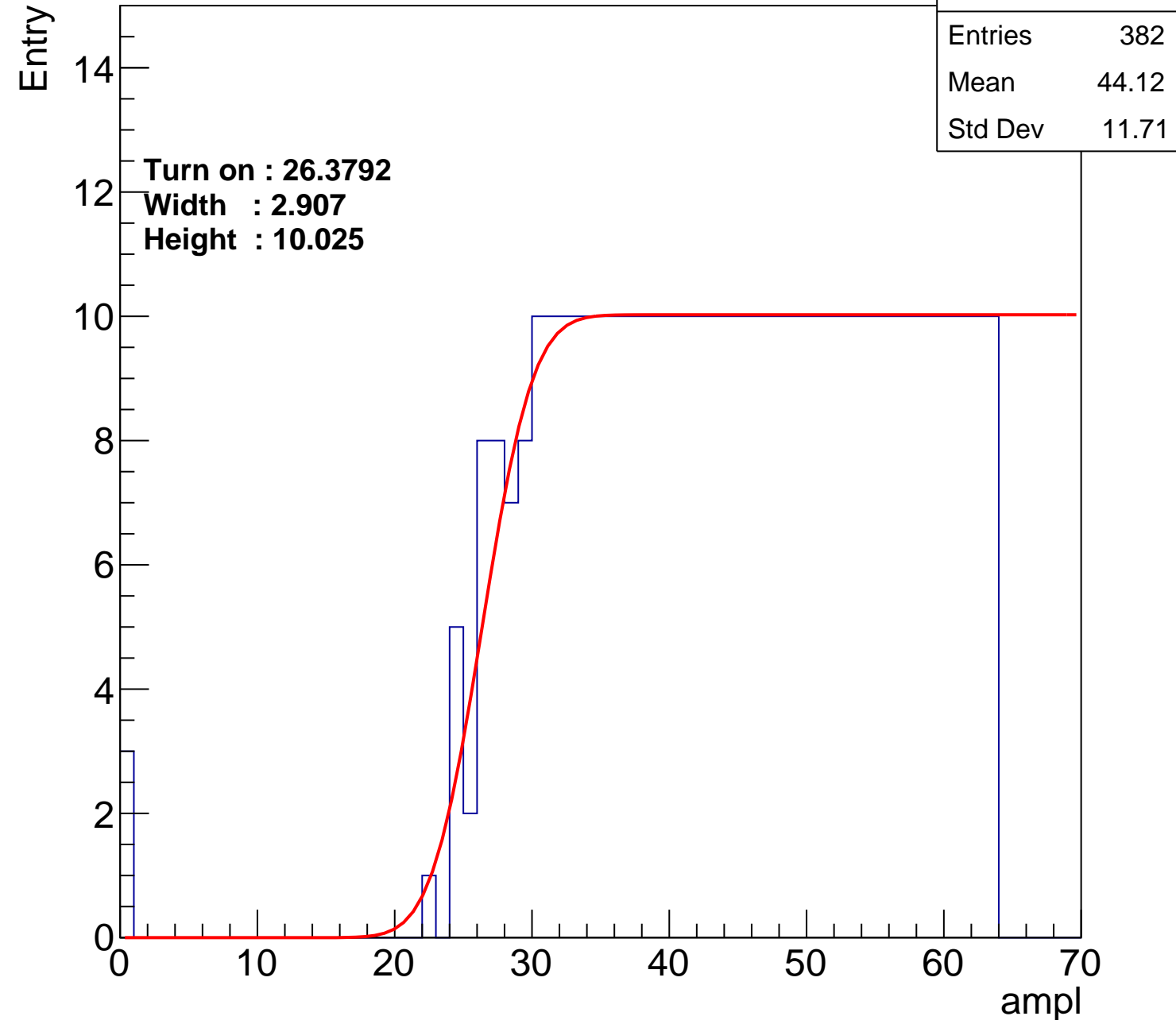
Width : 2.907

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch32

calib_packv5_042523_0143.root, FC#3, port B3

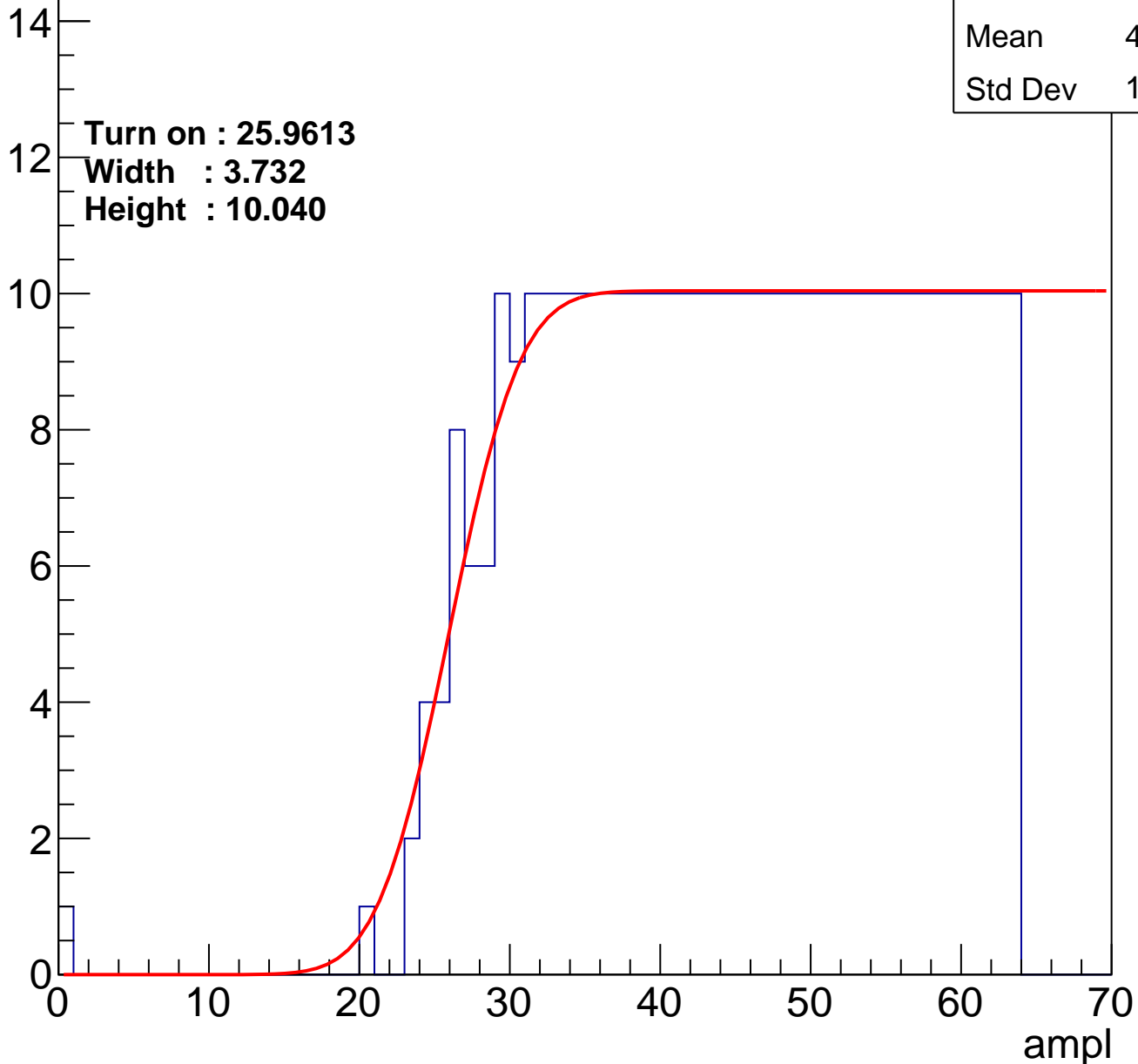
Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 25.9613

Width : 3.732

Height : 10.040

Entry



B1L000S, U25-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.86
Std Dev	11.25

Turn on : 27.8253

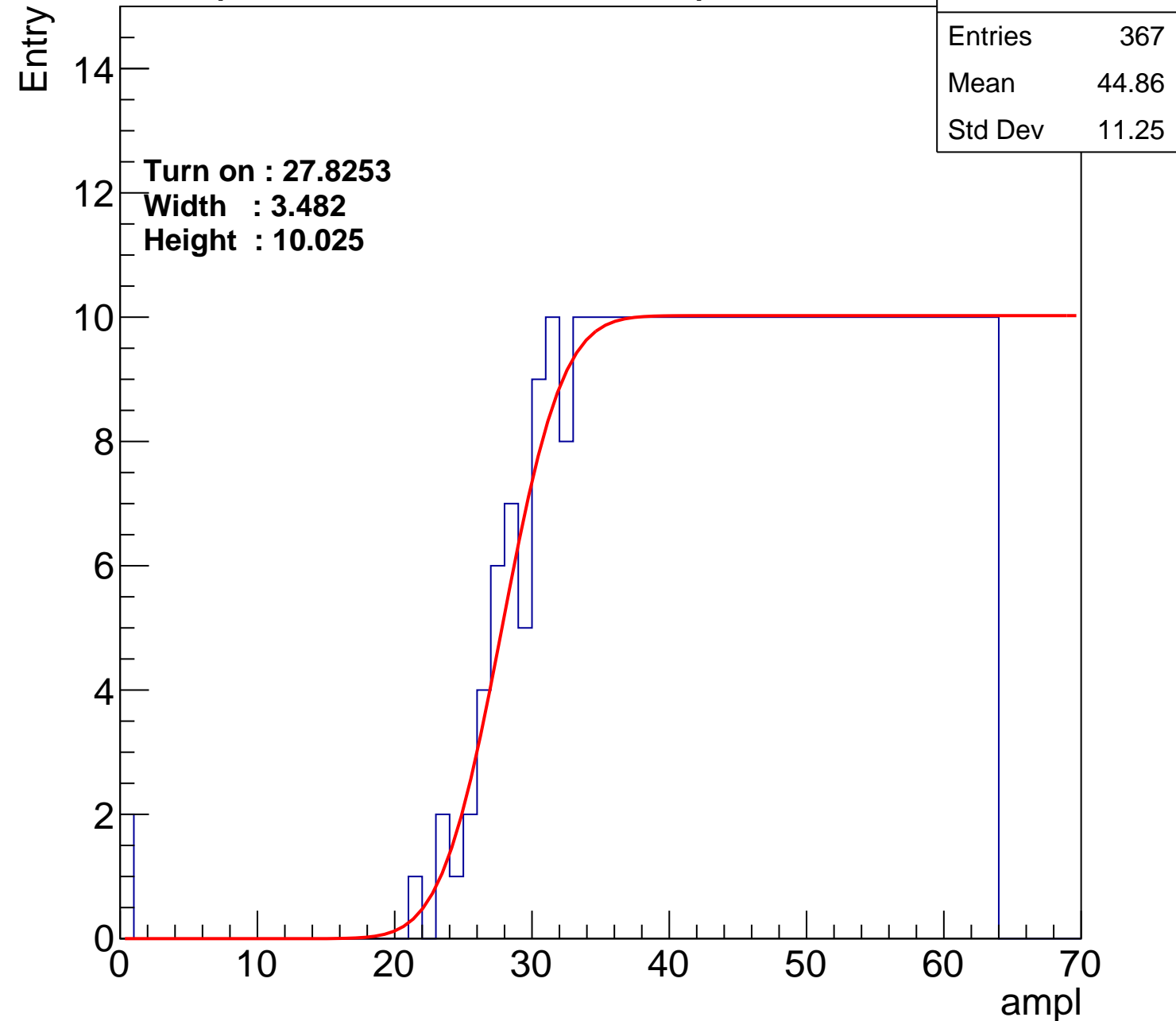
Width : 3.482

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch34

calib_packv5_042523_0143.root, FC#3, port B3

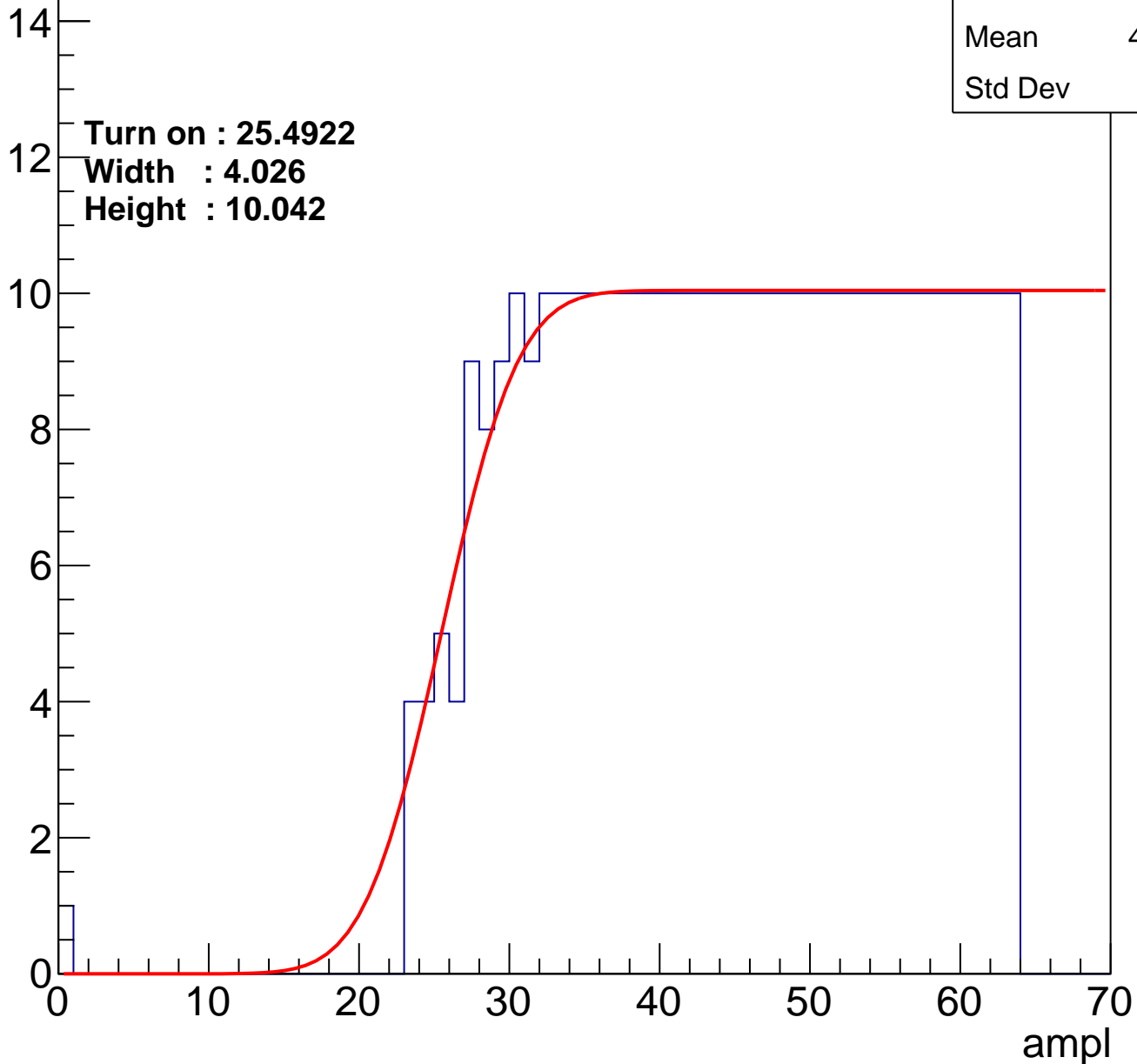
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.4922

Width : 4.026

Height : 10.042

Entry



B1L000S, U25-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.21
Std Dev	11.98

Turn on : 27.0779

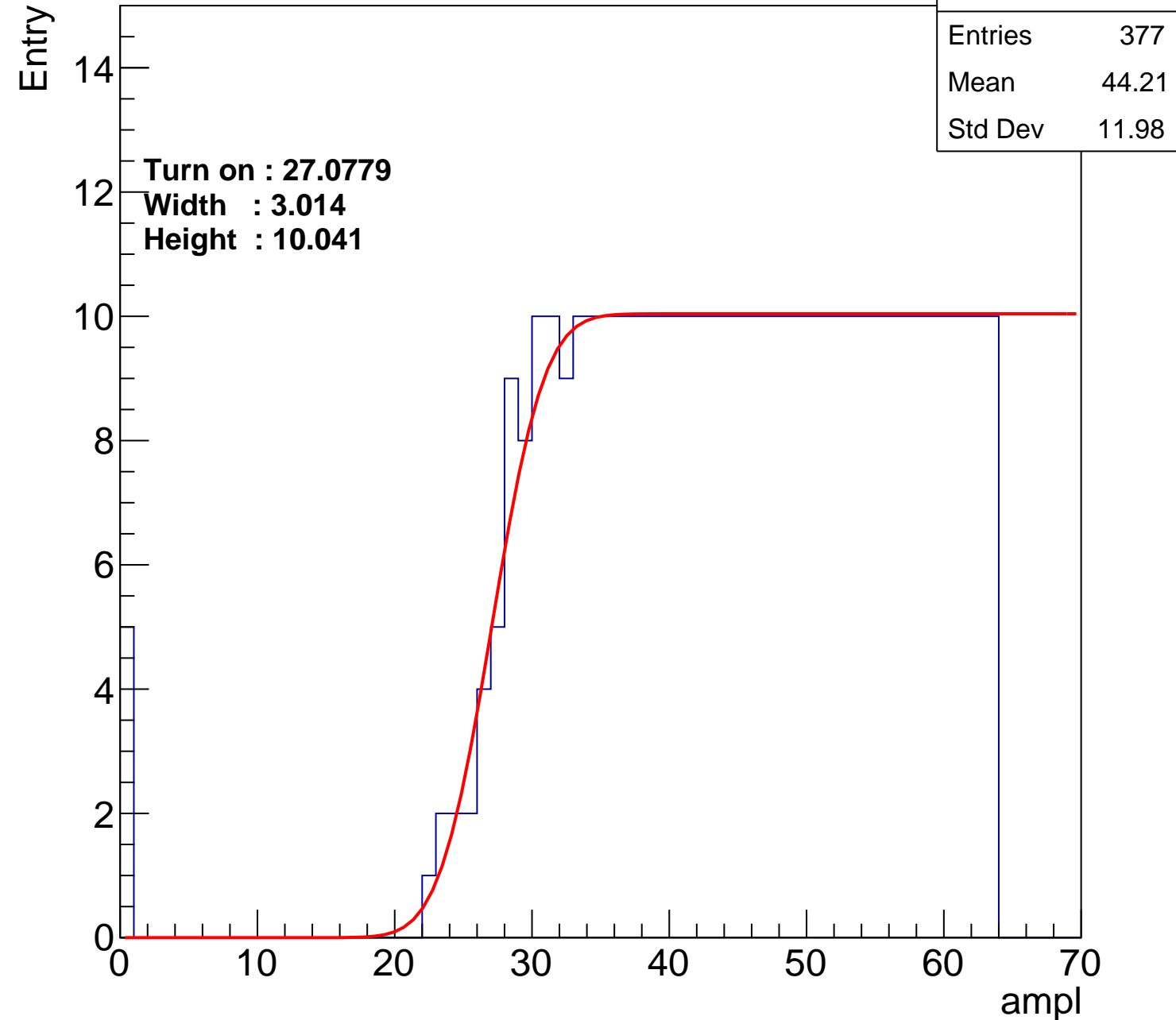
Width : 3.014

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch36

calib_packv5_042523_0143.root, FC#3, port B3

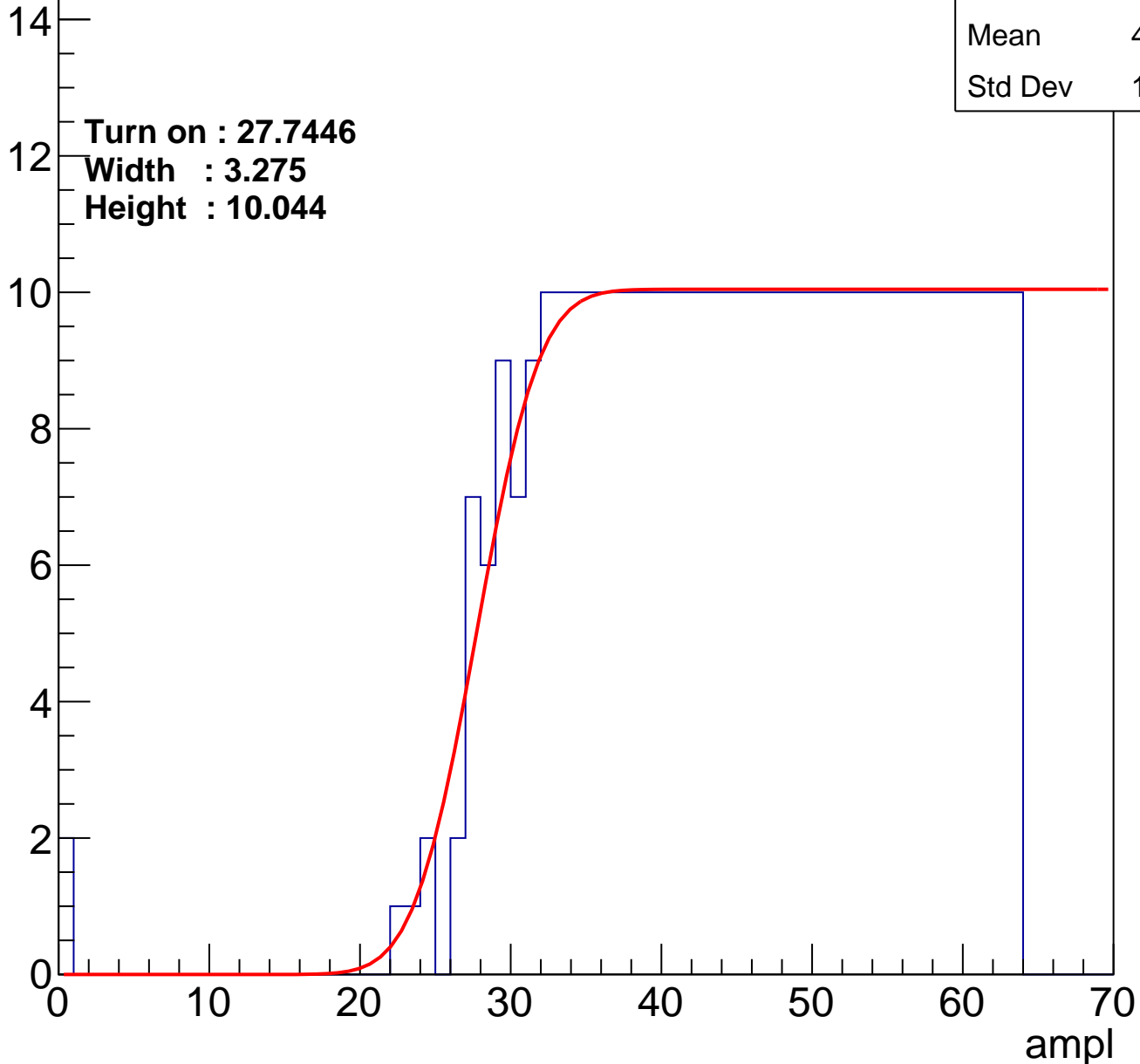
Entries	366
Mean	44.95
Std Dev	11.16

Turn on : 27.7446

Width : 3.275

Height : 10.044

Entry



B1L000S, U25-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.9
Std Dev	11.68

Turn on : 26.0304

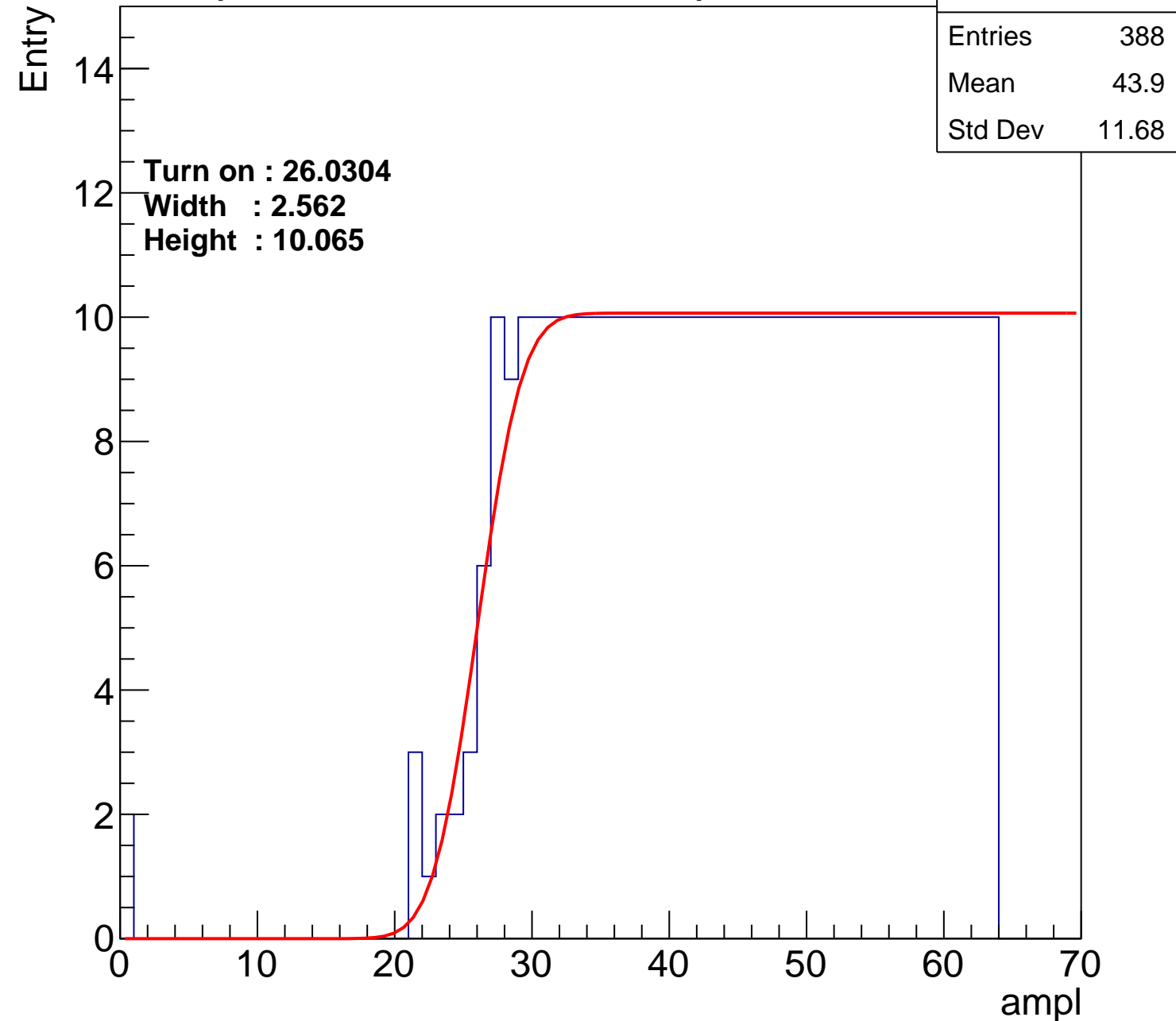
Width : 2.562

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.99
Std Dev	10.96

Turn on : 27.7269

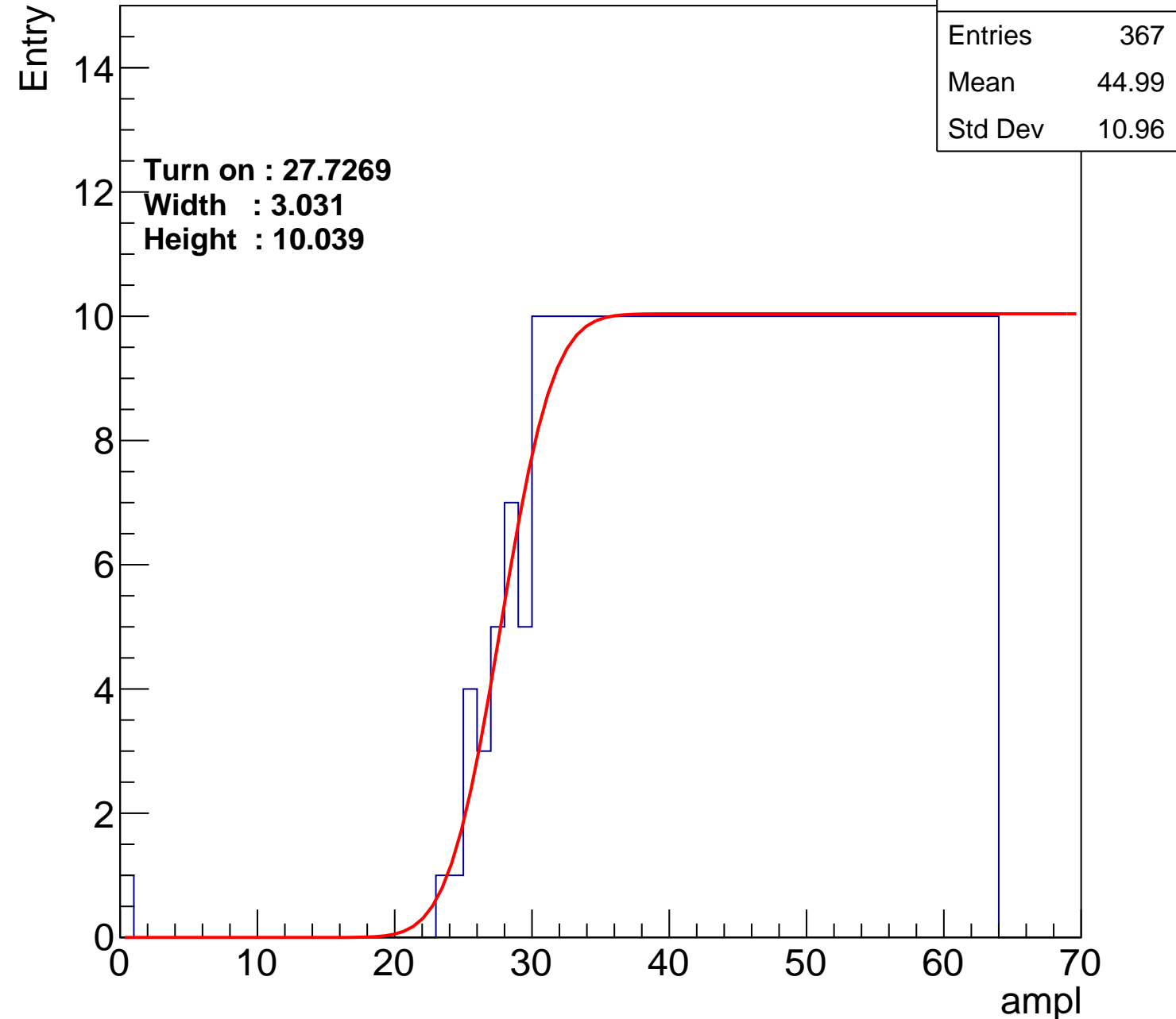
Width : 3.031

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.49
Std Dev	11.9

Turn on : 24.7953

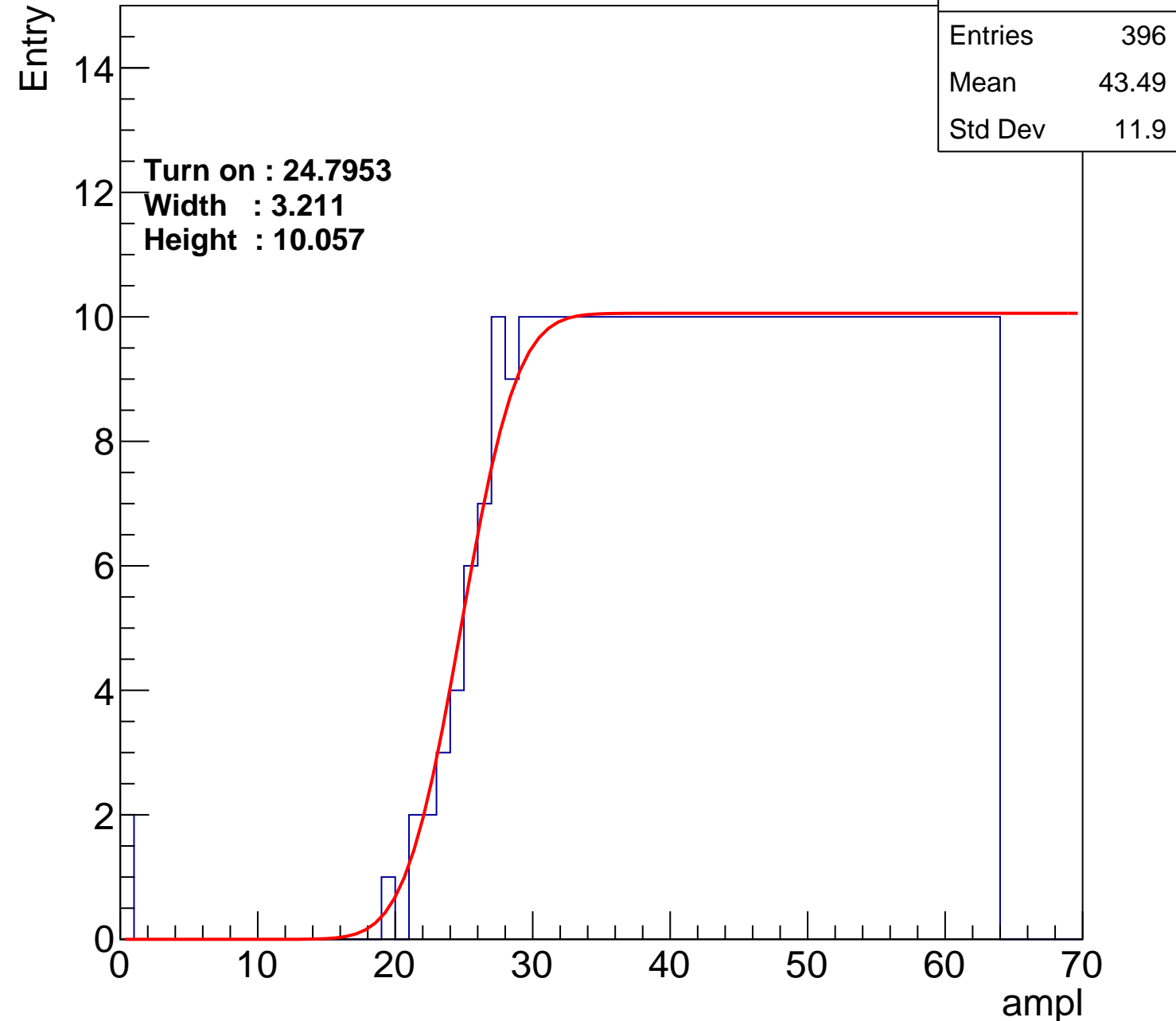
Width : 3.211

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.34
Std Dev	11.3

Turn on : 26.5306

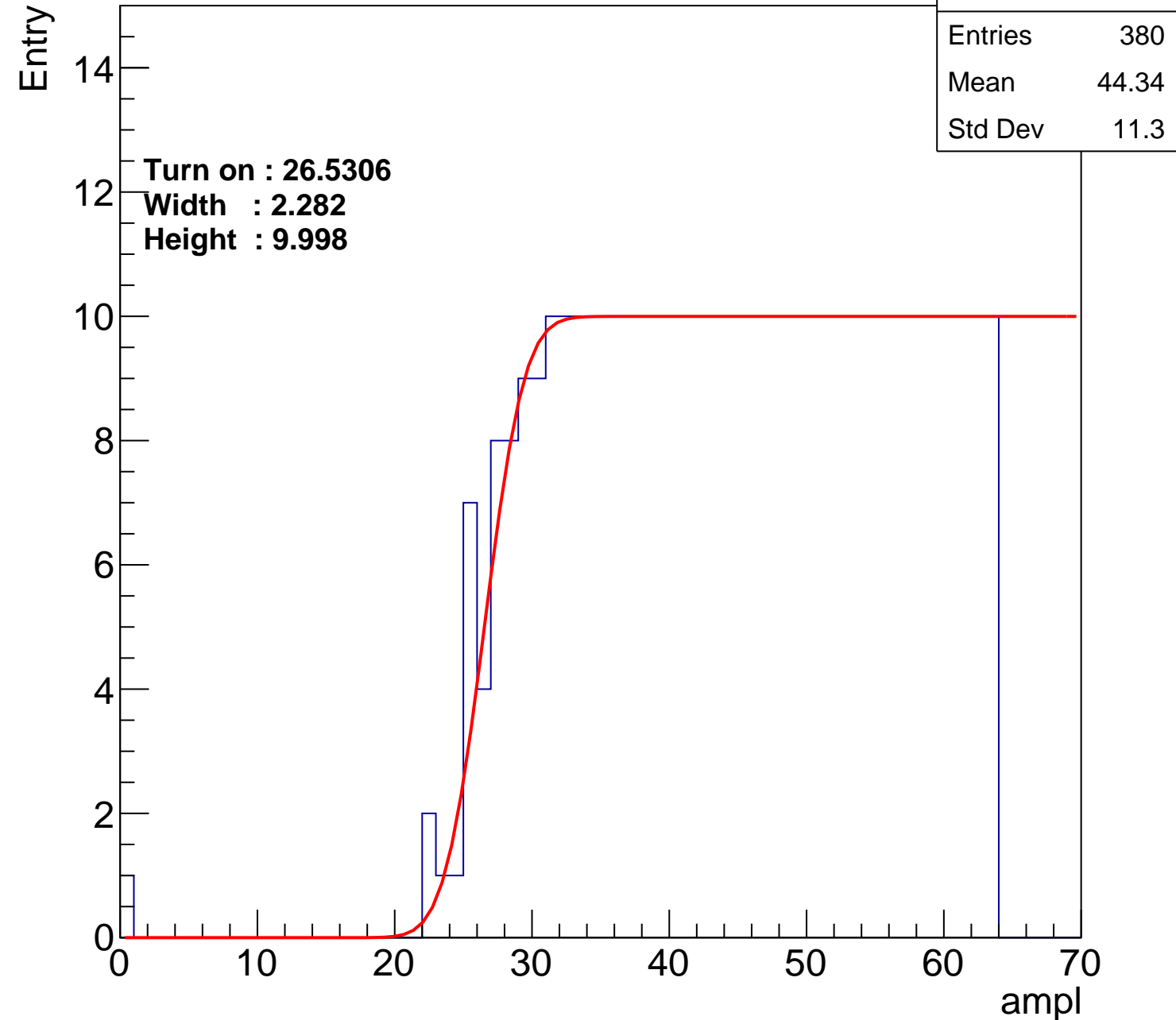
Width : 2.282

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.71
Std Dev	11.94

Turn on : 25.6364

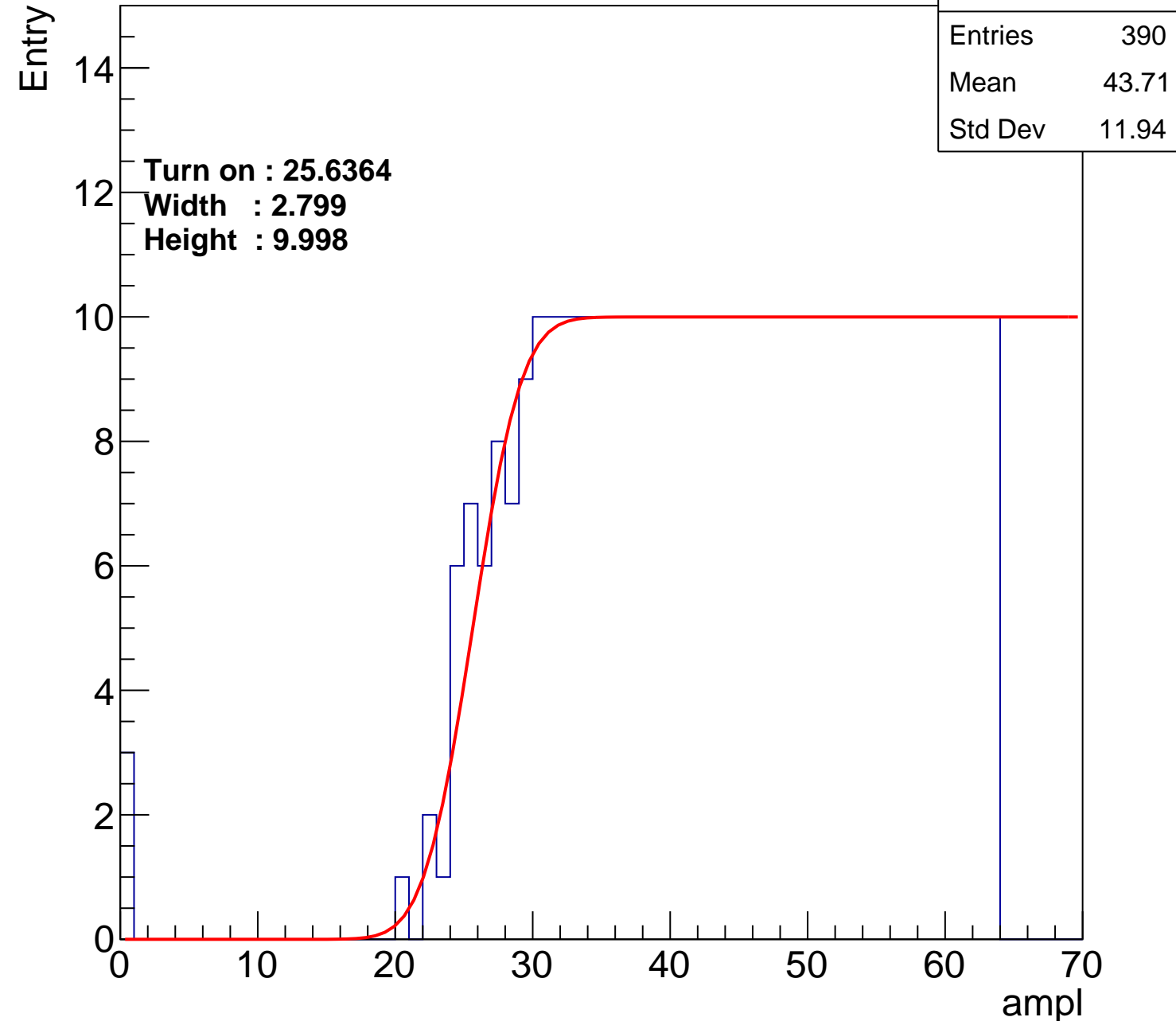
Width : 2.799

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch42

calib_packv5_042523_0143.root, FC#3, port B3

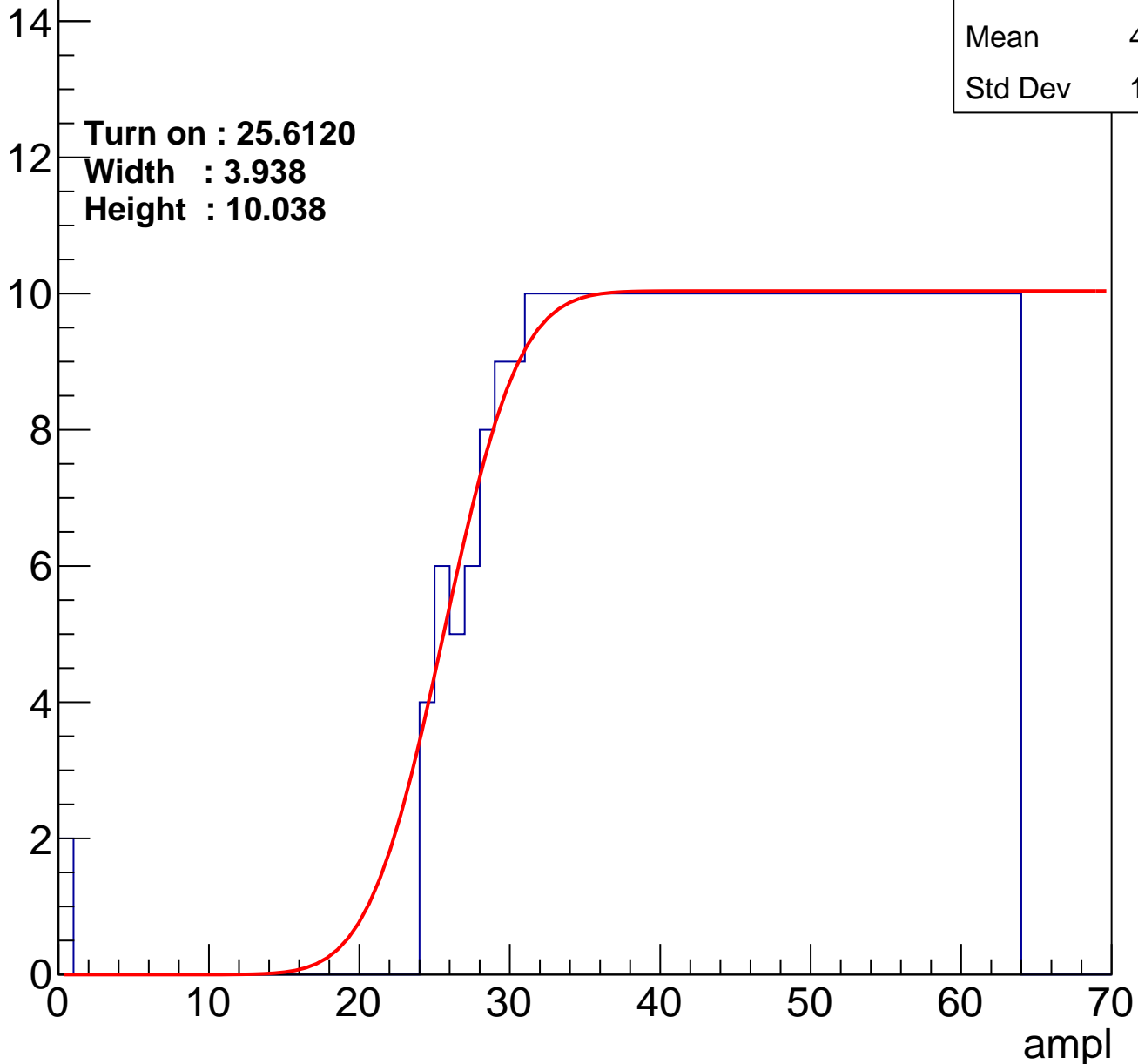
Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 25.6120

Width : 3.938

Height : 10.038

Entry



B1L000S, U25-ch43

calib_packv5_042523_0143.root, FC#3, port B3

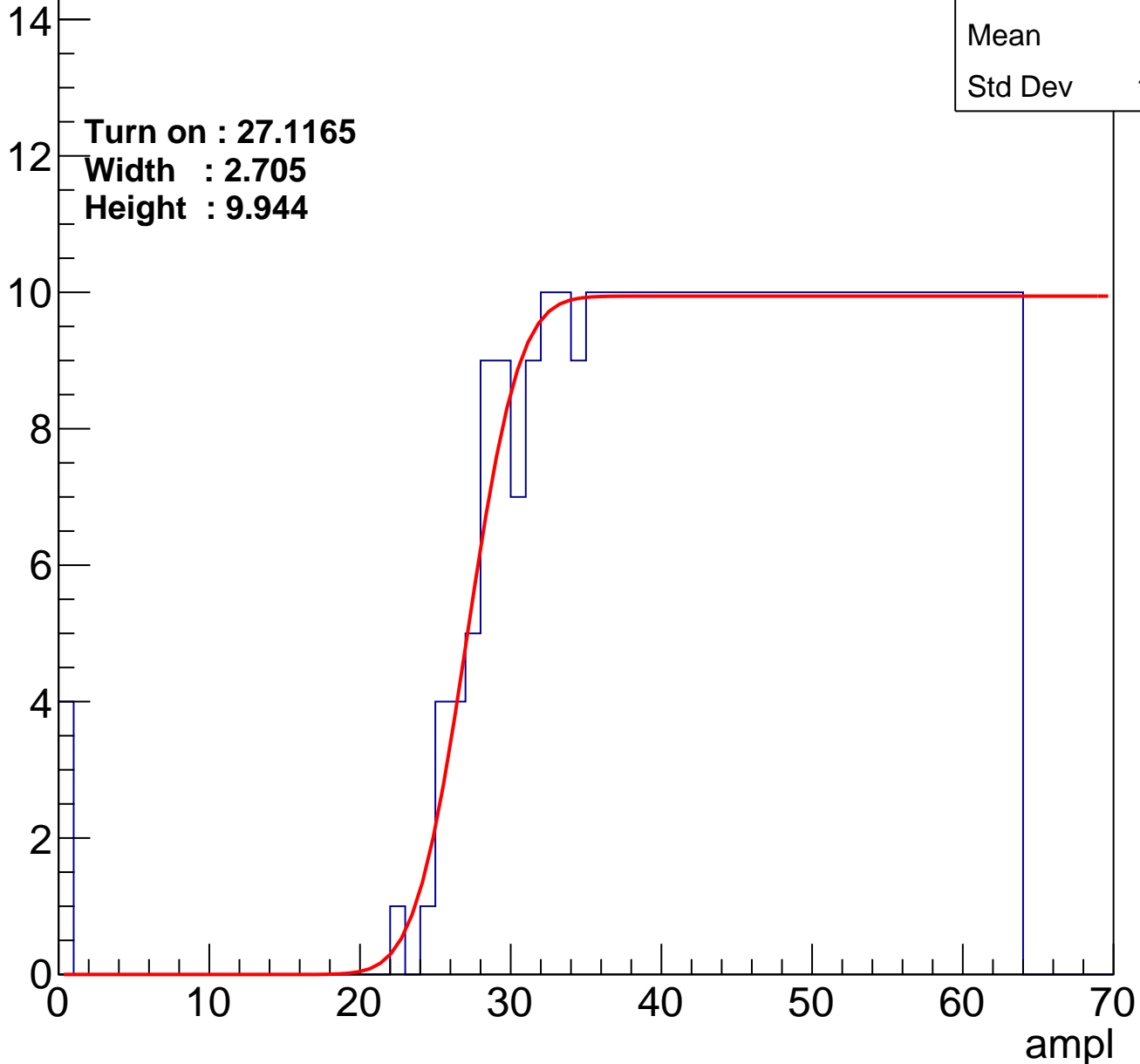
Entries	372
Mean	44.5
Std Dev	11.71

Turn on : 27.1165

Width : 2.705

Height : 9.944

Entry



B1L000S, U25-ch44

calib_packv5_042523_0143.root, FC#3, port B3

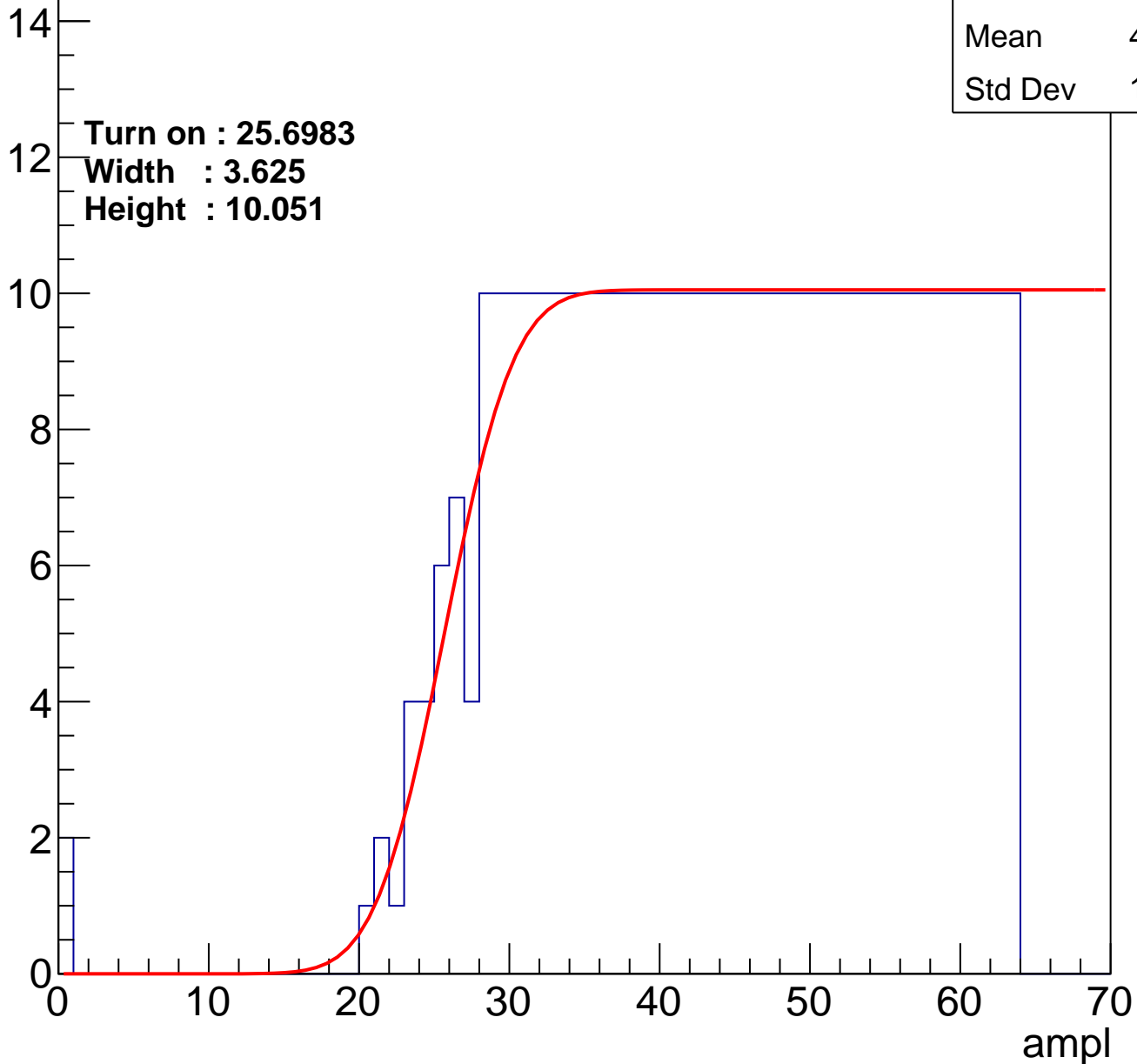
Entries	391
Mean	43.71
Std Dev	11.81

Turn on : 25.6983

Width : 3.625

Height : 10.051

Entry



B1L000S, U25-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.84
Std Dev	11.95

Turn on : 25.1728

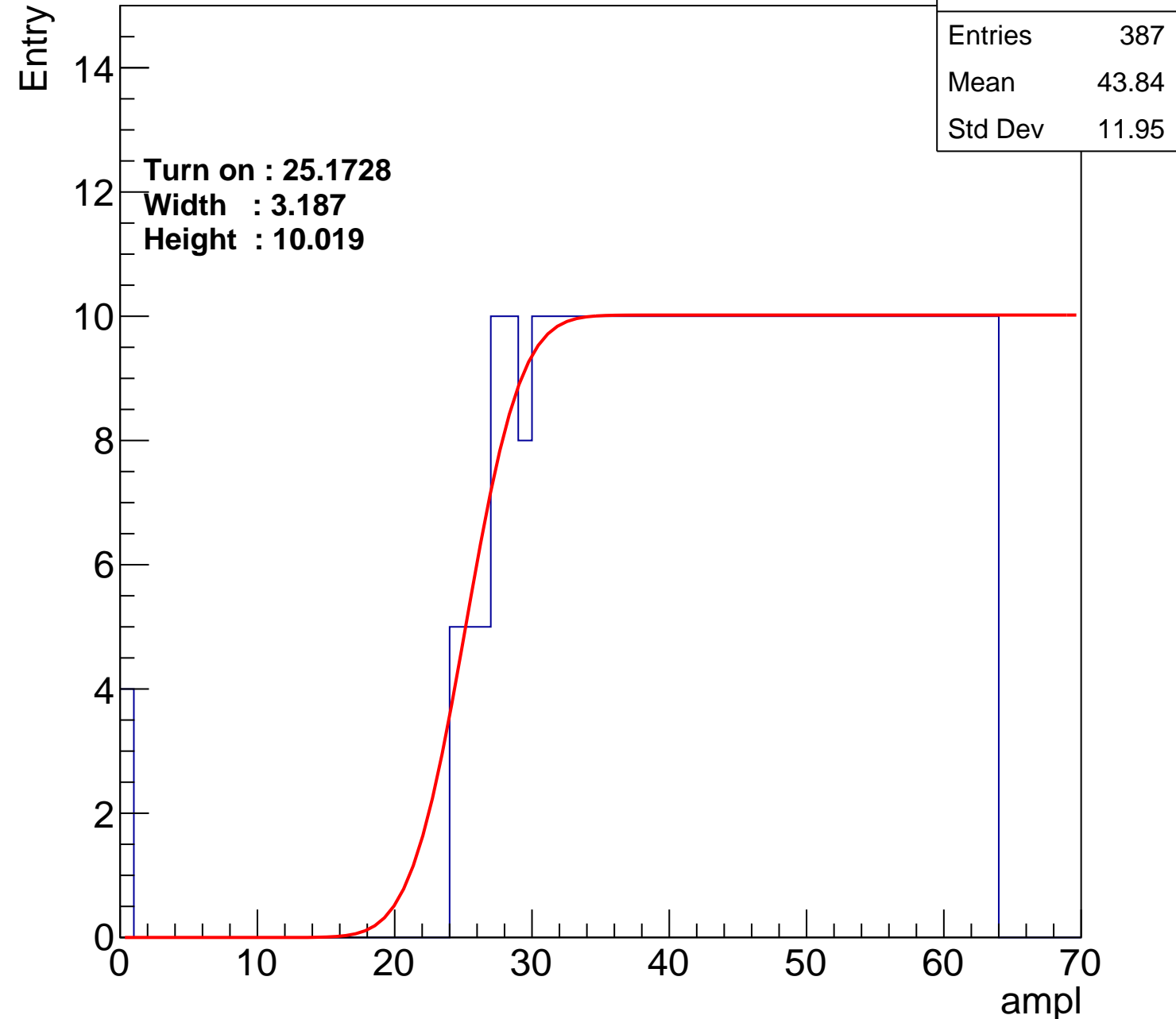
Width : 3.187

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.41
Std Dev	10.9

Turn on : 28.6615

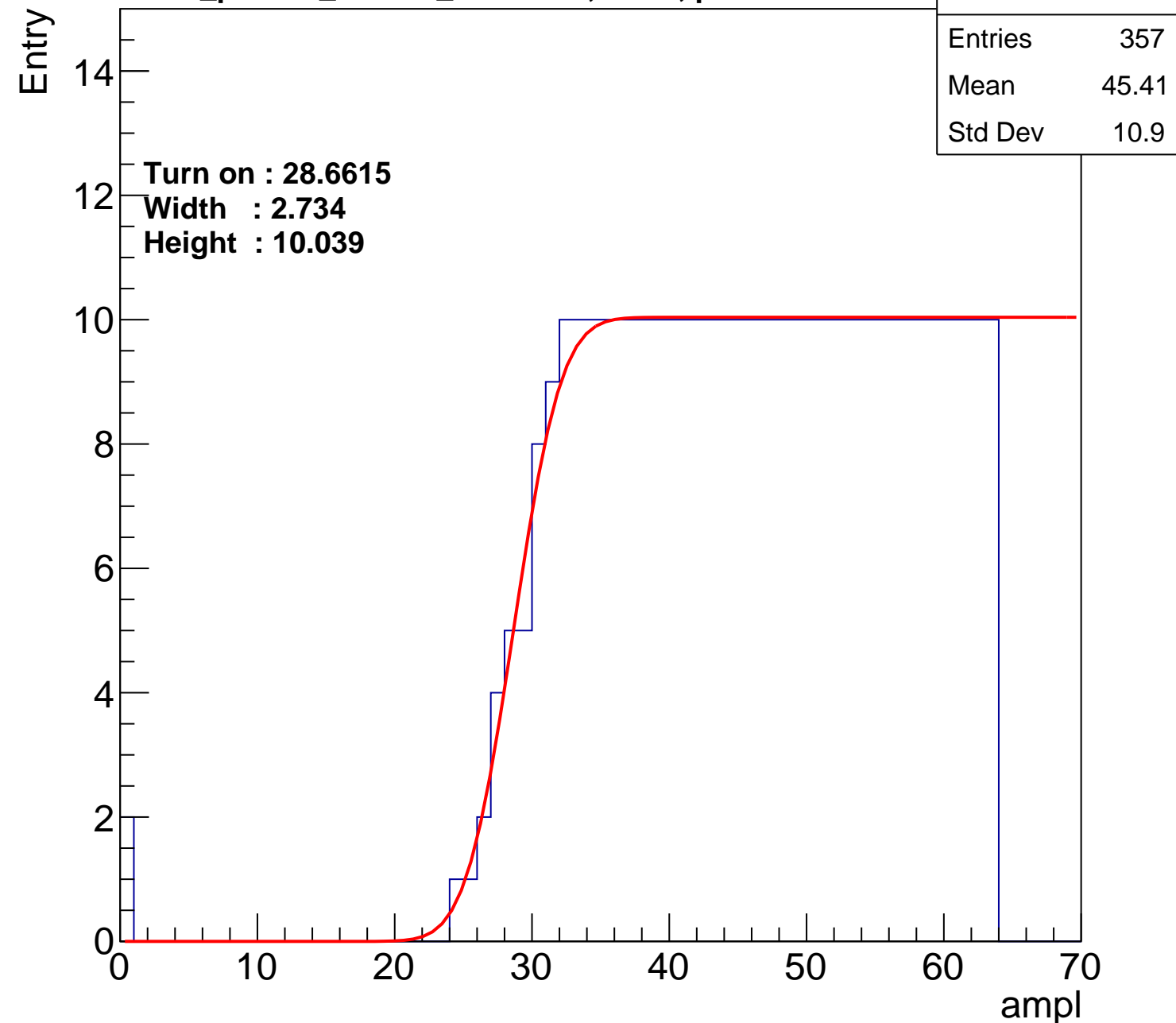
Width : 2.734

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch47

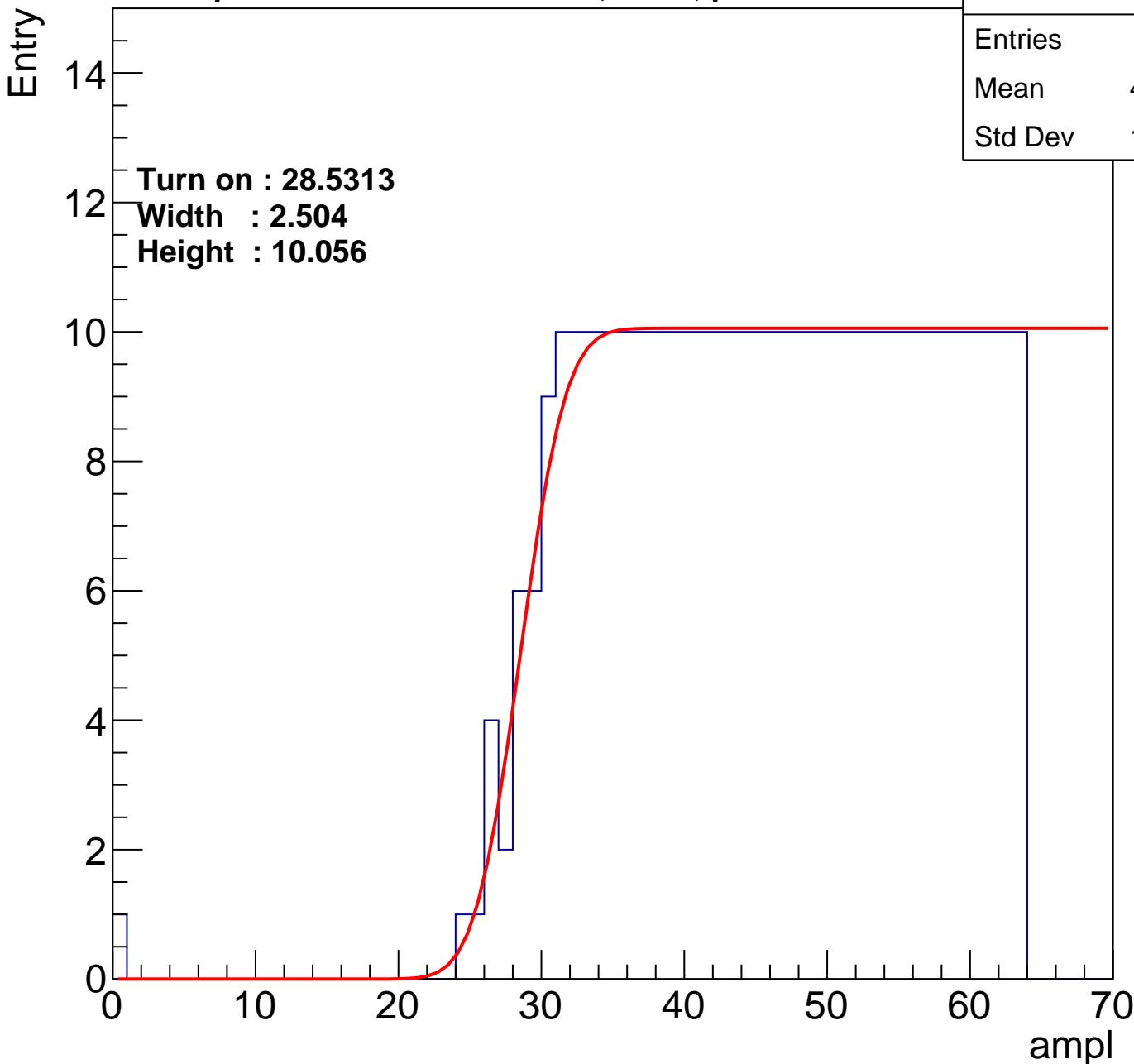
calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.36
Std Dev	10.73

Turn on : 28.5313

Width : 2.504

Height : 10.056



B1L000S, U25-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	352
Mean	45.67
Std Dev	10.76

Turn on : 29.2588

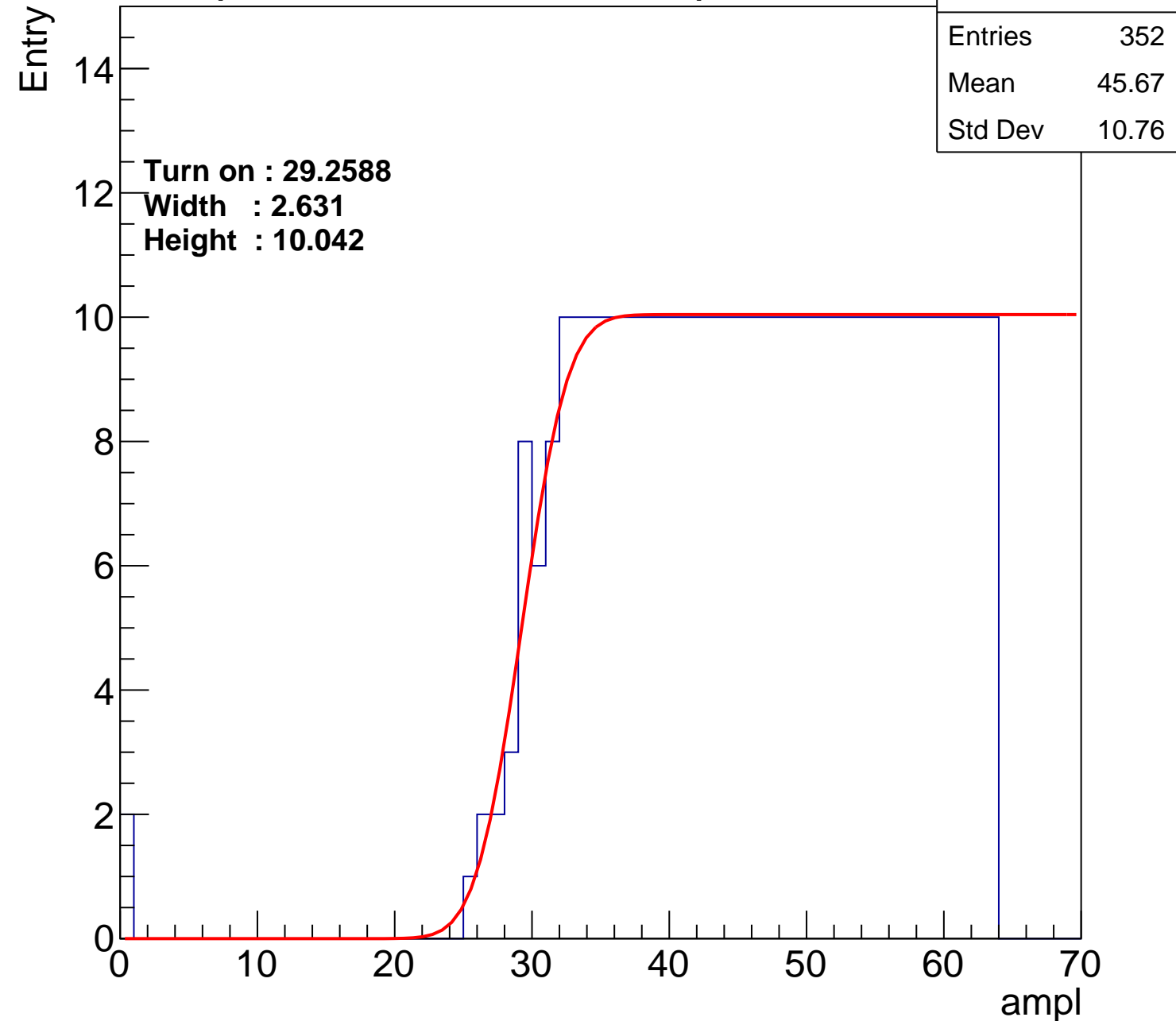
Width : 2.631

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch49

calib_packv5_042523_0143.root, FC#3, port B3

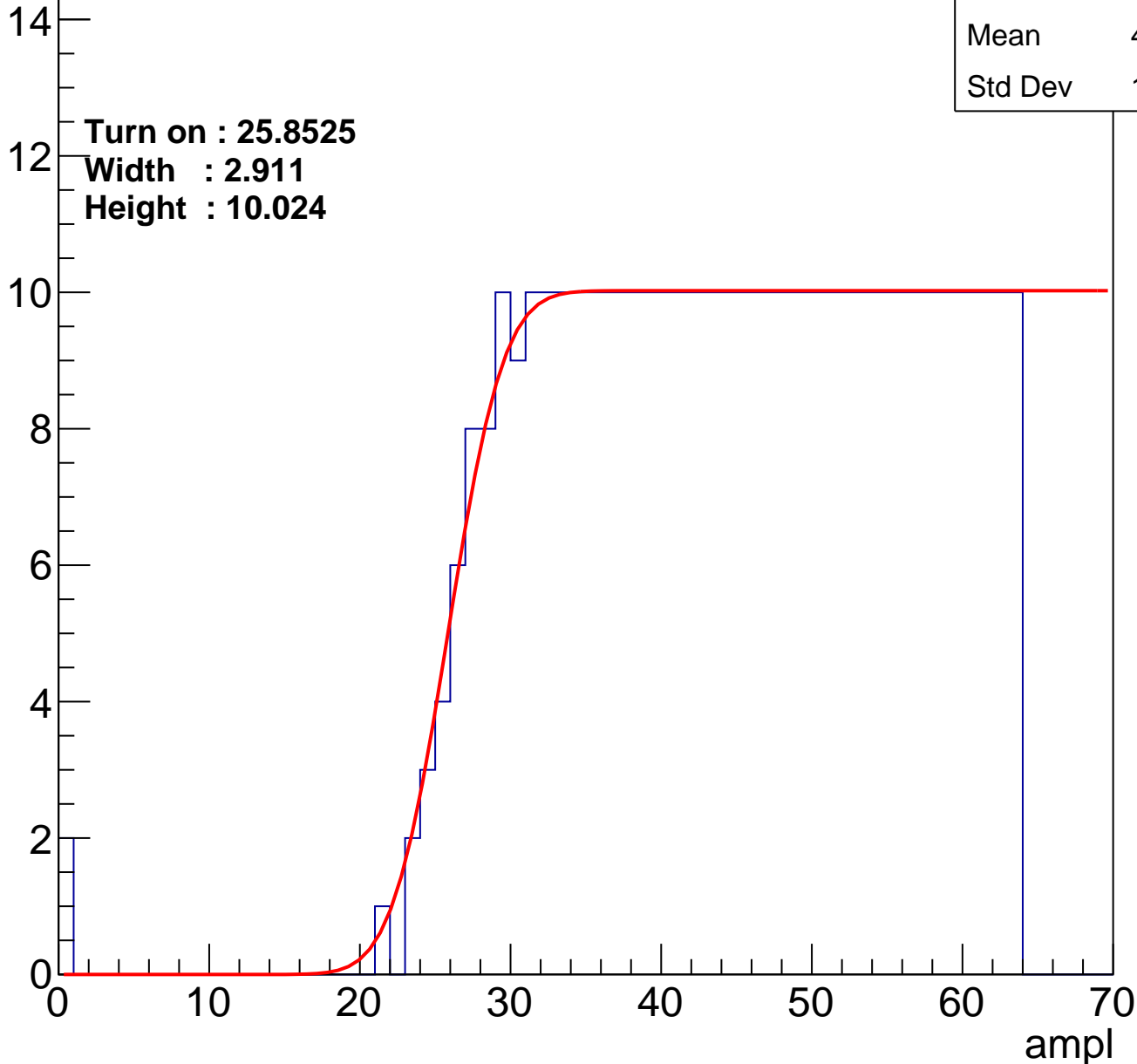
Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 25.8525

Width : 2.911

Height : 10.024

Entry



B1L000S, U25-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.19
Std Dev	11.65

Turn on : 26.0628

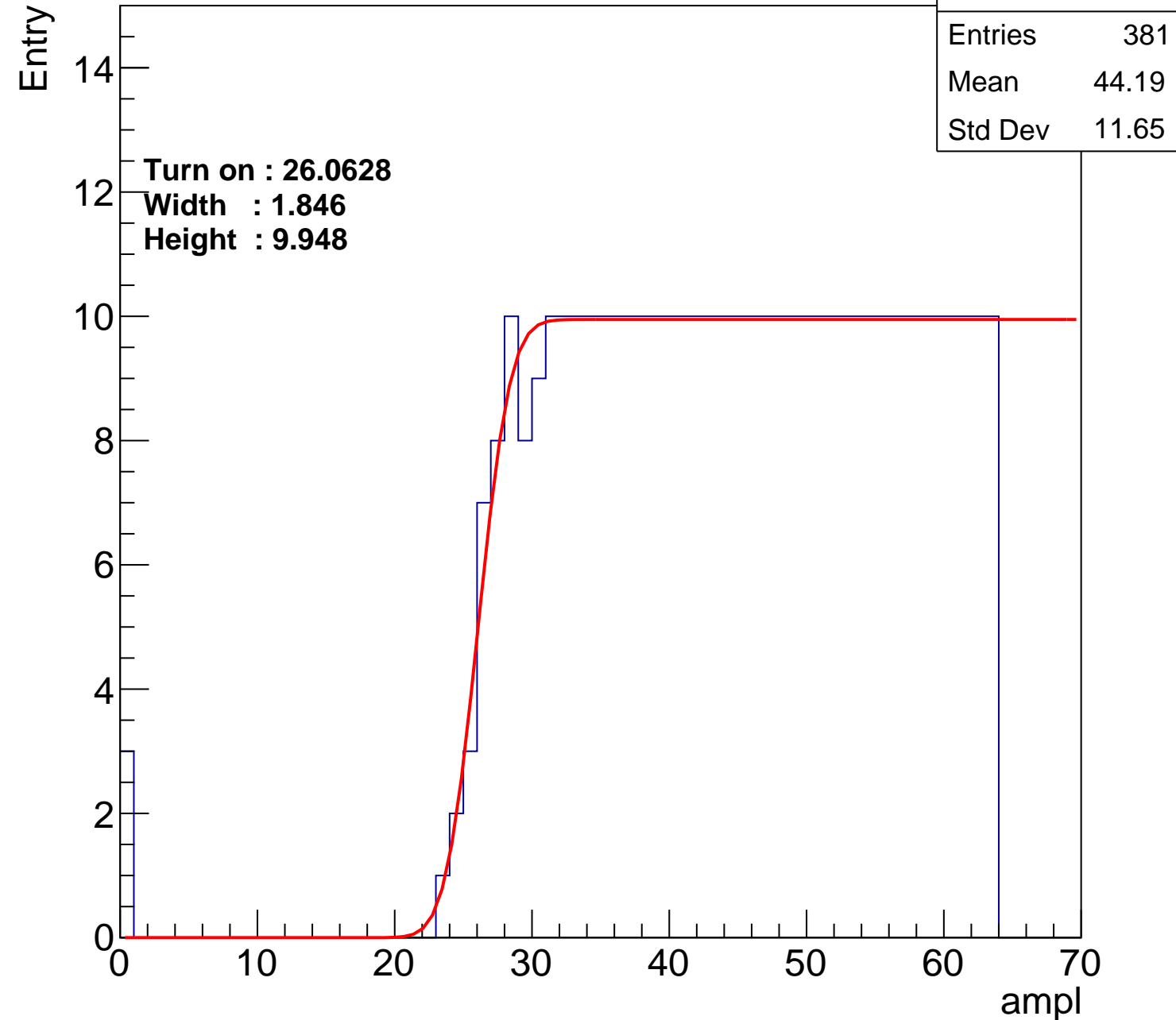
Width : 1.846

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.28
Std Dev	11.61

Turn on : 26.7005

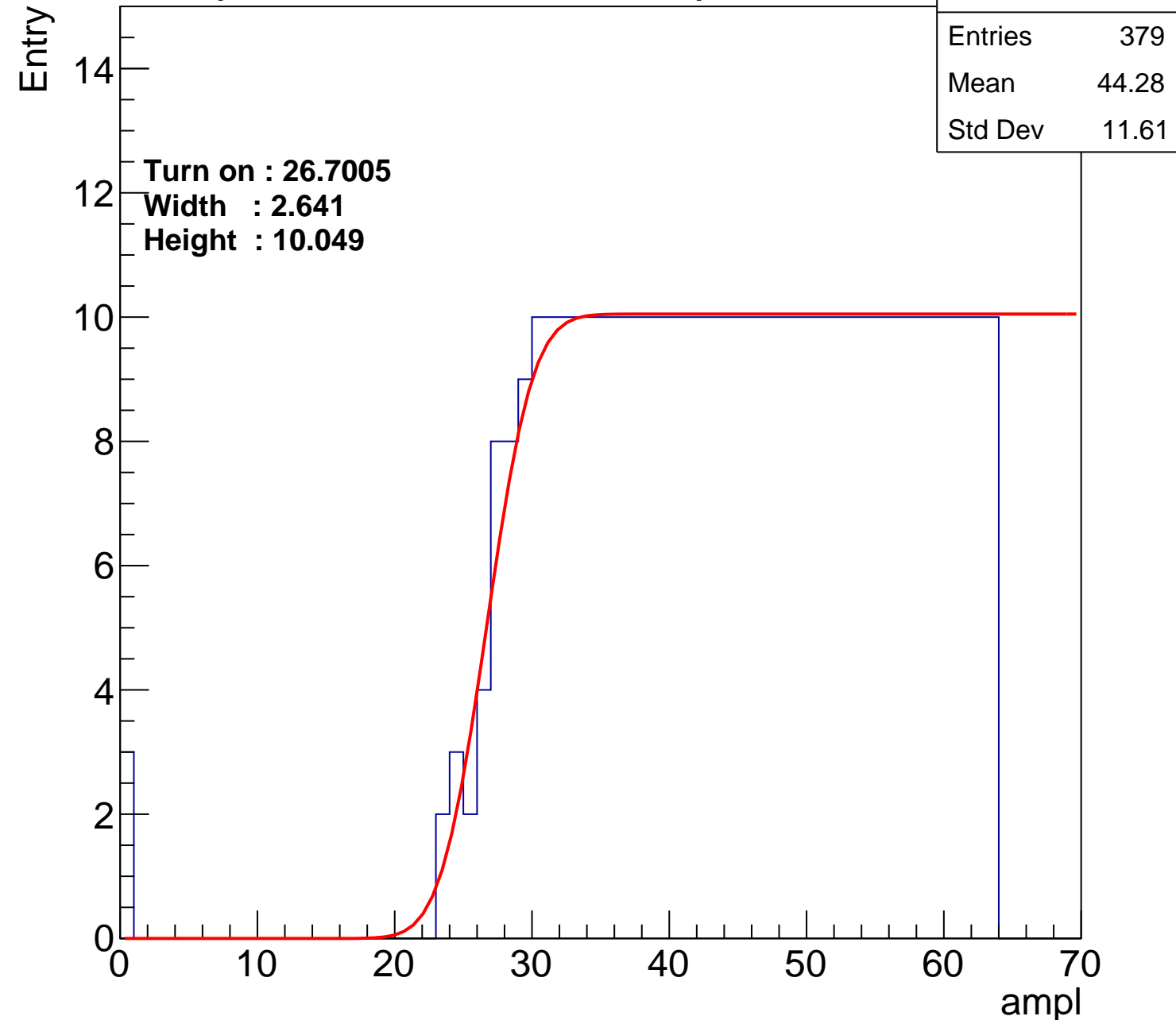
Width : 2.641

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch52

calib_packv5_042523_0143.root, FC#3, port B3

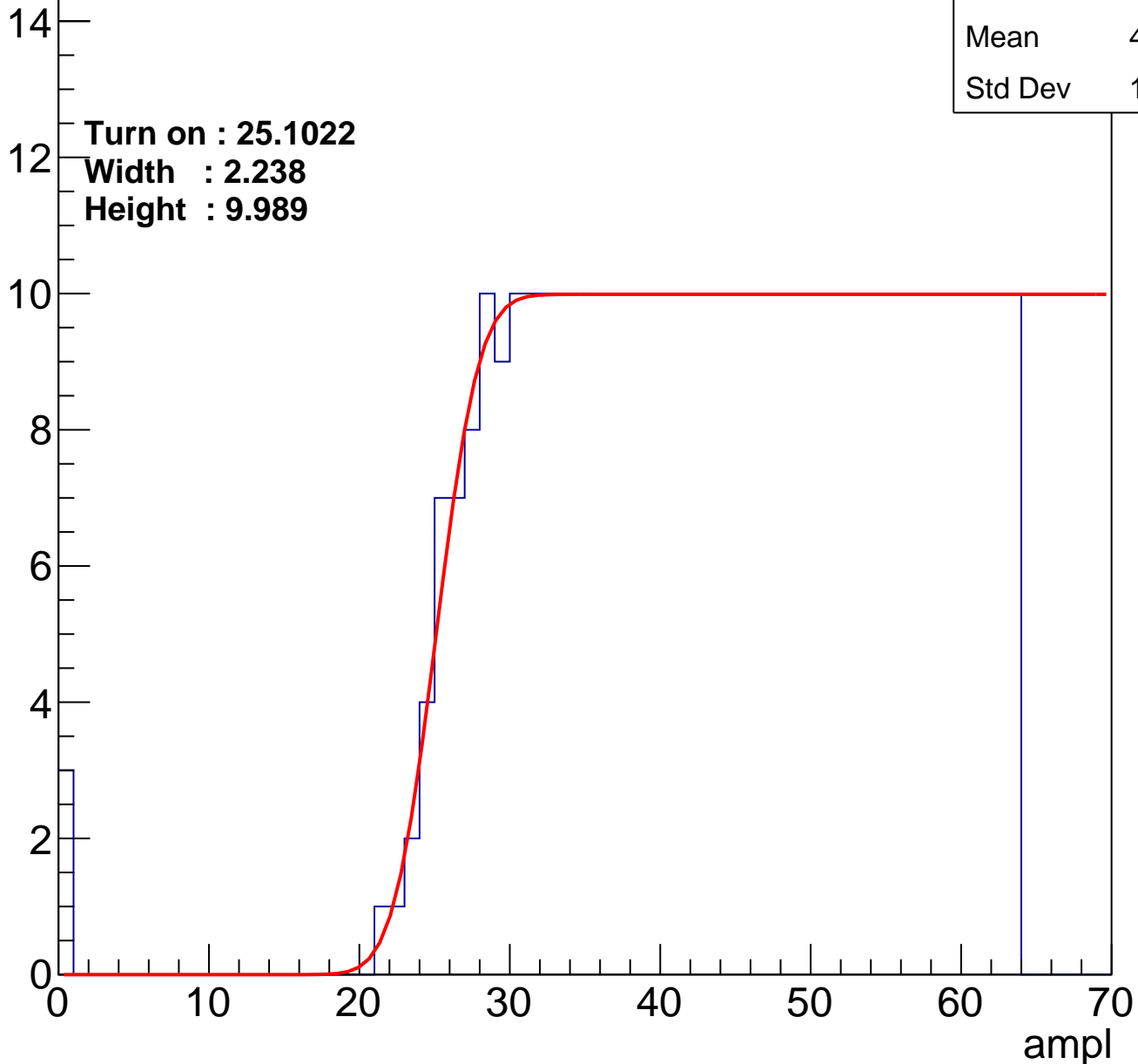
Entries	392
Mean	43.65
Std Dev	11.93

Turn on : 25.1022

Width : 2.238

Height : 9.989

Entry



B1L000S, U25-ch53

calib_packv5_042523_0143.root, FC#3, port B3

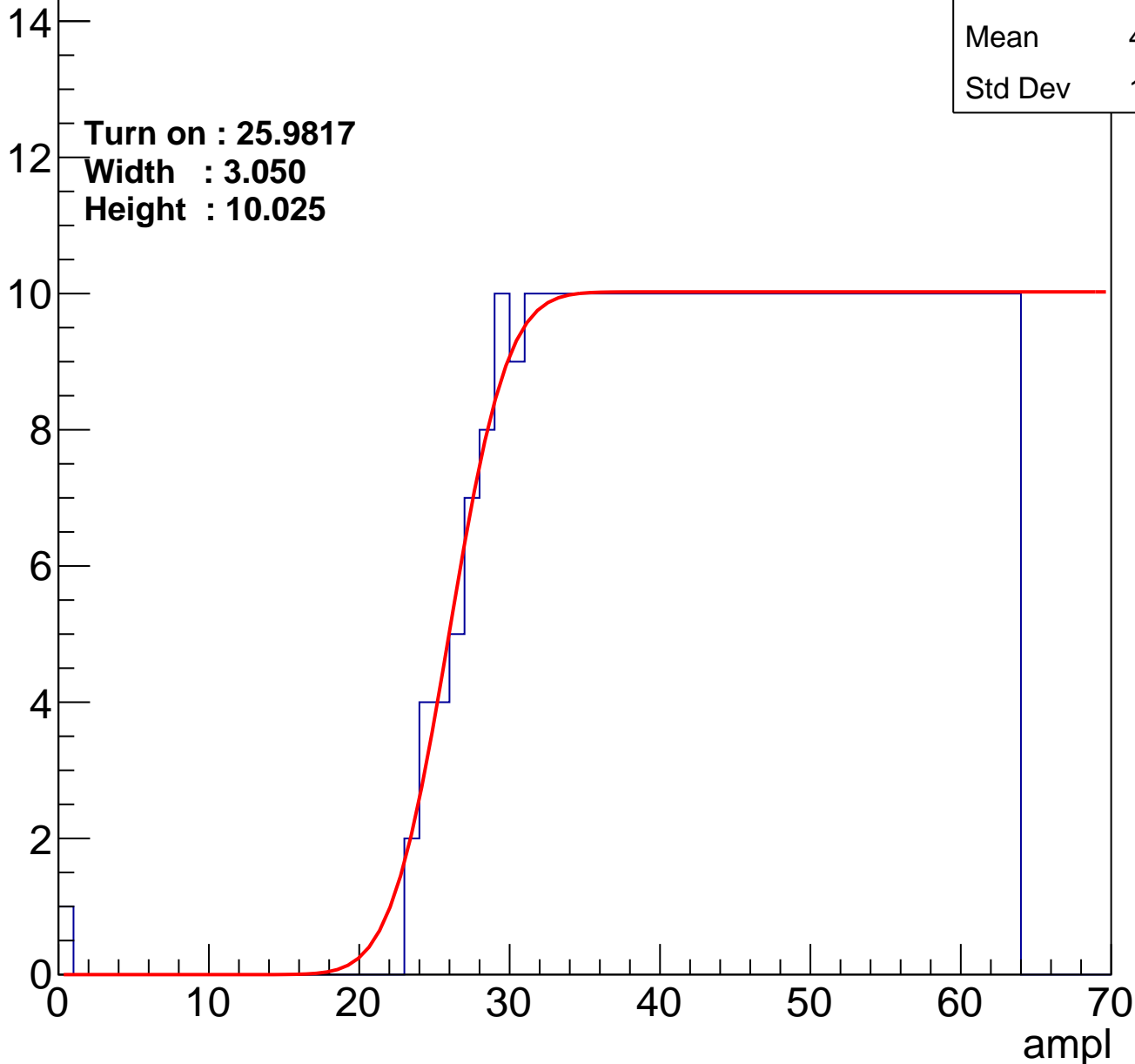
Entries	380
Mean	44.36
Std Dev	11.29

Turn on : 25.9817

Width : 3.050

Height : 10.025

Entry



B1L000S, U25-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.45
Std Dev	11.39

Turn on : 26.9493

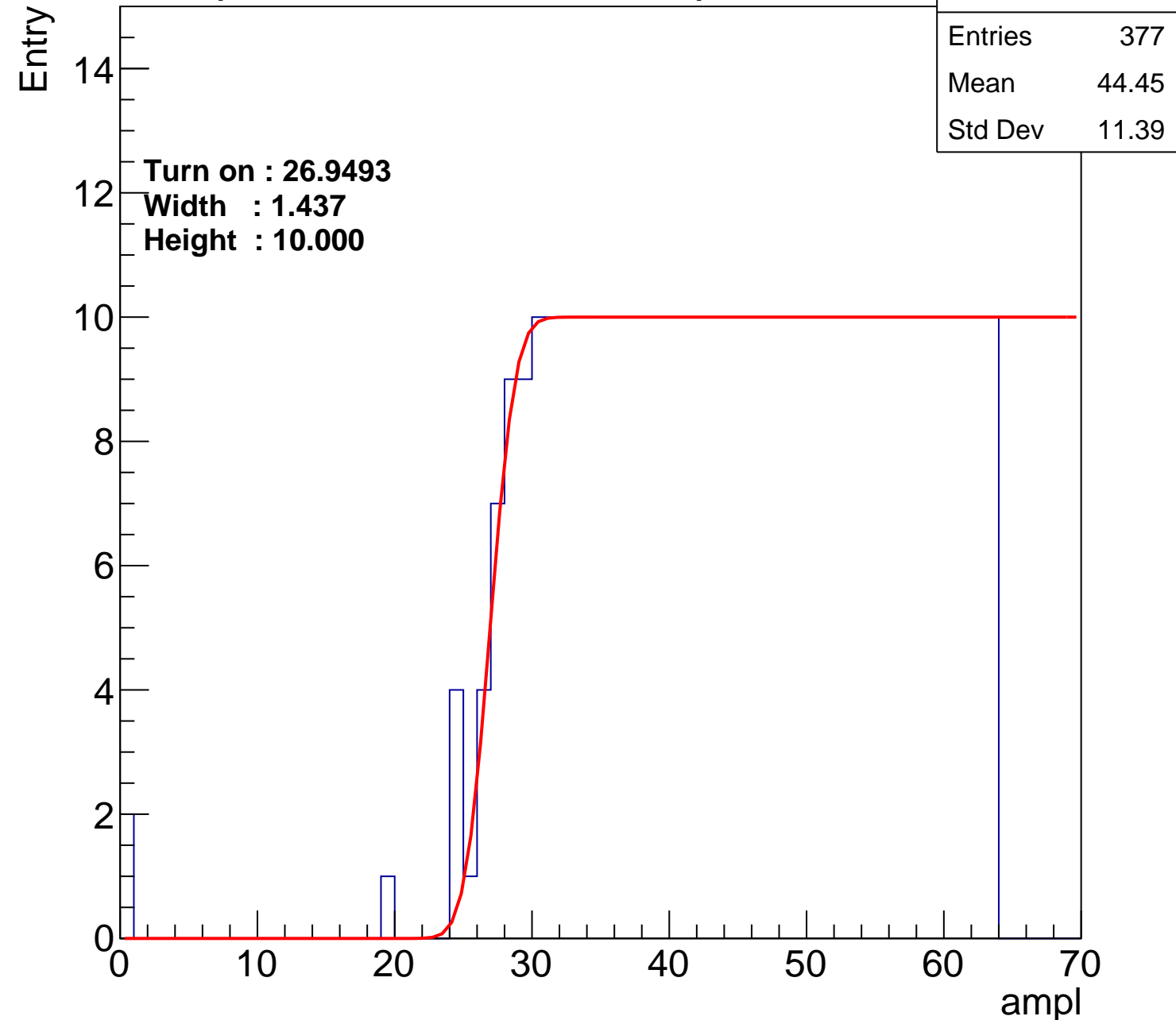
Width : 1.437

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch55

calib_packv5_042523_0143.root, FC#3, port B3

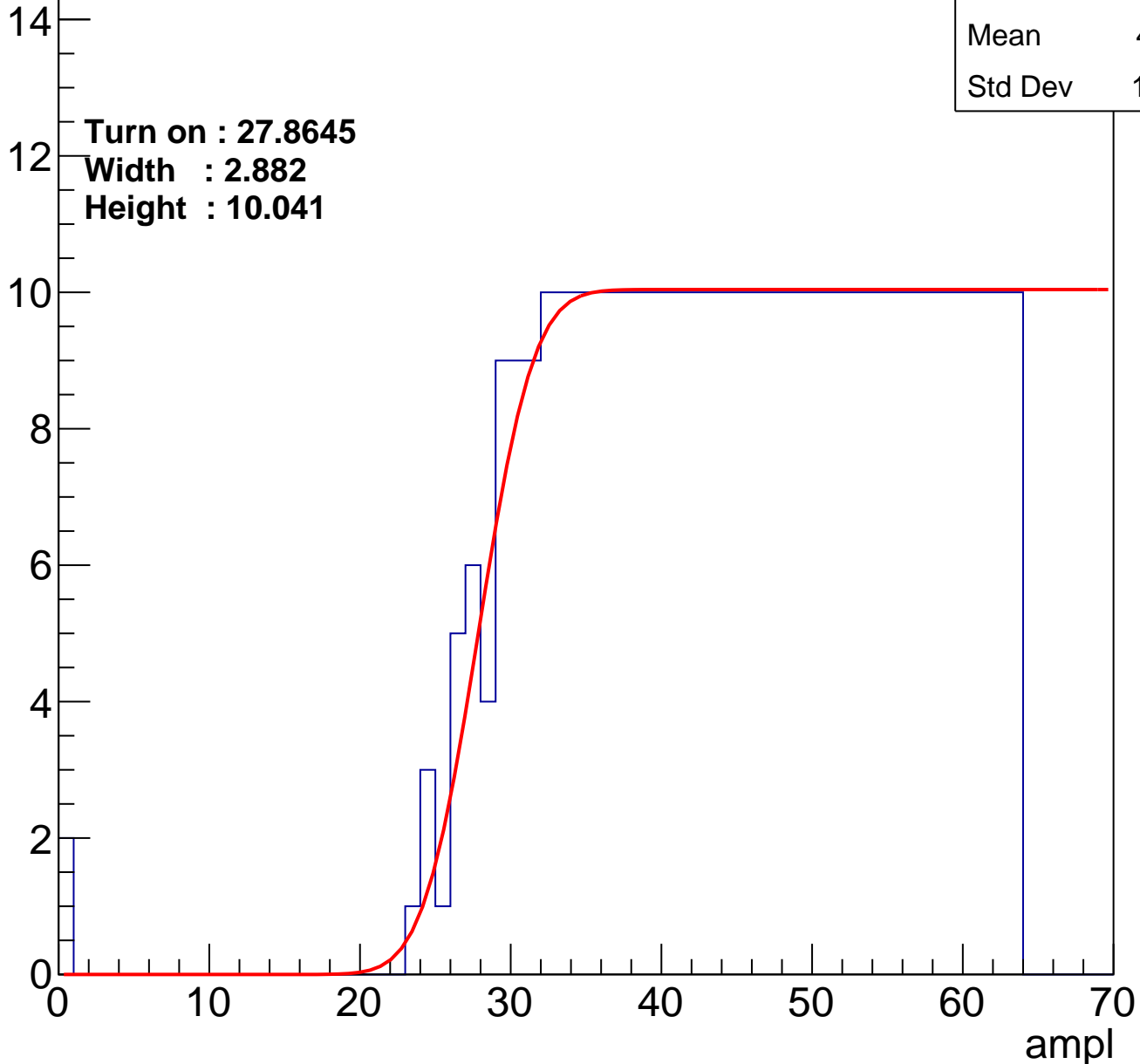
Entries	369
Mean	44.81
Std Dev	11.23

Turn on : 27.8645

Width : 2.882

Height : 10.041

Entry



B1L000S, U25-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 25.2709

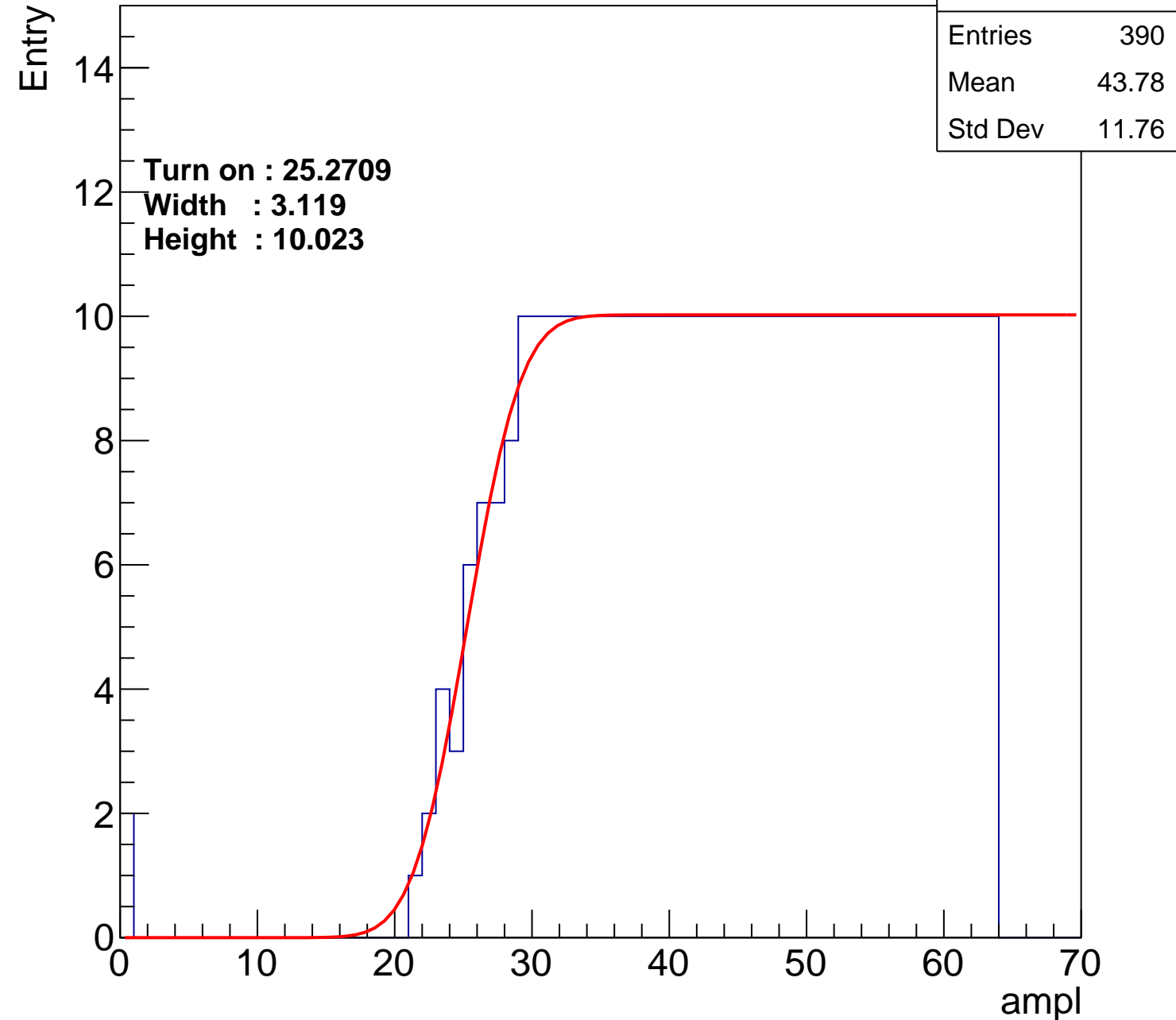
Width : 3.119

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch57

calib_packv5_042523_0143.root, FC#3, port B3

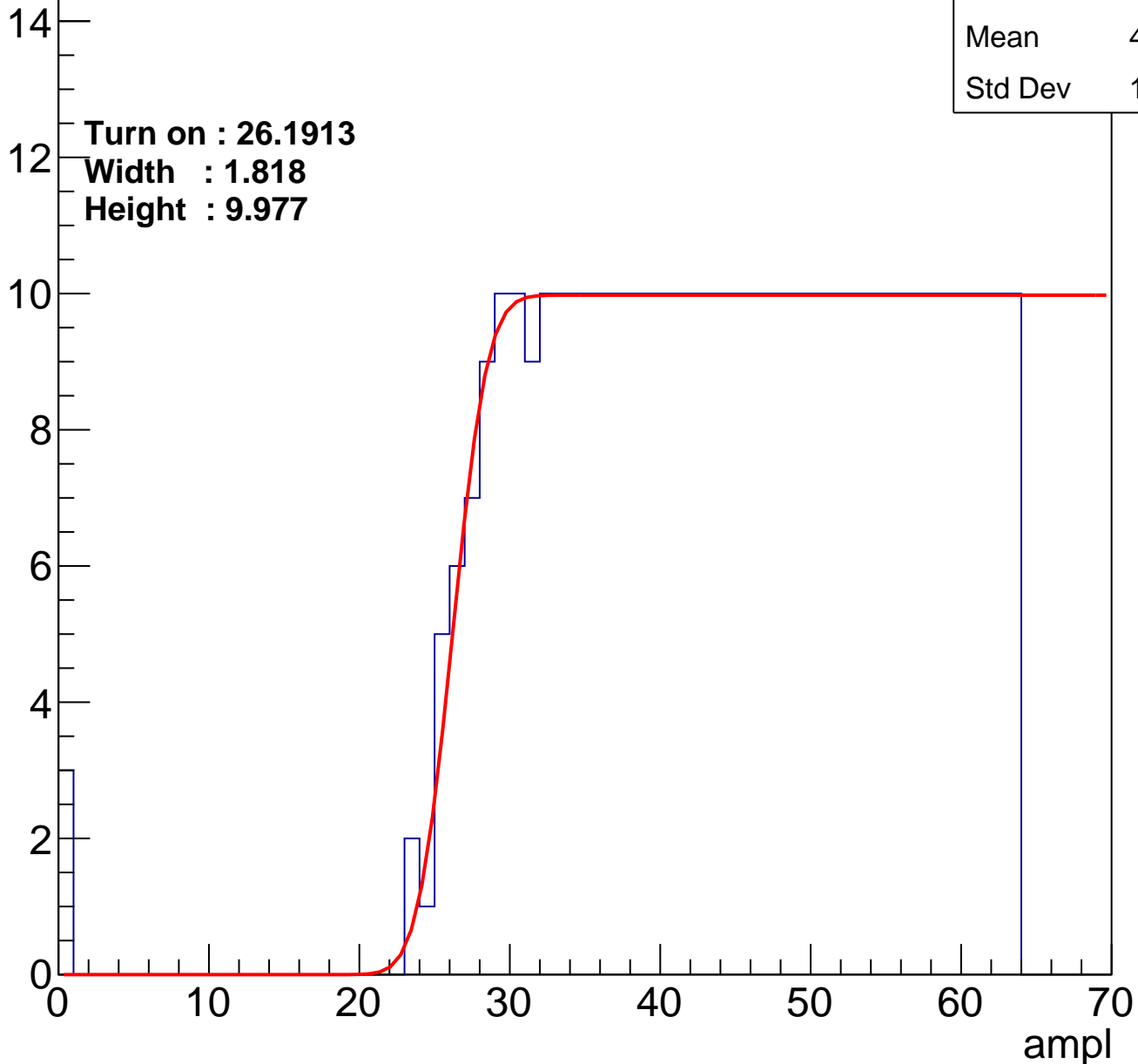
Entries	382
Mean	44.14
Std Dev	11.68

Turn on : 26.1913

Width : 1.818

Height : 9.977

Entry



B1L000S, U25-ch58

calib_packv5_042523_0143.root, FC#3, port B3

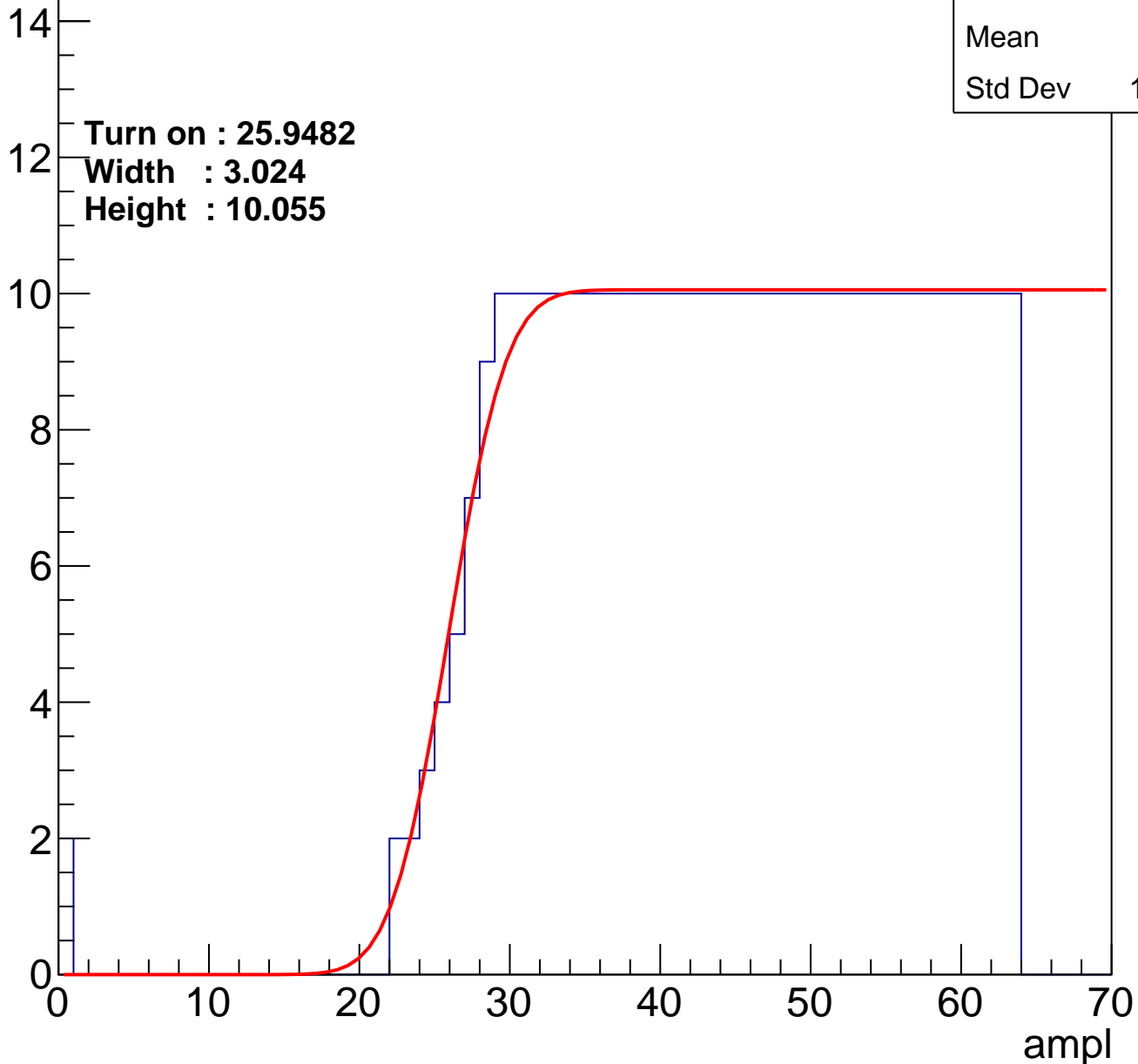
Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 25.9482

Width : 3.024

Height : 10.055

Entry



B1L000S, U25-ch59

calib_packv5_042523_0143.root, FC#3, port B3

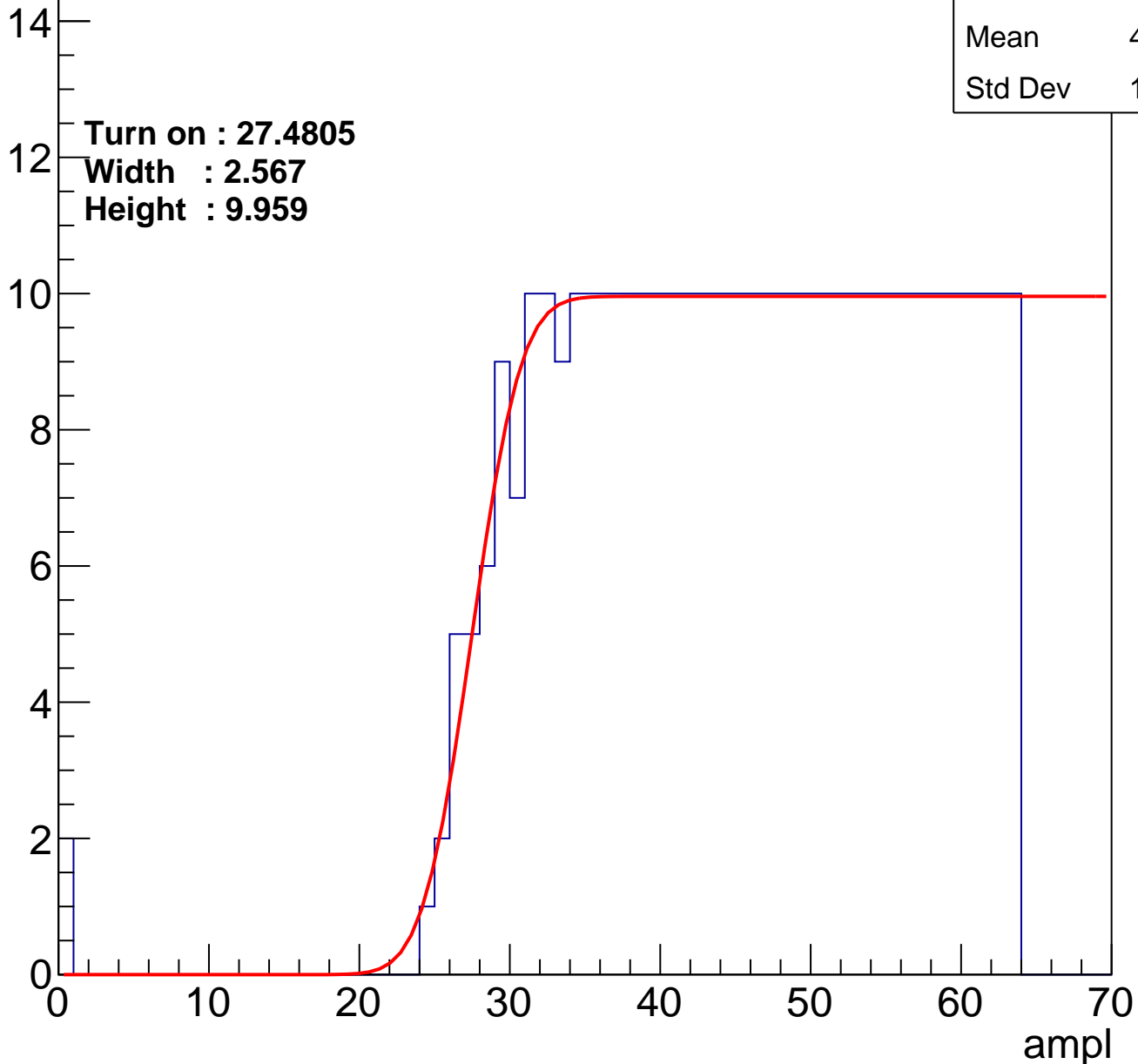
Entries	366
Mean	44.96
Std Dev	11.14

Turn on : 27.4805

Width : 2.567

Height : 9.959

Entry



B1L000S, U25-ch60

calib_packv5_042523_0143.root, FC#3, port B3

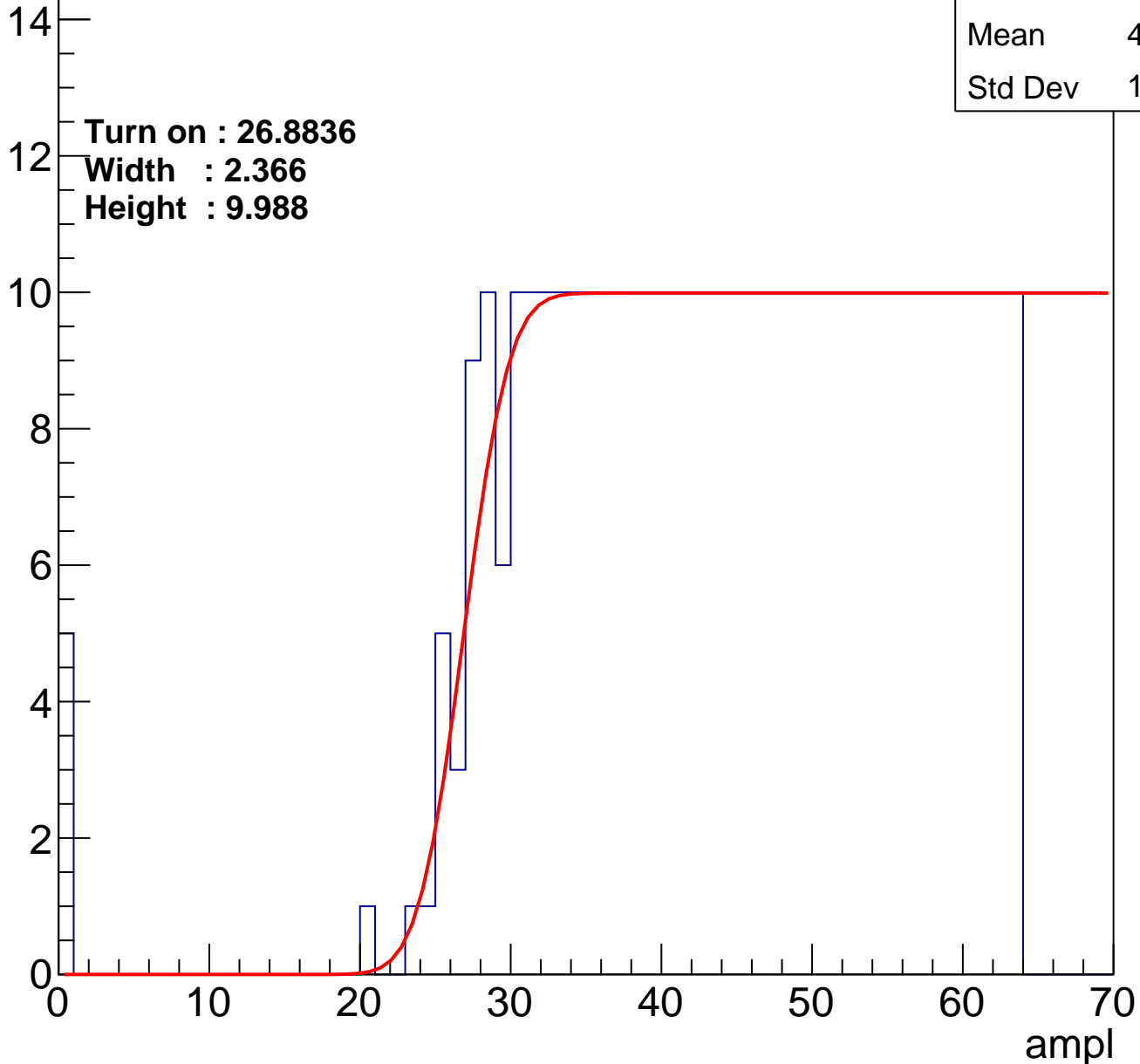
Entries	381
Mean	44.03
Std Dev	12.04

Turn on : 26.8836

Width : 2.366

Height : 9.988

Entry



B1L000S, U25-ch61

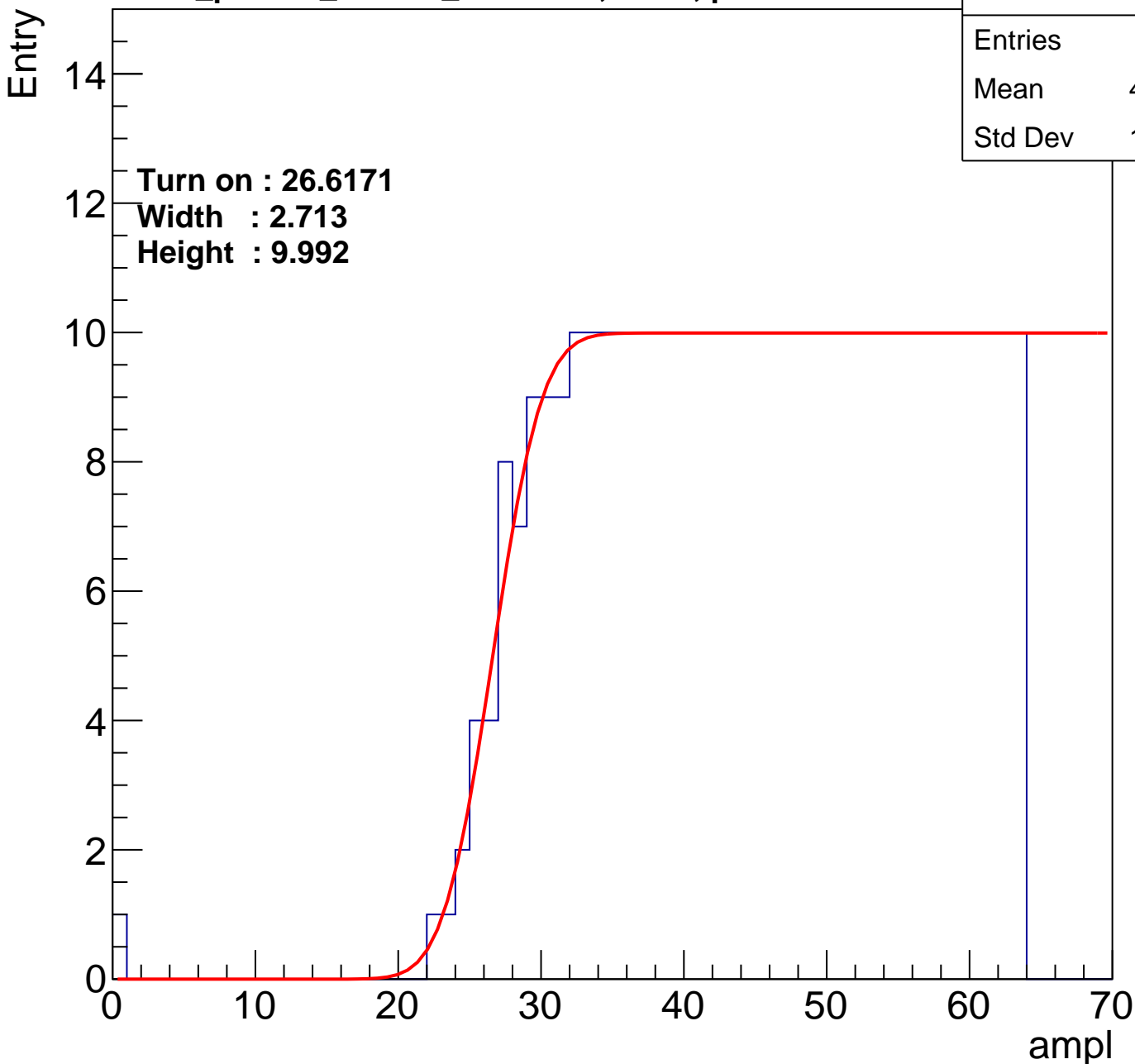
calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.58
Std Dev	11.18

Turn on : 26.6171

Width : 2.713

Height : 9.992



B1L000S, U25-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 25.7958

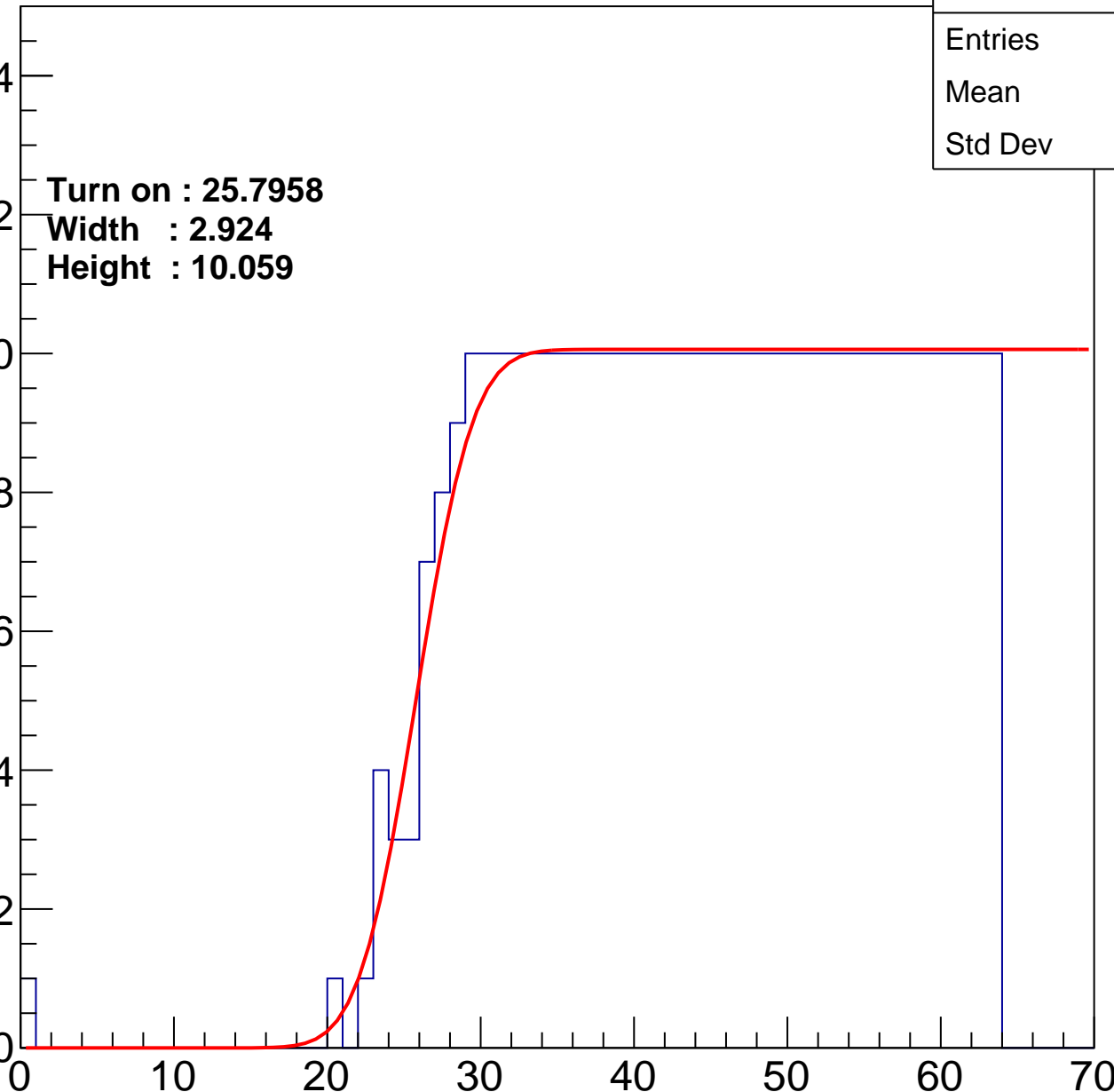
Width : 2.924

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.45
Std Dev	11.69

Turn on : 27.3862

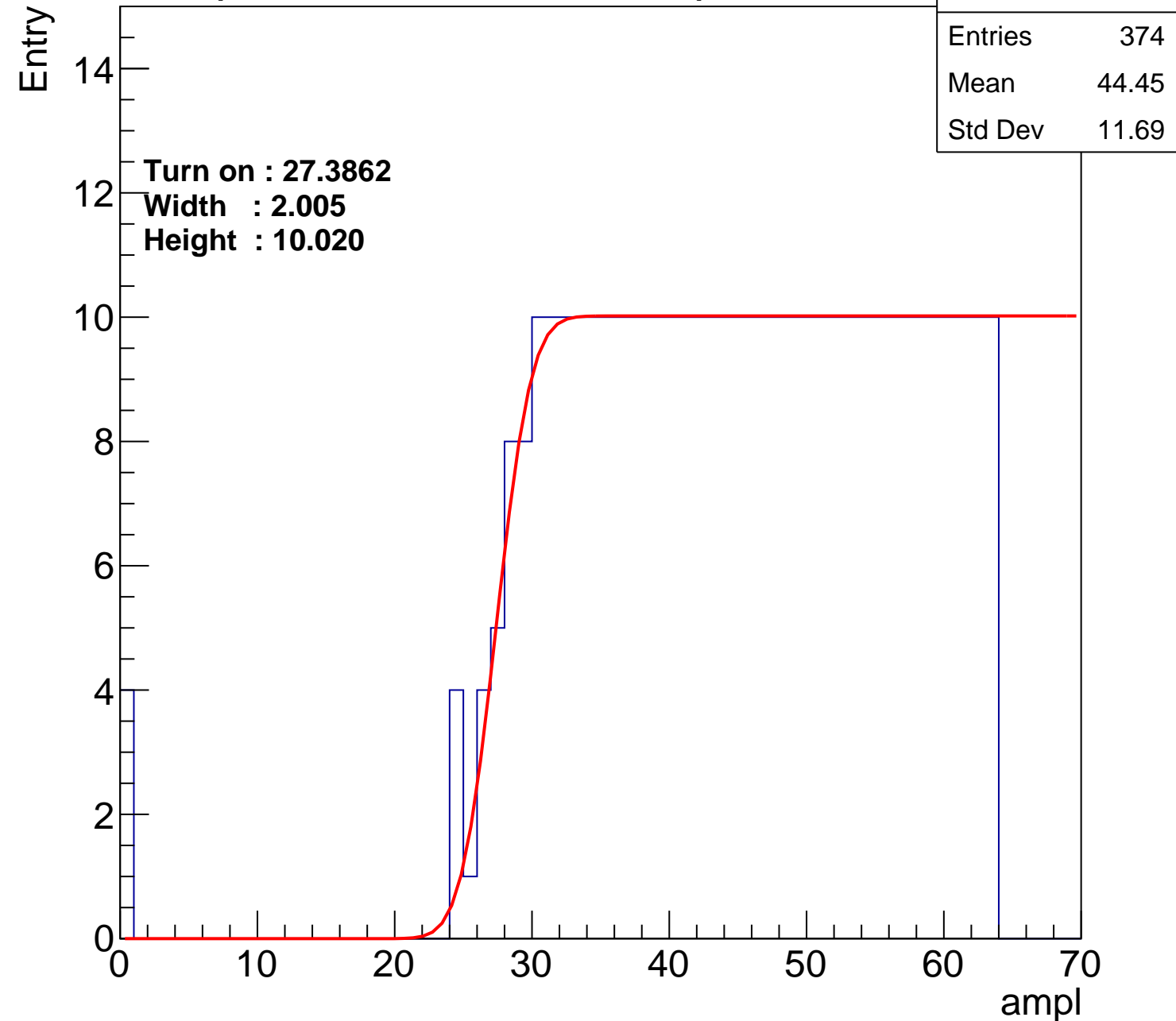
Width : 2.005

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.99
Std Dev	11.75

Turn on : 26.0063

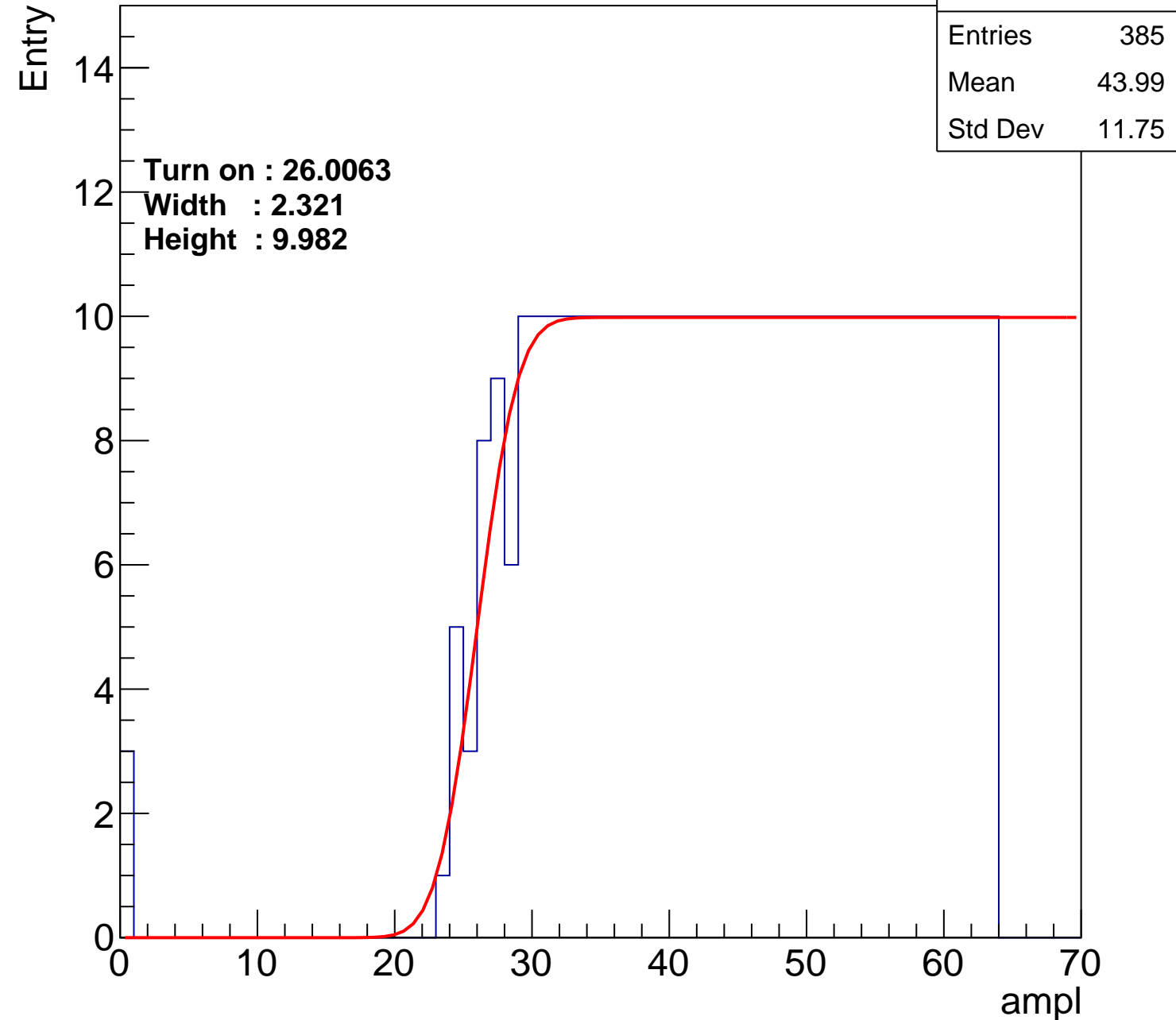
Width : 2.321

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45
Std Dev	11.28

Turn on : 28.4503

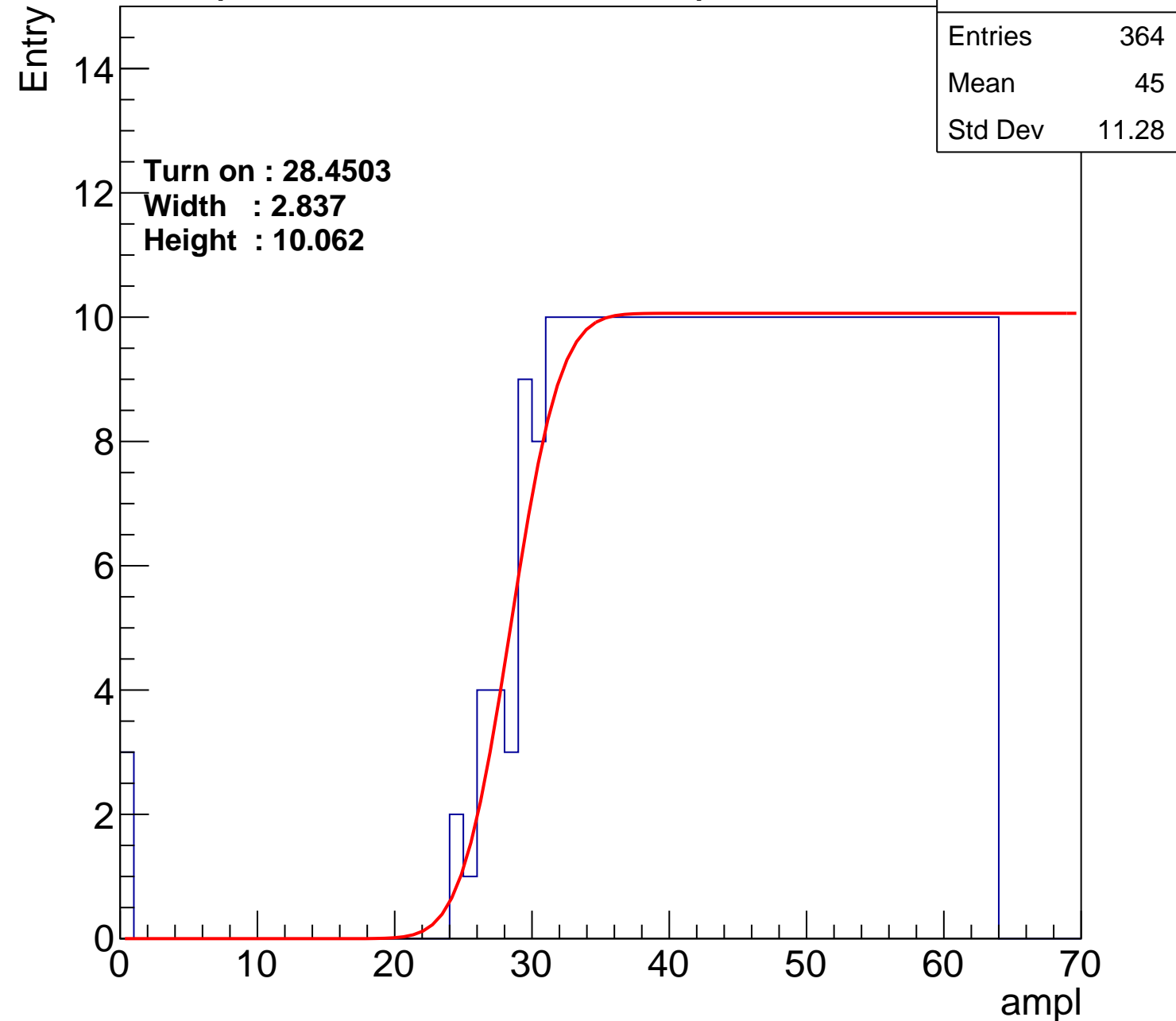
Width : 2.837

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch66

calib_packv5_042523_0143.root, FC#3, port B3

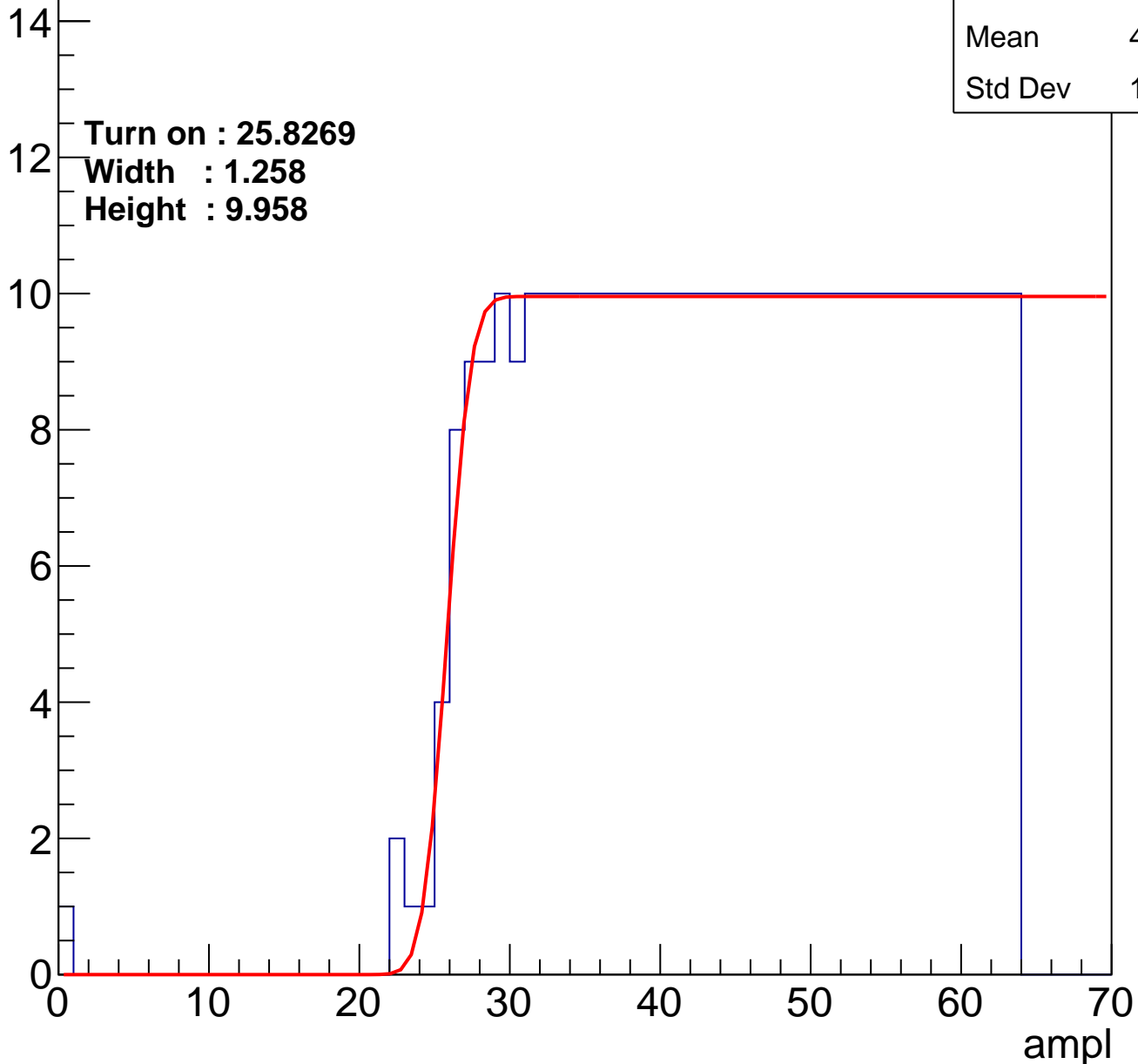
Entries	384
Mean	44.18
Std Dev	11.36

Turn on : 25.8269

Width : 1.258

Height : 9.958

Entry



B1L000S, U25-ch67

calib_packv5_042523_0143.root, FC#3, port B3

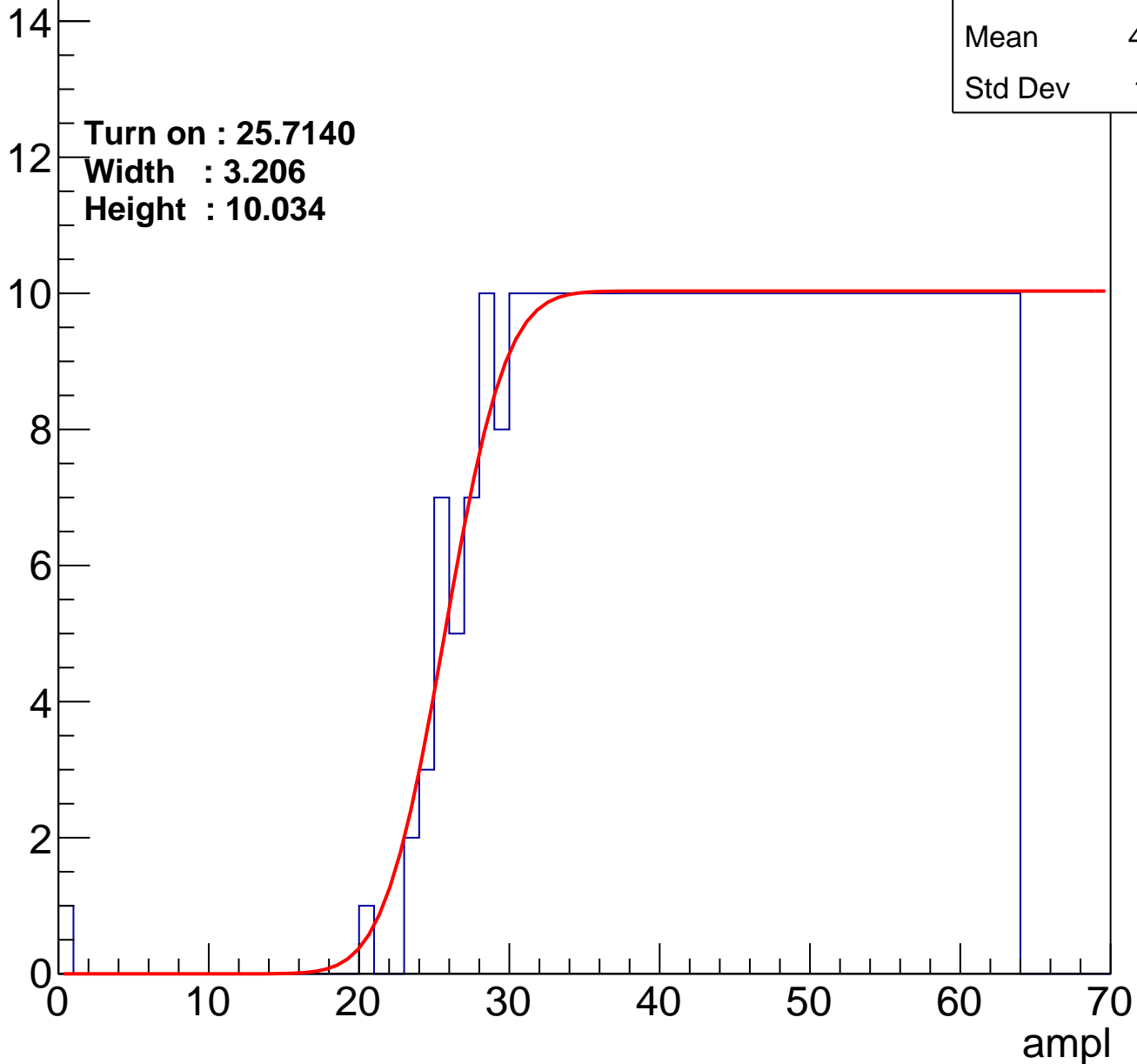
Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.7140

Width : 3.206

Height : 10.034

Entry



B1L000S, U25-ch68

calib_packv5_042523_0143.root, FC#3, port B3

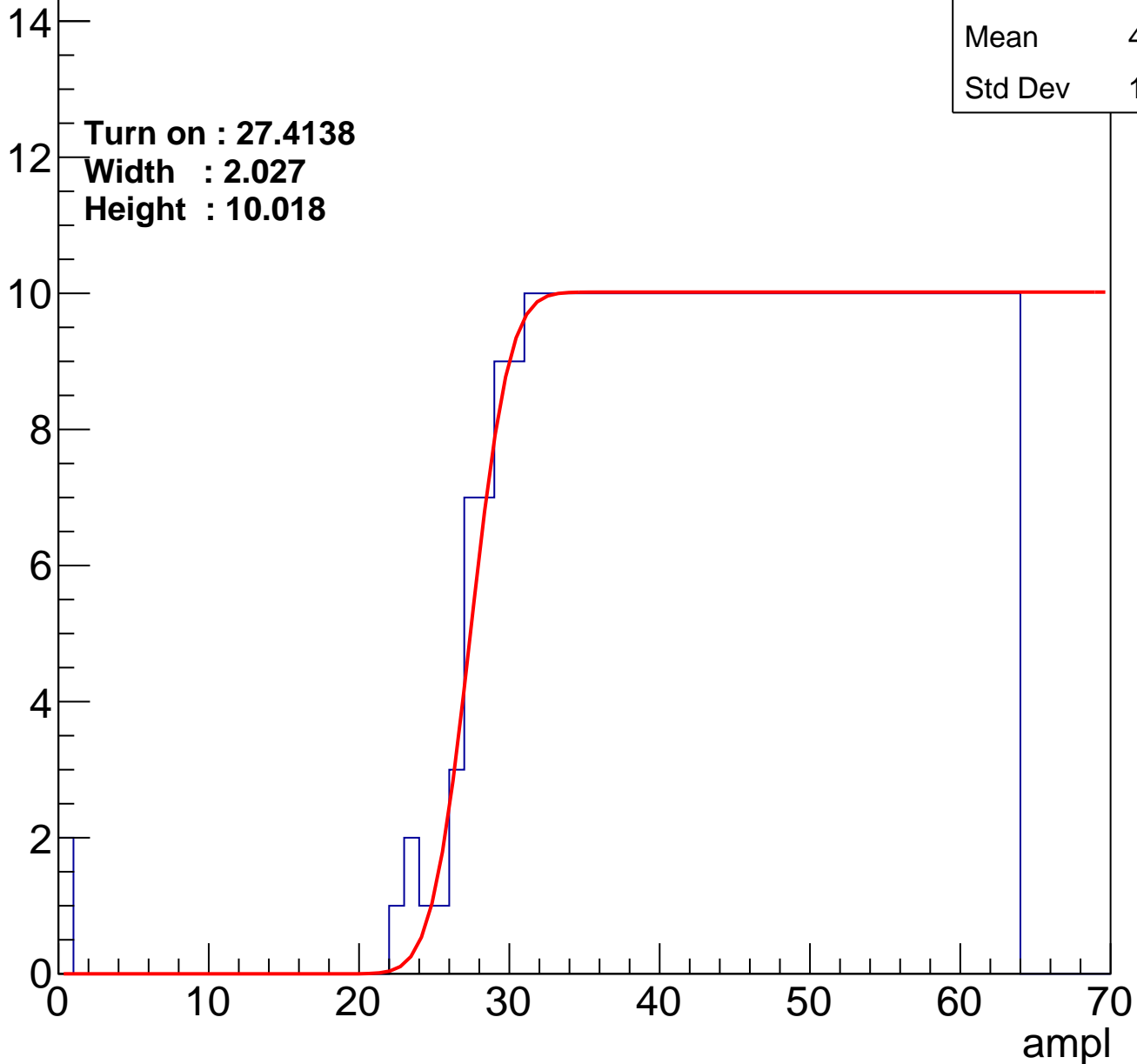
Entries	372
Mean	44.68
Std Dev	11.28

Turn on : 27.4138

Width : 2.027

Height : 10.018

Entry



B1L000S, U25-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.0142

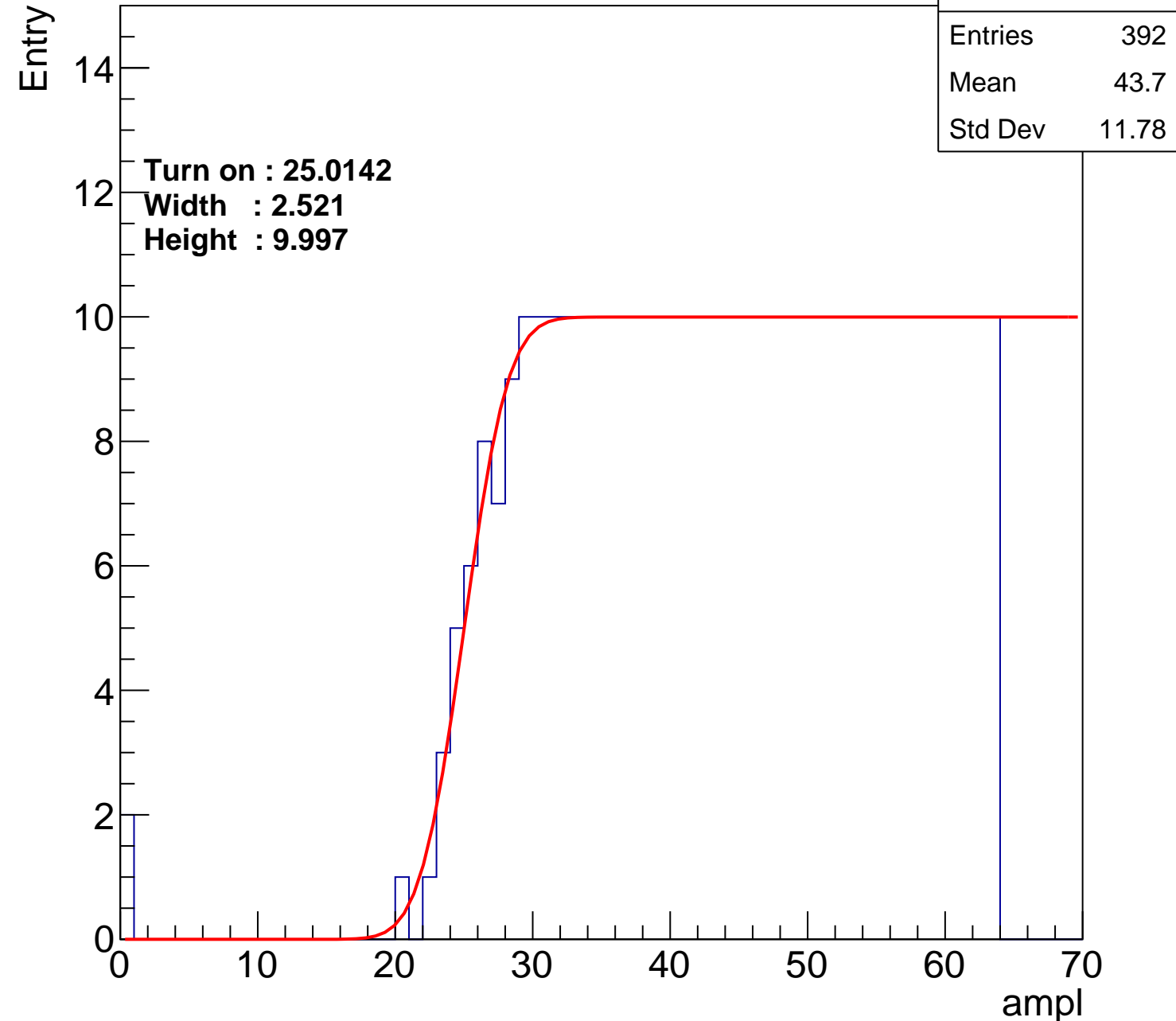
Width : 2.521

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.3995

Width : 3.593

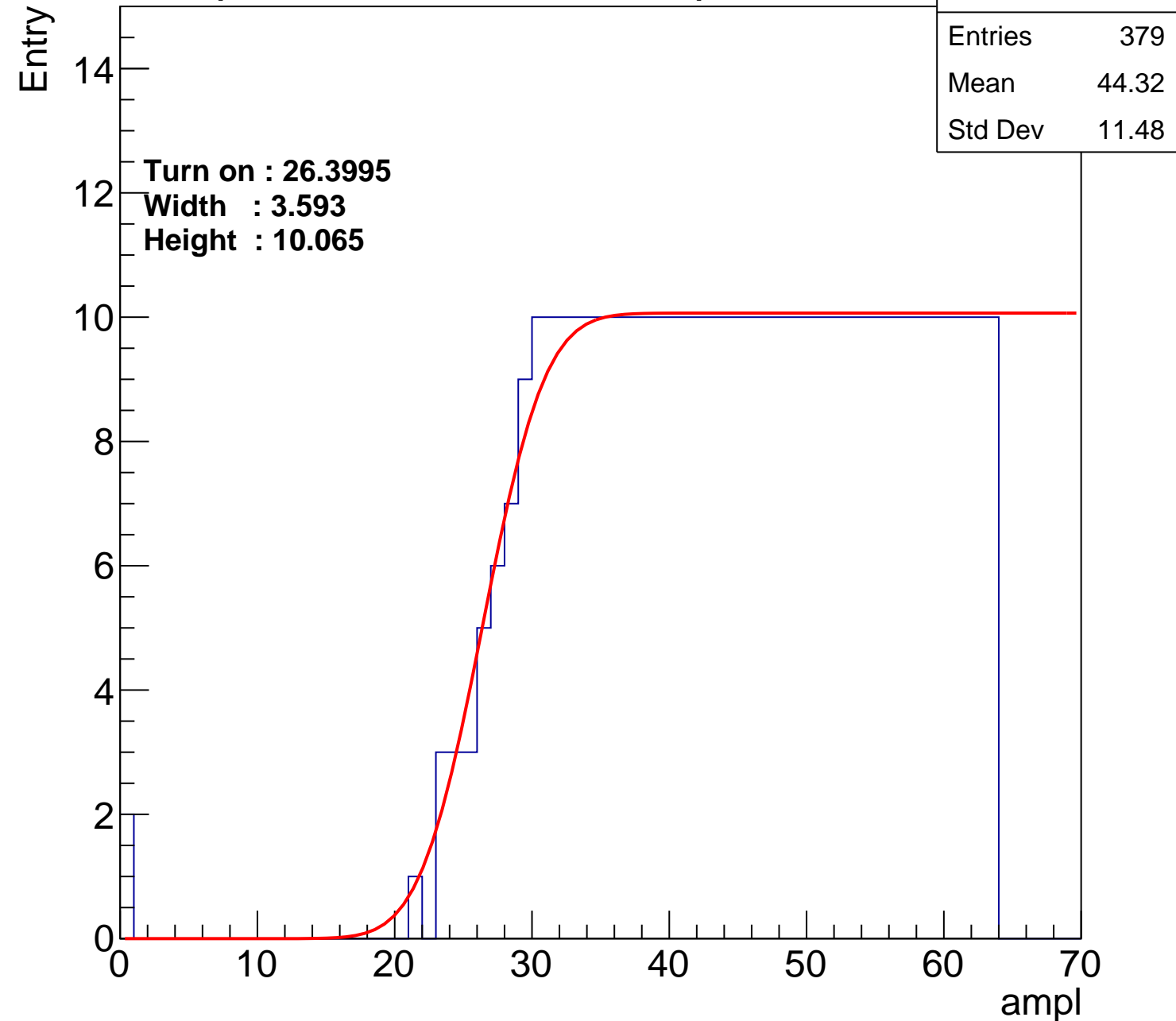
Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U25-ch71

calib_packv5_042523_0143.root, FC#3, port B3

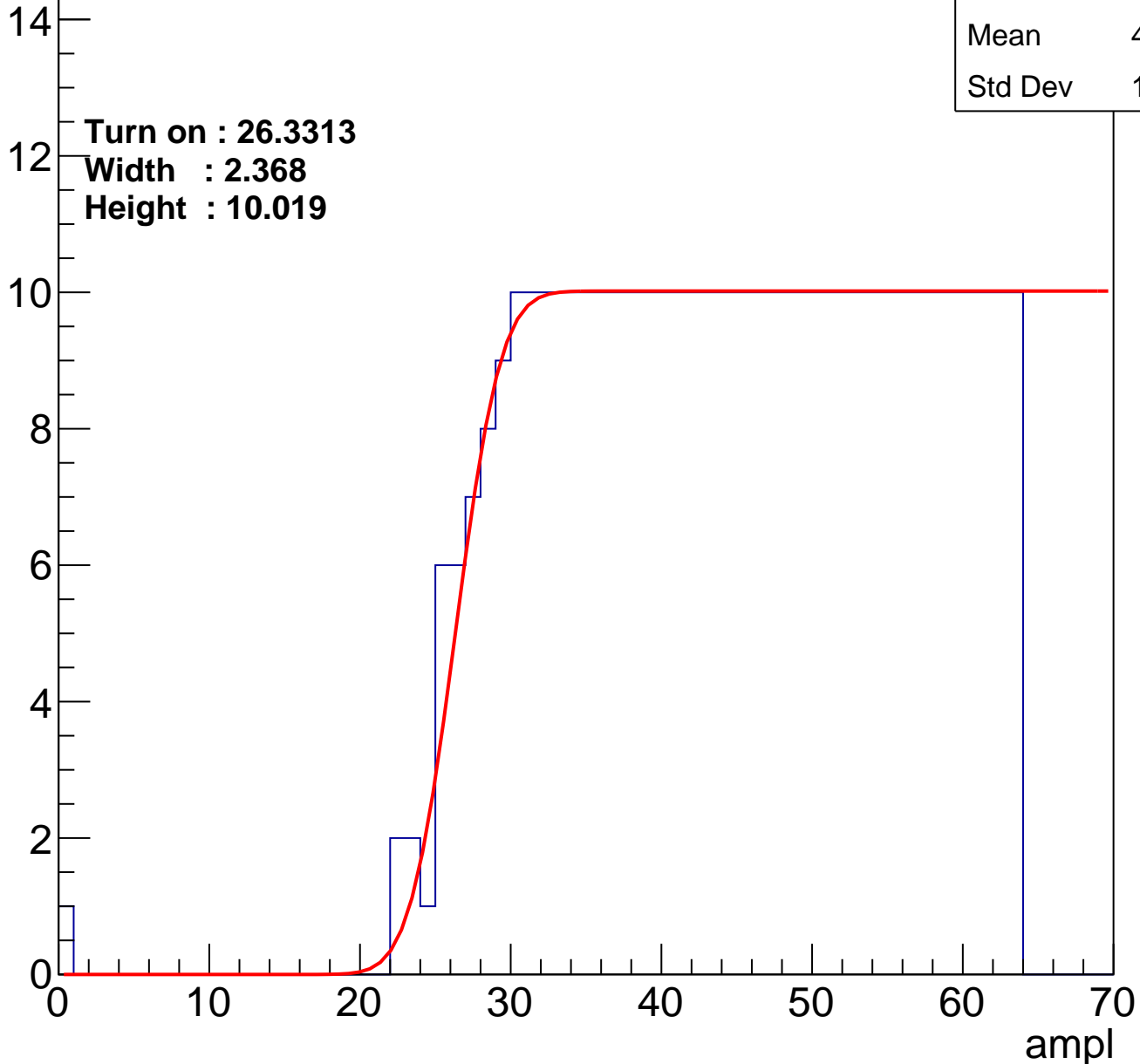
Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.3313

Width : 2.368

Height : 10.019

Entry



B1L000S, U25-ch72

calib_packv5_042523_0143.root, FC#3, port B3

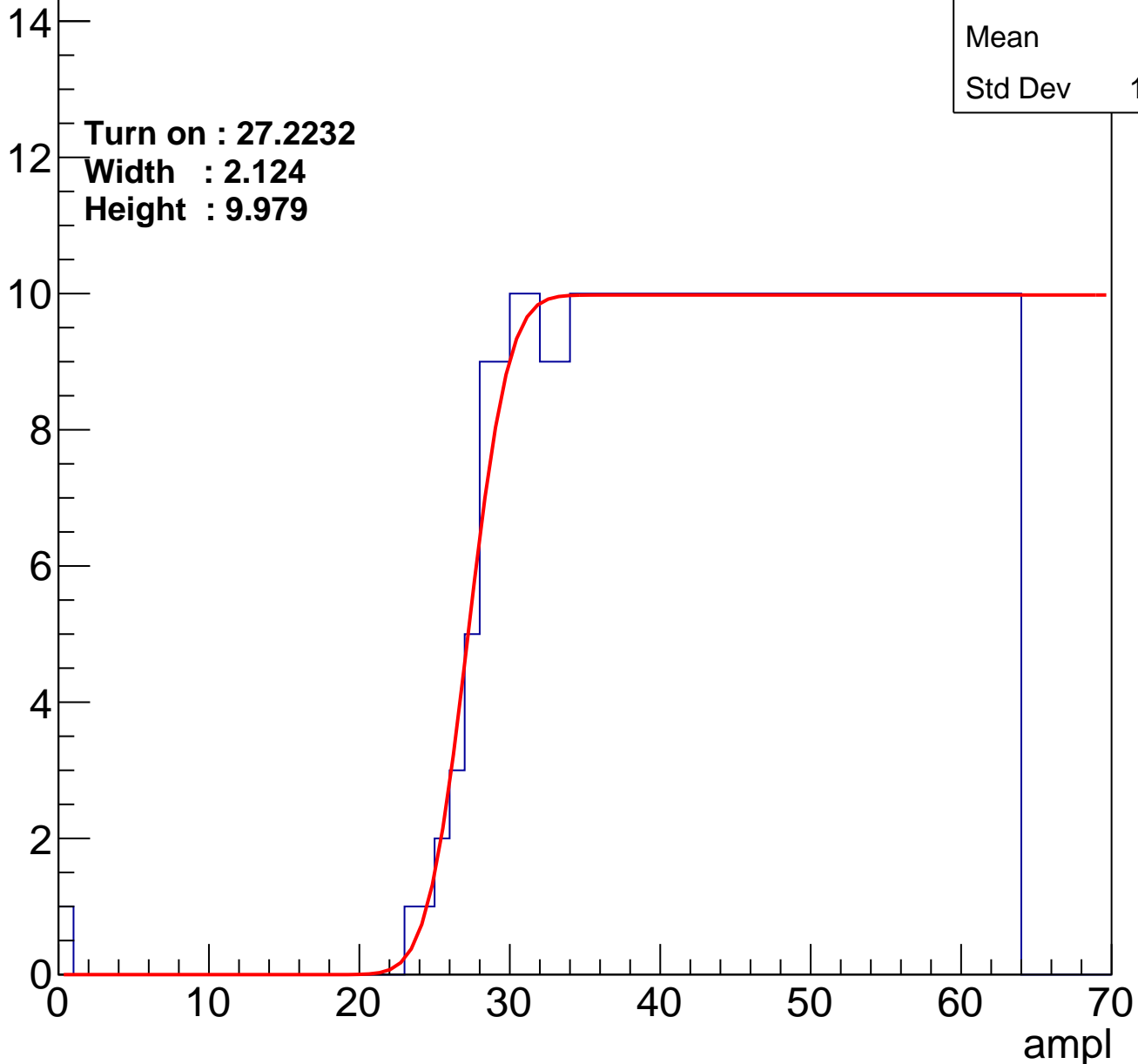
Entries	369
Mean	44.9
Std Dev	10.99

Turn on : 27.2232

Width : 2.124

Height : 9.979

Entry



B1L000S, U25-ch73

calib_packv5_042523_0143.root, FC#3, port B3

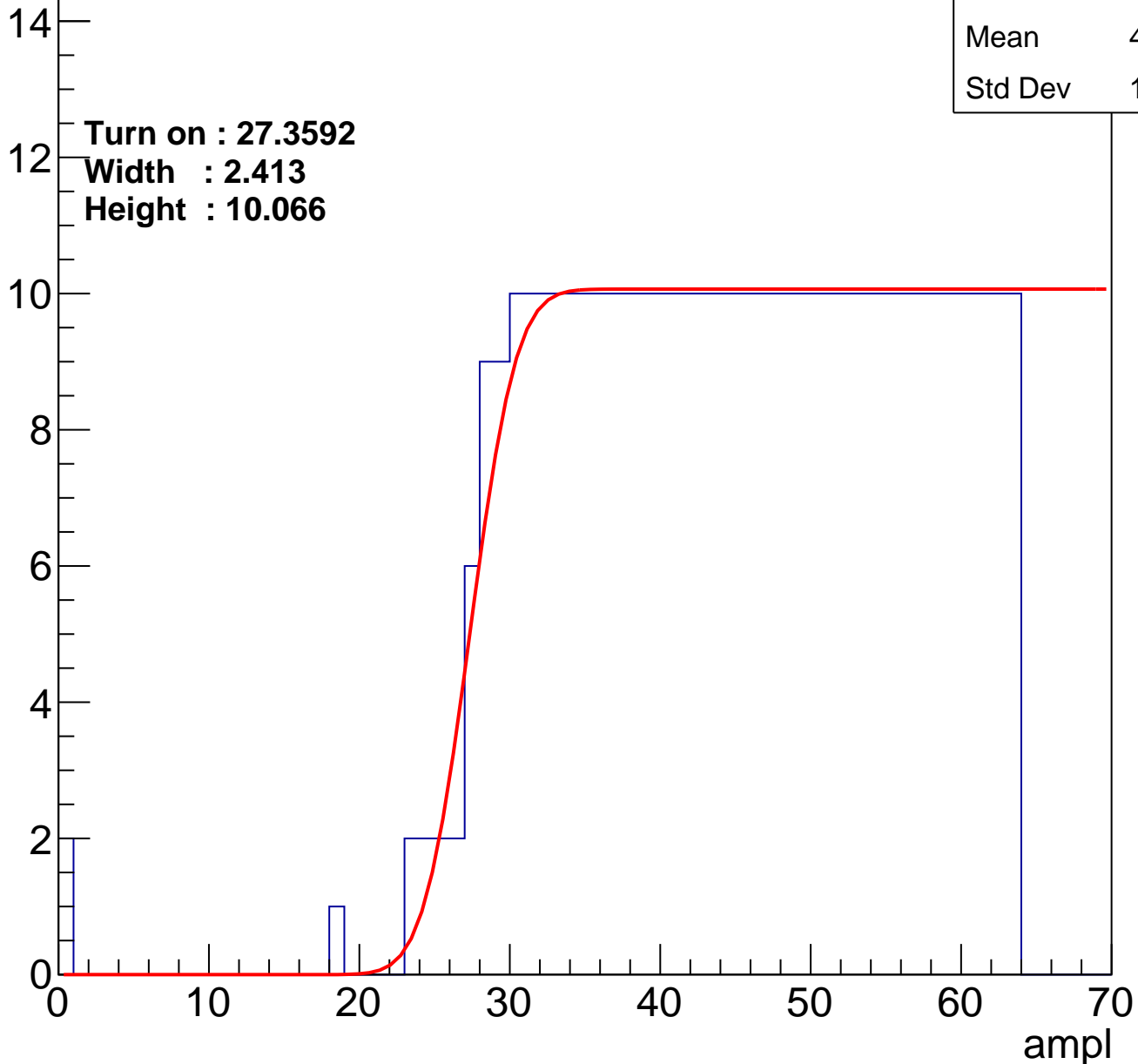
Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 27.3592

Width : 2.413

Height : 10.066

Entry



B1L000S, U25-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.2630

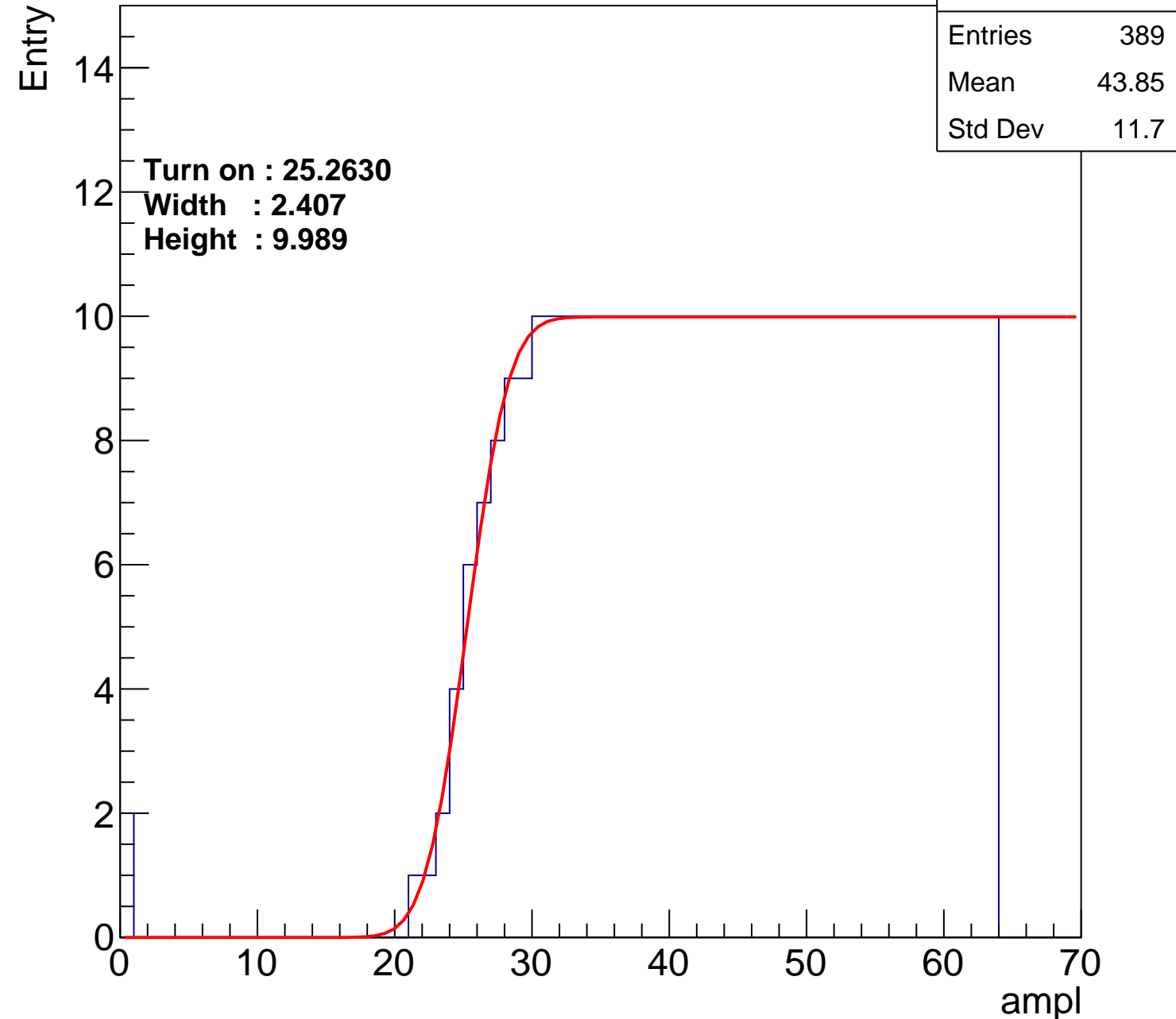
Width : 2.407

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.03
Std Dev	11.08

Turn on : 27.8141

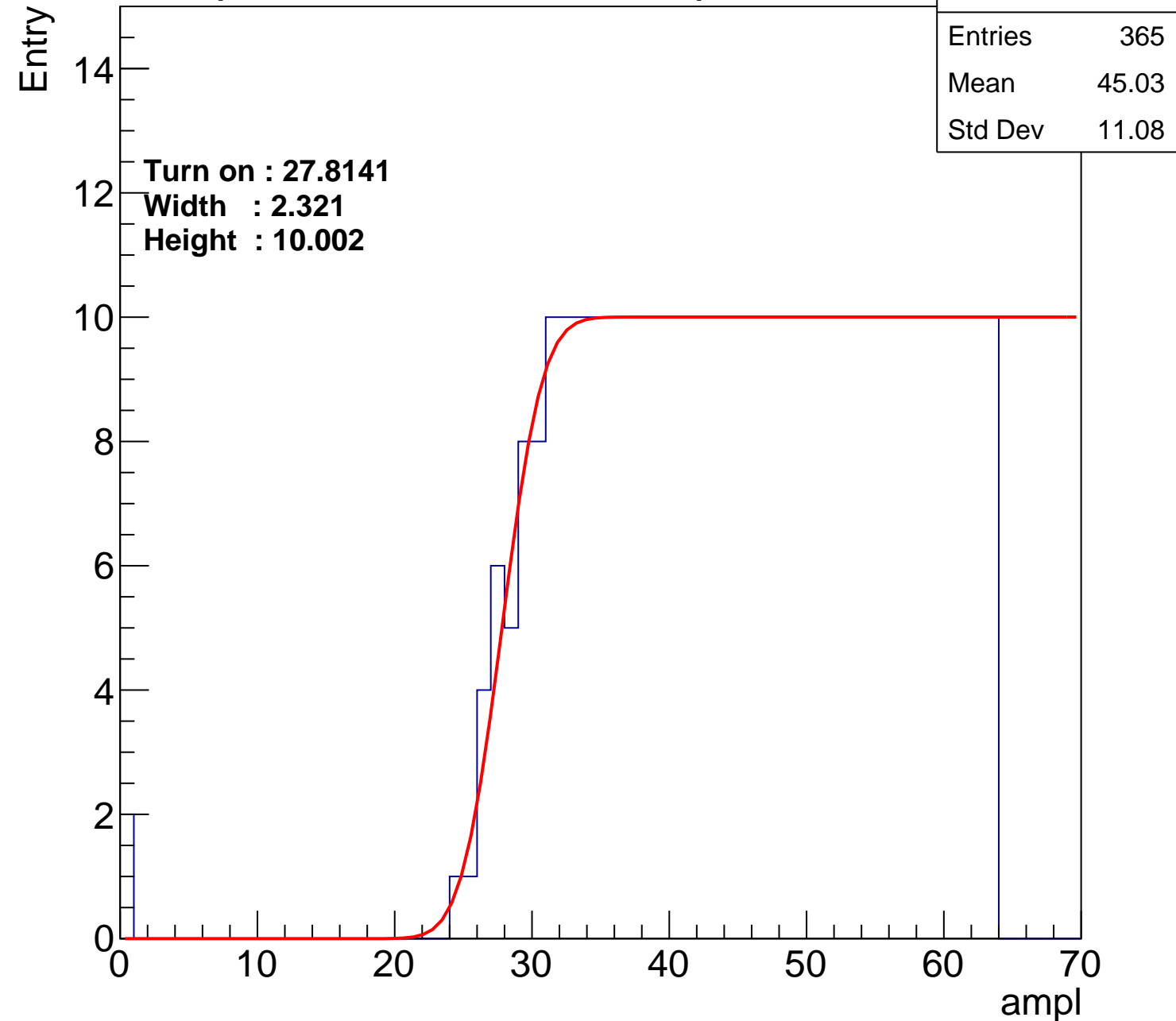
Width : 2.321

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch76

calib_packv5_042523_0143.root, FC#3, port B3

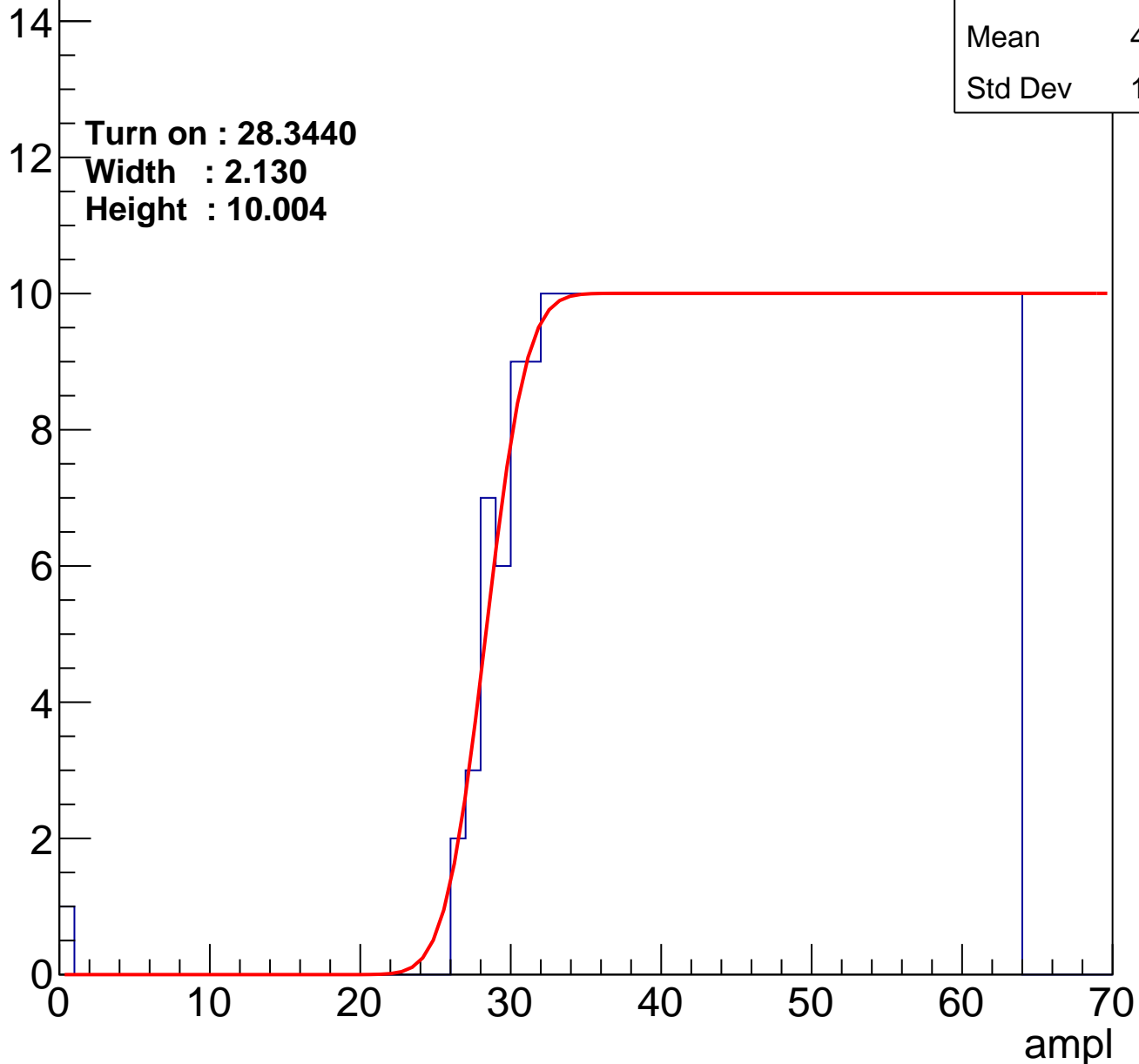
Entries	357
Mean	45.52
Std Dev	10.62

Turn on : 28.3440

Width : 2.130

Height : 10.004

Entry



B1L000S, U25-ch77

calib_packv5_042523_0143.root, FC#3, port B3

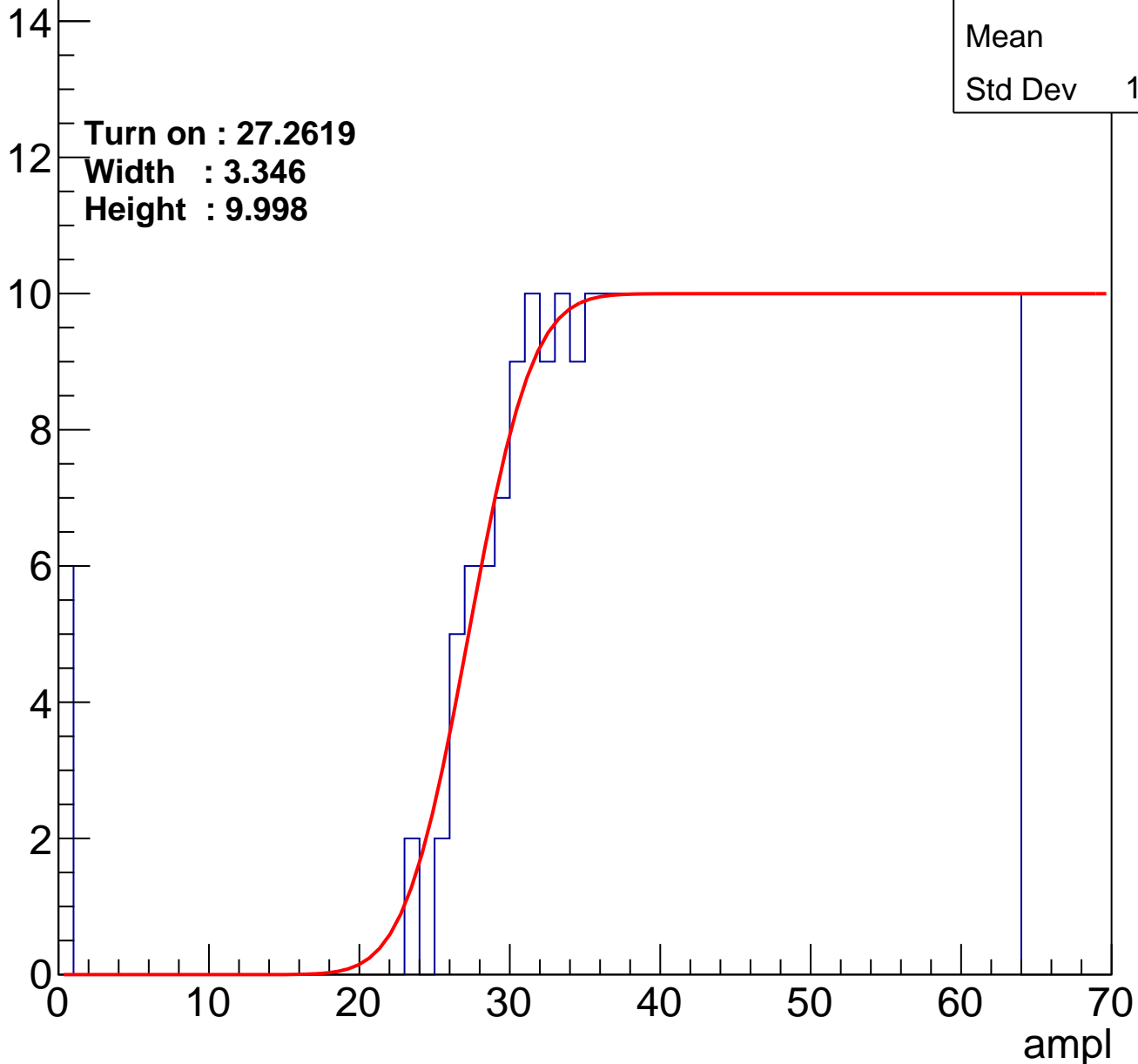
Entries	371
Mean	44.4
Std Dev	12.07

Turn on : 27.2619

Width : 3.346

Height : 9.998

Entry



B1L000S, U25-ch78

calib_packv5_042523_0143.root, FC#3, port B3

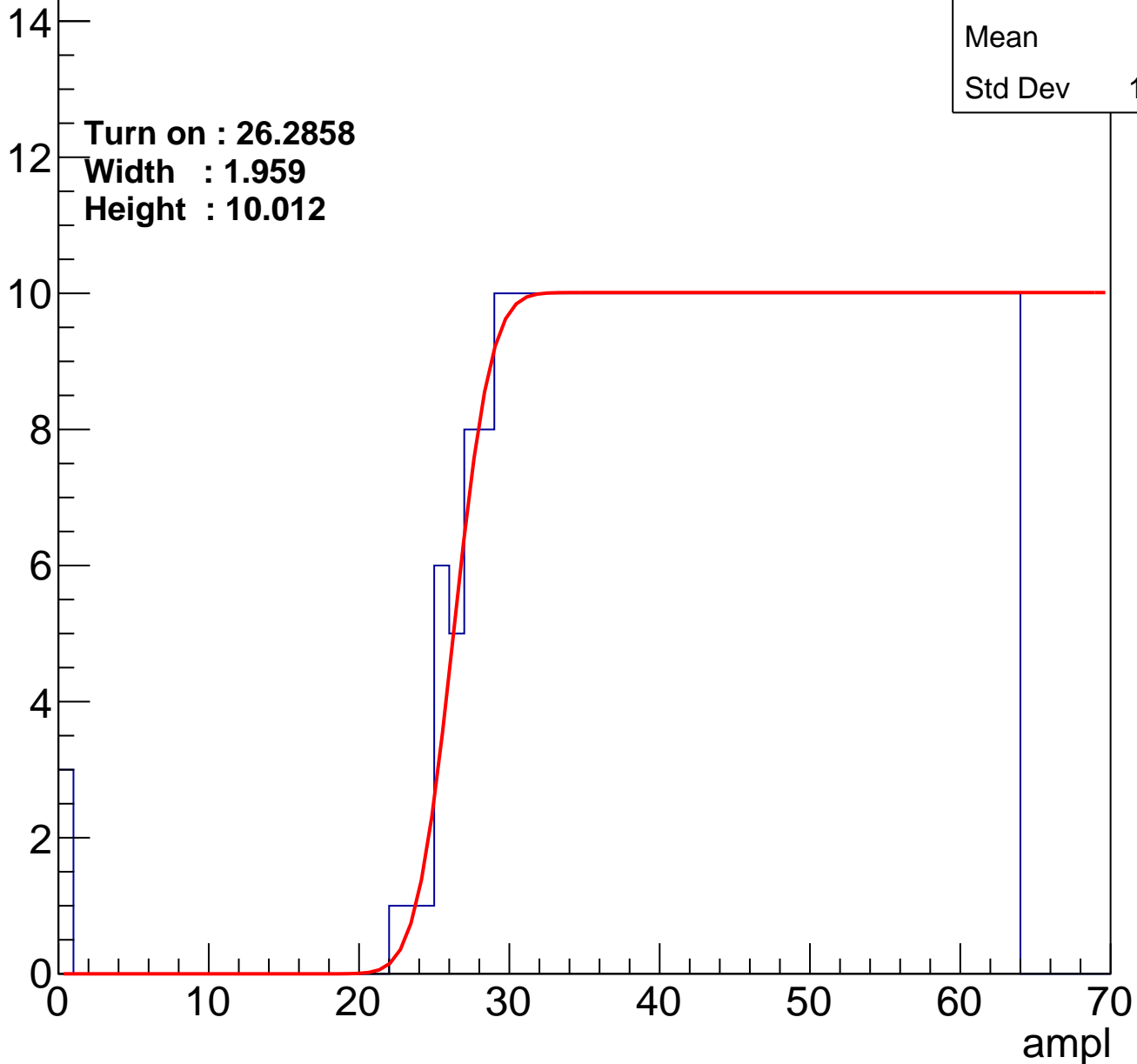
Entries	383
Mean	44.1
Std Dev	11.69

Turn on : 26.2858

Width : 1.959

Height : 10.012

Entry



B1L000S, U25-ch79

calib_packv5_042523_0143.root, FC#3, port B3

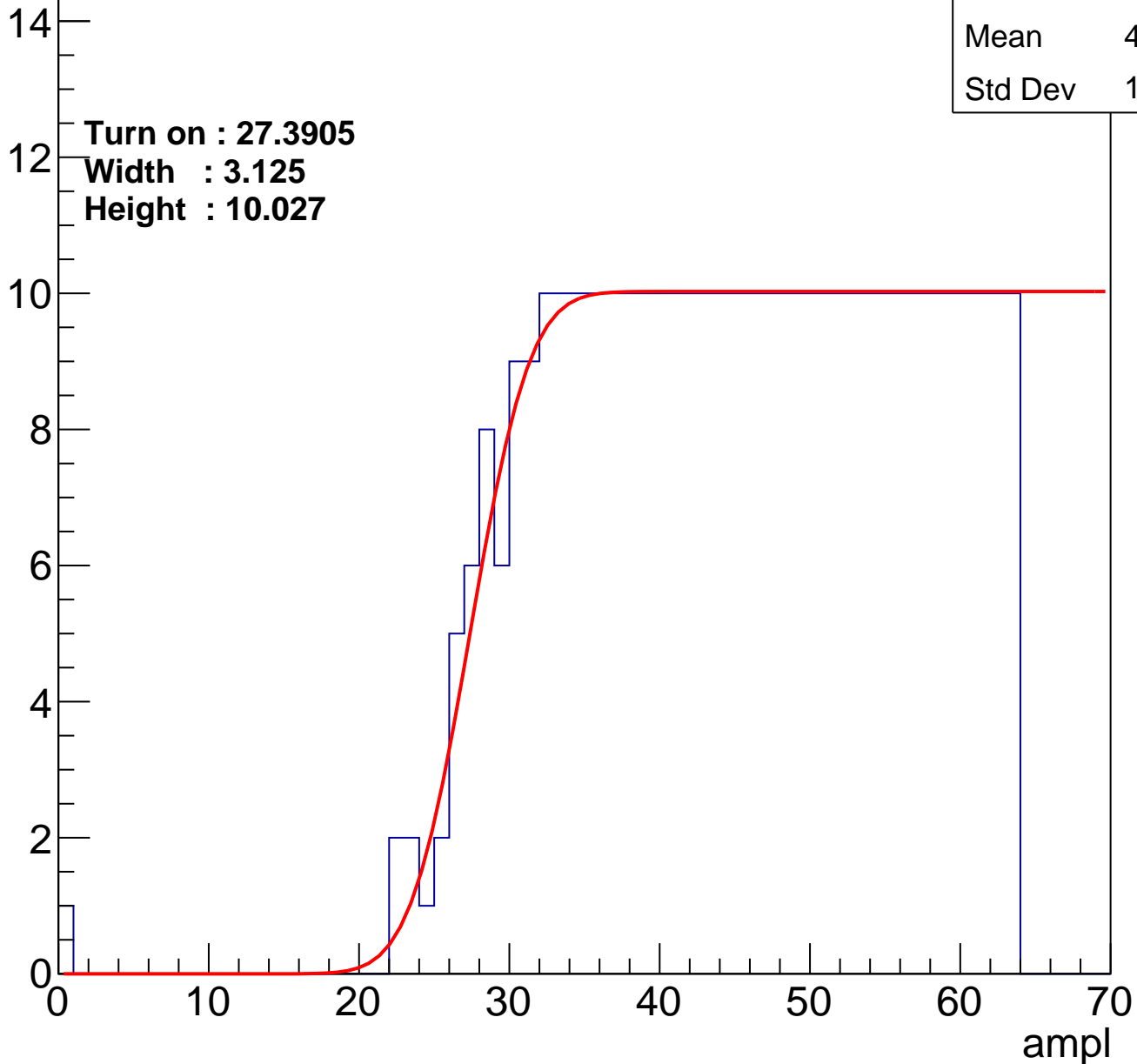
Entries	371
Mean	44.75
Std Dev	11.13

Turn on : 27.3905

Width : 3.125

Height : 10.027

Entry



B1L000S, U25-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.93
Std Dev	11.92

Turn on : 25.8585

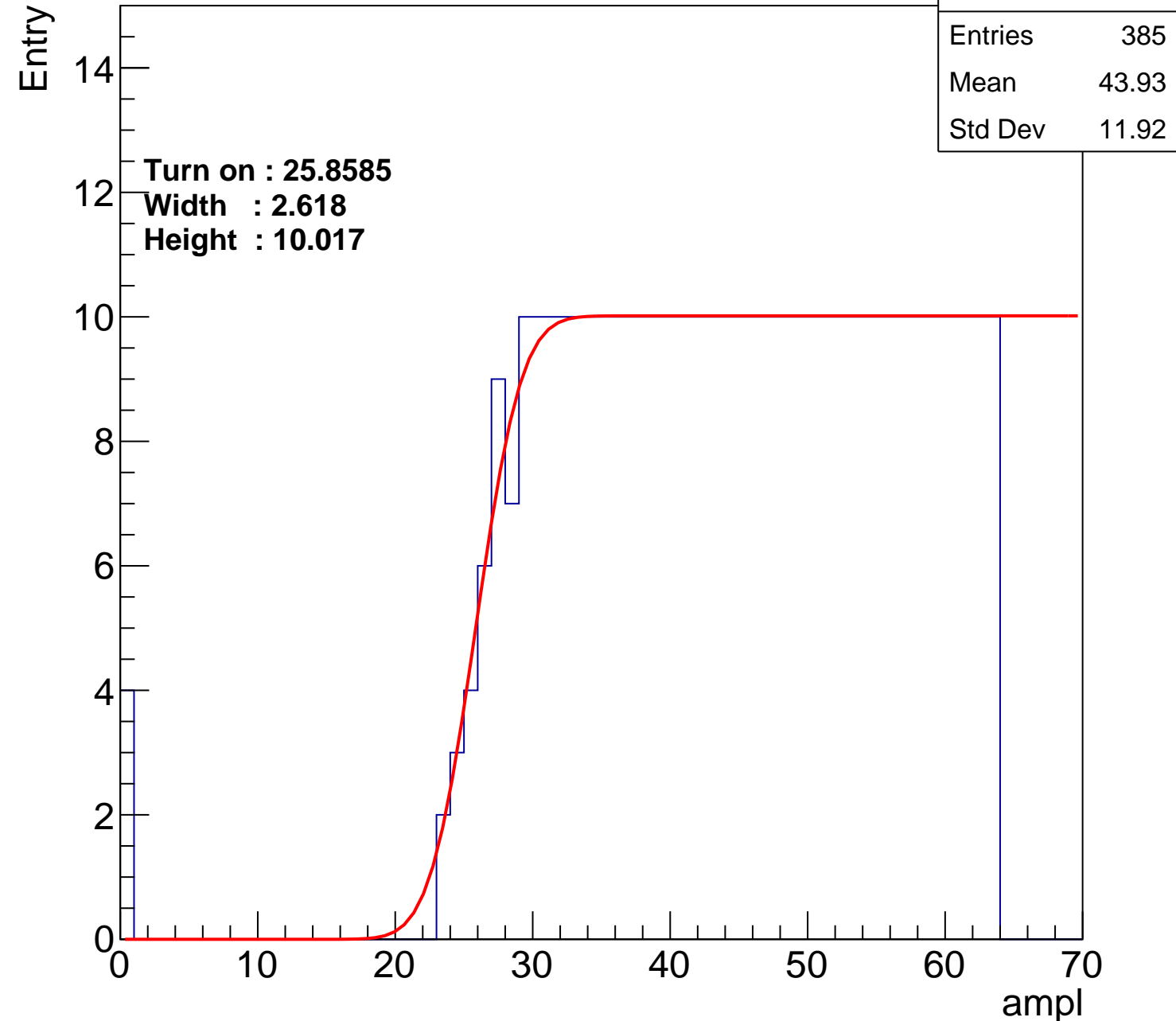
Width : 2.618

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch81

calib_packv5_042523_0143.root, FC#3, port B3

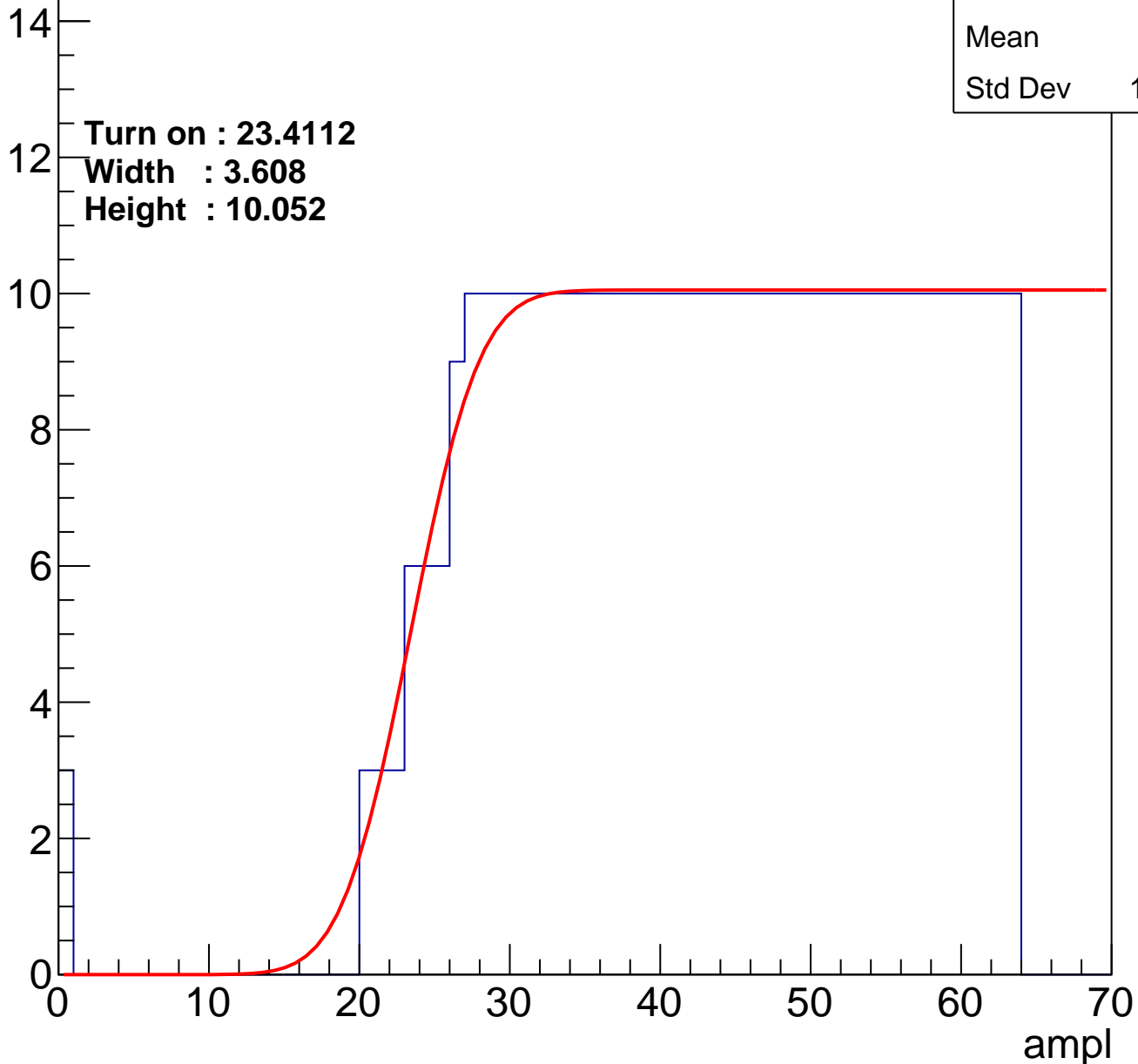
Entries	409
Mean	42.8
Std Dev	12.38

Turn on : 23.4112

Width : 3.608

Height : 10.052

Entry



B1L000S, U25-ch82

calib_packv5_042523_0143.root, FC#3, port B3

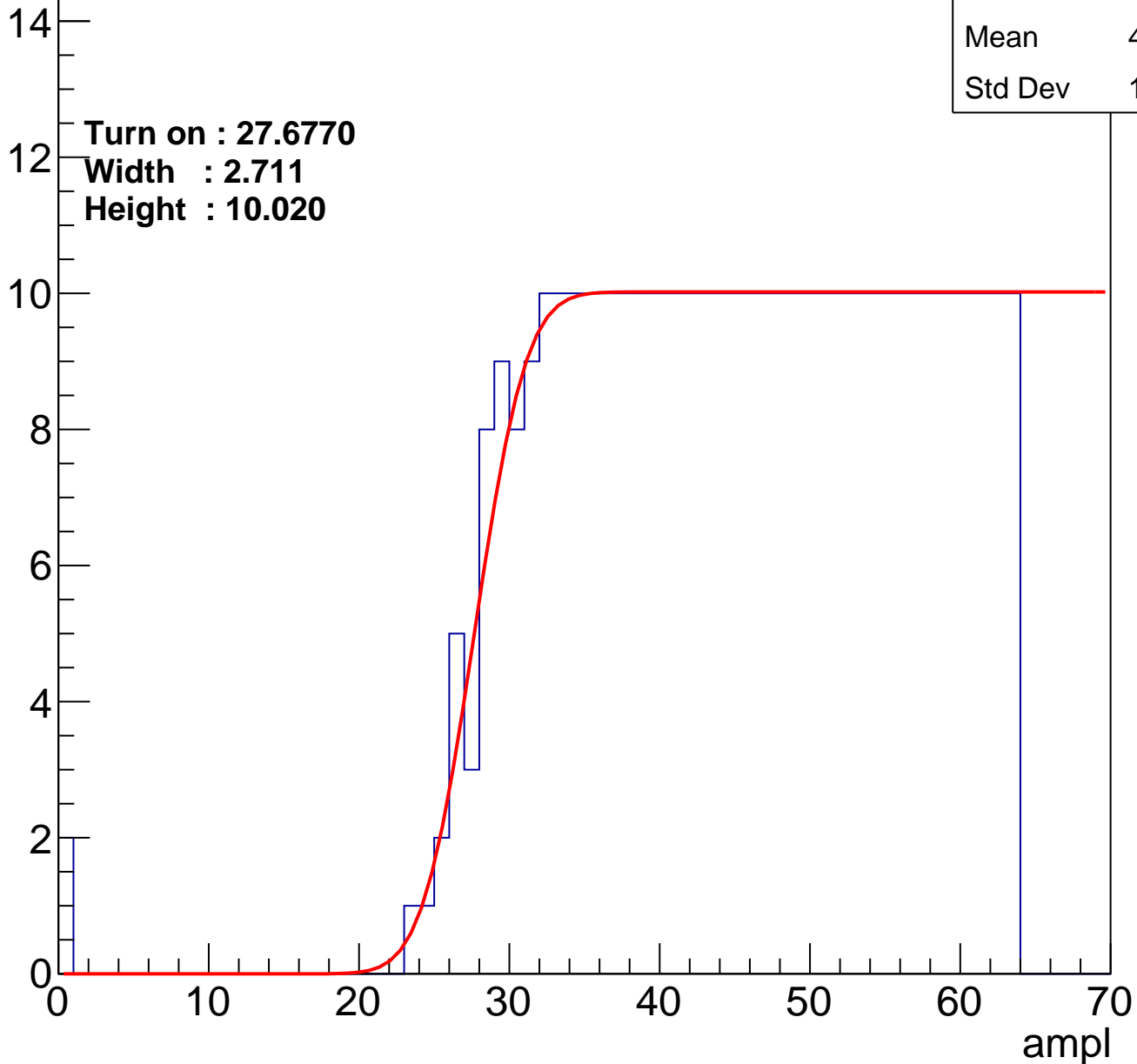
Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 27.6770

Width : 2.711

Height : 10.020

Entry



B1L000S, U25-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.69
Std Dev	11.27

Turn on : 27.0260

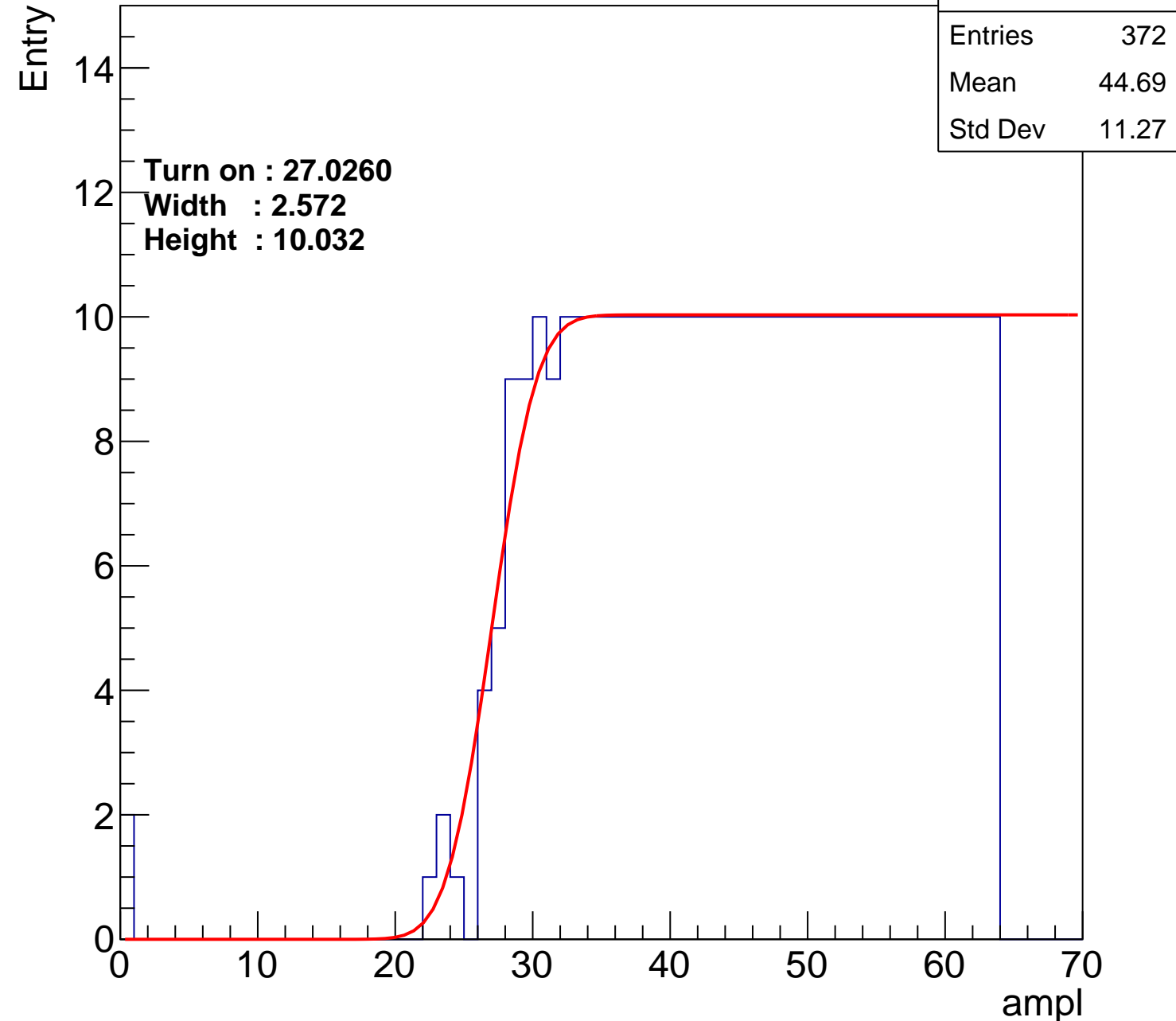
Width : 2.572

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.93
Std Dev	11.14

Turn on : 27.5576

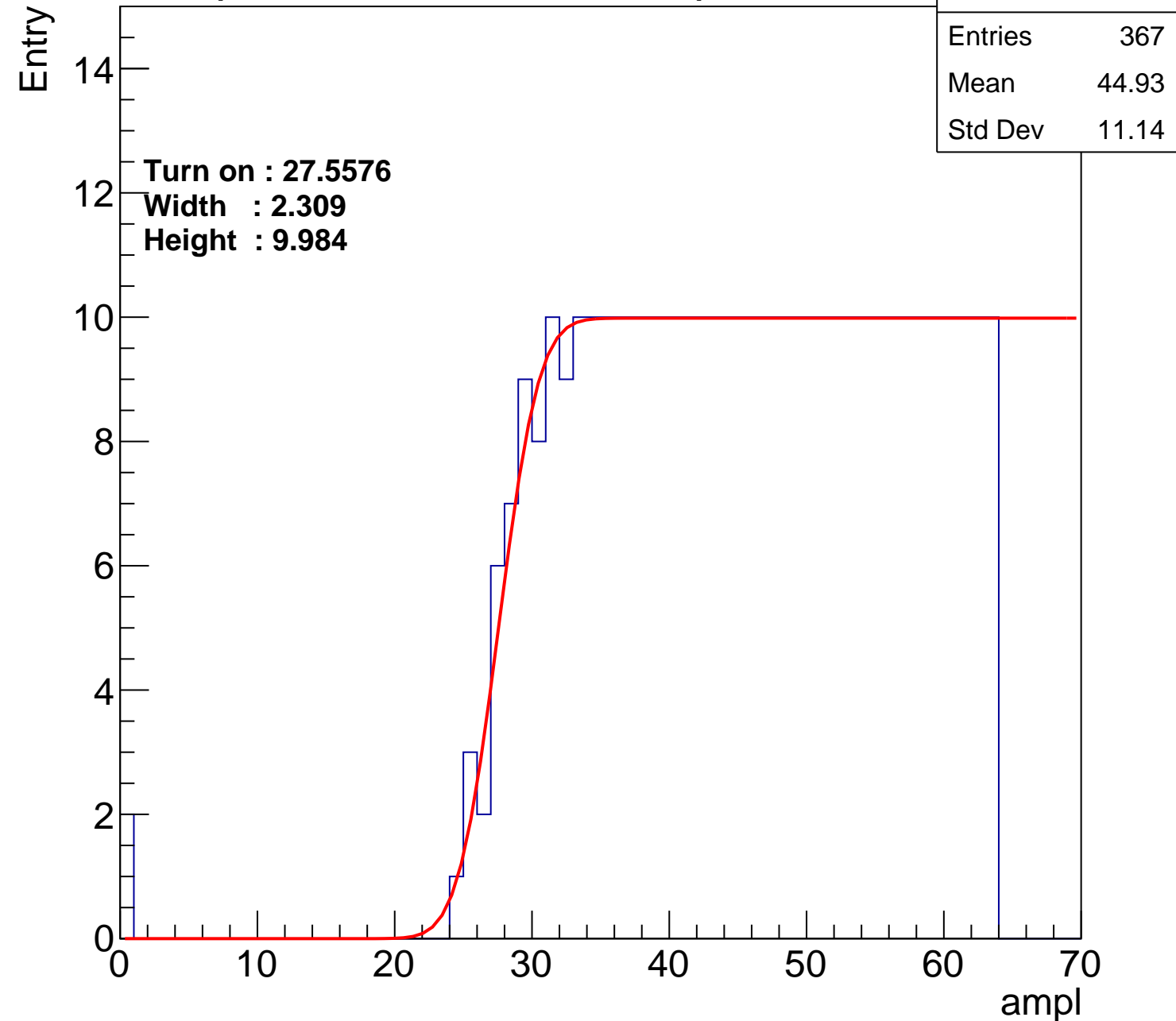
Width : 2.309

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.29
Std Dev	11.32

Turn on : 25.5091

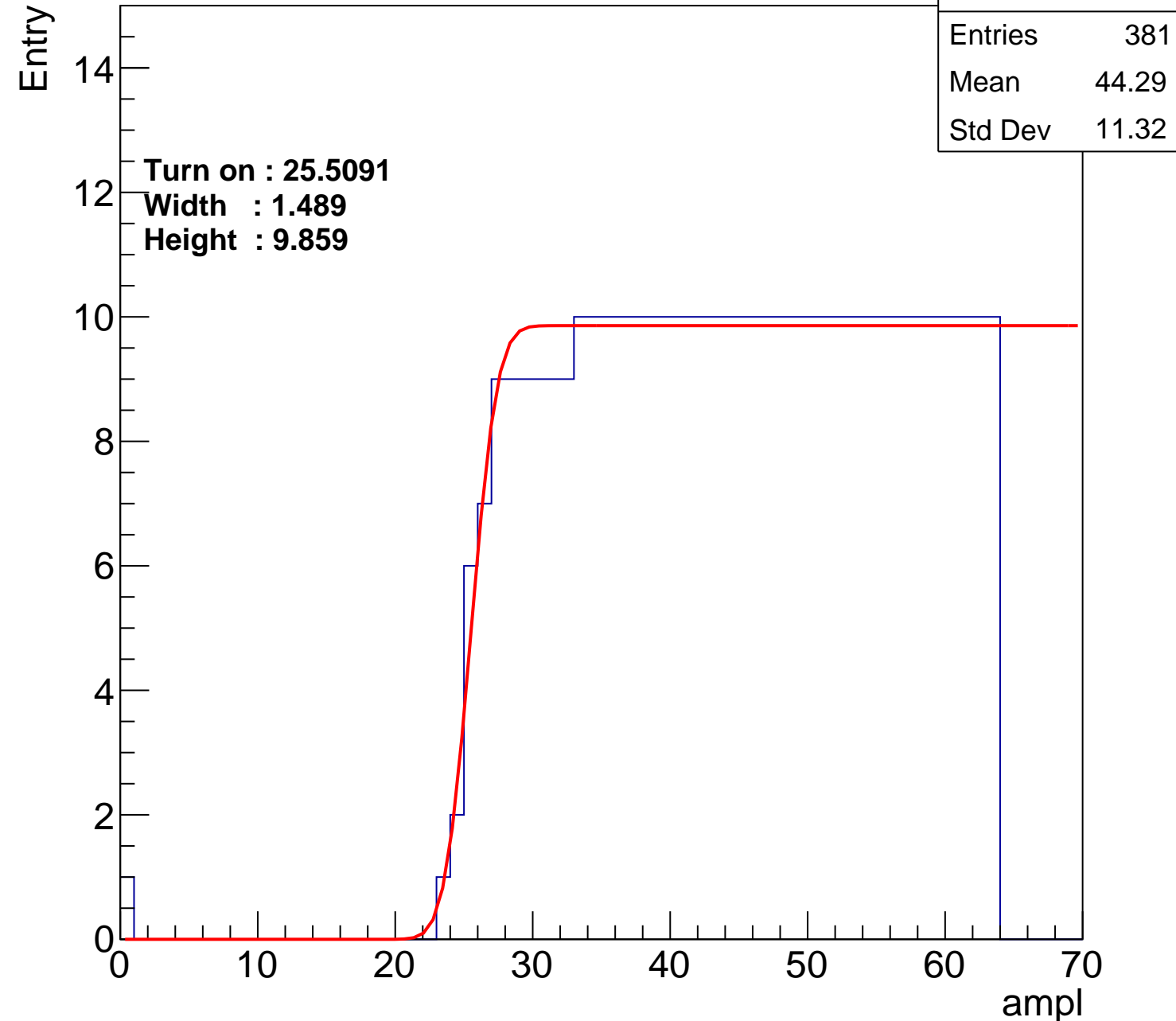
Width : 1.489

Height : 9.859

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch86

calib_packv5_042523_0143.root, FC#3, port B3

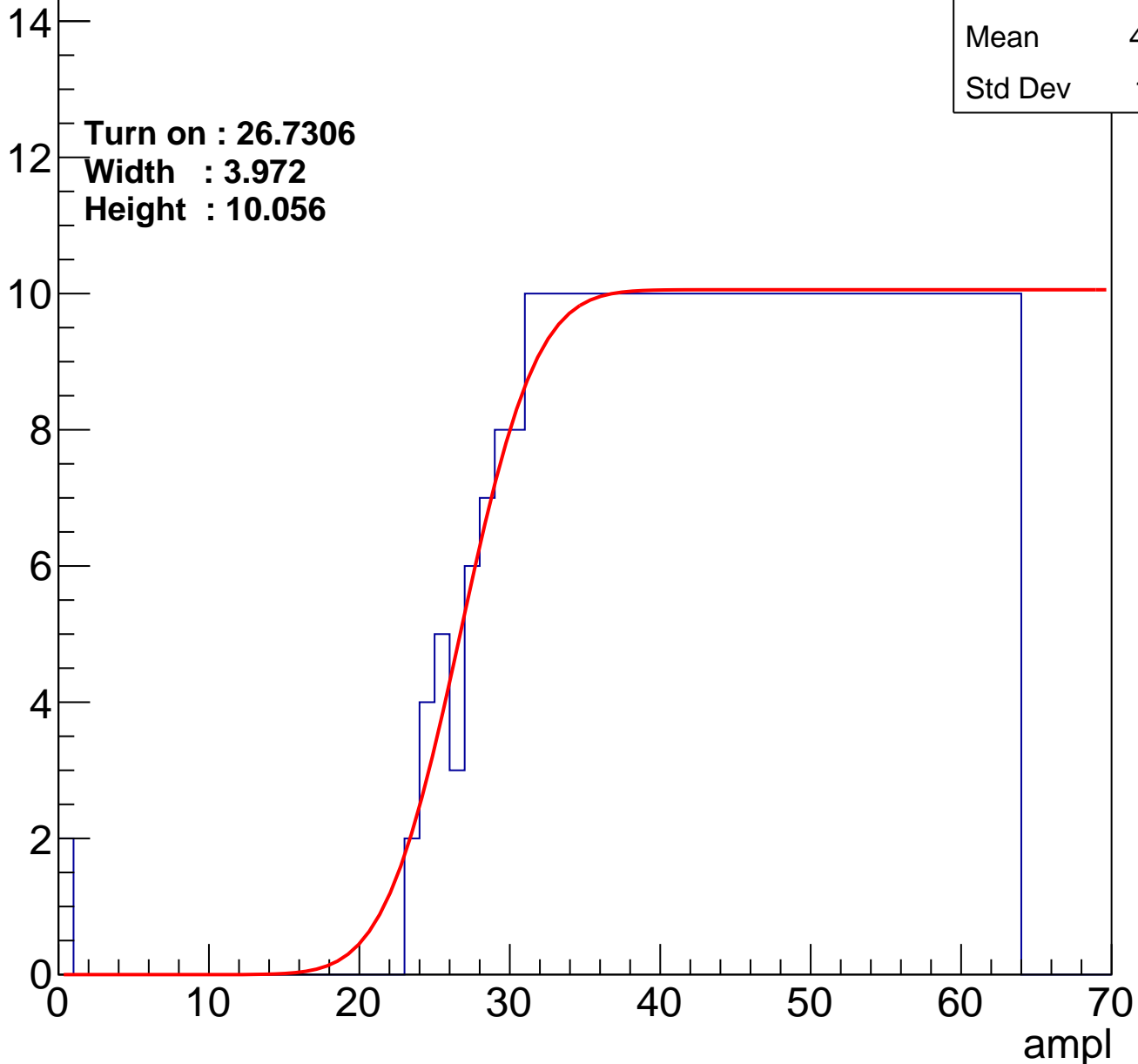
Entries	375
Mean	44.49
Std Dev	11.41

Turn on : 26.7306

Width : 3.972

Height : 10.056

Entry



B1L000S, U25-ch87

calib_packv5_042523_0143.root, FC#3, port B3

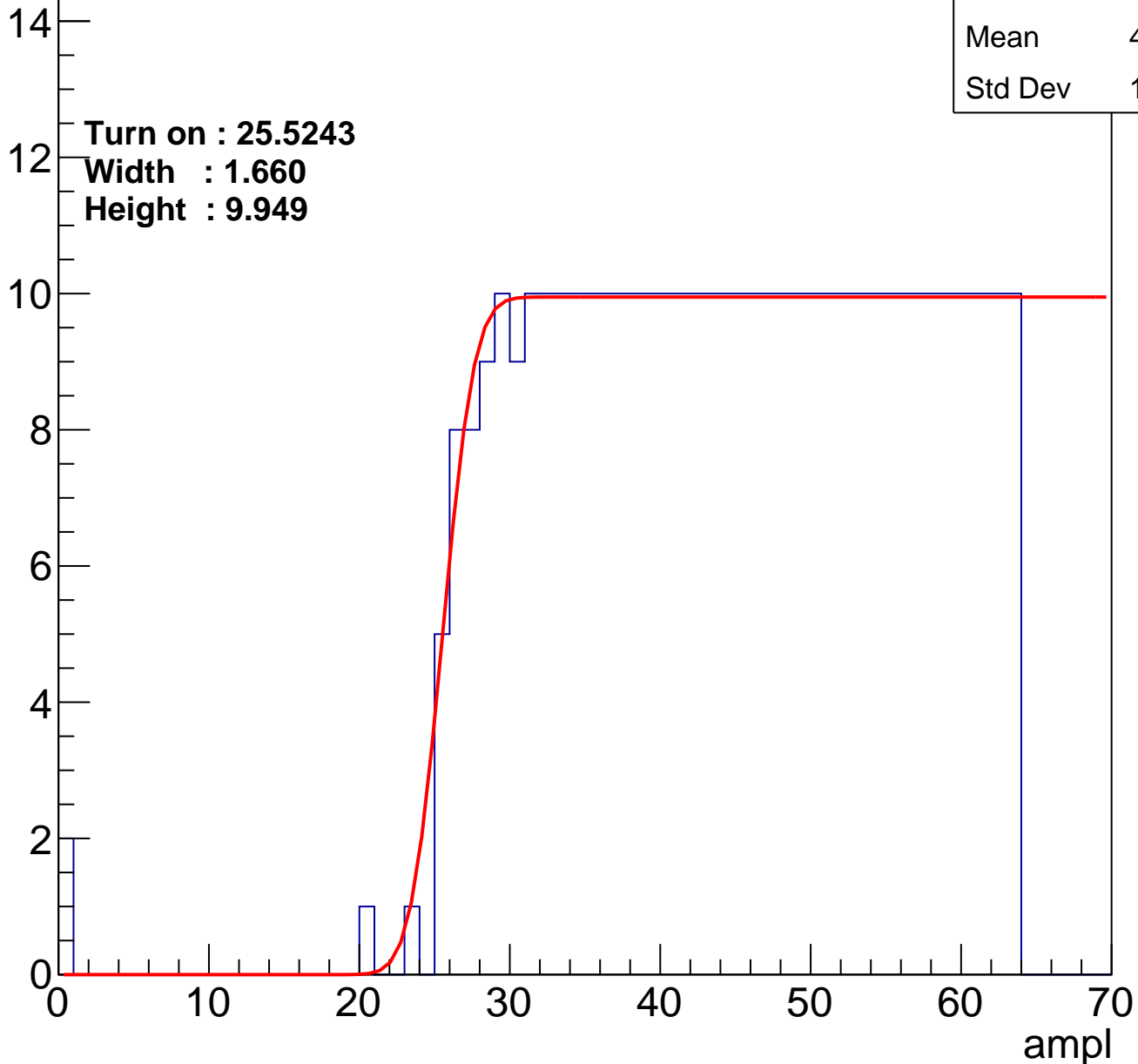
Entries	383
Mean	44.16
Std Dev	11.52

Turn on : 25.5243

Width : 1.660

Height : 9.949

Entry



B1L000S, U25-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.34
Std Dev	11.87

Turn on : 24.5598

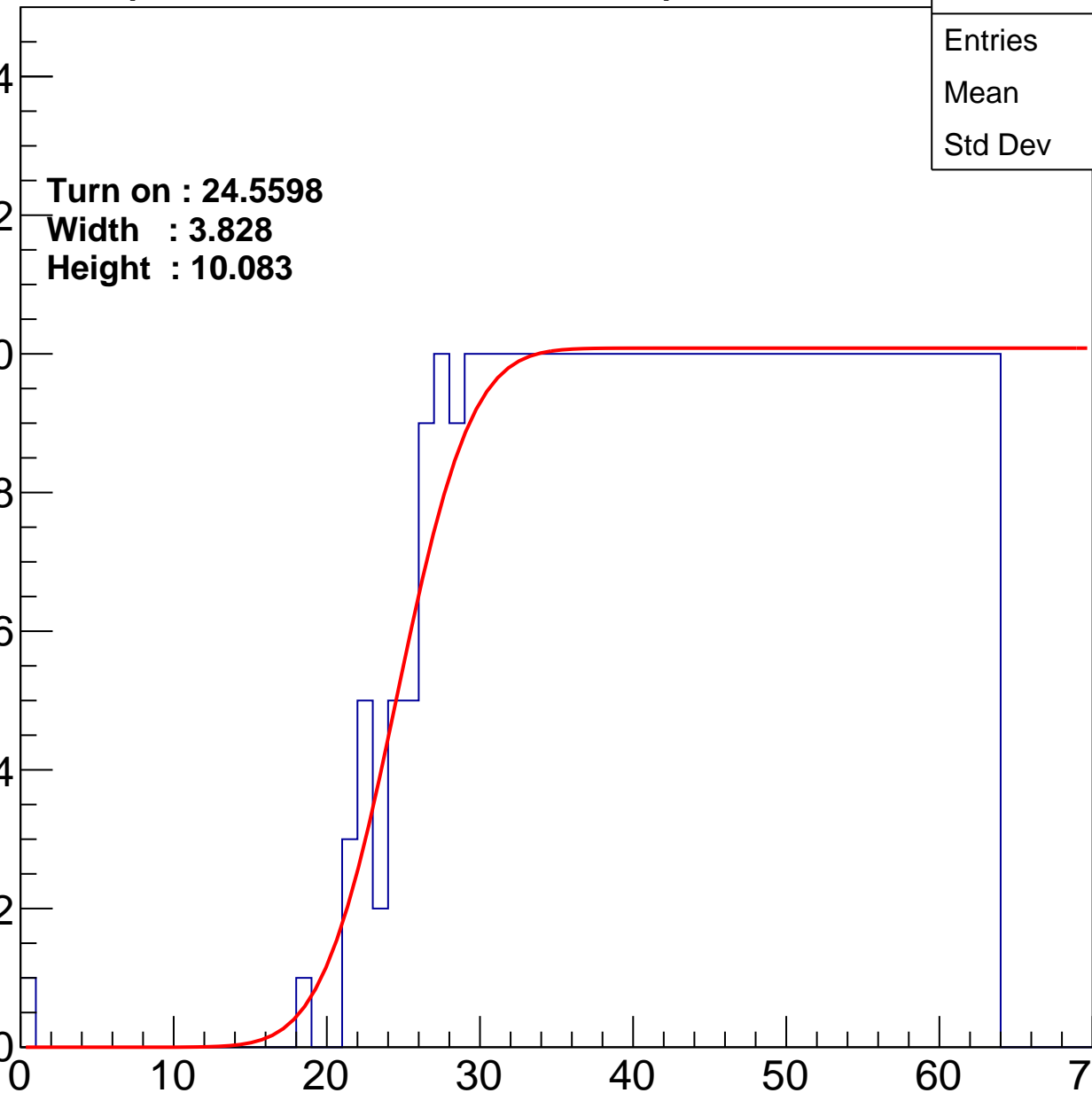
Width : 3.828

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.98
Std Dev	11.66

Turn on : 25.6766

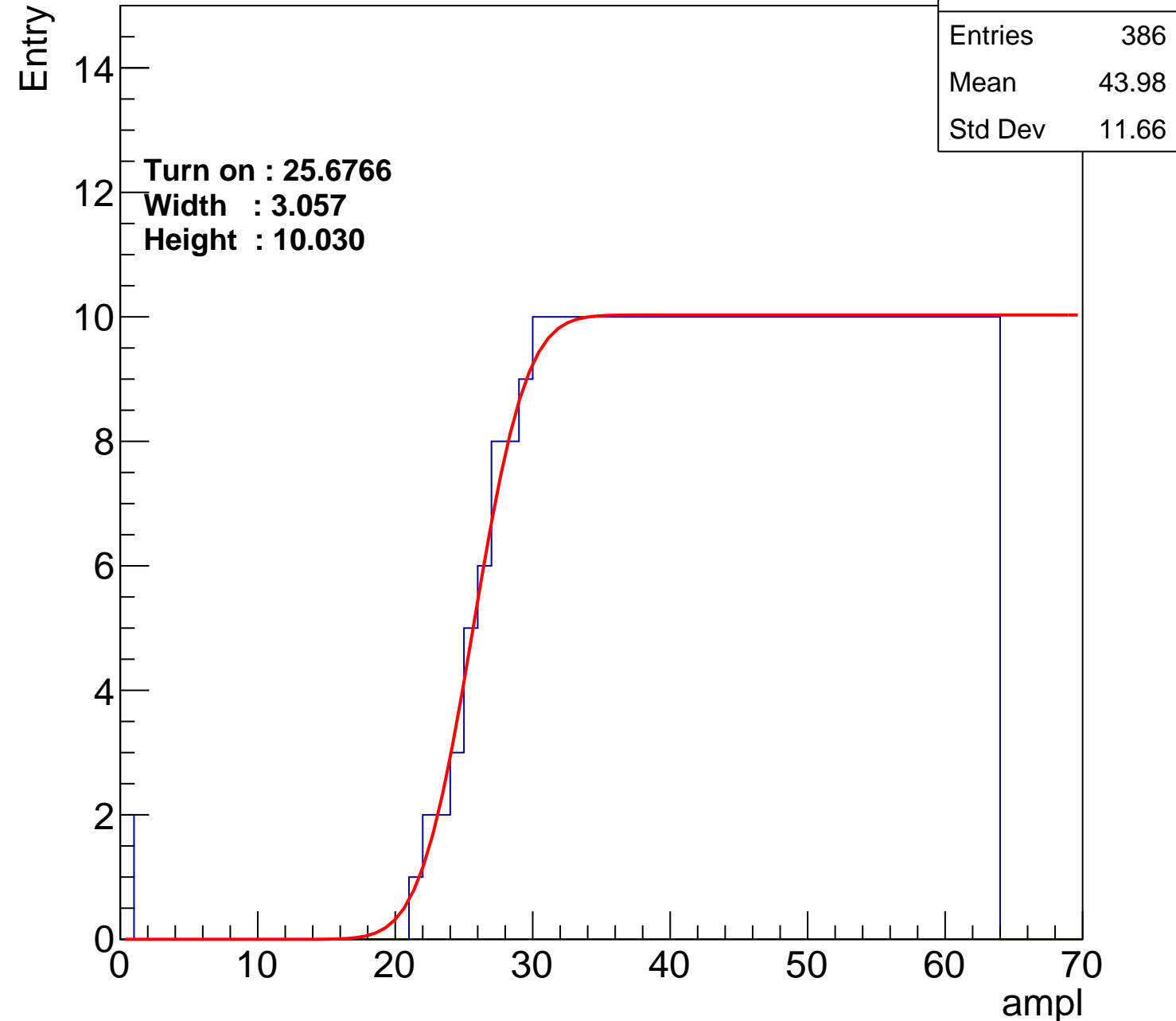
Width : 3.057

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch90

calib_packv5_042523_0143.root, FC#3, port B3

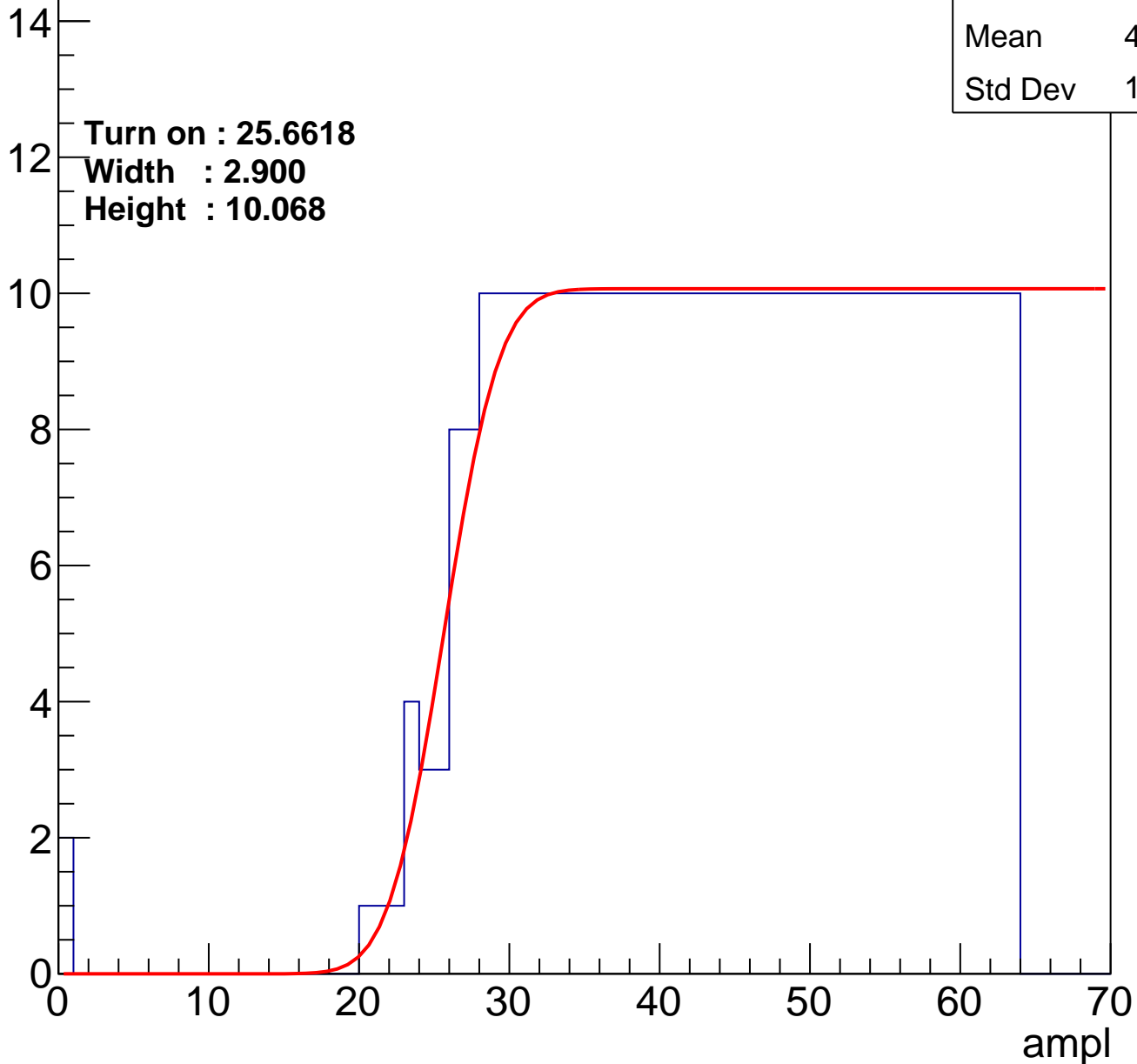
Entries	391
Mean	43.75
Std Dev	11.76

Turn on : 25.6618

Width : 2.900

Height : 10.068

Entry



B1L000S, U25-ch91

calib_packv5_042523_0143.root, FC#3, port B3

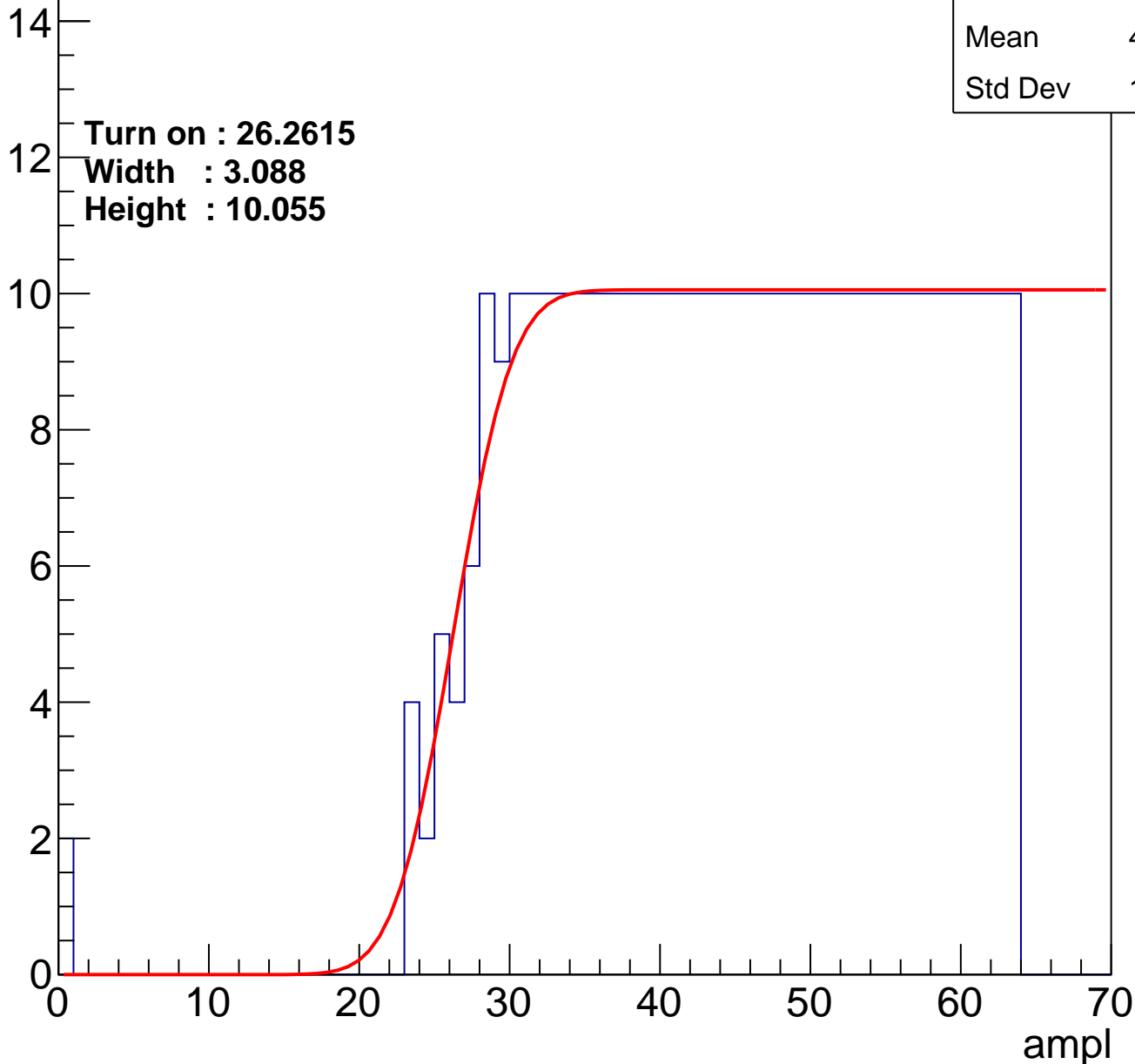
Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 26.2615

Width : 3.088

Height : 10.055

Entry



B1L000S, U25-ch92

calib_packv5_042523_0143.root, FC#3, port B3

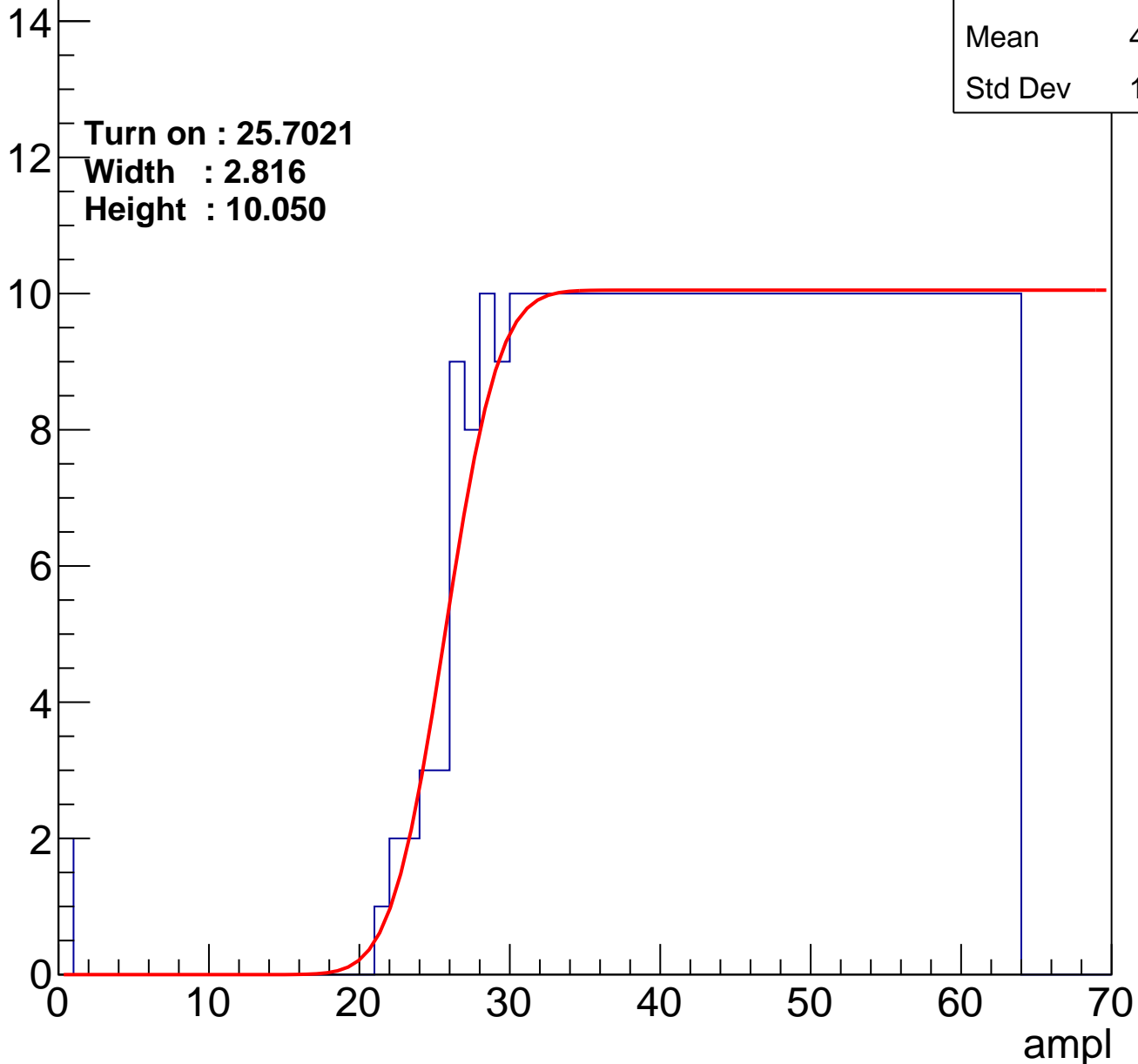
Entries	389
Mean	43.85
Std Dev	11.69

Turn on : 25.7021

Width : 2.816

Height : 10.050

Entry



B1L000S, U25-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 27.0567

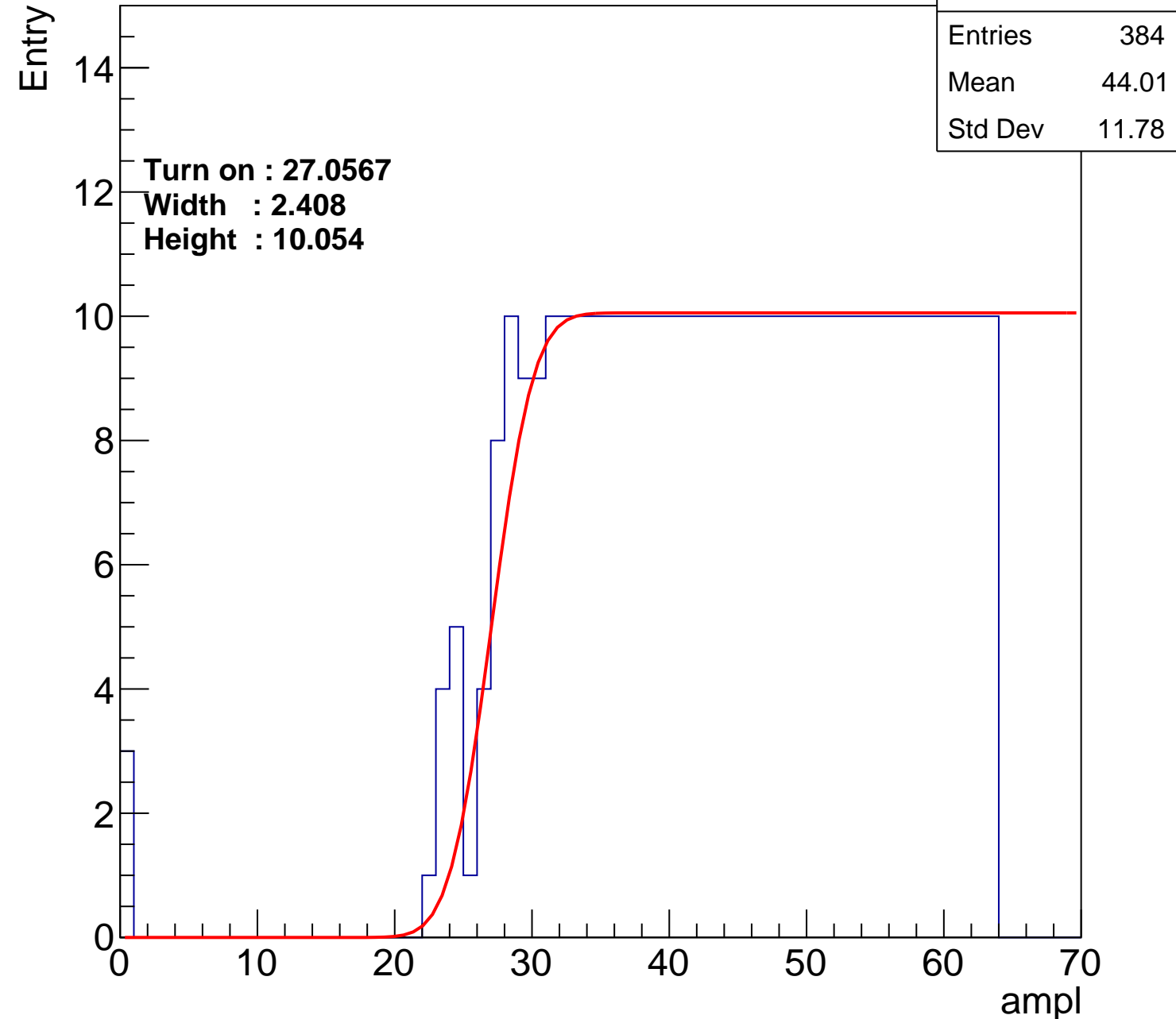
Width : 2.408

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch94

calib_packv5_042523_0143.root, FC#3, port B3

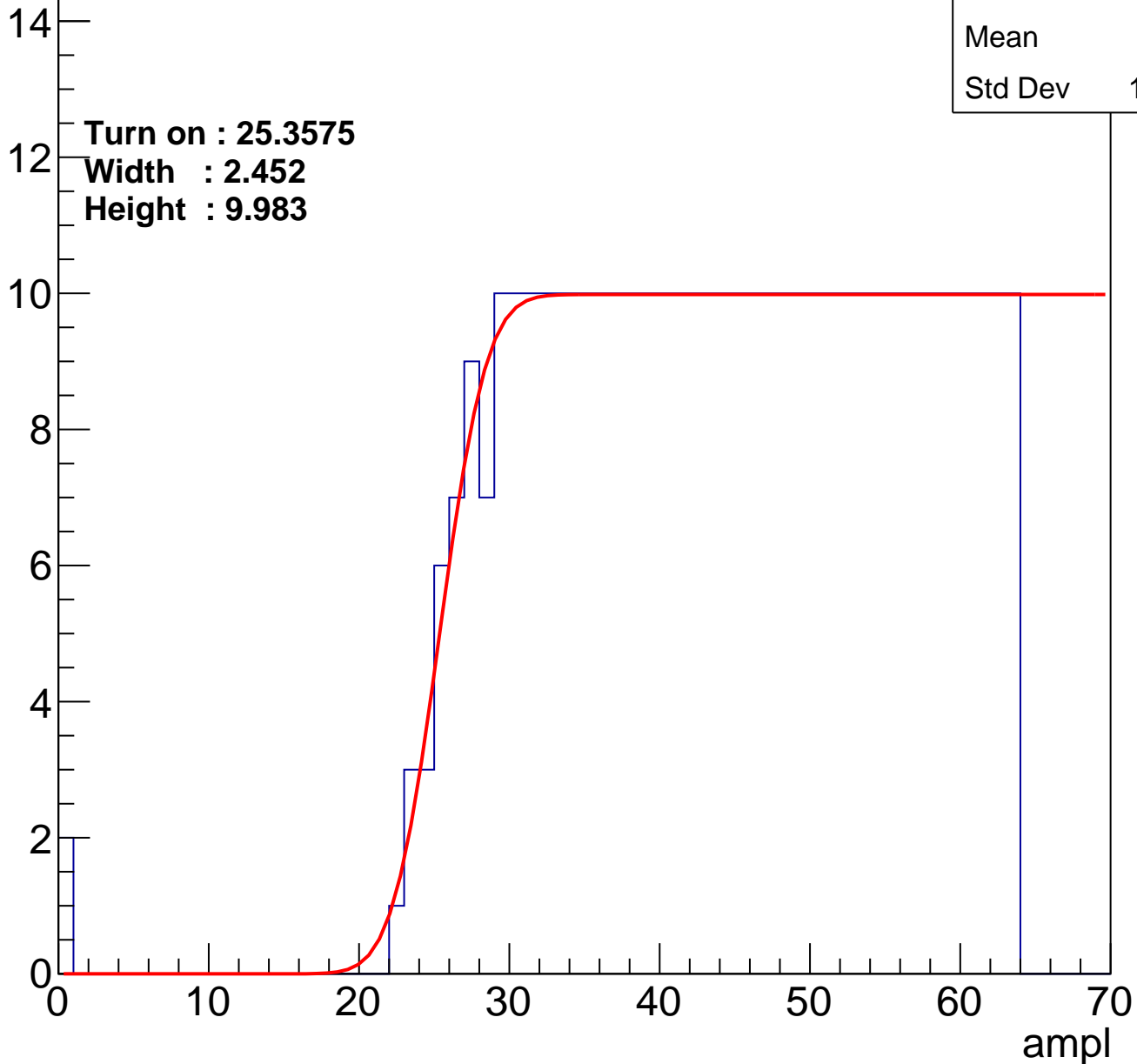
Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.3575

Width : 2.452

Height : 9.983

Entry



B1L000S, U25-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 26.1141

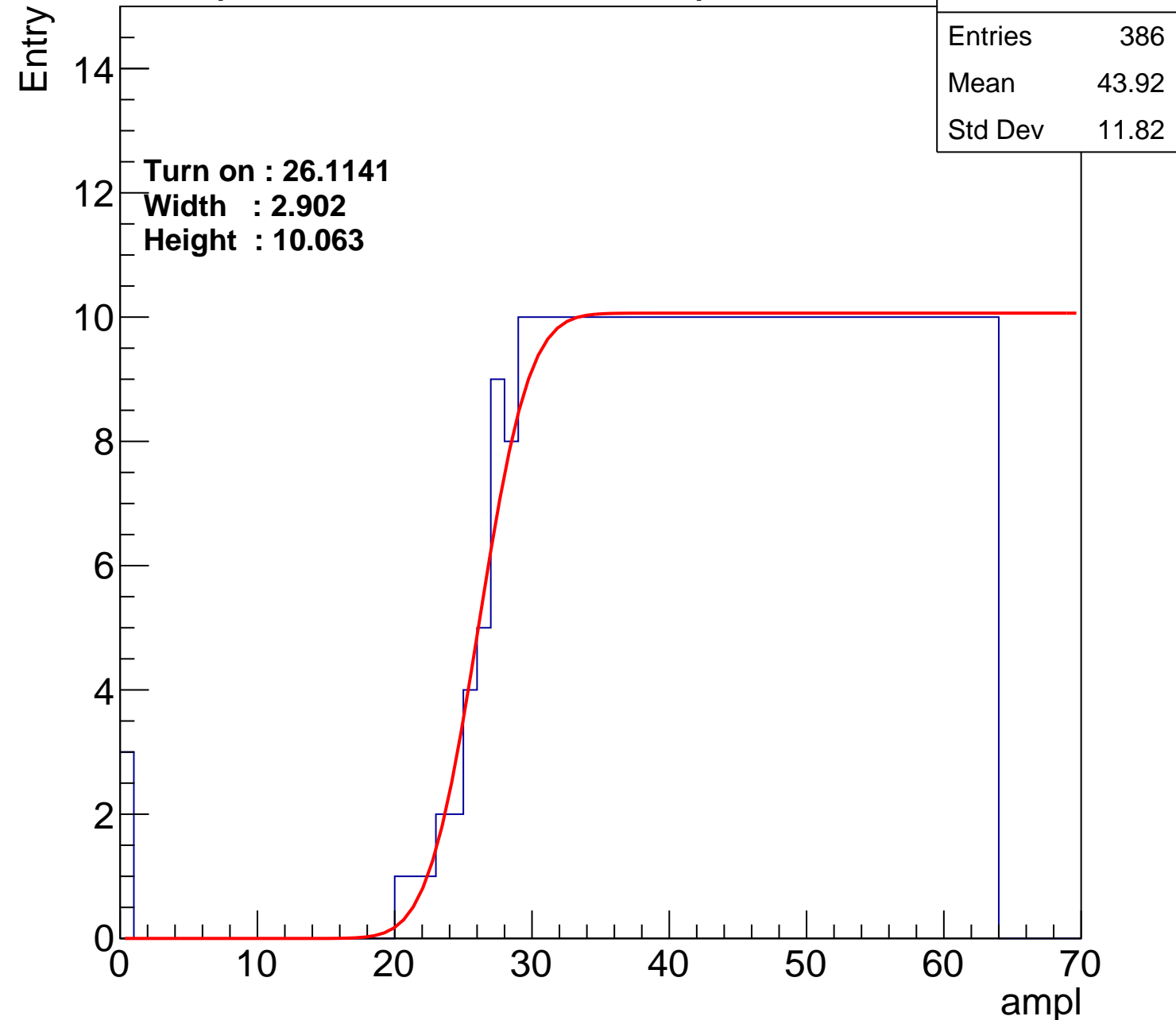
Width : 2.902

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.65
Std Dev	11.14

Turn on : 26.9984

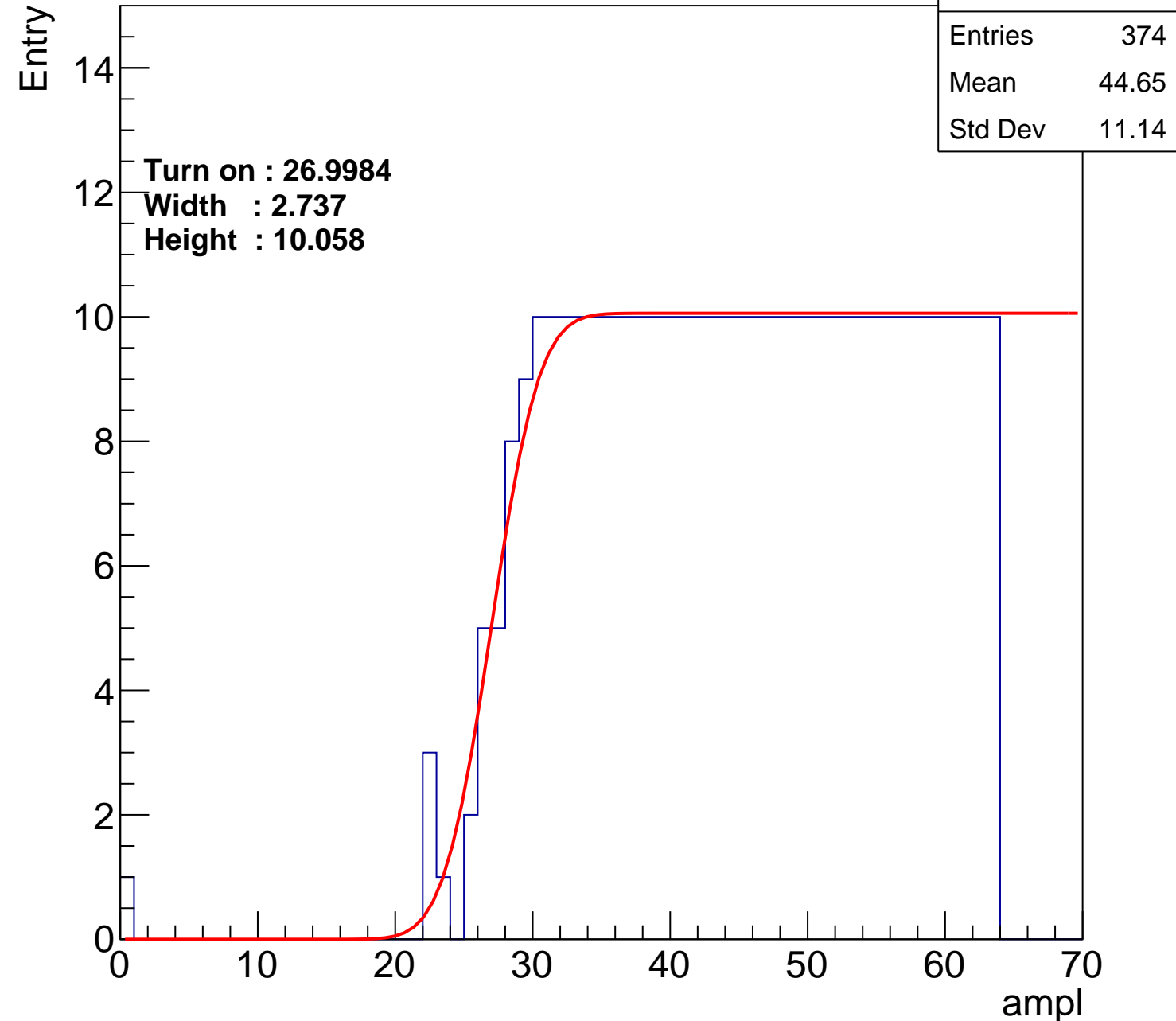
Width : 2.737

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch97

calib_packv5_042523_0143.root, FC#3, port B3

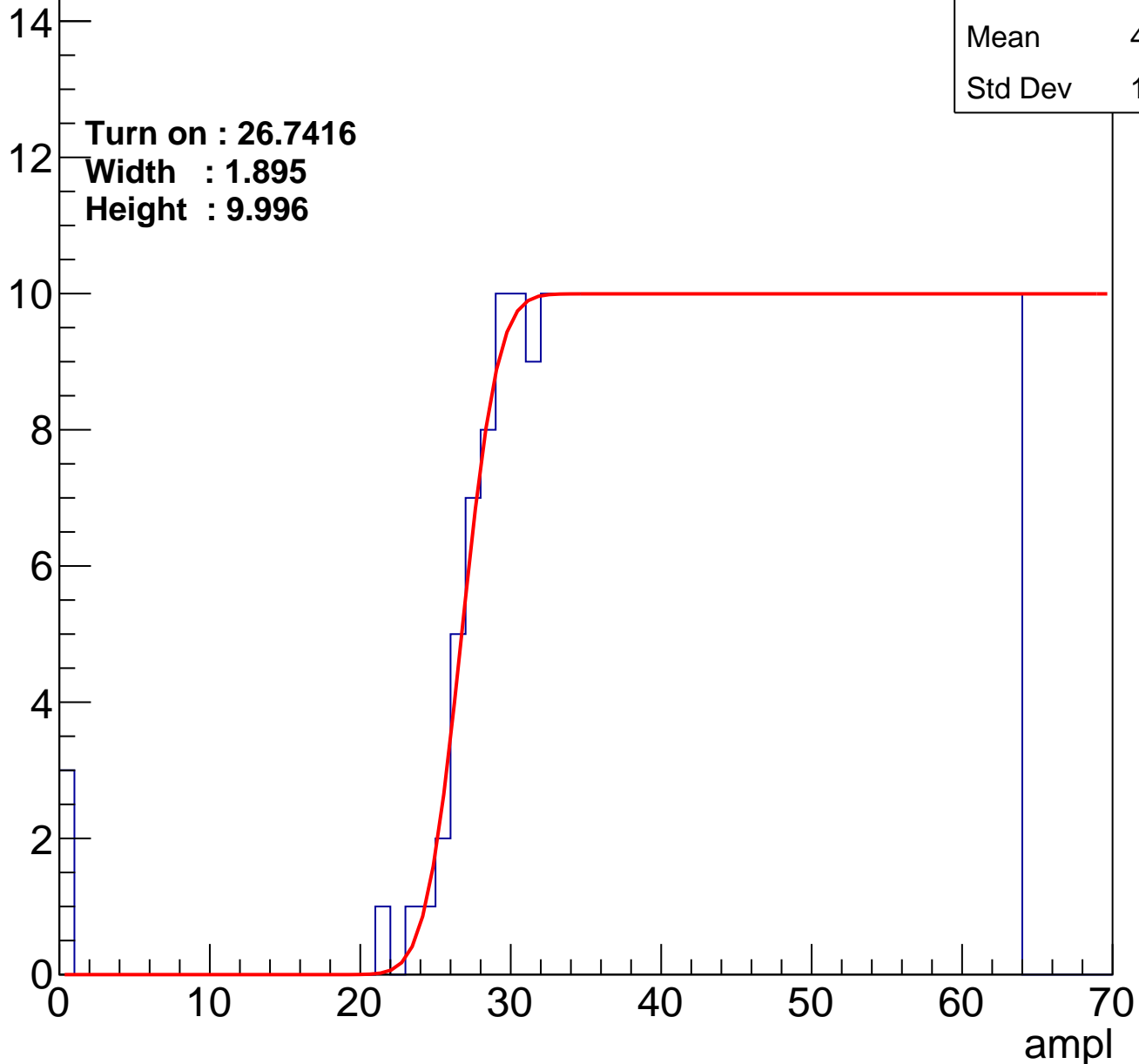
Entries	377
Mean	44.38
Std Dev	11.57

Turn on : 26.7416

Width : 1.895

Height : 9.996

Entry



B1L000S, U25-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.73
Std Dev	11.79

Turn on : 25.2778

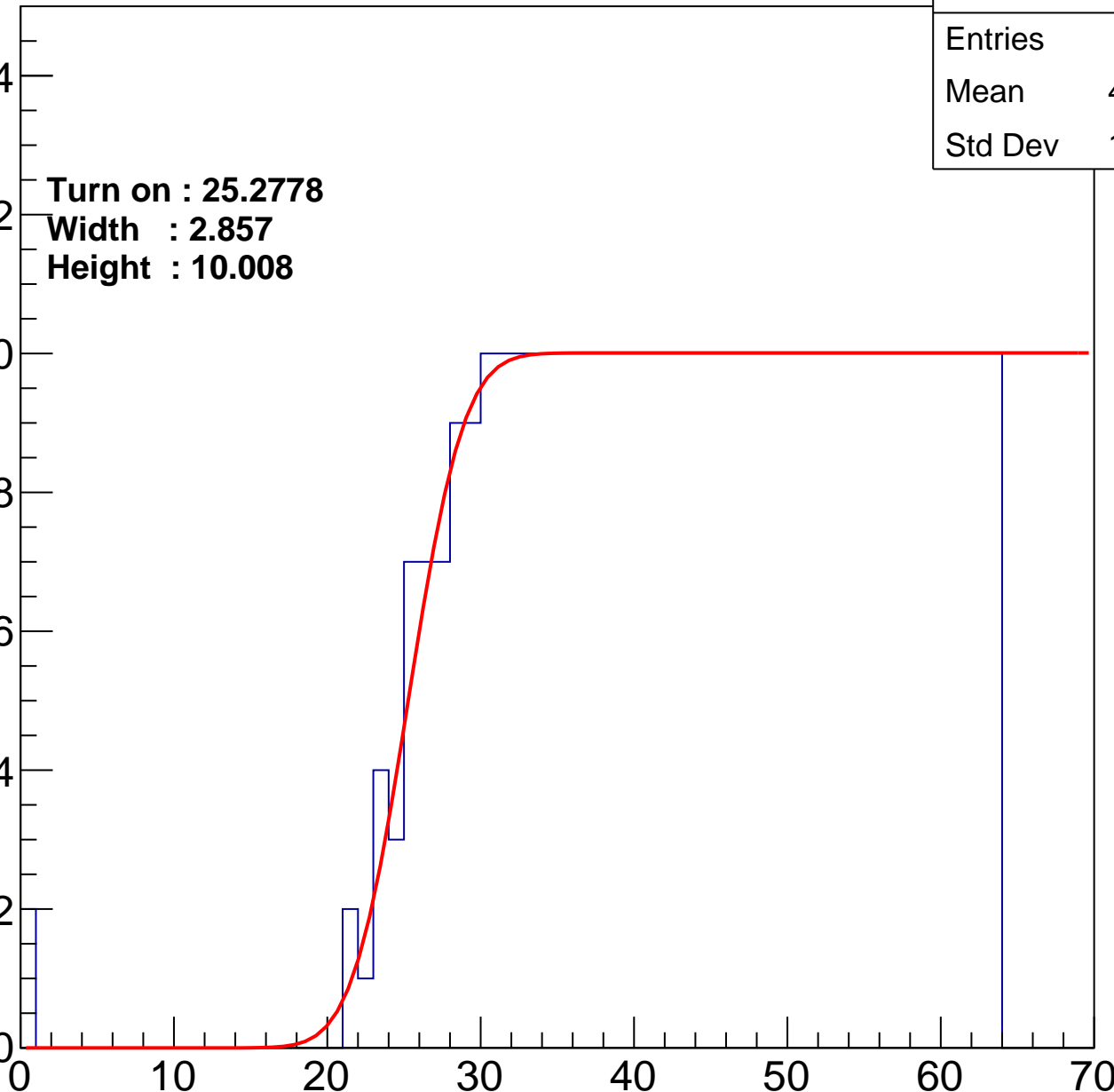
Width : 2.857

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.67
Std Dev	11.94

Turn on : 25.4620

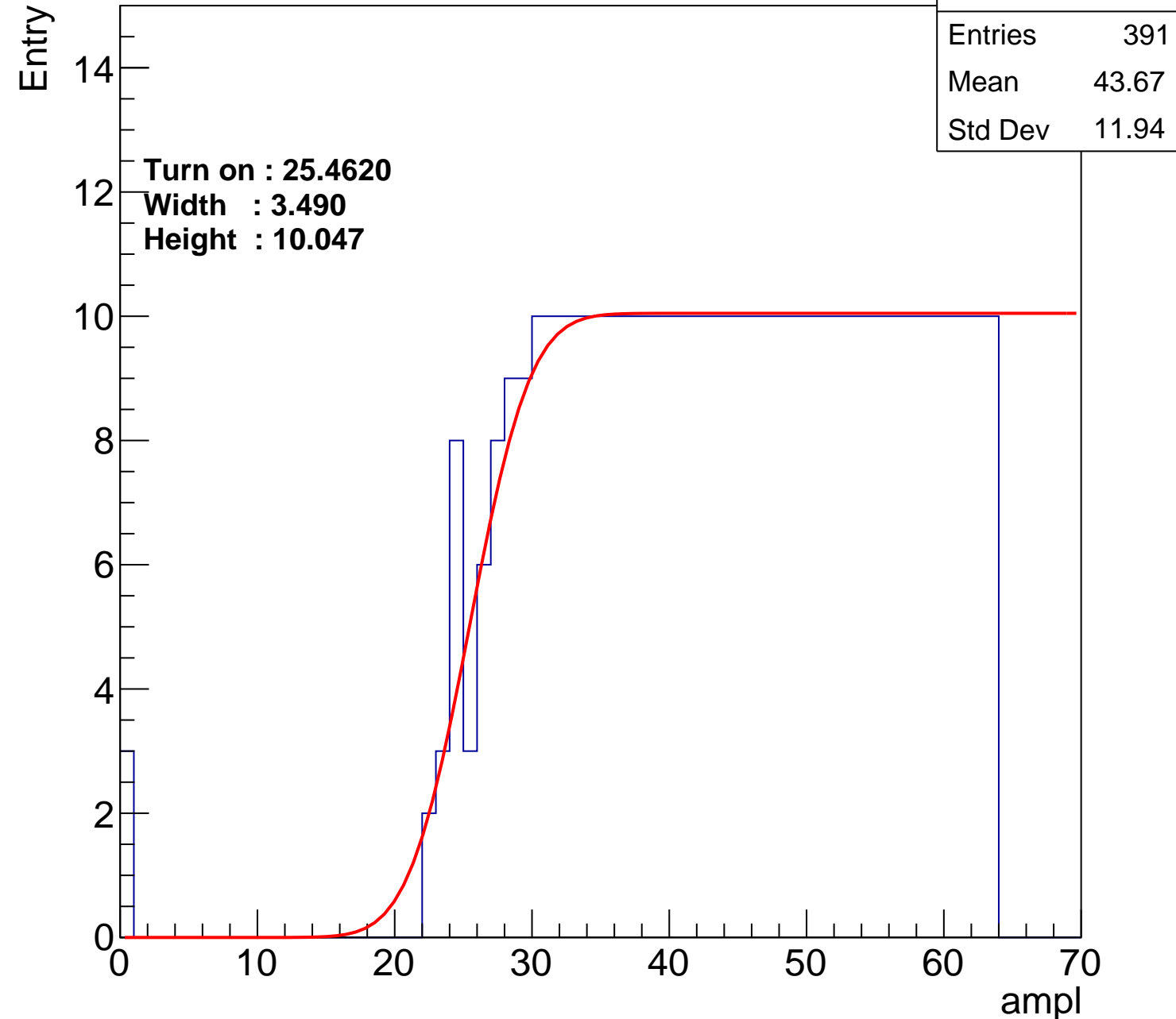
Width : 3.490

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.69
Std Dev	11.3

Turn on : 27.2097

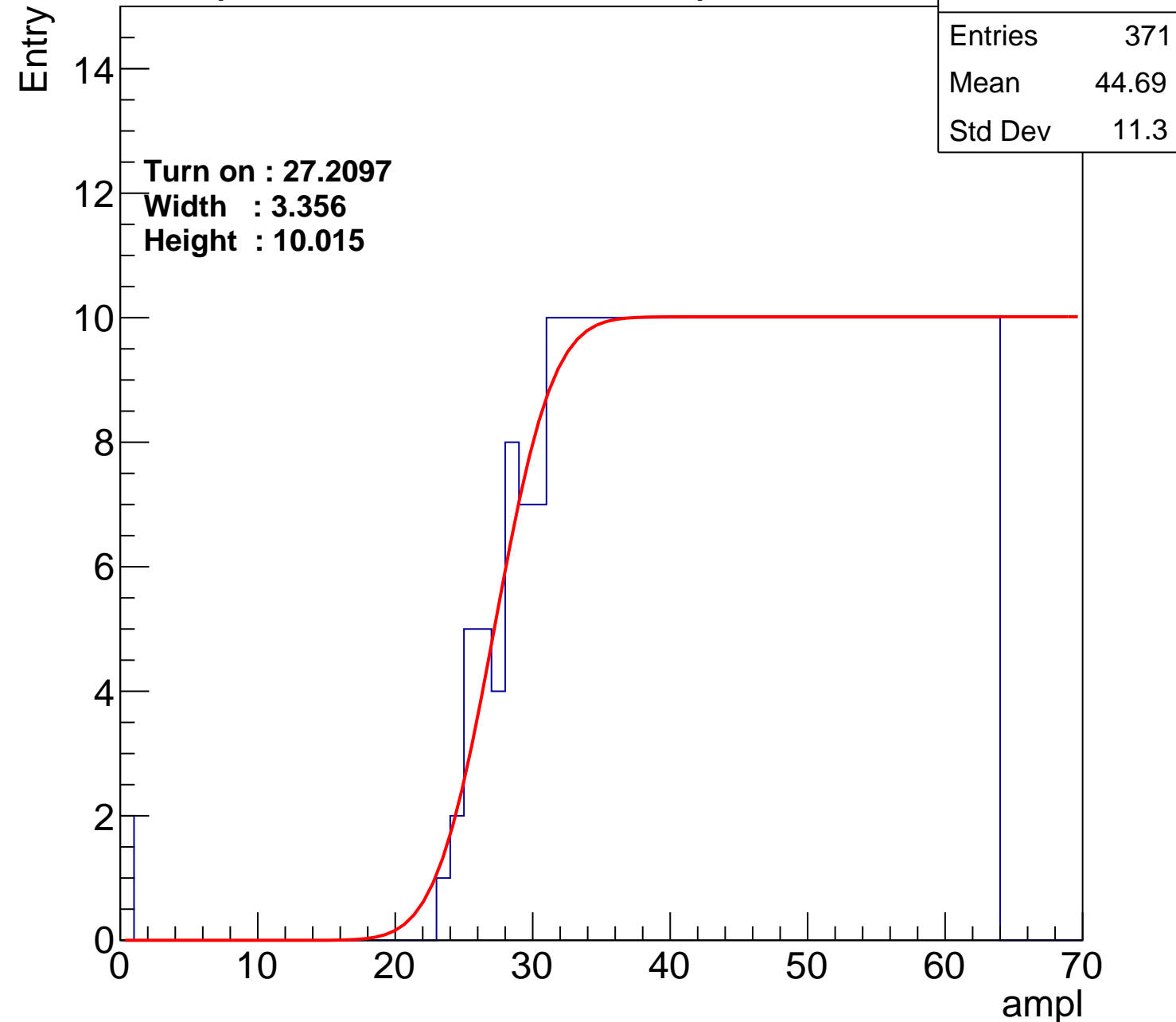
Width : 3.356

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.73
Std Dev	11.17

Turn on : 27.8454

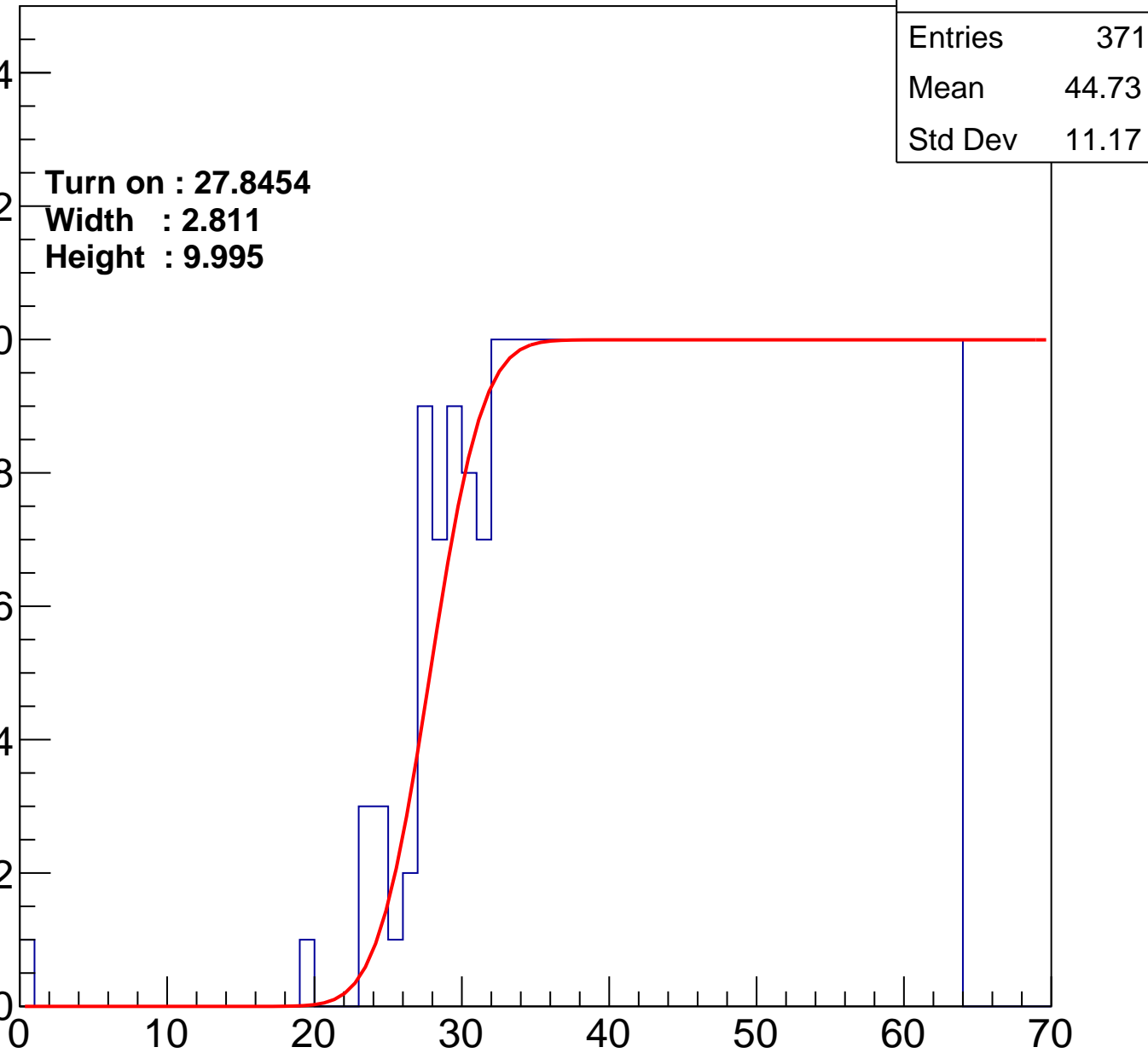
Width : 2.811

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch102

calib_packv5_042523_0143.root, FC#3, port B3

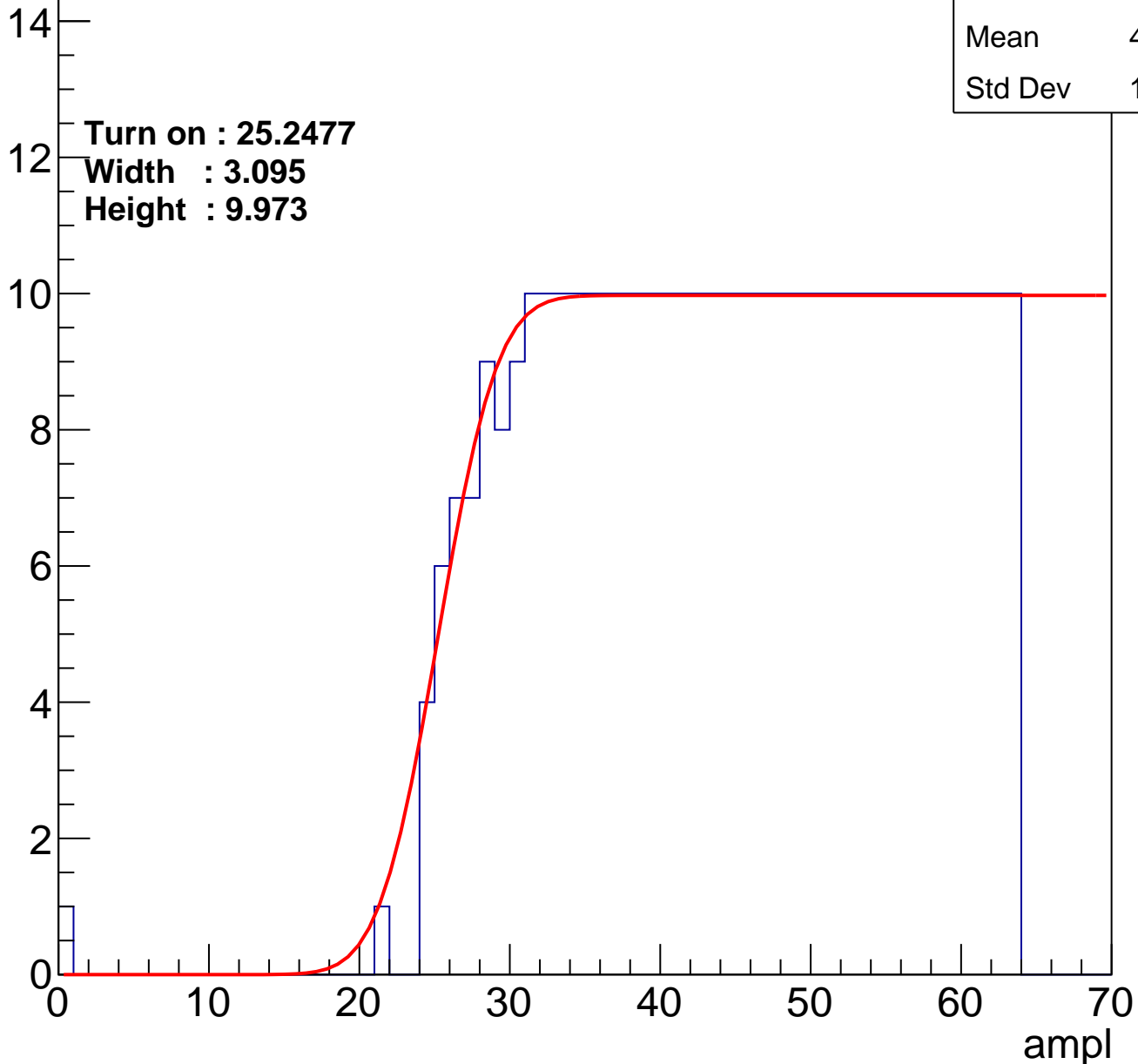
Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 25.2477

Width : 3.095

Height : 9.973

Entry



B1L000S, U25-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 26.7457

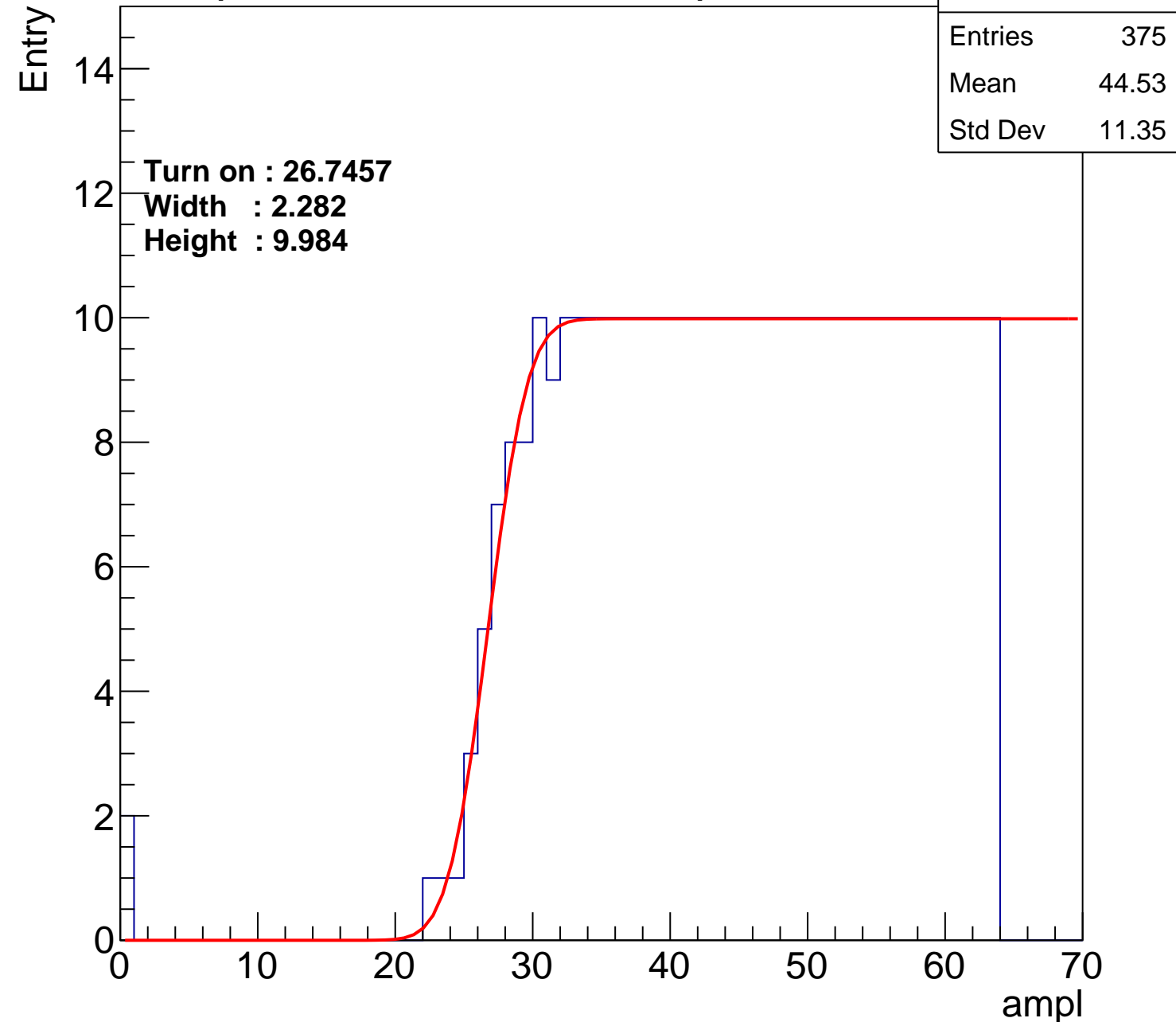
Width : 2.282

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.83
Std Dev	11.59

Turn on : 28.4818

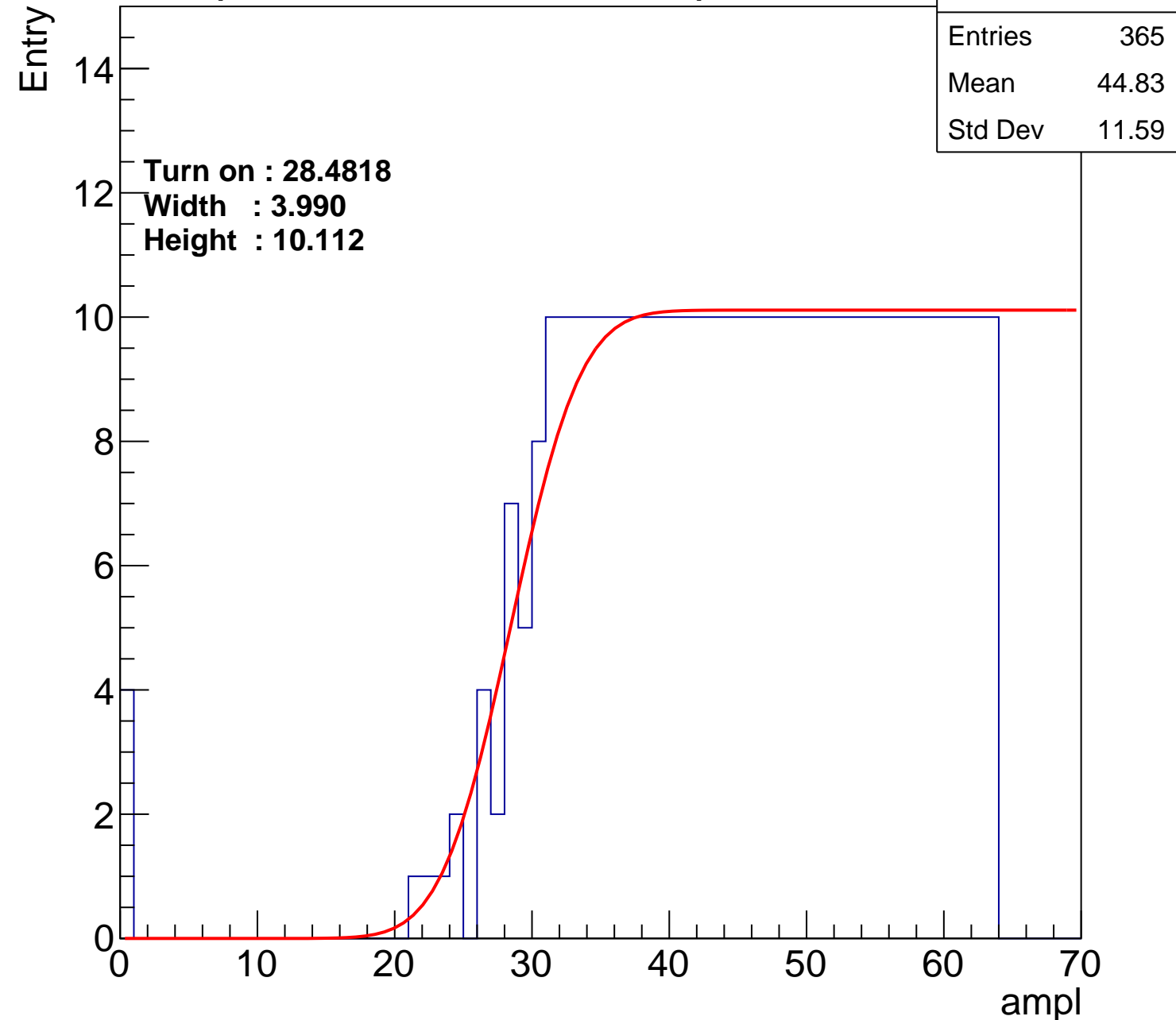
Width : 3.990

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 27.4605

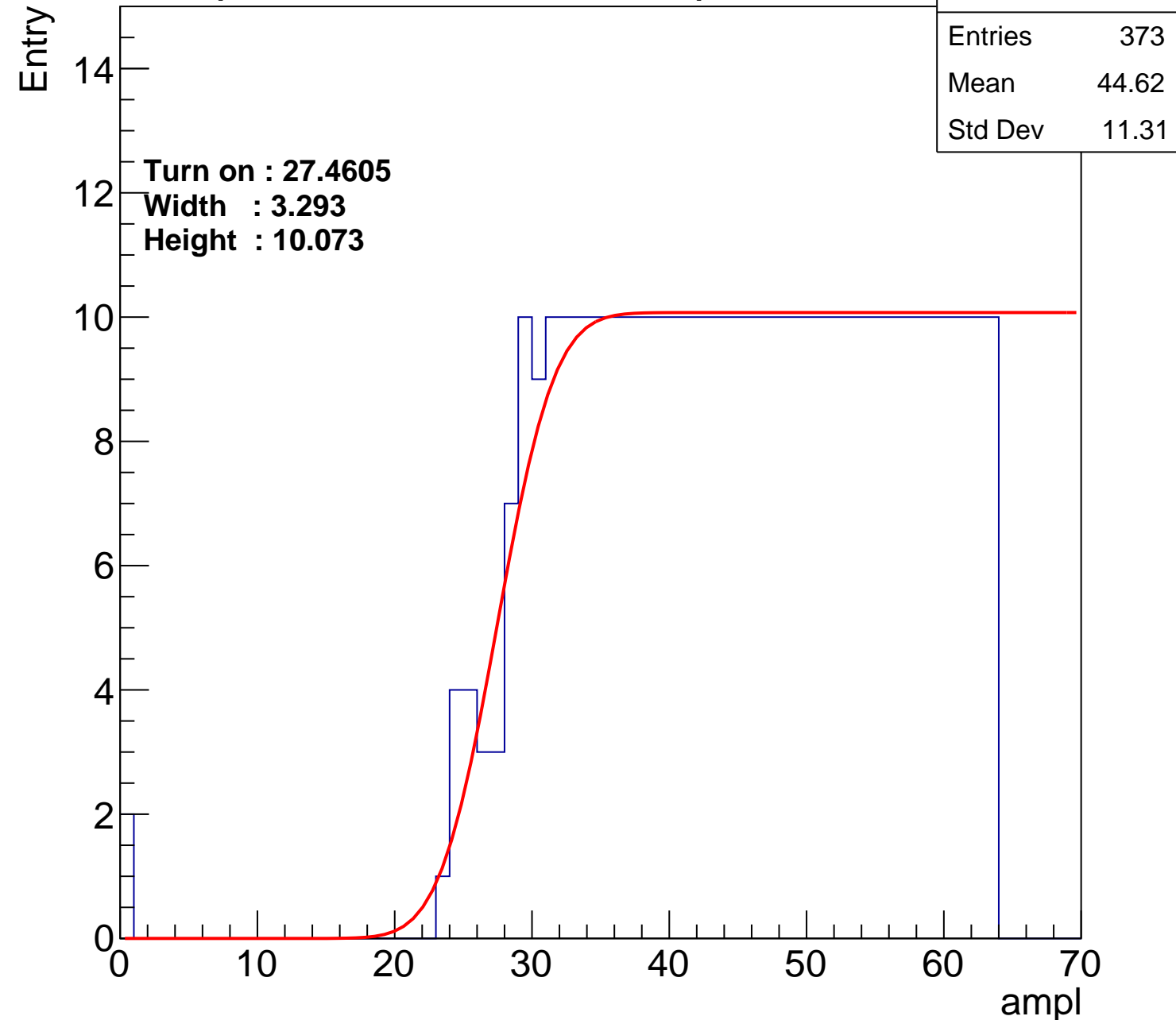
Width : 3.293

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 24.9920

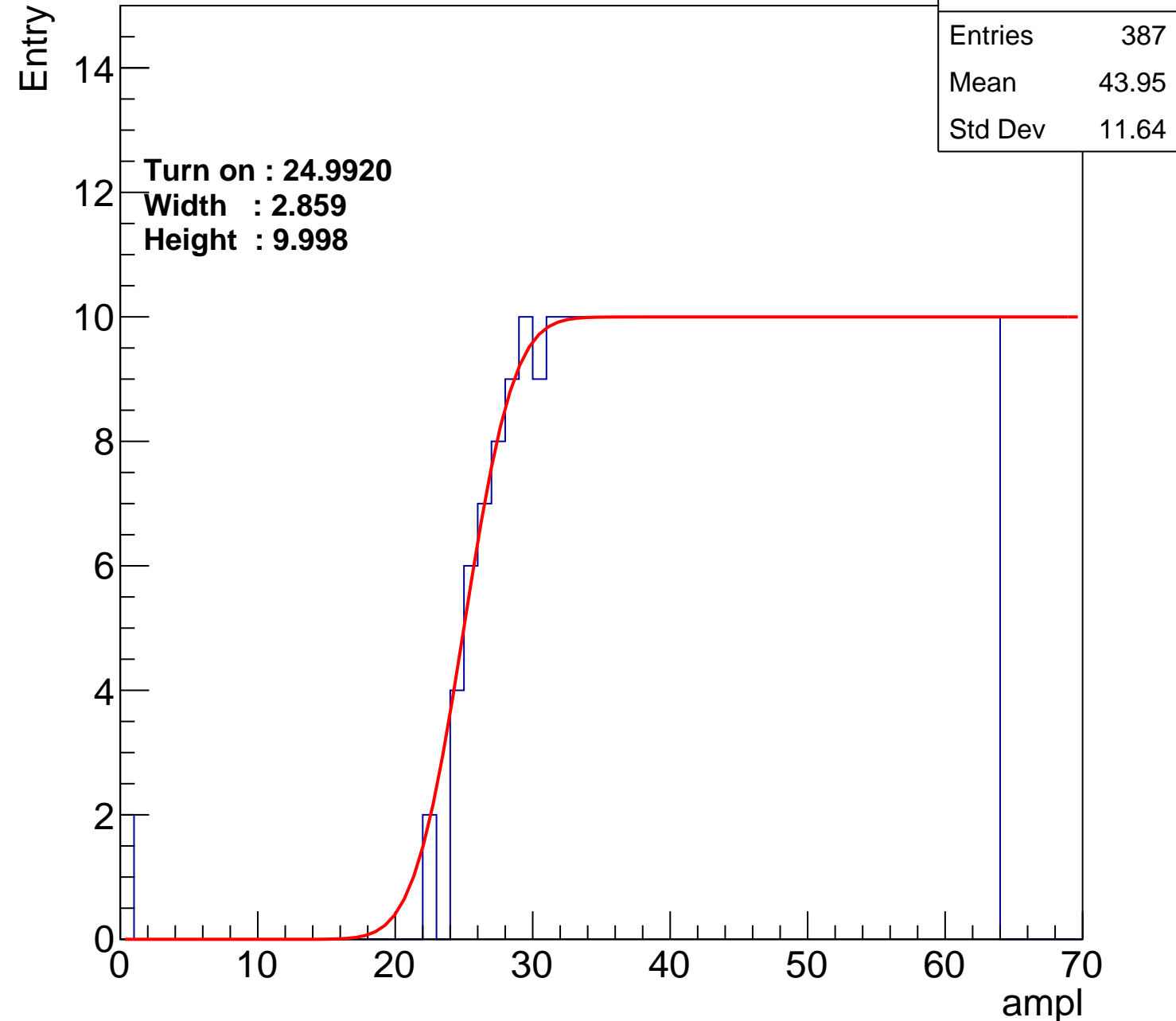
Width : 2.859

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 28.0725

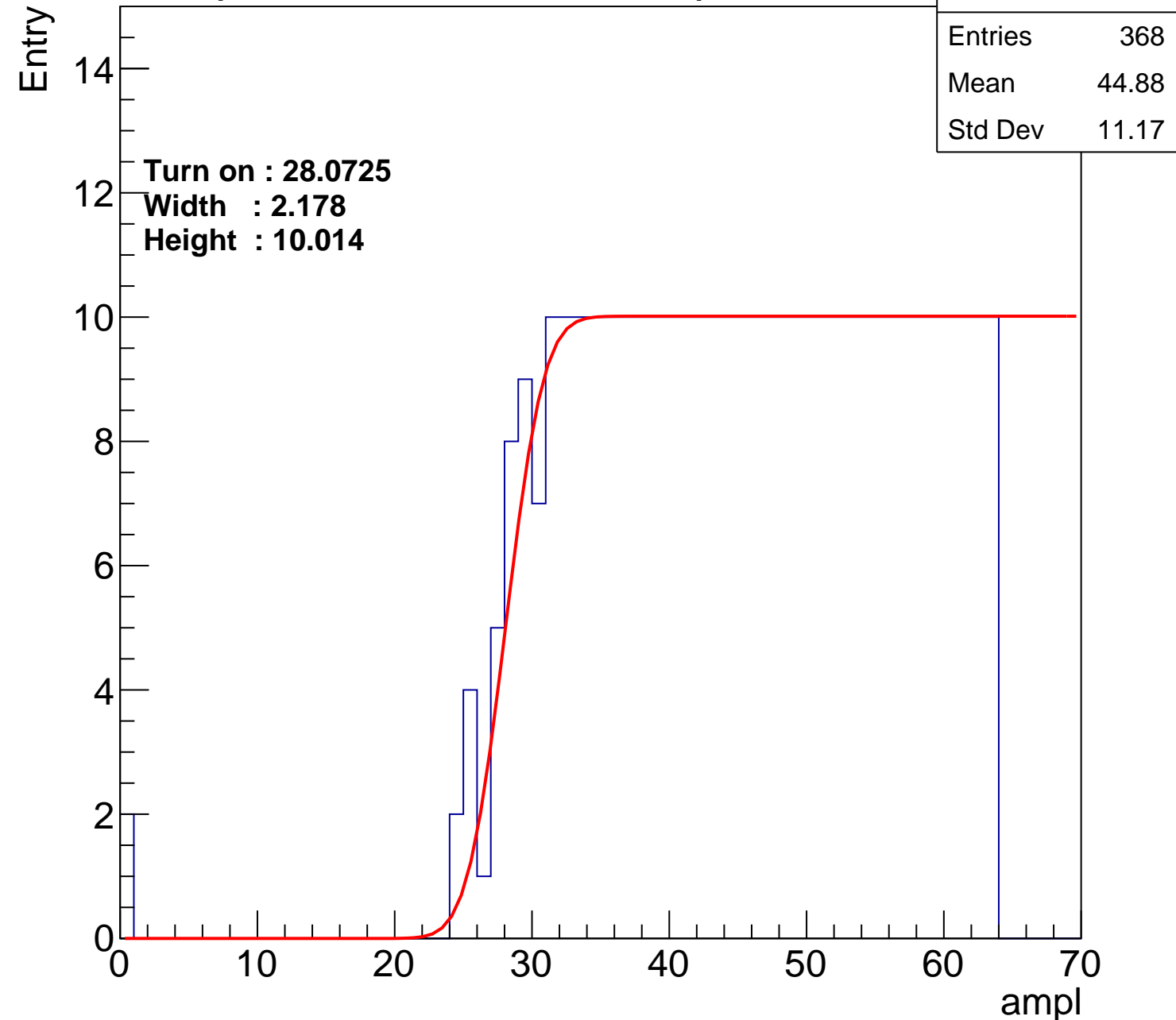
Width : 2.178

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.58
Std Dev	11.38

Turn on : 27.1962

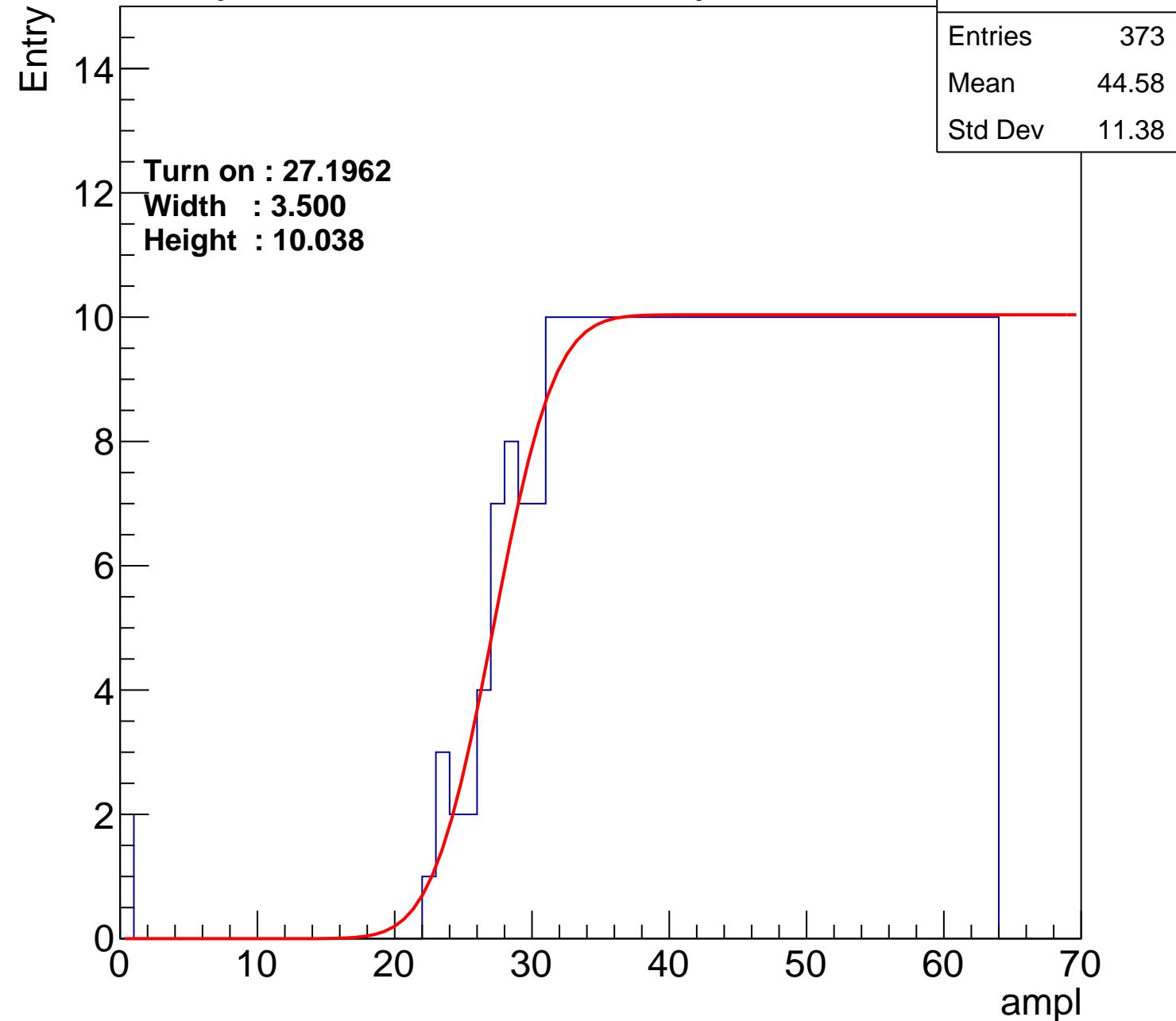
Width : 3.500

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 26.0984

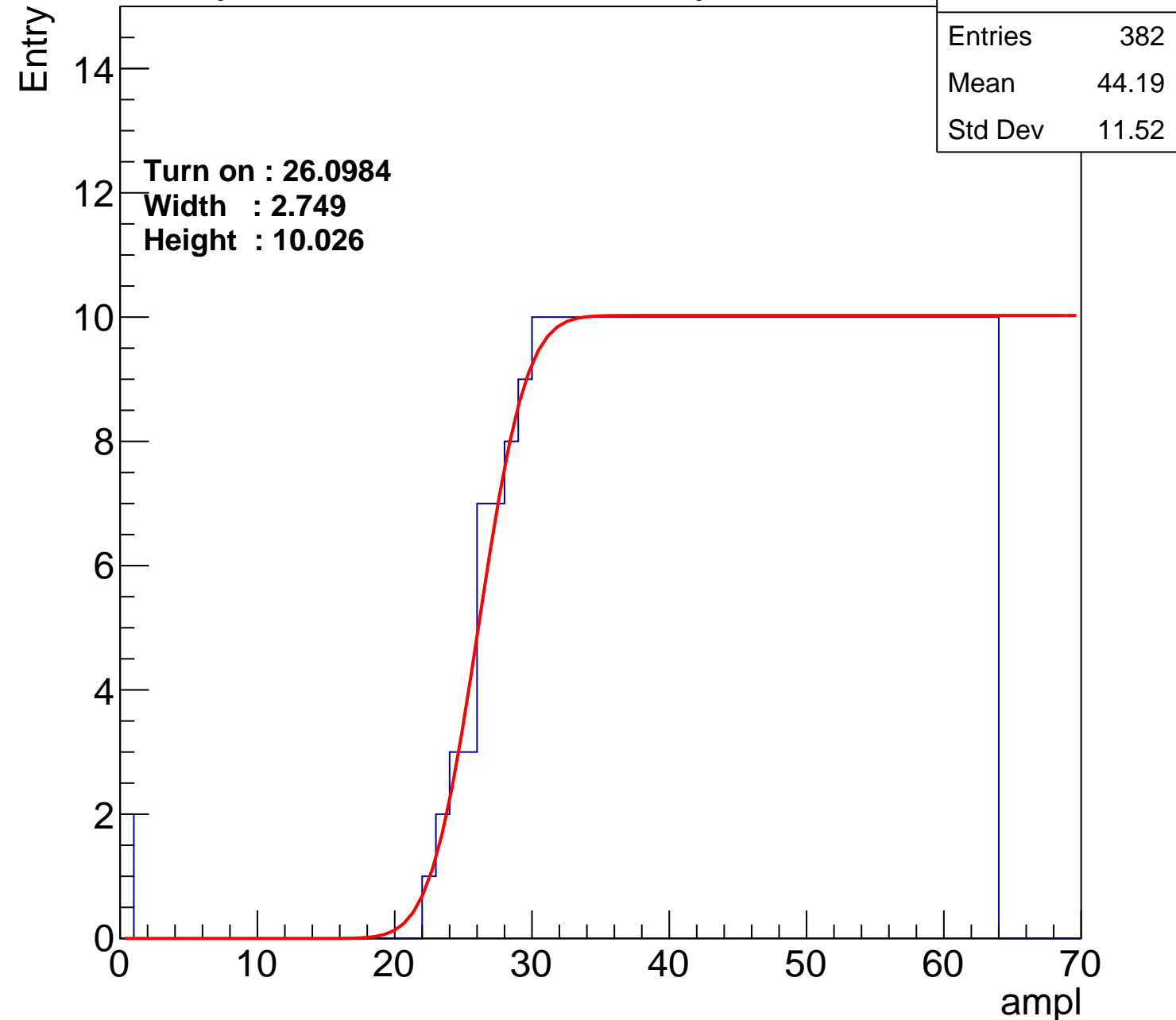
Width : 2.749

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch110

calib_packv5_042523_0143.root, FC#3, port B3

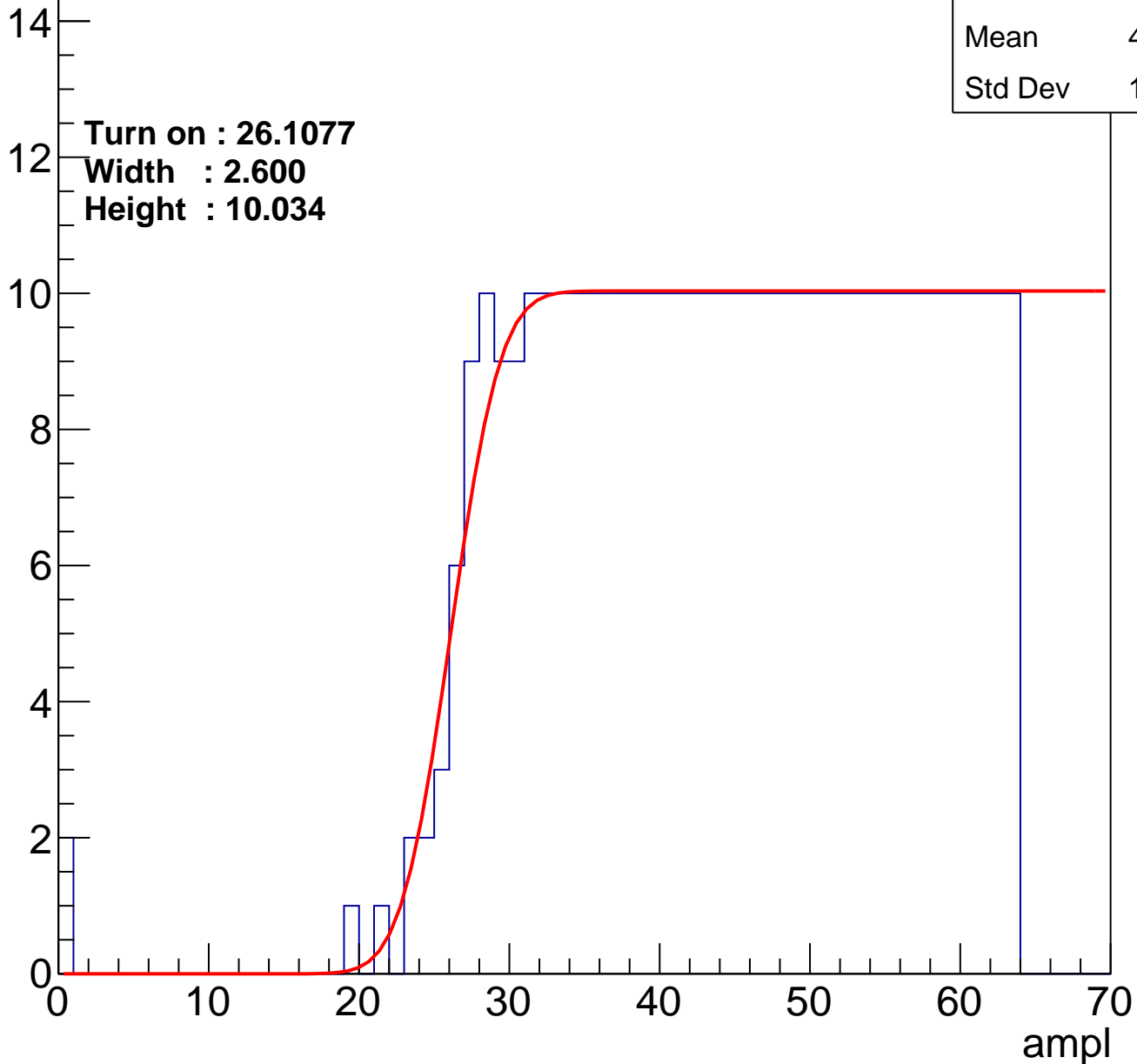
Entries	384
Mean	44.09
Std Dev	11.59

Turn on : 26.1077

Width : 2.600

Height : 10.034

Entry



B1L000S, U25-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 26.0077

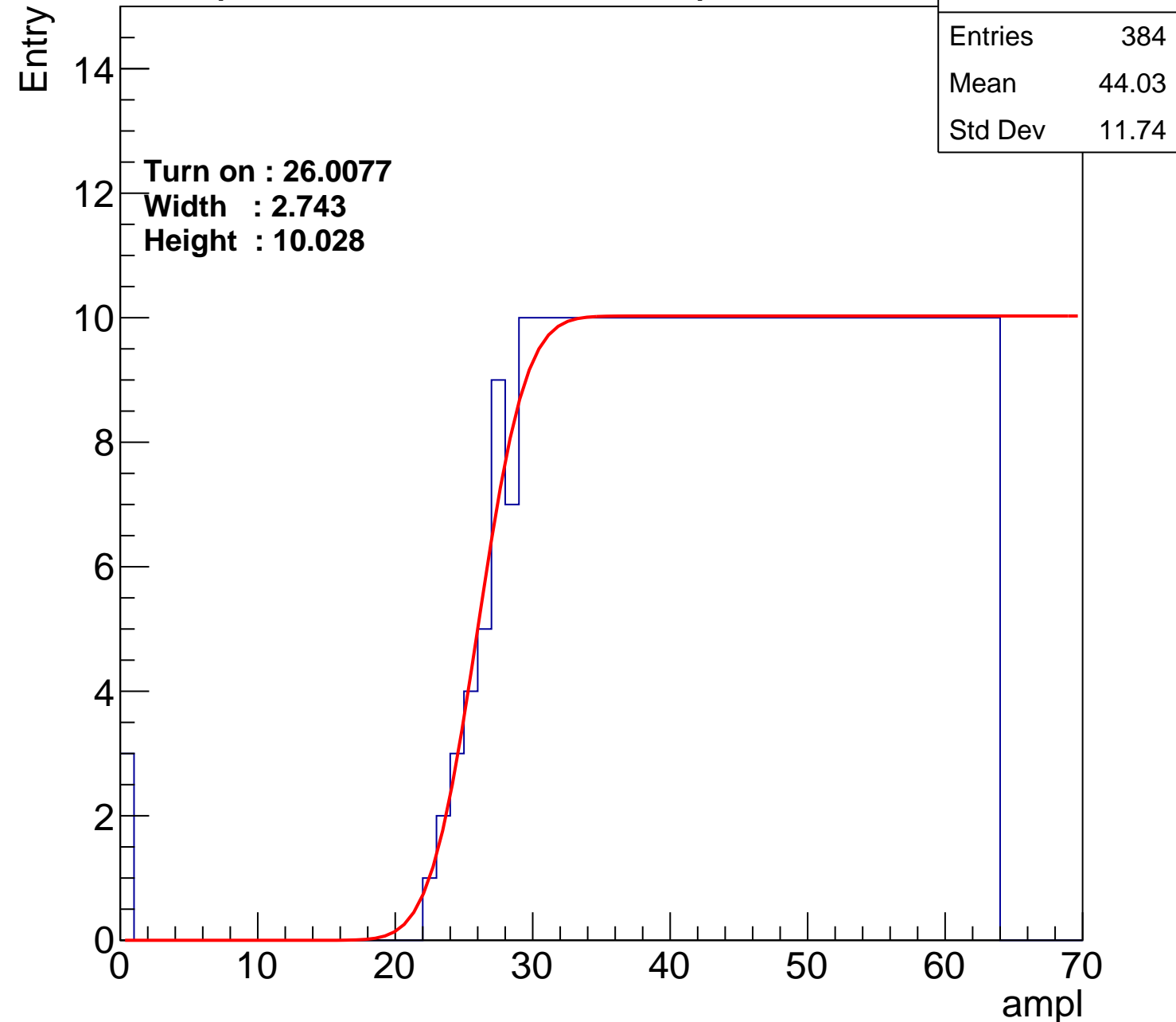
Width : 2.743

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.6196

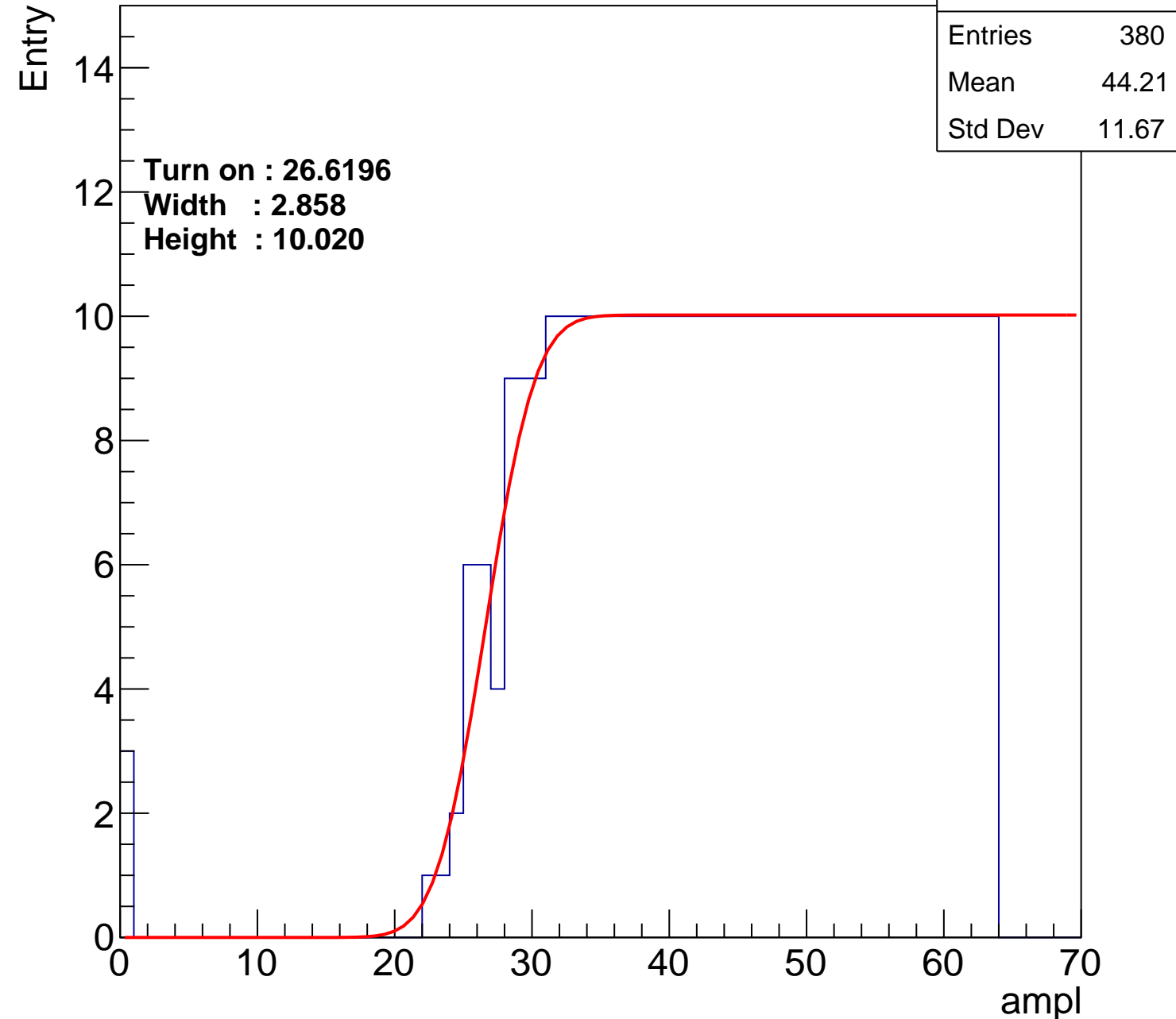
Width : 2.858

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.49
Std Dev	11.74

Turn on : 24.2429

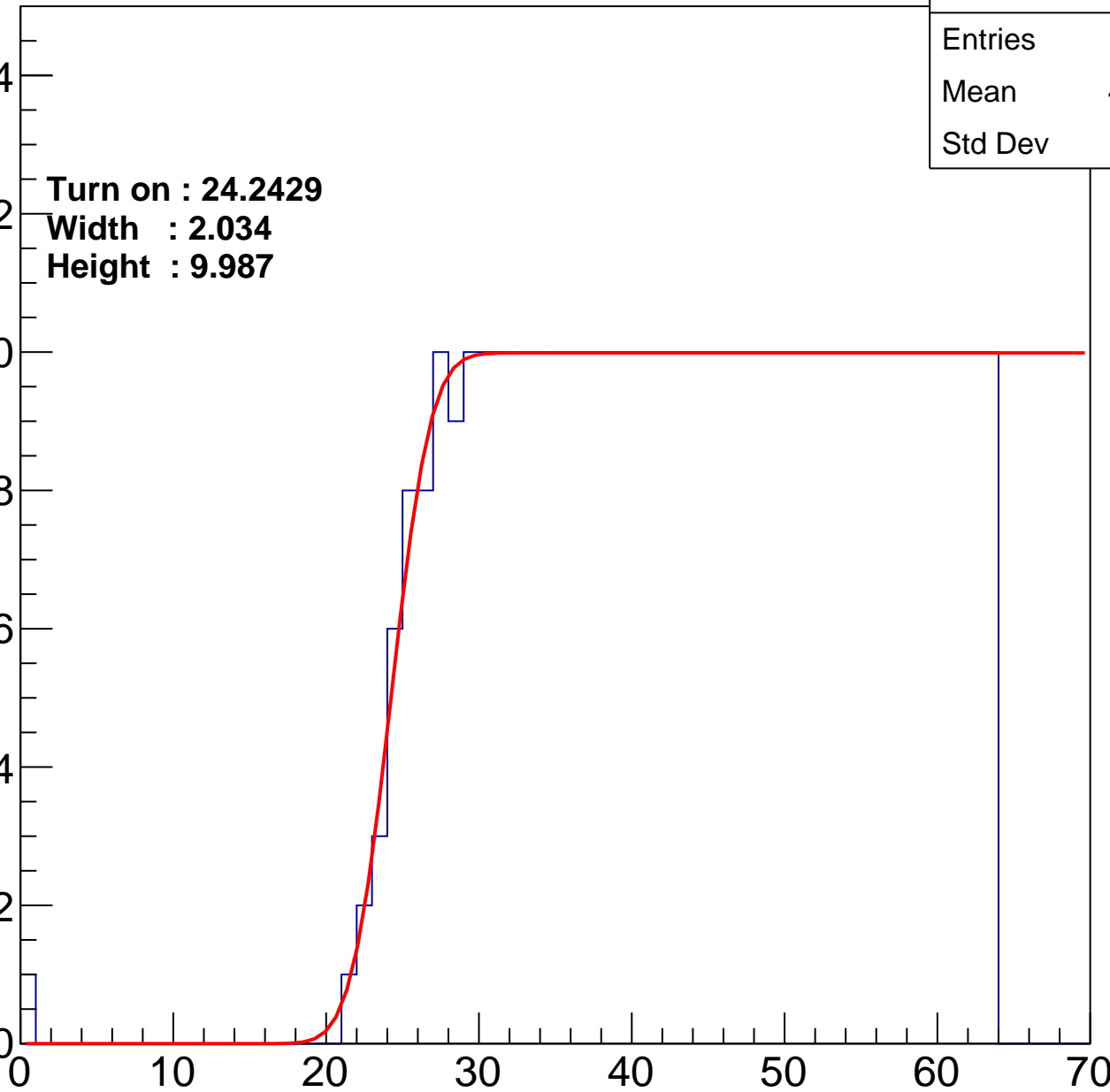
Width : 2.034

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch114

calib_packv5_042523_0143.root, FC#3, port B3

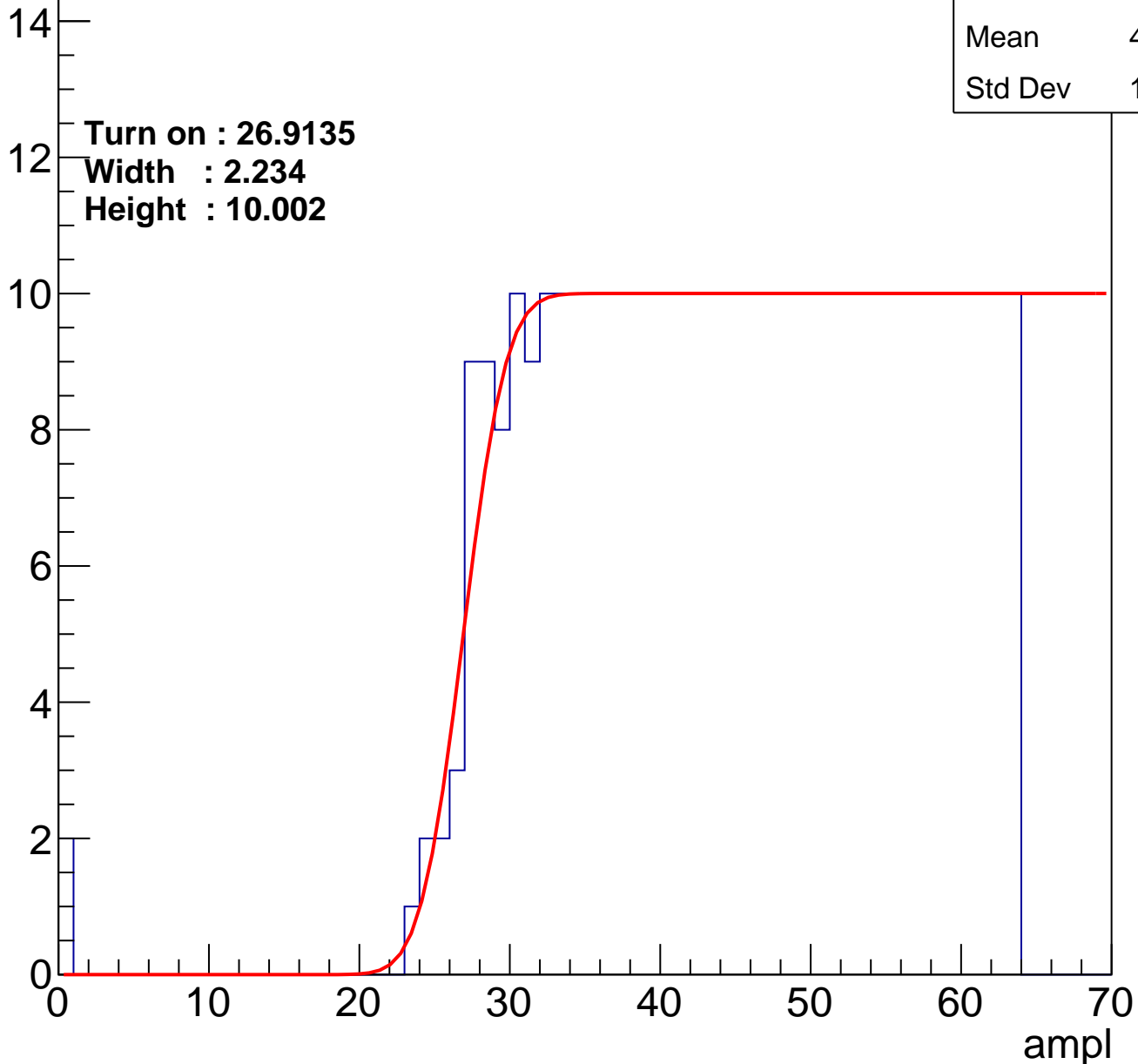
Entries	375
Mean	44.55
Std Dev	11.32

Turn on : 26.9135

Width : 2.234

Height : 10.002

Entry



B1L000S, U25-ch115

calib_packv5_042523_0143.root, FC#3, port B3

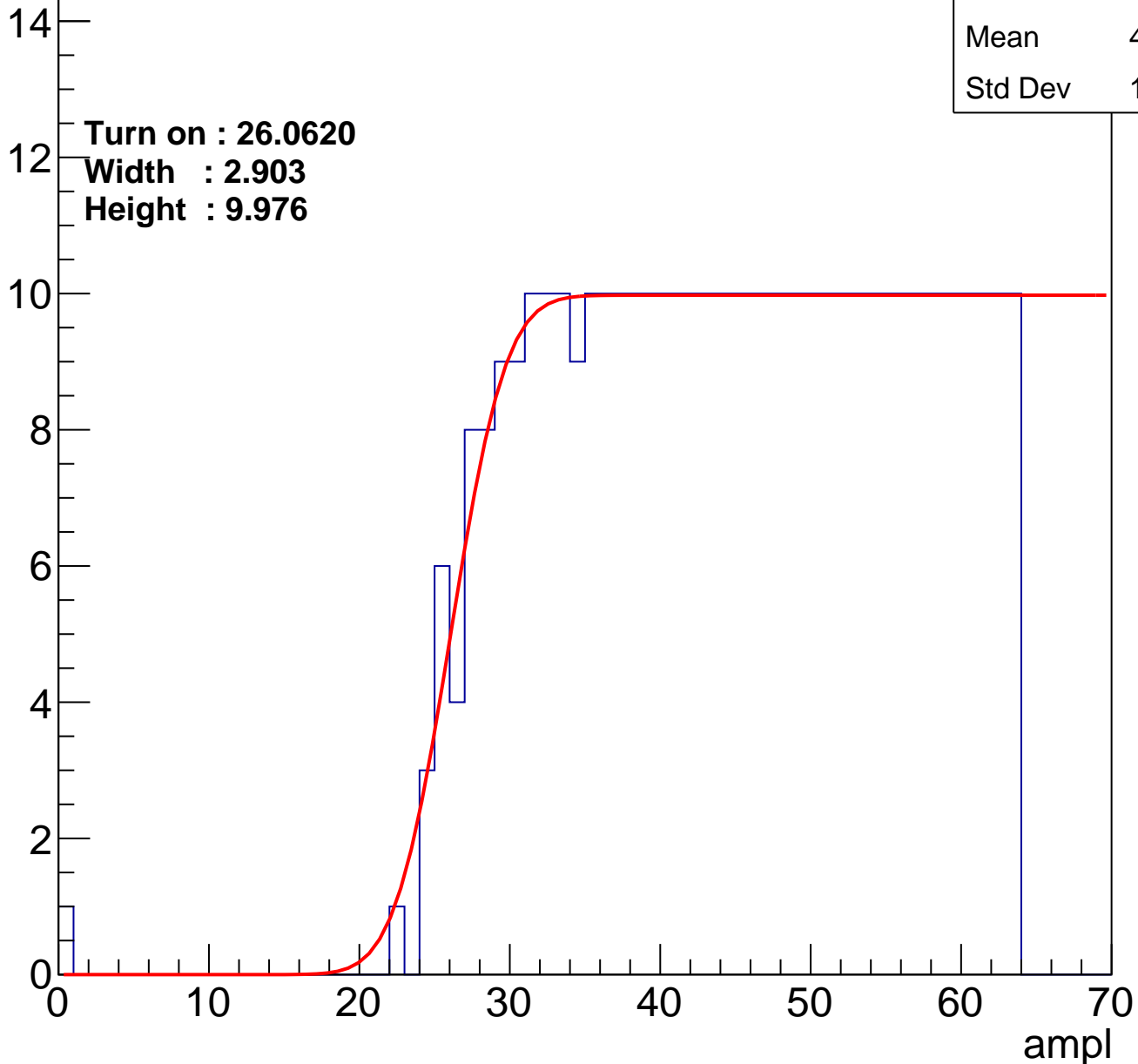
Entries	378
Mean	44.43
Std Dev	11.26

Turn on : 26.0620

Width : 2.903

Height : 9.976

Entry



B1L000S, U25-ch116

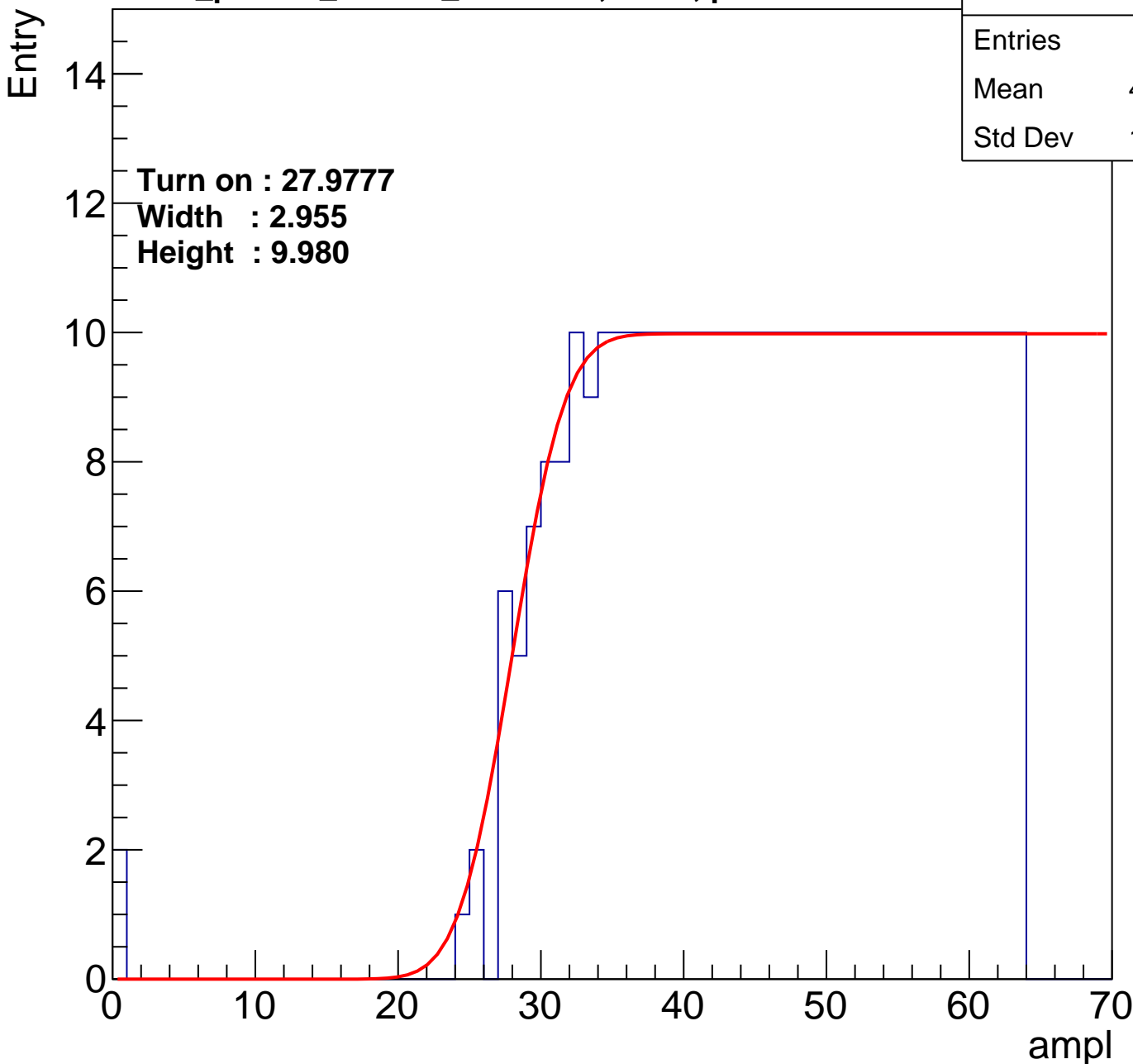
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 27.9777

Width : 2.955

Height : 9.980

Entries	358
Mean	45.35
Std Dev	10.95



B1L000S, U25-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
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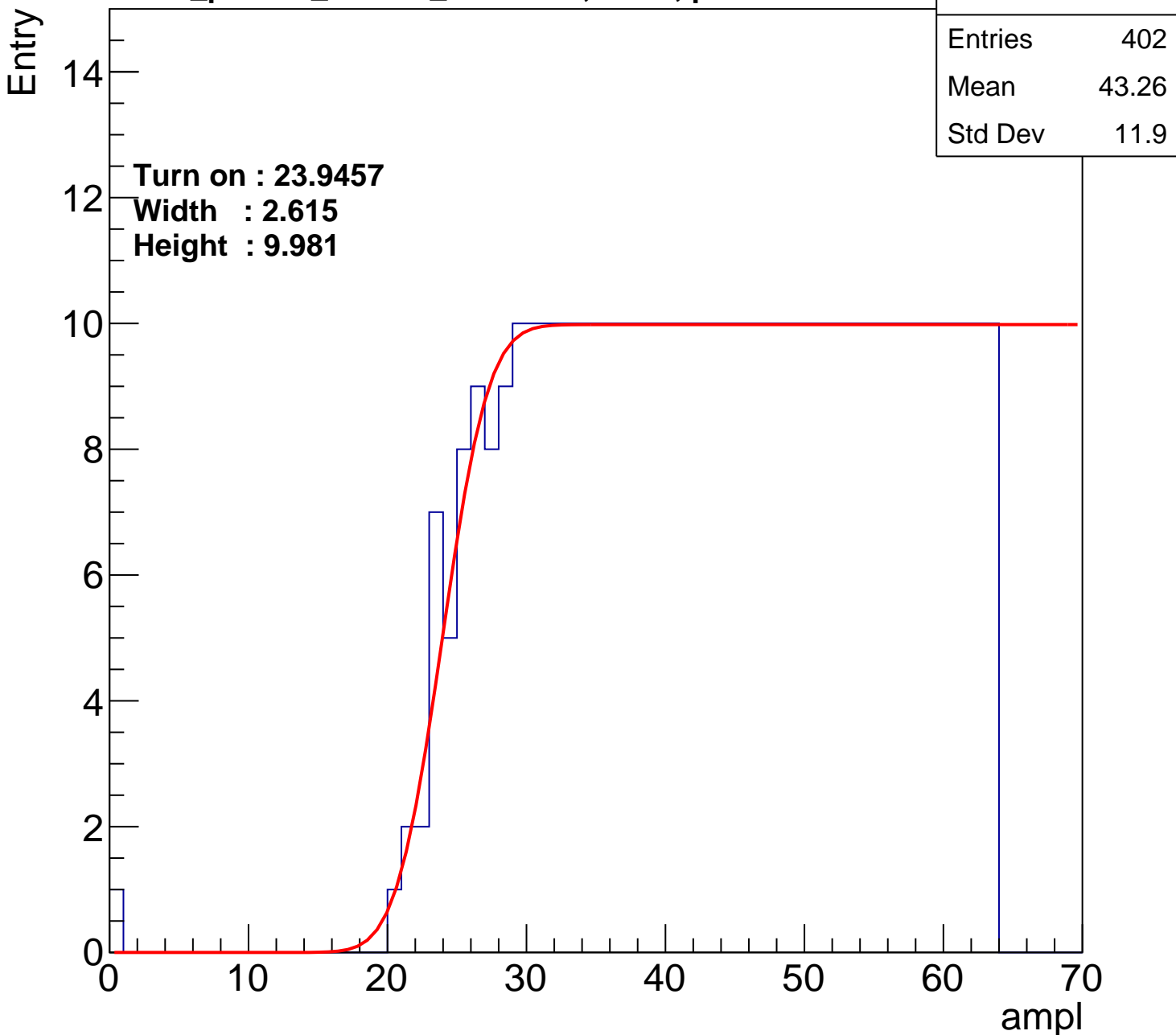
Mean	43.26
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Std Dev	11.9
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Turn on : 23.9457

Width : 2.615

Height : 9.981



B1L000S, U25-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.64
Std Dev	11.71

Turn on : 24.8757

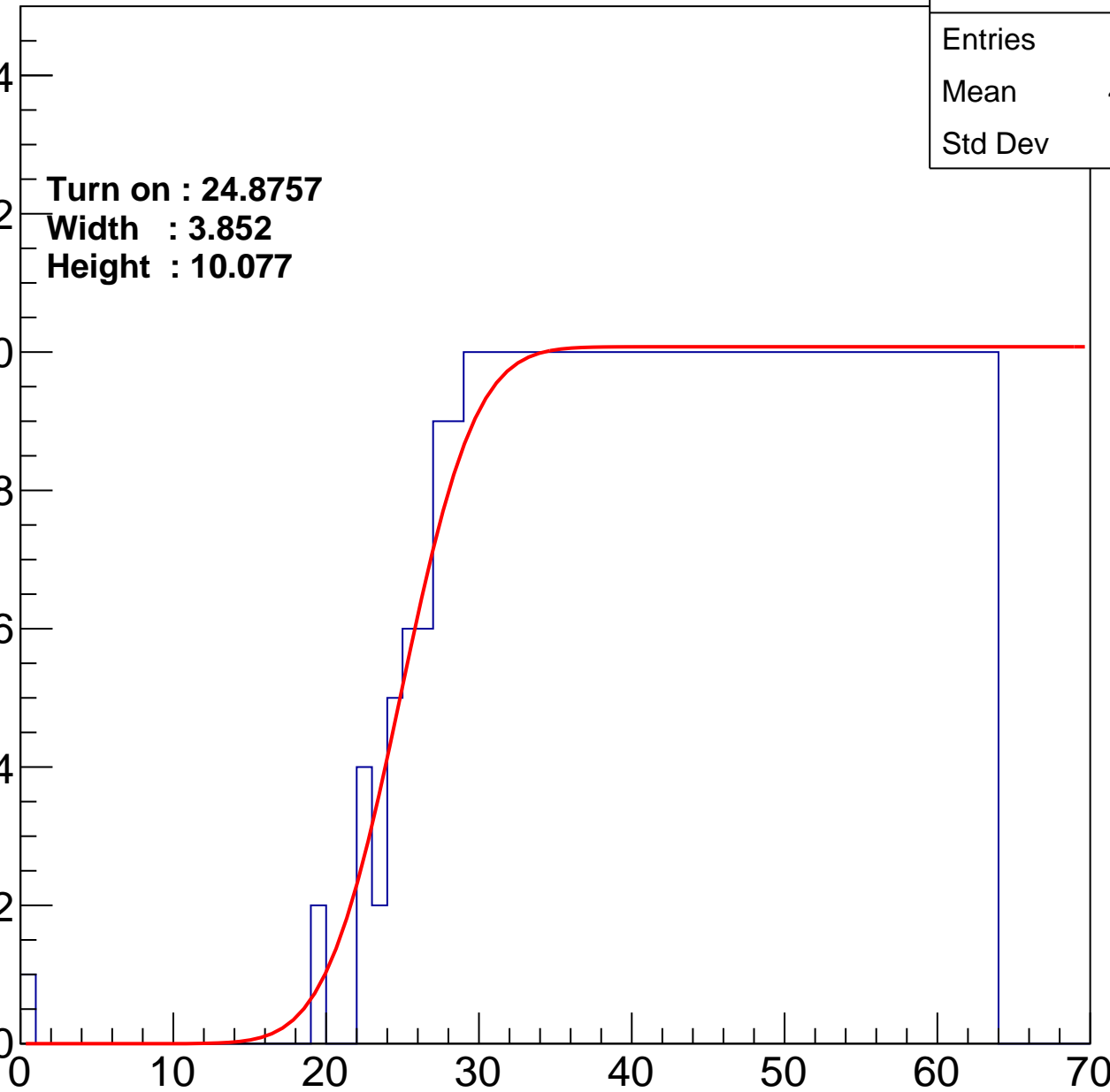
Width : 3.852

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.5

Turn on : 26.1966

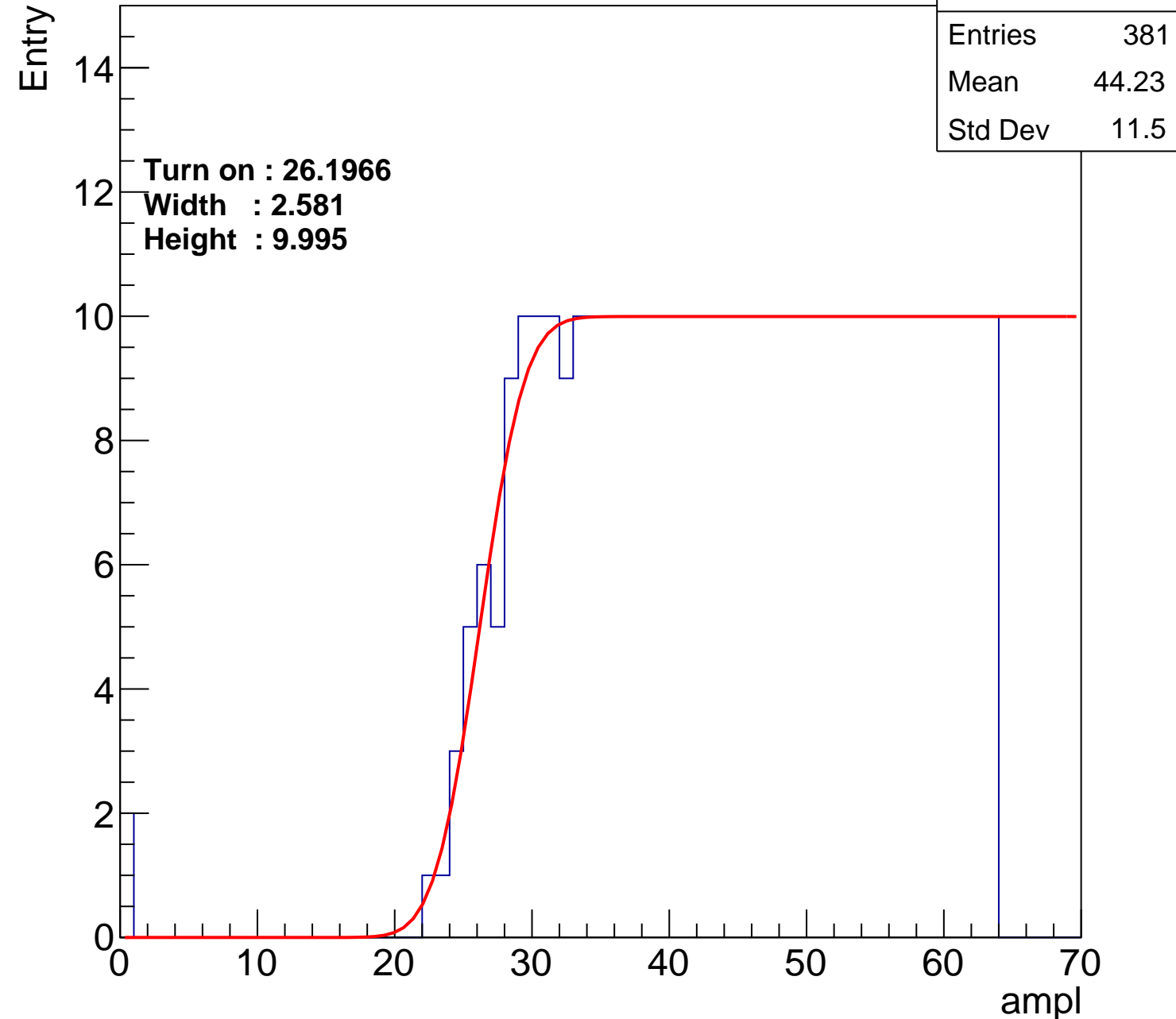
Width : 2.581

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch120

calib_packv5_042523_0143.root, FC#3, port B3

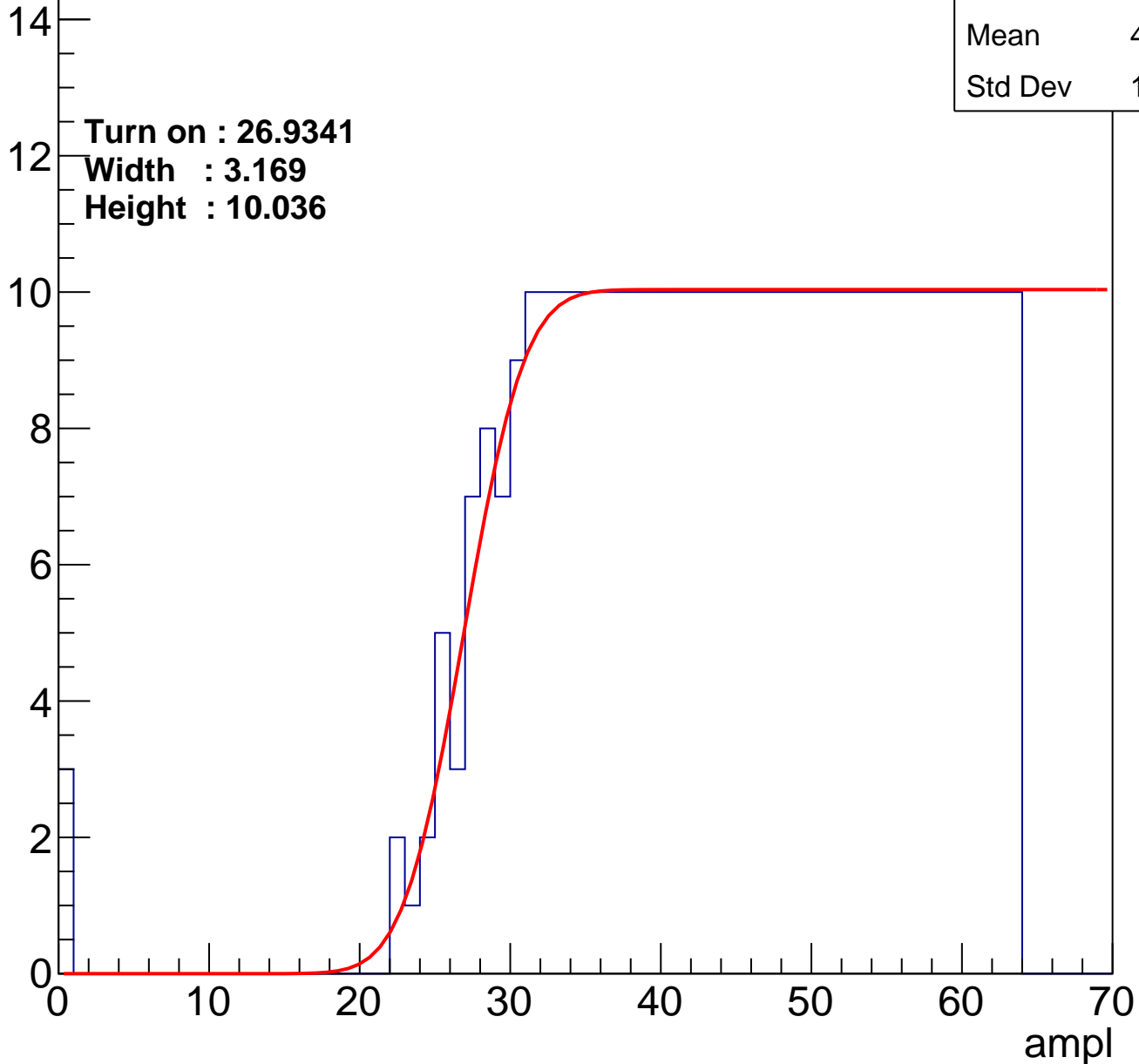
Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 26.9341

Width : 3.169

Height : 10.036

Entry



B1L000S, U25-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.1660

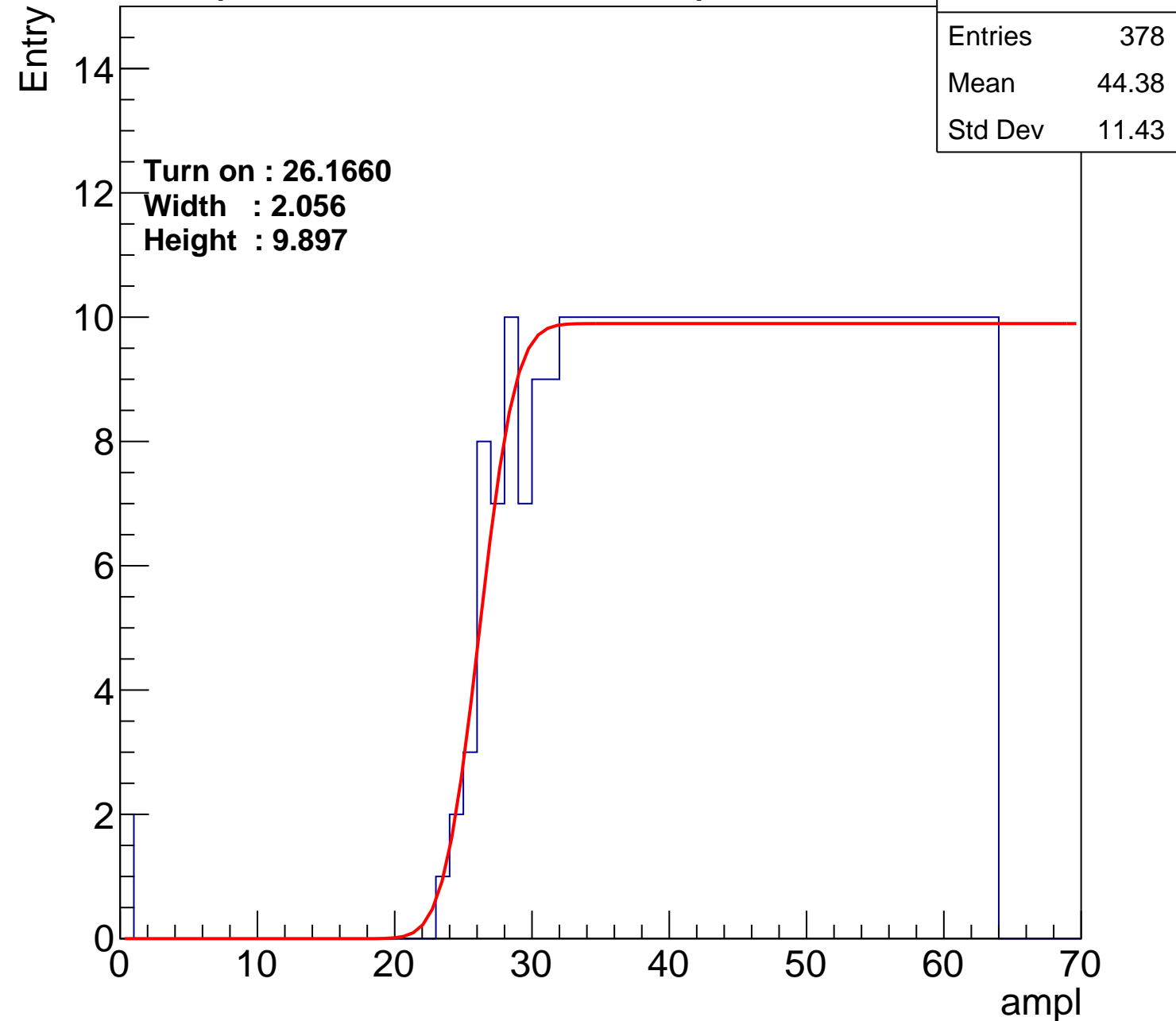
Width : 2.056

Height : 9.897

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.72
Std Dev	11.64

Turn on : 24.9966

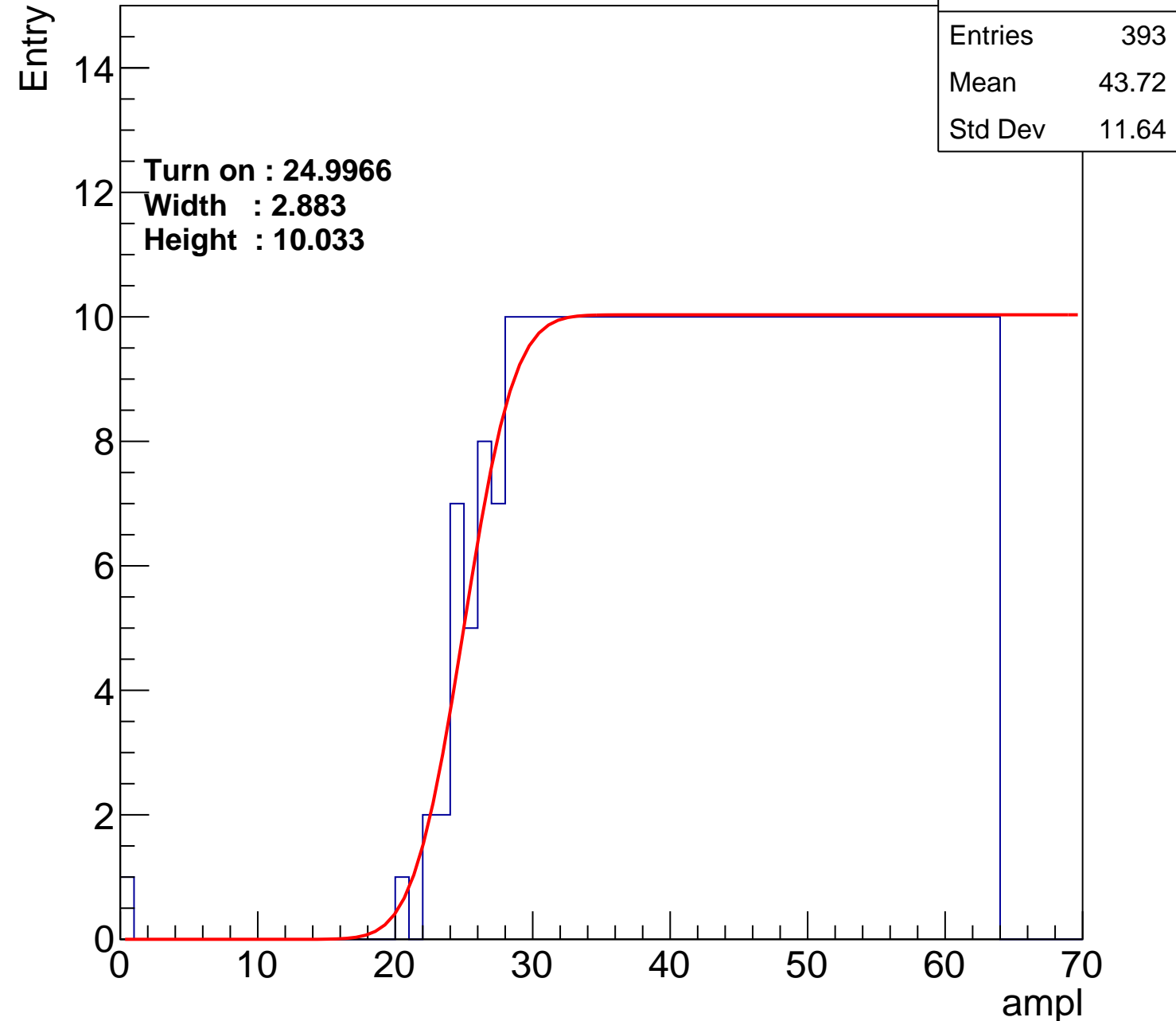
Width : 2.883

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch123

calib_packv5_042523_0143.root, FC#3, port B3

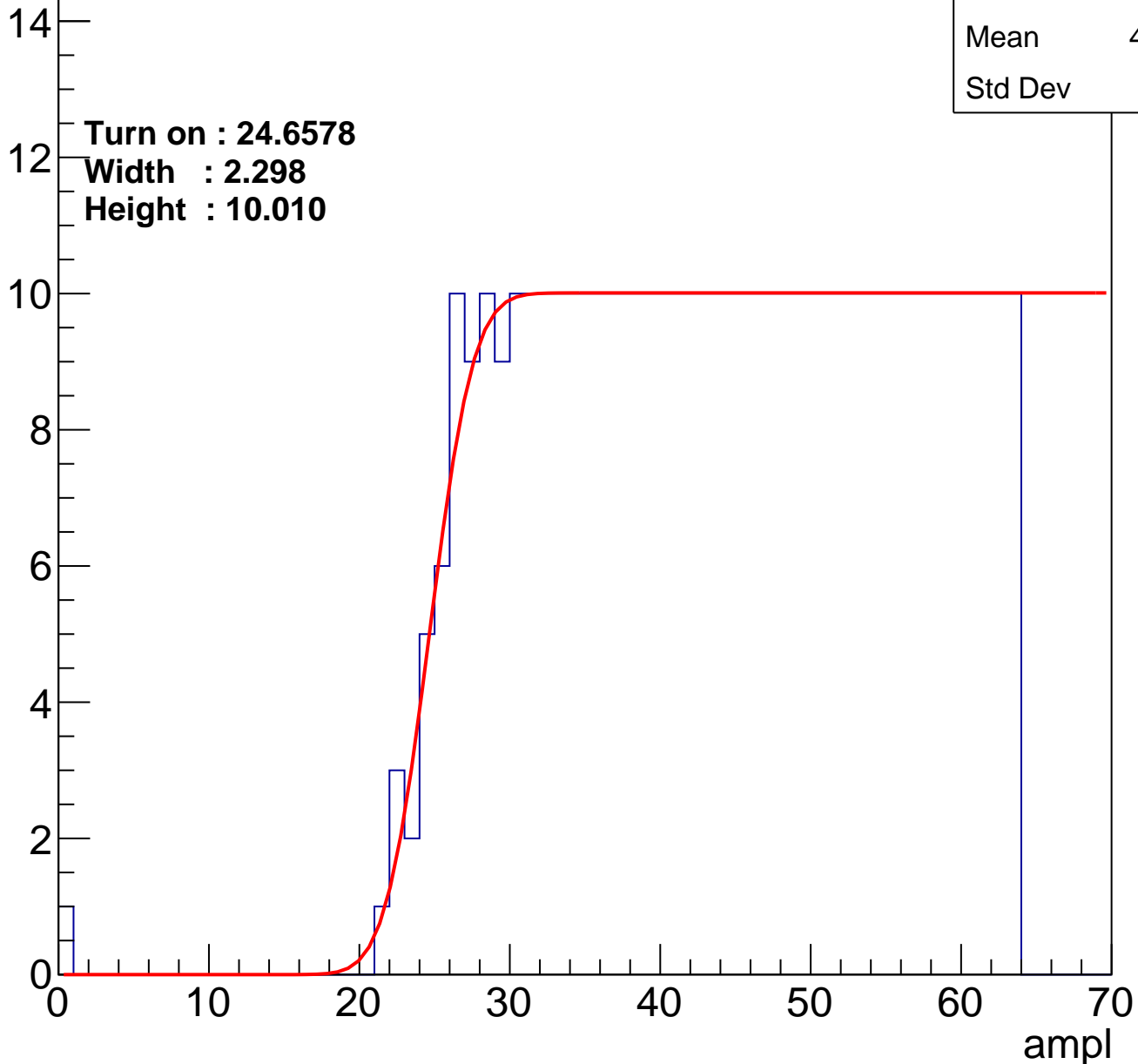
Entries	396
Mean	43.58
Std Dev	11.7

Turn on : 24.6578

Width : 2.298

Height : 10.010

Entry



B1L000S, U25-ch124

calib_packv5_042523_0143.root, FC#3, port B3

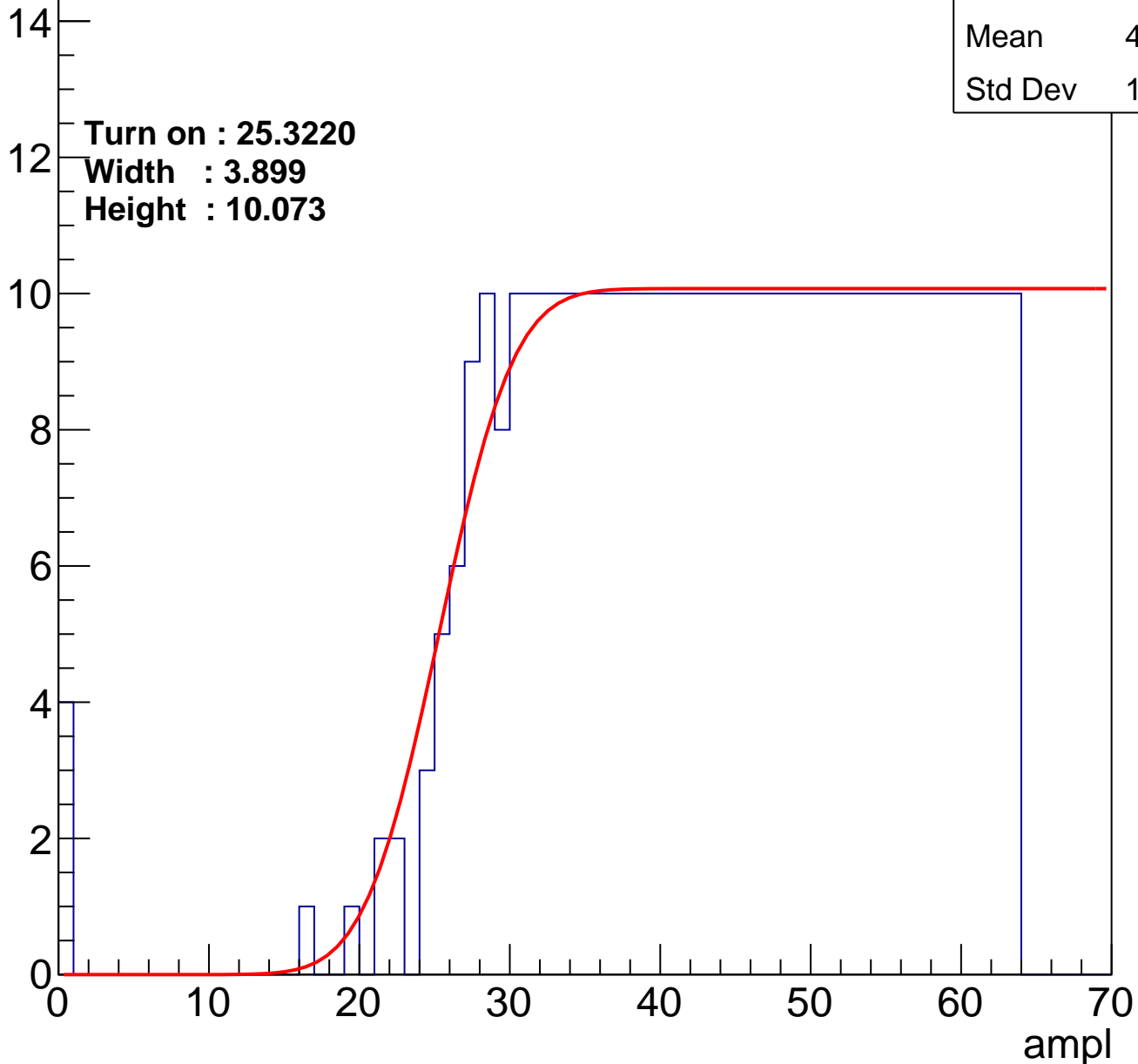
Entries	391
Mean	43.58
Std Dev	12.17

Turn on : 25.3220

Width : 3.899

Height : 10.073

Entry



B1L000S, U25-ch125

calib_packv5_042523_0143.root, FC#3, port B3

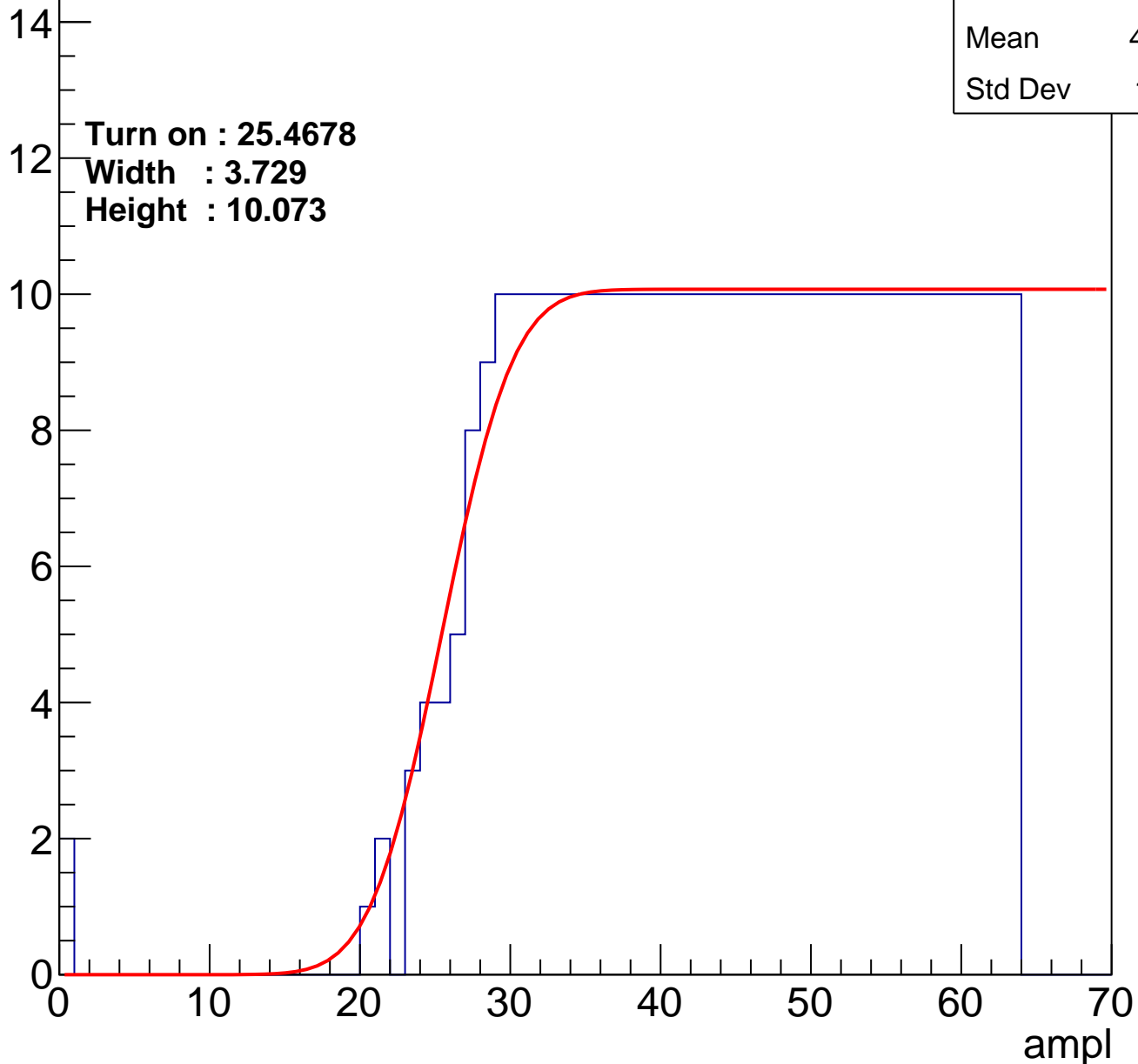
Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.4678

Width : 3.729

Height : 10.073

Entry



B1L000S, U25-ch126

calib_packv5_042523_0143.root, FC#3, port B3

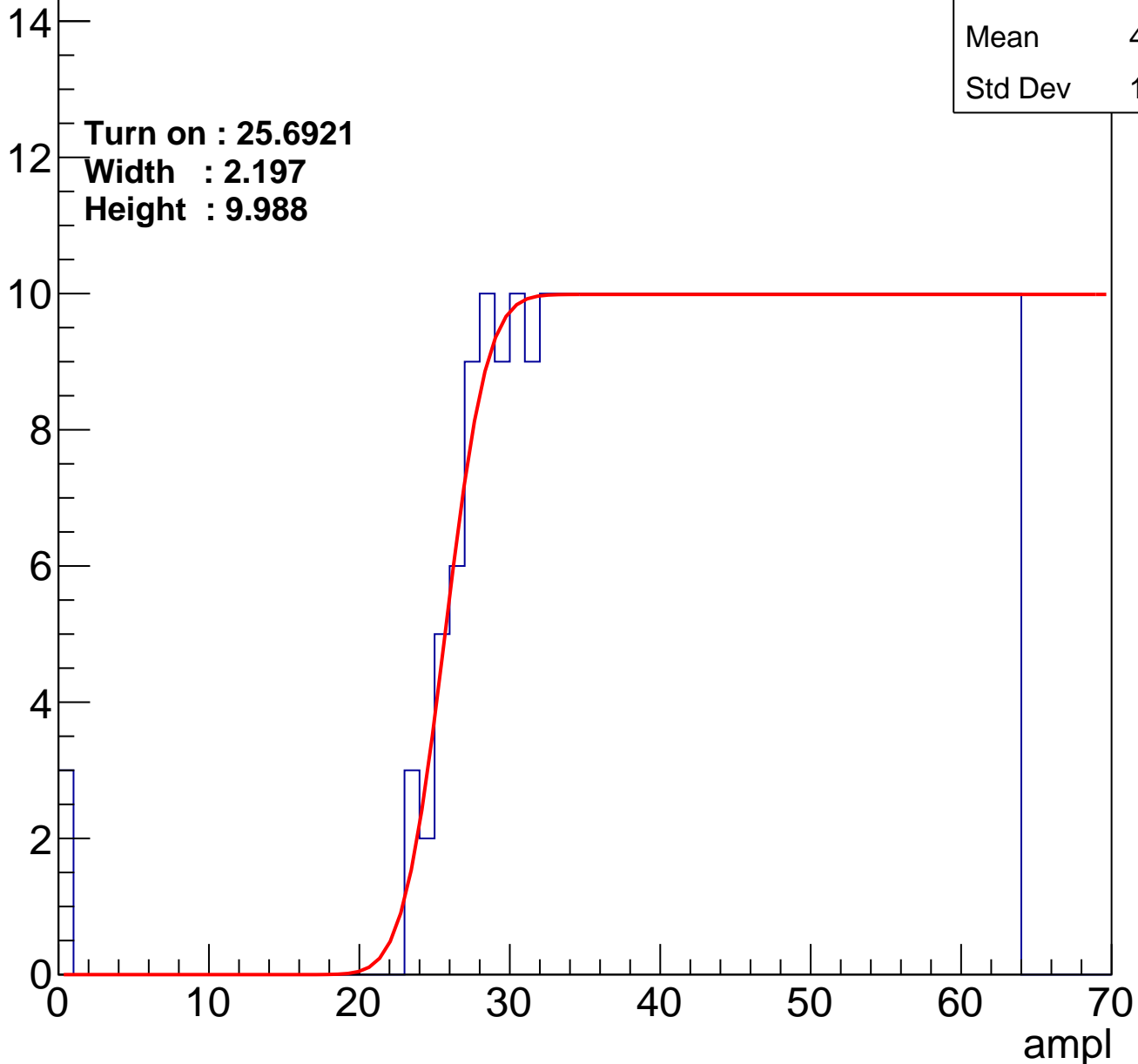
Entries	386
Mean	43.94
Std Dev	11.78

Turn on : 25.6921

Width : 2.197

Height : 9.988

Entry



B1L000S, U25-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.86
Std Dev	11.34

Turn on : 27.6310

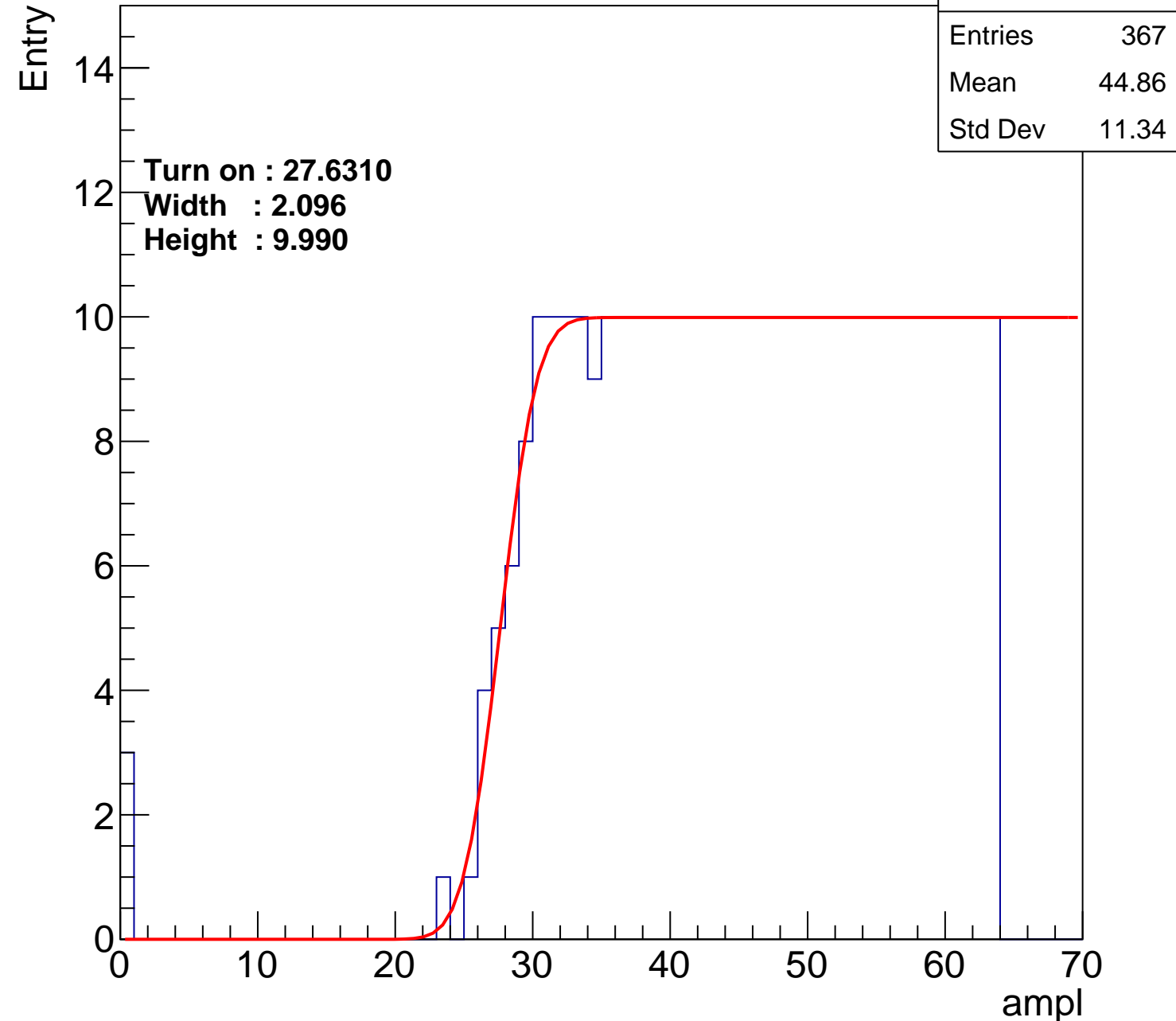
Width : 2.096

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U25-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.86
Std Dev	11.34

Turn on : 27.6310

Width : 2.096

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl

