

B0L101S, U25-ch0, adc0

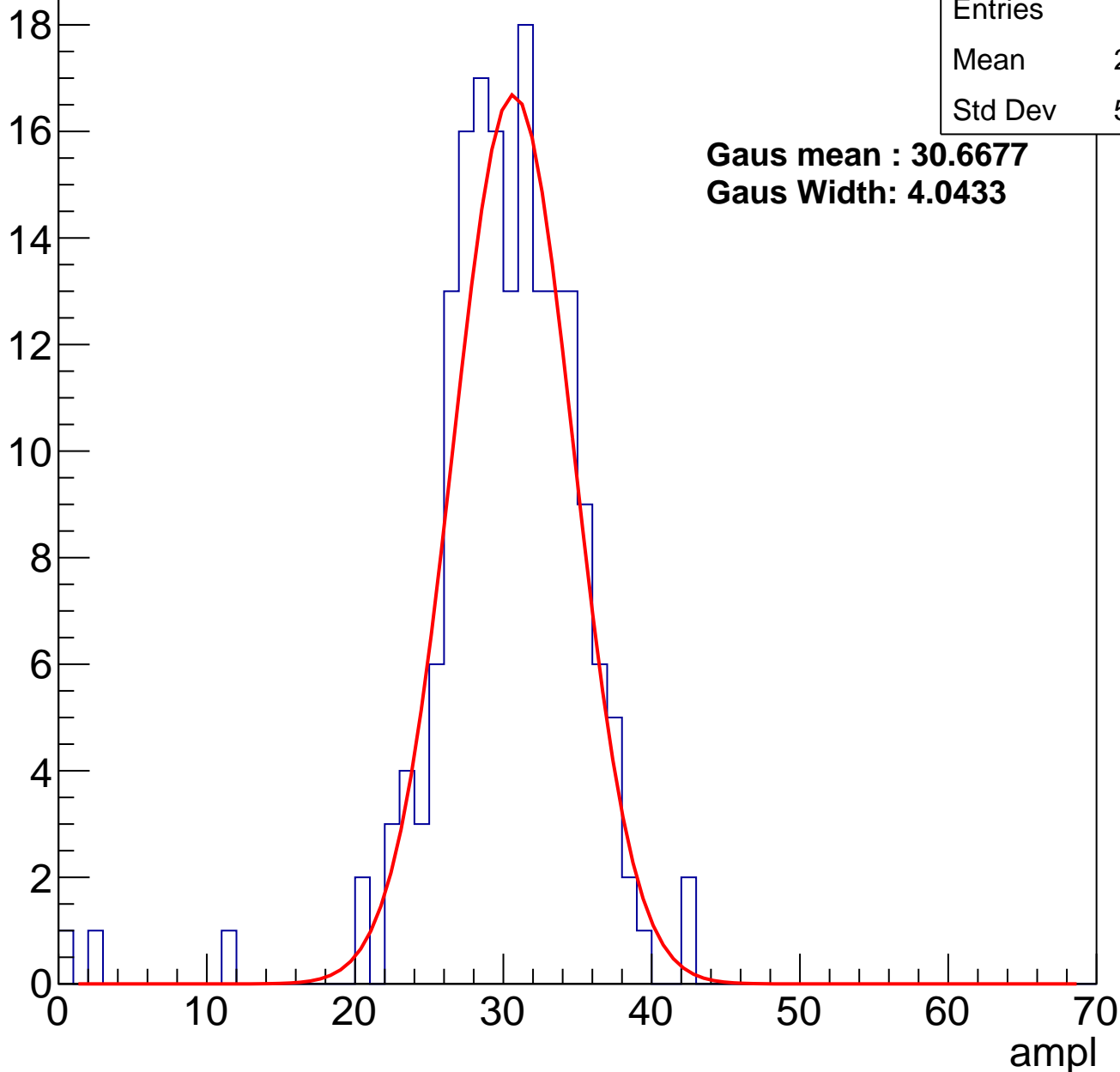
calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	29.74
Std Dev	5.243

Gaus mean : 30.6677

Gaus Width: 4.0433

Entry



B0L101S, U25-ch0, adc1

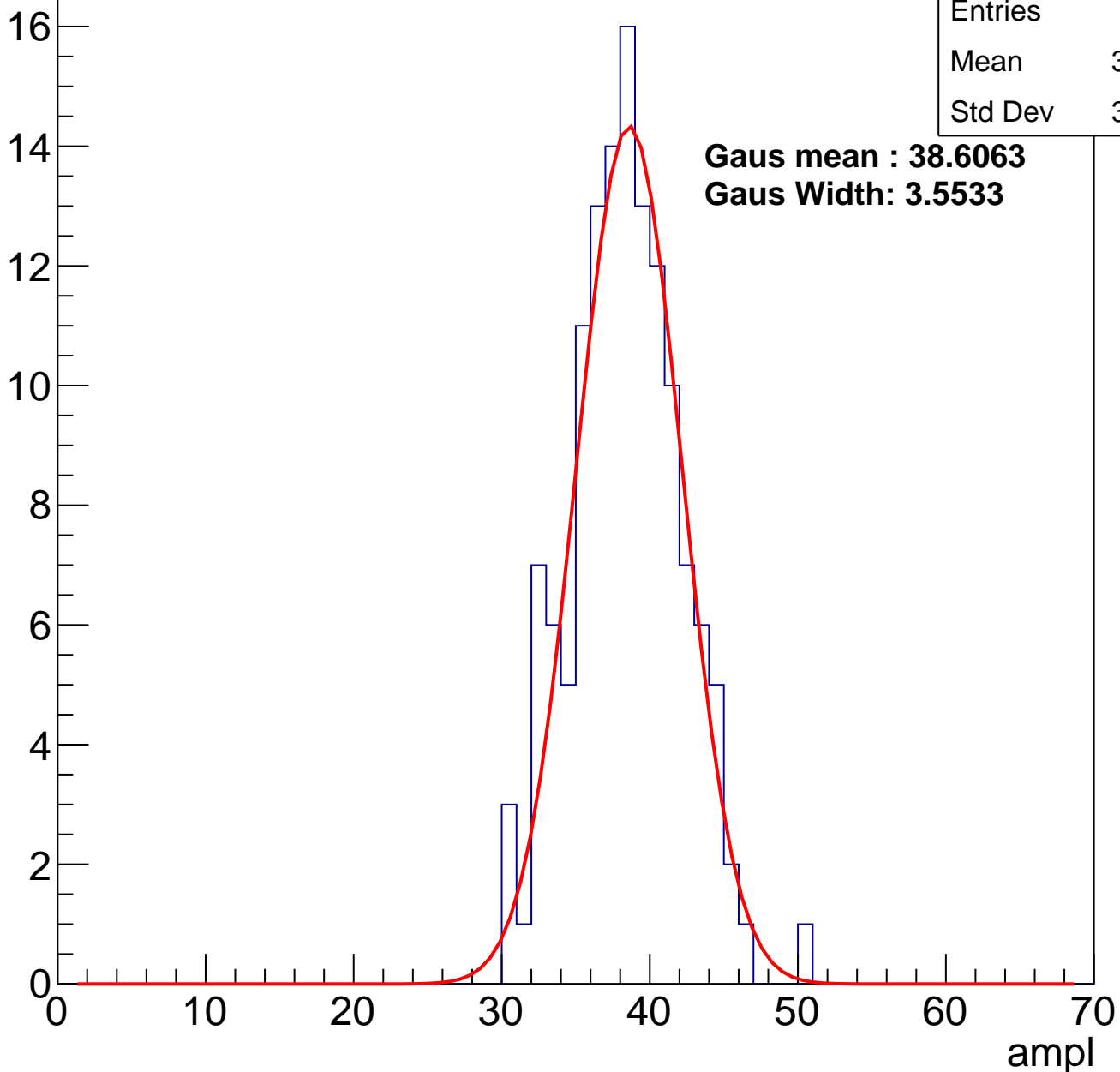
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	37.95
Std Dev	3.665

Gaus mean : 38.6063

Gaus Width: 3.5533

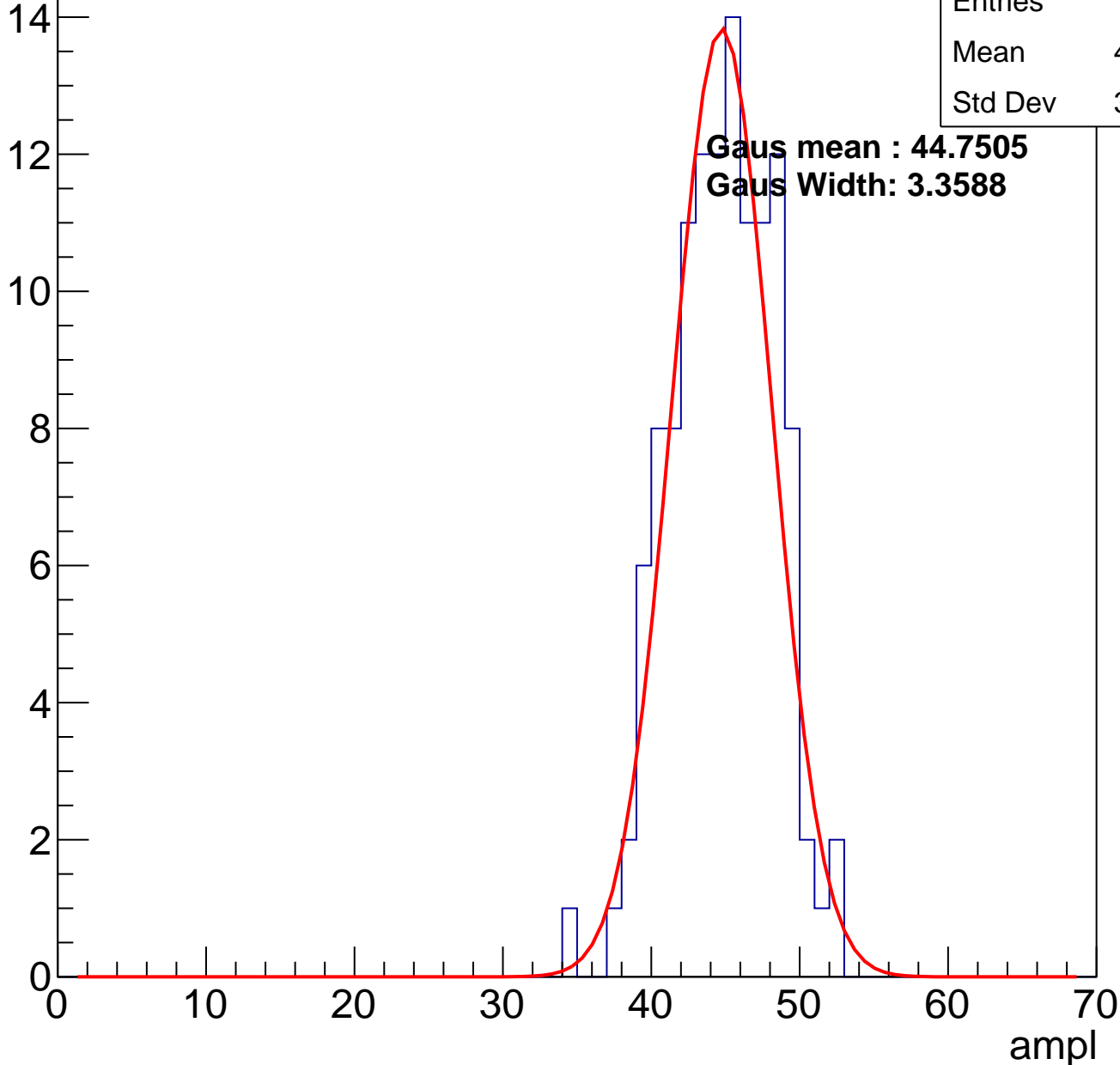
Entry



B0L101S, U25-ch0, adc2

calib_packv5_042523_0143.root, FC#1, port C1

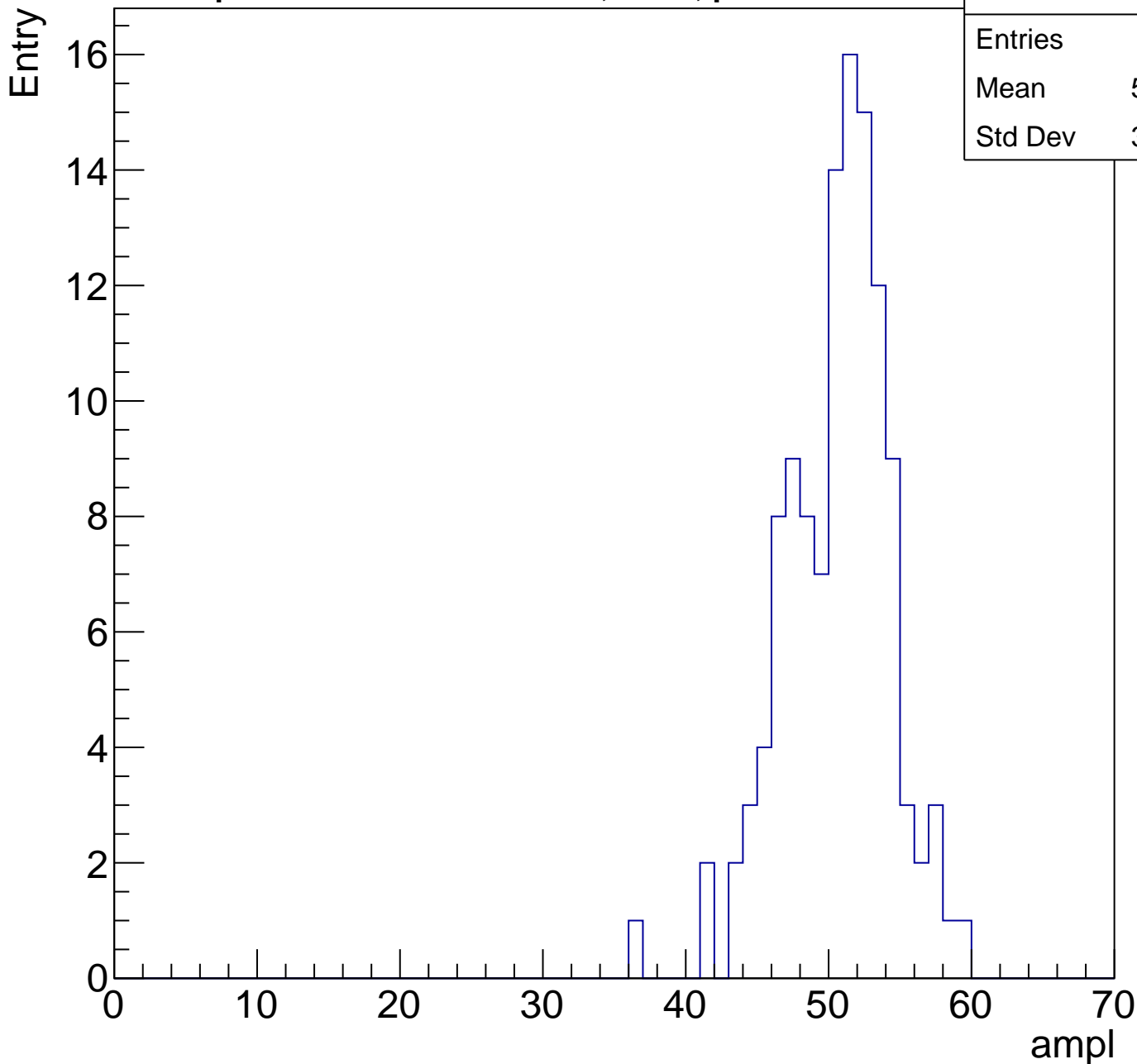
Entry



B0L101S, U25-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	50.14
Std Dev	3.738



B0L101S, U25-ch0, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	56.06
Std Dev	5.893

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

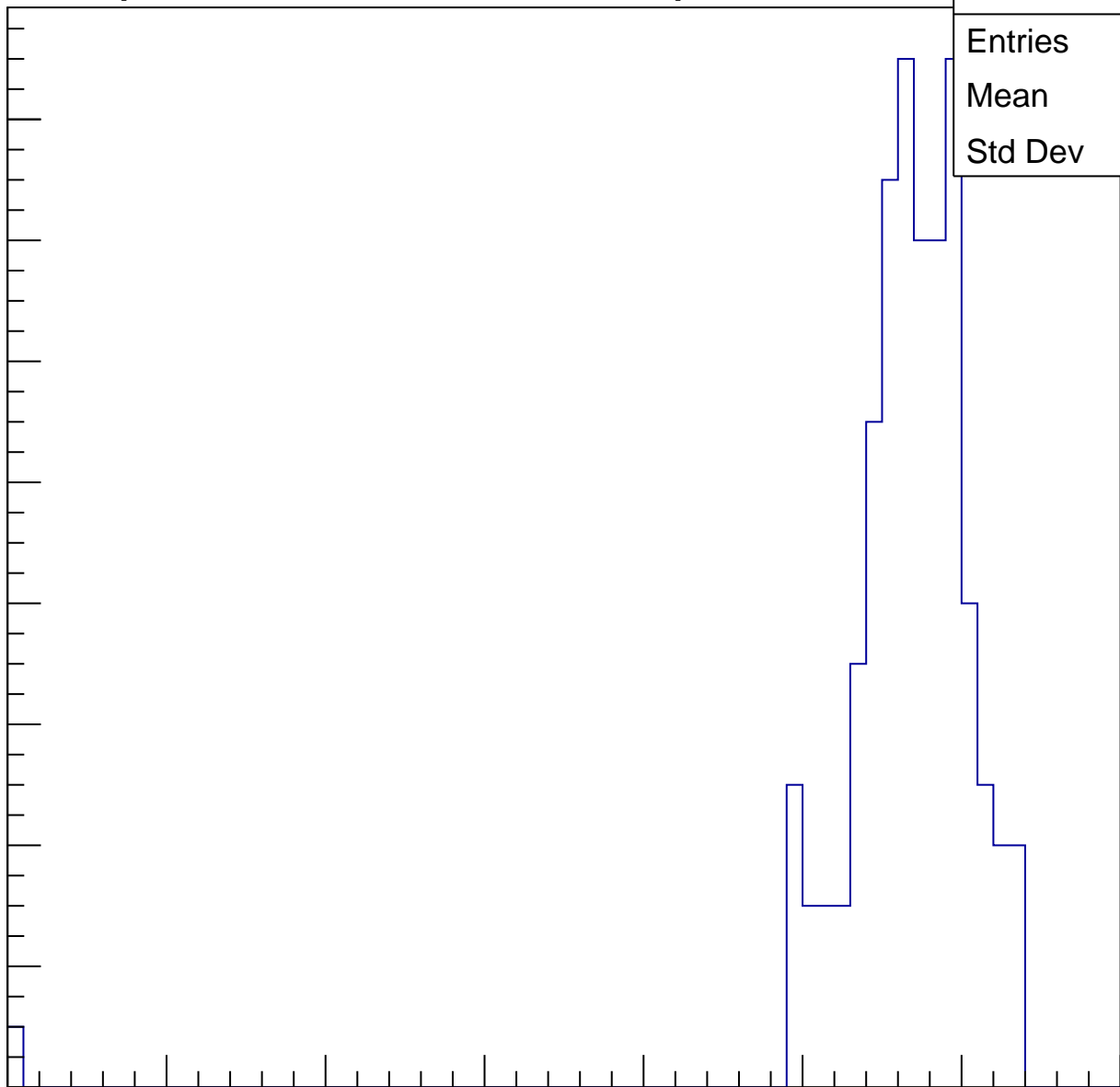
40

50

60

70

ampl



B0L101S, U25-ch0, adc5

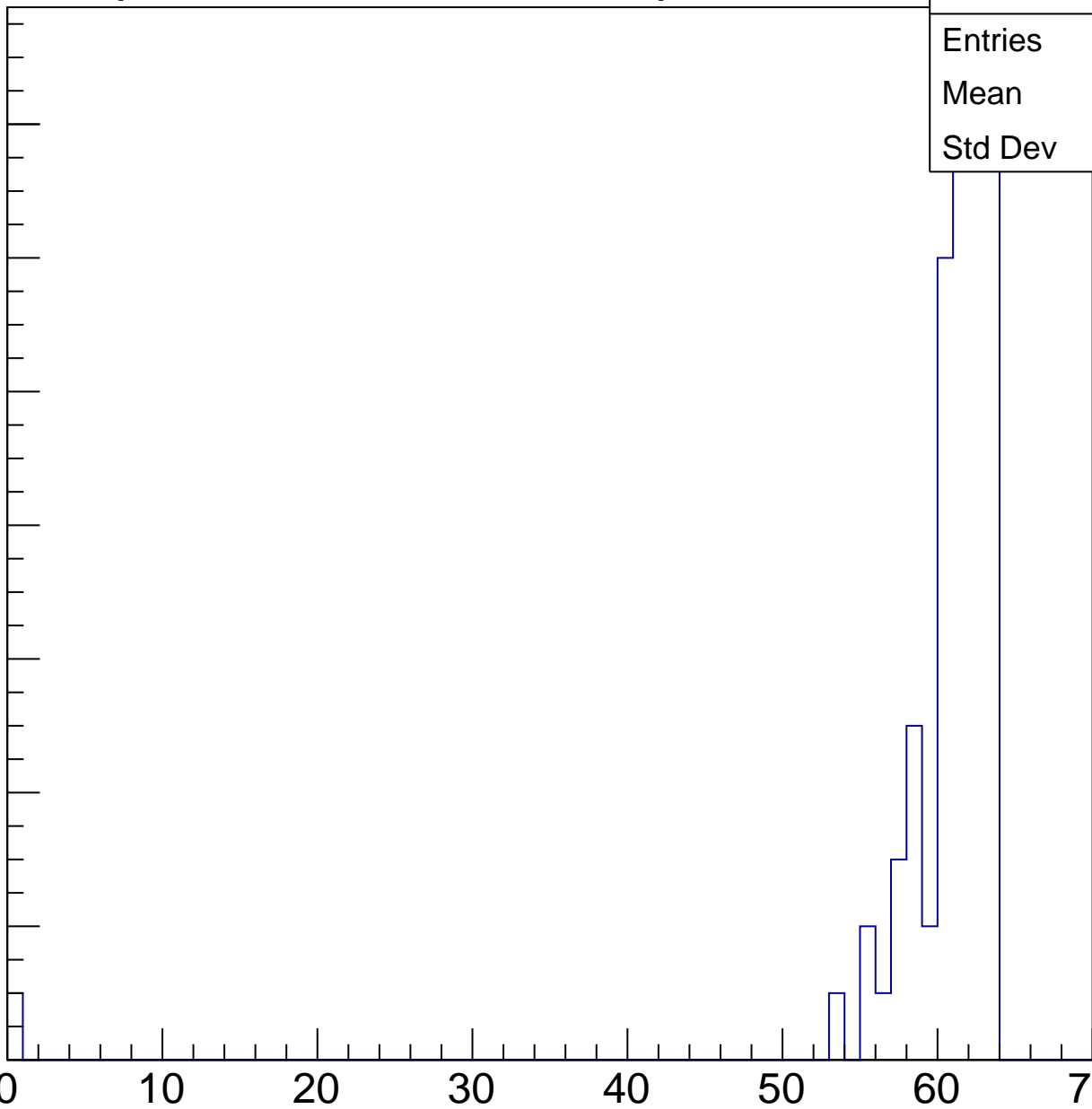
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.83
Std Dev	7.427

ampl



B0L101S, U25-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



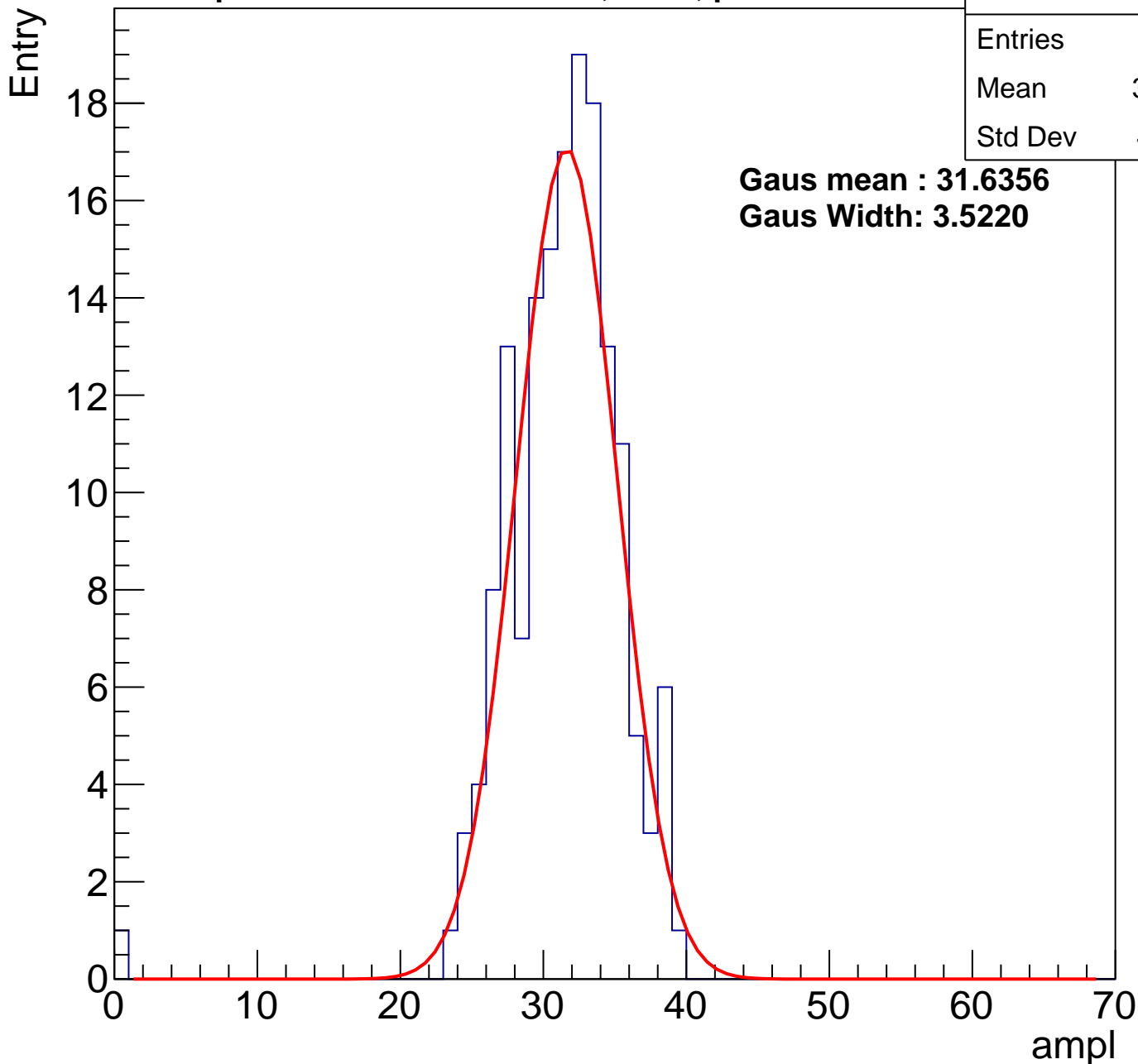
B0L101S, U25-ch1, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	30.94
Std Dev	4.221

Gaus mean : 31.6356

Gaus Width: 3.5220



B0L101S, U25-ch1, adc1

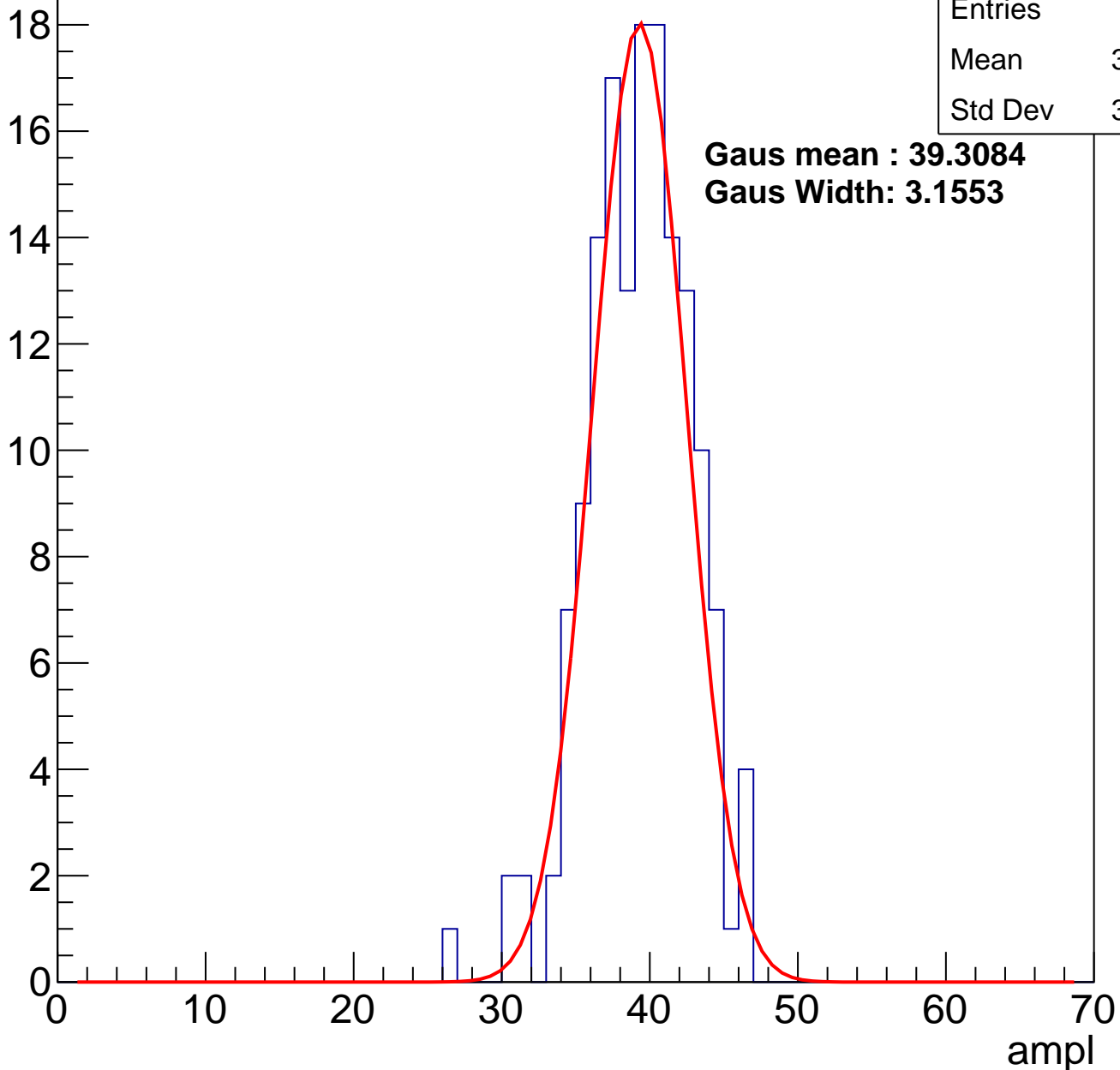
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	38.84
Std Dev	3.452

Gaus mean : 39.3084

Gaus Width: 3.1553

Entry



B0L101S, U25-ch1, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	45.95
Std Dev	3.227

Gaus mean : 46.5409

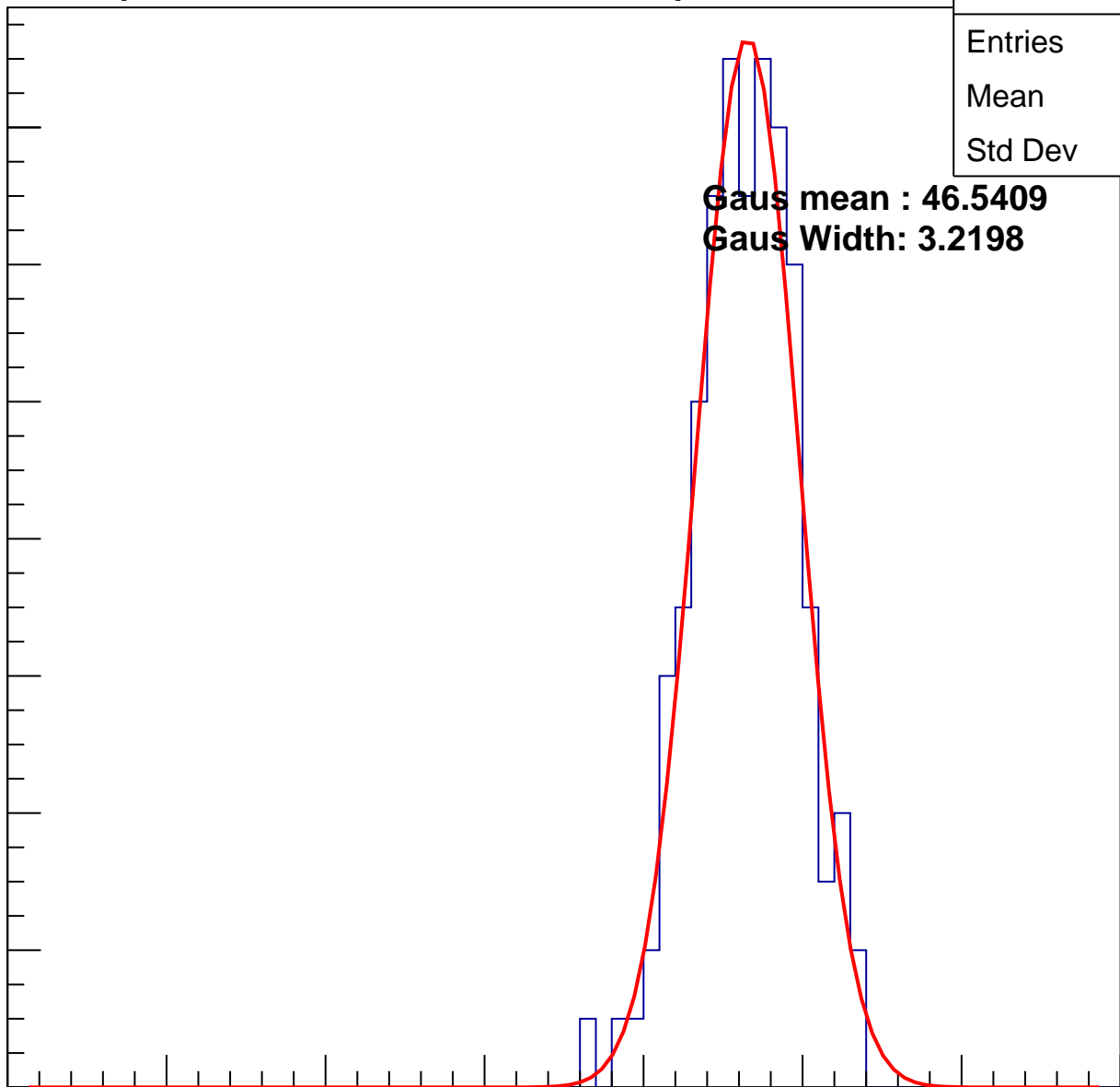
Gaus Width: 3.2198

Entry

14
12
10
8
6
4
2
0

ampl

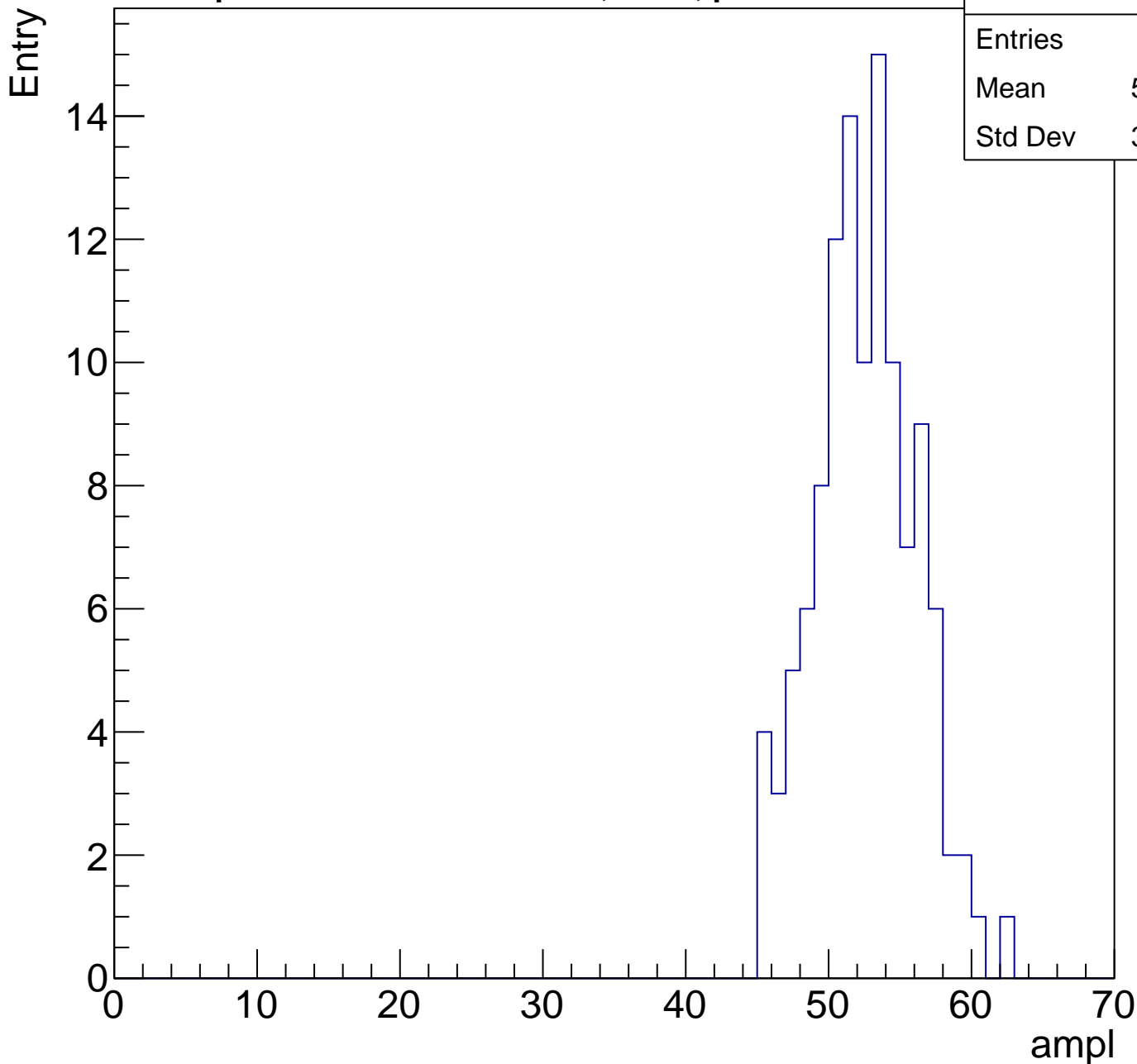
0 10 20 30 40 50 60 70



B0L101S, U25-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	52.08
Std Dev	3.494



B0L101S, U25-ch1, adc4

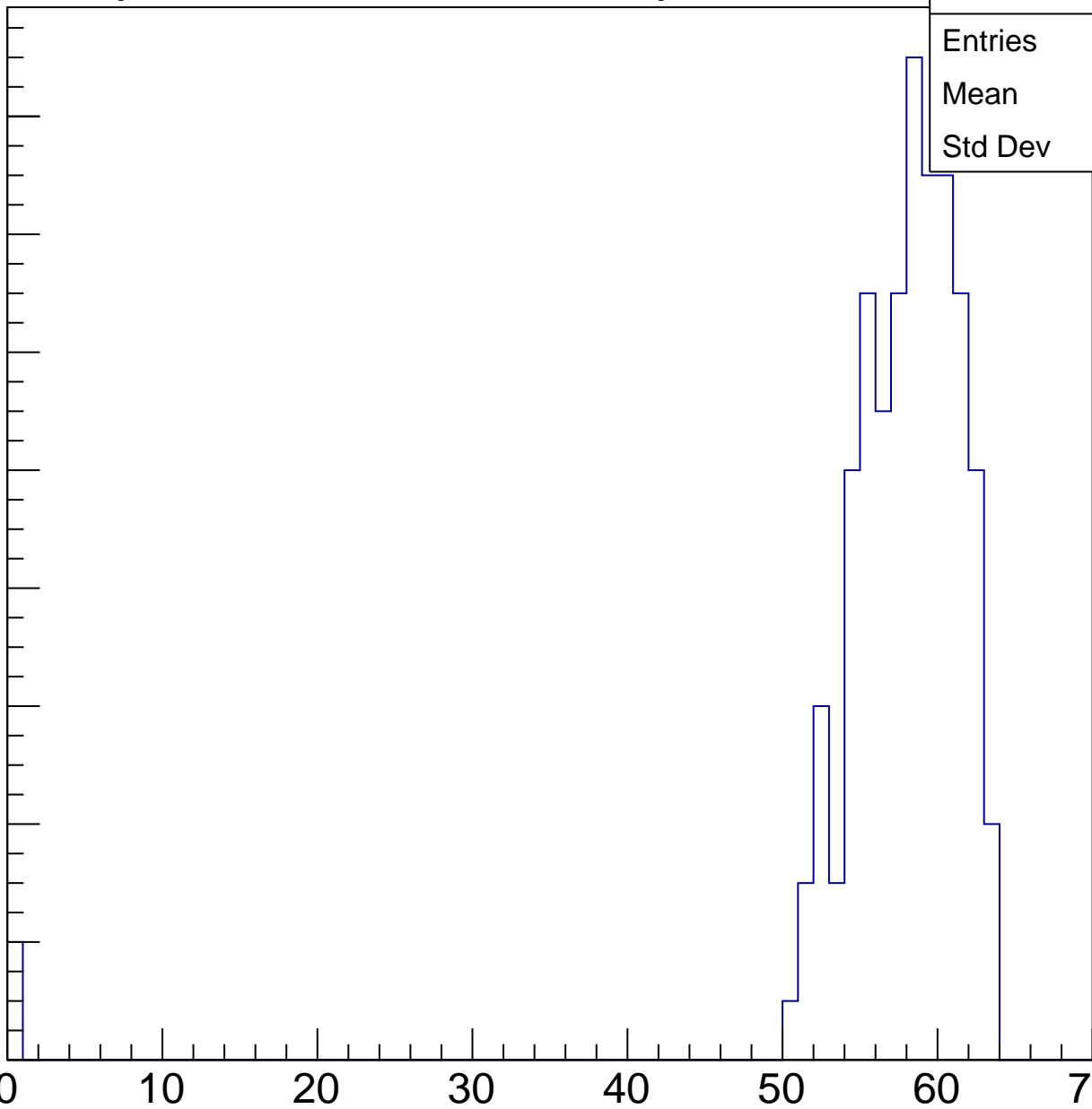
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	56.78
Std Dev	7.58

ampl



B0L101S, U25-ch1, adc5

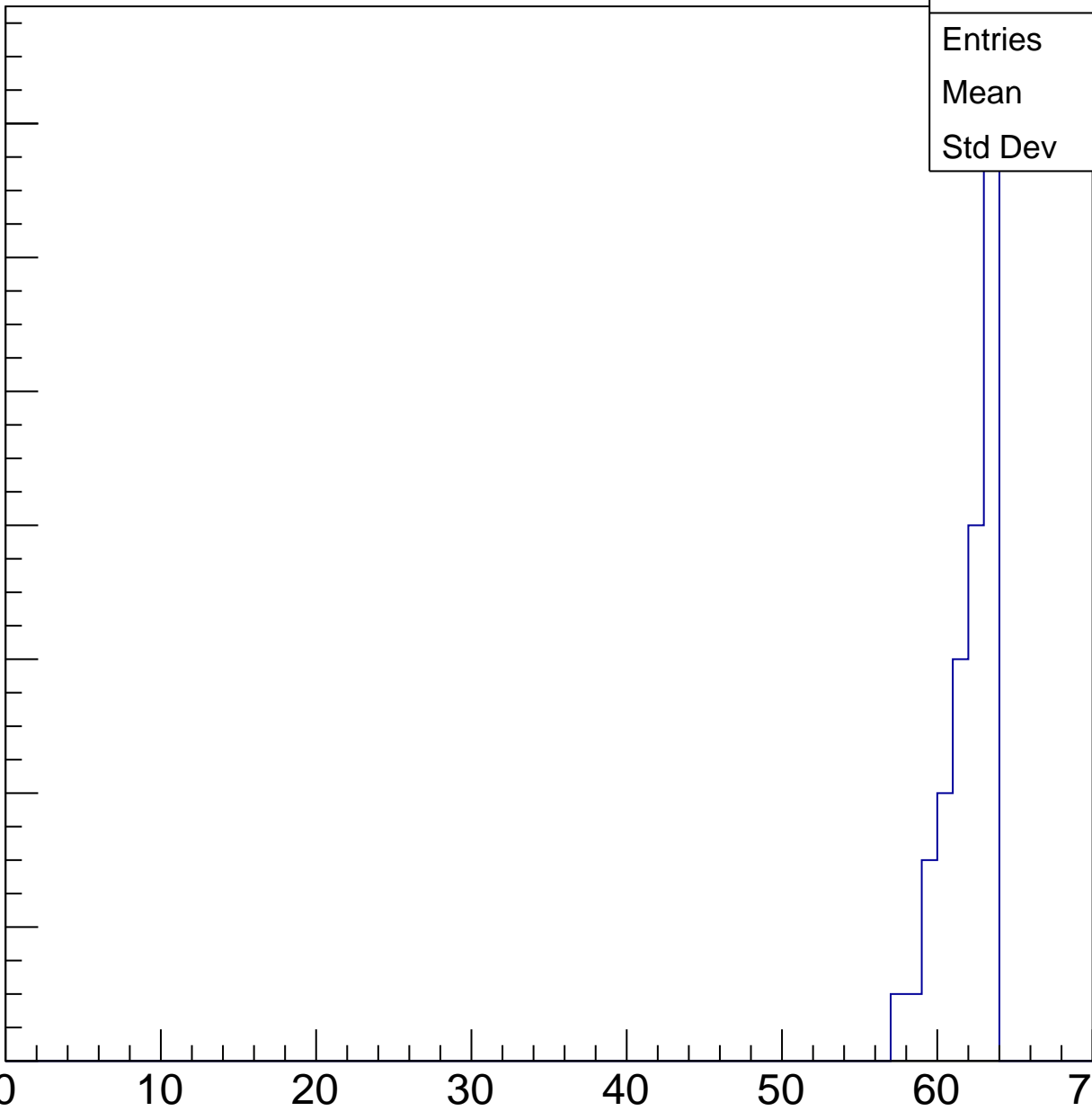
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	38
Mean	61.55
Std Dev	1.601

ampl



B0L101S, U25-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

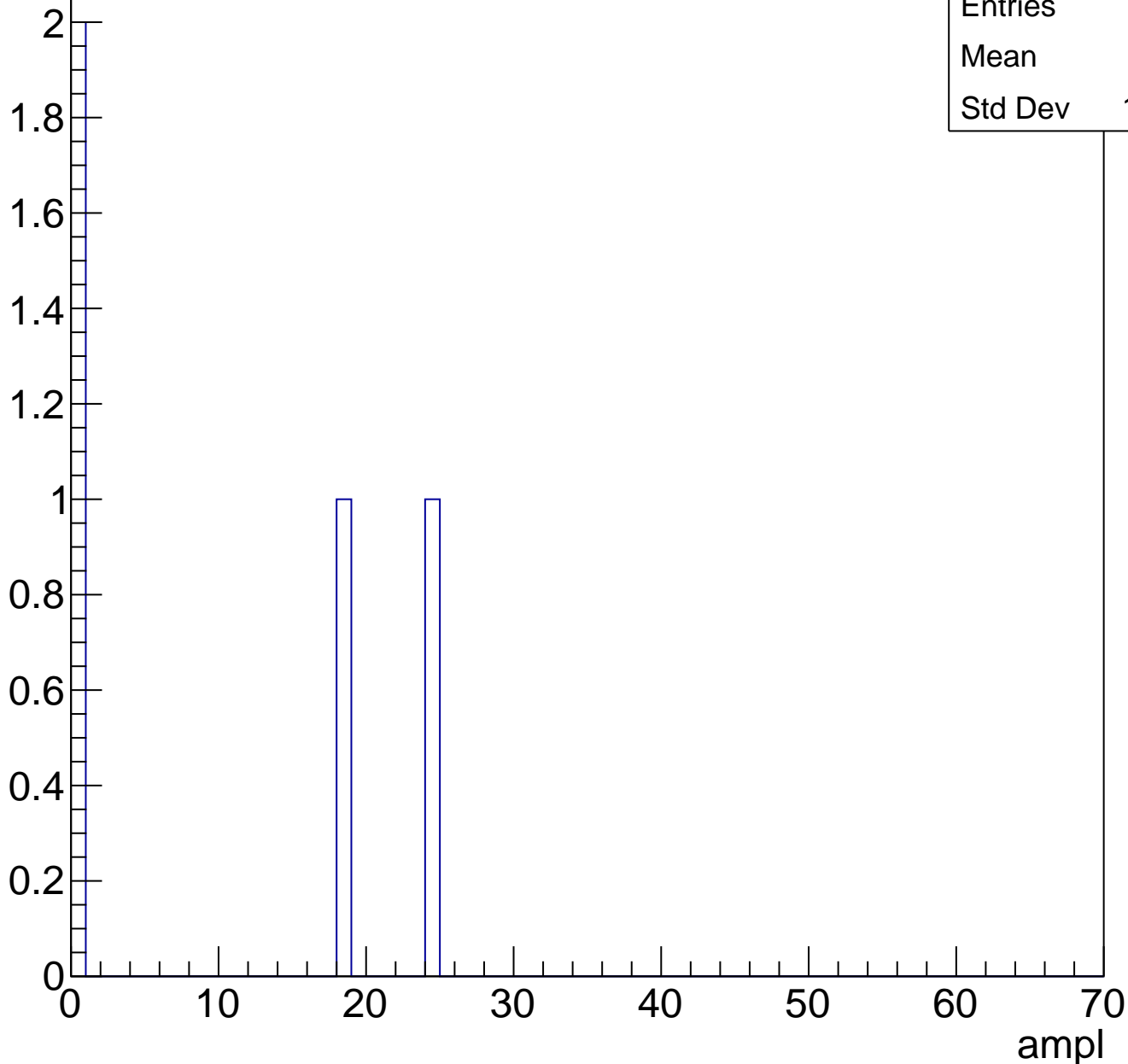
Entry



B0L101S, U25-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

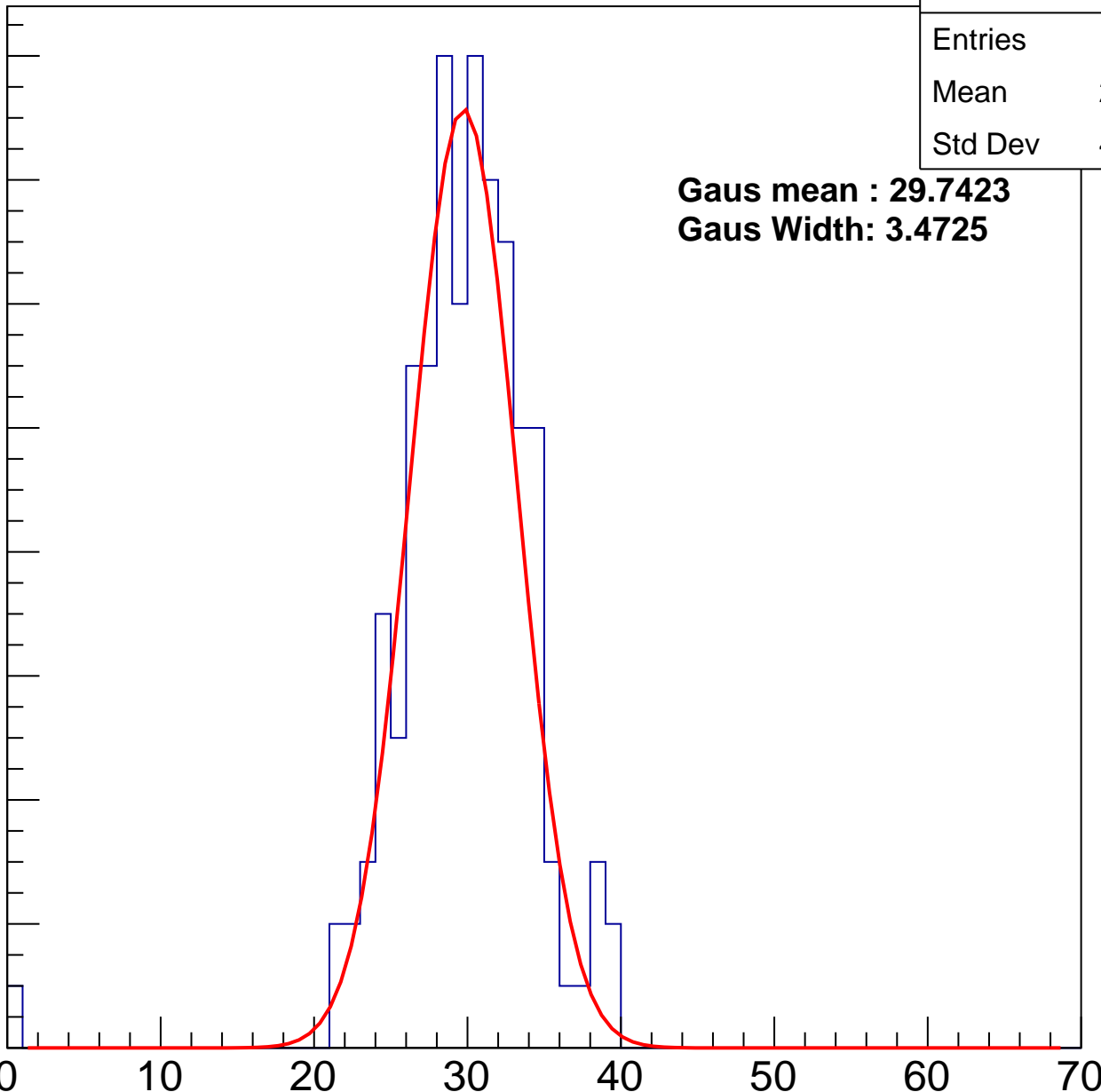
16
14
12
10
8
6
4
2
0

Entries	143
Mean	29.35
Std Dev	4.452

Gaus mean : 29.7423

Gaus Width: 3.4725

ampl



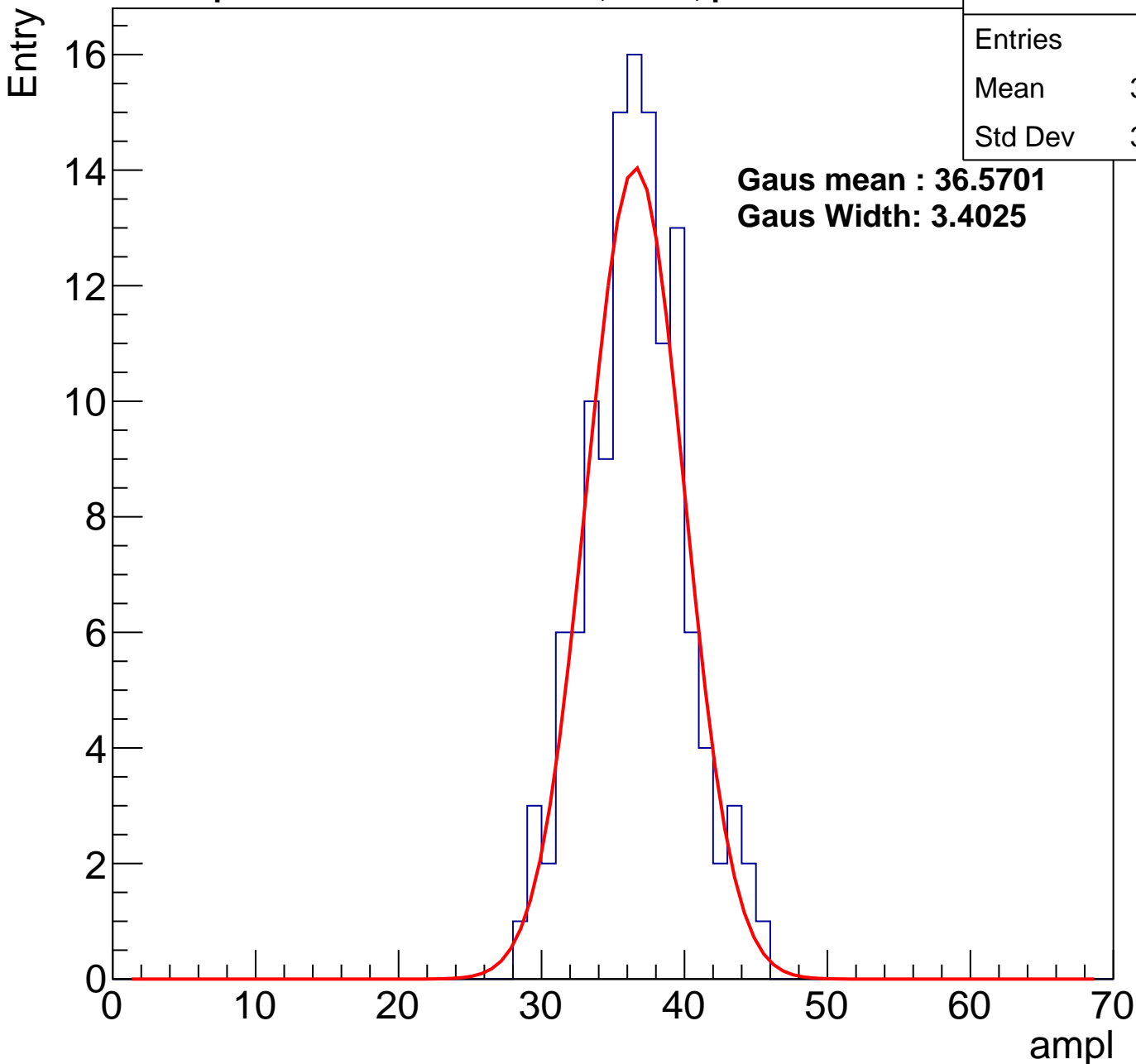
B0L101S, U25-ch2, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	36.16
Std Dev	3.426

Gaus mean : 36.5701

Gaus Width: 3.4025



B0L101S, U25-ch2, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

160

Mean

43.41

Std Dev

3.705

Gaus mean : 43.8575

Gaus Width: 3.7104

0

10

20

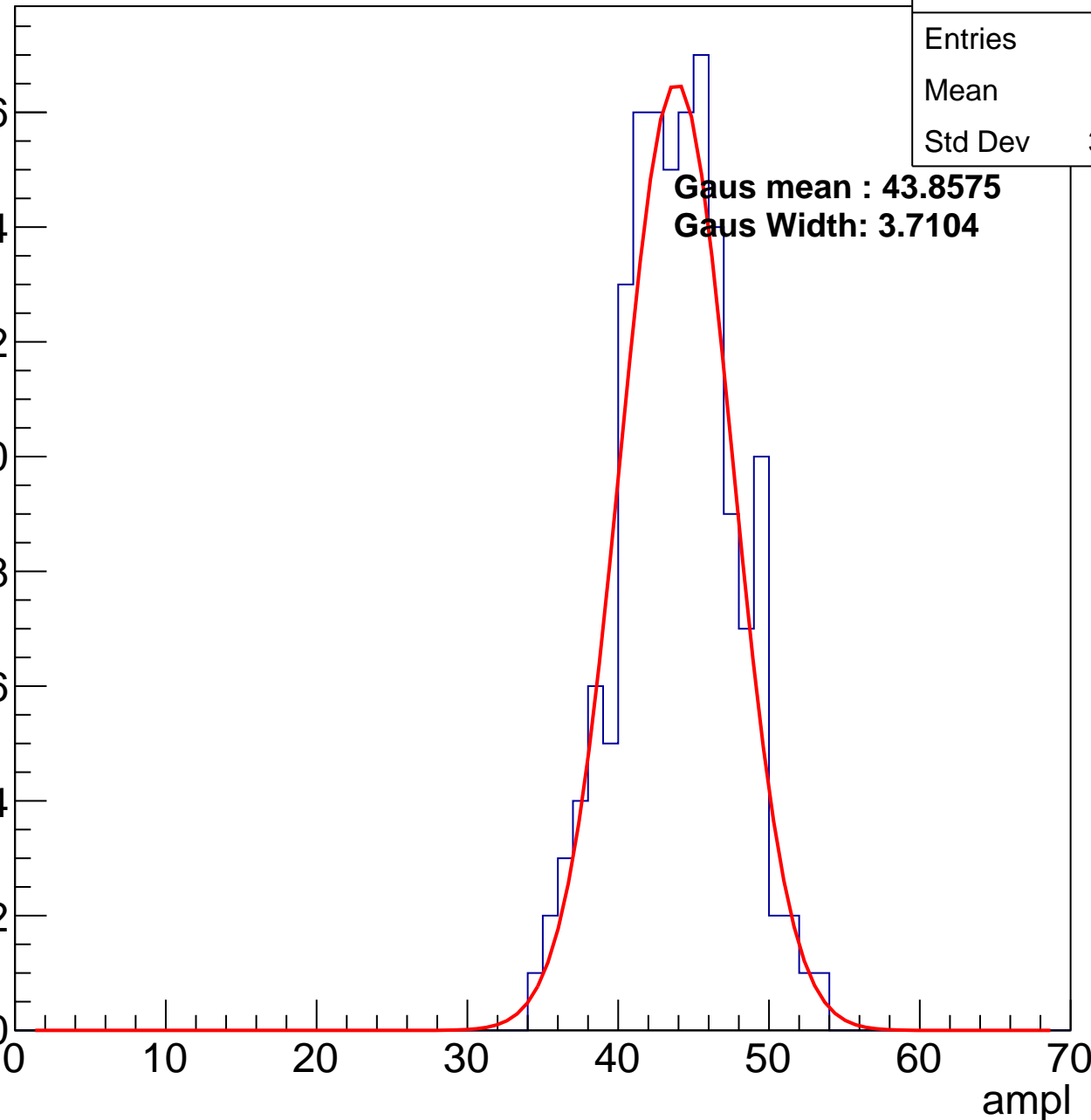
30

40

50

60

ampl

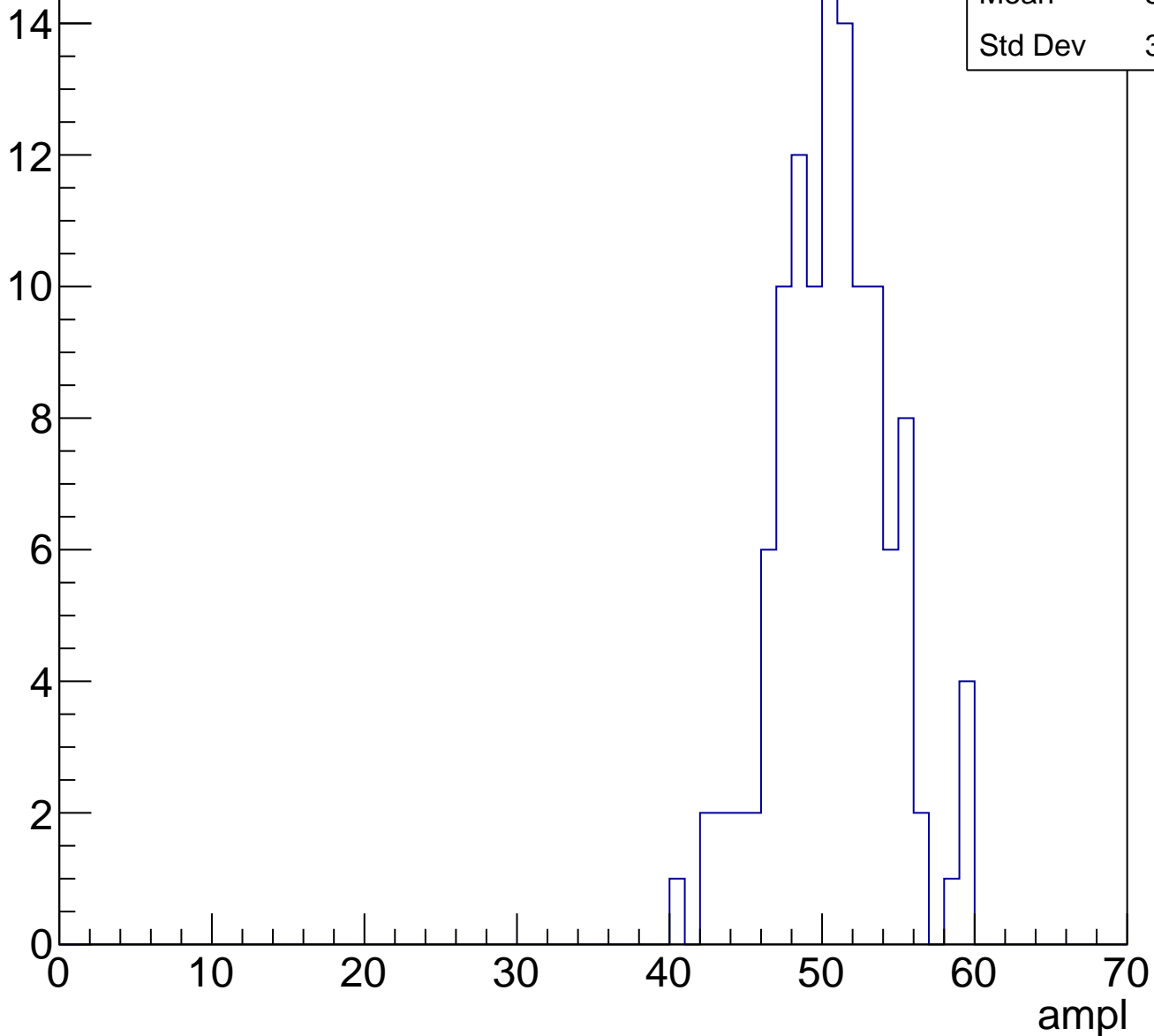


B0L101S, U25-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	50.29
Std Dev	3.677

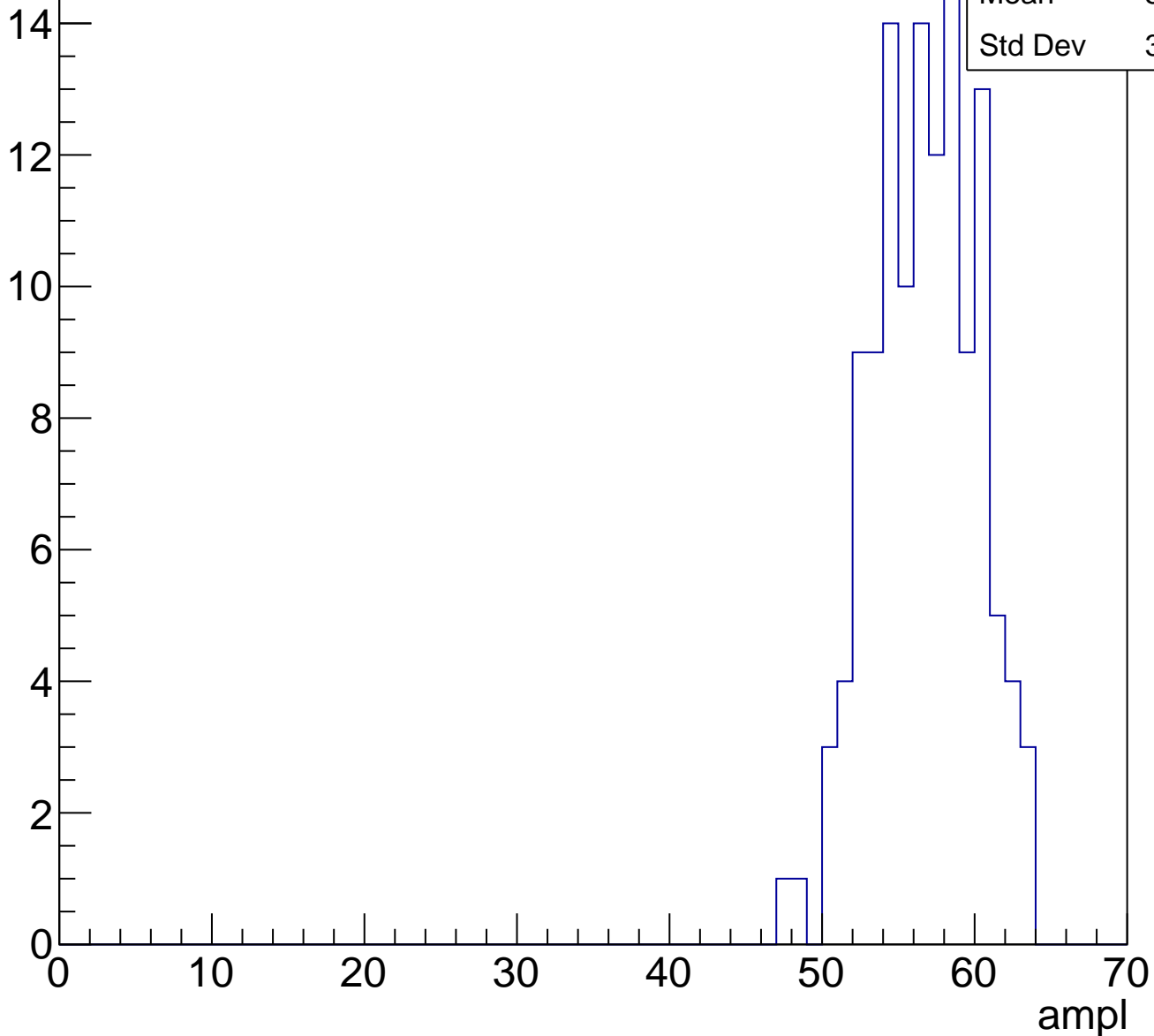
Entry



B0L101S, U25-ch2, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

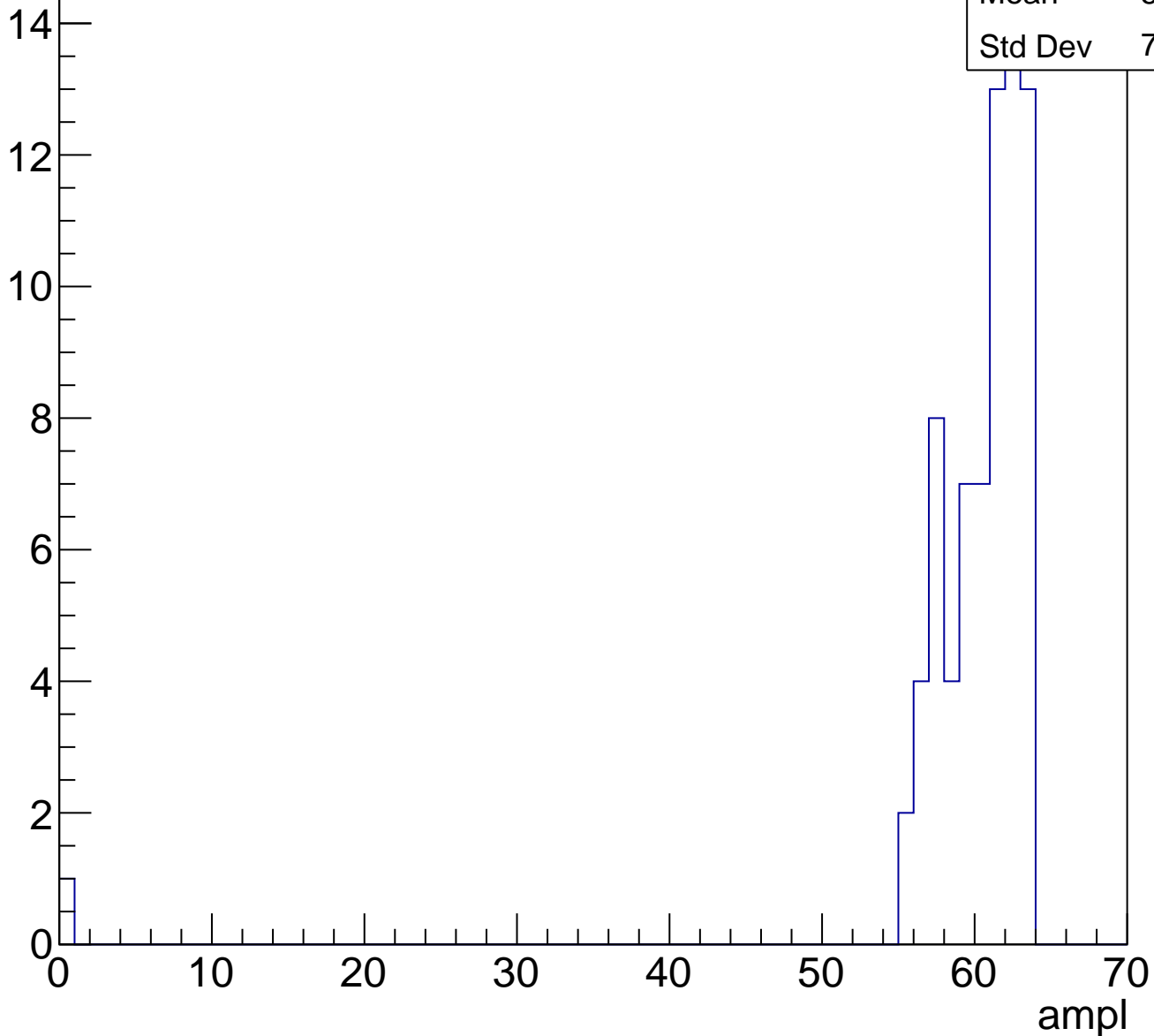


Entries	126
Mean	56.28
Std Dev	3.347

B0L101S, U25-ch2, adc5

calib_packv5_042523_0143.root, FC#1, port C1

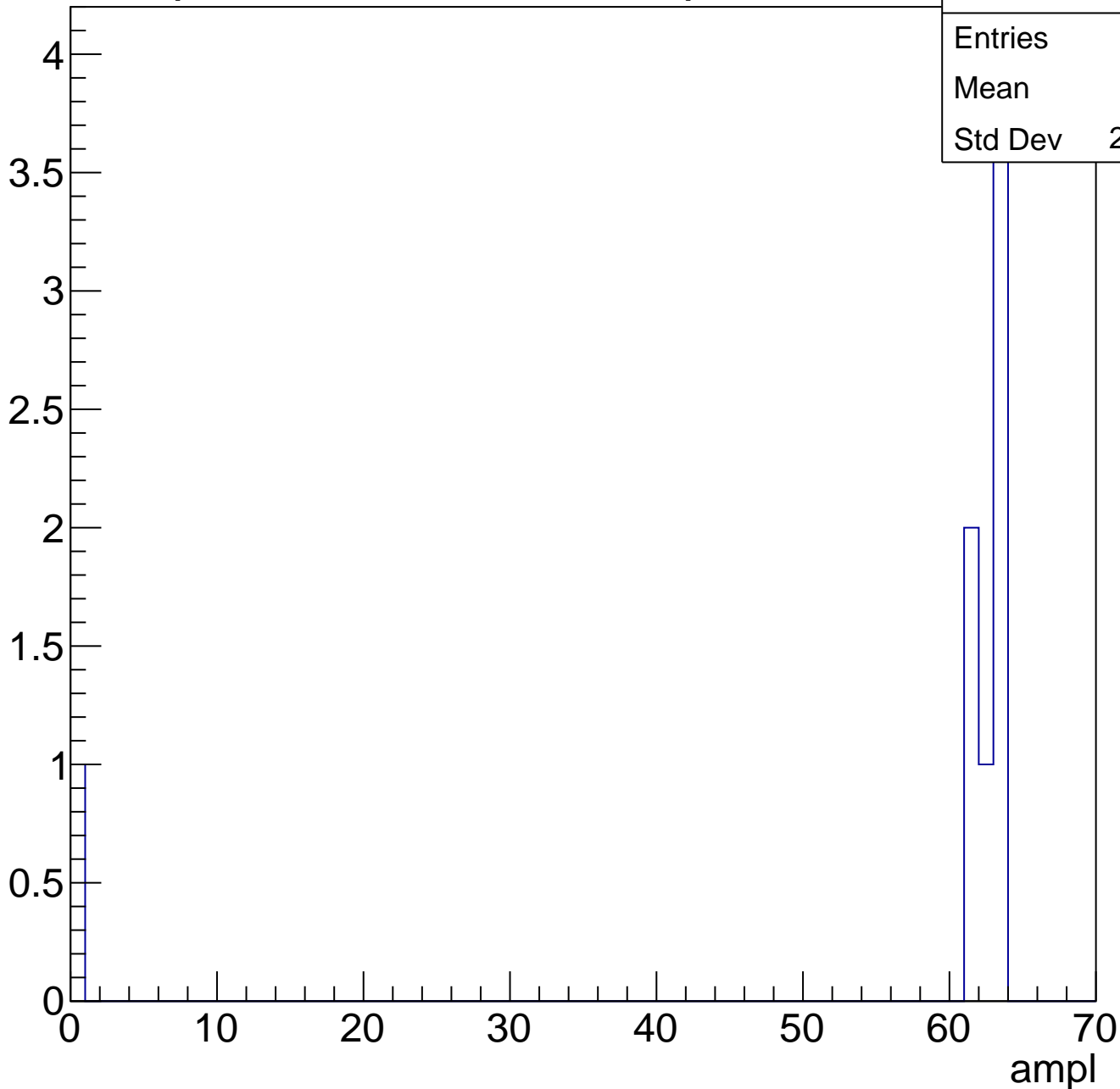
Entry



B0L101S, U25-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch3, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	28.76
Std Dev	3.69

Gaus mean : 29.0945

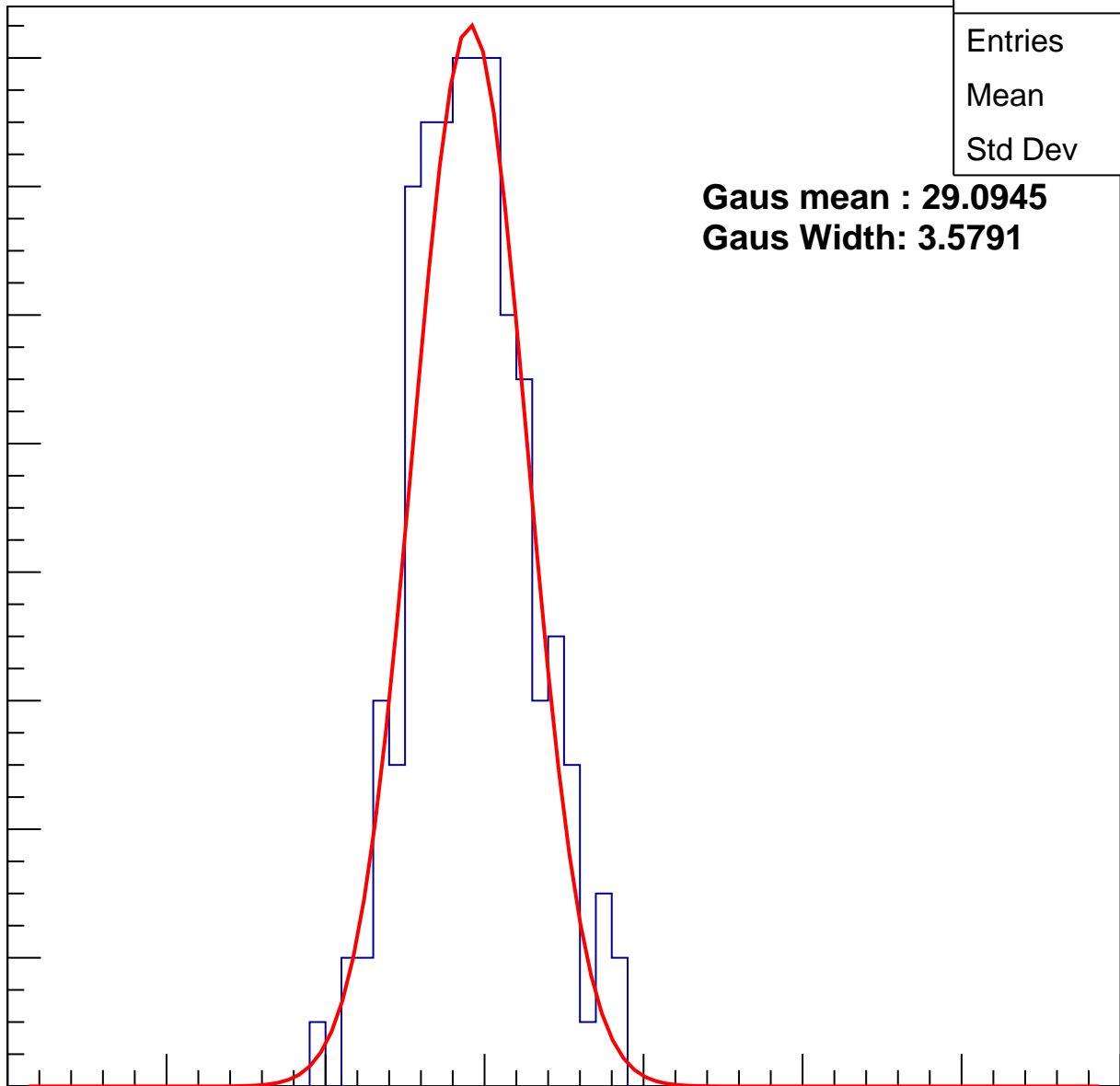
Gaus Width: 3.5791

Entry

16
14
12
10
8
6
4
2
0

ampl

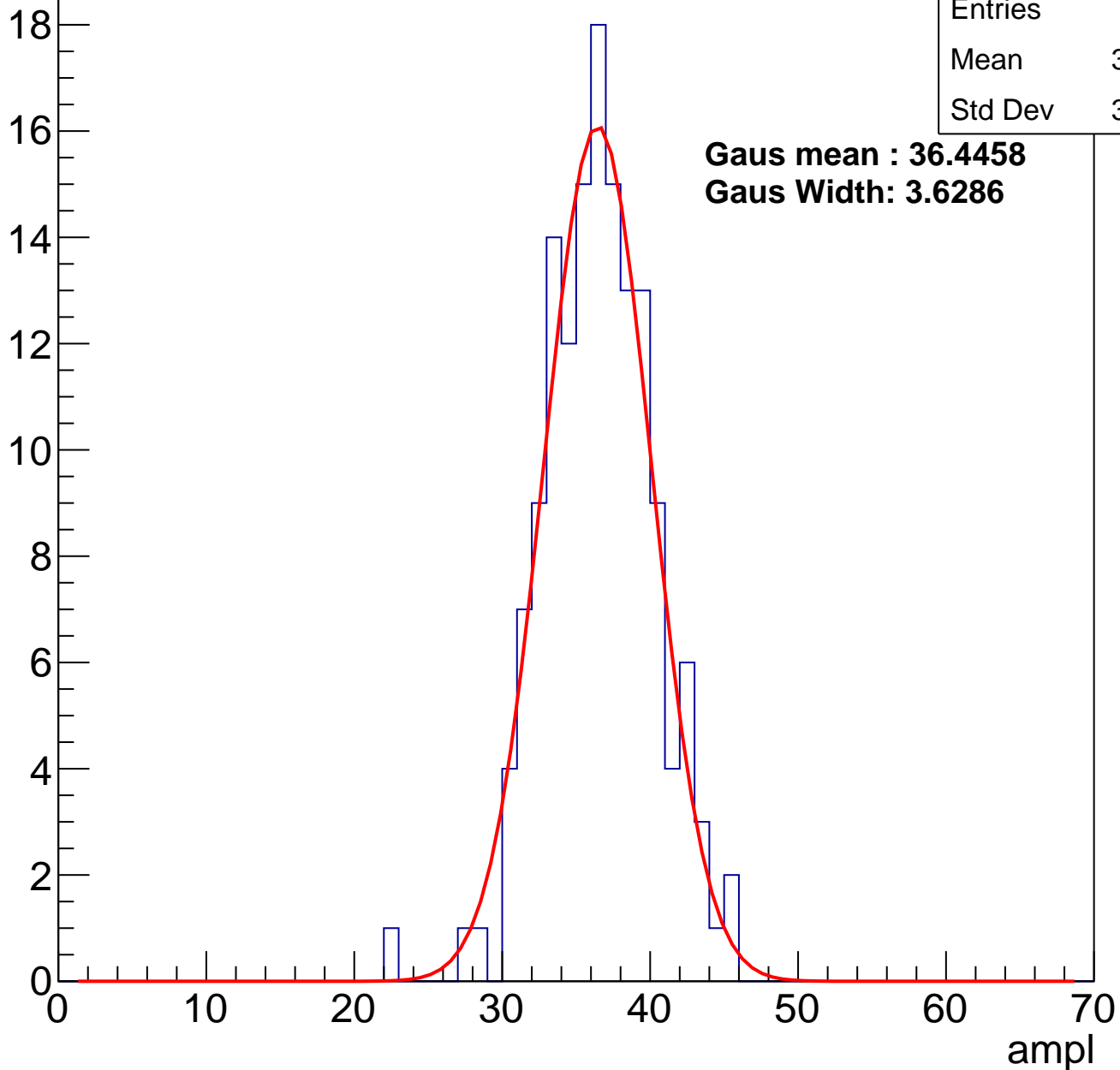
0 10 20 30 40 50 60 70



B0L101S, U25-ch3, adc1

calib_packv5_042523_0143.root, FC#1, port C1

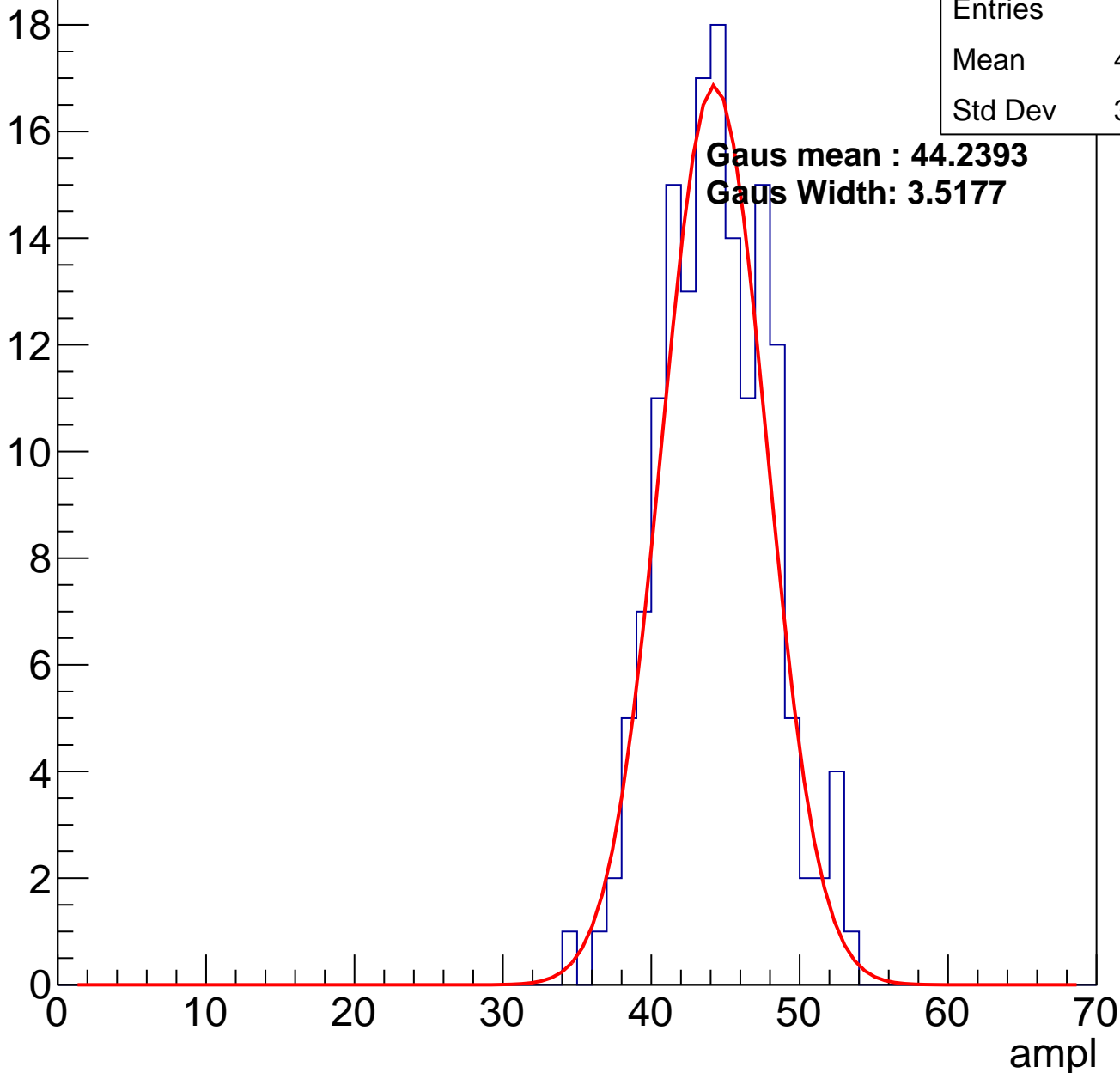
Entry



B0L101S, U25-ch3, adc2

calib_packv5_042523_0143.root, FC#1, port C1

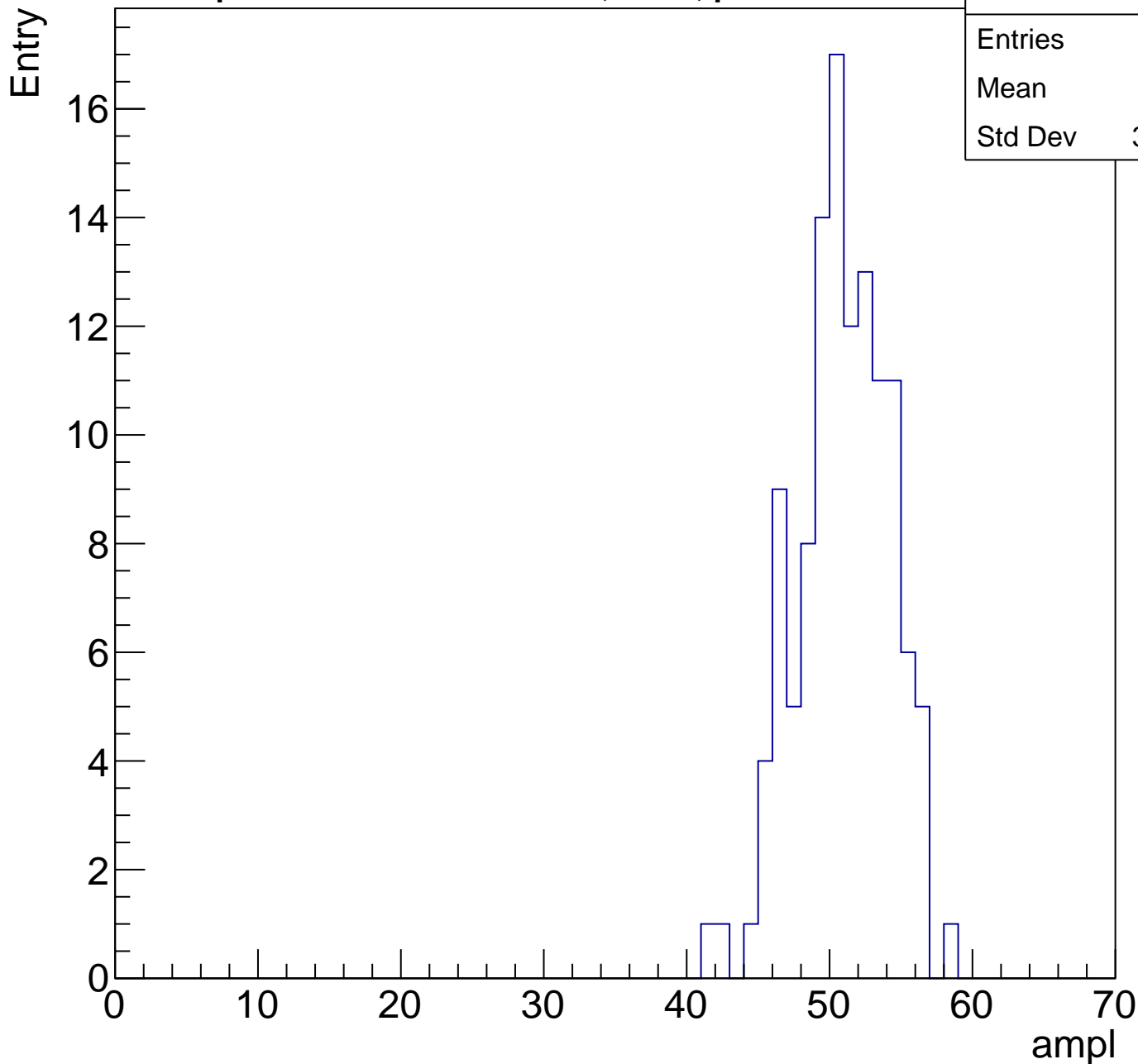
Entry



B0L101S, U25-ch3, adc3

calib_packv5_042523_0143.root, FC#1, port C1

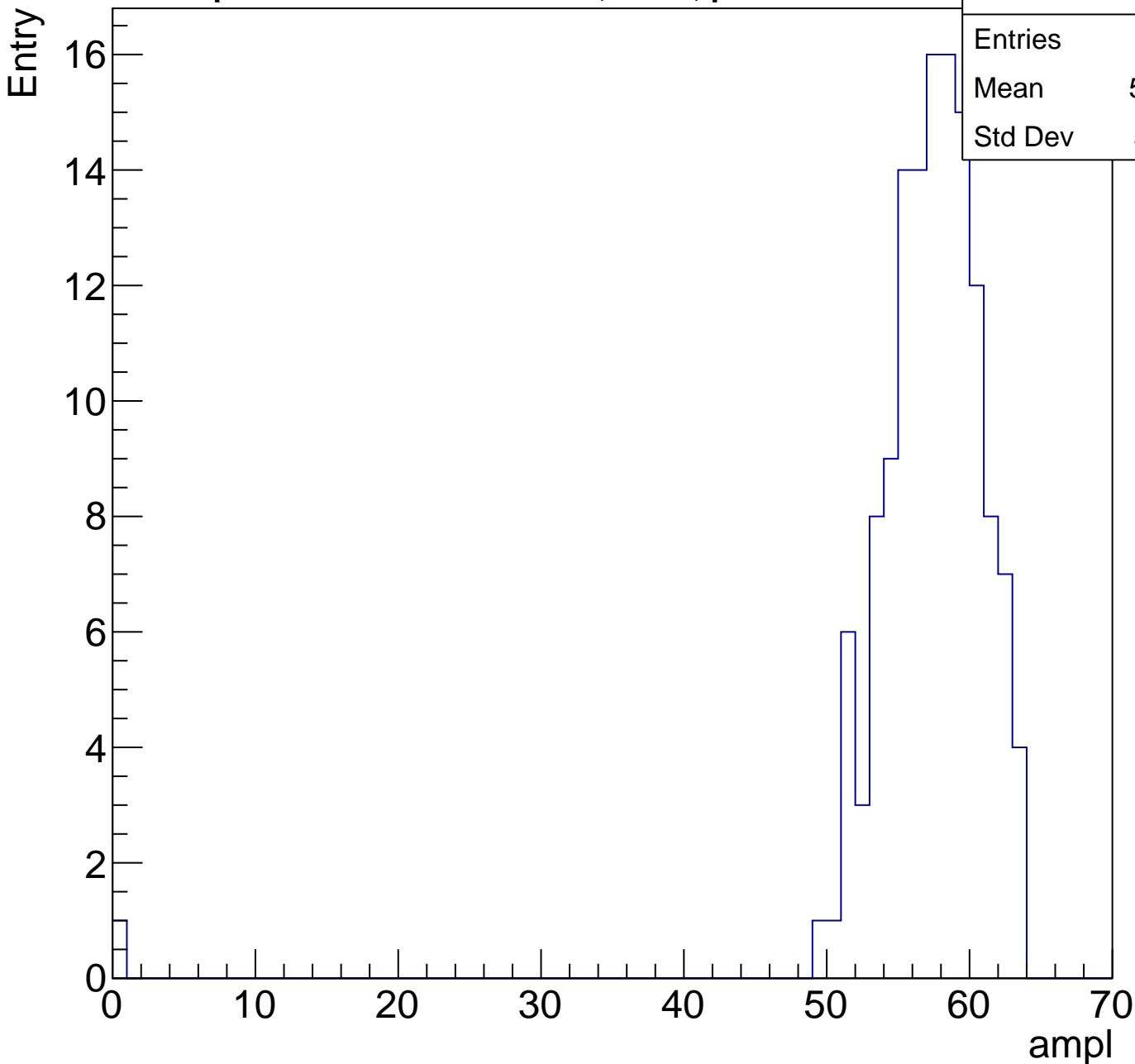
Entries	119
Mean	50.5
Std Dev	3.202



B0L101S, U25-ch3, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	56.62
Std Dev	5.801



B0L101S, U25-ch3, adc5

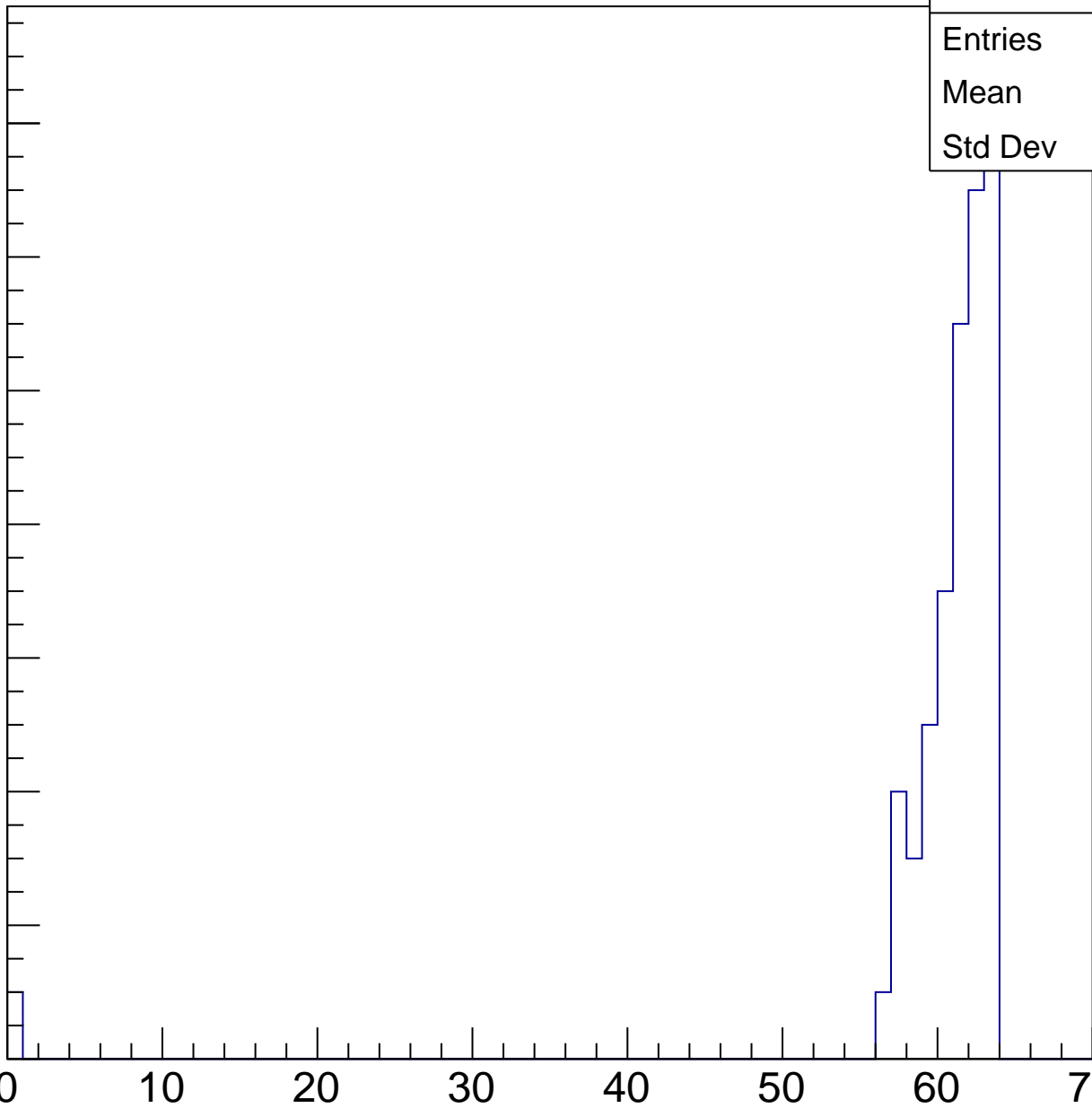
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	60
Mean	59.92
Std Dev	8.028

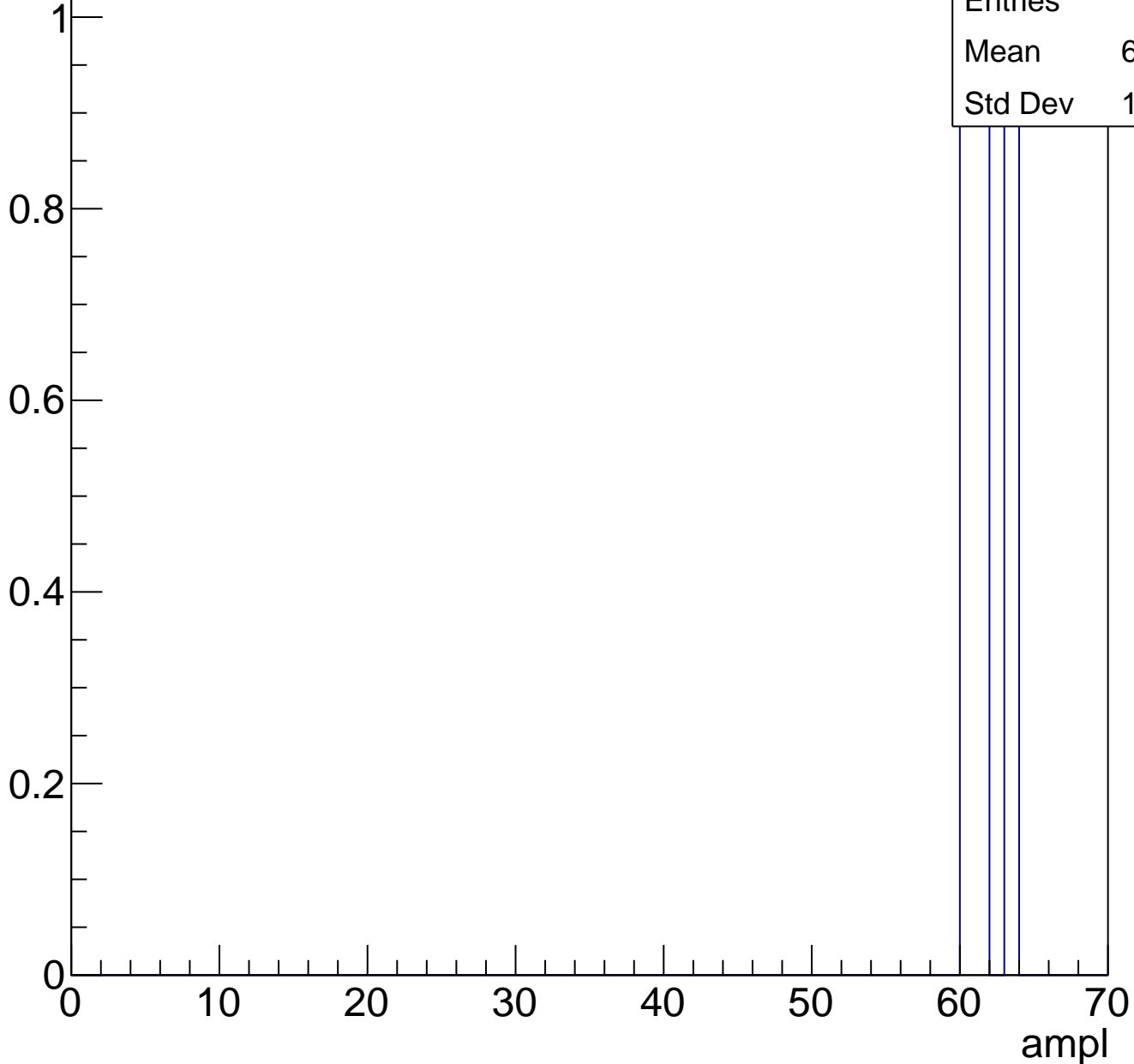
ampl



B0L101S, U25-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

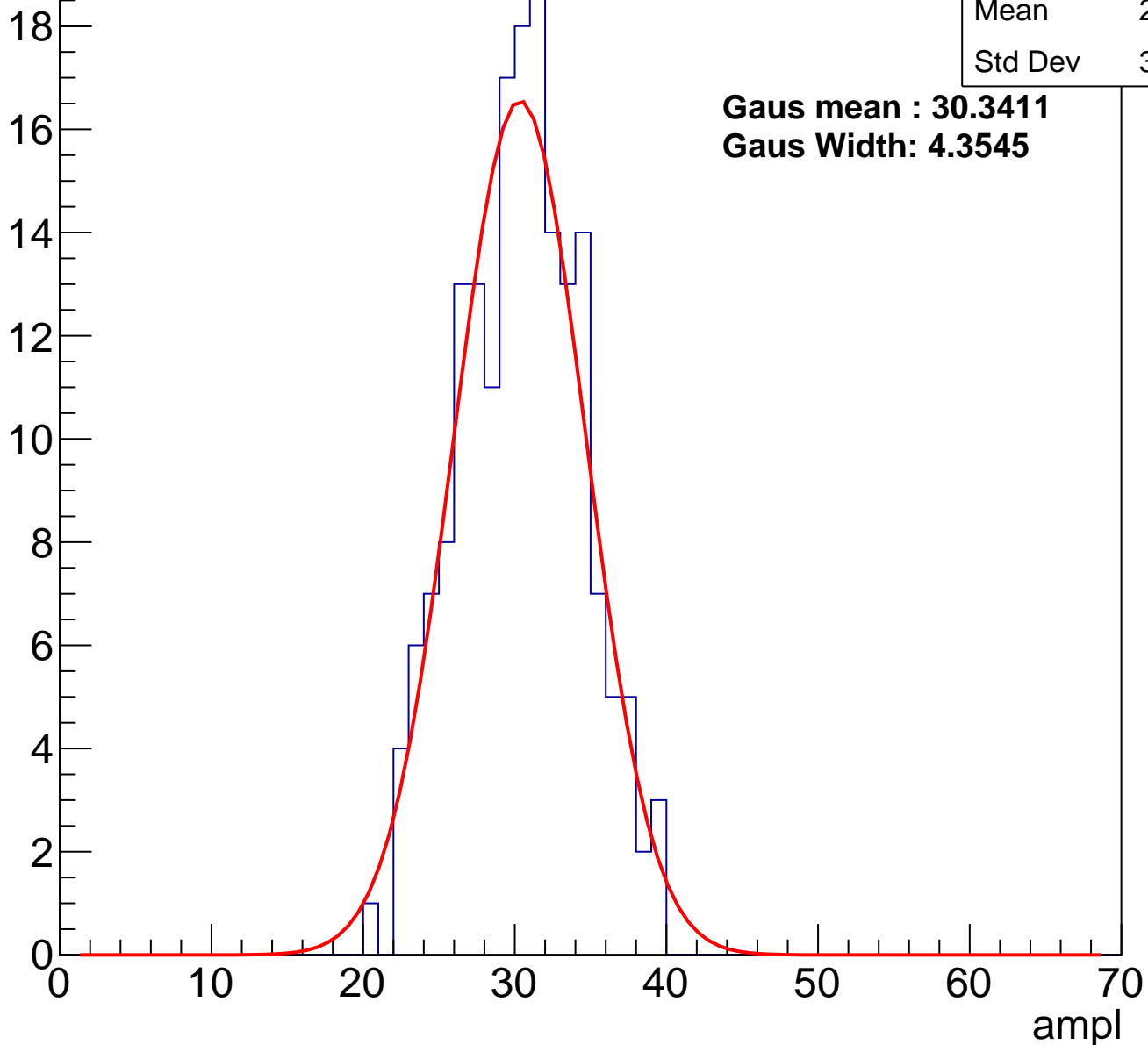


Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch4, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	37.78
Std Dev	3.298

Gaus mean : 38.3243

Gaus Width: 3.3017

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

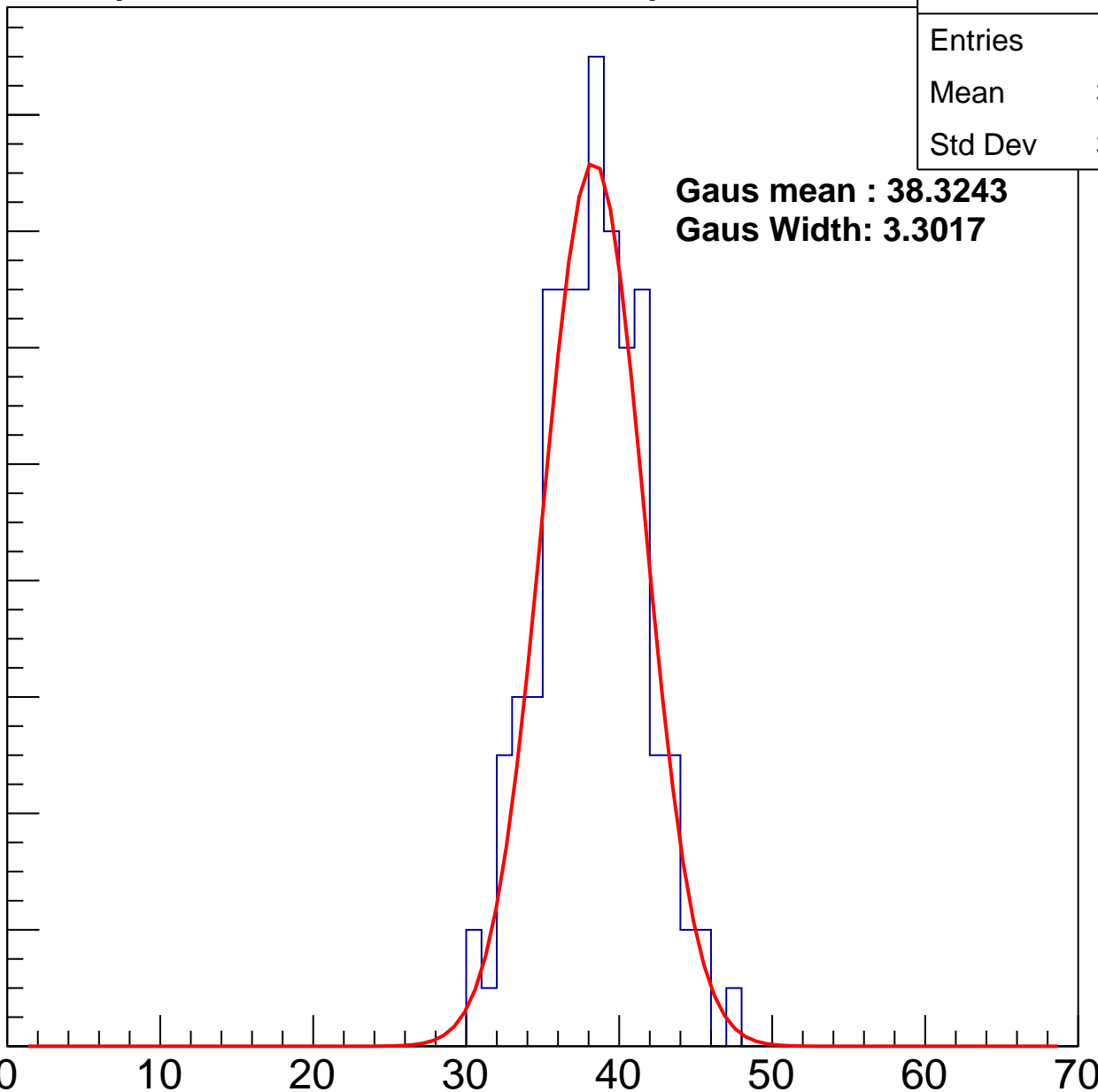
40

50

60

70

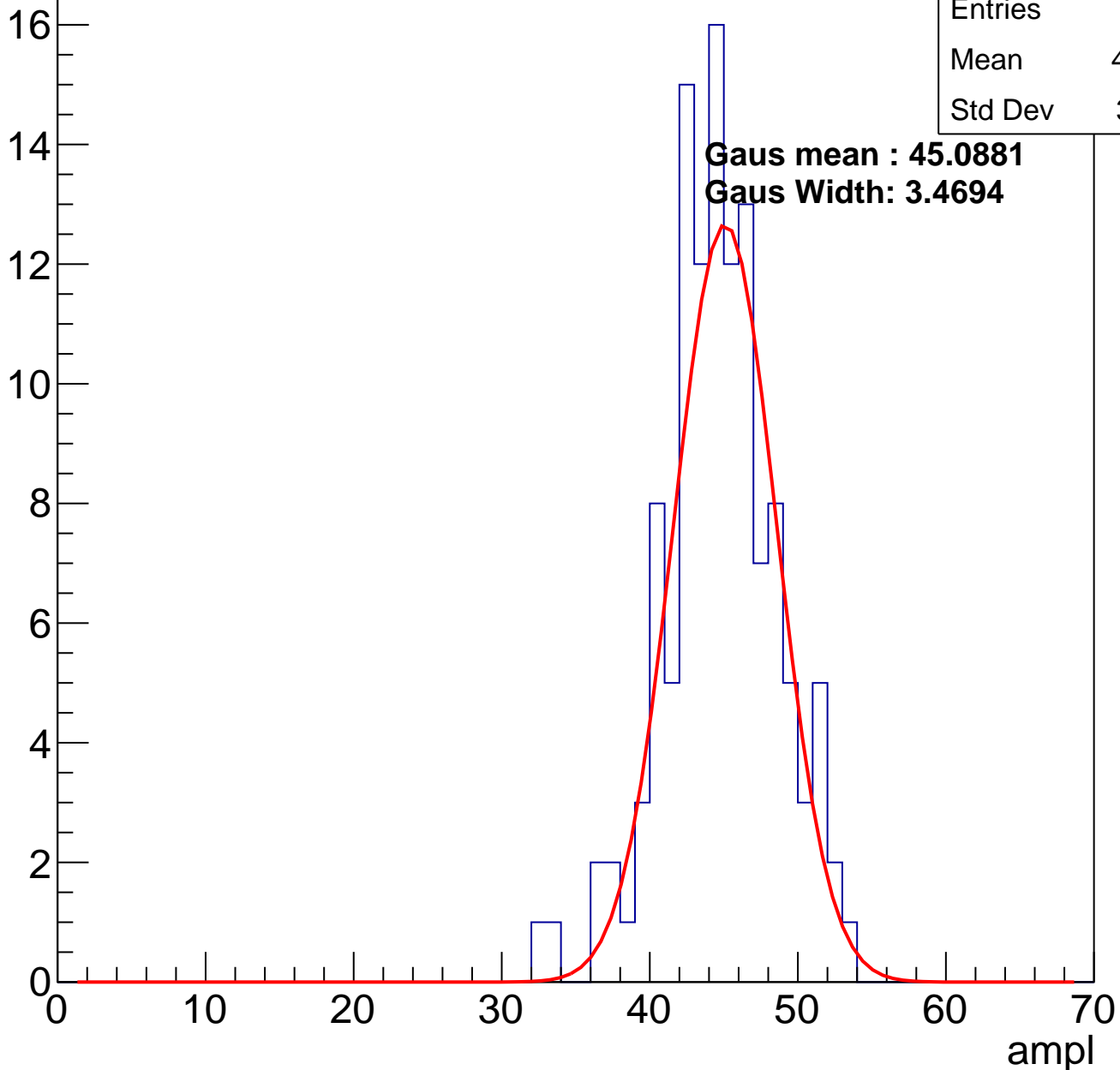
ampl



B0L101S, U25-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch4, adc3

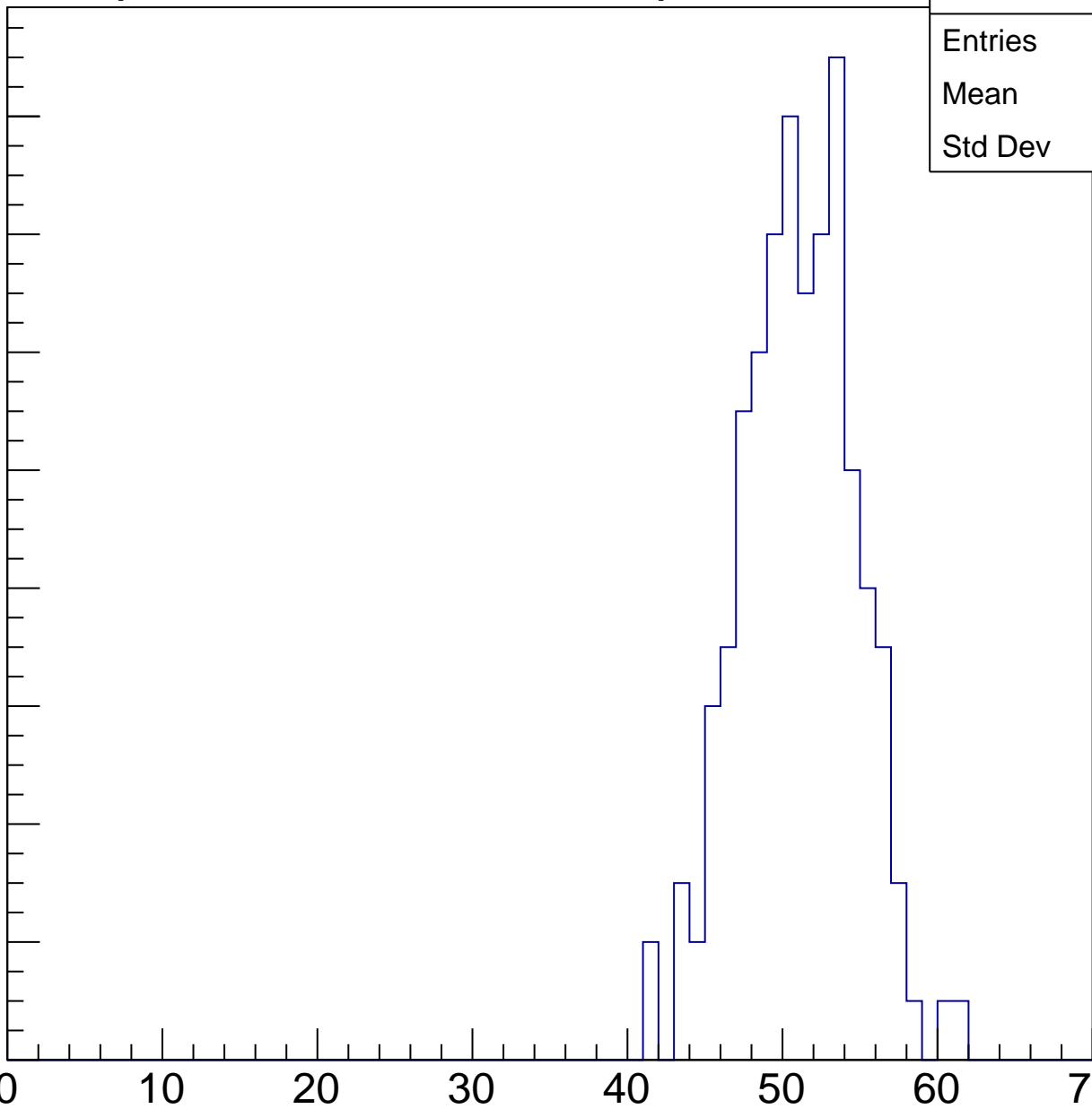
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	148
Mean	50.57
Std Dev	3.697

ampl



B0L101S, U25-ch4, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	87
Mean	55.87
Std Dev	6.768

0

2

4

6

8

10

B0L101S, U25-ch4, adc5

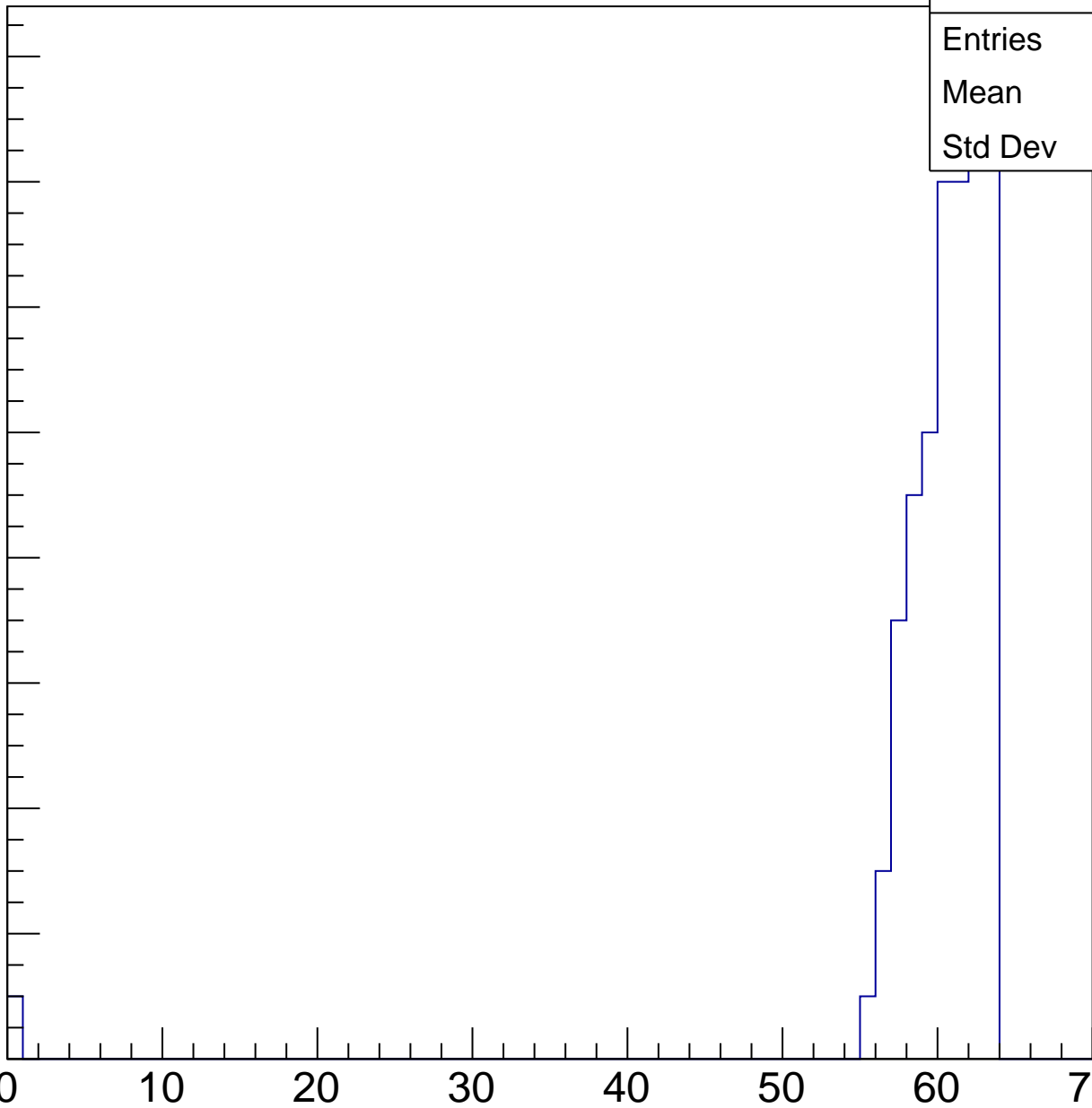
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.65
Std Dev	6.624

ampl



B0L101S, U25-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch5, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	30.48
Std Dev	5.088

Gaus mean : 31.2670

Gaus Width: 3.6471

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

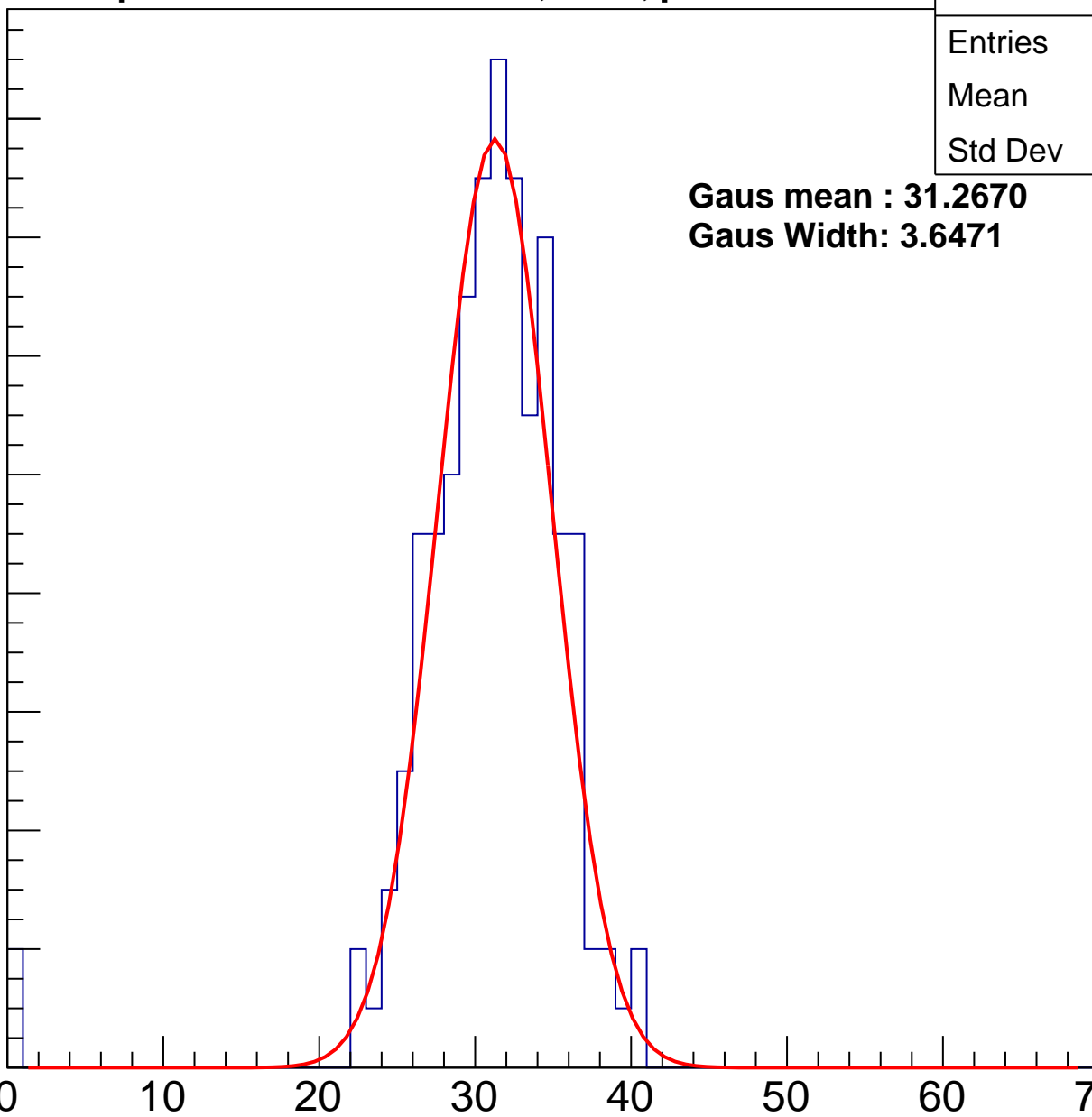
40

50

60

70

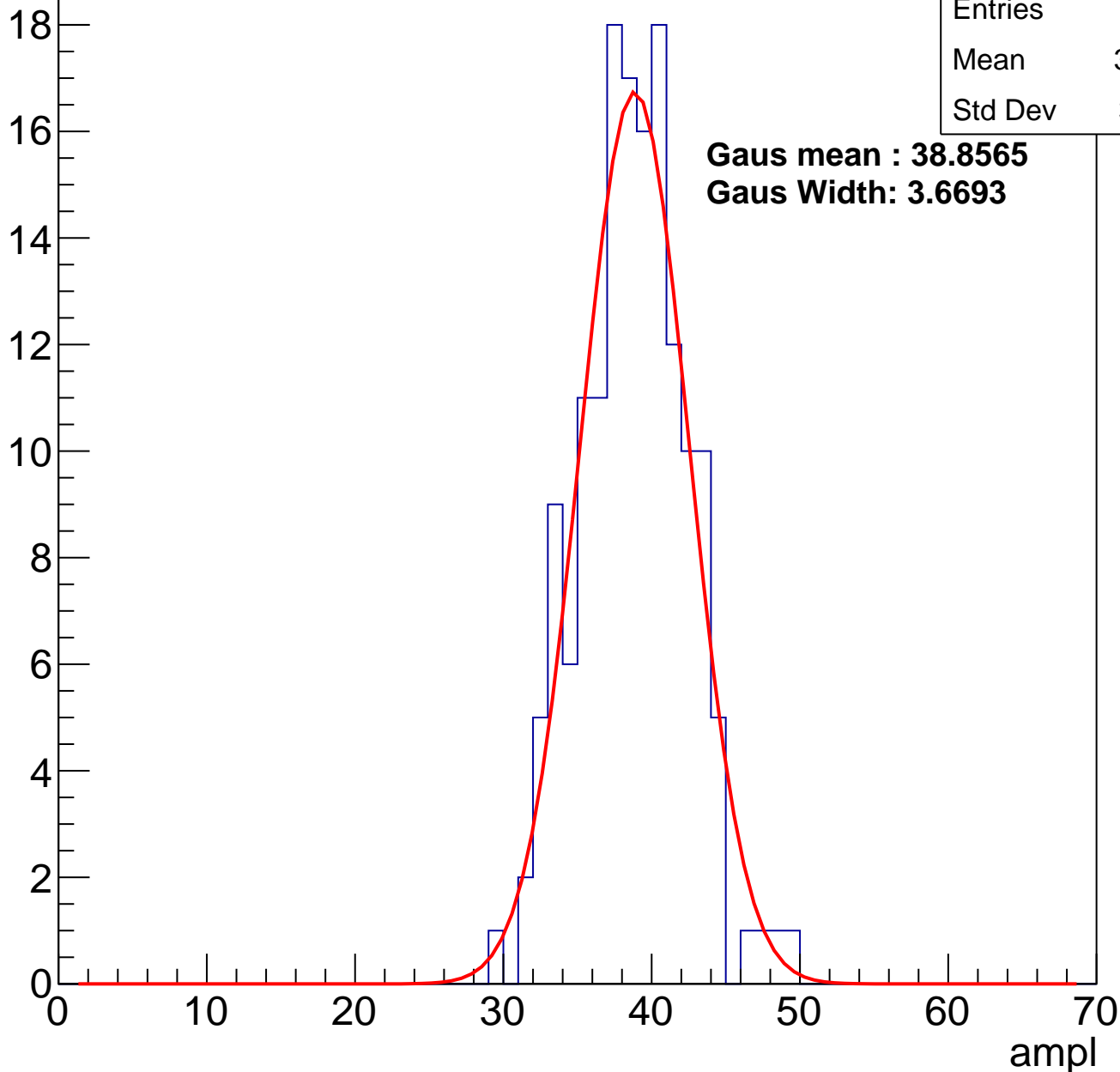
ampl



B0L101S, U25-ch5, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch5, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

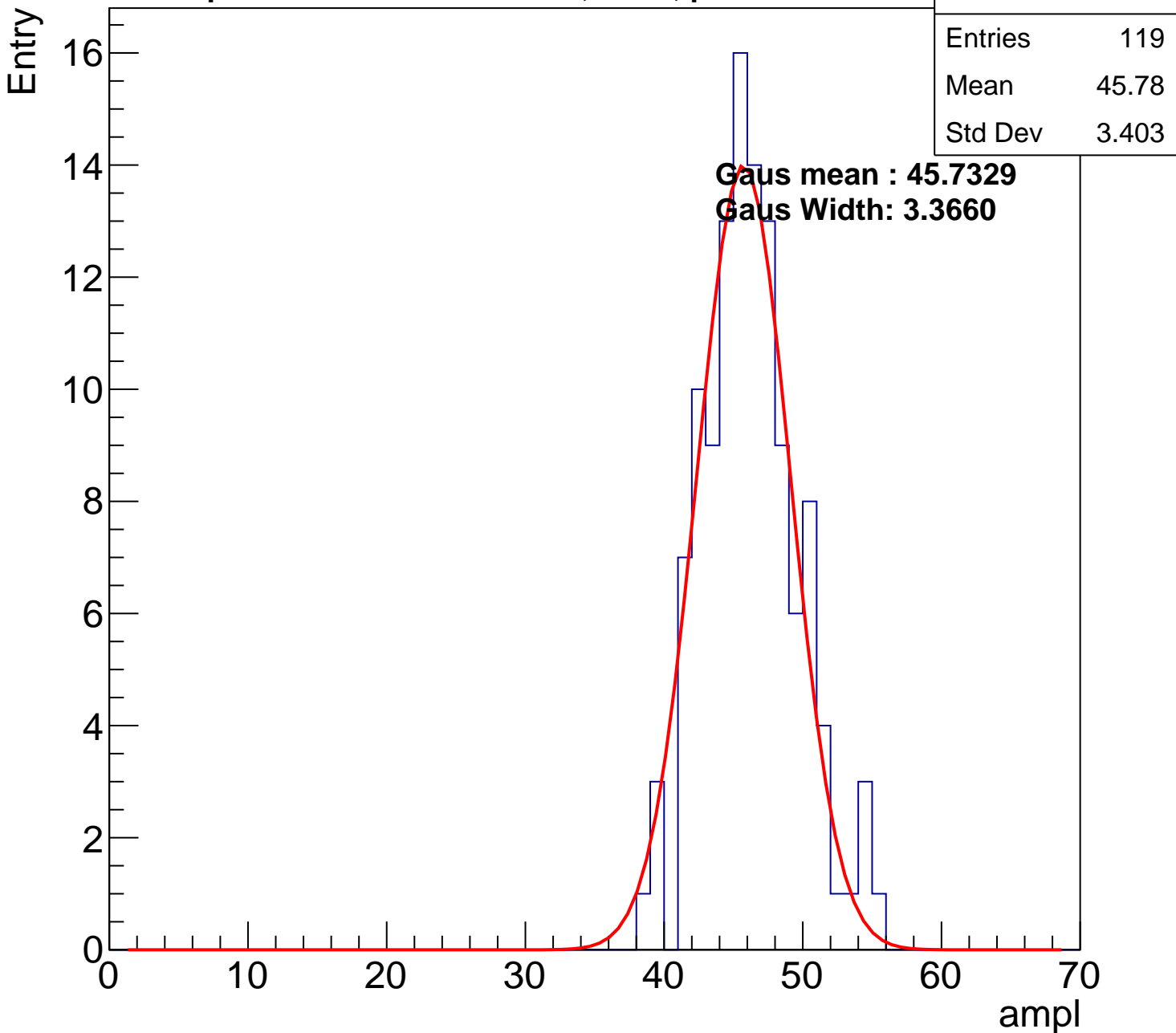
Entries	119
Mean	45.78
Std Dev	3.403

Gaus mean : 45.7329

Gaus Width: 3.3660

ampl

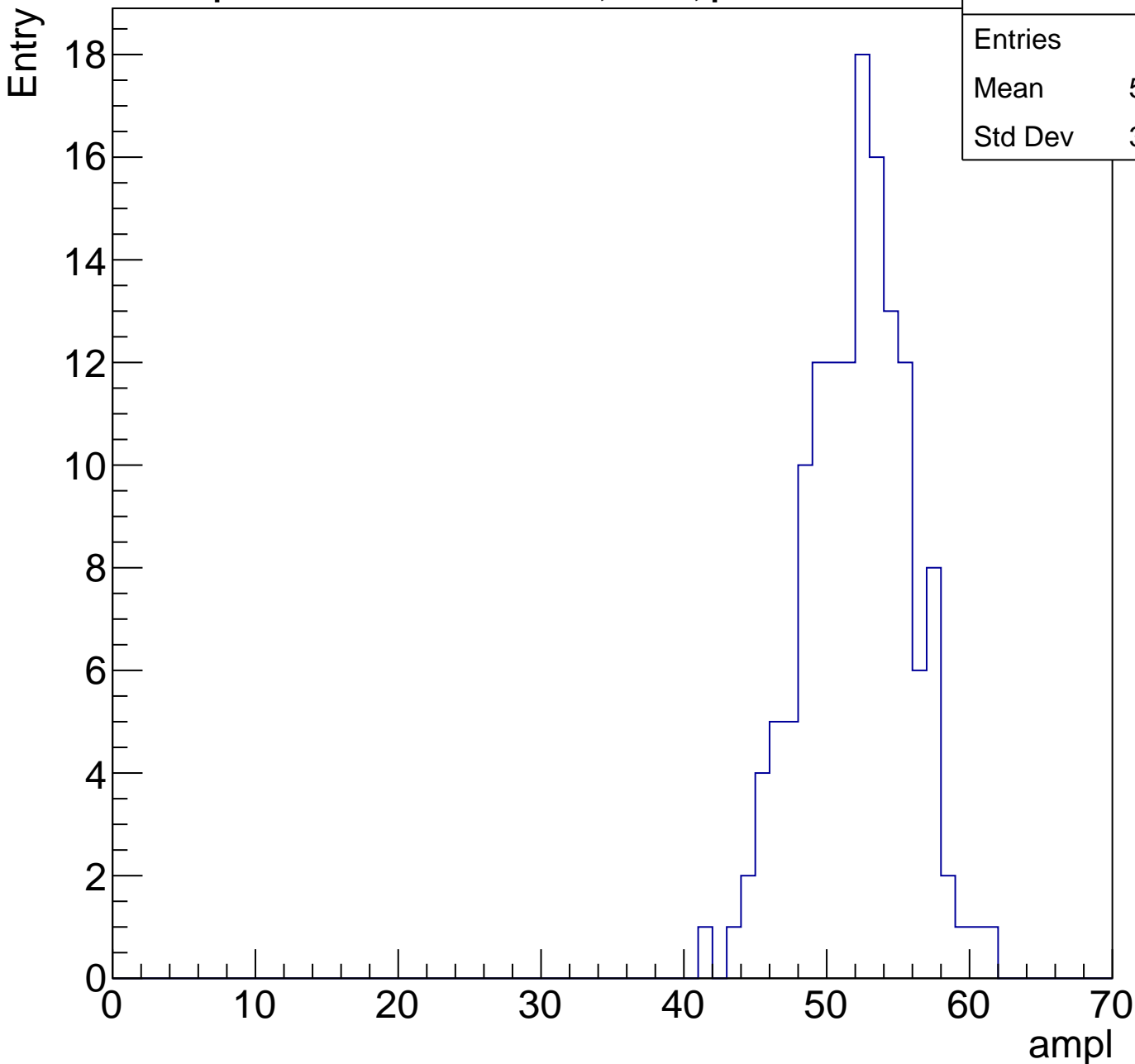
0 10 20 30 40 50 60 70



B0L101S, U25-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	51.63
Std Dev	3.636

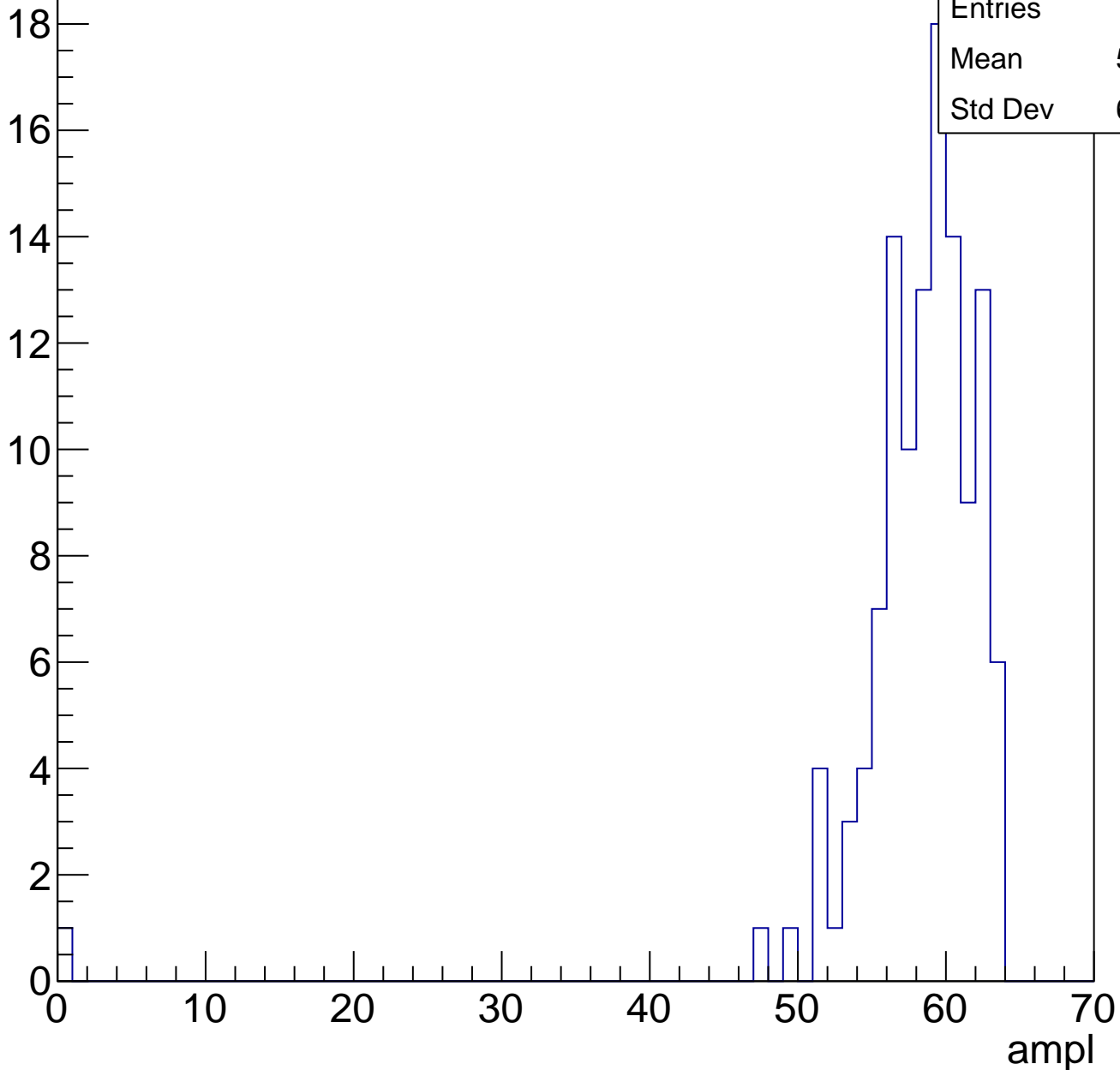


B0L101S, U25-ch5, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	57.61
Std Dev	6.191

Entry



B0L101S, U25-ch5, adc5

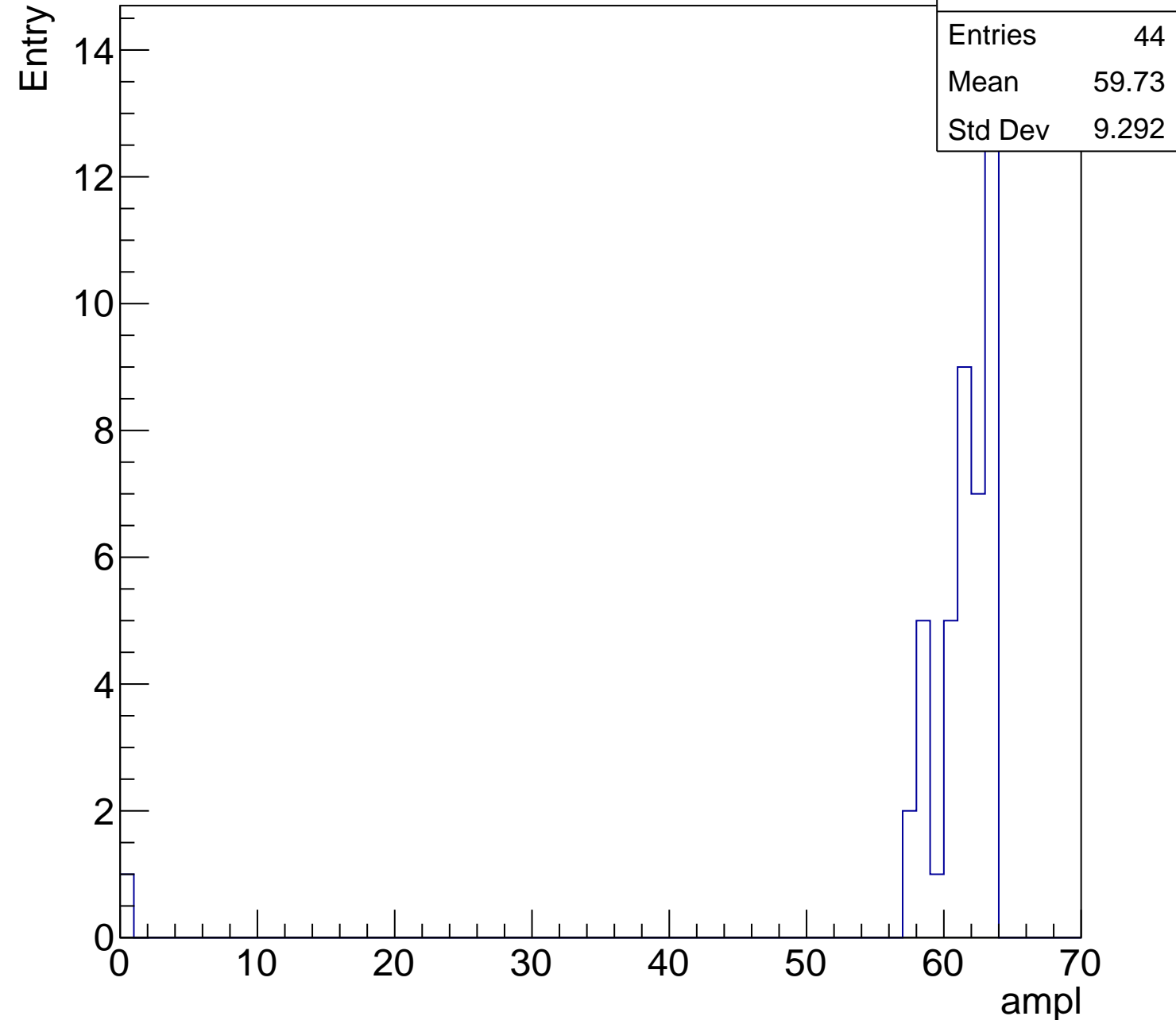
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	44
Mean	59.73
Std Dev	9.292

ampl



B0L101S, U25-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch6, adc0

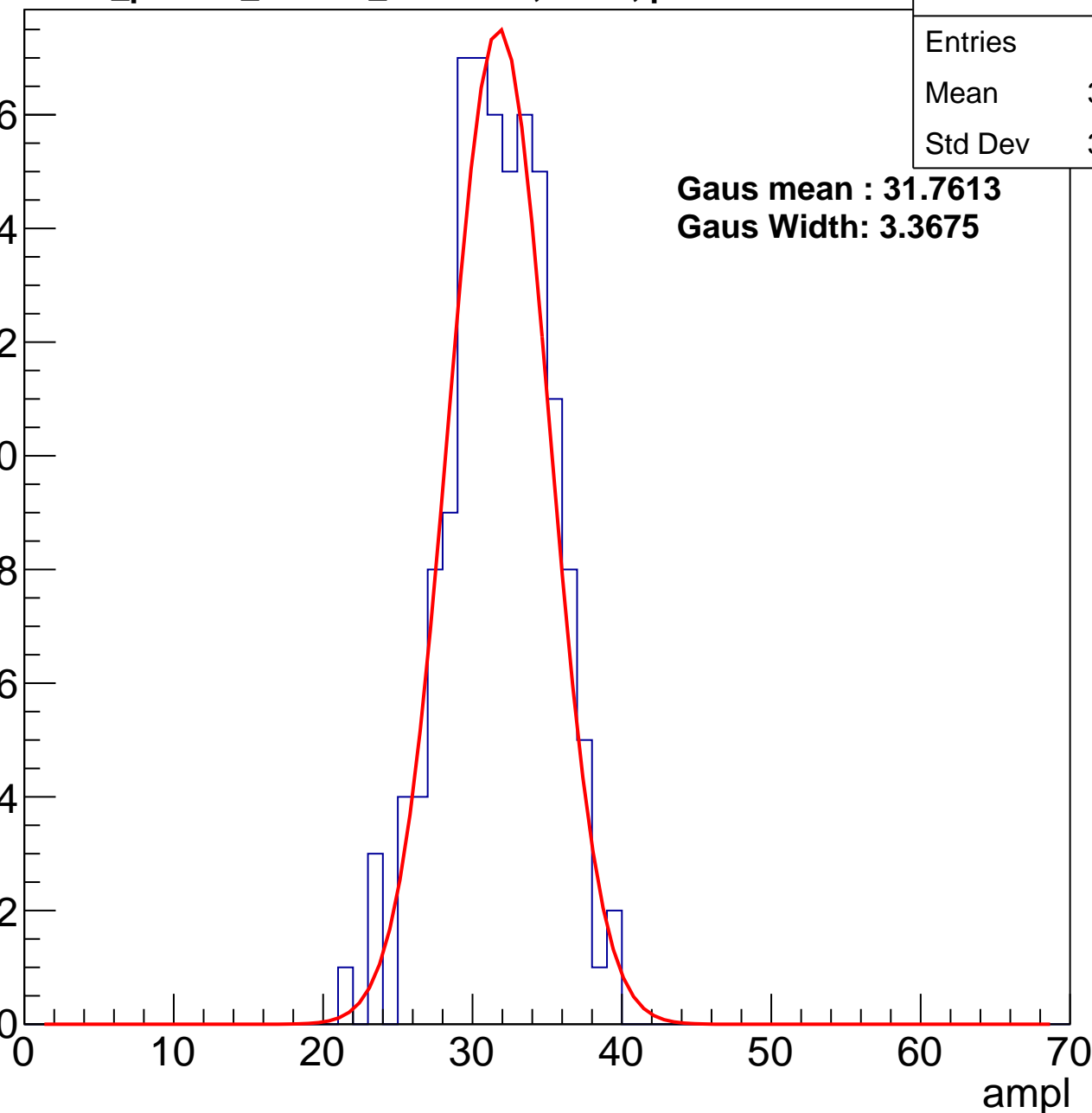
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	152
Mean	31.27
Std Dev	3.409

Gaus mean : 31.7613
Gaus Width: 3.3675



B0L101S, U25-ch6, adc1

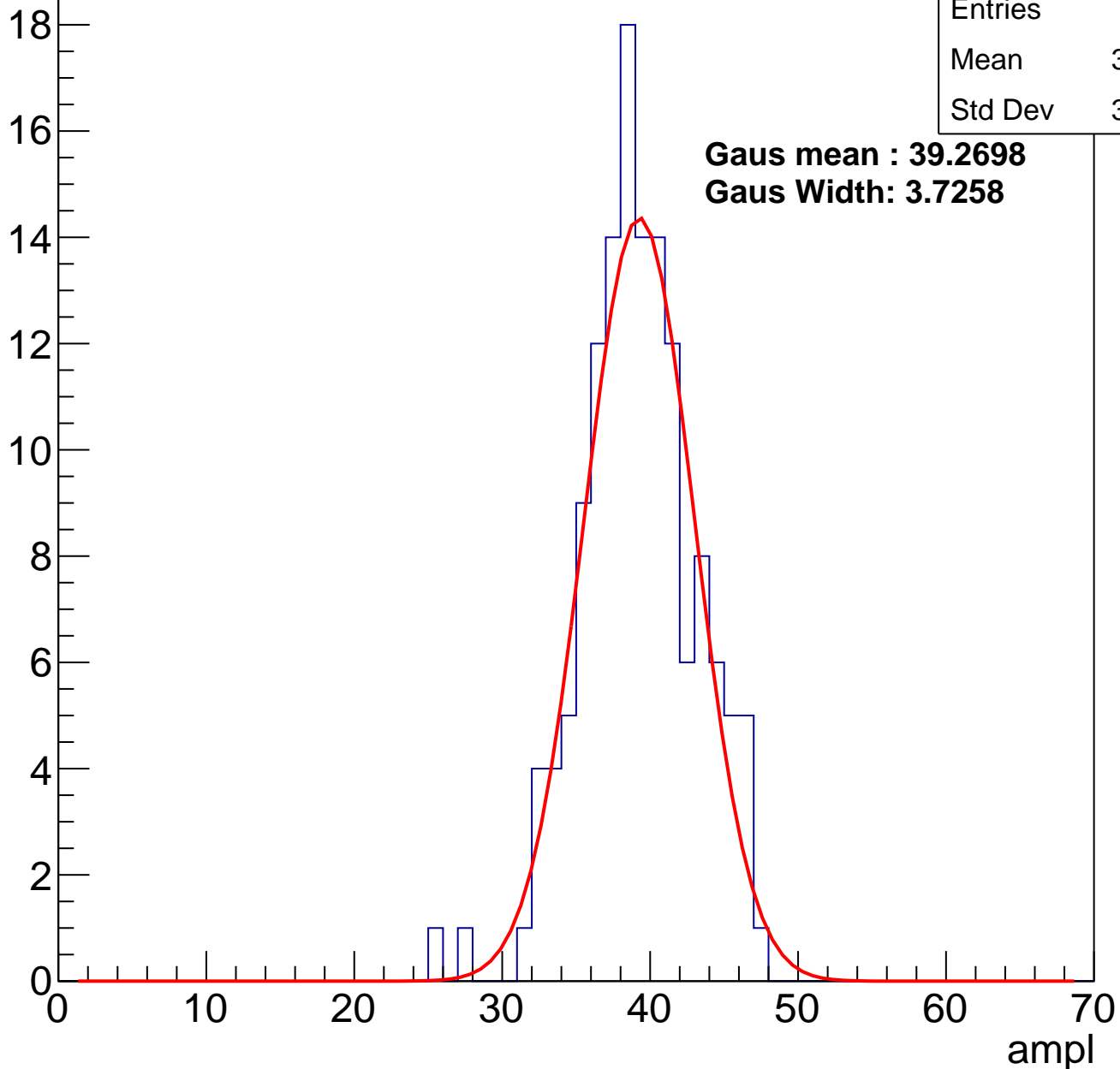
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	38.73
Std Dev	3.843

Gaus mean : 39.2698

Gaus Width: 3.7258

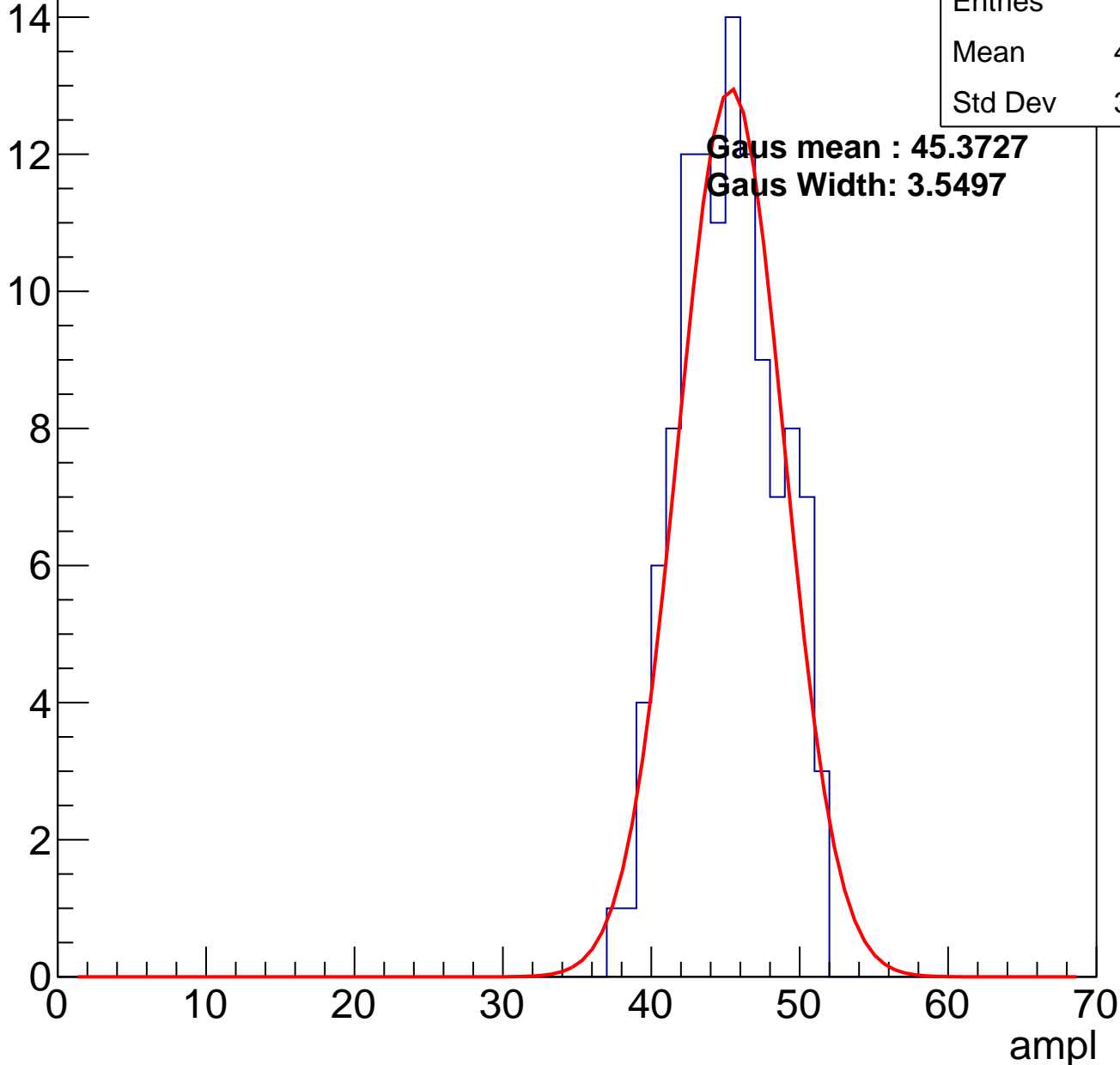
Entry



B0L101S, U25-ch6, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch6, adc3

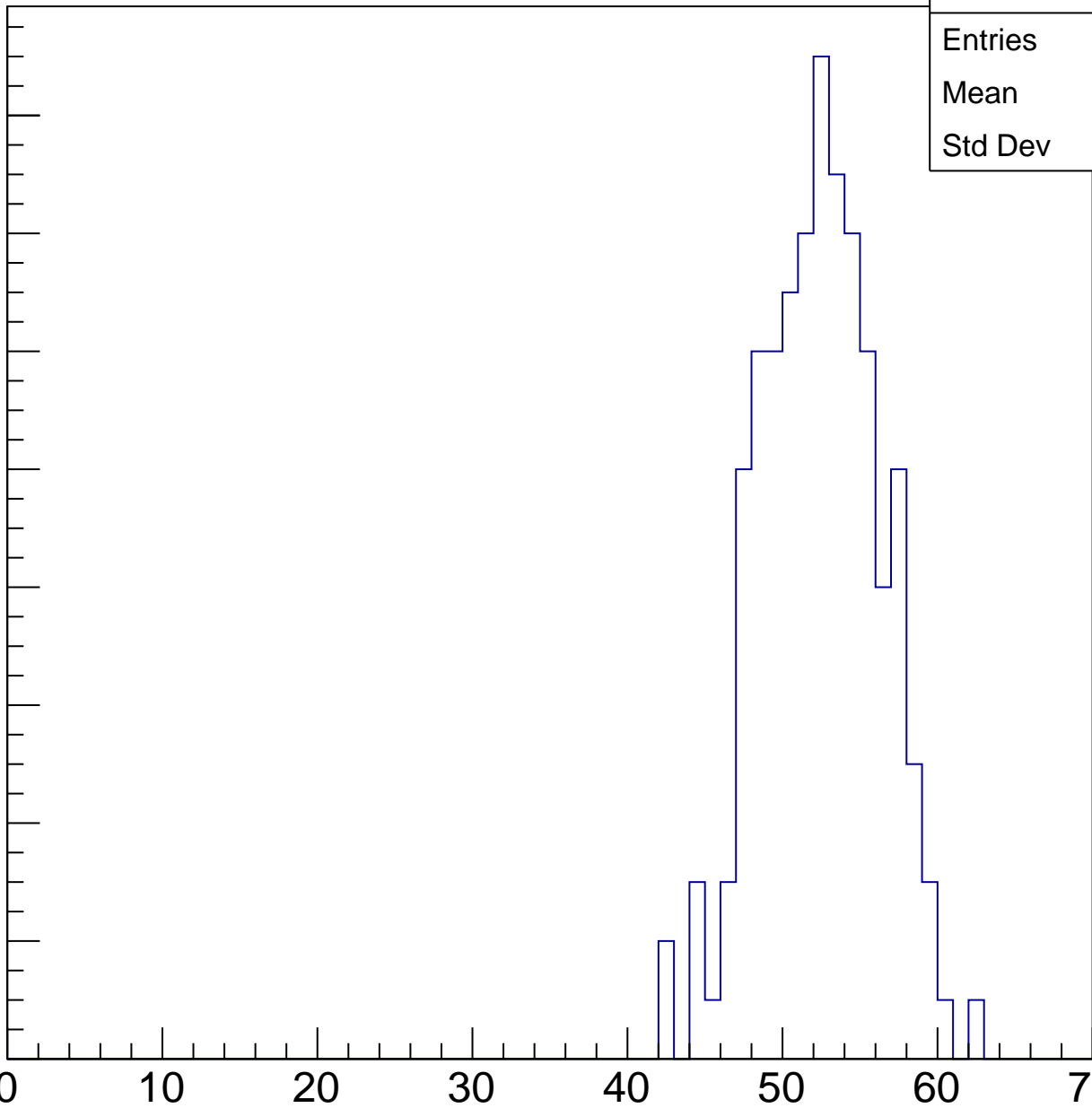
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	156
Mean	51.92
Std Dev	3.765

ampl



B0L101S, U25-ch6, adc4

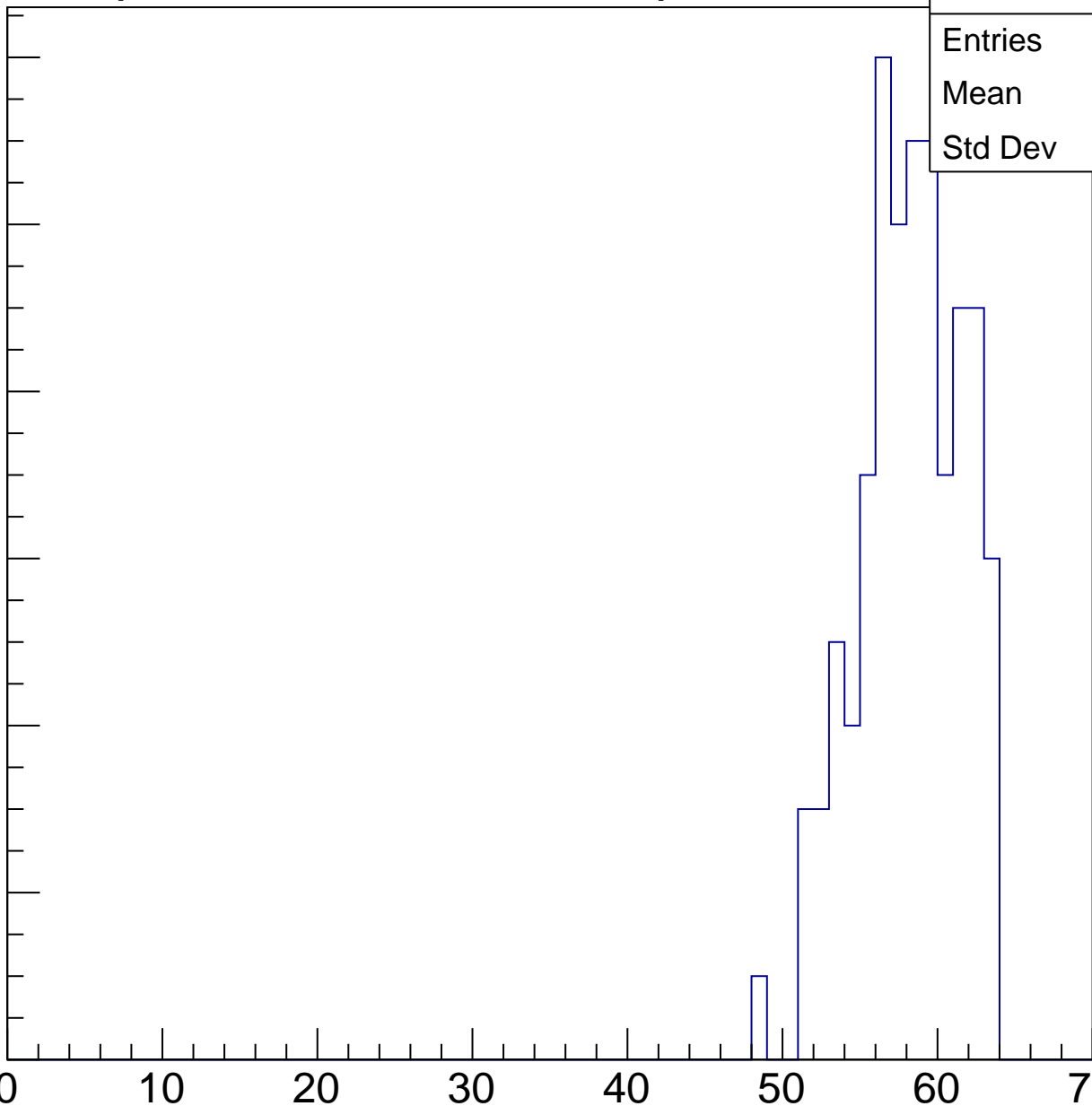
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	98
Mean	57.72
Std Dev	3.307

ampl



B0L101S, U25-ch6, adc5

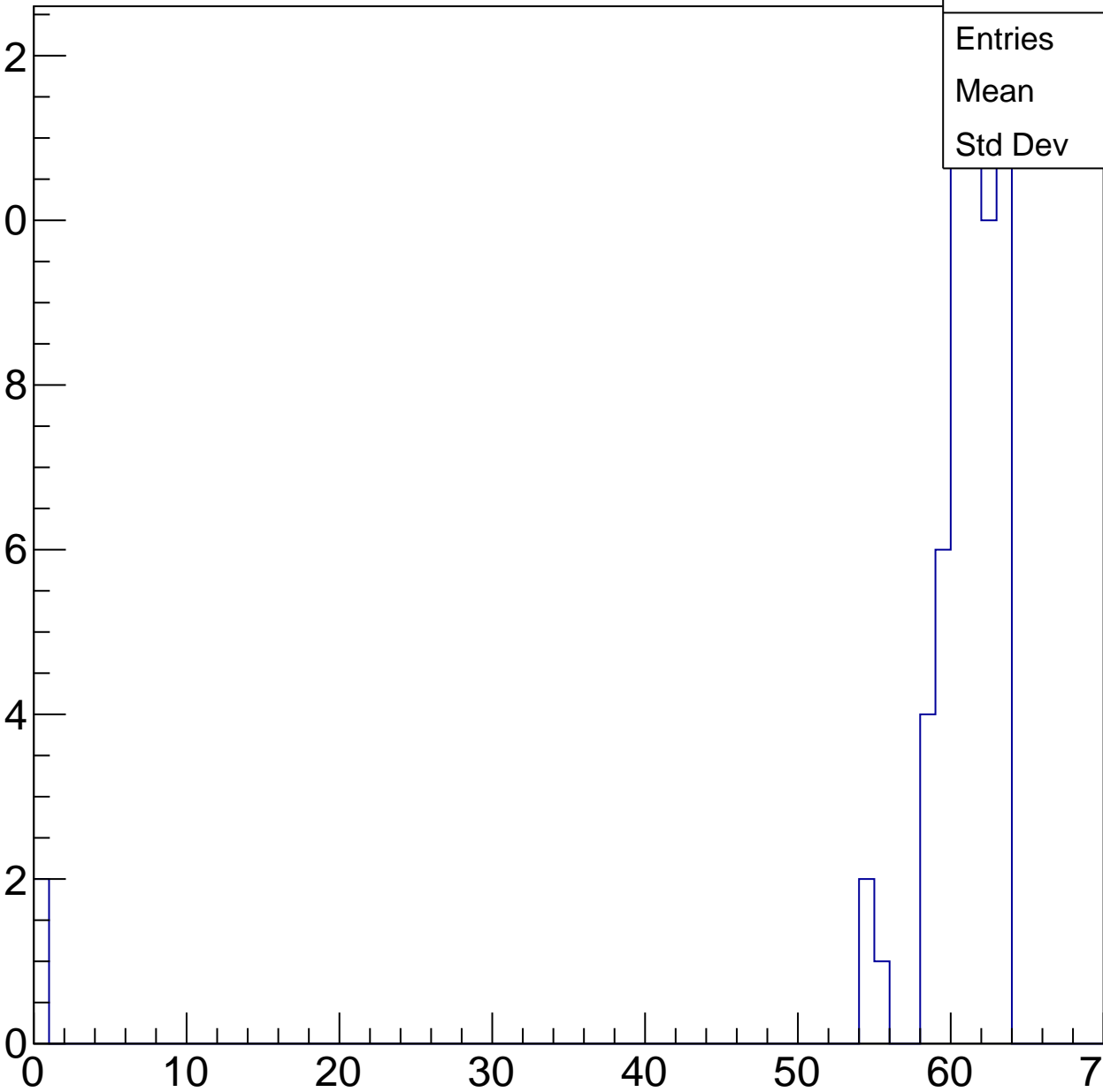
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	59
Mean	58.58
Std Dev	11.17

ampl



B0L101S, U25-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

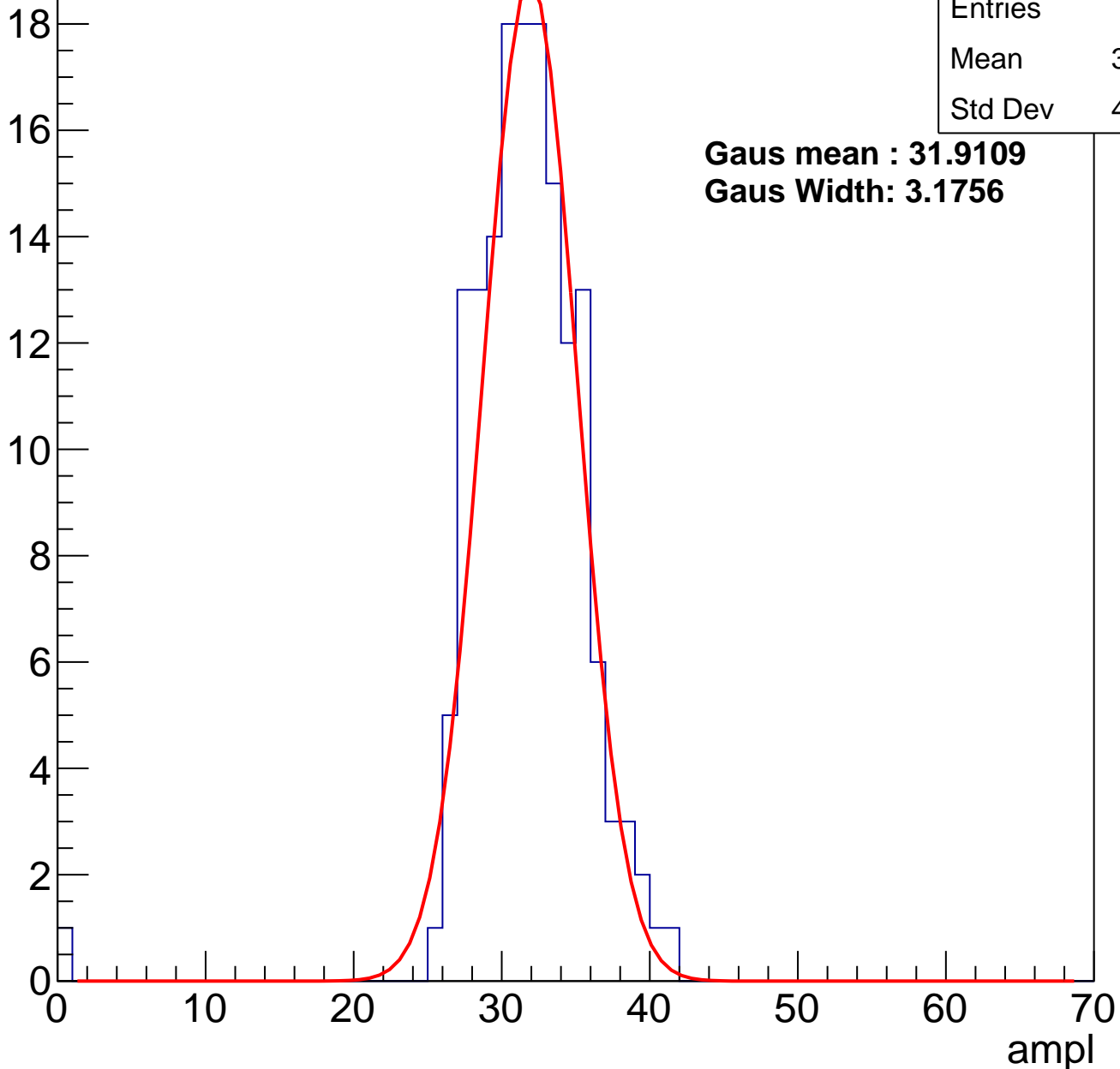


Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch7, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	157
Mean	31.26
Std Dev	4.084

B0L101S, U25-ch7, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	38.65
Std Dev	3.293

Gaus mean : 39.1753

Gaus Width: 3.4513

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

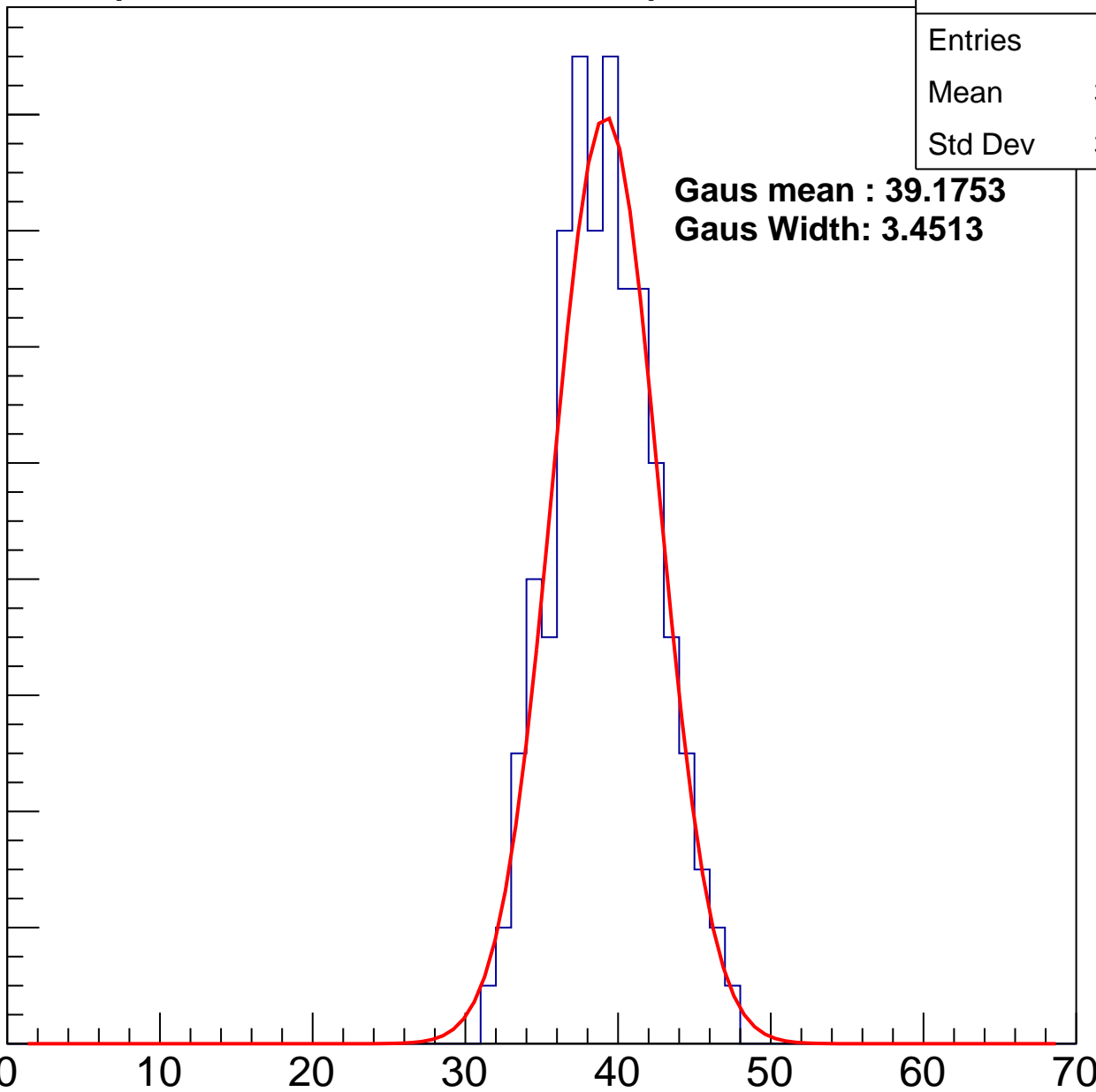
40

50

60

70

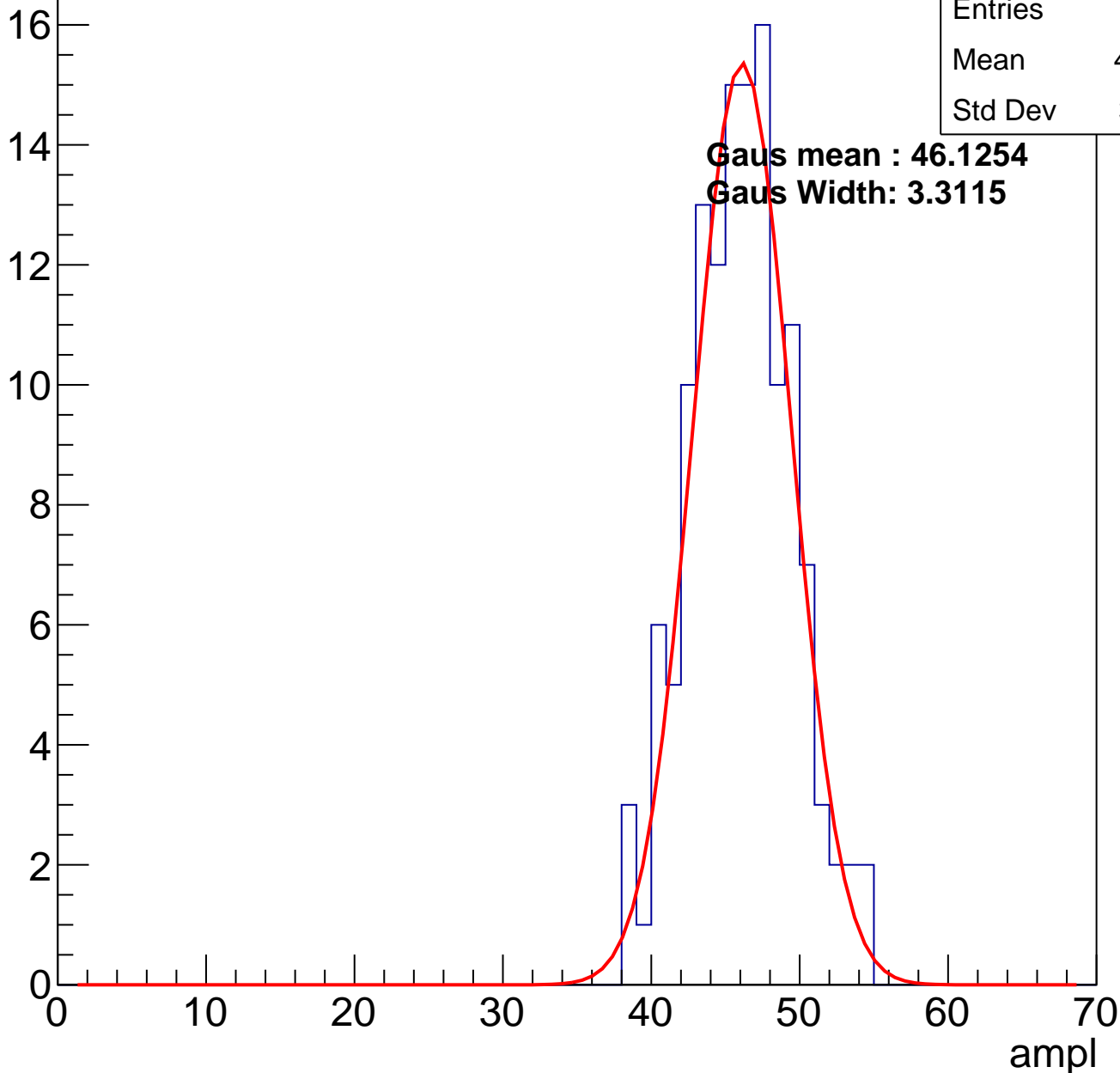
ampl



B0L101S, U25-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

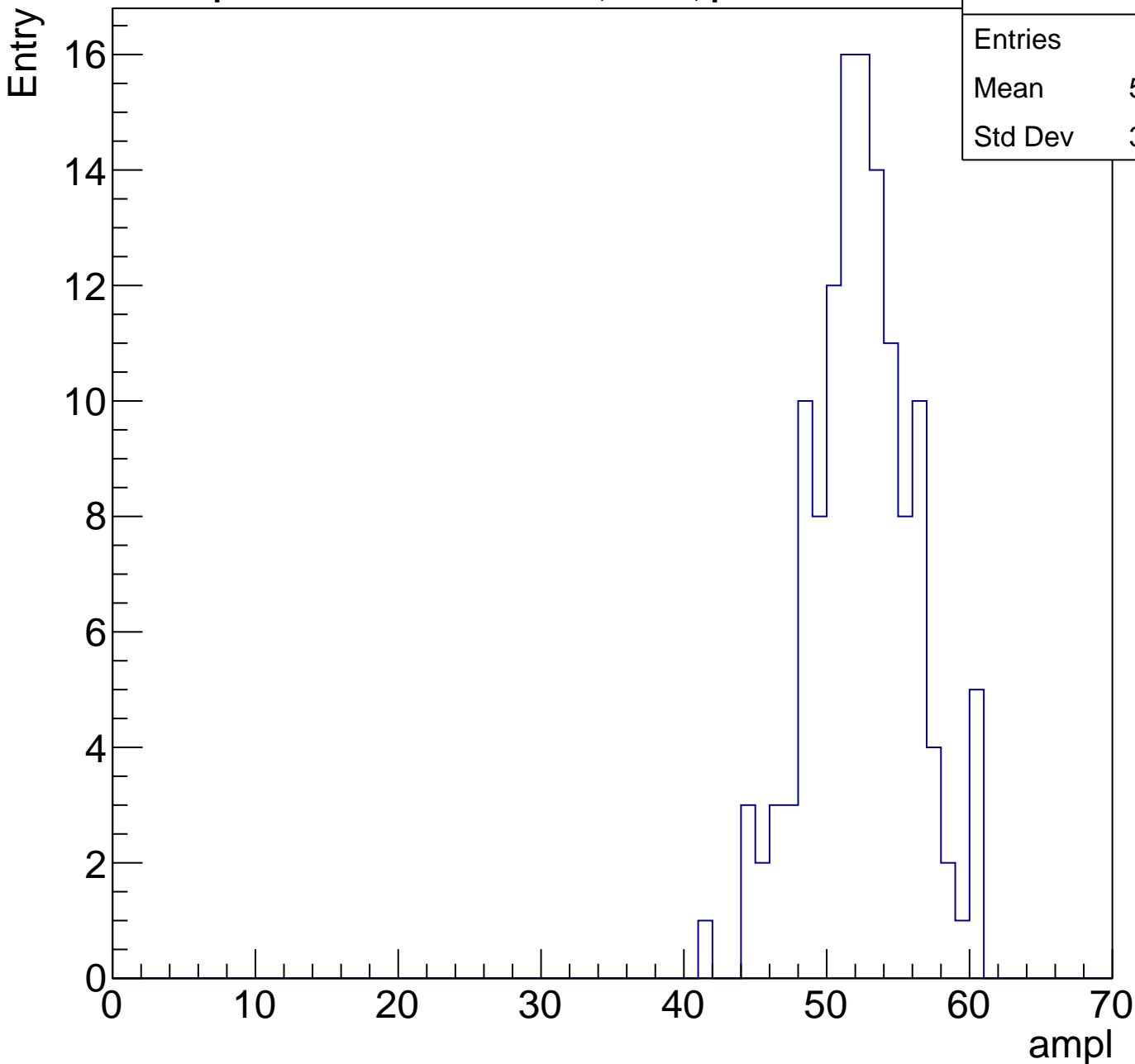
Entry



B0L101S, U25-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	51.95
Std Dev	3.652



B0L101S, U25-ch7, adc4

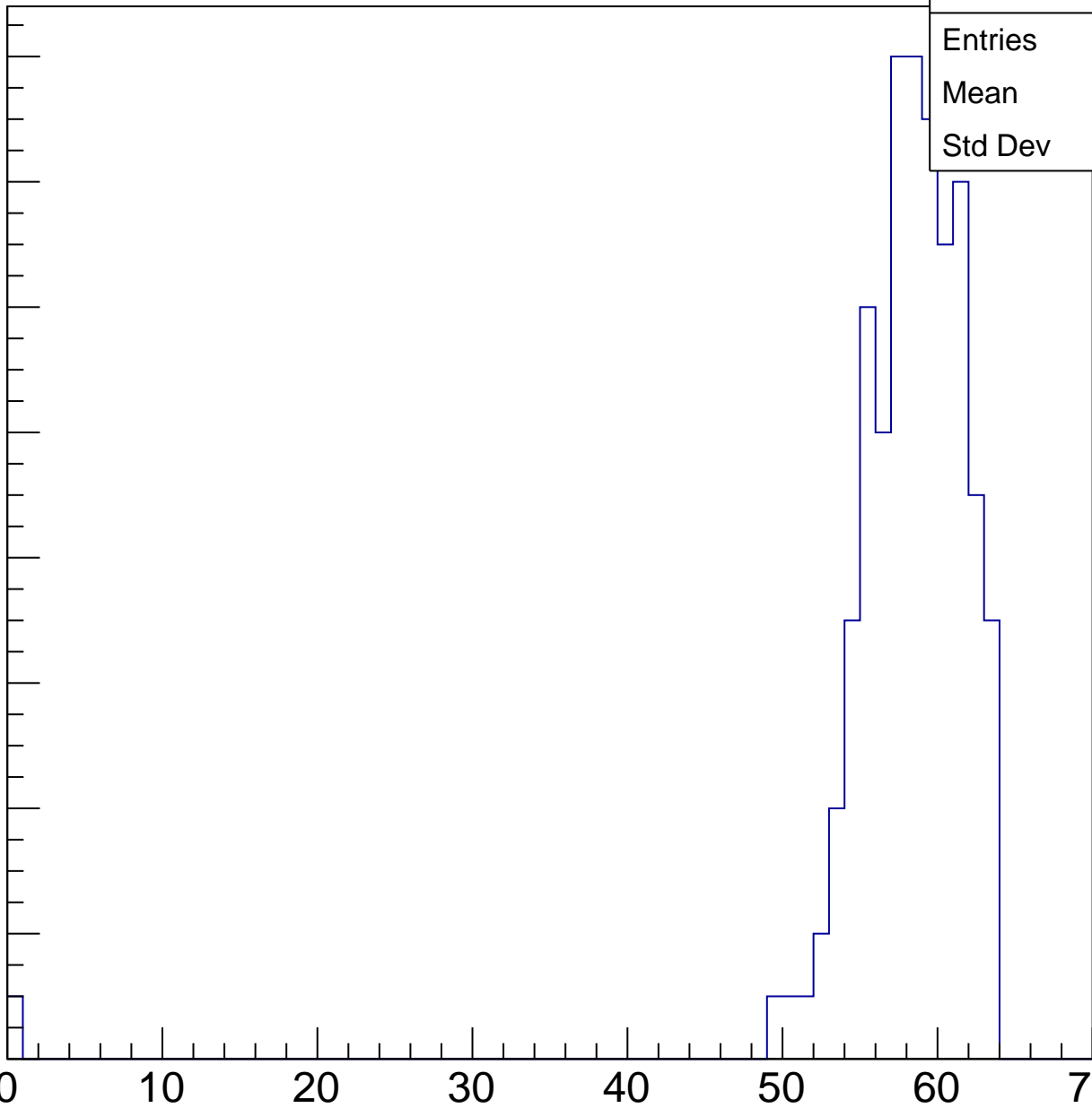
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	57.53
Std Dev	5.9

ampl



B0L101S, U25-ch7, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	38
Mean	59.89
Std Dev	9.96

ampl

0

10

20

30

40

50

60

70

B0L101S, U25-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch7, adc7

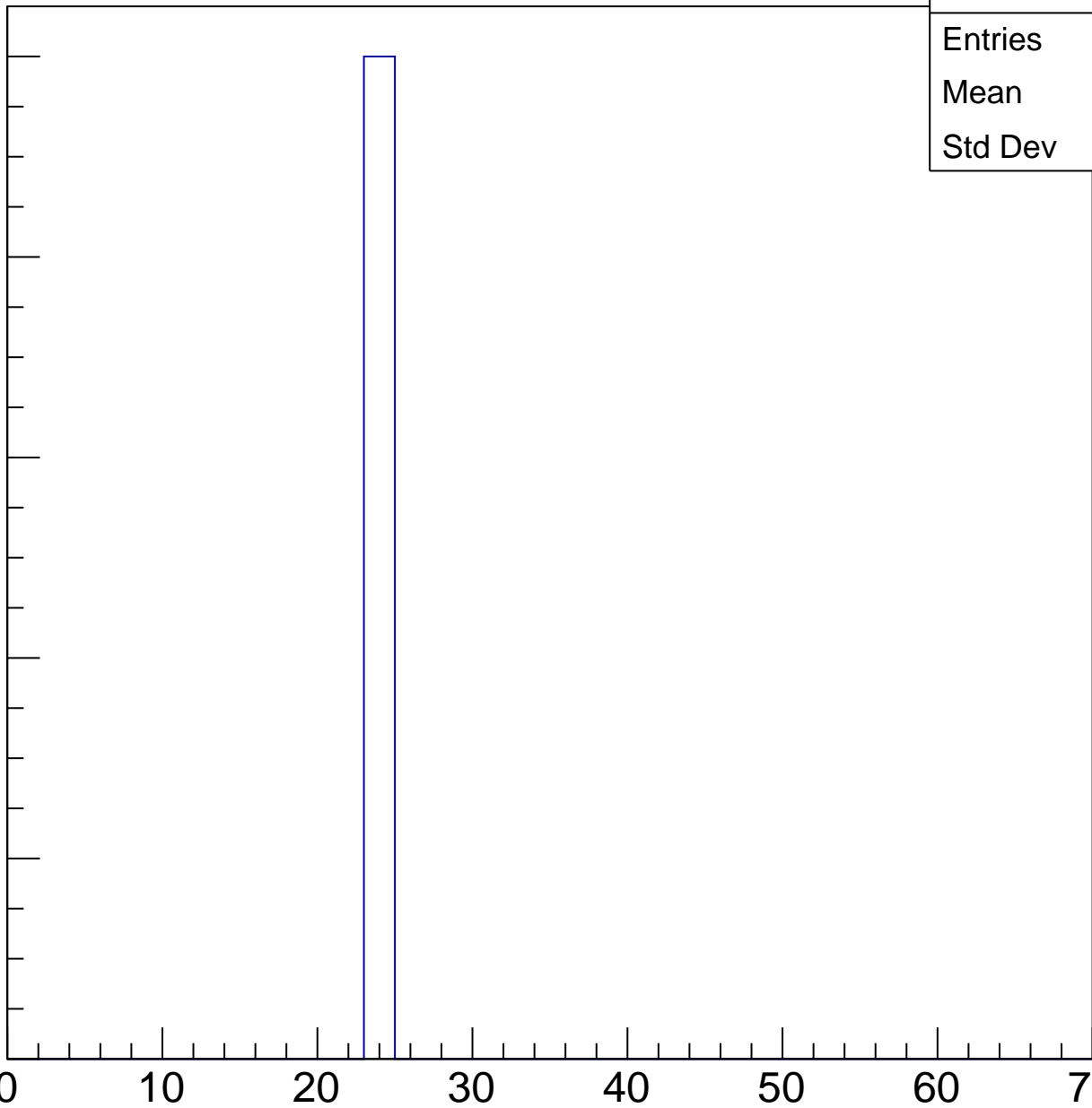
calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23.5
Std Dev	0.5

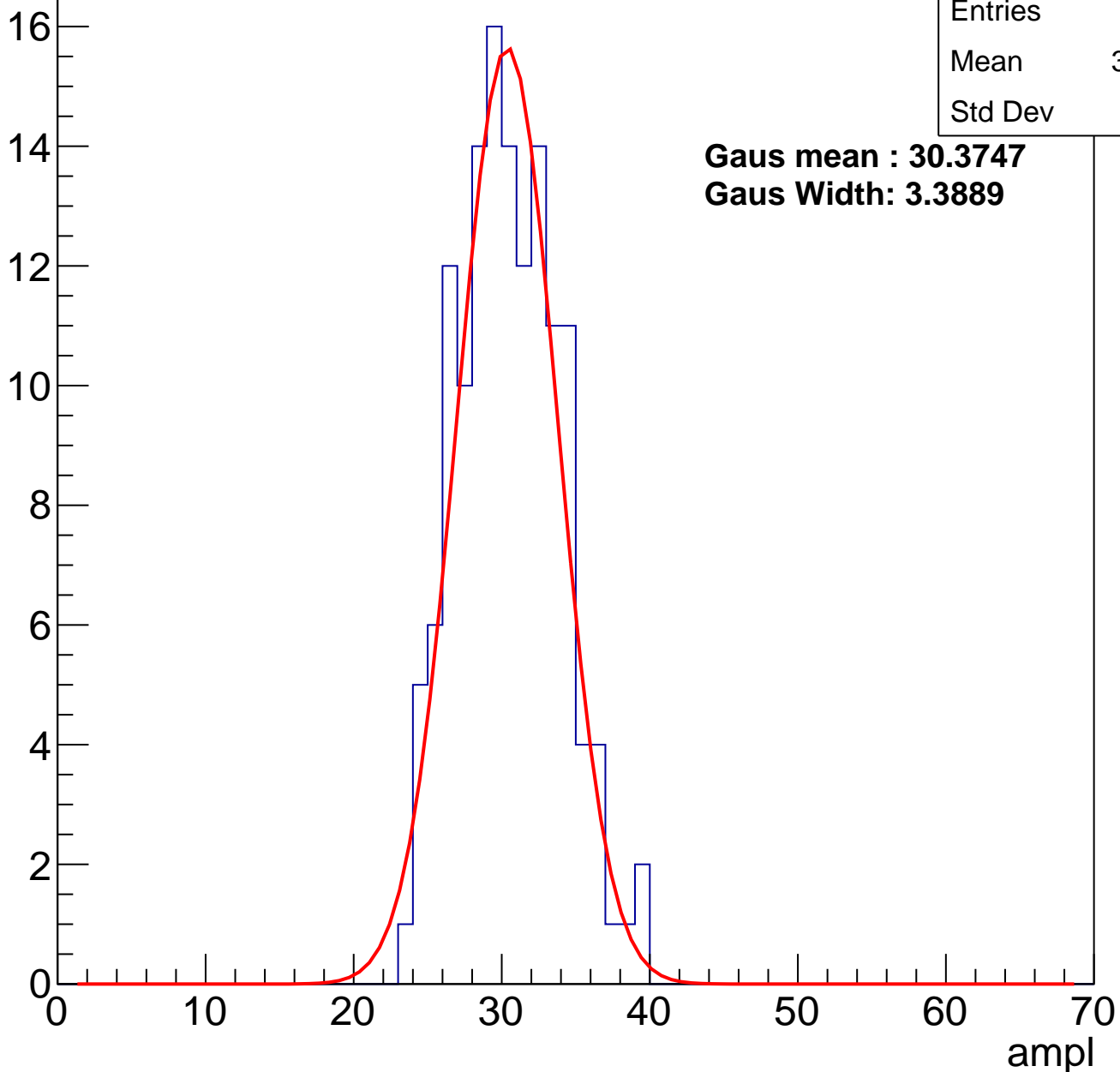
ampl



B0L101S, U25-ch8, adc0

calib_packv5_042523_0143.root, FC#1, port C1

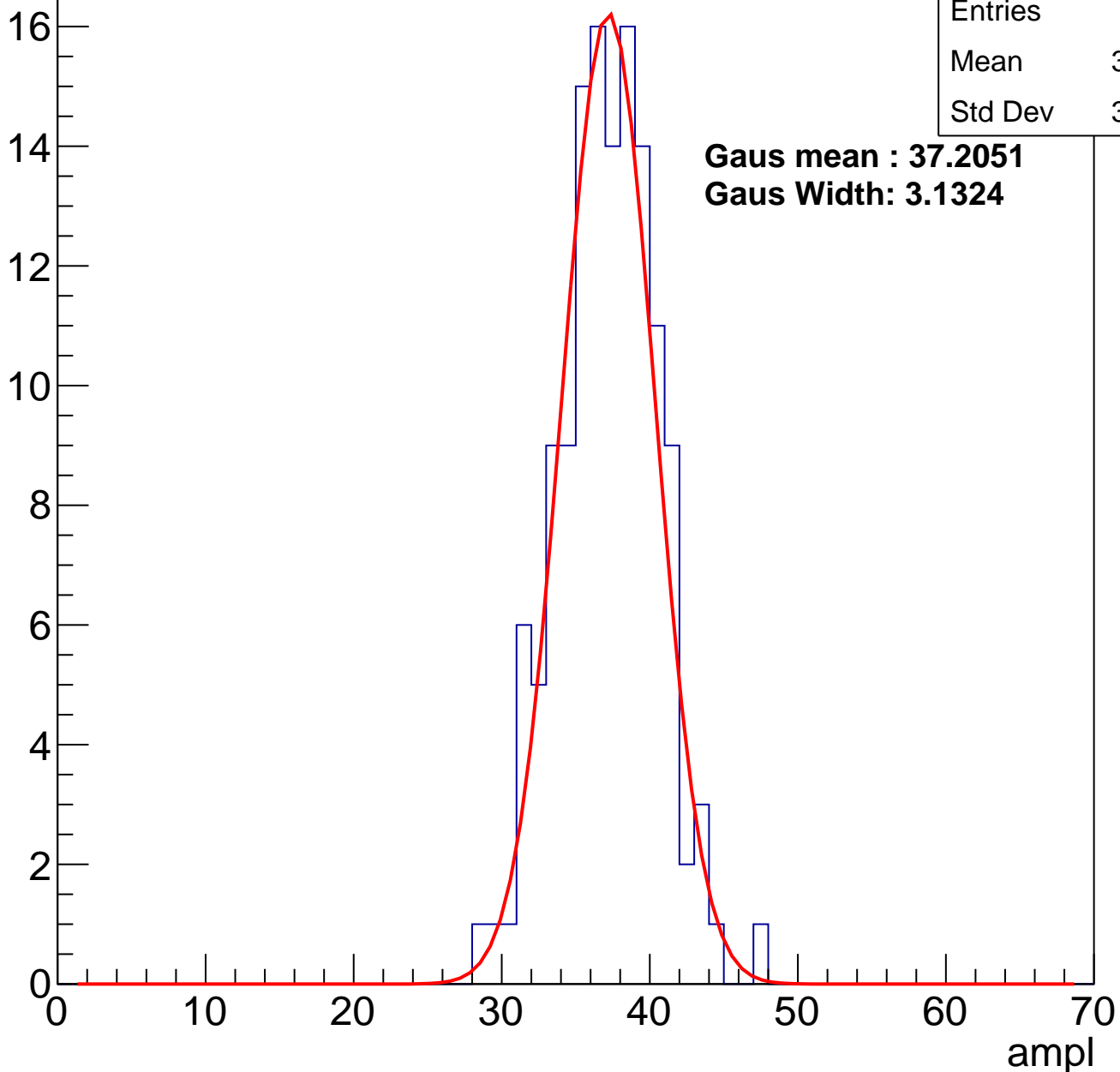
Entry



B0L101S, U25-ch8, adc1

calib_packv5_042523_0143.root, FC#1, port C1

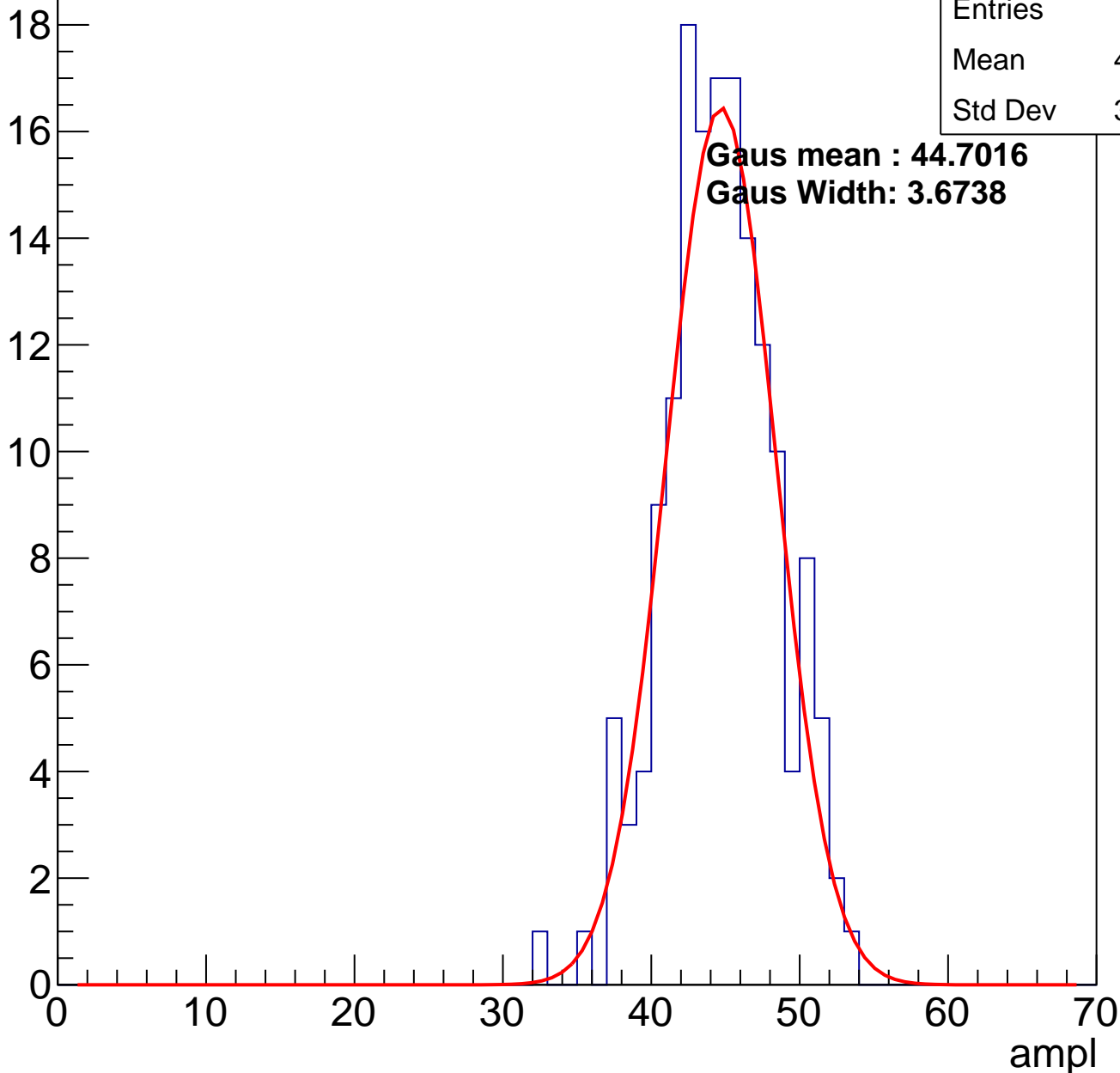
Entry



B0L101S, U25-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

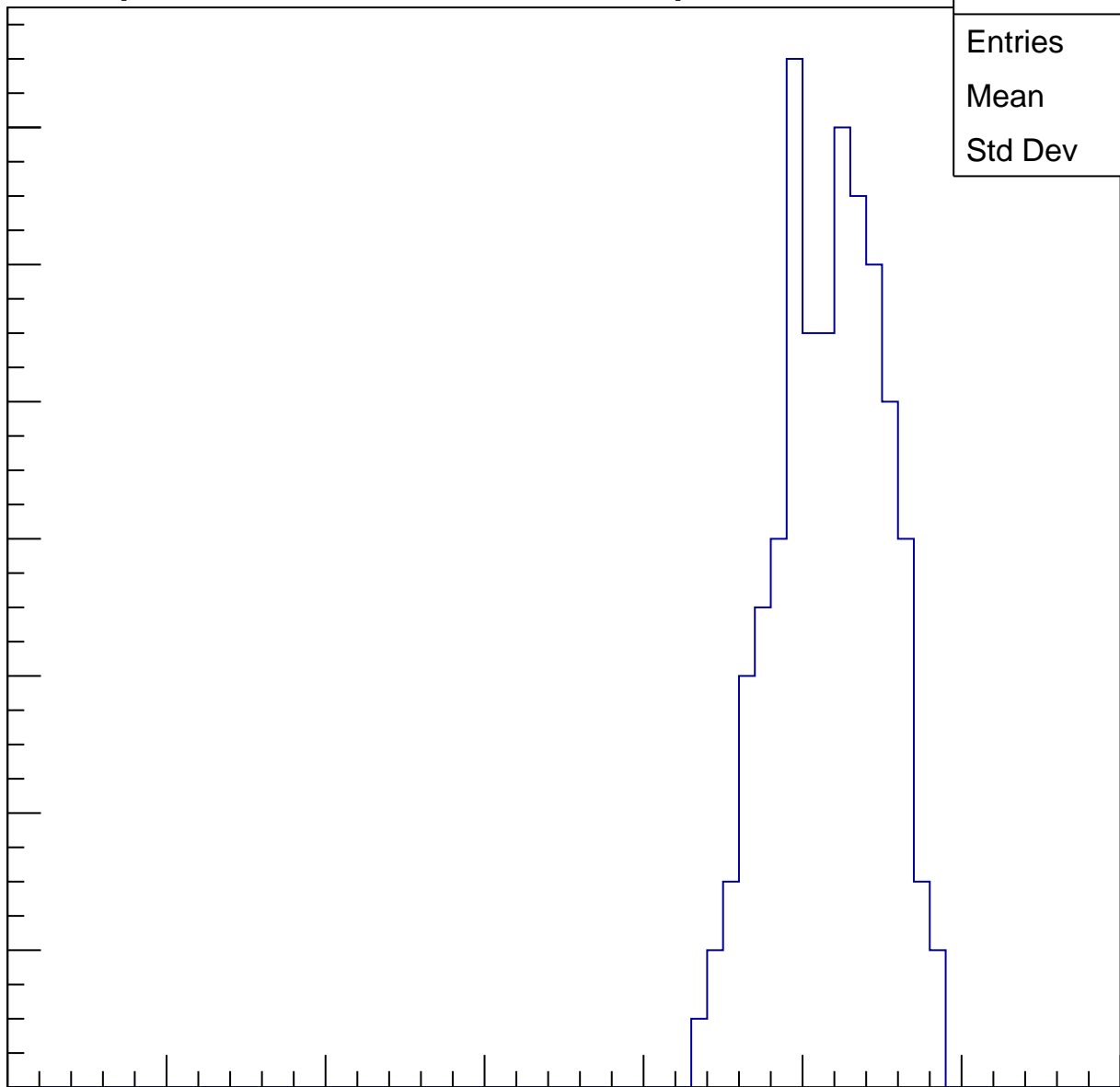
Entries	126
Mean	51.2
Std Dev	3.345

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch8, adc4

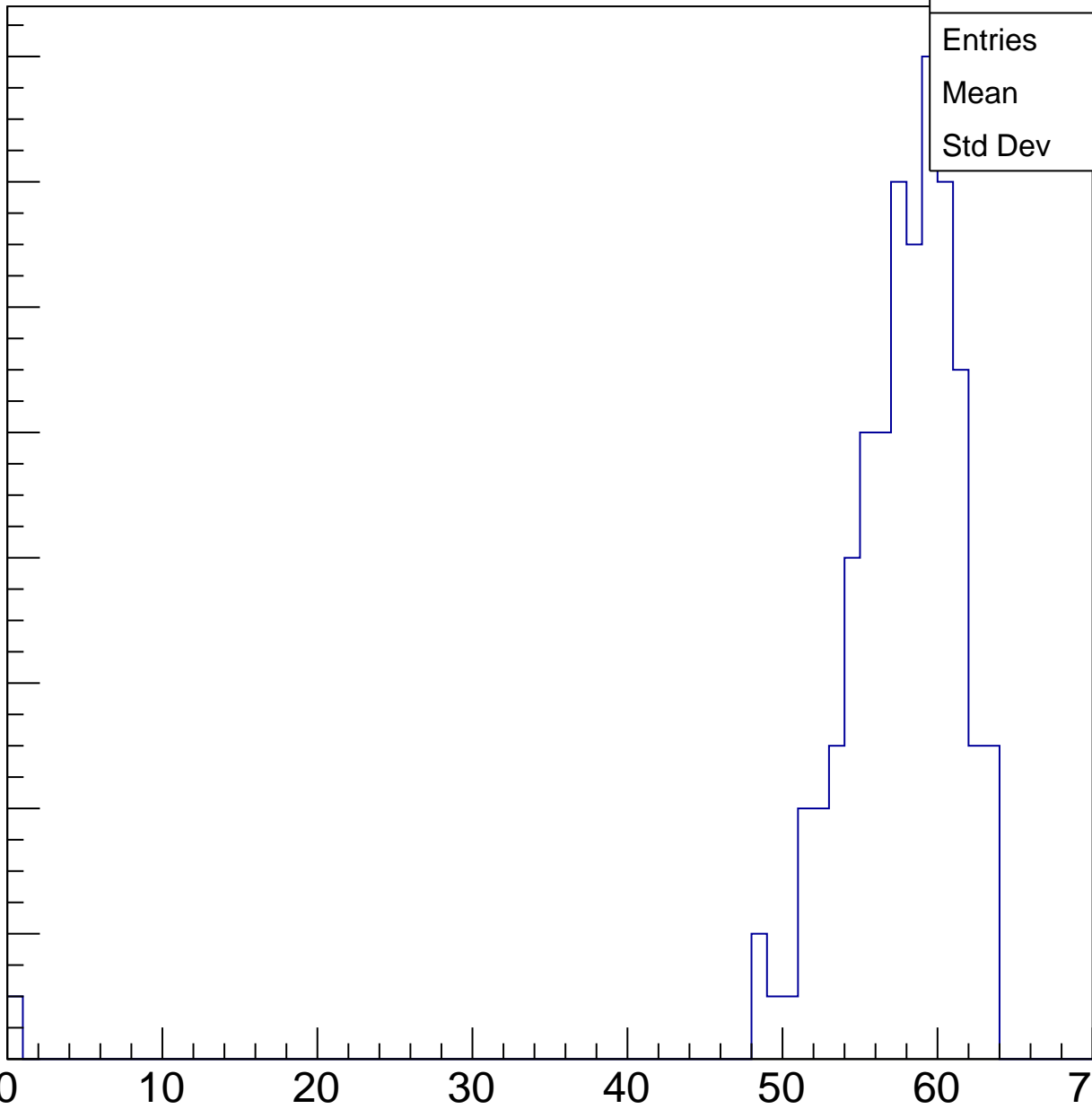
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	56.82
Std Dev	6.124

ampl



B0L101S, U25-ch8, adc5

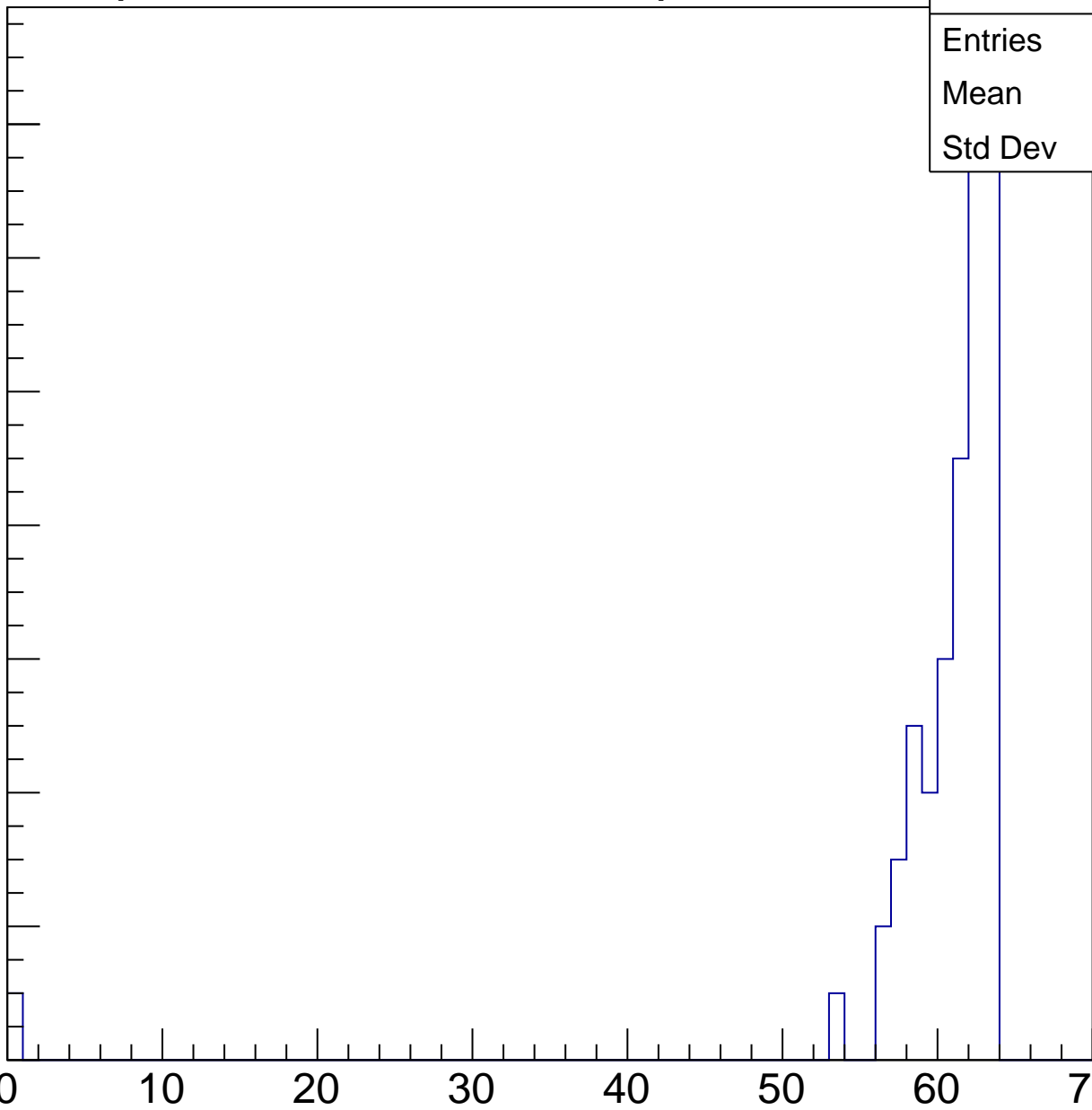
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	60
Mean	59.73
Std Dev	8.091

ampl



B0L101S, U25-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch9, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	31.54
Std Dev	3.556

Gaus mean : 31.8741

Gaus Width: 3.8370

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

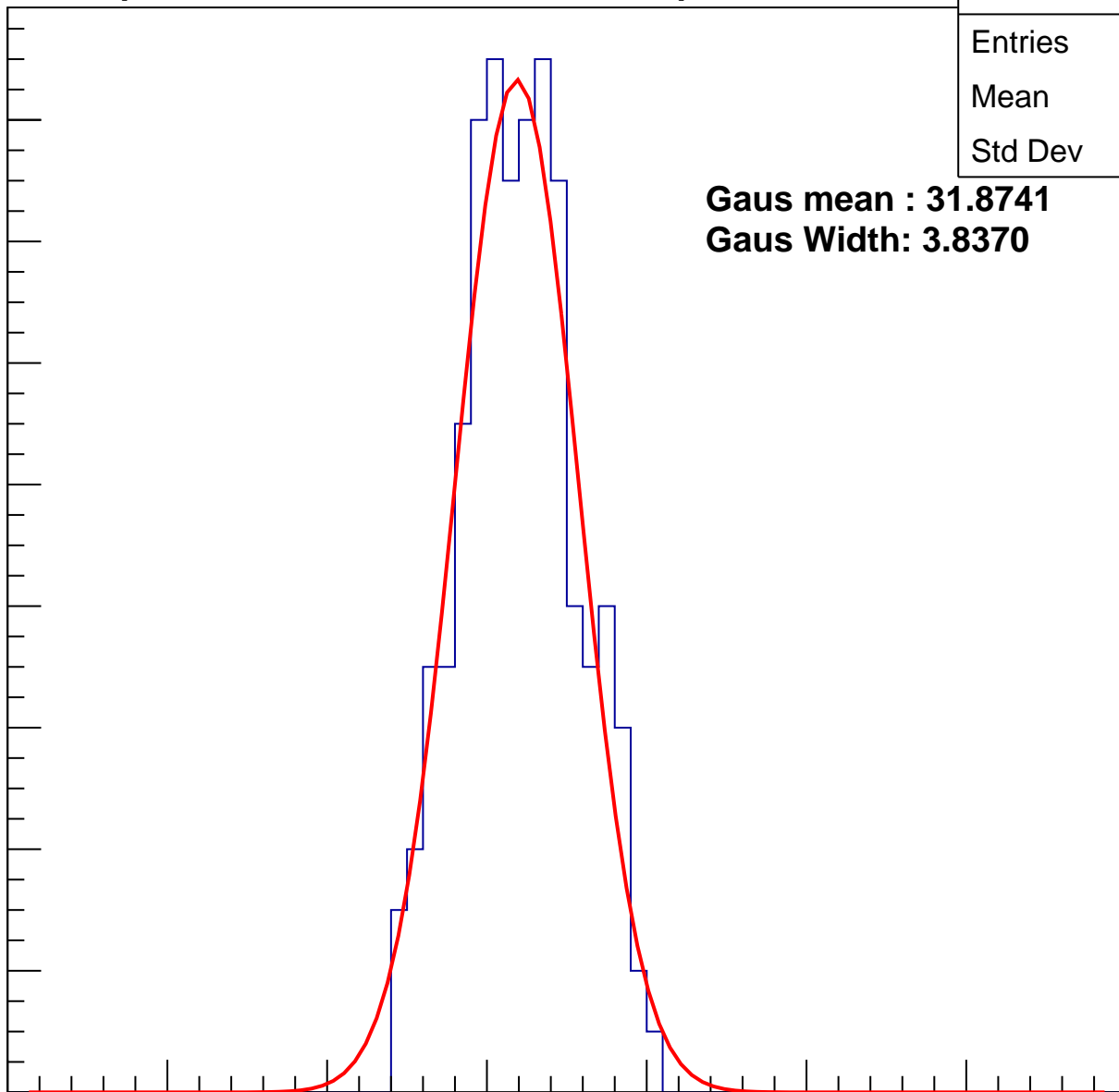
40

50

60

70

ampl



B0L101S, U25-ch9, adc1

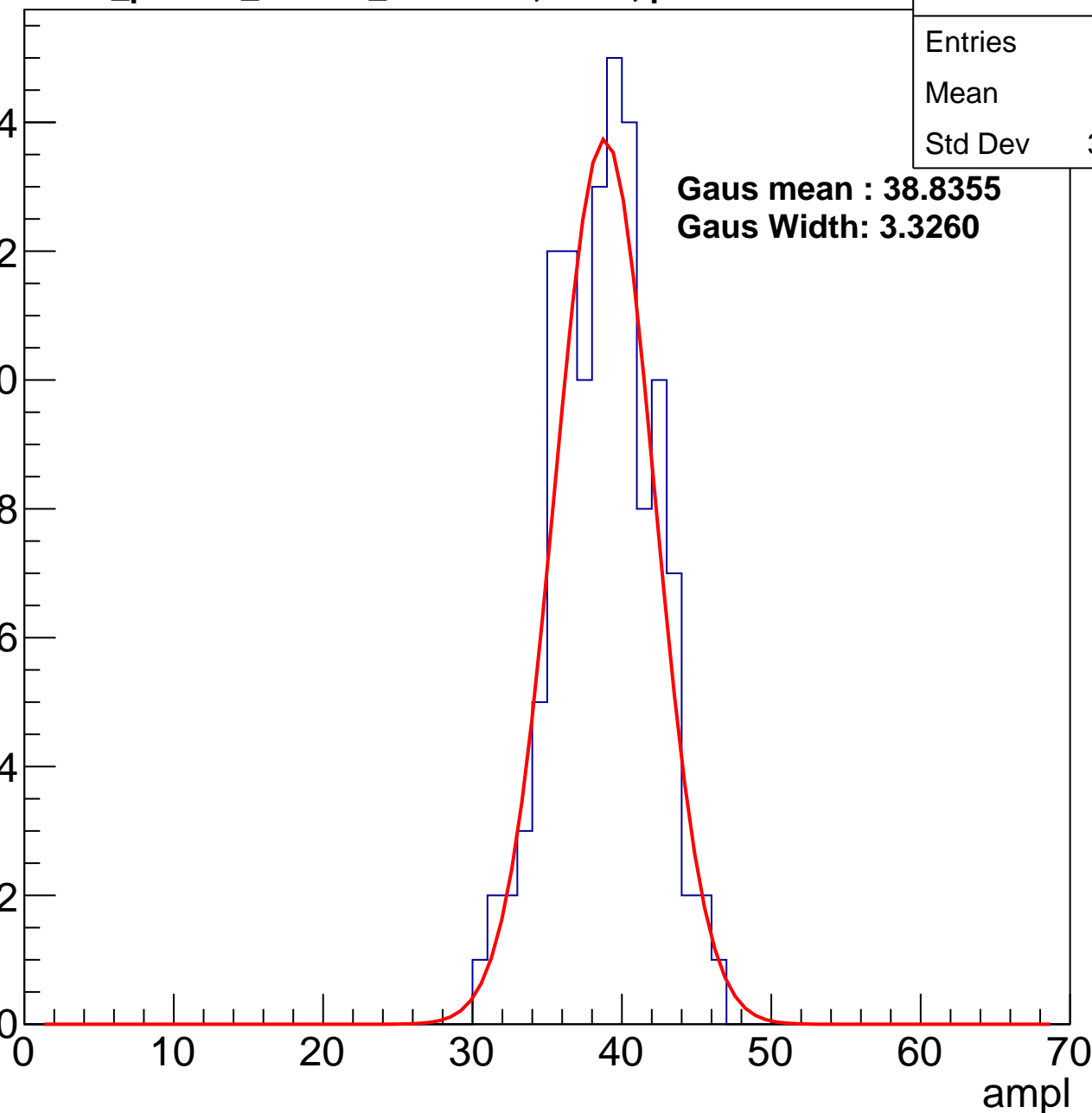
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	38.31
Std Dev	3.248

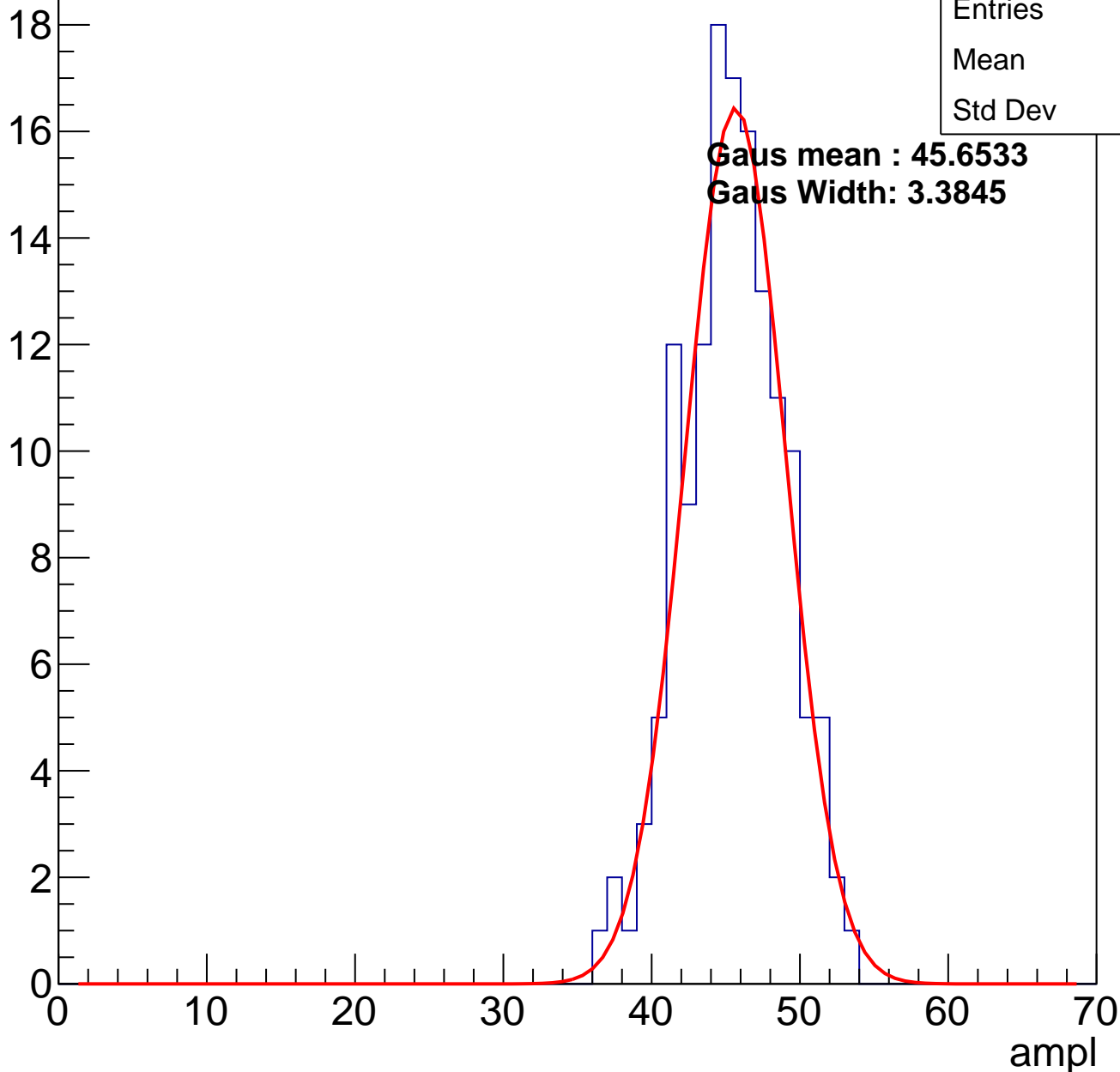
Gaus mean : 38.8355
Gaus Width: 3.3260



B0L101S, U25-ch9, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch9, adc3

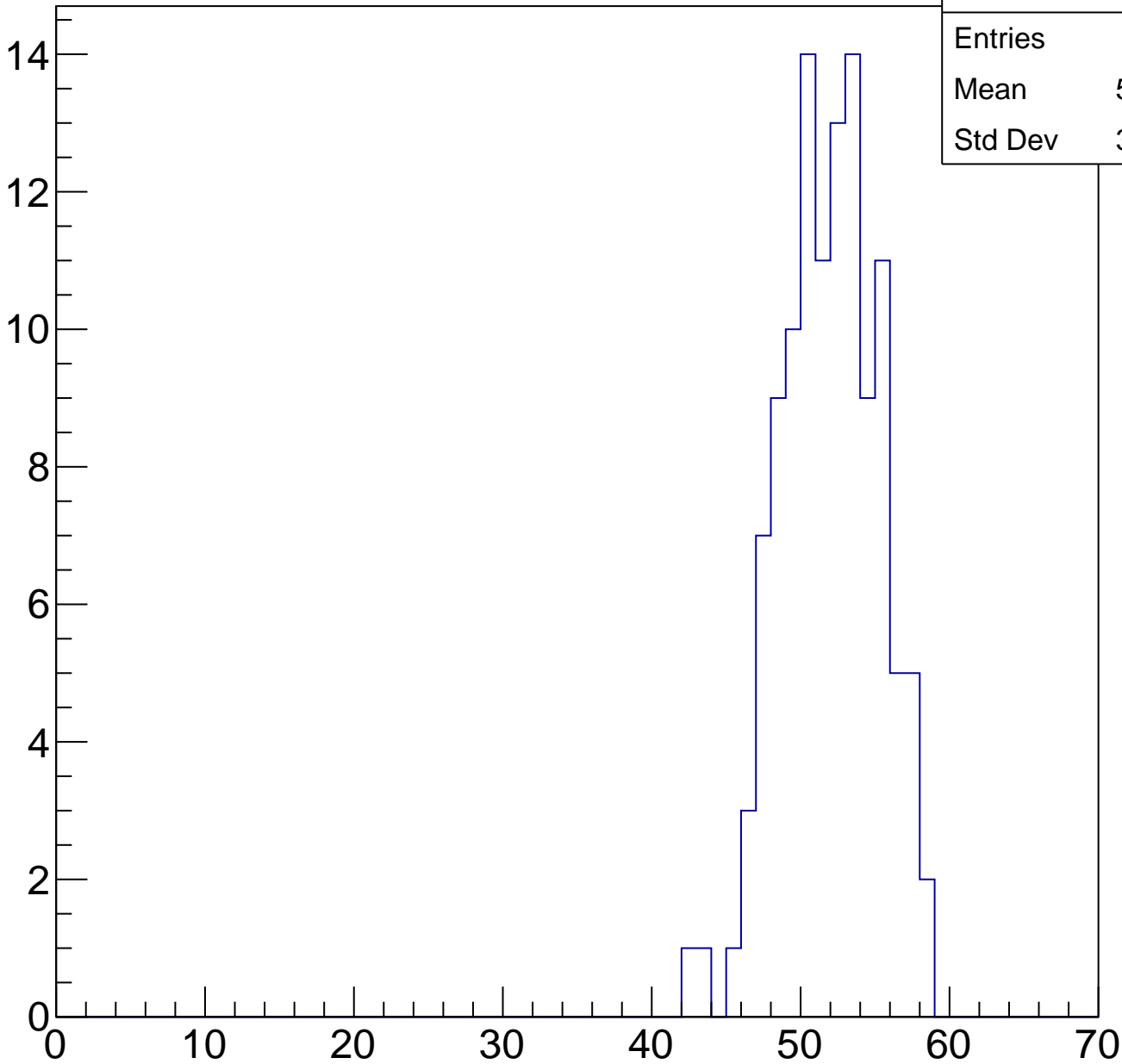
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	116
Mean	51.47
Std Dev	3.226

ampl



B0L101S, U25-ch9, adc4

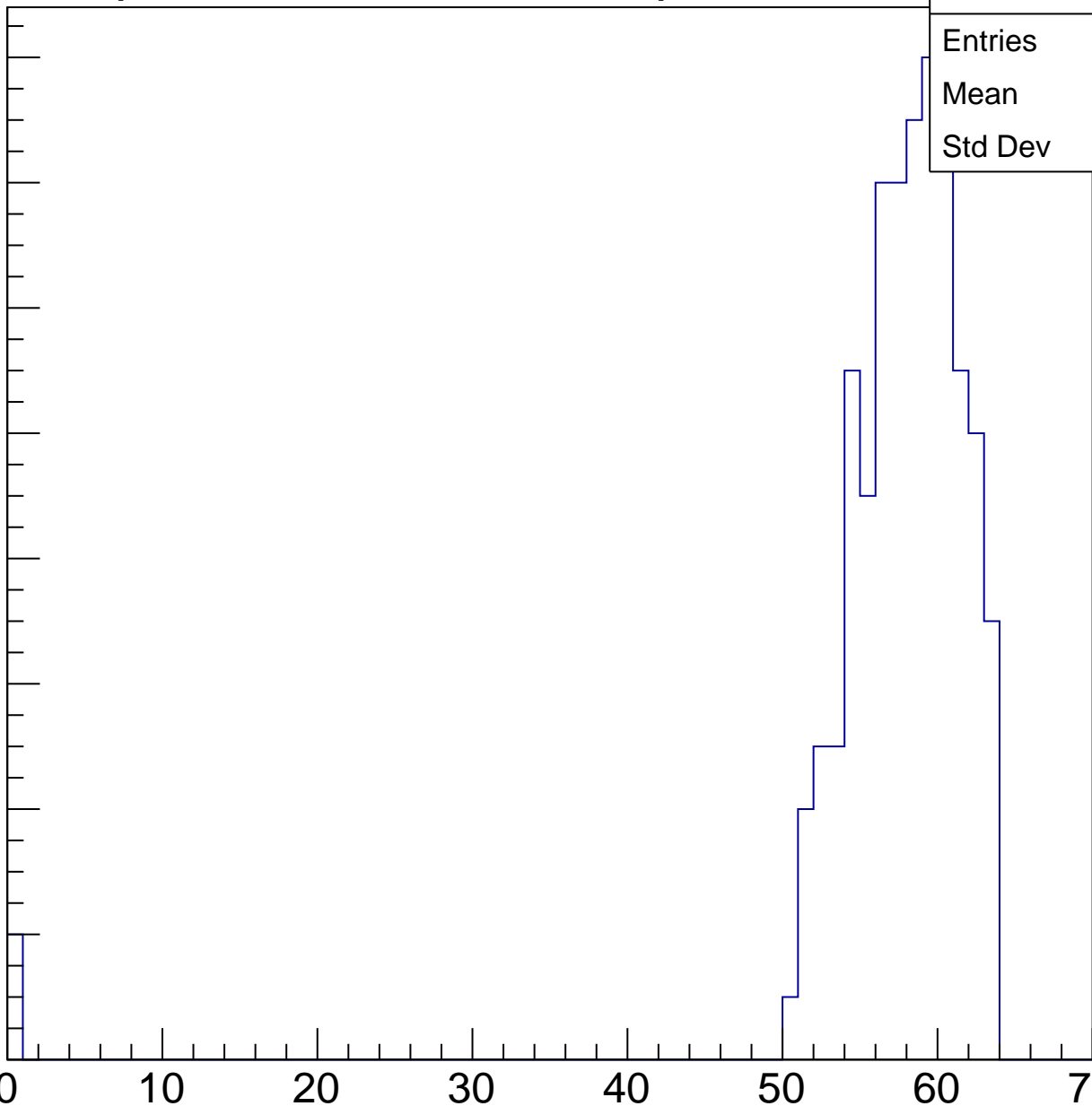
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	139
Mean	56.81
Std Dev	7.554

ampl



B0L101S, U25-ch9, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

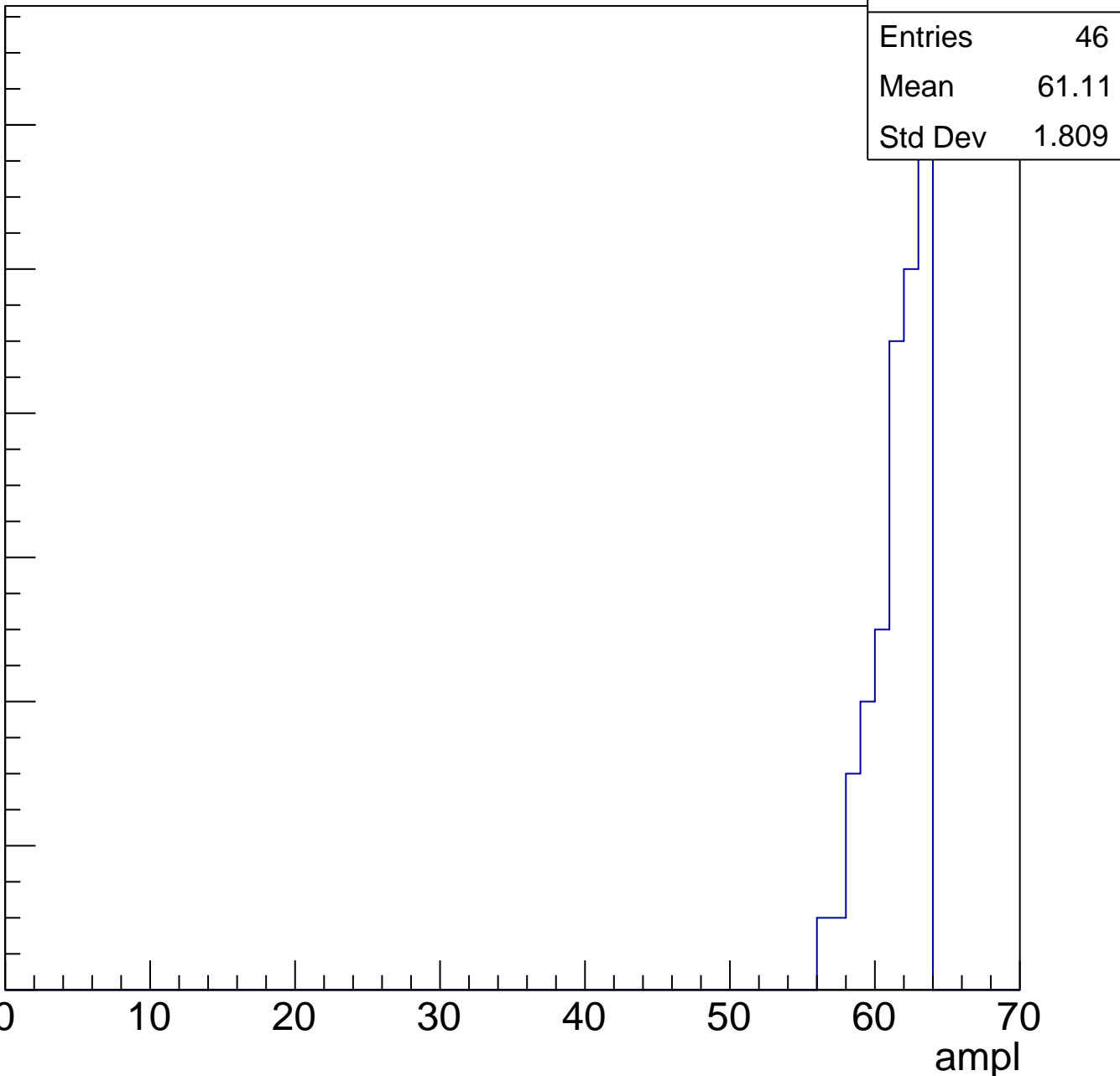
40

50

60

ampl

Entries	46
Mean	61.11
Std Dev	1.809



B0L101S, U25-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

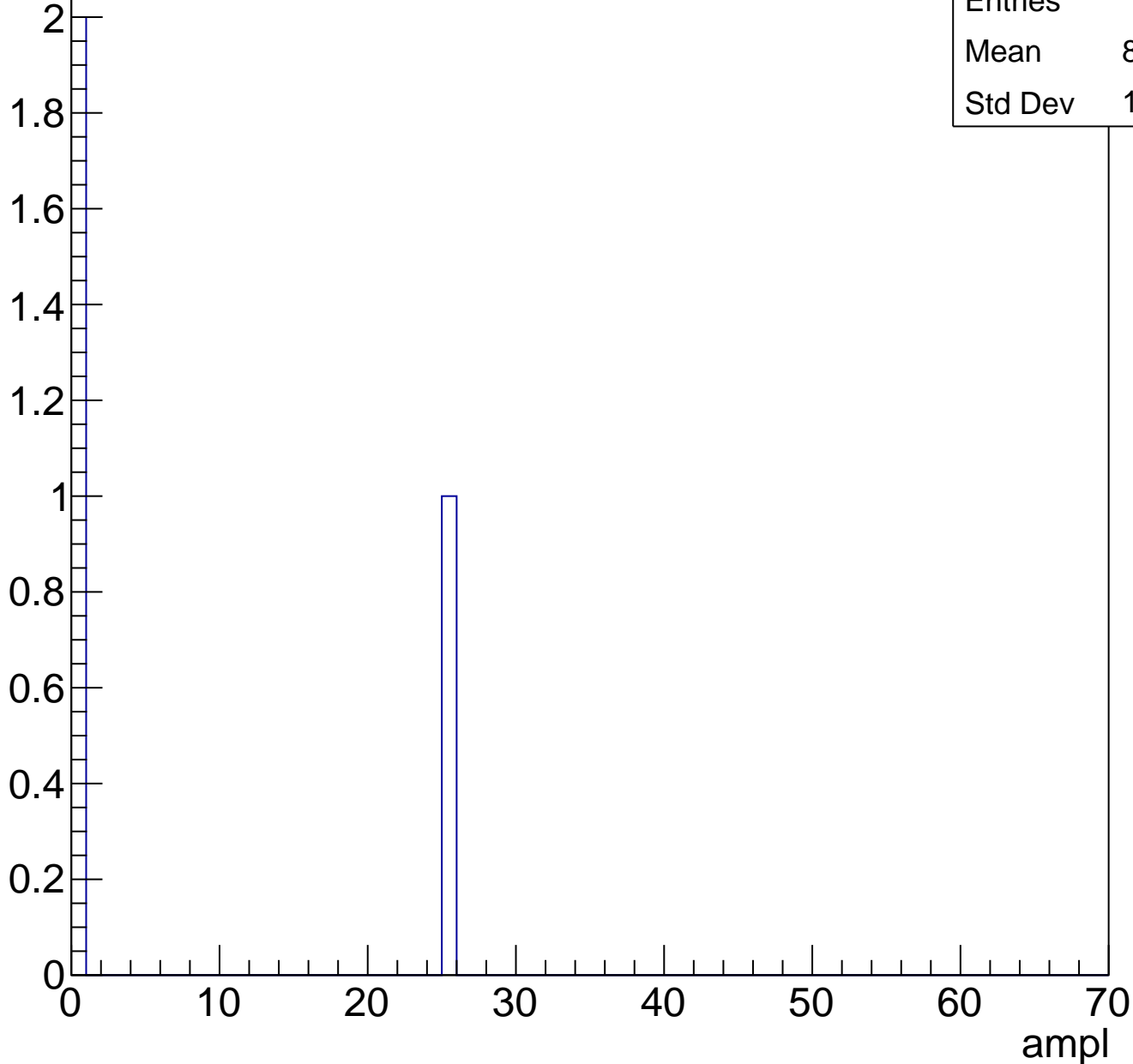


Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L101S, U25-ch10, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	30.45
Std Dev	3.532

Gaus mean : 30.9942

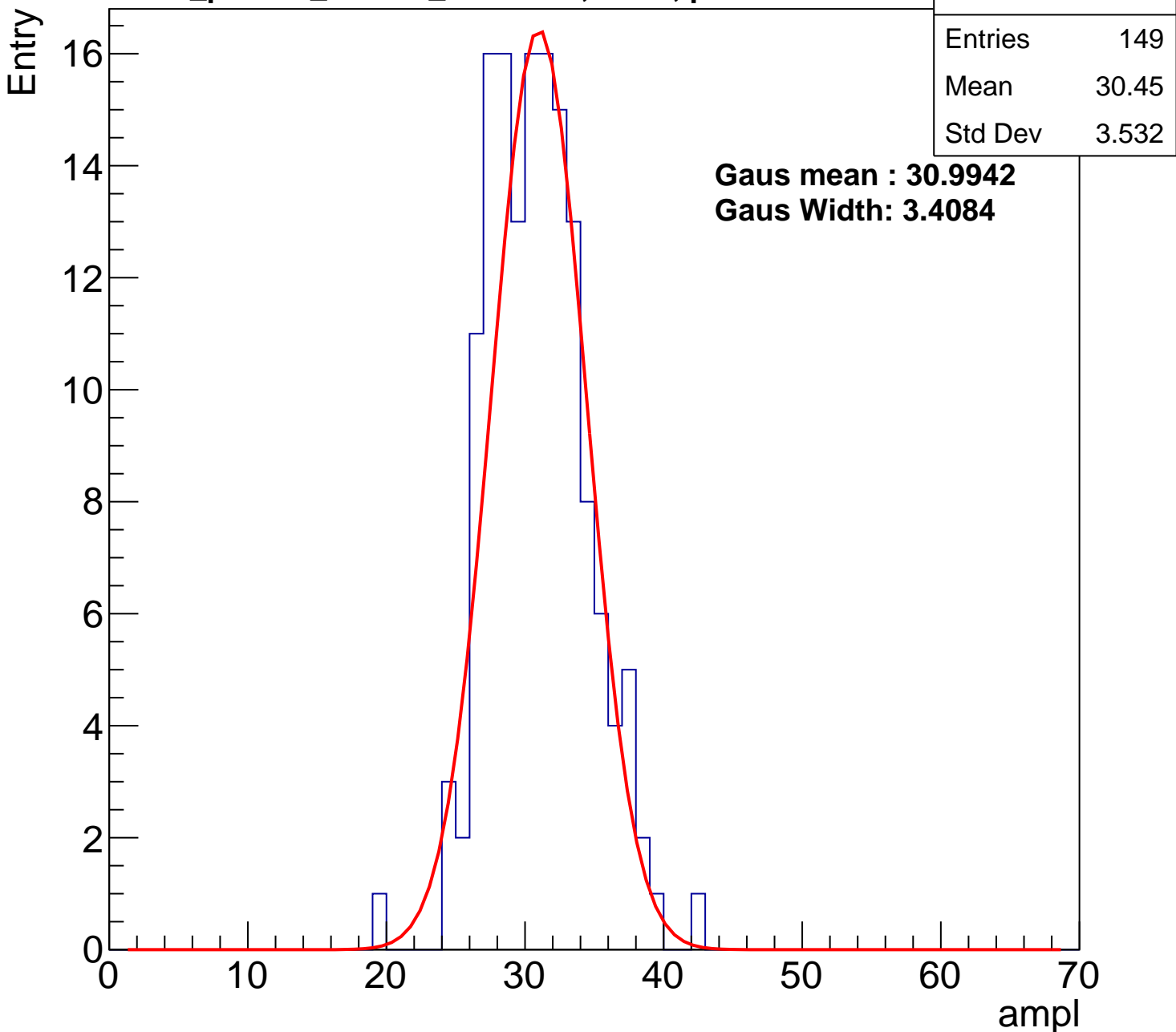
Gaus Width: 3.4084

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

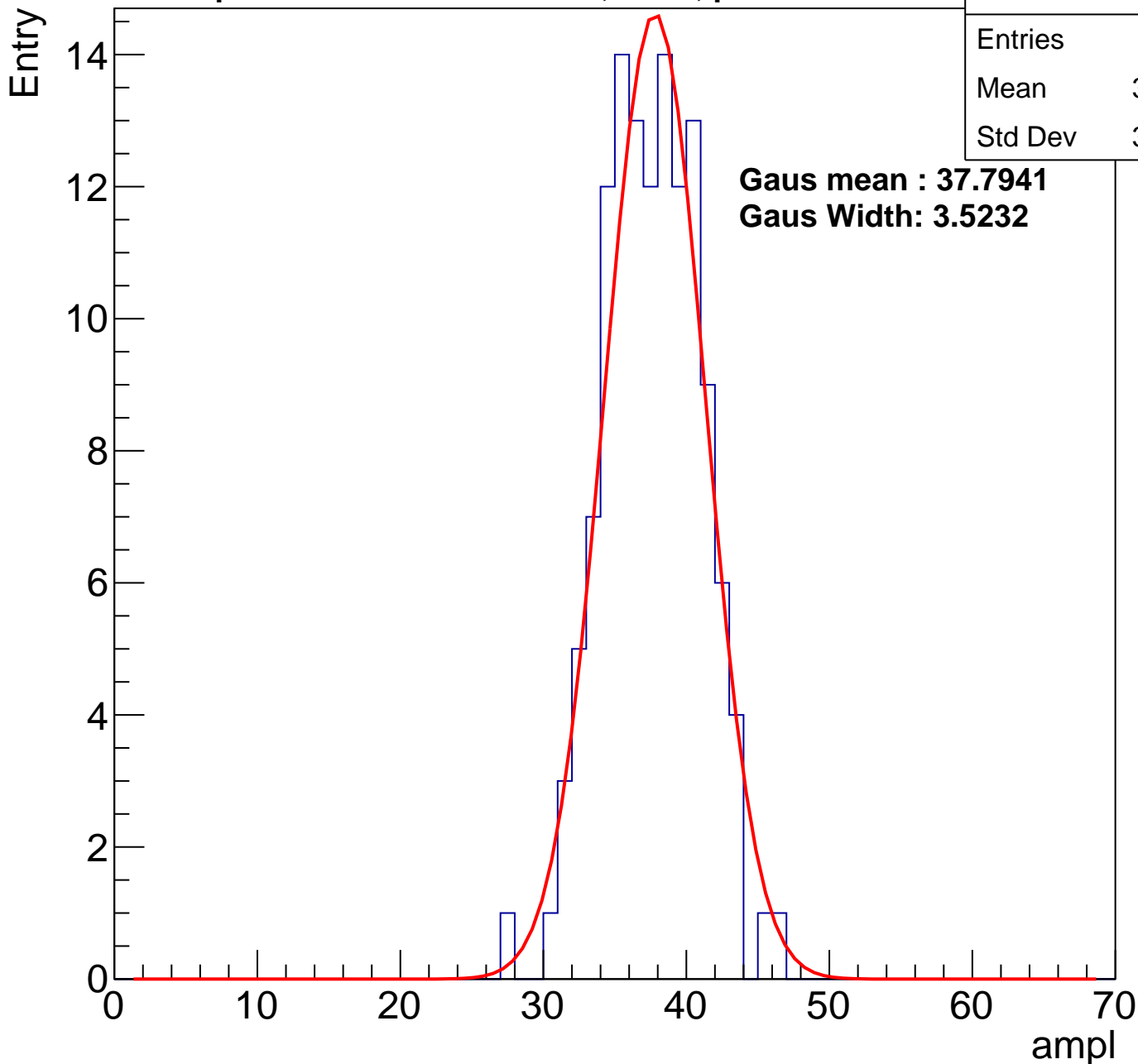


B0L101S, U25-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	37.15
Std Dev	3.343

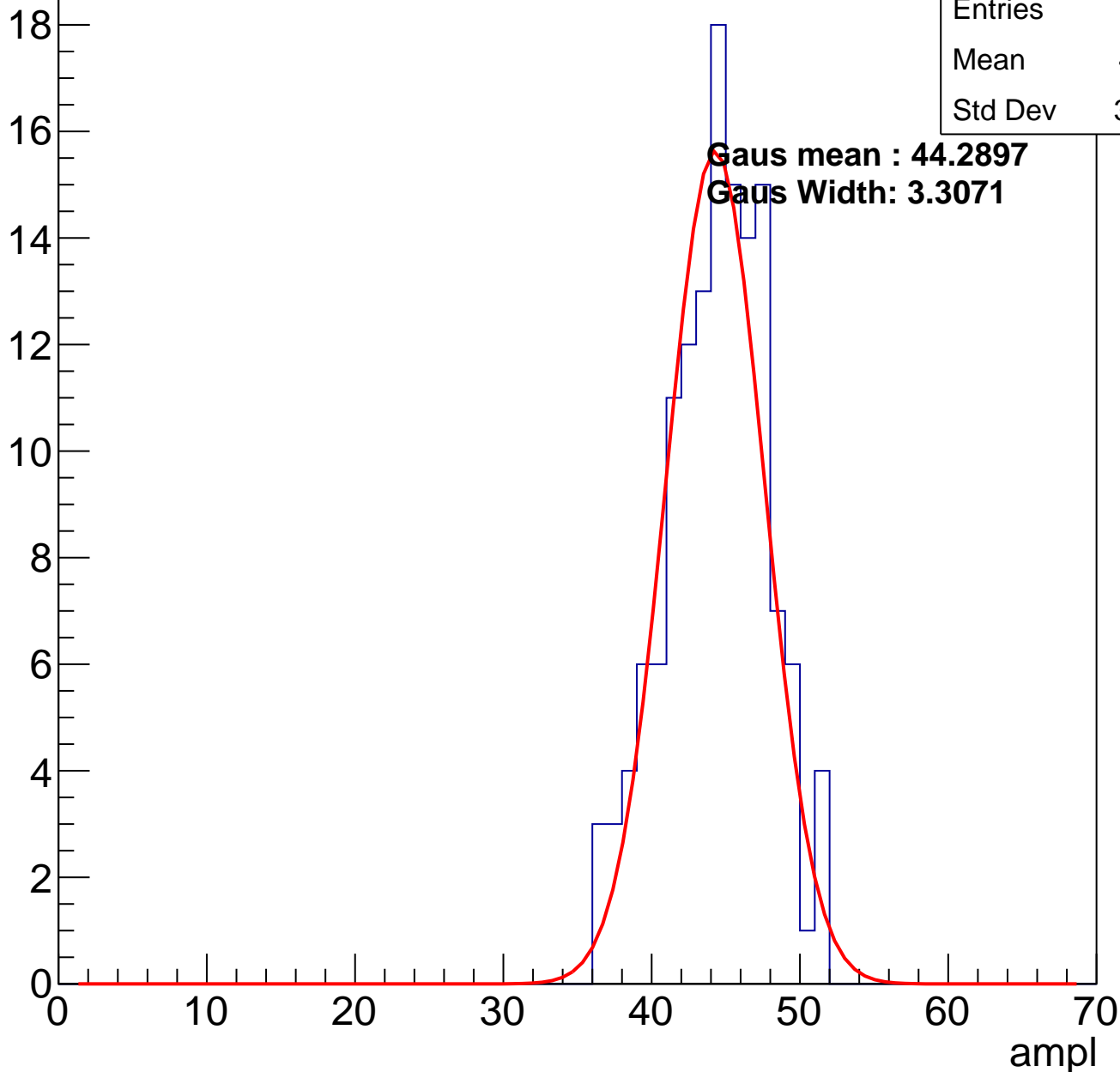
Gaus mean : 37.7941
Gaus Width: 3.5232



B0L101S, U25-ch10, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

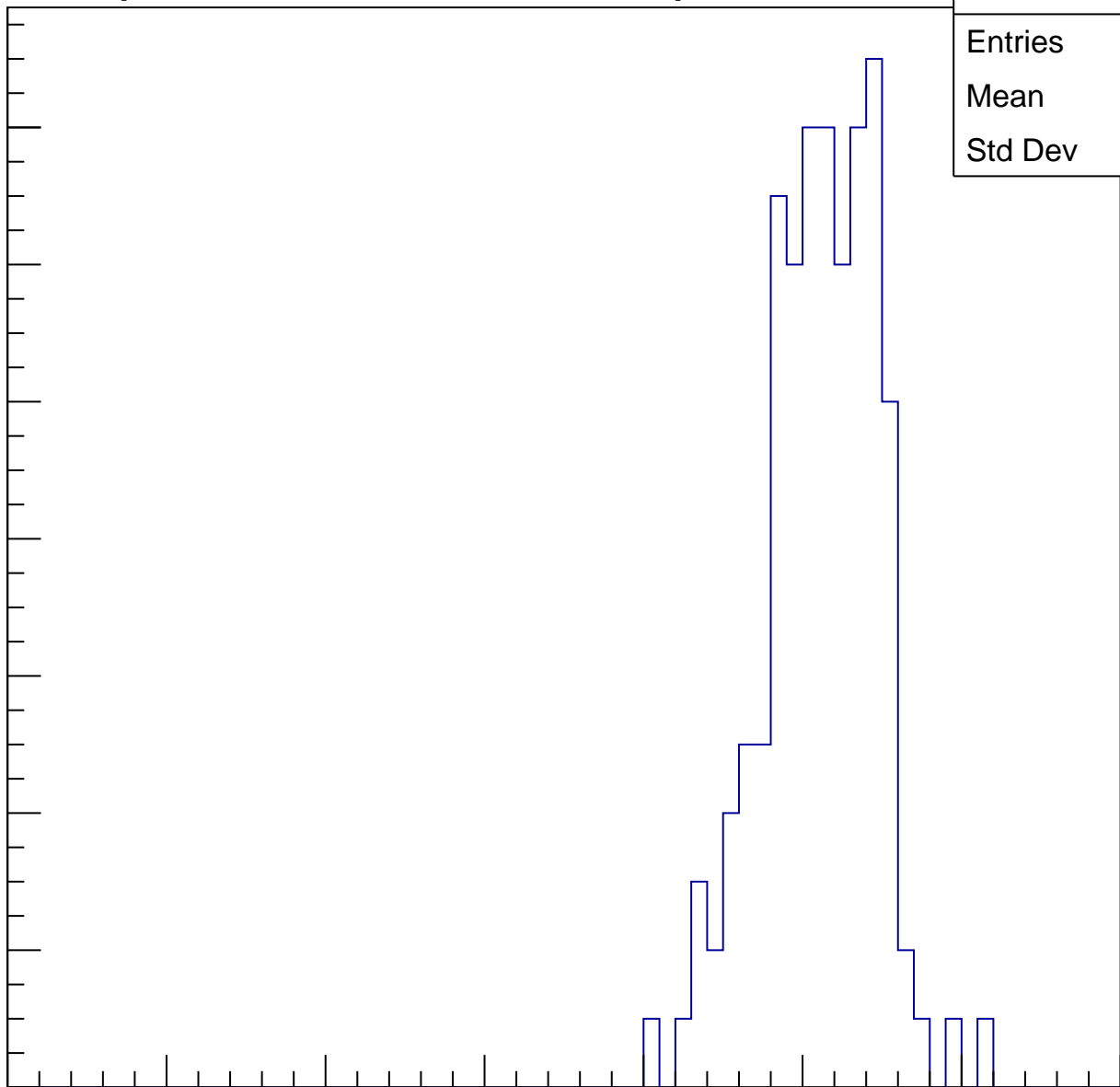
Entries	130
Mean	50.65
Std Dev	3.527

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch10, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	56.21
Std Dev	6.134

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

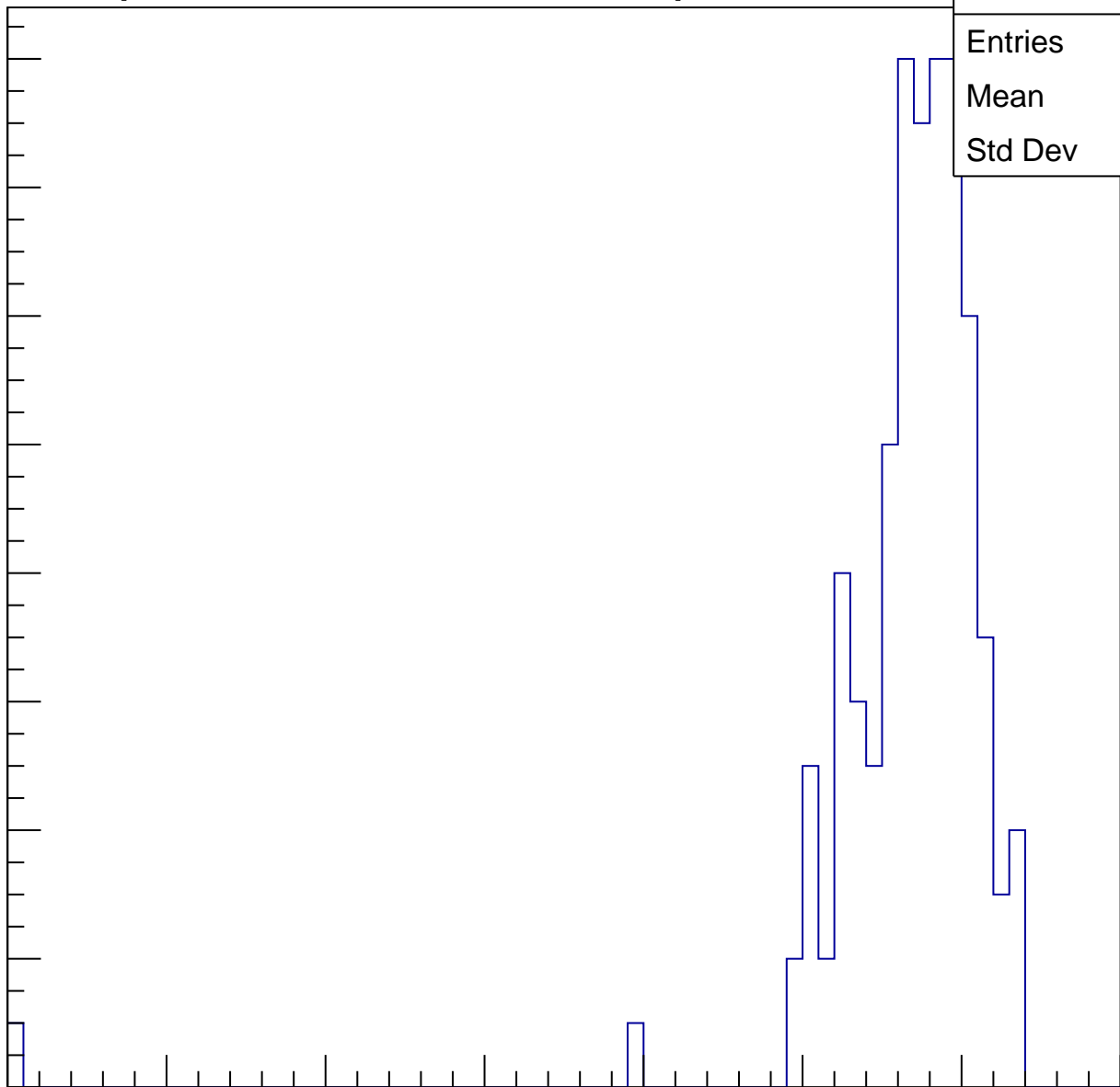
40

50

60

70

ampl



B0L101S, U25-ch10, adc5

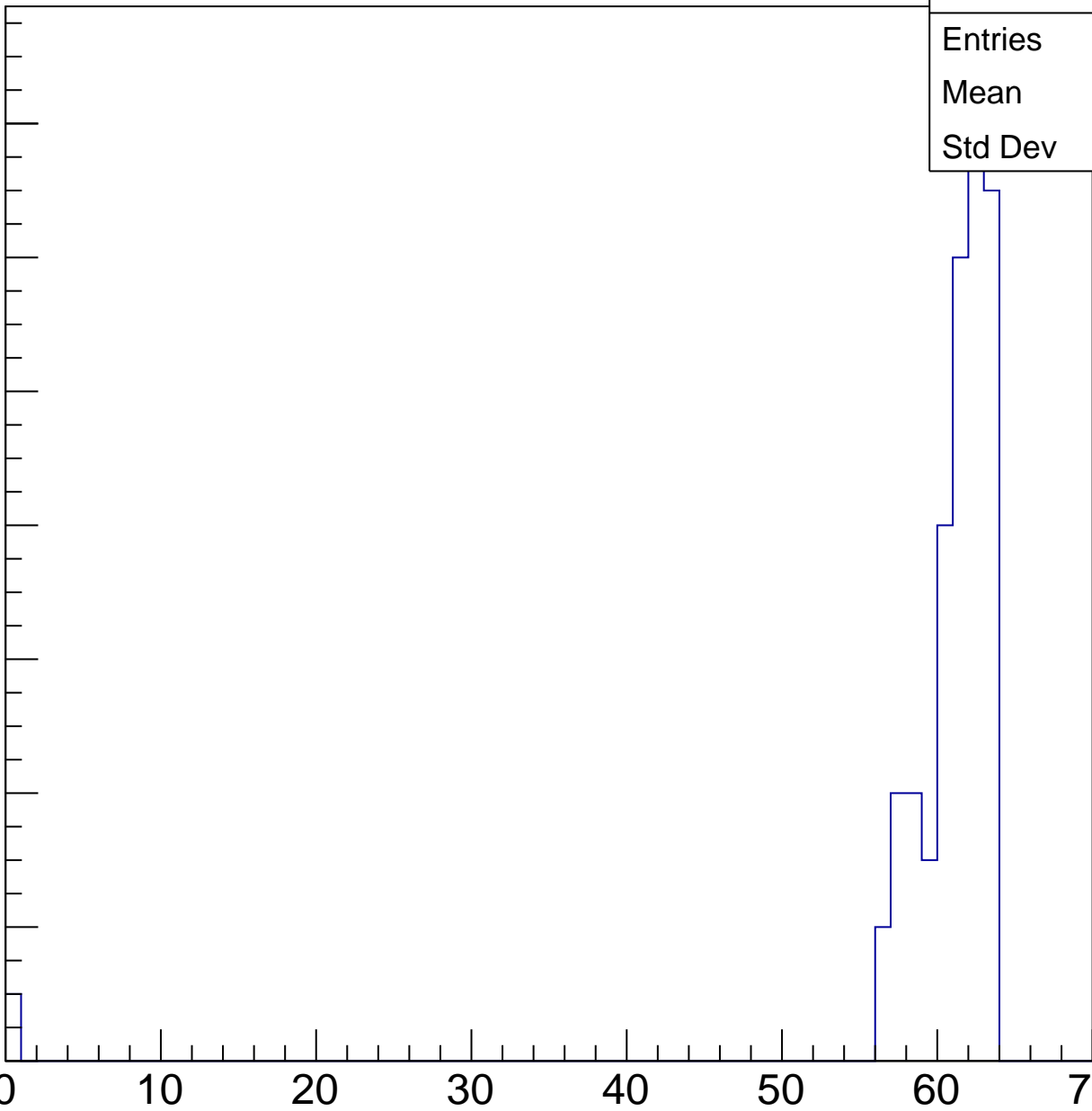
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	62
Mean	59.84
Std Dev	7.905

ampl



B0L101S, U25-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L101S, U25-ch11, adc0

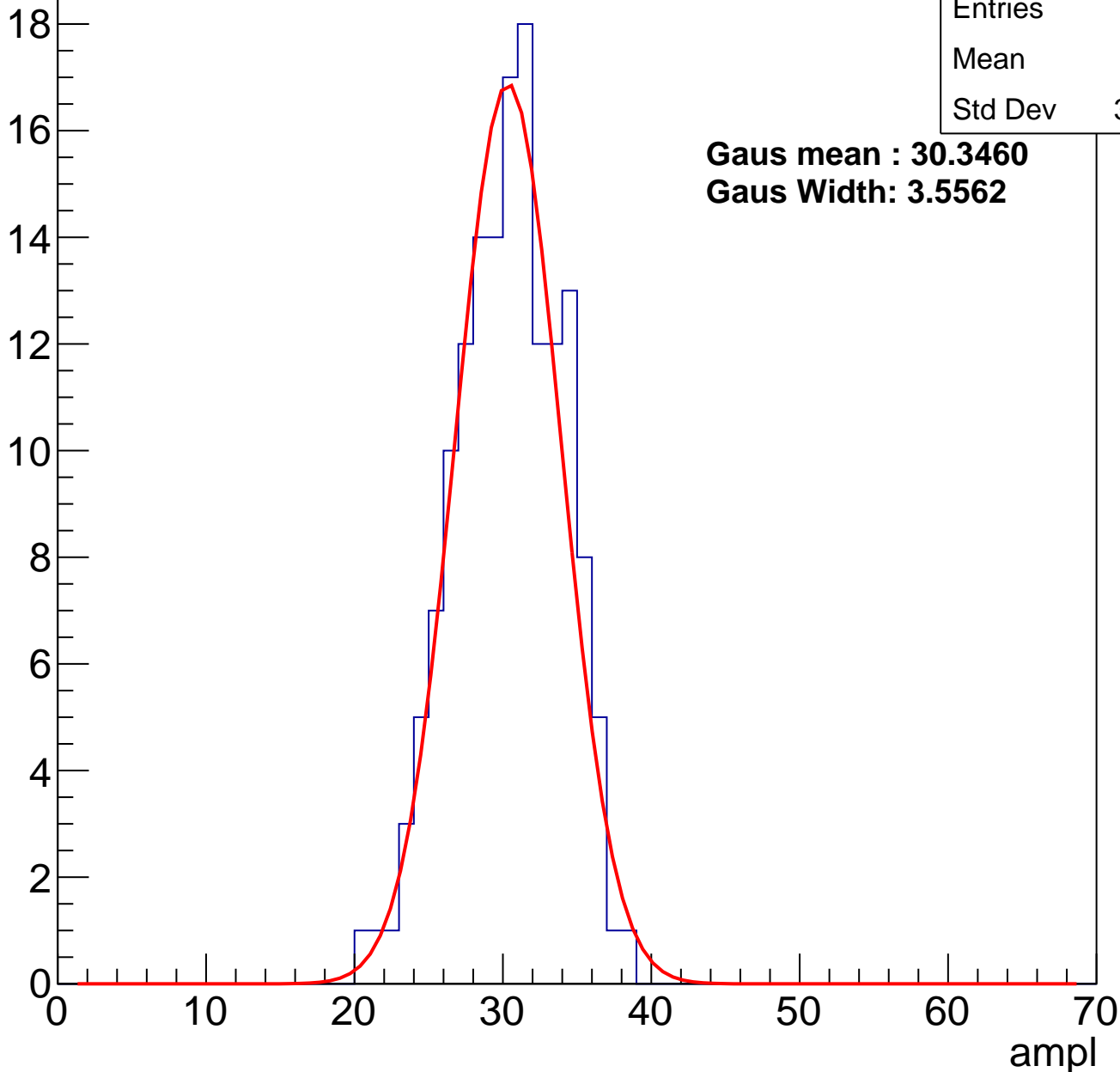
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	29.9
Std Dev	3.555

Gaus mean : 30.3460

Gaus Width: 3.5562

Entry



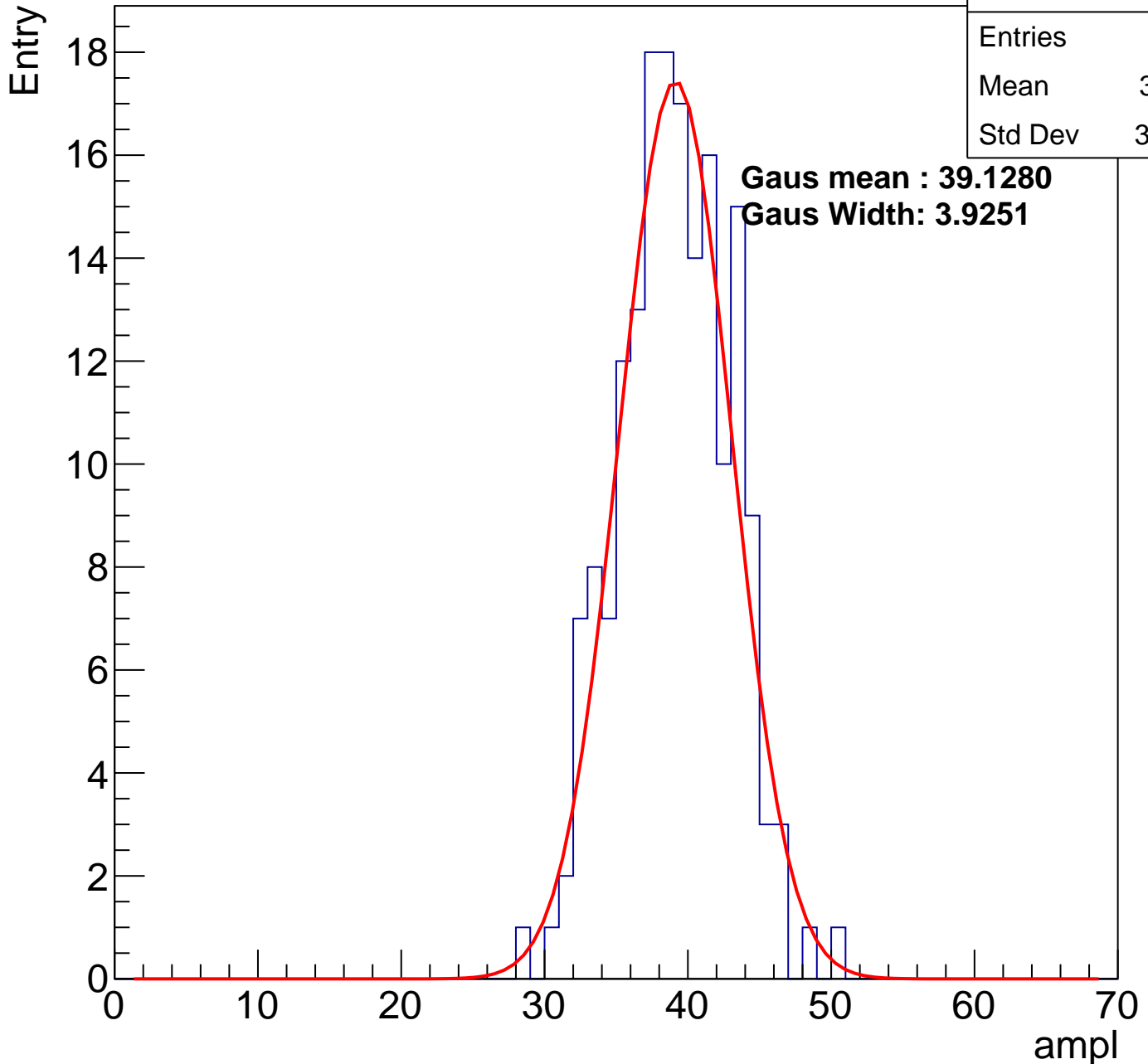
B0L101S, U25-ch11, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	176
Mean	38.61
Std Dev	3.842

Gaus mean : 39.1280

Gaus Width: 3.9251



B0L101S, U25-ch11, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

116

Mean

45.36

Std Dev

3.647

Gaus mean : 46.0292

Gaus Width: 3.4745

0

10

20

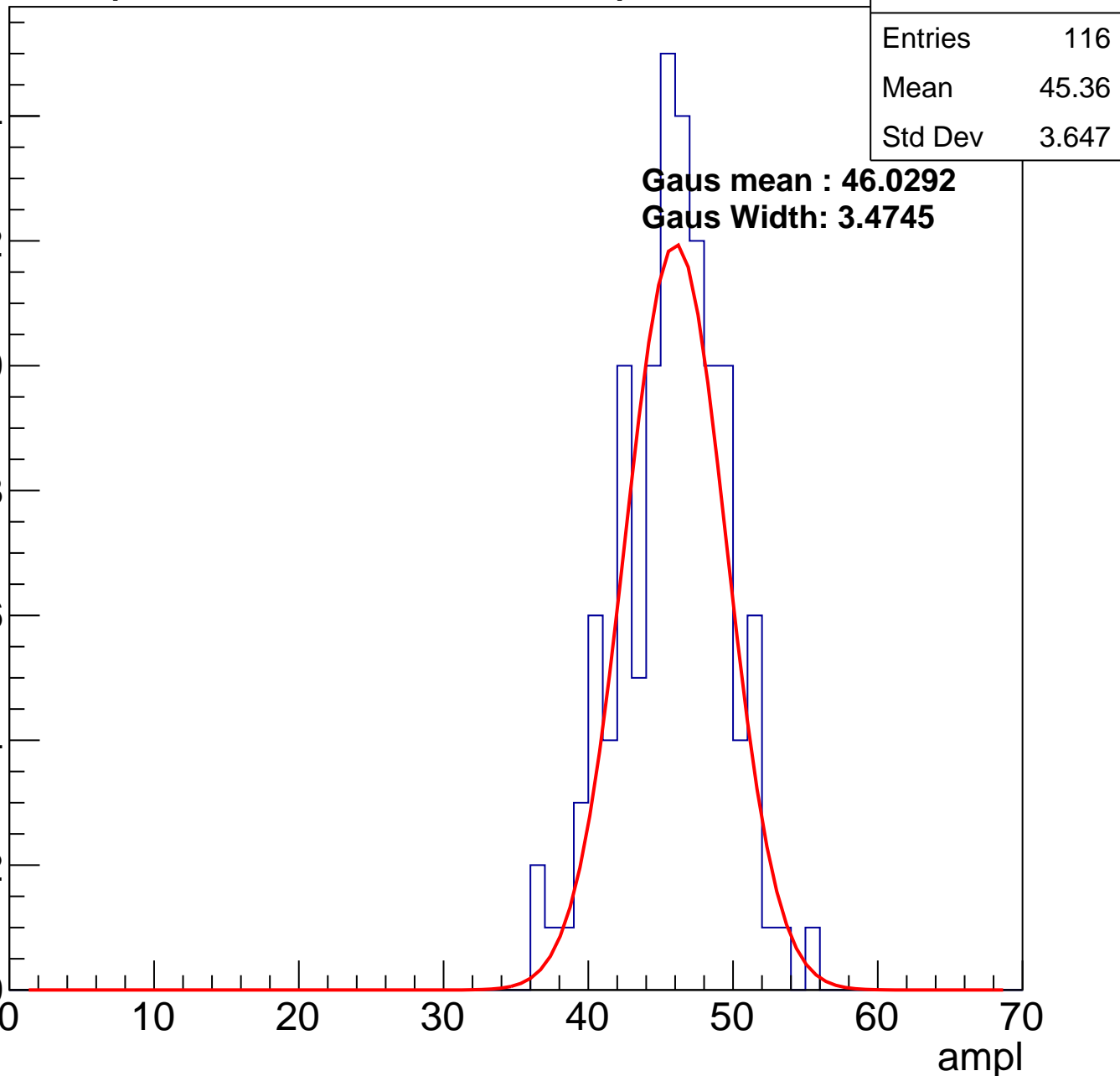
30

40

50

60

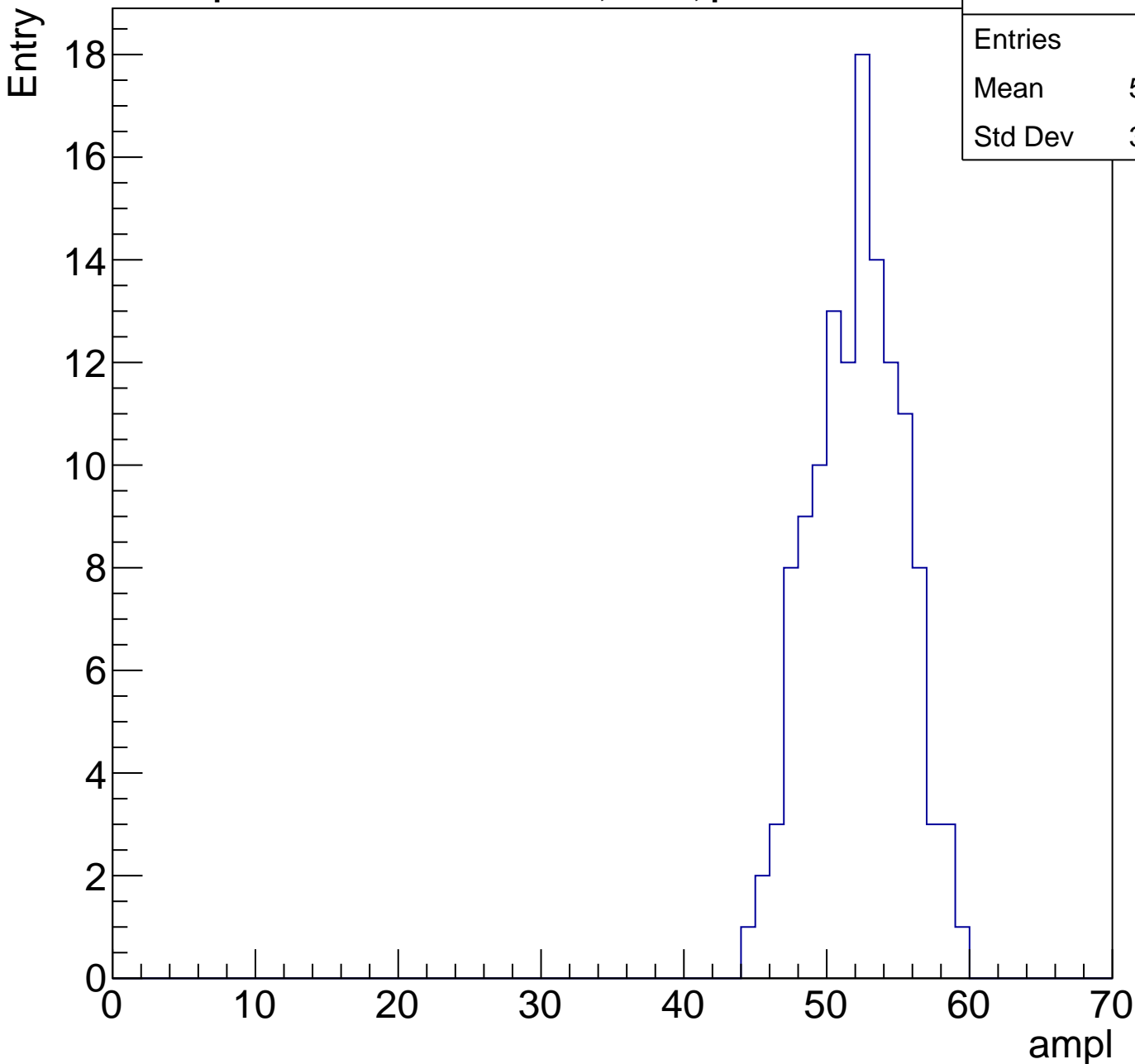
ampl



B0L101S, U25-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	51.68
Std Dev	3.157



B0L101S, U25-ch11, adc4

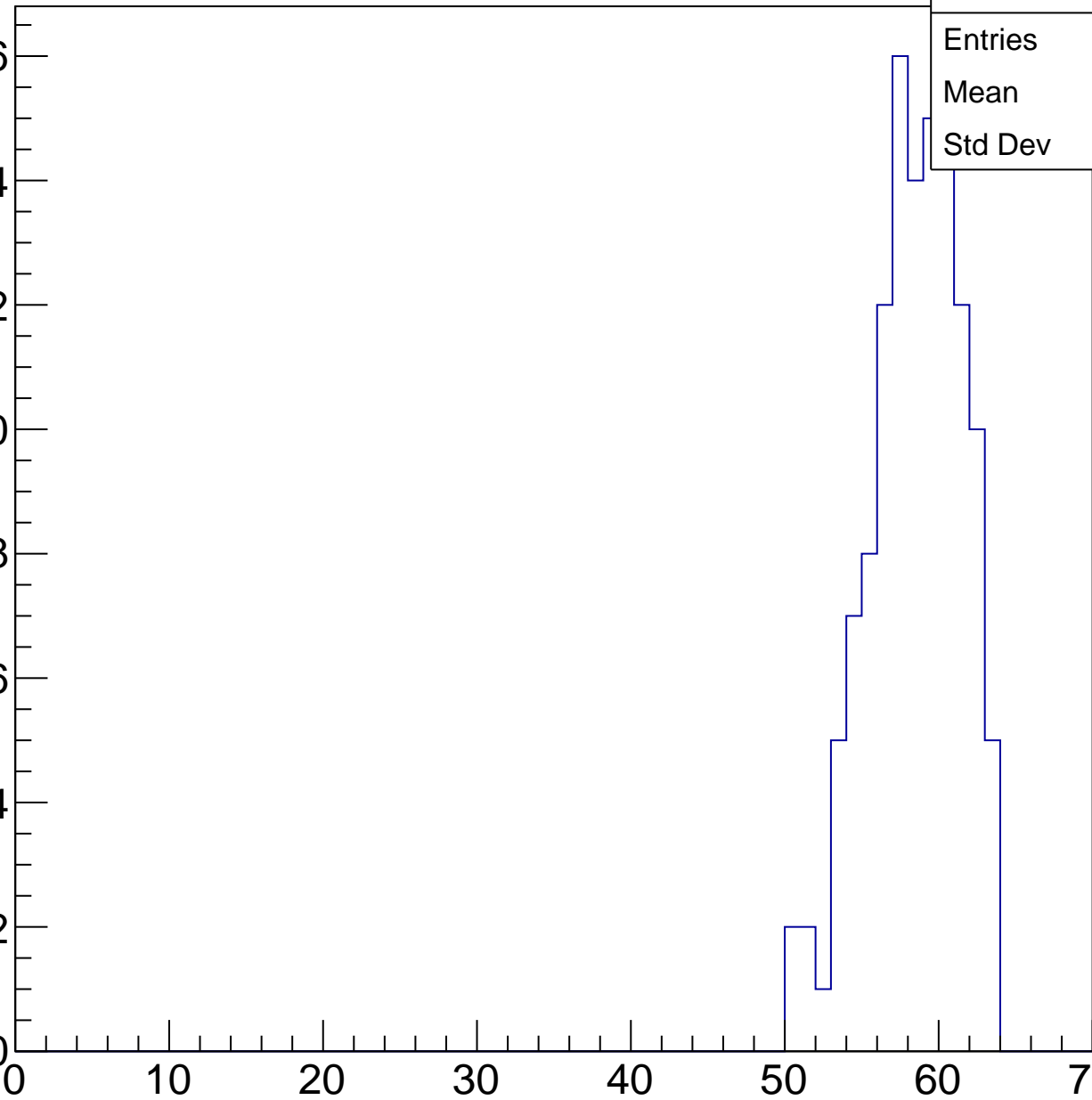
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	57.96
Std Dev	2.976

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch11, adc5

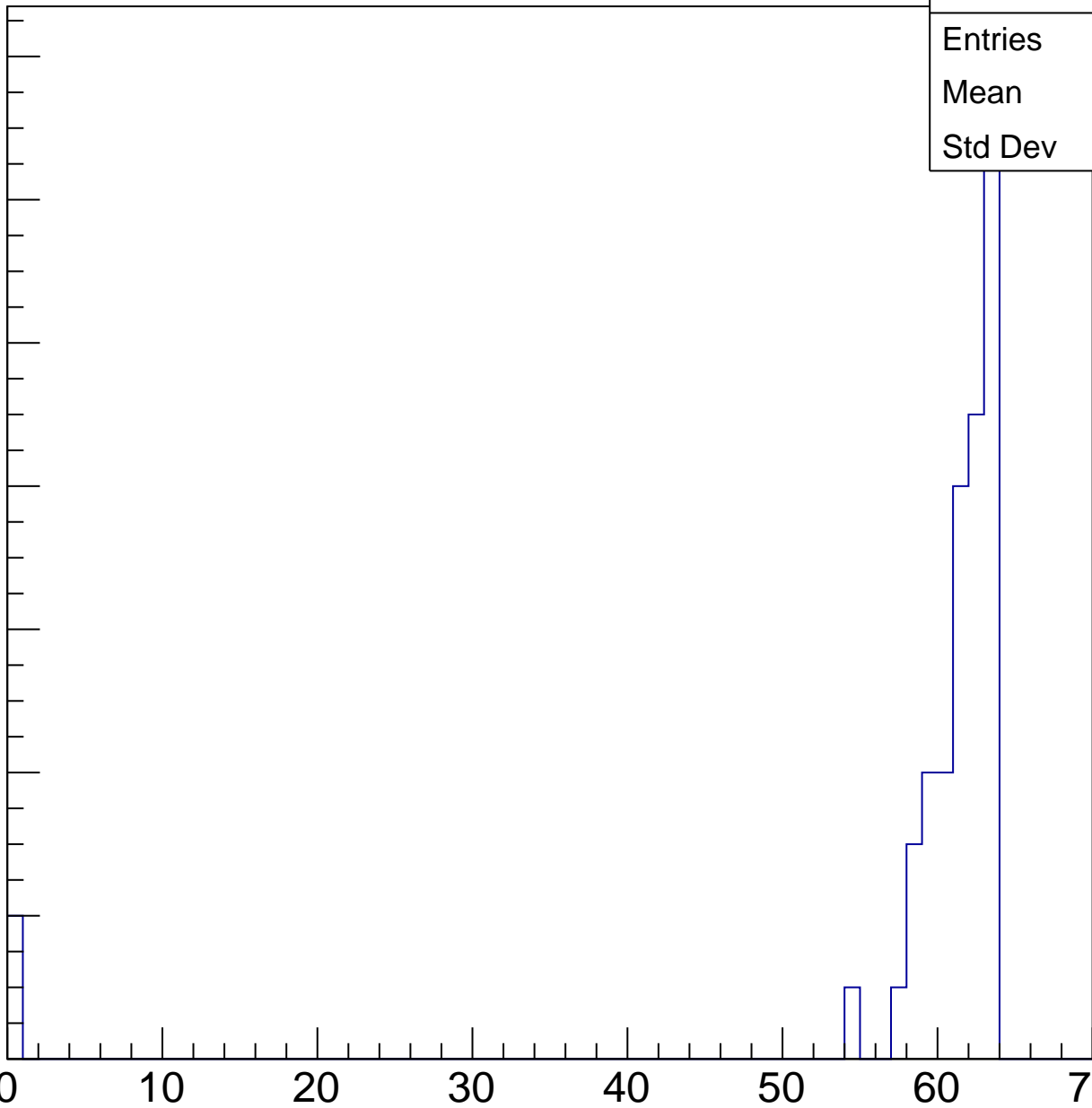
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	46
Mean	58.46
Std Dev	12.62

ampl



B0L101S, U25-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch12, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

130

Mean

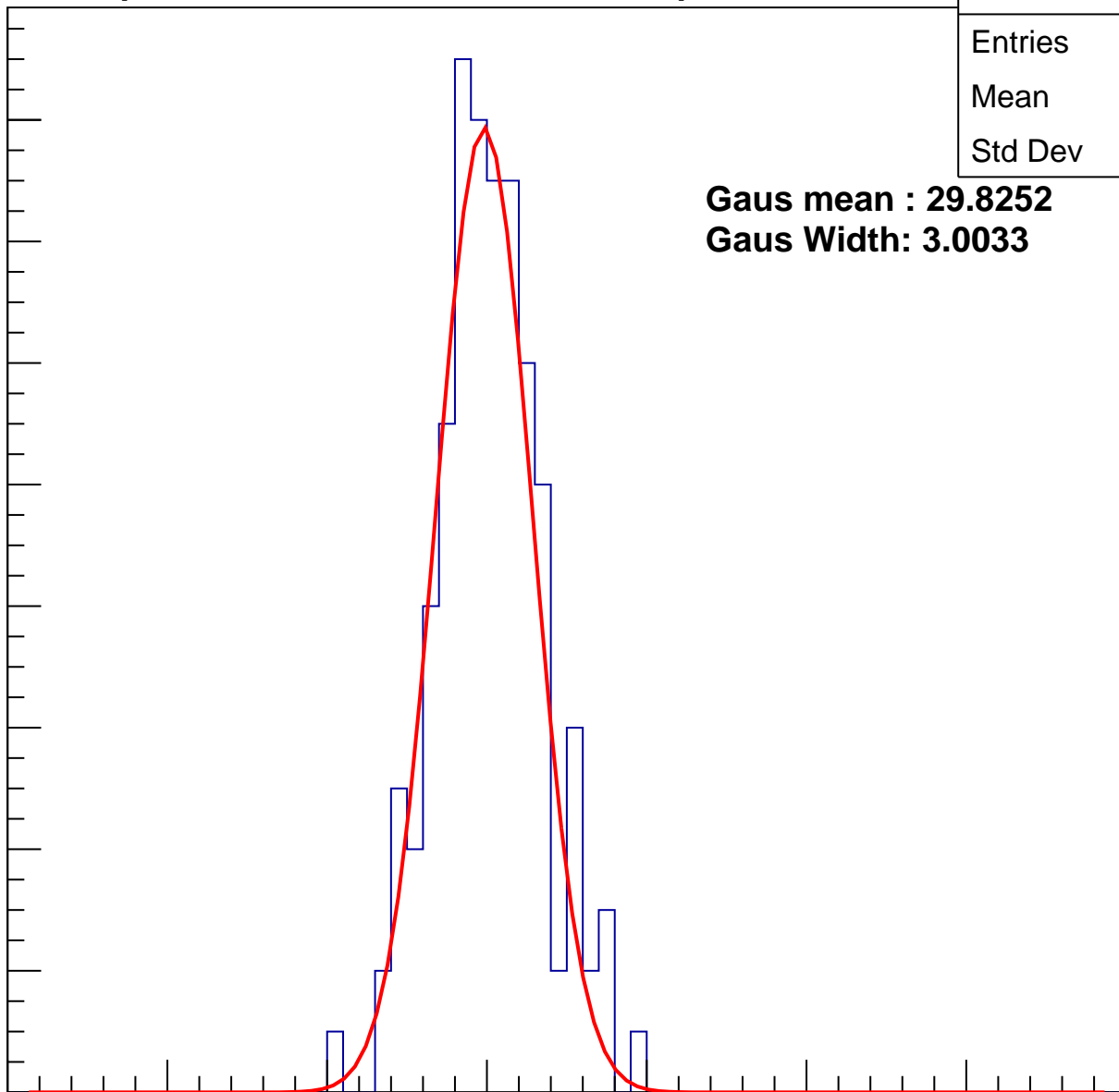
29.69

Std Dev

3.295

Gaus mean : 29.8252

Gaus Width: 3.0033



B0L101S, U25-ch12, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	36.46
Std Dev	3.624

Gaus mean : 37.1334

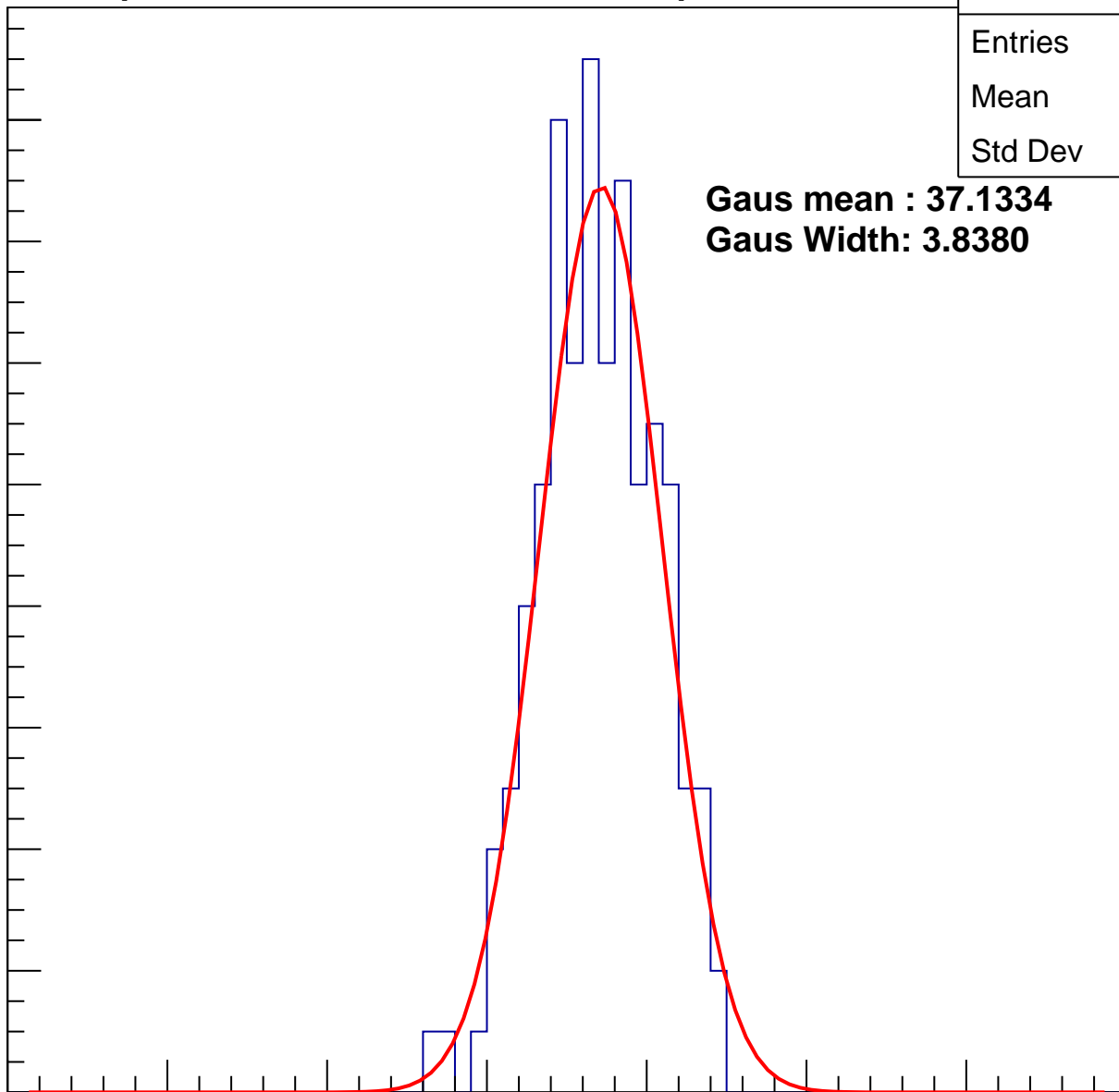
Gaus Width: 3.8380

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

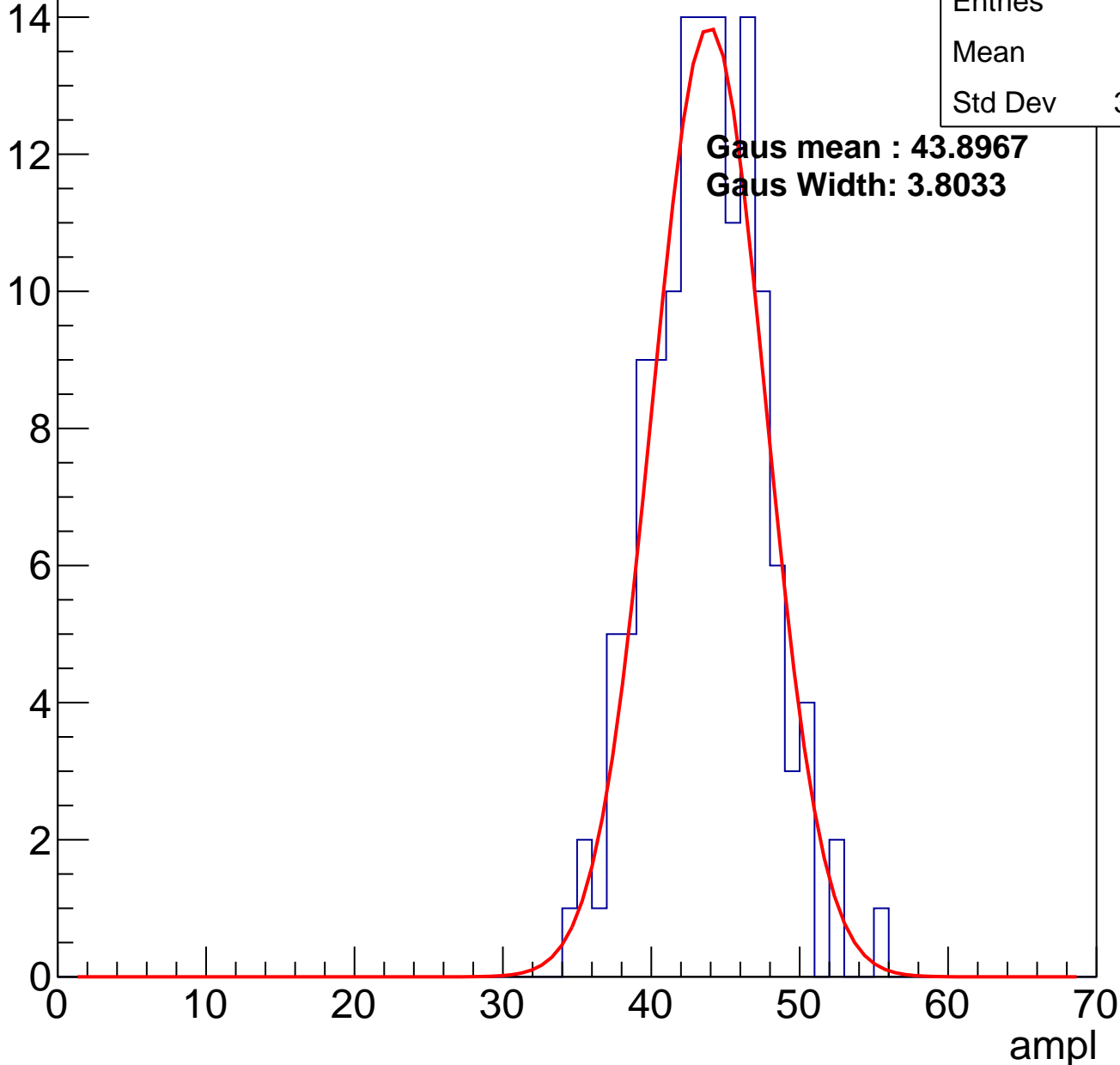
ampl



B0L101S, U25-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	50.58
Std Dev	3.572

Entry

16

14

12

10

8

6

4

2

0

0

10

20

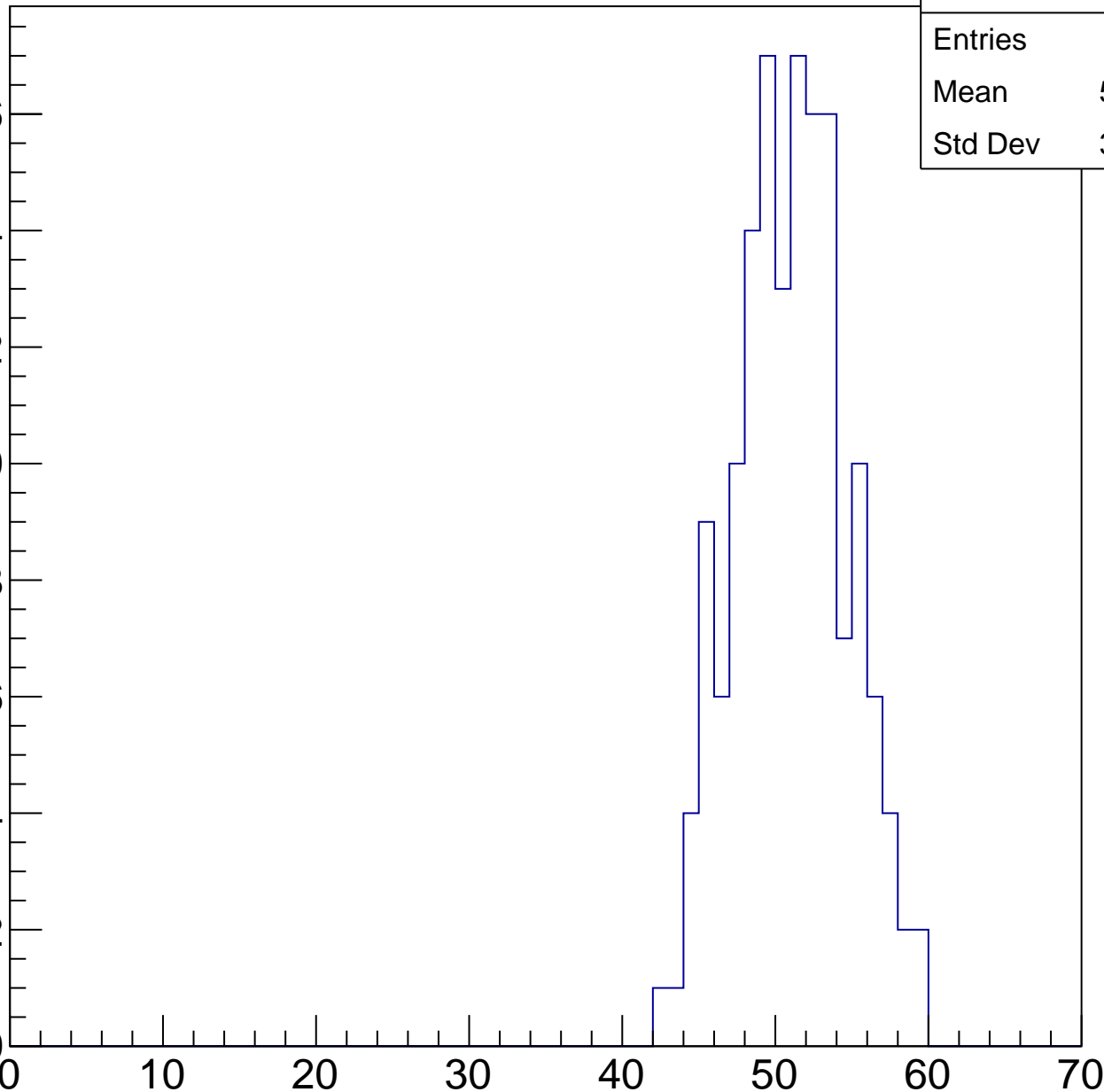
30

40

50

60

ampl

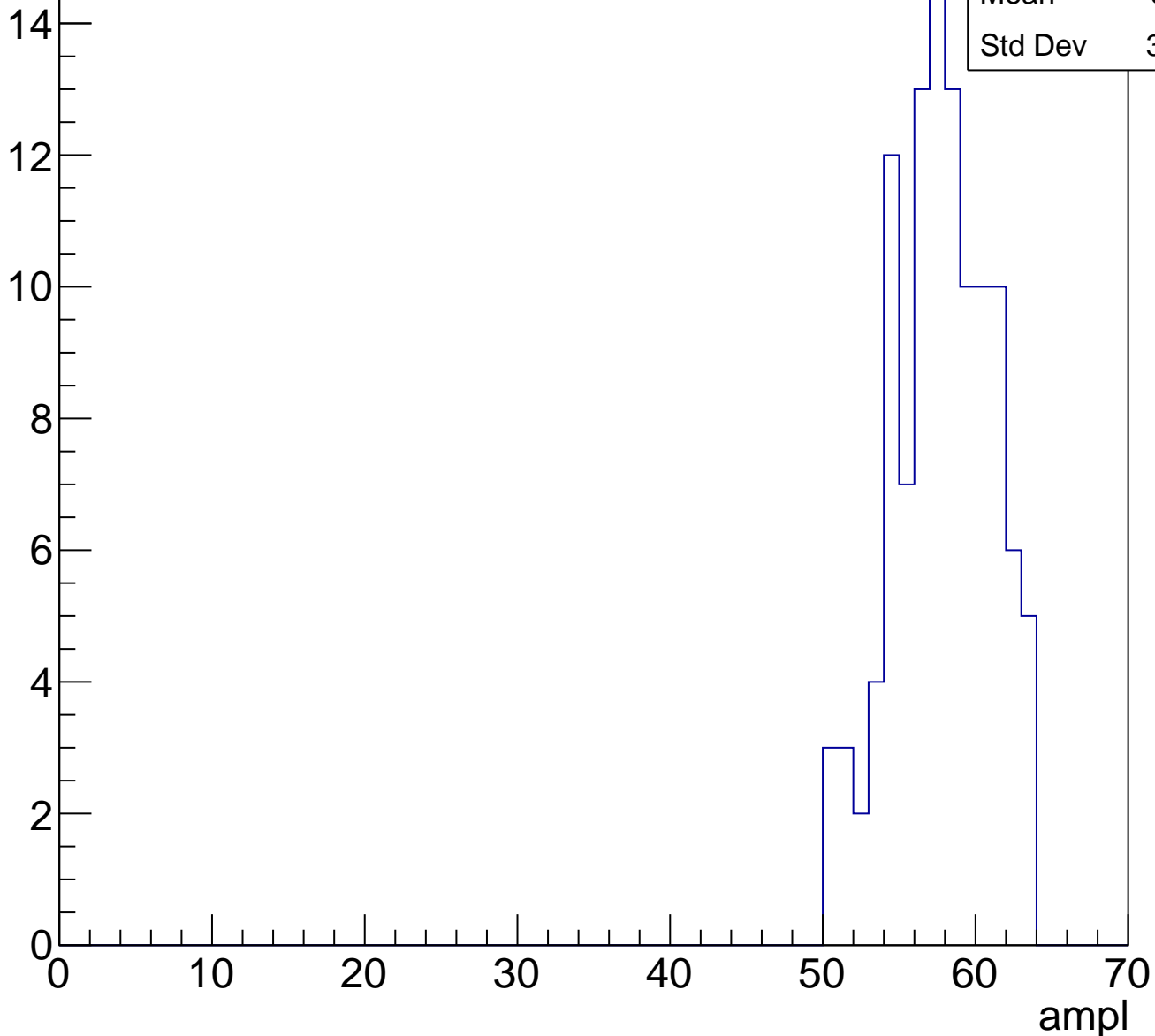


B0L101S, U25-ch12, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	57.31
Std Dev	3.176

Entry



B0L101S, U25-ch12, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

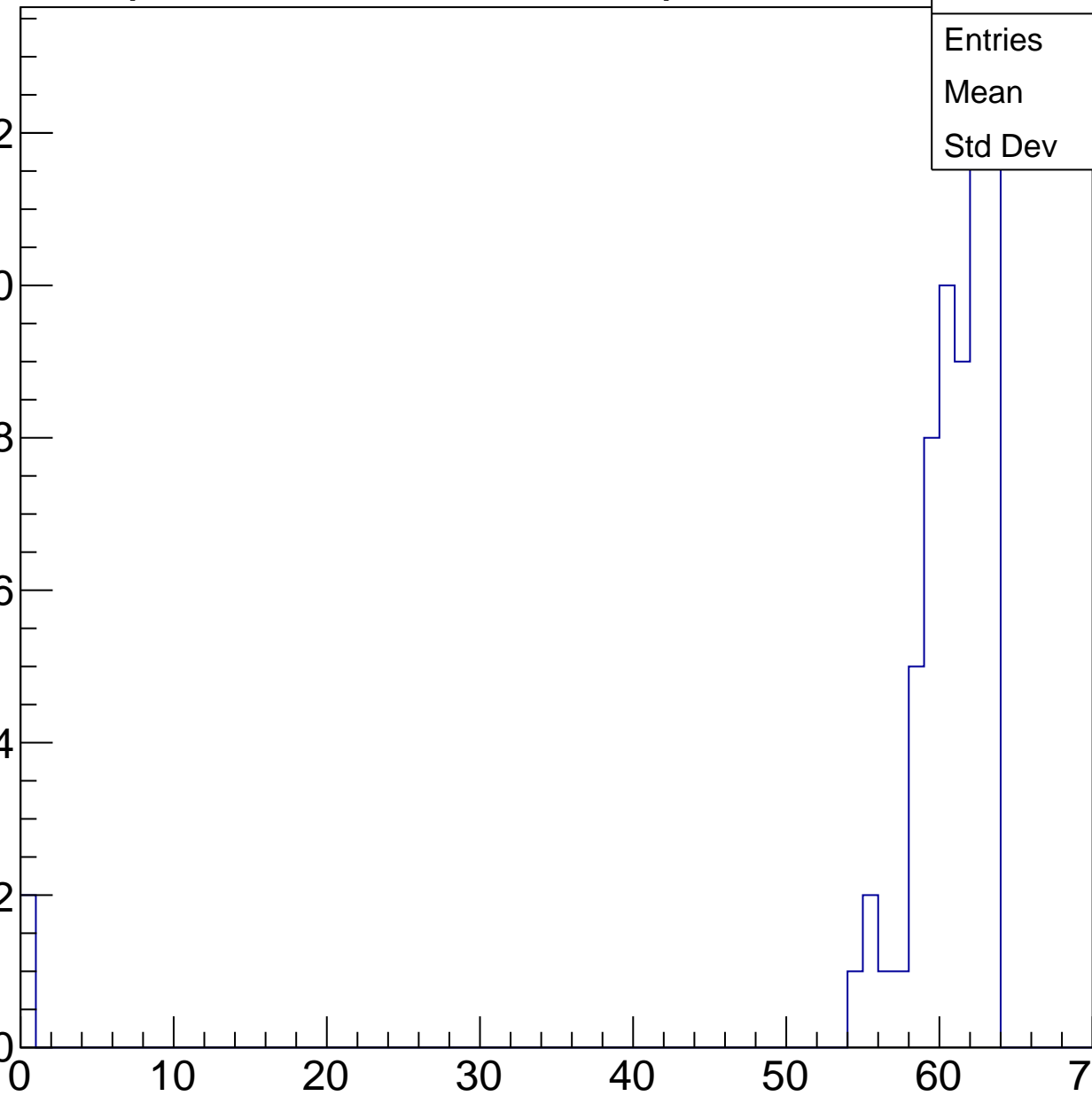
4

2

0

Entries	64
Mean	58.59
Std Dev	10.74

ampl



B0L101S, U25-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

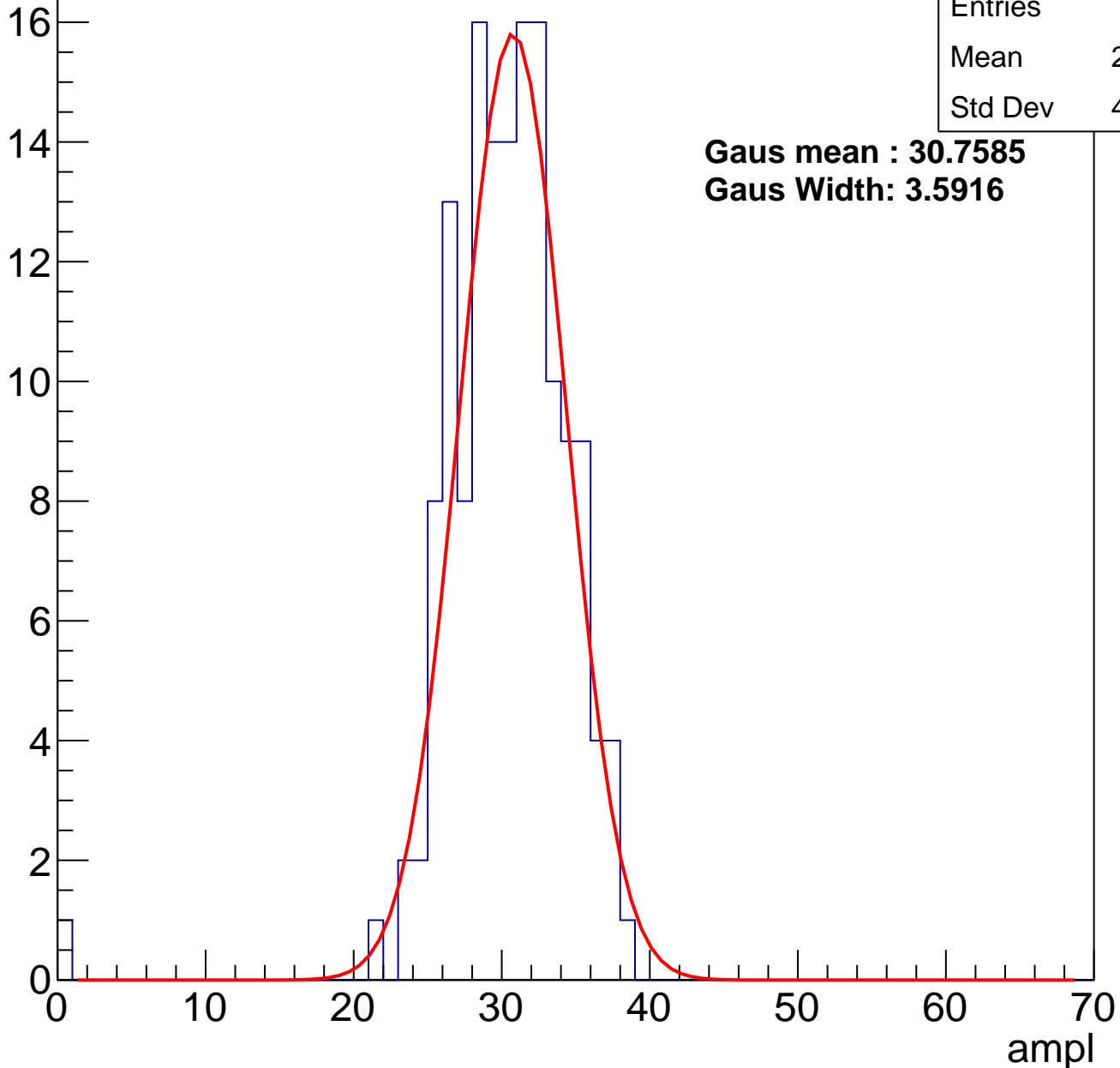
B0L101S, U25-ch13, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	29.95
Std Dev	4.233

Gaus mean : 30.7585
Gaus Width: 3.5916

Entry



B0L101S, U25-ch13, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	37.46
Std Dev	3.475

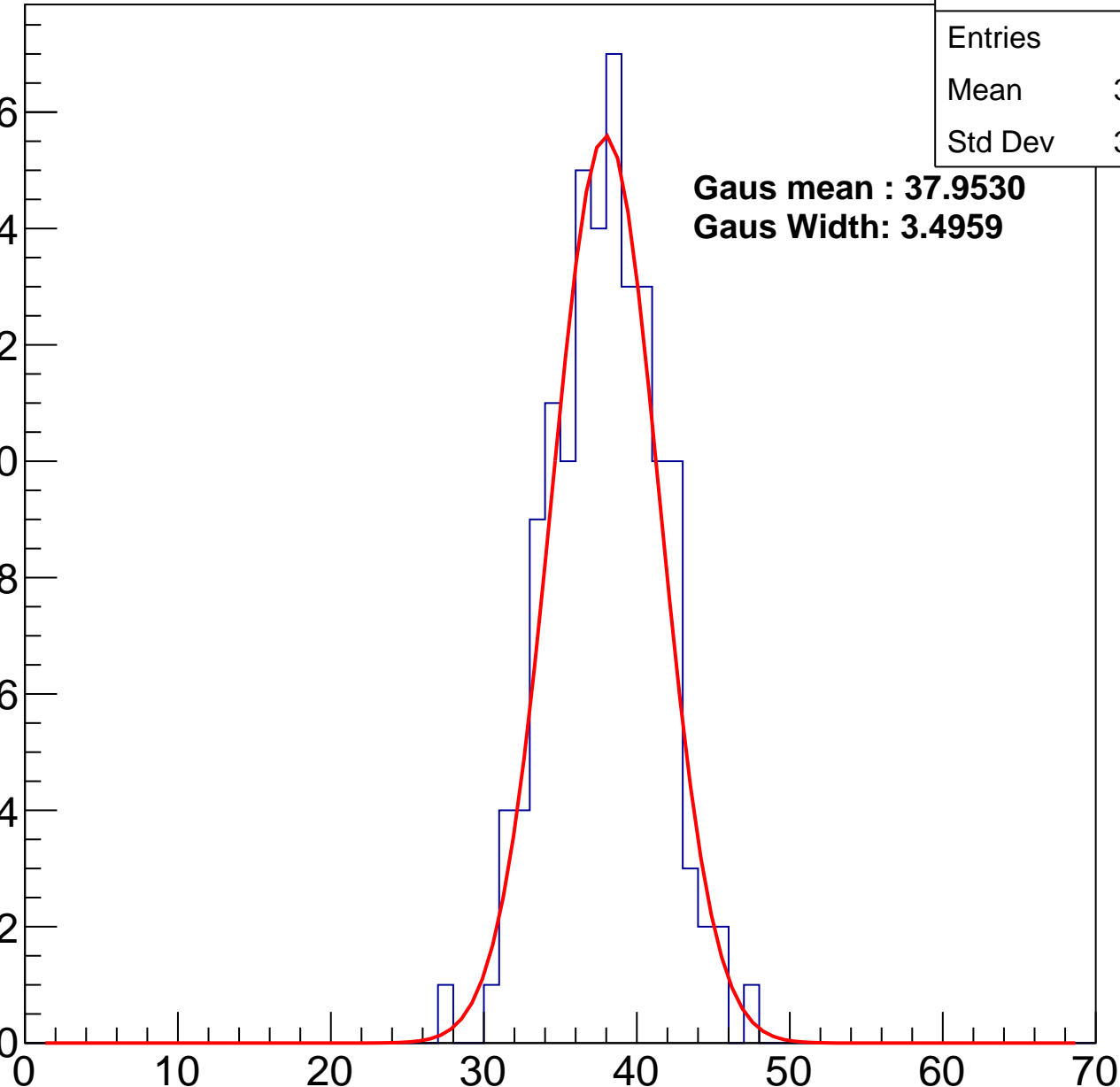
Gaus mean : 37.9530

Gaus Width: 3.4959

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch13, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	44.27
Std Dev	3.333

Gaus mean : 45.1029

Gaus Width: 3.5800

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

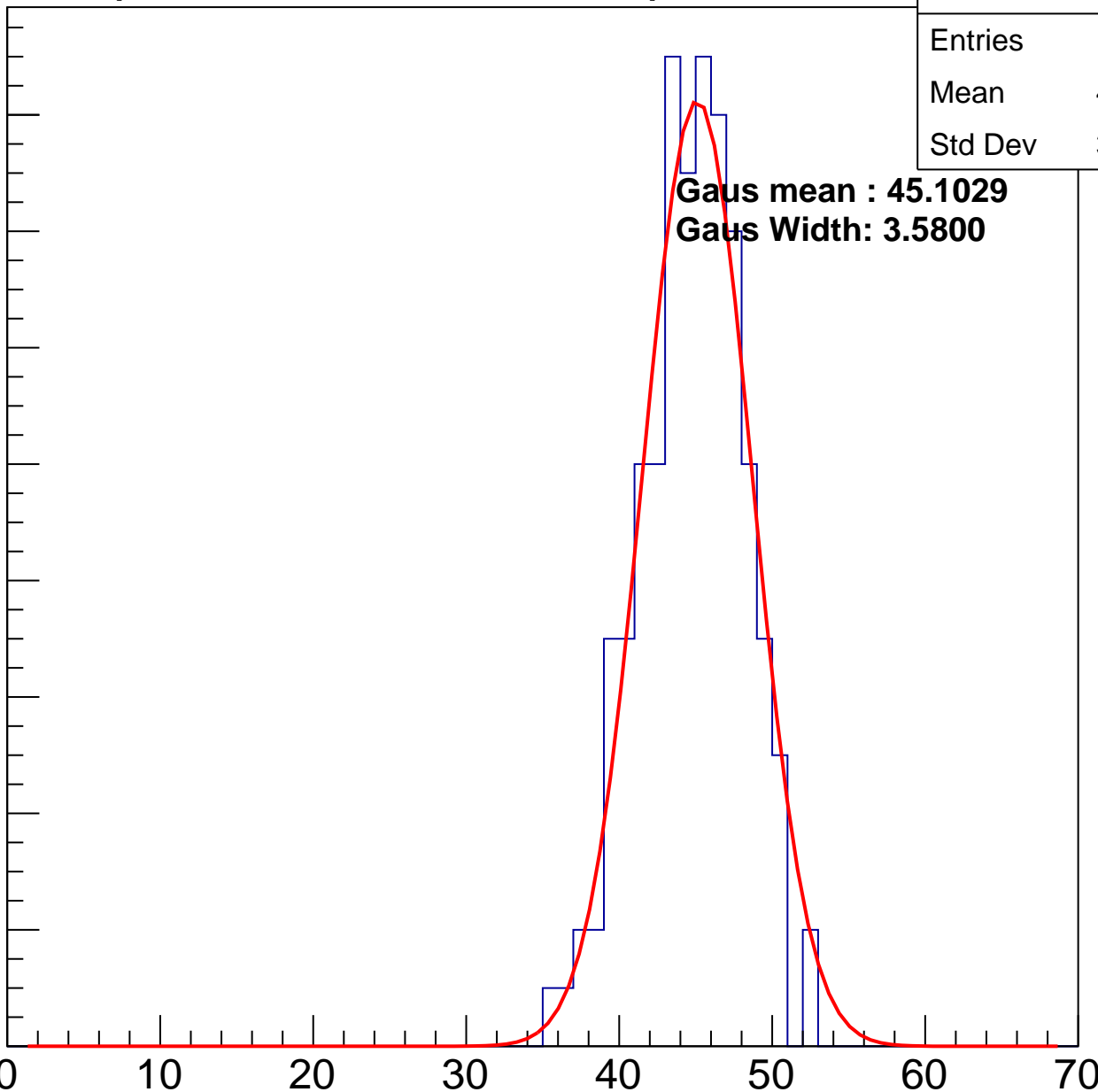
40

50

60

70

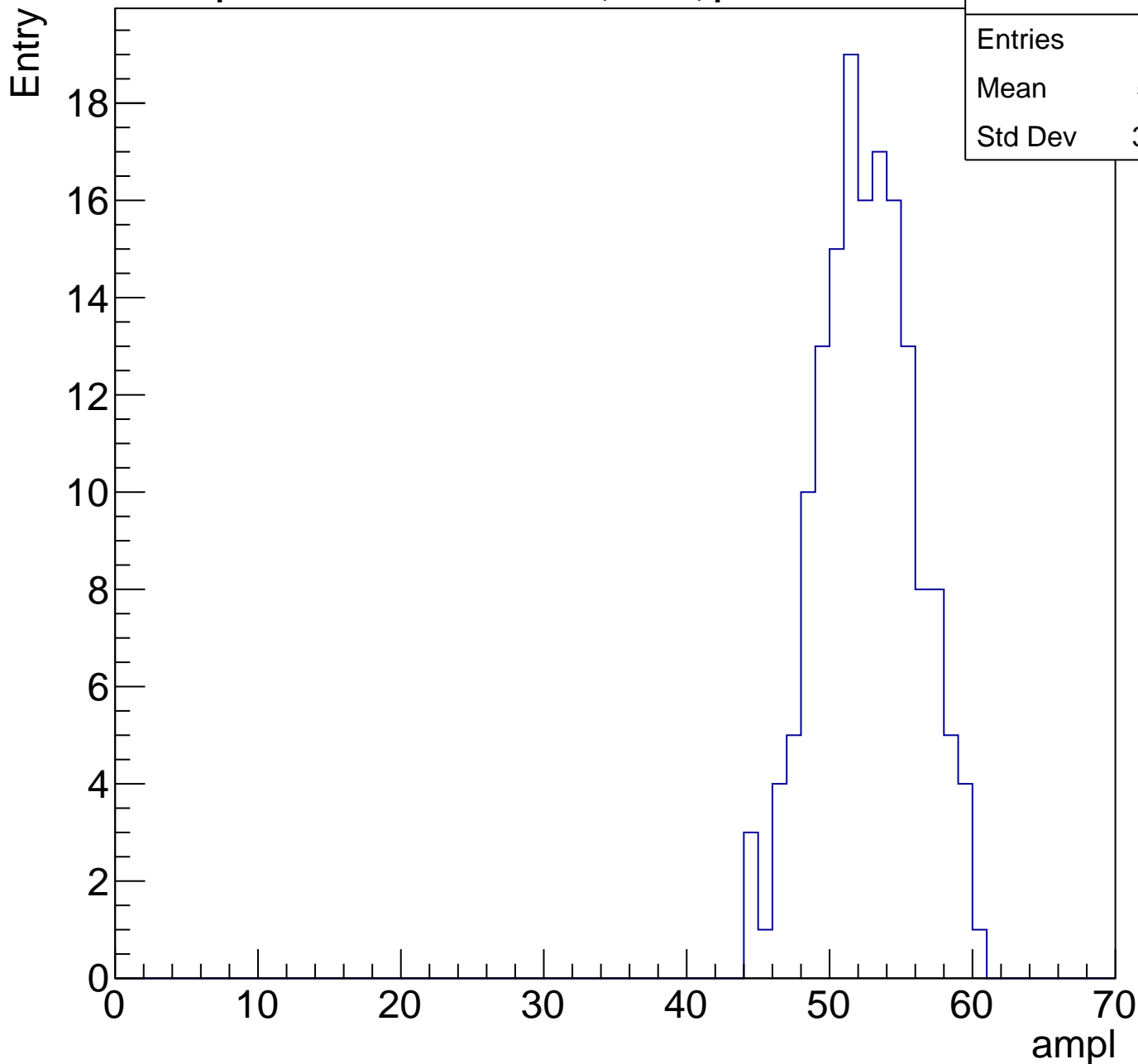
ampl



B0L101S, U25-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	52.11
Std Dev	3.403



B0L101S, U25-ch13, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

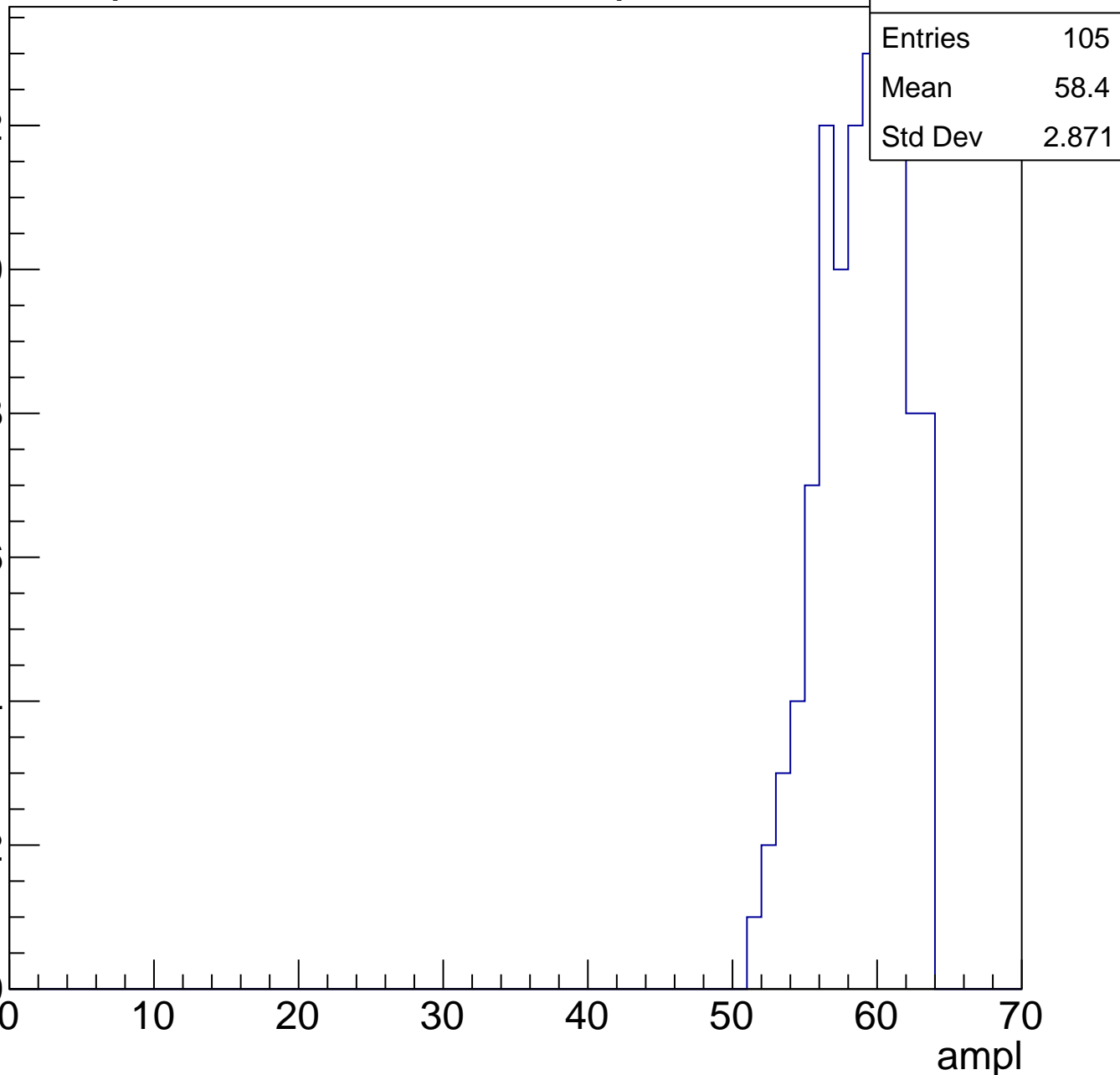
50

60

70

ampl

Entries	105
Mean	58.4
Std Dev	2.871



B0L101S, U25-ch13, adc5

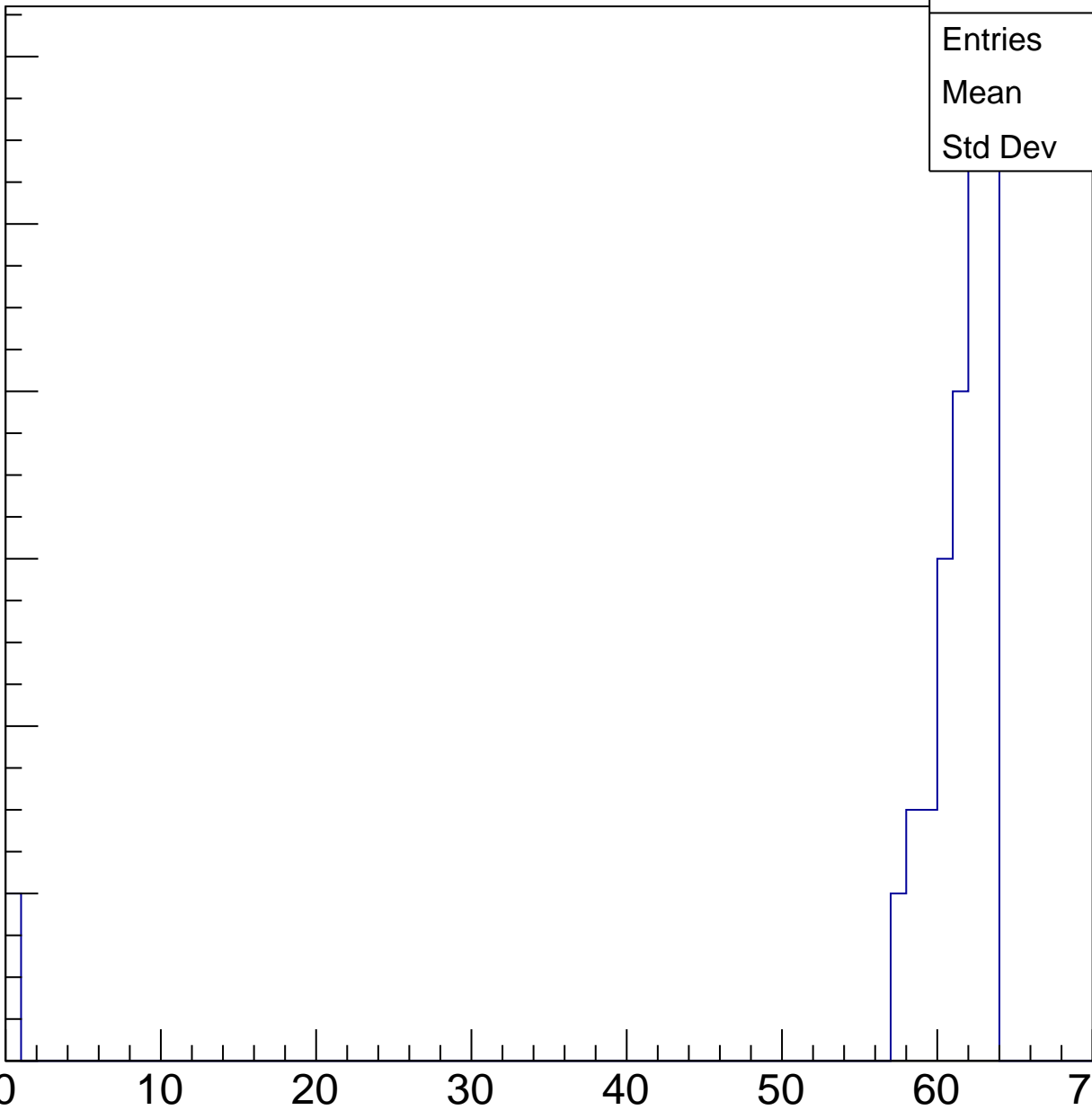
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	47
Mean	58.53
Std Dev	12.46

ampl



B0L101S, U25-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L101S, U25-ch14, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	30.49
Std Dev	5.128

Gaus mean : 31.3695

Gaus Width: 3.6406

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

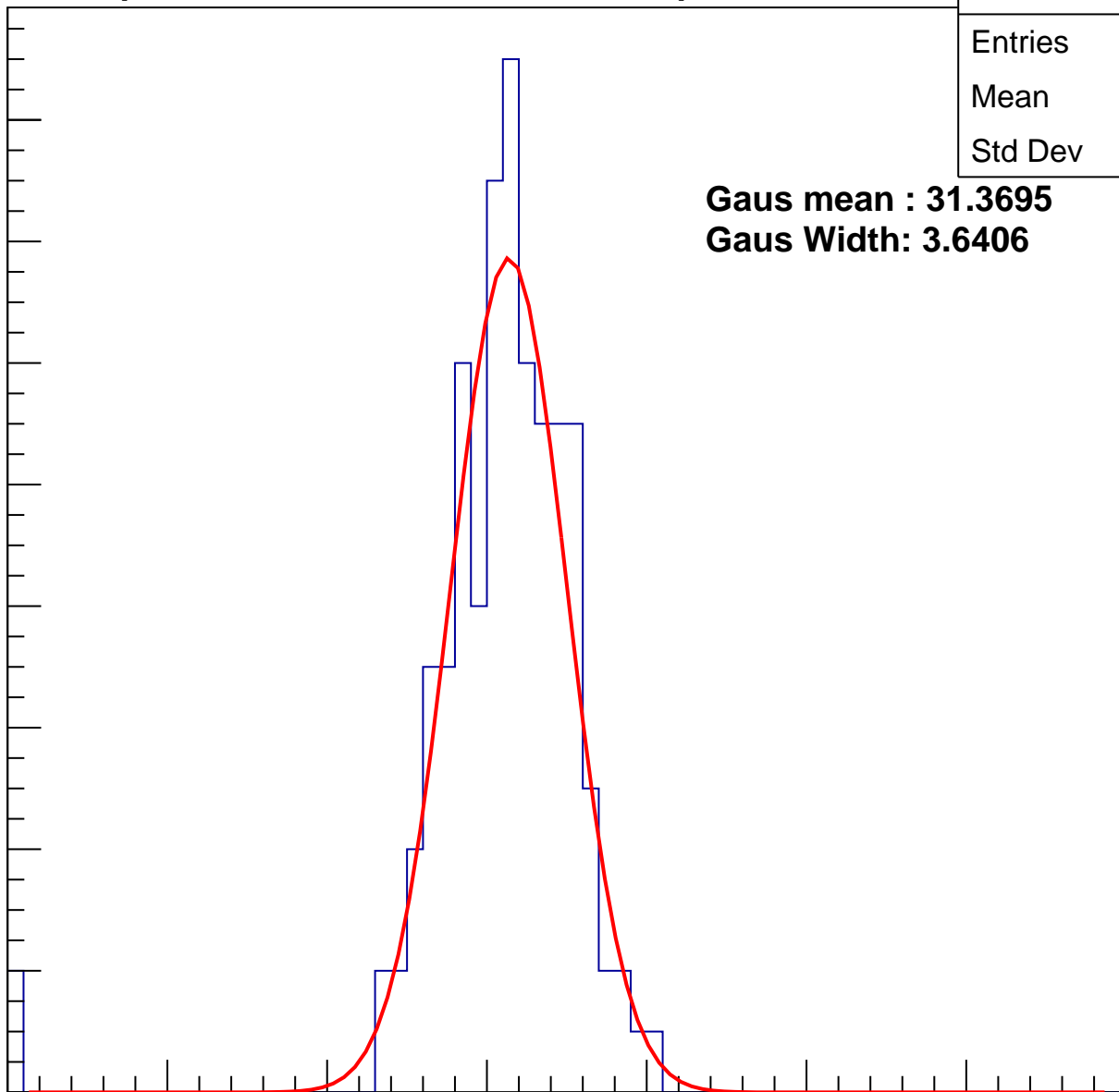
40

50

60

70

ampl



B0L101S, U25-ch14, adc1

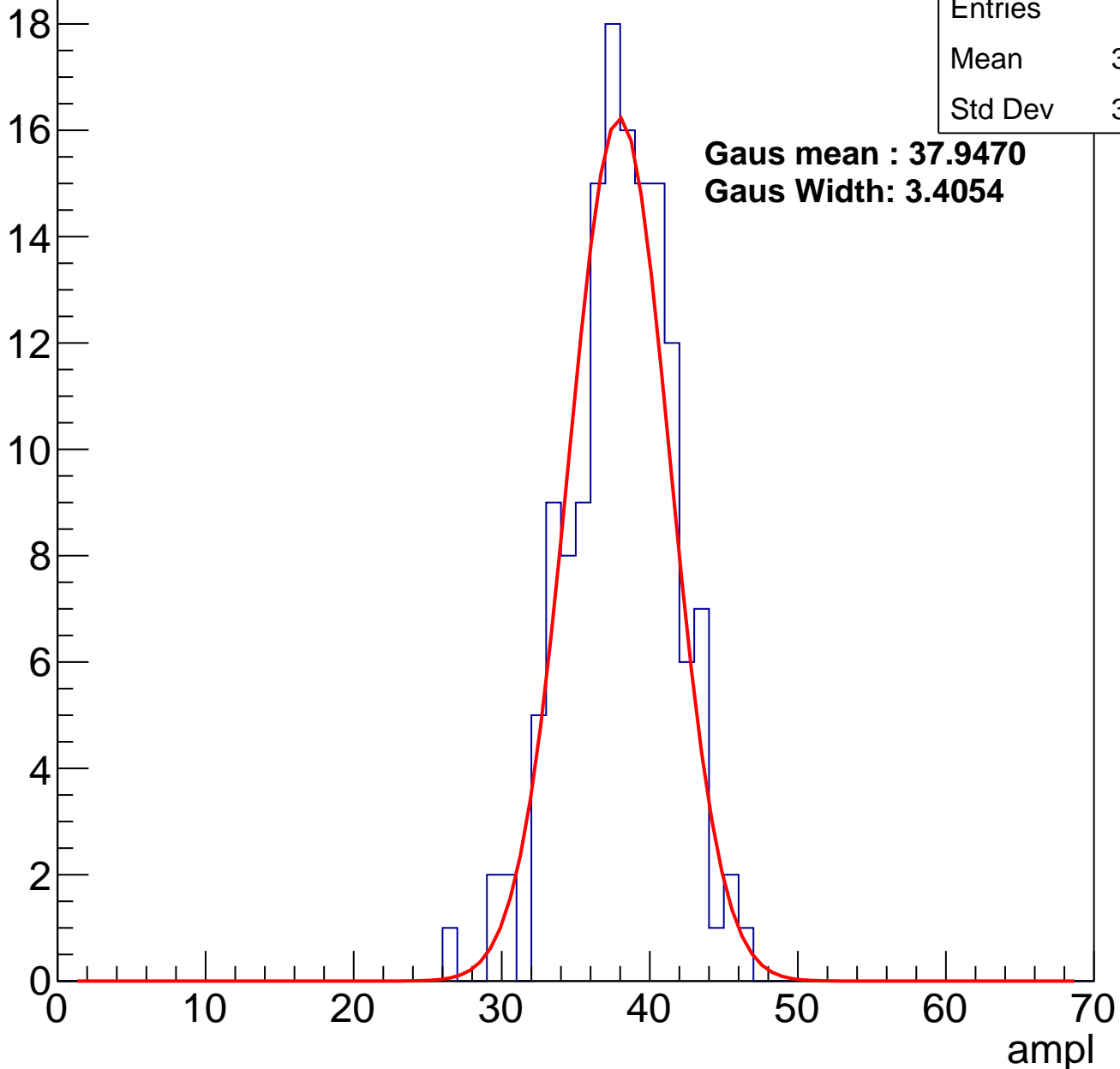
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	37.58
Std Dev	3.489

Gaus mean : 37.9470

Gaus Width: 3.4054

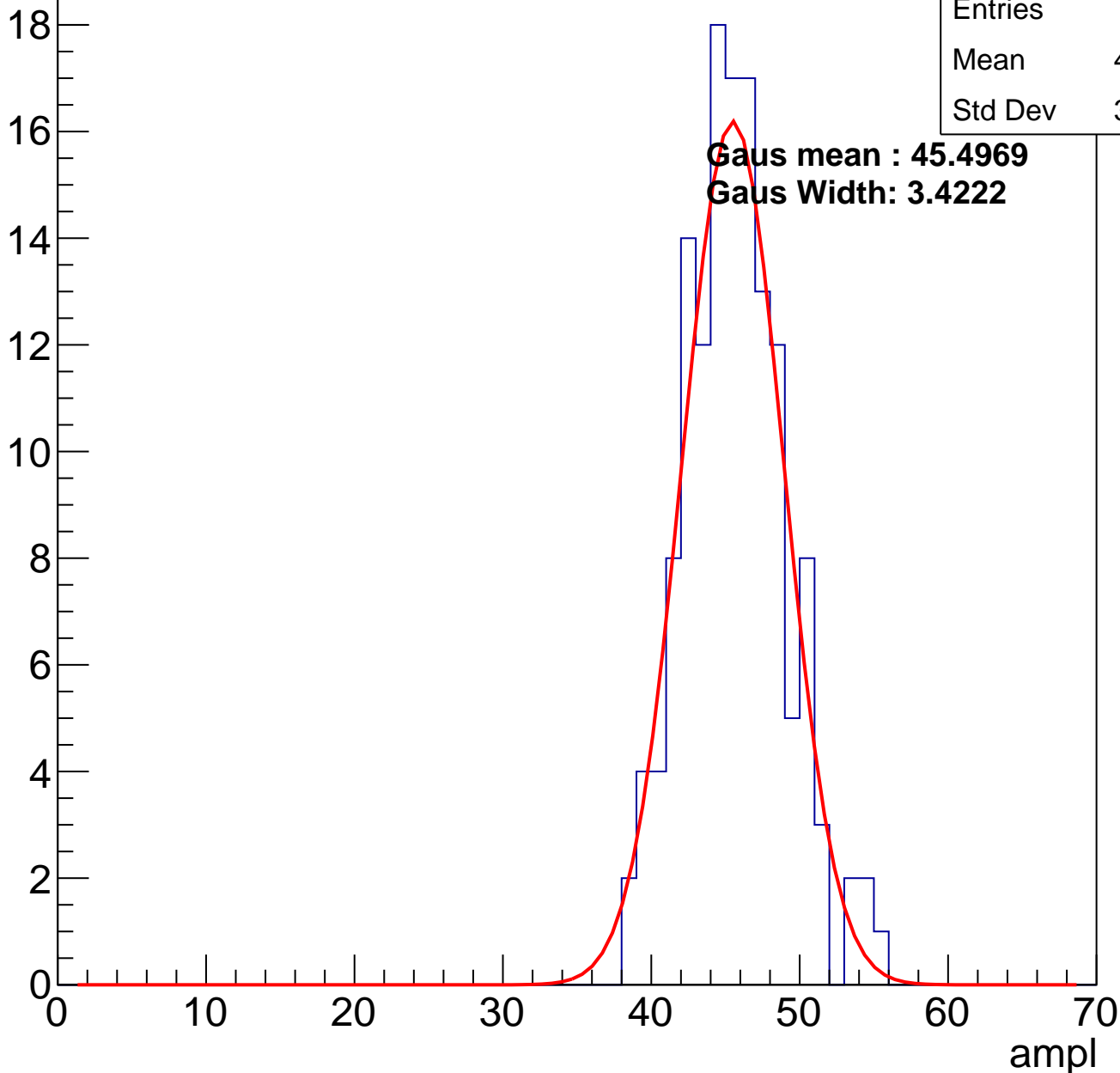
Entry



B0L101S, U25-ch14, adc2

calib_packv5_042523_0143.root, FC#1, port C1

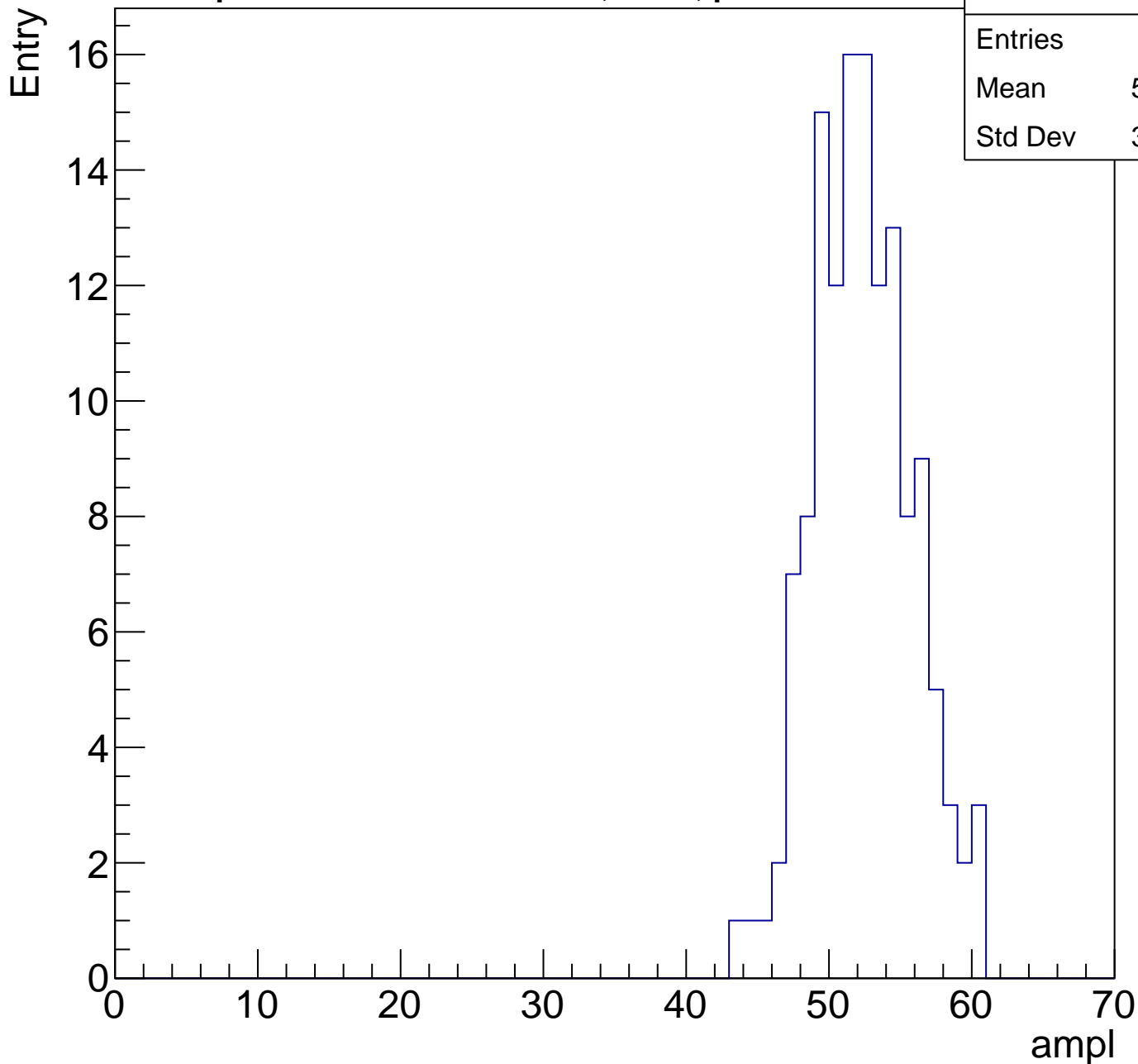
Entry



B0L101S, U25-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	51.93
Std Dev	3.423



B0L101S, U25-ch14, adc4

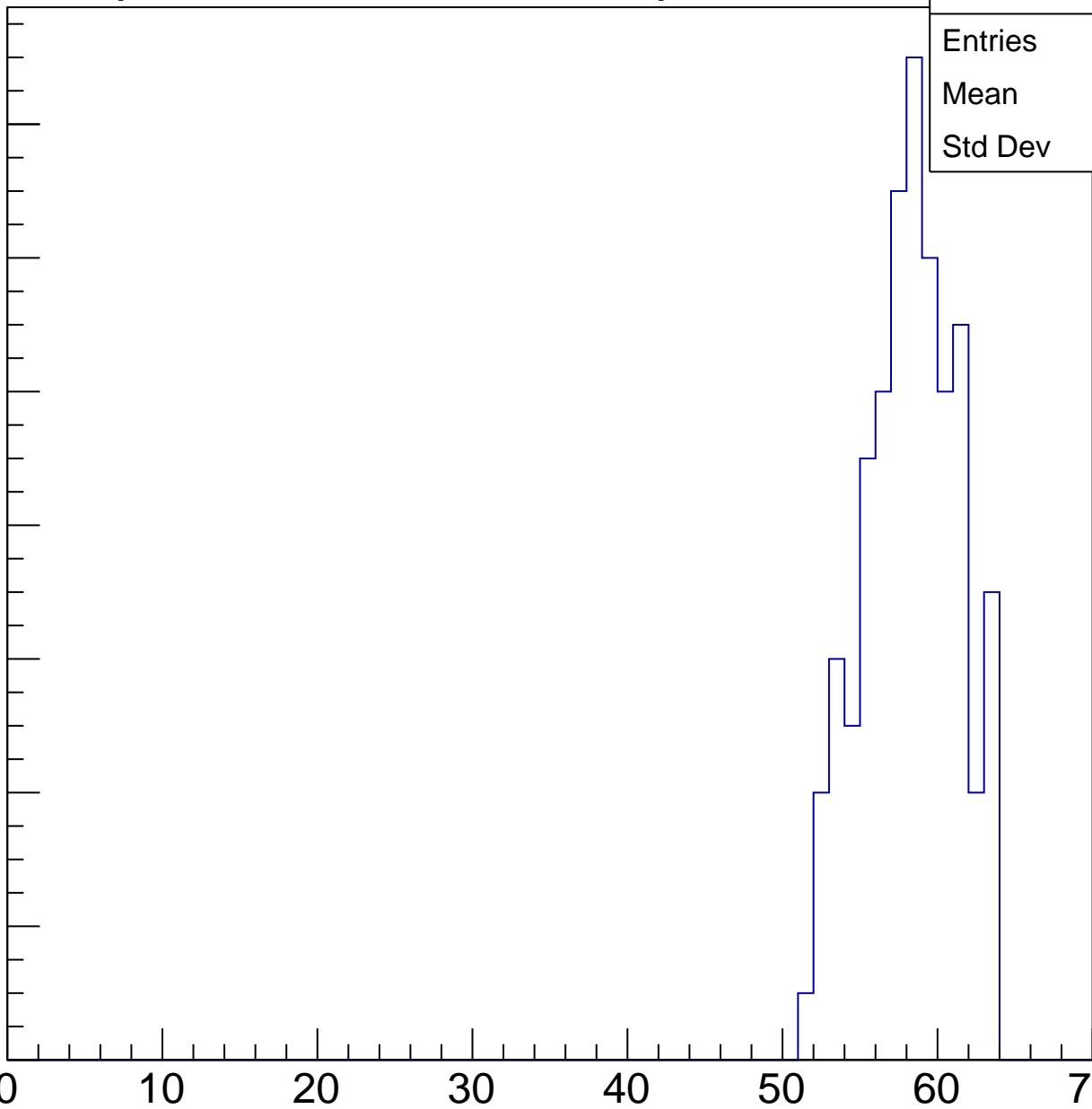
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	57.77
Std Dev	2.966

ampl



B0L101S, U25-ch14, adc5

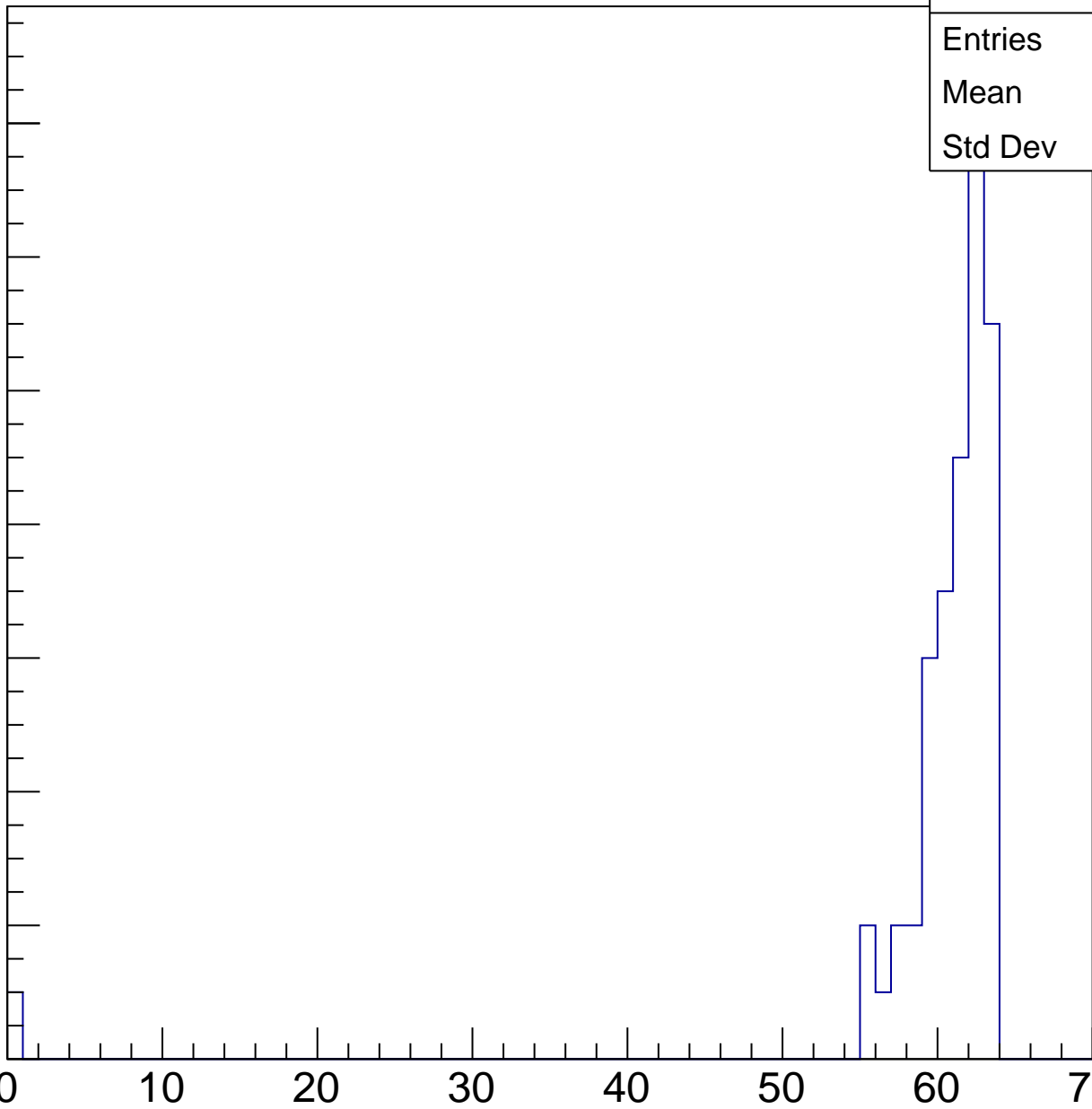
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	56
Mean	59.68
Std Dev	8.303

ampl



B0L101S, U25-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

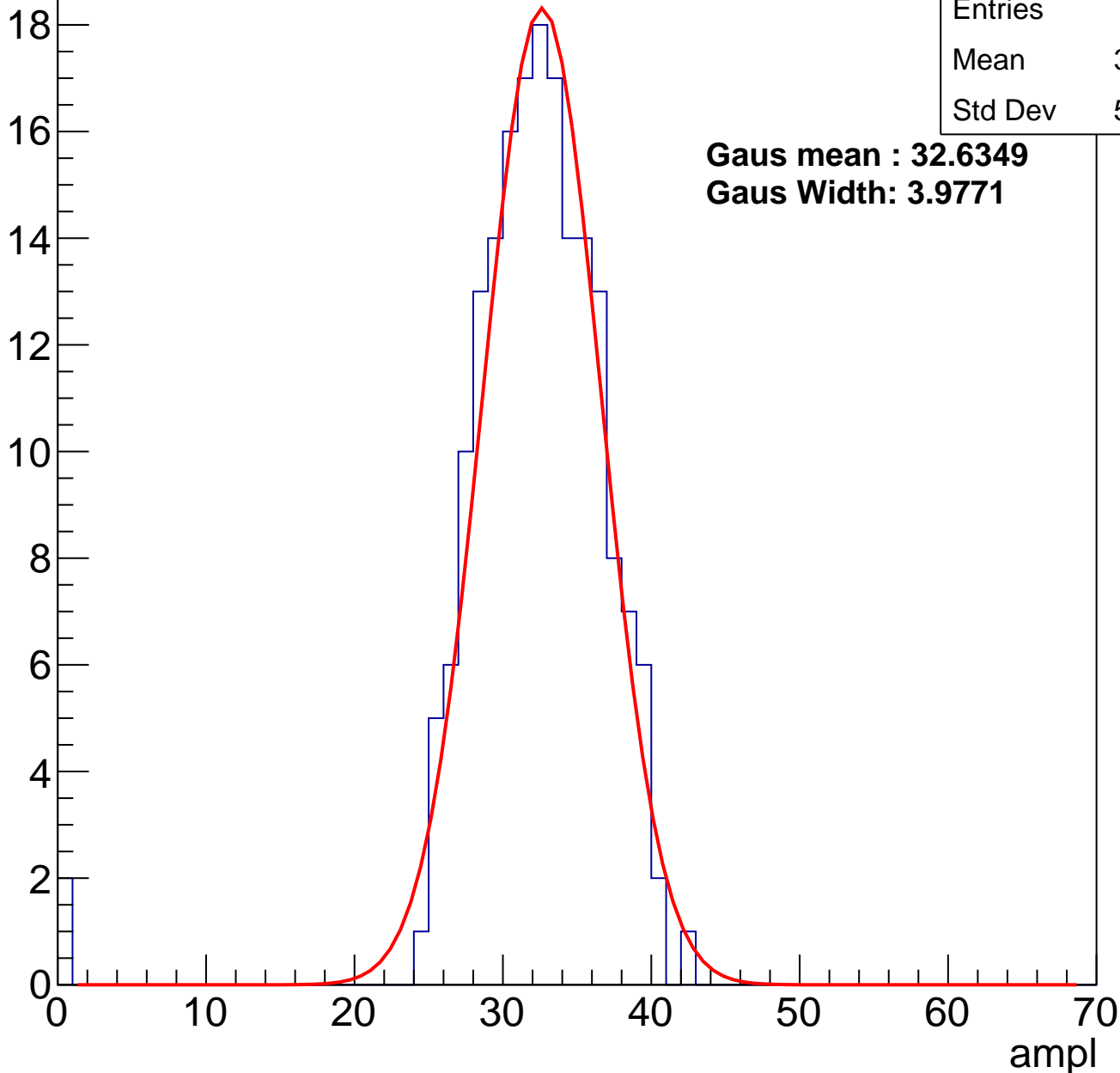


Entries	1
Mean	23
Std Dev	0

B0L101S, U25-ch15, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



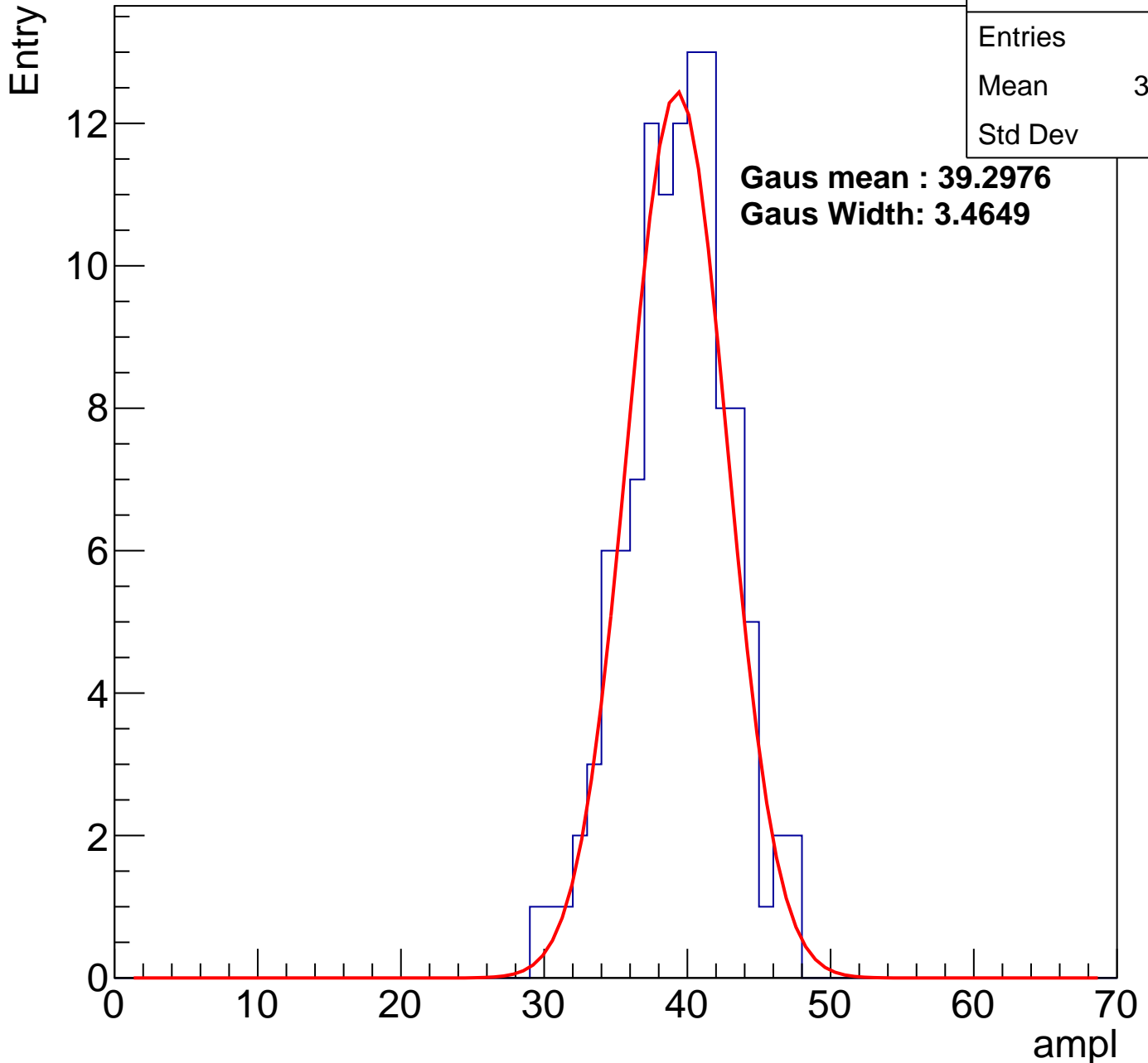
B0L101S, U25-ch15, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	38.89
Std Dev	3.59

Gaus mean : 39.2976

Gaus Width: 3.4649

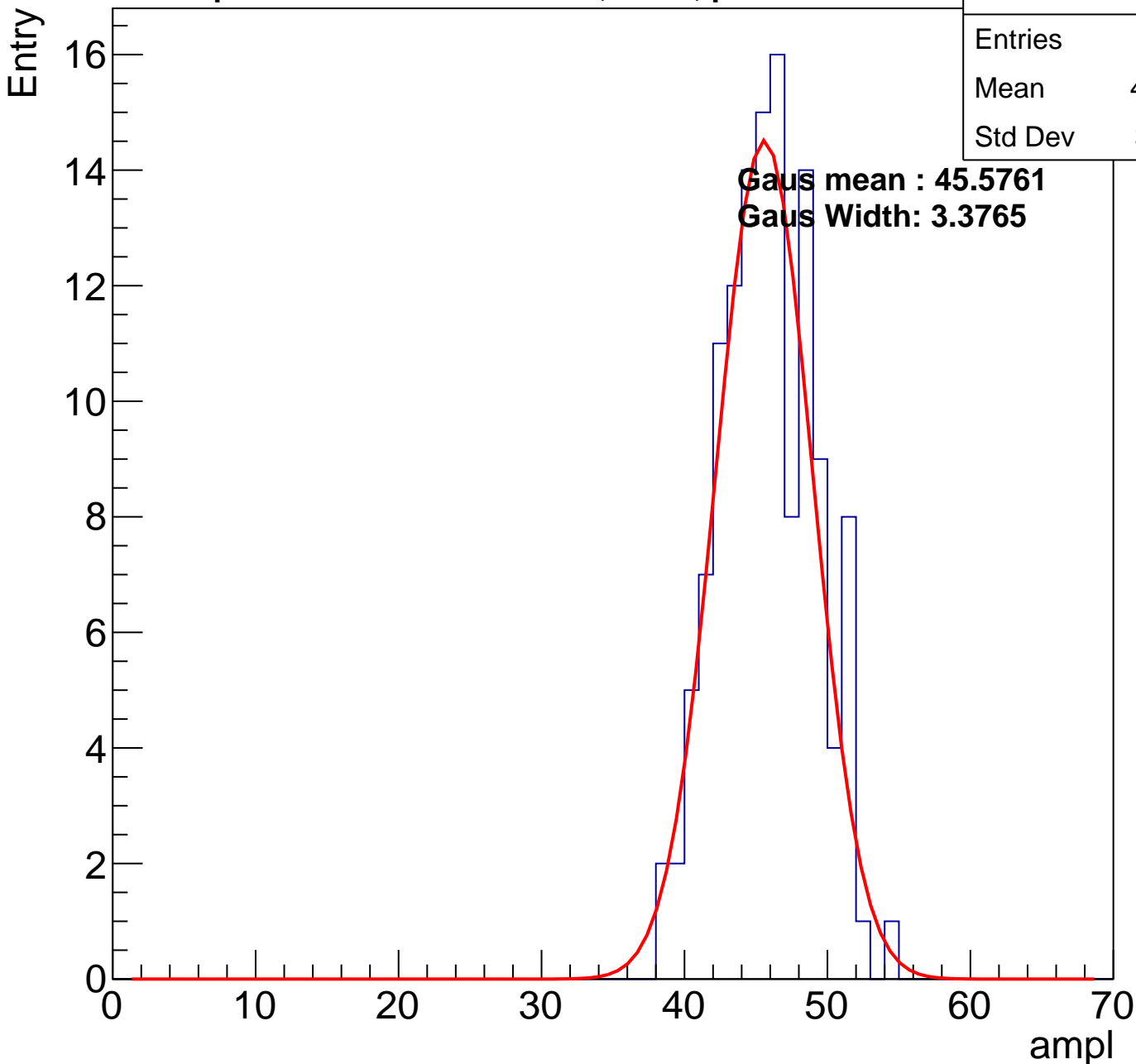


B0L101S, U25-ch15, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	45.34
Std Dev	3.281

Gaus mean : 45.5761
Gaus Width: 3.3765



B0L101S, U25-ch15, adc3

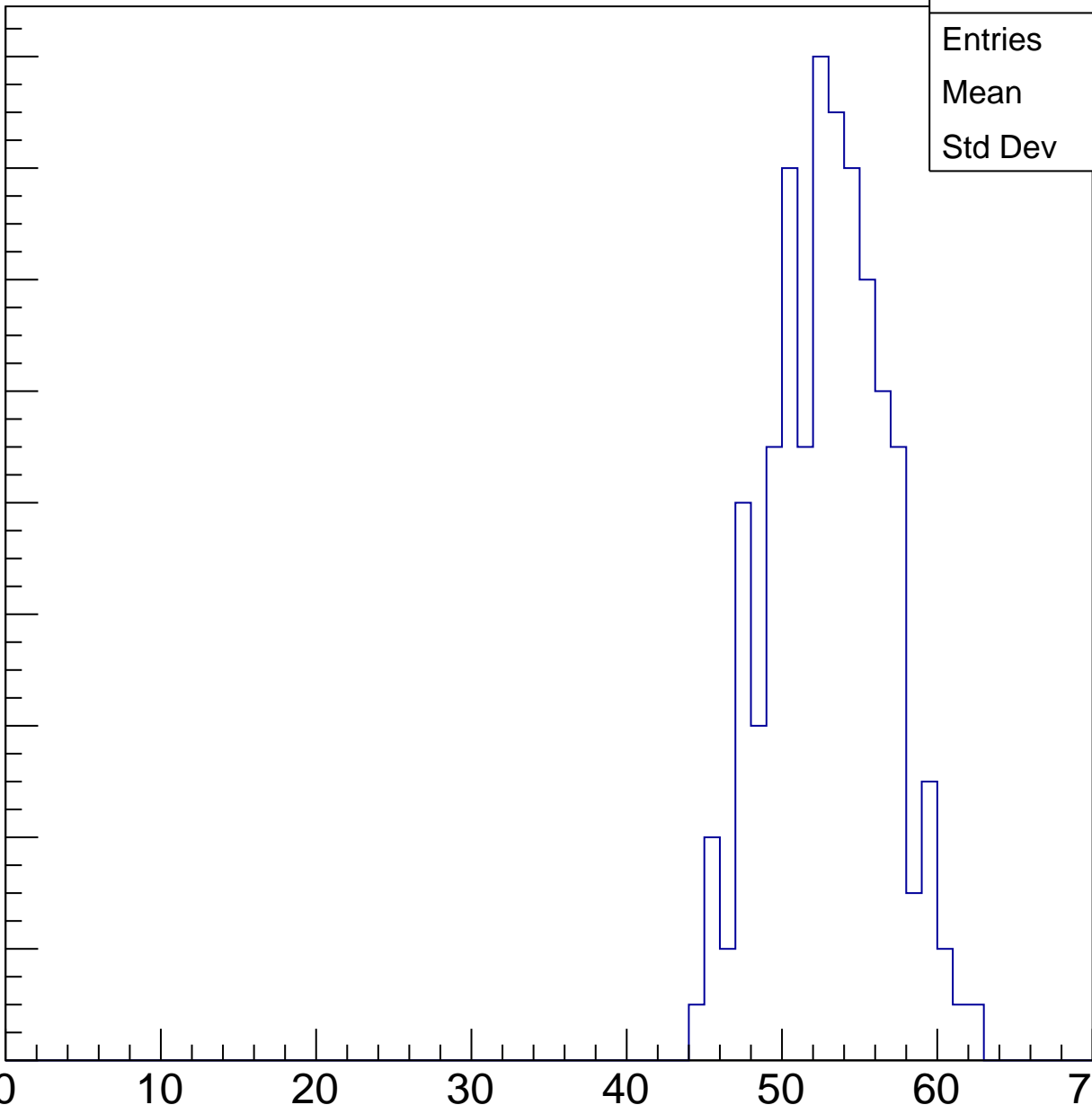
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	161
Mean	52.52
Std Dev	3.649

ampl



B0L101S, U25-ch15, adc4

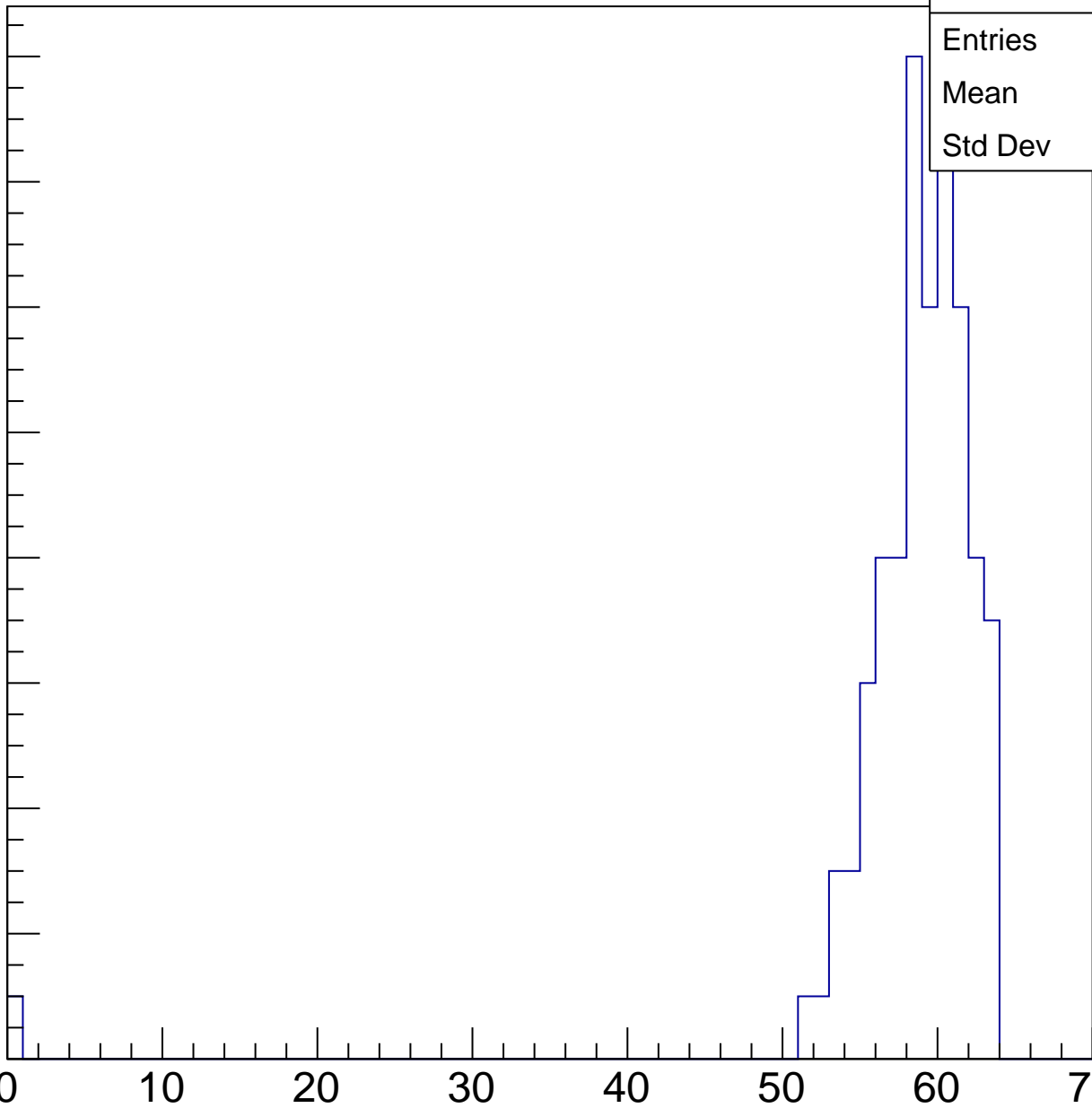
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.07
Std Dev	6.381

ampl



B0L101S, U25-ch15, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	36
Mean	59.92
Std Dev	10.24

ampl

0

10

20

30

40

50

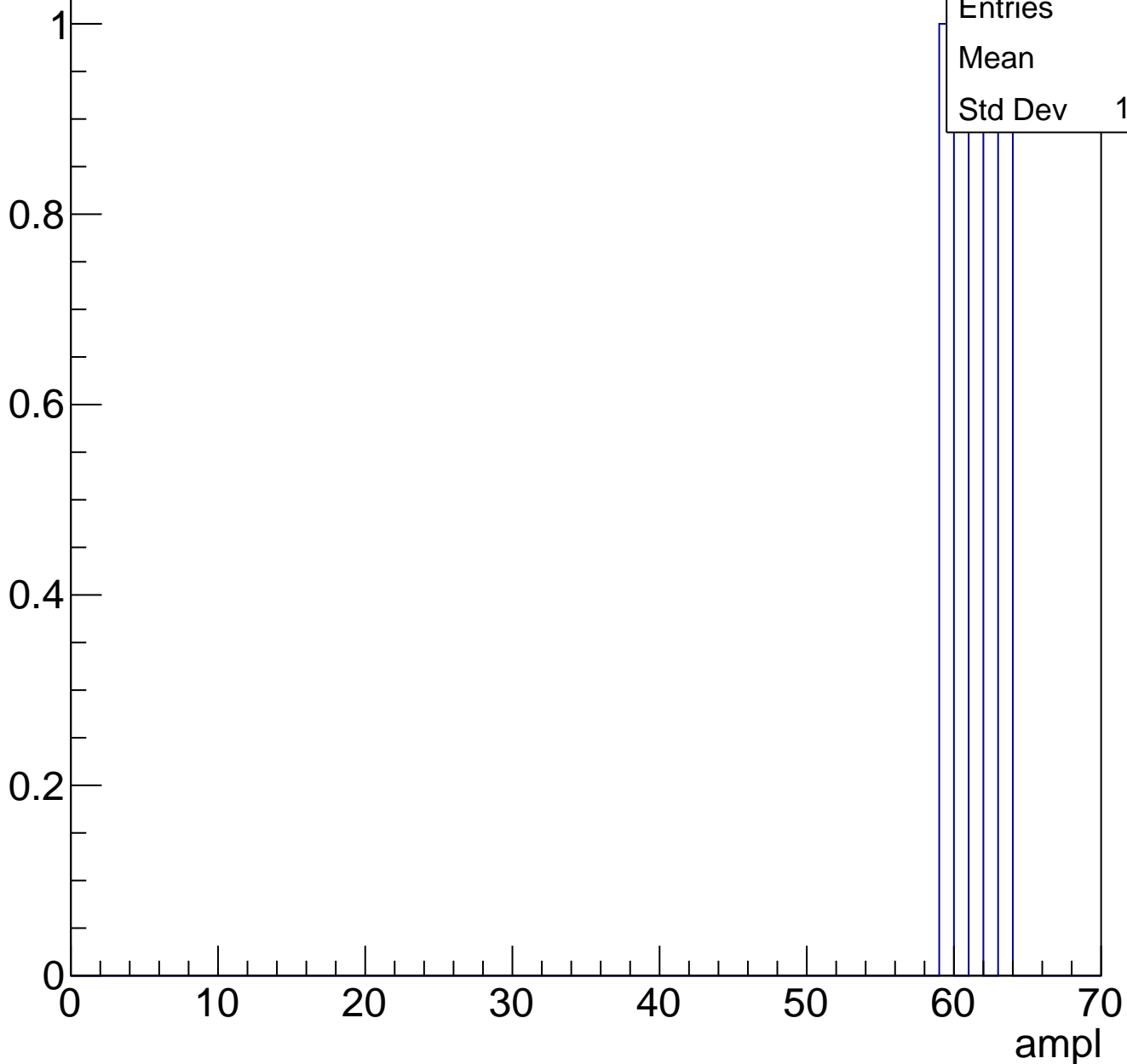
60

70

B0L101S, U25-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch16, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.63
Std Dev	5.926

Gaus mean : 30.9519

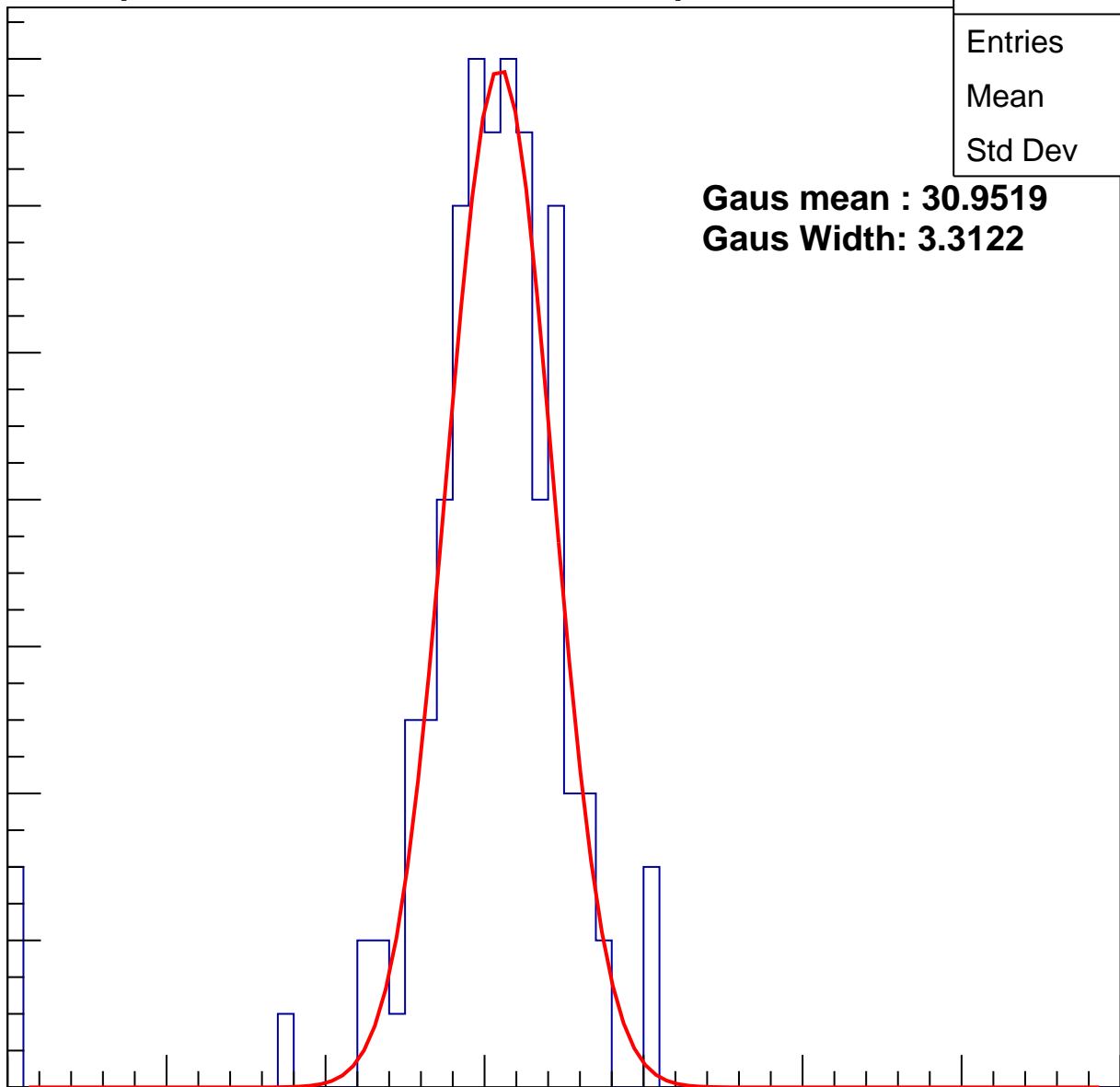
Gaus Width: 3.3122

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



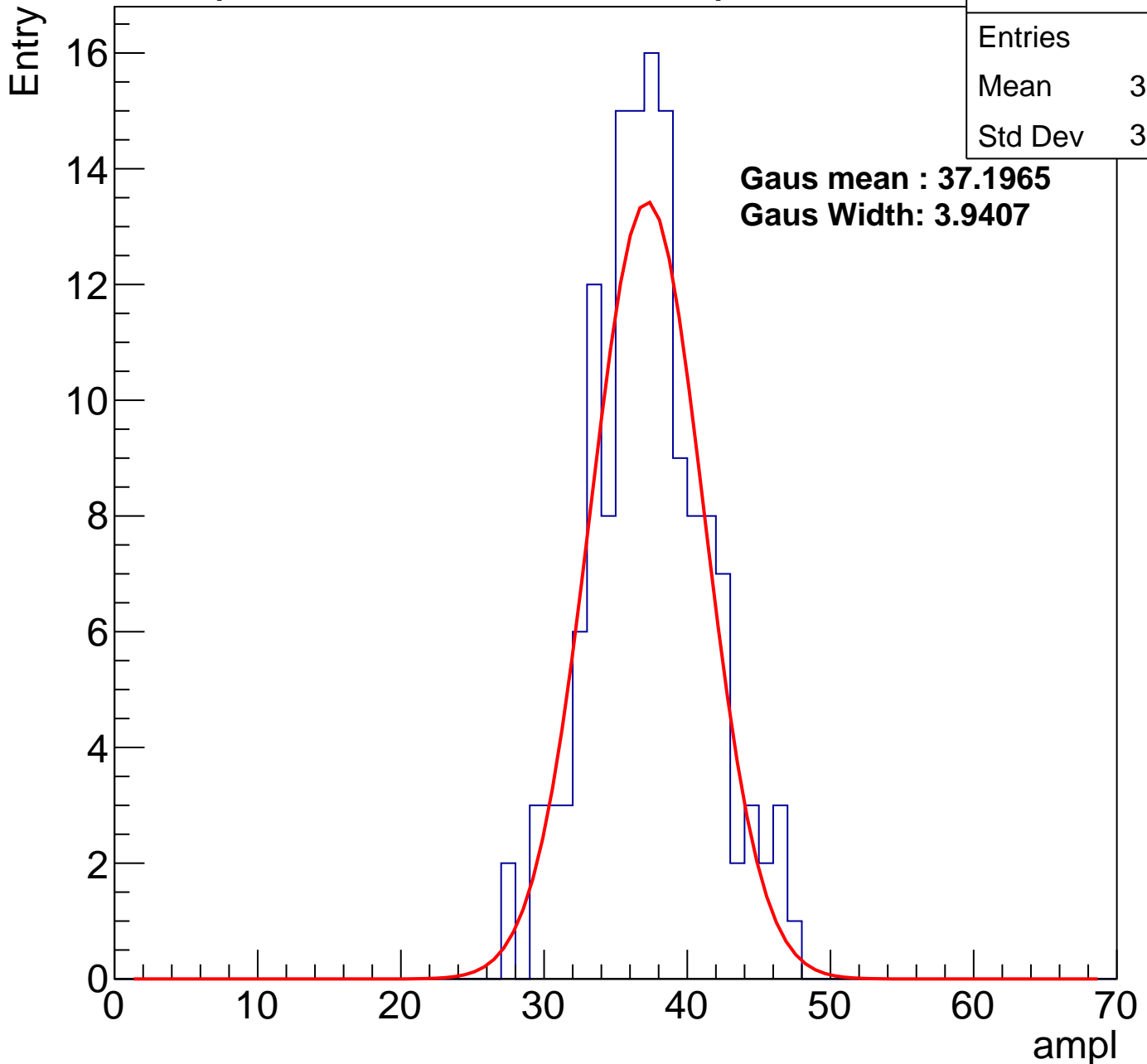
B0L101S, U25-ch16, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.86
Std Dev	3.994

Gaus mean : 37.1965

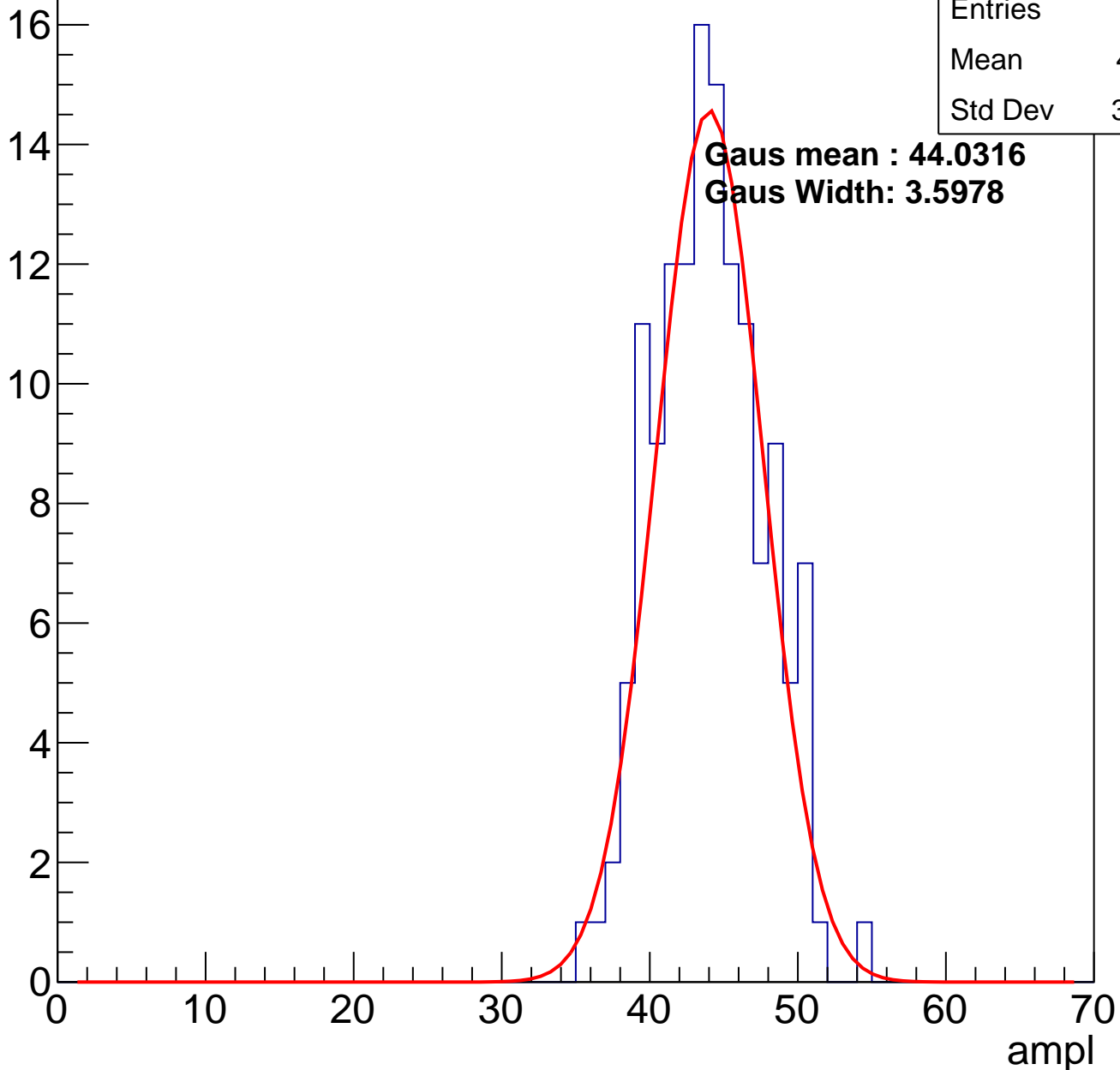
Gaus Width: 3.9407



B0L101S, U25-ch16, adc2

calib_packv5_042523_0143.root, FC#1, port C1

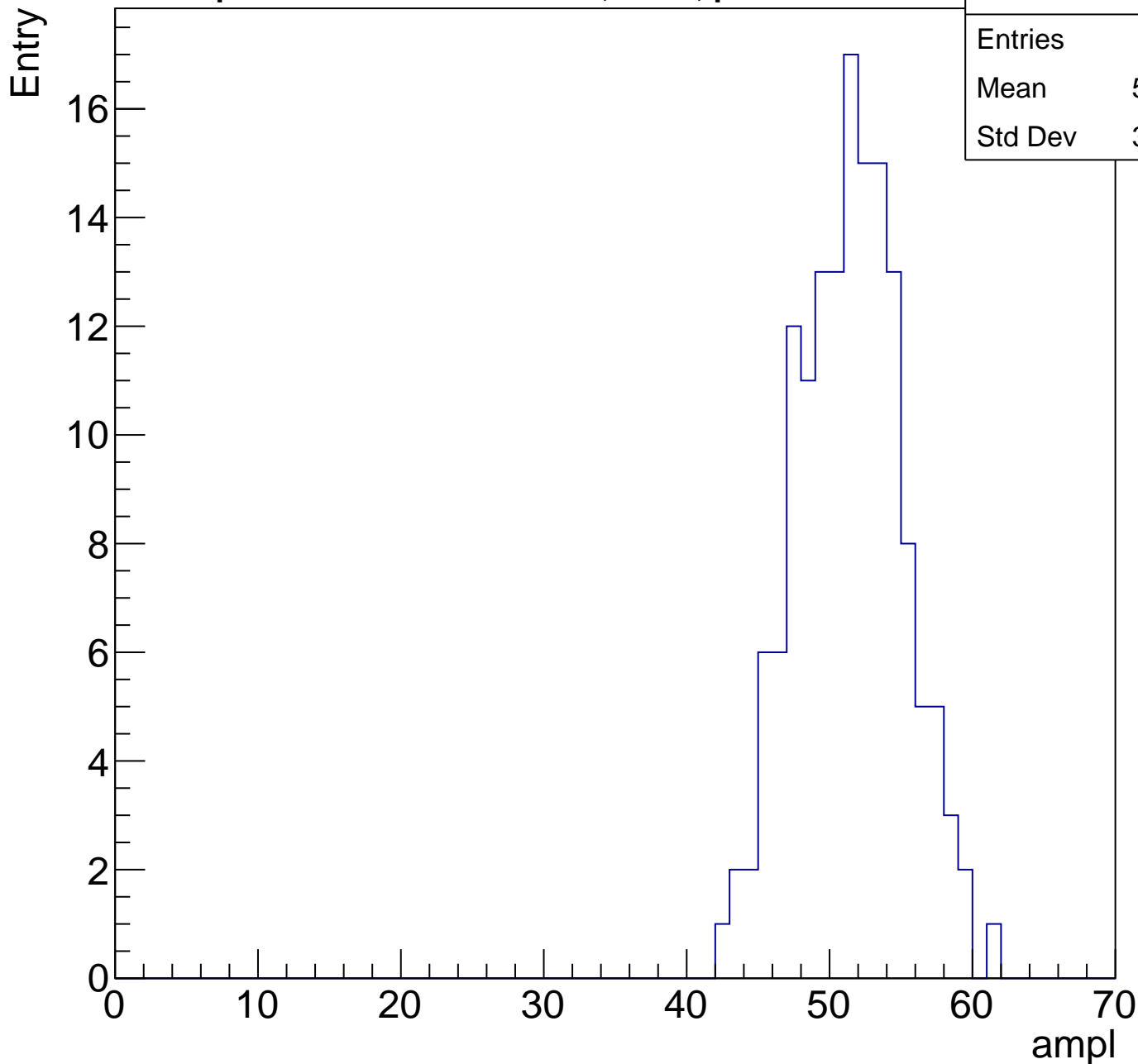
Entry



B0L101S, U25-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	50.95
Std Dev	3.678



B0L101S, U25-ch16, adc4

calib_packv5_042523_0143.root, FC#1, port C1

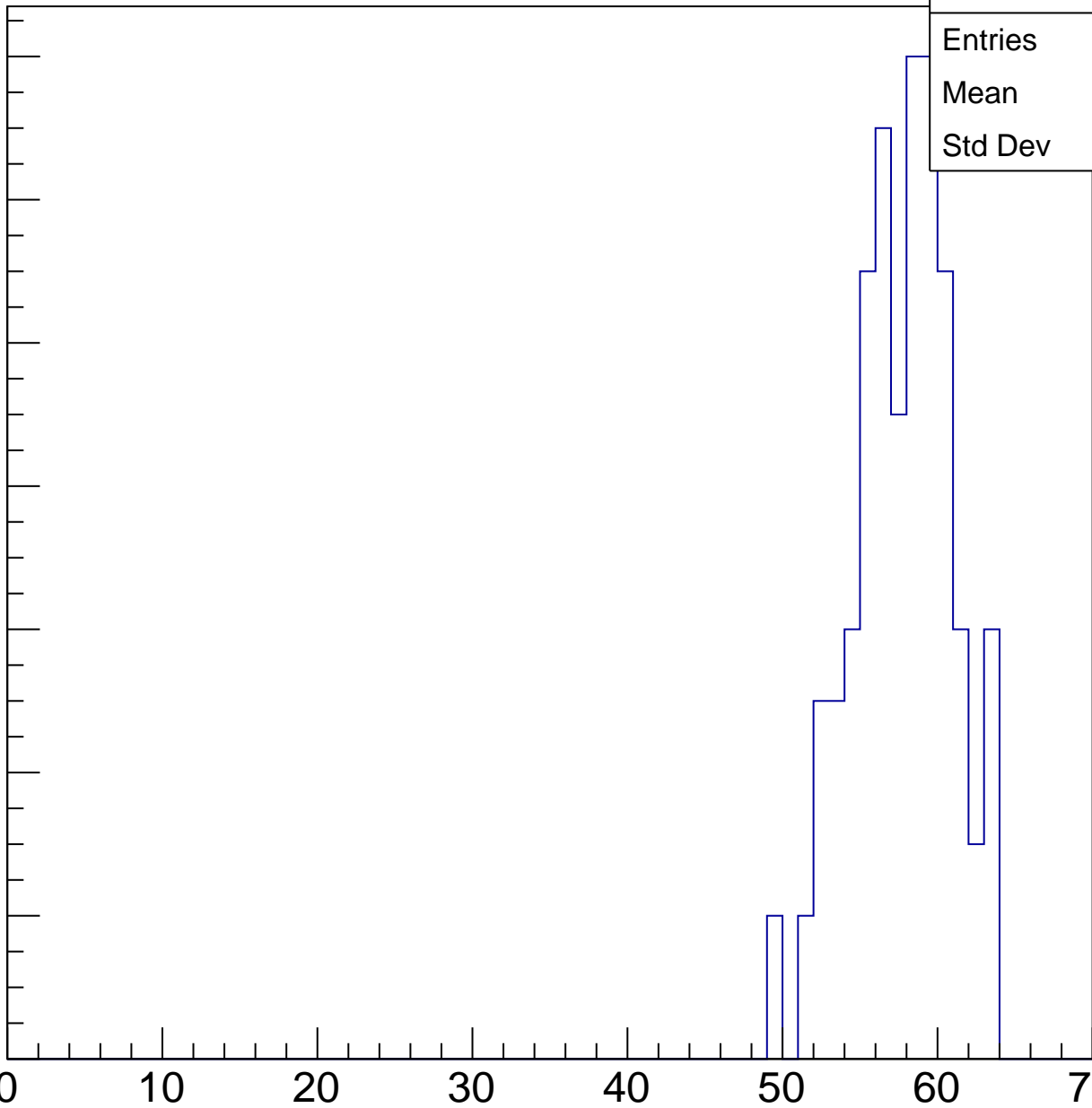
Entries	107
Mean	57.22
Std Dev	3.157

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch16, adc5

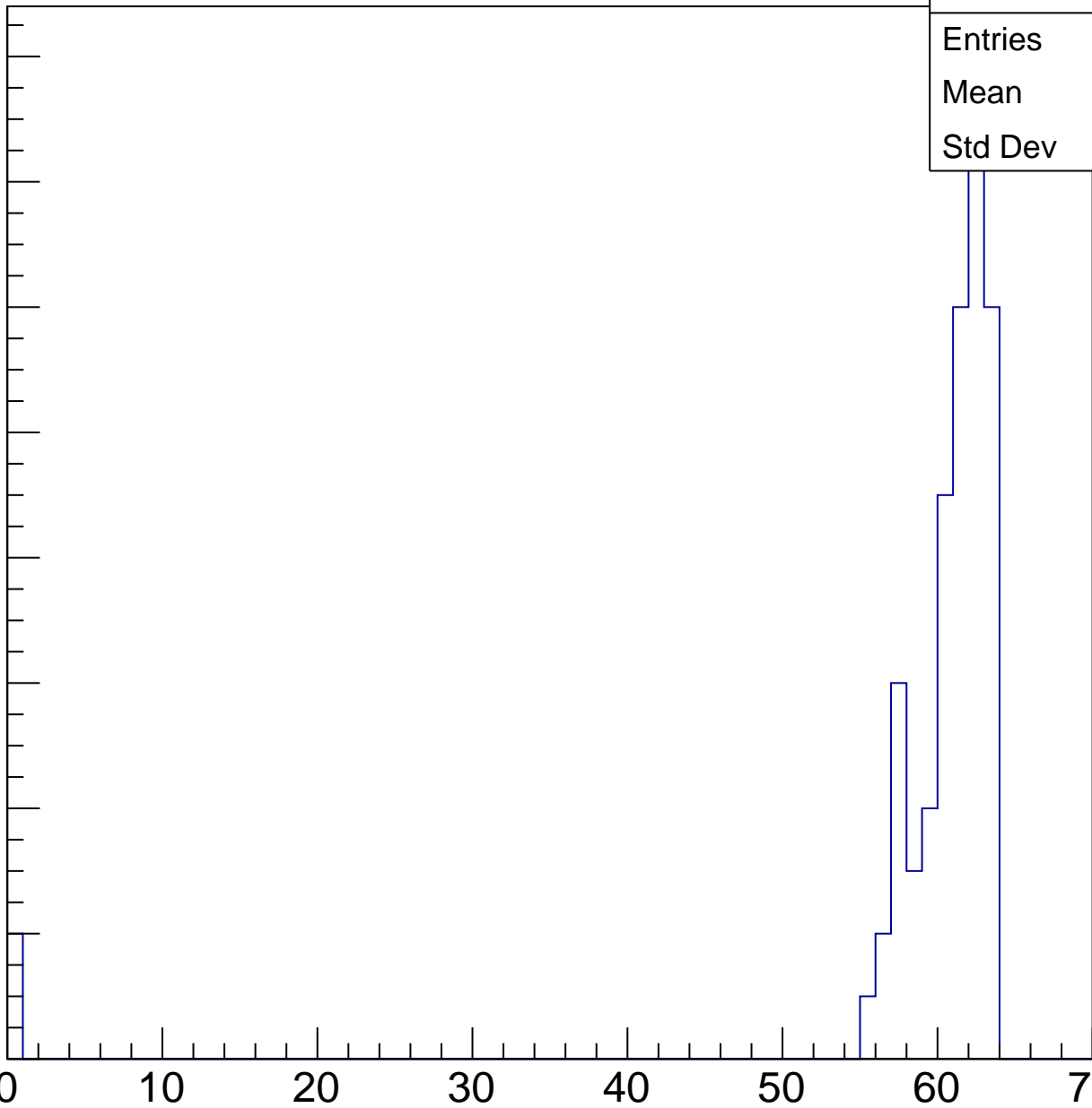
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	67
Mean	58.79
Std Dev	10.52

ampl



B0L101S, U25-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U25-ch17, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	31.94
Std Dev	4.305

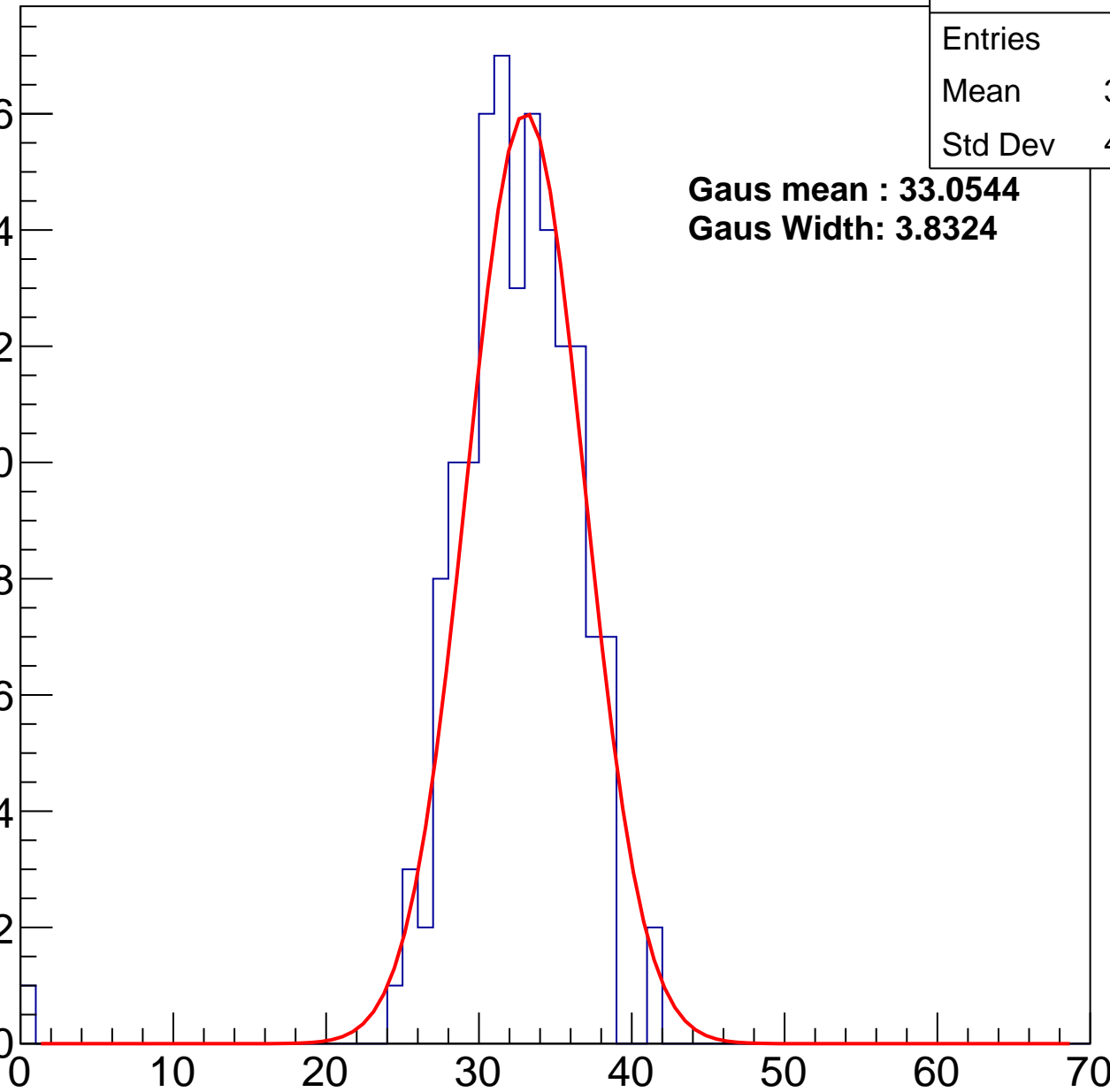
Gaus mean : 33.0544

Gaus Width: 3.8324

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch17, adc1

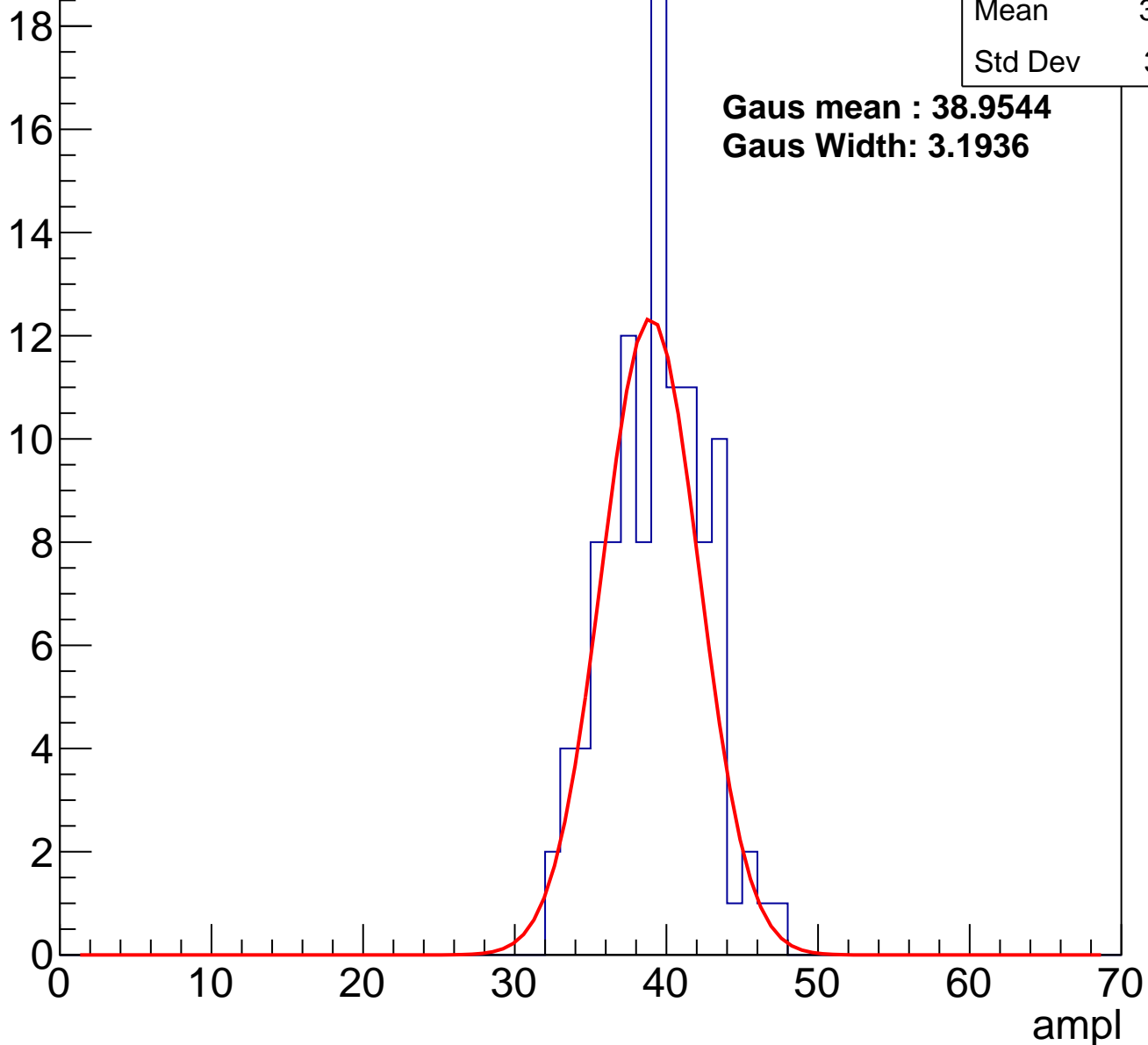
calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	38.85
Std Dev	3.151

Gaus mean : 38.9544

Gaus Width: 3.1936

Entry



B0L101S, U25-ch17, adc2

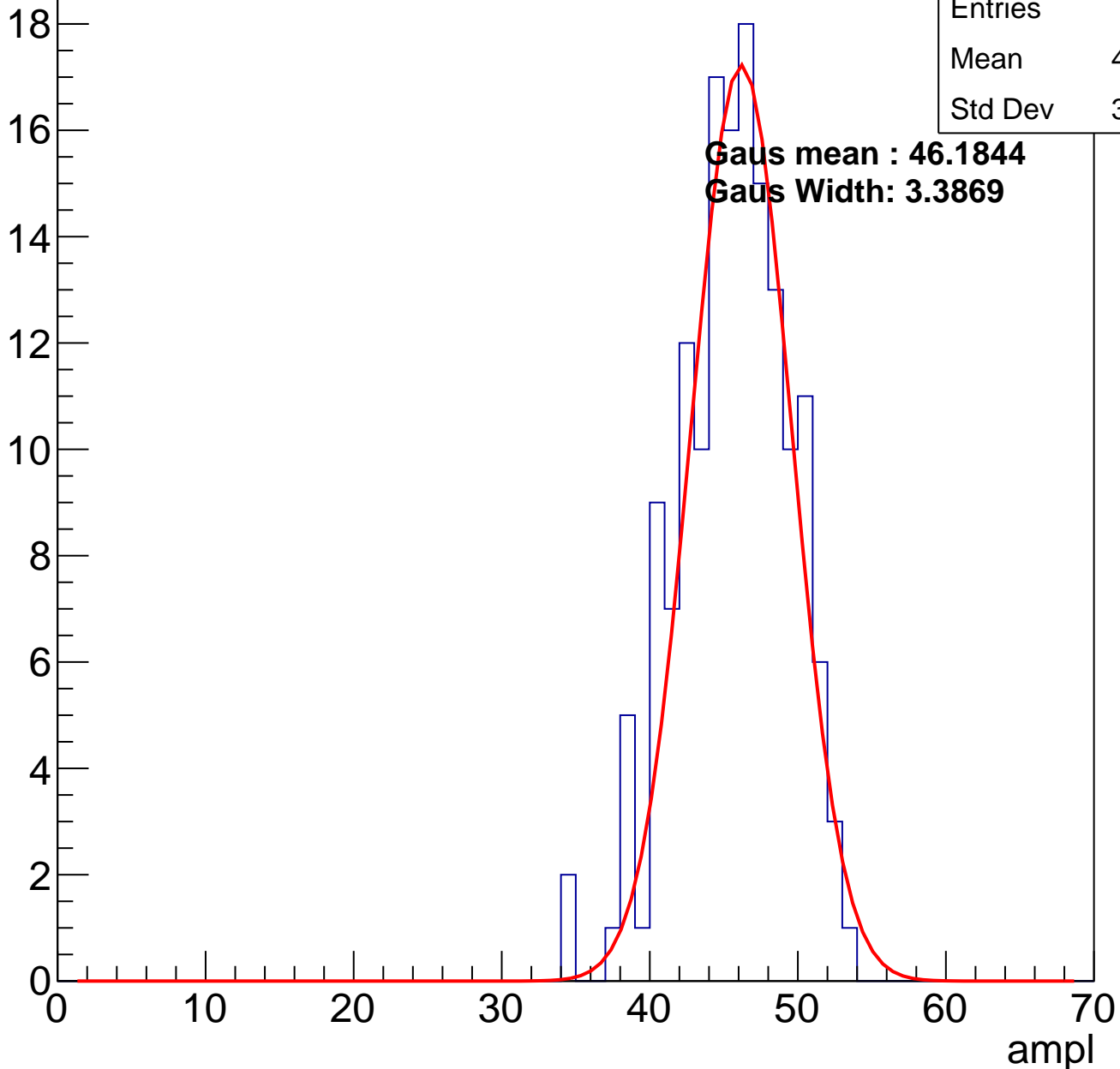
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	45.19
Std Dev	3.692

Gaus mean : 46.1844

Gaus Width: 3.3869

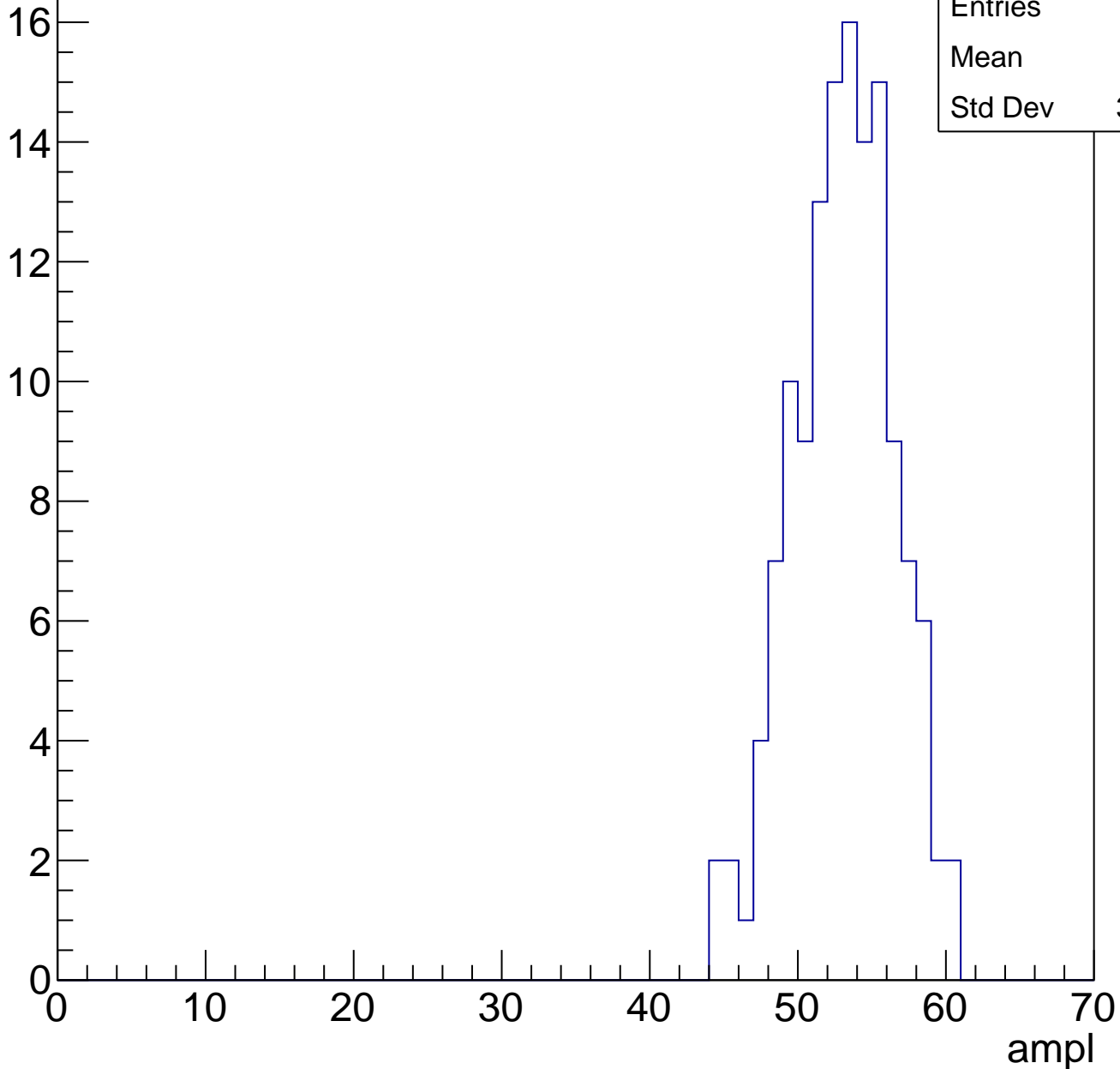
Entry



B0L101S, U25-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	134
Mean	52.6
Std Dev	3.381

B0L101S, U25-ch17, adc4

calib_packv5_042523_0143.root, FC#1, port C1

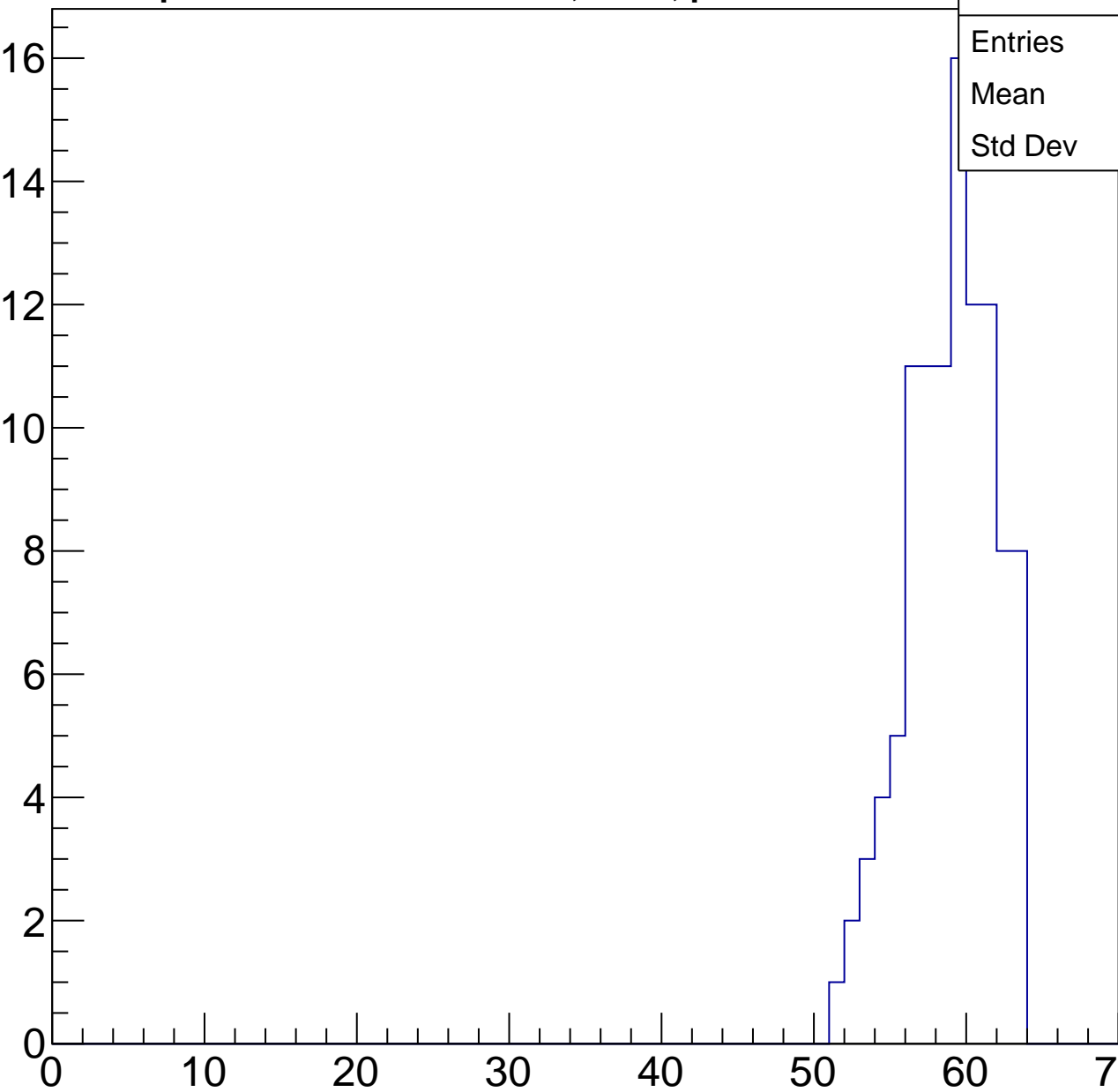
Entries	104
Mean	58.48
Std Dev	2.835

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

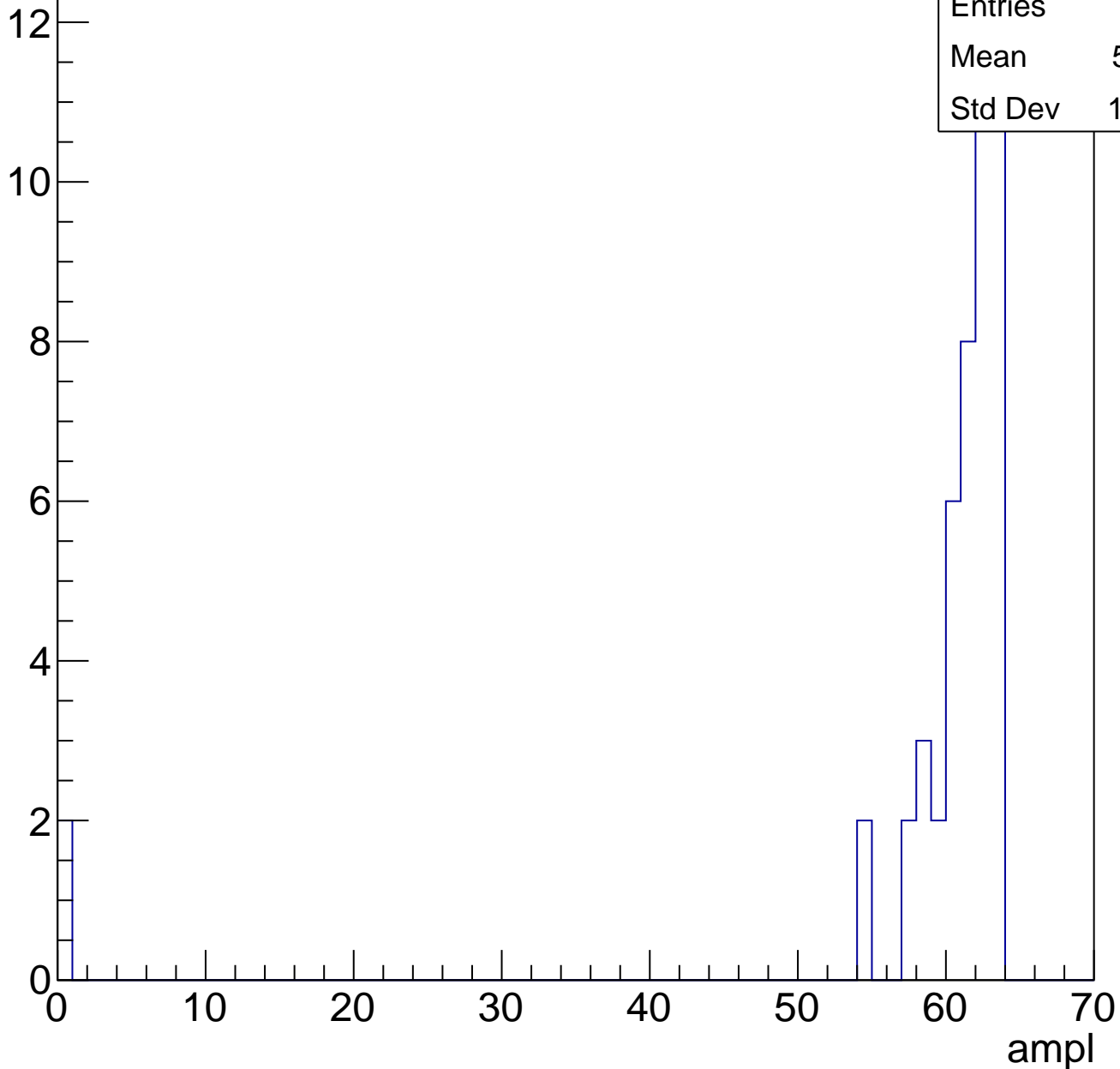


B0L101S, U25-ch17, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	49
Mean	58.41
Std Dev	12.24

Entry



B0L101S, U25-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	29.79
Std Dev	4.138

Gaus mean : 30.3105

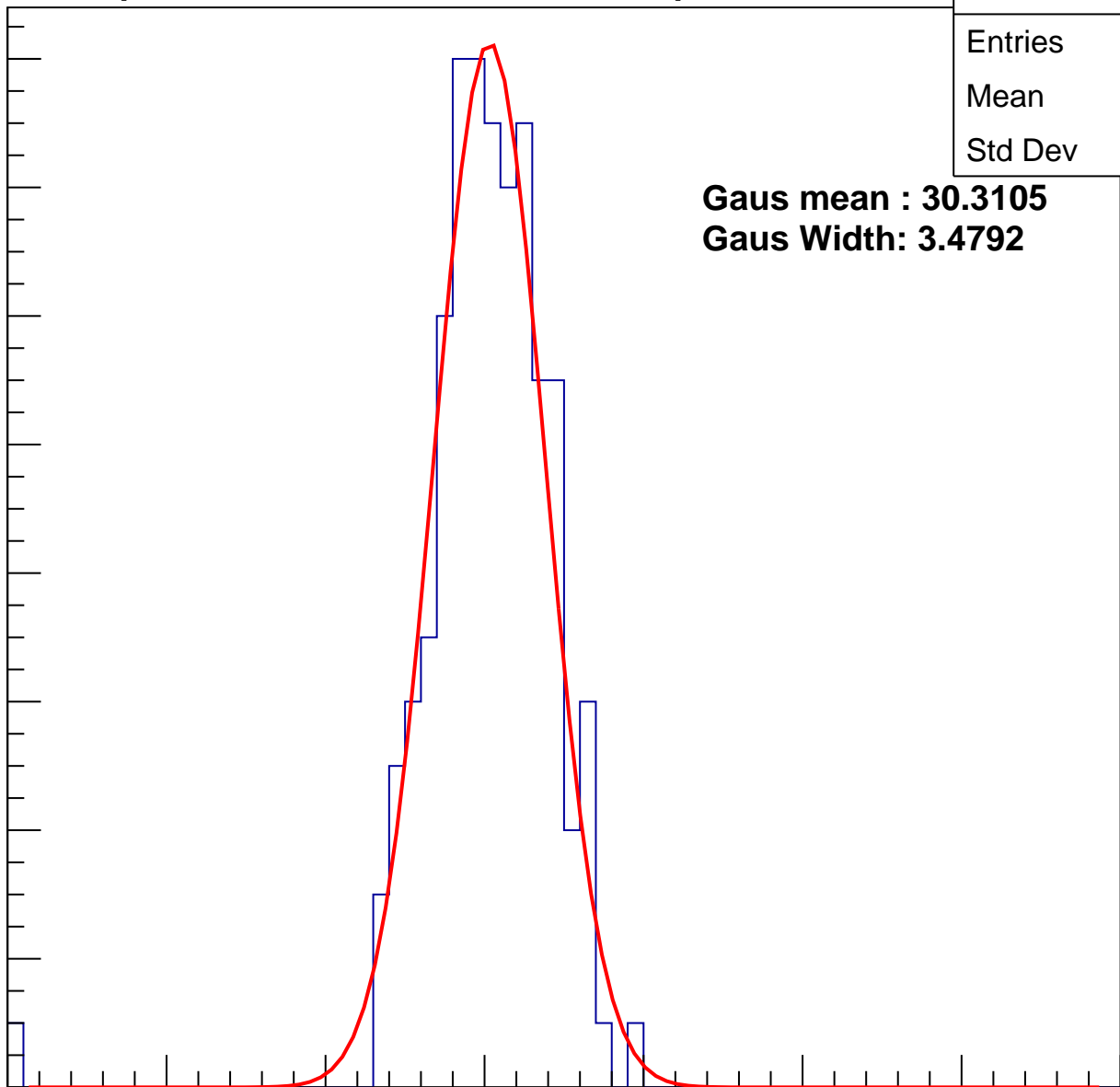
Gaus Width: 3.4792

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch18, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	37.12
Std Dev	3.382

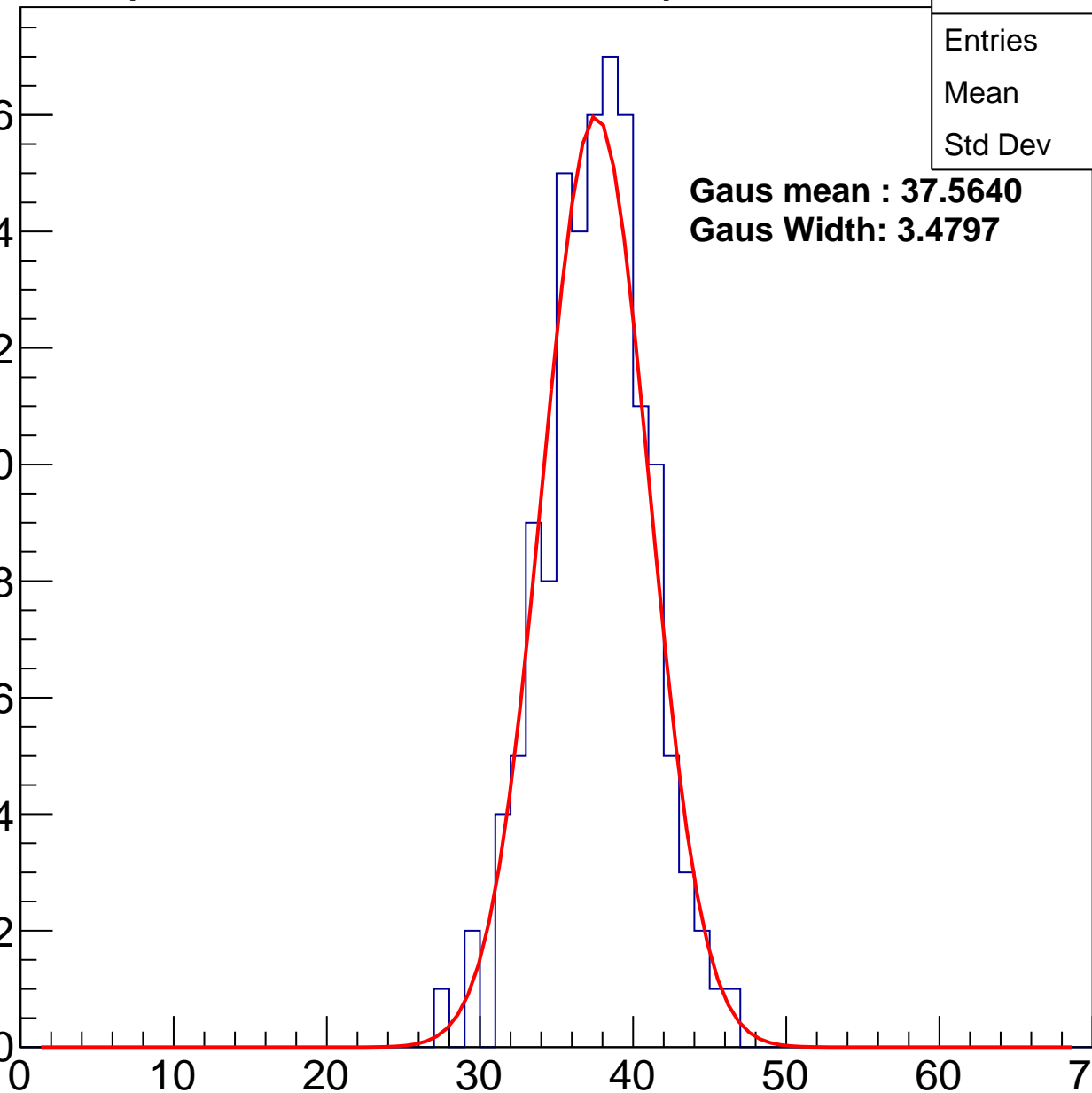
Gaus mean : 37.5640

Gaus Width: 3.4797

Entry

16
14
12
10
8
6
4
2
0

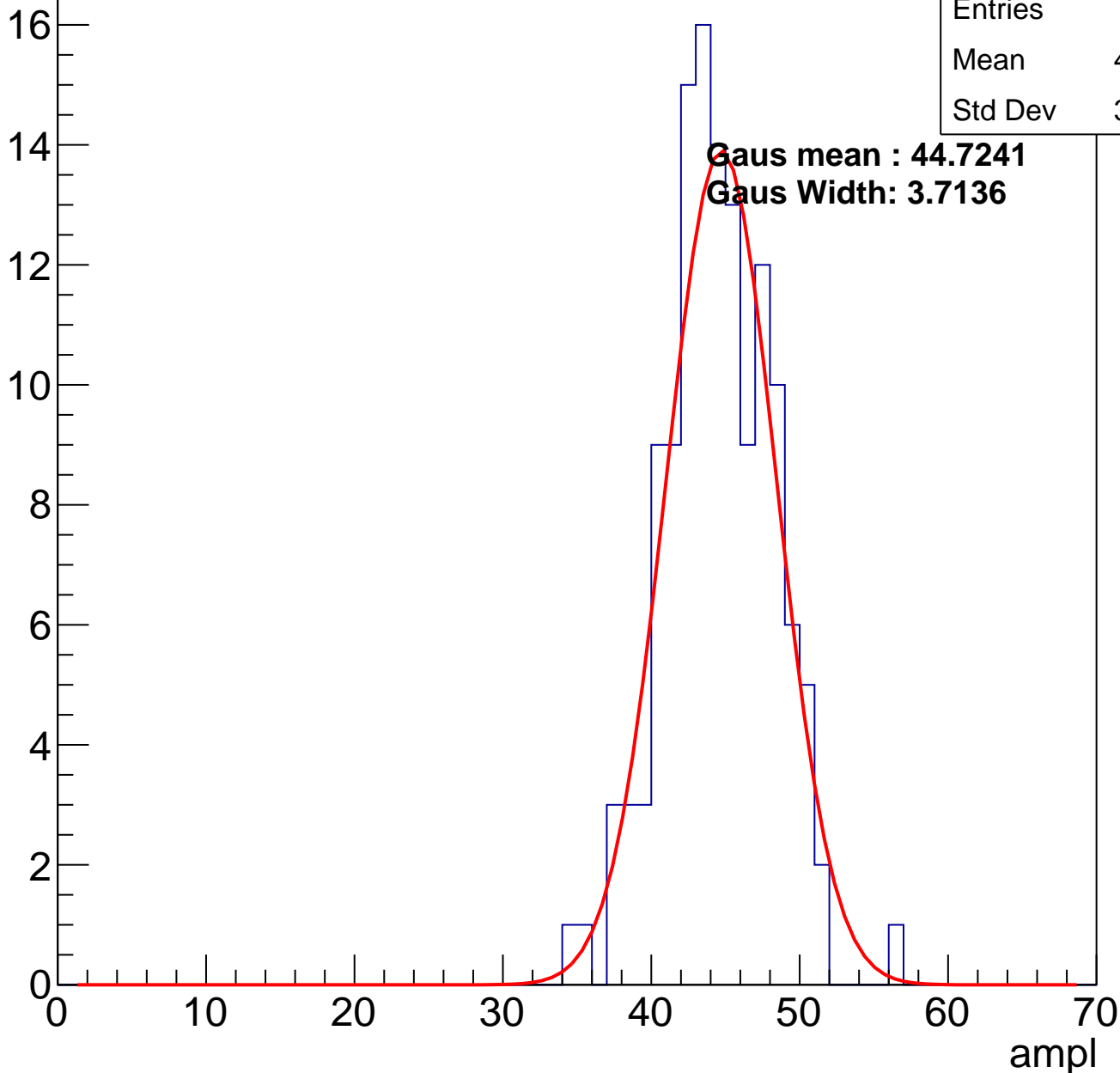
ampl



B0L101S, U25-ch18, adc2

calib_packv5_042523_0143.root, FC#1, port C1

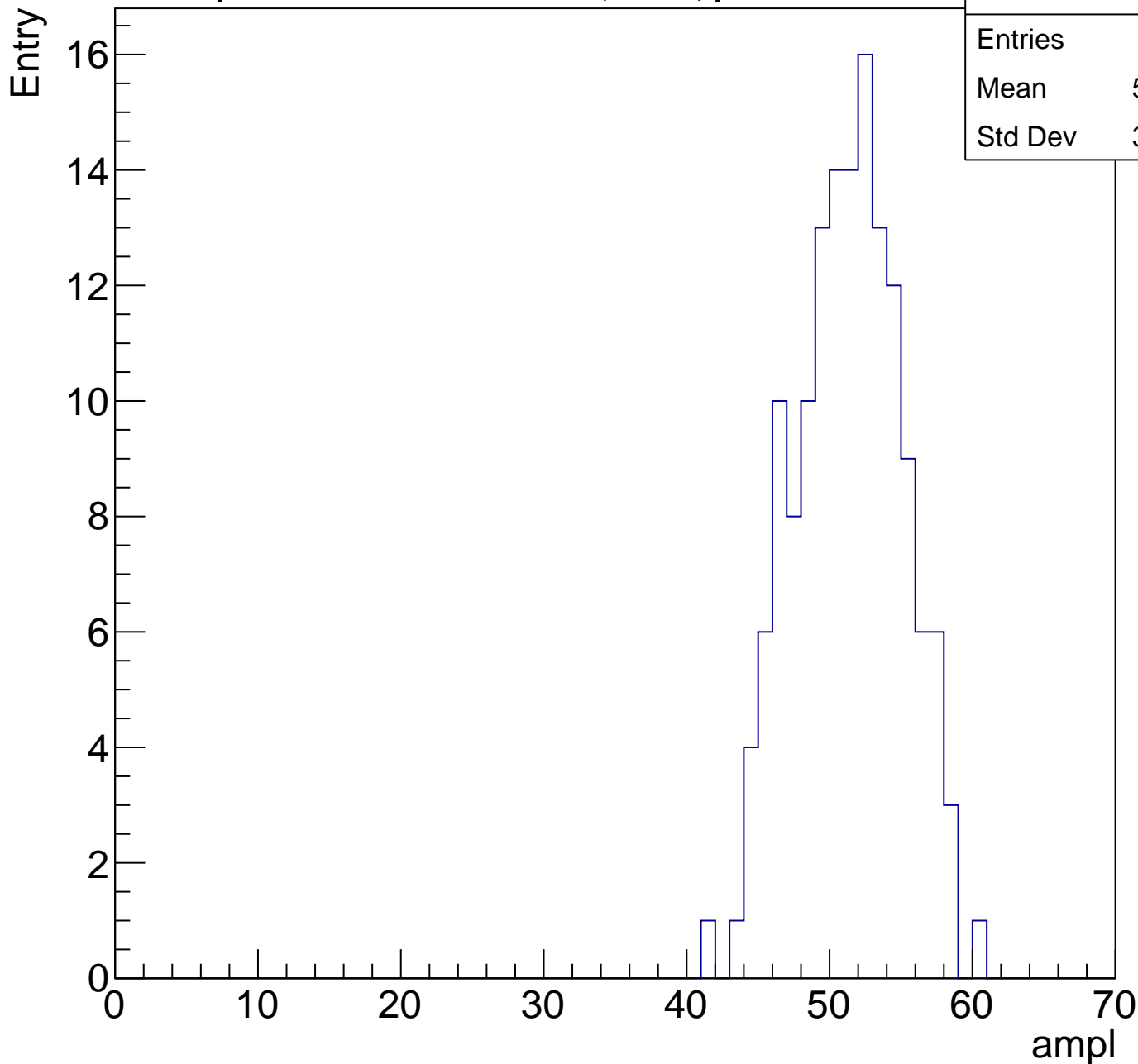
Entry



B0L101S, U25-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	50.84
Std Dev	3.707



B0L101S, U25-ch18, adc4

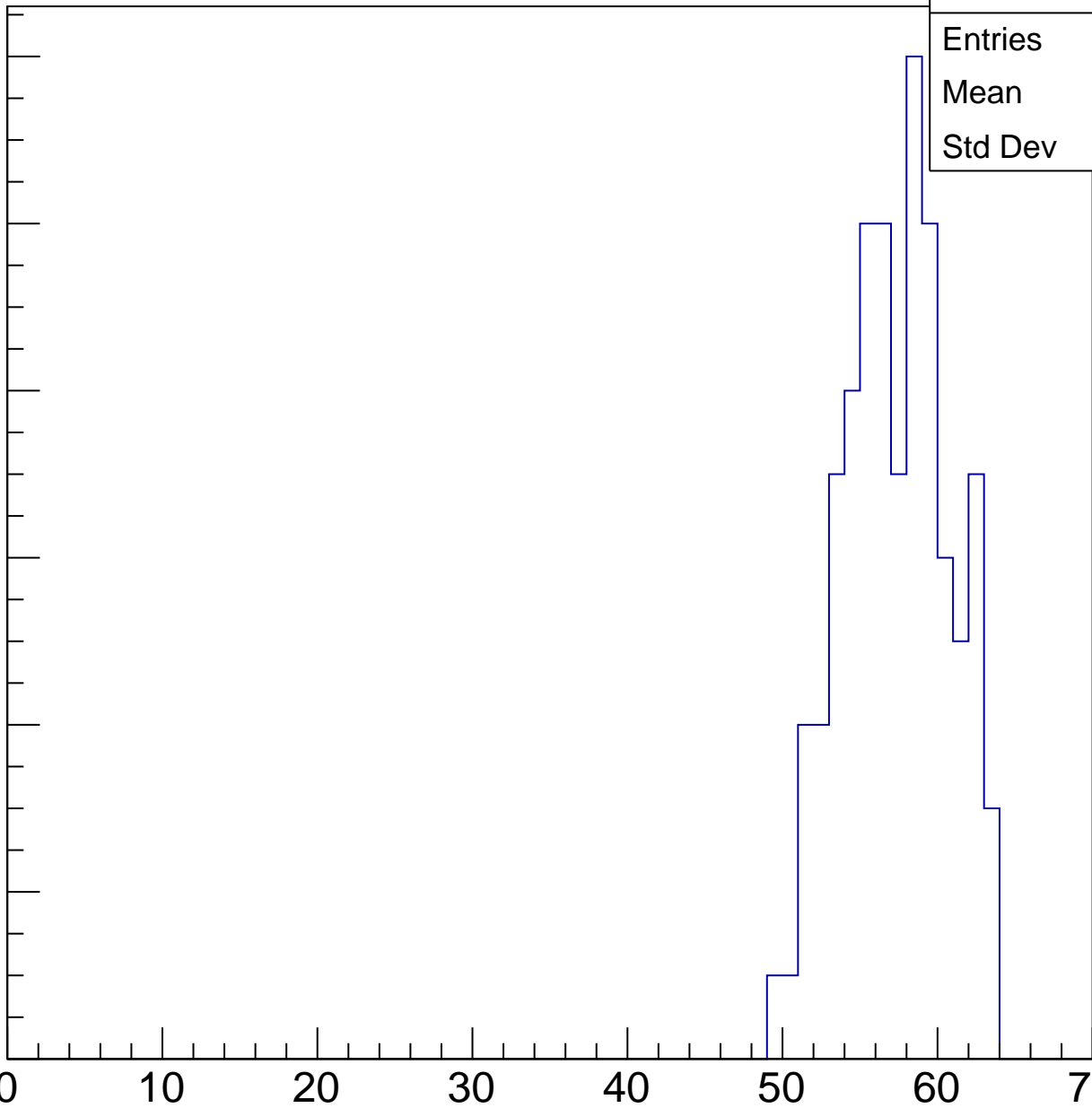
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	95
Mean	56.81
Std Dev	3.335

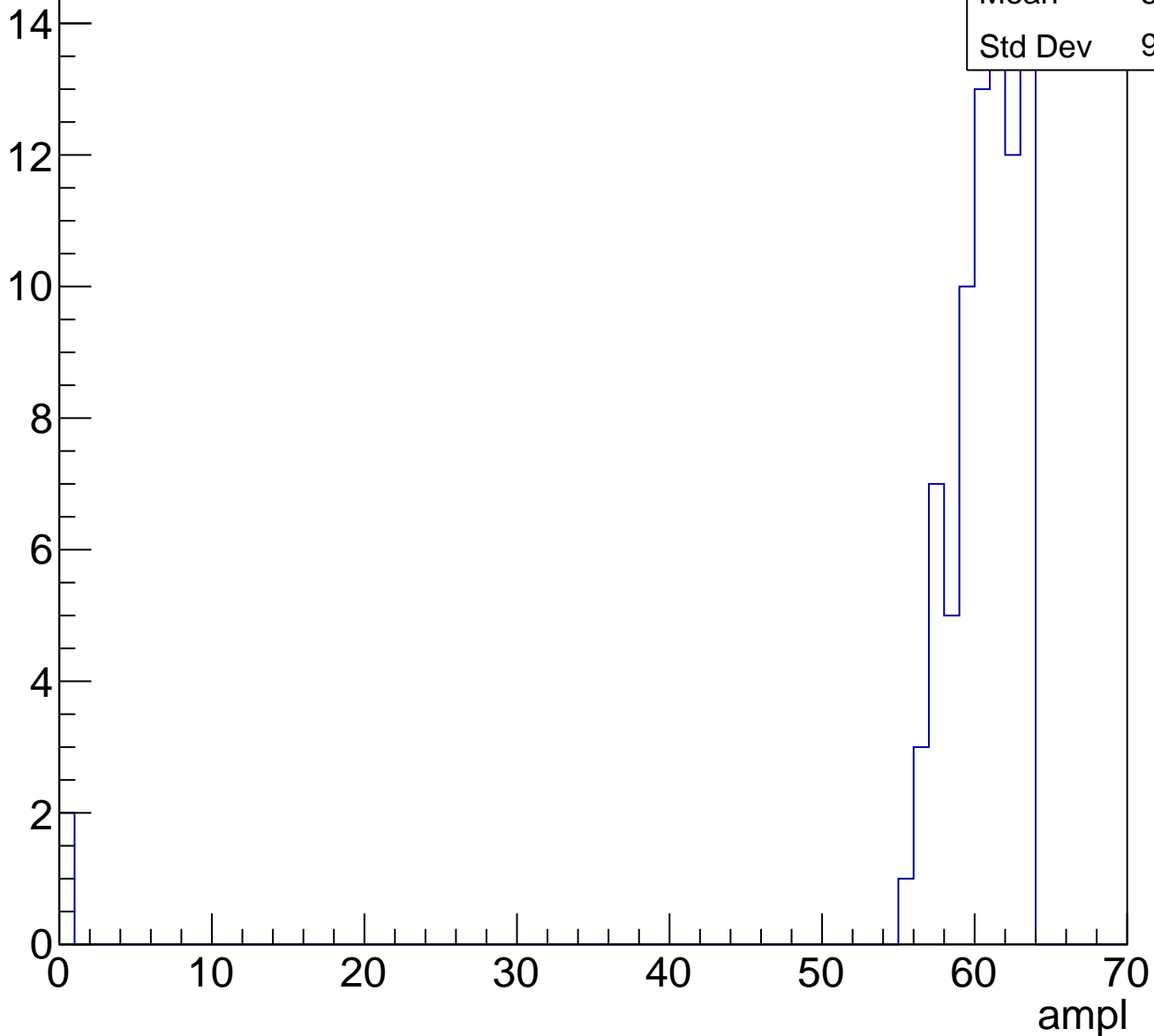
ampl



B0L101S, U25-ch18, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

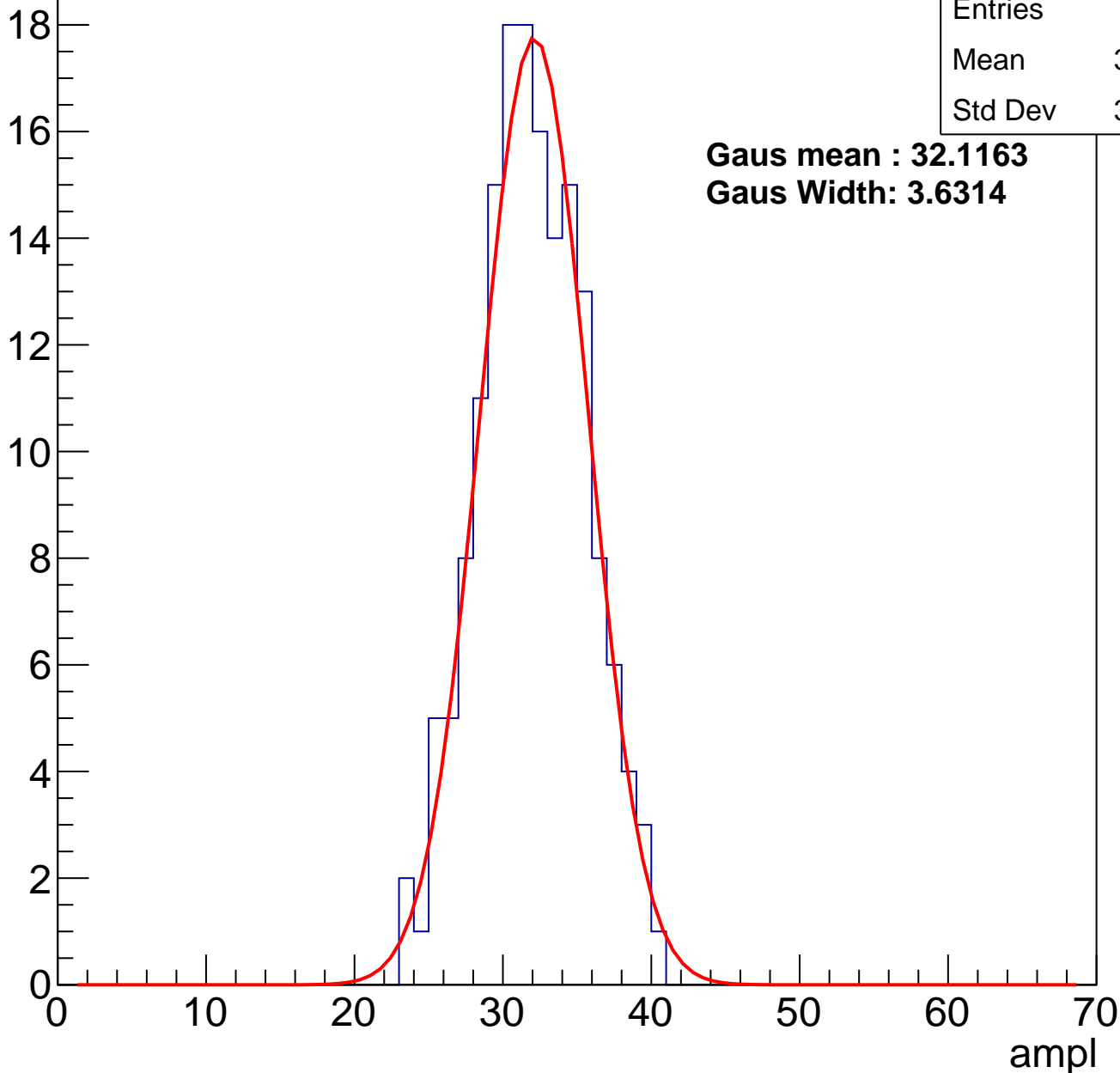
Entry



B0L101S, U25-ch19, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch19, adc1

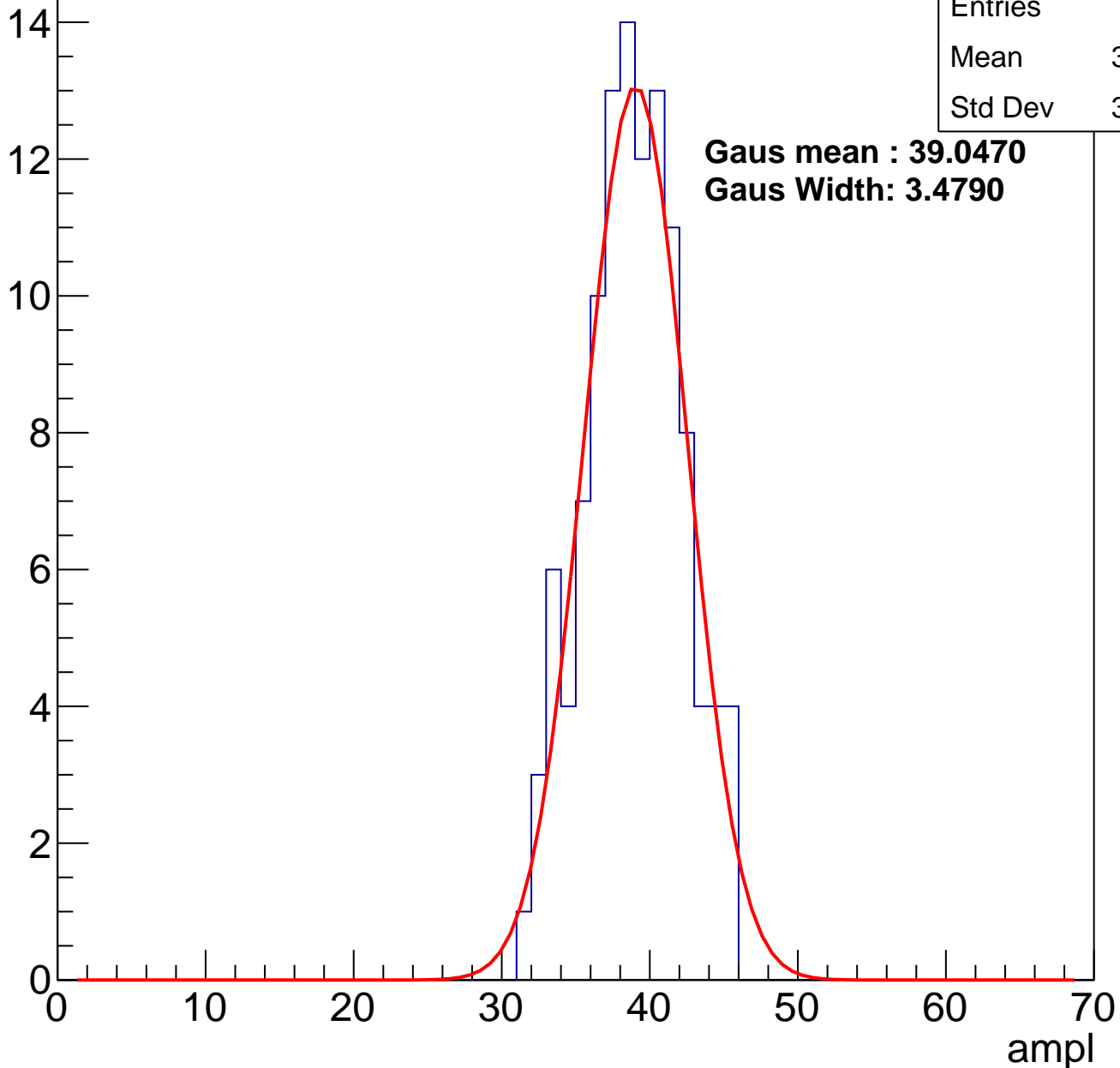
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	38.44
Std Dev	3.239

Gaus mean : 39.0470

Gaus Width: 3.4790

Entry



B0L101S, U25-ch19, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

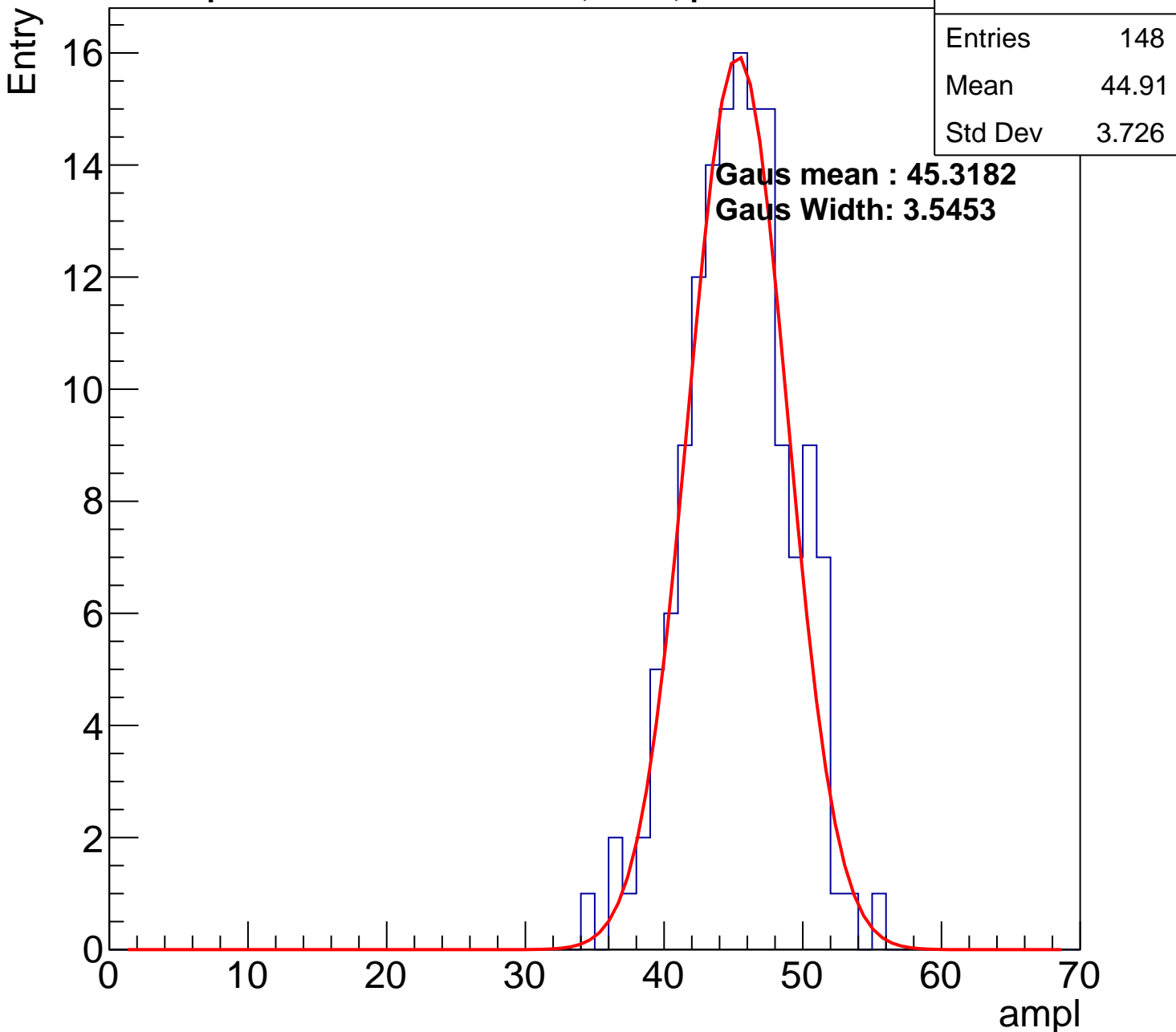
16
14
12
10
8
6
4
2
0

Entries	148
Mean	44.91
Std Dev	3.726

Gaus mean : 45.3182
Gaus Width: 3.5453

ampl

0 10 20 30 40 50 60 70

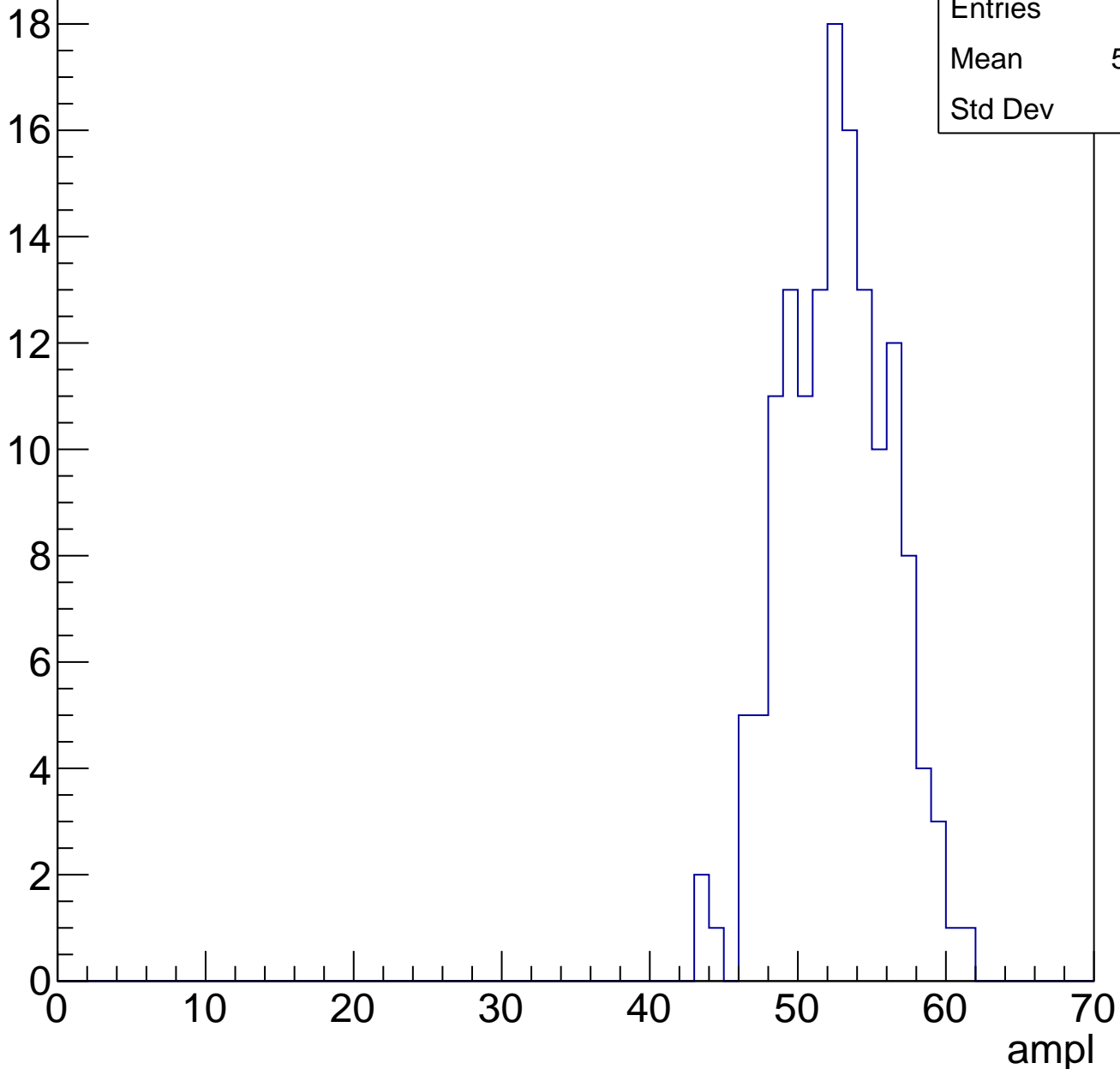


B0L101S, U25-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	52.16
Std Dev	3.56

Entry



B0L101S, U25-ch19, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

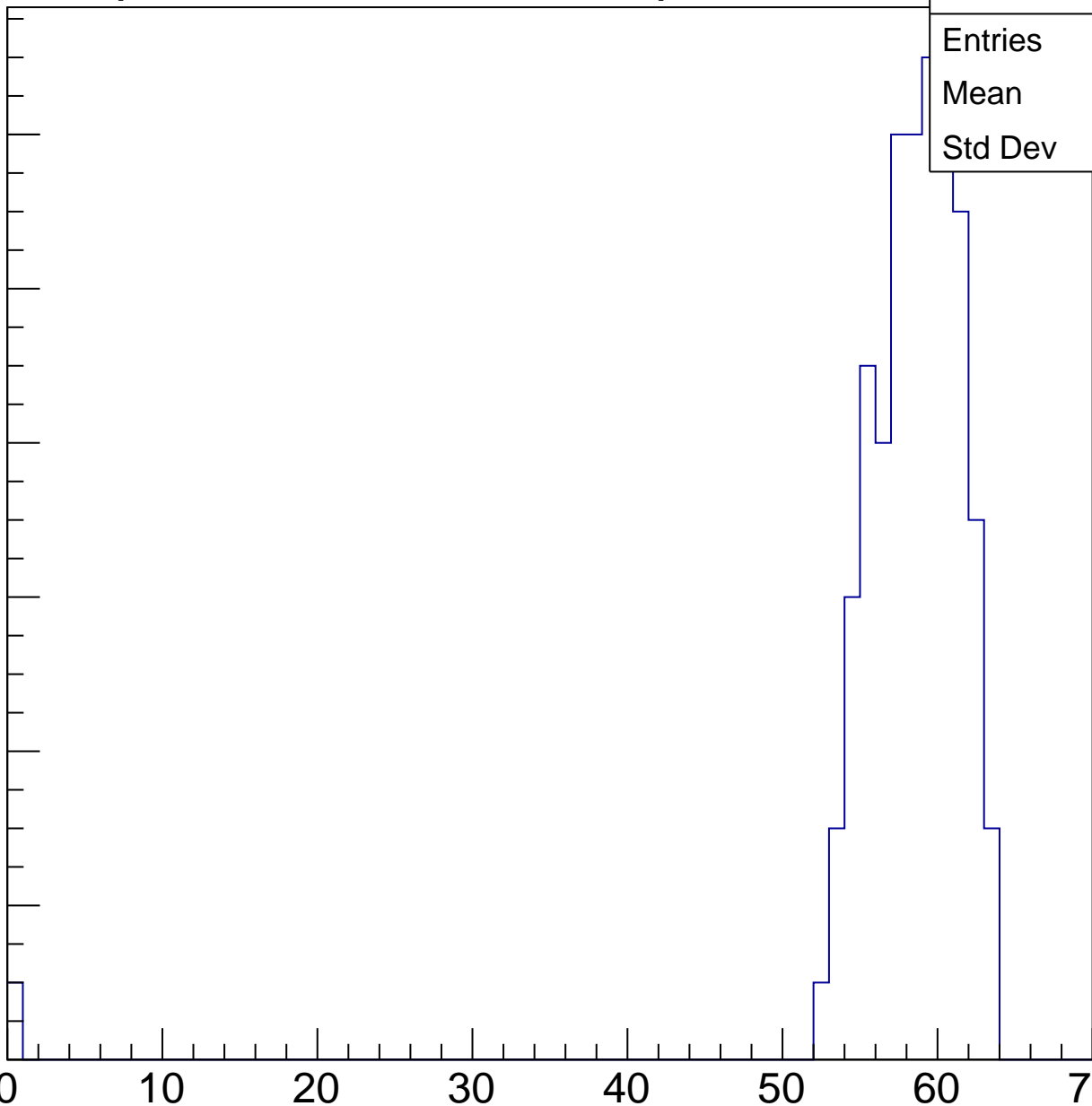
0

Entries	99
---------	----

Mean	57.57
------	-------

Std Dev	6.379
---------	-------

ampl



B0L101S, U25-ch19, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	53
Mean	60.08
Std Dev	8.534

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

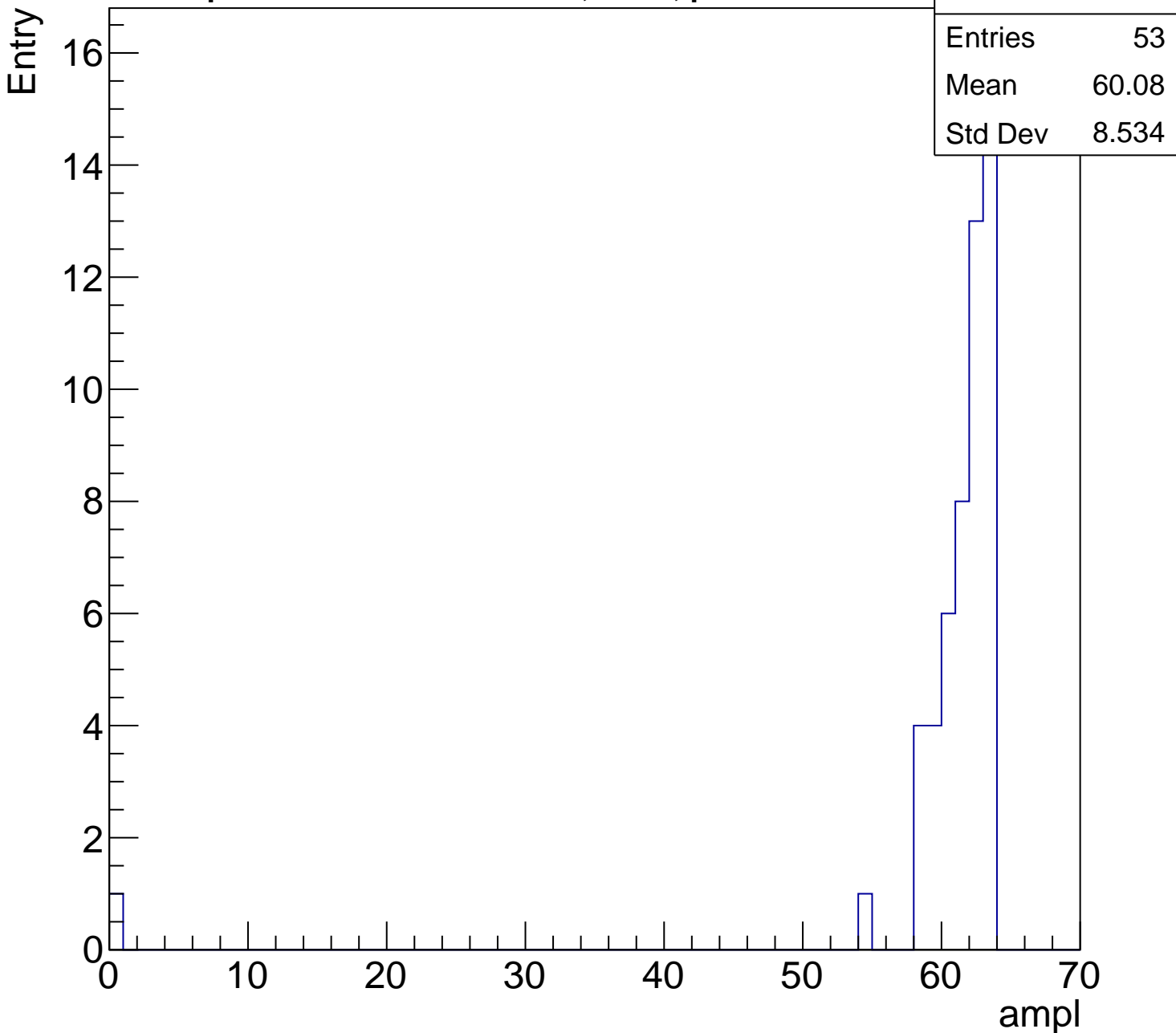
40

50

60

70

ampl



B0L101S, U25-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

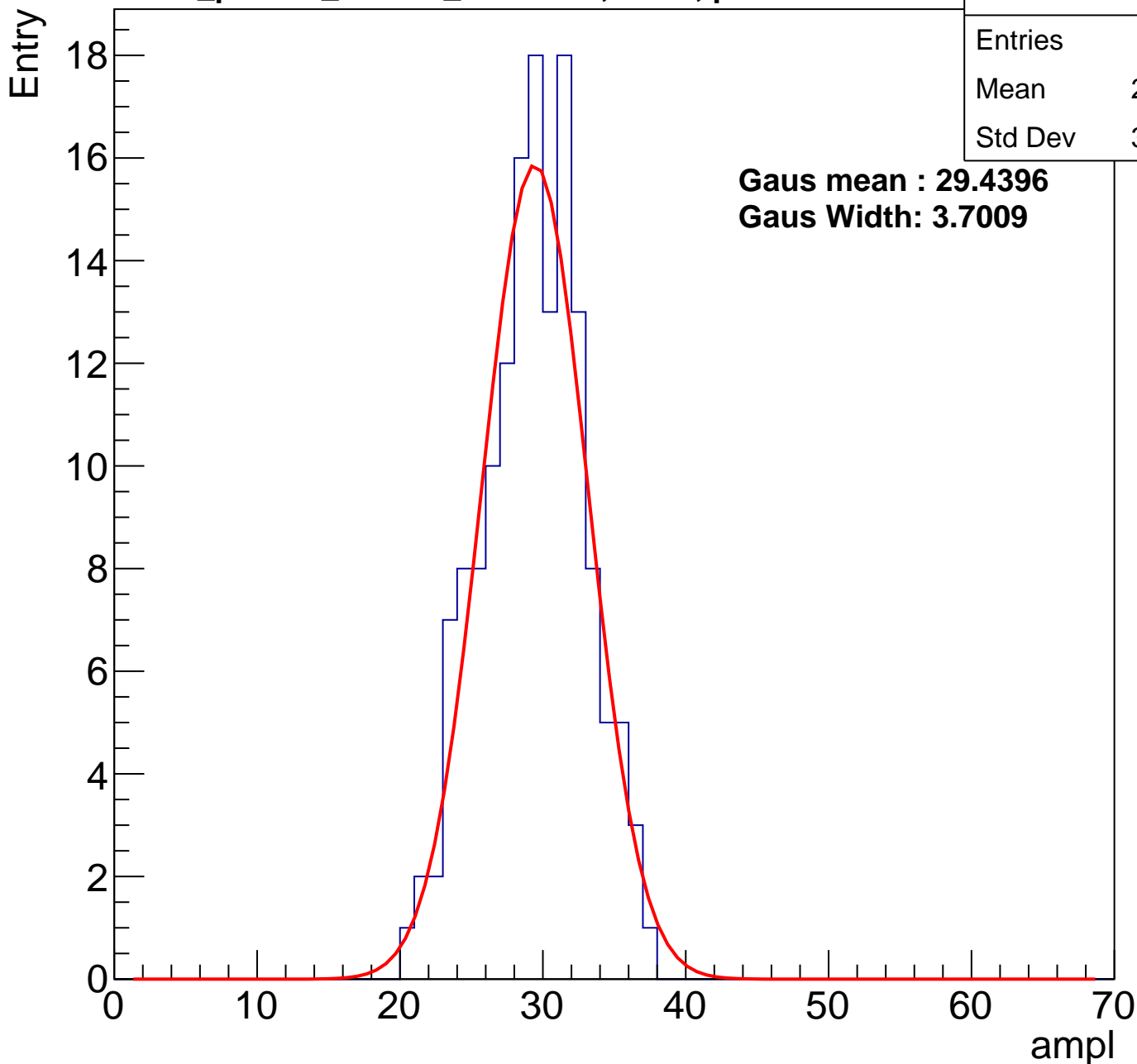
B0L101S, U25-ch20, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	28.87
Std Dev	3.545

Gaus mean : 29.4396

Gaus Width: 3.7009



B0L101S, U25-ch20, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

147

Mean

36.52

Std Dev

3.548

Gaus mean : 37.1331

Gaus Width: 3.3827

0

10

20

30

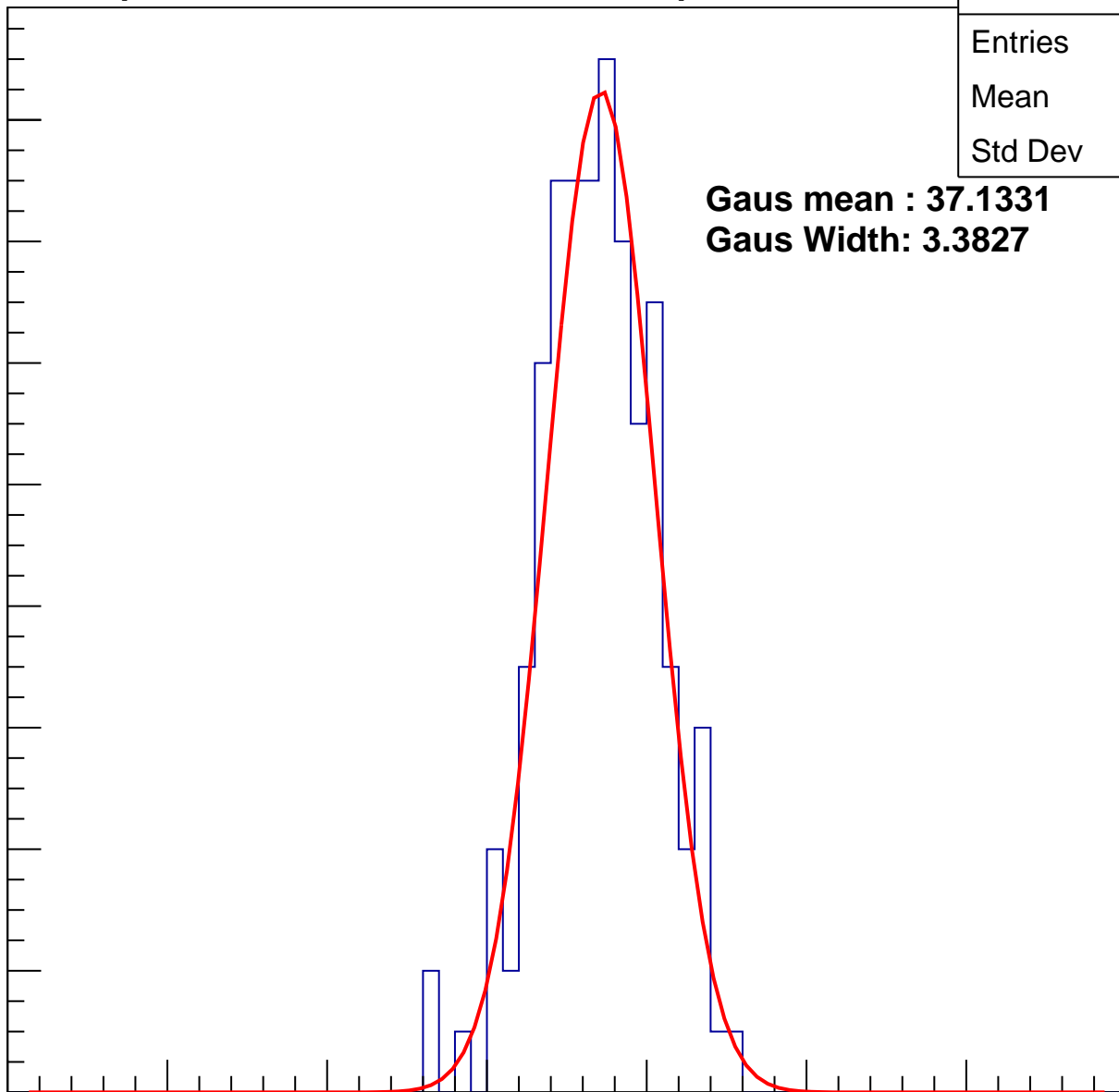
40

50

60

70

ampl



B0L101S, U25-ch20, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

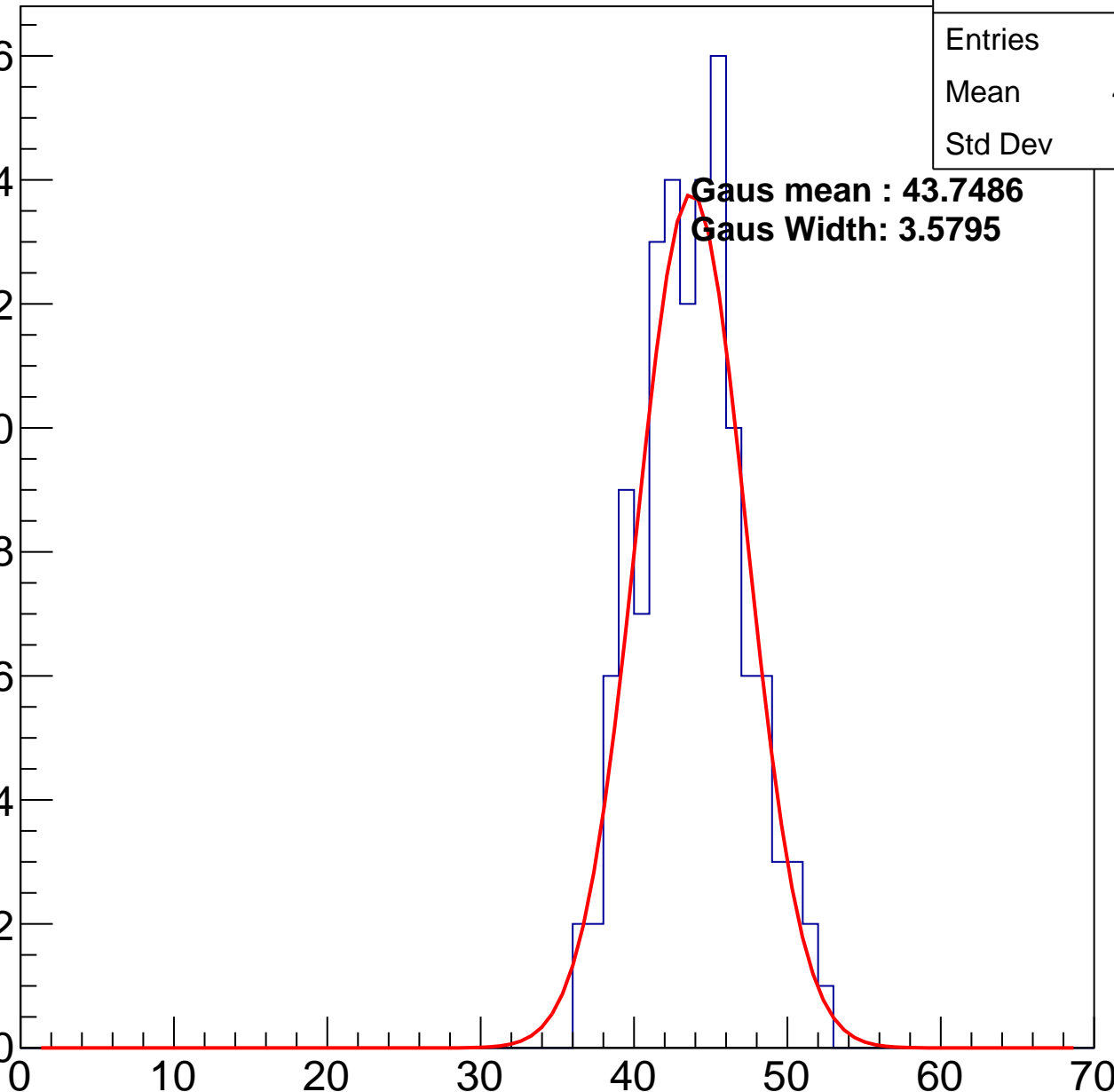
16
14
12
10
8
6
4
2
0

Entries	126
Mean	43.33
Std Dev	3.401

Gaus mean : 43.7486

Gaus Width: 3.5795

ampl



B0L101S, U25-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

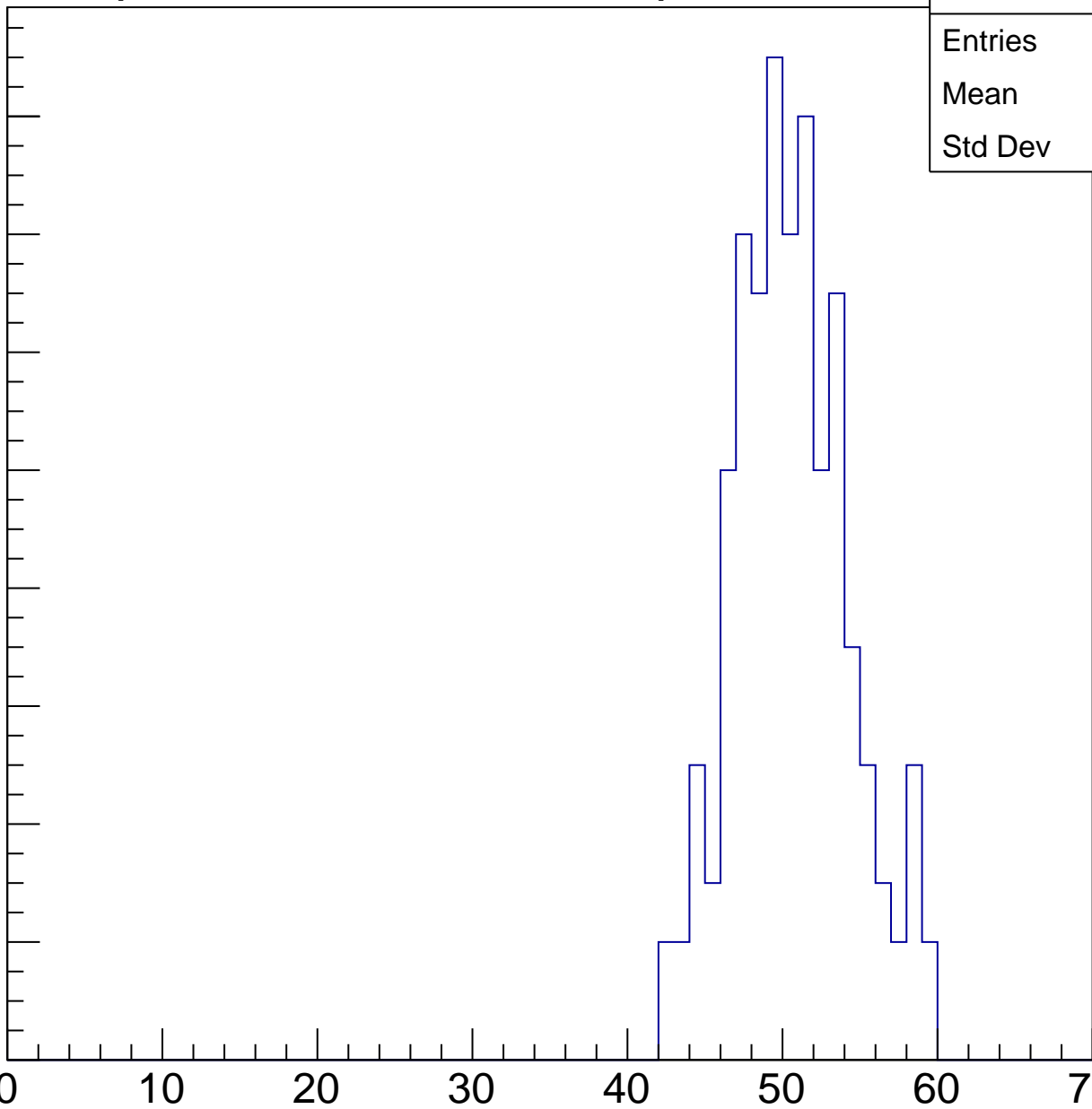
Entries	143
Mean	50.13
Std Dev	3.675

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	56.93
Std Dev	3.358

Entry

16

14

12

10

8

6

4

2

0

0

10

20

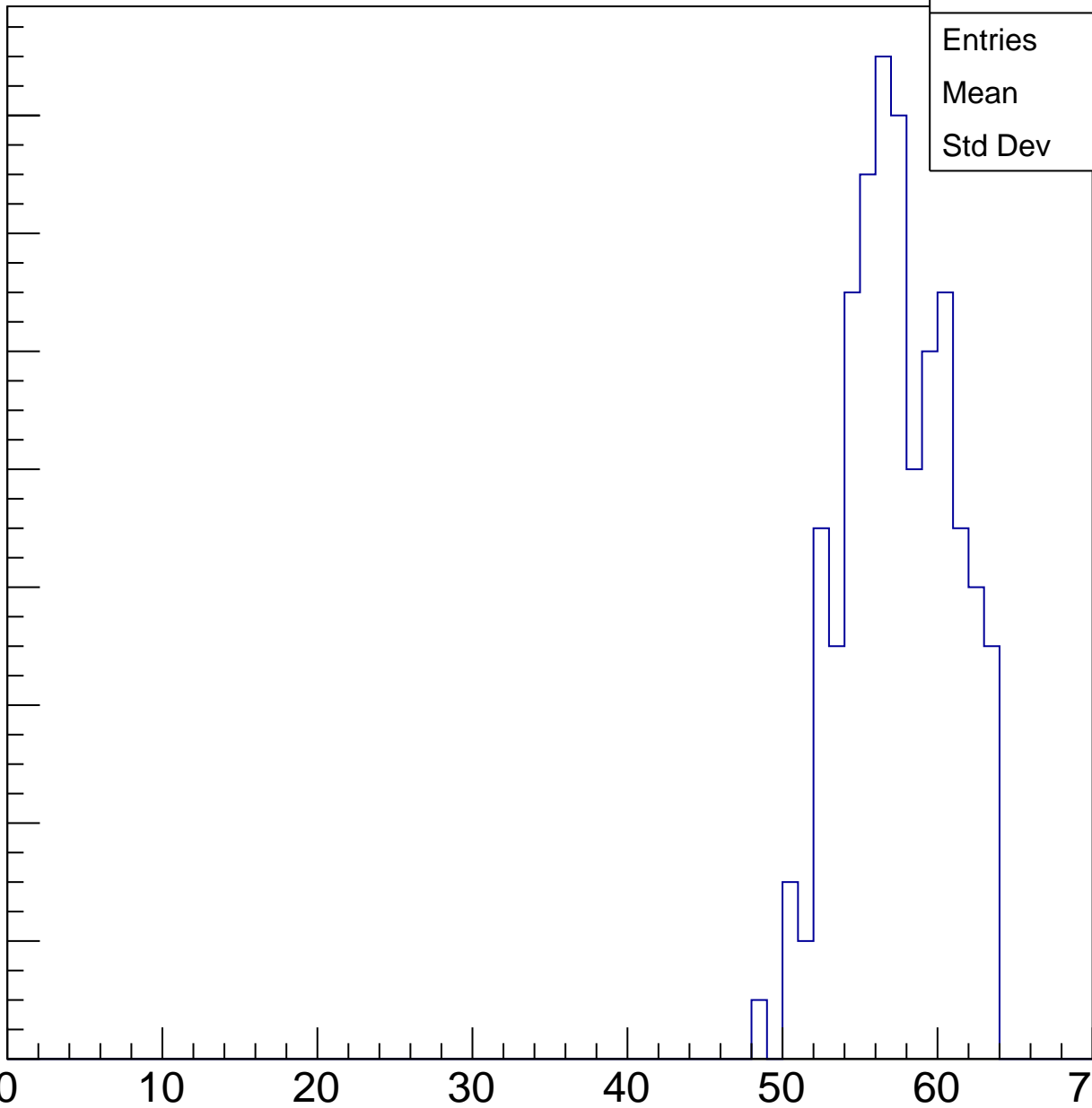
30

40

50

60

ampl



B0L101S, U25-ch20, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	49
Mean	59.63
Std Dev	8.785

B0L101S, U25-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

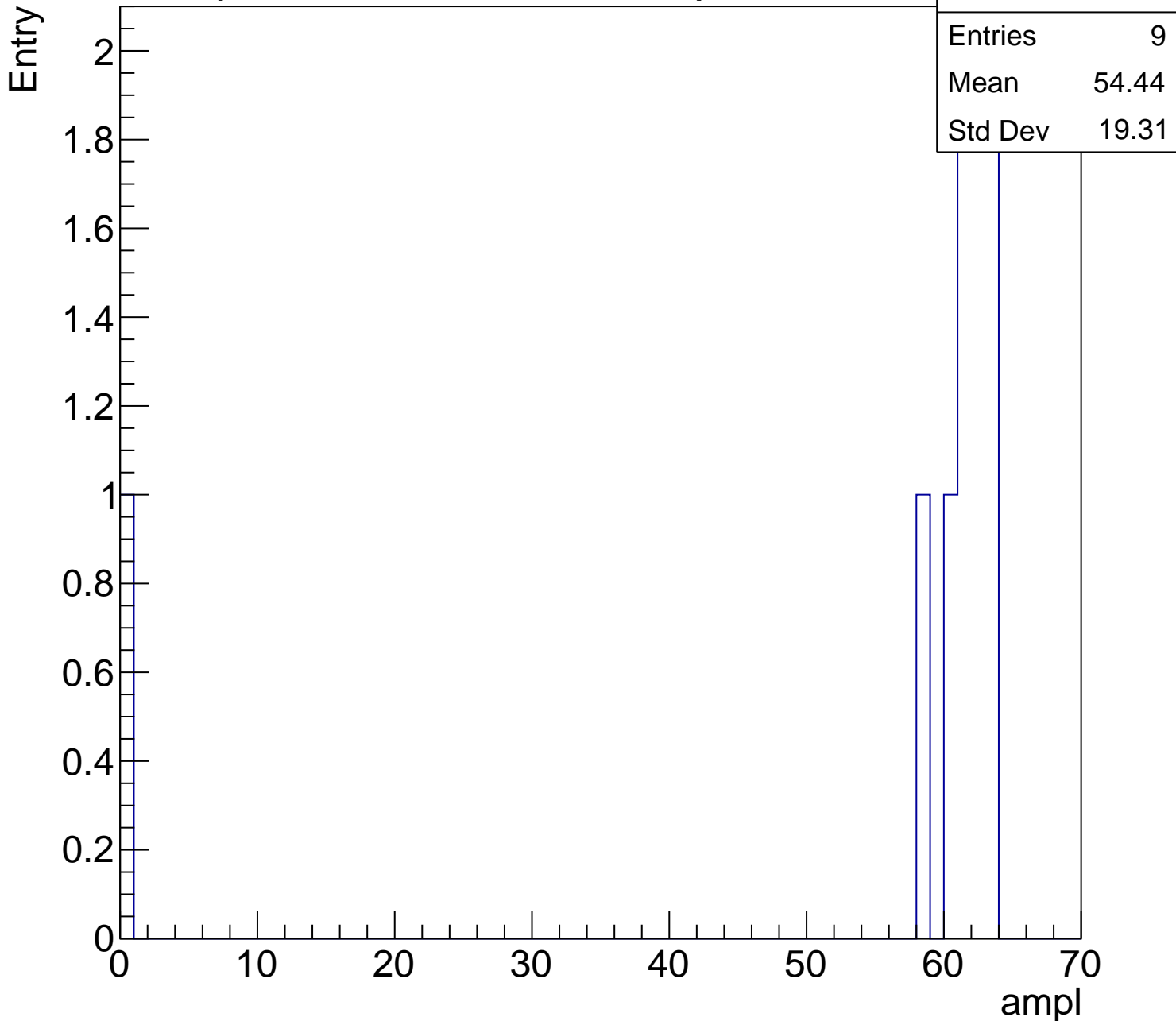
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	54.44
Std Dev	19.31

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

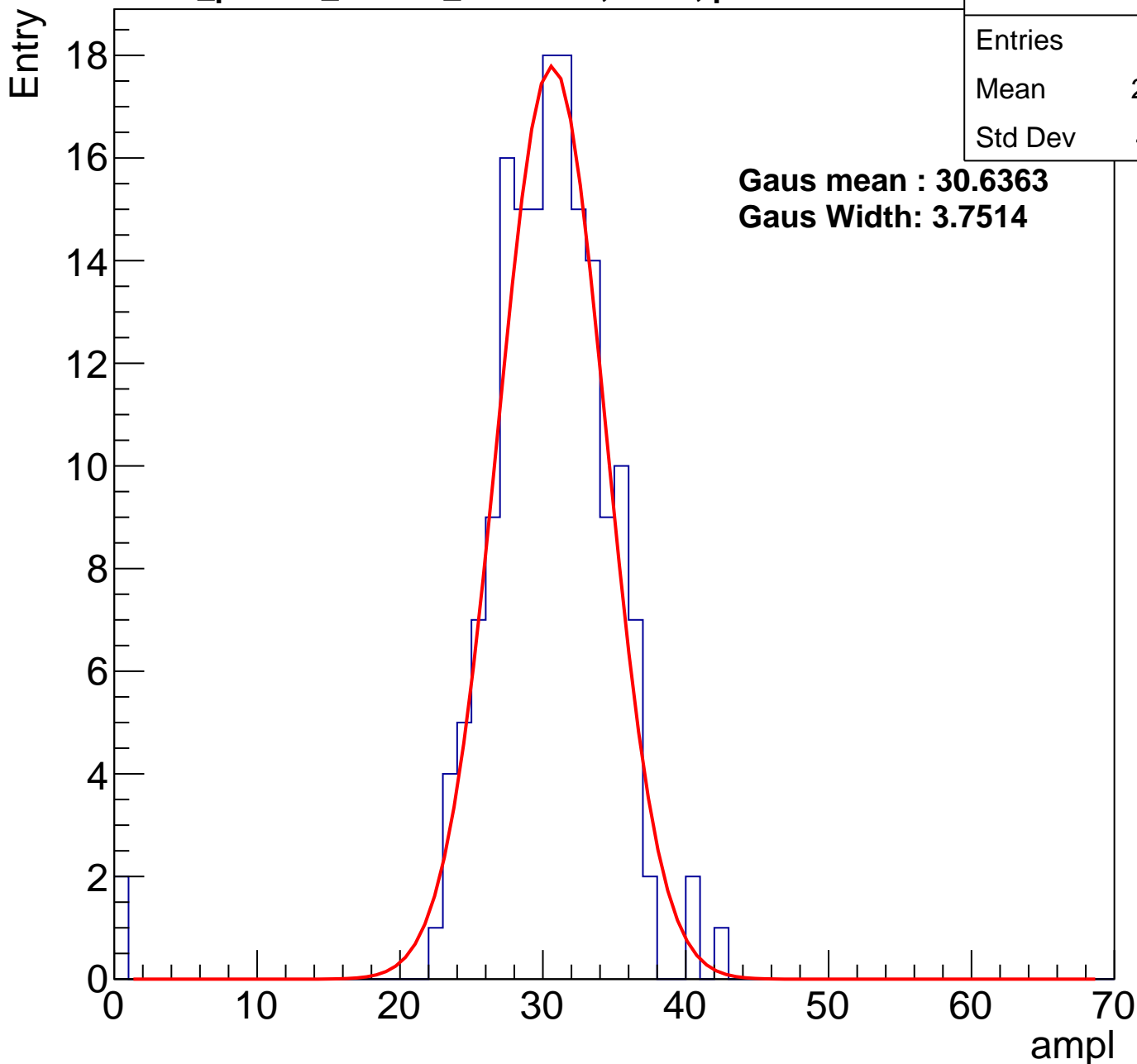
B0L101S, U25-ch21, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	29.85
Std Dev	4.891

Gaus mean : 30.6363

Gaus Width: 3.7514



B0L101S, U25-ch21, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	37.17
Std Dev	3.522

Gaus mean : 37.9558

Gaus Width: 3.1347

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

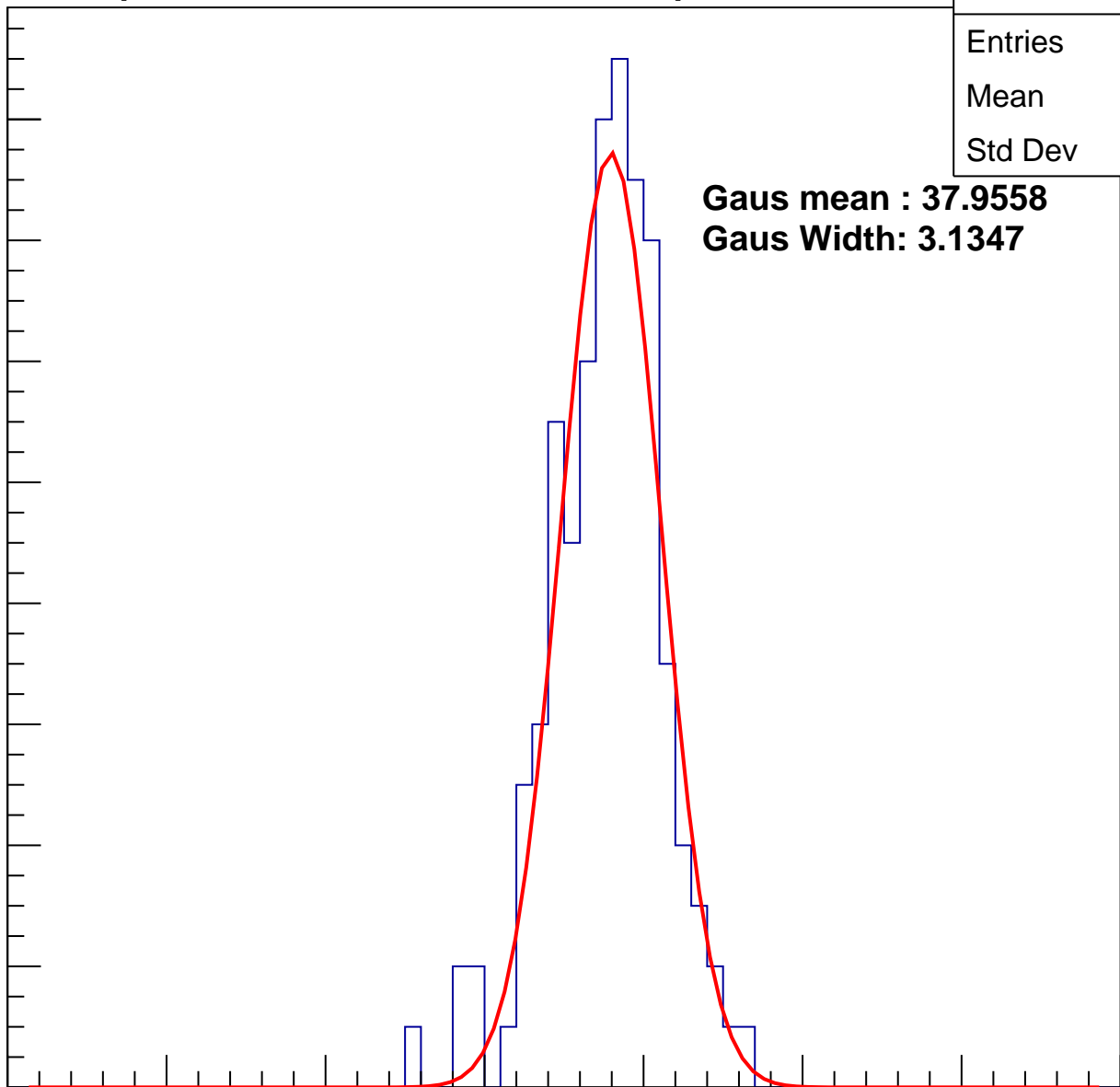
40

50

60

70

ampl



B0L101S, U25-ch21, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries 161

Mean 44.8

Std Dev 3.661

Gaus mean : 45.2629

Gaus Width: 3.5614

0

10

20

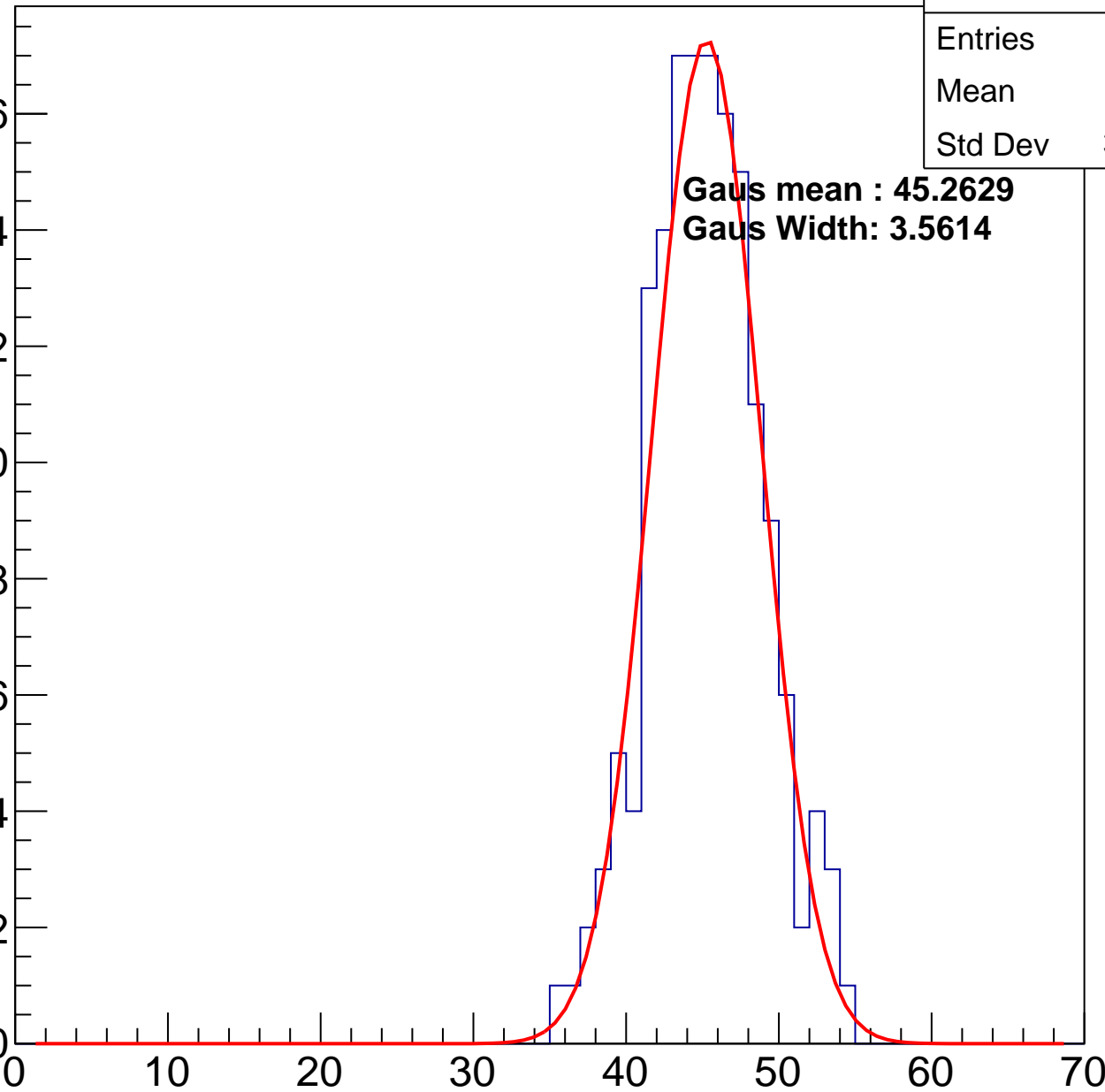
30

40

50

60

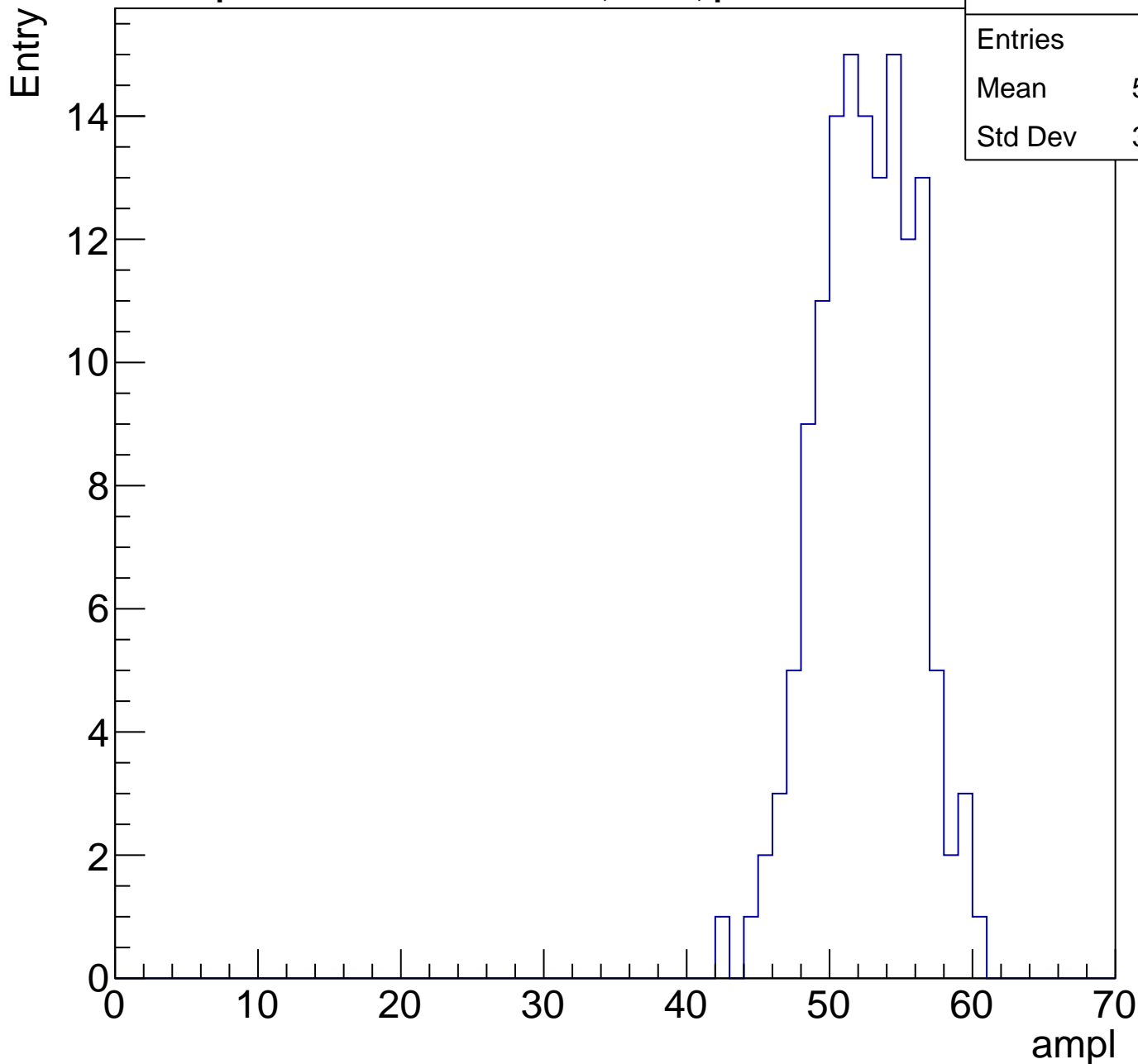
ampl



B0L101S, U25-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	52.07
Std Dev	3.398

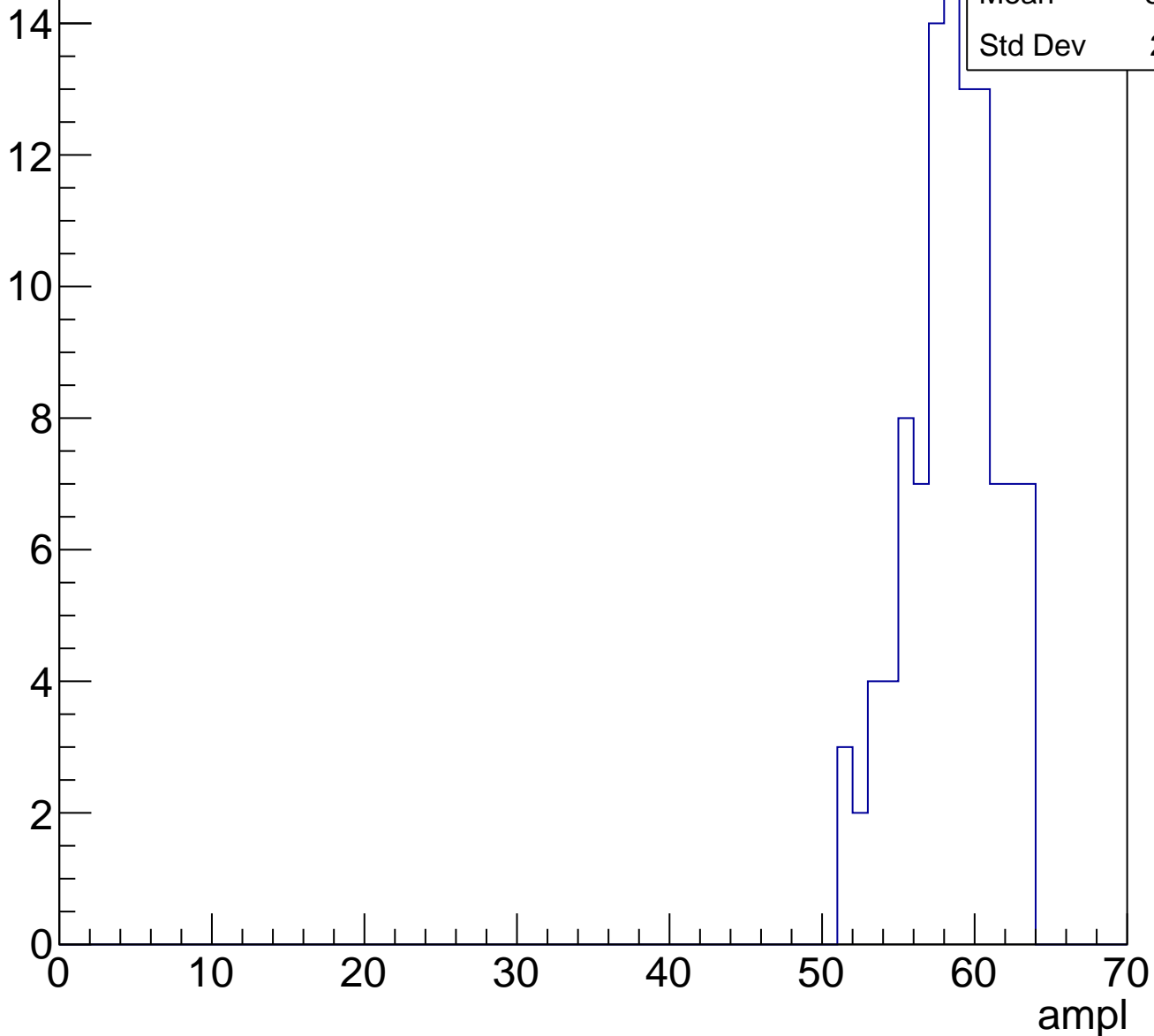


B0L101S, U25-ch21, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	58.02
Std Dev	2.961

Entry



B0L101S, U25-ch21, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	51
Mean	58.78
Std Dev	11.98

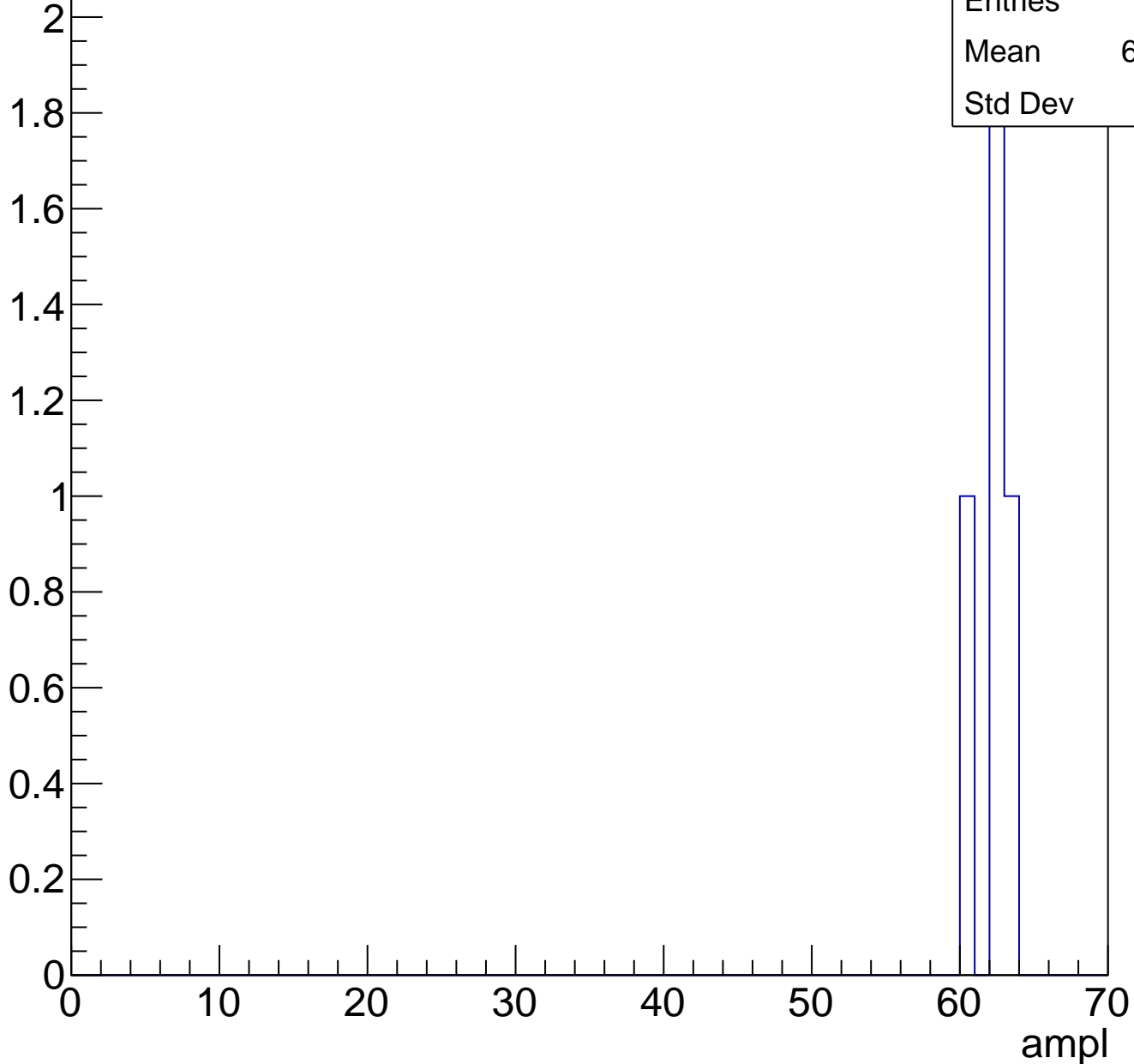
ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



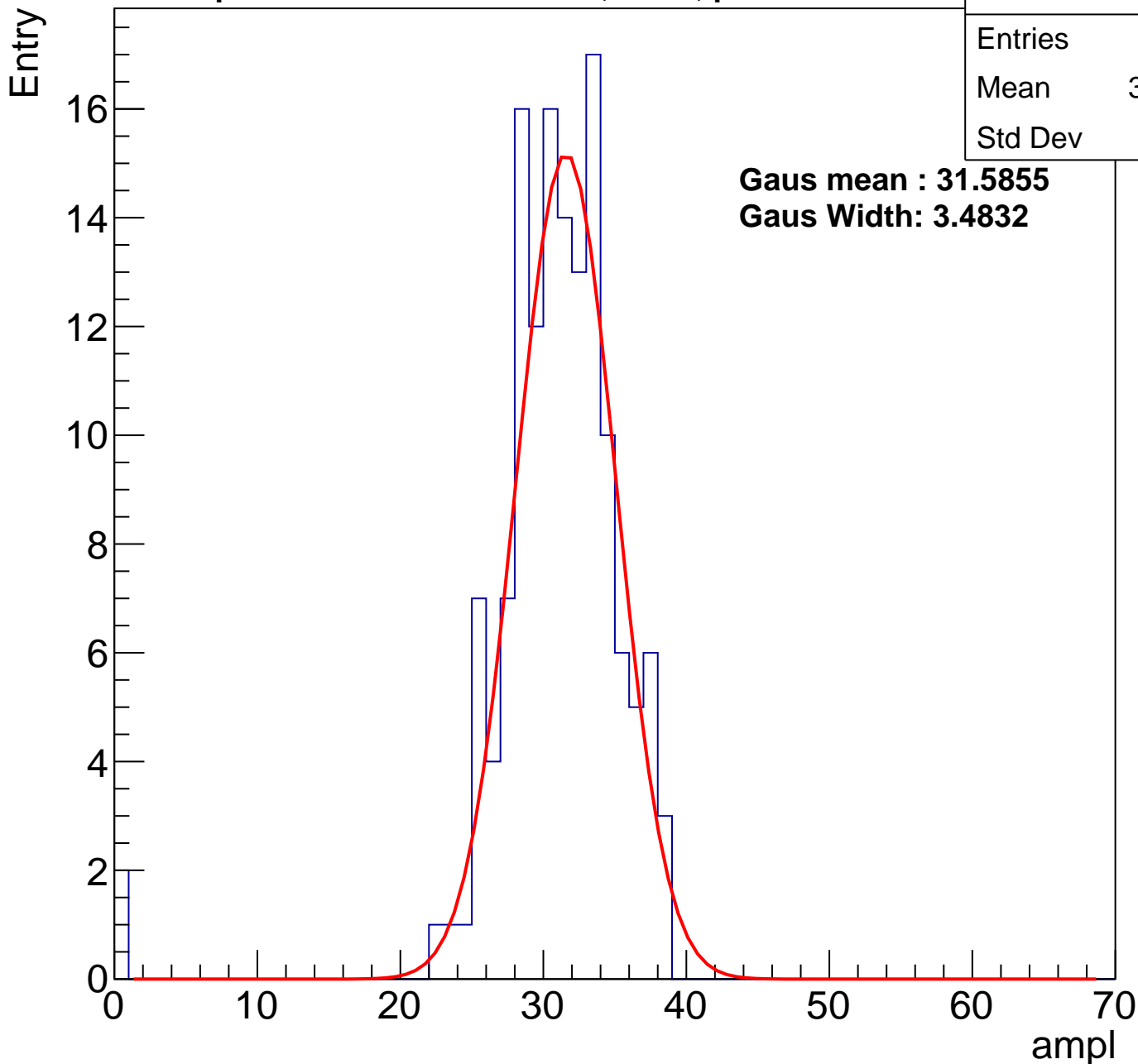
B0L101S, U25-ch22, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	30.43
Std Dev	4.98

Gaus mean : 31.5855

Gaus Width: 3.4832



B0L101S, U25-ch22, adc1

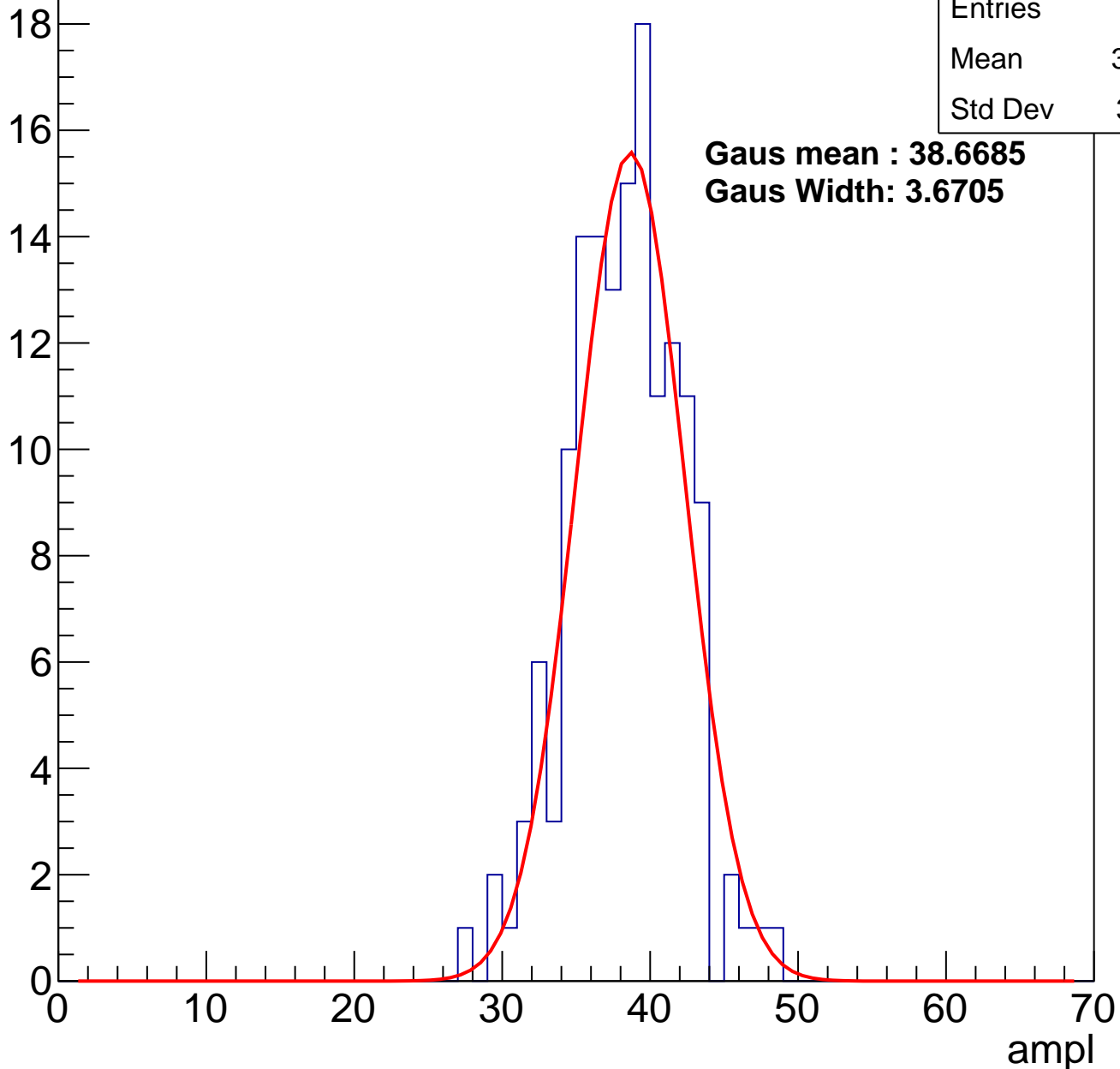
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	37.82
Std Dev	3.721

Gaus mean : 38.6685

Gaus Width: 3.6705

Entry



B0L101S, U25-ch22, adc2

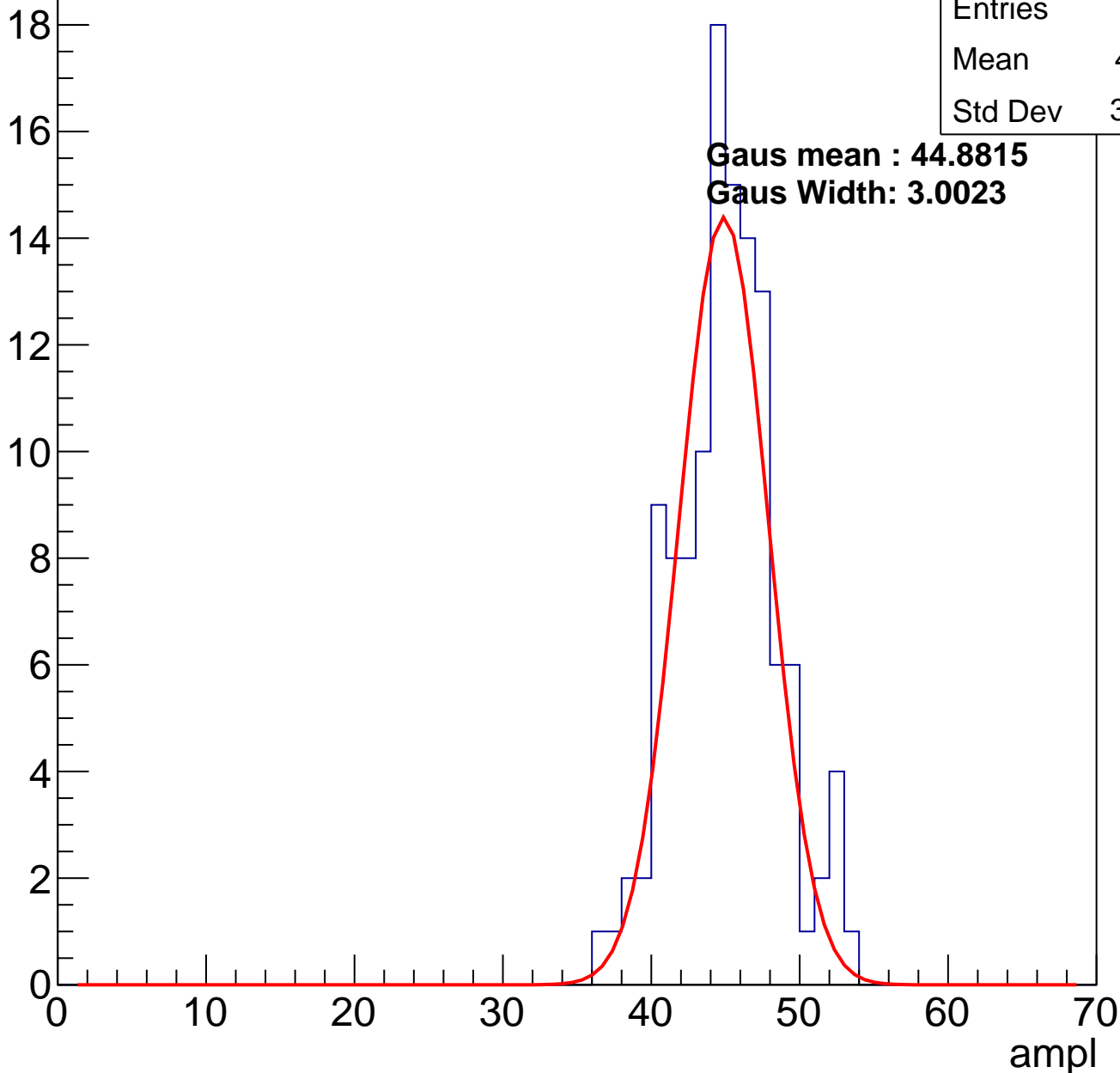
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	44.61
Std Dev	3.344

Gaus mean : 44.8815

Gaus Width: 3.0023

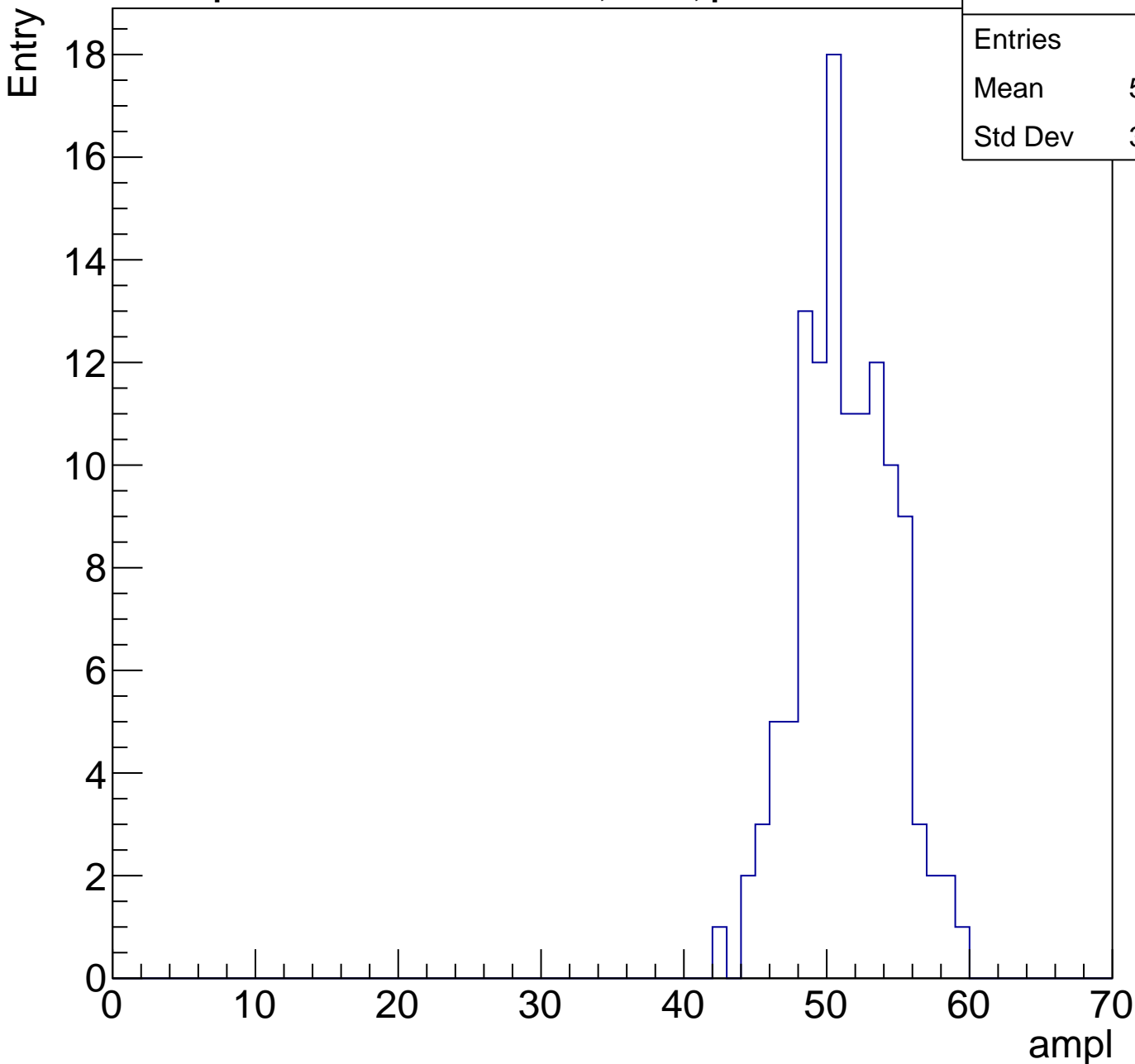
Entry



B0L101S, U25-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	50.86
Std Dev	3.259



B0L101S, U25-ch22, adc4

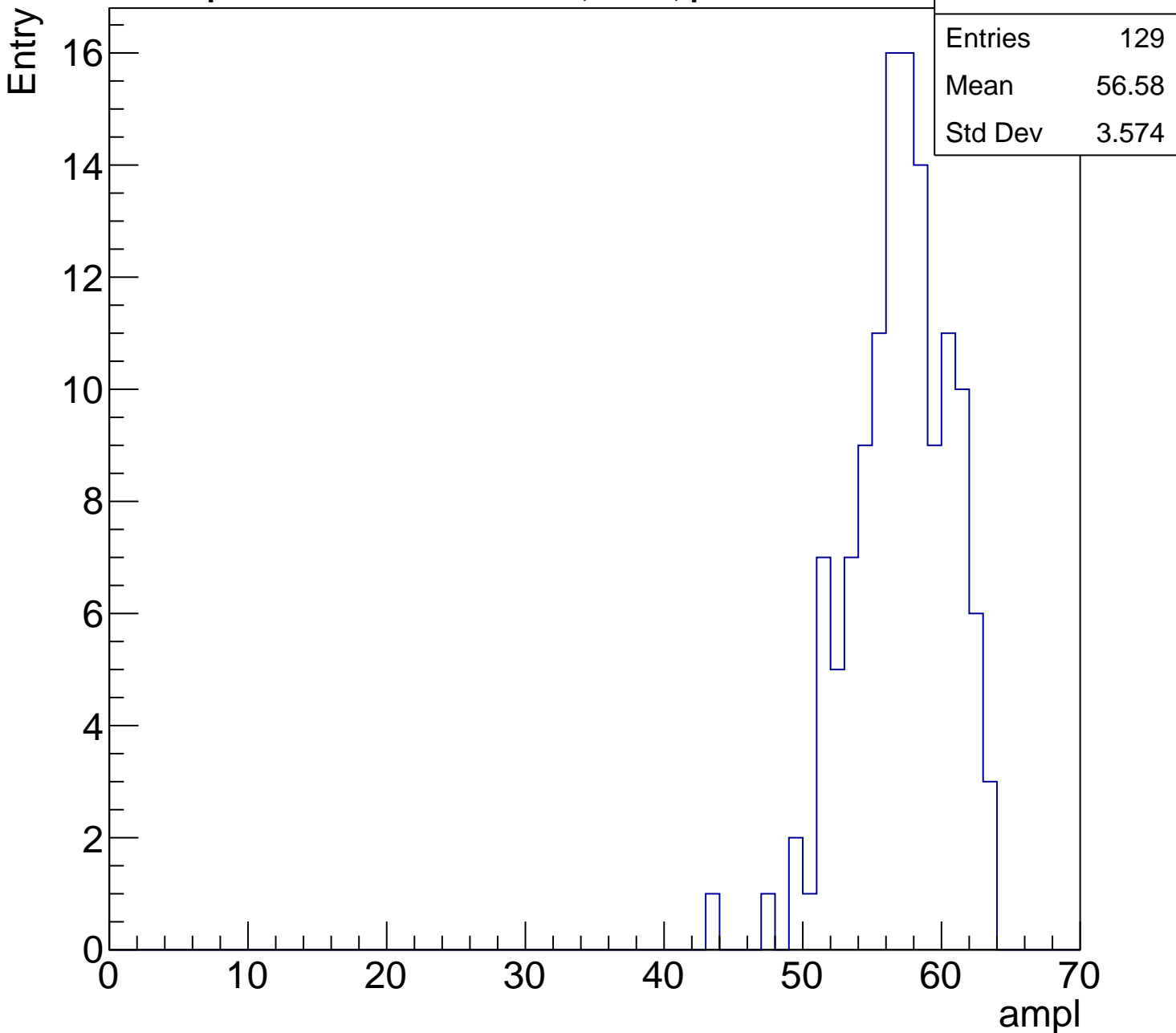
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	56.58
Std Dev	3.574

ampl



B0L101S, U25-ch22, adc5

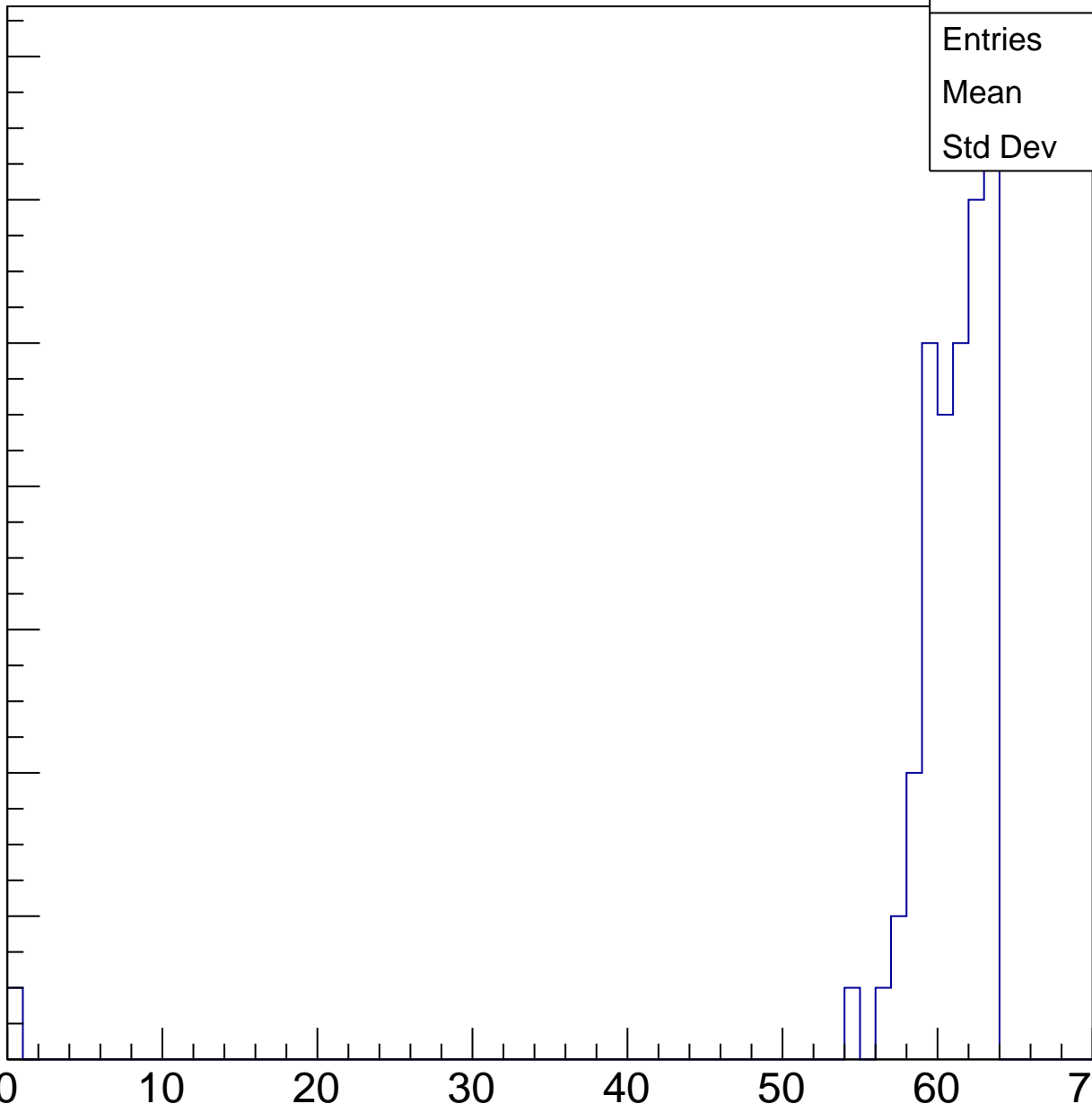
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	64
Mean	59.72
Std Dev	7.781

ampl



B0L101S, U25-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U25-ch23, adc0

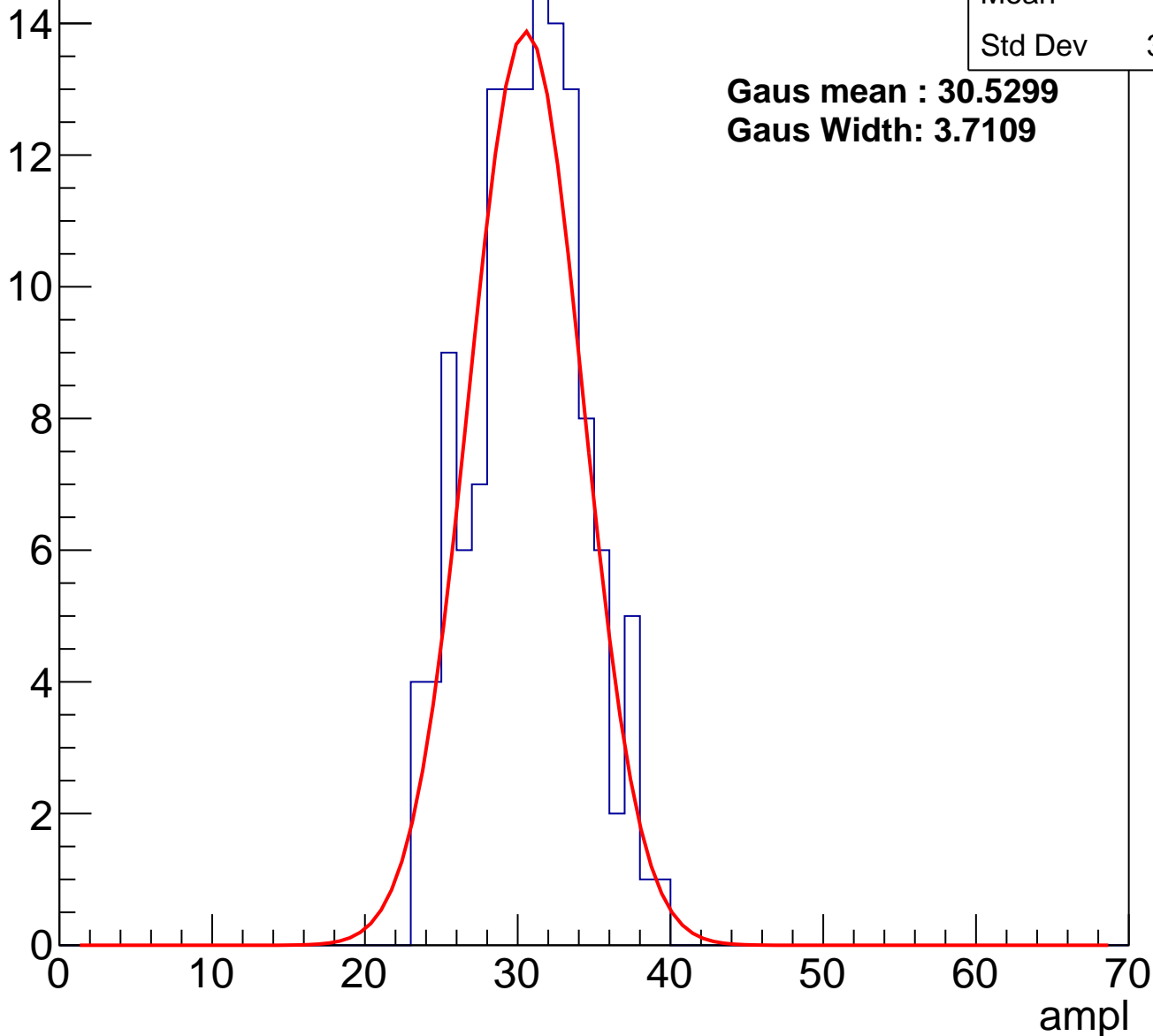
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	30.2
Std Dev	3.574

Gaus mean : 30.5299

Gaus Width: 3.7109

Entry



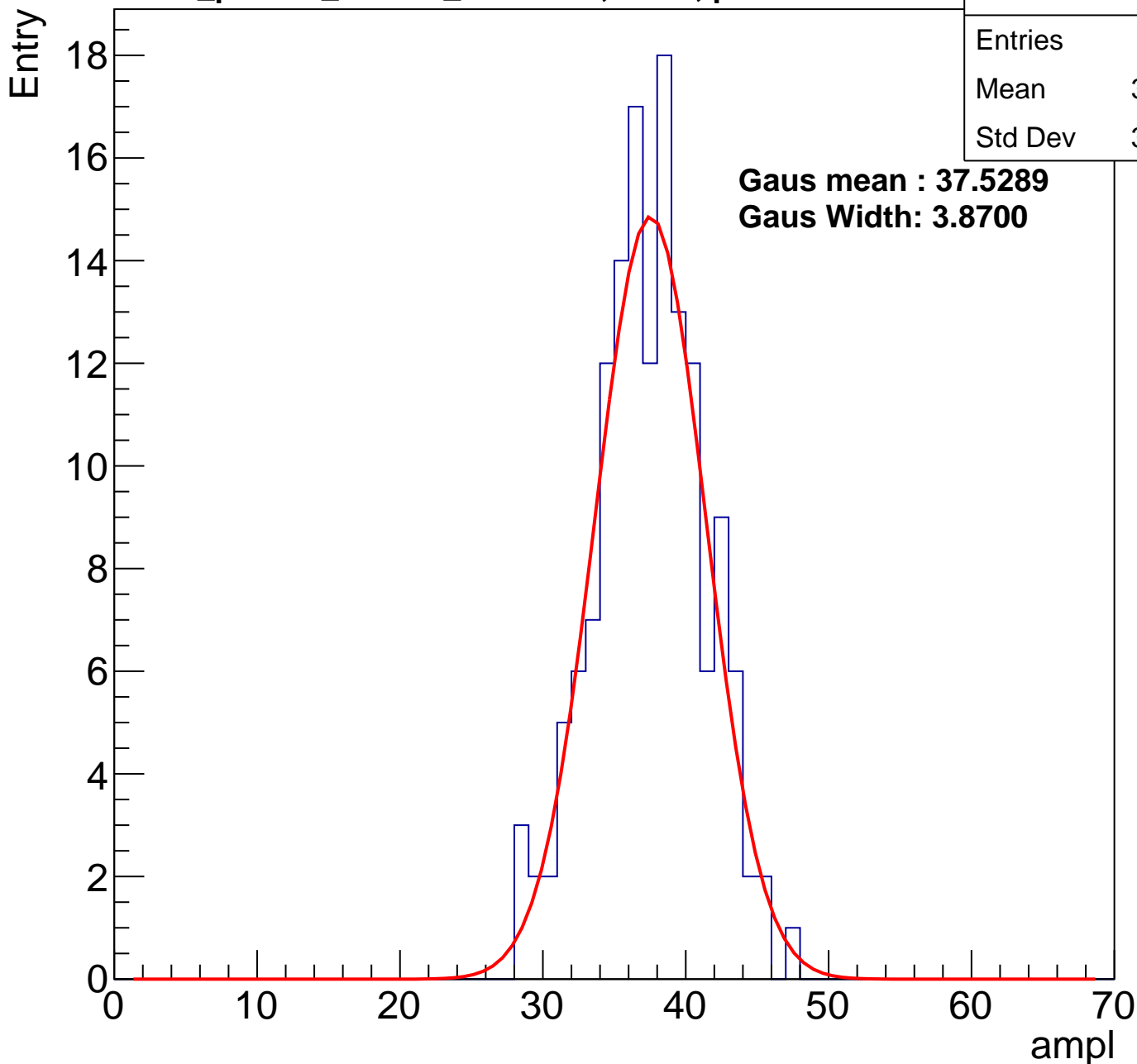
B0L101S, U25-ch23, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	36.99
Std Dev	3.784

Gaus mean : 37.5289

Gaus Width: 3.8700



B0L101S, U25-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	43.97
Std Dev	3.205

Gaus mean : 44.4992

Gaus Width: 3.0201

0

10

20

30

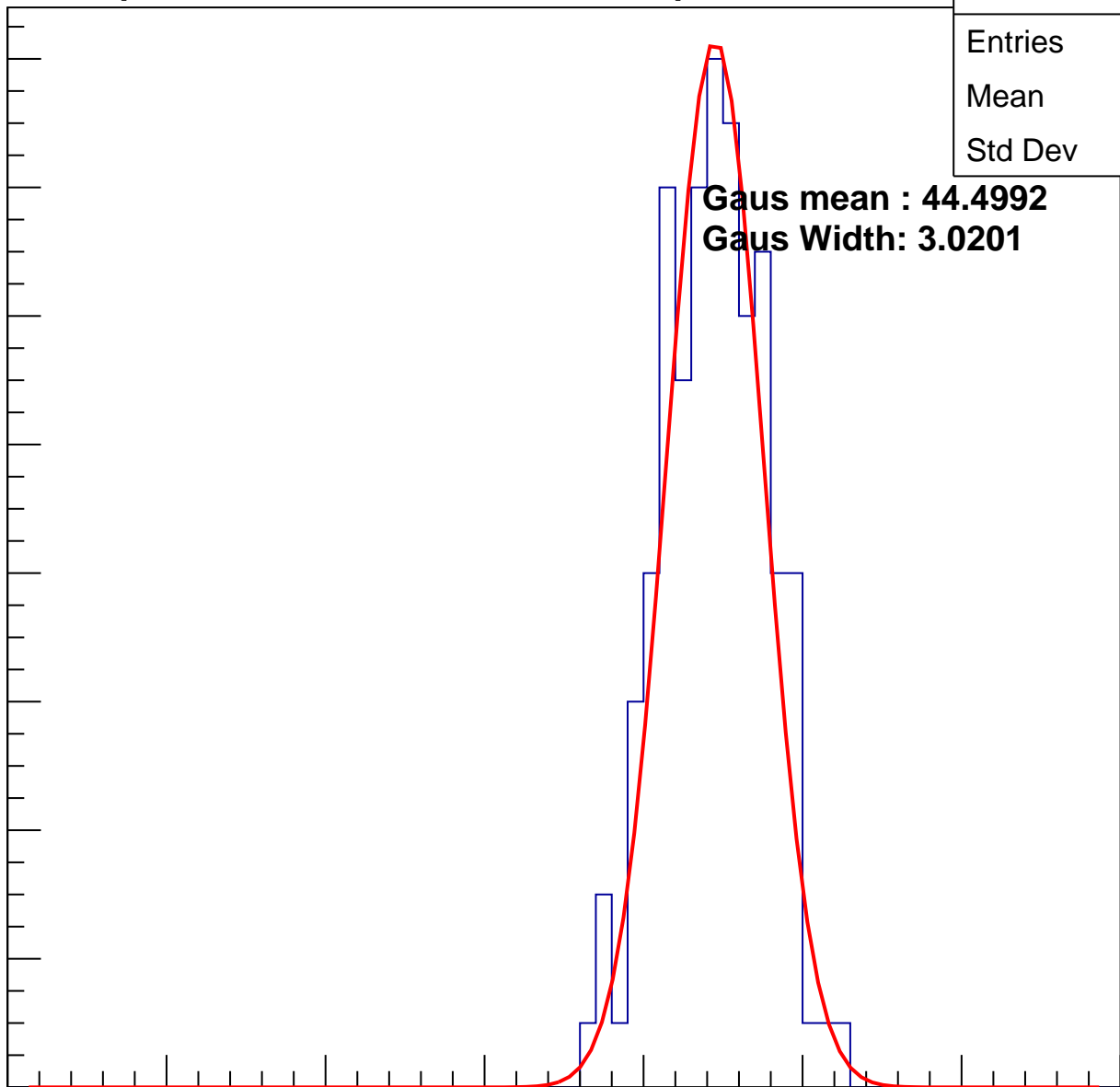
40

50

60

70

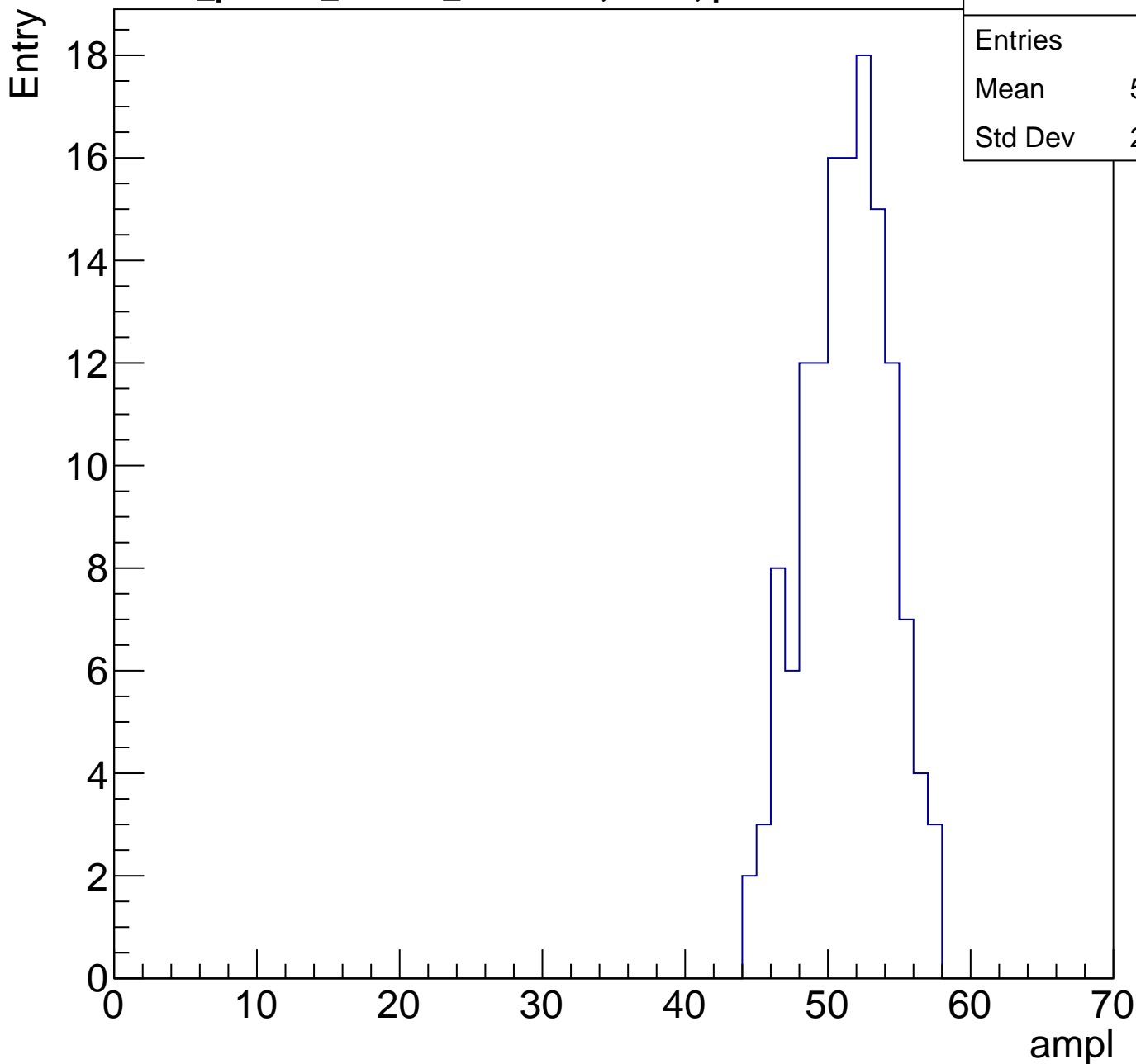
ampl



B0L101S, U25-ch23, adc3

calib_packv5_042523_0143.root, FC#1, port C1

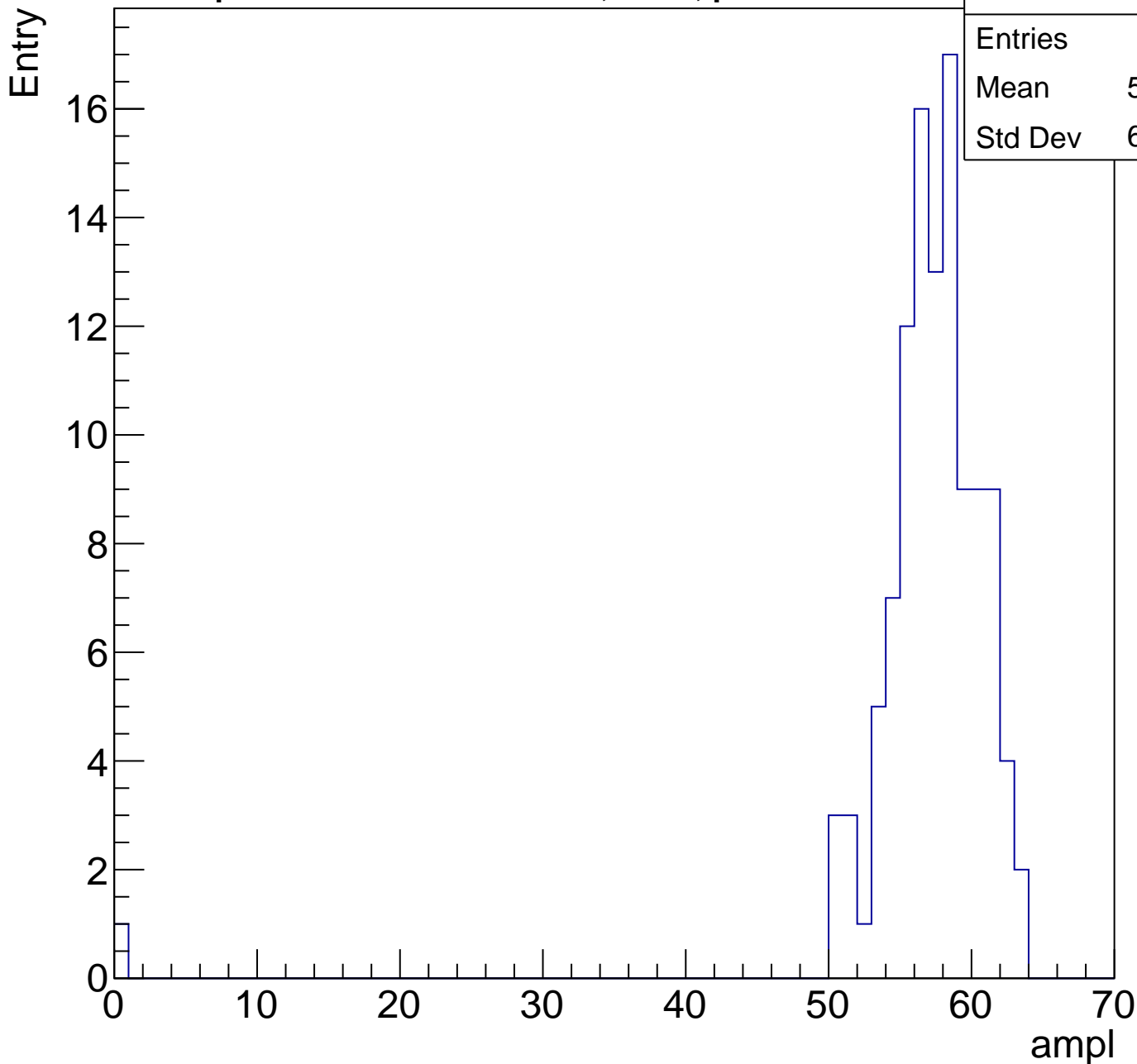
Entries	134
Mean	50.84
Std Dev	2.963



B0L101S, U25-ch23, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	56.53
Std Dev	6.123



B0L101S, U25-ch23, adc5

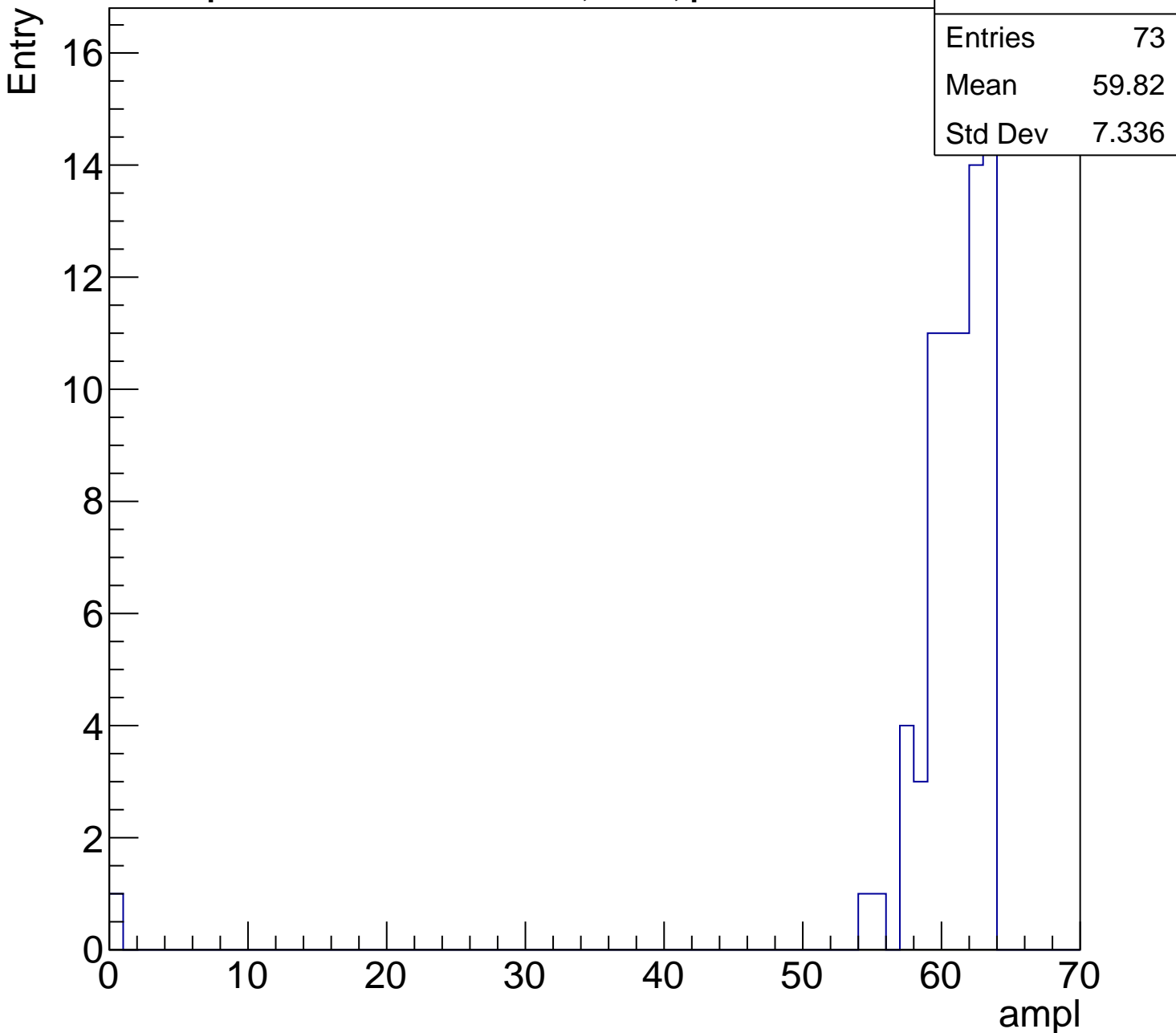
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59.82
Std Dev	7.336

ampl



B0L101S, U25-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

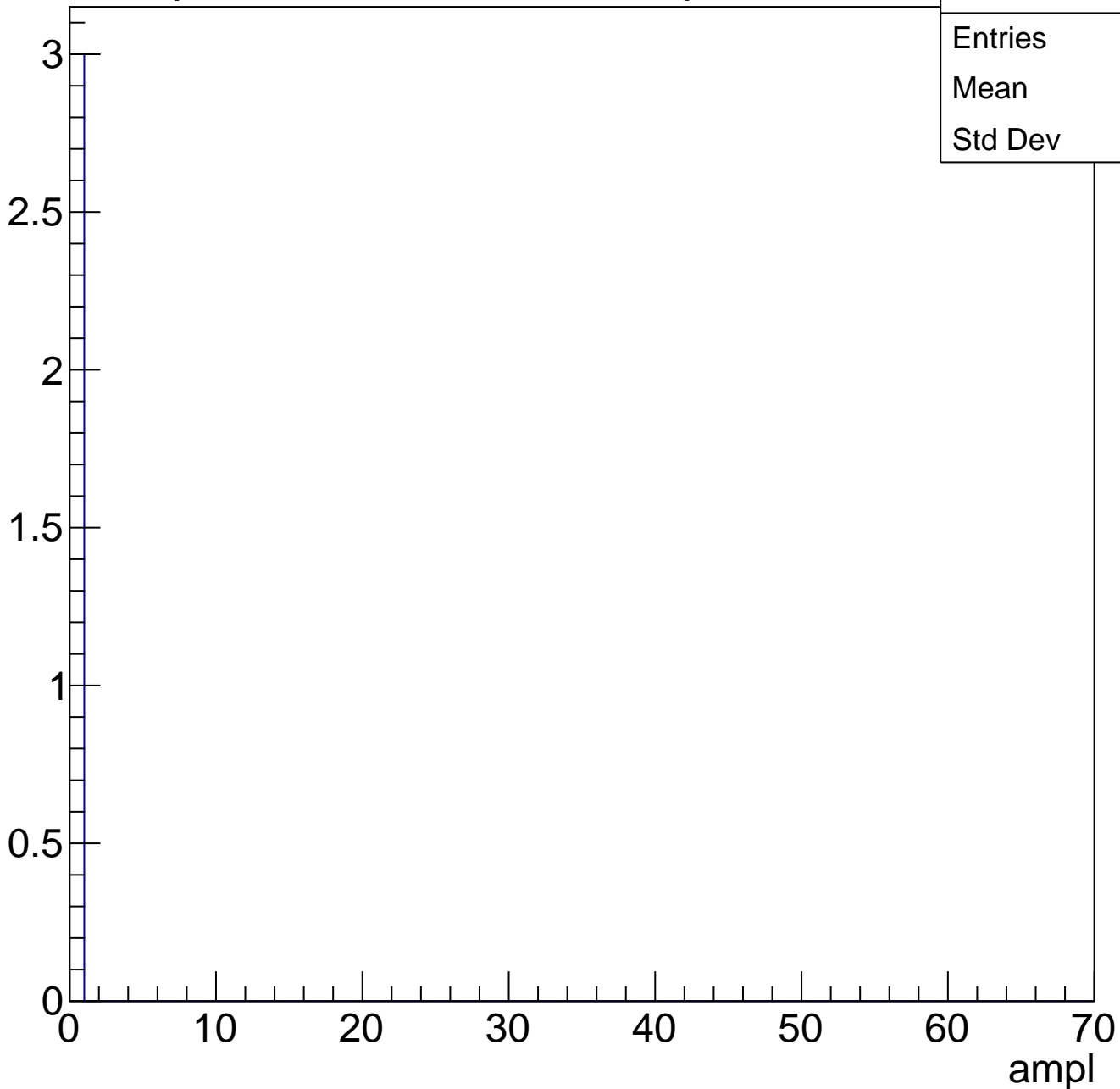
Entry



B0L101S, U25-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U25-ch24, adc0

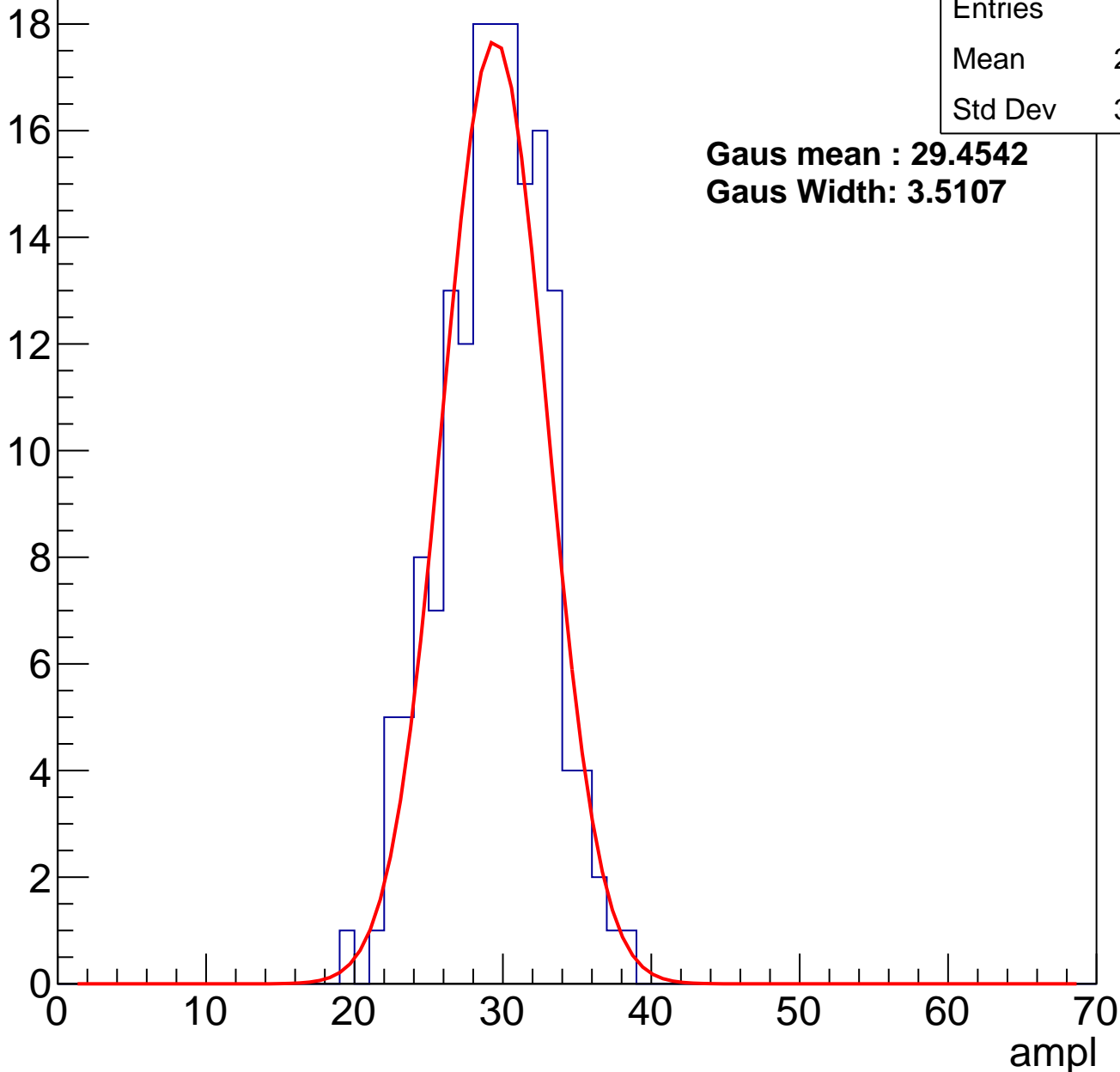
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	28.94
Std Dev	3.514

Gaus mean : 29.4542

Gaus Width: 3.5107

Entry



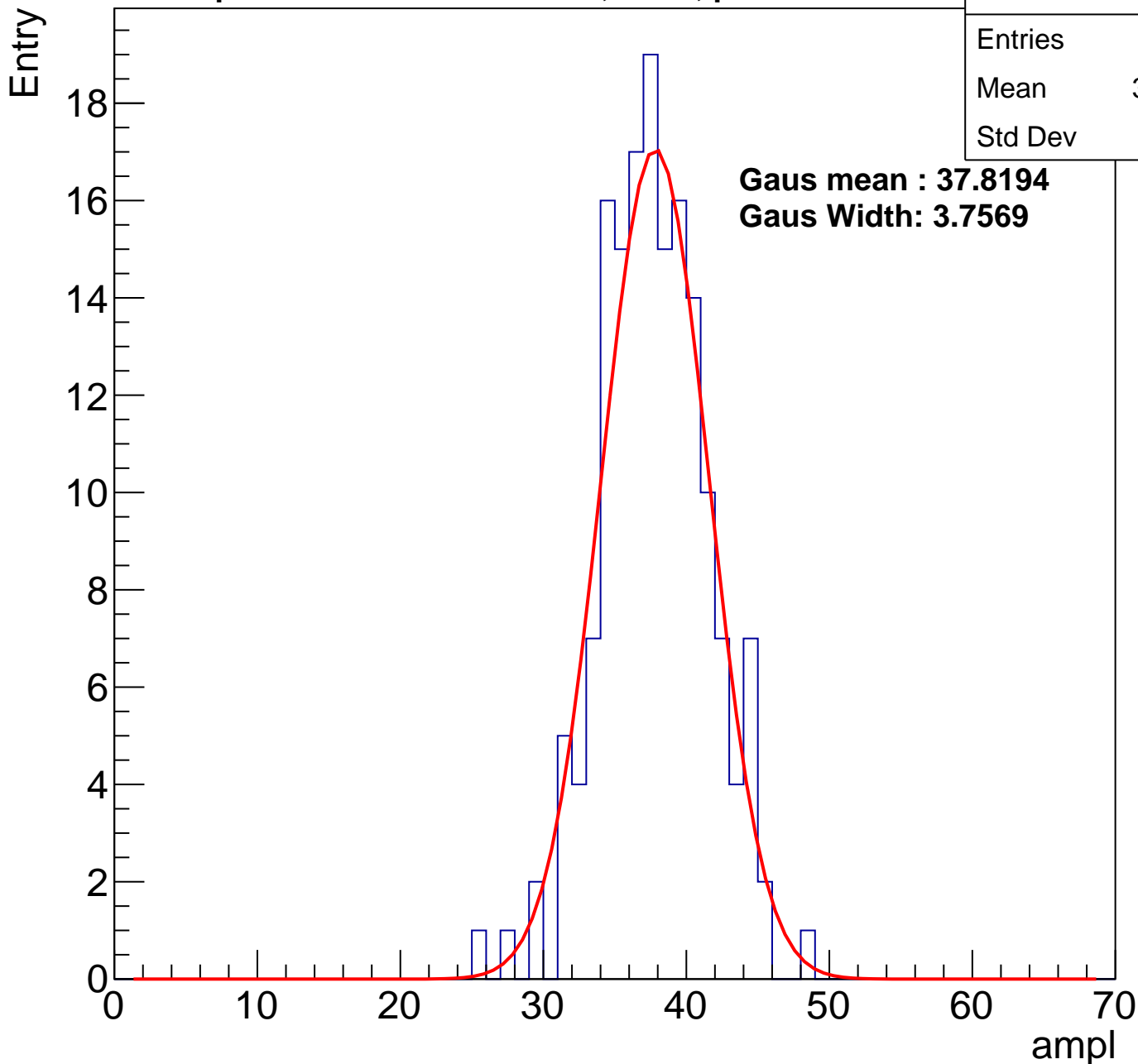
B0L101S, U25-ch24, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	37.33
Std Dev	3.71

Gaus mean : 37.8194

Gaus Width: 3.7569



B0L101S, U25-ch24, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

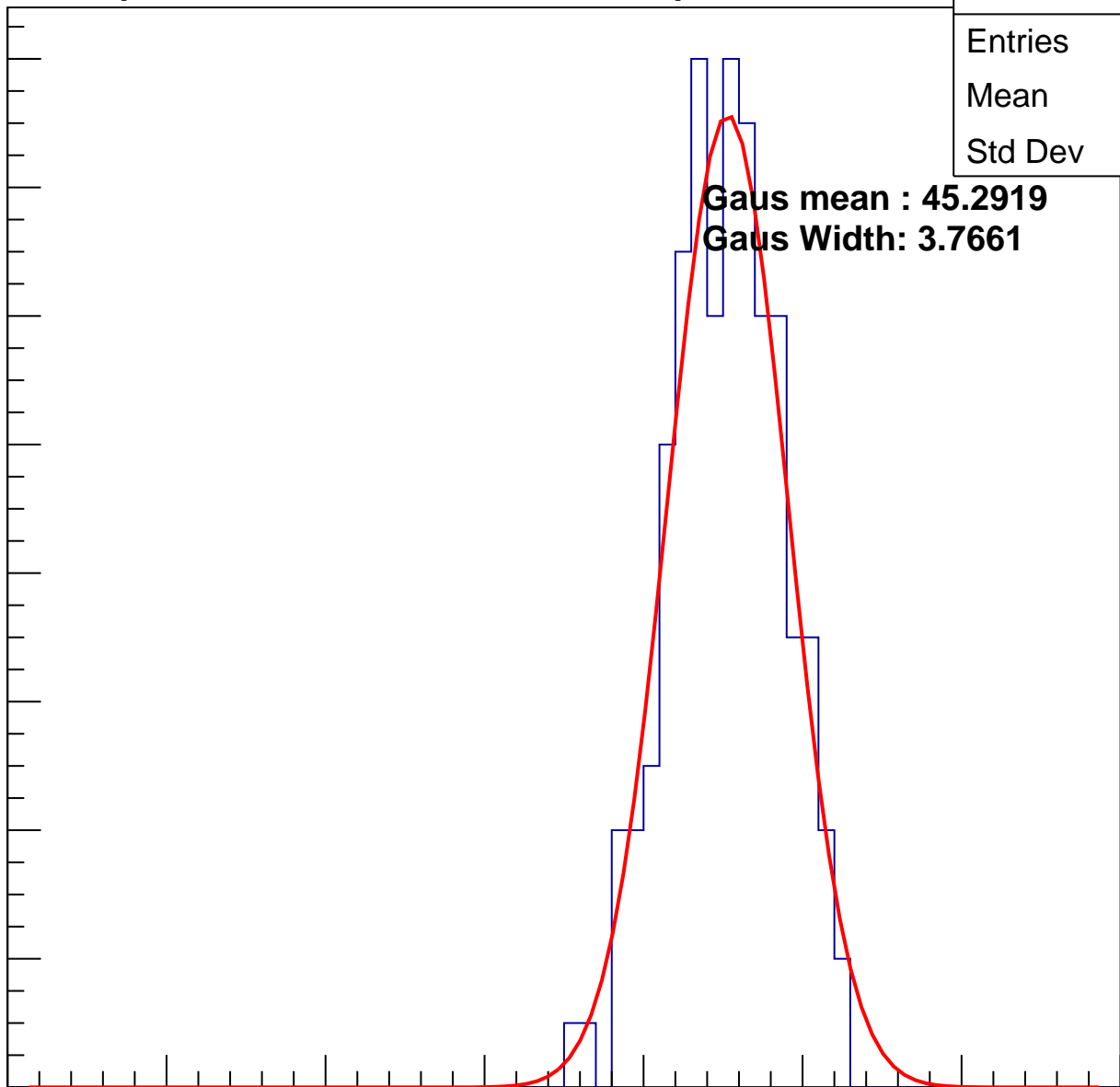
Entries	141
Mean	44.7
Std Dev	3.458

Gaus mean : 45.2919

Gaus Width: 3.7661

ampl

0 10 20 30 40 50 60 70

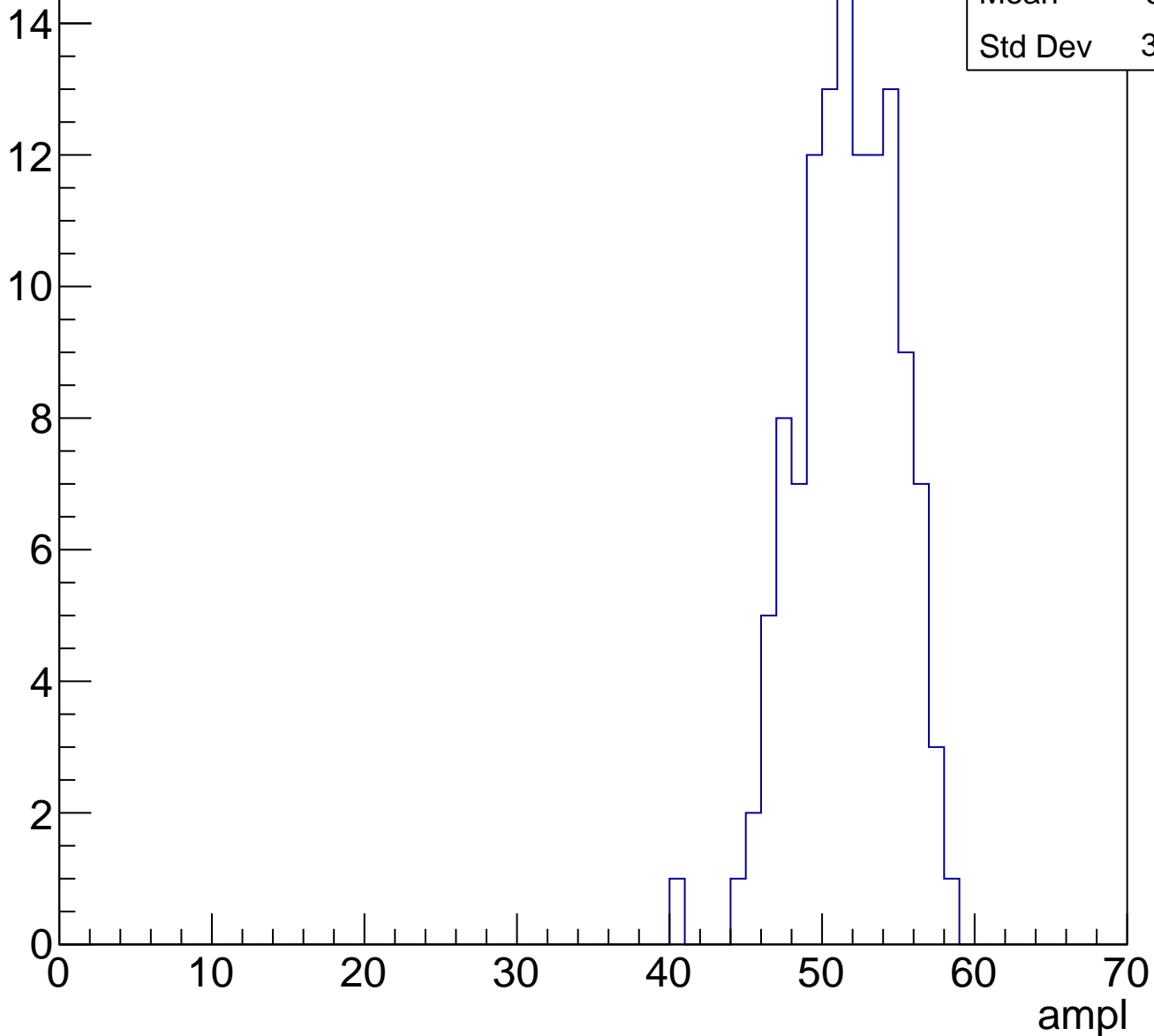


B0L101S, U25-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	51.21
Std Dev	3.235

Entry



B0L101S, U25-ch24, adc4

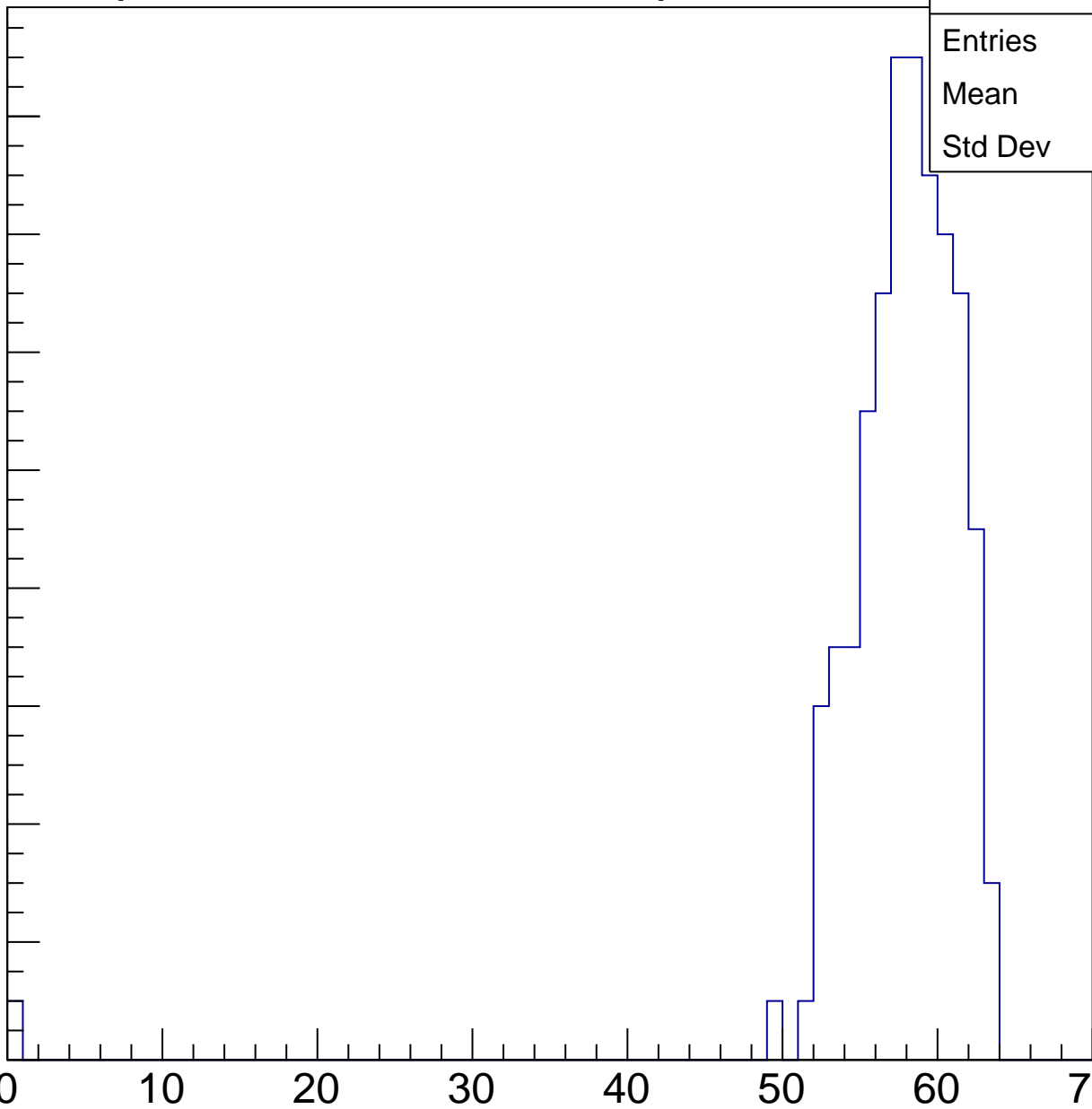
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	57.14
Std Dev	5.756

ampl



B0L101S, U25-ch24, adc5

calib_packv5_042523_0143.root, FC#1, port C1

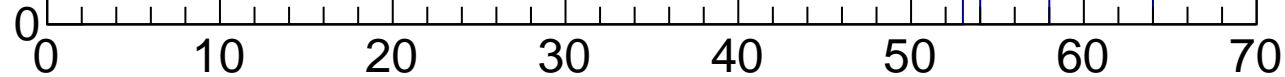
Entry

14
12
10
8
6
4
2
0

Entries	45
Mean	61.18
Std Dev	1.958

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch25, adc0

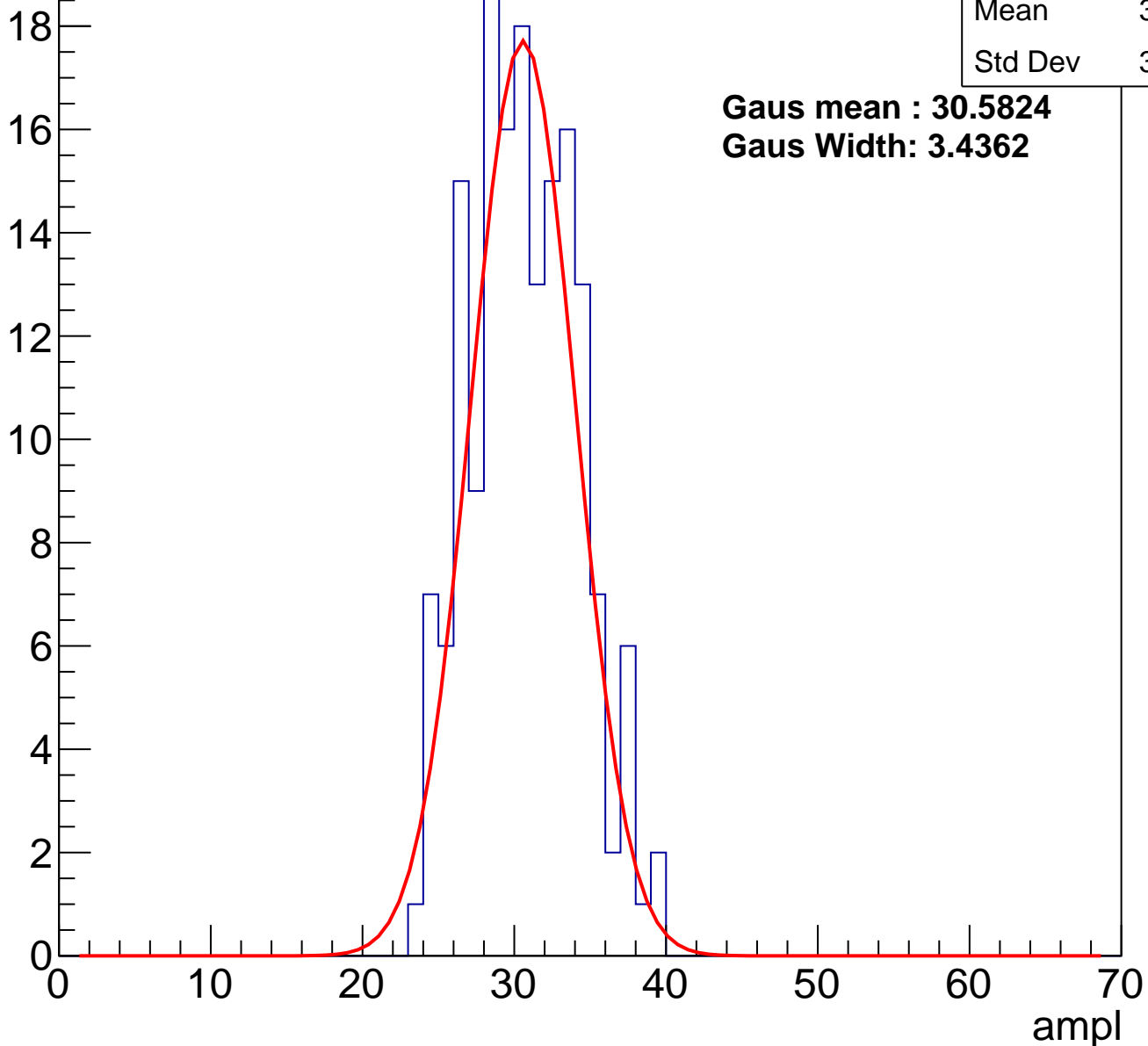
calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	30.23
Std Dev	3.512

Gaus mean : 30.5824

Gaus Width: 3.4362

Entry



B0L101S, U25-ch25, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	37.93
Std Dev	3.671

Gaus mean : 38.1354

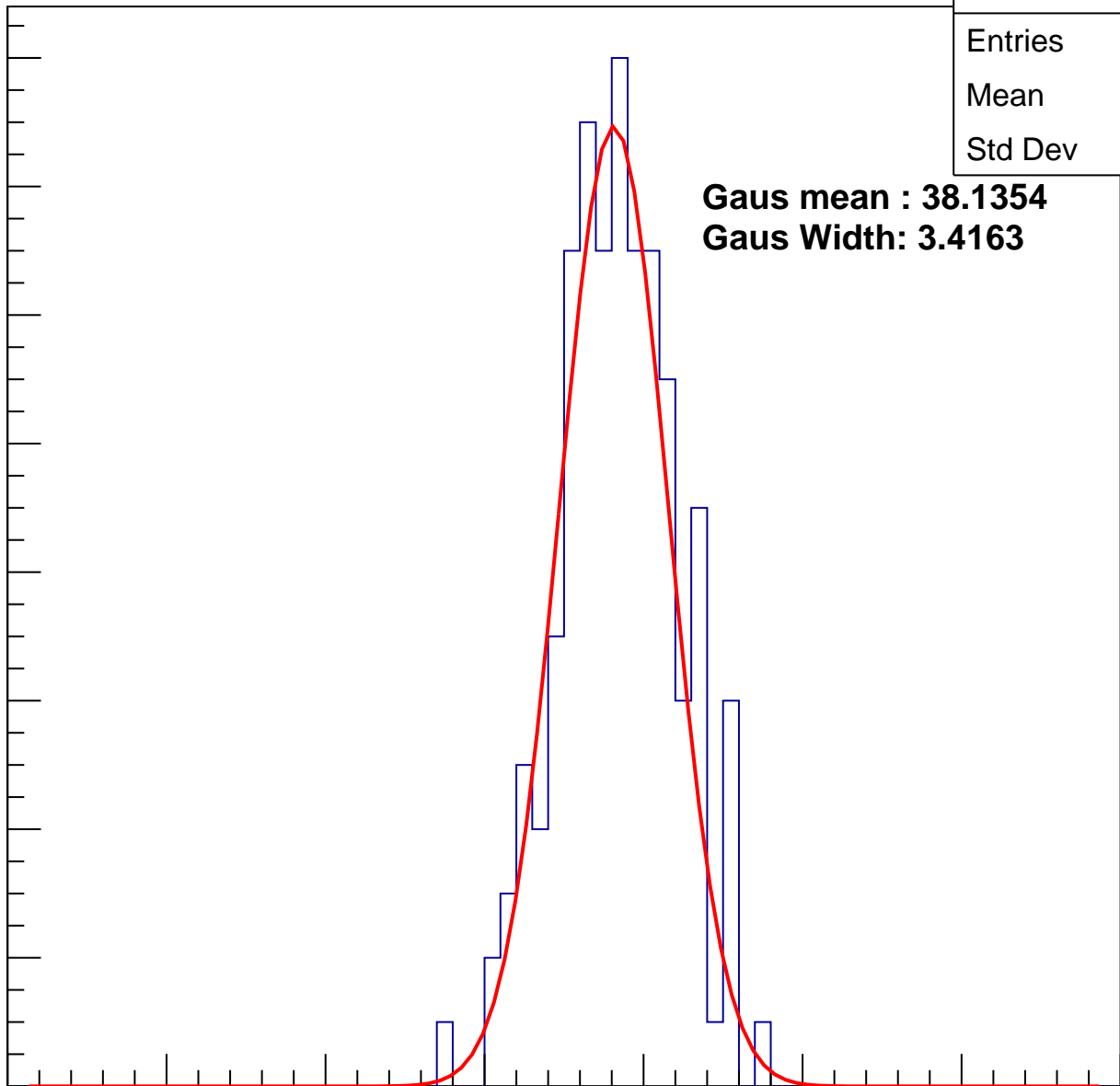
Gaus Width: 3.4163

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch25, adc2

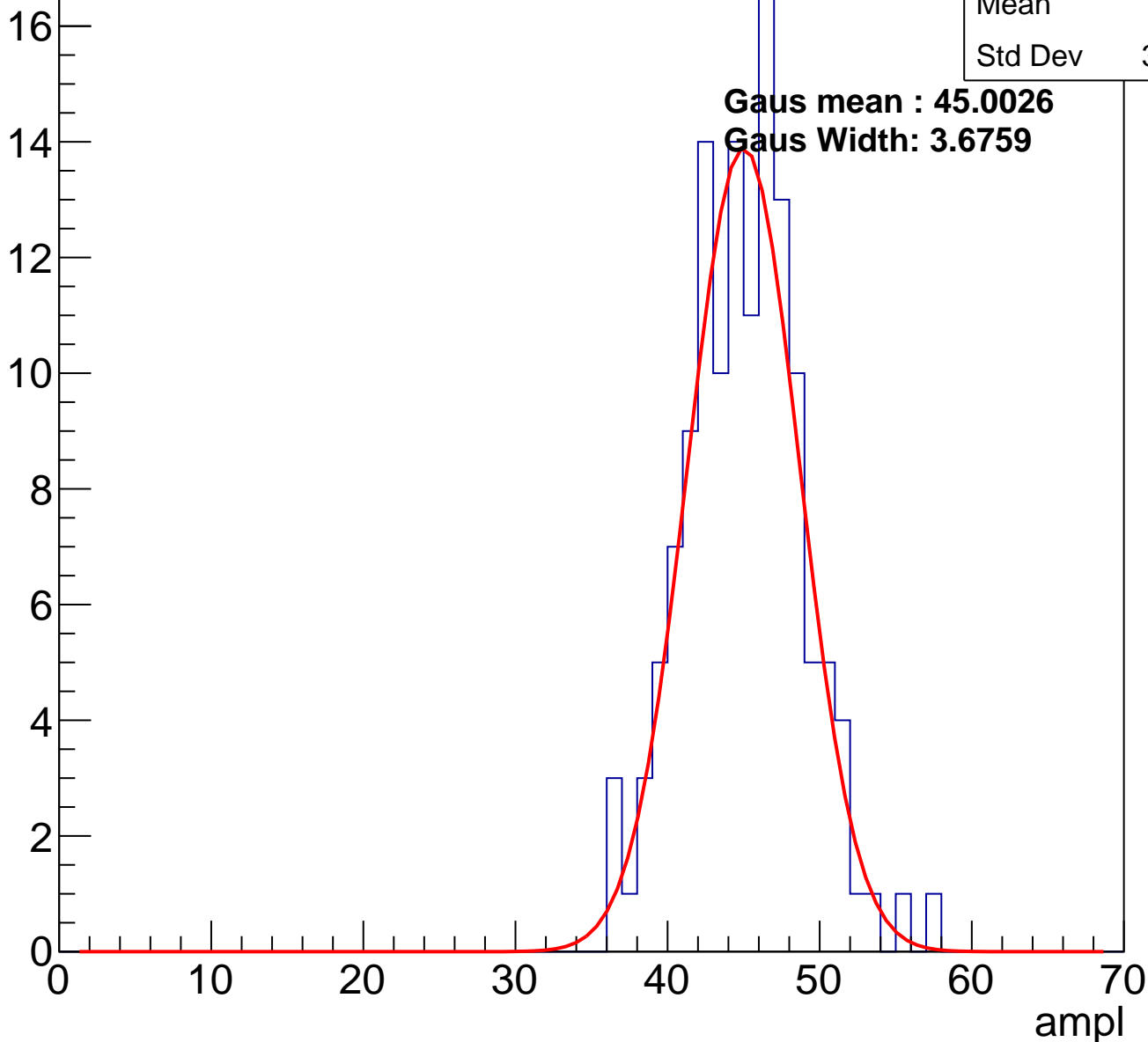
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	135
Mean	44.6
Std Dev	3.826

Gaus mean : 45.0026

Gaus Width: 3.6759

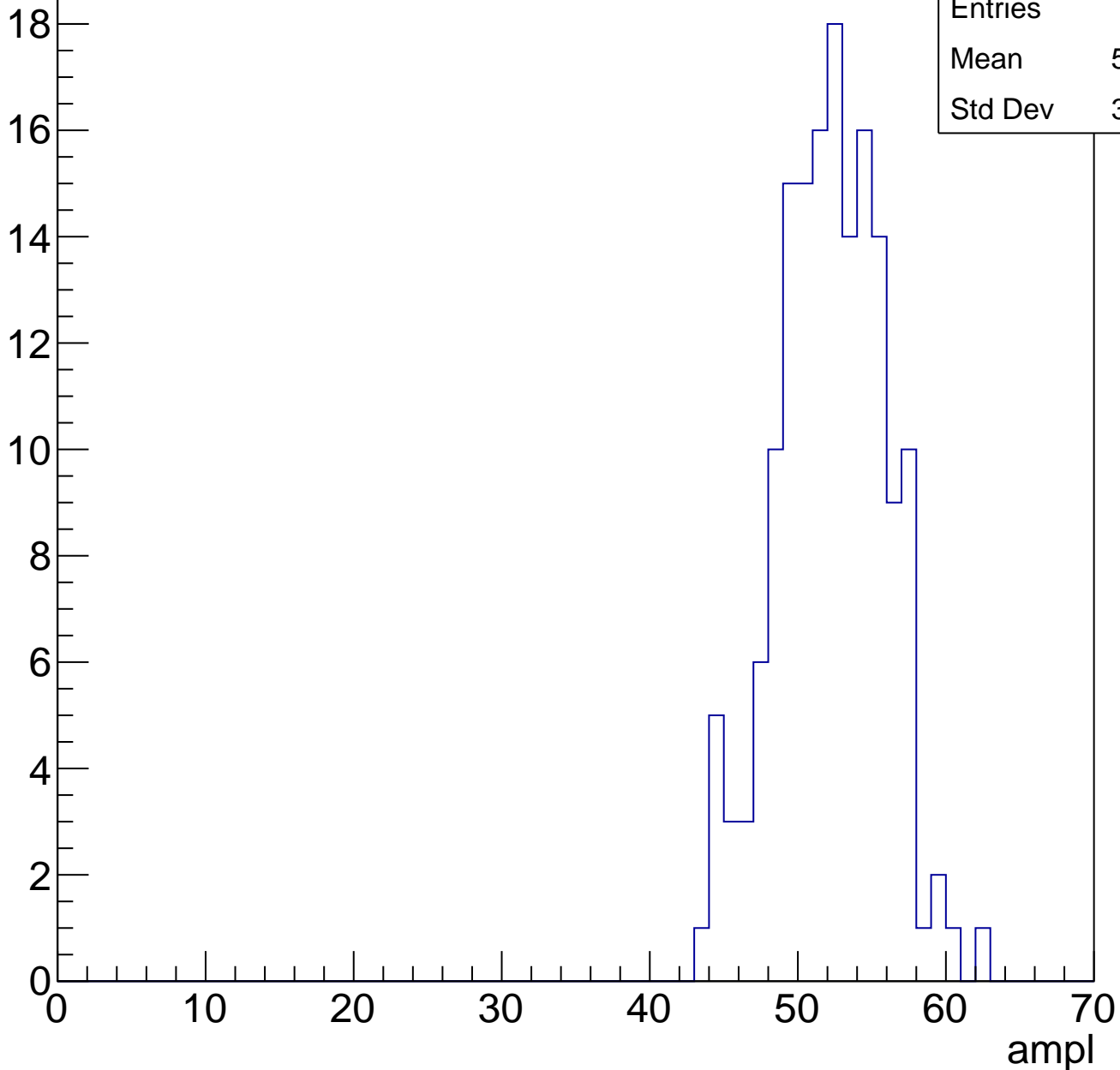


B0L101S, U25-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	51.77
Std Dev	3.592

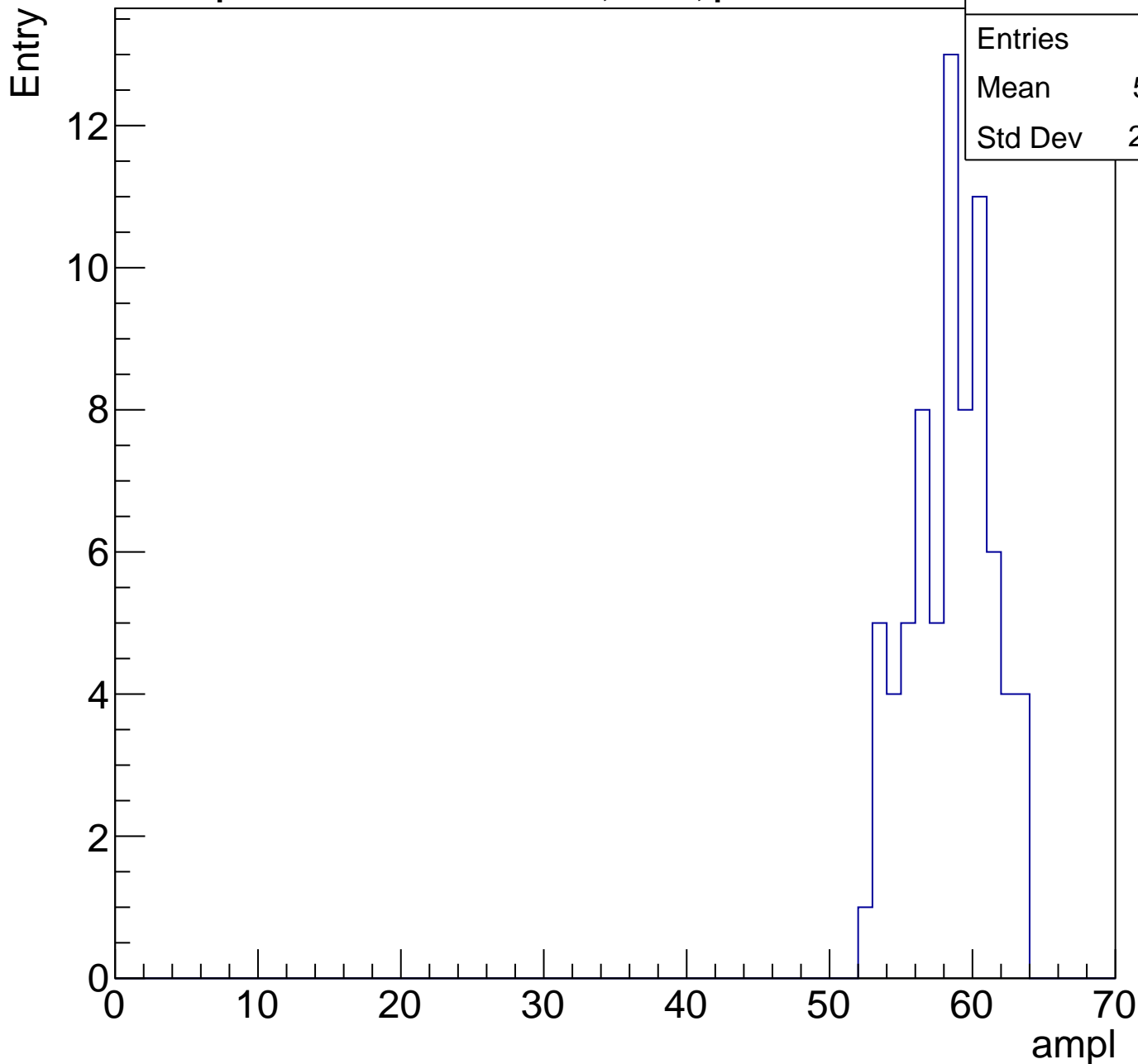
Entry



B0L101S, U25-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	74
Mean	58.01
Std Dev	2.792



B0L101S, U25-ch25, adc5

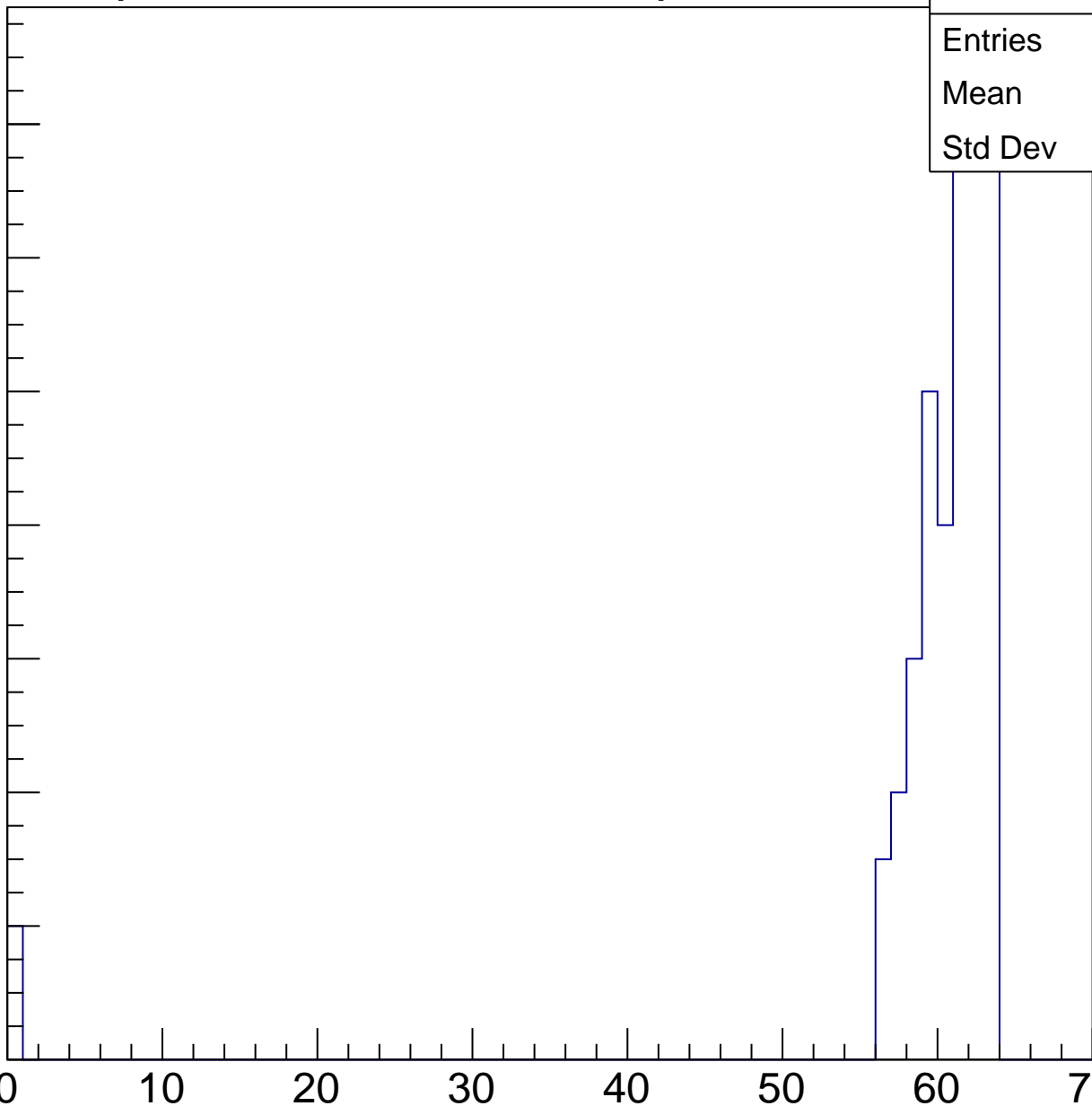
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	58.95
Std Dev	9.89

ampl



B0L101S, U25-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

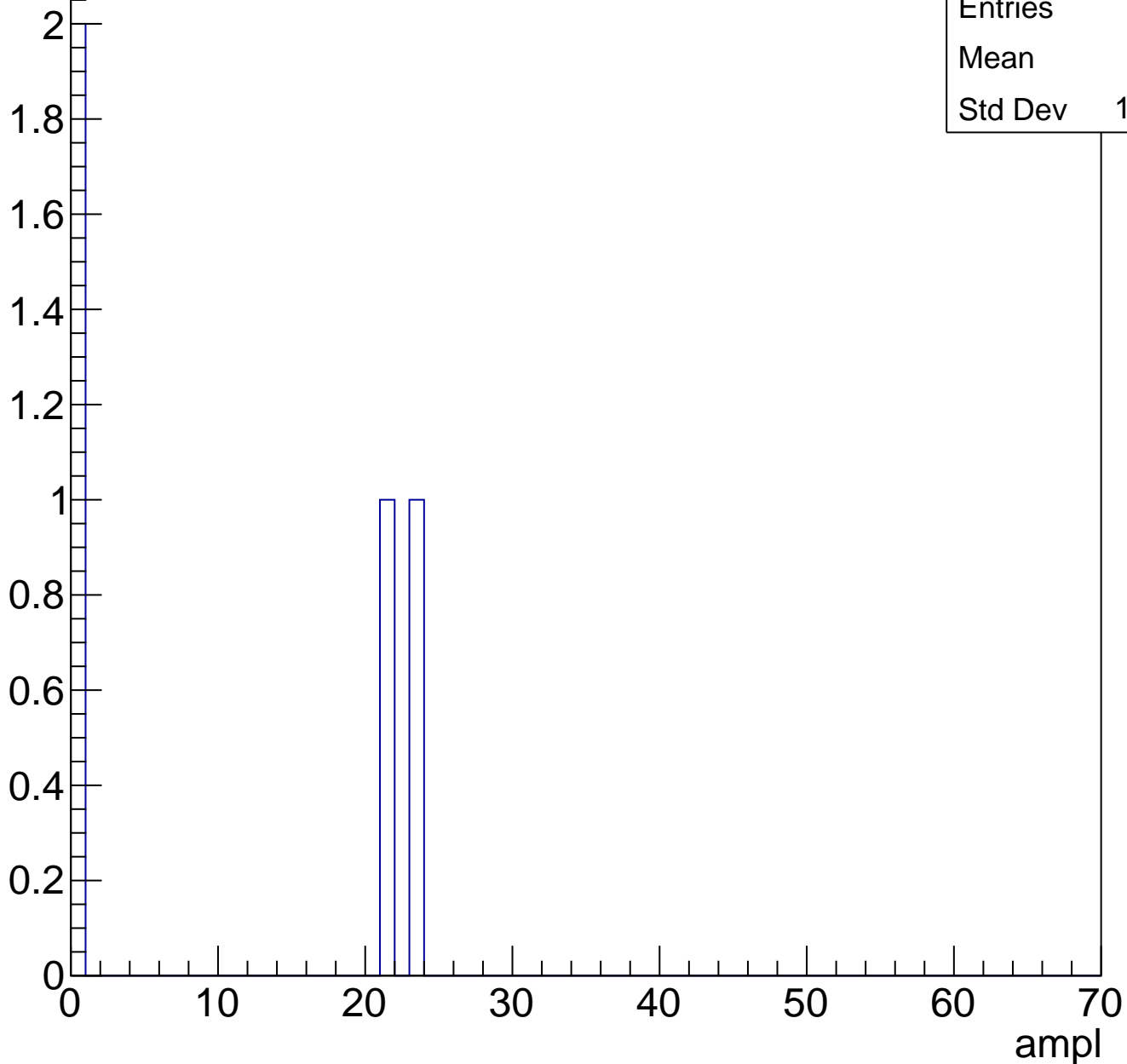
0 10 20 30 40 50 60 70

ampl

B0L101S, U25-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch26, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	30.45
Std Dev	5.162

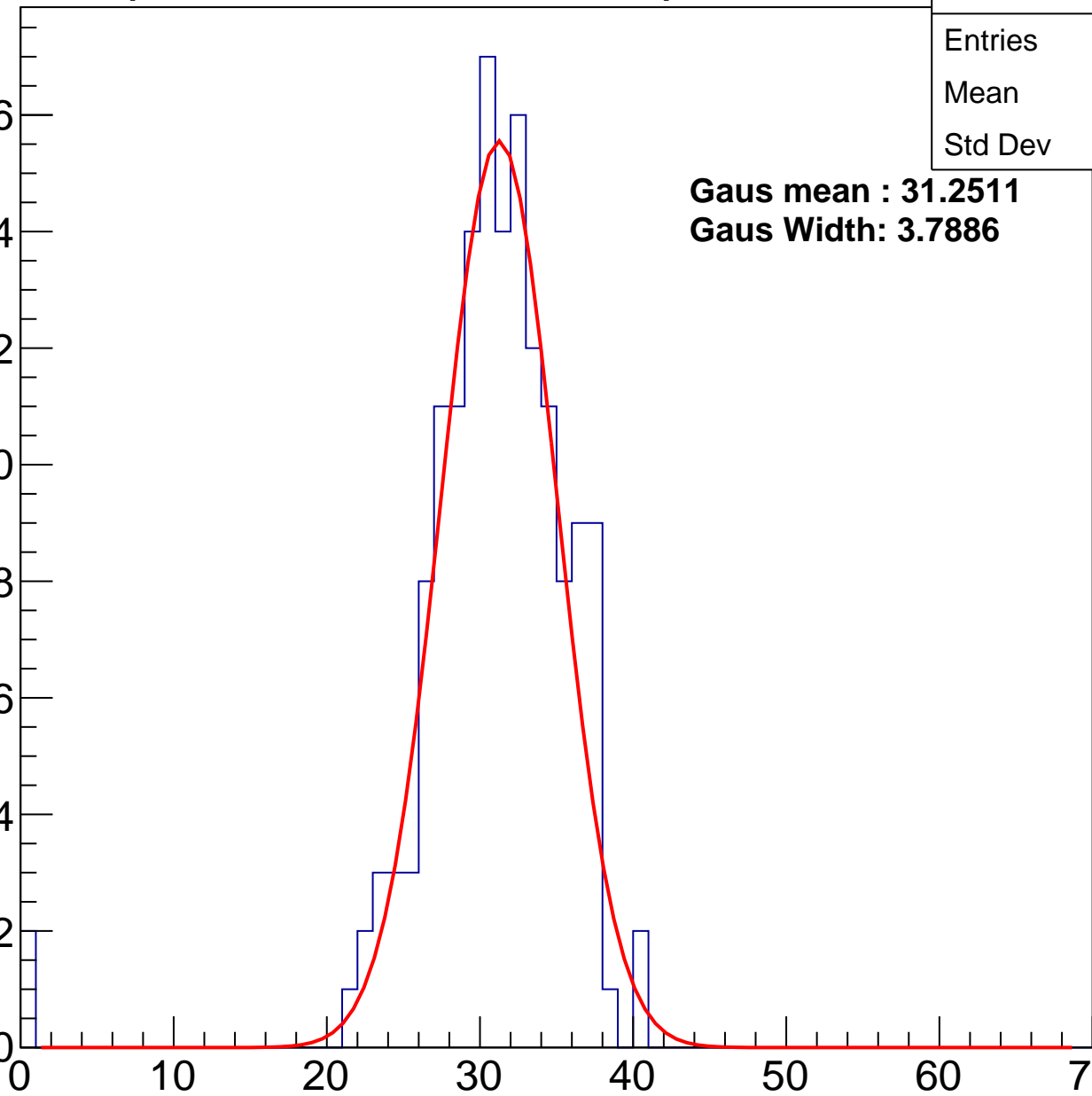
Gaus mean : 31.2511

Gaus Width: 3.7886

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch26, adc1

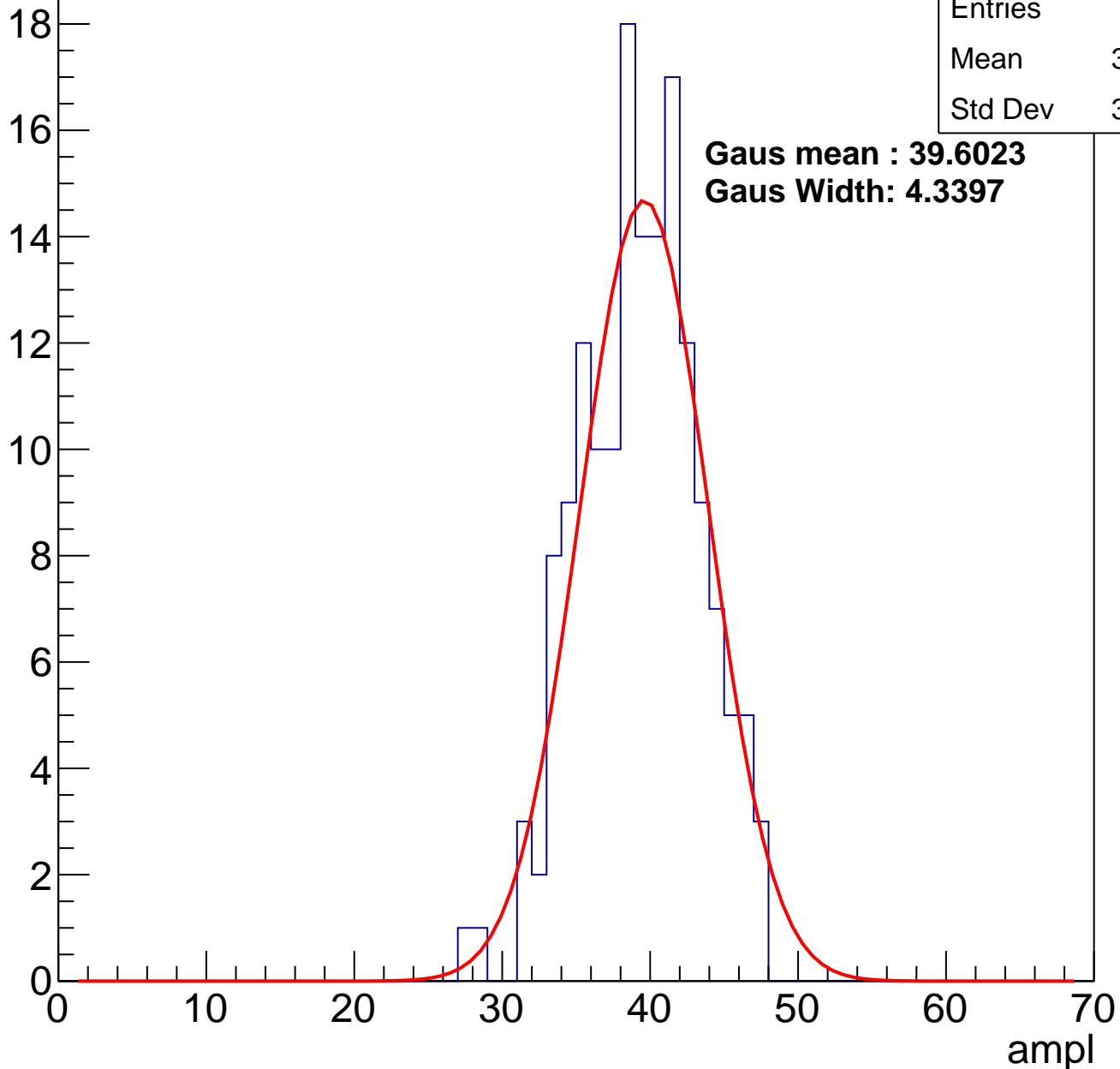
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	38.84
Std Dev	3.976

Gaus mean : 39.6023

Gaus Width: 4.3397

Entry



B0L101S, U25-ch26, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	45.81
Std Dev	3.844

Gaus mean : 46.5280

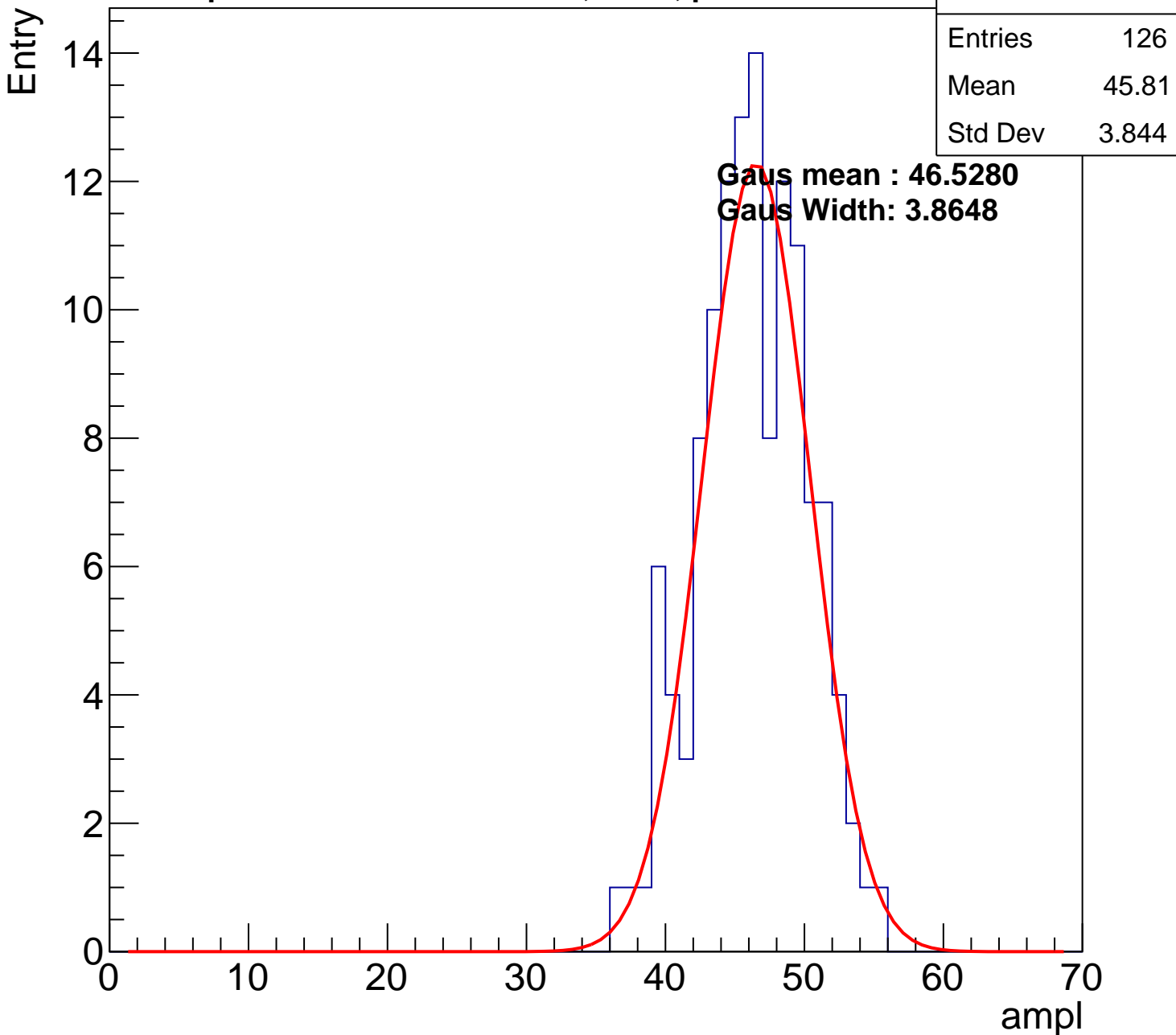
Gaus Width: 3.8648

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

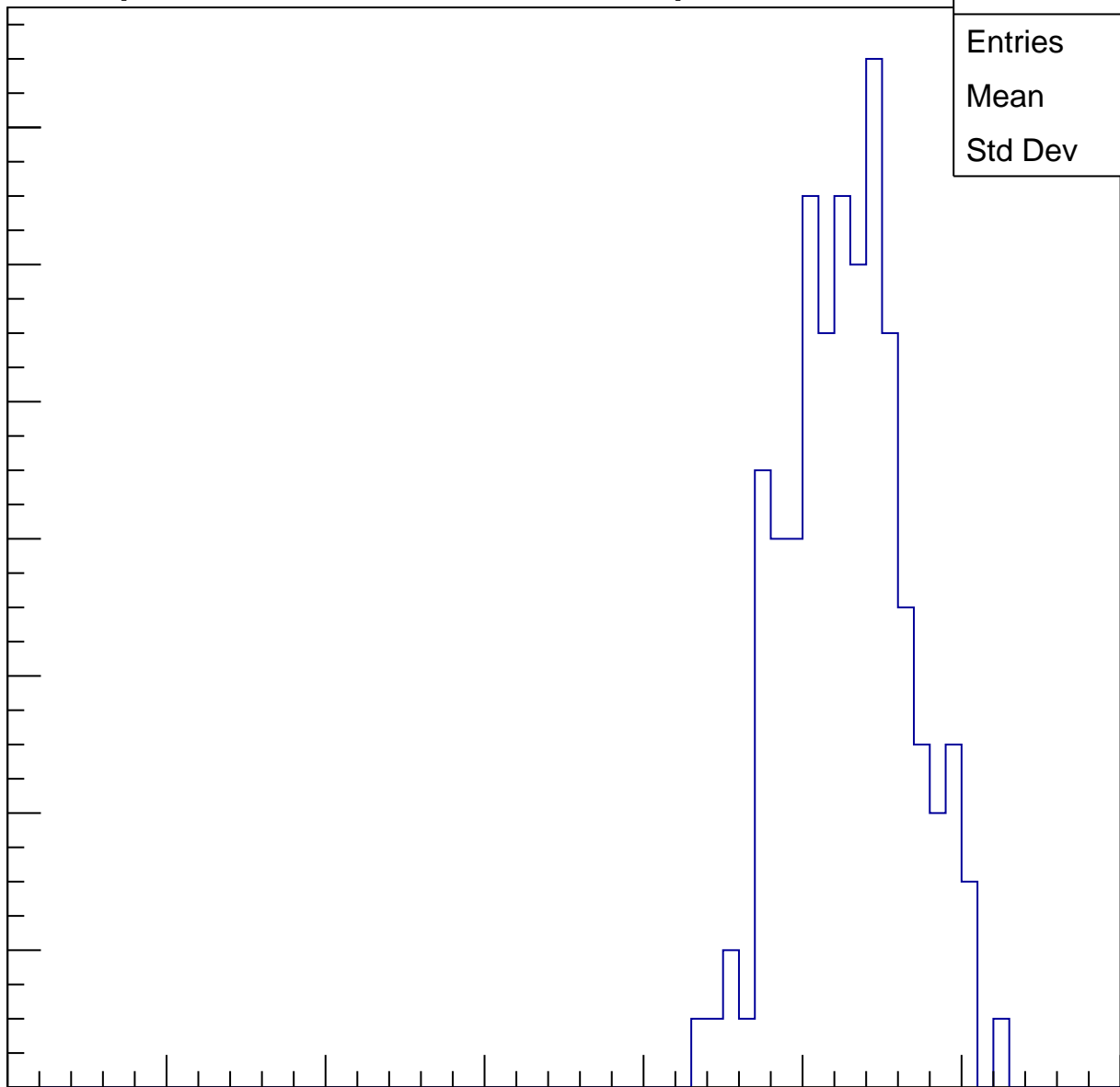
Entries	130
Mean	52.35
Std Dev	3.762

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch26, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

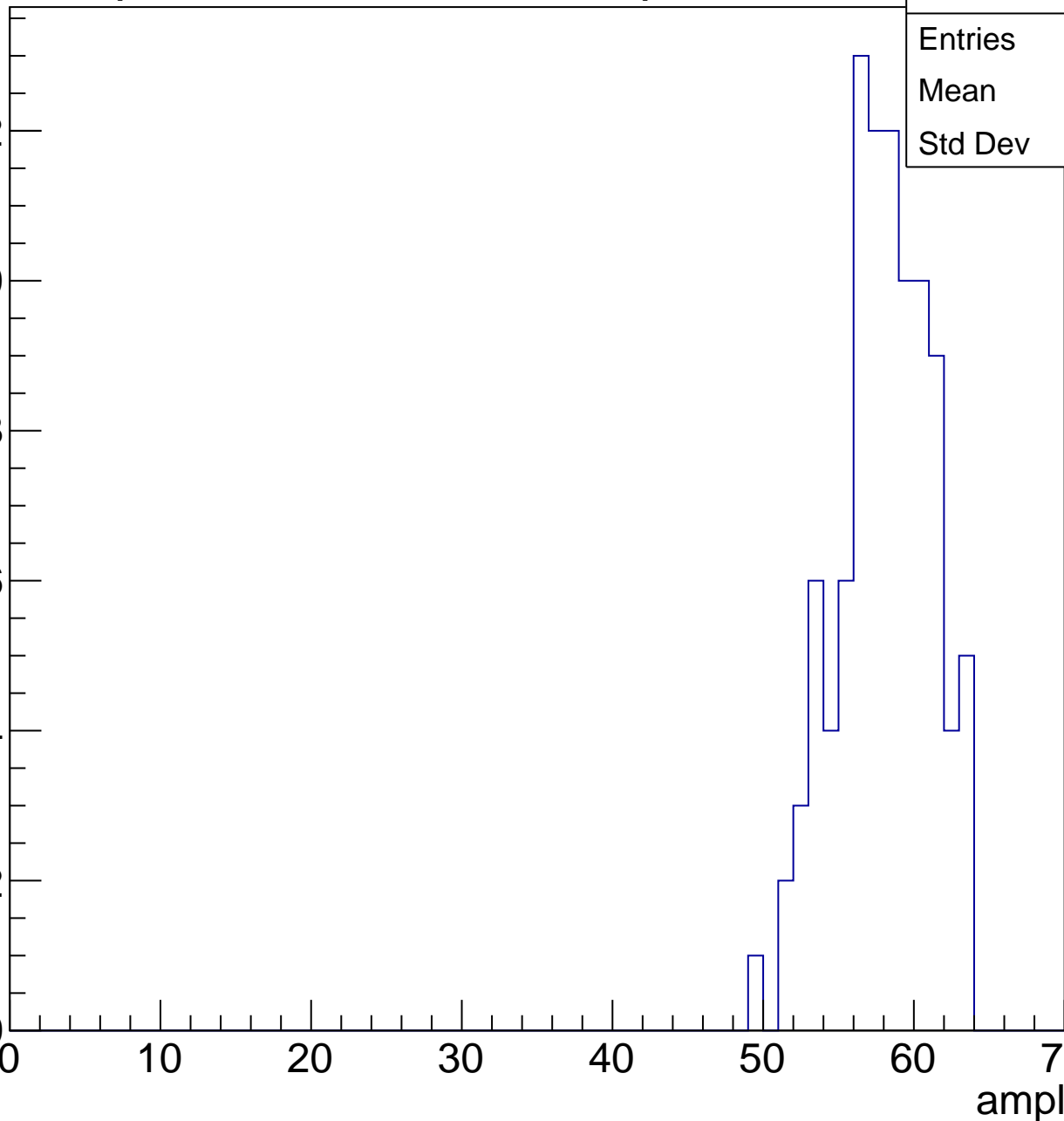
50

60

70

ampl

Entries	97
Mean	57.54
Std Dev	3.077

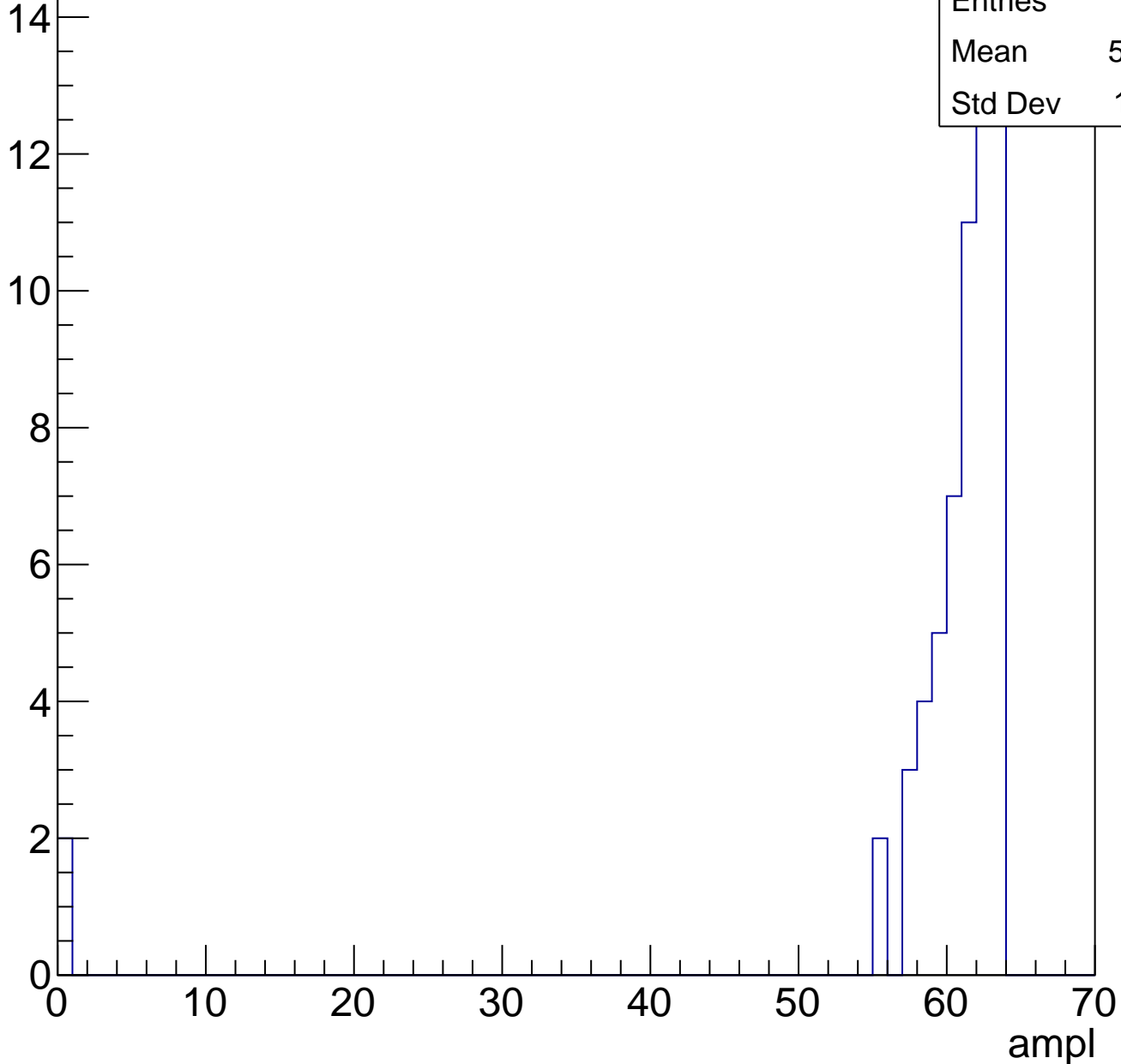


B0L101S, U25-ch26, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	61
Mean	58.79
Std Dev	11.01

Entry



B0L101S, U25-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch27, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	29.89
Std Dev	4.08

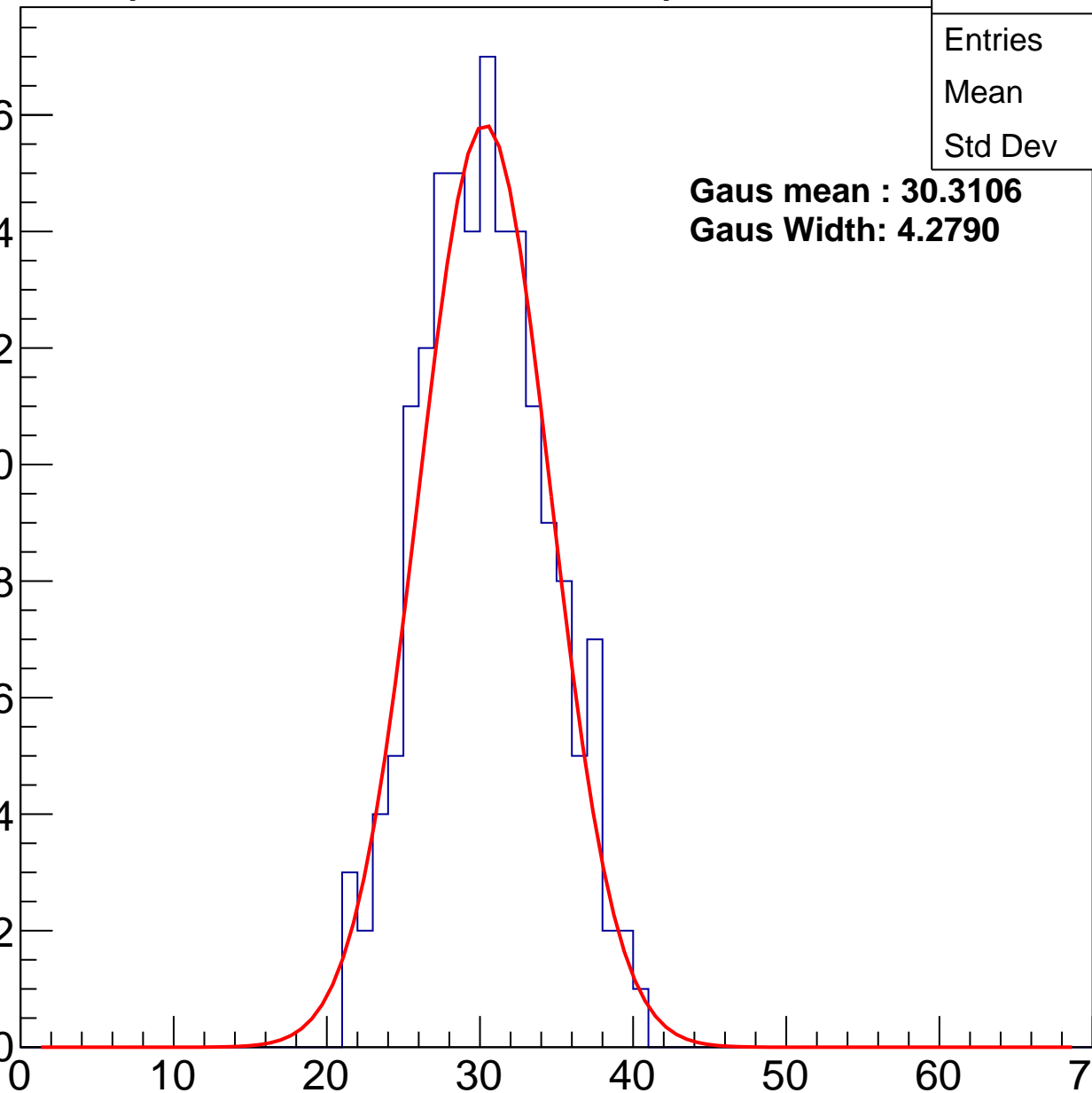
Gaus mean : 30.3106

Gaus Width: 4.2790

Entry

16
14
12
10
8
6
4
2
0

ampl



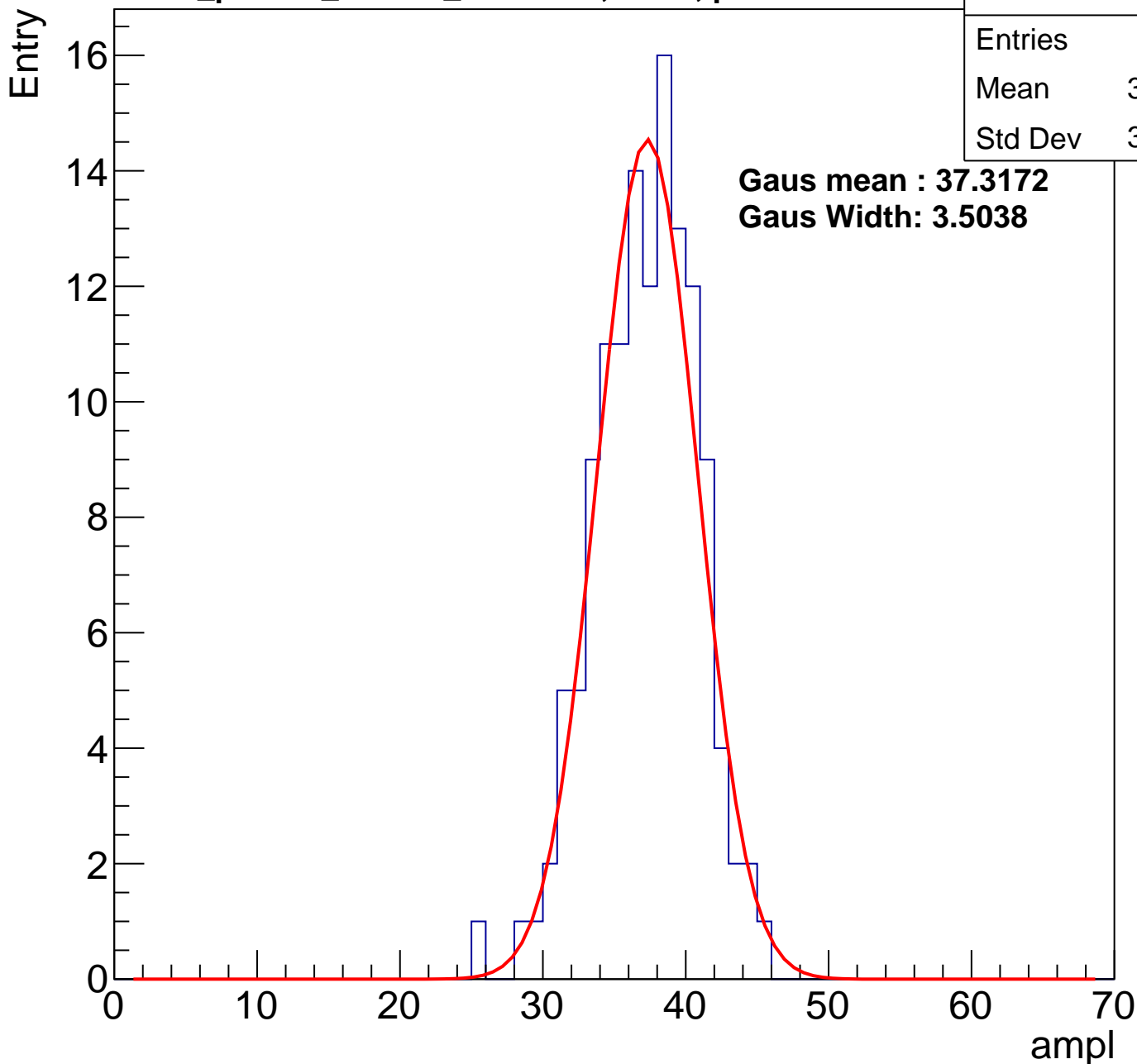
B0L101S, U25-ch27, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.73
Std Dev	3.536

Gaus mean : 37.3172

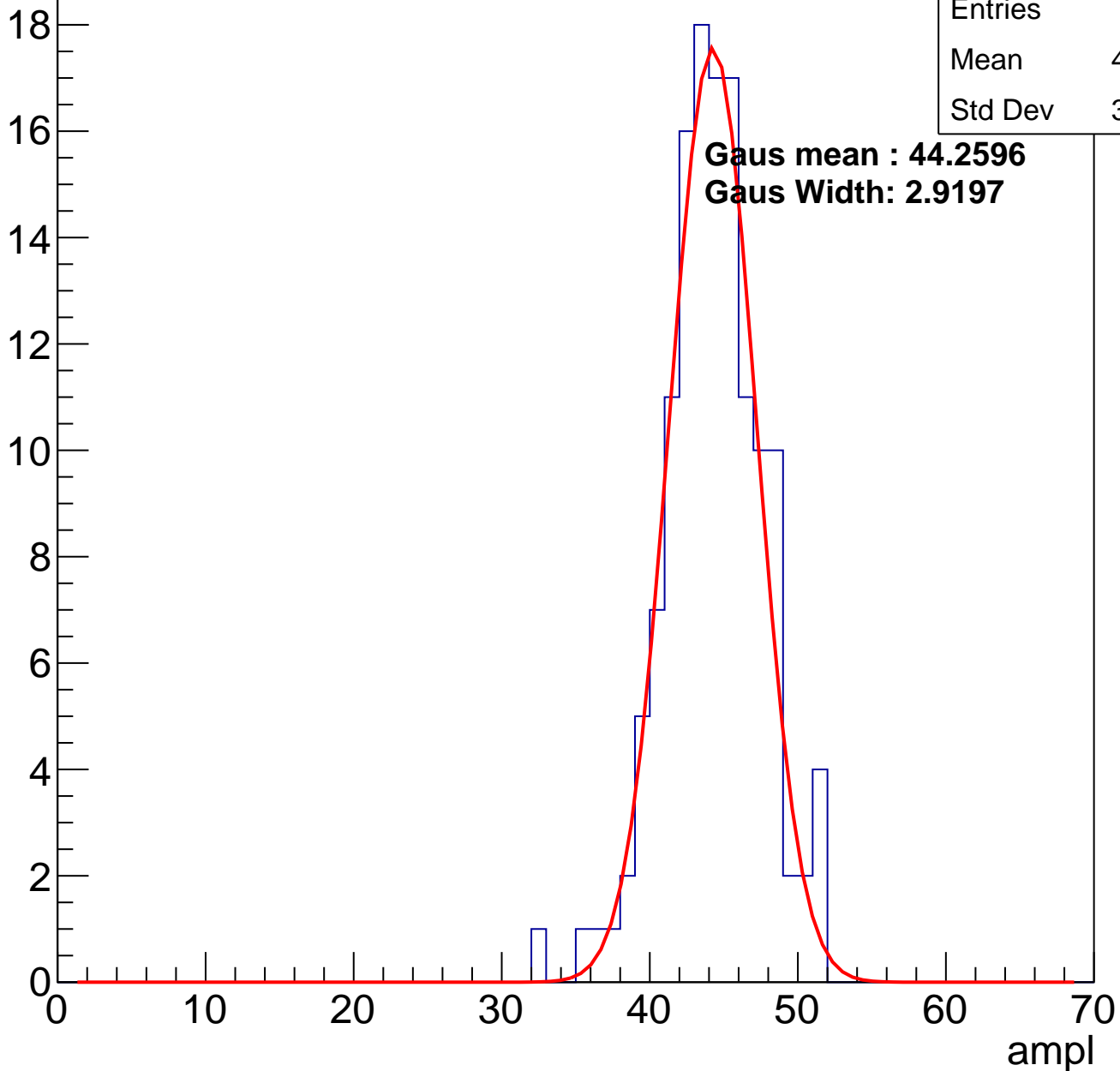
Gaus Width: 3.5038



B0L101S, U25-ch27, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

142

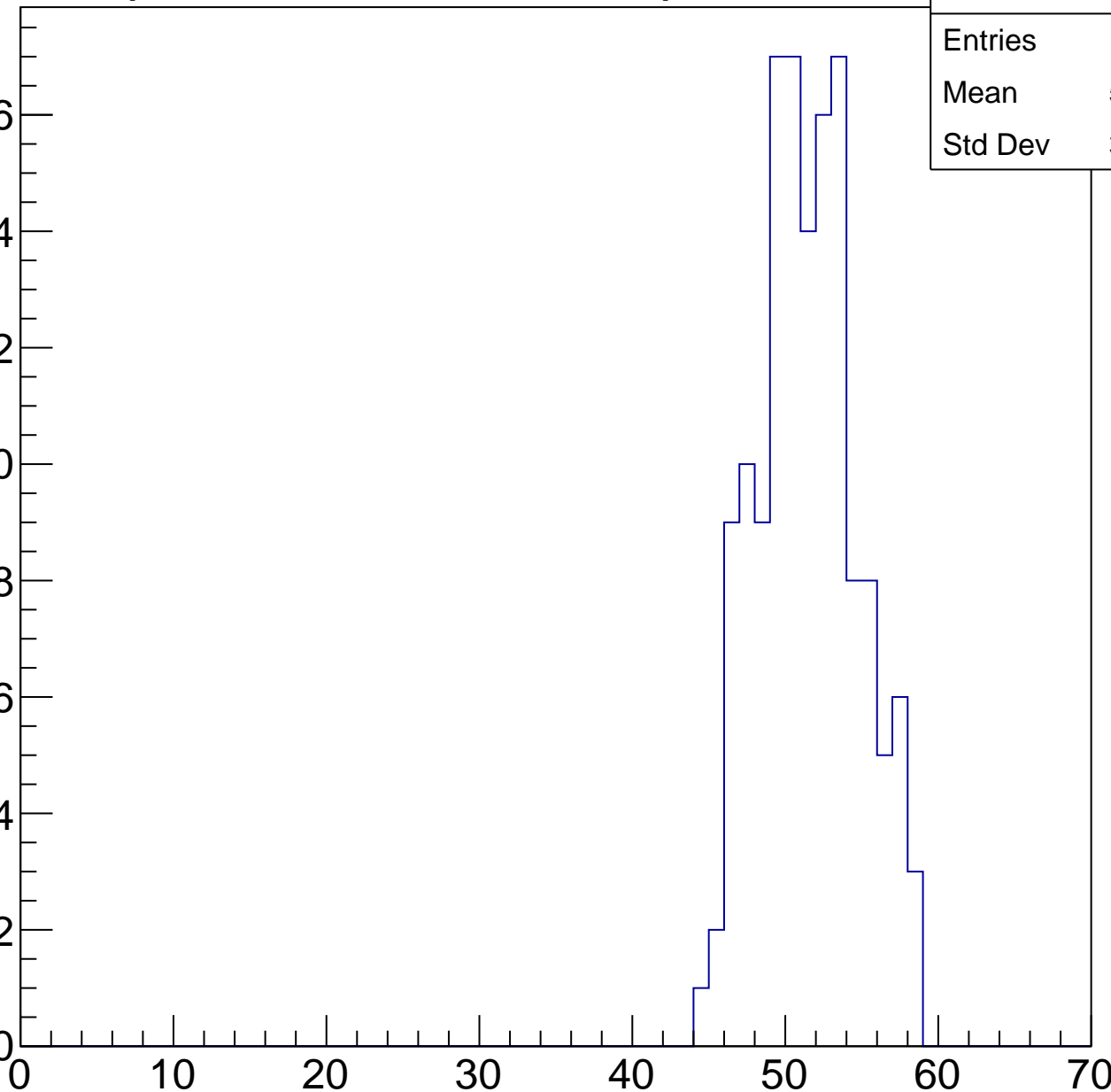
Mean

51.04

Std Dev

3.189

ampl

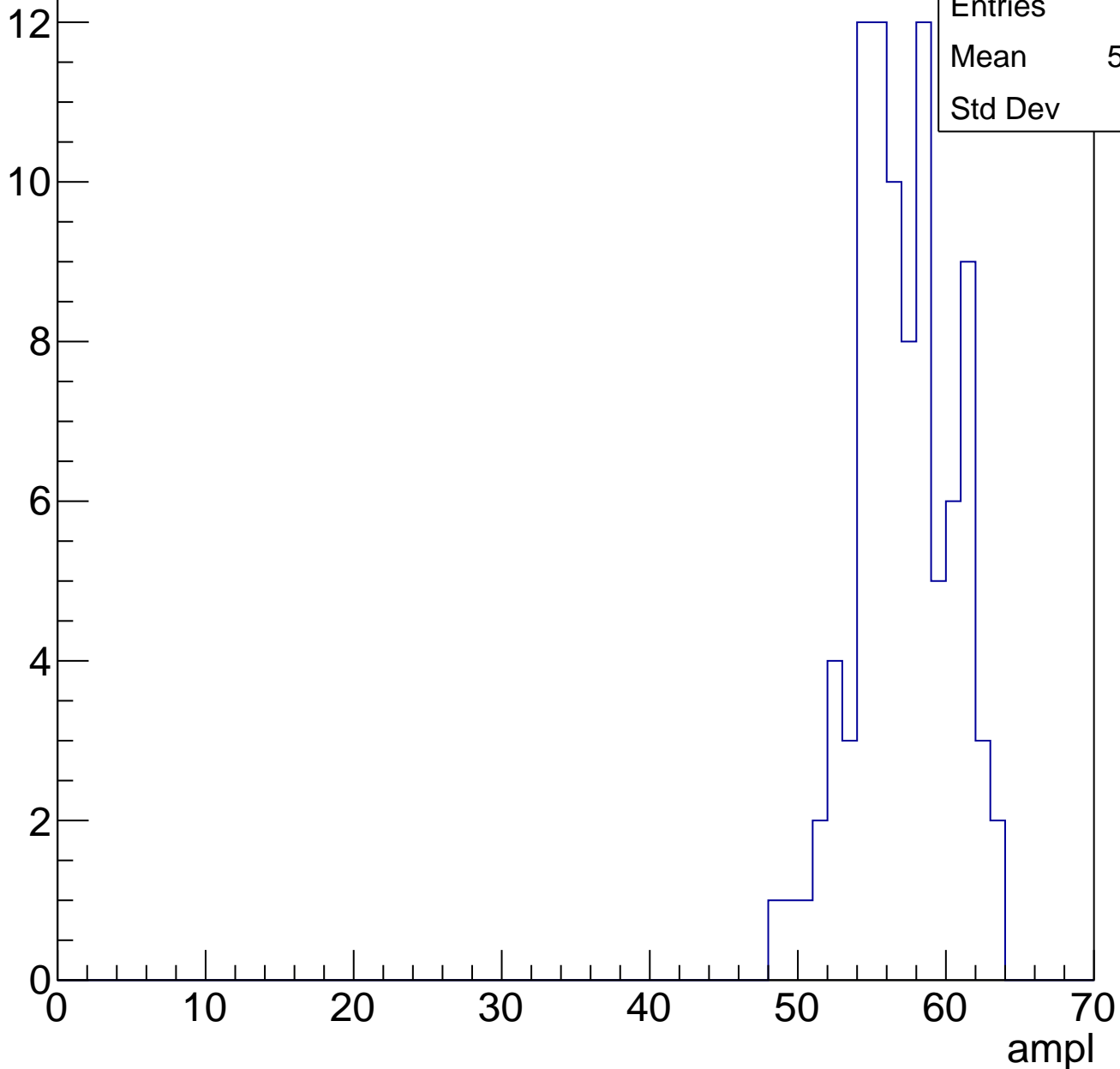


B0L101S, U25-ch27, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	91
Mean	56.62
Std Dev	3.22

Entry



B0L101S, U25-ch27, adc5

calib_packv5_042523_0143.root, FC#1, port C1

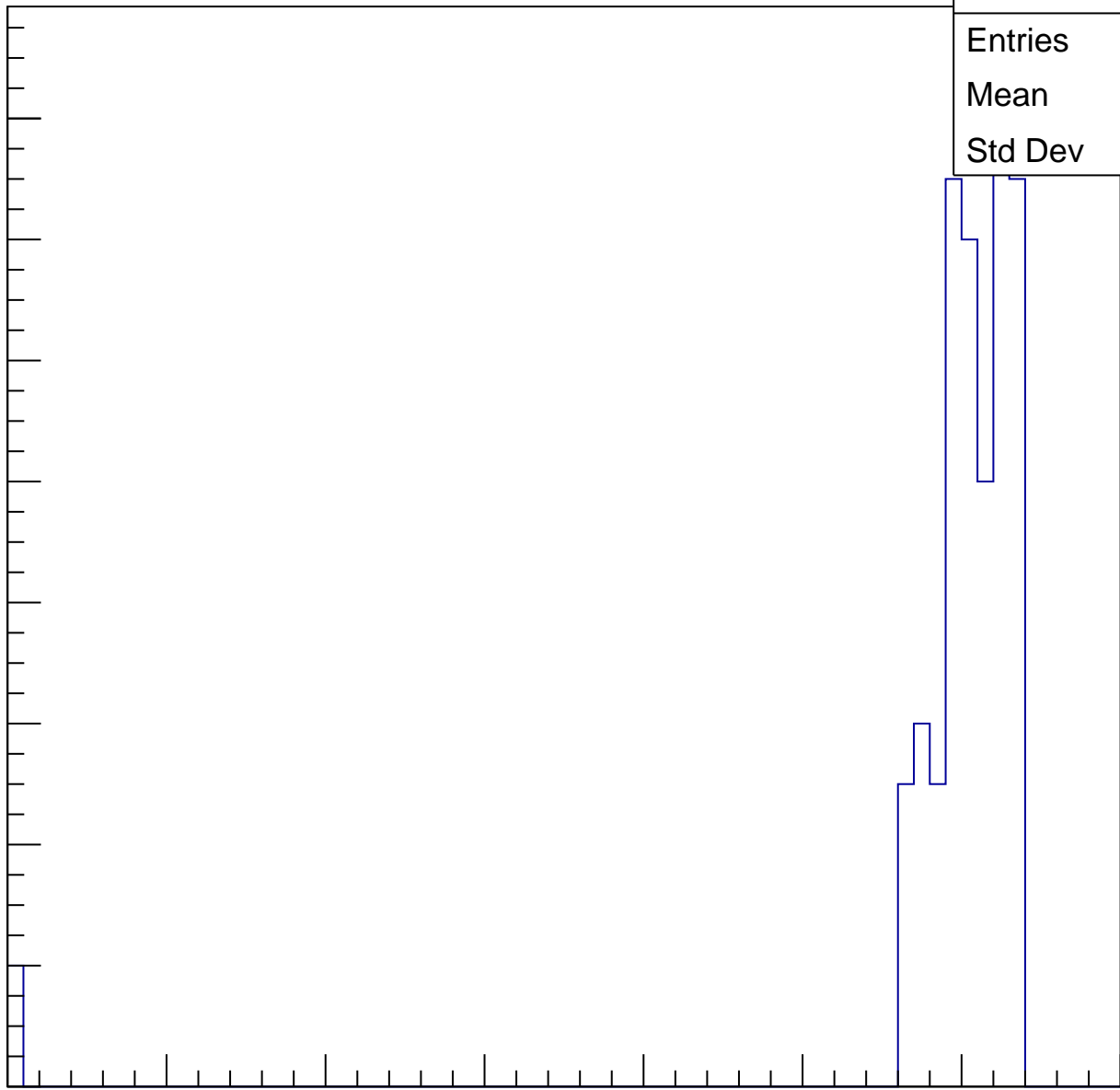
Entries	89
Mean	58.94
Std Dev	9.169

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

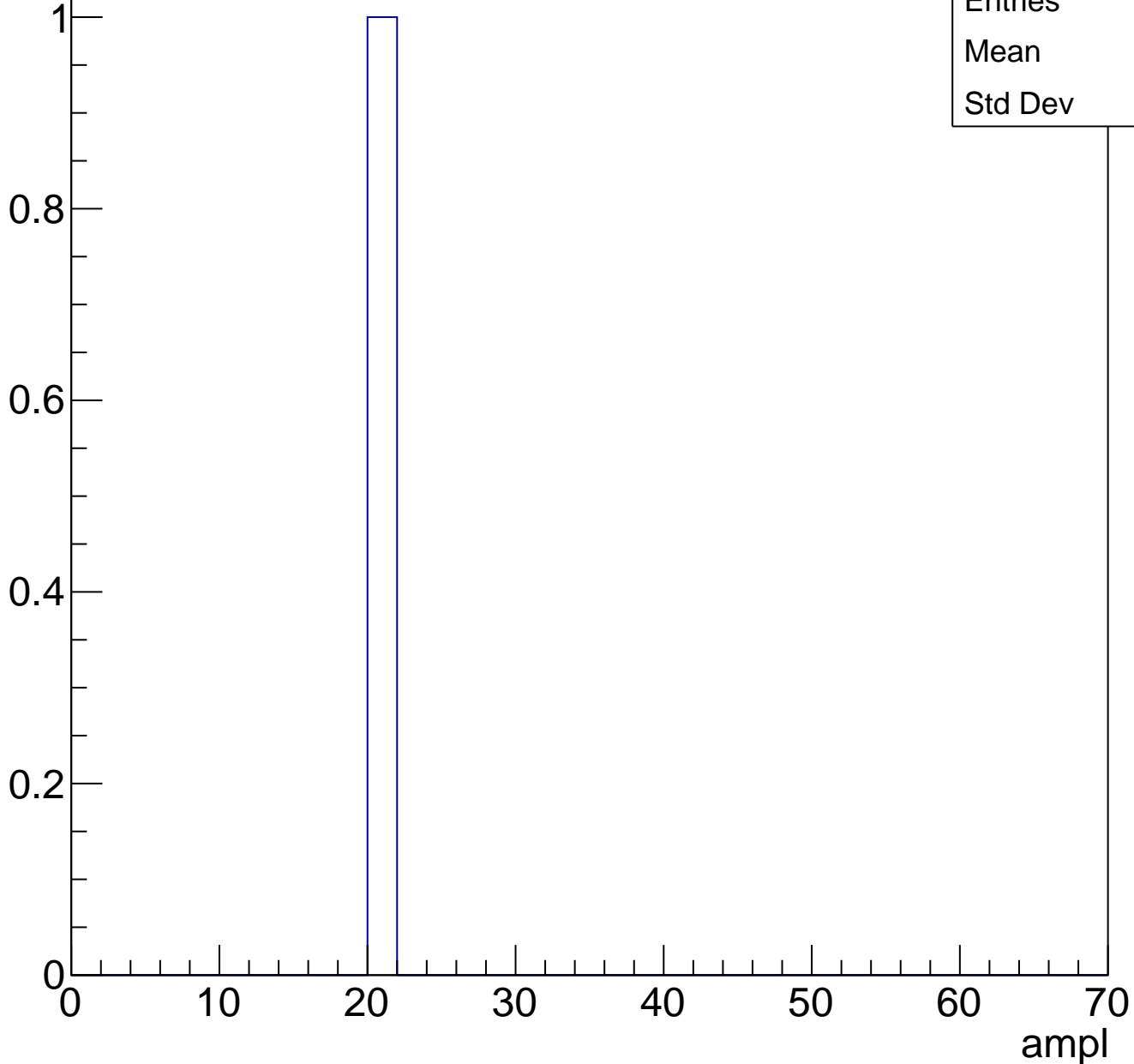
Entry



B0L101S, U25-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch28, adc0

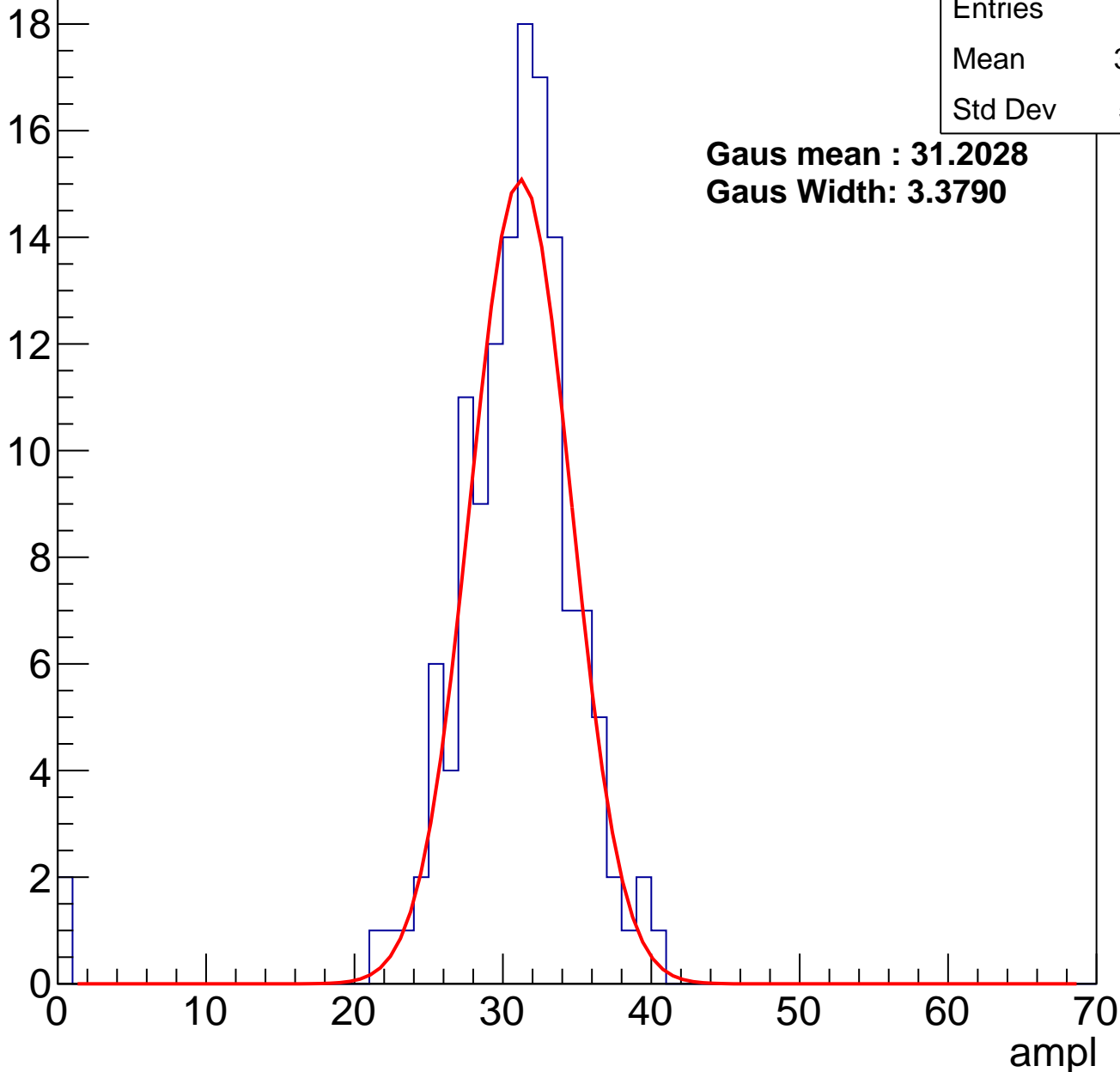
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	30.23
Std Dev	5.071

Gaus mean : 31.2028

Gaus Width: 3.3790

Entry



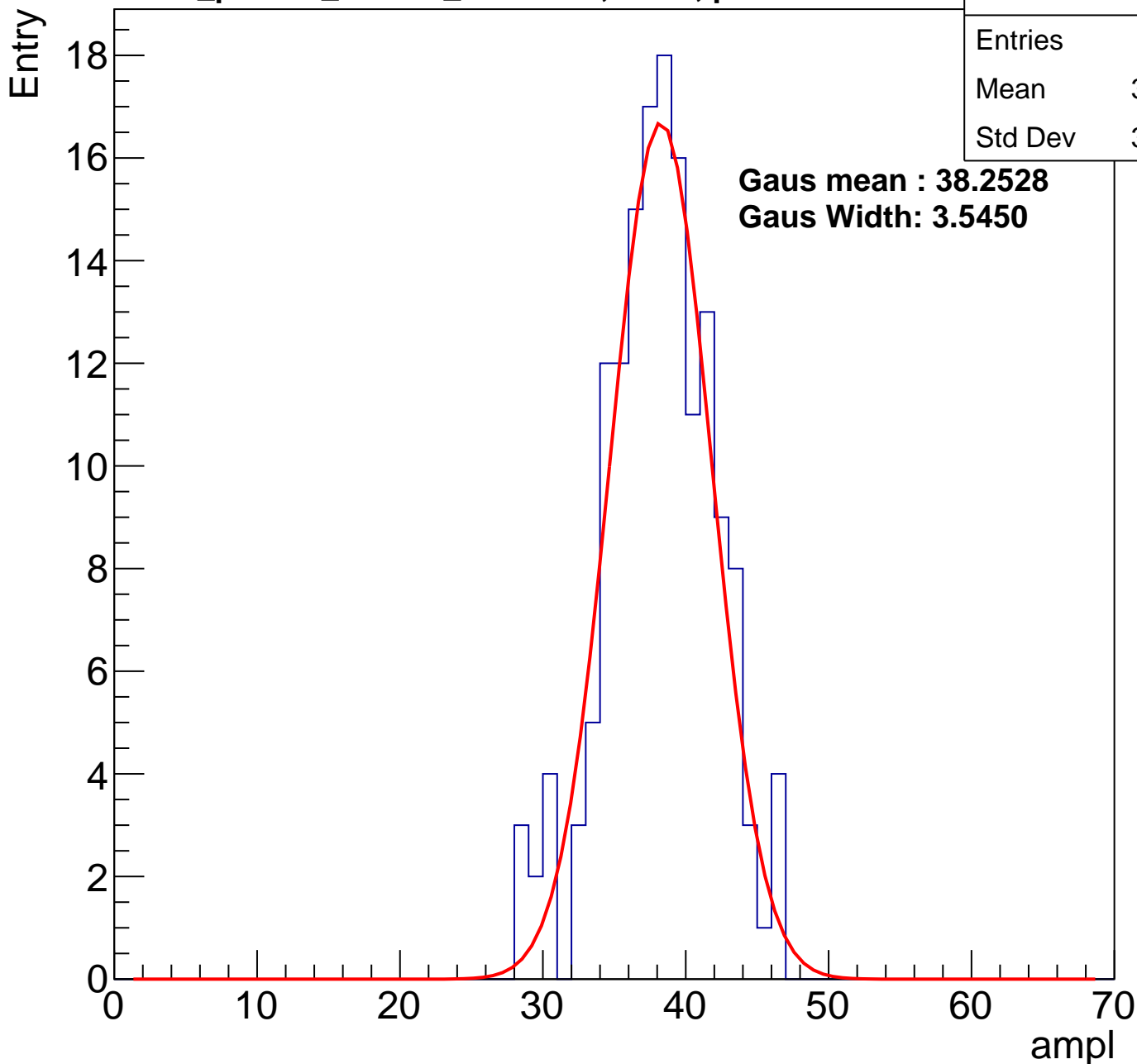
B0L101S, U25-ch28, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	37.72
Std Dev	3.797

Gaus mean : 38.2528

Gaus Width: 3.5450



B0L101S, U25-ch28, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	45.05
Std Dev	3.207

Gaus mean : 45.4418

Gaus Width: 3.1587

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

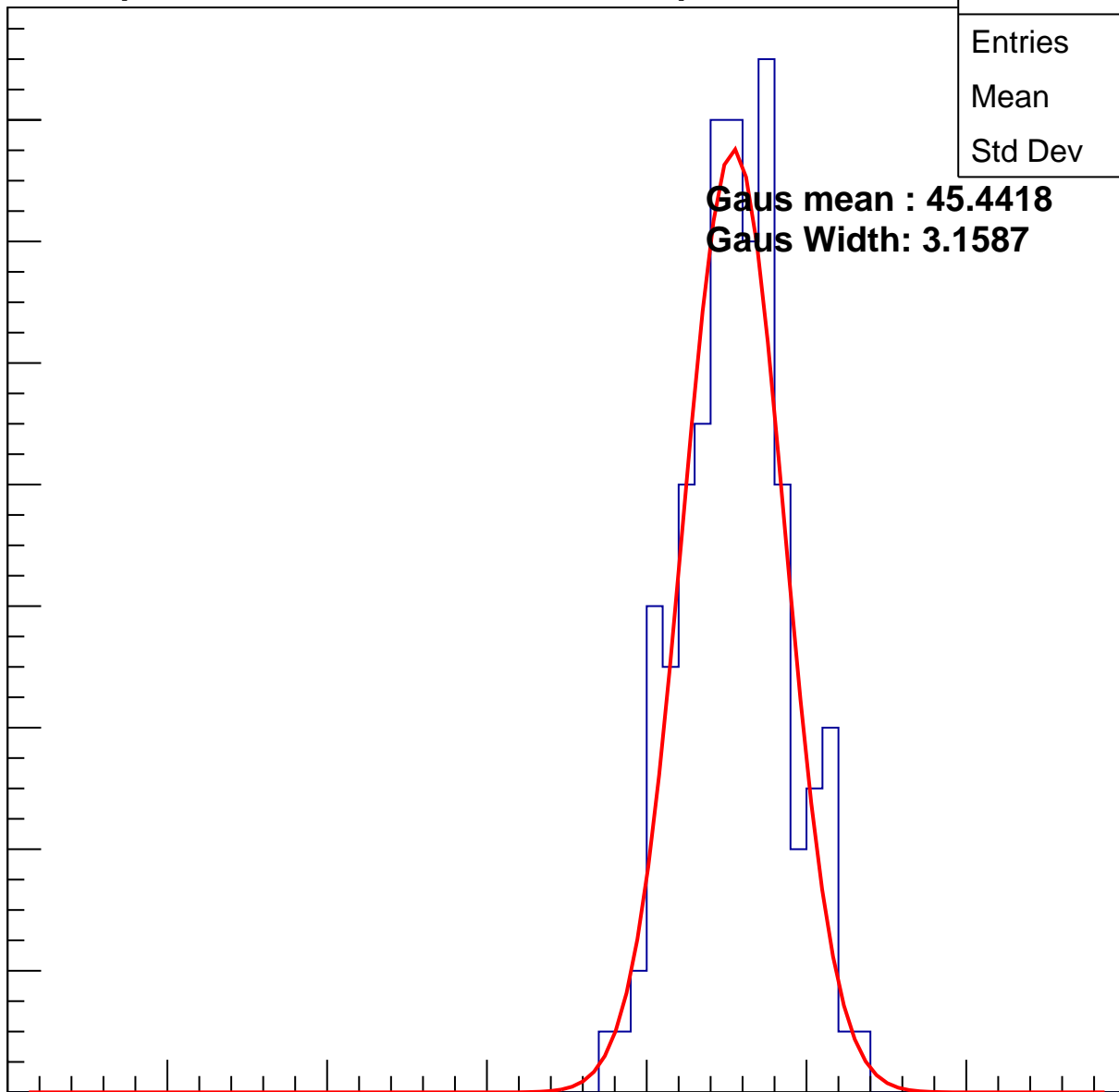
40

50

60

70

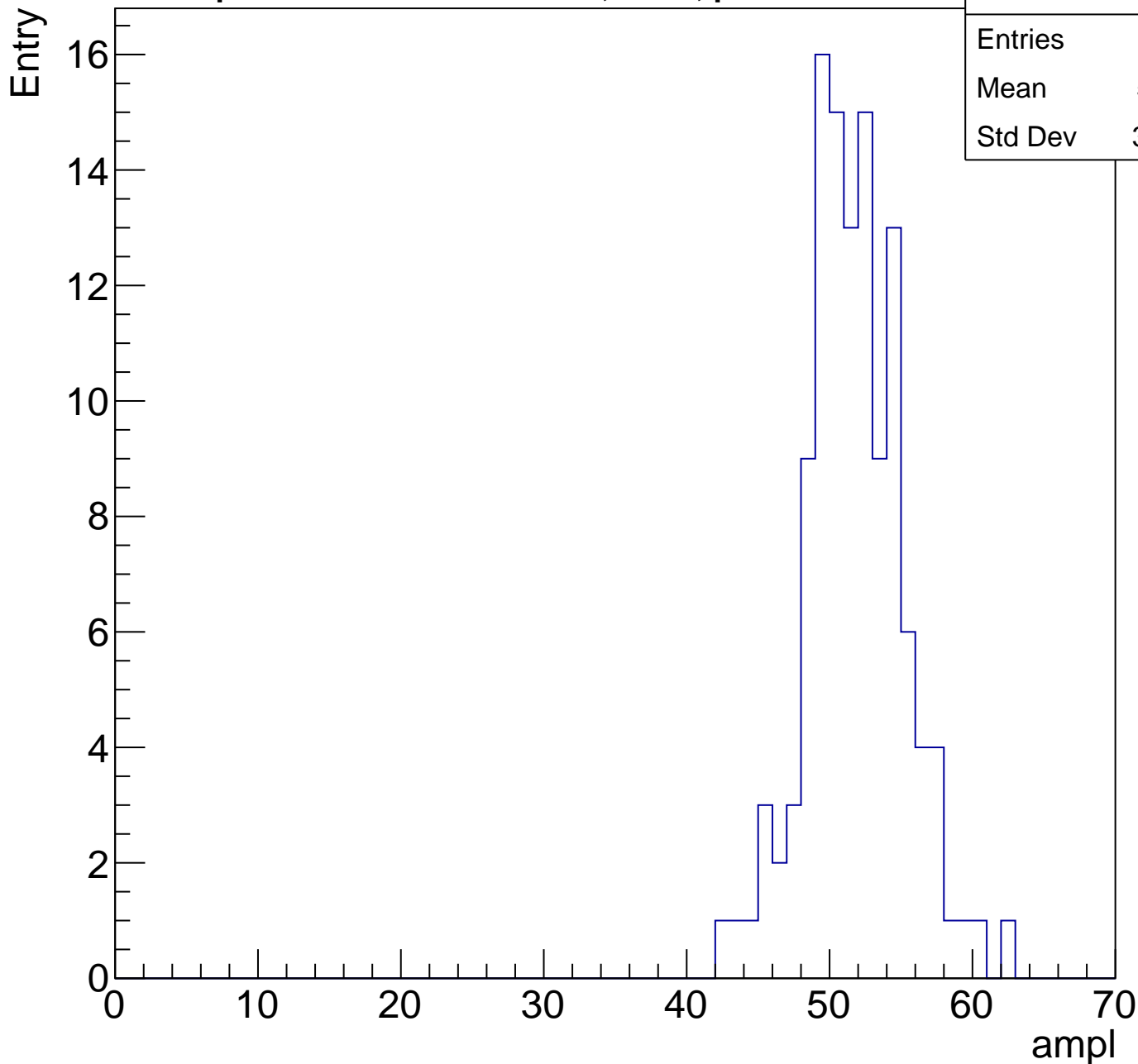
ampl



B0L101S, U25-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

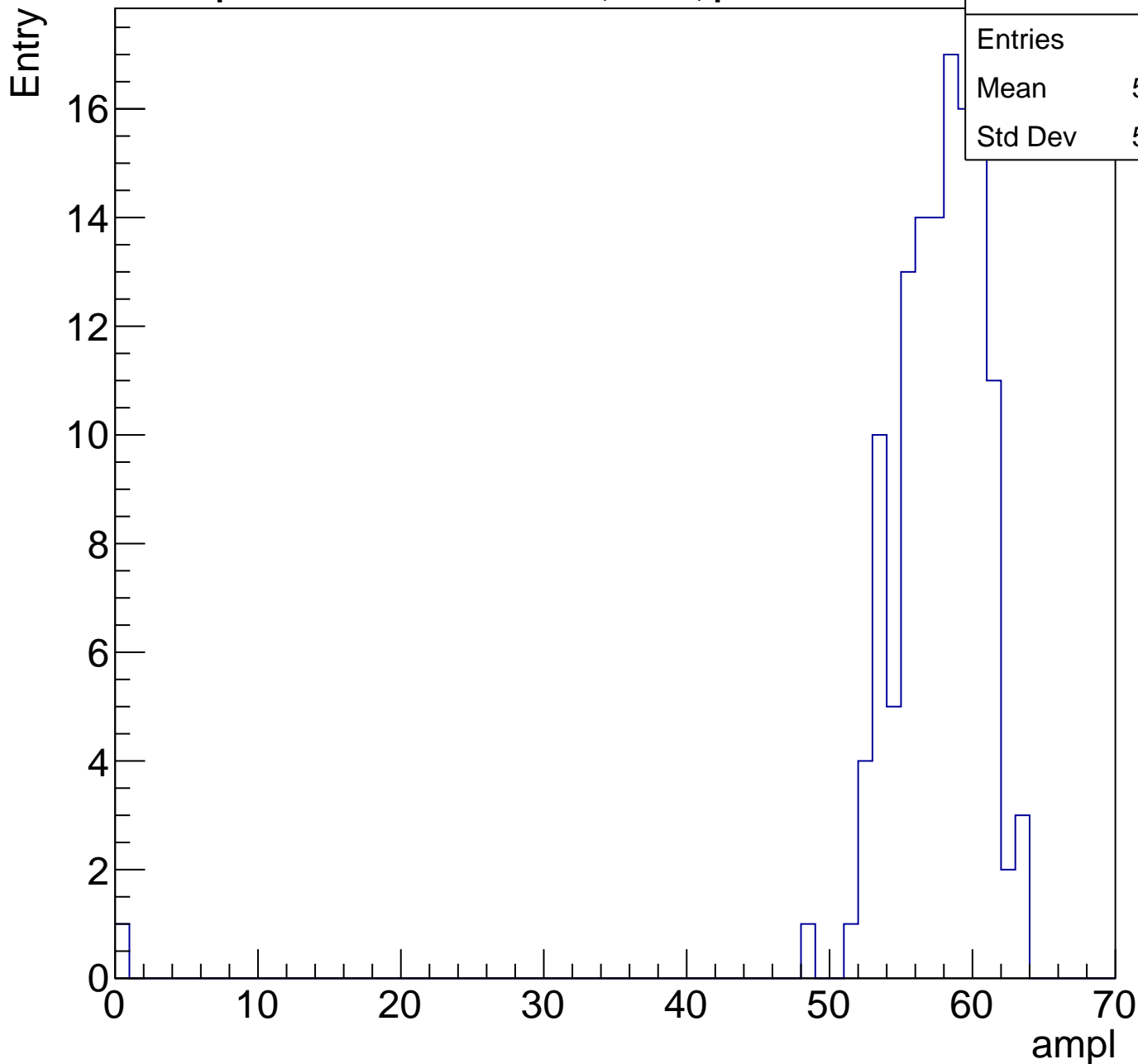
Entries	119
Mean	51.31
Std Dev	3.395



B0L101S, U25-ch28, adc4

calib_packv5_042523_0143.root, FC#1, port C1

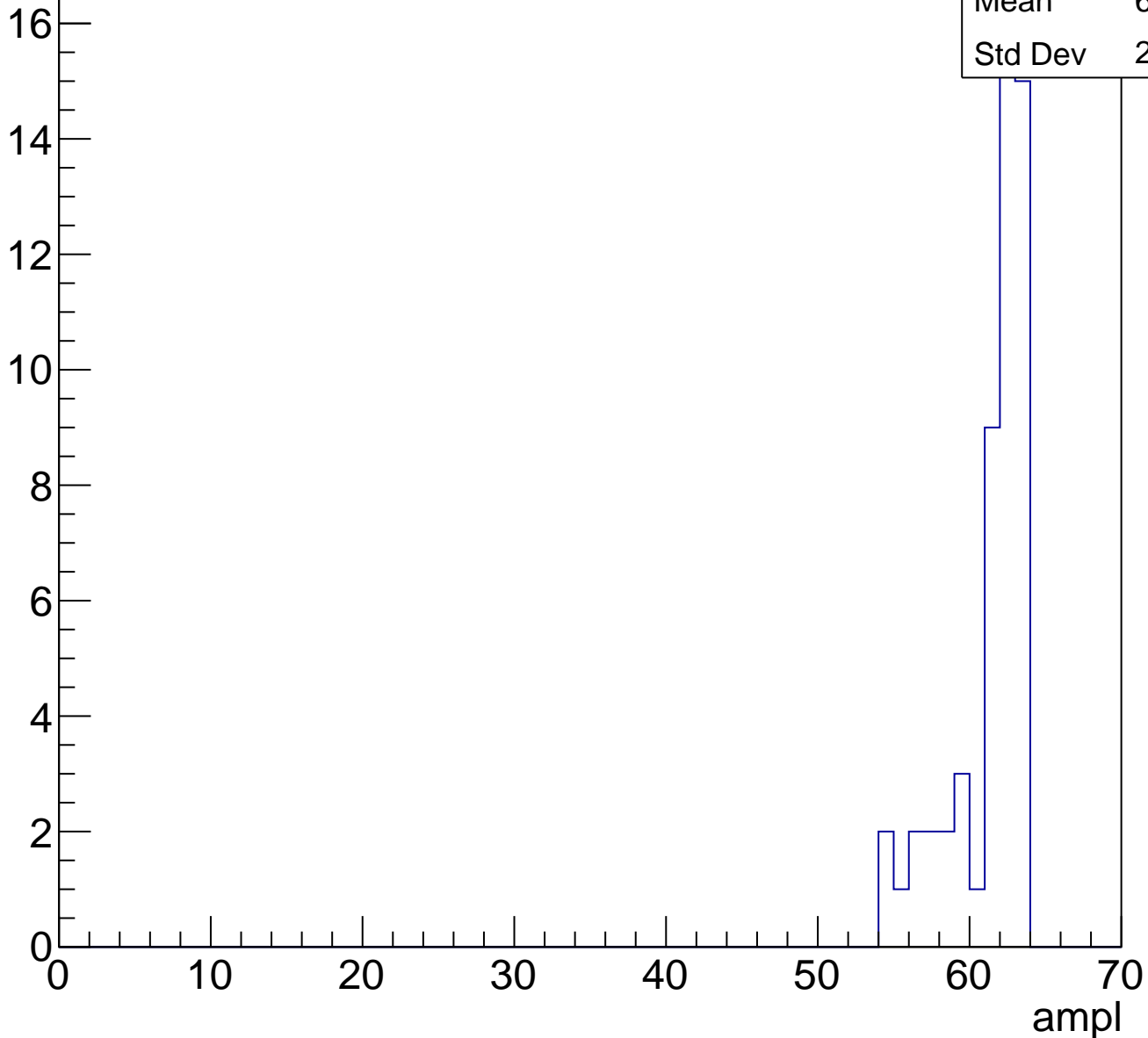
Entries	129
Mean	56.88
Std Dev	5.776



B0L101S, U25-ch28, adc5

calib_packv5_042523_0143.root, FC#1, port C1

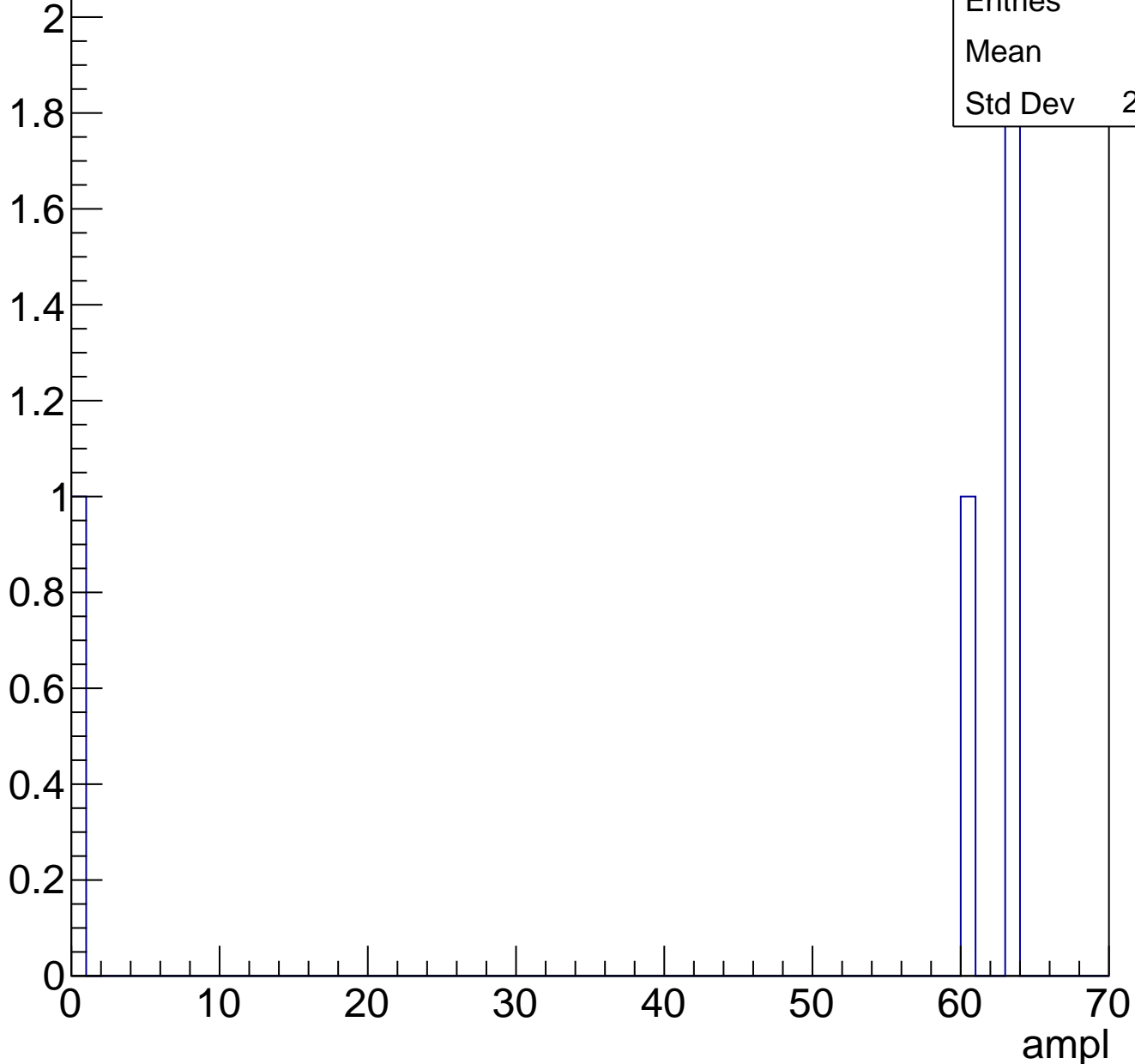
Entry



B0L101S, U25-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

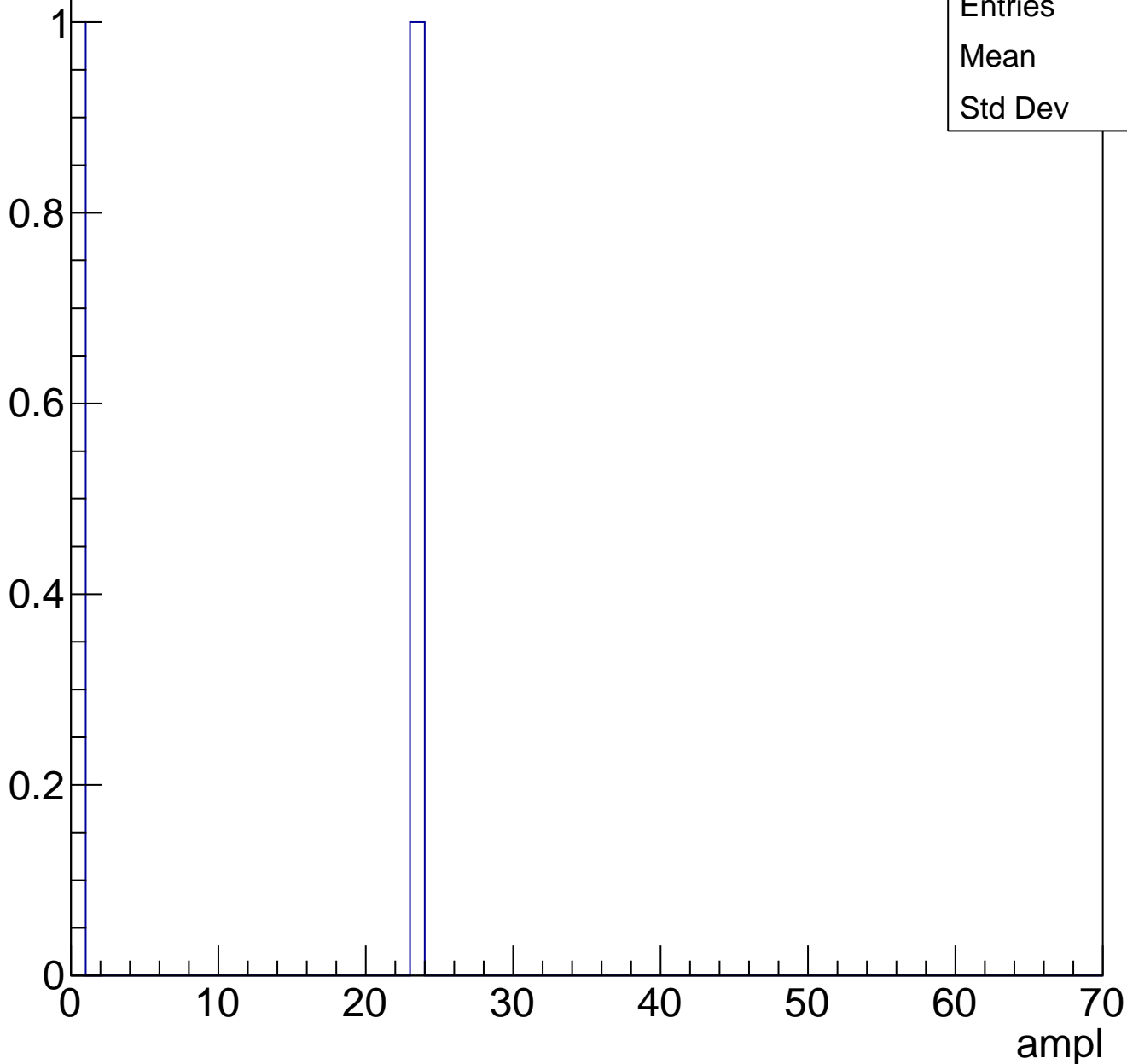
Entry



B0L101S, U25-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch29, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	29.64
Std Dev	5.463

Gaus mean : 31.0825

Gaus Width: 3.3670

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

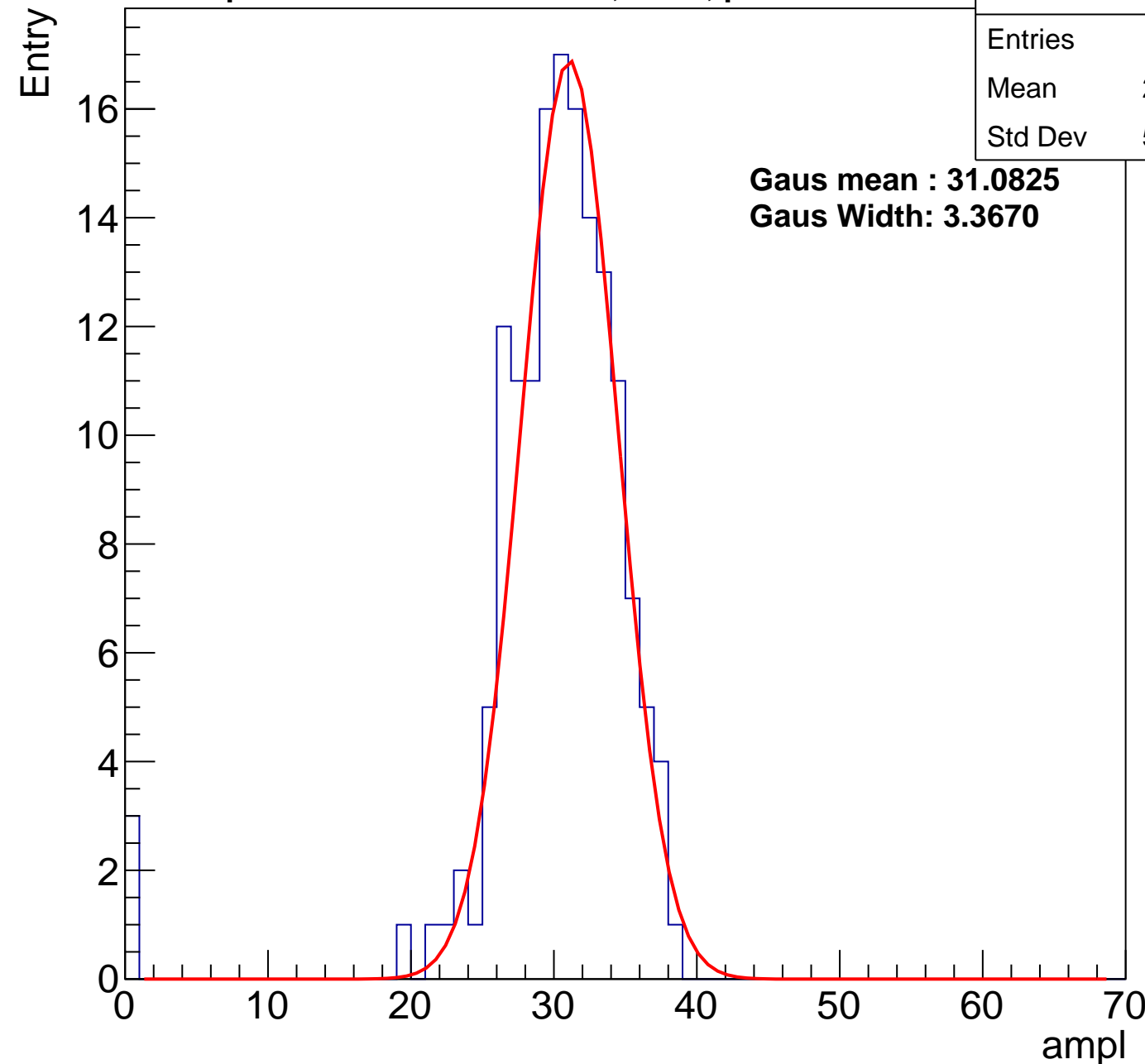
40

50

60

70

ampl



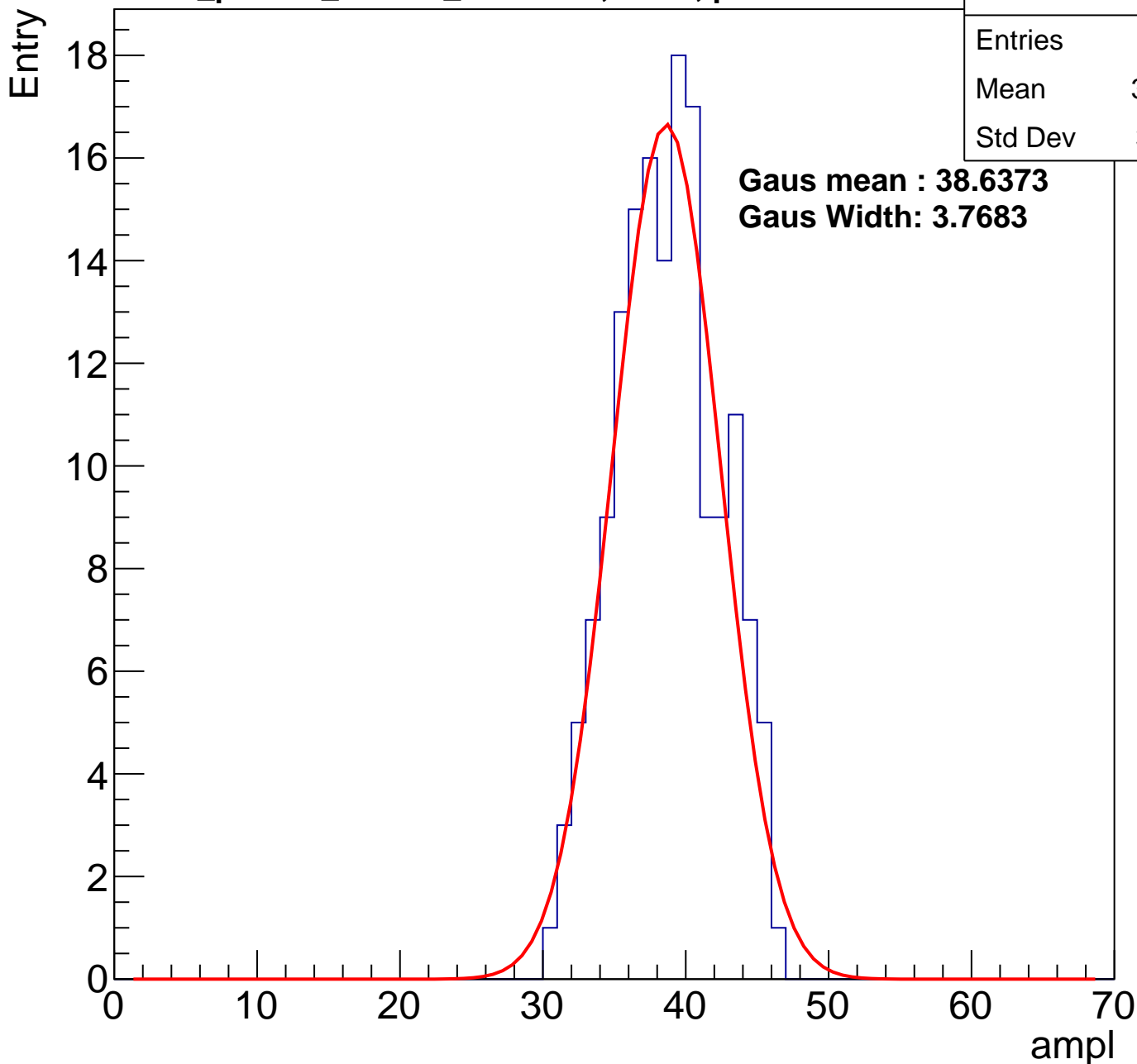
B0L101S, U25-ch29, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	38.25
Std Dev	3.571

Gaus mean : 38.6373

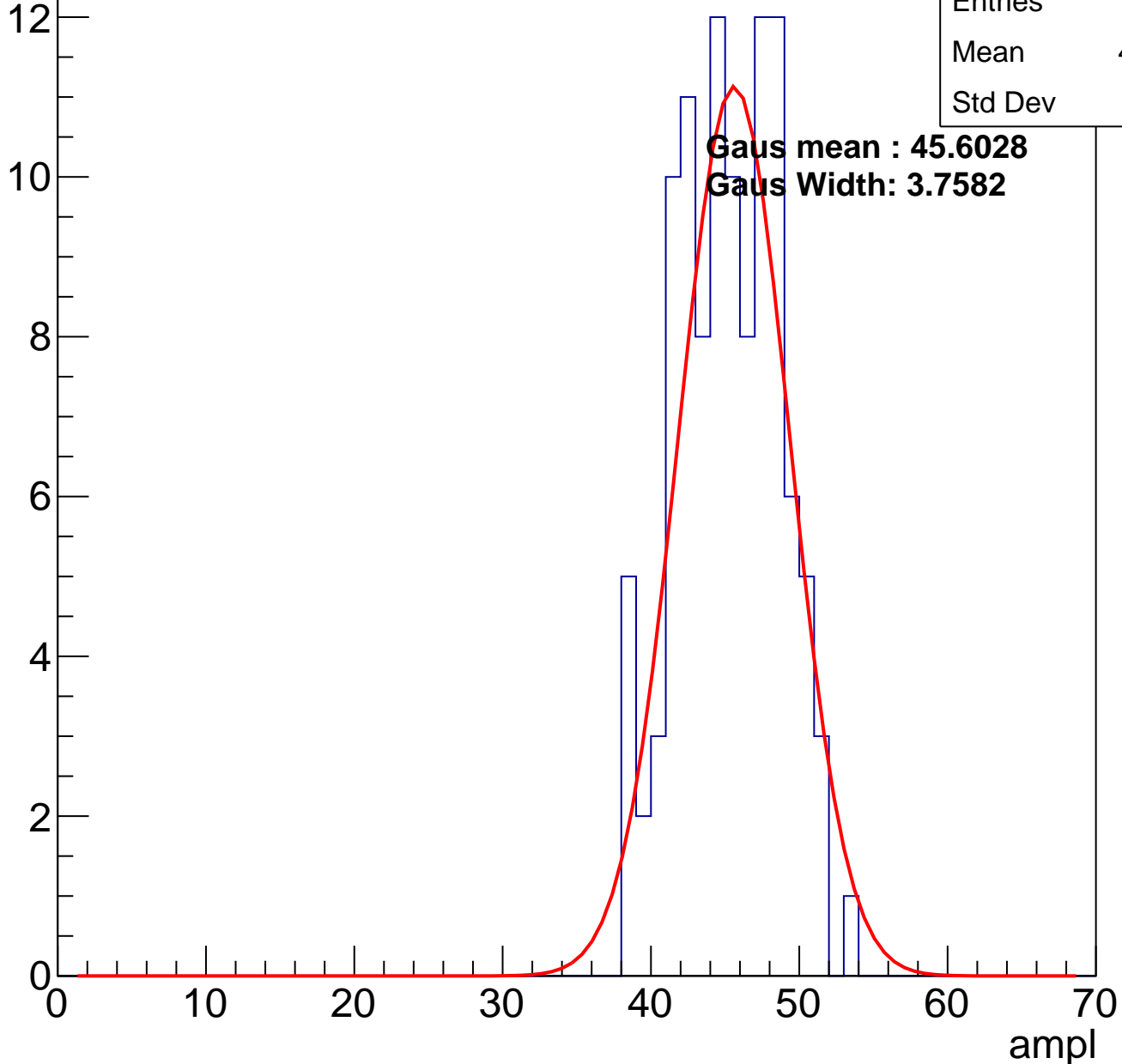
Gaus Width: 3.7683



B0L101S, U25-ch29, adc2

calib_packv5_042523_0143.root, FC#1, port C1

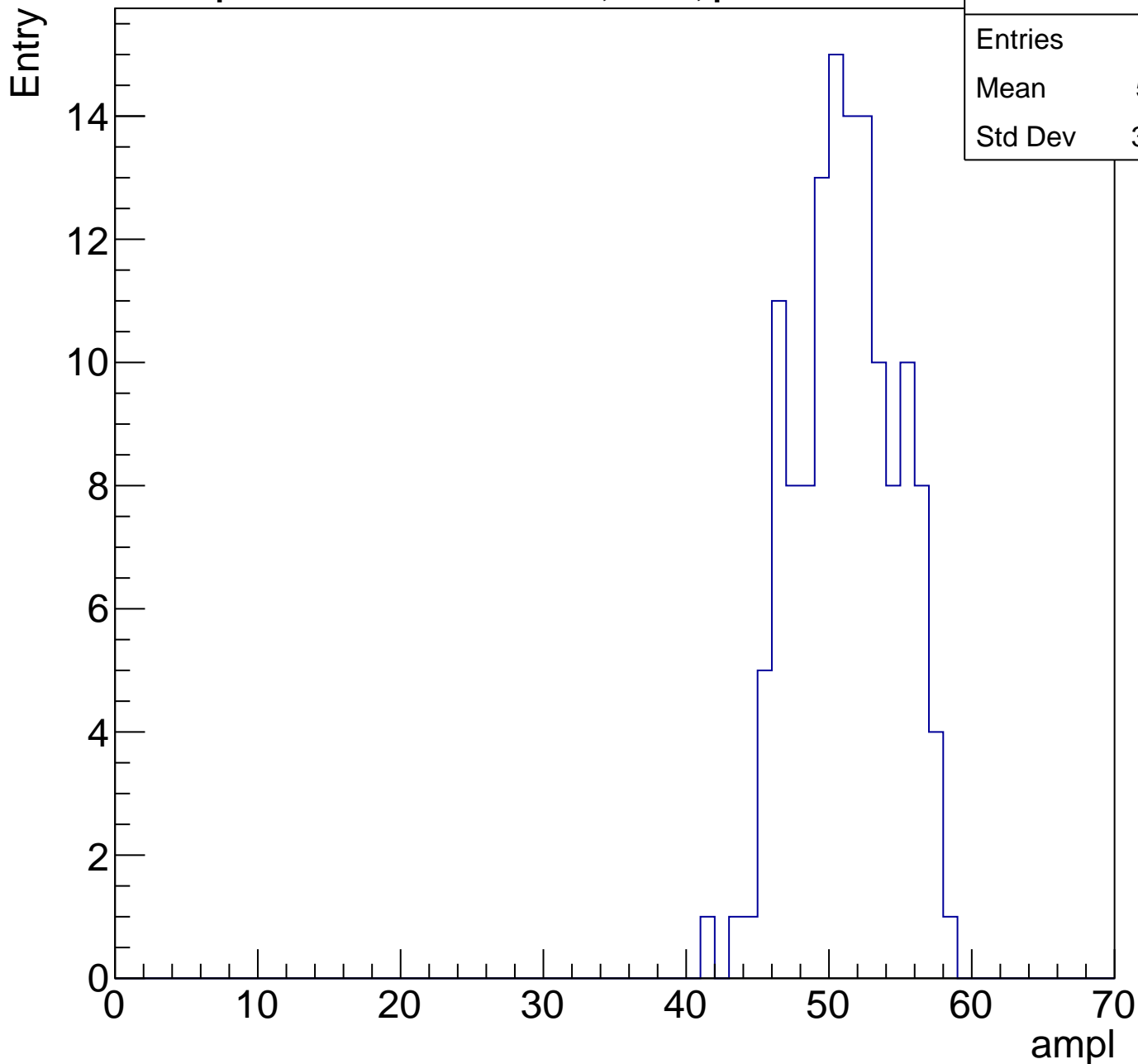
Entry



B0L101S, U25-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	50.71
Std Dev	3.485



B0L101S, U25-ch29, adc4

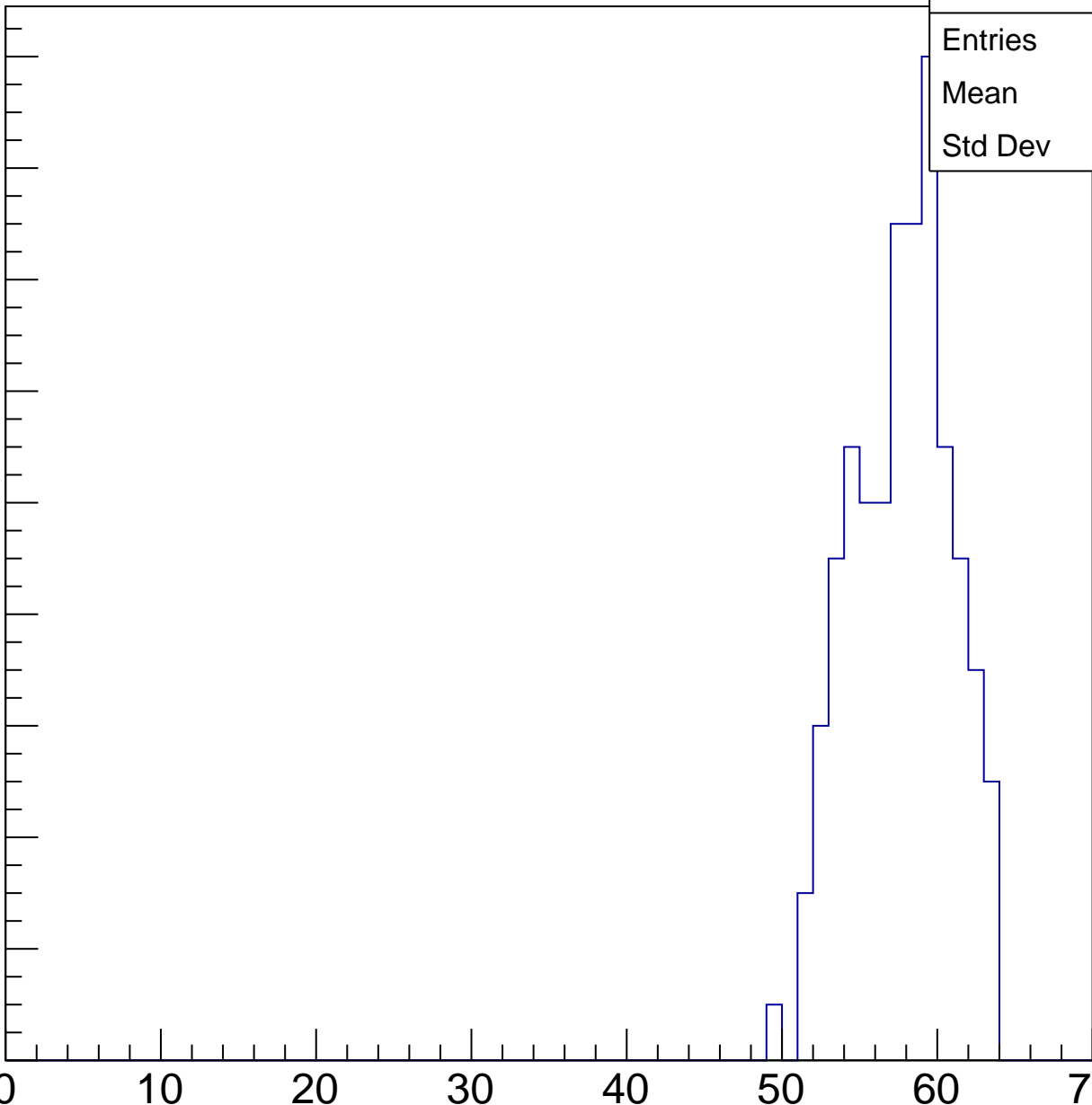
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	130
Mean	57.23
Std Dev	3.154

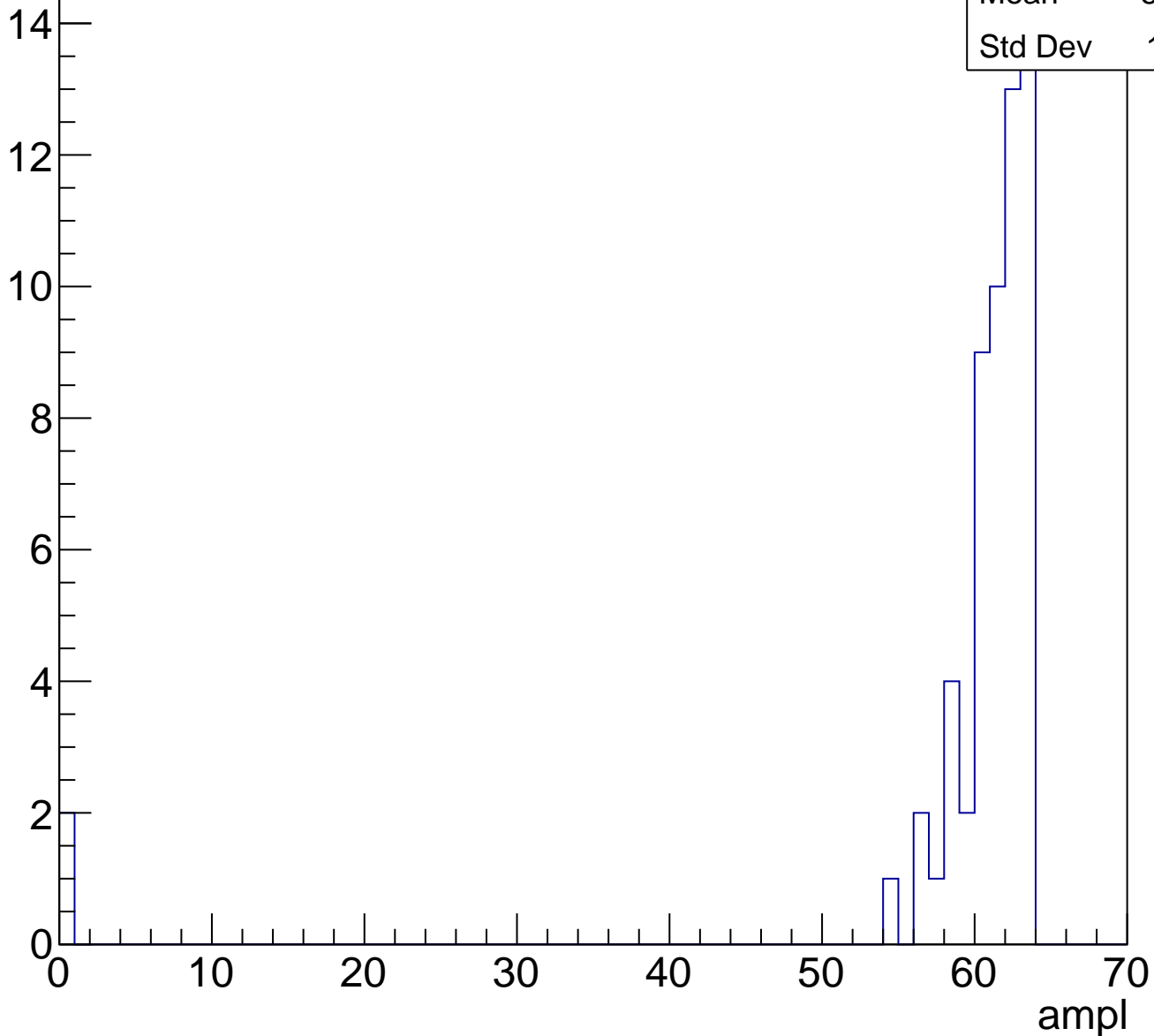
ampl



B0L101S, U25-ch29, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



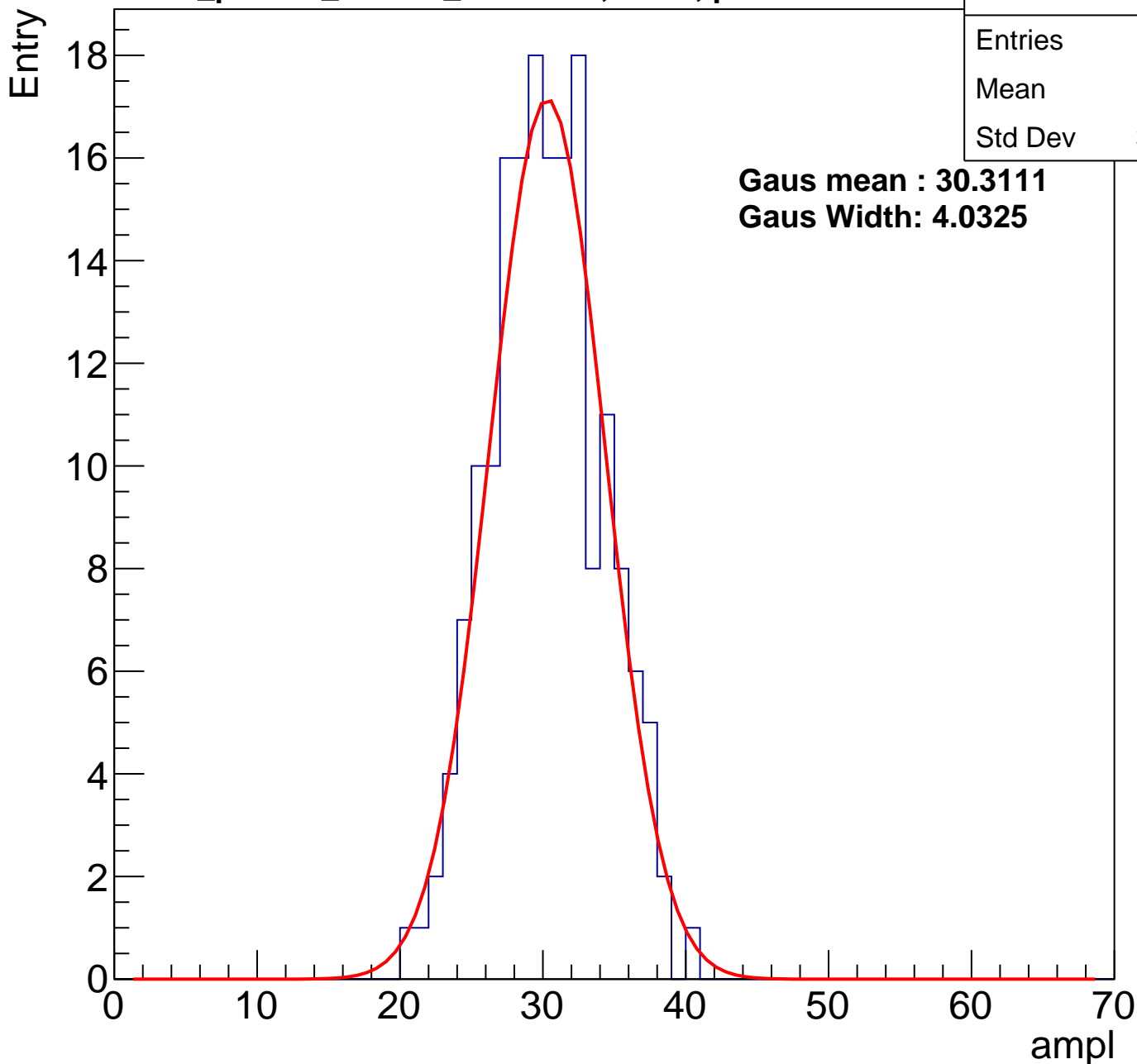
B0L101S, U25-ch30, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	176
Mean	29.8
Std Dev	3.851

Gaus mean : 30.3111

Gaus Width: 4.0325

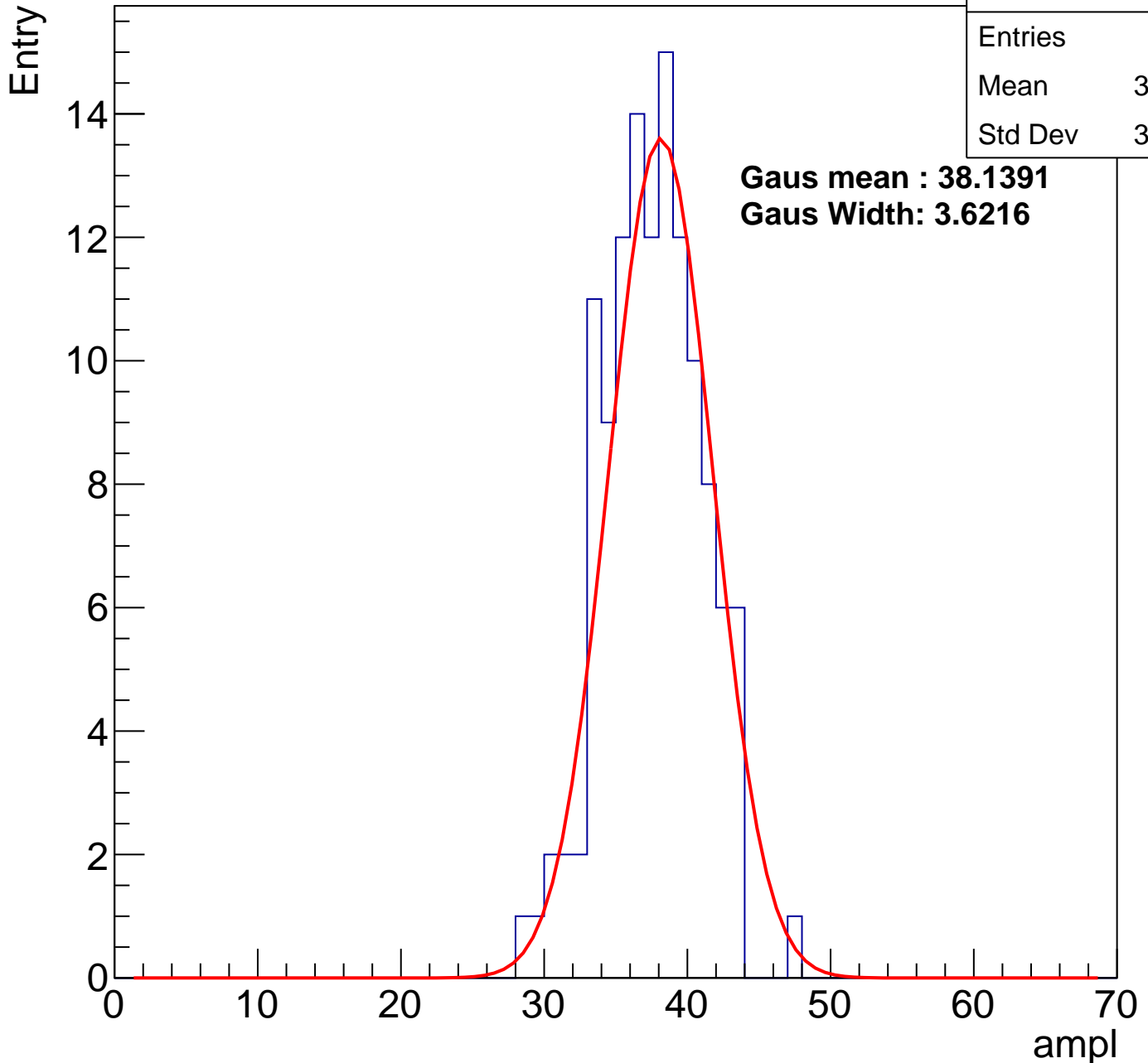


B0L101S, U25-ch30, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	37.12
Std Dev	3.395

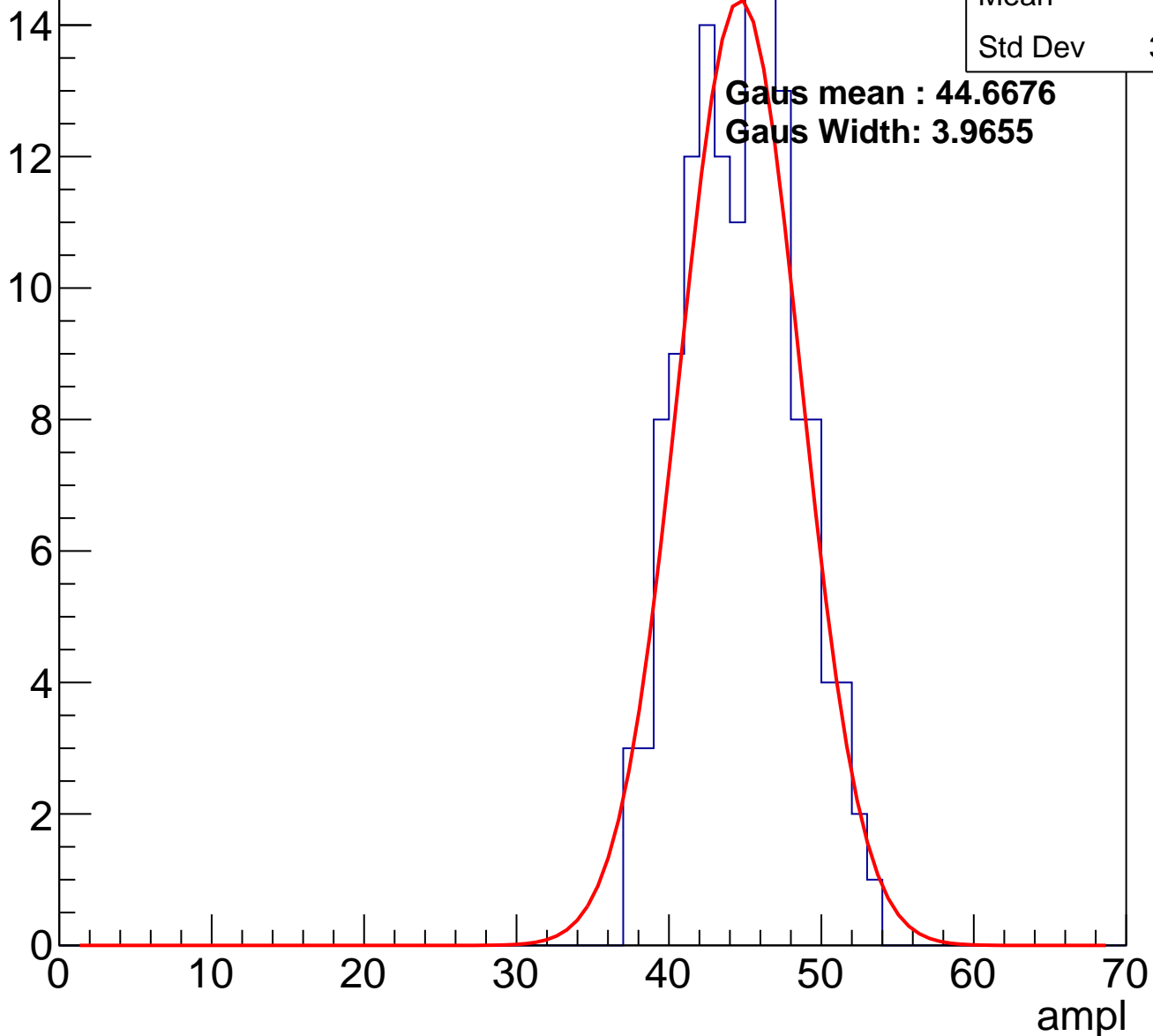
Gaus mean : 38.1391
Gaus Width: 3.6216



B0L101S, U25-ch30, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch30, adc3

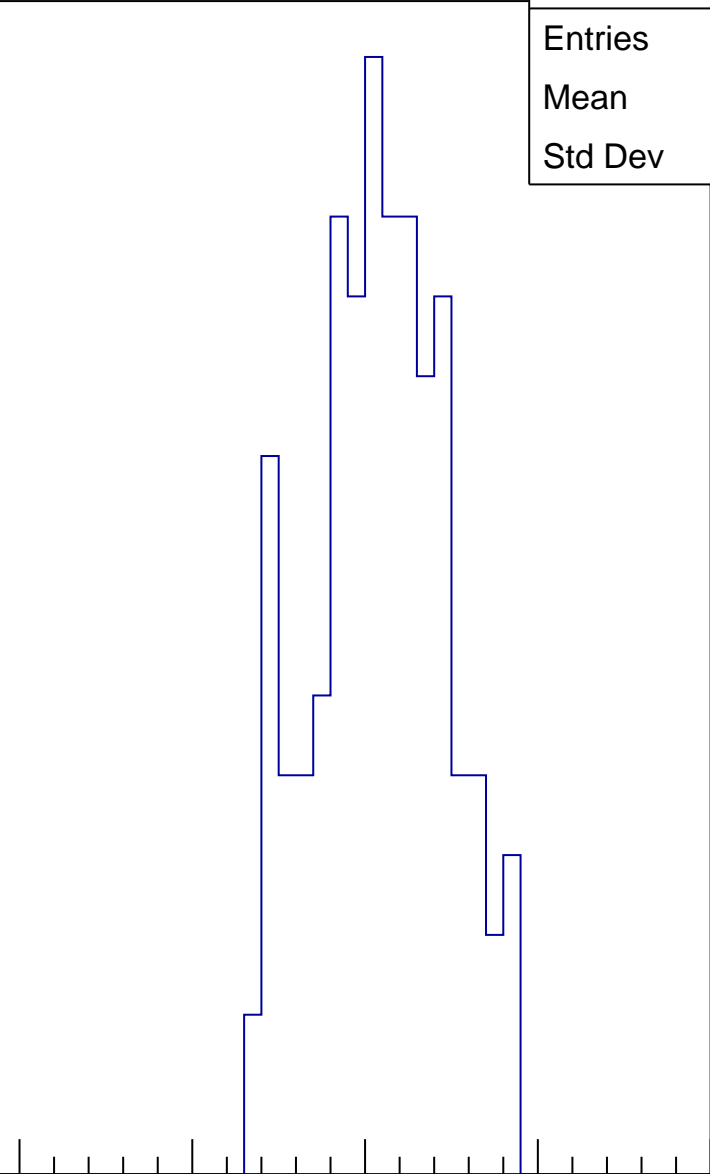
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	50.41
Std Dev	3.729

Entry

14
12
10
8
6
4
2
0

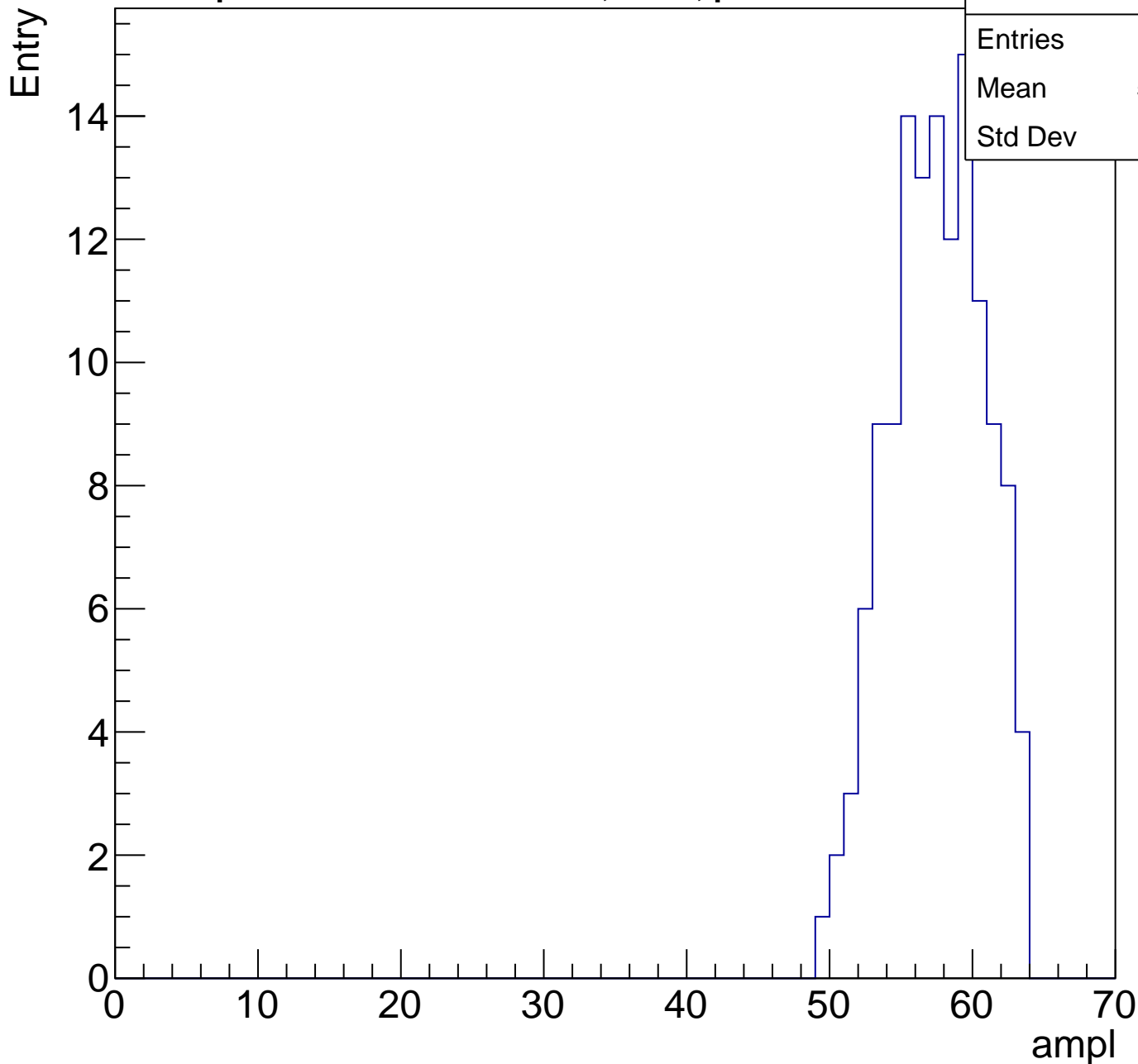
ampl



B0L101S, U25-ch30, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	57.01
Std Dev	3.25



B0L101S, U25-ch30, adc5

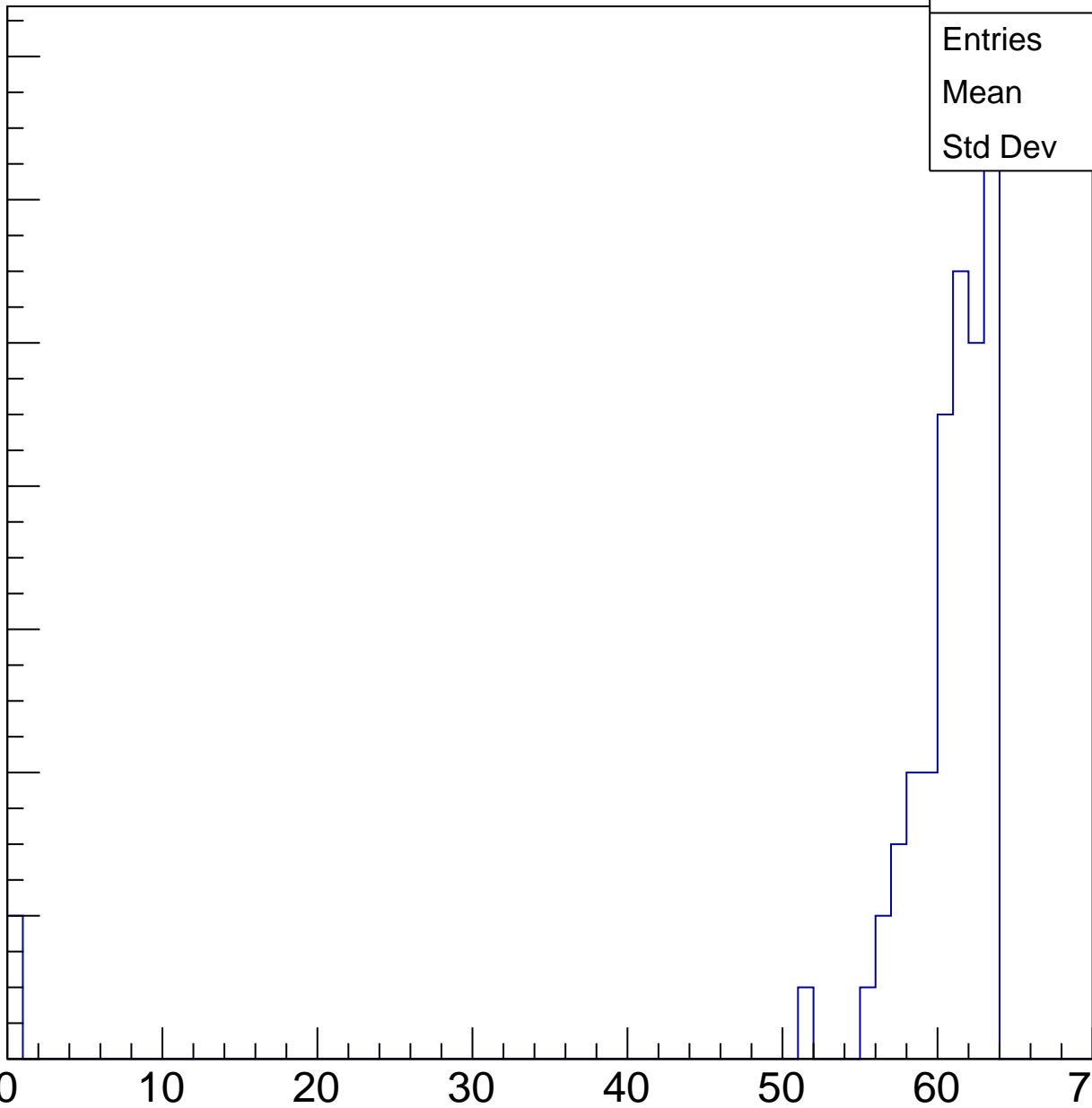
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	58.52
Std Dev	11.04

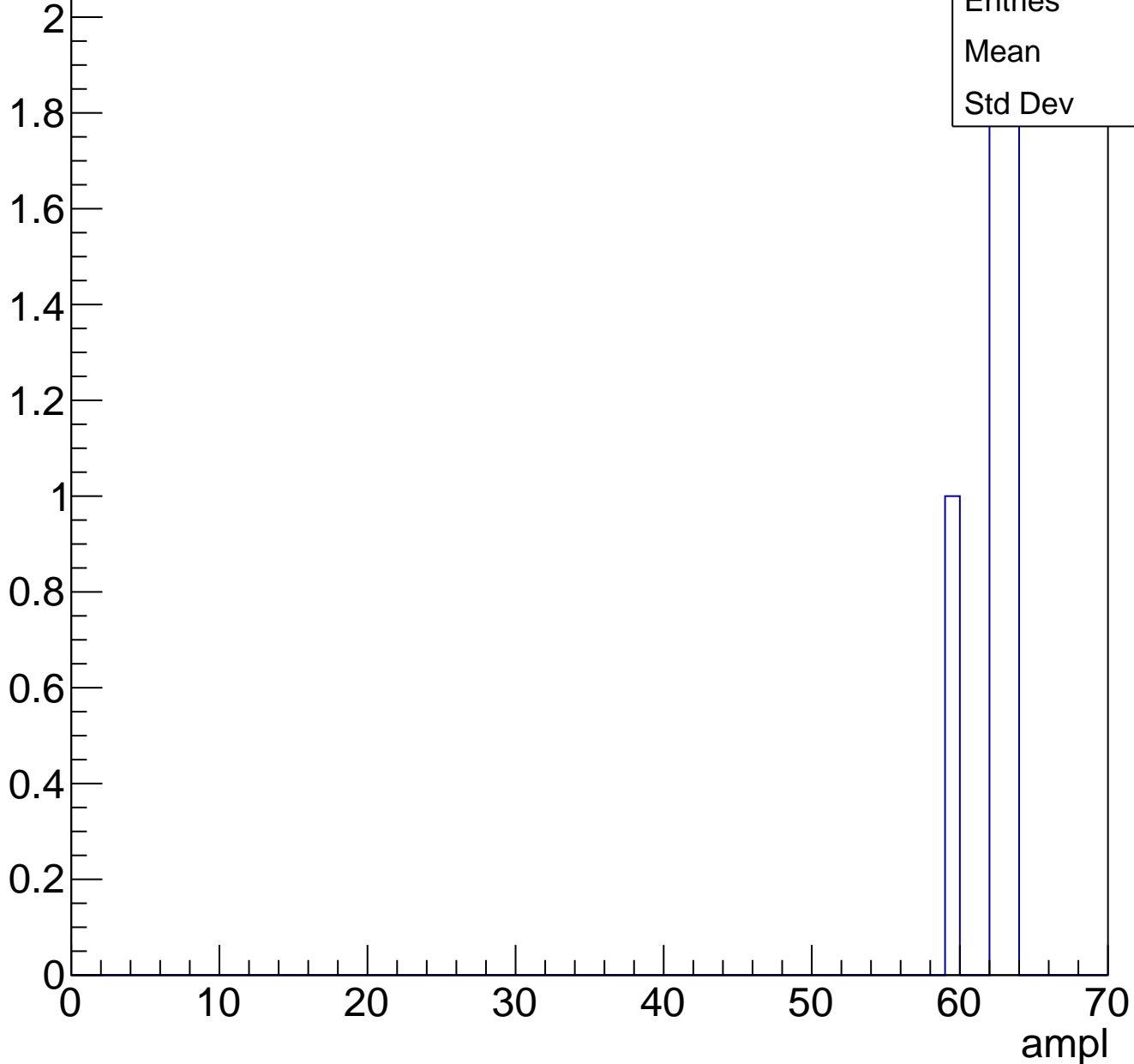
ampl



B0L101S, U25-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



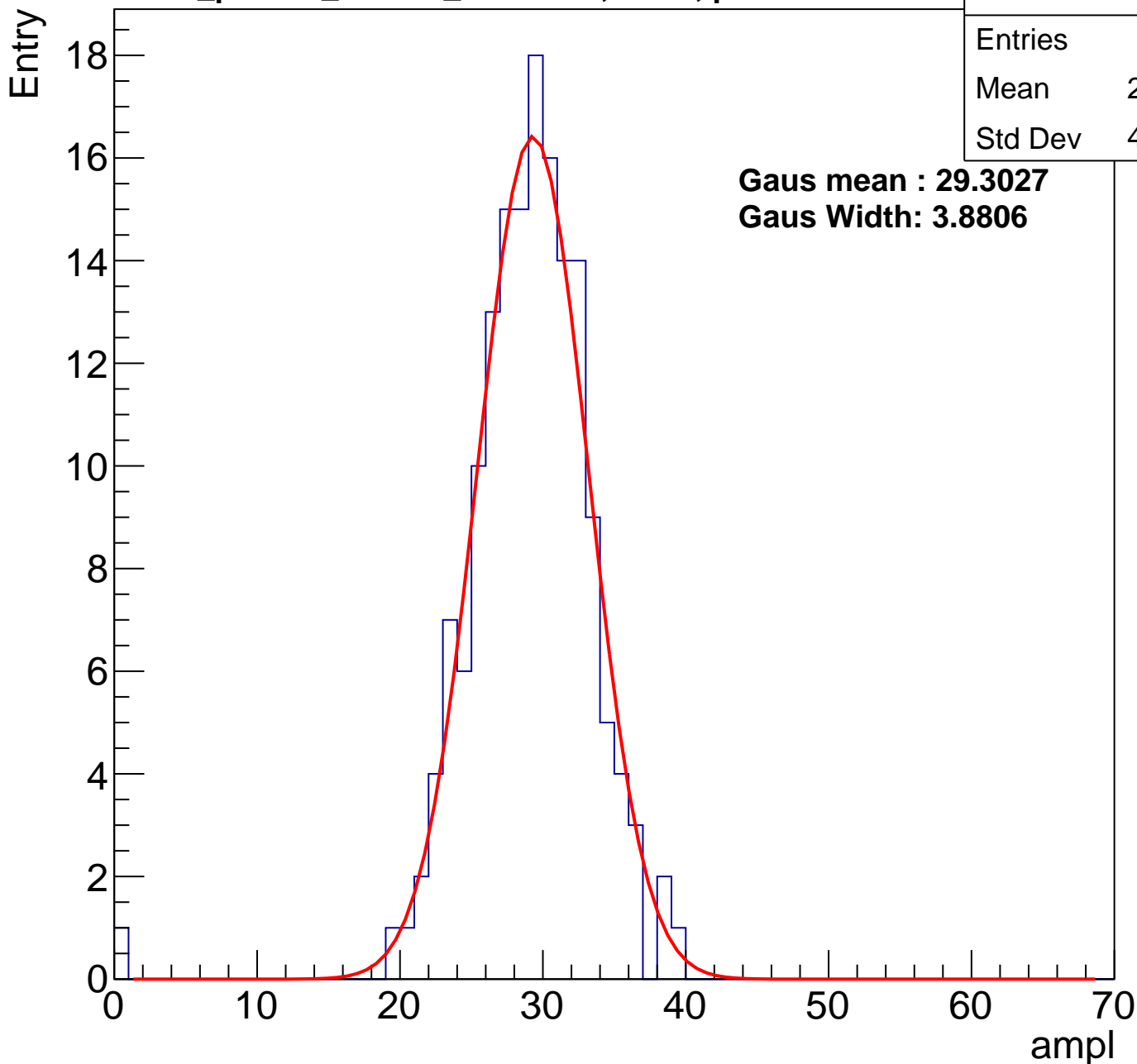
B0L101S, U25-ch31, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	28.58
Std Dev	4.382

Gaus mean : 29.3027

Gaus Width: 3.8806



B0L101S, U25-ch31, adc1

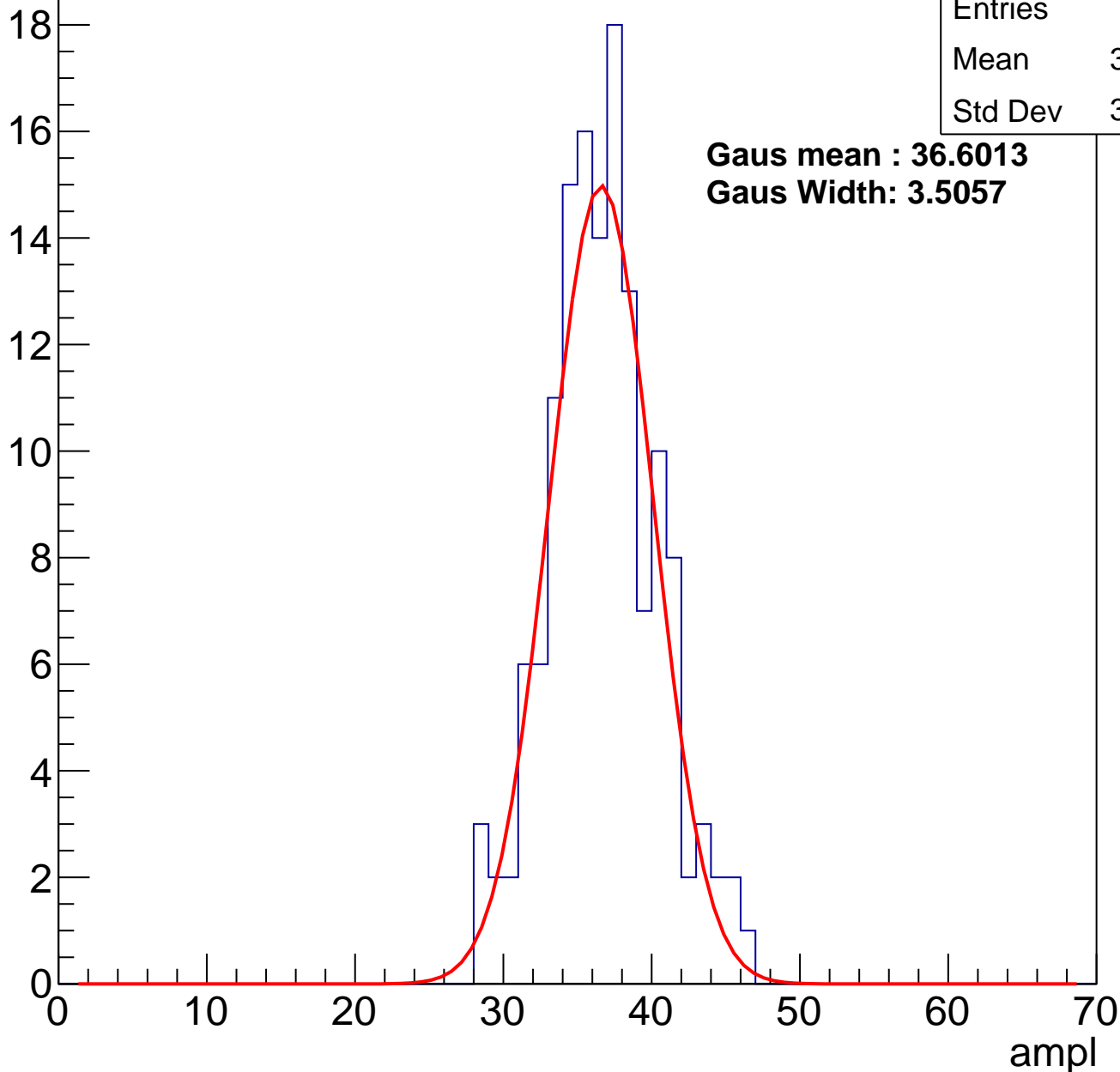
calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.28
Std Dev	3.646

Gaus mean : 36.6013

Gaus Width: 3.5057

Entry



B0L101S, U25-ch31, adc2

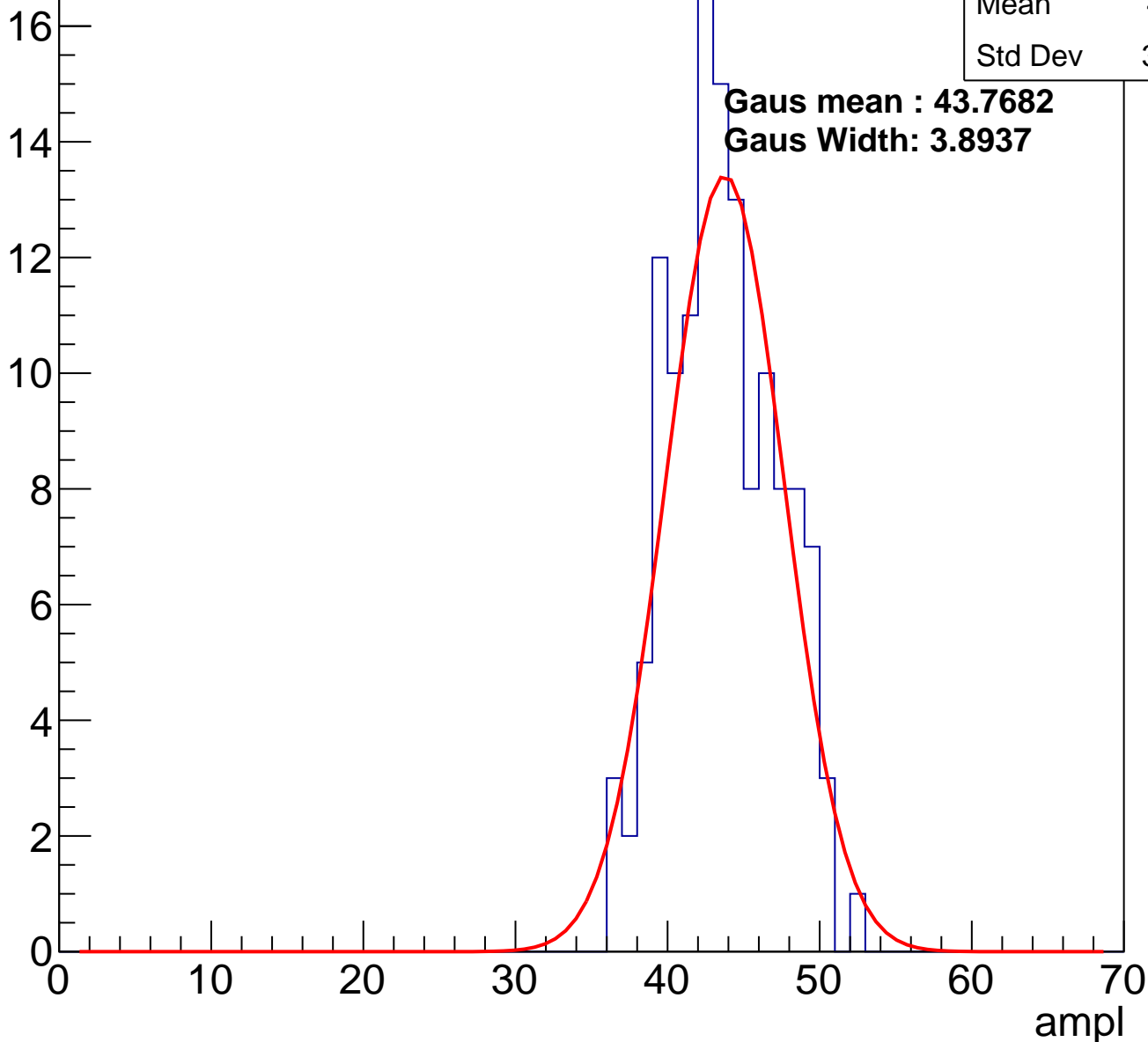
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	43.21
Std Dev	3.494

Gaus mean : 43.7682

Gaus Width: 3.8937

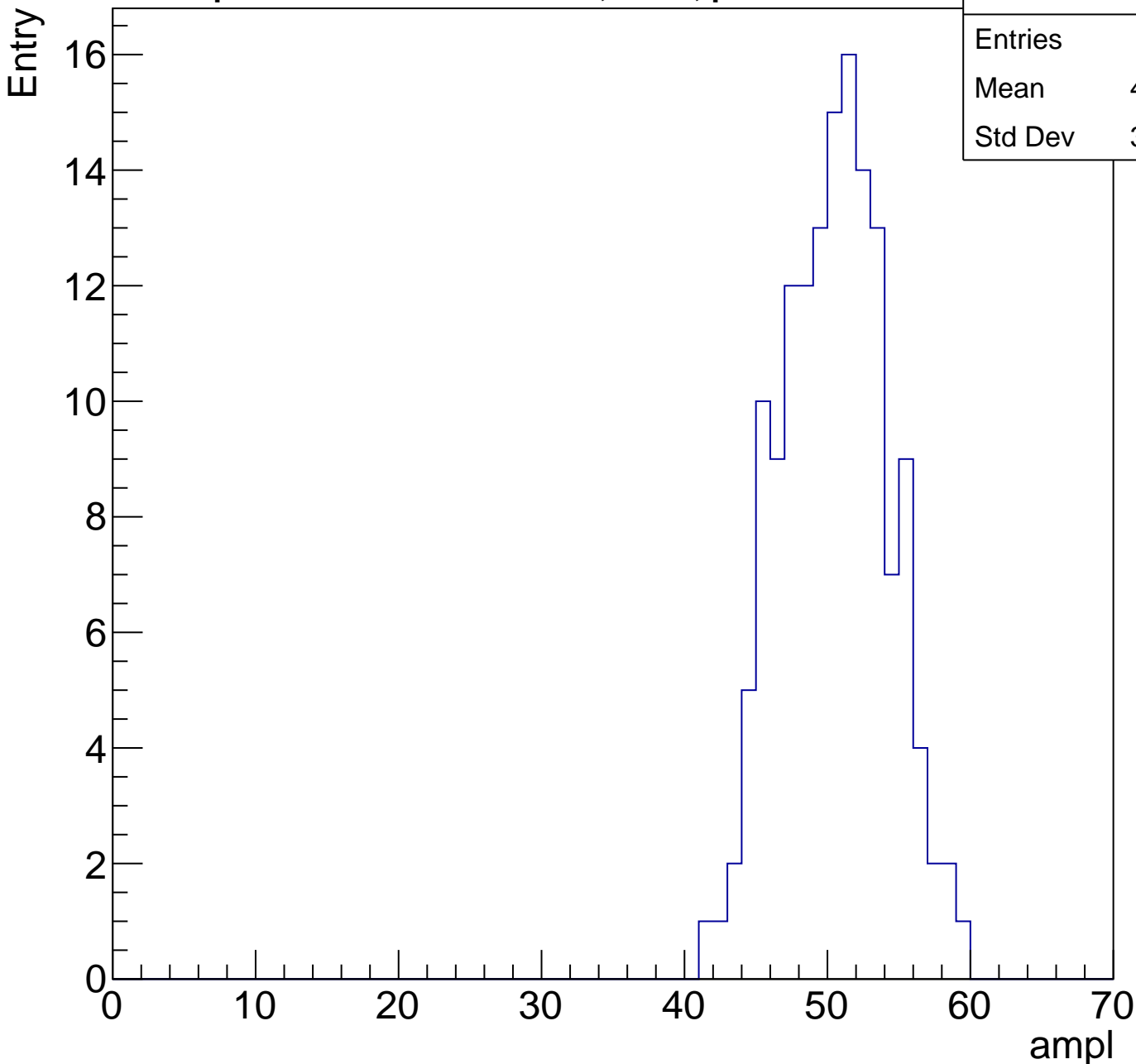
Entry



B0L101S, U25-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	49.99
Std Dev	3.642



B0L101S, U25-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

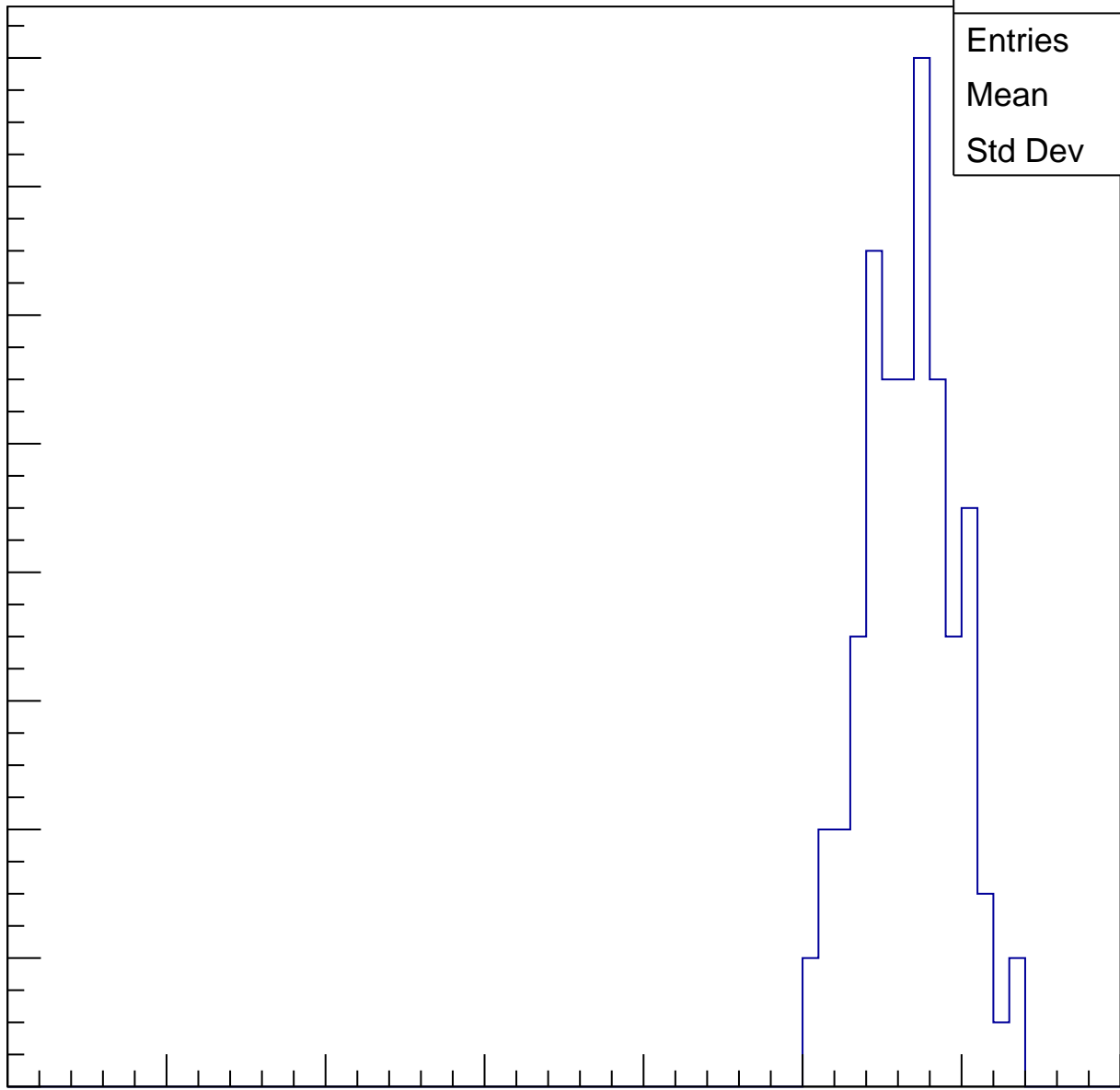
Entries	101
Mean	56.24
Std Dev	2.871

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch31, adc5

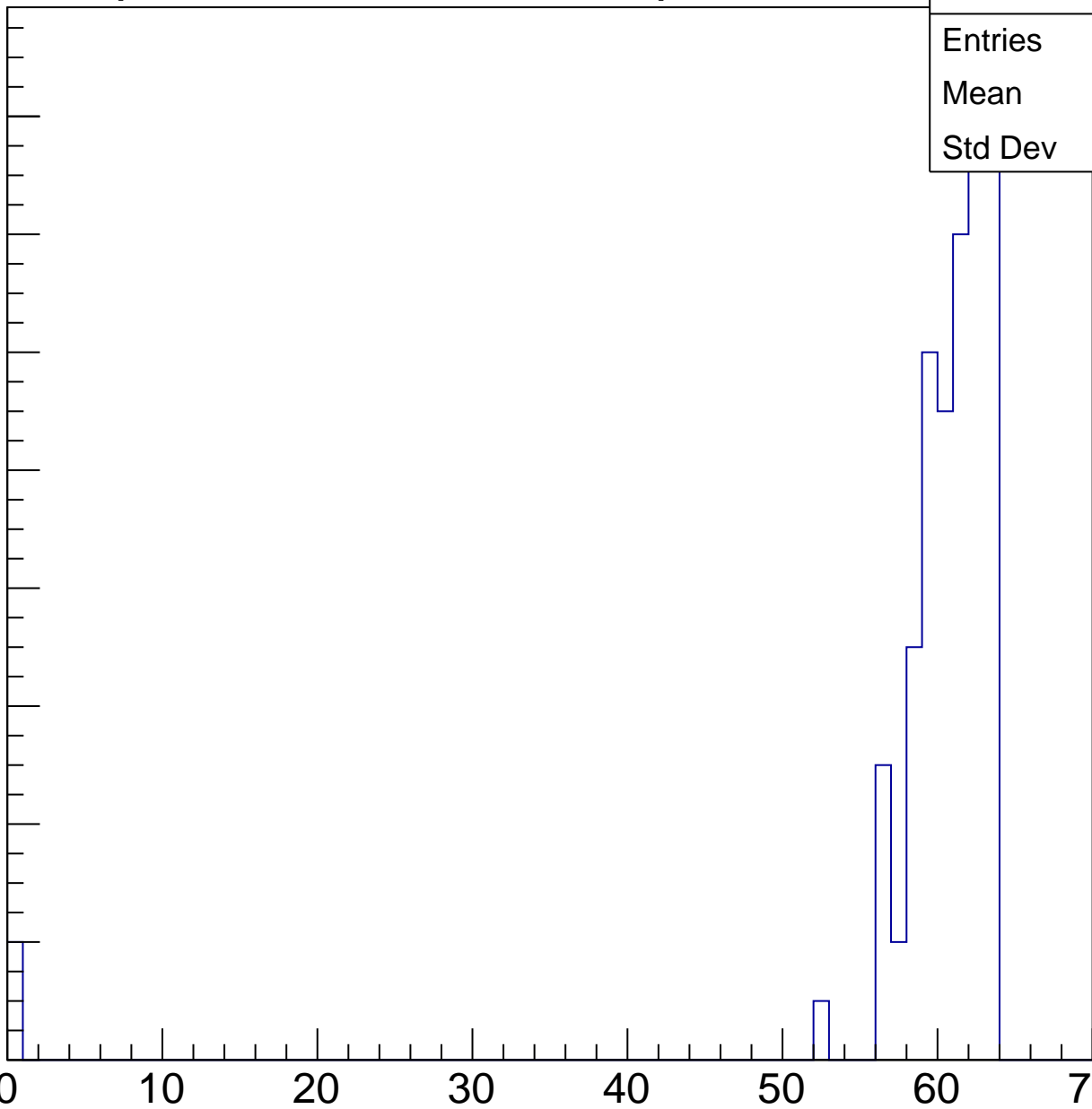
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.08
Std Dev	9.27

ampl



B0L101S, U25-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

6

Mean

61.67

Std Dev

0.7454

B0L101S, U25-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

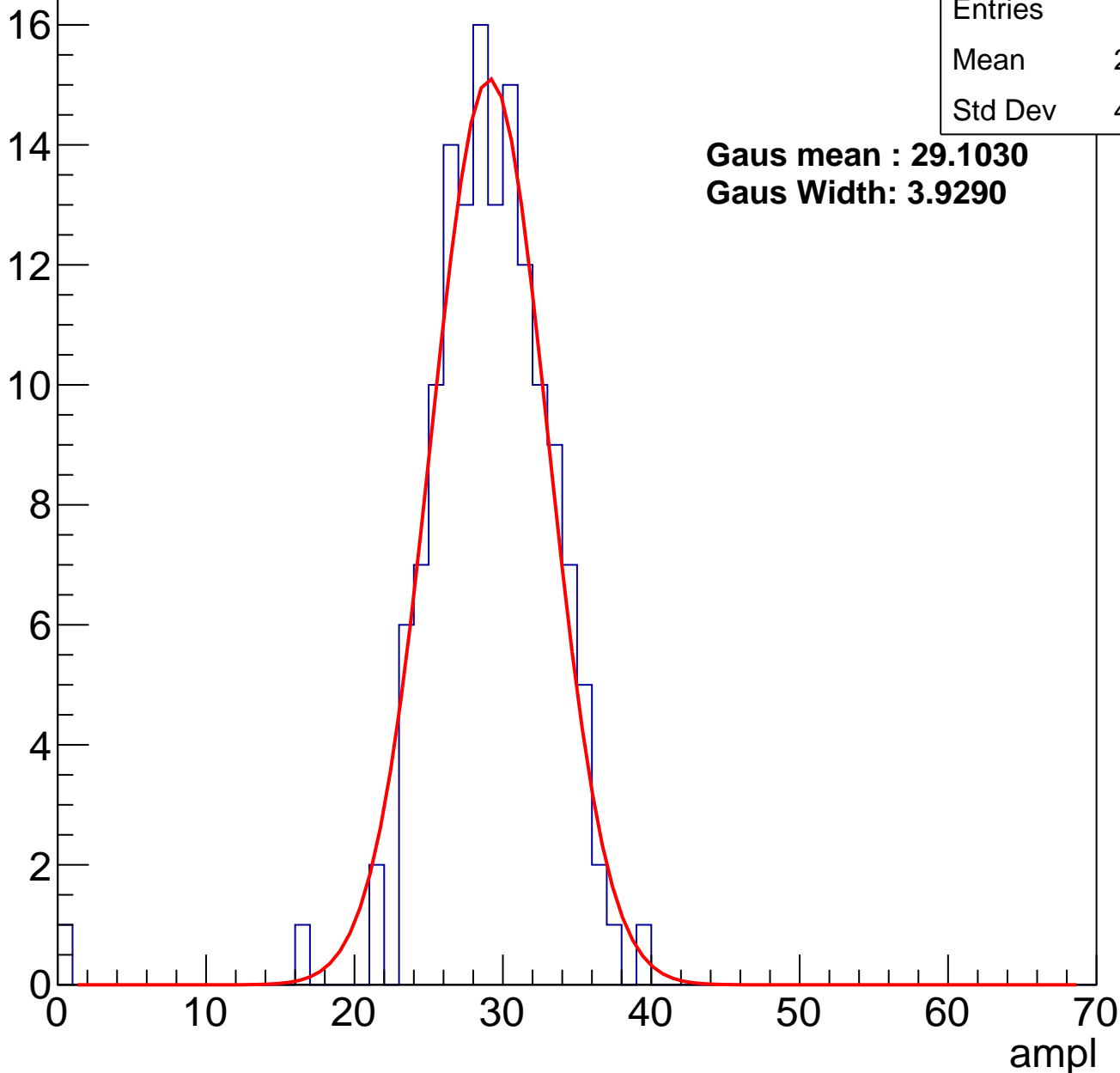


Entries	2
Mean	9.5
Std Dev	9.5

B0L101S, U25-ch32, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch32, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	36.24
Std Dev	3.668

Gaus mean : 36.9202

Gaus Width: 4.1199

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

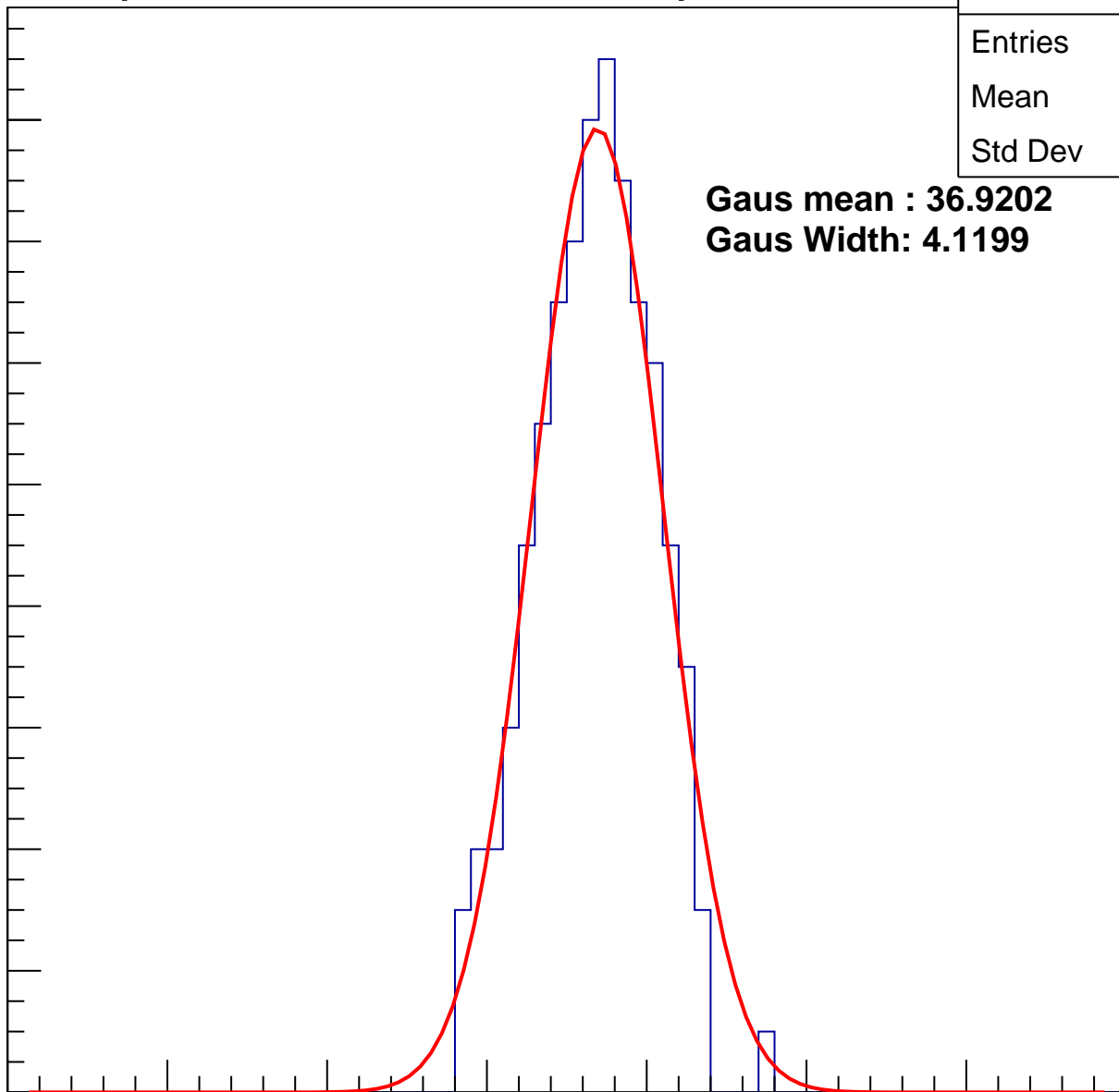
40

50

60

70

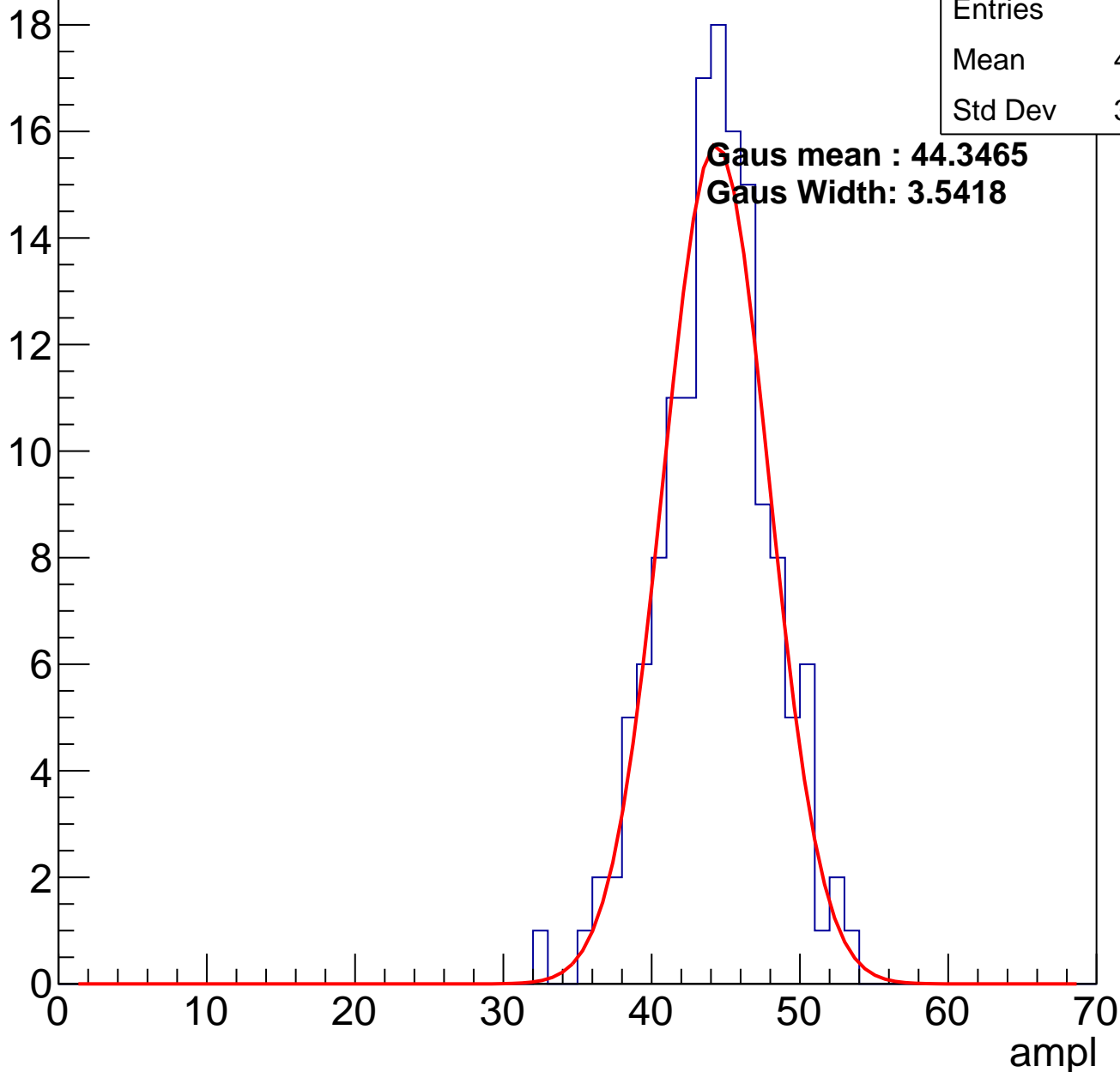
ampl



B0L101S, U25-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

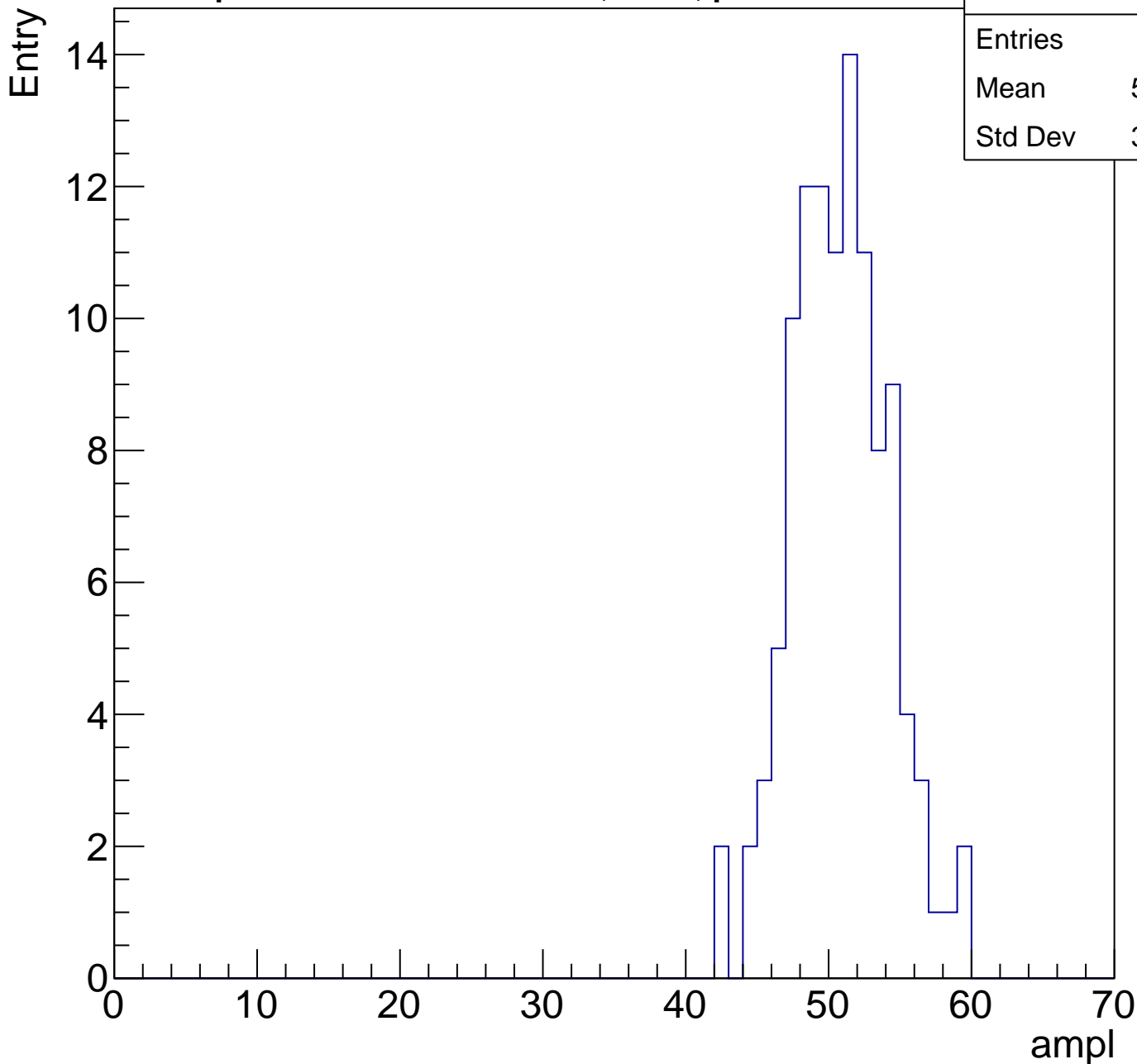
Entry



B0L101S, U25-ch32, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	50.35
Std Dev	3.369



B0L101S, U25-ch32, adc4

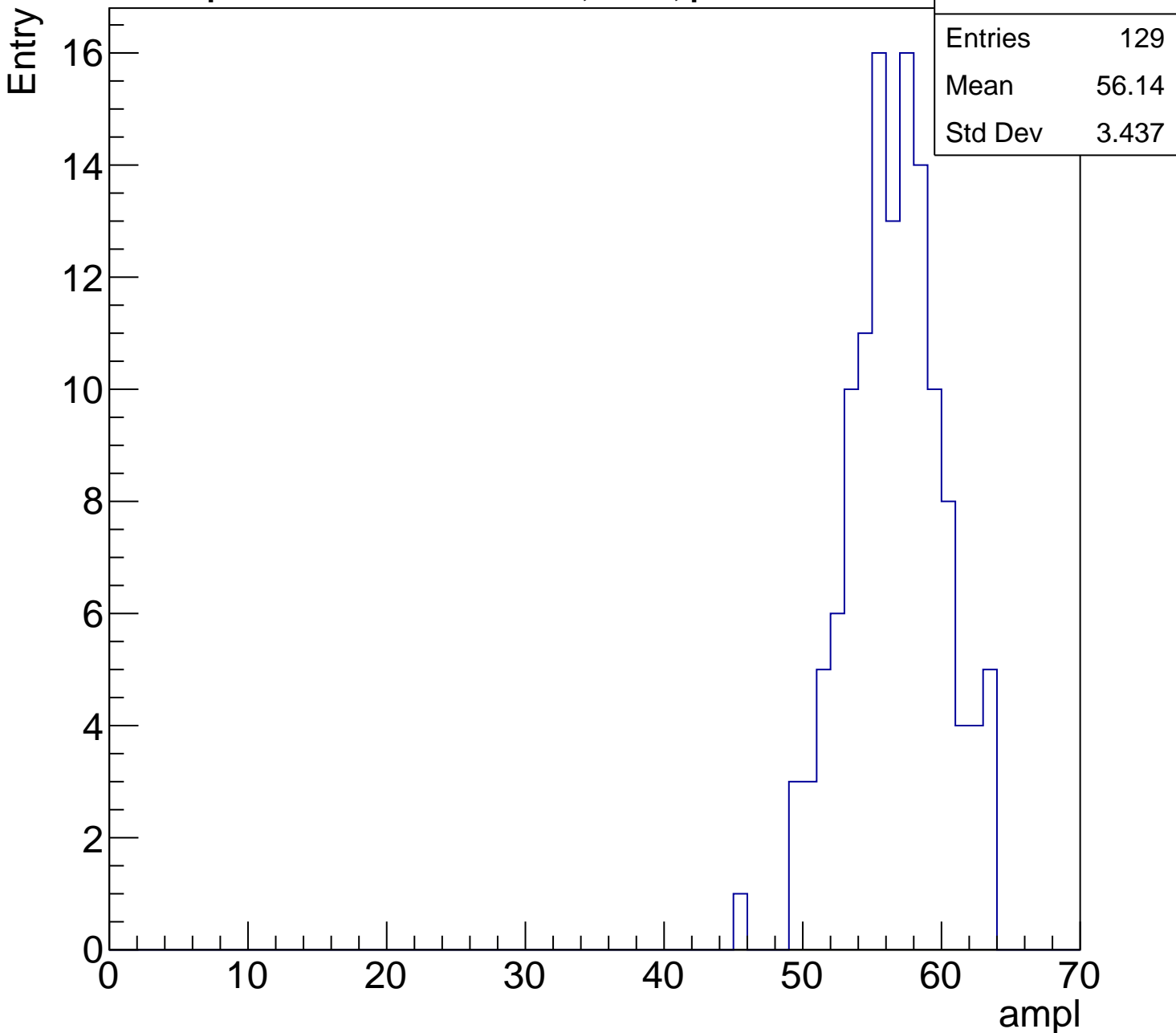
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	56.14
Std Dev	3.437

ampl



B0L101S, U25-ch32, adc5

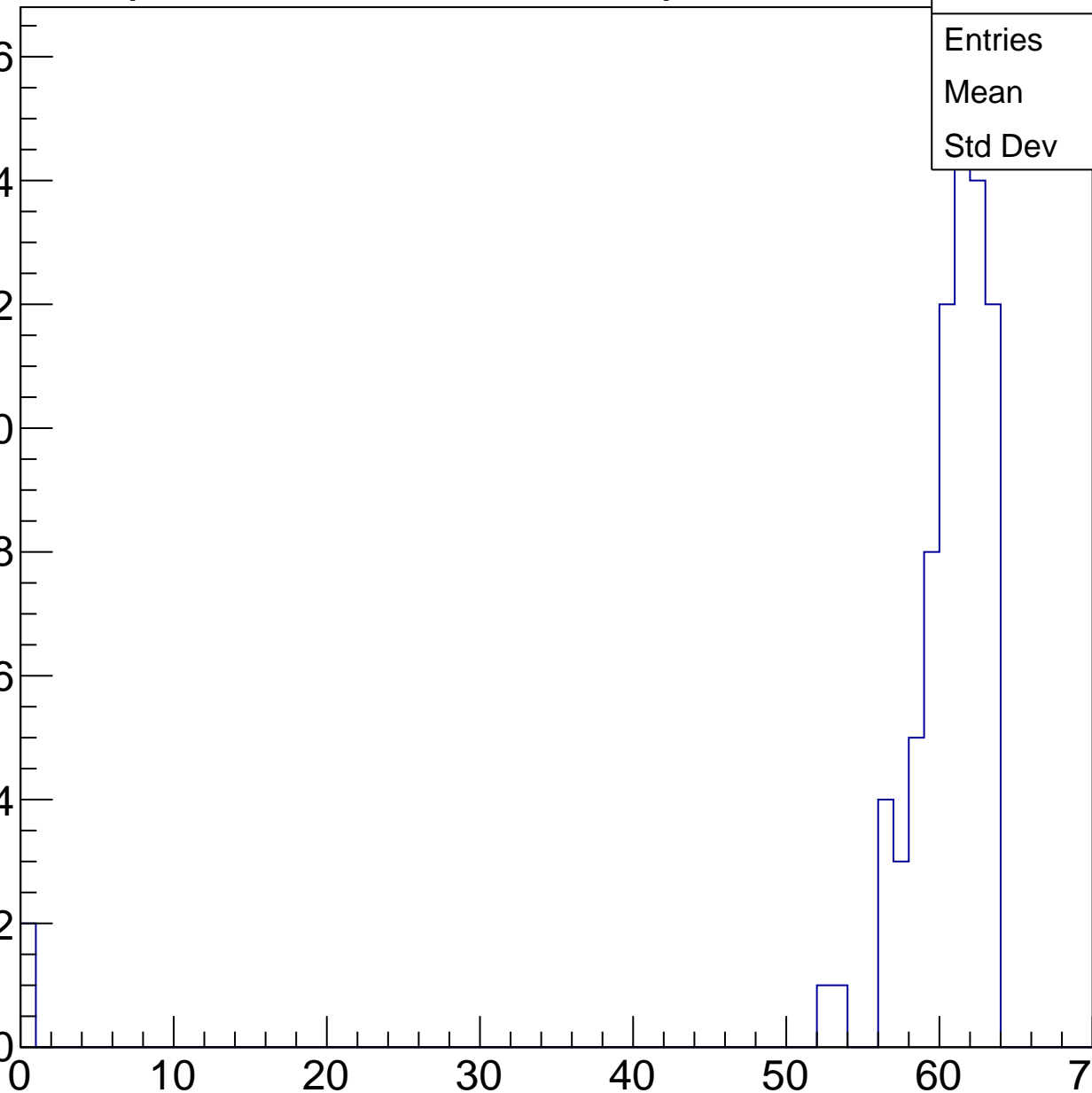
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	58.74
Std Dev	9.799

ampl



B0L101S, U25-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch33, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	31.86
Std Dev	3.377

Gaus mean : 32.3603

Gaus Width: 3.7157

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U25-ch33, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	38.31
Std Dev	3.204

Gaus mean : 39.1511

Gaus Width: 3.5532

Entry

12

10

8

6

4

2

0

0

10

20

30

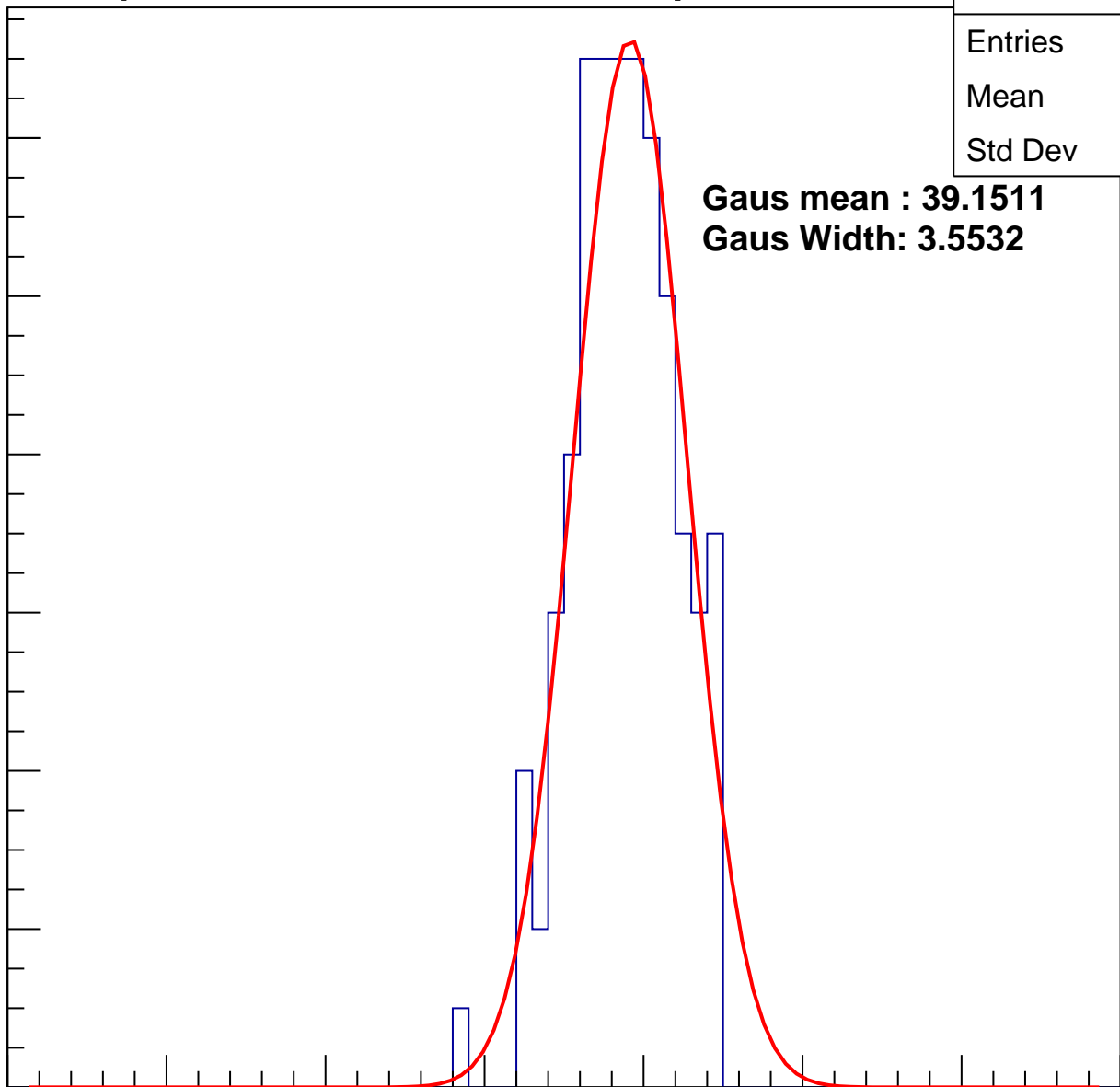
40

50

60

70

ampl



B0L101S, U25-ch33, adc2

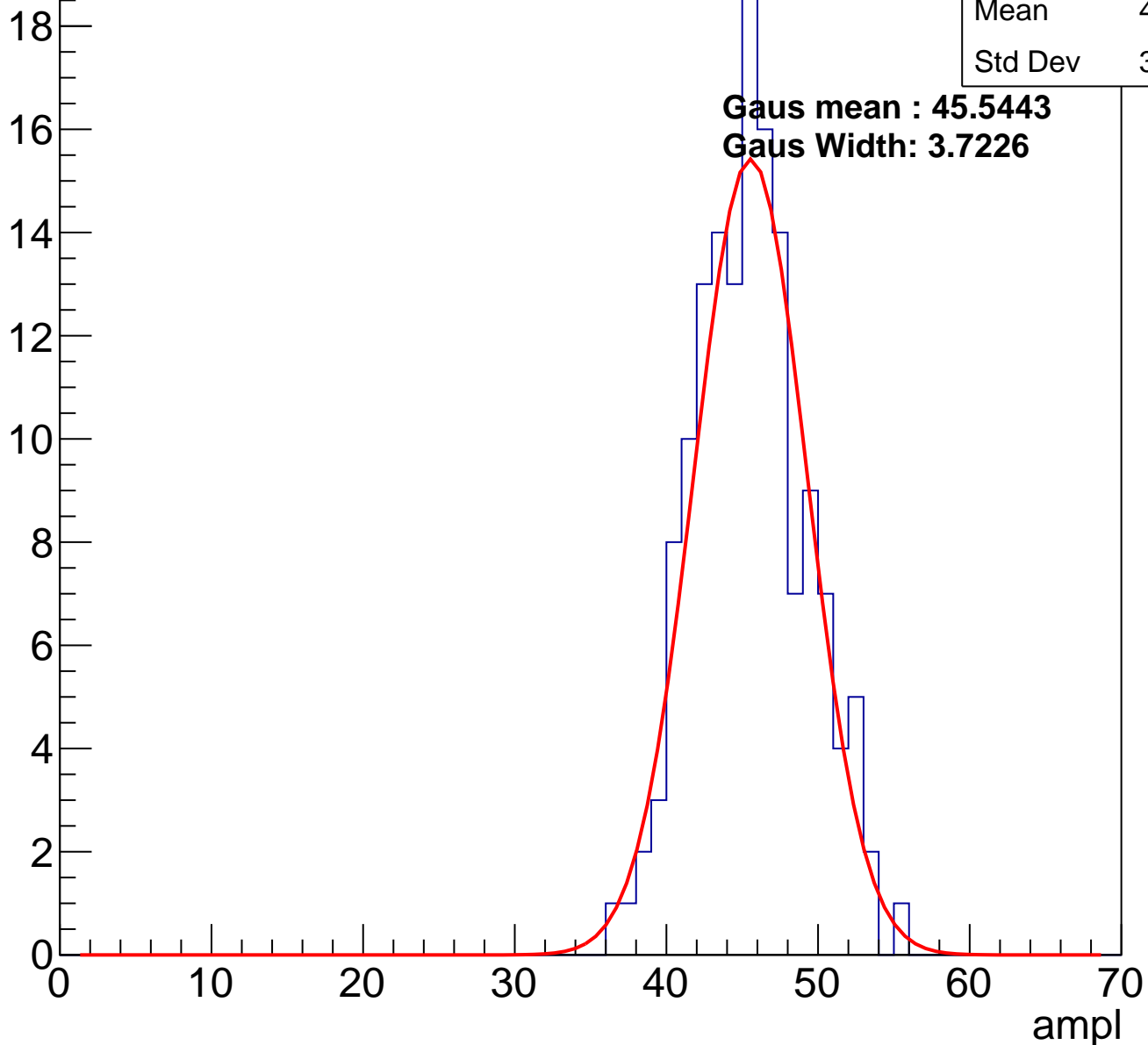
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	45.08
Std Dev	3.633

Gaus mean : 45.5443

Gaus Width: 3.7226

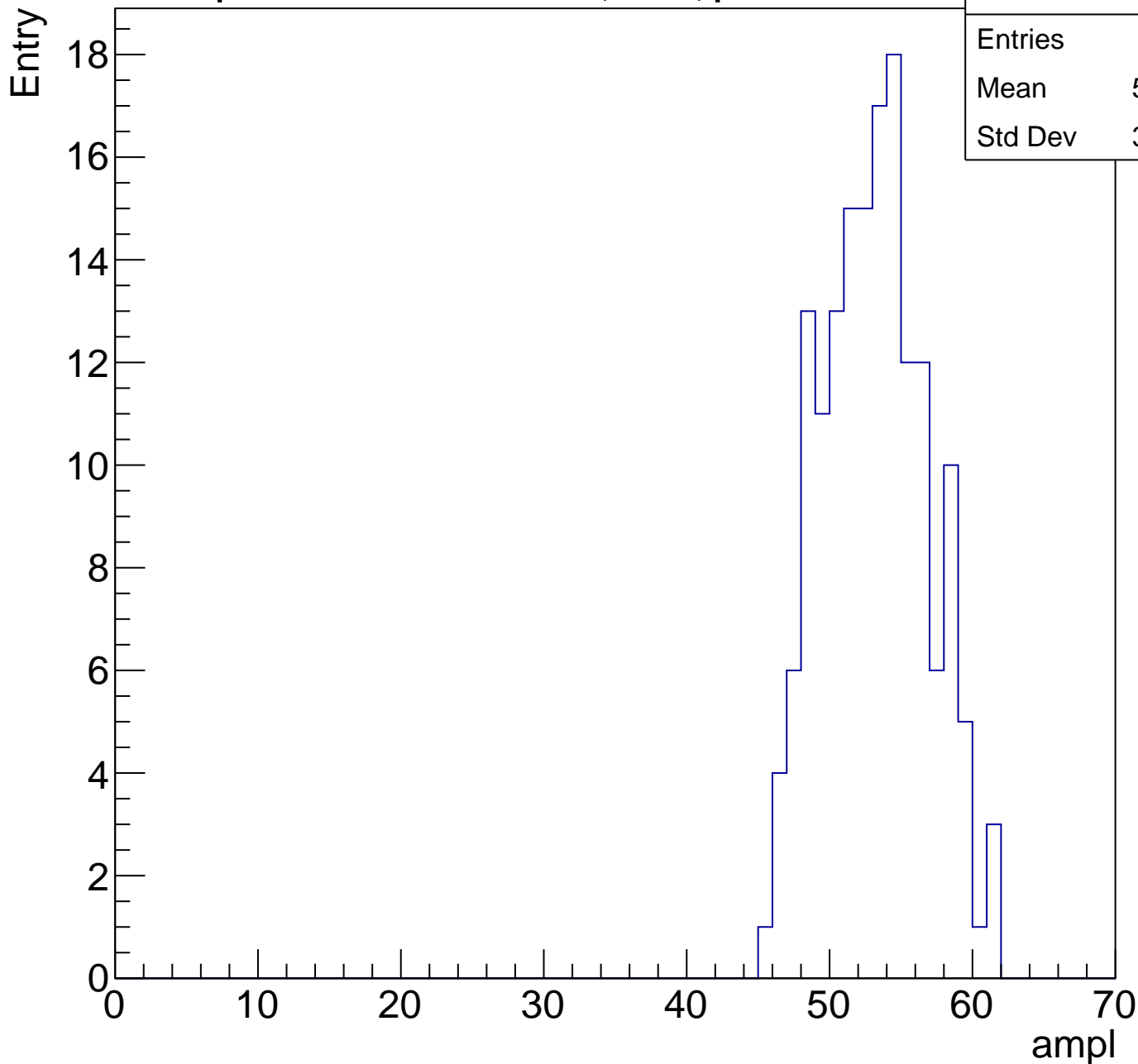
Entry



B0L101S, U25-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

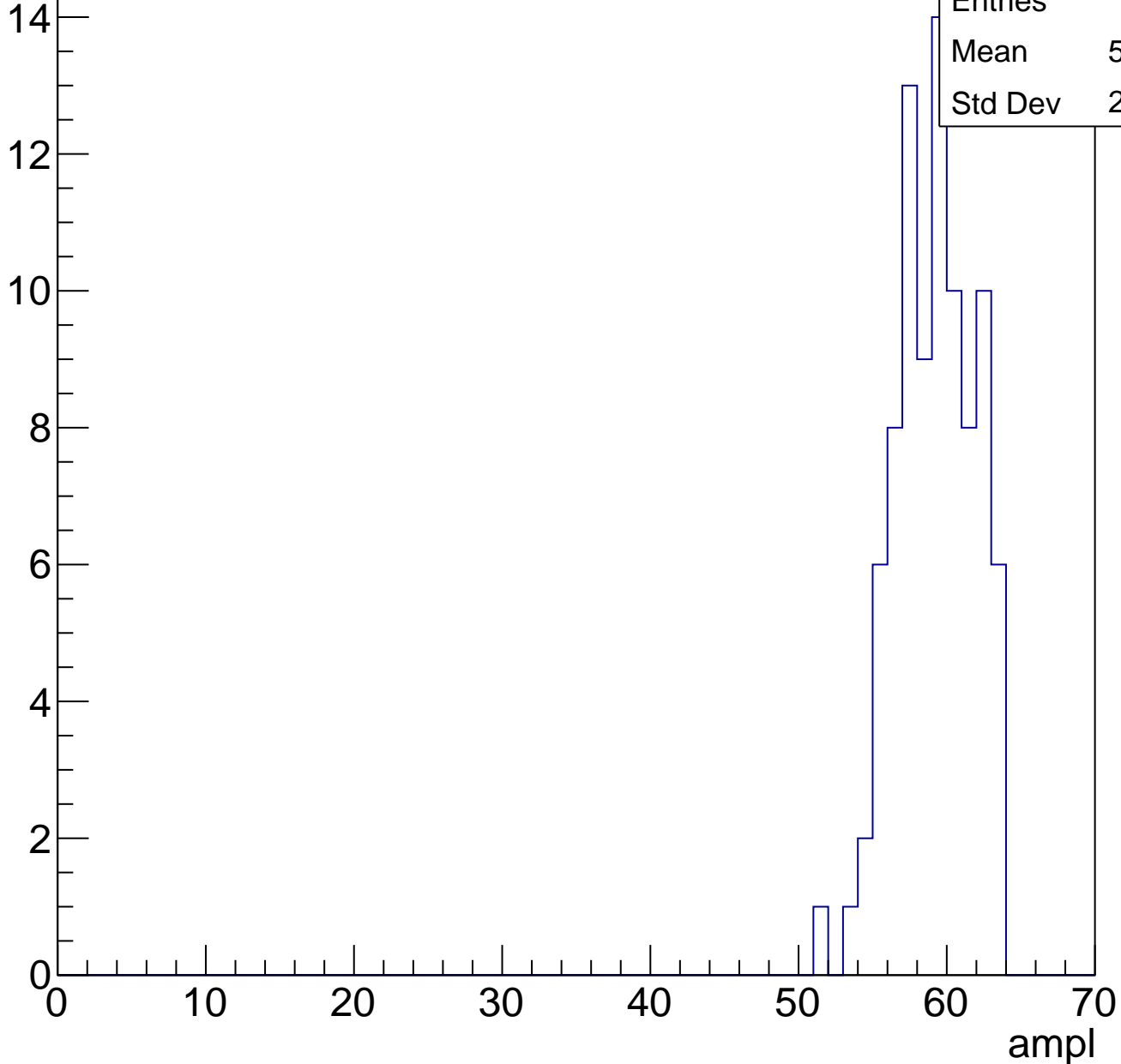
Entries	162
Mean	52.68
Std Dev	3.595



B0L101S, U25-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch33, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

45

Mean

58.49

Std Dev

12.73

ampl

0

10

20

30

40

50

60

70

B0L101S, U25-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

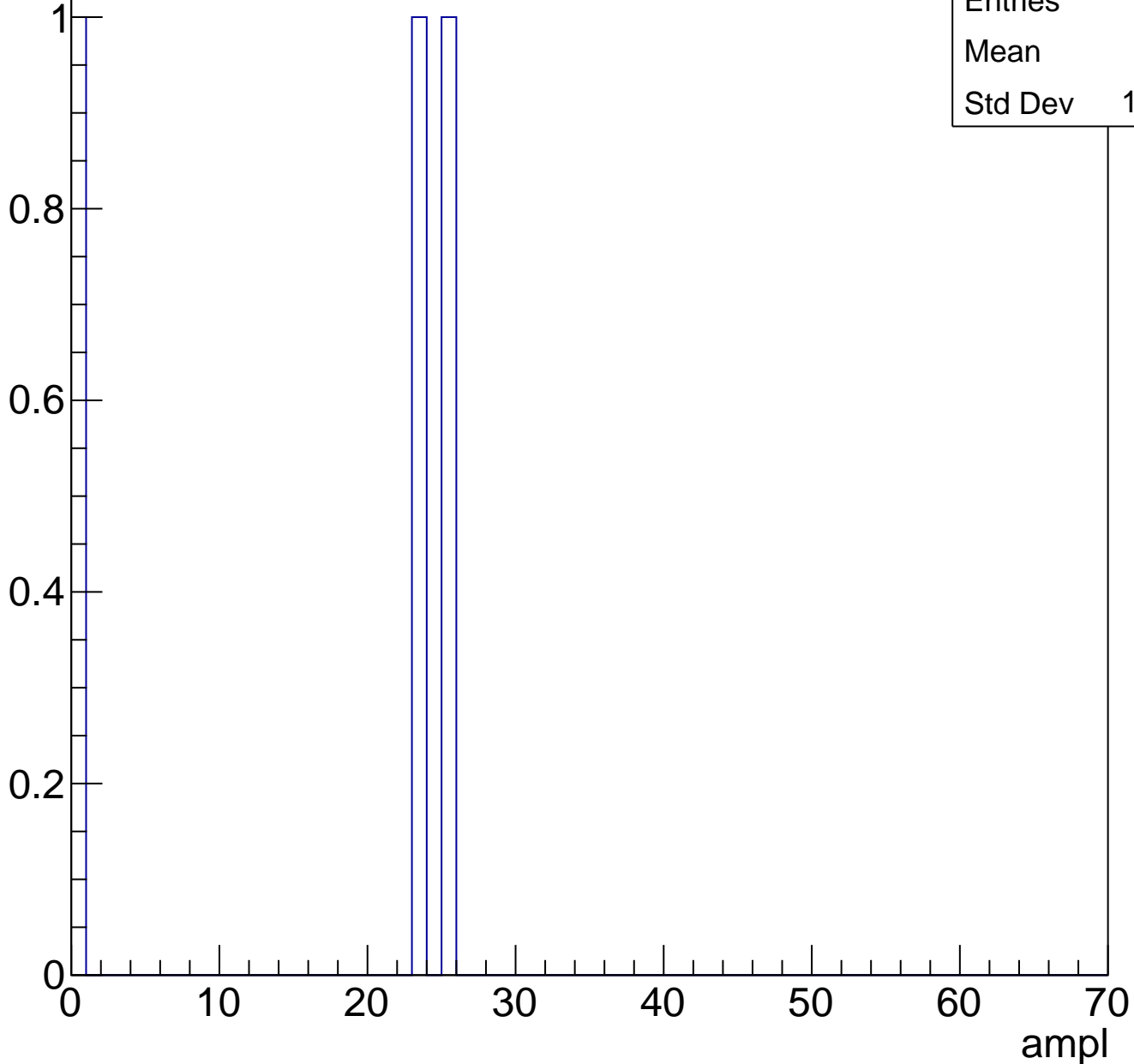
Entry



B0L101S, U25-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



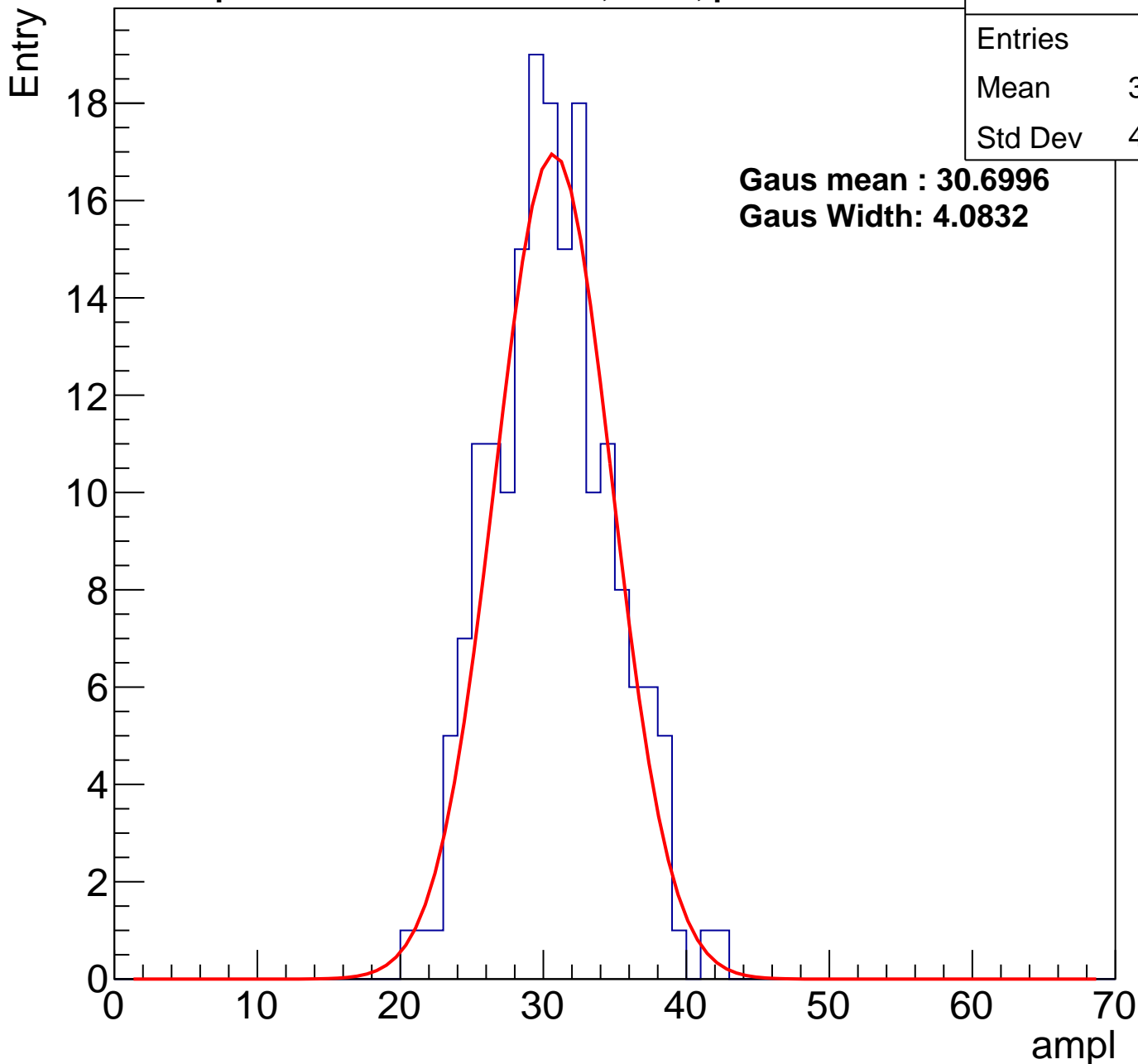
B0L101S, U25-ch34, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	181
Mean	30.18
Std Dev	4.128

Gaus mean : 30.6996

Gaus Width: 4.0832



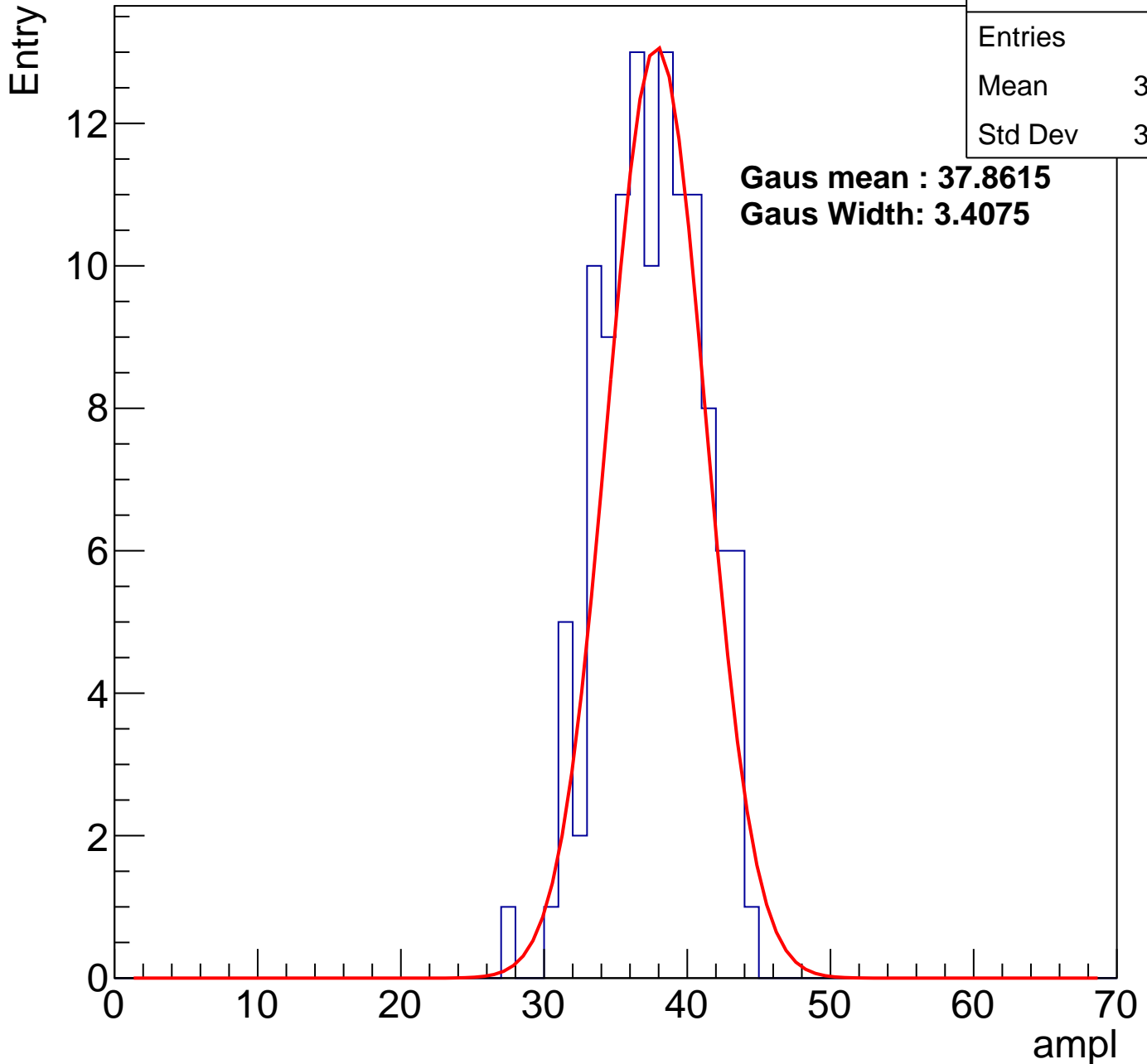
B0L101S, U25-ch34, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	37.12
Std Dev	3.413

Gaus mean : 37.8615

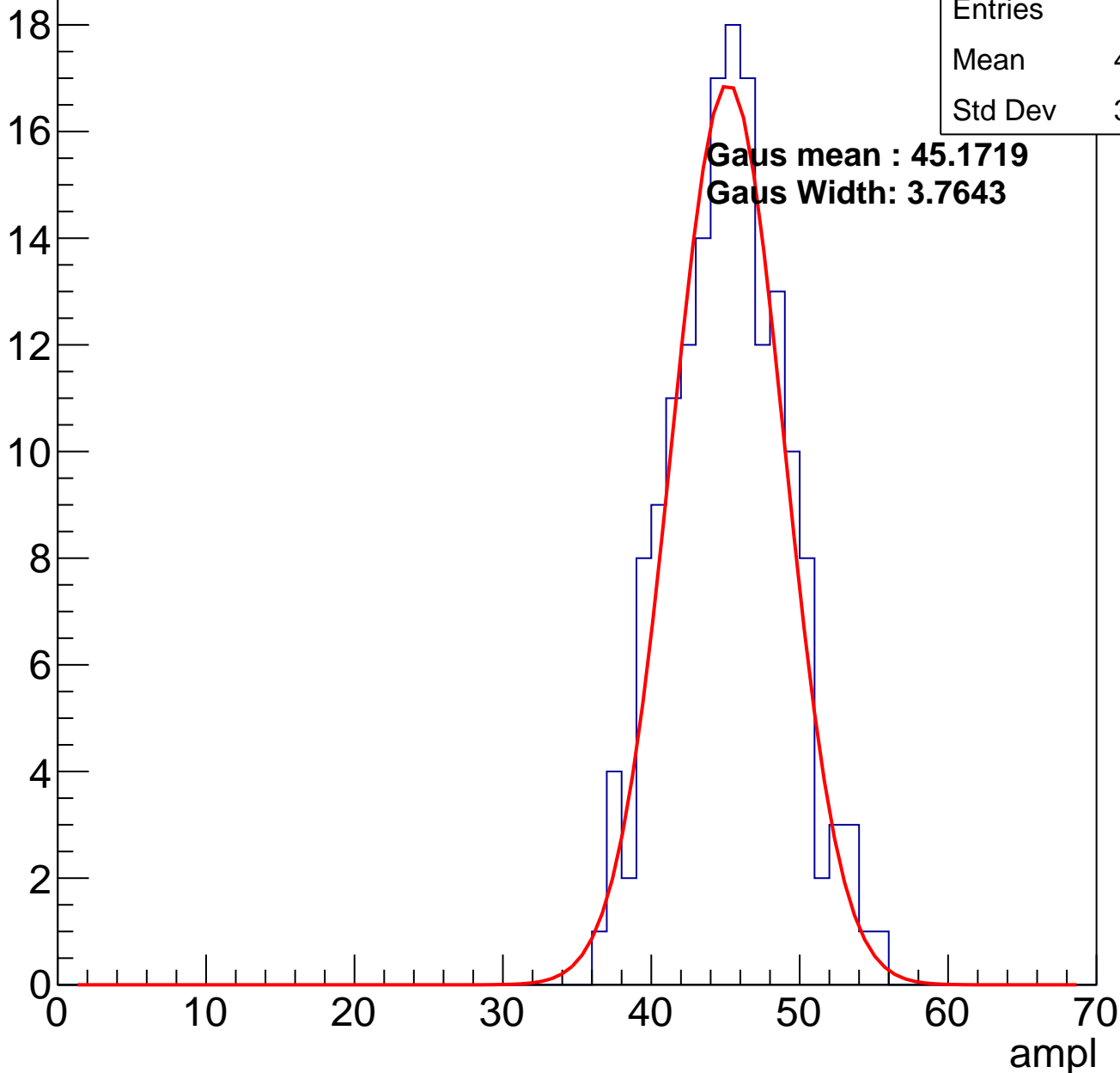
Gaus Width: 3.4075



B0L101S, U25-ch34, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch34, adc3

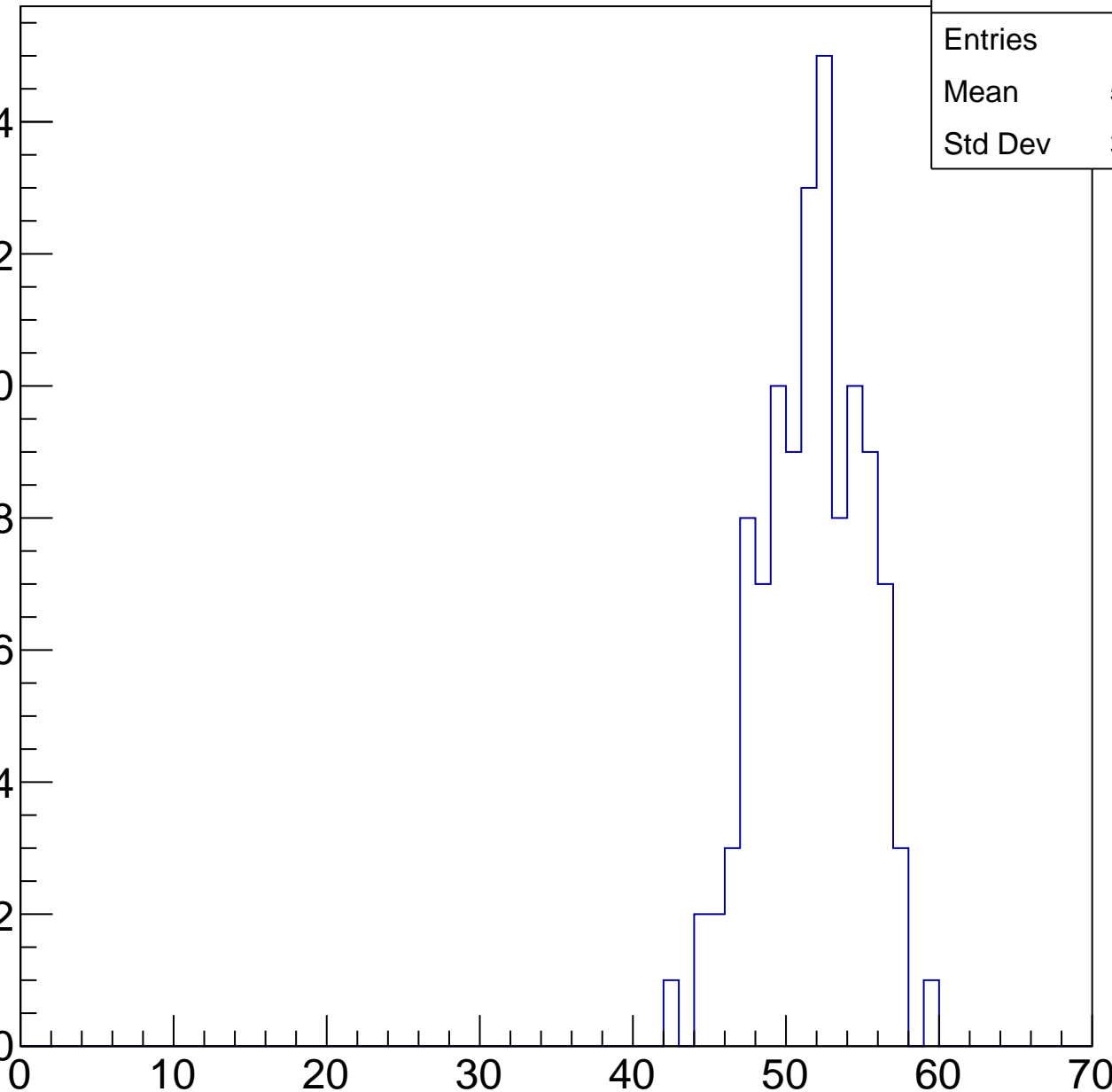
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	51.24
Std Dev	3.316

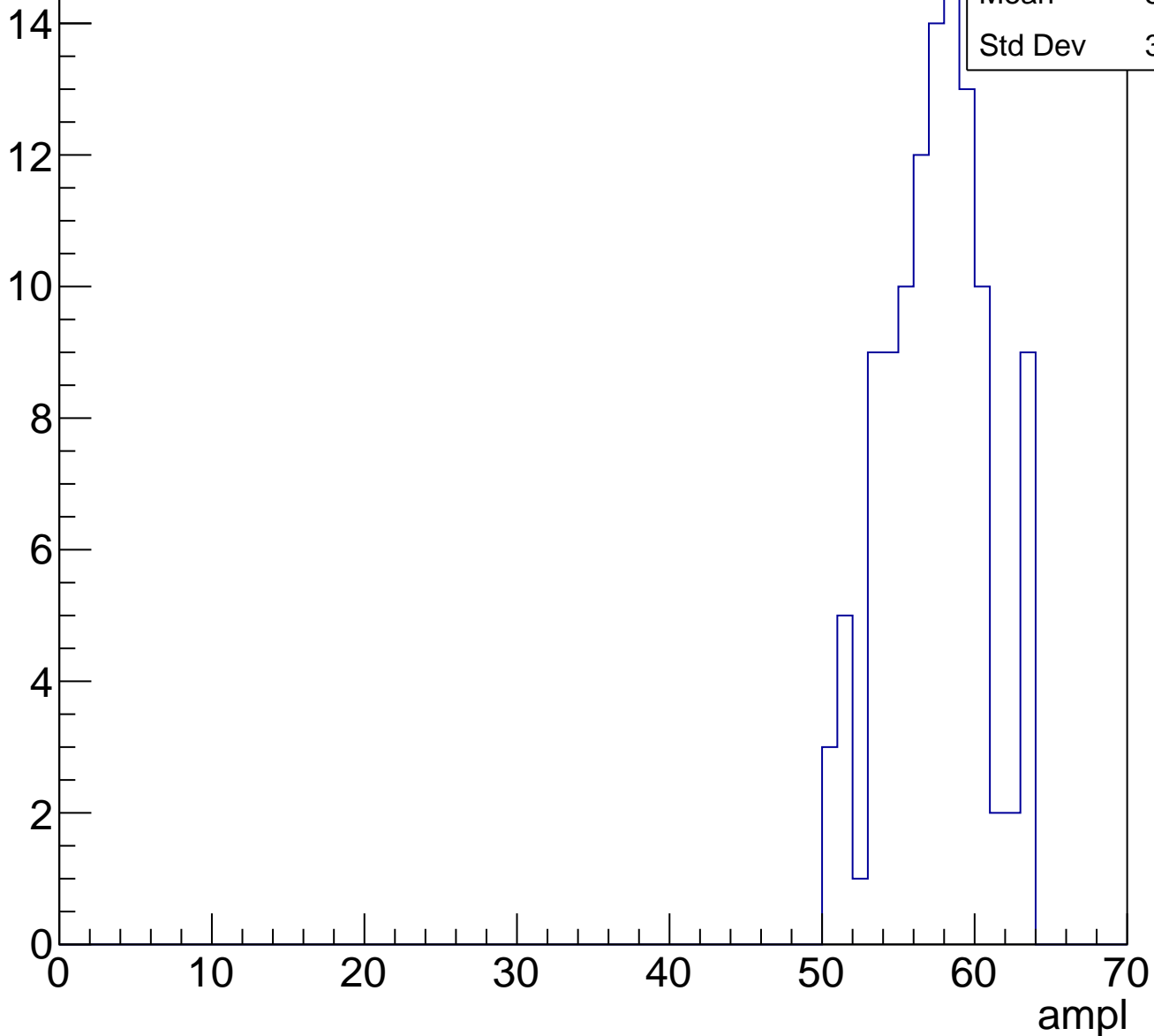
ampl



B0L101S, U25-ch34, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch34, adc5

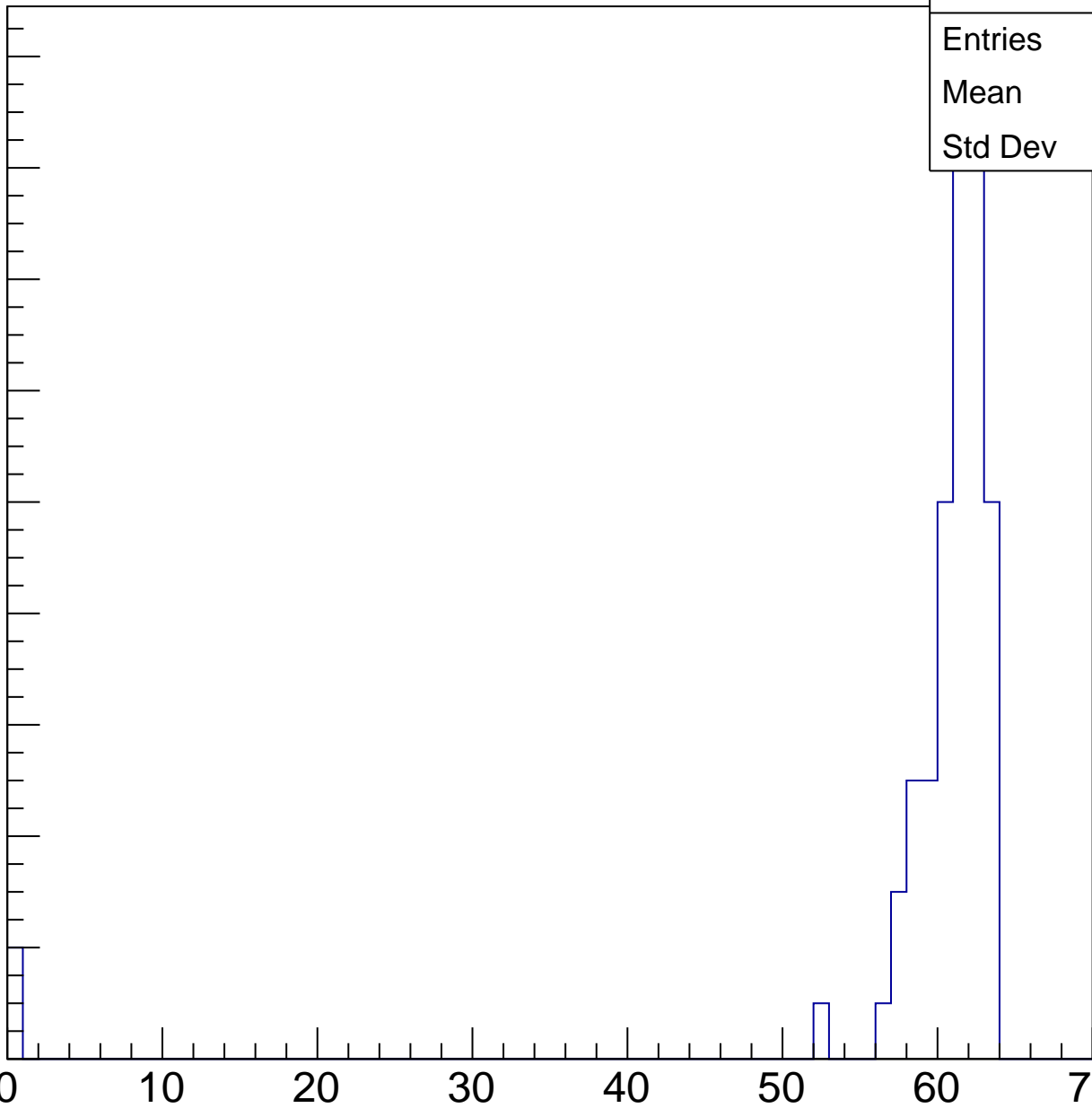
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	72
Mean	58.97
Std Dev	10.16

ampl



B0L101S, U25-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch35, adc0

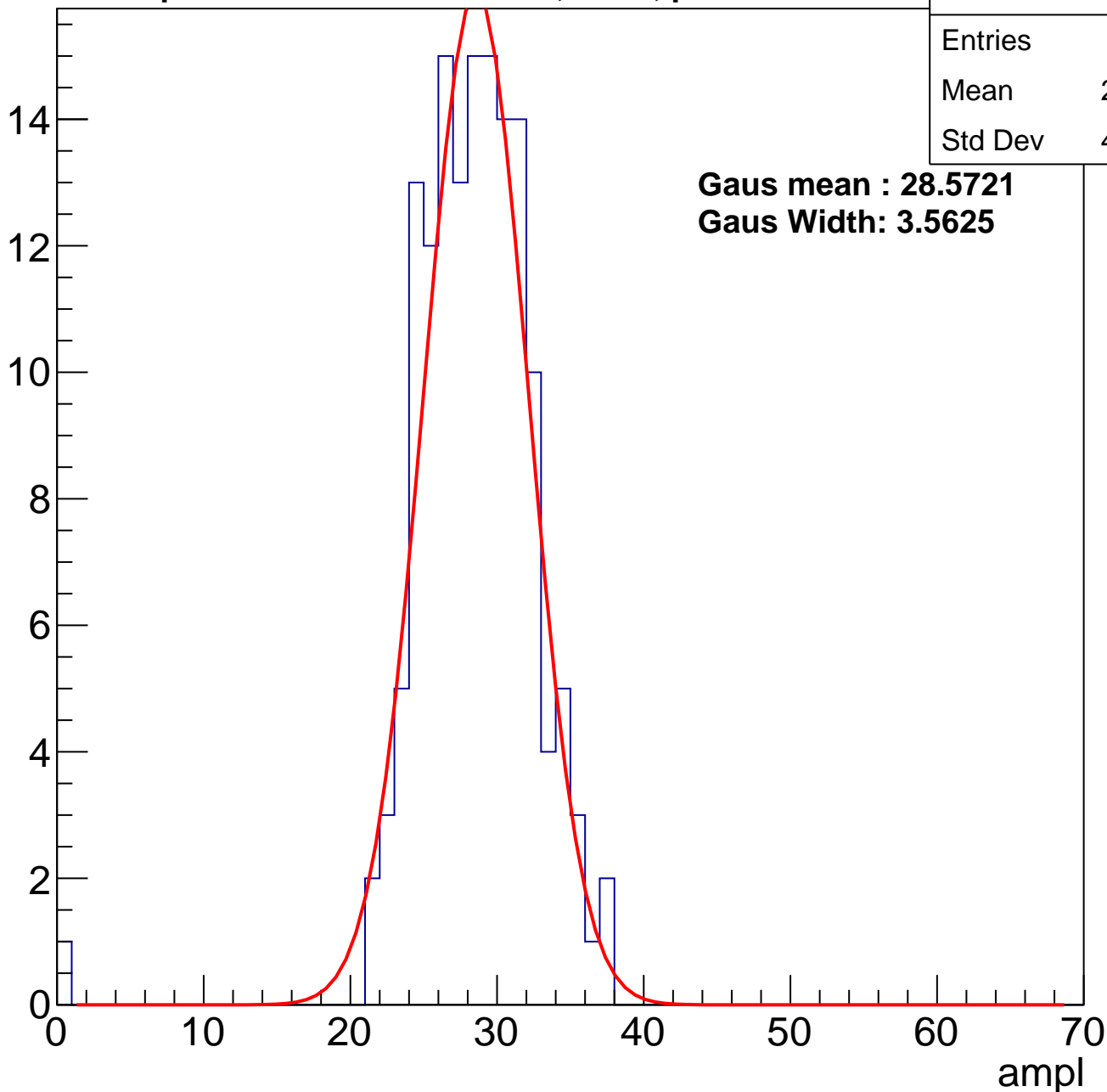
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	28.04
Std Dev	4.135

Gaus mean : 28.5721

Gaus Width: 3.5625

Entry



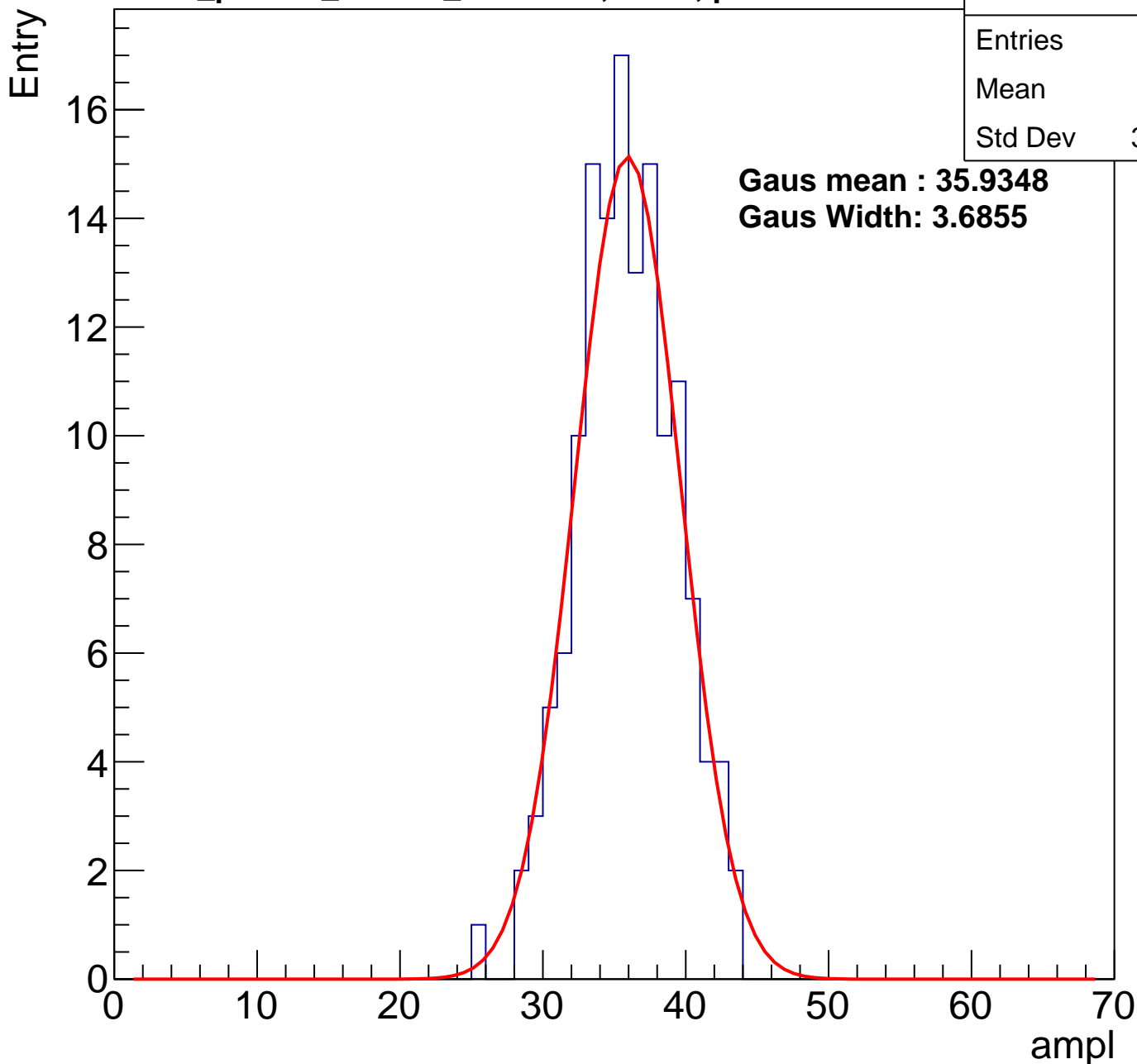
B0L101S, U25-ch35, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	35.4
Std Dev	3.447

Gaus mean : 35.9348

Gaus Width: 3.6855



B0L101S, U25-ch35, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

147

Mean

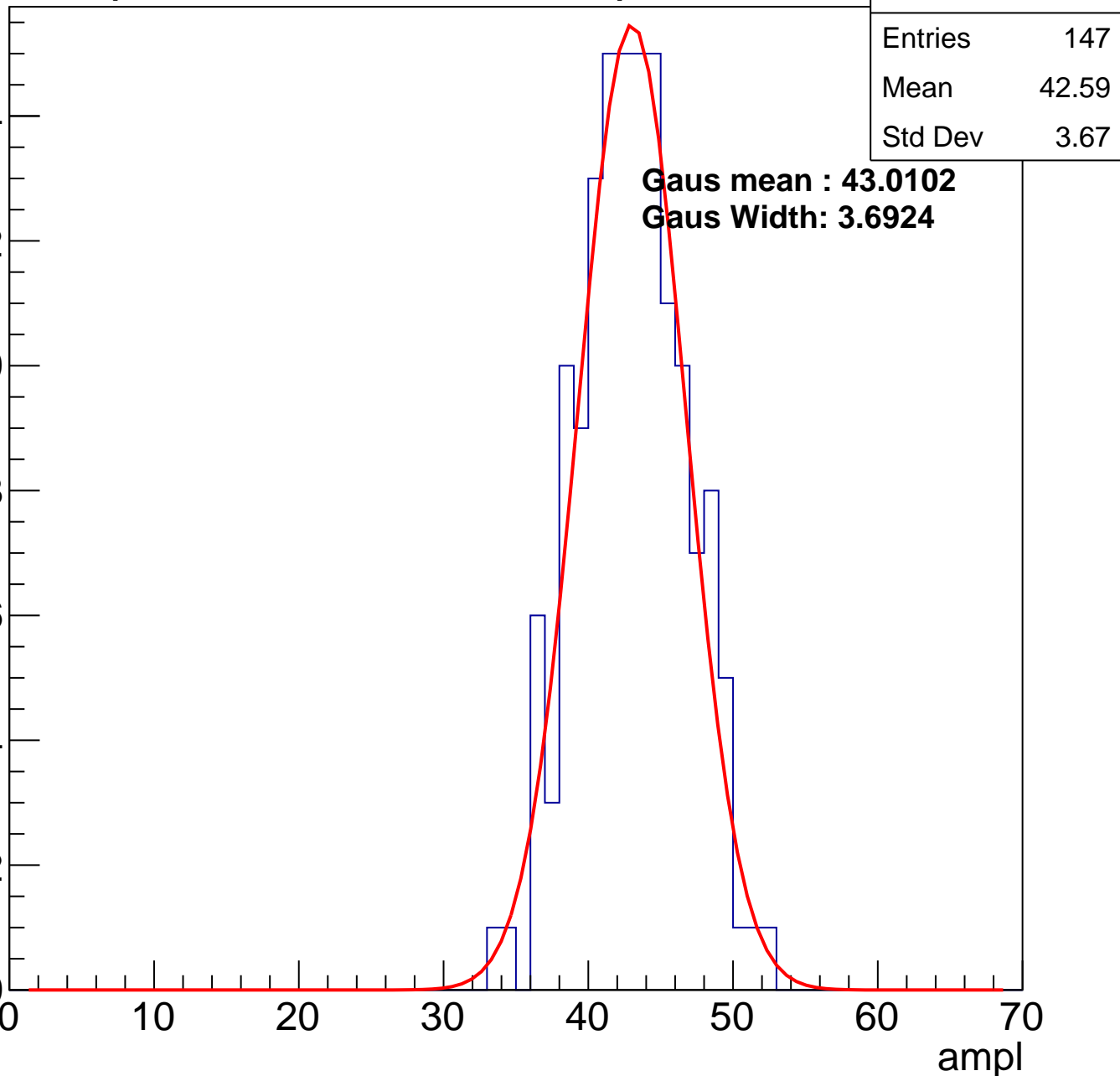
42.59

Std Dev

3.67

Gaus mean : 43.0102

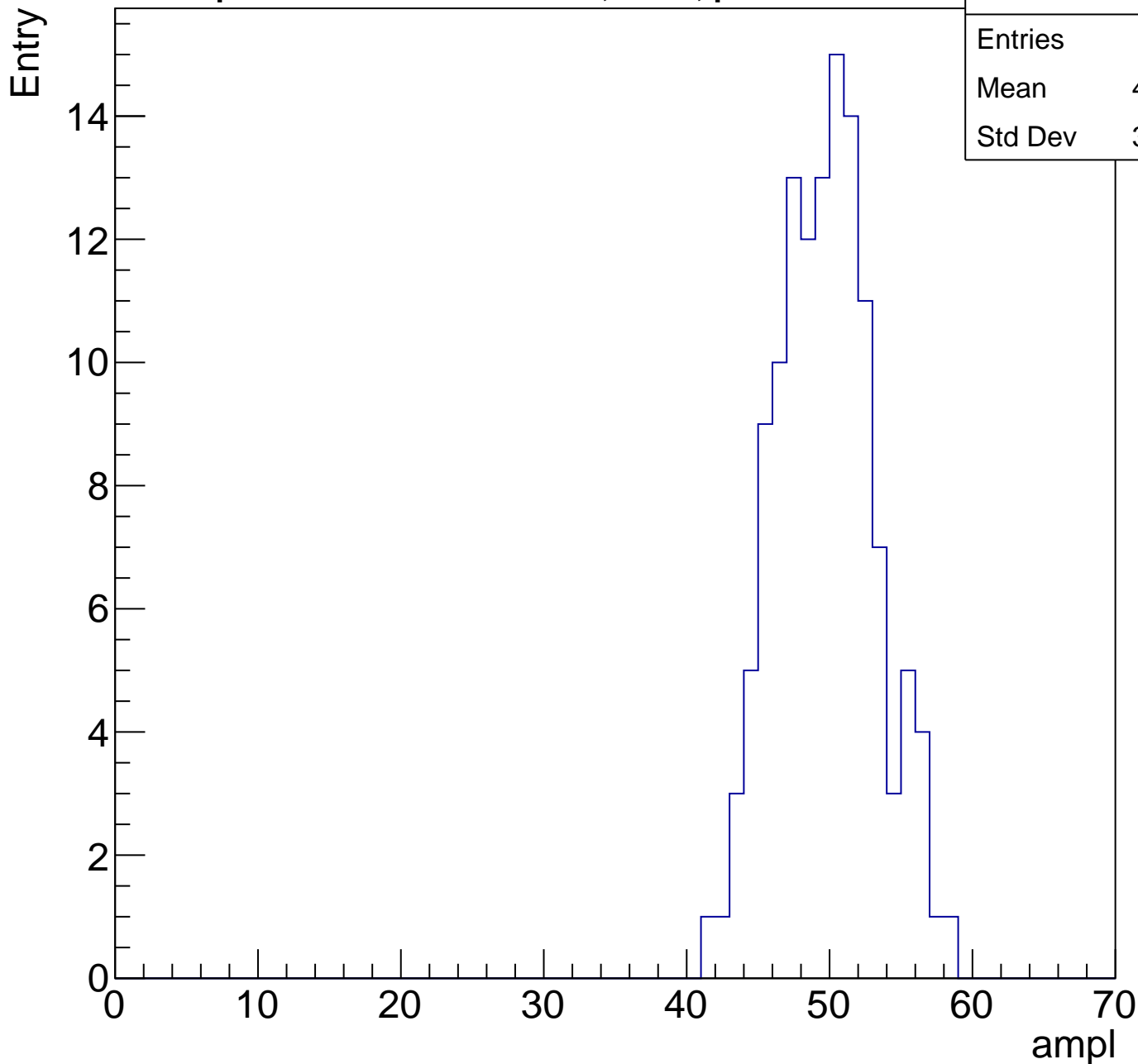
Gaus Width: 3.6924



B0L101S, U25-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	49.25
Std Dev	3.439



B0L101S, U25-ch35, adc4

calib_packv5_042523_0143.root, FC#1, port C1

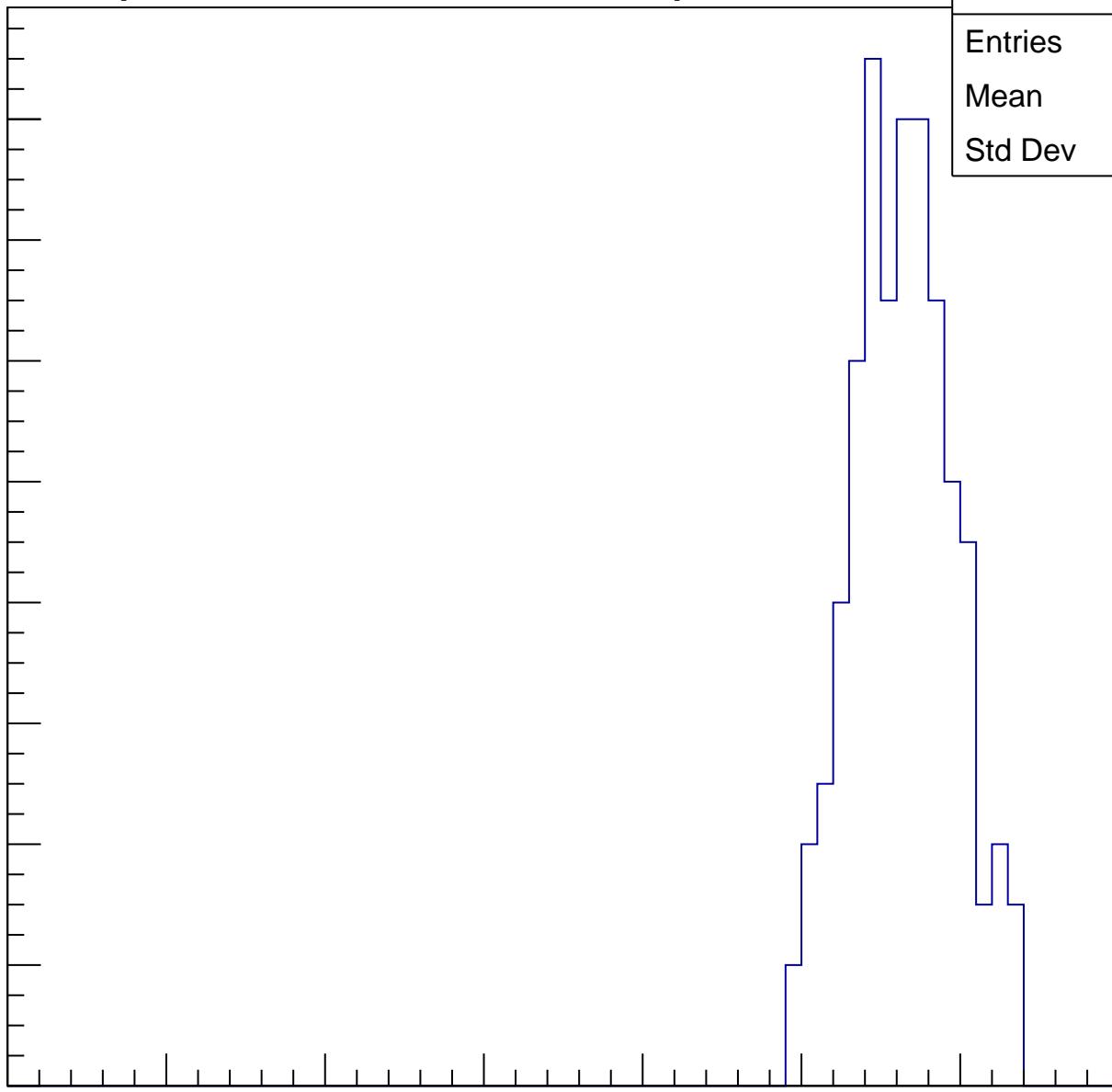
Entries	135
Mean	55.93
Std Dev	3.164

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch35, adc5

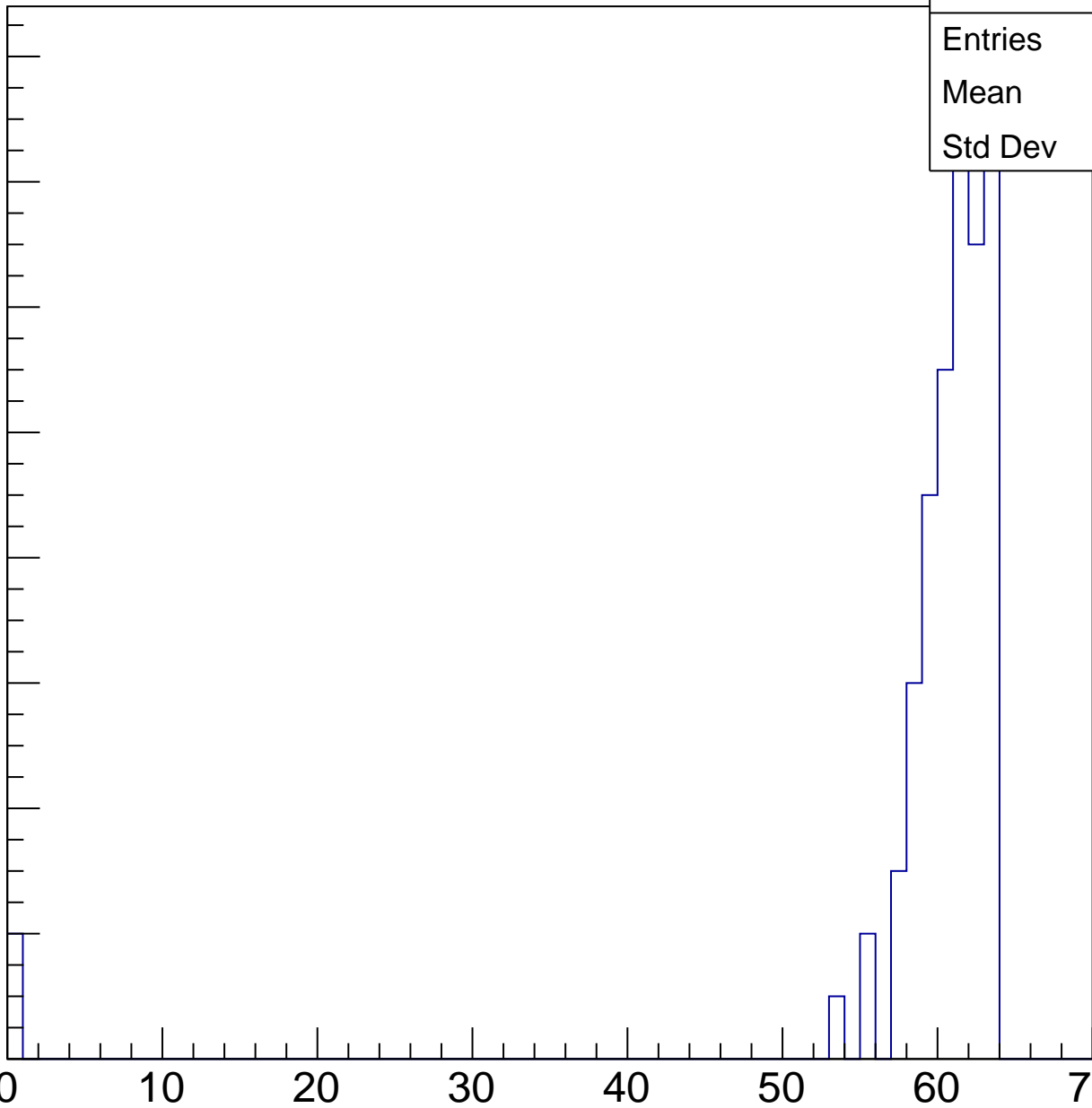
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	58.97
Std Dev	9.793

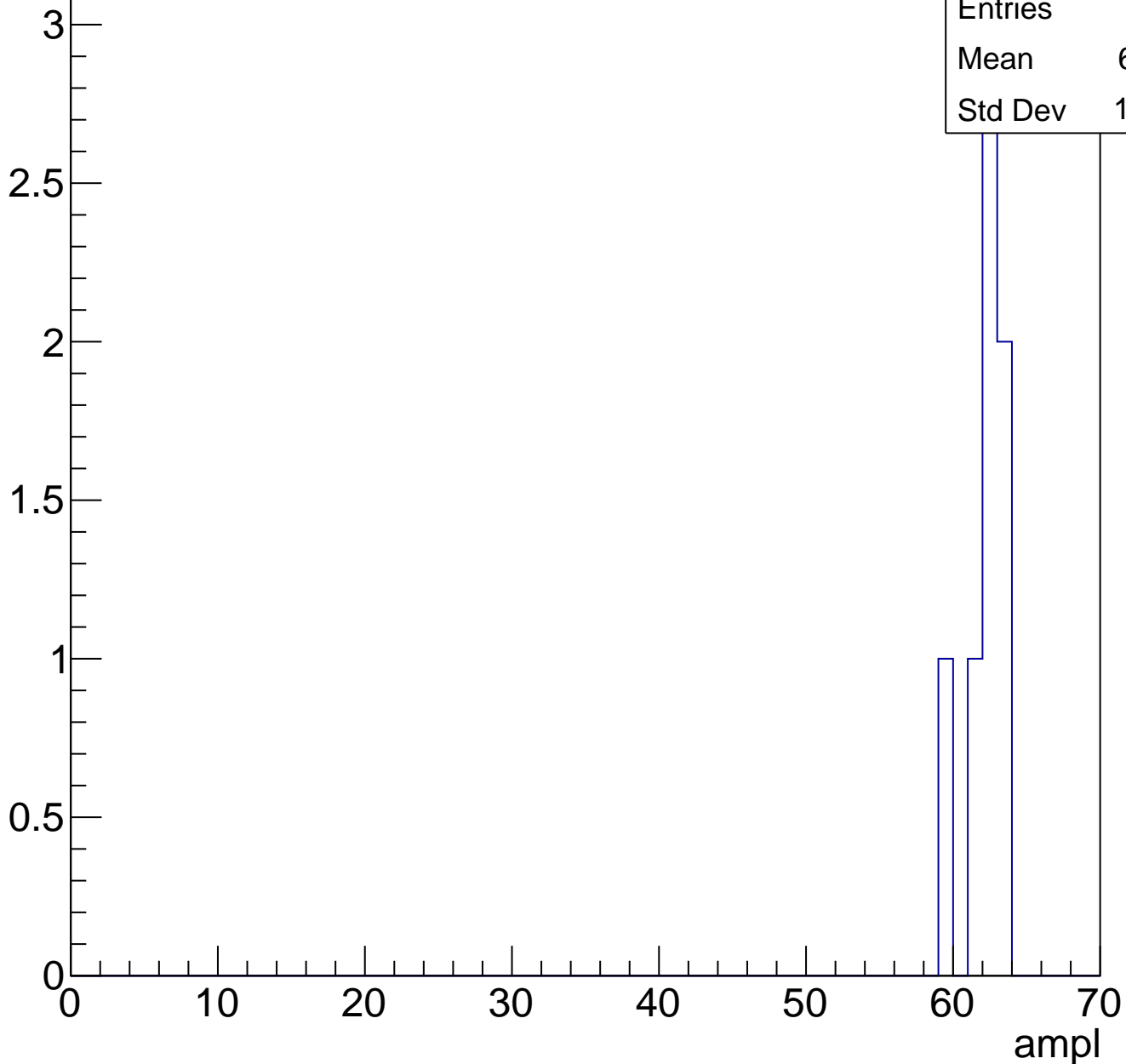
ampl



B0L101S, U25-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

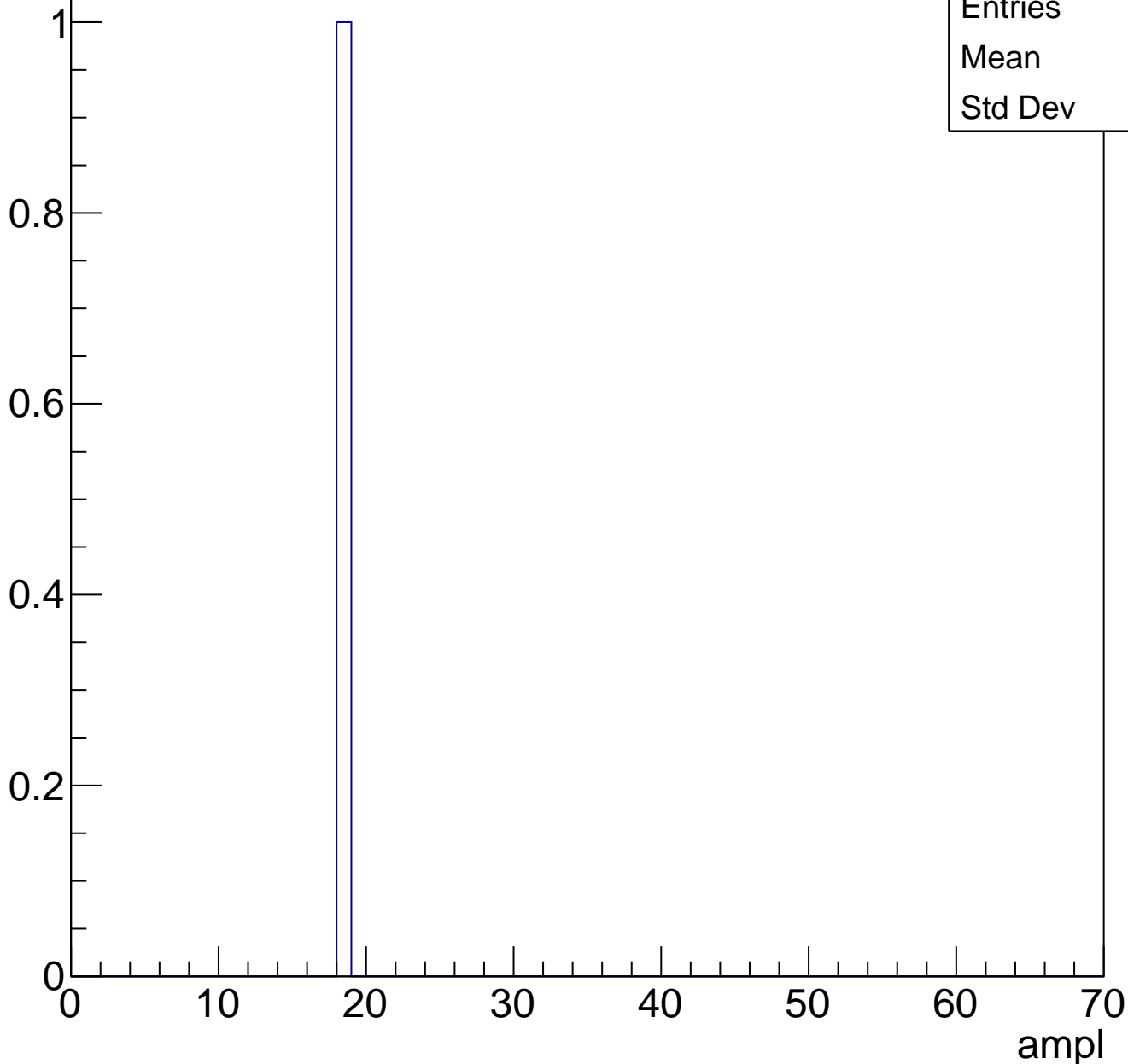
Entry



B0L101S, U25-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

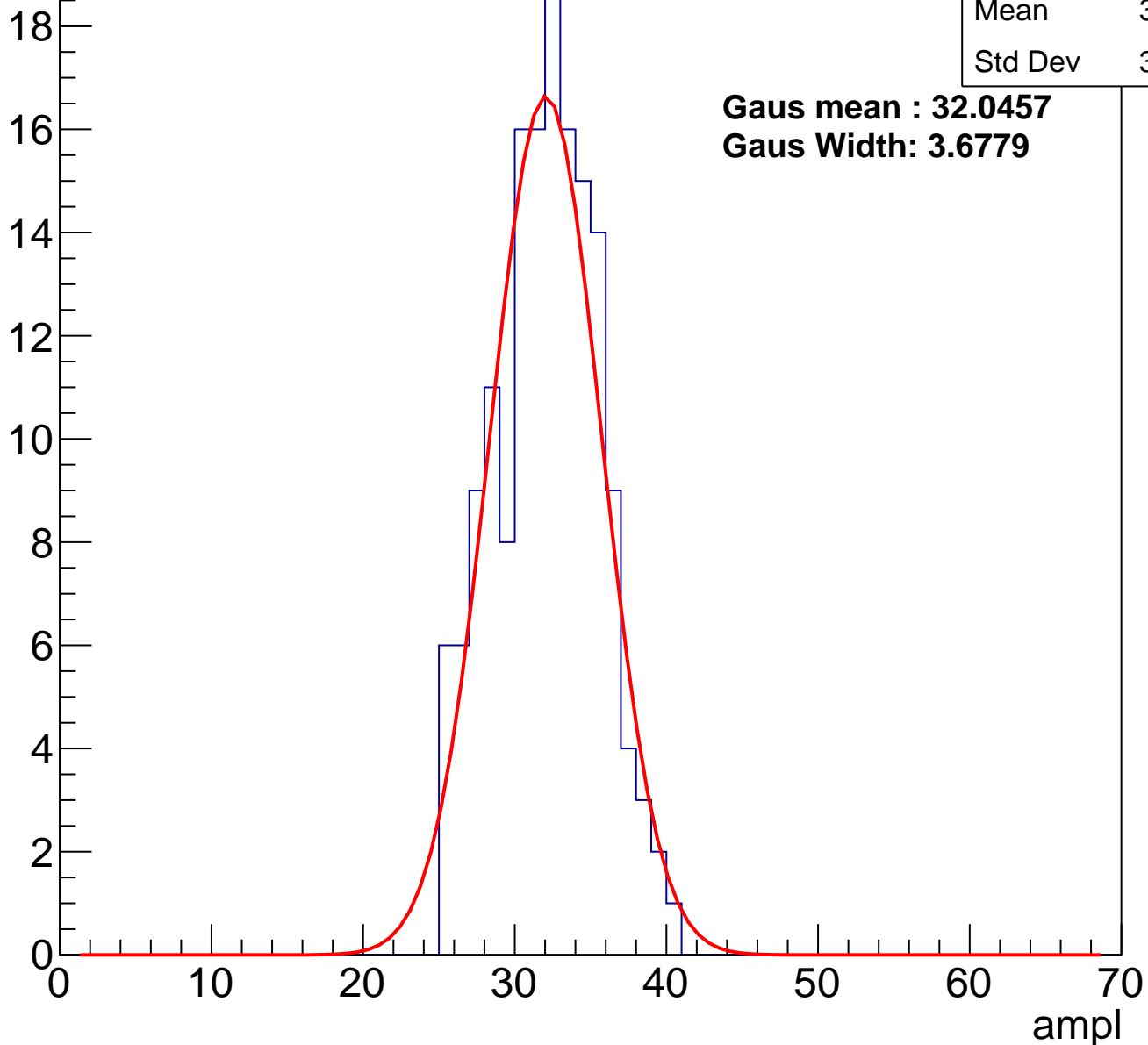
Entry



B0L101S, U25-ch36, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch36, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	38.29
Std Dev	3.118

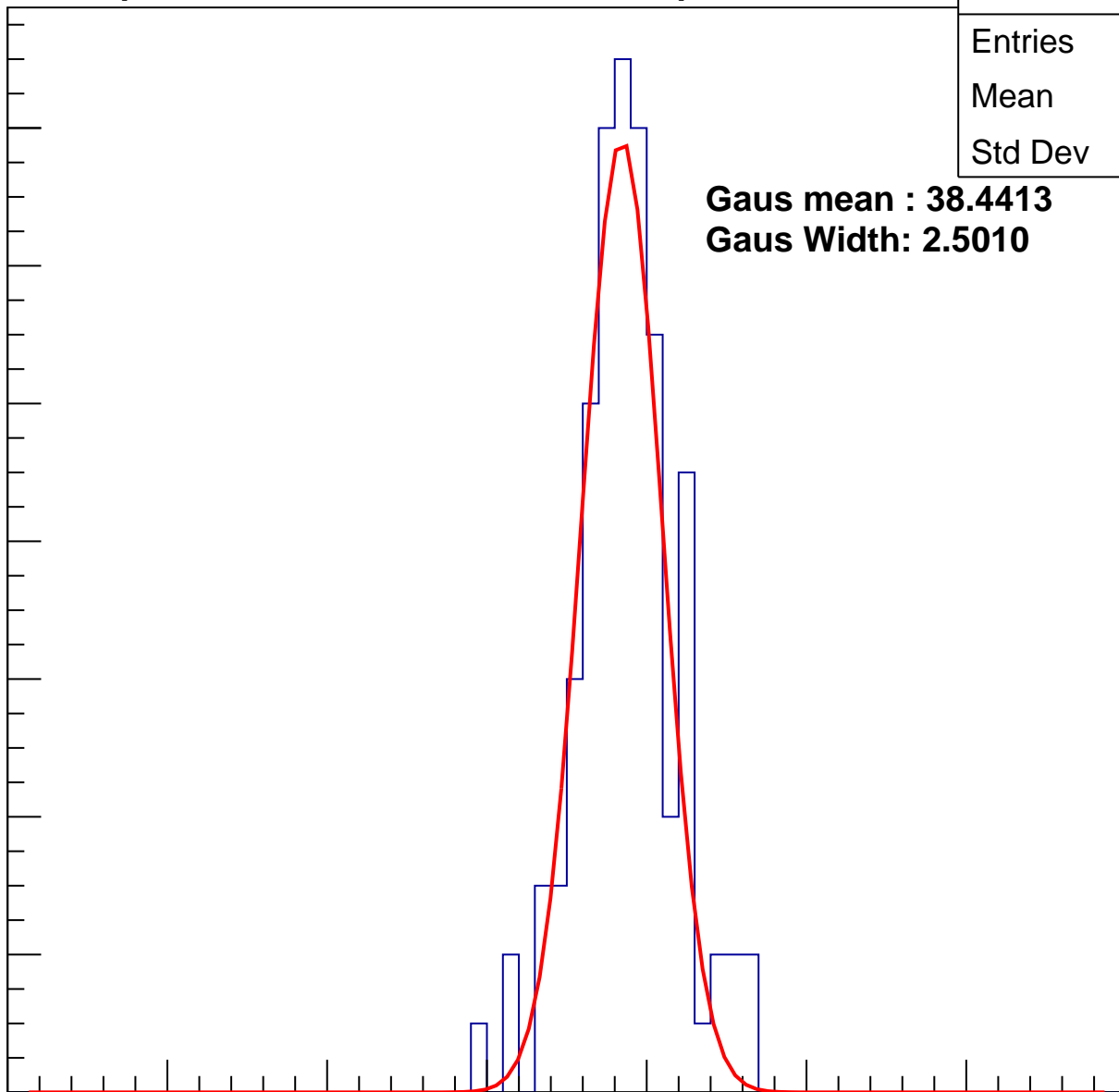
Gaus mean : 38.4413
Gaus Width: 2.5010

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

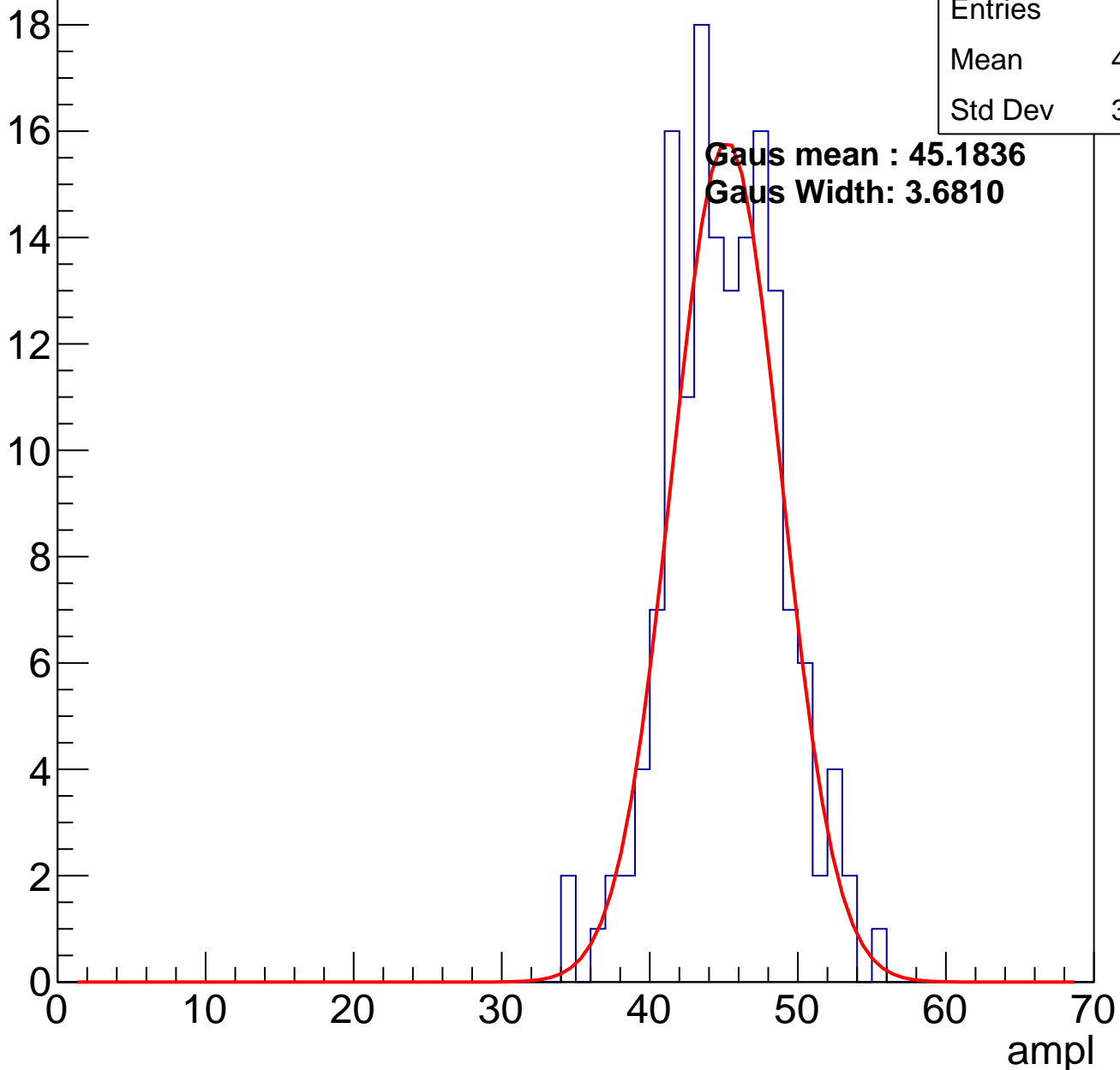
ampl



B0L101S, U25-ch36, adc2

calib_packv5_042523_0143.root, FC#1, port C1

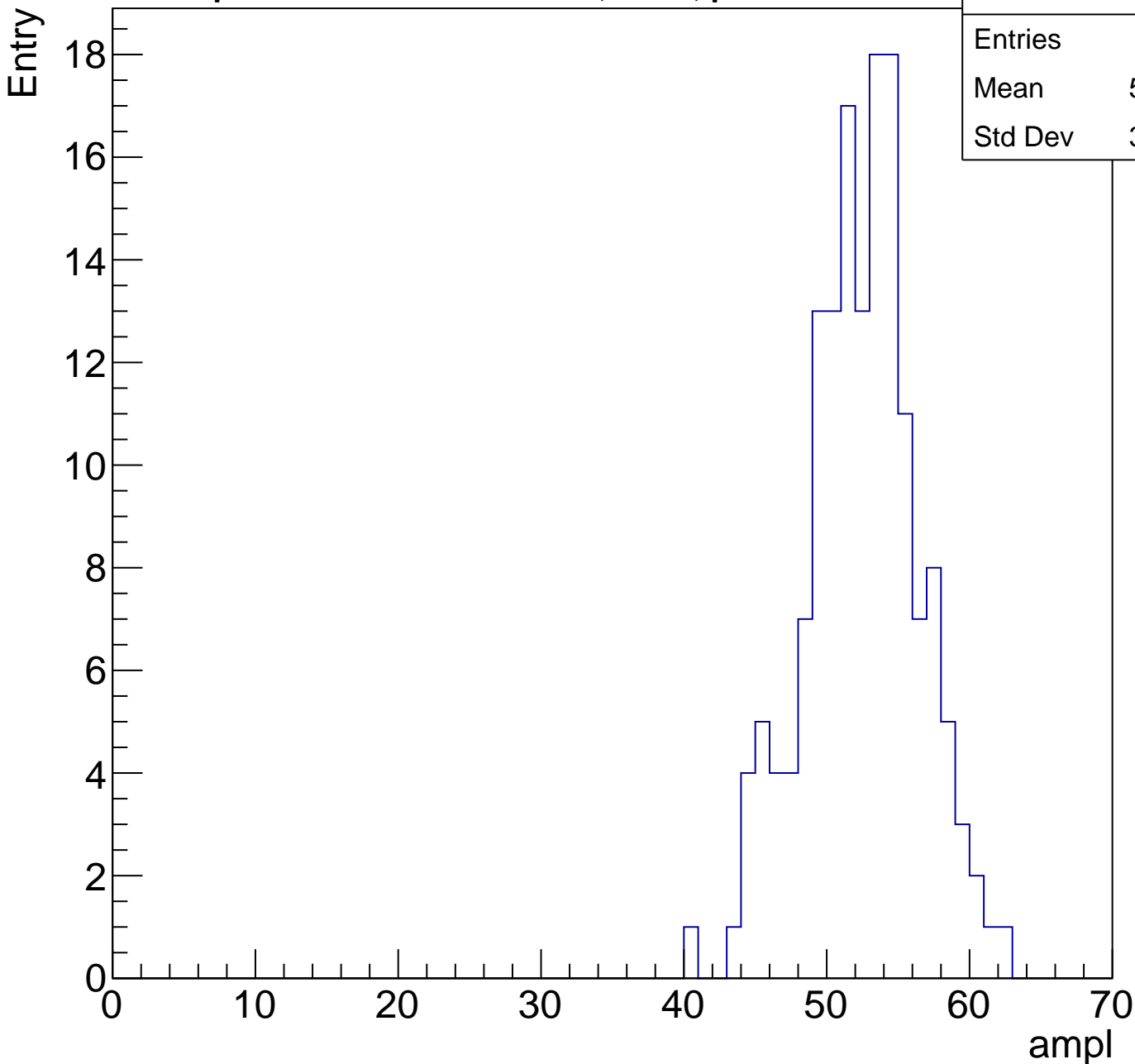
Entry



B0L101S, U25-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

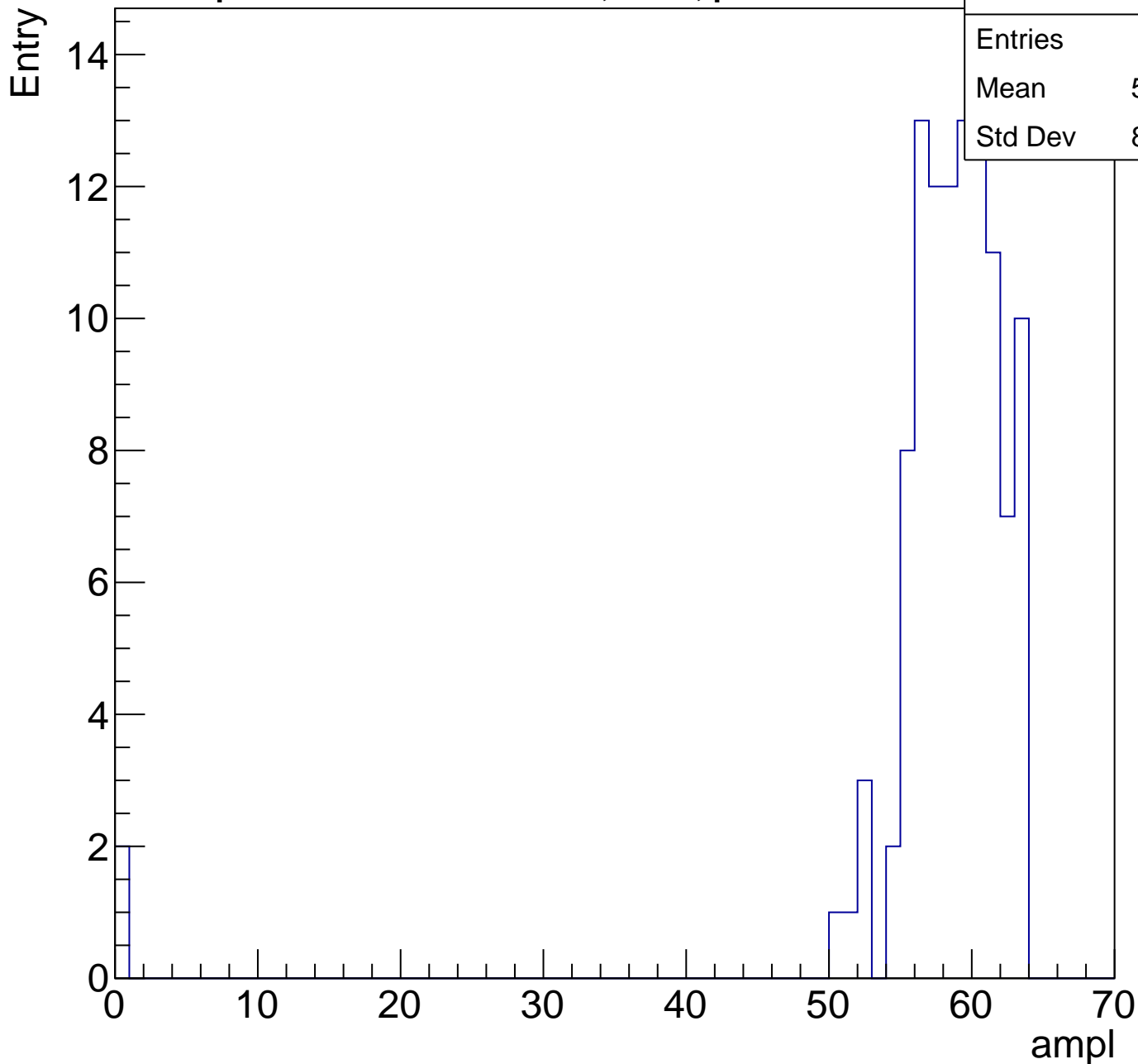
Entries	156
Mean	51.99
Std Dev	3.926



B0L101S, U25-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	57.39
Std Dev	8.353



B0L101S, U25-ch36, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	41
Mean	61.27
Std Dev	1.531

Entry

12

10

8

6

4

2

0

0

10

20

30

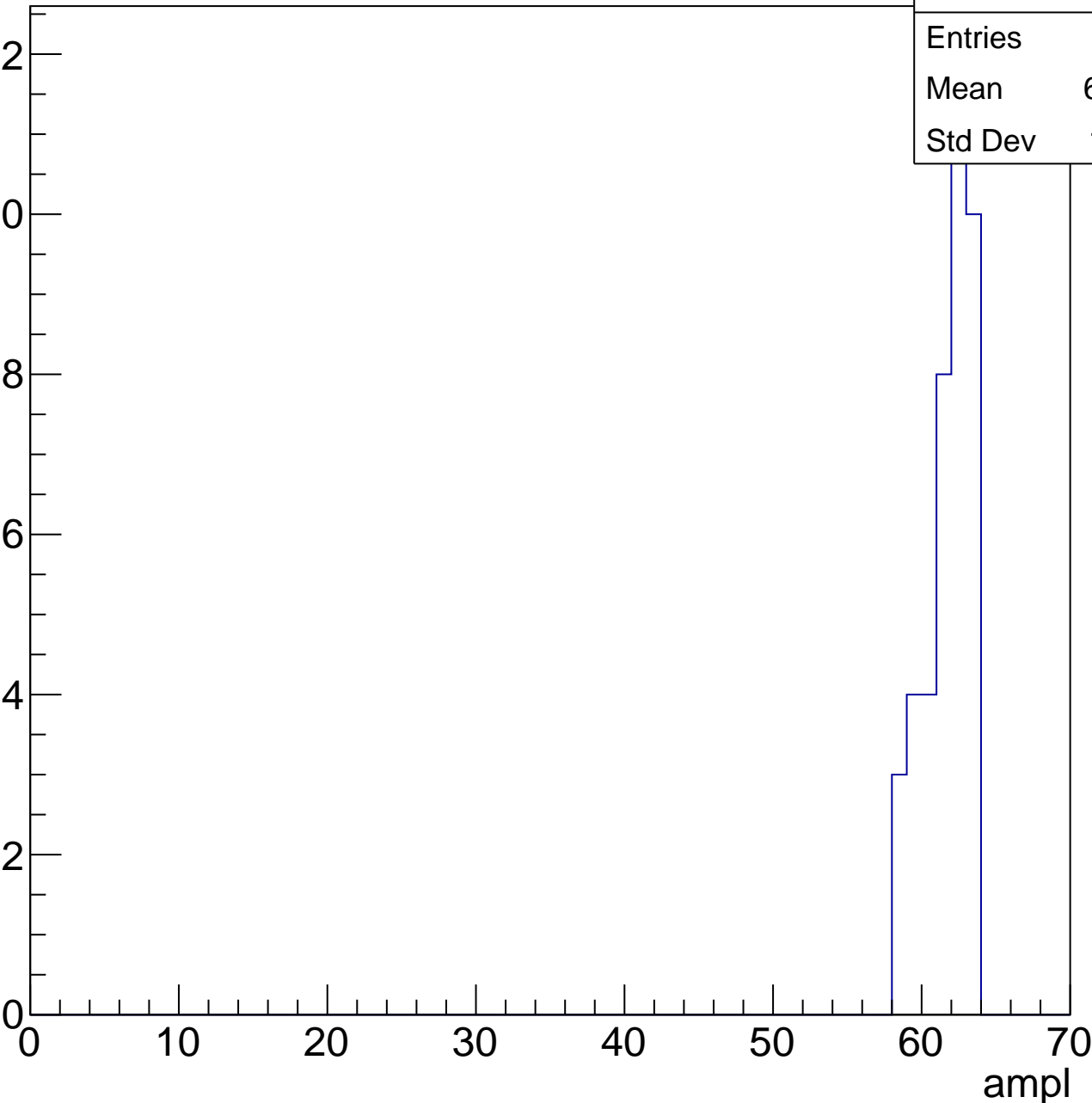
40

50

60

70

ampl



B0L101S, U25-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch37, adc0

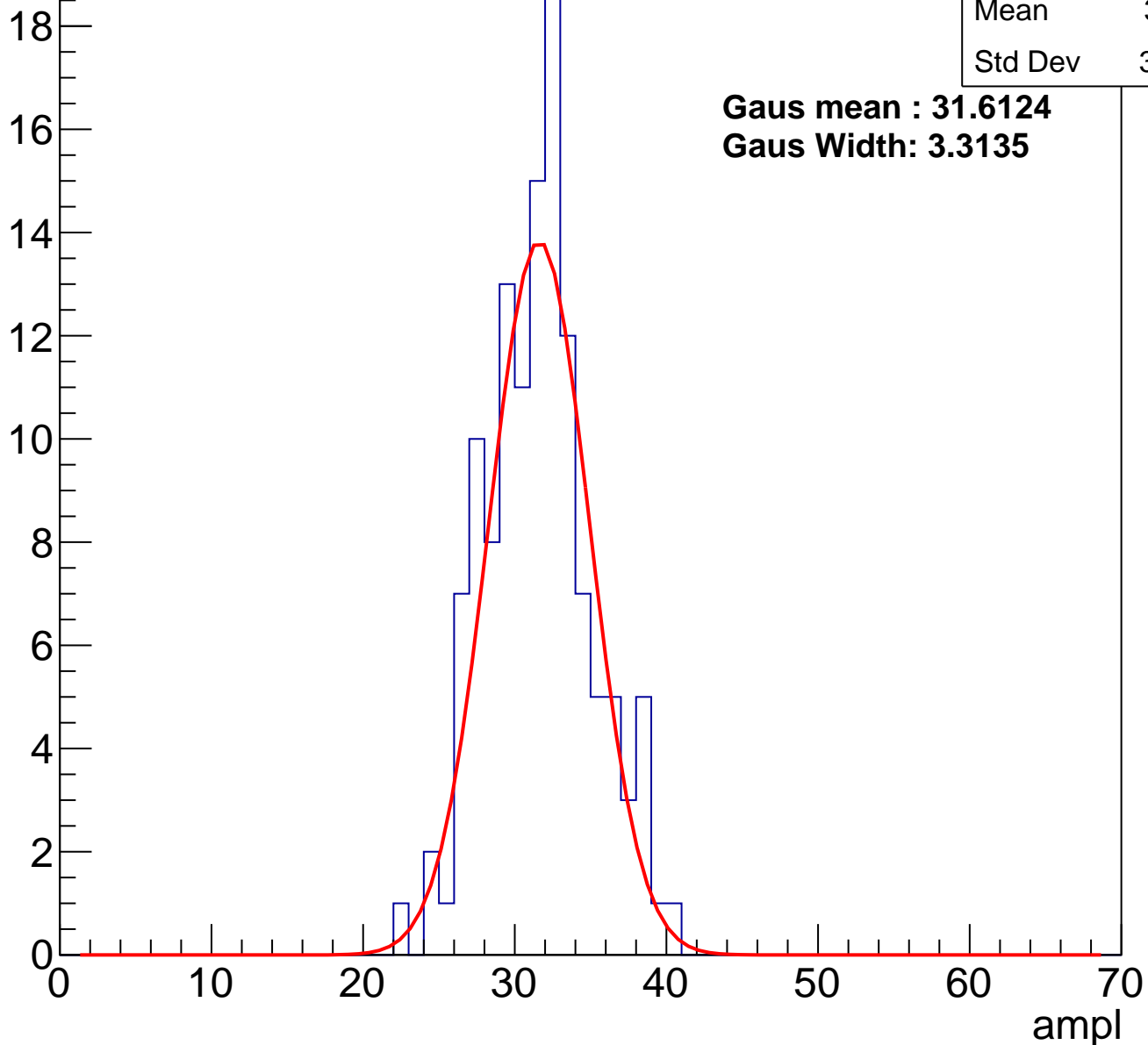
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	31.11
Std Dev	3.469

Gaus mean : 31.6124

Gaus Width: 3.3135

Entry



B0L101S, U25-ch37, adc1

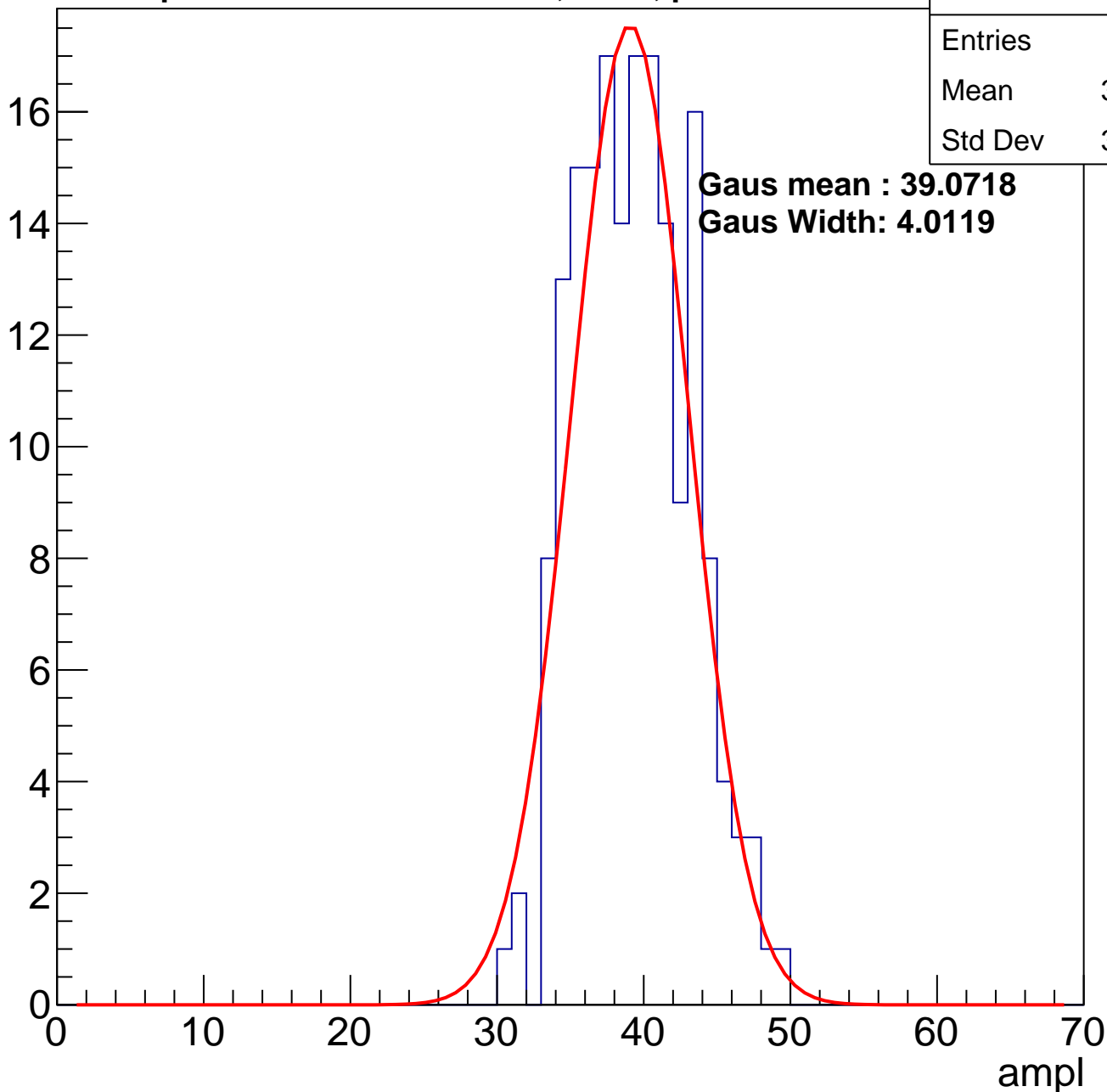
calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	38.85
Std Dev	3.784

Gaus mean : 39.0718

Gaus Width: 4.0119

Entry



B0L101S, U25-ch37, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

154

Mean

47.01

Std Dev

3.704

Gaus mean : 47.5635

Gaus Width: 3.6512

0

10

20

30

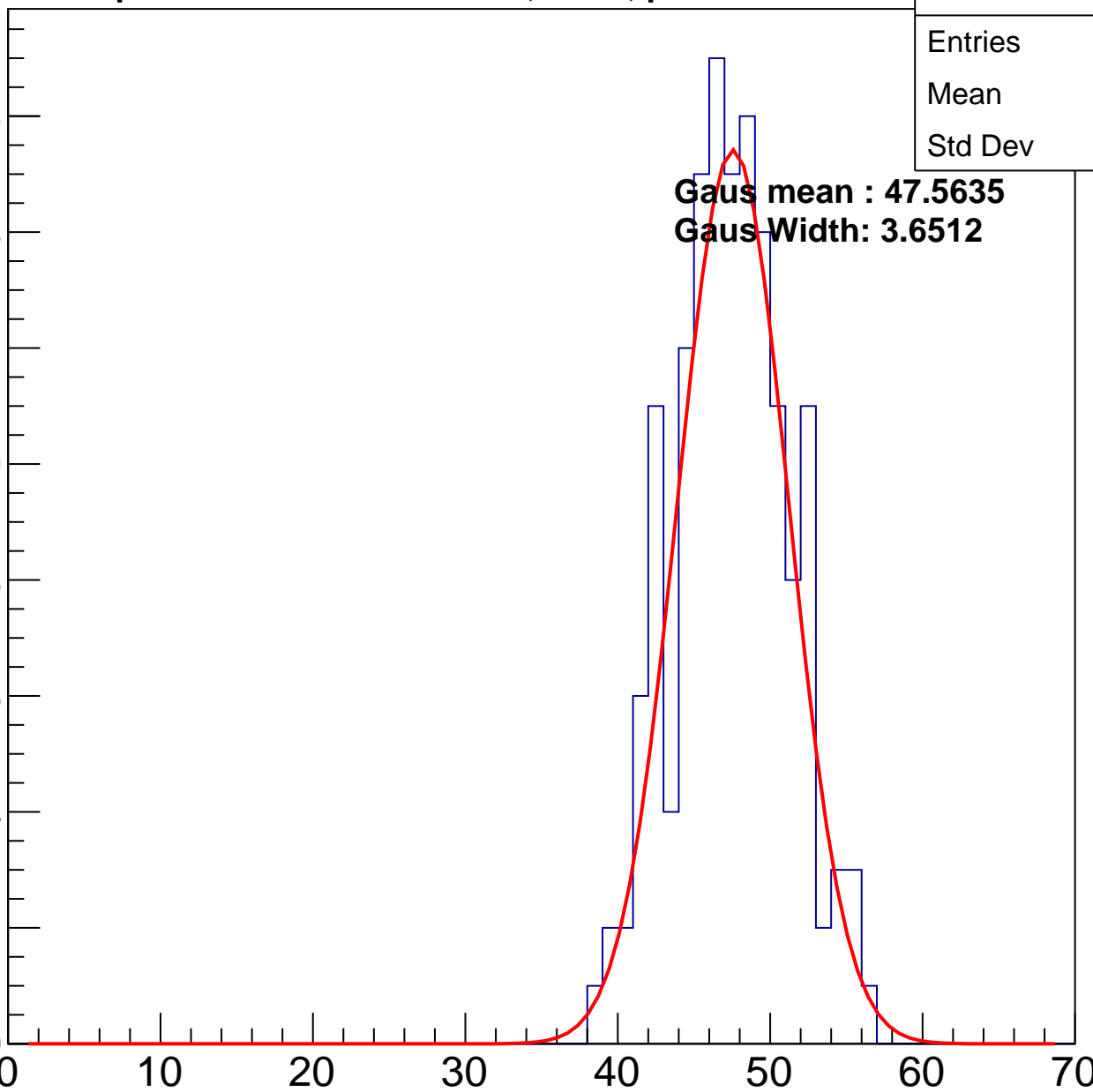
40

50

60

70

ampl



B0L101S, U25-ch37, adc3

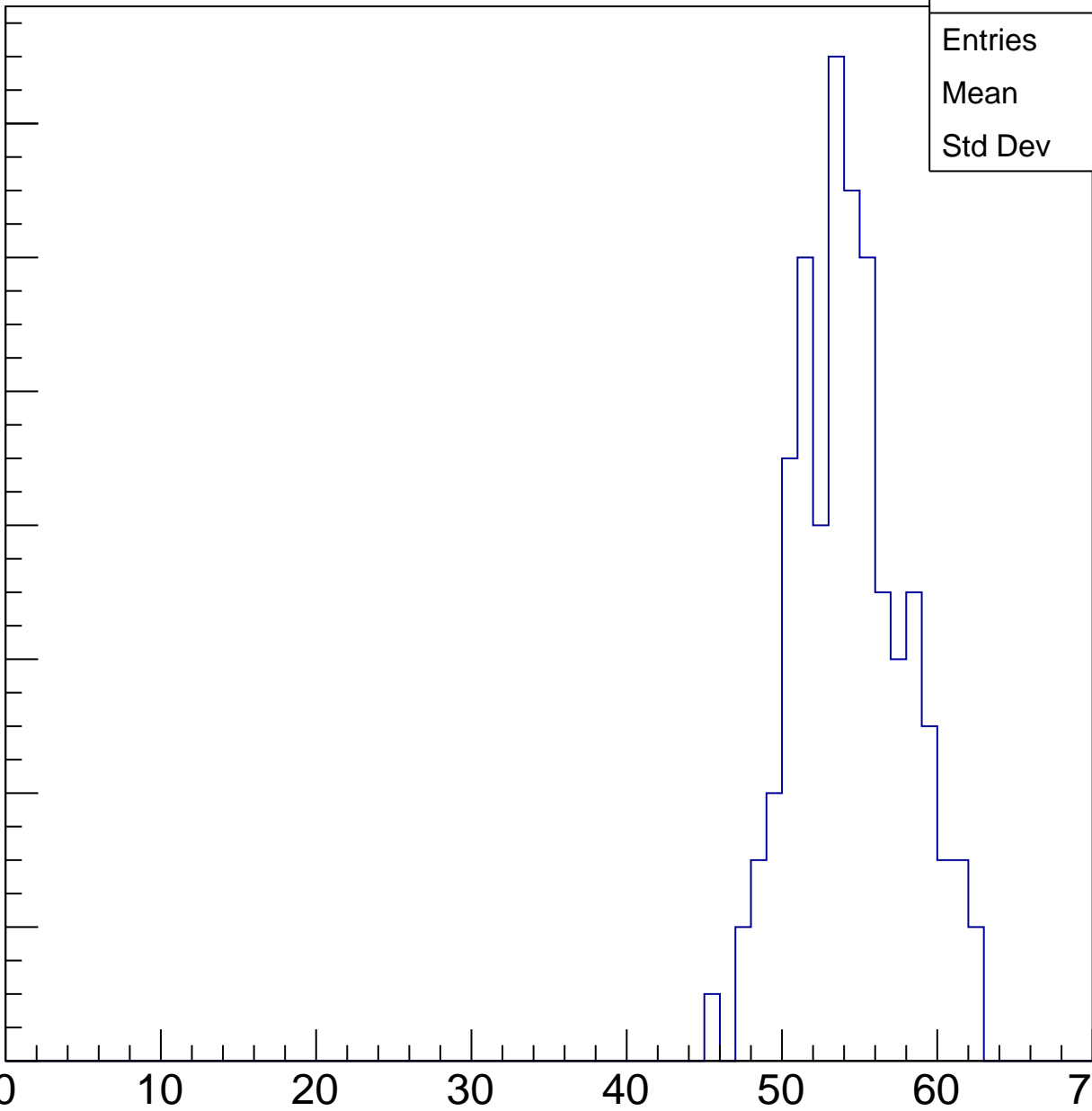
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	112
Mean	53.89
Std Dev	3.534

ampl



B0L101S, U25-ch37, adc4

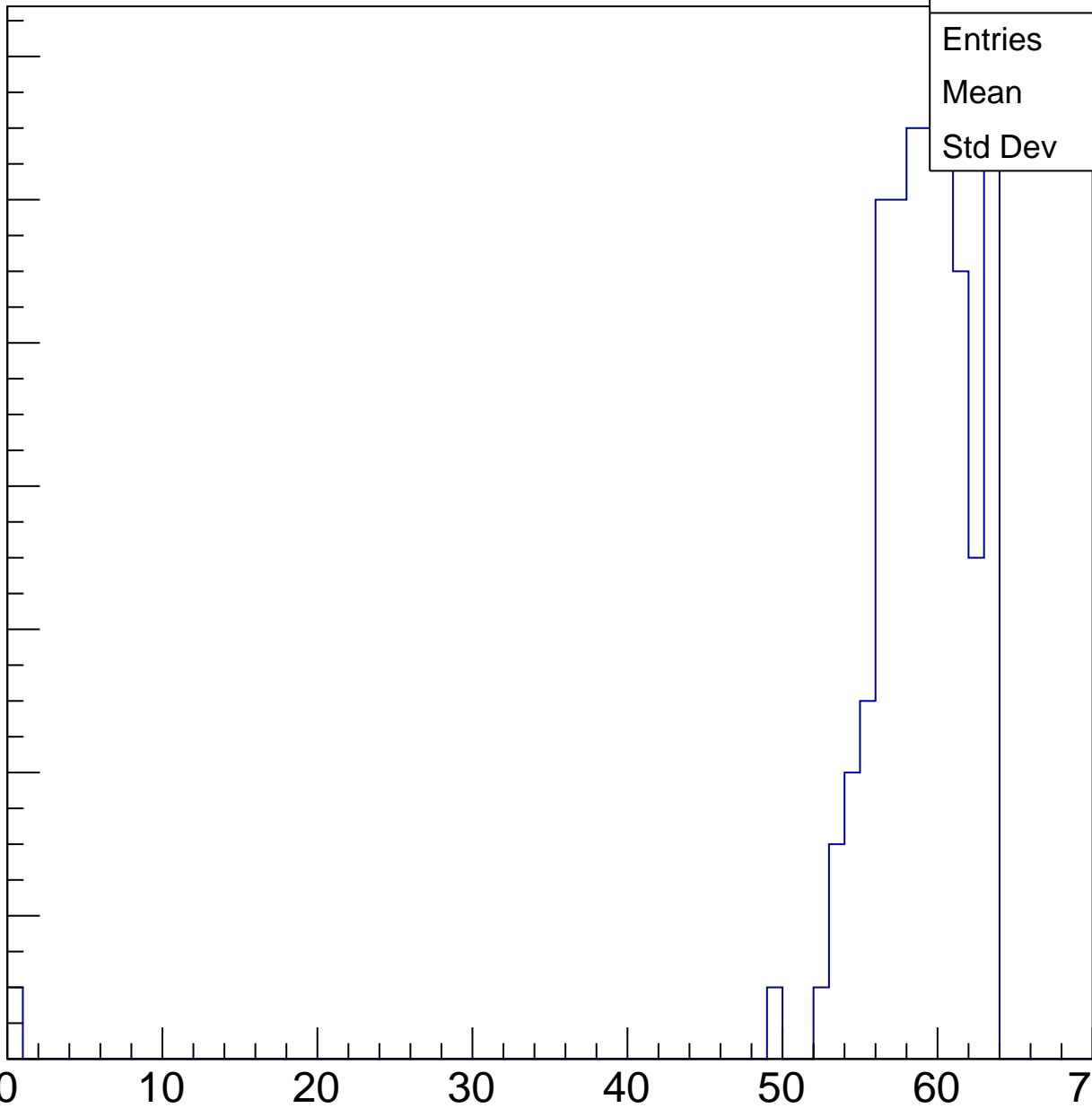
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	58.15
Std Dev	6.262

ampl



B0L101S, U25-ch37, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

ampl

Entries	31
Mean	59.29
Std Dev	10.94

0

10

20

30

40

50

60

70

B0L101S, U25-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

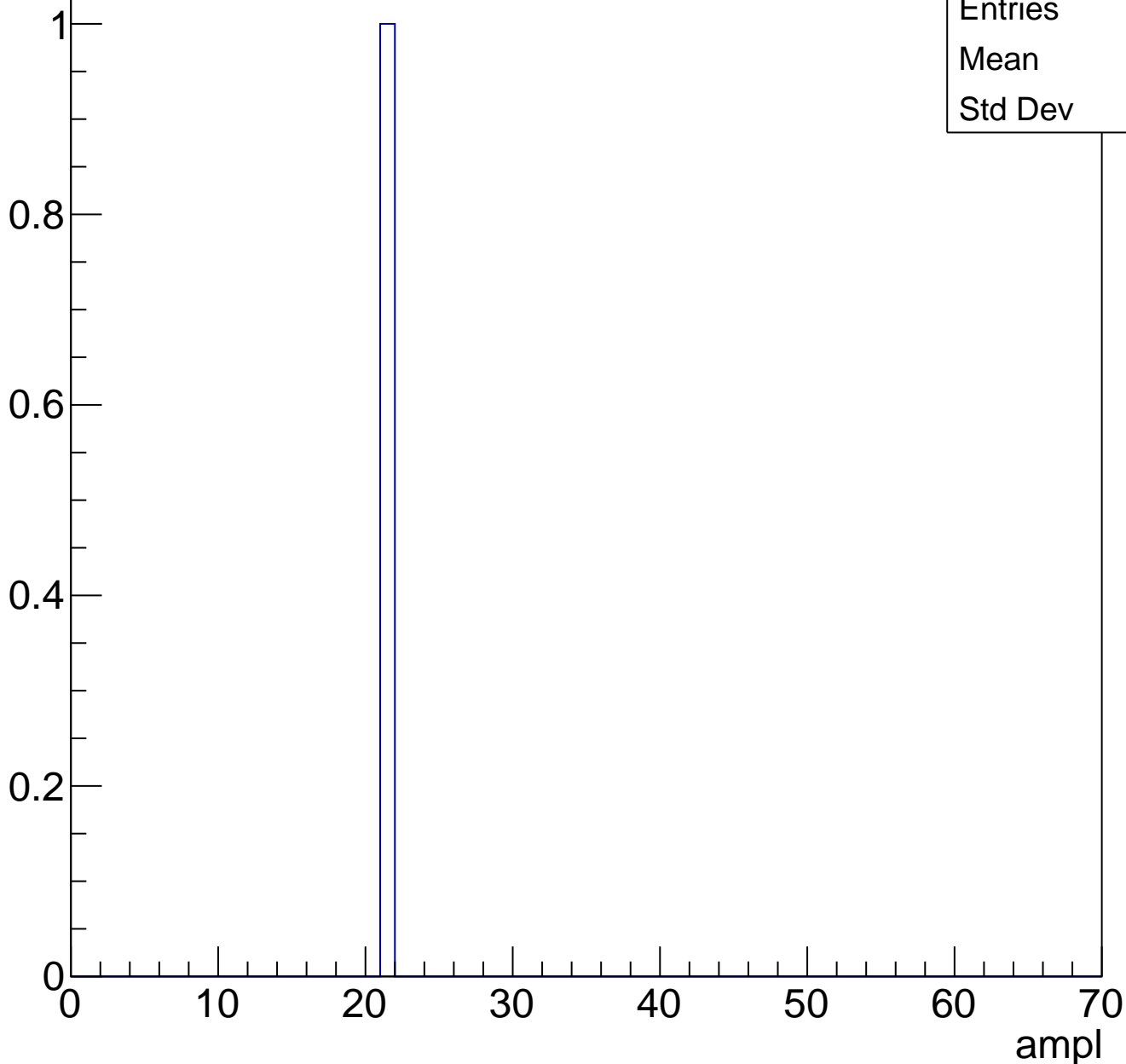


Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch38, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	28.77
Std Dev	3.399

Gaus mean : 29.5429

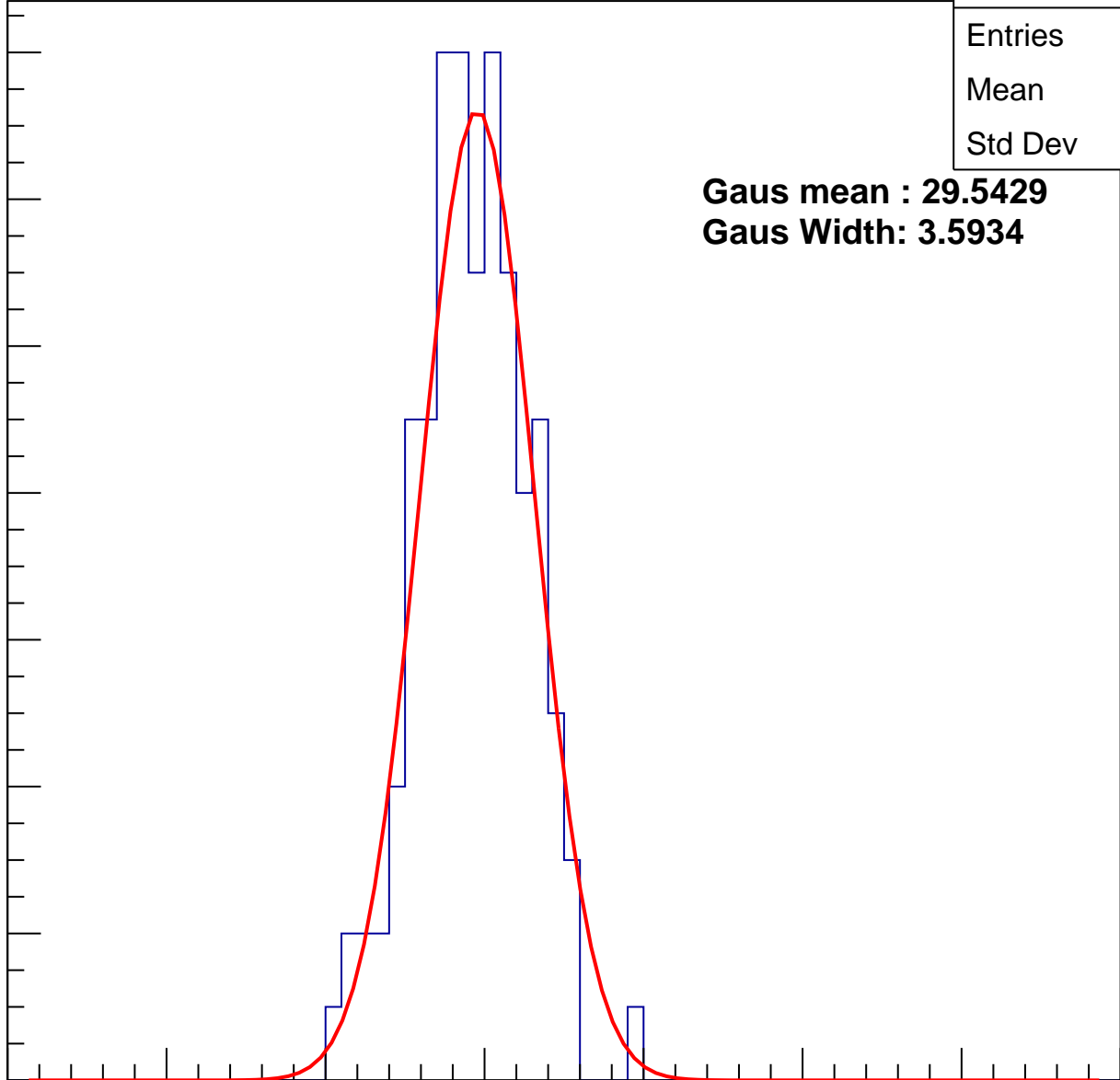
Gaus Width: 3.5934

Entry

14
12
10
8
6
4
2
0

ampl

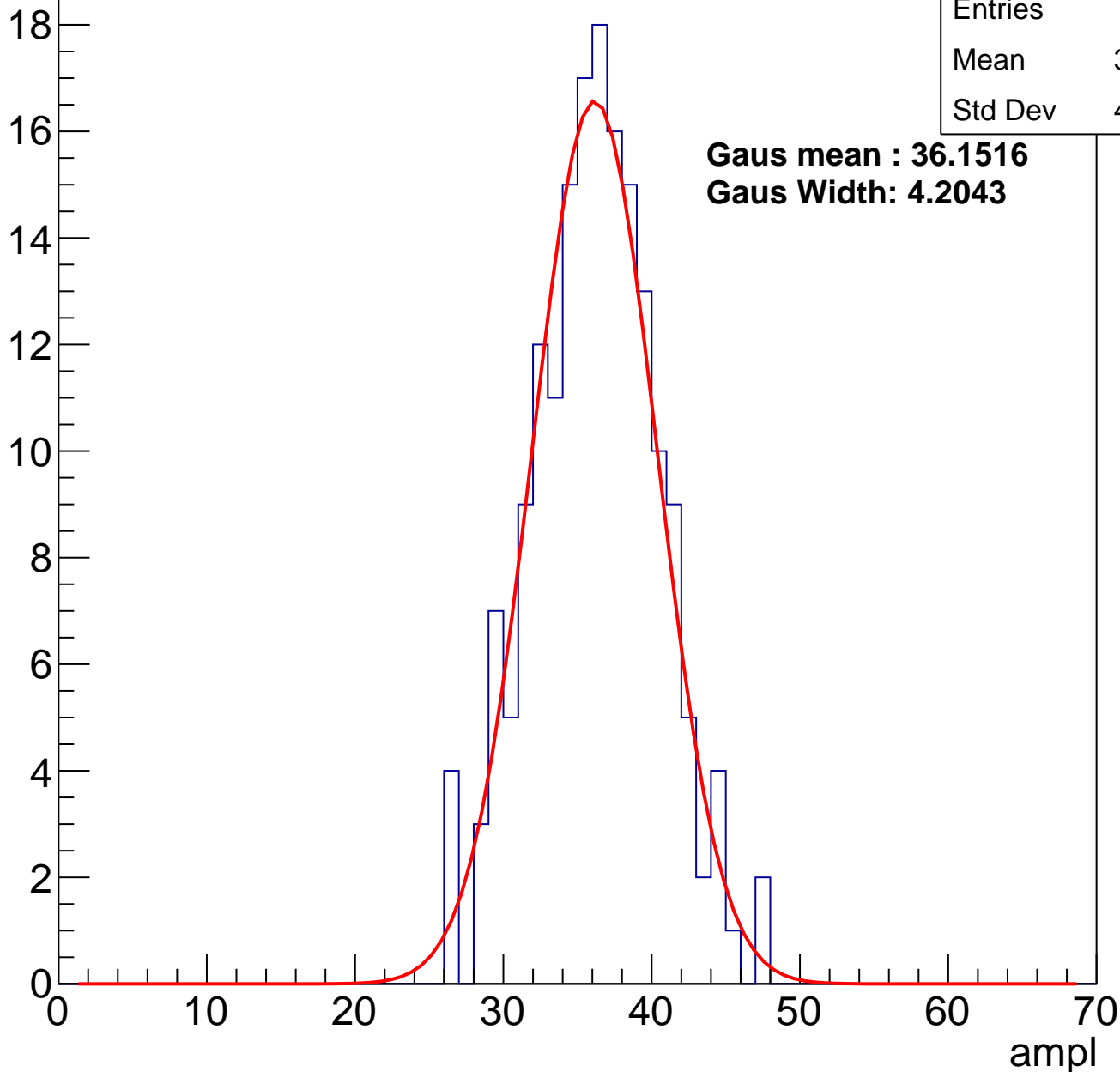
0 10 20 30 40 50 60 70



B0L101S, U25-ch38, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch38, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

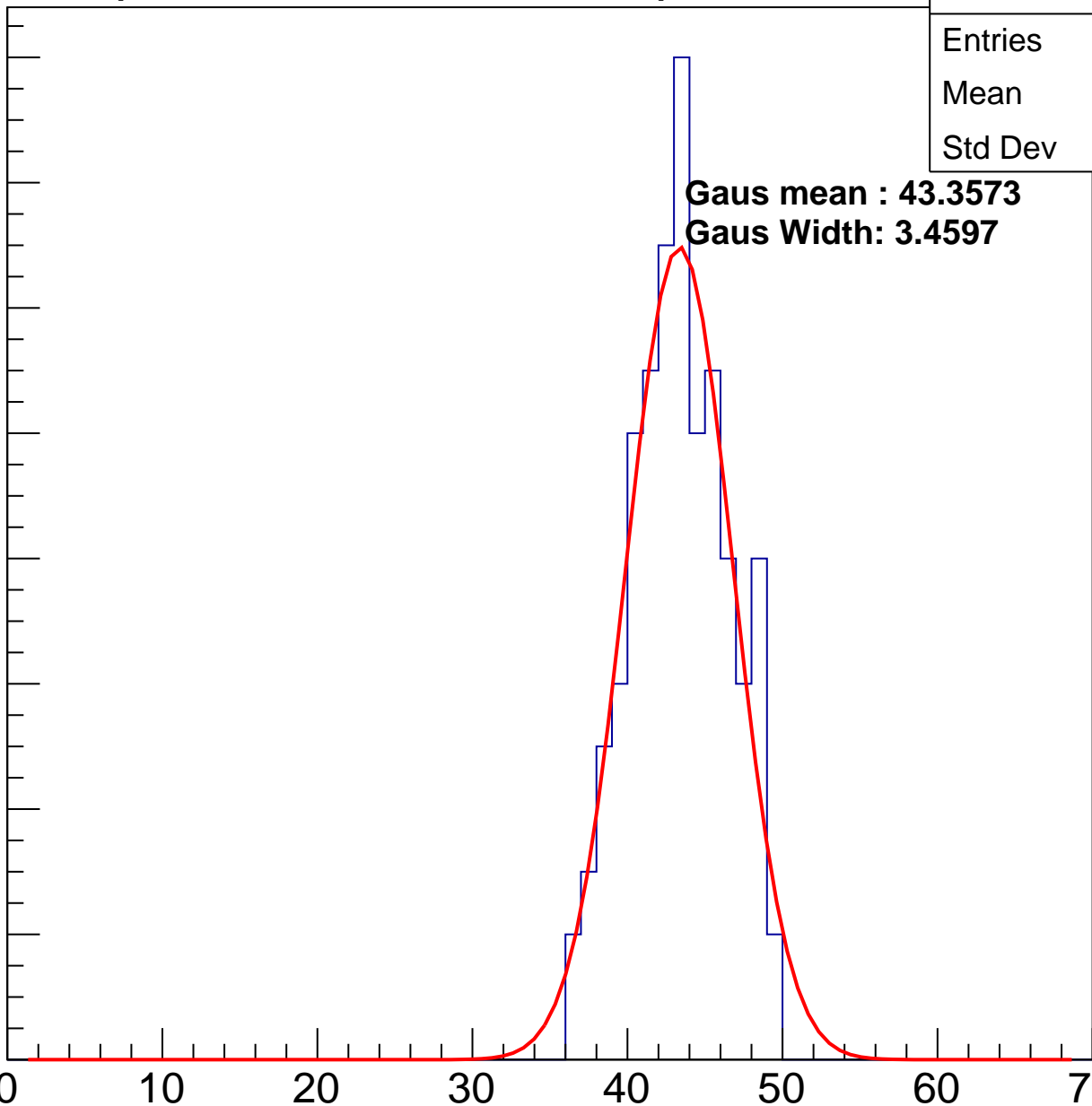
16
14
12
10
8
6
4
2
0

Entries	111
Mean	42.87
Std Dev	3.099

Gaus mean : 43.3573

Gaus Width: 3.4597

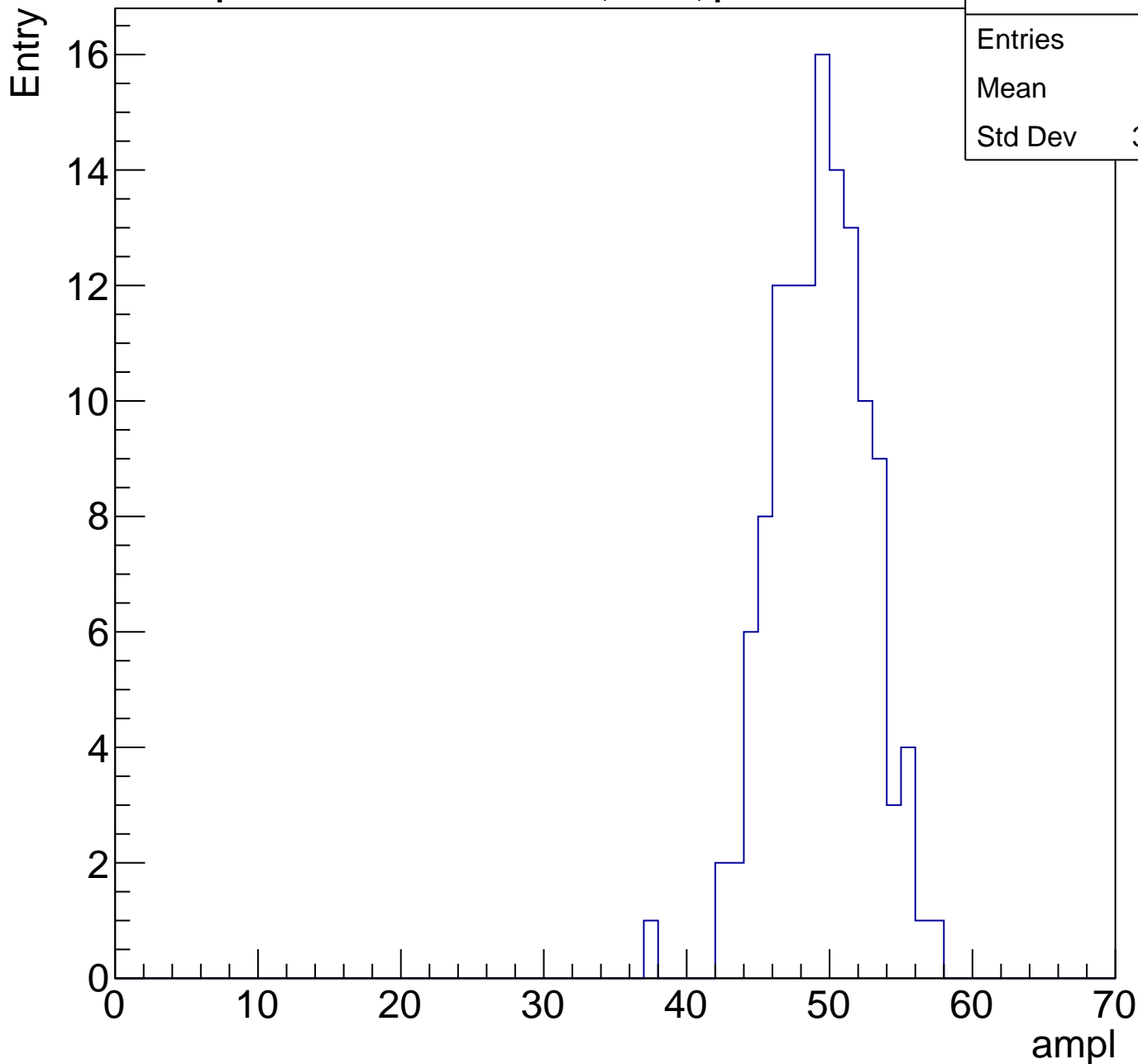
0 10 20 30 40 50 60 70
ampl



B0L101S, U25-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

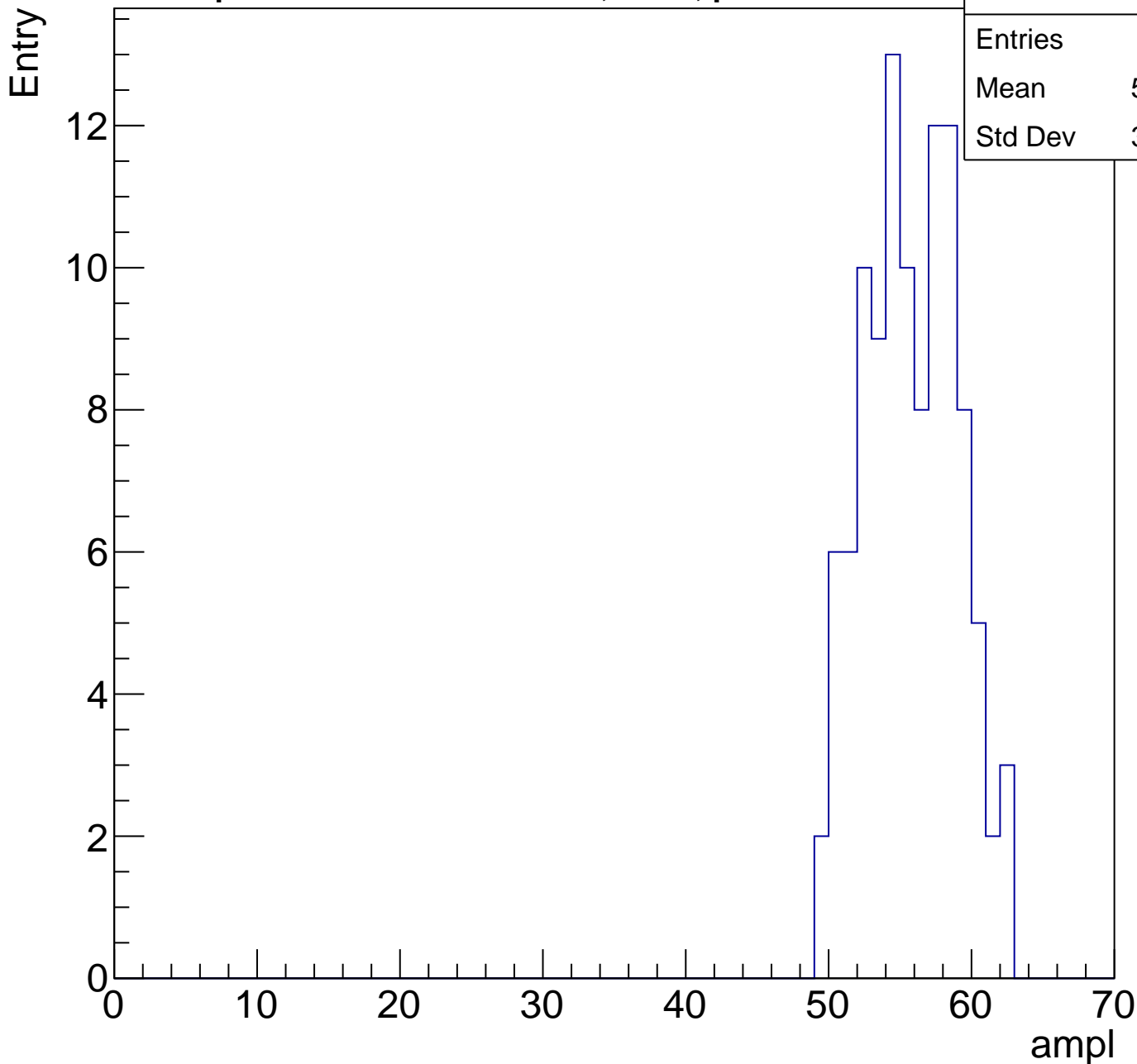
Entries	126
Mean	48.9
Std Dev	3.322



B0L101S, U25-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	55.29
Std Dev	3.198



B0L101S, U25-ch38, adc5

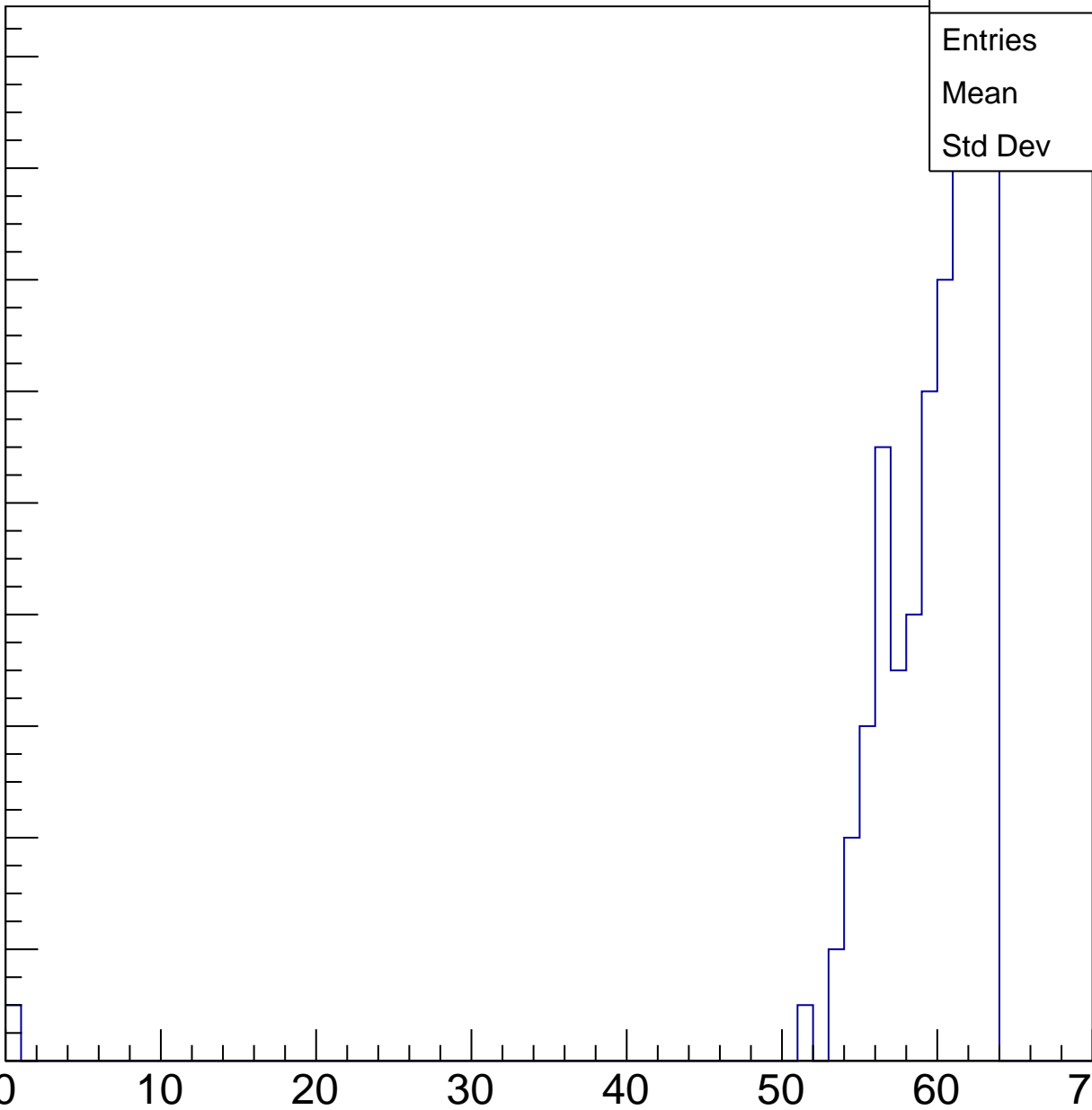
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	117
Mean	58.92
Std Dev	6.178

ampl



B0L101S, U25-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

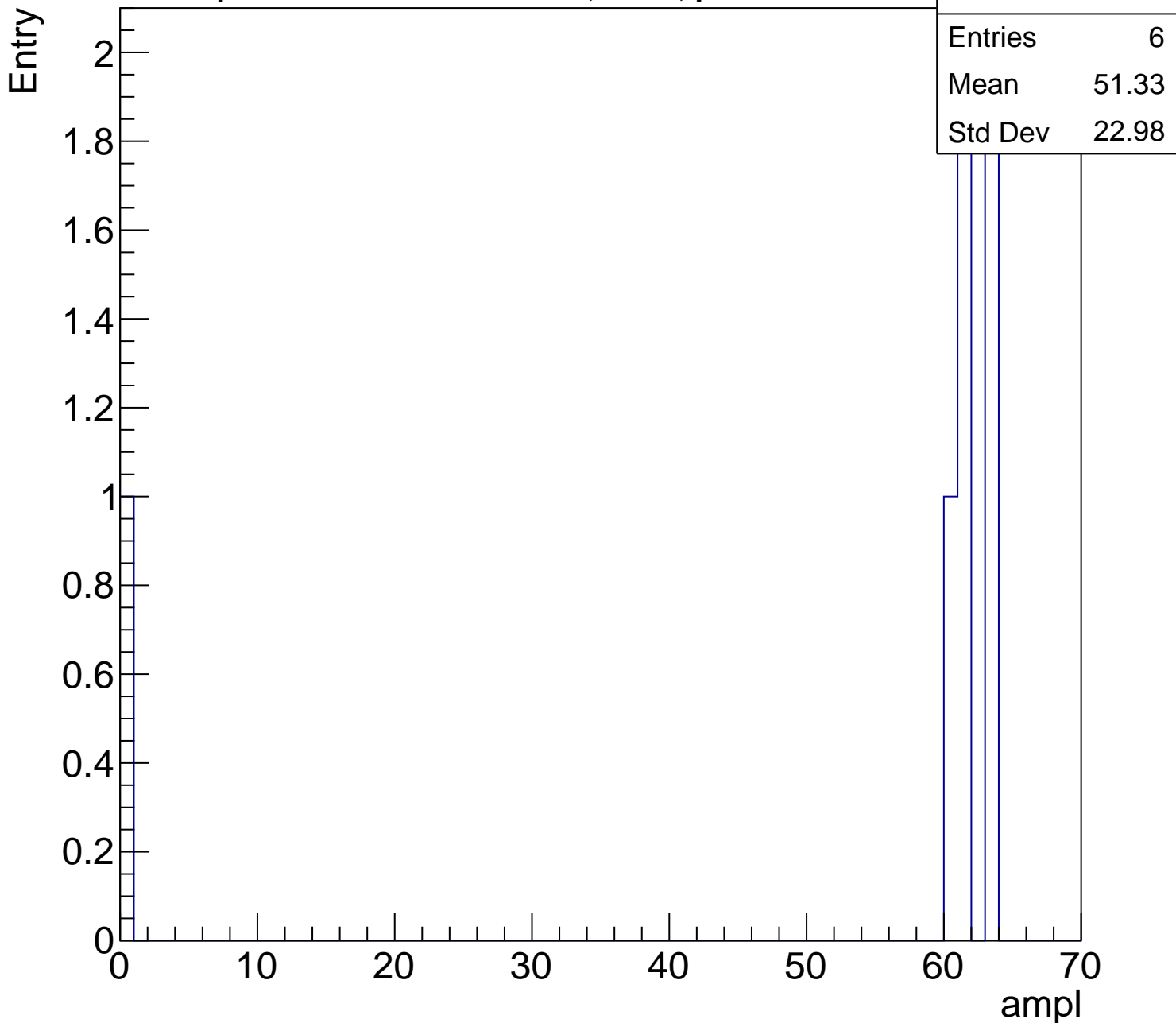
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.33
Std Dev	22.98

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch39, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	32.93
Std Dev	5.327

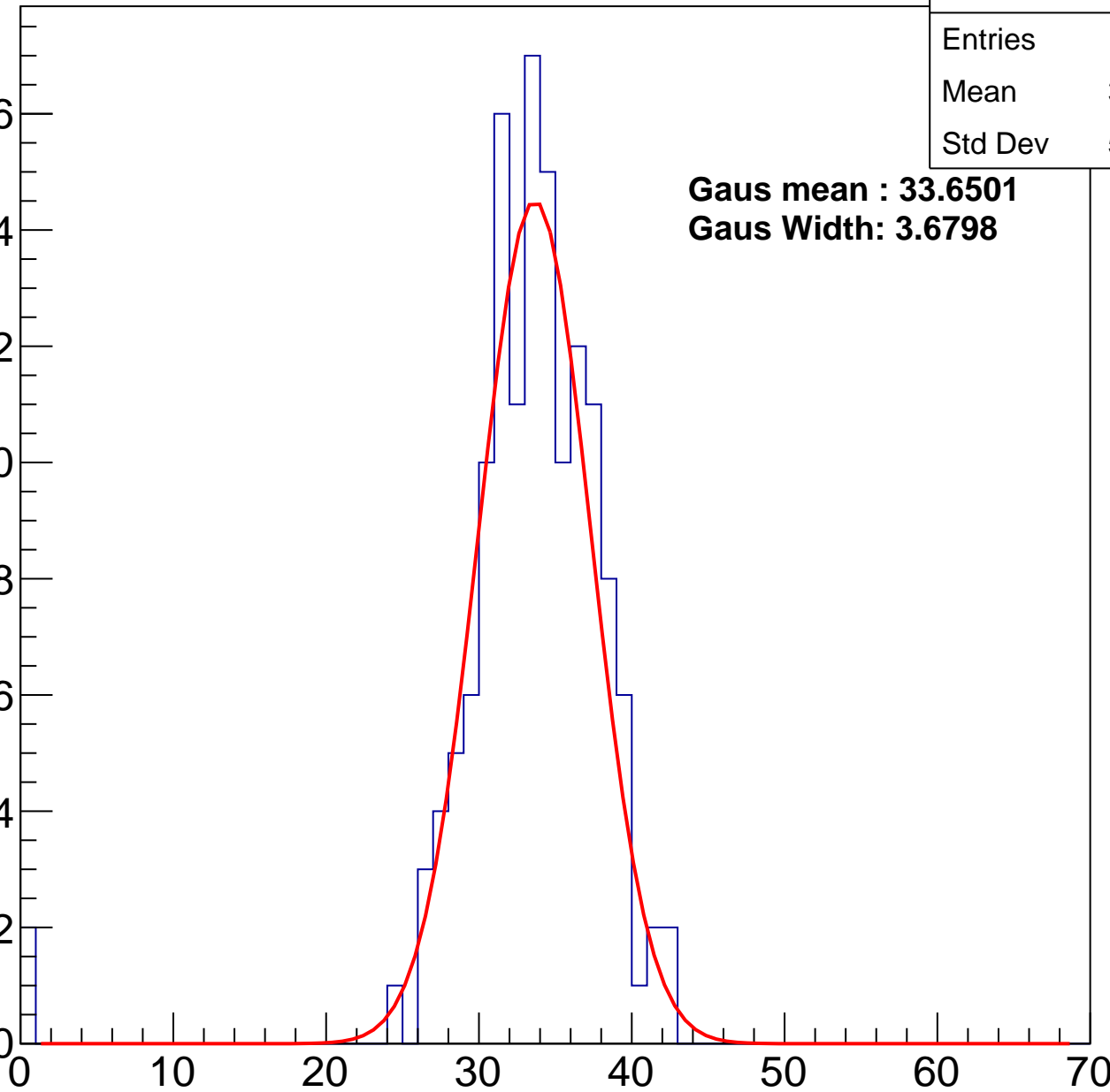
Gaus mean : 33.6501

Gaus Width: 3.6798

Entry

16
14
12
10
8
6
4
2
0

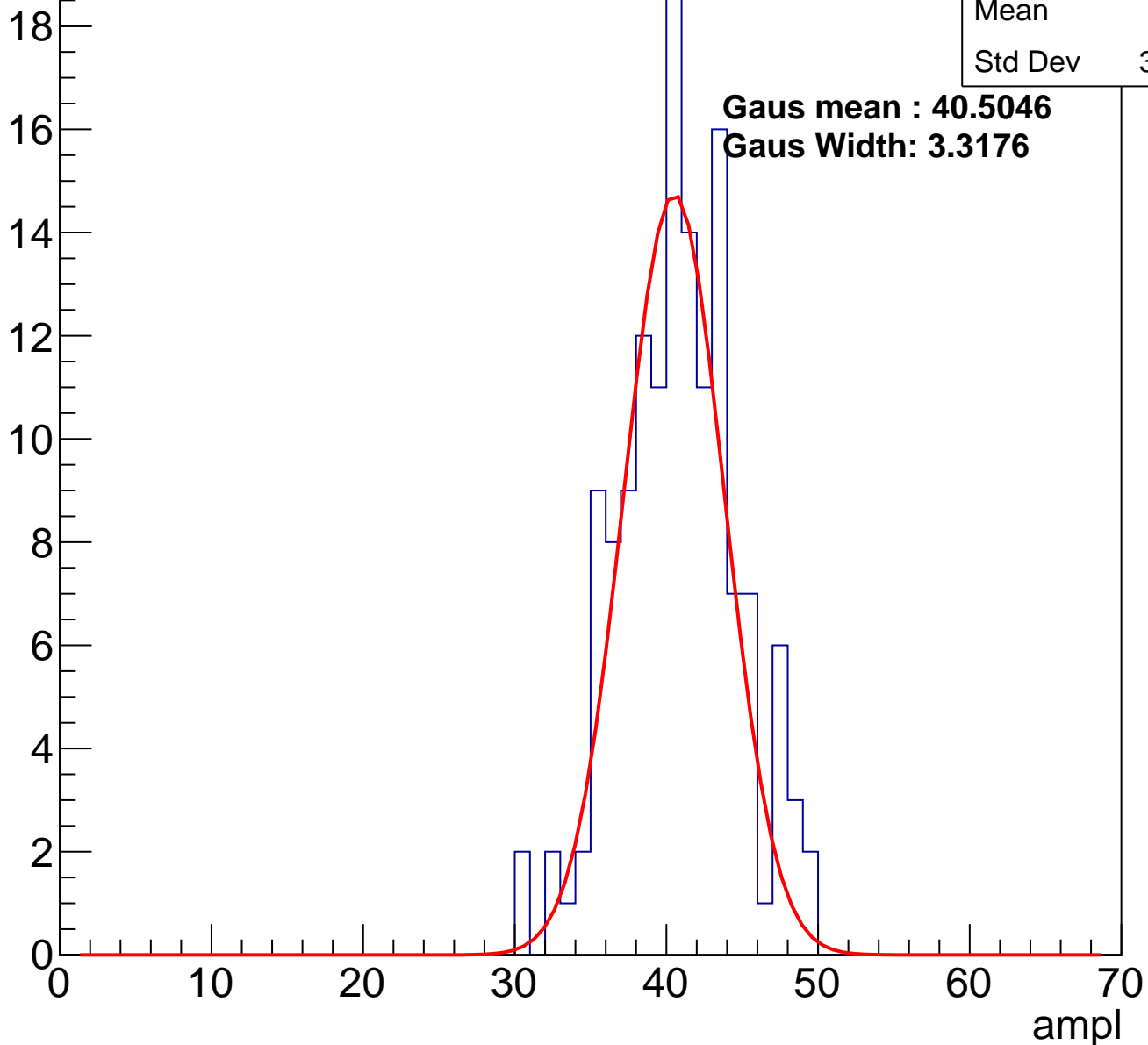
ampl



B0L101S, U25-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch39, adc2

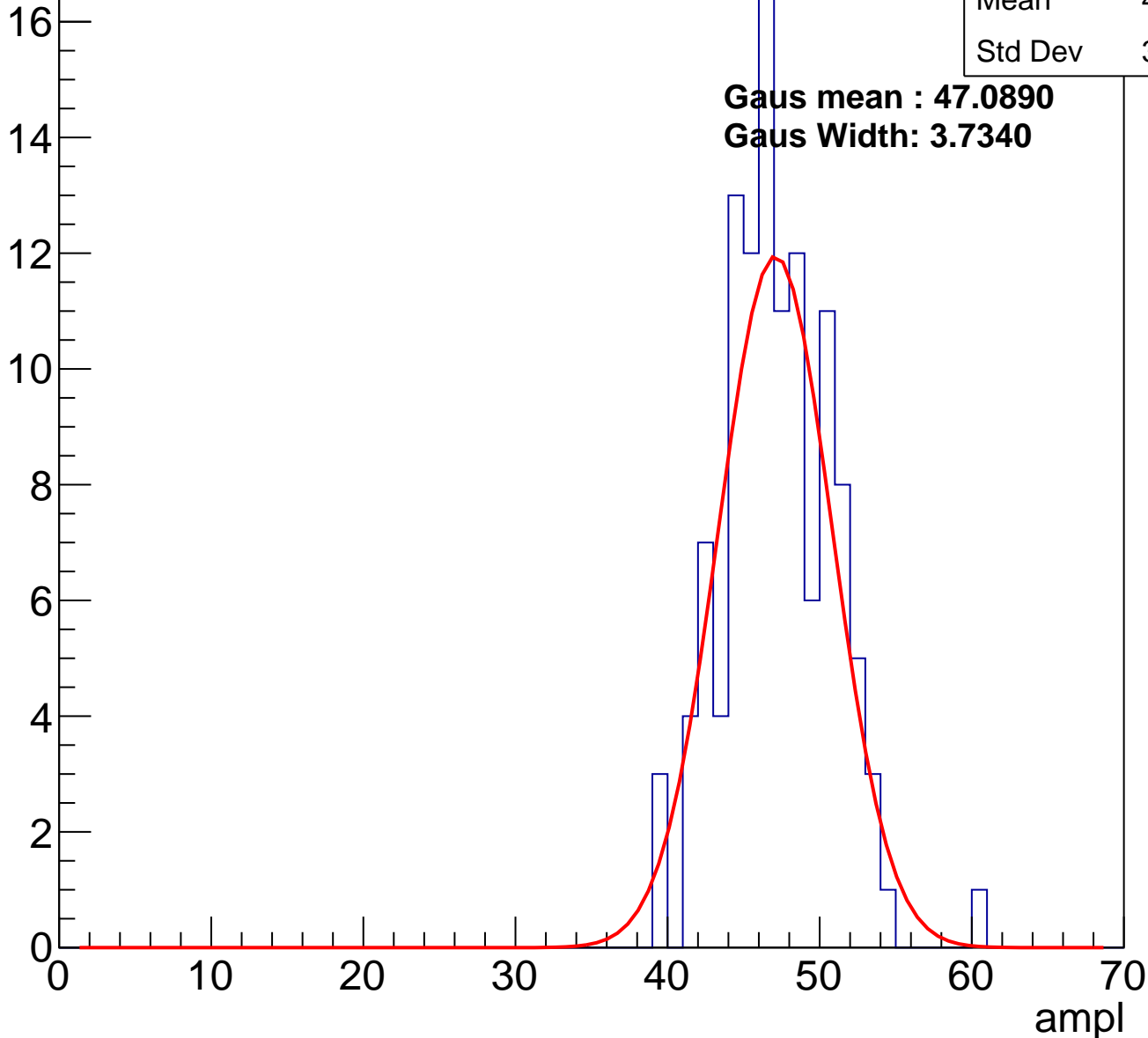
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	46.77
Std Dev	3.523

Gaus mean : 47.0890

Gaus Width: 3.7340

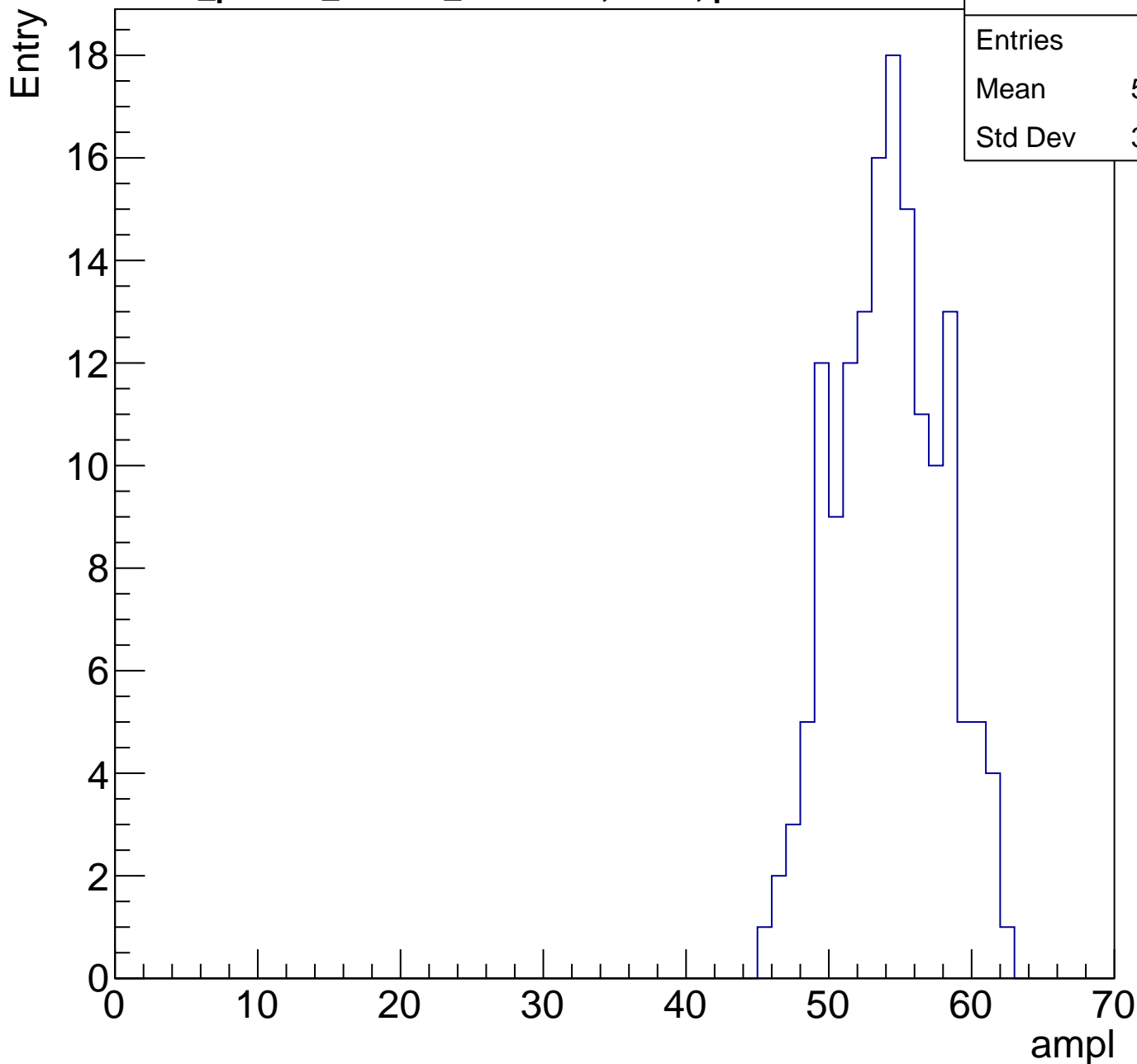
Entry



B0L101S, U25-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	53.74
Std Dev	3.652



B0L101S, U25-ch39, adc4

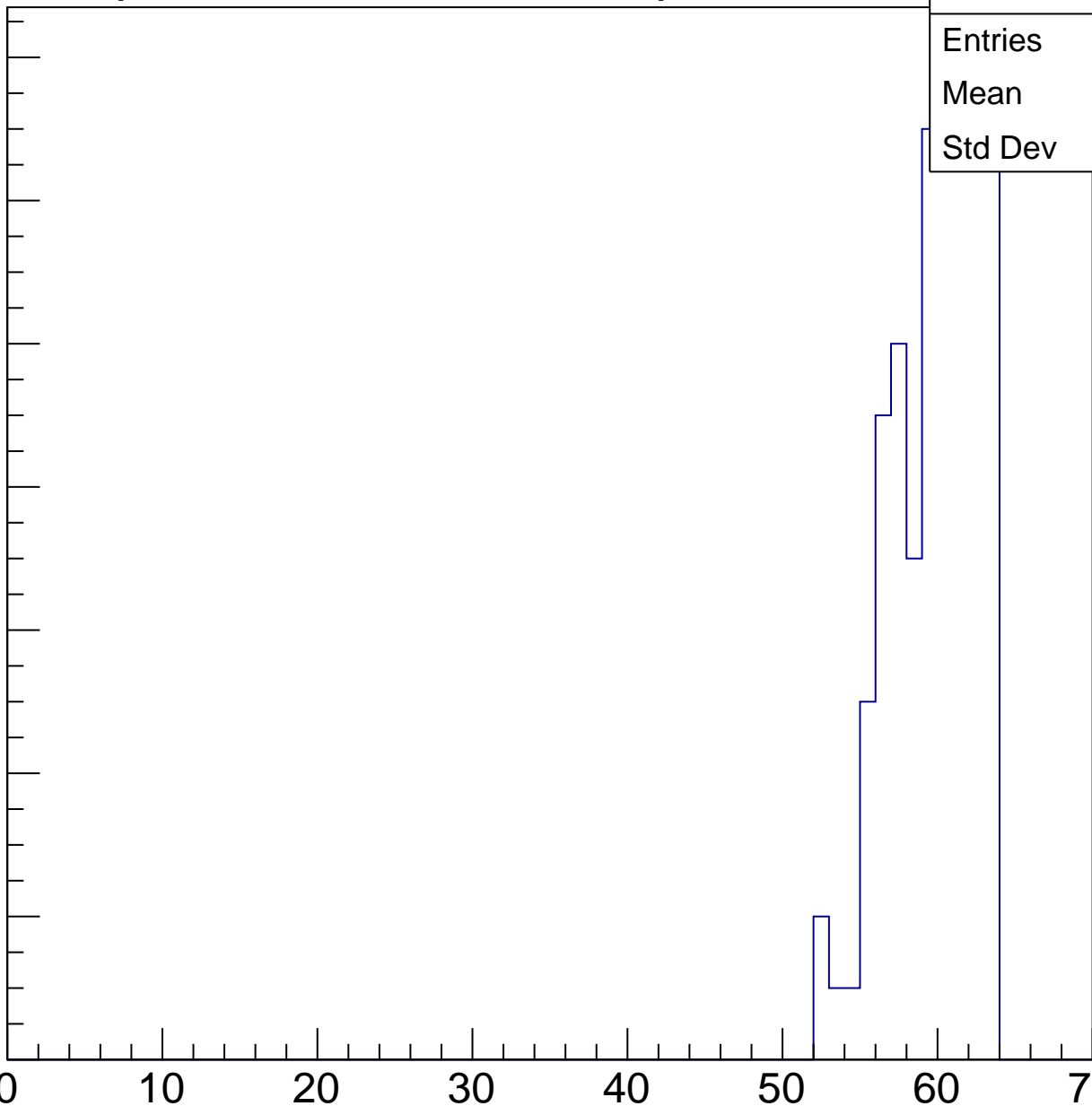
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	59.33
Std Dev	2.725

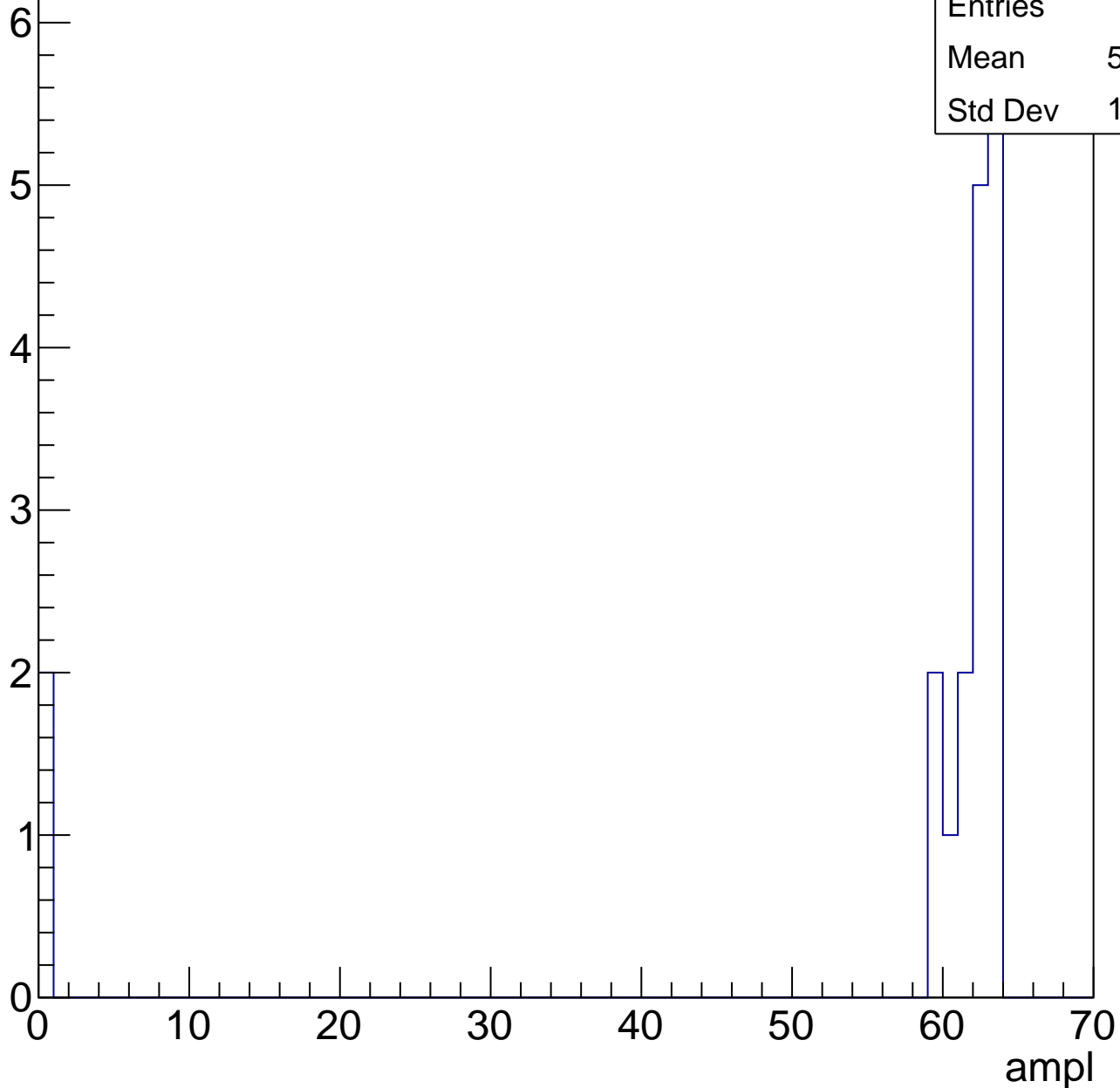
ampl



B0L101S, U25-ch39, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch40, adc0

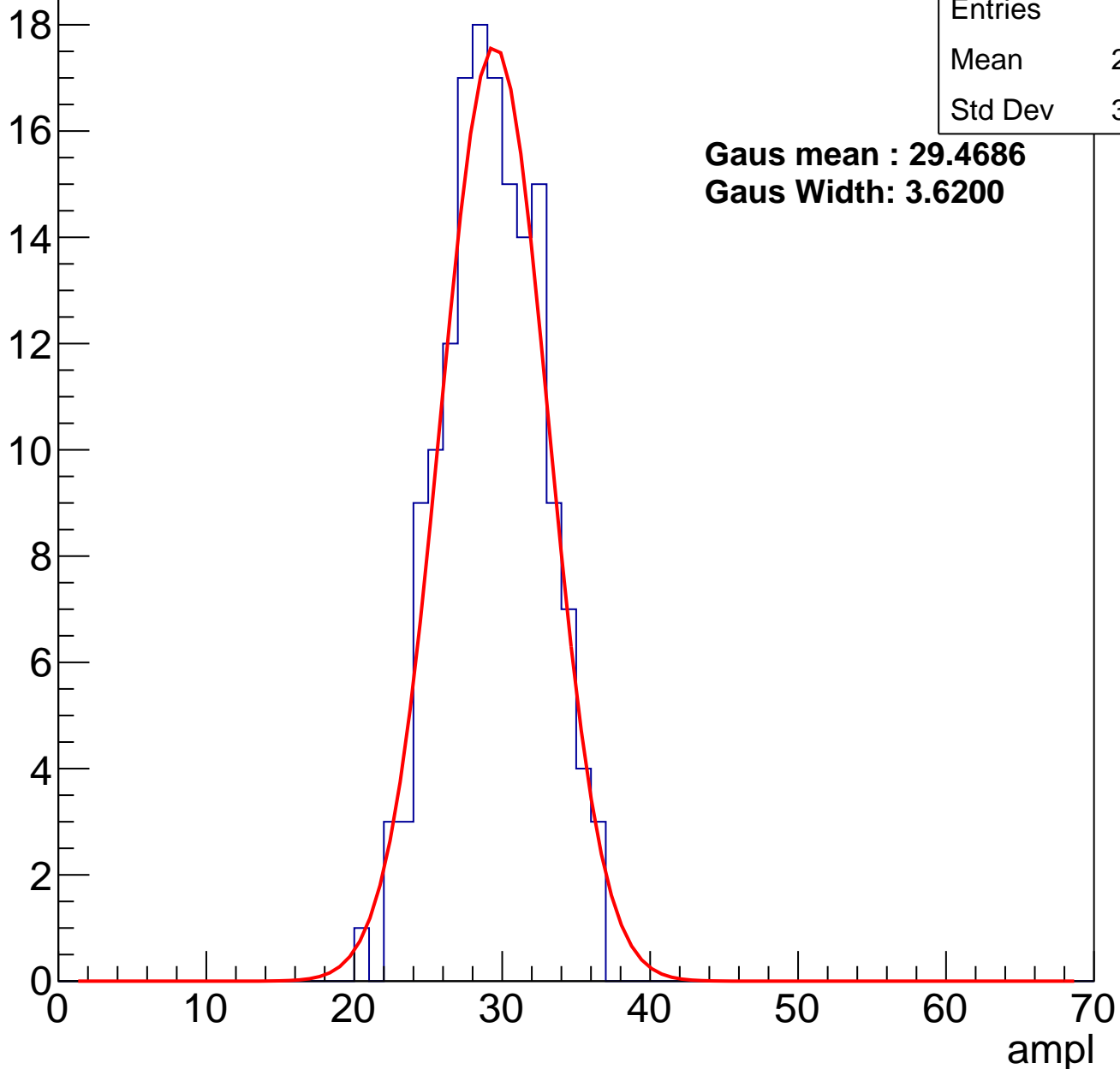
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	28.89
Std Dev	3.319

Gaus mean : 29.4686

Gaus Width: 3.6200

Entry



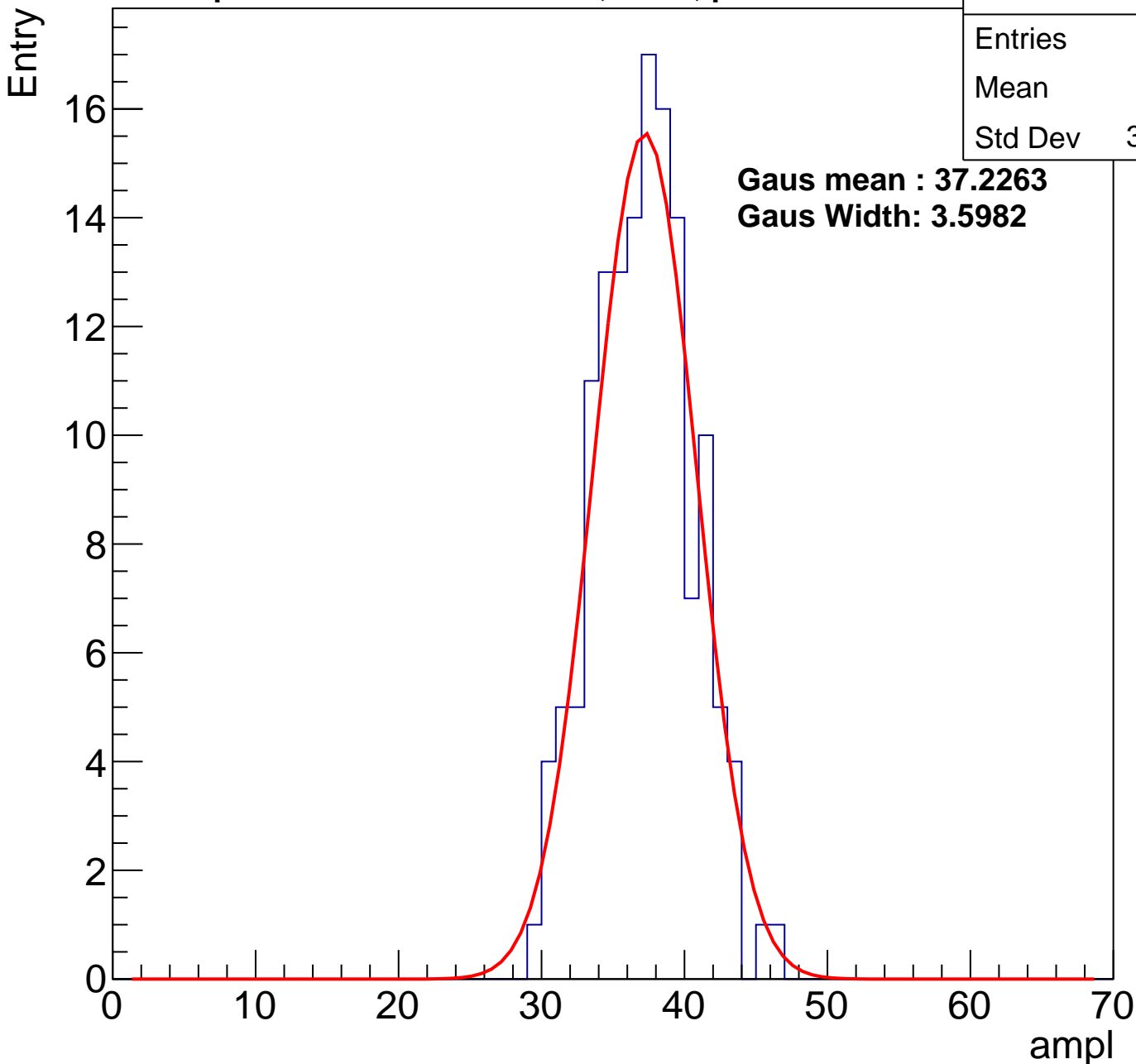
B0L101S, U25-ch40, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.7
Std Dev	3.389

Gaus mean : 37.2263

Gaus Width: 3.5982



B0L101S, U25-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

115

Mean

42.77

Std Dev

3.544

Gaus mean : 43.3051

Gaus Width: 3.9603

ampl

70

60

50

40

30

20

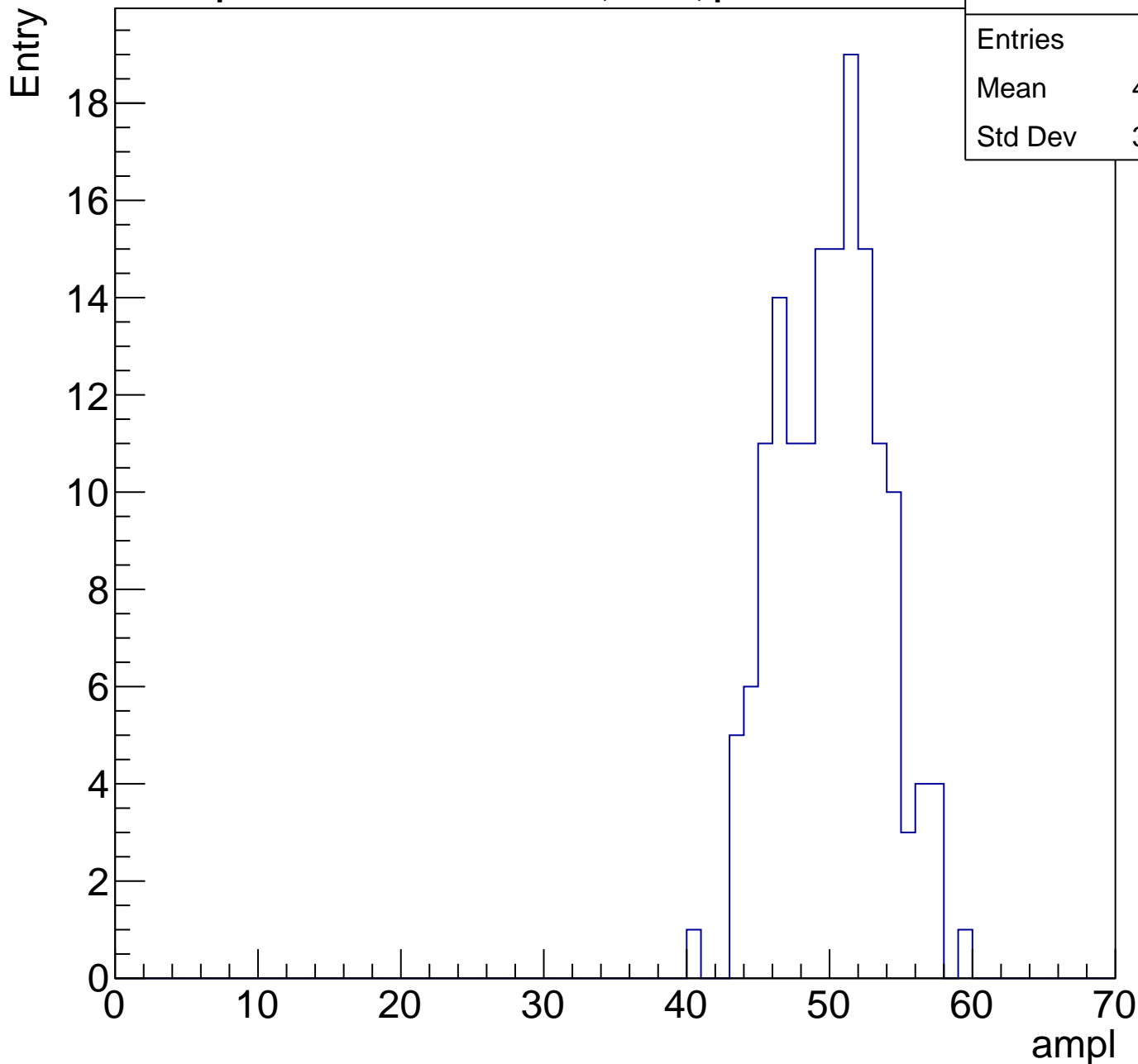
10

0

B0L101S, U25-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	49.59
Std Dev	3.604



B0L101S, U25-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	56.43
Std Dev	3.38

Entry

14

12

10

8

6

4

2

0

0

10

20

30

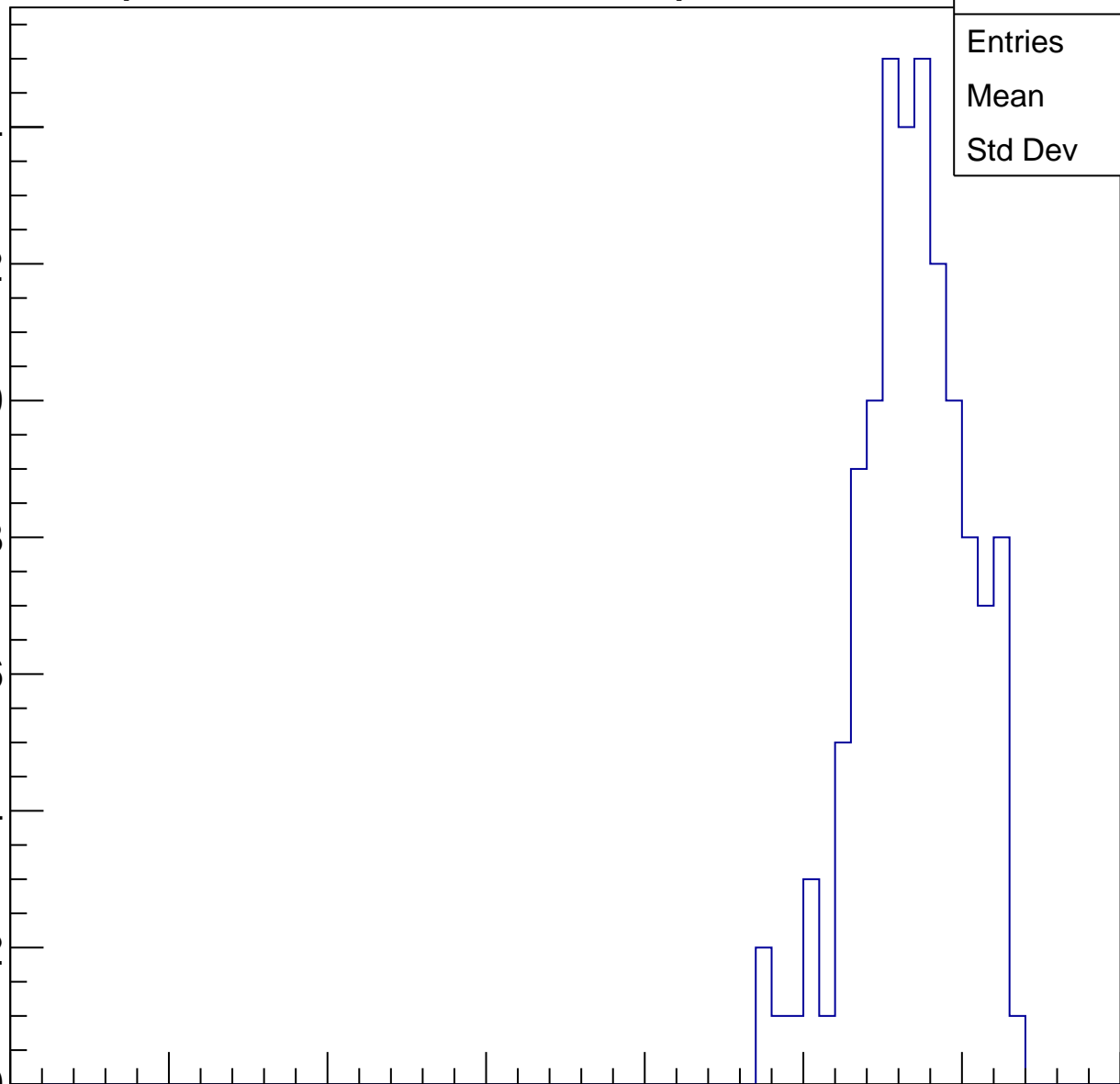
40

50

60

70

ampl



B0L101S, U25-ch40, adc5

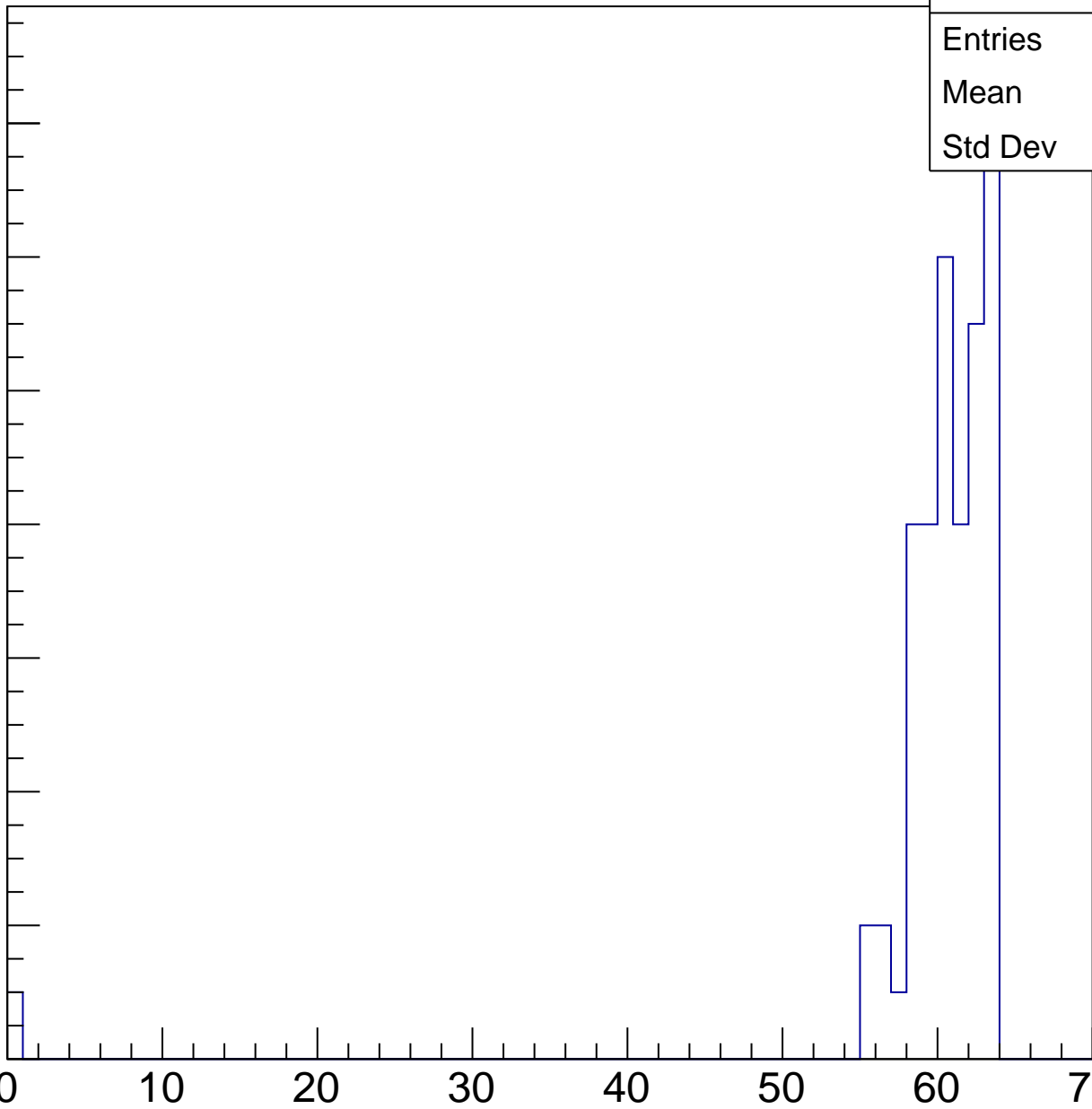
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	59.56
Std Dev	7.578

ampl

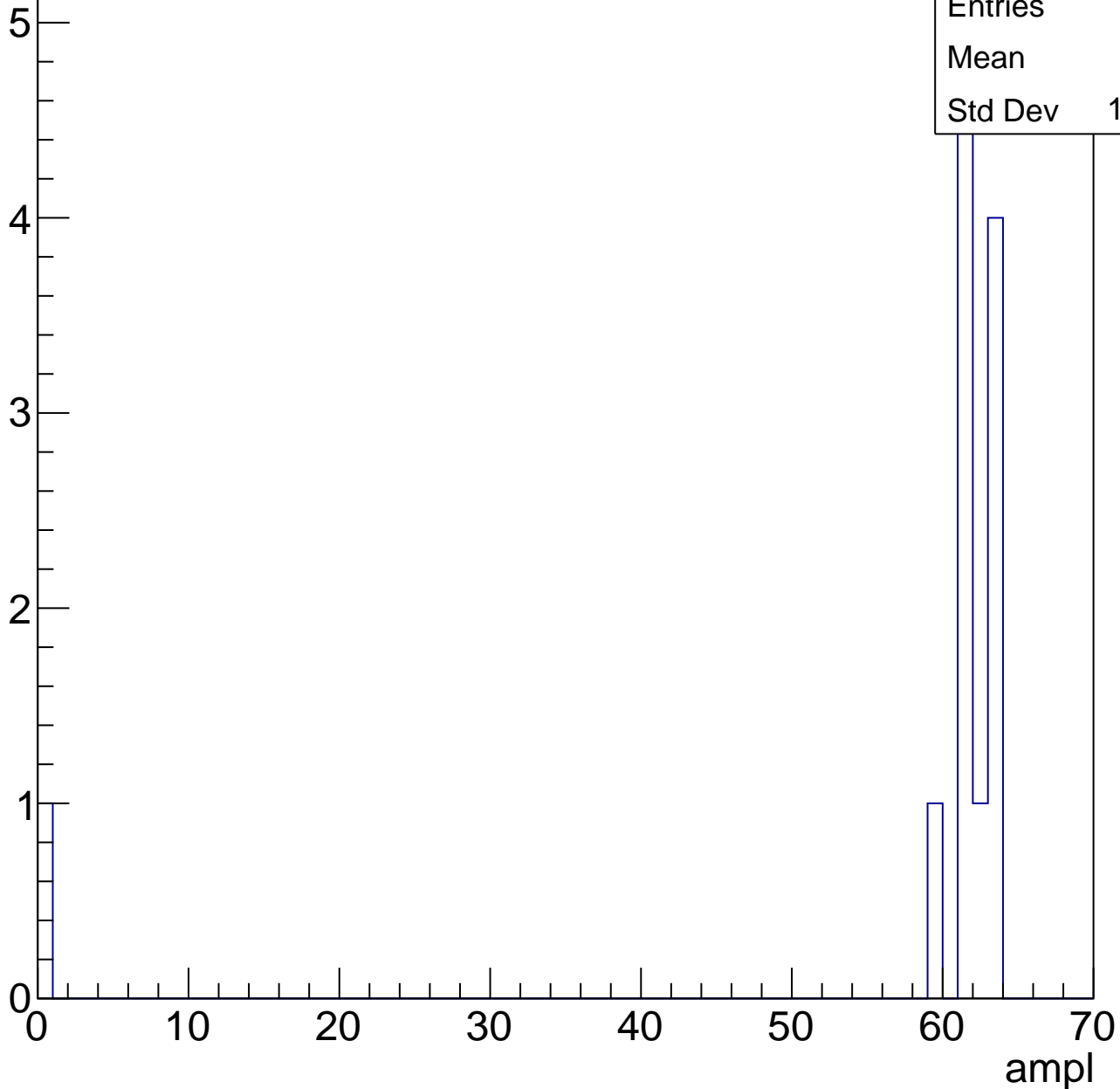


B0L101S, U25-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.5
Std Dev	17.08



B0L101S, U25-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch41, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	30.2
Std Dev	3.408

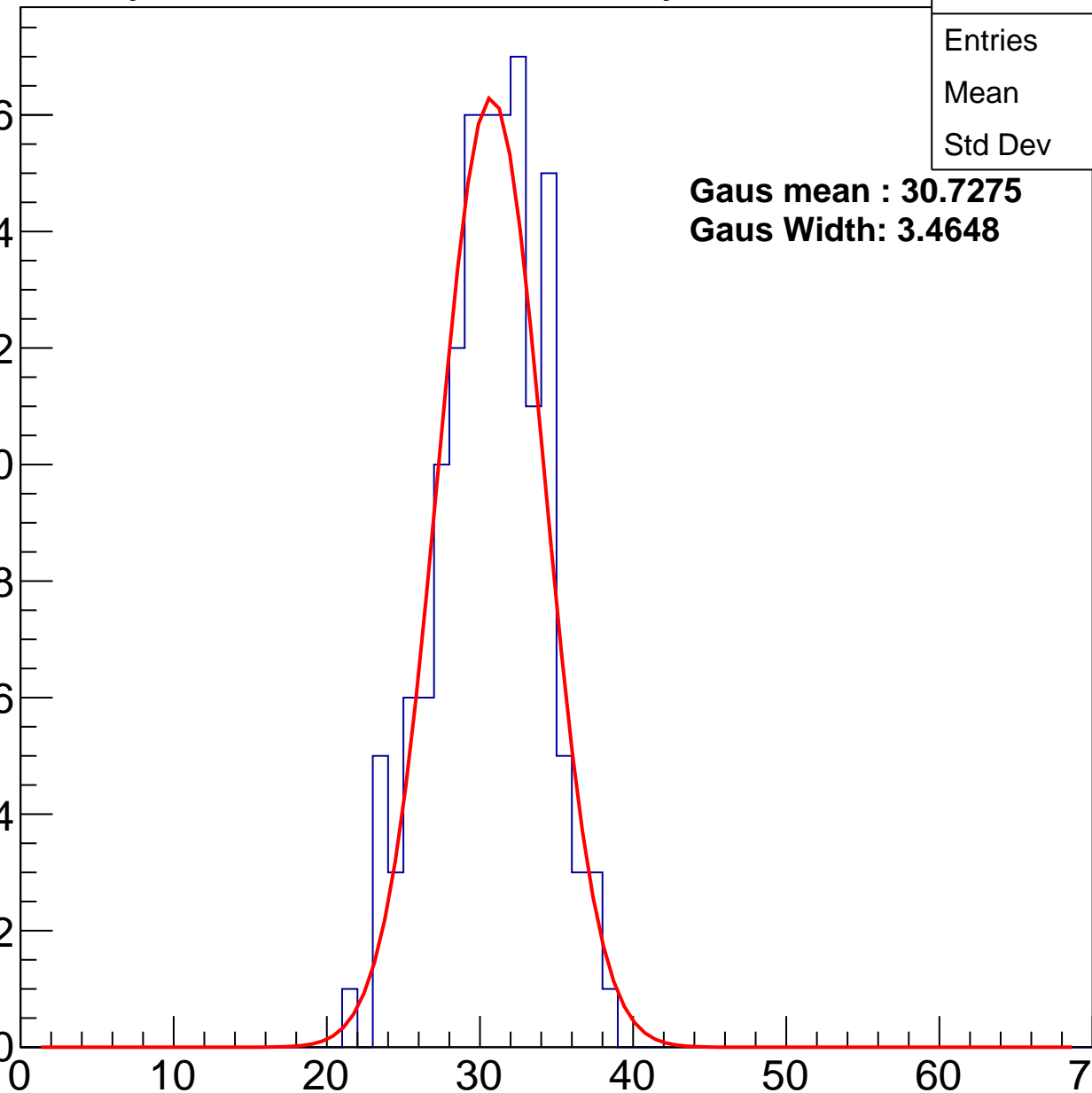
Gaus mean : 30.7275

Gaus Width: 3.4648

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch41, adc1

calib_packv5_042523_0143.root, FC#1, port C1

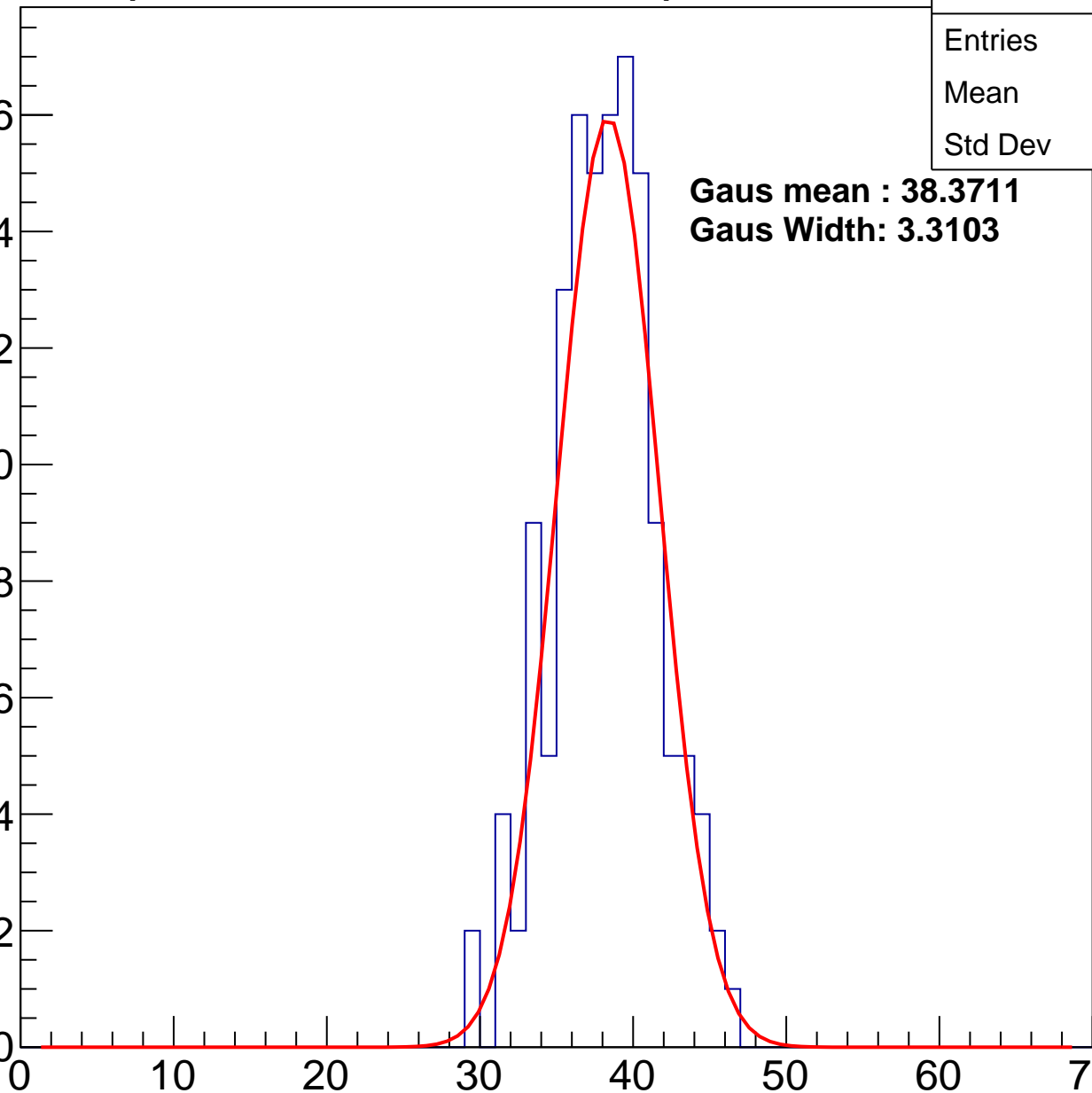
Entries	140
Mean	37.69
Std Dev	3.366

Gaus mean : 38.3711
Gaus Width: 3.3103

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch41, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

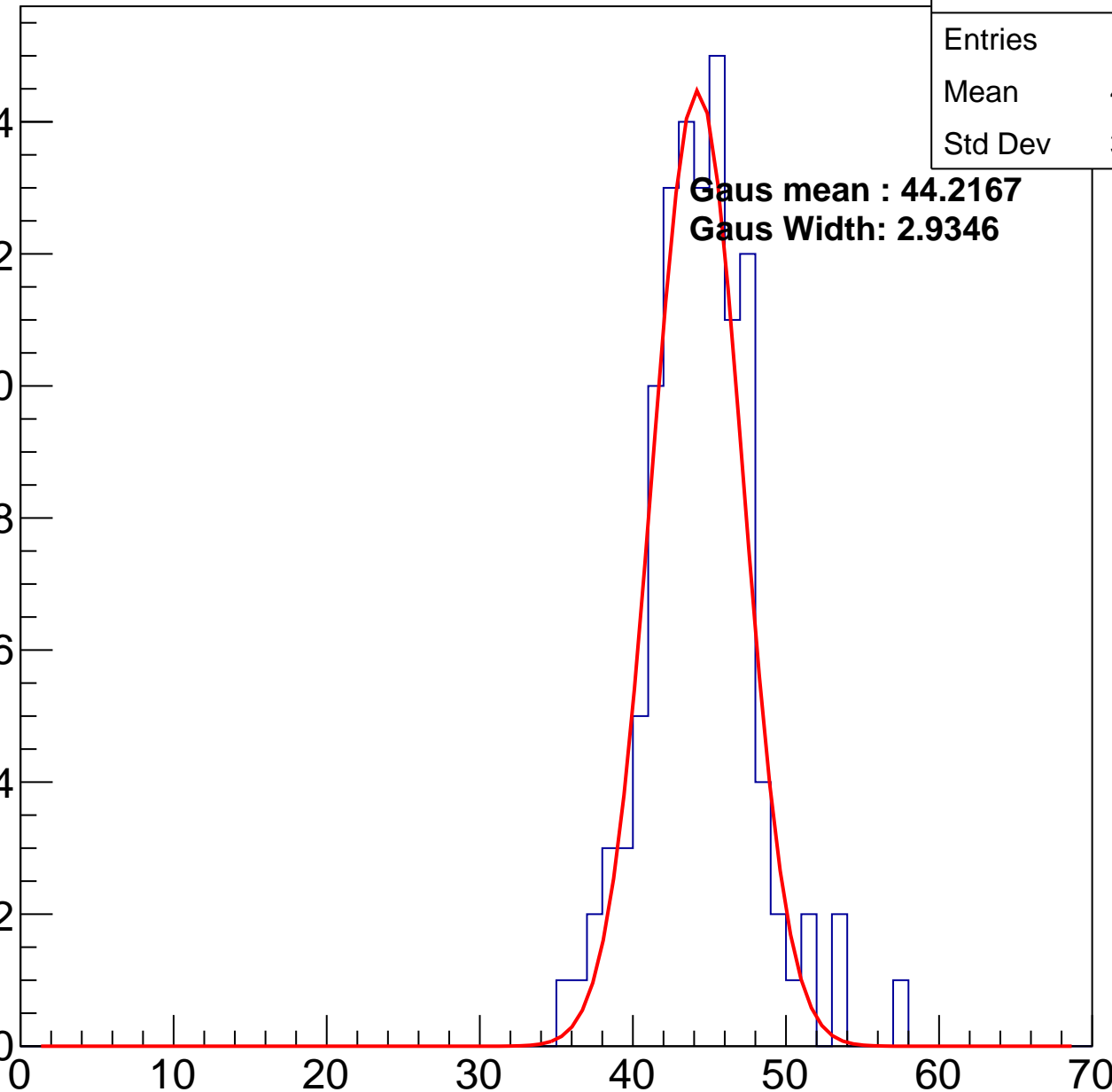
14
12
10
8
6
4
2
0

Entries	115
Mean	43.97
Std Dev	3.499

Gaus mean : 44.2167

Gaus Width: 2.9346

ampl

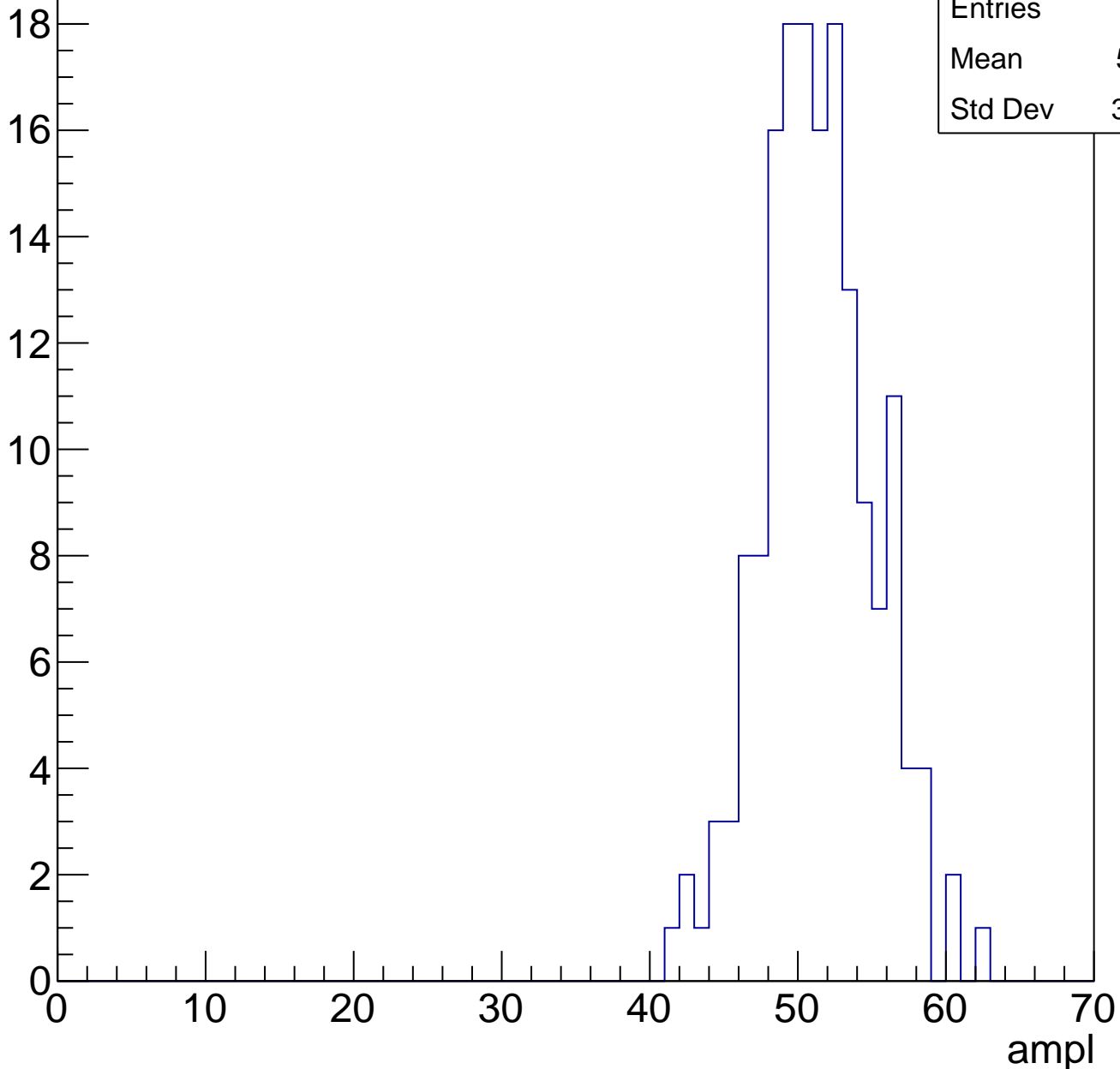


B0L101S, U25-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	50.91
Std Dev	3.759

Entry



B0L101S, U25-ch41, adc4

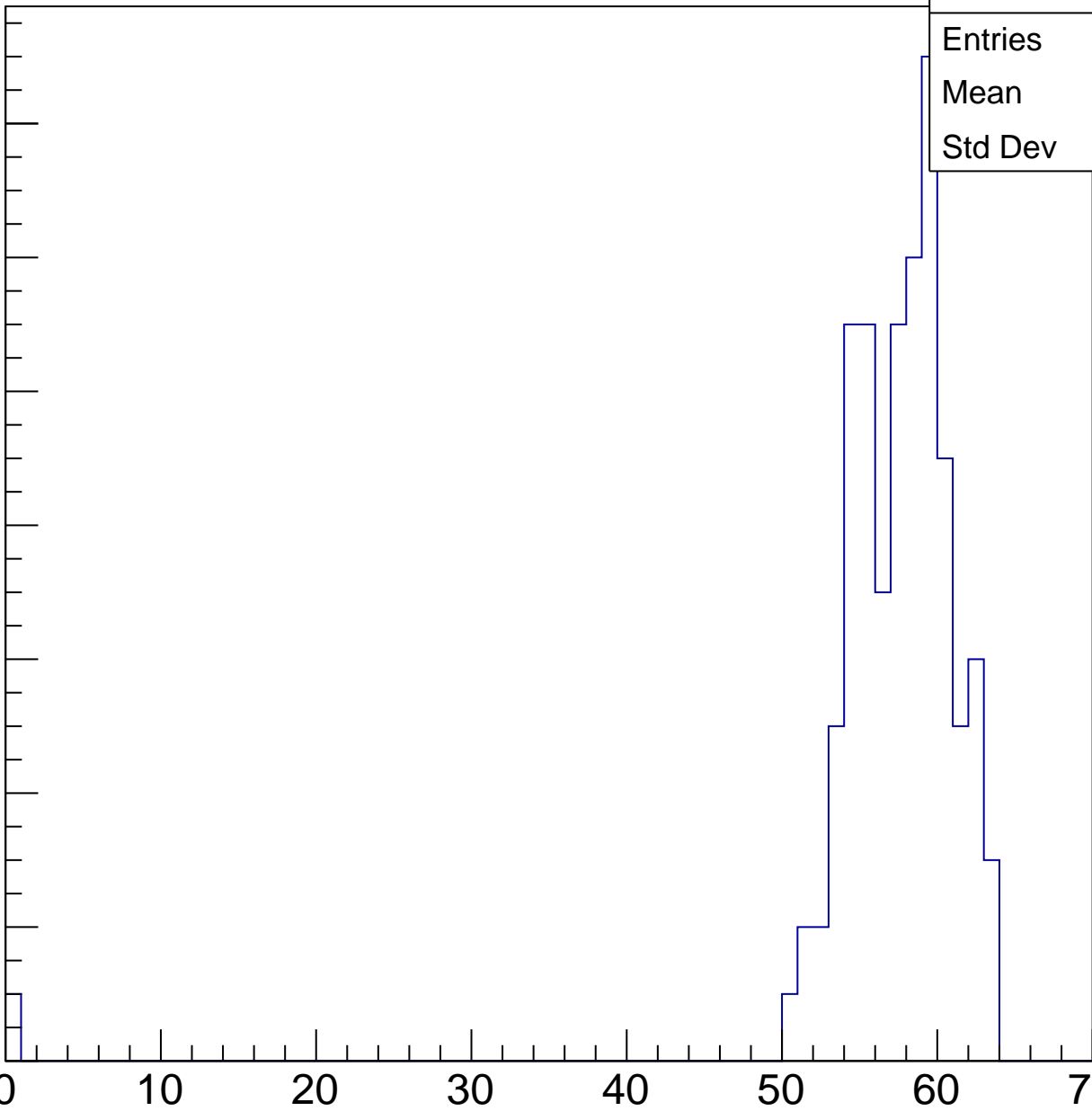
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	56.69
Std Dev	6.396

ampl



B0L101S, U25-ch41, adc5

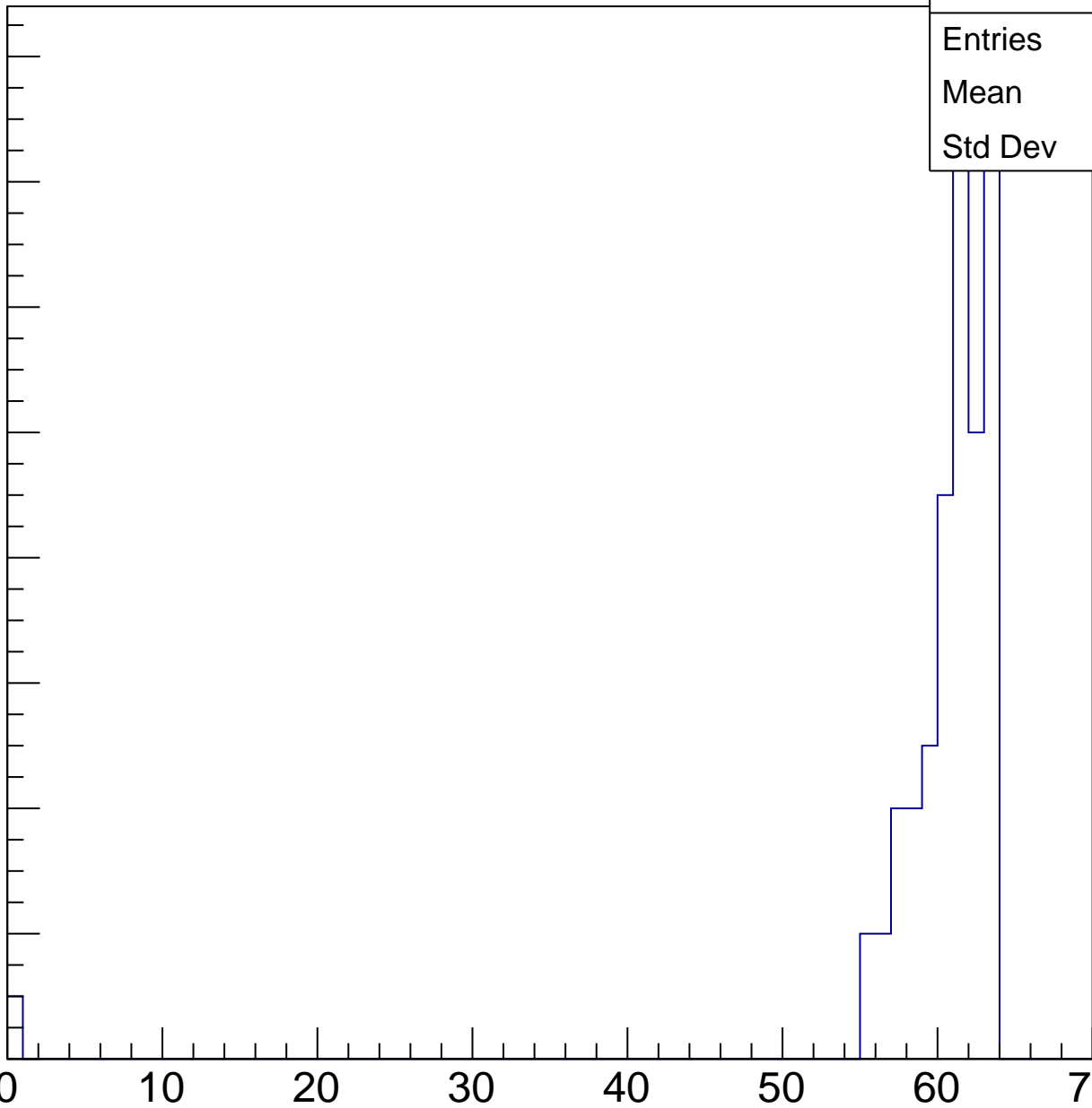
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	68
Mean	59.71
Std Dev	7.604

ampl



B0L101S, U25-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

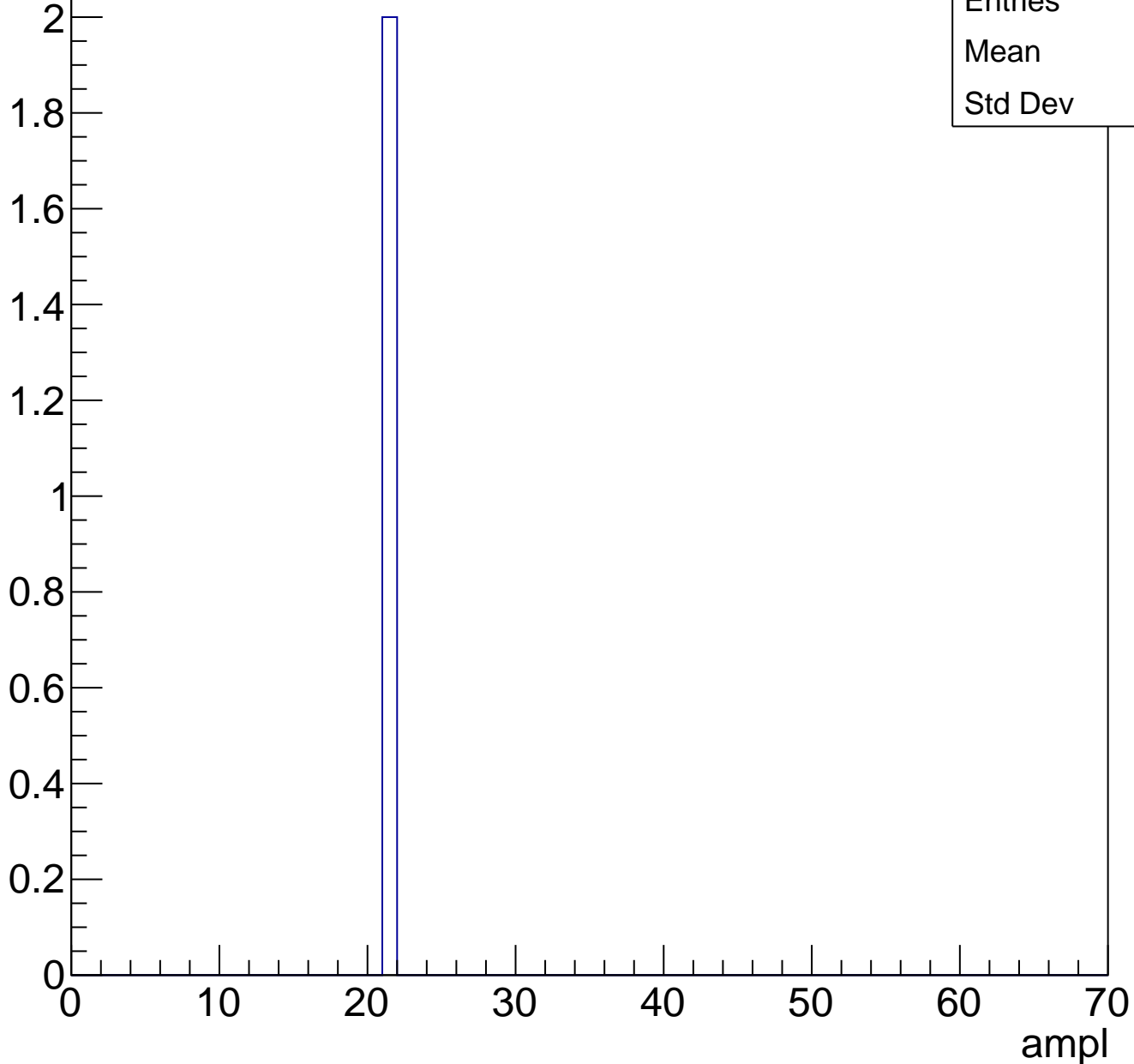
Entry



B0L101S, U25-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch42, adc0

calib_packv5_042523_0143.root, FC#1, port C1

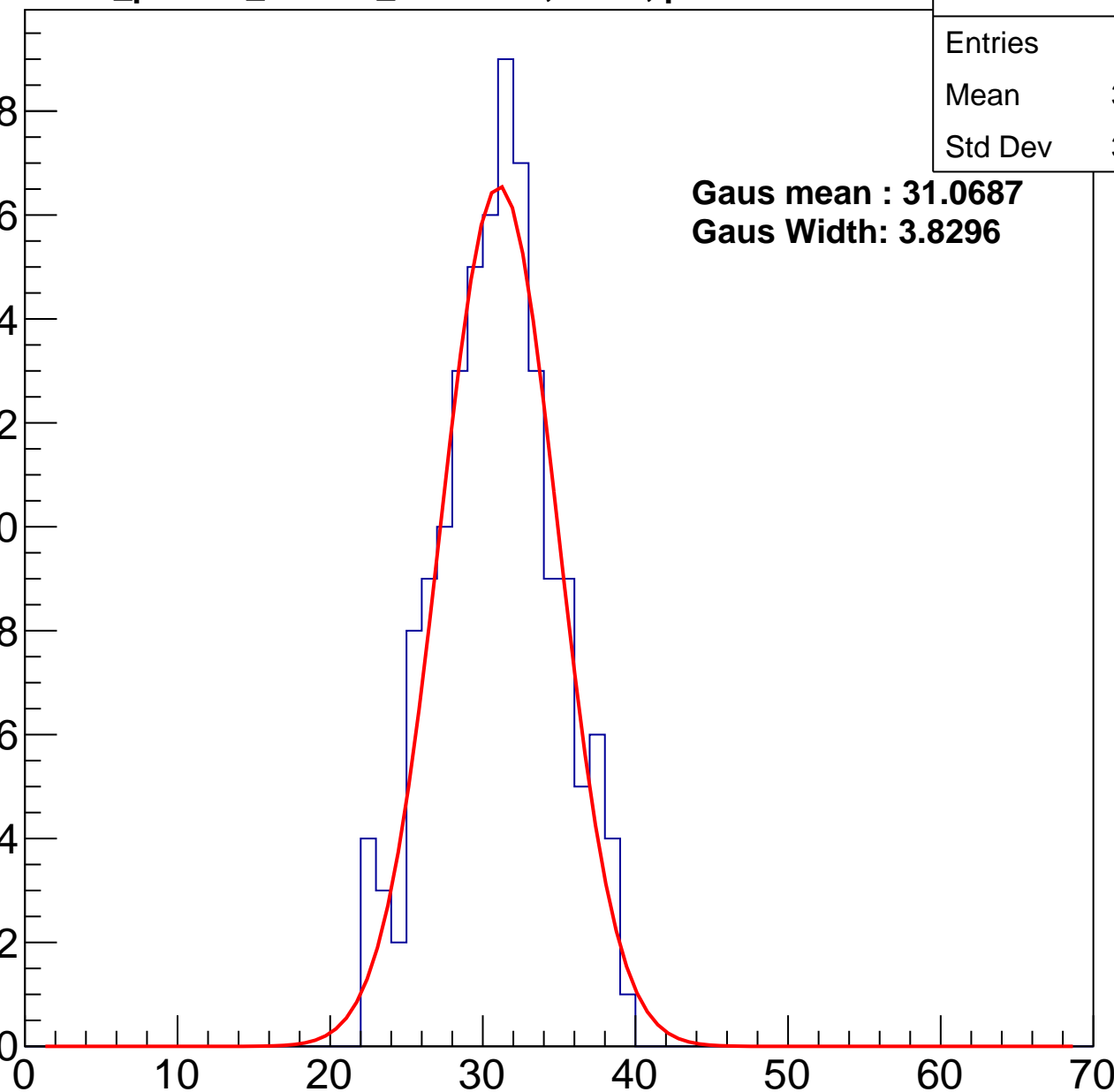
Entry

18
16
14
12
10
8
6
4
2
0

Entries	163
Mean	30.45
Std Dev	3.778

Gaus mean : 31.0687

Gaus Width: 3.8296



ampl

B0L101S, U25-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	38.25
Std Dev	4.796

Gaus mean : 38.8402

Gaus Width: 3.5835

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

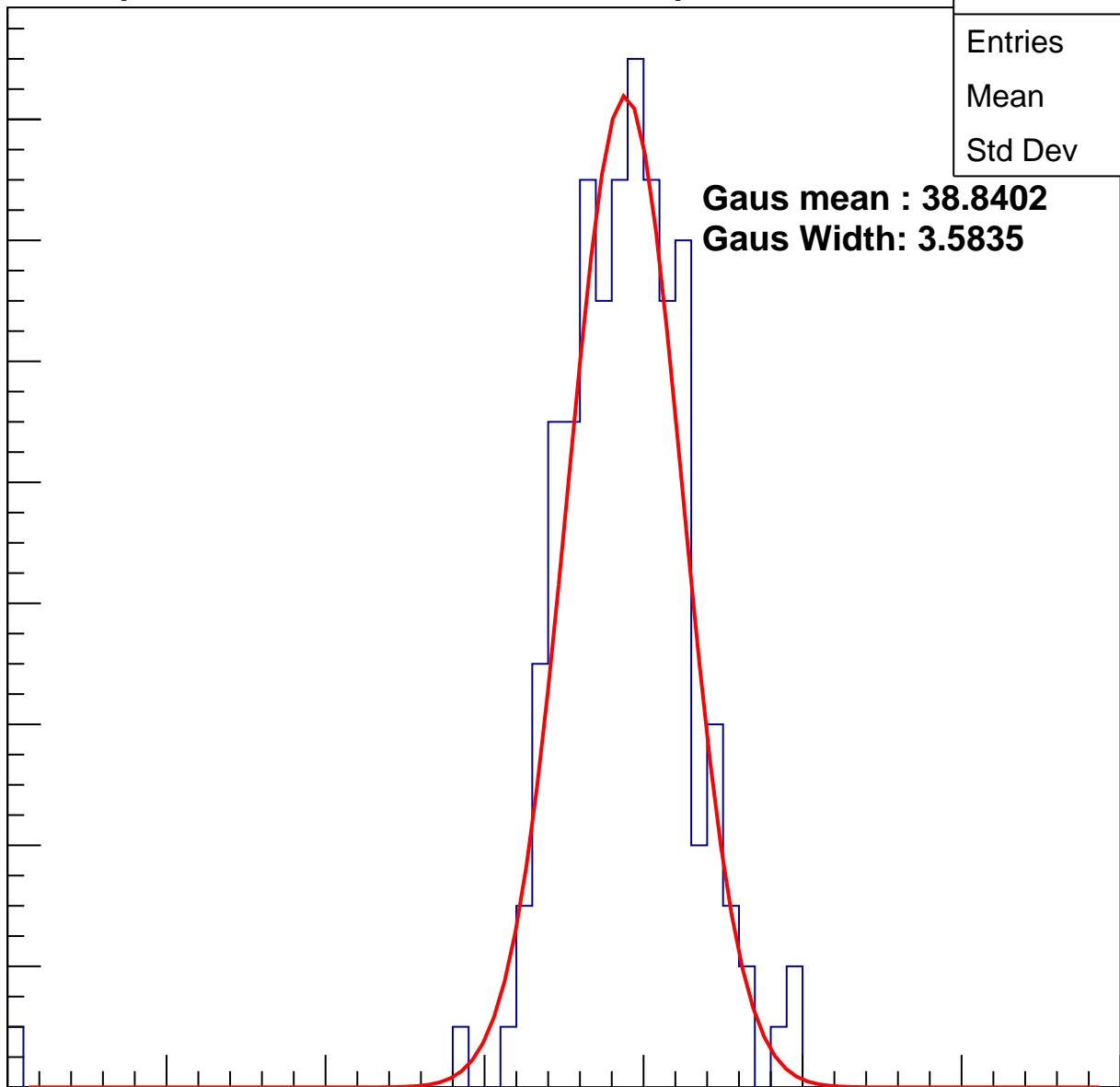
40

50

60

70

ampl



B0L101S, U25-ch42, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

45.52

Std Dev

3.309

Gaus mean : 45.9319

Gaus Width: 3.2208

0

10

20

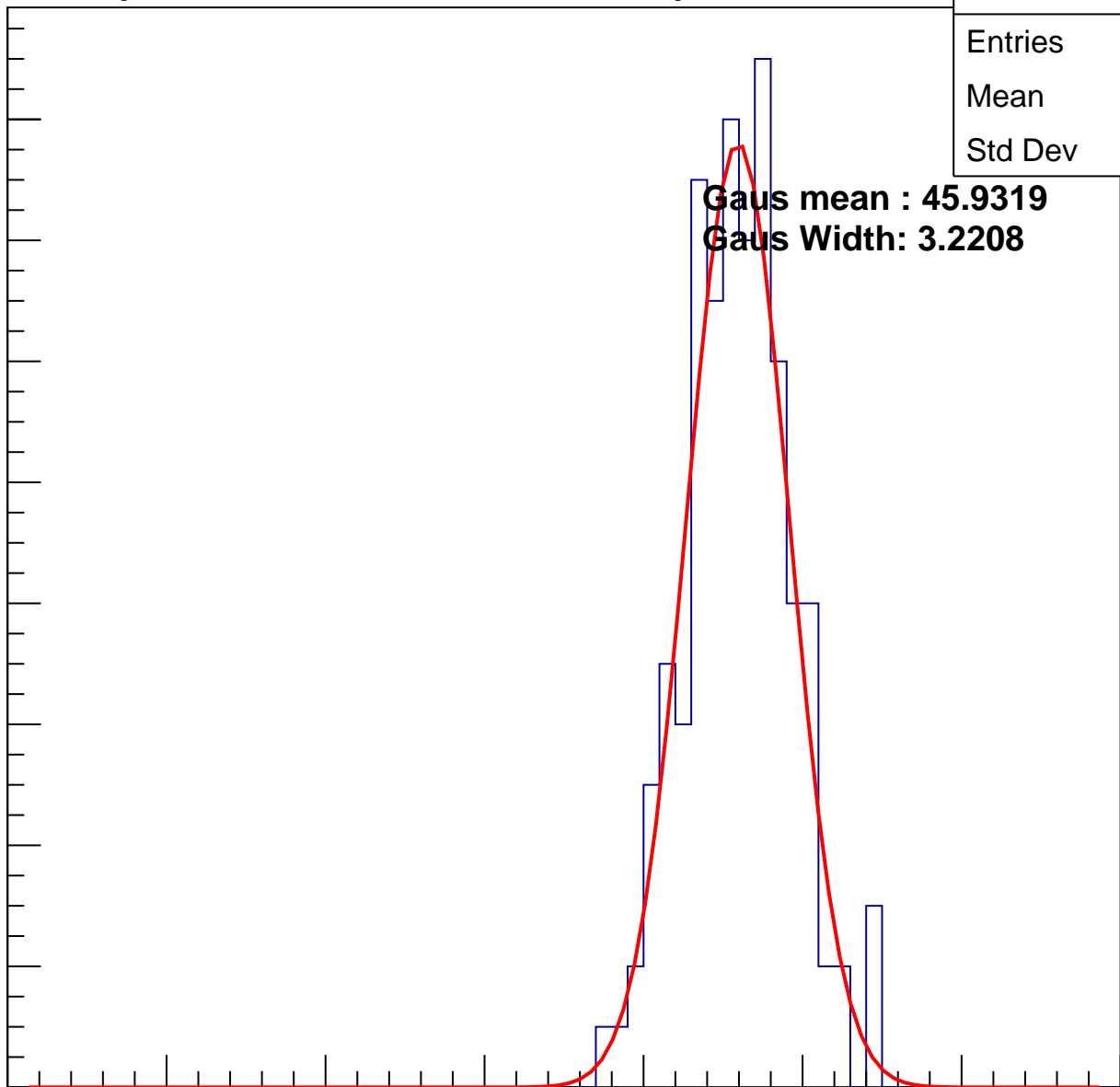
30

40

50

60

ampl



B0L101S, U25-ch42, adc3

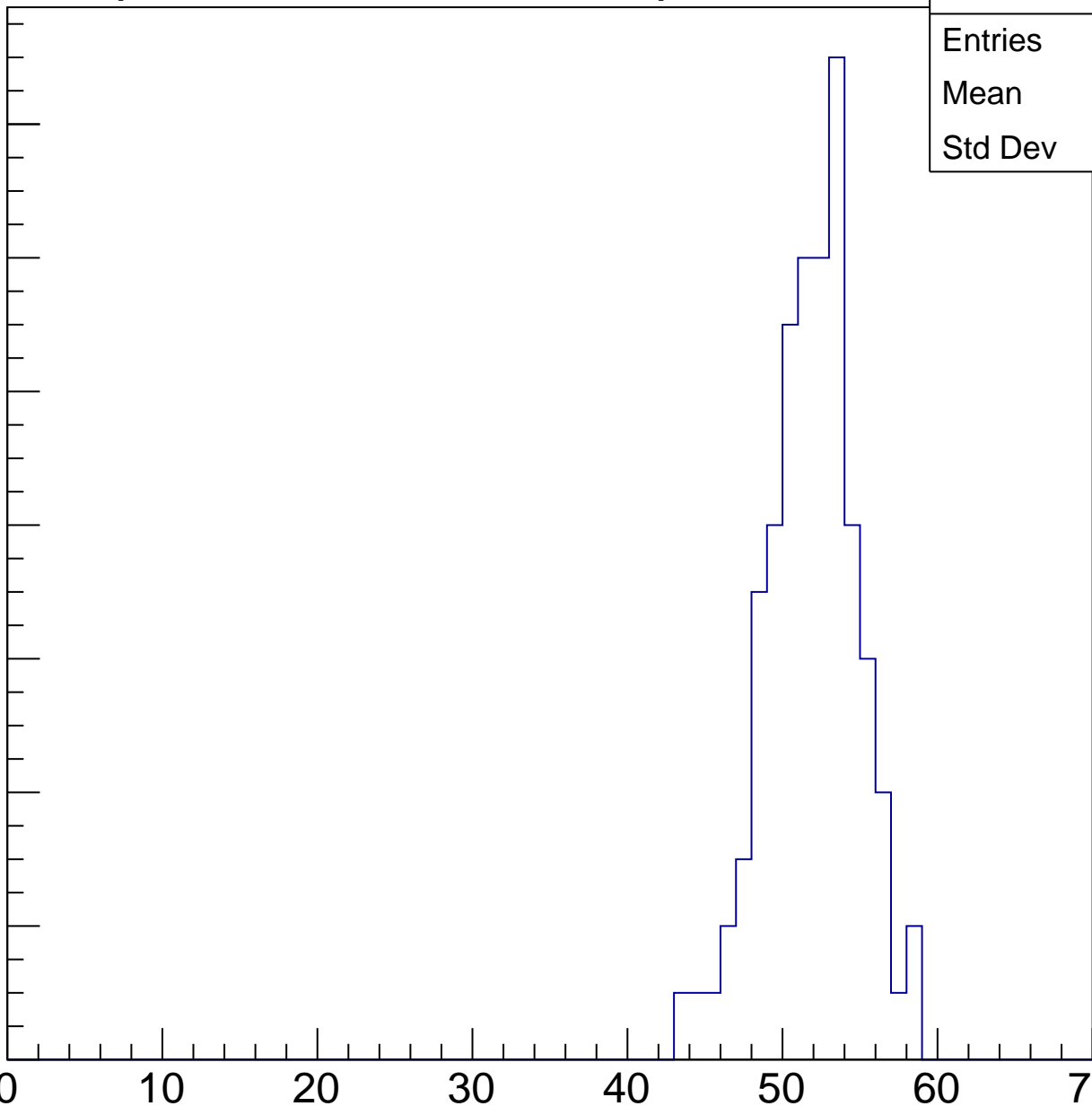
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	51.41
Std Dev	2.937

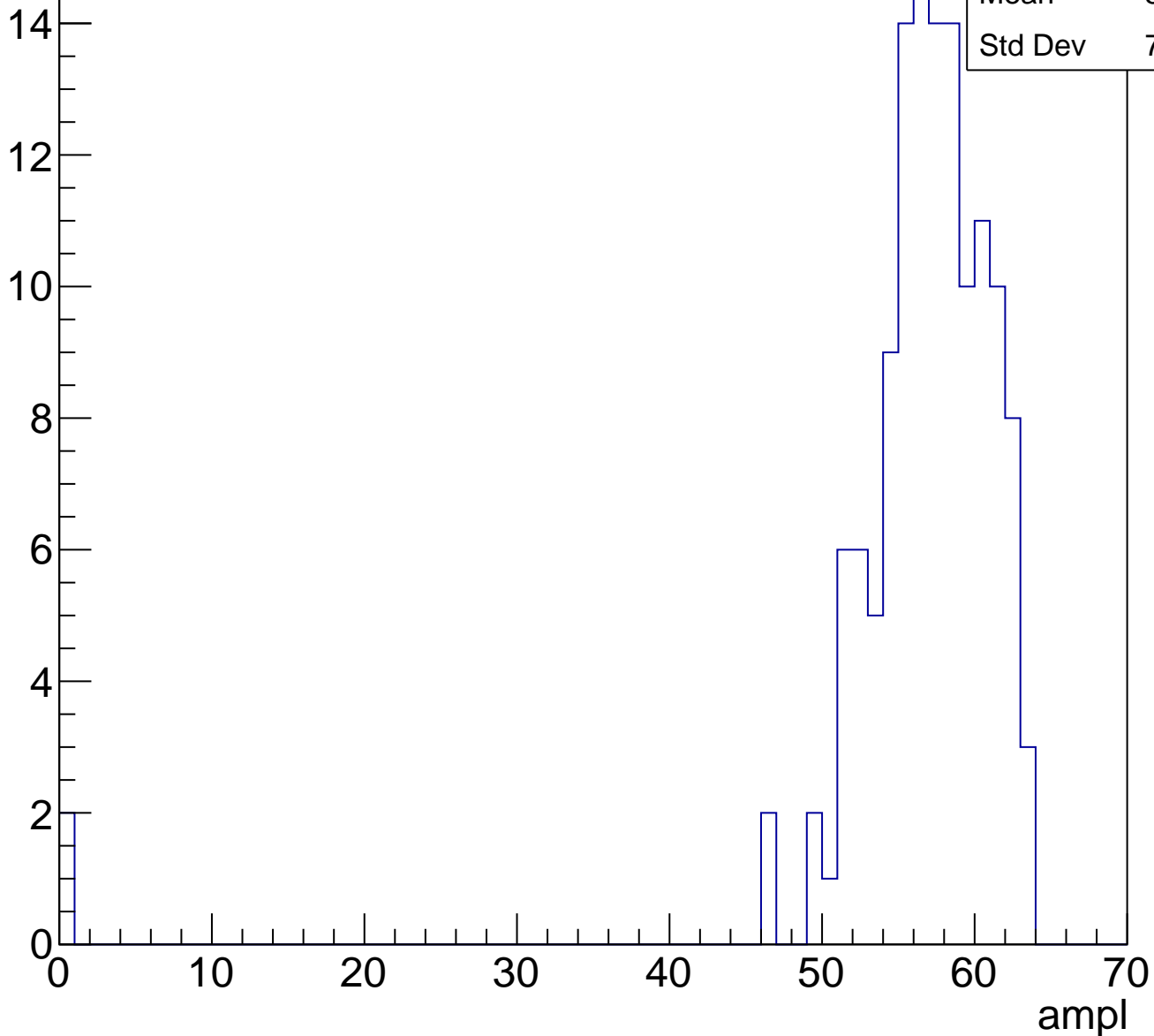
ampl



B0L101S, U25-ch42, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch42, adc5

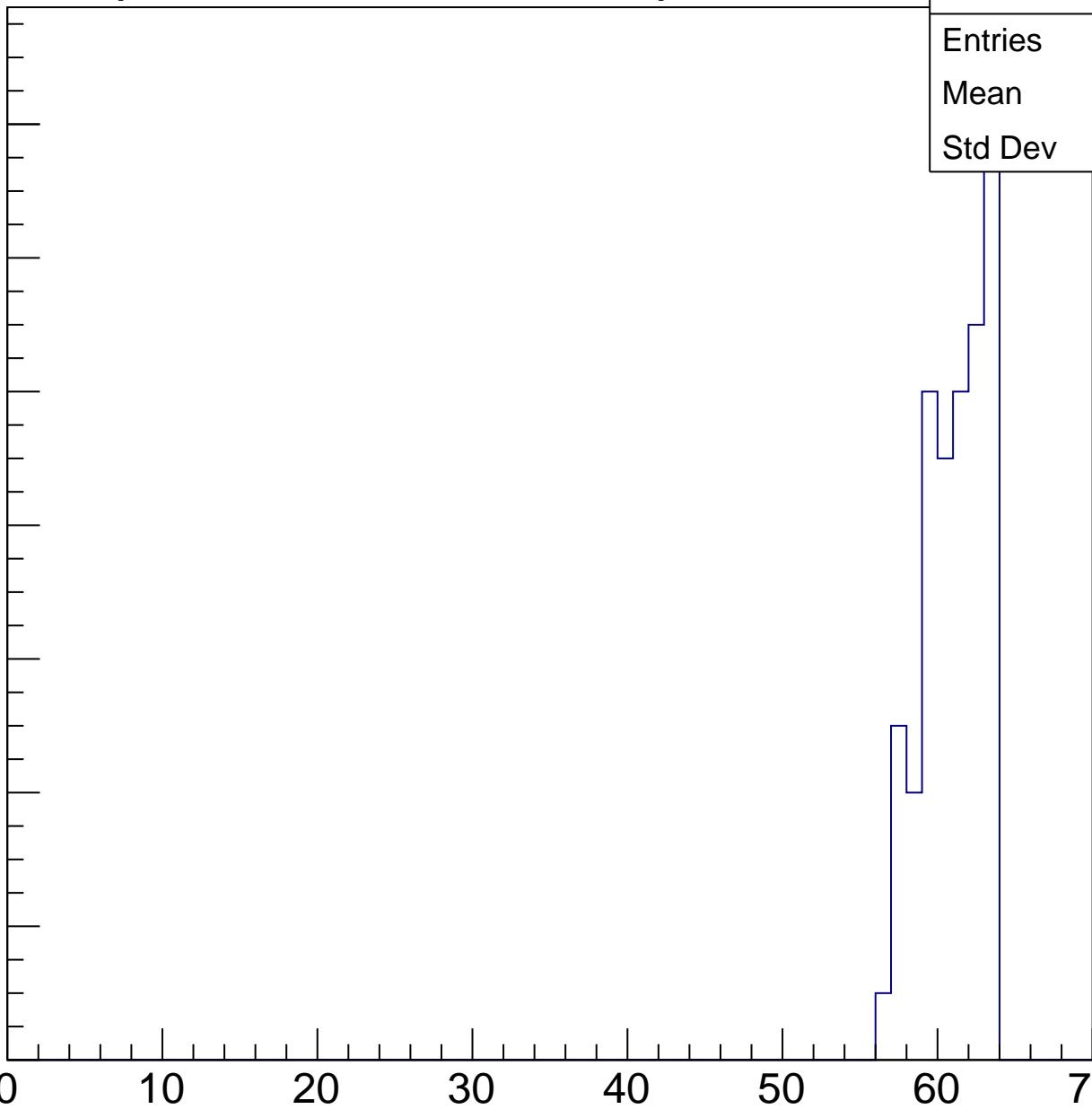
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	60.62
Std Dev	1.967

ampl



B0L101S, U25-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L101S, U25-ch43, adc0

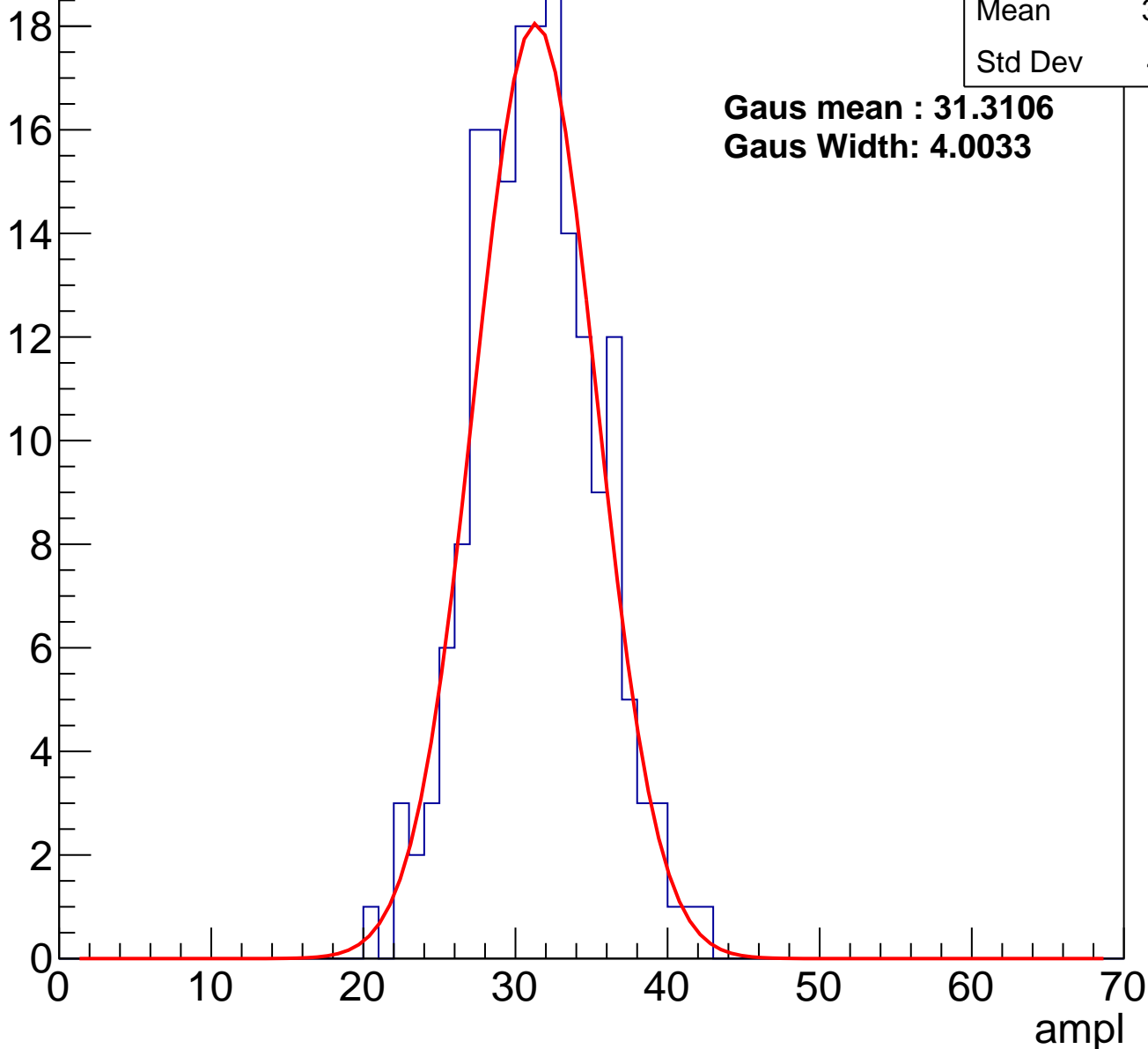
calib_packv5_042523_0143.root, FC#1, port C1

Entries	186
Mean	30.85
Std Dev	4.001

Gaus mean : 31.3106

Gaus Width: 4.0033

Entry



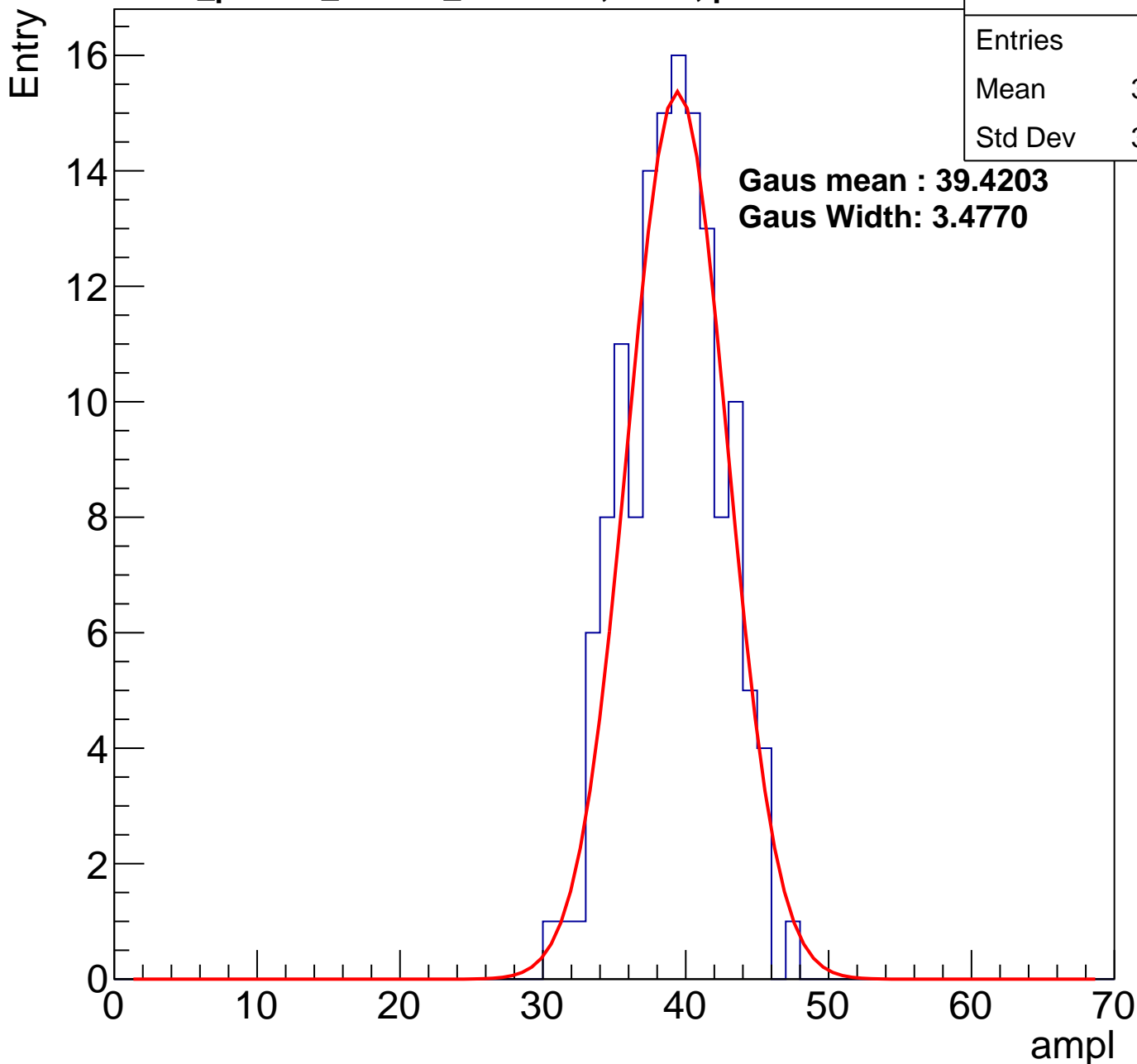
B0L101S, U25-ch43, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	38.64
Std Dev	3.357

Gaus mean : 39.4203

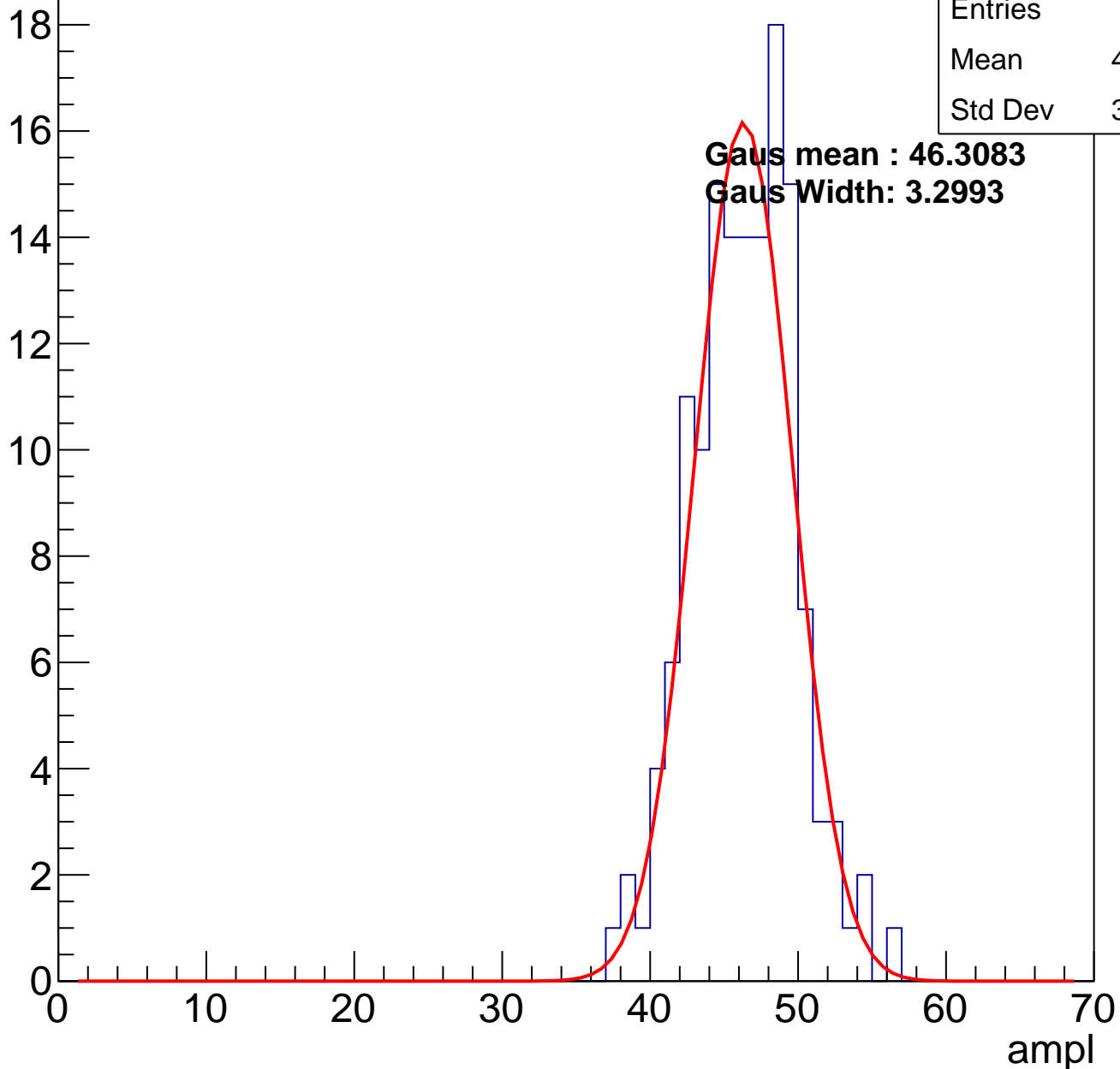
Gaus Width: 3.4770



B0L101S, U25-ch43, adc2

calib_packv5_042523_0143.root, FC#1, port C1

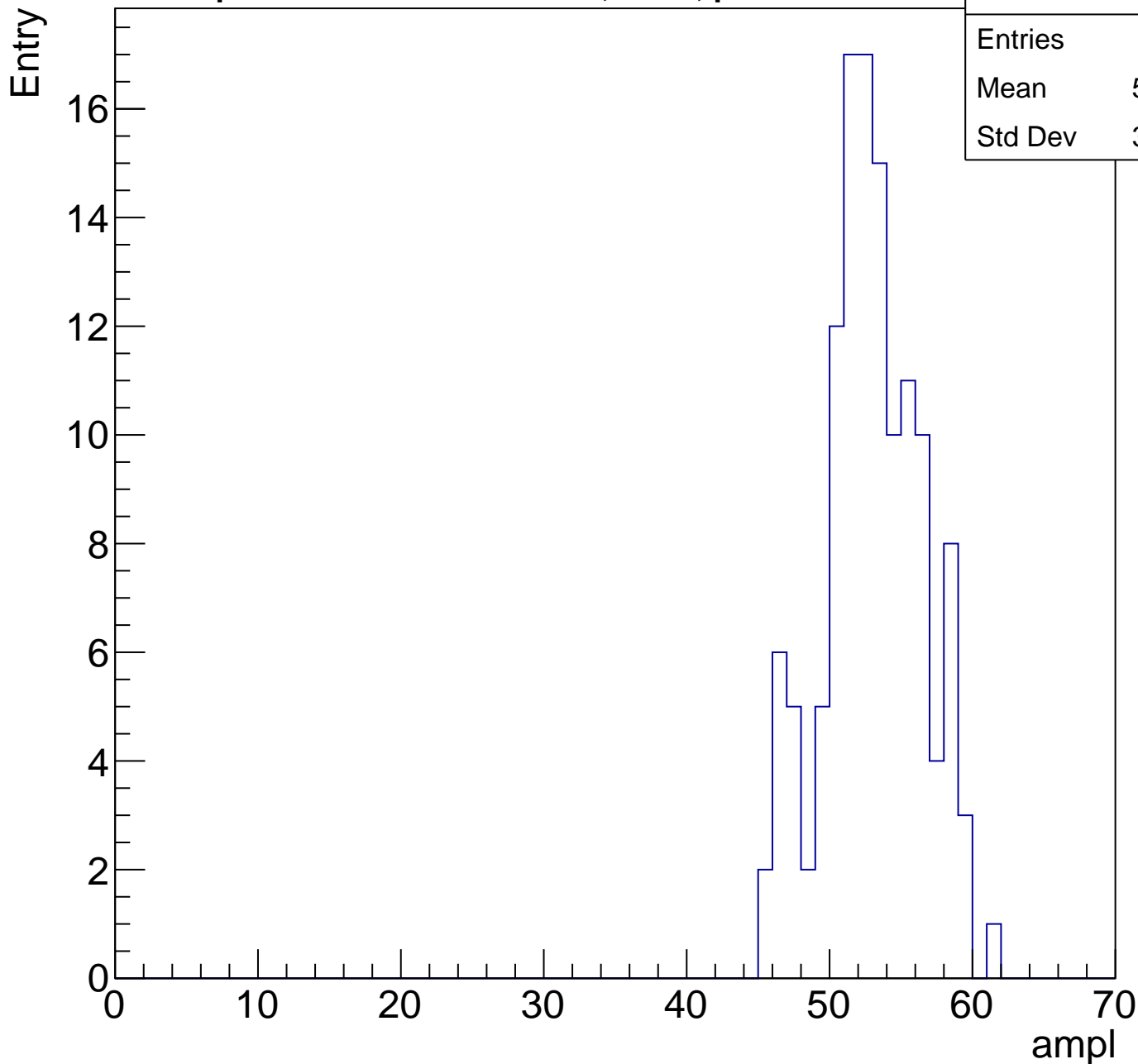
Entry



B0L101S, U25-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	52.52
Std Dev	3.396



B0L101S, U25-ch43, adc4

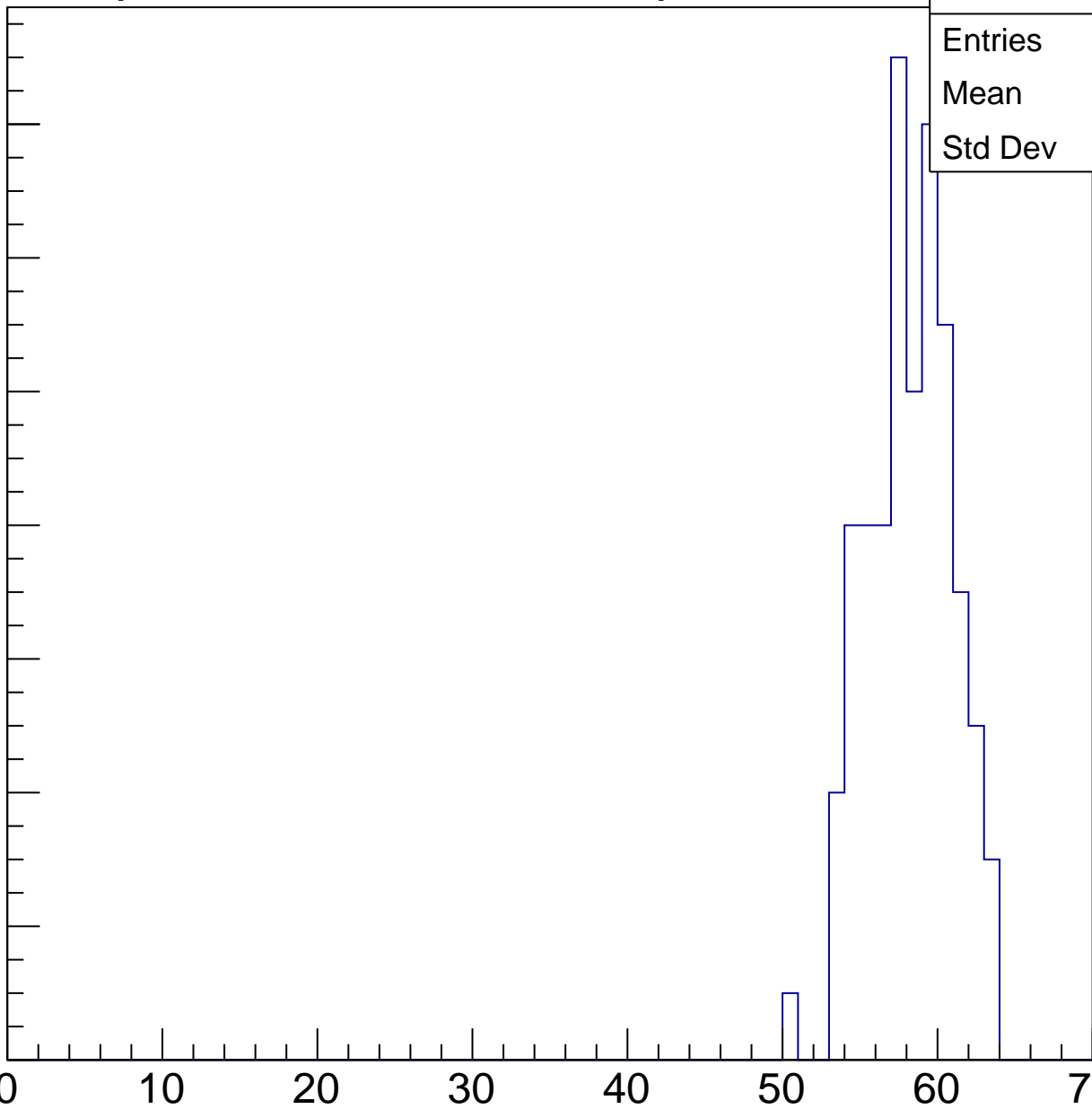
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	57.76
Std Dev	2.692

ampl



B0L101S, U25-ch43, adc5

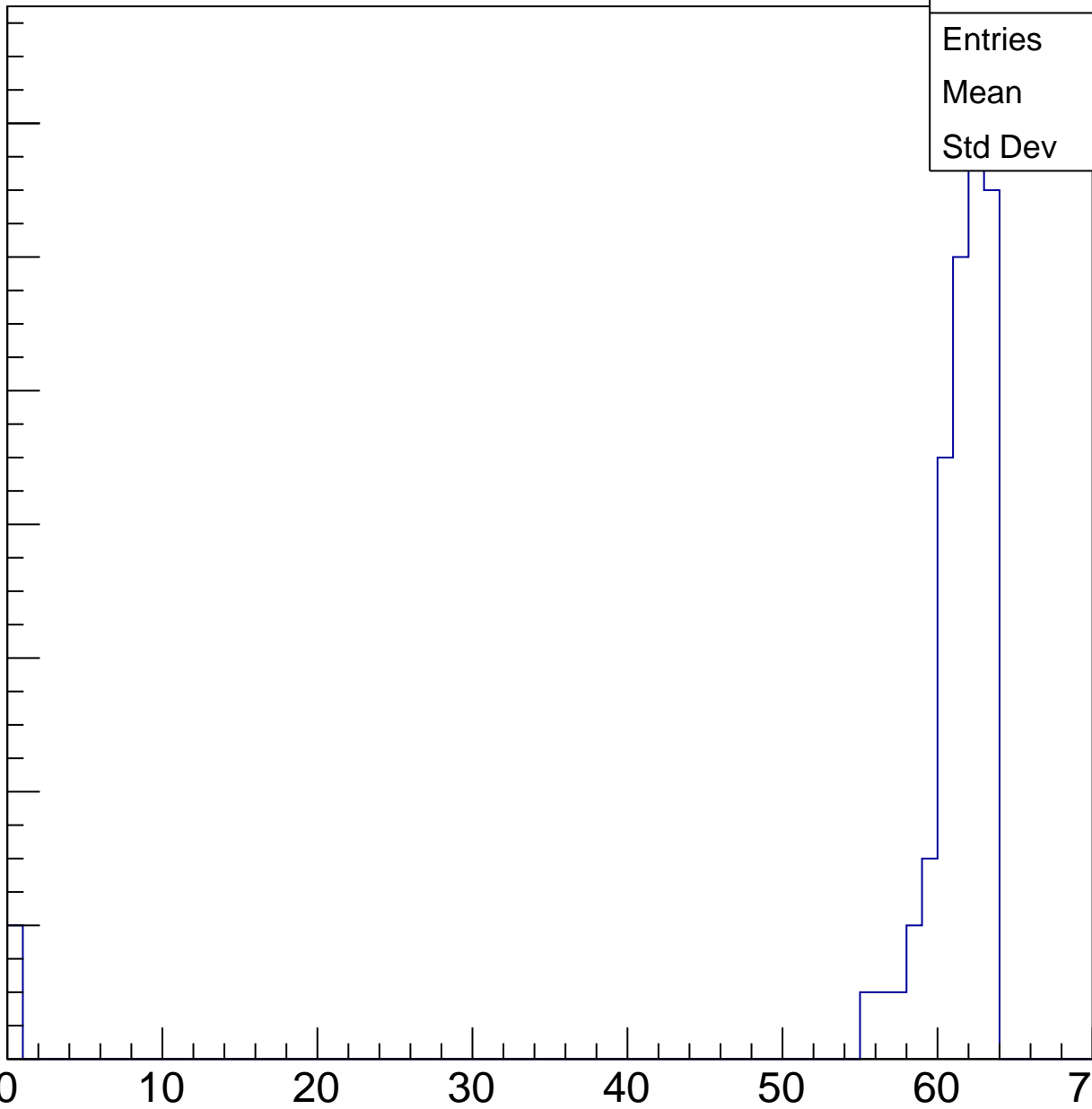
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.02
Std Dev	11.19

ampl



B0L101S, U25-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

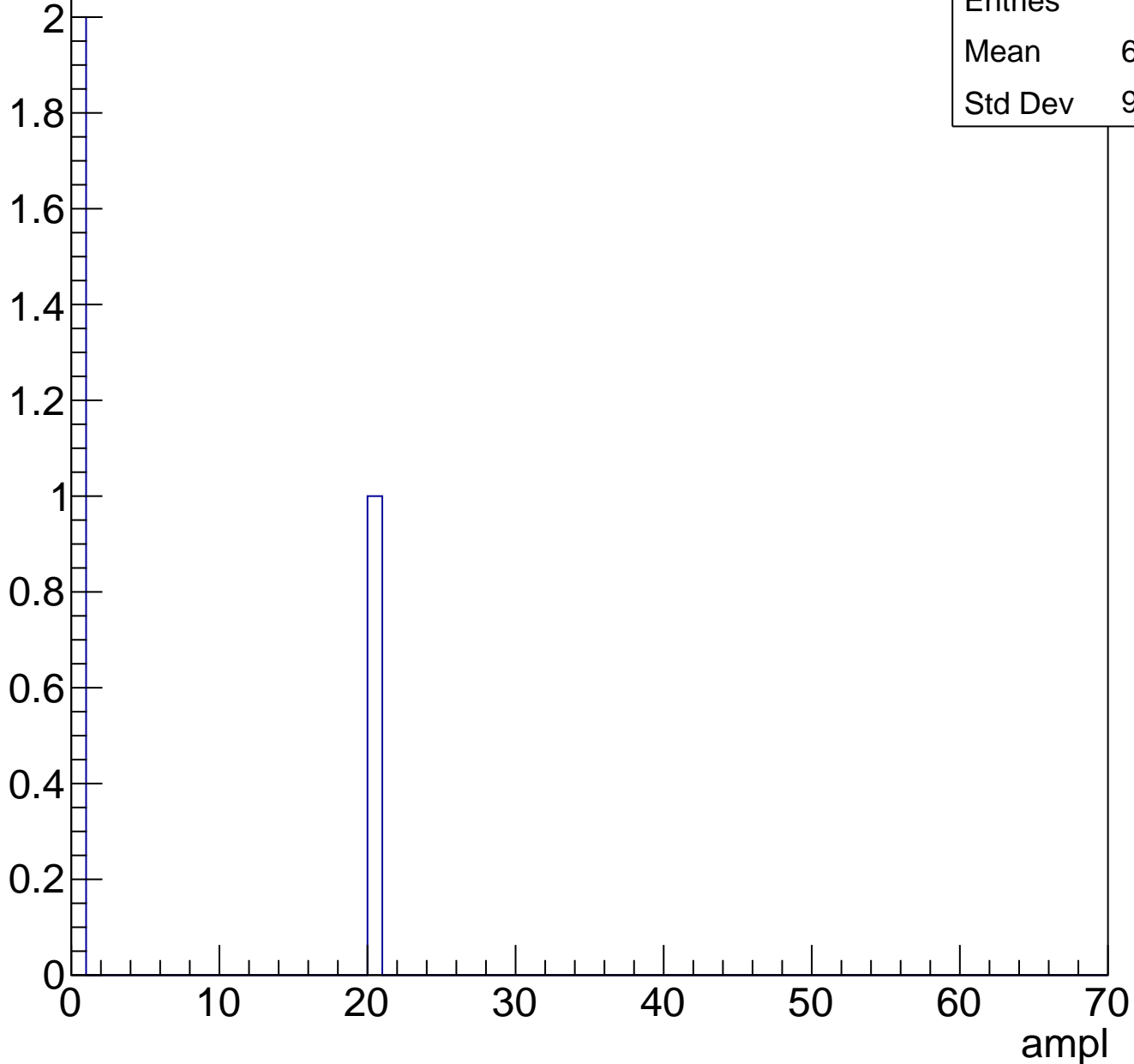
Entry



B0L101S, U25-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L101S, U25-ch44, adc0

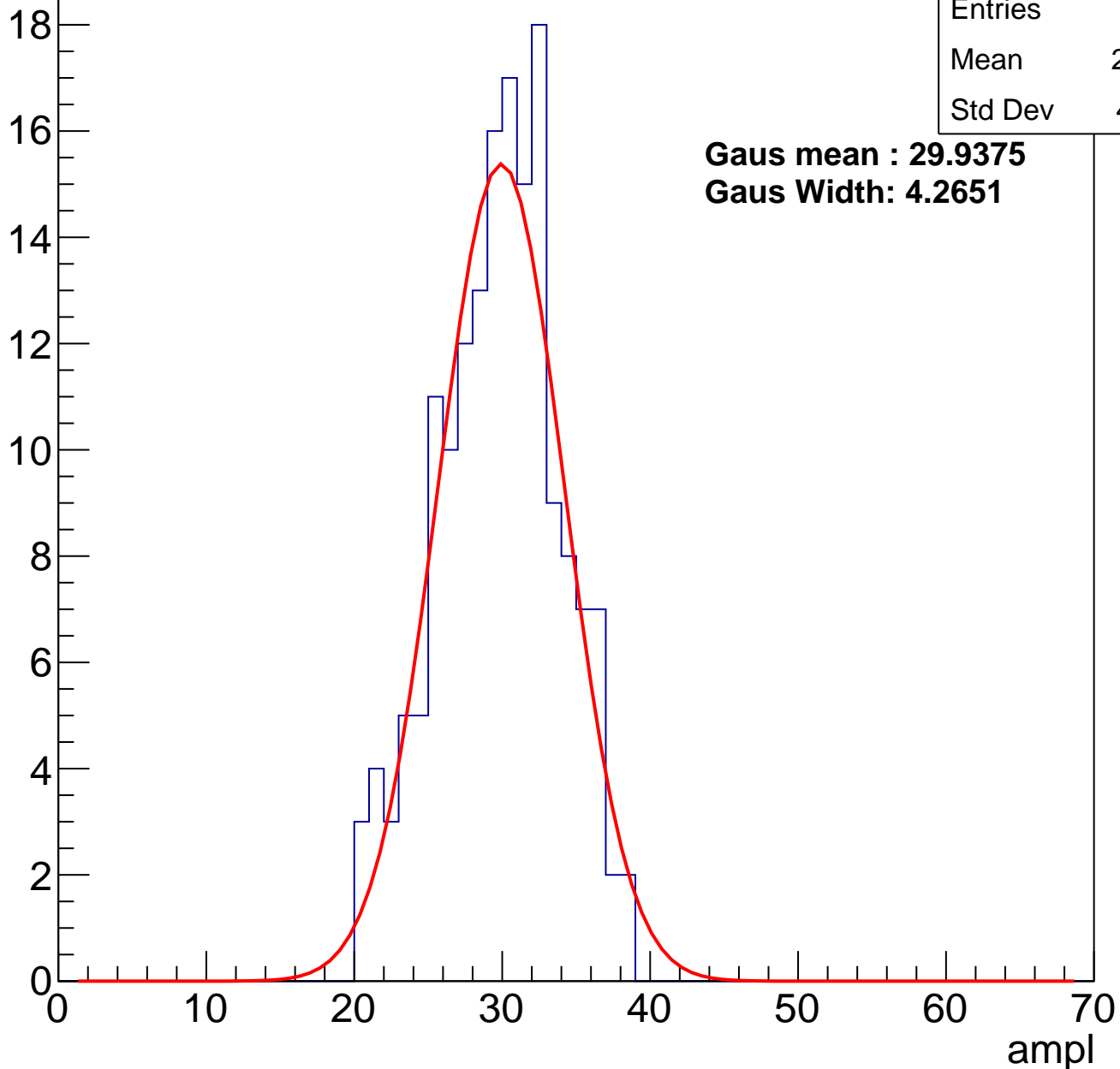
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	29.34
Std Dev	4.041

Gaus mean : 29.9375

Gaus Width: 4.2651

Entry



B0L101S, U25-ch44, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	36.35
Std Dev	3.767

Gaus mean : 37.0662

Gaus Width: 3.5084

Entry

12

10

8

6

4

2

0

0

10

20

30

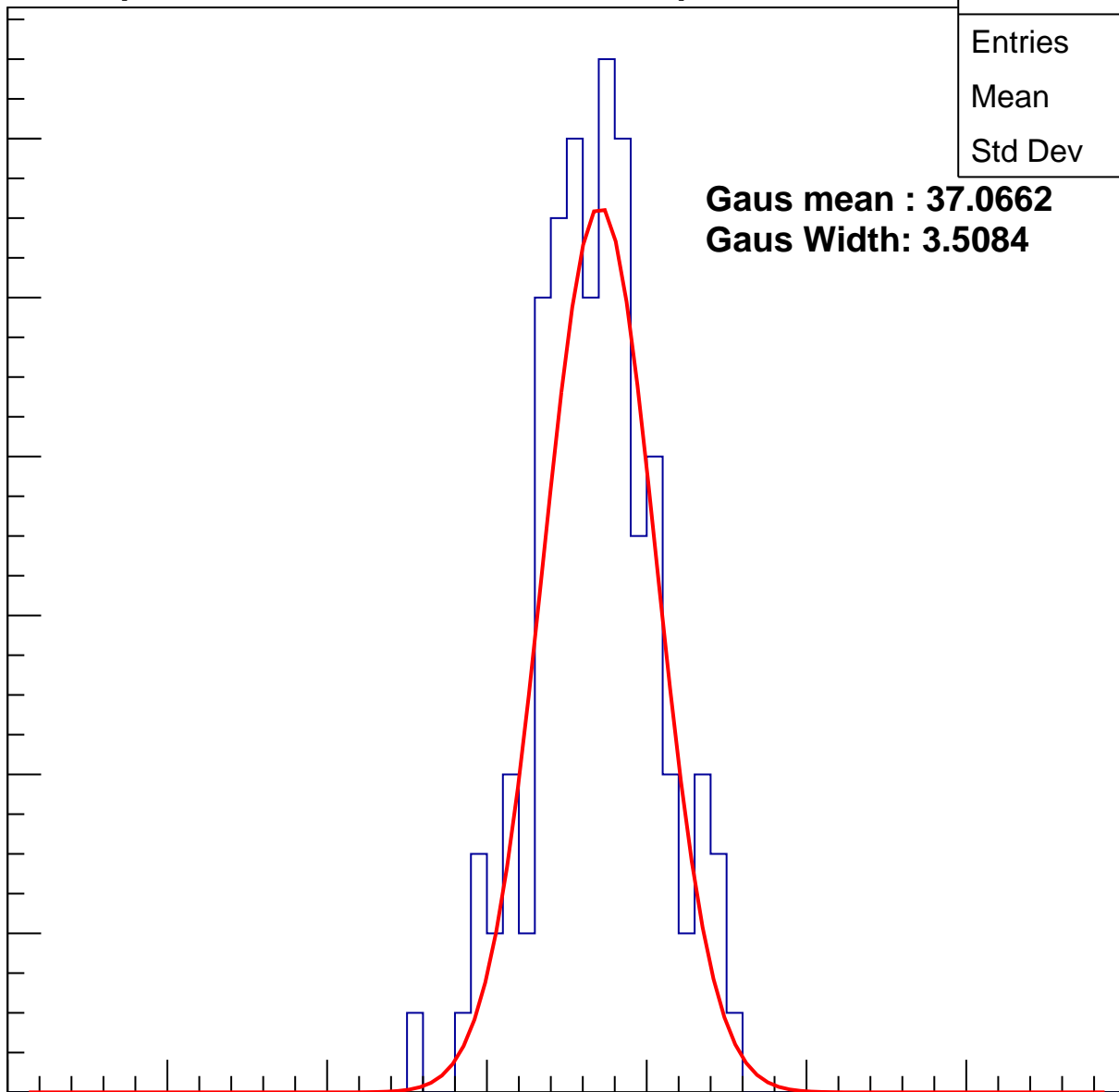
40

50

60

70

ampl



B0L101S, U25-ch44, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

157

Mean

43.22

Std Dev

3.871

Gaus mean : 43.8505

Gaus Width: 3.9981

0

10

20

30

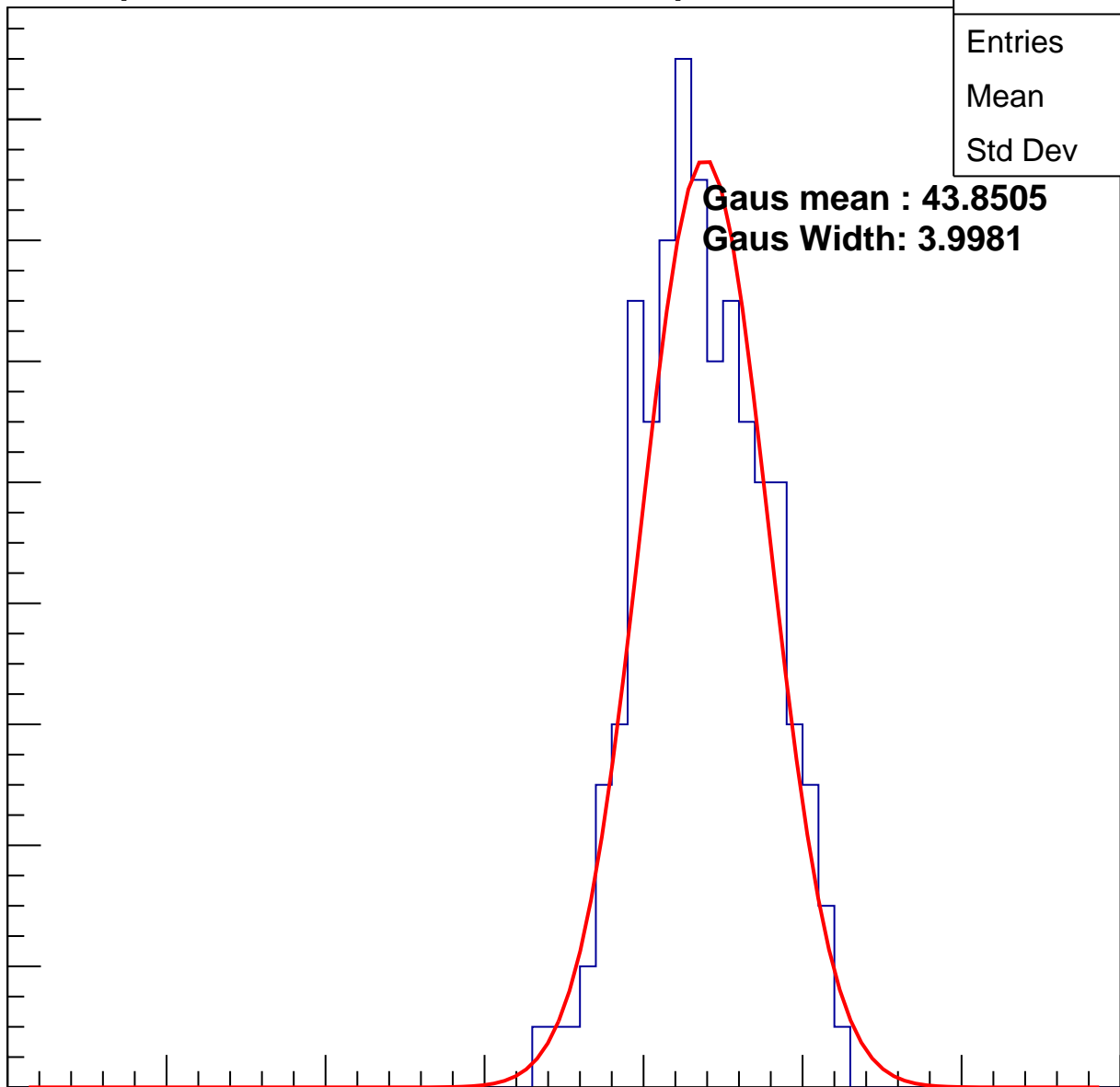
40

50

60

70

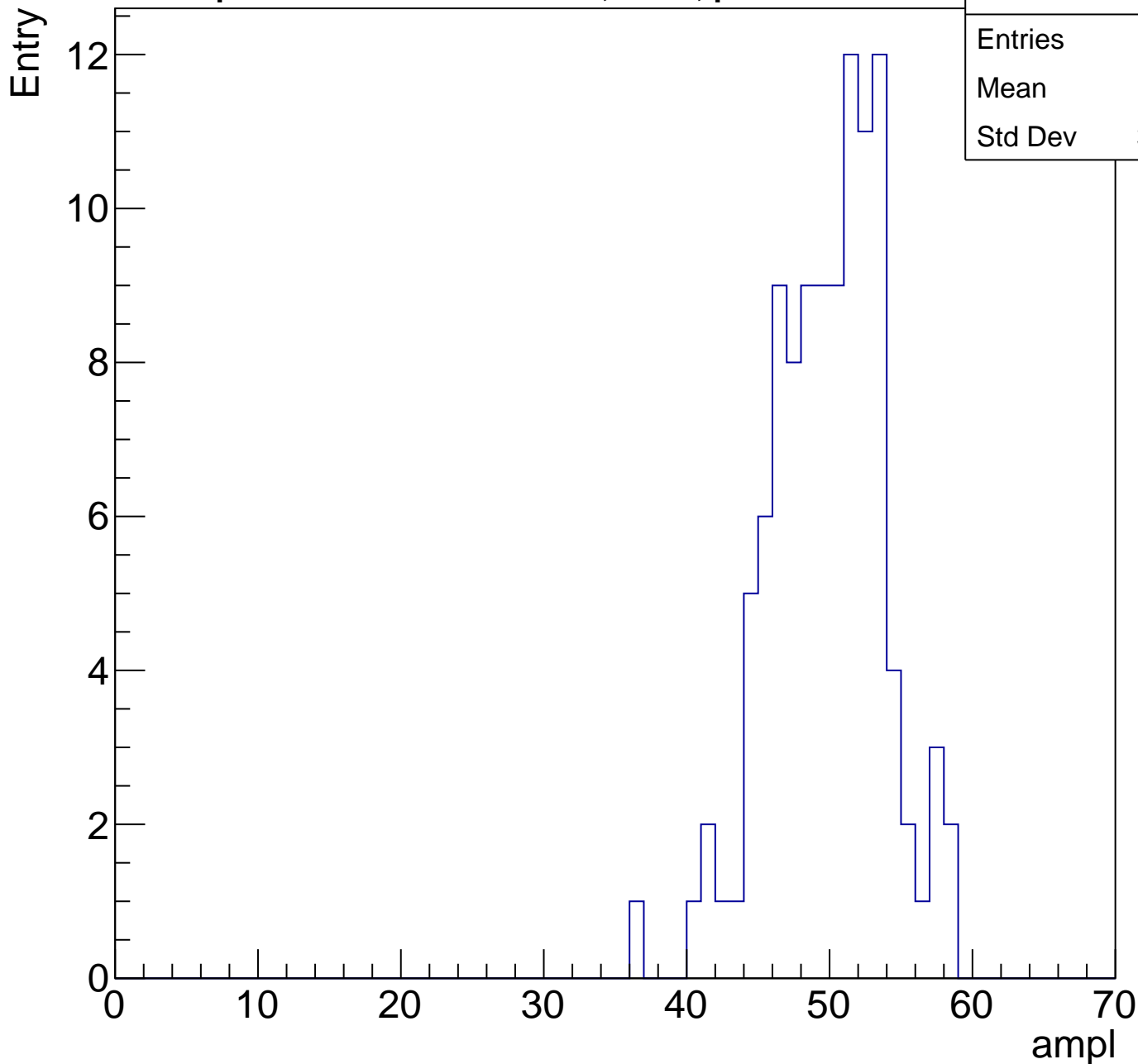
ampl



B0L101S, U25-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

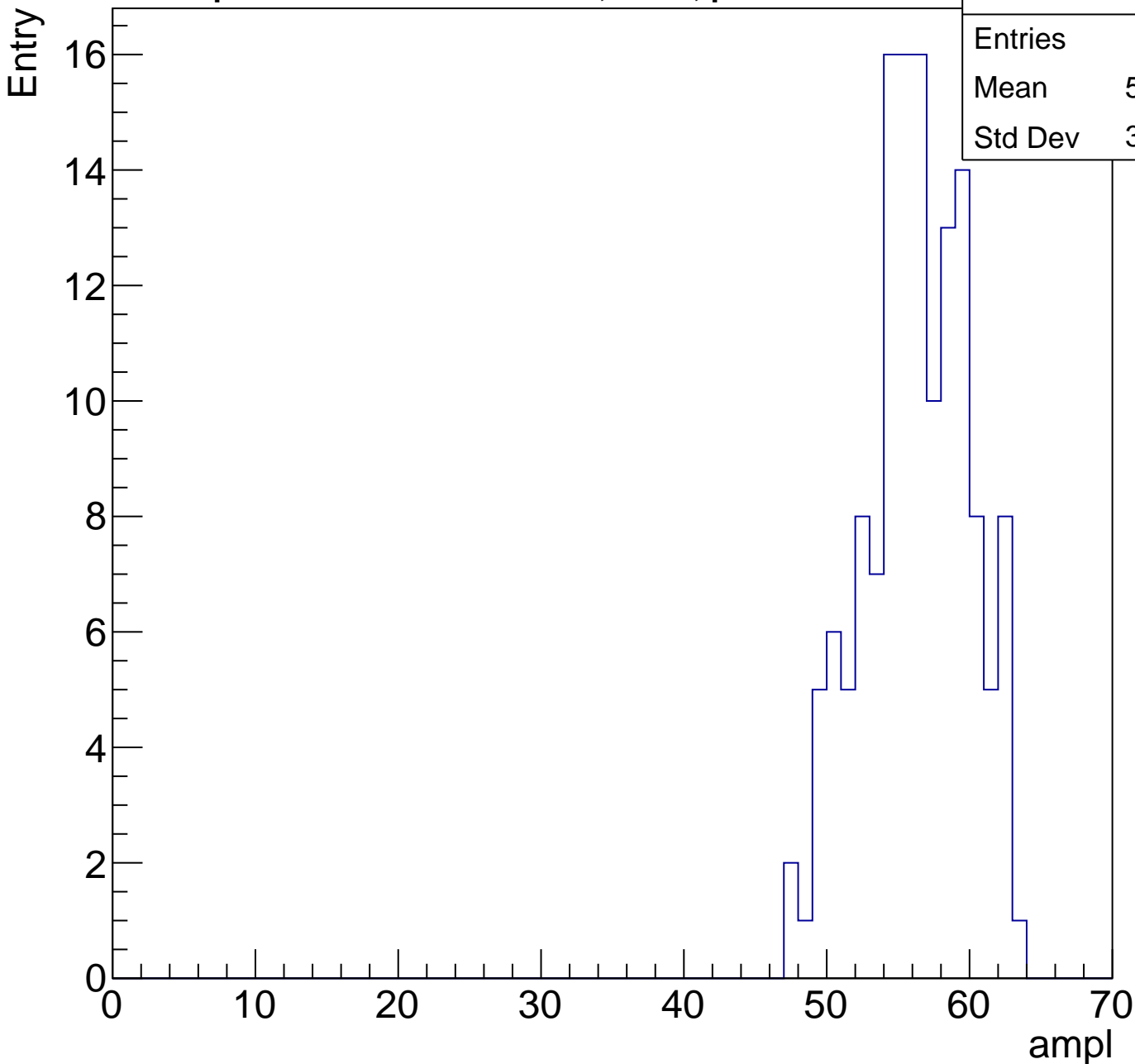
Entries	108
Mean	49.4
Std Dev	3.981



B0L101S, U25-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	55.77
Std Dev	3.625



B0L101S, U25-ch44, adc5

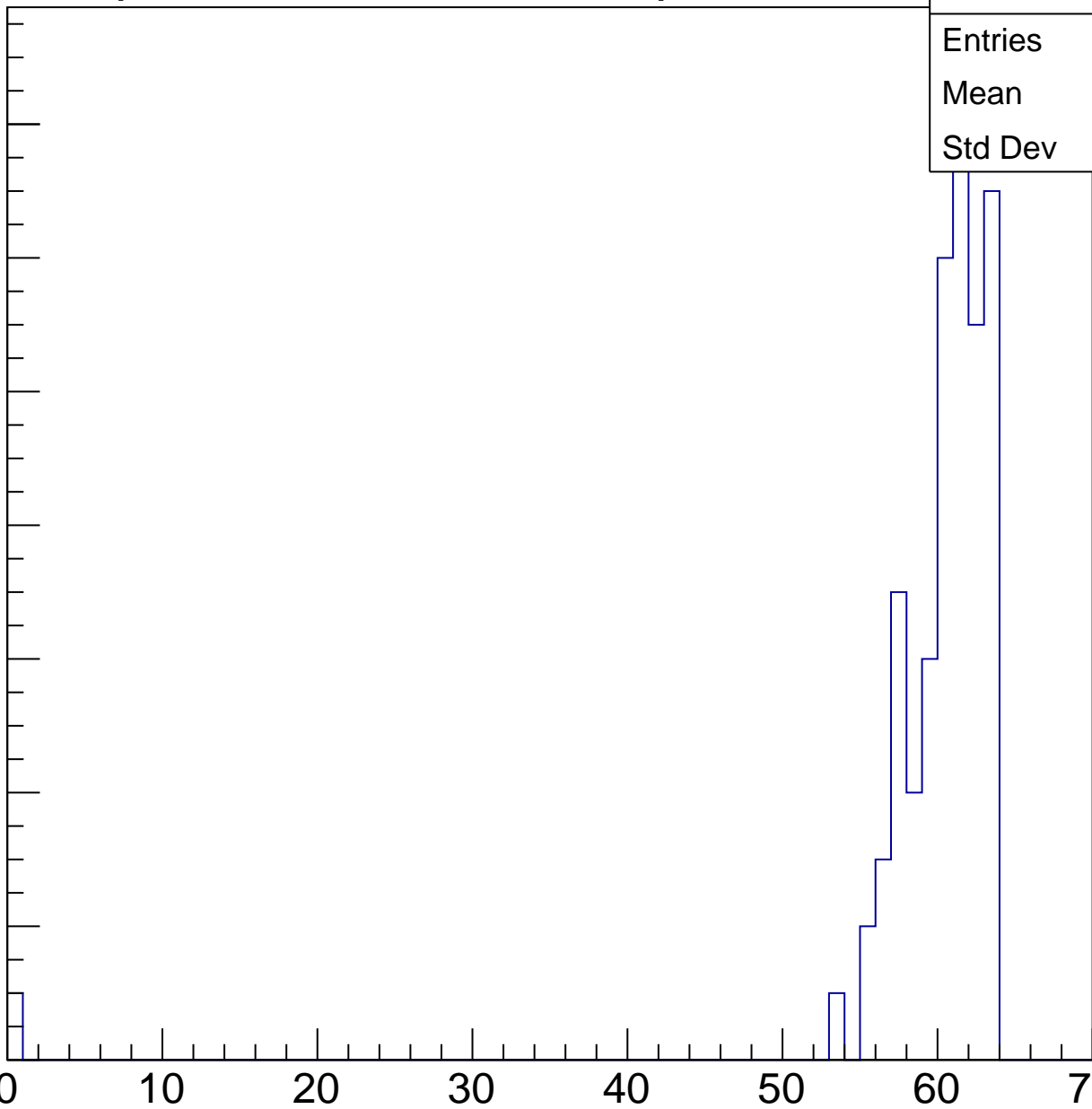
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	59.36
Std Dev	7.282

ampl

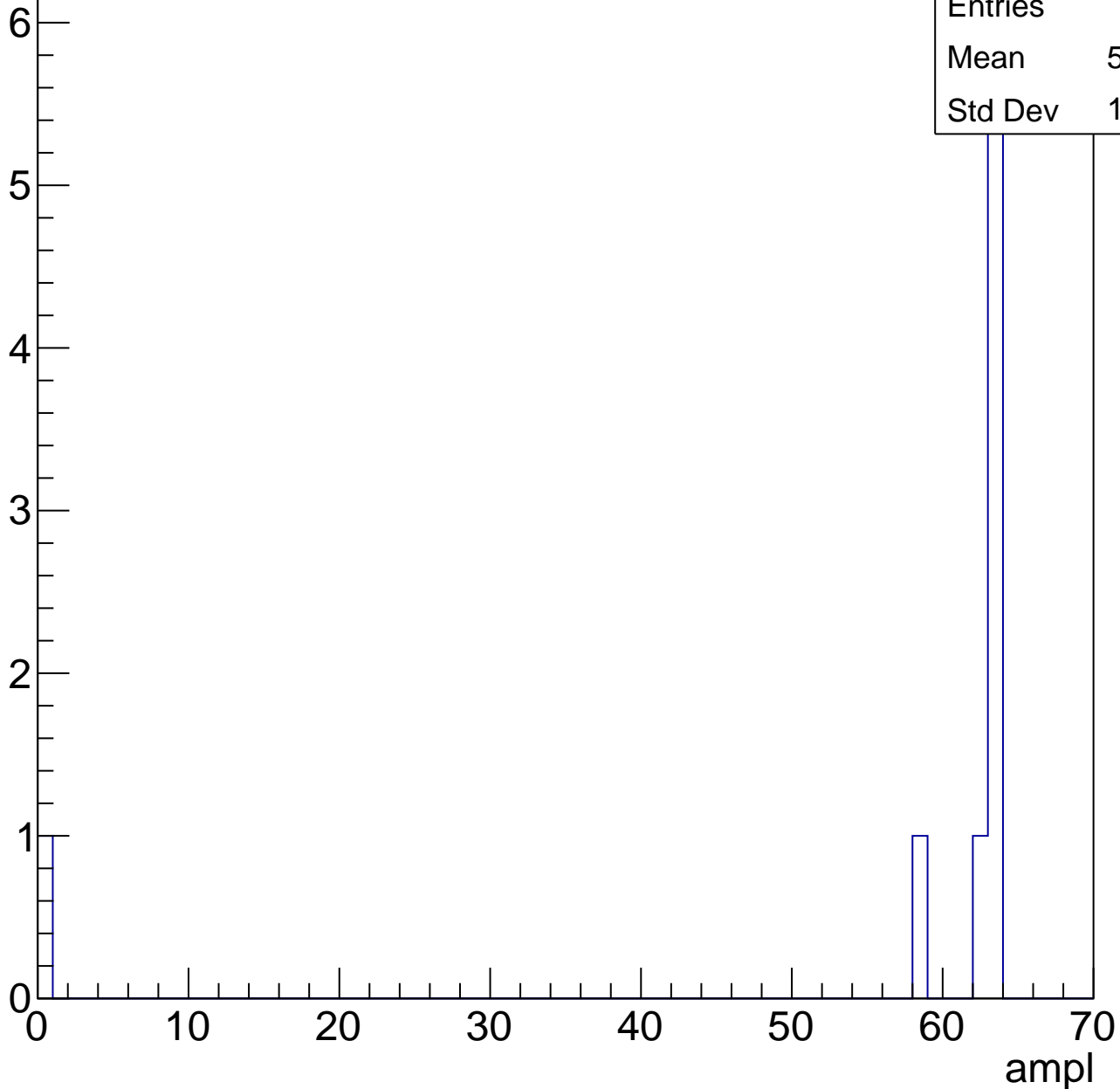


B0L101S, U25-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	55.33
Std Dev	19.62



B0L101S, U25-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch45, adc0

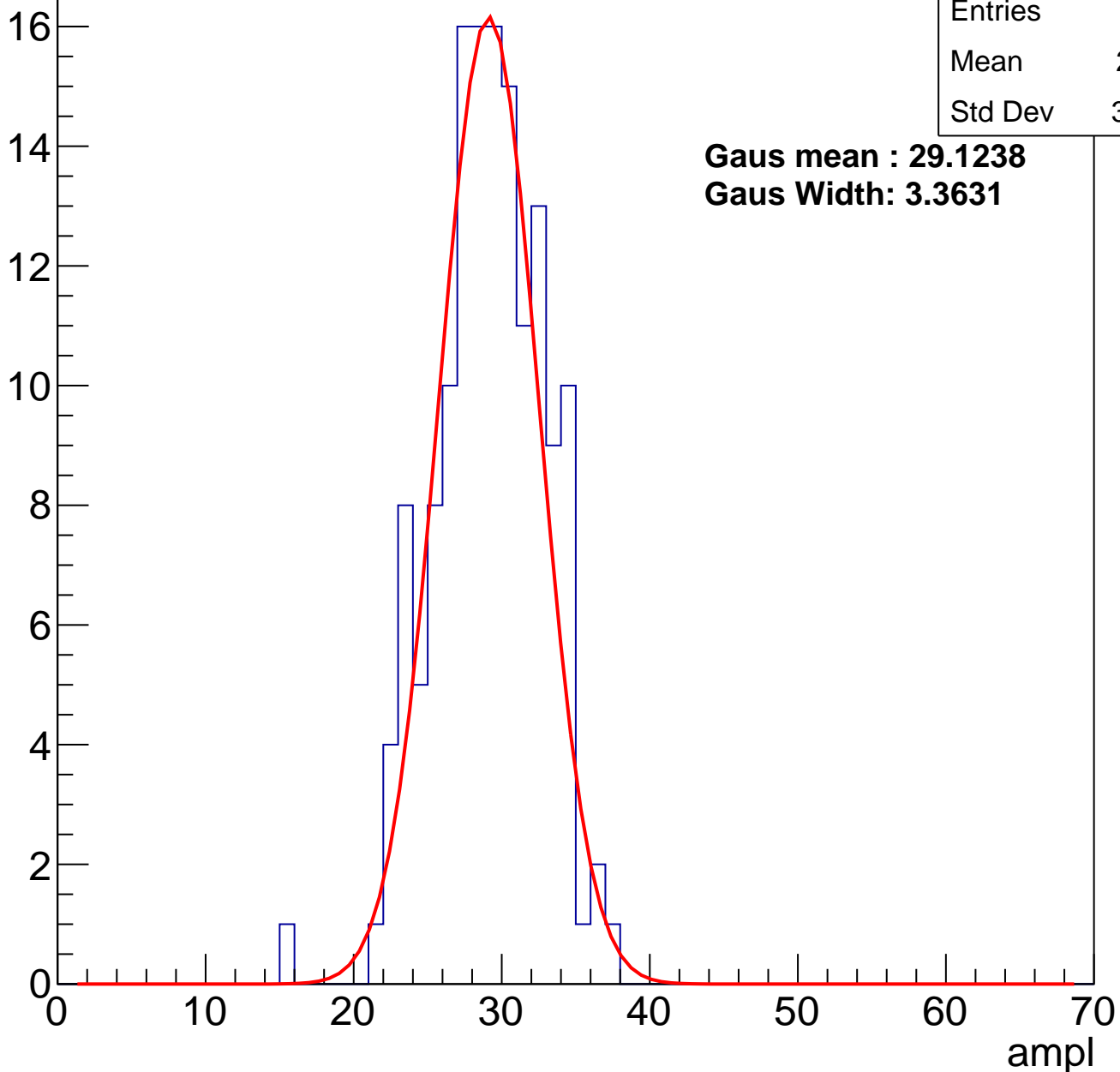
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	28.71
Std Dev	3.616

Gaus mean : 29.1238

Gaus Width: 3.3631

Entry



B0L101S, U25-ch45, adc1

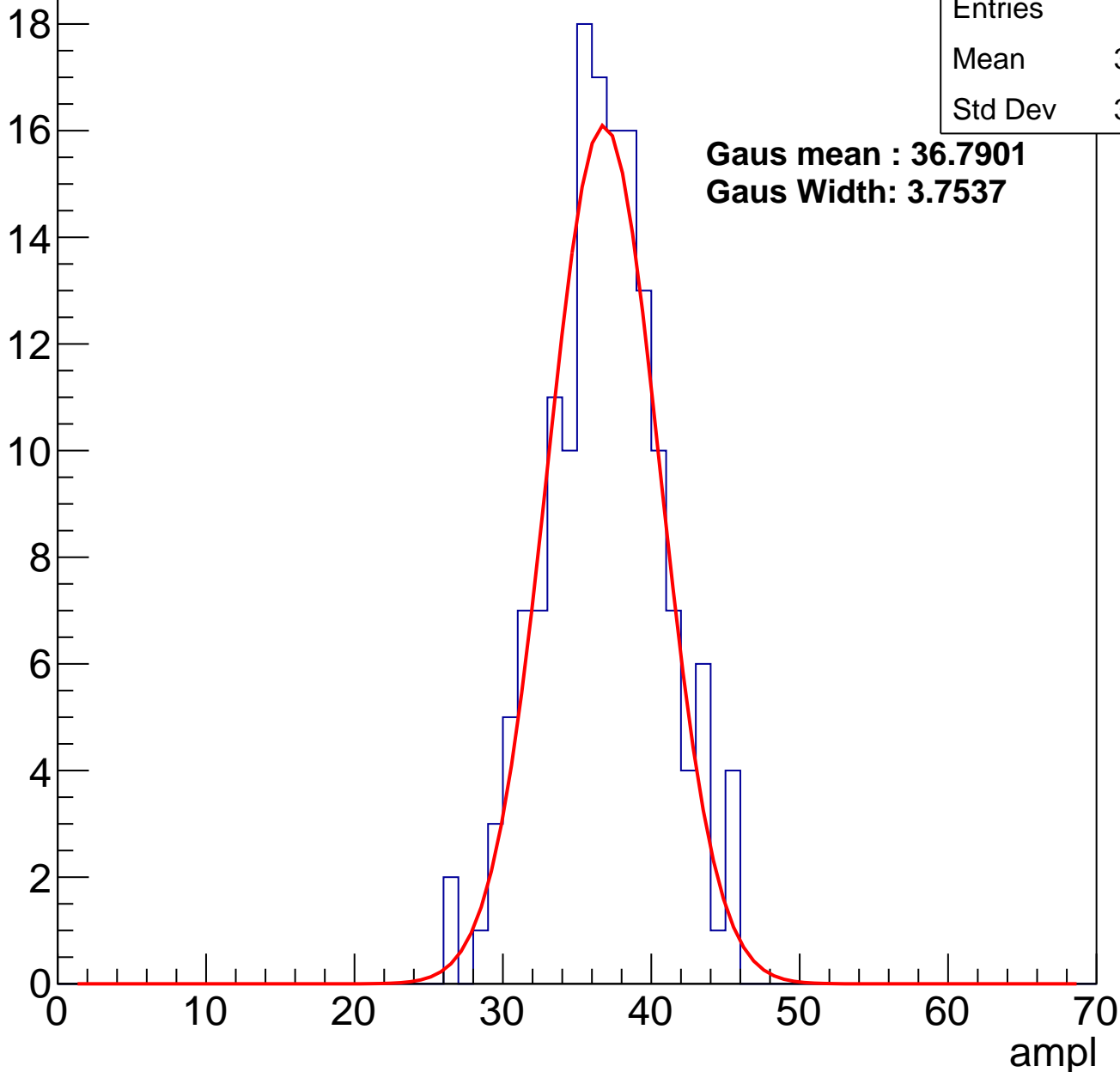
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	36.37
Std Dev	3.874

Gaus mean : 36.7901

Gaus Width: 3.7537

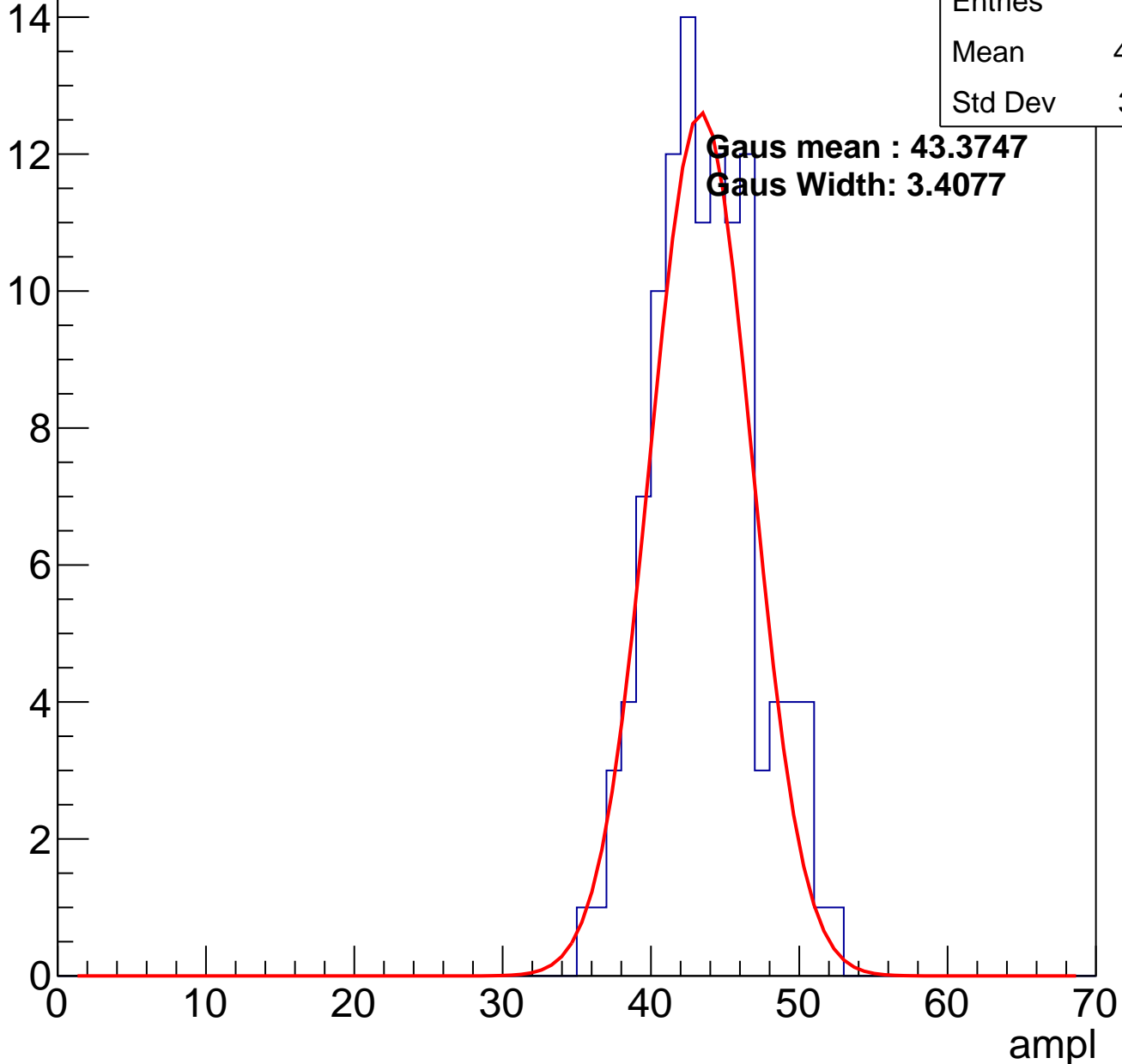
Entry



B0L101S, U25-ch45, adc2

calib_packv5_042523_0143.root, FC#1, port C1

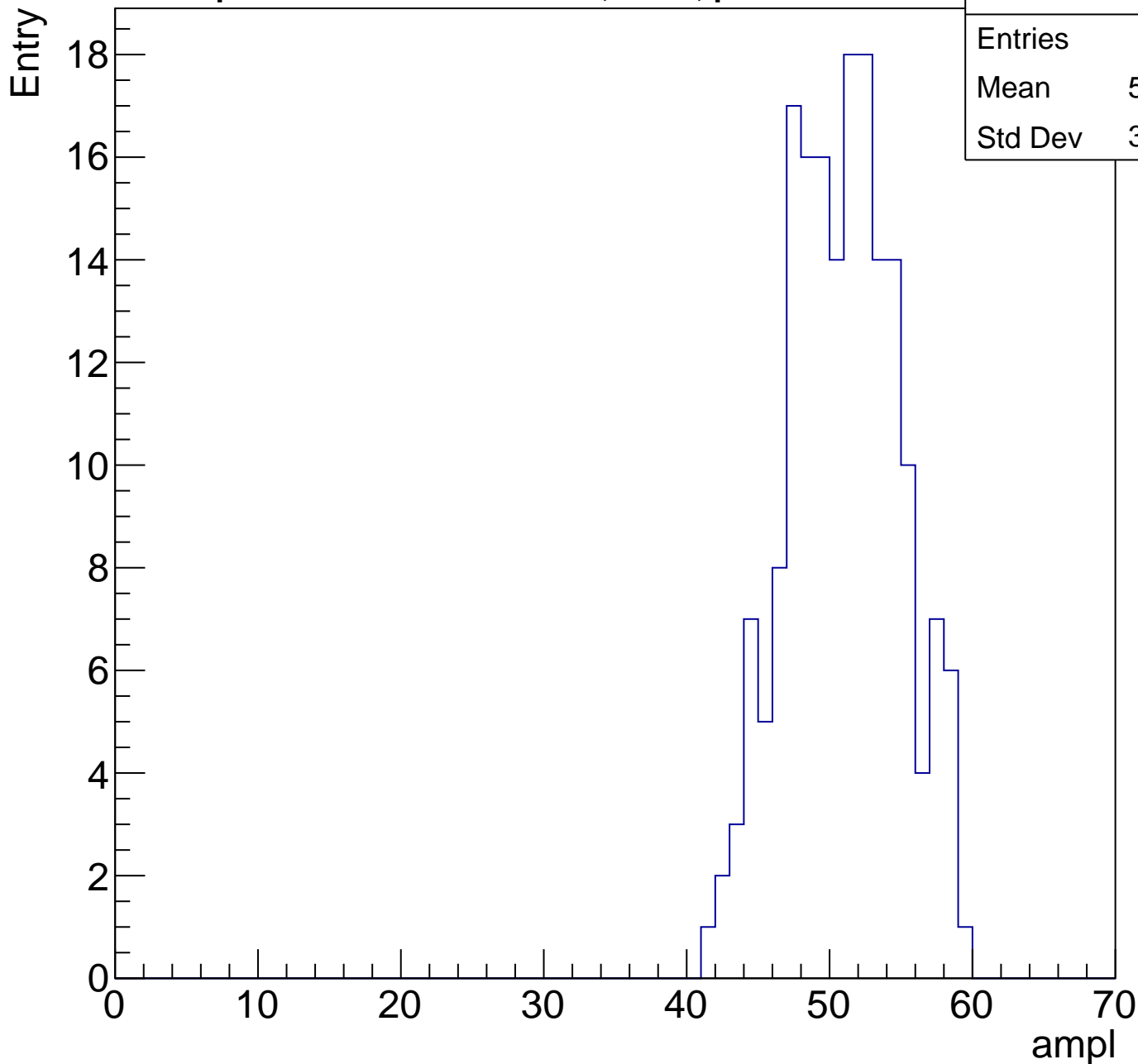
Entry



B0L101S, U25-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

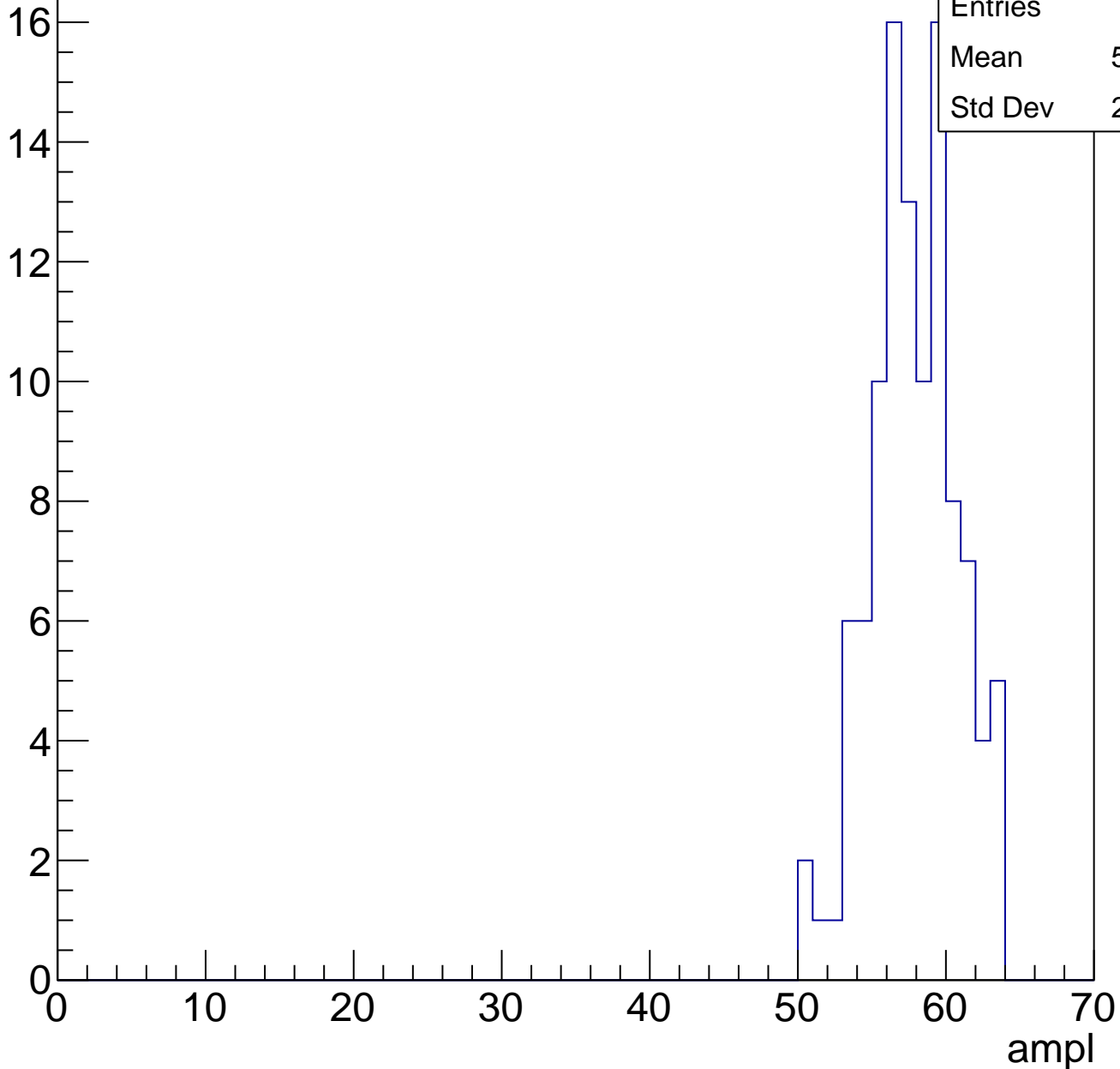
Entries	181
Mean	50.49
Std Dev	3.857



B0L101S, U25-ch45, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

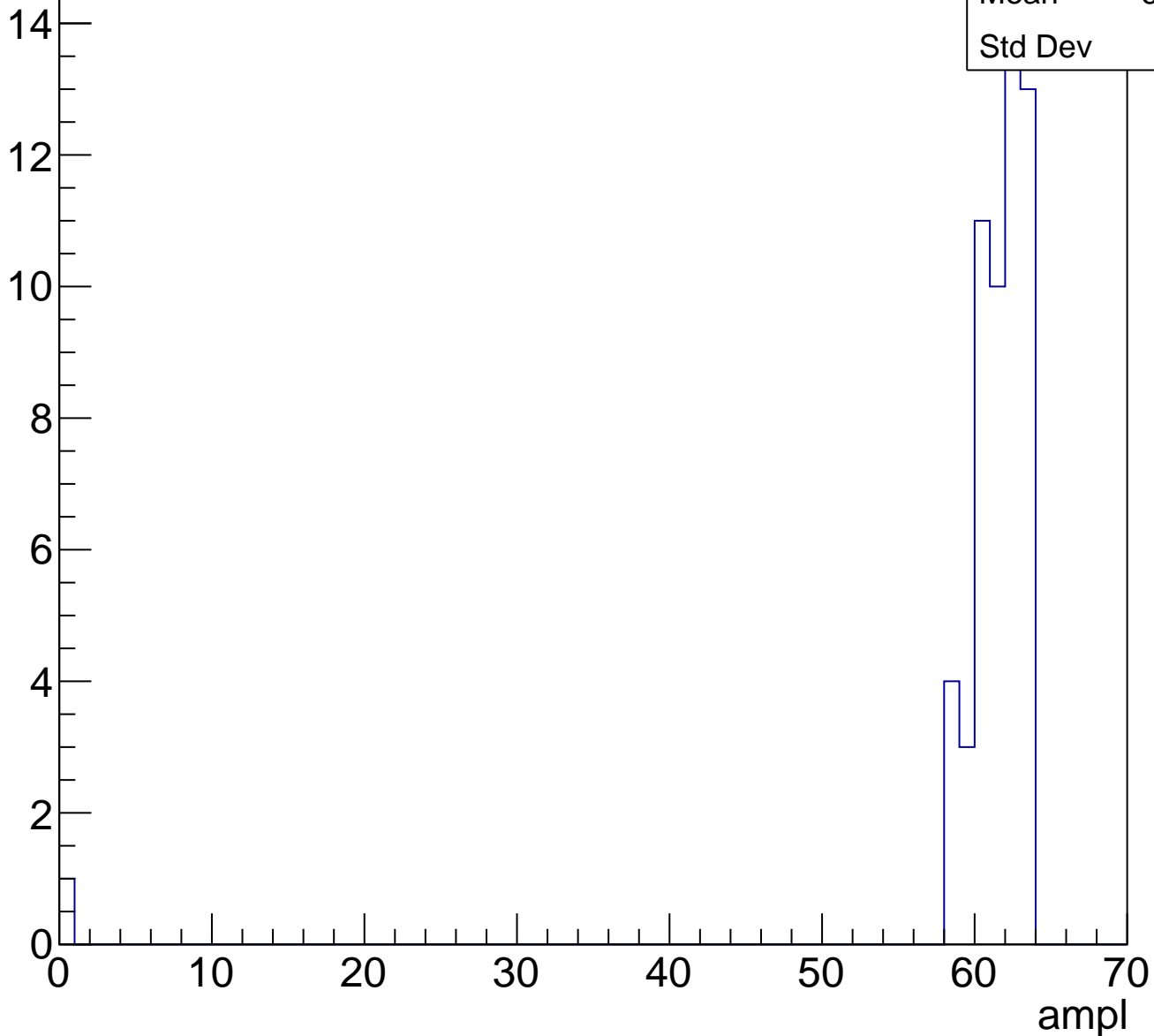


Entries	105
Mean	57.39
Std Dev	2.906

B0L101S, U25-ch45, adc5

calib_packv5_042523_0143.root, FC#1, port C1

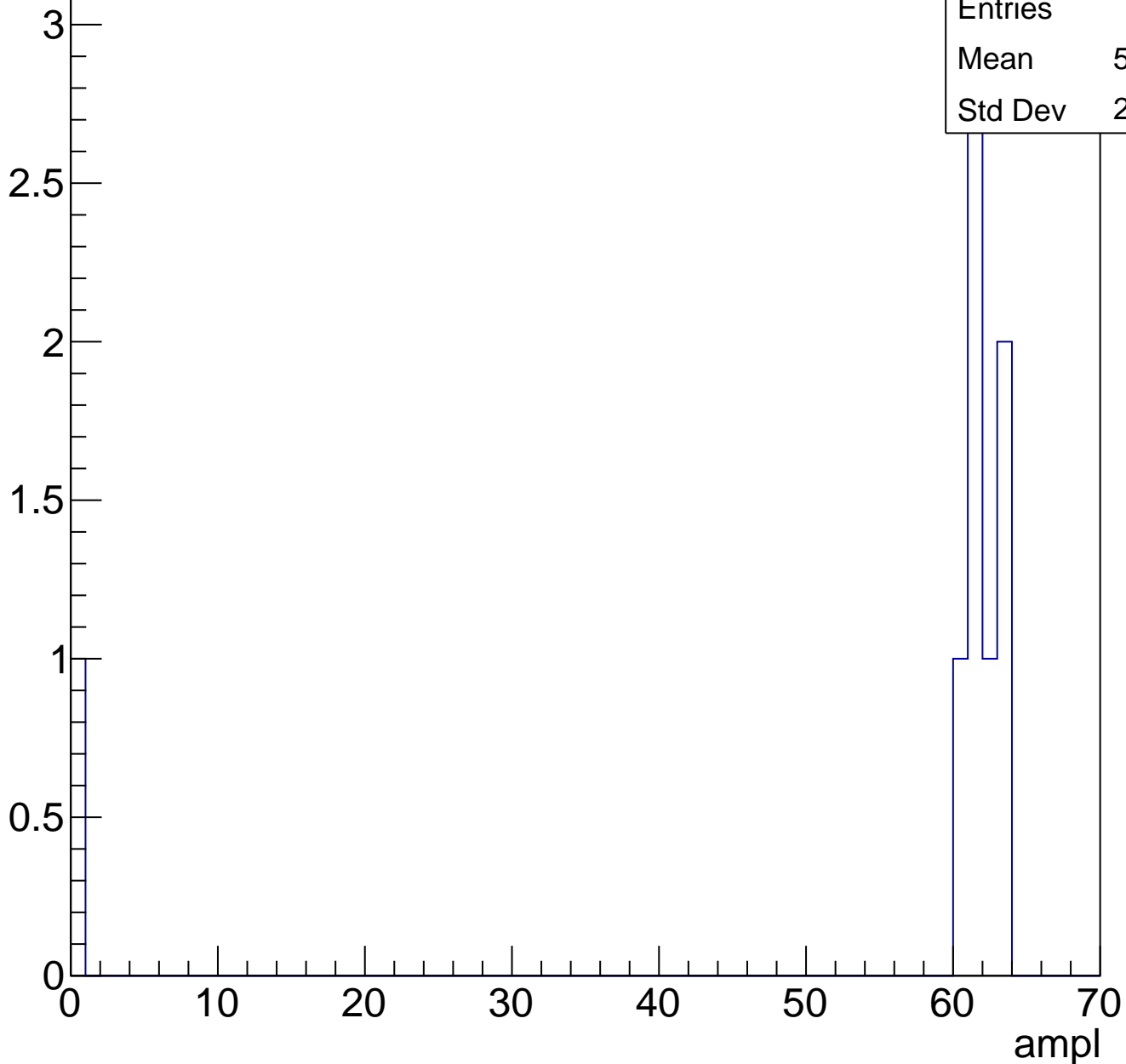
Entry



B0L101S, U25-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	8
Mean	53.88
Std Dev	20.39

B0L101S, U25-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



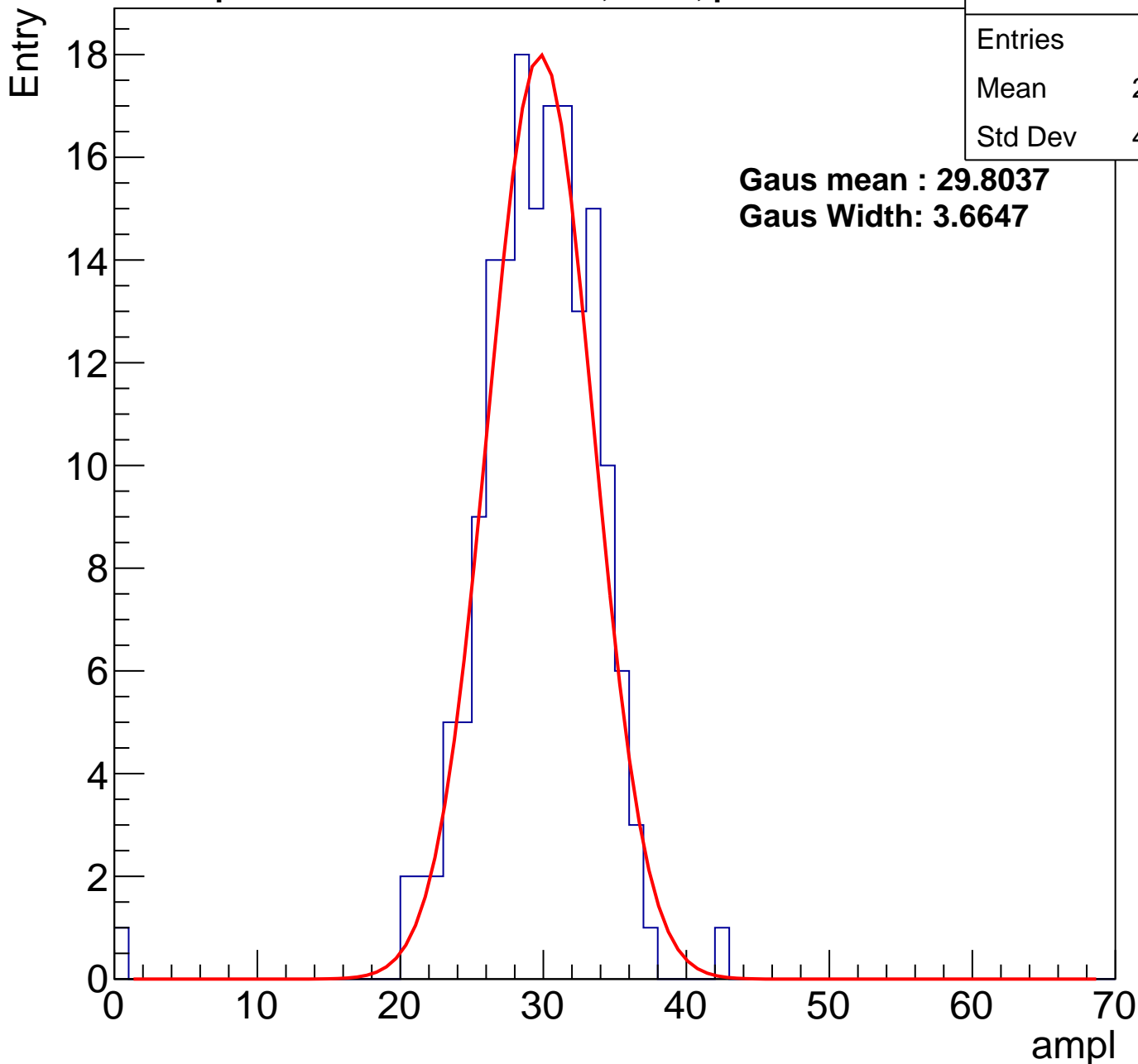
B0L101S, U25-ch46, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	29.13
Std Dev	4.314

Gaus mean : 29.8037

Gaus Width: 3.6647



B0L101S, U25-ch46, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	36.87
Std Dev	3.346

Gaus mean : 36.9957

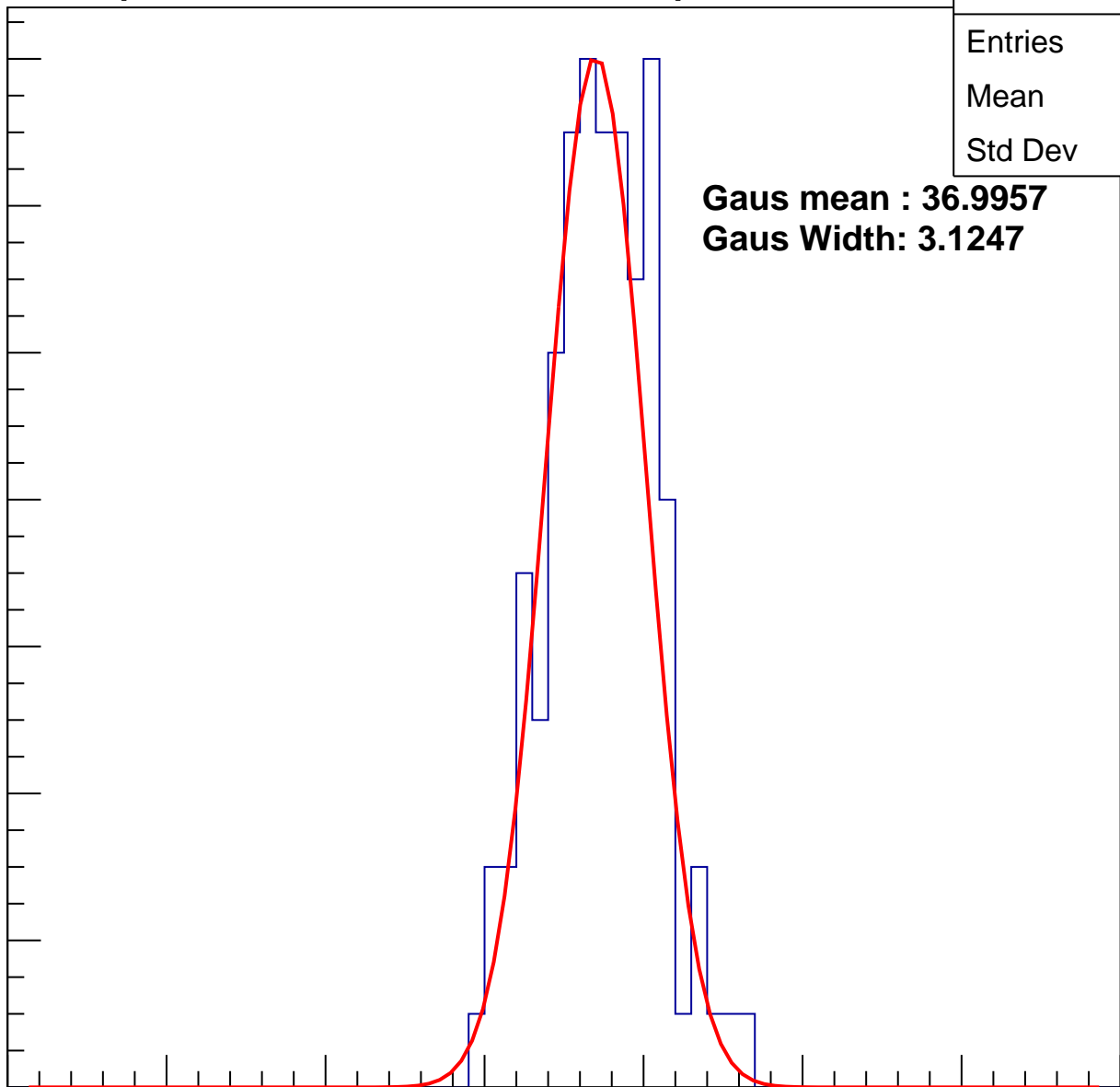
Gaus Width: 3.1247

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

146

Mean

43.16

Std Dev

3.488

Gaus mean : 43.7881

Gaus Width: 3.6427

0

10

20

30

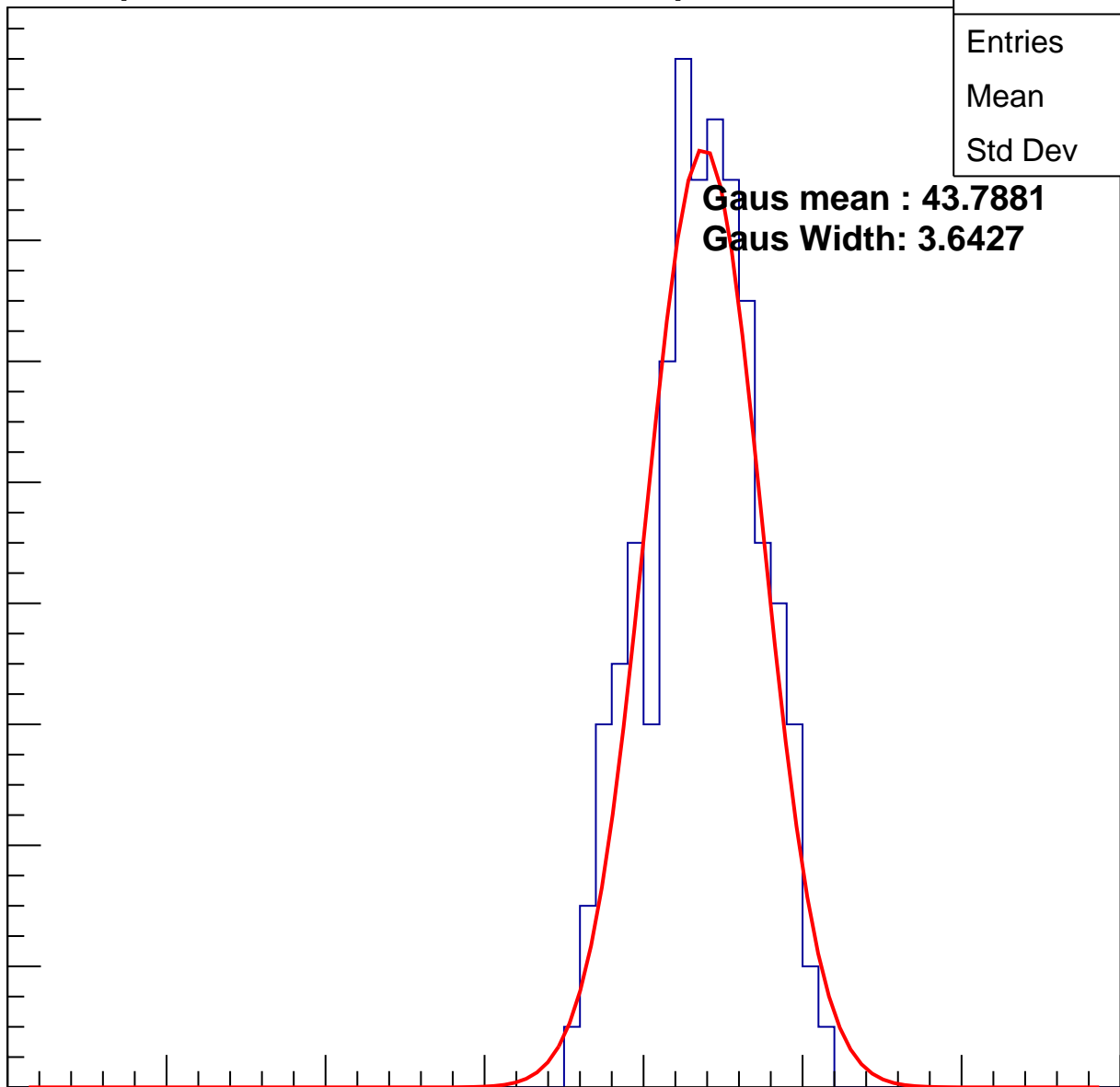
40

50

60

70

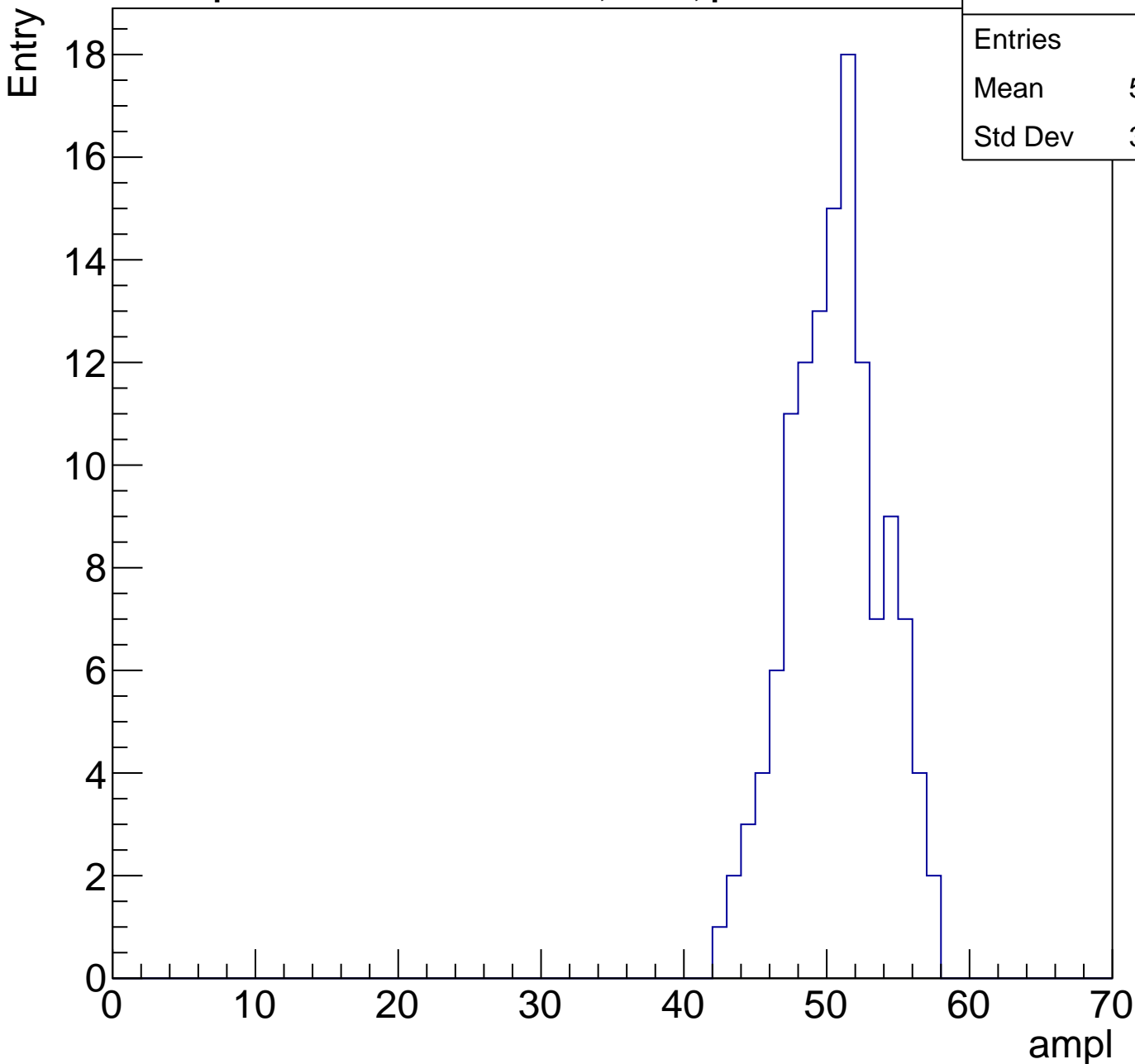
ampl



B0L101S, U25-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

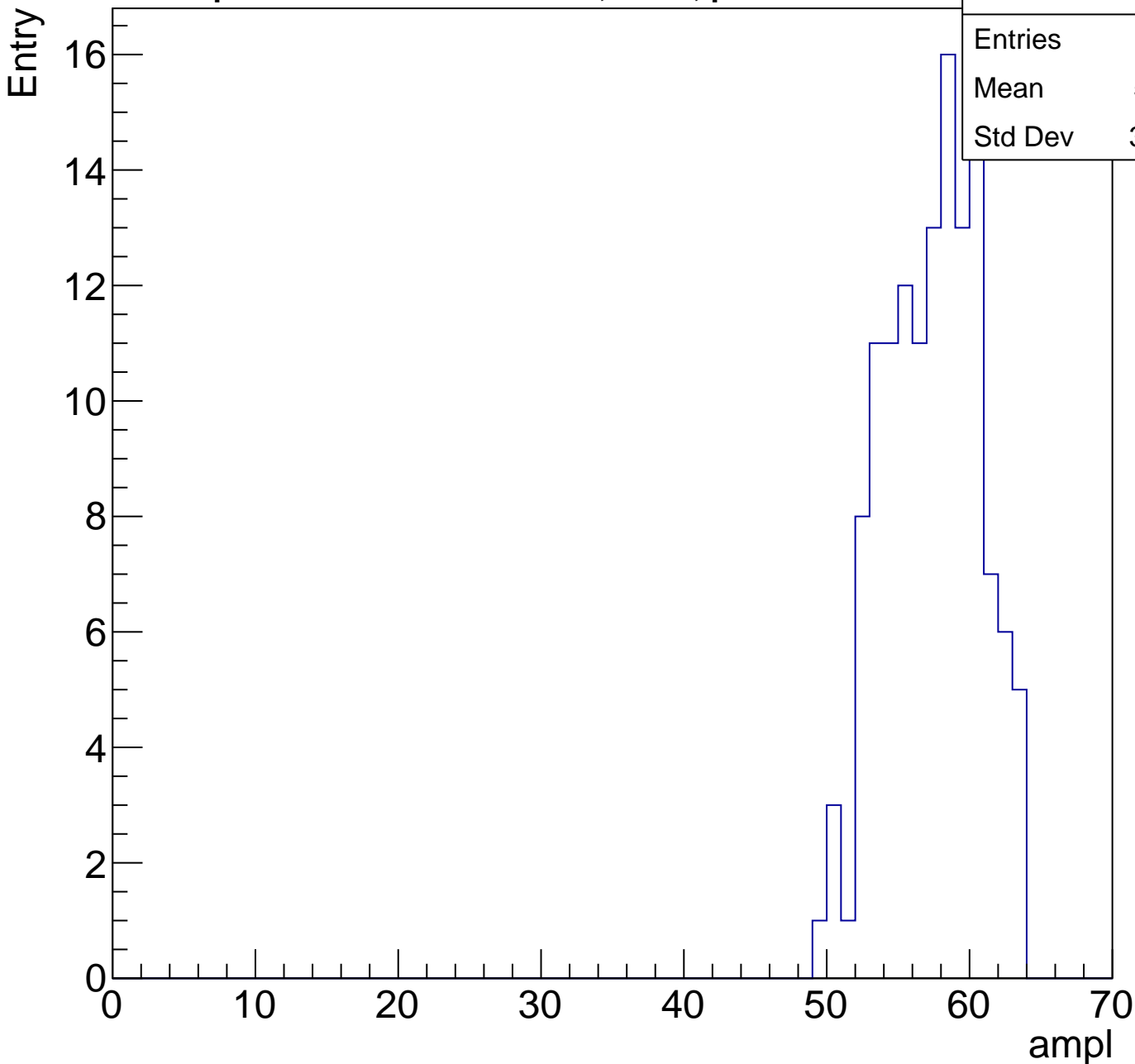
Entries	126
Mean	50.14
Std Dev	3.229



B0L101S, U25-ch46, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	56.91
Std Dev	3.273



B0L101S, U25-ch46, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	66
Mean	58.15
Std Dev	10.59

ampl

0

10

20

30

40

50

60

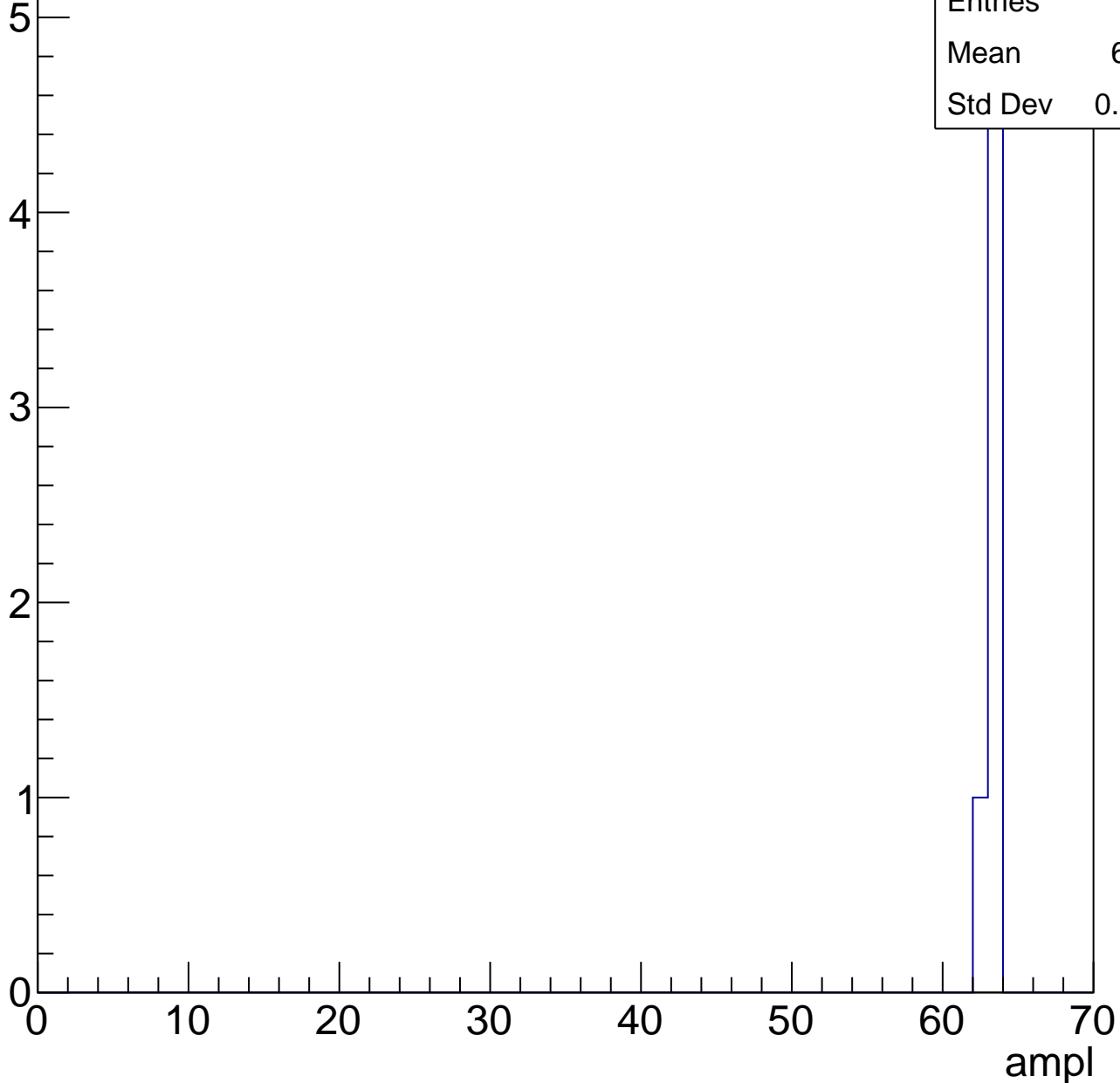
70

B0L101S, U25-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

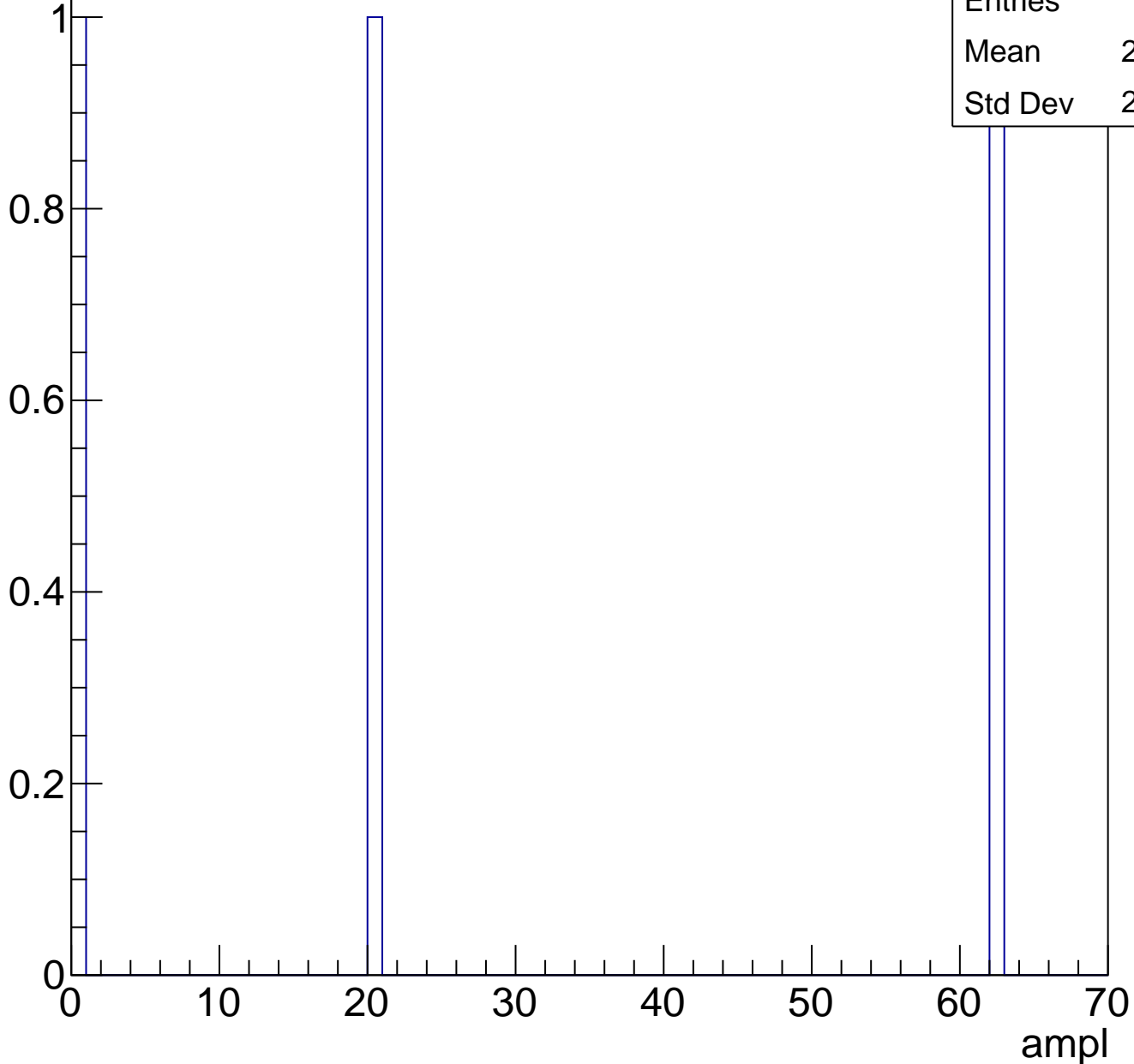
Entries	6
Mean	62.83
Std Dev	0.3727



B0L101S, U25-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	27.33
Std Dev	25.84

B0L101S, U25-ch47, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

172

Mean

31.36

Std Dev

4.568

Gaus mean : 31.9005

Gaus Width: 3.8370

0

10

20

30

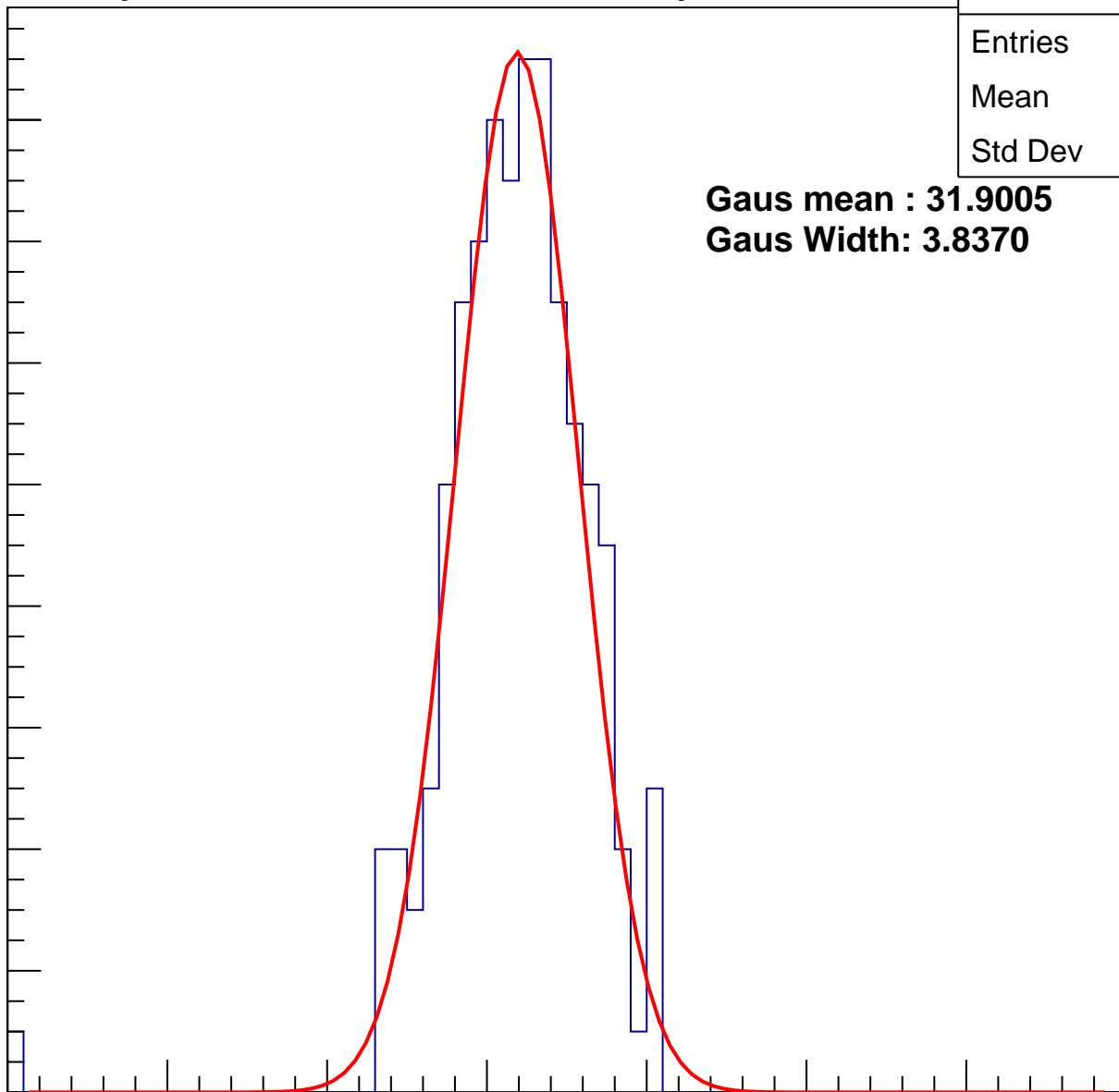
40

50

60

70

ampl



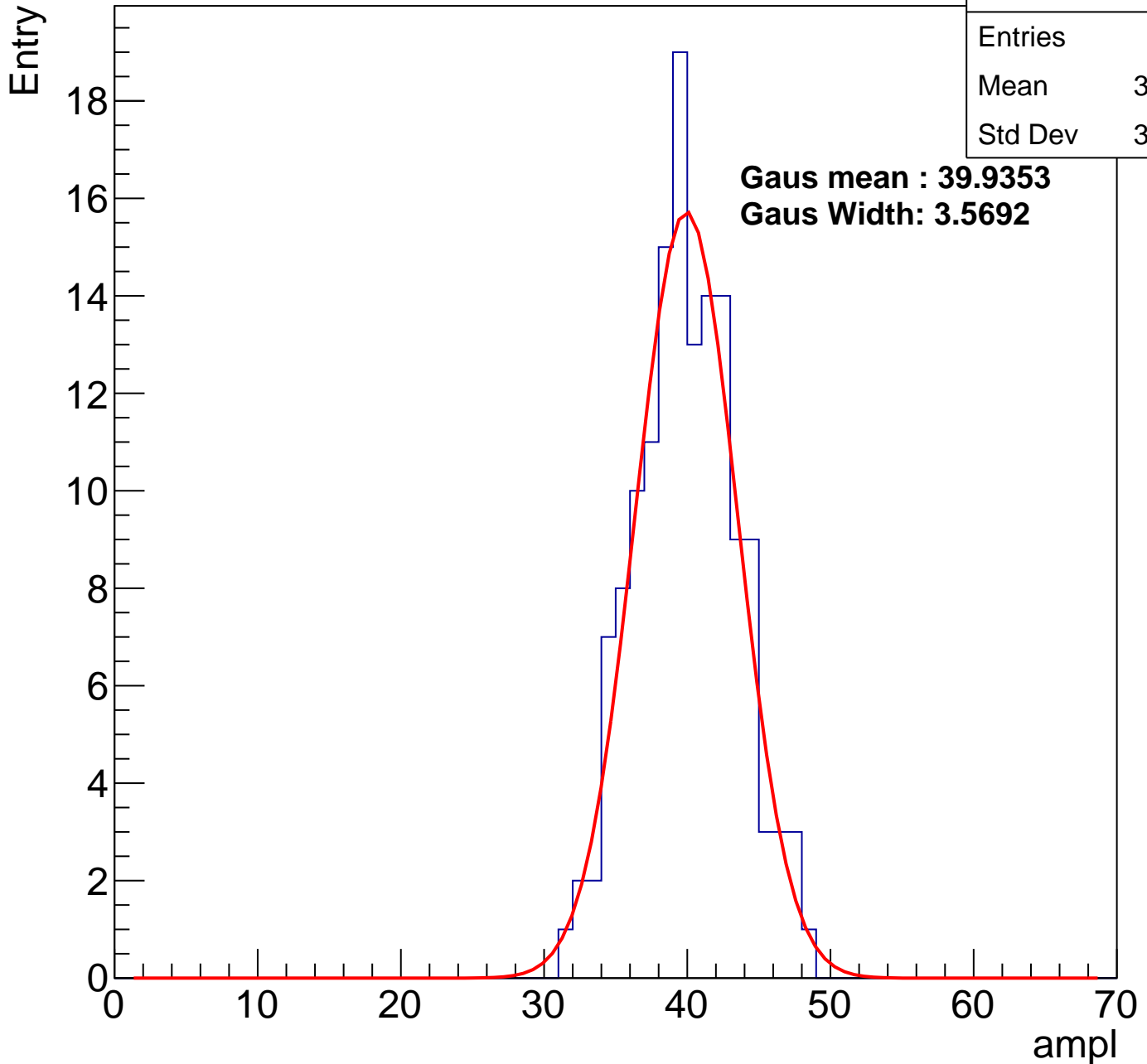
B0L101S, U25-ch47, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	39.47
Std Dev	3.482

Gaus mean : 39.9353

Gaus Width: 3.5692



B0L101S, U25-ch47, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

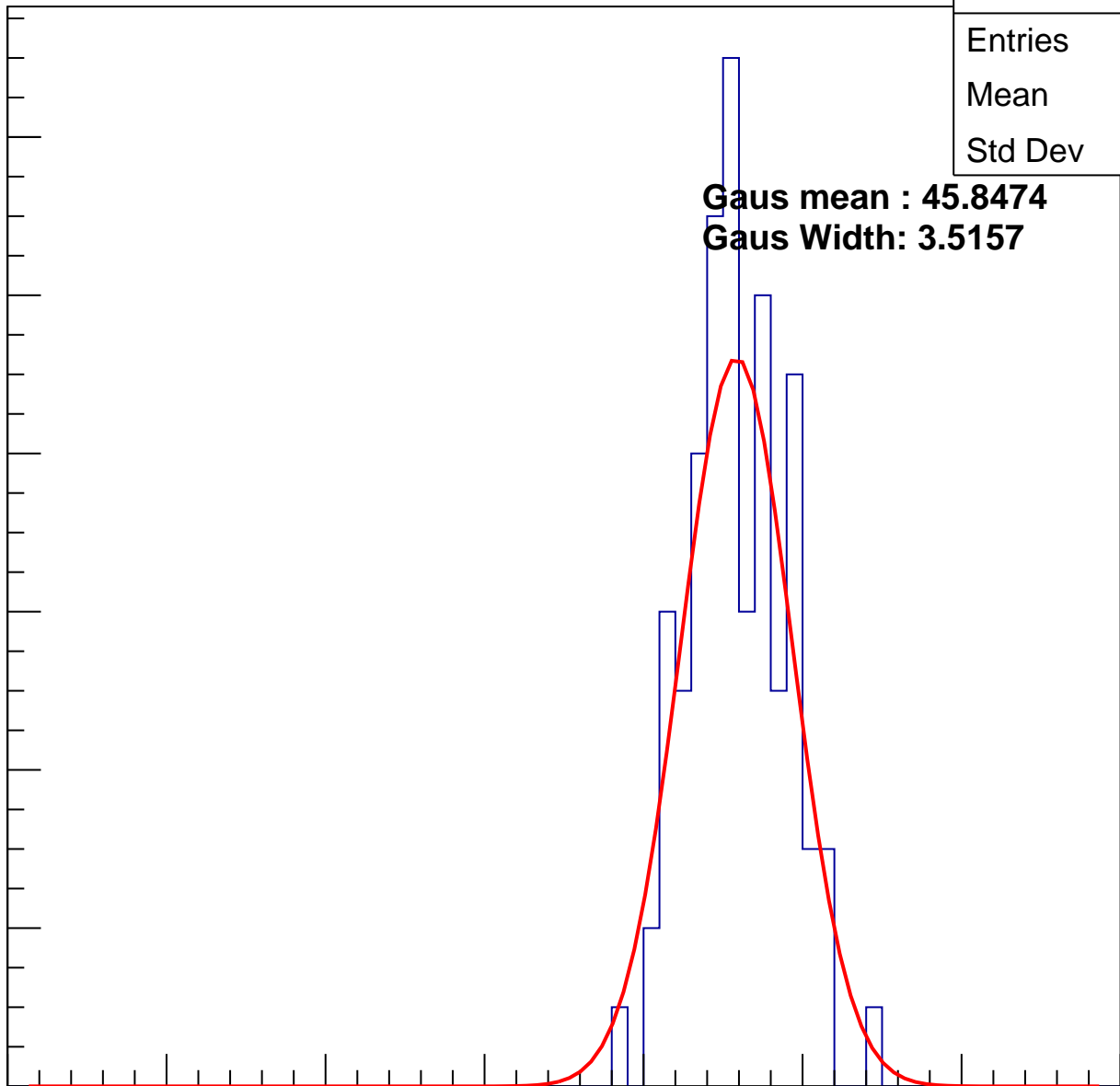
Entries	83
Mean	45.43
Std Dev	3.039

Gaus mean : 45.8474

Gaus Width: 3.5157

0 10 20 30 40 50 60 70

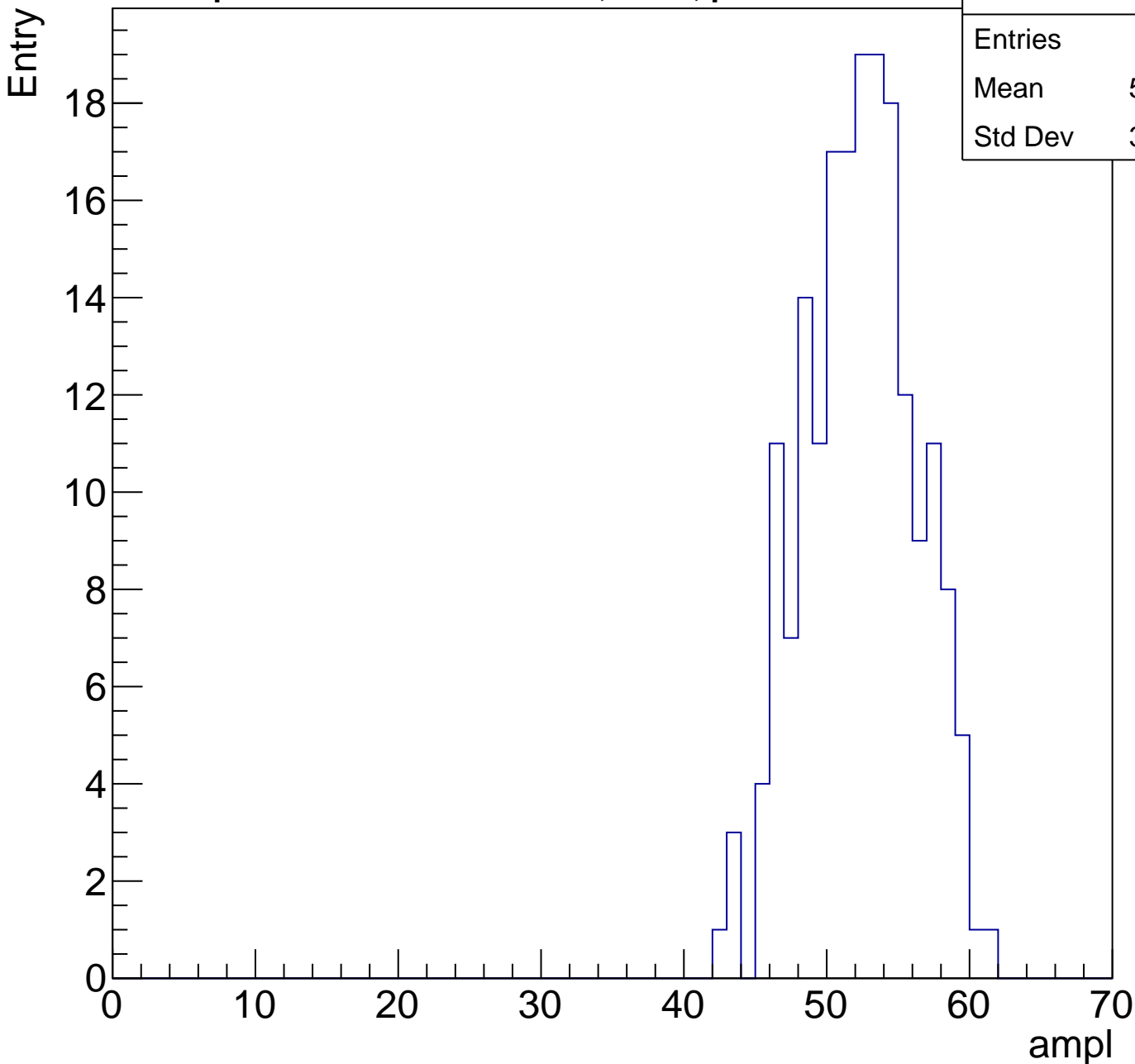
ampl



B0L101S, U25-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

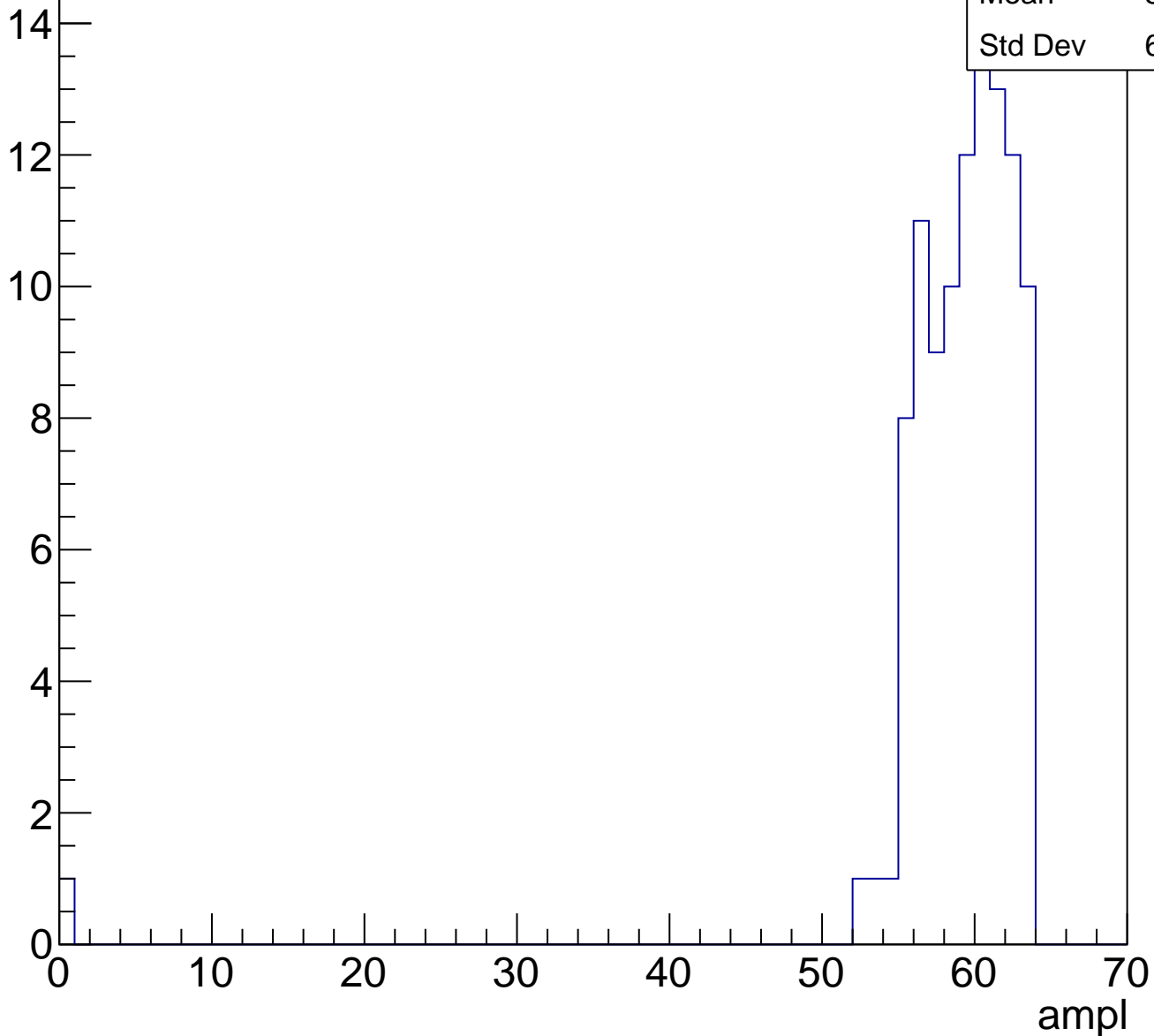
Entries	188
Mean	51.87
Std Dev	3.867



B0L101S, U25-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

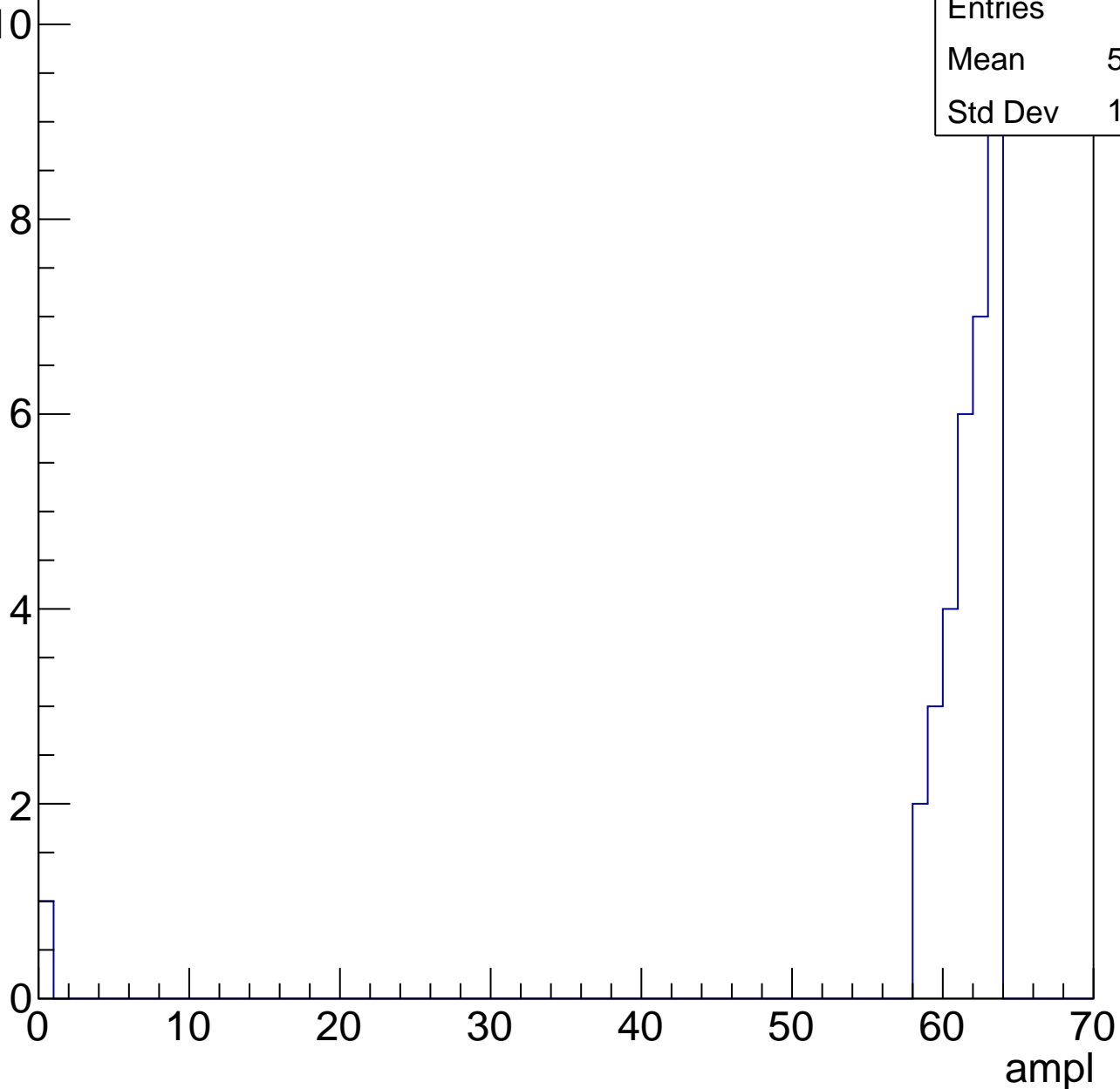


B0L101S, U25-ch47, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	33
Mean	59.48
Std Dev	10.63



B0L101S, U25-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch48, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	28.67
Std Dev	4.403

Gaus mean : 29.1178

Gaus Width: 3.7828

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

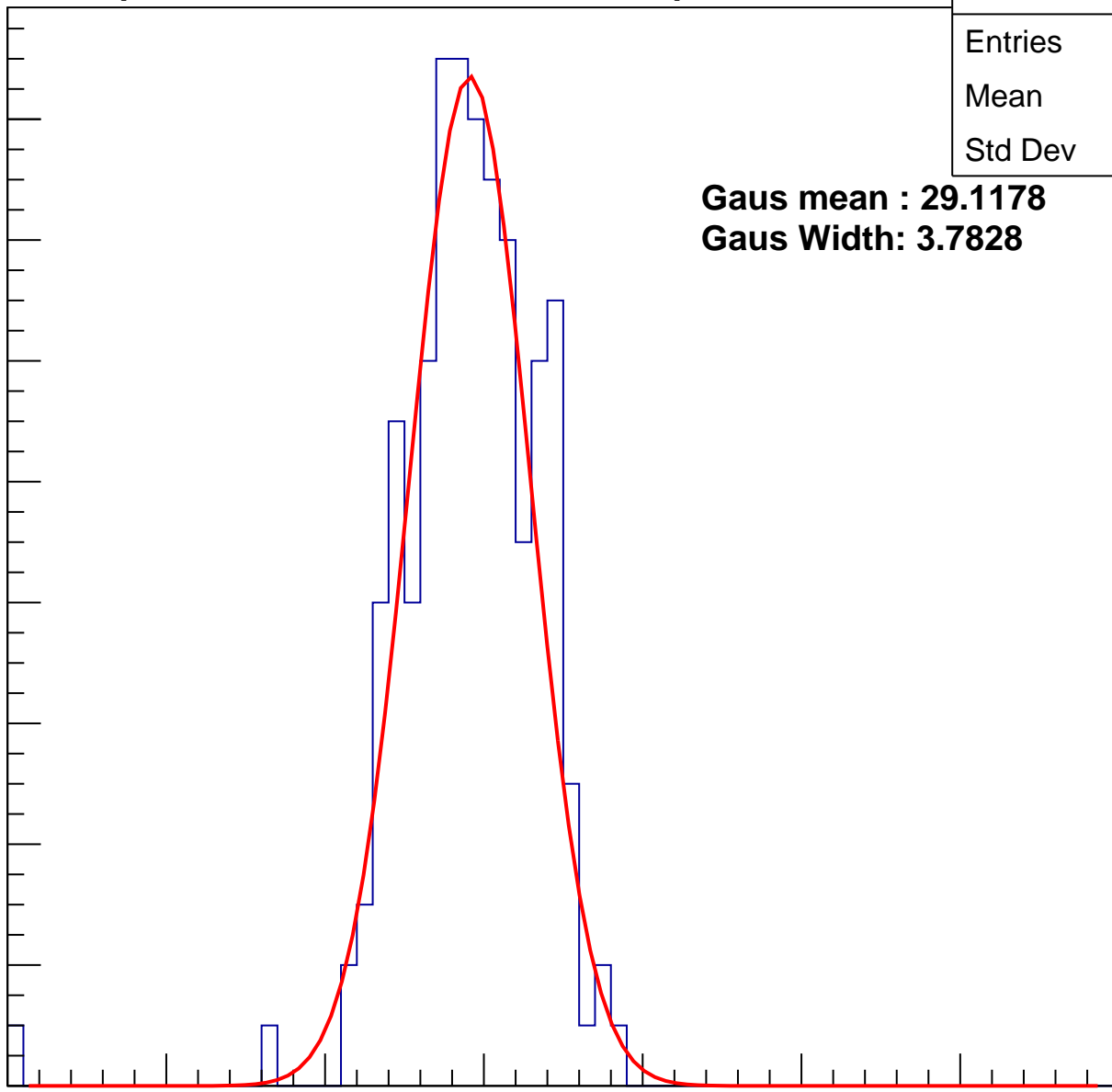
40

50

60

70

ampl



B0L101S, U25-ch48, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	35.65
Std Dev	3.226

Gaus mean : 36.2106

Gaus Width: 3.3937

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

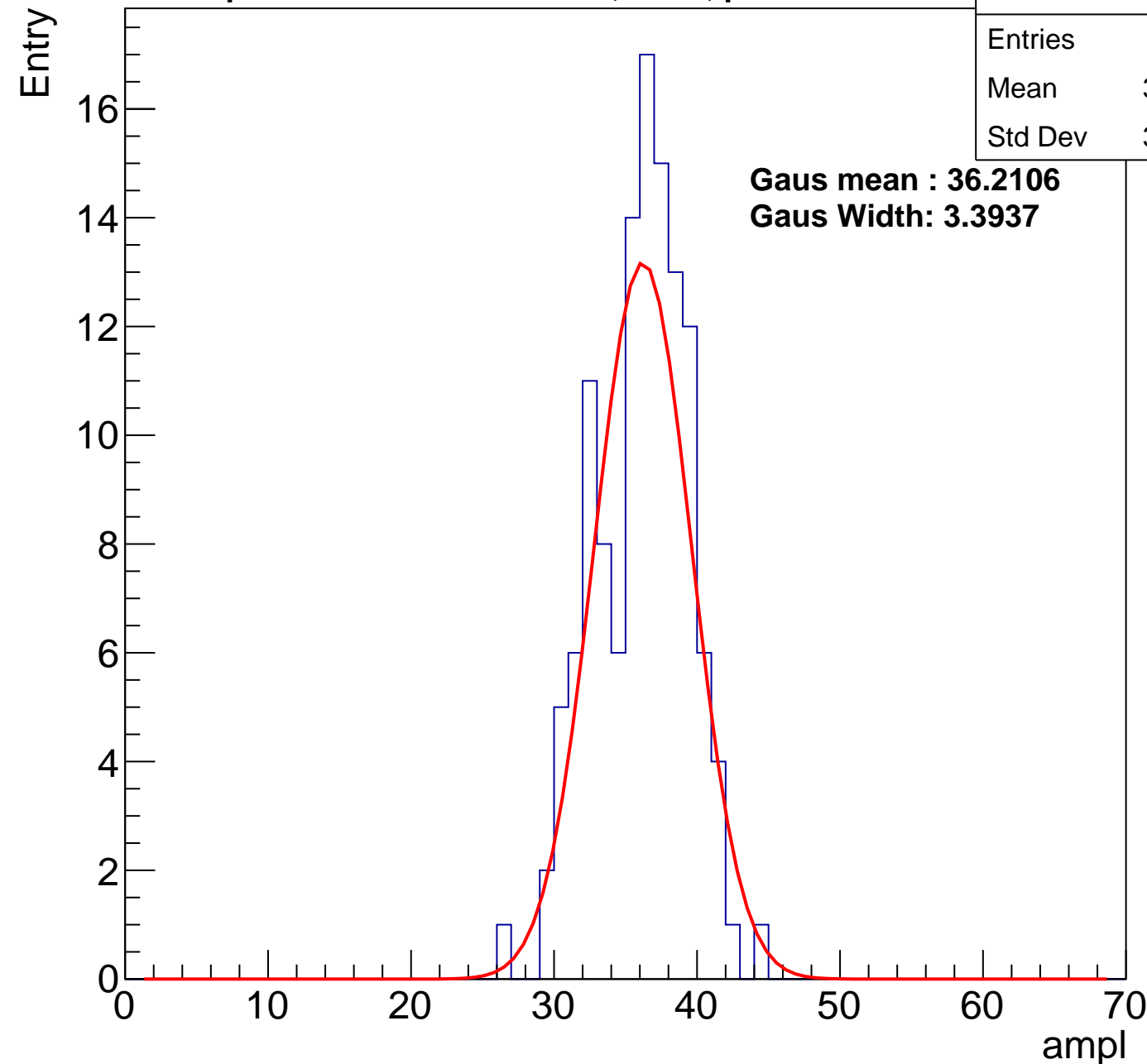
40

50

60

70

ampl



B0L101S, U25-ch48, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

152

Mean

43.21

Std Dev

3.616

Gaus mean : 43.3824

Gaus Width: 3.4233

0

B0L101S, U25-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	49.9
Std Dev	3.577

Entry

16

14

12

10

8

6

4

2

0

0

10

20

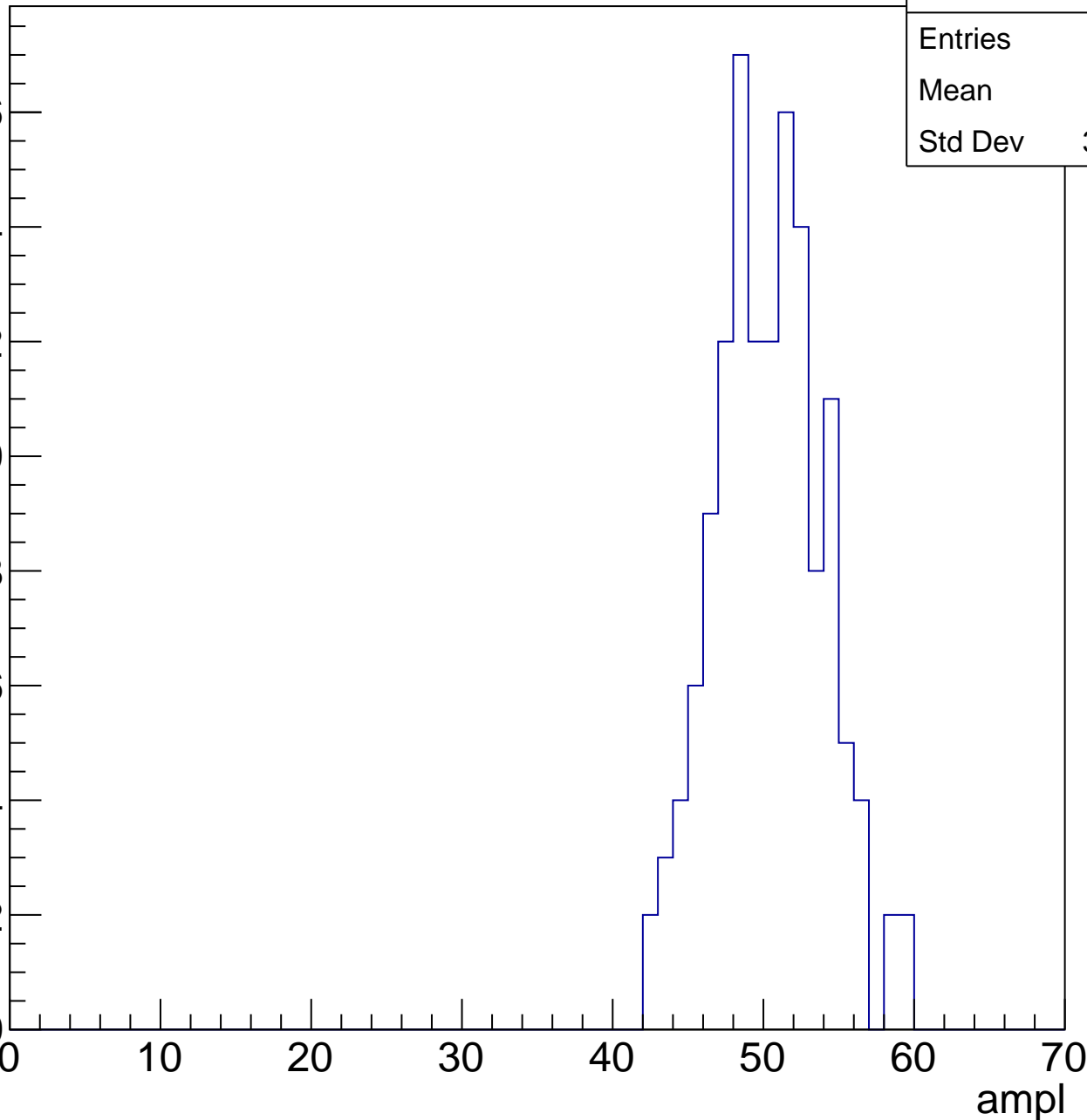
30

40

50

60

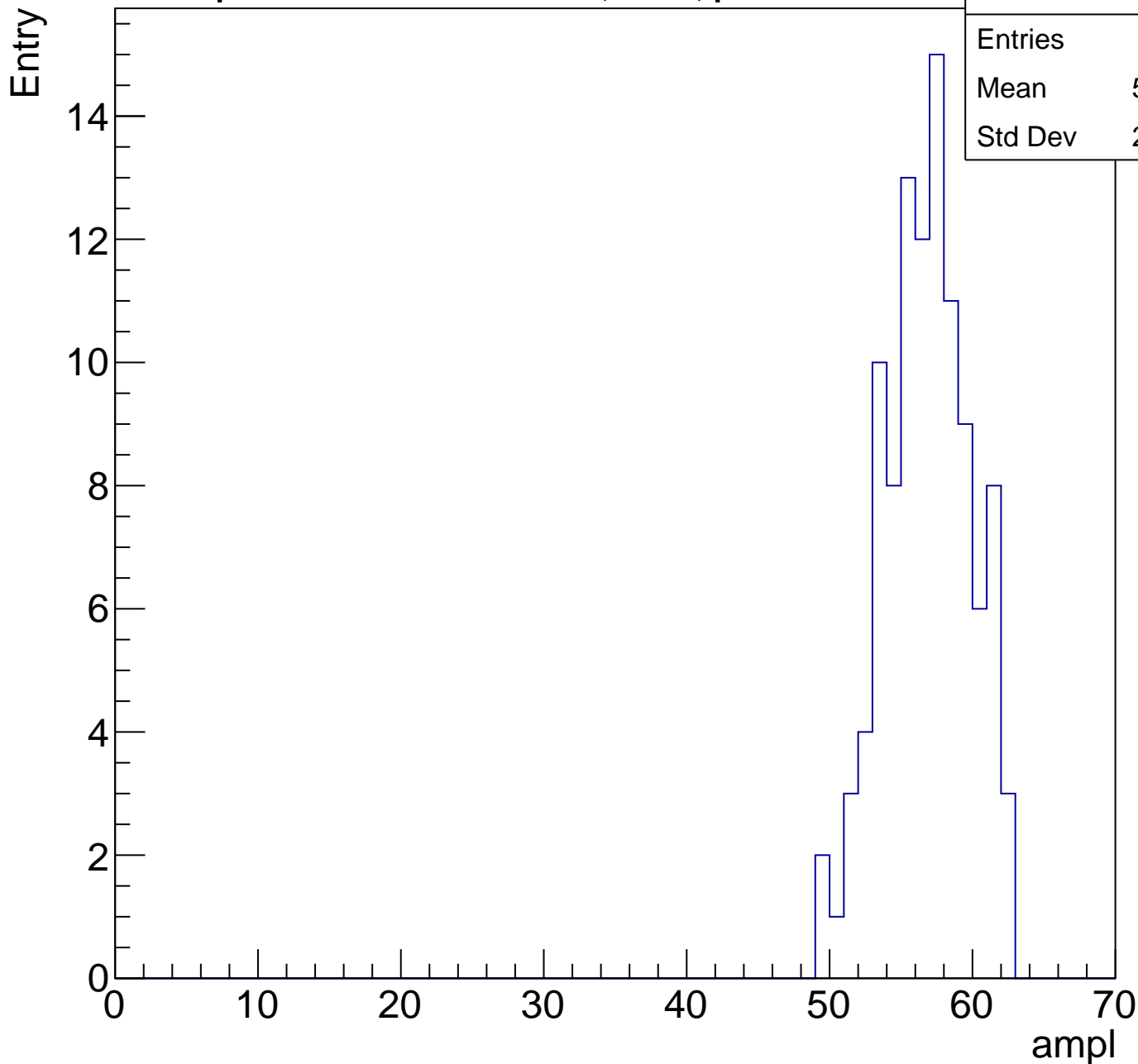
ampl



B0L101S, U25-ch48, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	56.34
Std Dev	2.992



B0L101S, U25-ch48, adc5

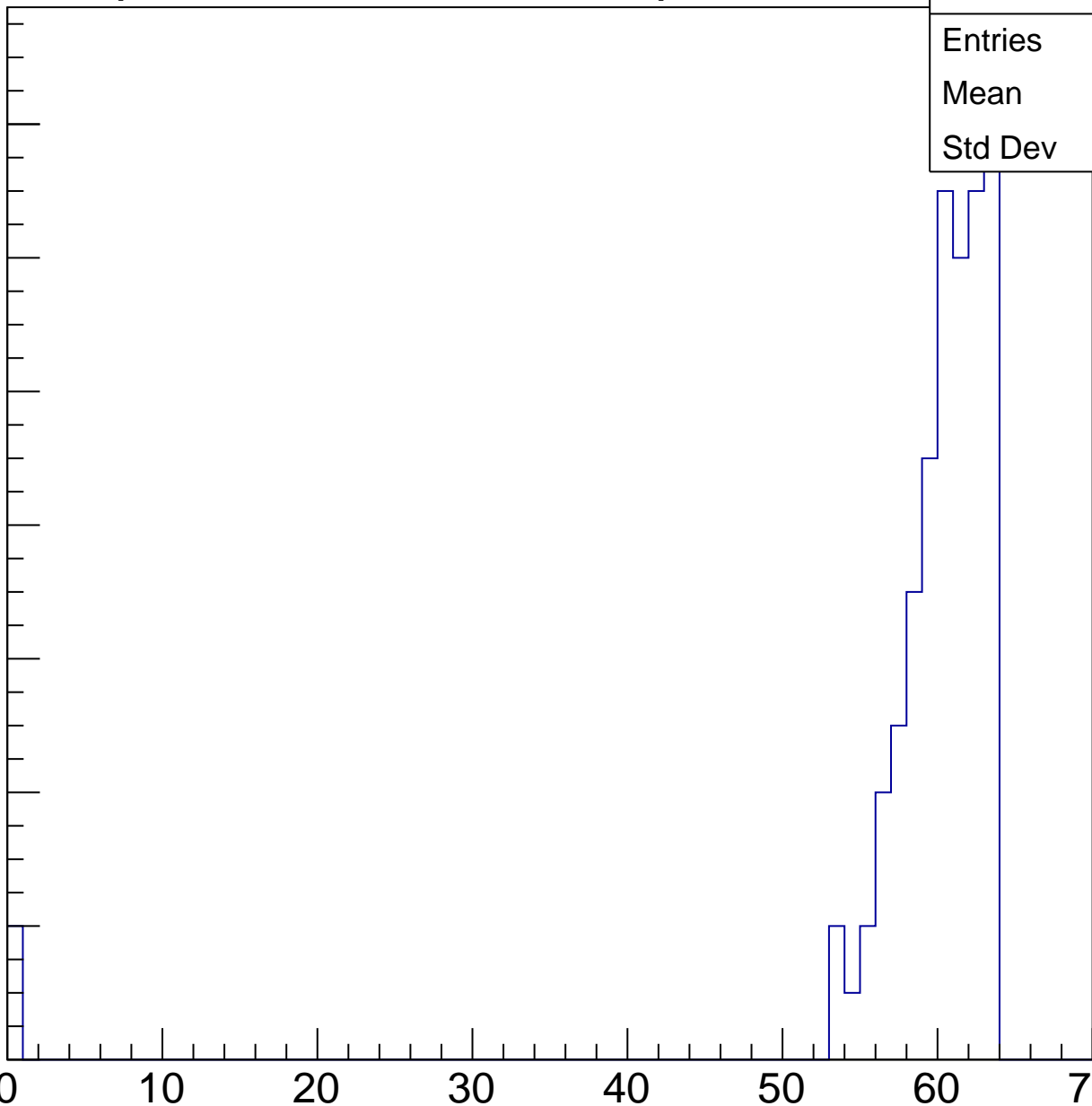
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	58.58
Std Dev	9.429

ampl

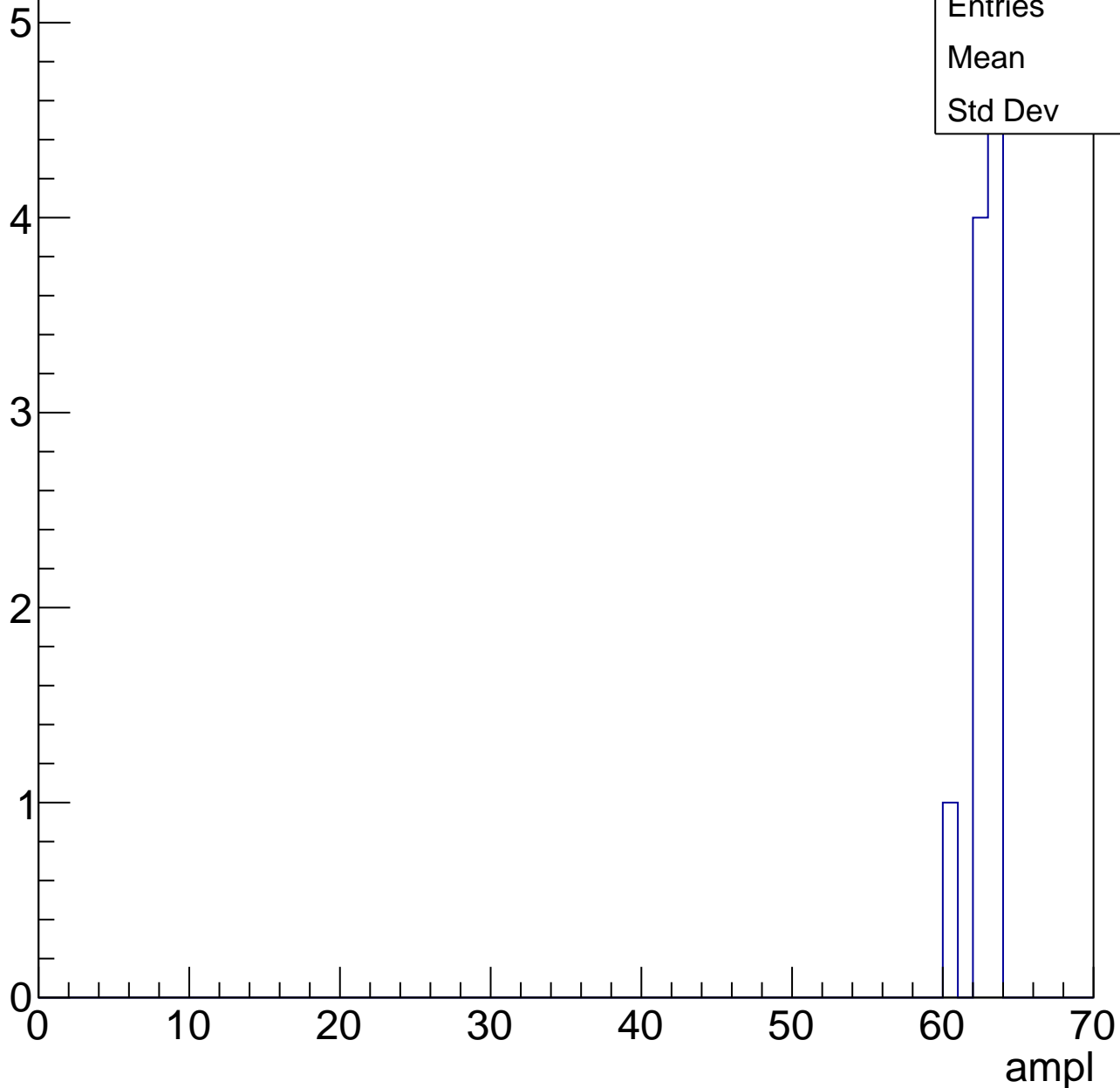


B0L101S, U25-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.3
Std Dev	0.9



B0L101S, U25-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U25-ch49, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	30.72
Std Dev	4.319

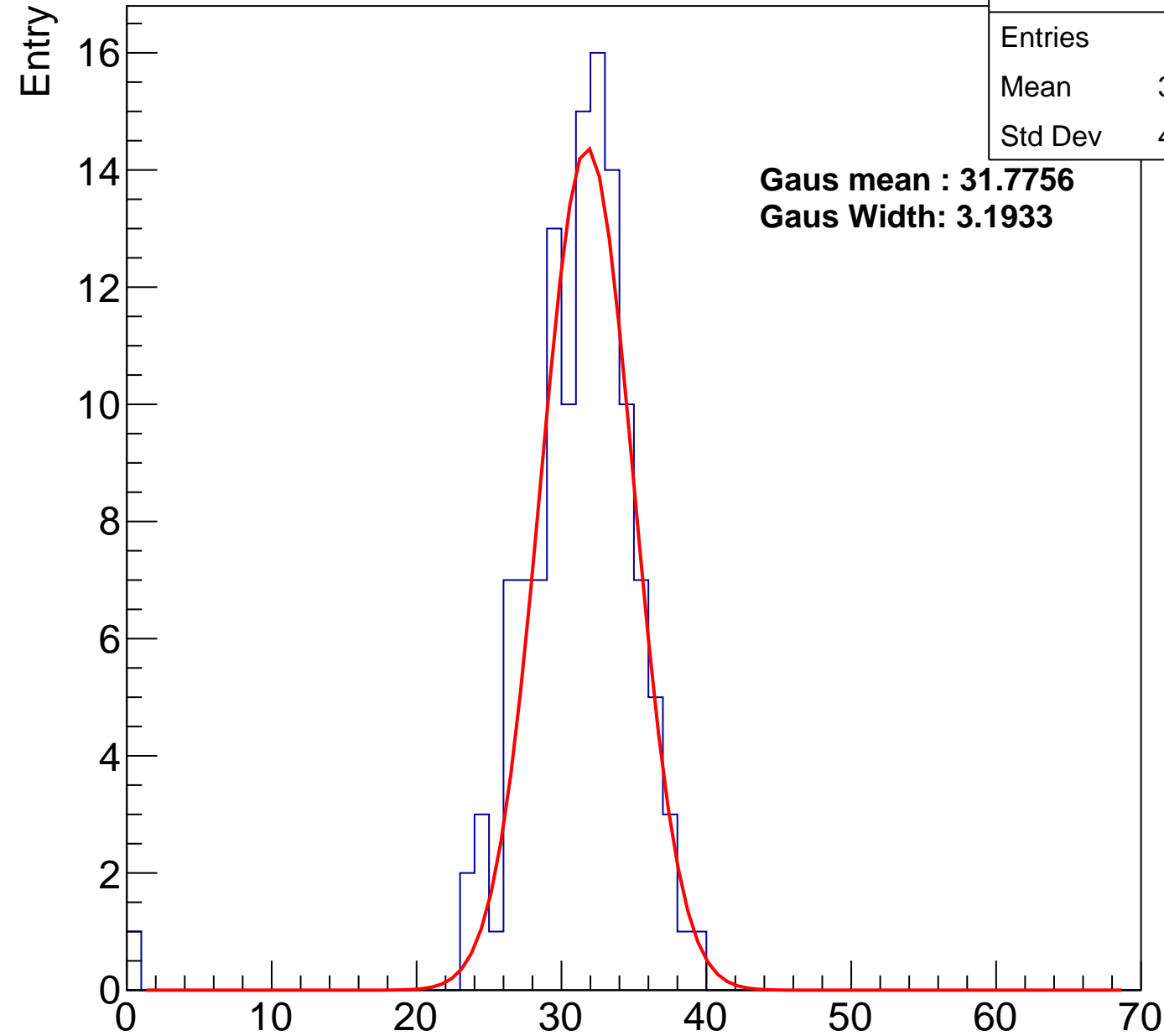
Gaus mean : 31.7756

Gaus Width: 3.1933

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch49, adc1

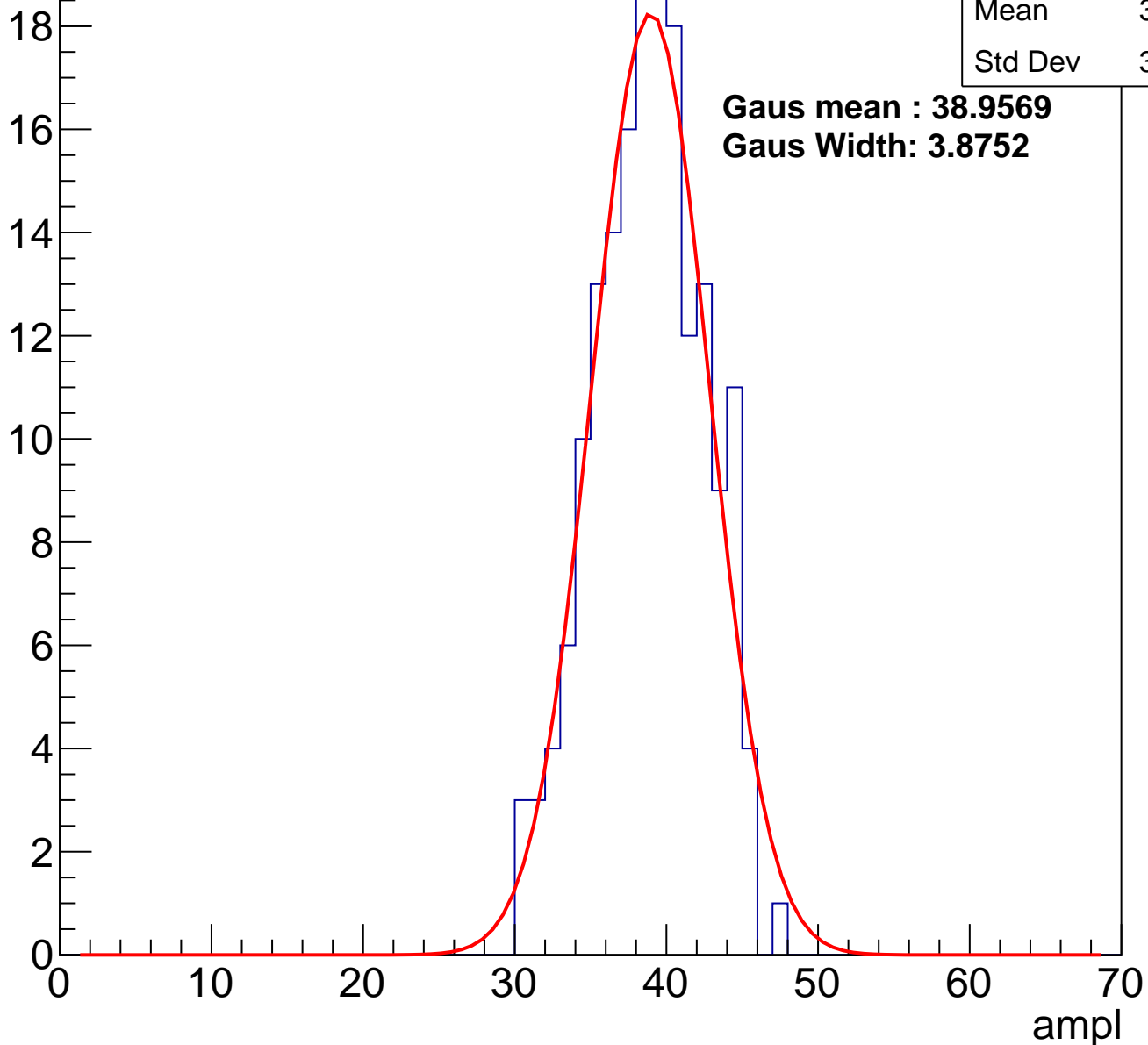
calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	38.39
Std Dev	3.593

Gaus mean : 38.9569

Gaus Width: 3.8752

Entry



B0L101S, U25-ch49, adc2

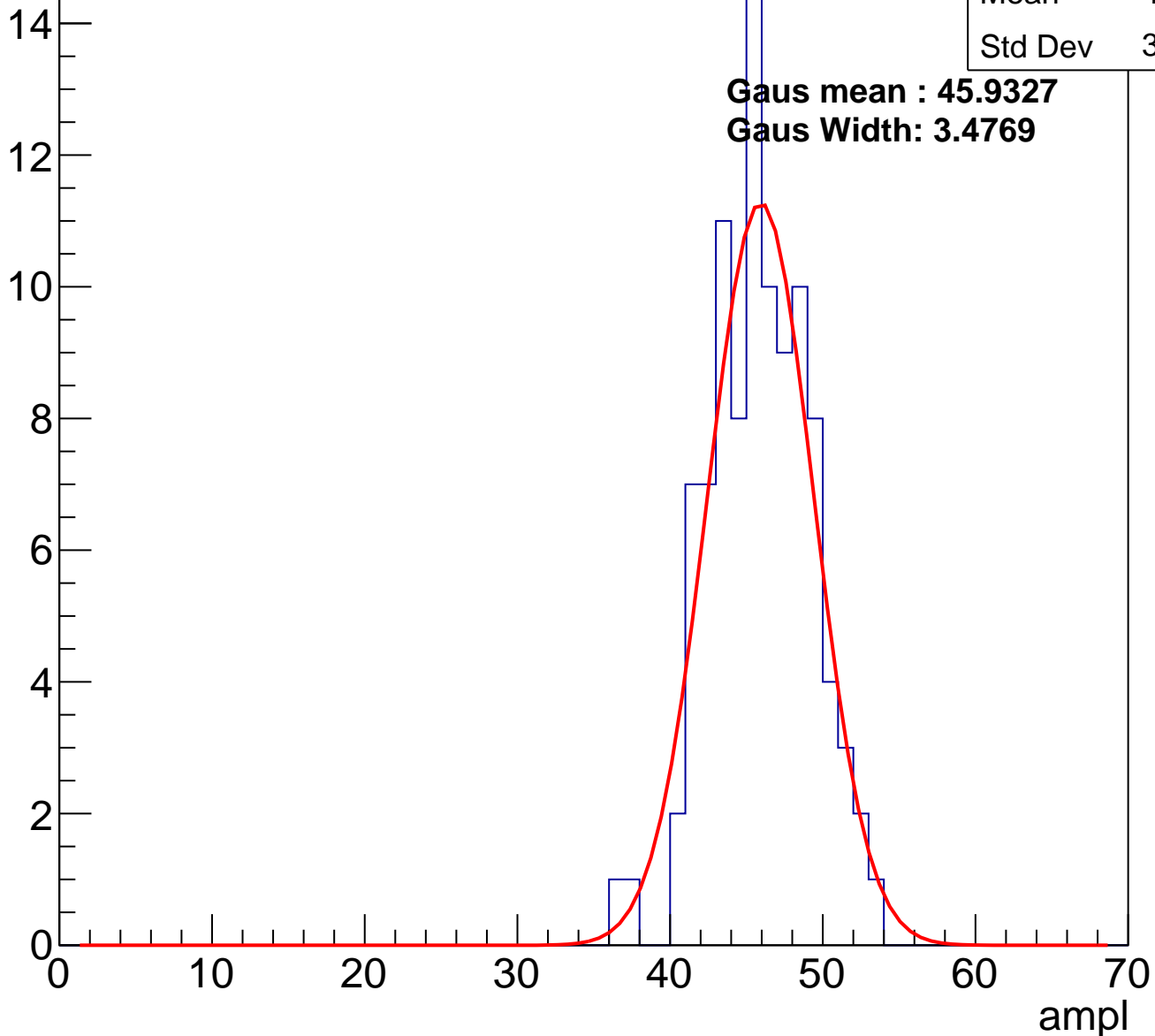
calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	45.44
Std Dev	3.236

Gaus mean : 45.9327

Gaus Width: 3.4769

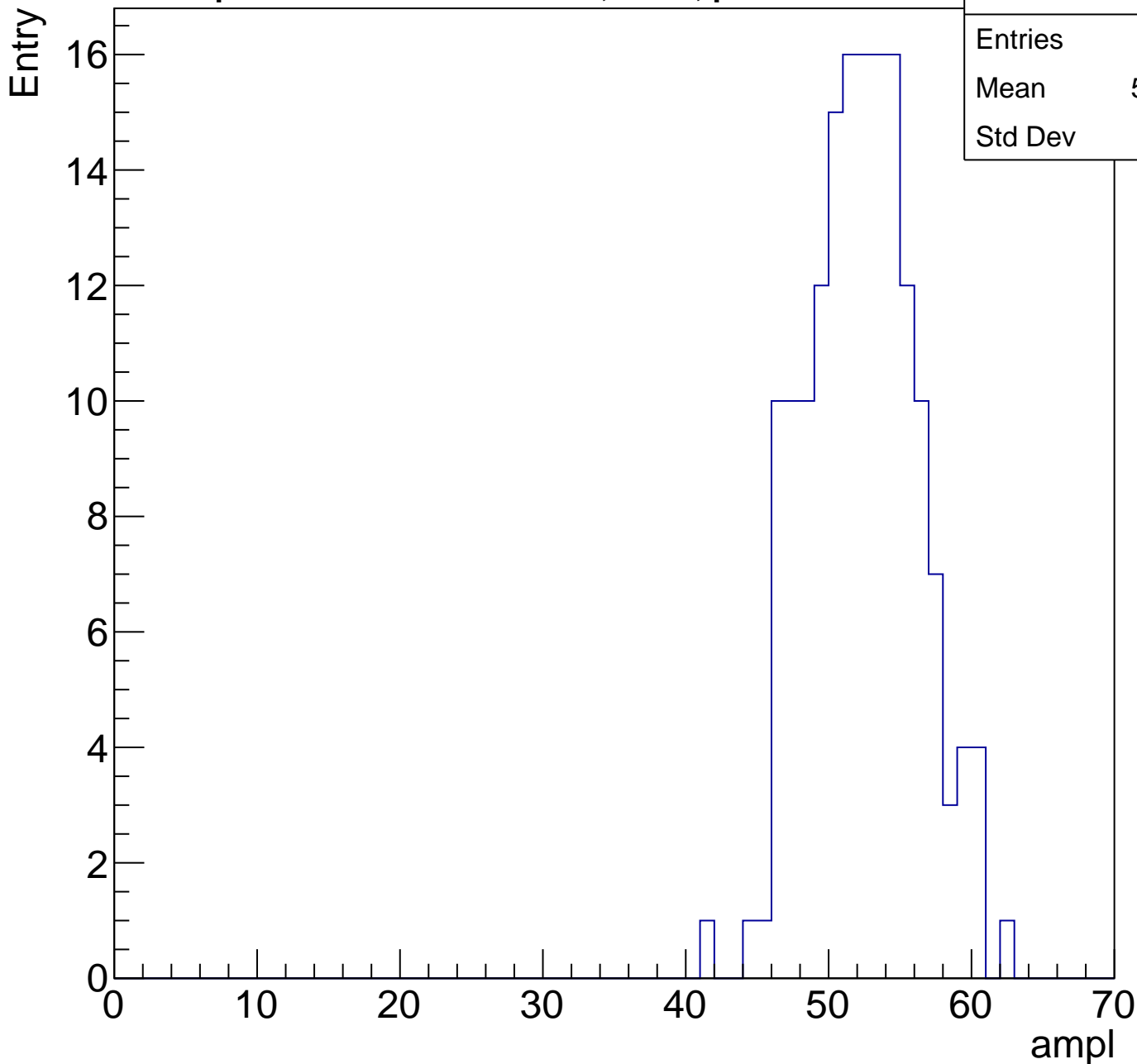
Entry



B0L101S, U25-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	51.93
Std Dev	3.79



B0L101S, U25-ch49, adc4

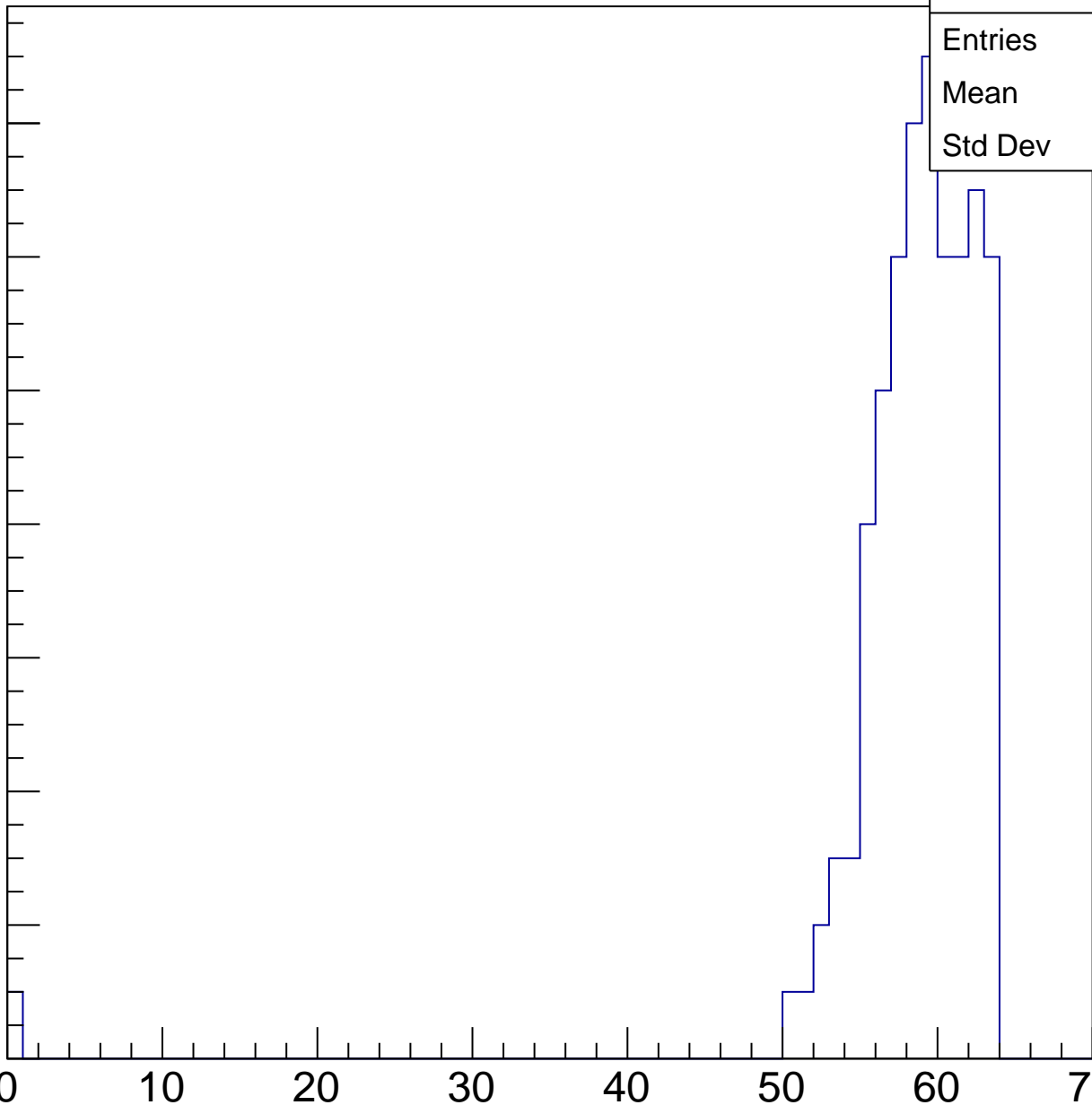
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	58.16
Std Dev	6.13

ampl

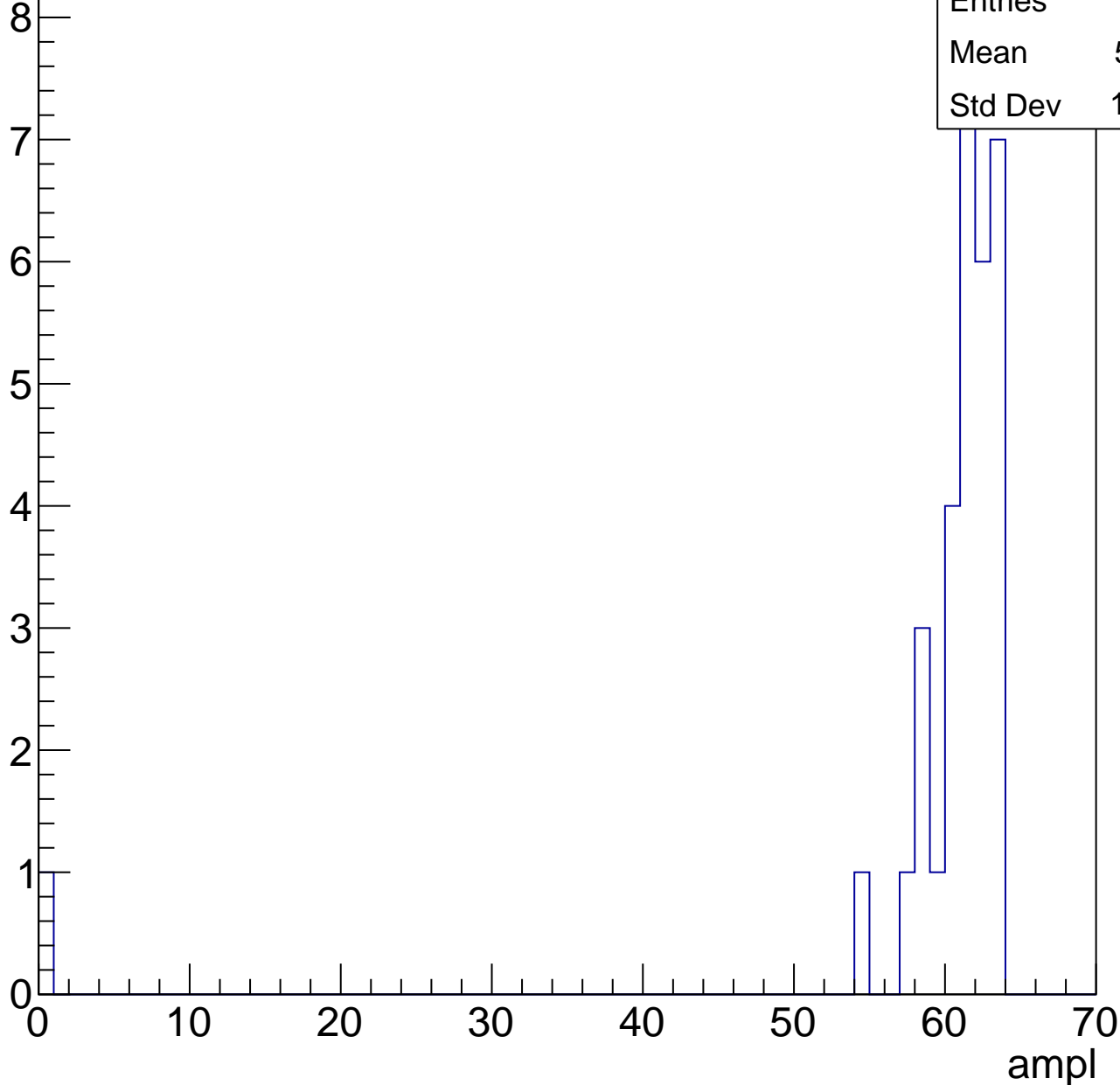


B0L101S, U25-ch49, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	32
Mean	58.91
Std Dev	10.77



B0L101S, U25-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch50, adc0

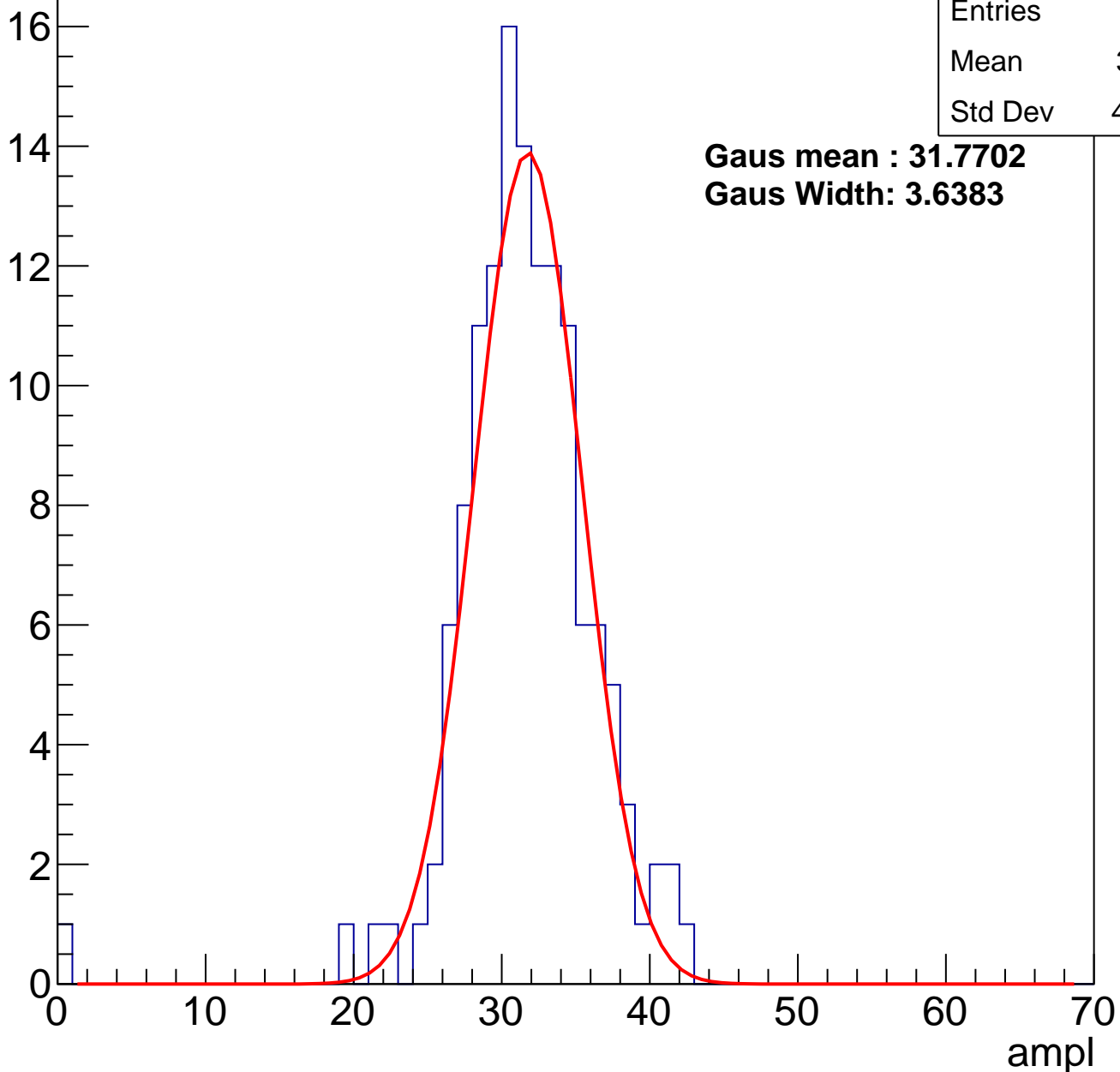
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	31.11
Std Dev	4.812

Gaus mean : 31.7702

Gaus Width: 3.6383

Entry



B0L101S, U25-ch50, adc1

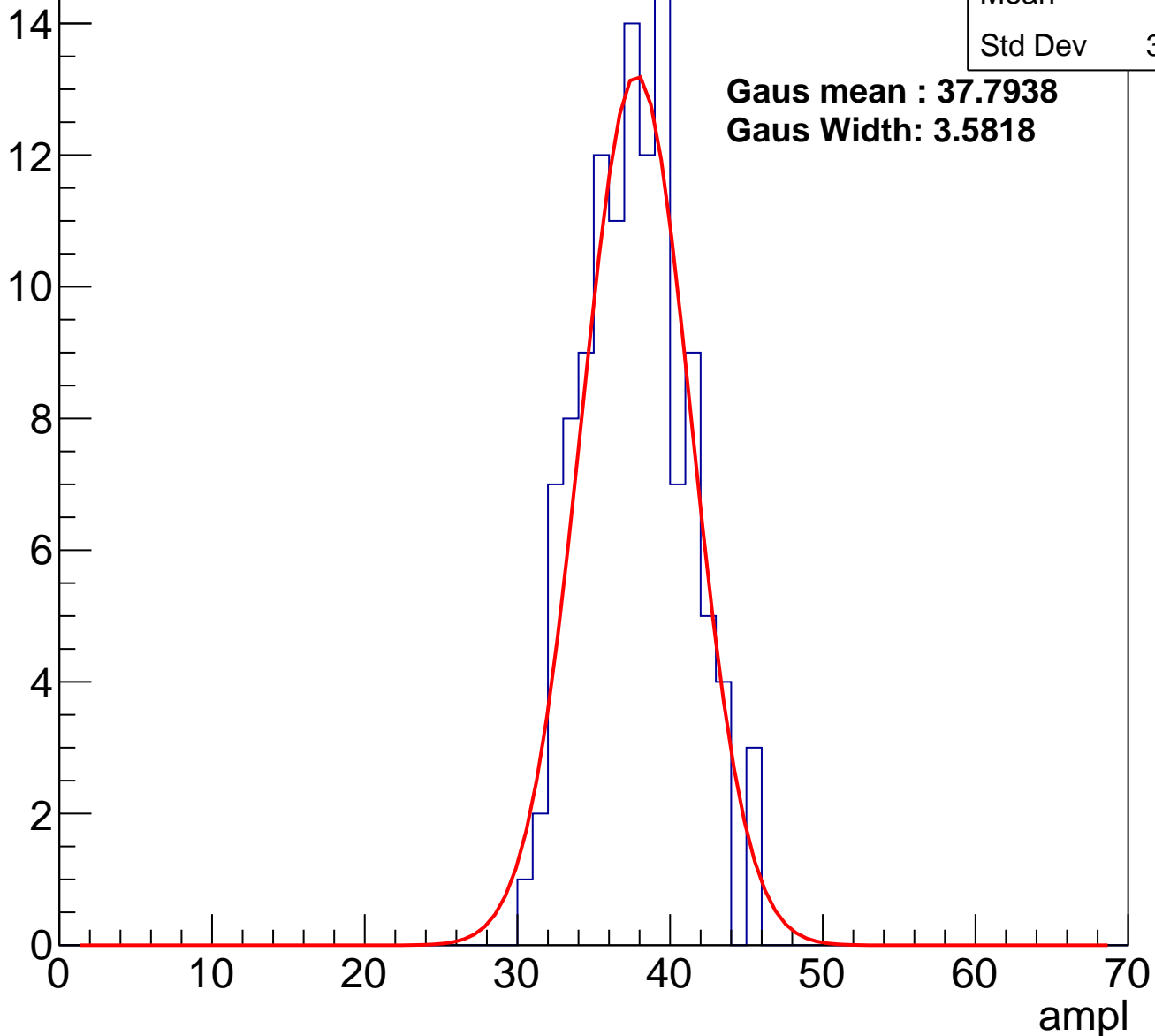
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	37.2
Std Dev	3.304

Gaus mean : 37.7938

Gaus Width: 3.5818

Entry



B0L101S, U25-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

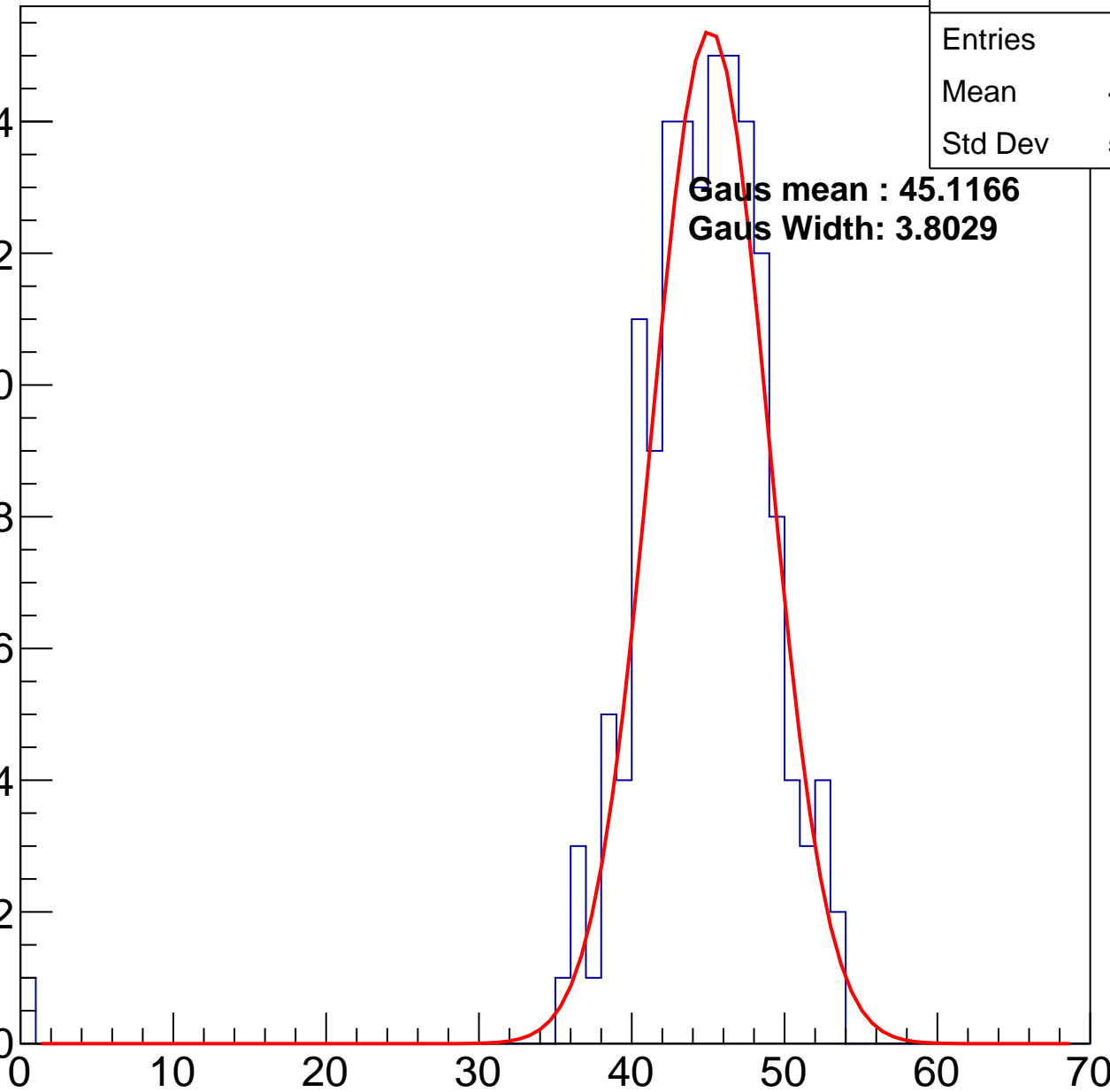
14
12
10
8
6
4
2
0

Entries	153
Mean	44.15
Std Dev	5.217

Gaus mean : 45.1166

Gaus Width: 3.8029

ampl



B0L101S, U25-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

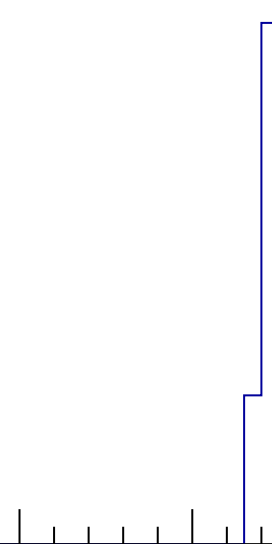
Entries	125
Mean	50.88
Std Dev	3.632

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch50, adc4

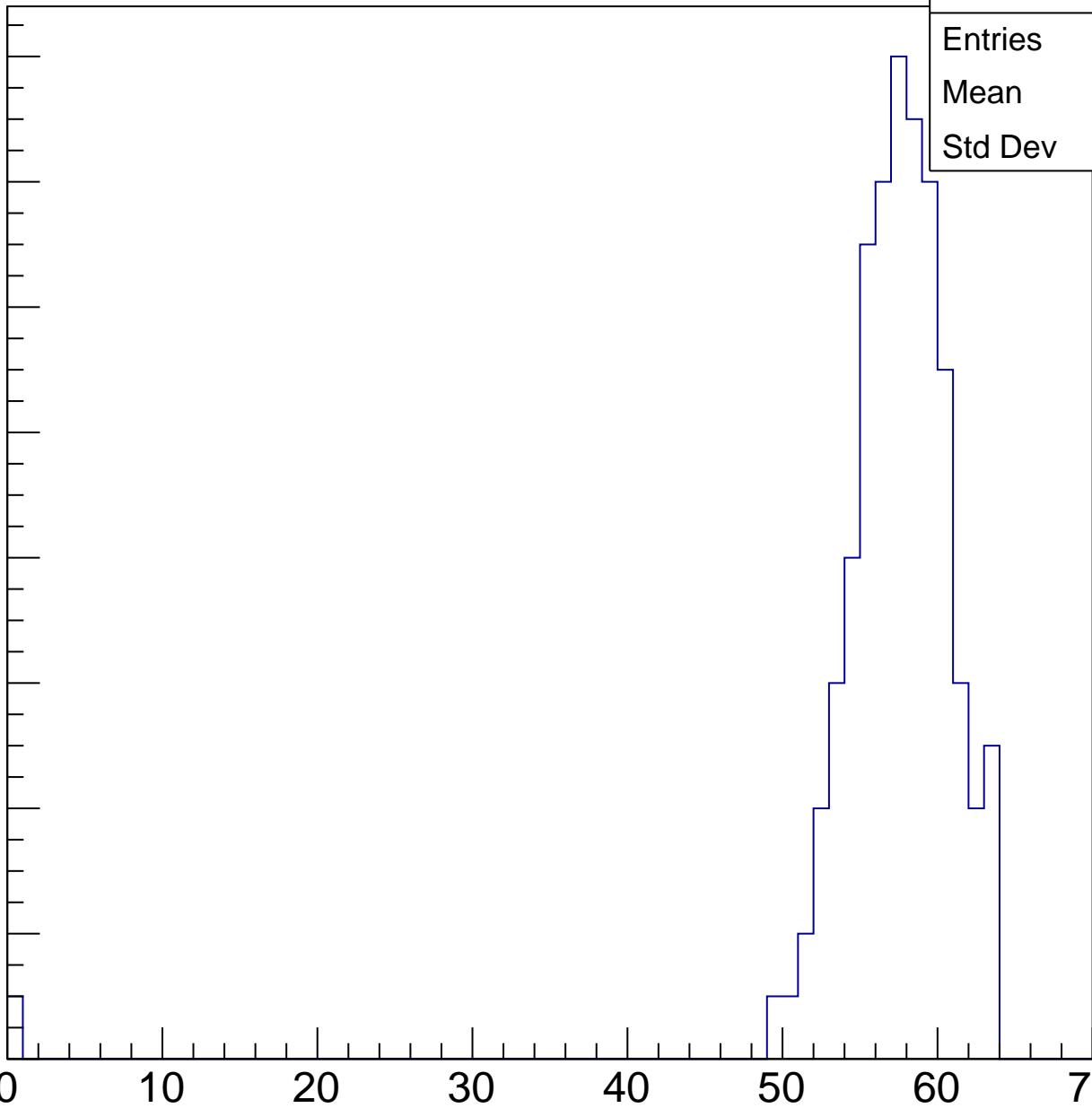
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	56.65
Std Dev	5.961

ampl



B0L101S, U25-ch50, adc5

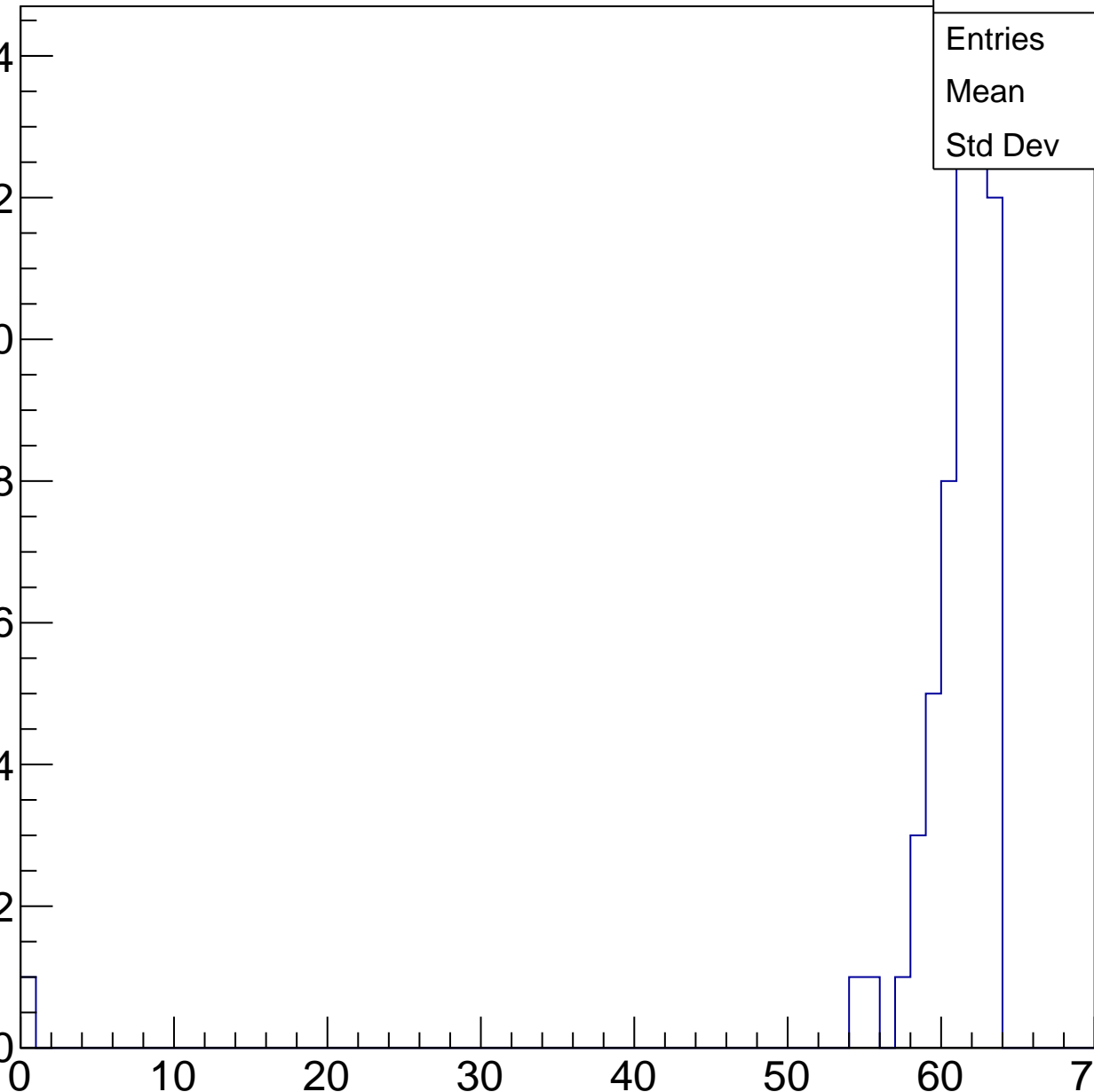
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.85
Std Dev	8.088

ampl



B0L101S, U25-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

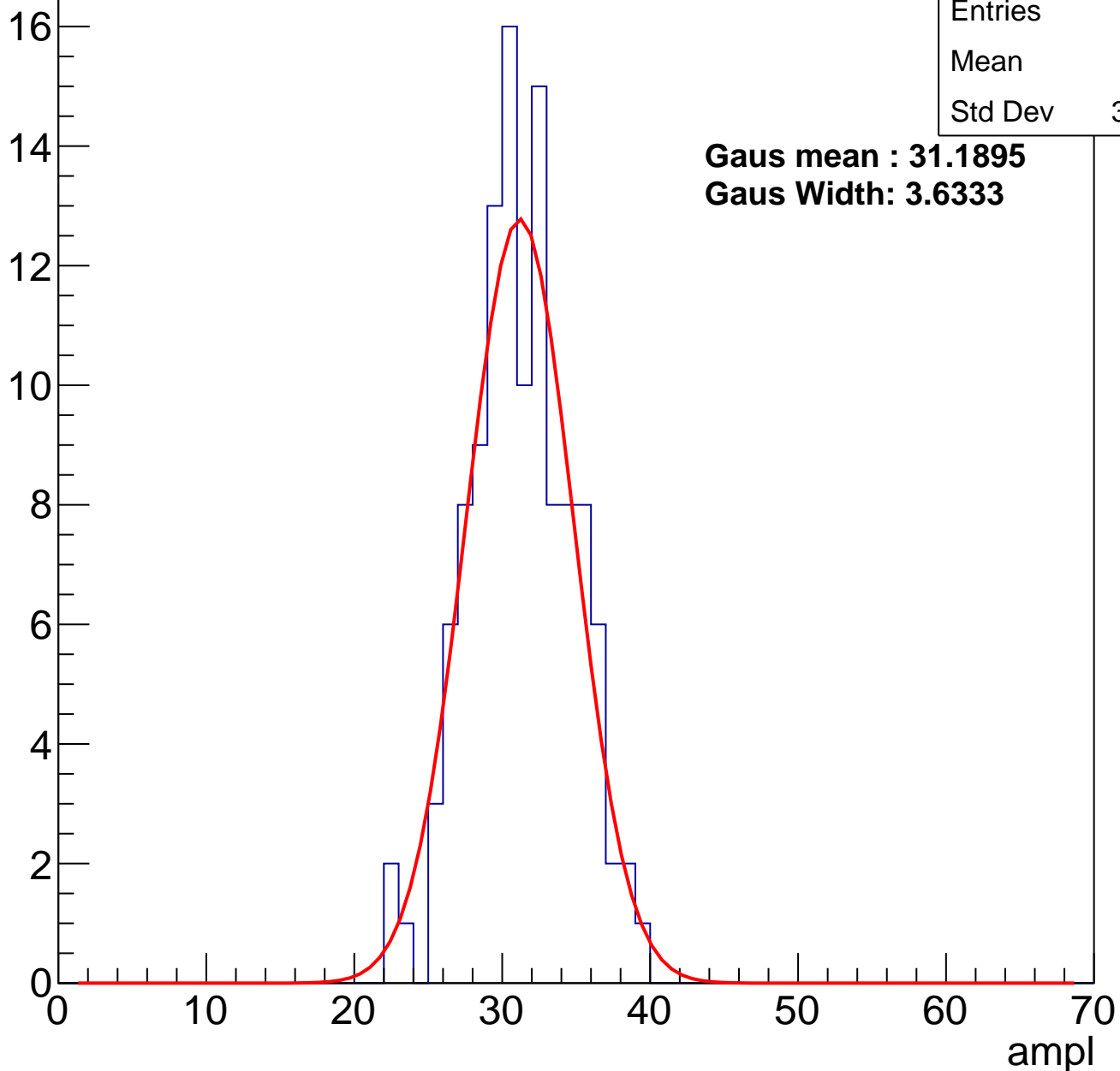
Entry



B0L101S, U25-ch51, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch51, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries	171
Mean	37.91
Std Dev	4.194

Gaus mean : 38.5943

Gaus Width: 4.0440

0

10

20

30

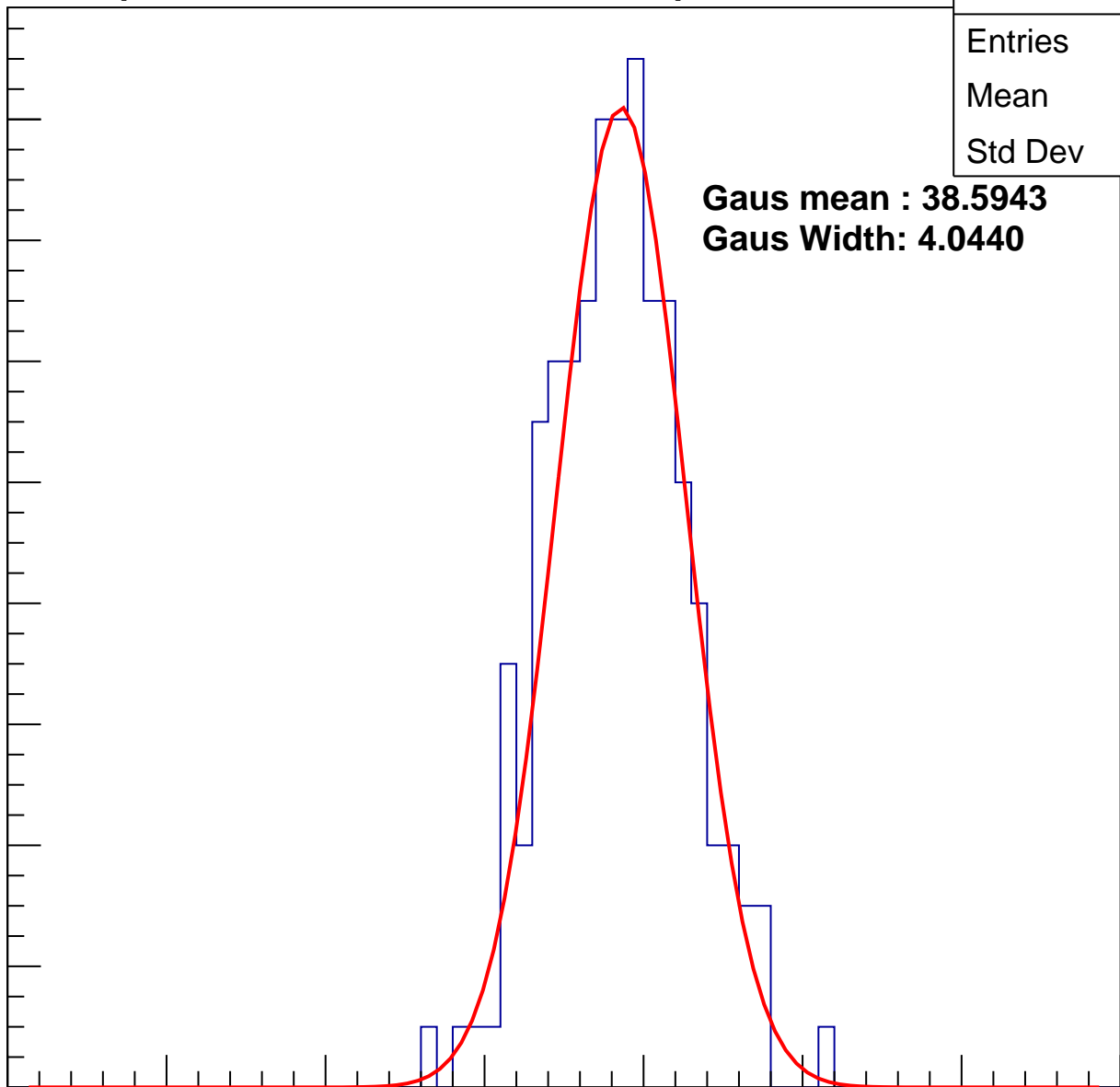
40

50

60

70

ampl



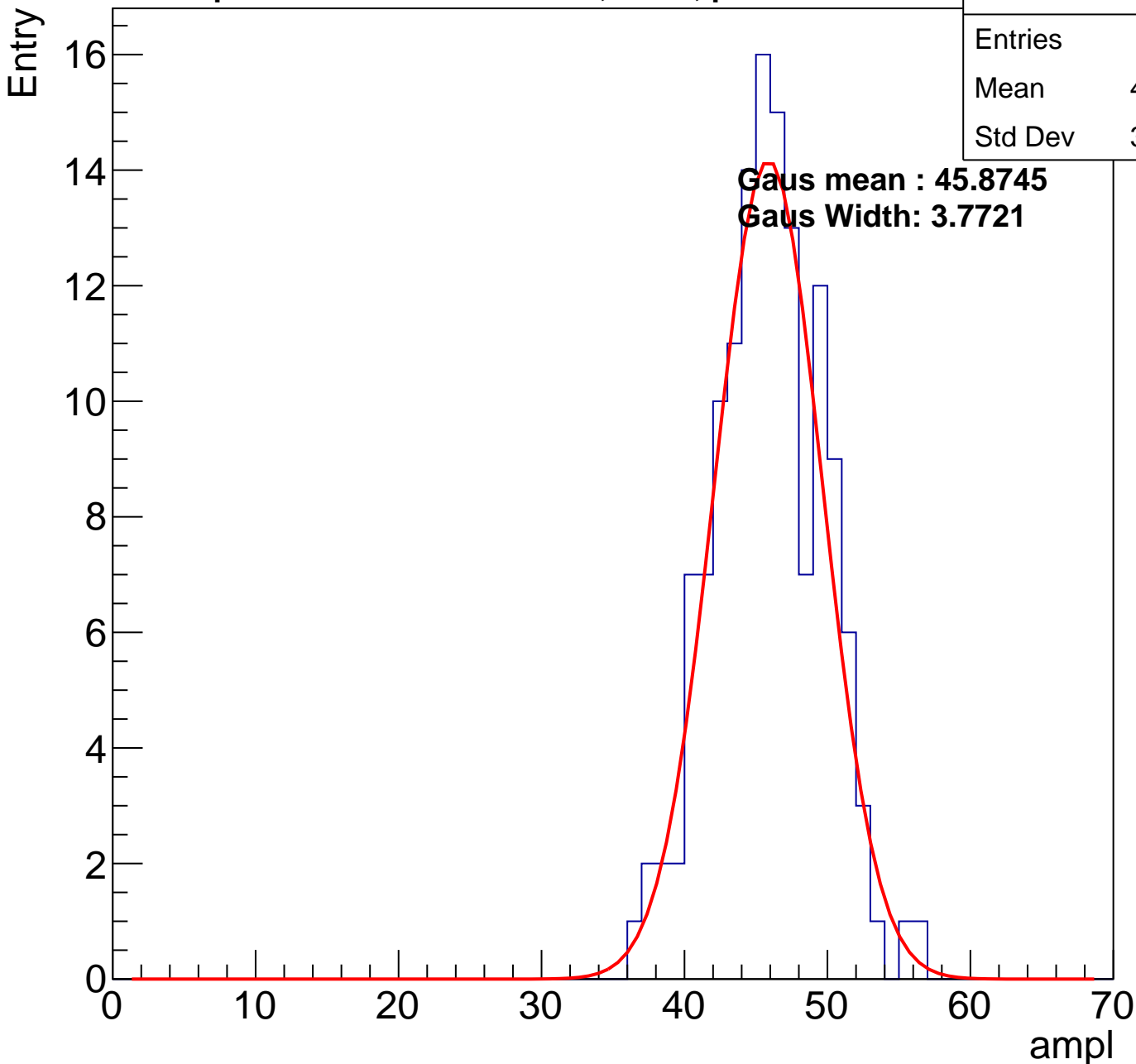
B0L101S, U25-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	45.44
Std Dev	3.763

Gaus mean : 45.8745

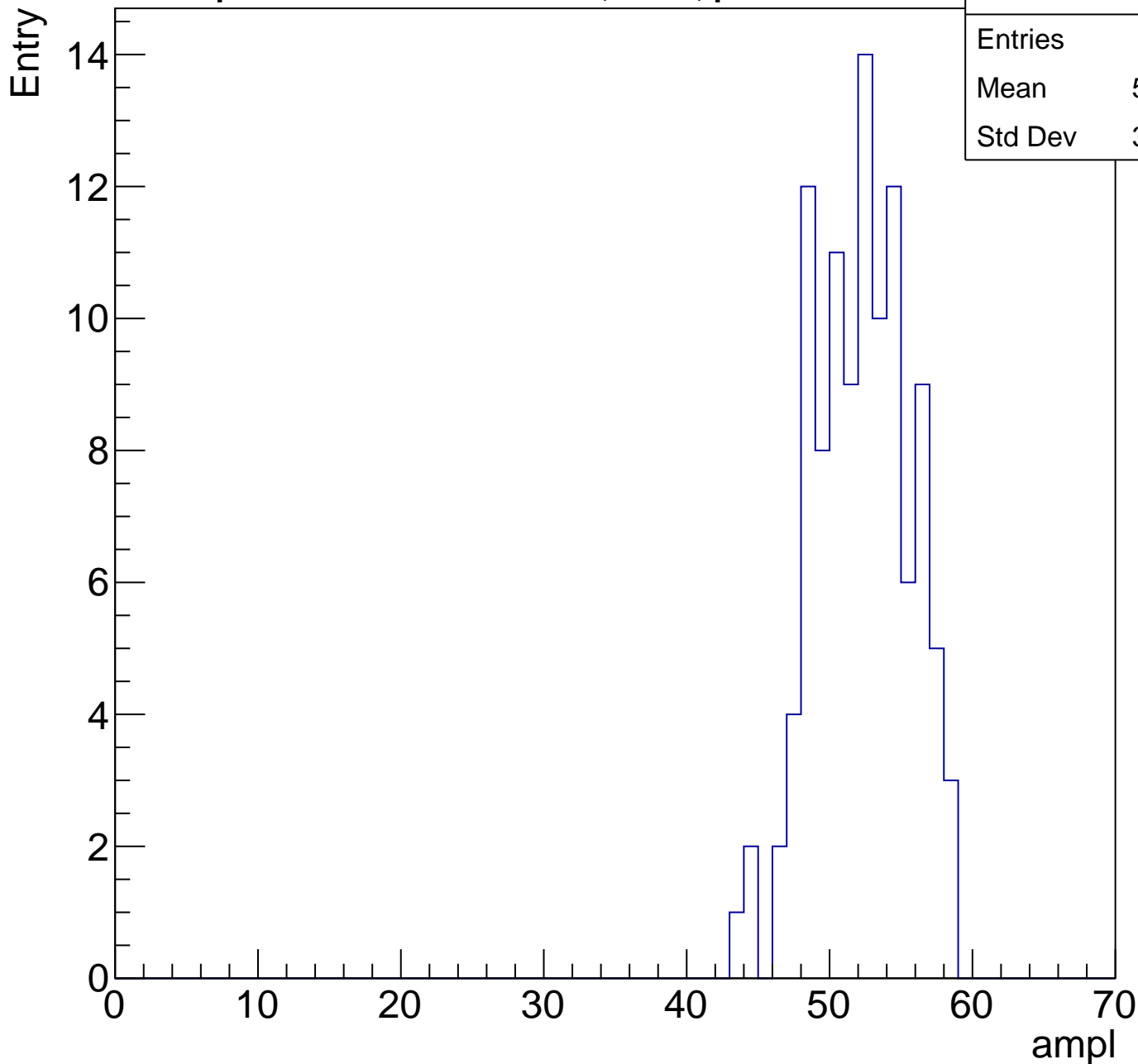
Gaus Width: 3.7721



B0L101S, U25-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

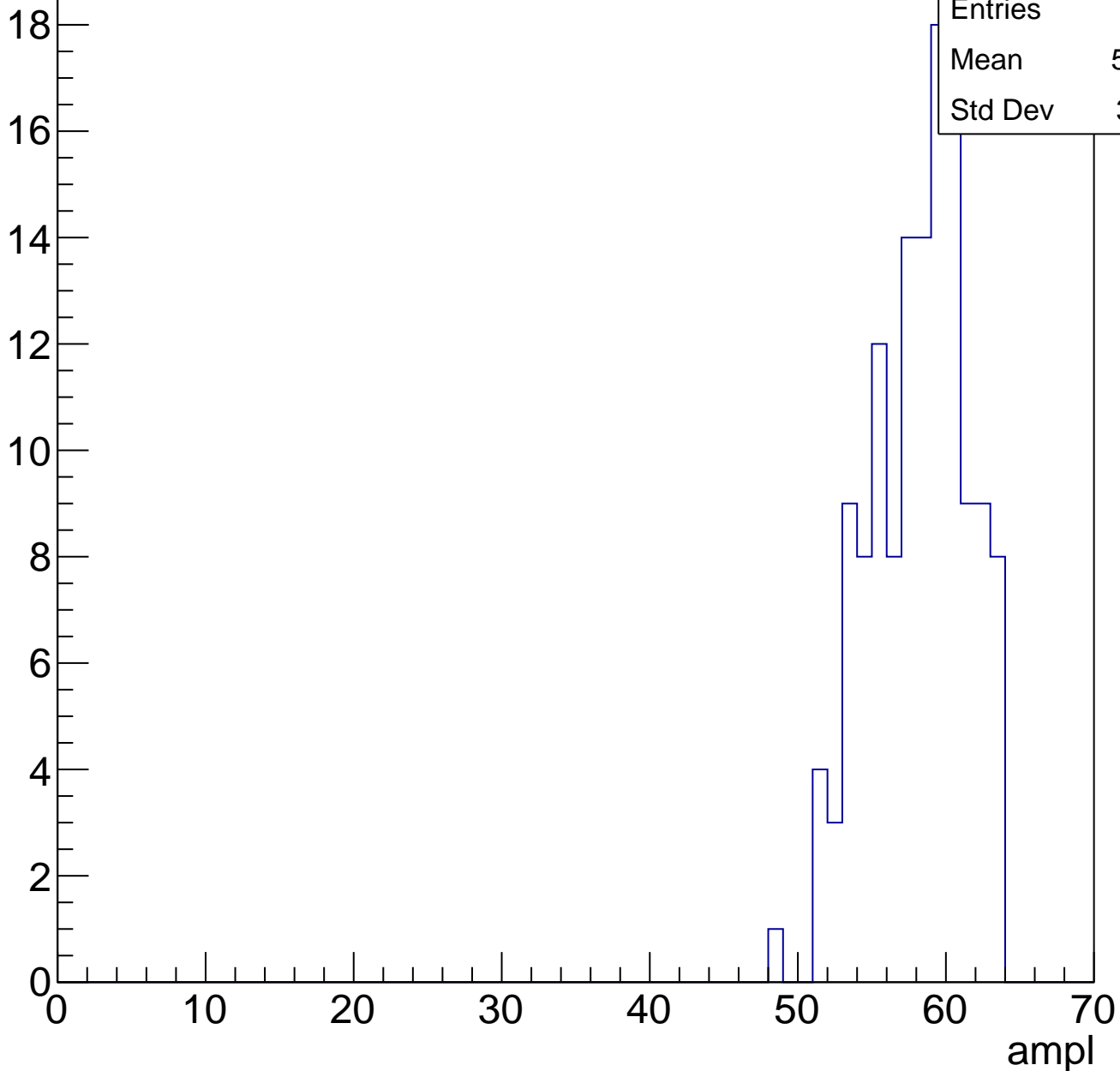
Entries	108
Mean	51.73
Std Dev	3.304



B0L101S, U25-ch51, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch51, adc5

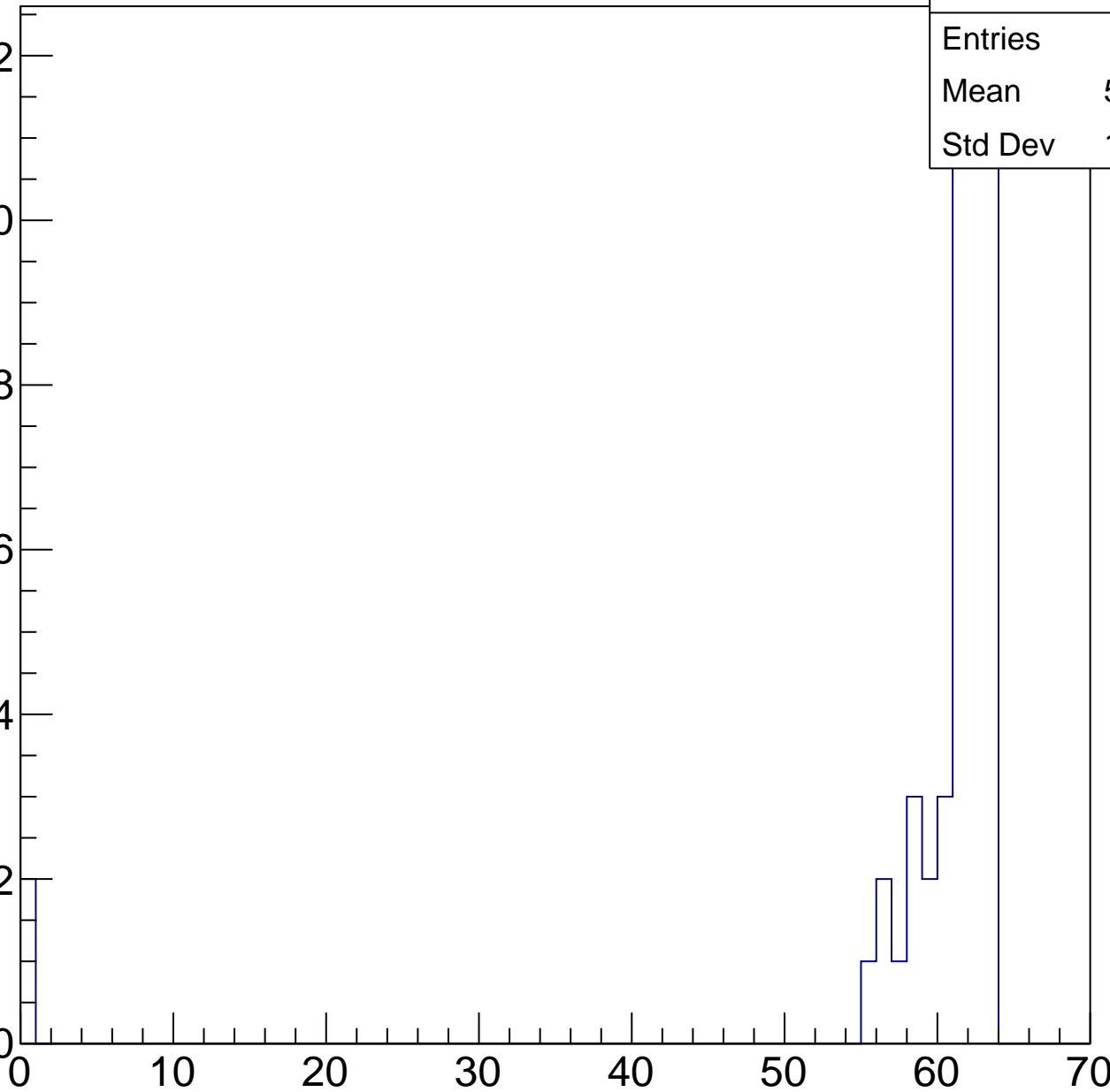
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	48
Mean	58.44
Std Dev	12.35

ampl



B0L101S, U25-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

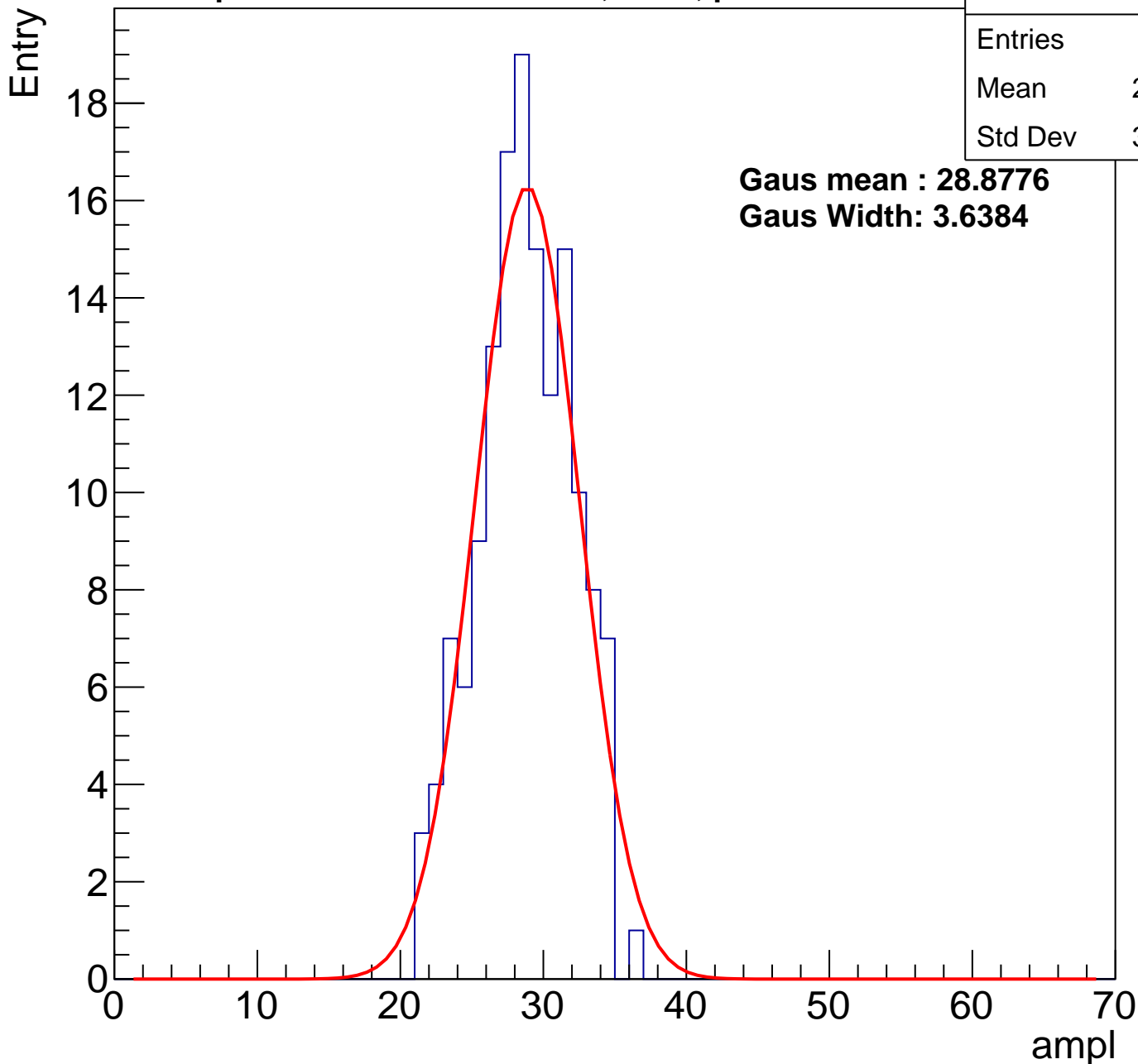
B0L101S, U25-ch52, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	28.27
Std Dev	3.285

Gaus mean : 28.8776

Gaus Width: 3.6384



B0L101S, U25-ch52, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	36.17
Std Dev	4.039

Gaus mean : 36.5767

Gaus Width: 3.8672

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

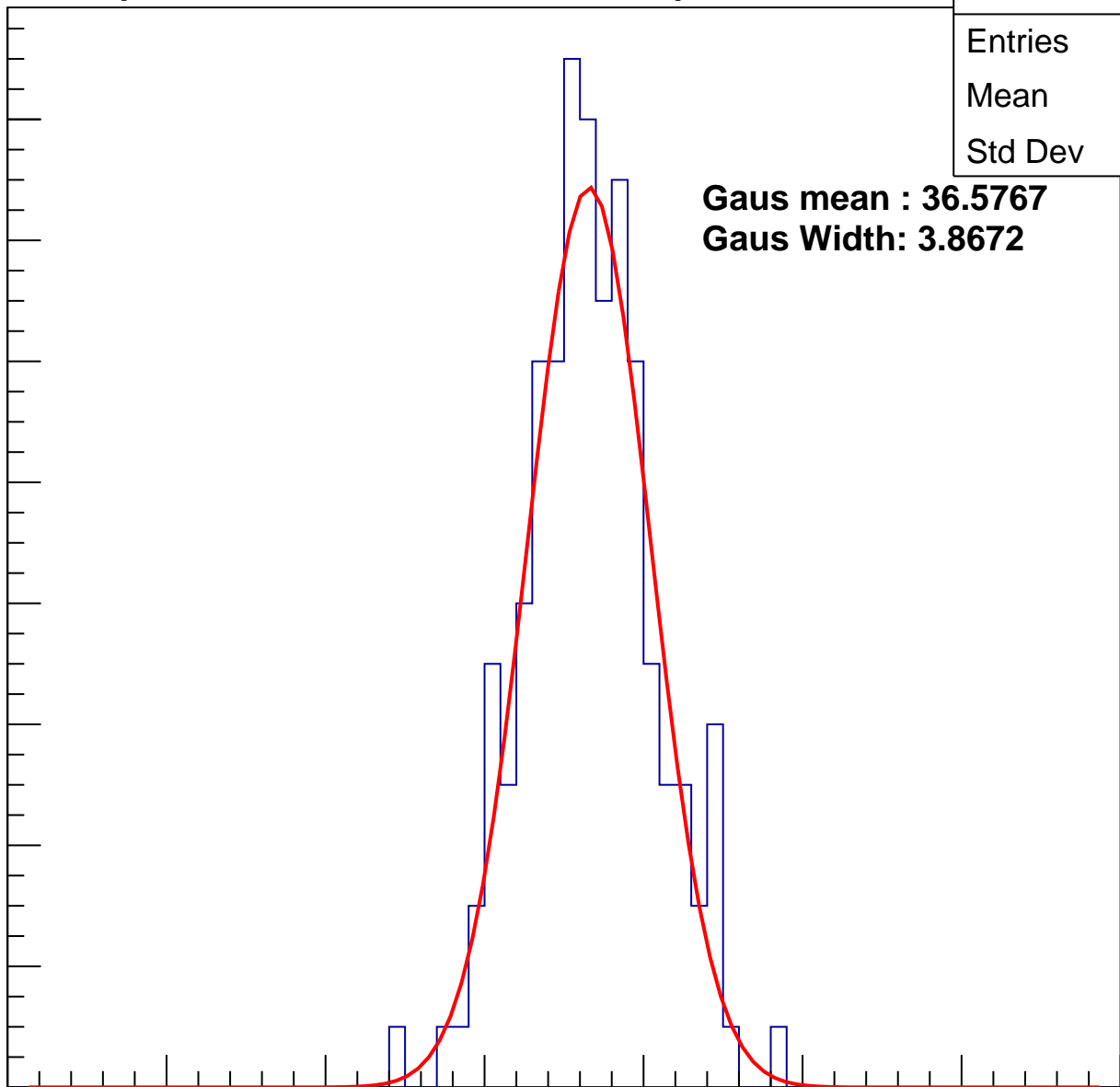
40

50

60

70

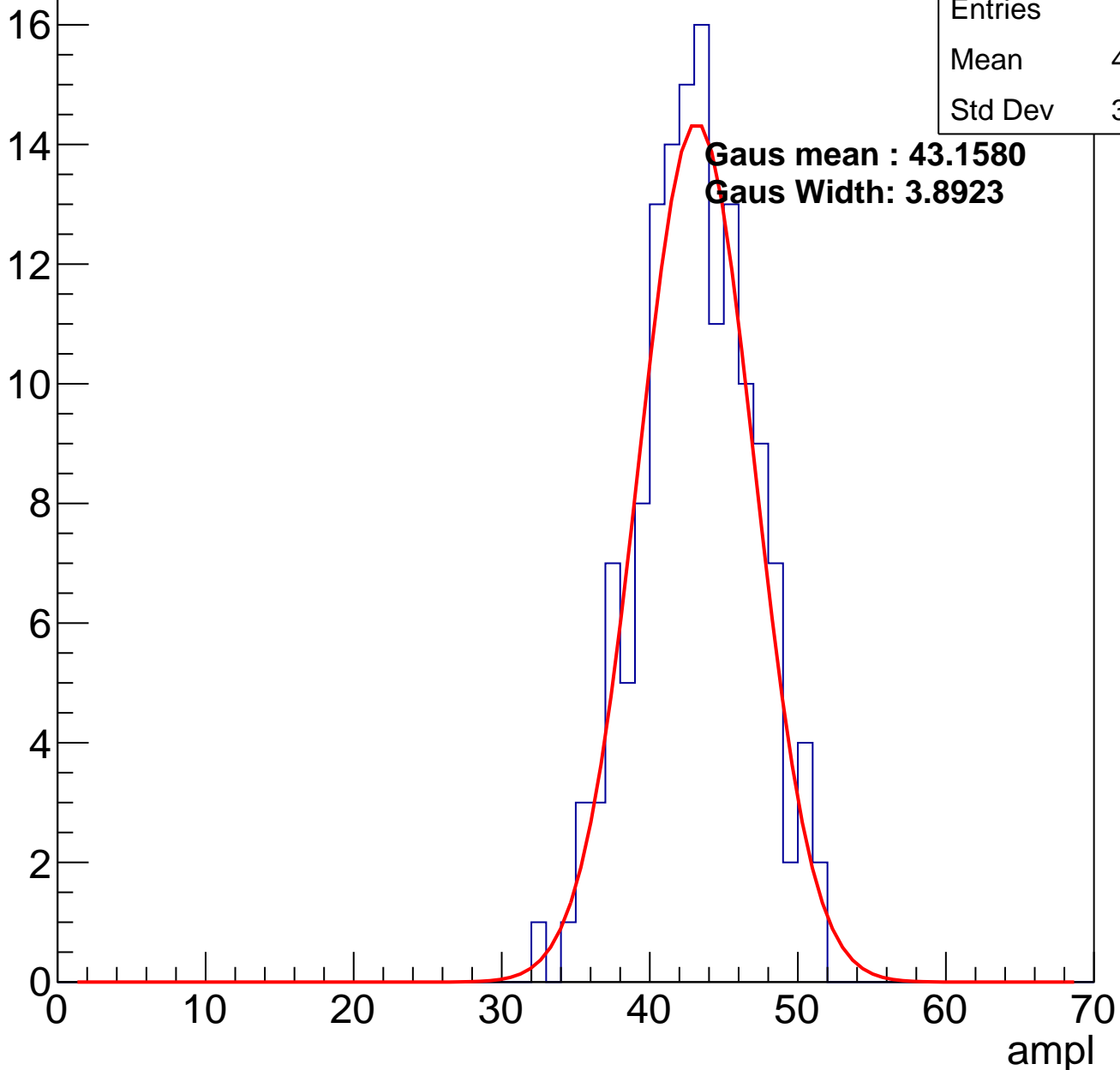
ampl



B0L101S, U25-ch52, adc2

calib_packv5_042523_0143.root, FC#1, port C1

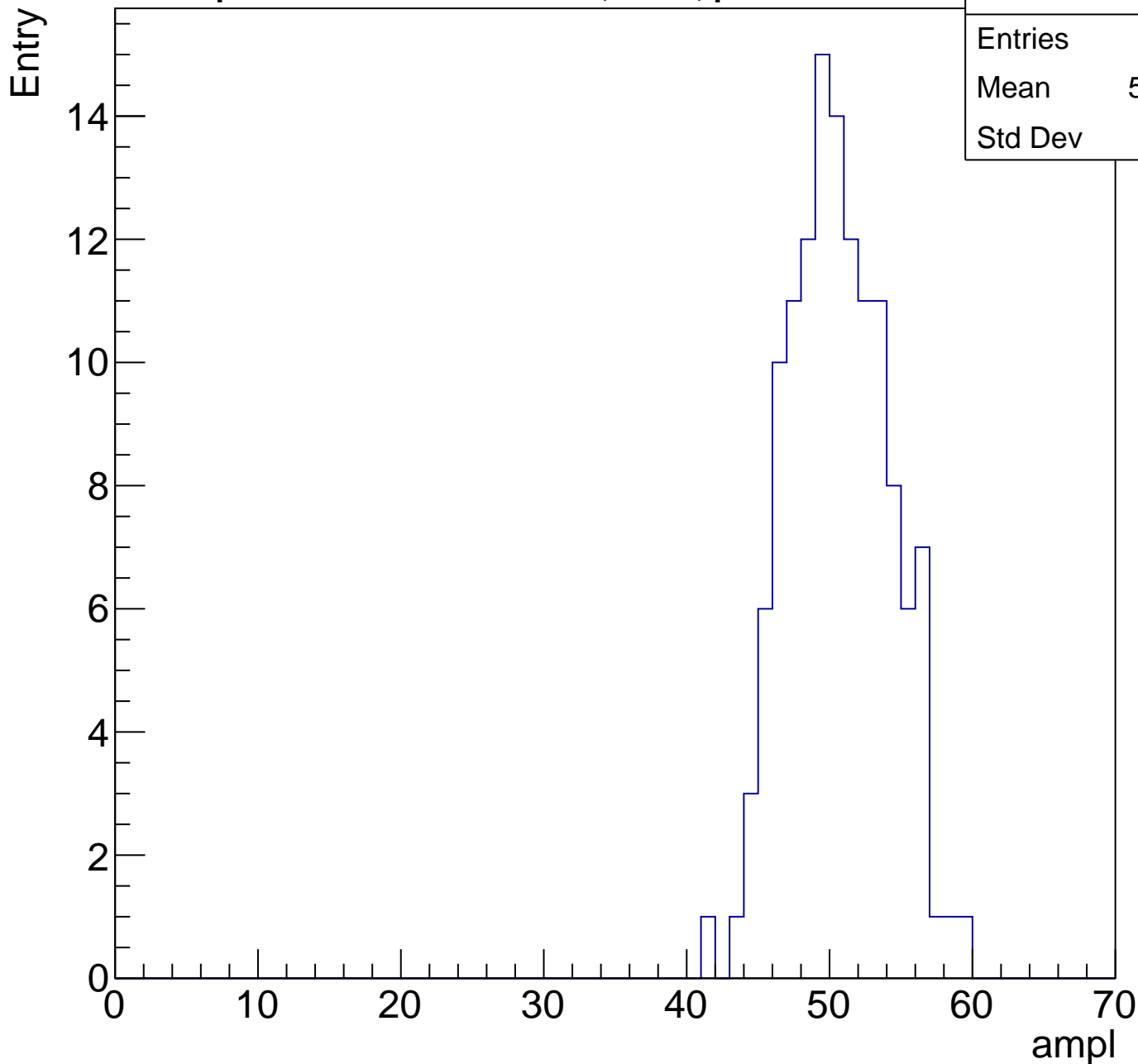
Entry



B0L101S, U25-ch52, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	50.15
Std Dev	3.48



B0L101S, U25-ch52, adc4

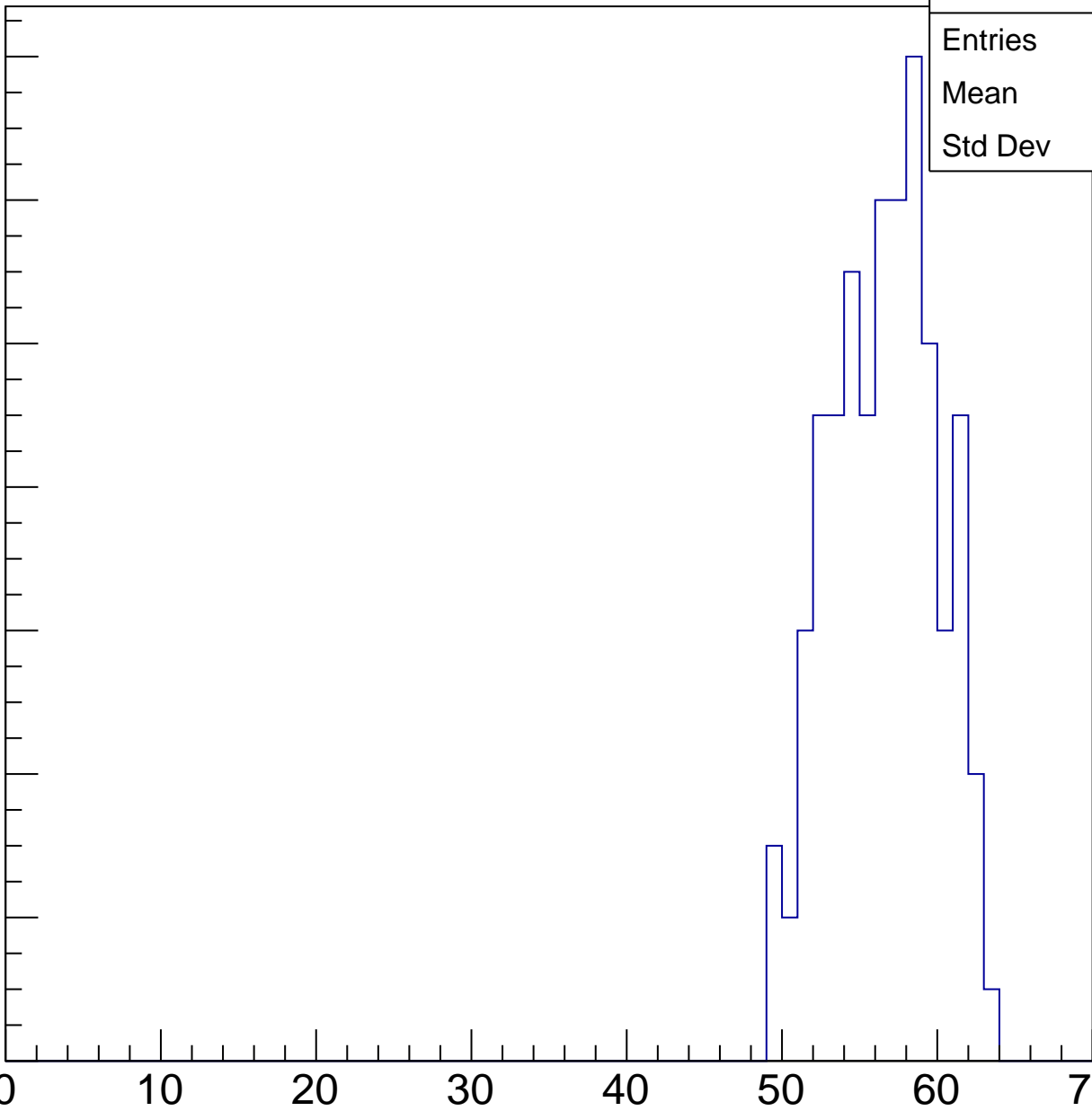
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	56.11
Std Dev	3.356

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch52, adc5

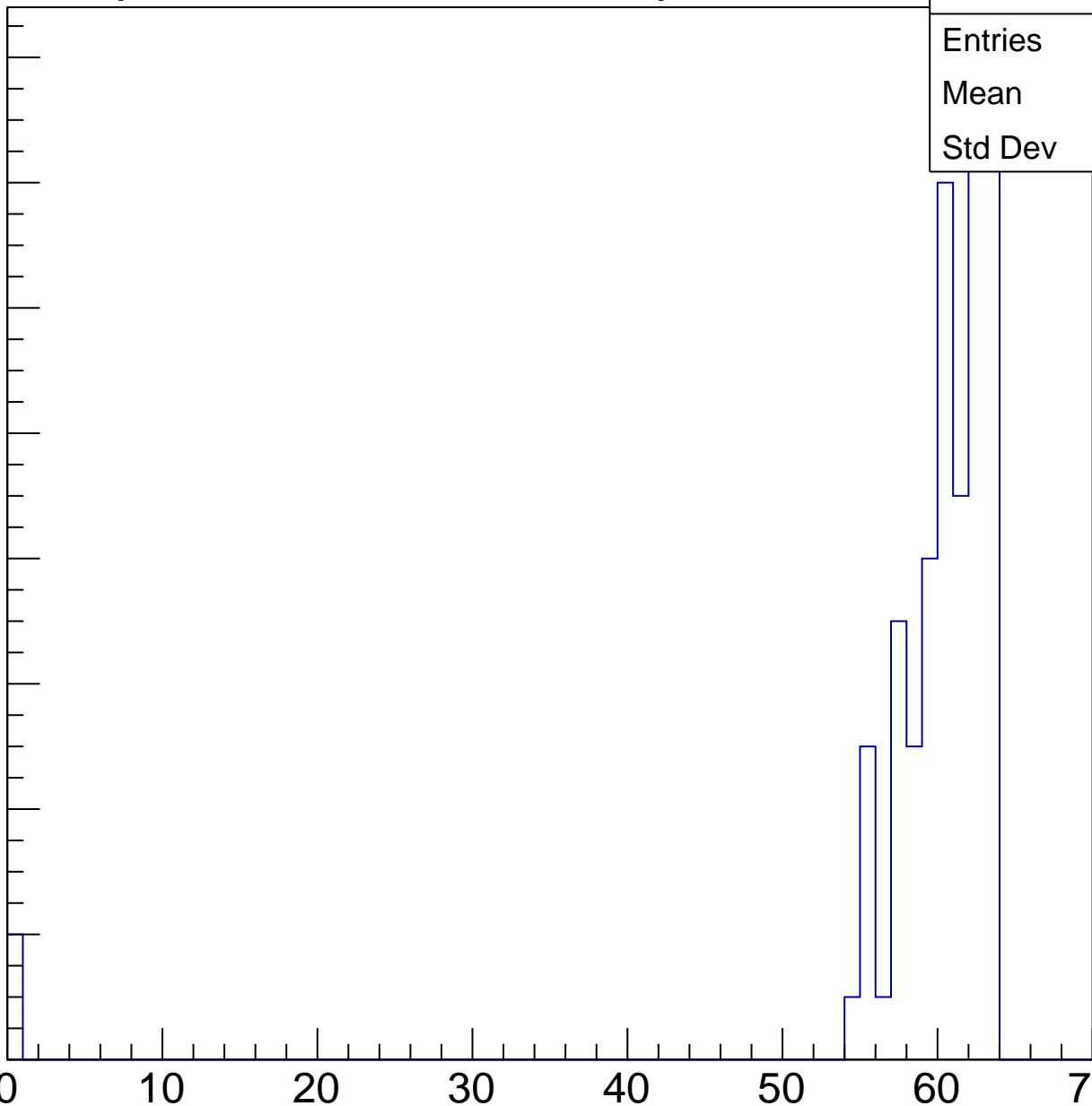
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.75
Std Dev	9.483

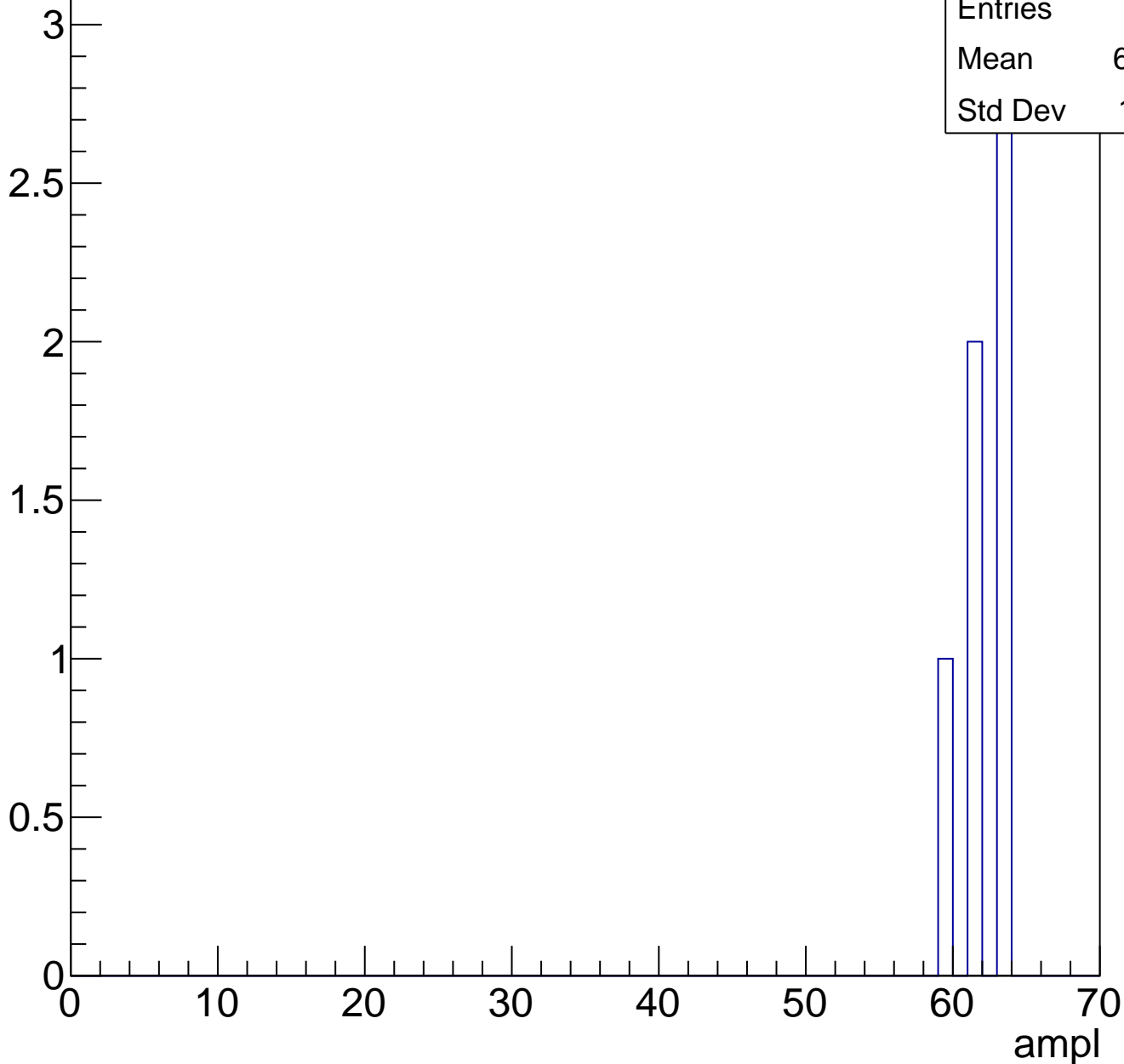
ampl



B0L101S, U25-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch53, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	29.45
Std Dev	4.182

Gaus mean : 29.0820

Gaus Width: 4.3394

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

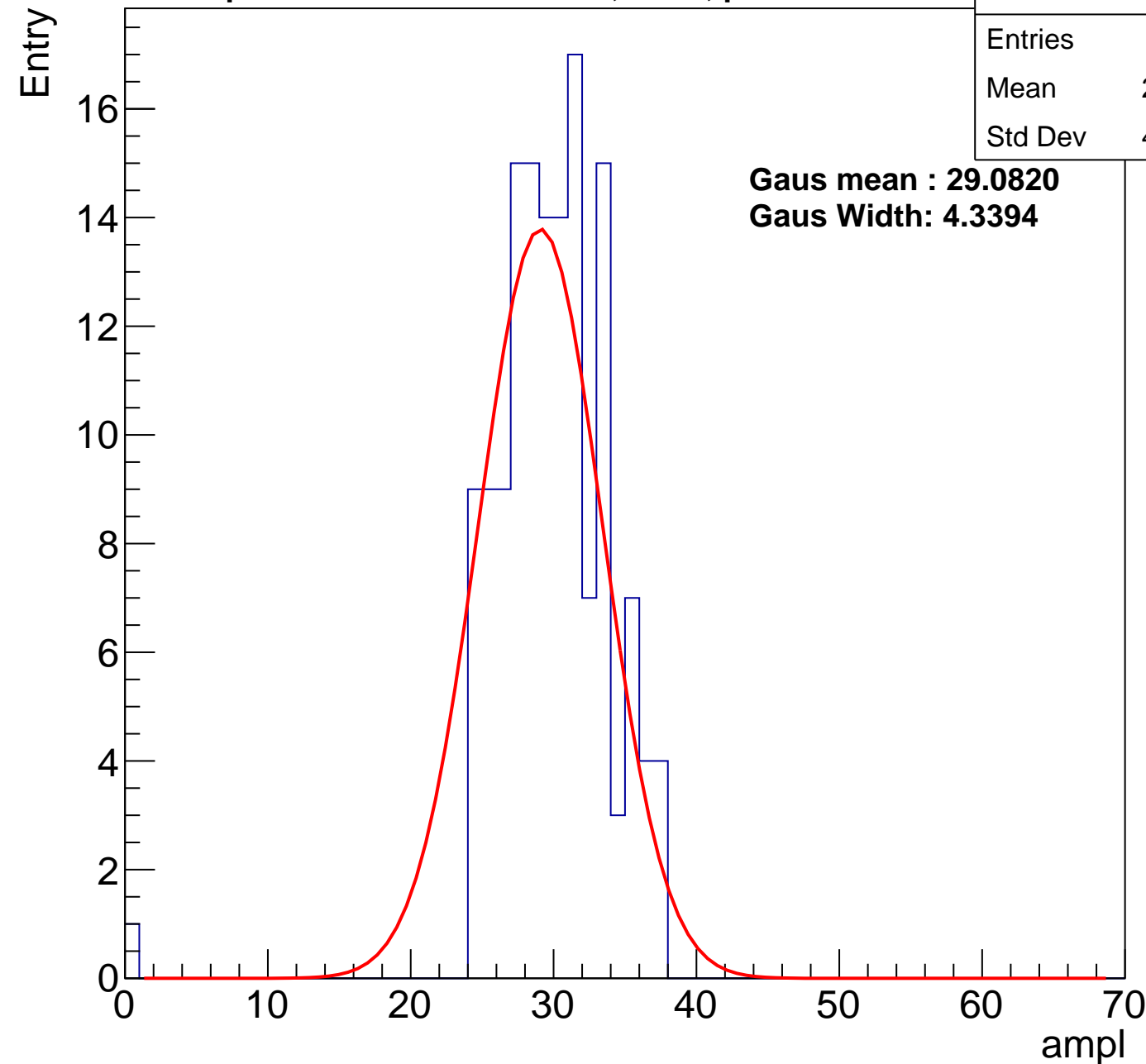
40

50

60

70

ampl



B0L101S, U25-ch53, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

166

Mean

37.21

Std Dev

3.667

Gaus mean : 37.7737

Gaus Width: 3.6111

0

10

20

30

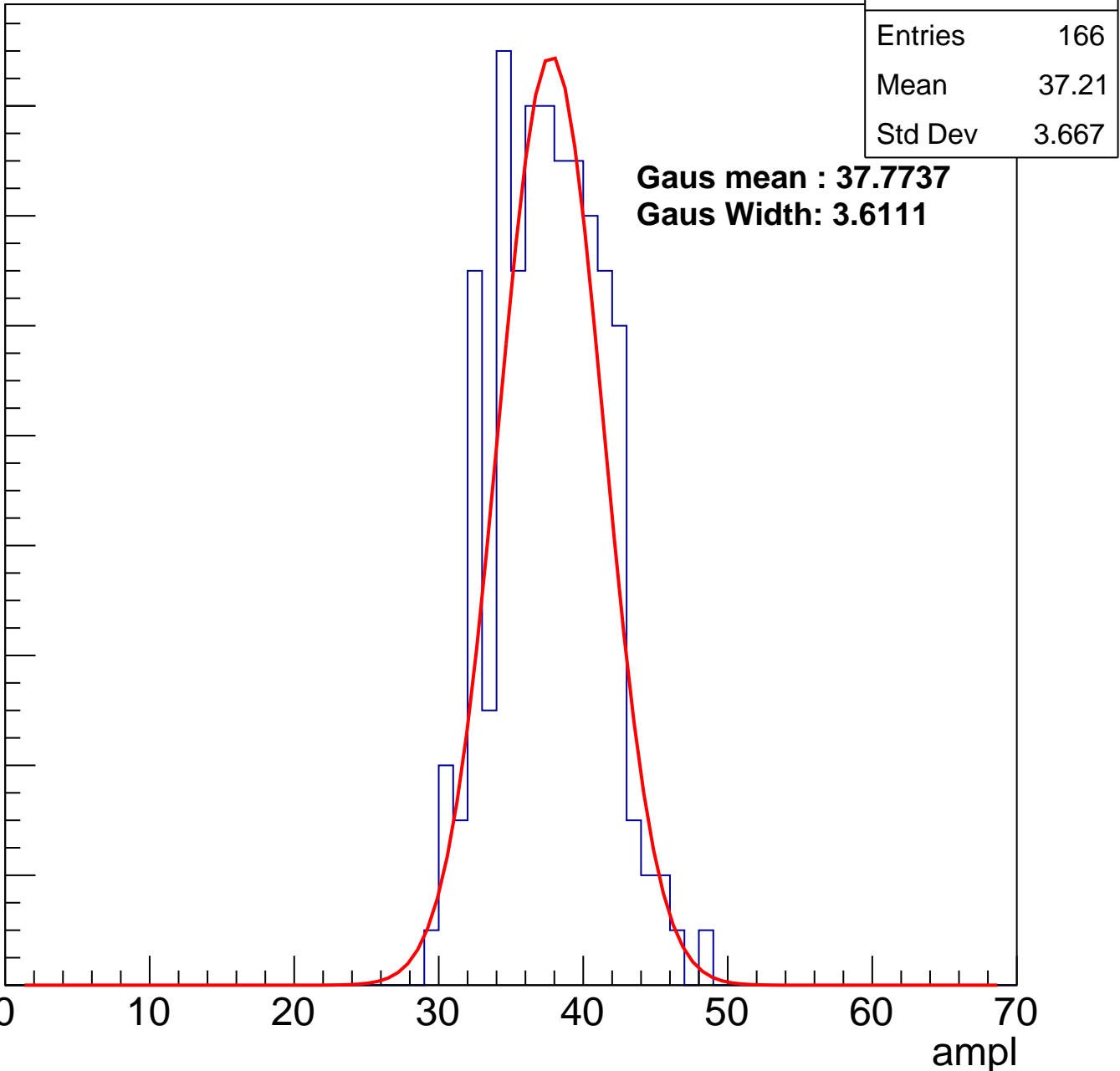
40

50

60

70

ampl



B0L101S, U25-ch53, adc2

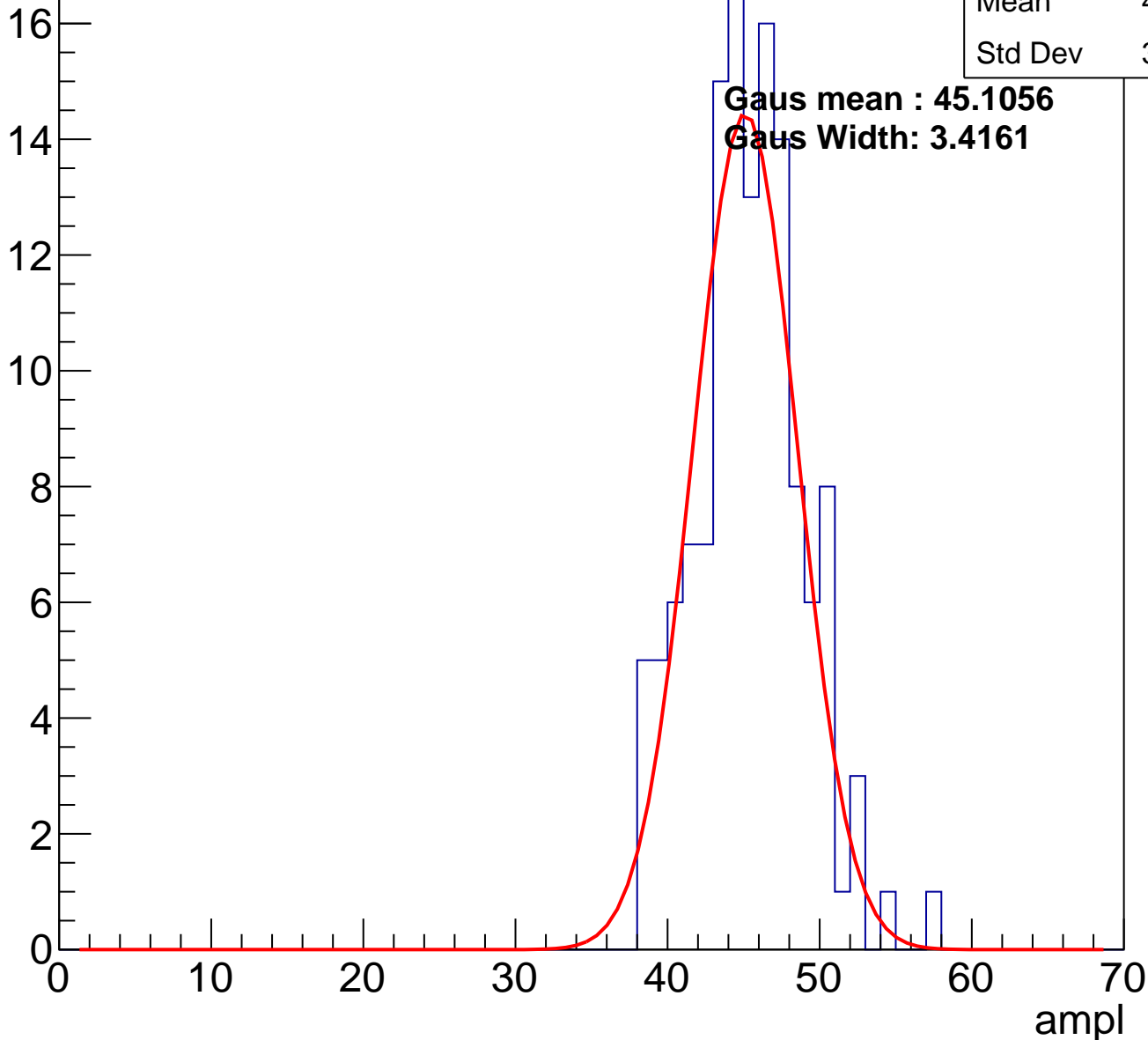
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	133
Mean	44.92
Std Dev	3.548

Gaus mean : 45.1056

Gaus Width: 3.4161



B0L101S, U25-ch53, adc3

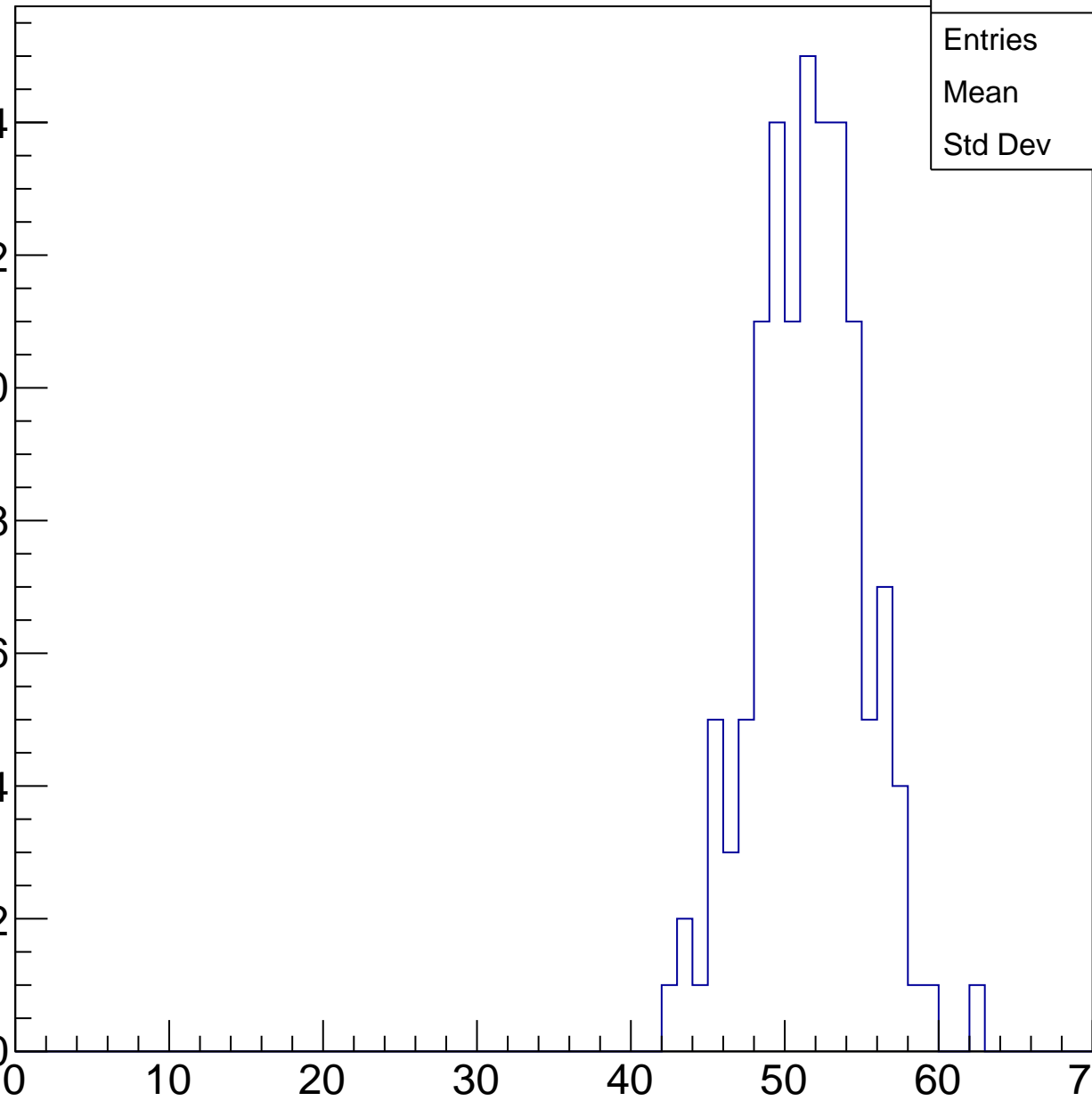
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	51.09
Std Dev	3.519

Entry

14
12
10
8
6
4
2
0

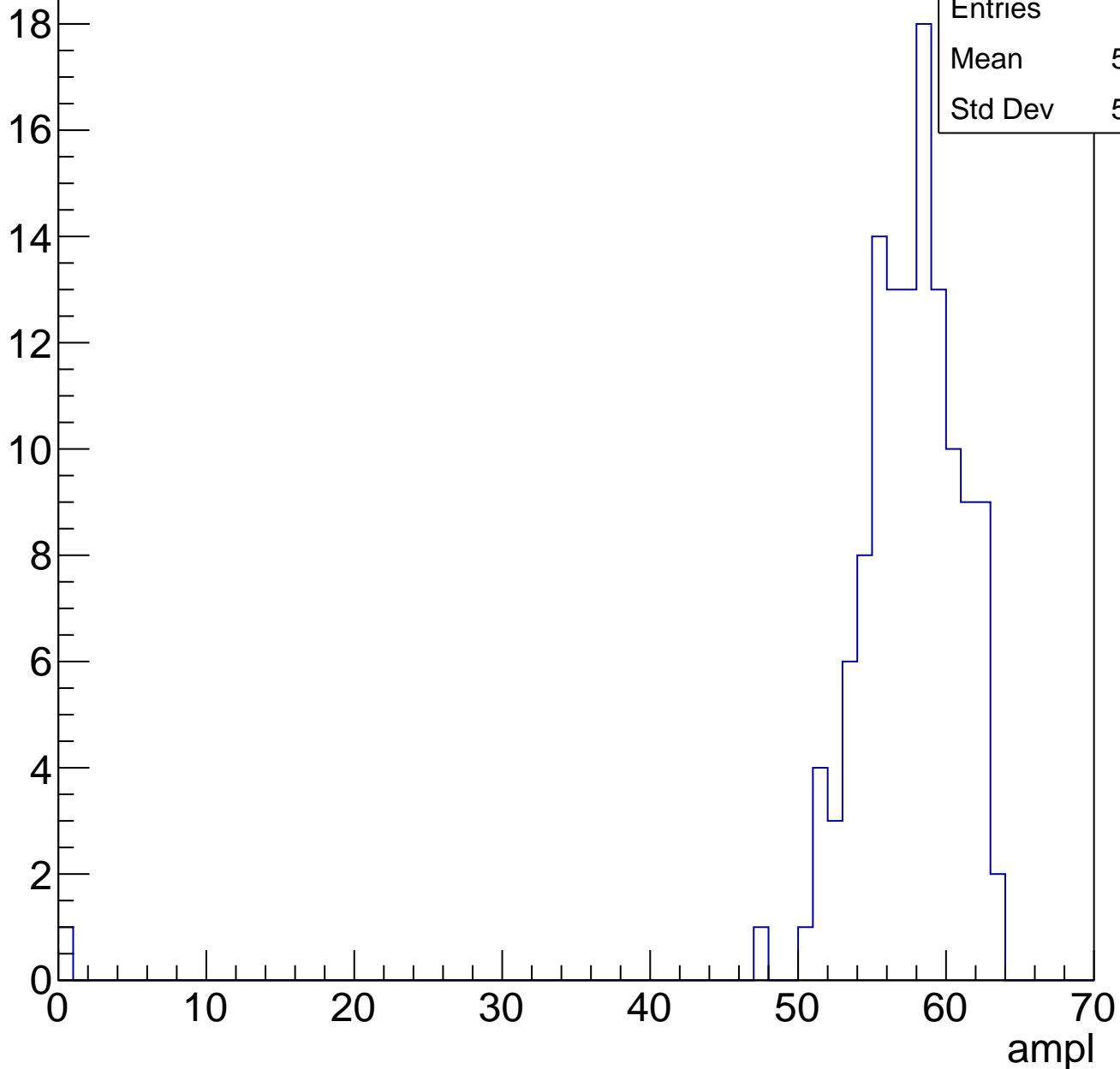
ampl



B0L101S, U25-ch53, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	125
Mean	56.72
Std Dev	5.963

B0L101S, U25-ch53, adc5

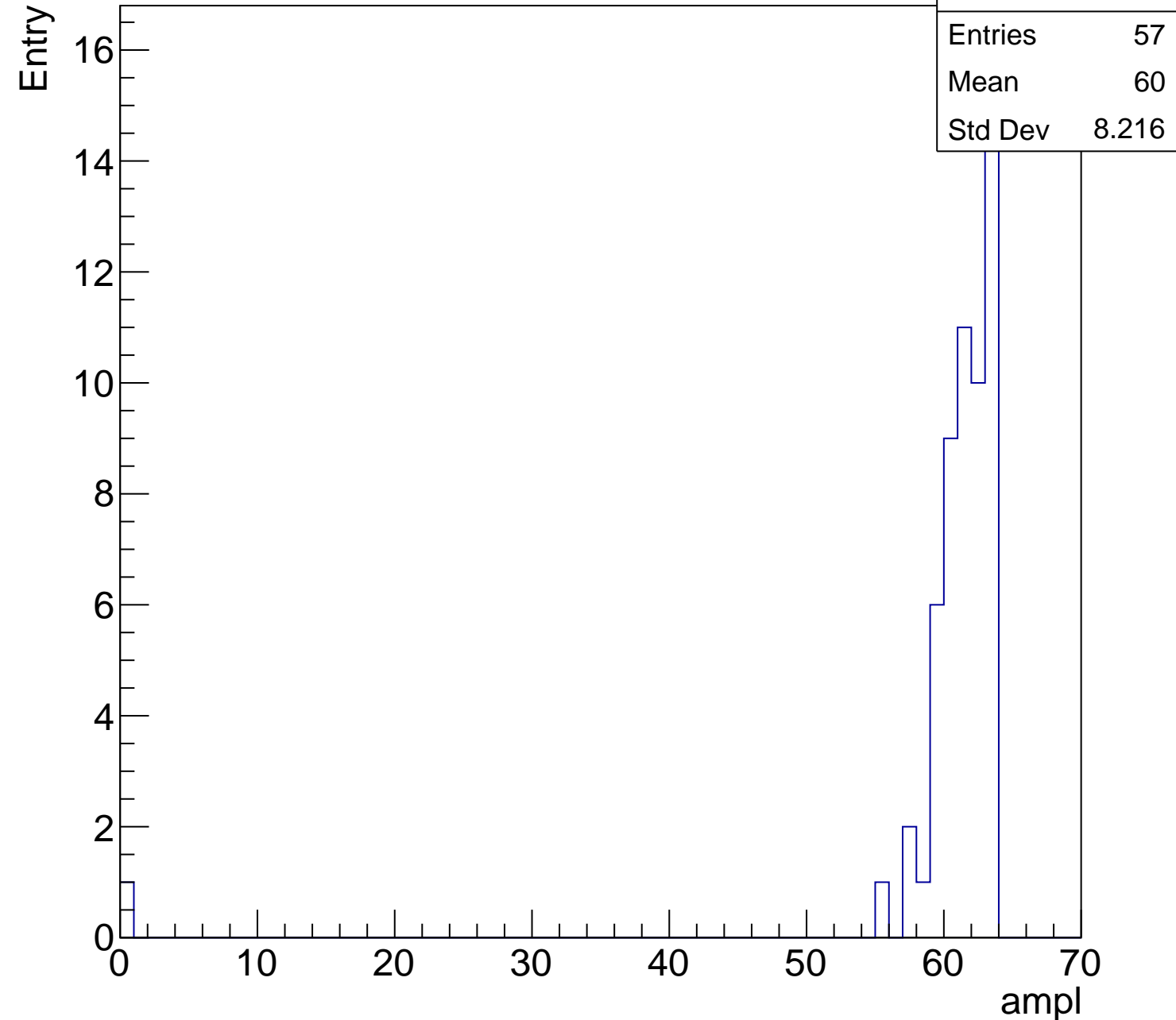
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	57
Mean	60
Std Dev	8.216

ampl



B0L101S, U25-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch54, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	29.08
Std Dev	4.291

Gaus mean : 29.8524

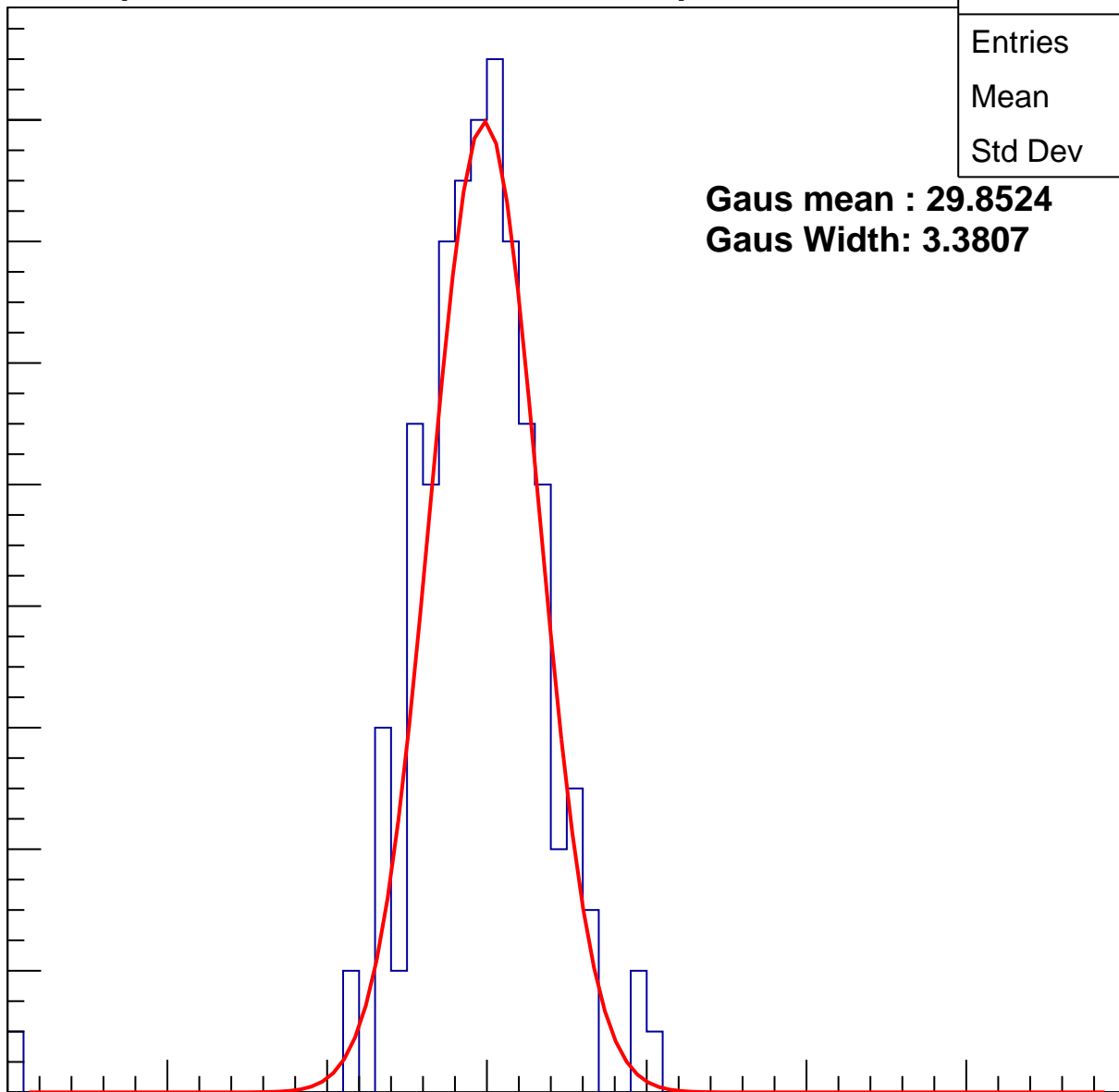
Gaus Width: 3.3807

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



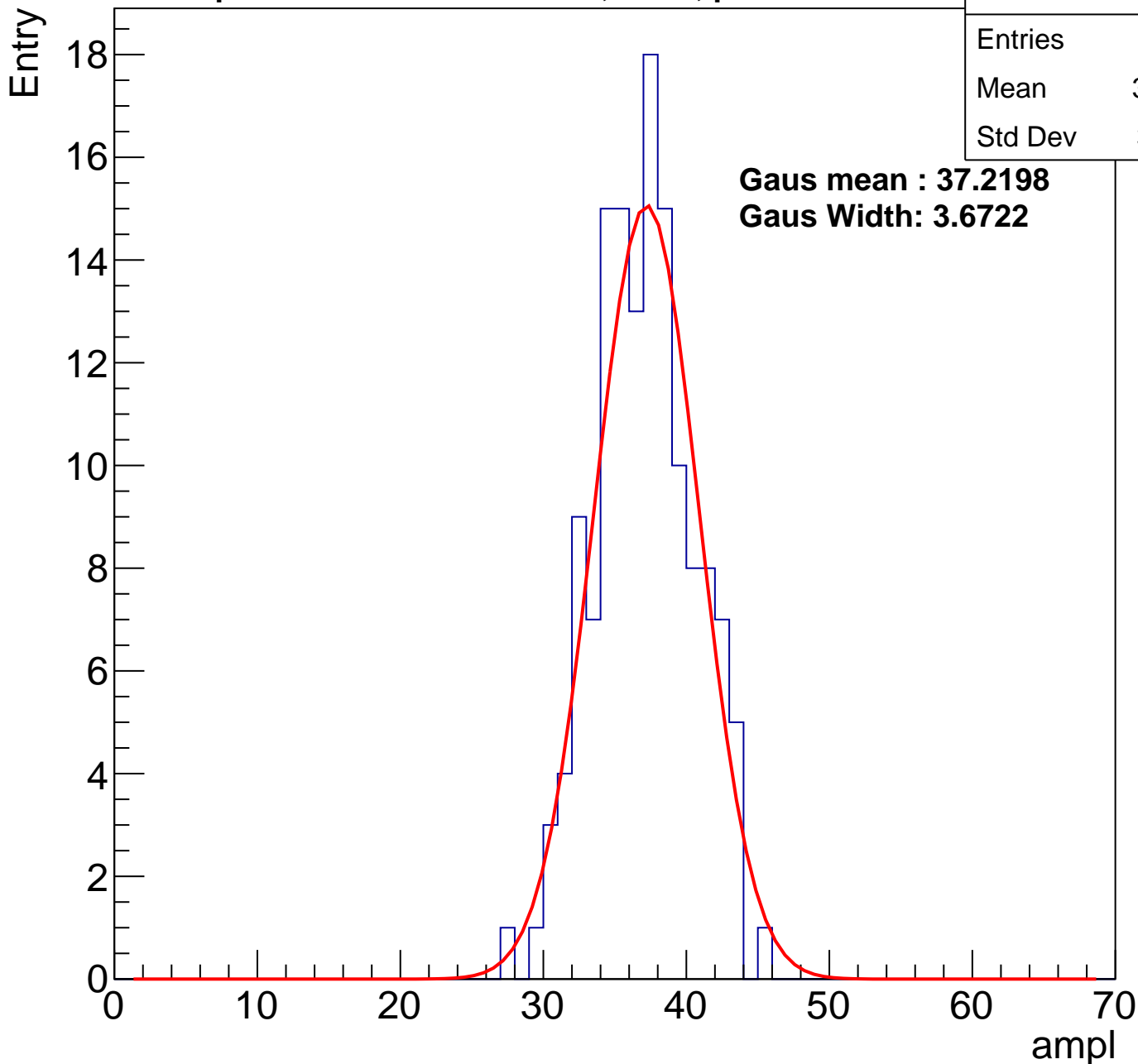
B0L101S, U25-ch54, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	36.57
Std Dev	3.431

Gaus mean : 37.2198

Gaus Width: 3.6722



B0L101S, U25-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

119

Mean

42.76

Std Dev

3.548

Gaus mean : 43.4134

Gaus Width: 3.5663

0

2

4

6

8

10

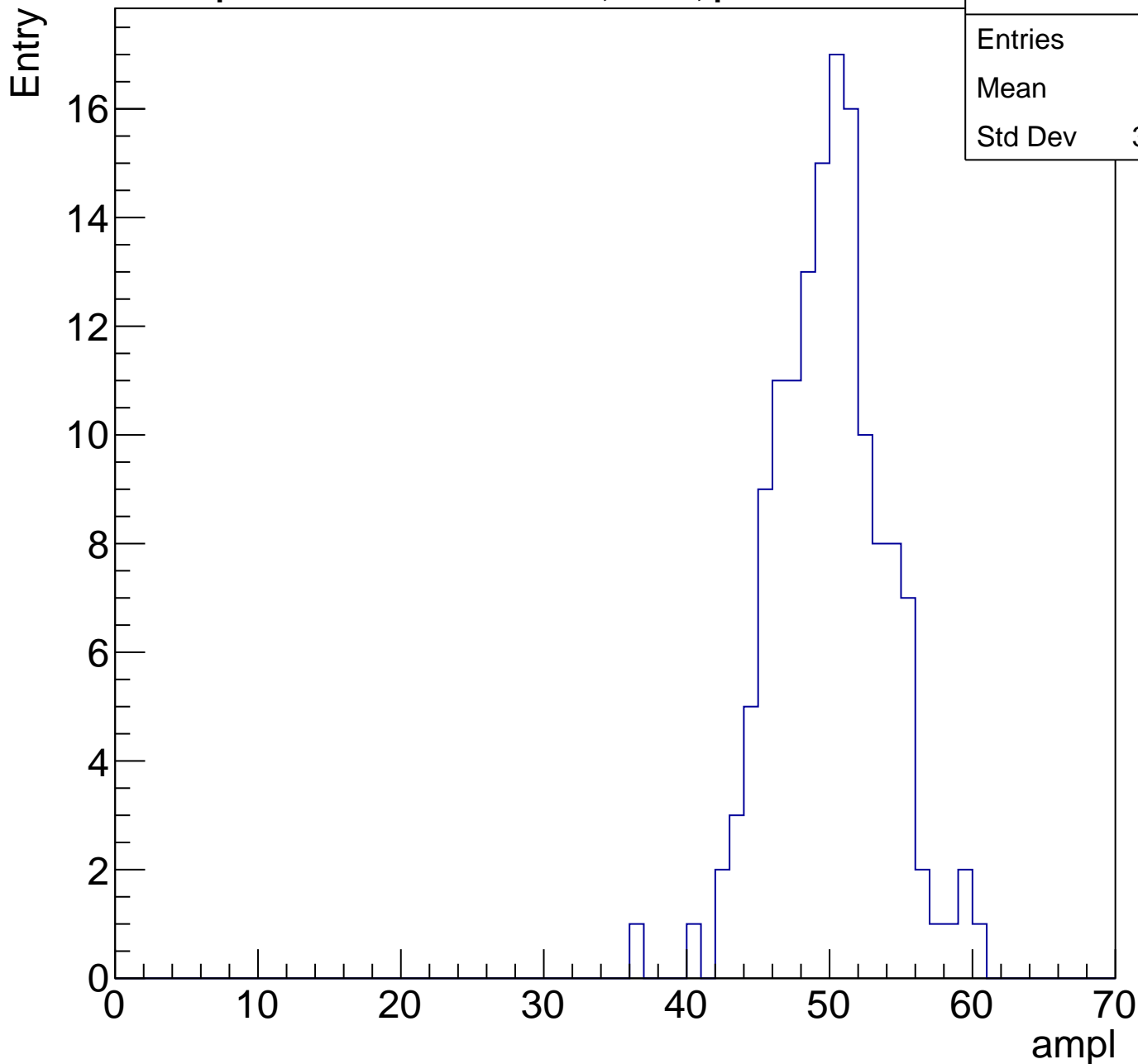
12

14

B0L101S, U25-ch54, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	49.5
Std Dev	3.873

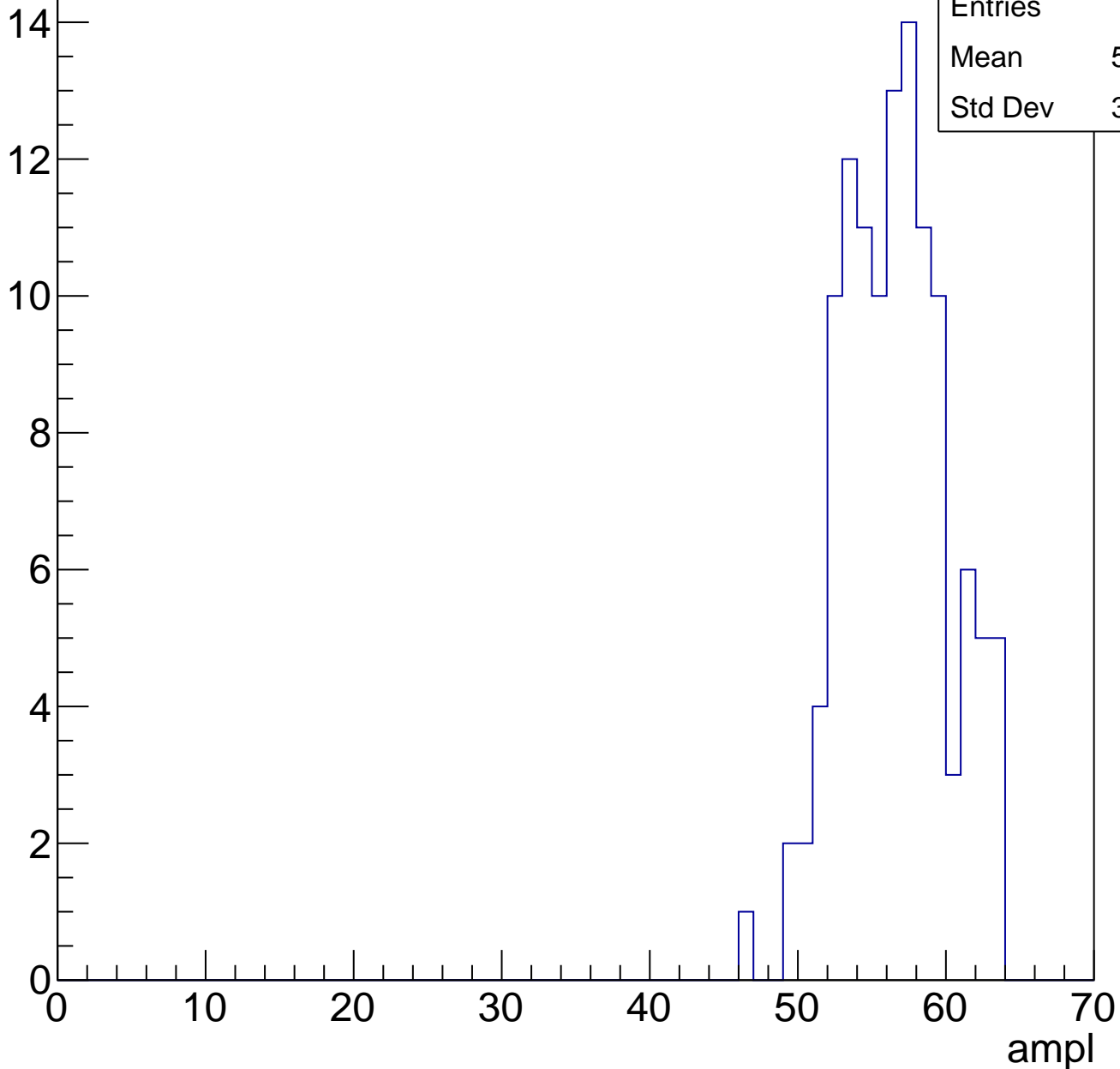


B0L101S, U25-ch54, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	56.08
Std Dev	3.505

Entry



B0L101S, U25-ch54, adc5

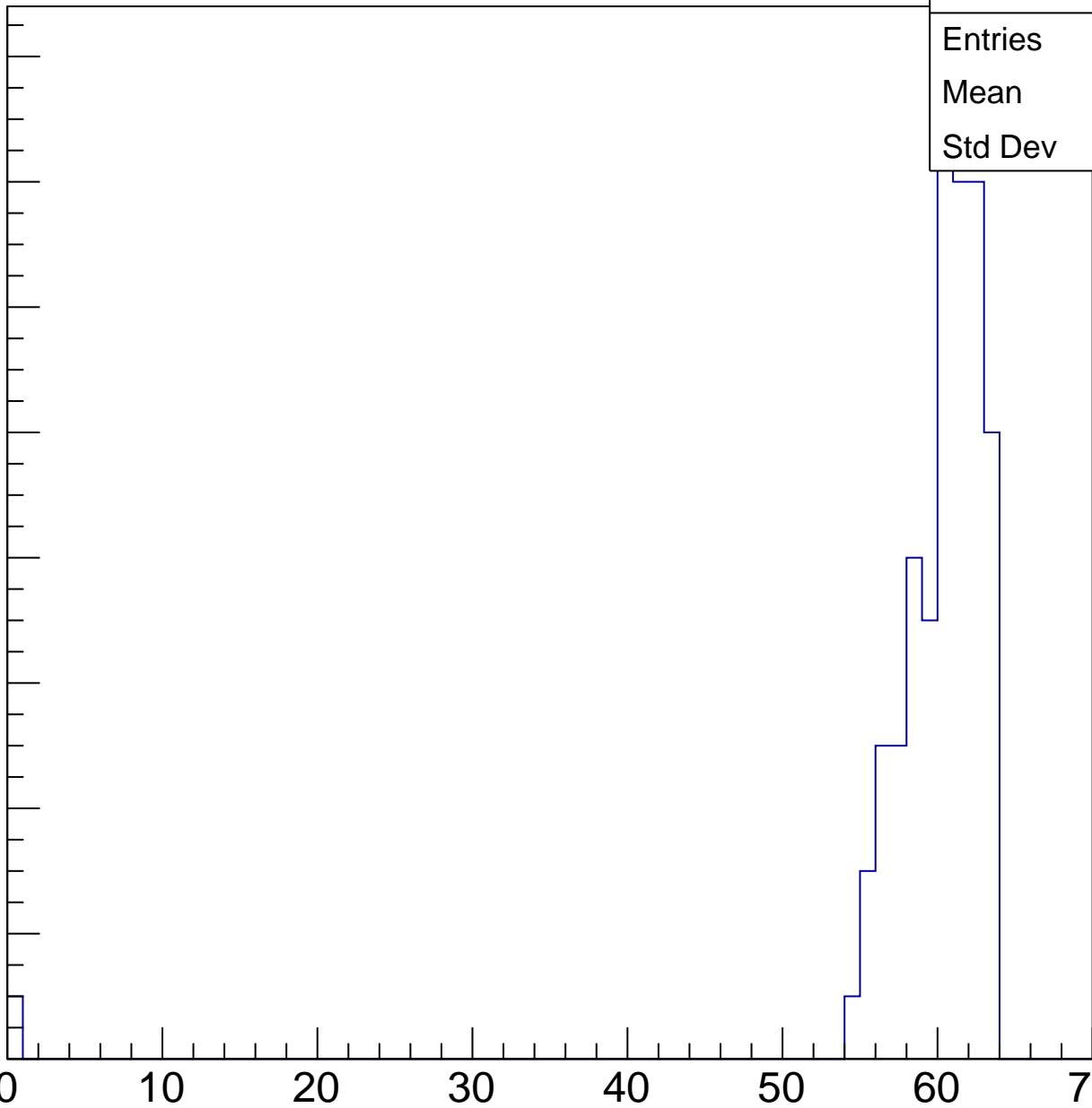
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.2
Std Dev	6.885

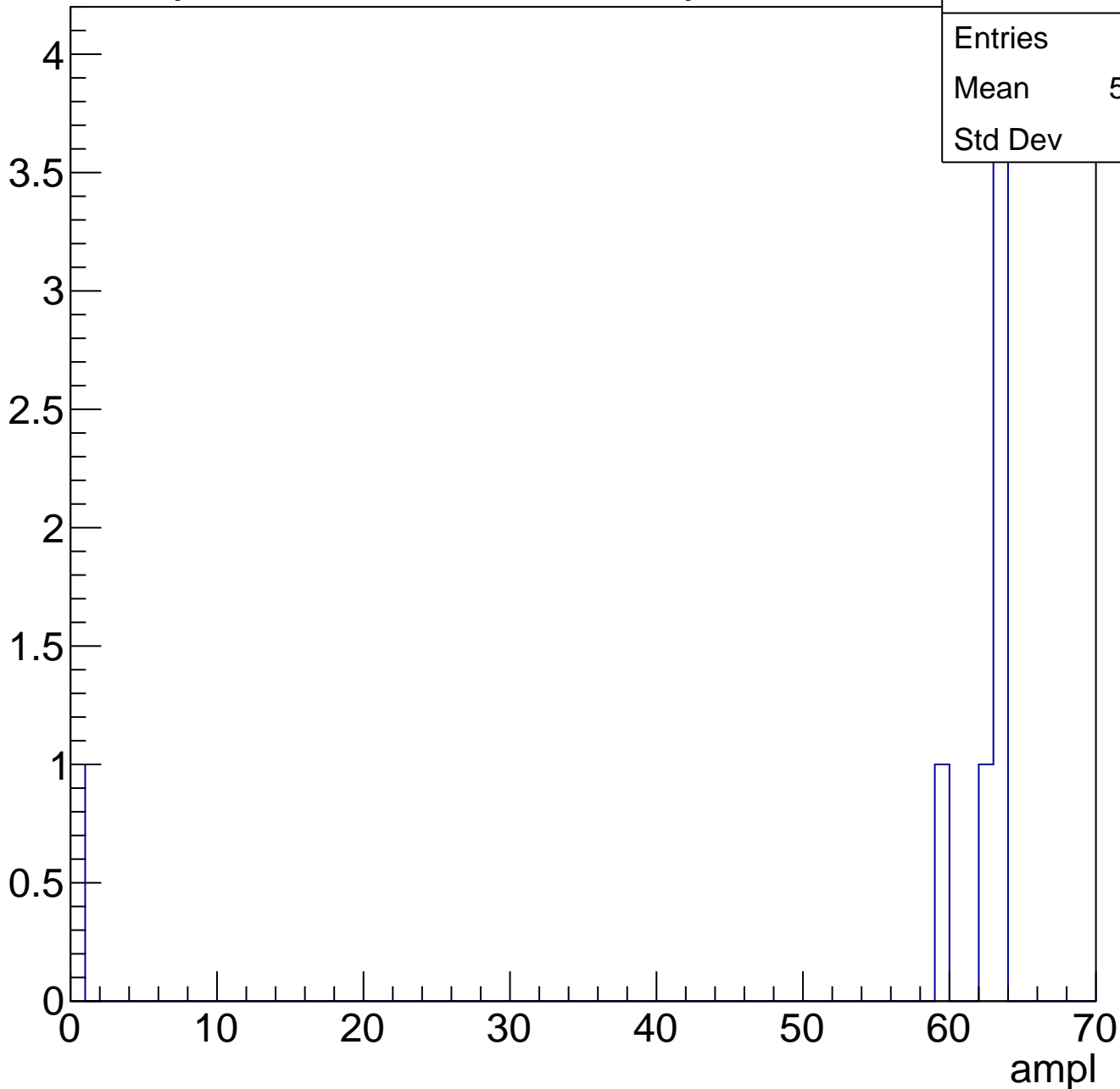
ampl



B0L101S, U25-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	7
Mean	53.29
Std Dev	21.8

B0L101S, U25-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch55, adc0

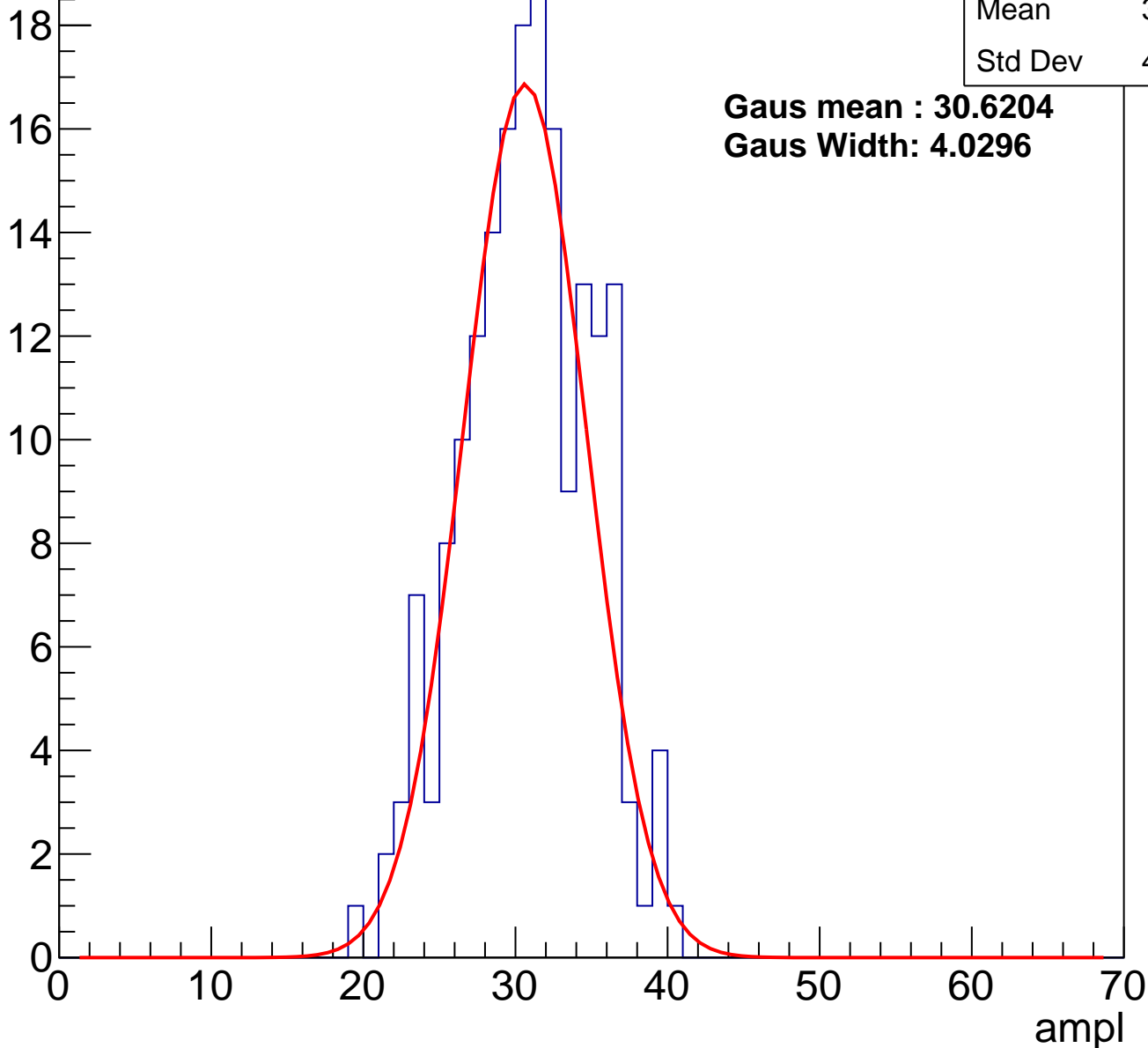
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	30.34
Std Dev	4.172

Gaus mean : 30.6204

Gaus Width: 4.0296

Entry



B0L101S, U25-ch55, adc1

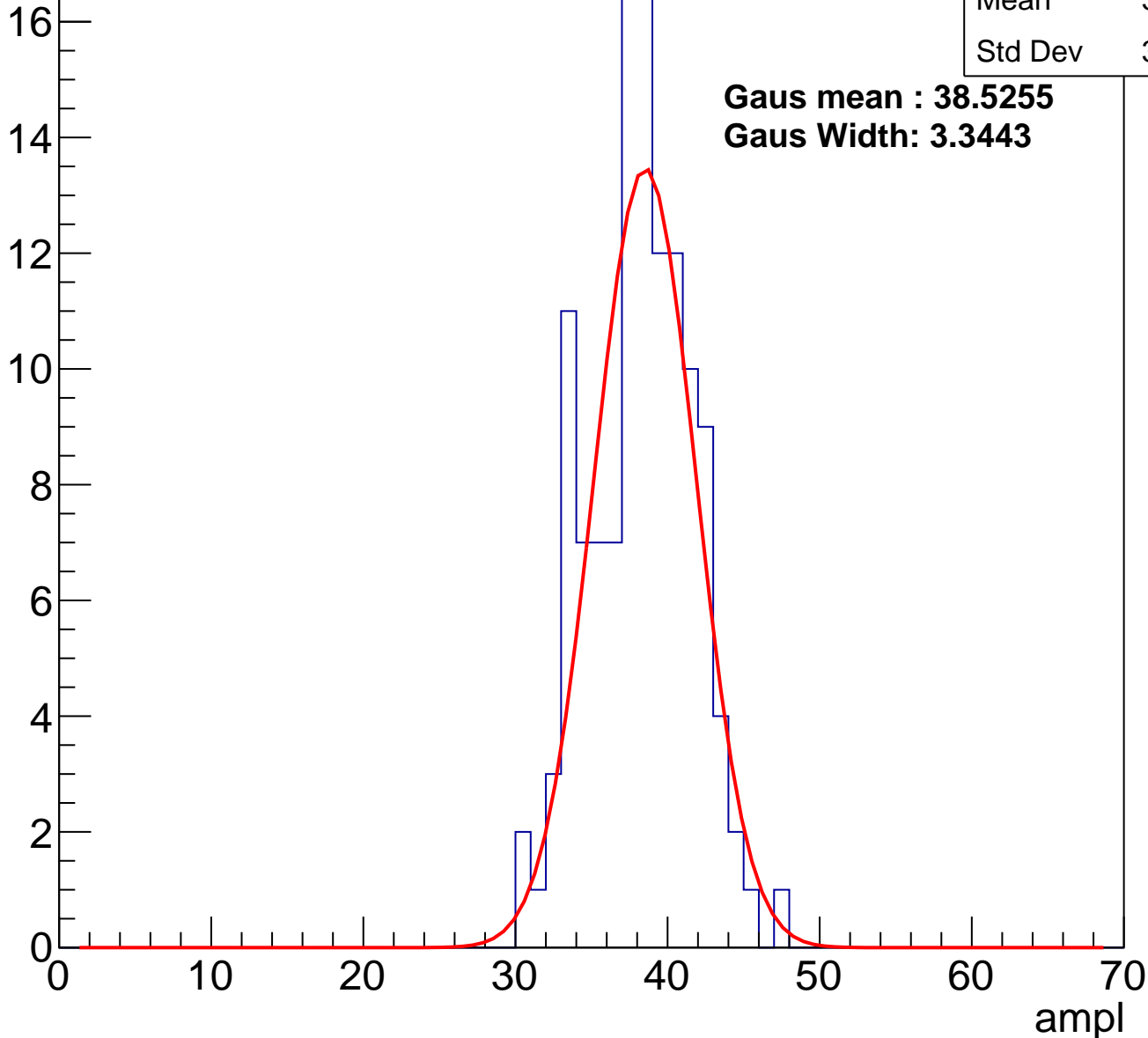
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	123
Mean	37.79
Std Dev	3.338

Gaus mean : 38.5255

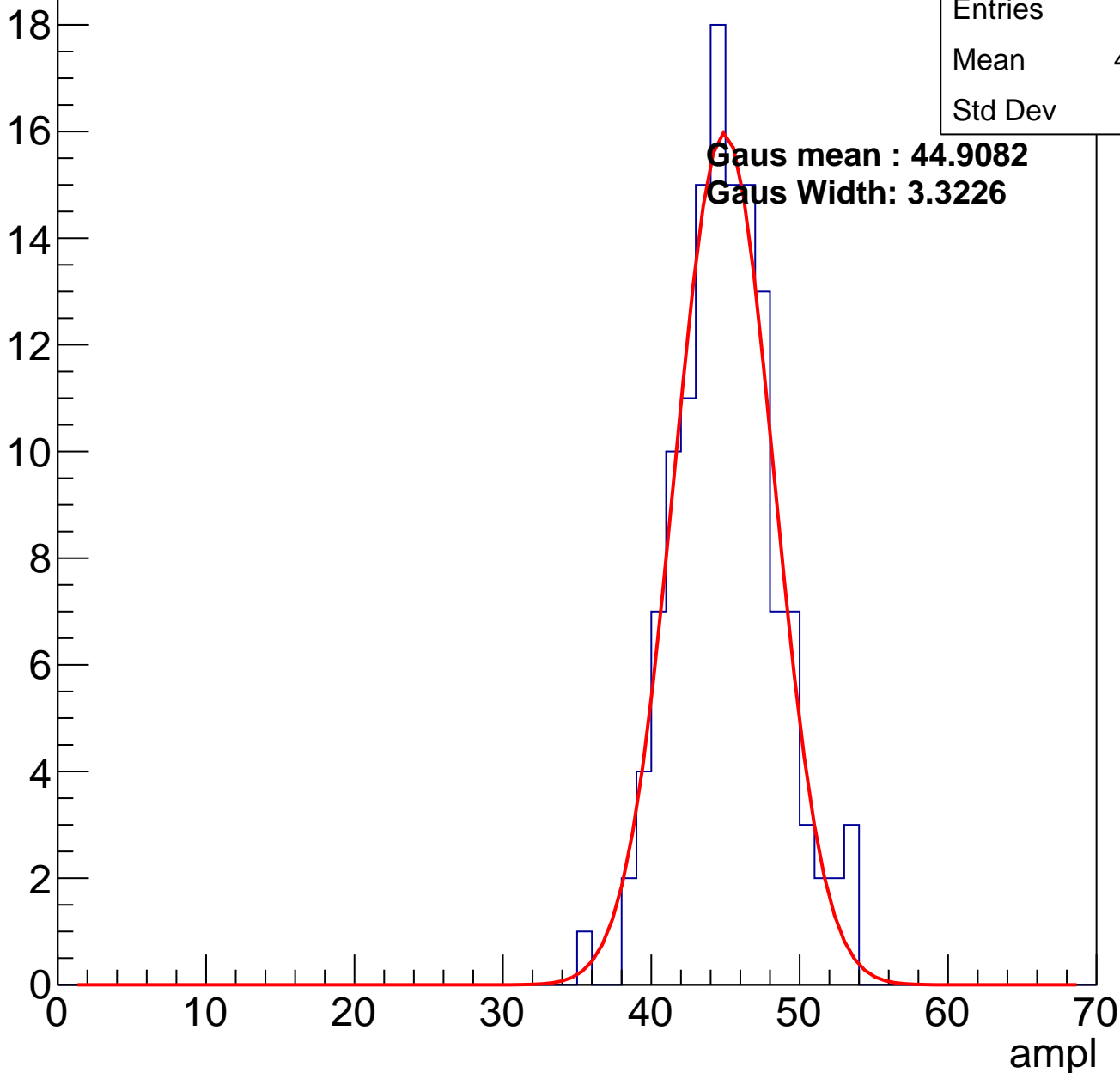
Gaus Width: 3.3443



B0L101S, U25-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

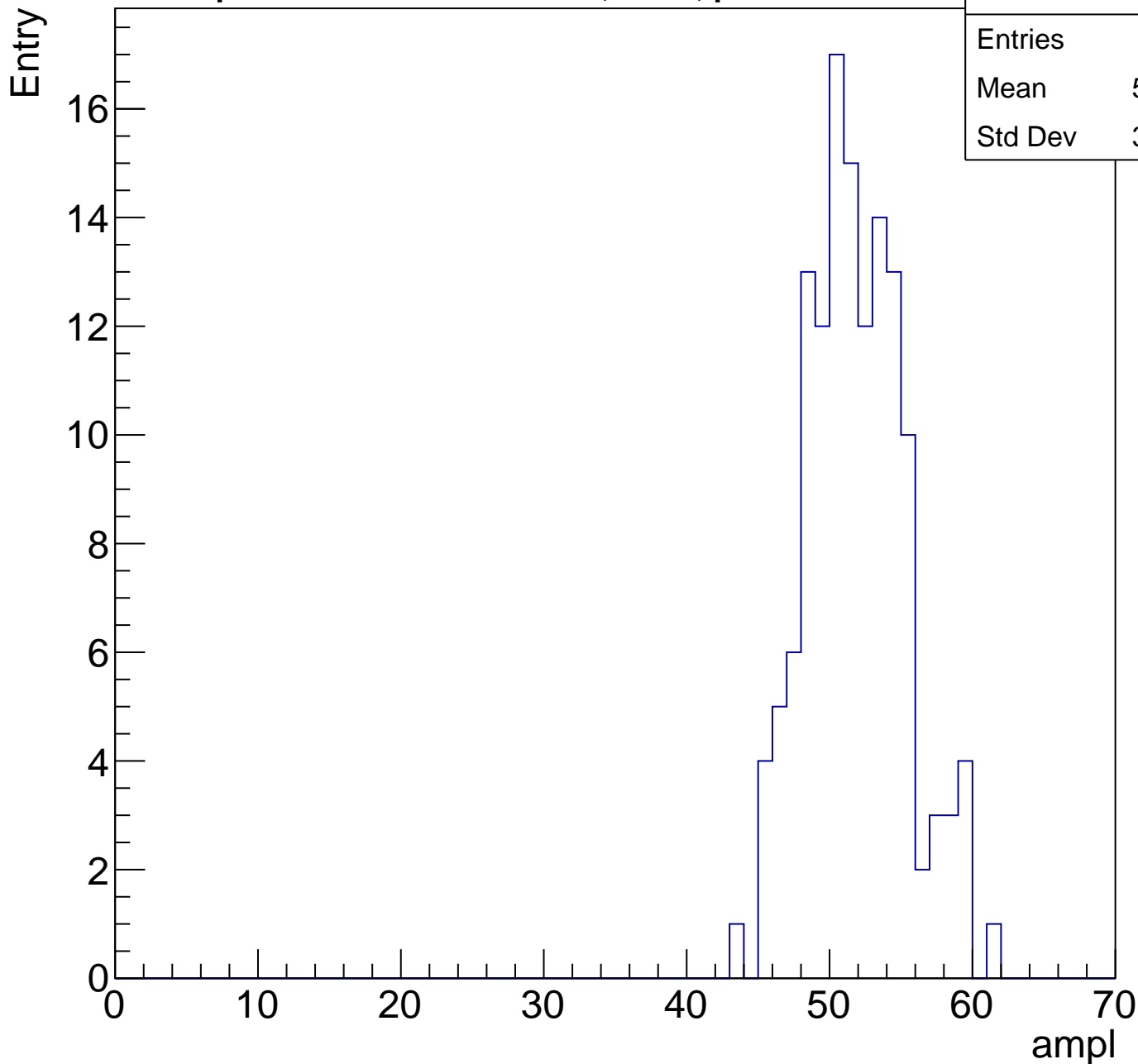
Entry



B0L101S, U25-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	51.36
Std Dev	3.442



B0L101S, U25-ch55, adc4

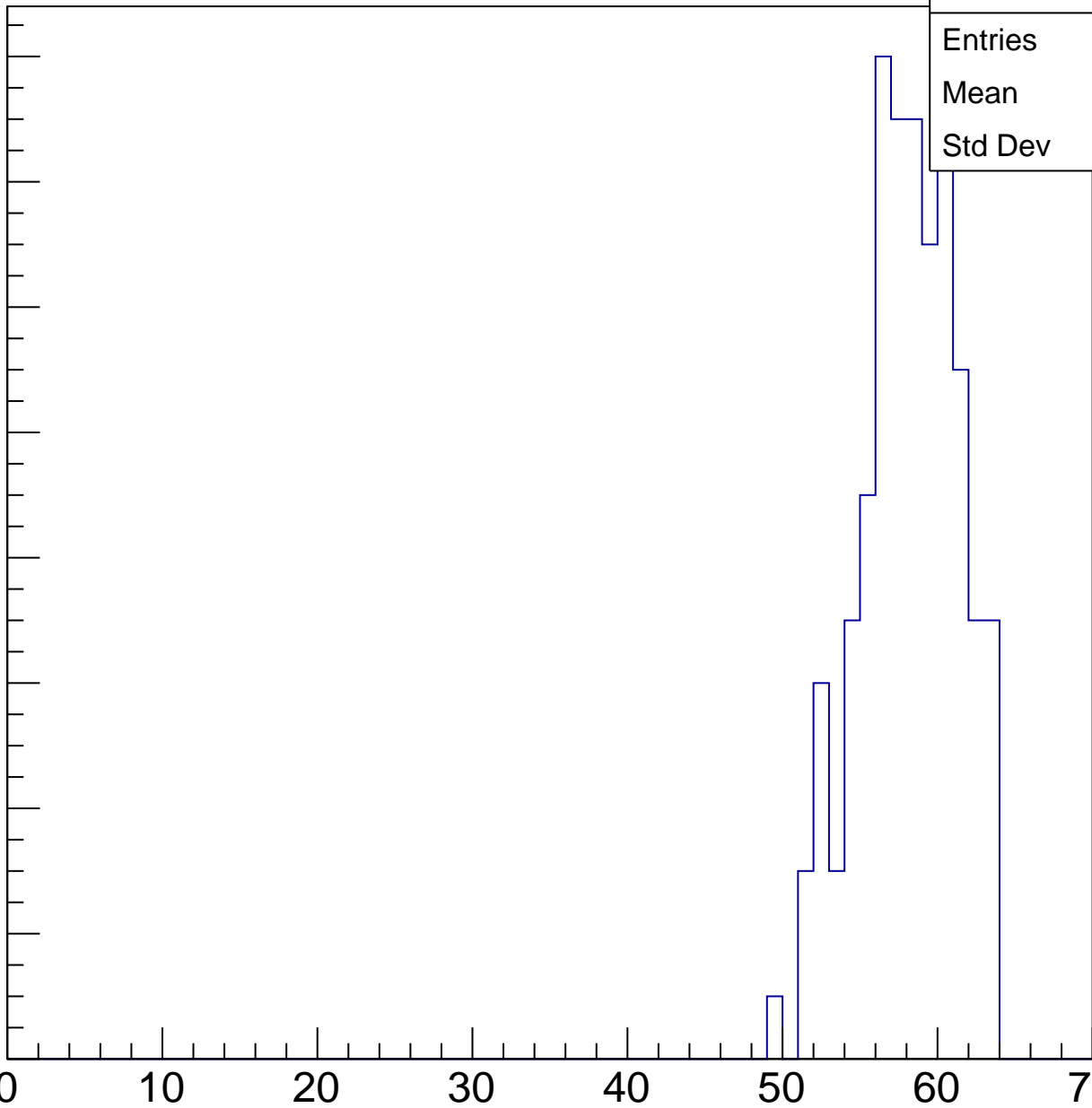
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	57.67
Std Dev	3.101

ampl



B0L101S, U25-ch55, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	50
Mean	58.54
Std Dev	12.13

ampl

0

10

20

30

40

50

60

70

B0L101S, U25-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch56, adc0

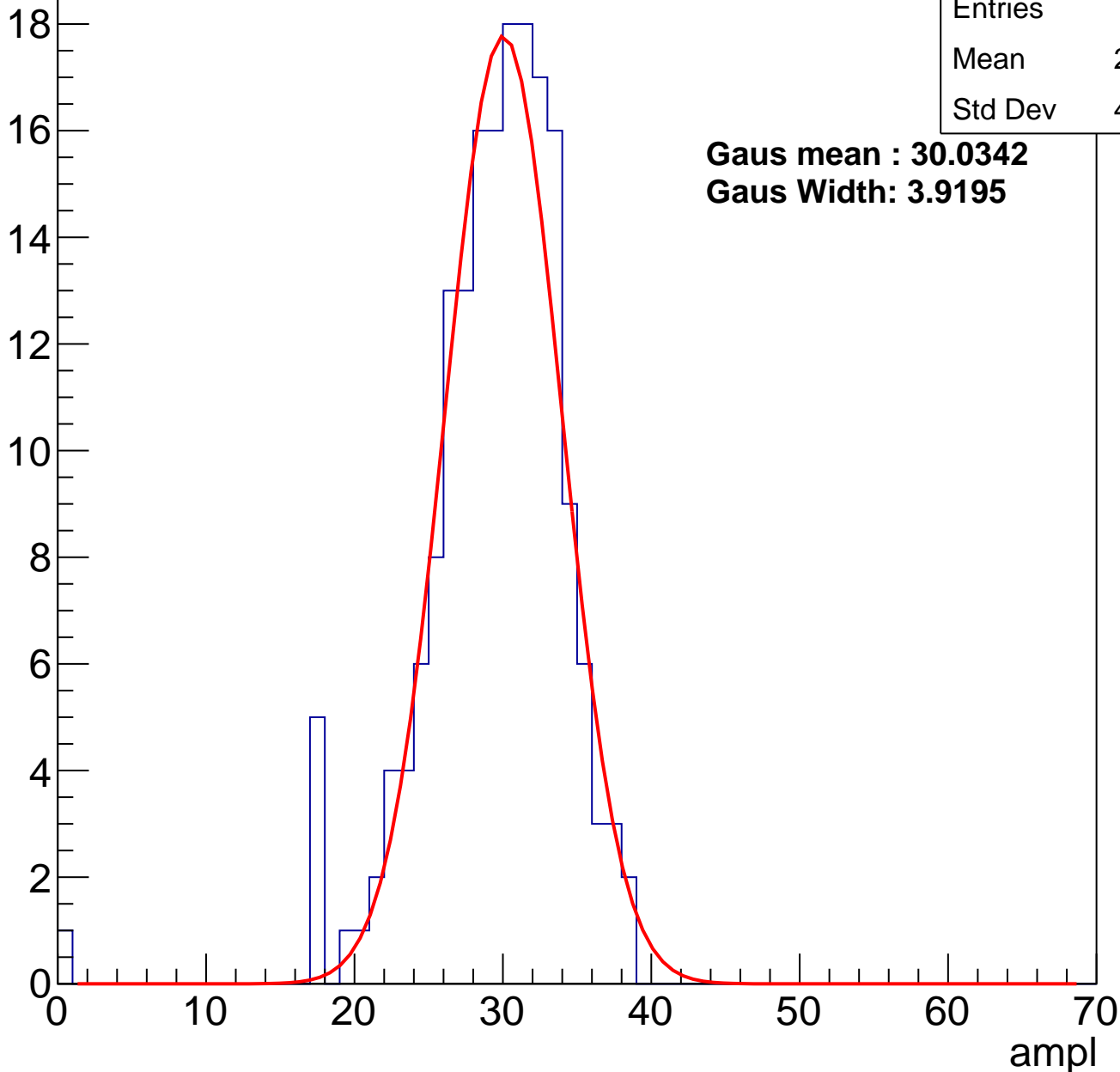
calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	28.97
Std Dev	4.754

Gaus mean : 30.0342

Gaus Width: 3.9195

Entry



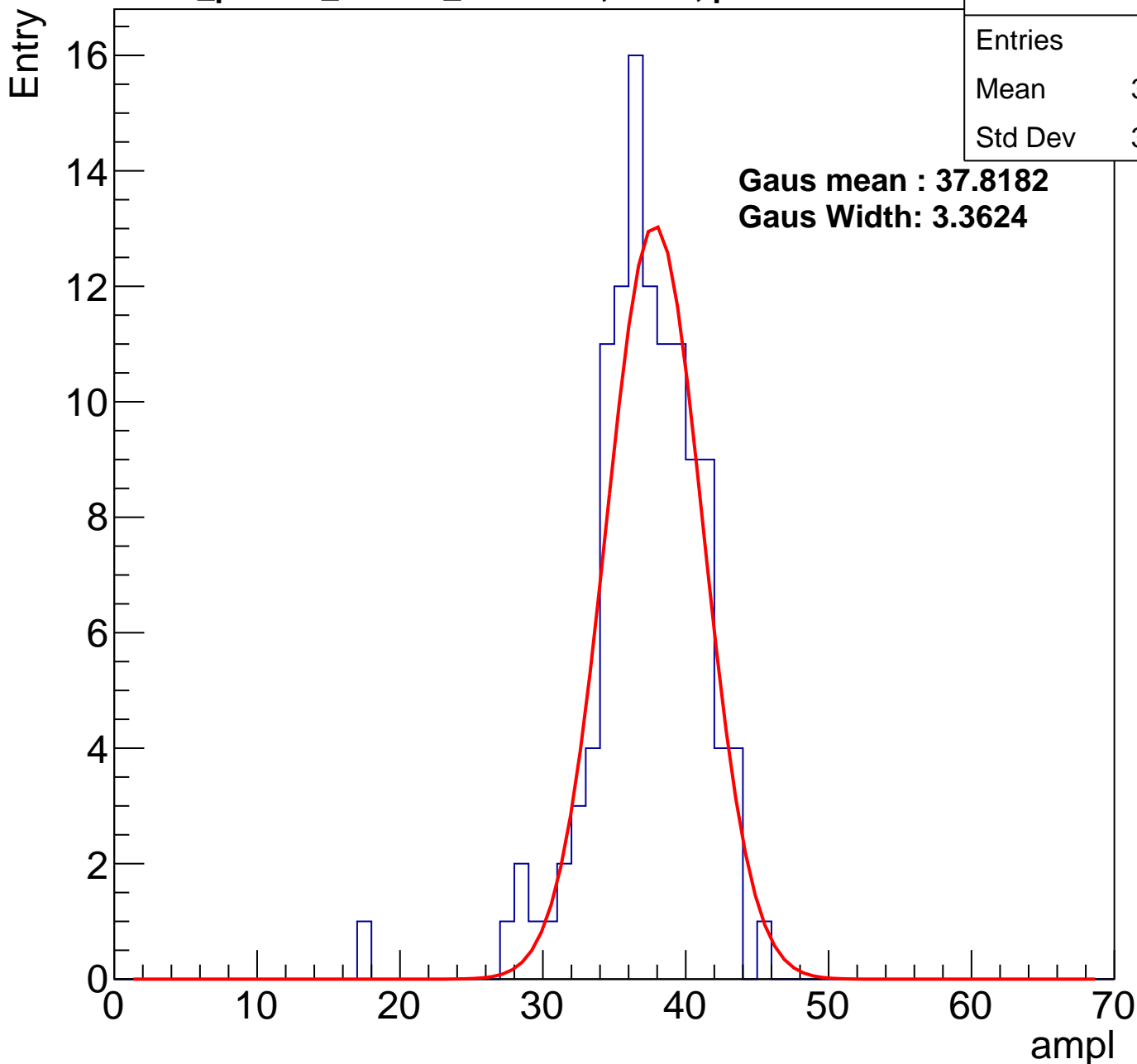
B0L101S, U25-ch56, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	36.73
Std Dev	3.877

Gaus mean : 37.8182

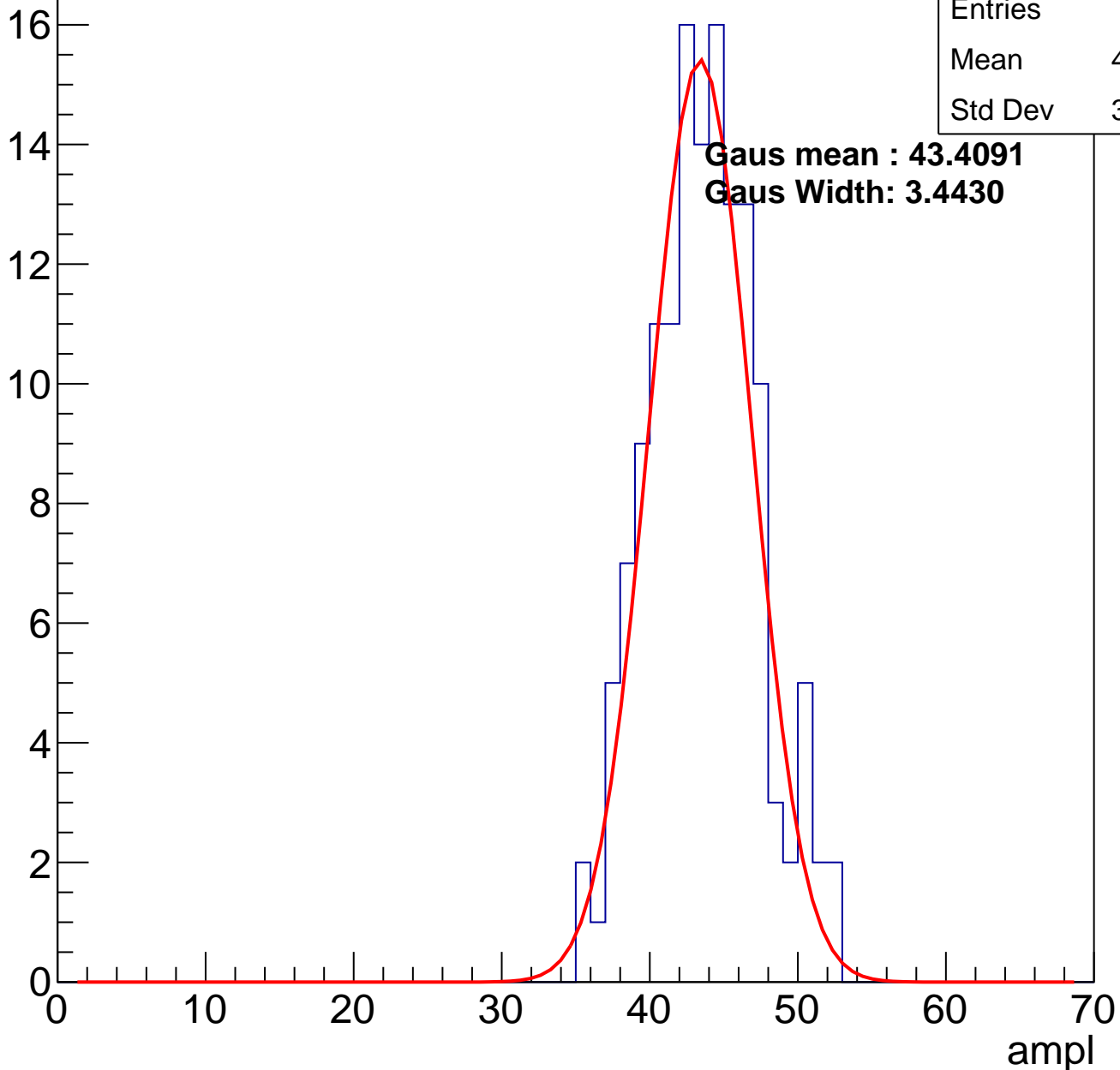
Gaus Width: 3.3624



B0L101S, U25-ch56, adc2

calib_packv5_042523_0143.root, FC#1, port C1

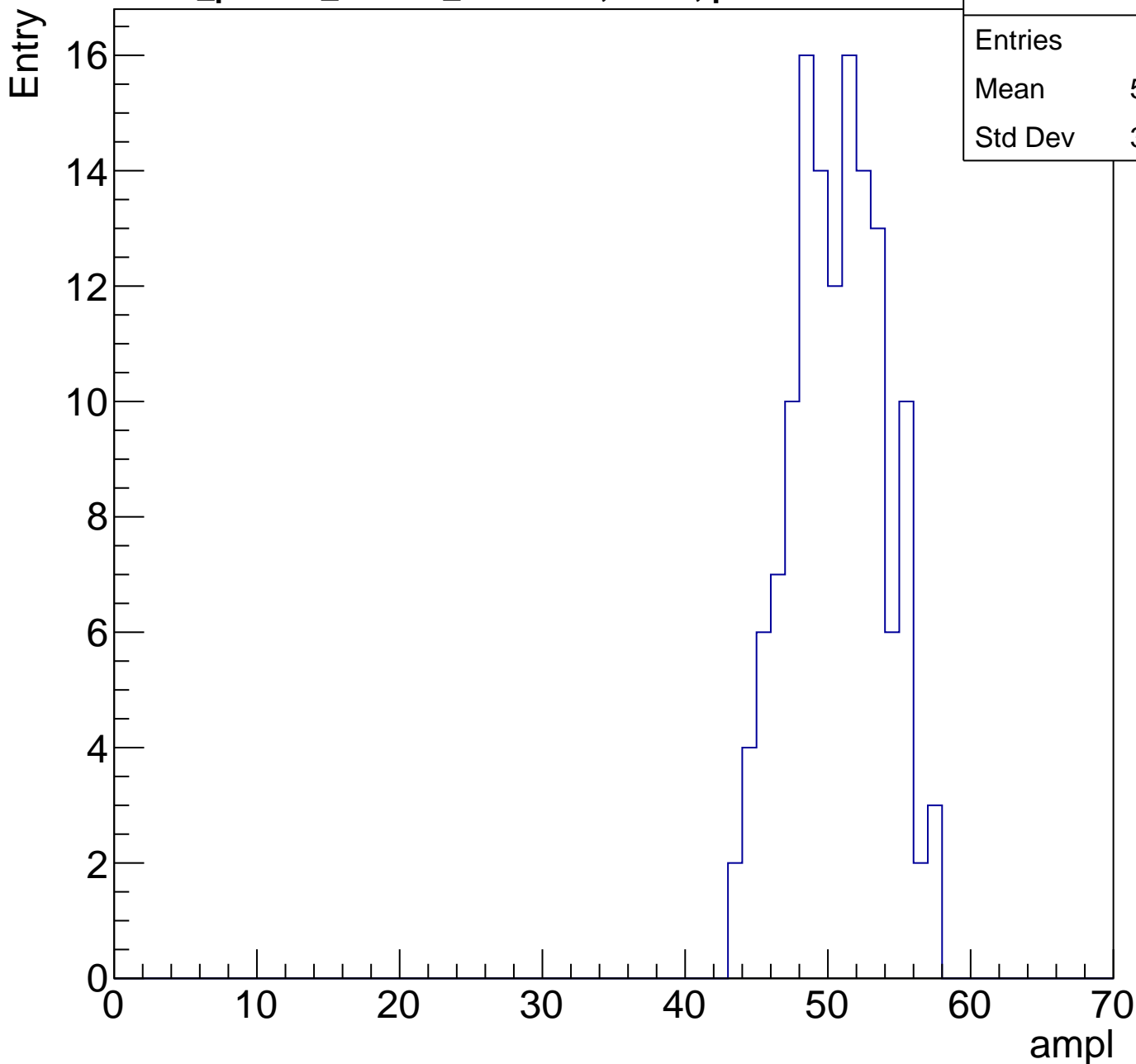
Entry



B0L101S, U25-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	50.13
Std Dev	3.247



B0L101S, U25-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

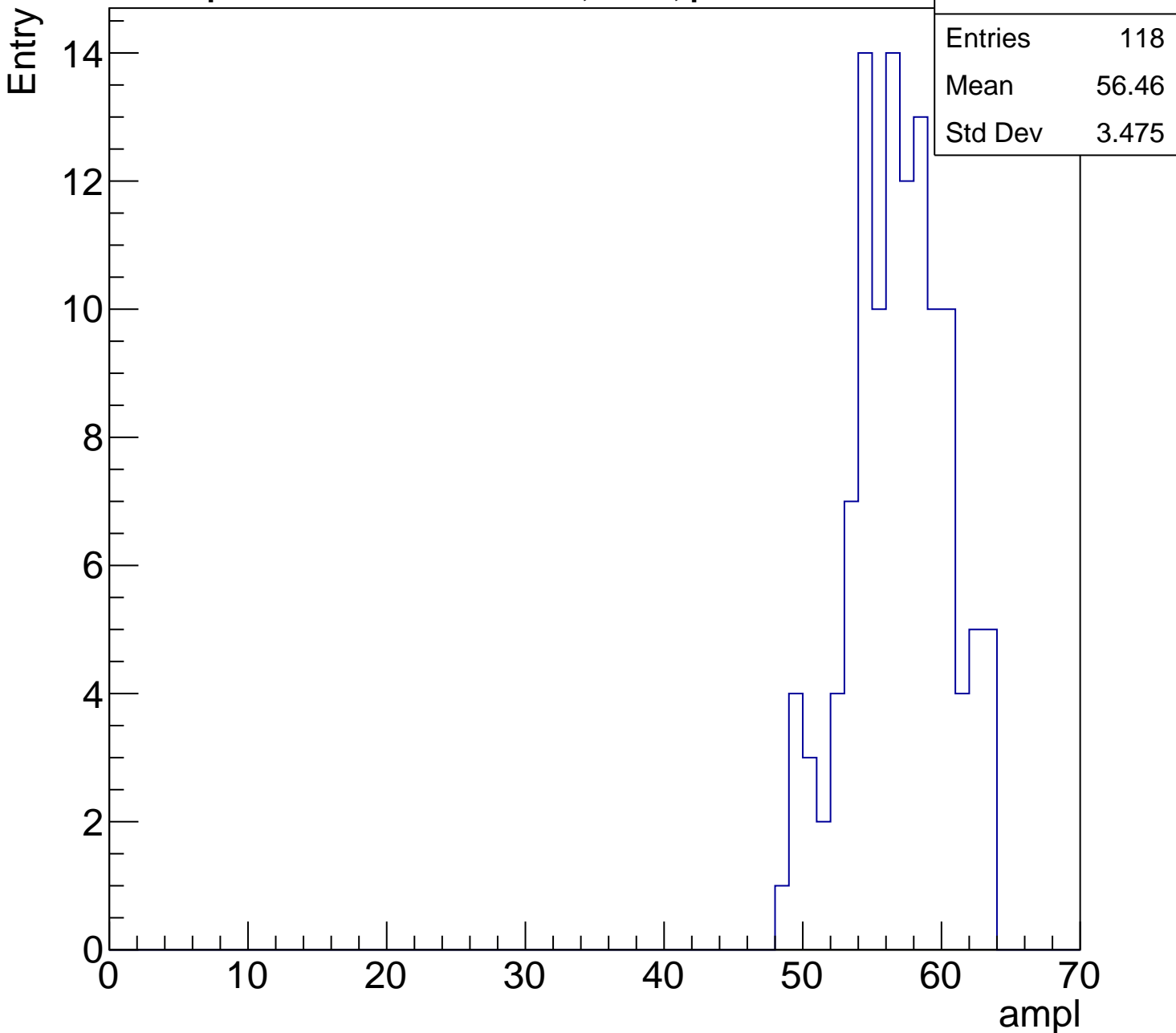
Entries	118
Mean	56.46
Std Dev	3.475

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch56, adc5

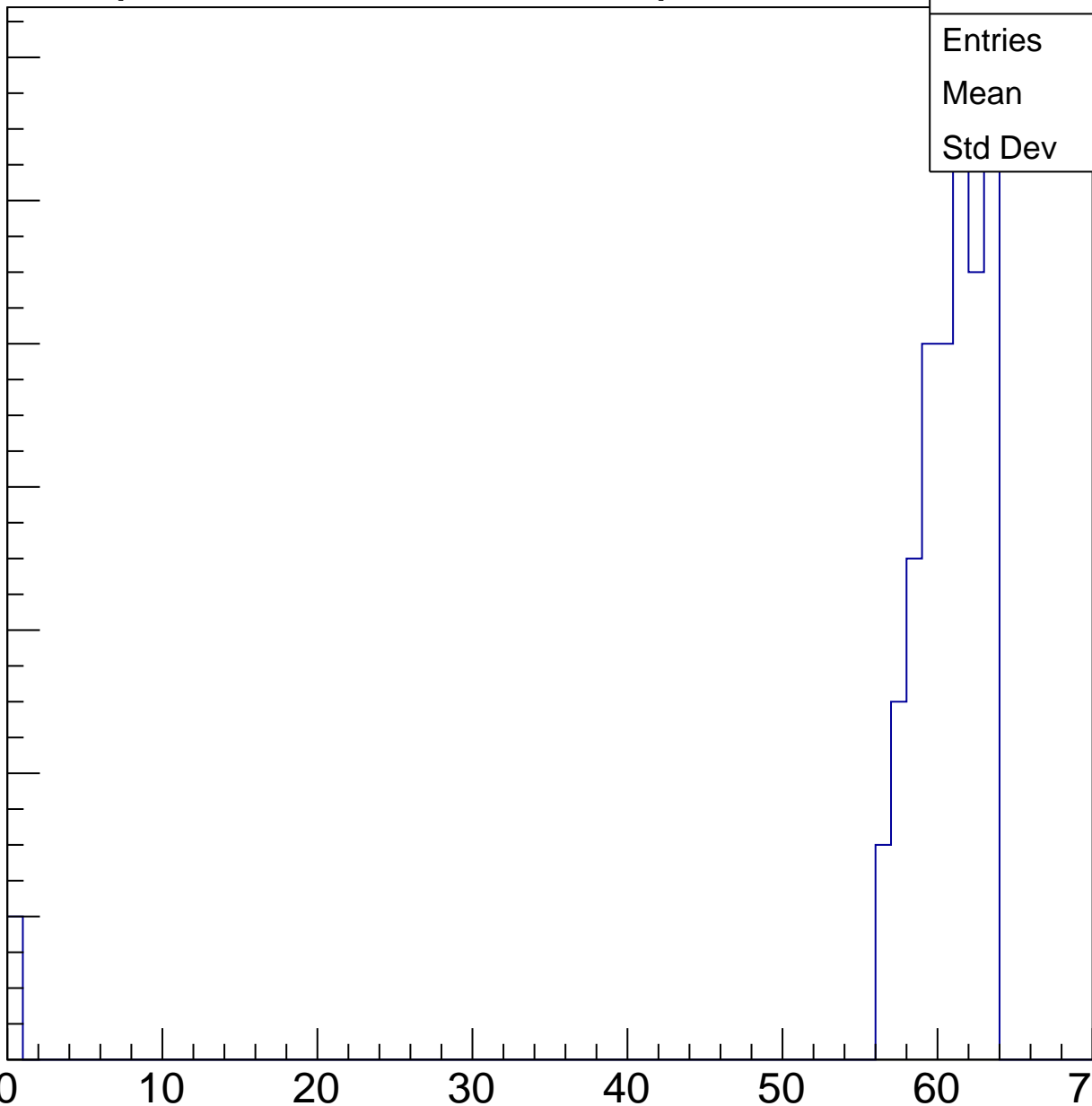
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	58.72
Std Dev	9.922

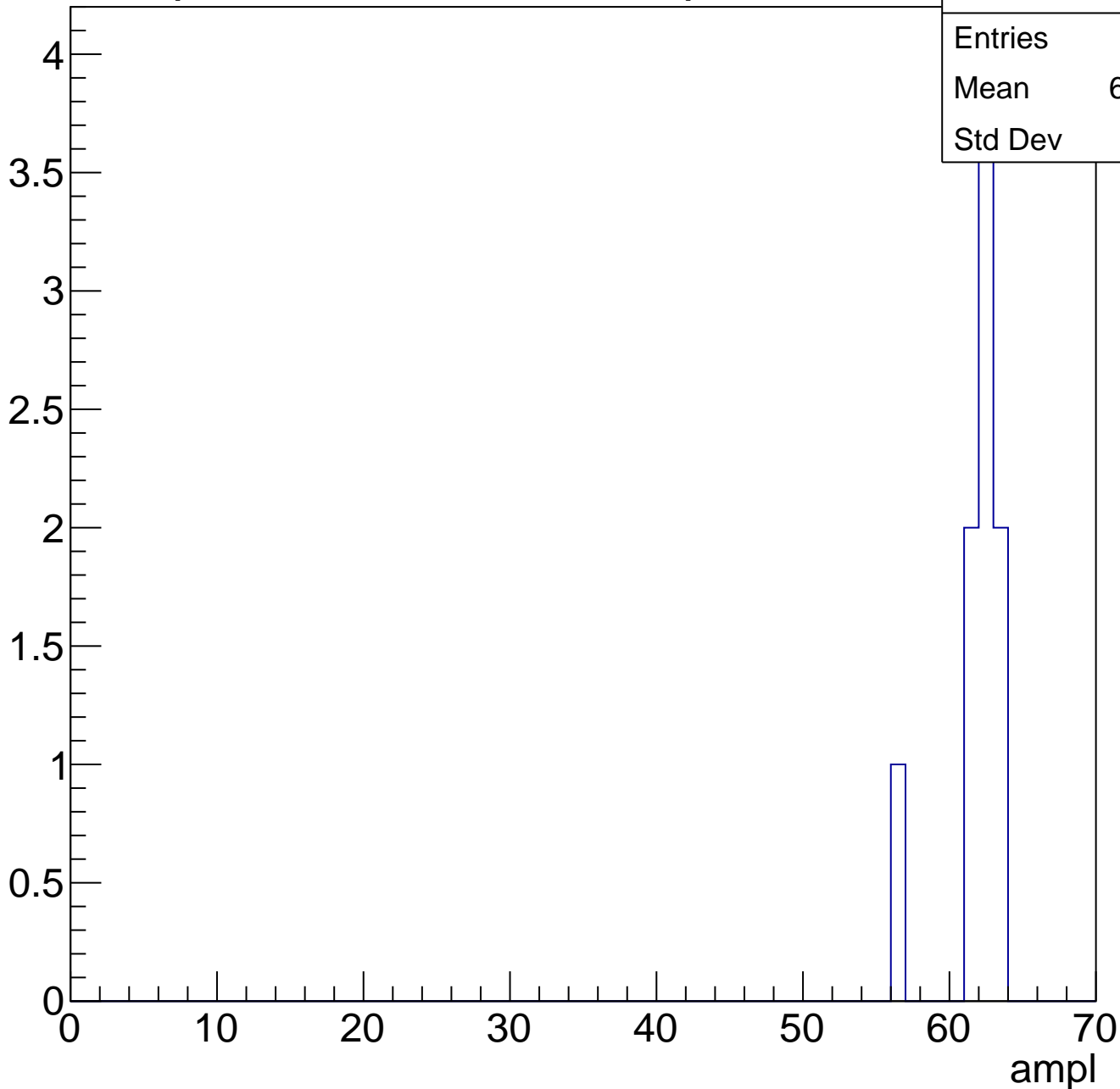
ampl



B0L101S, U25-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

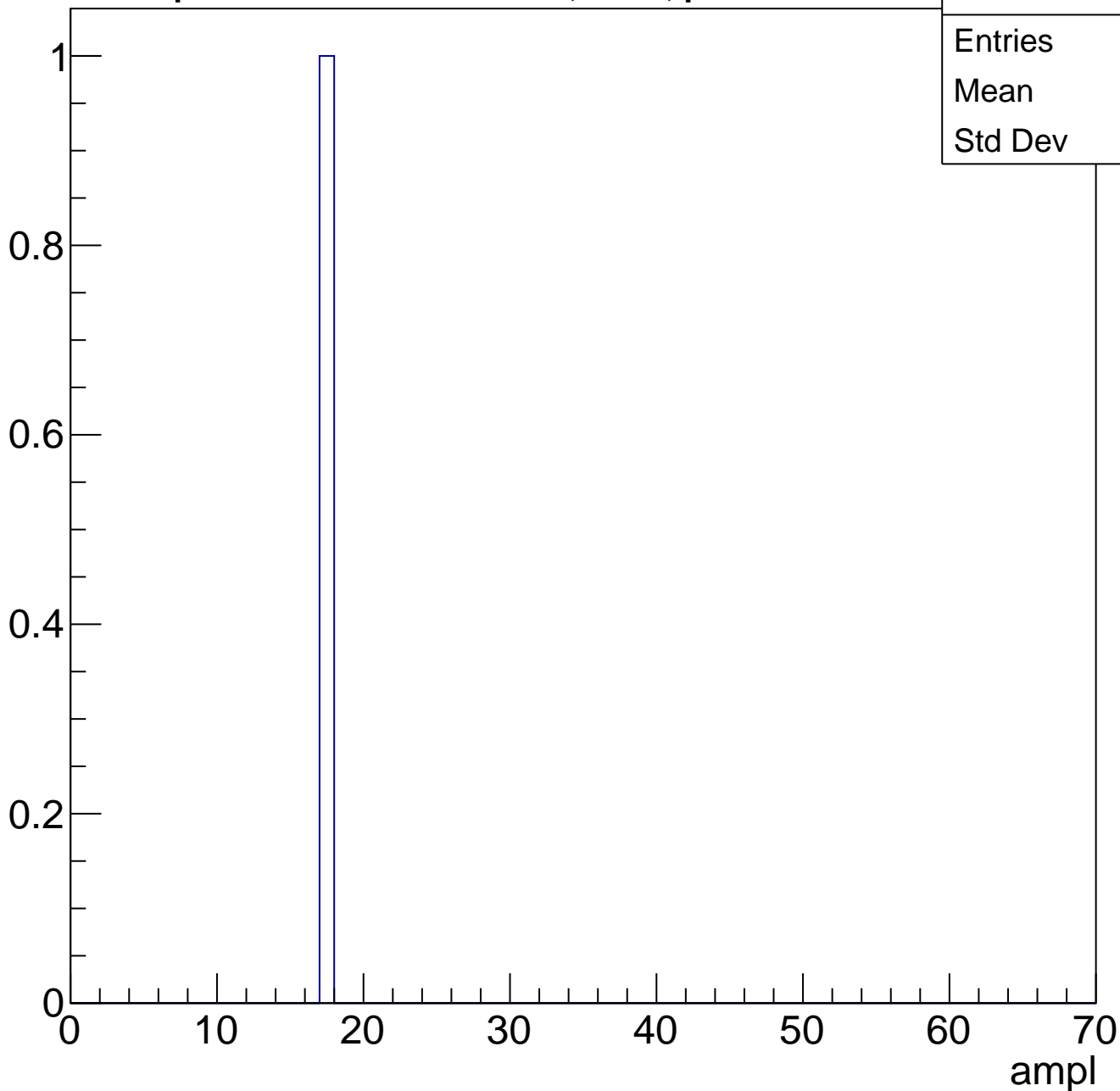
Entry



B0L101S, U25-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch57, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

153

Mean

29.99

Std Dev

3.416

Gaus mean : 30.3162

Gaus Width: 3.3439

0

10

20

30

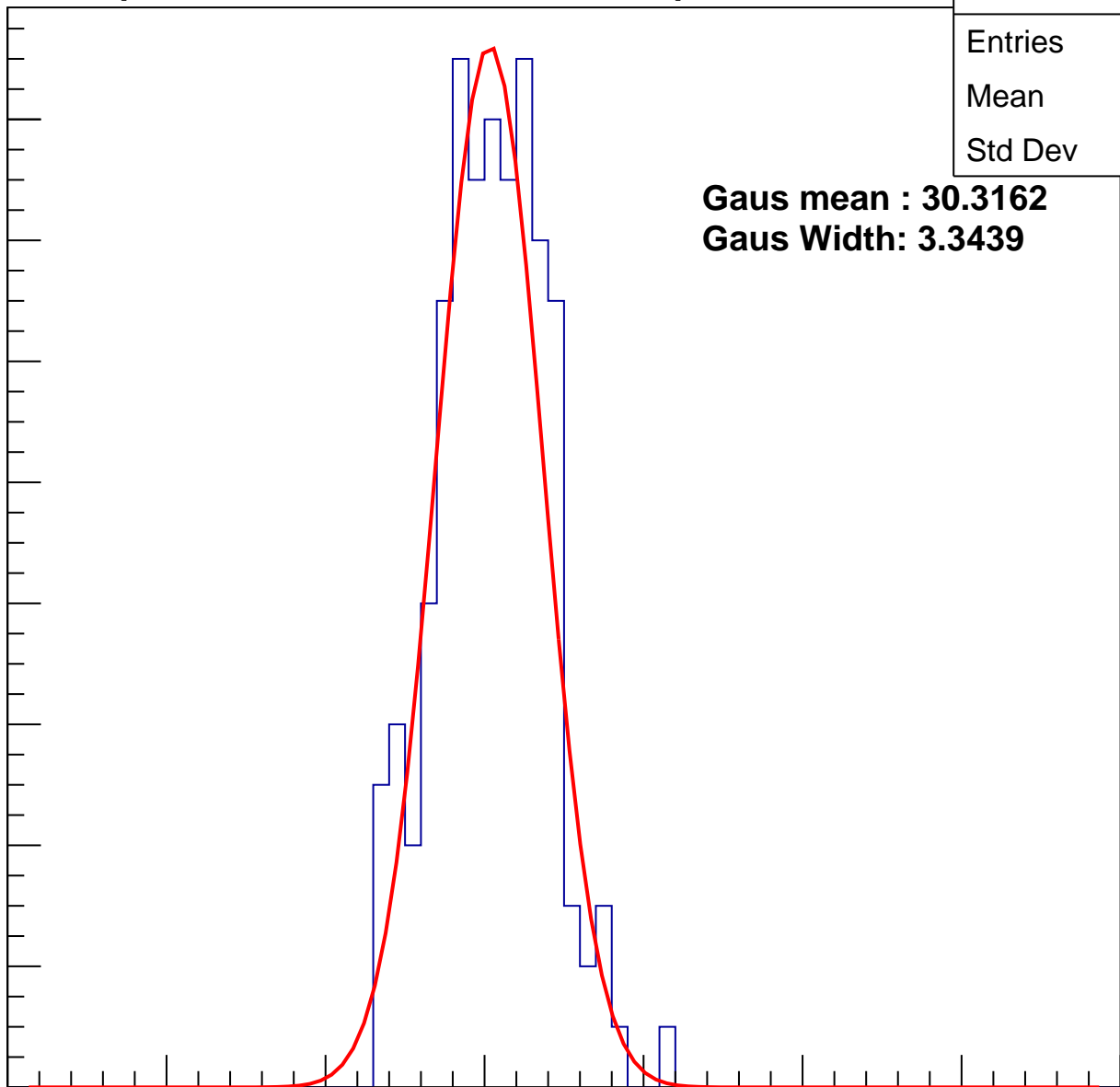
40

50

60

70

ampl



B0L101S, U25-ch57, adc1

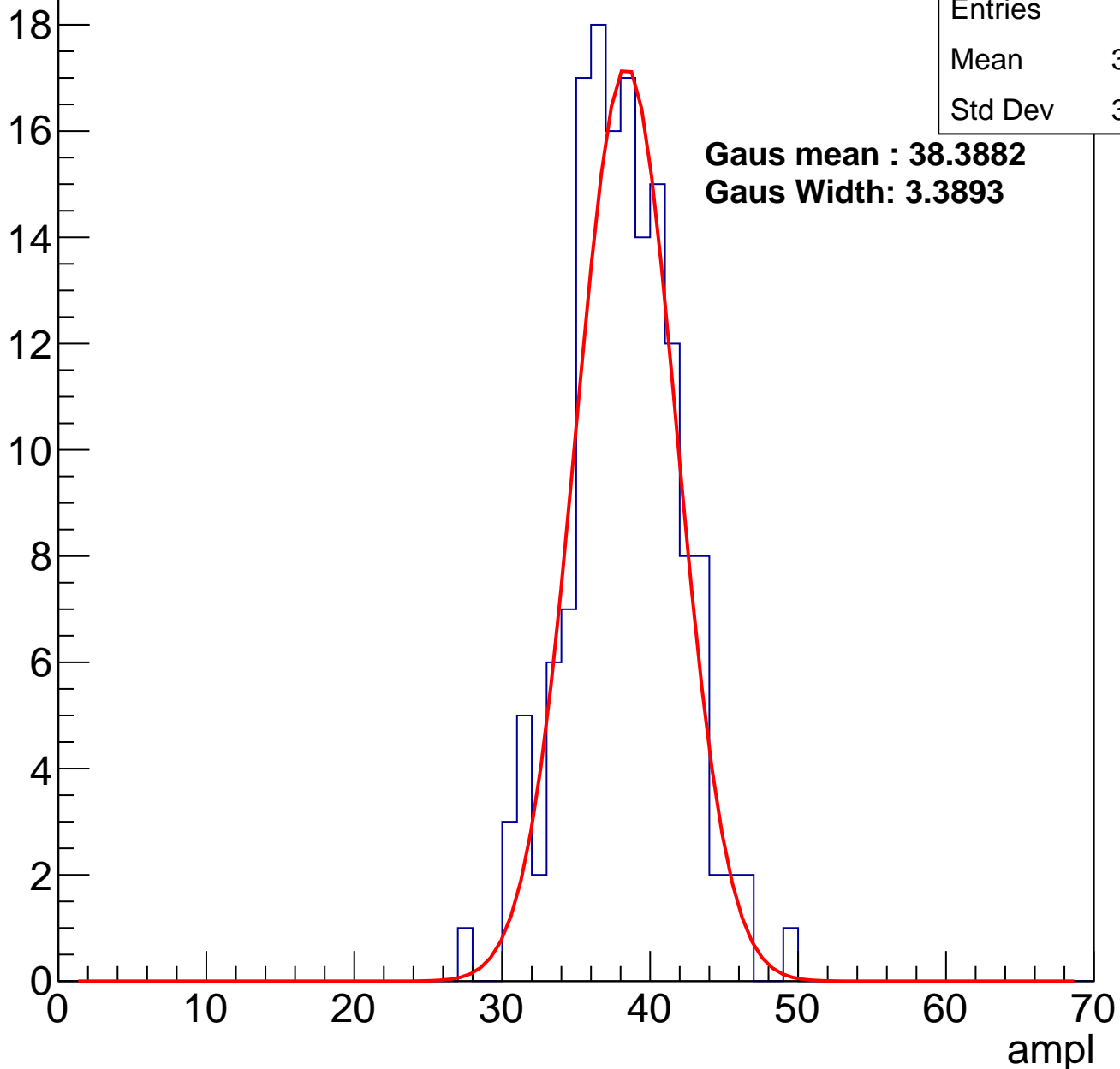
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	37.76
Std Dev	3.627

Gaus mean : 38.3882

Gaus Width: 3.3893

Entry



B0L101S, U25-ch57, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

142

Mean

45.27

Std Dev

3.627

Gaus mean : 45.5575

Gaus Width: 3.5442

0

10

20

30

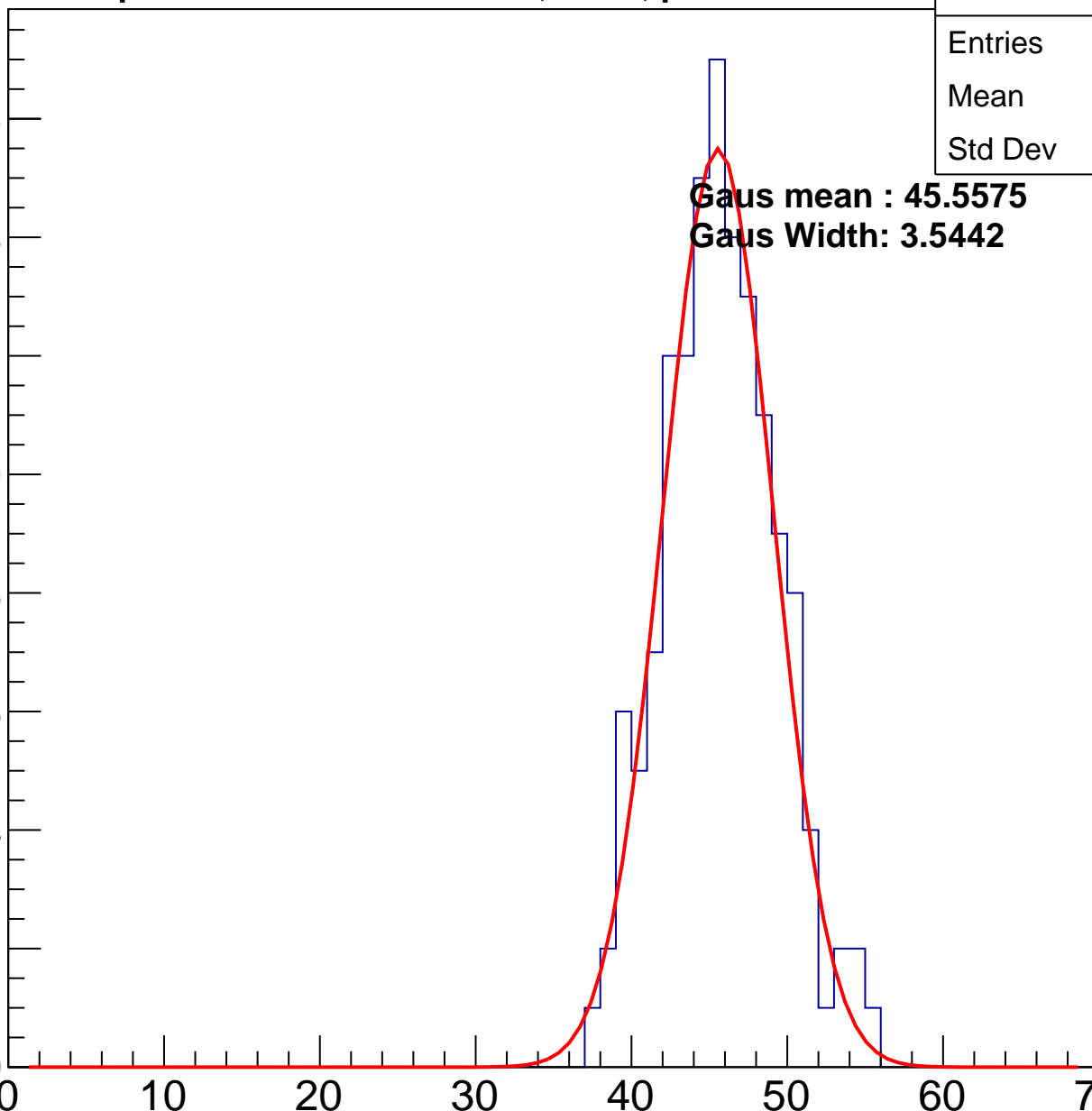
40

50

60

70

ampl

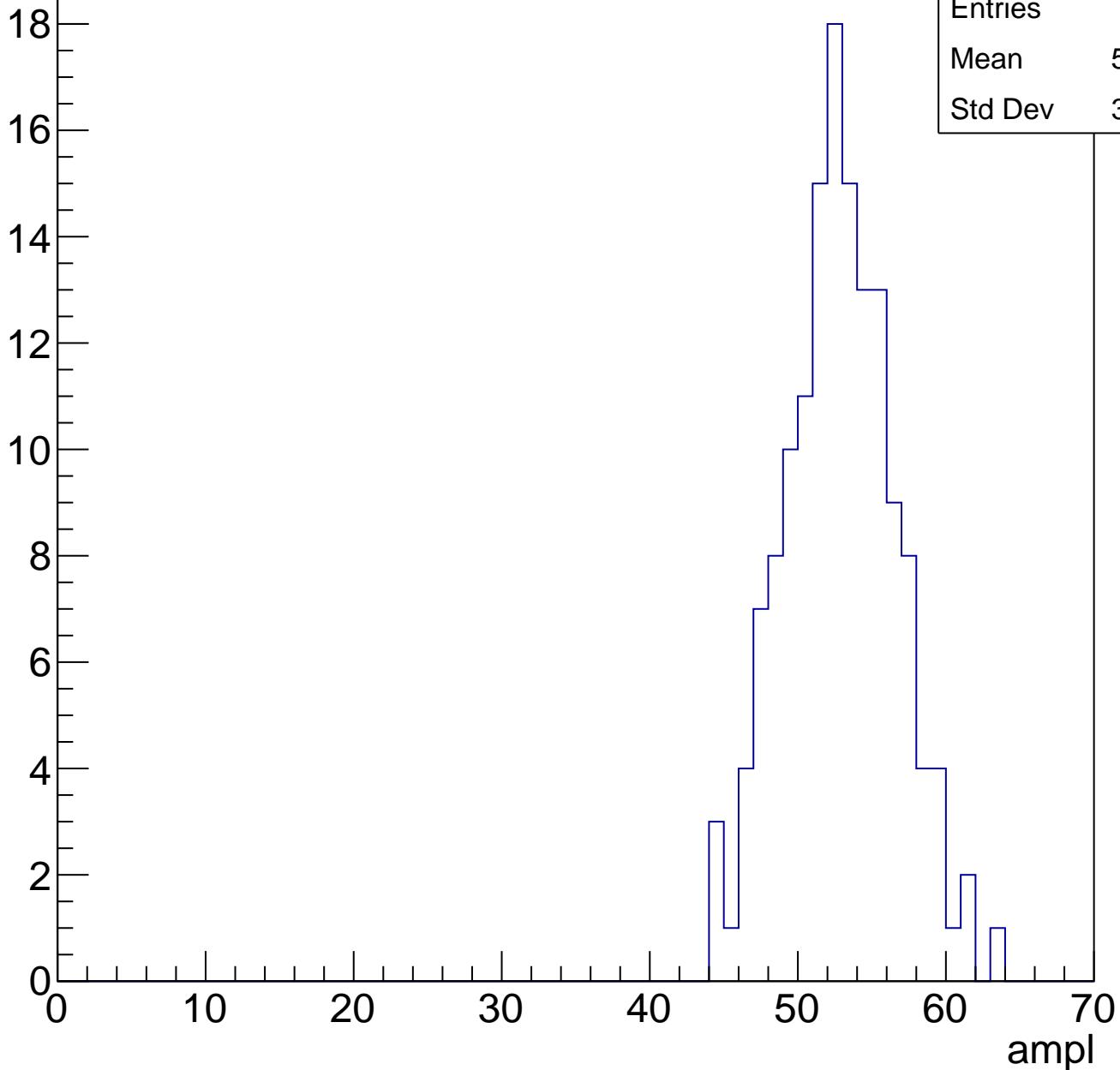


B0L101S, U25-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	52.38
Std Dev	3.717

Entry



B0L101S, U25-ch57, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

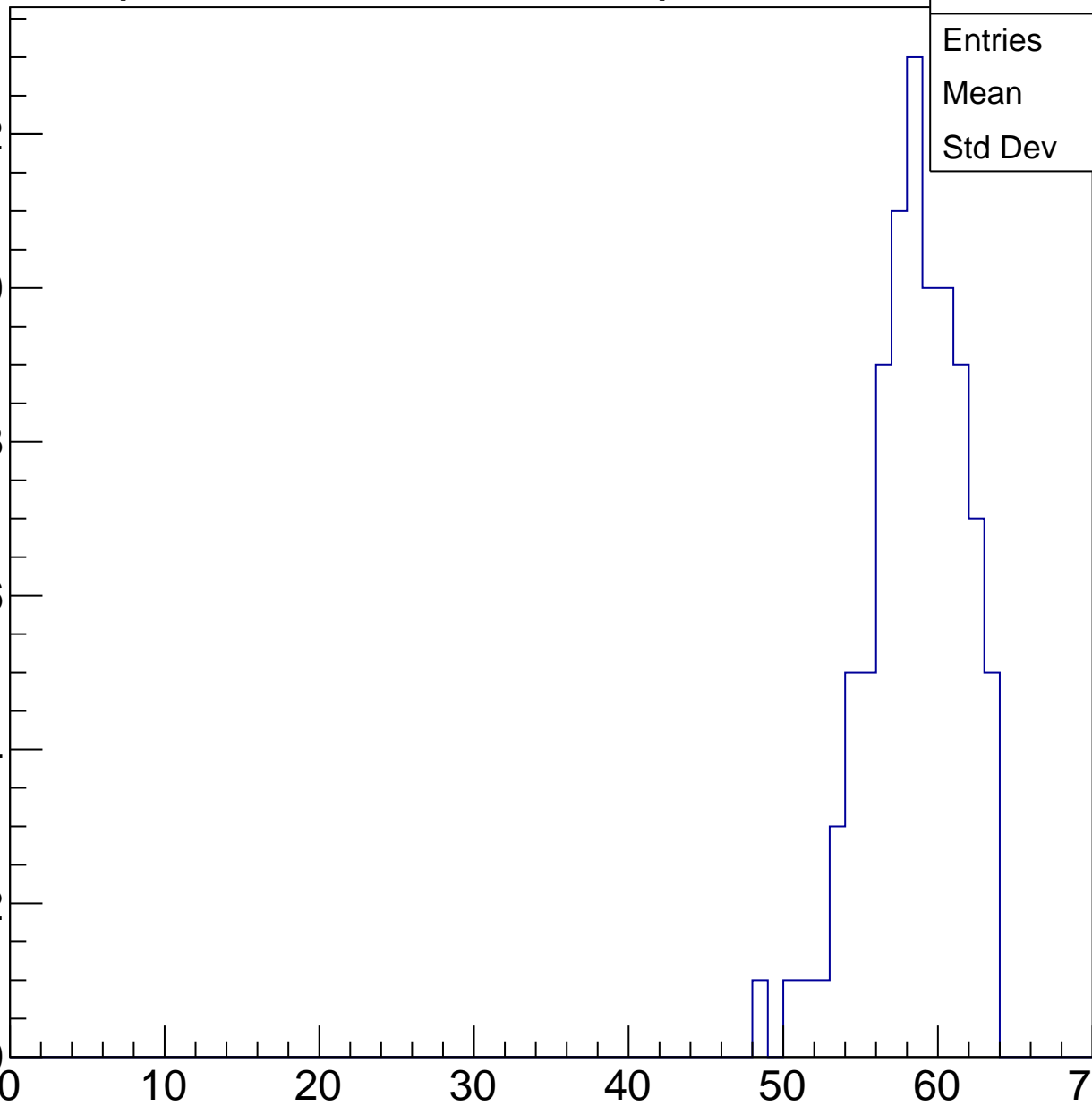
50

60

70

ampl

Entries	91
Mean	58
Std Dev	3.081



B0L101S, U25-ch57, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

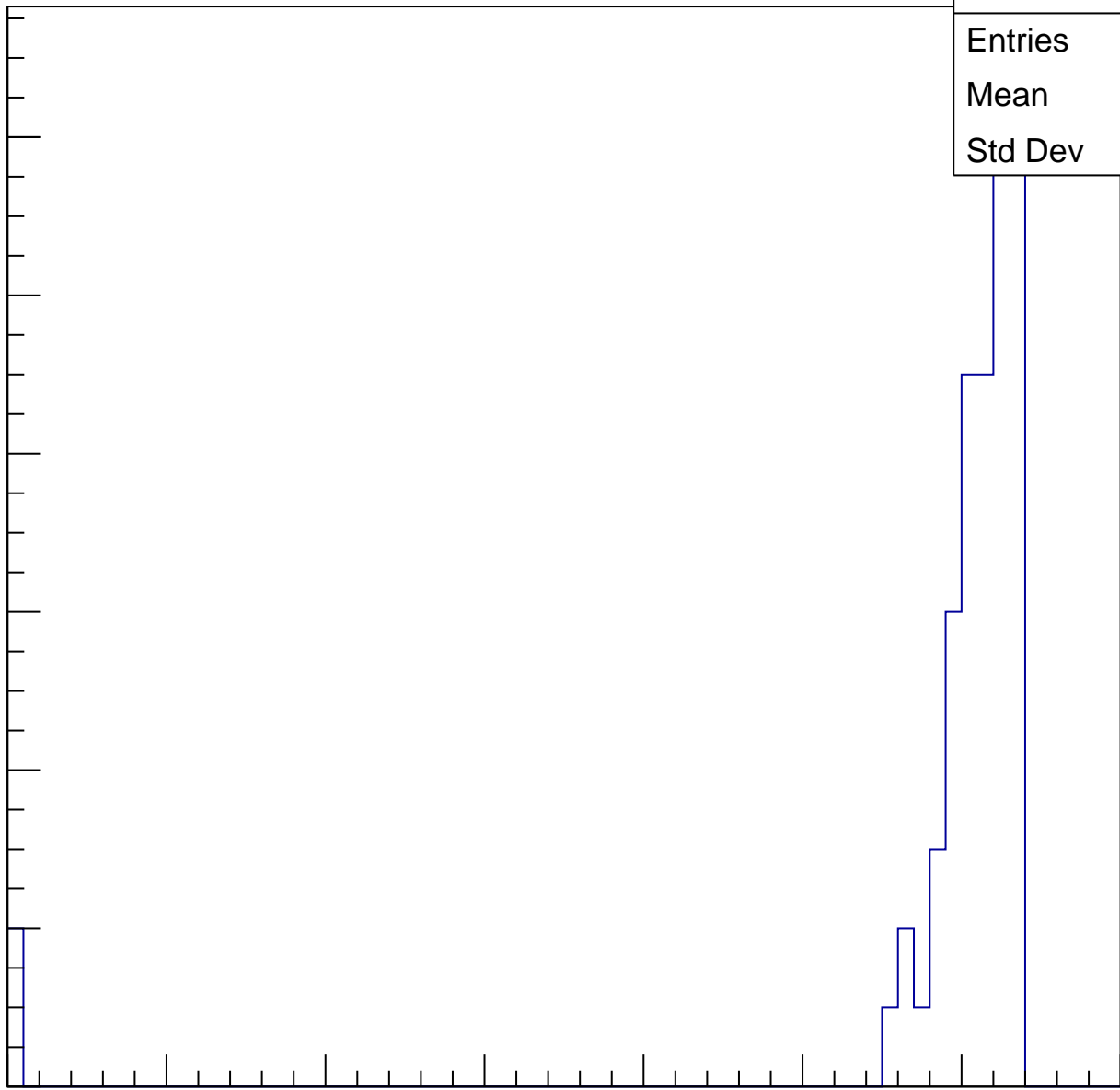
2

0

Entries	58
Mean	58.69
Std Dev	11.26

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

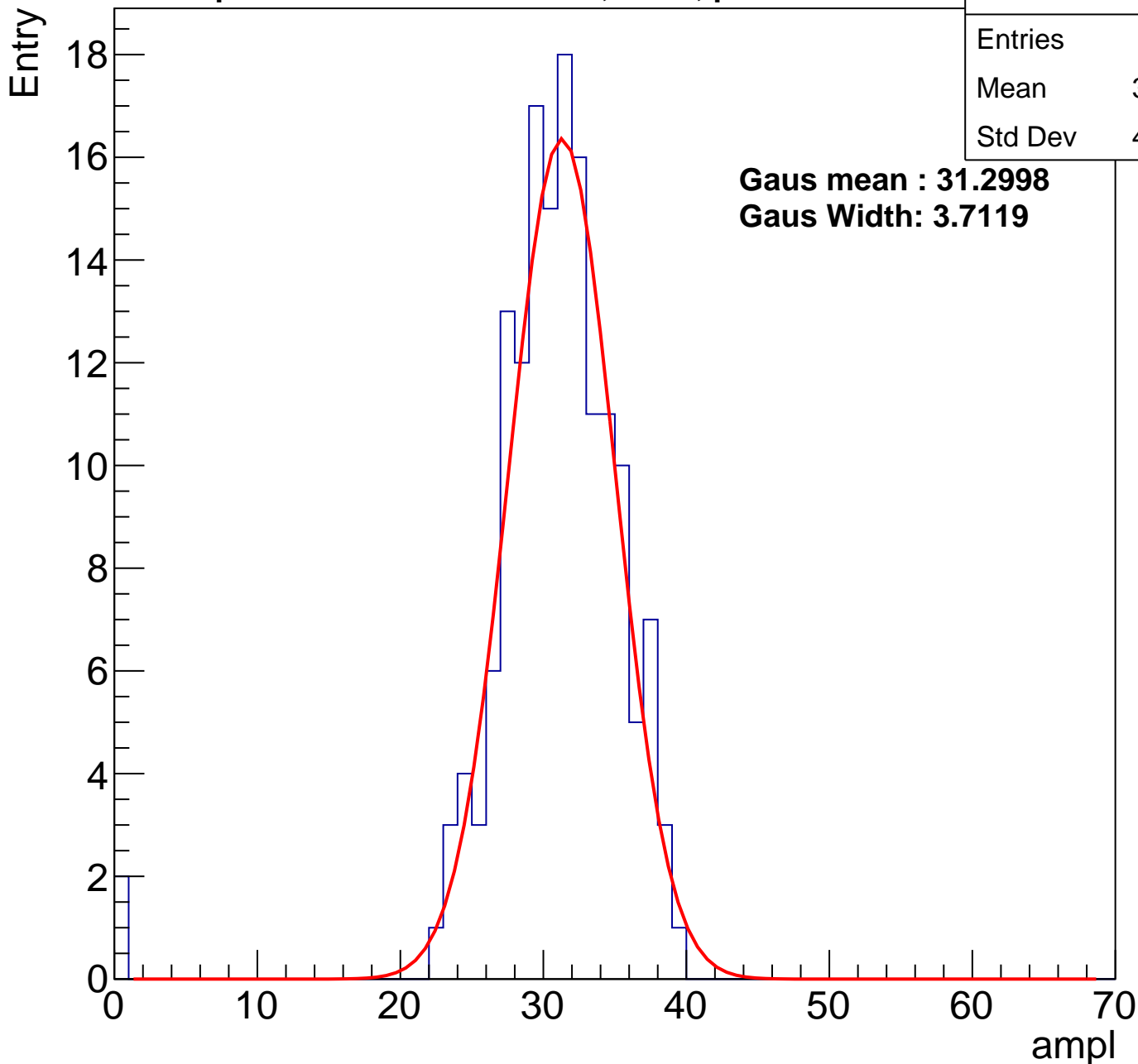
B0L101S, U25-ch58, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	30.36
Std Dev	4.962

Gaus mean : 31.2998

Gaus Width: 3.7119



B0L101S, U25-ch58, adc1

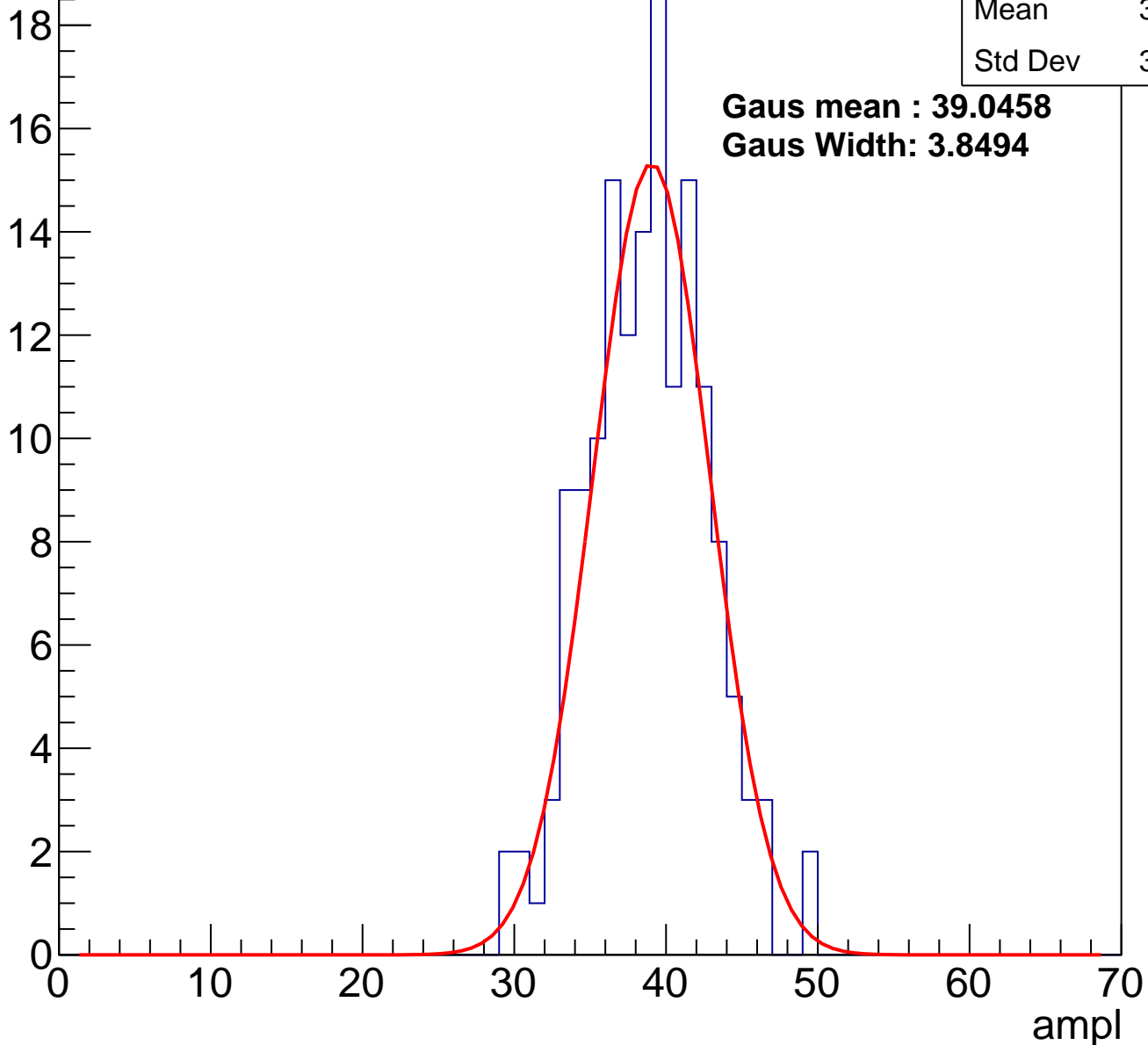
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	38.36
Std Dev	3.859

Gaus mean : 39.0458

Gaus Width: 3.8494

Entry



B0L101S, U25-ch58, adc2

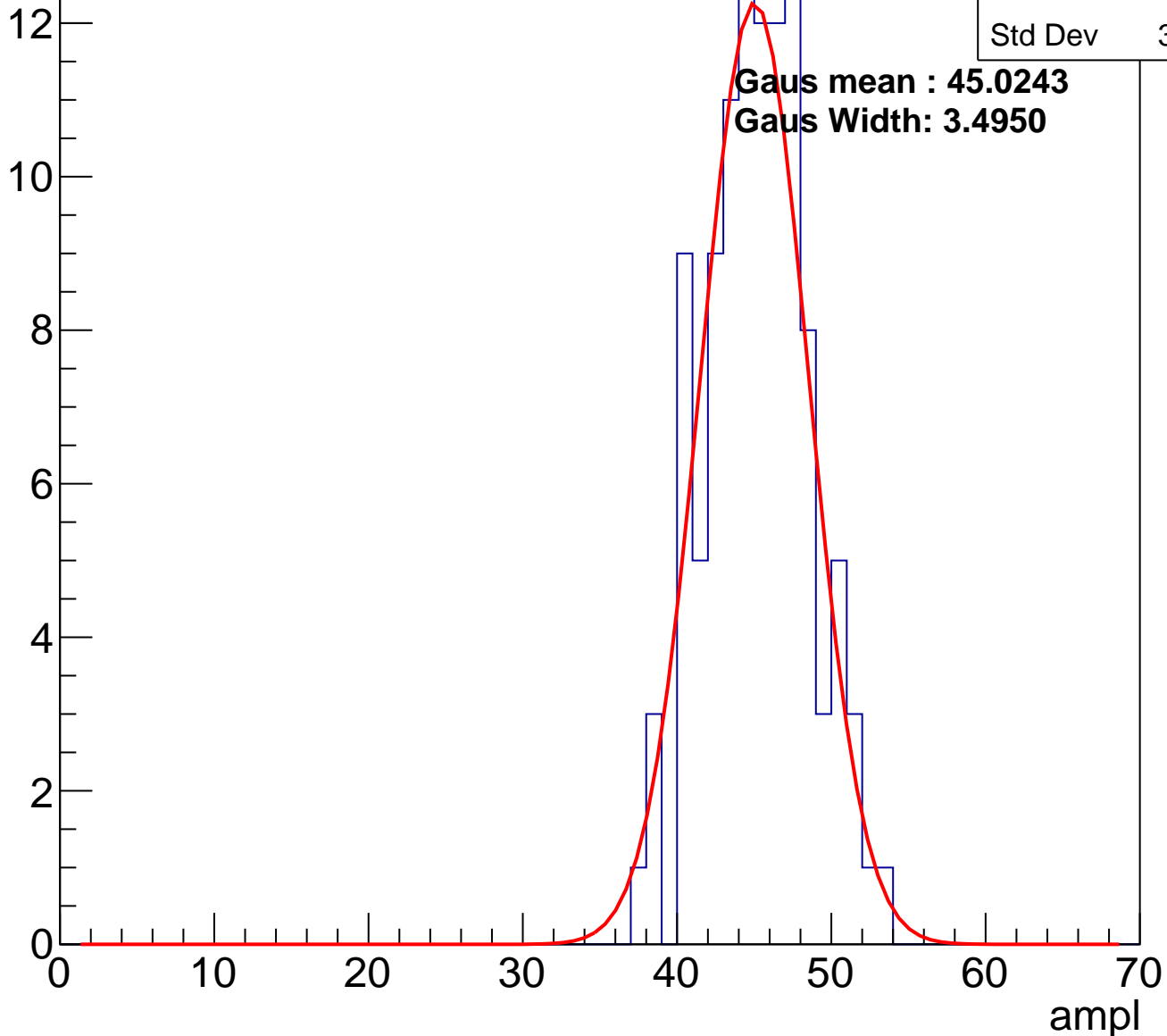
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	44.78
Std Dev	3.288

Gaus mean : 45.0243

Gaus Width: 3.4950

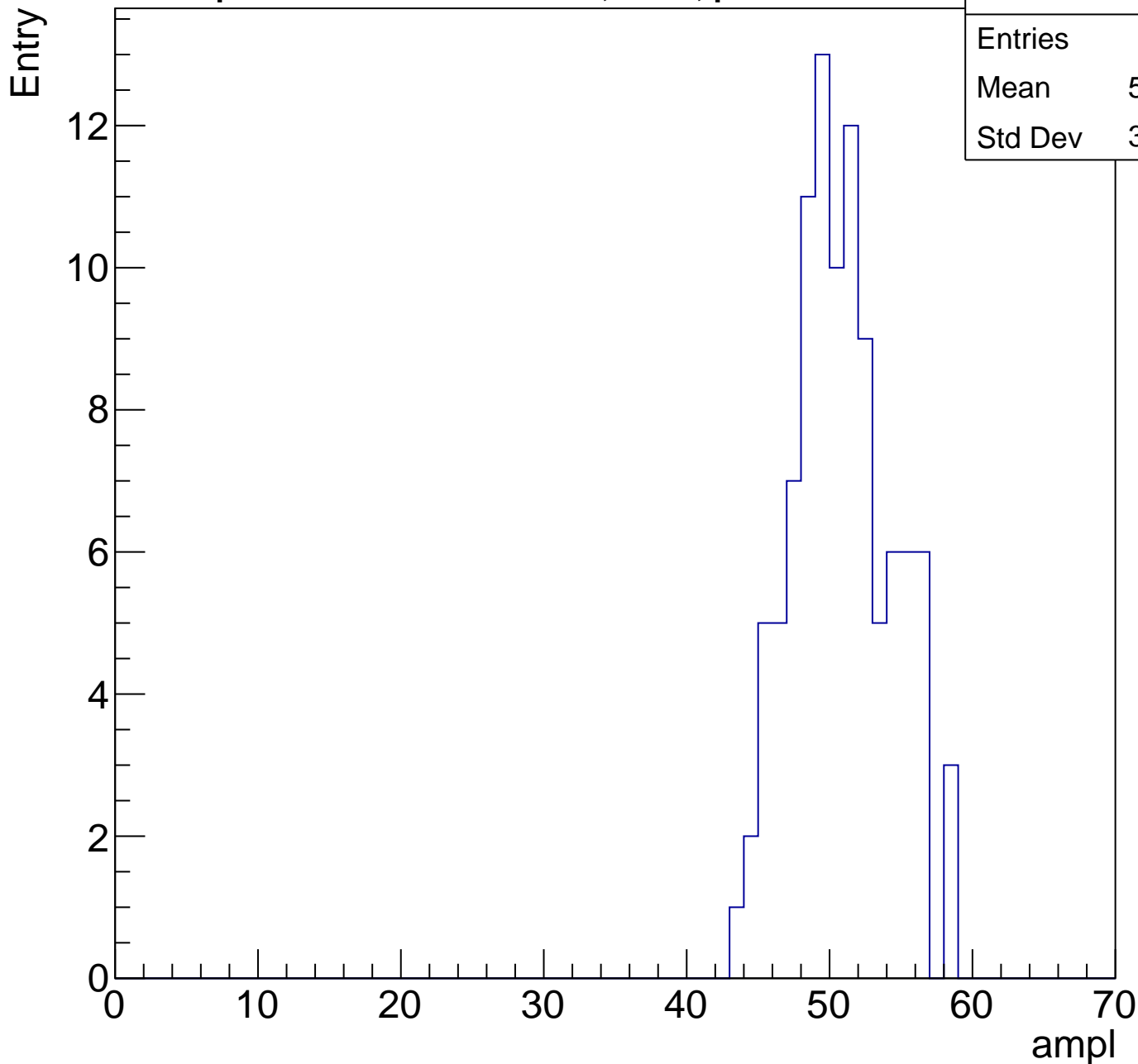
Entry



B0L101S, U25-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

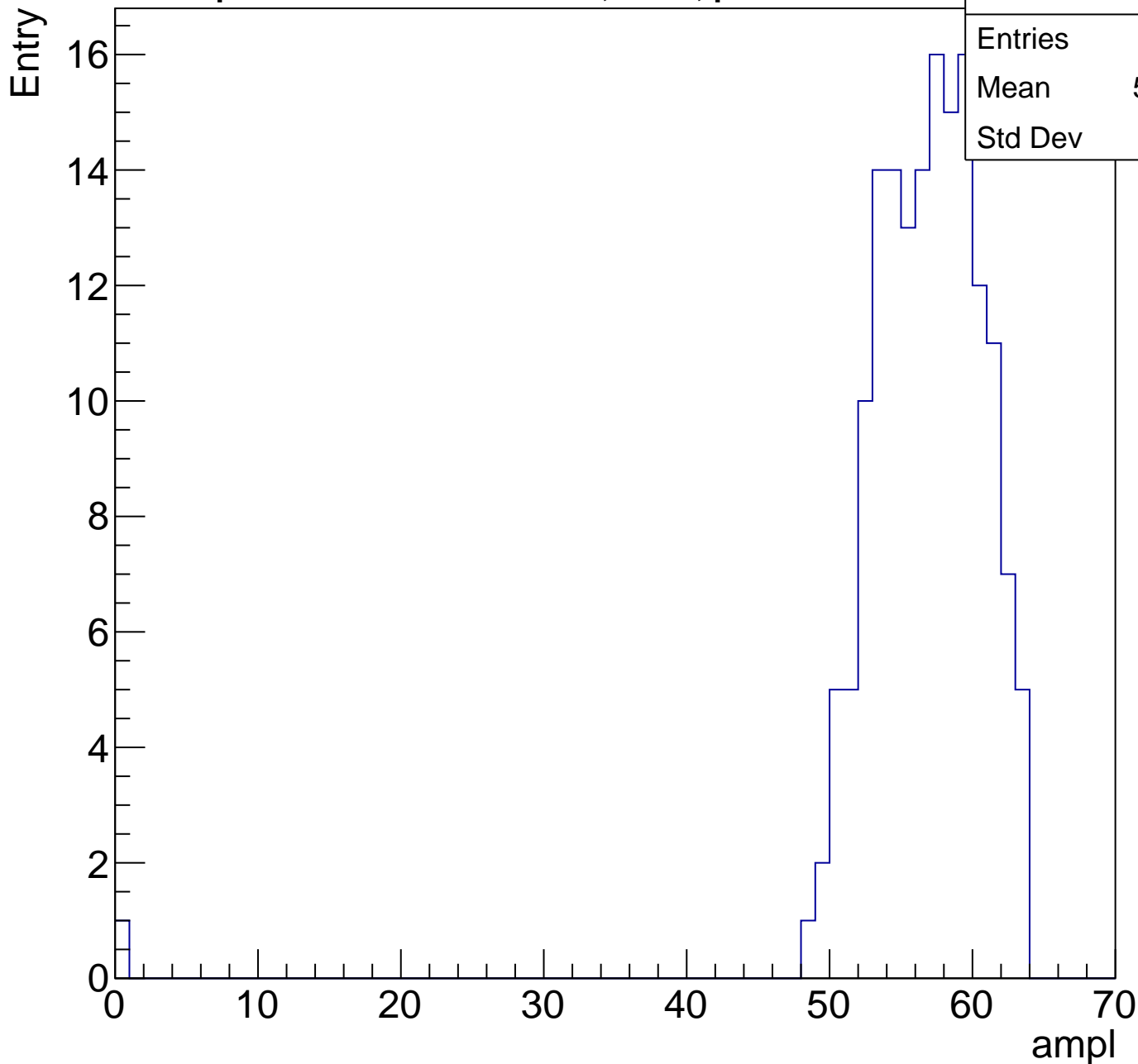
Entries	101
Mean	50.39
Std Dev	3.415



B0L101S, U25-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	56.11
Std Dev	5.65



B0L101S, U25-ch58, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

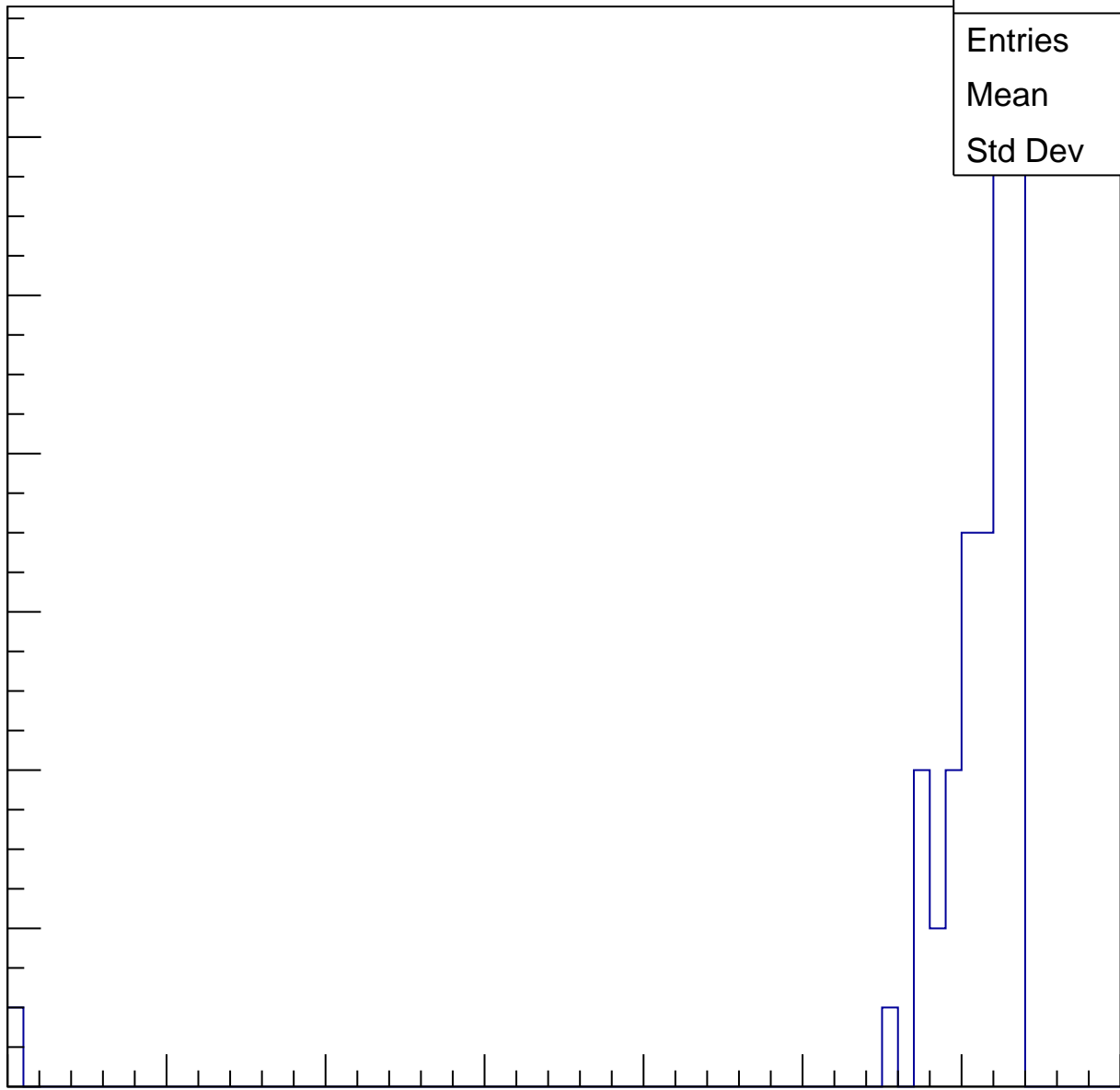
2

0

Entries	51
Mean	59.71
Std Dev	8.678

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch59, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	27.18
Std Dev	4.317

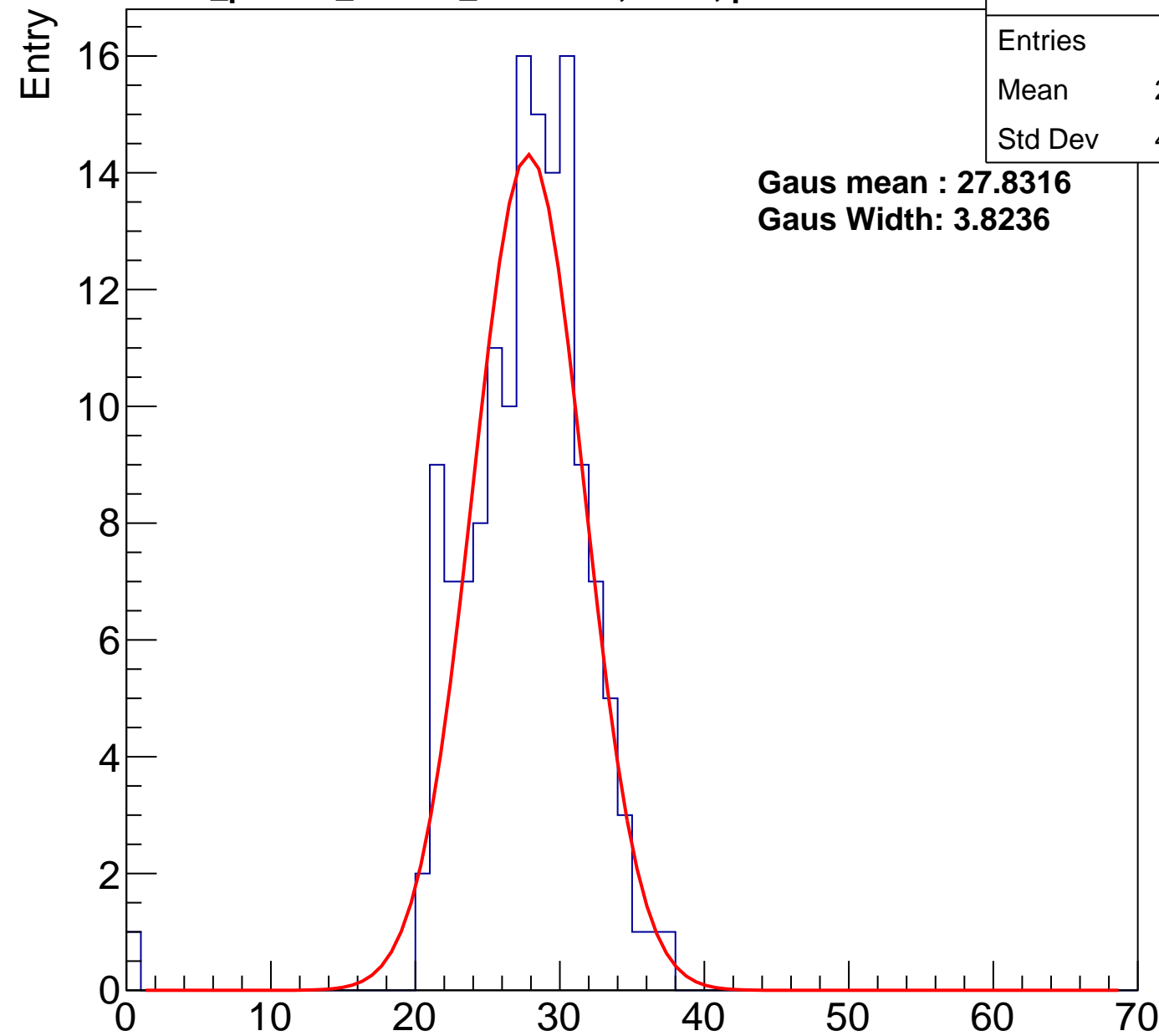
Gaus mean : 27.8316

Gaus Width: 3.8236

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch59, adc1

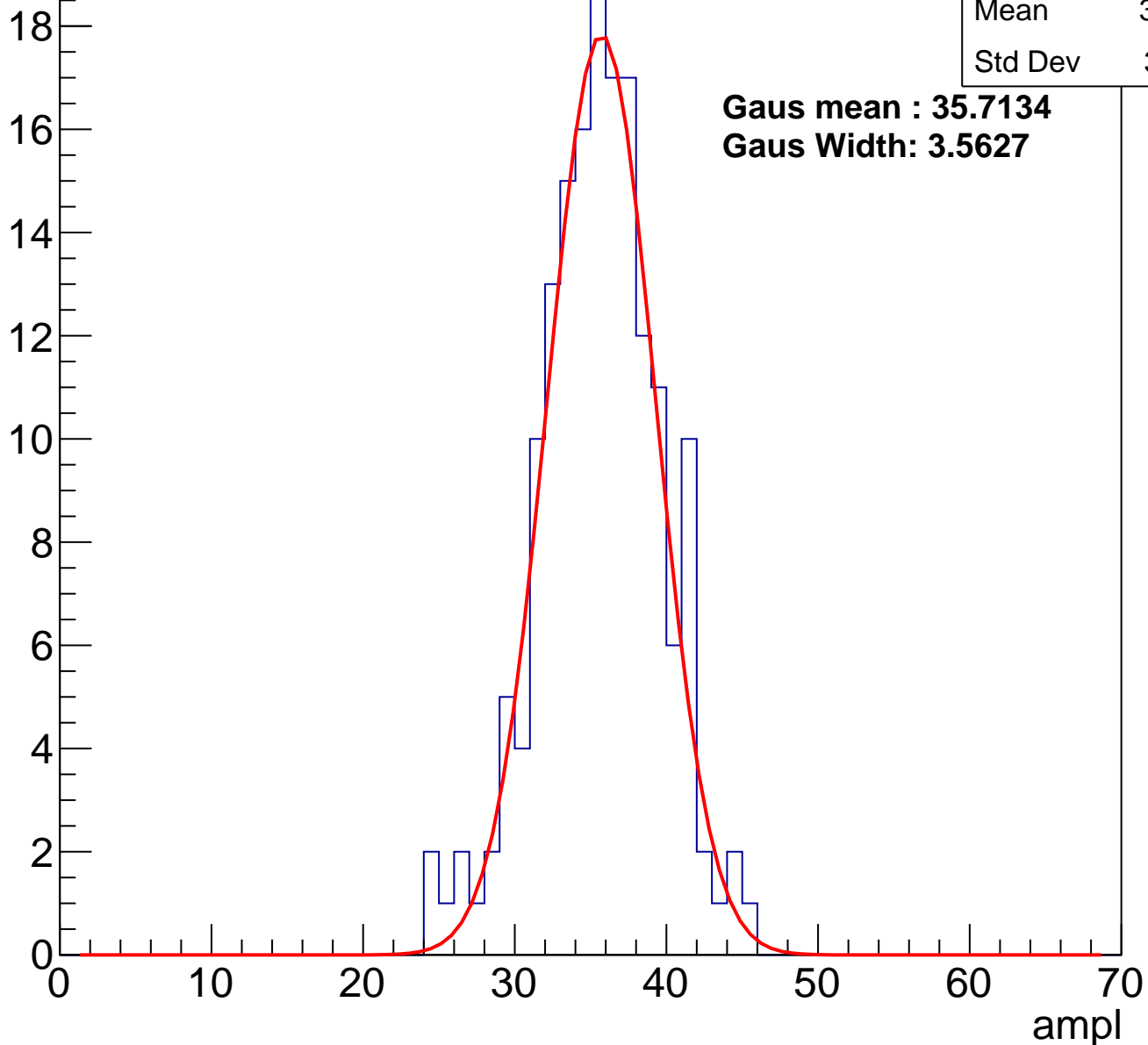
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	35.14
Std Dev	3.921

Gaus mean : 35.7134

Gaus Width: 3.5627

Entry



B0L101S, U25-ch59, adc2

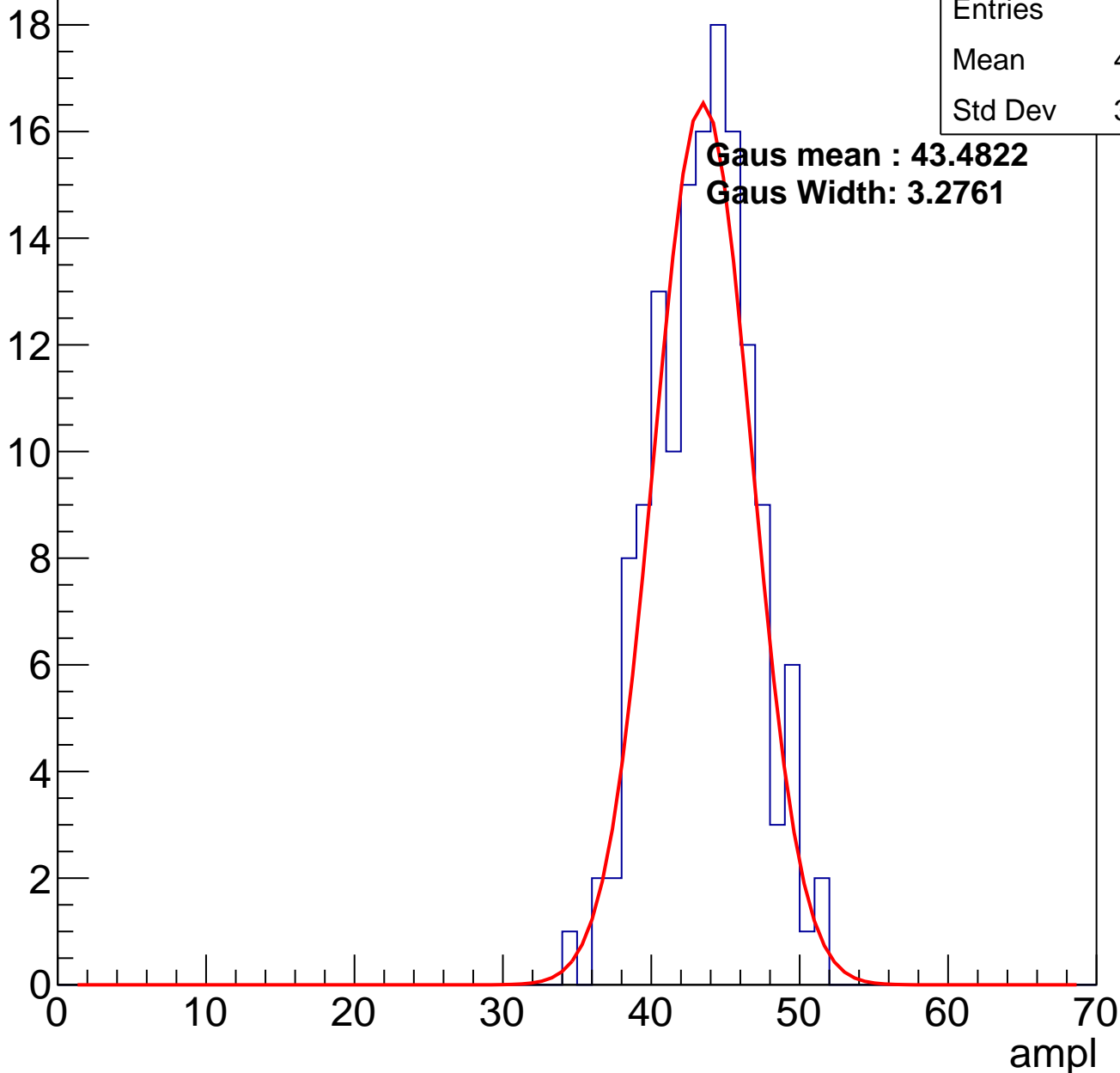
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	43.08
Std Dev	3.312

Gaus mean : 43.4822

Gaus Width: 3.2761

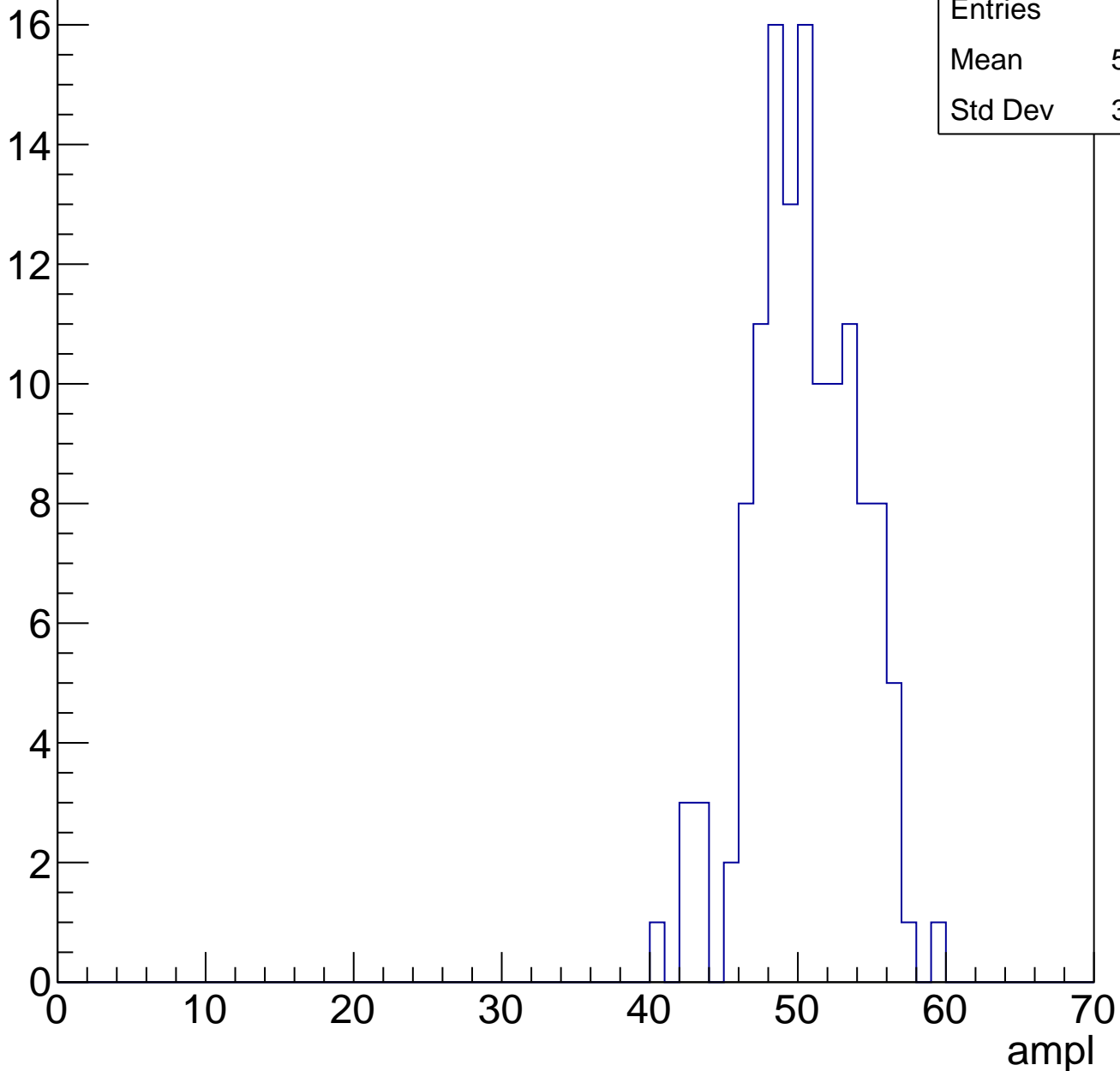
Entry



B0L101S, U25-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

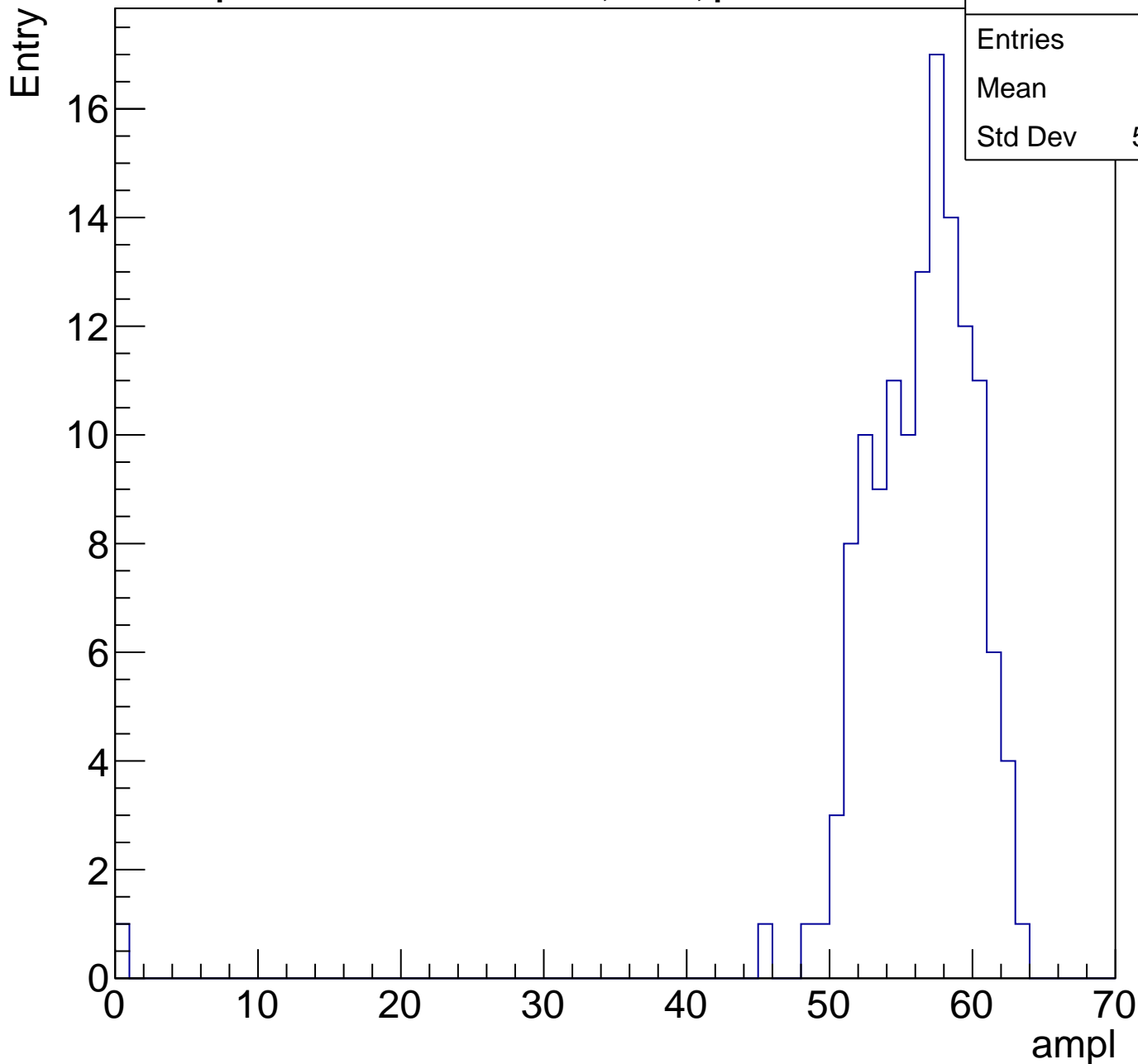
Entry



B0L101S, U25-ch59, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	55.6
Std Dev	5.914



B0L101S, U25-ch59, adc5

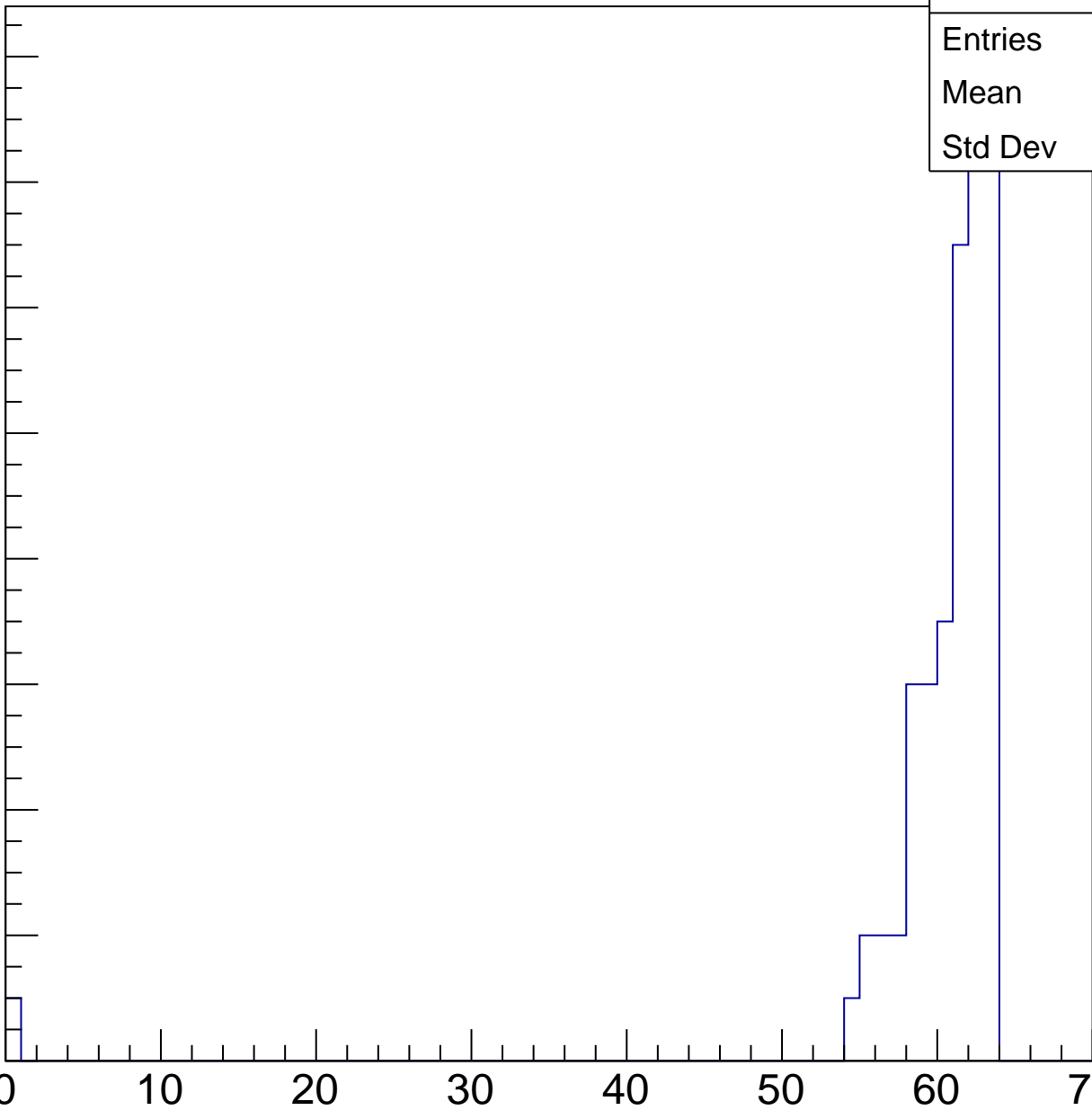
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	71
Mean	59.76
Std Dev	7.486

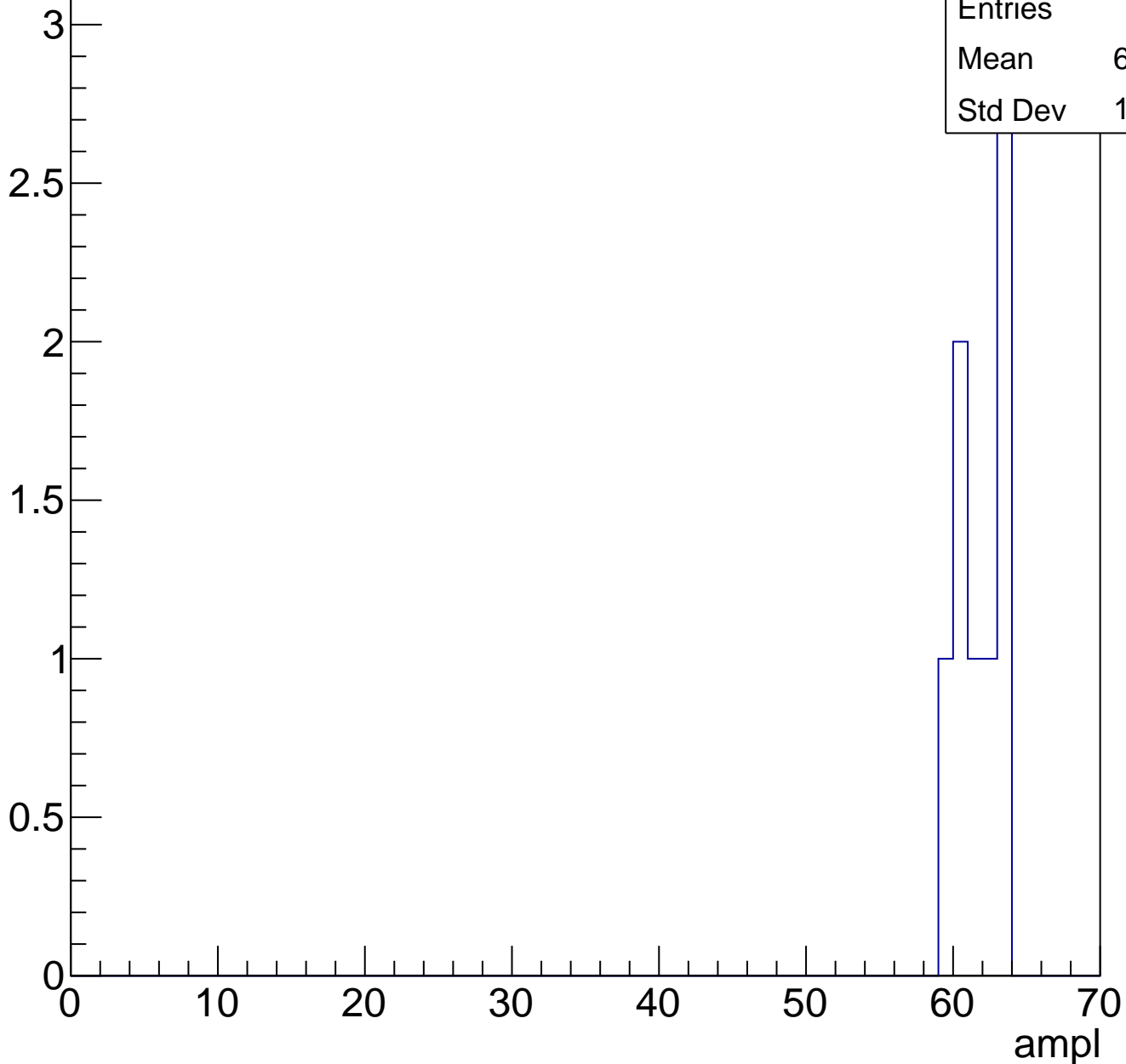
ampl



B0L101S, U25-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U25-ch60, adc0

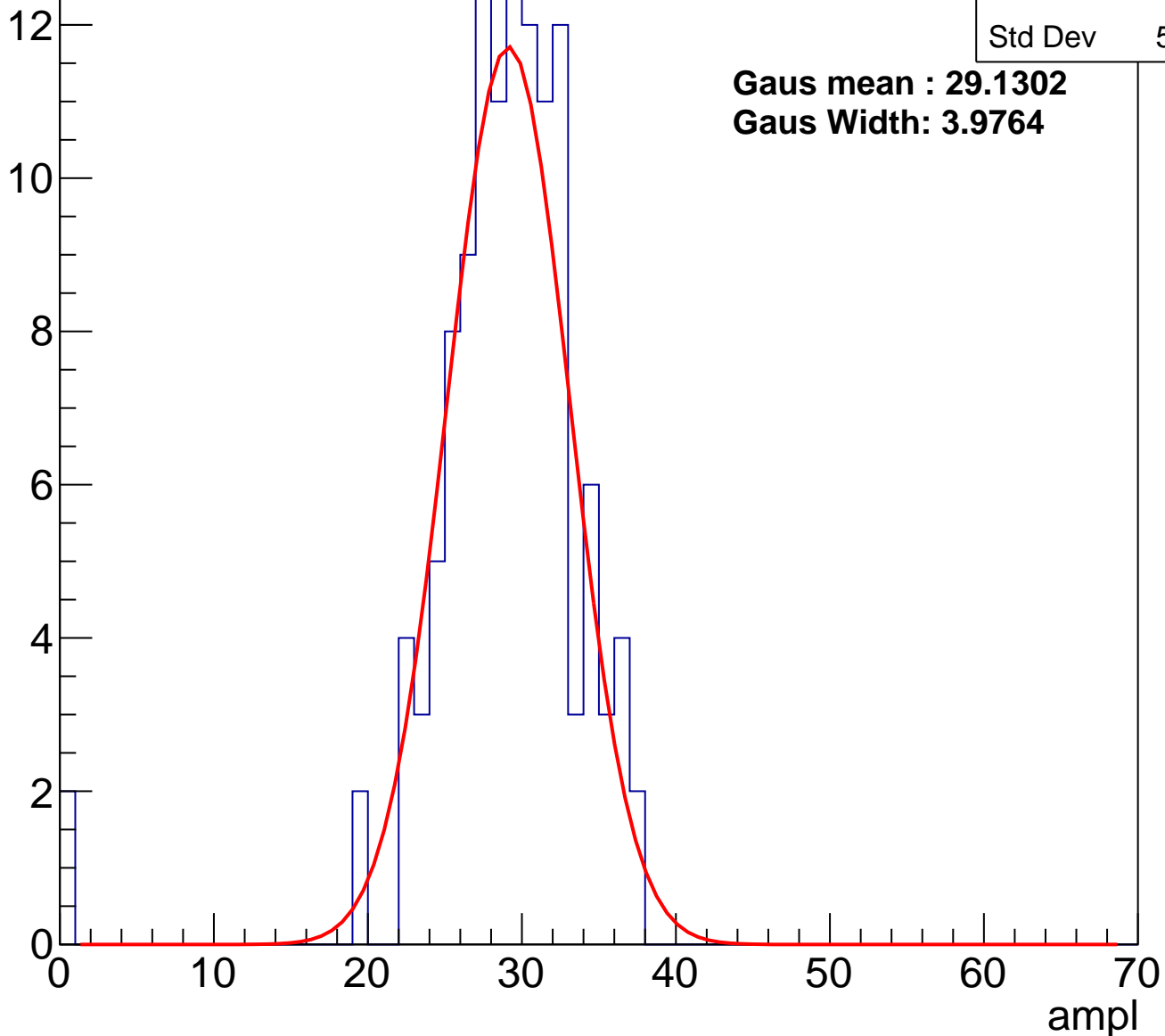
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	28.42
Std Dev	5.208

Gaus mean : 29.1302

Gaus Width: 3.9764

Entry



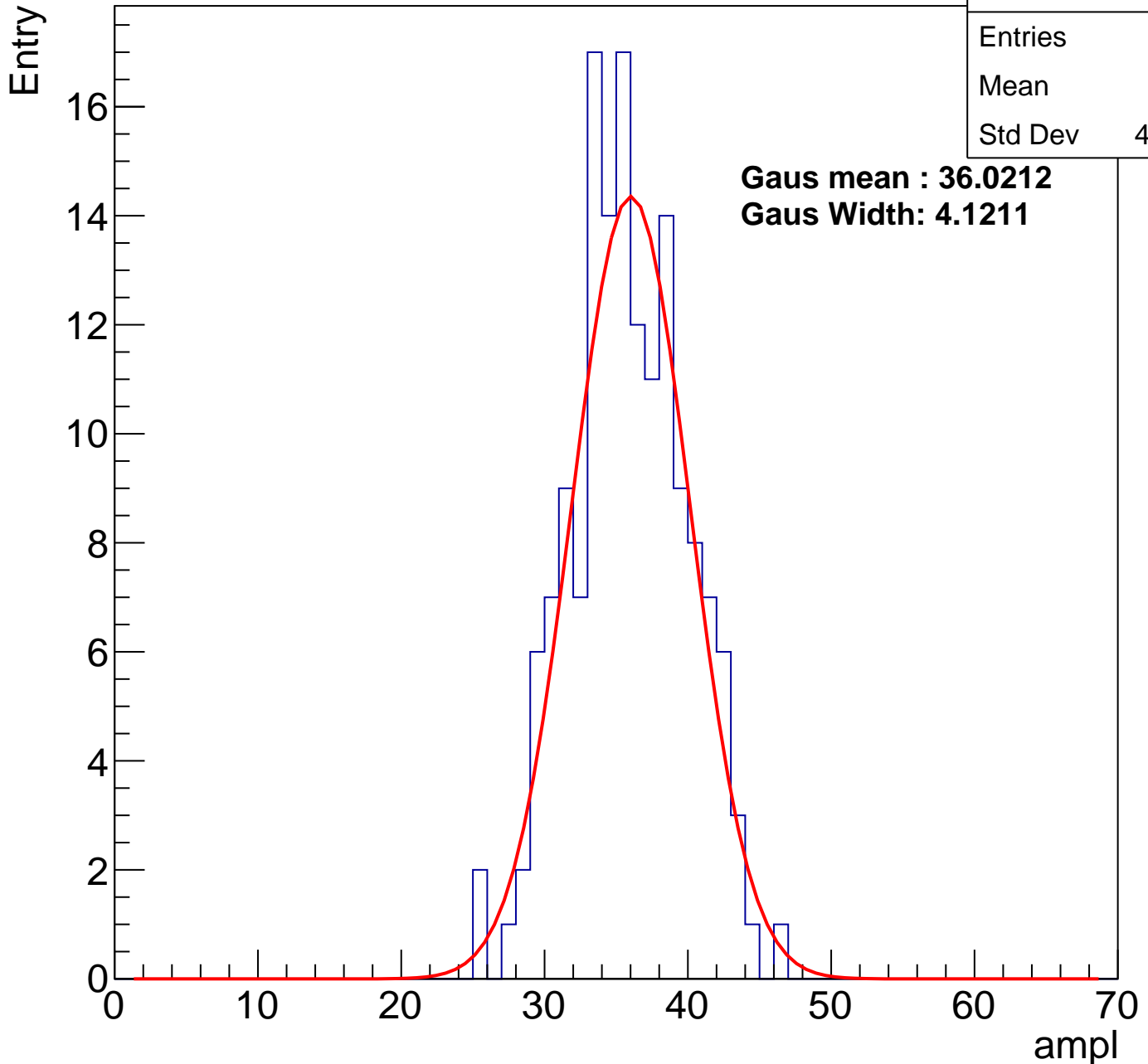
B0L101S, U25-ch60, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	35.4
Std Dev	4.015

Gaus mean : 36.0212

Gaus Width: 4.1211



B0L101S, U25-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

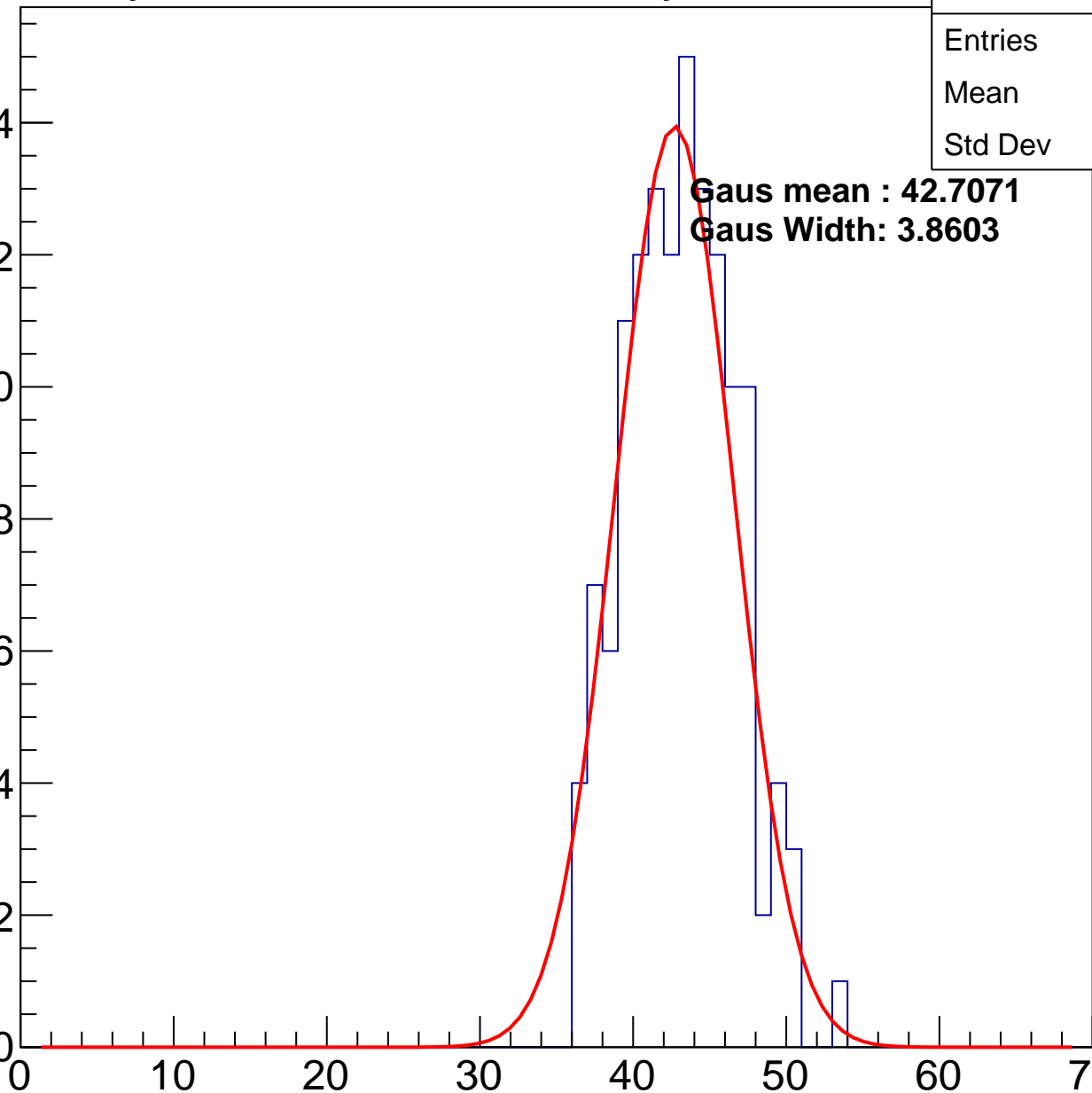
14
12
10
8
6
4
2
0

Entries	135
Mean	42.66
Std Dev	3.53

Gaus mean : 42.7071

Gaus Width: 3.8603

ampl

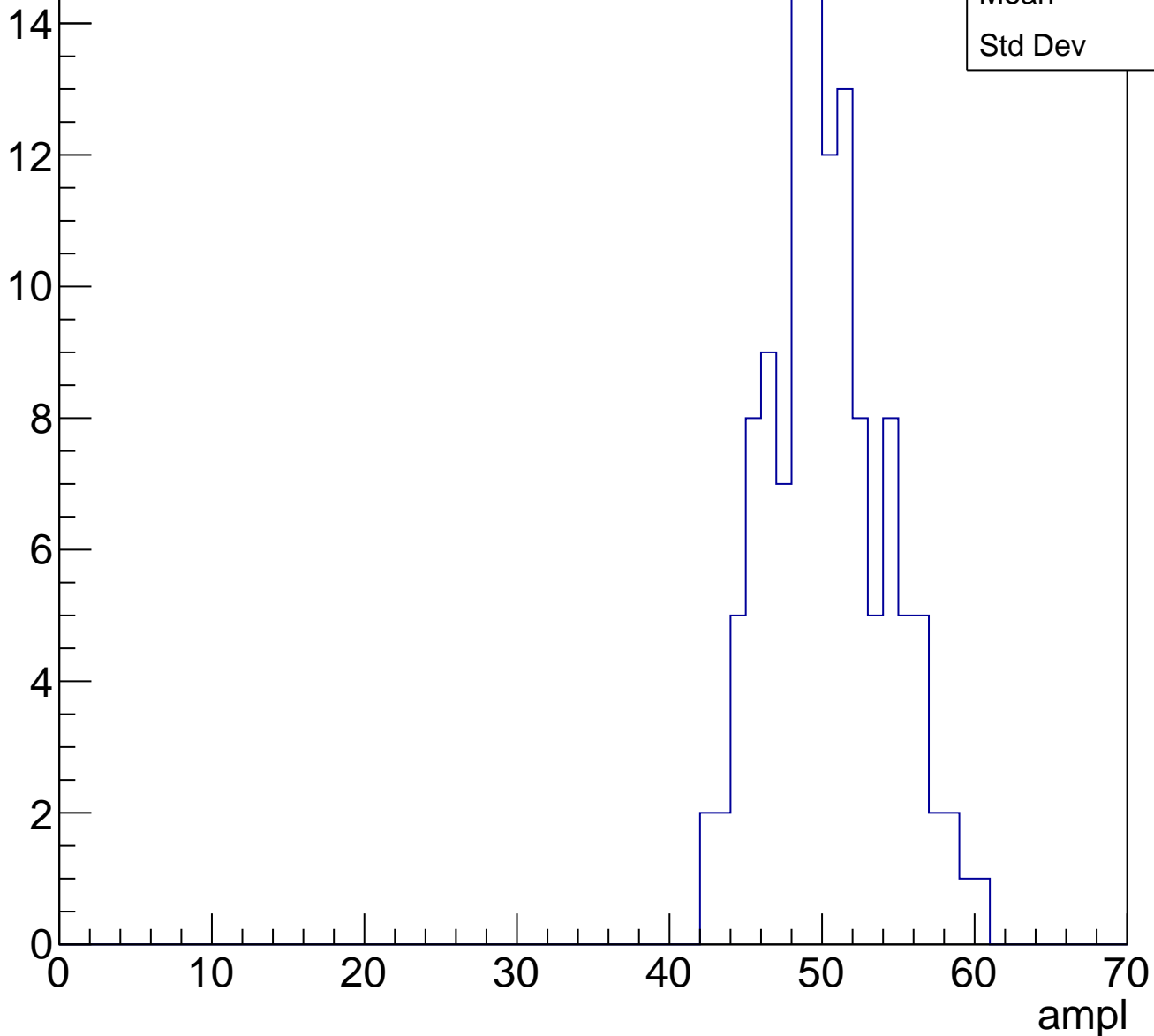


B0L101S, U25-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	49.82
Std Dev	3.81

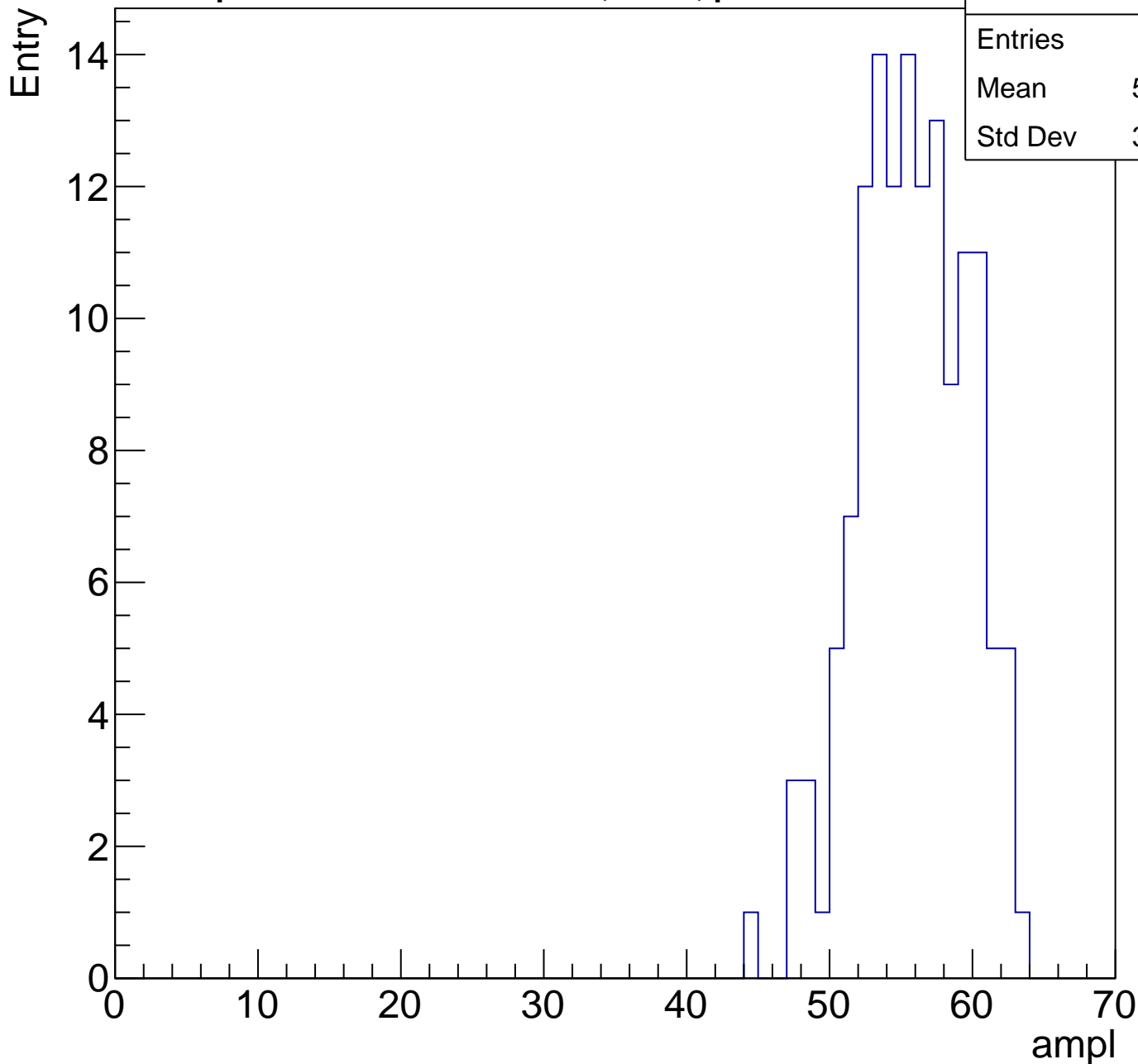
Entry



B0L101S, U25-ch60, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	55.33
Std Dev	3.773



B0L101S, U25-ch60, adc5

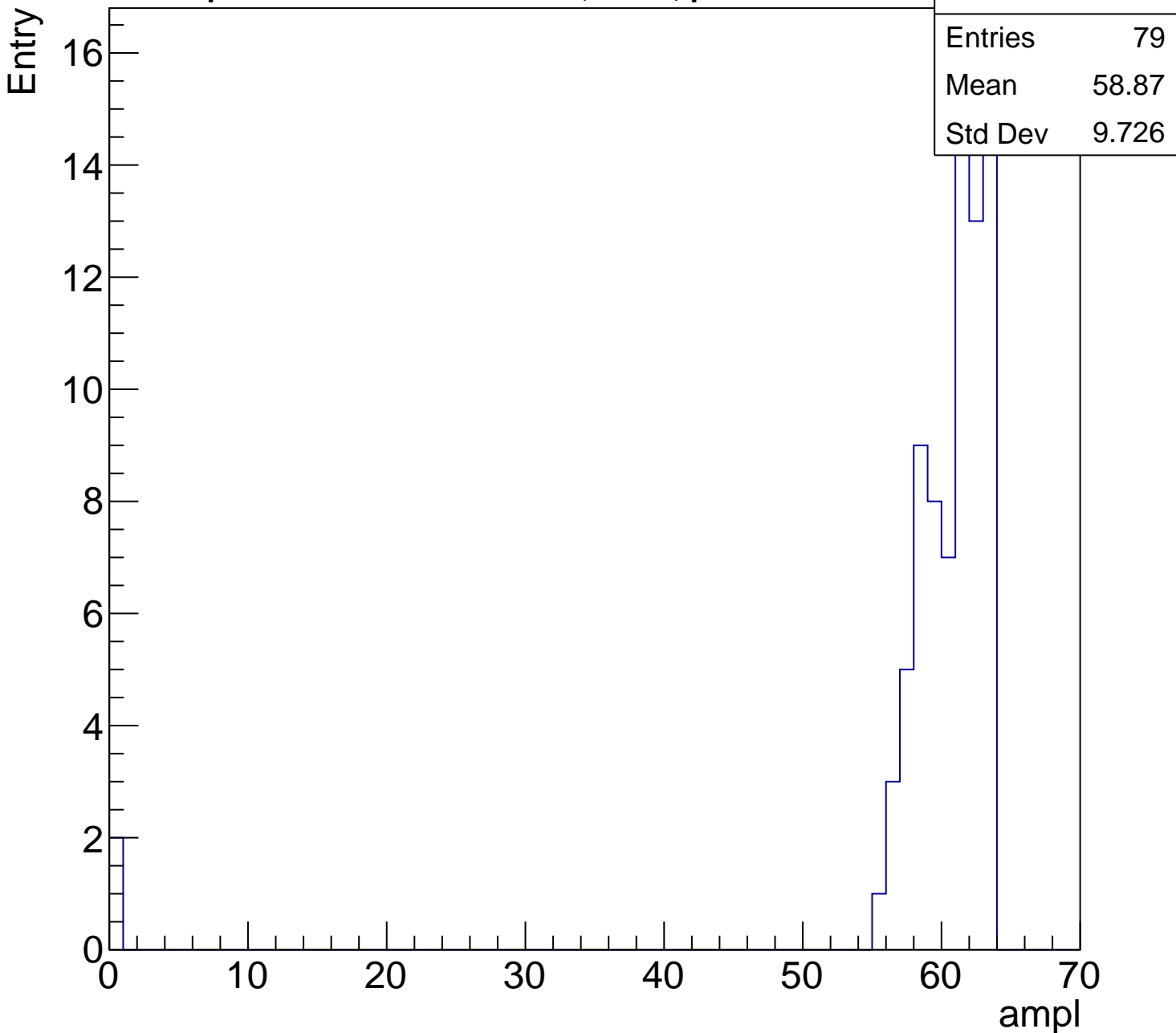
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	58.87
Std Dev	9.726

ampl



B0L101S, U25-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch61, adc0

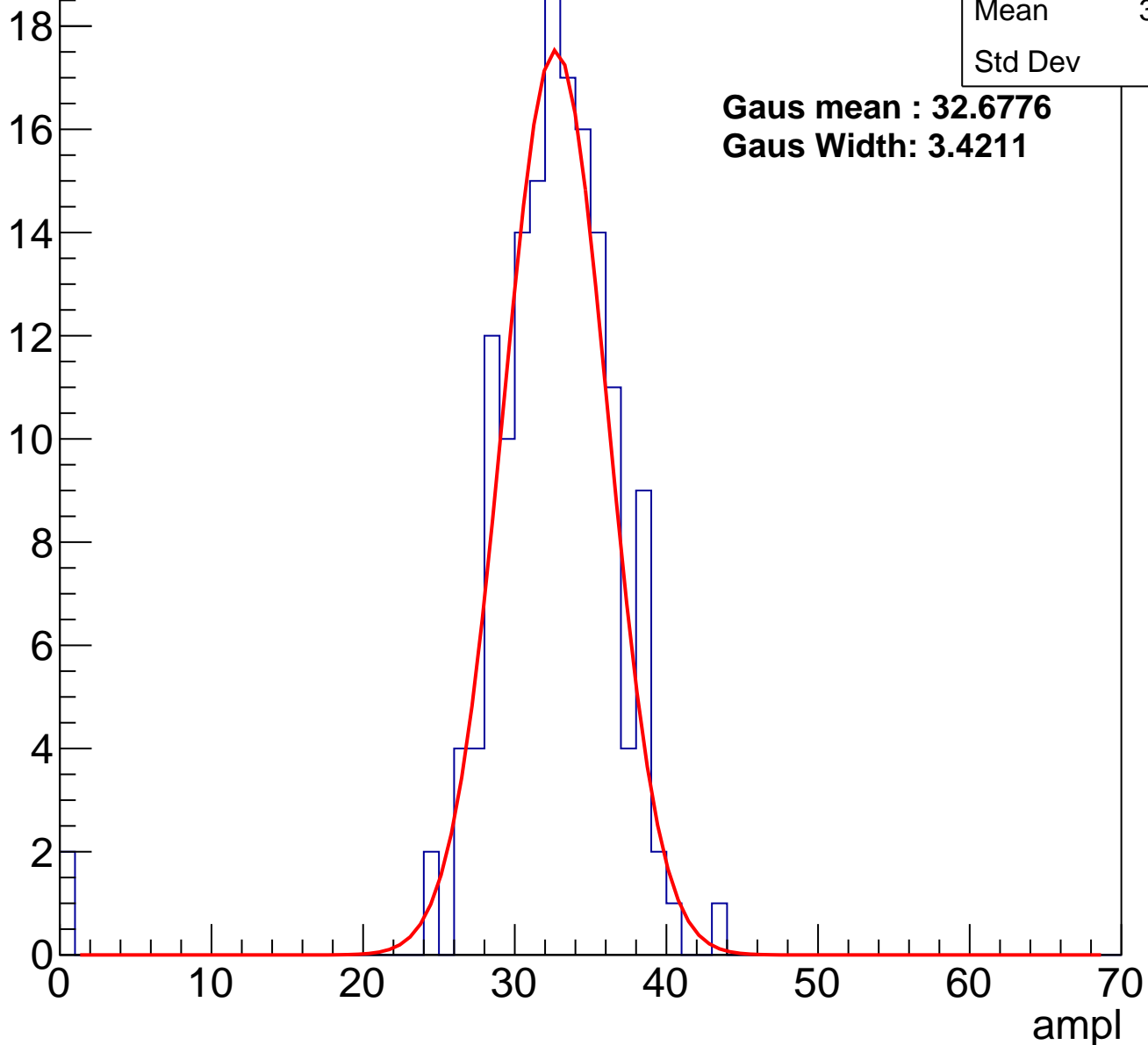
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	31.98
Std Dev	4.96

Gaus mean : 32.6776

Gaus Width: 3.4211

Entry



B0L101S, U25-ch61, adc1

calib_packv5_042523_0143.root, FC#1, port C1

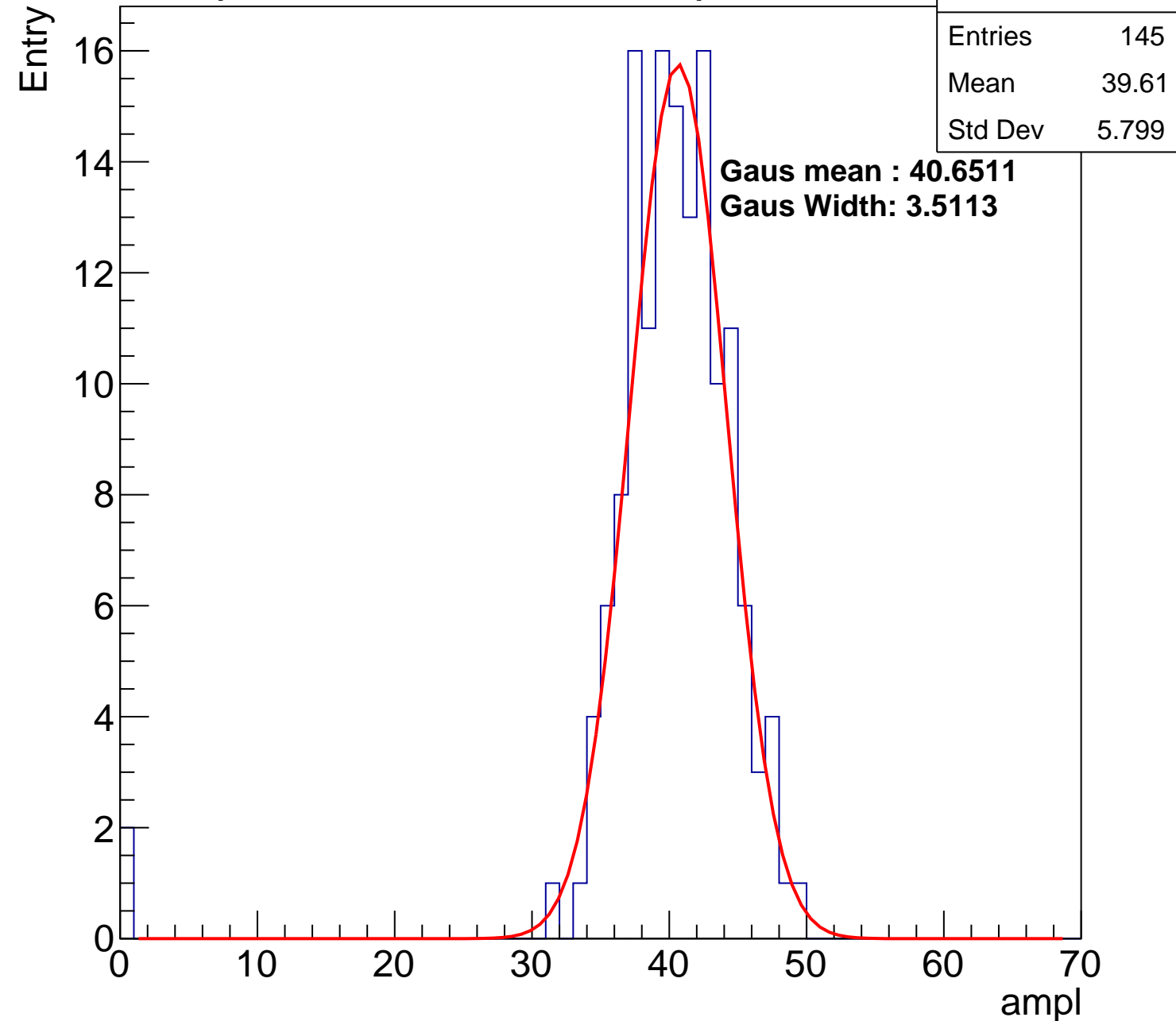
Entry

16
14
12
10
8
6
4
2
0

Entries	145
Mean	39.61
Std Dev	5.799

Gaus mean : 40.6511
Gaus Width: 3.5113

ampl



B0L101S, U25-ch61, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	46.06
Std Dev	3.461

Gaus mean : 46.0714

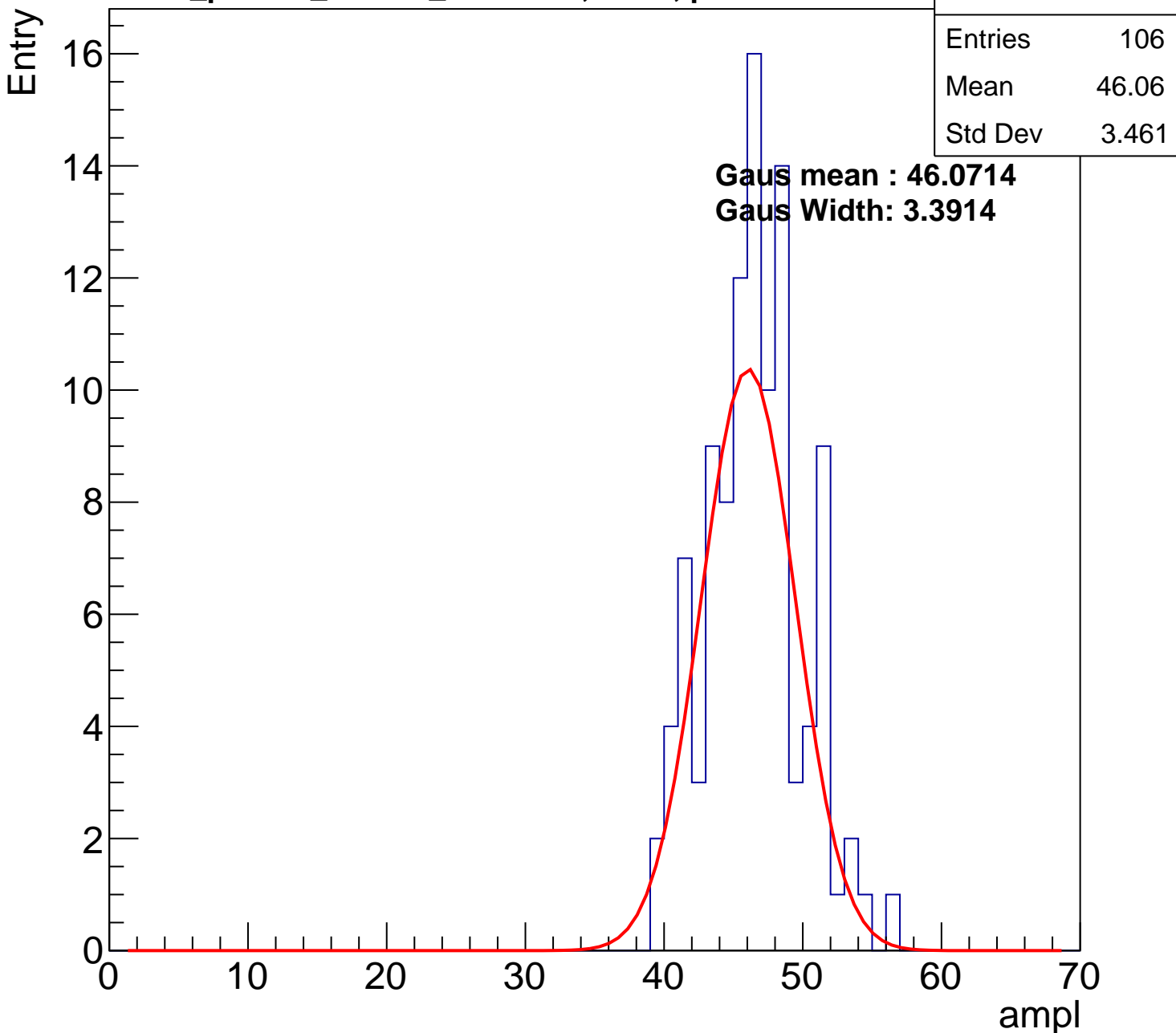
Gaus Width: 3.3914

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

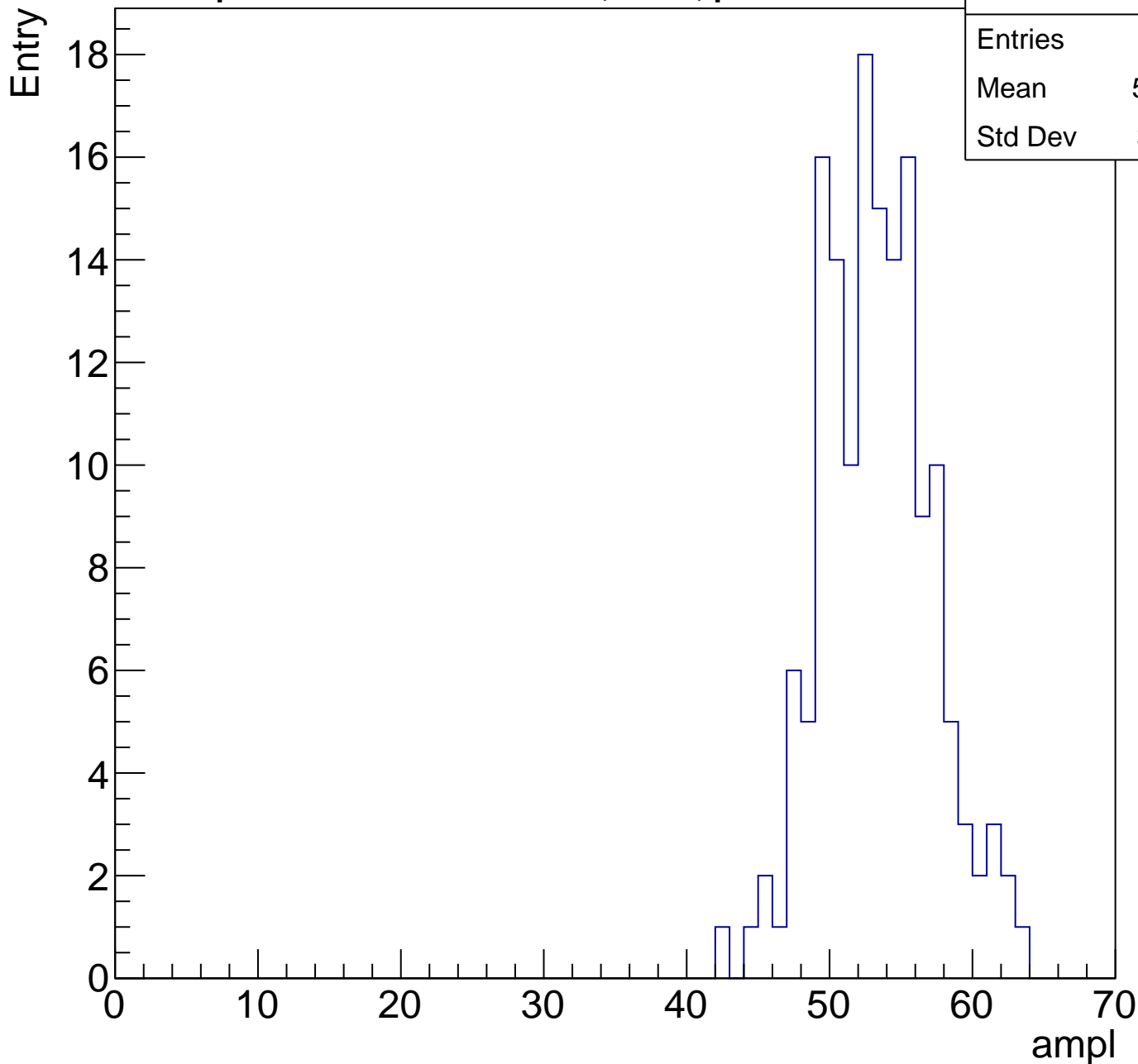
ampl



B0L101S, U25-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	52.83
Std Dev	3.831



B0L101S, U25-ch61, adc4

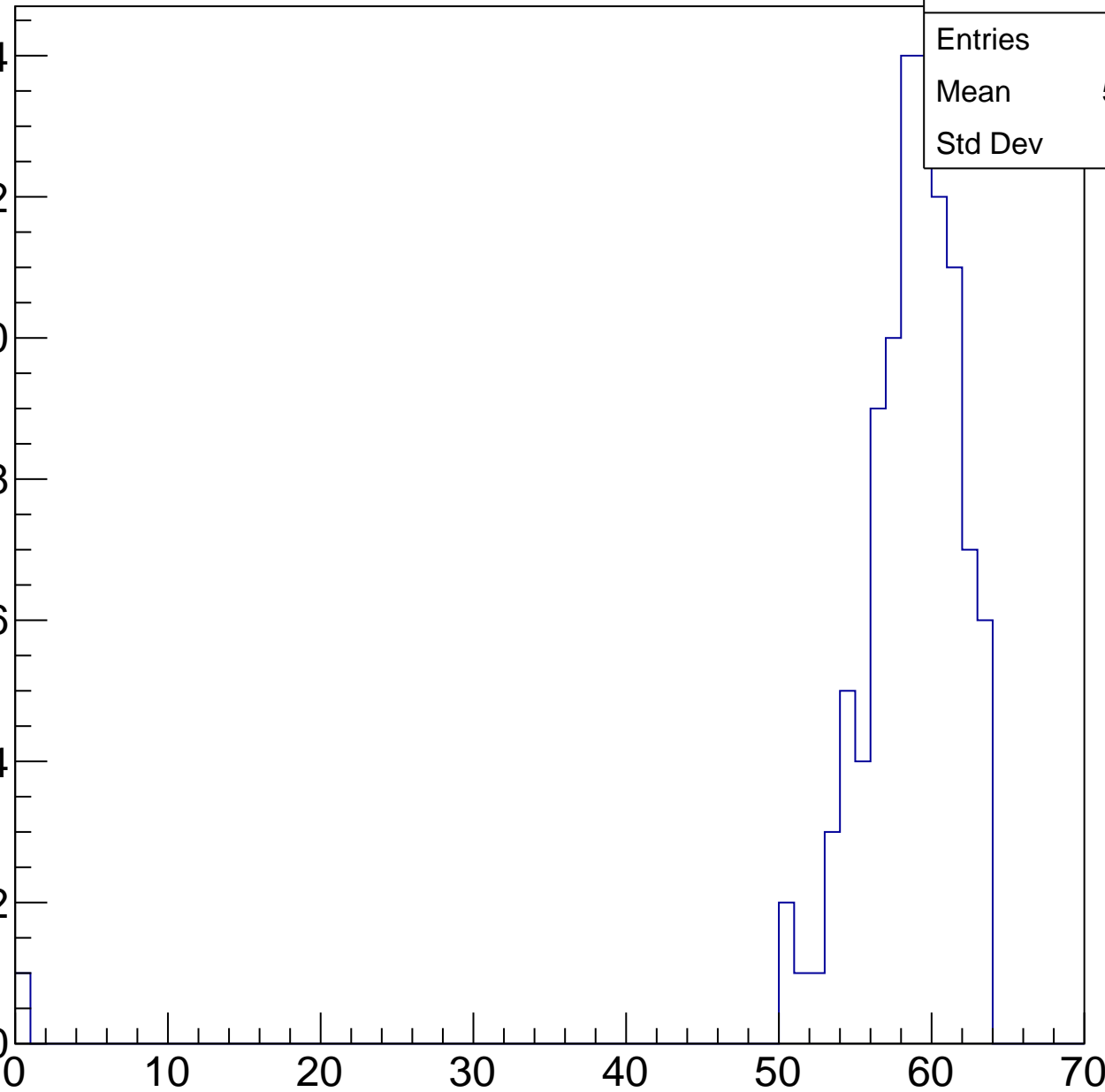
calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	57.67
Std Dev	6.501

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch61, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	40
Mean	59.8
Std Dev	9.709

B0L101S, U25-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch62, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	29.44
Std Dev	4.648

Gaus mean : 30.5215

Gaus Width: 3.7808

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

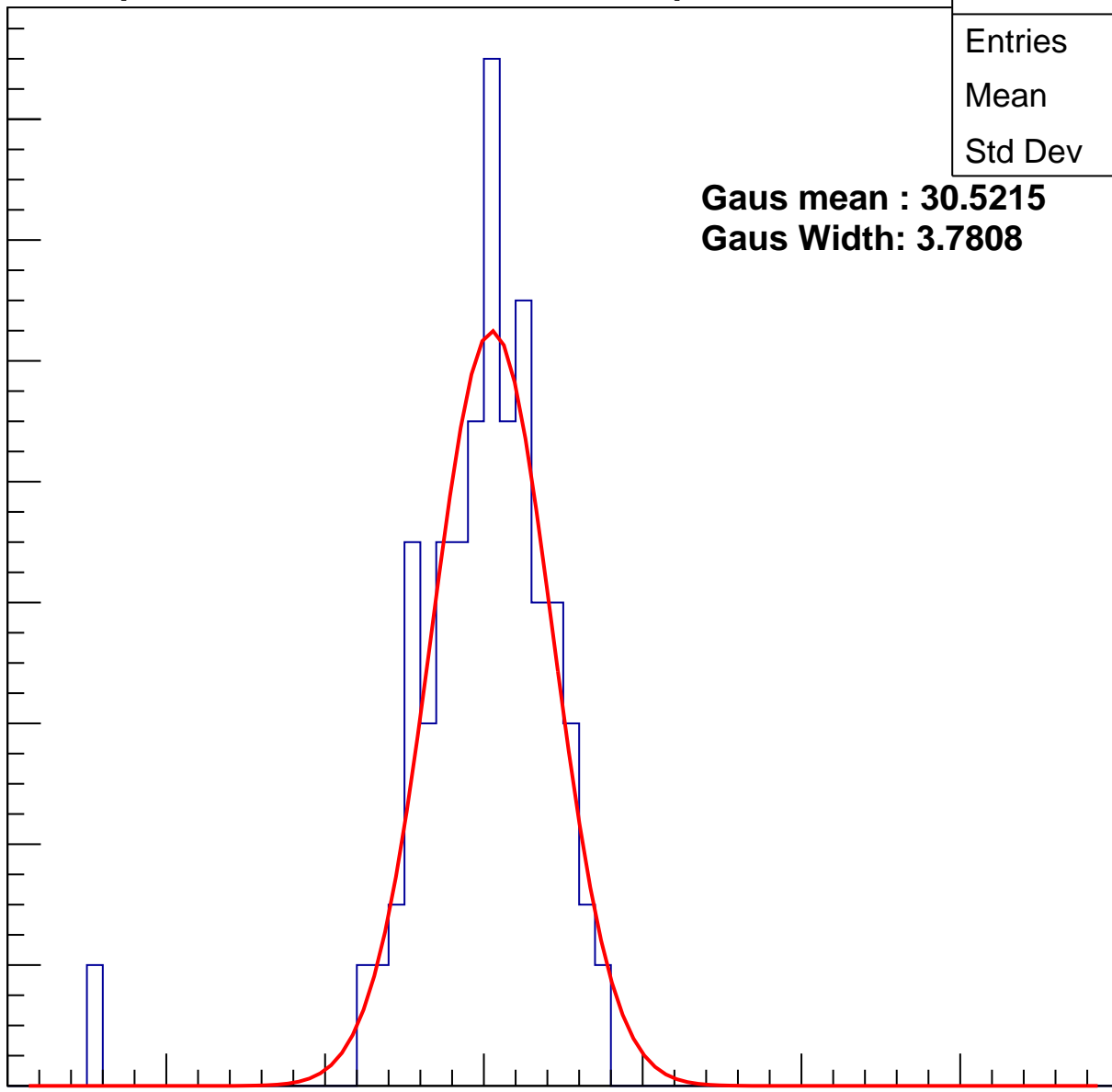
40

50

60

70

ampl



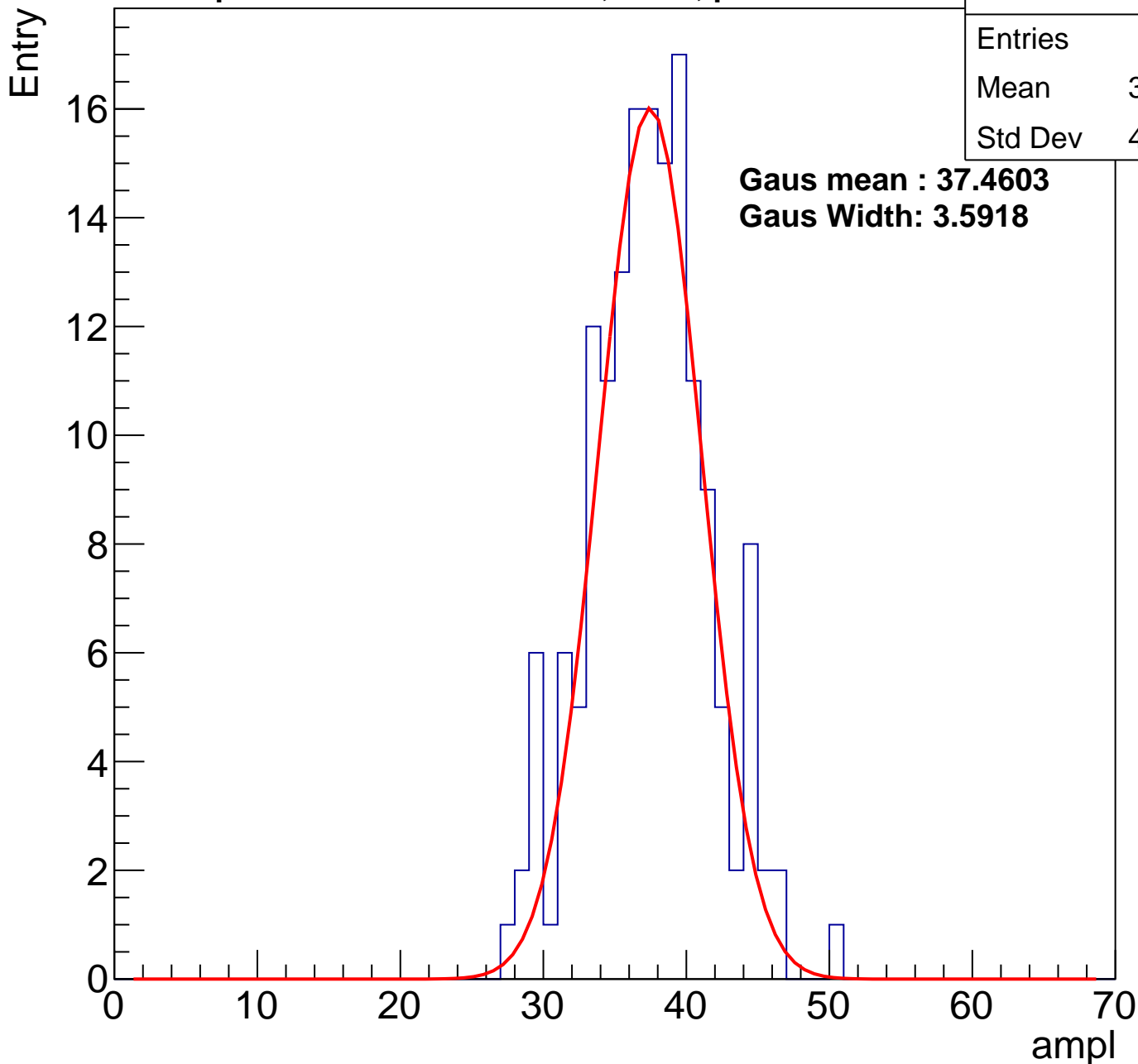
B0L101S, U25-ch62, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	36.94
Std Dev	4.147

Gaus mean : 37.4603

Gaus Width: 3.5918



B0L101S, U25-ch62, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	44.44
Std Dev	3.858

Gaus mean : 44.9293

Gaus Width: 3.9379

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

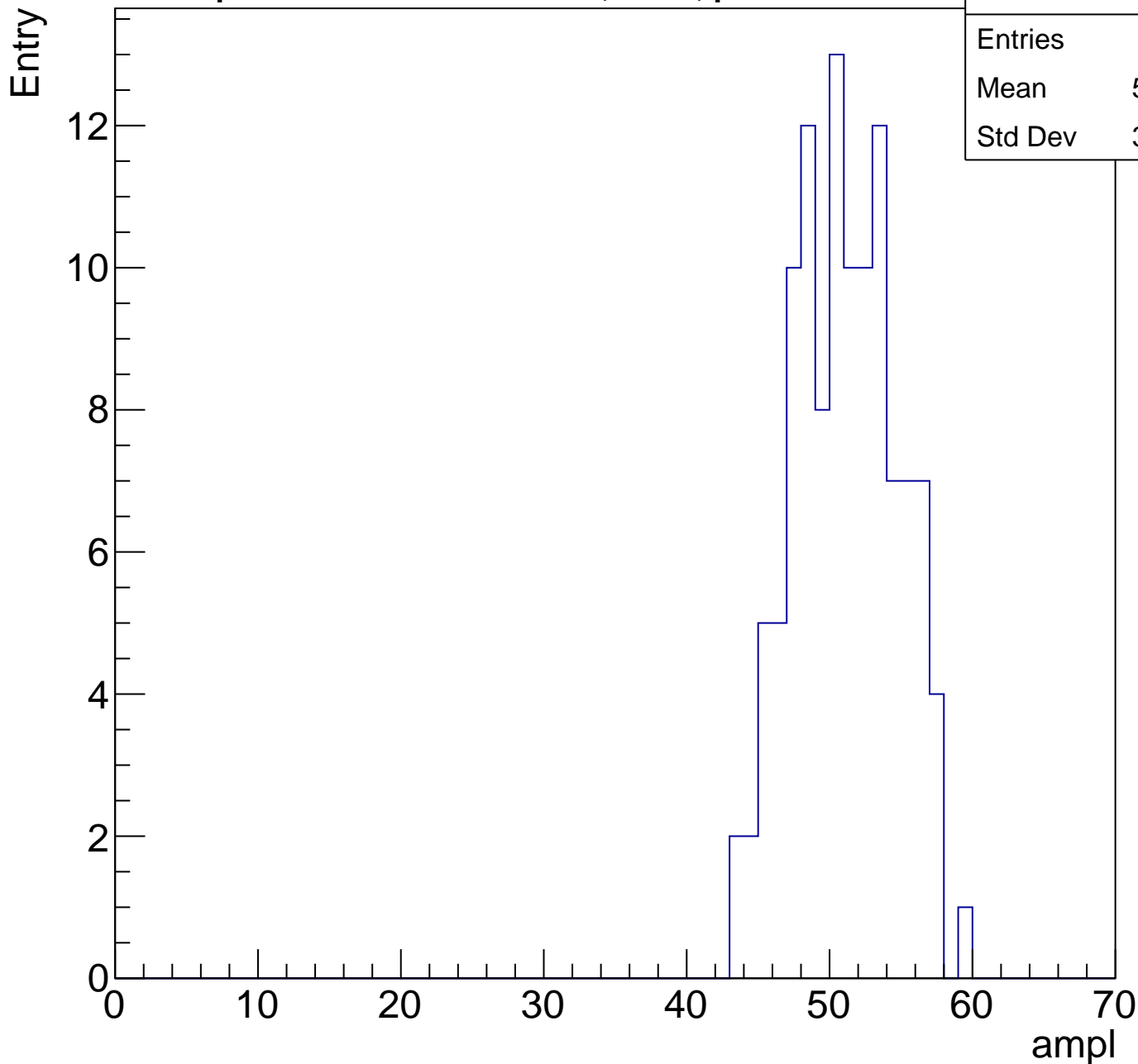
60

70

B0L101S, U25-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	50.65
Std Dev	3.546



B0L101S, U25-ch62, adc4

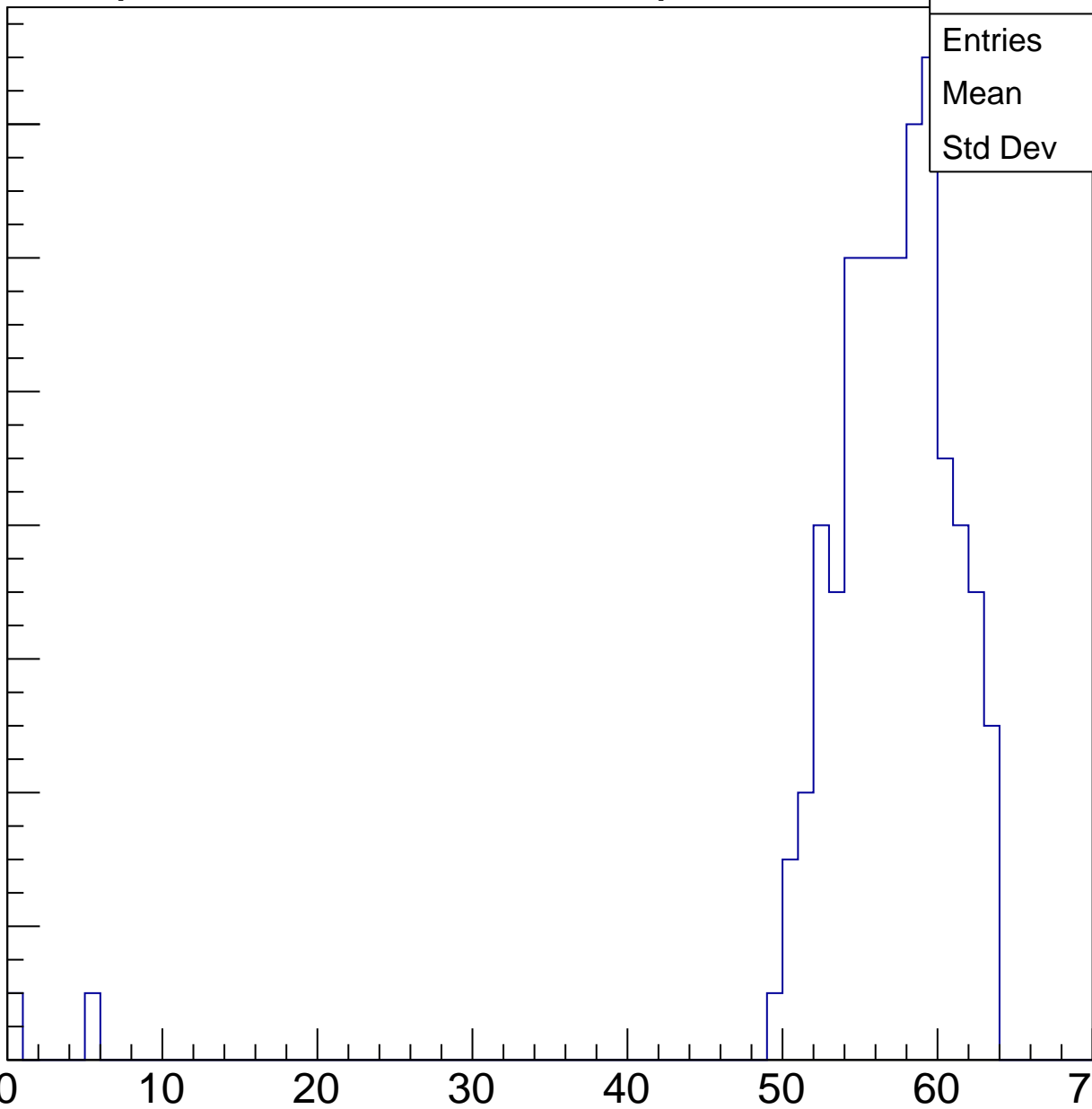
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	131
Mean	55.98
Std Dev	7.459

ampl



B0L101S, U25-ch62, adc5

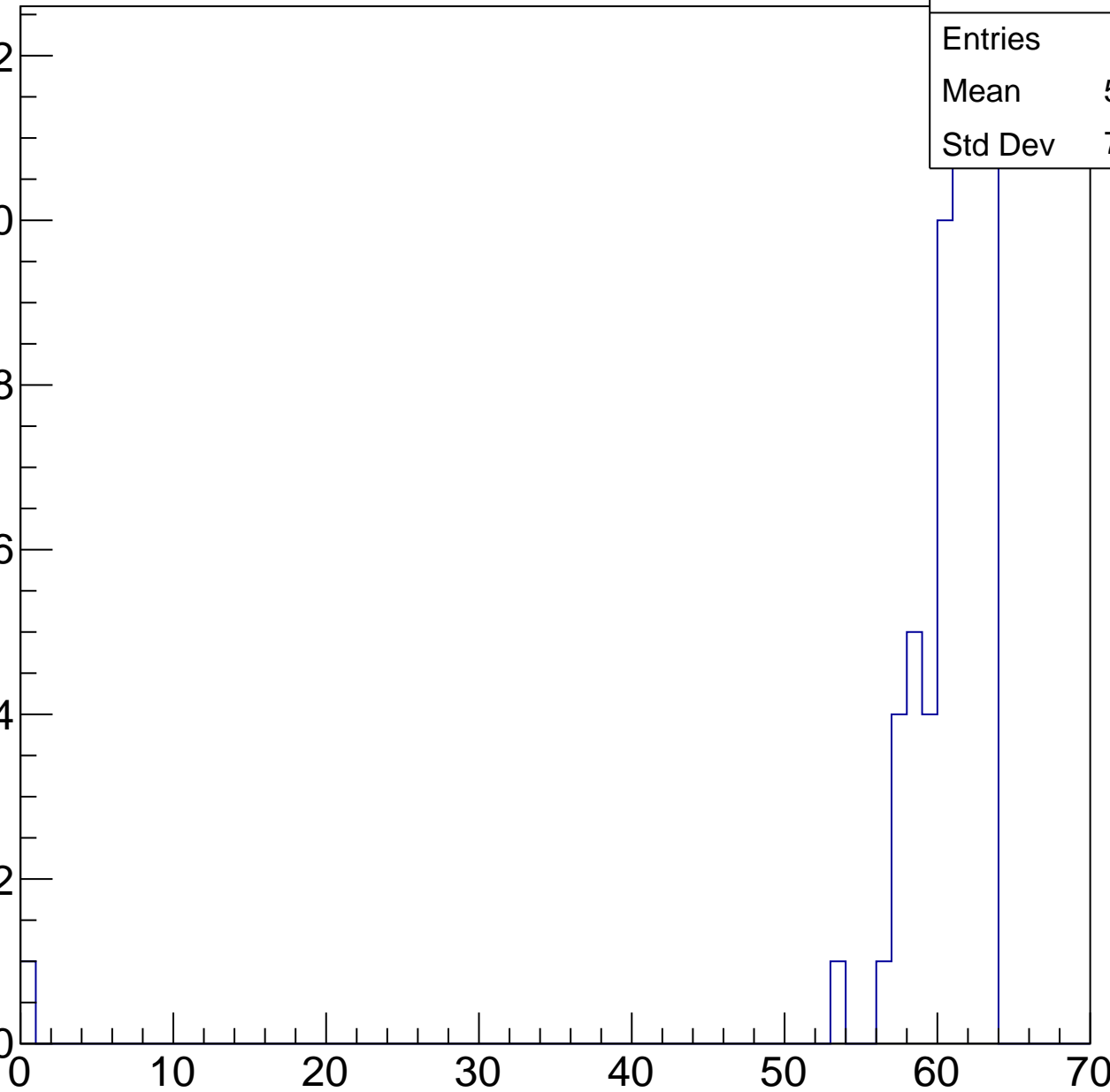
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	61
Mean	59.57
Std Dev	7.976

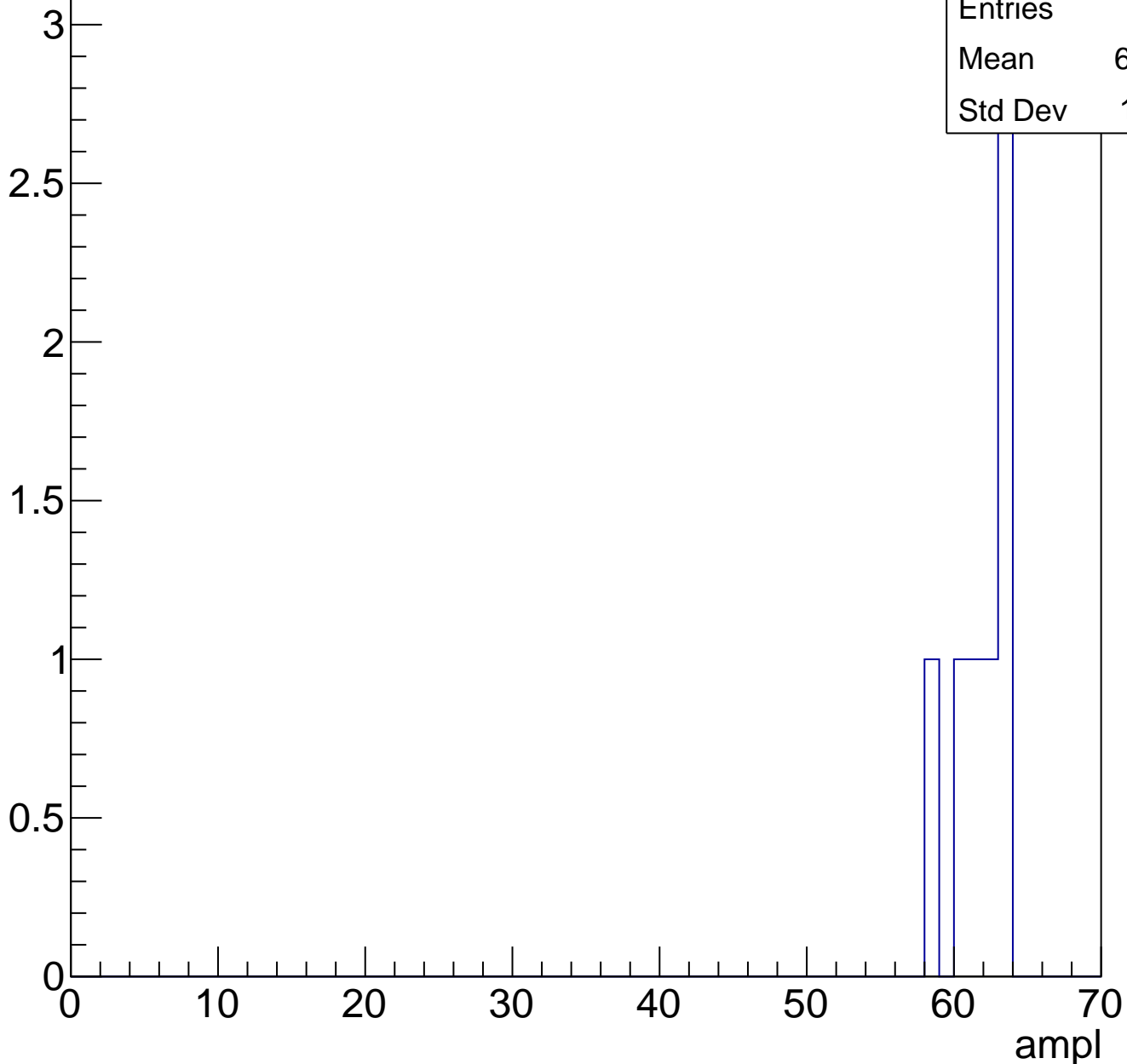
ampl



B0L101S, U25-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

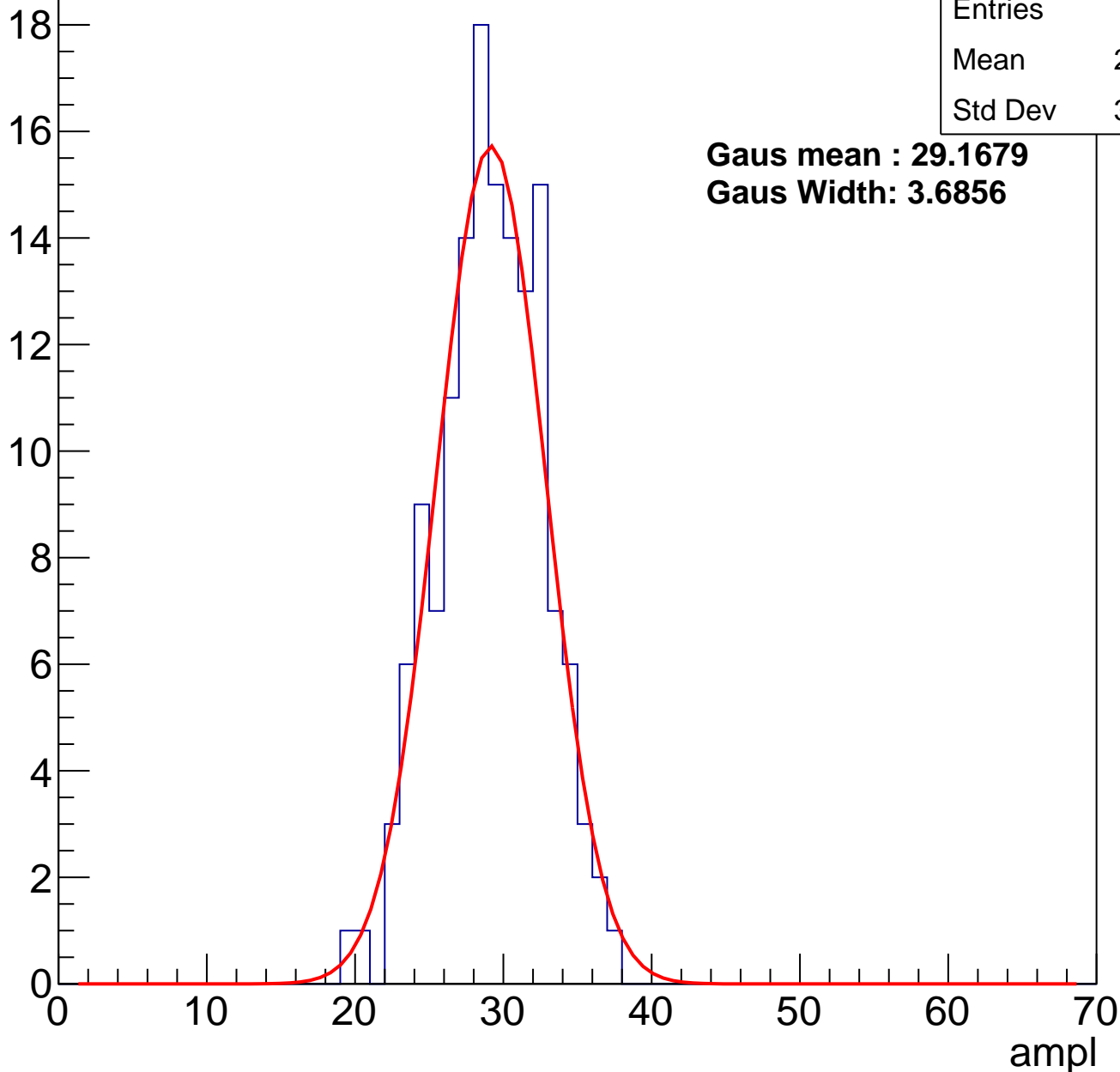
Entry



B0L101S, U25-ch63, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch63, adc1

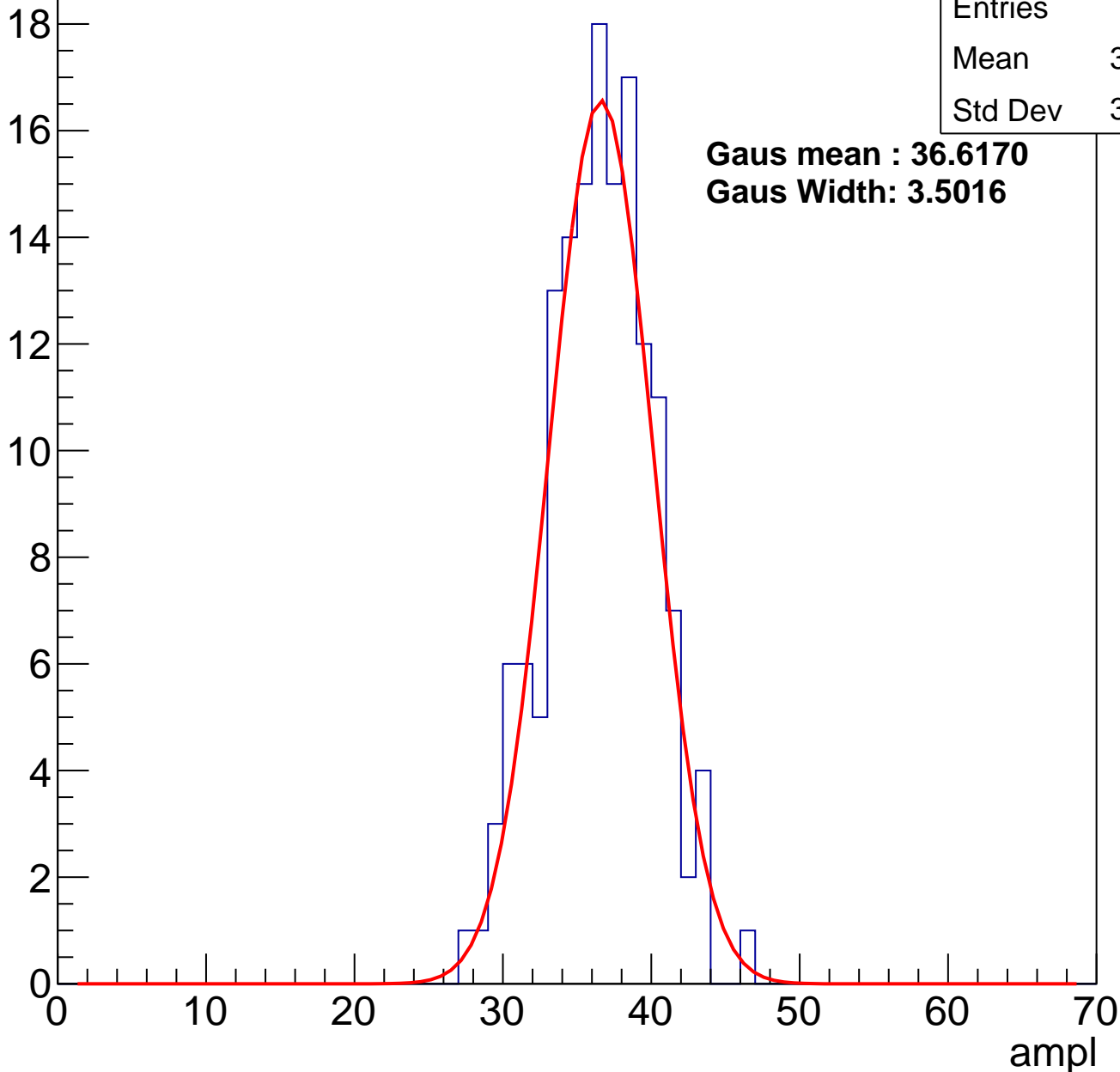
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	36.05
Std Dev	3.487

Gaus mean : 36.6170

Gaus Width: 3.5016

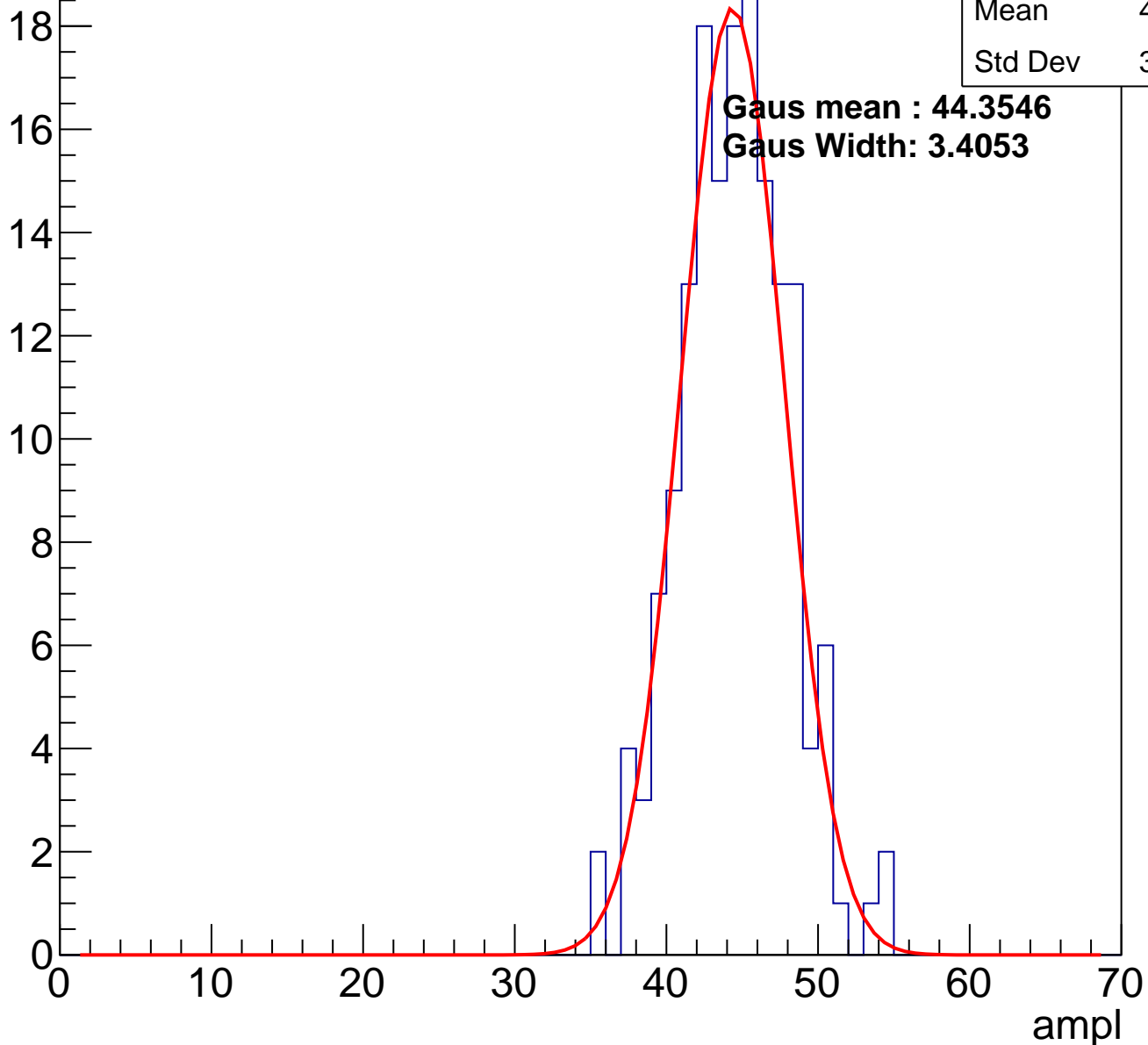
Entry



B0L101S, U25-ch63, adc2

calib_packv5_042523_0143.root, FC#1, port C1

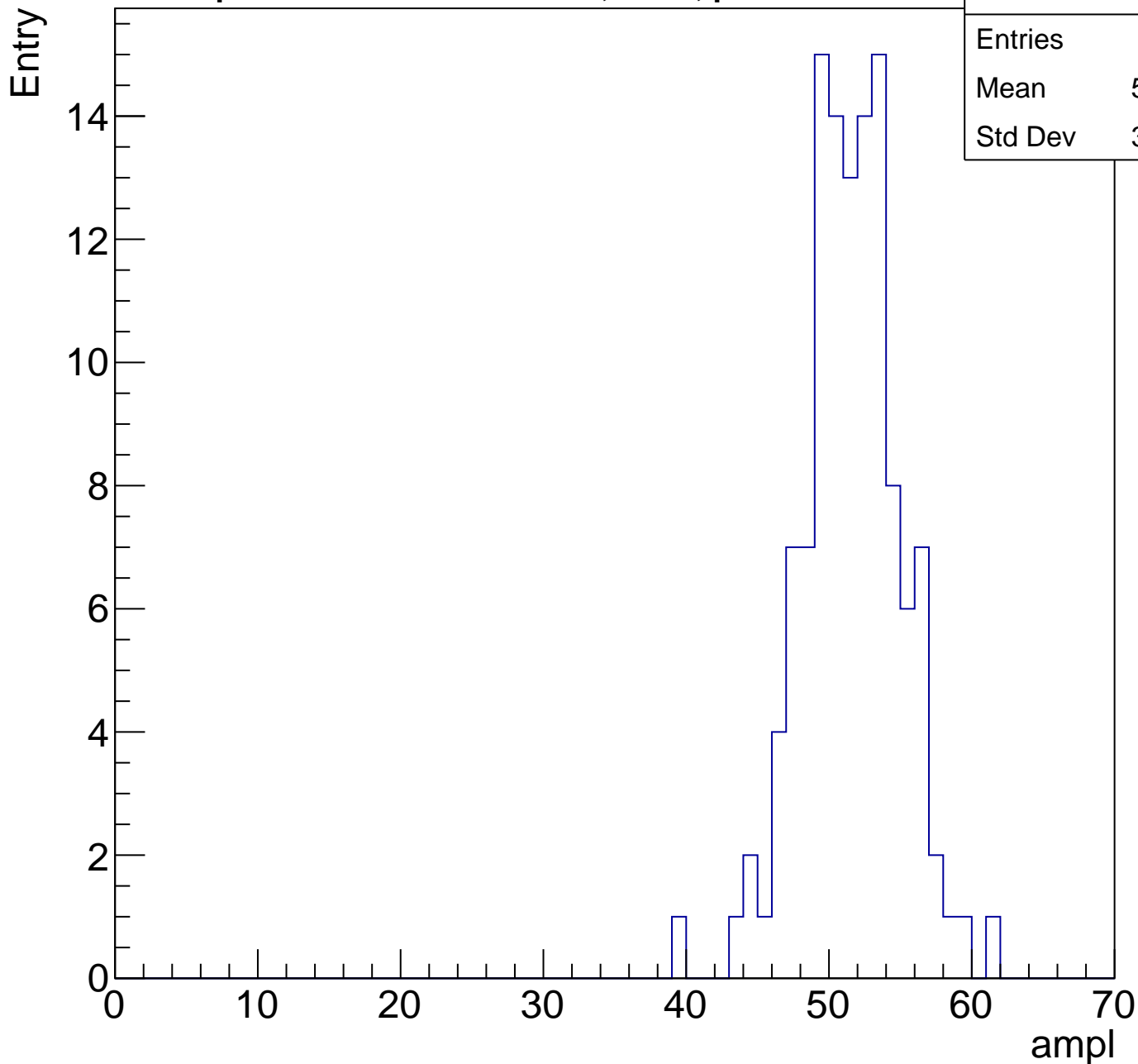
Entry



B0L101S, U25-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	51.09
Std Dev	3.406



B0L101S, U25-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

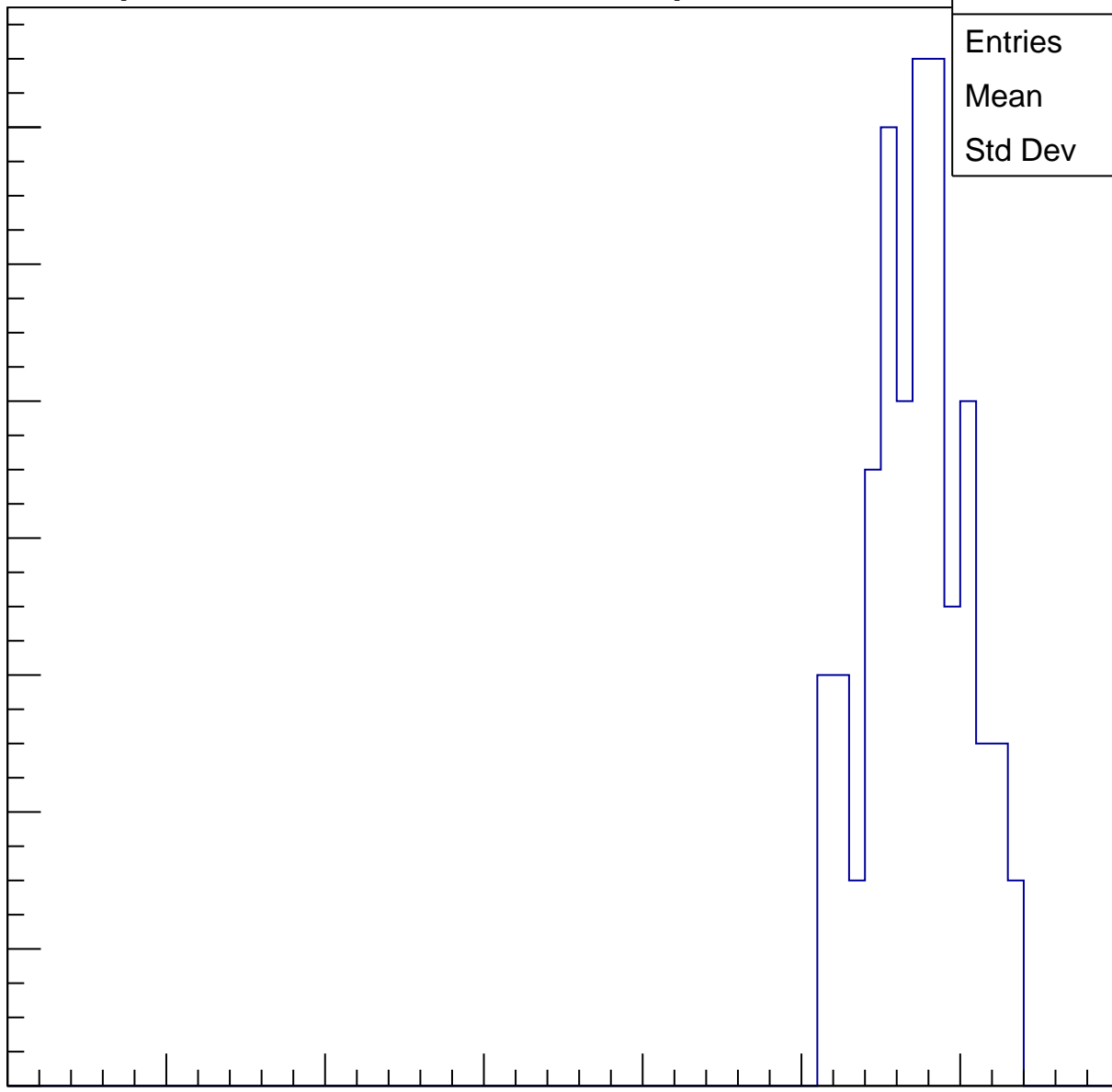
Entries	108
Mean	56.81
Std Dev	3.047

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch63, adc5

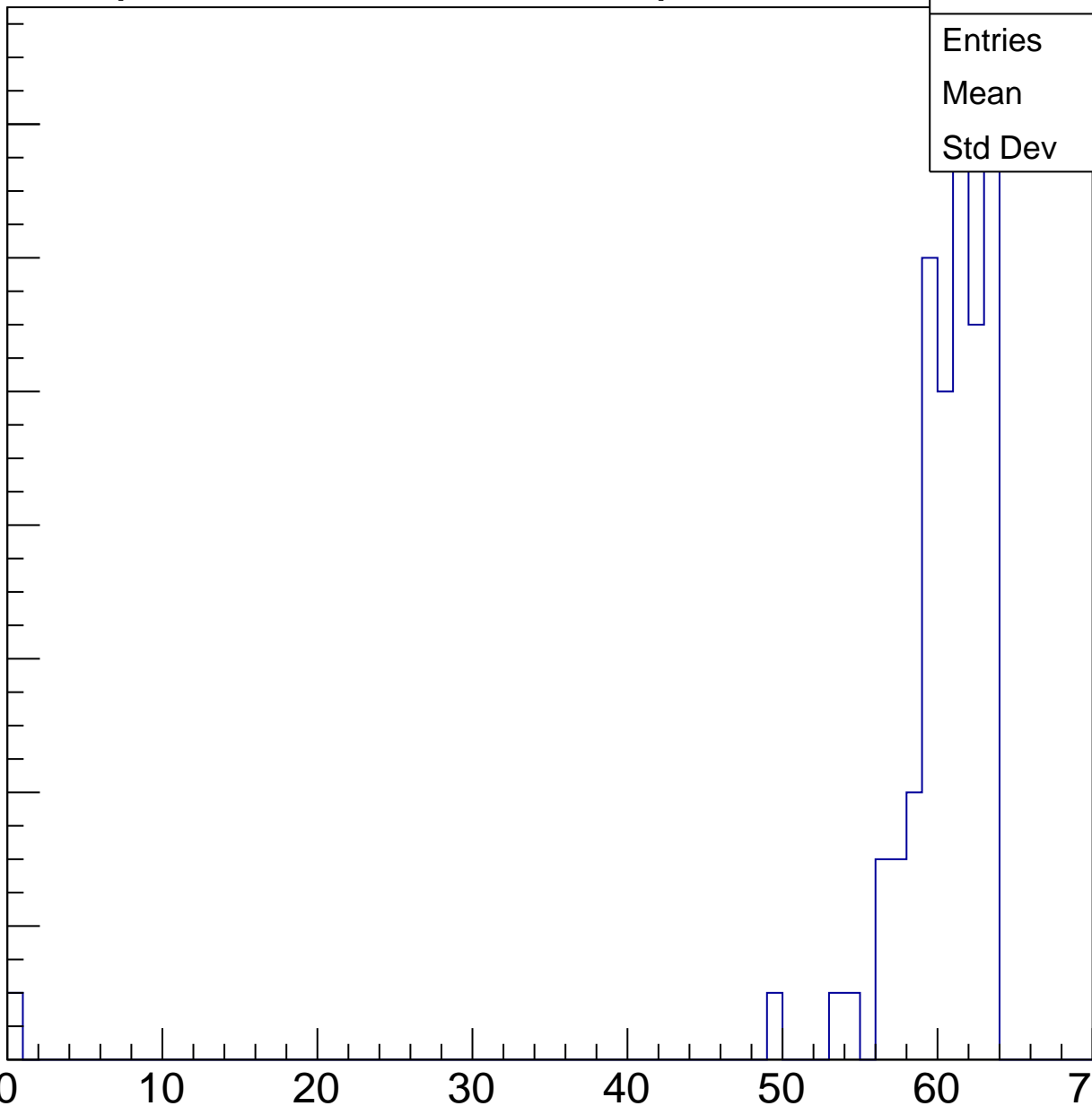
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.42
Std Dev	7.324

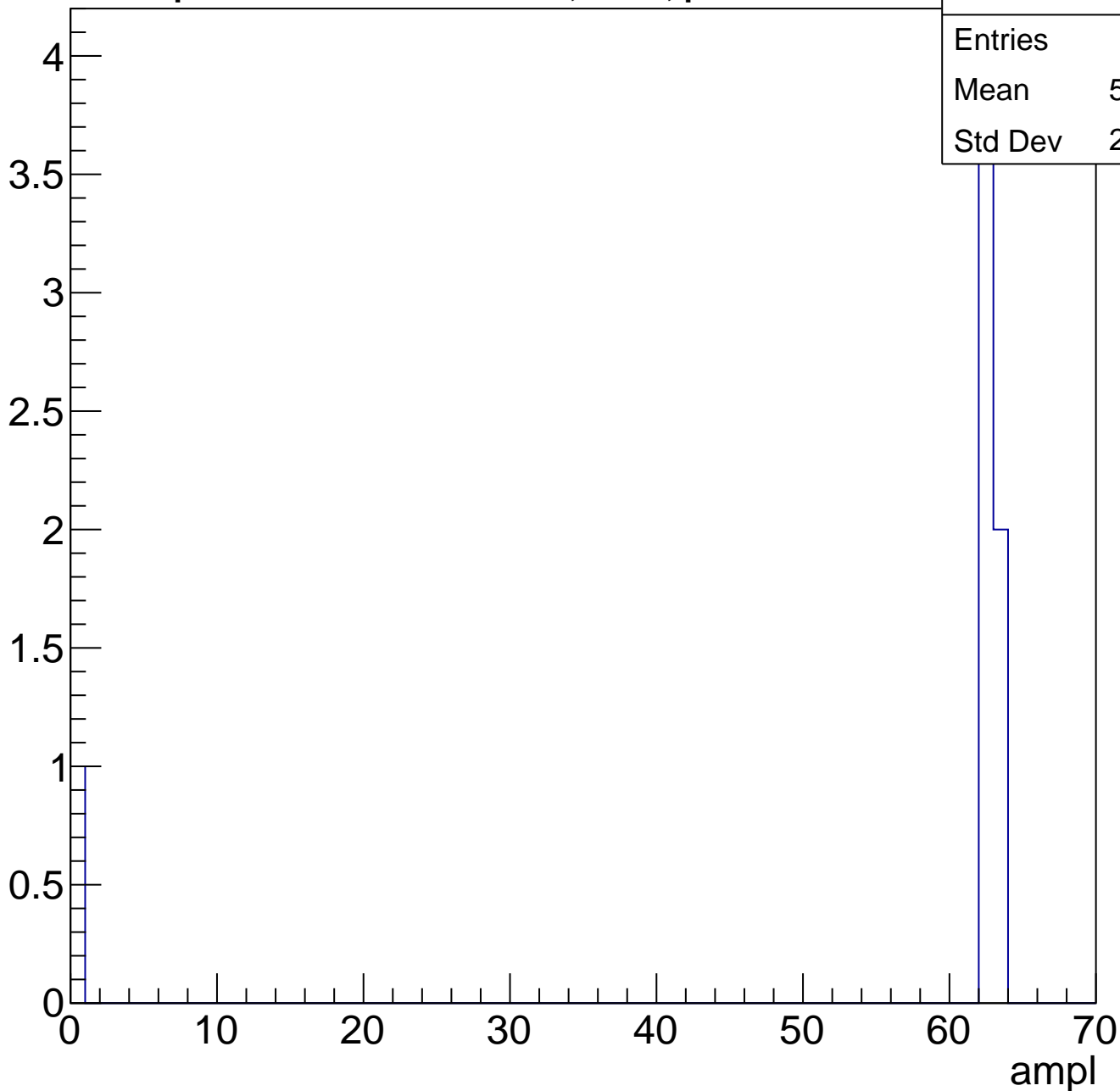
ampl



B0L101S, U25-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



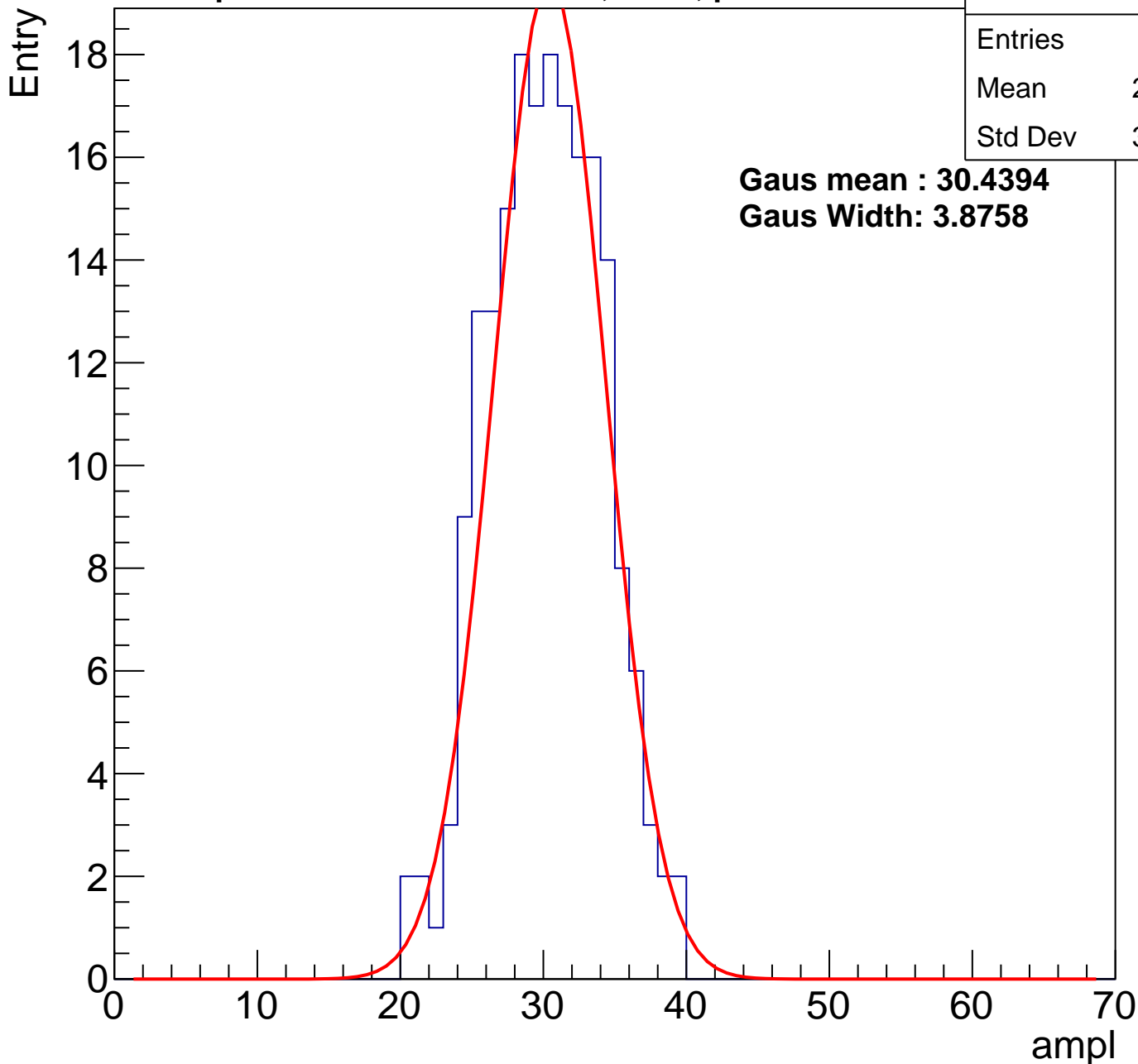
B0L101S, U25-ch64, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	195
Mean	29.73
Std Dev	3.884

Gaus mean : 30.4394

Gaus Width: 3.8758

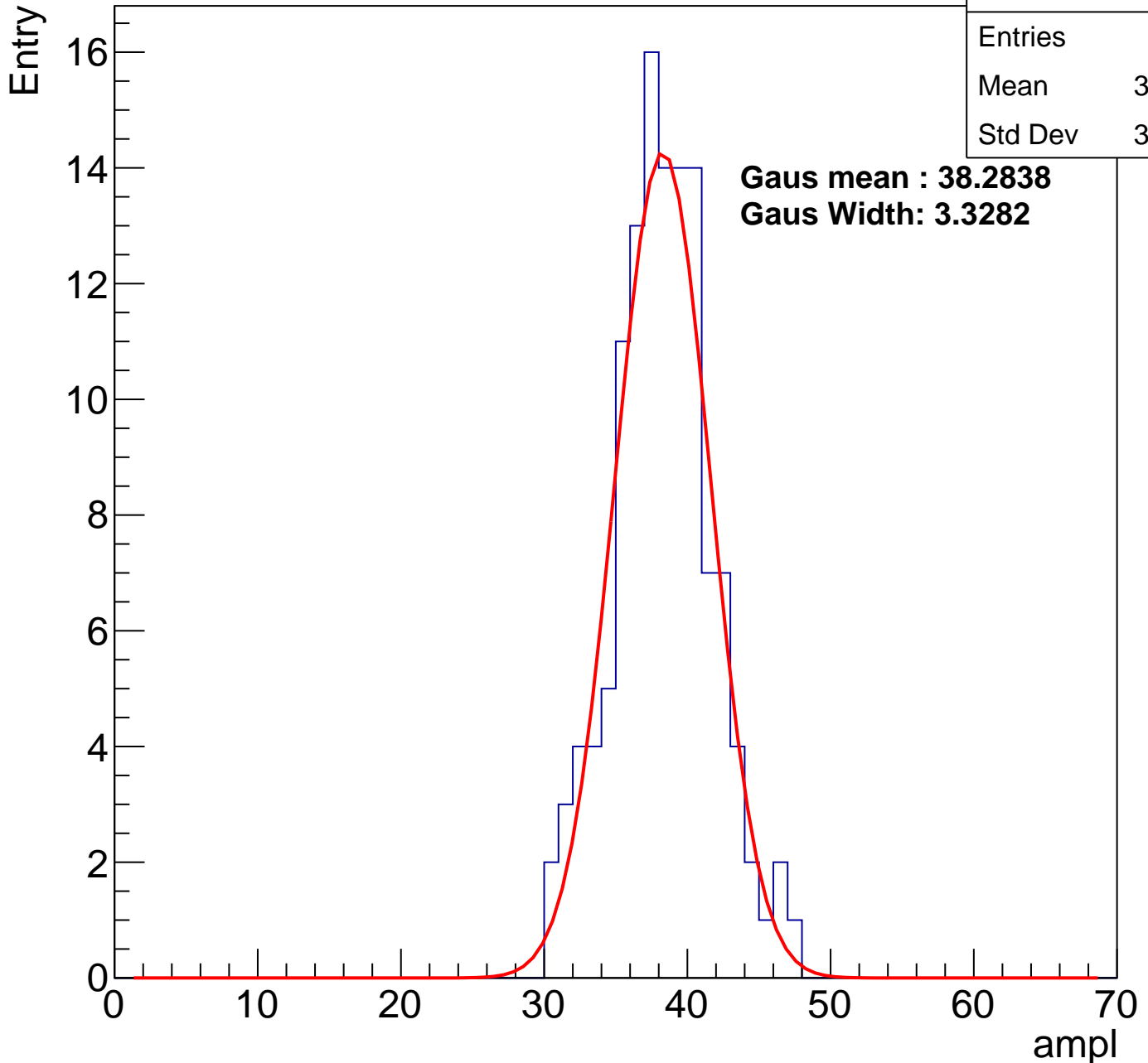


B0L101S, U25-ch64, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	37.83
Std Dev	3.405

Gaus mean : 38.2838
Gaus Width: 3.3282



B0L101S, U25-ch64, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

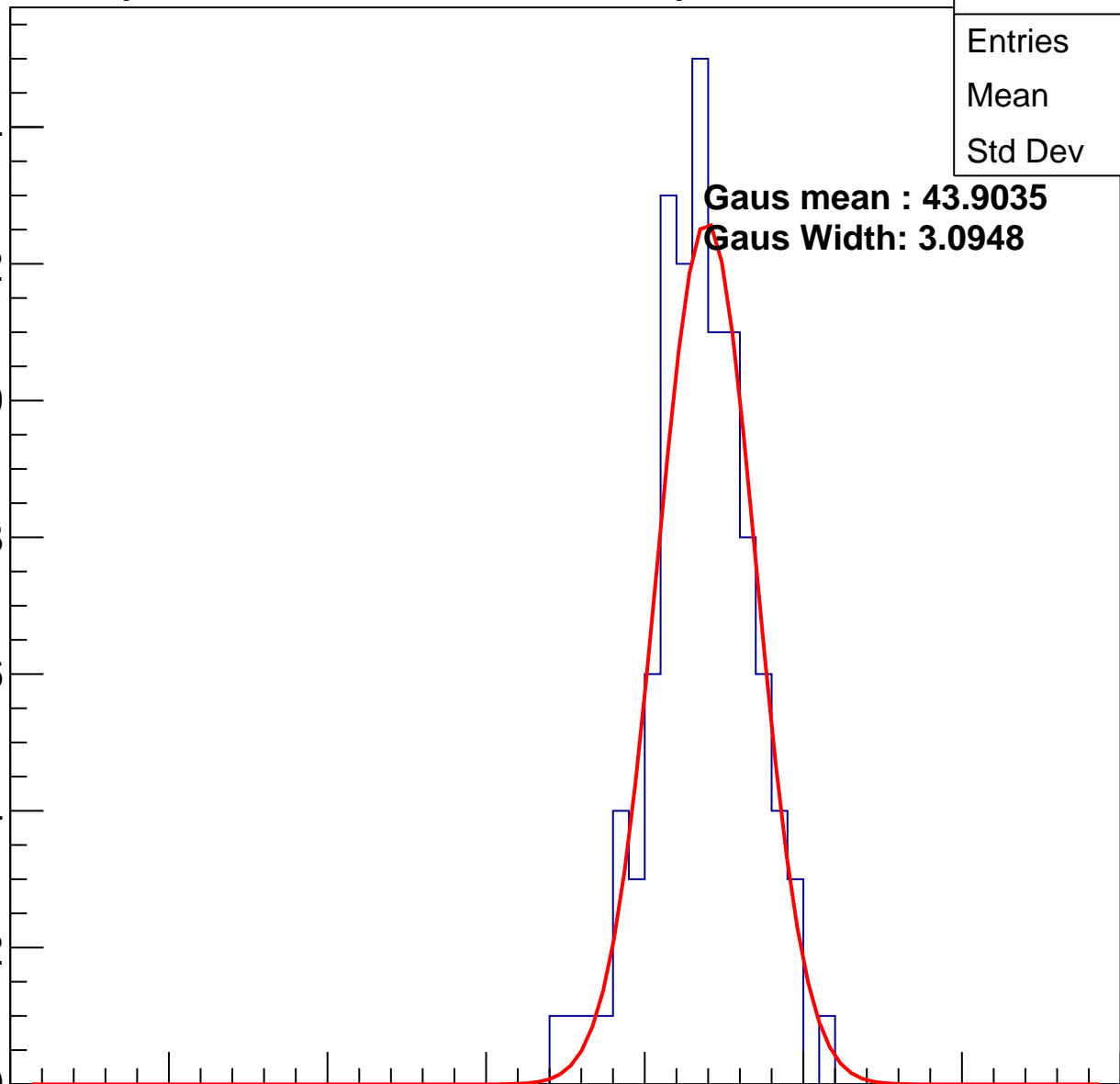
70

ampl

Entries	101
Mean	43.09
Std Dev	3.131

Gaus mean : 43.9035

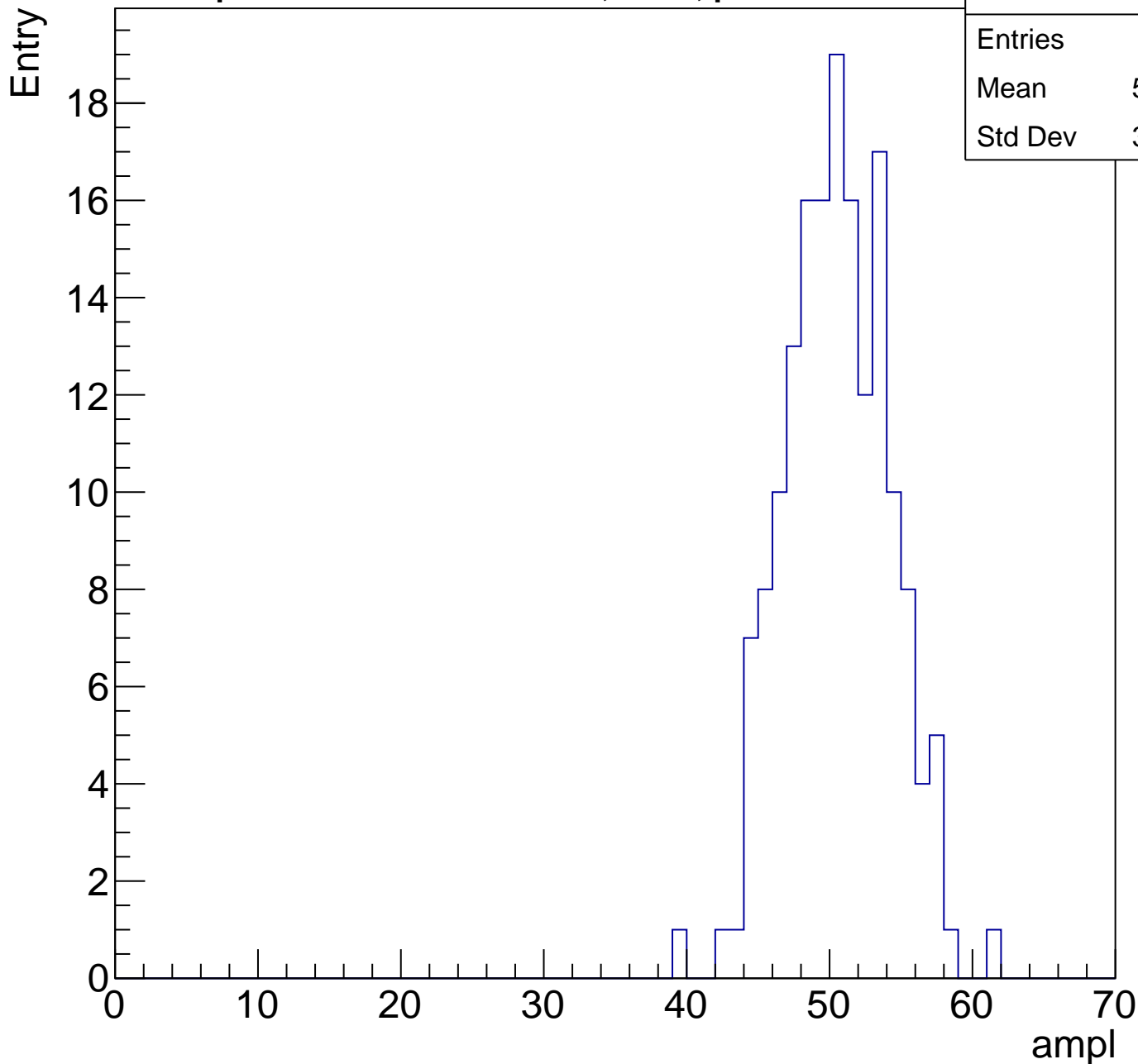
Gaus Width: 3.0948



B0L101S, U25-ch64, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	50.08
Std Dev	3.635



B0L101S, U25-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

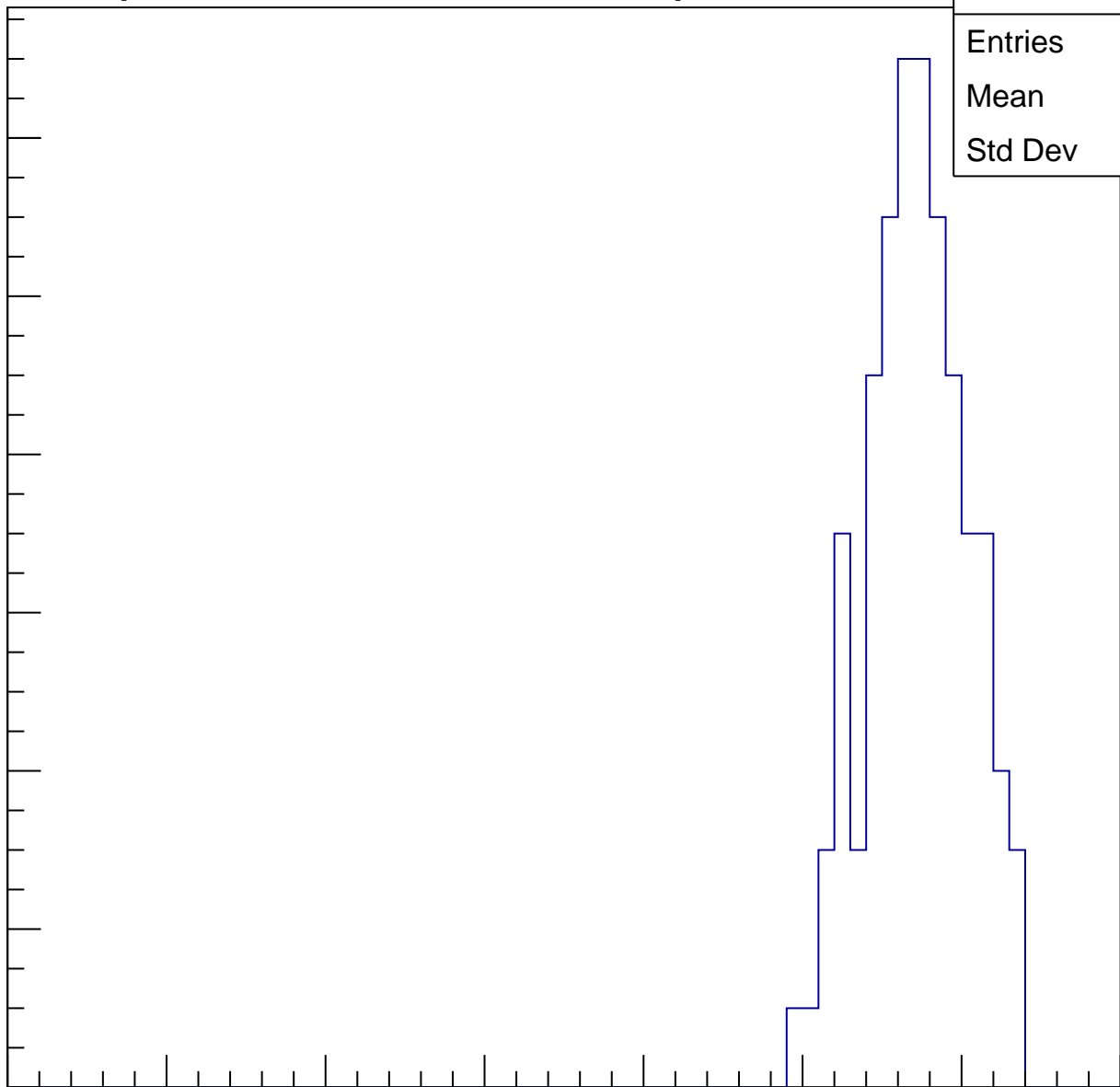
50

60

70

ampl

Entries	102
Mean	56.75
Std Dev	3.14



B0L101S, U25-ch64, adc5

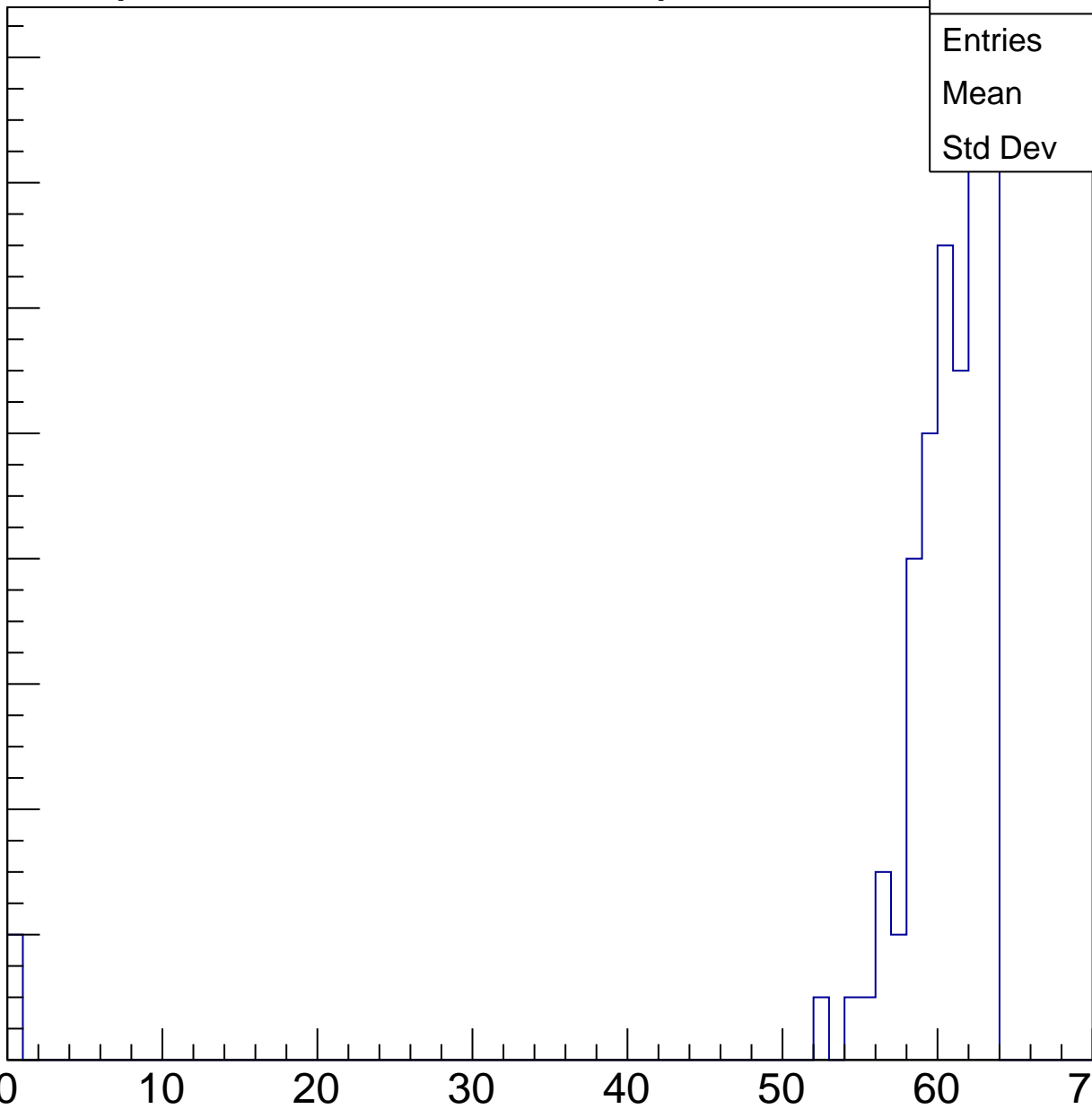
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.87
Std Dev	9.532

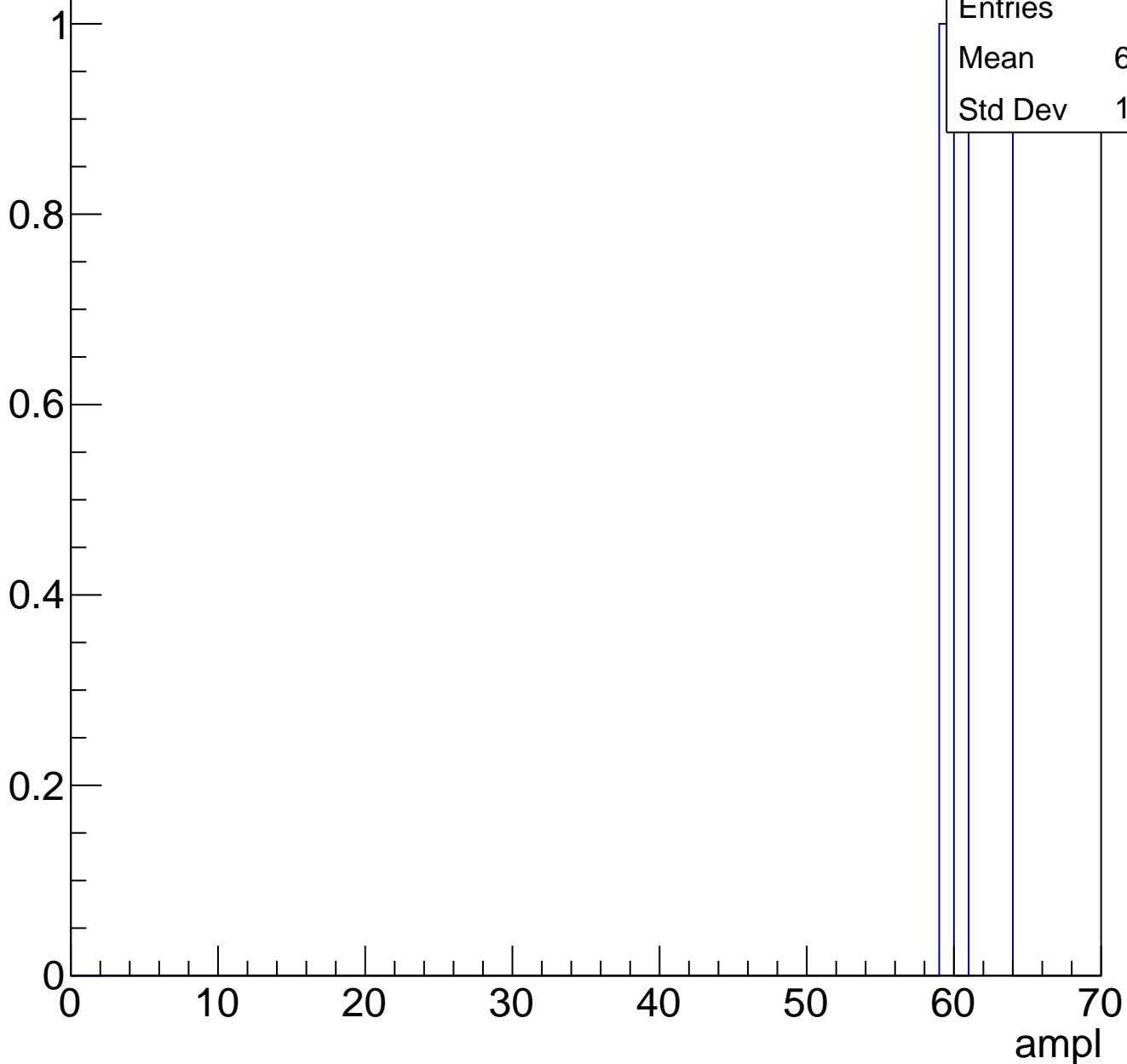
ampl



B0L101S, U25-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

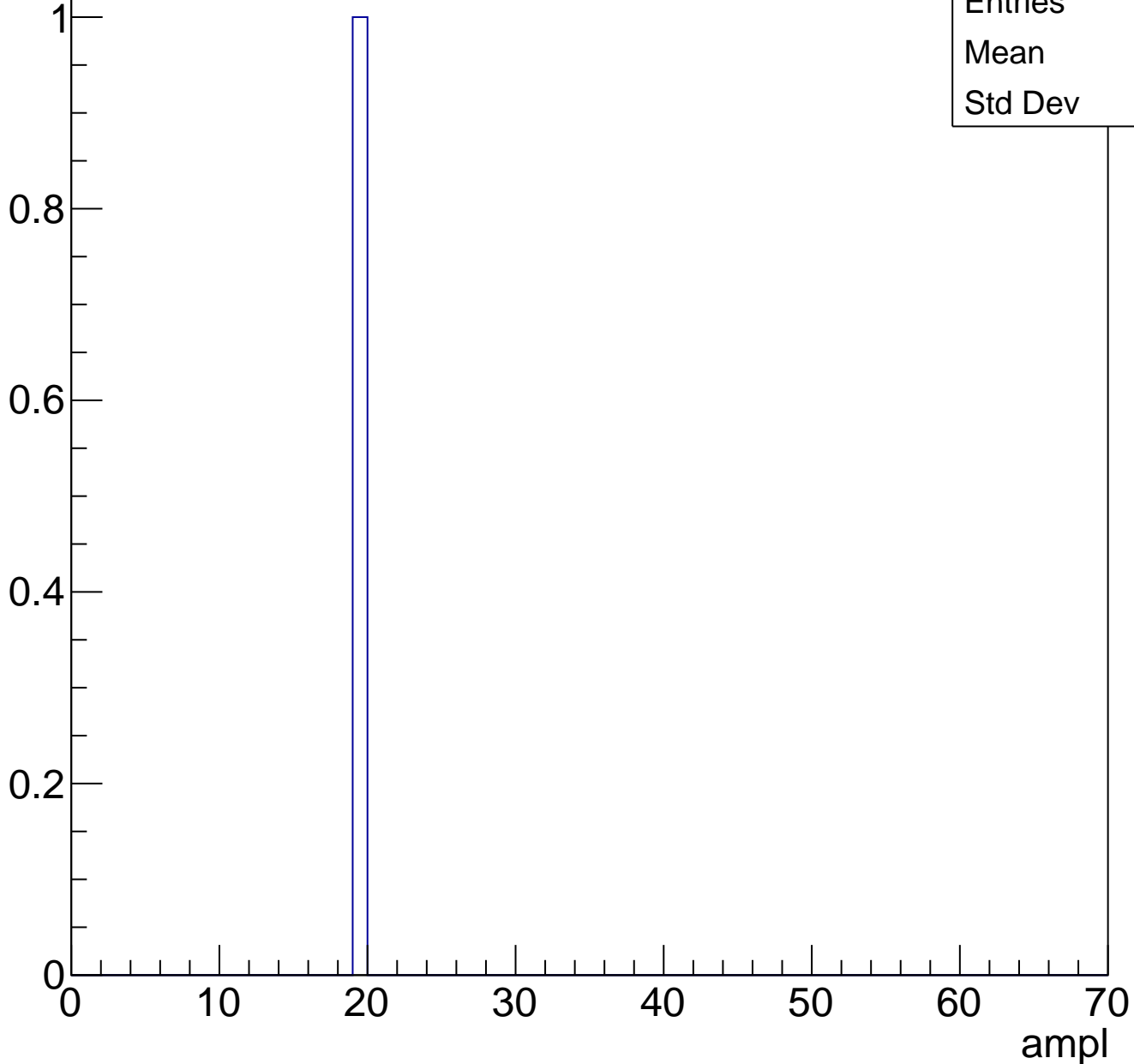
Entry



B0L101S, U25-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch65, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	28.86
Std Dev	3.838

Gaus mean : 29.1721

Gaus Width: 4.0076

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

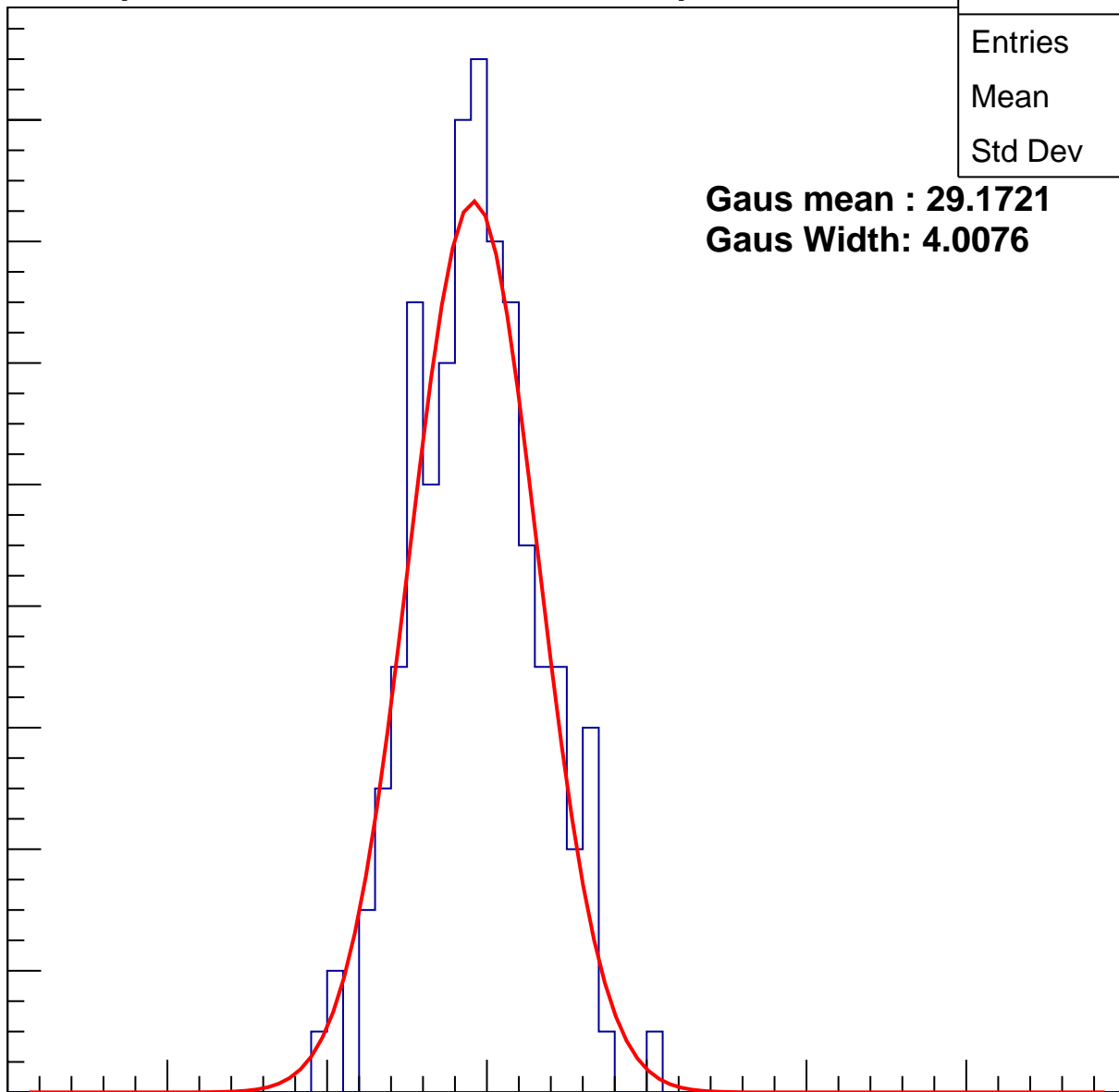
40

50

60

70

ampl



B0L101S, U25-ch65, adc1

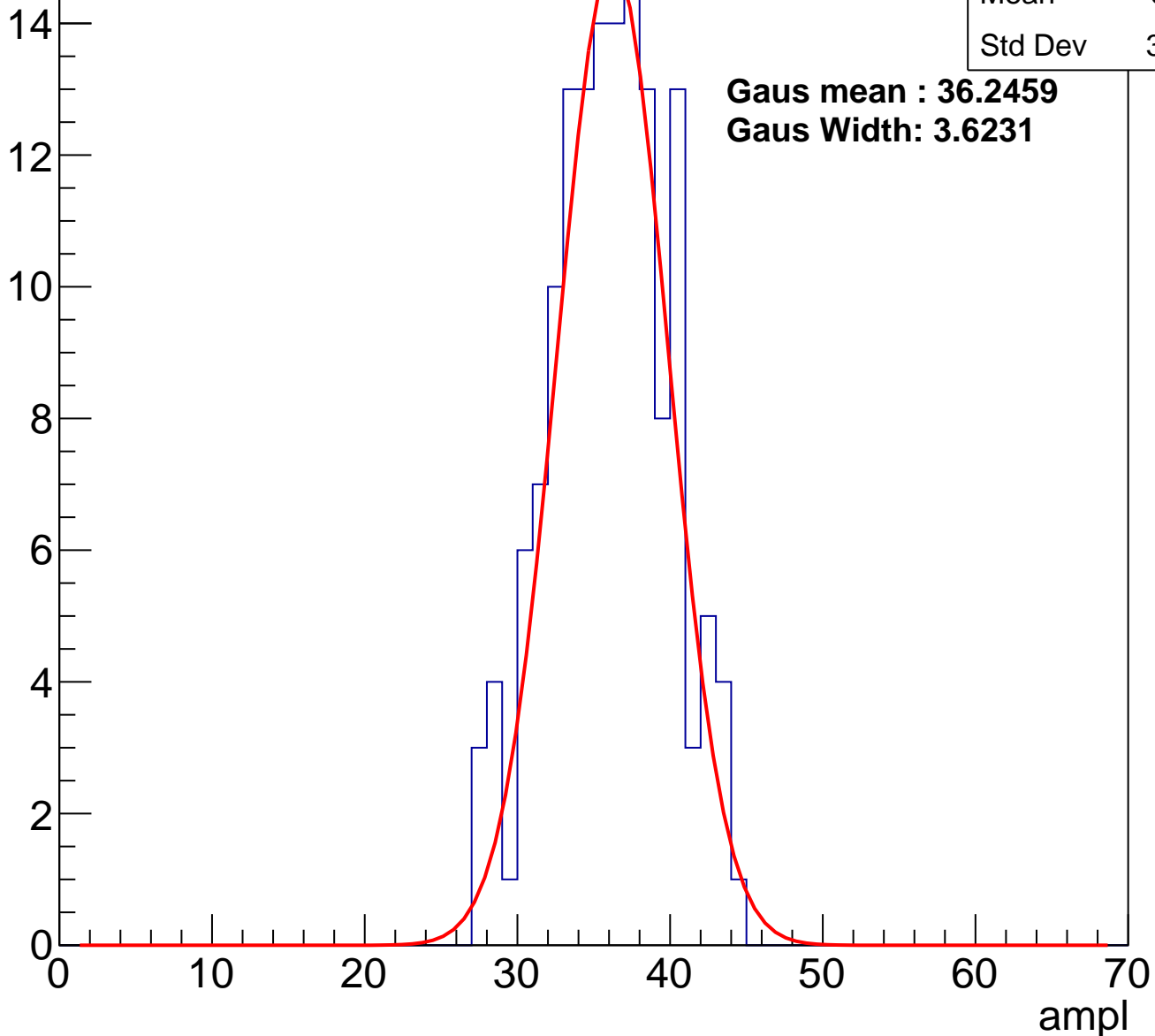
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	35.61
Std Dev	3.799

Gaus mean : 36.2459

Gaus Width: 3.6231

Entry



B0L101S, U25-ch65, adc2

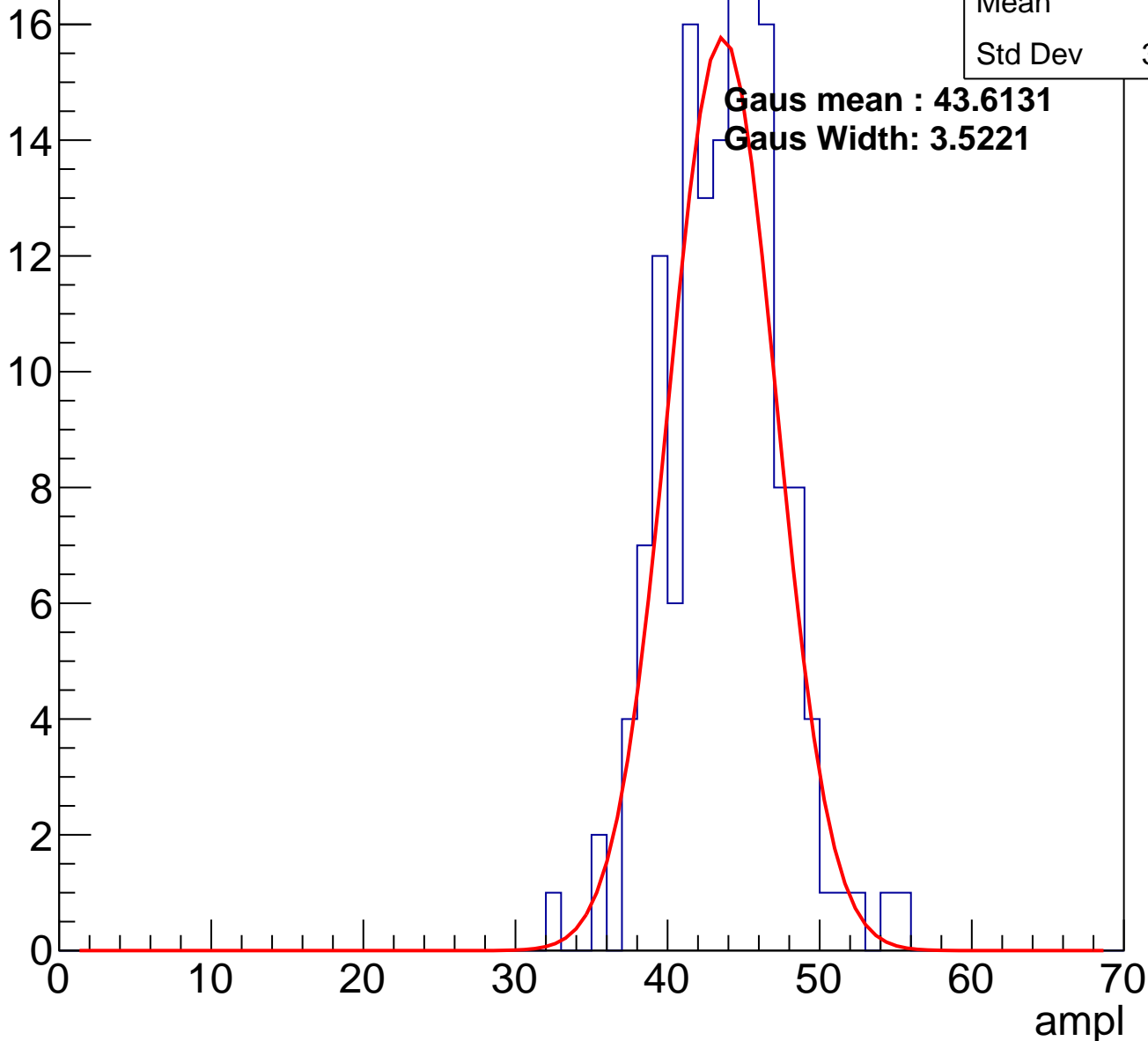
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	150
Mean	43.3
Std Dev	3.686

Gaus mean : 43.6131

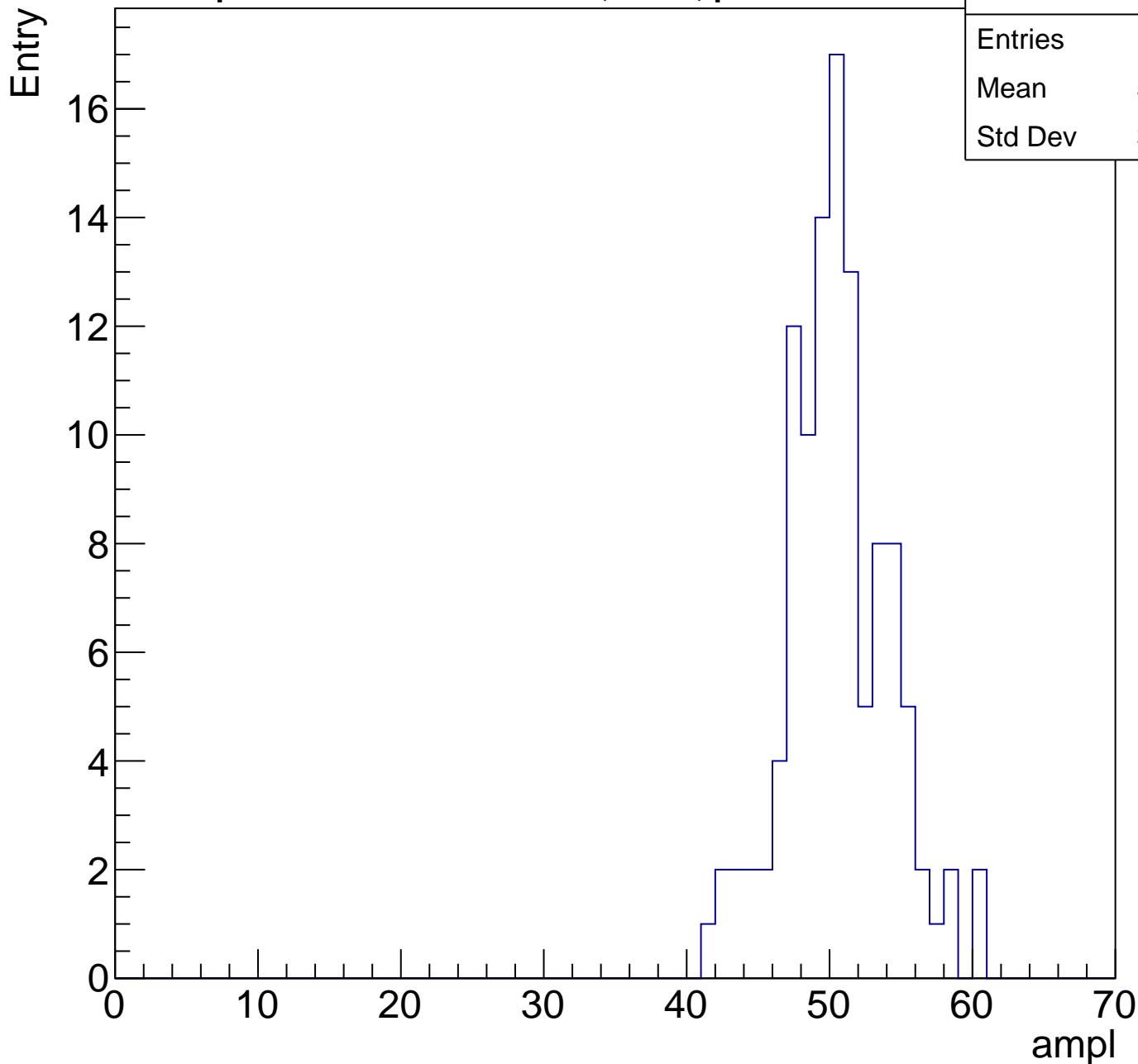
Gaus Width: 3.5221



B0L101S, U25-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	50.11
Std Dev	3.611



B0L101S, U25-ch65, adc4

calib_packv5_042523_0143.root, FC#1, port C1

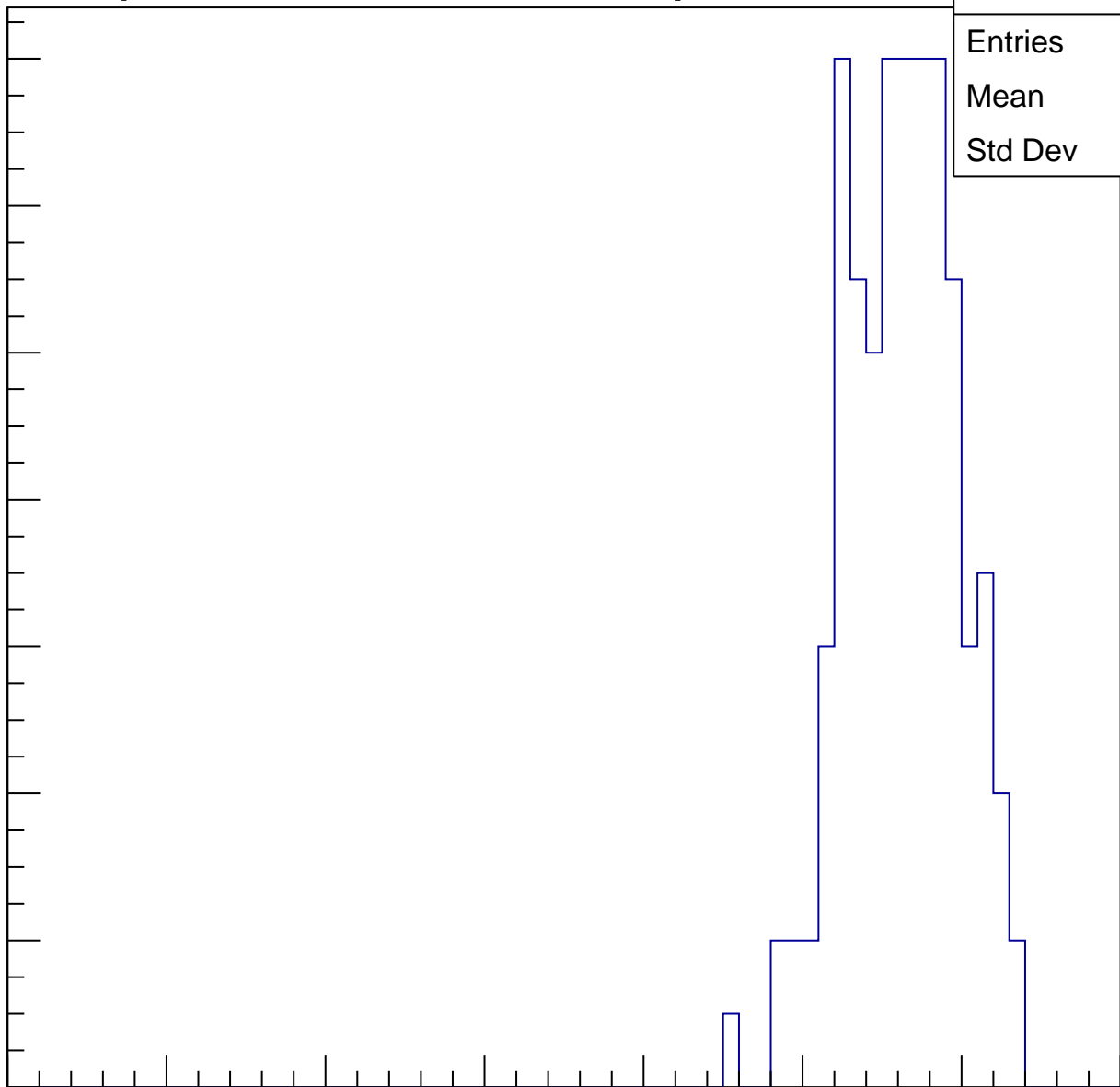
Entries	134
Mean	55.75
Std Dev	3.476

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch65, adc5

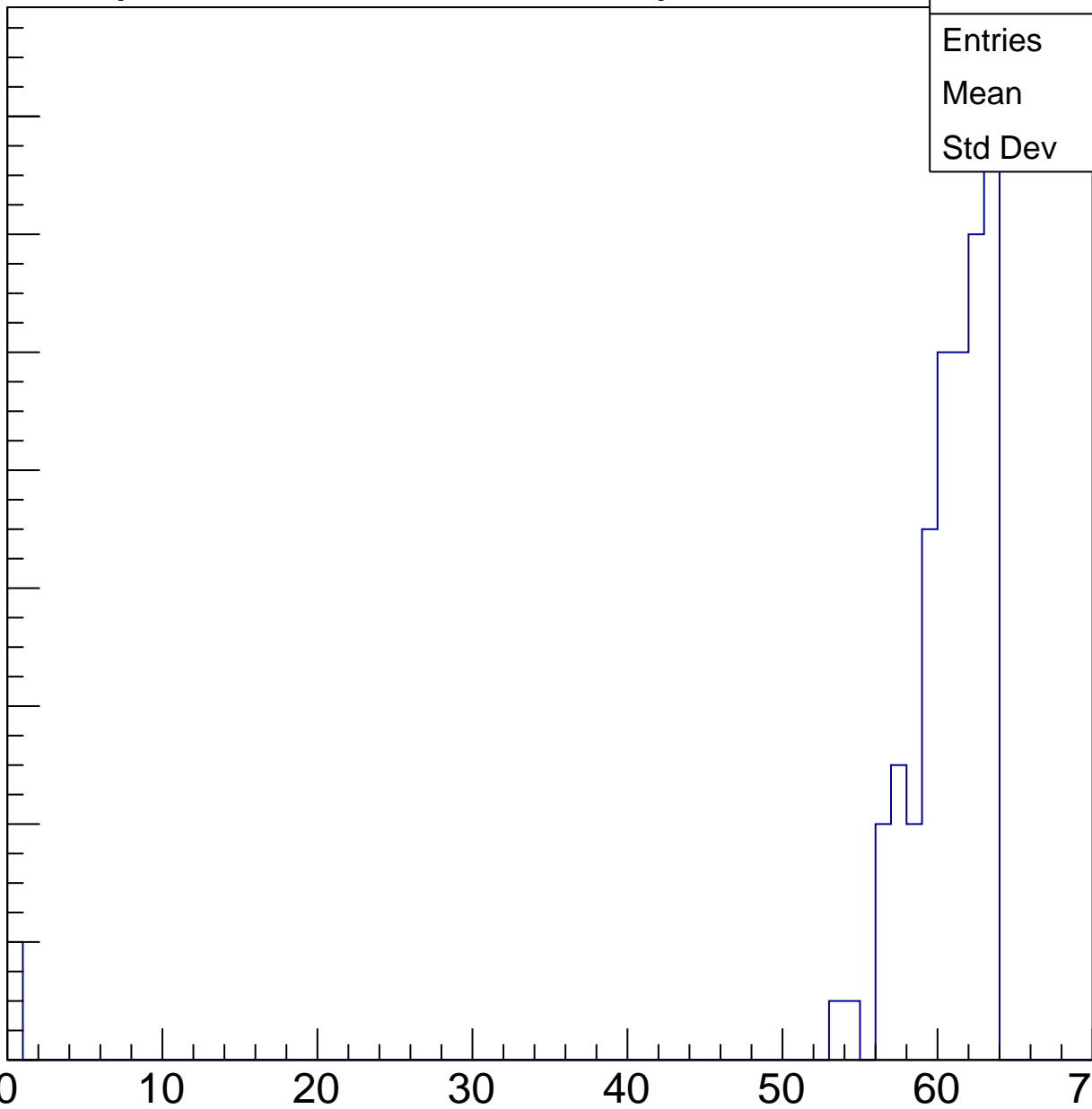
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.89
Std Dev	9.648

ampl



B0L101S, U25-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch66, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	28.68
Std Dev	4.83

Gaus mean : 29.4117

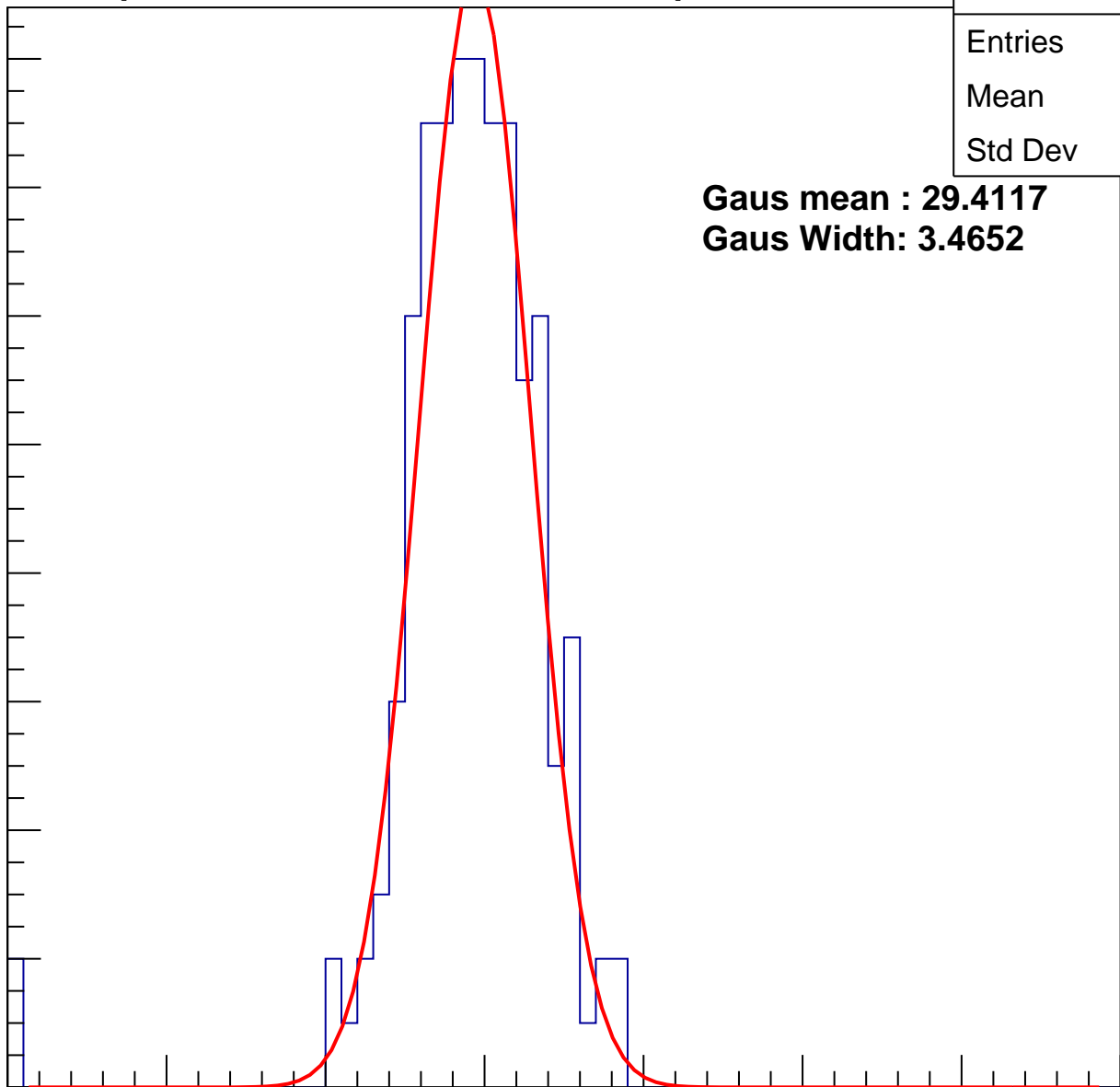
Gaus Width: 3.4652

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch66, adc1

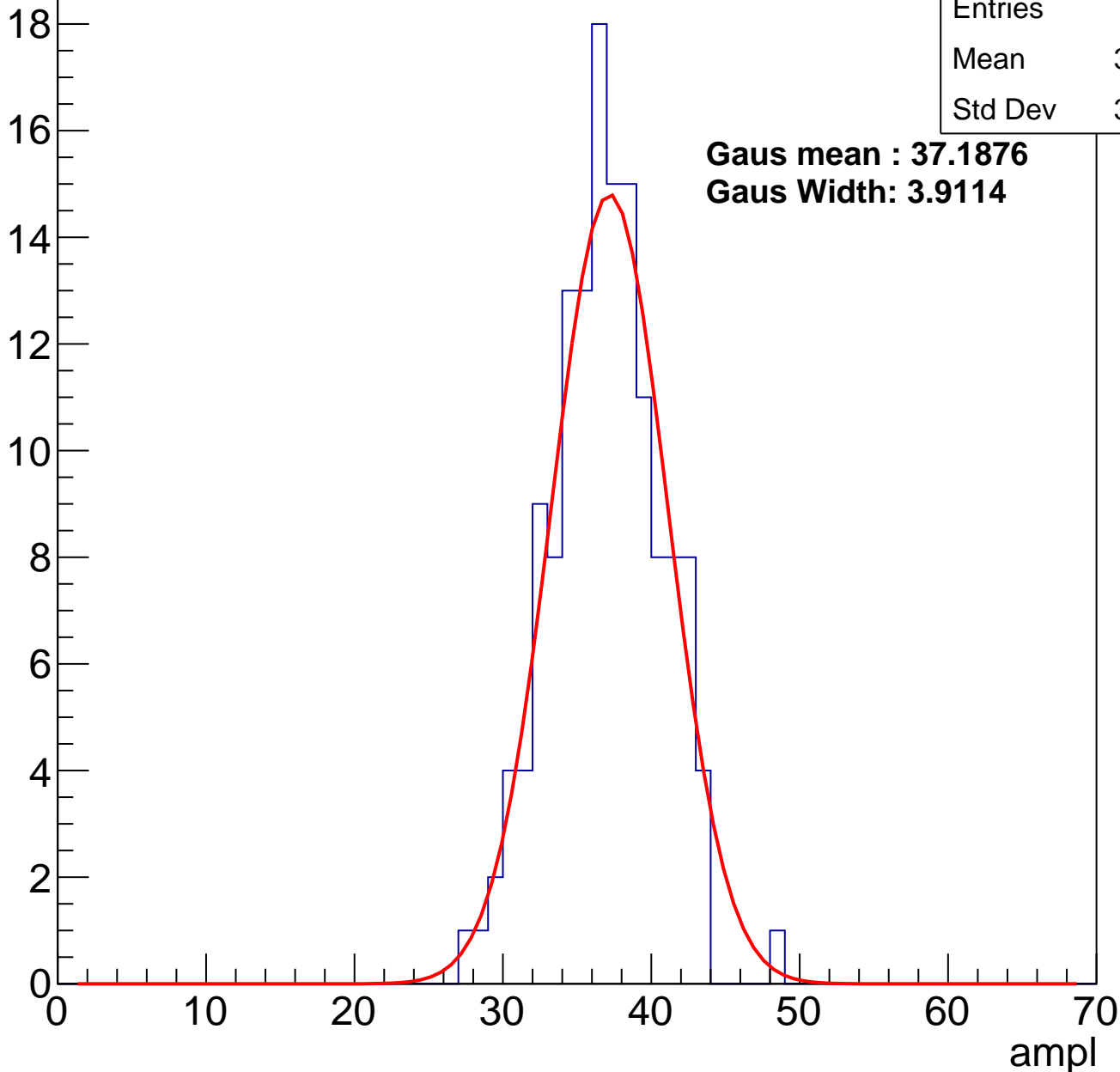
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	36.45
Std Dev	3.616

Gaus mean : 37.1876

Gaus Width: 3.9114

Entry



B0L101S, U25-ch66, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	115
Mean	43.02
Std Dev	3.375

Gaus mean : 43.7346

Gaus Width: 3.4470

0

10

20

30

40

50

60

70

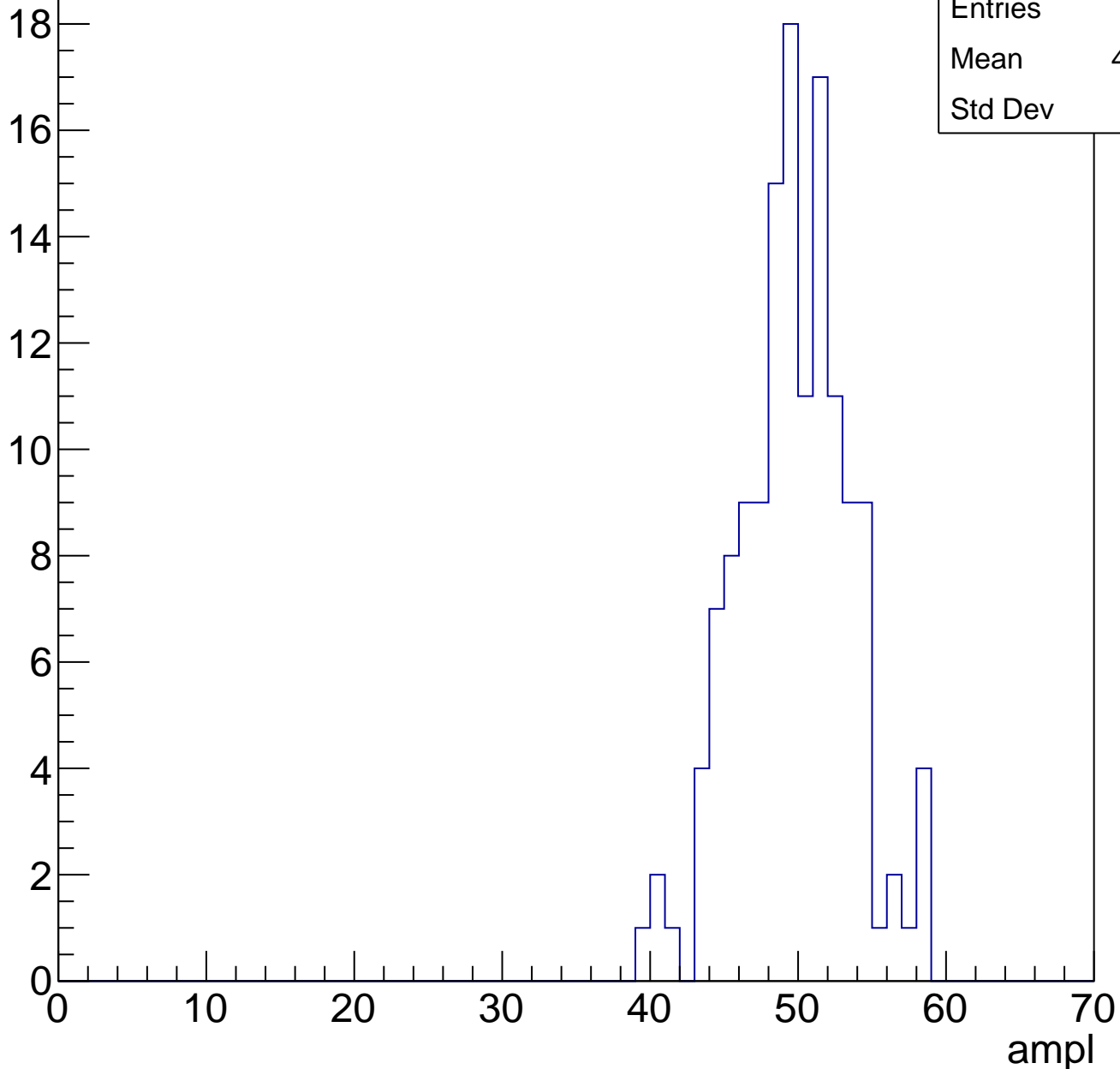
ampl

B0L101S, U25-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	49.26
Std Dev	3.76

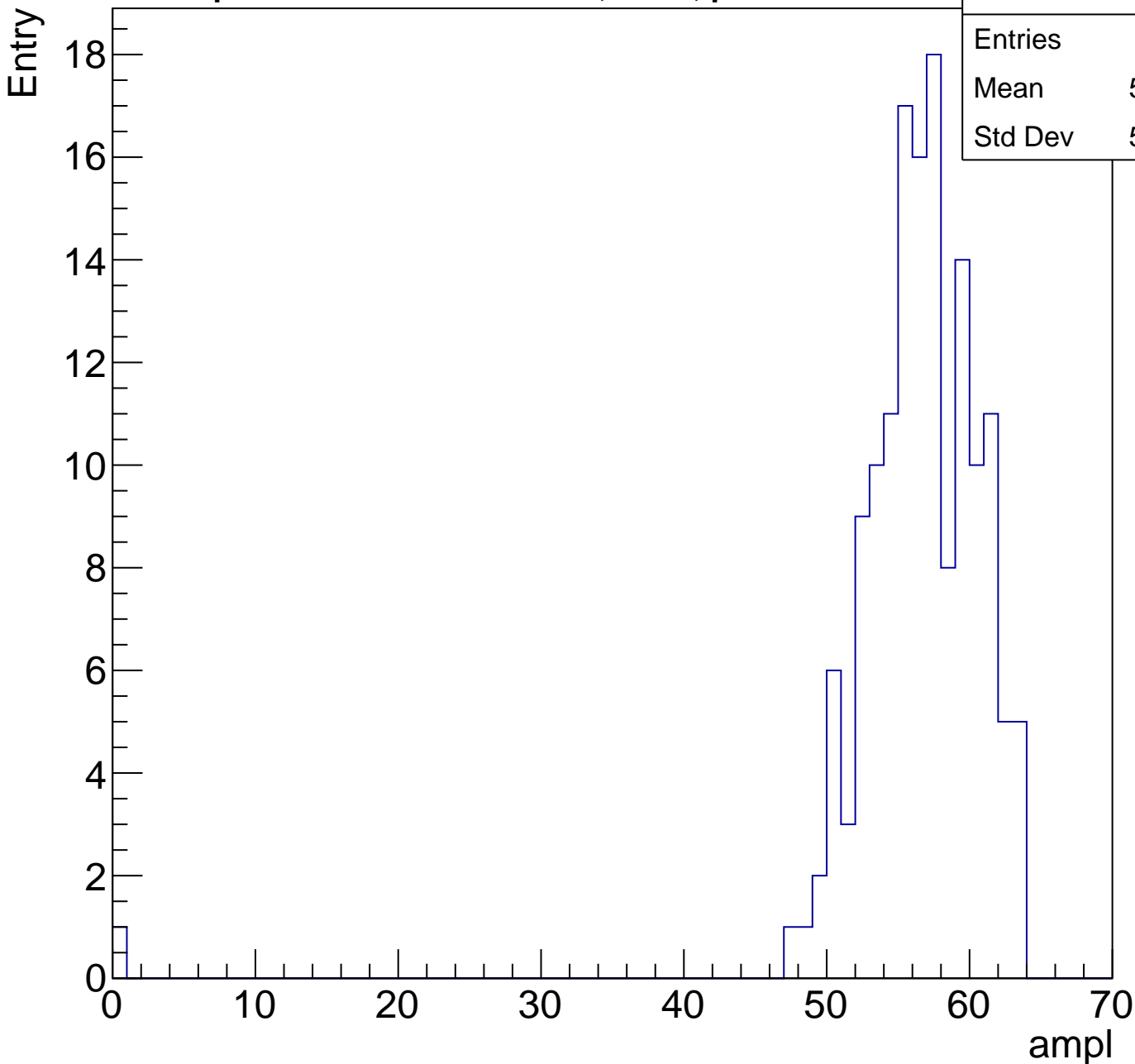
Entry



B0L101S, U25-ch66, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	55.95
Std Dev	5.813



B0L101S, U25-ch66, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

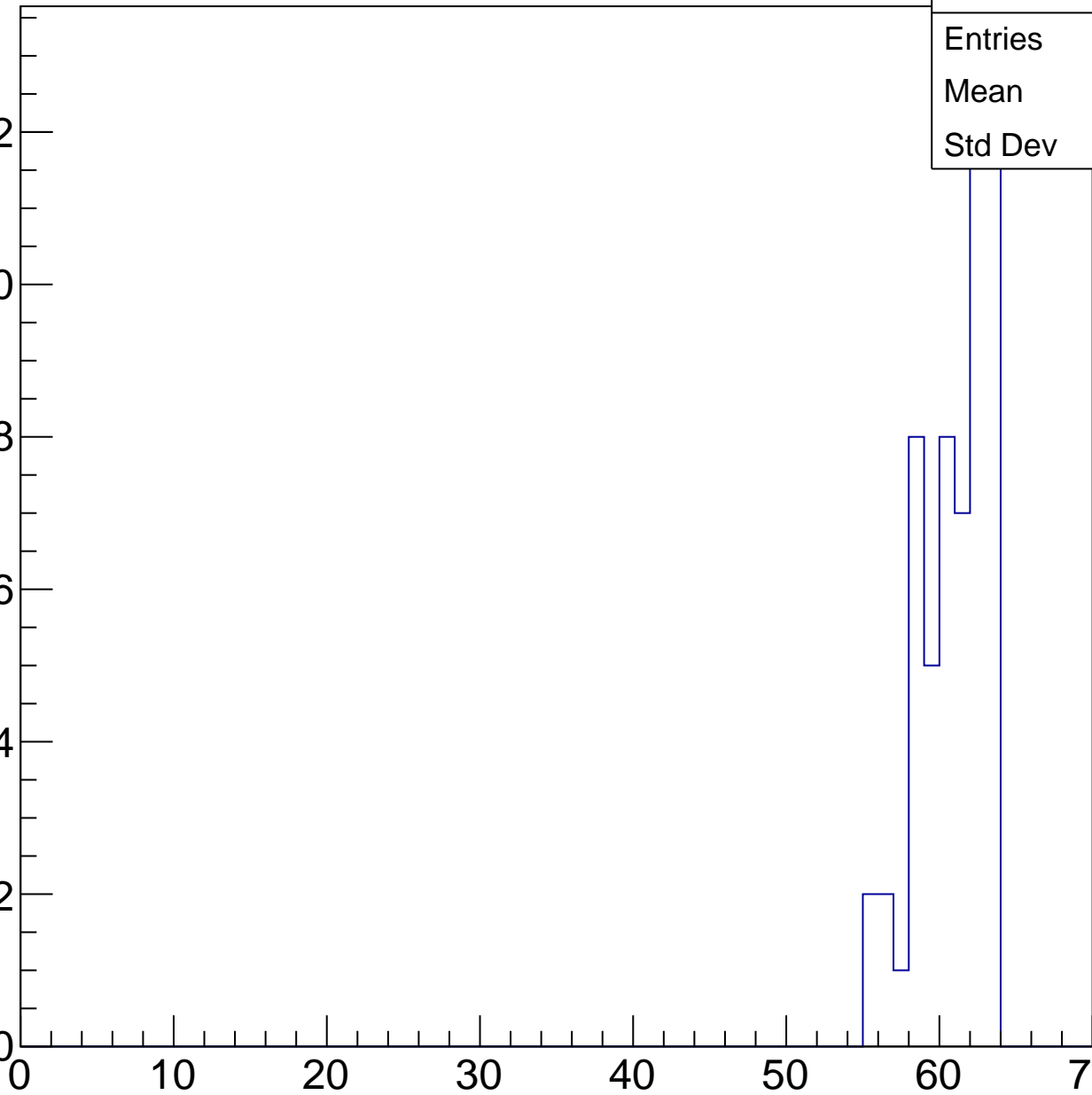
4

2

0

Entries	58
Mean	60.48
Std Dev	2.222

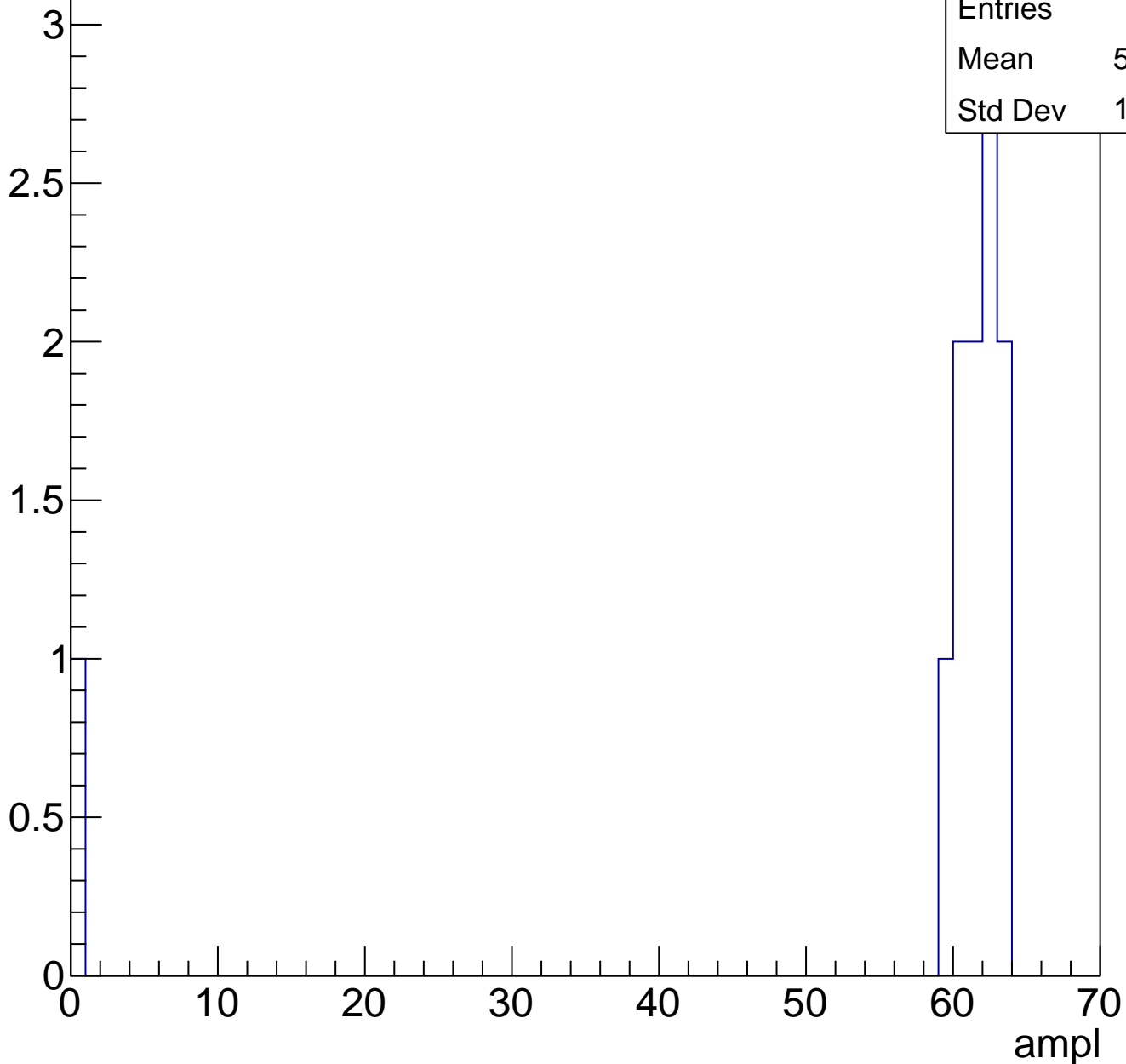
ampl



B0L101S, U25-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch67, adc0

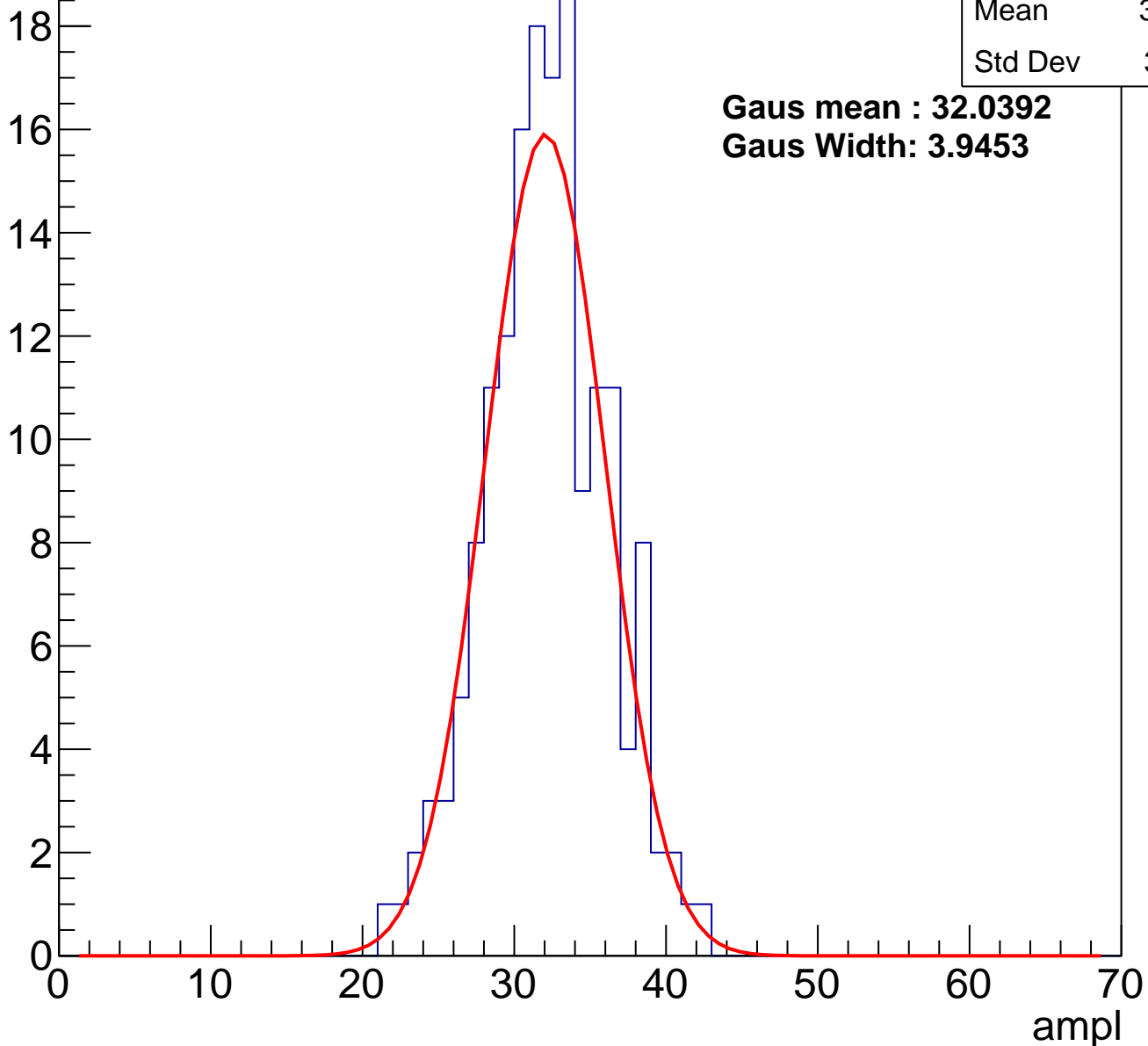
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	31.68
Std Dev	3.951

Gaus mean : 32.0392

Gaus Width: 3.9453

Entry



B0L101S, U25-ch67, adc1

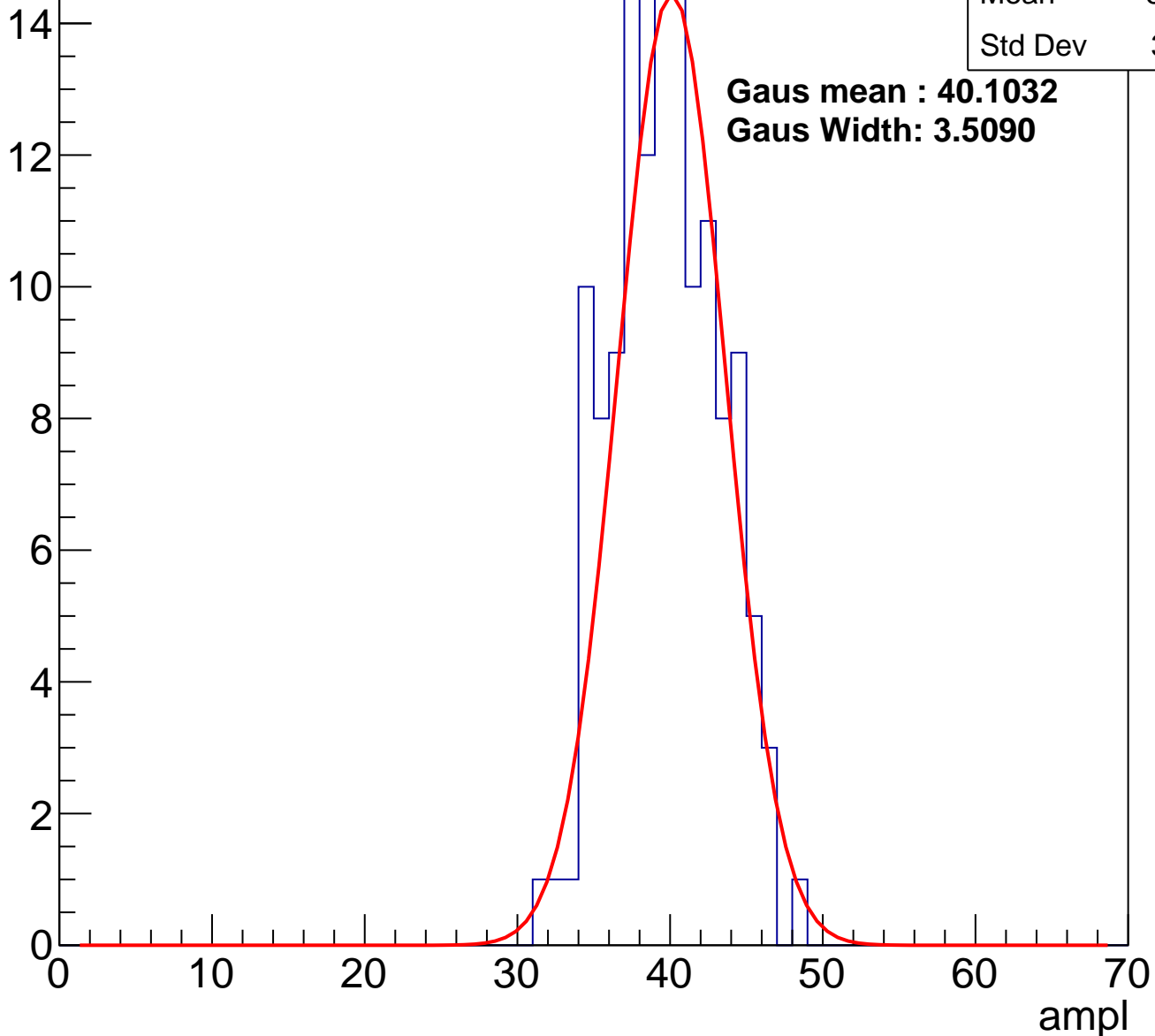
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	39.25
Std Dev	3.441

Gaus mean : 40.1032

Gaus Width: 3.5090

Entry



B0L101S, U25-ch67, adc2

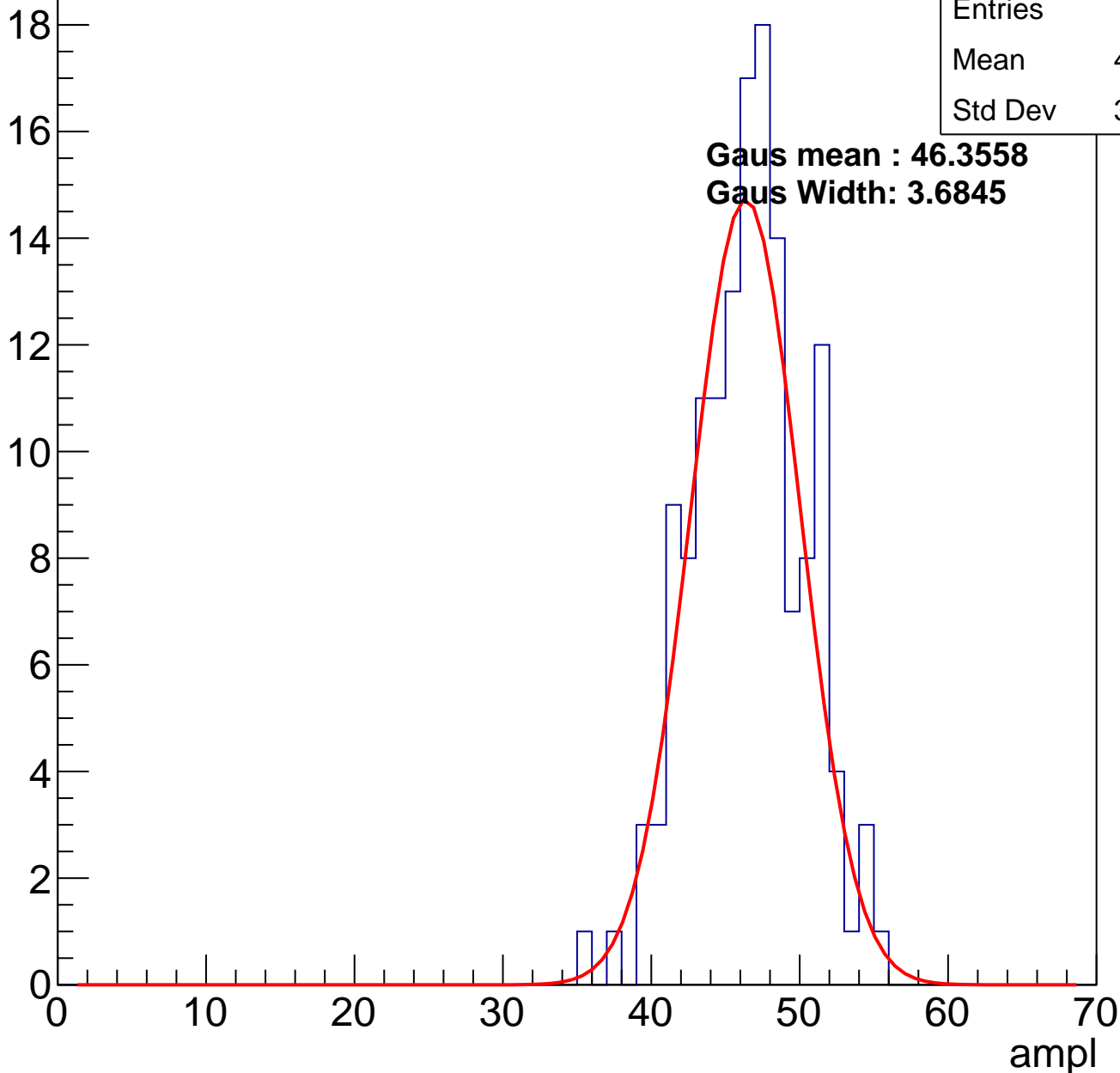
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	145
Mean	46.13
Std Dev	3.709

Gaus mean : 46.3558

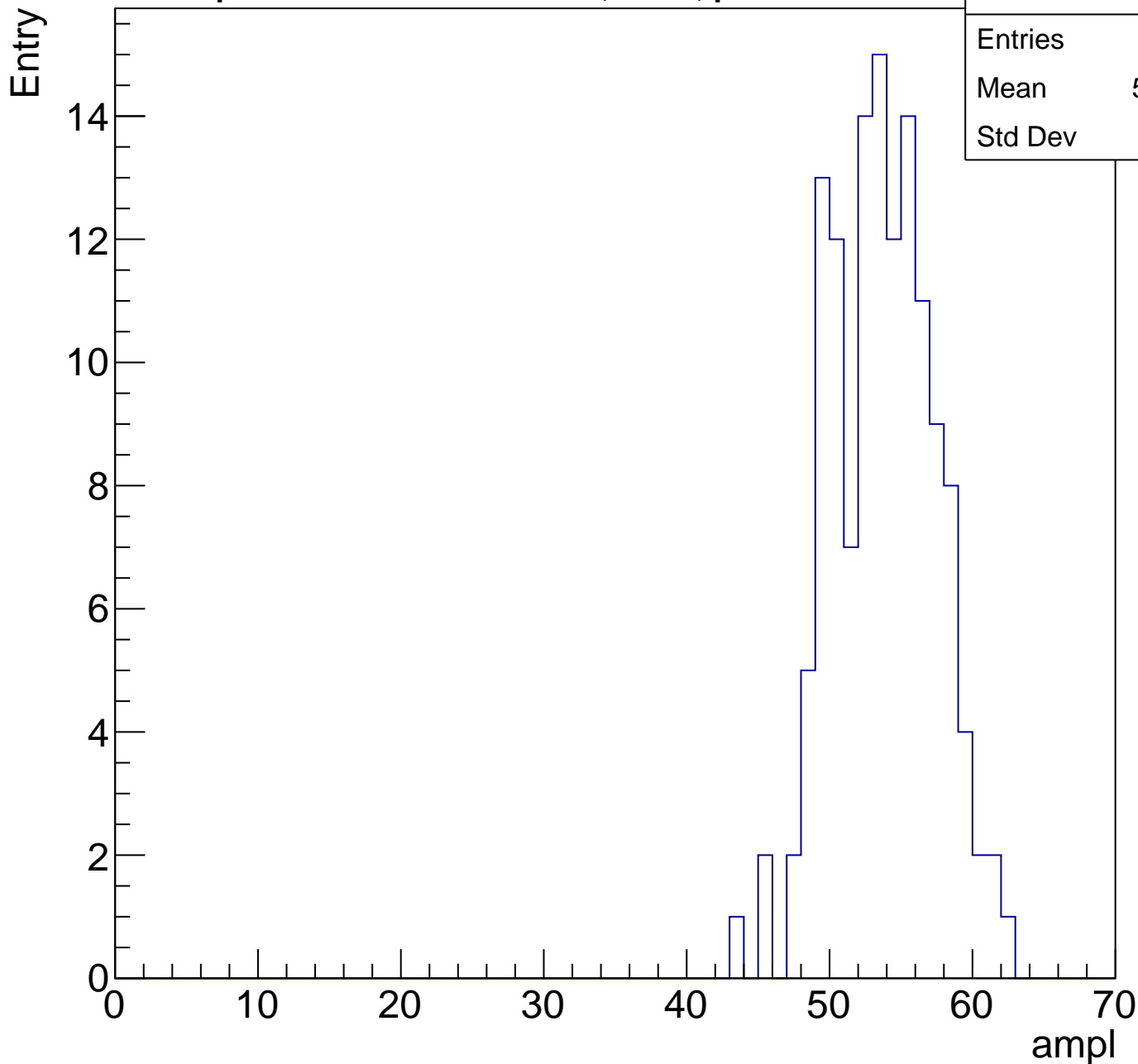
Gaus Width: 3.6845



B0L101S, U25-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	53.25
Std Dev	3.6



B0L101S, U25-ch67, adc4

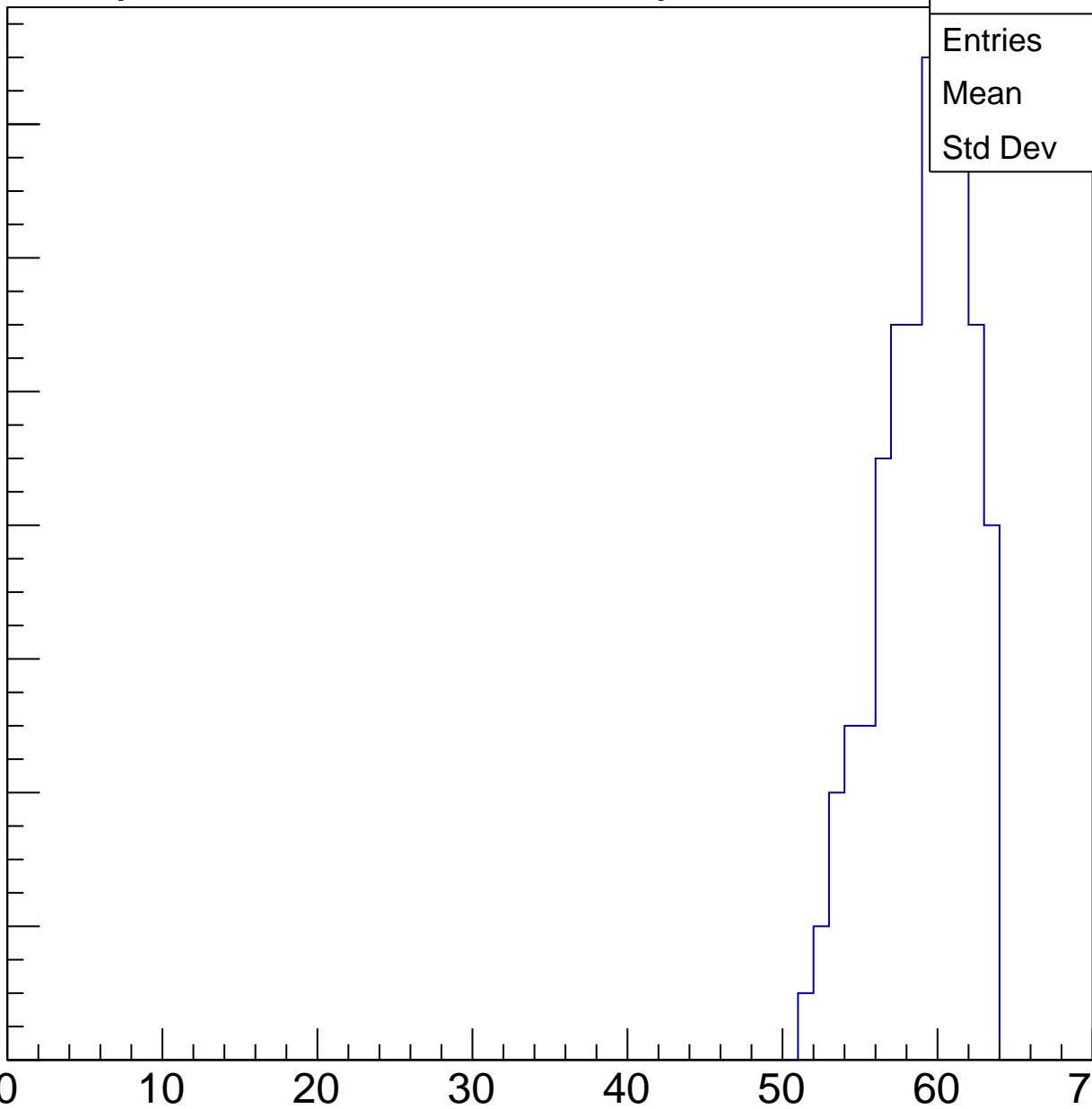
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	58.61
Std Dev	2.892

ampl



B0L101S, U25-ch67, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	31
Mean	57.84
Std Dev	15.24

0

ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L101S, U25-ch68, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	30.52
Std Dev	3.286

Gaus mean : 31.1683

Gaus Width: 3.2604

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

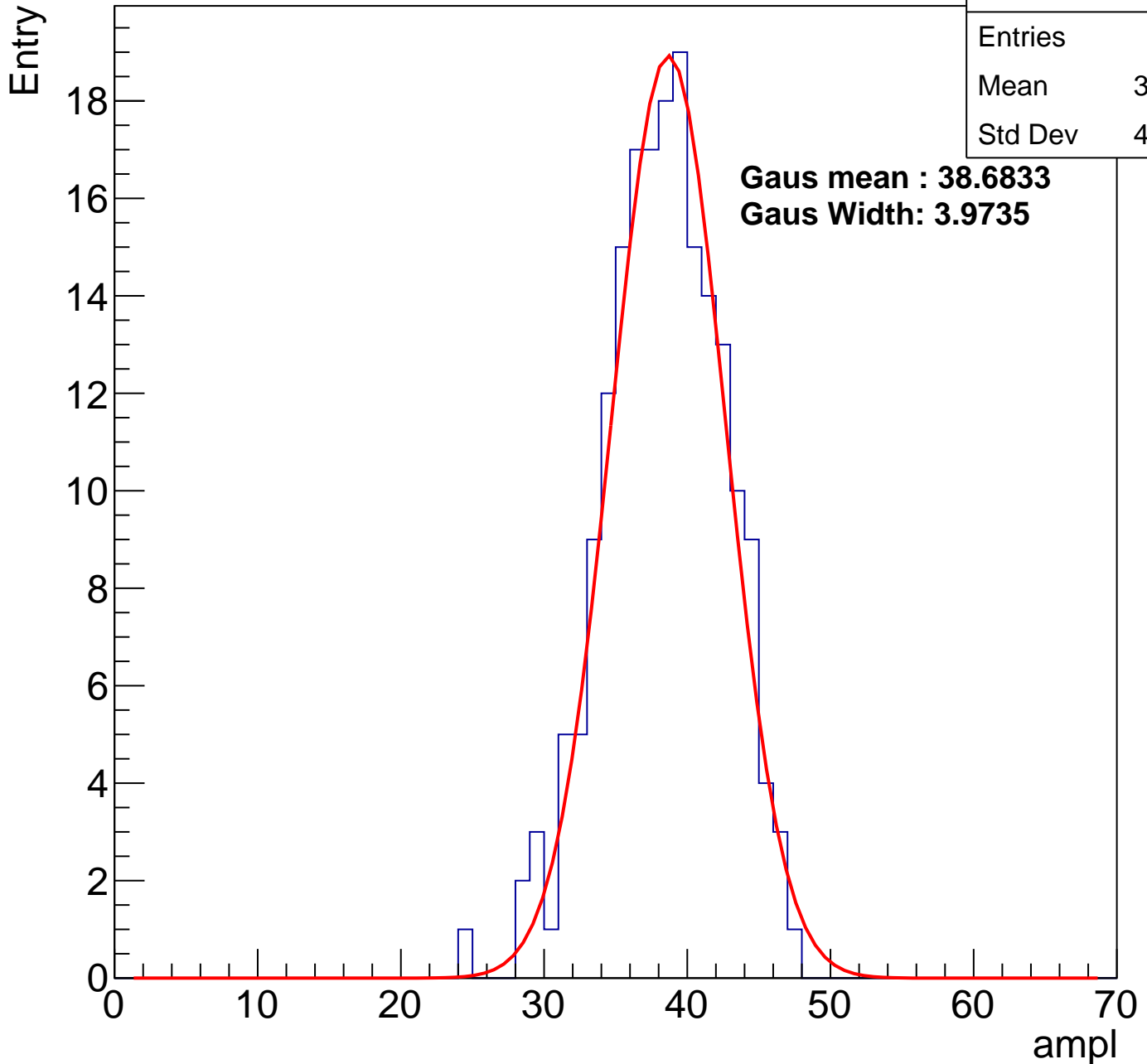
70

B0L101S, U25-ch68, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	193
Mean	37.92
Std Dev	4.084

Gaus mean : 38.6833
Gaus Width: 3.9735



B0L101S, U25-ch68, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

118

Mean

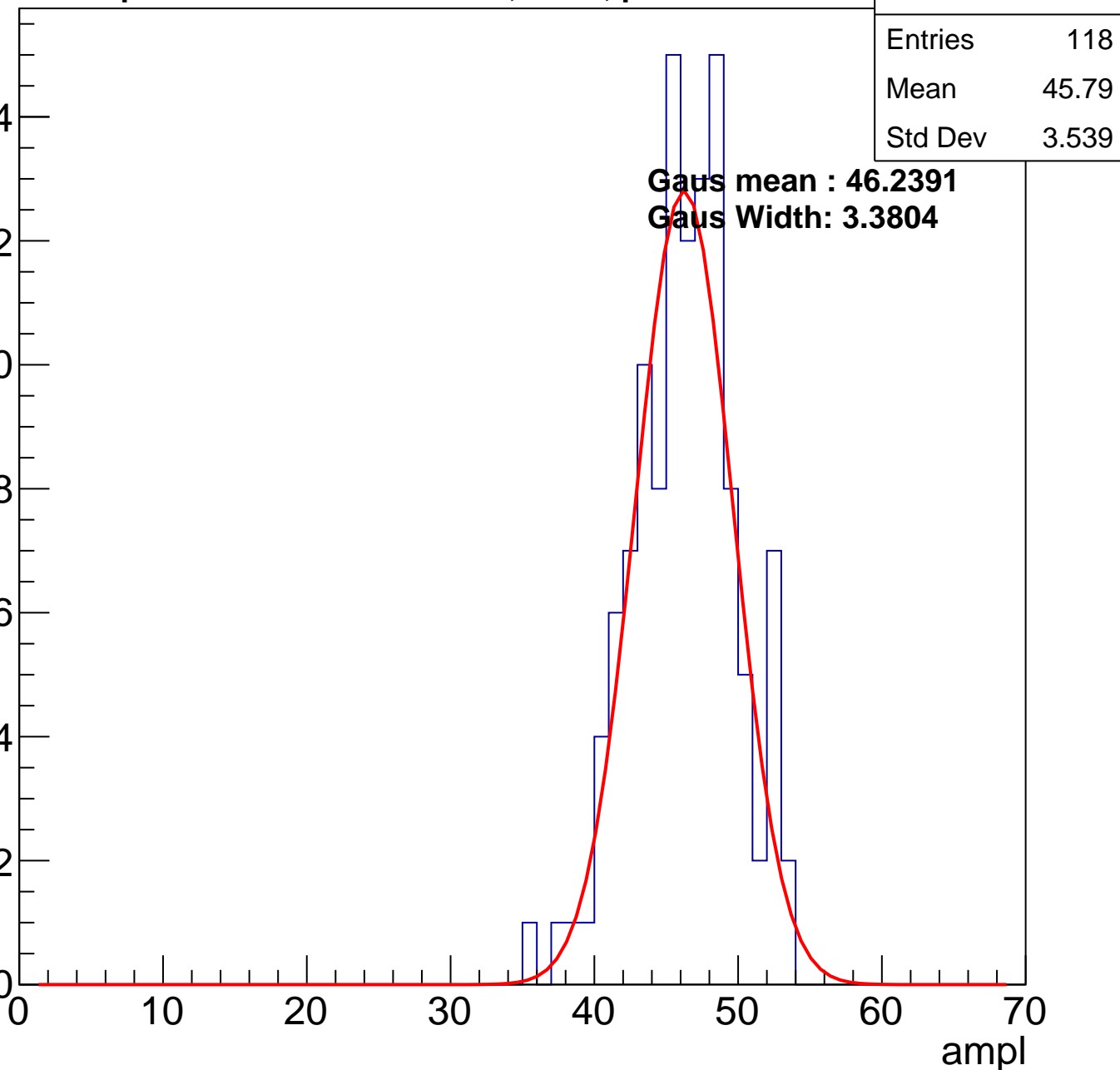
45.79

Std Dev

3.539

Gaus mean : 46.2391

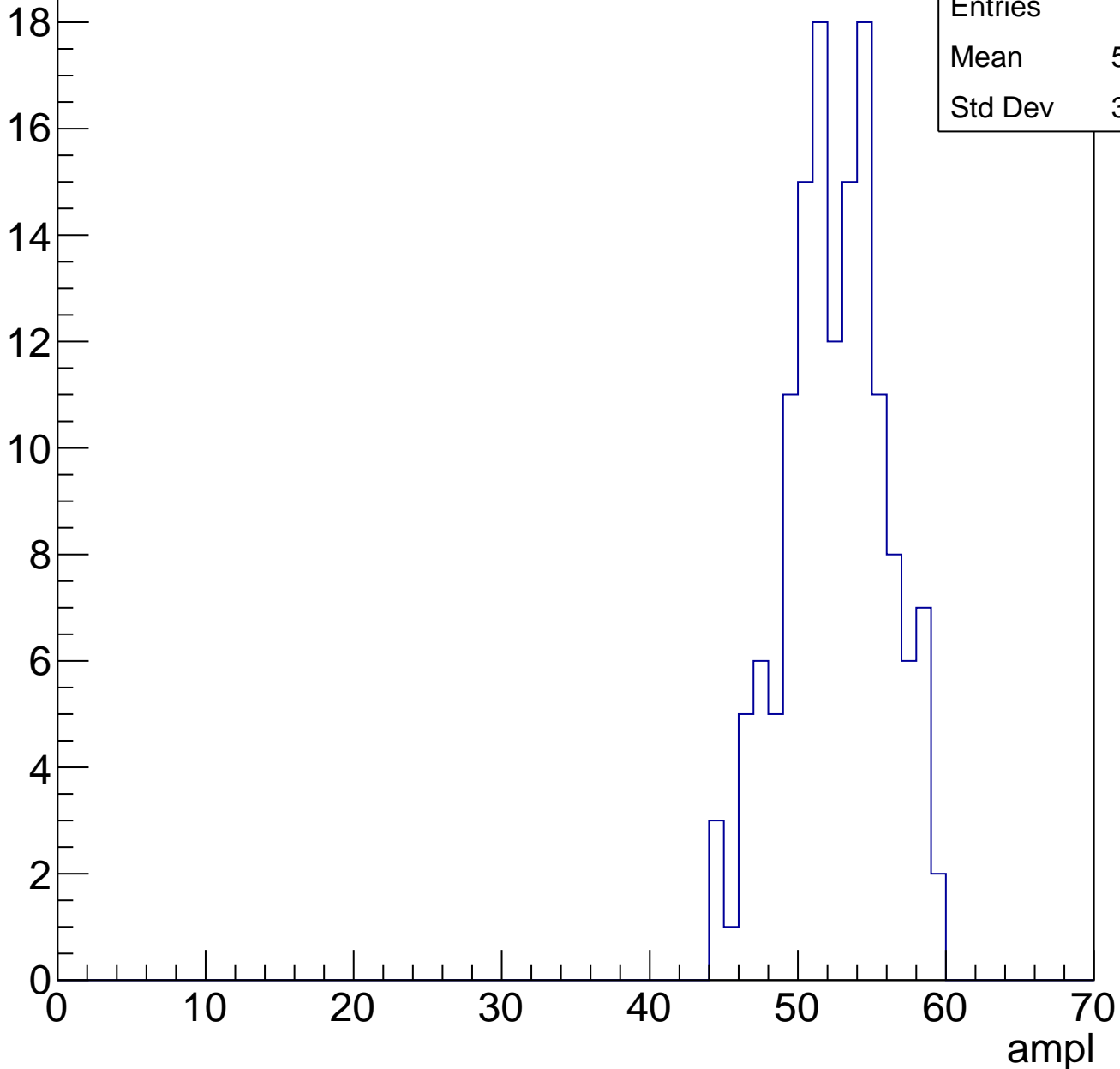
Gaus Width: 3.3804



B0L101S, U25-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

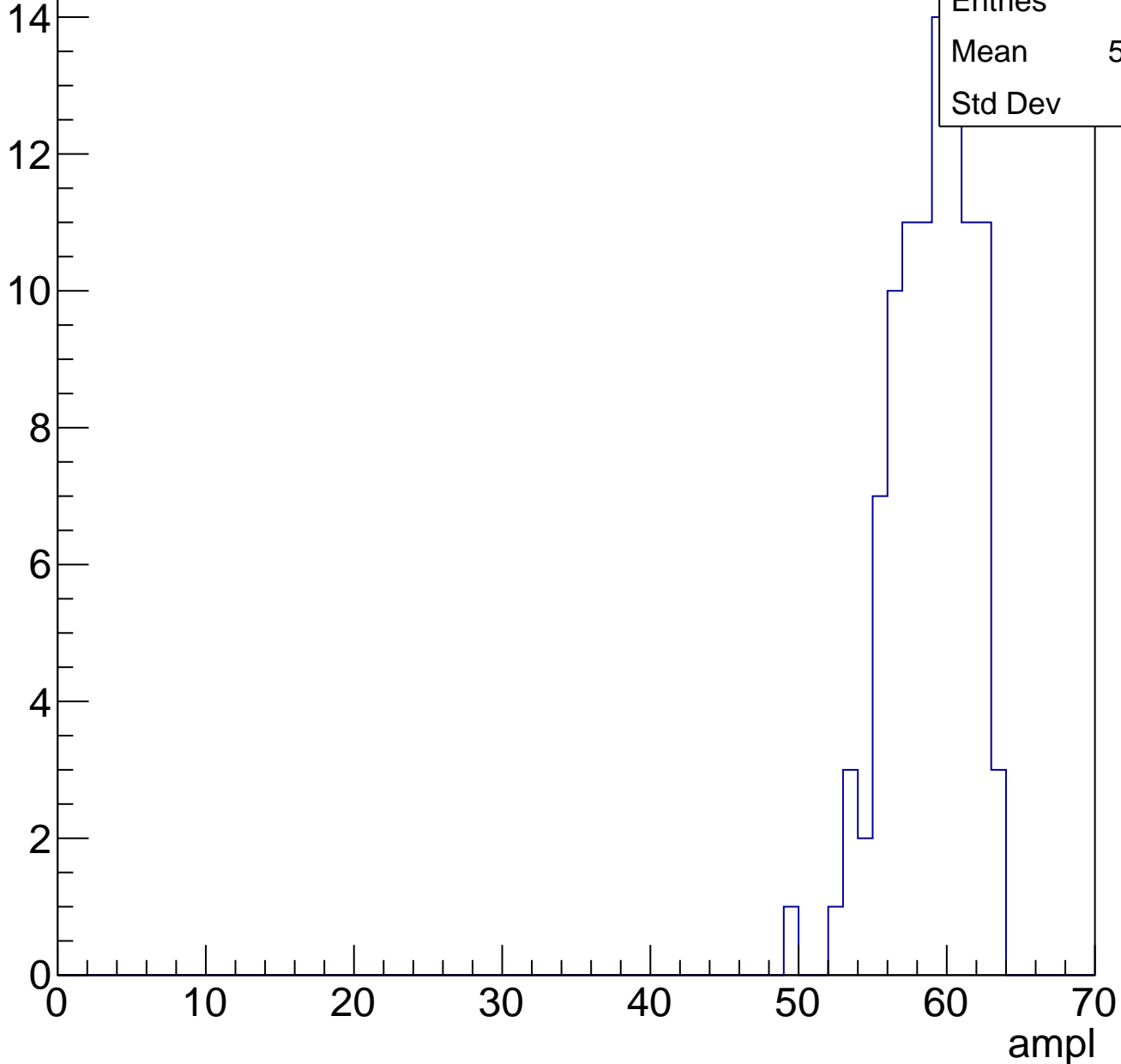


Entries	143
Mean	52.07
Std Dev	3.388

B0L101S, U25-ch68, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch68, adc5

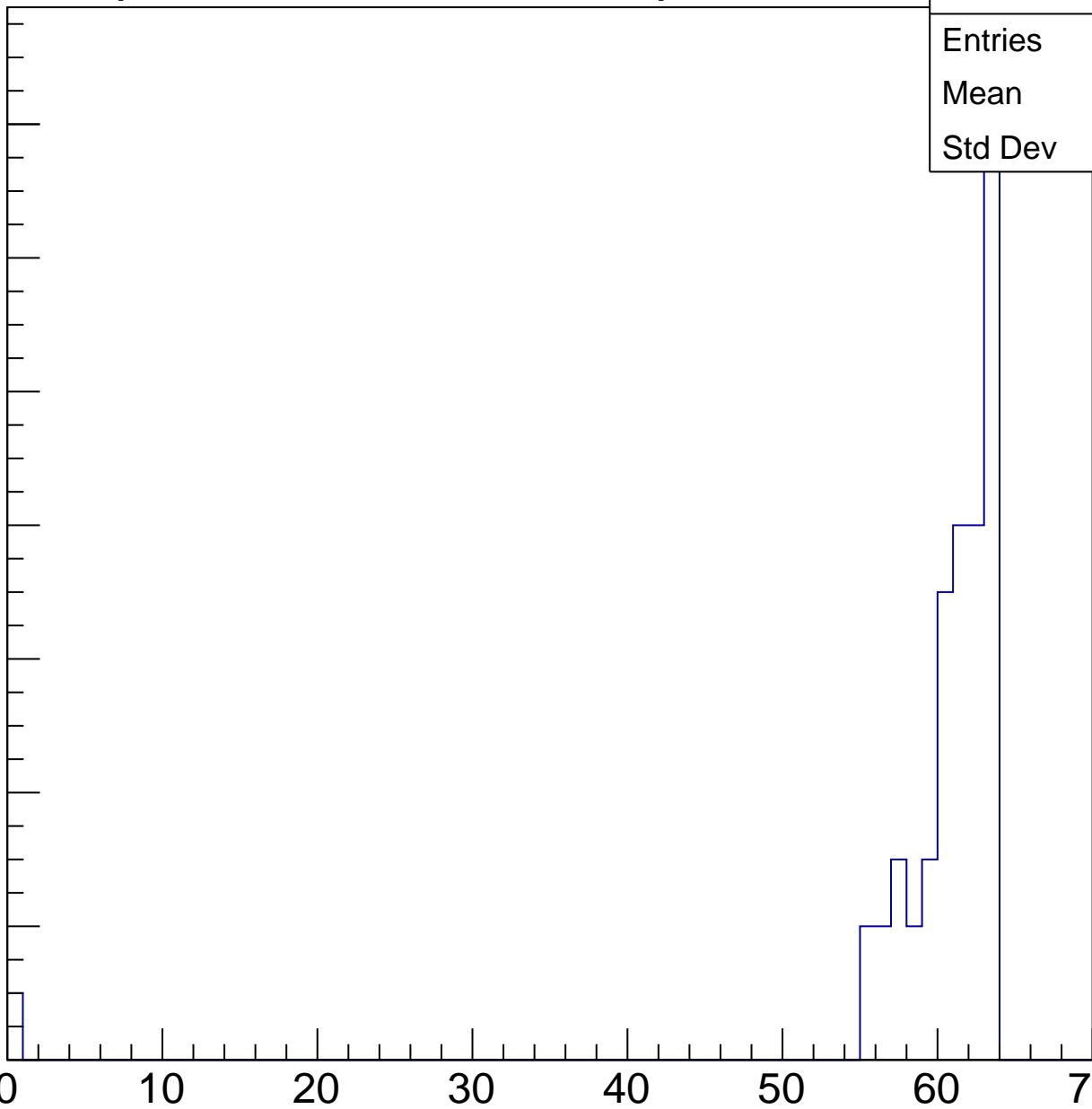
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	51
Mean	59.51
Std Dev	8.725

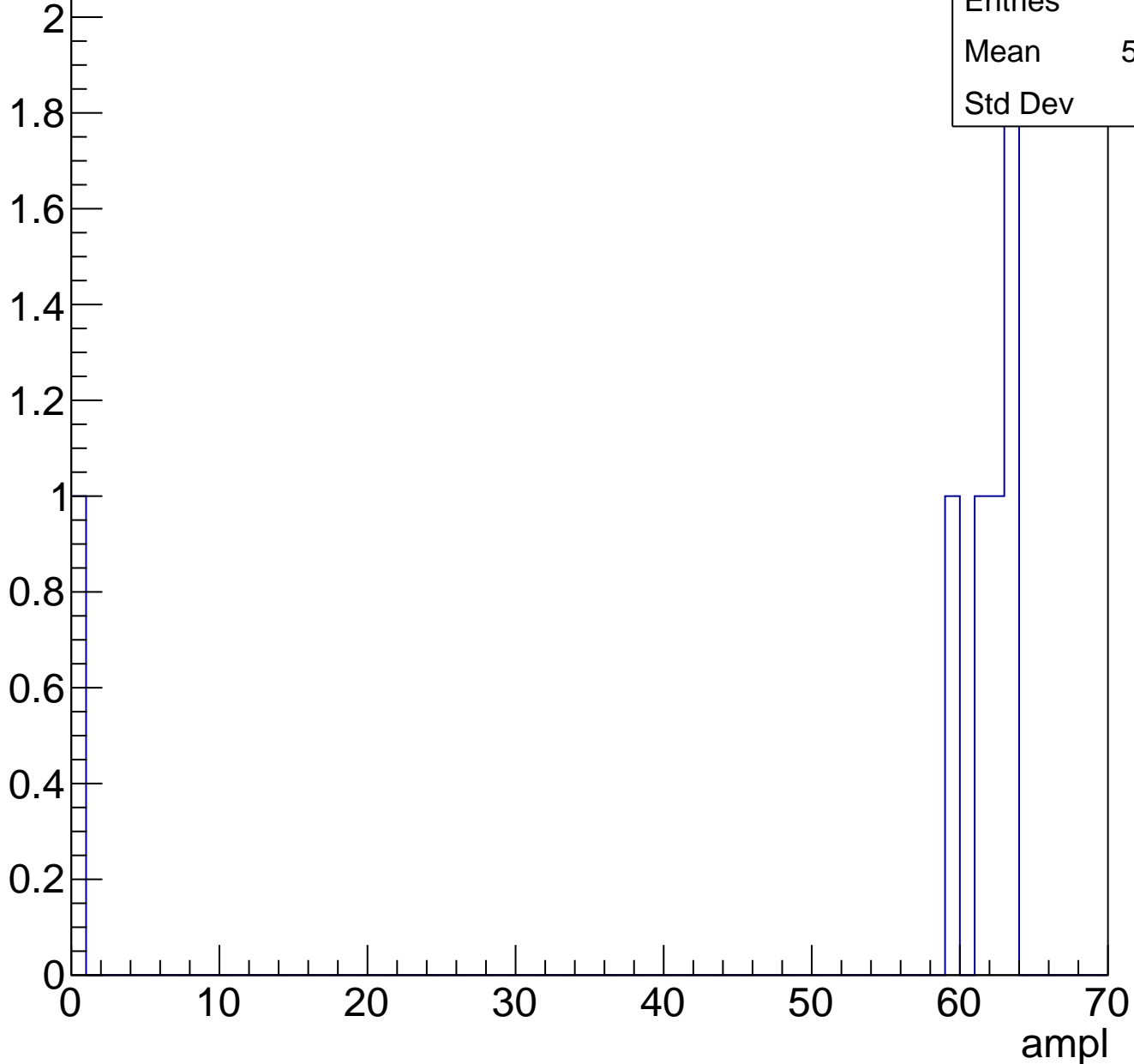
ampl



B0L101S, U25-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch69, adc0

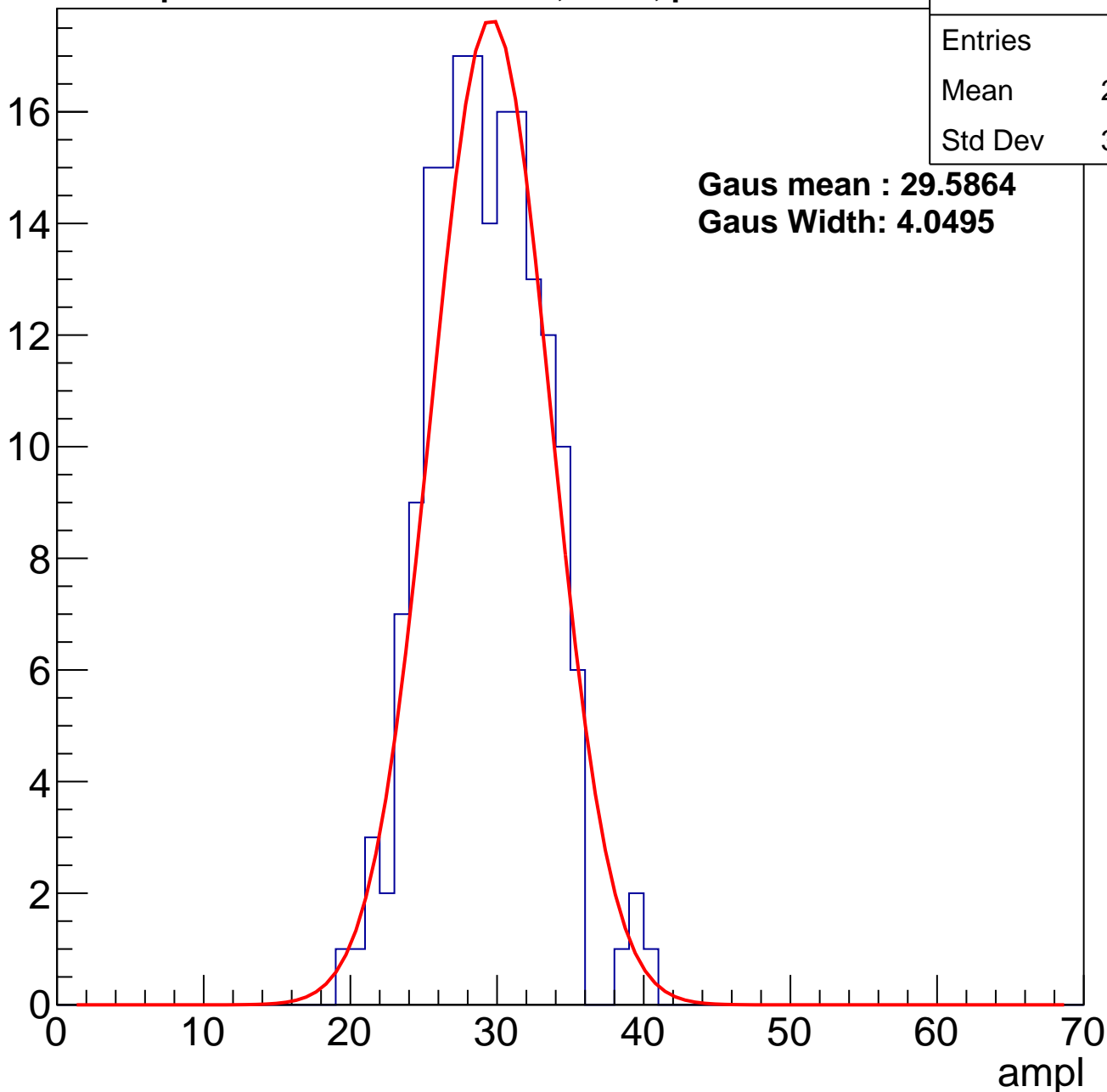
calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	28.78
Std Dev	3.863

Gaus mean : 29.5864

Gaus Width: 4.0495

Entry



B0L101S, U25-ch69, adc1

calib_packv5_042523_0143.root, FC#1, port C1

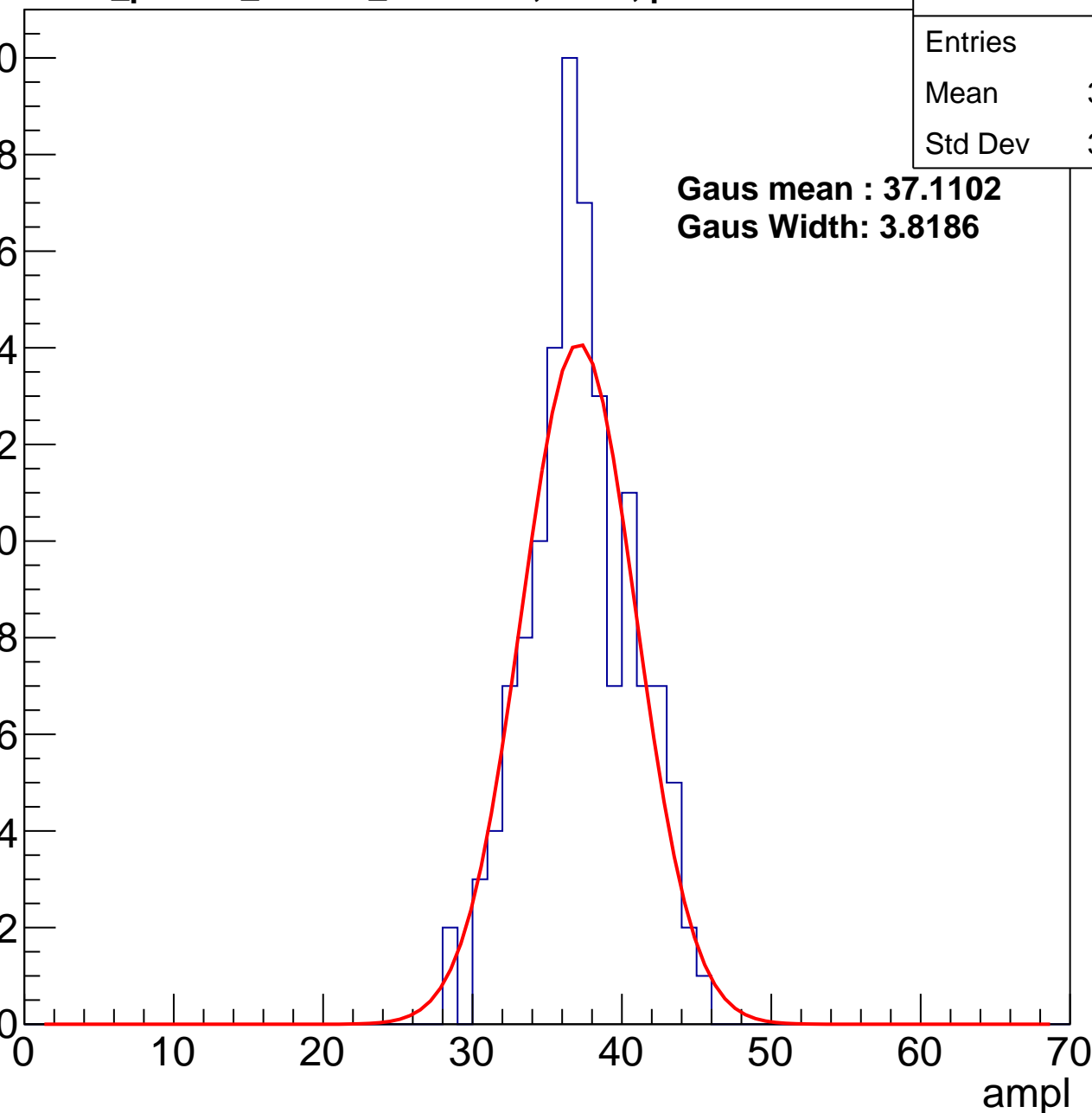
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	138
Mean	36.76
Std Dev	3.492

Gaus mean : 37.1102

Gaus Width: 3.8186



B0L101S, U25-ch69, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	43.86
Std Dev	4.074

Gaus mean : 44.1034

Gaus Width: 3.8915

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

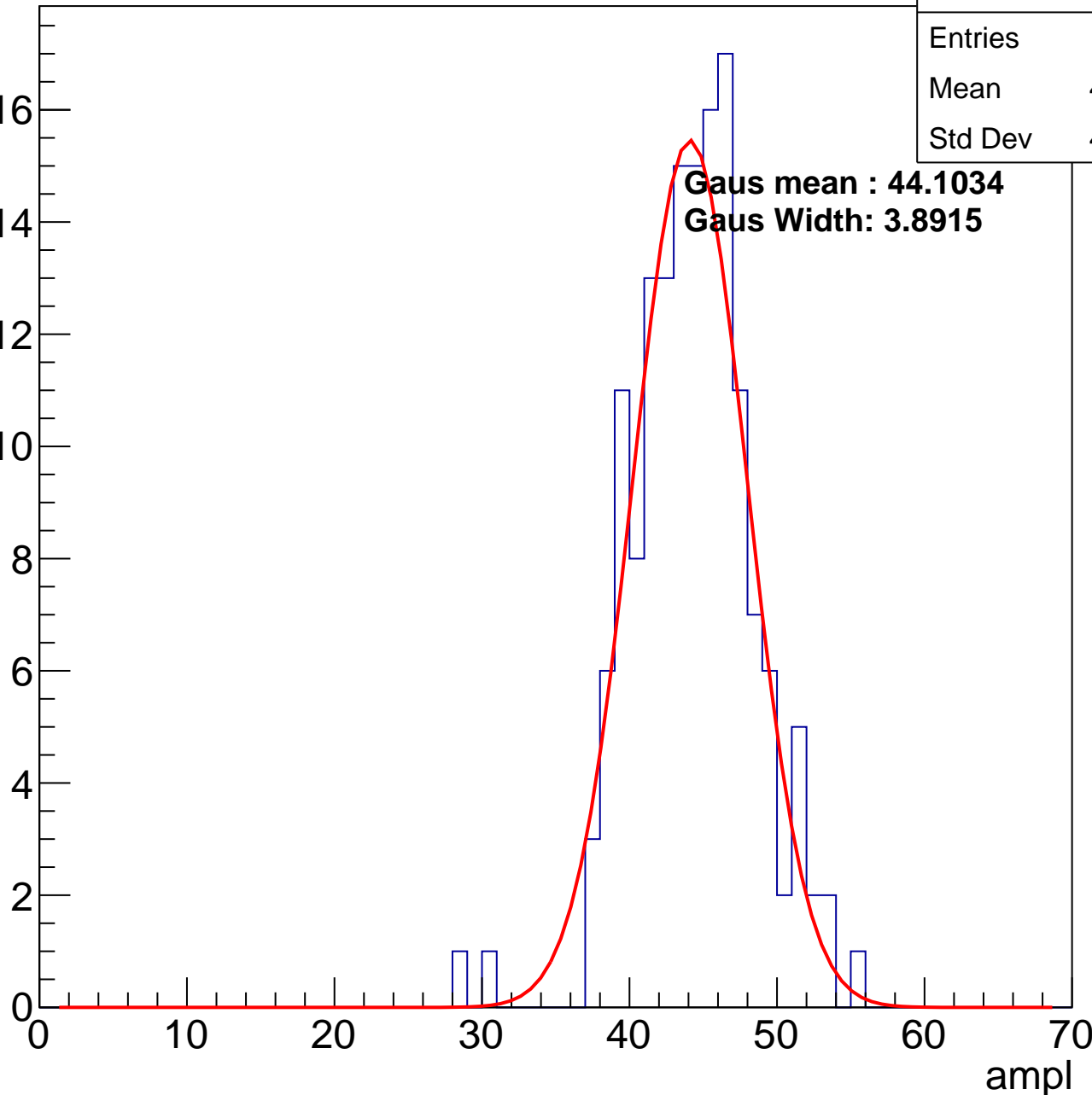
40

50

60

70

ampl



B0L101S, U25-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	50.44
Std Dev	3.134

Entry

12

10

8

6

4

2

0

0

10

20

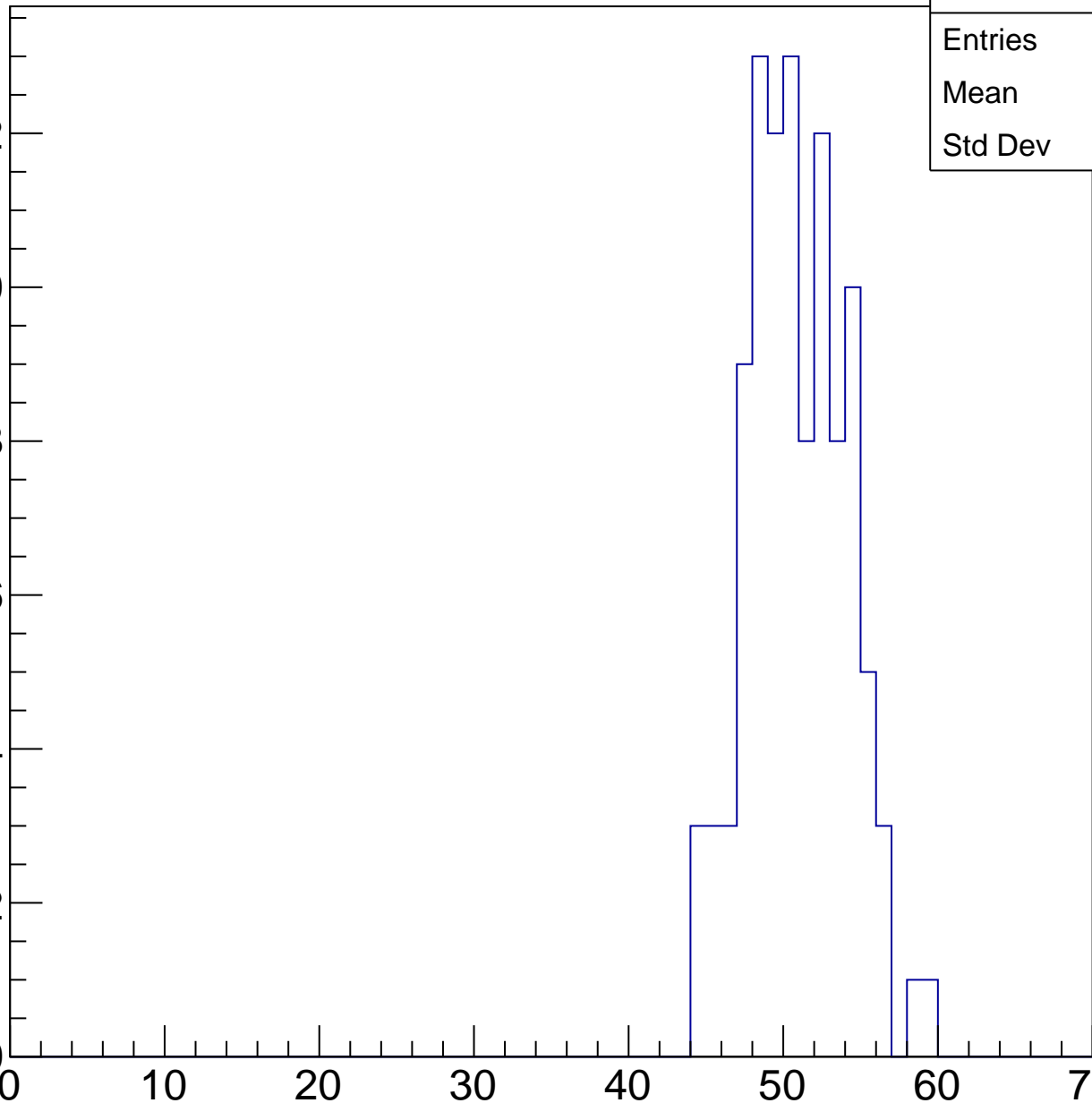
30

40

50

60

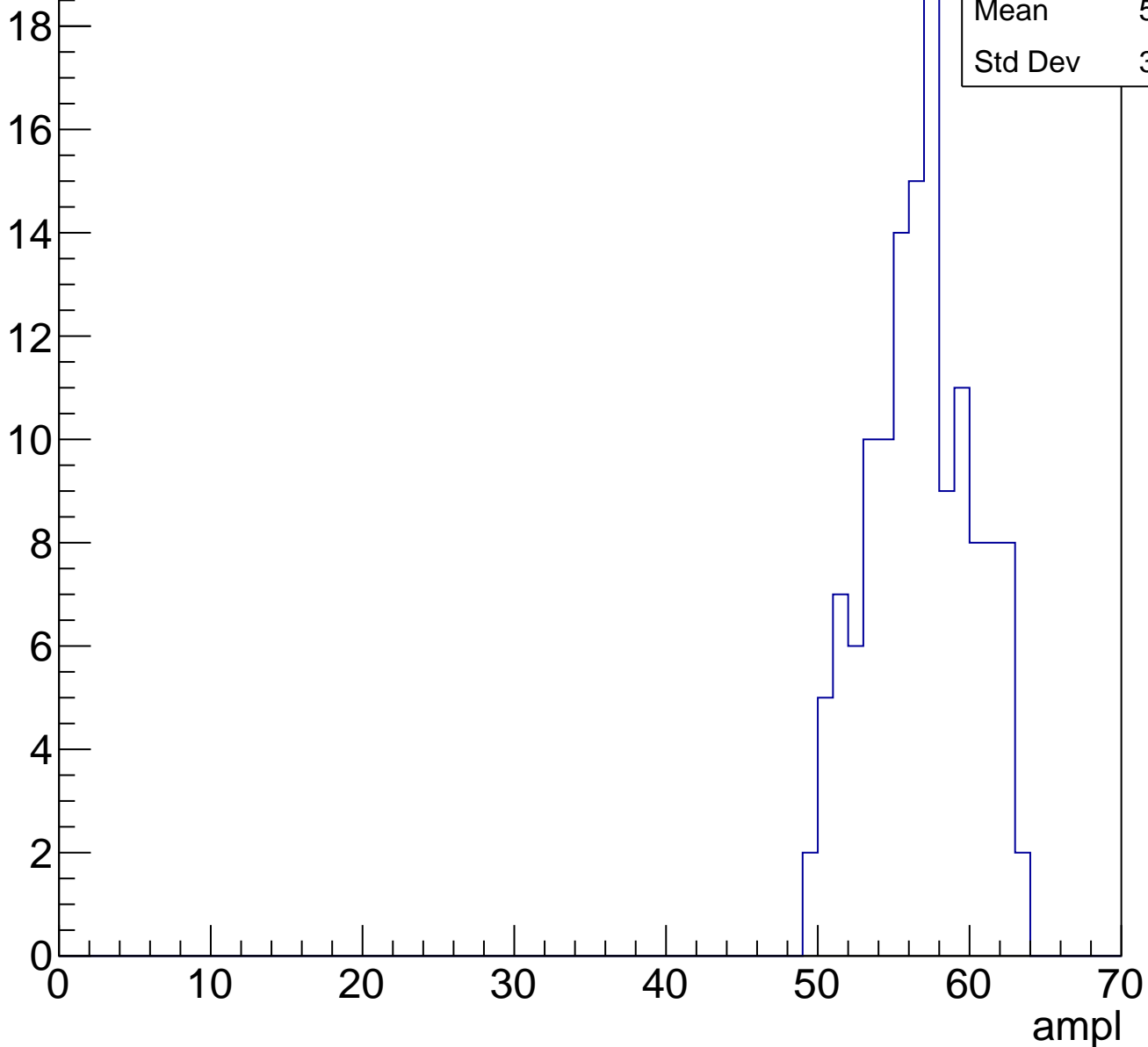
ampl



B0L101S, U25-ch69, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch69, adc5

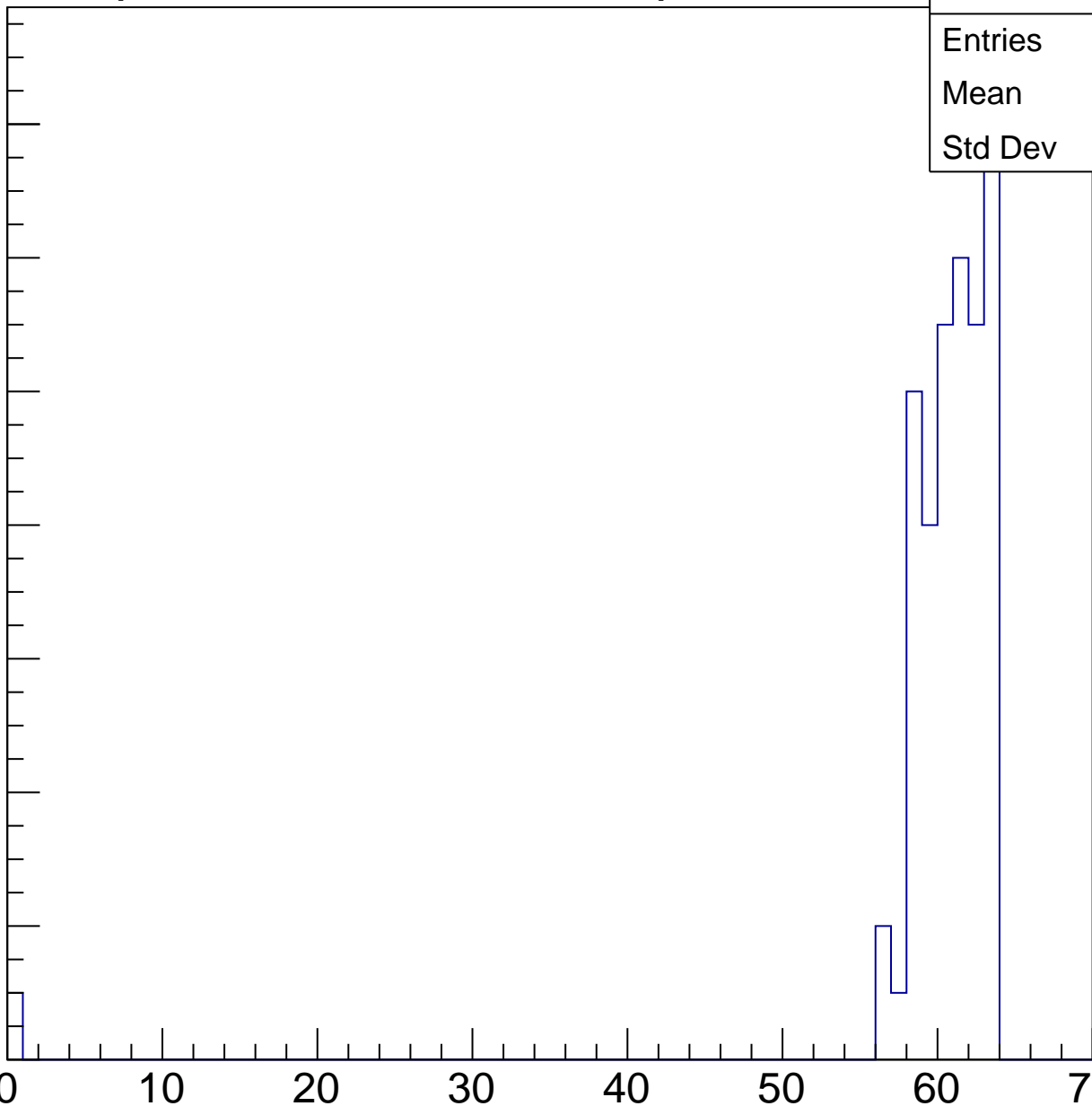
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	59.72
Std Dev	7.387

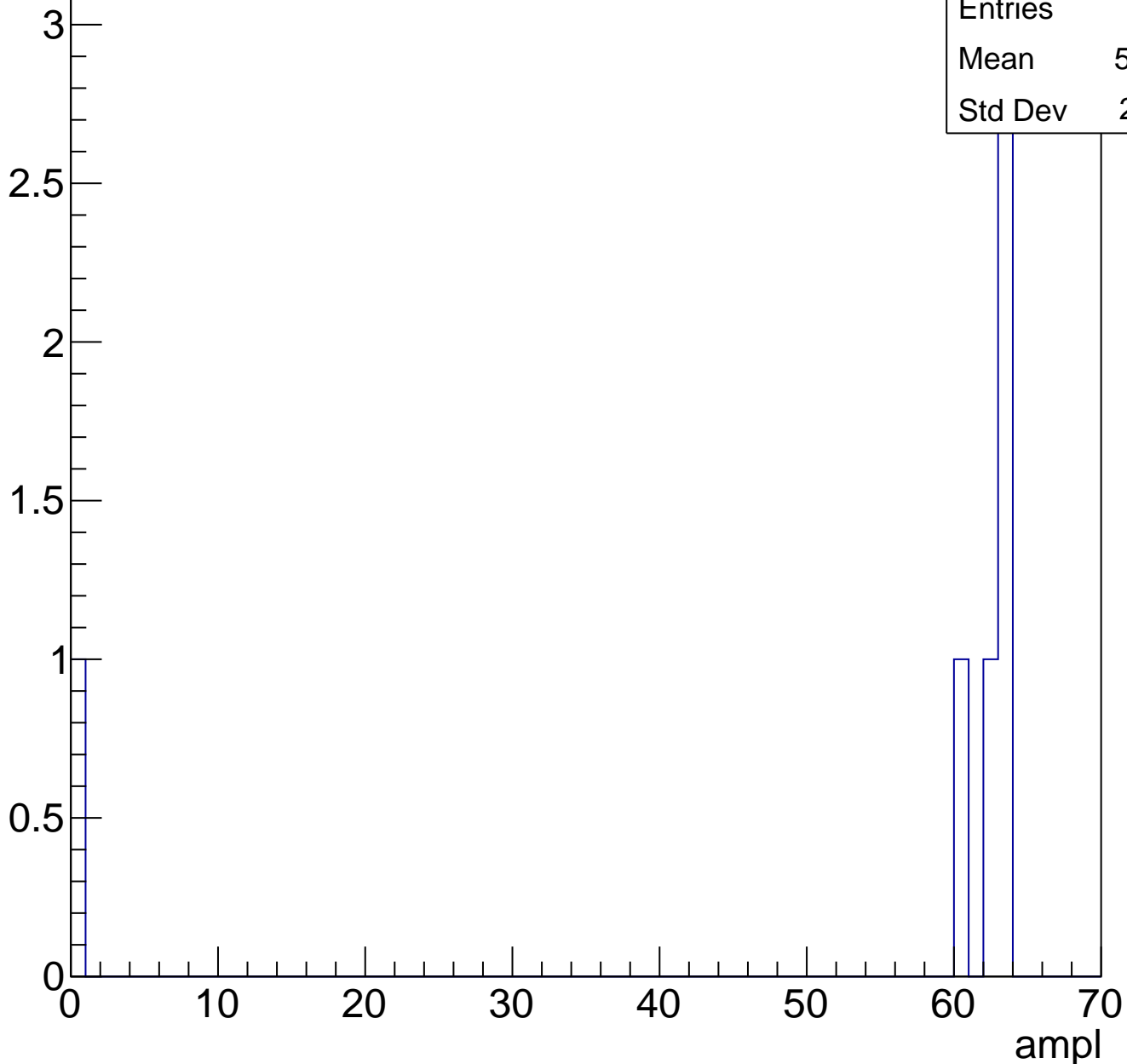
ampl



B0L101S, U25-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch70, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	28.87
Std Dev	4.484

Gaus mean : 29.2577

Gaus Width: 3.8038

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

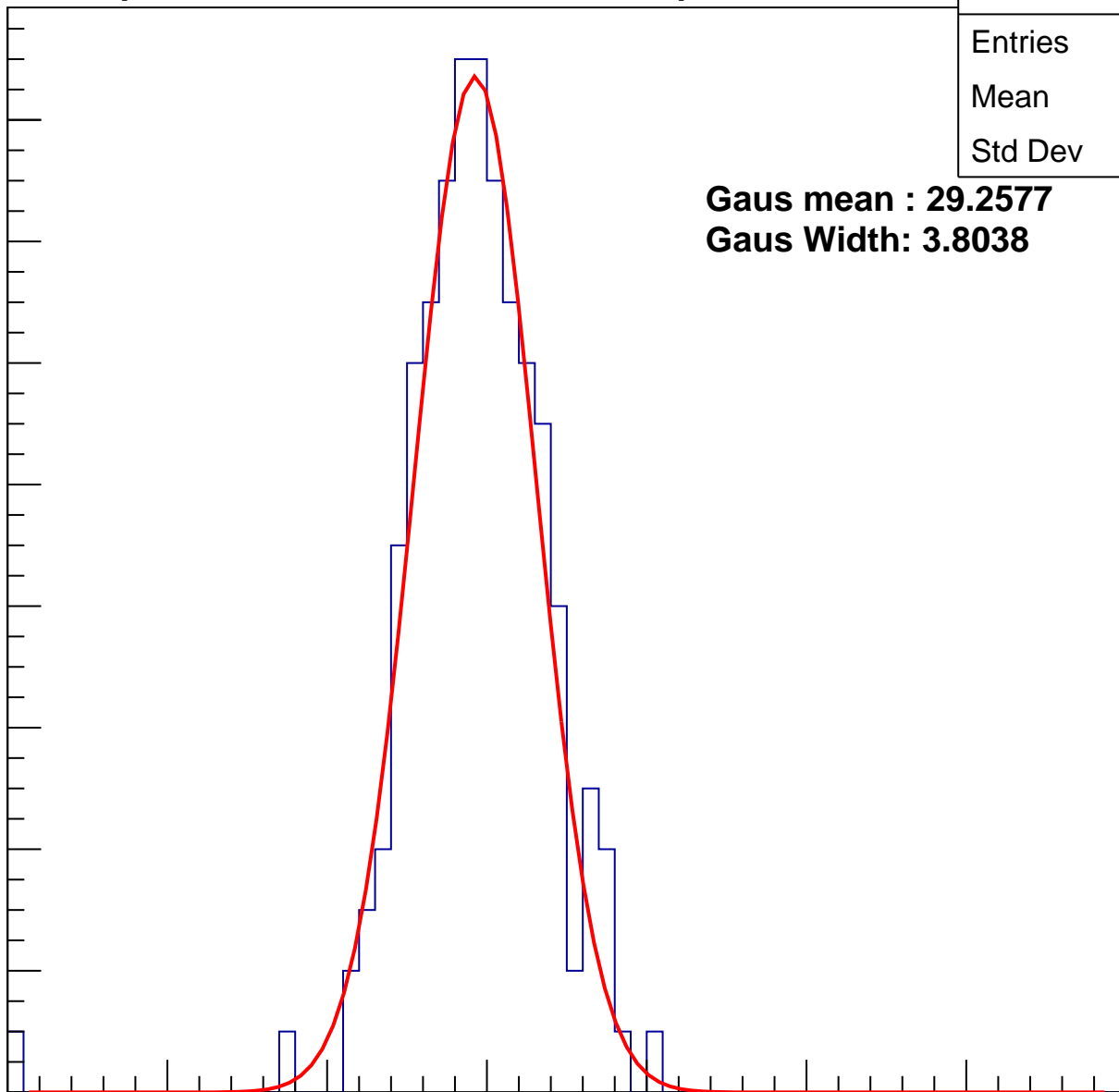
40

50

60

70

ampl



B0L101S, U25-ch70, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	36.4
Std Dev	3.543

Gaus mean : 36.8015

Gaus Width: 3.7551

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U25-ch70, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

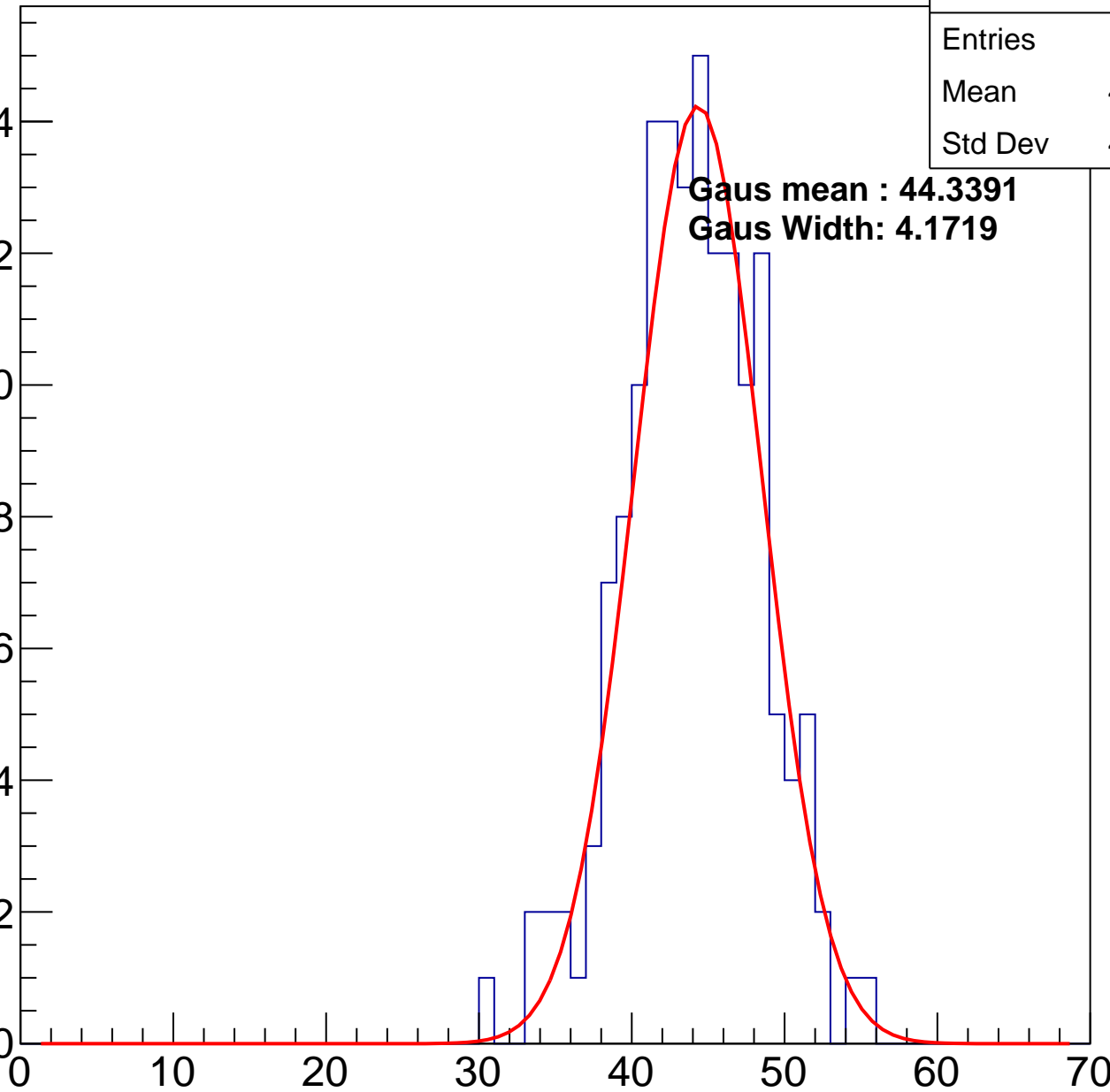
14
12
10
8
6
4
2
0

Entries	156
Mean	43.53
Std Dev	4.377

Gaus mean : 44.3391

Gaus Width: 4.1719

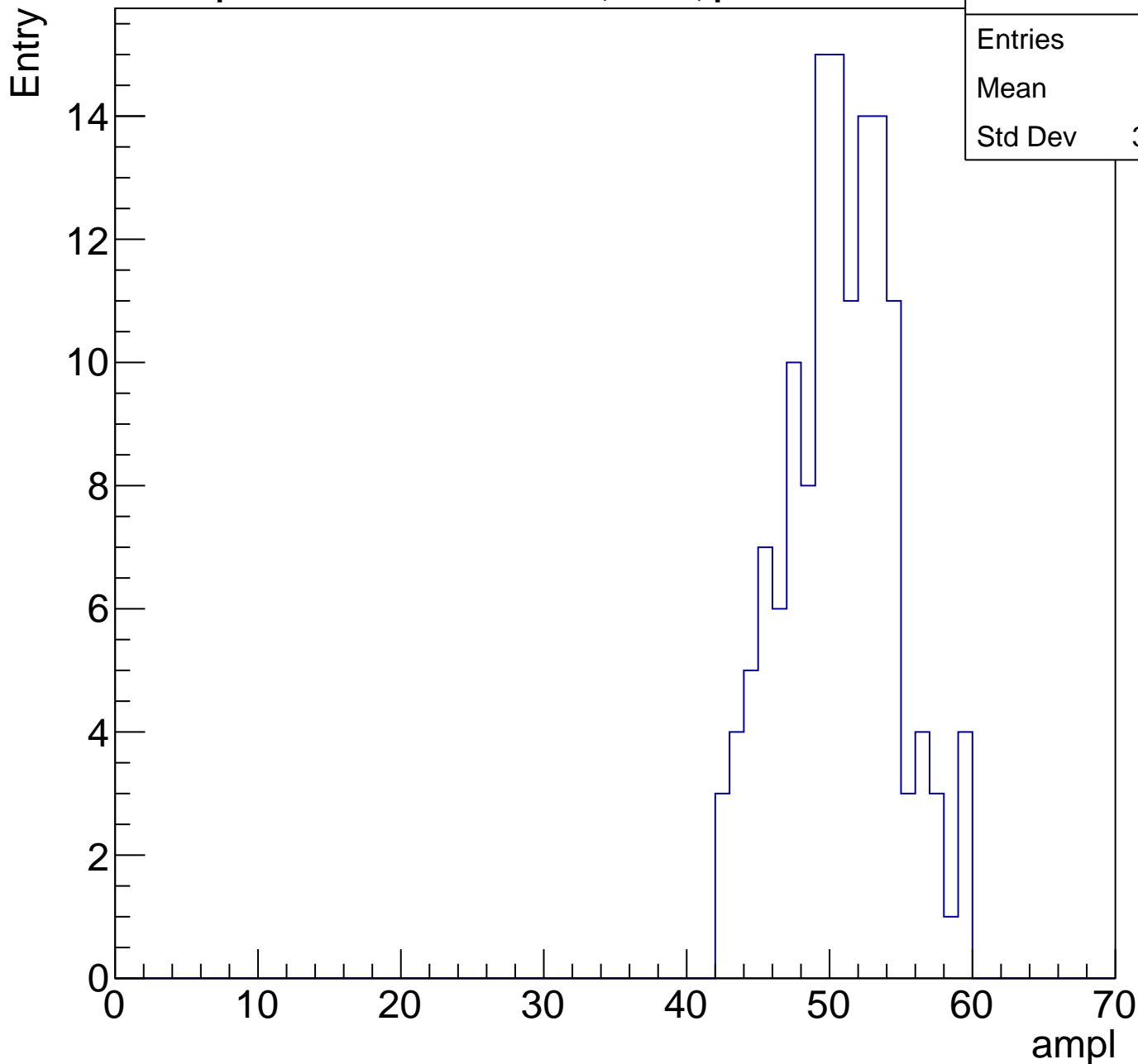
ampl



B0L101S, U25-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	50.2
Std Dev	3.895



B0L101S, U25-ch70, adc4

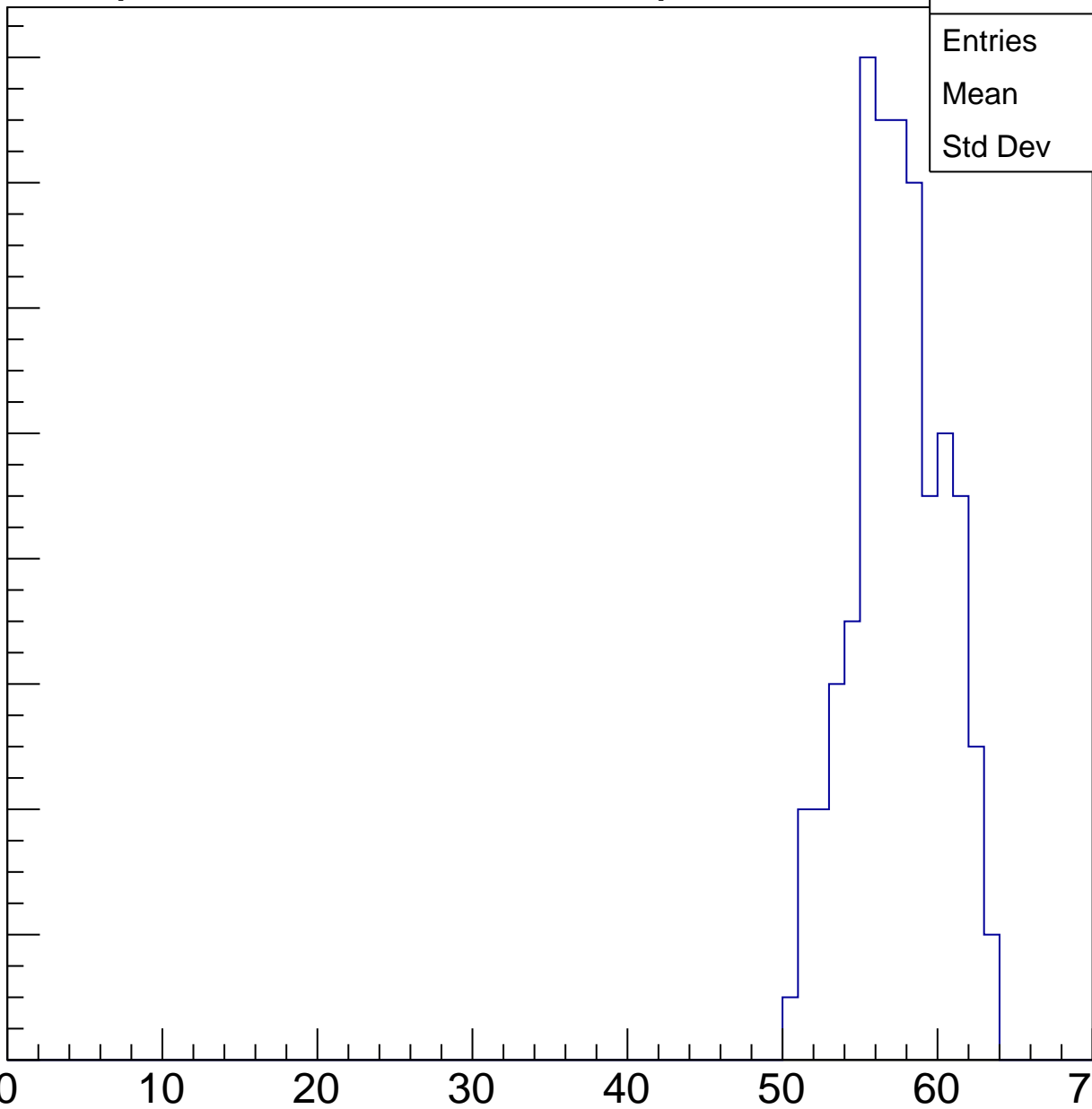
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	117
Mean	56.93
Std Dev	2.94

ampl



B0L101S, U25-ch70, adc5

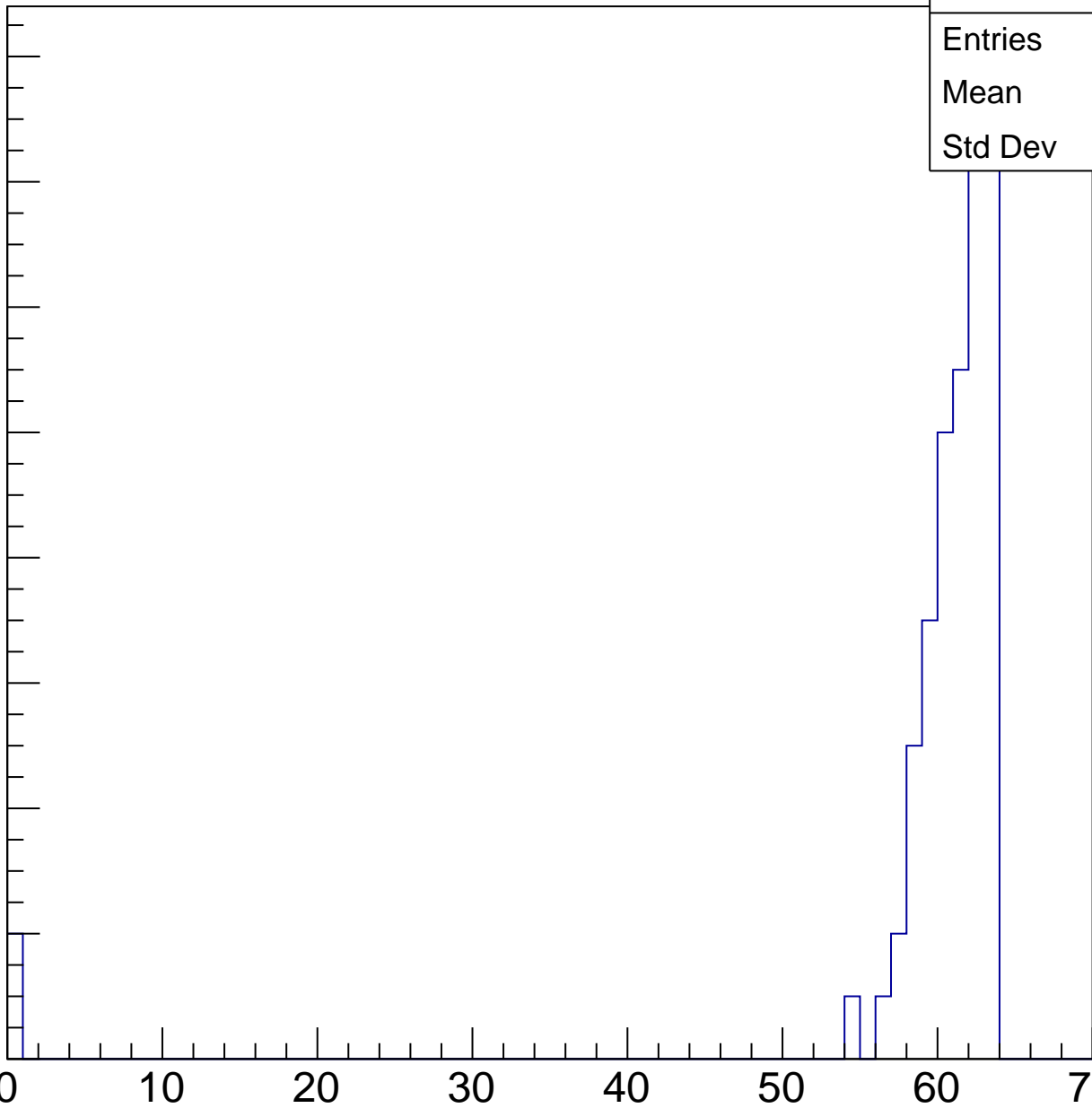
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	70
Mean	59.09
Std Dev	10.32

ampl



B0L101S, U25-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

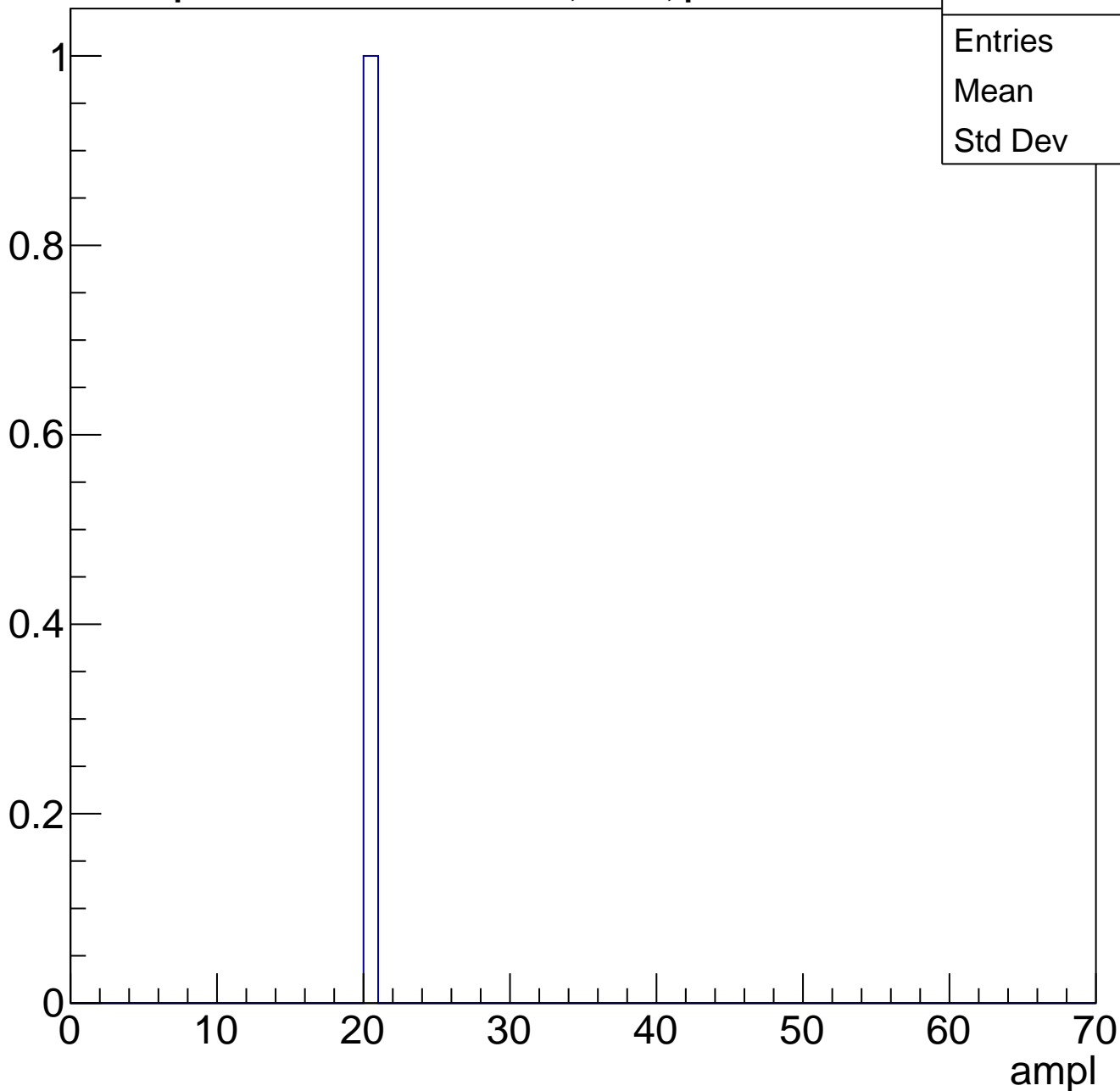
Entry



B0L101S, U25-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L101S, U25-ch71, adc0

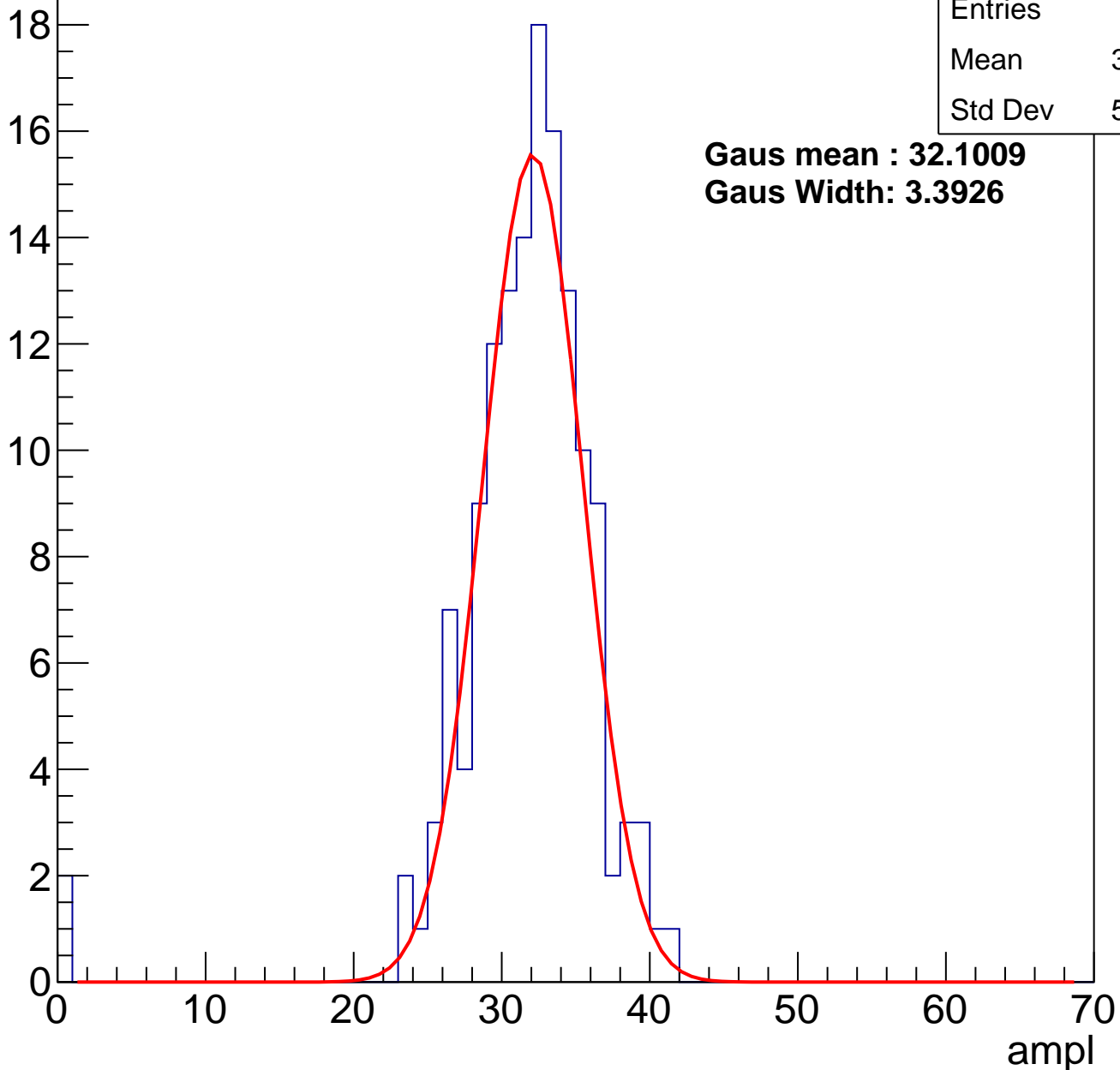
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	31.22
Std Dev	5.126

Gaus mean : 32.1009

Gaus Width: 3.3926

Entry



B0L101S, U25-ch71, adc1

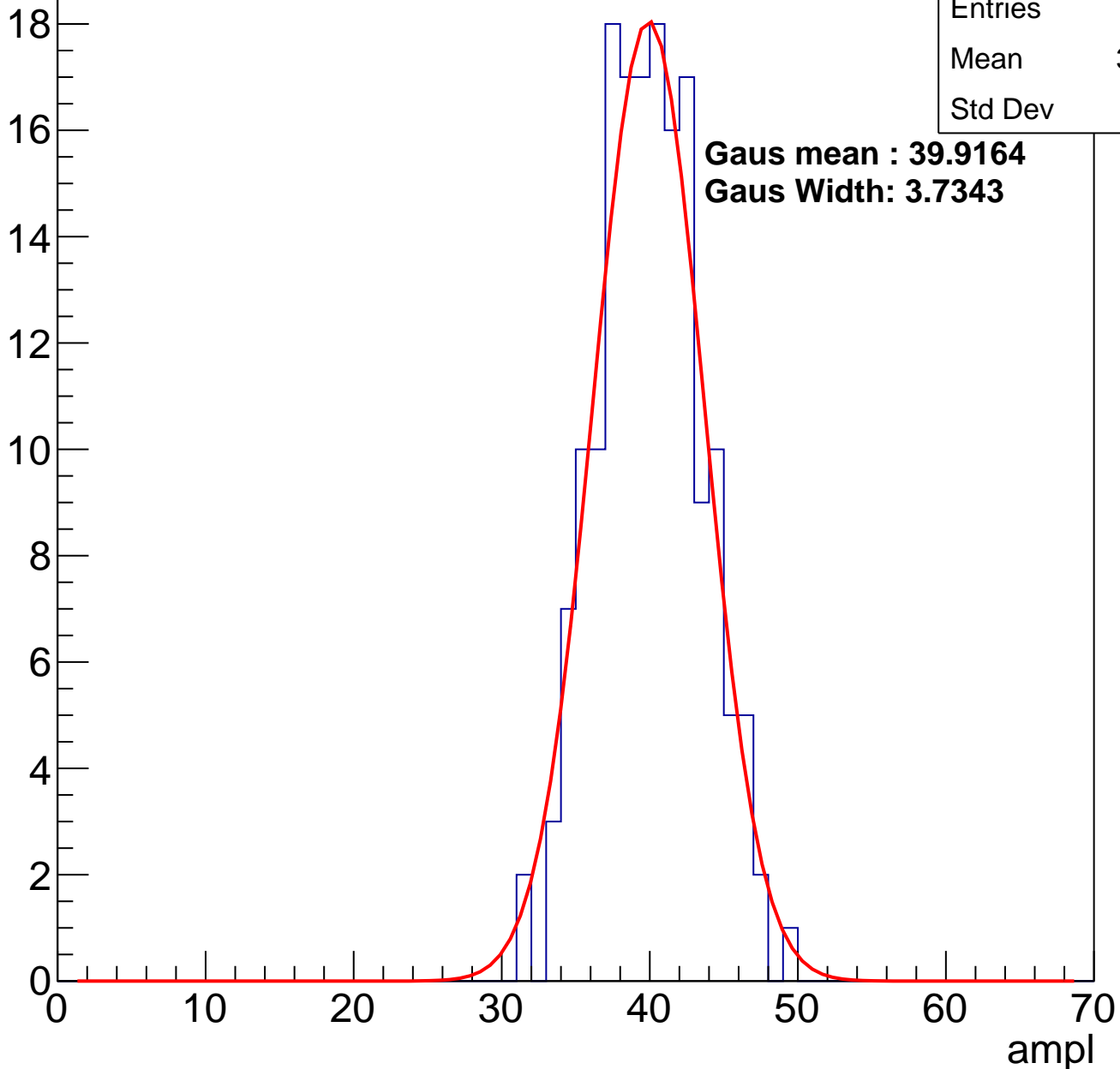
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	39.51
Std Dev	3.45

Gaus mean : 39.9164

Gaus Width: 3.7343

Entry



B0L101S, U25-ch71, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

129

Mean

47.27

Std Dev

3.354

Gaus mean : 47.7227

Gaus Width: 3.4289

0

10

20

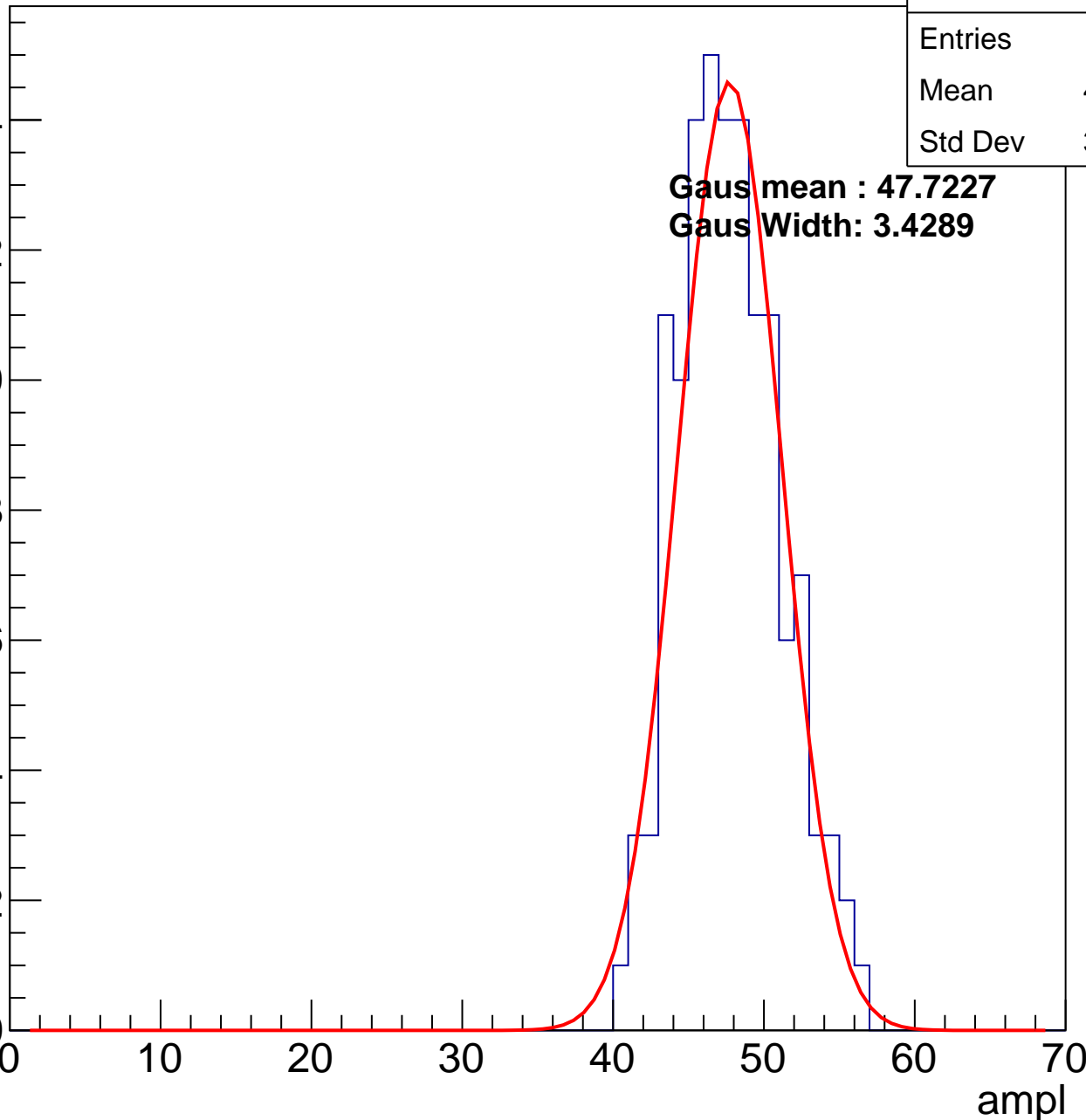
30

40

50

60

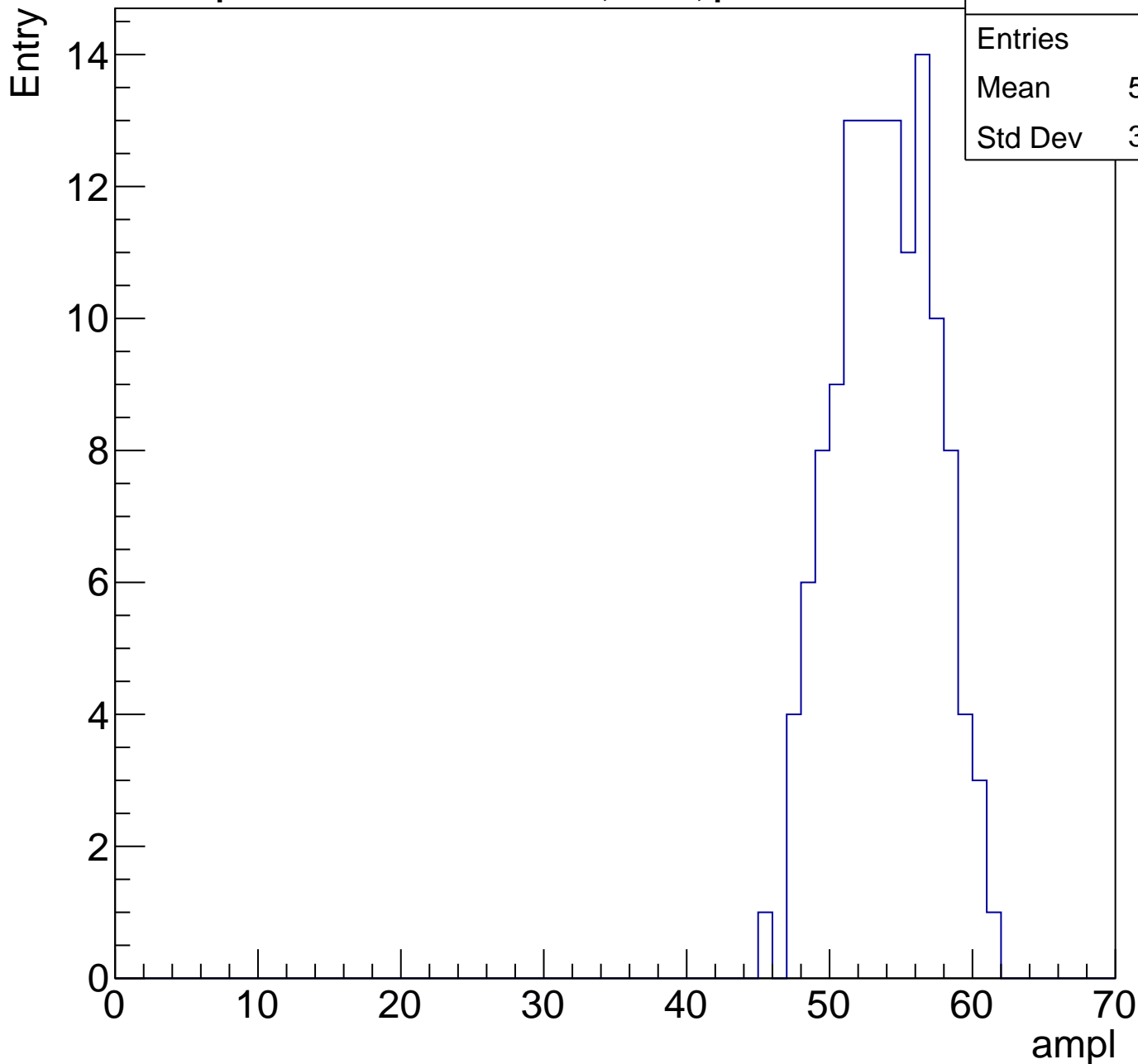
ampl



B0L101S, U25-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	53.38
Std Dev	3.396



B0L101S, U25-ch71, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

100

Mean

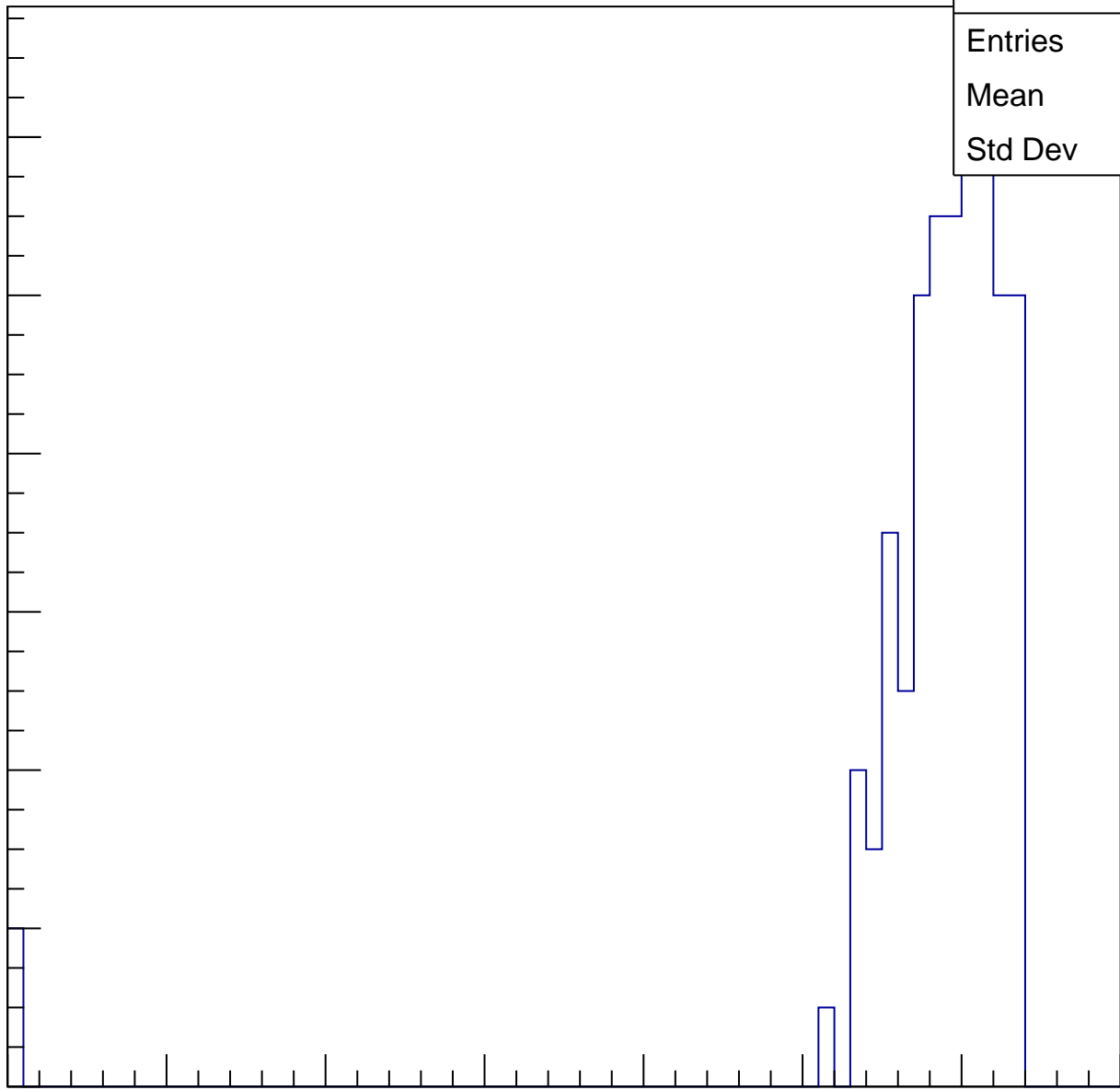
57.7

Std Dev

8.717

ampl

0 10 20 30 40 50 60 70

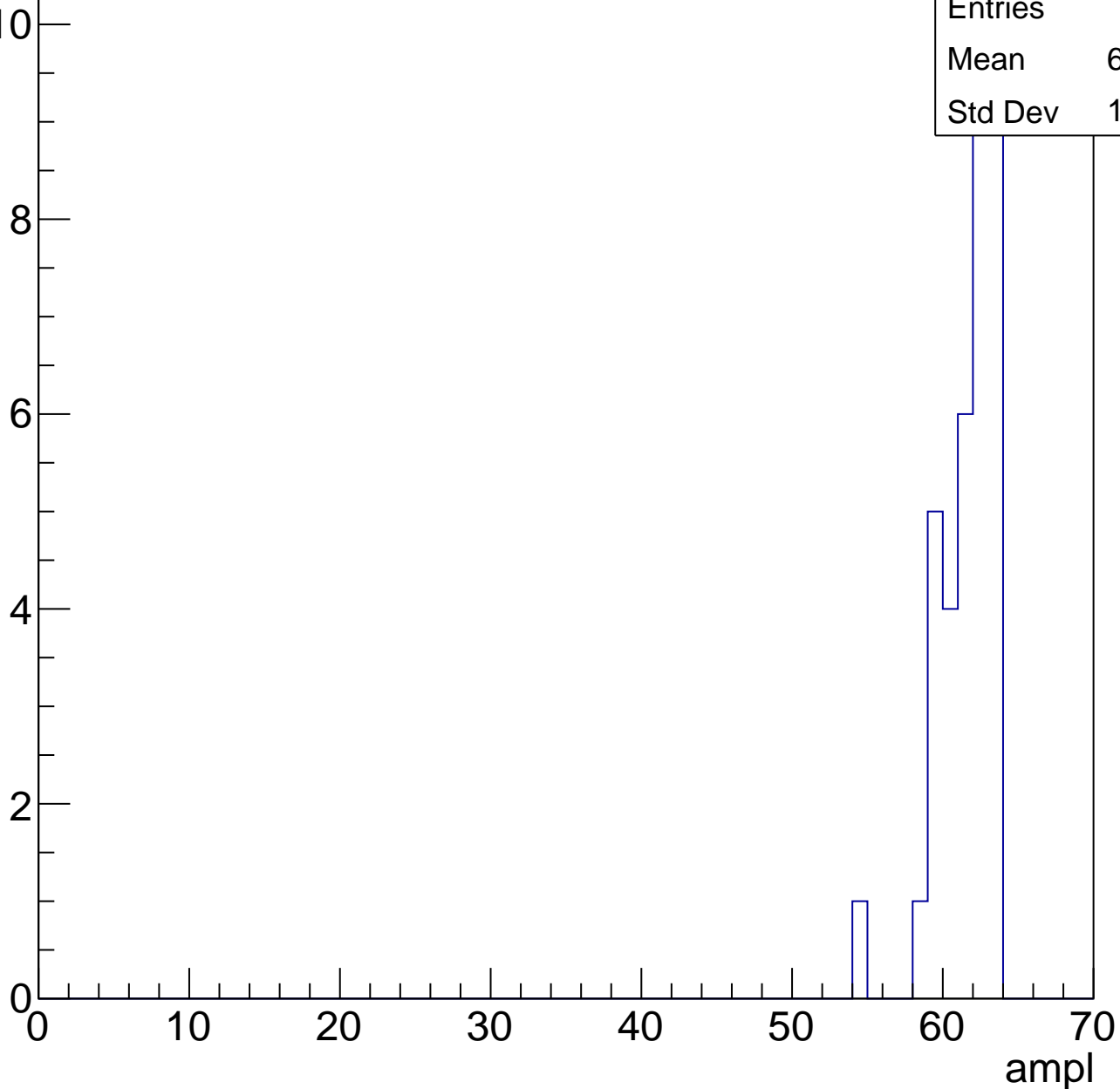


B0L101S, U25-ch71, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	37
Mean	61.16
Std Dev	1.882



B0L101S, U25-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch72, adc0

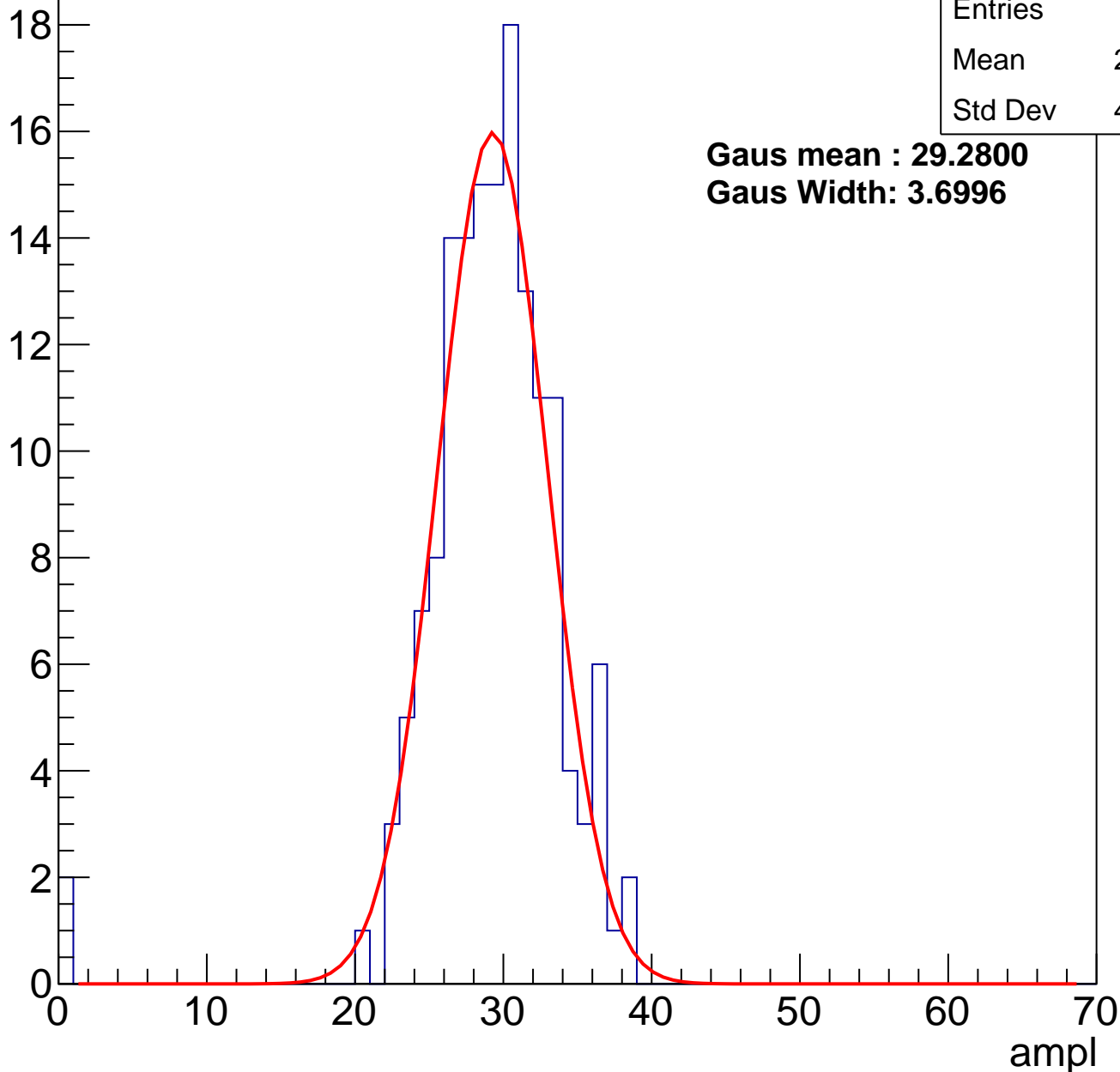
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.72
Std Dev	4.885

Gaus mean : 29.2800

Gaus Width: 3.6996

Entry



B0L101S, U25-ch72, adc1

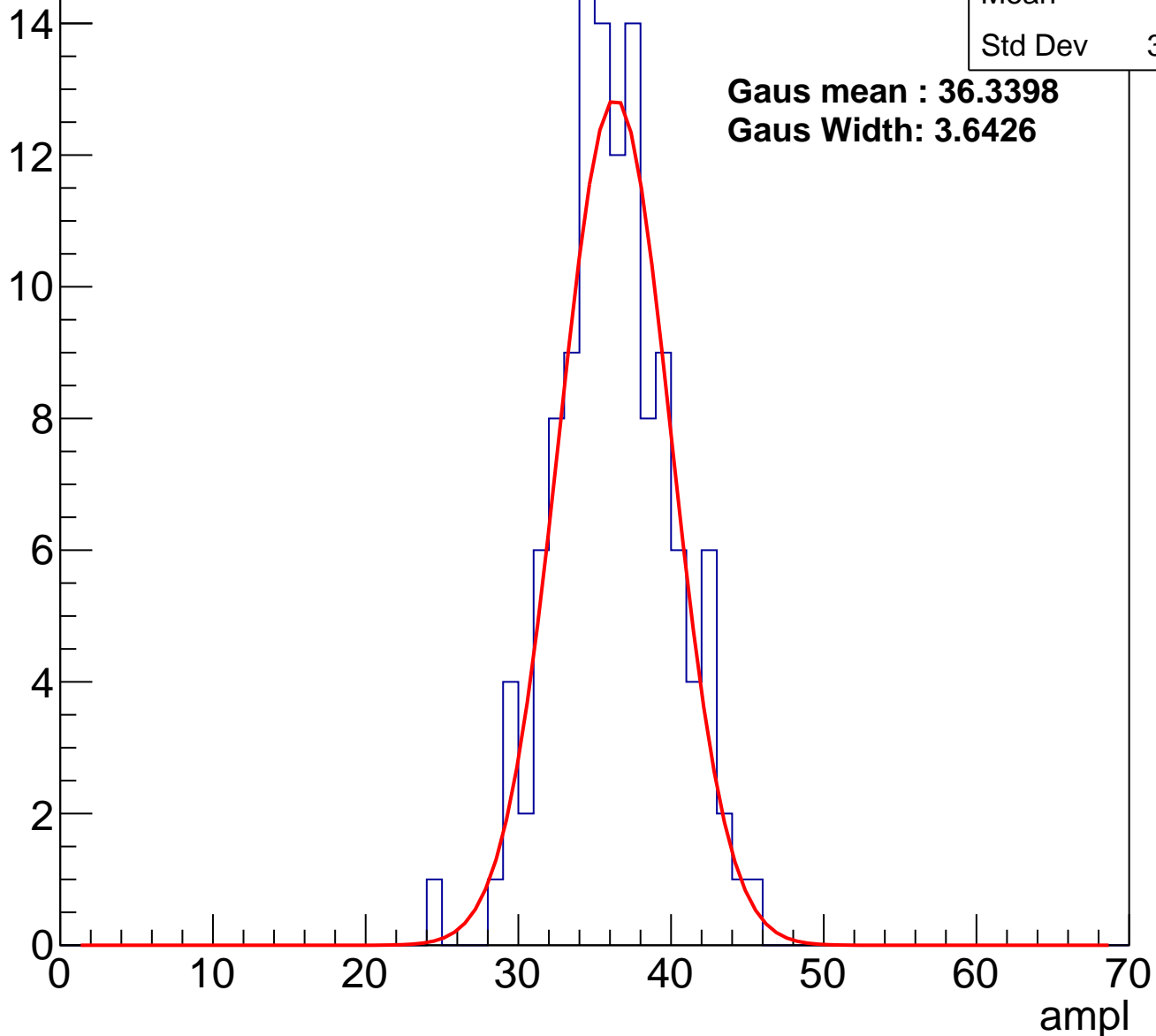
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	35.8
Std Dev	3.702

Gaus mean : 36.3398

Gaus Width: 3.6426

Entry



B0L101S, U25-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	42.14
Std Dev	3.724

Gaus mean : 42.2171

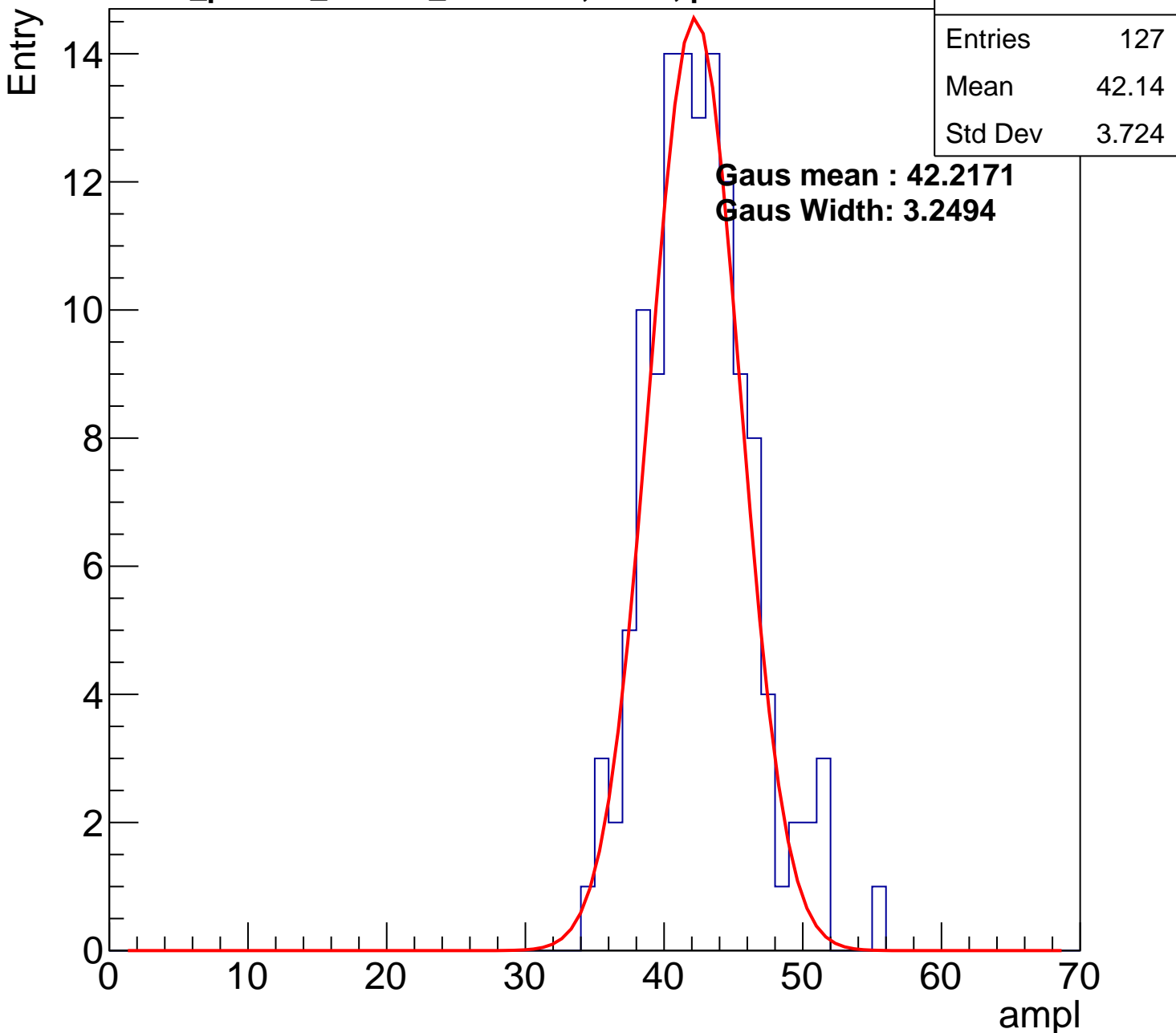
Gaus Width: 3.2494

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

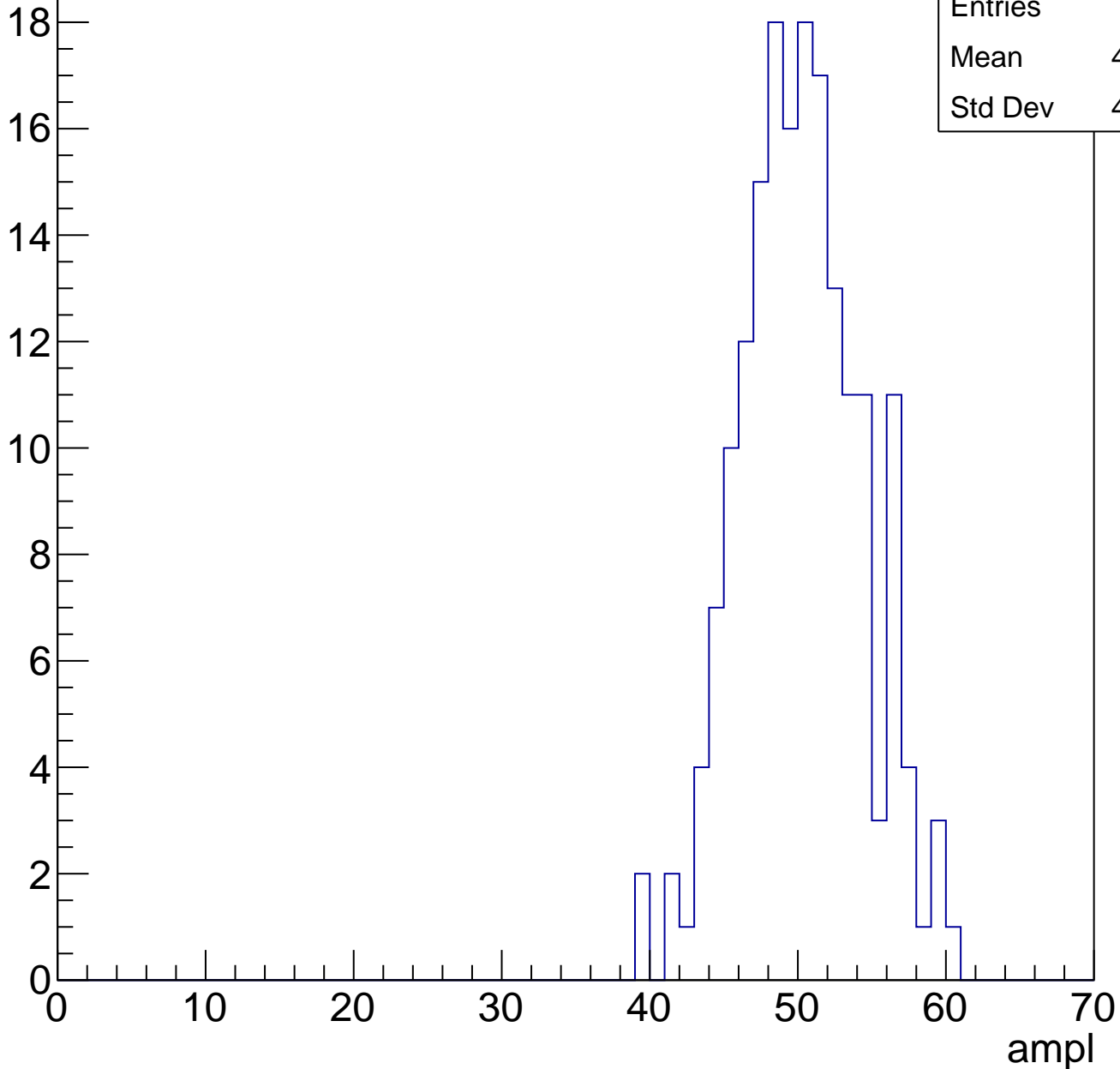


B0L101S, U25-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	49.78
Std Dev	4.076

Entry



B0L101S, U25-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

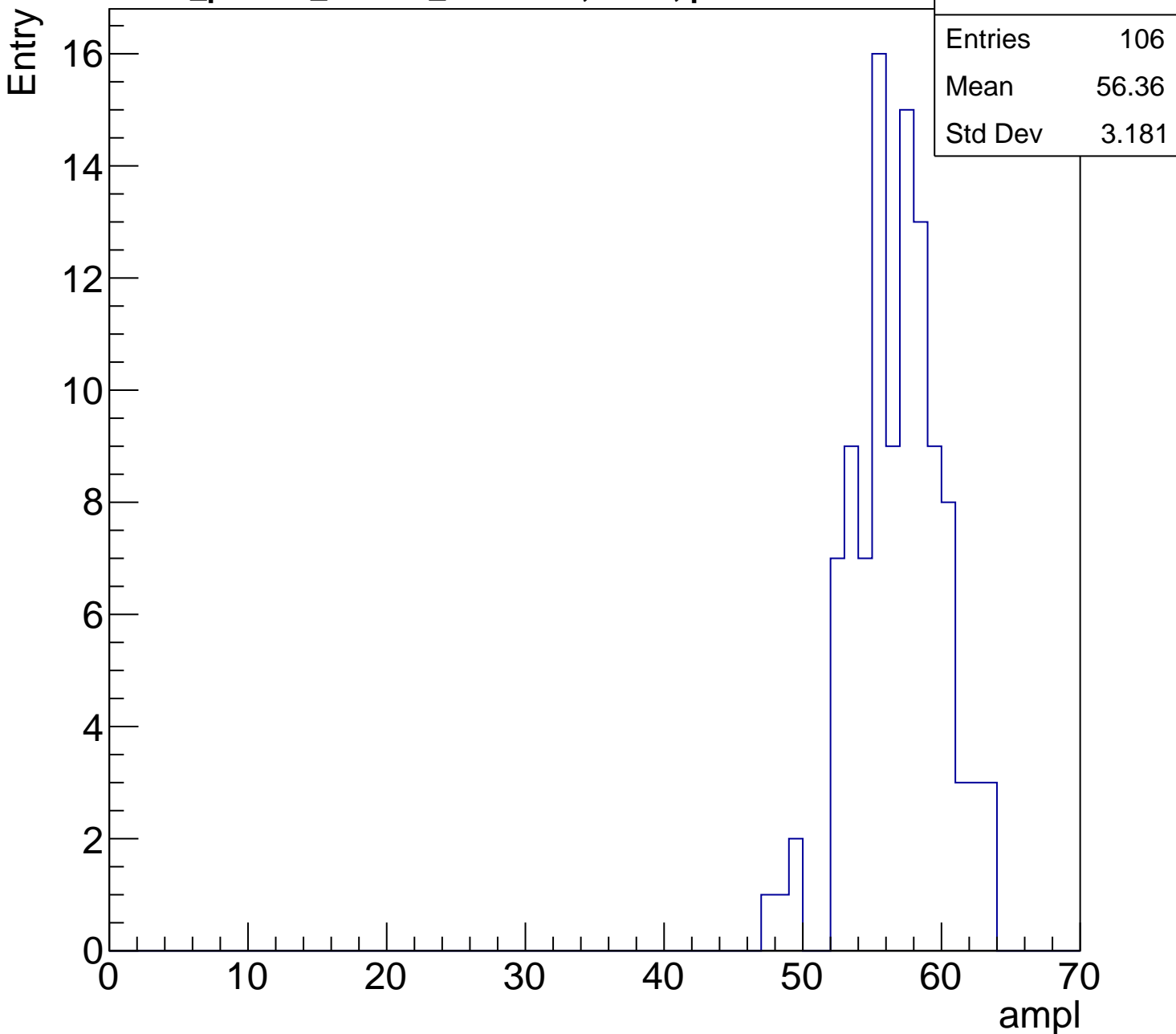
Entries	106
Mean	56.36
Std Dev	3.181

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch72, adc5

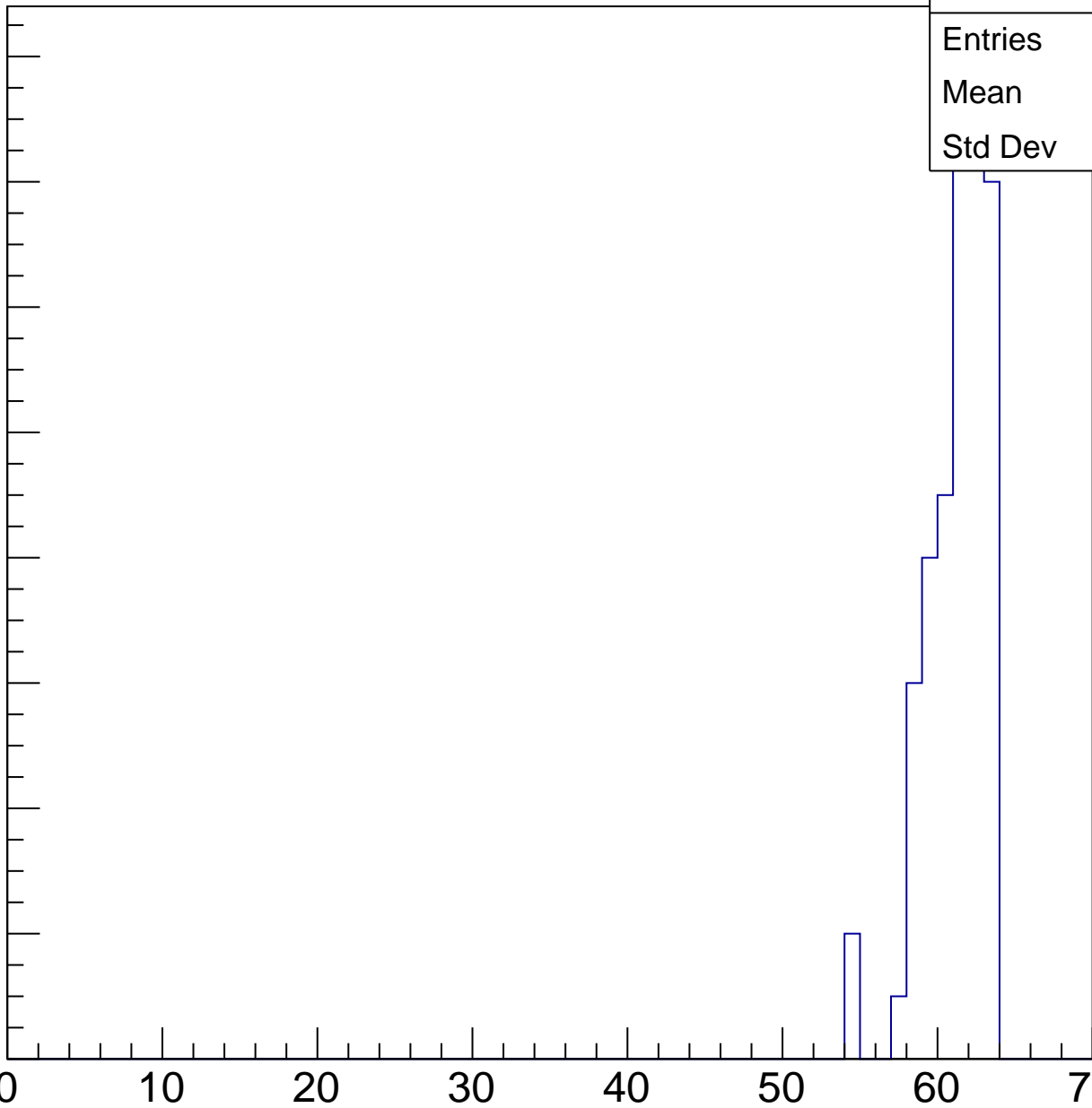
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	72
Mean	60.76
Std Dev	1.961

ampl



B0L101S, U25-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

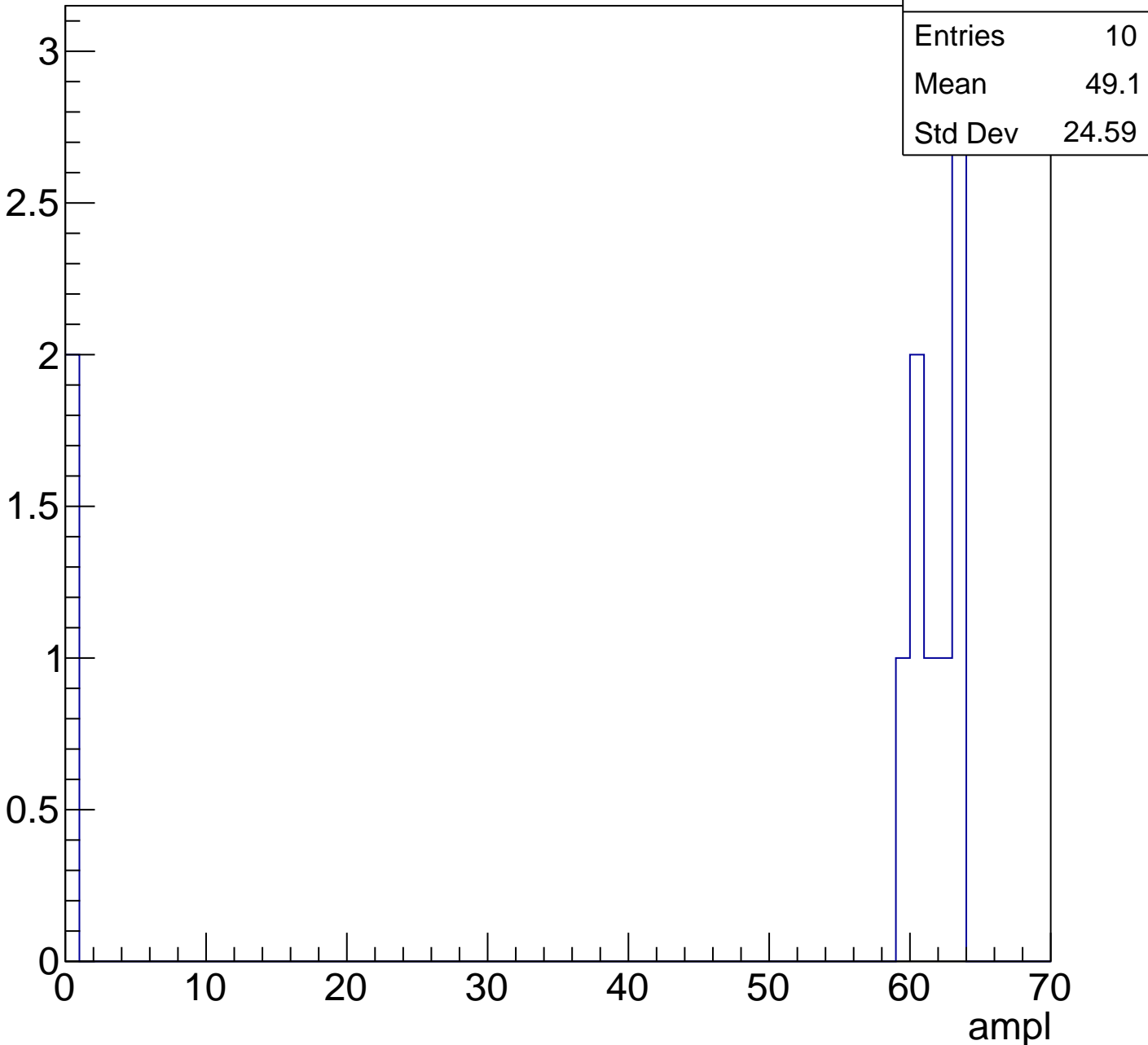
1

0.5

0

ampl

Entries	10
Mean	49.1
Std Dev	24.59



B0L101S, U25-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch73, adc0

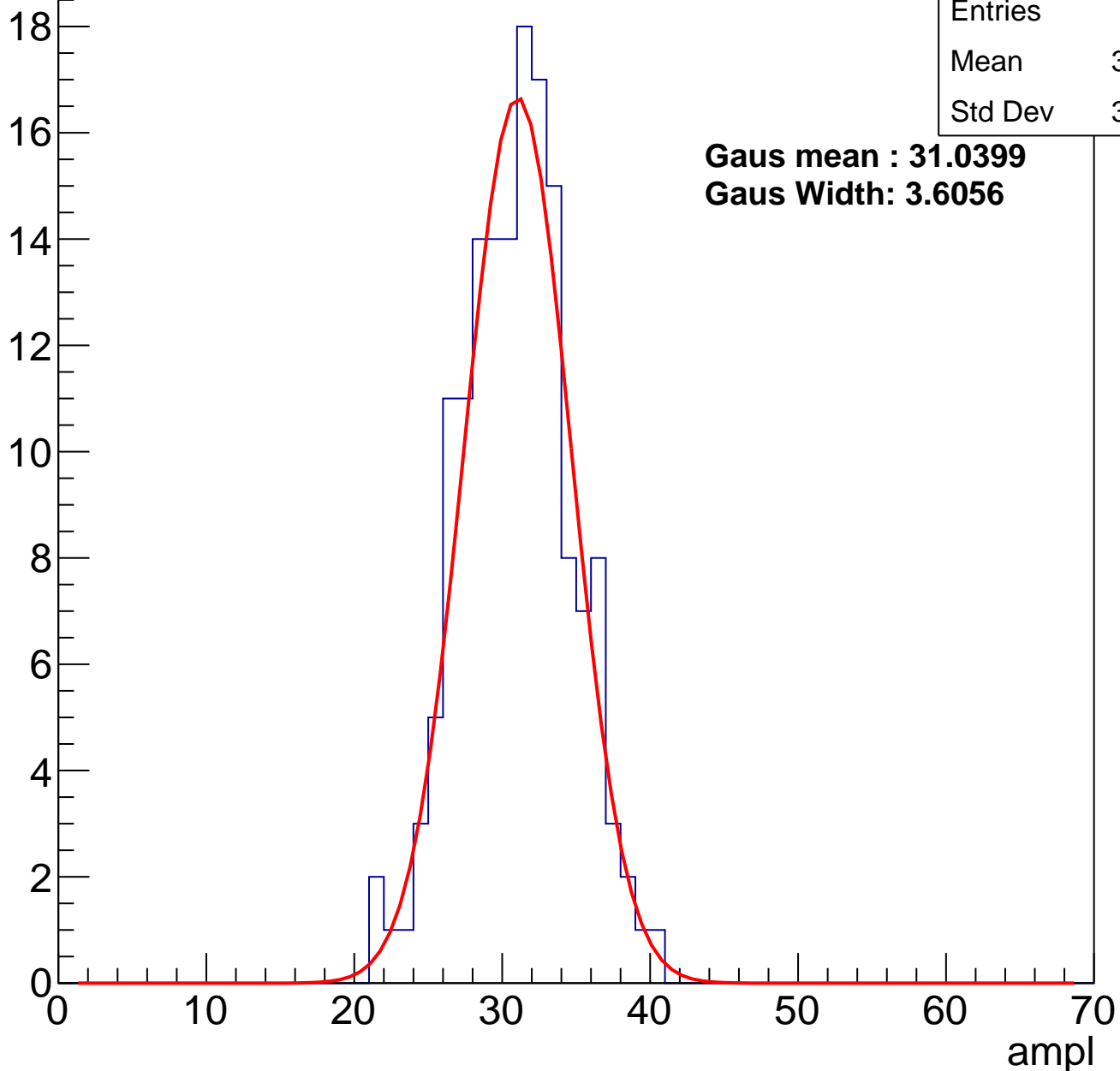
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	30.47
Std Dev	3.657

Gaus mean : 31.0399

Gaus Width: 3.6056

Entry



B0L101S, U25-ch73, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	37.77
Std Dev	3.217

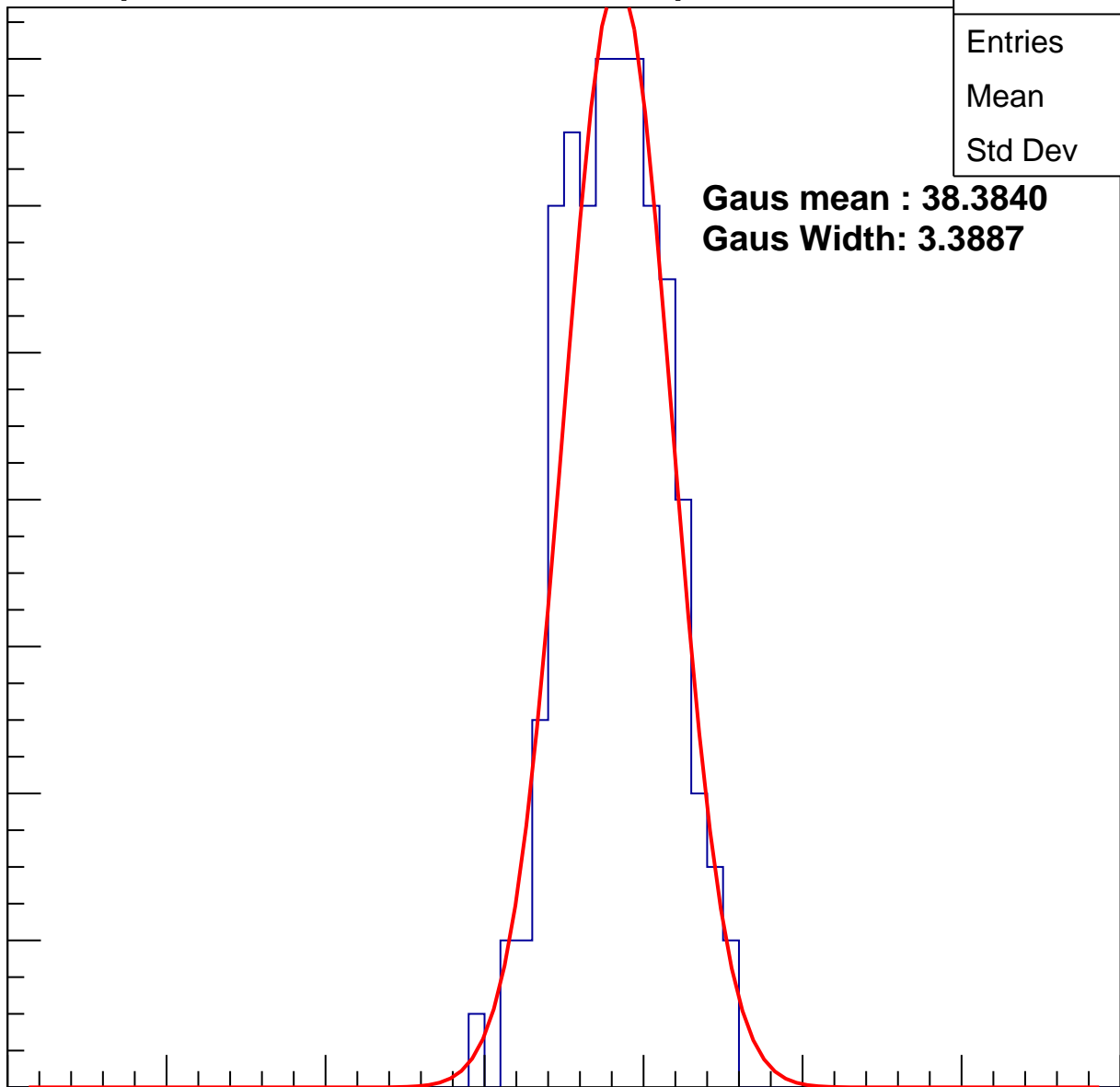
Gaus mean : 38.3840
Gaus Width: 3.3887

Entry

14
12
10
8
6
4
2
0

ampl

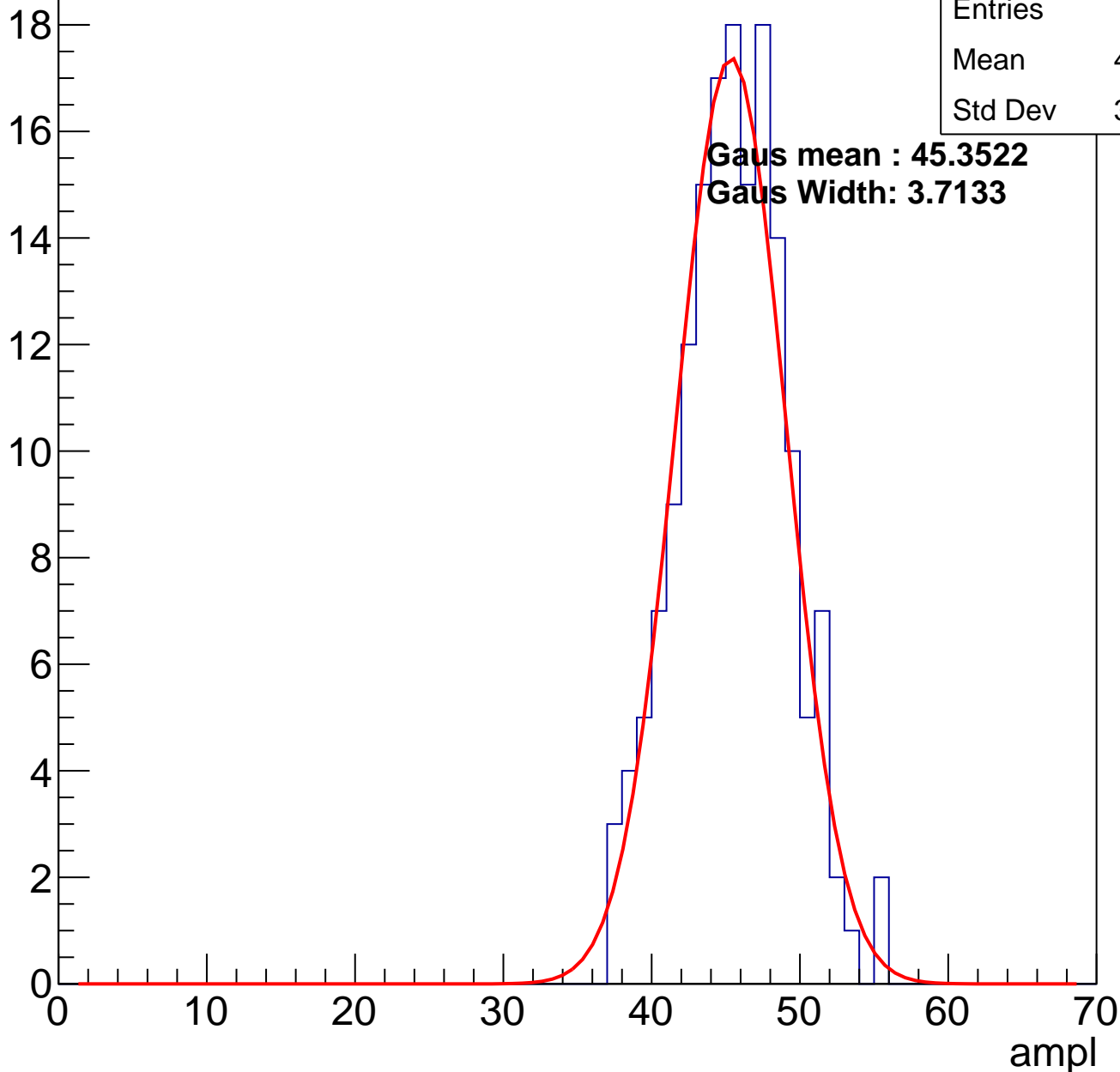
0 10 20 30 40 50 60 70



B0L101S, U25-ch73, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch73, adc3

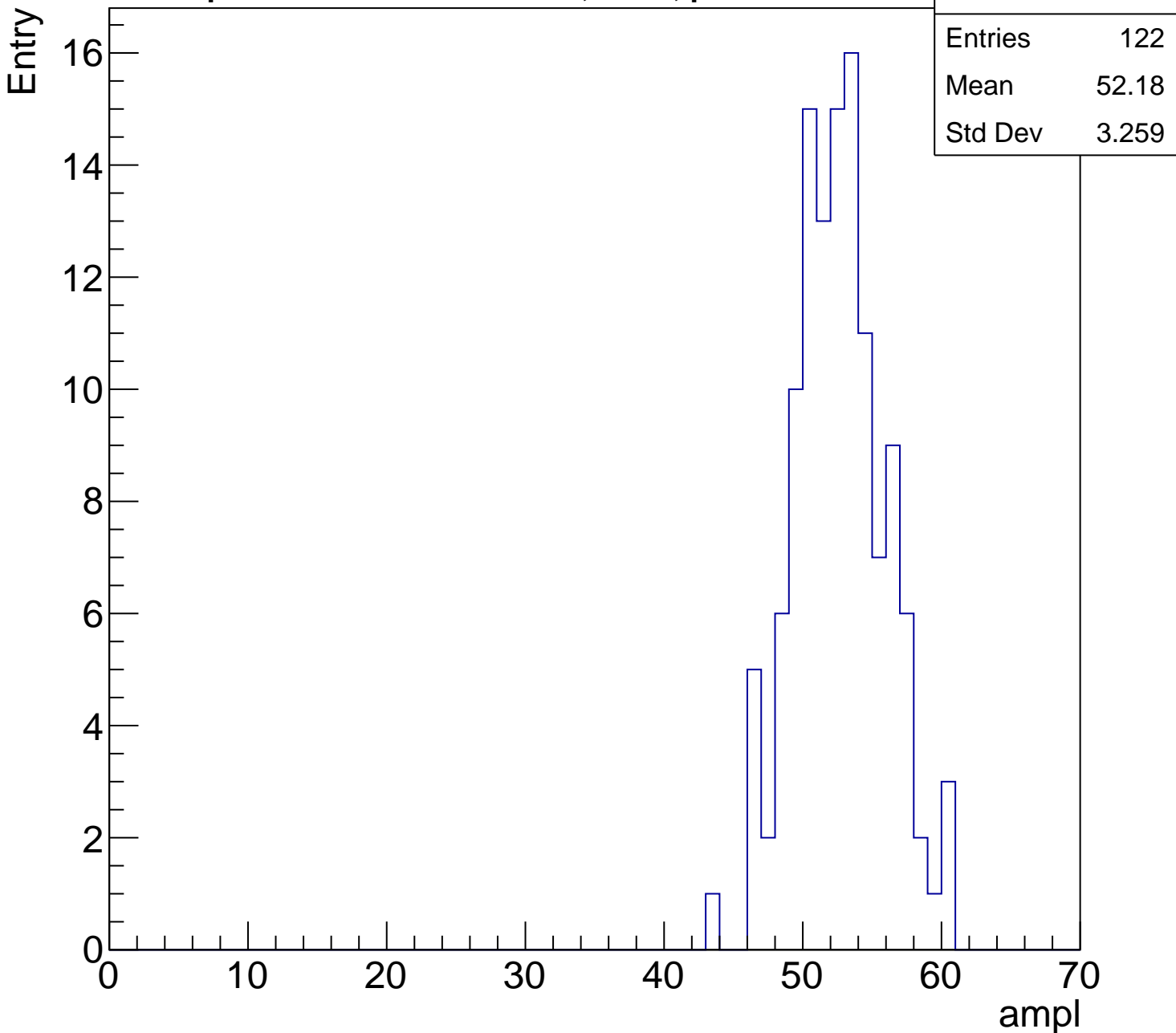
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	52.18
Std Dev	3.259

ampl



B0L101S, U25-ch73, adc4

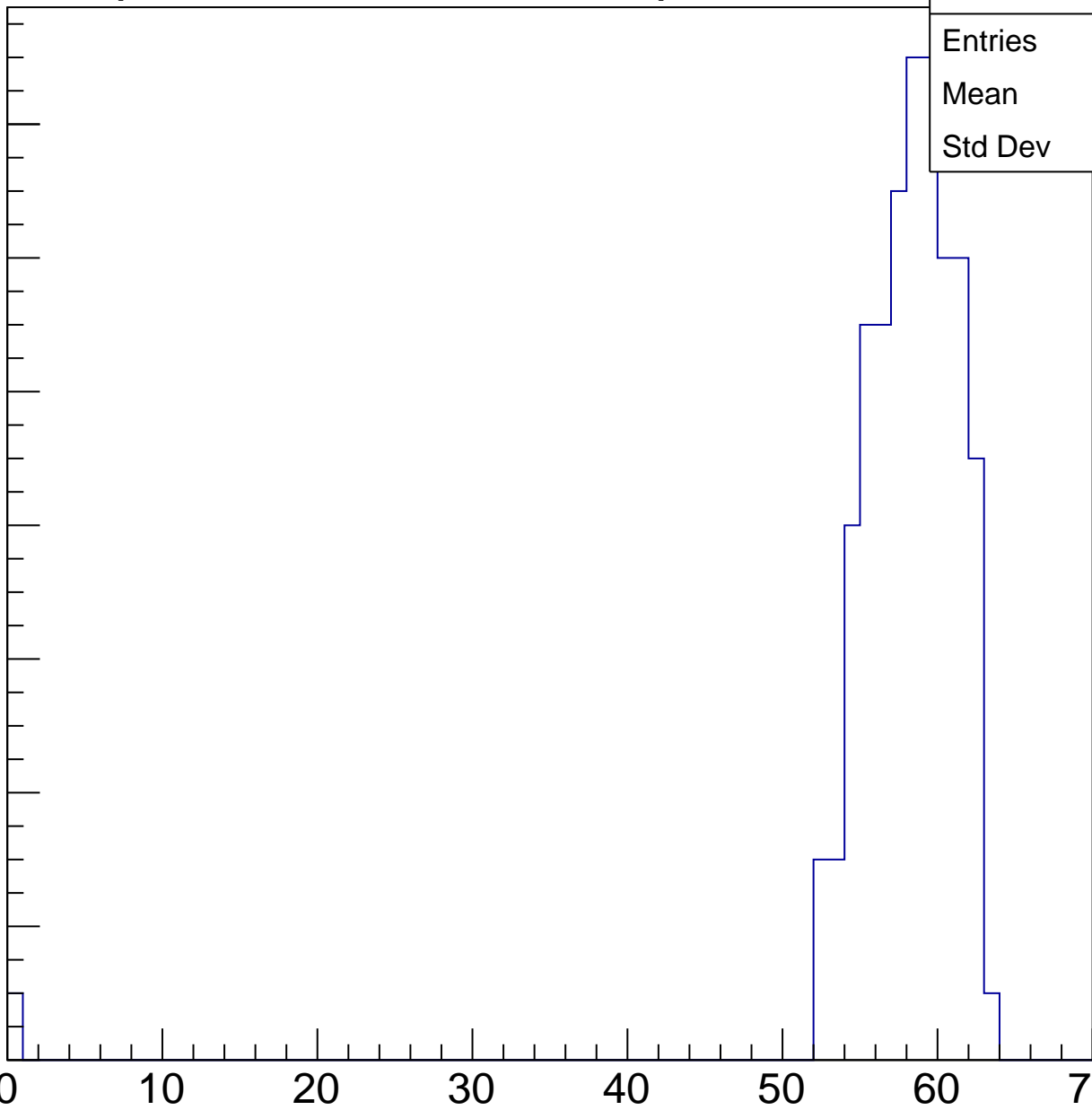
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	57.34
Std Dev	6.013

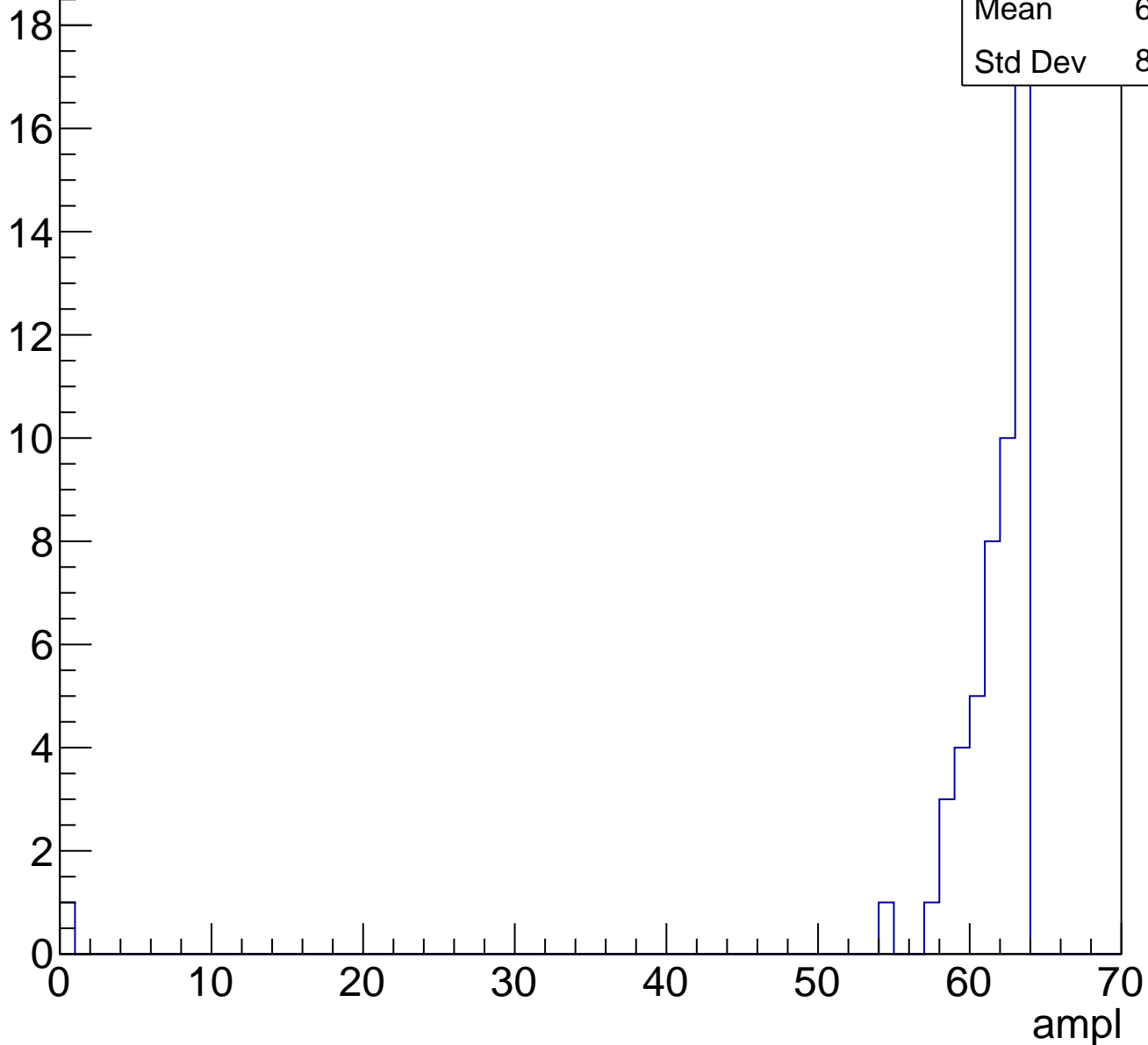
ampl



B0L101S, U25-ch73, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch74, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.08
Std Dev	3.987

Gaus mean : 28.5074

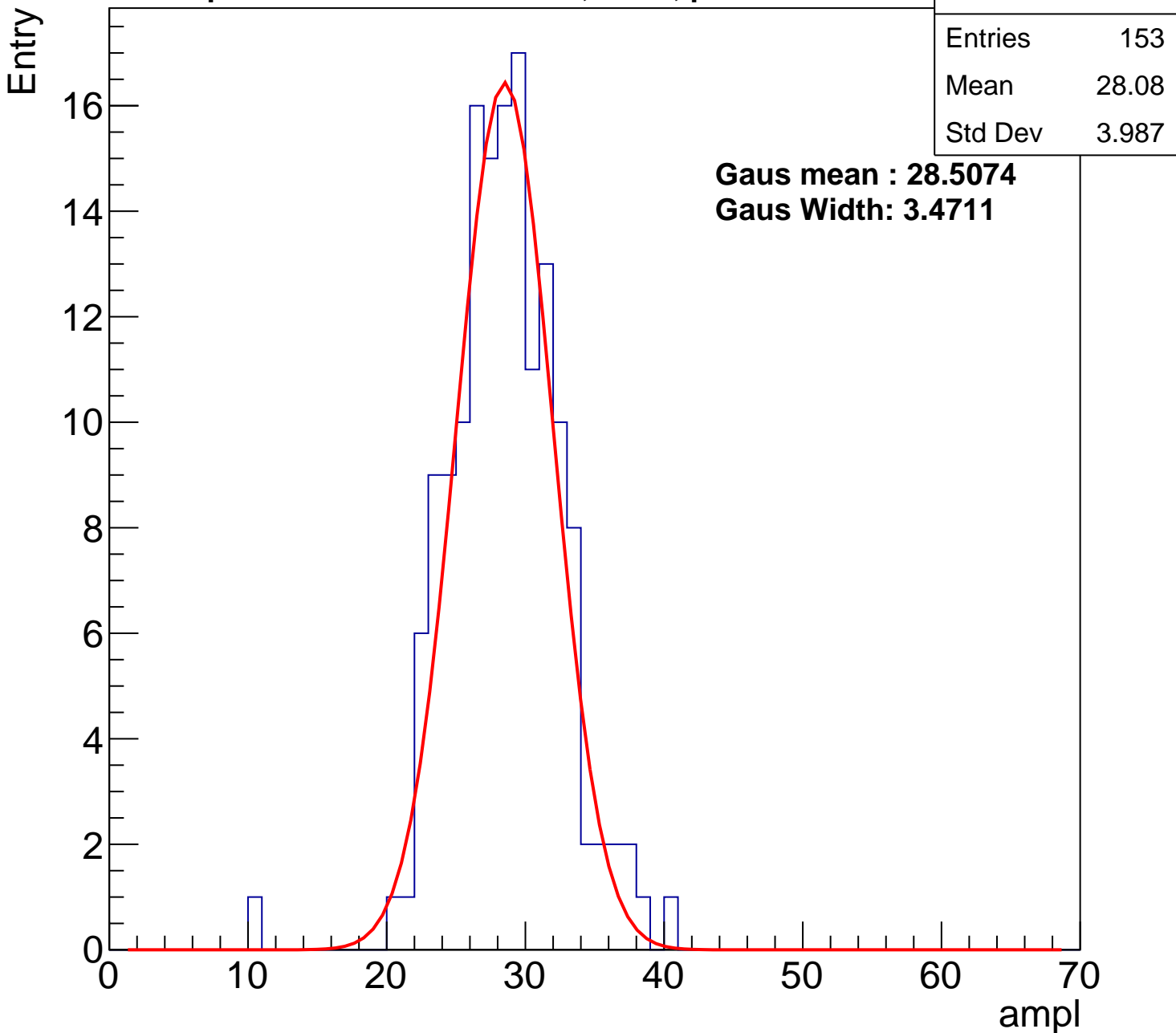
Gaus Width: 3.4711

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

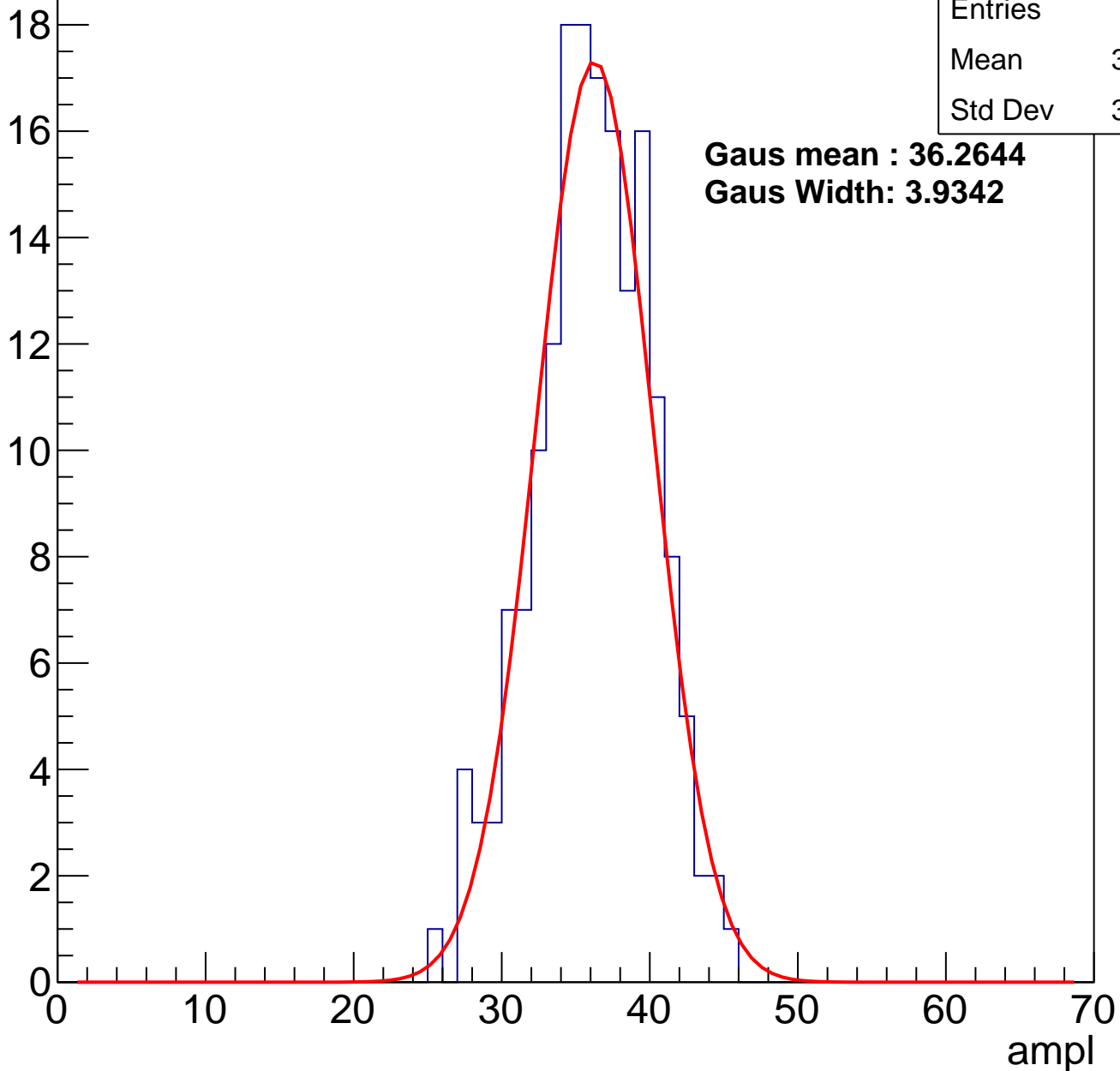
ampl



B0L101S, U25-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch74, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	43.82
Std Dev	3.251

Gaus mean : 44.3594

Gaus Width: 3.3315

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

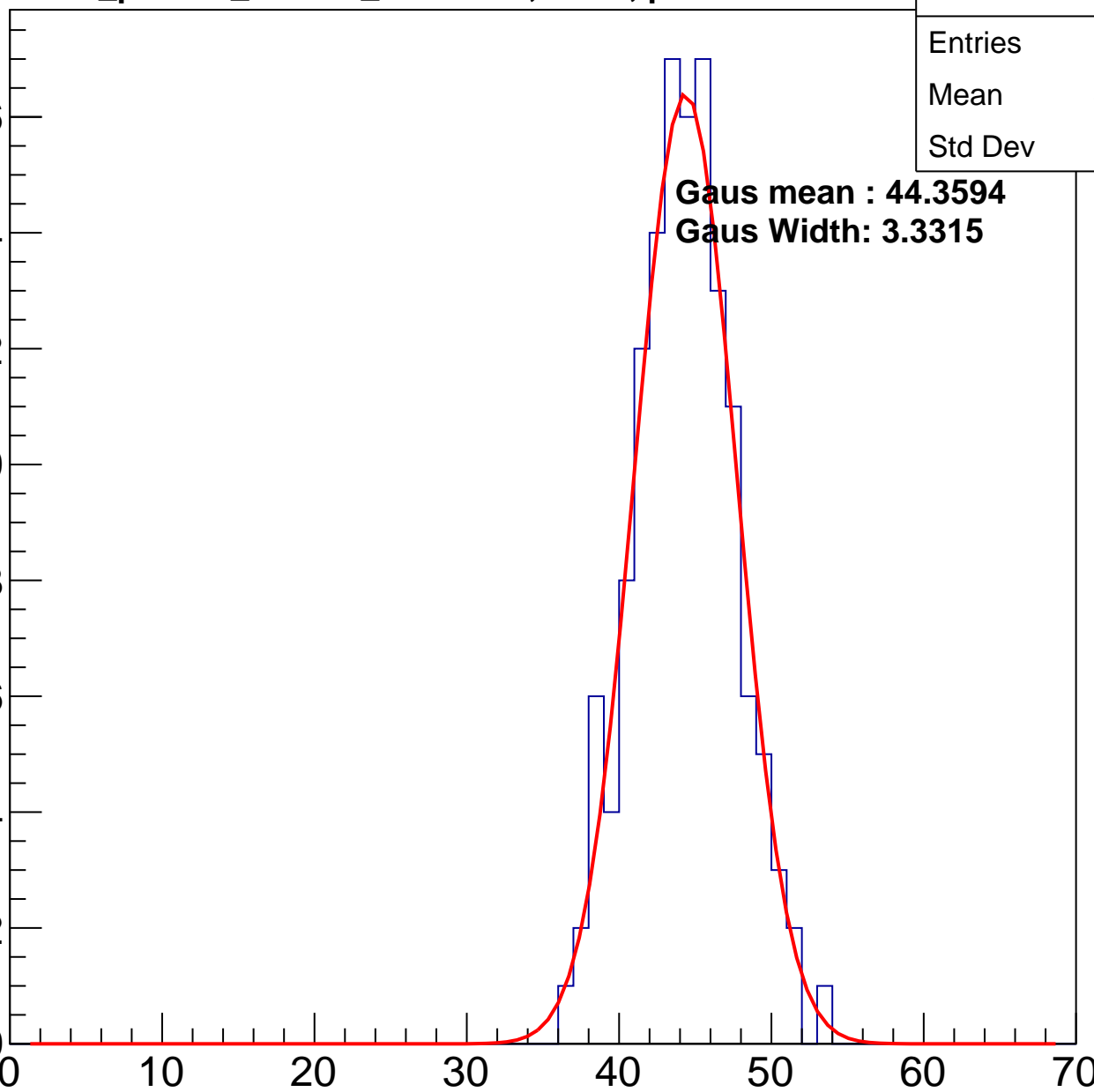
40

50

60

70

ampl



B0L101S, U25-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	50.92
Std Dev	3.492

Entry

16
14
12
10
8
6
4
2
0

0

10

20

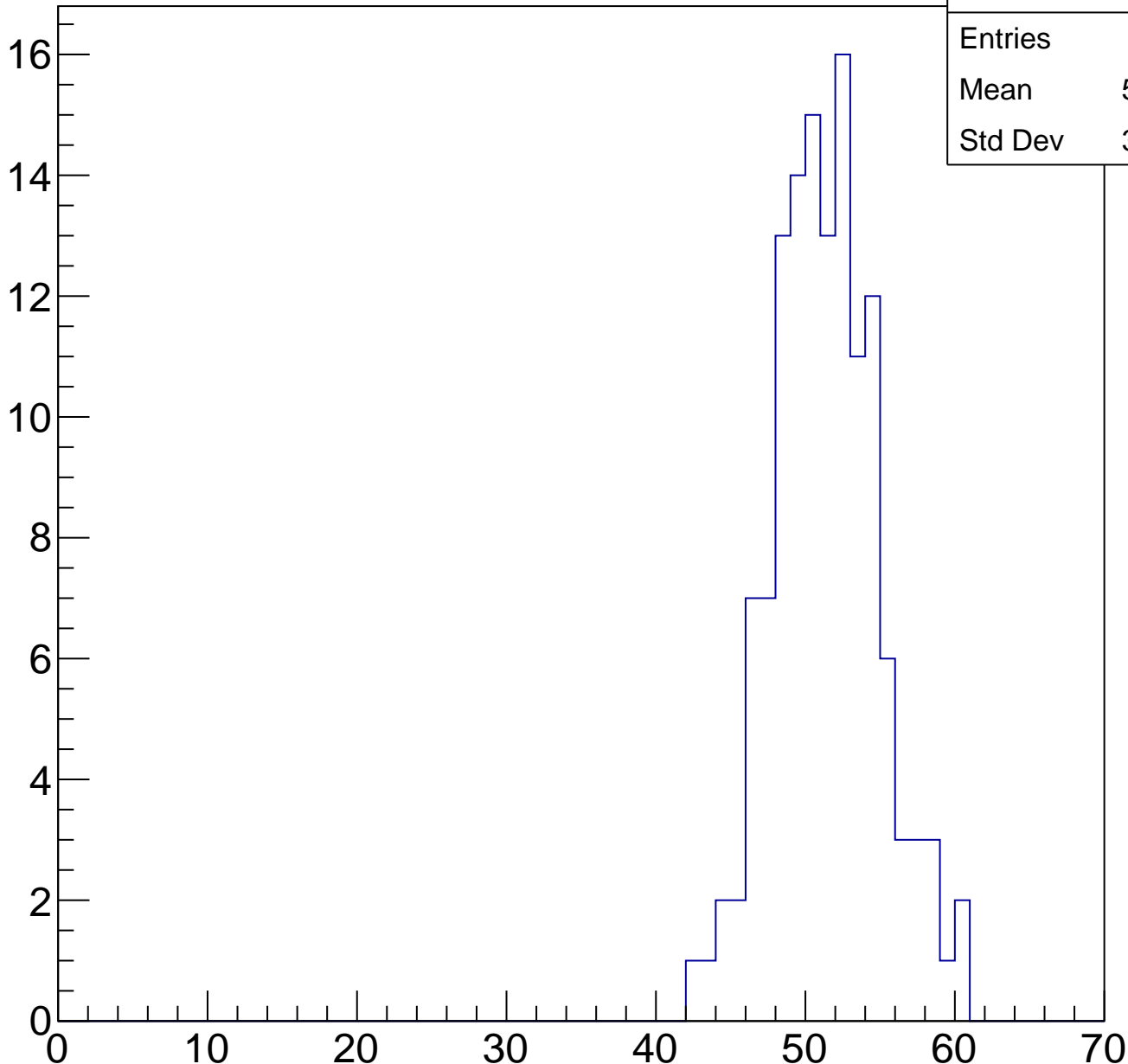
30

40

50

60

ampl



B0L101S, U25-ch74, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

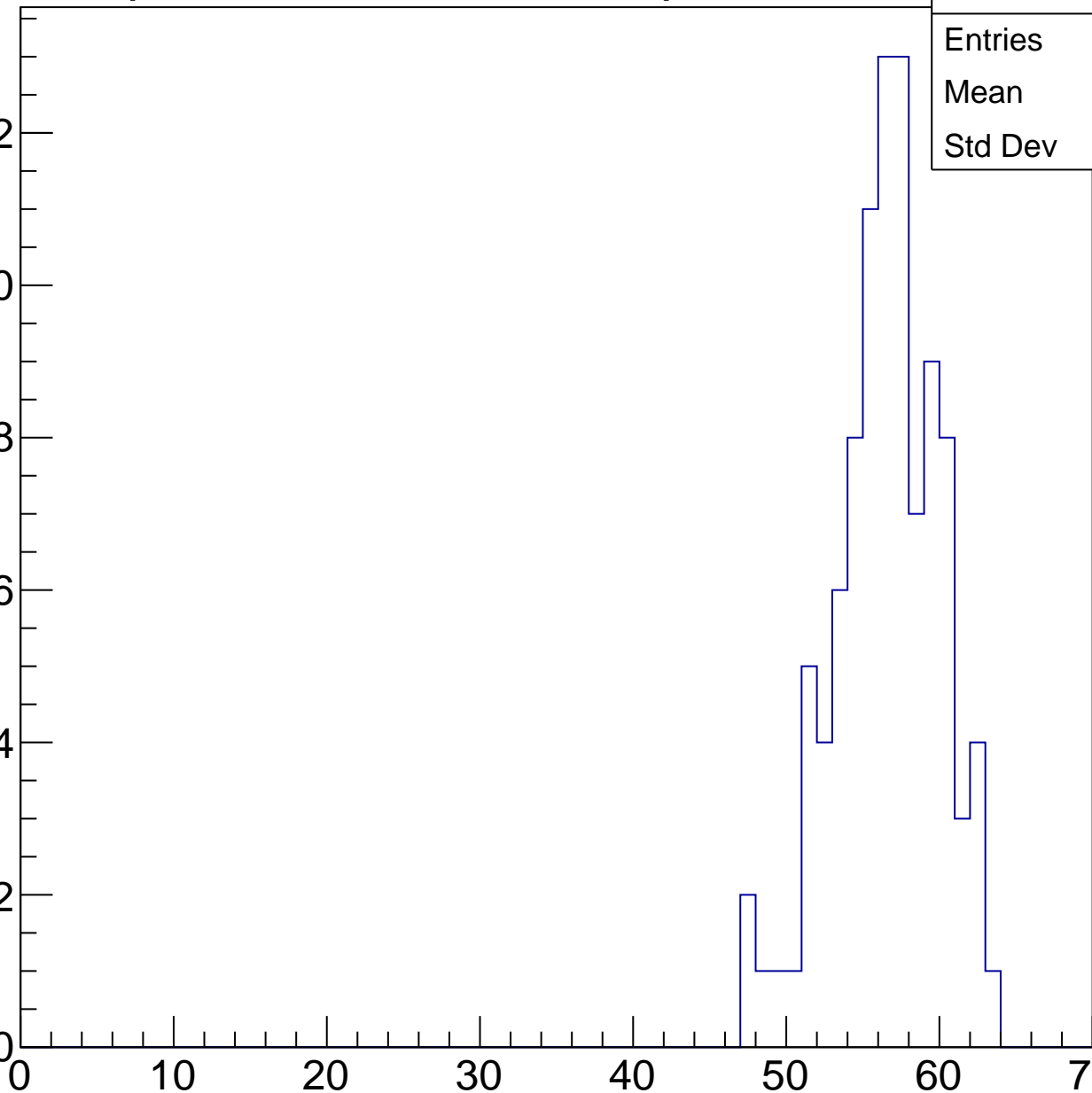
4

2

0

Entries	97
Mean	56.07
Std Dev	3.39

ampl



B0L101S, U25-ch74, adc5

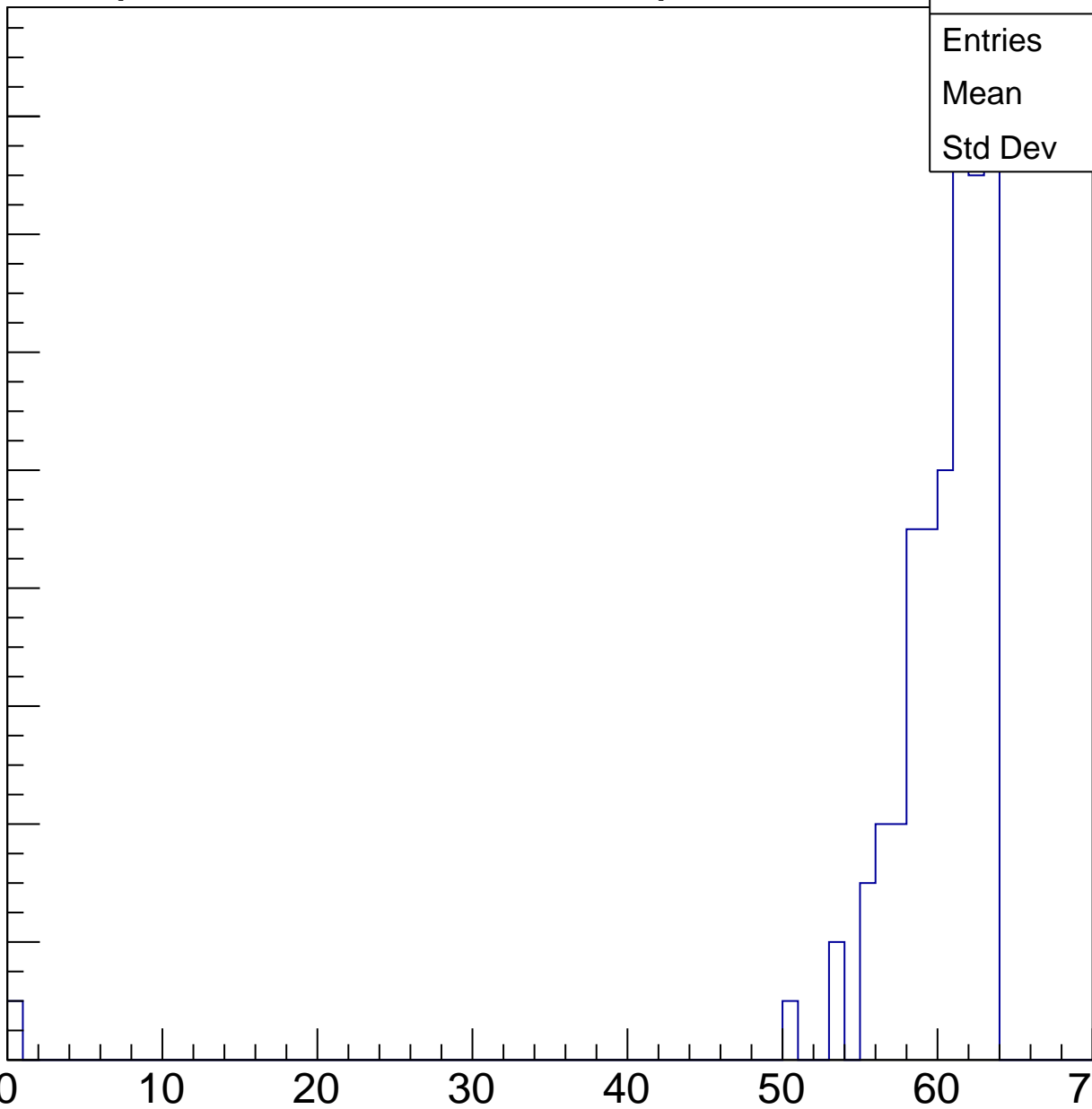
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.35
Std Dev	6.794

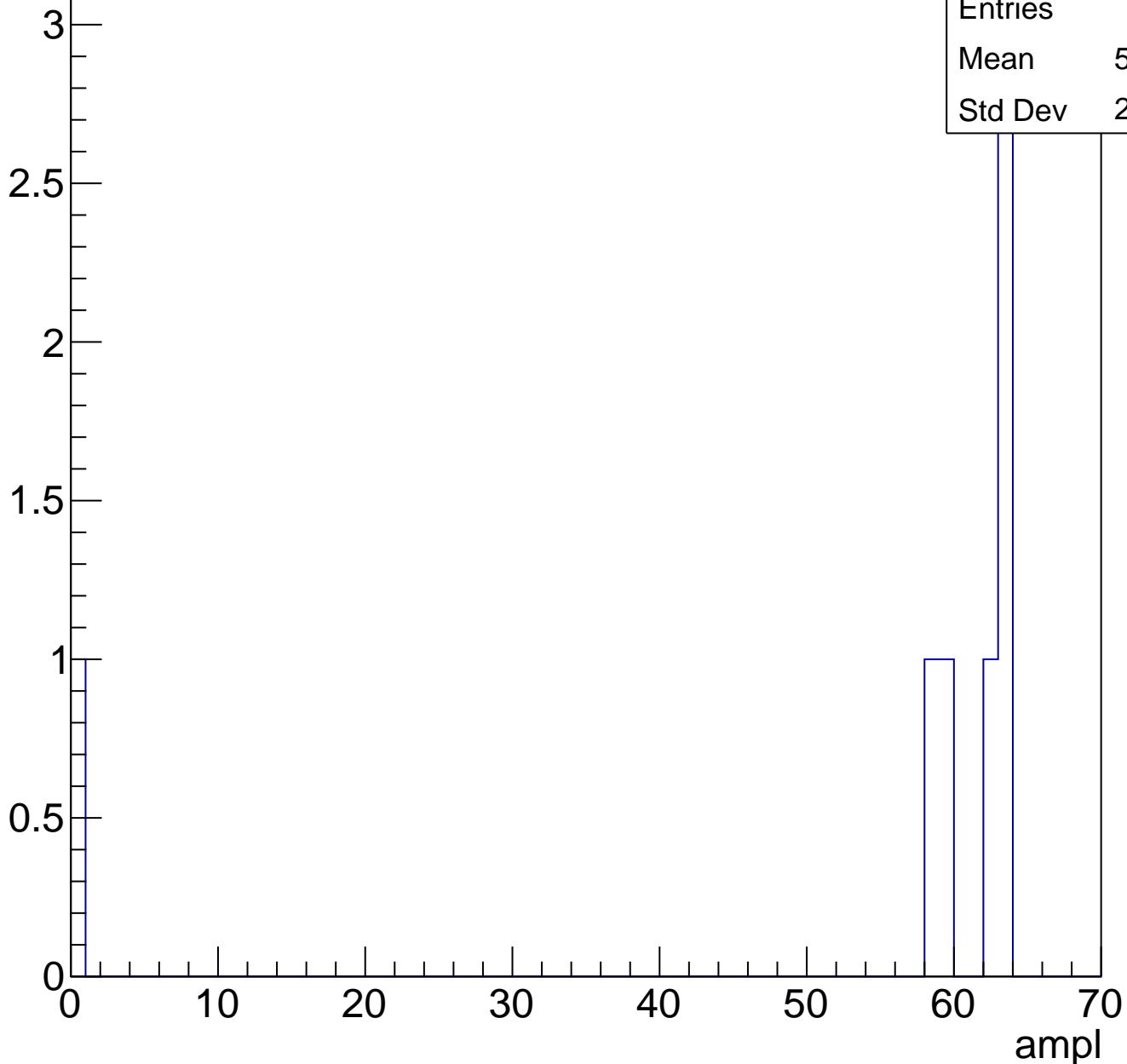
ampl



B0L101S, U25-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch75, adc0

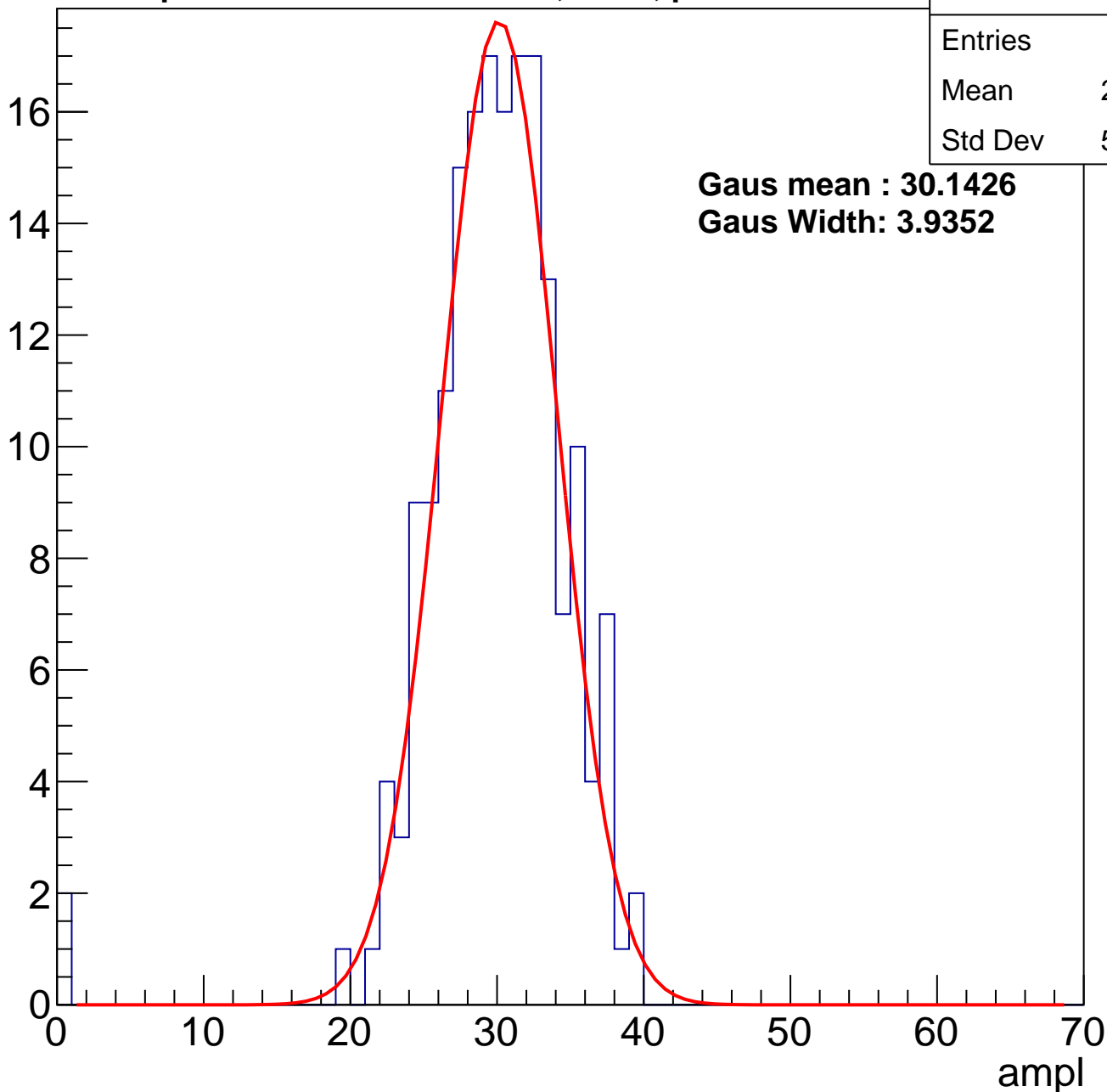
calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	29.43
Std Dev	5.008

Gaus mean : 30.1426

Gaus Width: 3.9352

Entry



B0L101S, U25-ch75, adc1

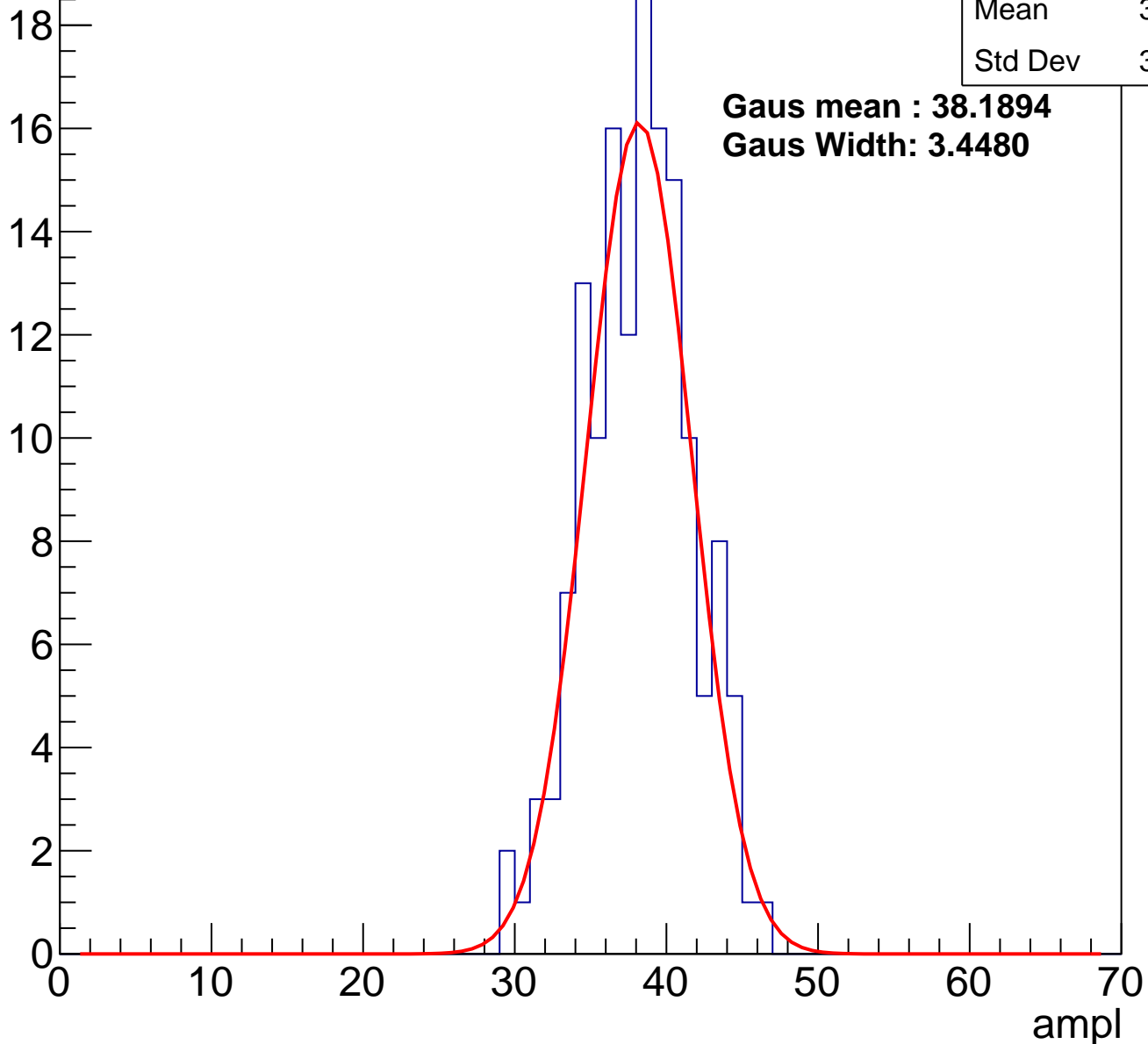
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	37.69
Std Dev	3.455

Gaus mean : 38.1894

Gaus Width: 3.4480

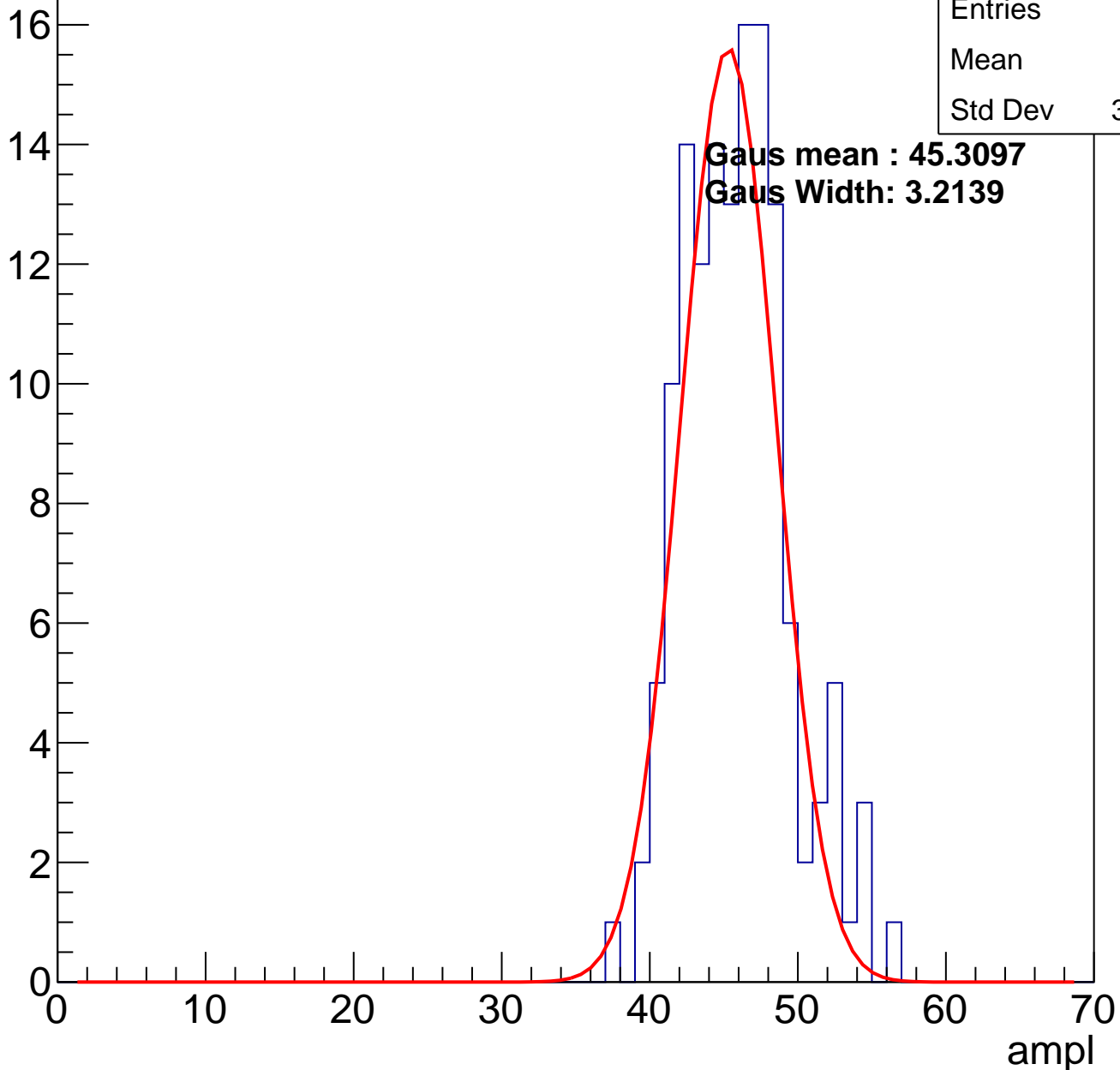
Entry



B0L101S, U25-ch75, adc2

calib_packv5_042523_0143.root, FC#1, port C1

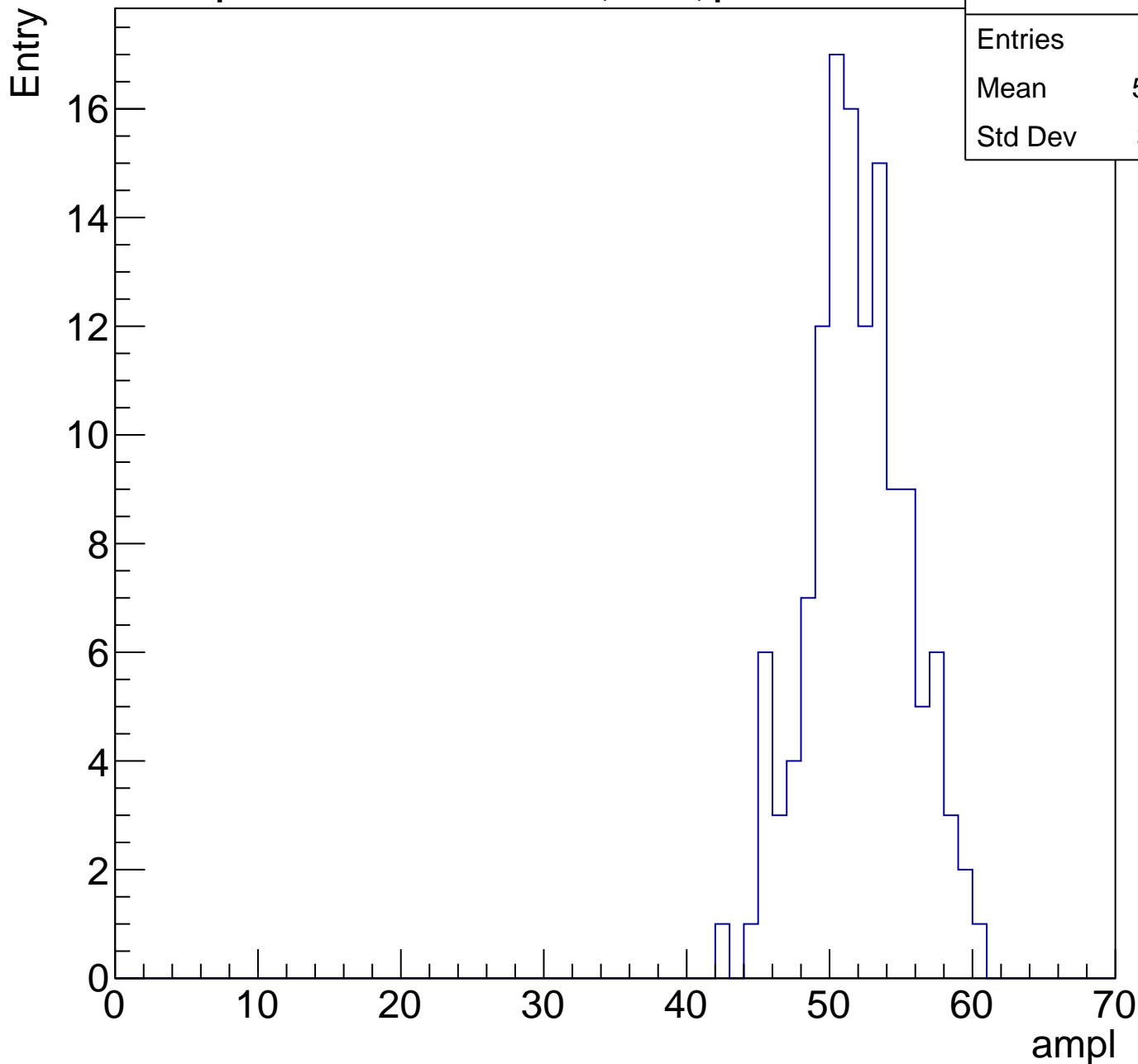
Entry



B0L101S, U25-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	51.52
Std Dev	3.491



B0L101S, U25-ch75, adc4

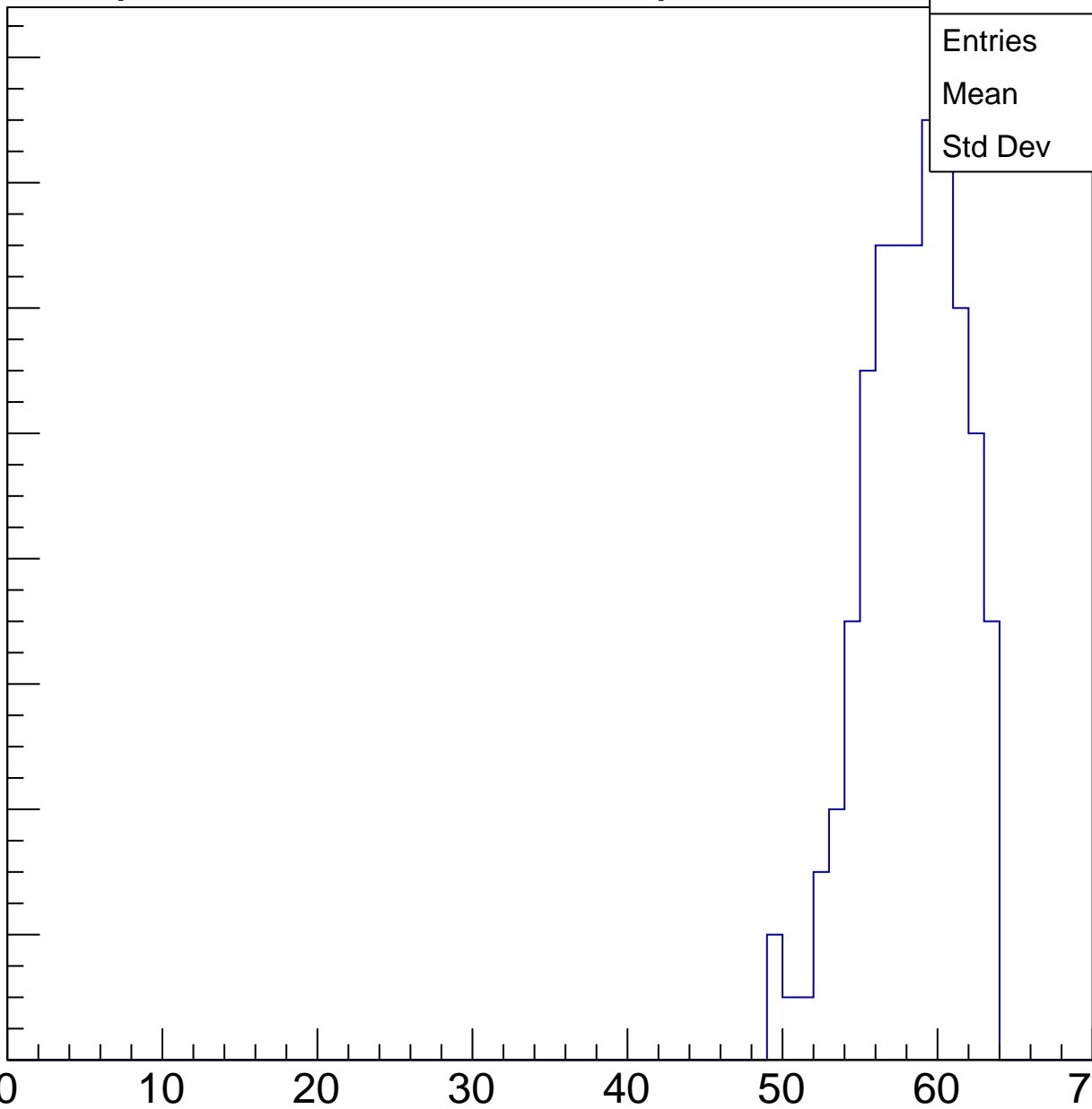
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	57.9
Std Dev	3.162

ampl



B0L101S, U25-ch75, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	43
Mean	58.02
Std Dev	12.99

B0L101S, U25-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch76, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	28.78
Std Dev	4.337

Gaus mean : 29.6196

Gaus Width: 3.6290

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

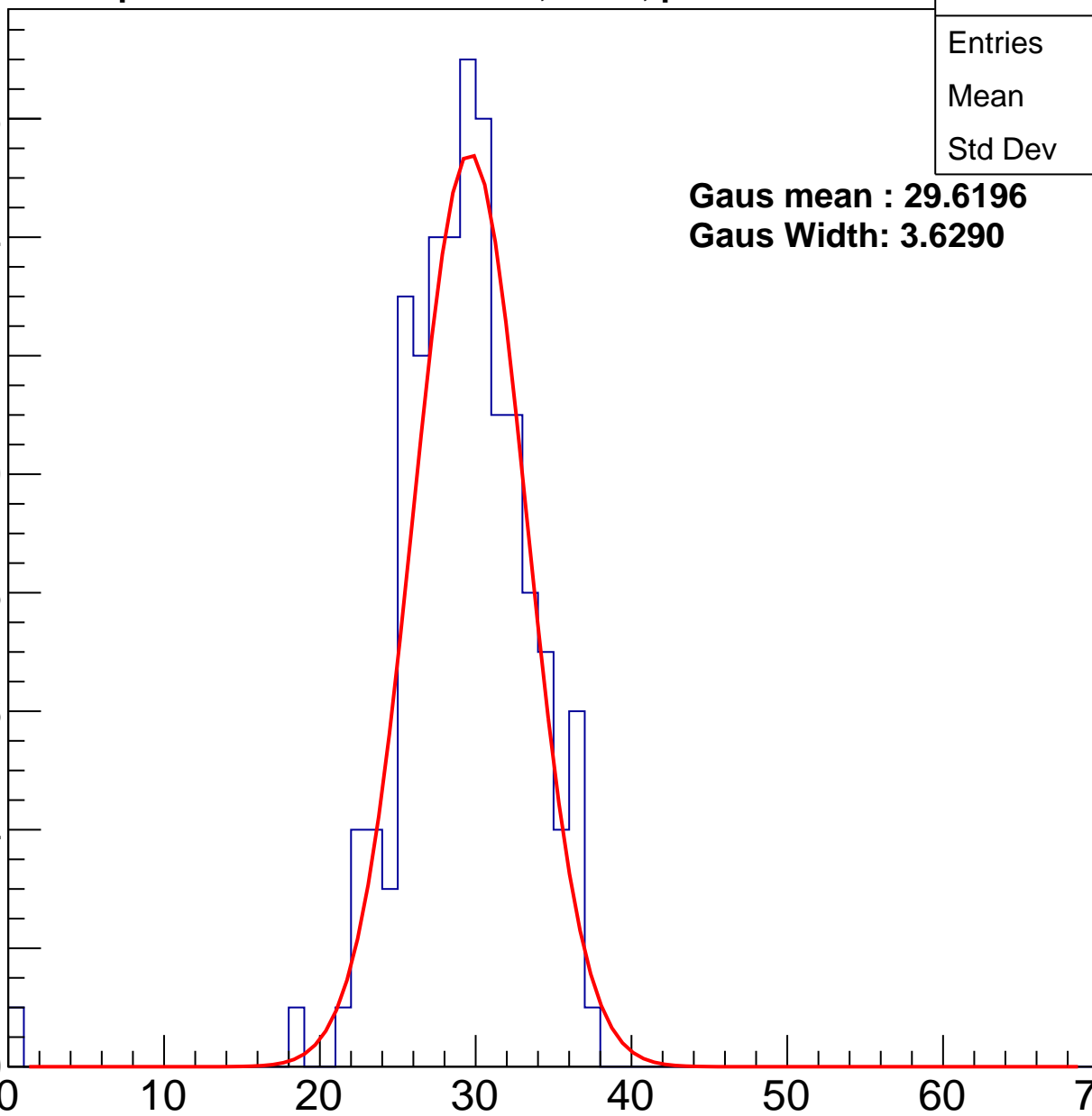
40

50

60

70

ampl



B0L101S, U25-ch76, adc1

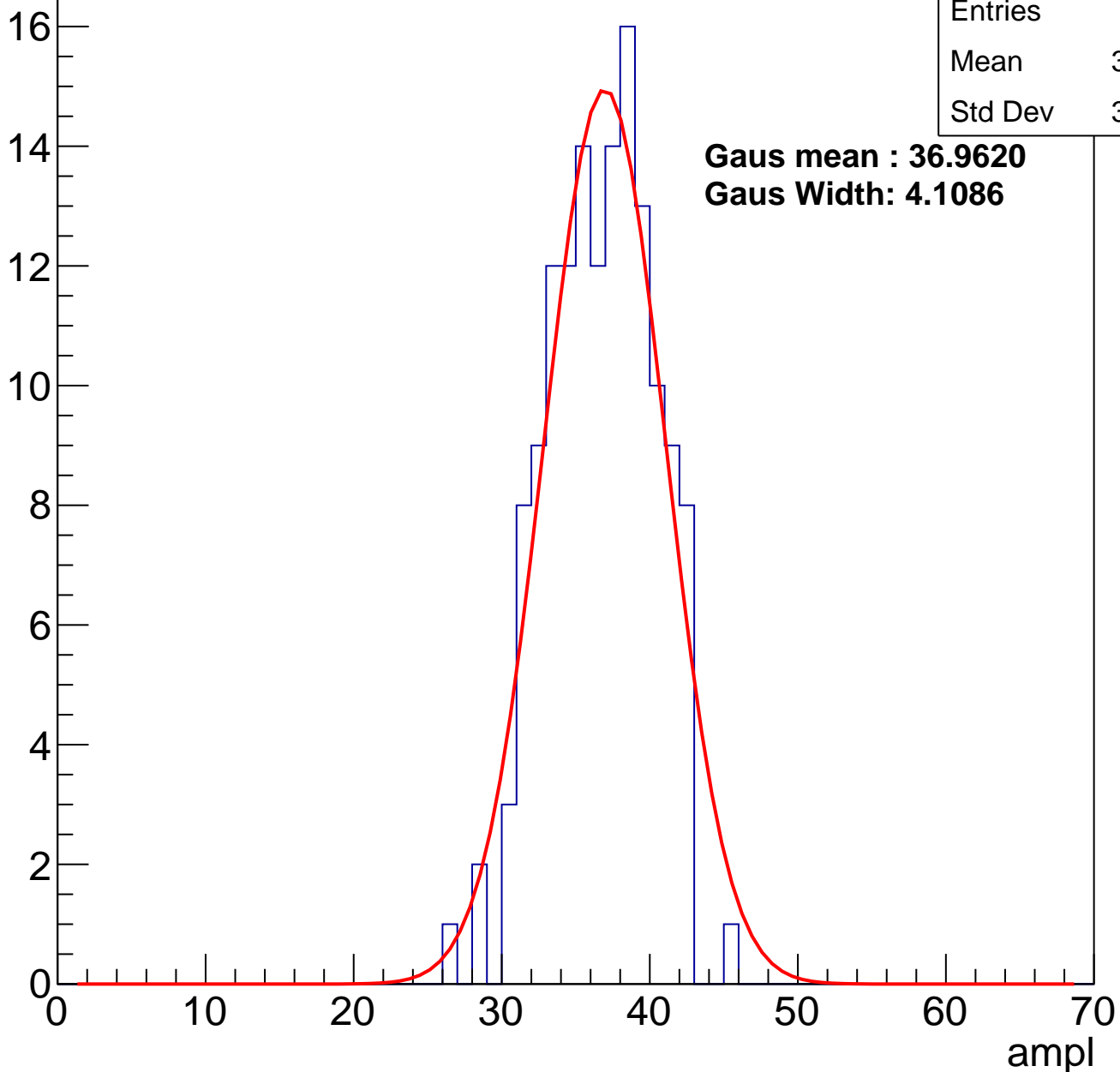
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	36.23
Std Dev	3.527

Gaus mean : 36.9620

Gaus Width: 4.1086

Entry



B0L101S, U25-ch76, adc2

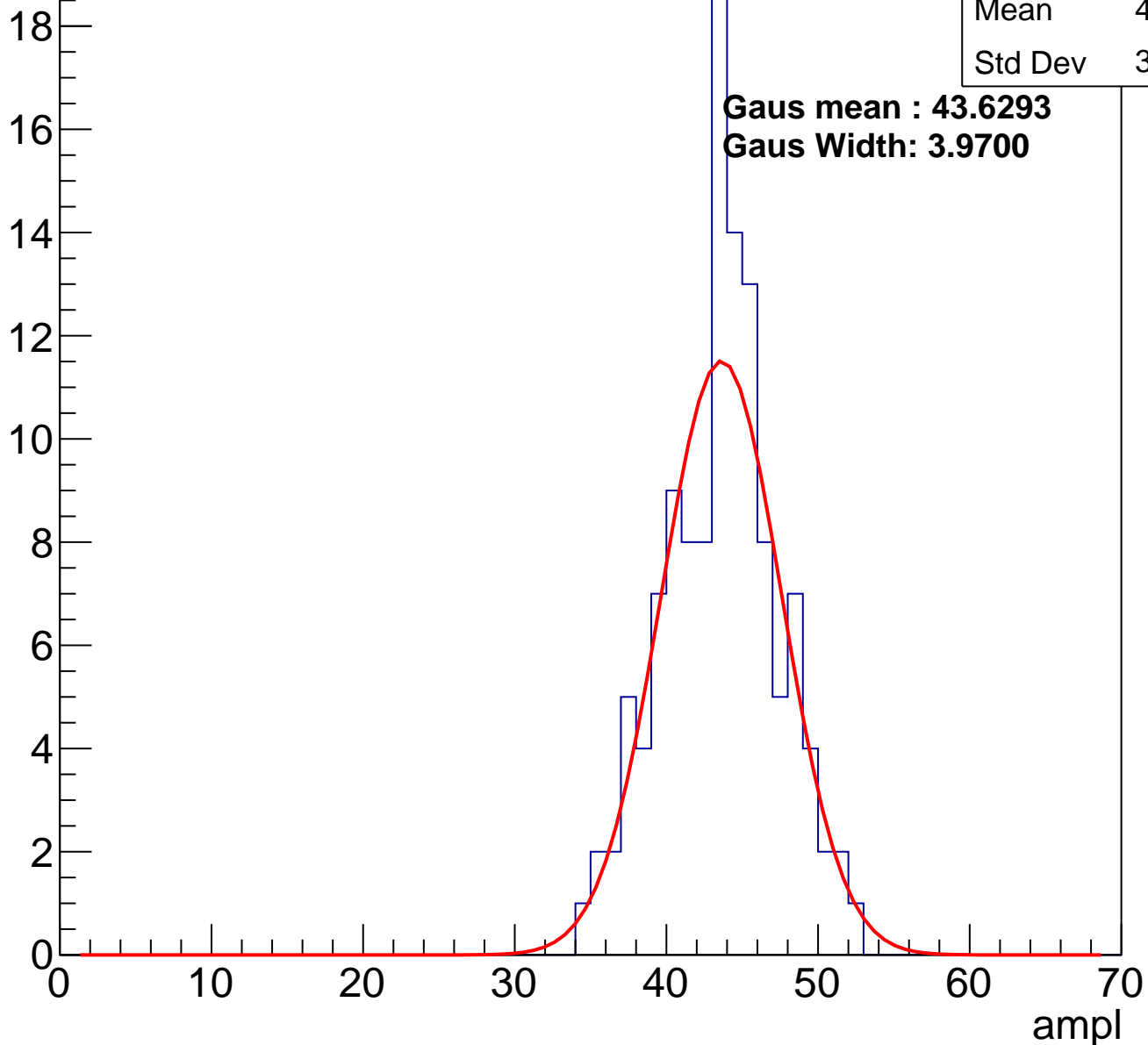
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	43.12
Std Dev	3.707

Gaus mean : 43.6293

Gaus Width: 3.9700

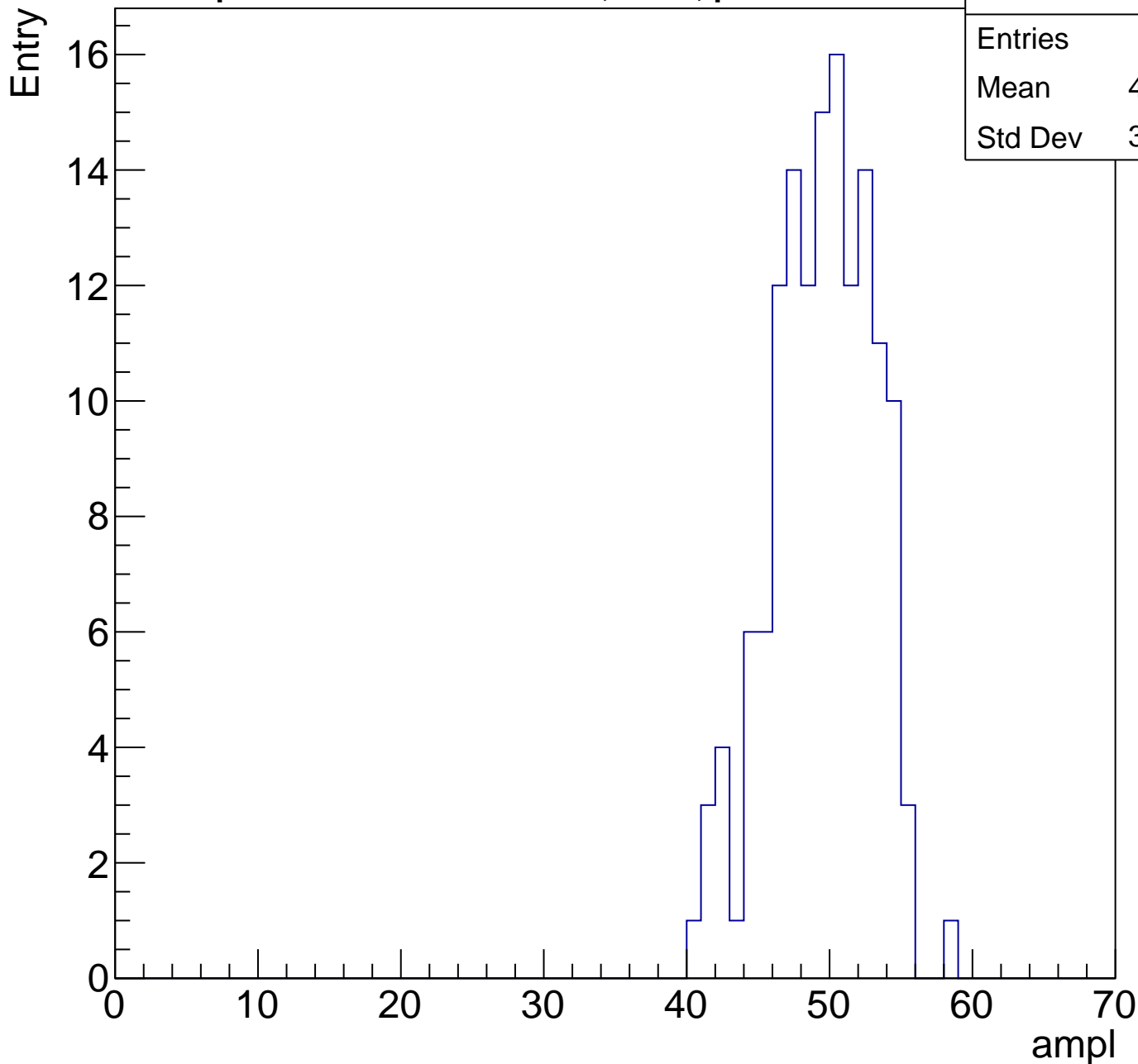
Entry



B0L101S, U25-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	49.04
Std Dev	3.505



B0L101S, U25-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	55.02
Std Dev	6.55

Entry

14
12
10
8
6
4
2
0

0

10

20

30

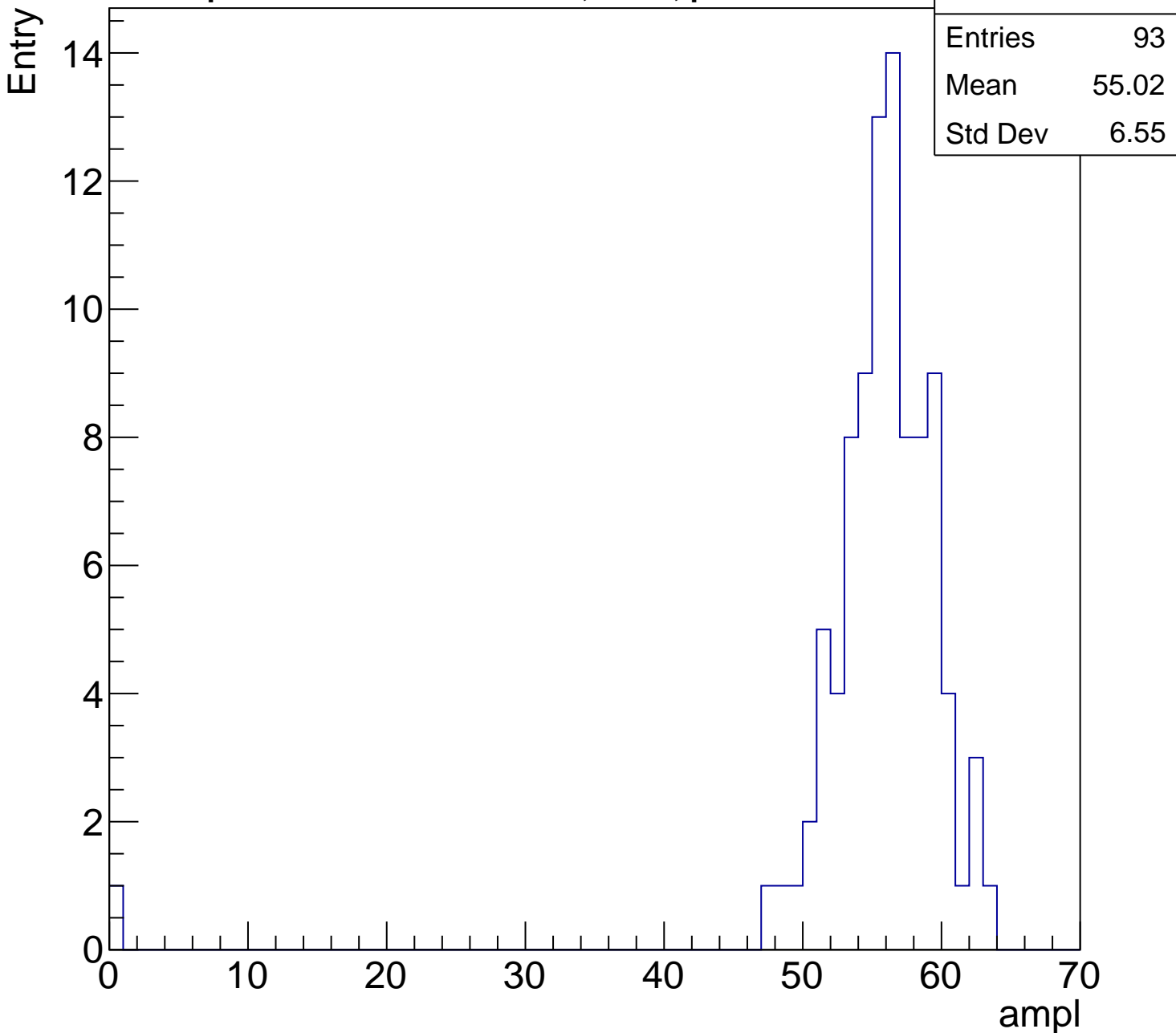
40

50

60

70

ampl



B0L101S, U25-ch76, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	59.43
Std Dev	2.627

ampl

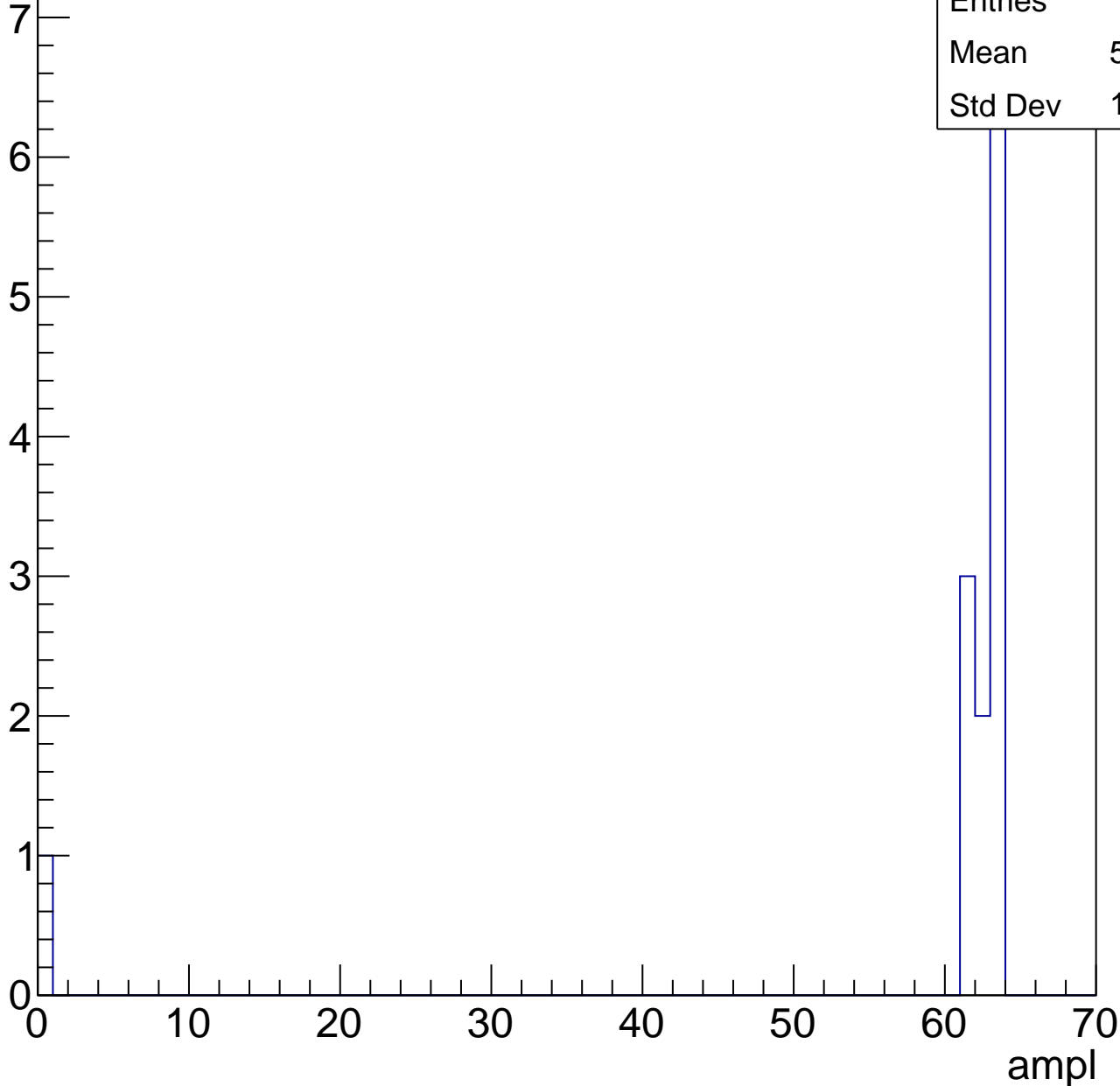
0 10 20 30 40 50 60 70

B0L101S, U25-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.54
Std Dev	16.63



B0L101S, U25-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch77, adc0

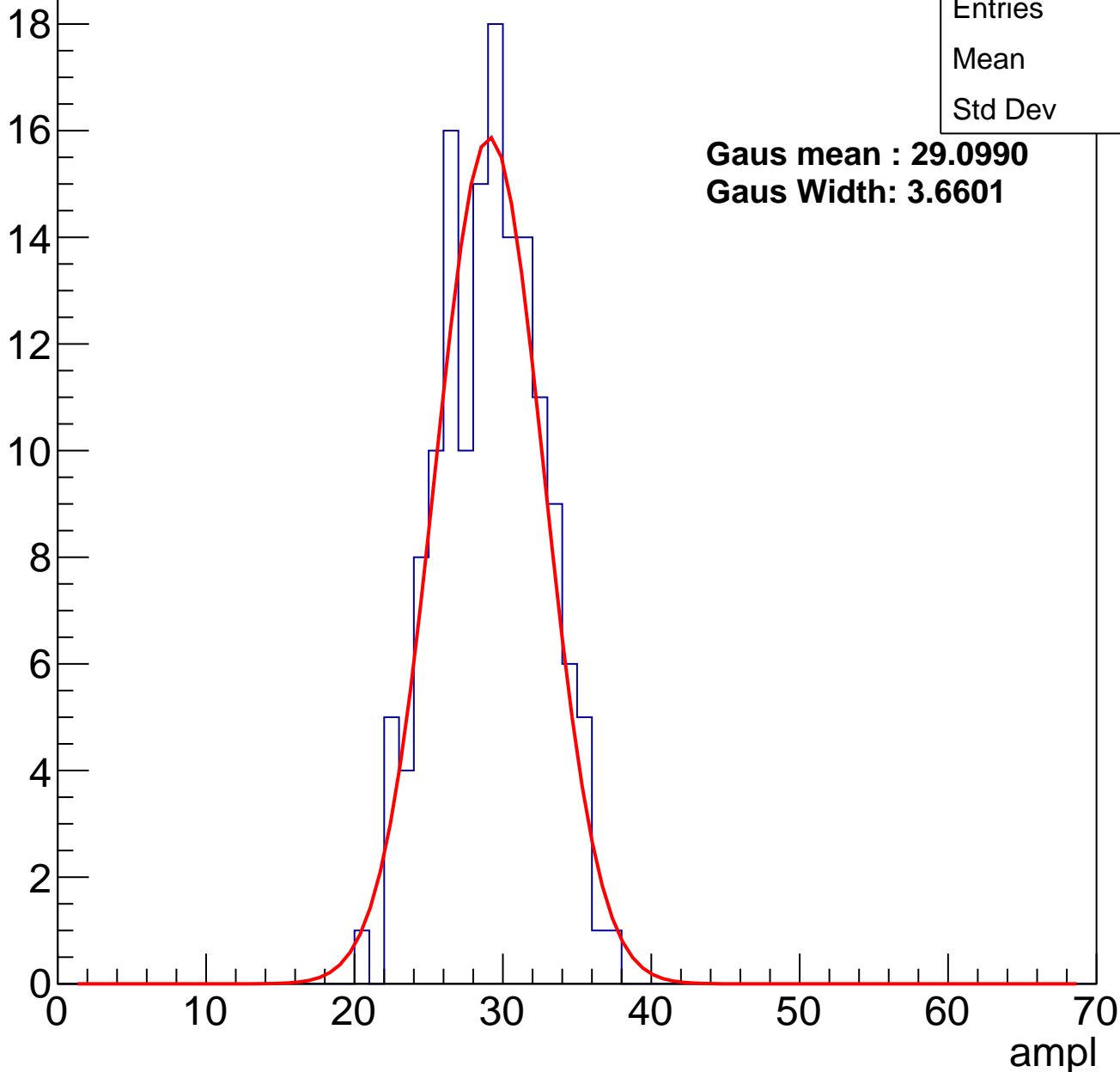
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	28.7
Std Dev	3.46

Gaus mean : 29.0990

Gaus Width: 3.6601

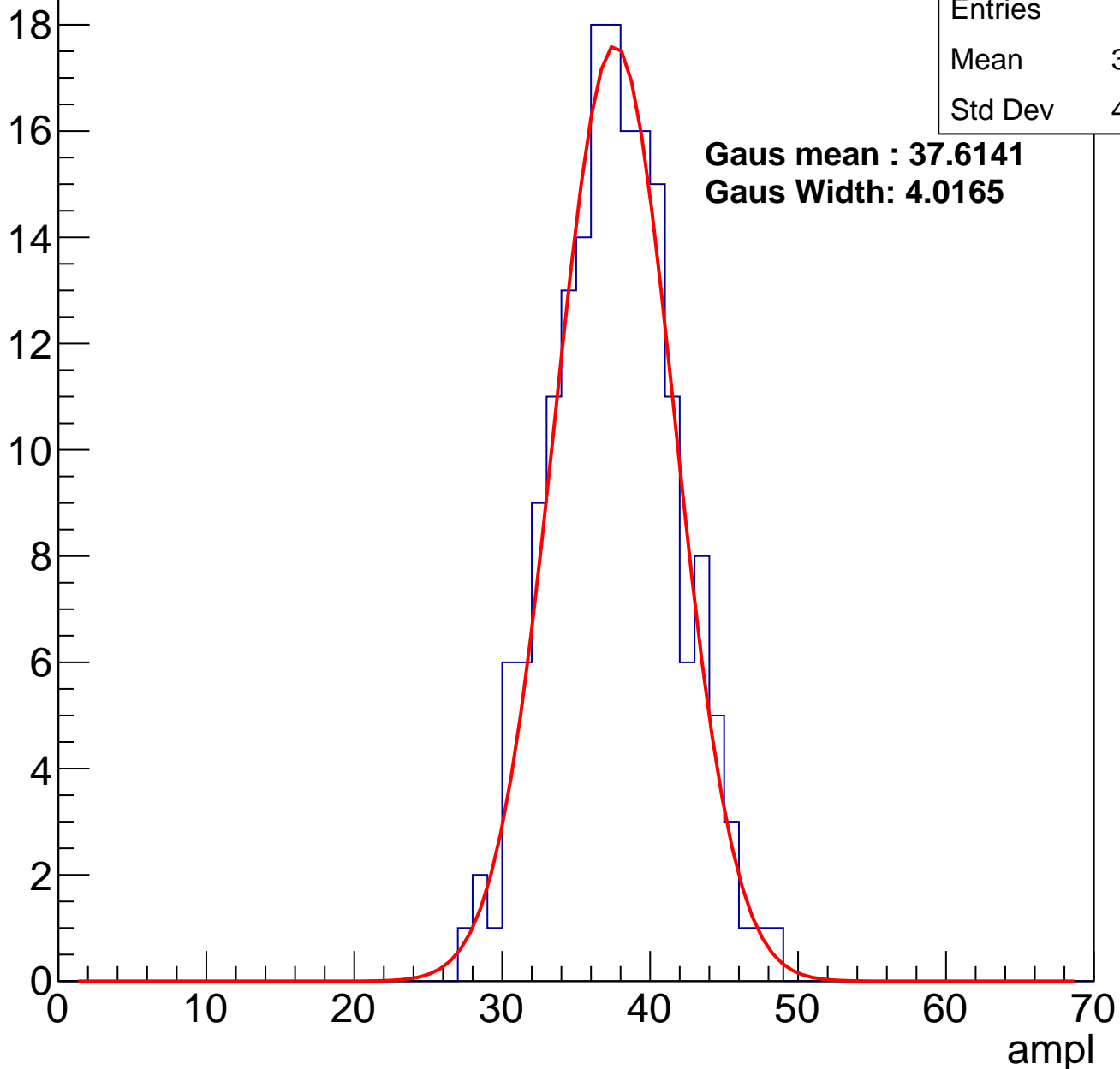
Entry



B0L101S, U25-ch77, adc1

calib_packv5_042523_0143.root, FC#1, port C1

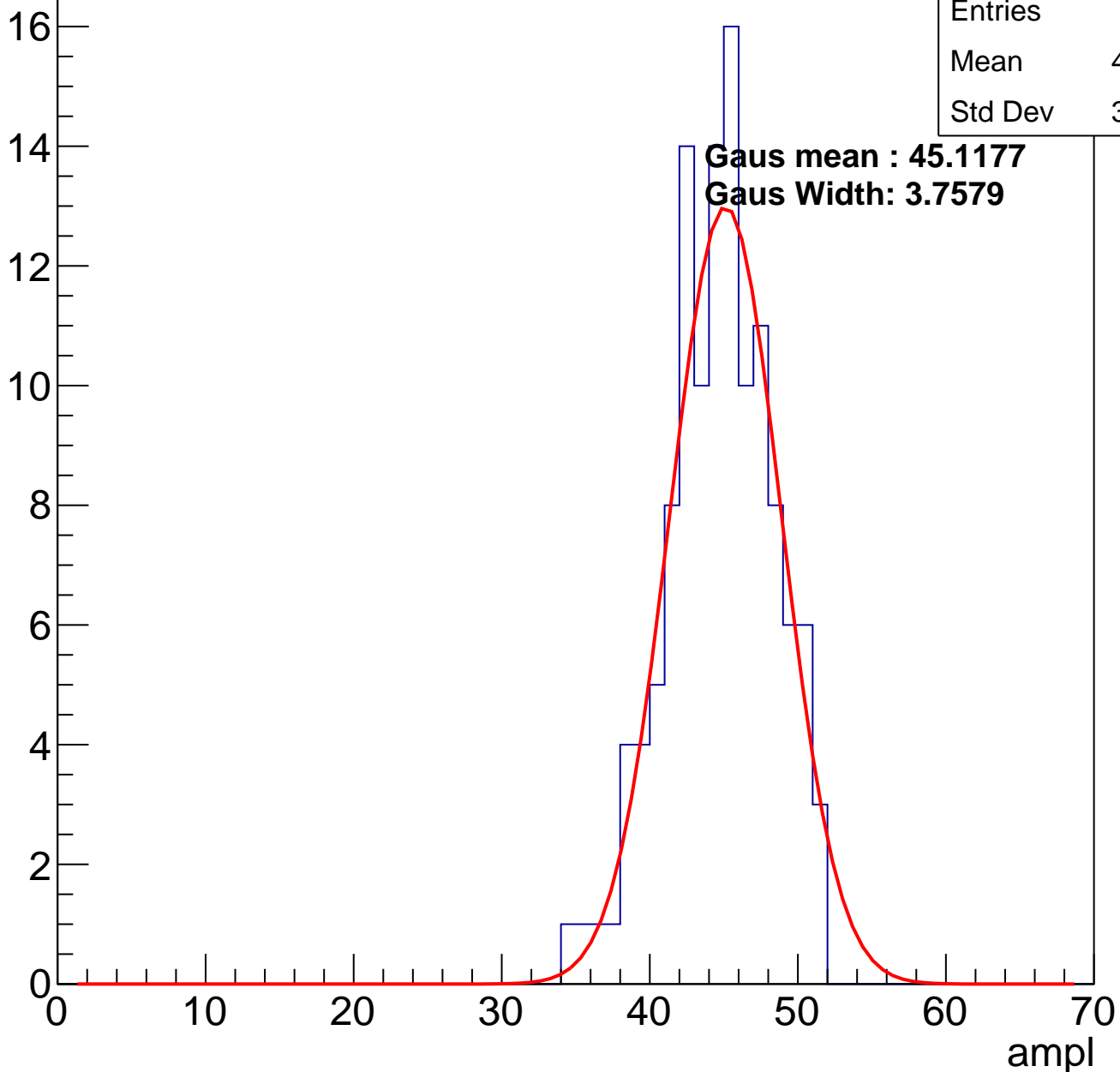
Entry



B0L101S, U25-ch77, adc2

calib_packv5_042523_0143.root, FC#1, port C1

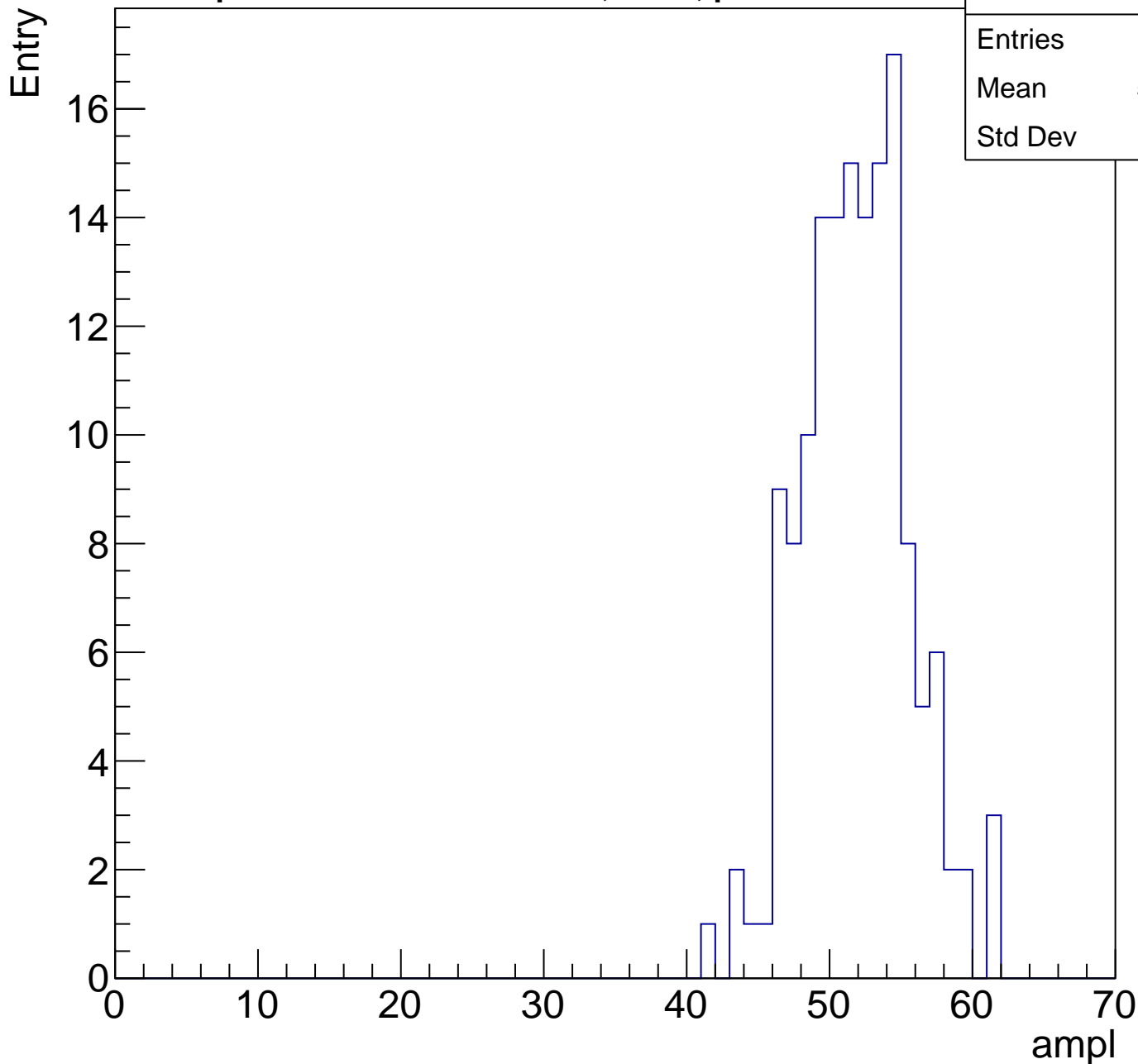
Entry



B0L101S, U25-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	51.41
Std Dev	3.71

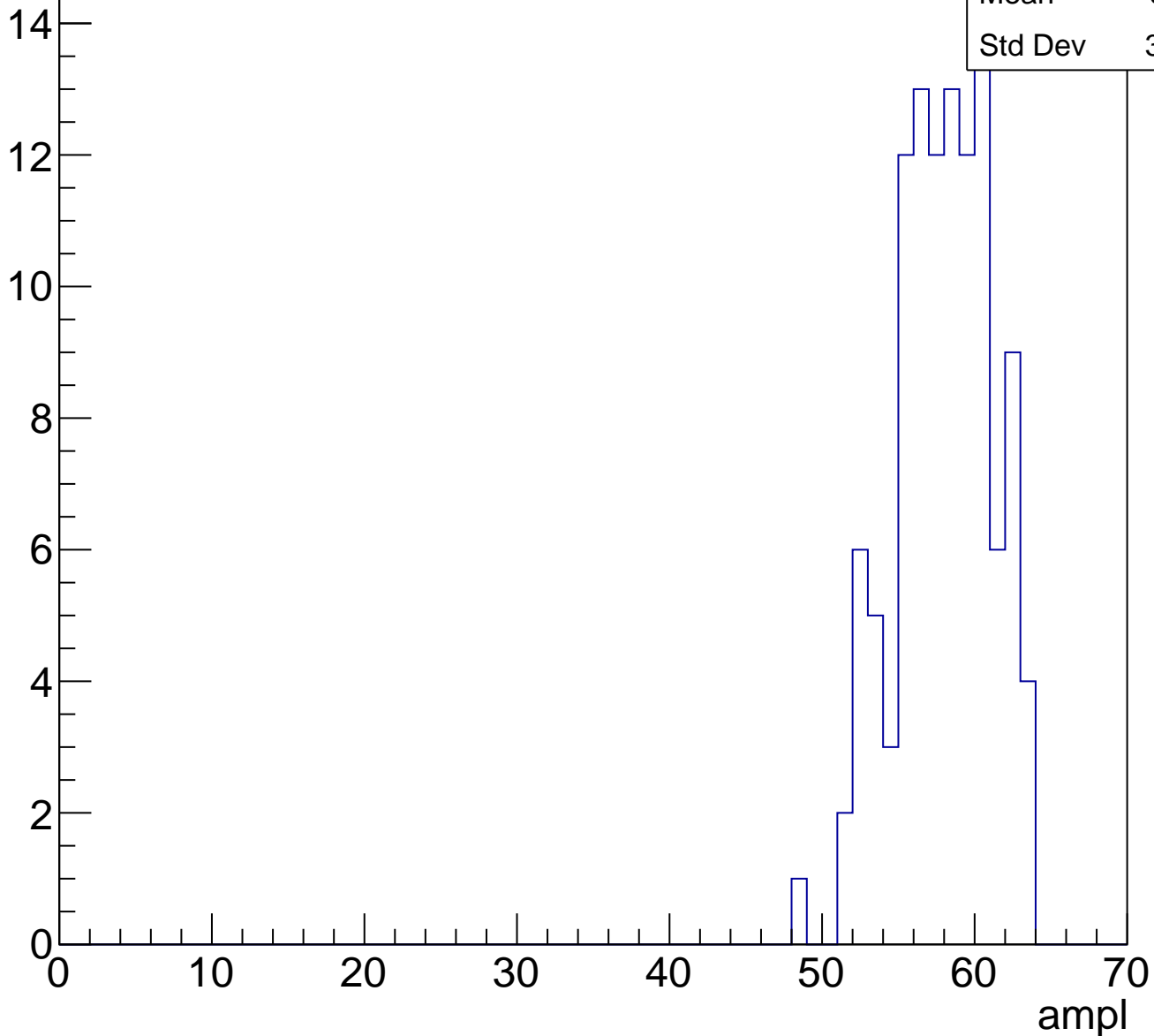


B0L101S, U25-ch77, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	57.51
Std Dev	3.123

Entry



B0L101S, U25-ch77, adc5

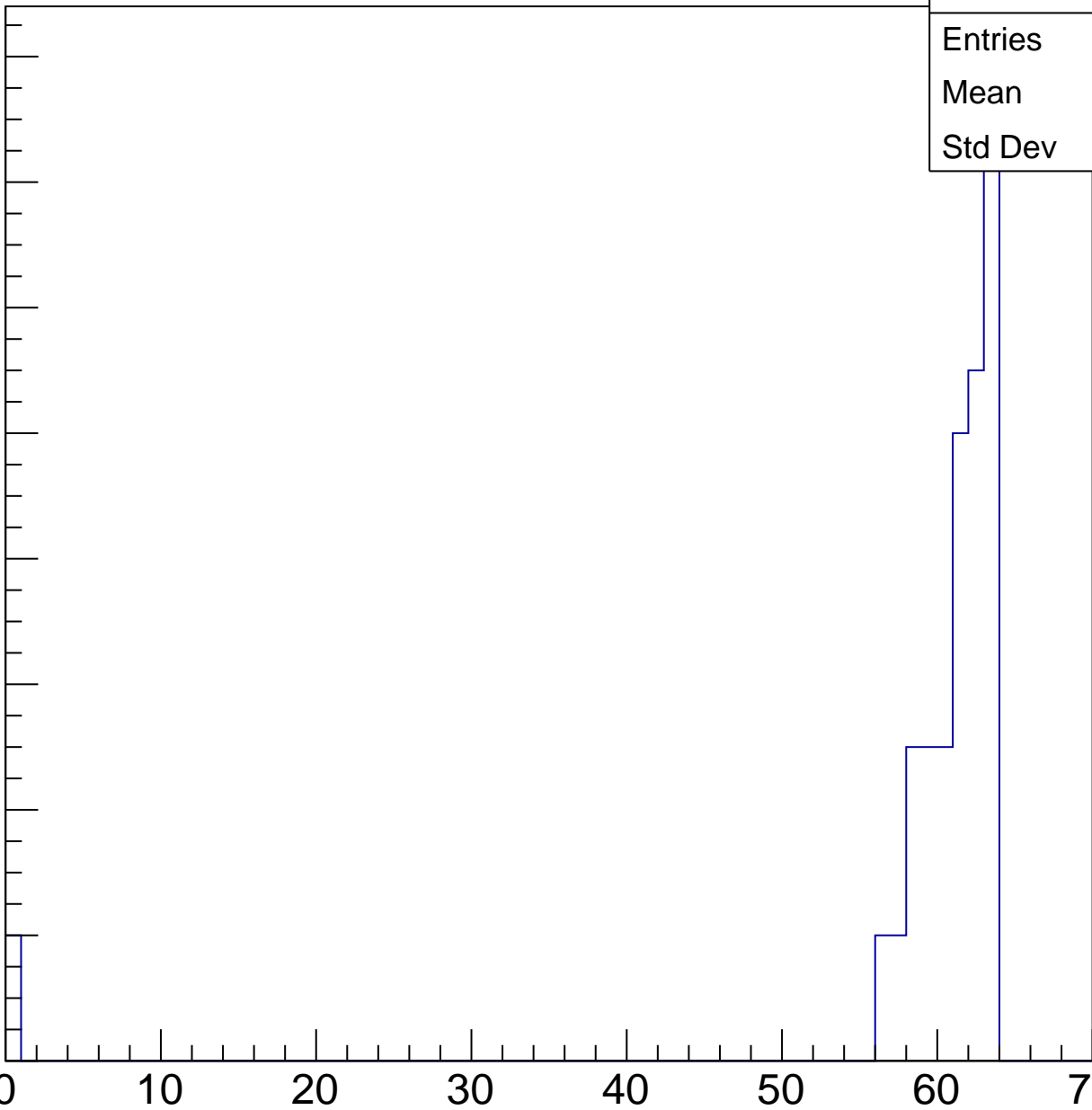
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	58
Mean	58.81
Std Dev	11.29

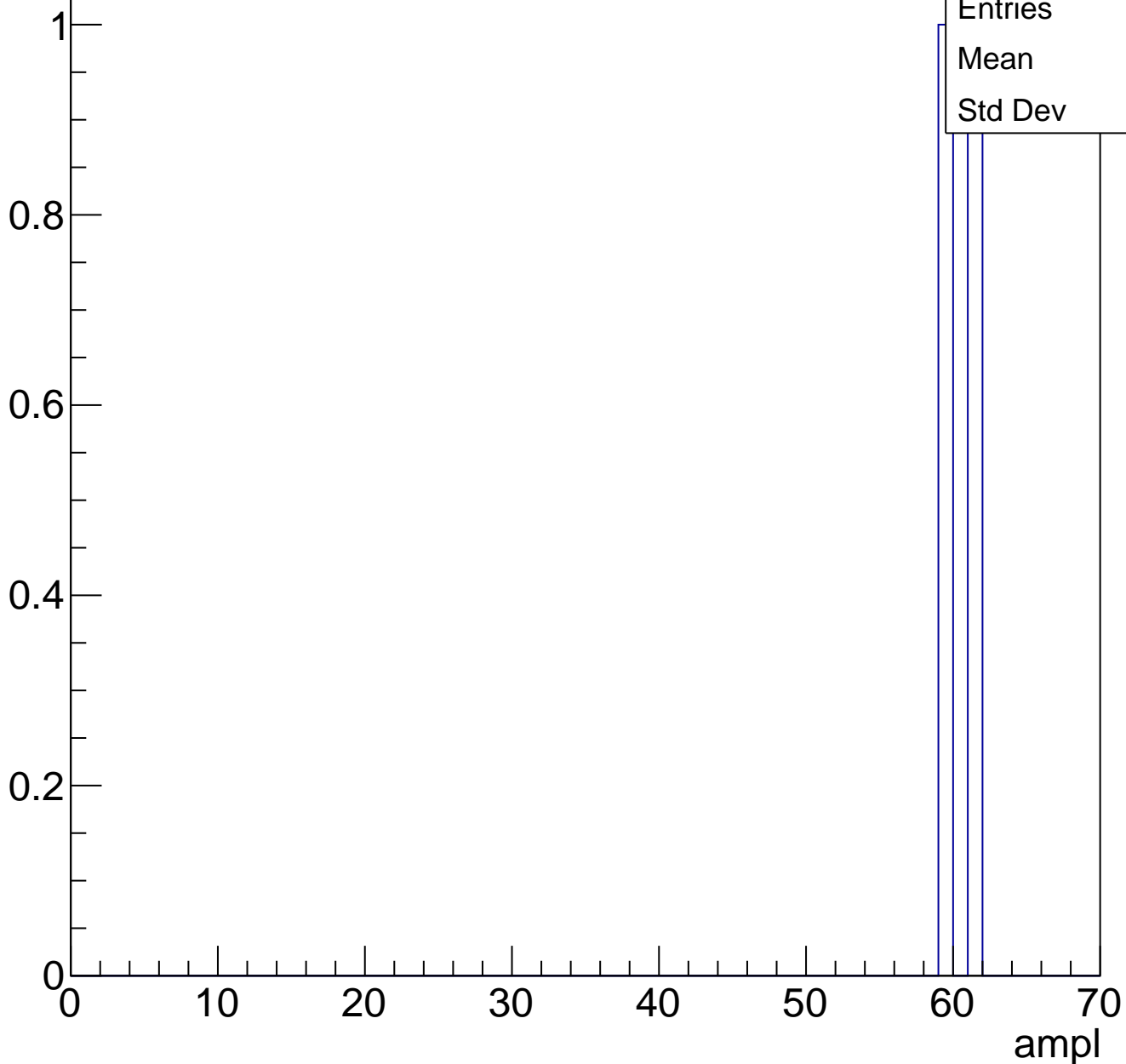
ampl



B0L101S, U25-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch78, adc0

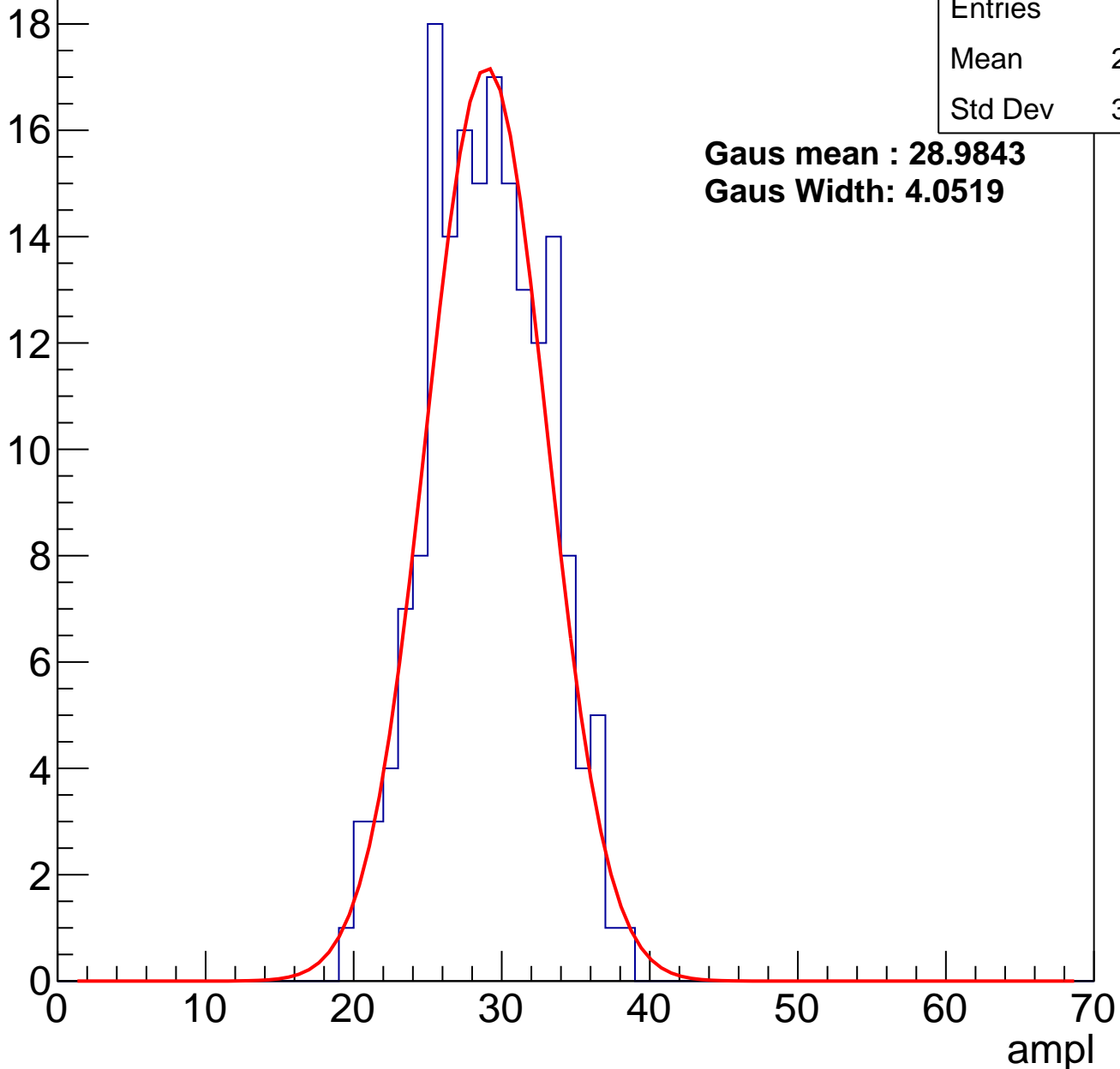
calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	28.54
Std Dev	3.934

Gaus mean : 28.9843

Gaus Width: 4.0519

Entry



B0L101S, U25-ch78, adc1

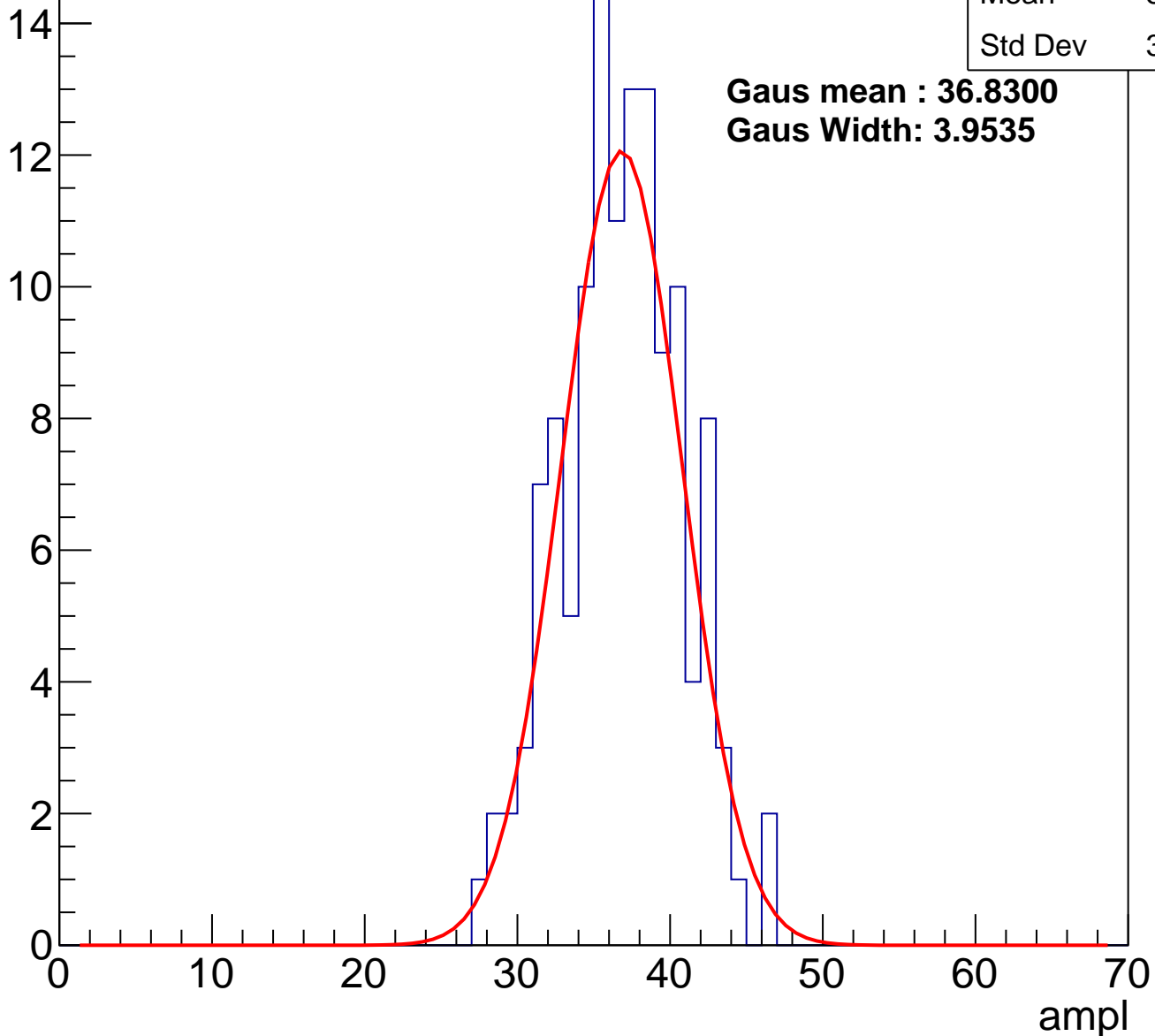
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	36.39
Std Dev	3.872

Gaus mean : 36.8300

Gaus Width: 3.9535

Entry



B0L101S, U25-ch78, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	130
Mean	42.38
Std Dev	3.777

Gaus mean : 42.9043

Gaus Width: 3.7640

0

0

10

20

30

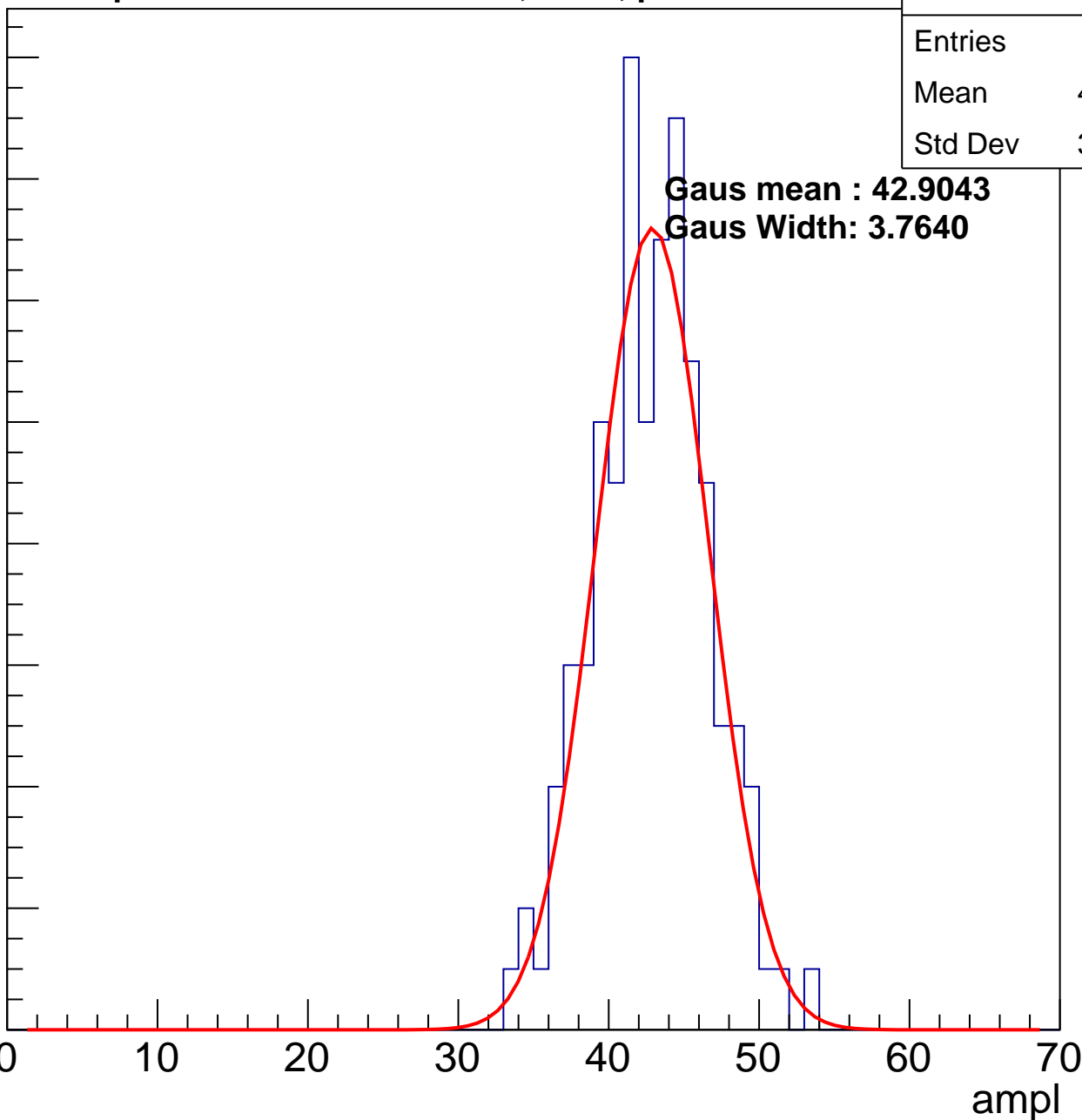
40

50

60

70

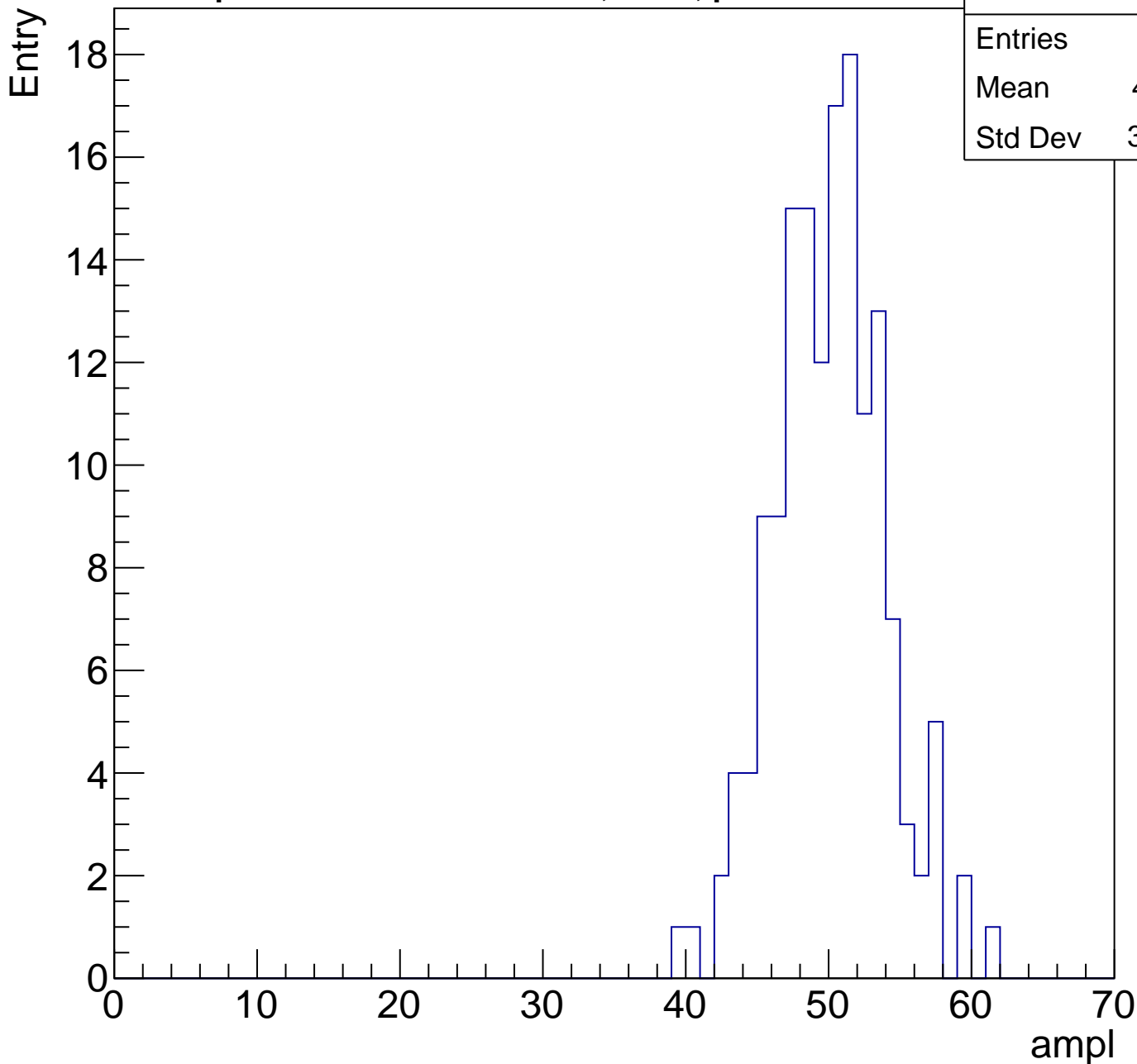
ampl



B0L101S, U25-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	49.61
Std Dev	3.824



B0L101S, U25-ch78, adc4

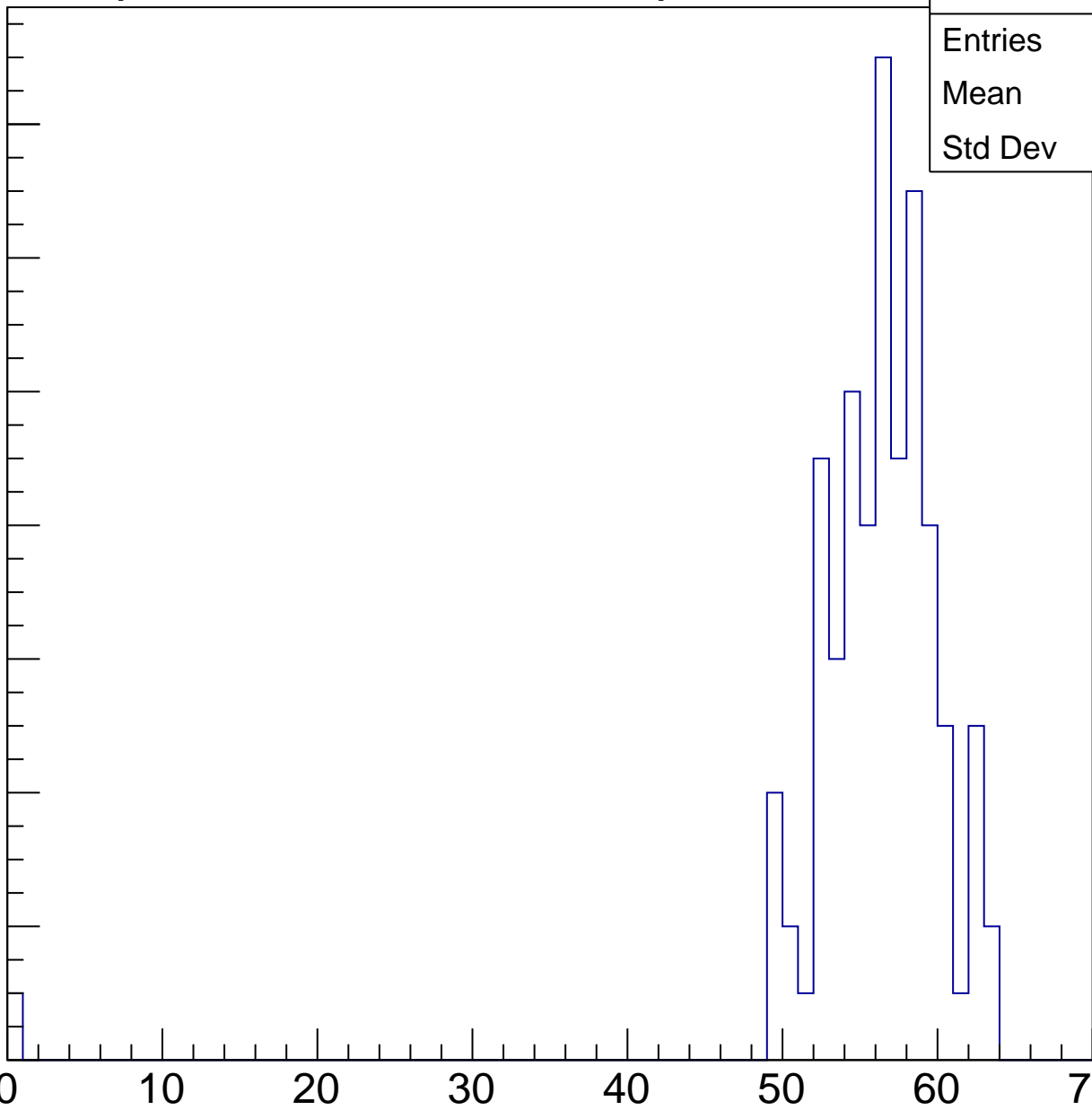
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	55.44
Std Dev	6.483

ampl



B0L101S, U25-ch78, adc5

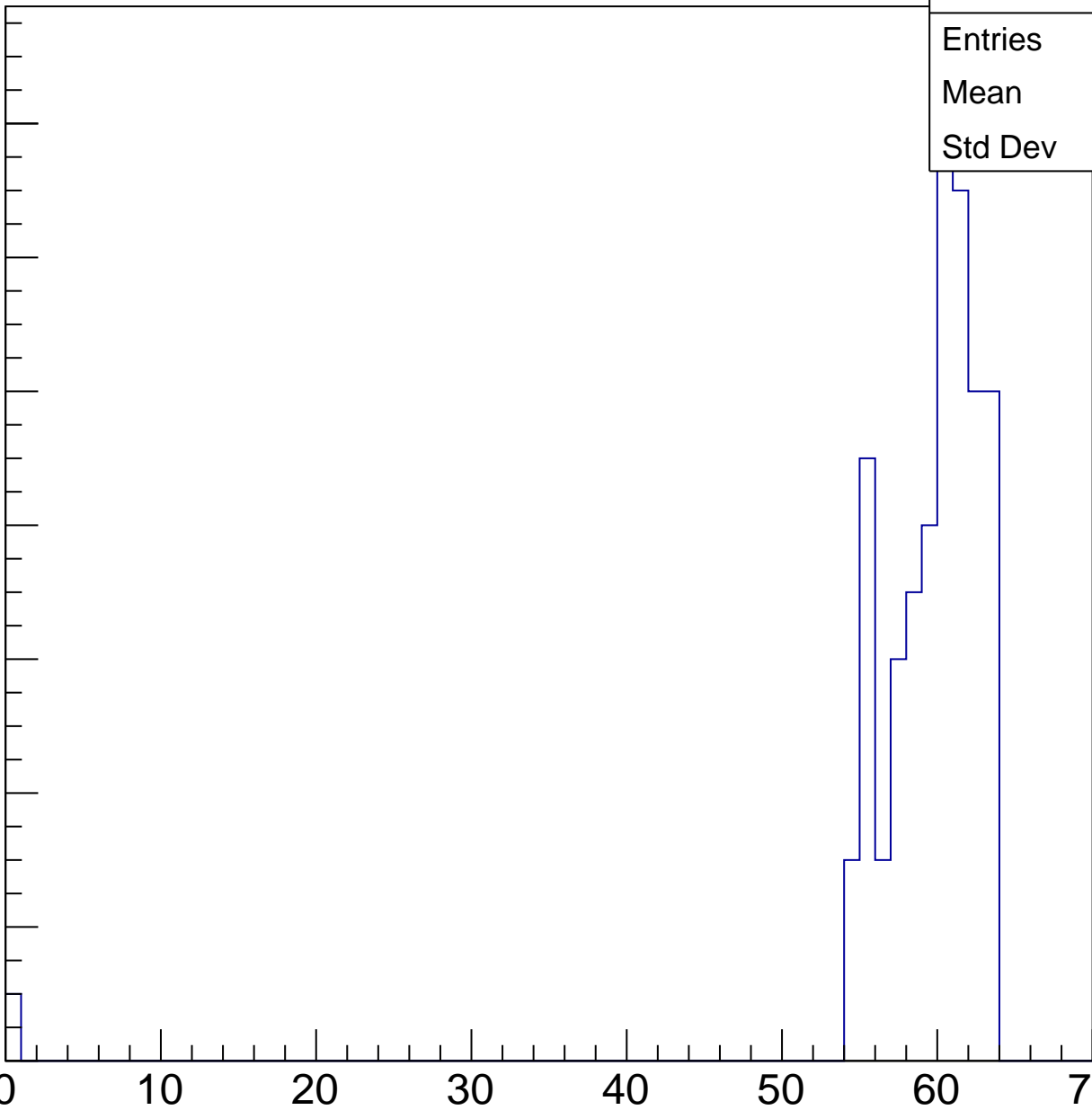
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	58.68
Std Dev	6.917

ampl

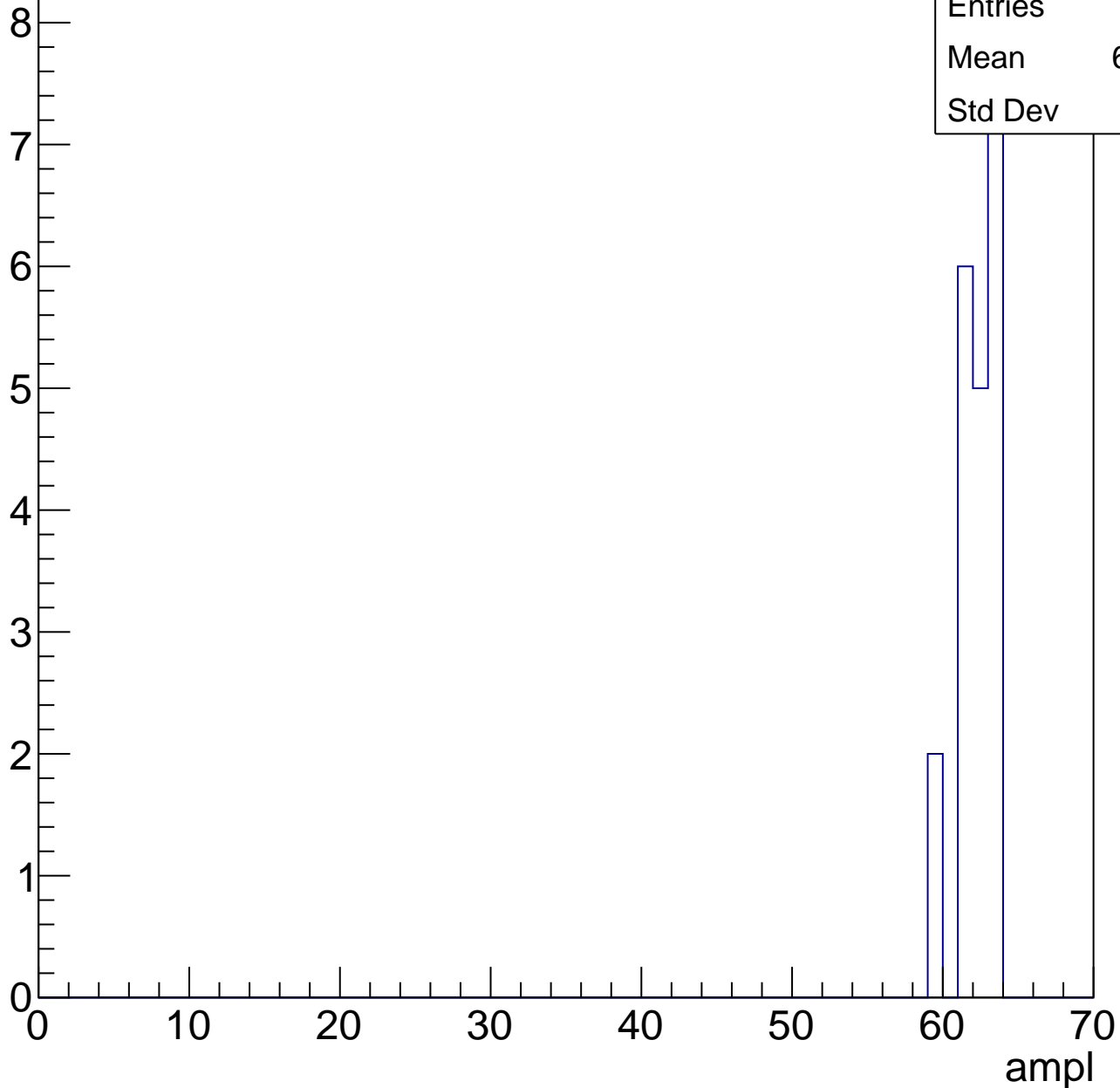


B0L101S, U25-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	61.81
Std Dev	1.22



B0L101S, U25-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

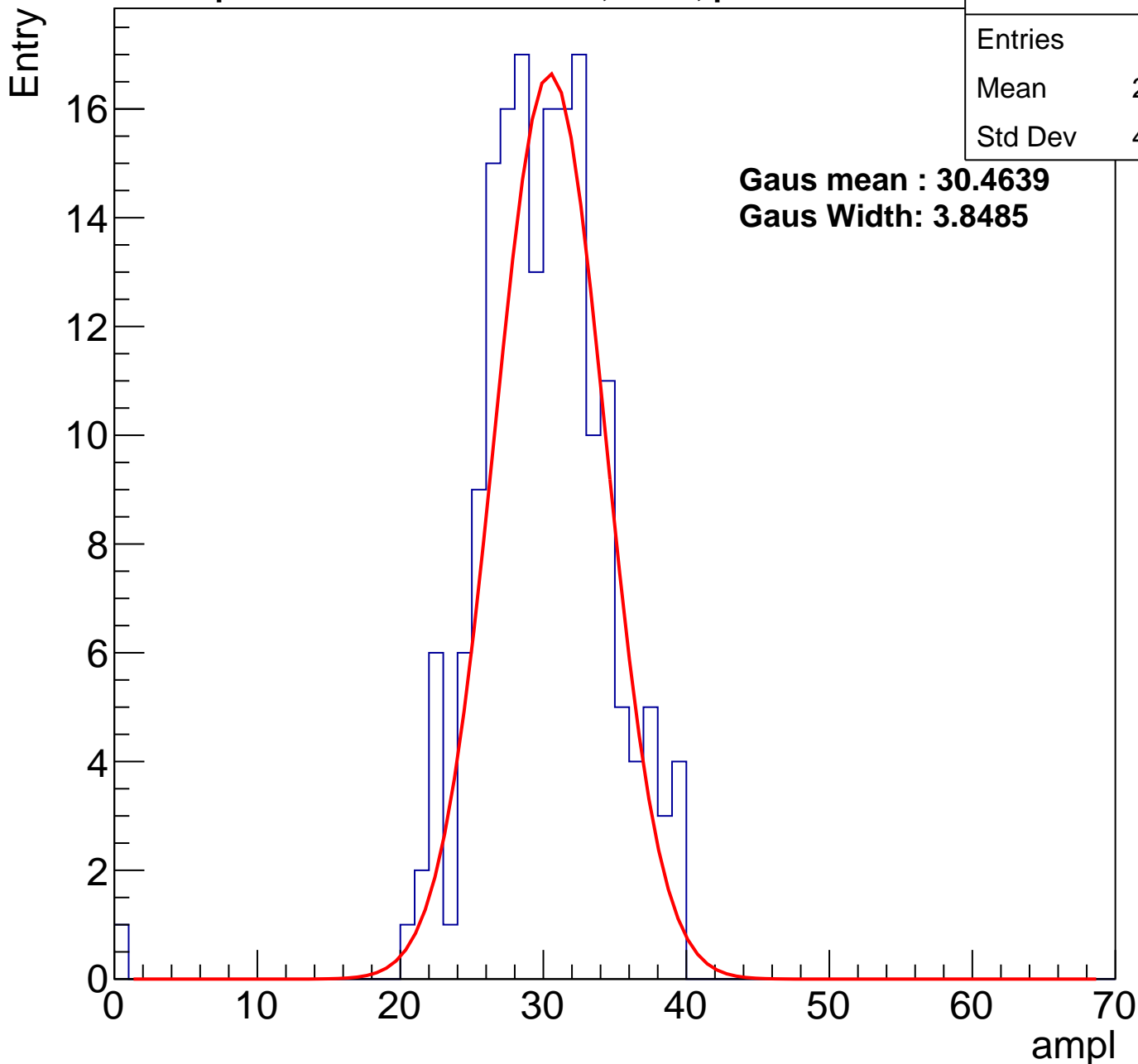
B0L101S, U25-ch79, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	29.54
Std Dev	4.647

Gaus mean : 30.4639

Gaus Width: 3.8485



B0L101S, U25-ch79, adc1

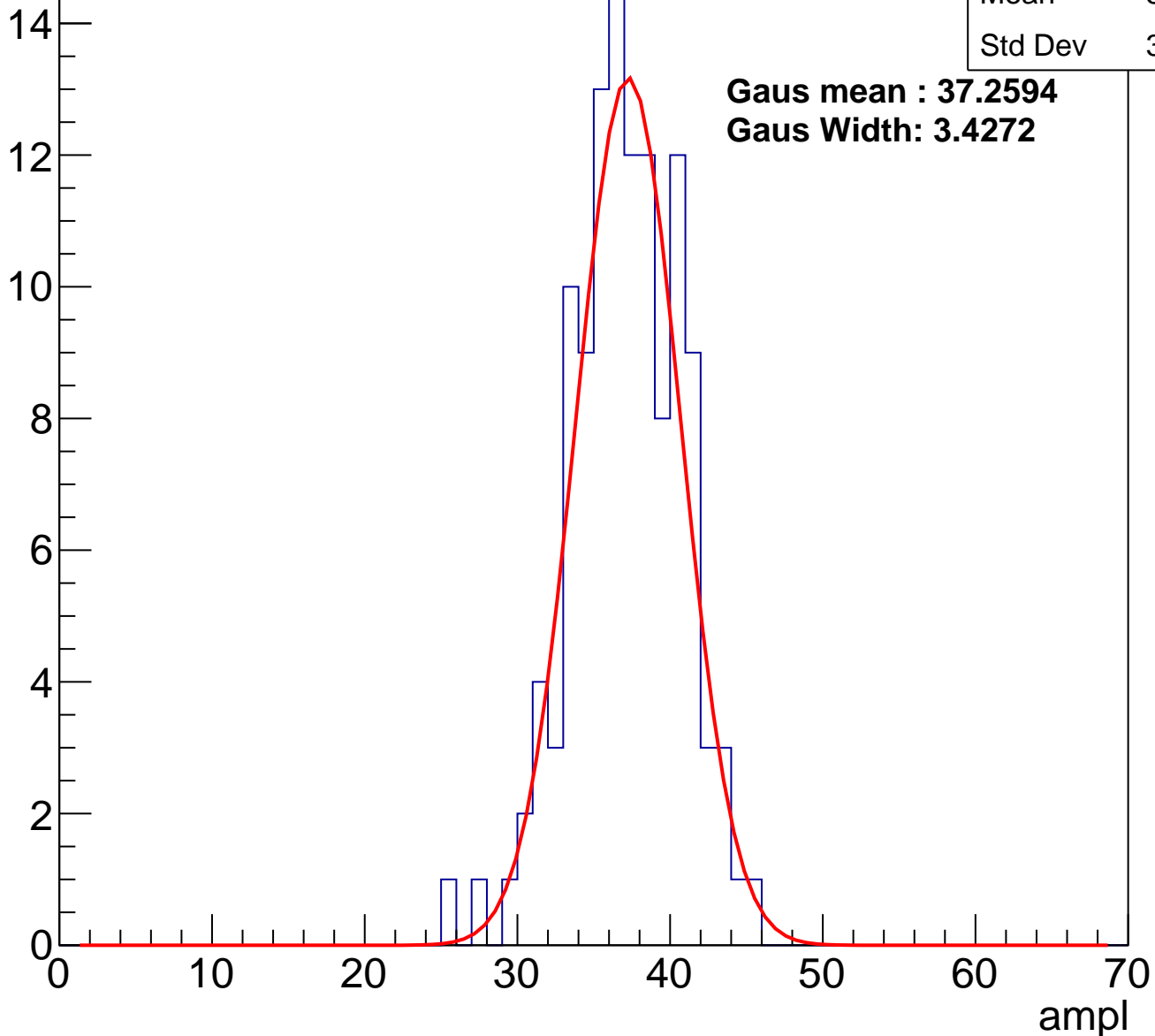
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	36.64
Std Dev	3.547

Gaus mean : 37.2594

Gaus Width: 3.4272

Entry



B0L101S, U25-ch79, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

145

Mean

43.79

Std Dev

3.622

Gaus mean : 44.2614

Gaus Width: 3.7251

0

10

20

30

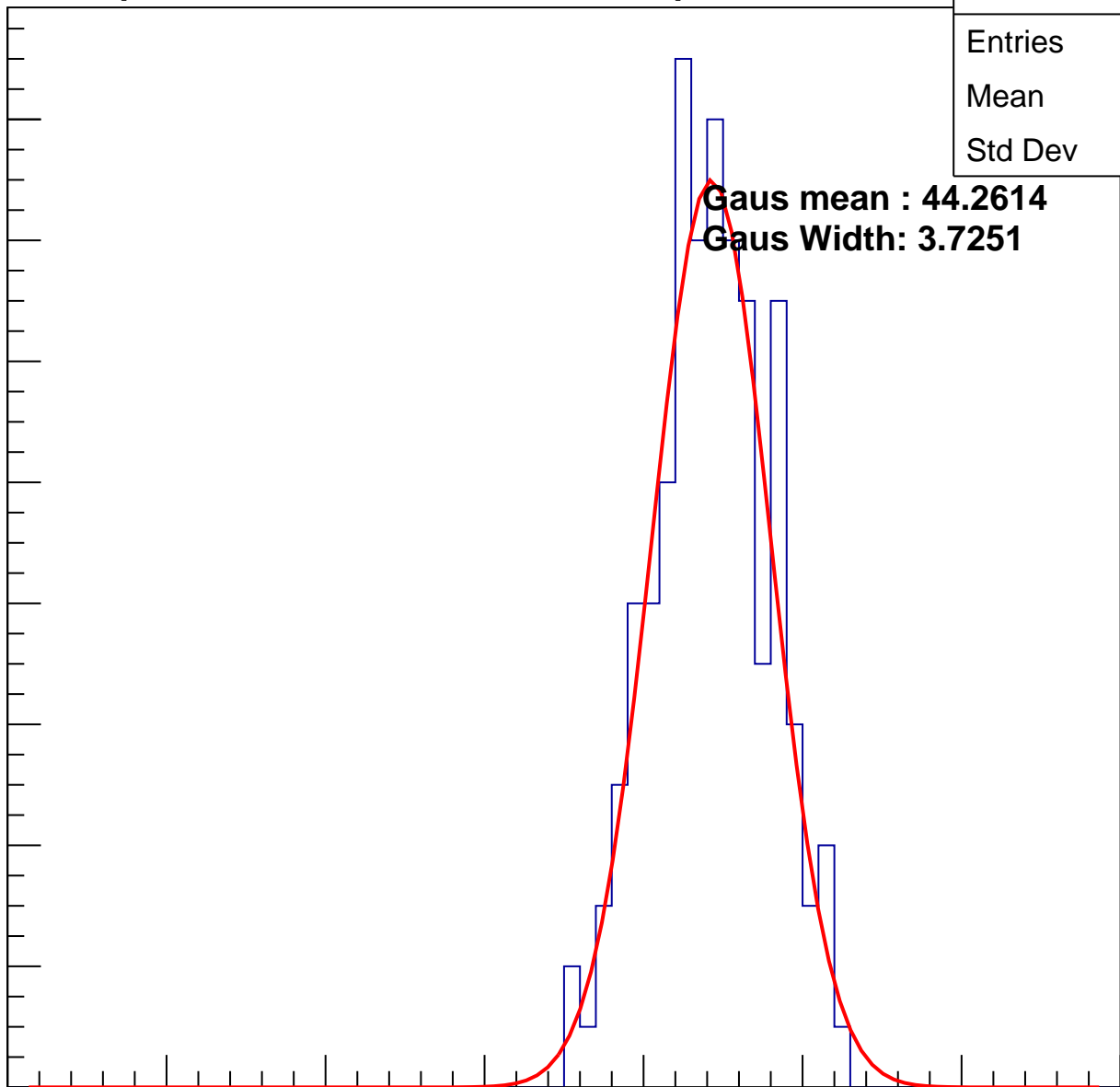
40

50

60

70

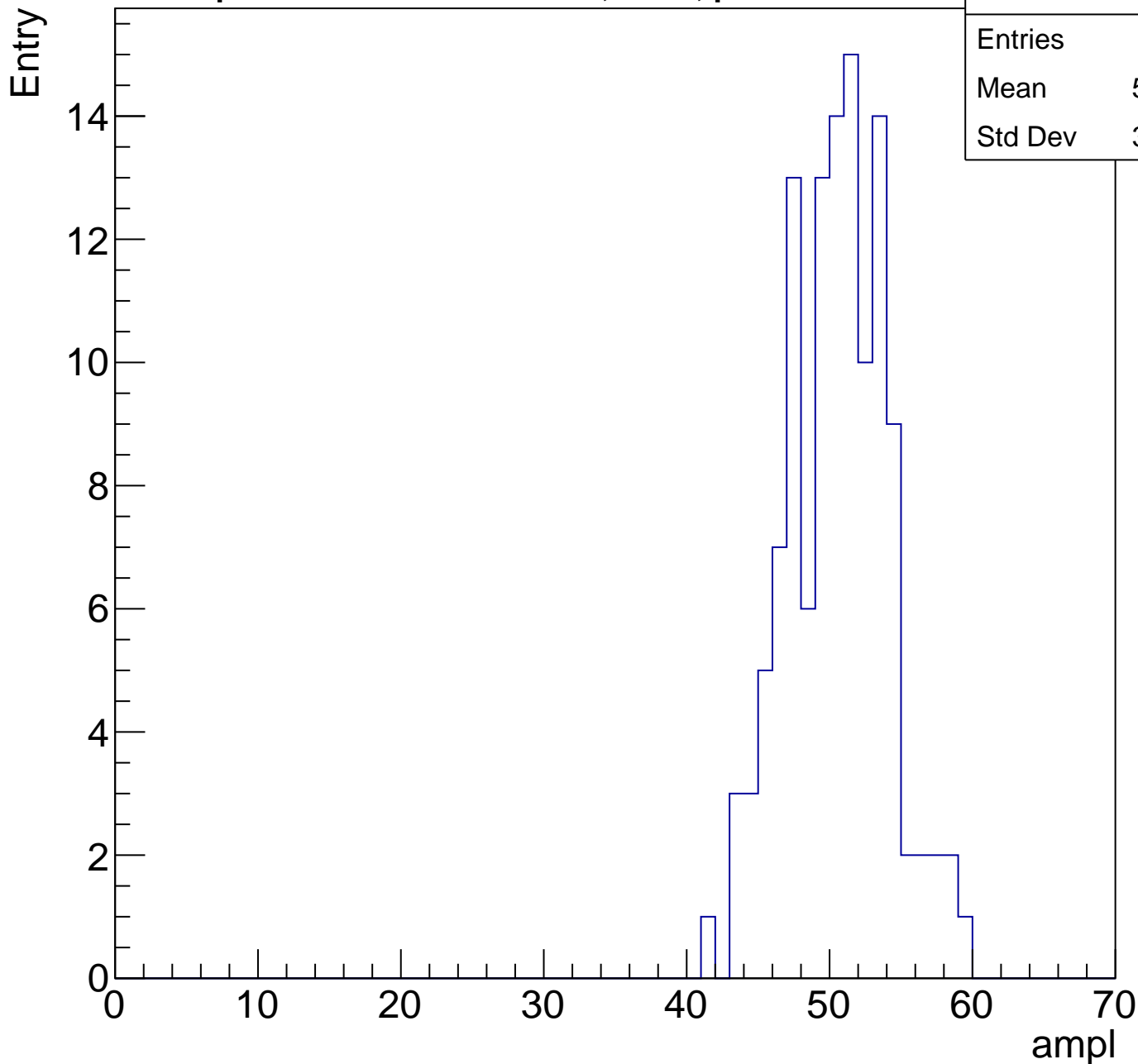
ampl



B0L101S, U25-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

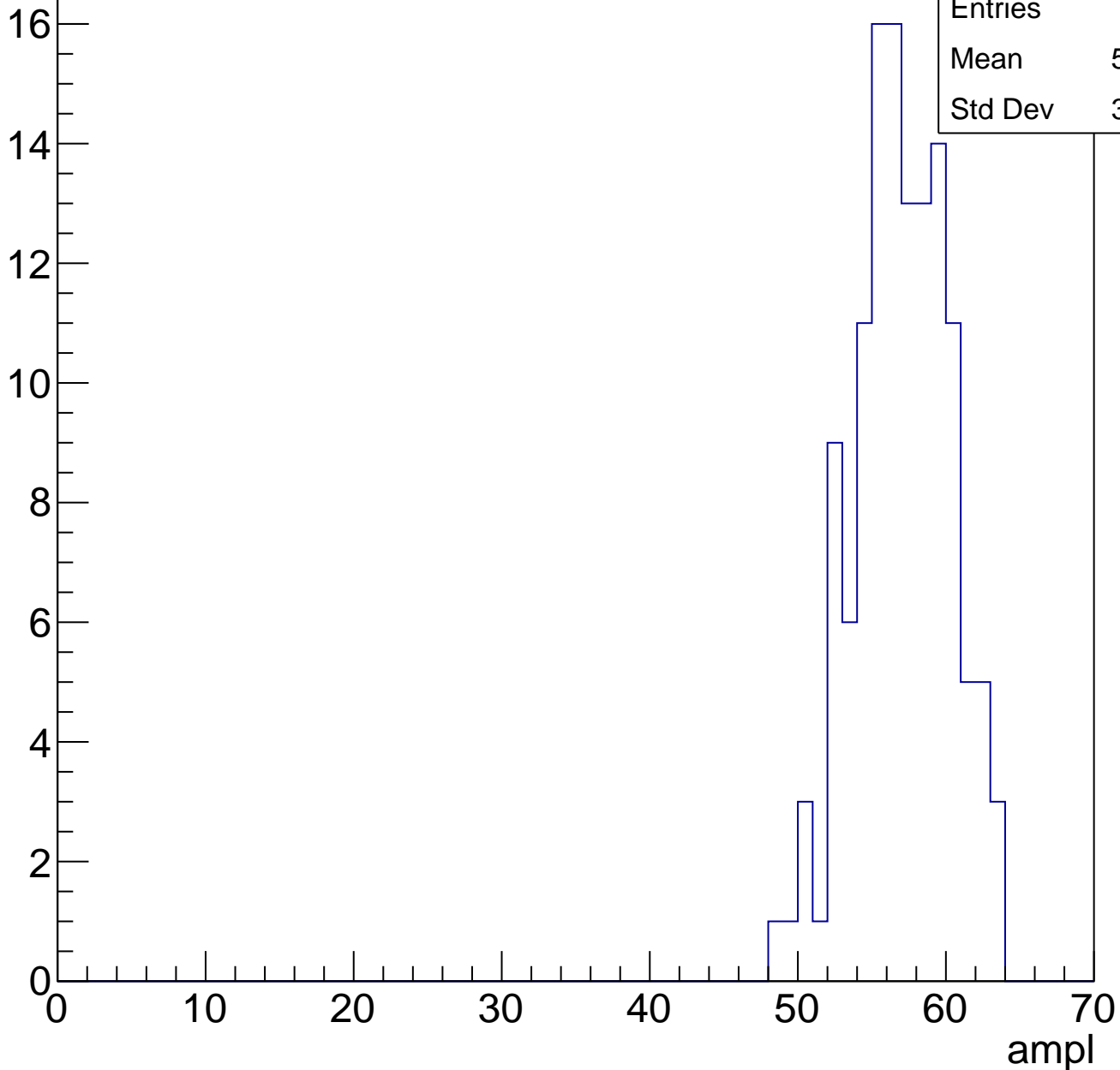
Entries	122
Mean	50.07
Std Dev	3.483



B0L101S, U25-ch79, adc4

calib_packv5_042523_0143.root, FC#1, port C1

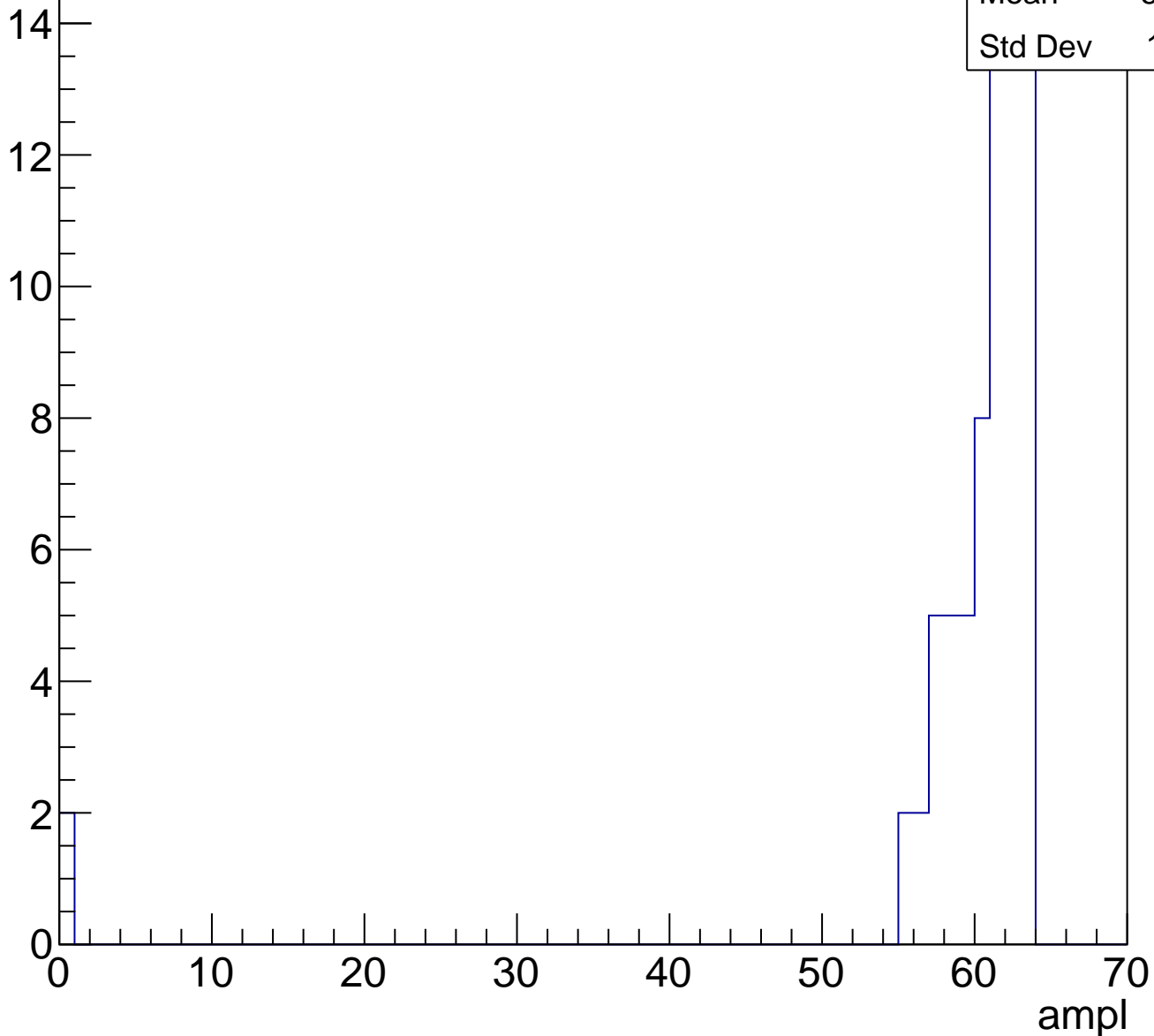
Entry



B0L101S, U25-ch79, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch80, adc0

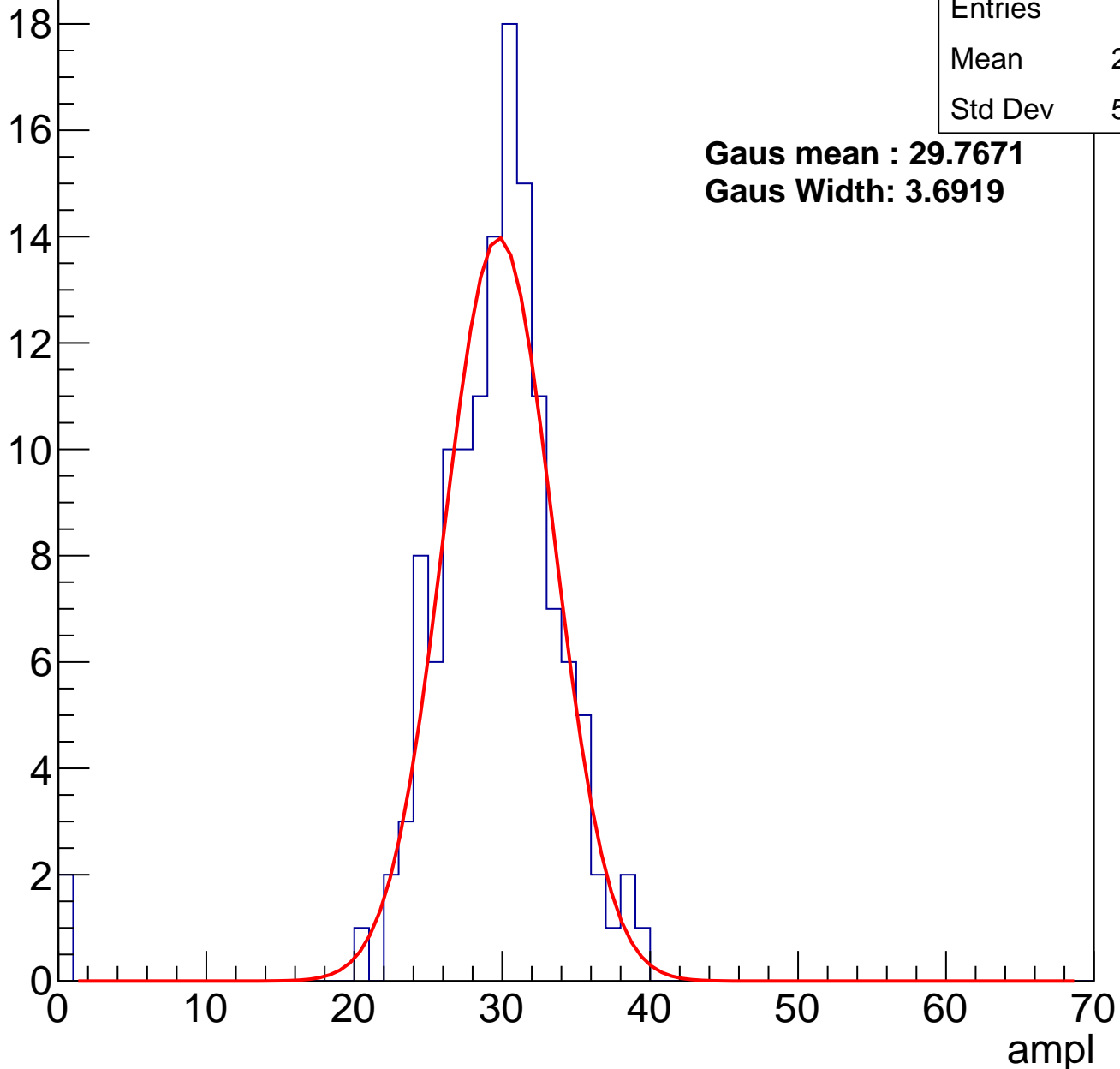
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	28.96
Std Dev	5.054

Gaus mean : 29.7671

Gaus Width: 3.6919

Entry



B0L101S, U25-ch80, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	36.24
Std Dev	3.687

Gaus mean : 36.4974

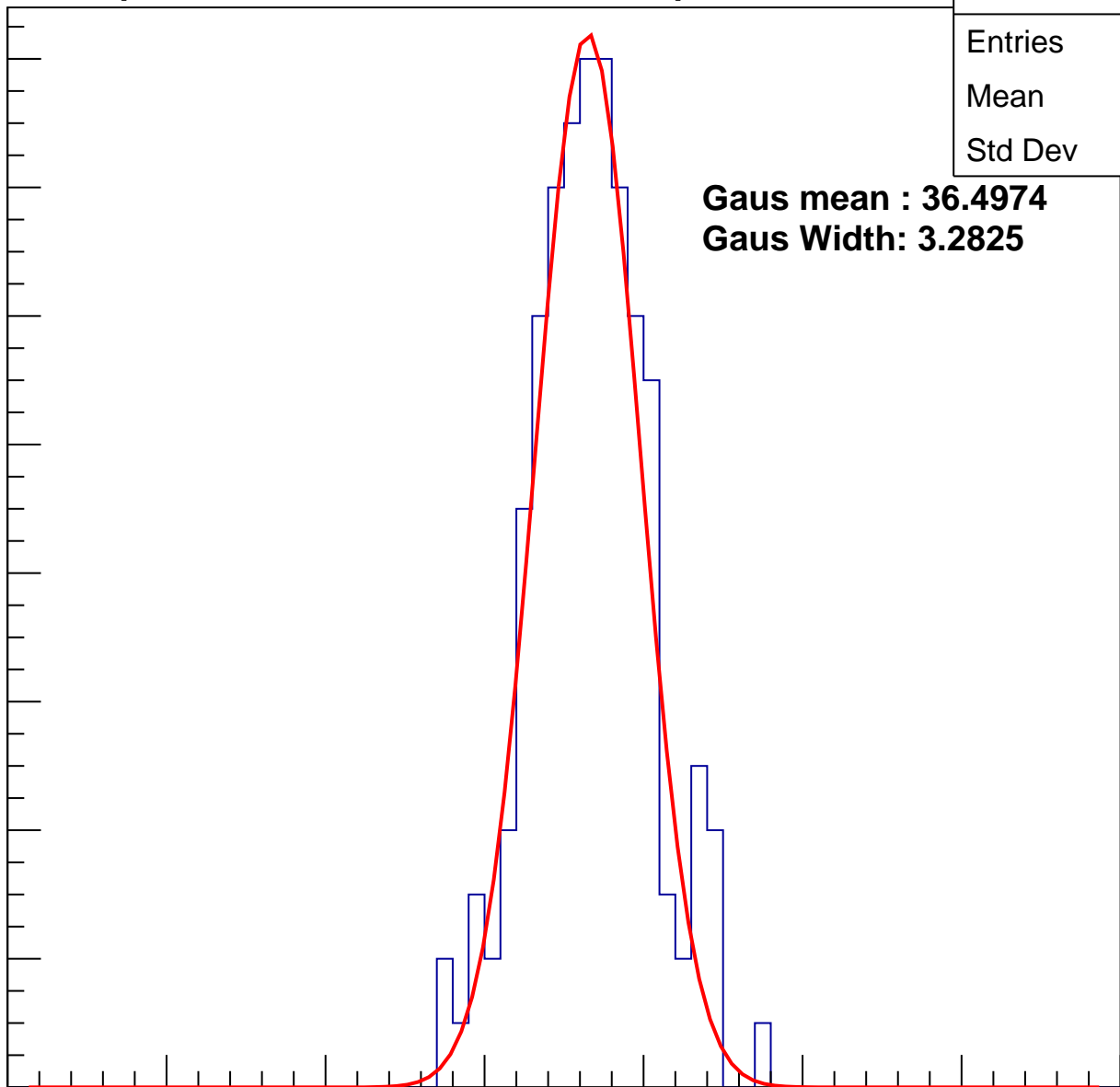
Gaus Width: 3.2825

Entry

16
14
12
10
8
6
4
2
0

ampl

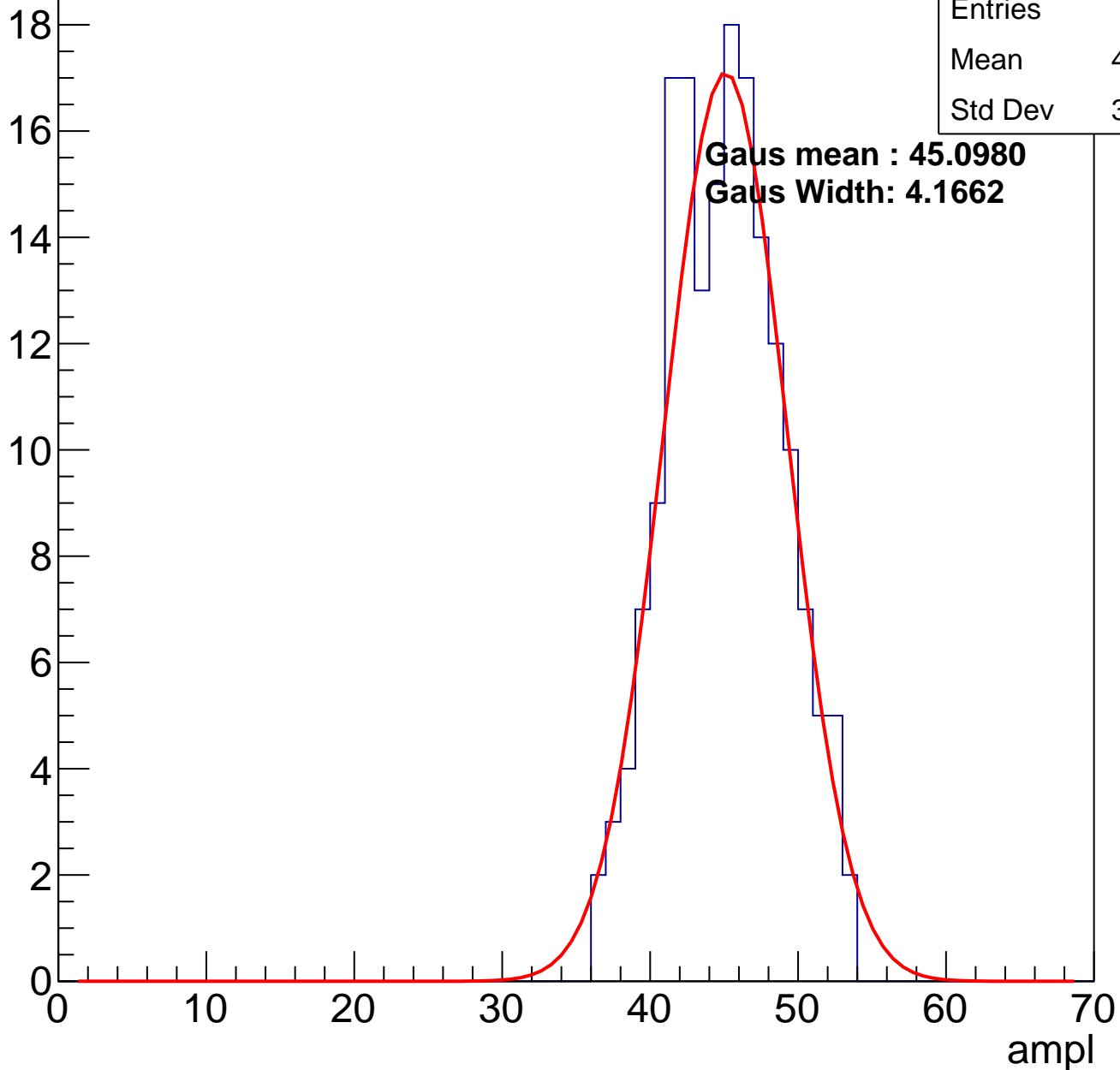
0 10 20 30 40 50 60 70



B0L101S, U25-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

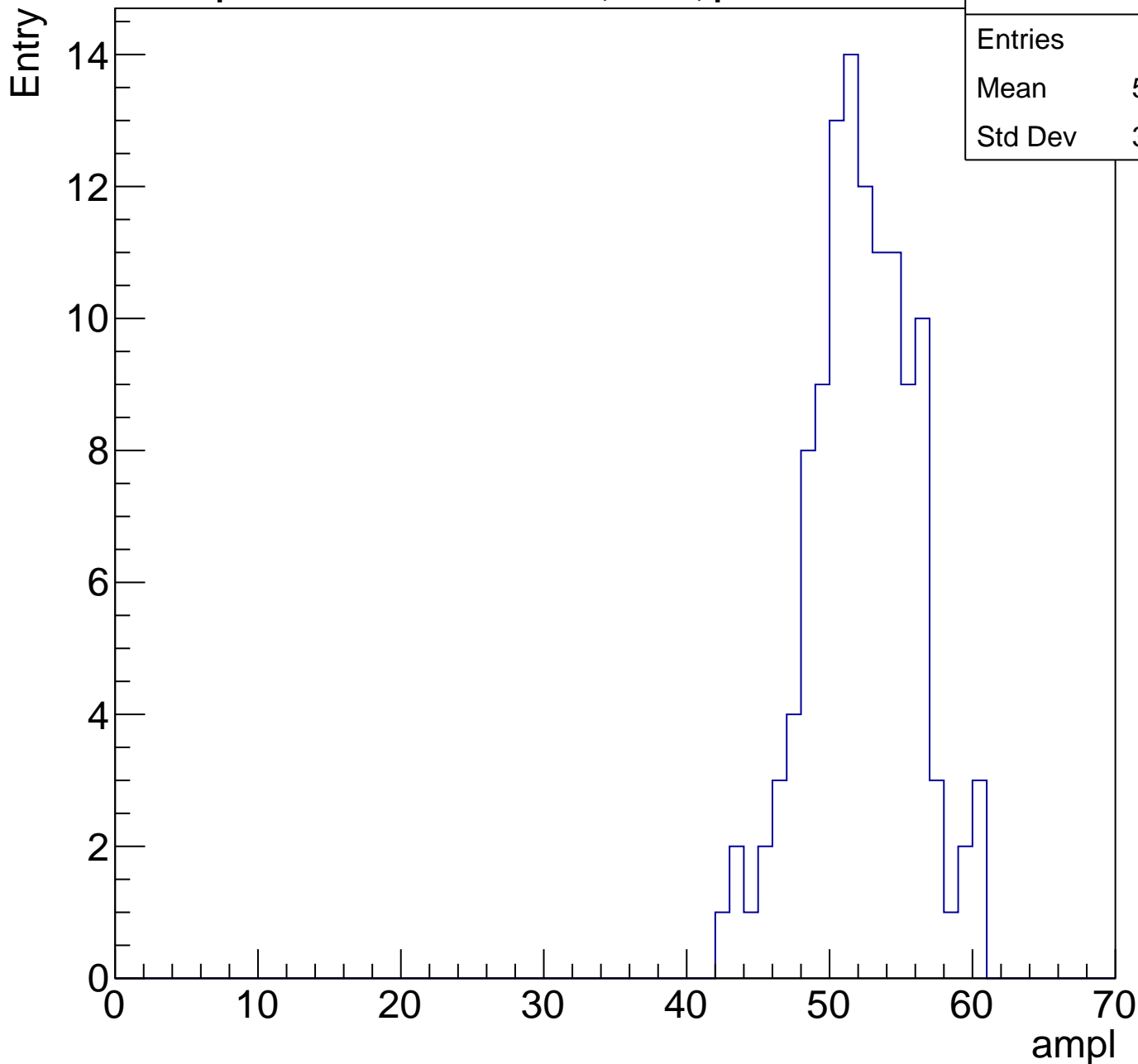
Entry



B0L101S, U25-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	51.76
Std Dev	3.664



B0L101S, U25-ch80, adc4

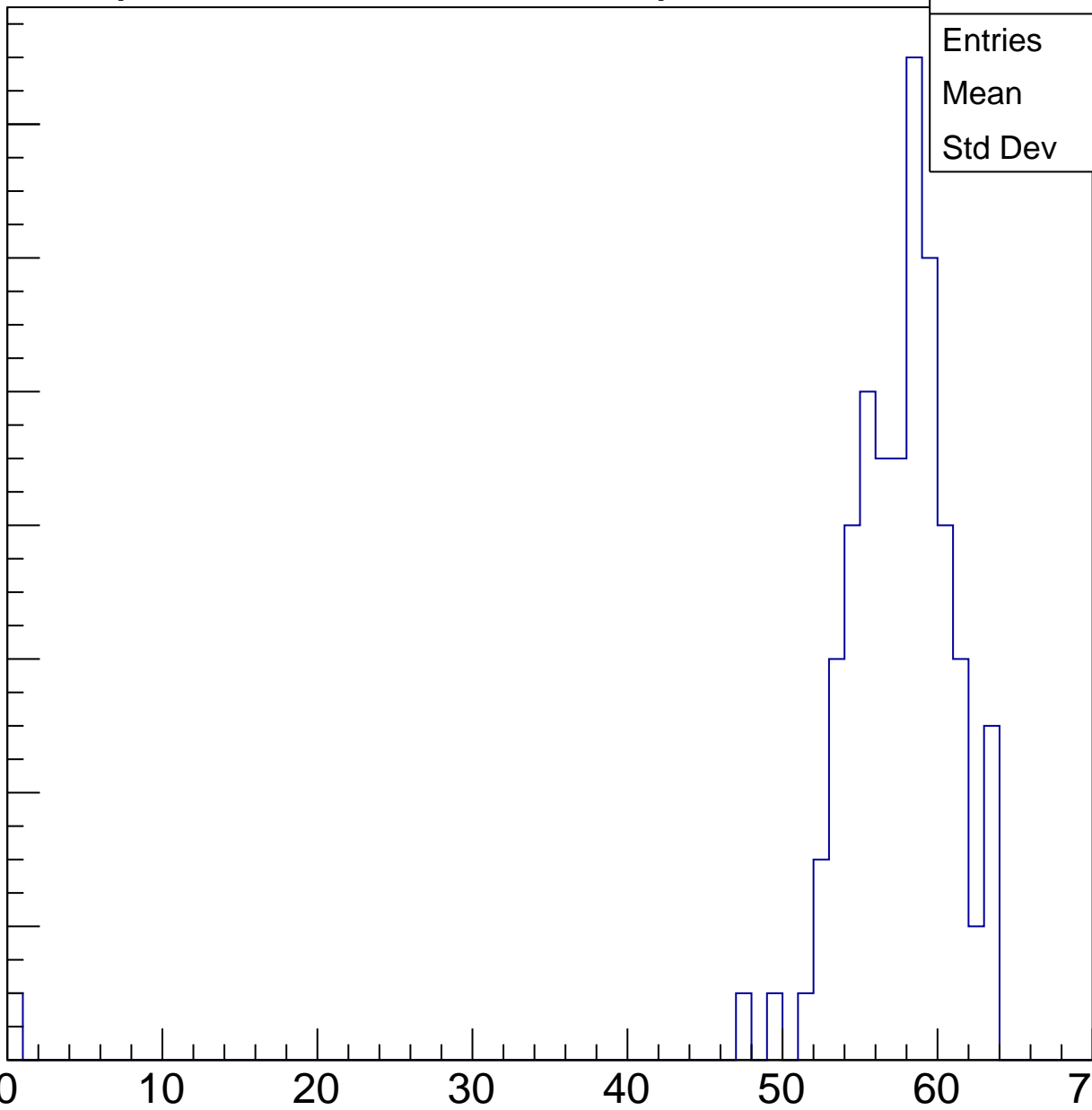
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	56.53
Std Dev	6.565

ampl



B0L101S, U25-ch80, adc5

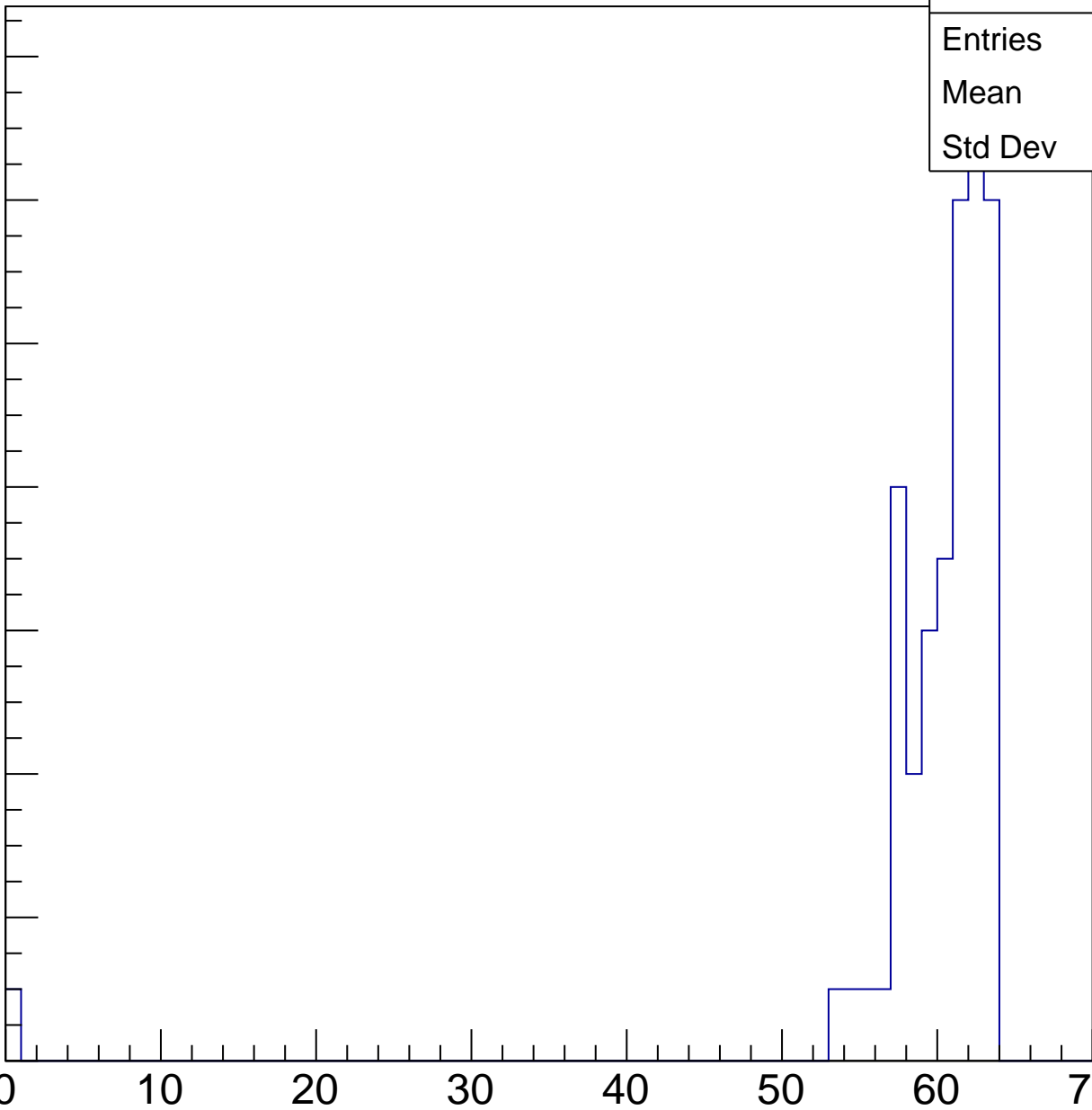
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	59.35
Std Dev	7.638

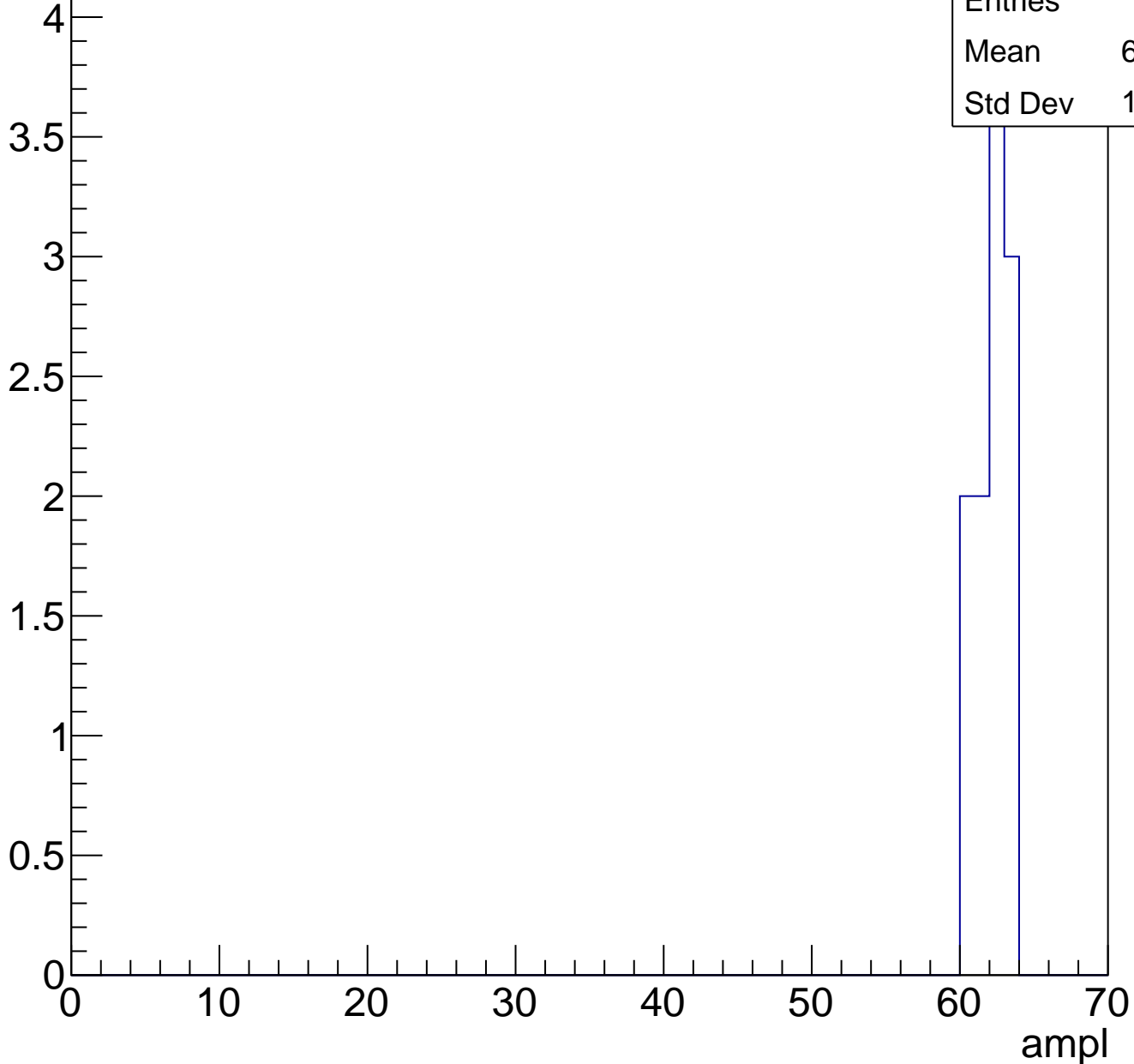
ampl



B0L101S, U25-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

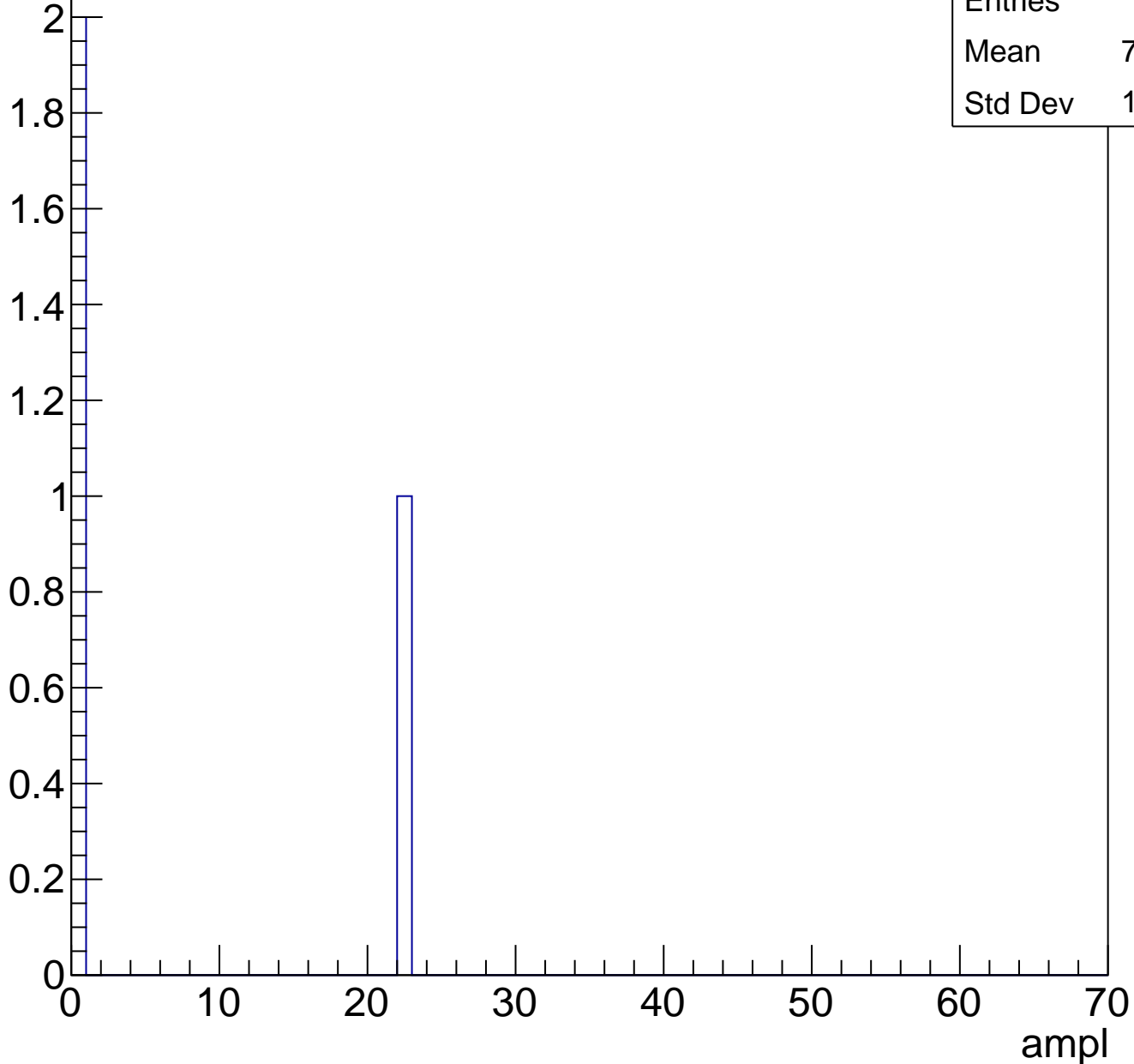
Entry



B0L101S, U25-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L101S, U25-ch81, adc0

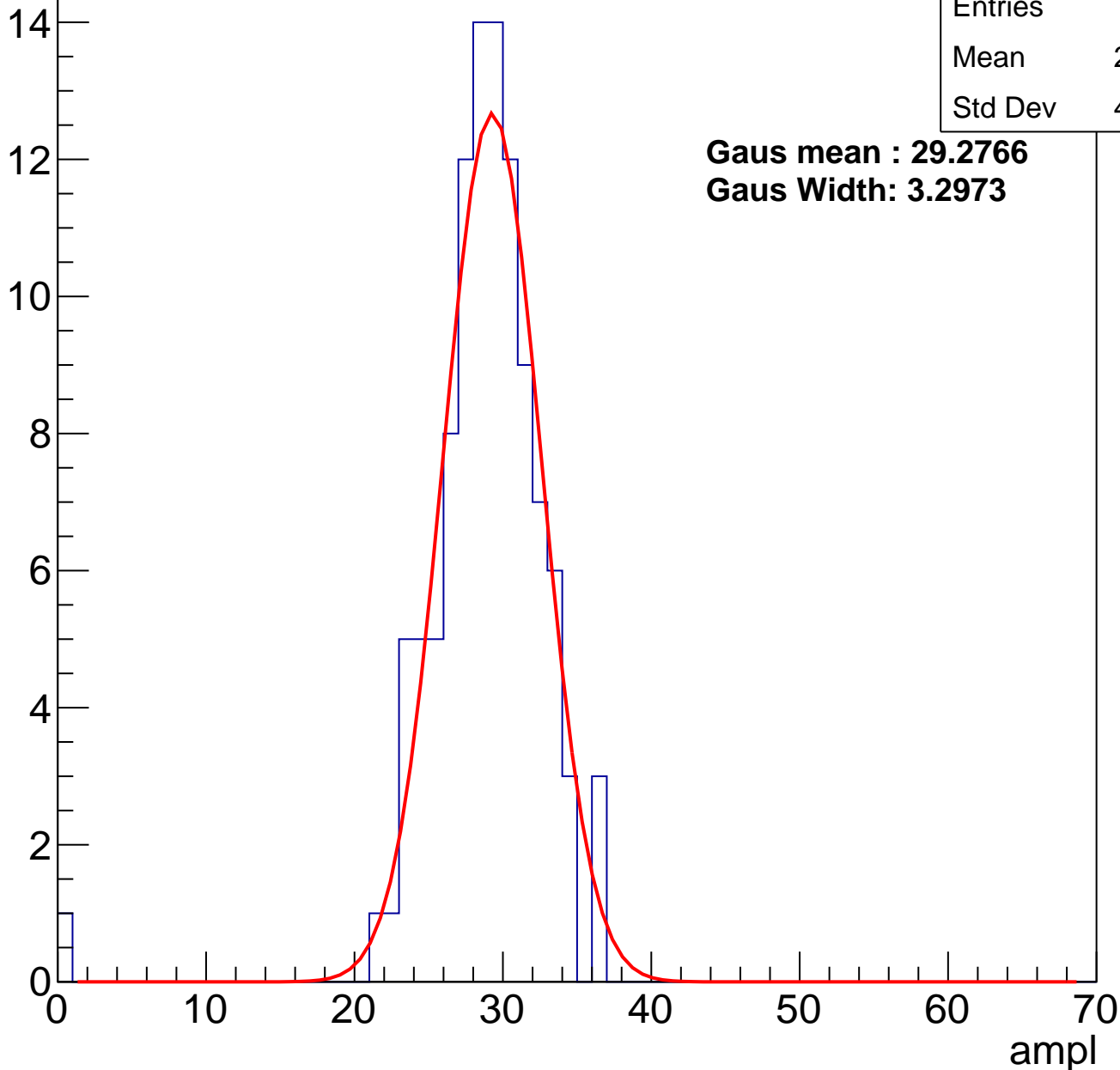
calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	28.34
Std Dev	4.184

Gaus mean : 29.2766

Gaus Width: 3.2973

Entry



B0L101S, U25-ch81, adc1

calib_packv5_042523_0143.root, FC#1, port C1

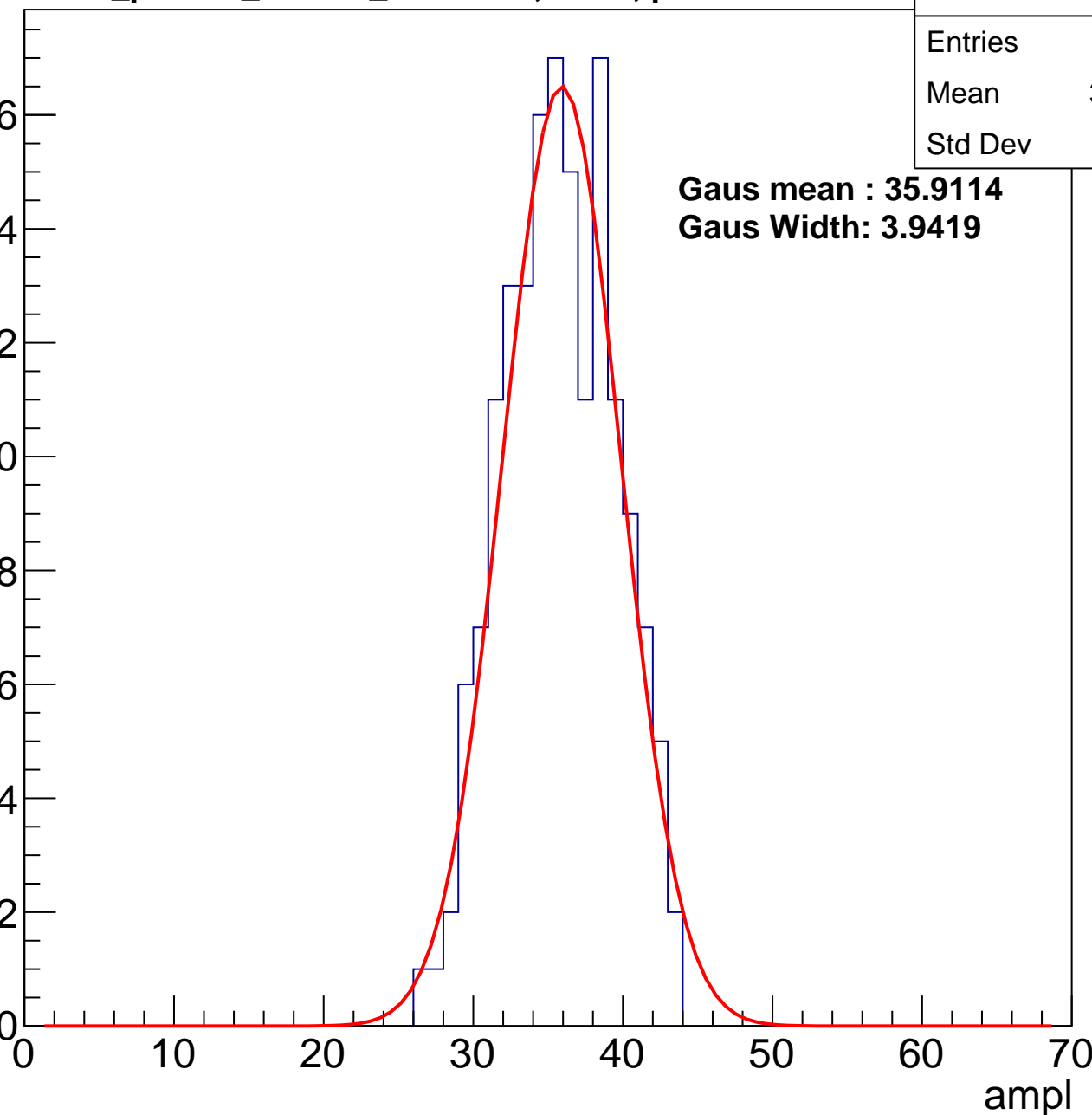
Entry

16
14
12
10
8
6
4
2
0

Entries	164
Mean	35.26
Std Dev	3.681

Gaus mean : 35.9114

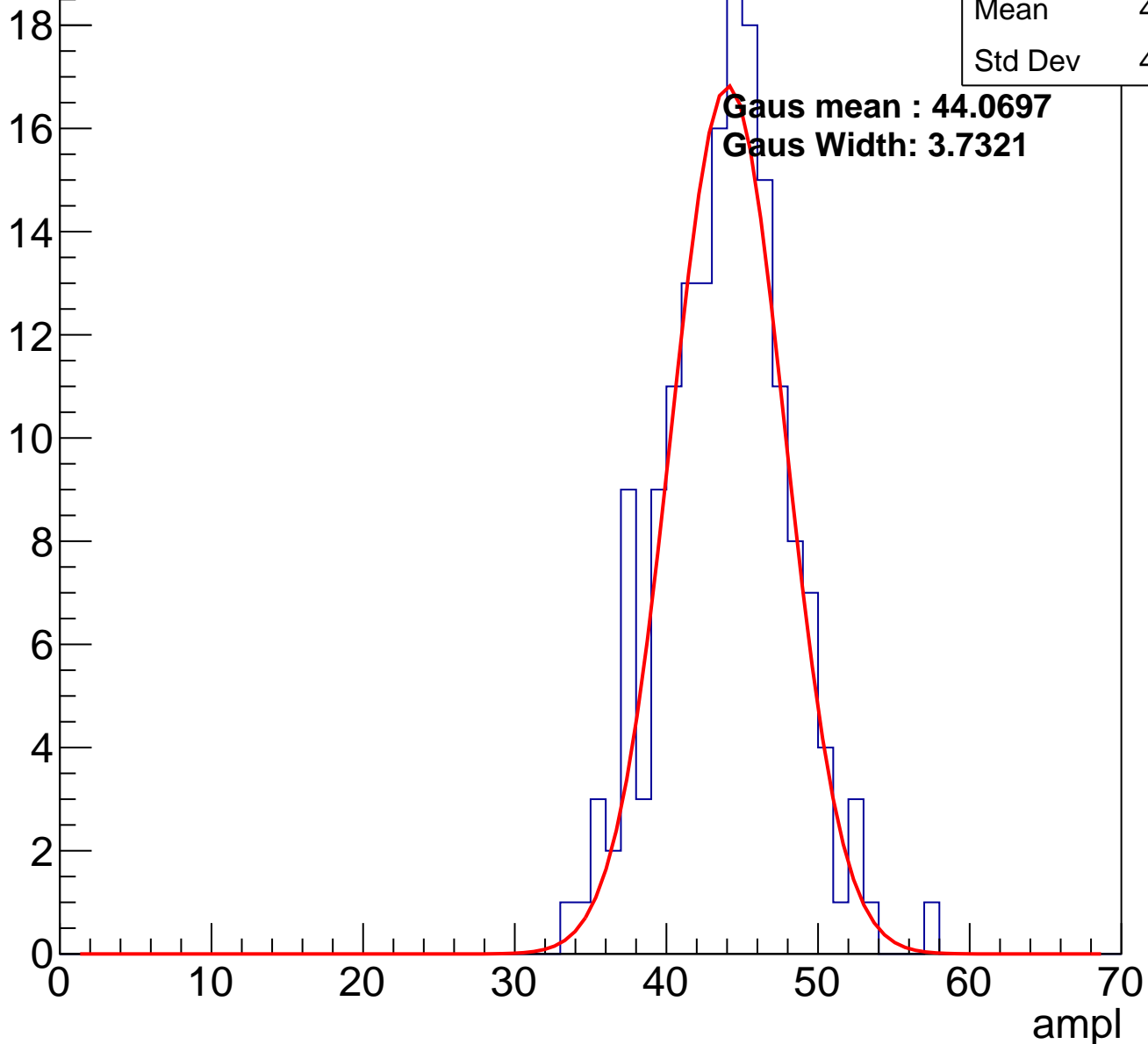
Gaus Width: 3.9419



B0L101S, U25-ch81, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

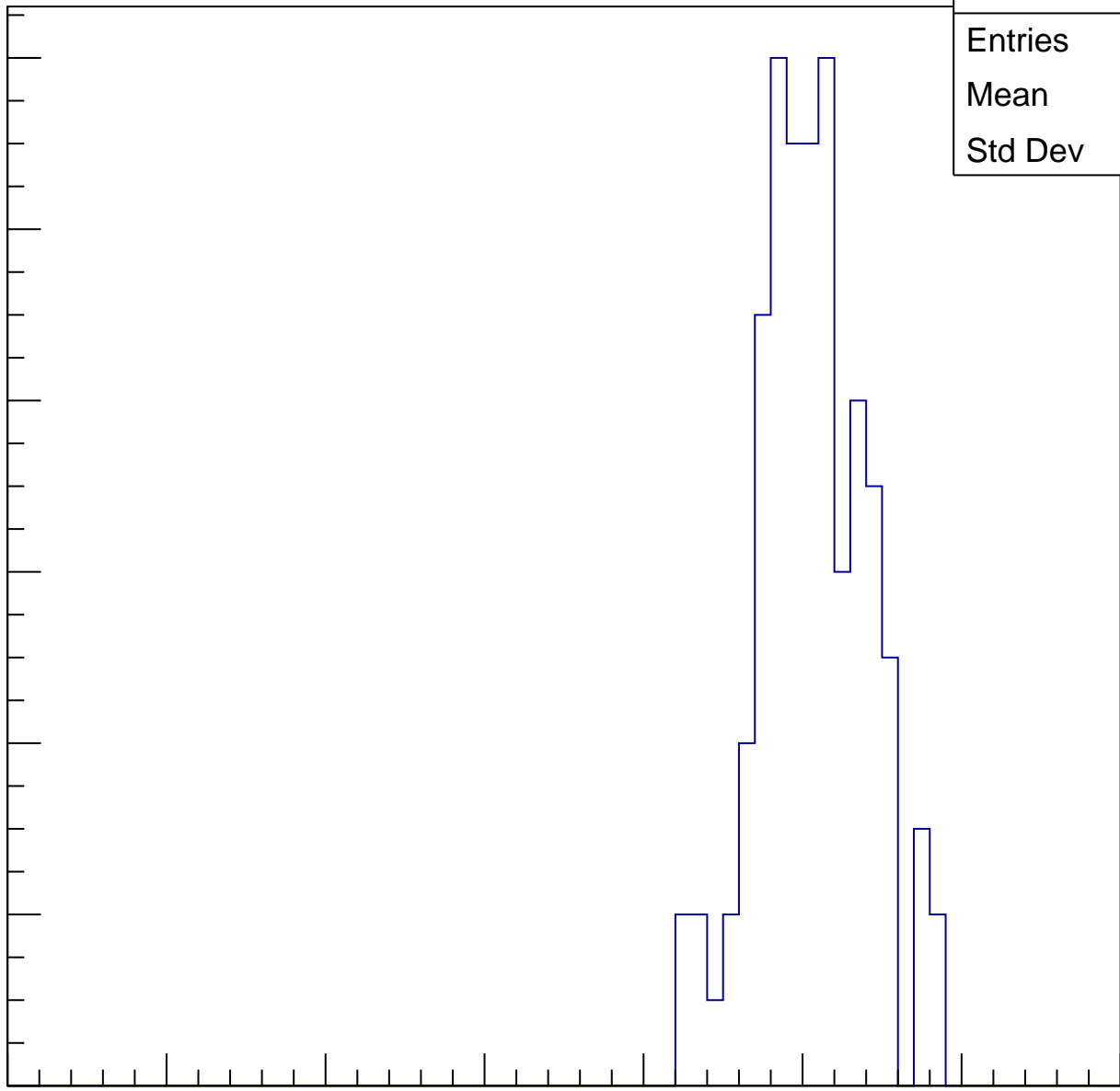
Entries	97
Mean	50.14
Std Dev	3.419

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

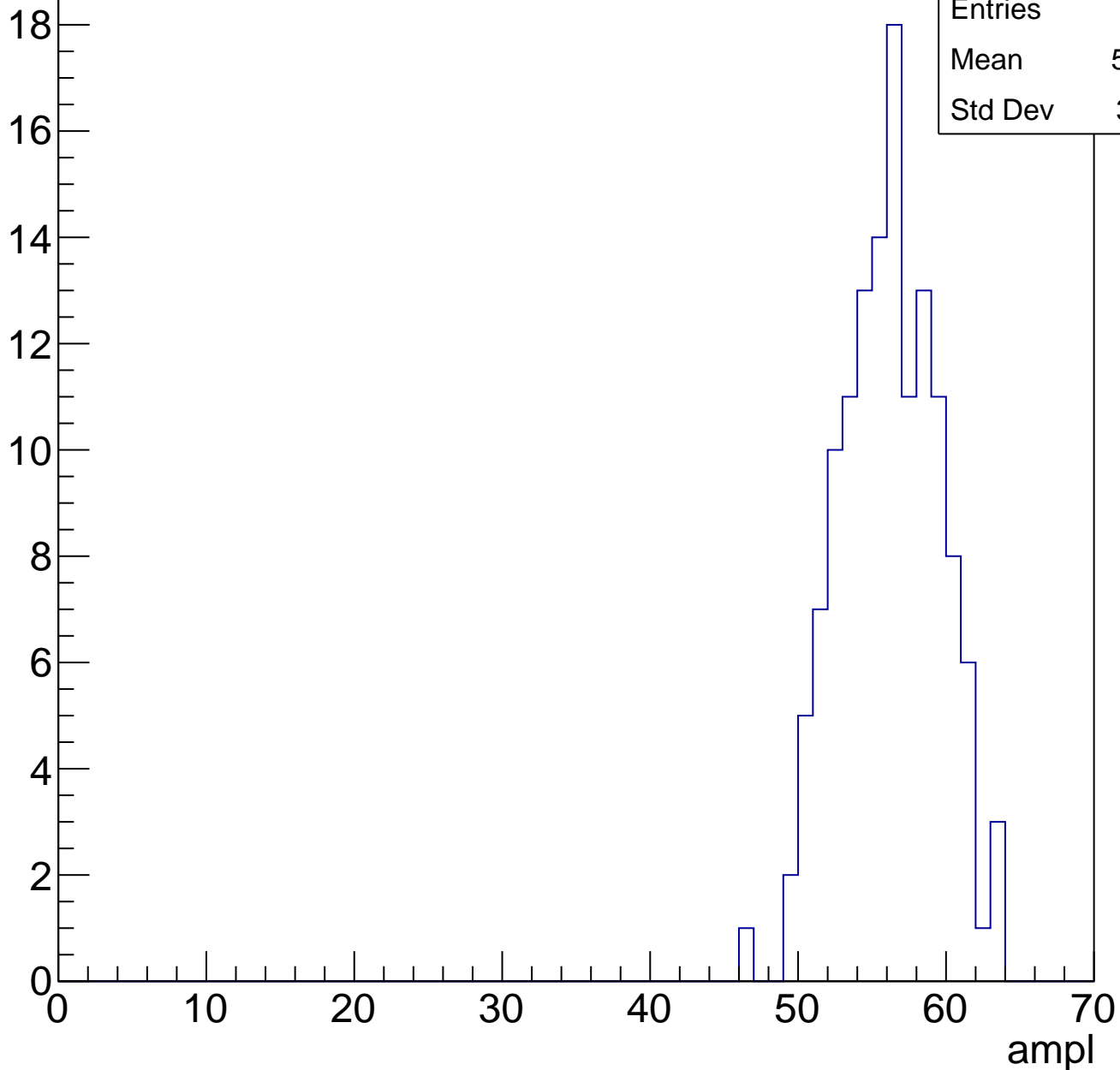
ampl



B0L101S, U25-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch81, adc5

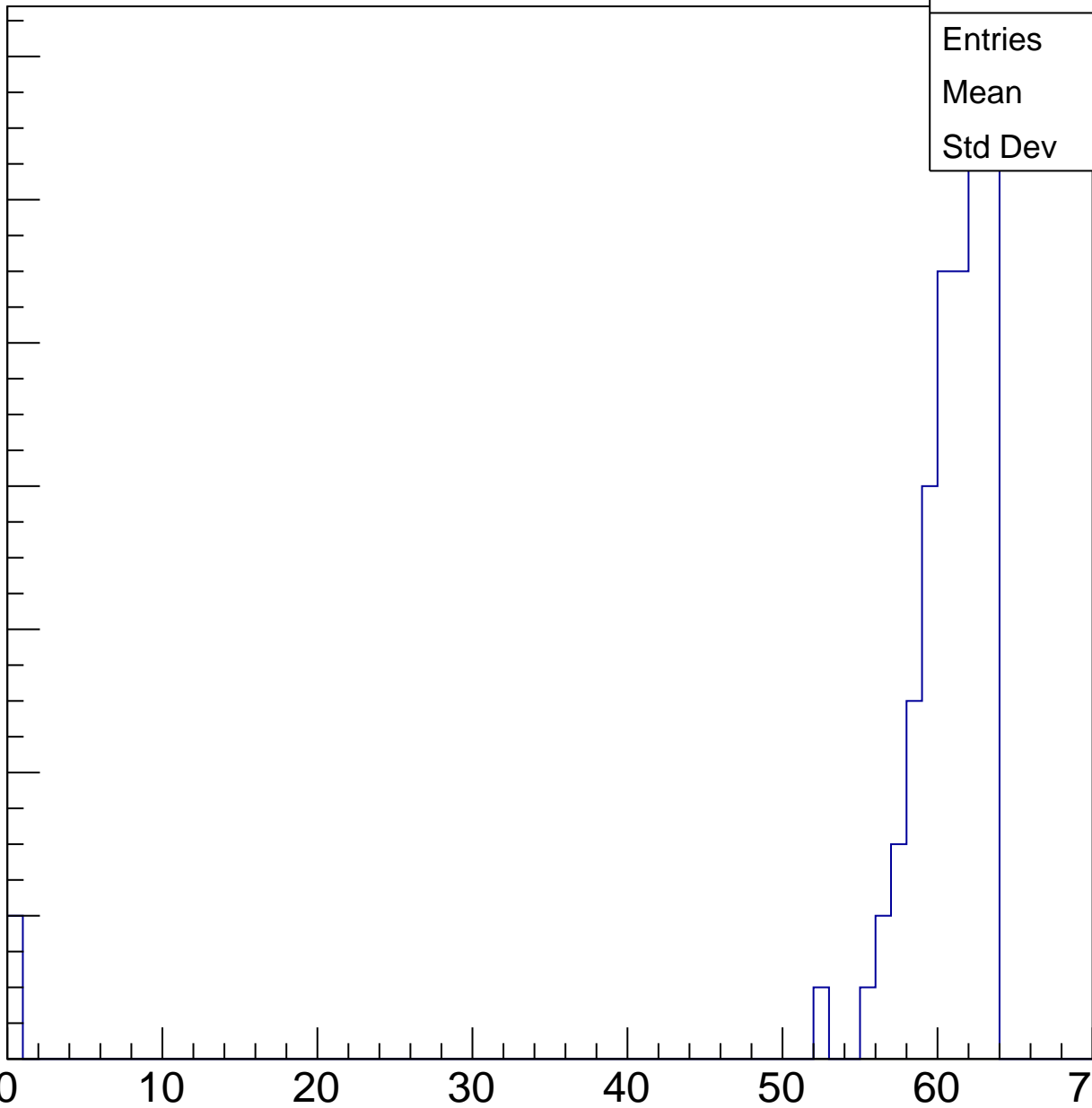
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.73
Std Dev	10.24

ampl

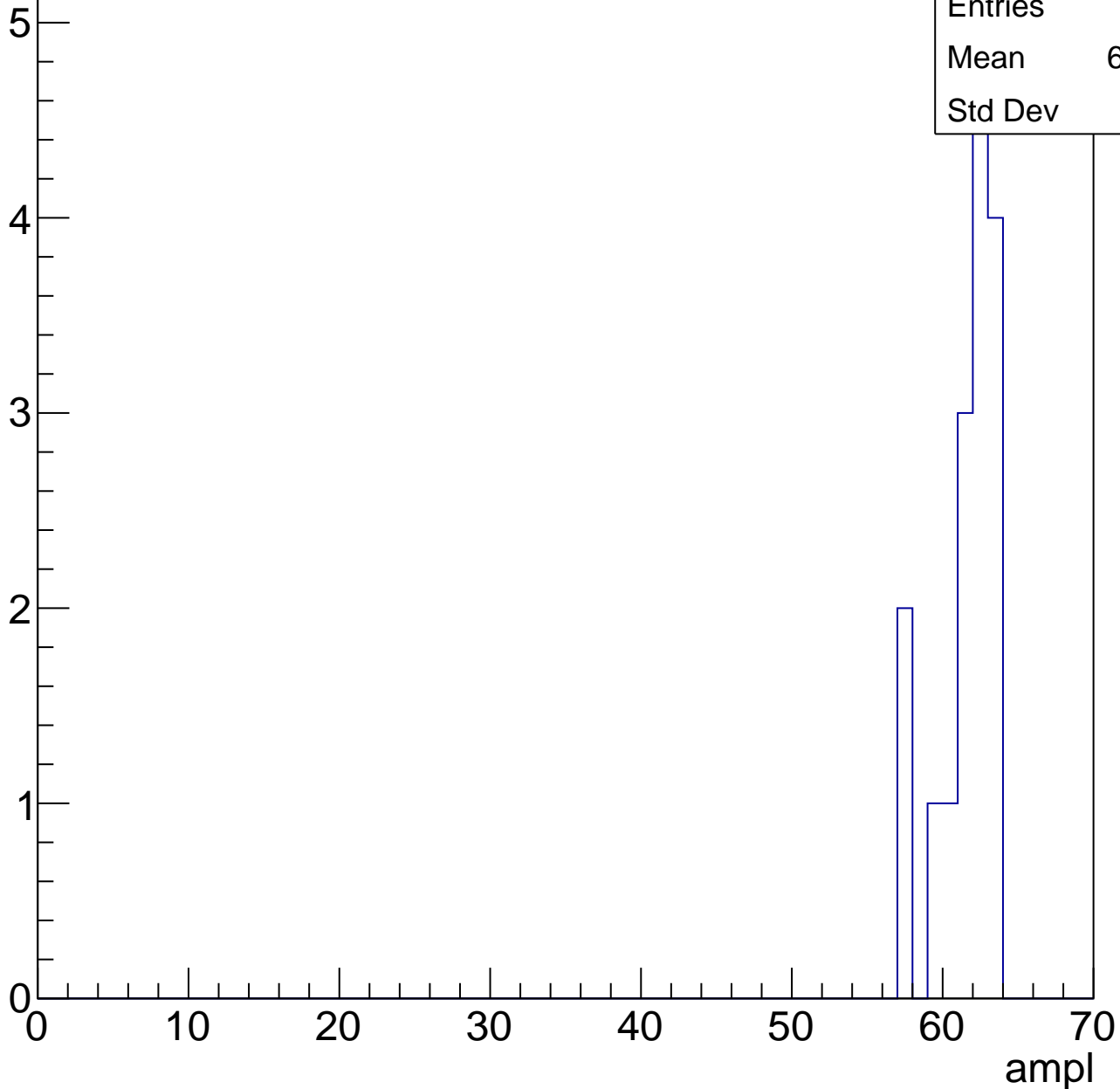


B0L101S, U25-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.12
Std Dev	1.9



B0L101S, U25-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch82, adc0

calib_packv5_042523_0143.root, FC#1, port C1

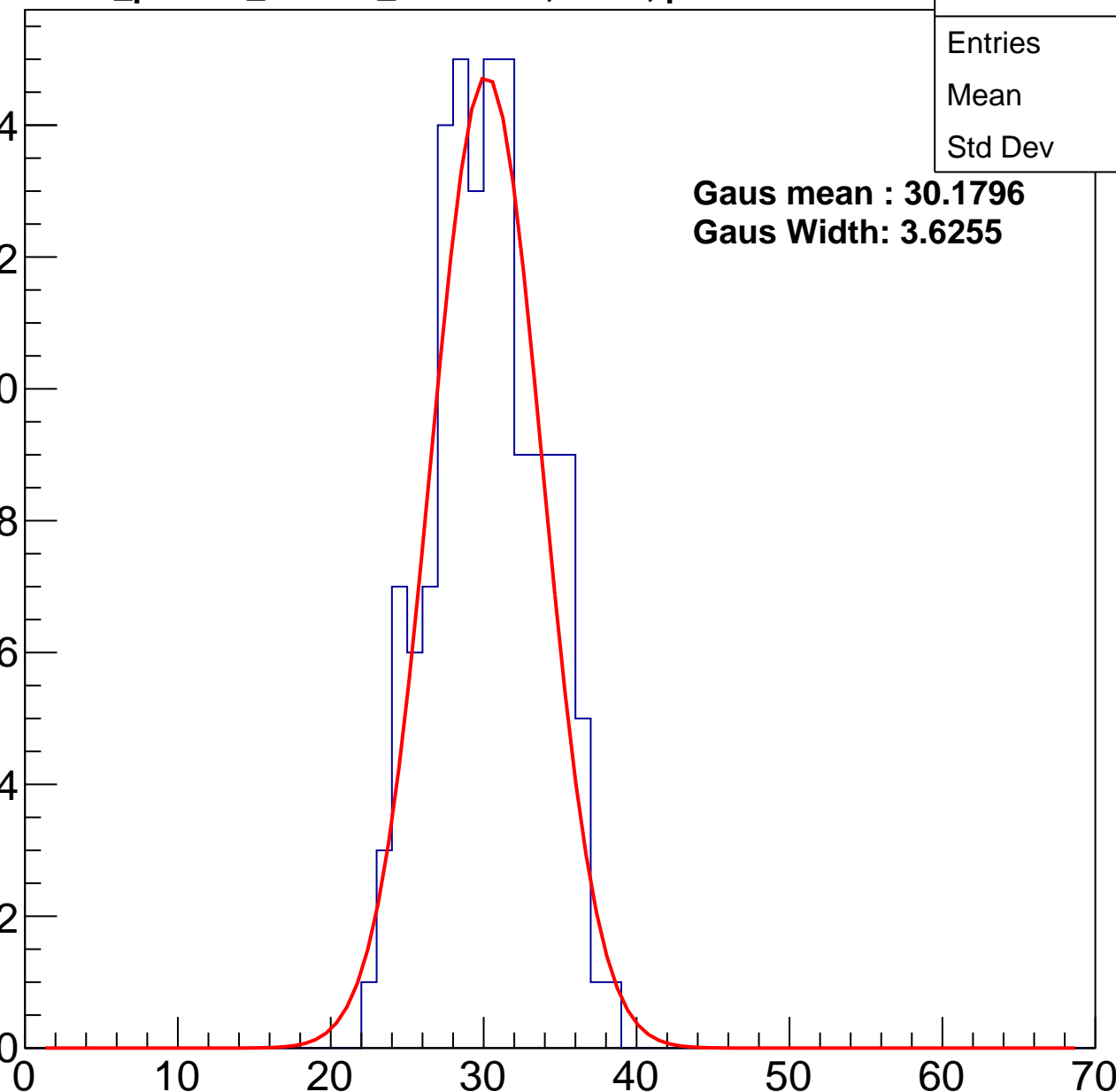
Entry

14
12
10
8
6
4
2
0

Entries	139
Mean	29.8
Std Dev	3.51

Gaus mean : 30.1796

Gaus Width: 3.6255



ampl

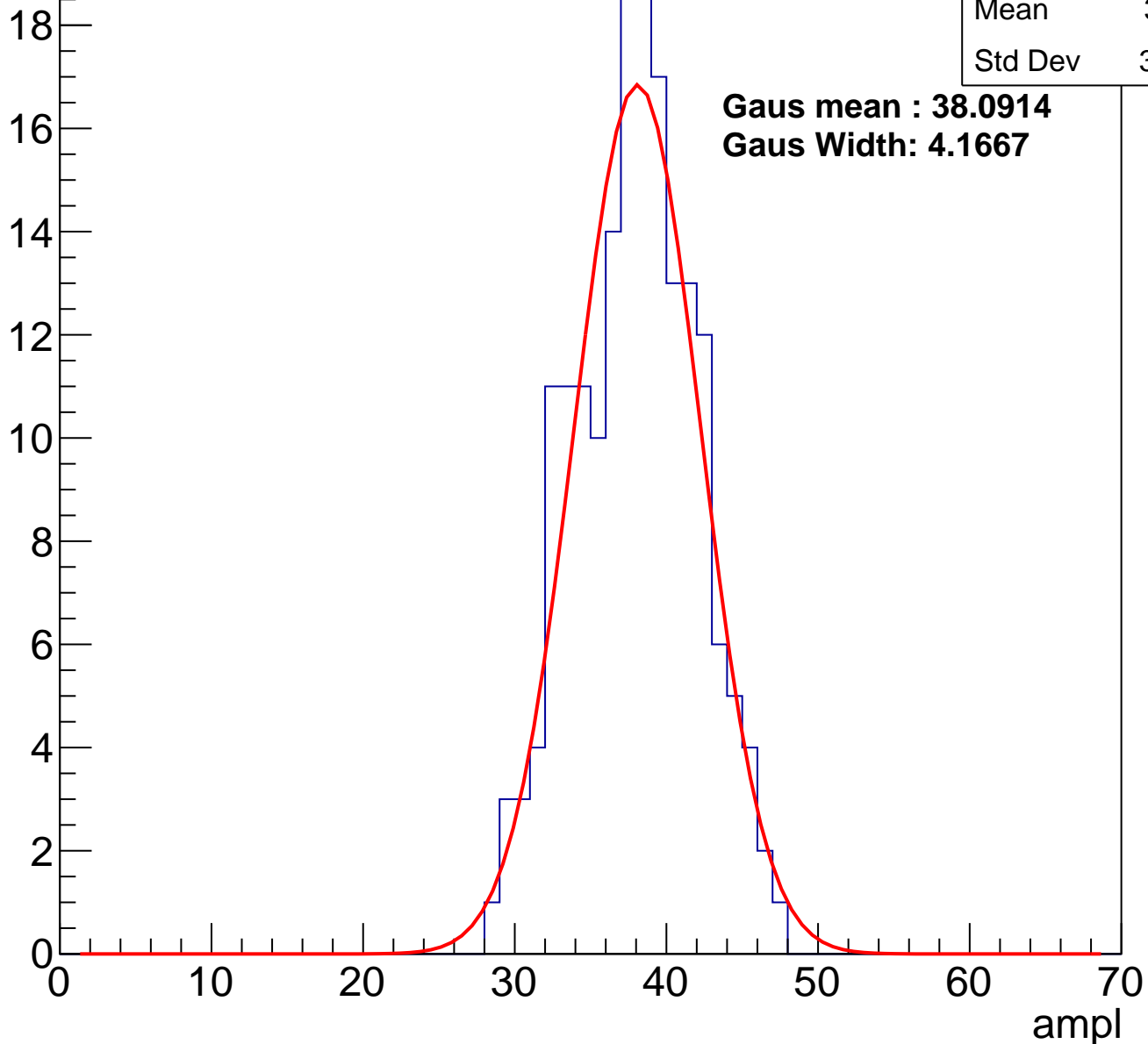
B0L101S, U25-ch82, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	37.51
Std Dev	3.964

Gaus mean : 38.0914
Gaus Width: 4.1667

Entry



B0L101S, U25-ch82, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	44.34
Std Dev	3.246

Gaus mean : 44.5904

Gaus Width: 3.2769

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

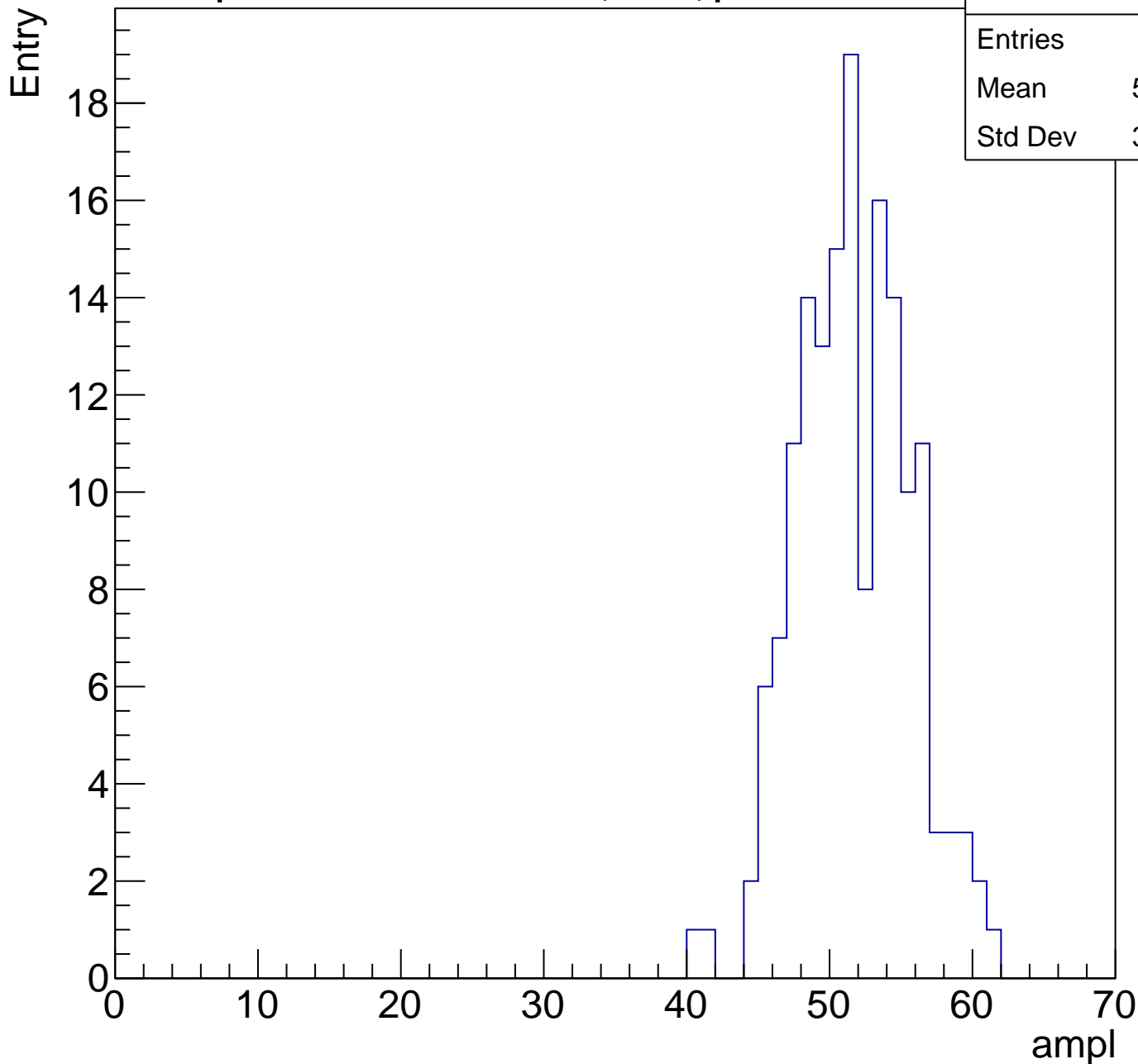
60

70

B0L101S, U25-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

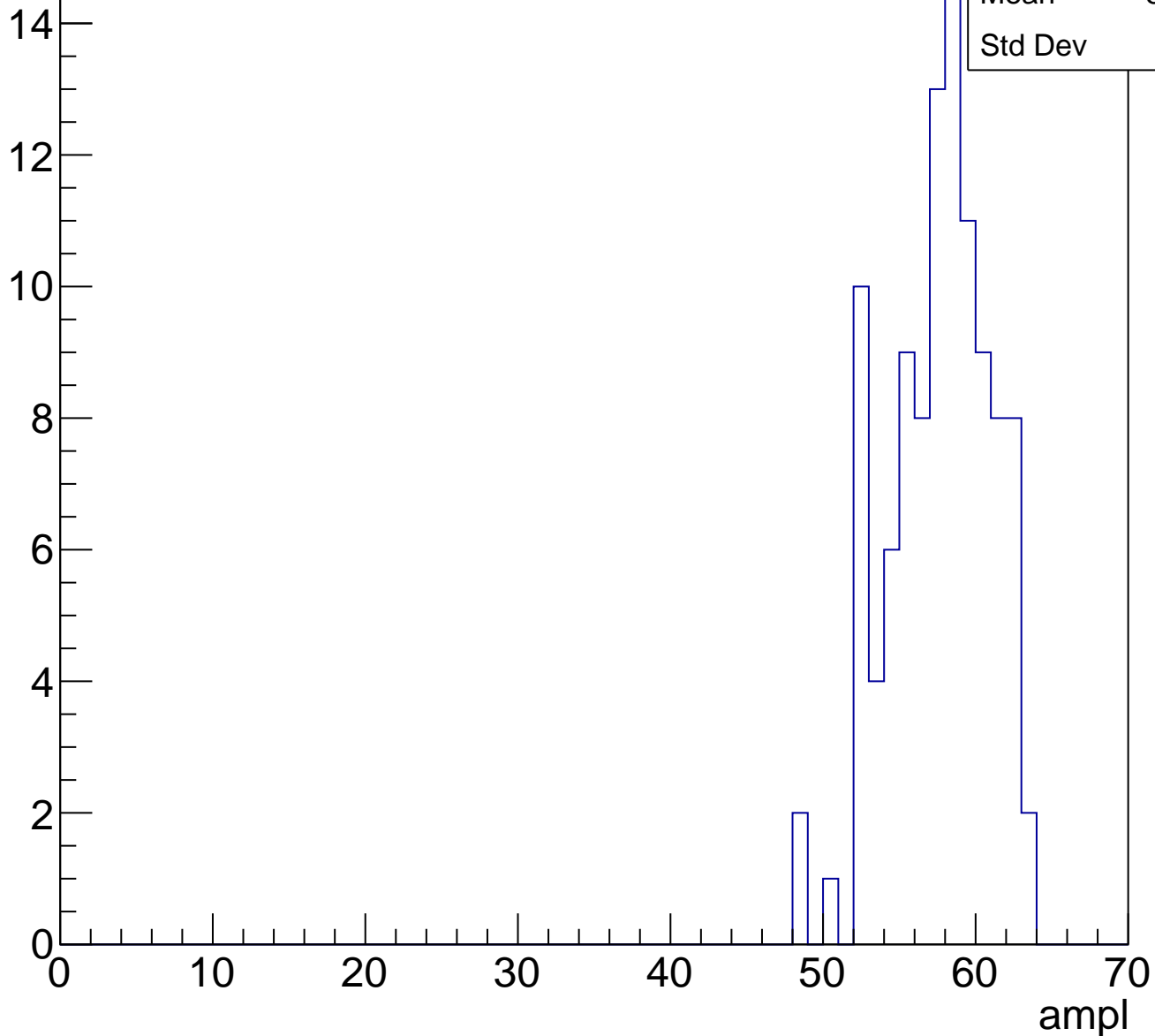
Entries	160
Mean	51.22
Std Dev	3.884



B0L101S, U25-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch82, adc5

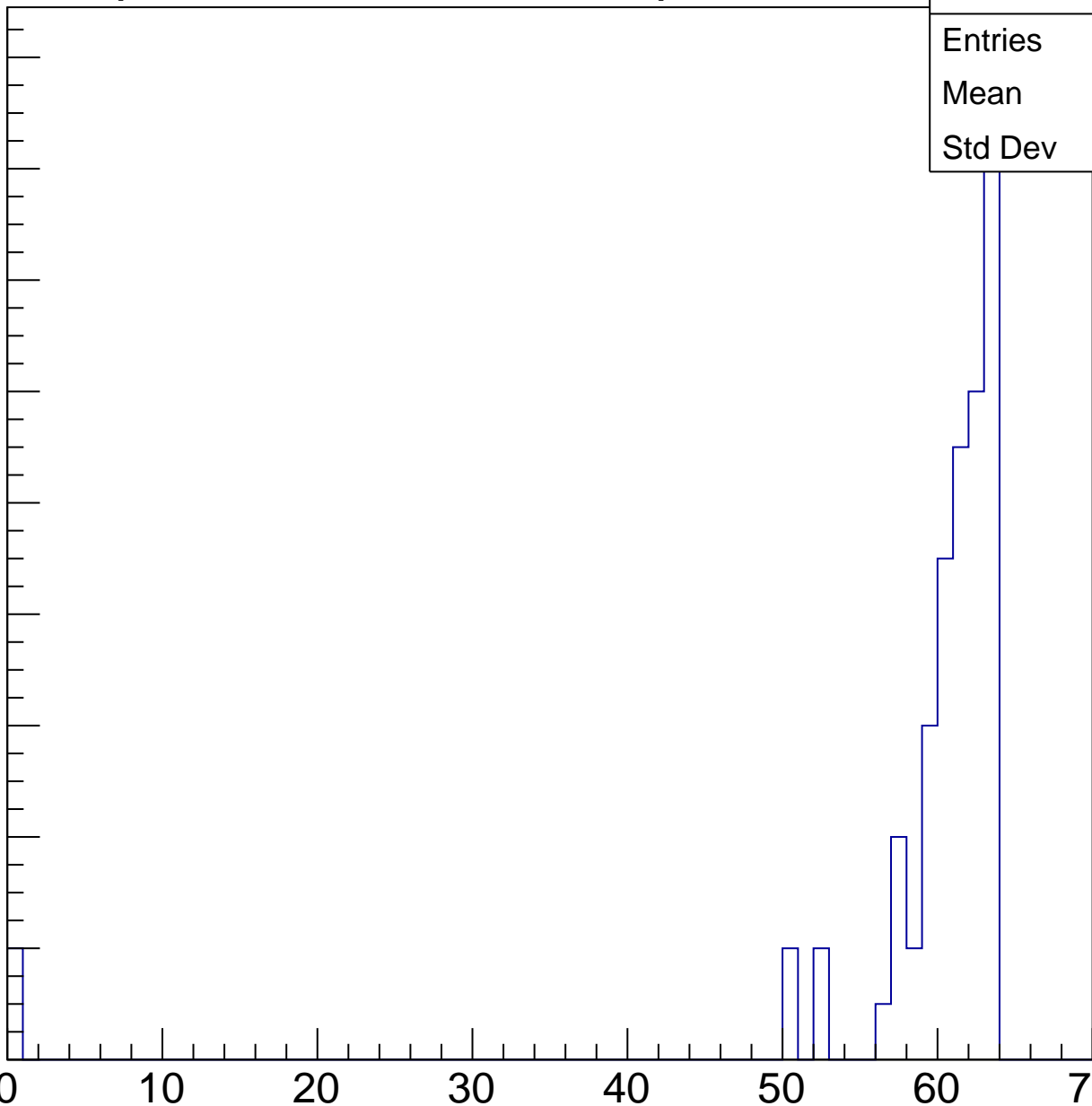
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.65
Std Dev	10.56

ampl



B0L101S, U25-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch83, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	30.98
Std Dev	4.541

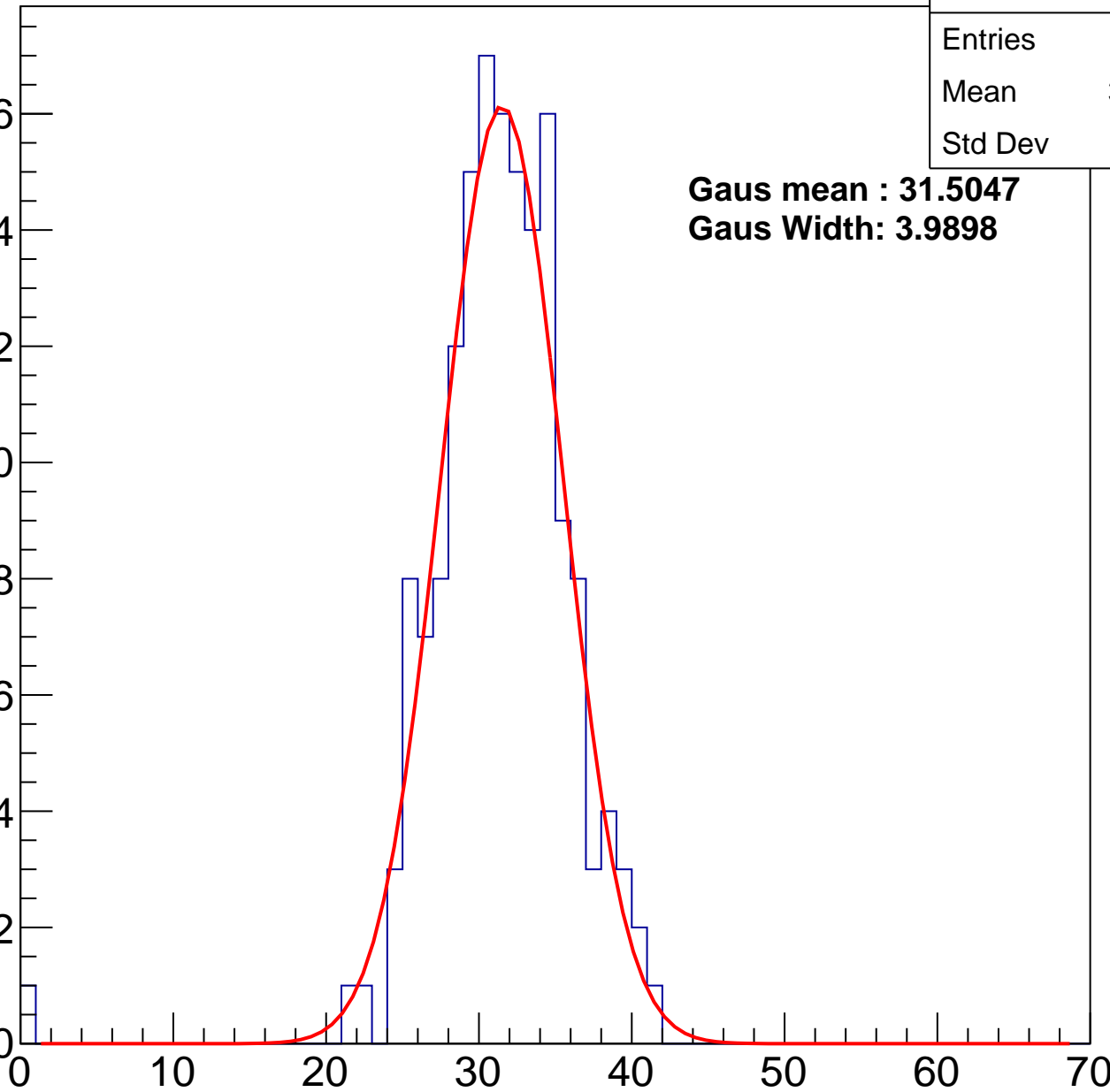
Gaus mean : 31.5047

Gaus Width: 3.9898

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch83, adc1

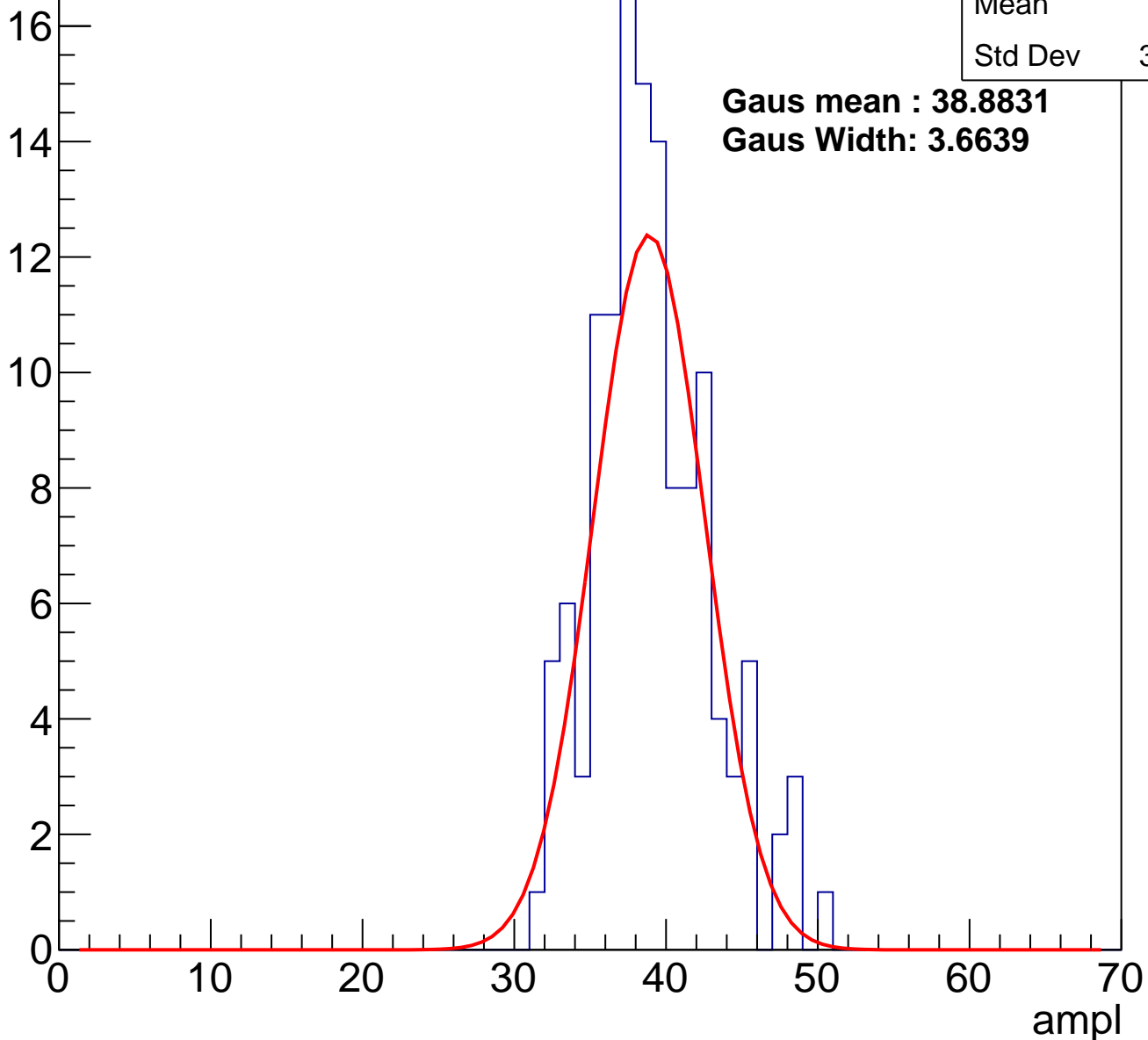
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	127
Mean	38.6
Std Dev	3.846

Gaus mean : 38.8831

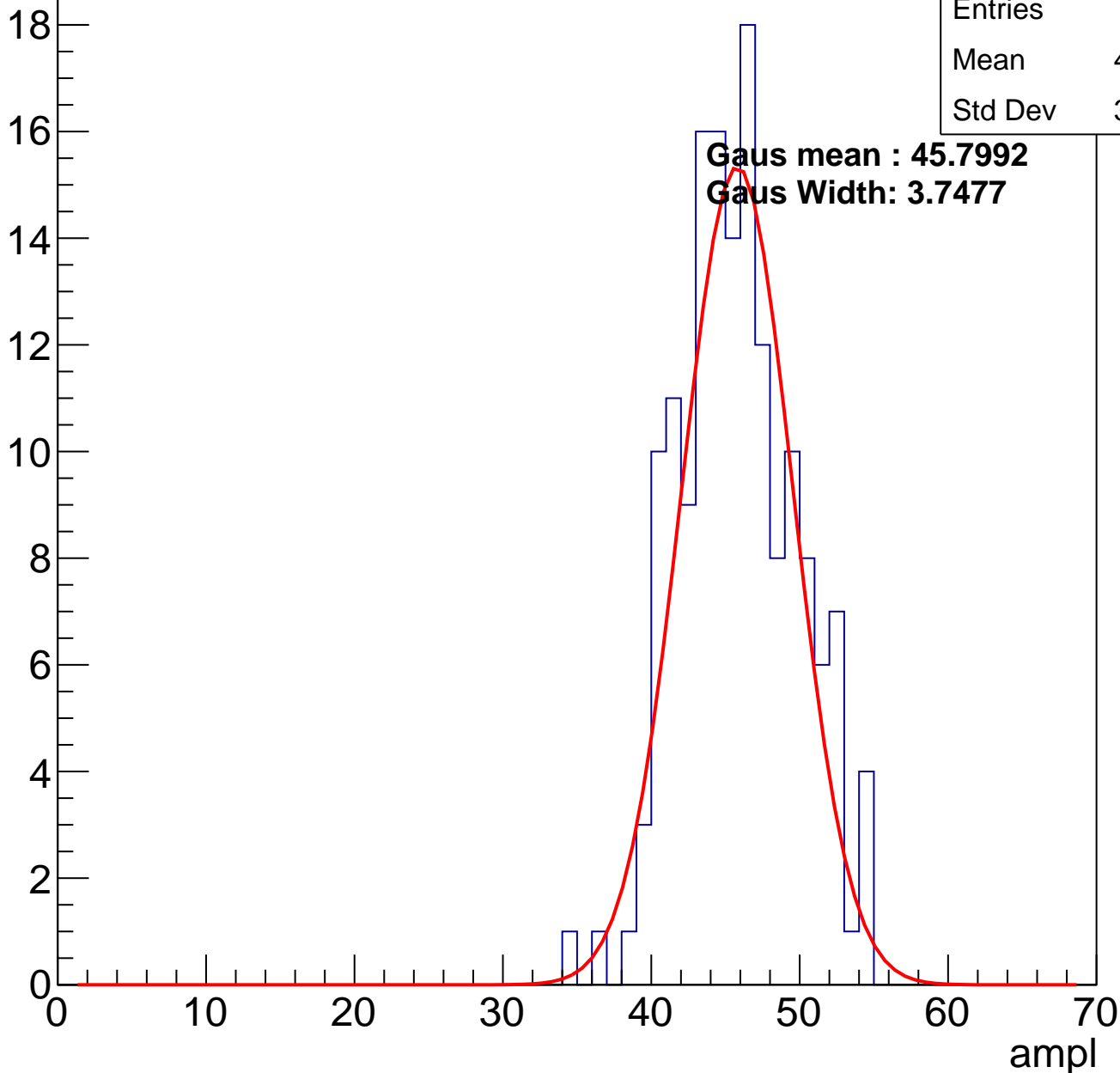
Gaus Width: 3.6639



B0L101S, U25-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

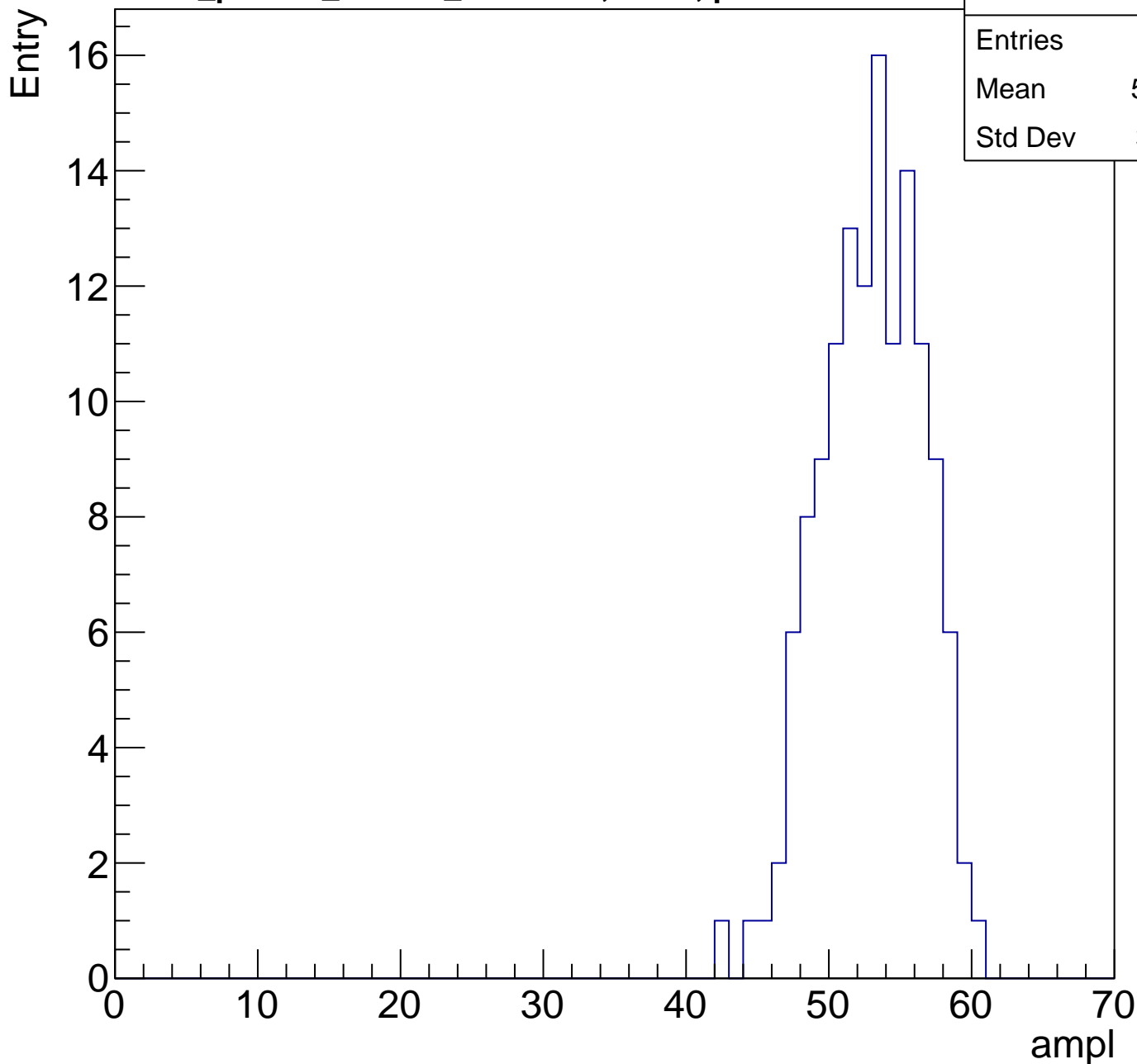
Entry



B0L101S, U25-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	52.49
Std Dev	3.491

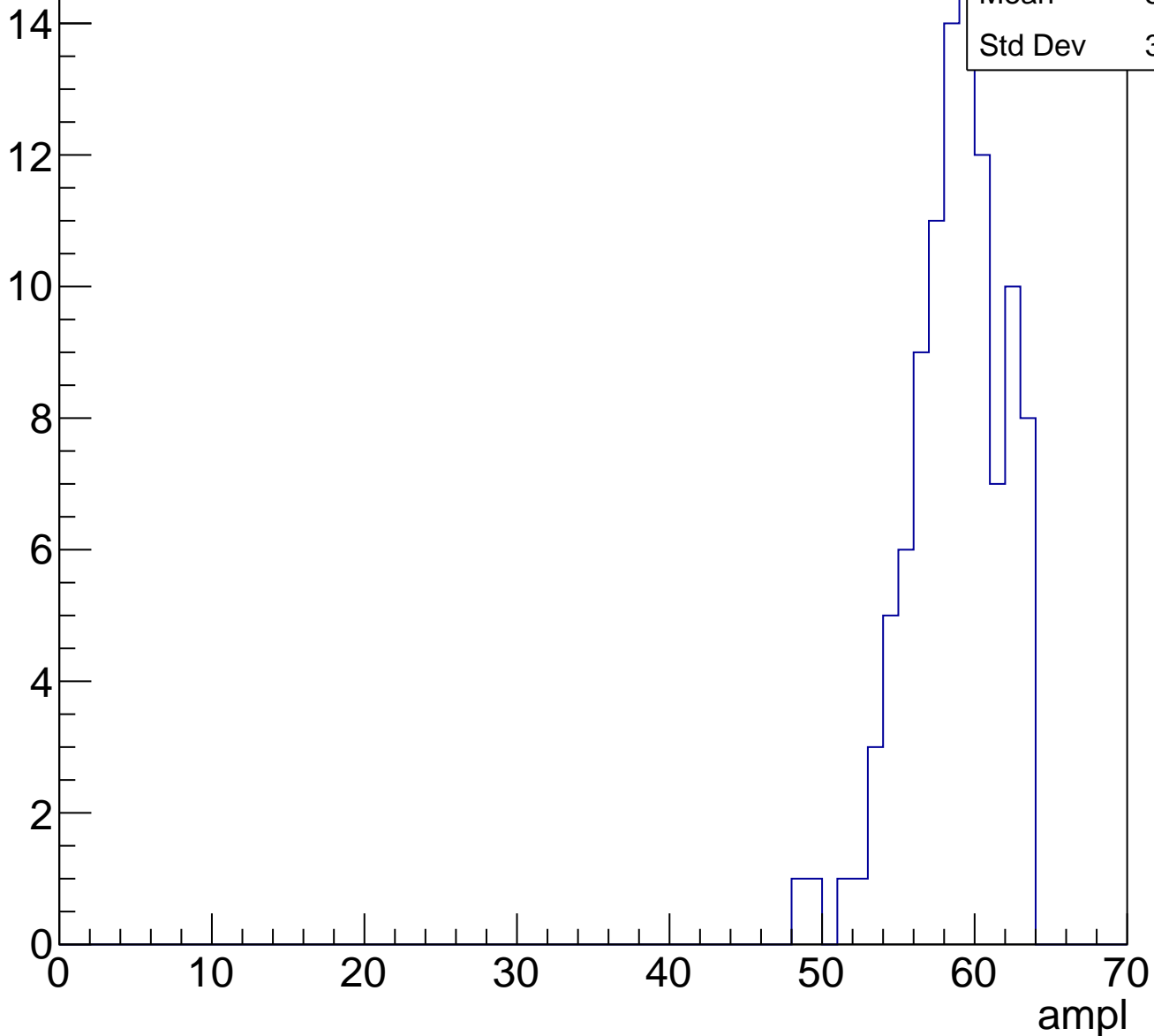


B0L101S, U25-ch83, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	58.25
Std Dev	3.103

Entry



B0L101S, U25-ch83, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

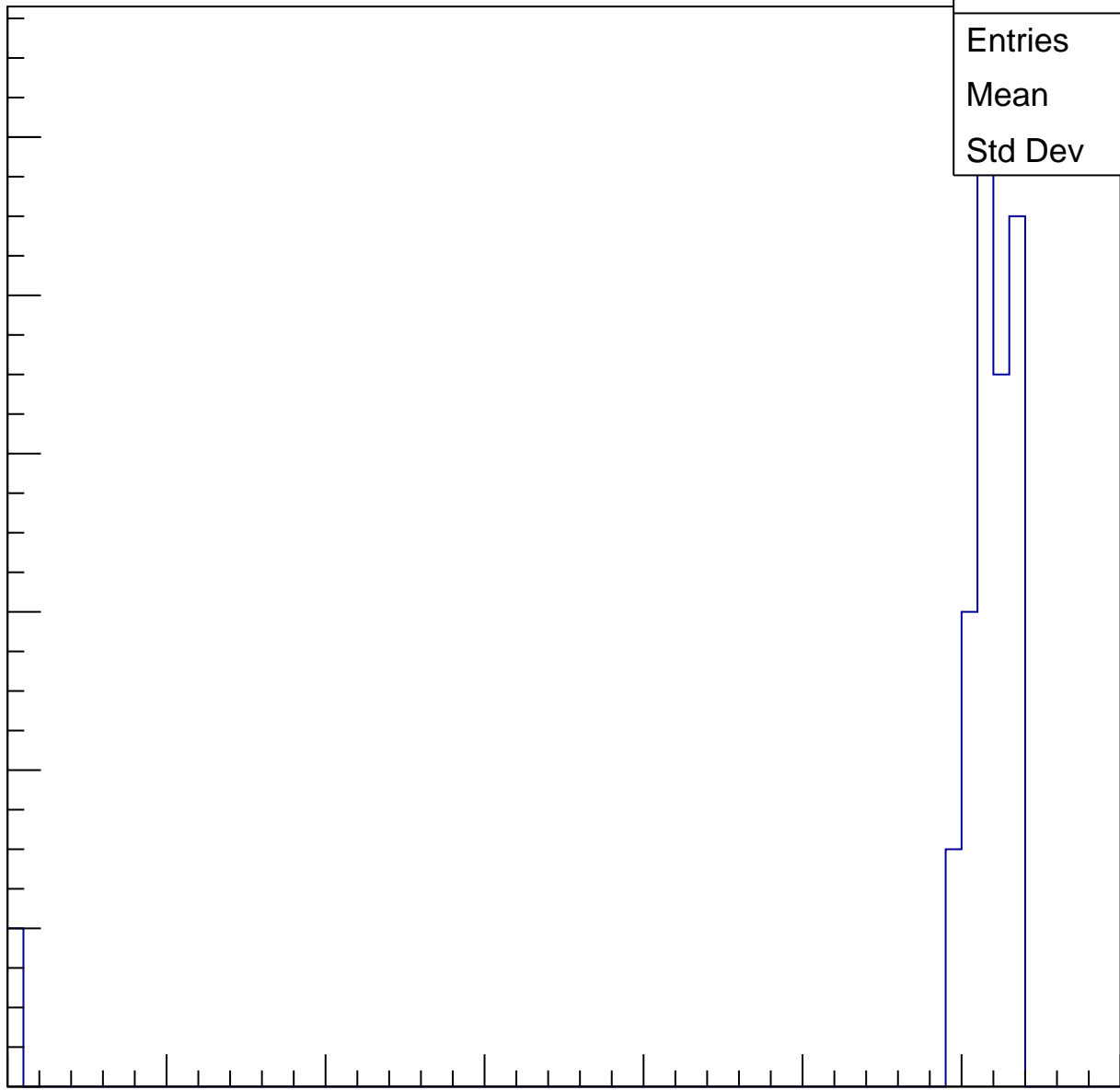
2

0

Entries	44
Mean	58.66
Std Dev	12.86

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch84, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	30.02
Std Dev	4.553

Gaus mean : 30.5809

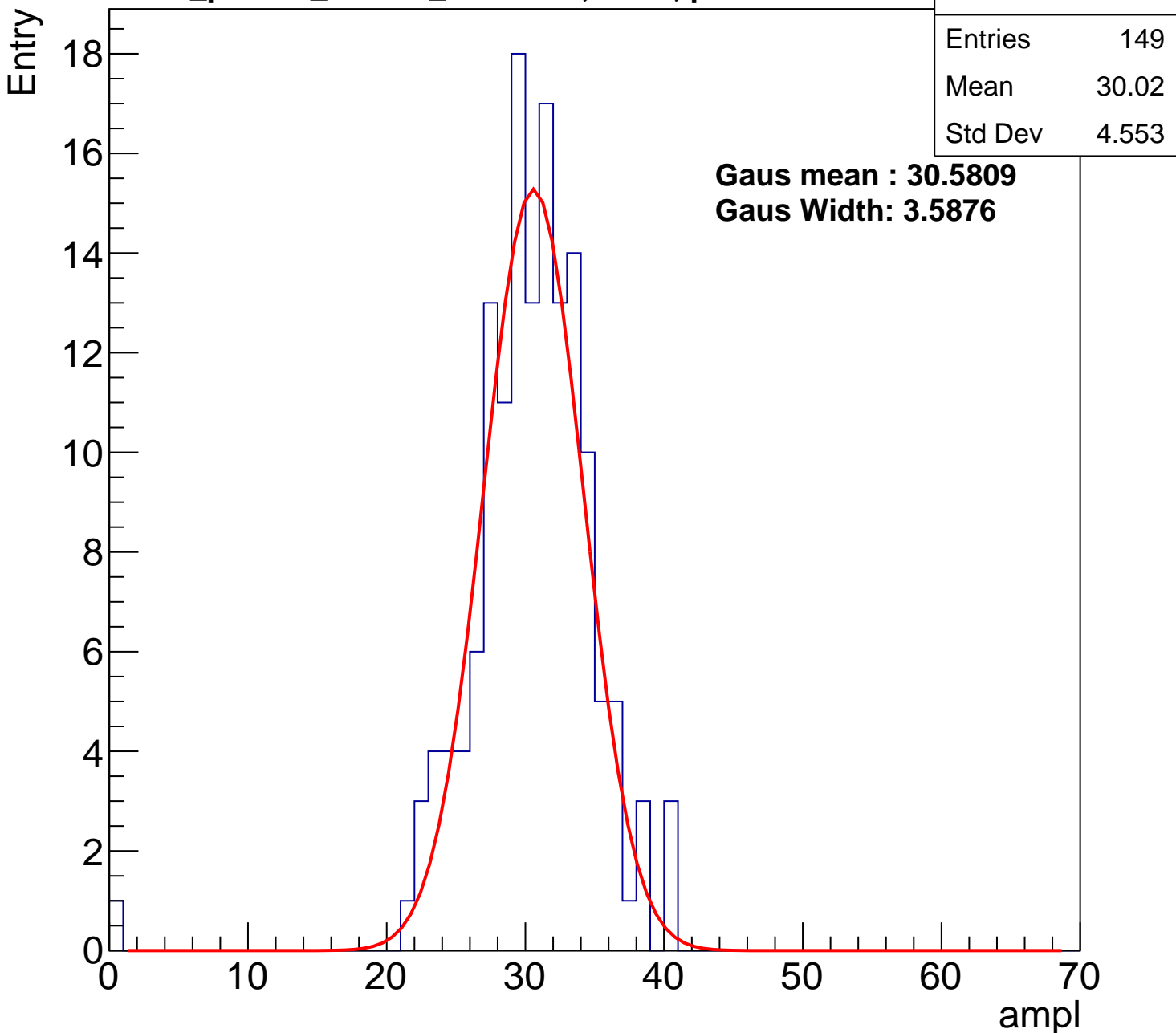
Gaus Width: 3.5876

Entry

18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



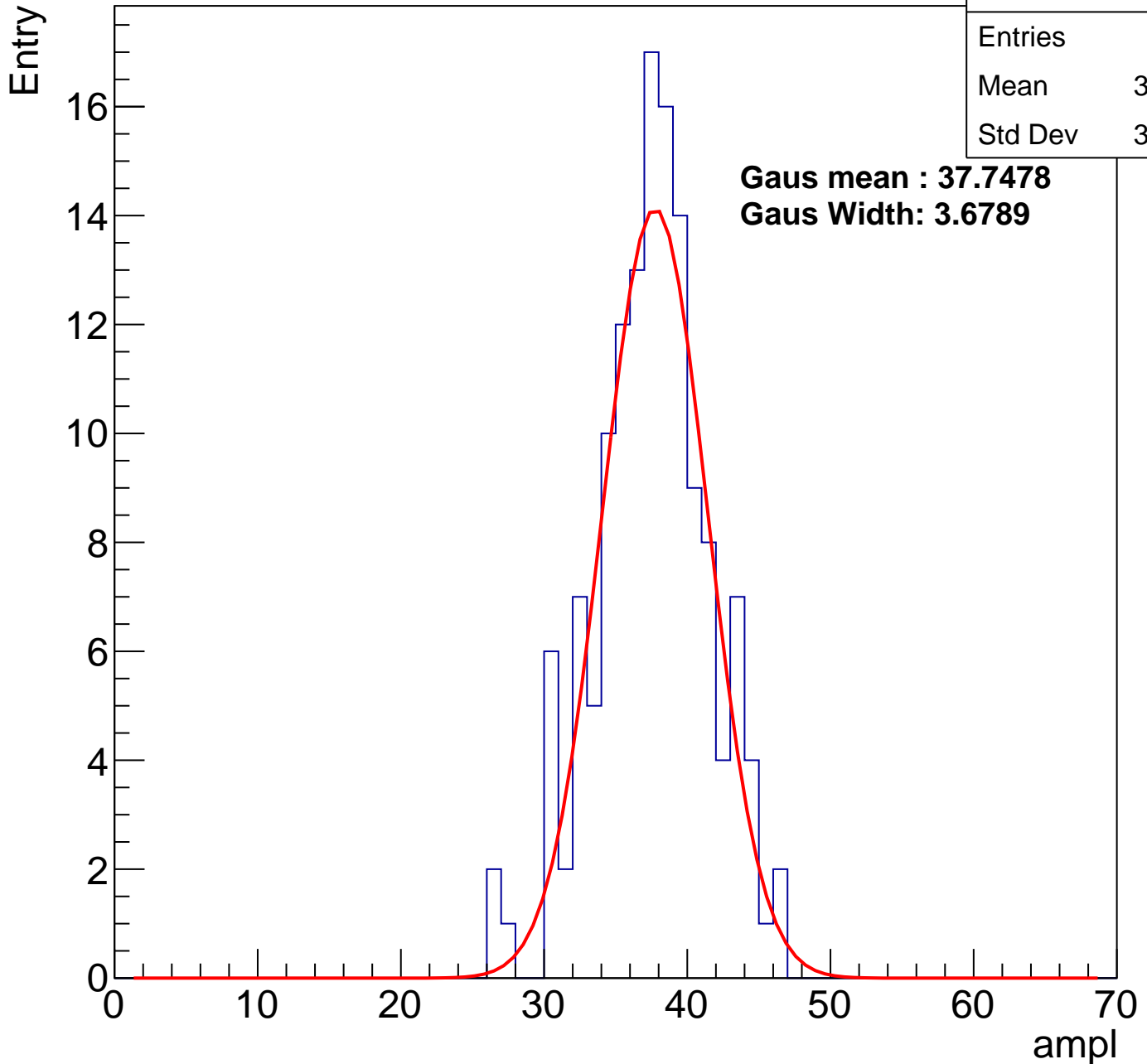
B0L101S, U25-ch84, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	37.08
Std Dev	3.935

Gaus mean : 37.7478

Gaus Width: 3.6789



B0L101S, U25-ch84, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

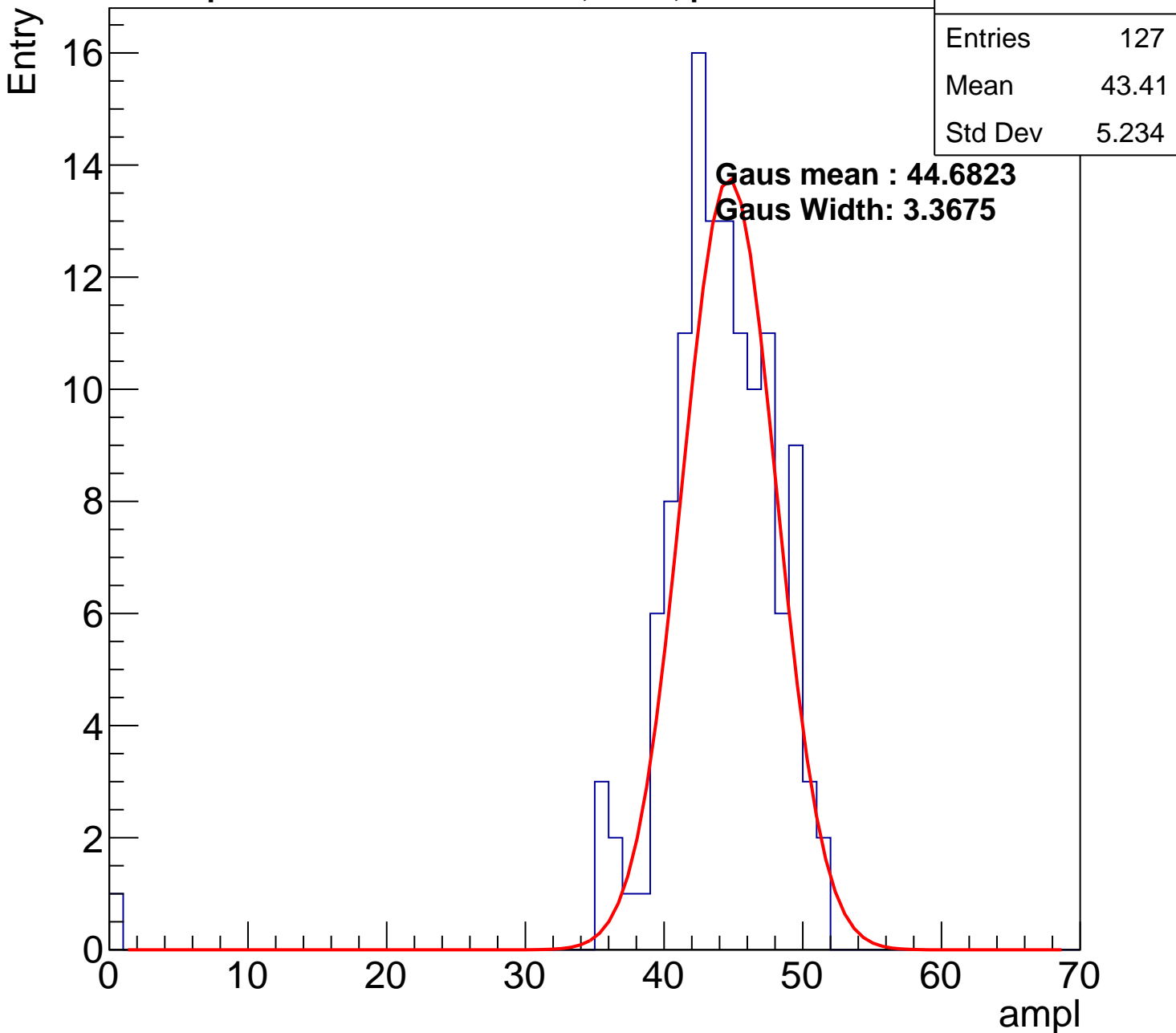
Entries	127
Mean	43.41
Std Dev	5.234

Gaus mean : 44.6823

Gaus Width: 3.3675

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch84, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	50.69
Std Dev	3.969

Entry

16

14

12

10

8

6

4

2

0

0

10

20

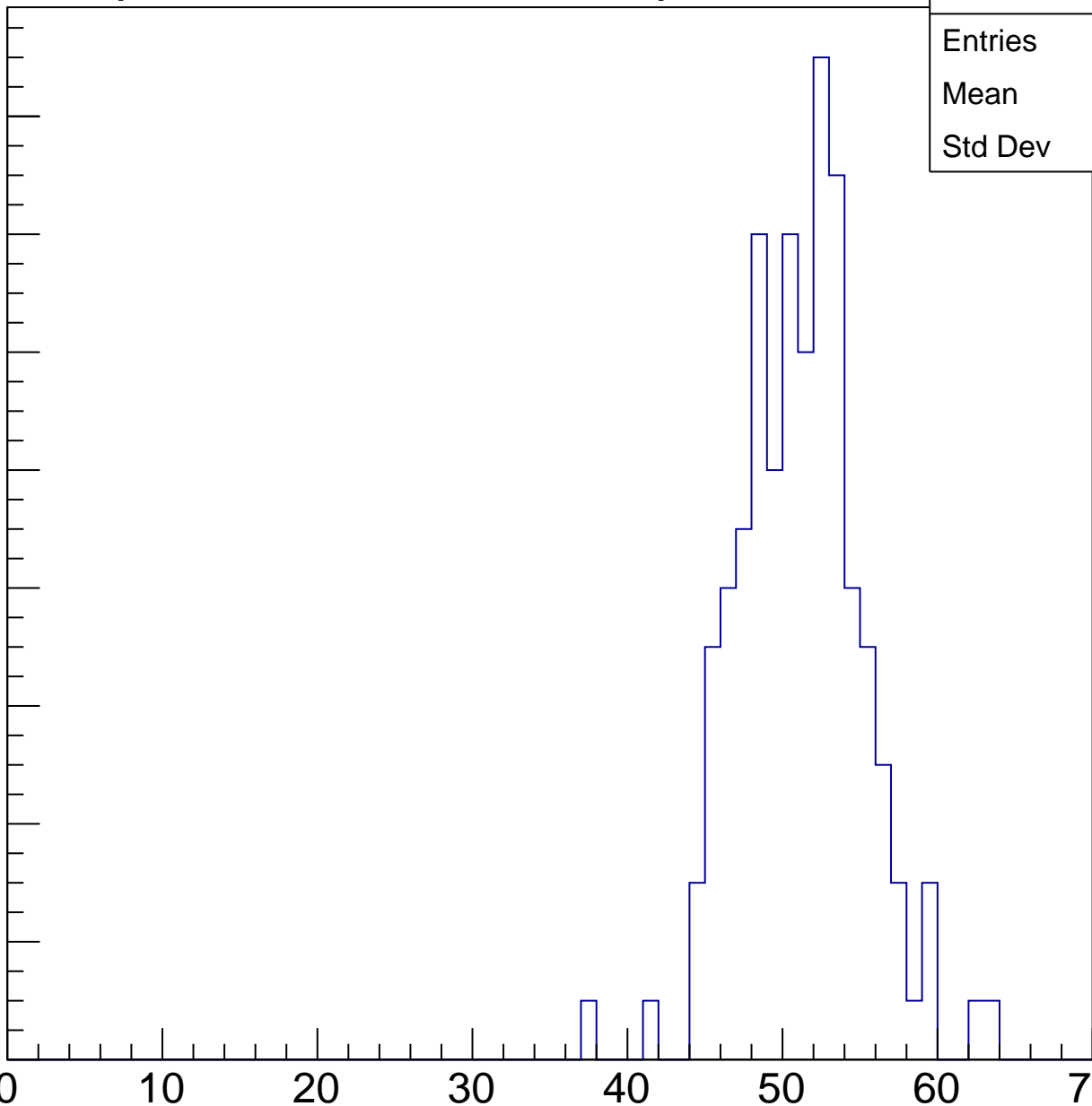
30

40

50

60

ampl



B0L101S, U25-ch84, adc4

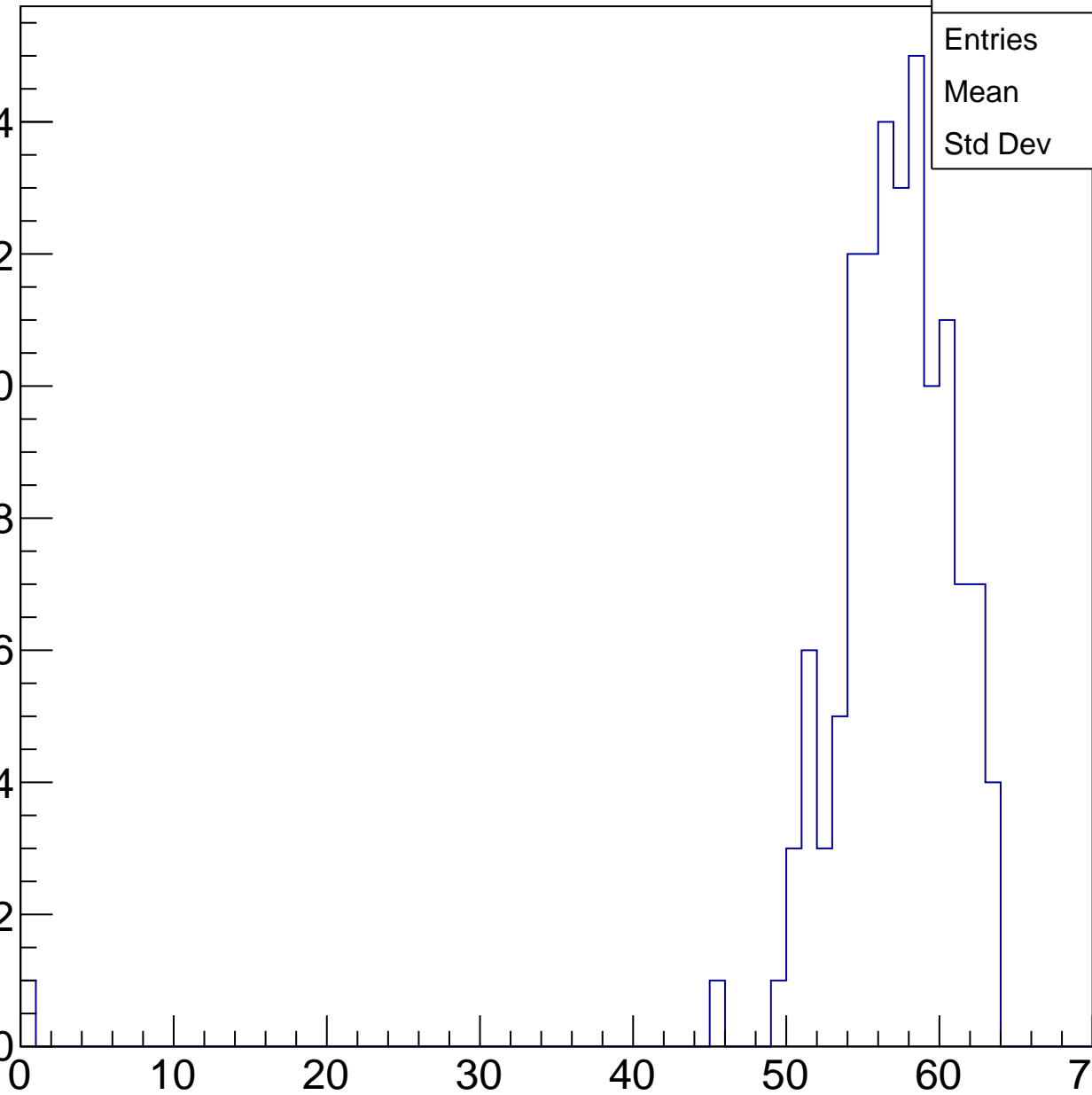
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	56.3
Std Dev	6.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch84, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

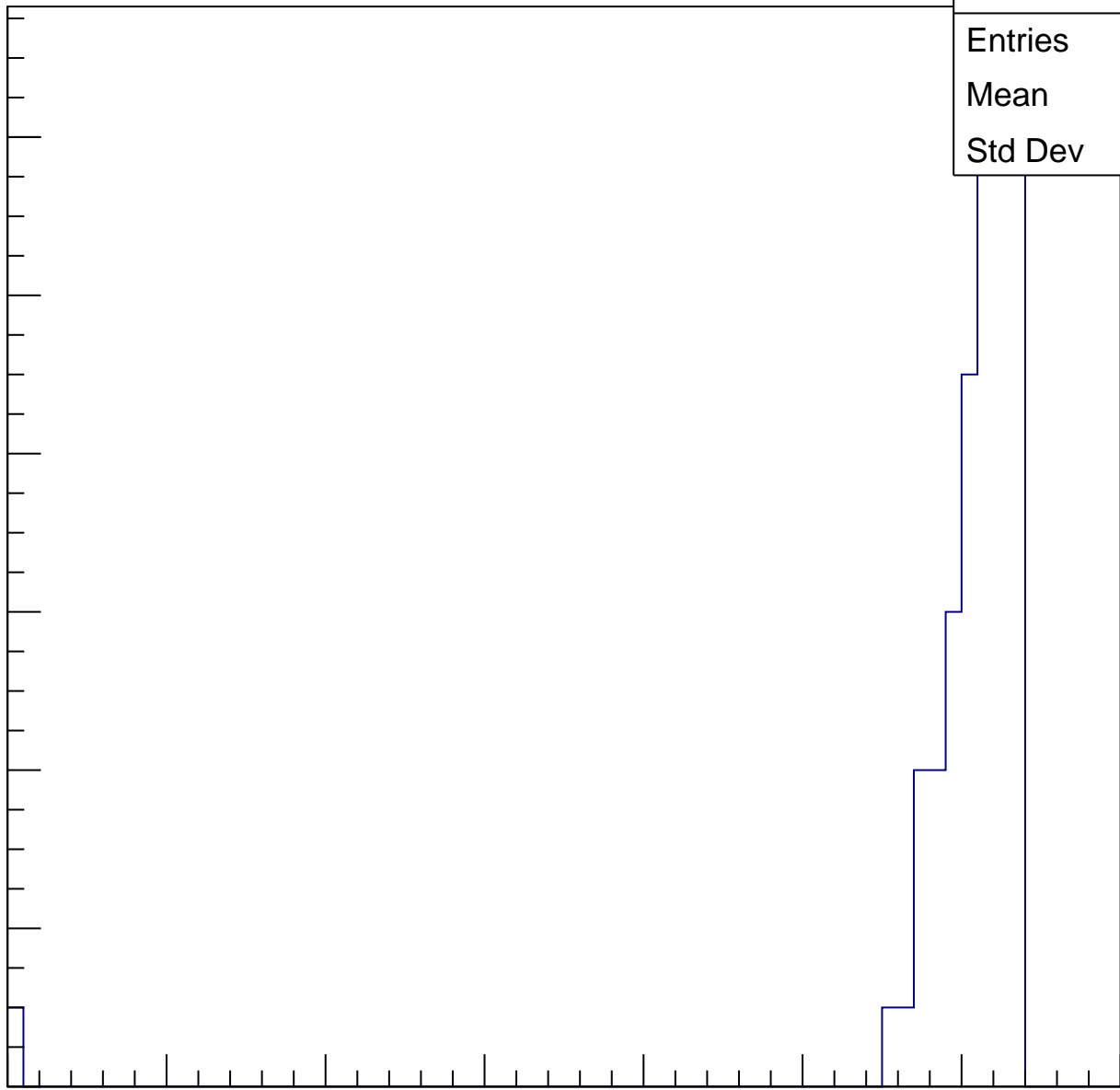
2

0

Entries	63
Mean	59.65
Std Dev	7.828

ampl

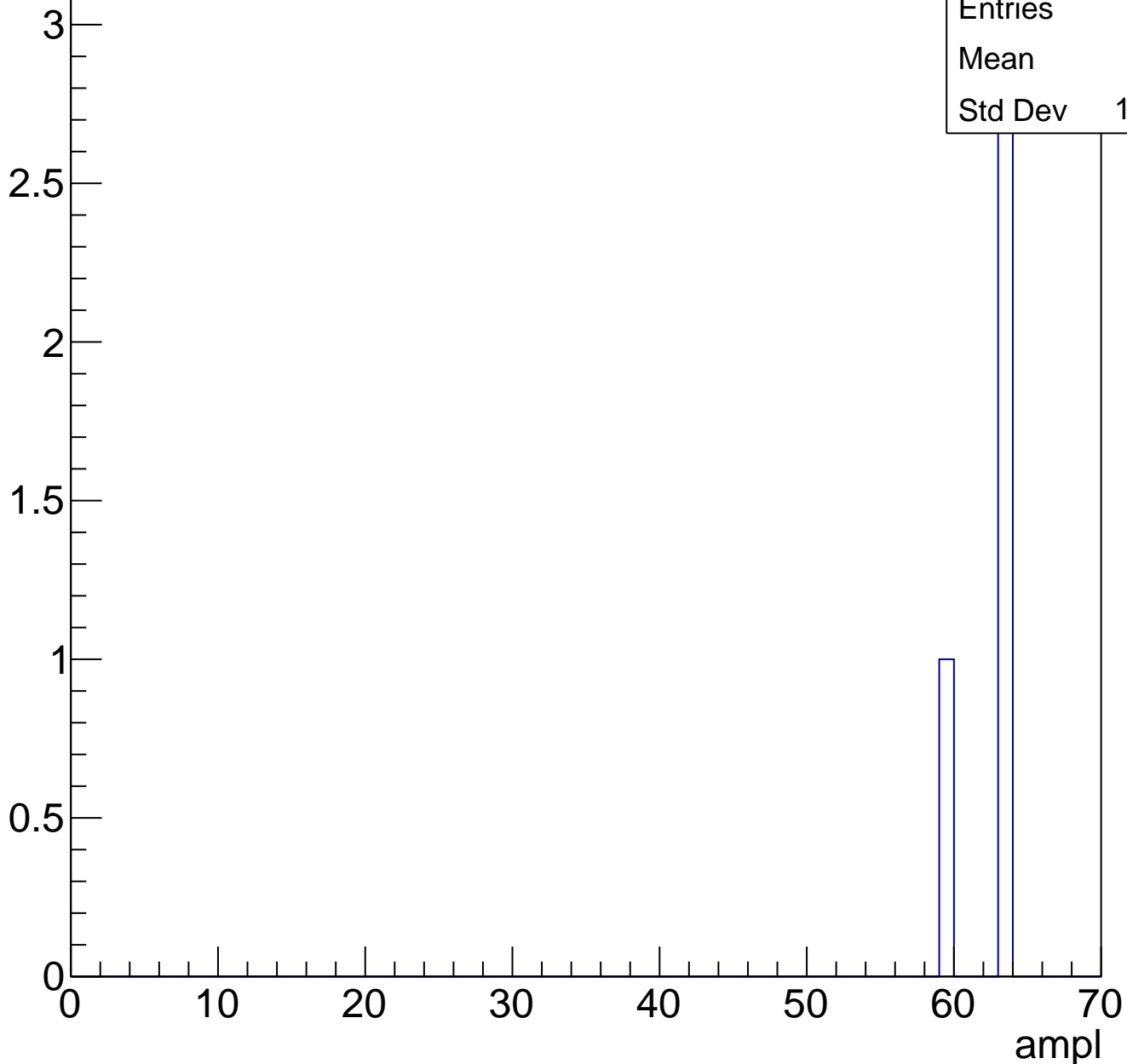
0 10 20 30 40 50 60 70



B0L101S, U25-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch85, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	29.29
Std Dev	4.916

Gaus mean : 30.3598

Gaus Width: 3.6916

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

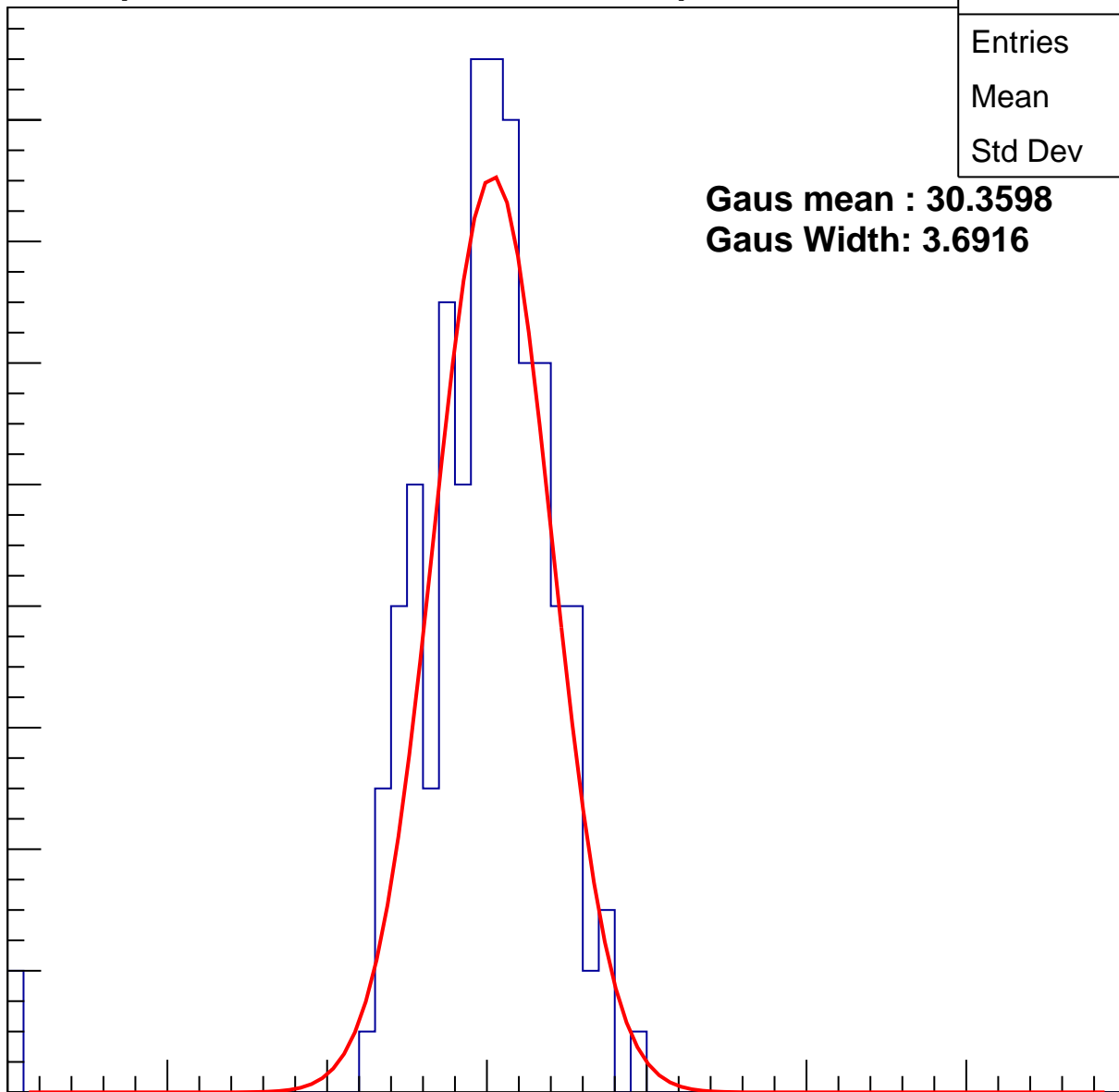
40

50

60

70

ampl



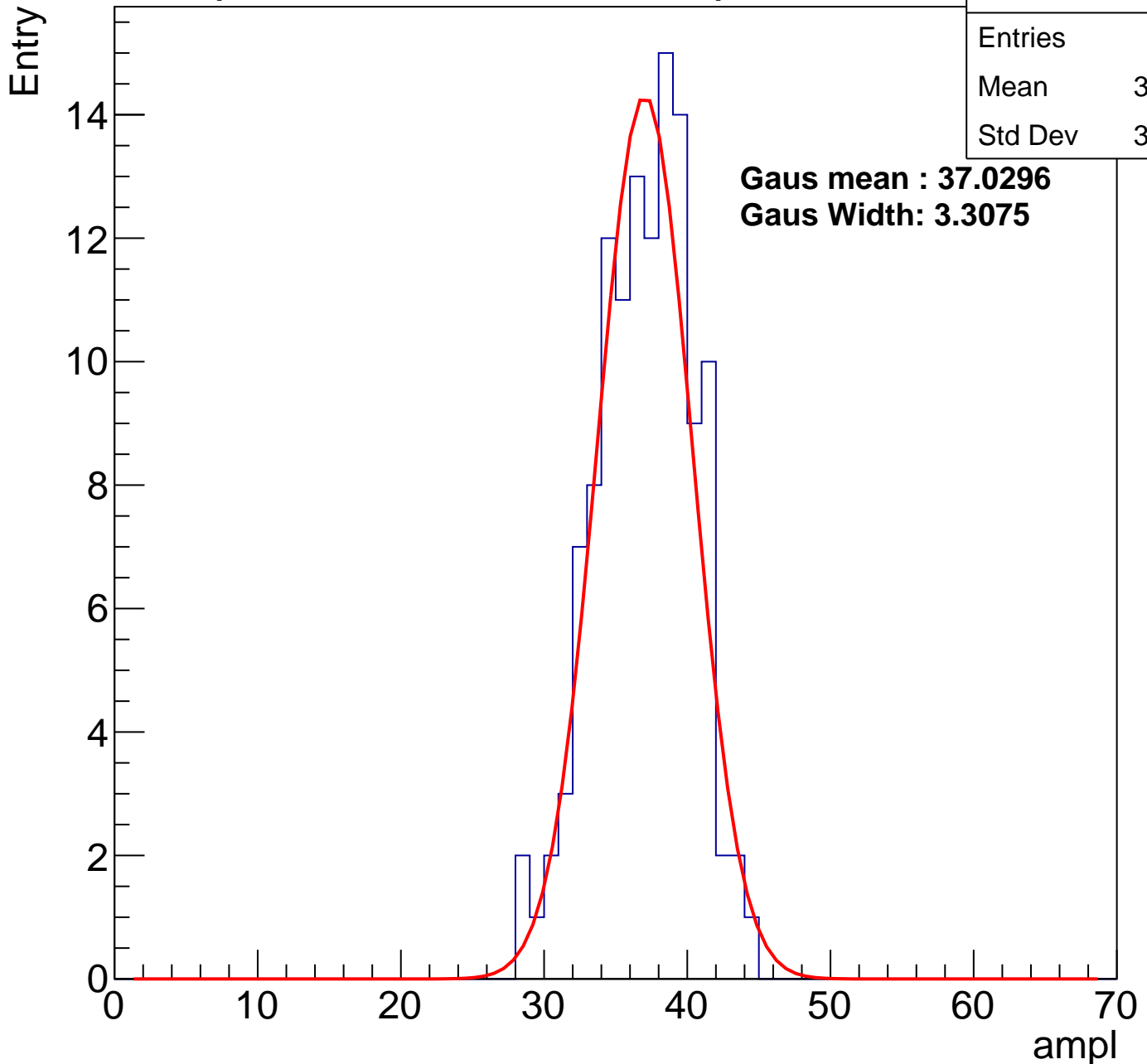
B0L101S, U25-ch85, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.54
Std Dev	3.308

Gaus mean : 37.0296

Gaus Width: 3.3075



B0L101S, U25-ch85, adc2

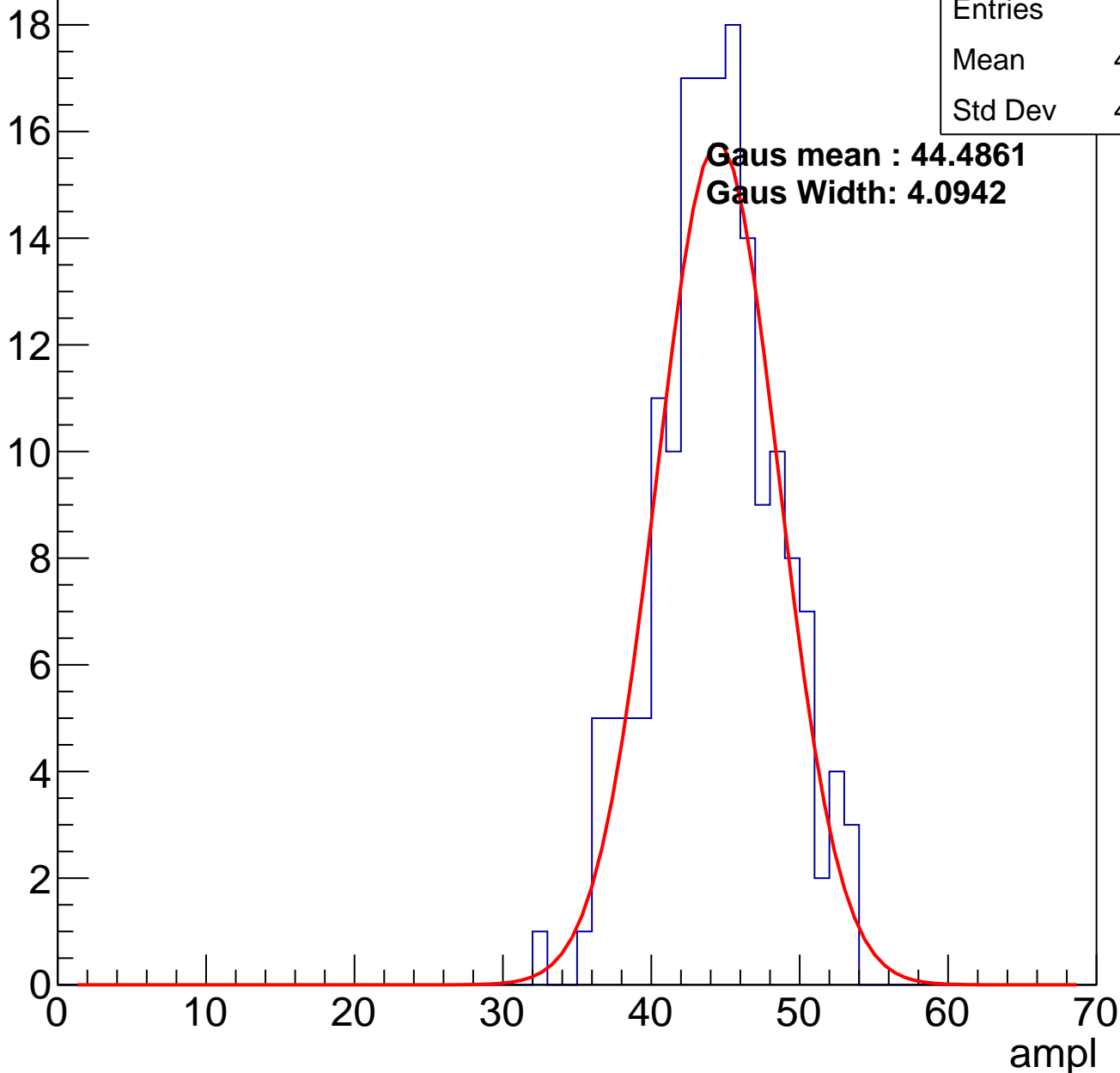
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	169
Mean	43.95
Std Dev	4.077

Gaus mean : 44.4861

Gaus Width: 4.0942



B0L101S, U25-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

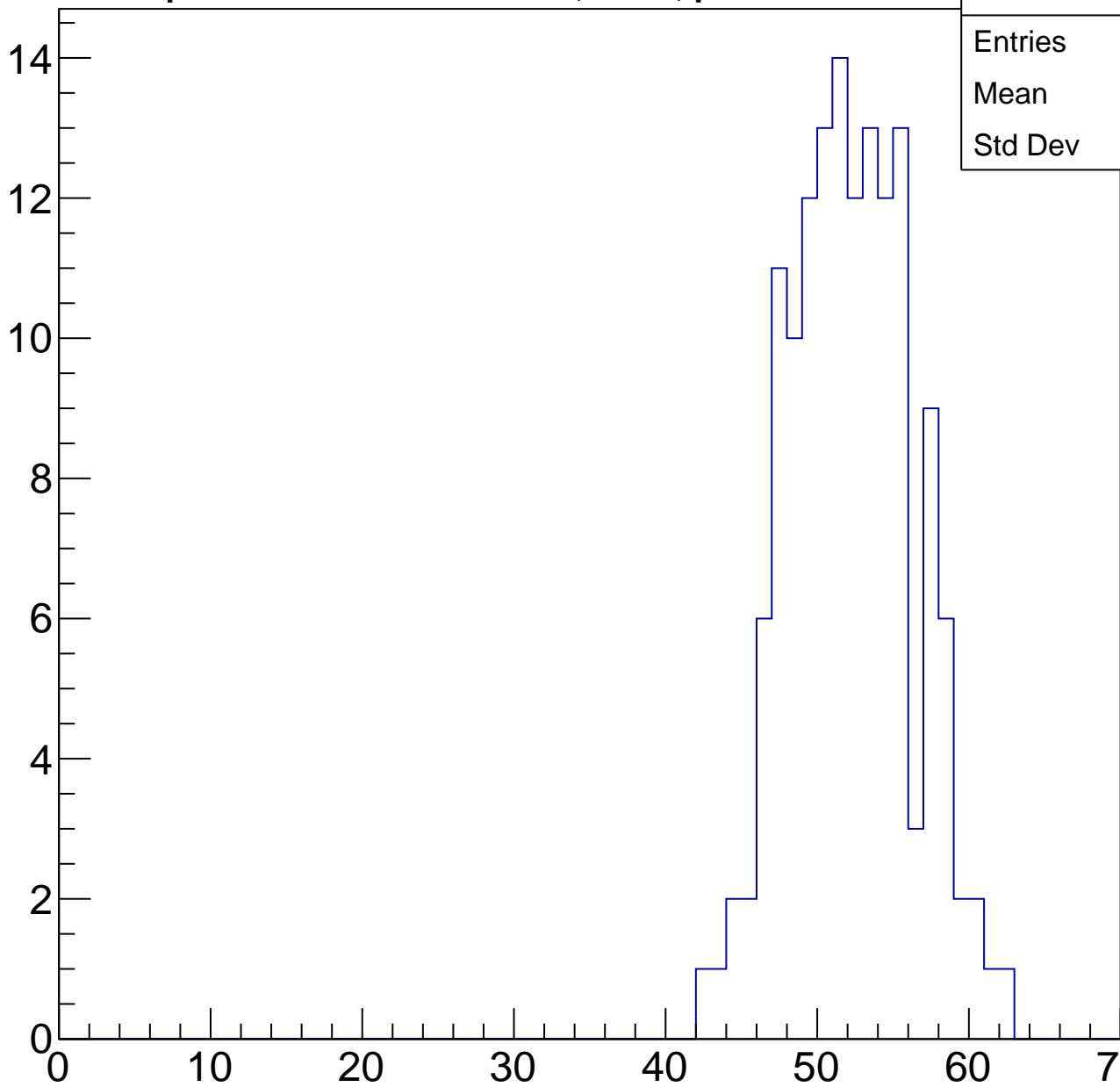
Entries	146
Mean	51.74
Std Dev	3.966

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch85, adc4

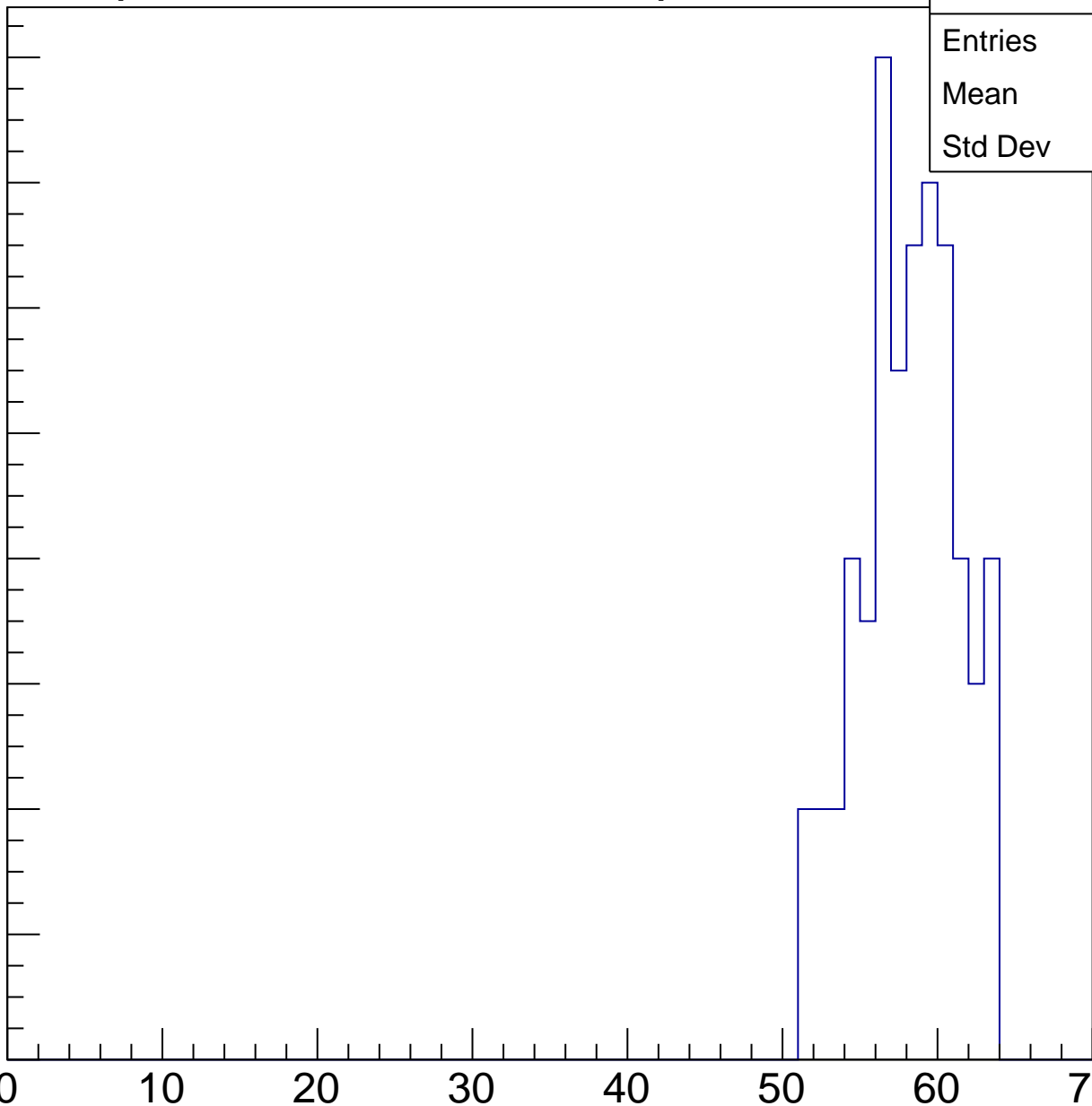
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	57.66
Std Dev	3.116

ampl



B0L101S, U25-ch85, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	46
Mean	58.61
Std Dev	12.59

0

B0L101S, U25-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch86, adc0

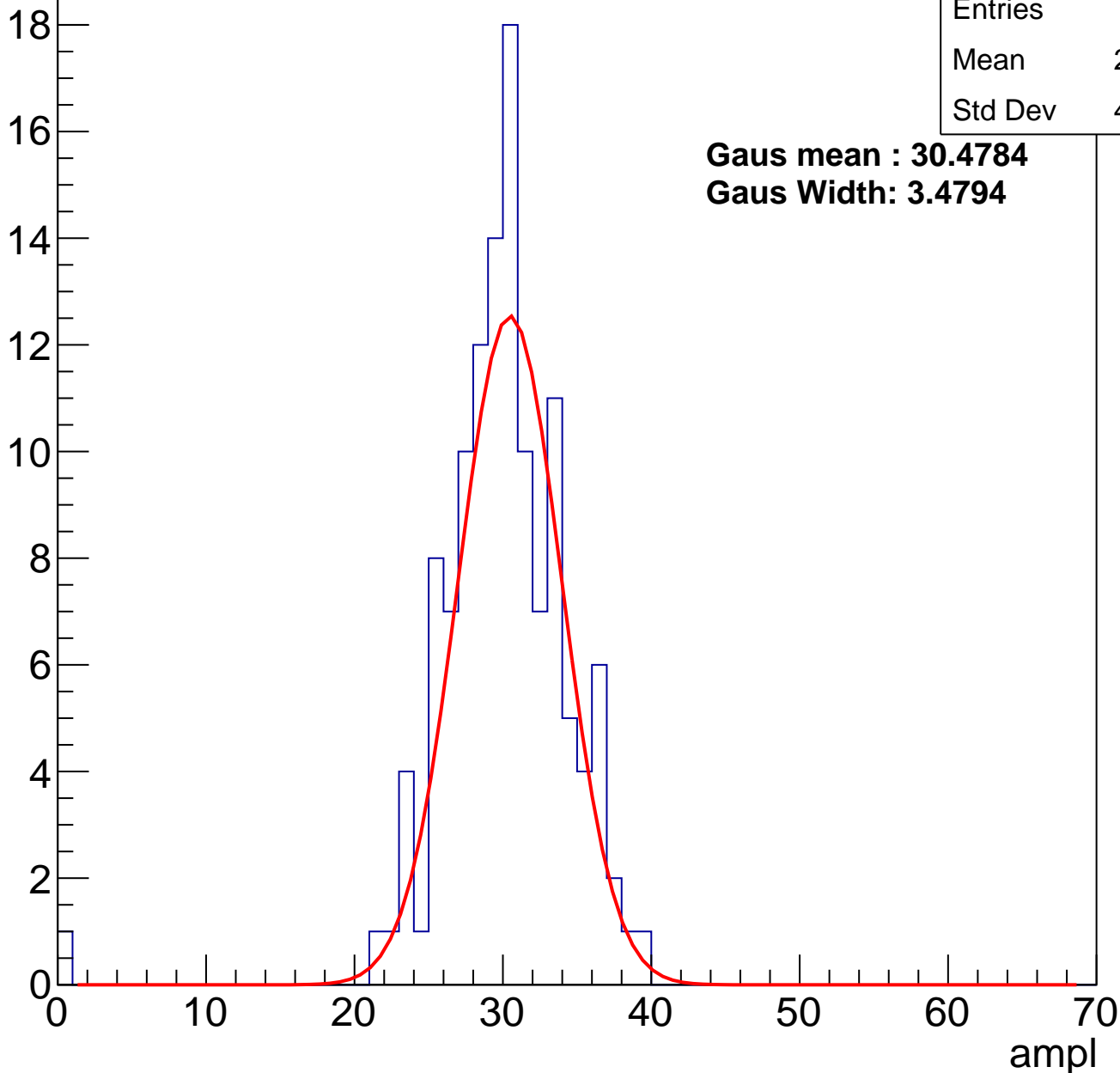
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	29.57
Std Dev	4.485

Gaus mean : 30.4784

Gaus Width: 3.4794

Entry

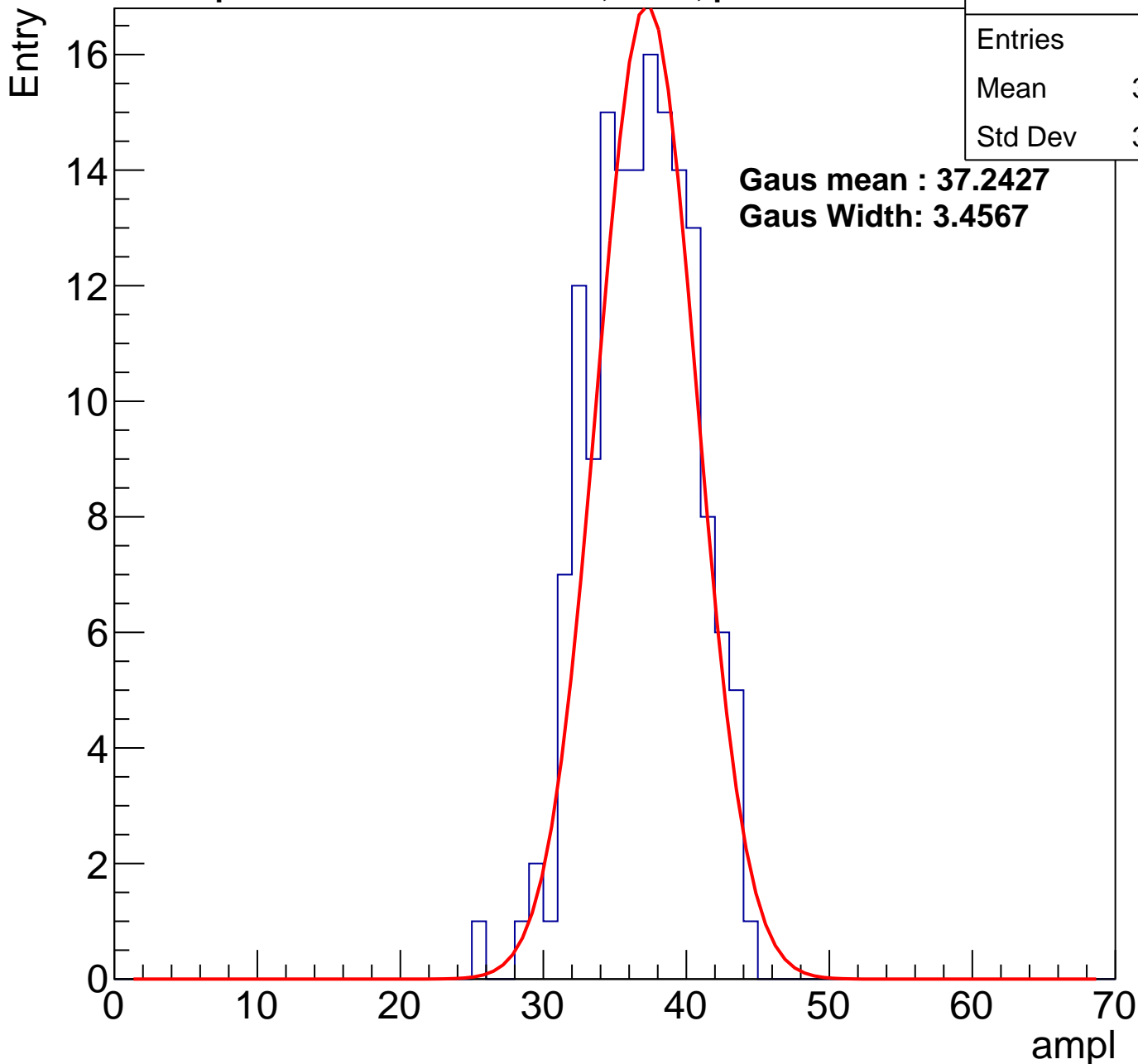


B0L101S, U25-ch86, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	36.43
Std Dev	3.556

Gaus mean : 37.2427
Gaus Width: 3.4567



B0L101S, U25-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

134

Mean

43.91

Std Dev

3.358

Gaus mean : 44.4157

Gaus Width: 3.3056

0

10

20

30

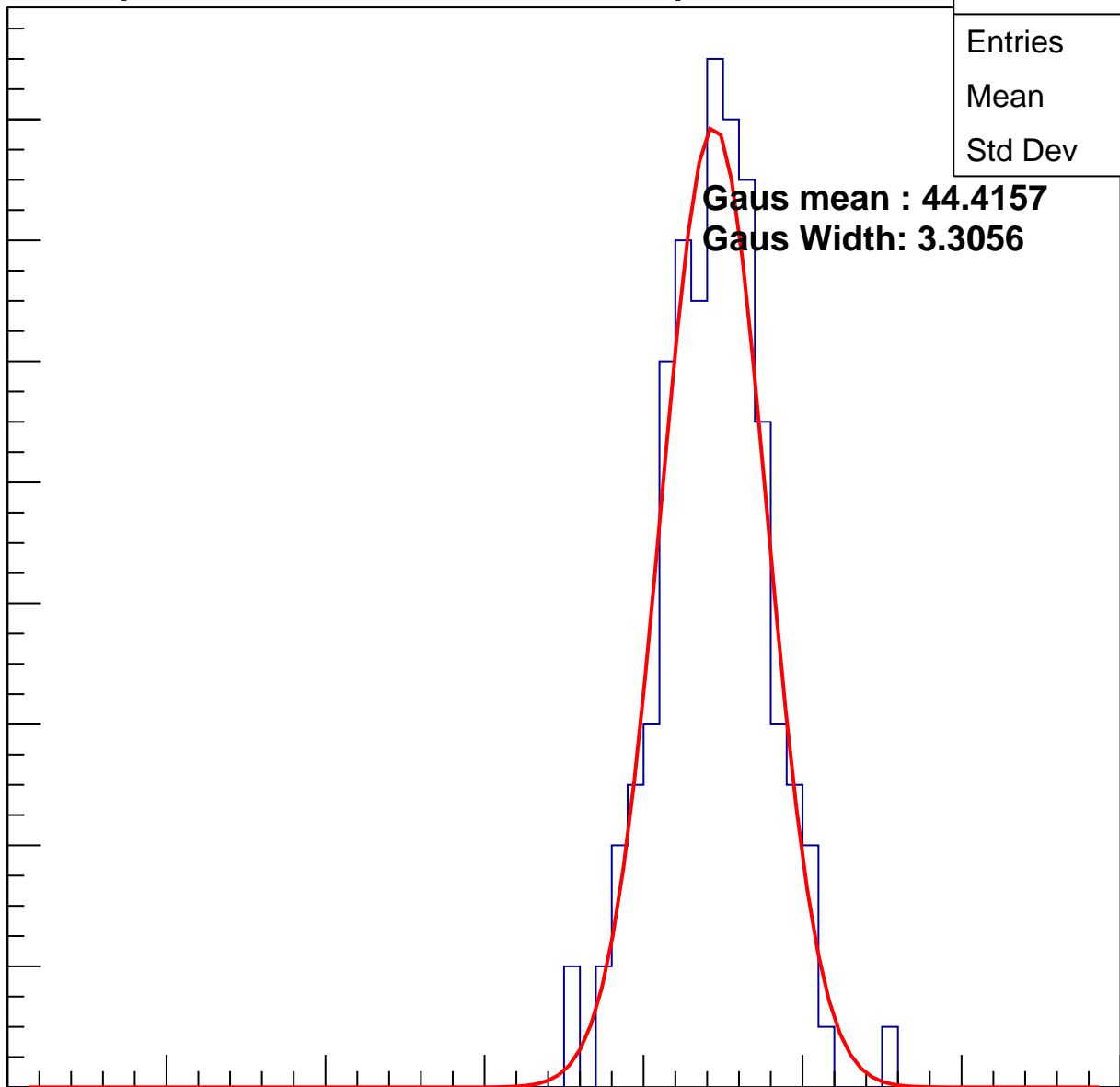
40

50

60

70

ampl

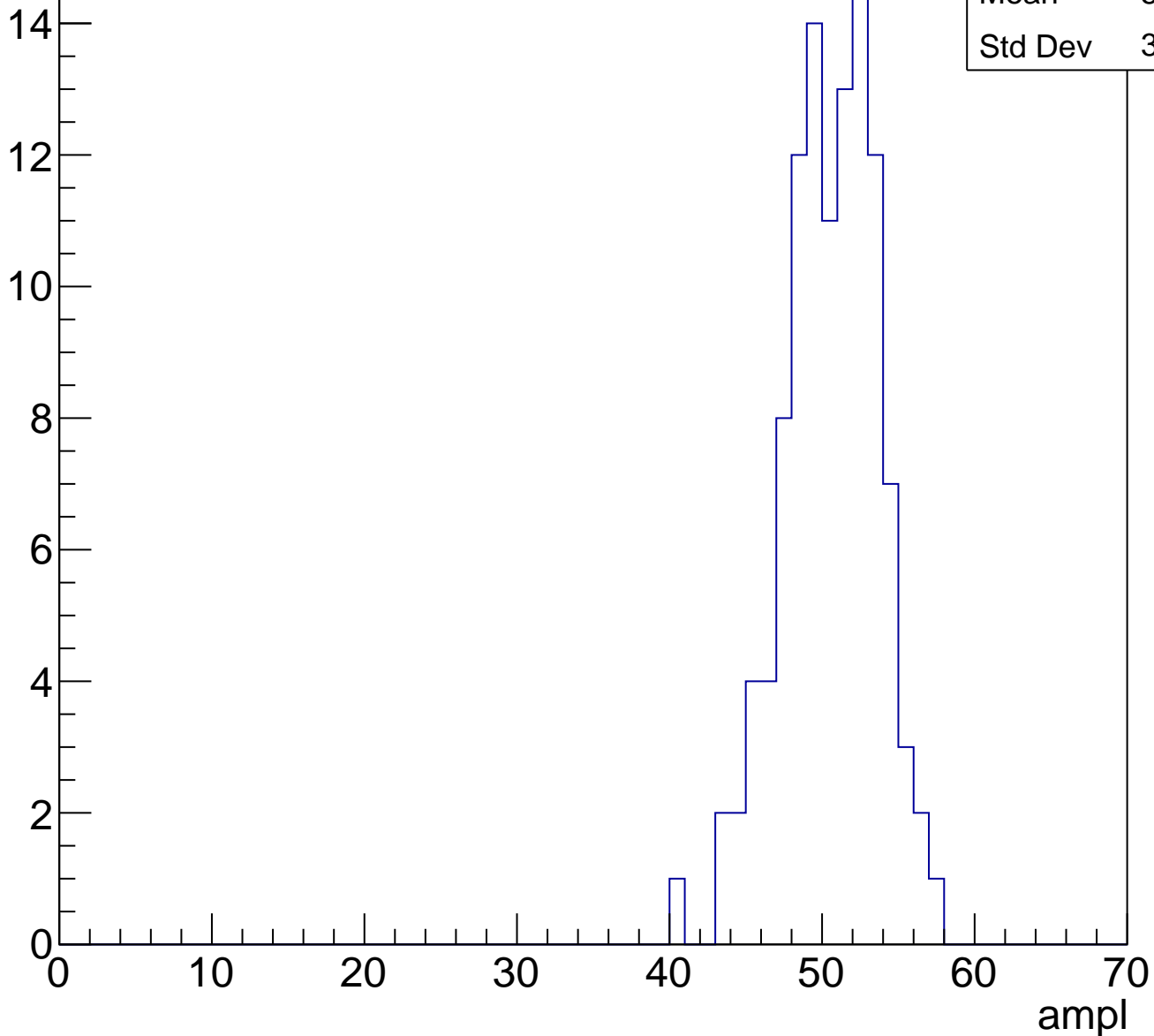


B0L101S, U25-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	50.06
Std Dev	3.085

Entry



B0L101S, U25-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	55.88
Std Dev	5.821

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

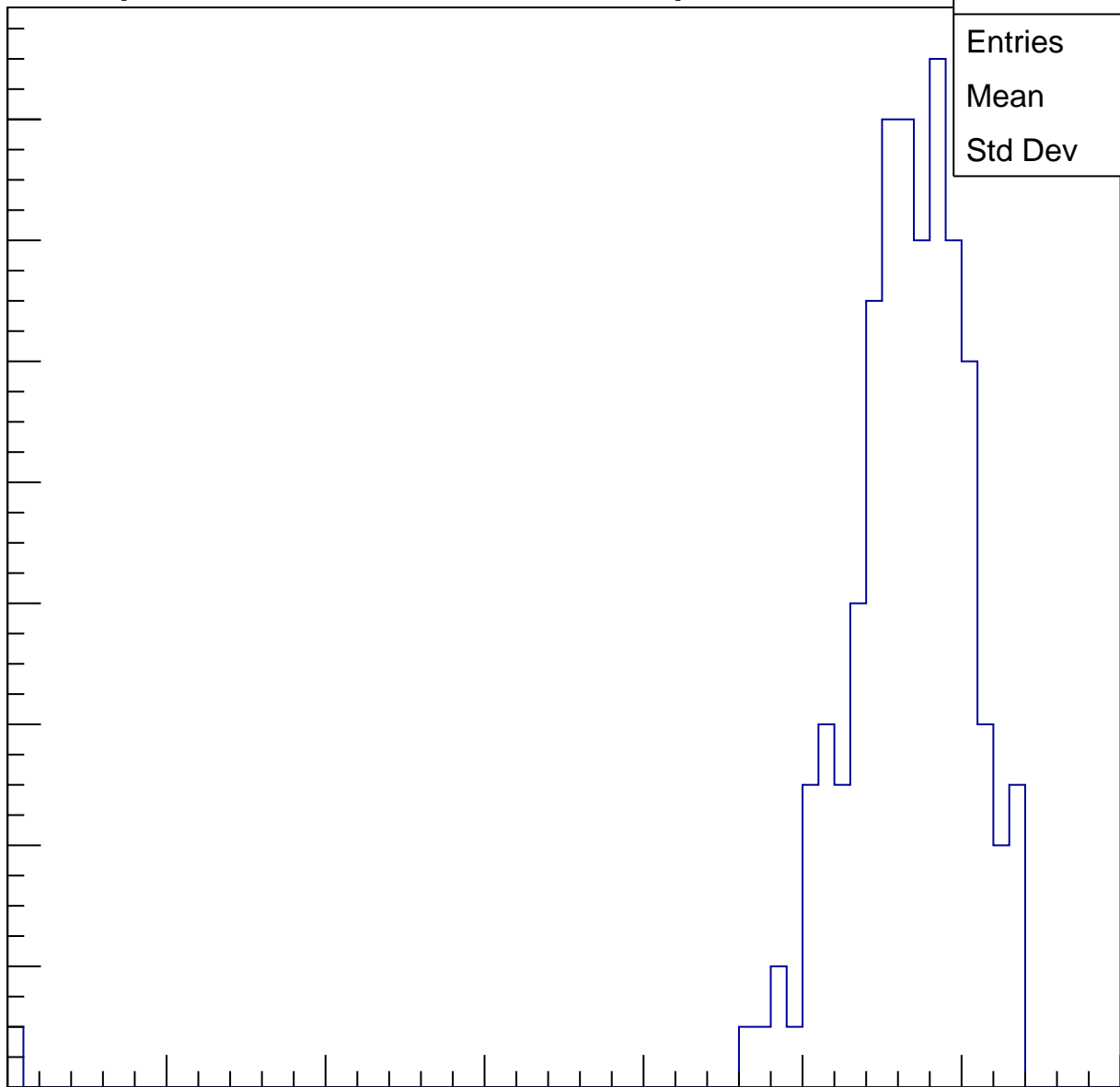
40

50

60

70

ampl



B0L101S, U25-ch86, adc5

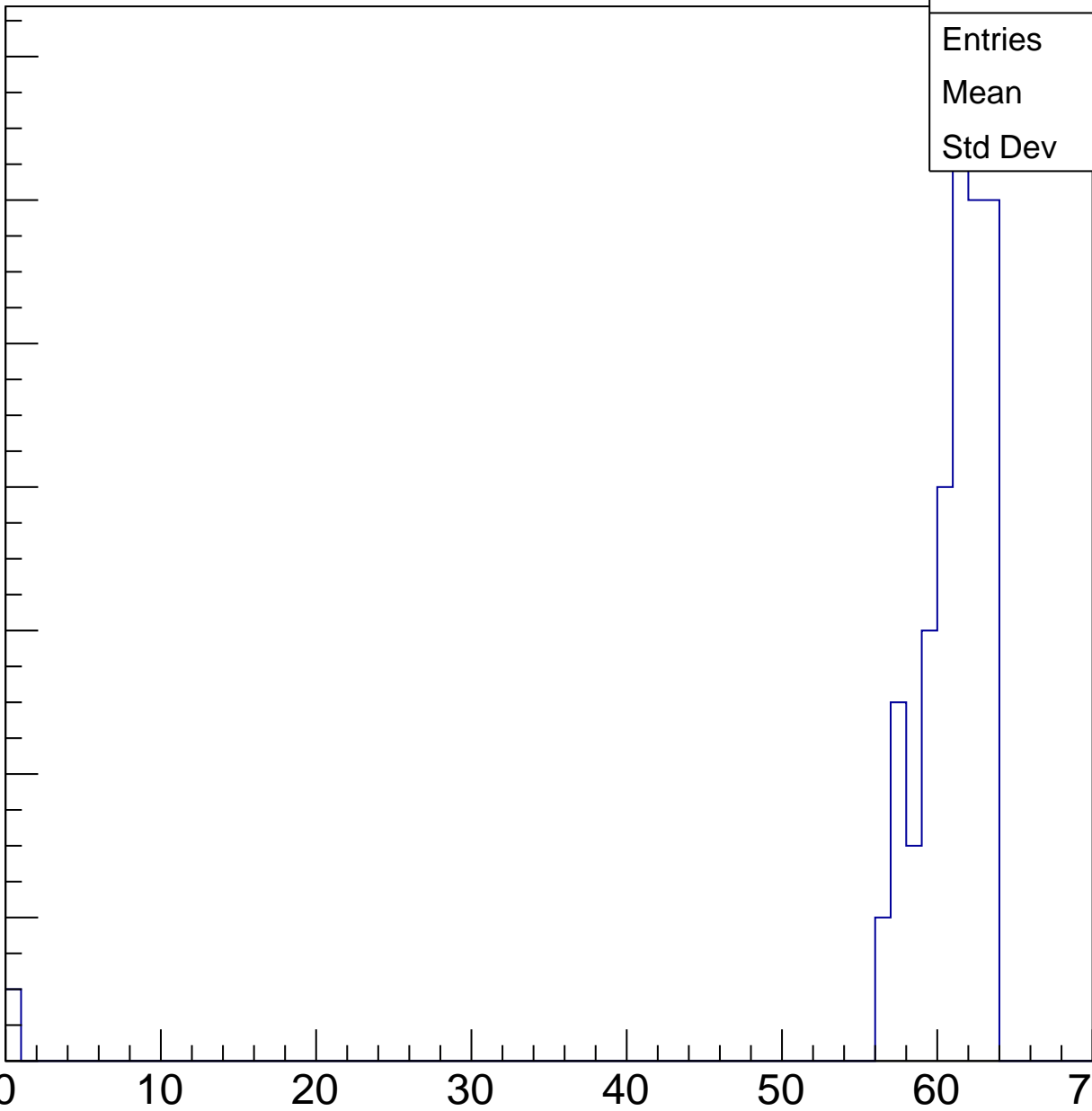
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	63
Mean	59.67
Std Dev	7.825

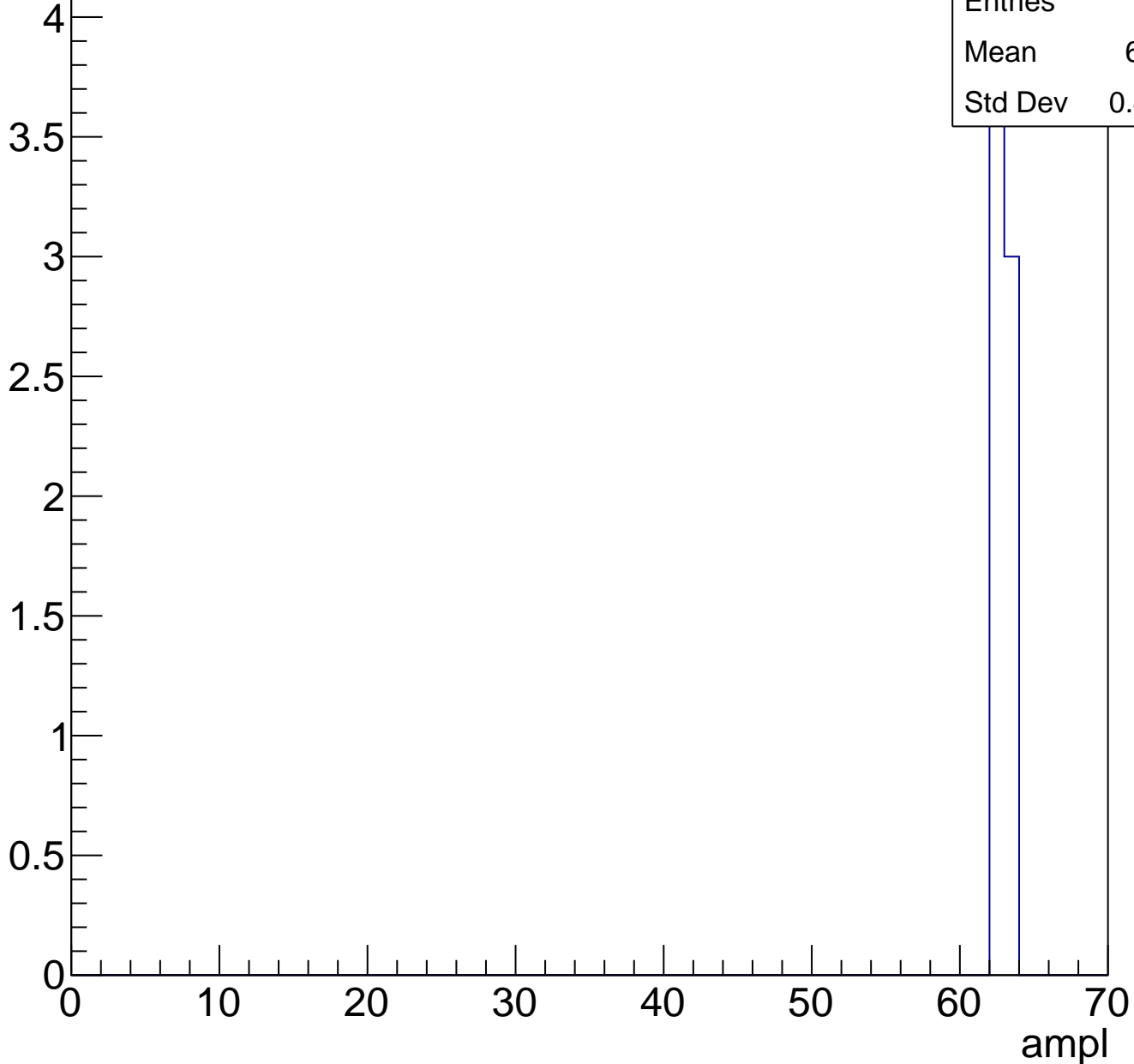
ampl



B0L101S, U25-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

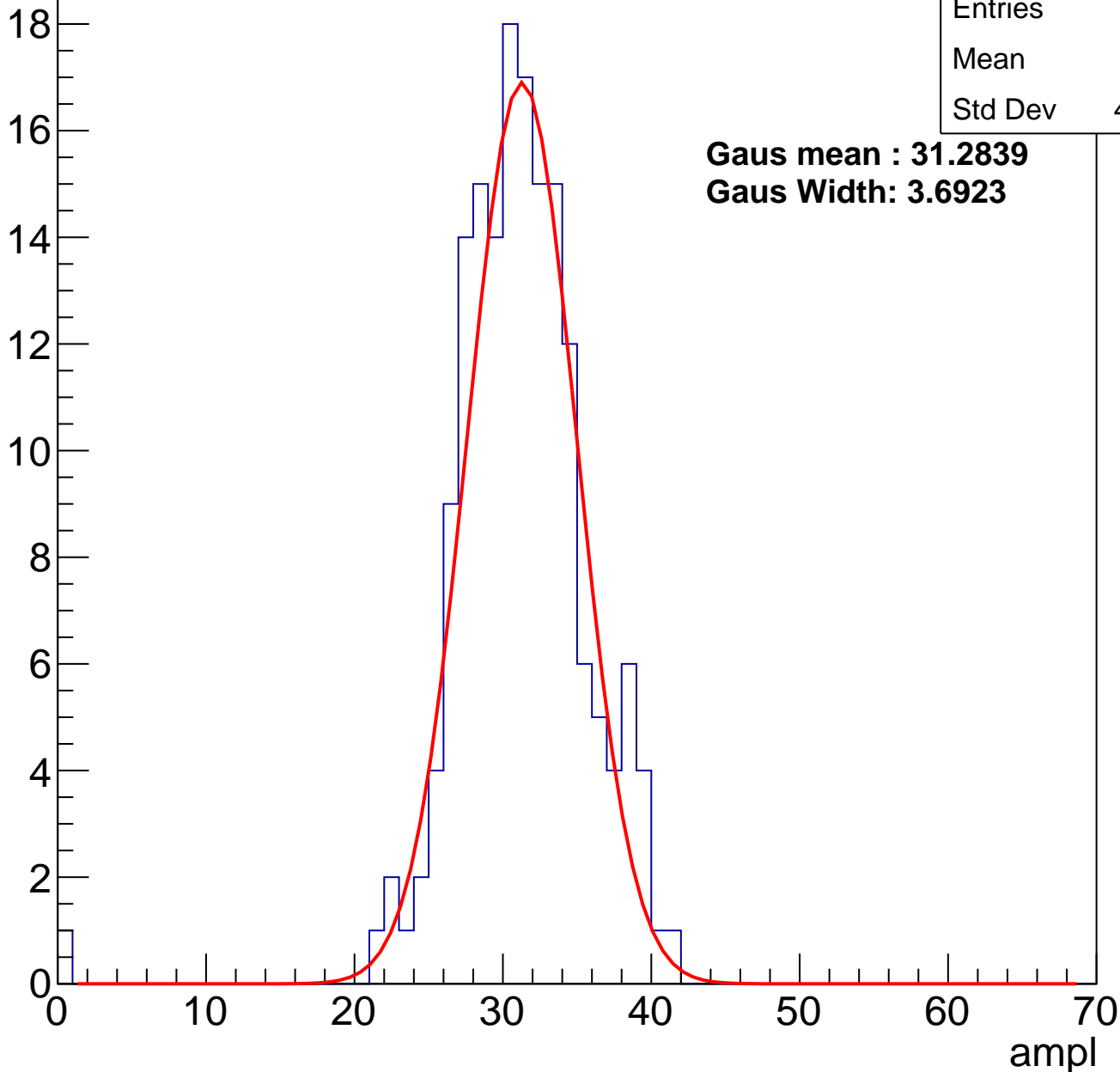


Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch87, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch87, adc1

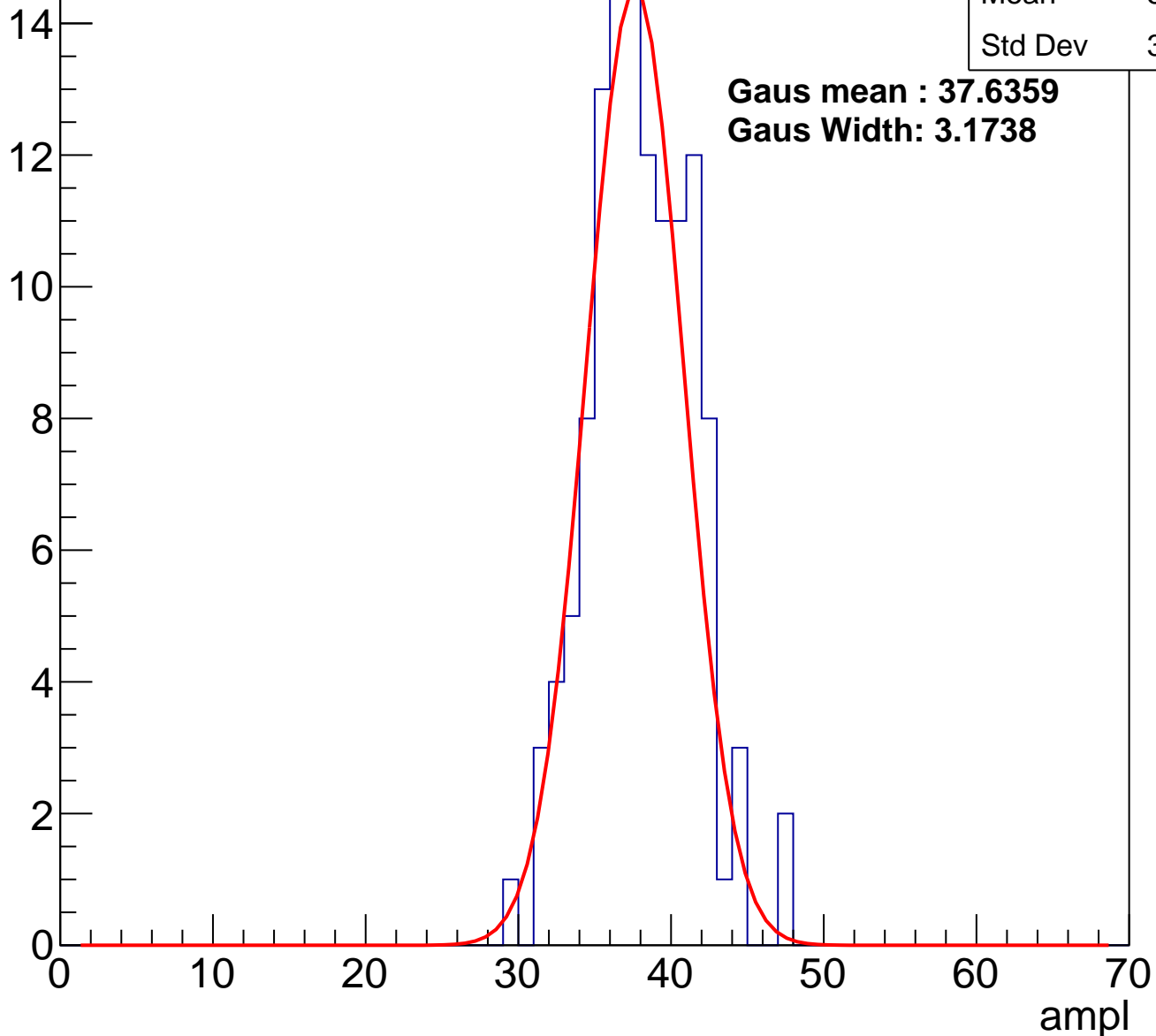
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	37.57
Std Dev	3.336

Gaus mean : 37.6359

Gaus Width: 3.1738

Entry



B0L101S, U25-ch87, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

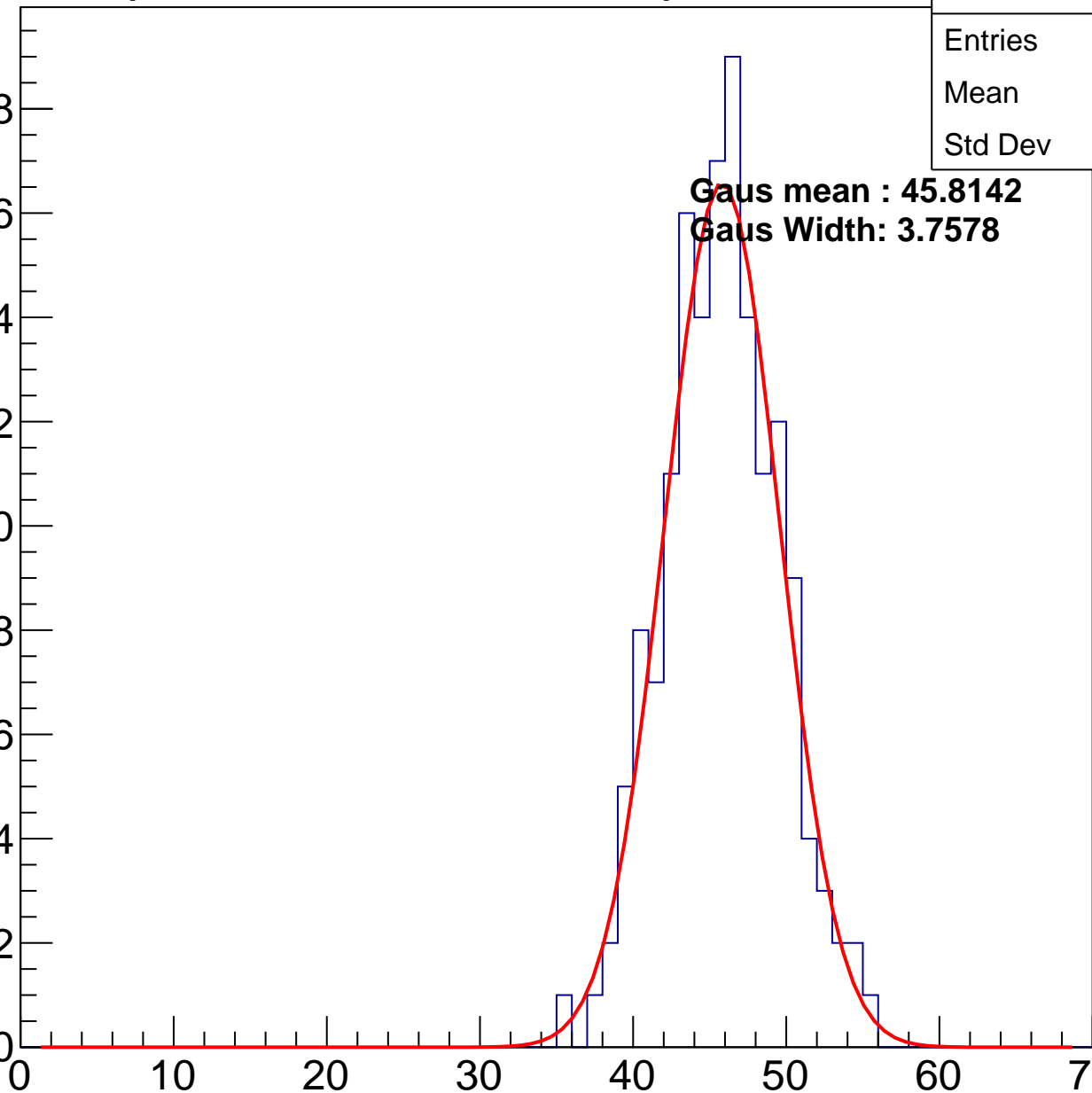
18
16
14
12
10
8
6
4
2
0

Entries	159
Mean	45.33
Std Dev	3.707

Gaus mean : 45.8142

Gaus Width: 3.7578

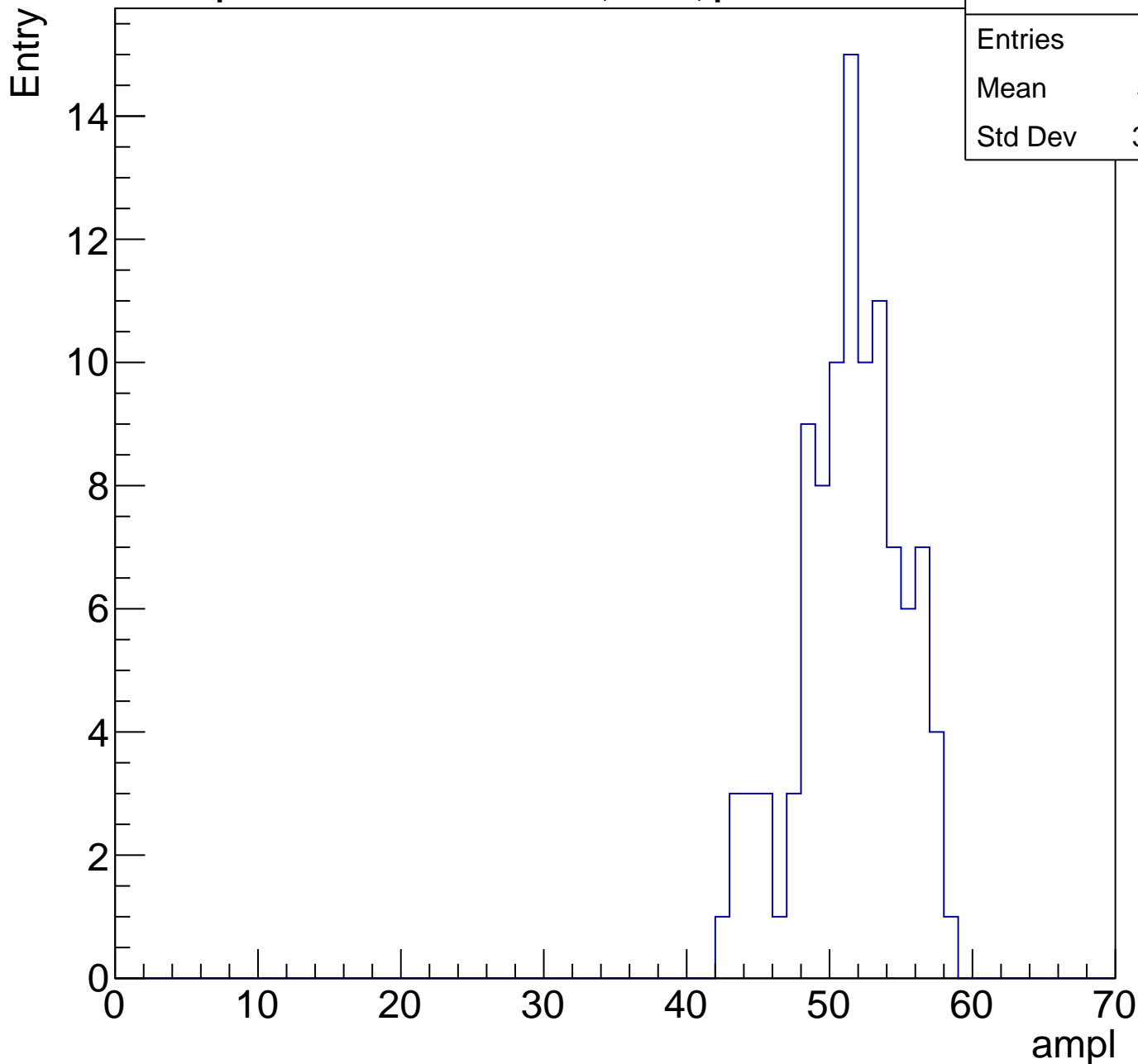
ampl



B0L101S, U25-ch87, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	51.01
Std Dev	3.577



B0L101S, U25-ch87, adc4

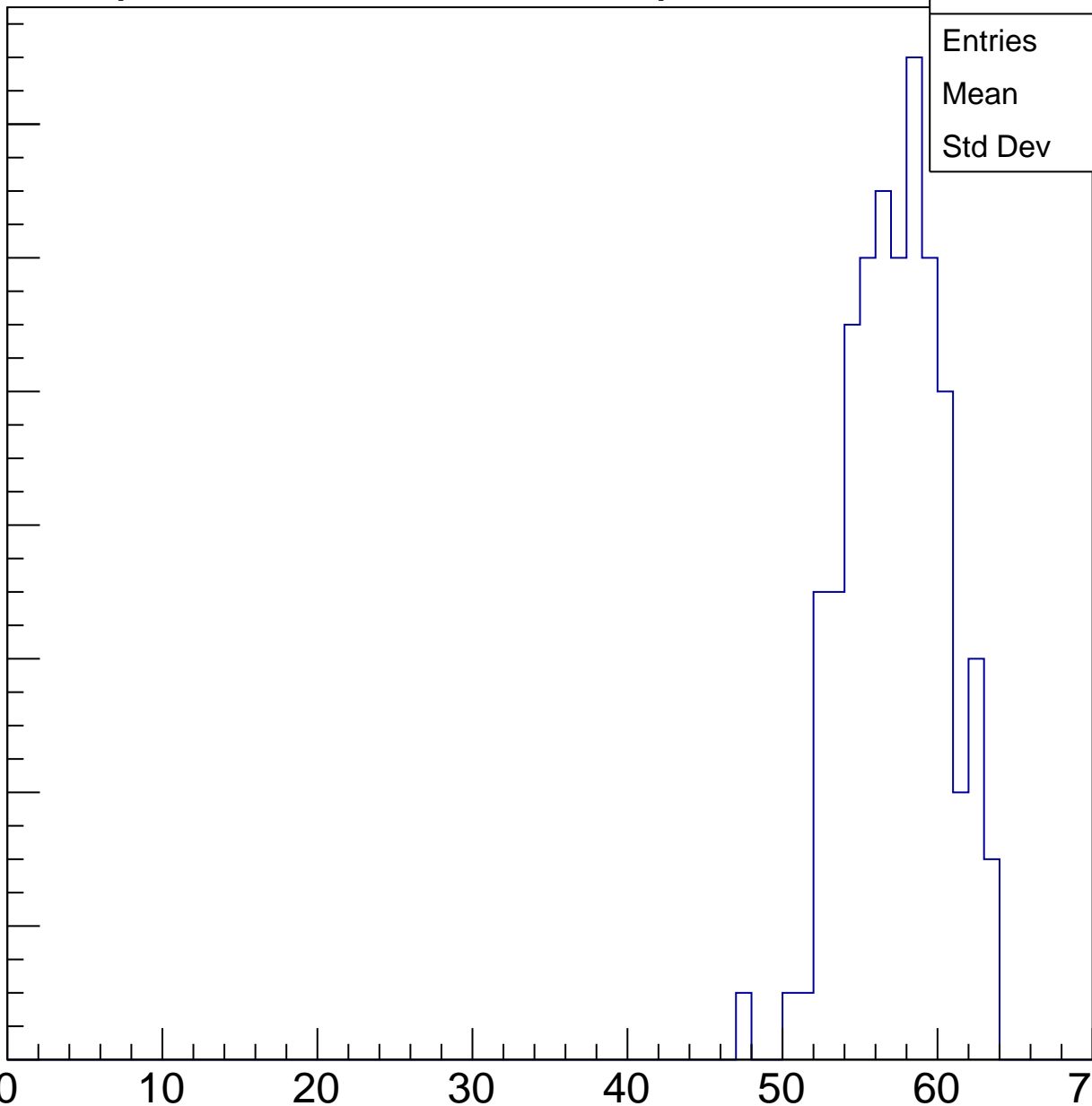
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	56.8
Std Dev	3.096

ampl



B0L101S, U25-ch87, adc5

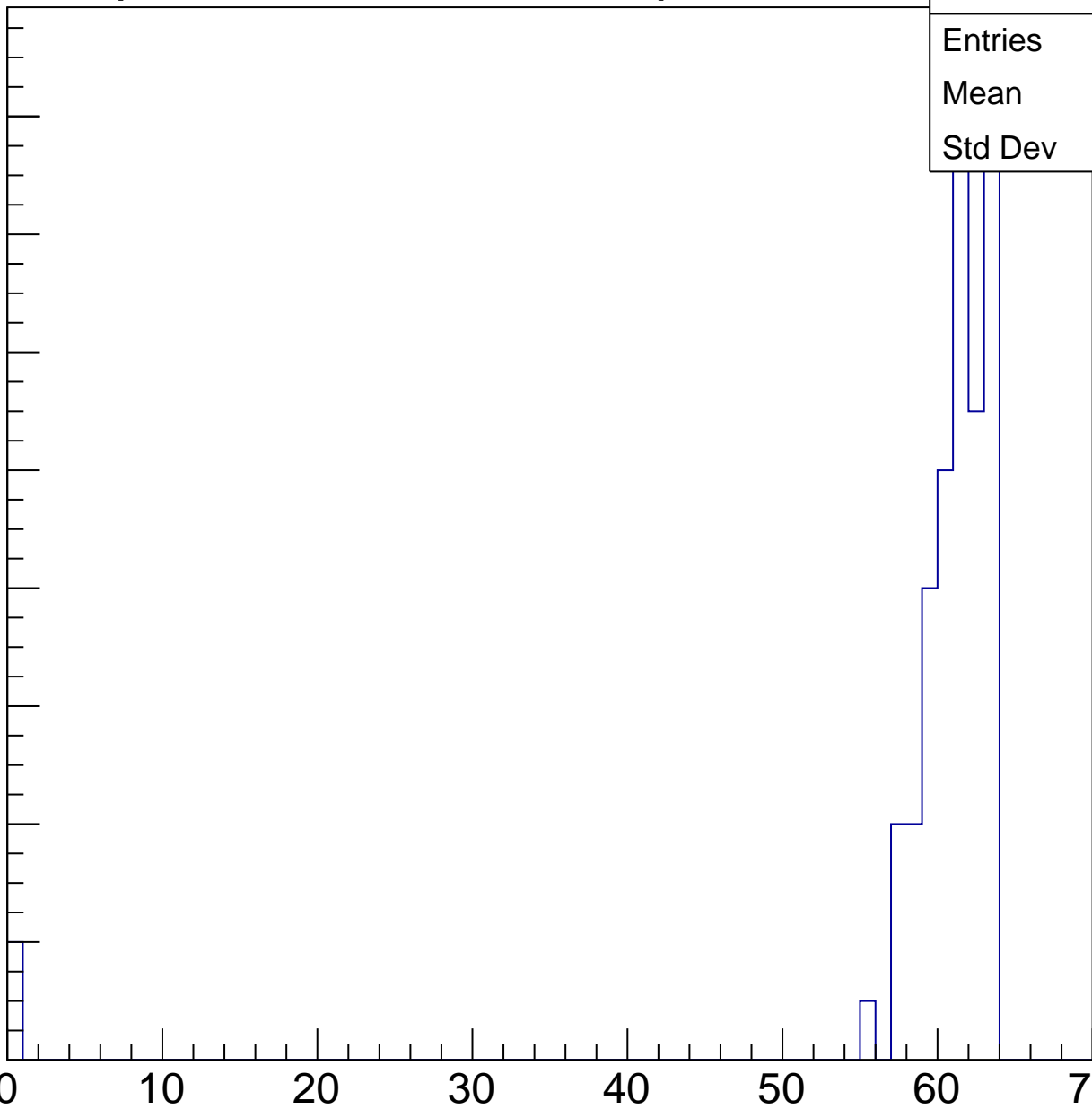
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	59.12
Std Dev	10.1

ampl



B0L101S, U25-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3
2.5
2
1.5
1
0.5
0

Entries	3
Mean	62
Std Dev	0

ampl

B0L101S, U25-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch88, adc0

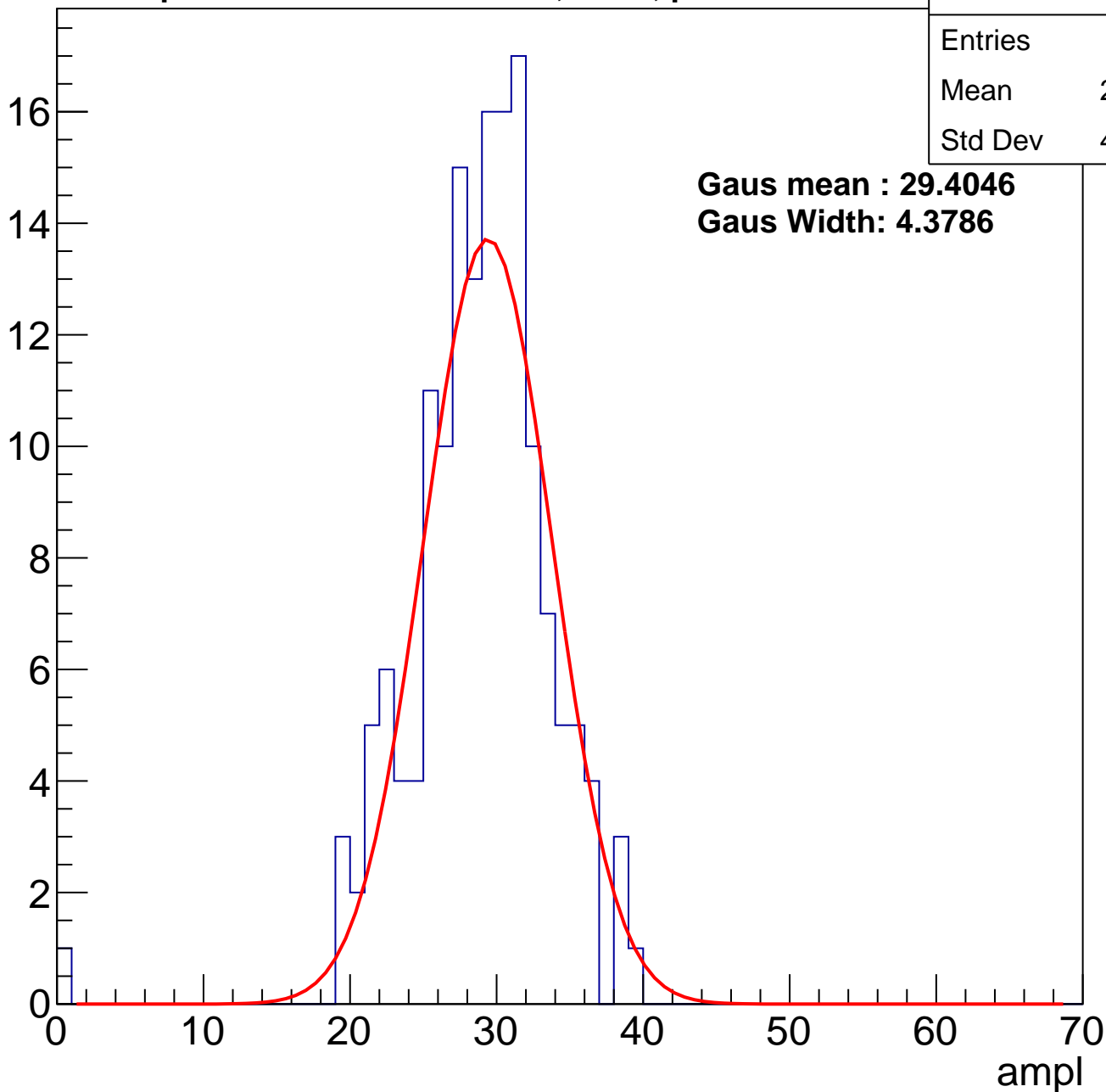
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	28.42
Std Dev	4.758

Gaus mean : 29.4046

Gaus Width: 4.3786

Entry



B0L101S, U25-ch88, adc1

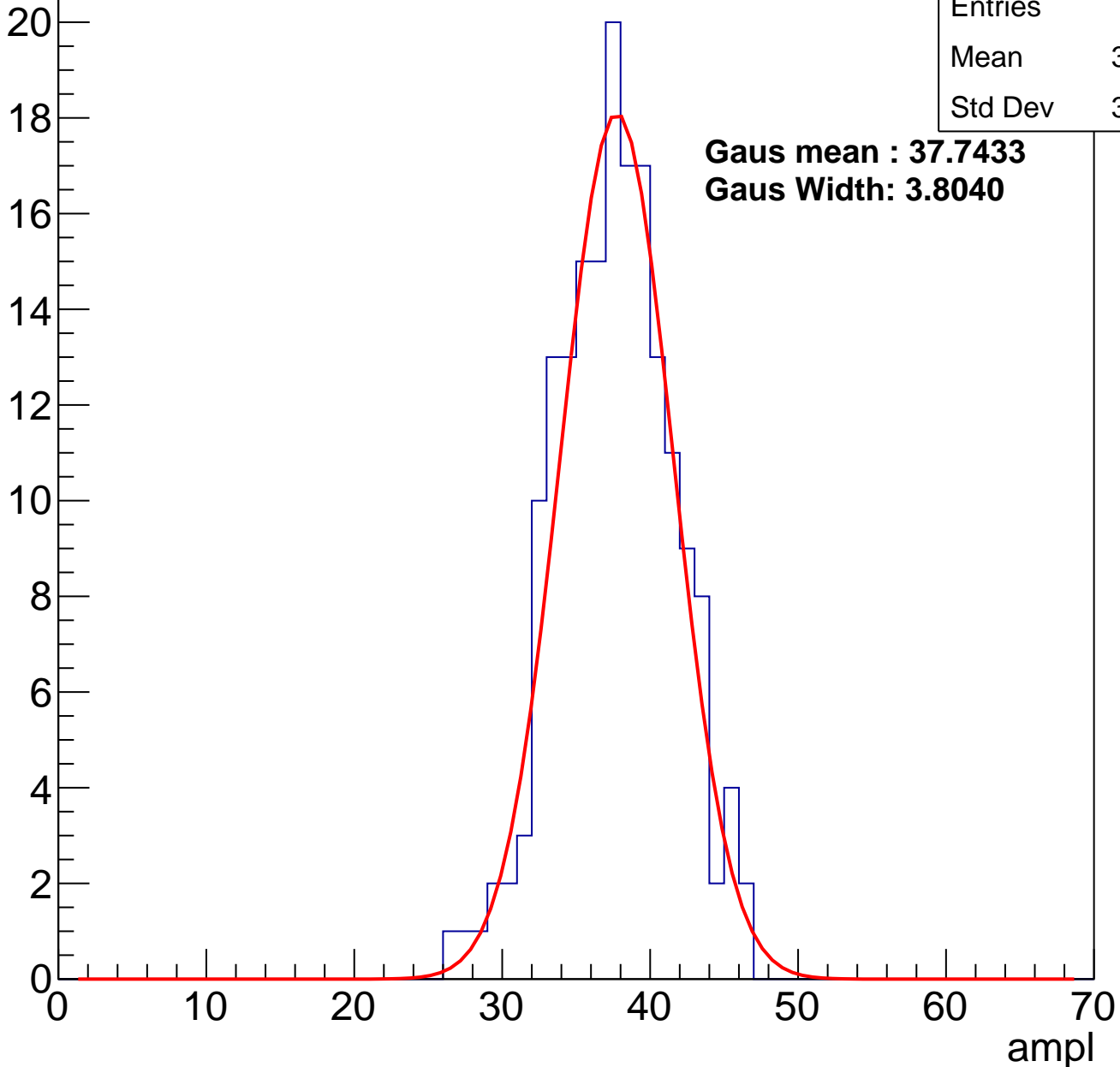
calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	37.15
Std Dev	3.876

Gaus mean : 37.7433

Gaus Width: 3.8040

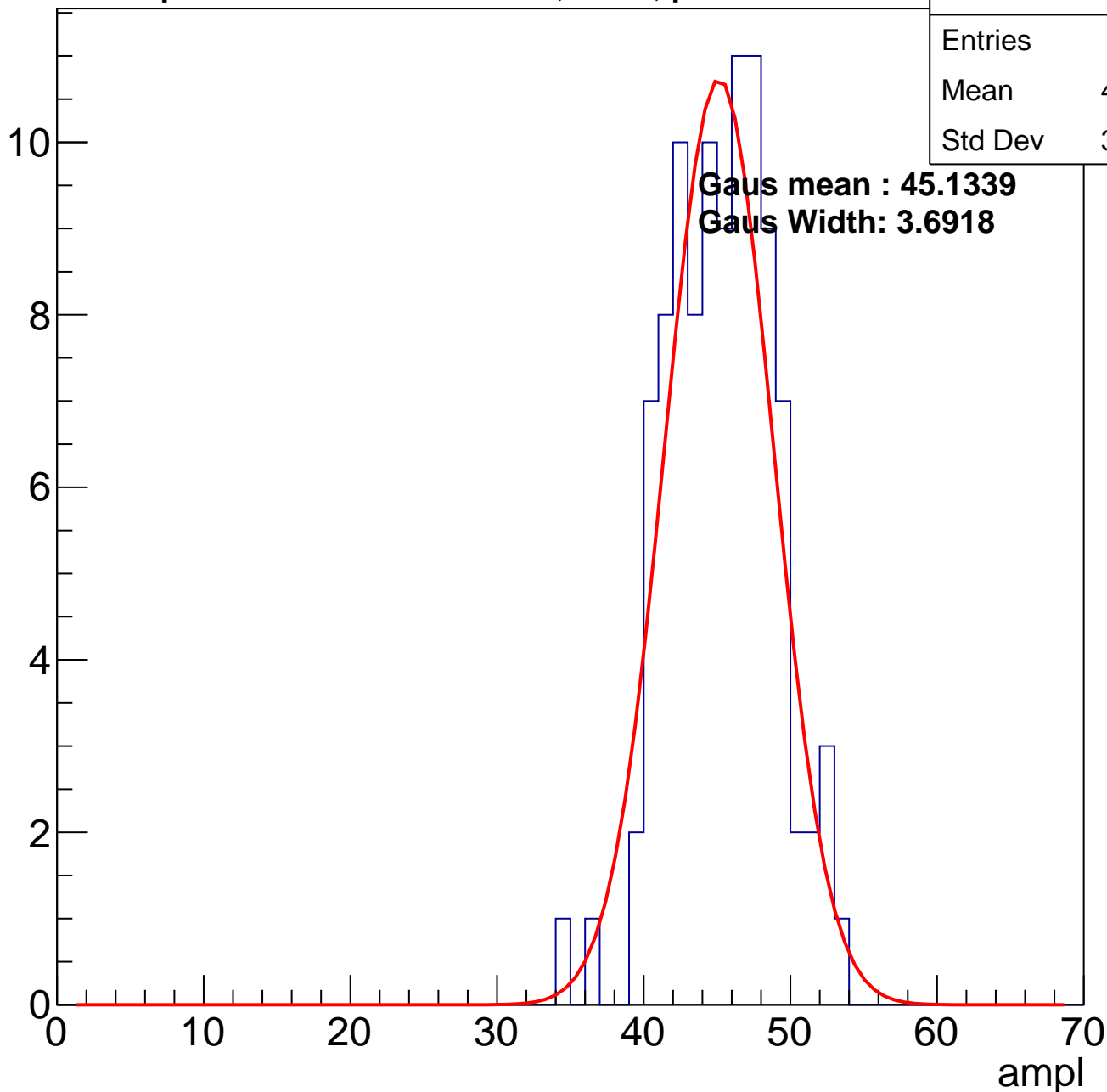
Entry



B0L101S, U25-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

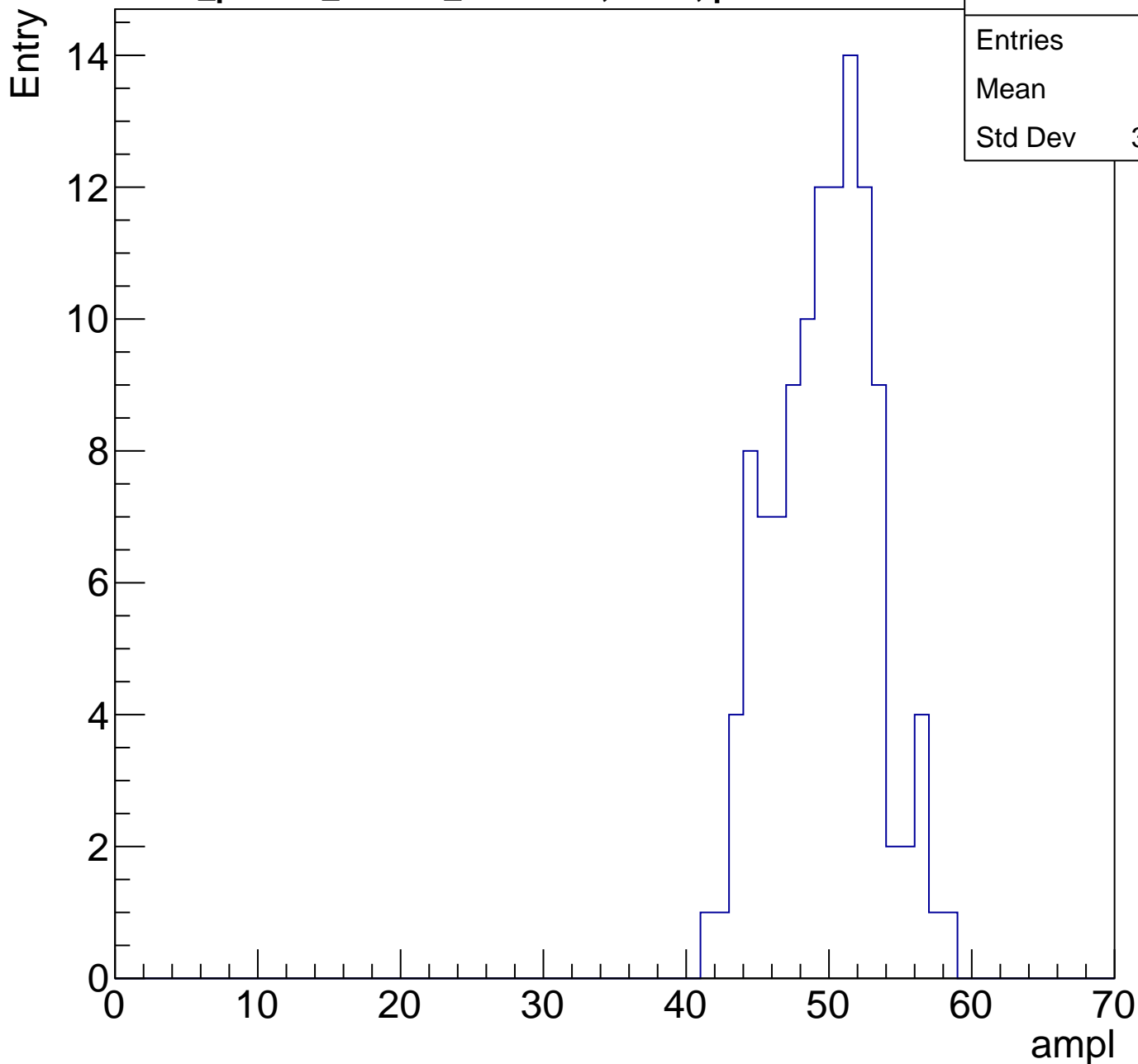
Entry



B0L101S, U25-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

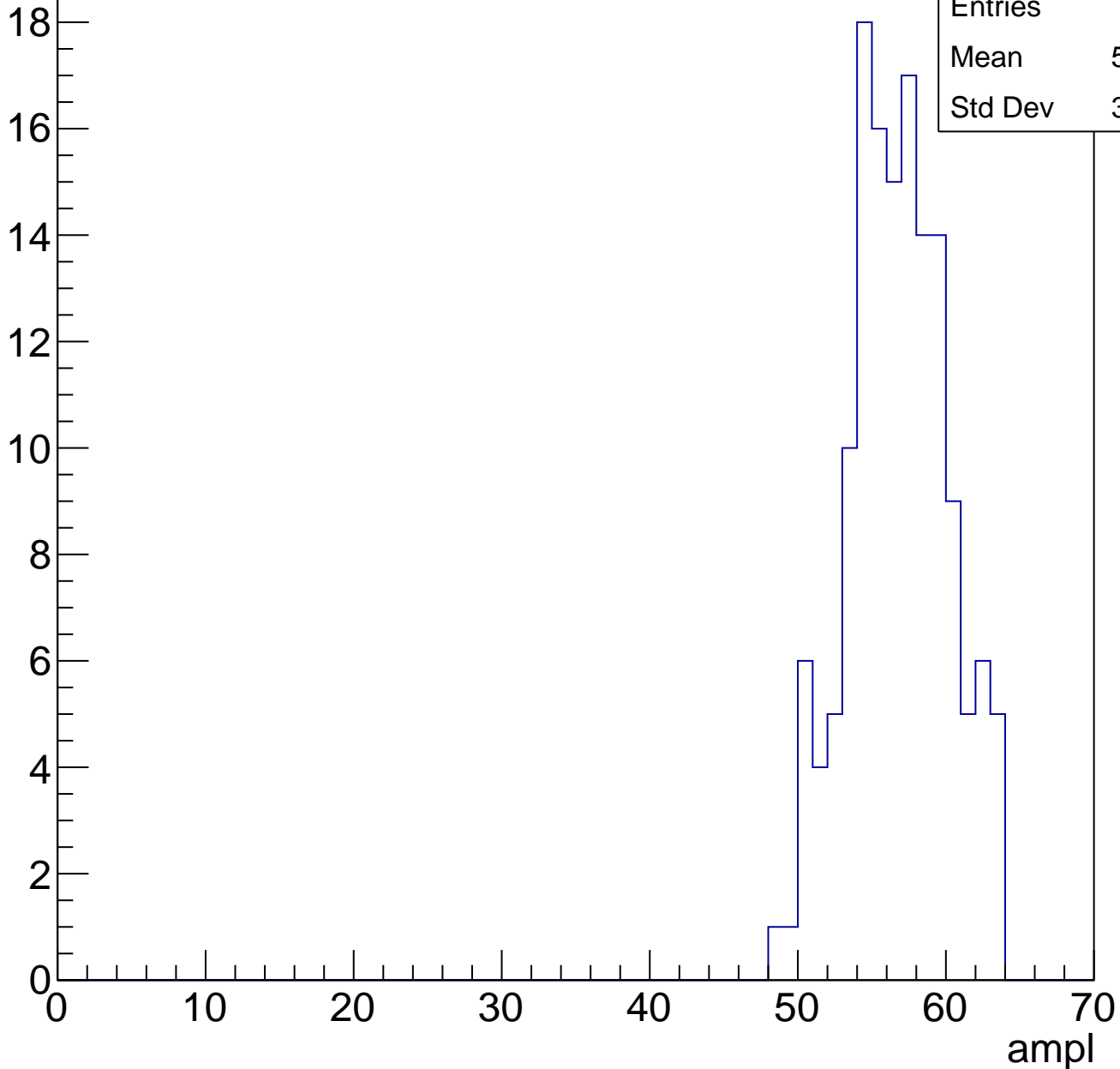
Entries	116
Mean	49.2
Std Dev	3.543



B0L101S, U25-ch88, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	146
Mean	56.32
Std Dev	3.312

B0L101S, U25-ch88, adc5

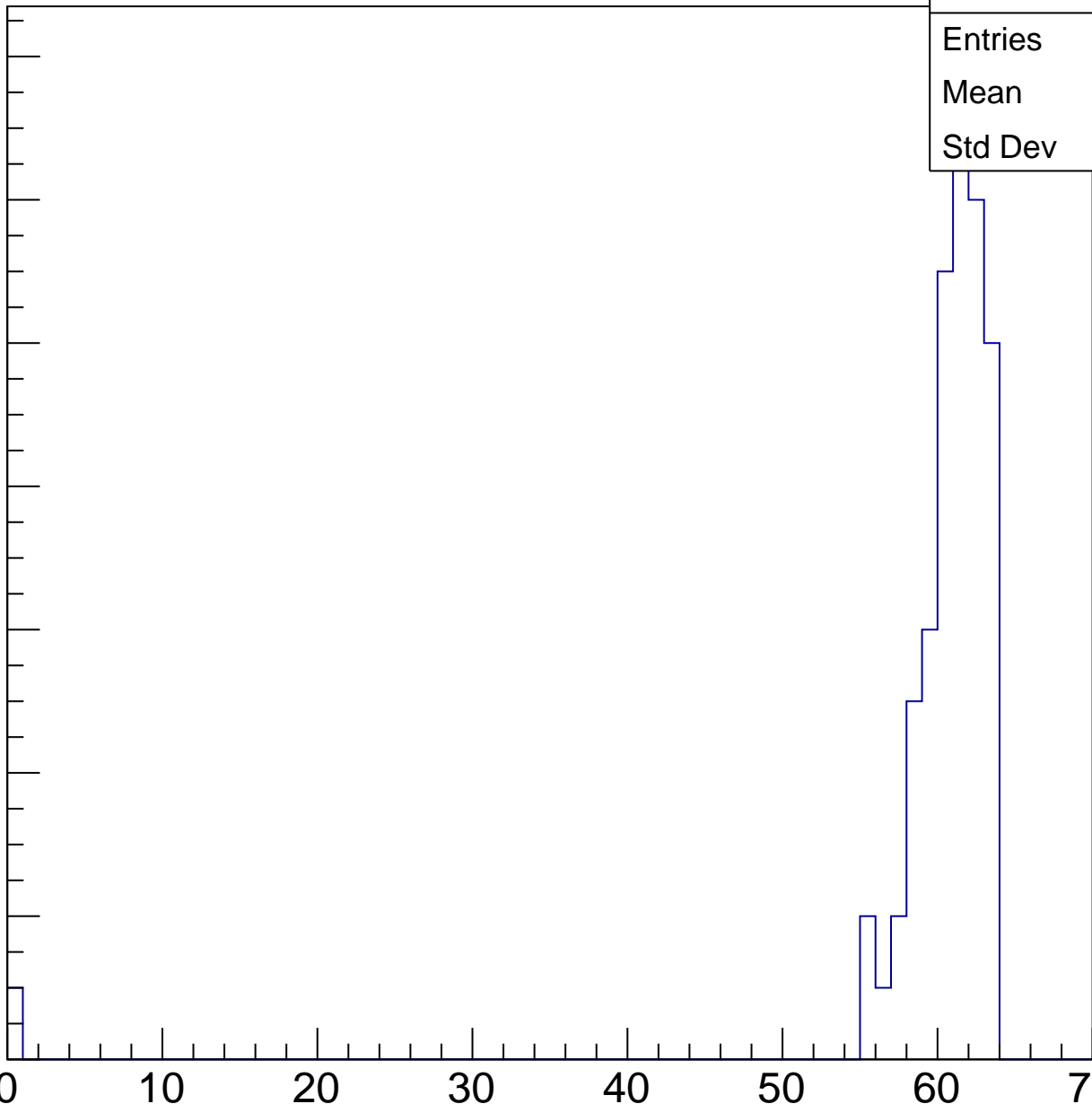
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	64
Mean	59.56
Std Dev	7.756

ampl

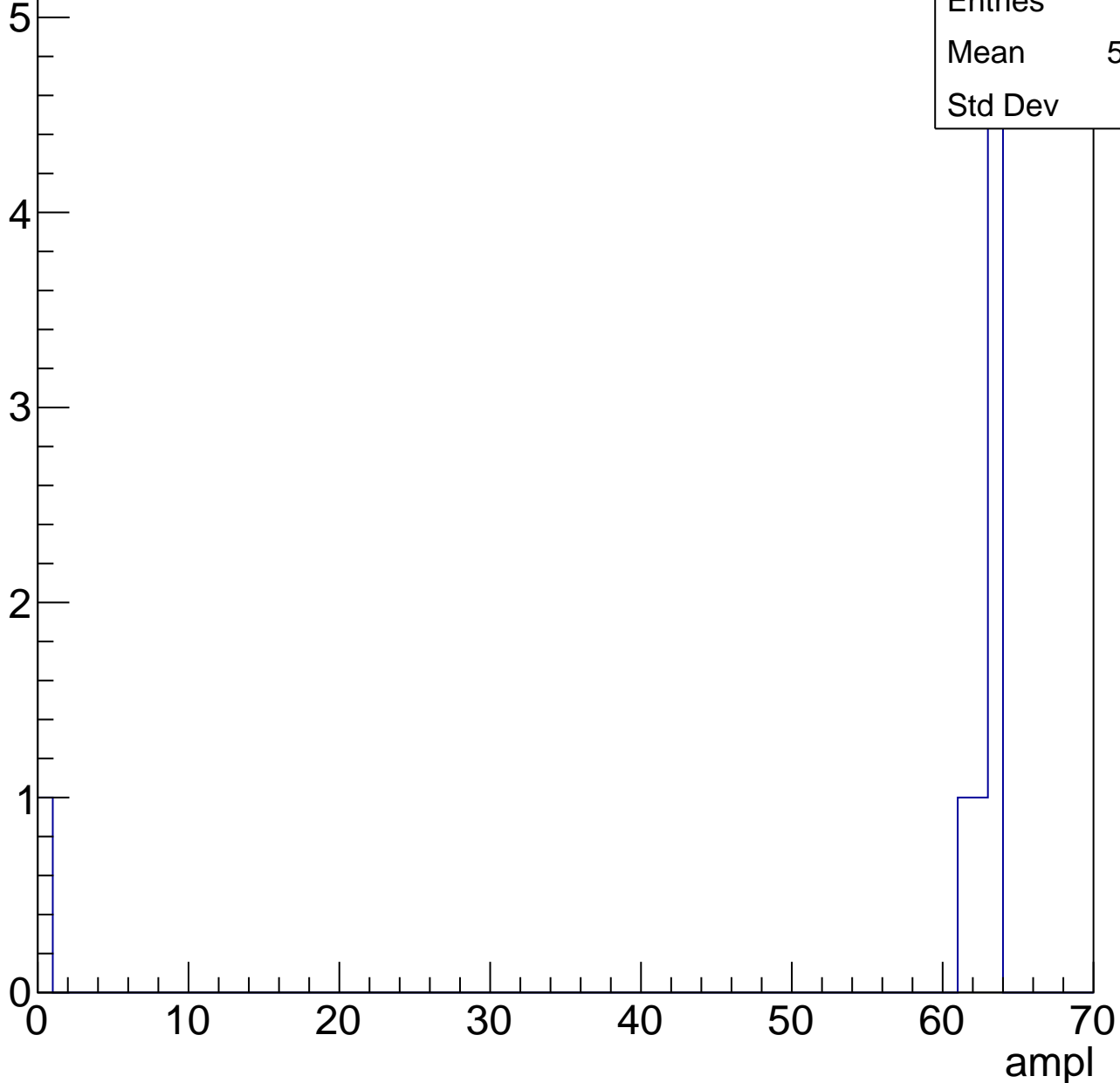


B0L101S, U25-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	54.75
Std Dev	20.7



B0L101S, U25-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch89, adc0

calib_packv5_042523_0143.root, FC#1, port C1

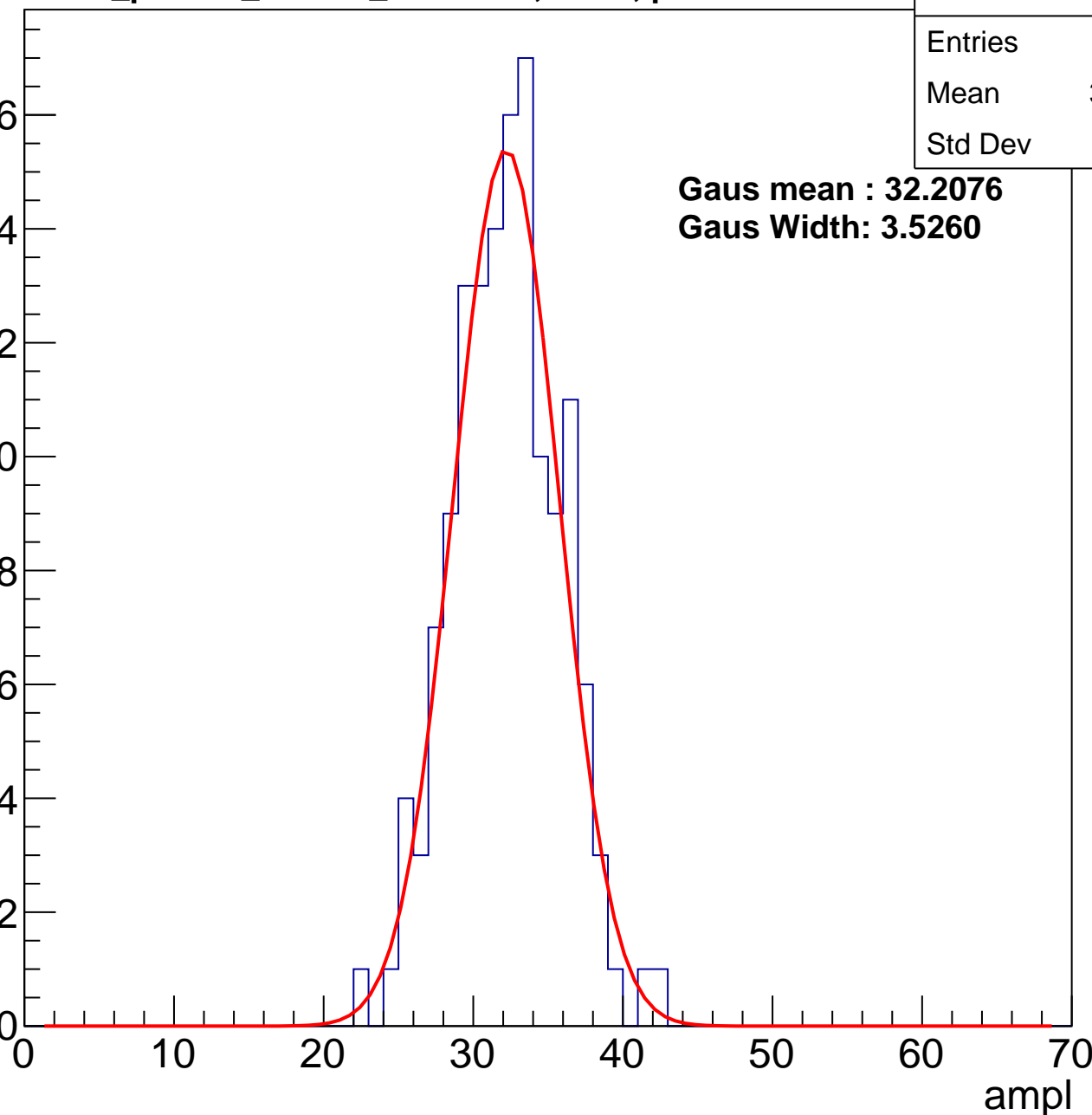
Entry

16
14
12
10
8
6
4
2
0

Entries	140
Mean	31.77
Std Dev	3.55

Gaus mean : 32.2076

Gaus Width: 3.5260



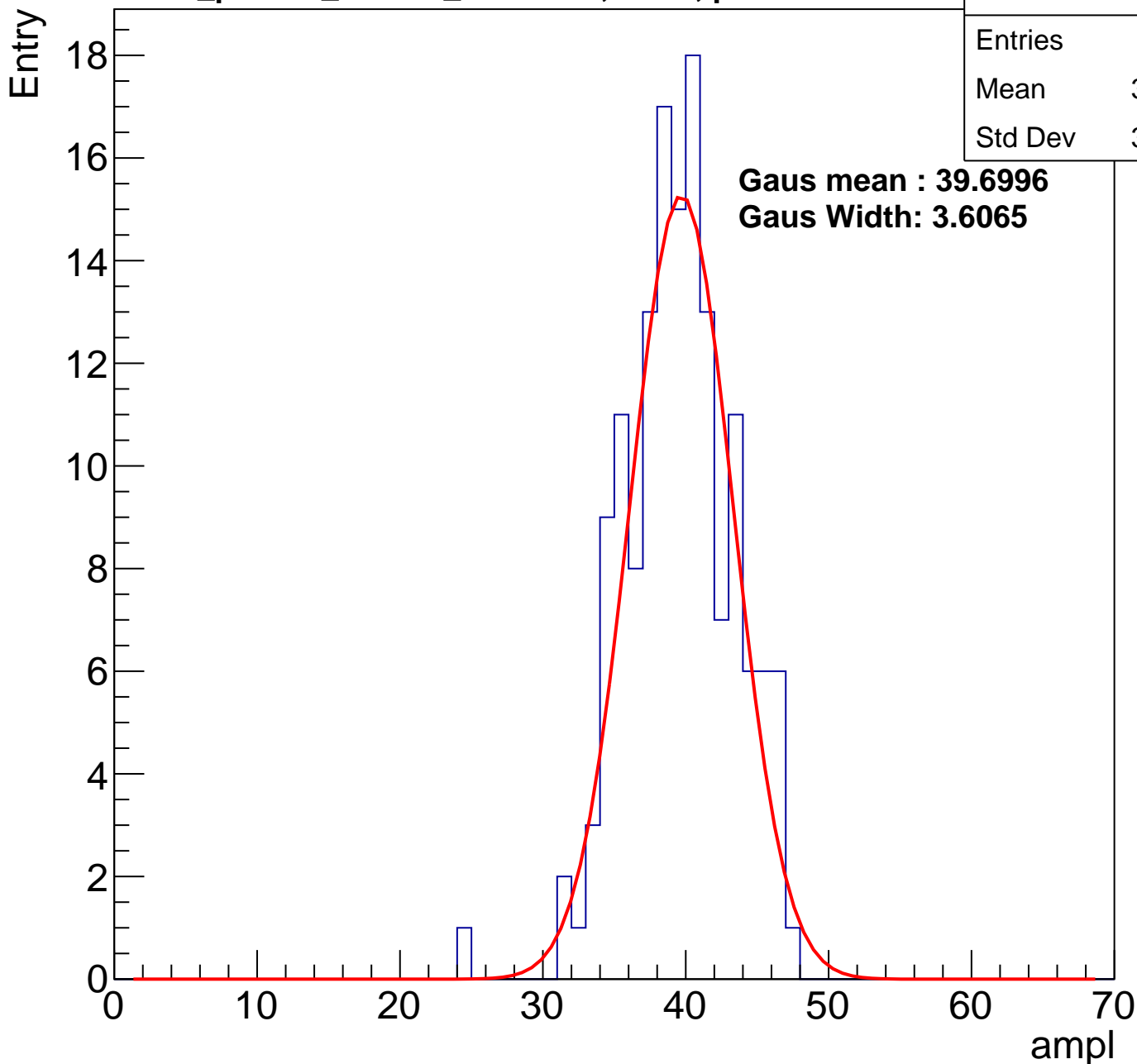
B0L101S, U25-ch89, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	39.09
Std Dev	3.752

Gaus mean : 39.6996

Gaus Width: 3.6065



B0L101S, U25-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

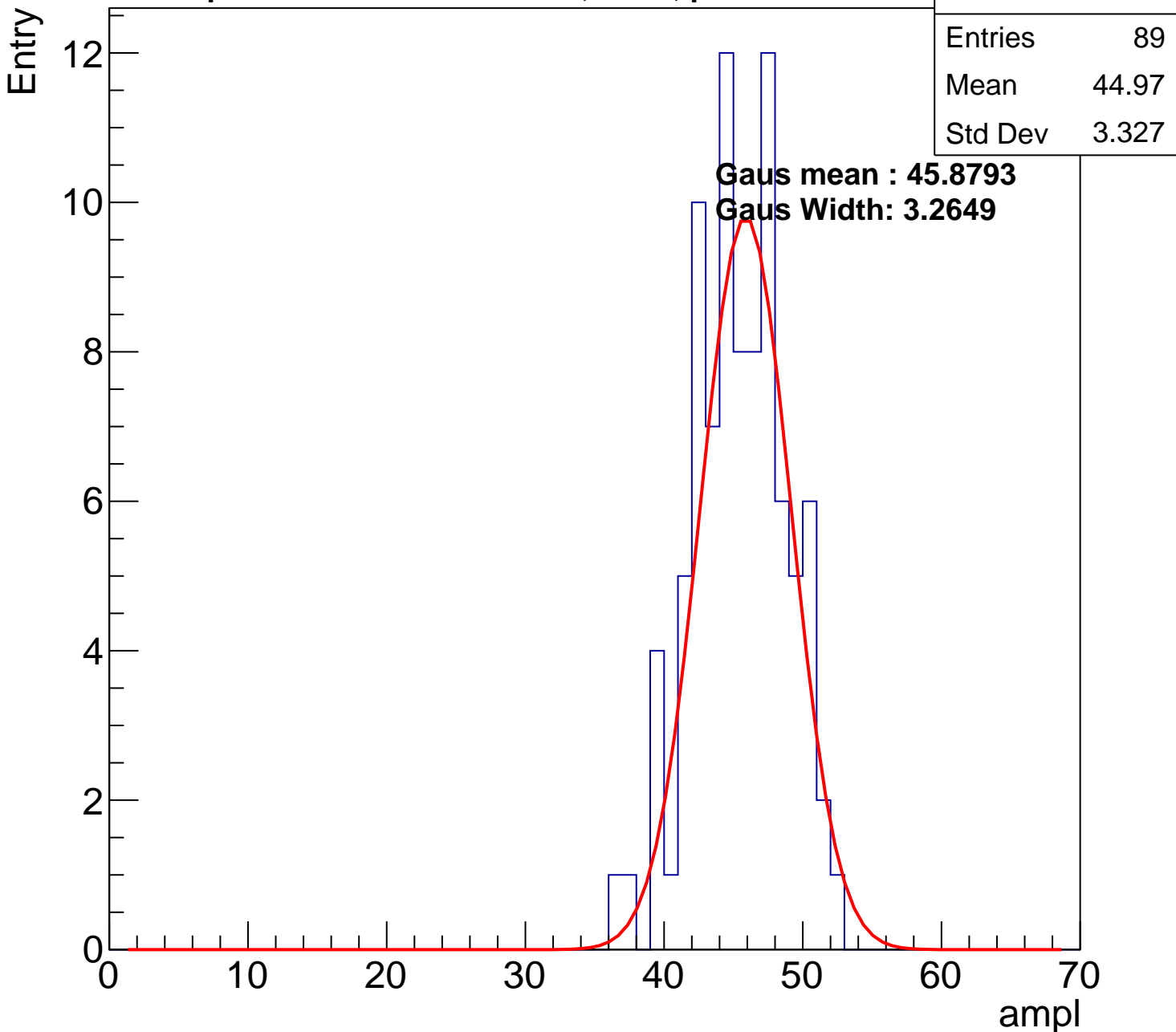
Entries	89
Mean	44.97
Std Dev	3.327

Gaus mean : 45.8793

Gaus Width: 3.2649

ampl

0 10 20 30 40 50 60 70

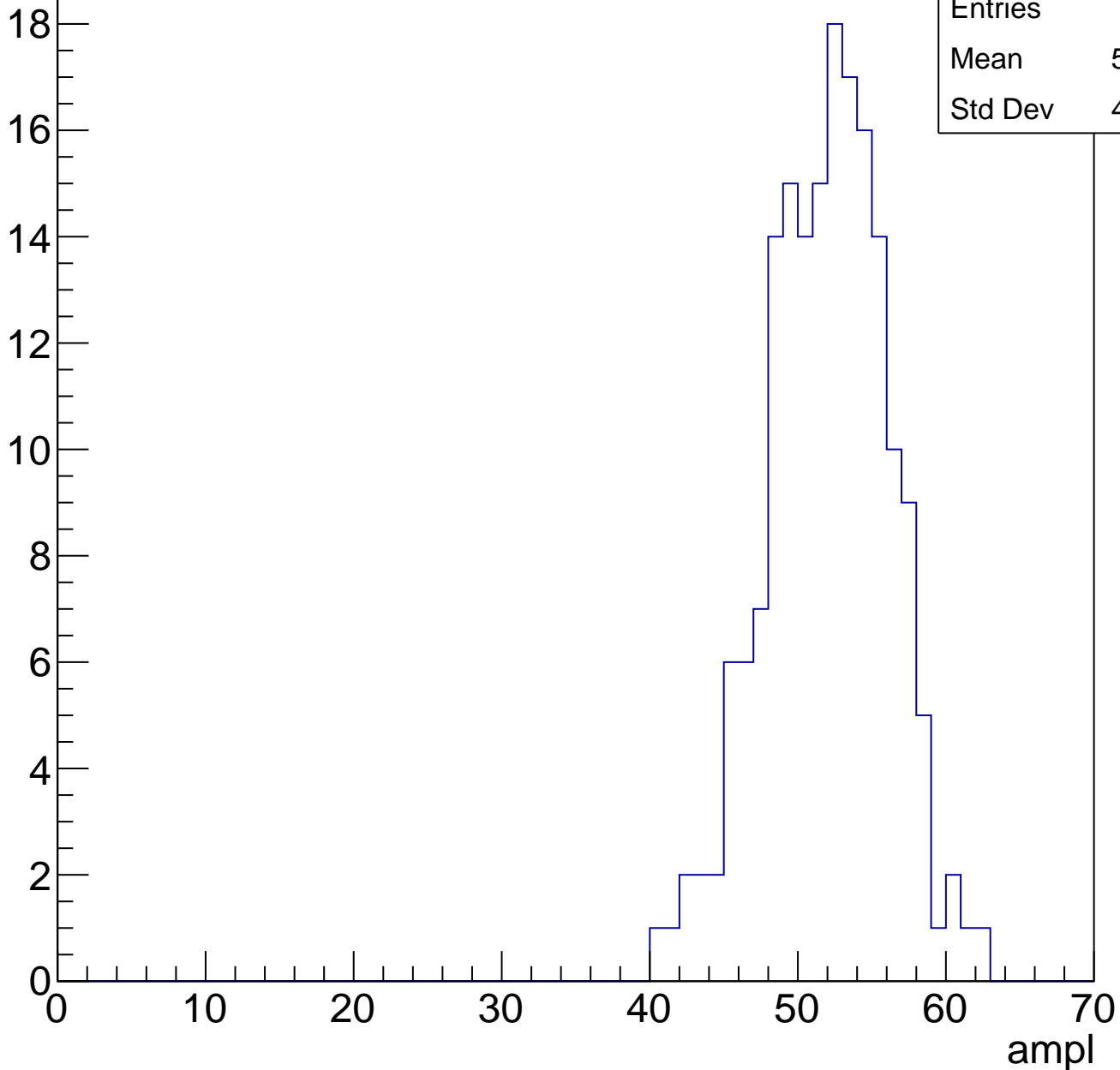


B0L101S, U25-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	51.52
Std Dev	4.066

Entry



B0L101S, U25-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	88
Mean	57.31
Std Dev	6.786

Entry

12

10

8

6

4

2

0

0

10

20

30

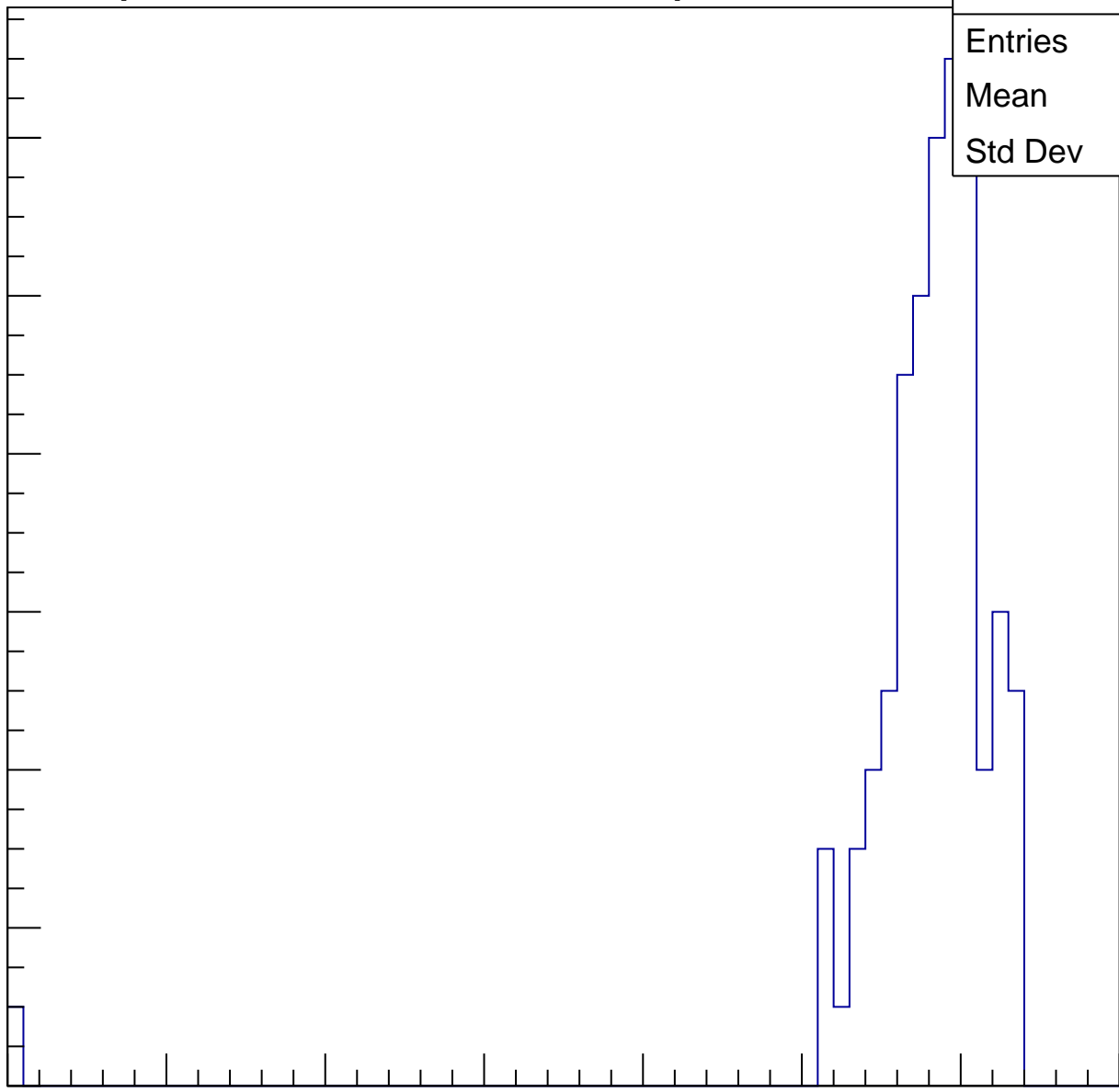
40

50

60

70

ampl



B0L101S, U25-ch89, adc5

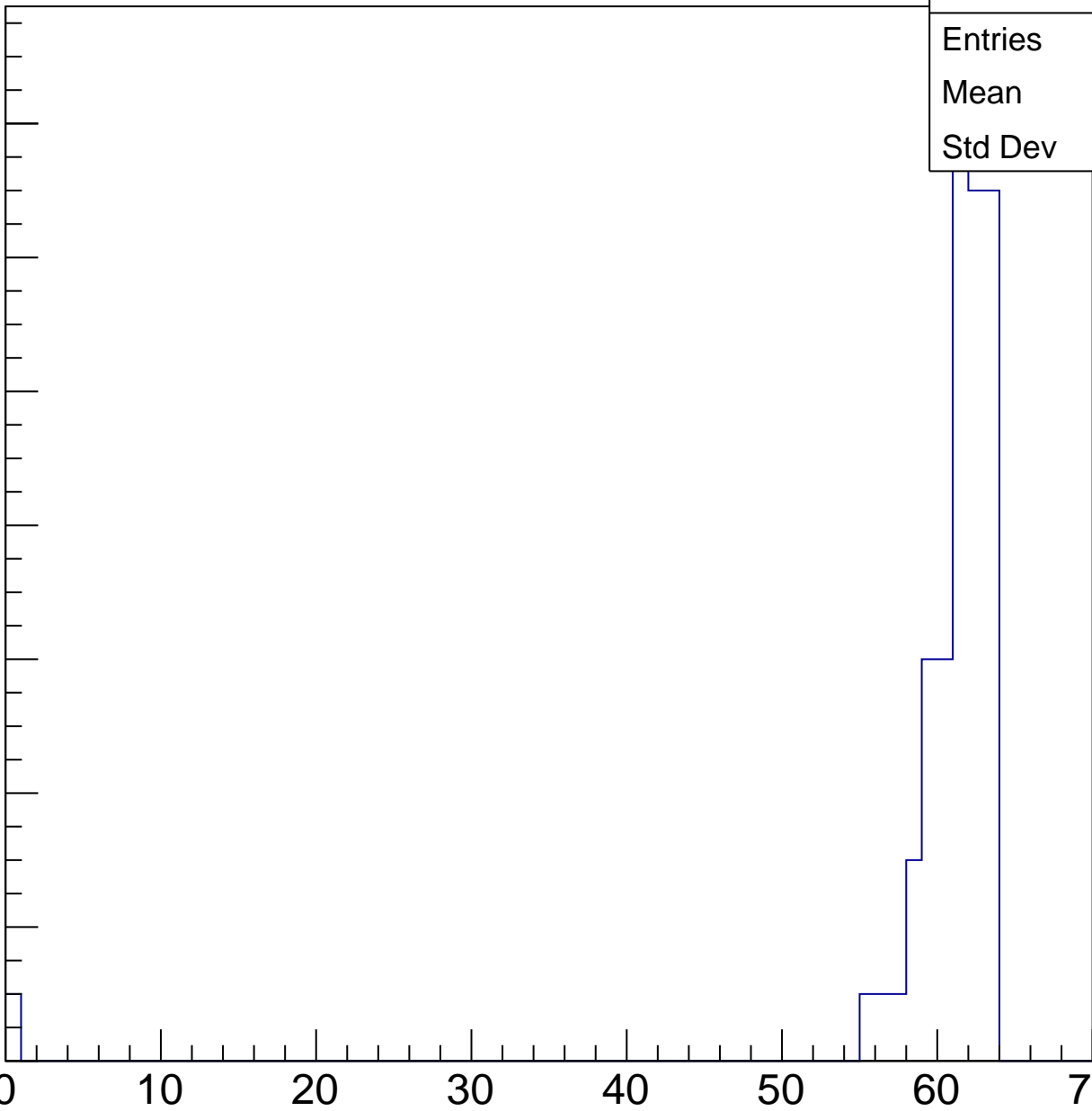
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	60
Mean	59.93
Std Dev	8.012

ampl



B0L101S, U25-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch90, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	27.42
Std Dev	4.949

Gaus mean : 28.2089

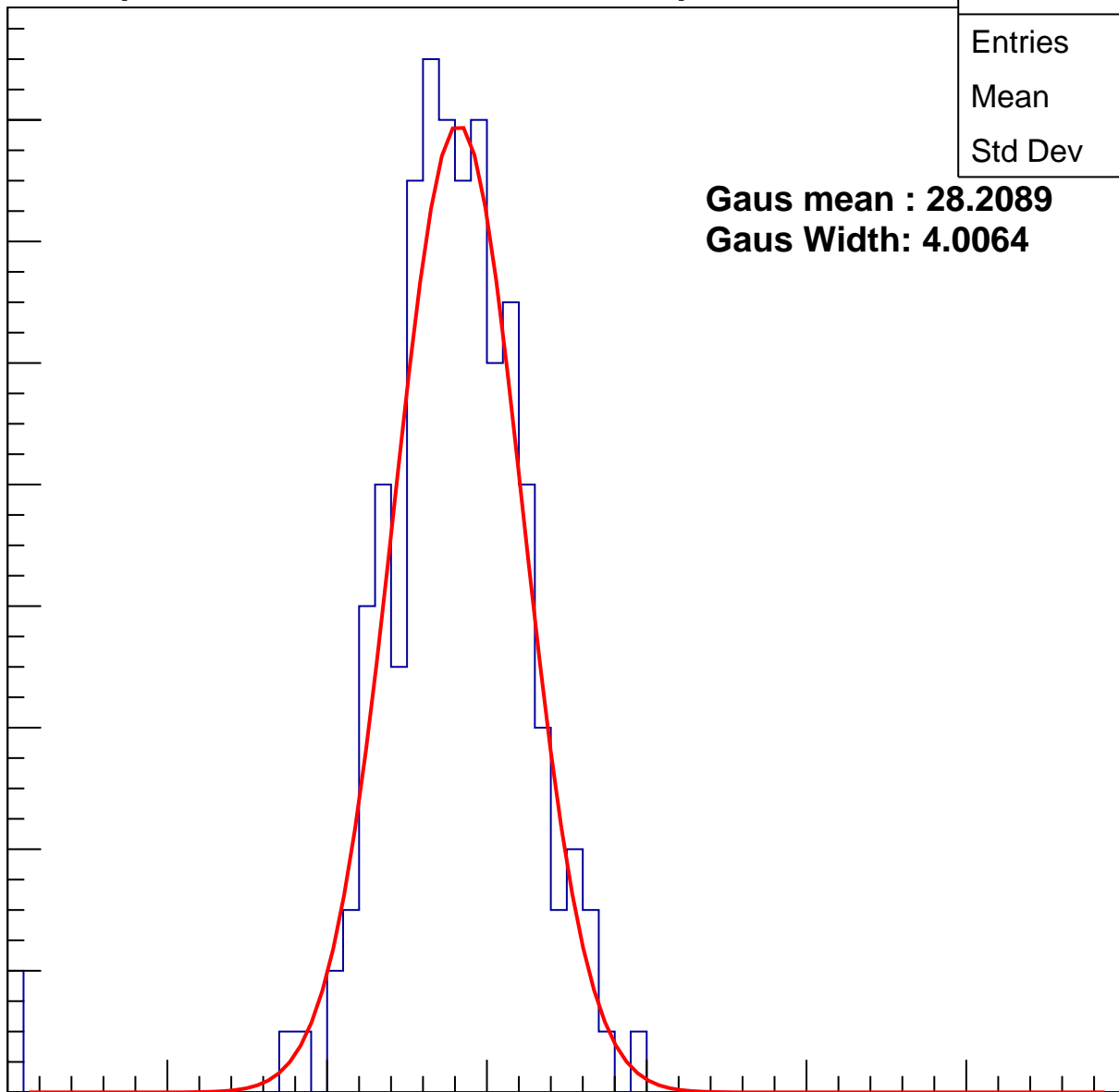
Gaus Width: 4.0064

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch90, adc1

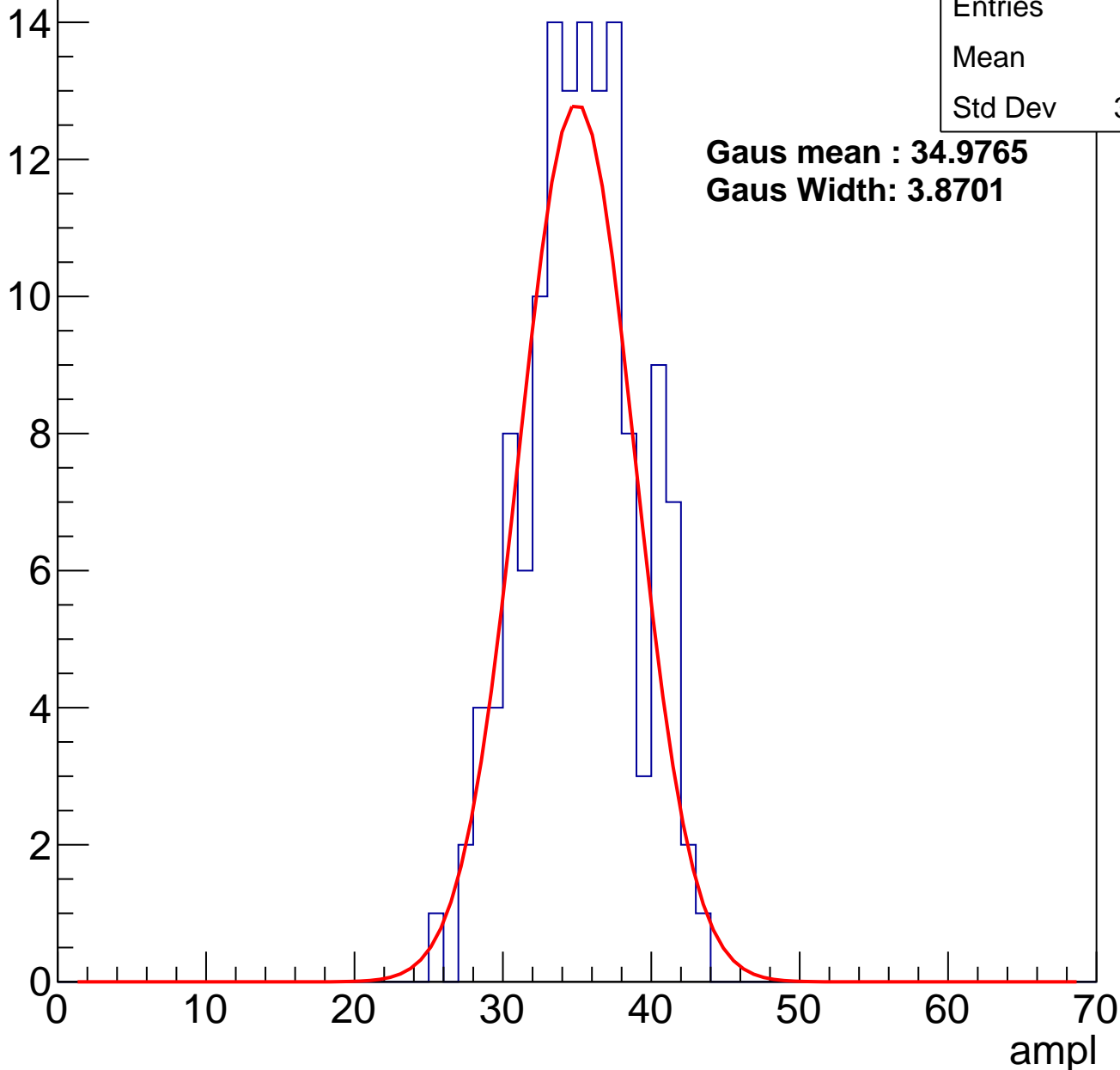
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	34.8
Std Dev	3.725

Gaus mean : 34.9765

Gaus Width: 3.8701

Entry



B0L101S, U25-ch90, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	41.68
Std Dev	3.585

Gaus mean : 42.3672

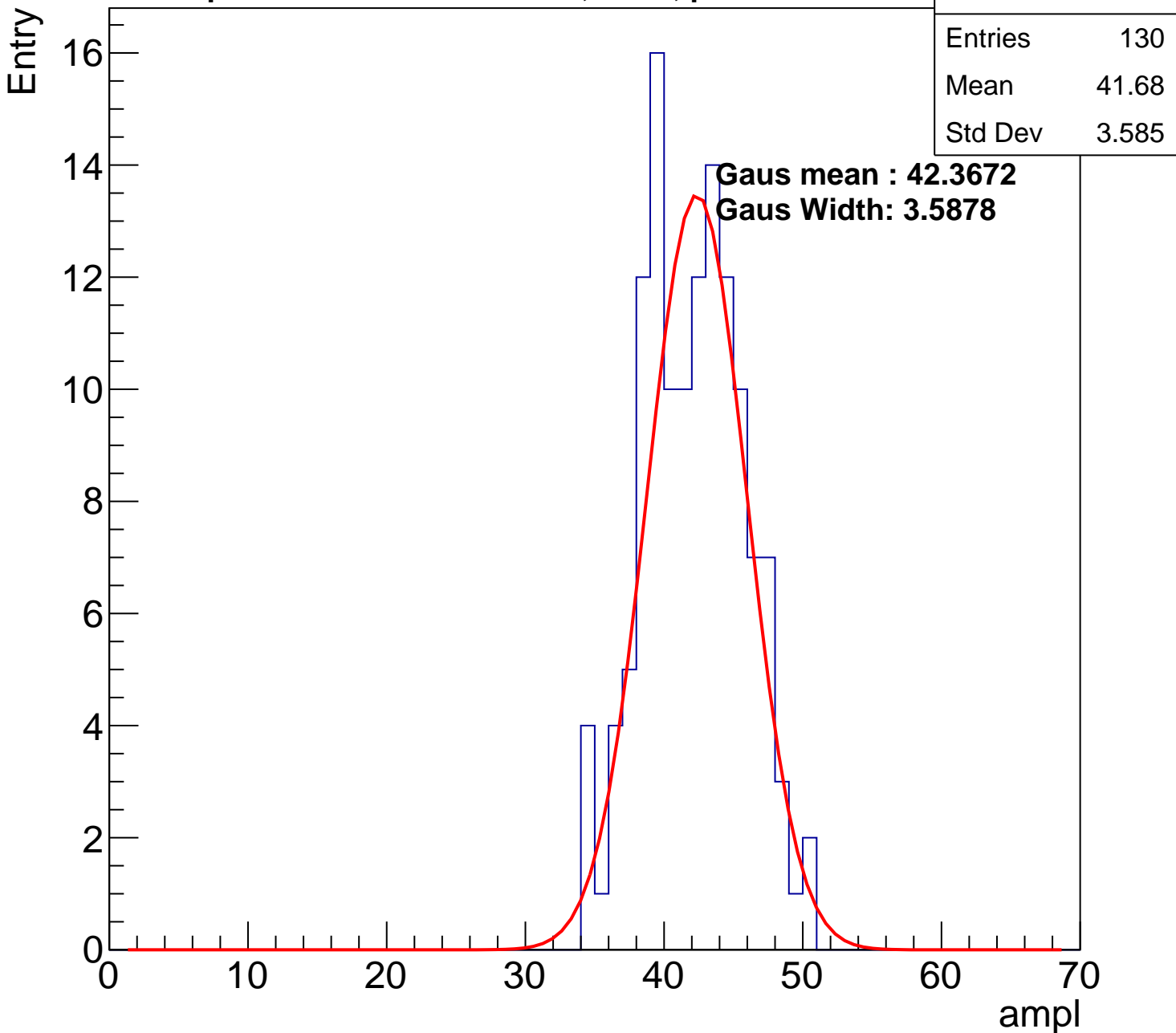
Gaus Width: 3.5878

Entry

16
14
12
10
8
6
4
2
0

ampl

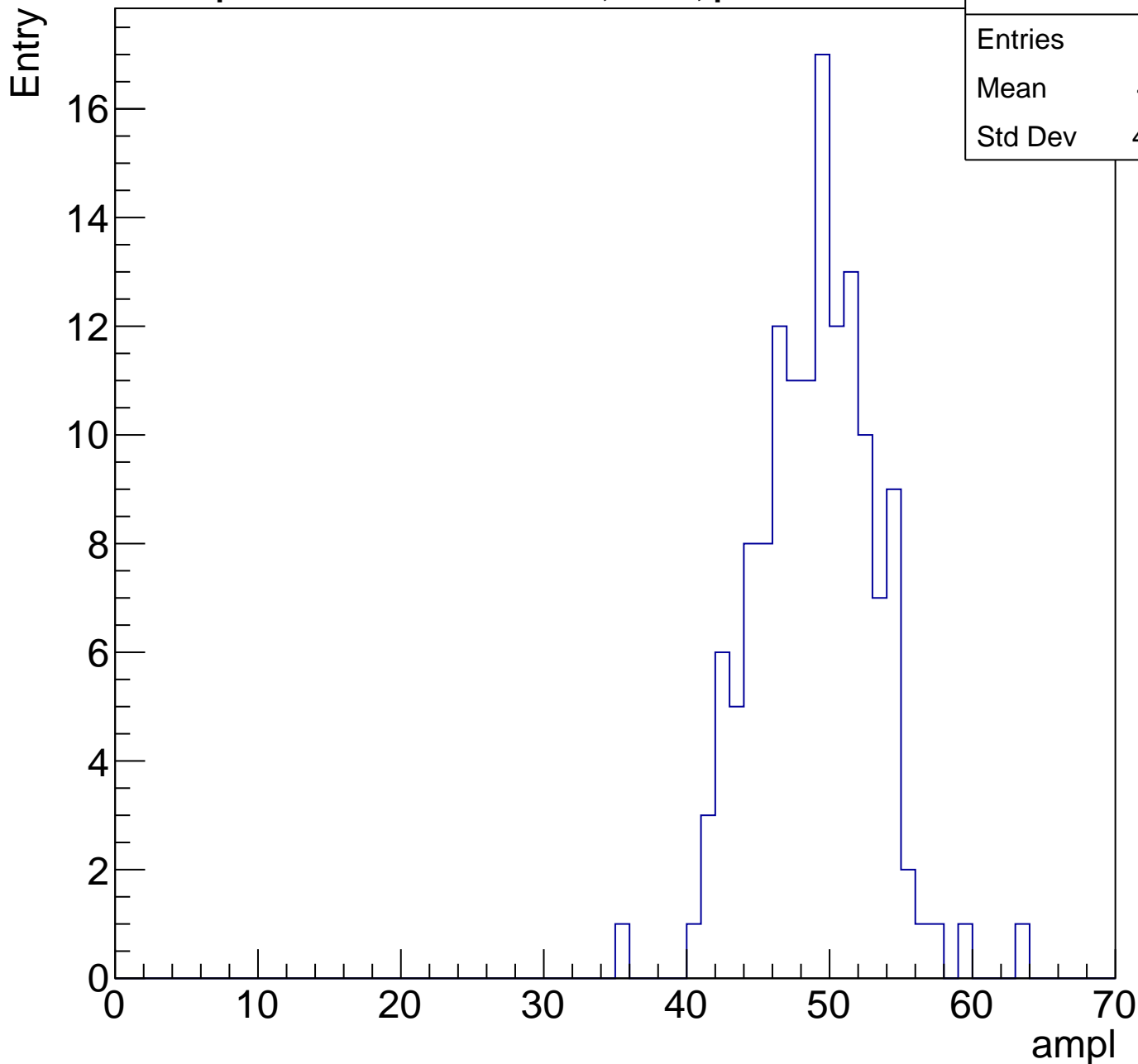
0 10 20 30 40 50 60 70



B0L101S, U25-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	48.51
Std Dev	4.117



B0L101S, U25-ch90, adc4

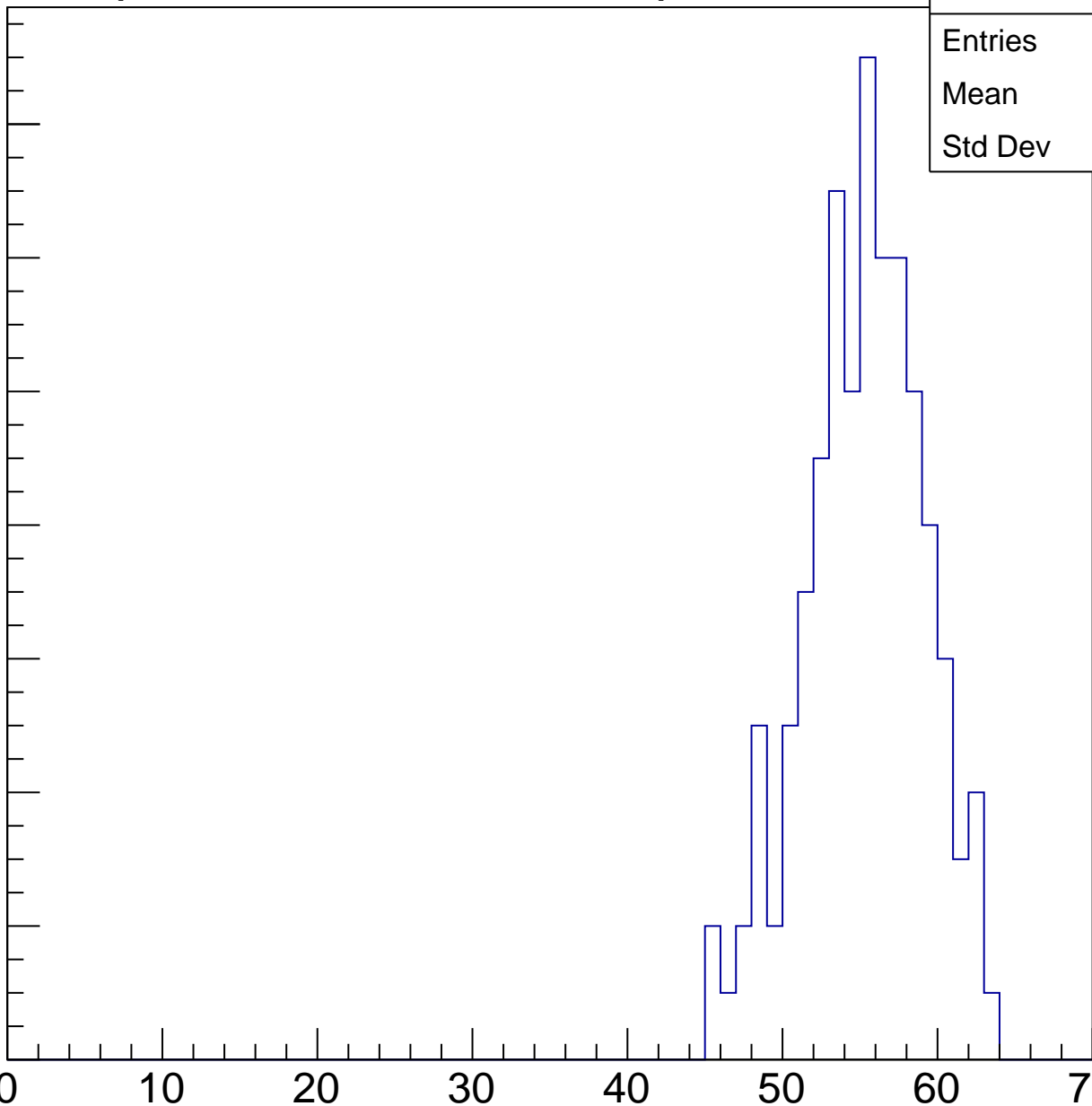
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	54.8
Std Dev	3.865

ampl



B0L101S, U25-ch90, adc5

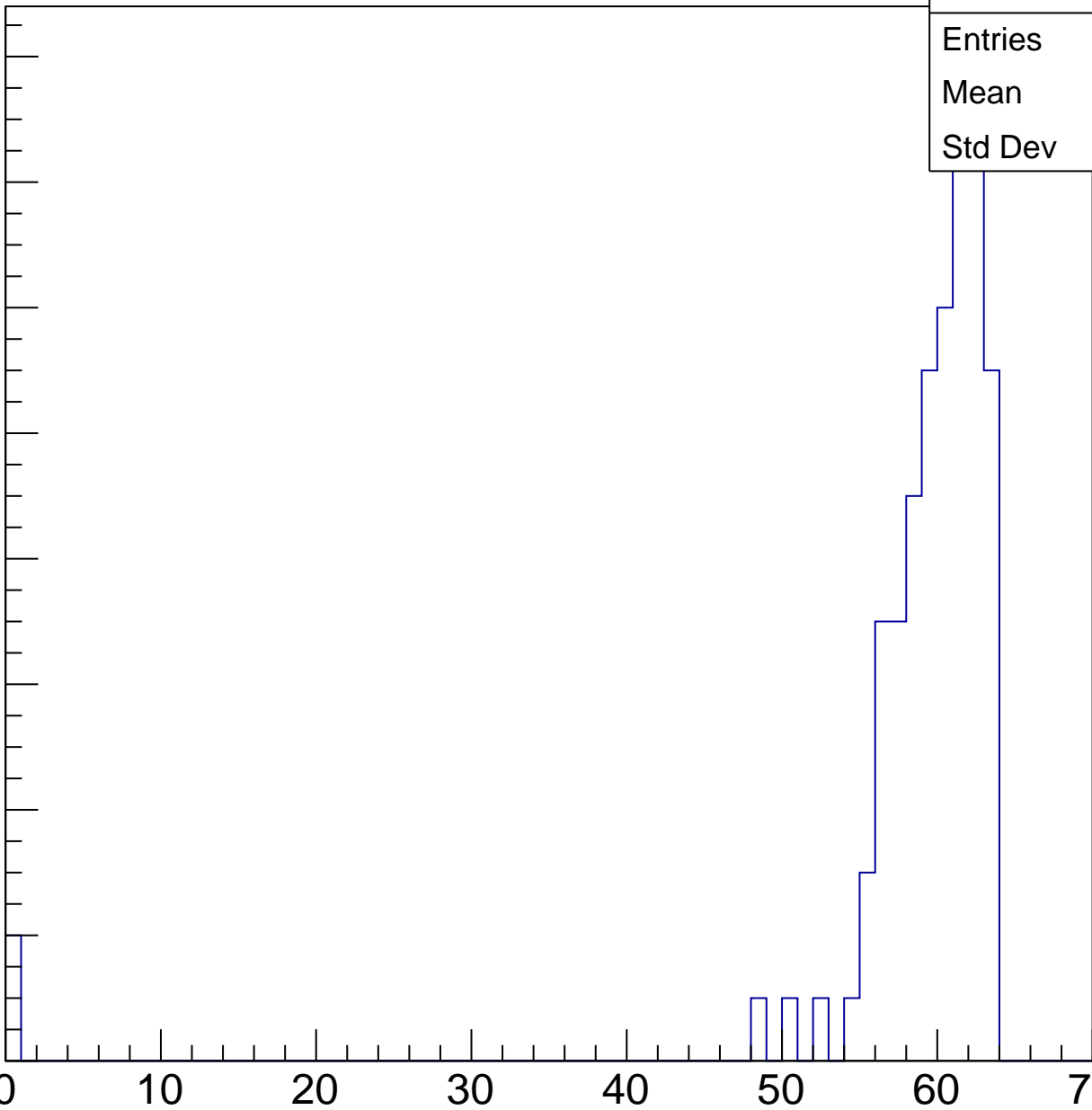
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.25
Std Dev	8.92

ampl

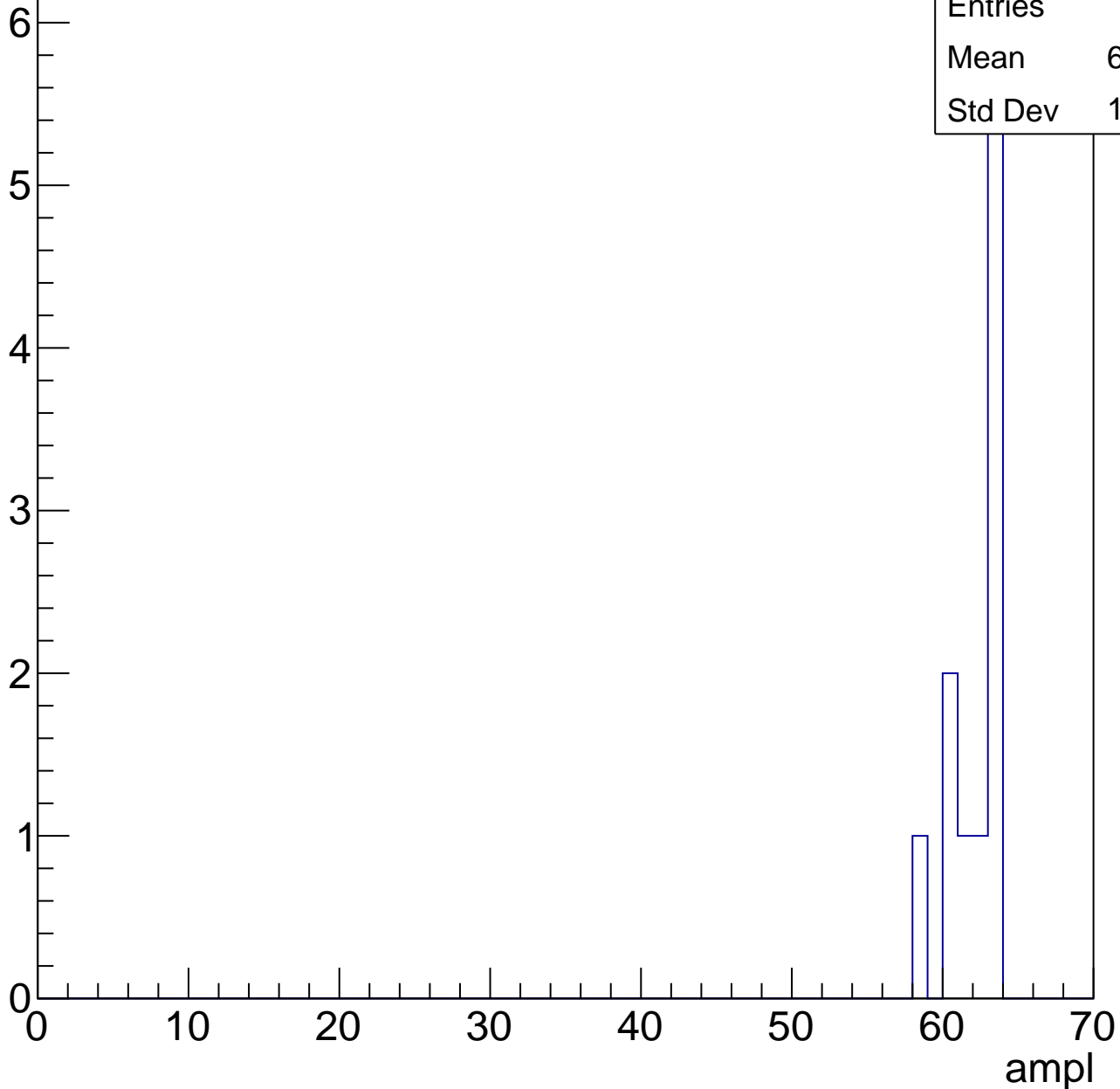


B0L101S, U25-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	61.73
Std Dev	1.656



B0L101S, U25-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch91, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	31.14
Std Dev	4.678

Gaus mean : 32.1128

Gaus Width: 3.8537

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U25-ch91, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	38.9
Std Dev	3.963

Gaus mean : 39.5791

Gaus Width: 3.7654

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

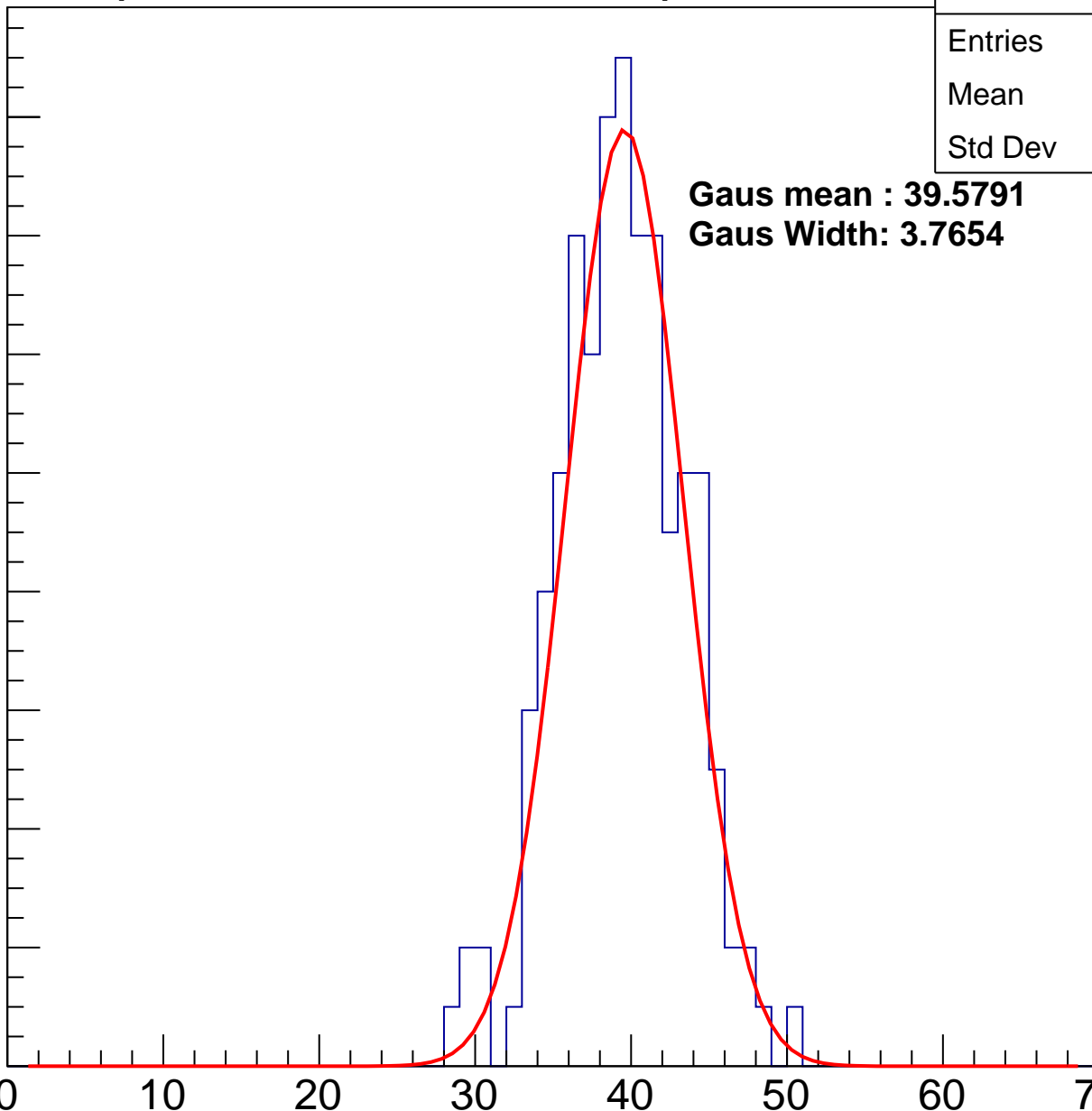
40

50

60

70

ampl



B0L101S, U25-ch91, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	46.39
Std Dev	3.891

Gaus mean : 47.0420

Gaus Width: 4.0114

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

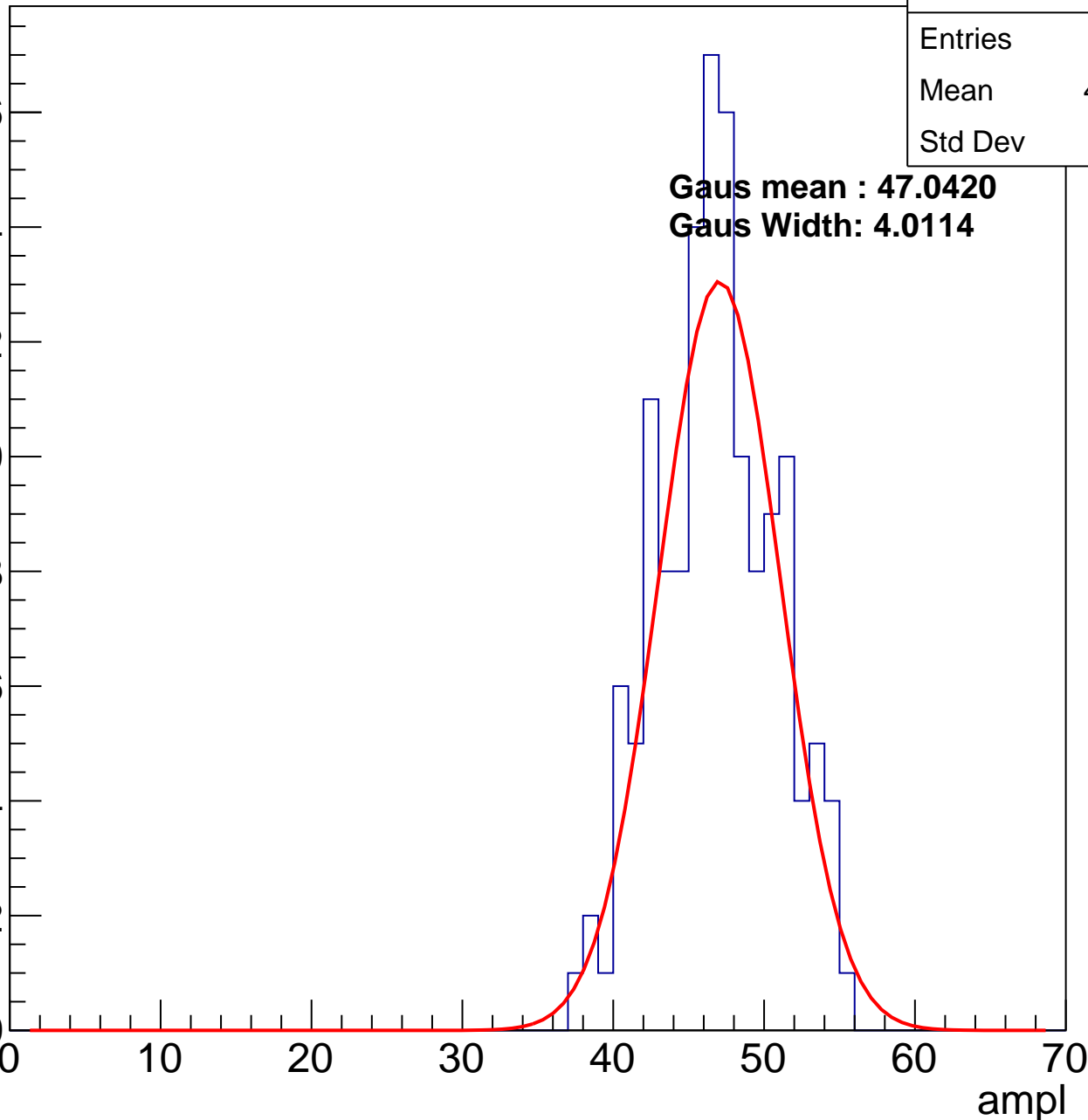
40

50

60

ampl

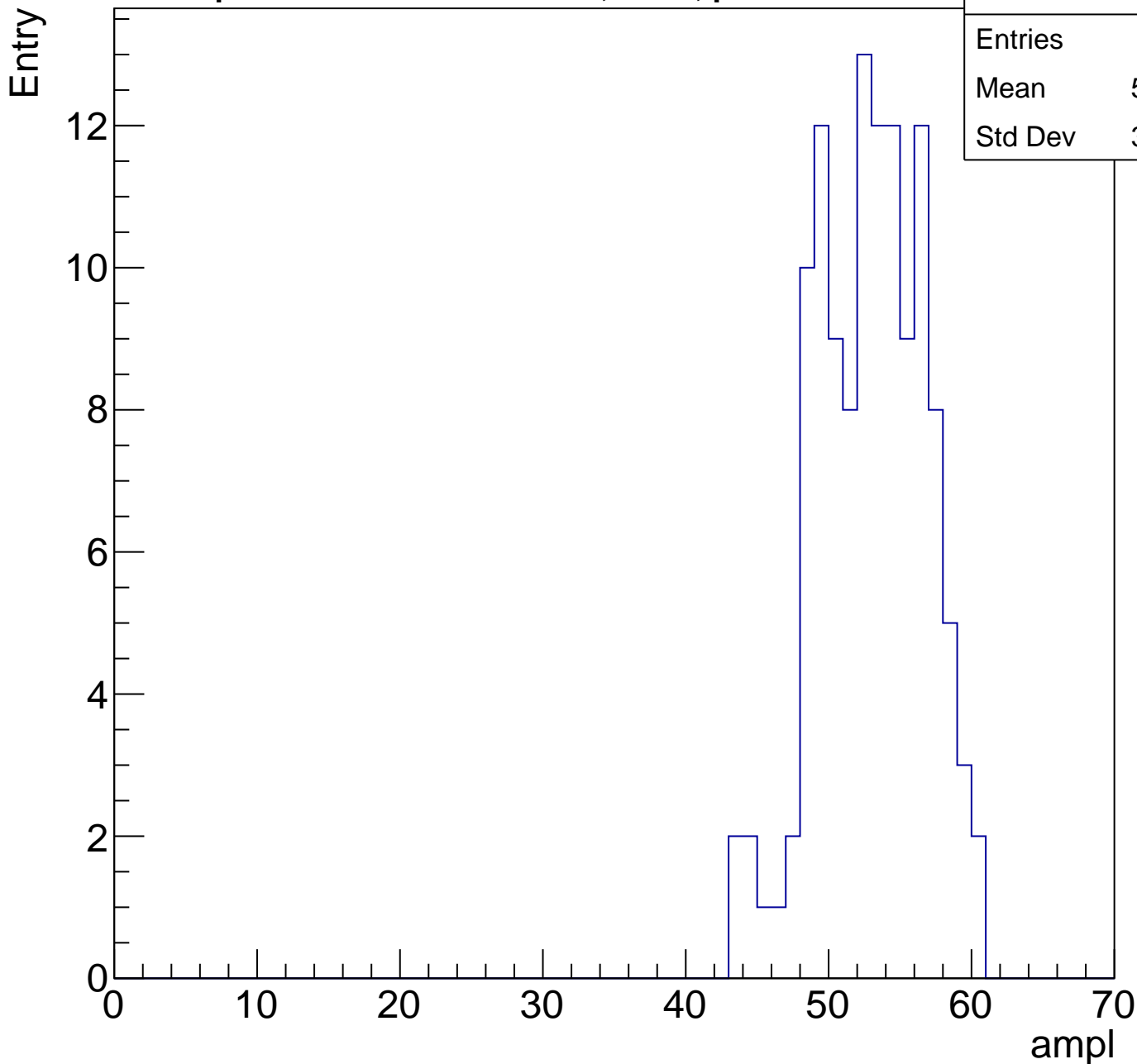
70



B0L101S, U25-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	52.48
Std Dev	3.714



B0L101S, U25-ch91, adc4

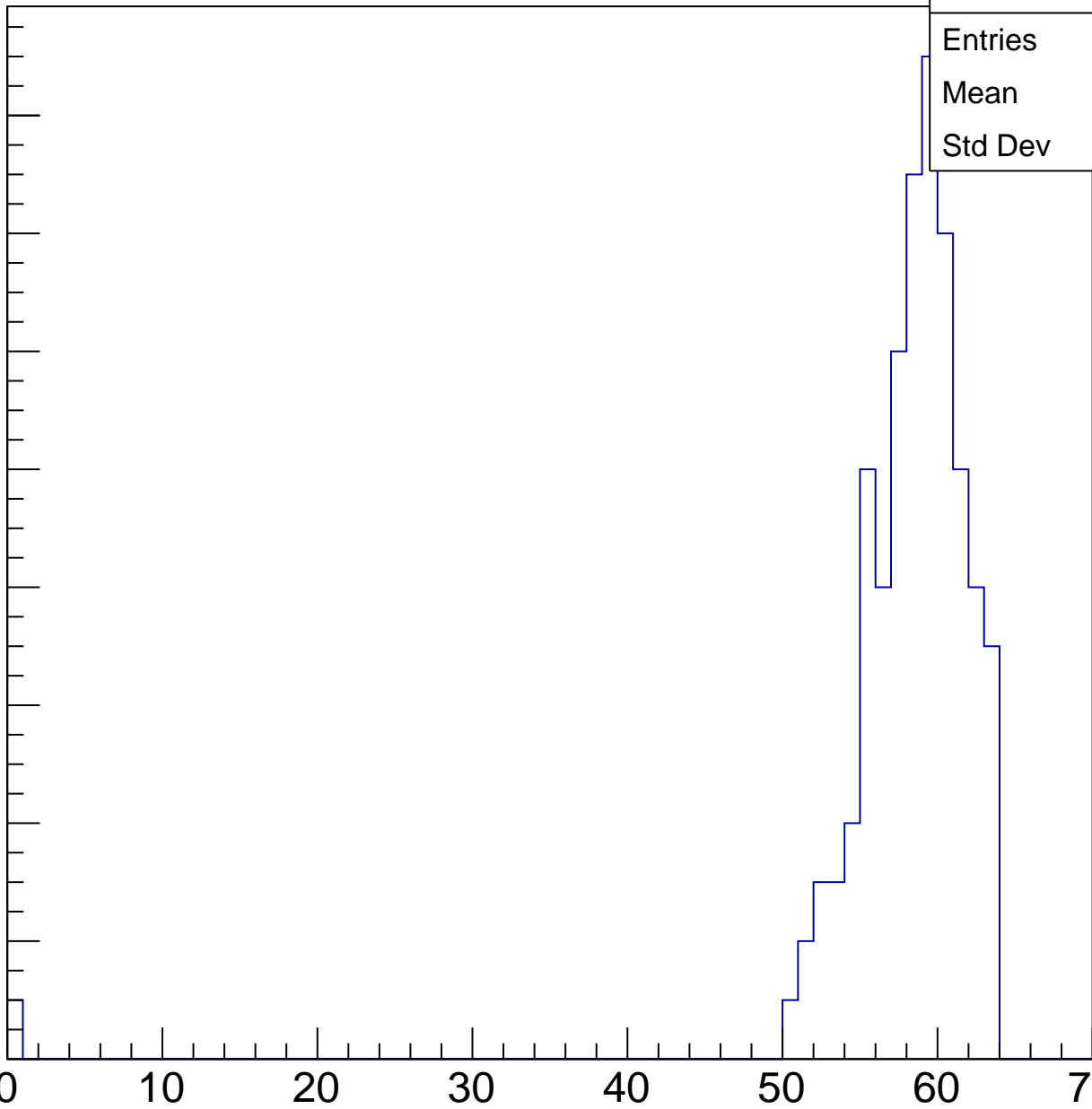
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	57.61
Std Dev	6.154

ampl



B0L101S, U25-ch91, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	38
Mean	60.24
Std Dev	9.951

ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch92, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	27.27
Std Dev	4.461

Gaus mean : 27.8446

Gaus Width: 3.9332

Entry

16

14

12

10

8

6

4

2

0

0

10

20

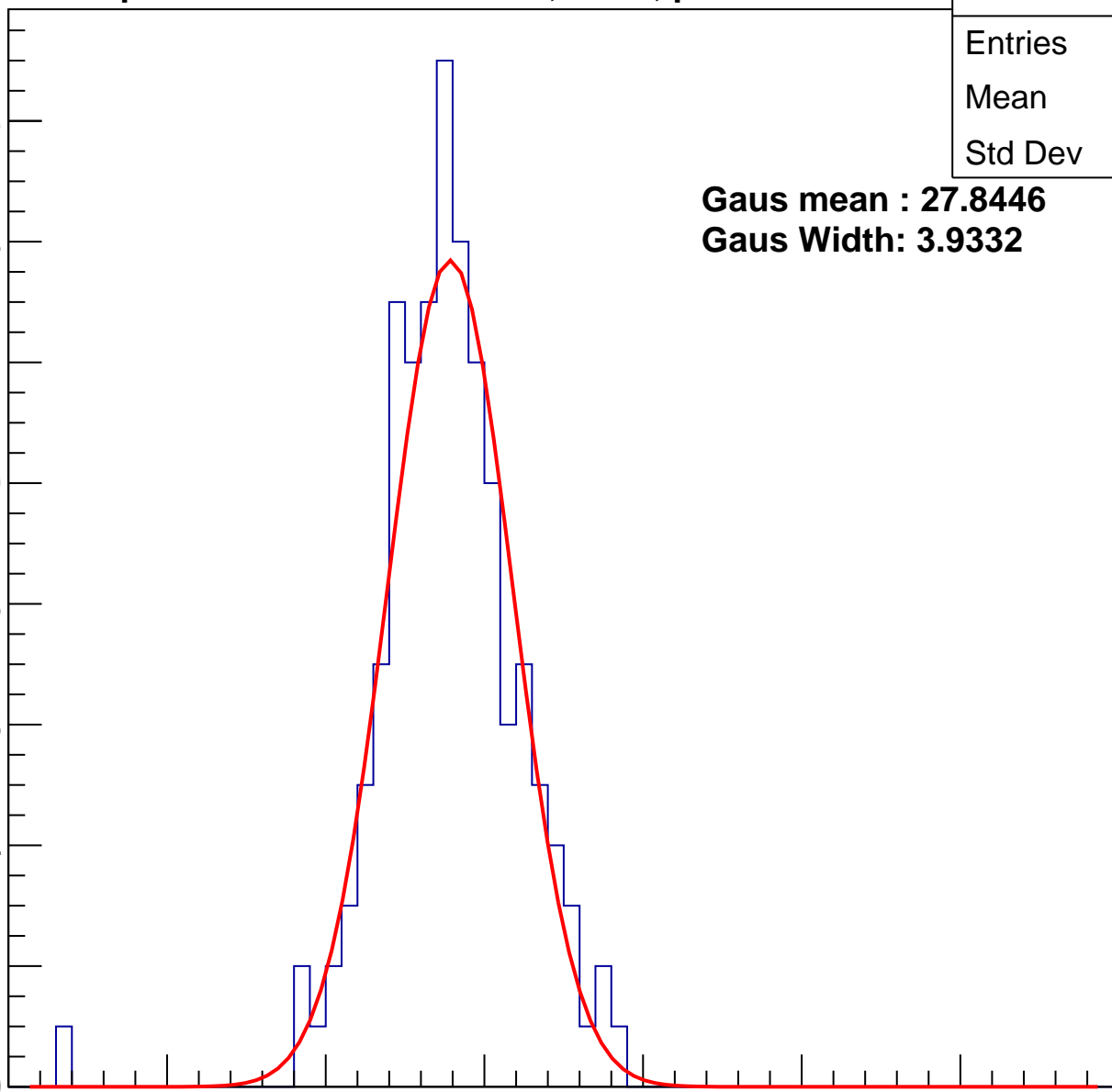
30

50

60

70

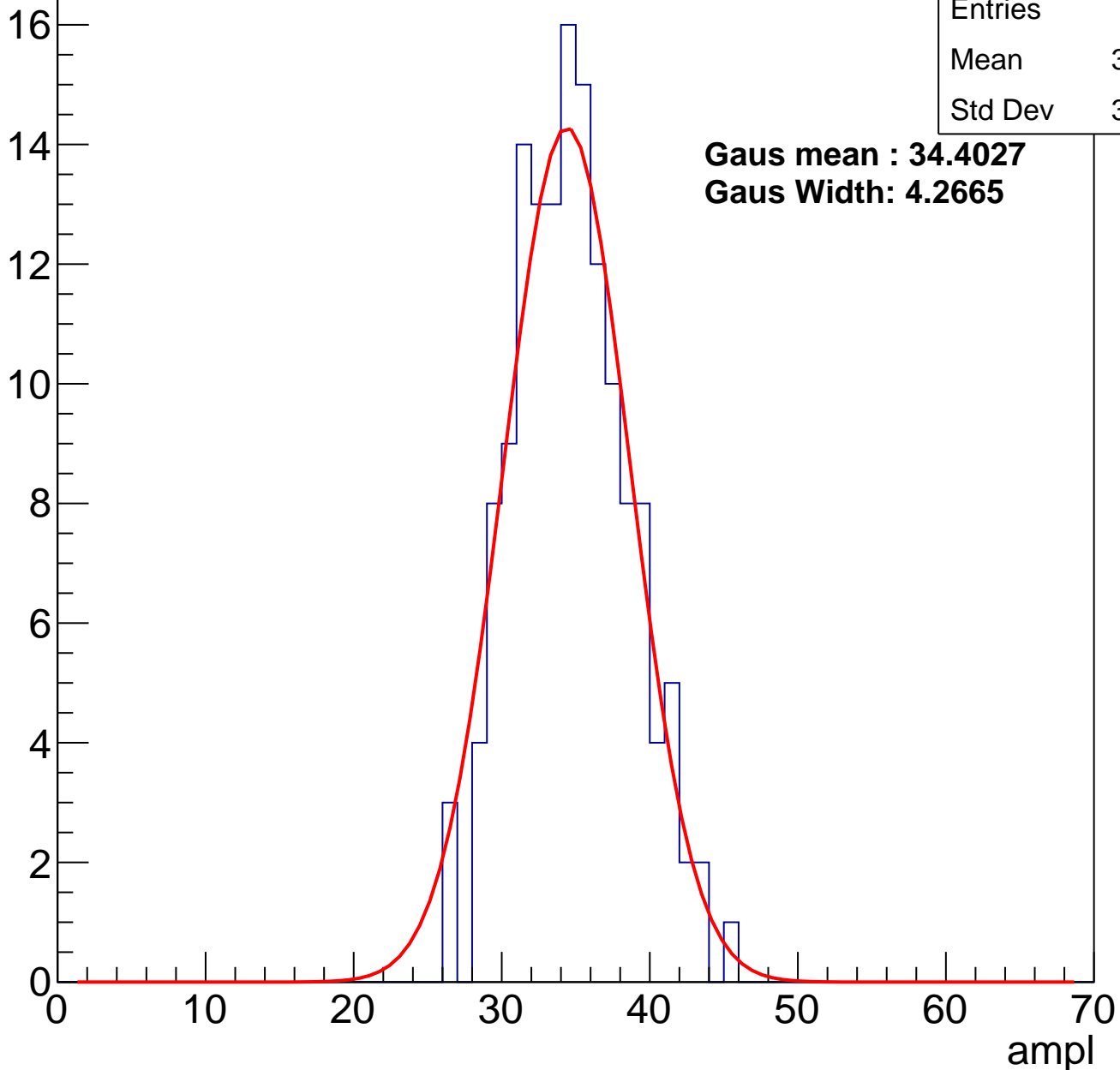
ampl



B0L101S, U25-ch92, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



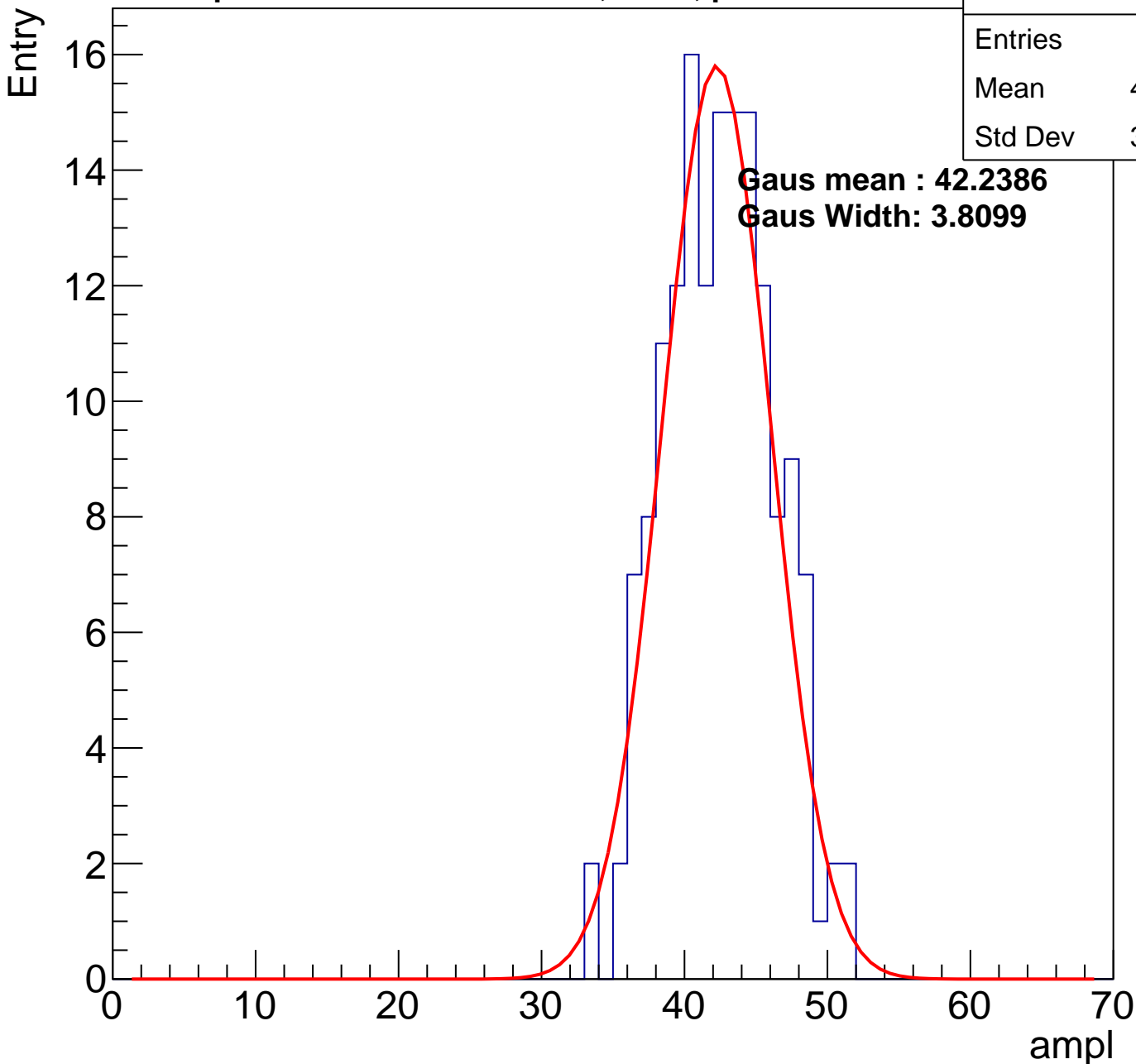
B0L101S, U25-ch92, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	42.02
Std Dev	3.754

Gaus mean : 42.2386

Gaus Width: 3.8099



B0L101S, U25-ch92, adc3

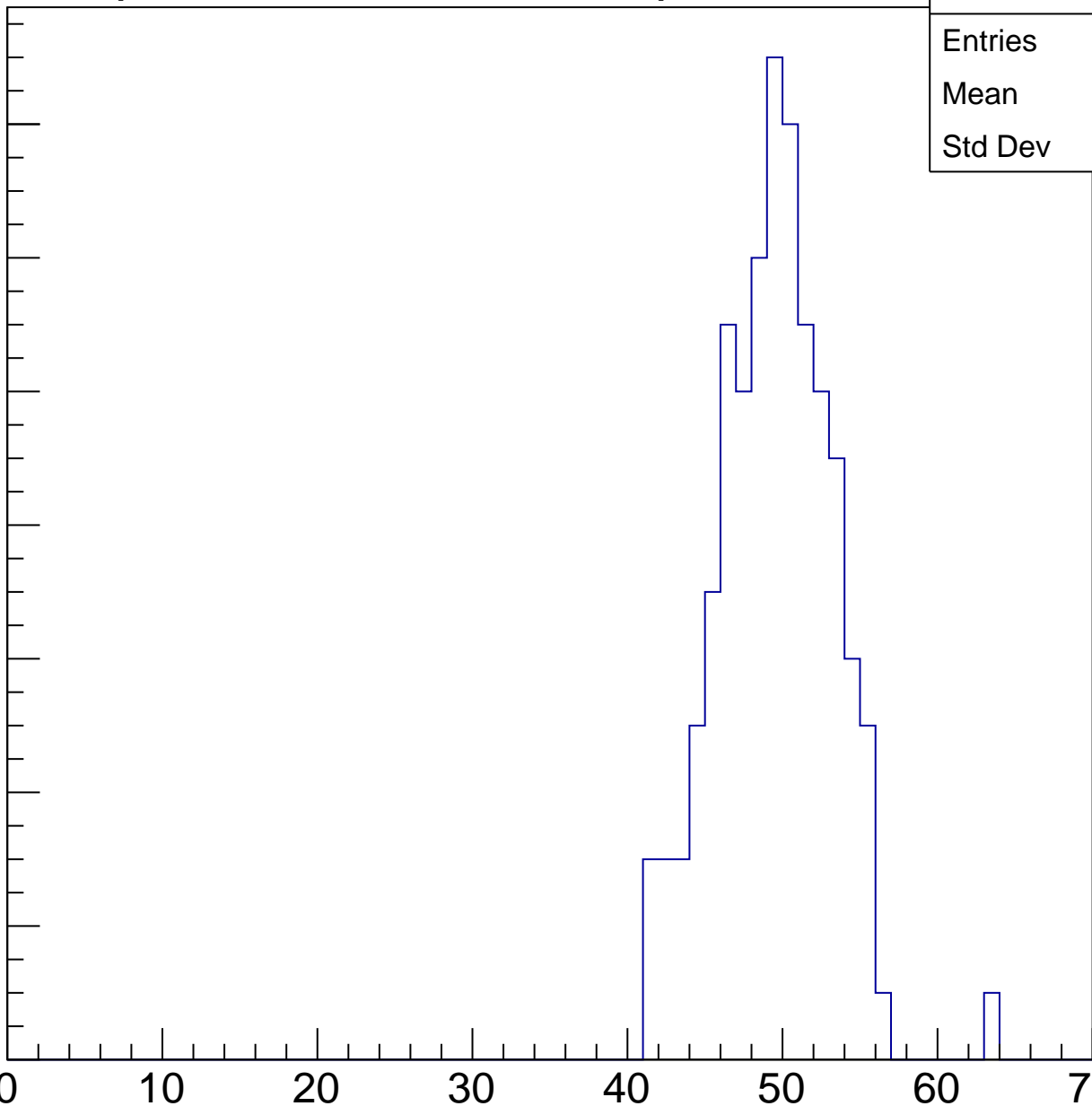
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	126
Mean	49.02
Std Dev	3.675

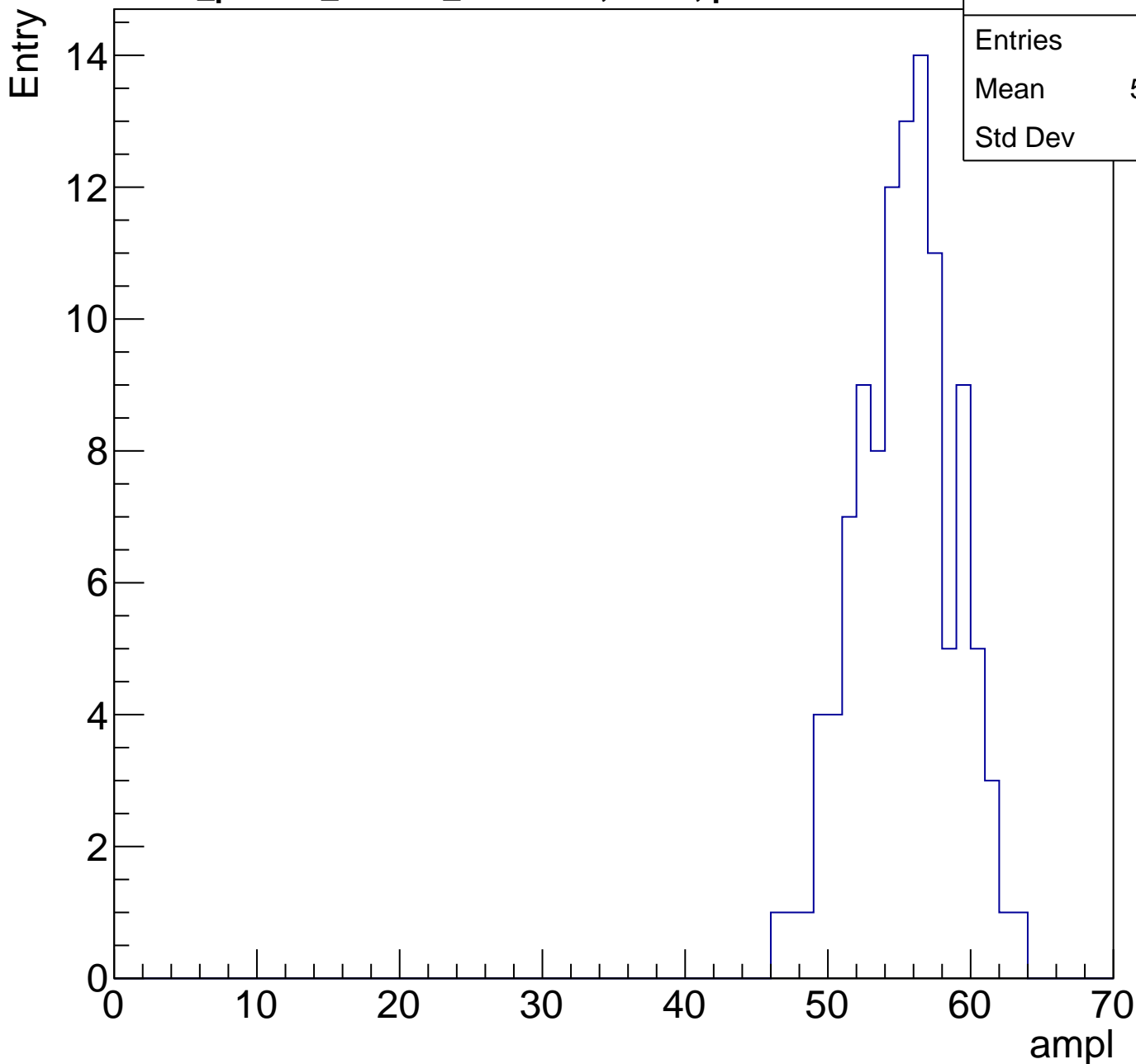
ampl



B0L101S, U25-ch92, adc4

calib_packv5_042523_0143.root, FC#1, port C1

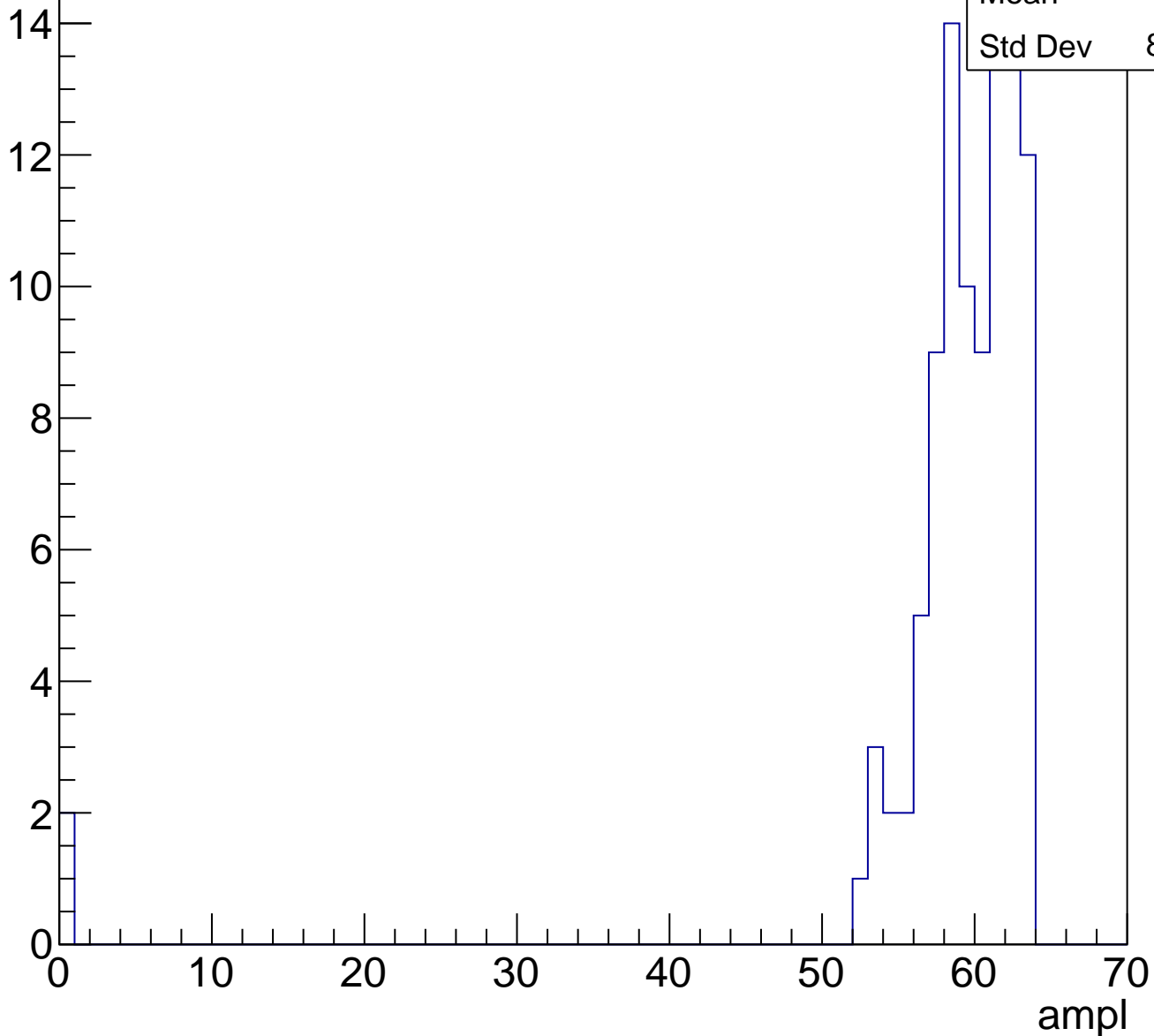
Entries	109
Mean	54.94
Std Dev	3.41



B0L101S, U25-ch92, adc5

calib_packv5_042523_0143.root, FC#1, port C1

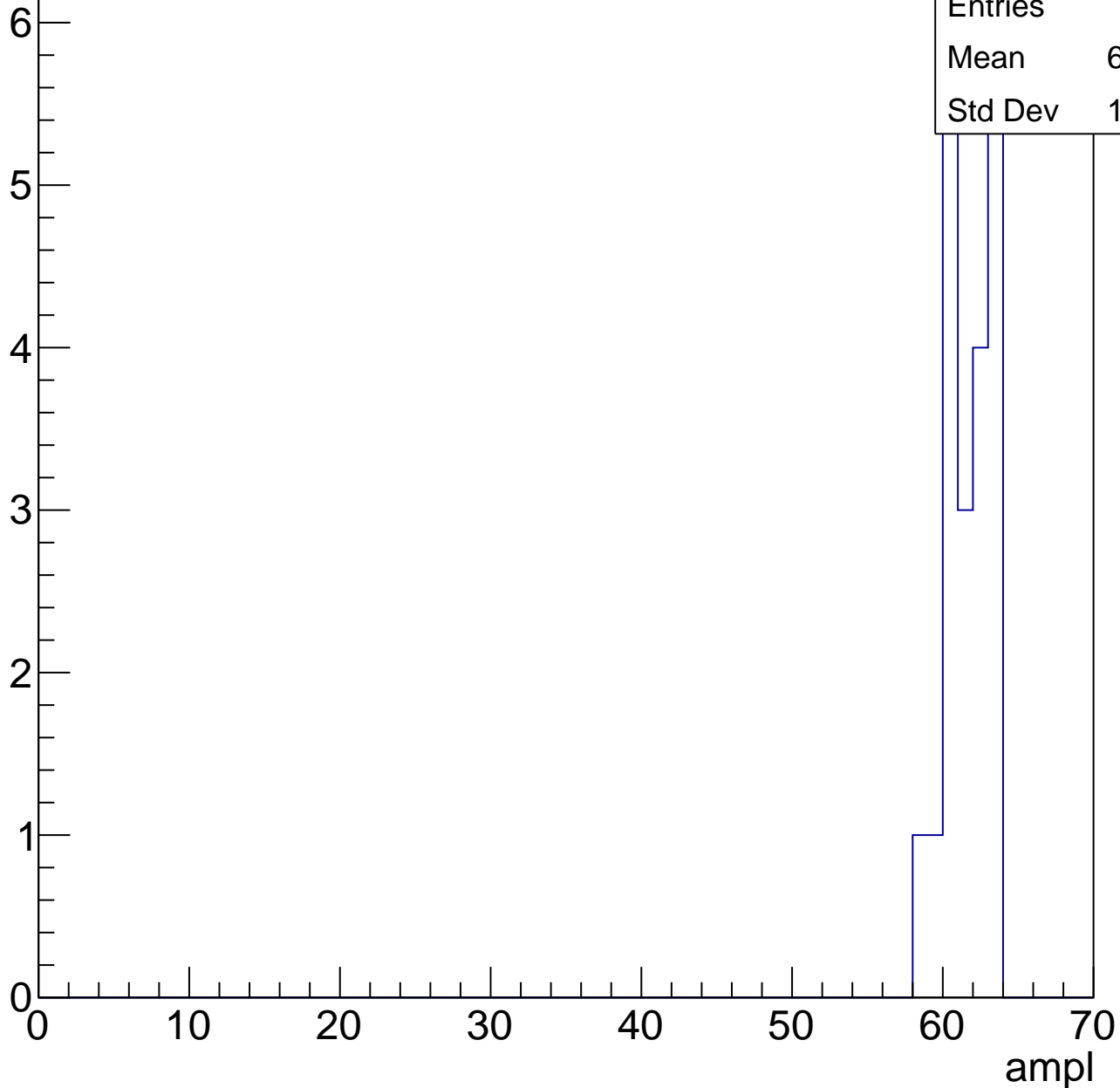
Entry



B0L101S, U25-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch93, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	31.33
Std Dev	4.533

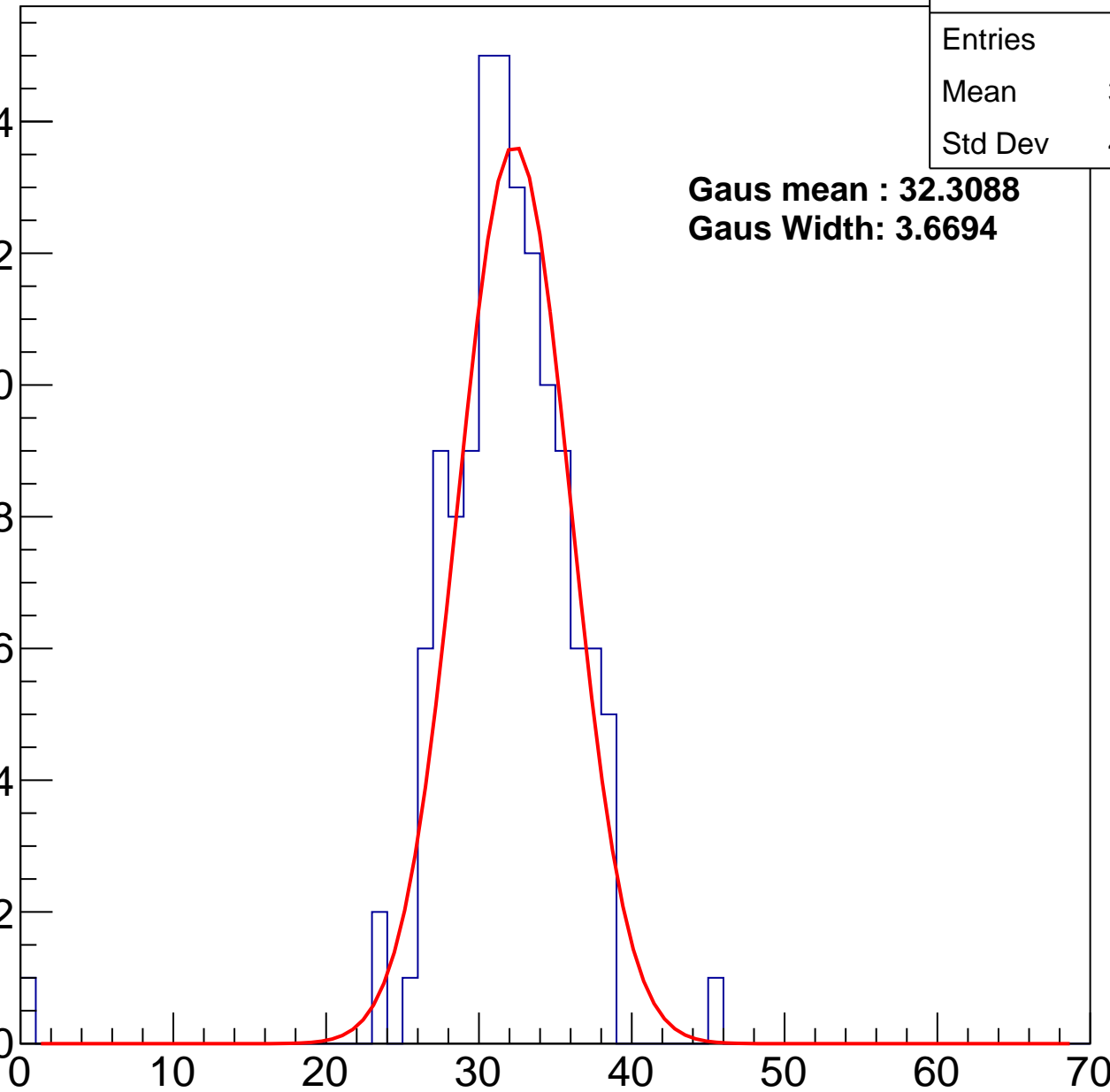
Gaus mean : 32.3088

Gaus Width: 3.6694

Entry

14
12
10
8
6
4
2
0

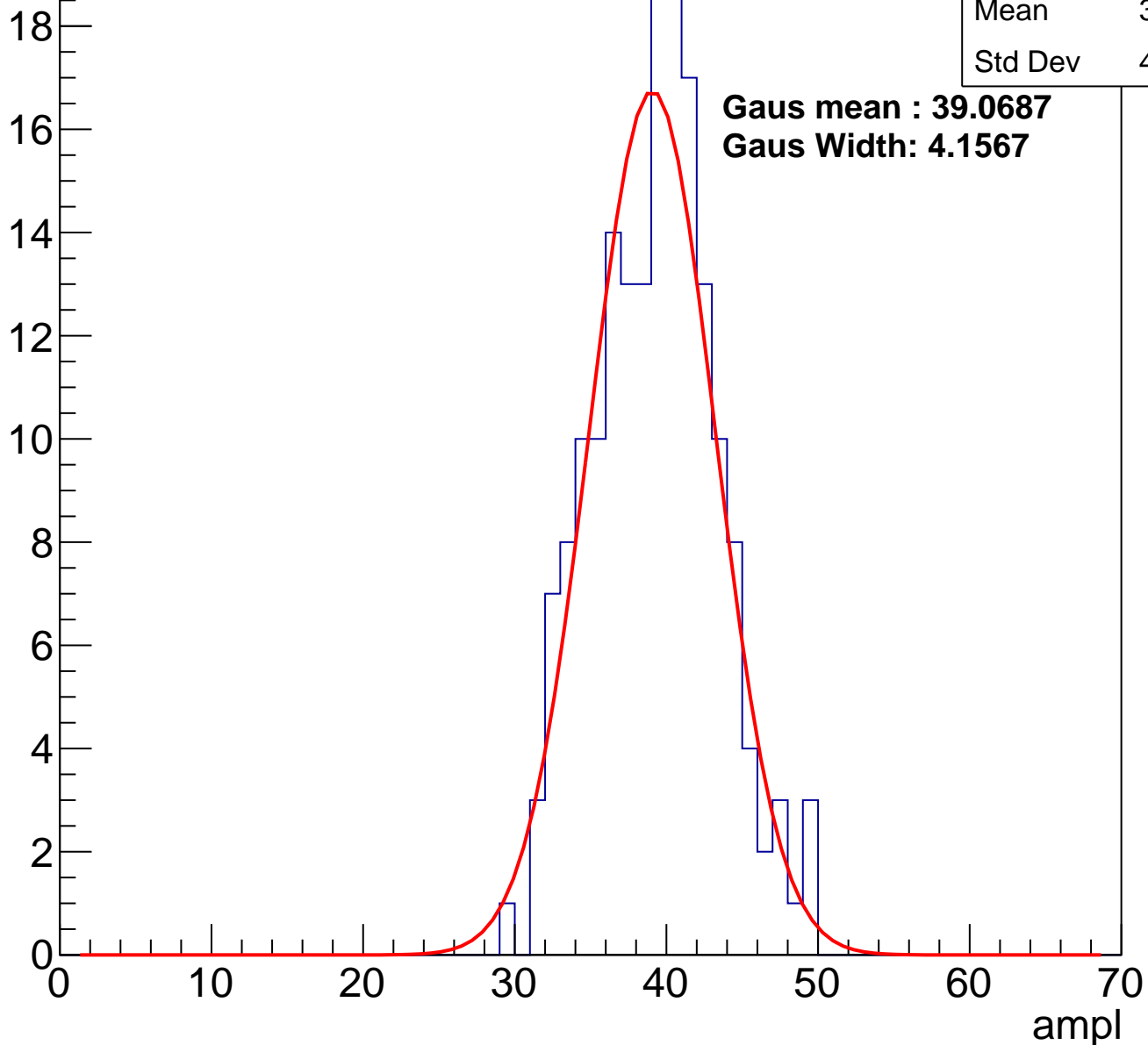
ampl



B0L101S, U25-ch93, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch93, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	45.93
Std Dev	3.568

Entry

14
12
10
8
6
4
2
0

Gaus mean : 46.3037
Gaus Width: 3.4633

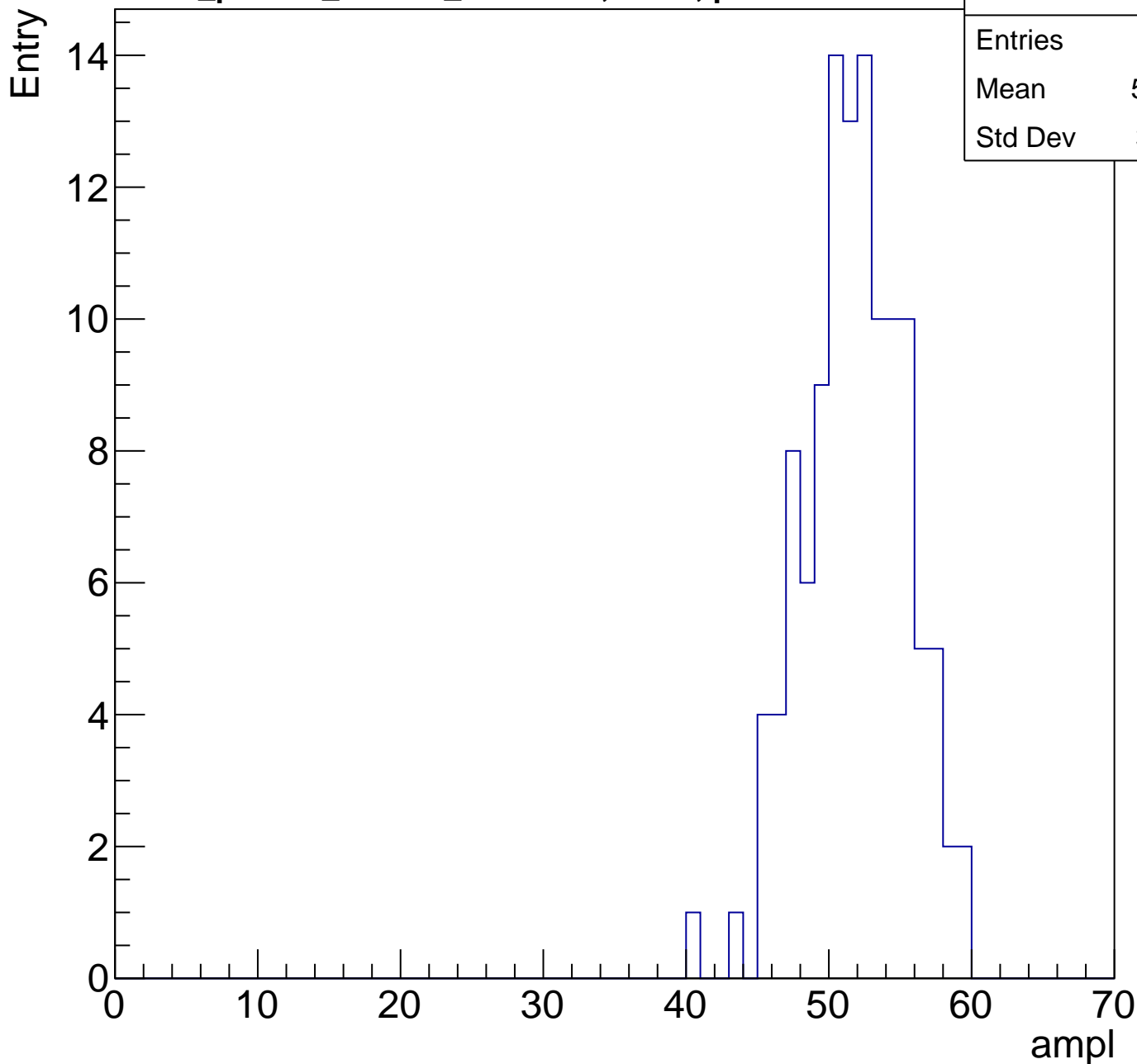
ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	51.37
Std Dev	3.541



B0L101S, U25-ch93, adc4

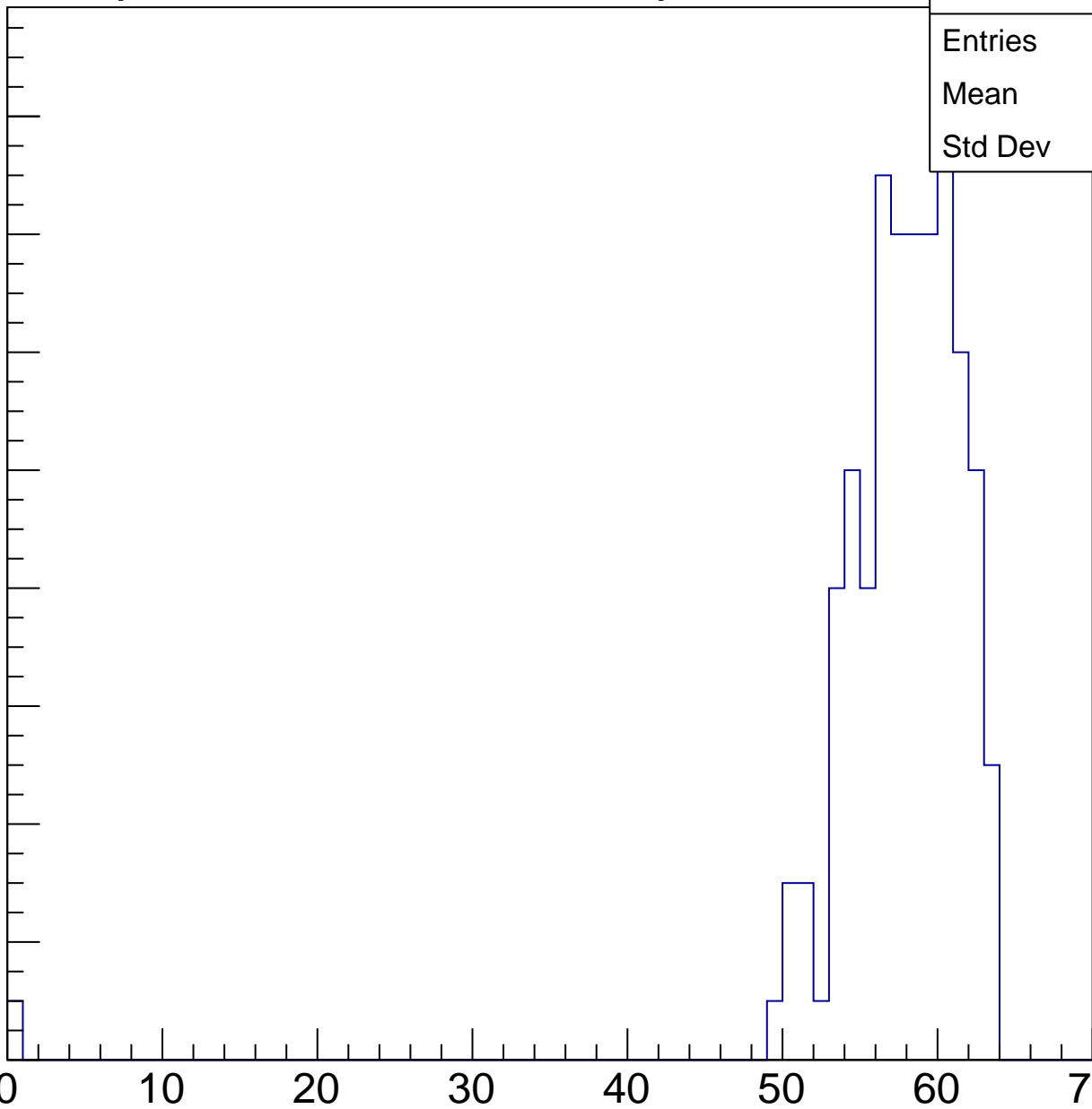
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	57.14
Std Dev	5.879

ampl



B0L101S, U25-ch93, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	44
Mean	59.66
Std Dev	9.305

Entry

12

10

8

6

4

2

0

0

10

20

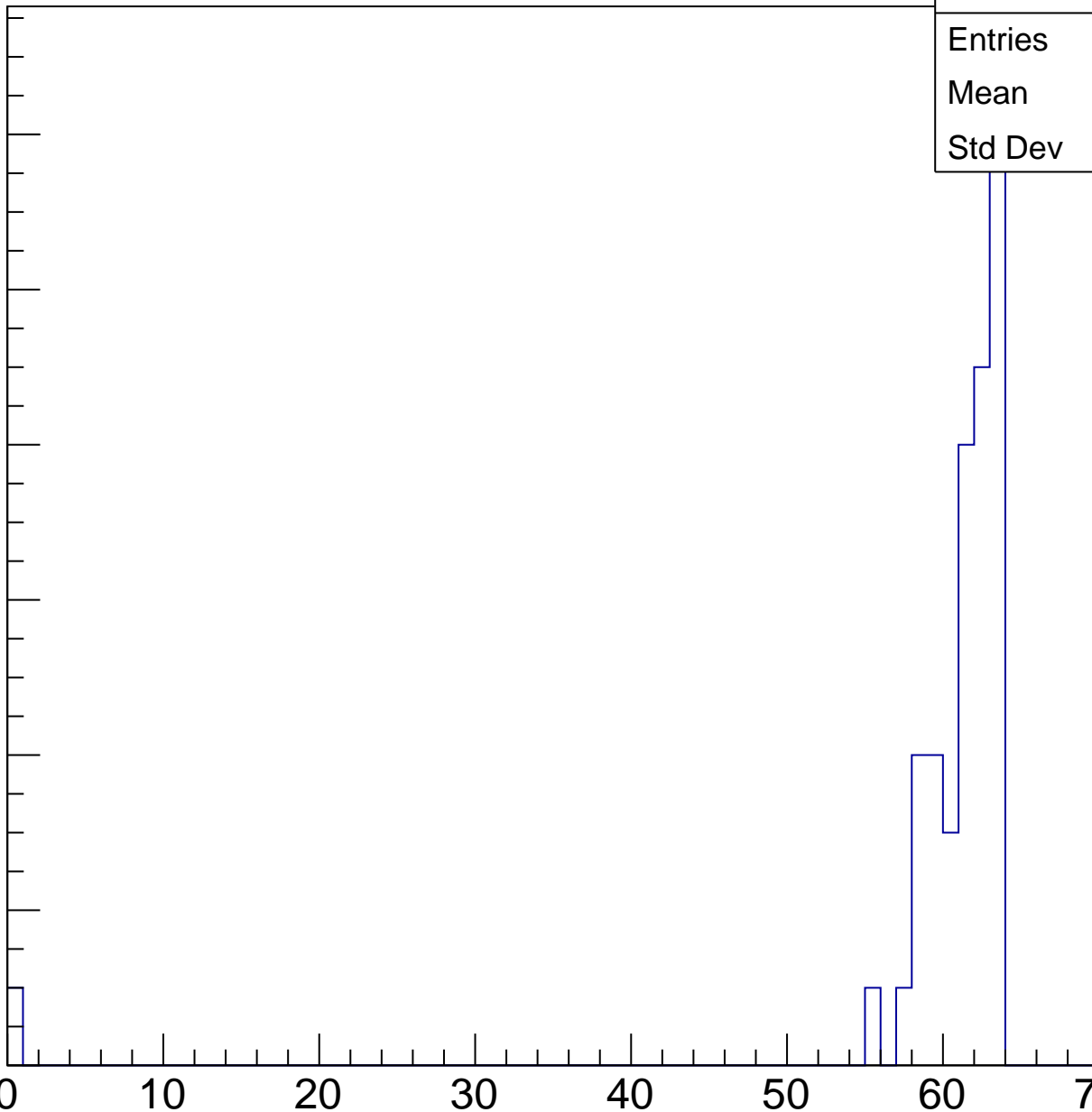
30

40

50

60

ampl



B0L101S, U25-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



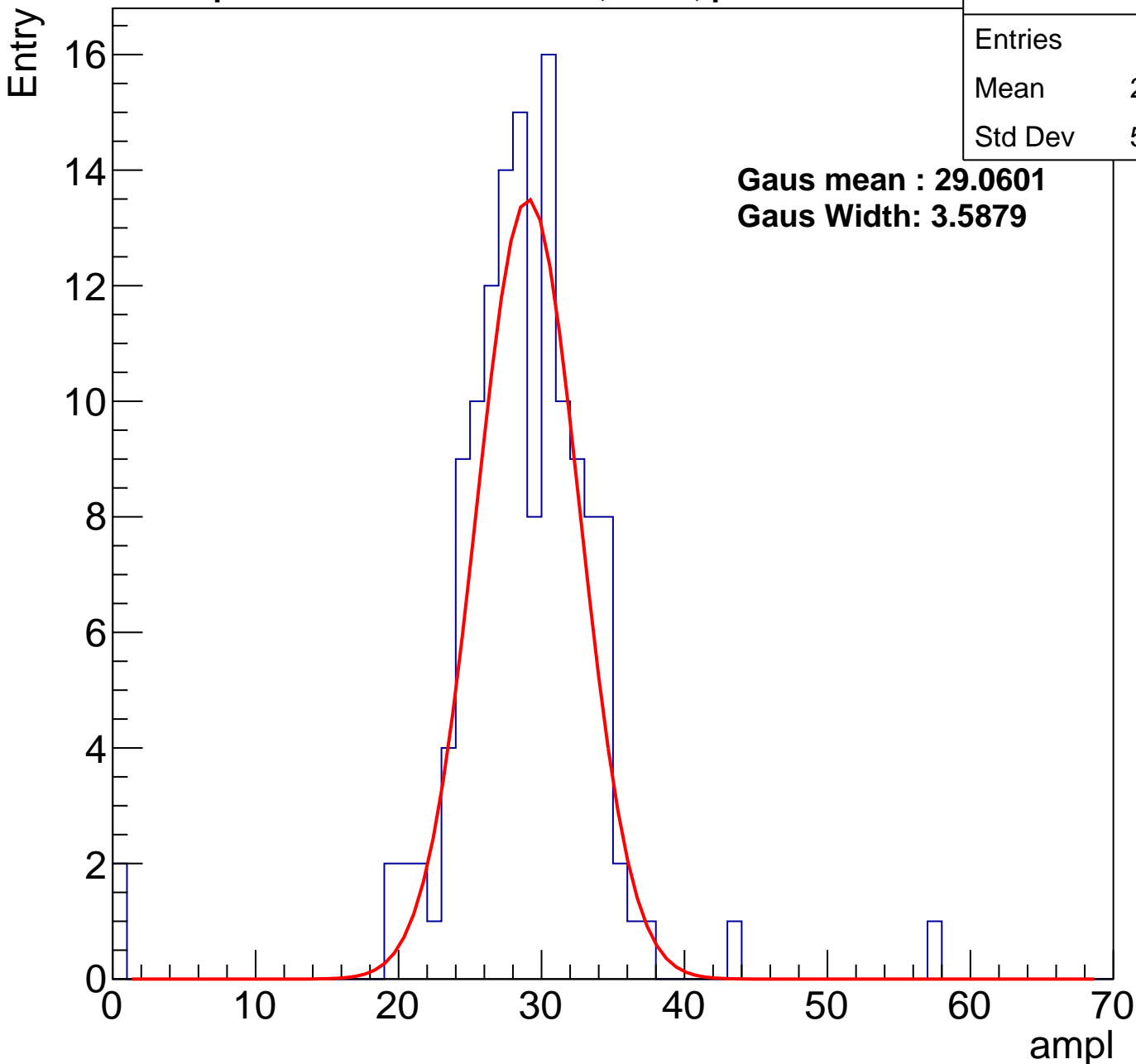
B0L101S, U25-ch94, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.25
Std Dev	5.699

Gaus mean : 29.0601

Gaus Width: 3.5879



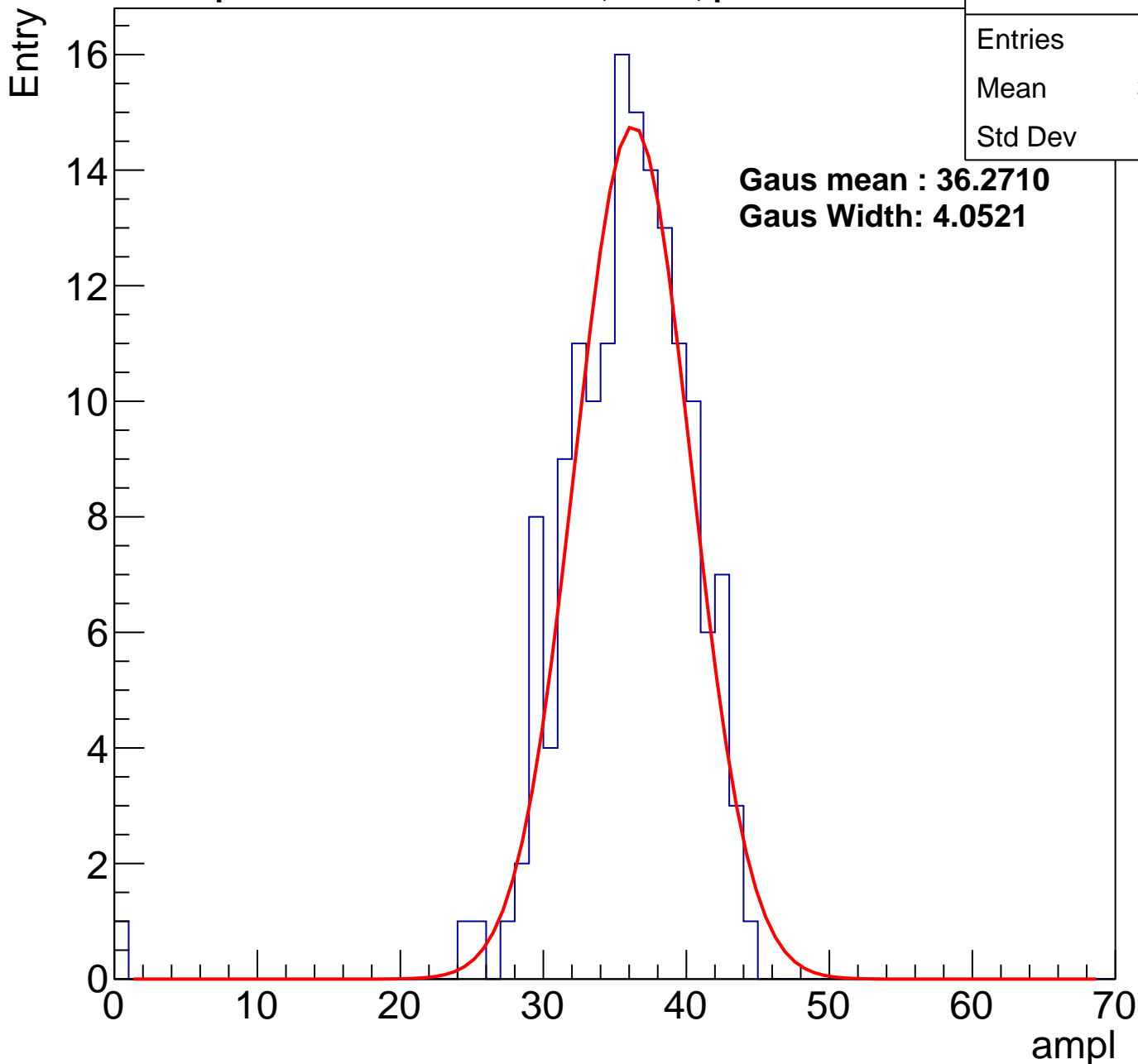
B0L101S, U25-ch94, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	35.31
Std Dev	4.9

Gaus mean : 36.2710

Gaus Width: 4.0521



B0L101S, U25-ch94, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

126

Mean

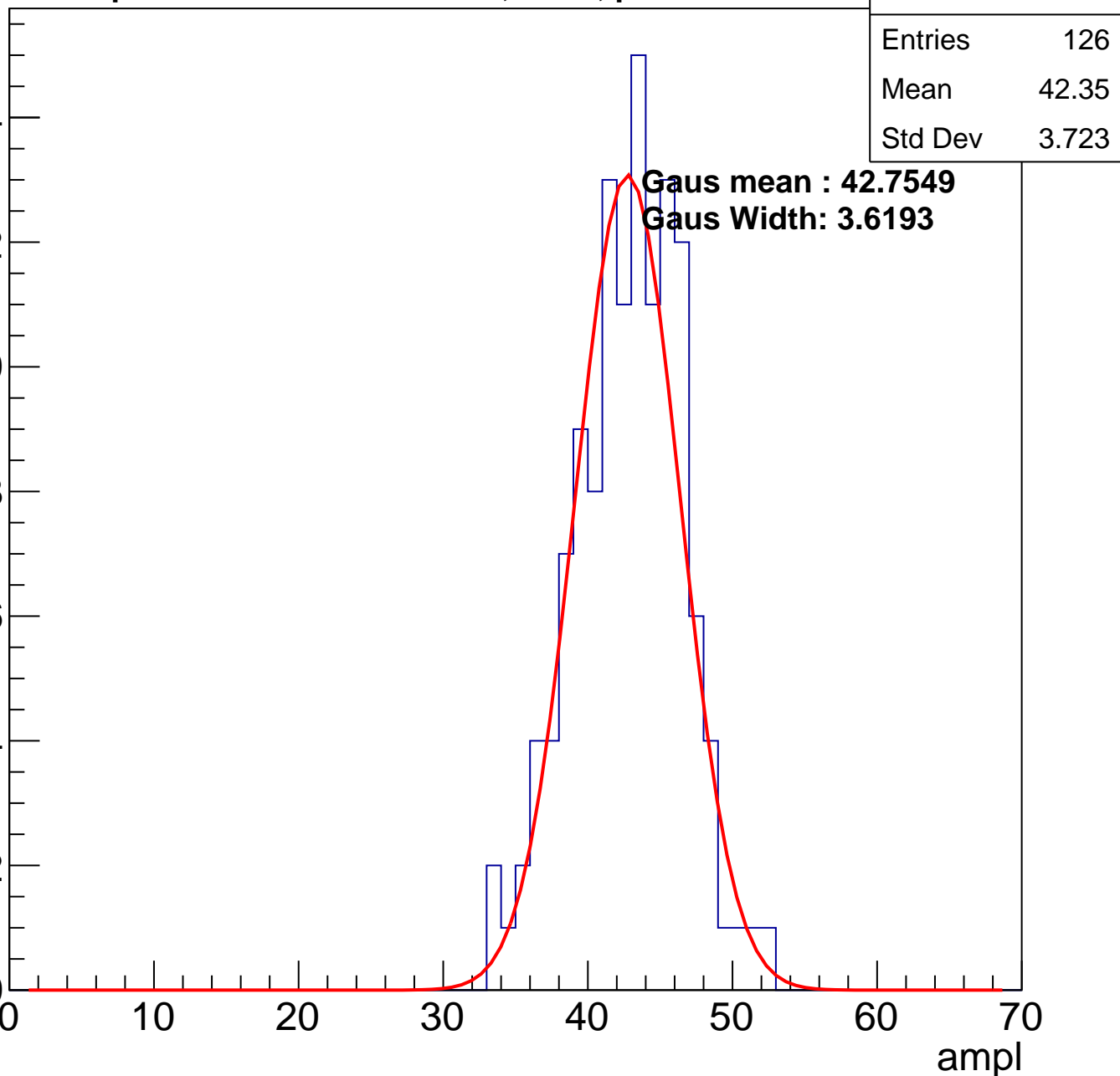
42.35

Std Dev

3.723

Gaus mean : 42.7549

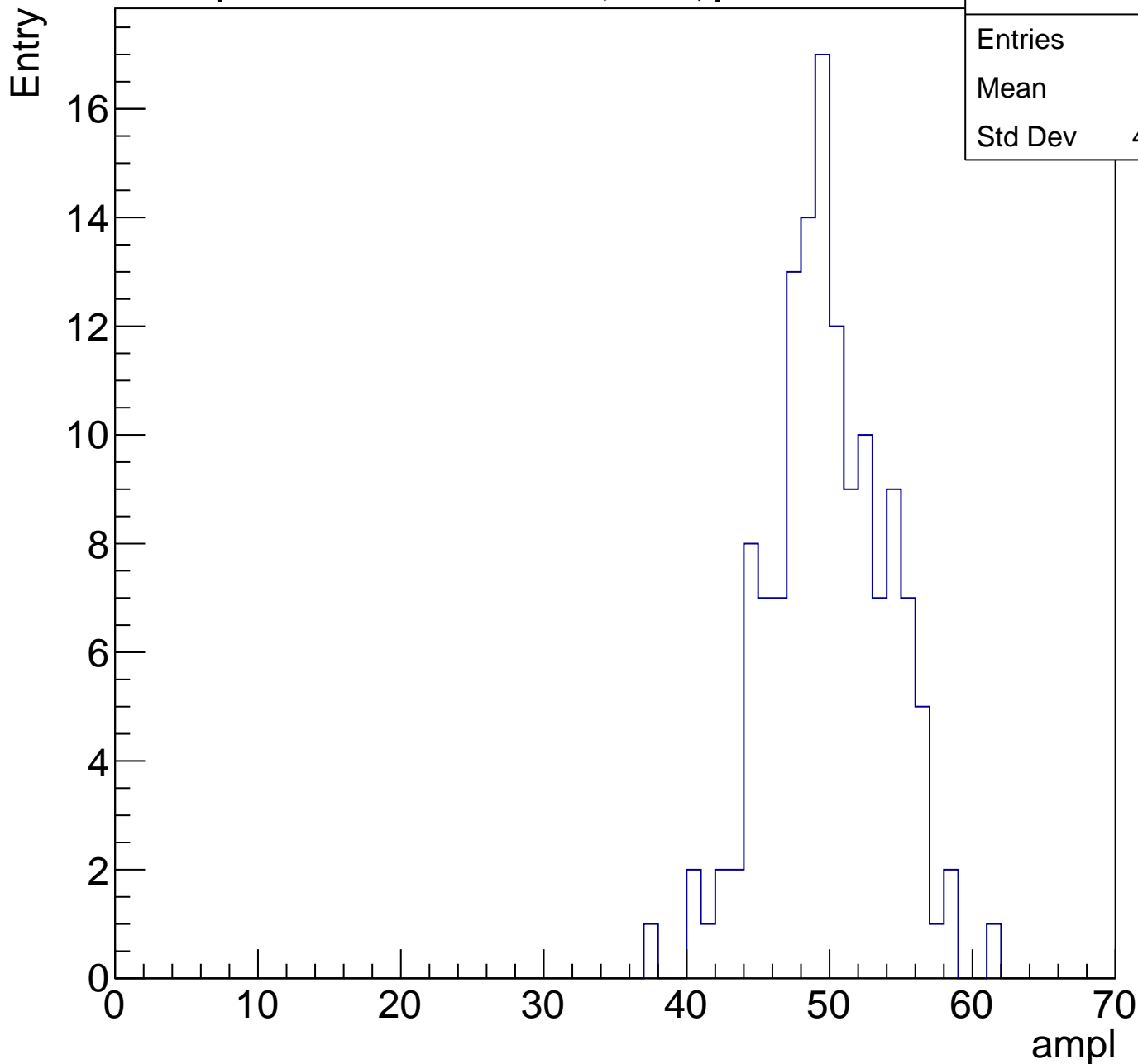
Gaus Width: 3.6193



B0L101S, U25-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	49.4
Std Dev	4.098



B0L101S, U25-ch94, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	54.11
Std Dev	6.461

Entry

10

8

6

4

2

0

0

10

20

30

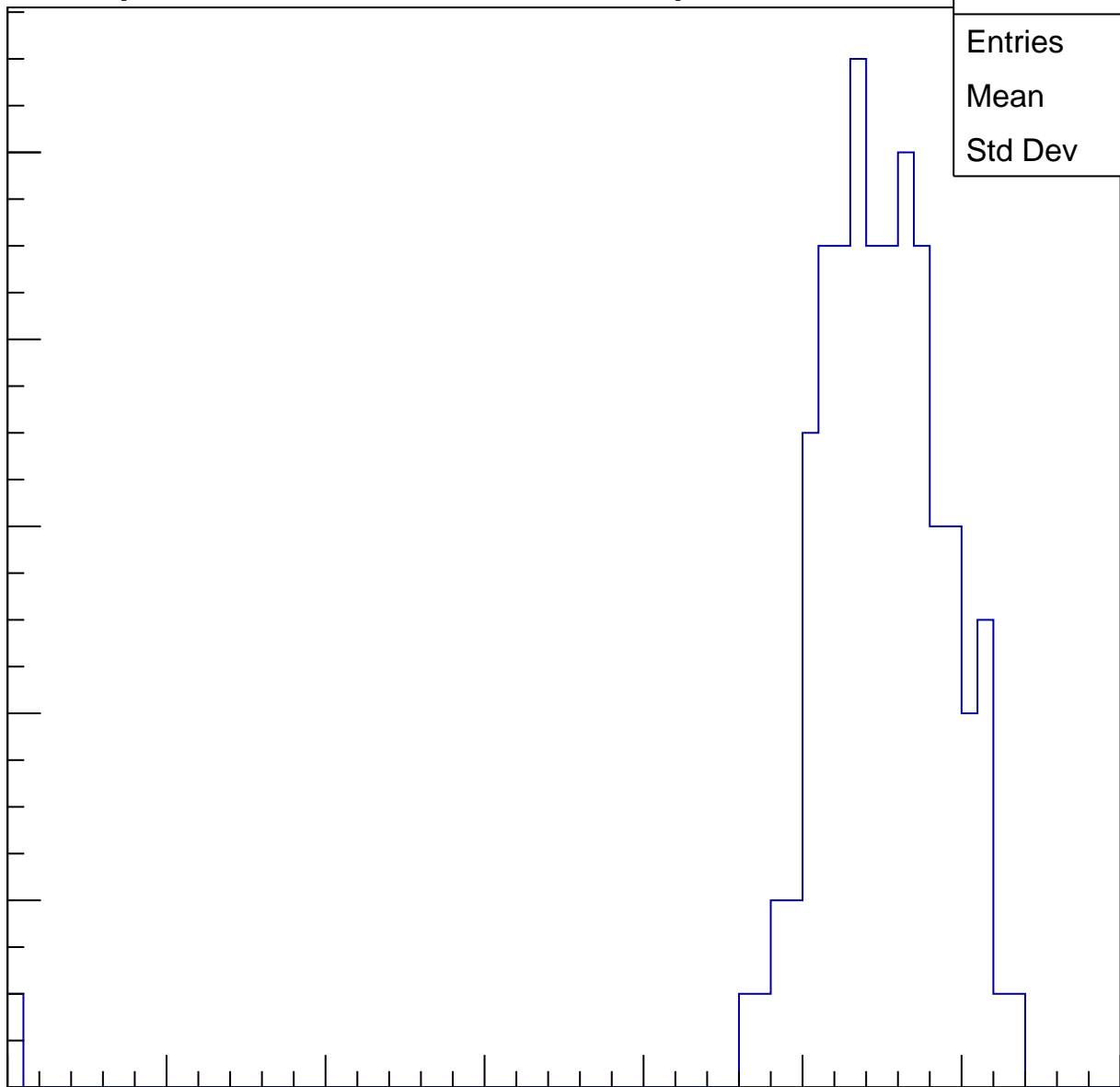
40

50

60

70

ampl



B0L101S, U25-ch94, adc5

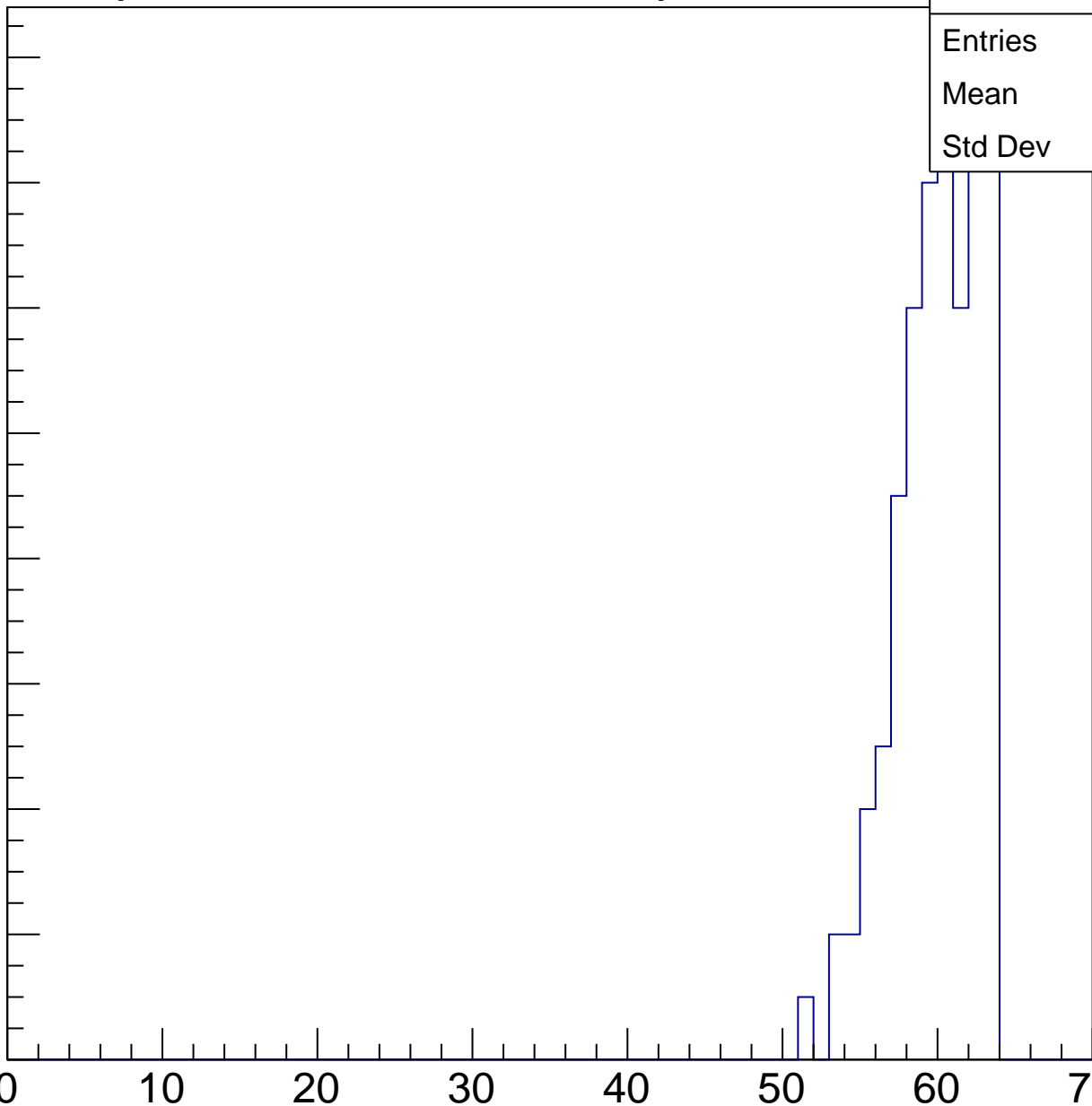
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	59.52
Std Dev	2.666

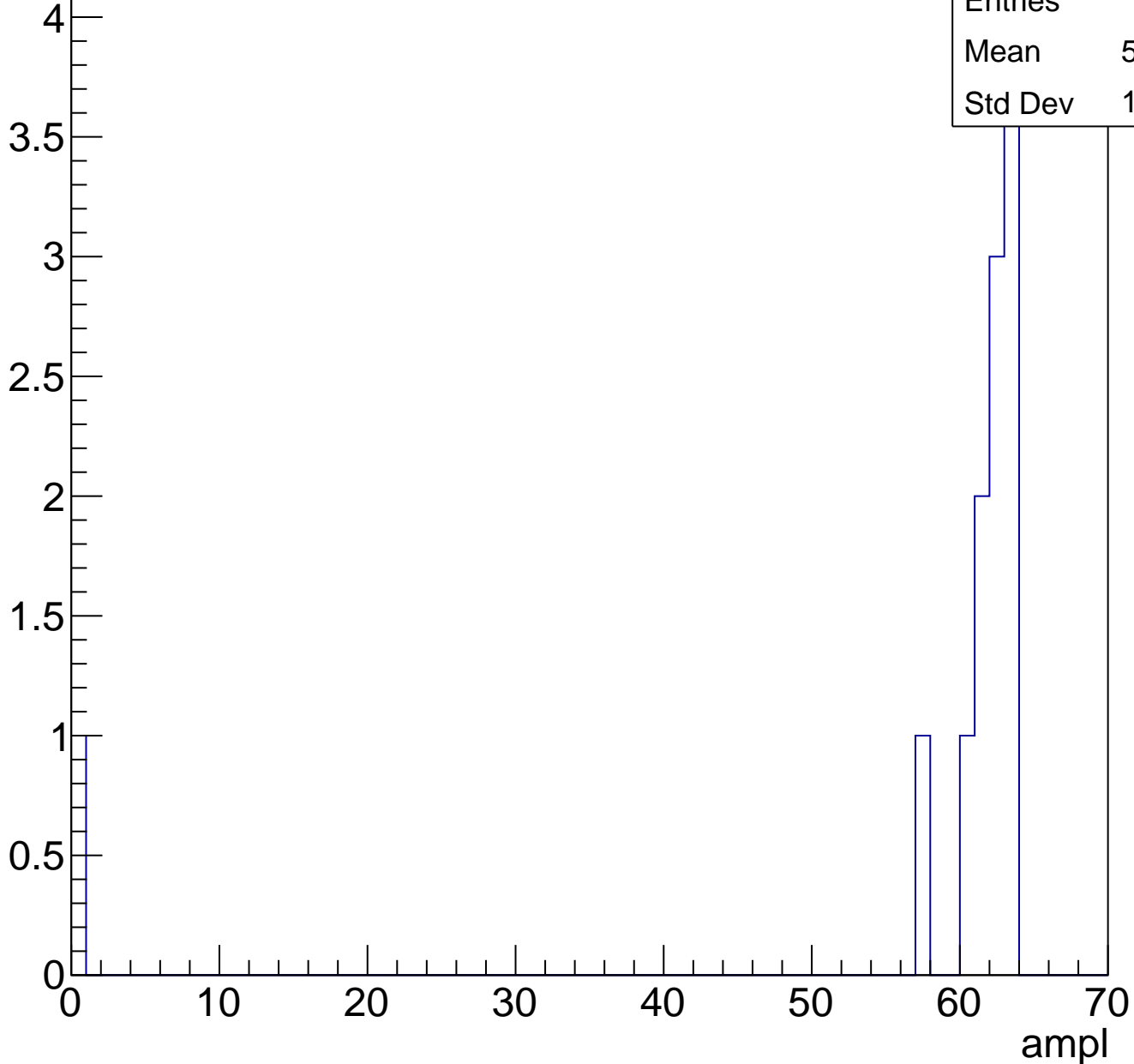
ampl



B0L101S, U25-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	12
Mean	56.42
Std Dev	17.09

B0L101S, U25-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch95, adc0

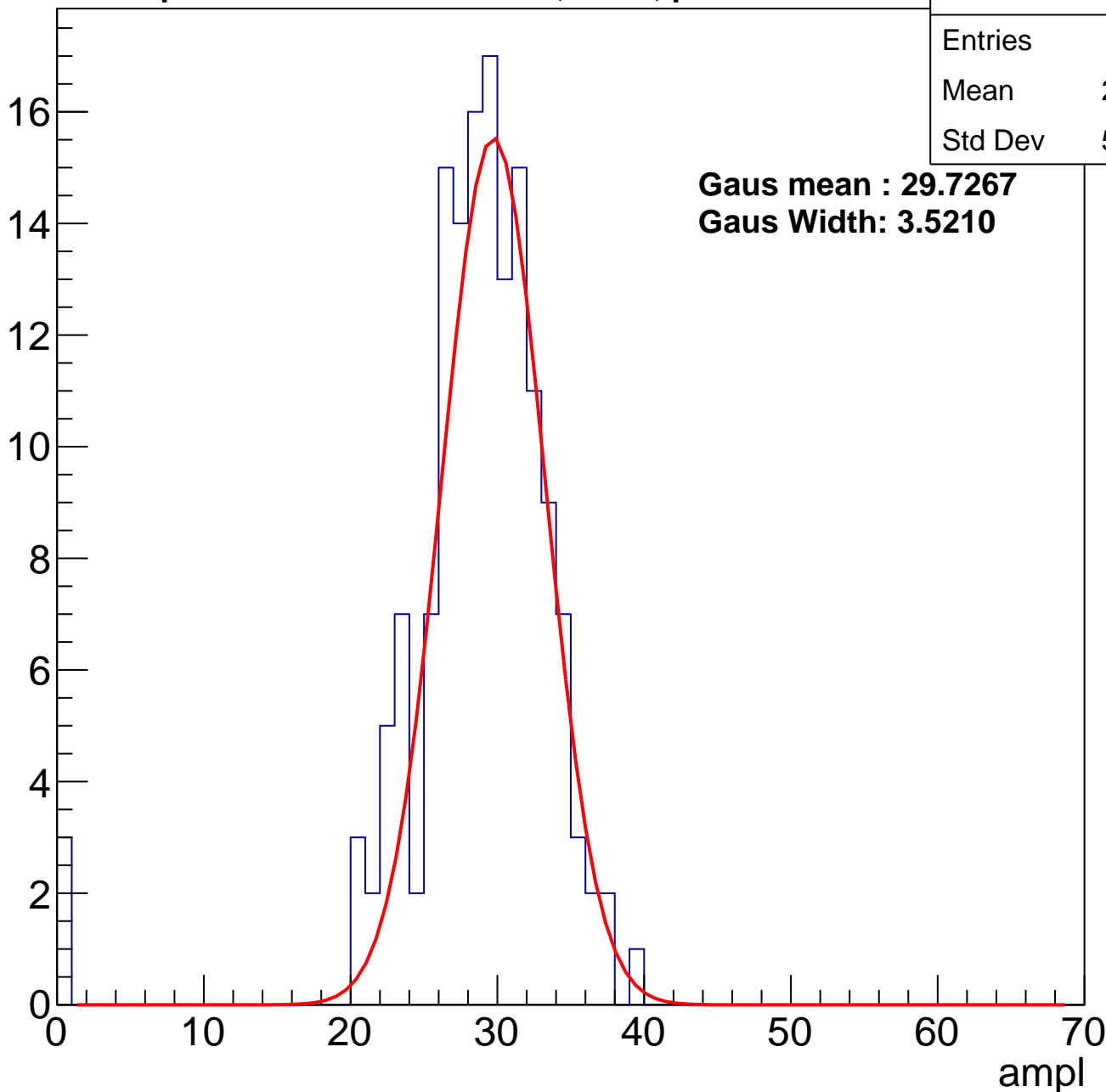
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	28.16
Std Dev	5.452

Gaus mean : 29.7267

Gaus Width: 3.5210

Entry



B0L101S, U25-ch95, adc1

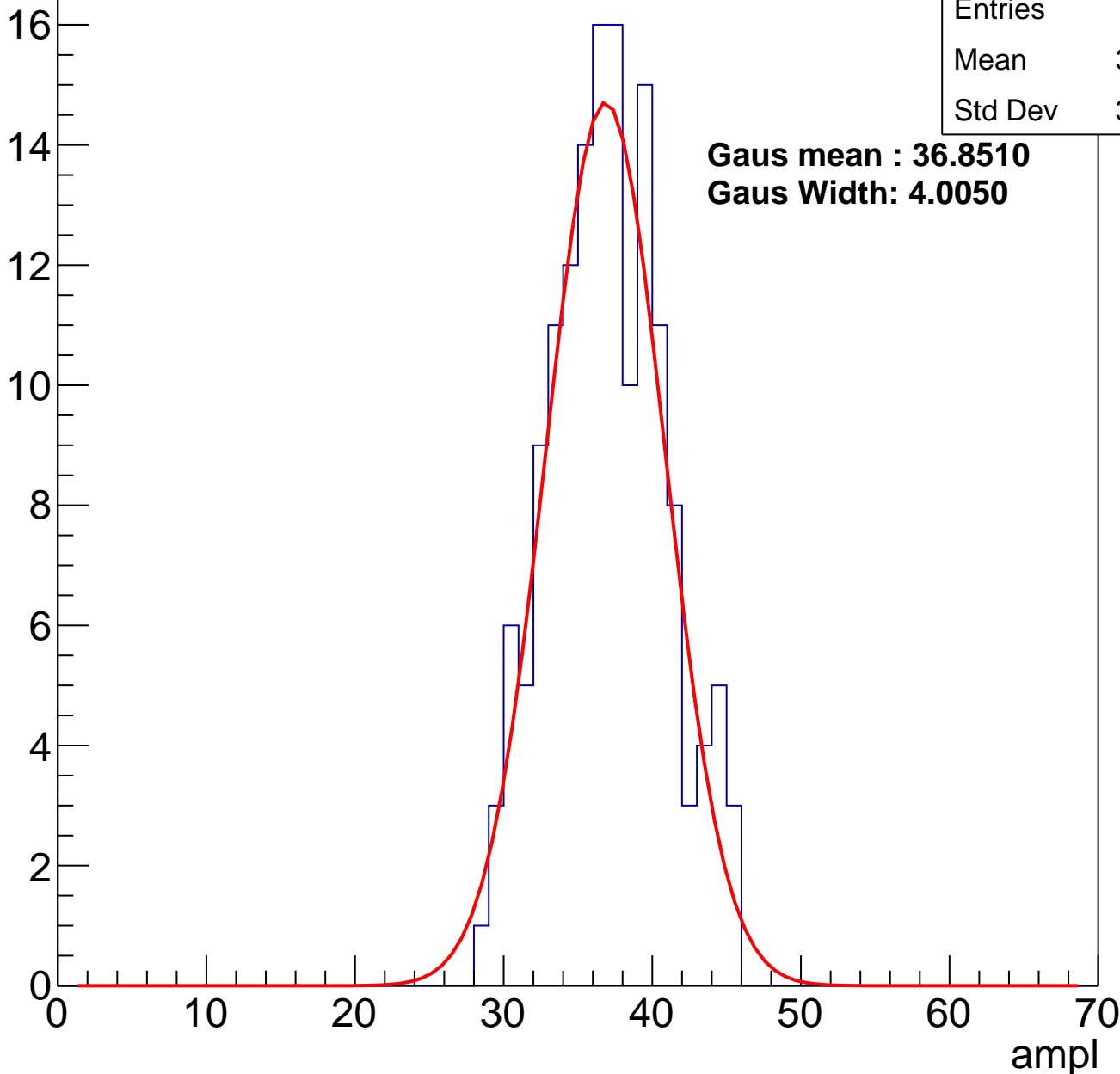
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	36.53
Std Dev	3.846

Gaus mean : 36.8510

Gaus Width: 4.0050

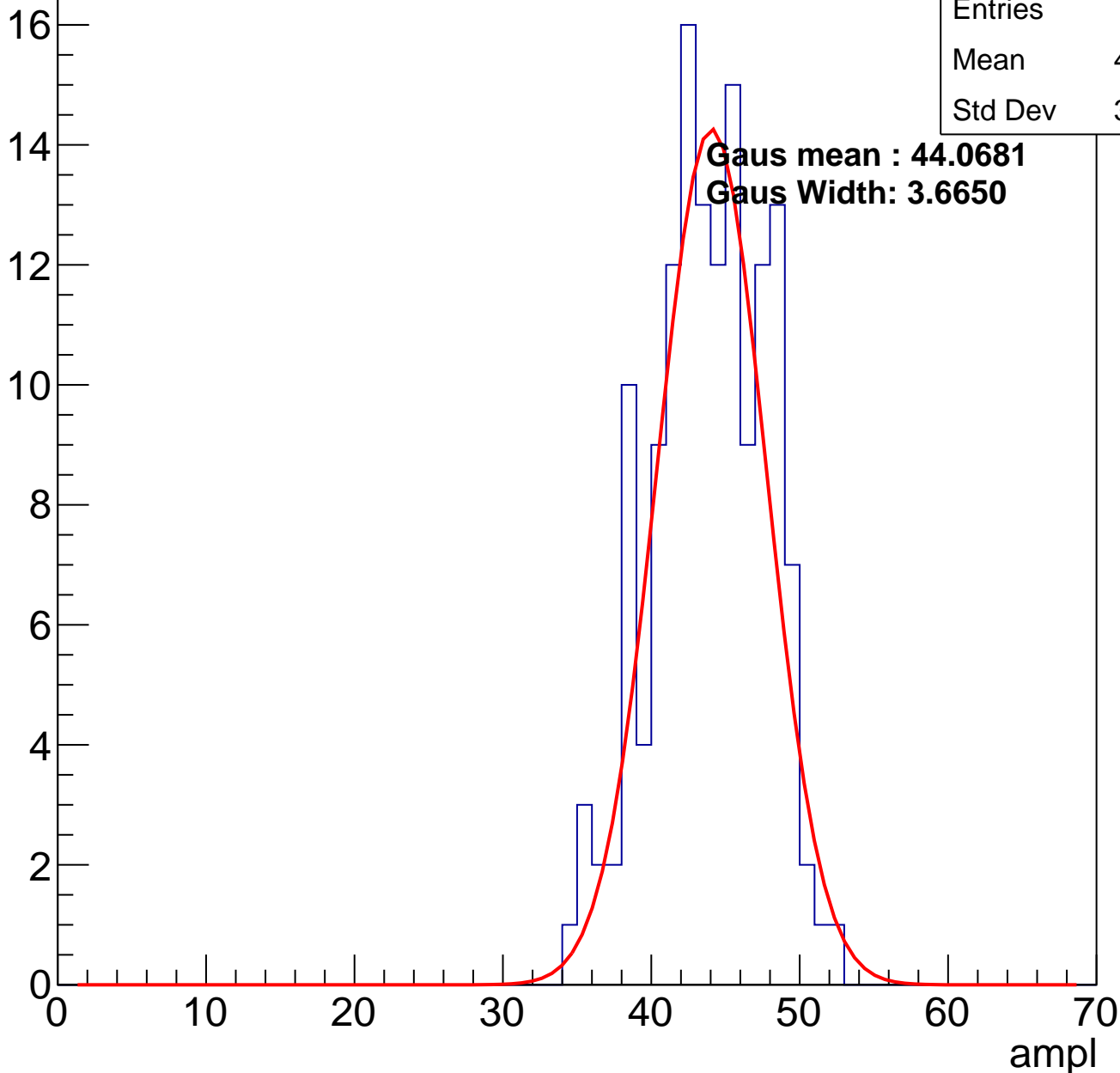
Entry



B0L101S, U25-ch95, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

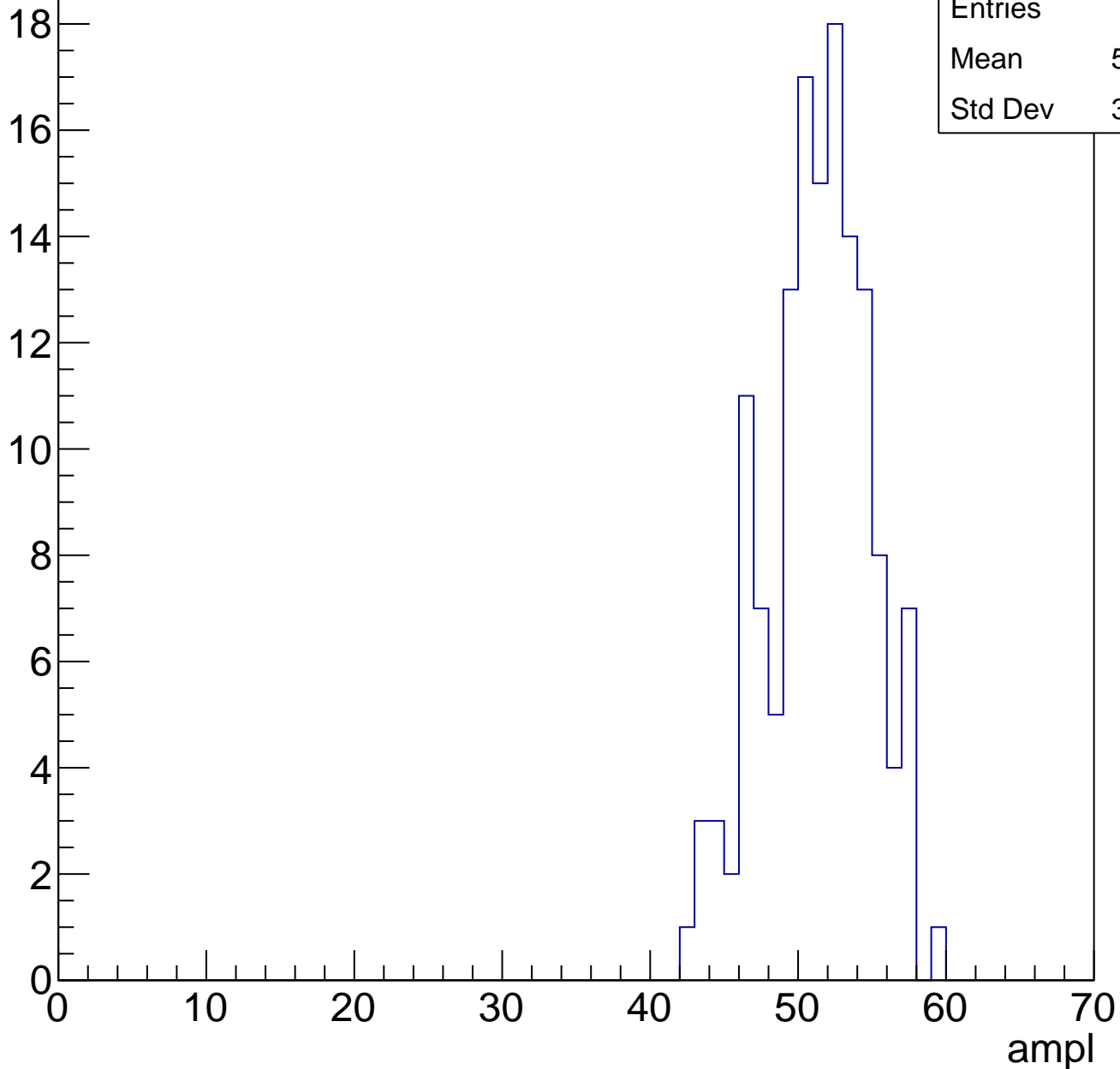


B0L101S, U25-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	50.86
Std Dev	3.486

Entry



B0L101S, U25-ch95, adc4

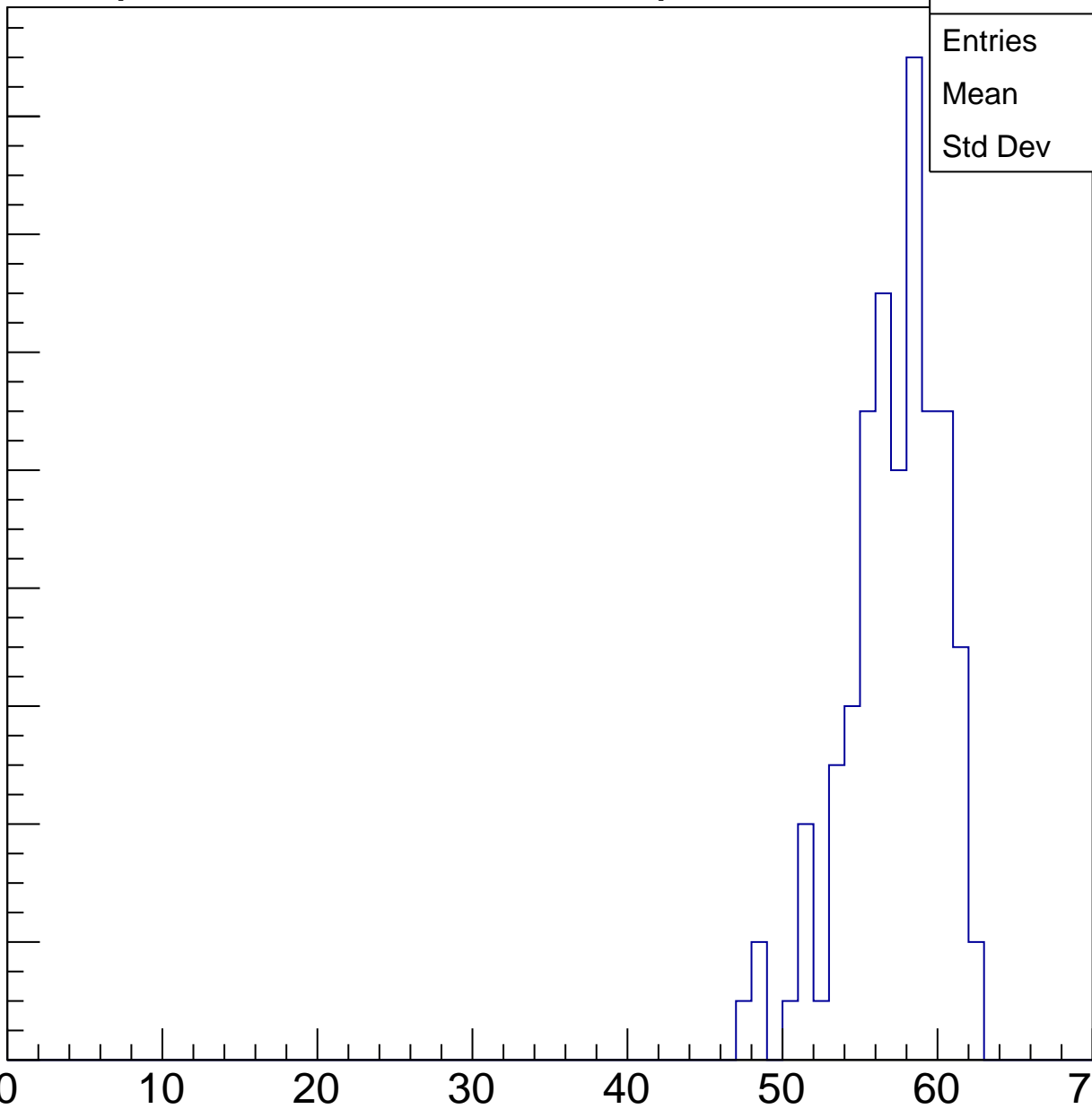
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	56.74
Std Dev	3.109

ampl



B0L101S, U25-ch95, adc5

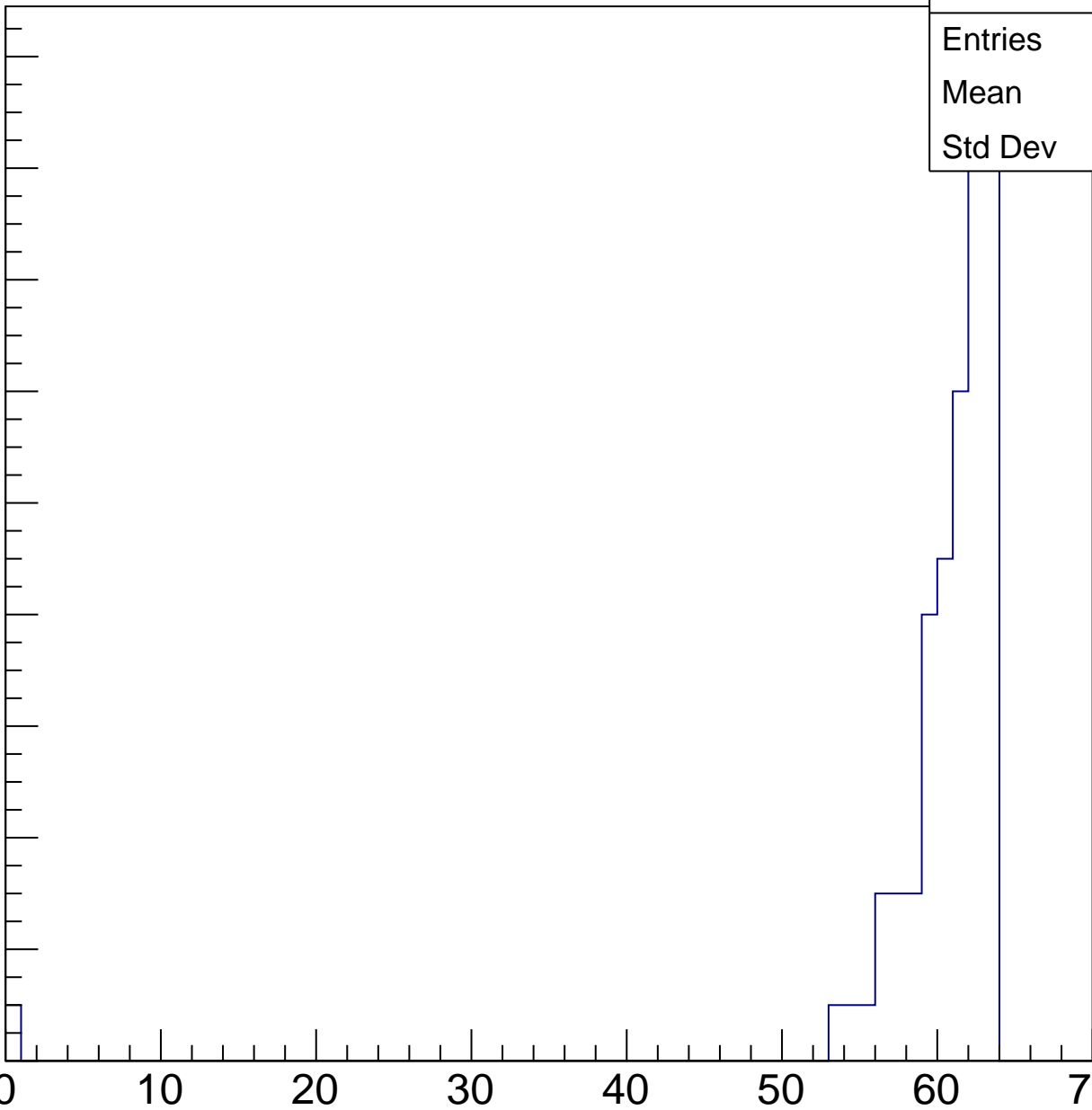
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.86
Std Dev	7.205

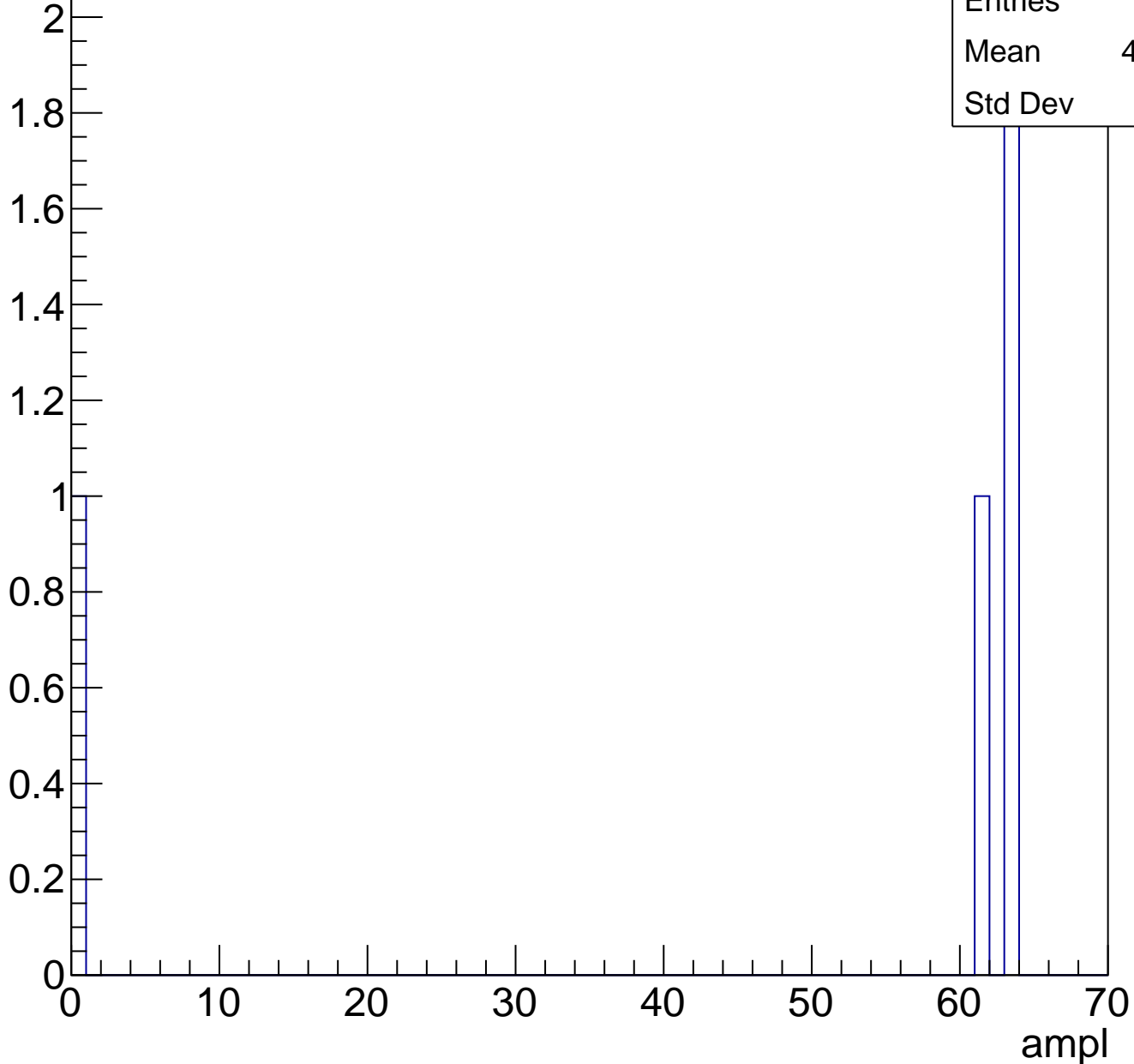
ampl



B0L101S, U25-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch96, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	28.3
Std Dev	4.003

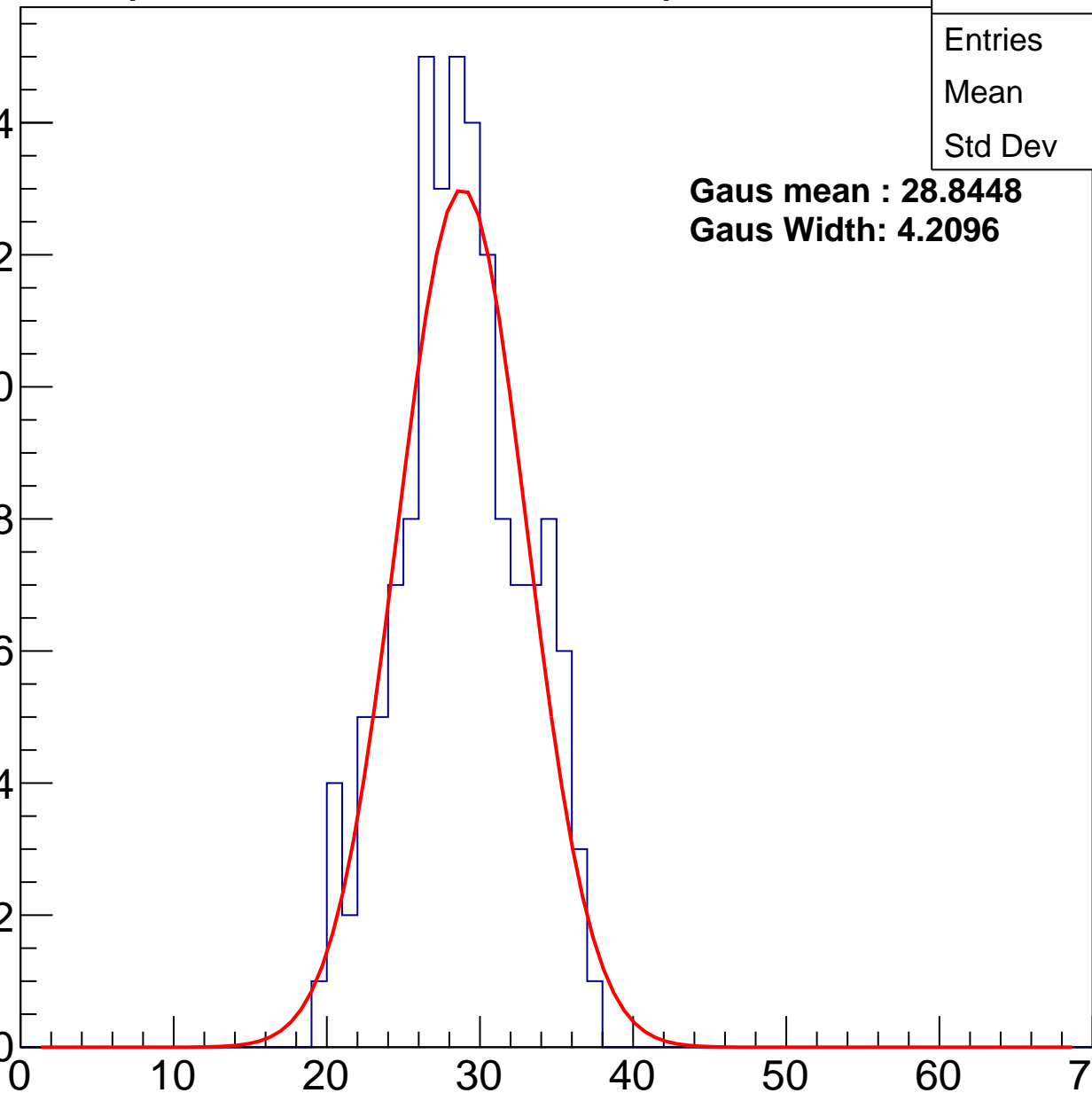
Gaus mean : 28.8448

Gaus Width: 4.2096

Entry

14
12
10
8
6
4
2
0

ampl



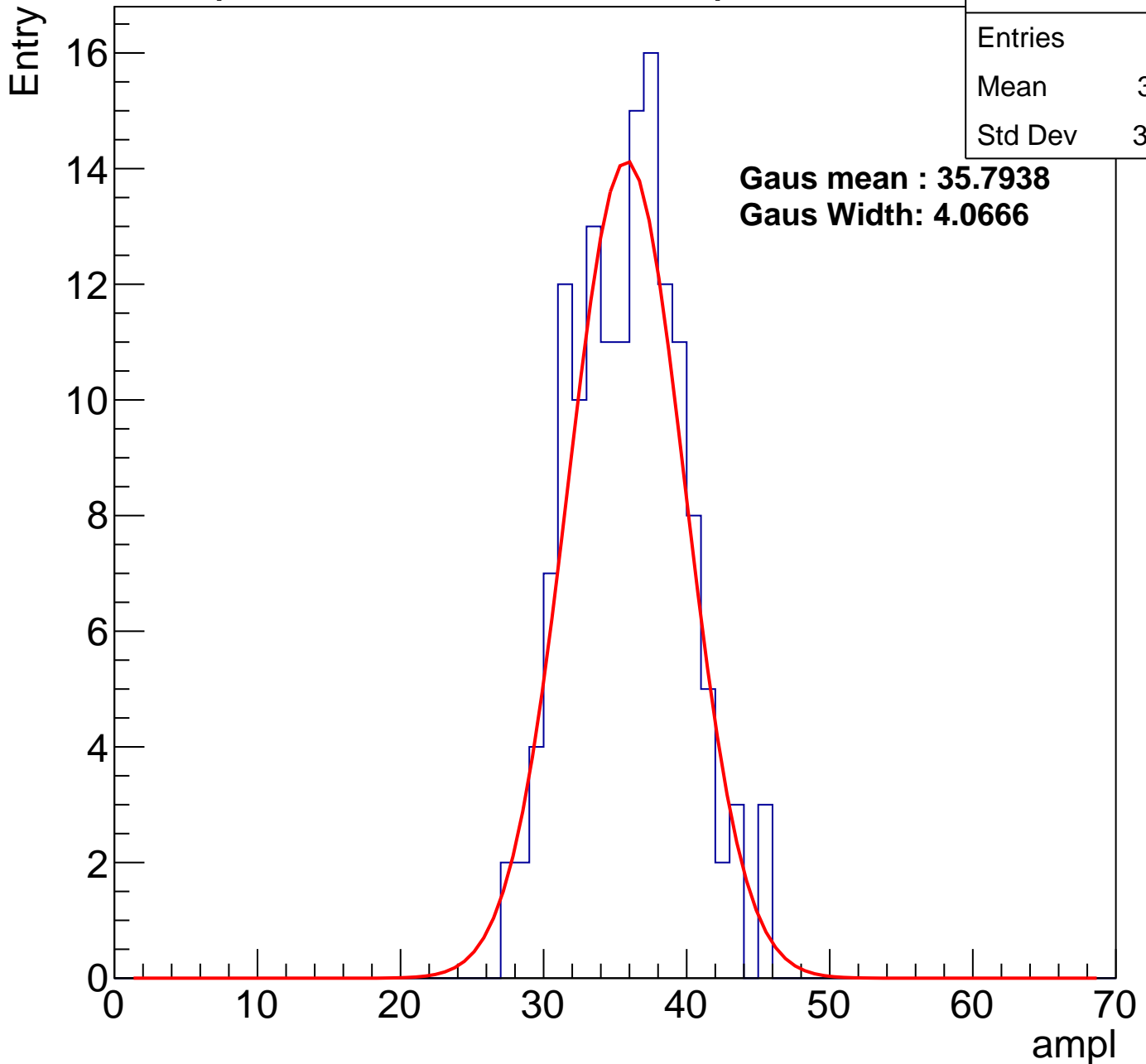
B0L101S, U25-ch96, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	35.41
Std Dev	3.867

Gaus mean : 35.7938

Gaus Width: 4.0666



B0L101S, U25-ch96, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

42.54

Std Dev

3.52

Gaus mean : 43.2210

Gaus Width: 3.6320

0

10

20

30

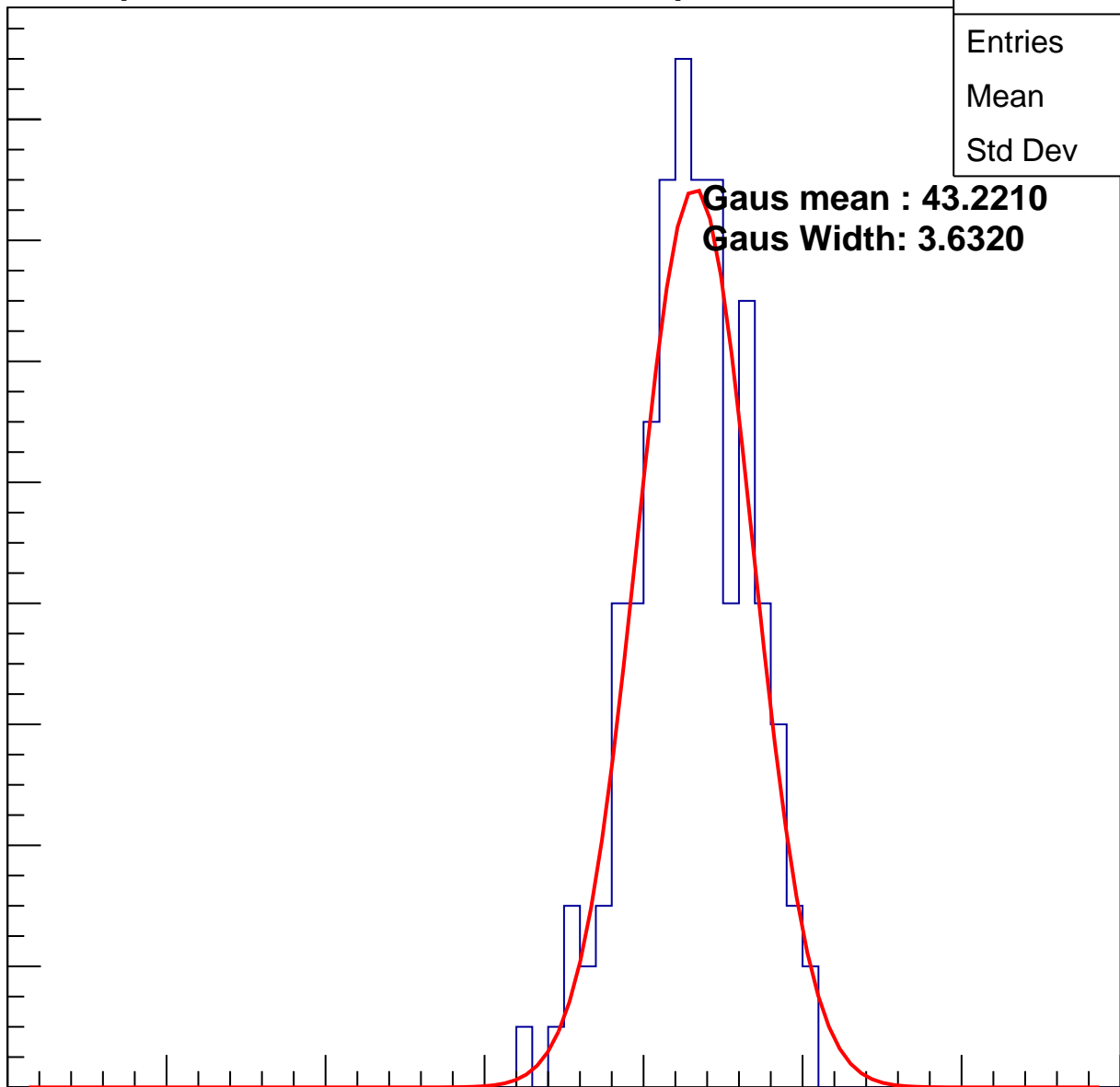
40

50

60

70

ampl



B0L101S, U25-ch96, adc3

calib_packv5_042523_0143.root, FC#1, port C1

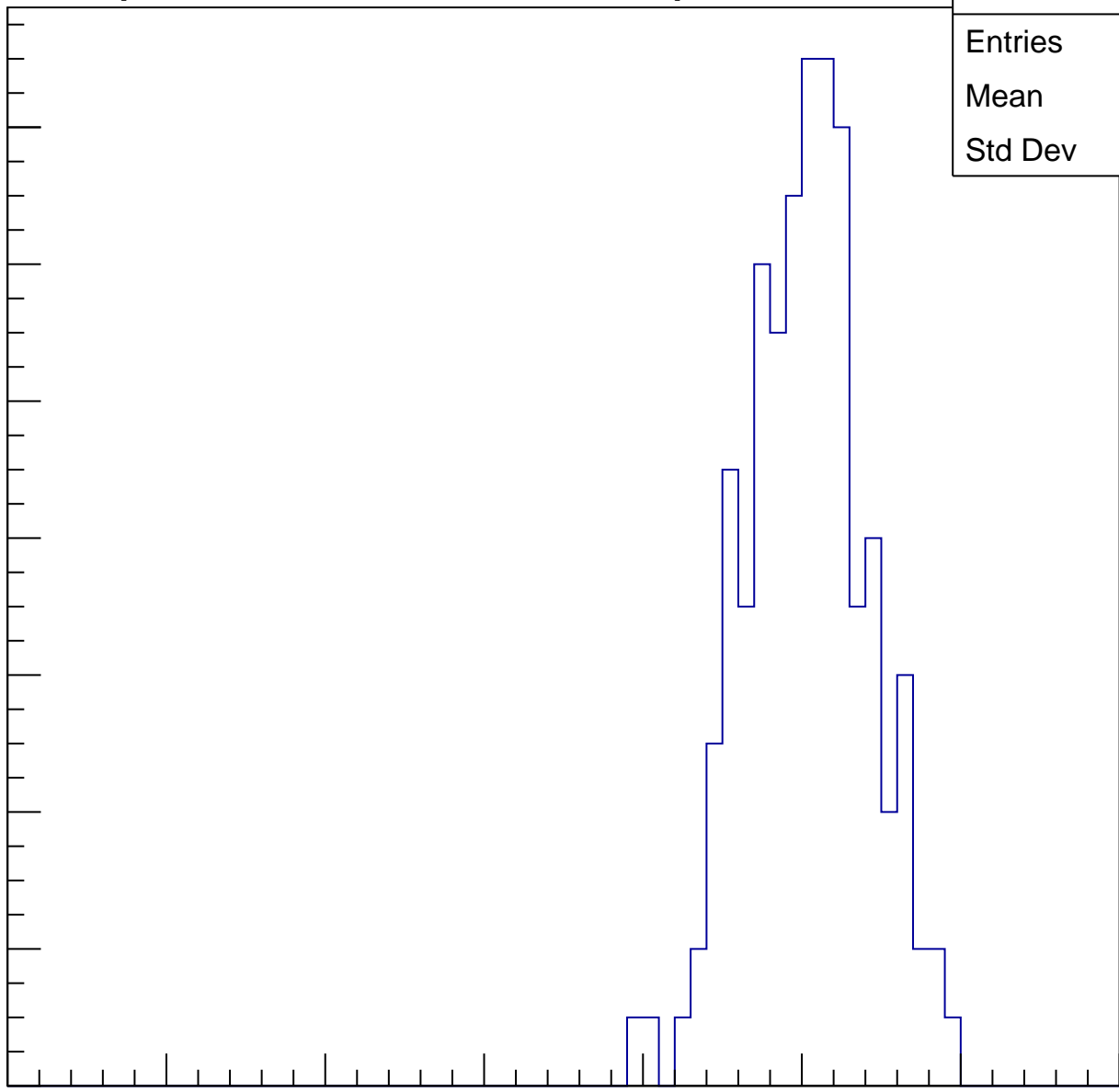
Entries	136
Mean	49.81
Std Dev	3.782

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch96, adc4

calib_packv5_042523_0143.root, FC#1, port C1

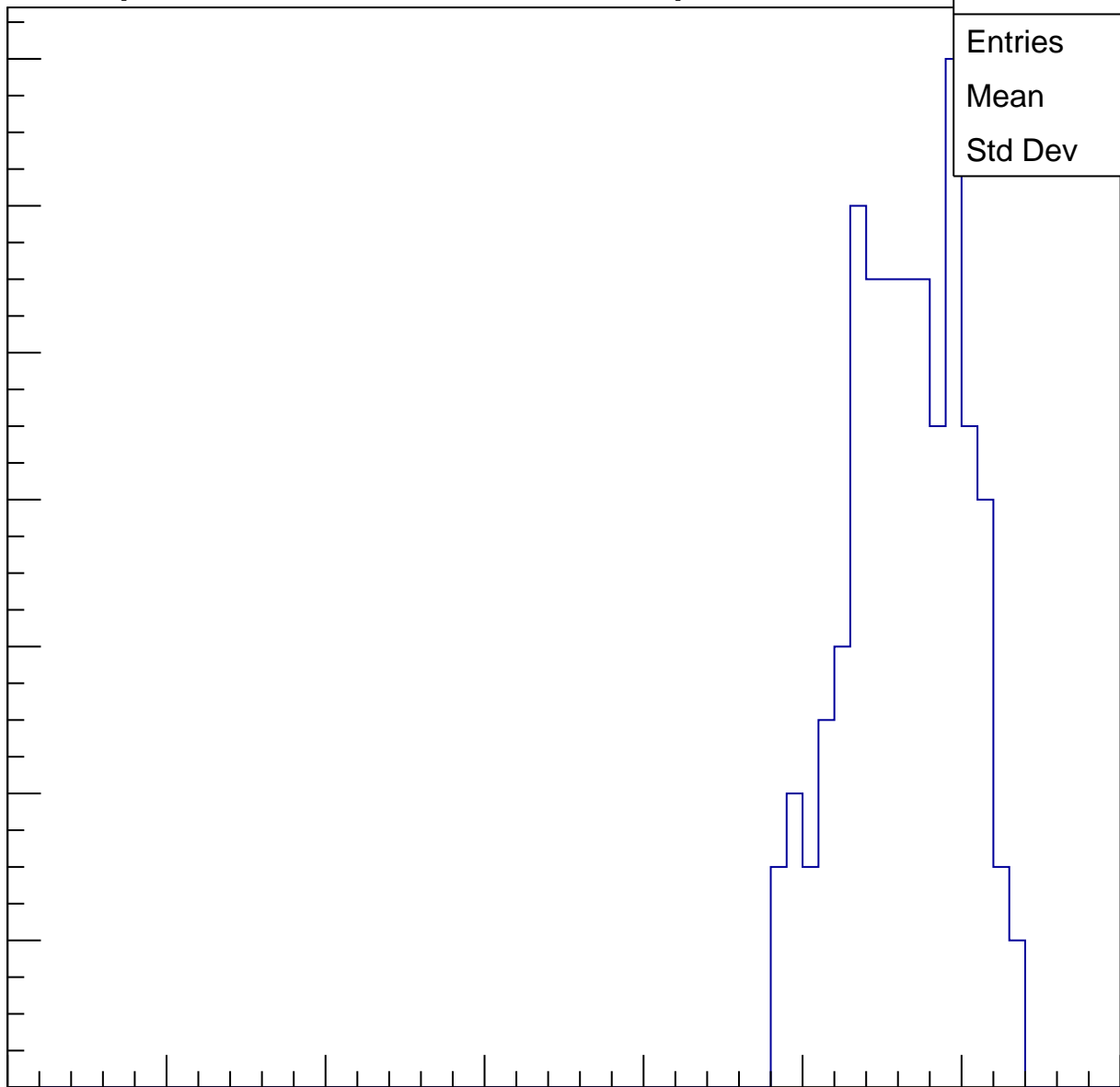
Entries	122
Mean	55.93
Std Dev	3.624

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

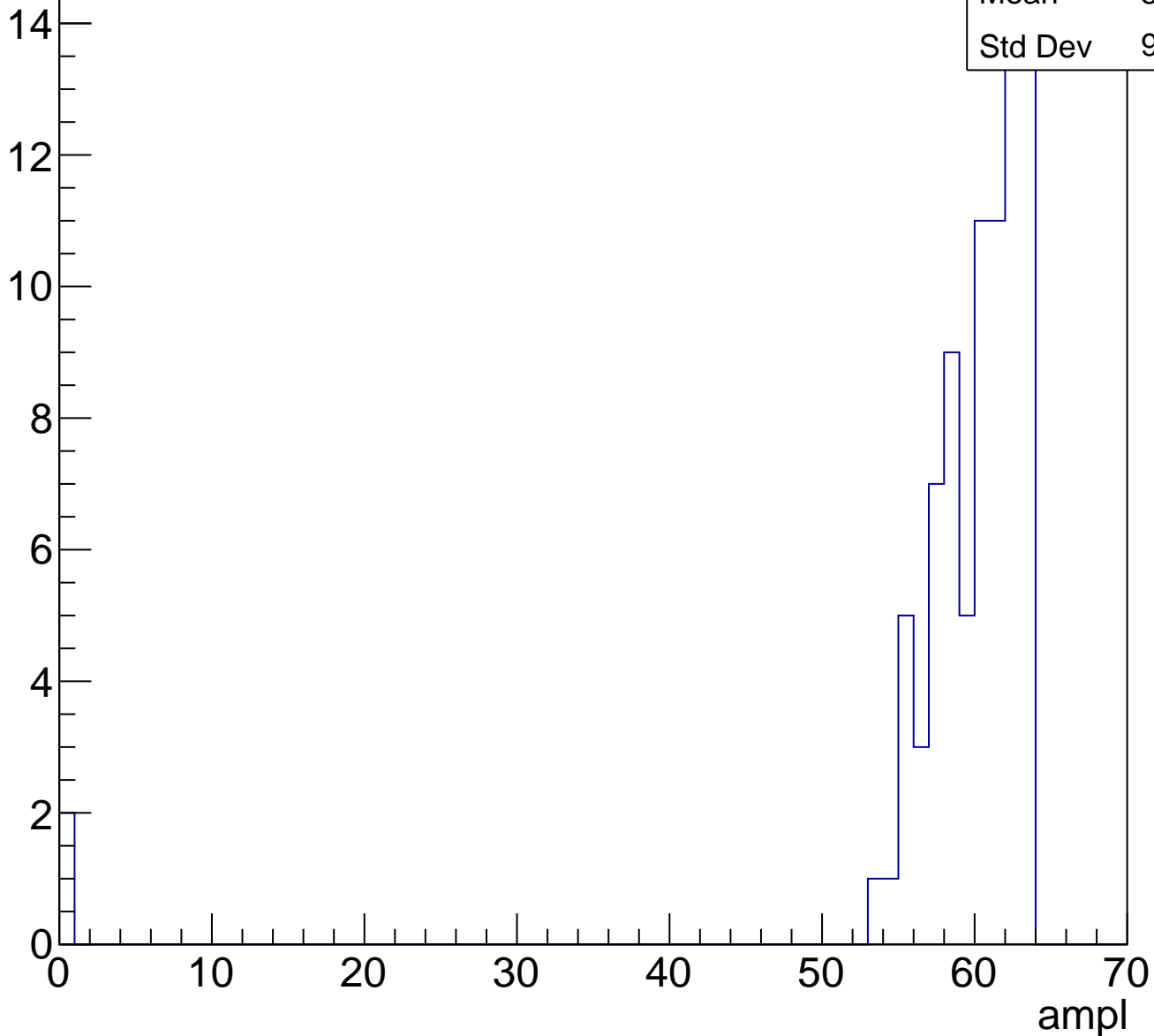
ampl



B0L101S, U25-ch96, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

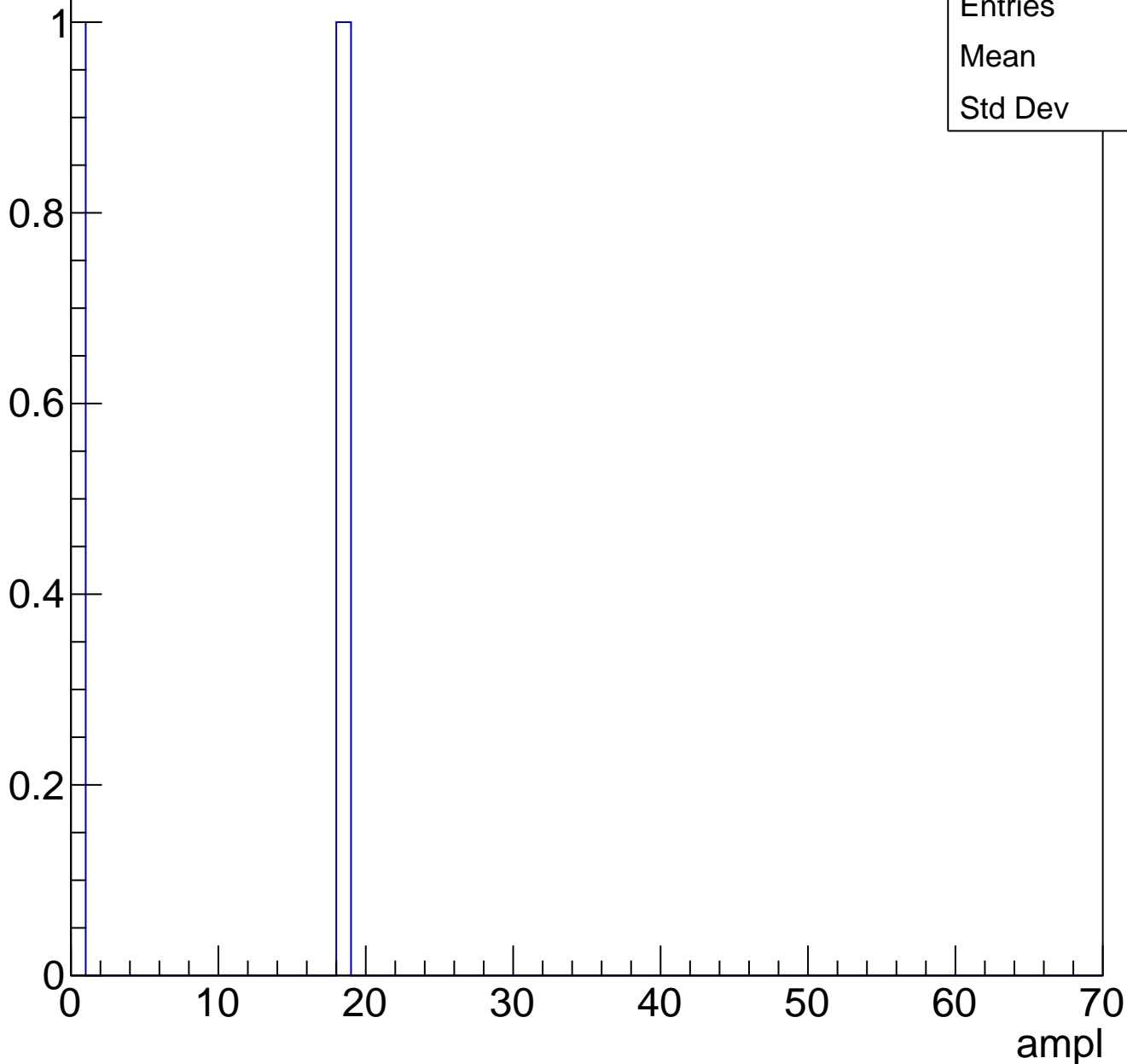
Entry



B0L101S, U25-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch97, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

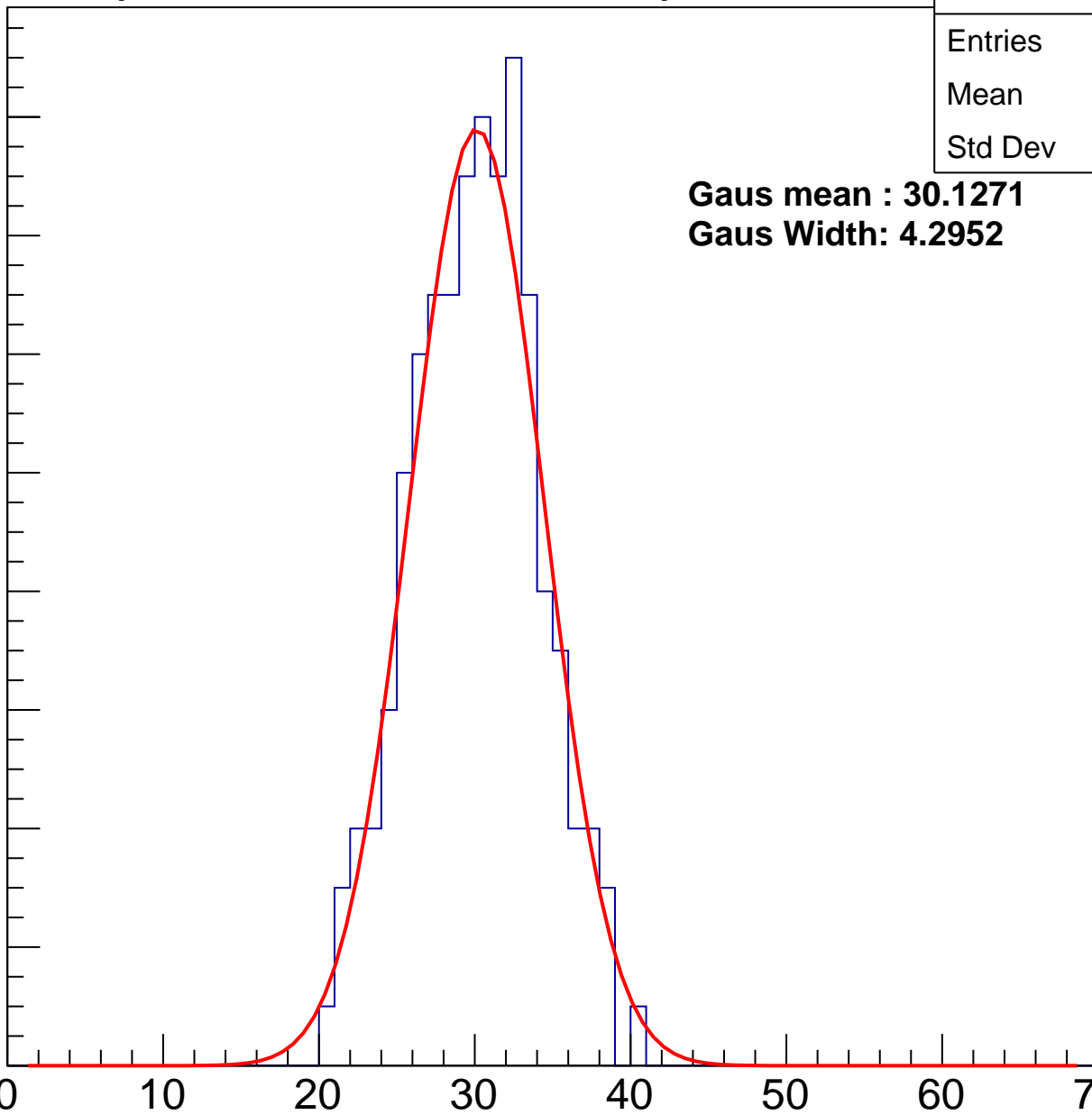
16
14
12
10
8
6
4
2
0

Entries	169
Mean	29.59
Std Dev	4.029

Gaus mean : 30.1271

Gaus Width: 4.2952

0 10 20 30 40 50 60 70
ampl



B0L101S, U25-ch97, adc1

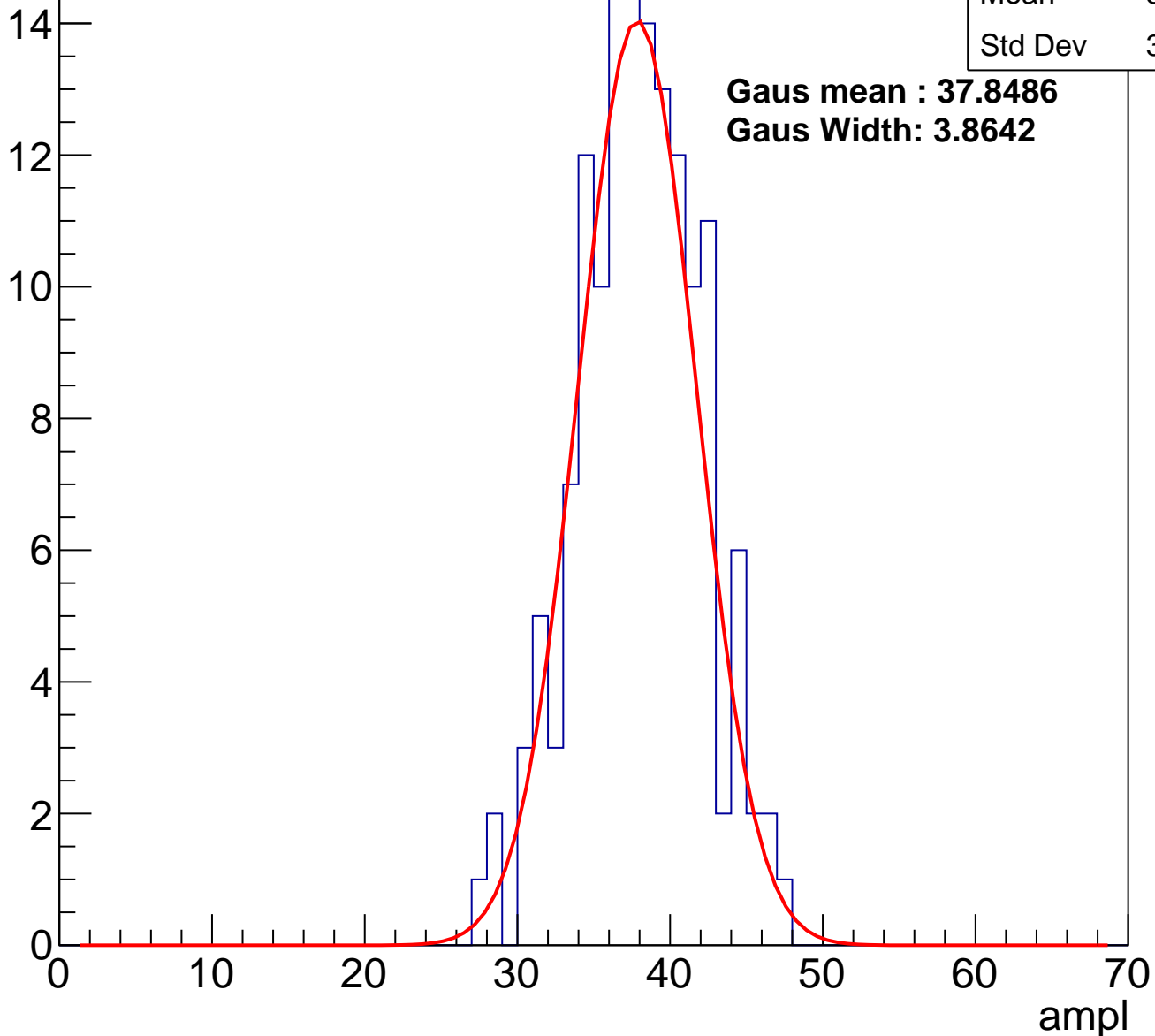
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	37.52
Std Dev	3.938

Gaus mean : 37.8486

Gaus Width: 3.8642

Entry



B0L101S, U25-ch97, adc2

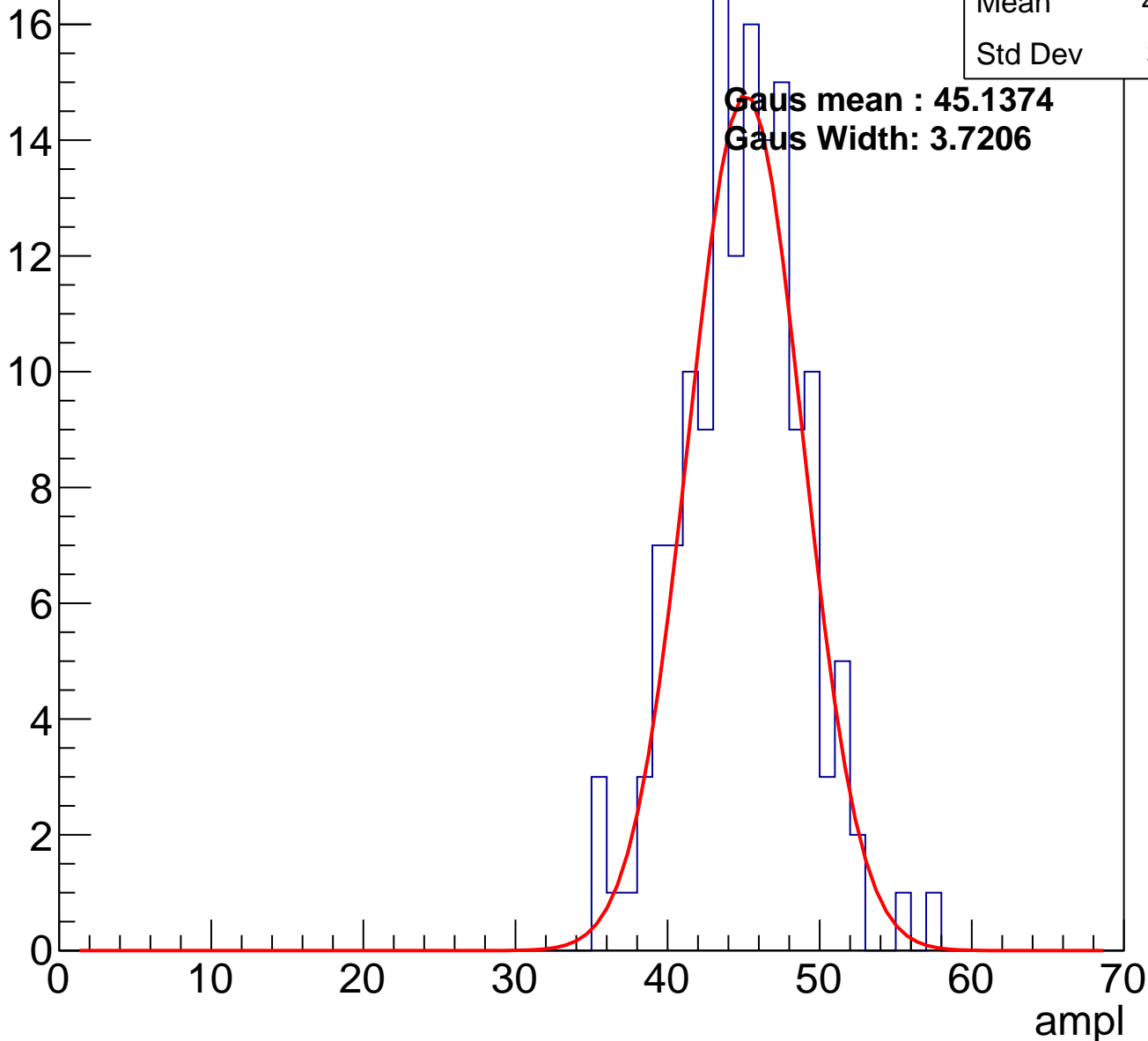
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	146
Mean	44.55
Std Dev	3.901

Gaus mean : 45.1374

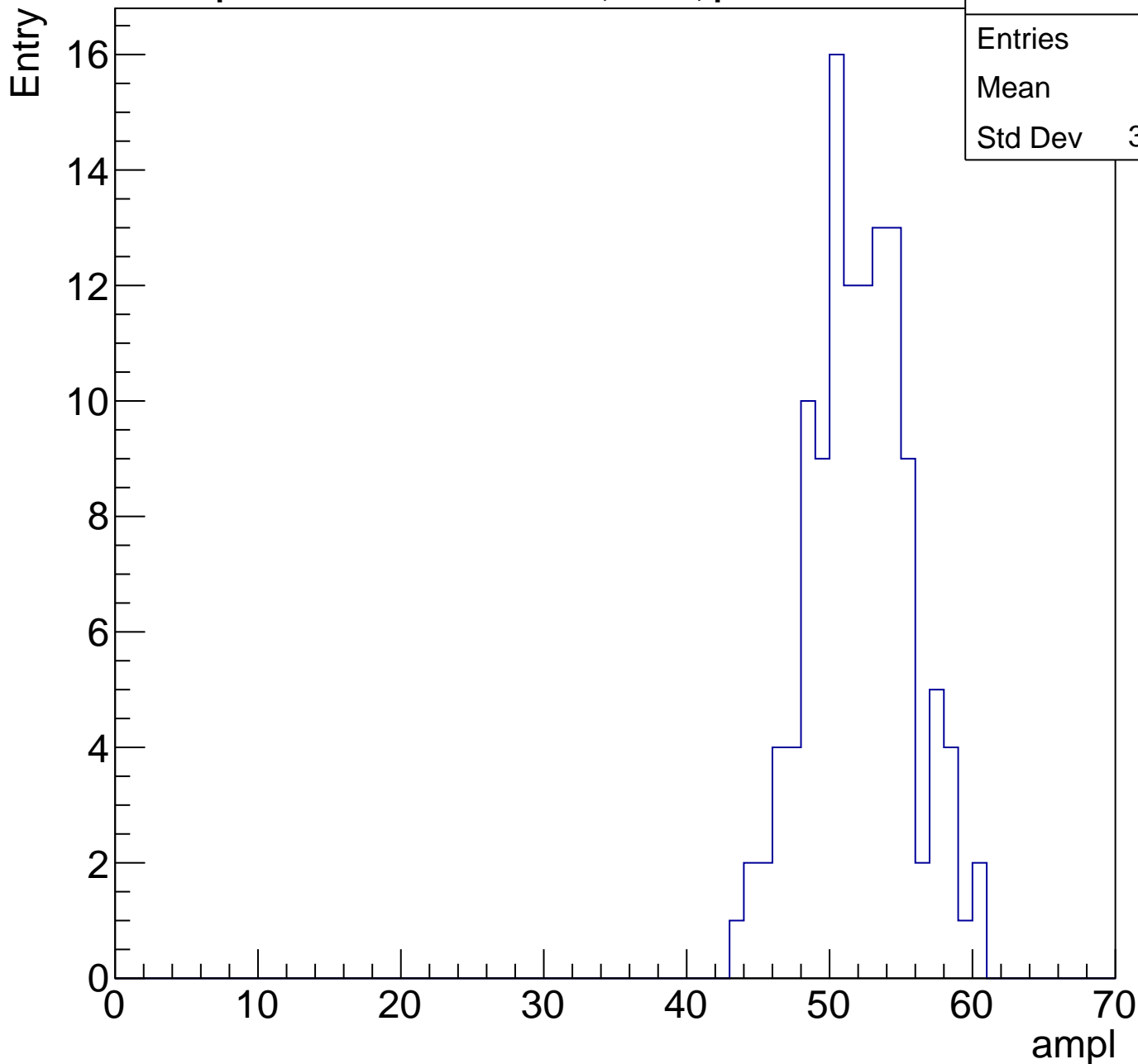
Gaus Width: 3.7206



B0L101S, U25-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

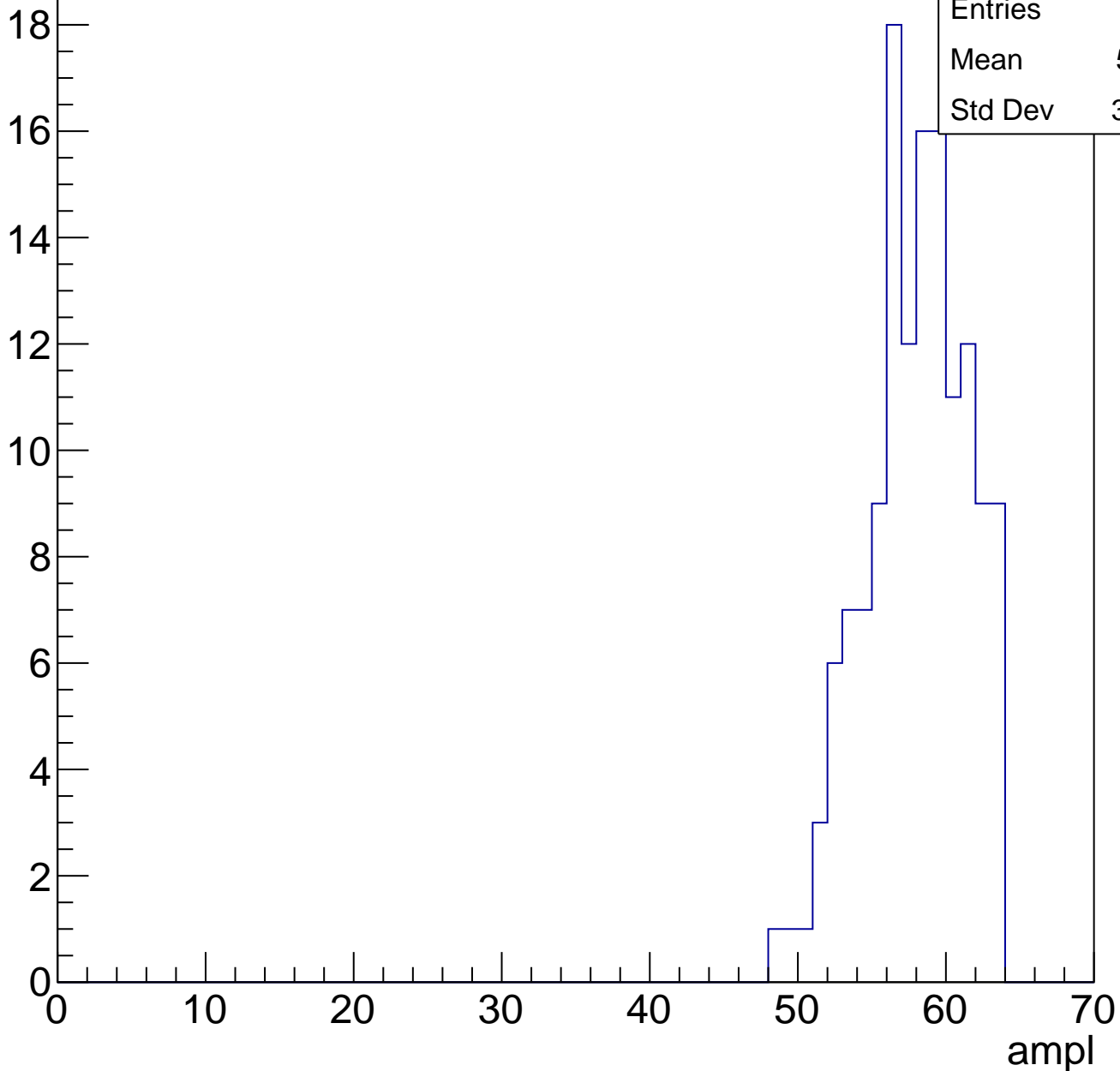
Entries	121
Mean	51.6
Std Dev	3.489



B0L101S, U25-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



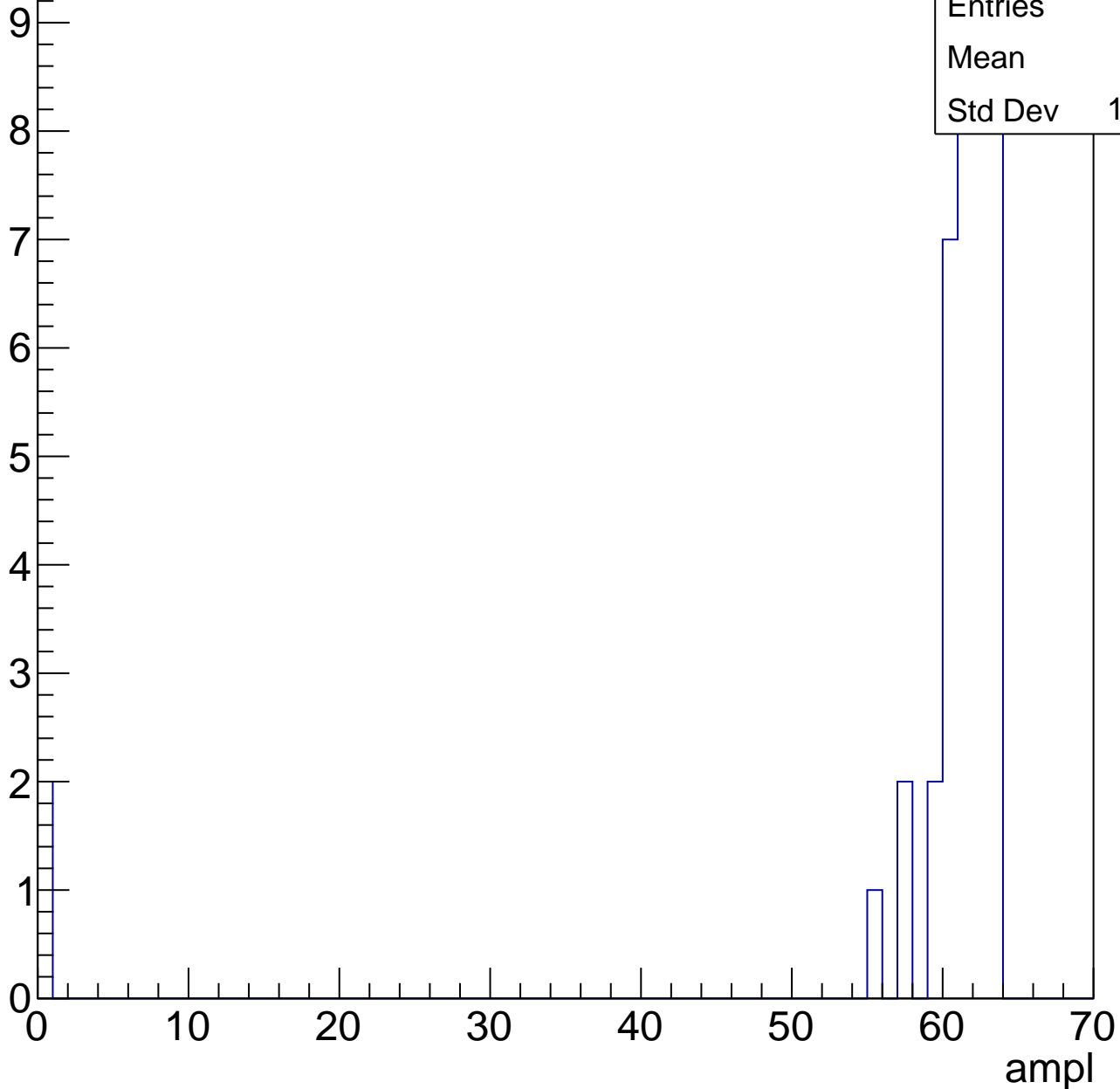
Entries	138
Mean	57.51
Std Dev	3.367

B0L101S, U25-ch97, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	40
Mean	58
Std Dev	13.43



B0L101S, U25-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch98, adc0

calib_packv5_042523_0143.root, FC#1, port C1

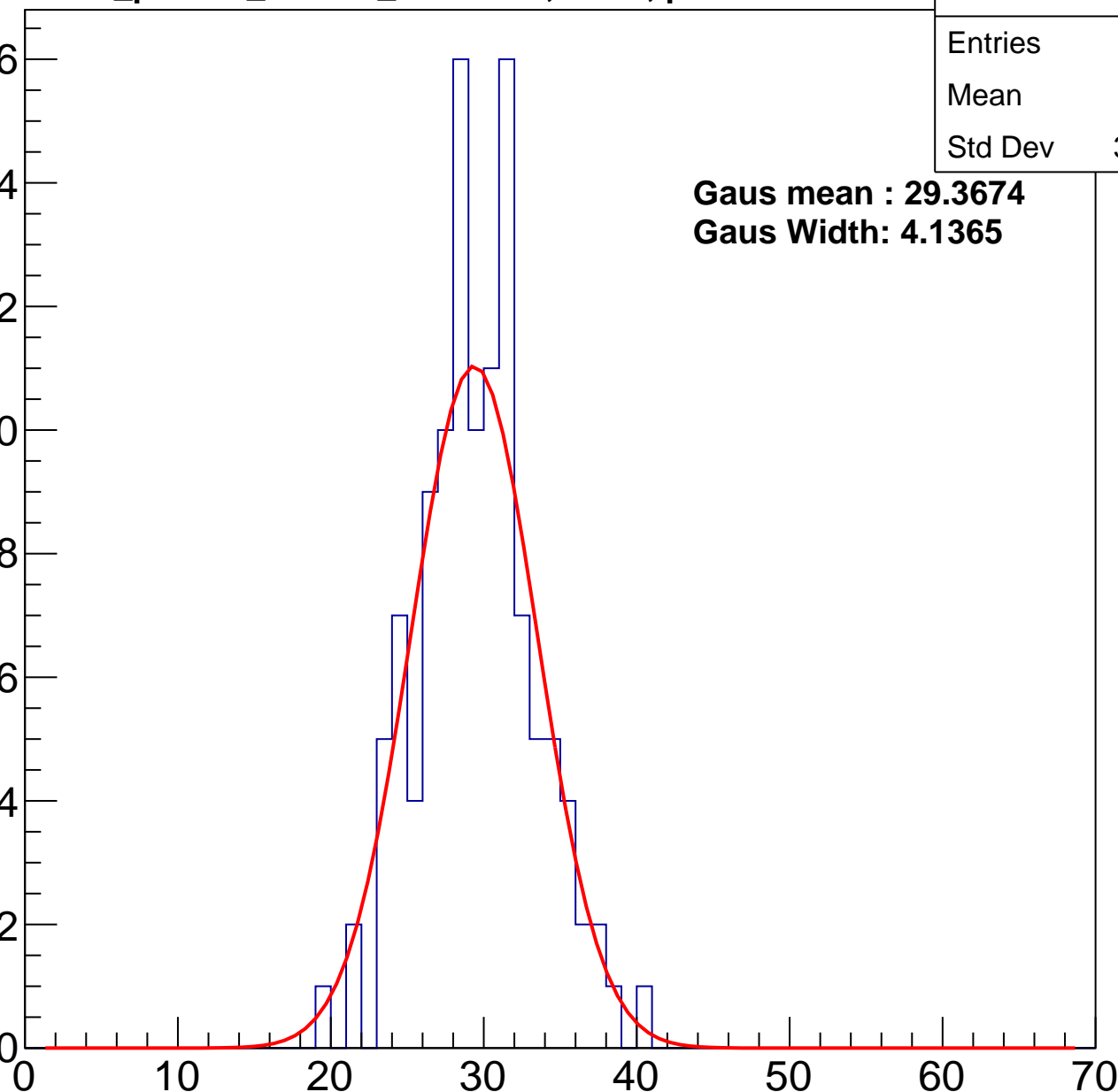
Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	29.11
Std Dev	3.793

Gaus mean : 29.3674

Gaus Width: 4.1365



ampl

B0L101S, U25-ch98, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	35.97
Std Dev	4.013

Gaus mean : 36.4803

Gaus Width: 3.8279

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

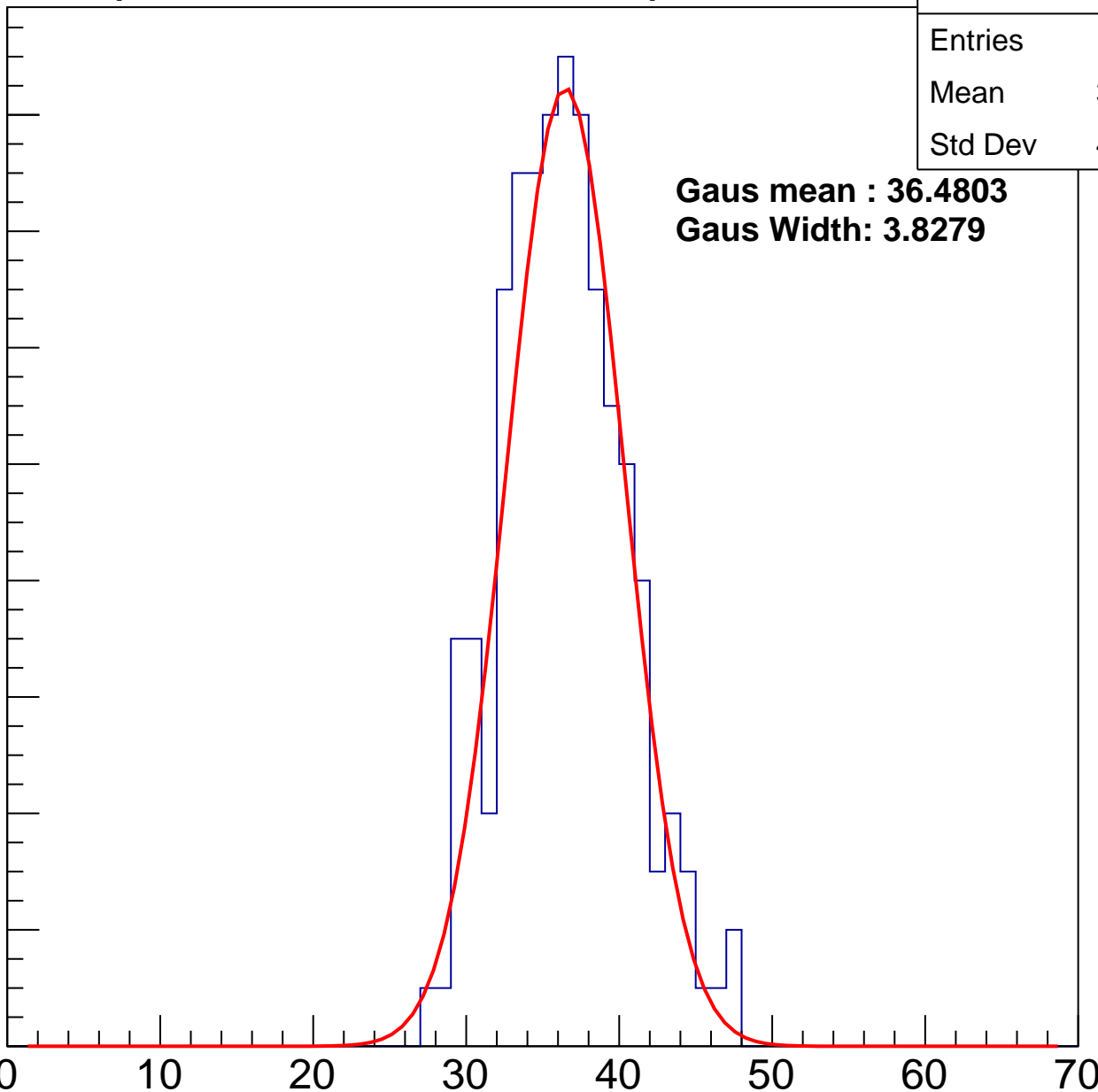
40

50

60

70

ampl



B0L101S, U25-ch98, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

134

Mean

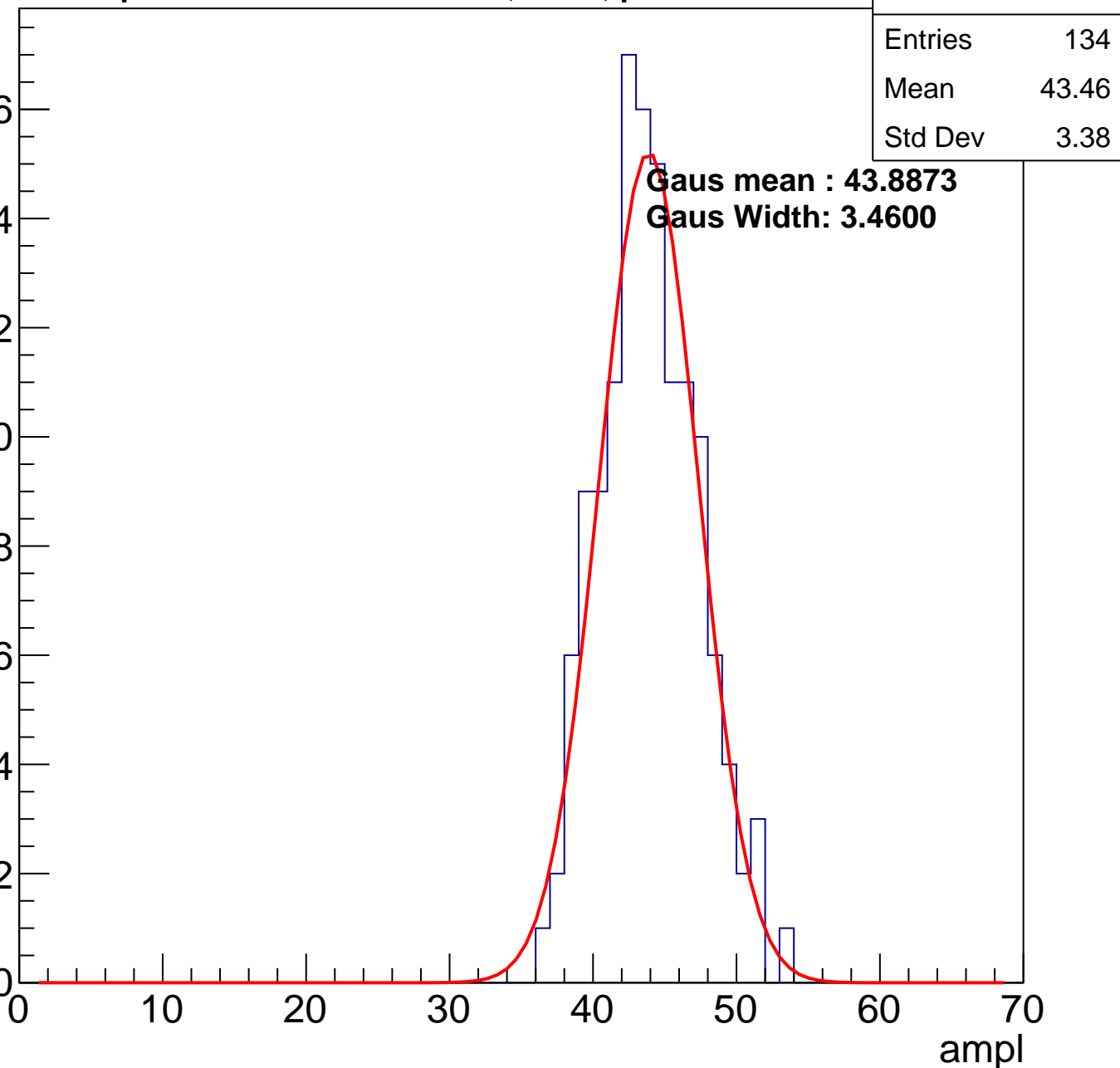
43.46

Std Dev

3.38

Gaus mean : 43.8873

Gaus Width: 3.4600

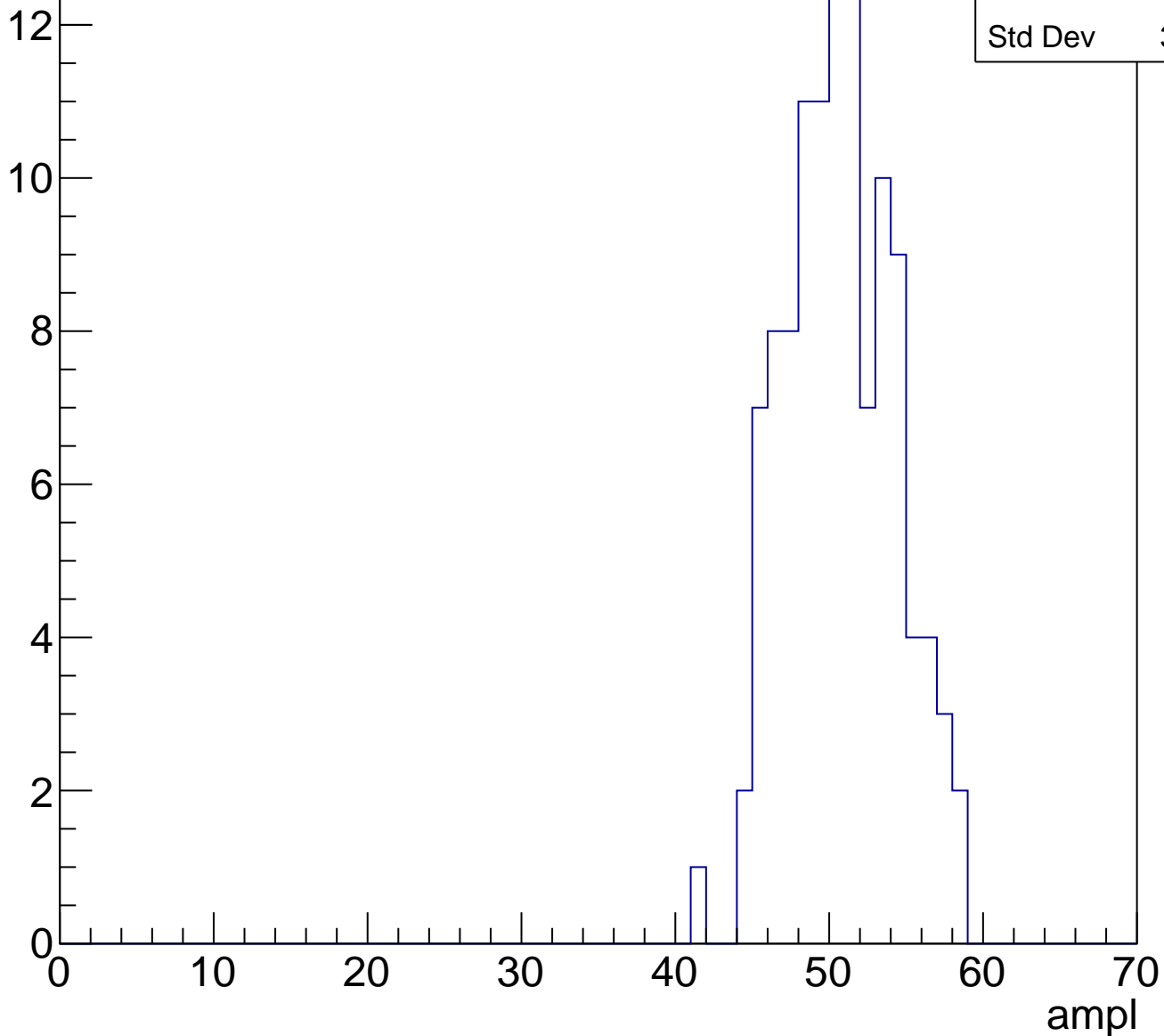


B0L101S, U25-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	50.26
Std Dev	3.481

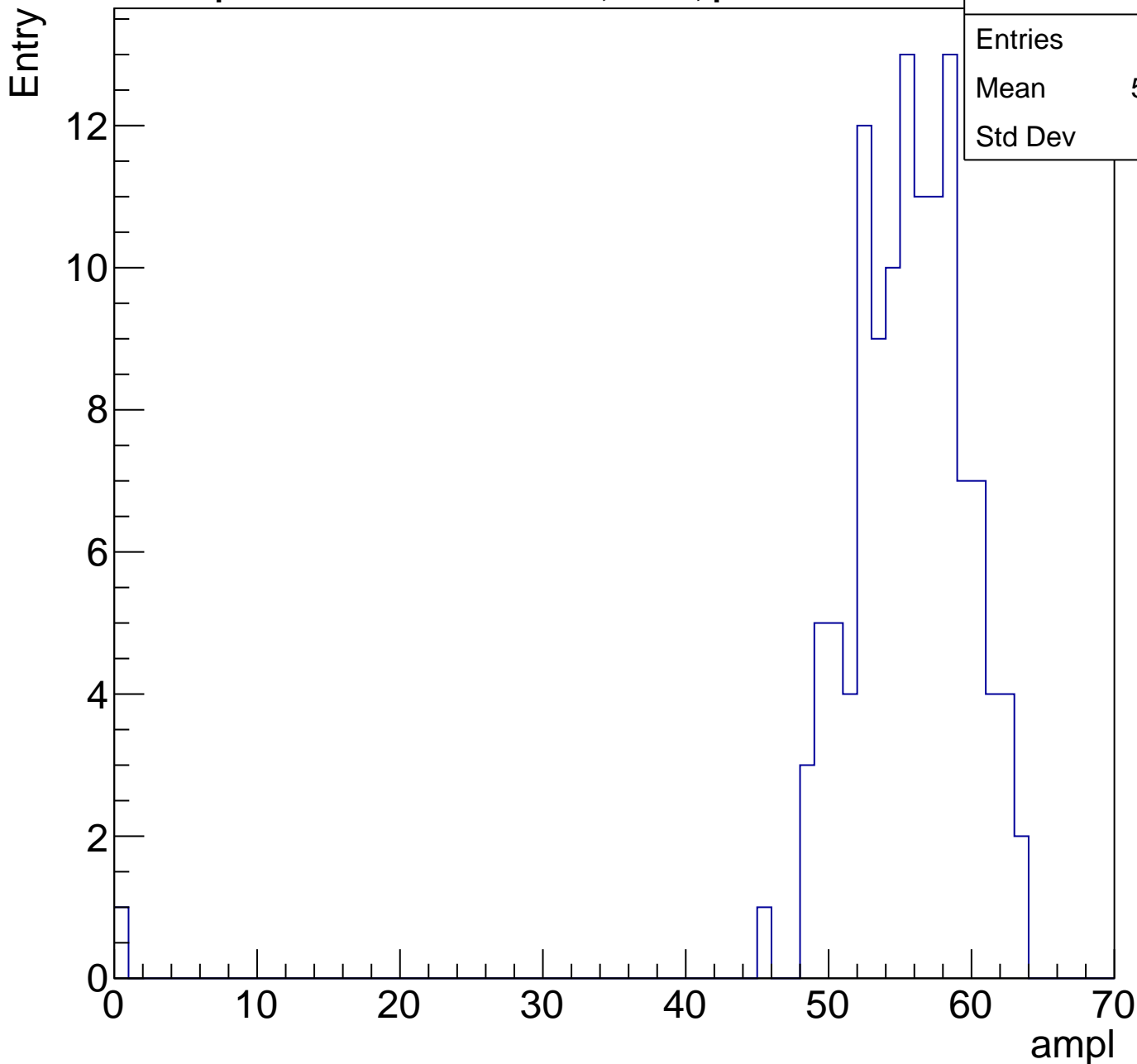
Entry



B0L101S, U25-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	54.85
Std Dev	6.22



B0L101S, U25-ch98, adc5

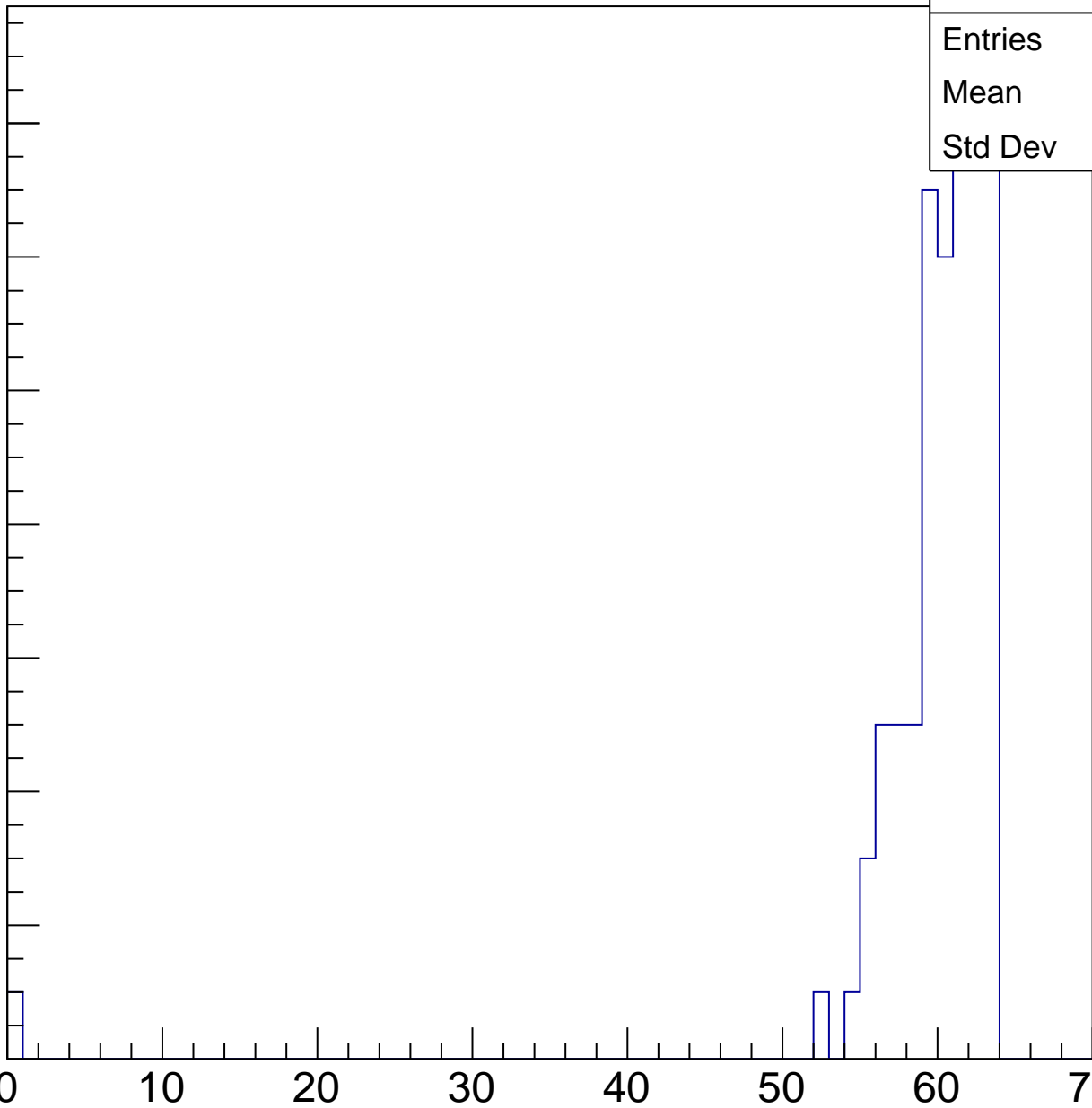
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	59.36
Std Dev	6.753

ampl



B0L101S, U25-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

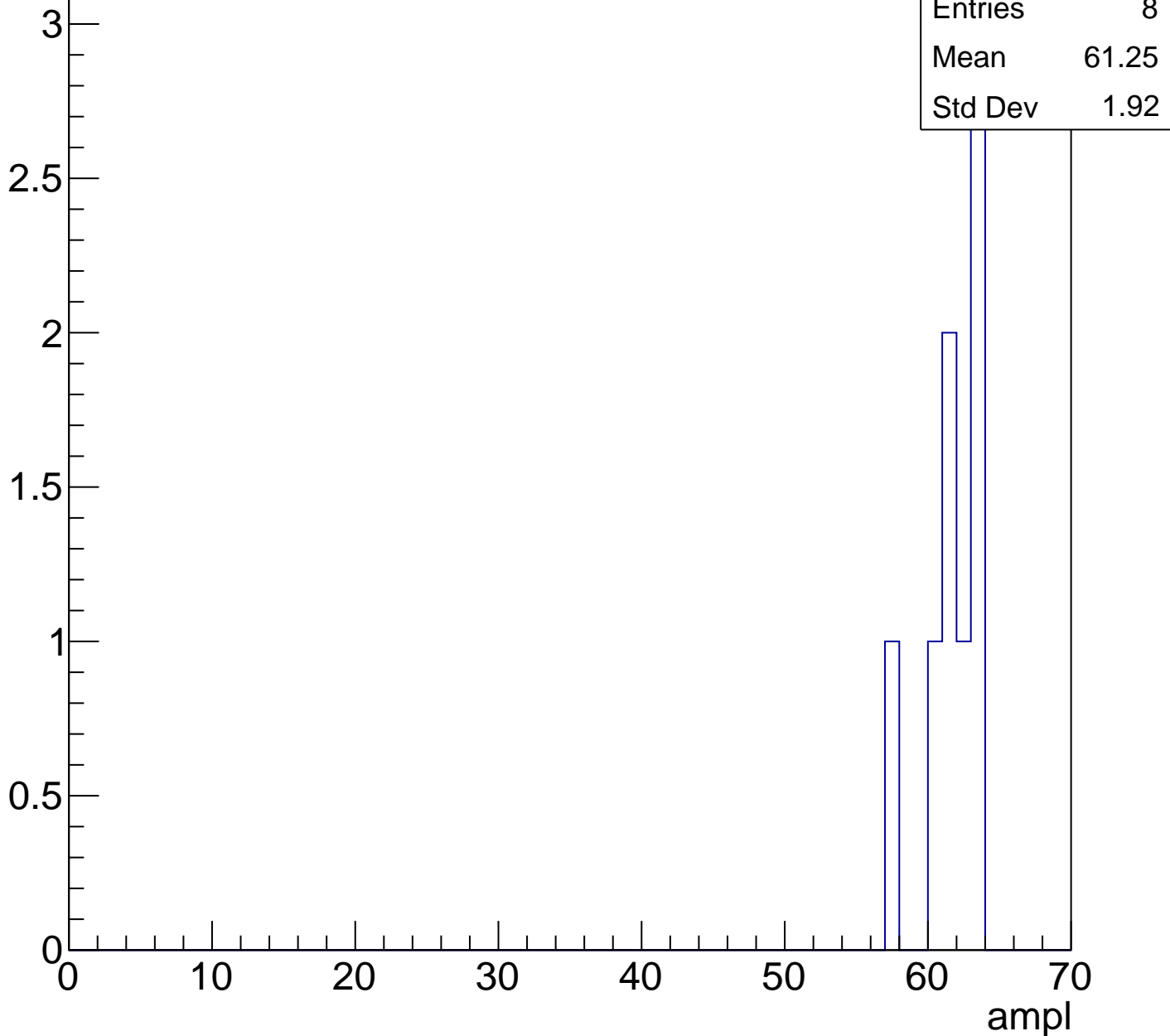
8

Mean

61.25

Std Dev

1.92



B0L101S, U25-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch99, adc0

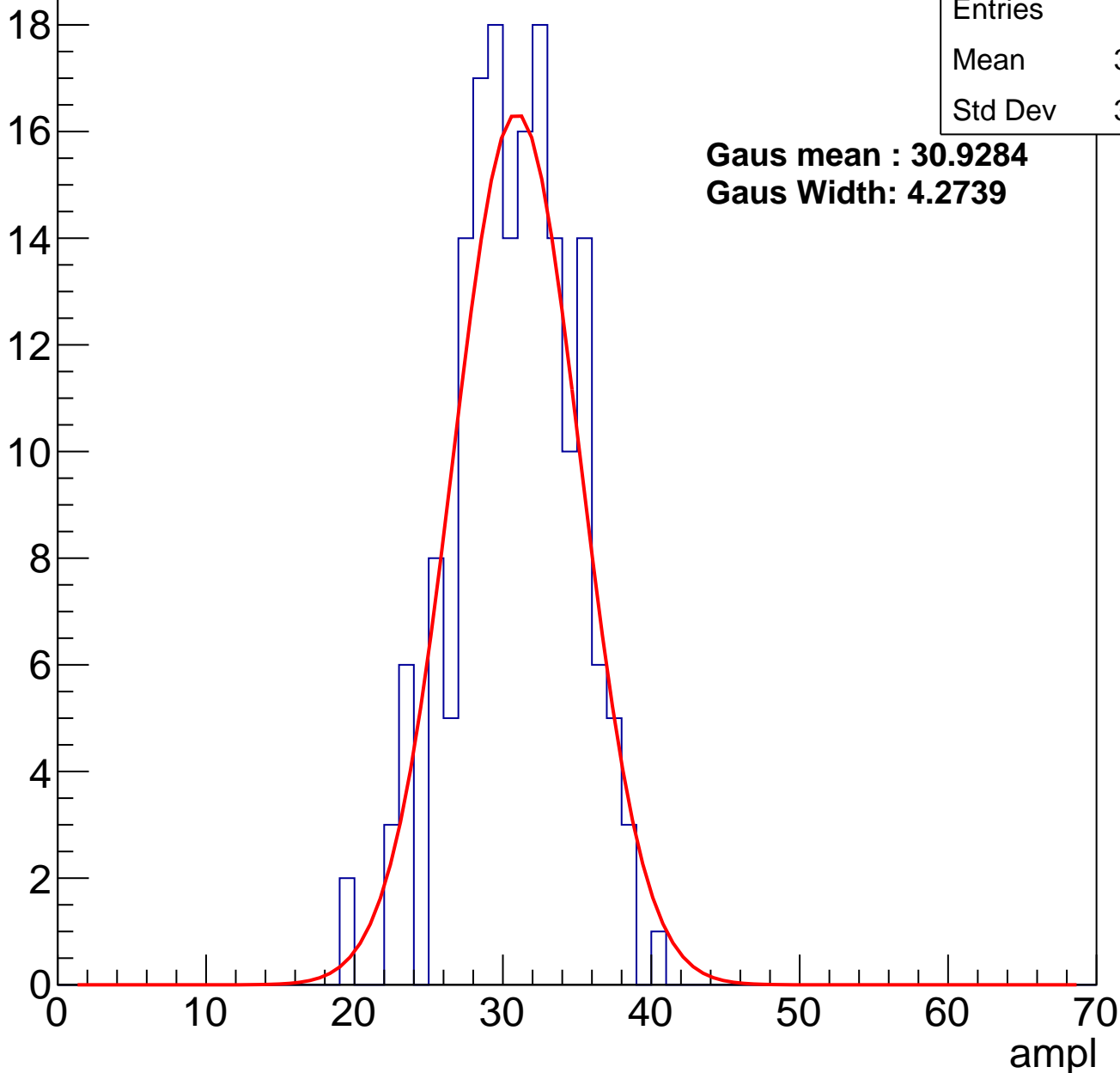
calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	30.39
Std Dev	3.918

Gaus mean : 30.9284

Gaus Width: 4.2739

Entry

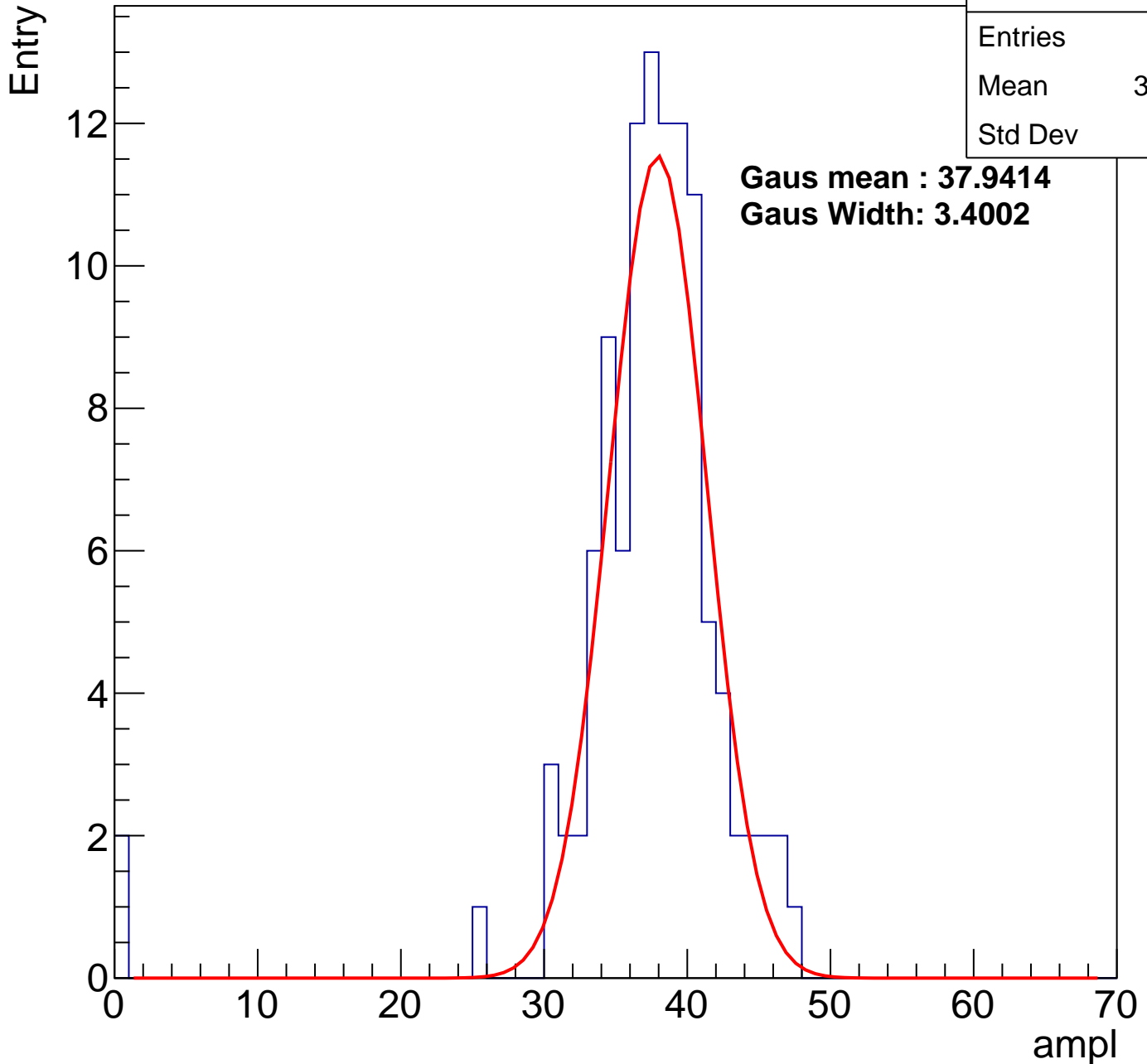


B0L101S, U25-ch99, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	36.77
Std Dev	6.25

Gaus mean : 37.9414
Gaus Width: 3.4002



B0L101S, U25-ch99, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	43.23
Std Dev	3.67

Gaus mean : 43.6574

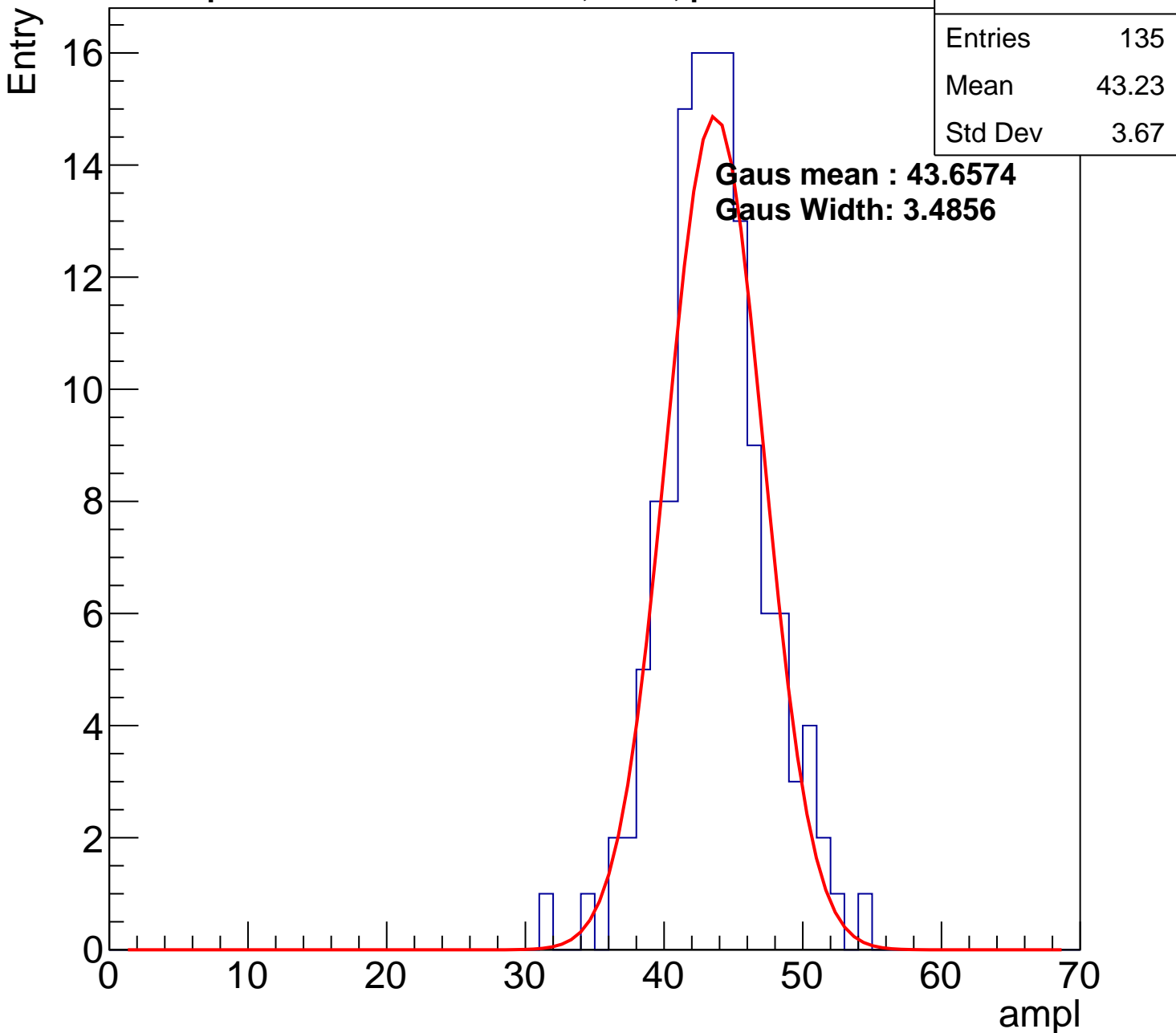
Gaus Width: 3.4856

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	51.14
Std Dev	3.742

Entry

16

14

12

10

8

6

4

2

0

0

10

20

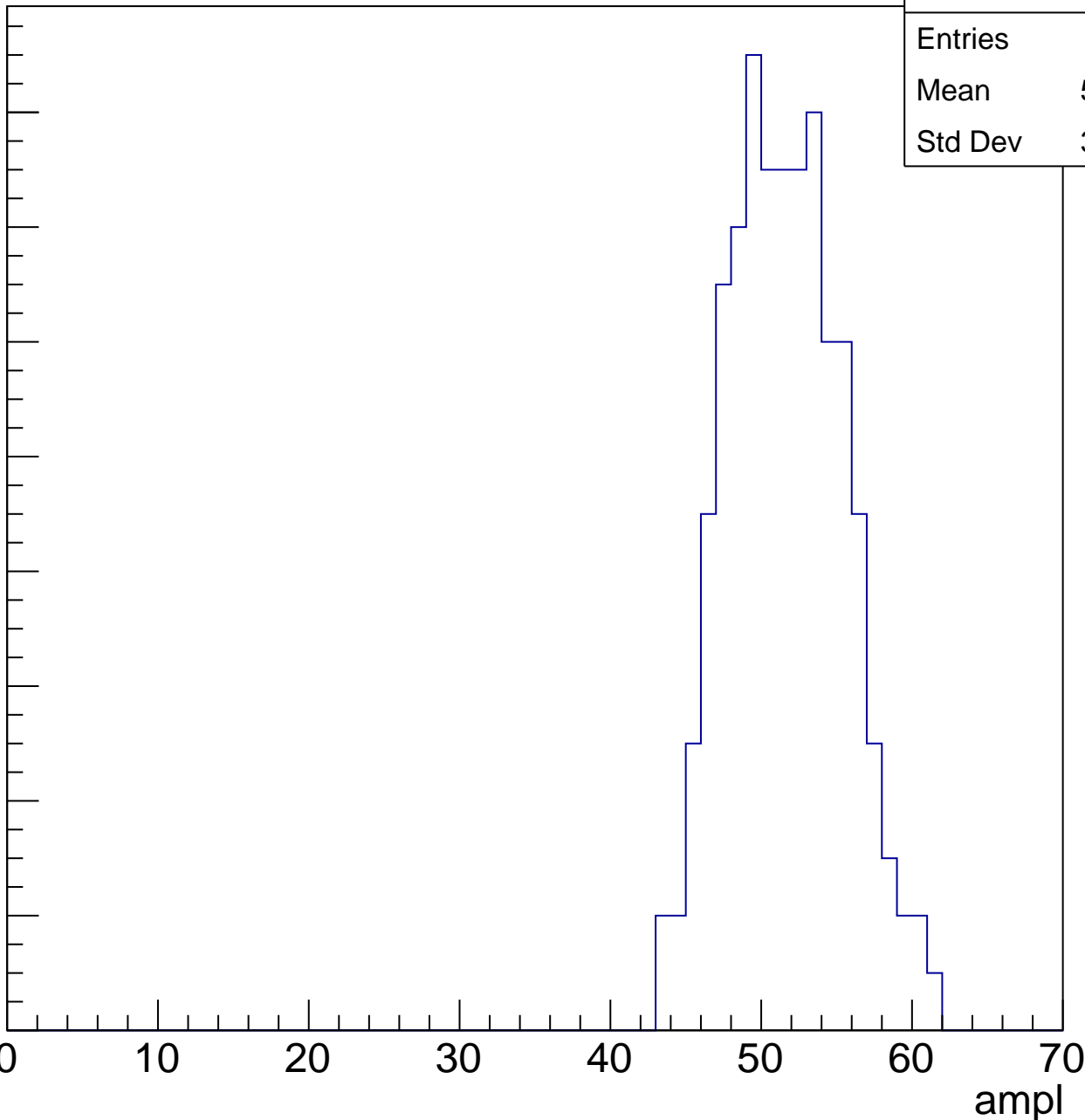
30

40

50

60

ampl



B0L101S, U25-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

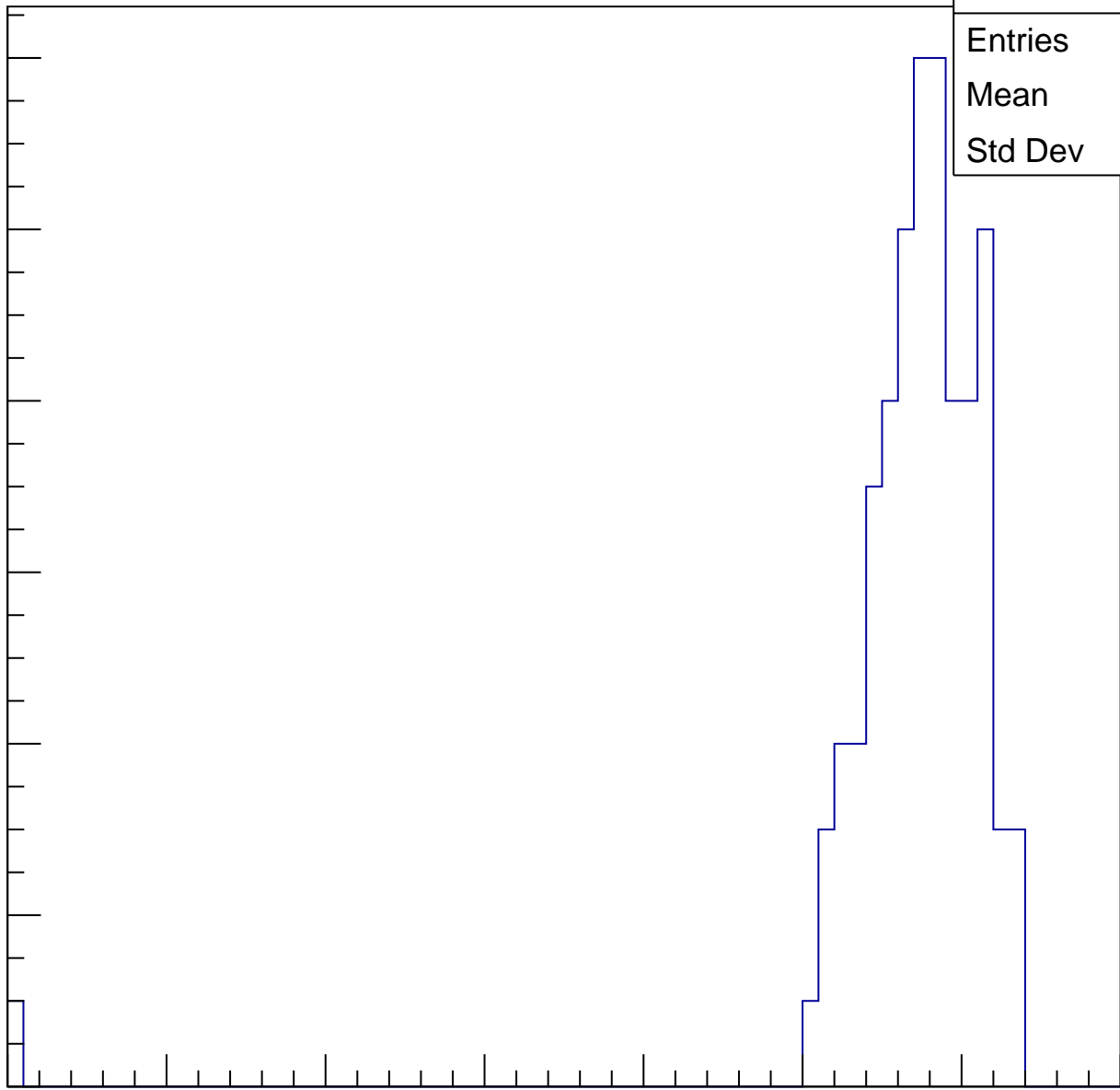
Entry

12
10
8
6
4
2
0

Entries	94
Mean	56.57
Std Dev	6.618

ampl

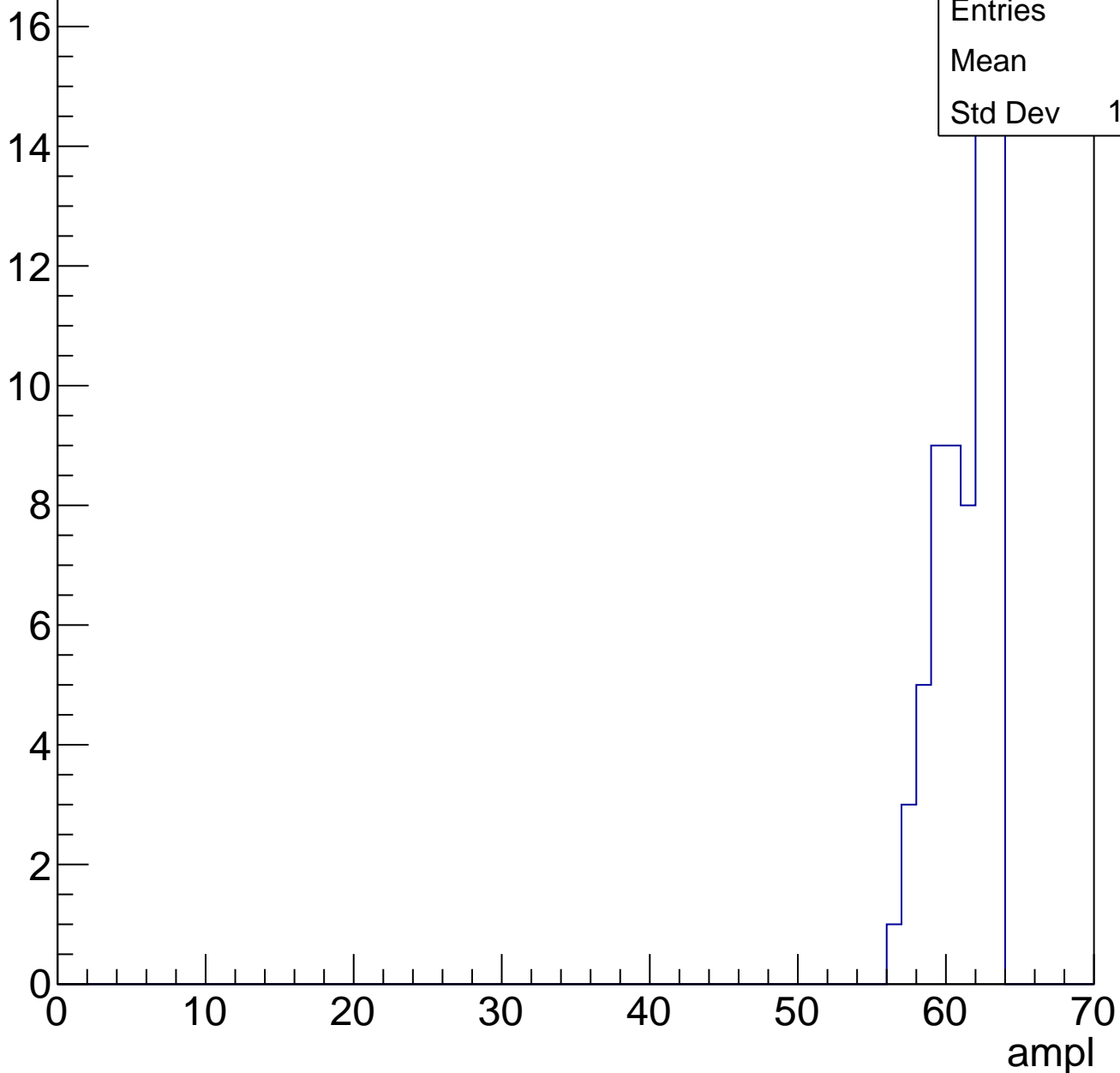
0 10 20 30 40 50 60 70



B0L101S, U25-ch99, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

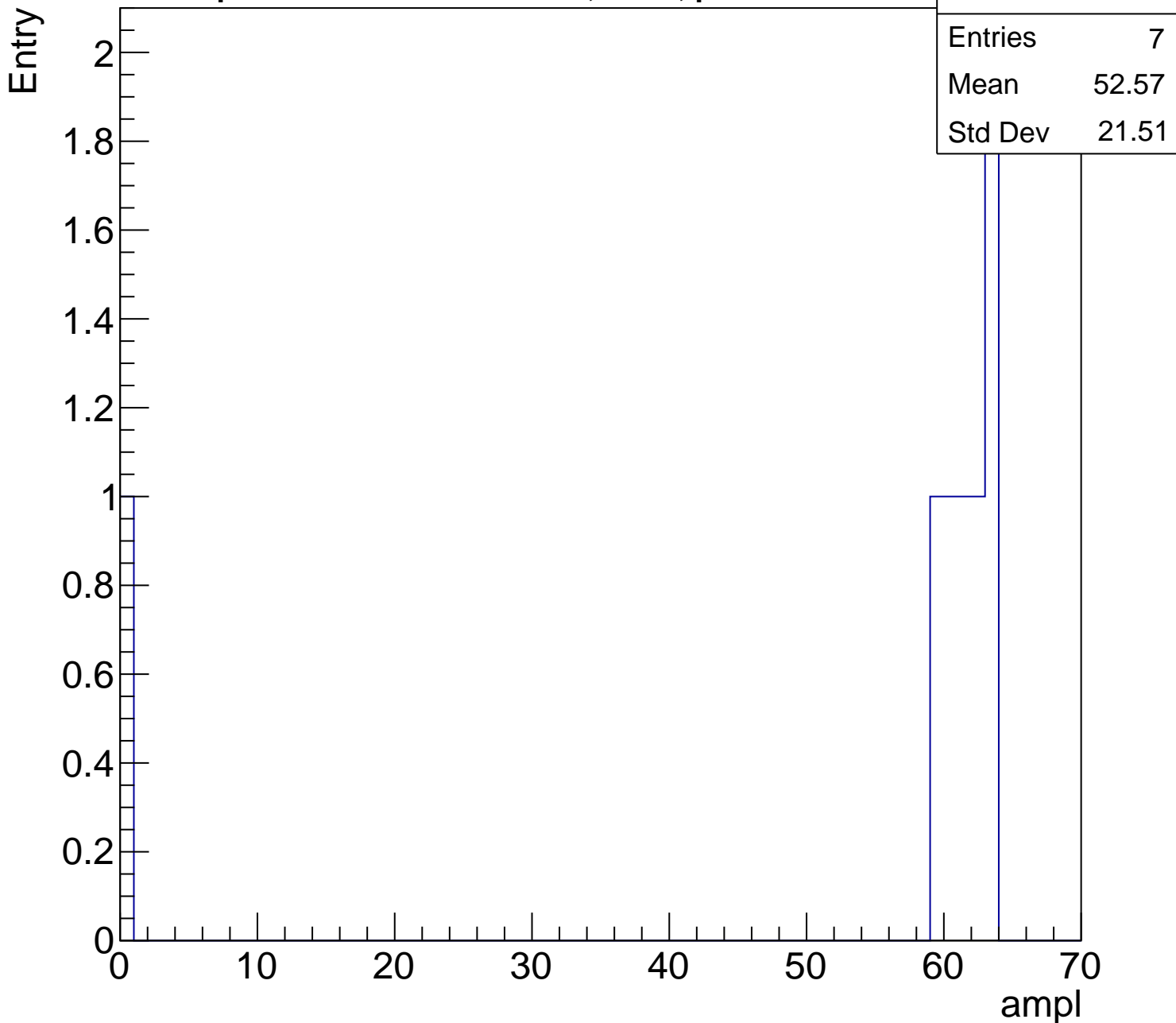
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.57
Std Dev	21.51

ampl

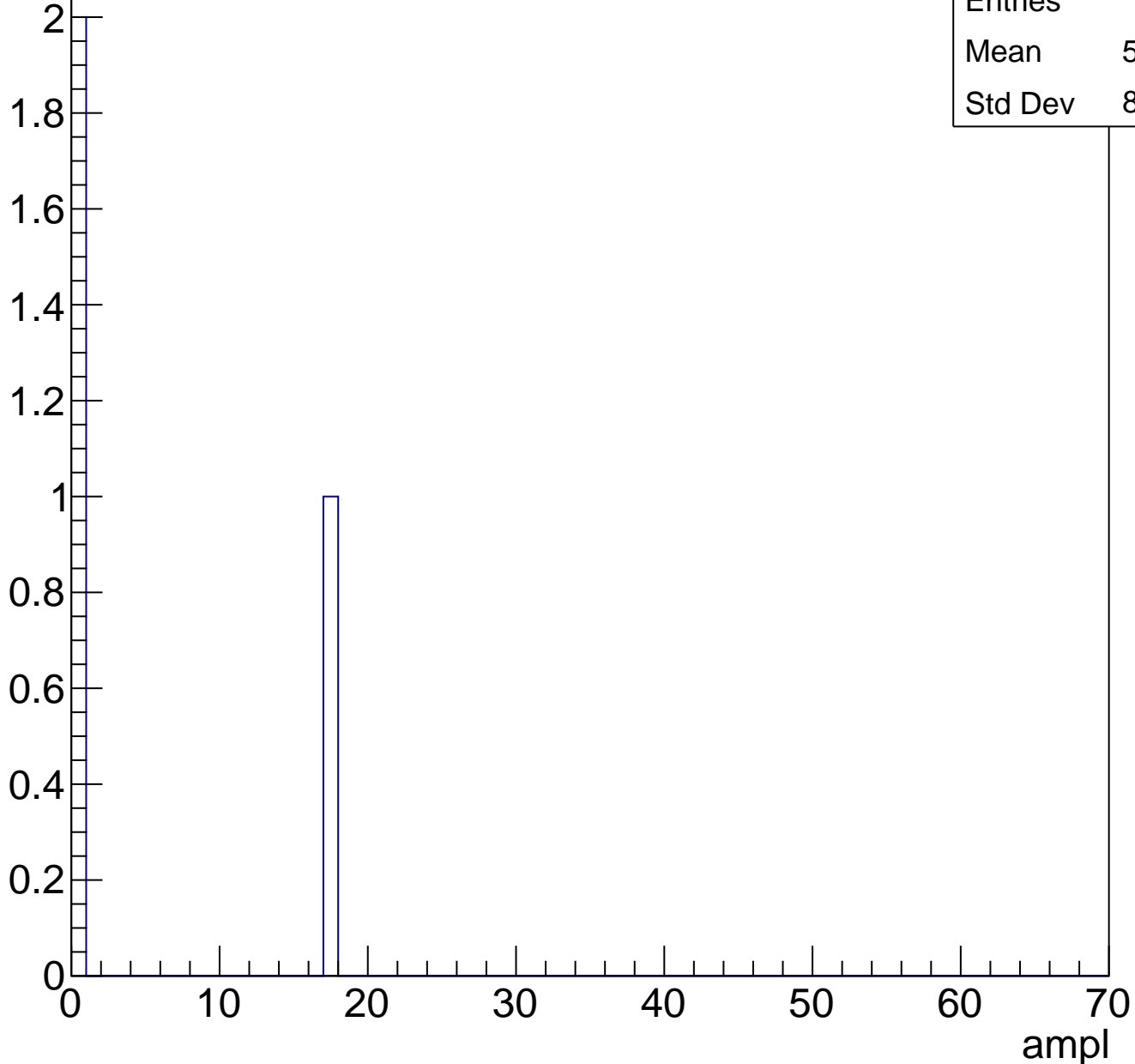
0 10 20 30 40 50 60 70



B0L101S, U25-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	5.667
Std Dev	8.014

B0L101S, U25-ch100, adc0

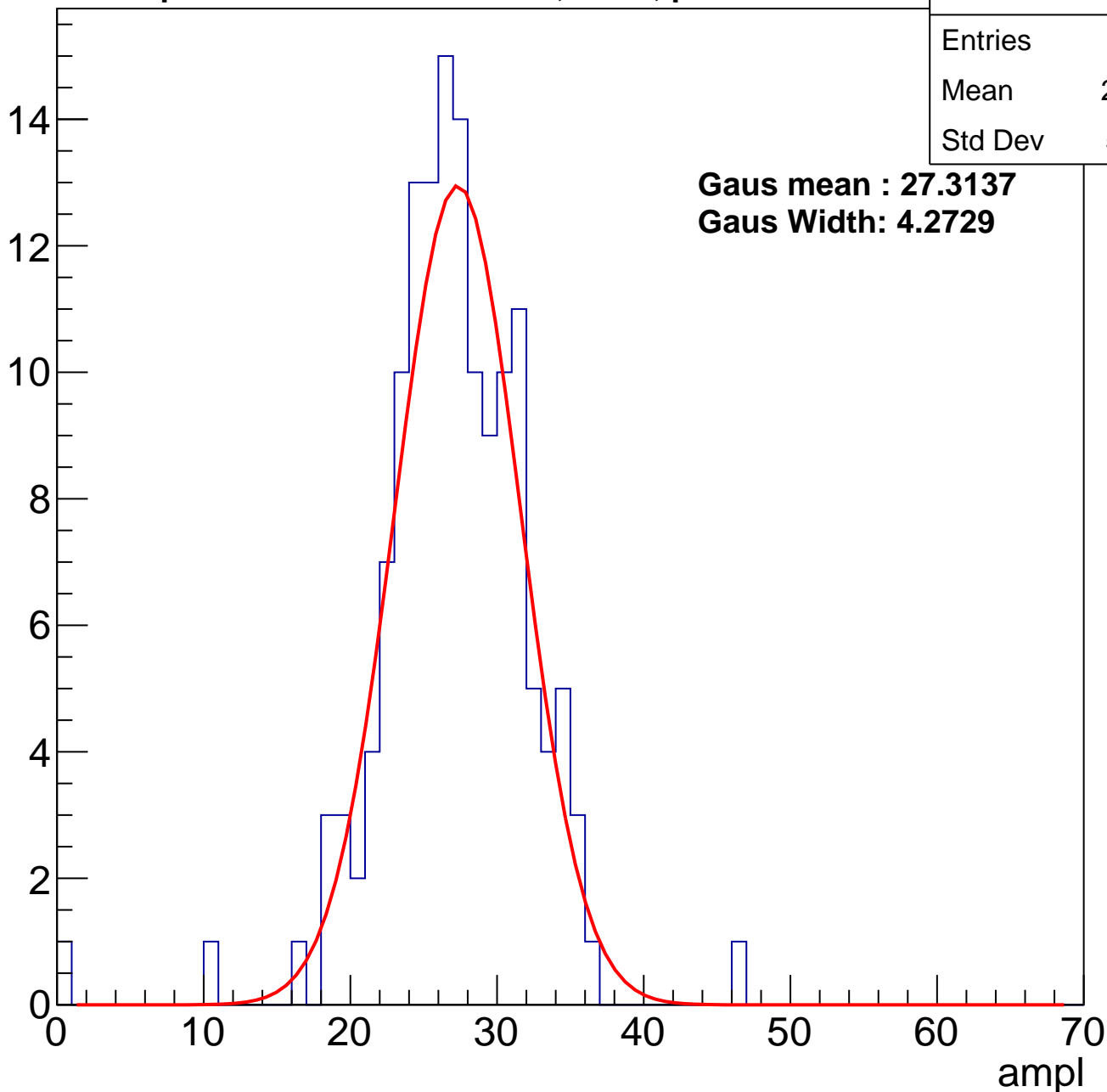
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	26.58
Std Dev	5.081

Gaus mean : 27.3137

Gaus Width: 4.2729

Entry



B0L101S, U25-ch100, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

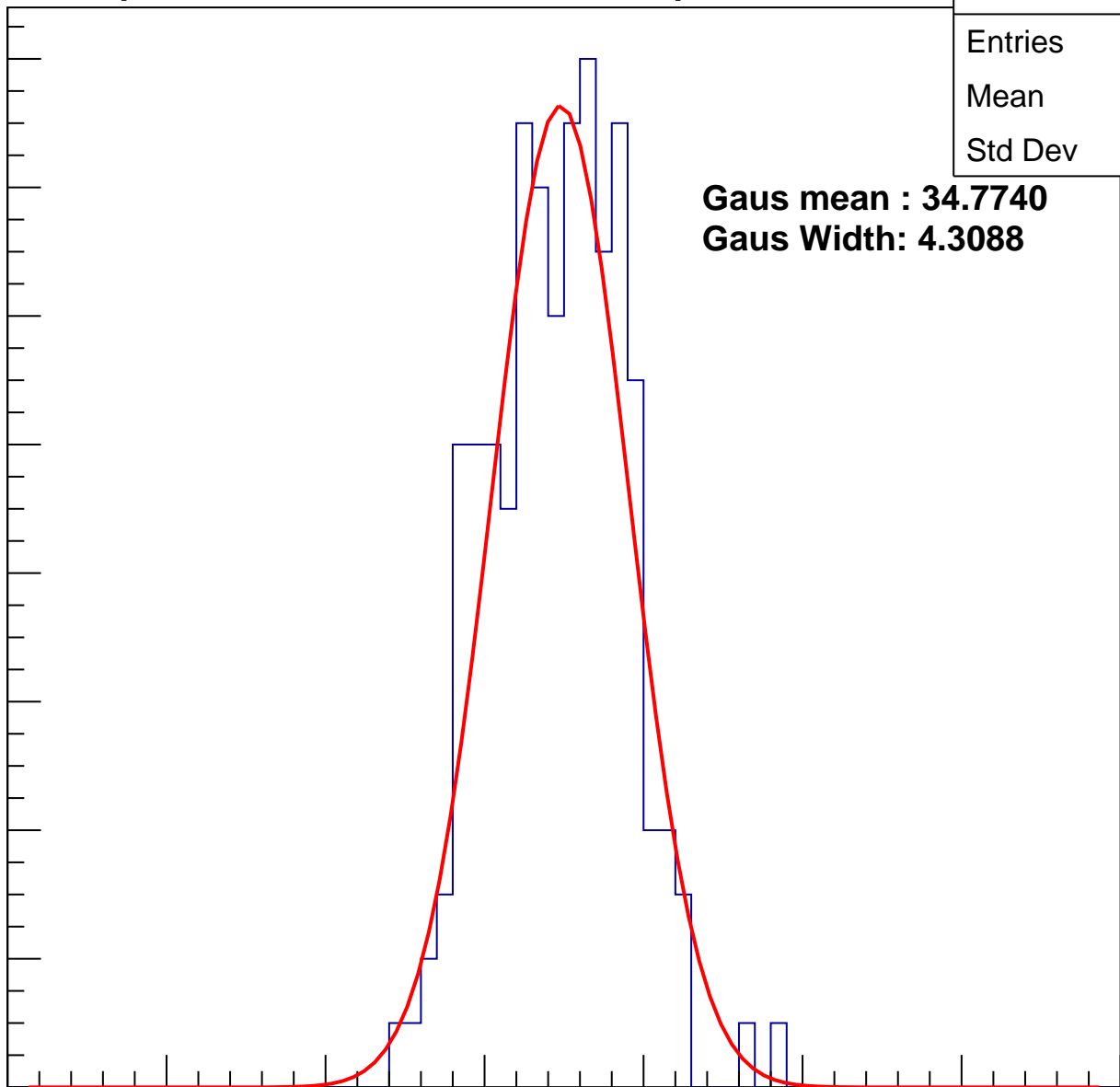
Entries	170
Mean	34.15
Std Dev	4.173

Gaus mean : 34.7740

Gaus Width: 4.3088

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch100, adc2

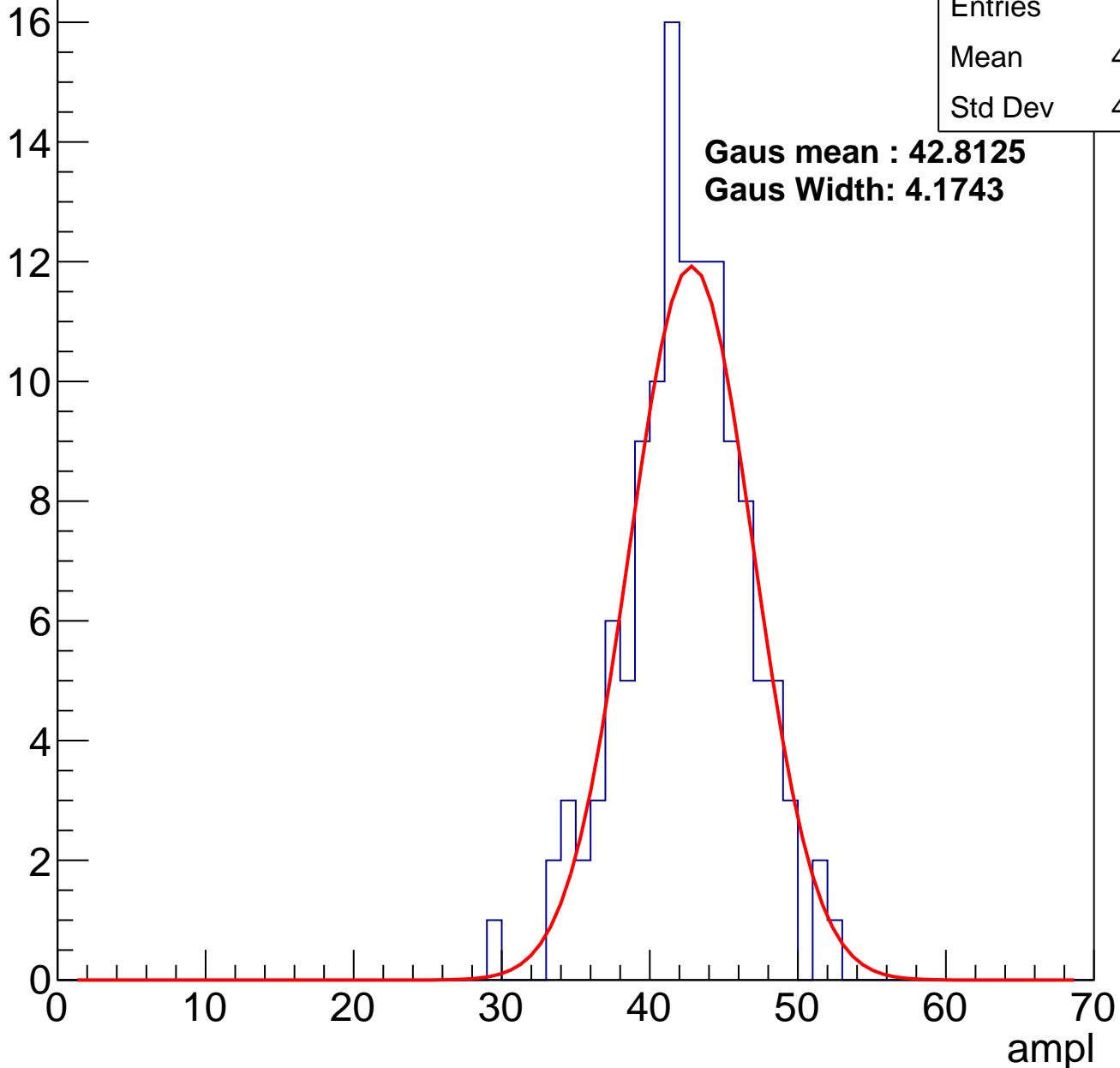
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	41.99
Std Dev	4.062

Gaus mean : 42.8125

Gaus Width: 4.1743

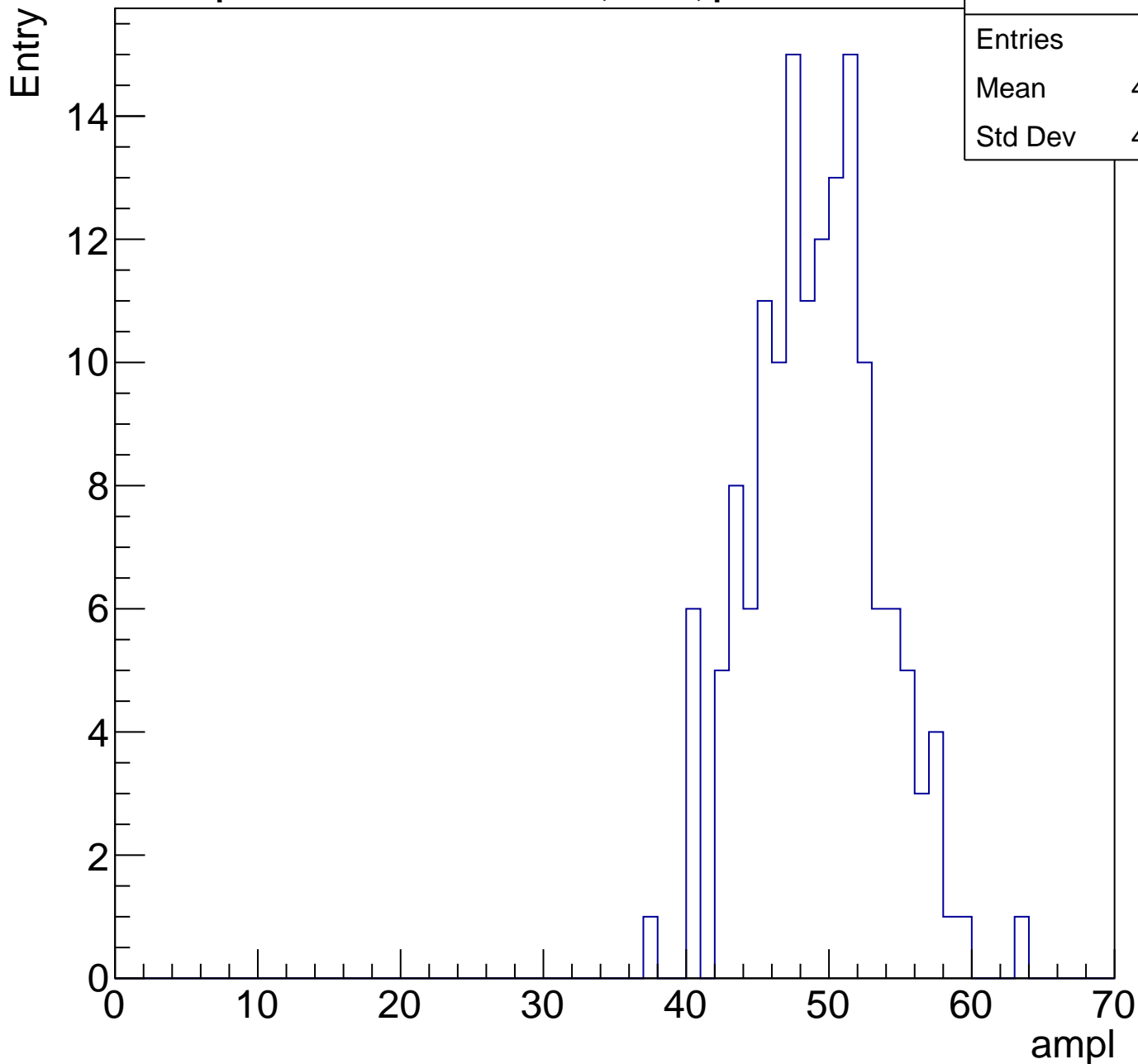
Entry



B0L101S, U25-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	48.66
Std Dev	4.473



B0L101S, U25-ch100, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	87
Mean	54.13
Std Dev	3.939

Entry

10

8

6

4

2

0

0

10

20

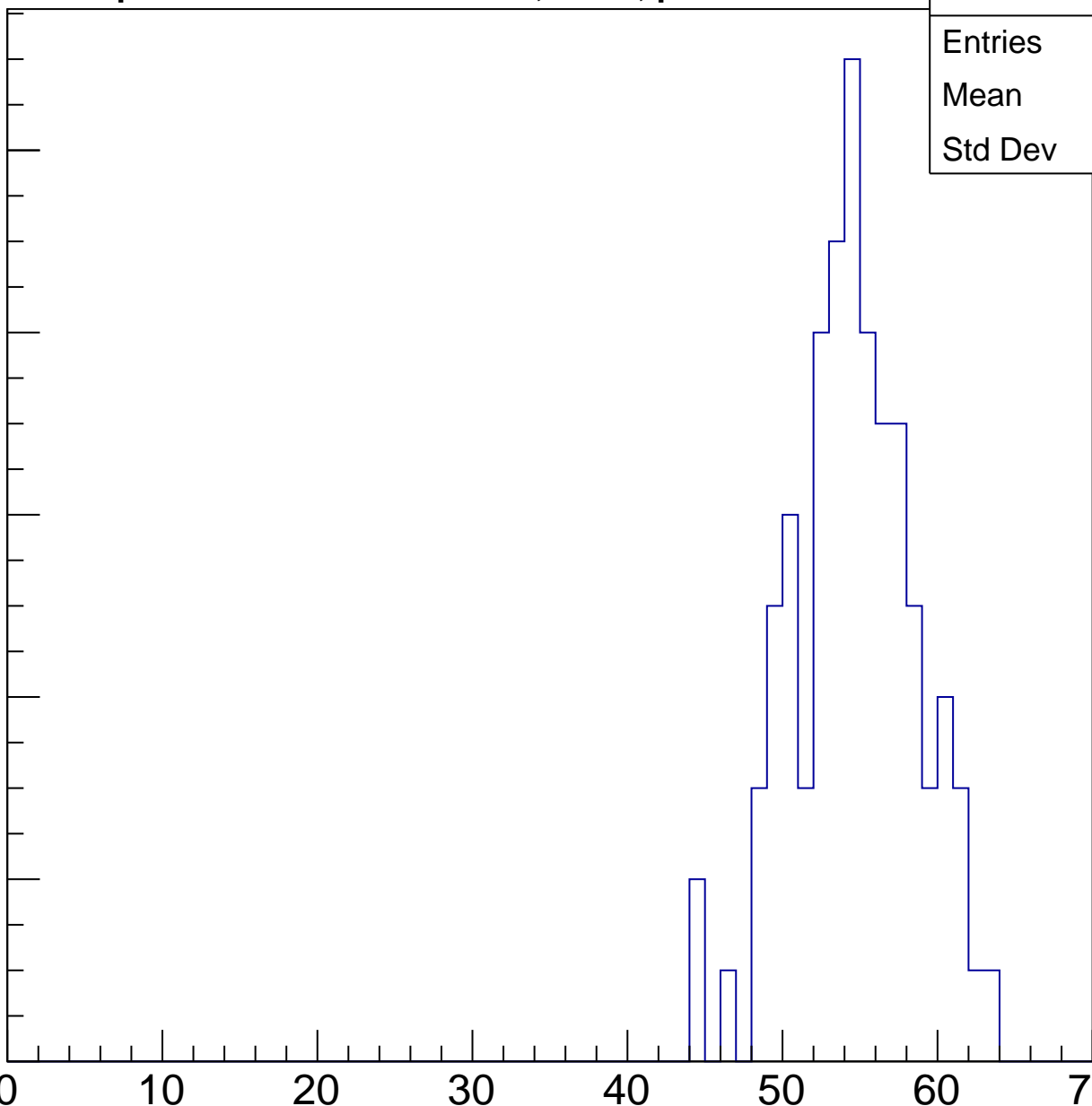
30

40

50

60

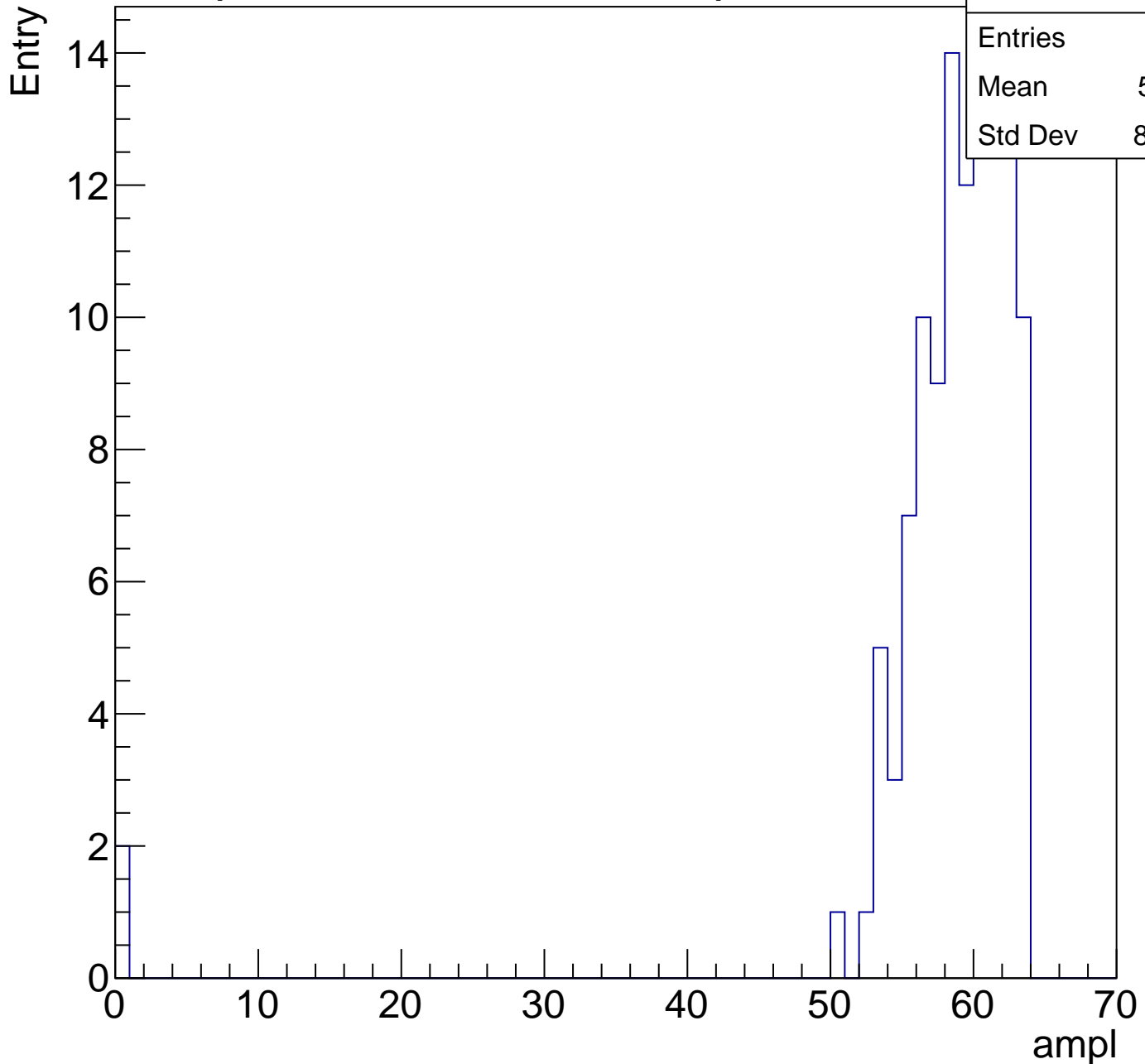
ampl



B0L101S, U25-ch100, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	57.71
Std Dev	8.212

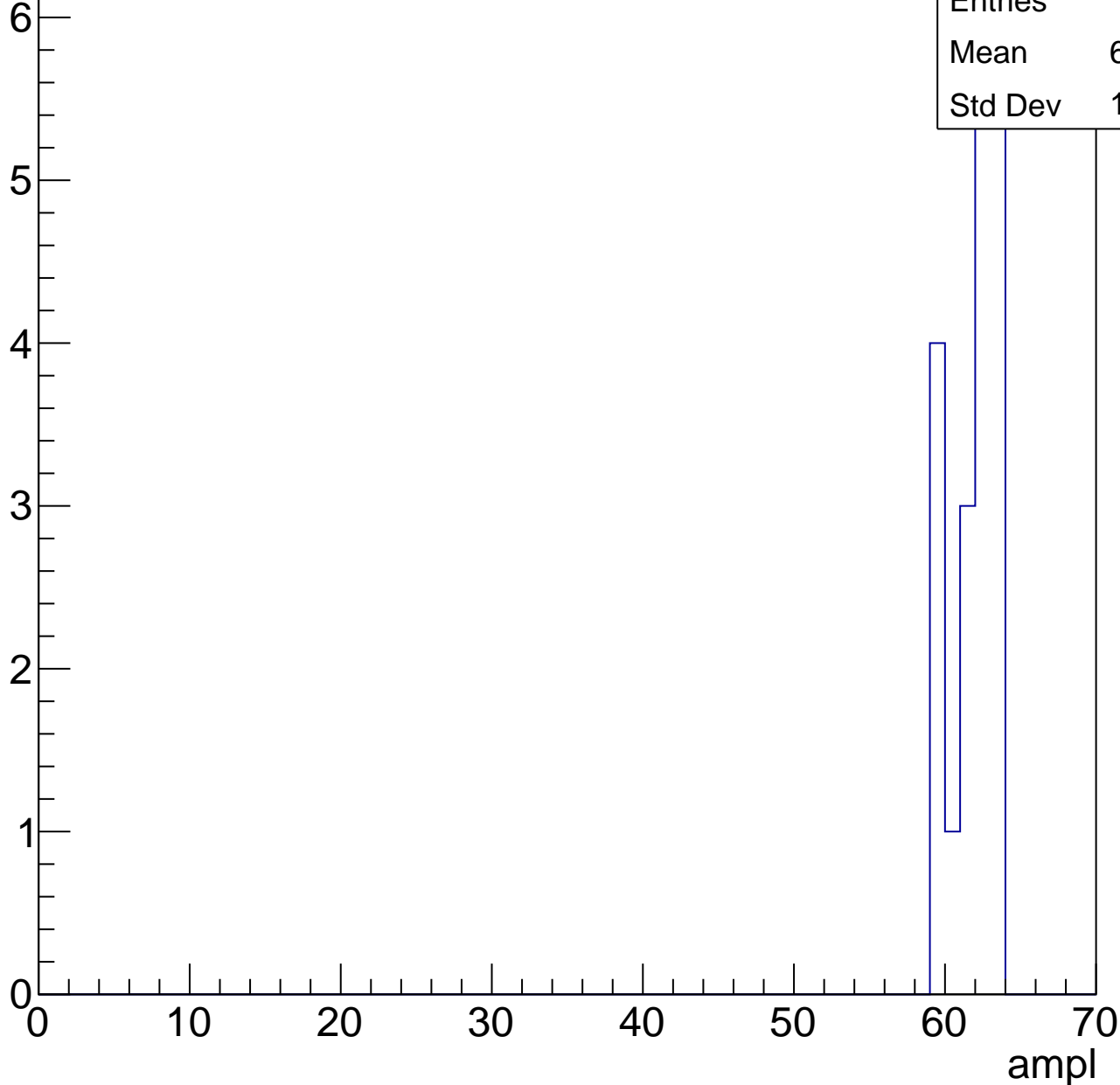


B0L101S, U25-ch100, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

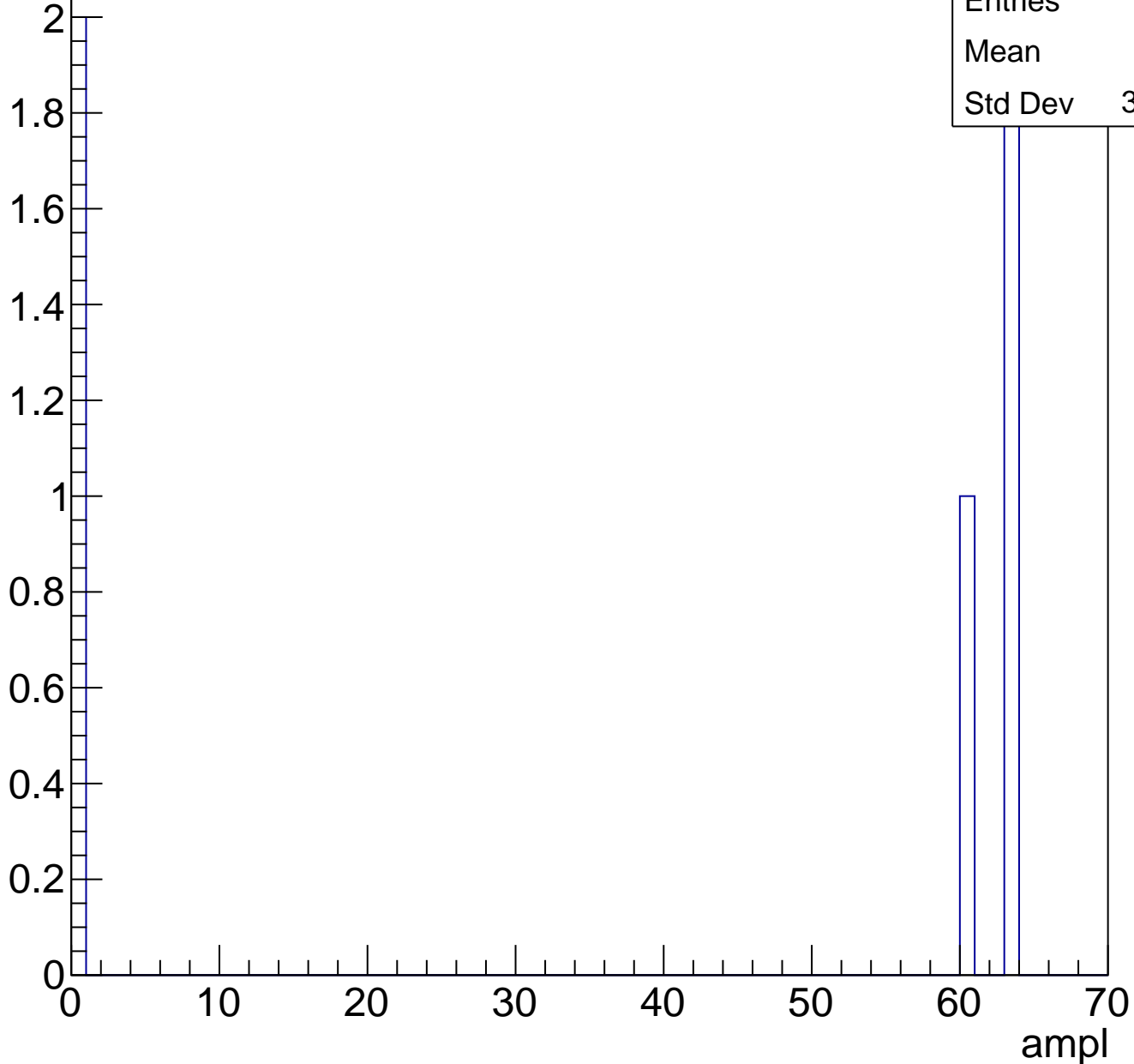
Entries	20
Mean	61.45
Std Dev	1.465



B0L101S, U25-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch101, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	29.26
Std Dev	5.121

Gaus mean : 30.3618

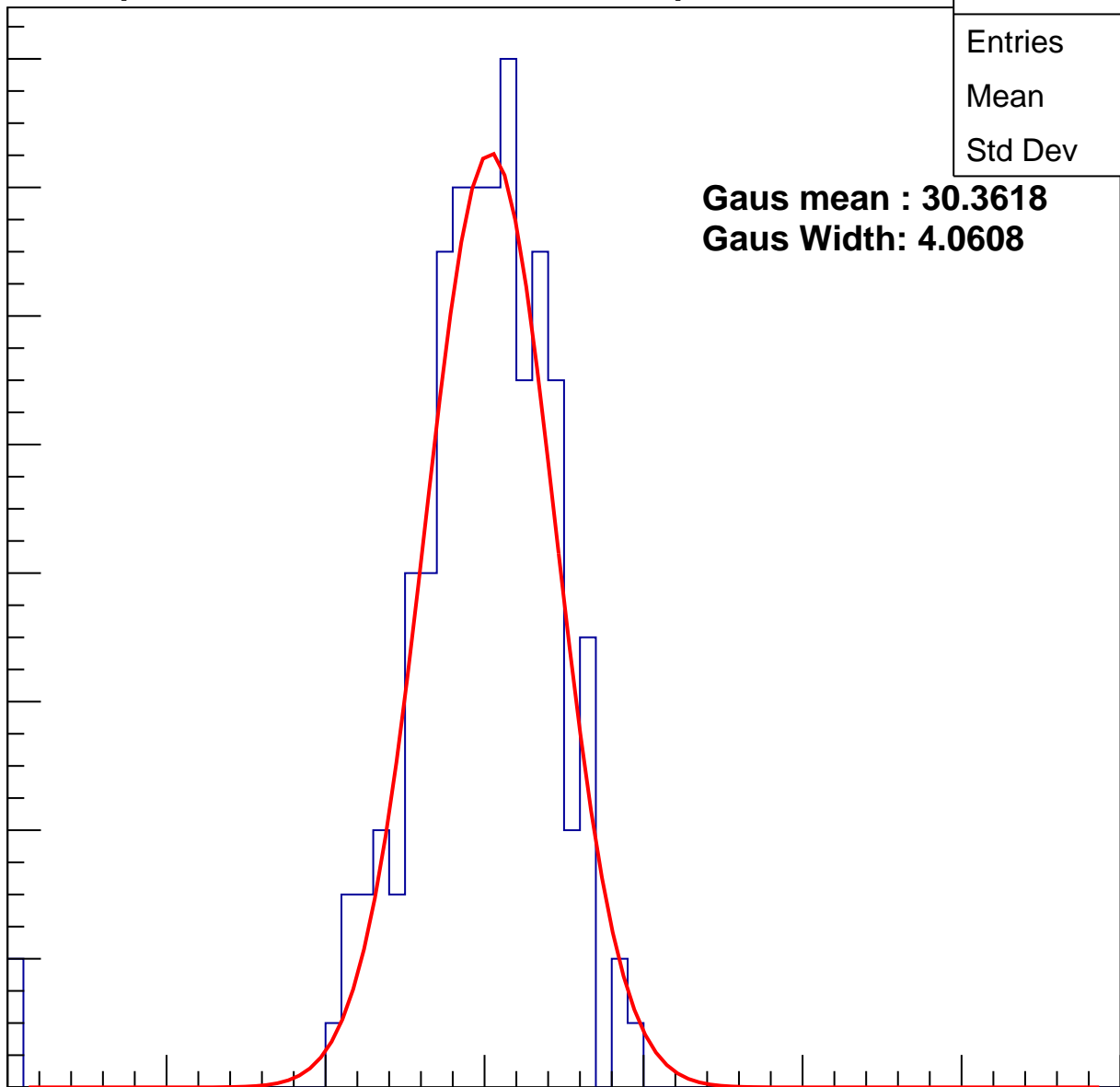
Gaus Width: 4.0608

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch101, adc1

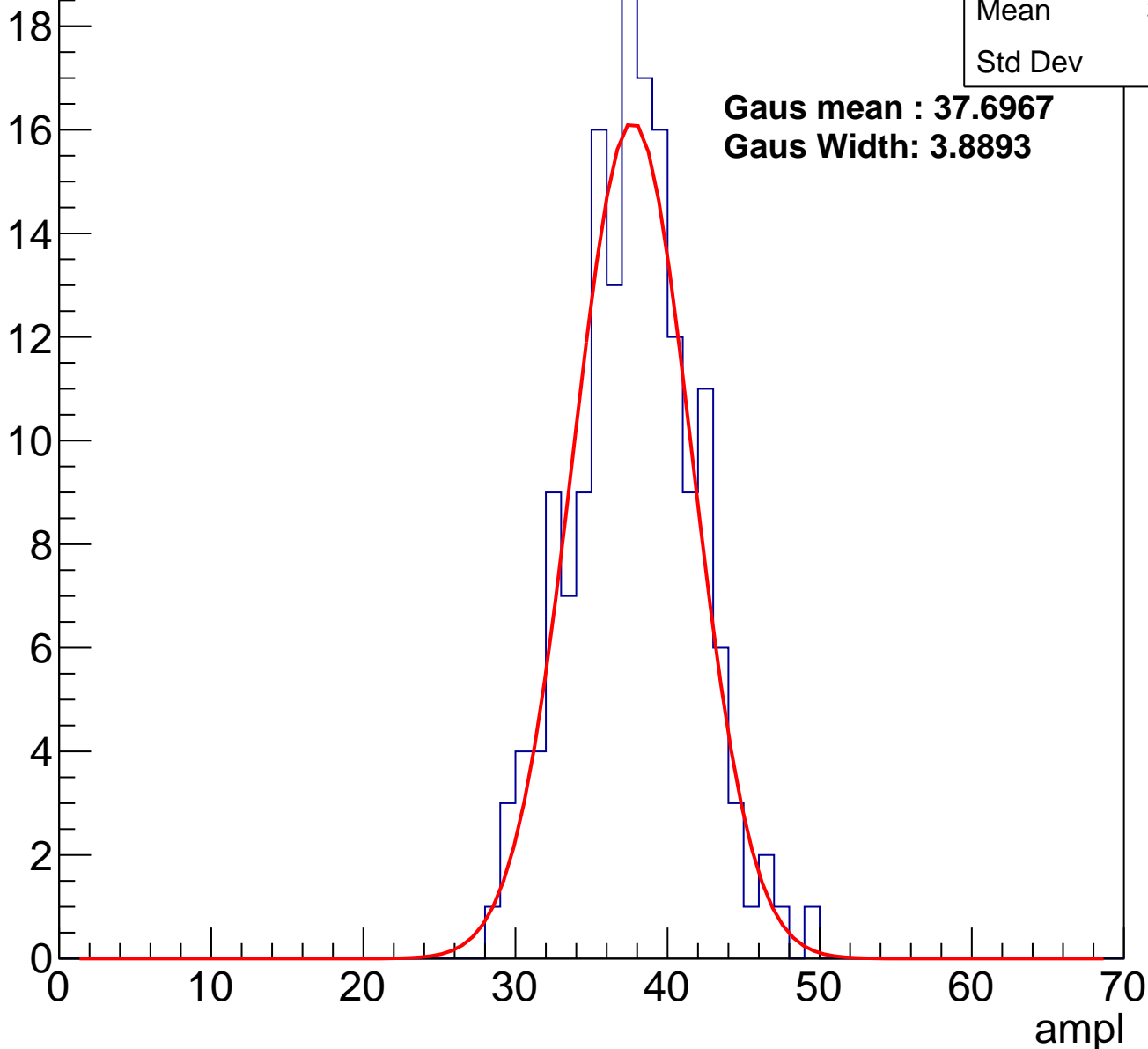
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	37.31
Std Dev	3.93

Gaus mean : 37.6967

Gaus Width: 3.8893

Entry



B0L101S, U25-ch101, adc2

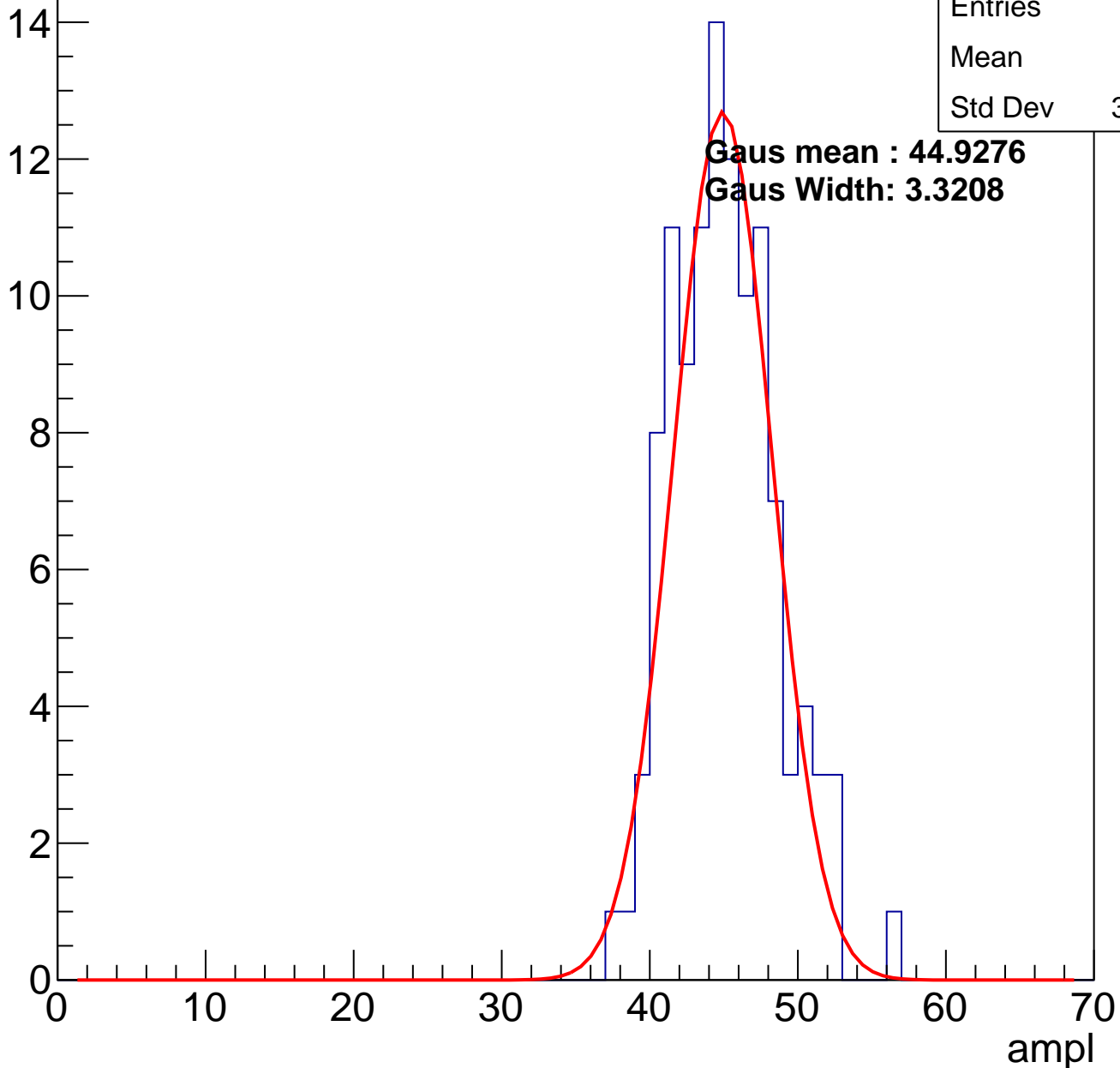
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	44.6
Std Dev	3.483

Gaus mean : 44.9276

Gaus Width: 3.3208

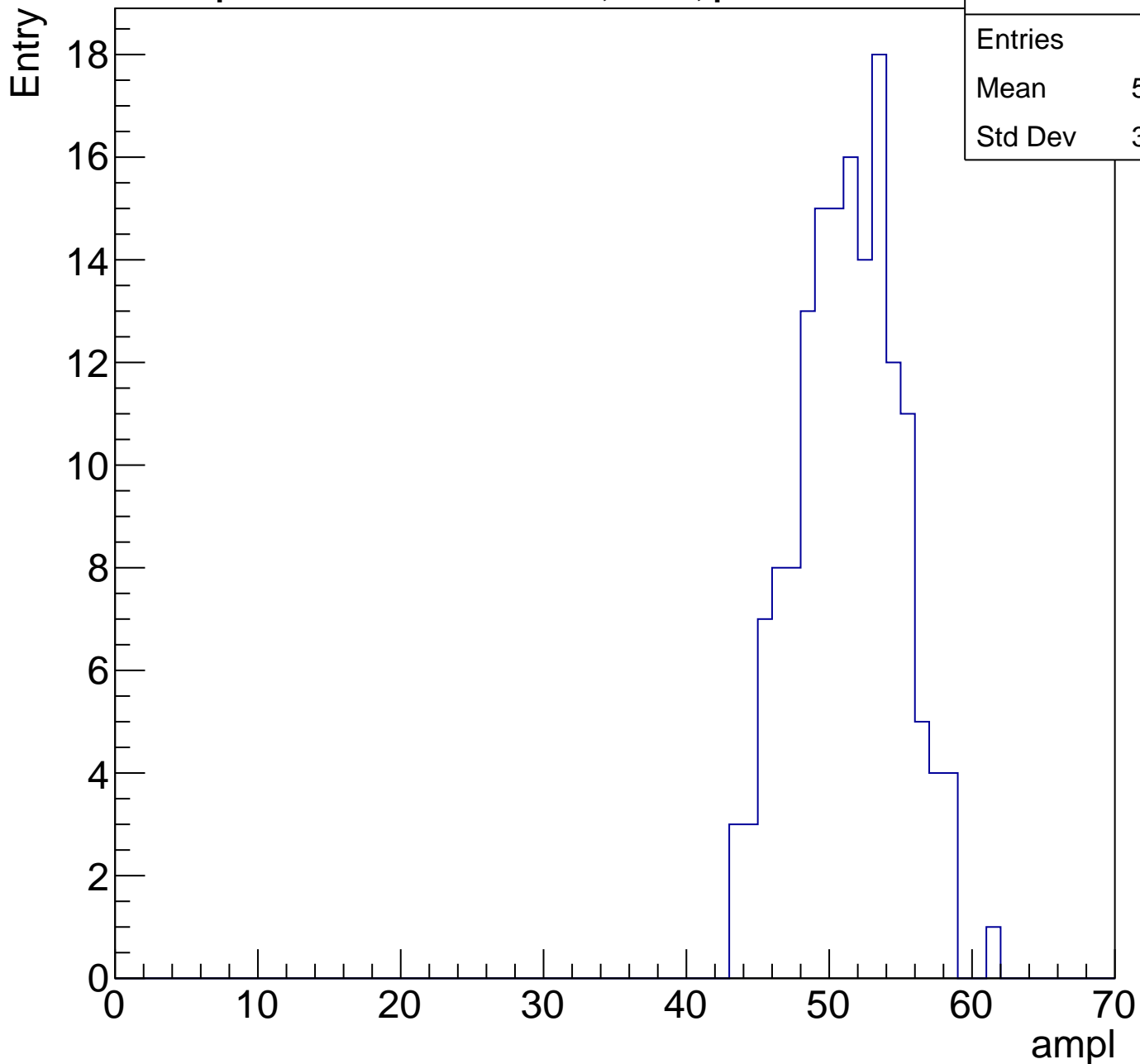
Entry



B0L101S, U25-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

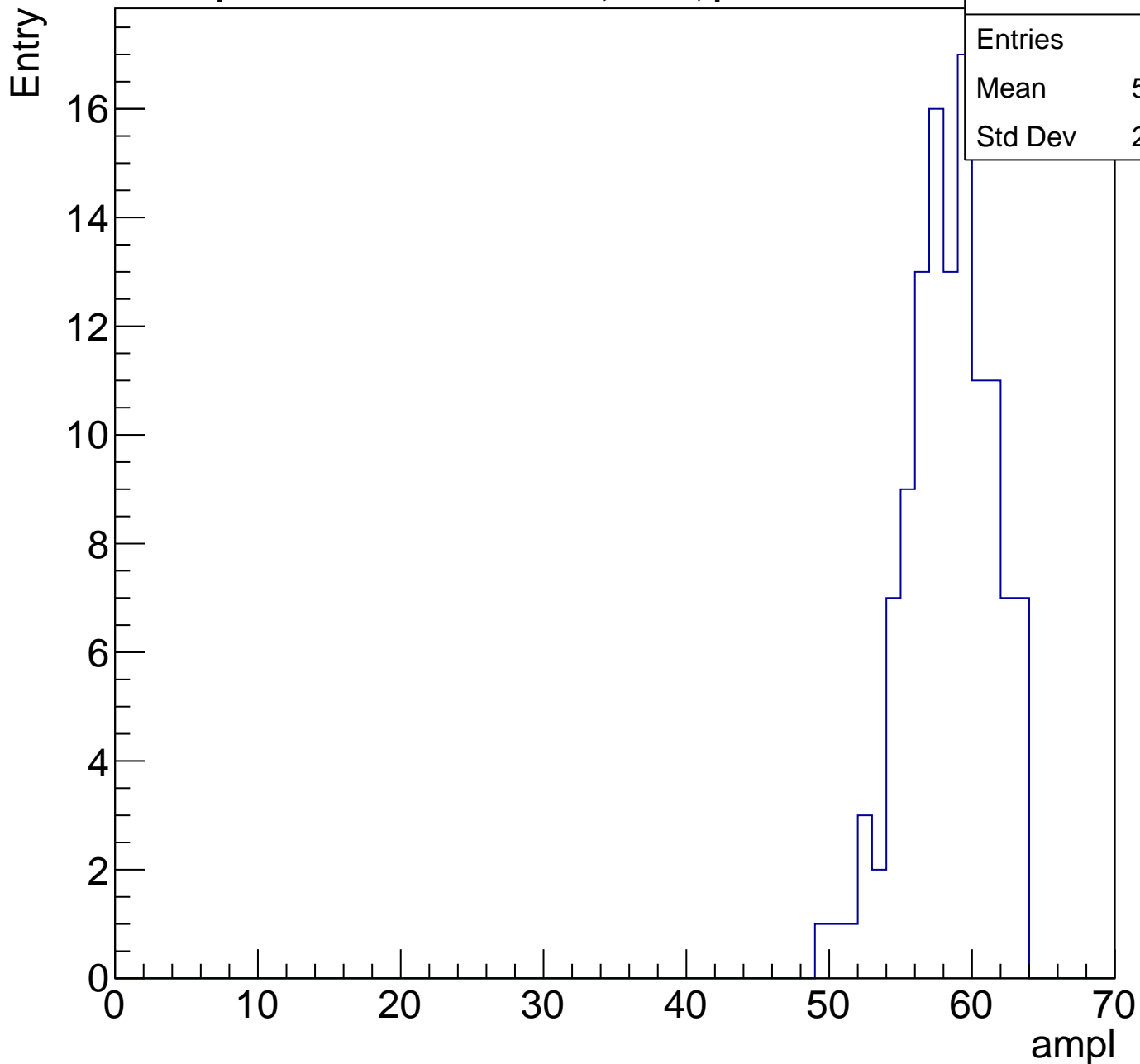
Entries	157
Mean	50.83
Std Dev	3.606



B0L101S, U25-ch101, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	57.88
Std Dev	2.982

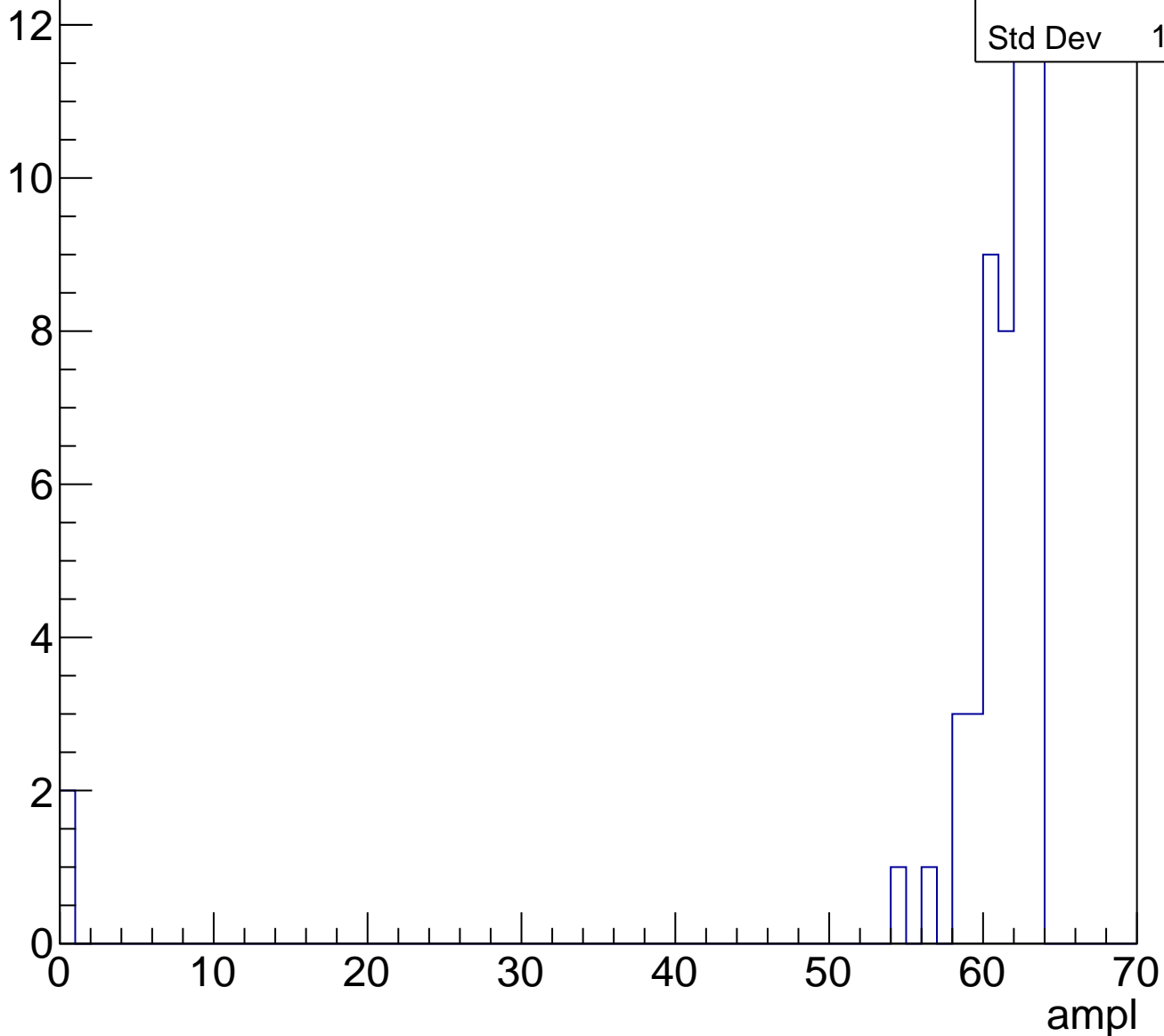


B0L101S, U25-ch101, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	52
Mean	58.67
Std Dev	11.88

Entry



B0L101S, U25-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L101S, U25-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch102, adc0

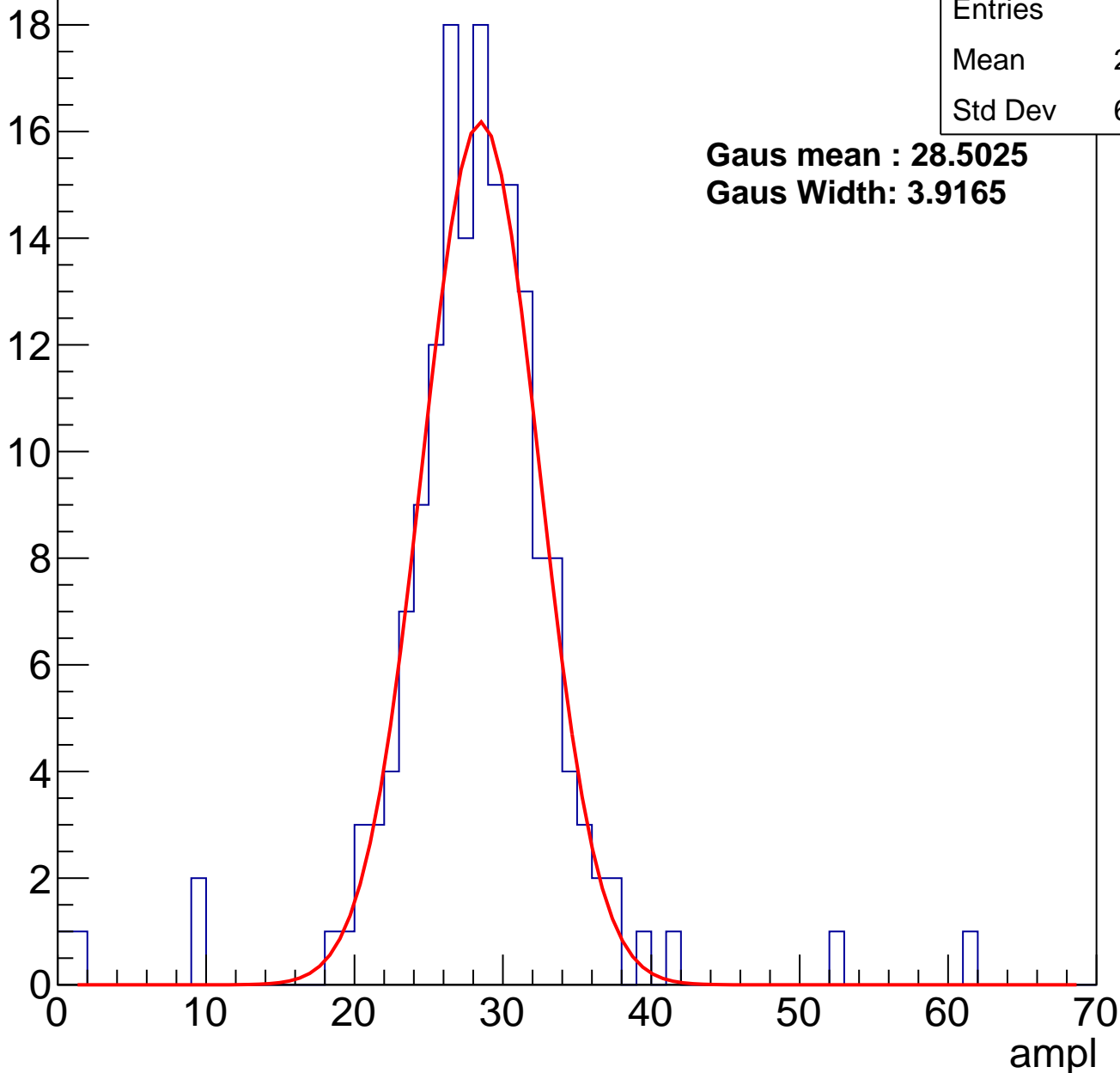
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	27.87
Std Dev	6.203

Gaus mean : 28.5025

Gaus Width: 3.9165

Entry



B0L101S, U25-ch102, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

149

Mean

35.51

Std Dev

3.778

Gaus mean : 35.8980

Gaus Width: 3.8211

0

10

20

30

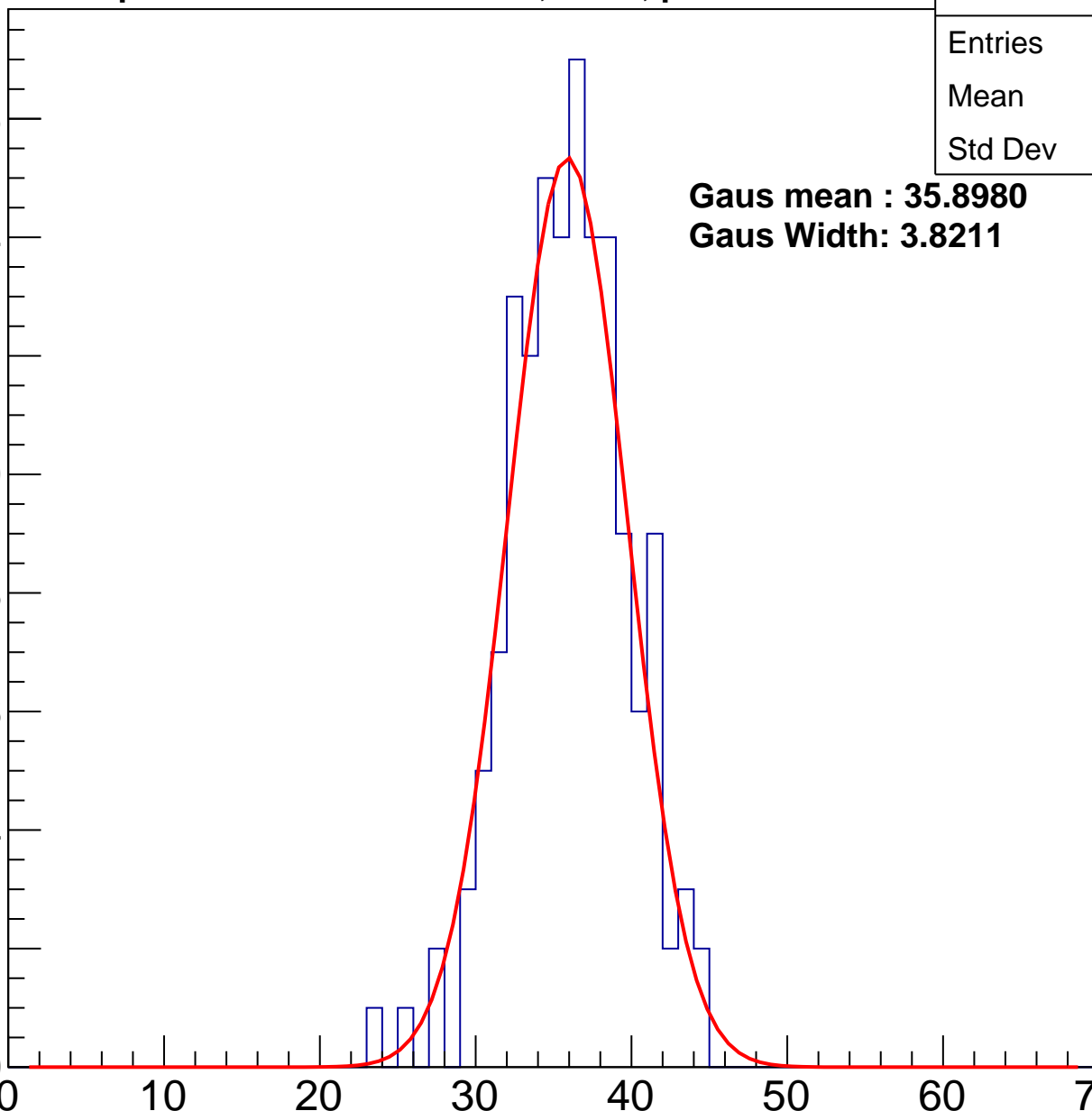
40

50

60

70

ampl



B0L101S, U25-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

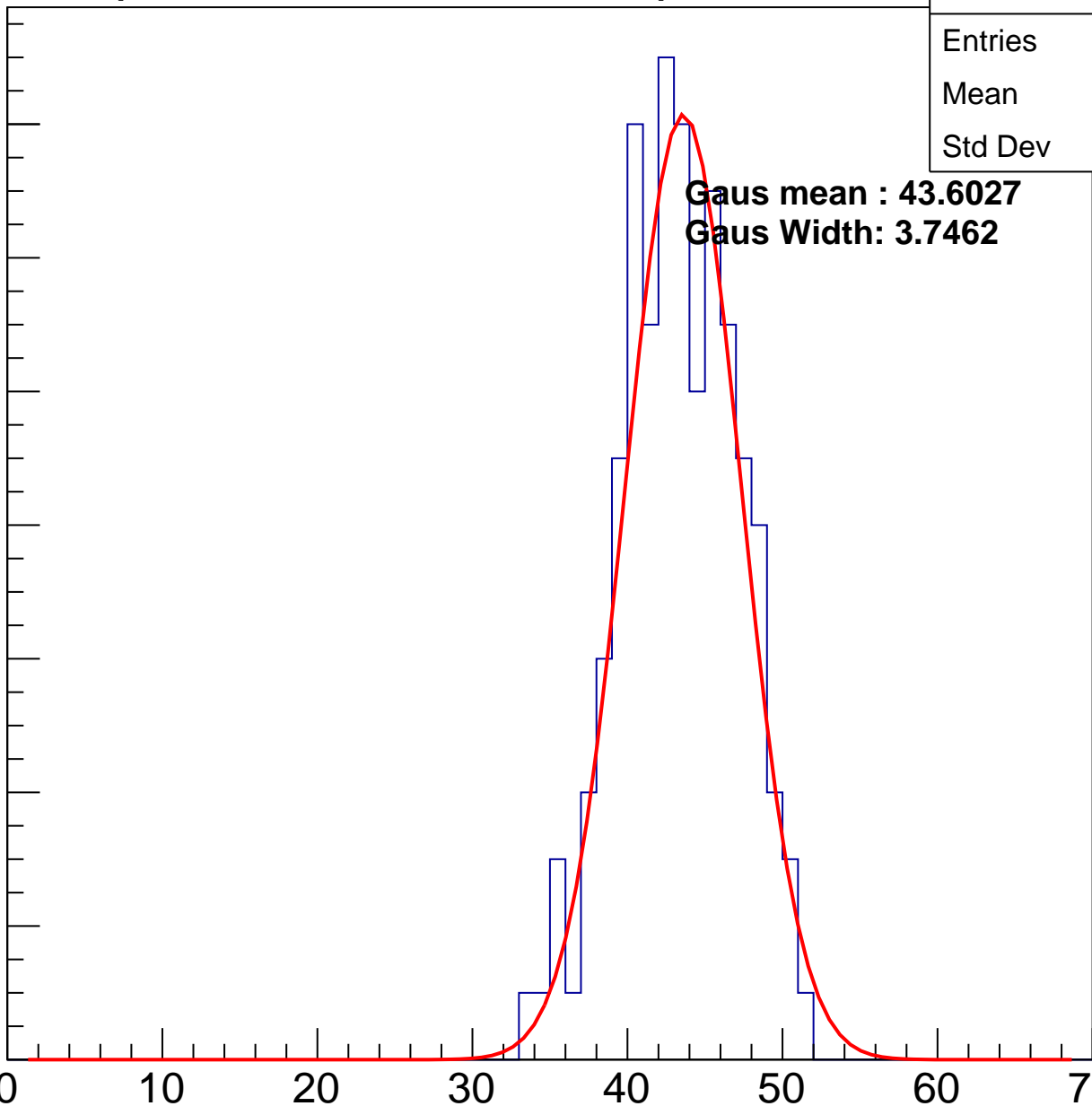
14
12
10
8
6
4
2
0

Entries	138
Mean	42.85
Std Dev	3.712

Gaus mean : 43.6027

Gaus Width: 3.7462

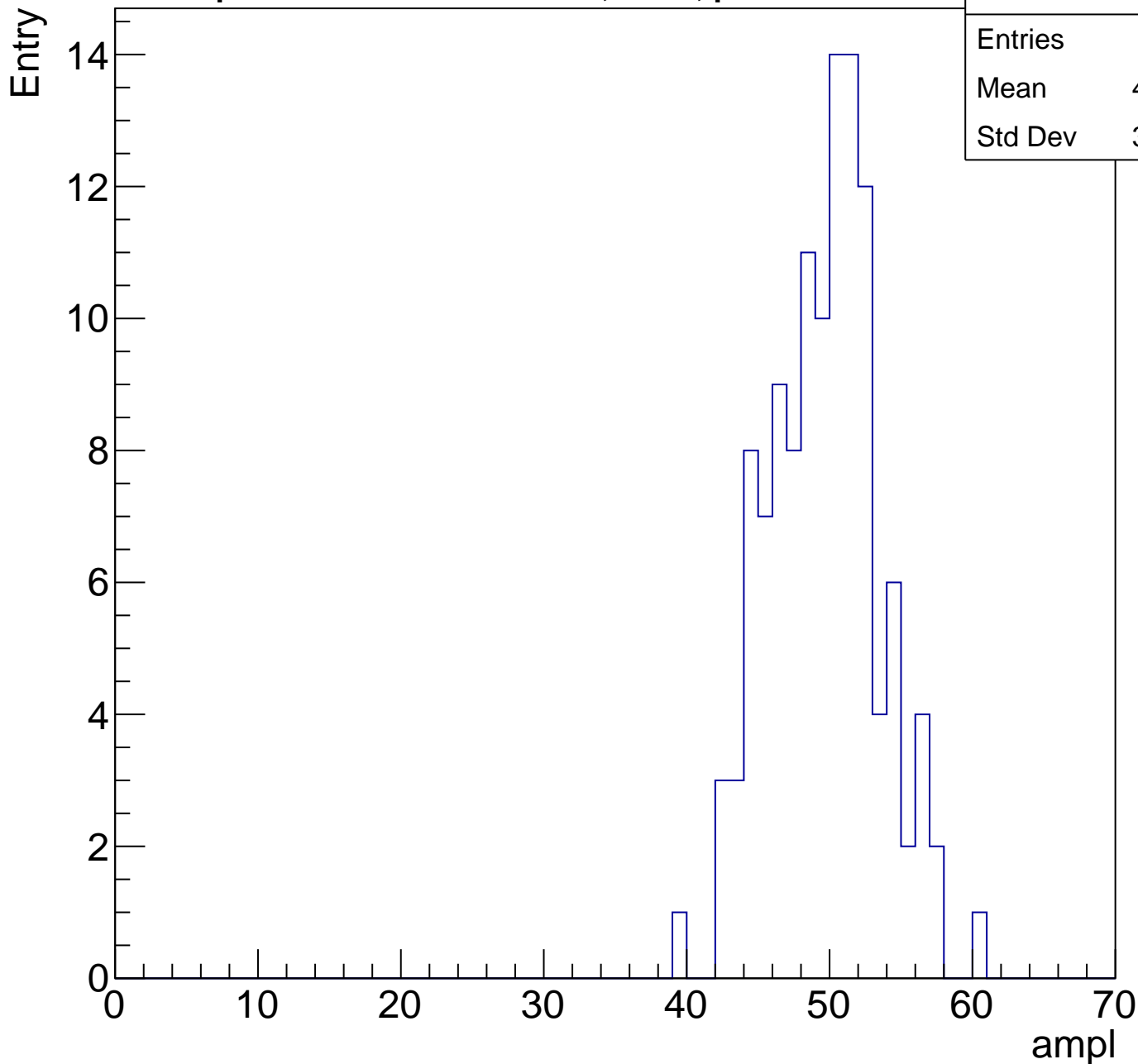
ampl



B0L101S, U25-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	49.17
Std Dev	3.776



B0L101S, U25-ch102, adc4

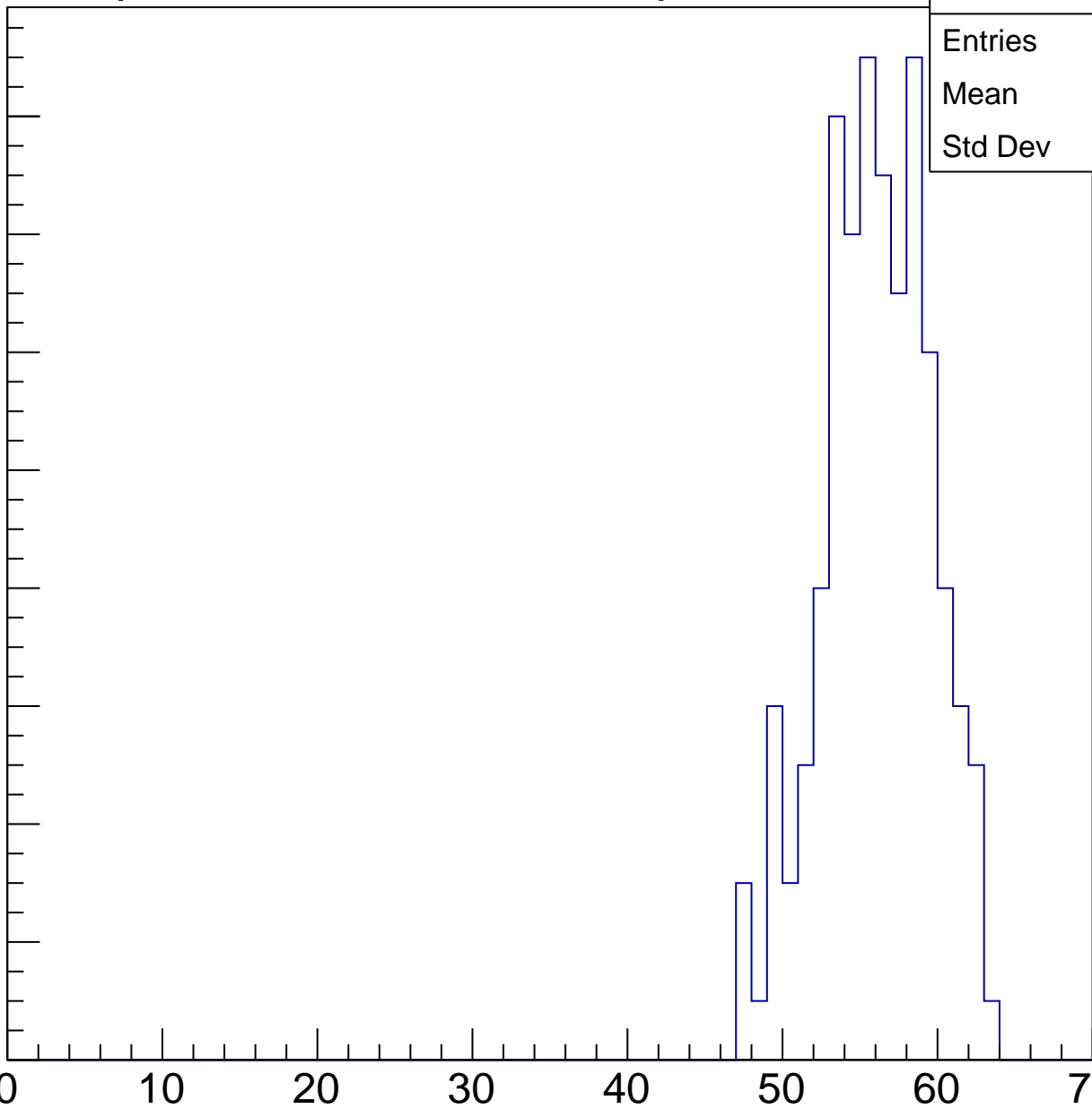
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	55.58
Std Dev	3.51

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch102, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

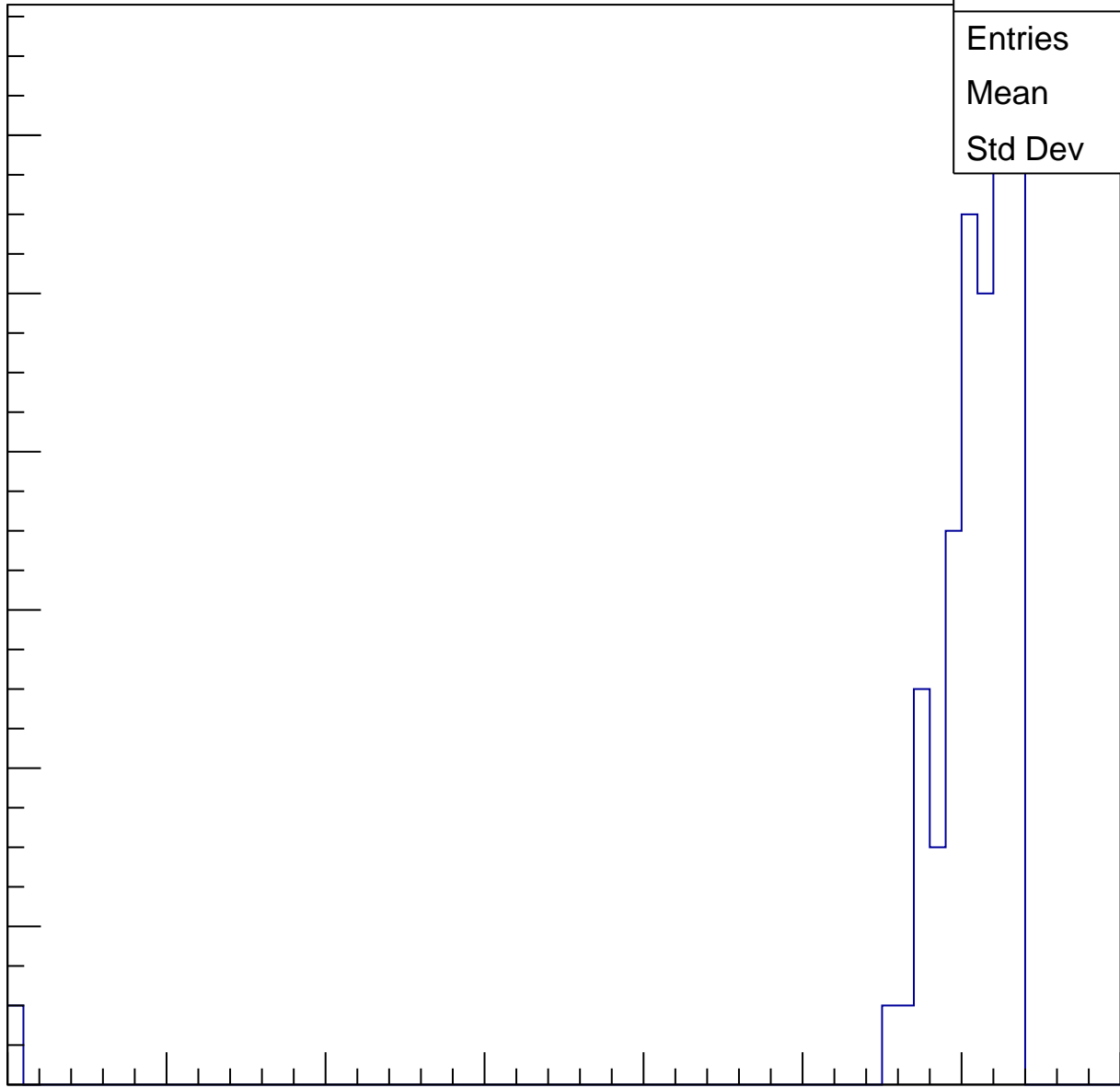
2

0

Entries	64
Mean	59.62
Std Dev	7.777

ampl

0 10 20 30 40 50 60 70

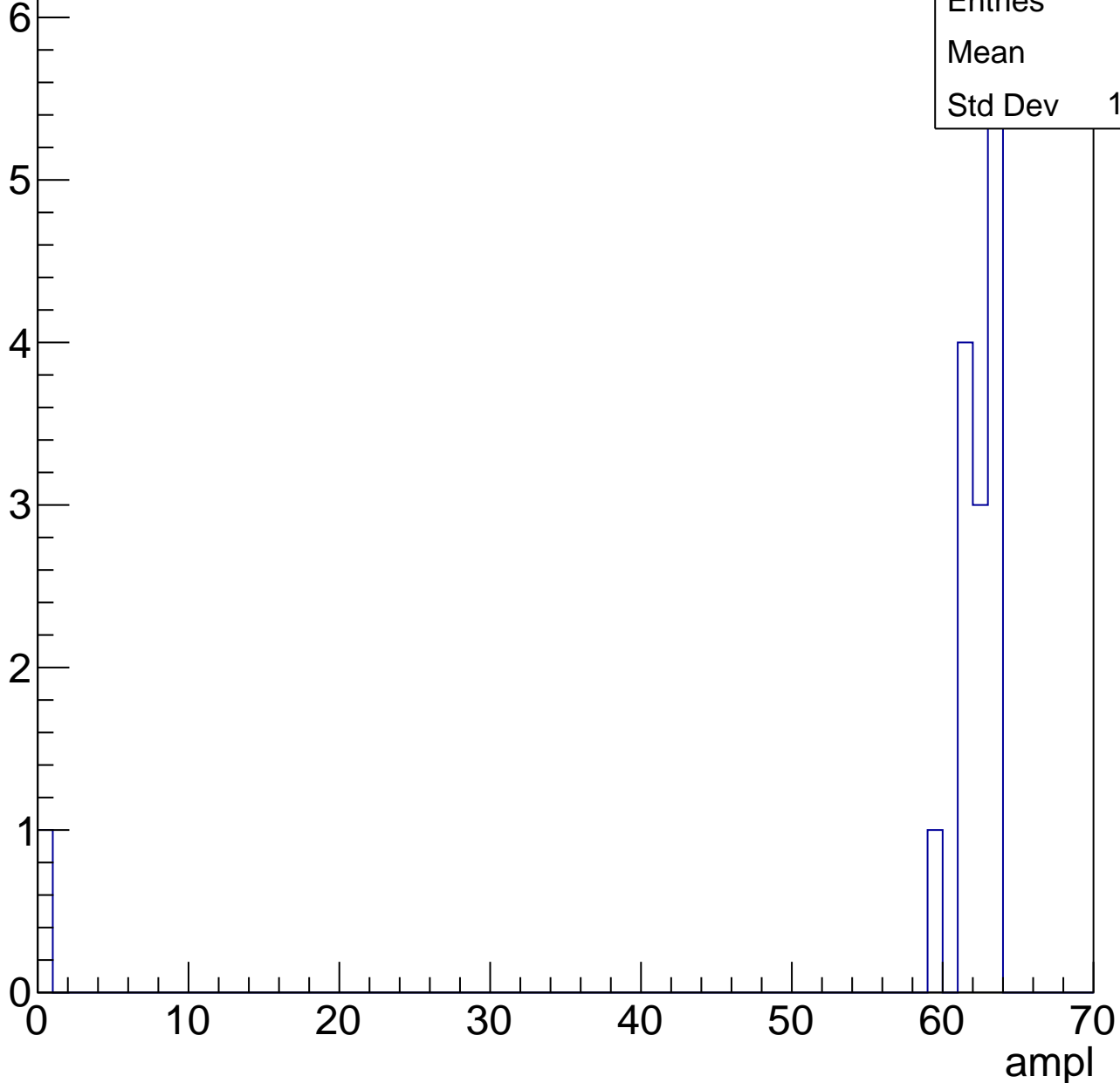


B0L101S, U25-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.8
Std Dev	15.49



B0L101S, U25-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U25-ch103, adc0

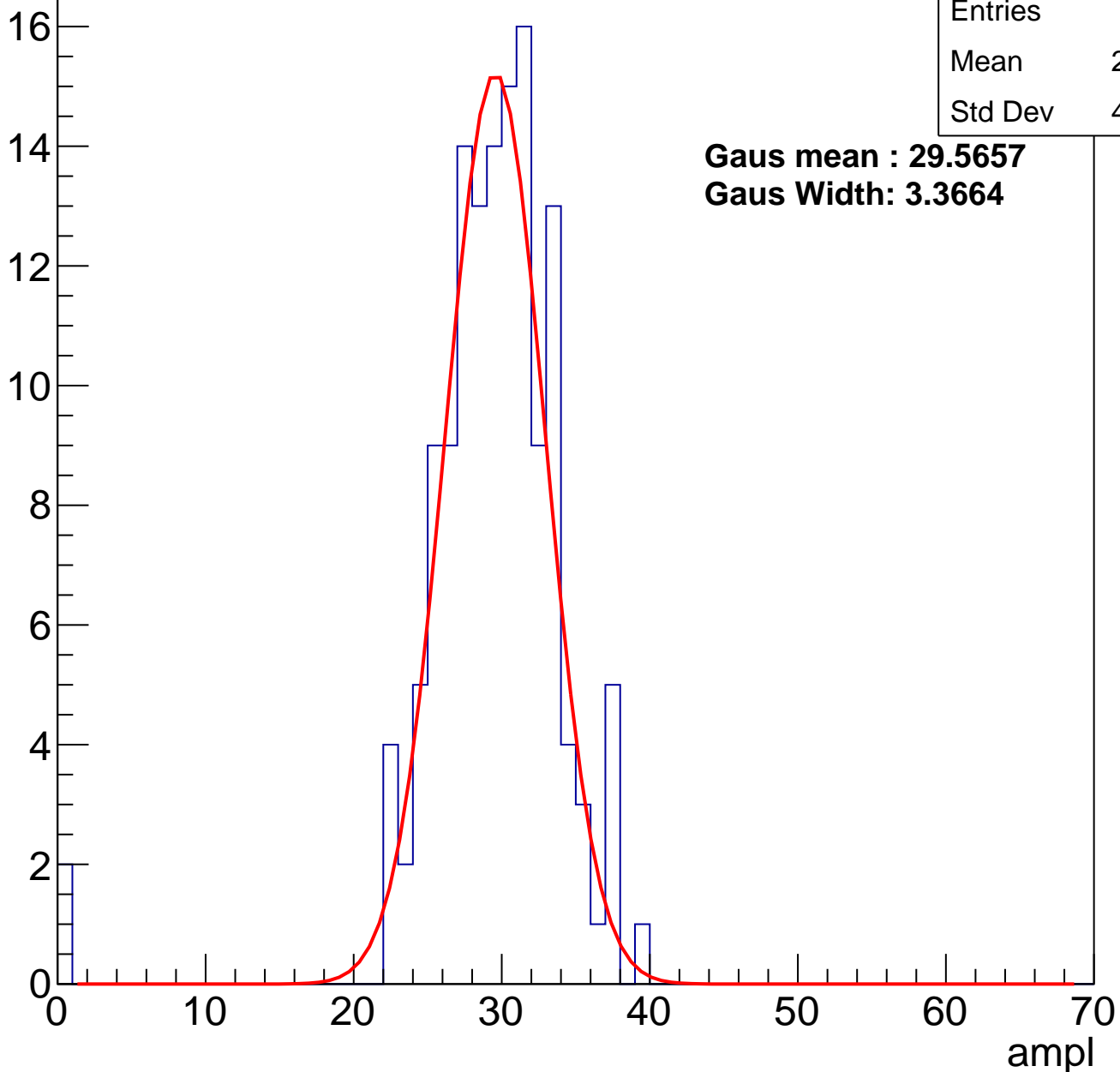
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	28.96
Std Dev	4.956

Gaus mean : 29.5657

Gaus Width: 3.3664

Entry



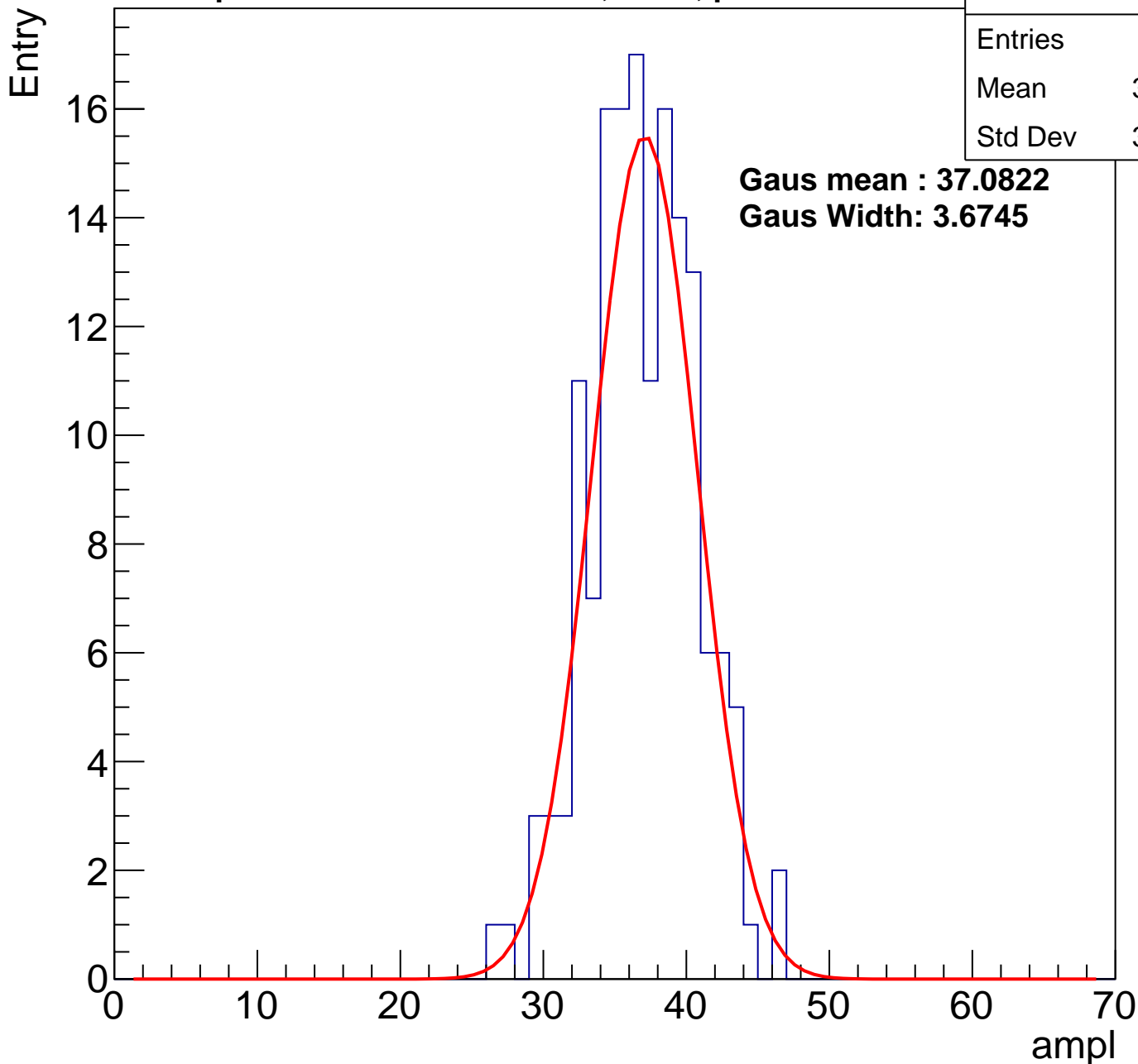
B0L101S, U25-ch103, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	36.53
Std Dev	3.692

Gaus mean : 37.0822

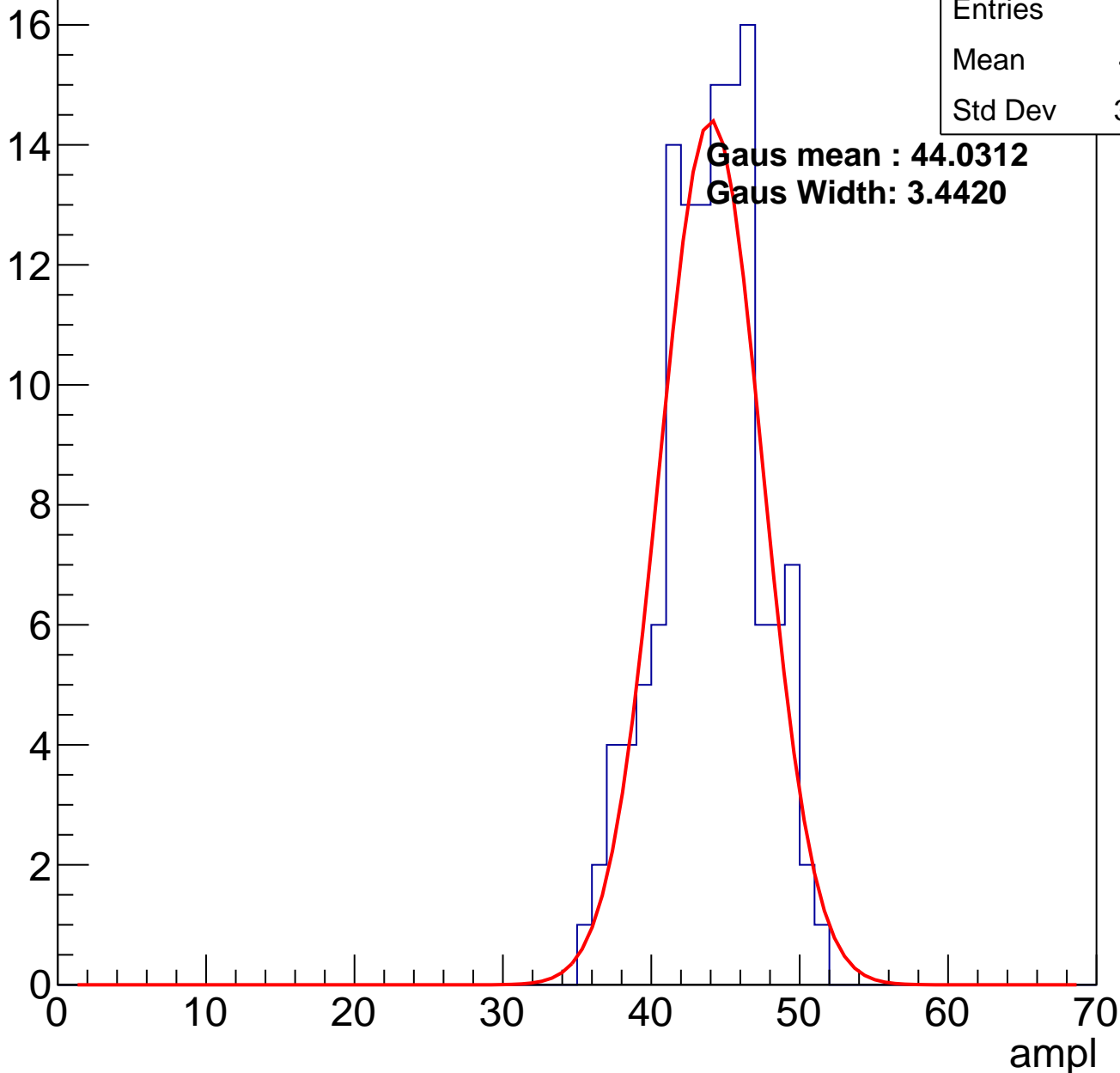
Gaus Width: 3.6745



B0L101S, U25-ch103, adc2

calib_packv5_042523_0143.root, FC#1, port C1

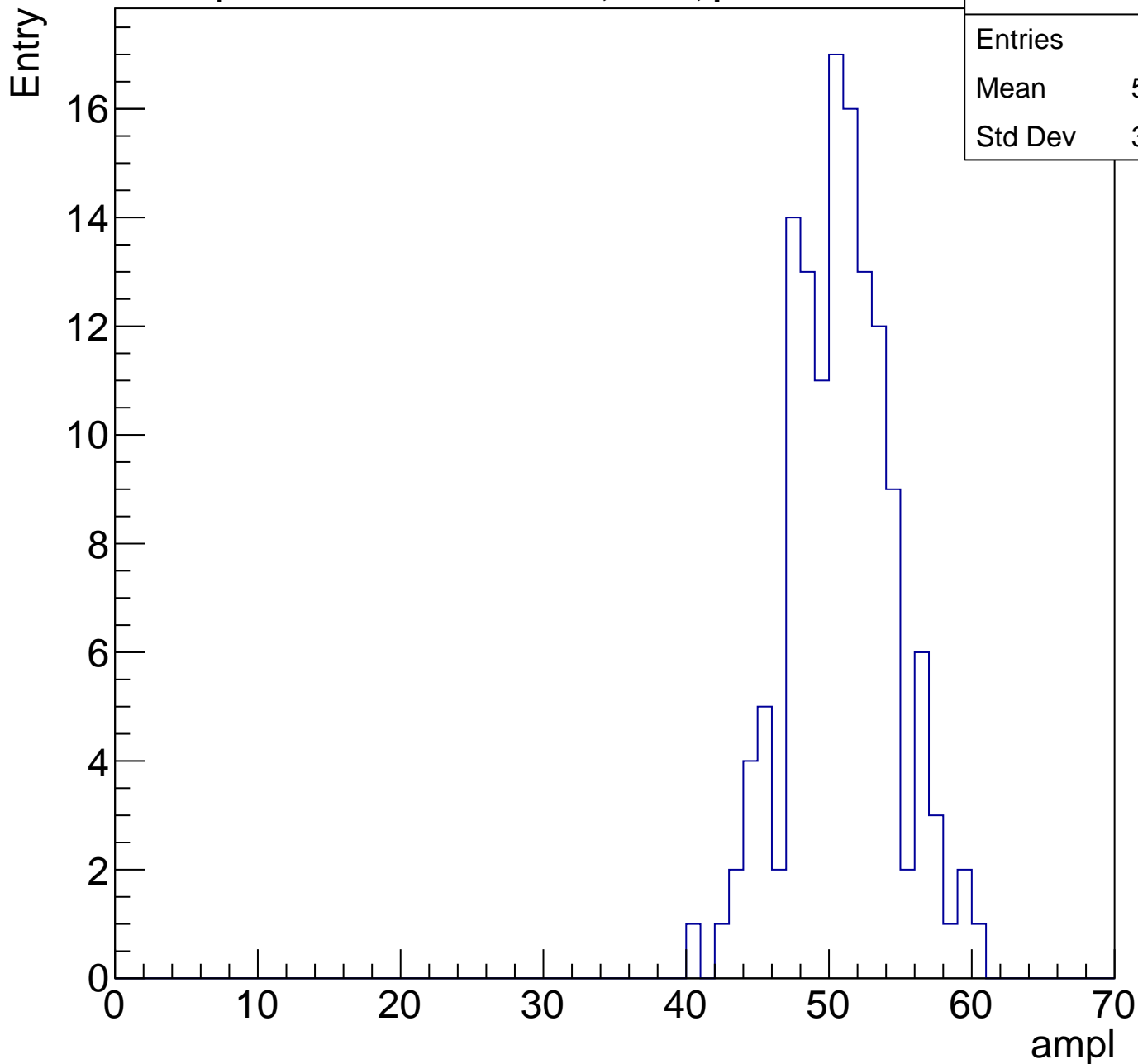
Entry



B0L101S, U25-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

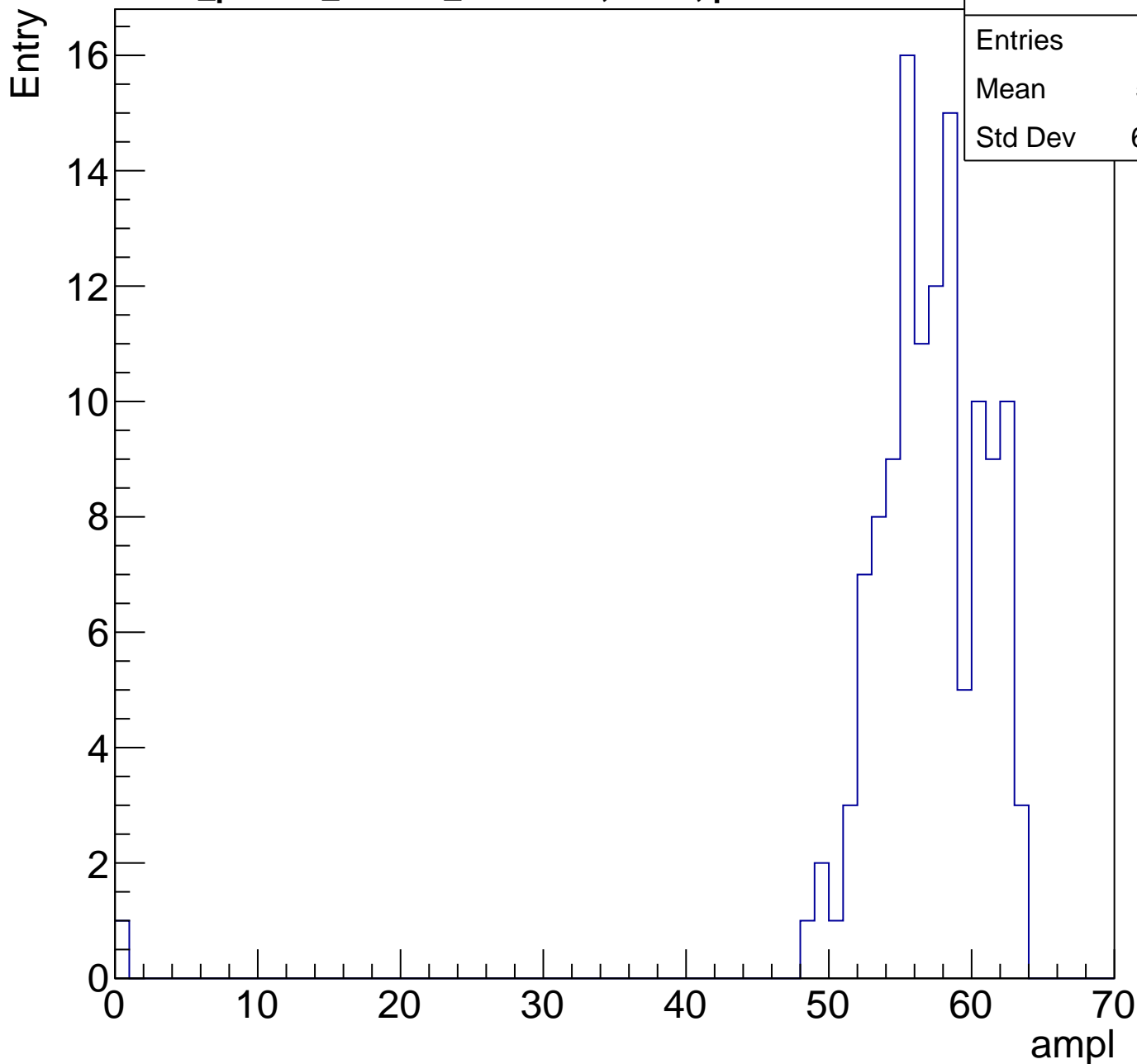
Entries	135
Mean	50.36
Std Dev	3.645



B0L101S, U25-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	56.31
Std Dev	6.143



B0L101S, U25-ch103, adc5

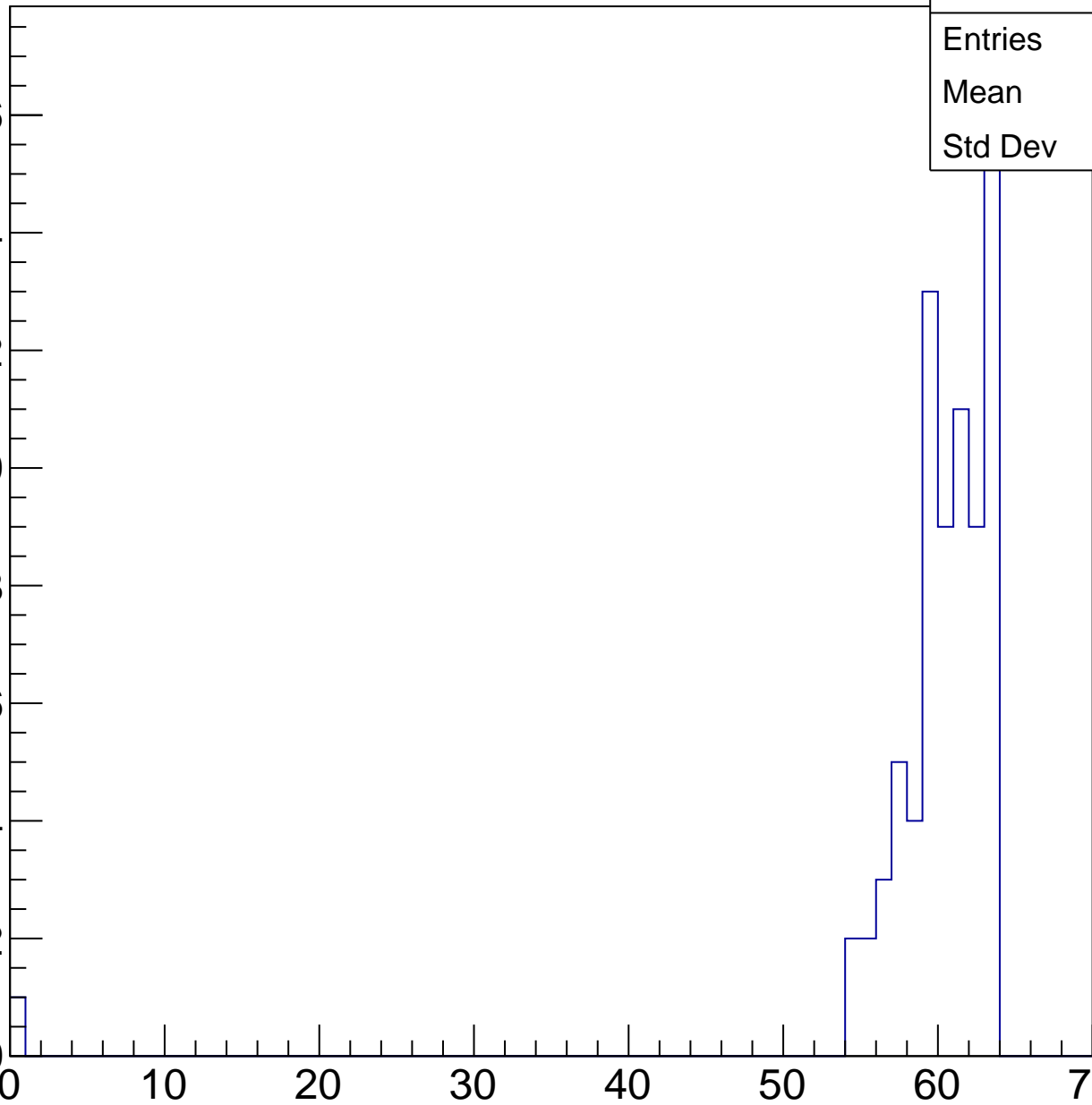
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.34
Std Dev	7.265

ampl



B0L101S, U25-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch104, adc0

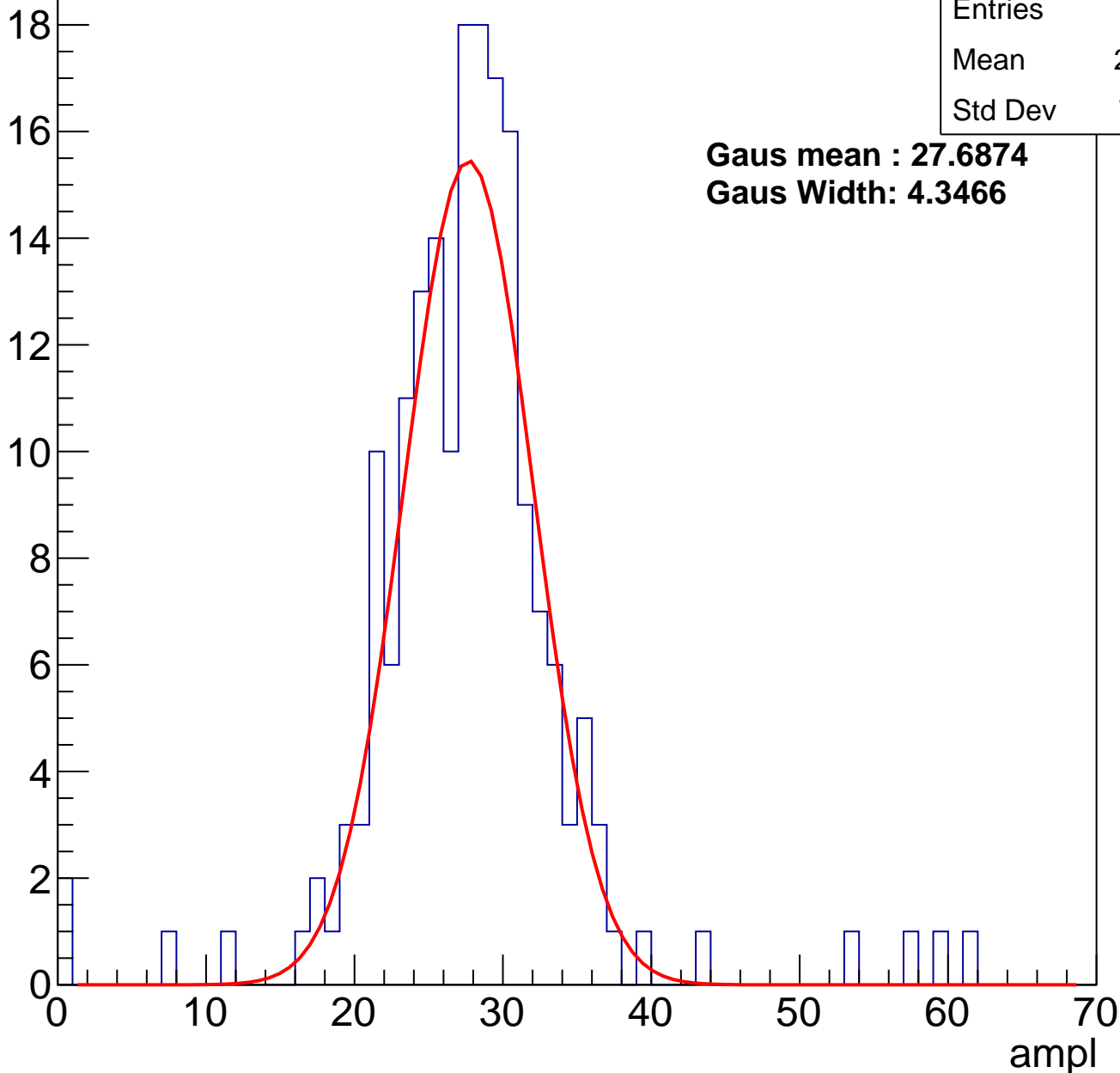
calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	27.37
Std Dev	7.101

Gaus mean : 27.6874

Gaus Width: 4.3466

Entry



B0L101S, U25-ch104, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	35.35
Std Dev	4.069

Gaus mean : 36.0168

Gaus Width: 3.7546

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

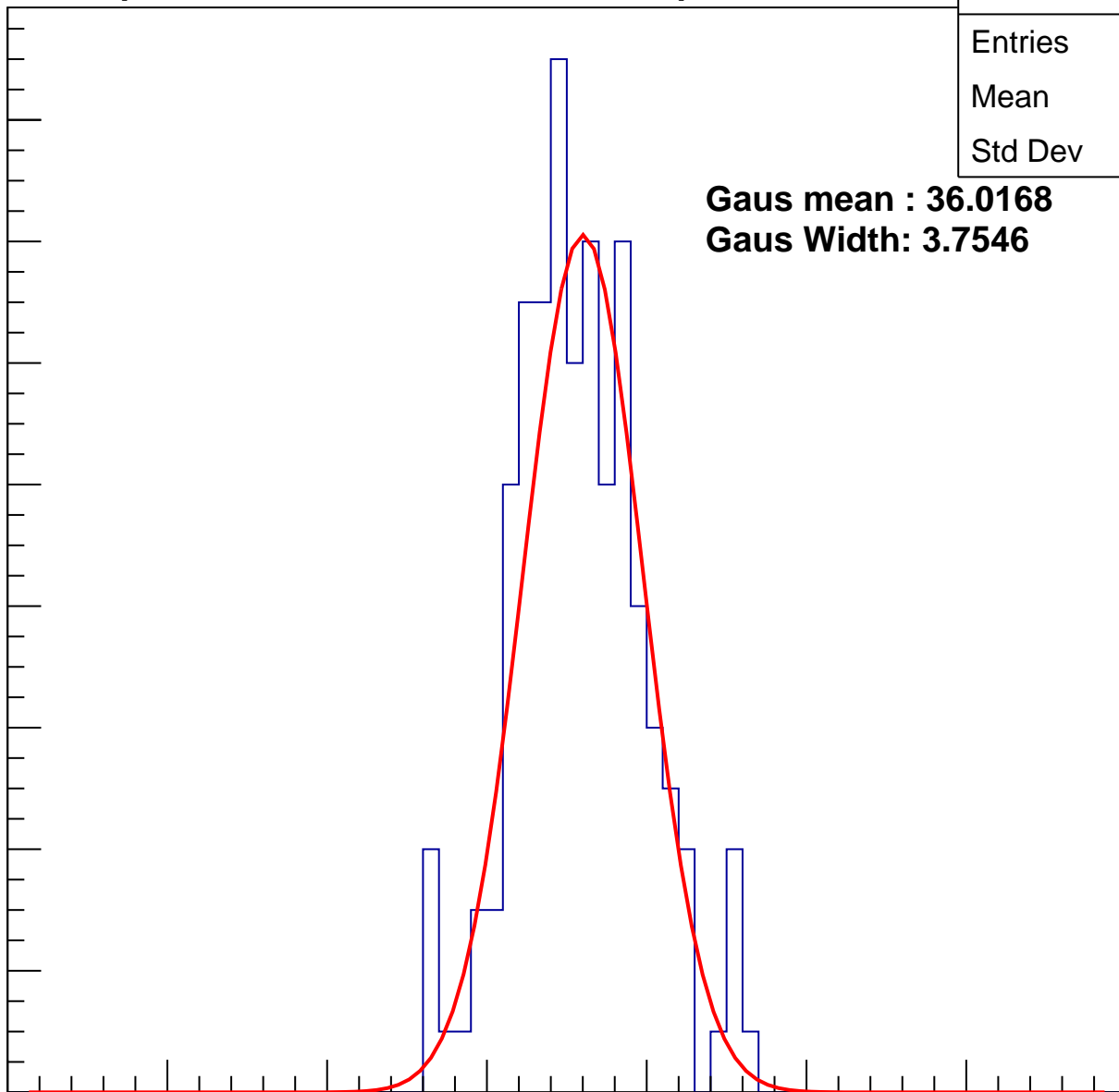
40

50

60

70

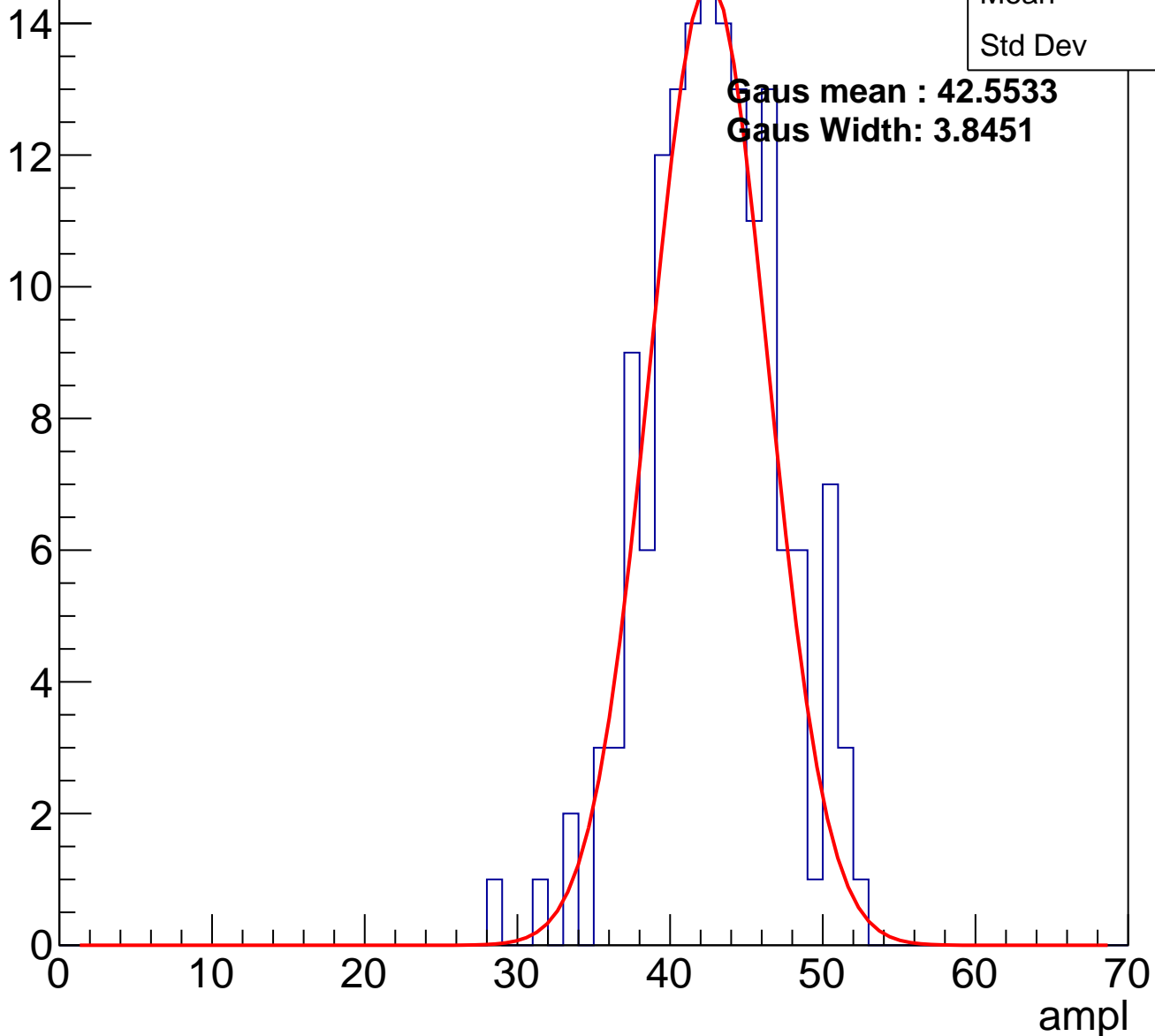
ampl



B0L101S, U25-ch104, adc2

calib_packv5_042523_0143.root, FC#1, port C1

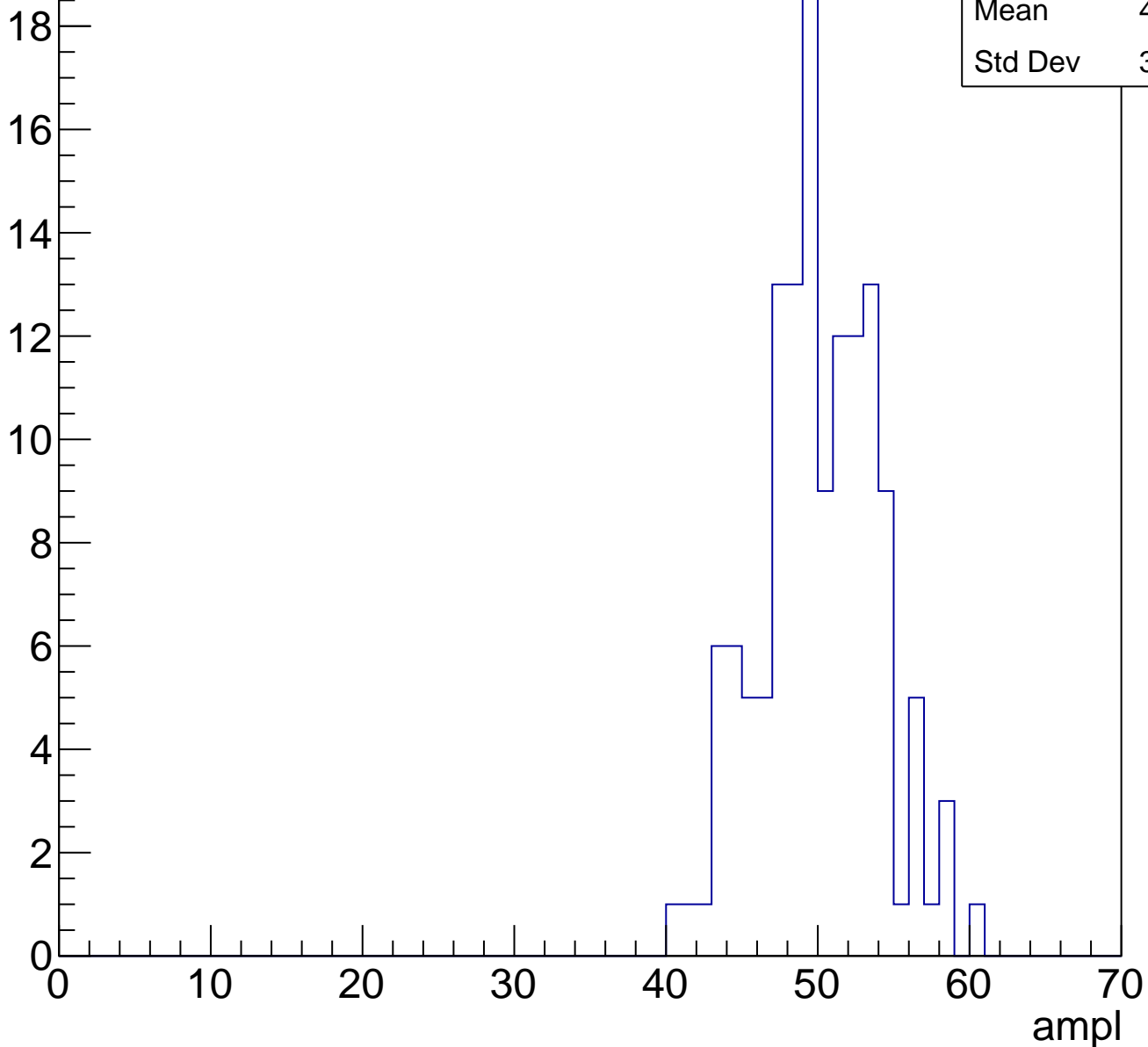
Entry



B0L101S, U25-ch104, adc3

calib_packv5_042523_0143.root, FC#1, port C1

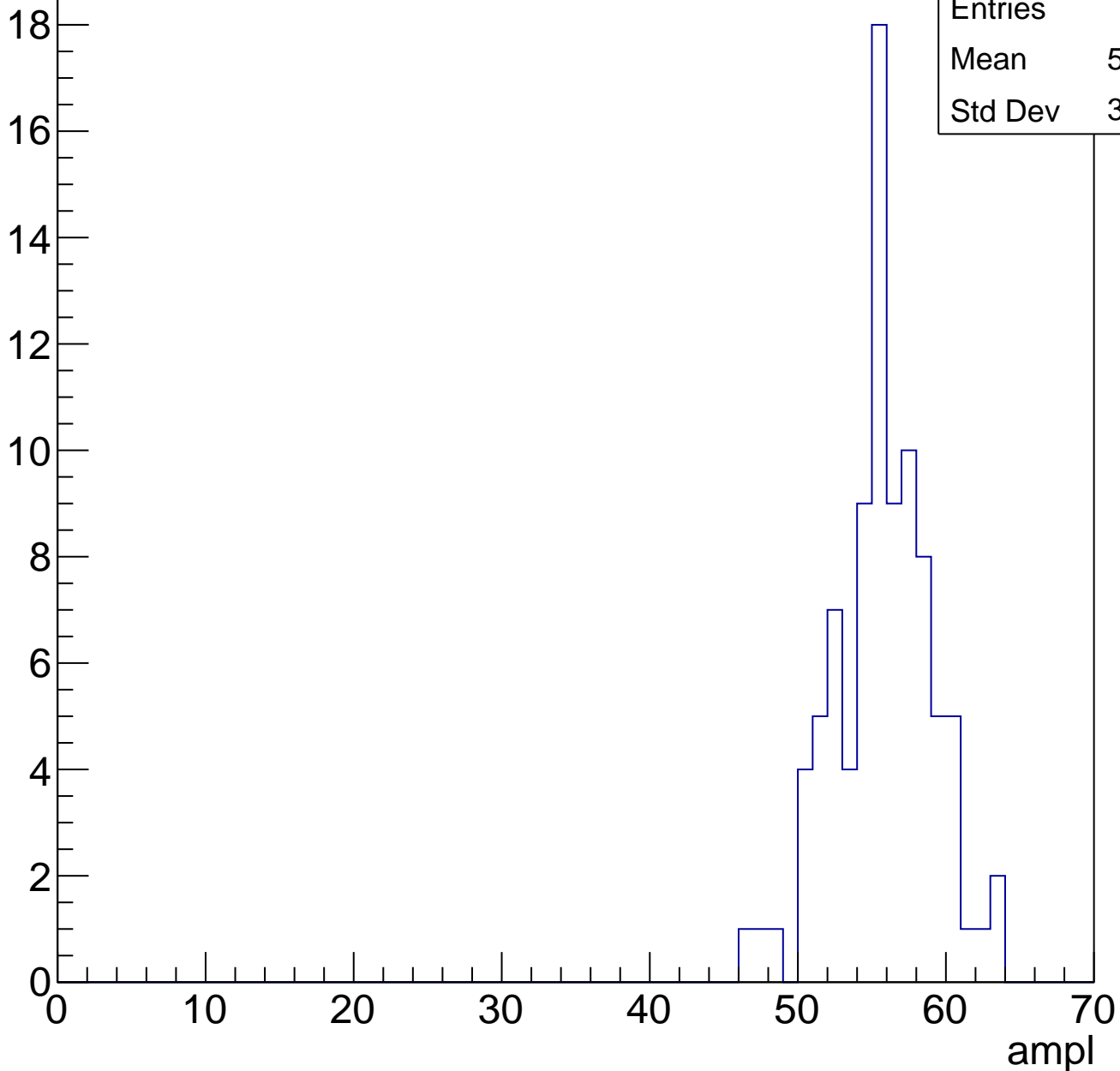
Entry



B0L101S, U25-ch104, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	91
Mean	55.27
Std Dev	3.305

B0L101S, U25-ch104, adc5

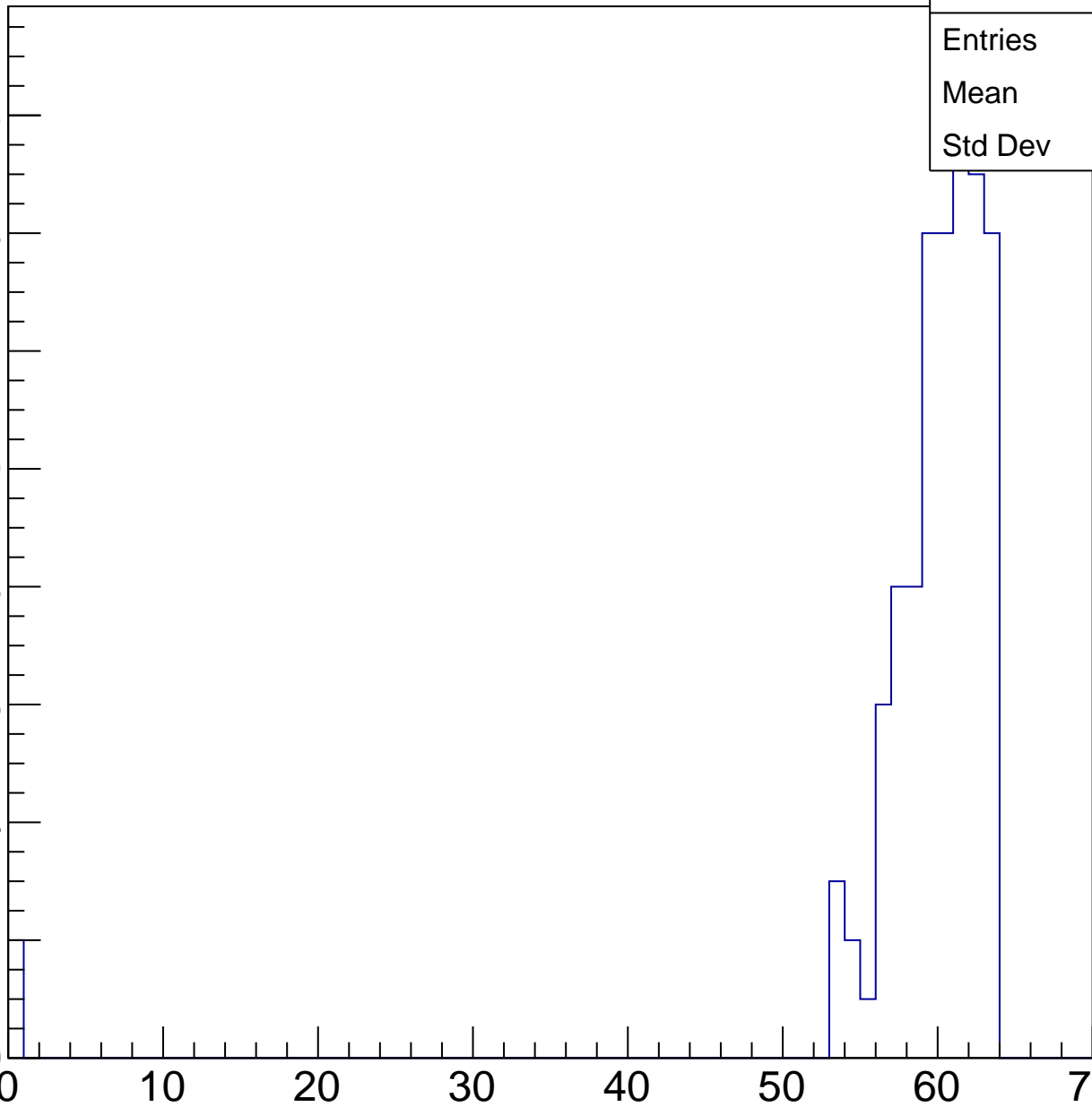
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.59
Std Dev	8.578

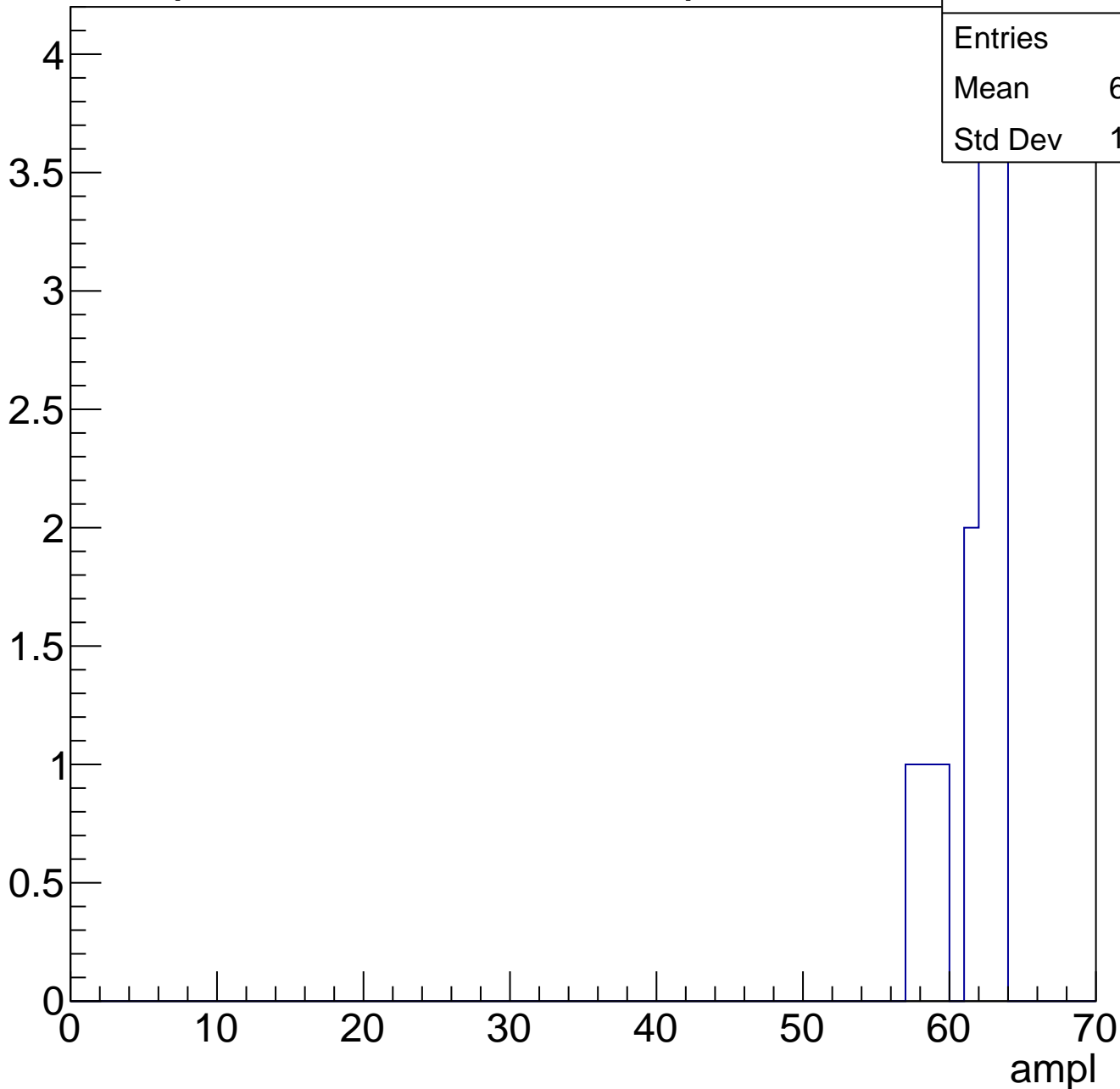
ampl



B0L101S, U25-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch105, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.84
Std Dev	4.608

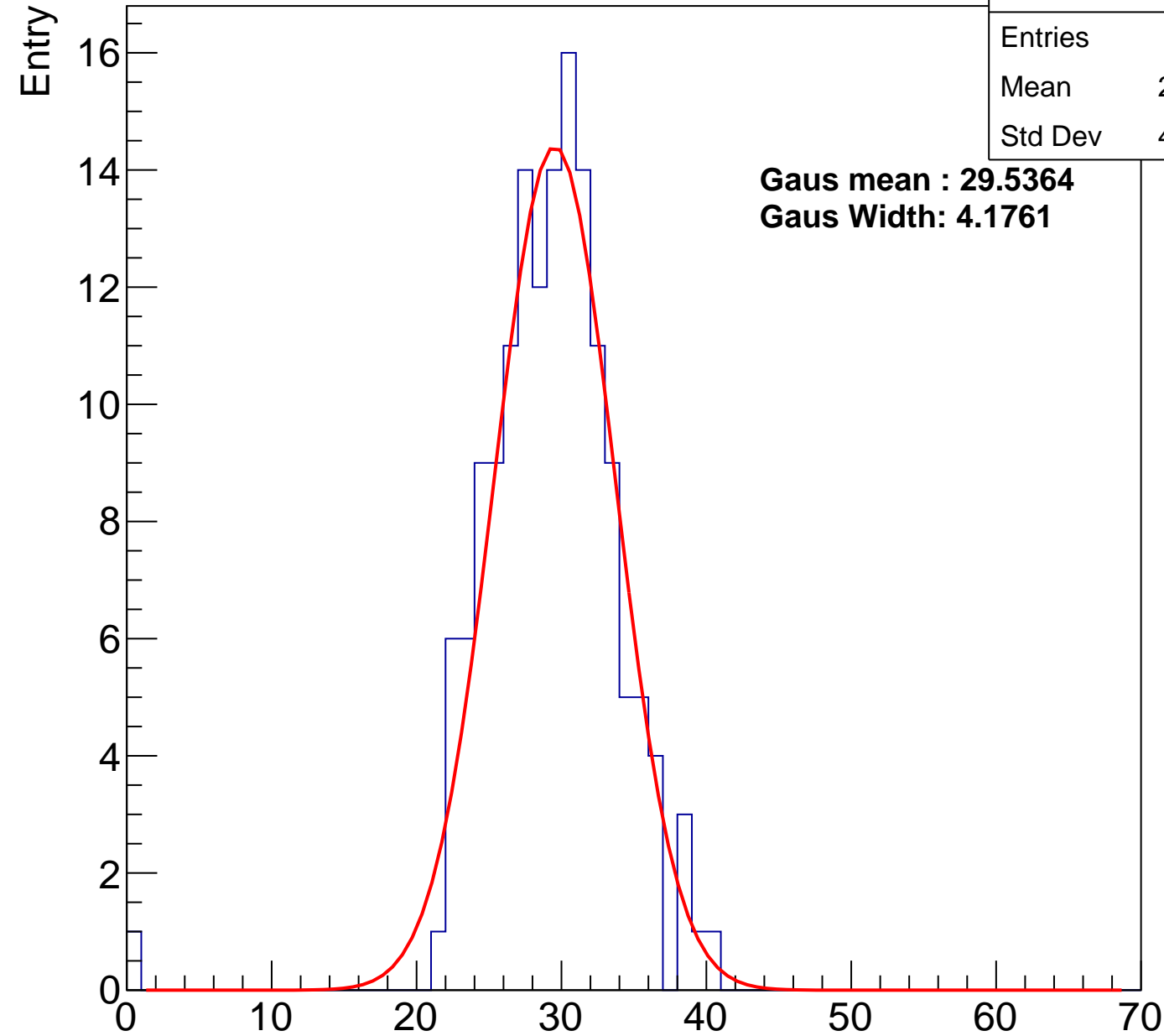
Gaus mean : 29.5364

Gaus Width: 4.1761

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch105, adc1

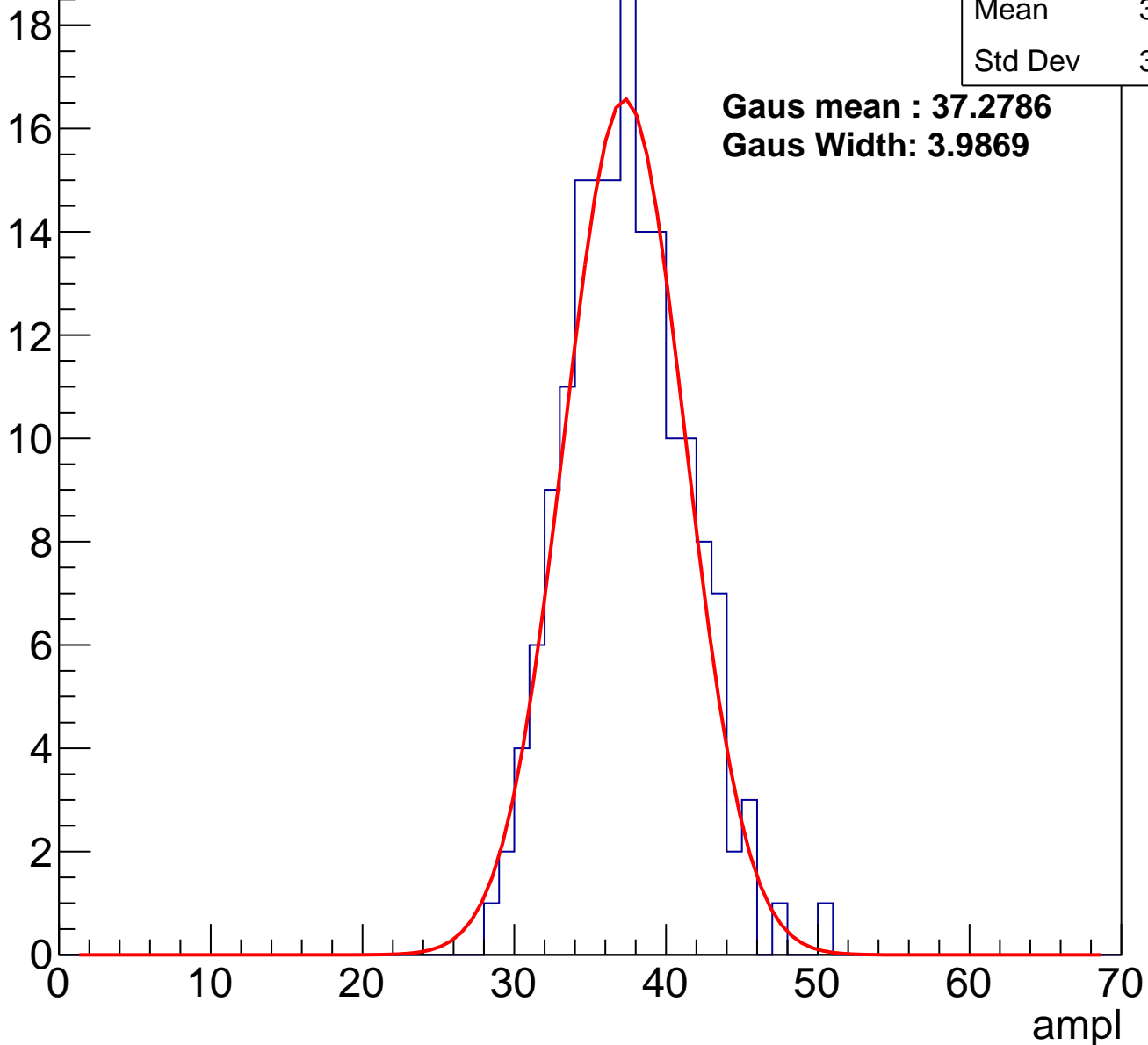
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	36.92
Std Dev	3.917

Gaus mean : 37.2786

Gaus Width: 3.9869

Entry



B0L101S, U25-ch105, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	117
Mean	44.25
Std Dev	3.595

Gaus mean : 44.7477

Gaus Width: 3.7368

0

2

4

6

8

10

12

Entry

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	44.25
Std Dev	3.595

Gaus mean : 44.7477

Gaus Width: 3.7368

B0L101S, U25-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

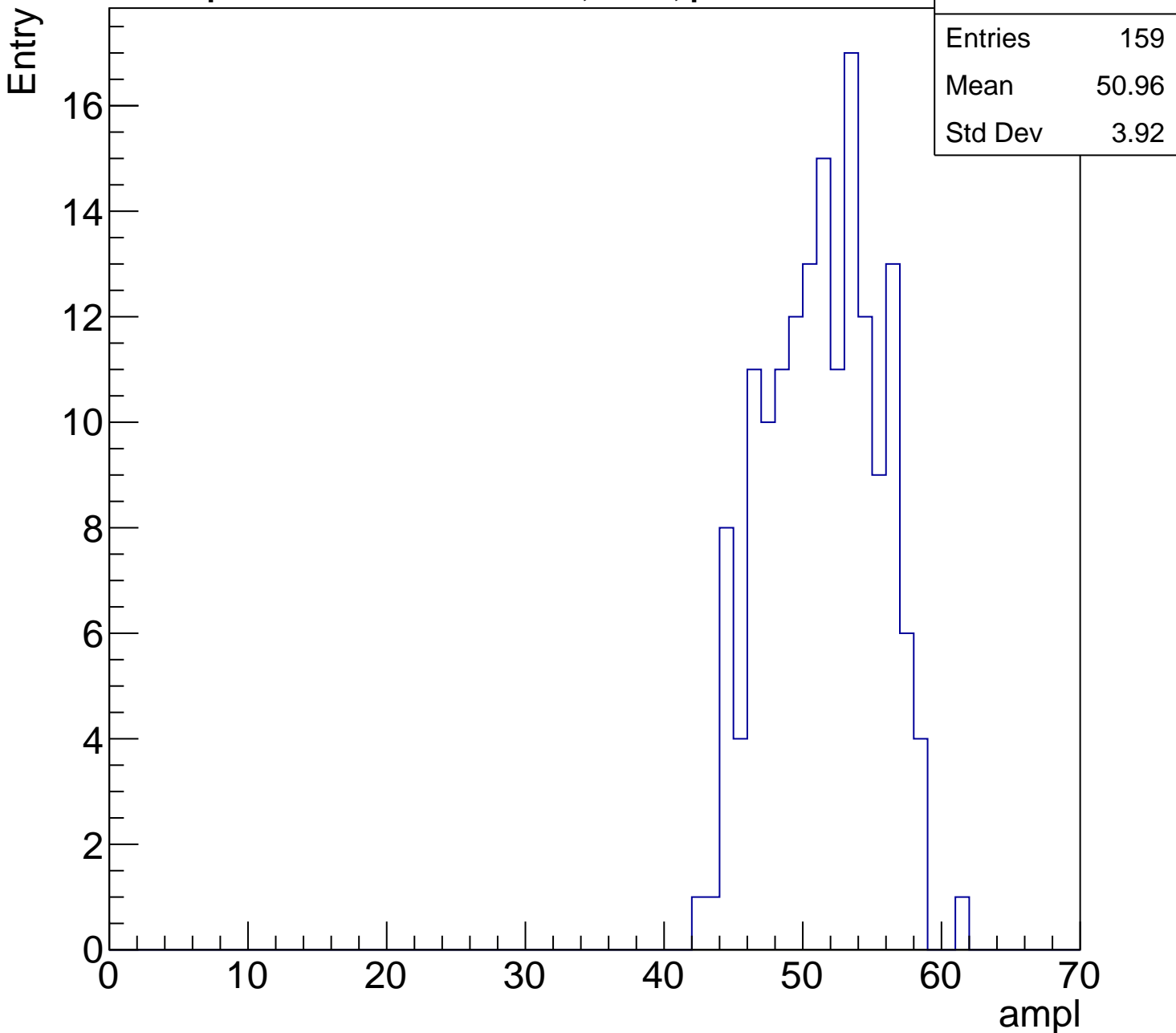
Entry

Entries	159
Mean	50.96
Std Dev	3.92

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch105, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	56.38
Std Dev	6.848

Entry

10

8

6

4

2

0

0

10

20

30

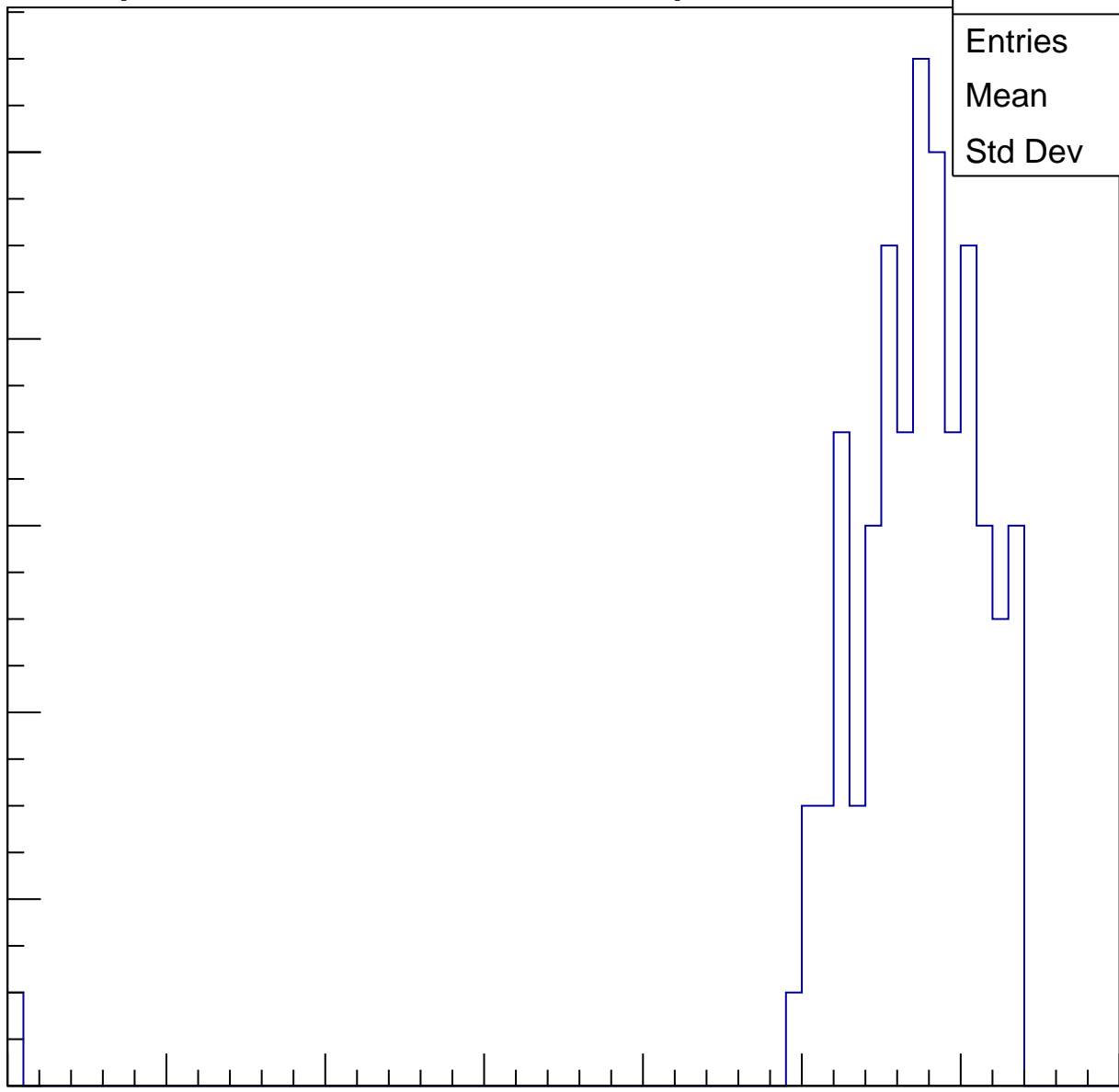
40

50

60

70

ampl



B0L101S, U25-ch105, adc5

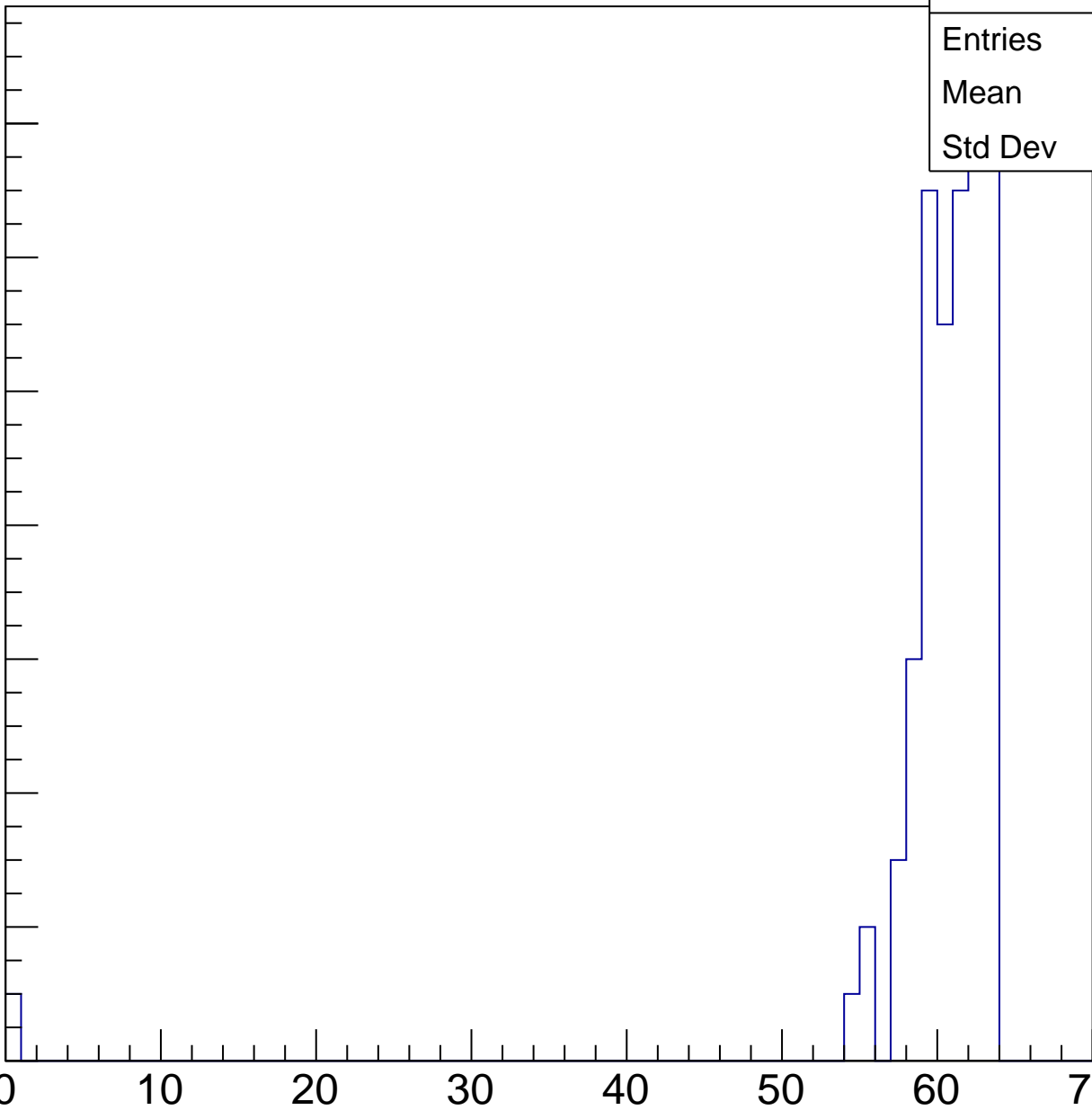
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	59.68
Std Dev	7.063

ampl



B0L101S, U25-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch106, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	27.43
Std Dev	5.36

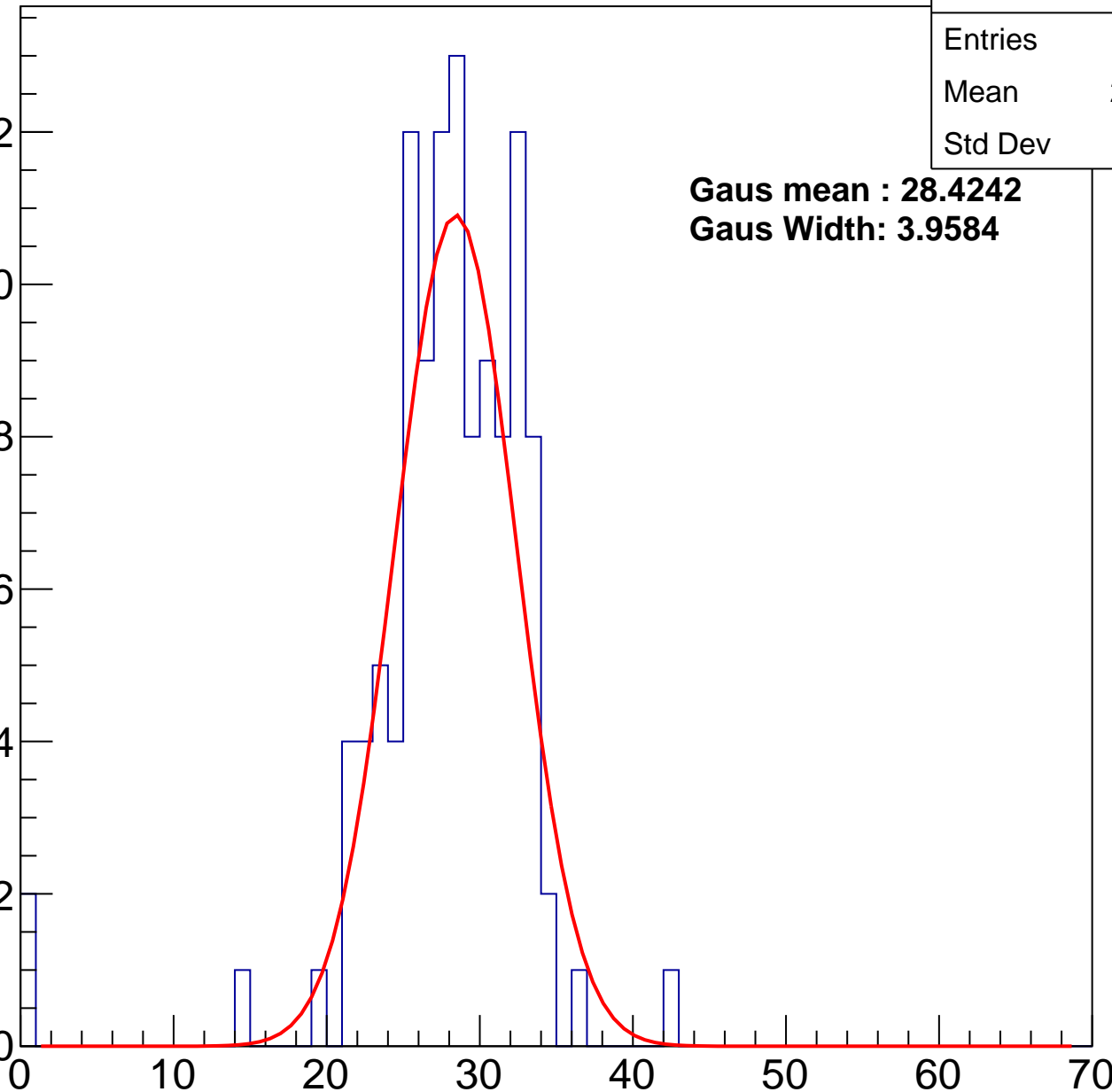
Gaus mean : 28.4242

Gaus Width: 3.9584

Entry

12
10
8
6
4
2
0

ampl



B0L101S, U25-ch106, adc1

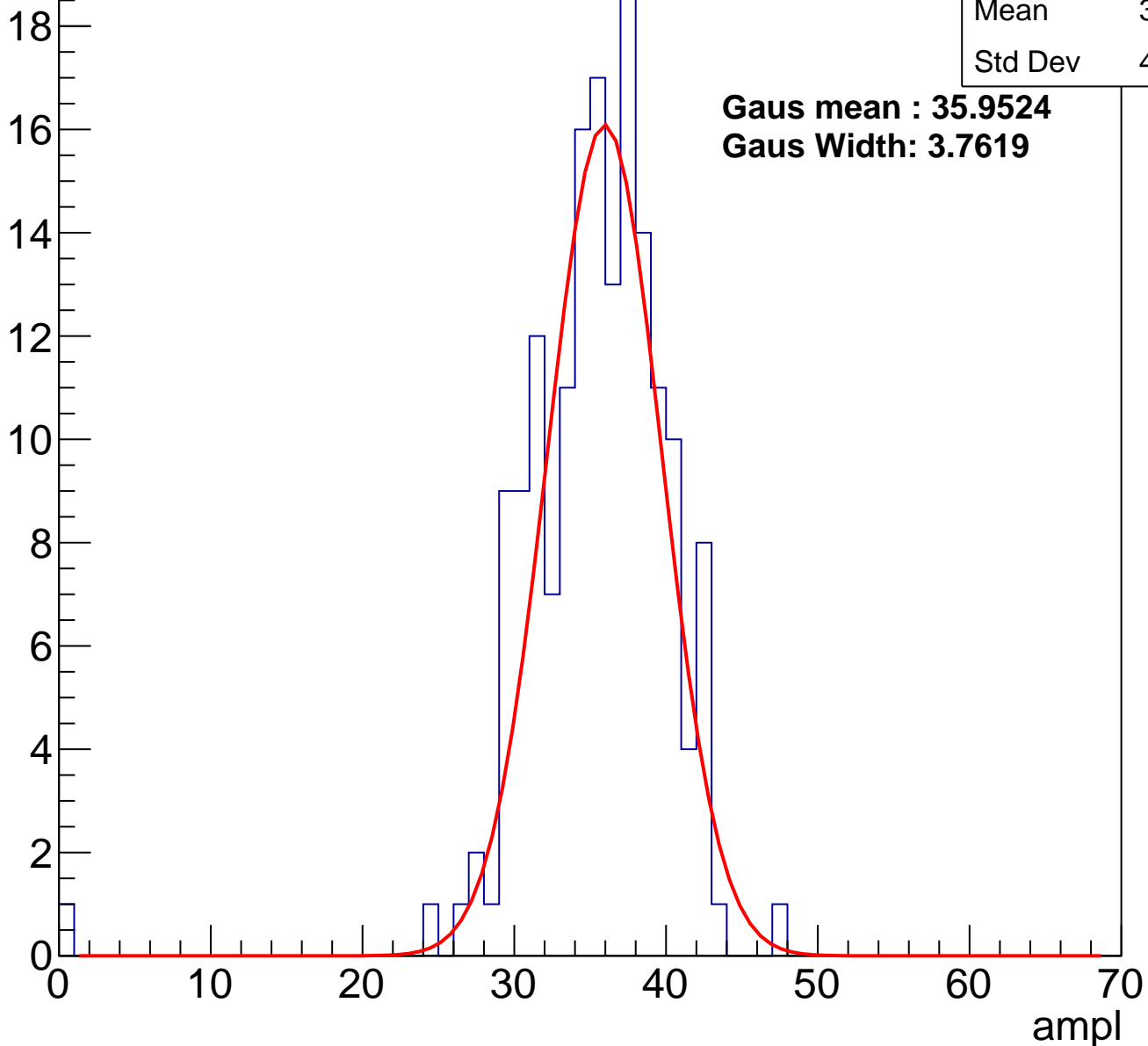
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	35.02
Std Dev	4.789

Gaus mean : 35.9524

Gaus Width: 3.7619

Entry



B0L101S, U25-ch106, adc2

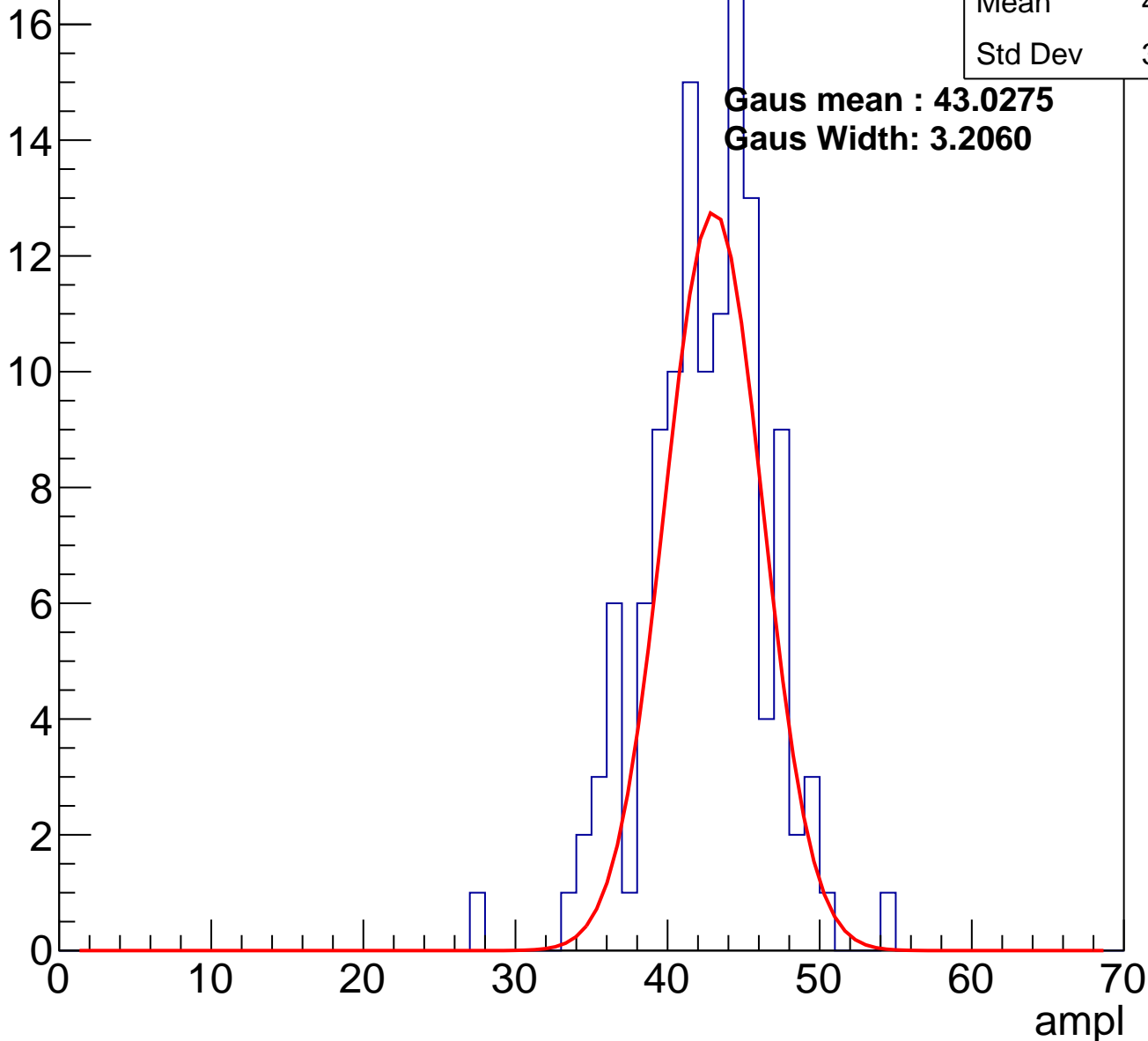
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	125
Mean	42.08
Std Dev	3.965

Gaus mean : 43.0275

Gaus Width: 3.2060



B0L101S, U25-ch106, adc3

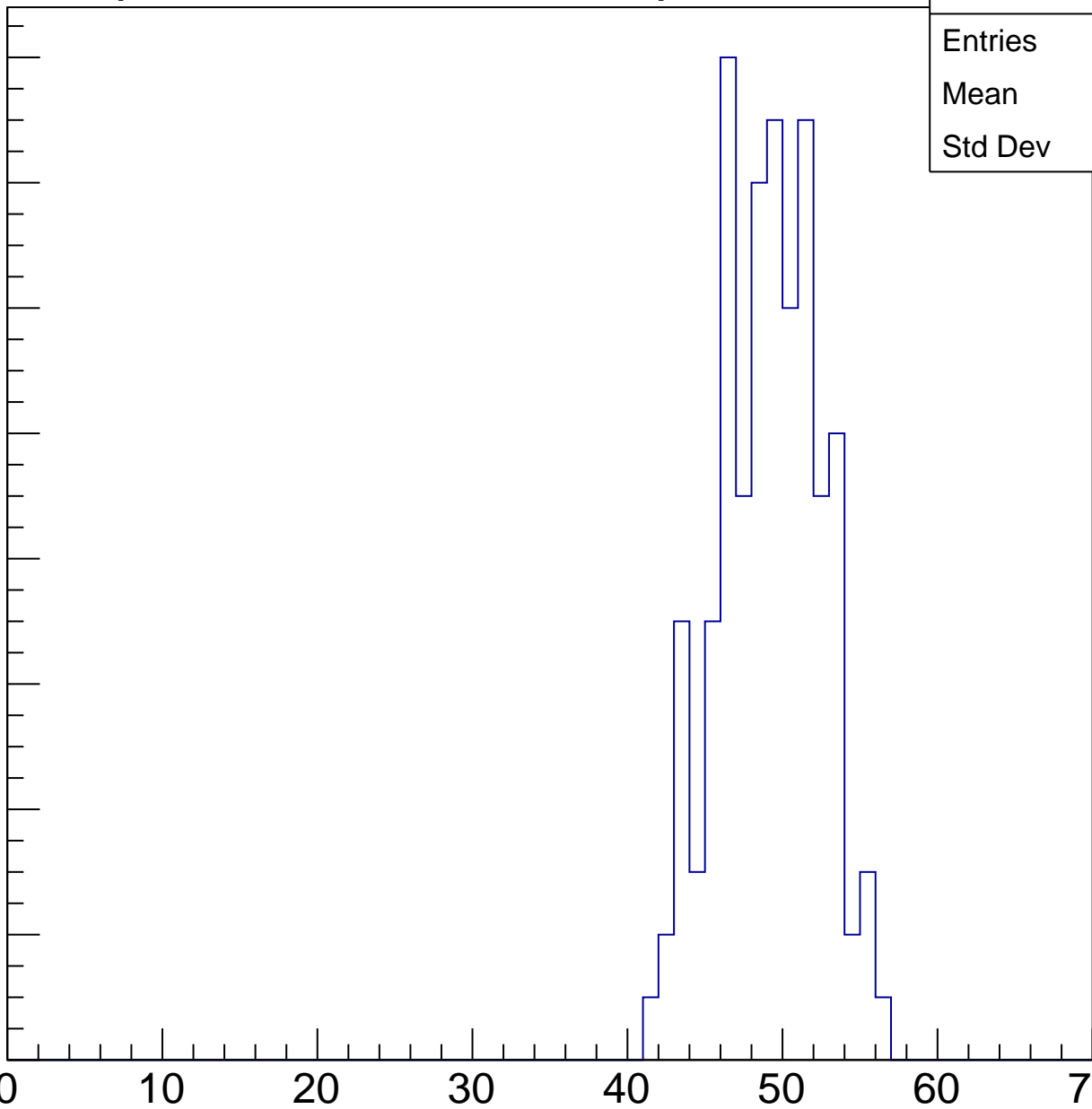
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	48.66
Std Dev	3.212

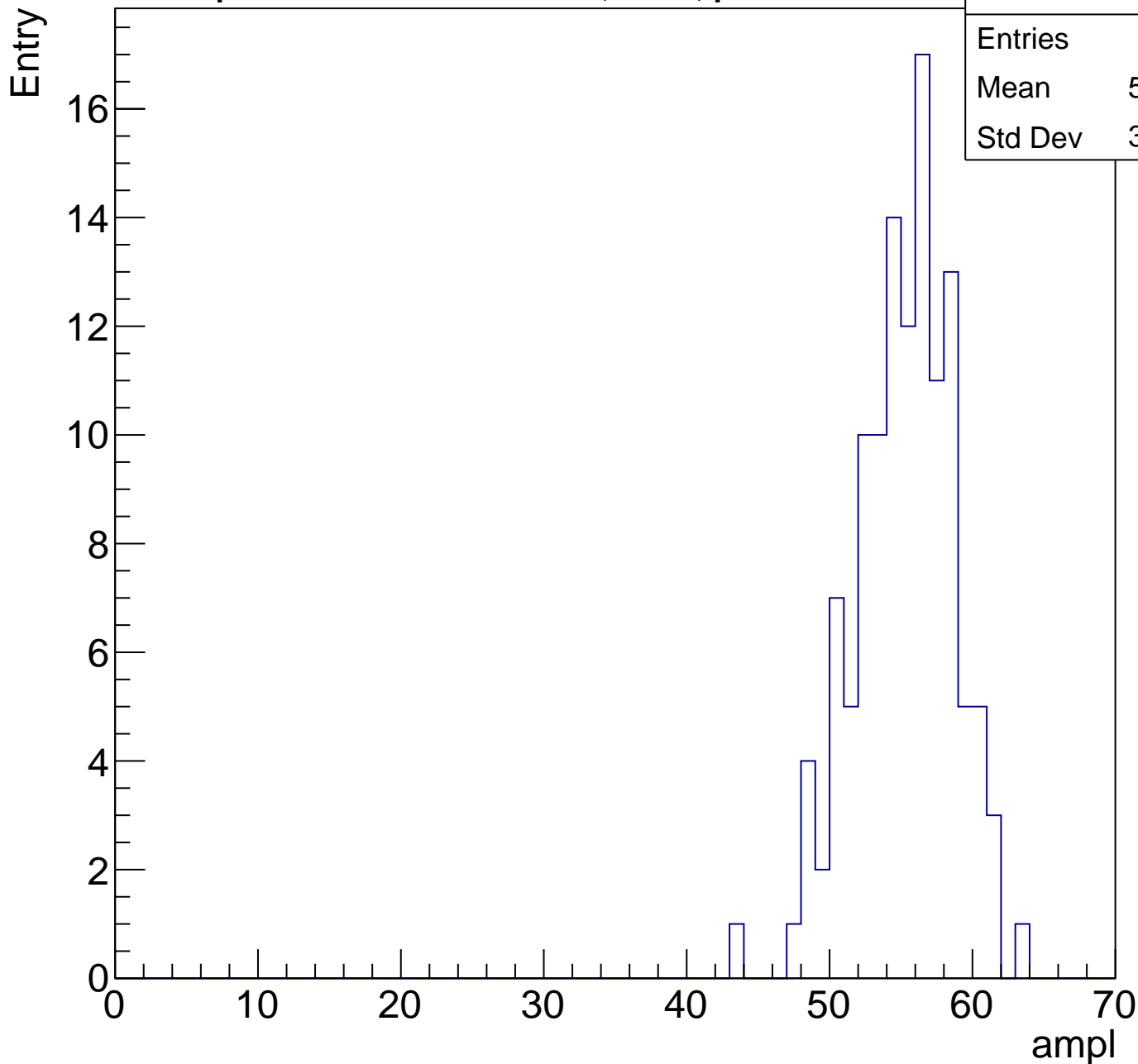
ampl



B0L101S, U25-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	54.75
Std Dev	3.429

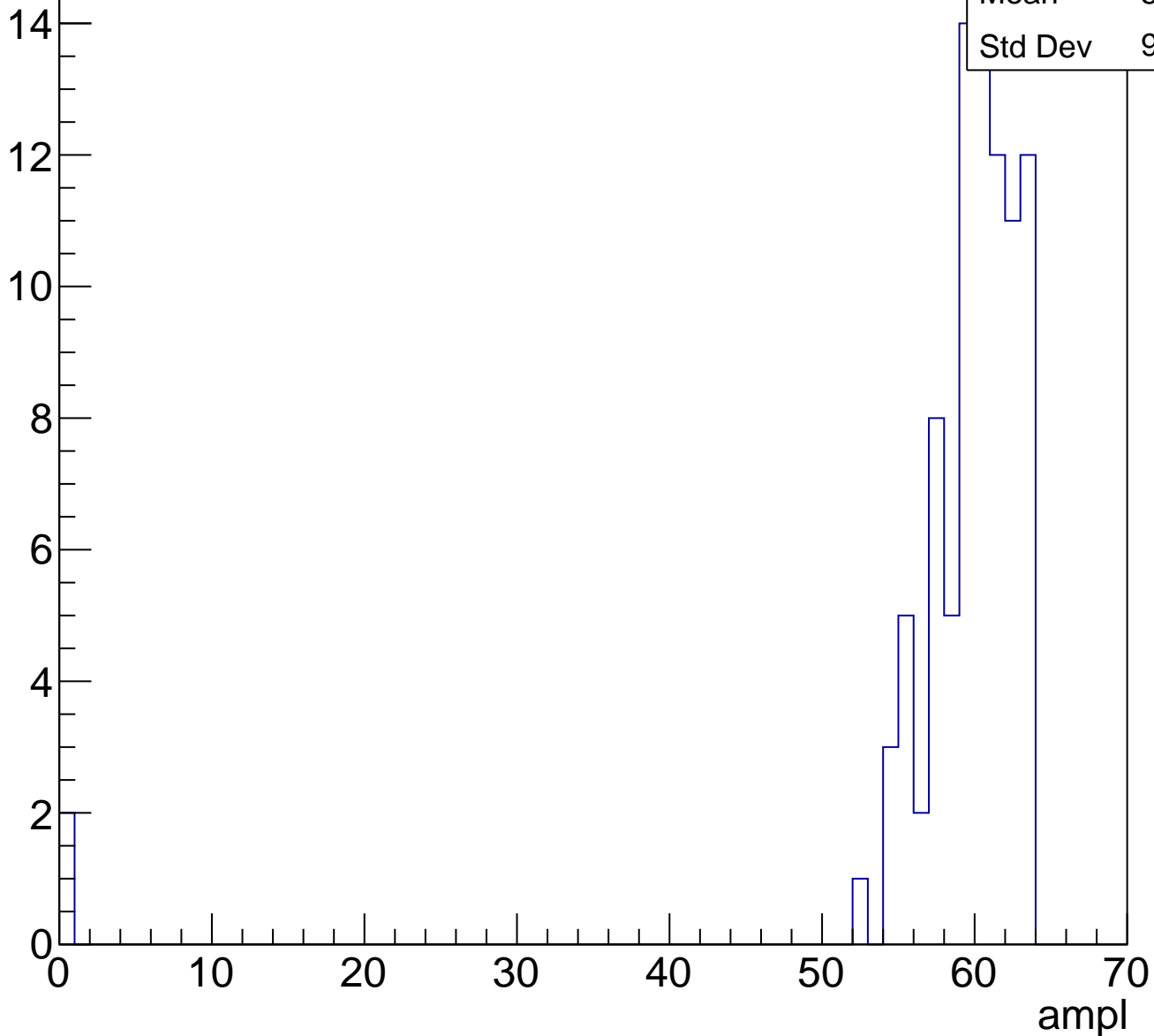


B0L101S, U25-ch106, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	90
Mean	58.26
Std Dev	9.144

Entry

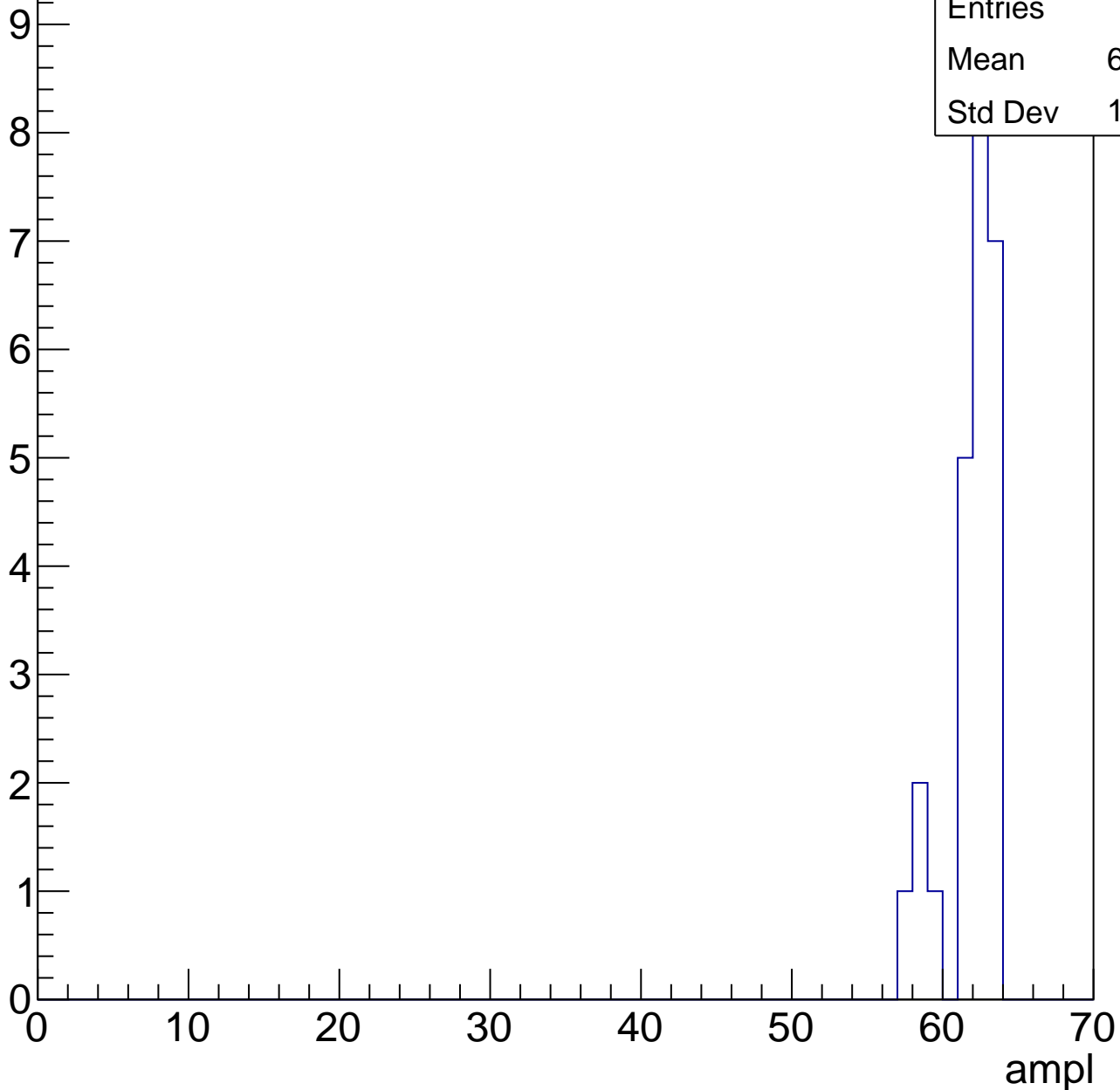


B0L101S, U25-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	25
Mean	61.44
Std Dev	1.675



B0L101S, U25-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch107, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	29.61
Std Dev	3.699

Gaus mean : 30.1422

Gaus Width: 3.6699

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

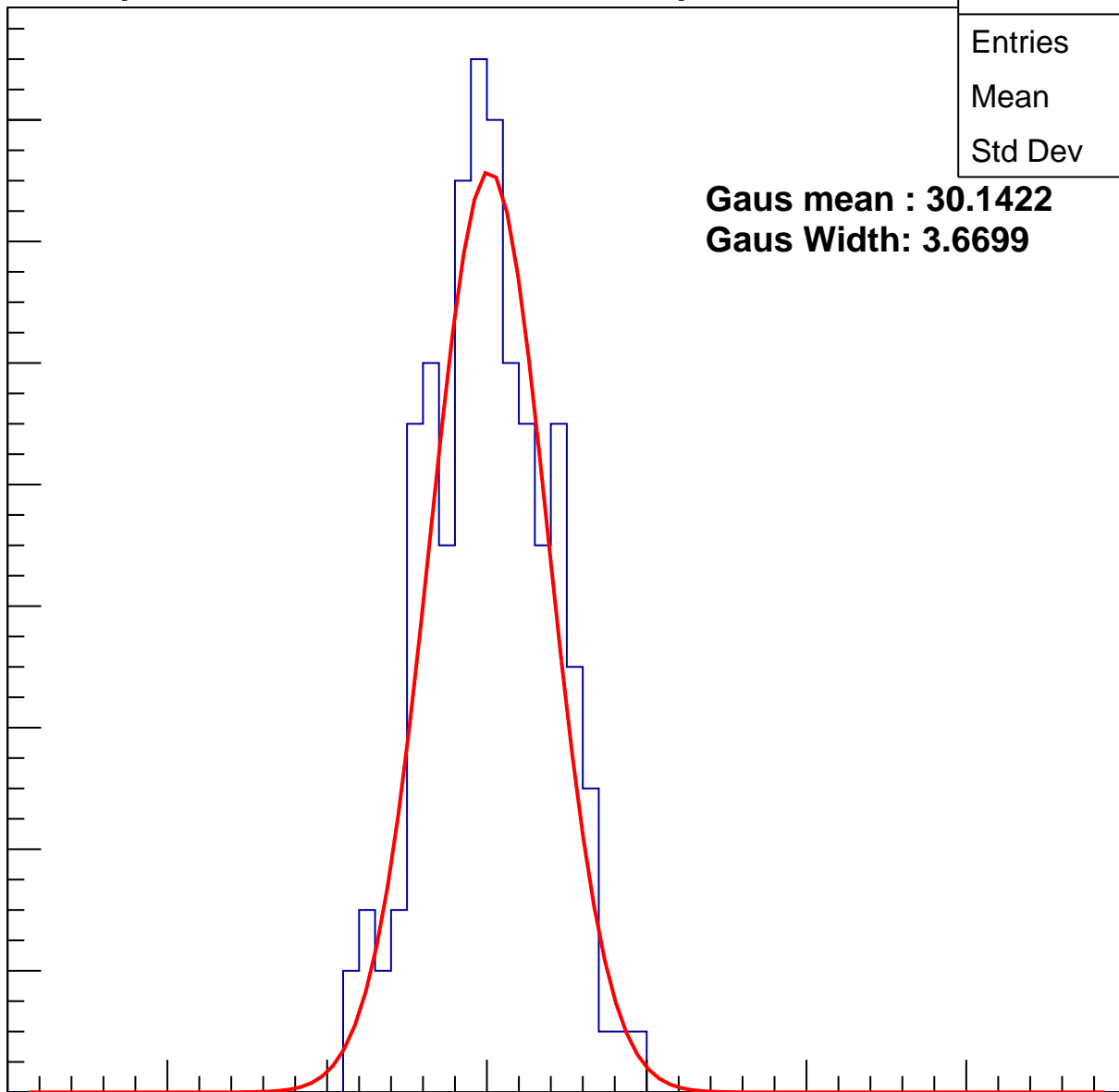
40

50

60

70

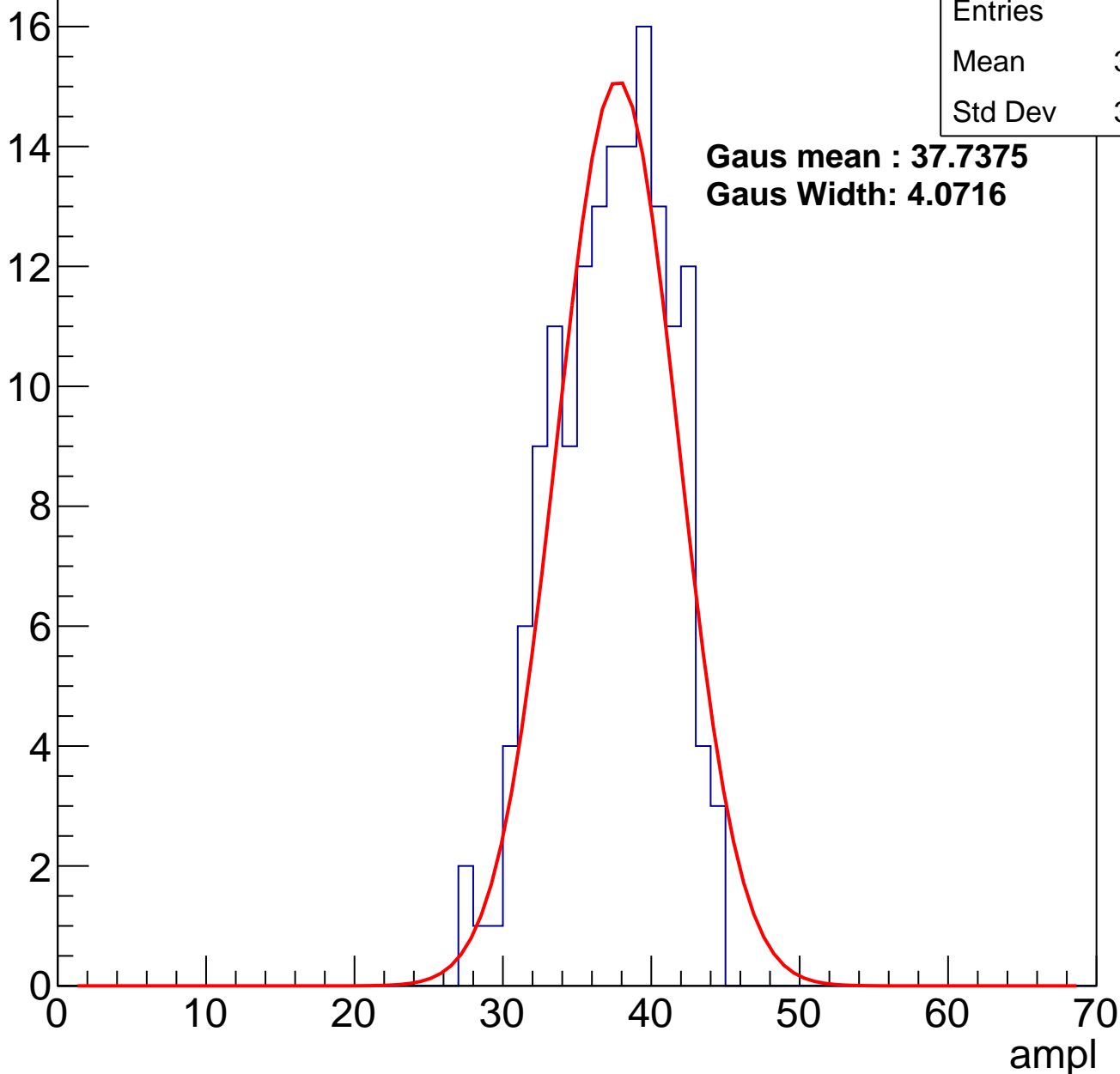
ampl



B0L101S, U25-ch107, adc1

calib_packv5_042523_0143.root, FC#1, port C1

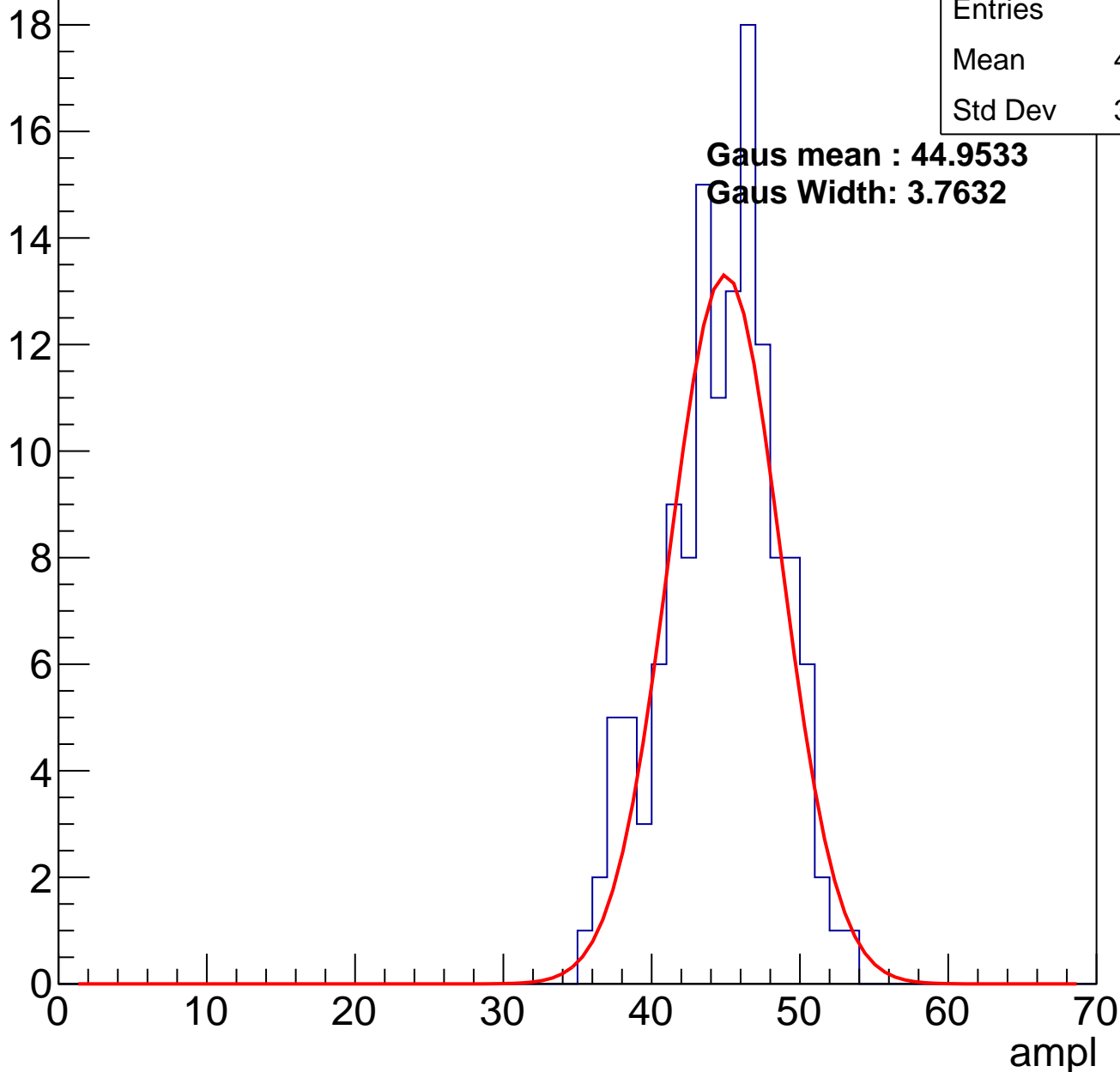
Entry



B0L101S, U25-ch107, adc2

calib_packv5_042523_0143.root, FC#1, port C1

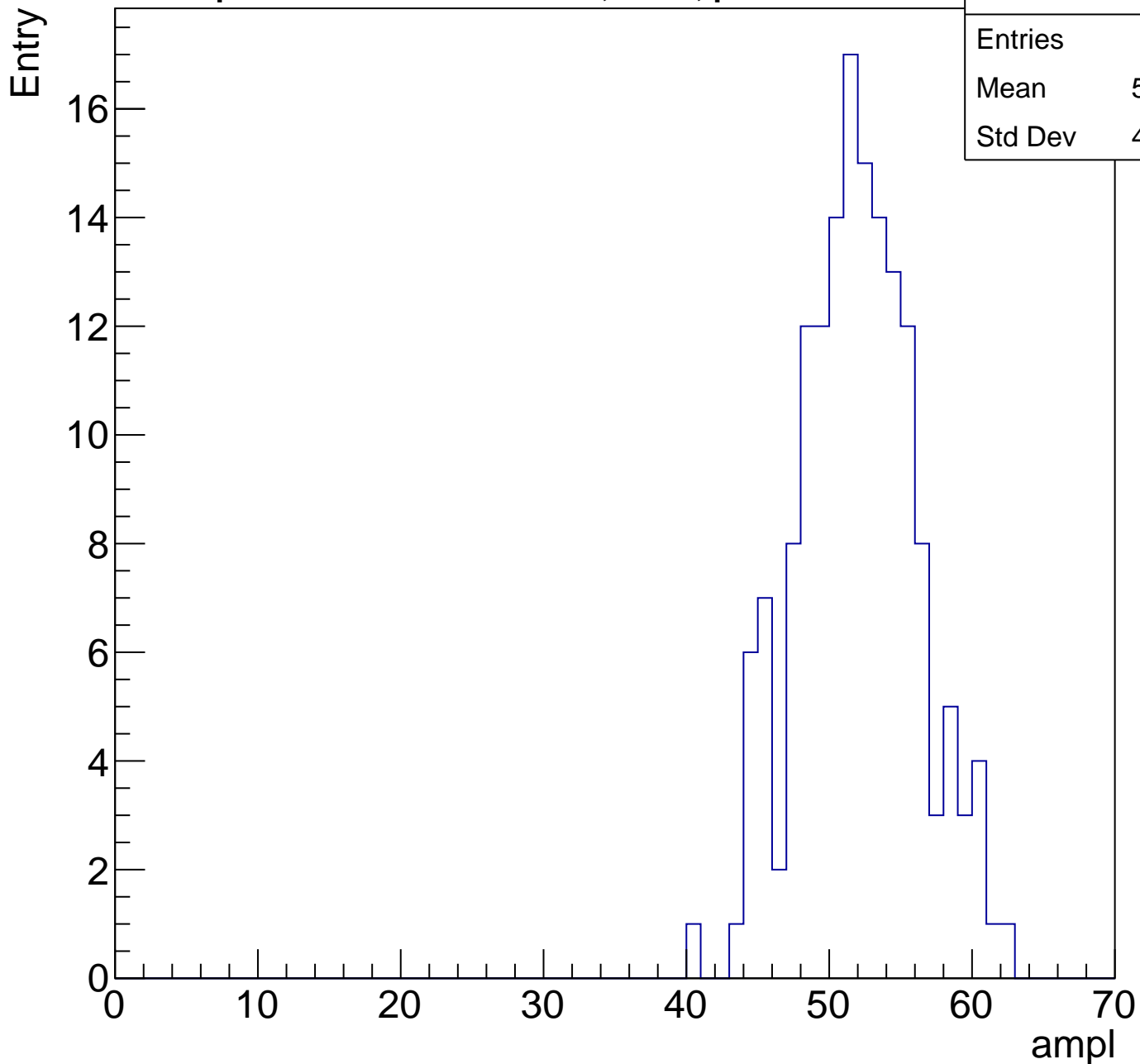
Entry



B0L101S, U25-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

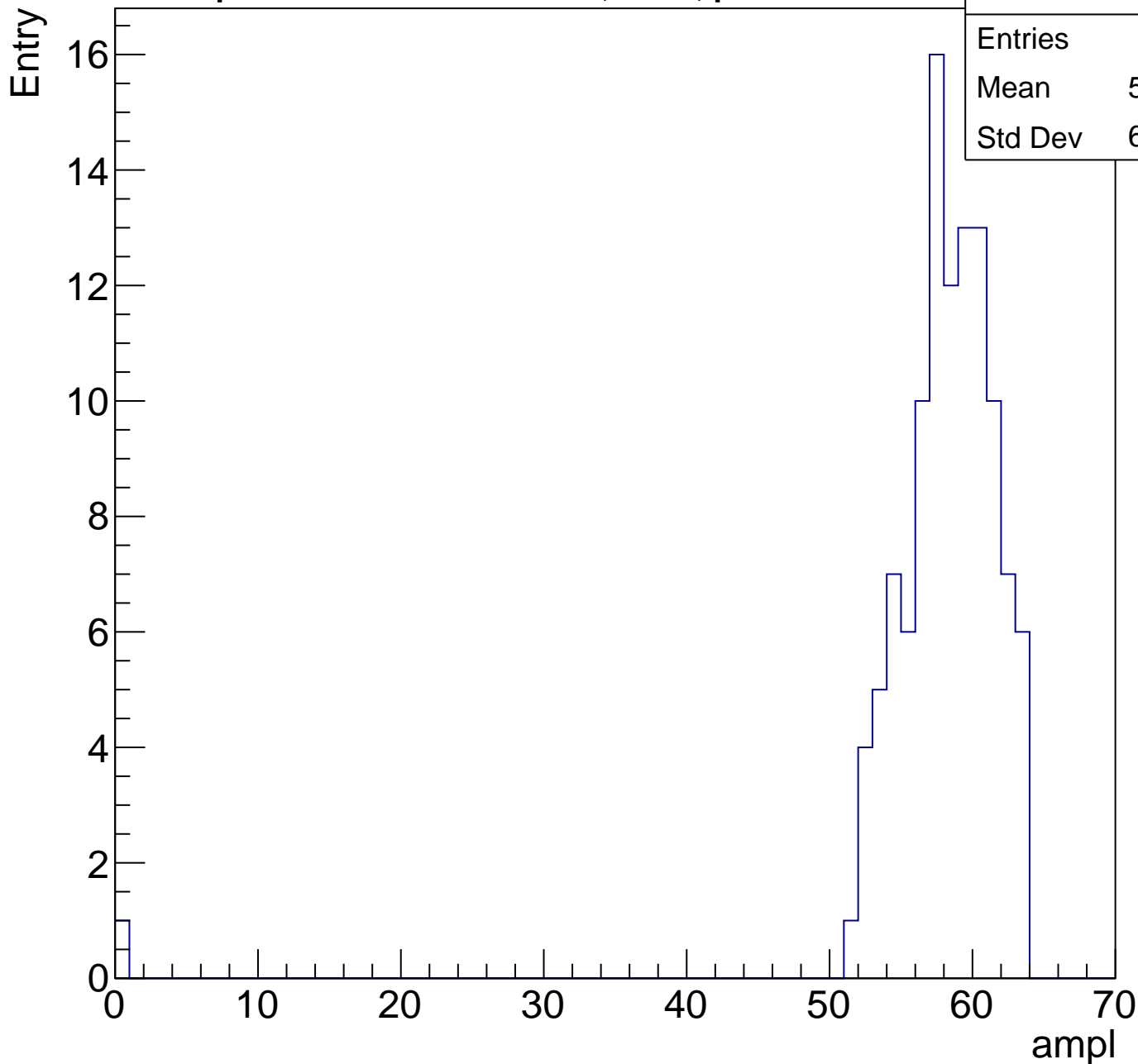
Entries	159
Mean	51.53
Std Dev	4.124



B0L101S, U25-ch107, adc4

calib_packv5_042523_0143.root, FC#1, port C1

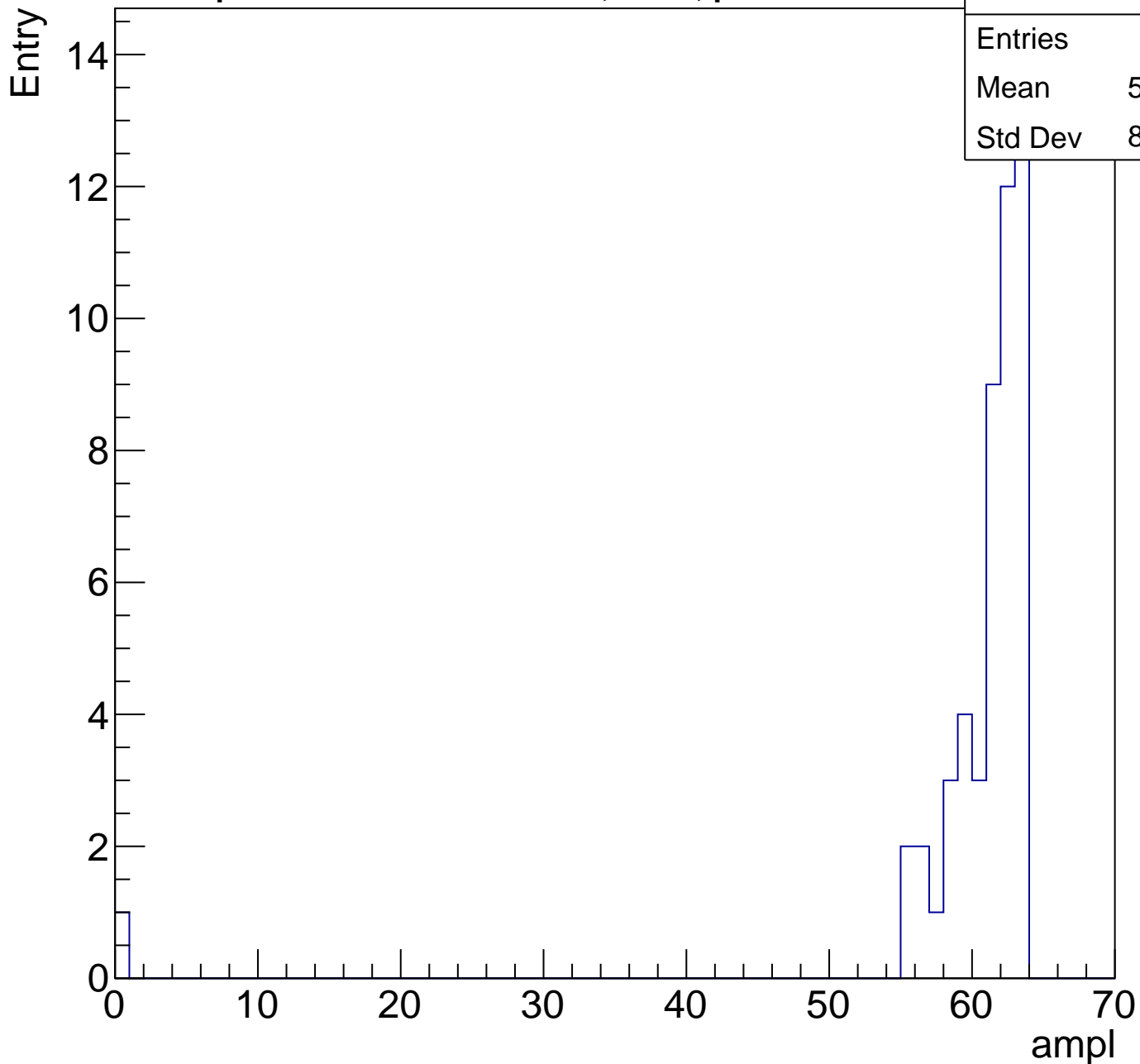
Entries	111
Mean	57.38
Std Dev	6.208



B0L101S, U25-ch107, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	51
Mean	59.69
Std Dev	8.728



B0L101S, U25-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch108, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	29.74
Std Dev	4.772

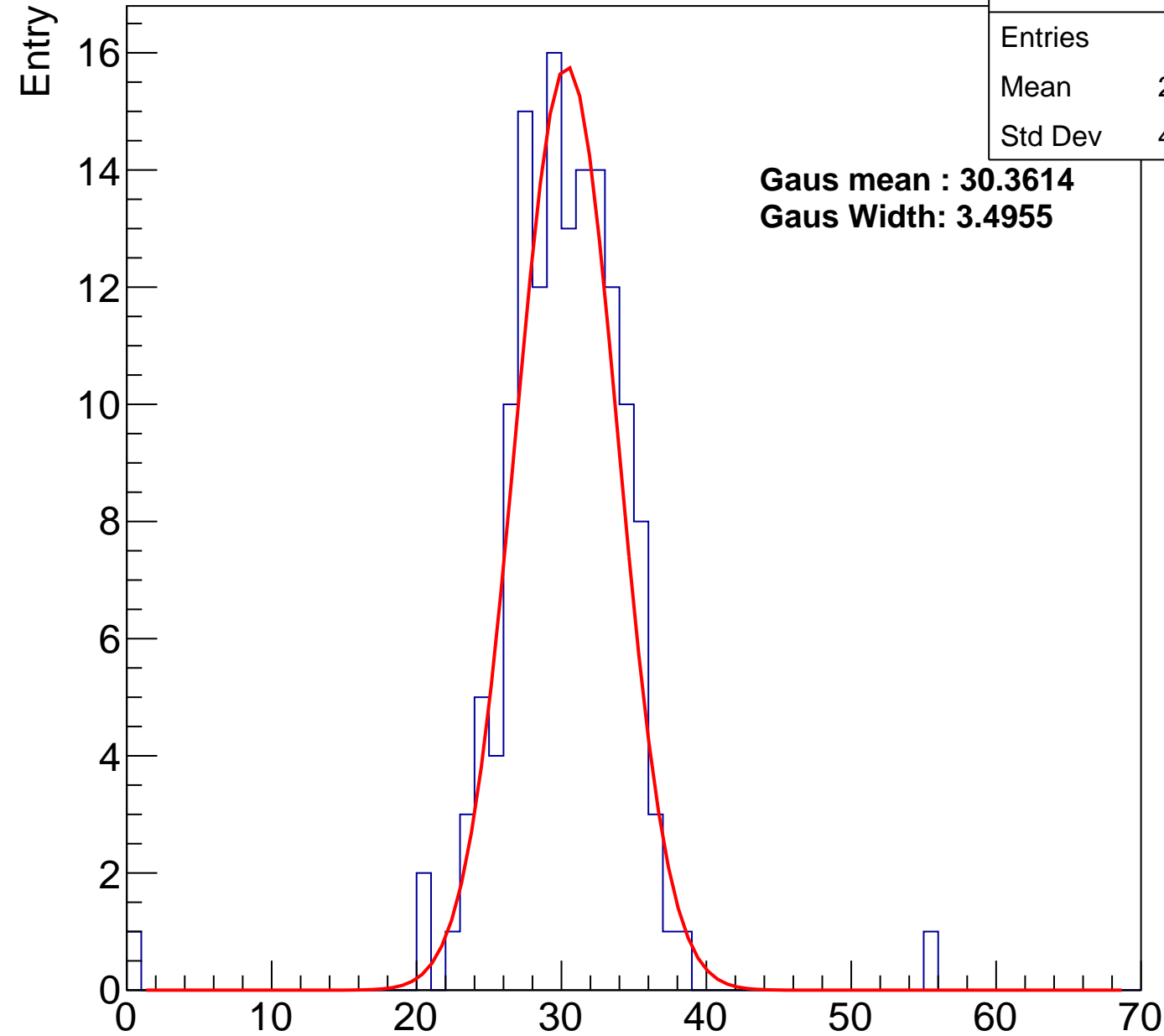
Gaus mean : 30.3614

Gaus Width: 3.4955

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch108, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	141
Mean	37.11
Std Dev	3.874

Gaus mean : 37.5814

Gaus Width: 3.9503

0

10

20

30

40

50

60

70

ampl

B0L101S, U25-ch108, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

118

Mean

43.15

Std Dev

3.5

Gaus mean : 43.8882

Gaus Width: 3.7312

0

10

20

30

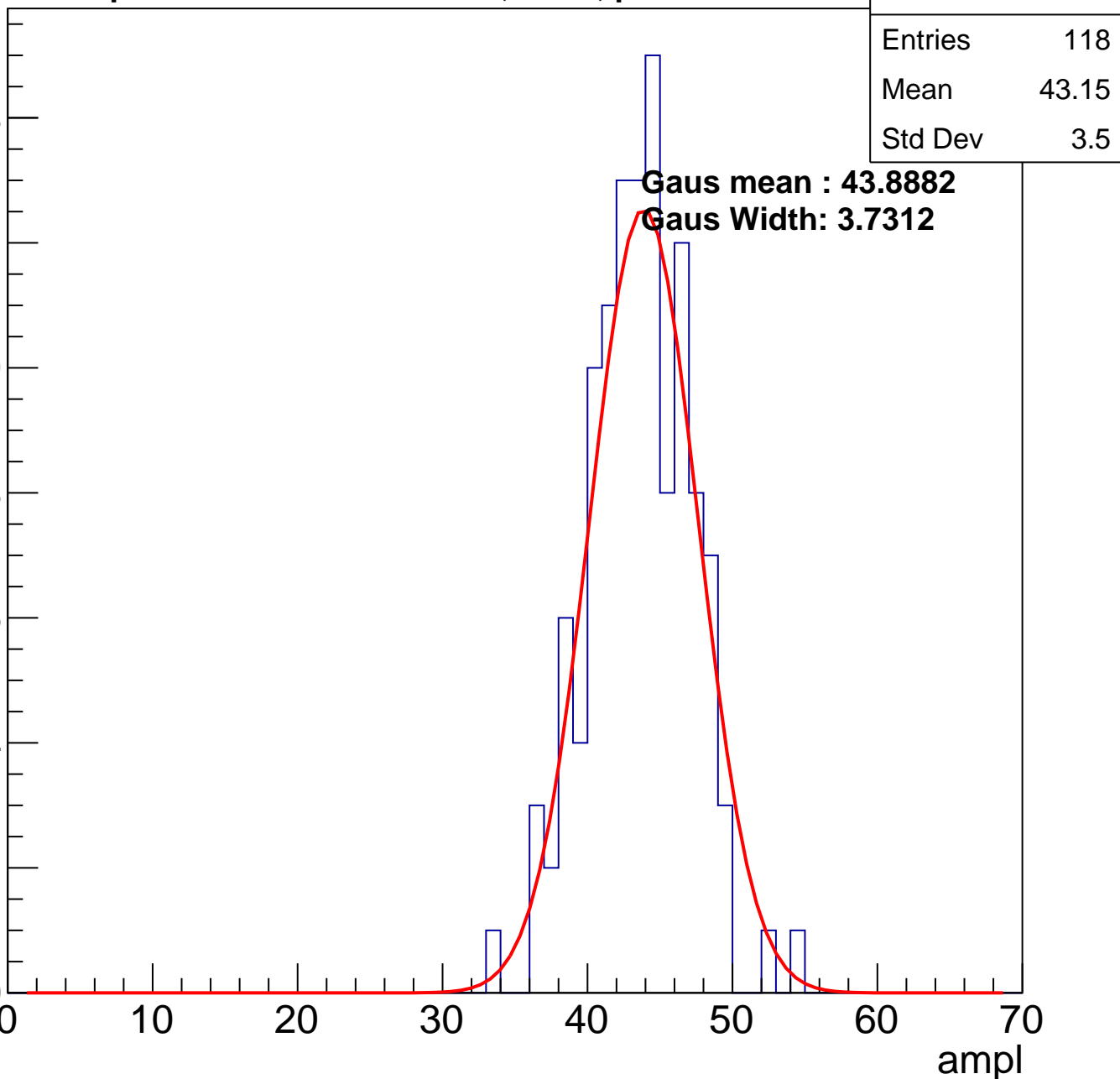
40

50

60

70

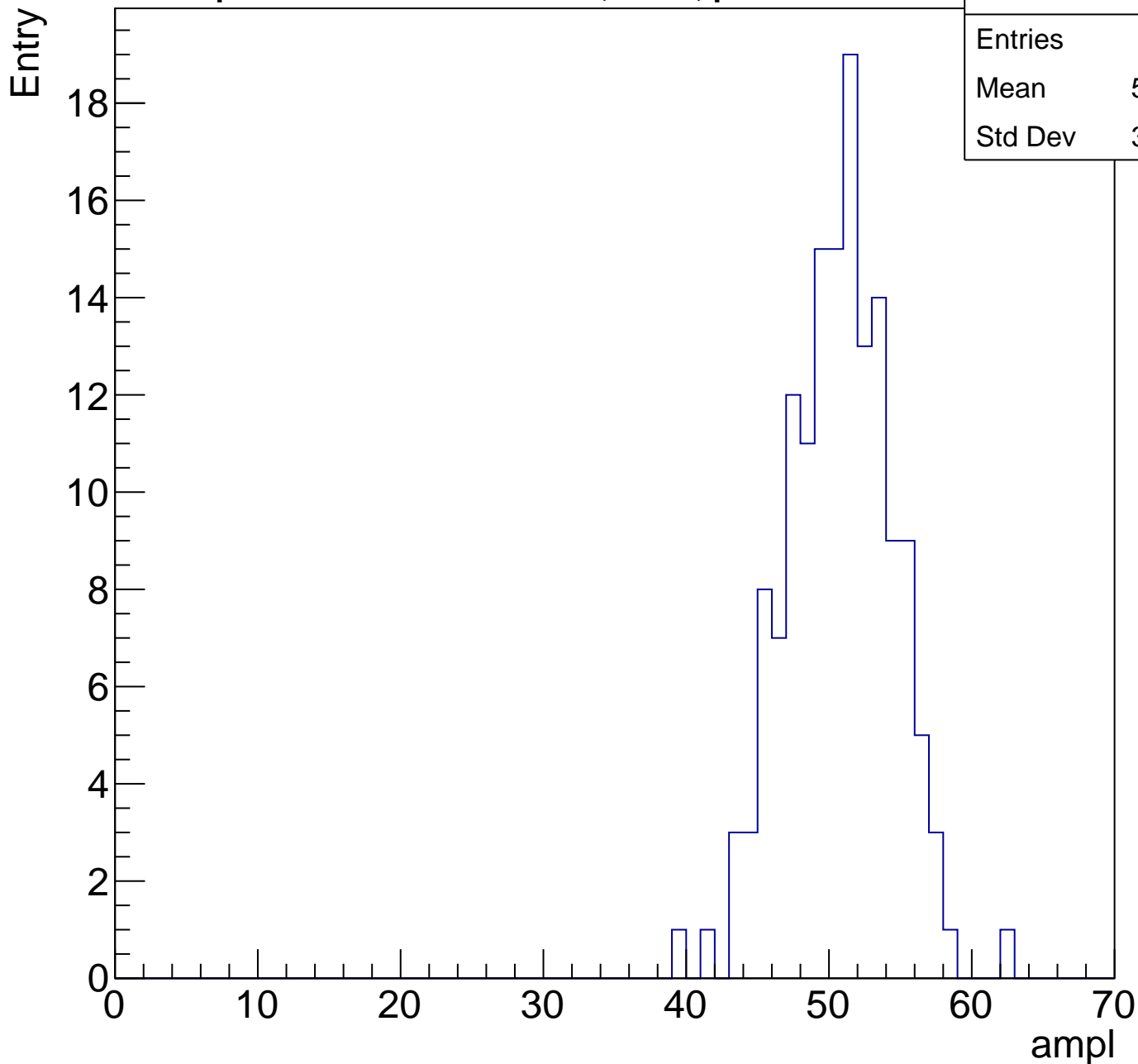
ampl



B0L101S, U25-ch108, adc3

calib_packv5_042523_0143.root, FC#1, port C1

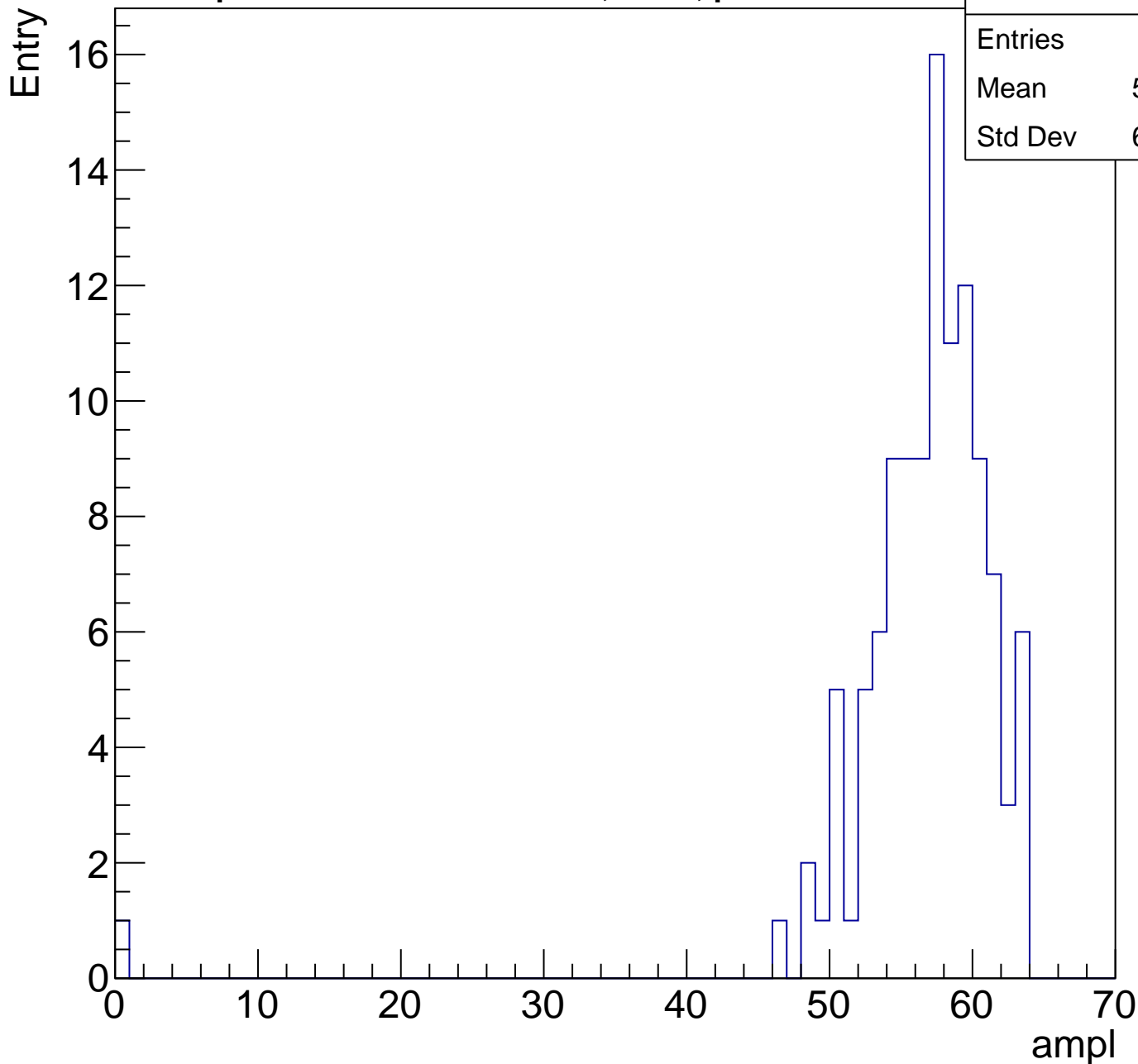
Entries	150
Mean	50.26
Std Dev	3.676



B0L101S, U25-ch108, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	56.14
Std Dev	6.438



B0L101S, U25-ch108, adc5

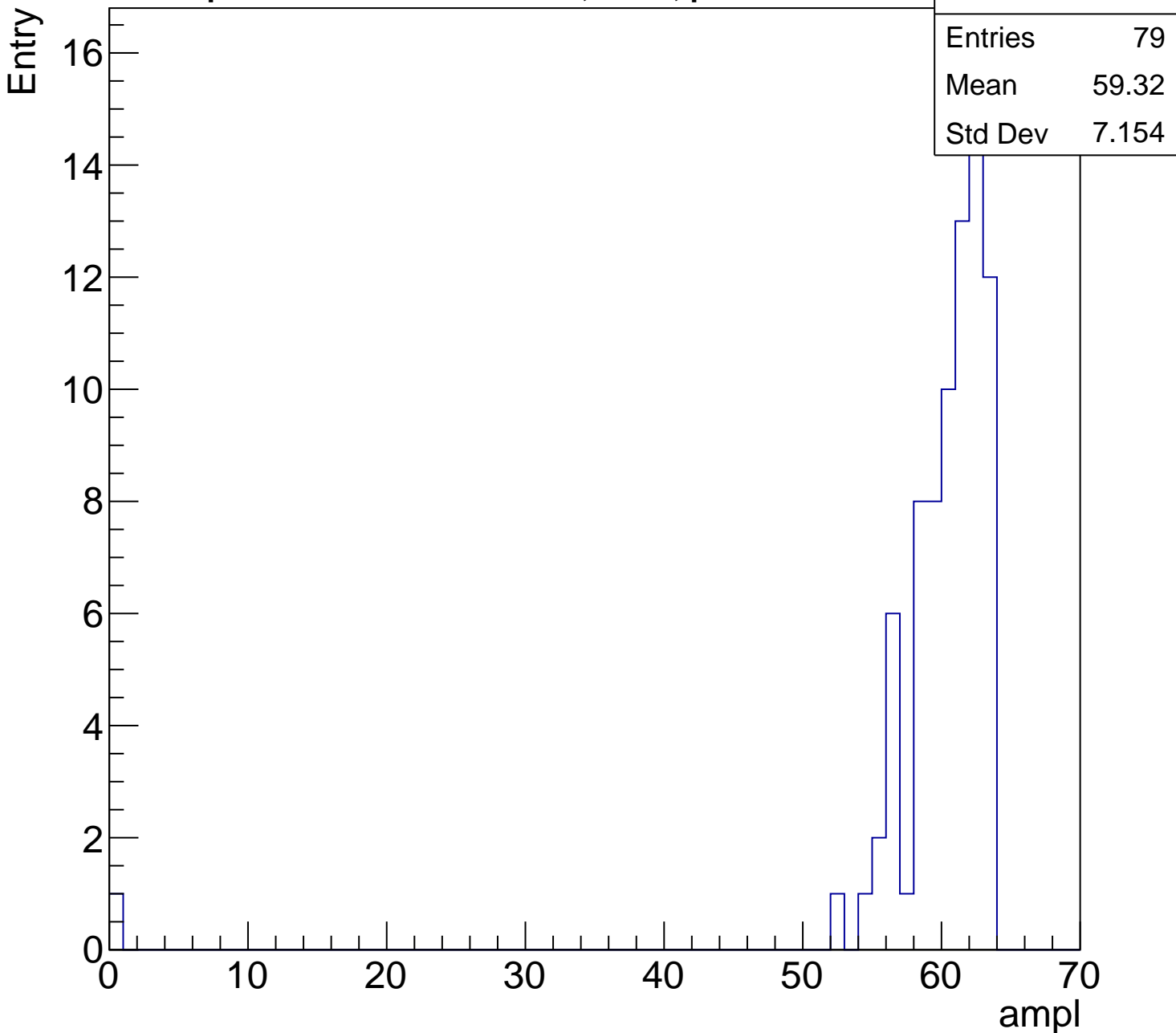
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.32
Std Dev	7.154

ampl



B0L101S, U25-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch109, adc0

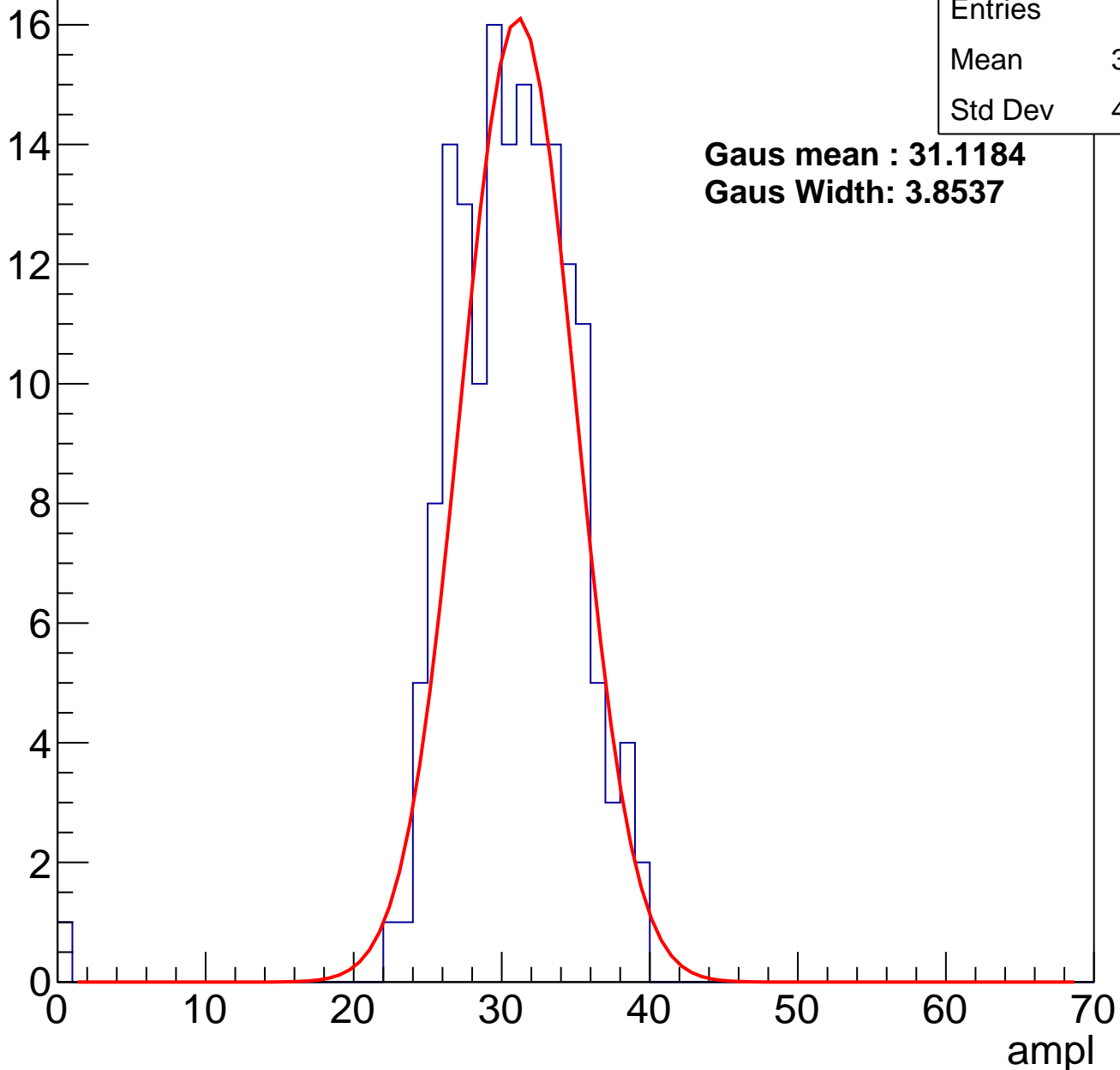
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	30.26
Std Dev	4.422

Gaus mean : 31.1184

Gaus Width: 3.8537

Entry



B0L101S, U25-ch109, adc1

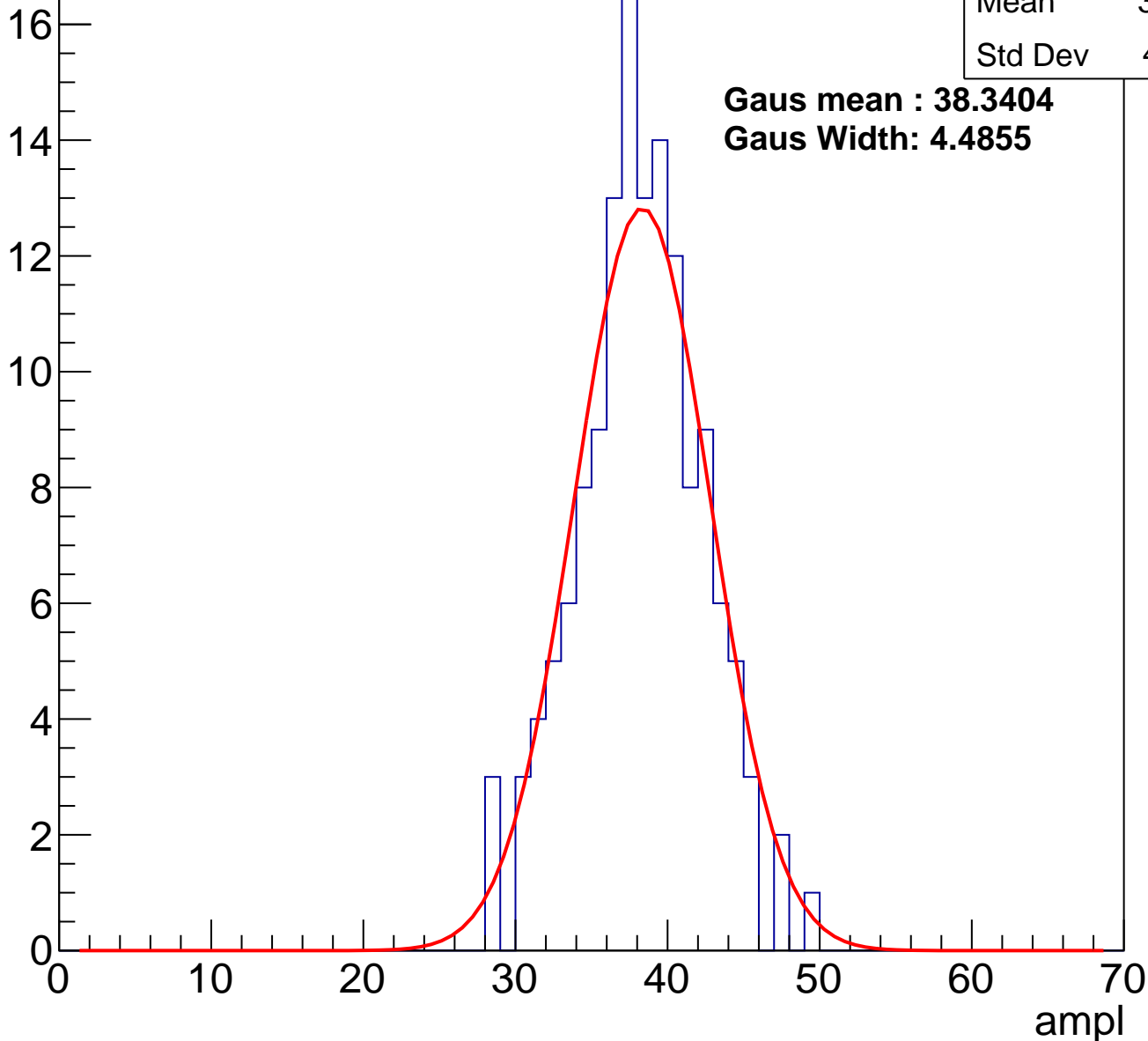
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	141
Mean	37.74
Std Dev	4.061

Gaus mean : 38.3404

Gaus Width: 4.4855



B0L101S, U25-ch109, adc2

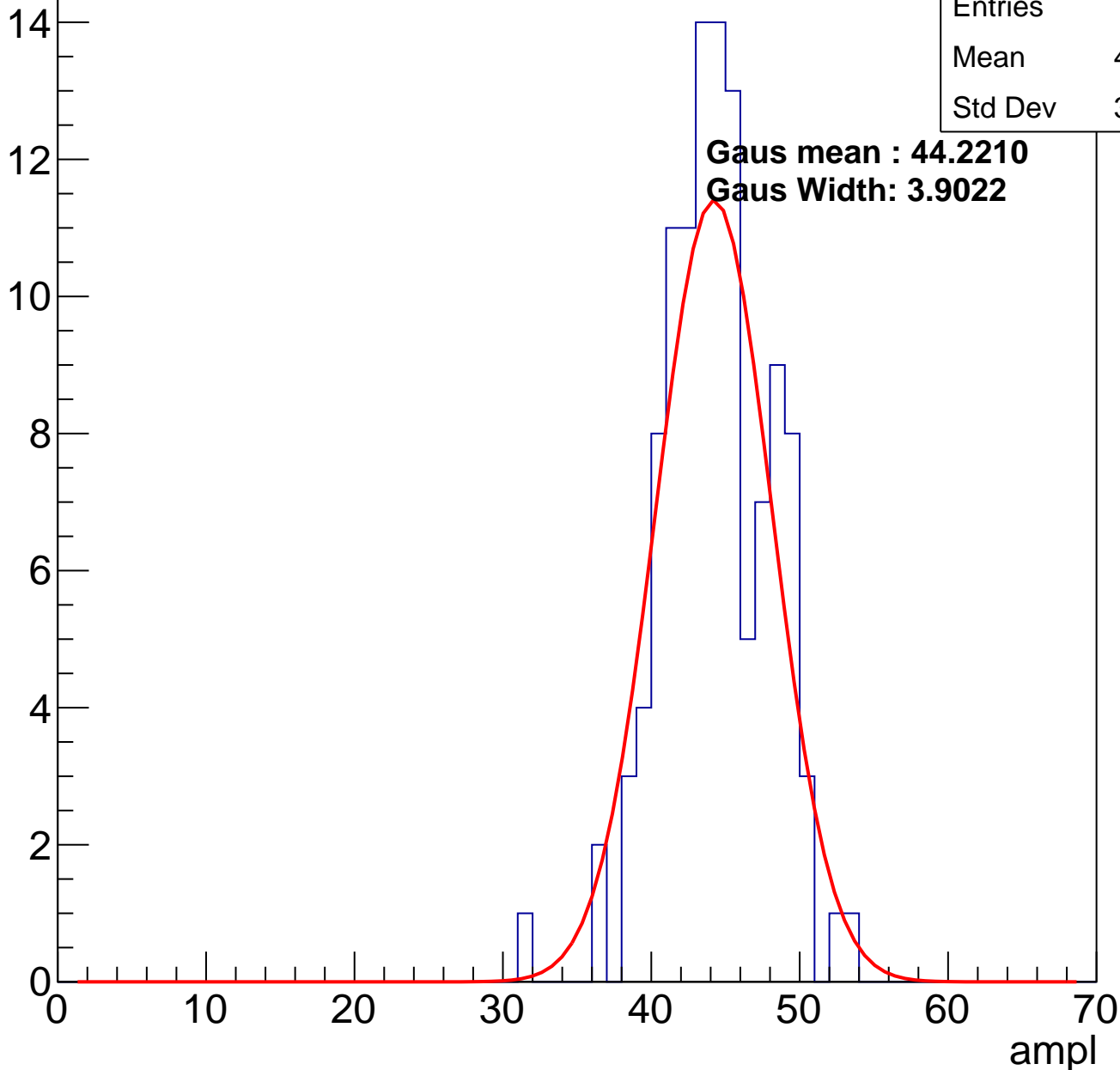
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	115
Mean	43.89
Std Dev	3.589

Gaus mean : 44.2210

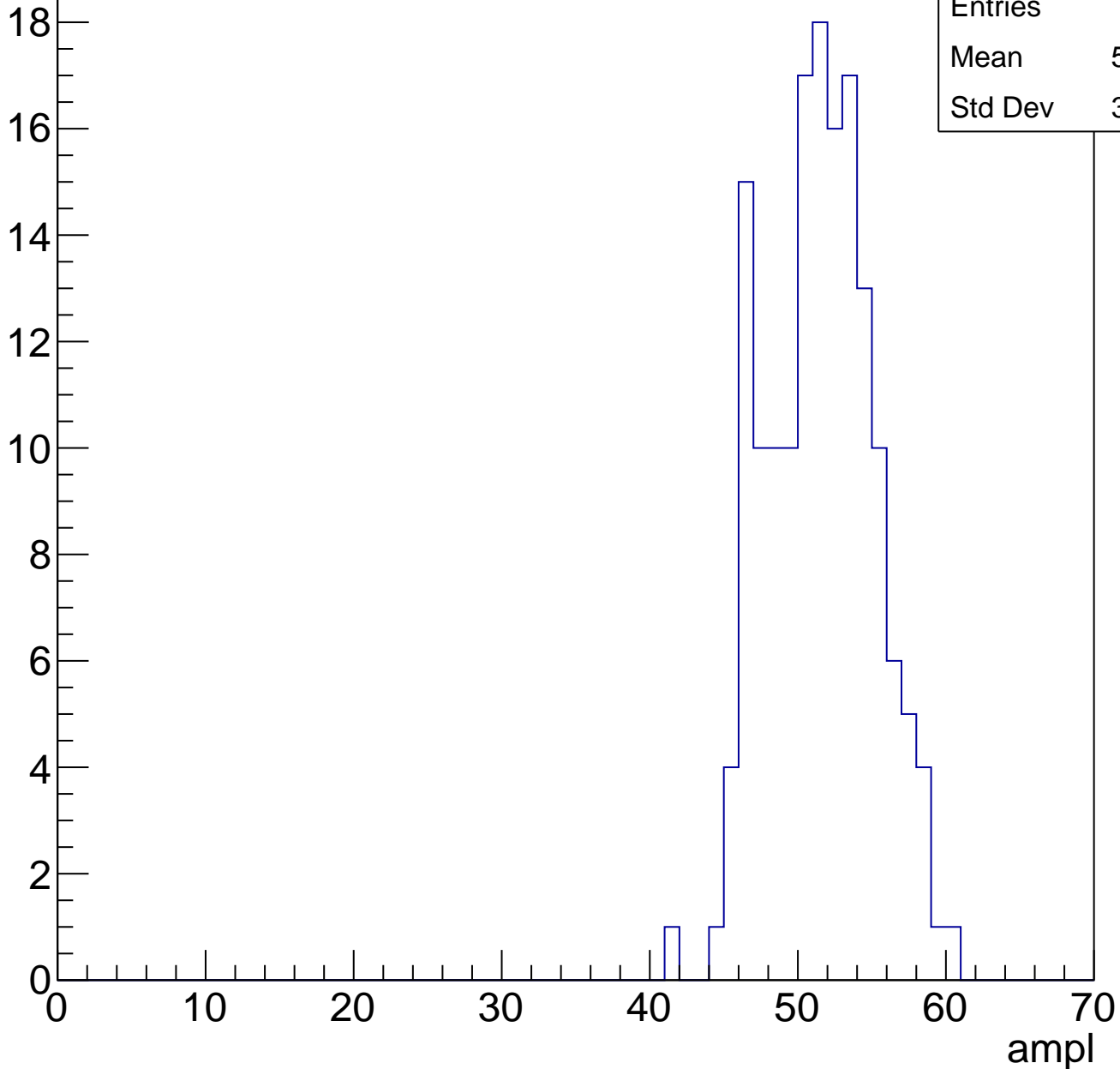
Gaus Width: 3.9022



B0L101S, U25-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	57.25
Std Dev	6.336

Entry

12

10

8

6

4

2

0

0

10

20

30

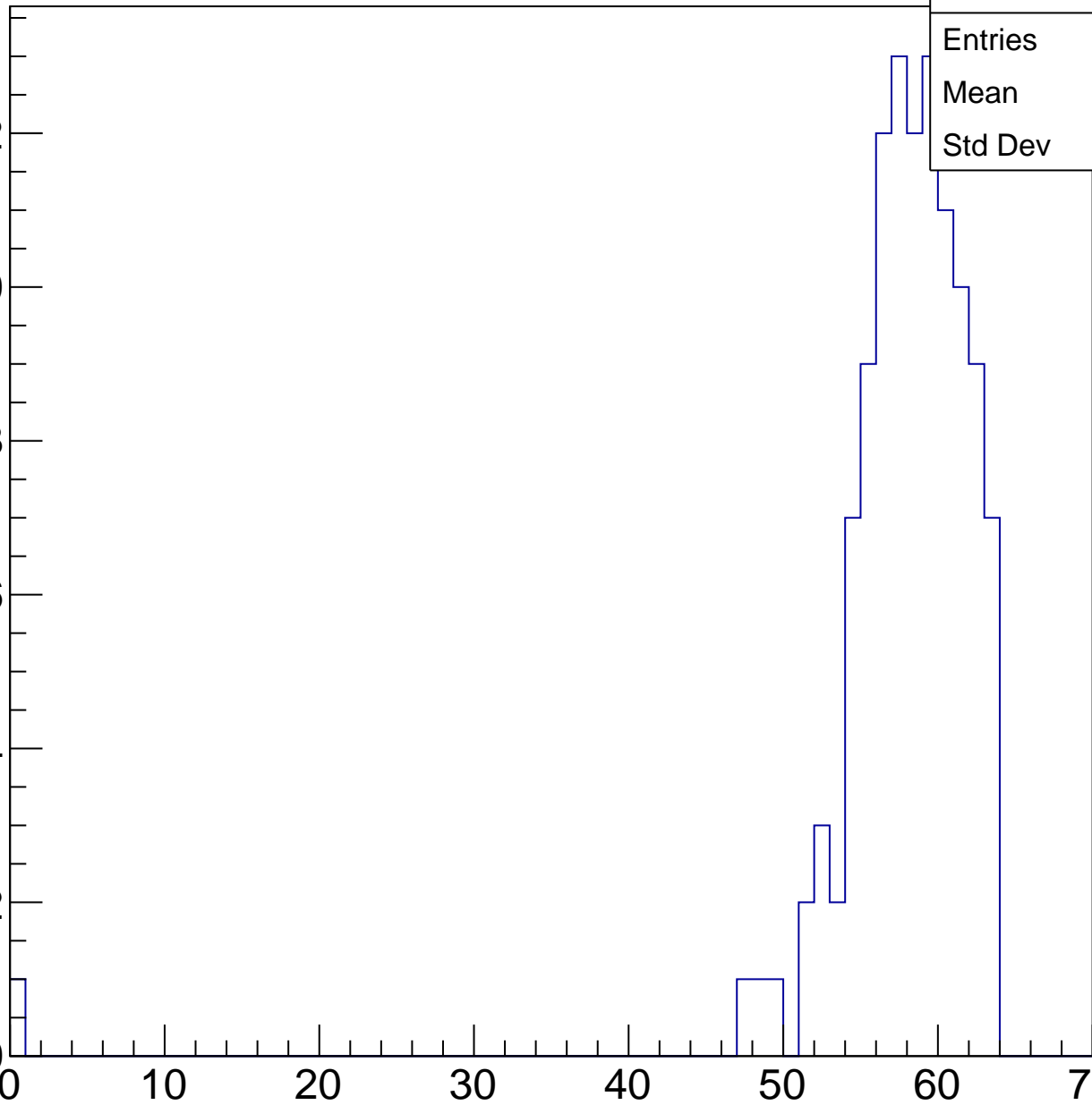
40

50

60

70

ampl



B0L101S, U25-ch109, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	56
Mean	59.57
Std Dev	8.296

ampl

0

10

20

30

40

50

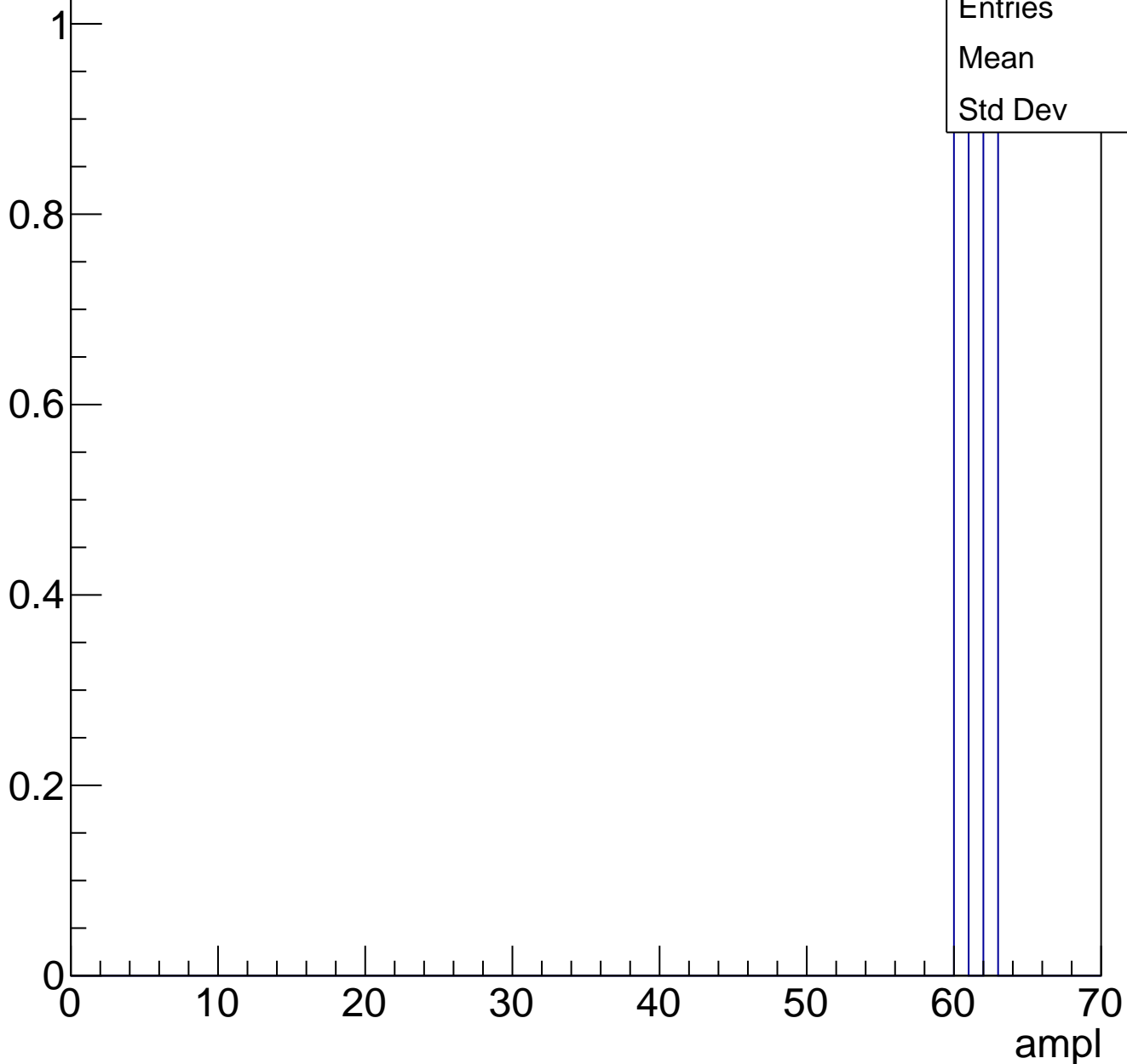
60

70

B0L101S, U25-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch110, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	28.13
Std Dev	5.753

Gaus mean : 28.7094

Gaus Width: 3.8322

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

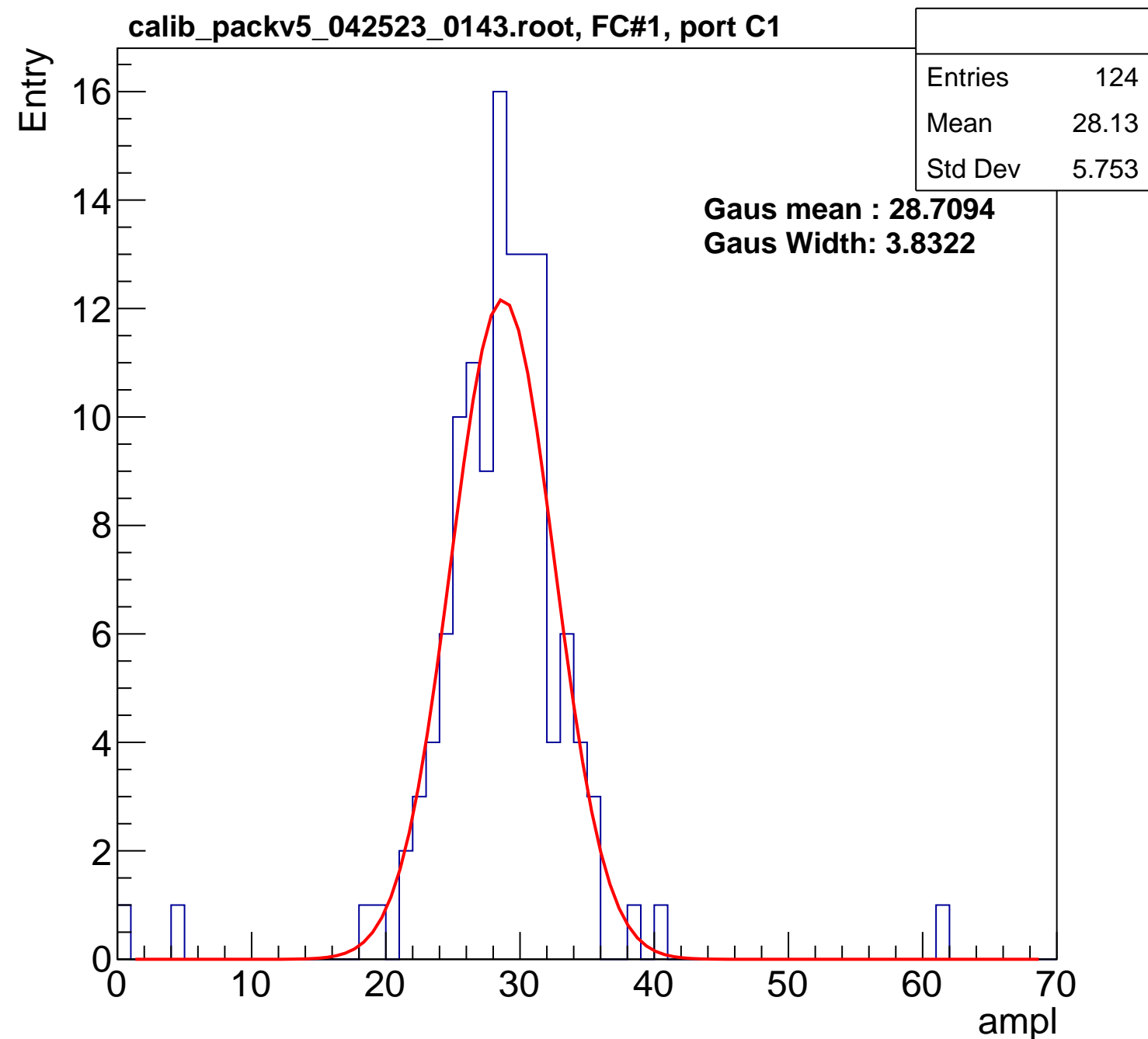
40

50

60

70

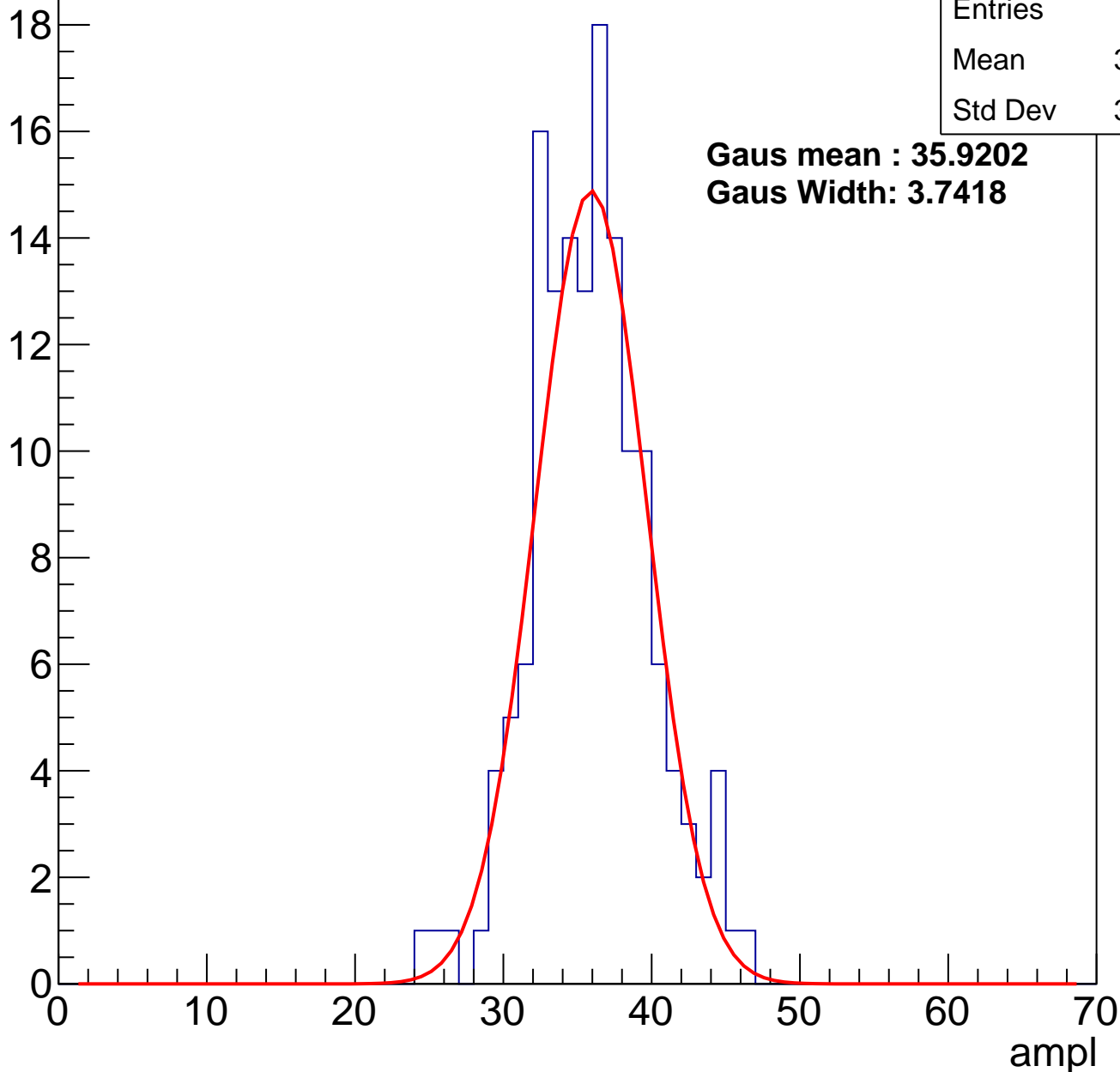
ampl



B0L101S, U25-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch110, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

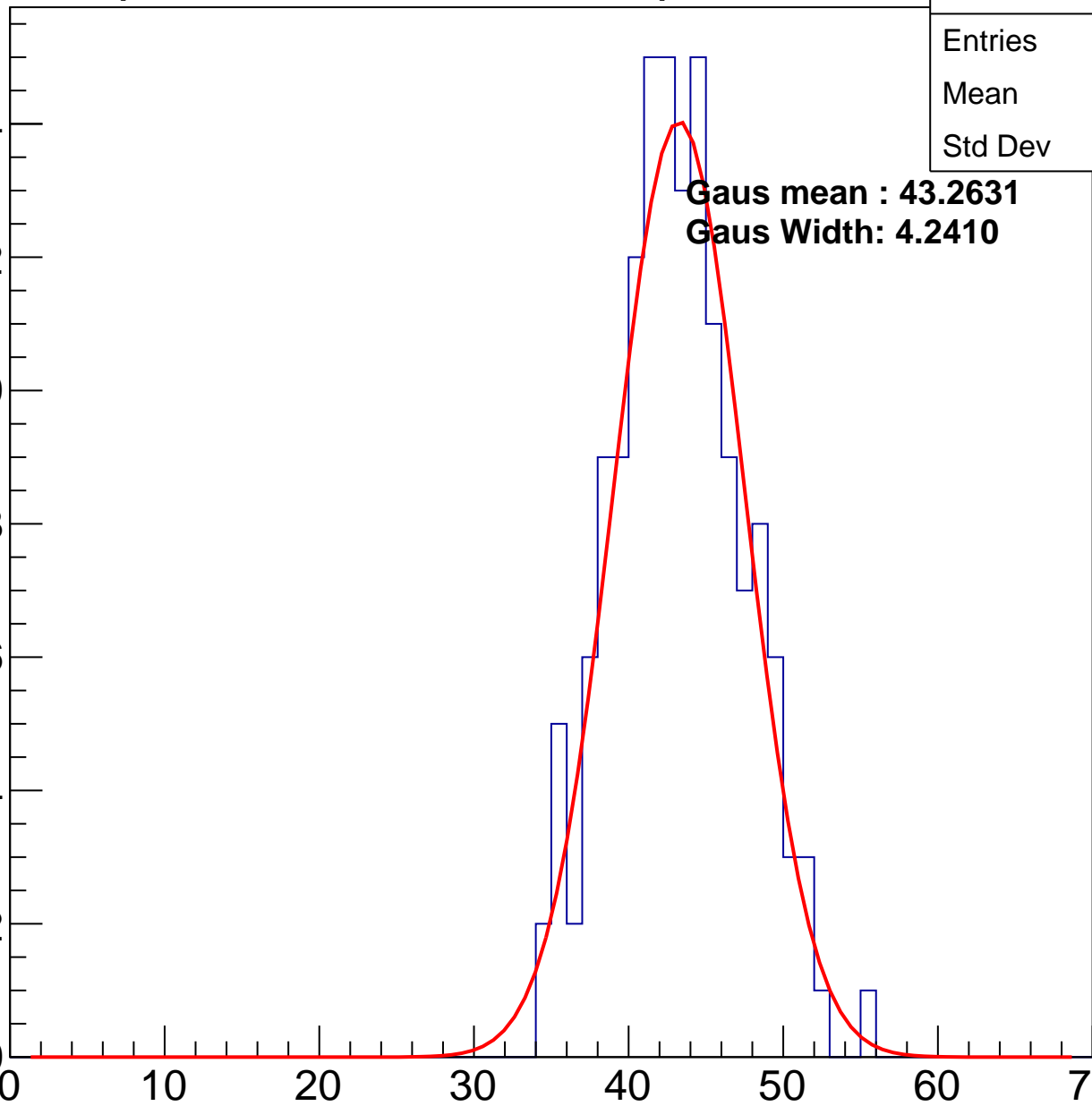
60

ampl

Entries	152
Mean	42.76
Std Dev	4.113

Gaus mean : 43.2631

Gaus Width: 4.2410



B0L101S, U25-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	129
Mean	49.54
Std Dev	3.866

B0L101S, U25-ch110, adc4

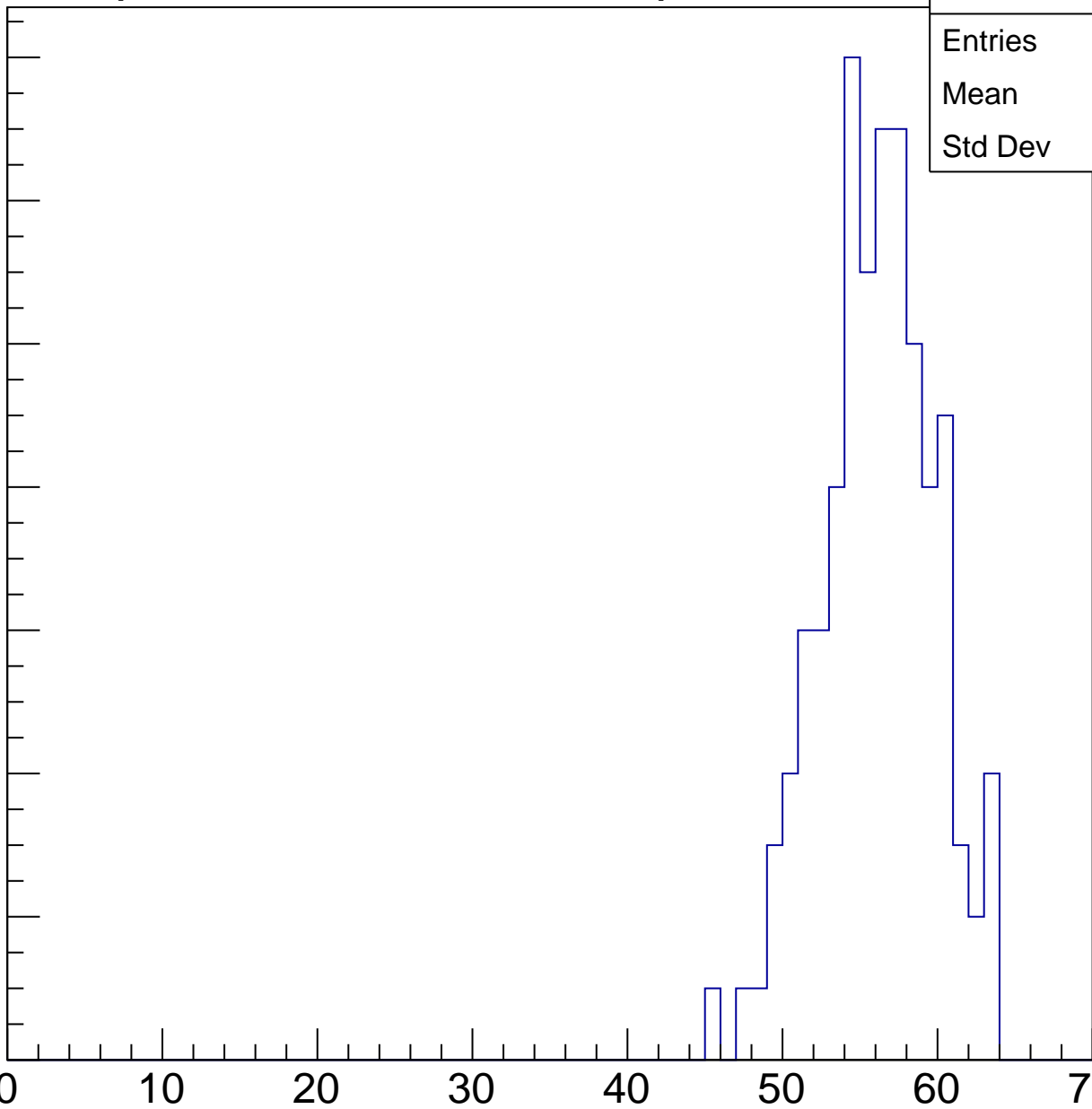
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	55.64
Std Dev	3.627

ampl



B0L101S, U25-ch110, adc5

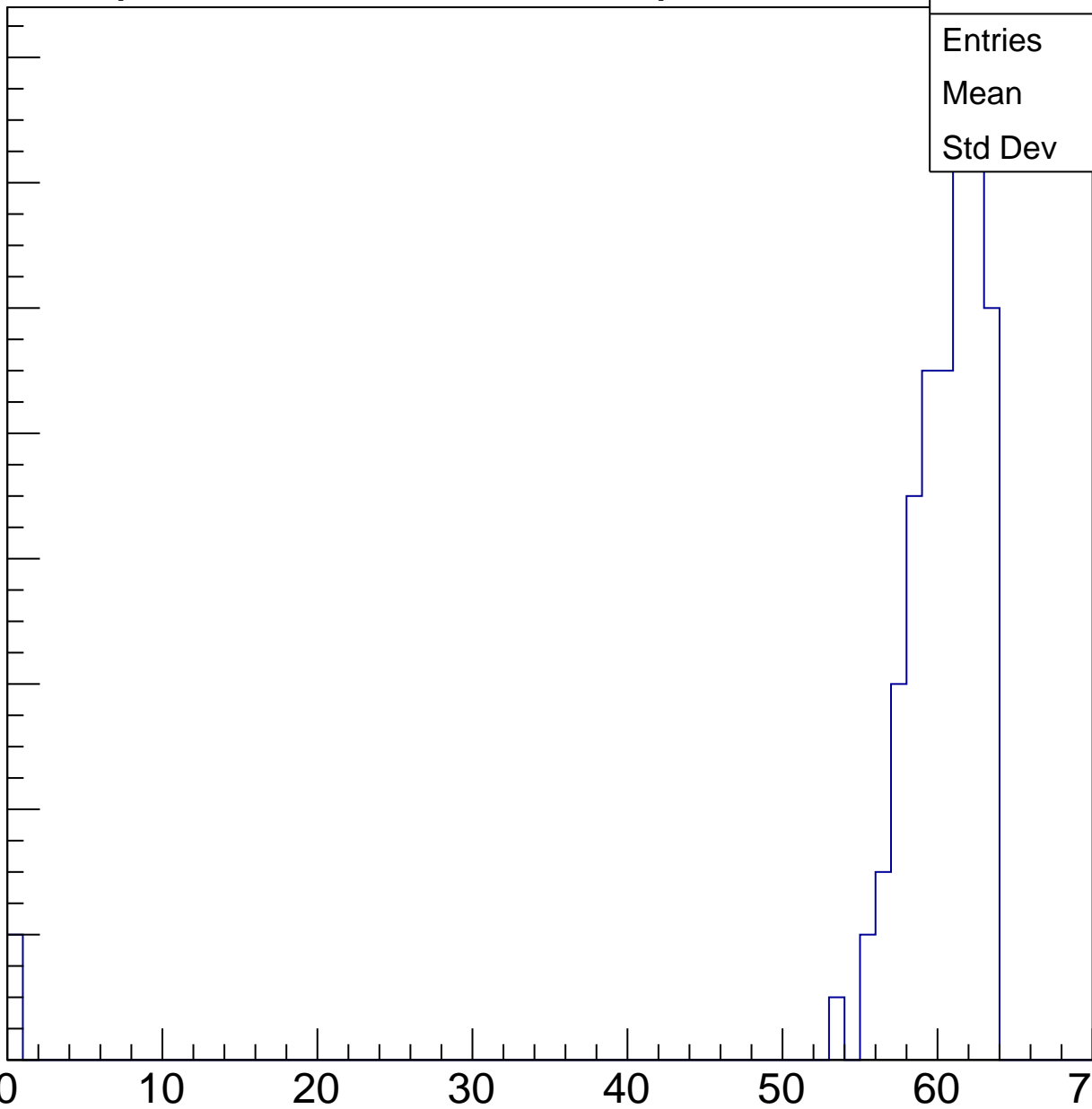
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	58.74
Std Dev	9.177

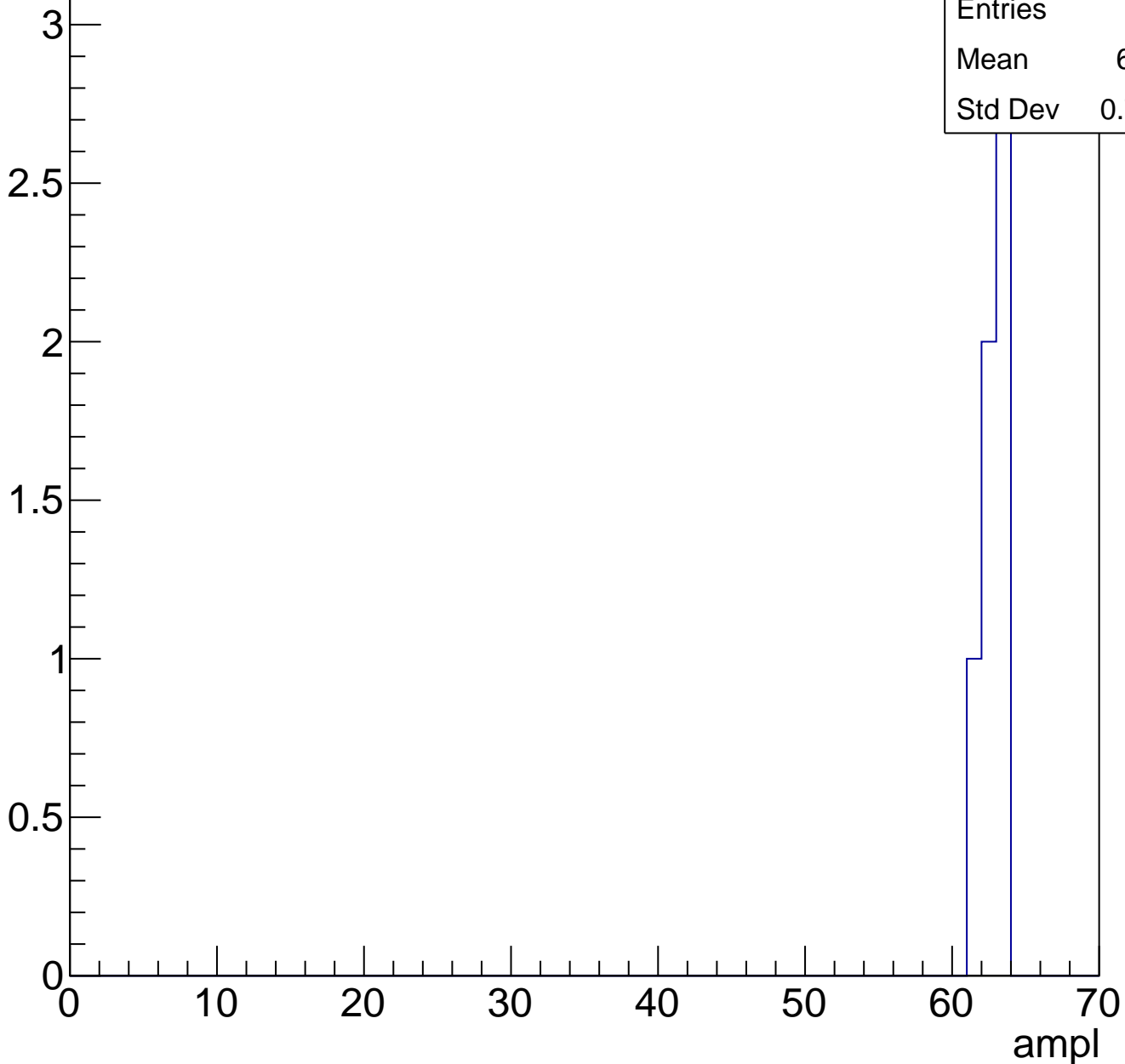
ampl



B0L101S, U25-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

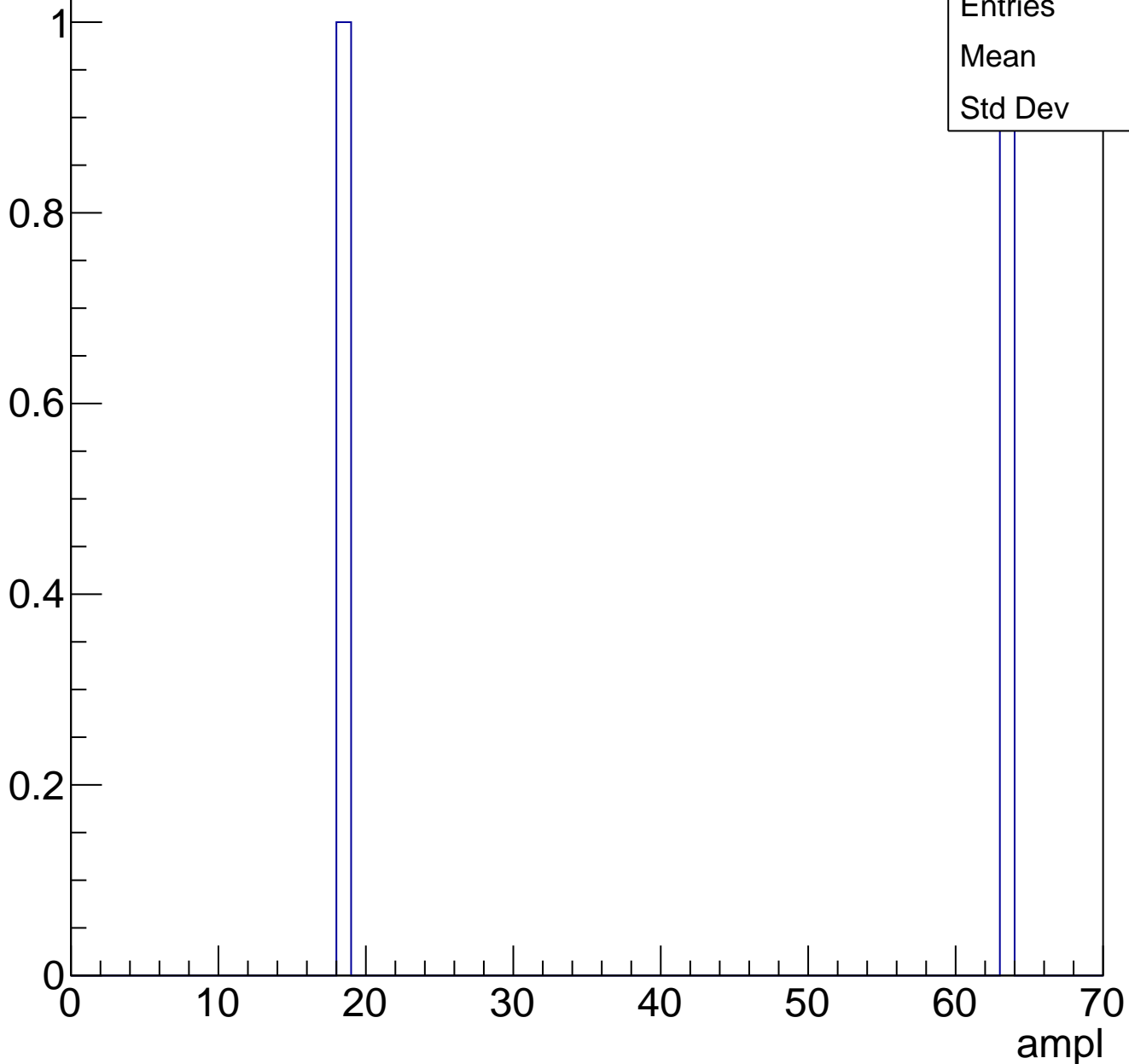
Entry



B0L101S, U25-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch111, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	29.06
Std Dev	3.684

Gaus mean : 29.3104

Gaus Width: 3.7586

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

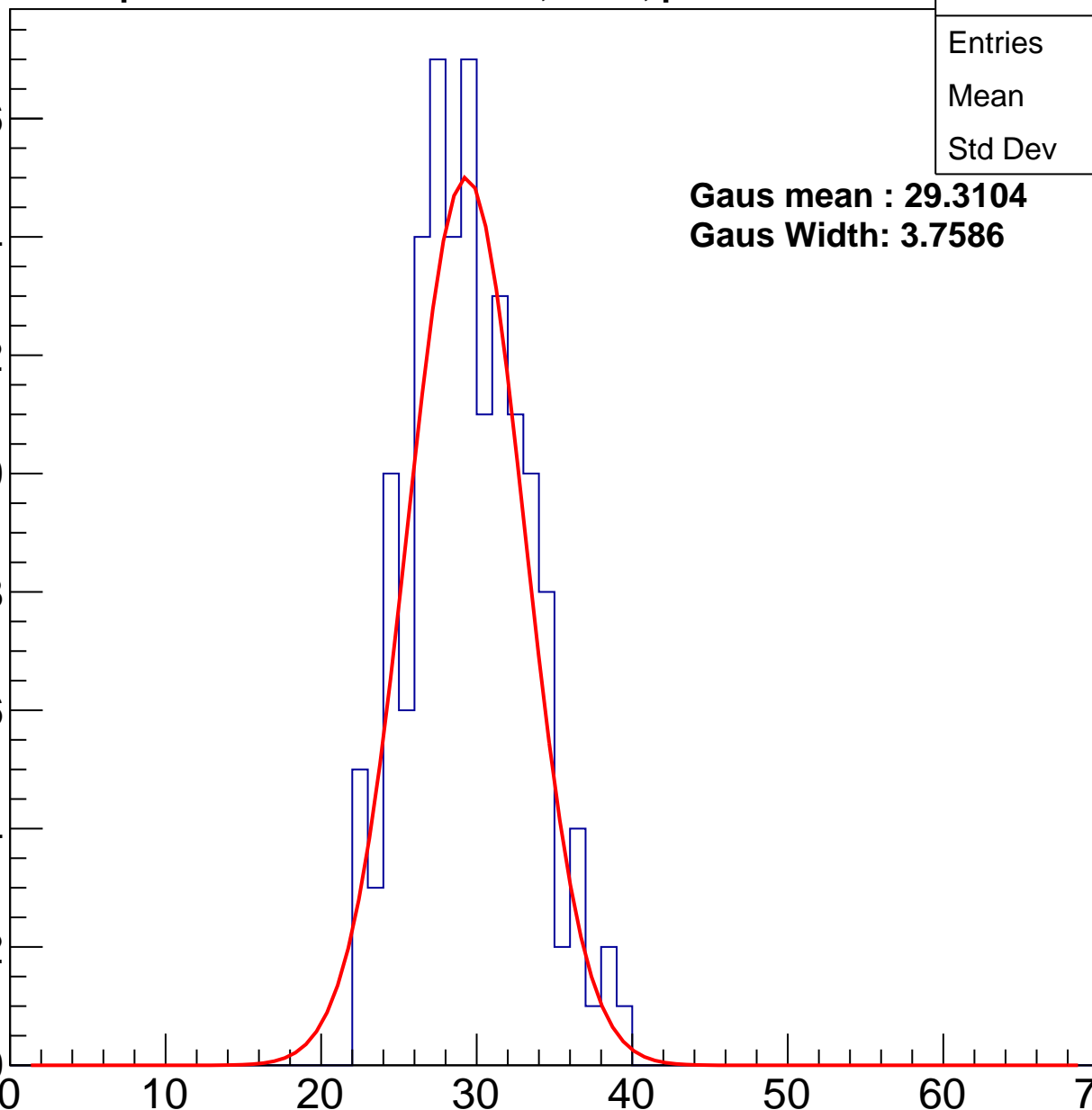
40

50

60

70

ampl



B0L101S, U25-ch111, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

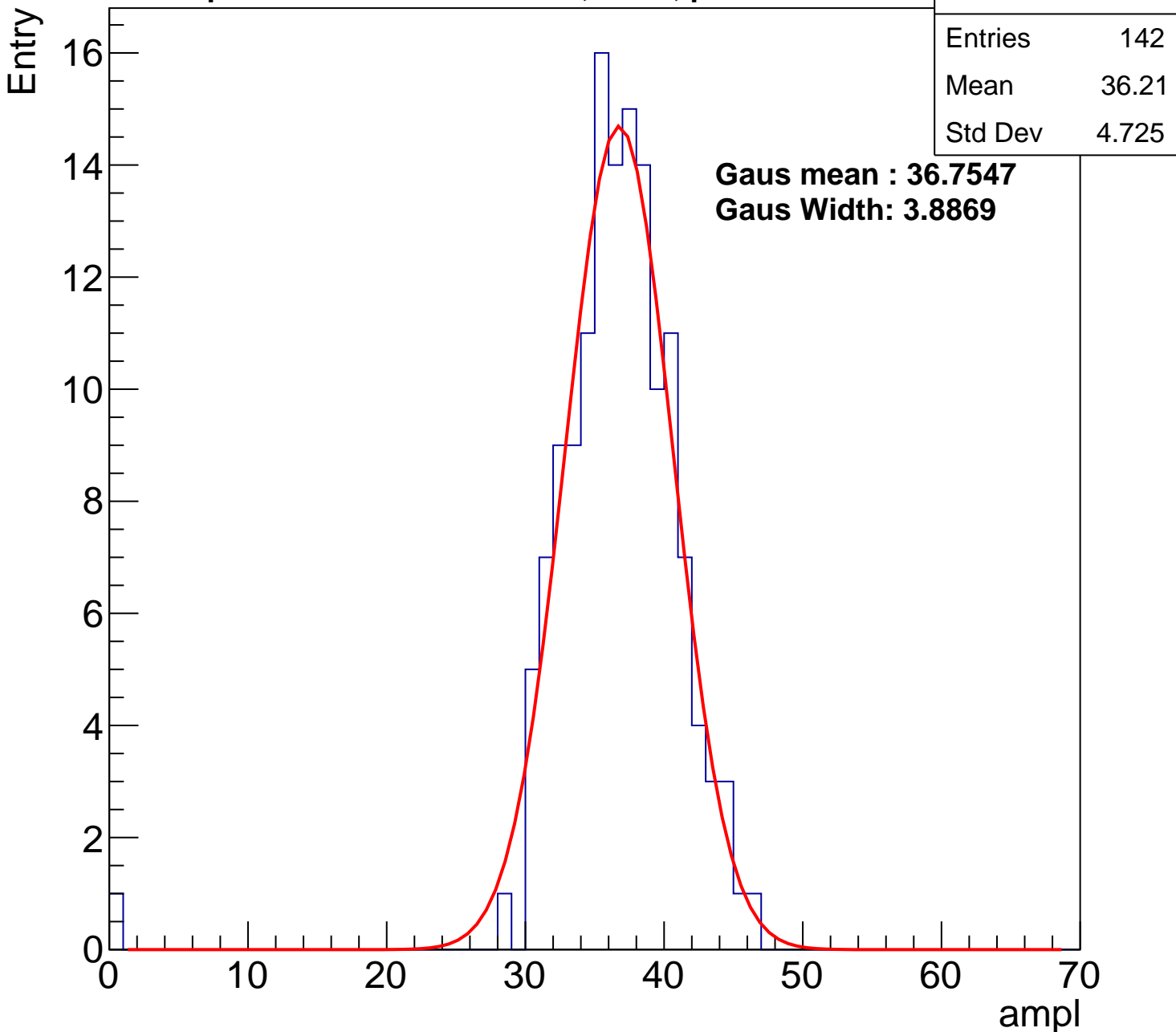
16
14
12
10
8
6
4
2
0

Entries	142
Mean	36.21
Std Dev	4.725

Gaus mean : 36.7547
Gaus Width: 3.8869

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch111, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

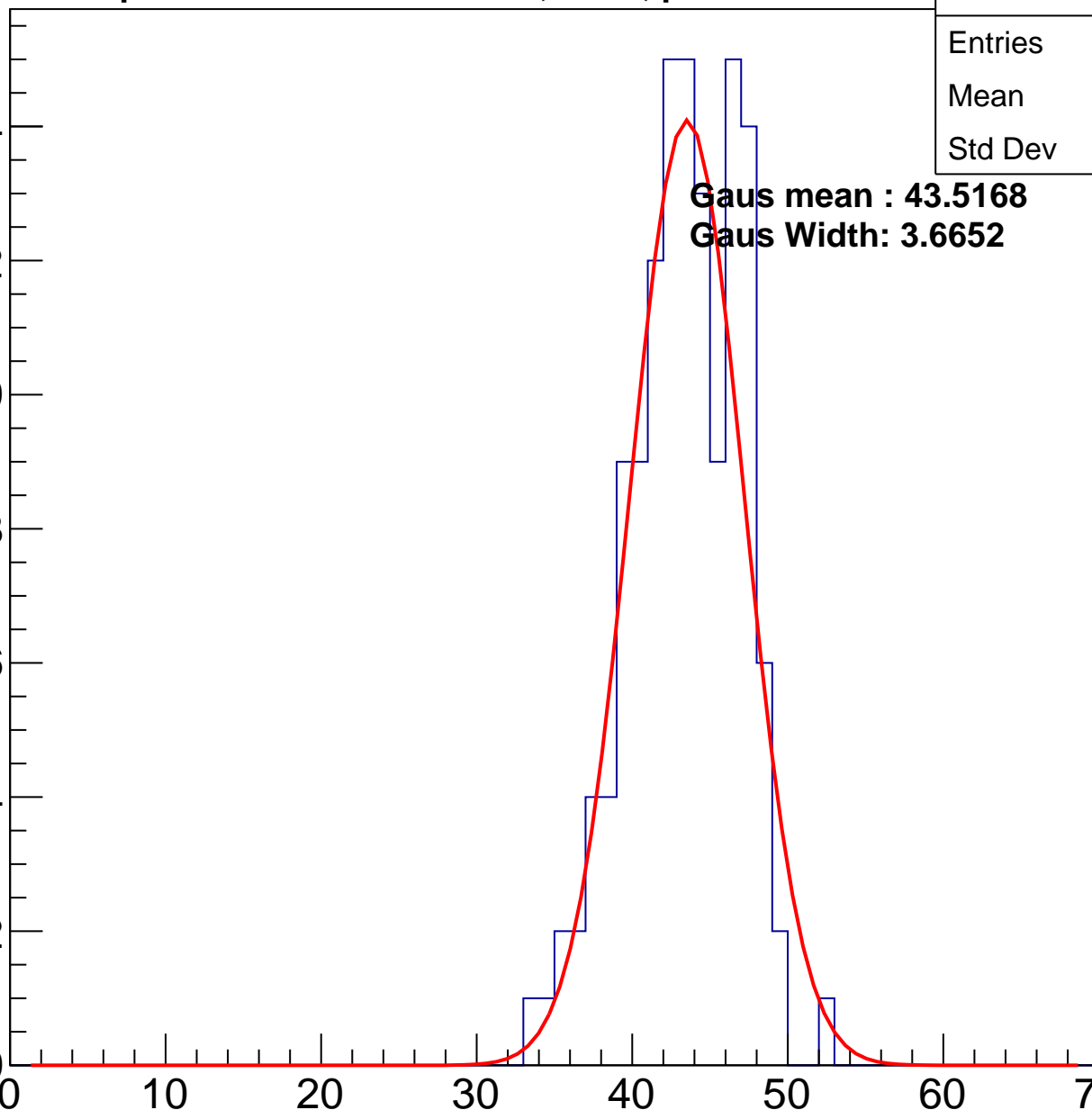
70

ampl

Entries	134
Mean	42.91
Std Dev	3.508

Gaus mean : 43.5168

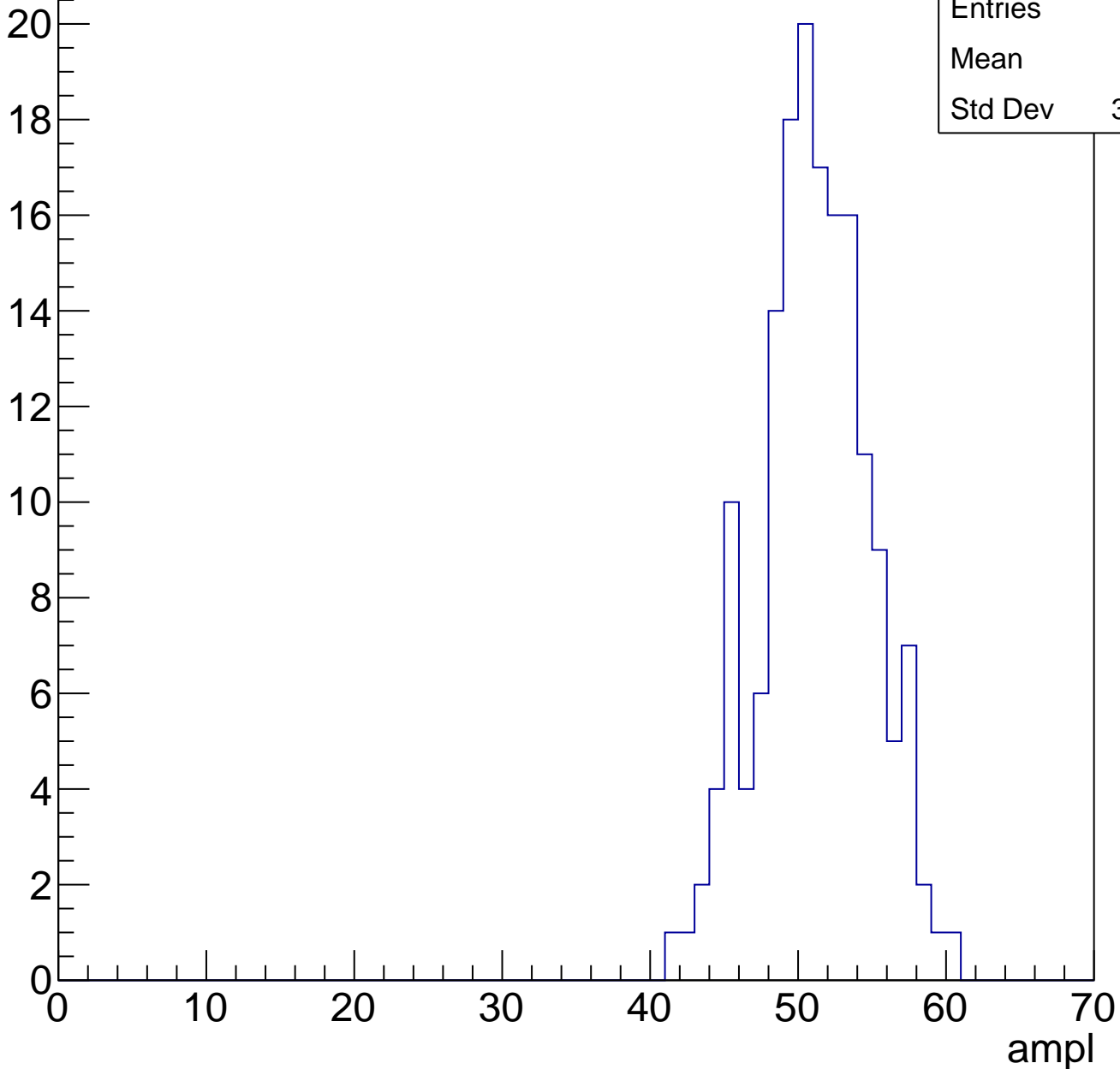
Gaus Width: 3.6652



B0L101S, U25-ch111, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch111, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

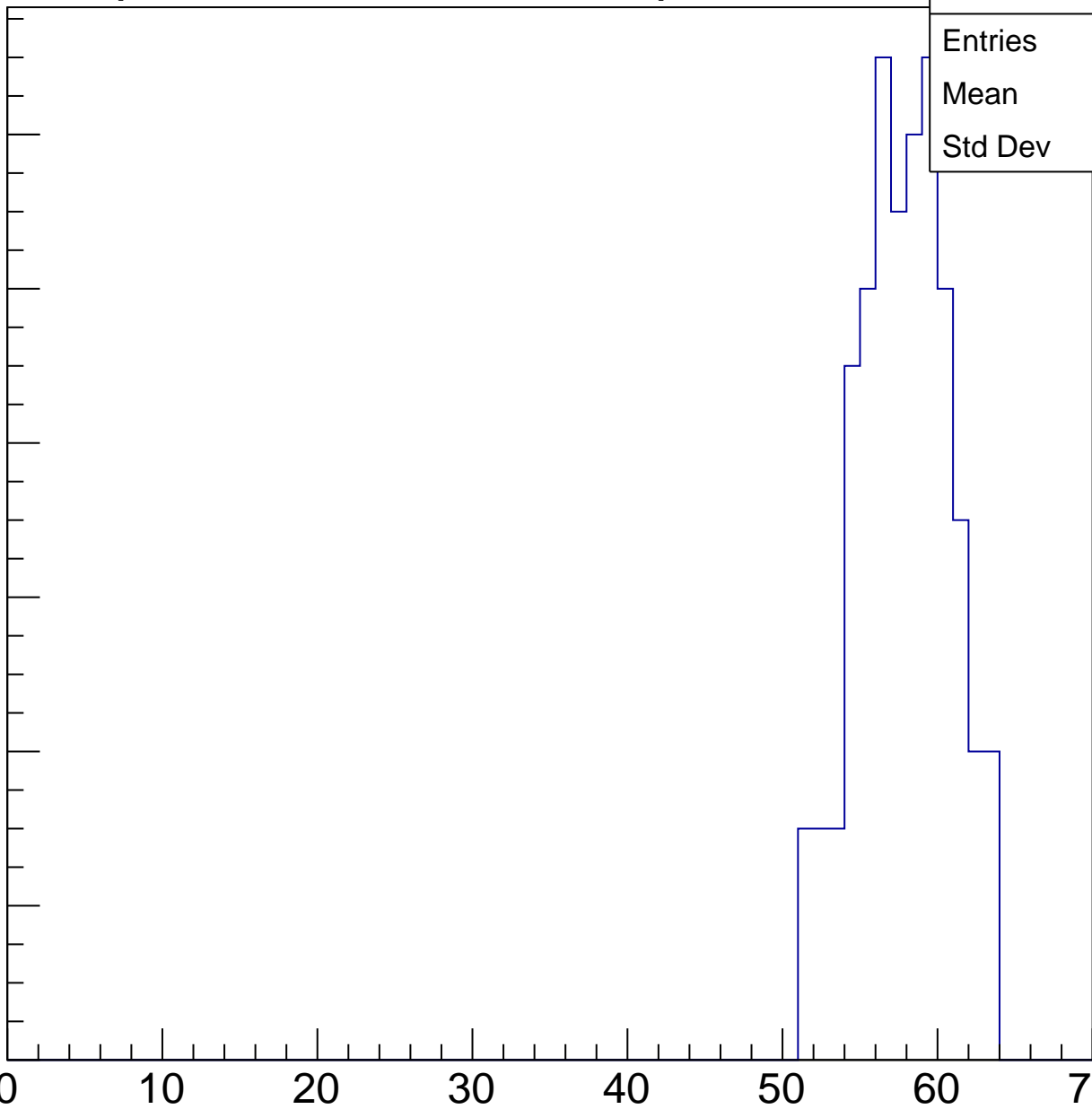
102

Mean

57.34

Std Dev

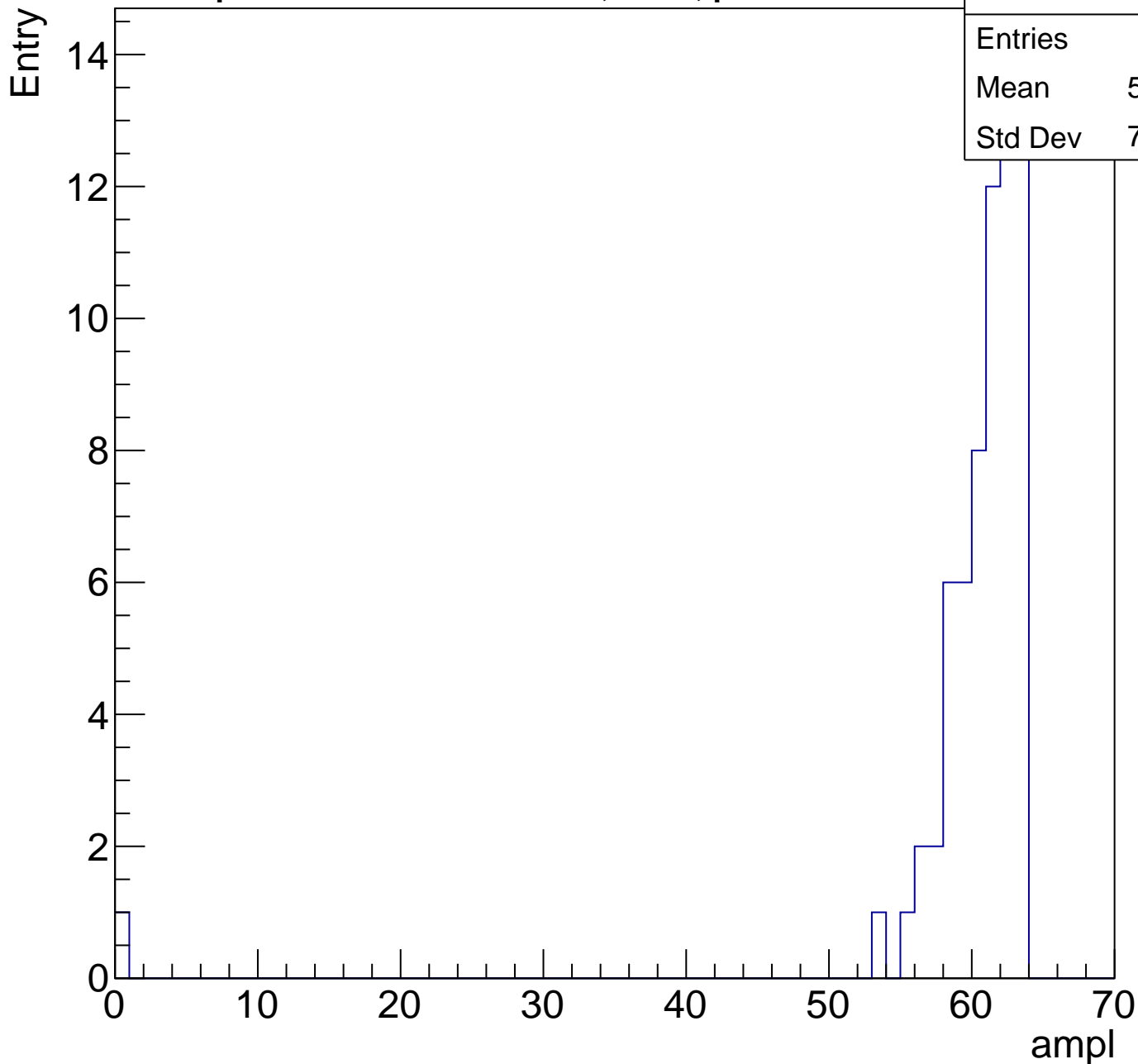
2.909



B0L101S, U25-ch111, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	66
Mean	59.62
Std Dev	7.716



B0L101S, U25-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

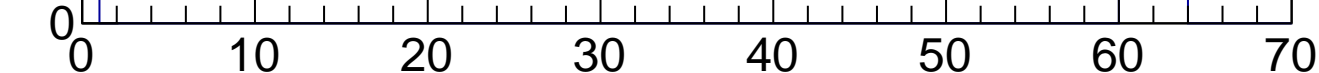
8

Mean

54.25

Std Dev

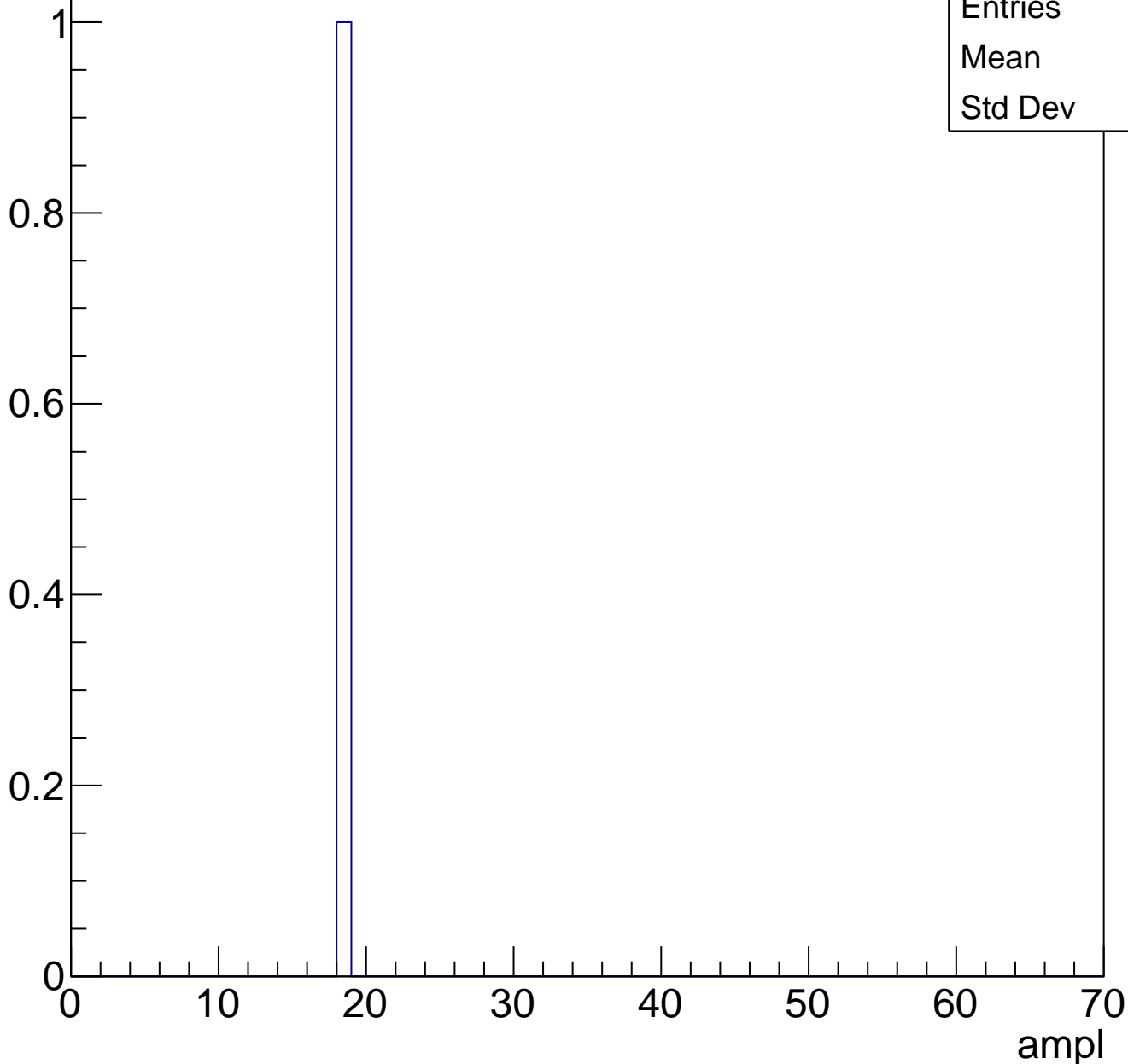
20.53



B0L101S, U25-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



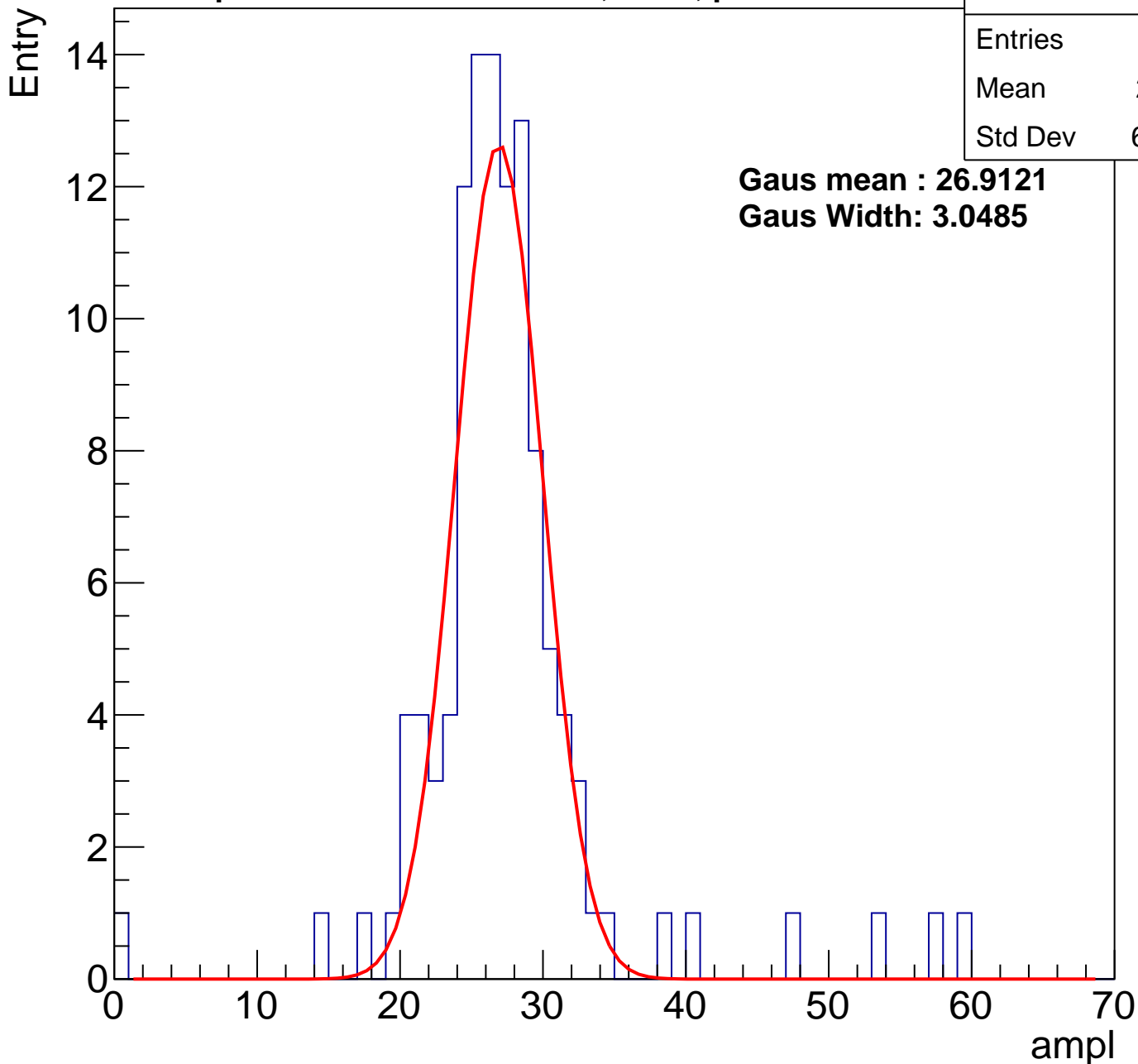
B0L101S, U25-ch112, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	27.01
Std Dev	6.894

Gaus mean : 26.9121

Gaus Width: 3.0485



B0L101S, U25-ch112, adc1

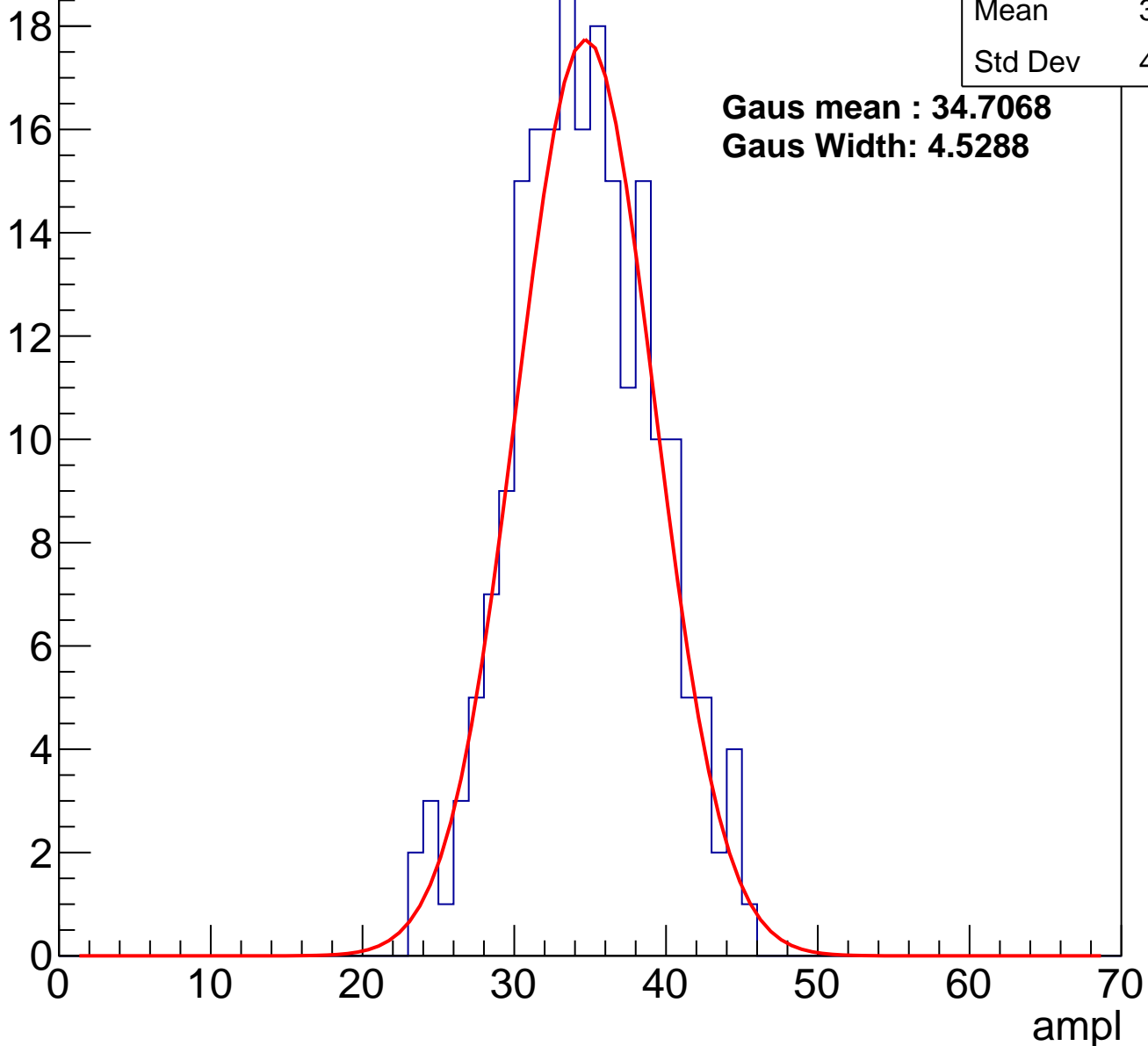
calib_packv5_042523_0143.root, FC#1, port C1

Entries	208
Mean	34.14
Std Dev	4.534

Gaus mean : 34.7068

Gaus Width: 4.5288

Entry



B0L101S, U25-ch112, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

114

Mean

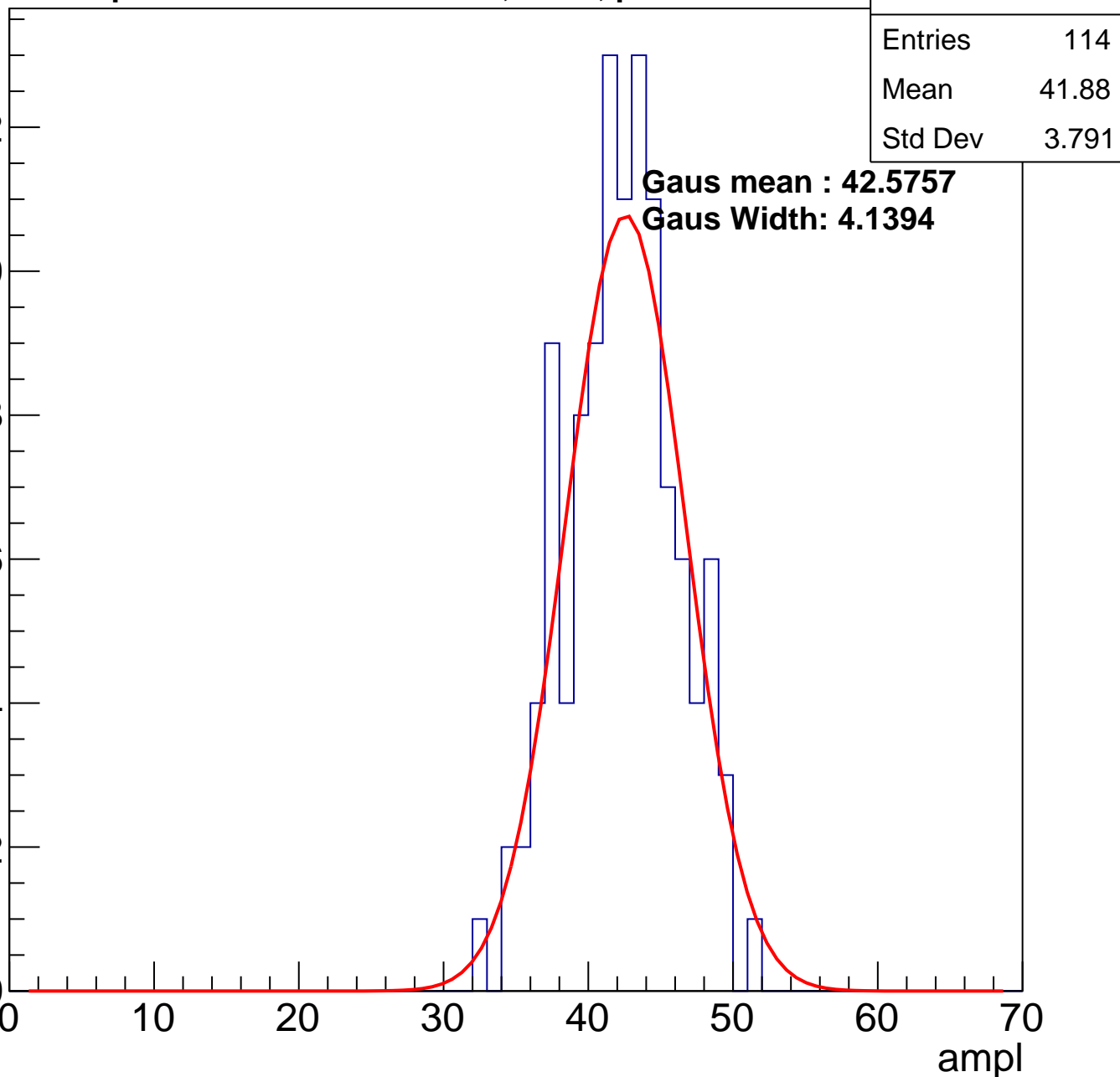
41.88

Std Dev

3.791

Gaus mean : 42.5757

Gaus Width: 4.1394



B0L101S, U25-ch112, adc3

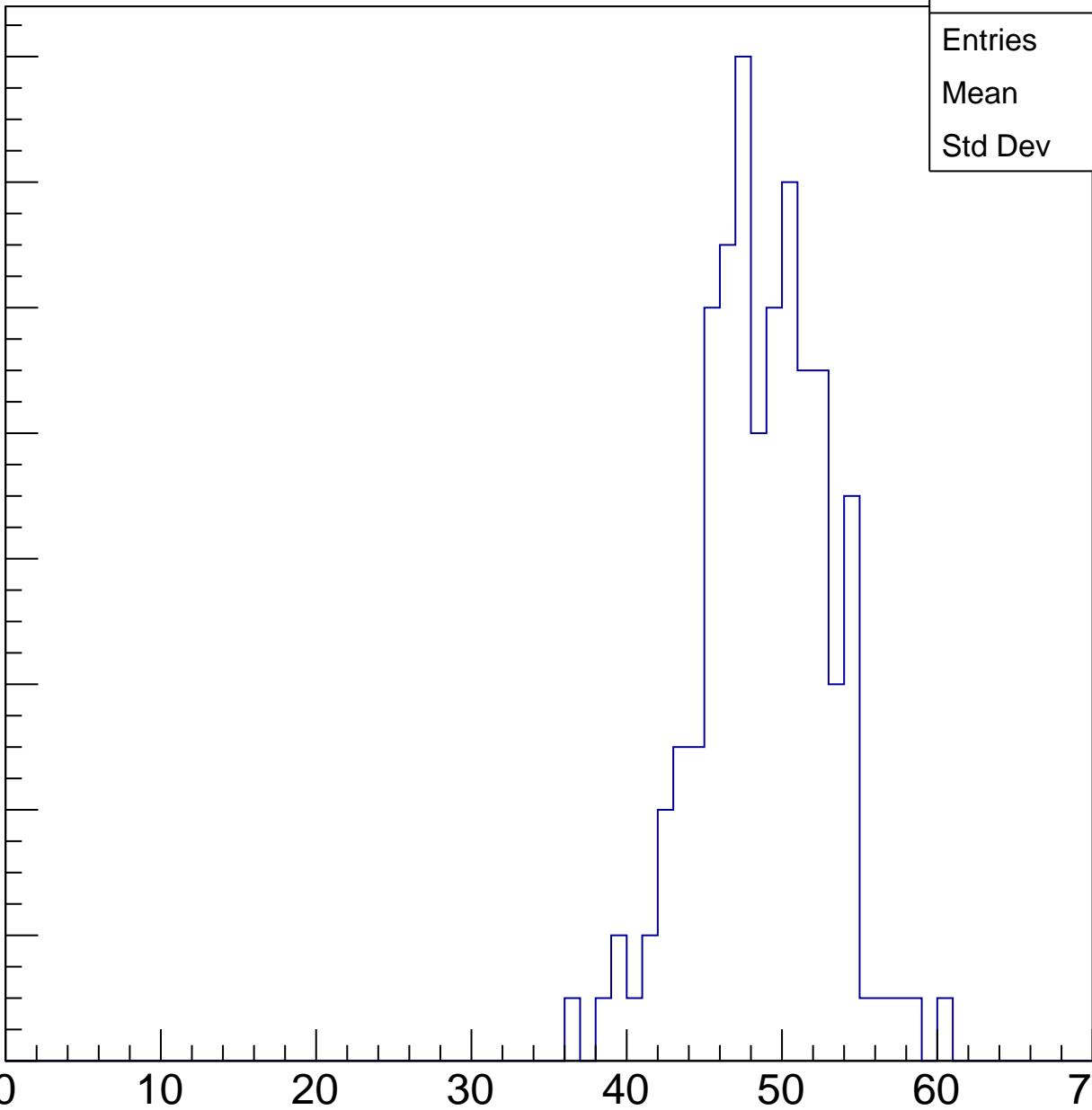
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	140
Mean	48.27
Std Dev	4.086

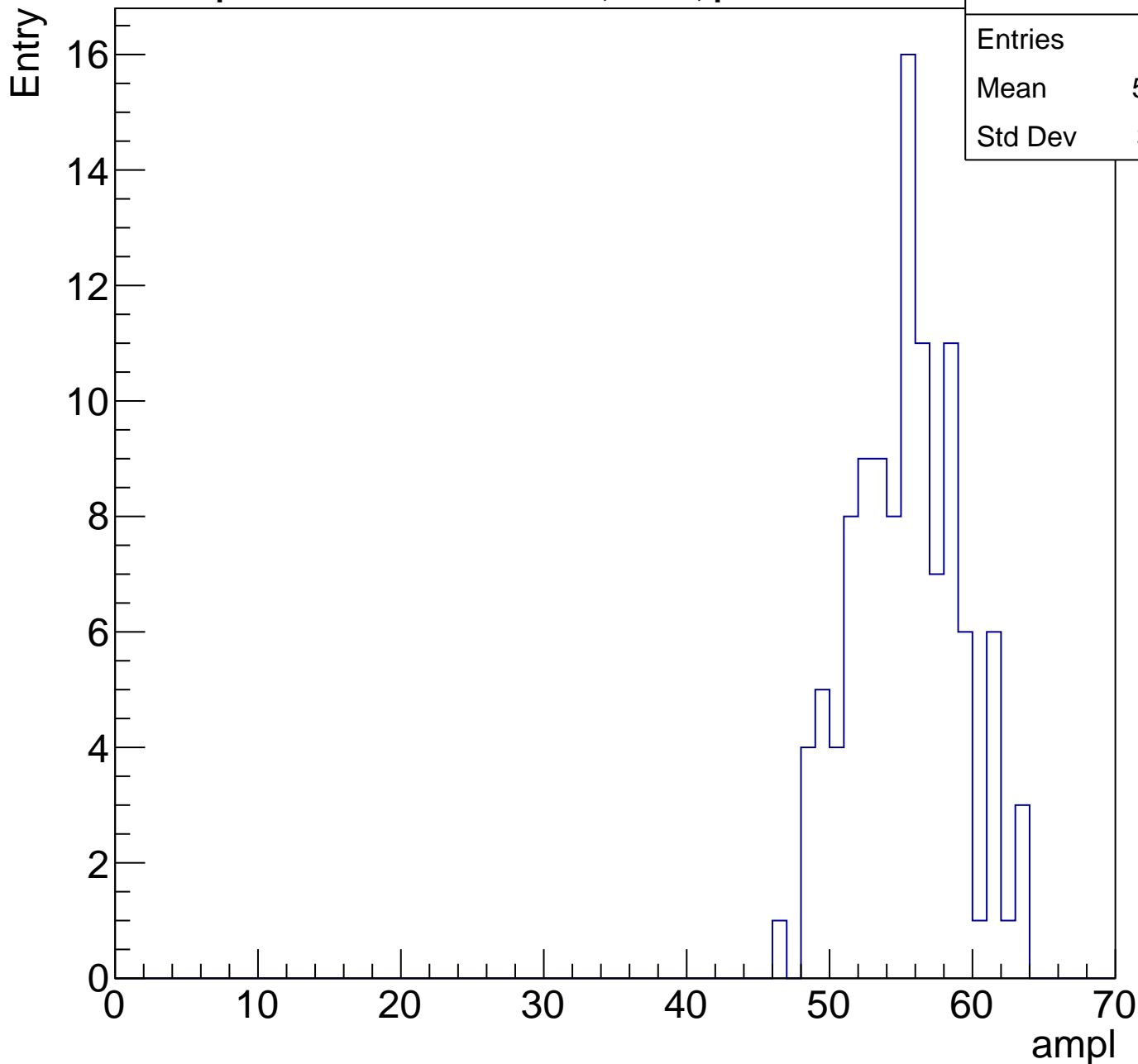
ampl



B0L101S, U25-ch112, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	54.84
Std Dev	3.721



B0L101S, U25-ch112, adc5

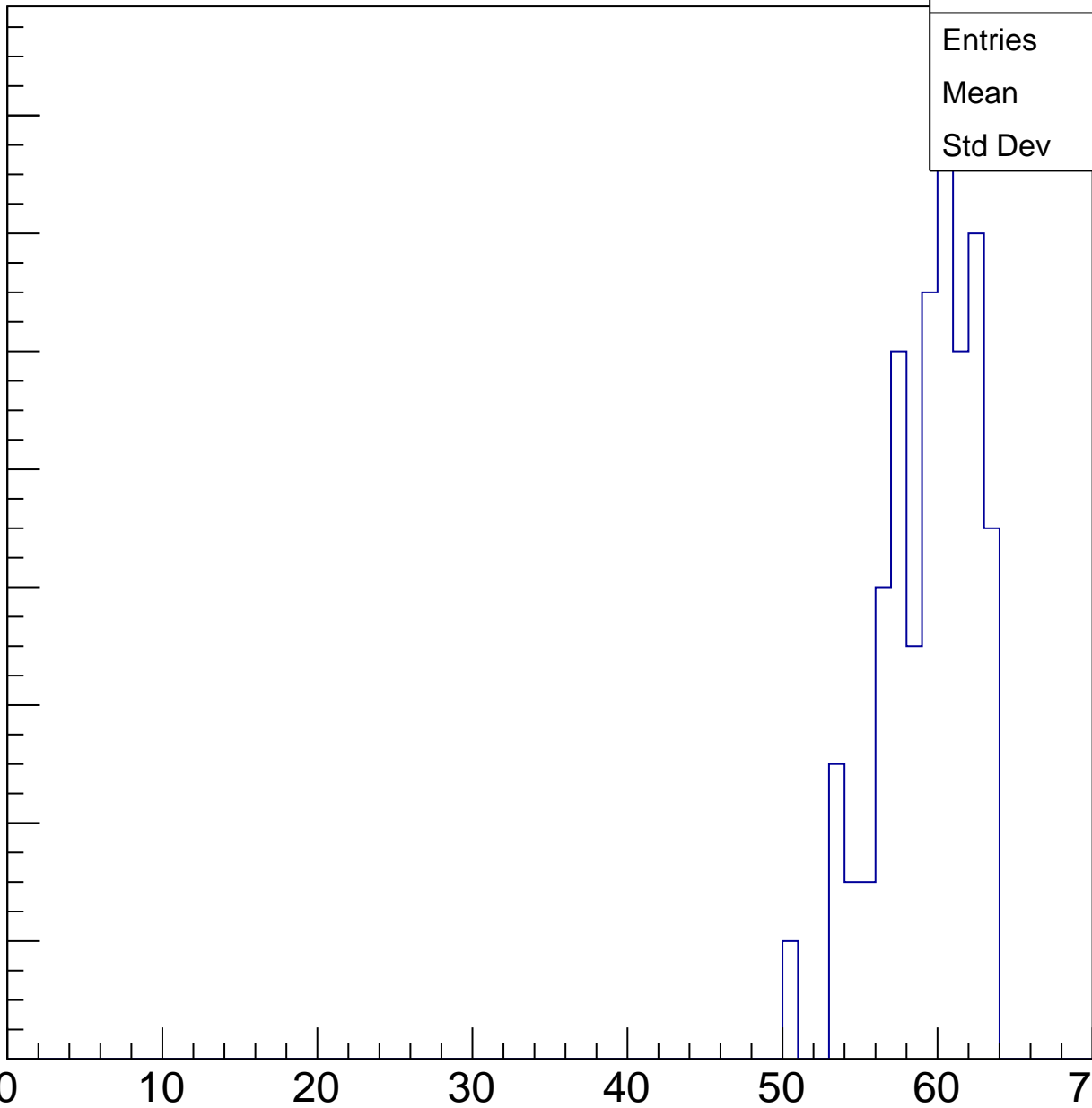
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.9
Std Dev	2.969

ampl

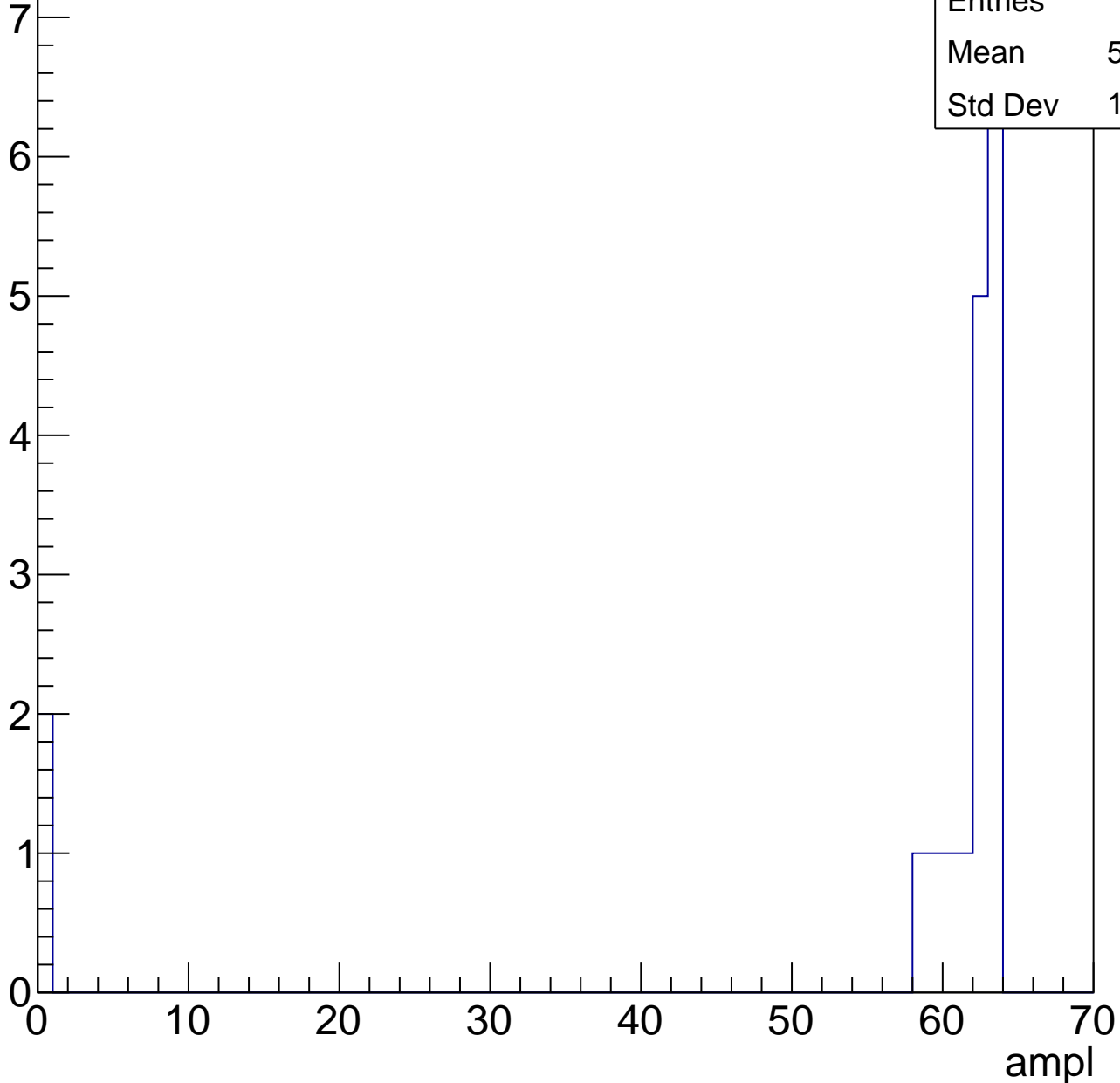


B0L101S, U25-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	54.94
Std Dev	19.48



B0L101S, U25-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

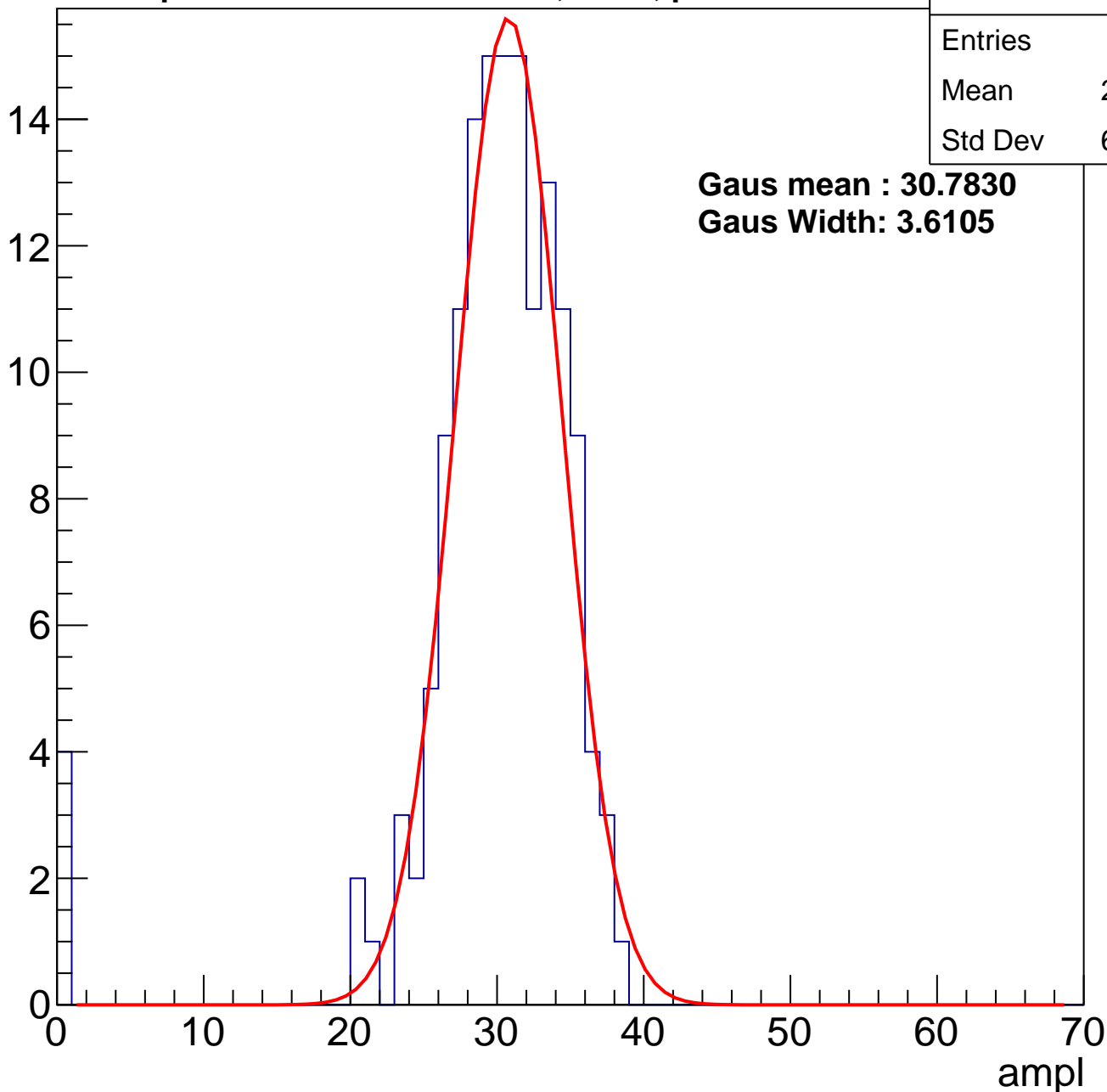
Entry



B0L101S, U25-ch113, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	148
Mean	29.32
Std Dev	6.042

B0L101S, U25-ch113, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

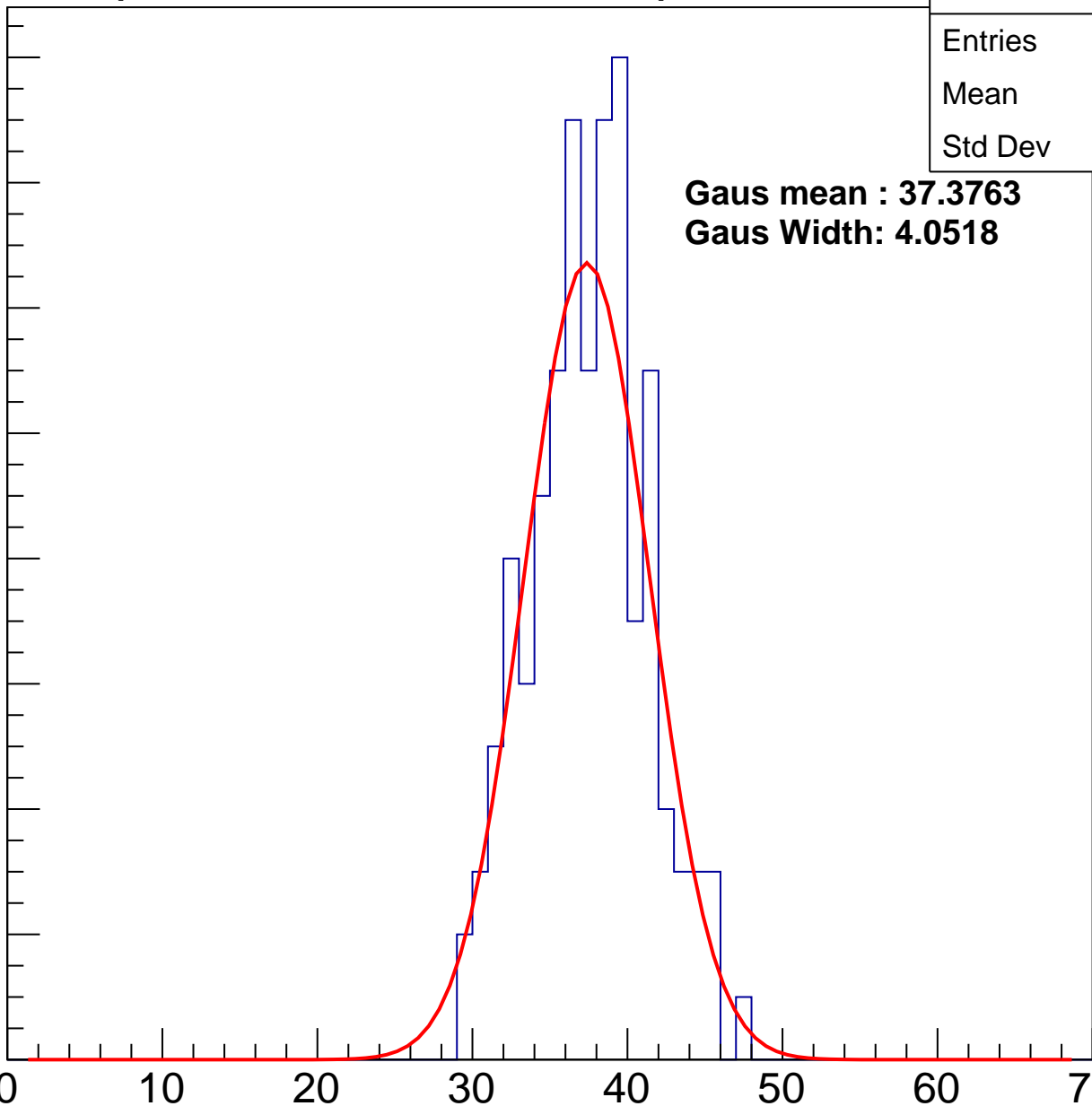
16
14
12
10
8
6
4
2
0

Entries	133
Mean	37.08
Std Dev	3.735

Gaus mean : 37.3763

Gaus Width: 4.0518

0 10 20 30 40 50 60 70
ampl



B0L101S, U25-ch113, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

118

Mean

43.28

Std Dev

3.417

Gaus mean : 43.6443

Gaus Width: 3.4409

0

10

20

30

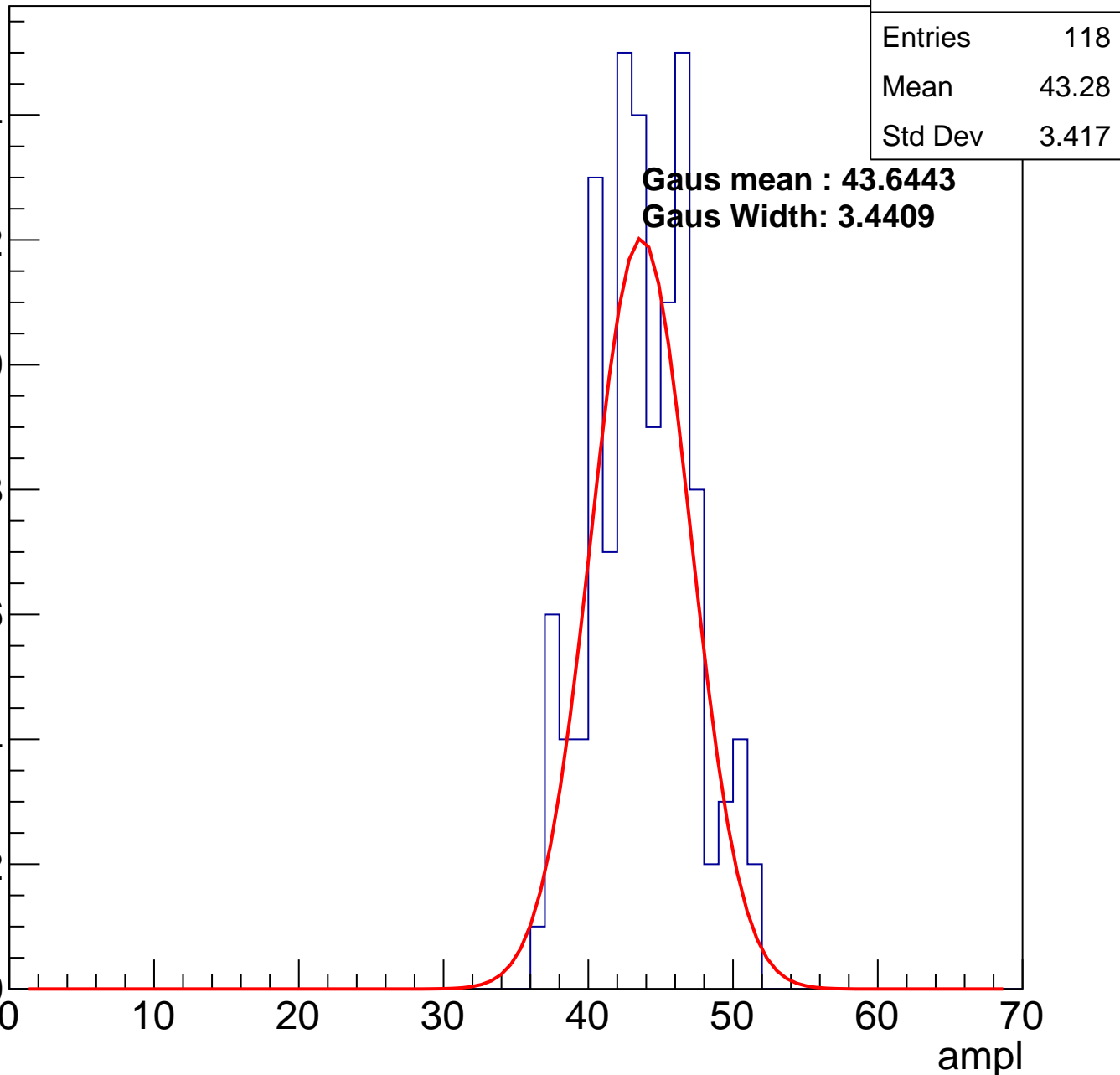
40

50

60

70

ampl

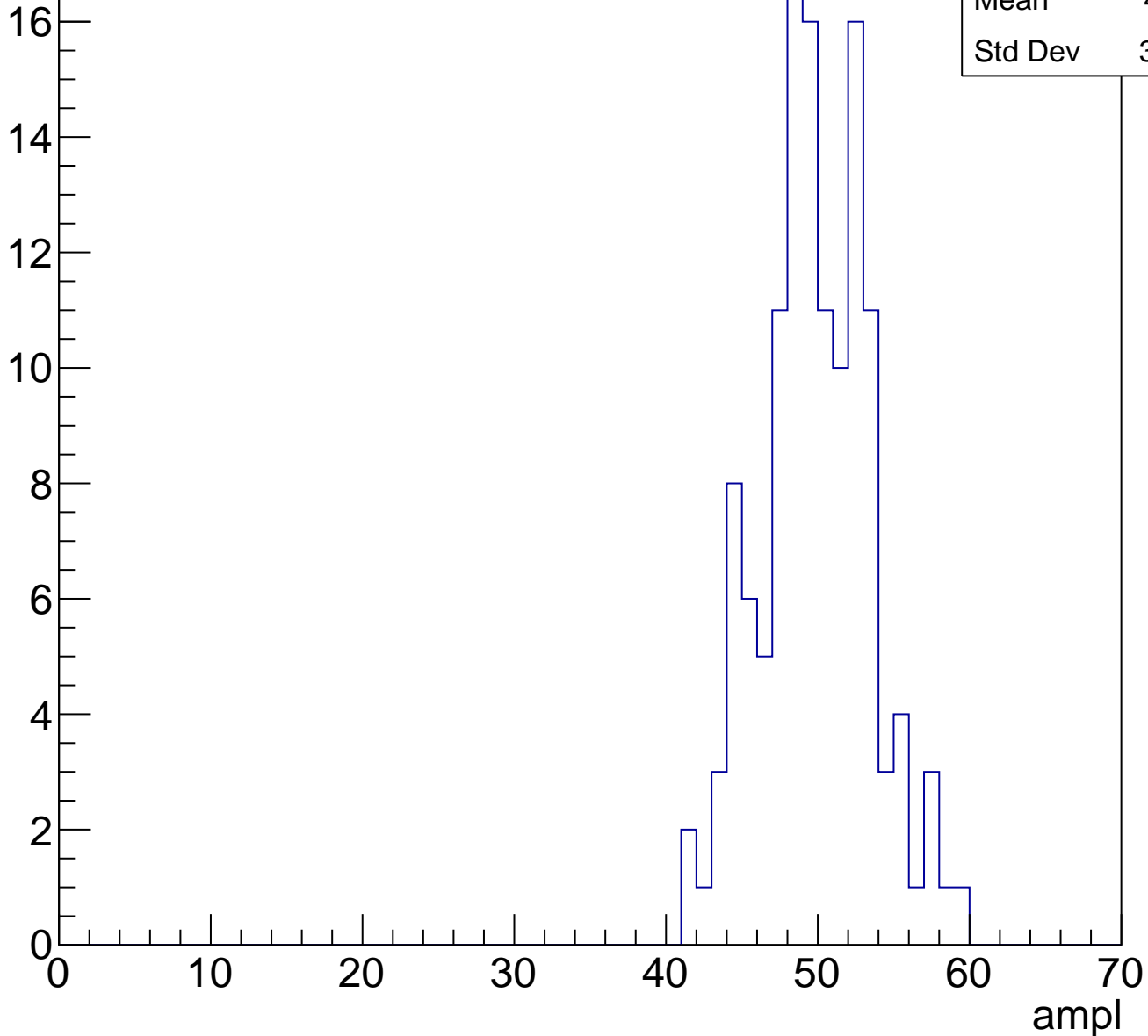


B0L101S, U25-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

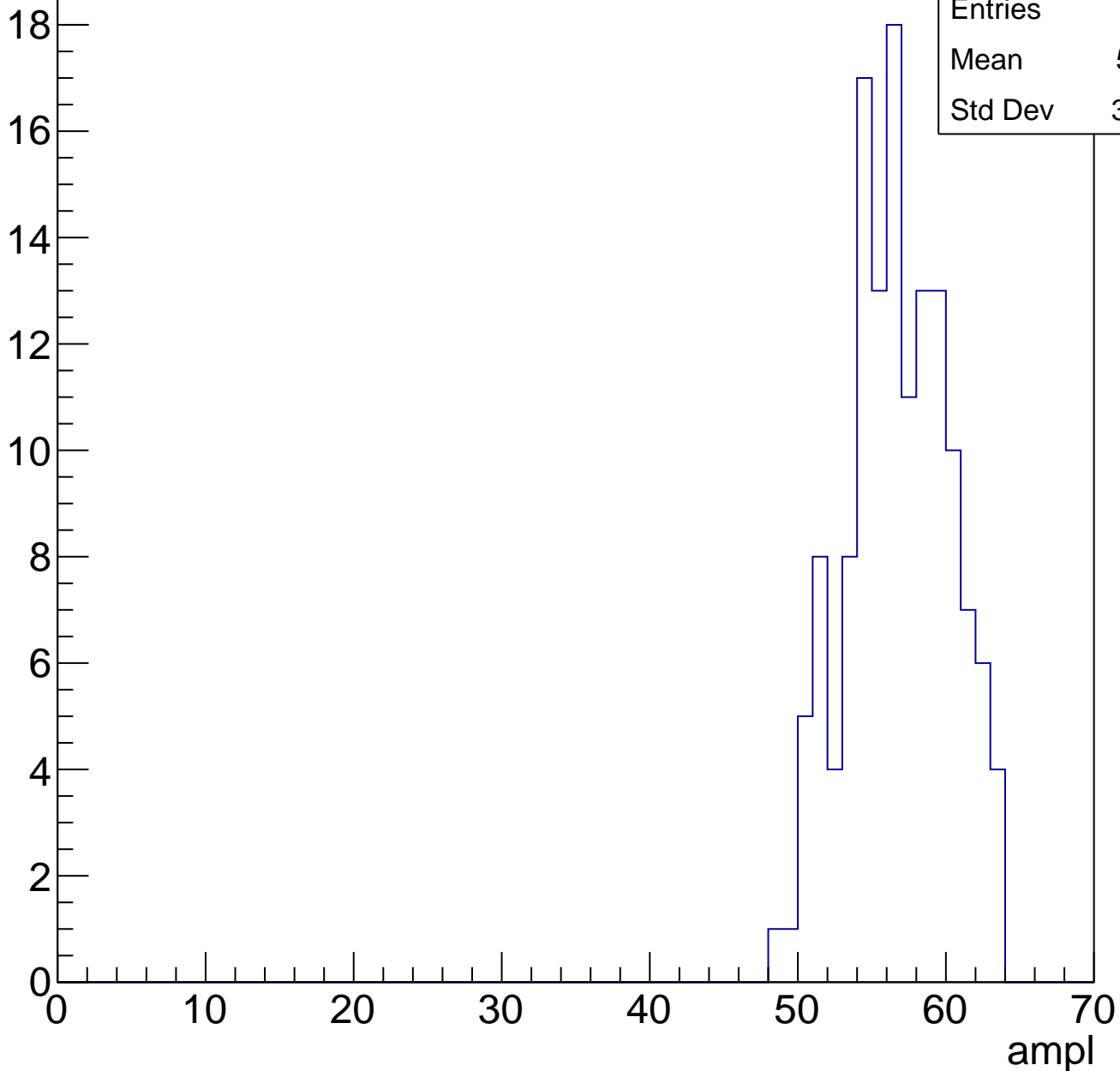
Entries	130
Mean	49.41
Std Dev	3.592



B0L101S, U25-ch113, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch113, adc5

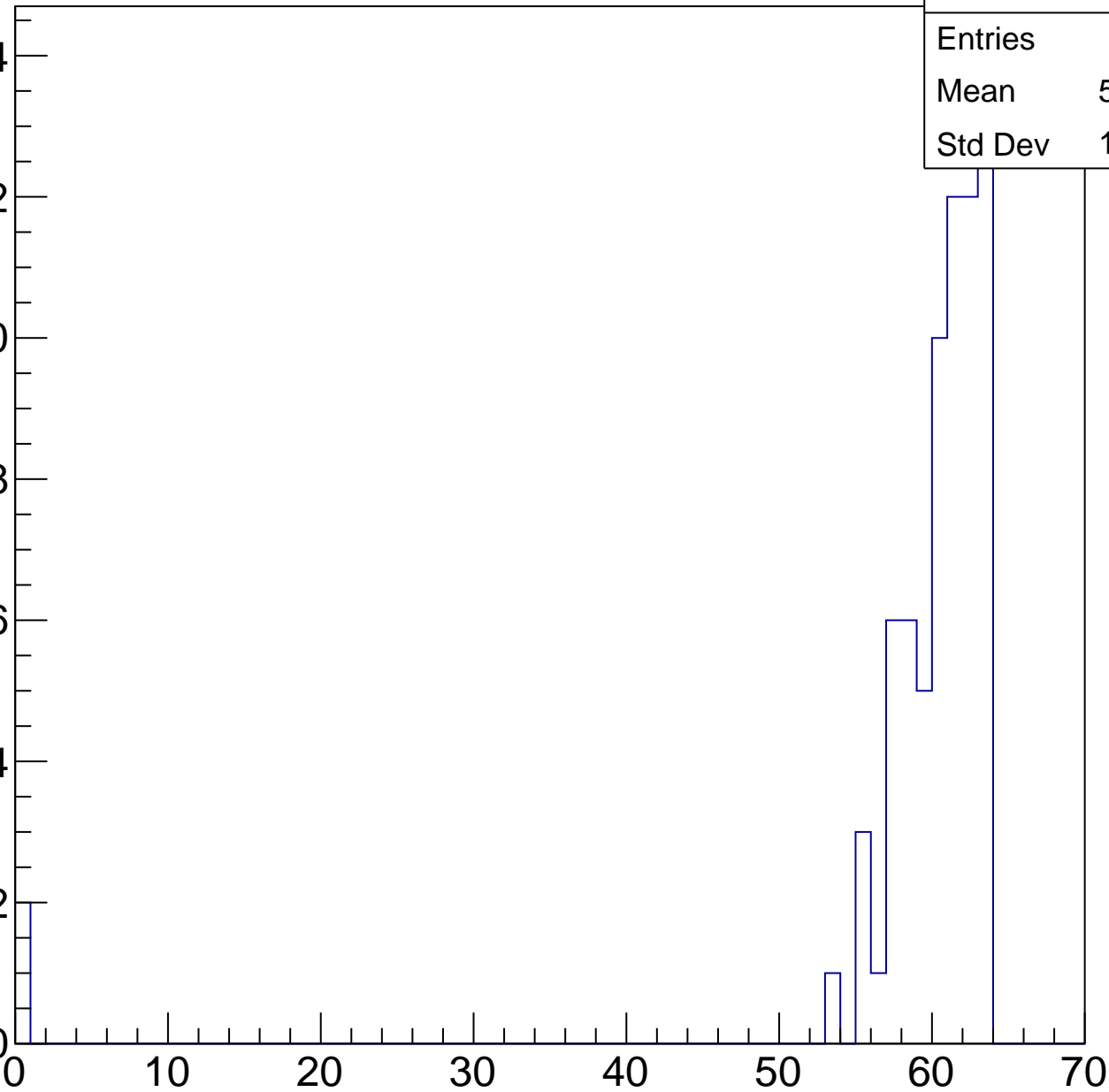
calib_packv5_042523_0143.root, FC#1, port C1

Entries	72
Mean	58.57
Std Dev	10.18

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

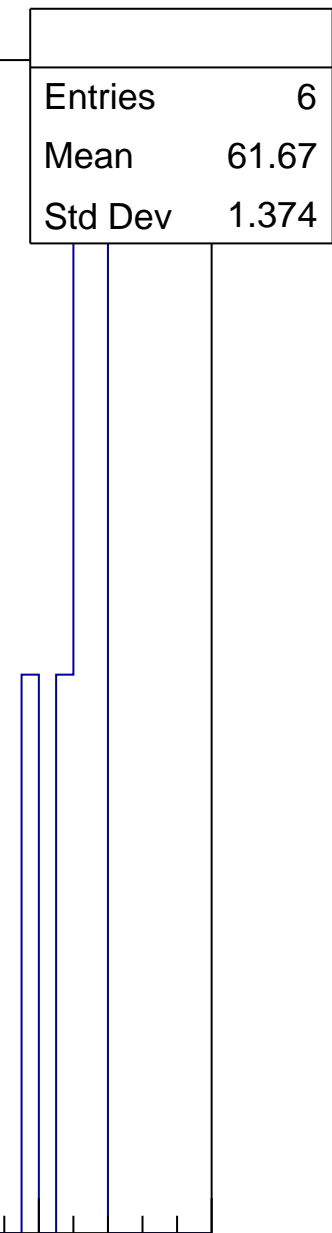
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.374

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch114, adc0

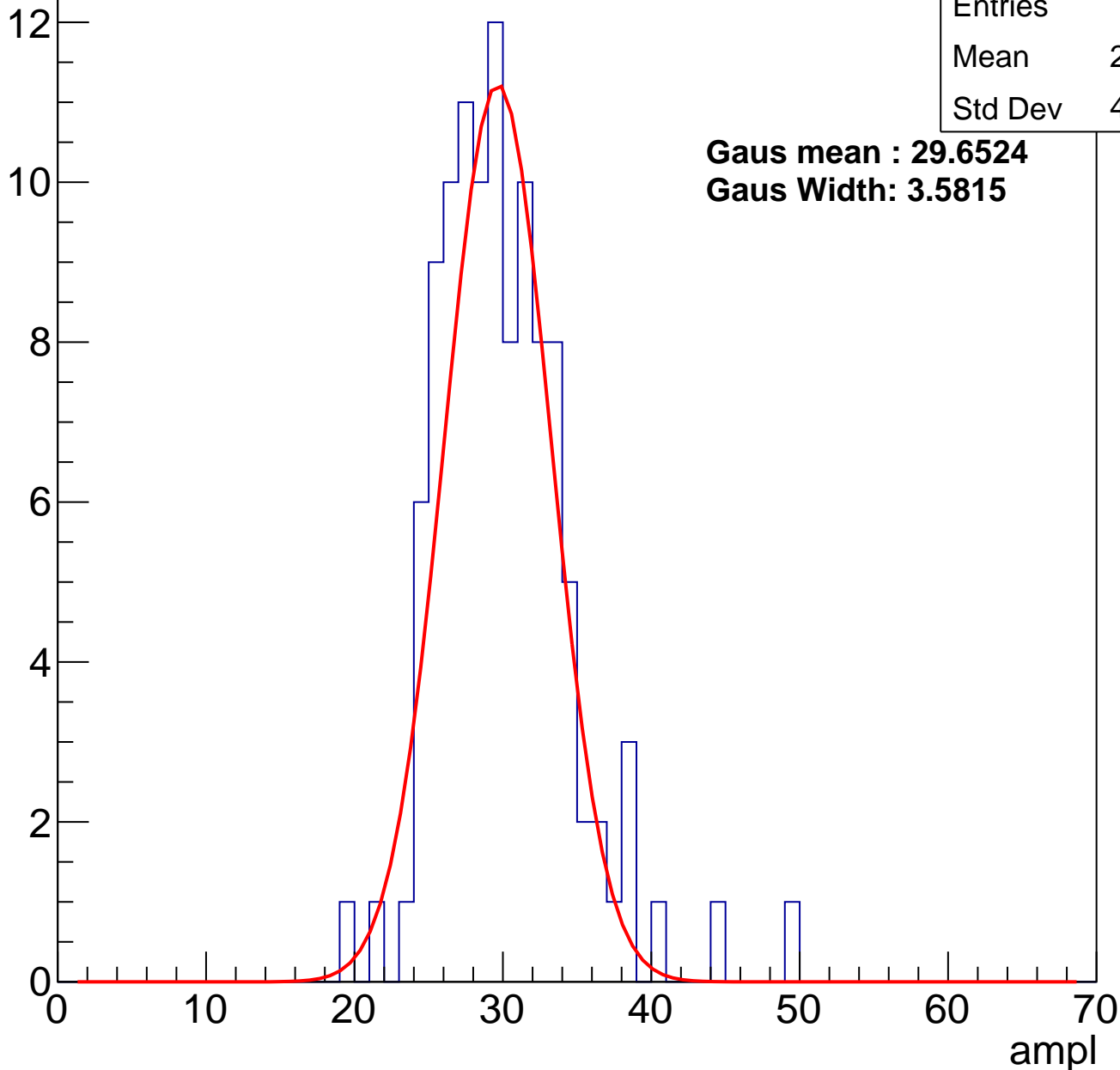
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	29.58
Std Dev	4.449

Gaus mean : 29.6524

Gaus Width: 3.5815

Entry



B0L101S, U25-ch114, adc1

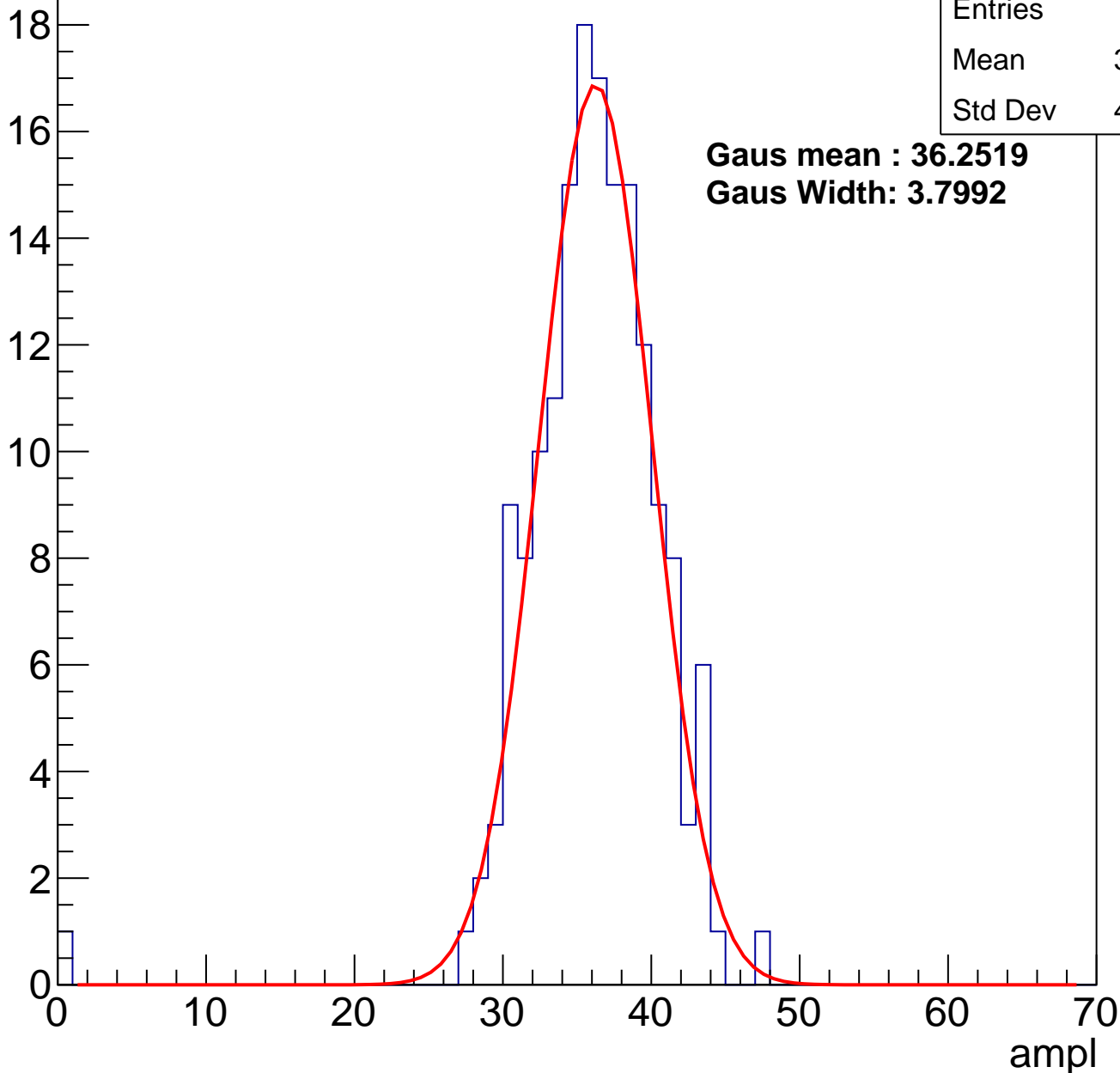
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	35.63
Std Dev	4.674

Gaus mean : 36.2519

Gaus Width: 3.7992

Entry



B0L101S, U25-ch114, adc2

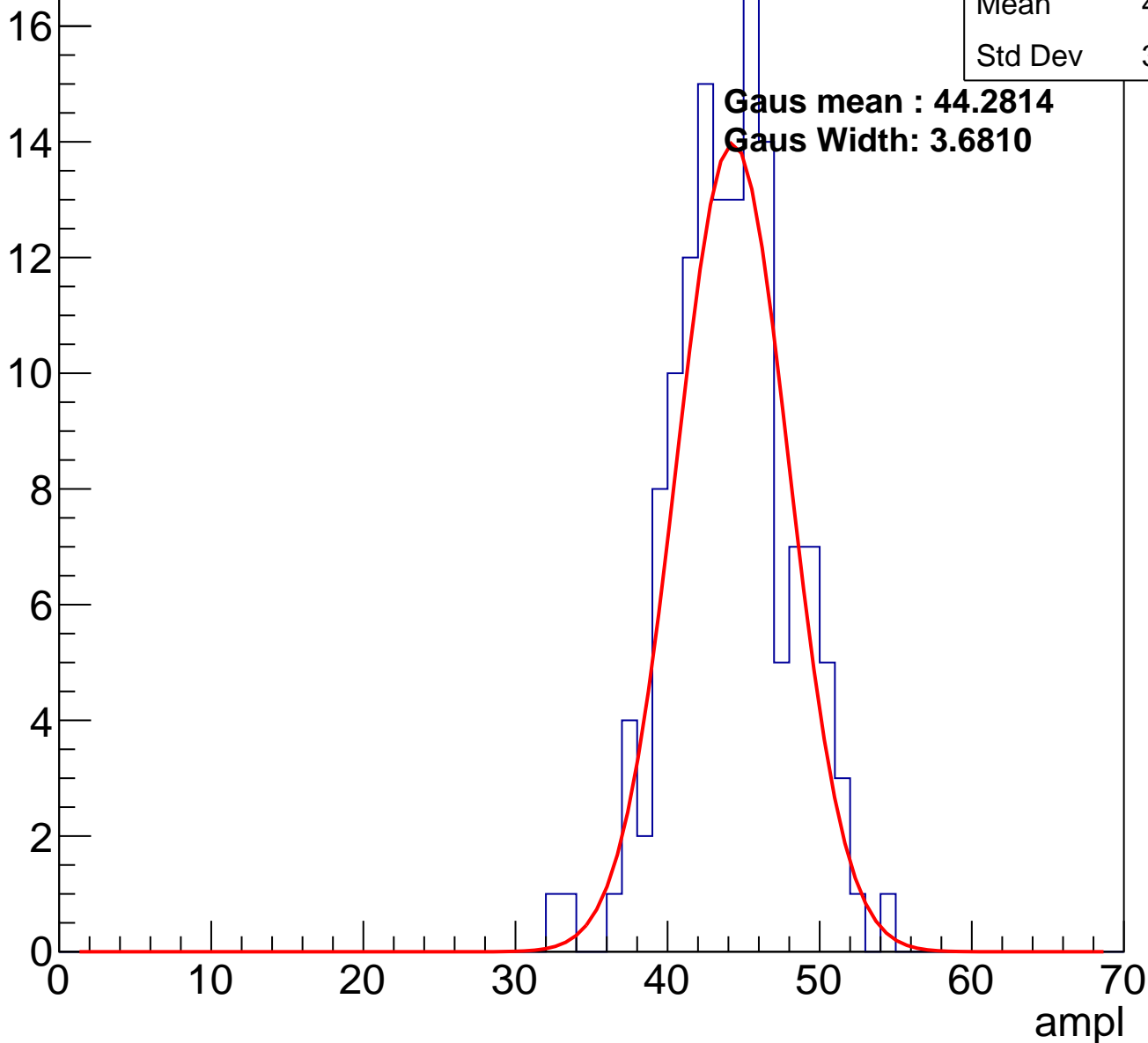
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	140
Mean	43.73
Std Dev	3.802

Gaus mean : 44.2814

Gaus Width: 3.6810



B0L101S, U25-ch114, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	50.36
Std Dev	3.663

Entry

16

14

12

10

8

6

4

2

0

0

10

20

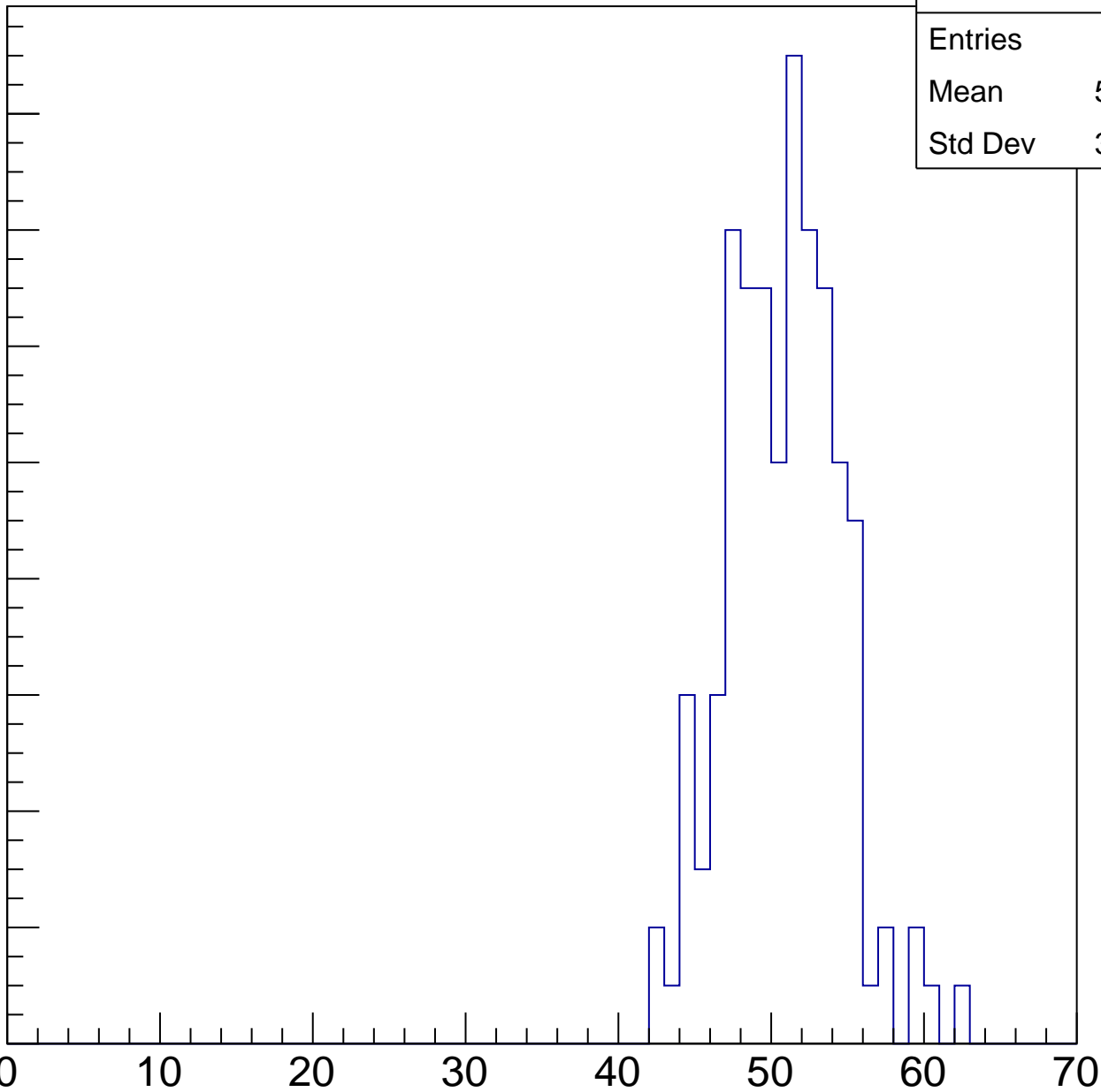
30

40

50

60

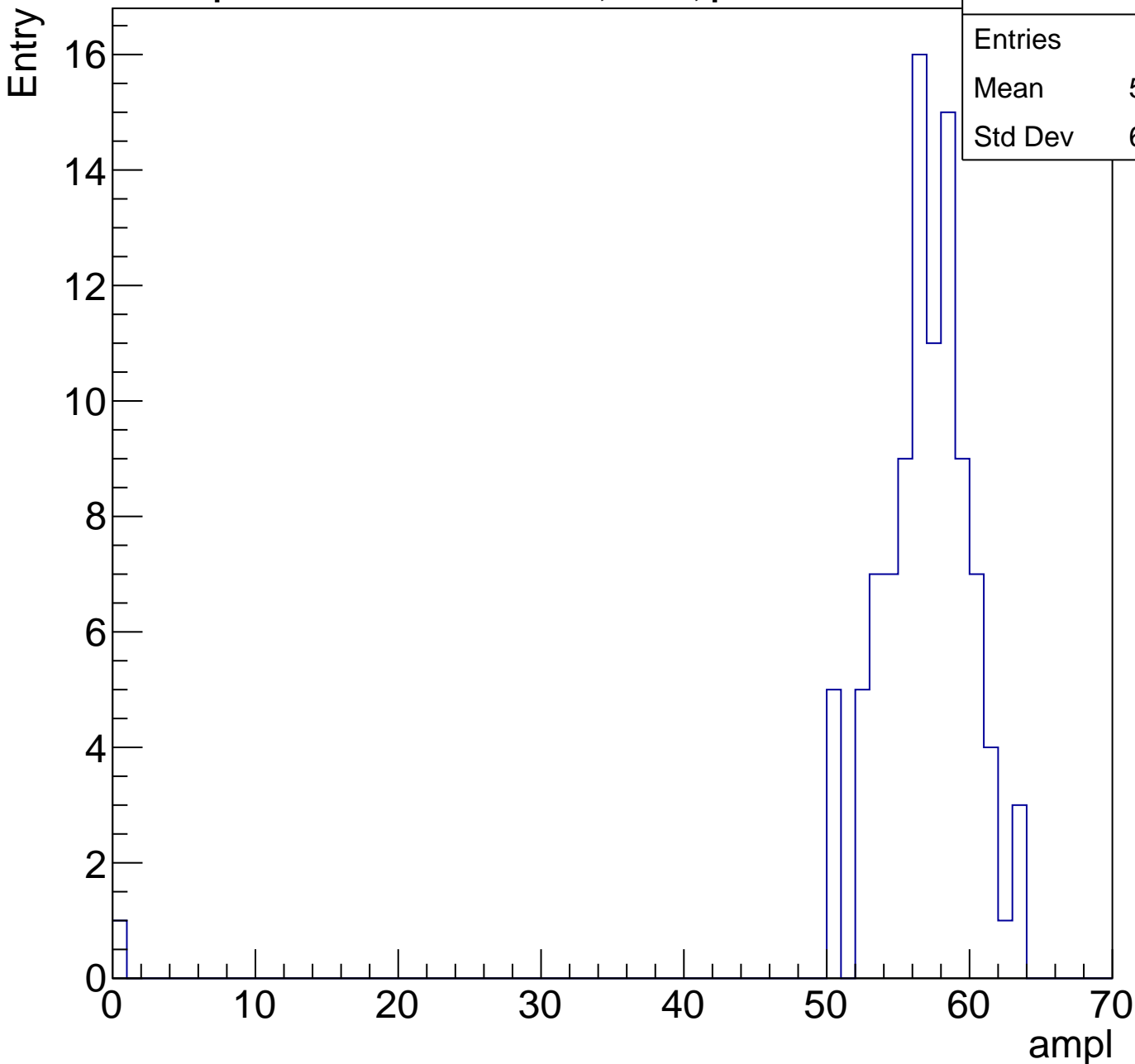
ampl



B0L101S, U25-ch114, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	55.93
Std Dev	6.358



B0L101S, U25-ch114, adc5

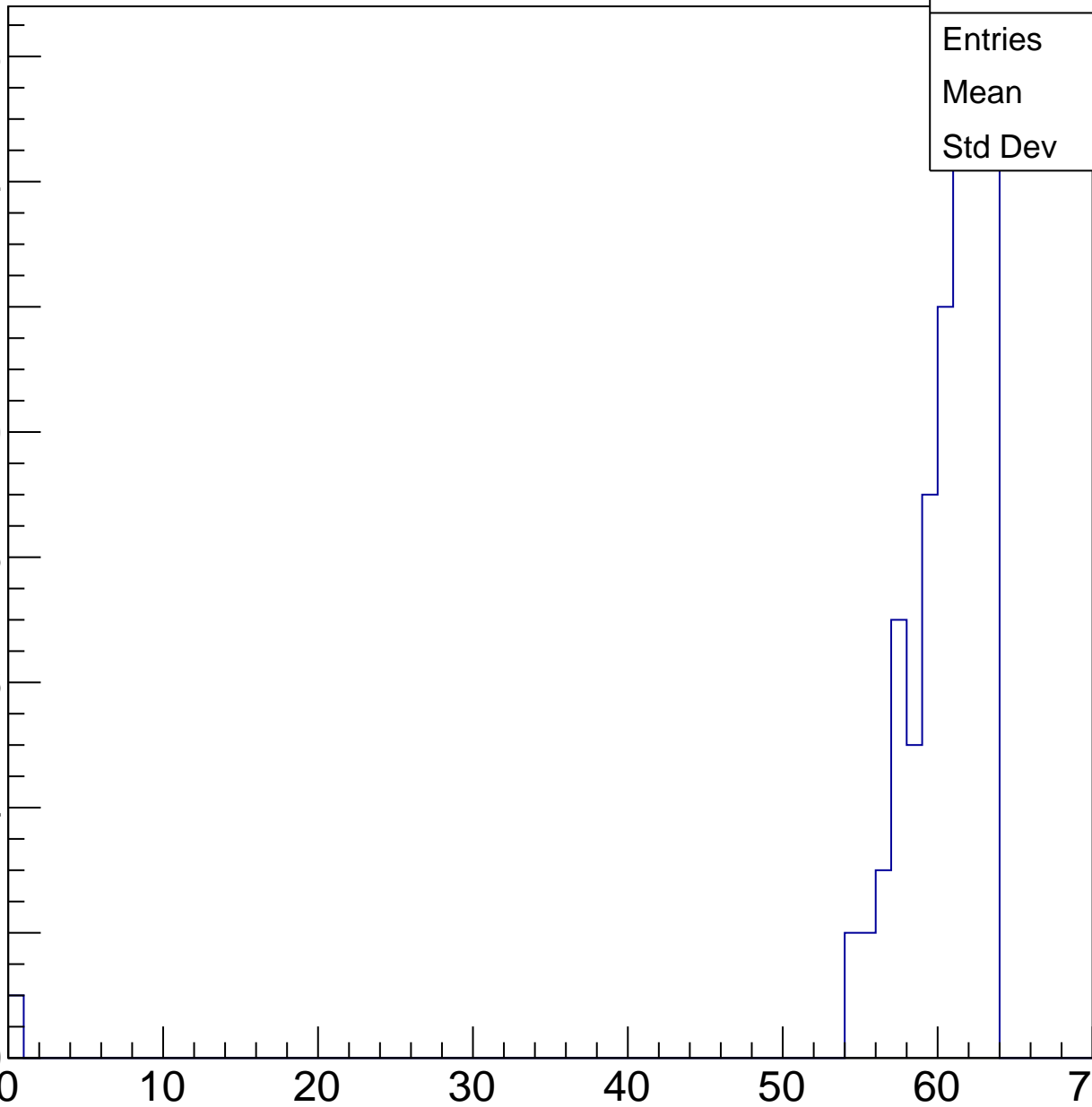
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.53
Std Dev	6.791

ampl



B0L101S, U25-ch114, adc6

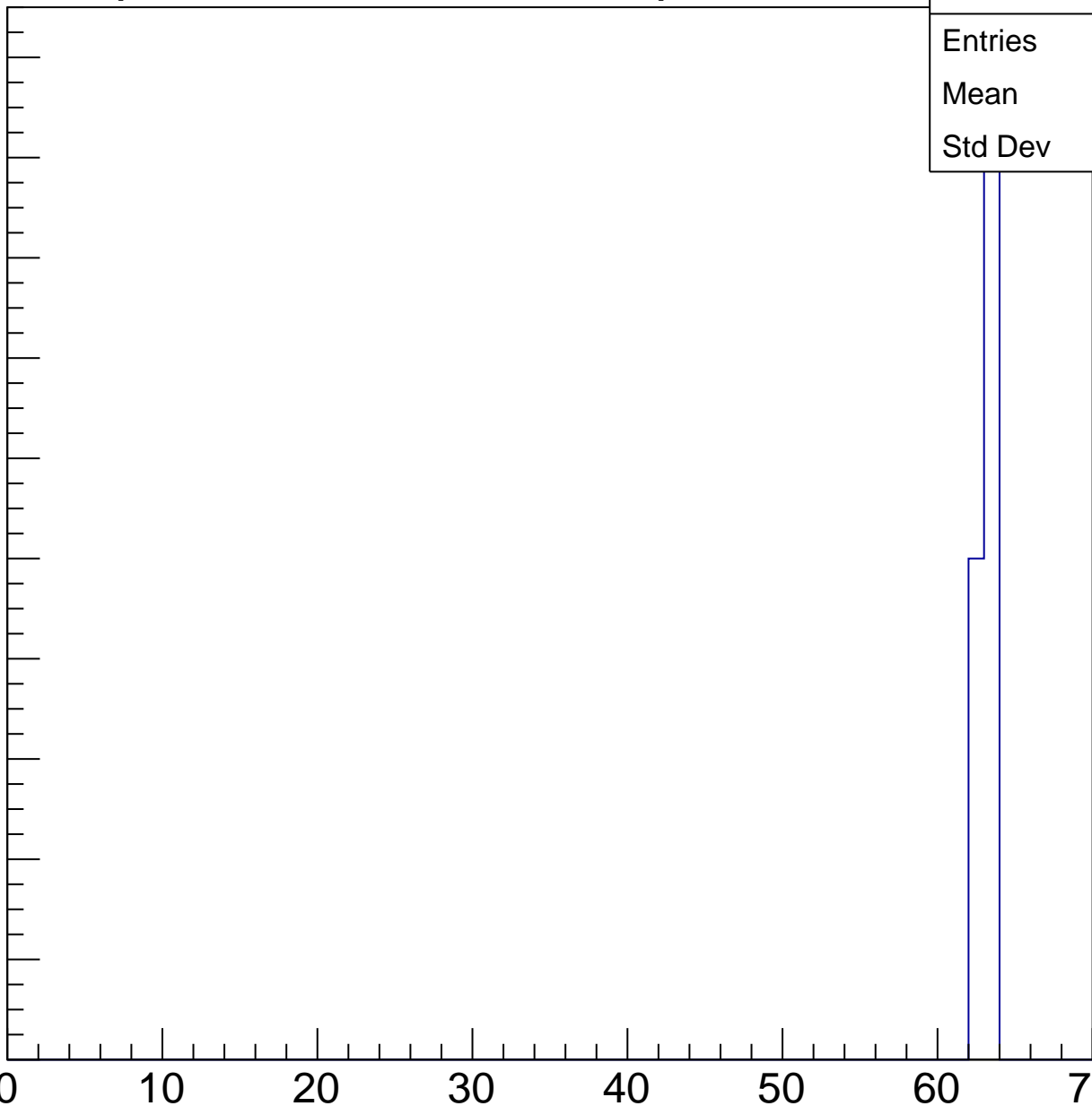
calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B0L101S, U25-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	62
Std Dev	0

B0L101S, U25-ch115, adc0

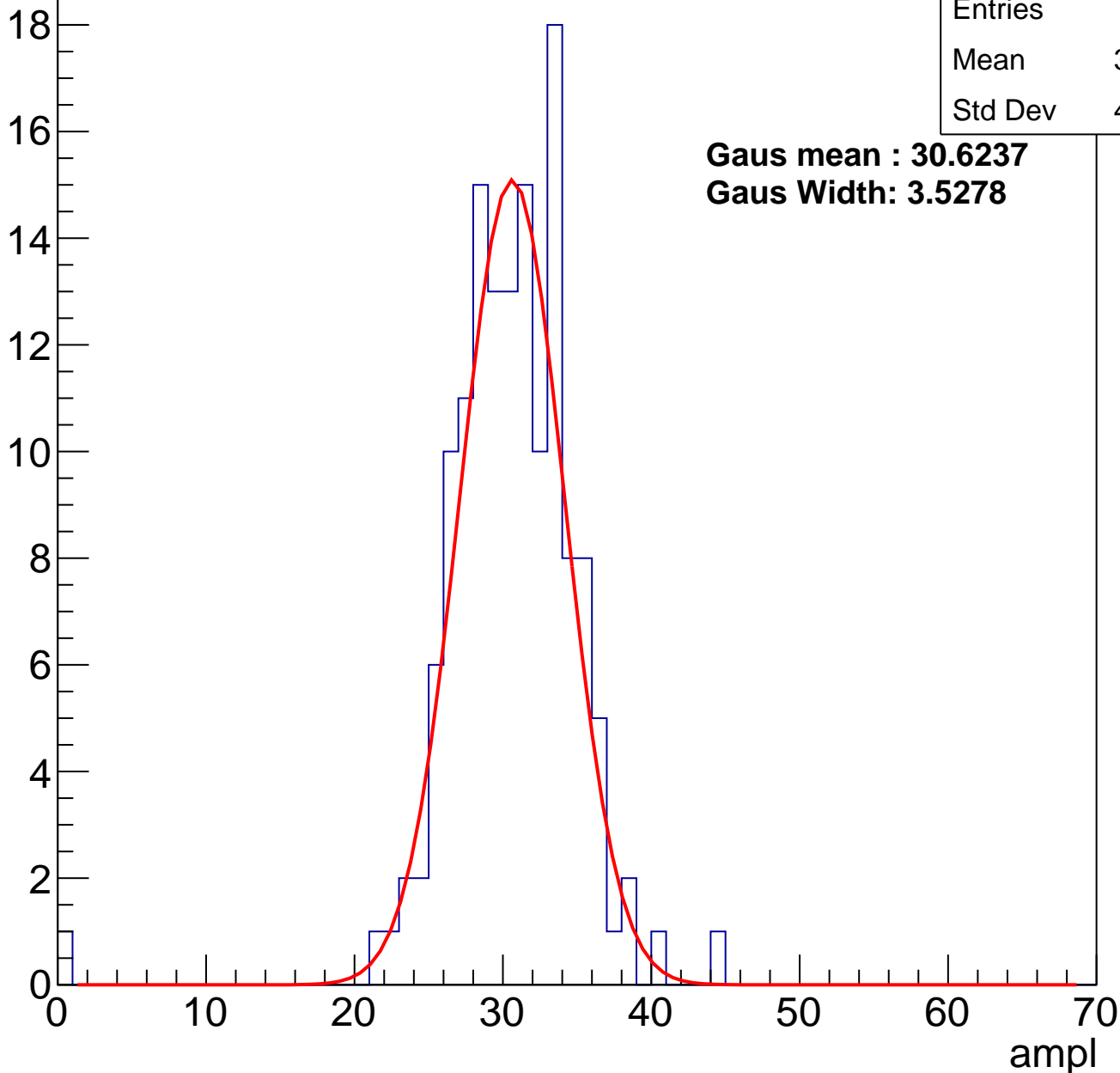
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	30.13
Std Dev	4.482

Gaus mean : 30.6237

Gaus Width: 3.5278

Entry



B0L101S, U25-ch115, adc1

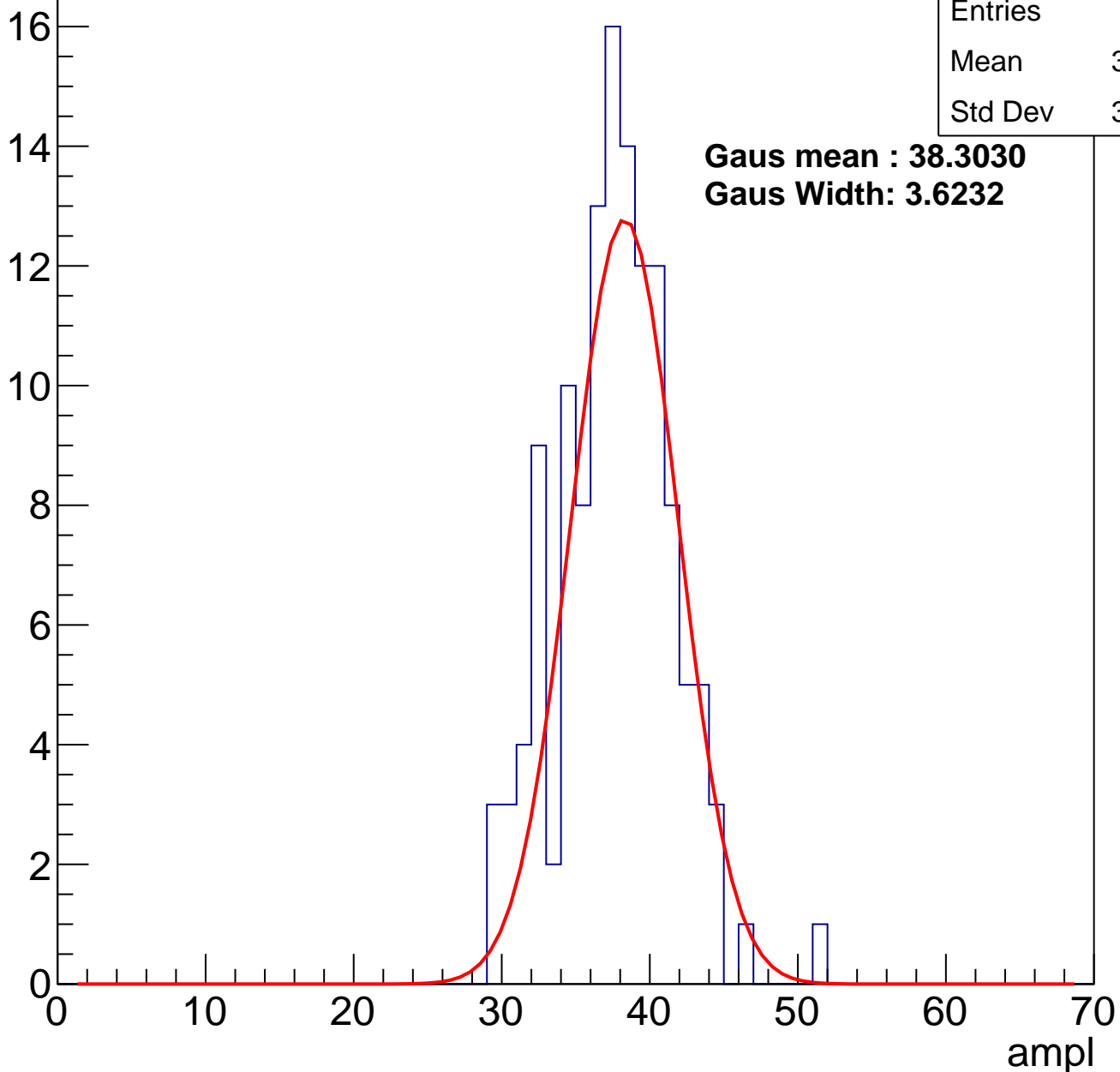
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	37.19
Std Dev	3.835

Gaus mean : 38.3030

Gaus Width: 3.6232

Entry



B0L101S, U25-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

161

Mean

44.22

Std Dev

4.064

Gaus mean : 44.7123

Gaus Width: 4.1516

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

ampl

B0L101S, U25-ch115, adc3

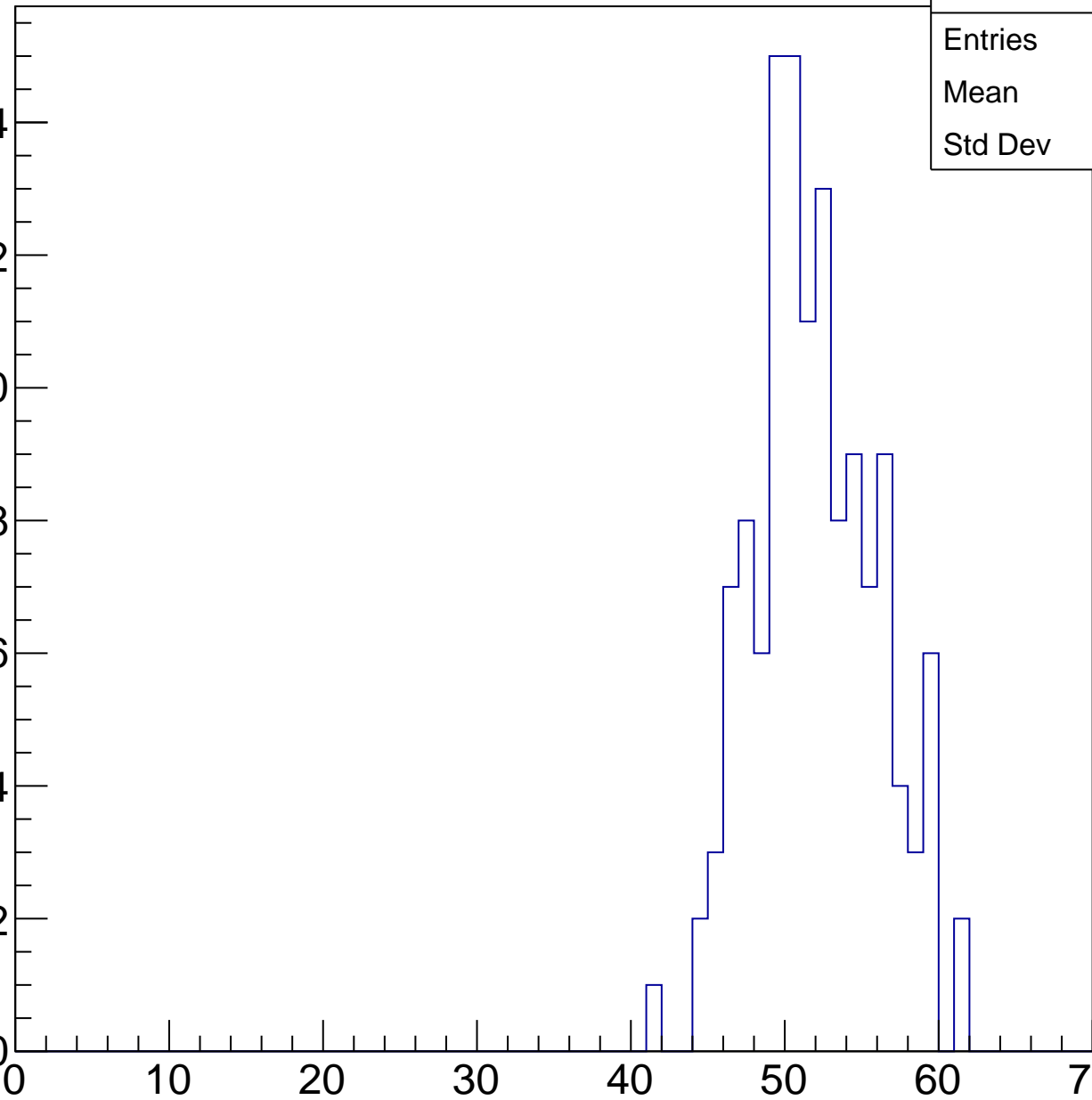
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	51.54
Std Dev	3.975

Entry

14
12
10
8
6
4
2
0

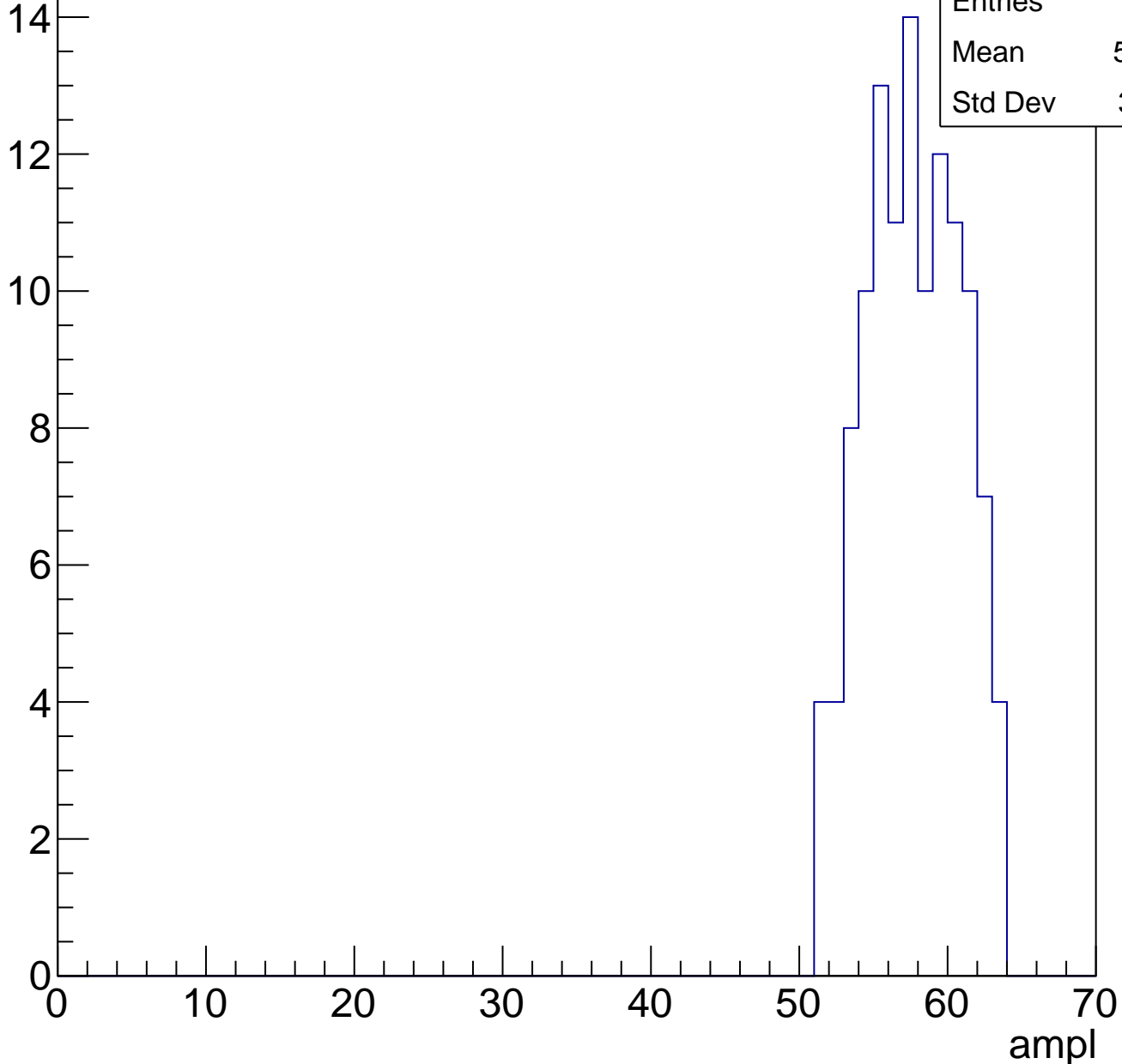
ampl



B0L101S, U25-ch115, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

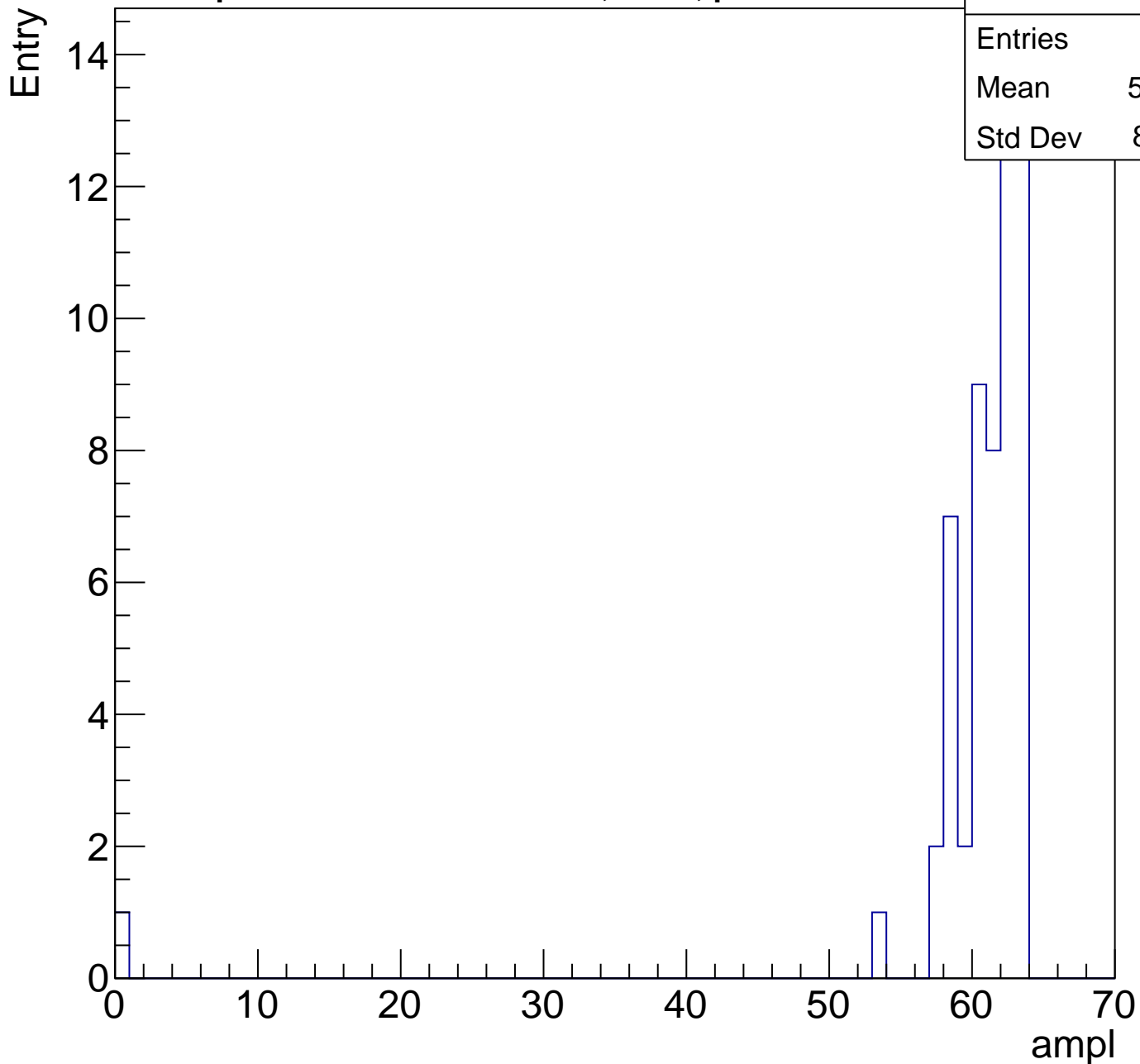


Entries	118
Mean	57.19
Std Dev	3.131

B0L101S, U25-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	57
Mean	59.77
Std Dev	8.251



B0L101S, U25-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch116, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	29.27
Std Dev	5.405

Gaus mean : 30.3788

Gaus Width: 4.4686

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

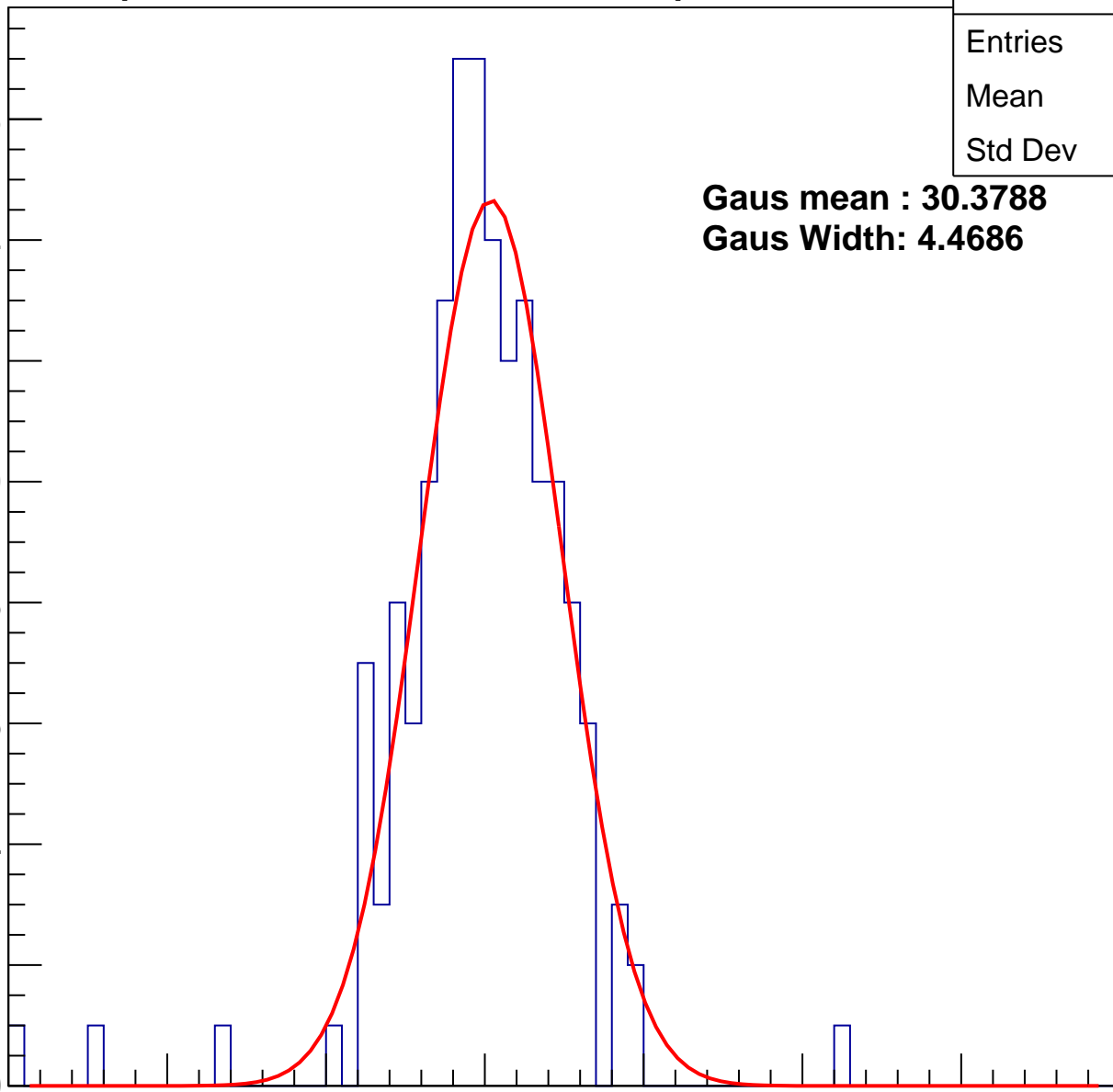
40

50

60

70

ampl



B0L101S, U25-ch116, adc1

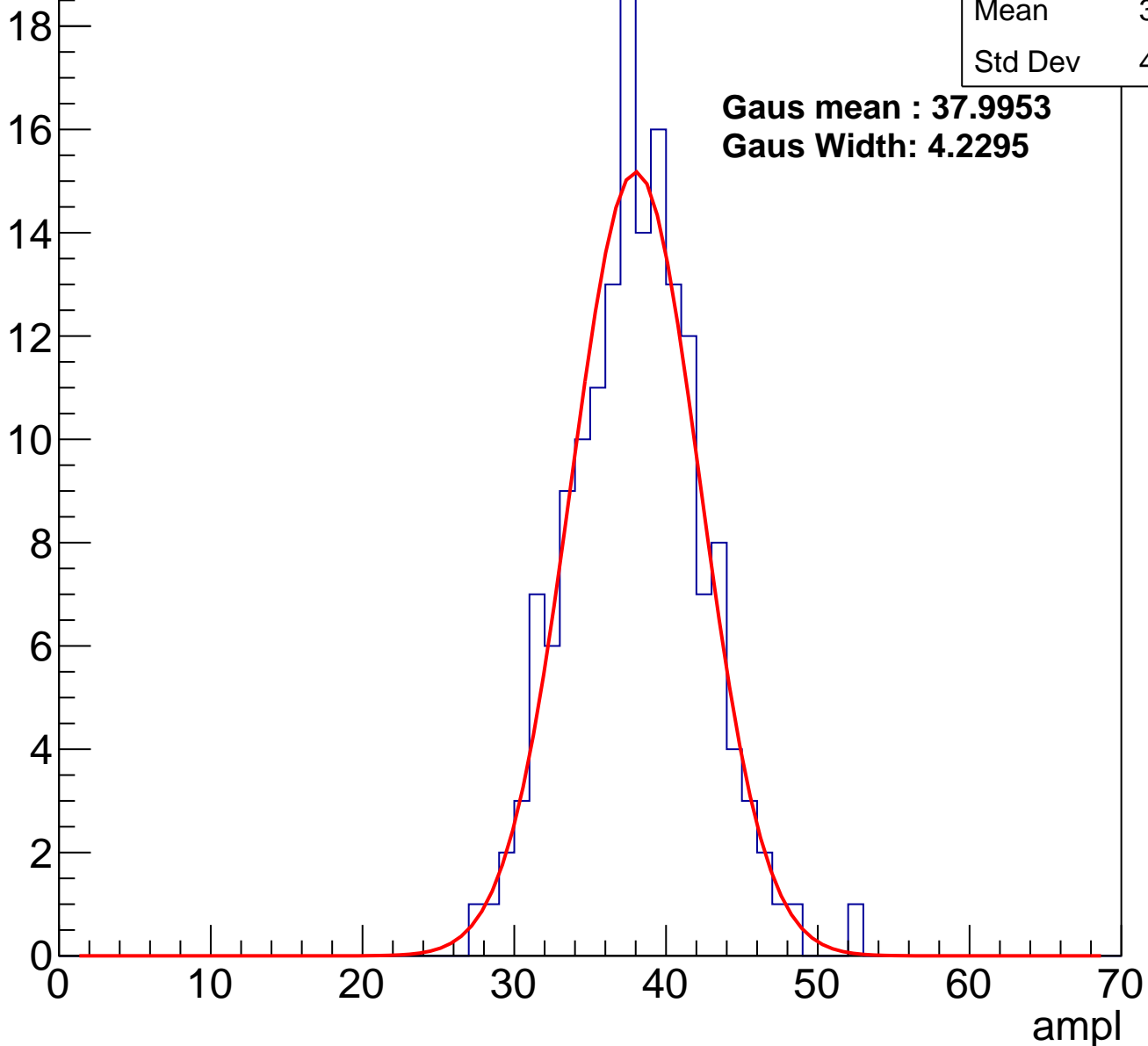
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	37.57
Std Dev	4.236

Gaus mean : 37.9953

Gaus Width: 4.2295

Entry



B0L101S, U25-ch116, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

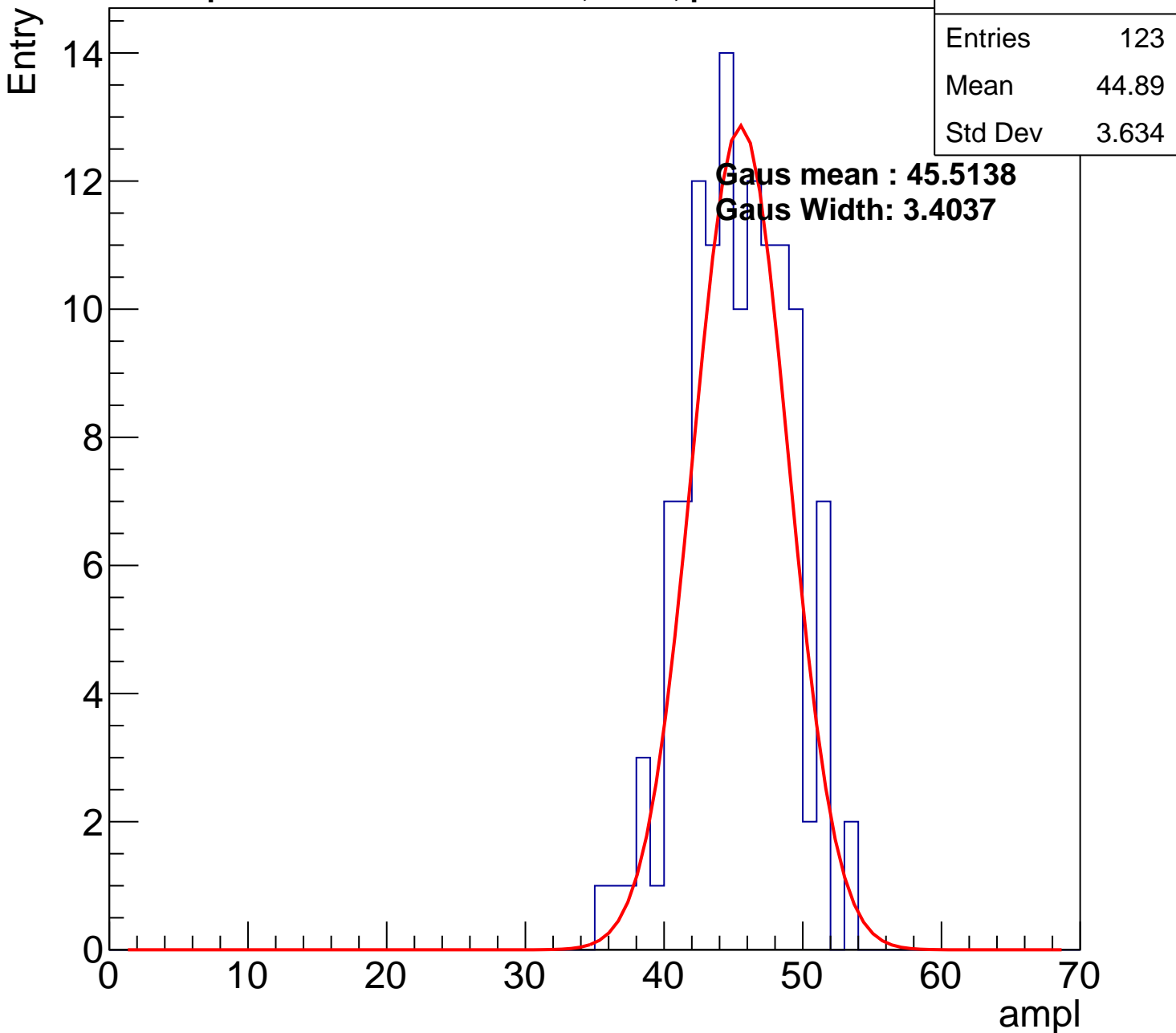
14
12
10
8
6
4
2
0

Entries	123
Mean	44.89
Std Dev	3.634

Gaus mean : 45.5138
Gaus Width: 3.4037

ampl

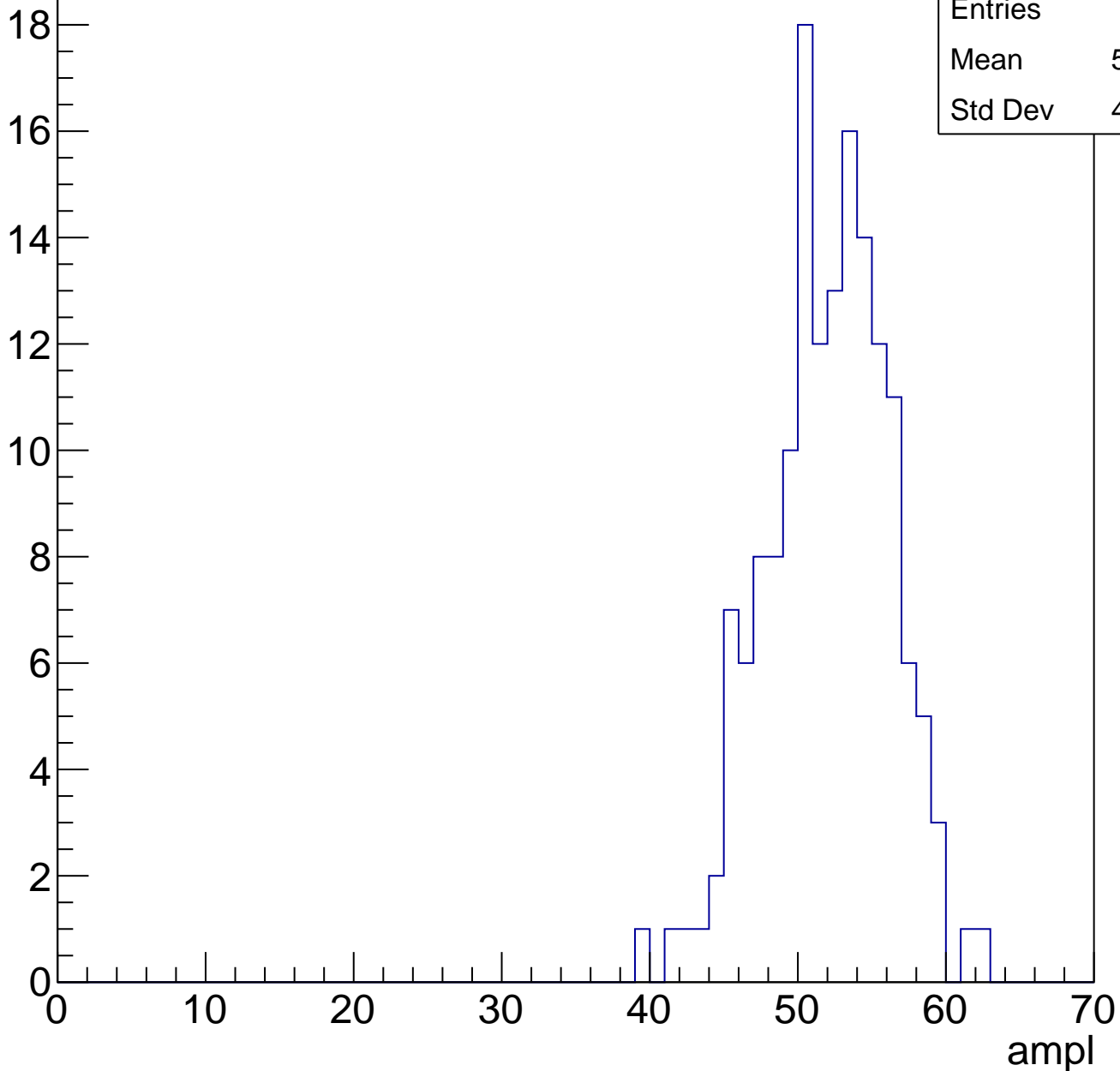
0 10 20 30 40 50 60 70



B0L101S, U25-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	56.75
Std Dev	8.528

Entry

12

10

8

6

4

2

0

0

10

20

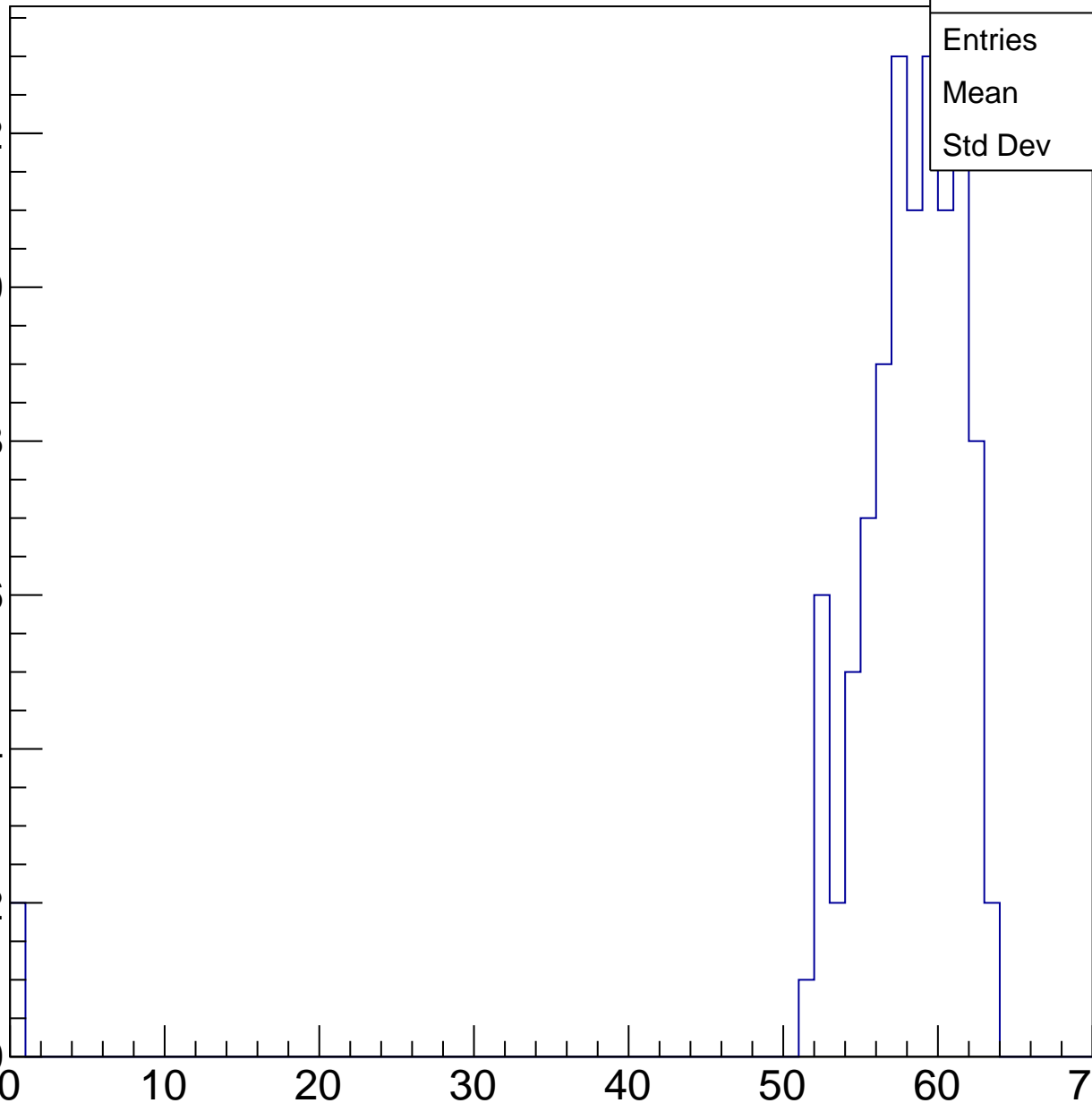
30

40

50

60

ampl



B0L101S, U25-ch116, adc5

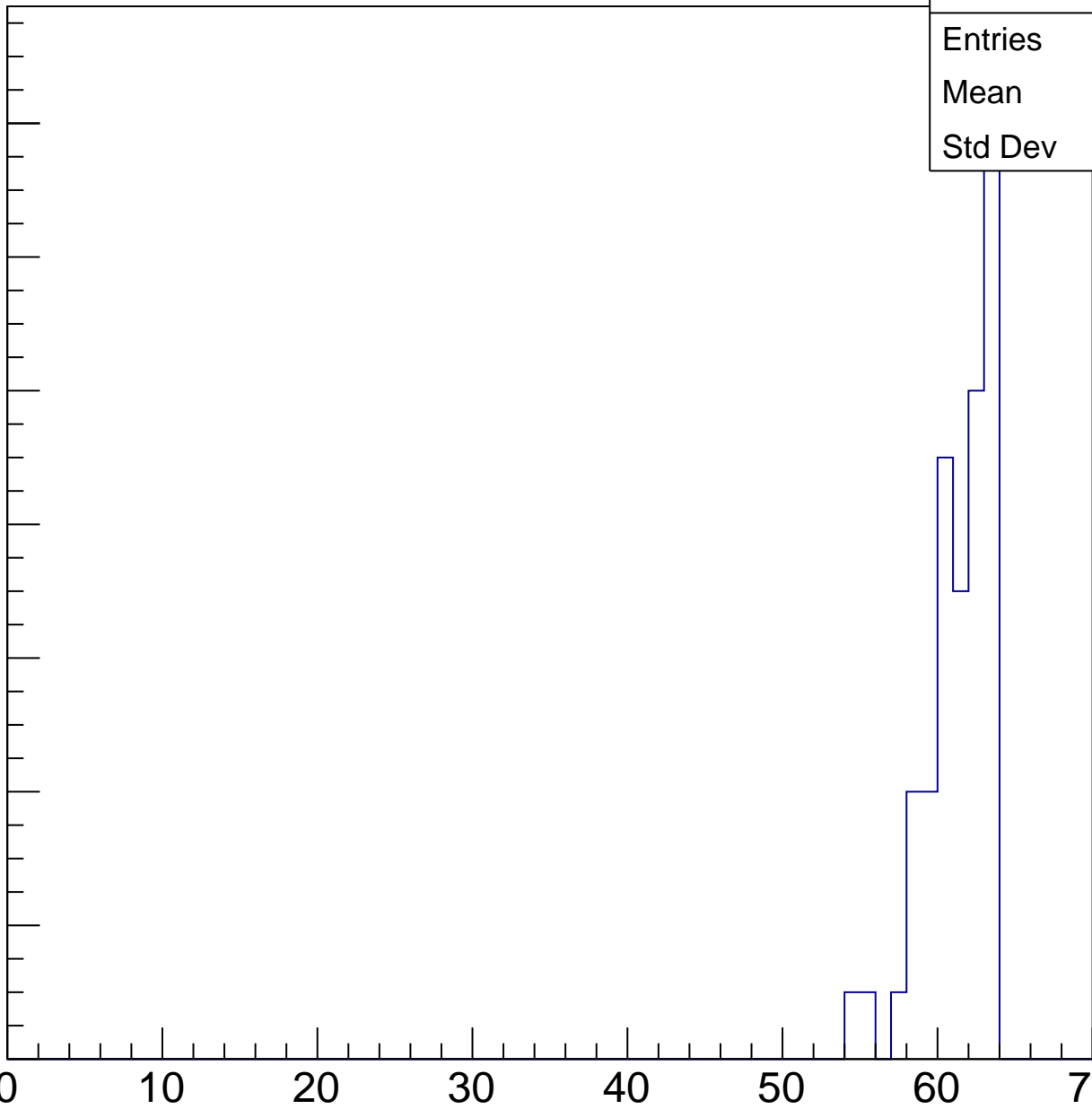
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	52
Mean	60.88
Std Dev	2.109

ampl



B0L101S, U25-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

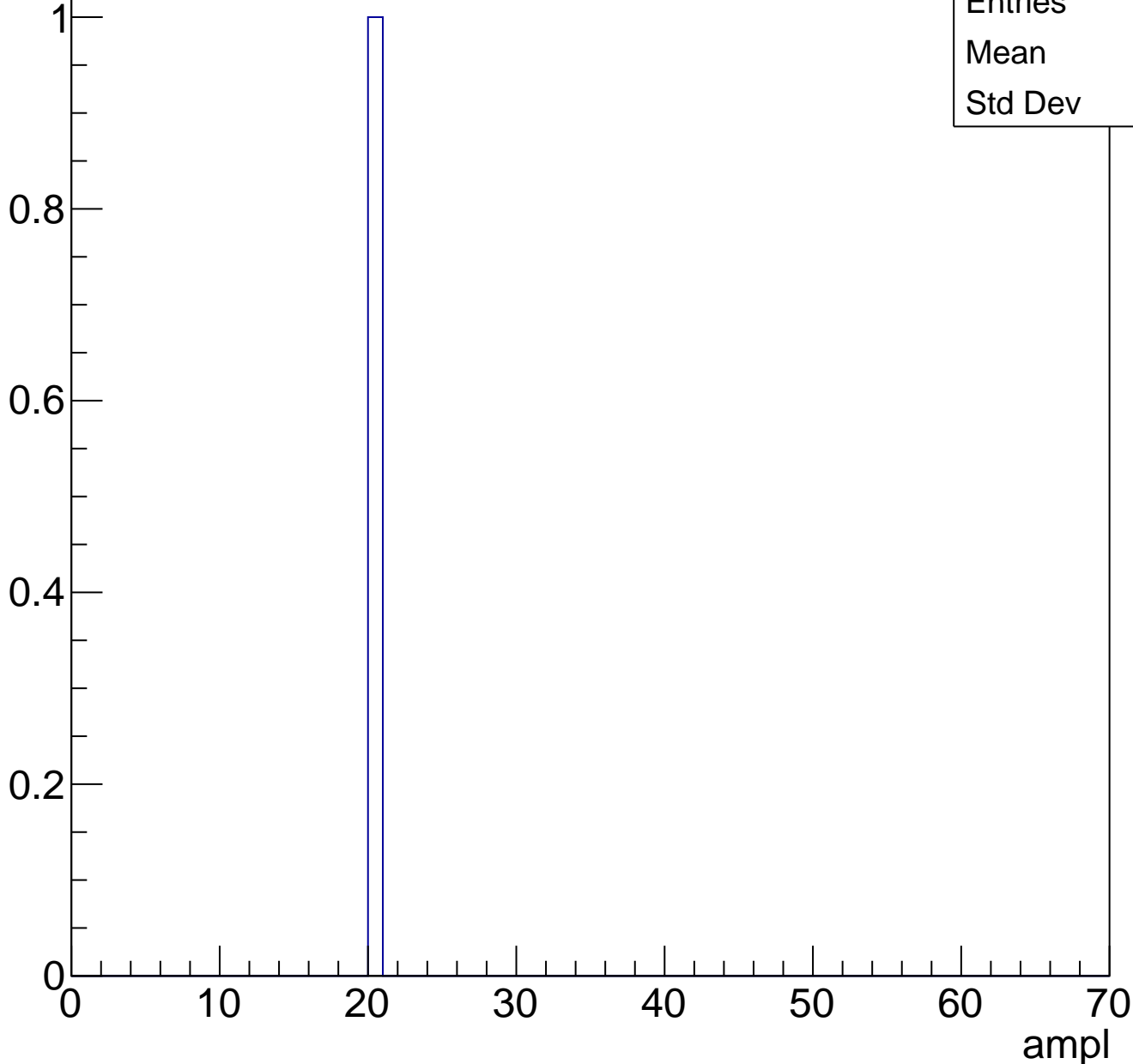
Entry



B0L101S, U25-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch117, adc0

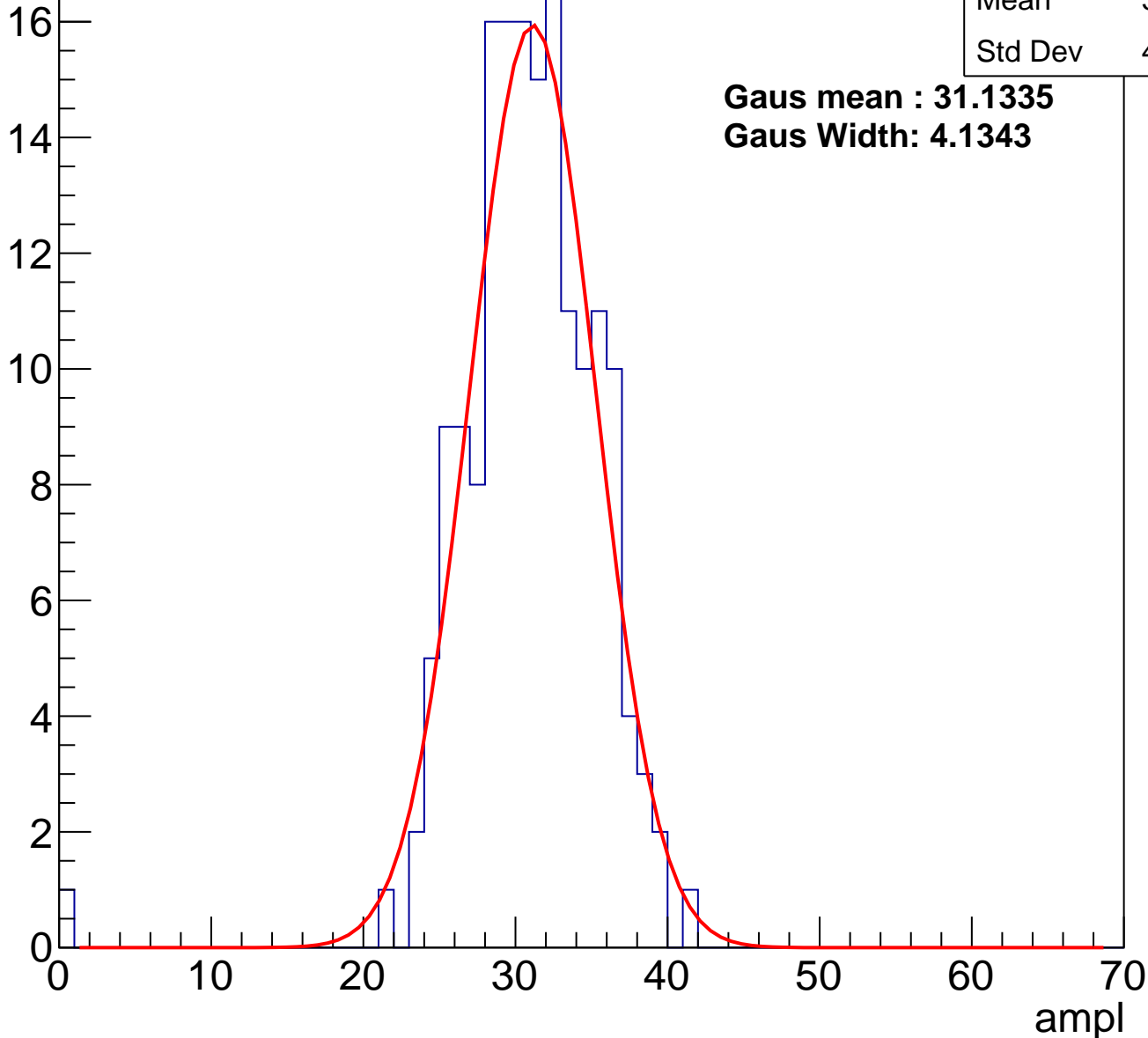
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	30.49
Std Dev	4.504

Gaus mean : 31.1335

Gaus Width: 4.1343

Entry



B0L101S, U25-ch117, adc1

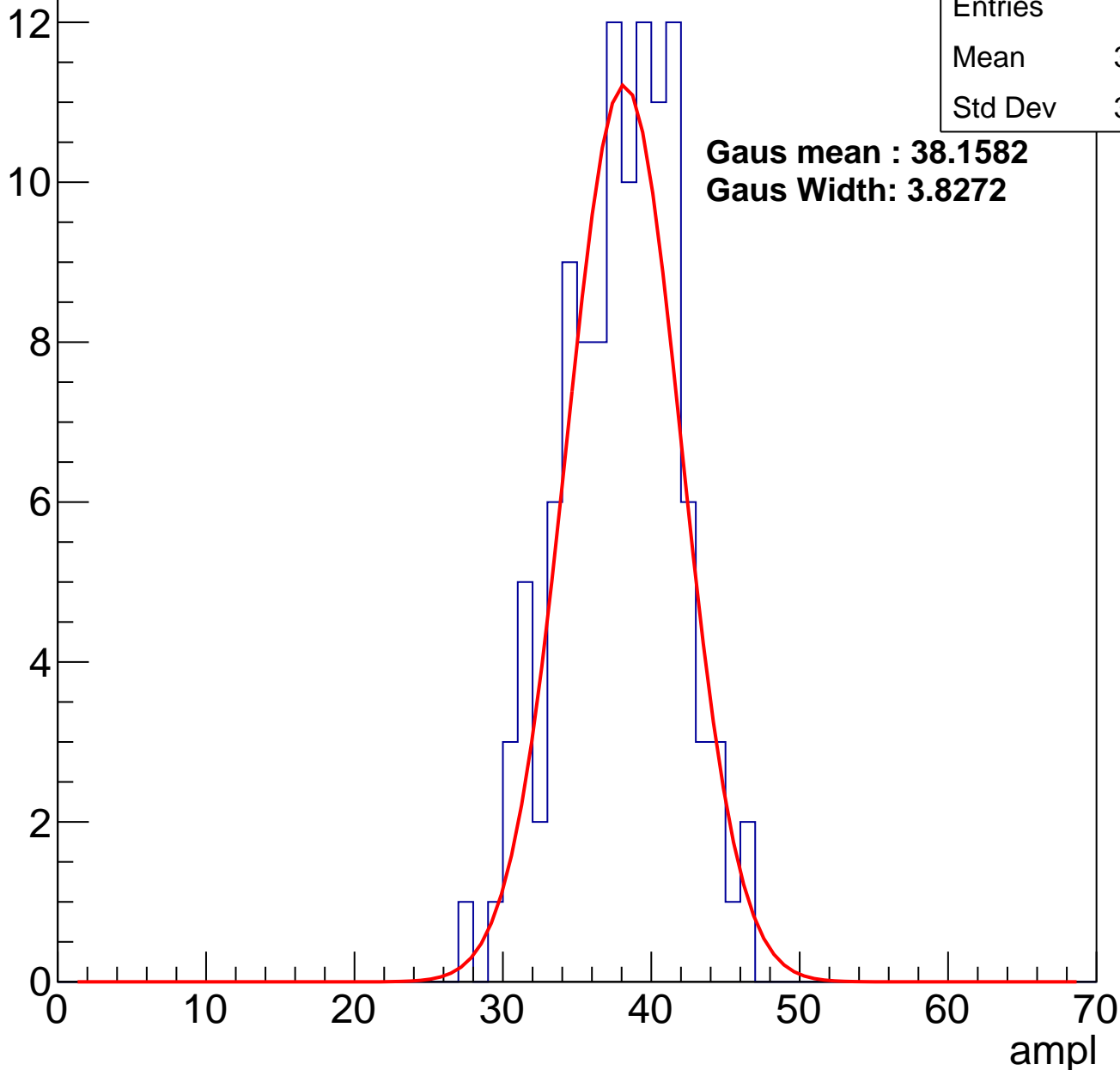
calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	37.49
Std Dev	3.857

Gaus mean : 38.1582

Gaus Width: 3.8272

Entry



B0L101S, U25-ch117, adc2

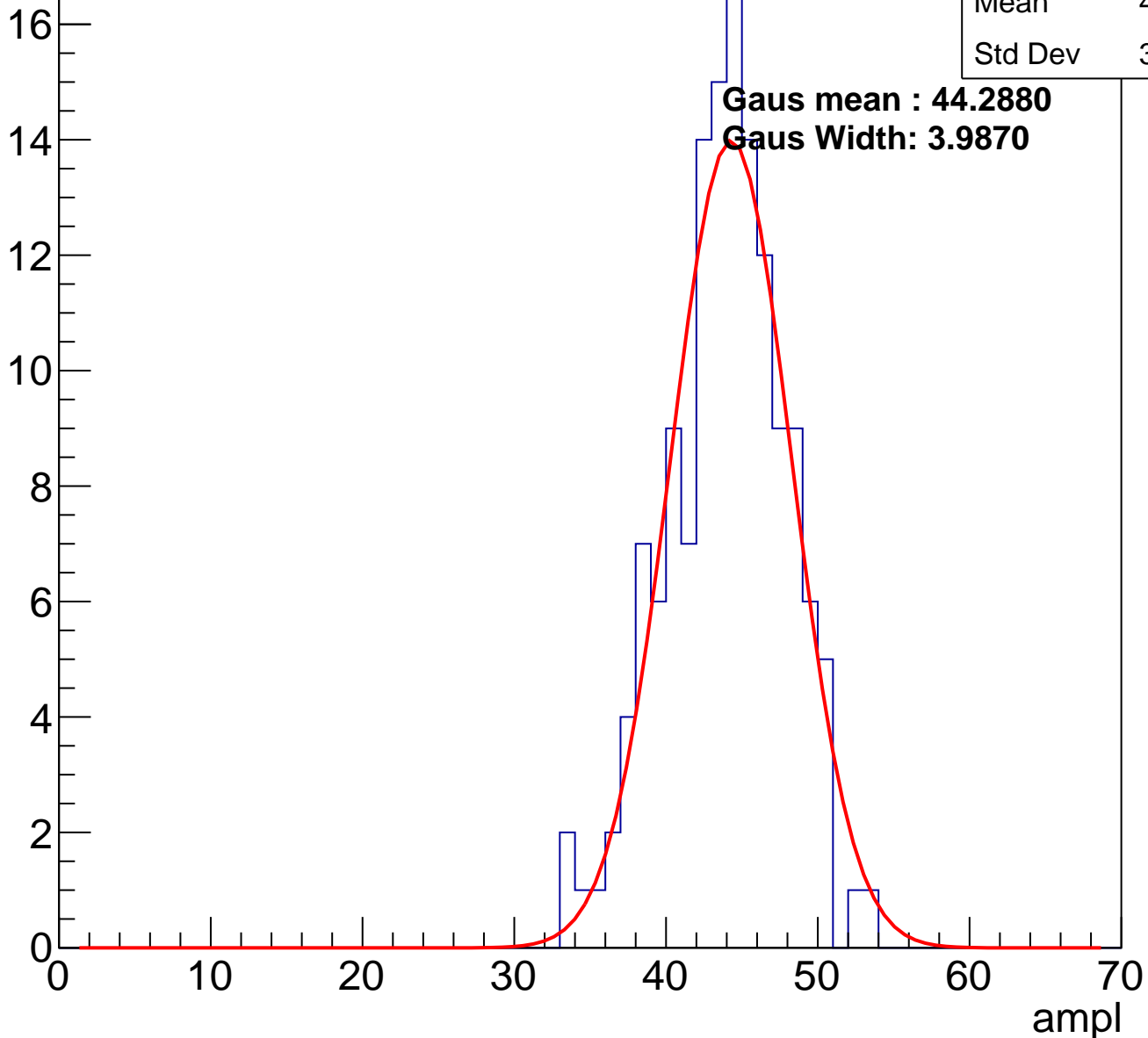
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	142
Mean	43.44
Std Dev	3.887

Gaus mean : 44.2880

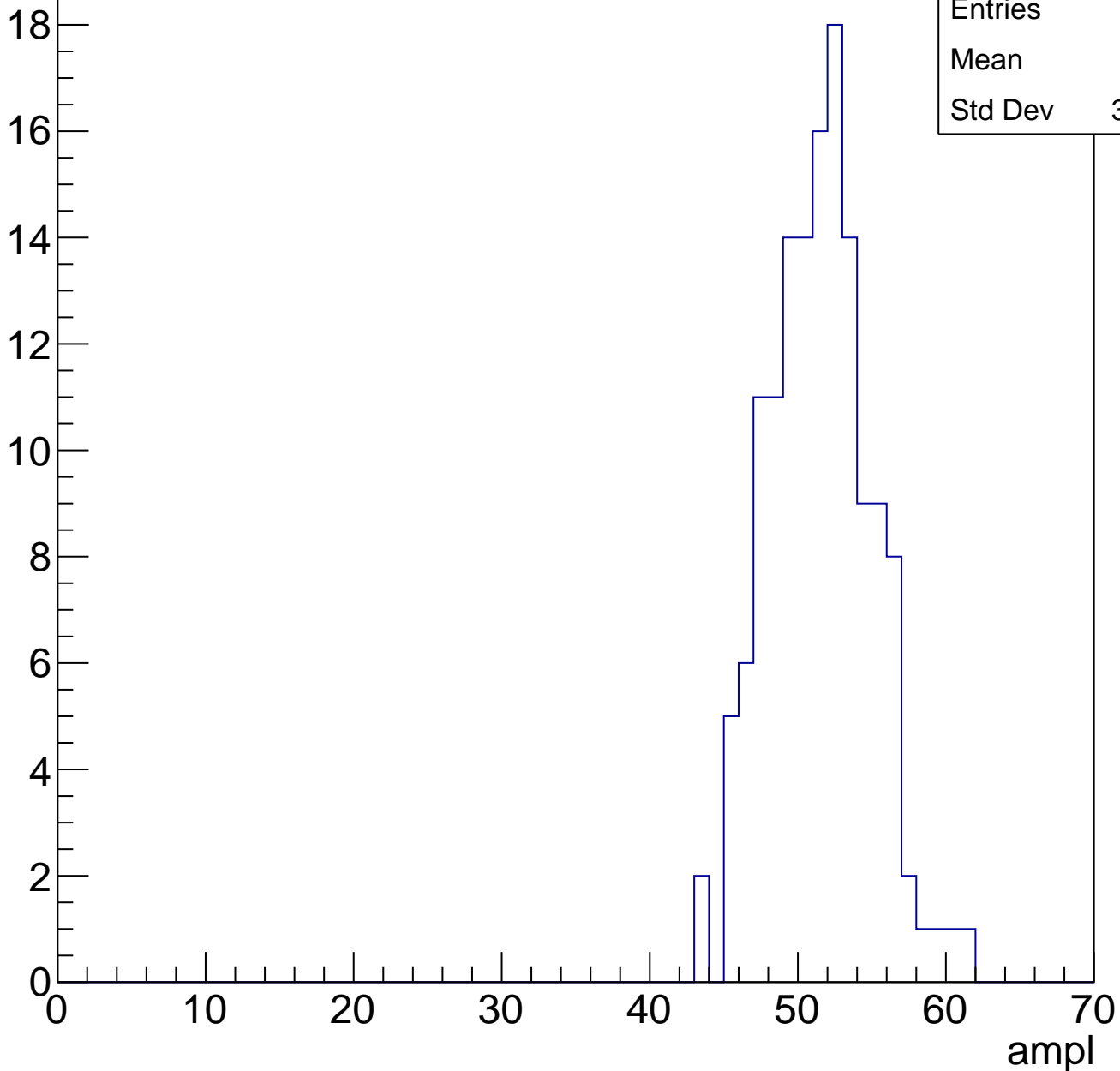
Gaus Width: 3.9870



B0L101S, U25-ch117, adc3

calib_packv5_042523_0143.root, FC#1, port C1

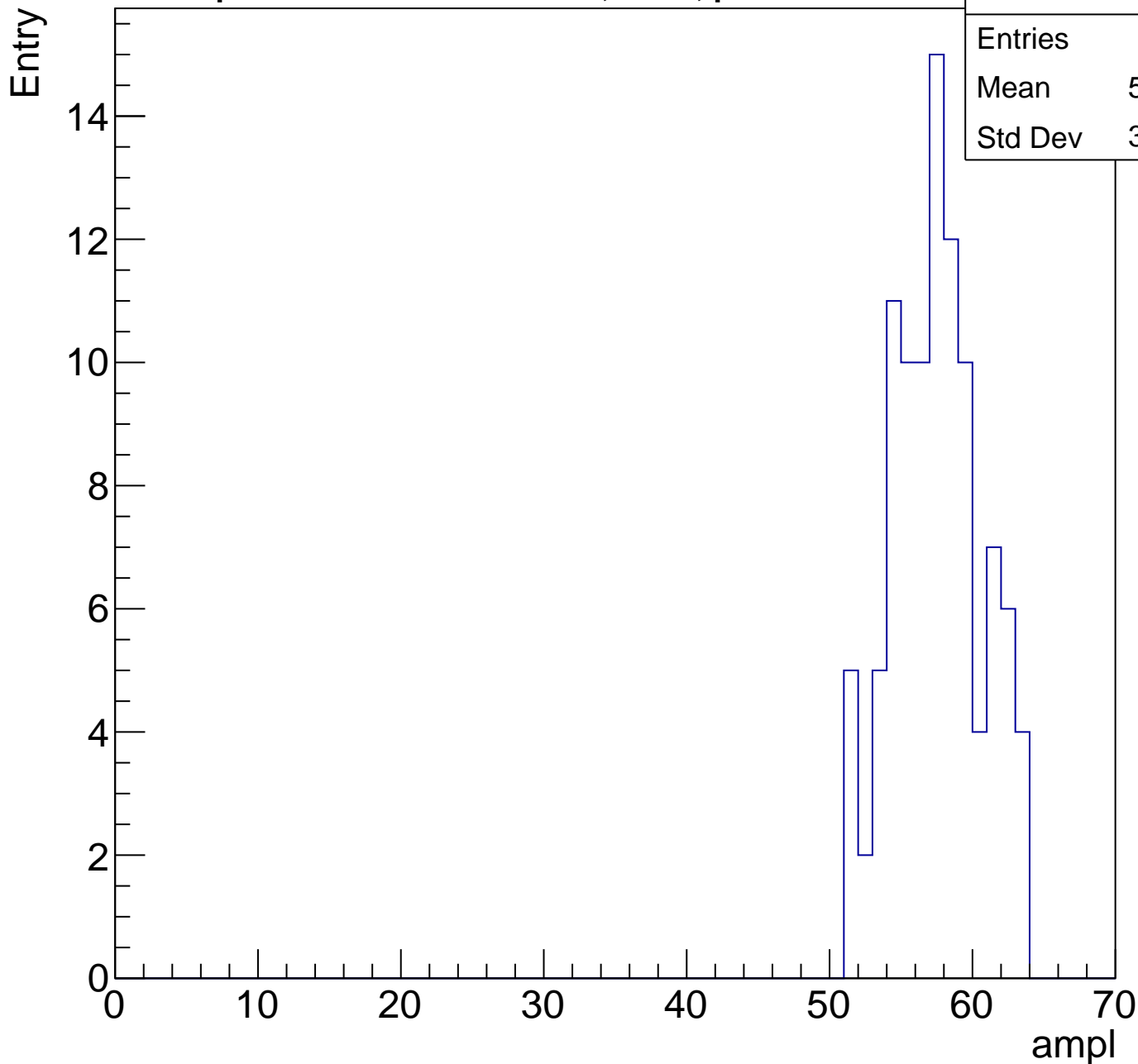
Entry



B0L101S, U25-ch117, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	57.03
Std Dev	3.072



B0L101S, U25-ch117, adc5

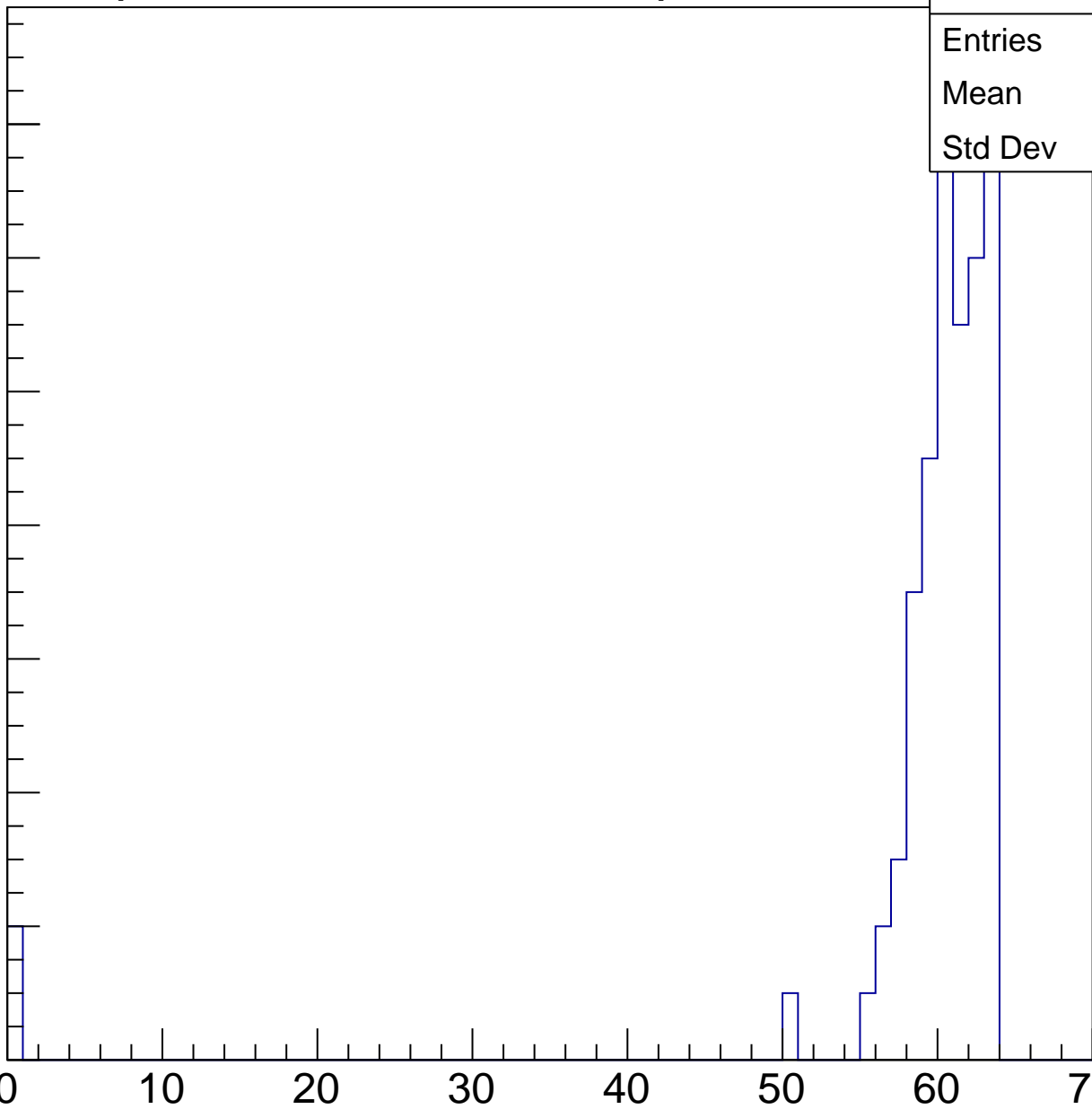
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	58.73
Std Dev	9.857

ampl



B0L101S, U25-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch118, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	29.62
Std Dev	4.802

Gaus mean : 30.3620

Gaus Width: 4.4023

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

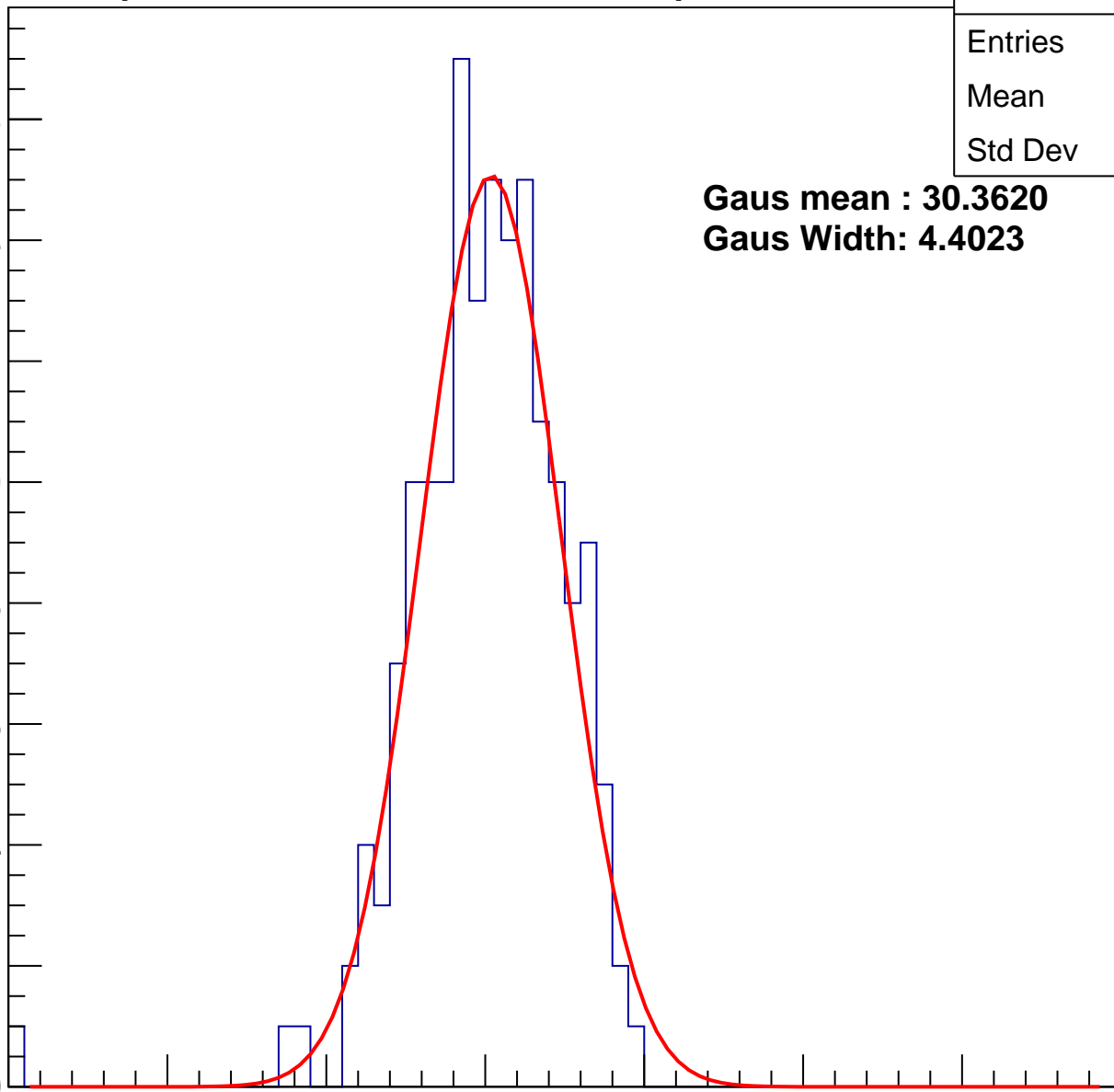
40

50

60

70

ampl



B0L101S, U25-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

132

Mean

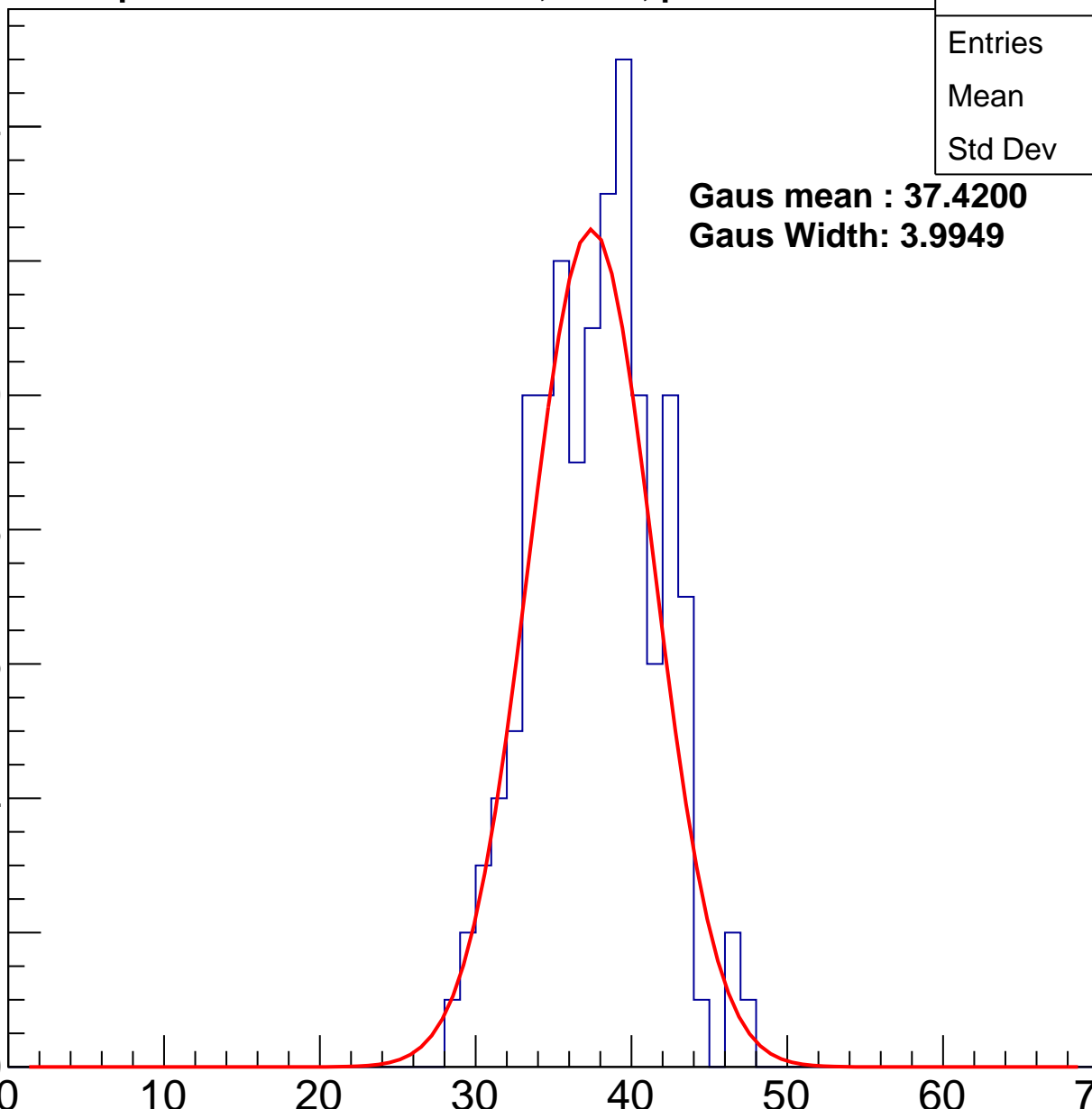
37.2

Std Dev

3.891

Gaus mean : 37.4200

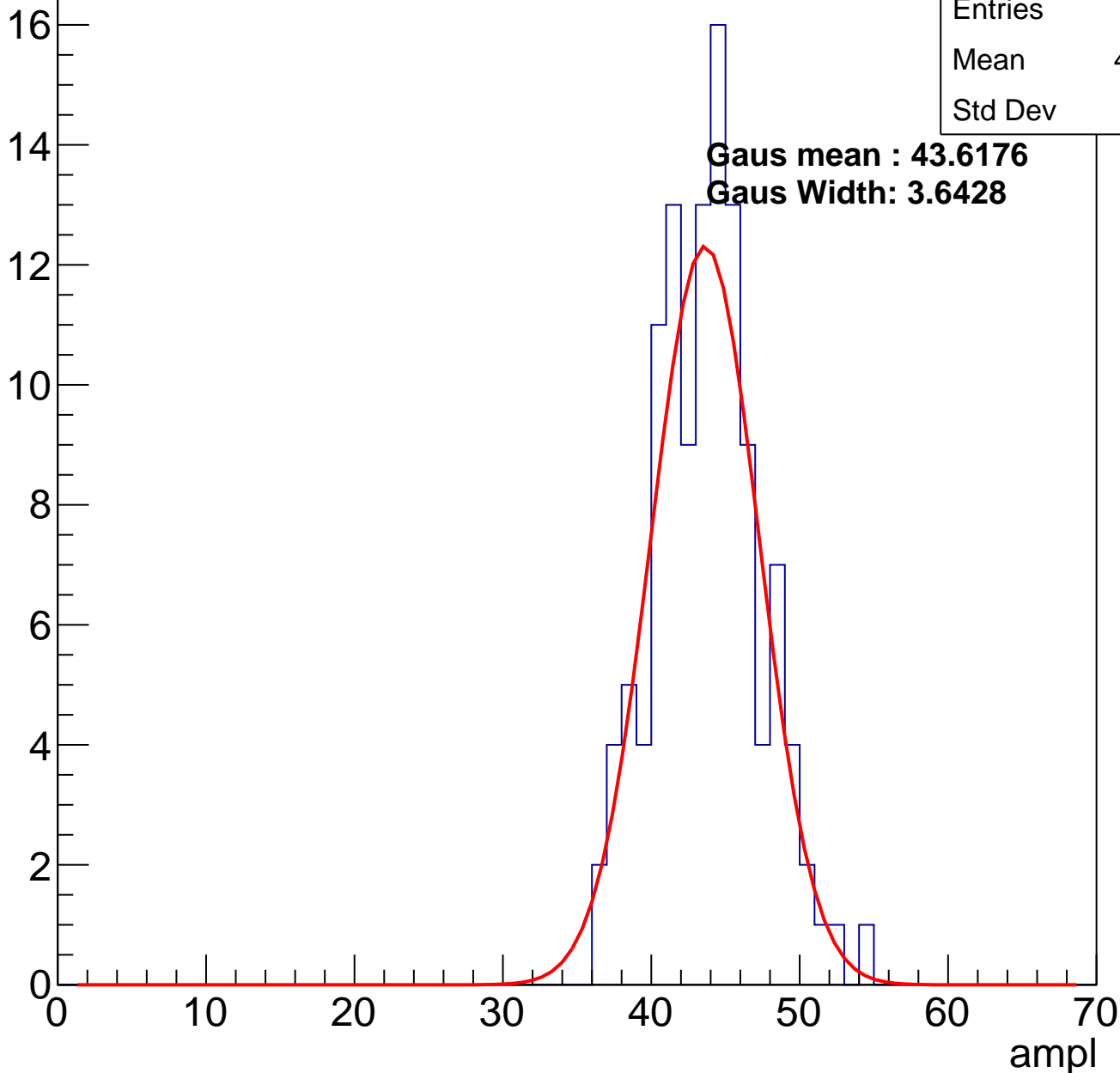
Gaus Width: 3.9949



B0L101S, U25-ch118, adc2

calib_packv5_042523_0143.root, FC#1, port C1

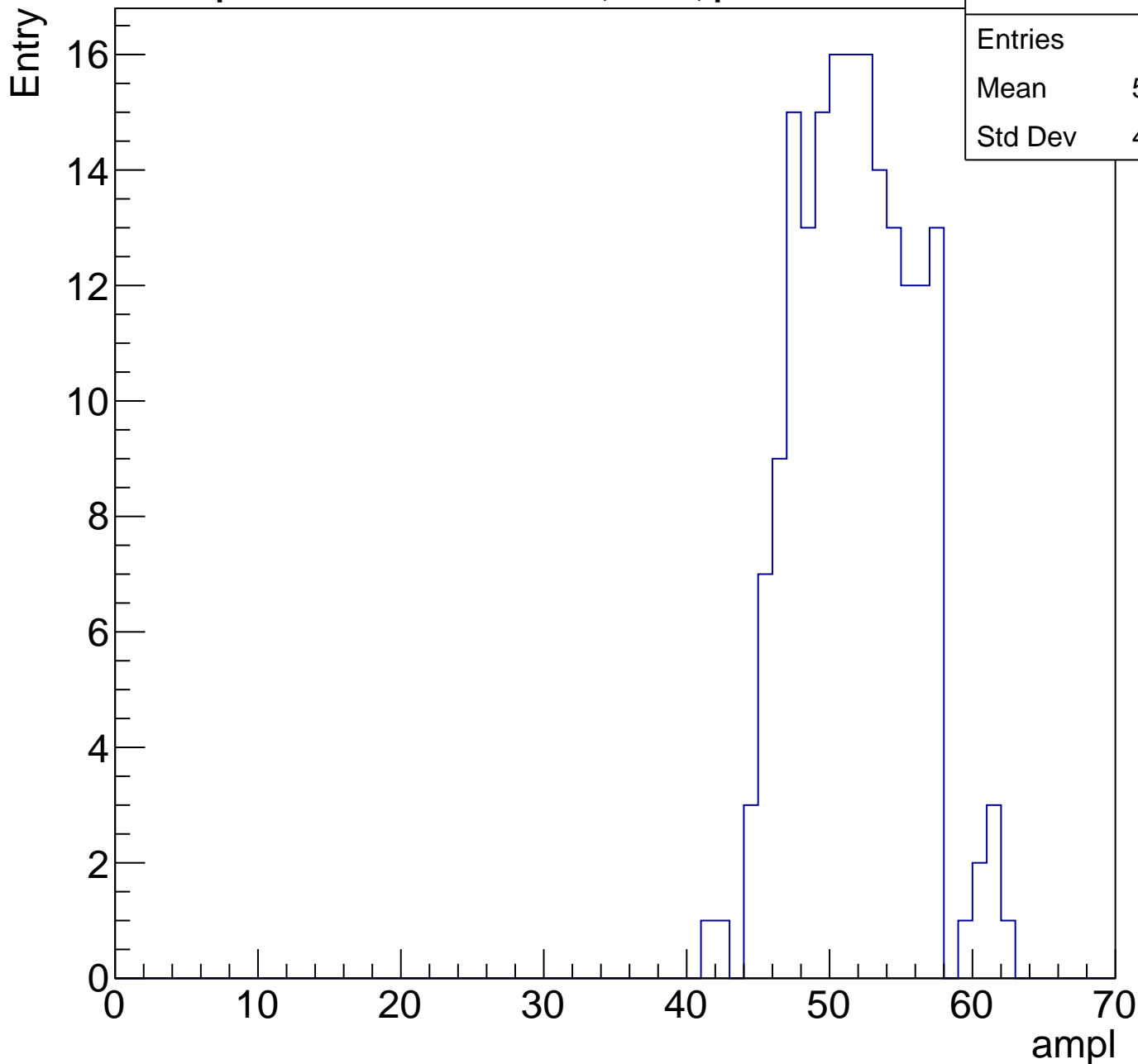
Entry



B0L101S, U25-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

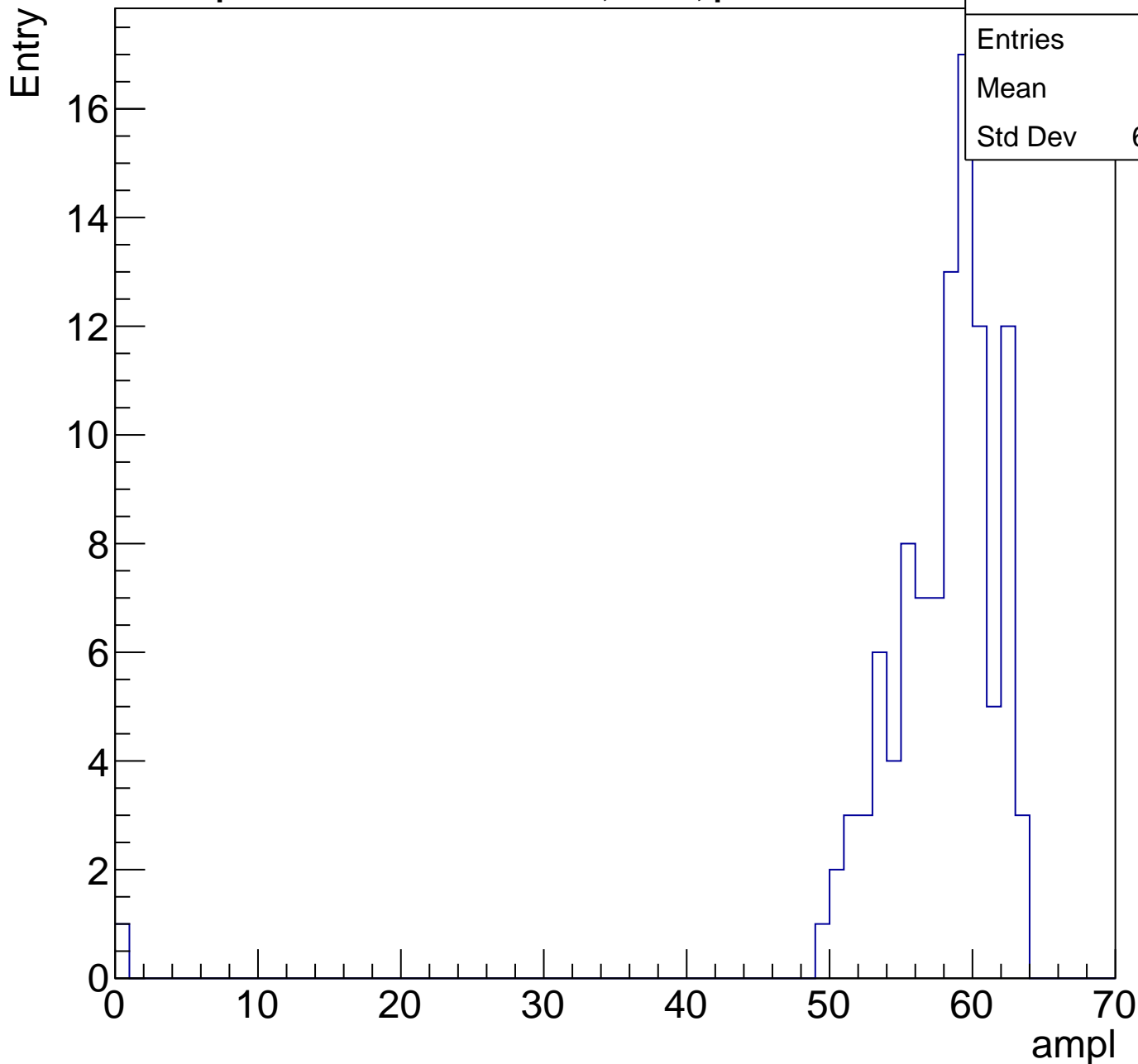
Entries	183
Mean	51.35
Std Dev	4.057



B0L101S, U25-ch118, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	57.1
Std Dev	6.535



B0L101S, U25-ch118, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	52
Mean	60.79
Std Dev	2.273

ampl

0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B0L101S, U25-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch119, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	31.02
Std Dev	5.486

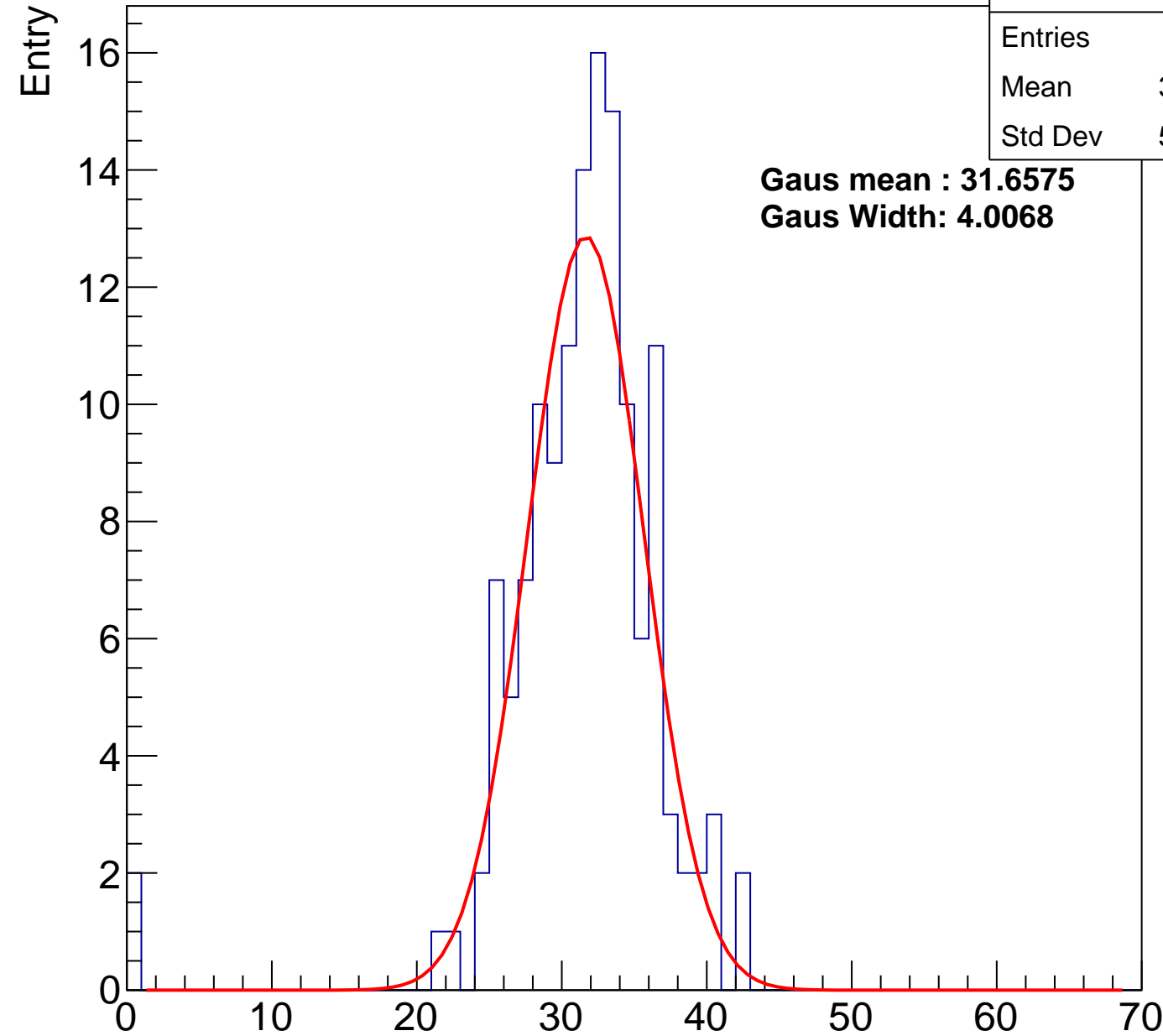
Gaus mean : 31.6575

Gaus Width: 4.0068

Entry

16
14
12
10
8
6
4
2
0

ampl



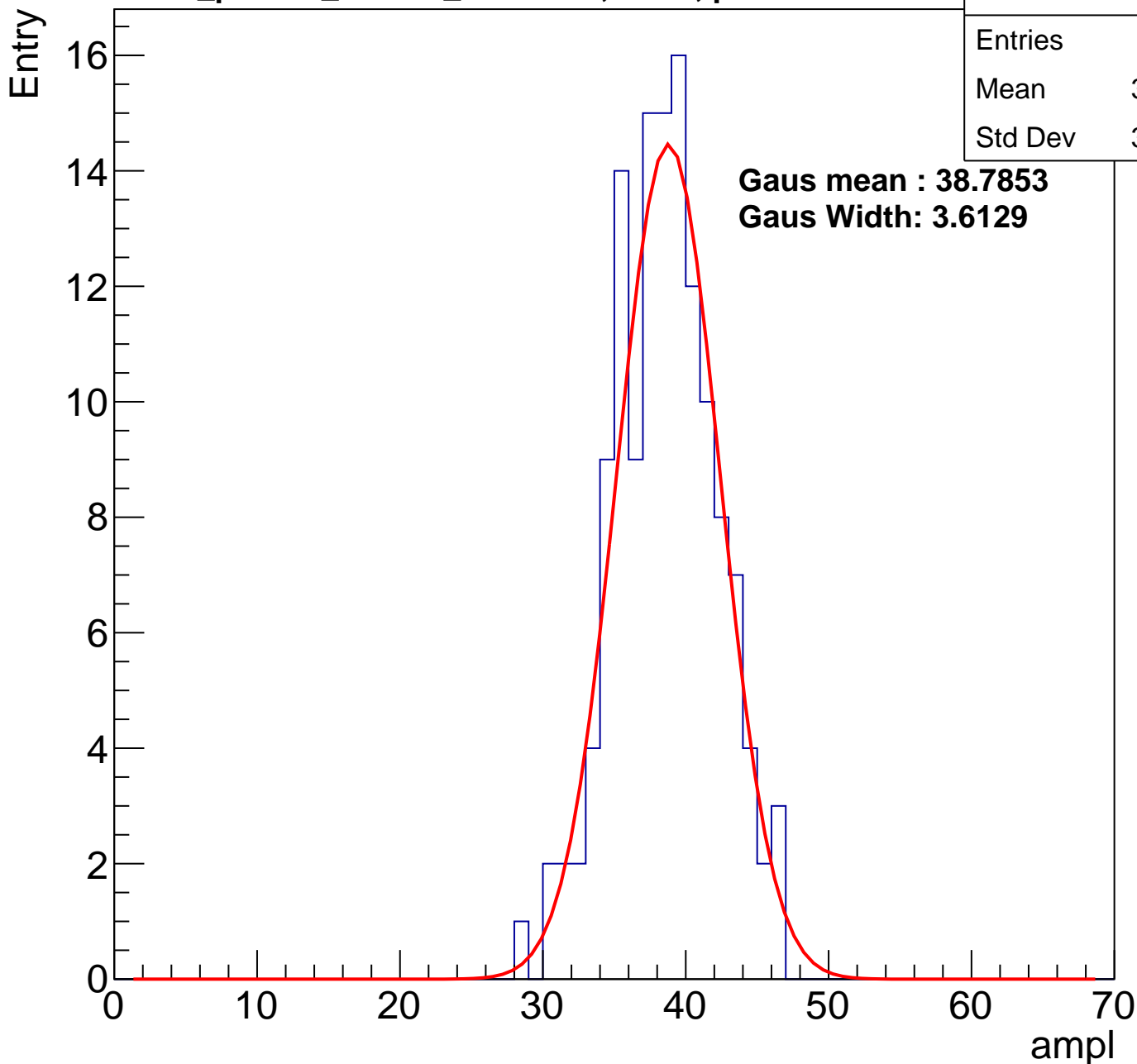
B0L101S, U25-ch119, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	38.12
Std Dev	3.557

Gaus mean : 38.7853

Gaus Width: 3.6129



B0L101S, U25-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

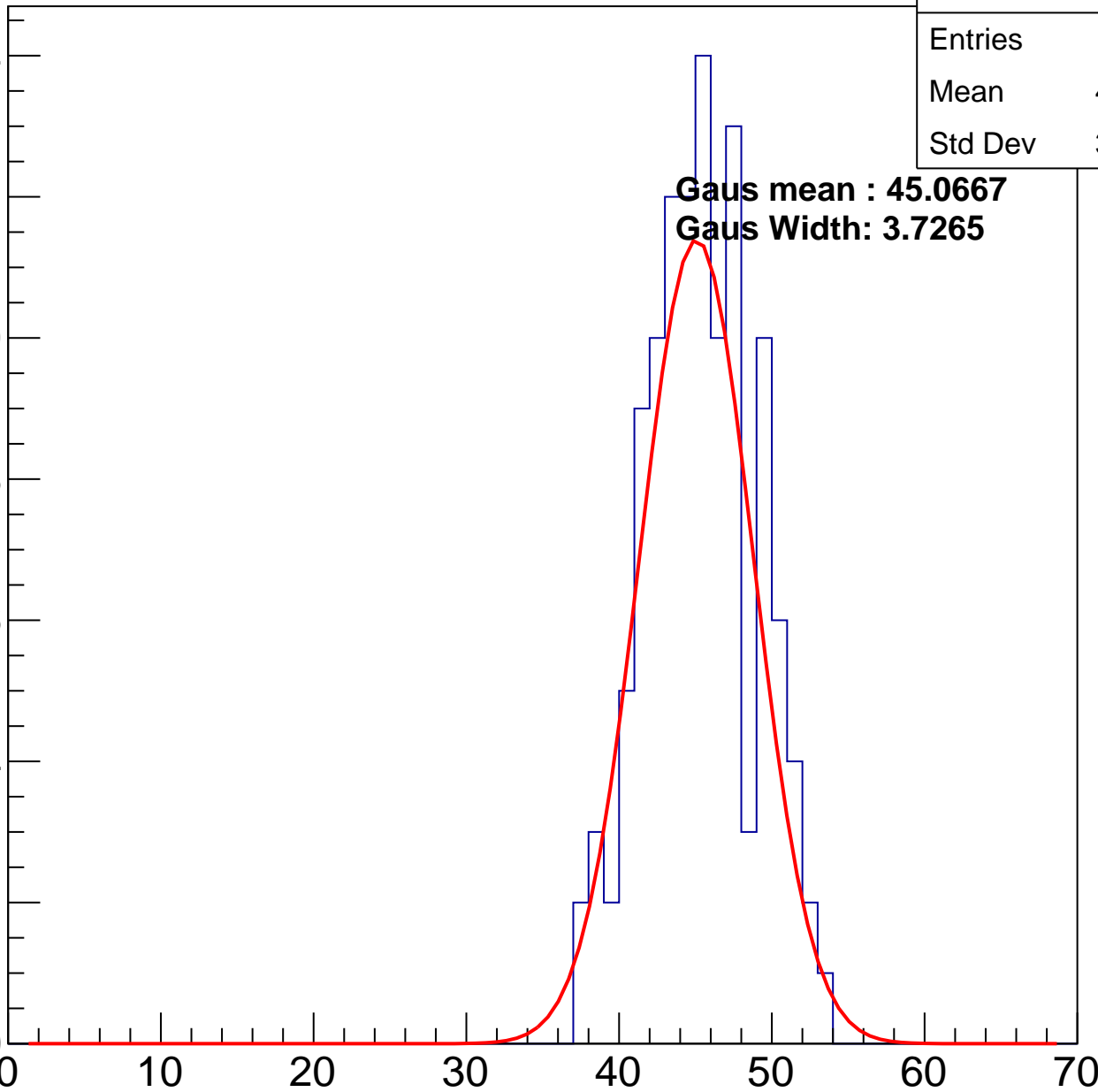
14
12
10
8
6
4
2
0

Entries	118
Mean	44.87
Std Dev	3.528

Gaus mean : 45.0667

Gaus Width: 3.7265

ampl



B0L101S, U25-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

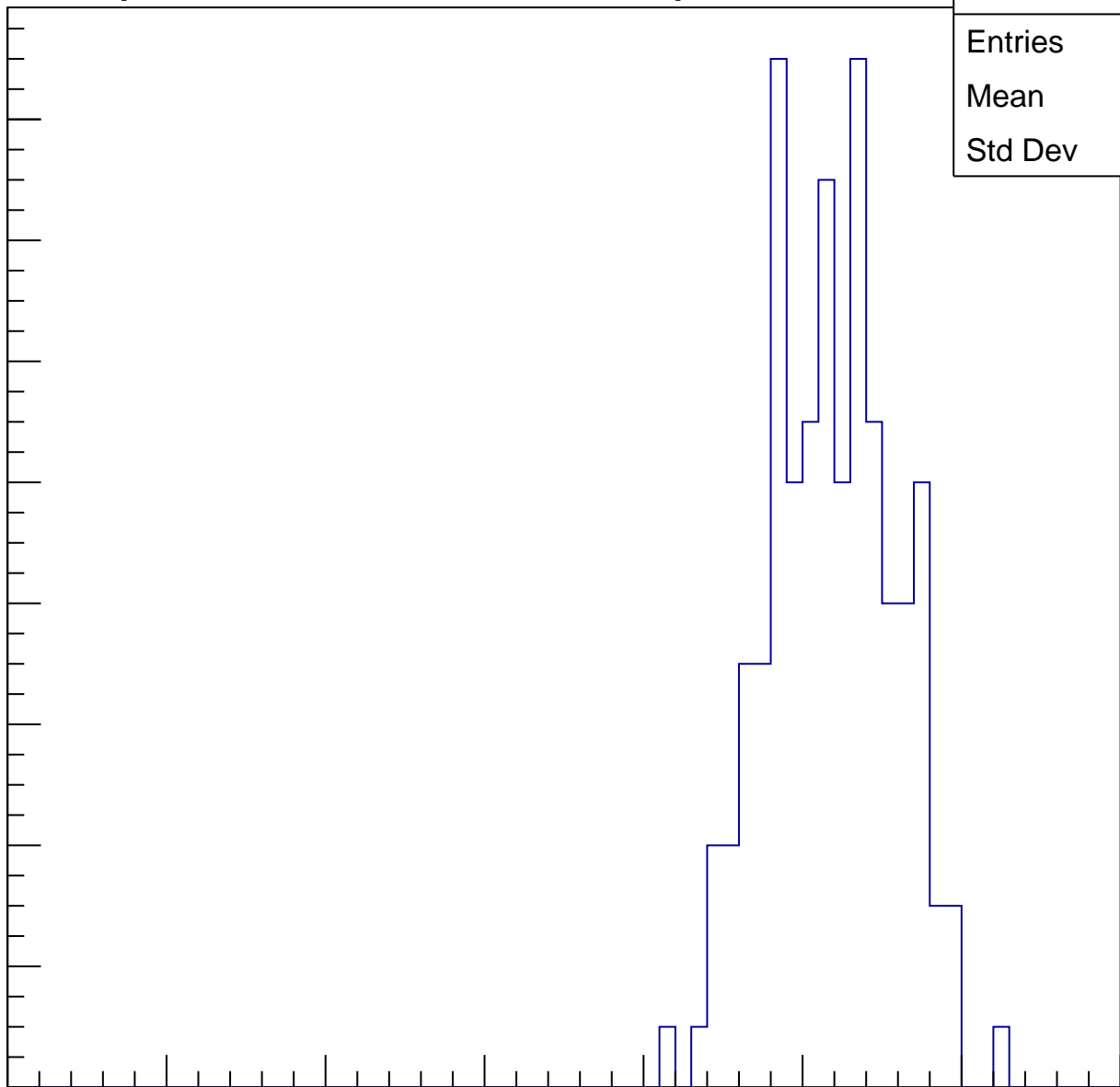
Entries	148
Mean	51.34
Std Dev	3.945

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	57.45
Std Dev	3.19

Entry

14

12

10

8

6

4

2

0

0

10

20

30

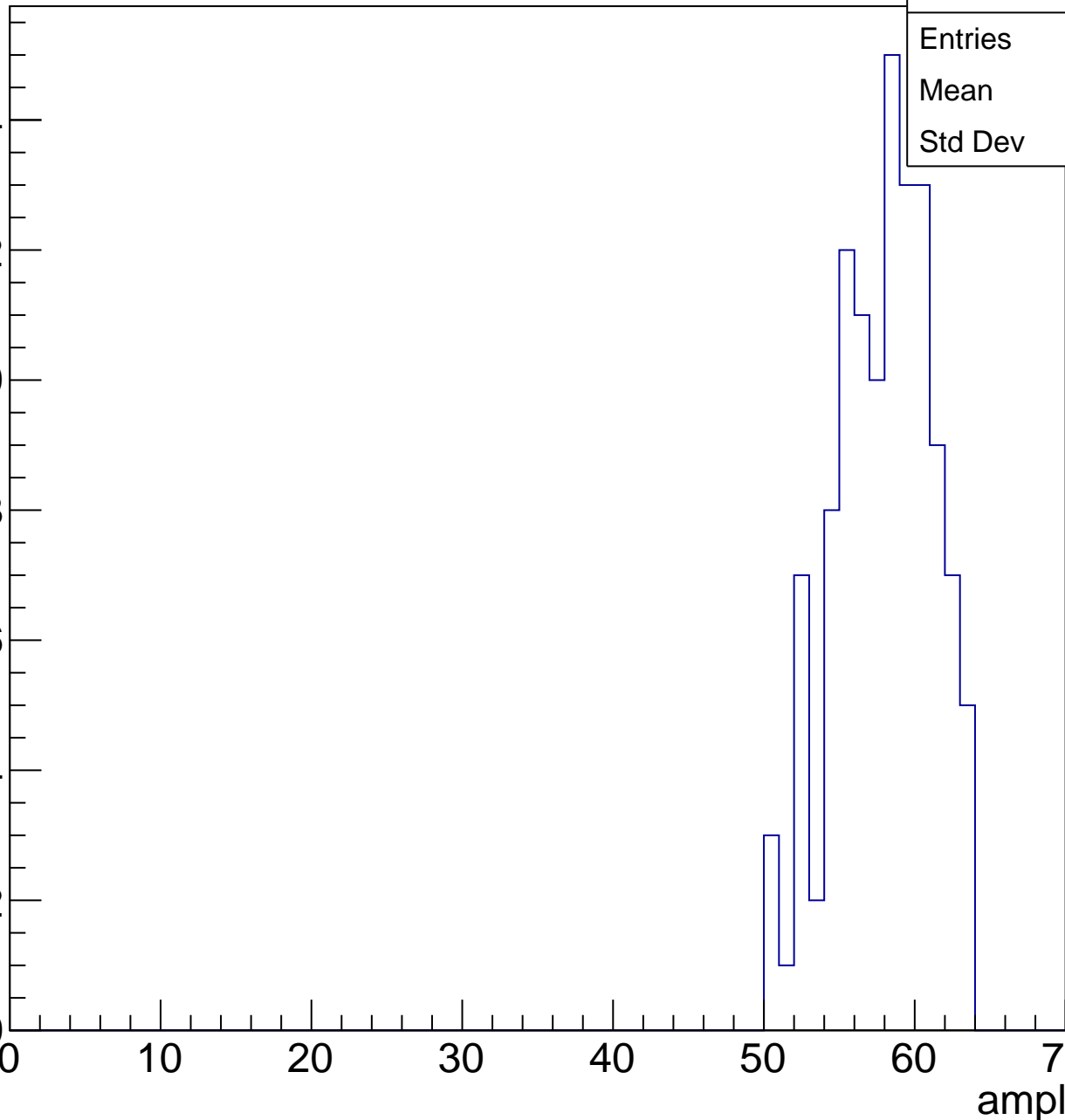
40

50

60

70

ampl



B0L101S, U25-ch119, adc5

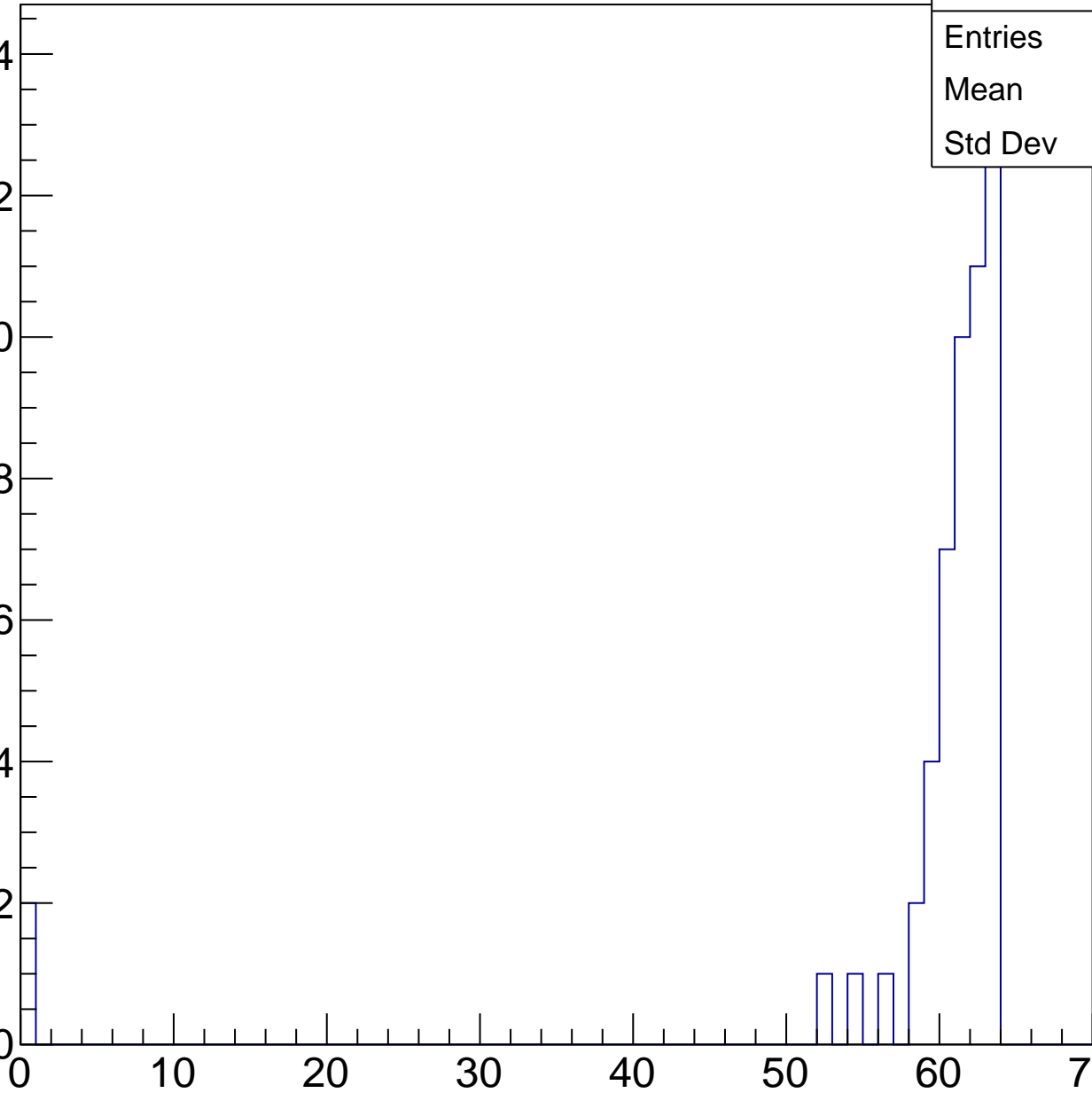
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	53
Mean	58.64
Std Dev	11.82

ampl



B0L101S, U25-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U25-ch120, adc0

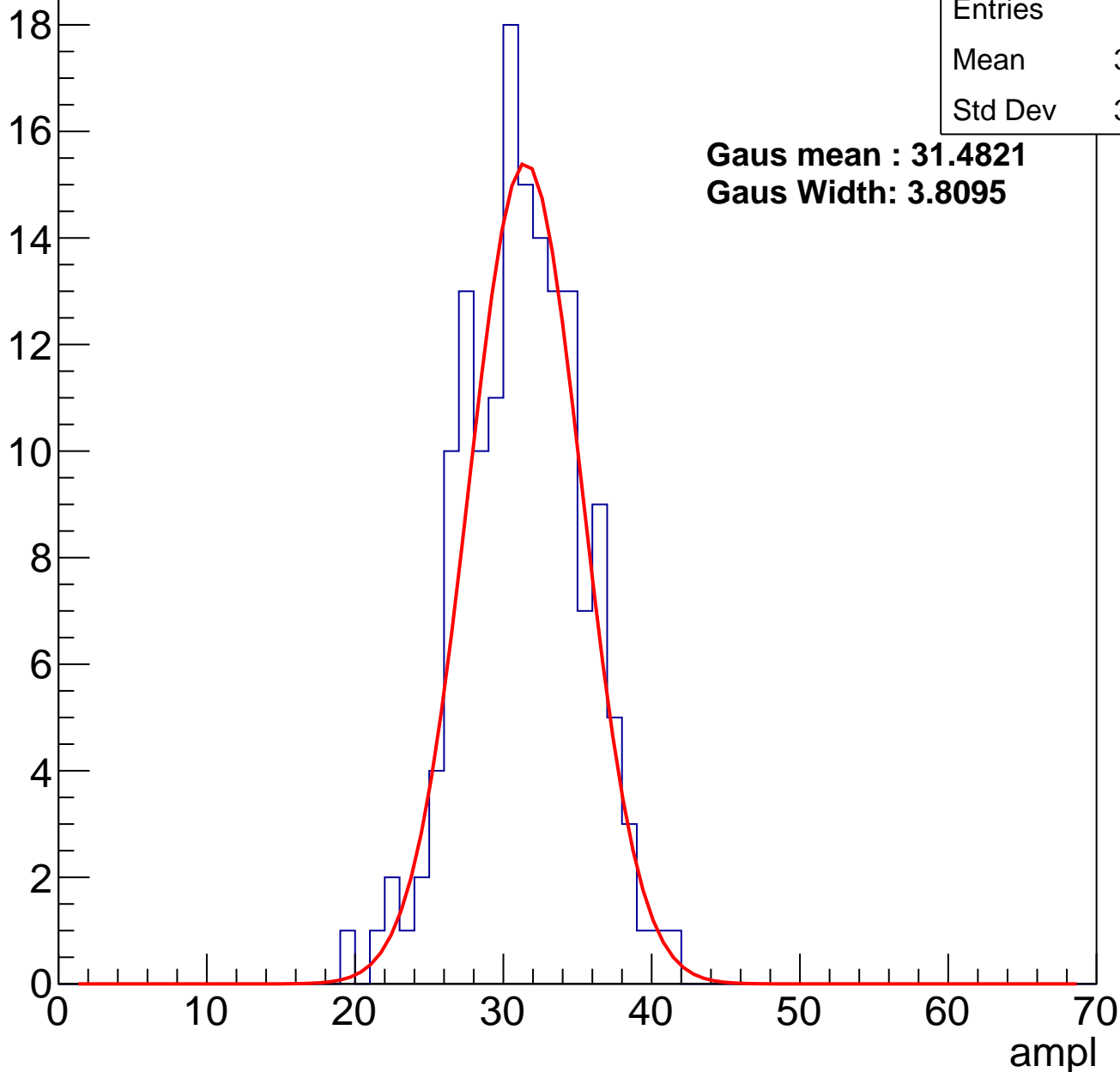
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	30.82
Std Dev	3.942

Gaus mean : 31.4821

Gaus Width: 3.8095

Entry



B0L101S, U25-ch120, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

162

Mean

38.01

Std Dev

5.047

Gaus mean : 39.0306

Gaus Width: 3.8007

0

10

20

30

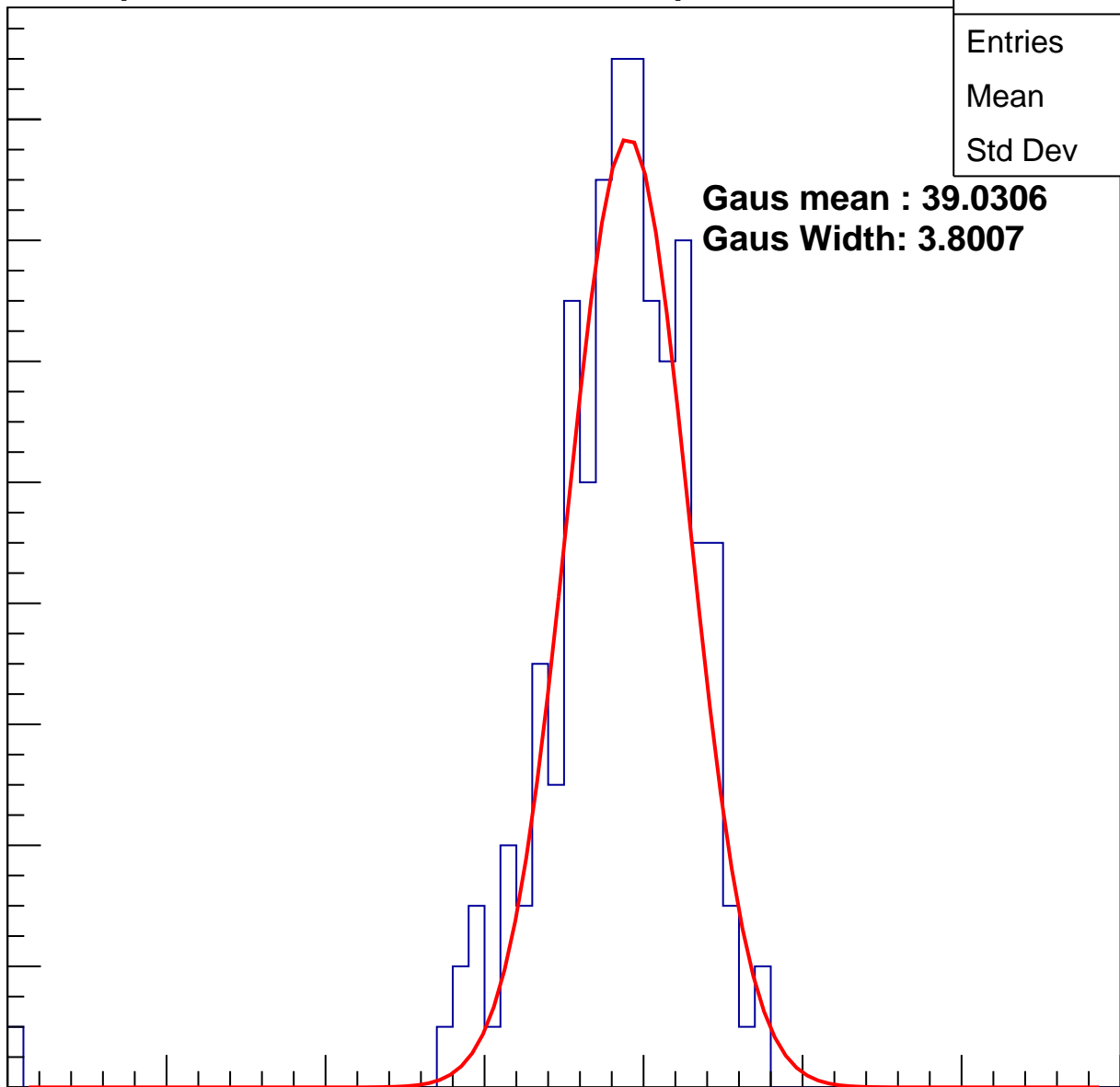
40

50

60

70

ampl



B0L101S, U25-ch120, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	45.82
Std Dev	3.751

Gaus mean : 45.8855

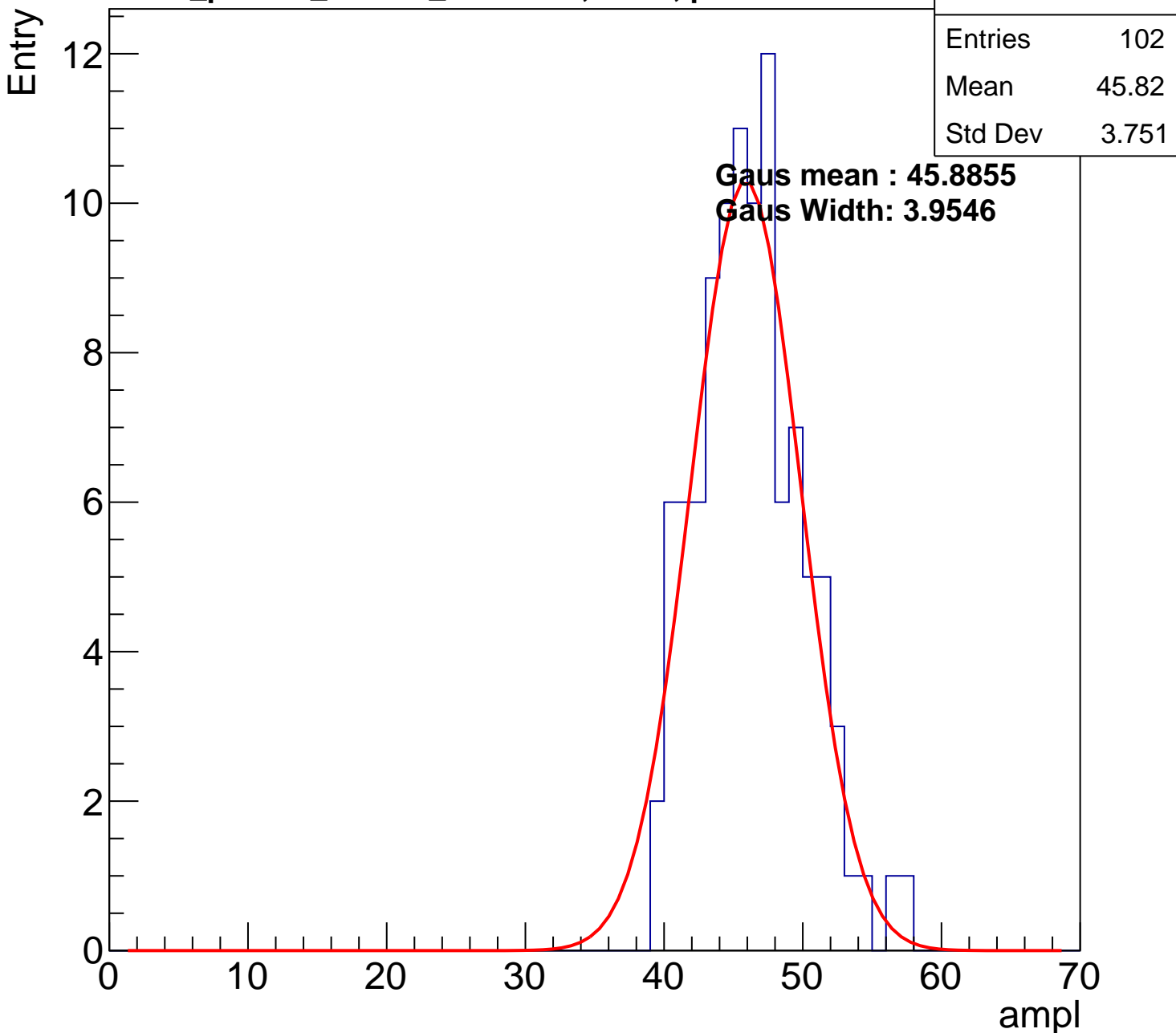
Gaus Width: 3.9546

Entry

12
10
8
6
4
2
0

ampl

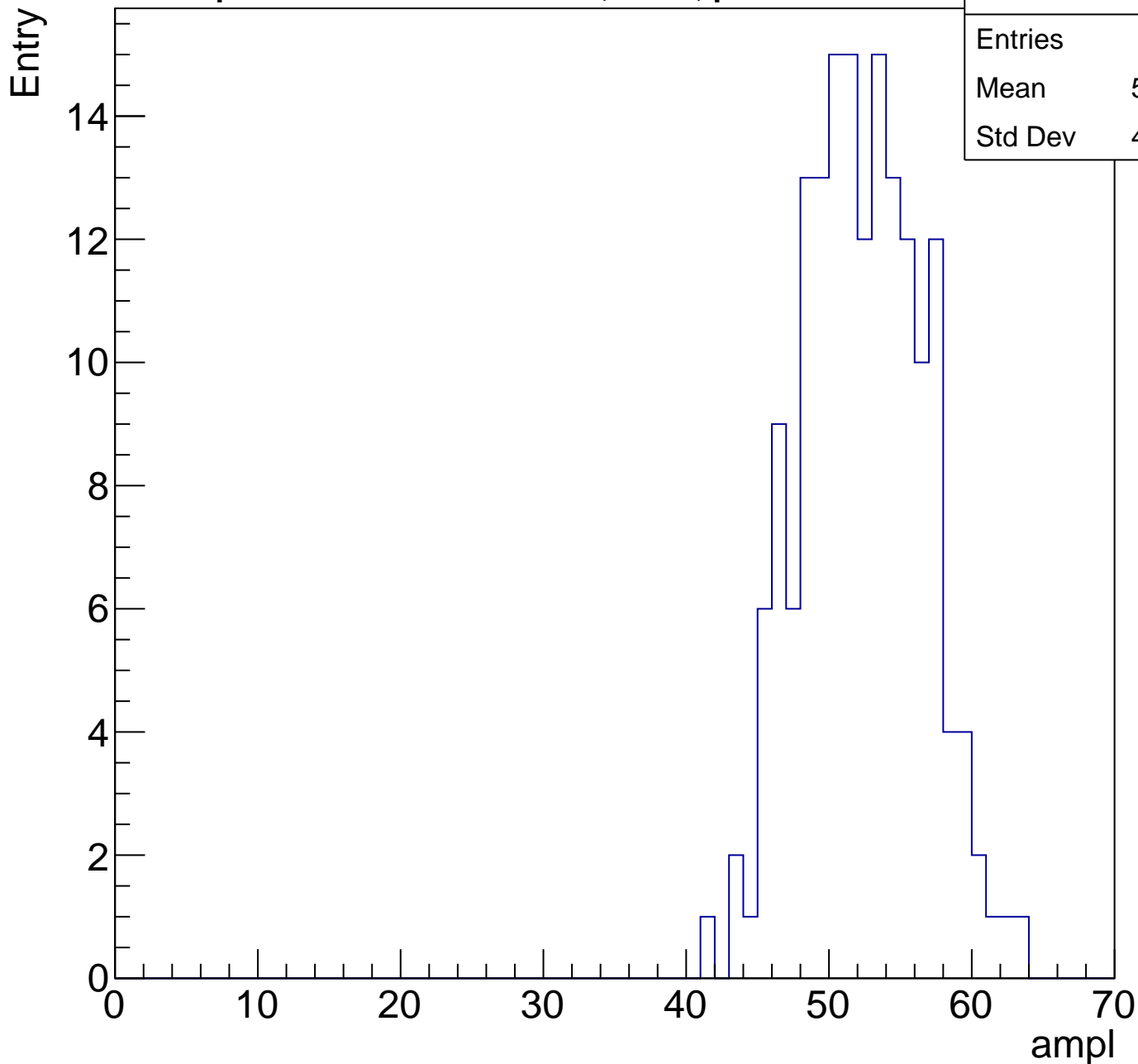
0 10 20 30 40 50 60 70



B0L101S, U25-ch120, adc3

calib_packv5_042523_0143.root, FC#1, port C1

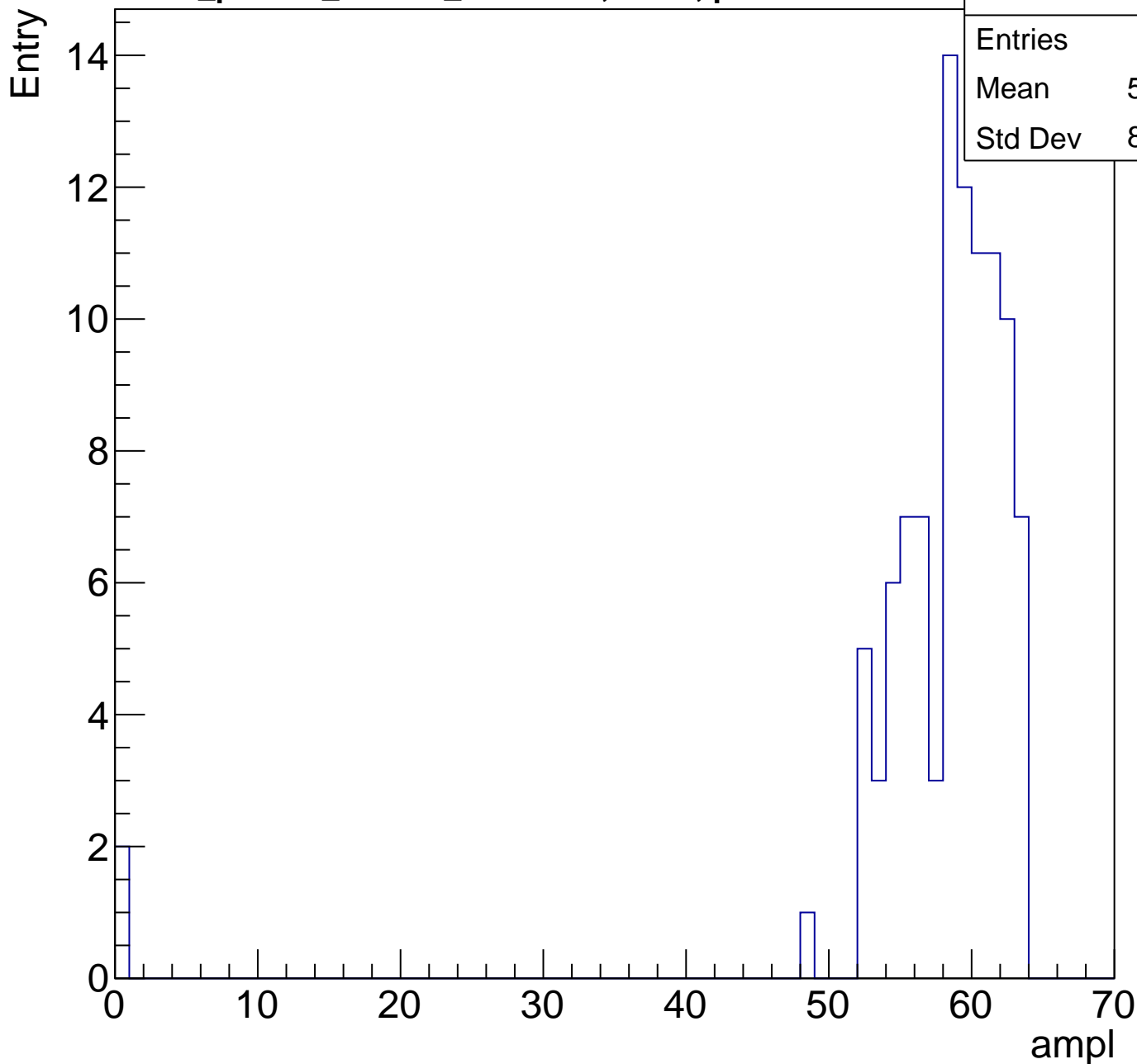
Entries	168
Mean	51.86
Std Dev	4.167



B0L101S, U25-ch120, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	57.08
Std Dev	8.807

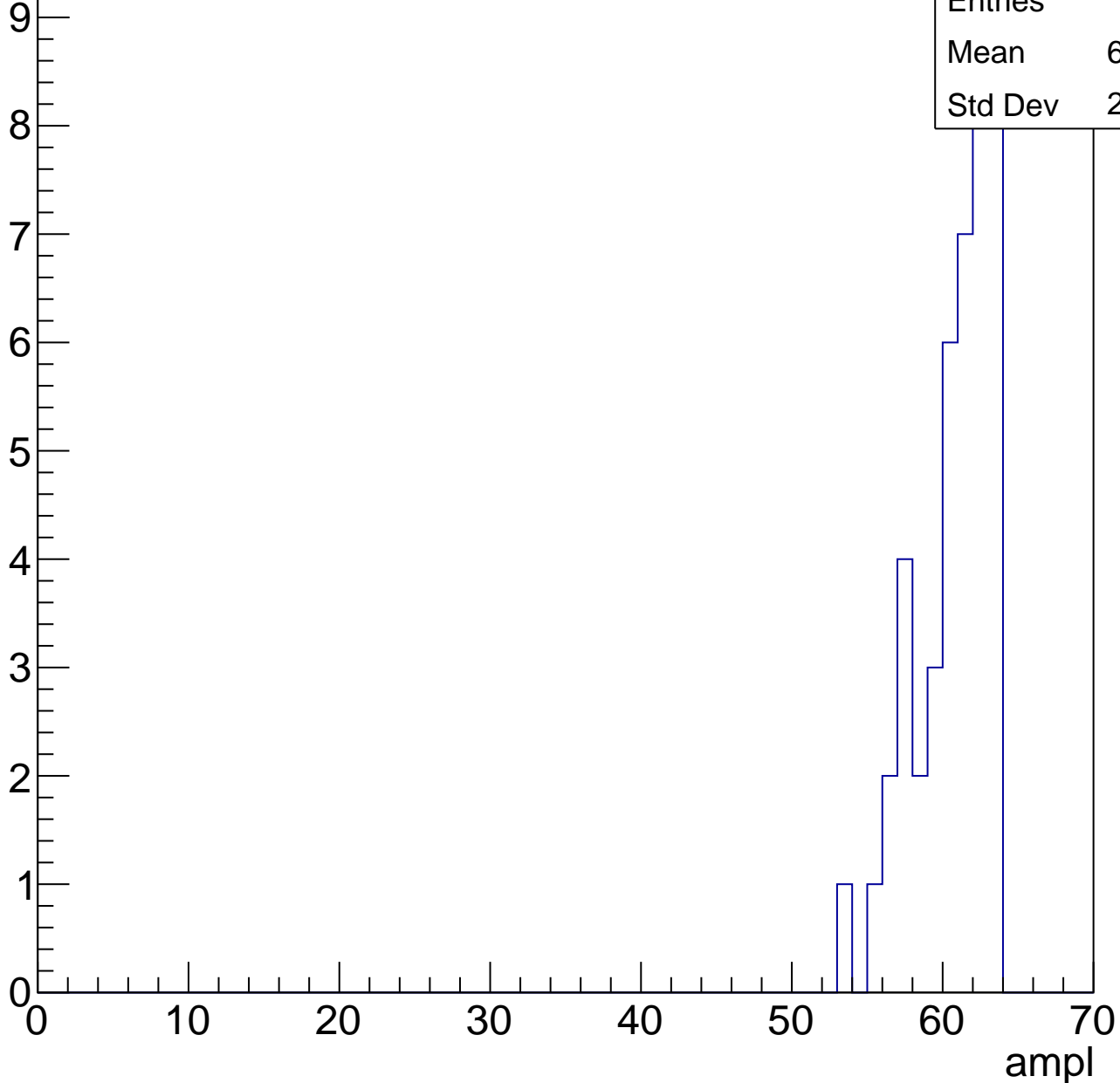


B0L101S, U25-ch120, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

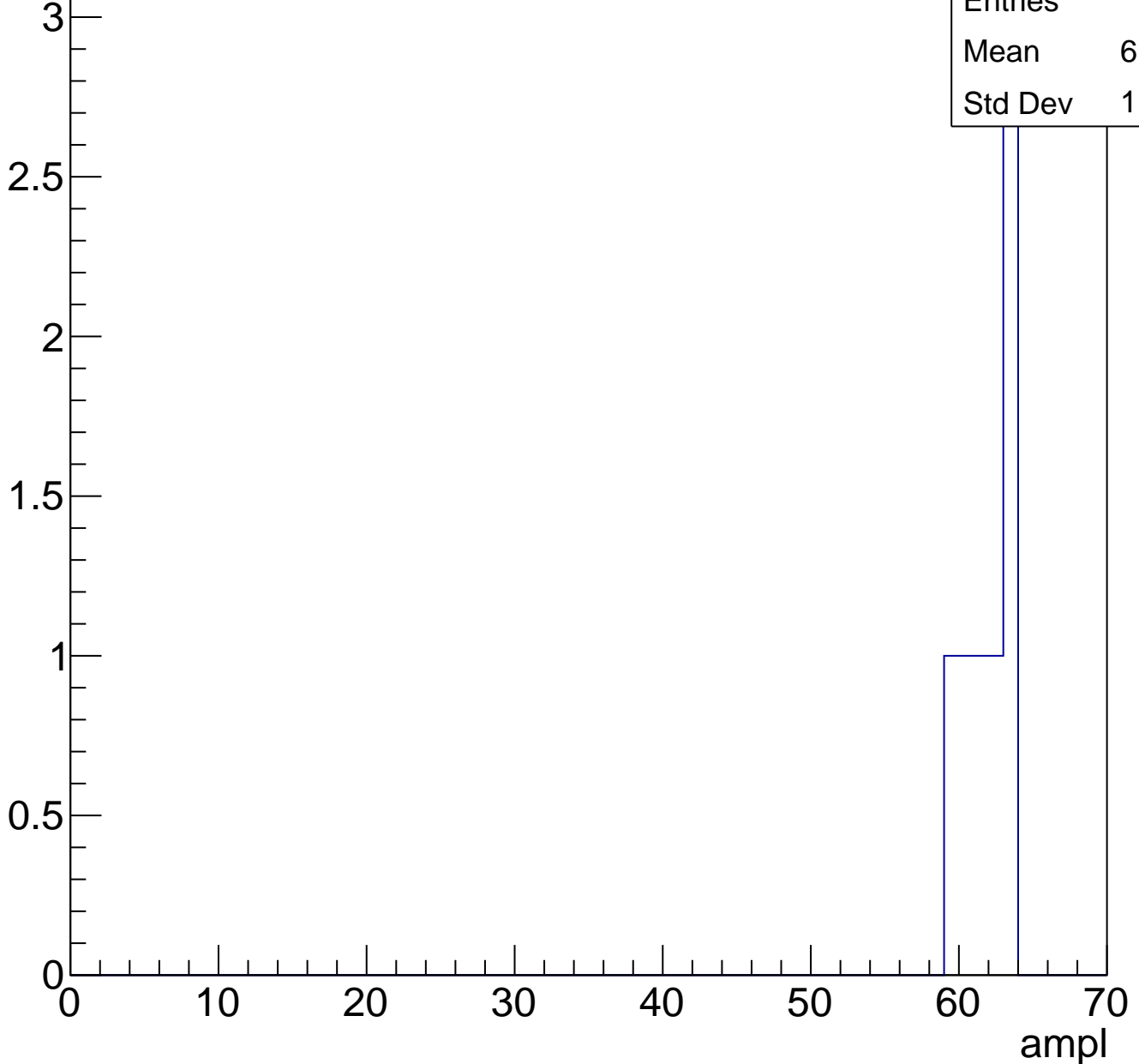
Entries	43
Mean	60.26
Std Dev	2.507



B0L101S, U25-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L101S, U25-ch121, adc0

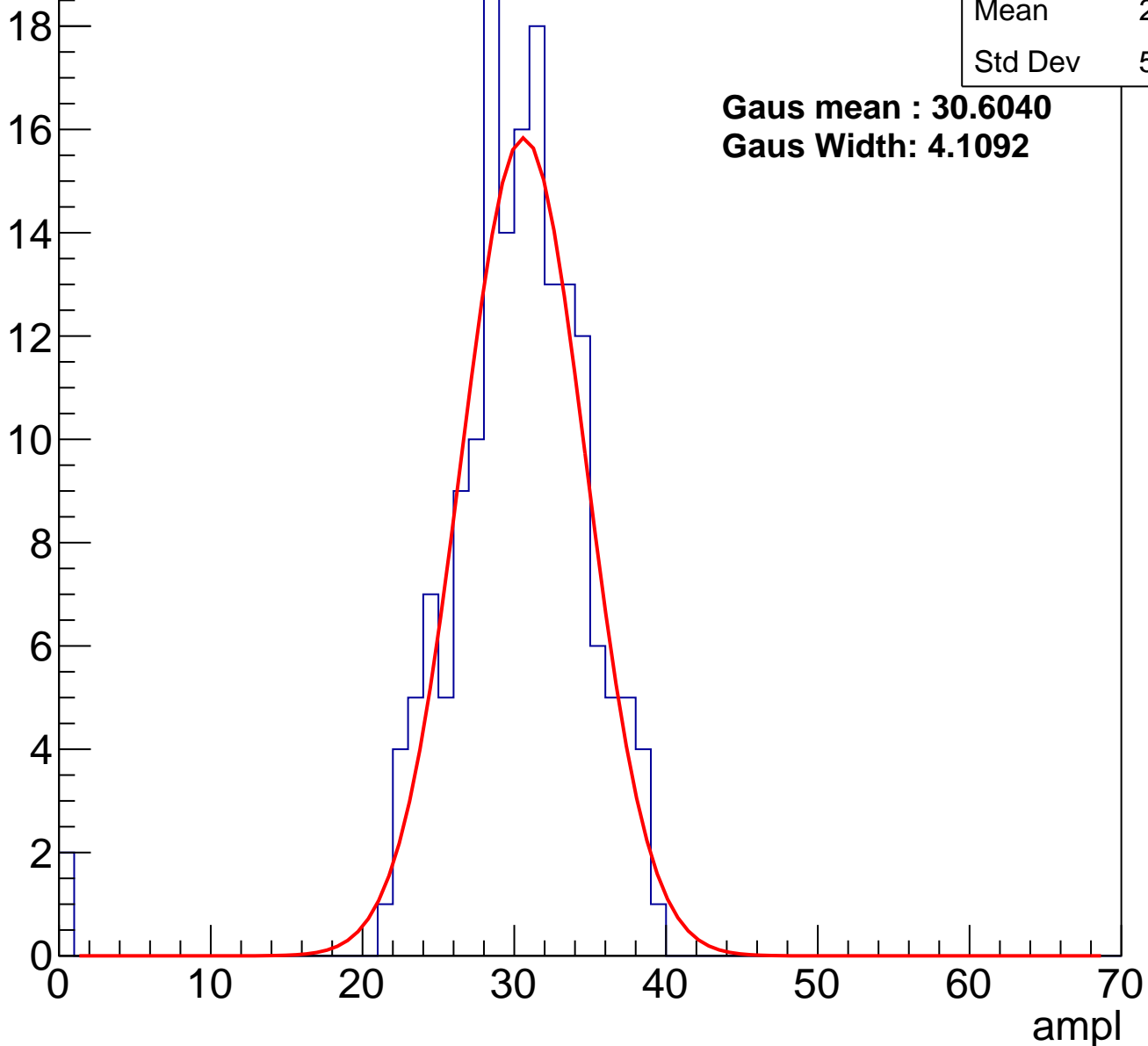
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	29.68
Std Dev	5.077

Gaus mean : 30.6040

Gaus Width: 4.1092

Entry



B0L101S, U25-ch121, adc1

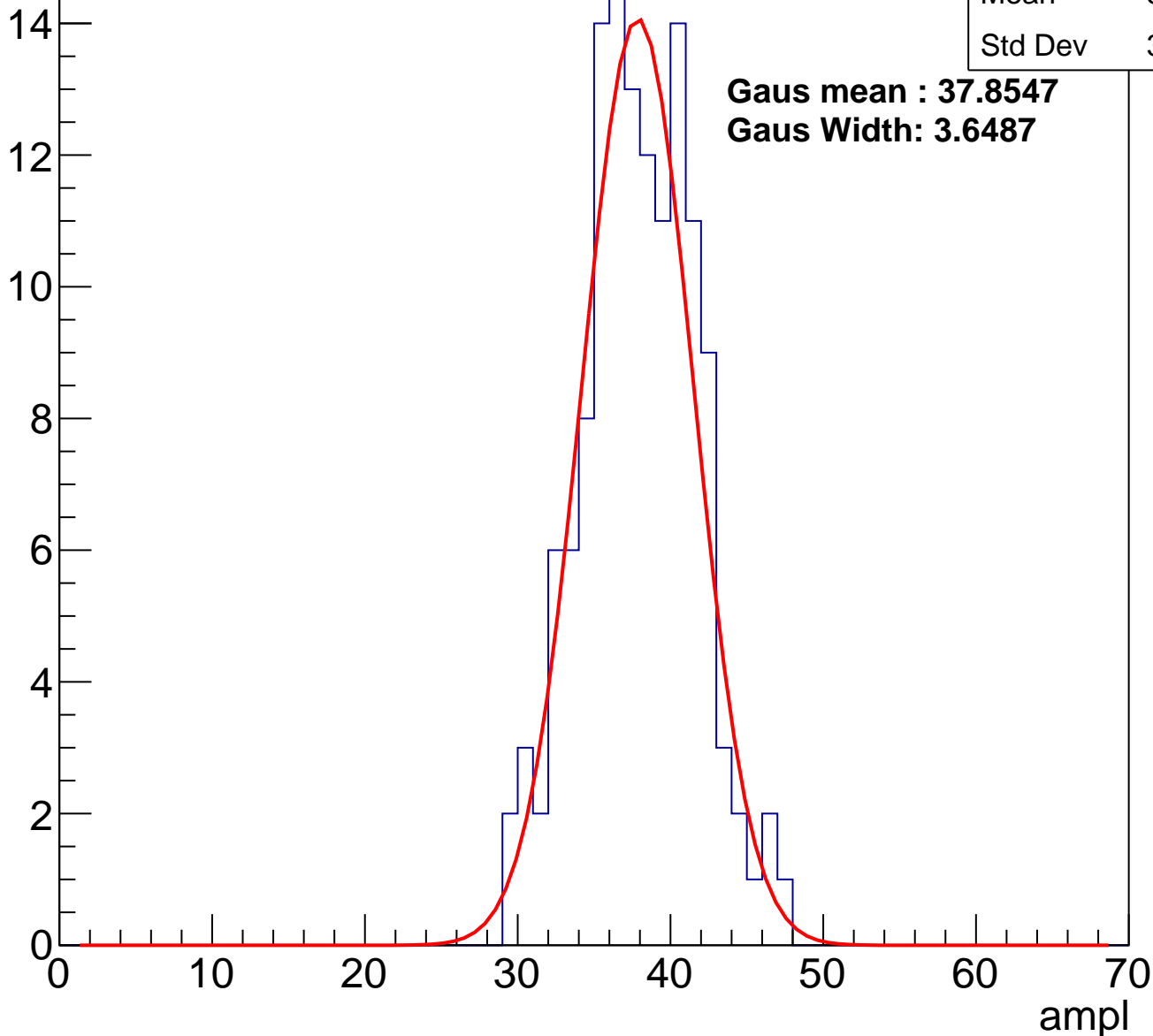
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	37.47
Std Dev	3.673

Gaus mean : 37.8547

Gaus Width: 3.6487

Entry



B0L101S, U25-ch121, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	44.43
Std Dev	3.803

Gaus mean : 44.6218

Gaus Width: 4.1495

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

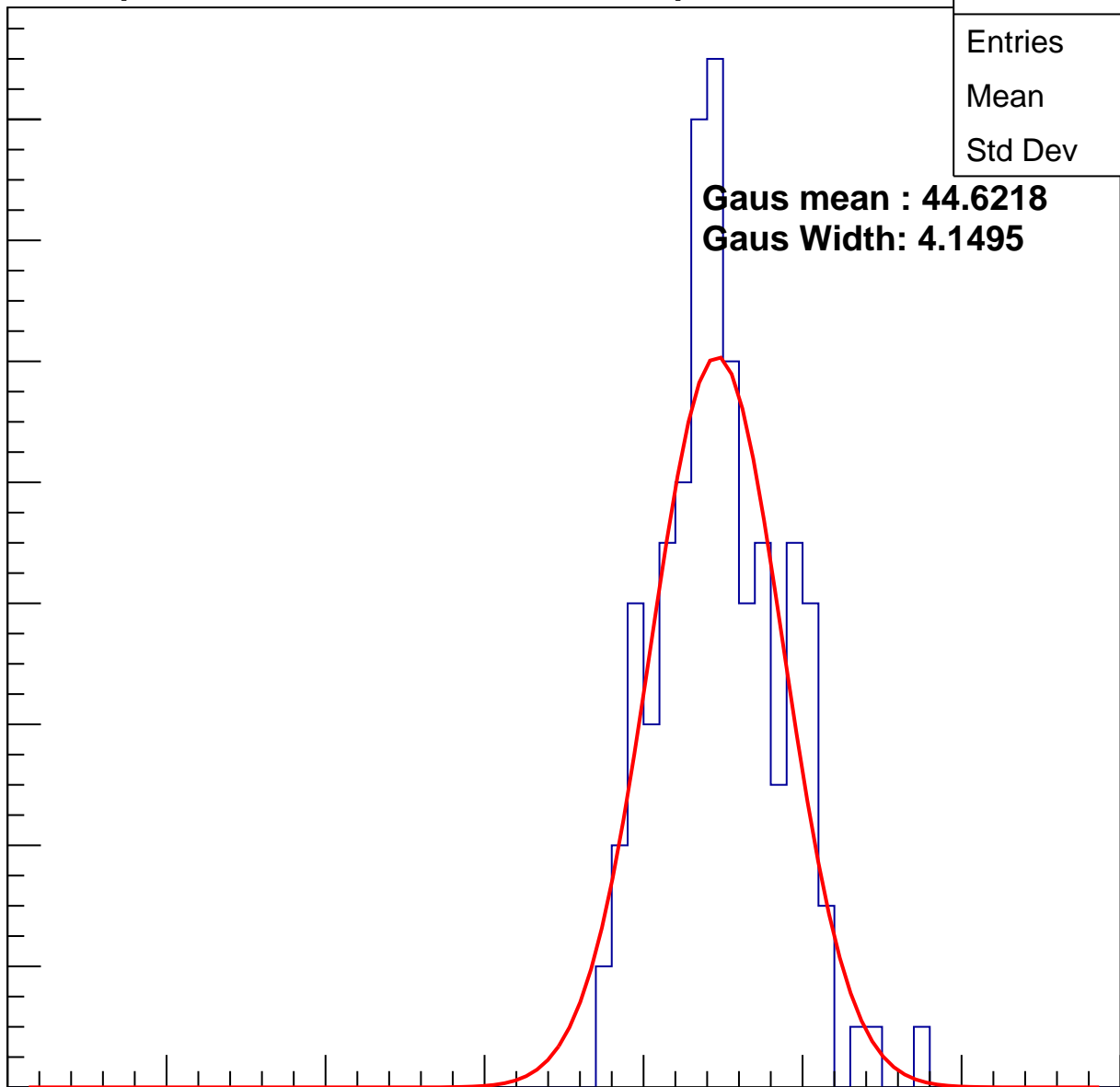
40

50

60

ampl

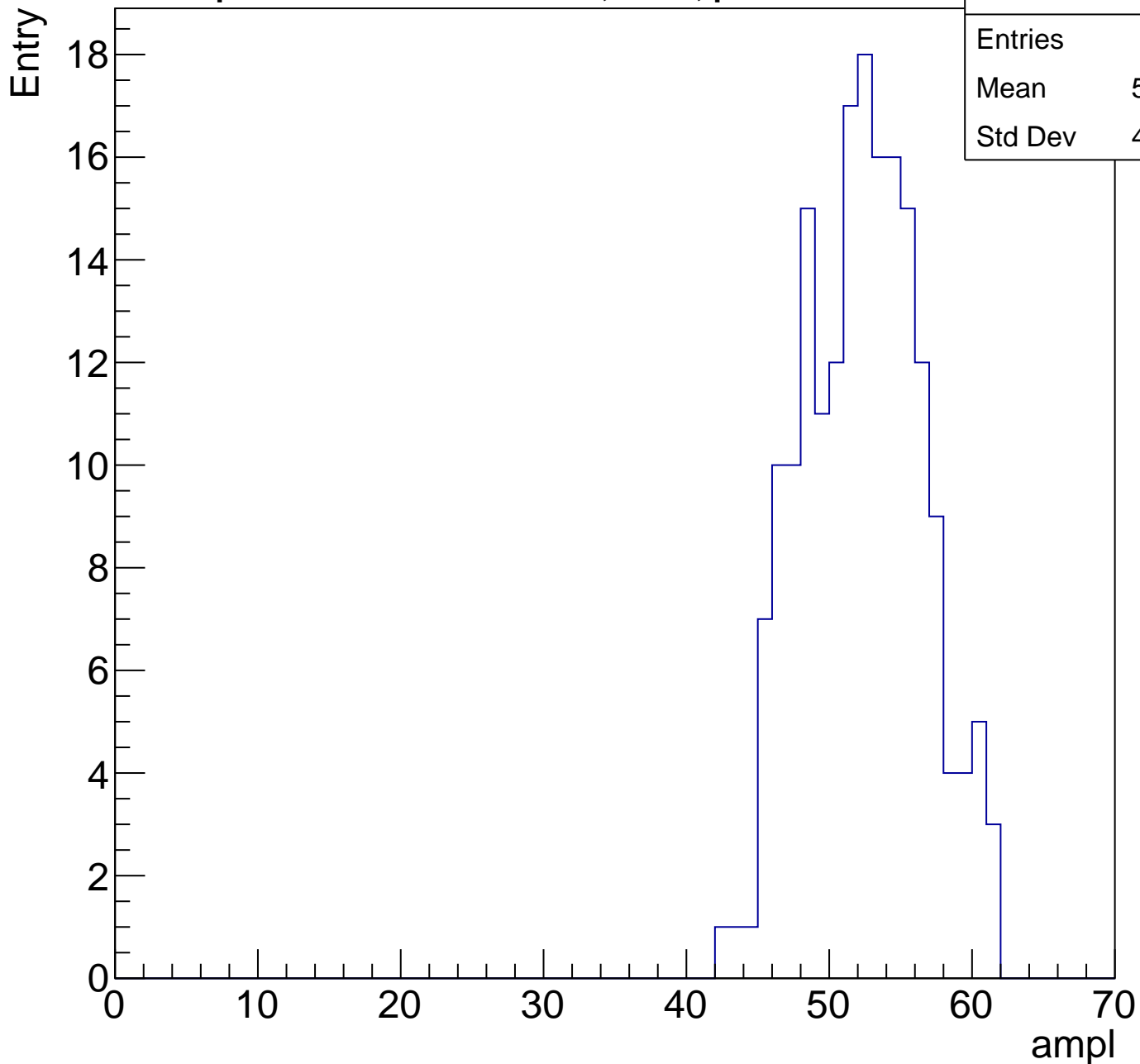
70



B0L101S, U25-ch121, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	51.92
Std Dev	4.095



B0L101S, U25-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

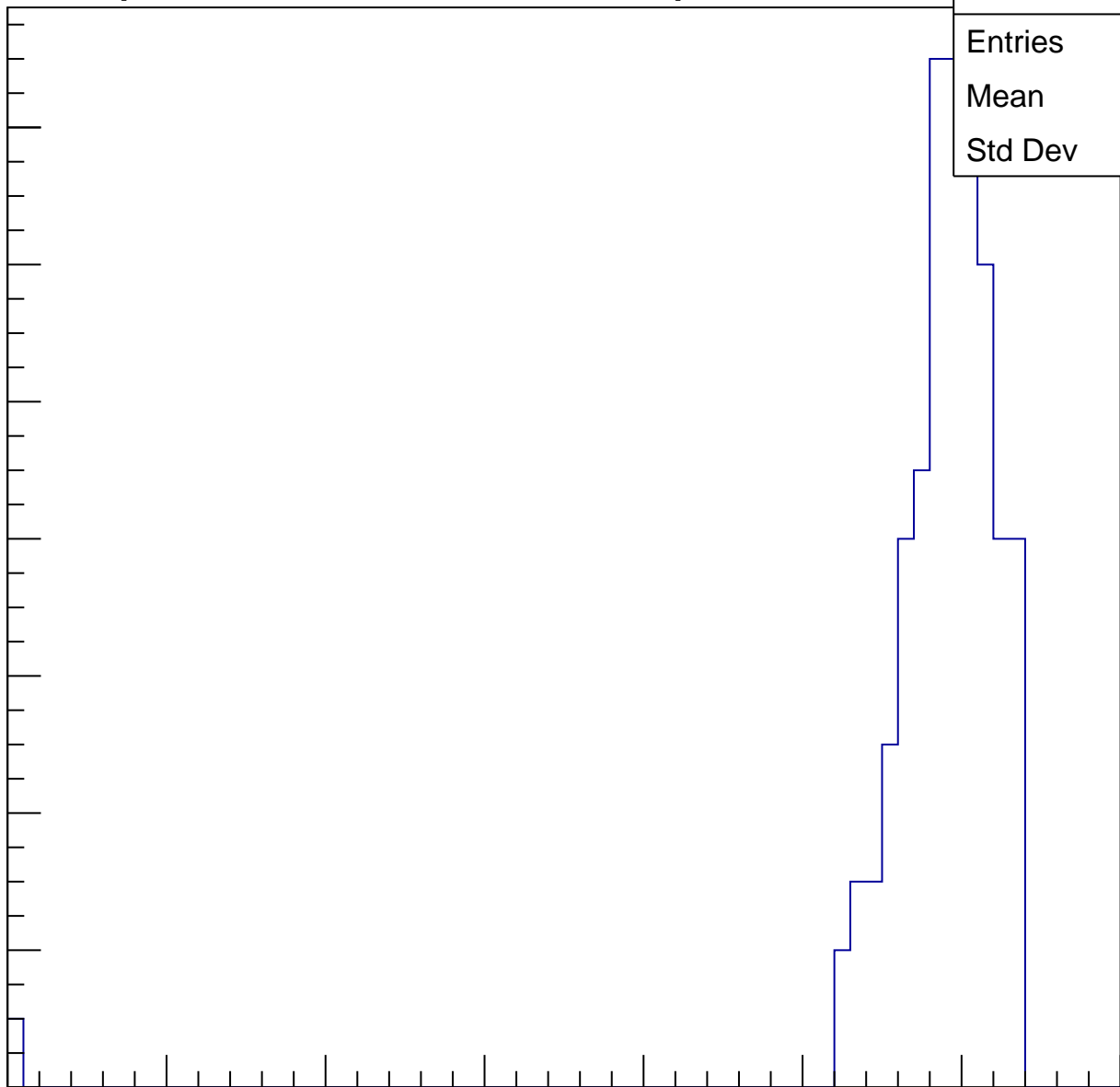
Entries	103
Mean	58.14
Std Dev	6.348

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch121, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	33
Mean	59.91
Std Dev	10.69

ampl

0

10

20

30

40

50

60

70

B0L101S, U25-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U25-ch122, adc0

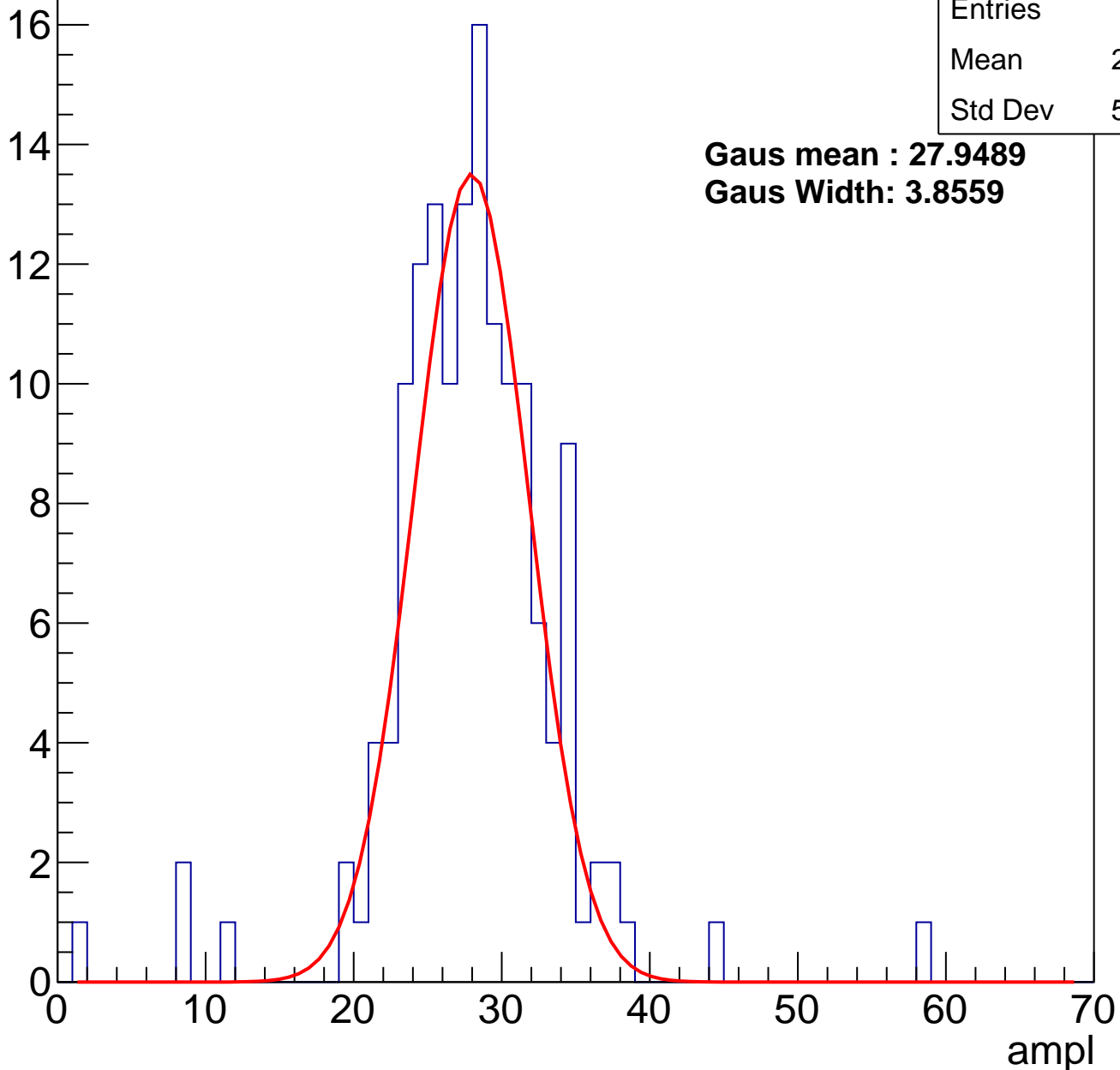
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	27.48
Std Dev	5.955

Gaus mean : 27.9489

Gaus Width: 3.8559

Entry



B0L101S, U25-ch122, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	34.62
Std Dev	3.987

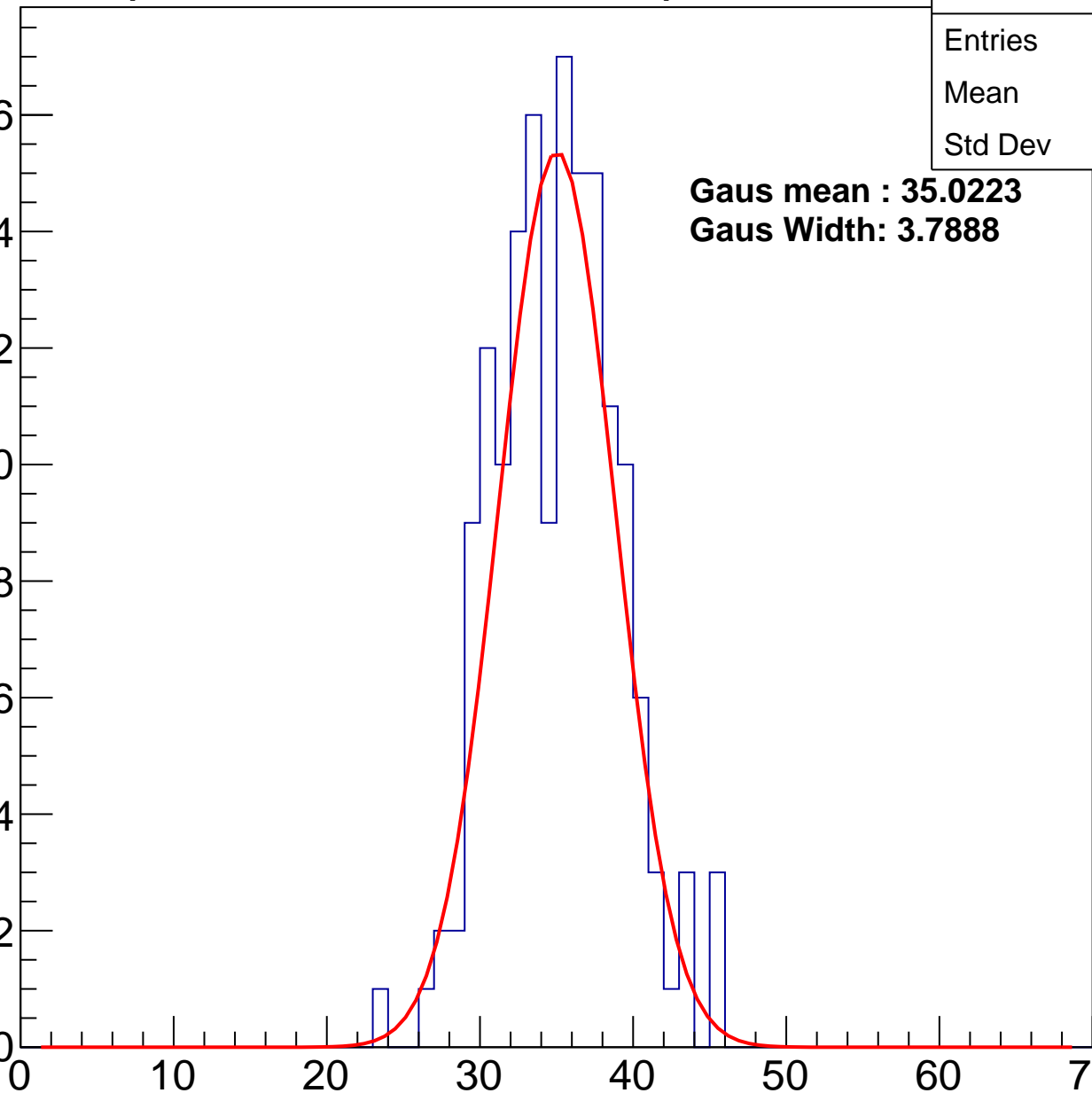
Gaus mean : 35.0223

Gaus Width: 3.7888

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch122, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

115

Mean

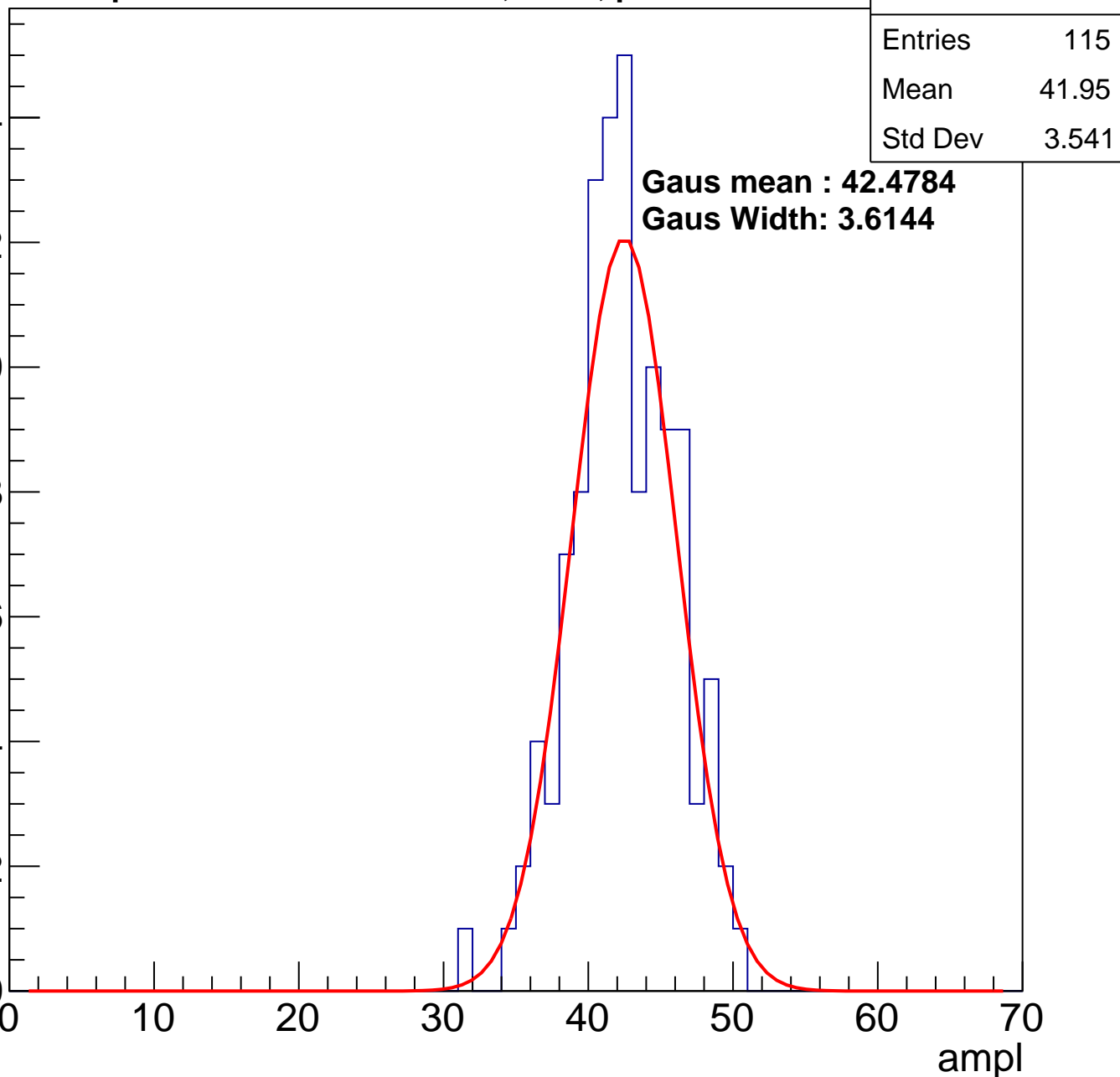
41.95

Std Dev

3.541

Gaus mean : 42.4784

Gaus Width: 3.6144

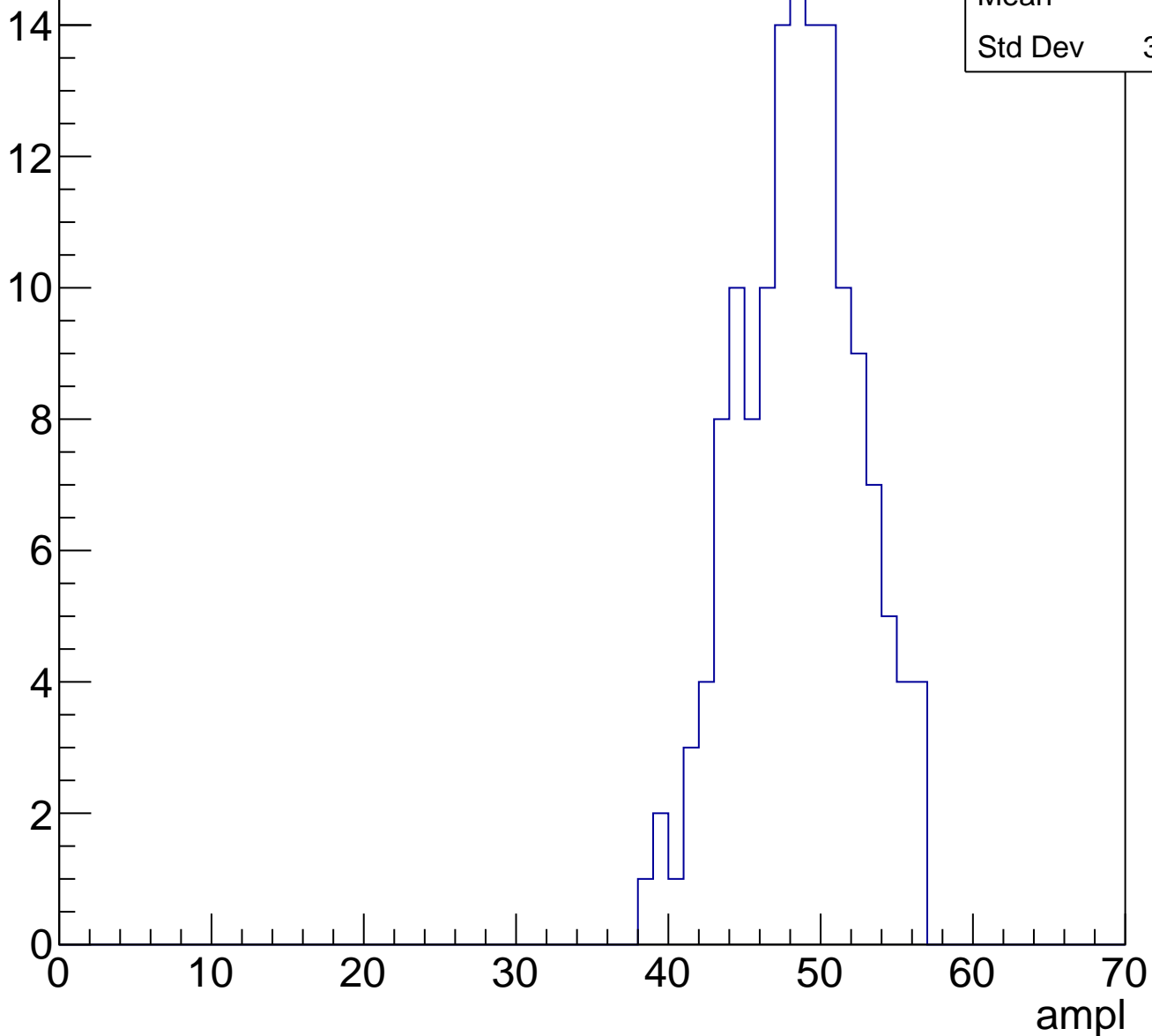


B0L101S, U25-ch122, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	48.1
Std Dev	3.934

Entry



B0L101S, U25-ch122, adc4

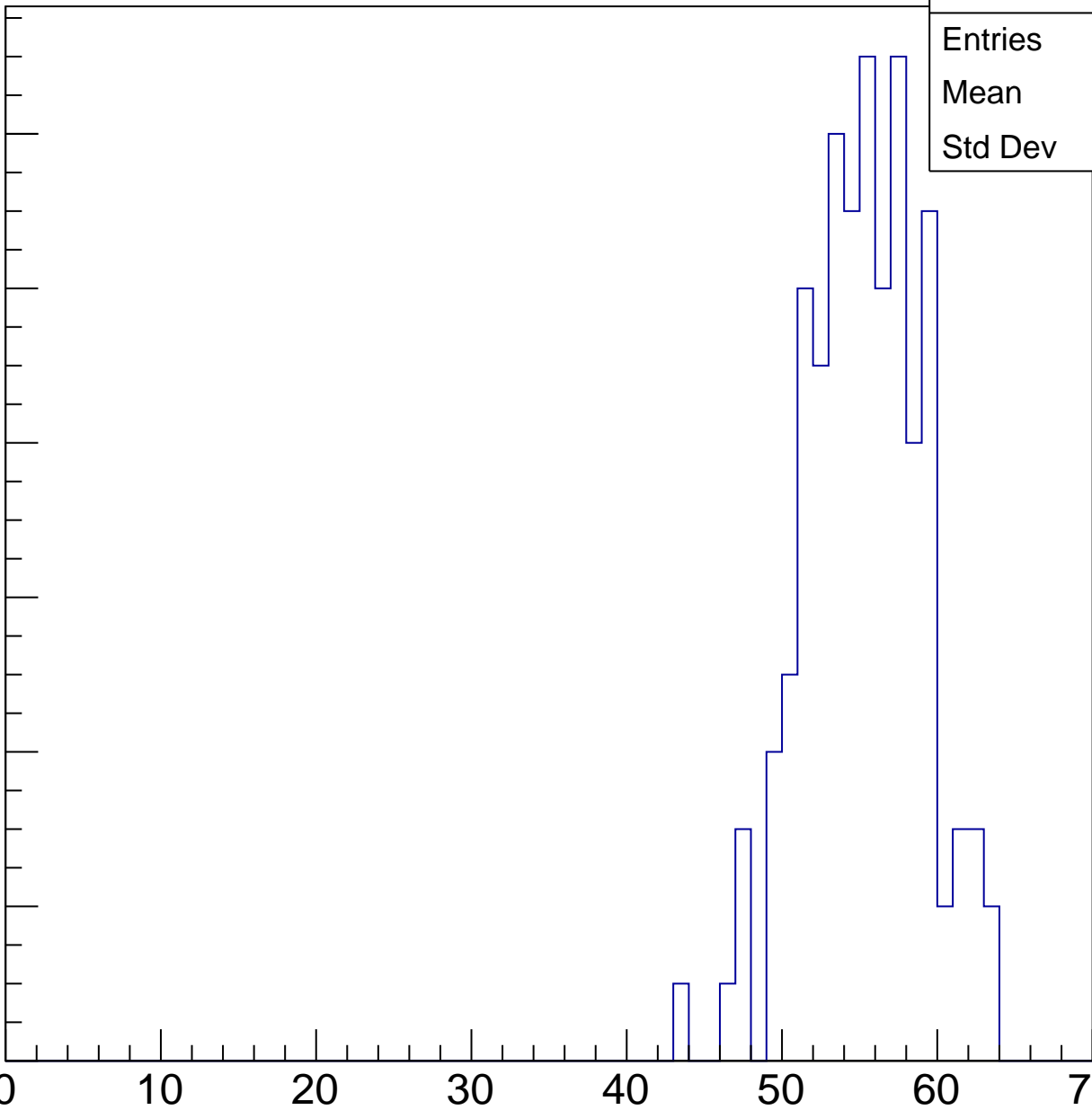
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	54.78
Std Dev	3.774

Entry

12
10
8
6
4
2
0

ampl



B0L101S, U25-ch122, adc5

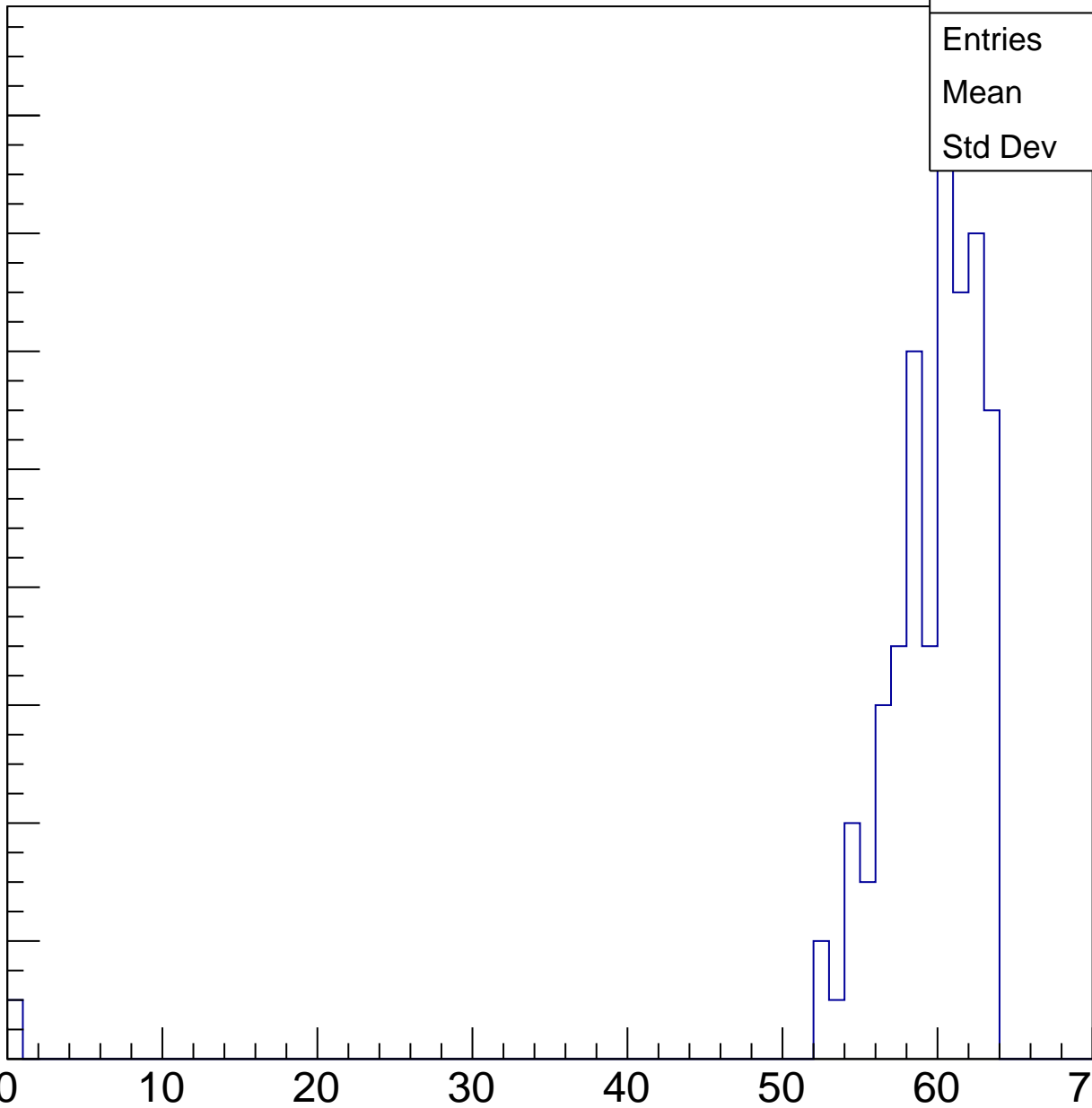
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.73
Std Dev	6.563

ampl

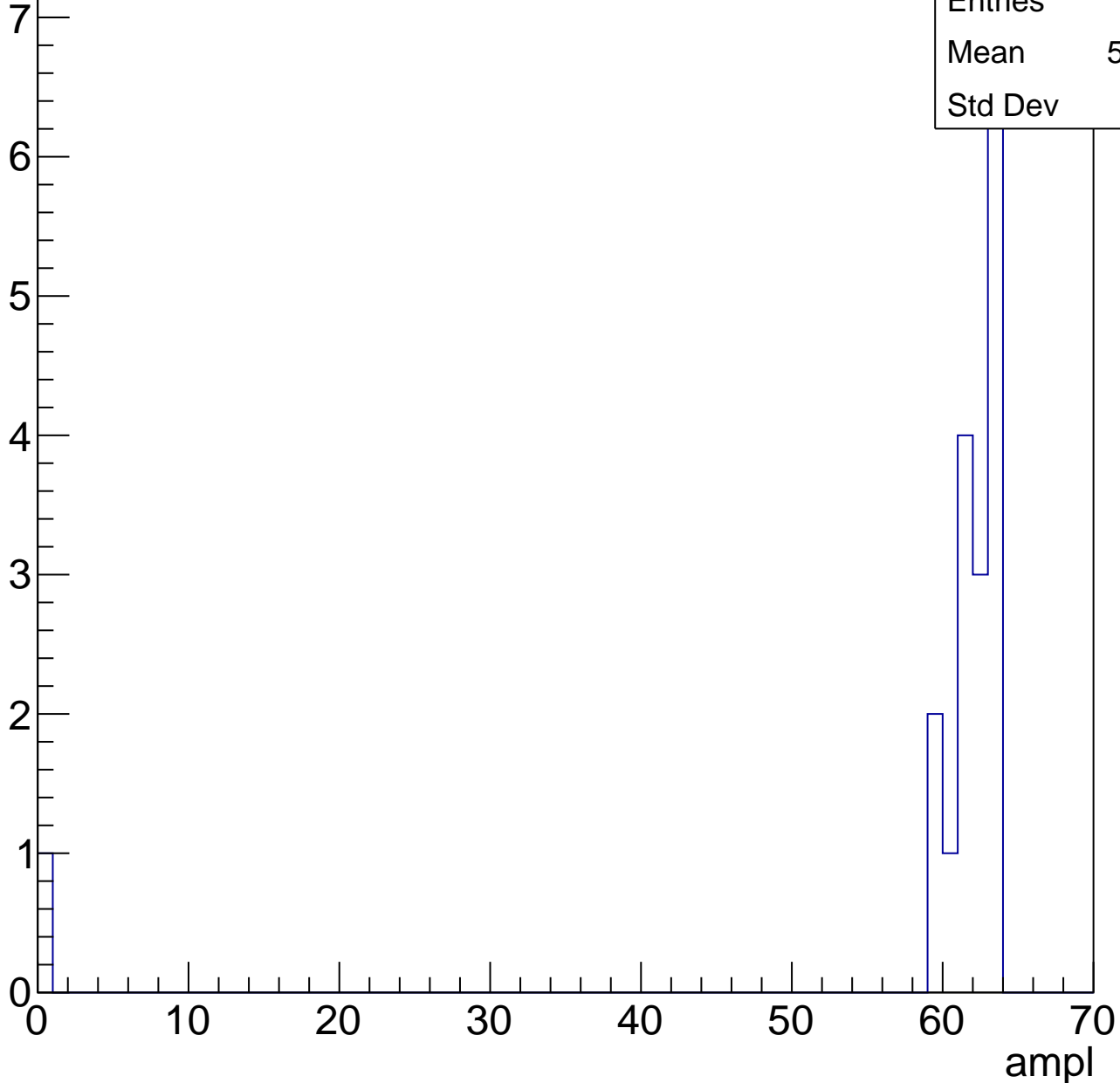


B0L101S, U25-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	58.28
Std Dev	14.2



B0L101S, U25-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch123, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	28.69
Std Dev	3.603

Gaus mean : 28.8673

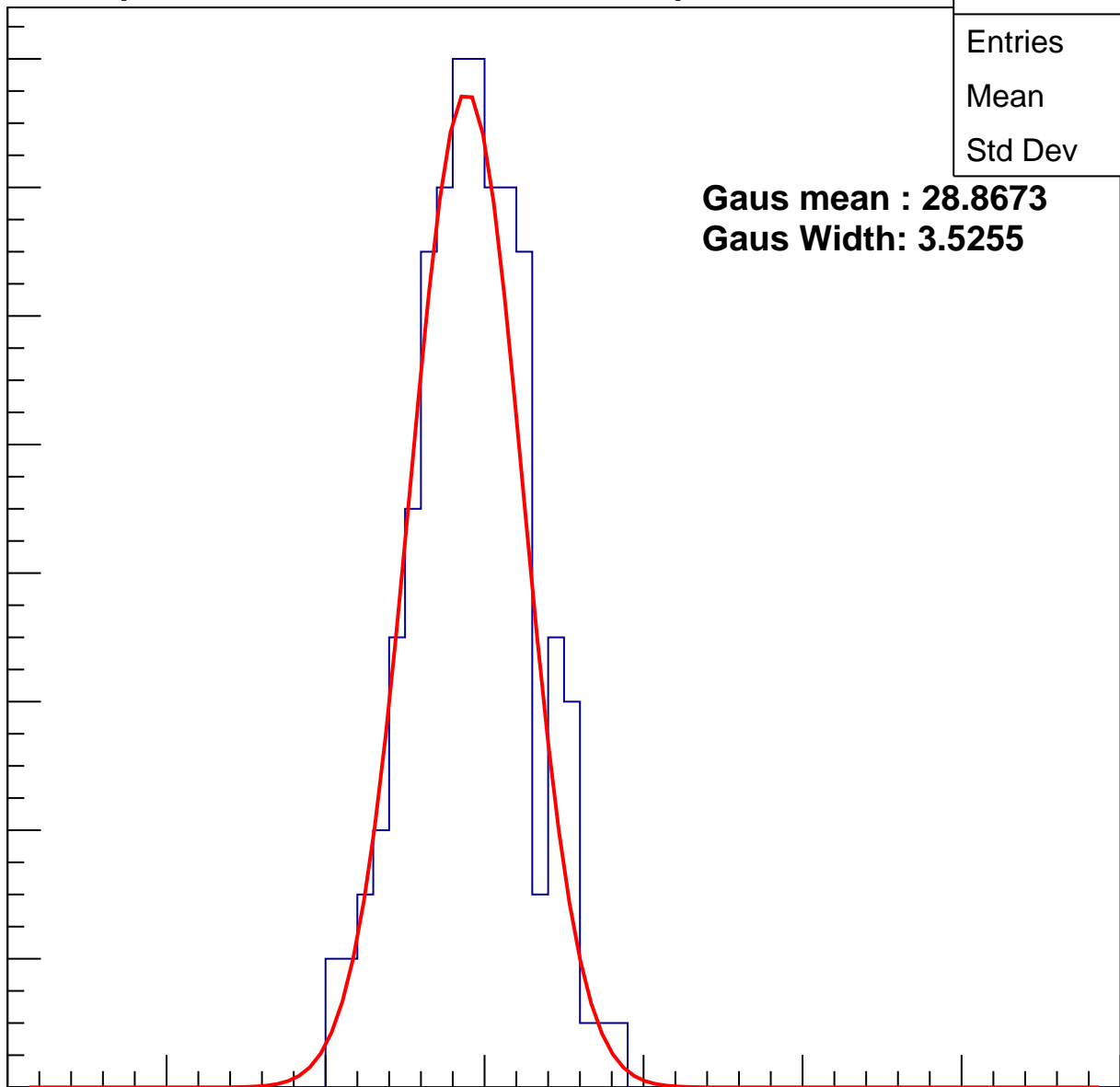
Gaus Width: 3.5255

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch123, adc1

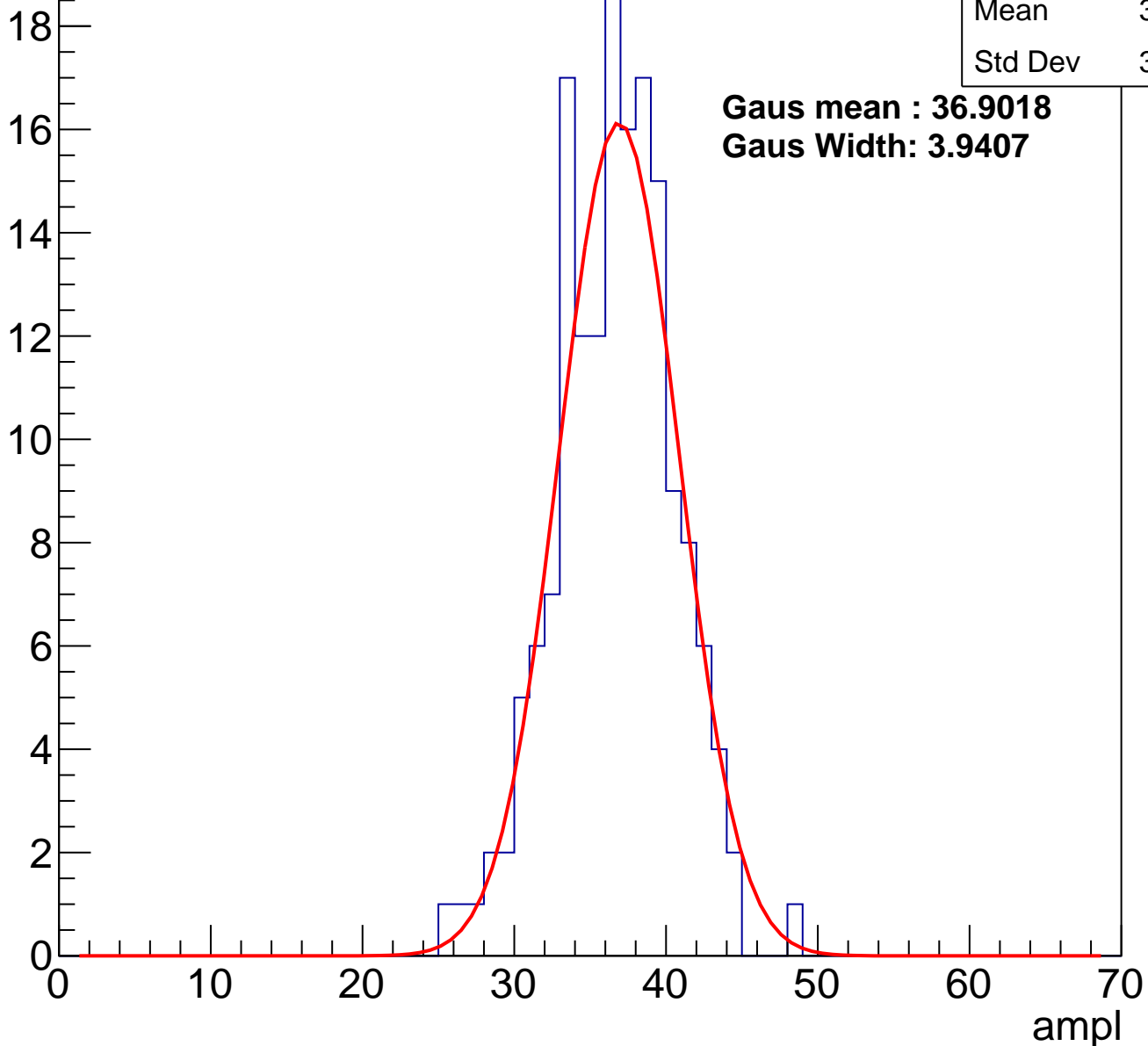
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	36.17
Std Dev	3.857

Gaus mean : 36.9018

Gaus Width: 3.9407

Entry



B0L101S, U25-ch123, adc2

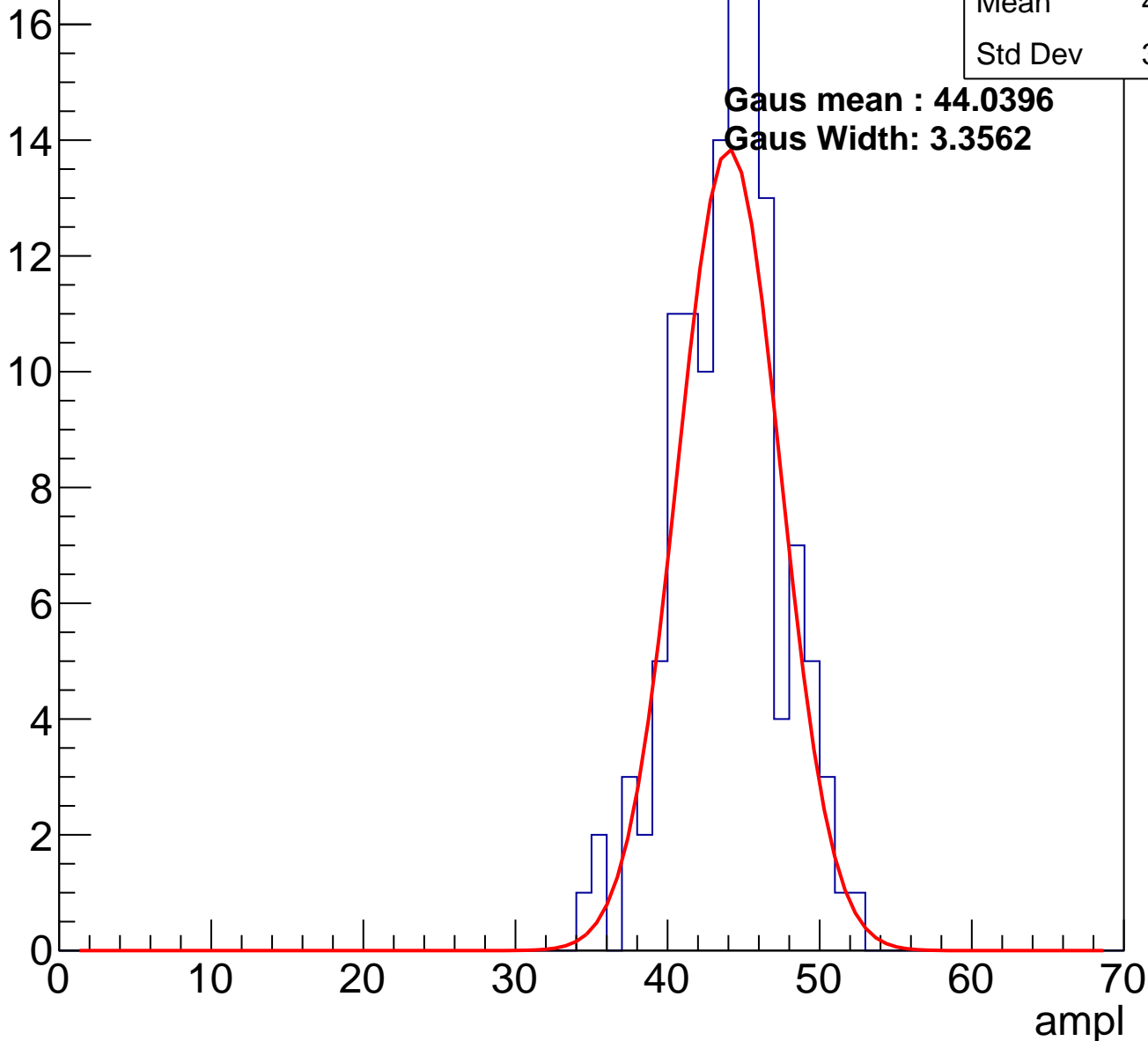
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	127
Mean	43.56
Std Dev	3.406

Gaus mean : 44.0396

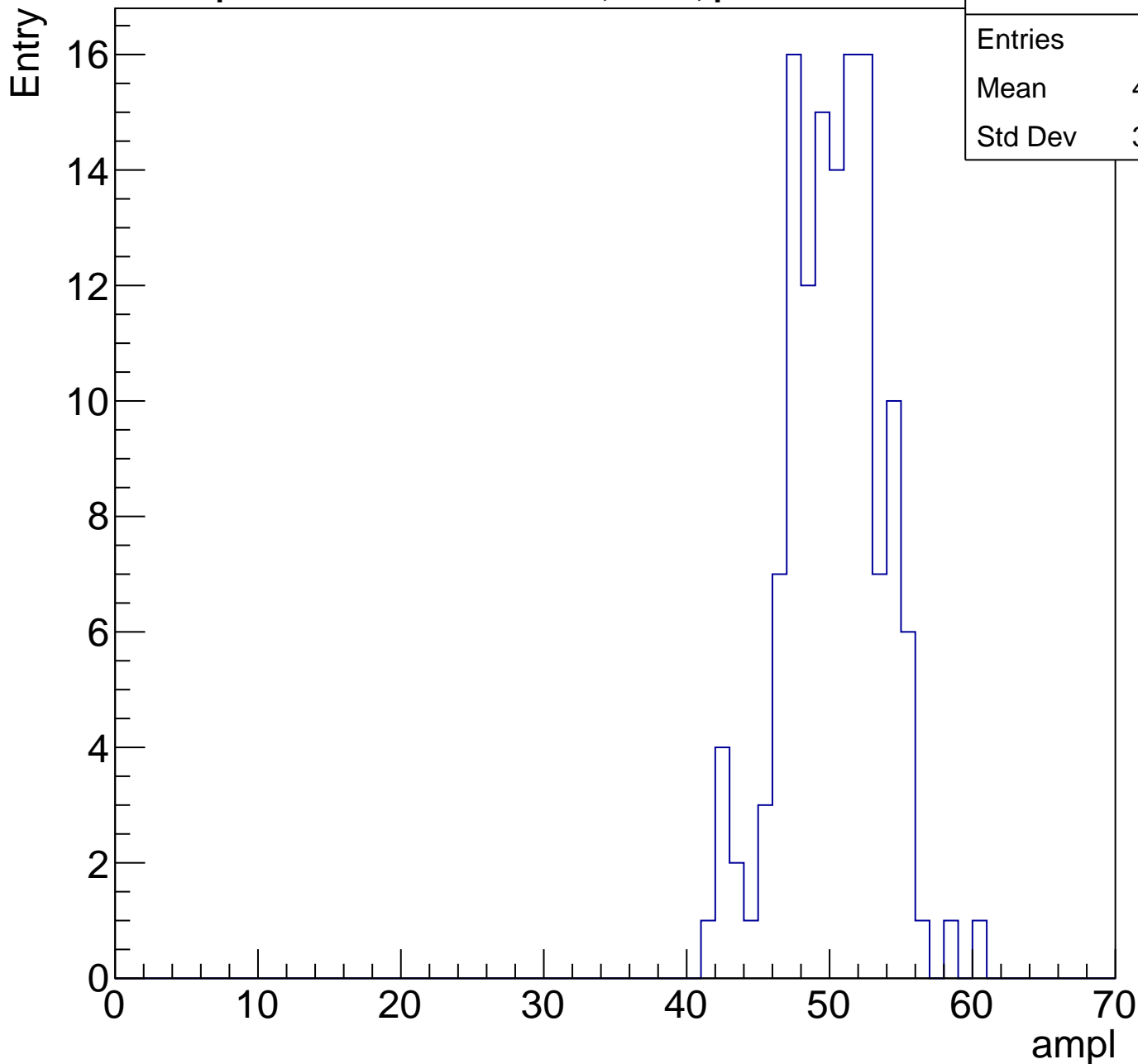
Gaus Width: 3.3562



B0L101S, U25-ch123, adc3

calib_packv5_042523_0143.root, FC#1, port C1

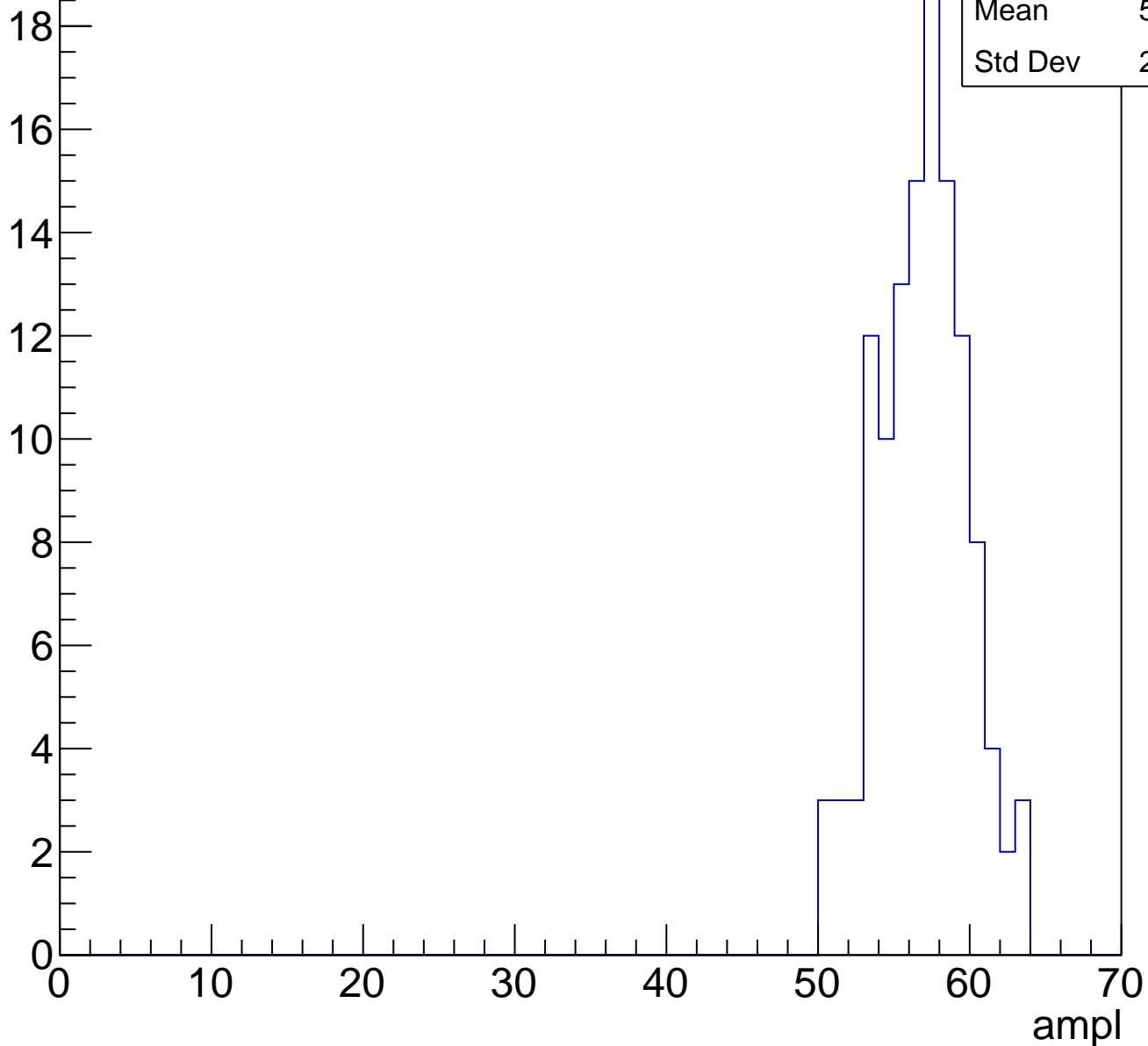
Entries	133
Mean	49.79
Std Dev	3.372



B0L101S, U25-ch123, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch123, adc5

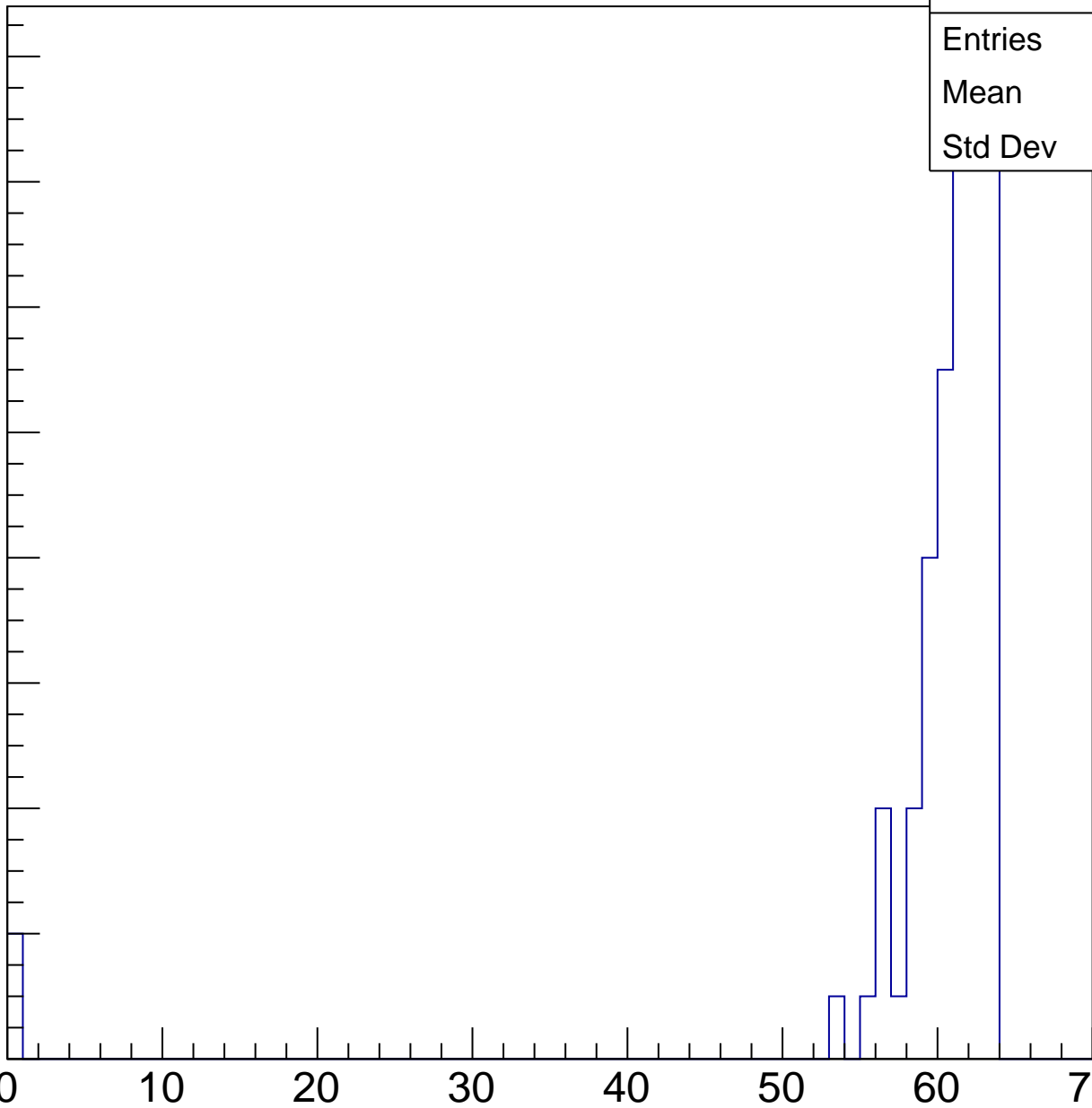
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.04
Std Dev	9.814

ampl



B0L101S, U25-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L101S, U25-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch124, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	29.86
Std Dev	6.216

Gaus mean : 29.6979

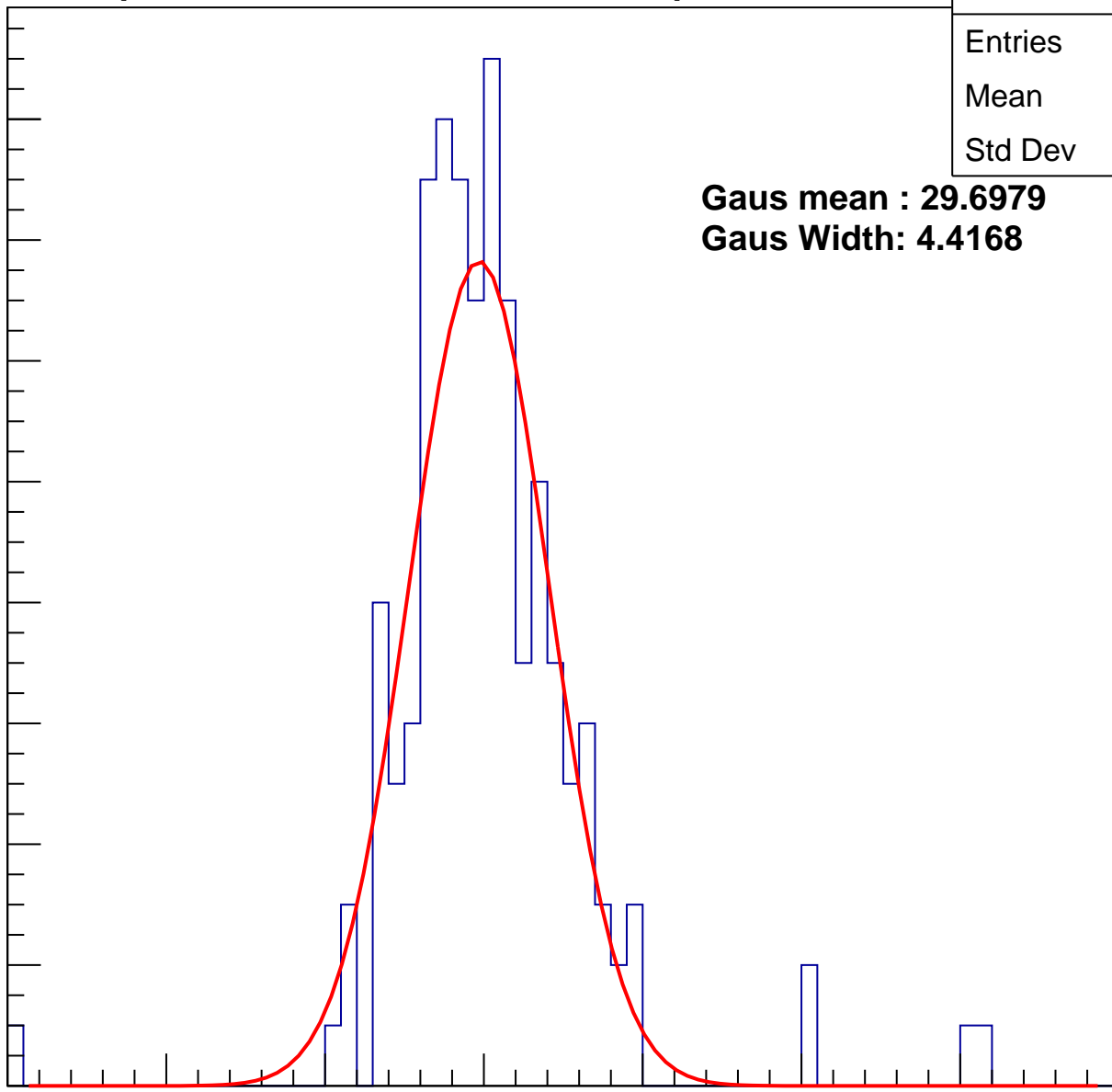
Gaus Width: 4.4168

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch124, adc1

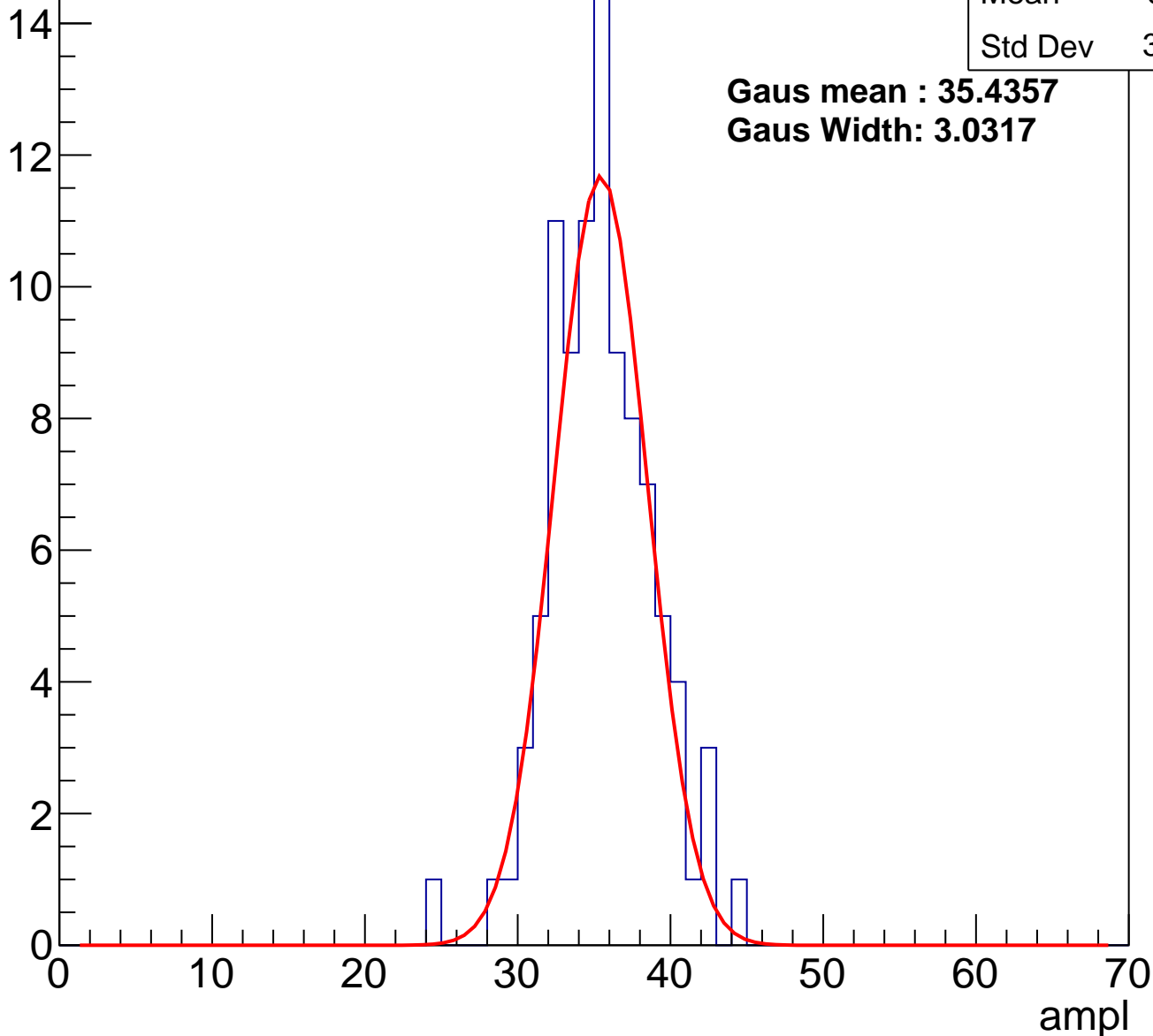
calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	35.01
Std Dev	3.332

Gaus mean : 35.4357

Gaus Width: 3.0317

Entry



B0L101S, U25-ch124, adc2

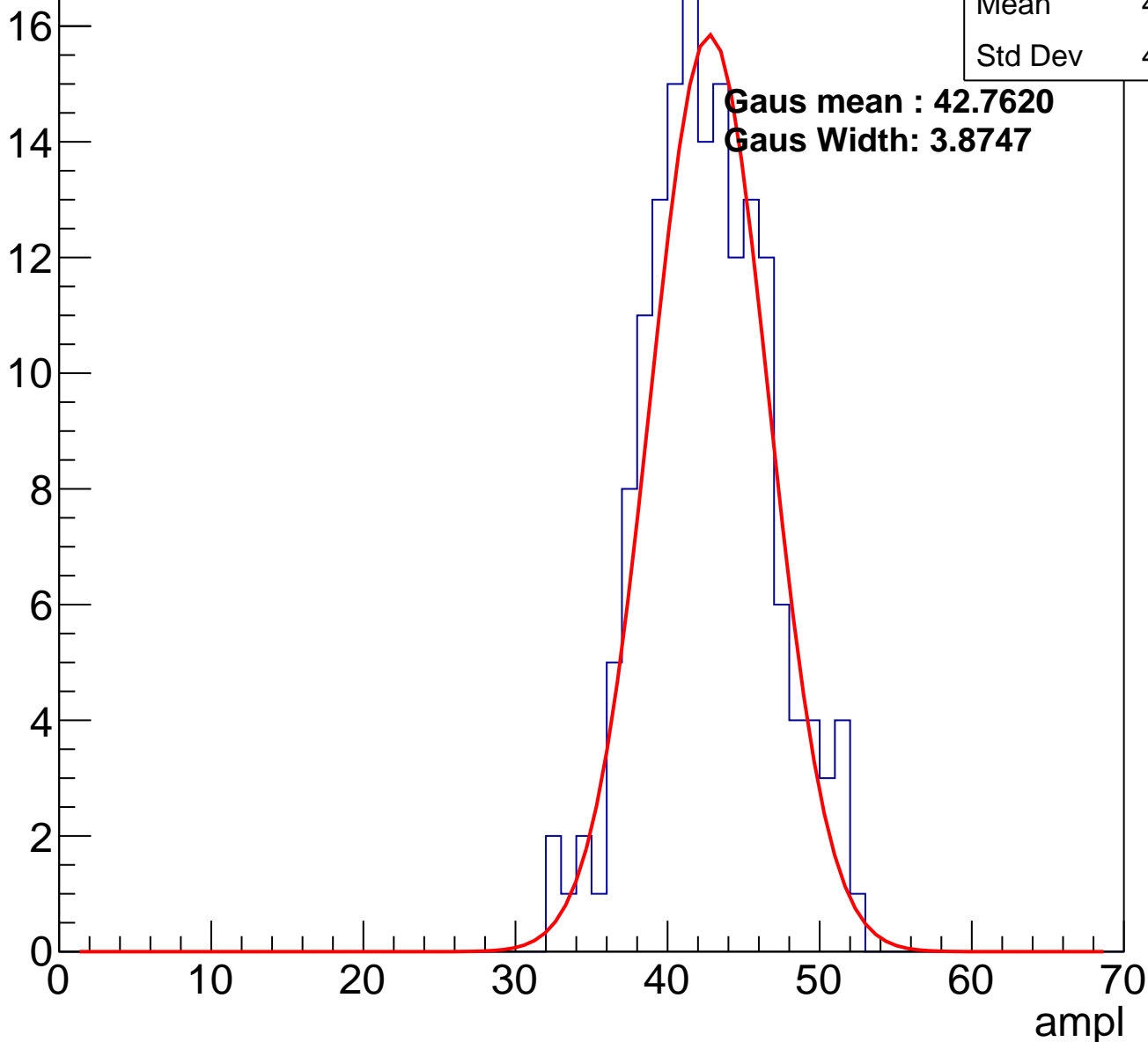
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	163
Mean	42.16
Std Dev	4.056

Gaus mean : 42.7620

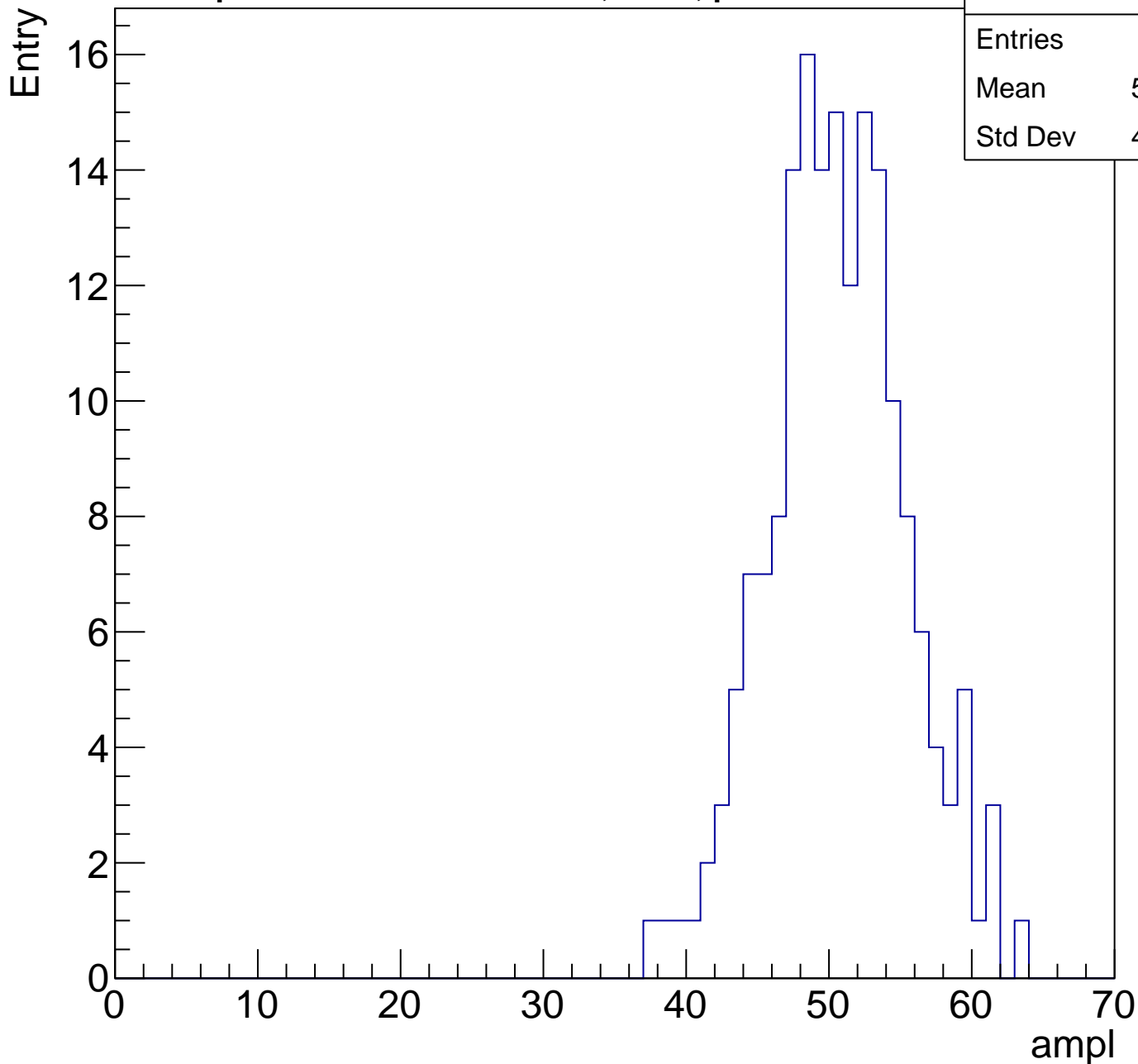
Gaus Width: 3.8747



B0L101S, U25-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	50.19
Std Dev	4.804



B0L101S, U25-ch124, adc4

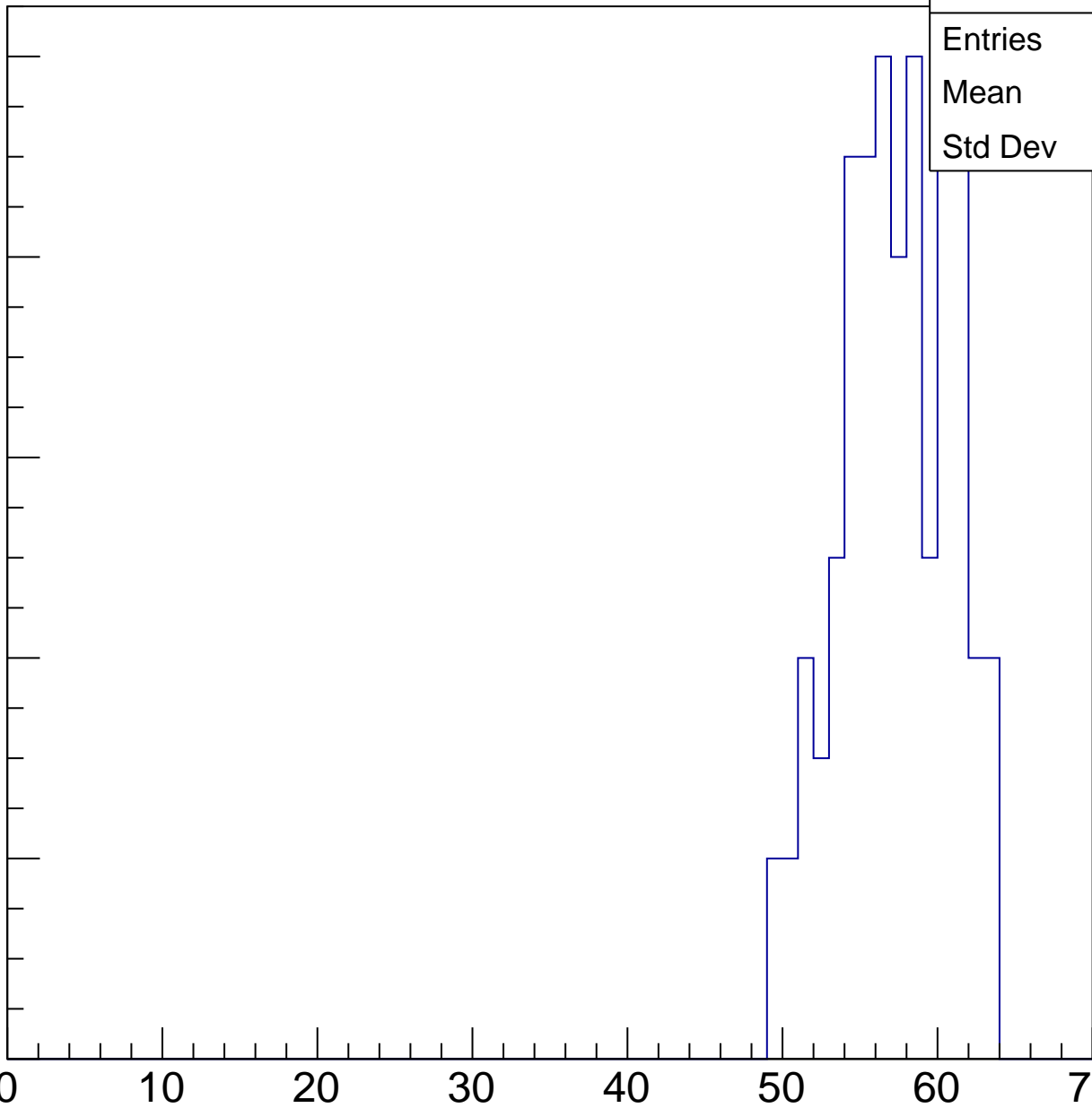
calib_packv5_042523_0143.root, FC#1, port C1

Entry

10
8
6
4
2
0

Entries	94
Mean	56.86
Std Dev	3.521

ampl



B0L101S, U25-ch124, adc5

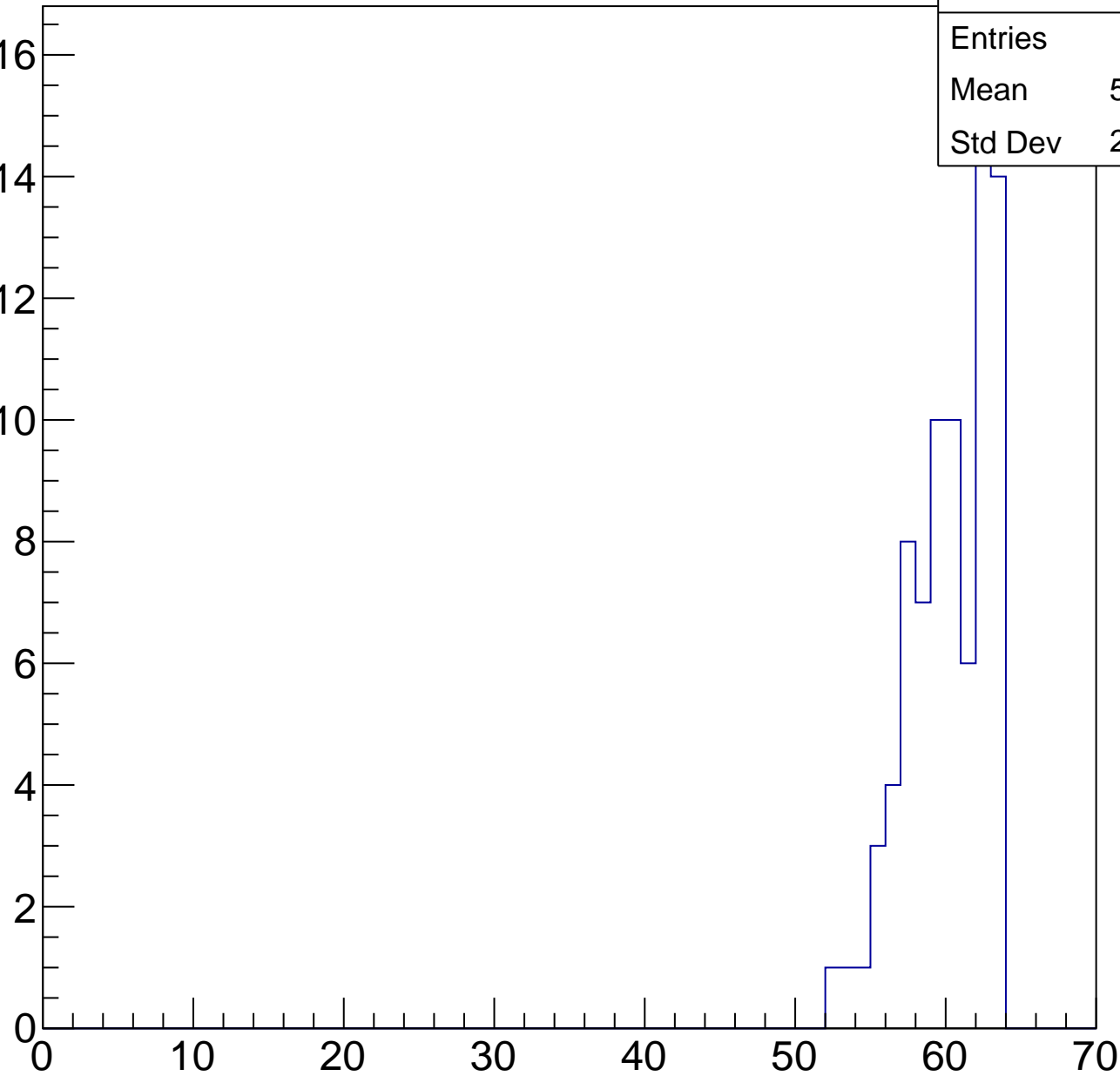
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	59.75
Std Dev	2.697

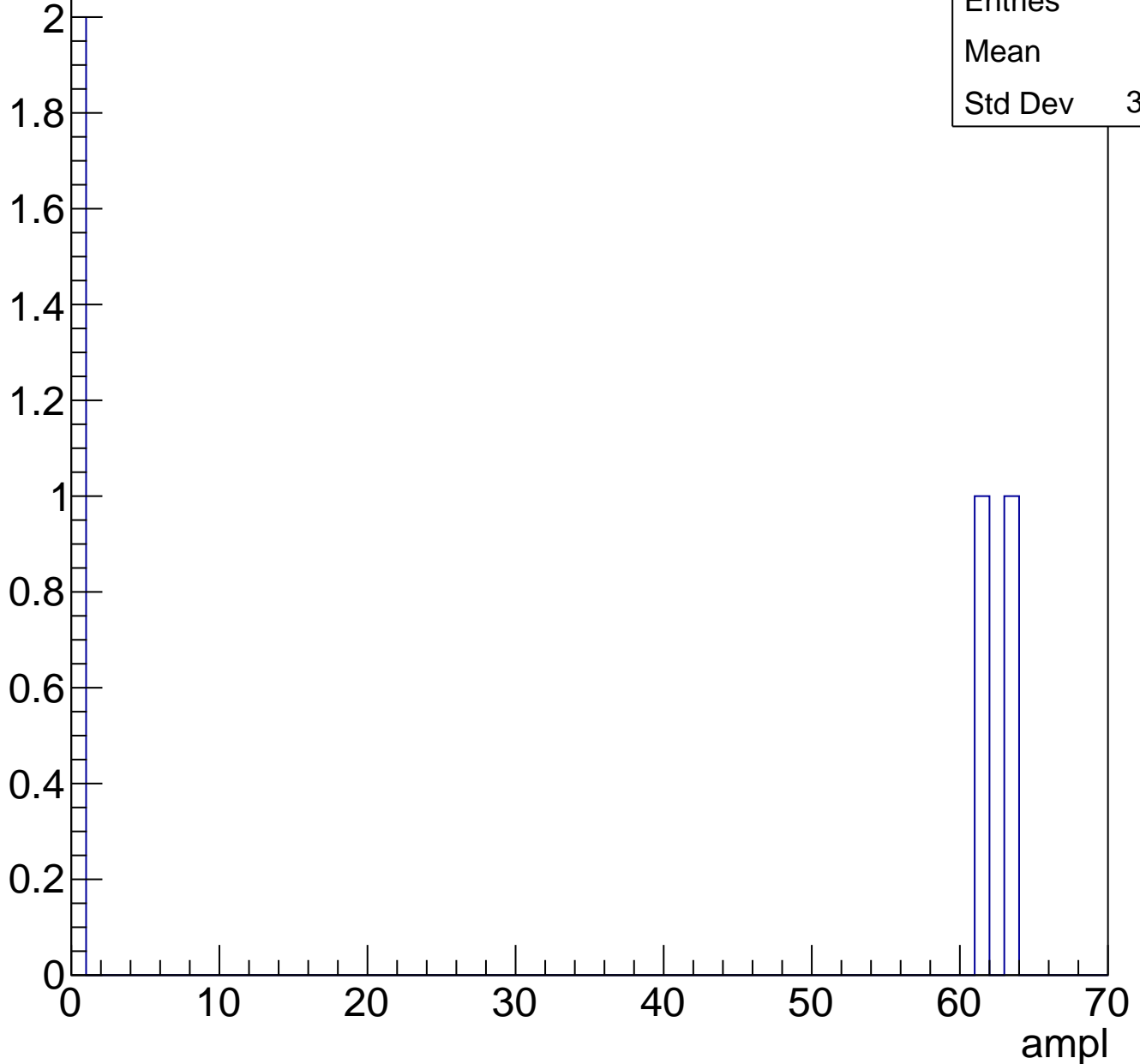
ampl



B0L101S, U25-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

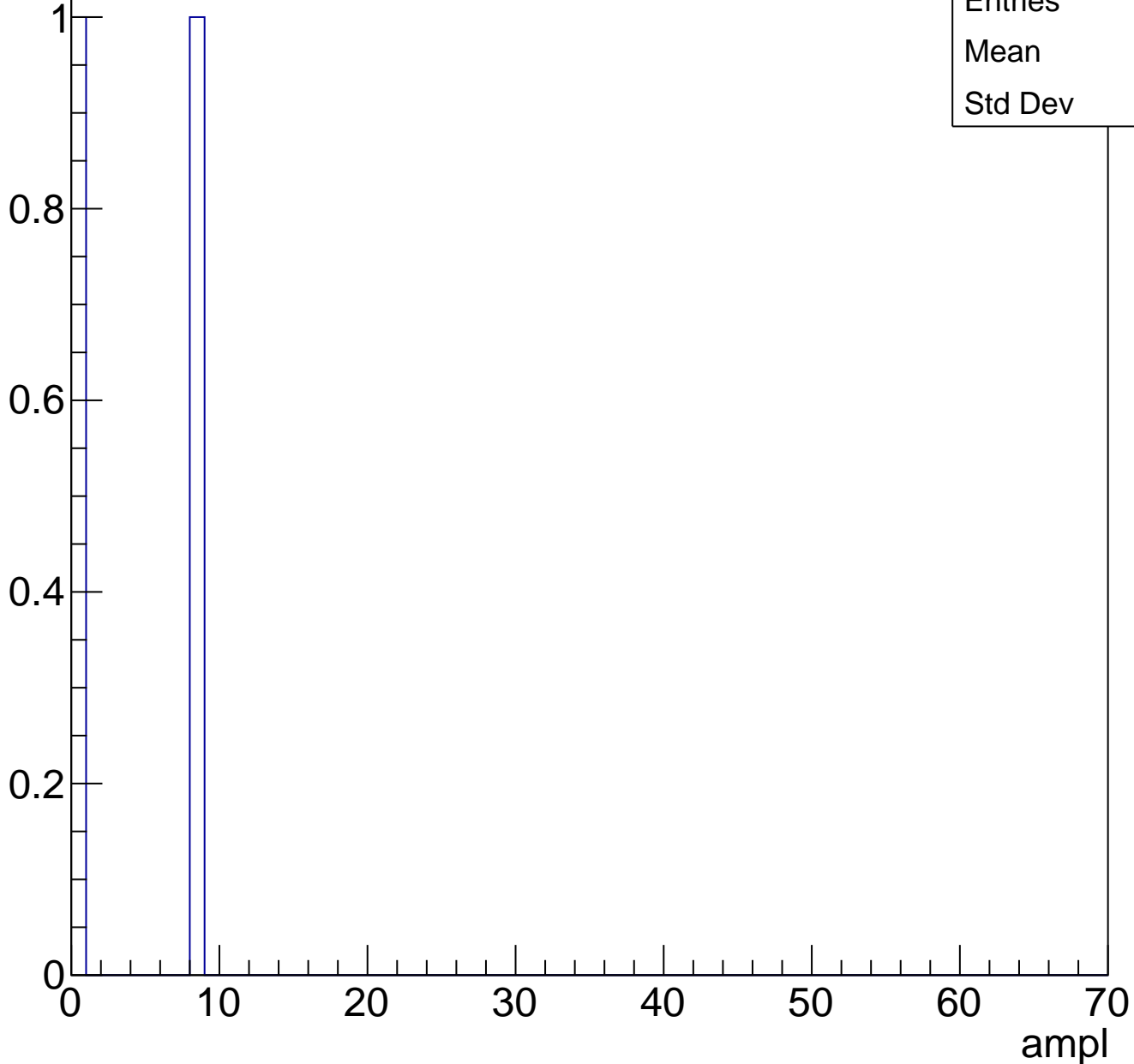
Entry



B0L101S, U25-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch125, adc0

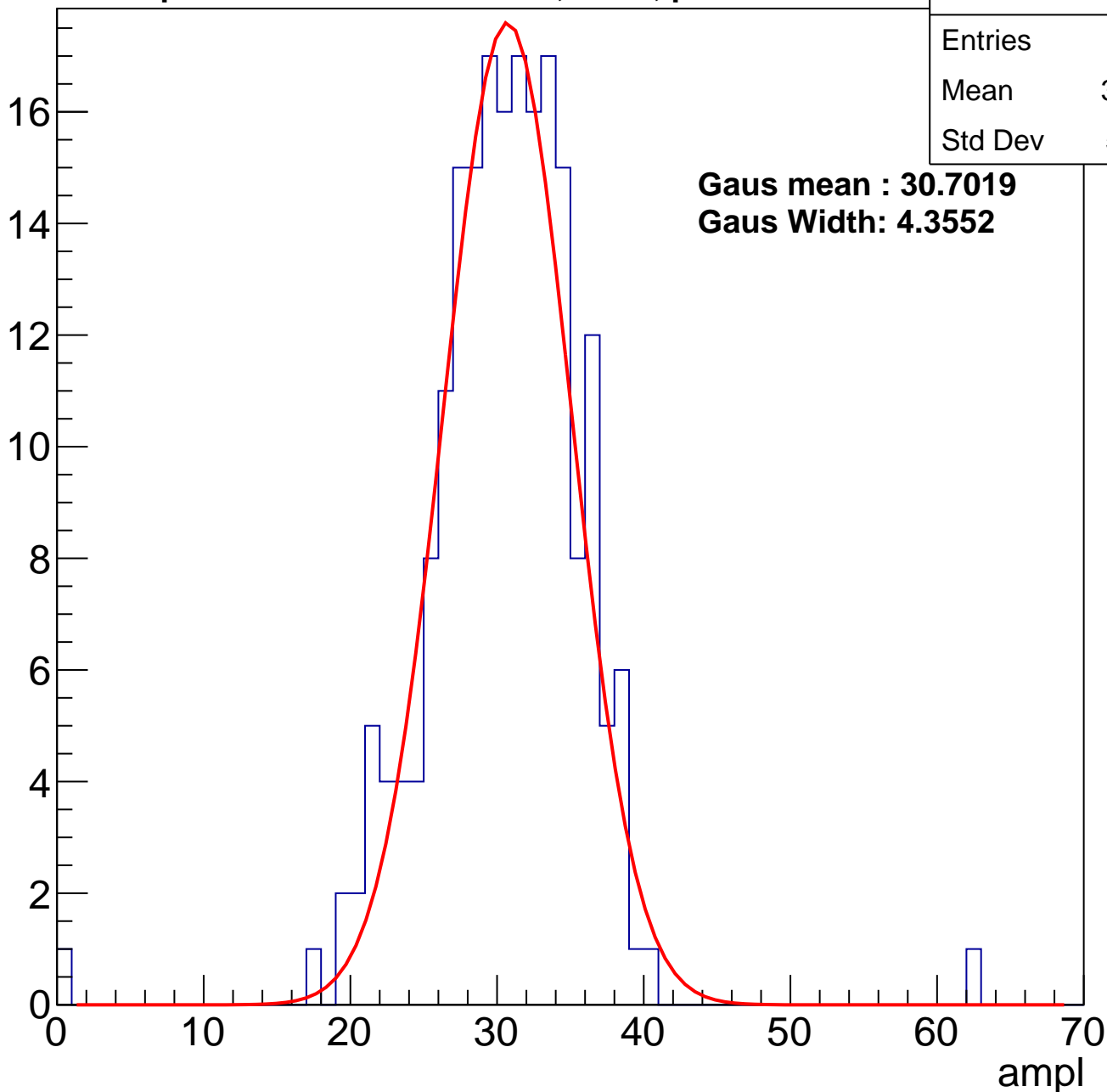
calib_packv5_042523_0143.root, FC#1, port C1

Entries	204
Mean	30.08
Std Dev	5.441

Gaus mean : 30.7019

Gaus Width: 4.3552

Entry



B0L101S, U25-ch125, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

119

Mean

38.17

Std Dev

3.677

Gaus mean : 39.0377

Gaus Width: 3.9505

0

10

20

30

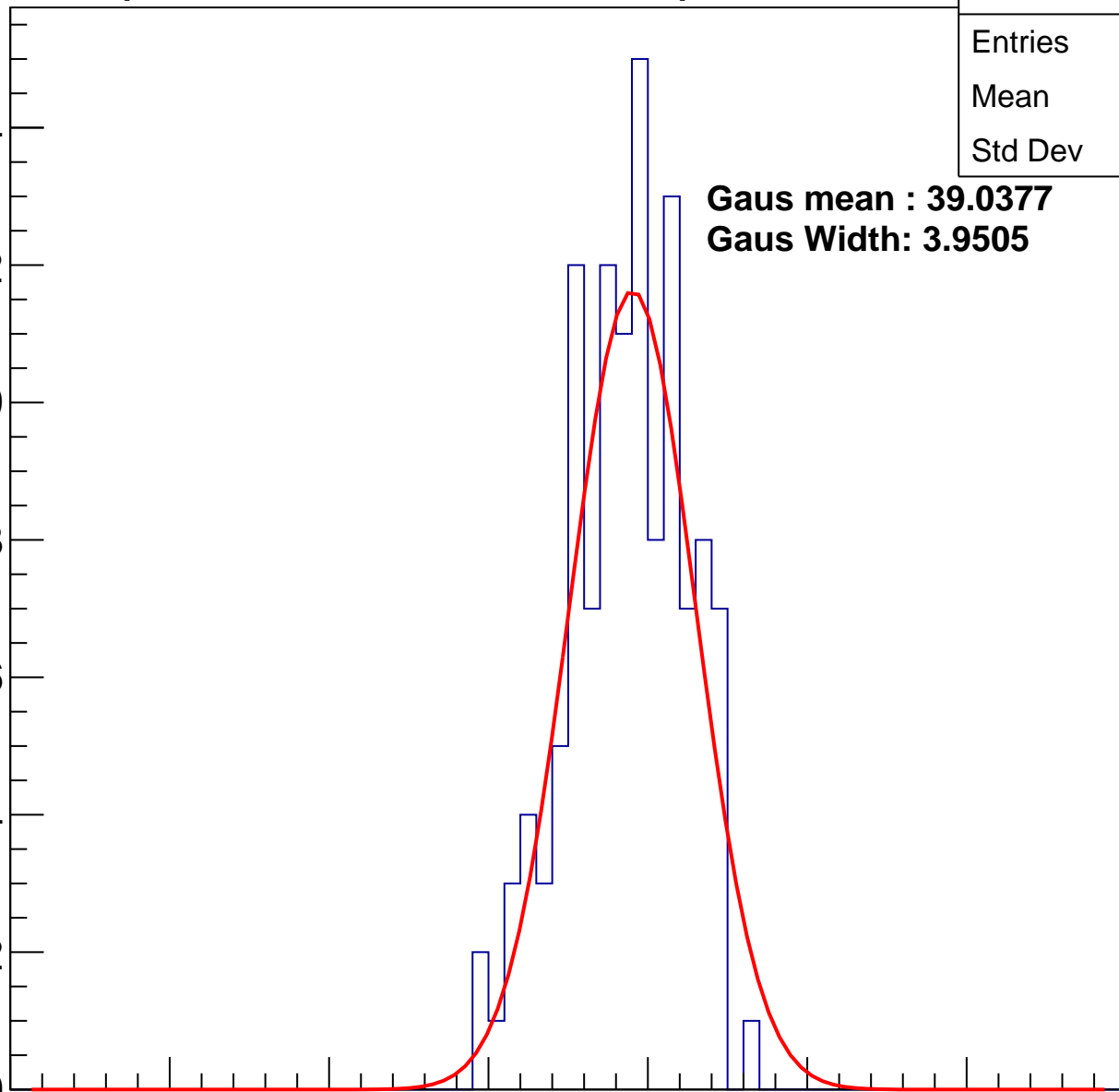
40

50

60

70

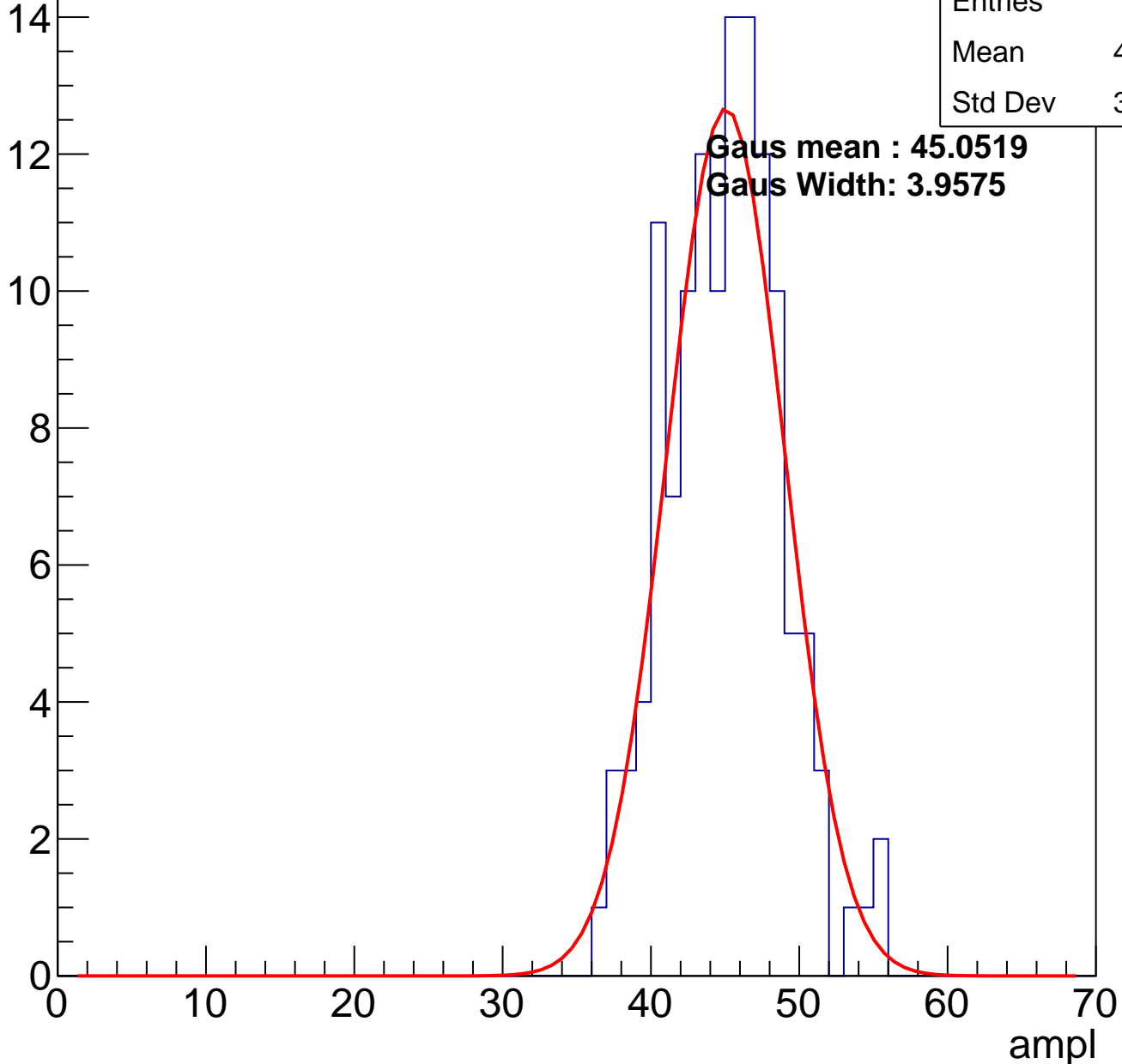
ampl



B0L101S, U25-ch125, adc2

calib_packv5_042523_0143.root, FC#1, port C1

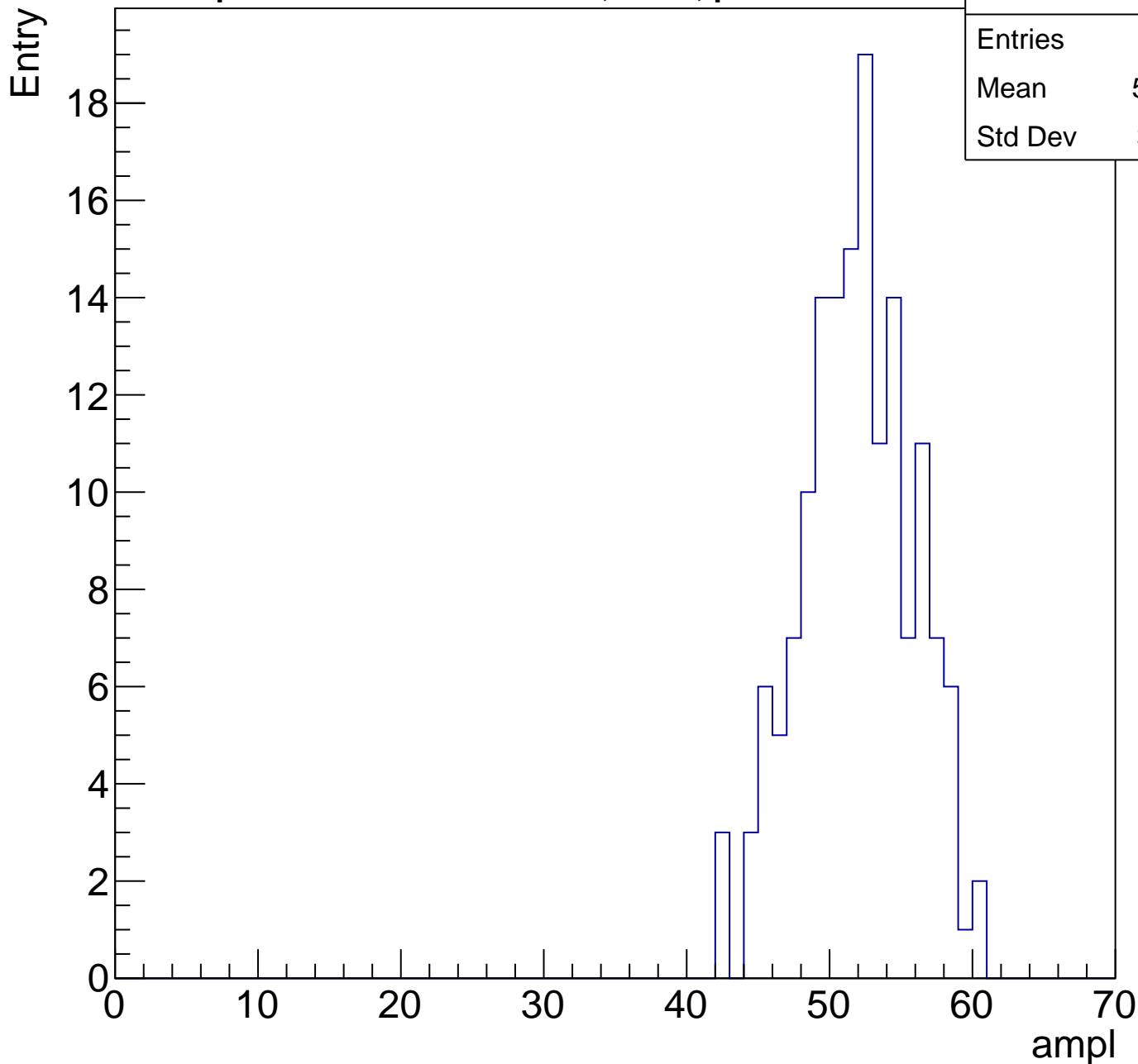
Entry



B0L101S, U25-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

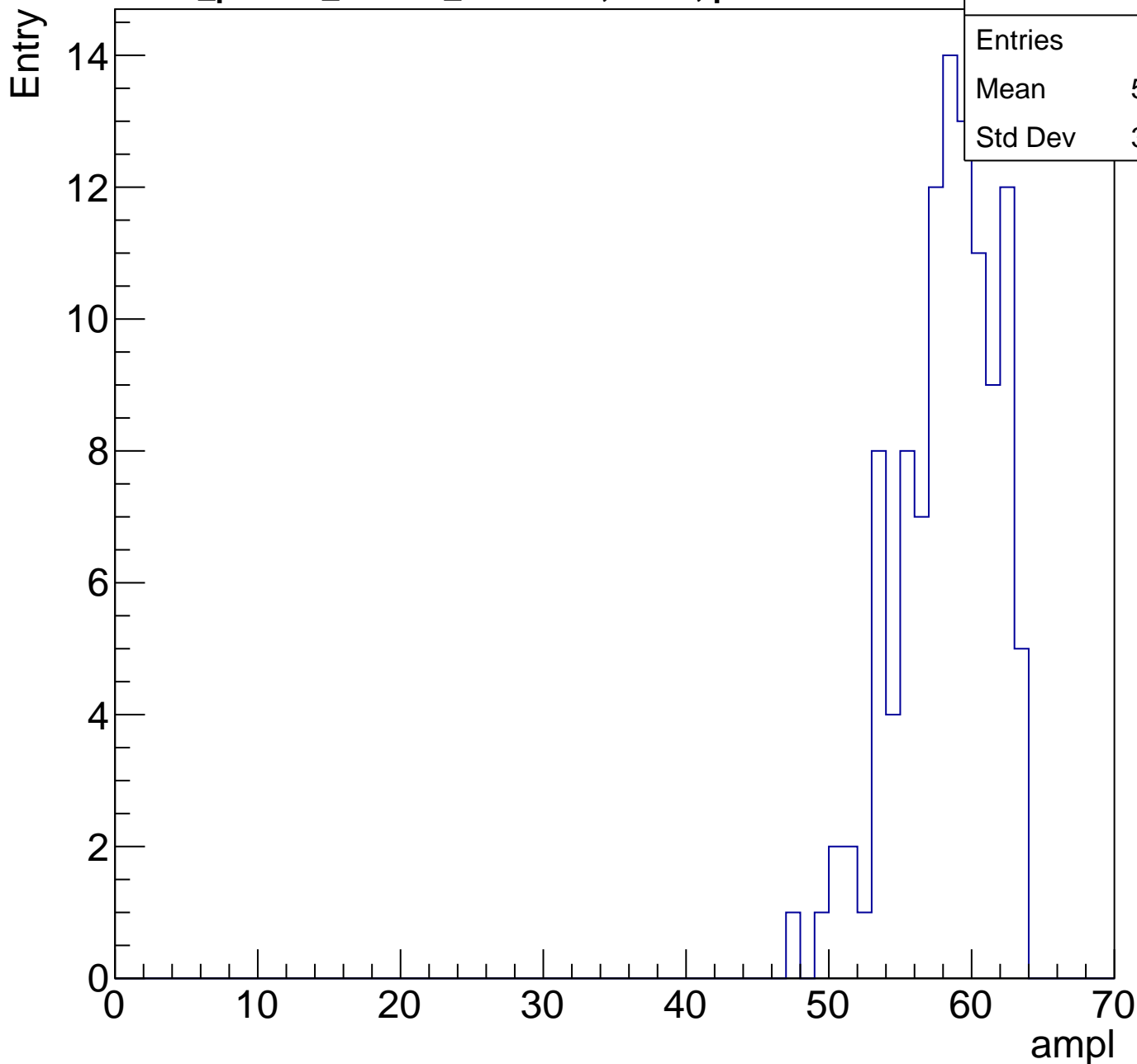
Entries	155
Mean	51.43
Std Dev	3.871



B0L101S, U25-ch125, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	57.75
Std Dev	3.422



B0L101S, U25-ch125, adc5

calib_packv5_042523_0143.root, FC#1, port C1

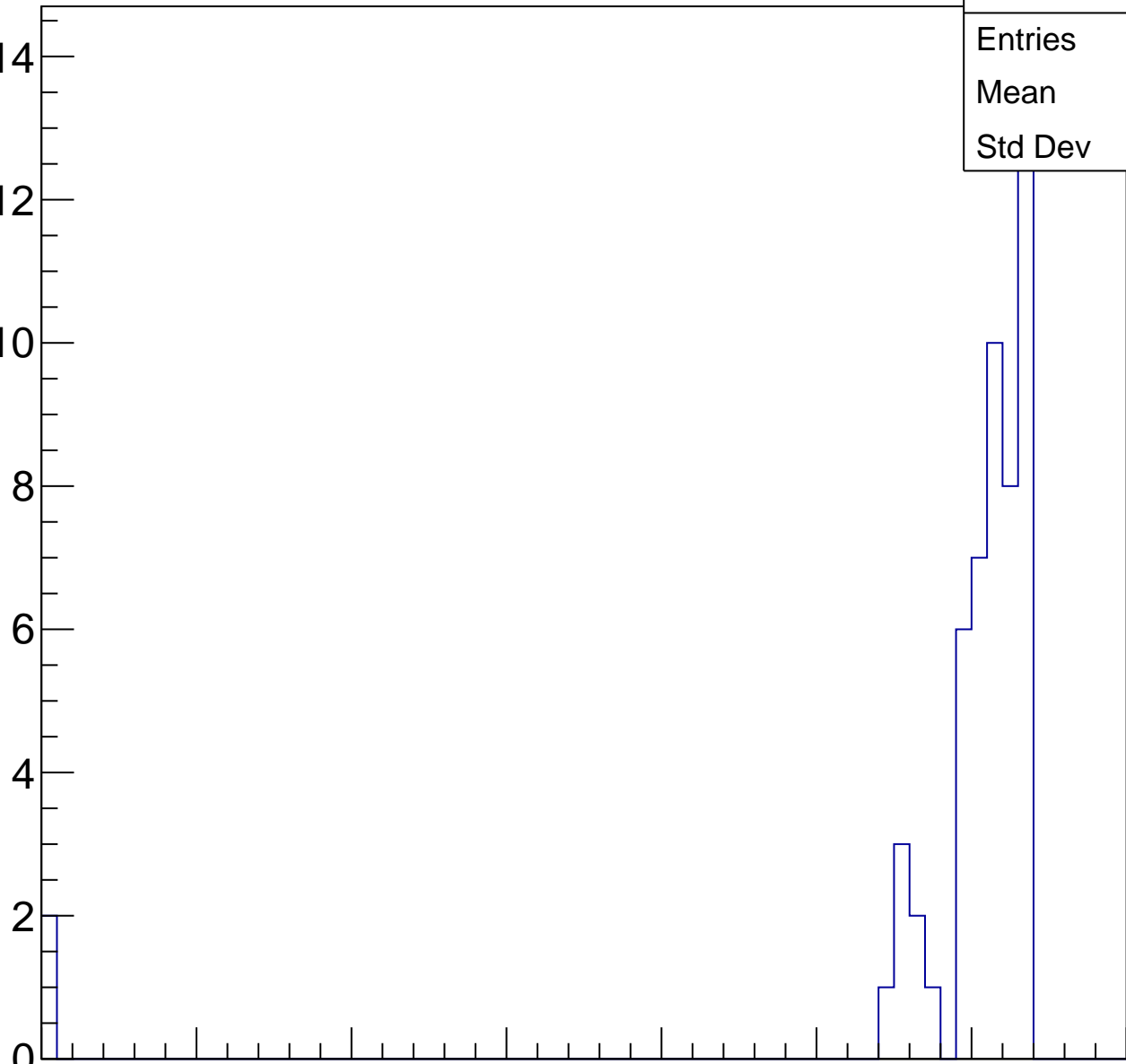
Entries	54
Mean	58.33
Std Dev	11.69

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U25-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U25-ch126, adc0

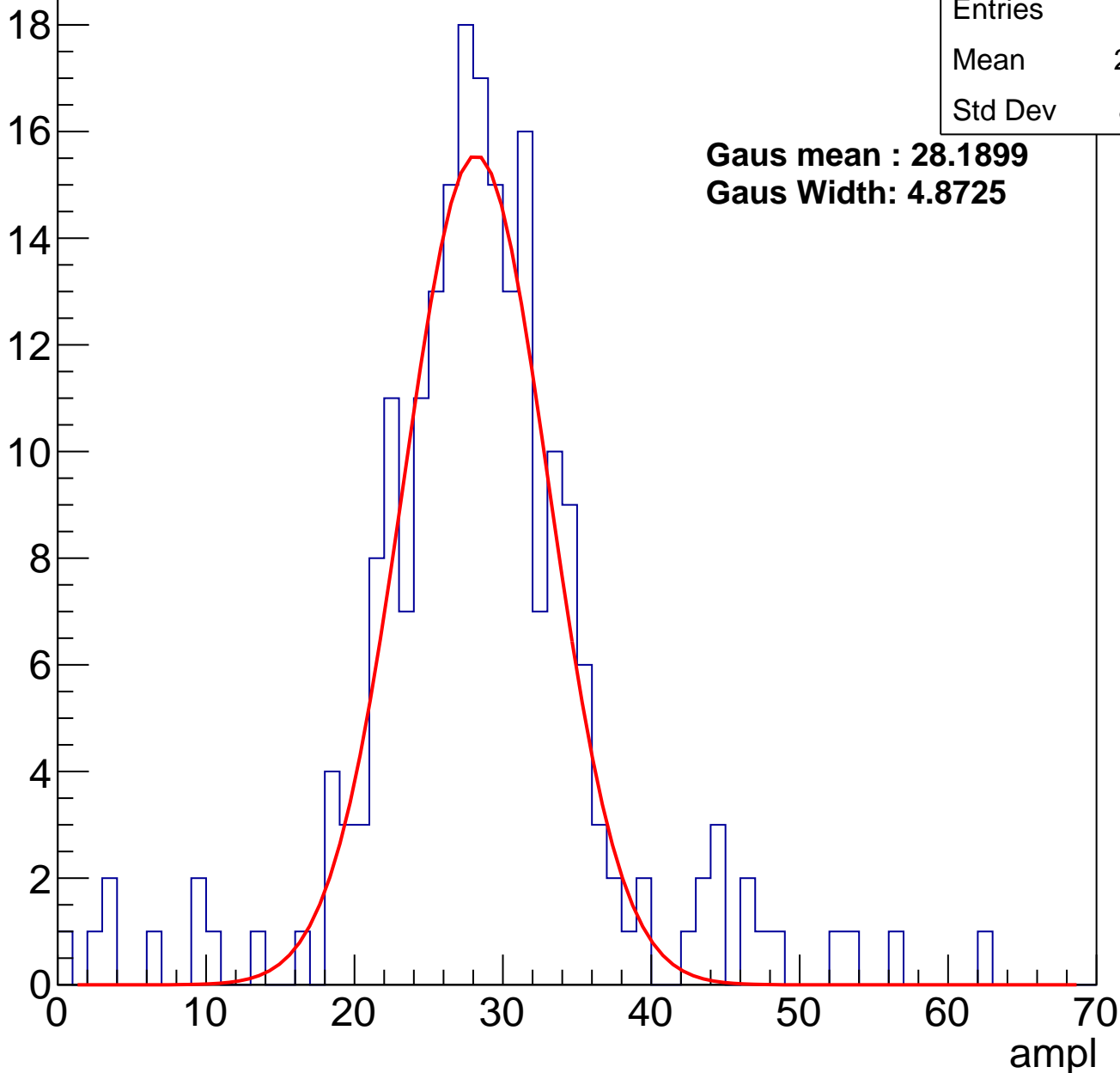
calib_packv5_042523_0143.root, FC#1, port C1

Entries	218
Mean	28.12
Std Dev	8.191

Gaus mean : 28.1899

Gaus Width: 4.8725

Entry



B0L101S, U25-ch126, adc1

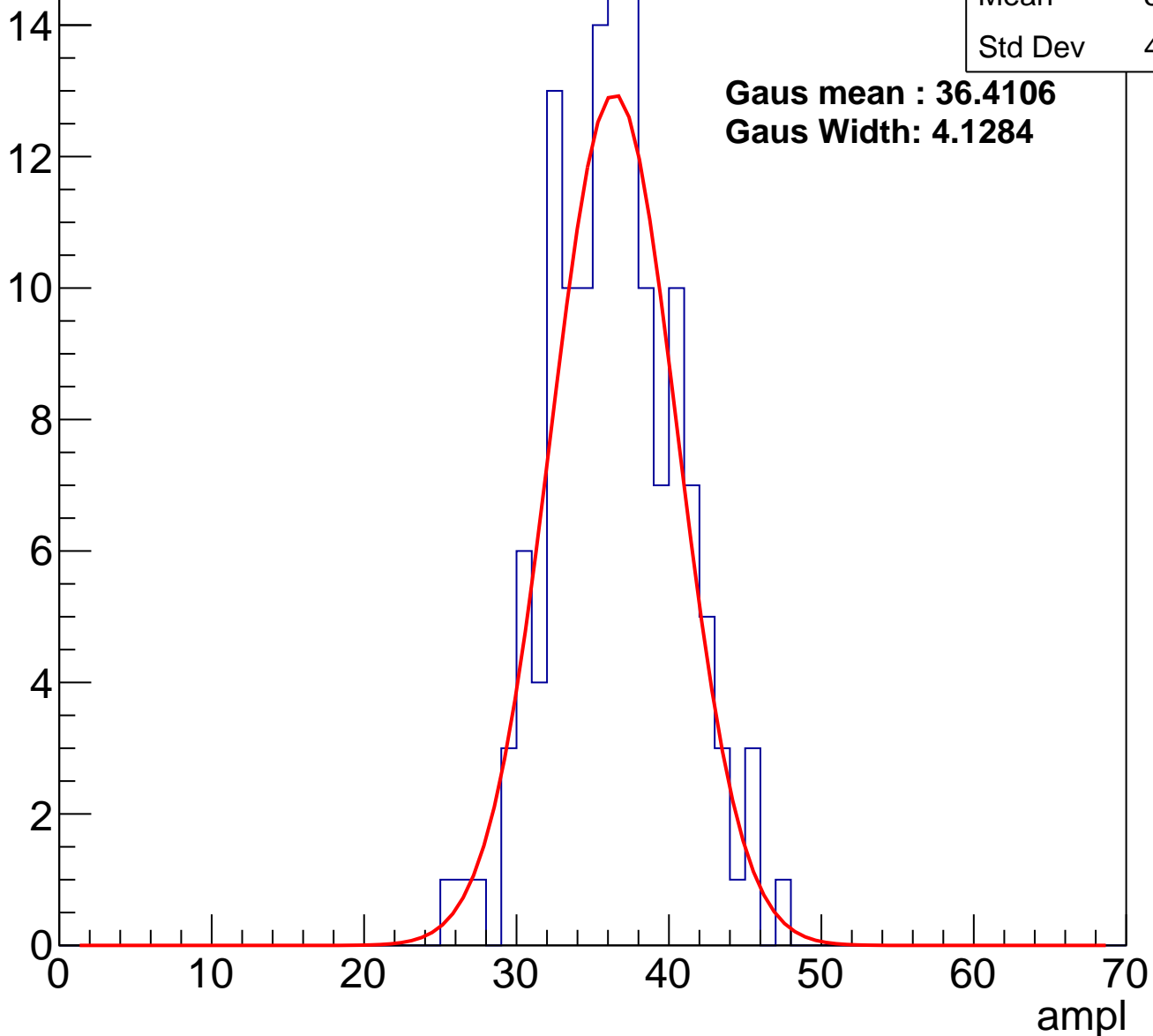
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	36.04
Std Dev	4.075

Gaus mean : 36.4106

Gaus Width: 4.1284

Entry



B0L101S, U25-ch126, adc2

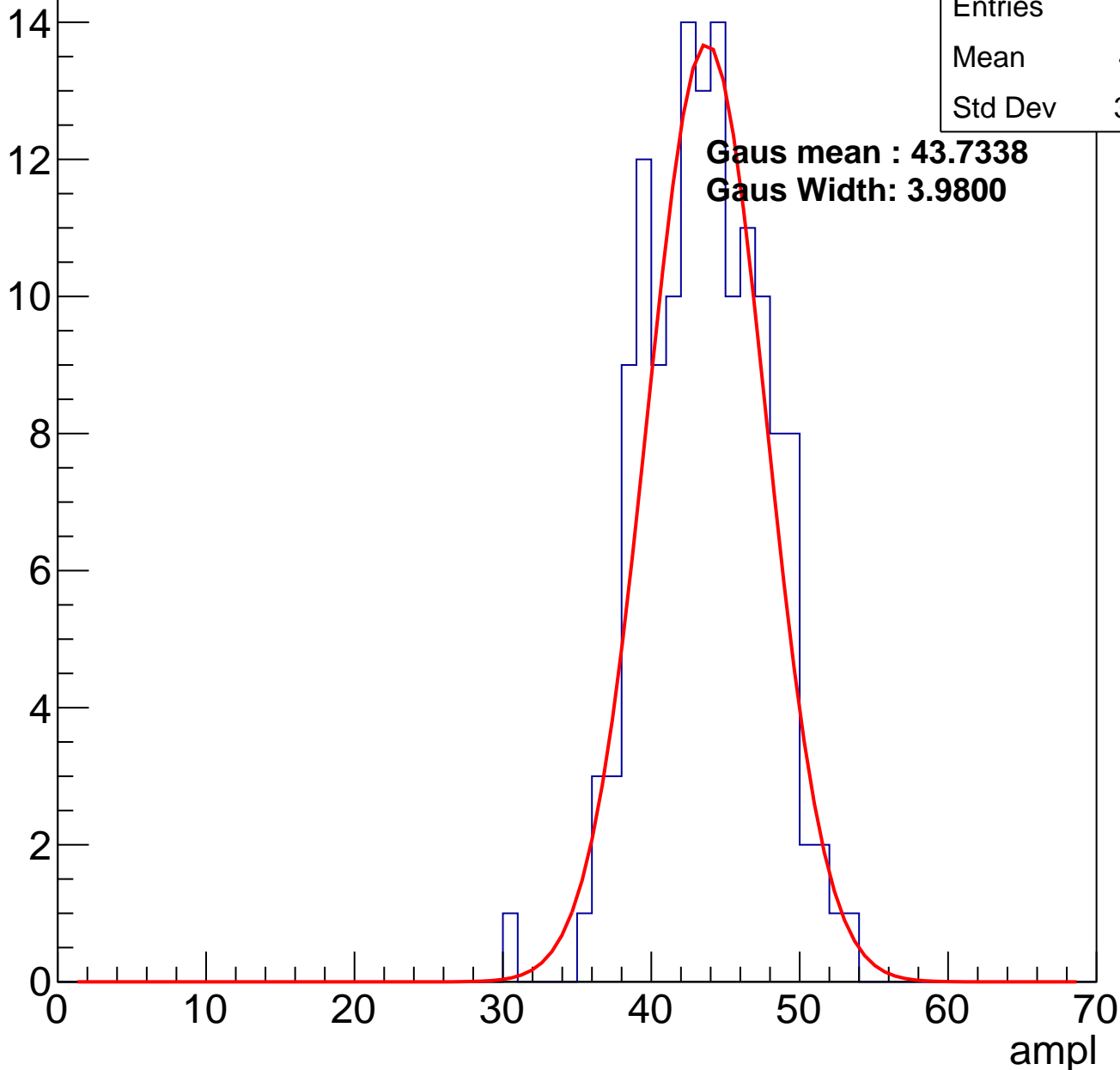
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	142
Mean	43.21
Std Dev	3.973

Gaus mean : 43.7338

Gaus Width: 3.9800



B0L101S, U25-ch126, adc3

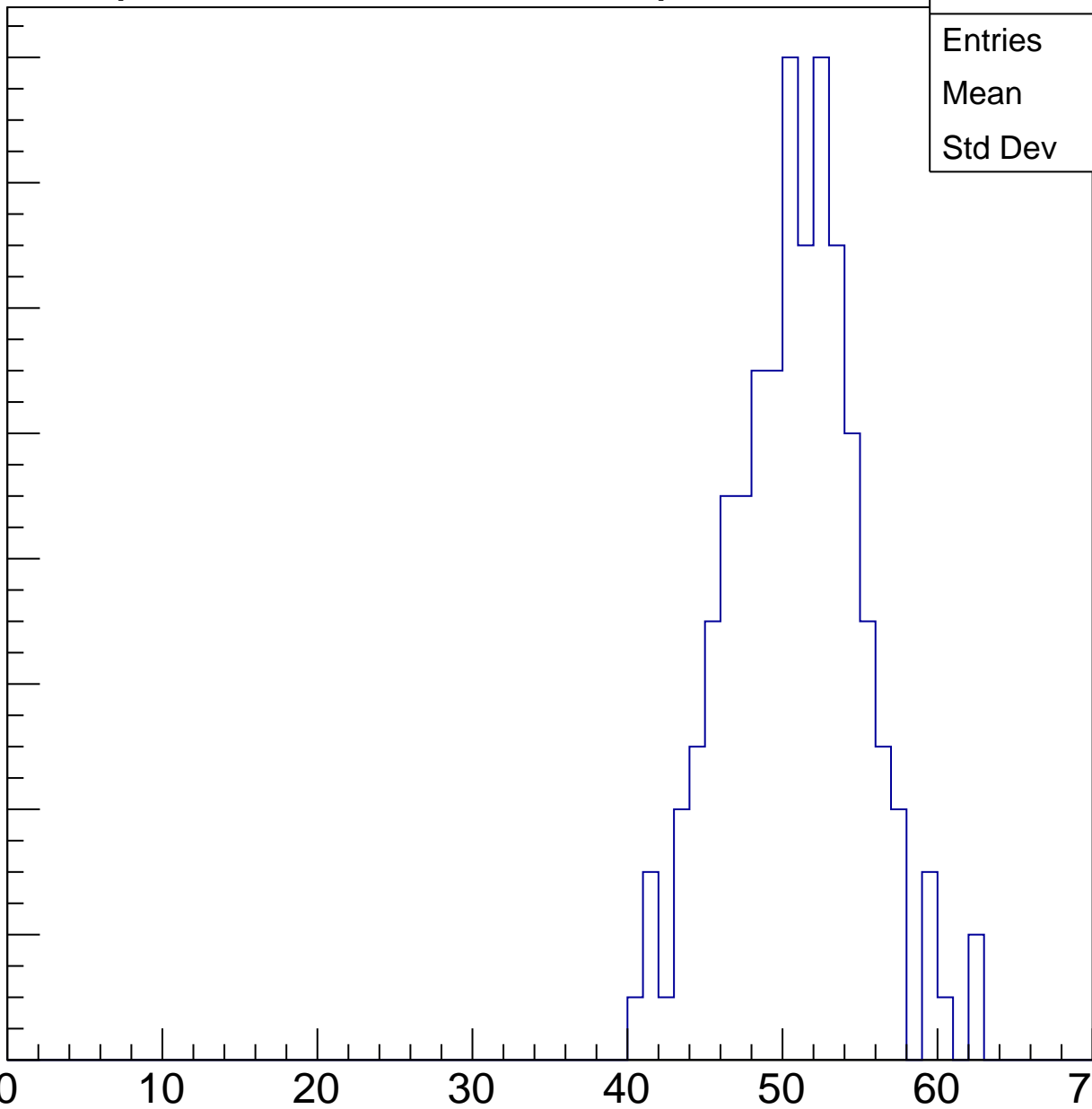
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	151
Mean	50.29
Std Dev	4.276

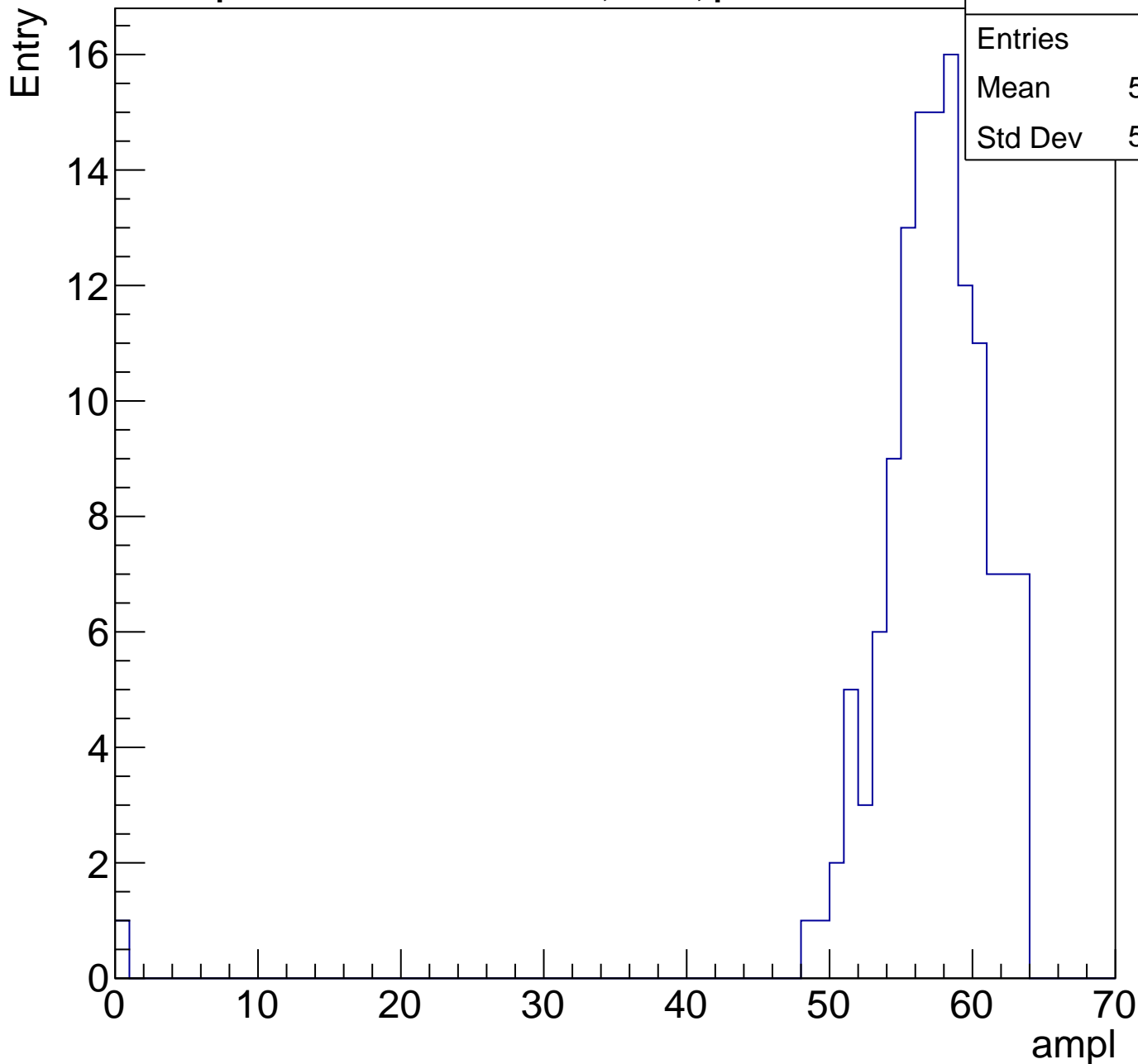
ampl



B0L101S, U25-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	56.64
Std Dev	5.975

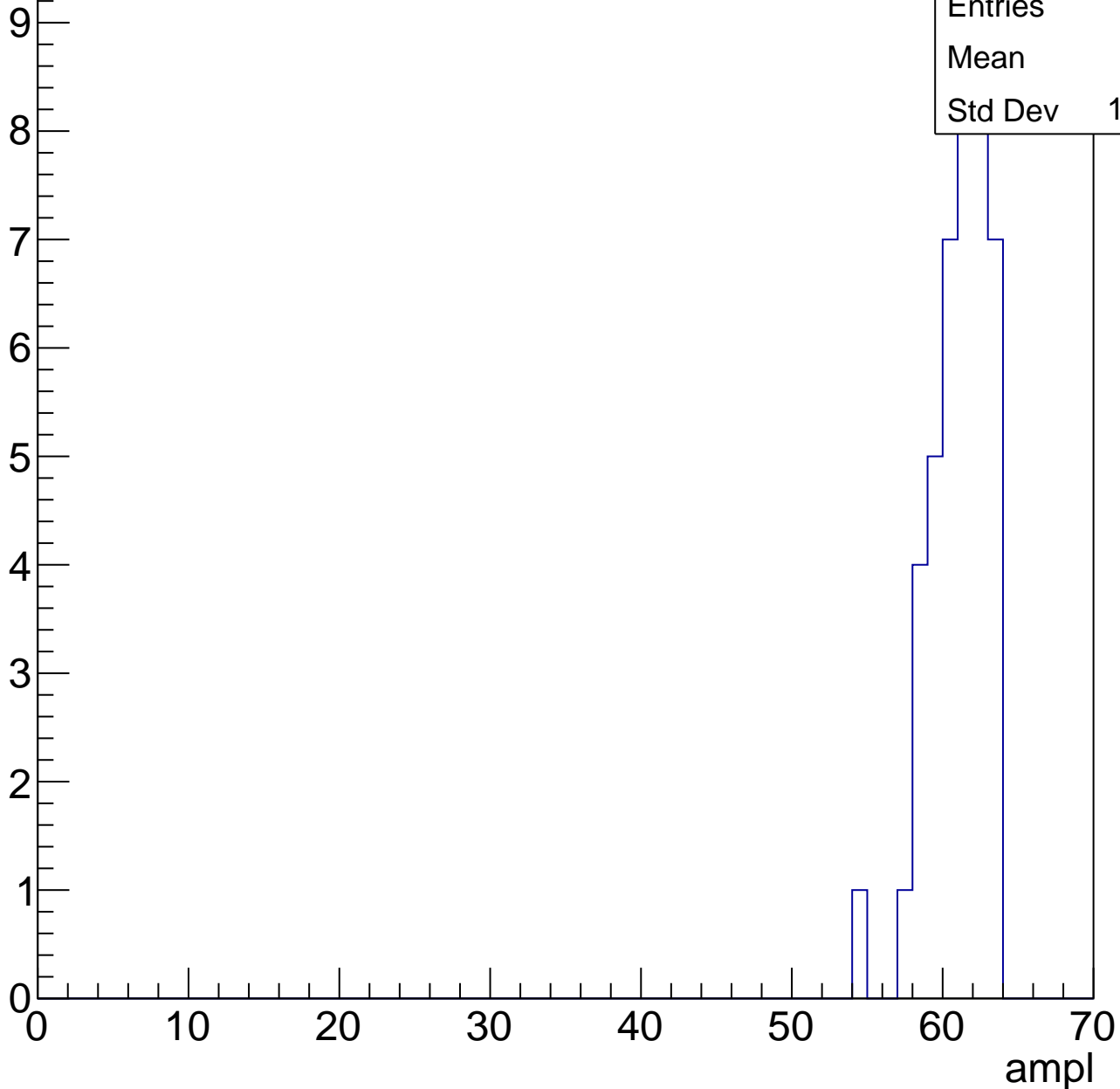


B0L101S, U25-ch126, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	43
Mean	60.6
Std Dev	1.919

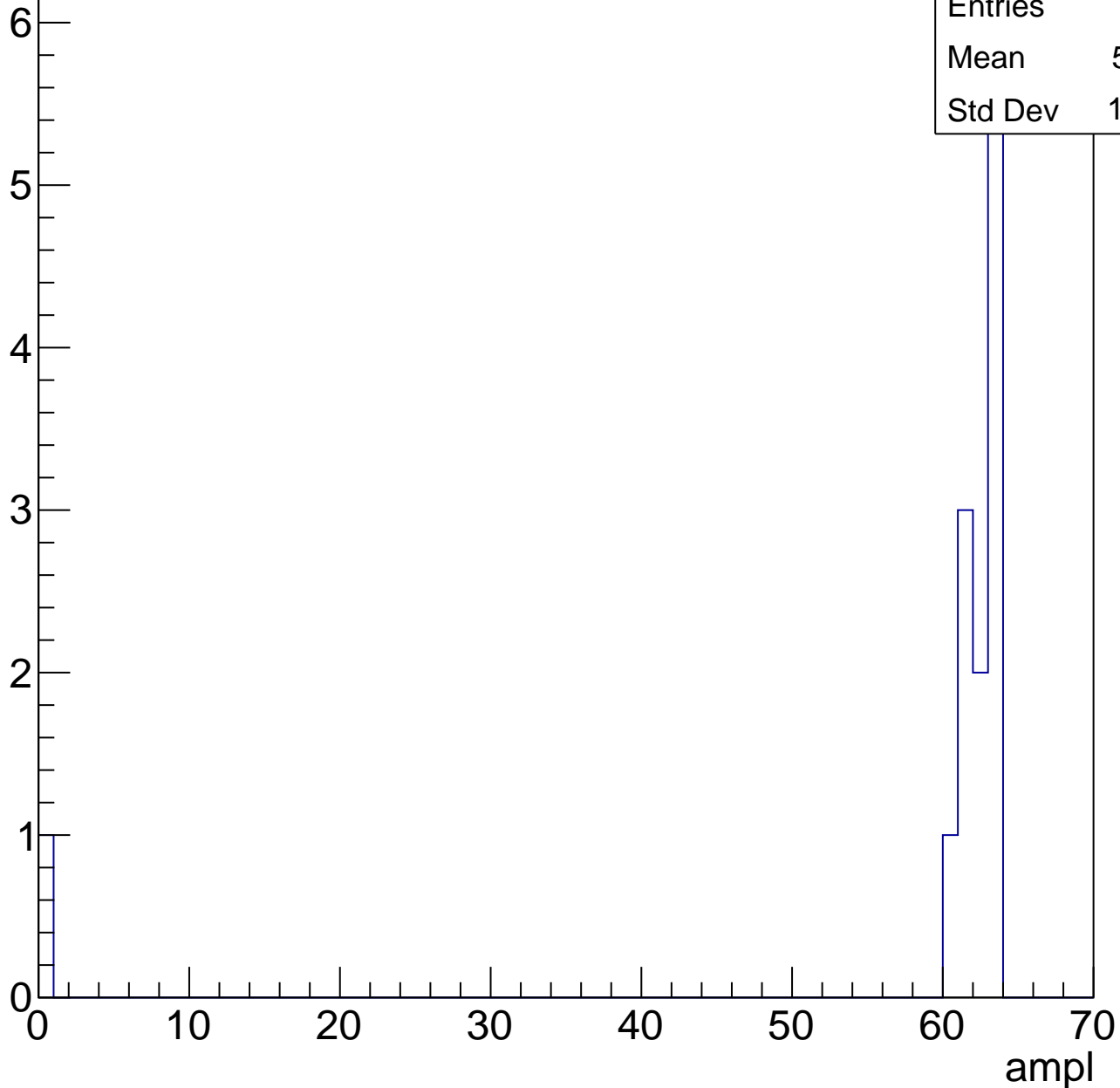


B0L101S, U25-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

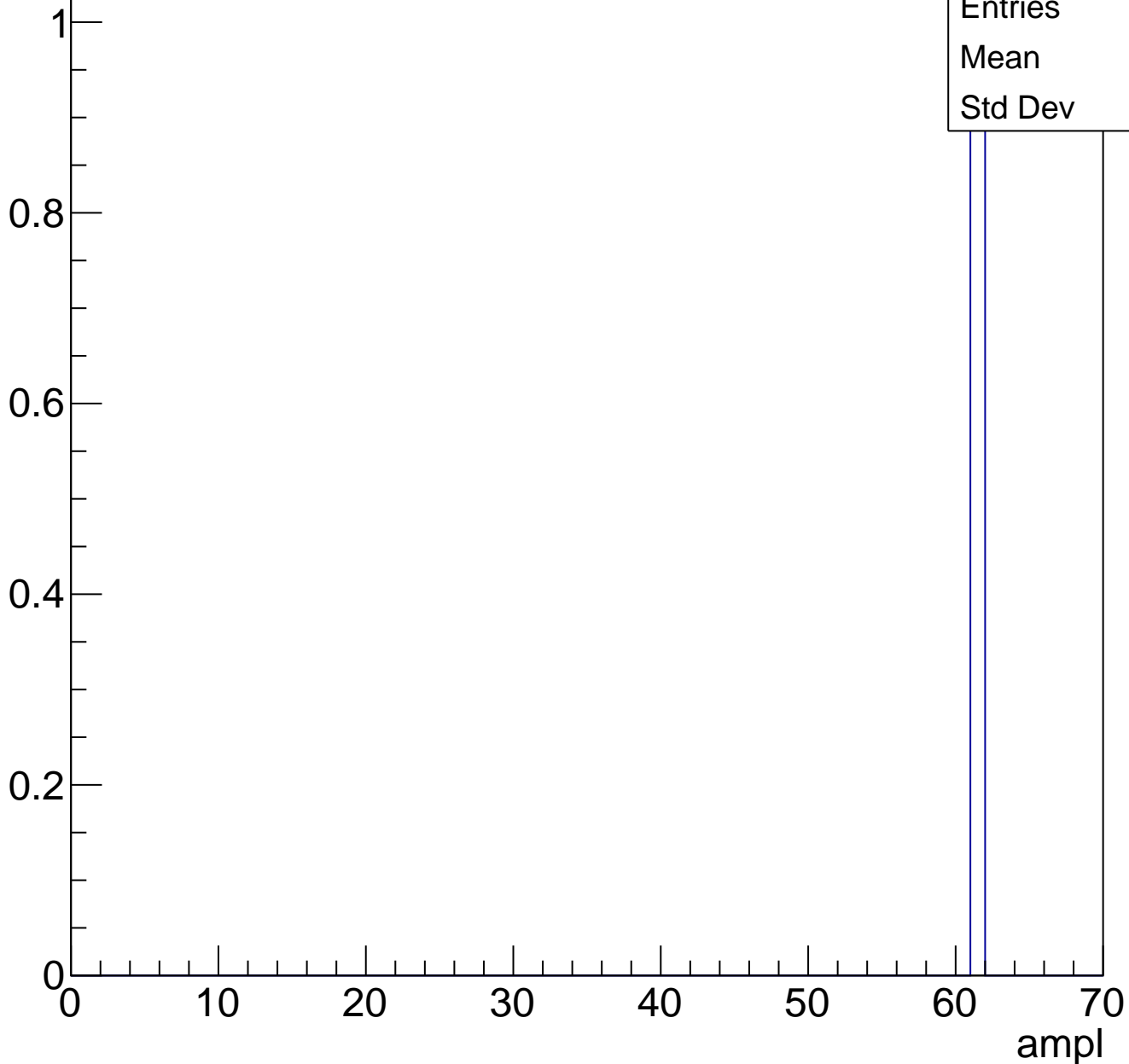
Entries	13
Mean	57.31
Std Dev	16.57



B0L101S, U25-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch127, adc0

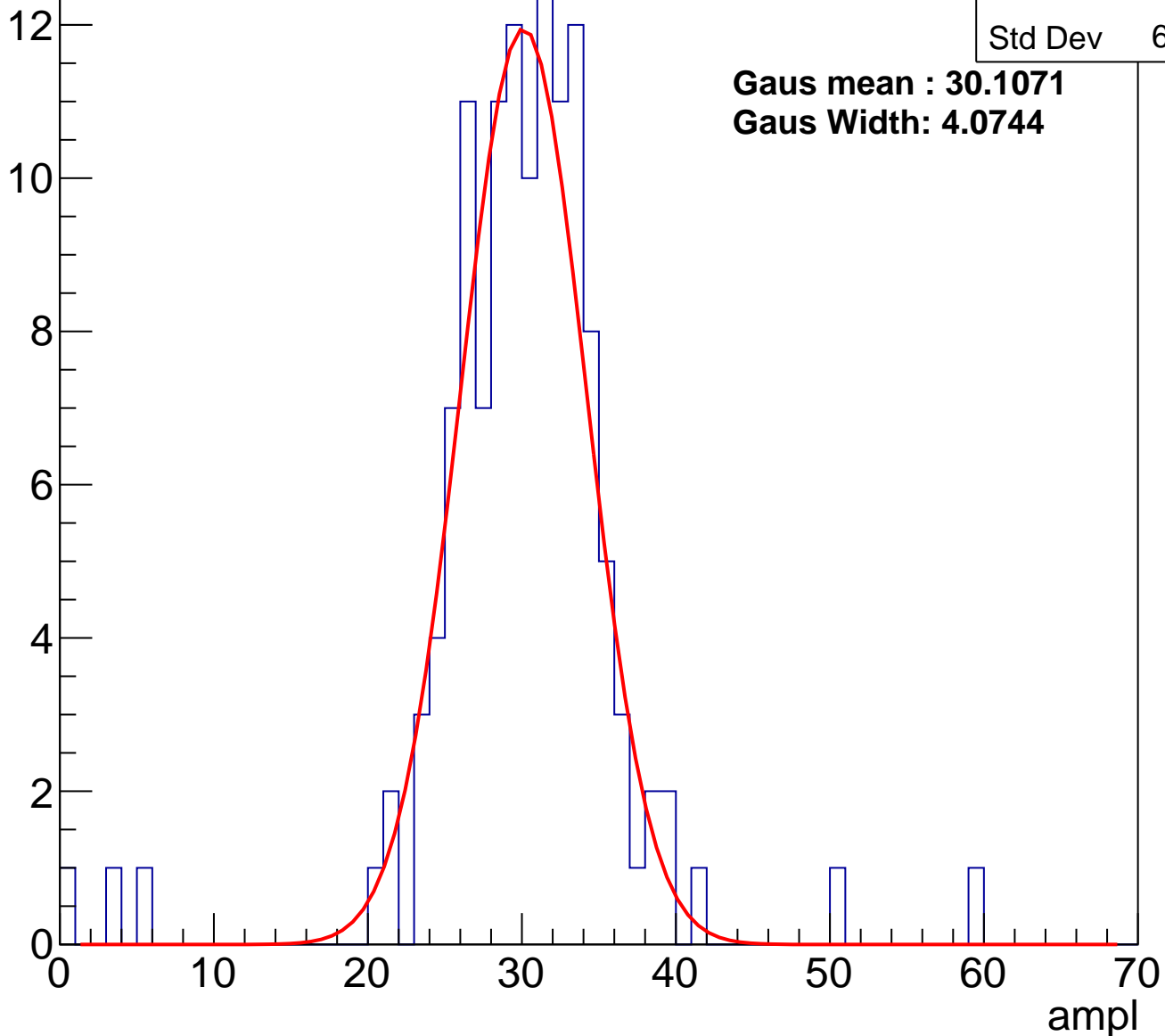
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.68
Std Dev	6.474

Gaus mean : 30.1071

Gaus Width: 4.0744

Entry



B0L101S, U25-ch127, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	36.43
Std Dev	4.047

Gaus mean : 36.6647

Gaus Width: 3.9503

Entry

14

12

10

8

6

4

2

0

0

10

20

30

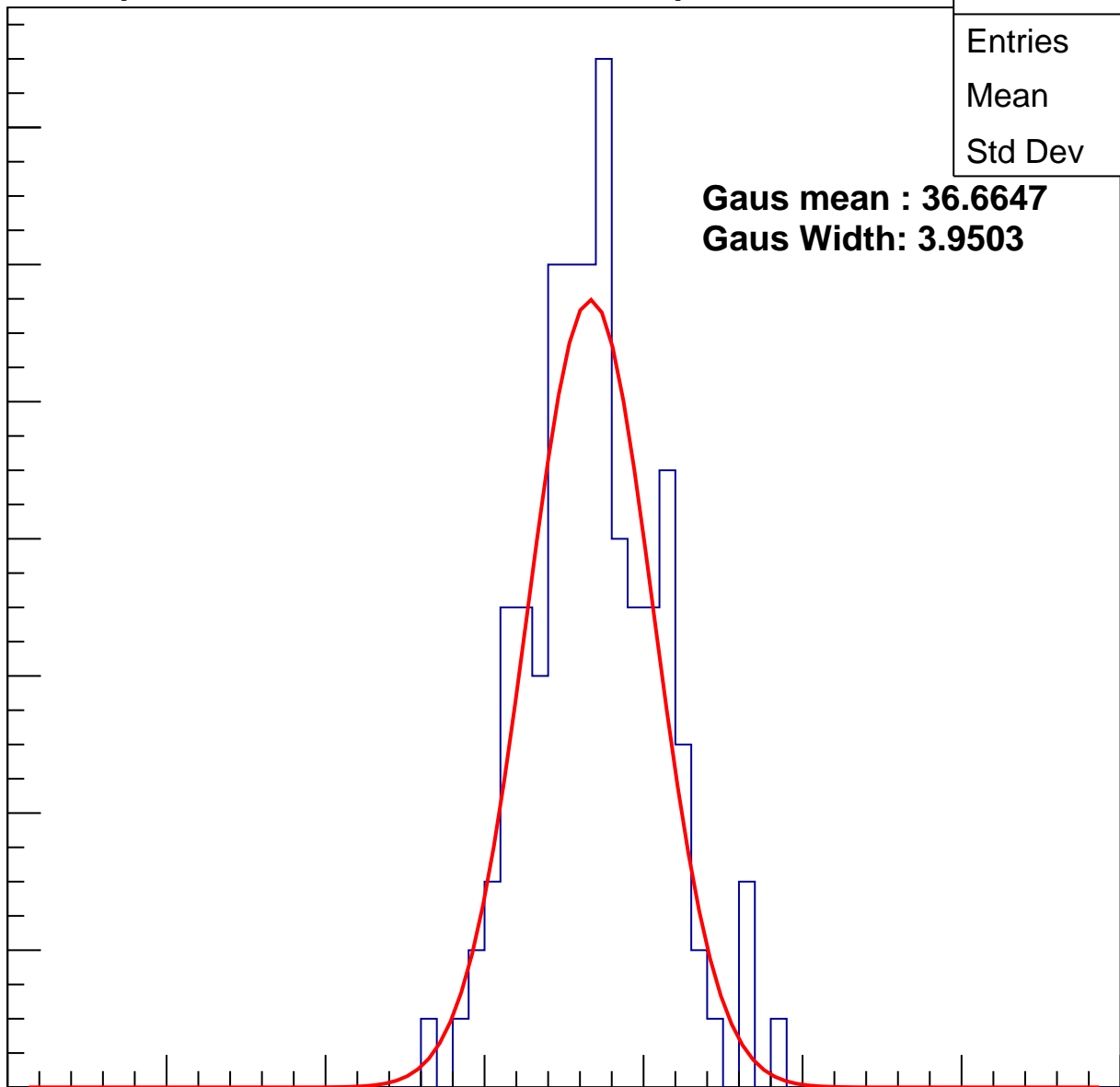
40

50

60

70

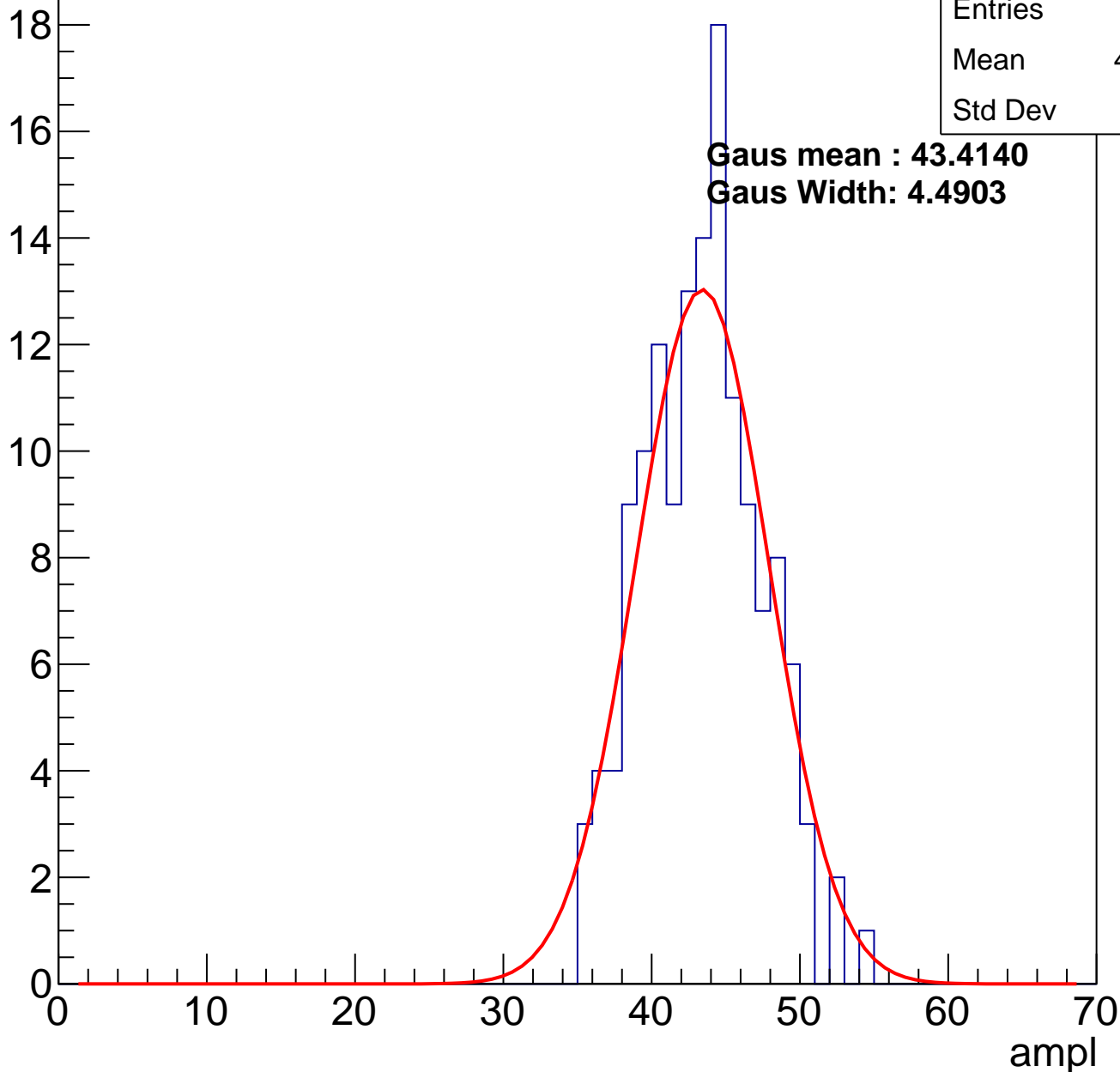
ampl



B0L101S, U25-ch127, adc2

calib_packv5_042523_0143.root, FC#1, port C1

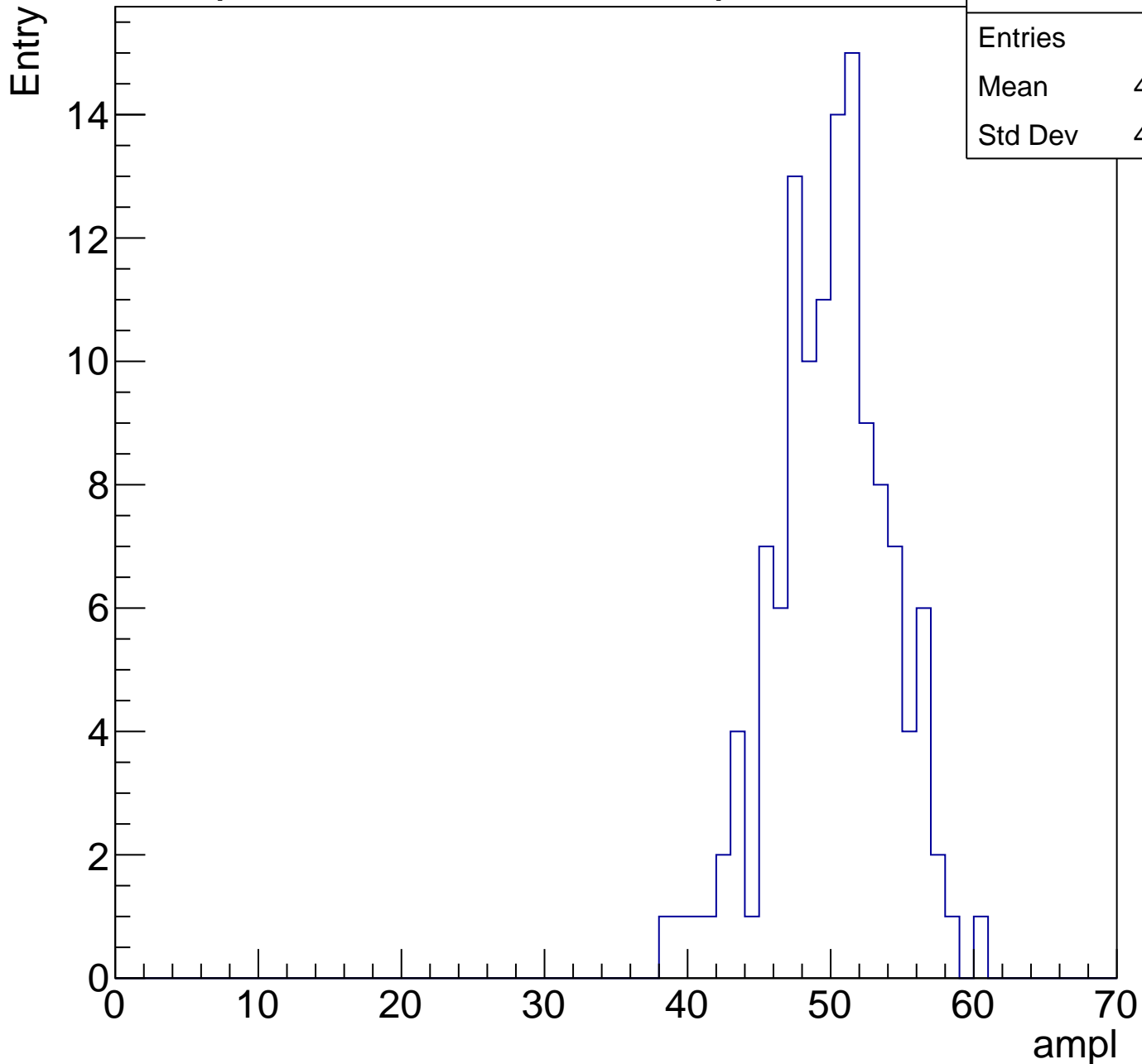
Entry



B0L101S, U25-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

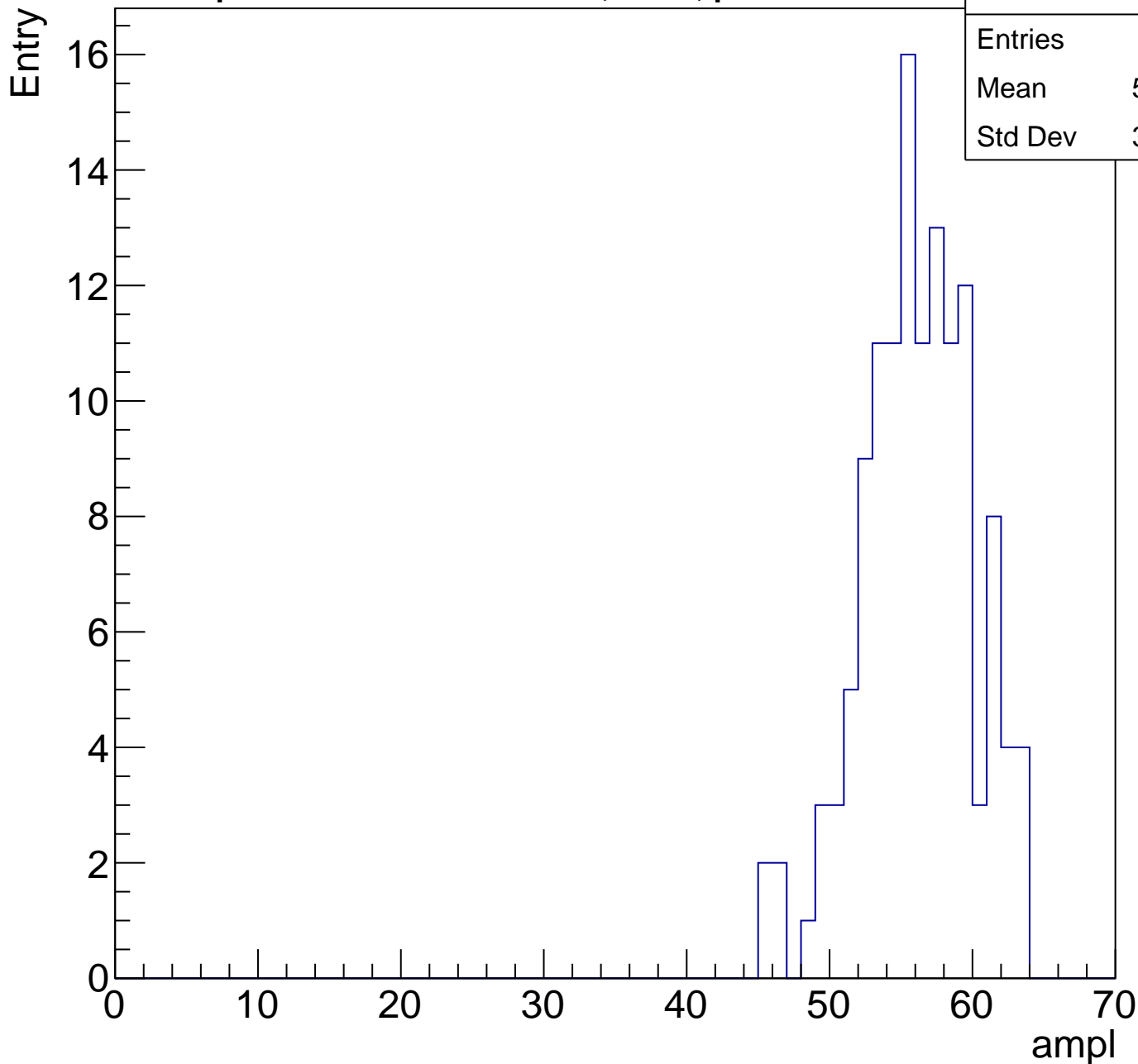
Entries	125
Mean	49.62
Std Dev	4.039



B0L101S, U25-ch127, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	55.64
Std Dev	3.864



B0L101S, U25-ch127, adc5

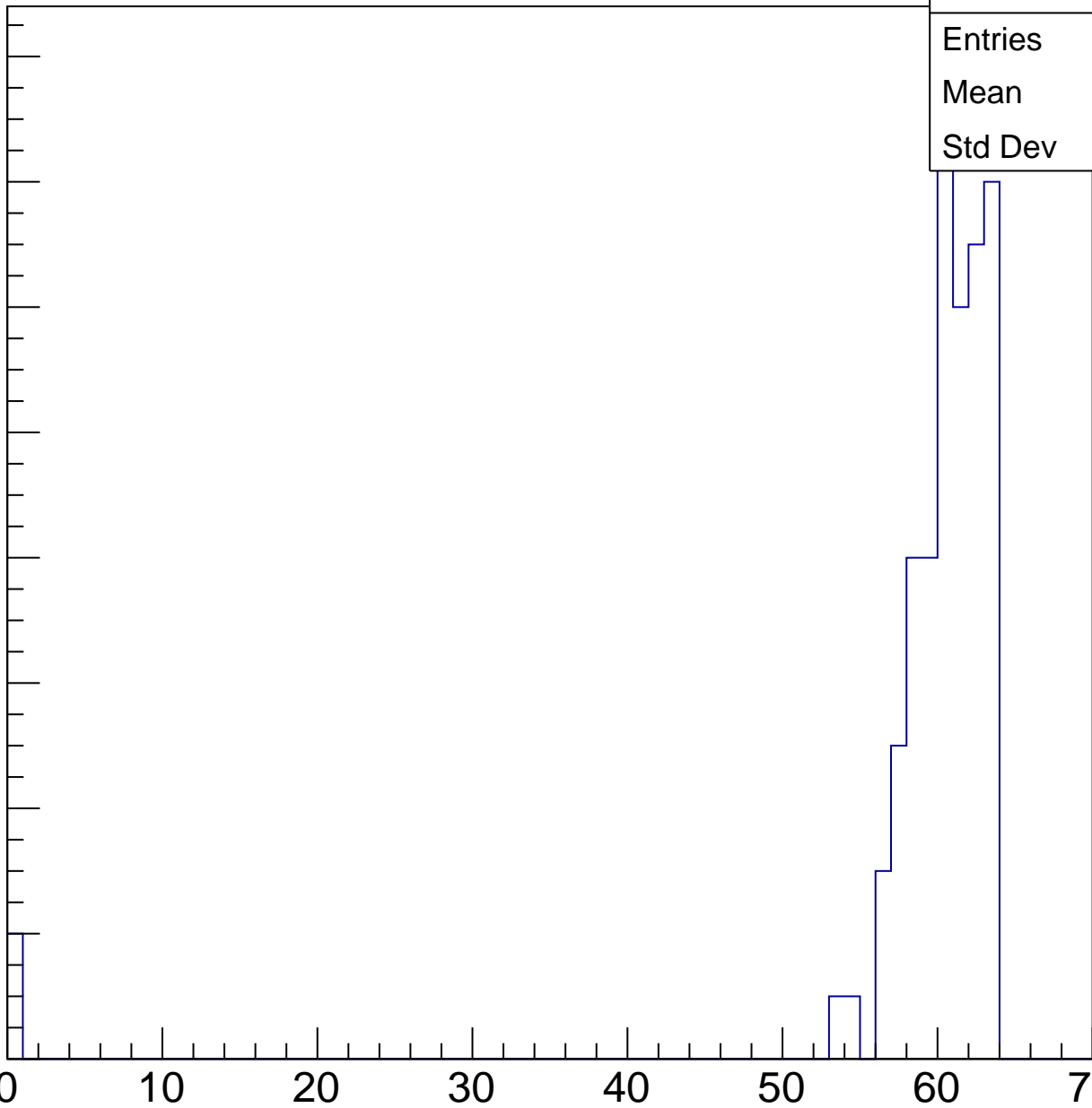
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.75
Std Dev	9.492

ampl



B0L101S, U25-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

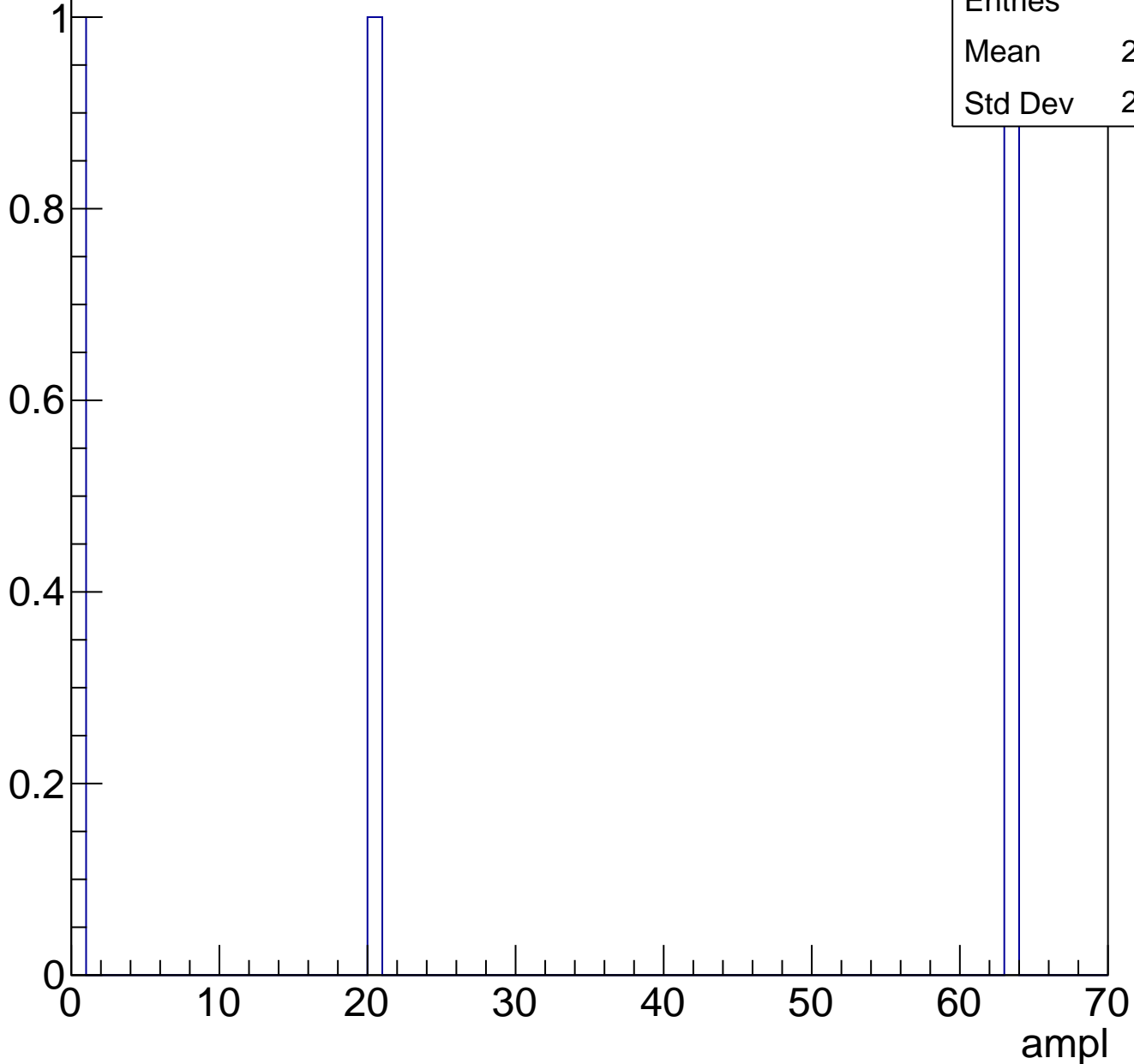
Entry



B0L101S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

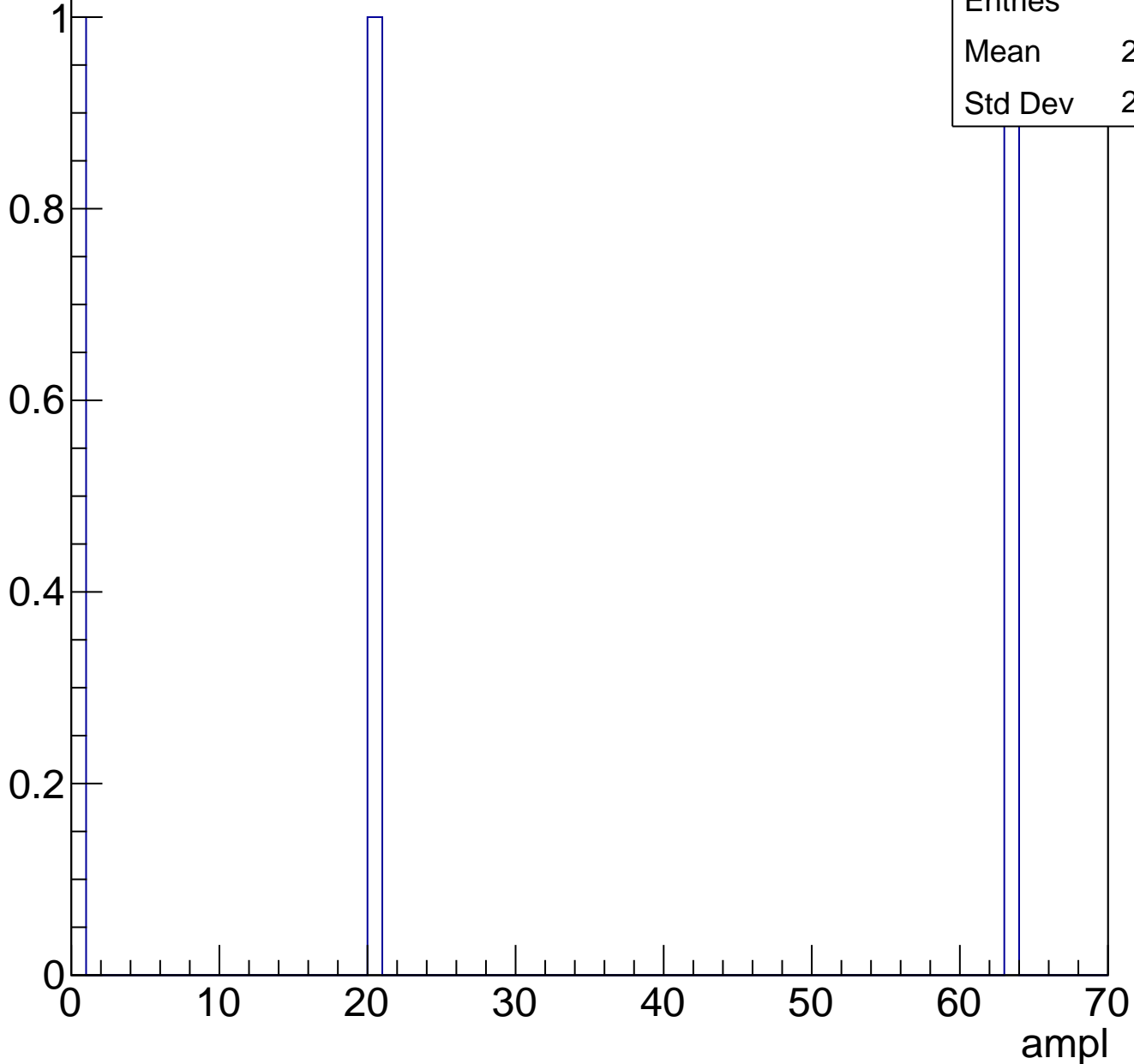


Entries	3
Mean	27.67
Std Dev	26.28

B0L101S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	27.67
Std Dev	26.28