



# B0L000S, U19-ch0, adc0

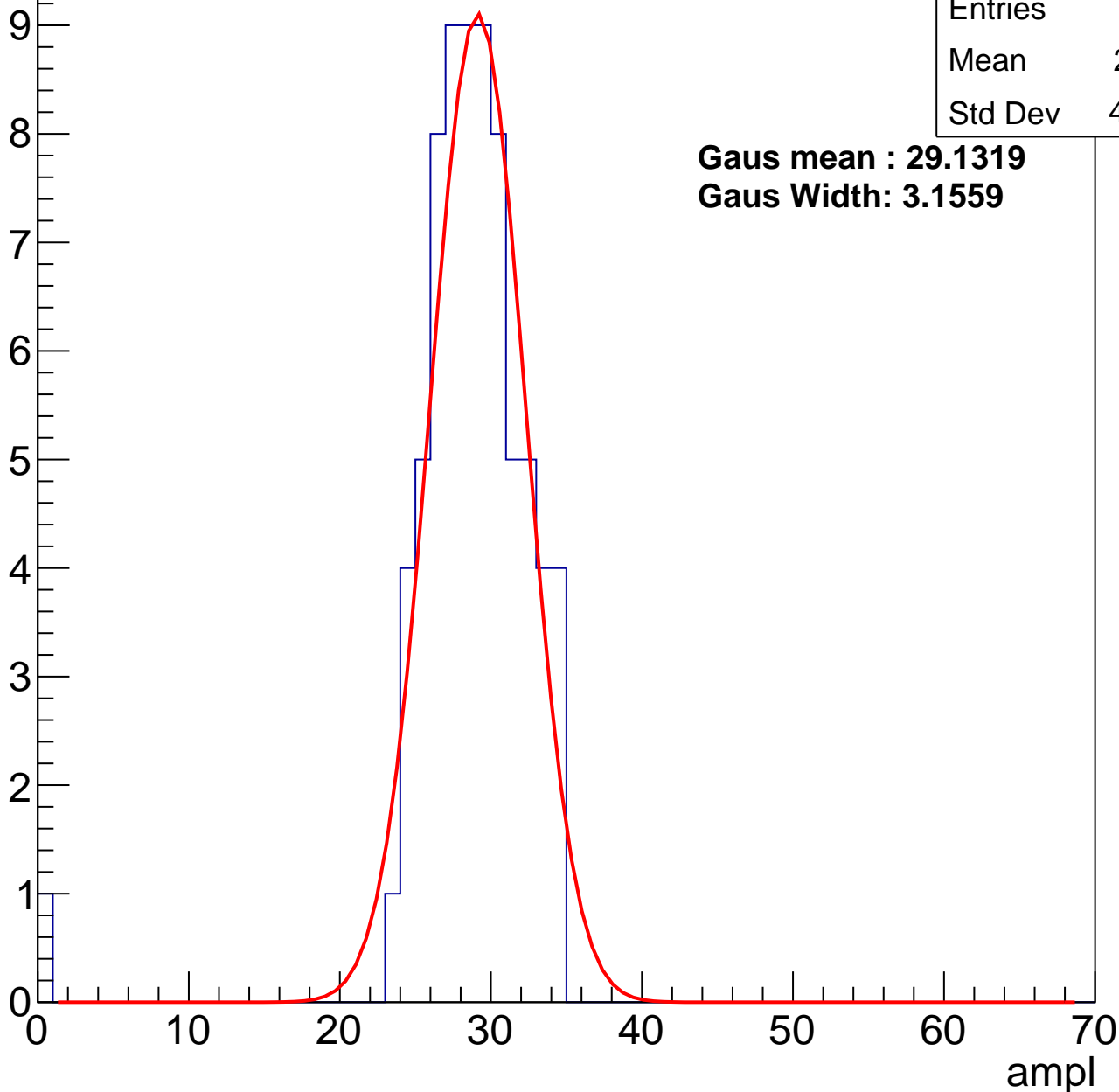
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	28.21
Std Dev	4.356

**Gaus mean : 29.1319**

**Gaus Width: 3.1559**



# B0L000S, U19-ch0, adc1

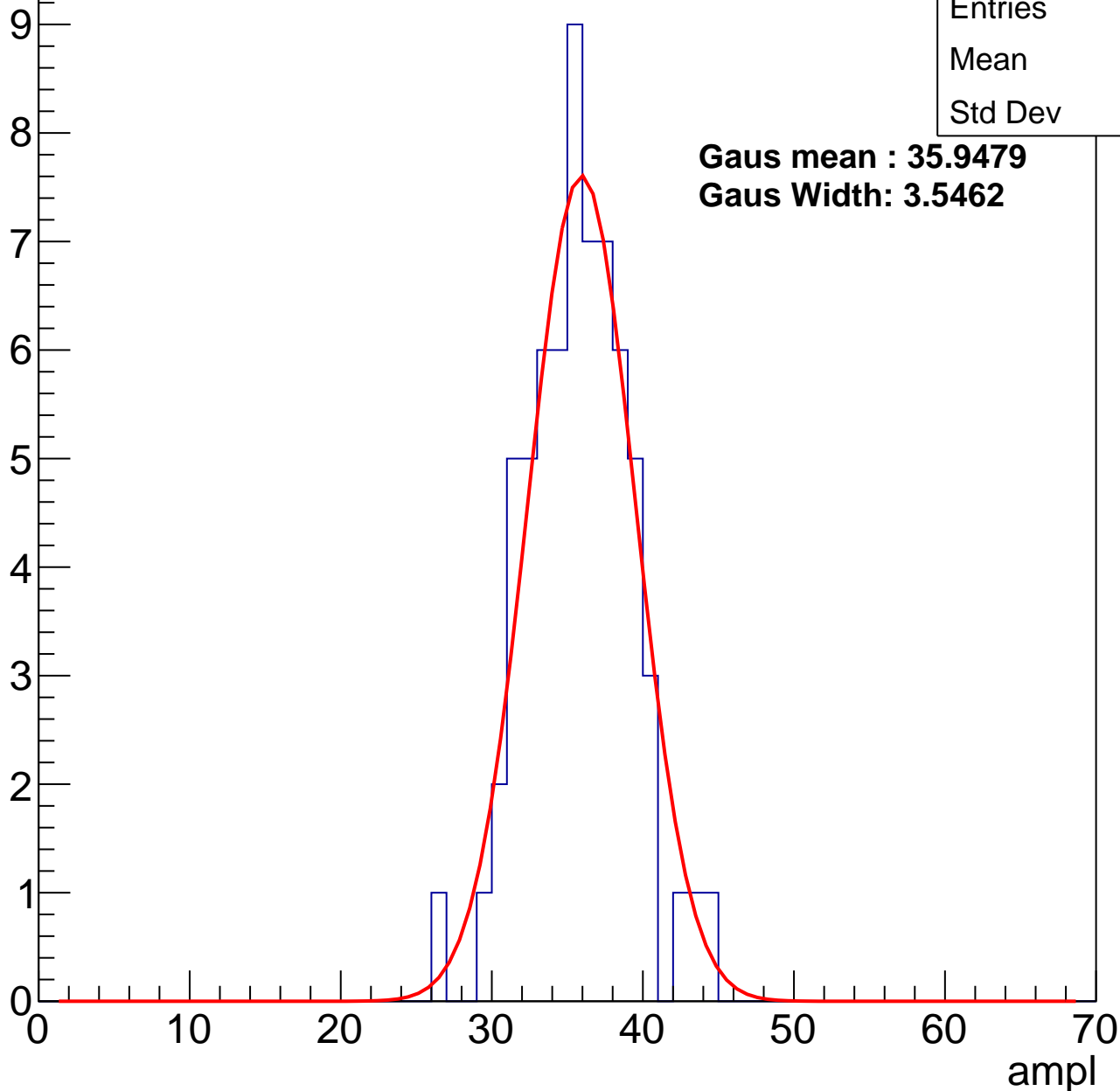
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	35.3
Std Dev	3.38

**Gaus mean : 35.9479**

**Gaus Width: 3.5462**



# B0L000S, U19-ch0, adc2

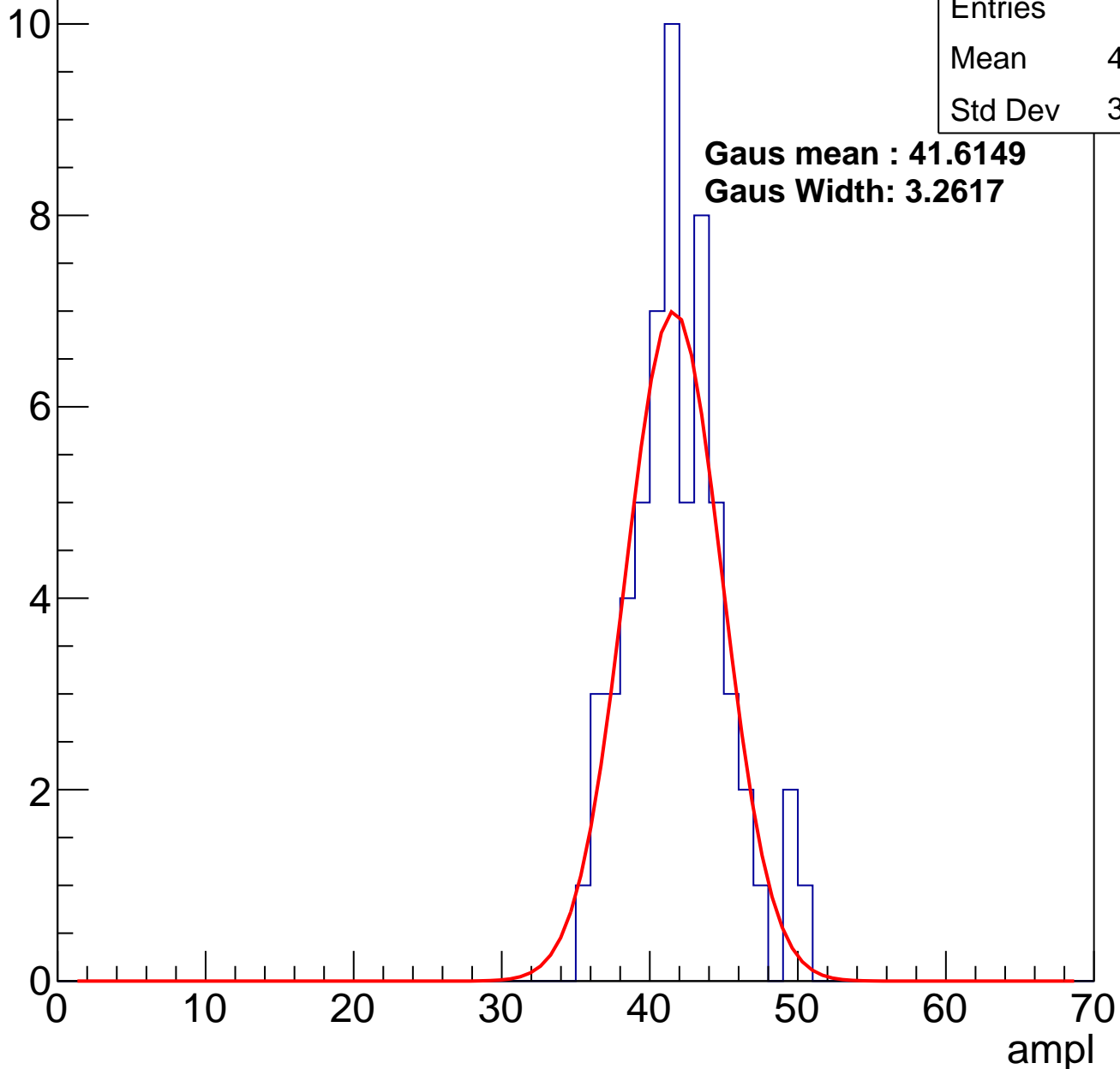
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	60
Mean	41.45
Std Dev	3.253

**Gaus mean : 41.6149**

**Gaus Width: 3.2617**

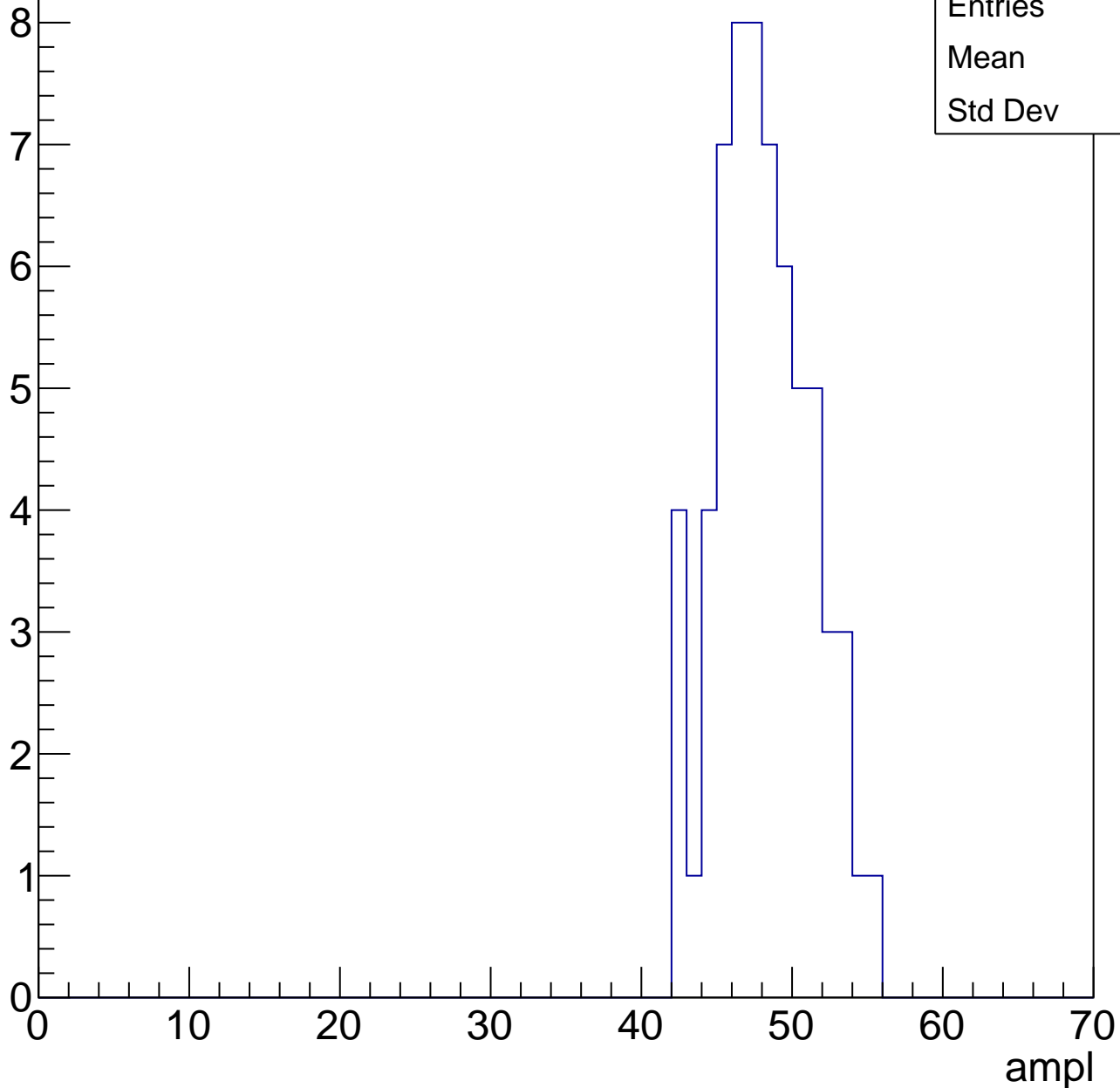
Entry



# B0L000S, U19-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

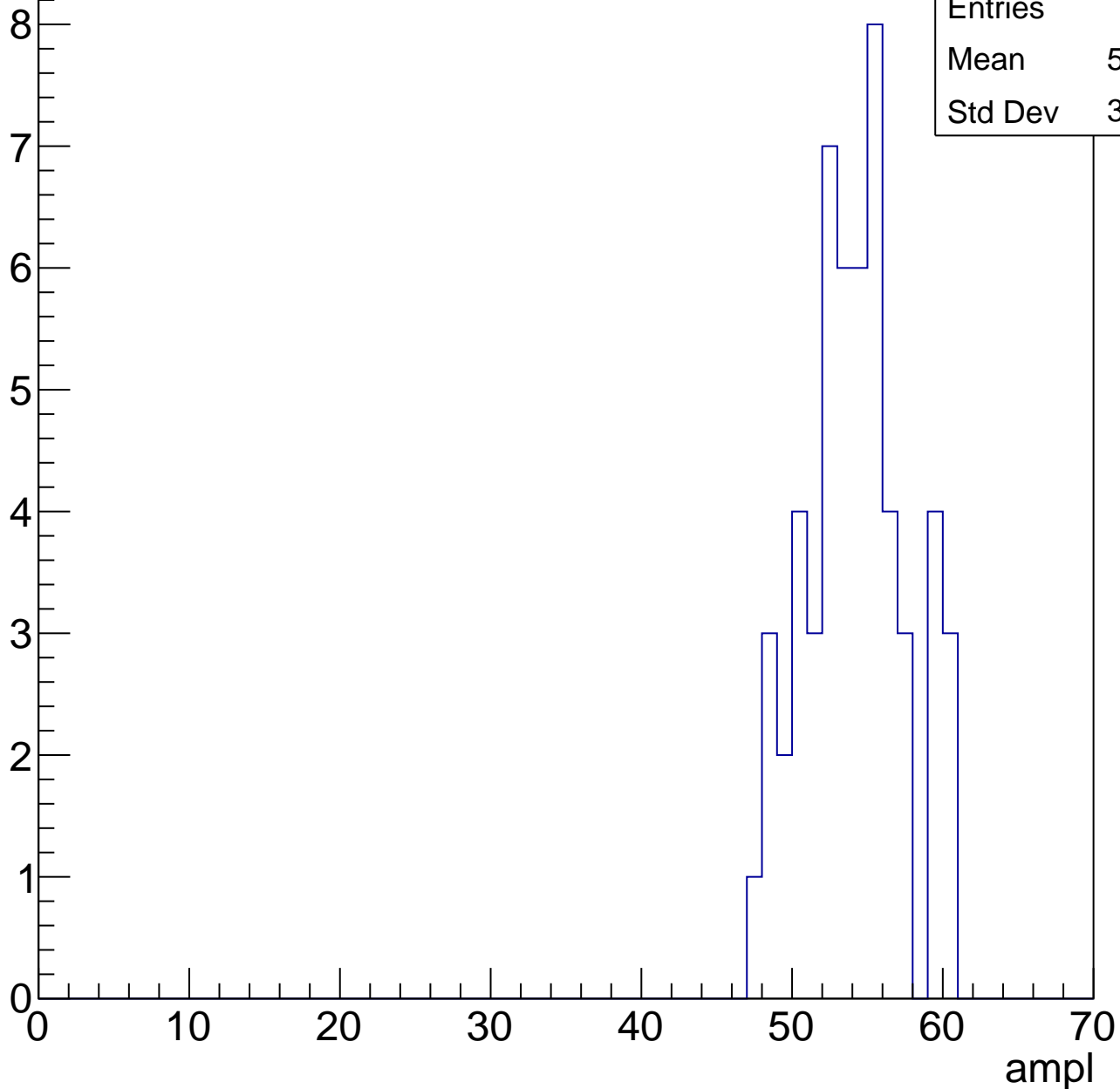
Entry



# B0L000S, U19-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



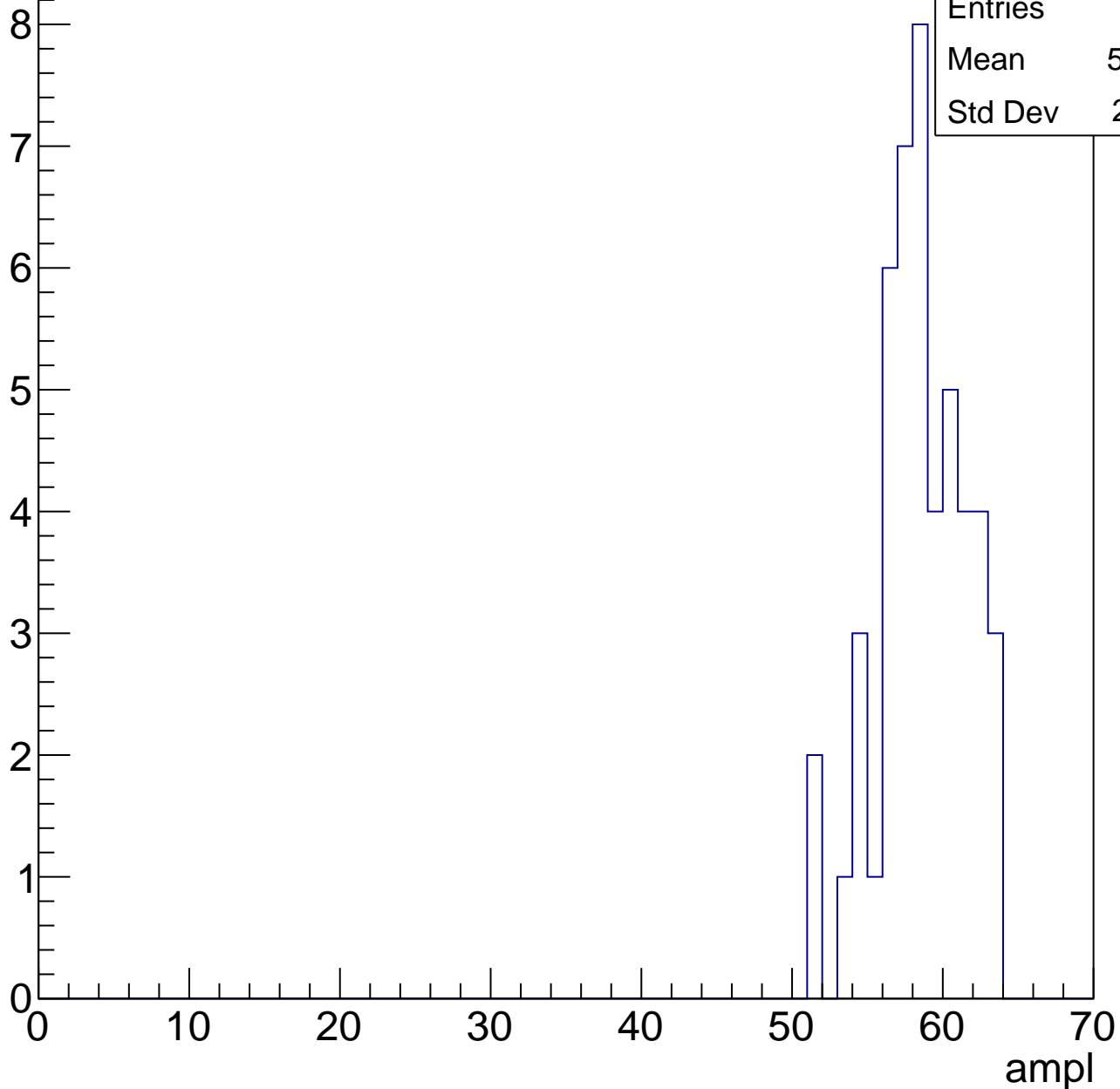
Entries	54
Mean	53.69
Std Dev	3.293

# B0L000S, U19-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	58.08
Std Dev	2.921

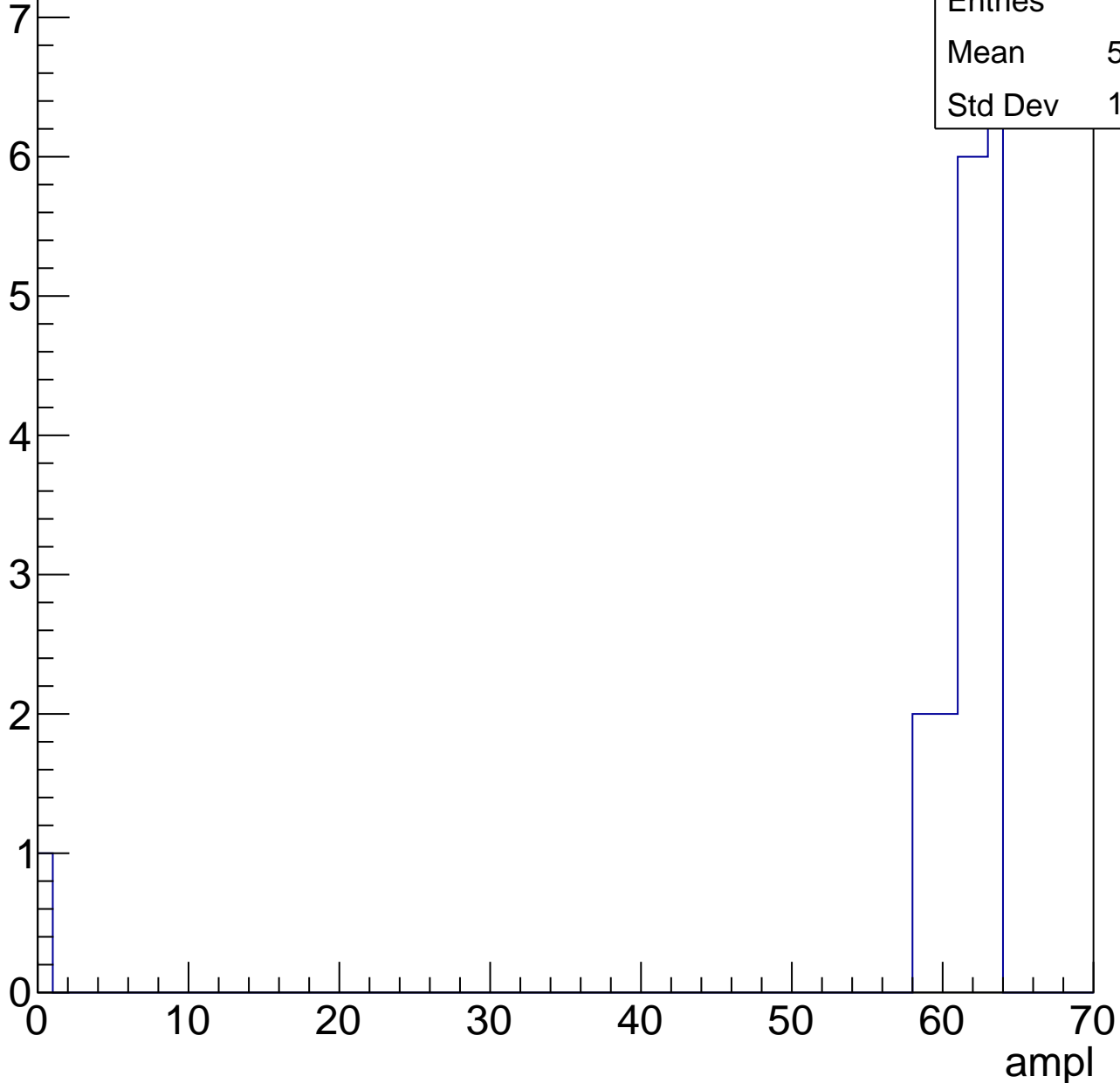


# B0L000S, U19-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	58.96
Std Dev	11.89





# B0L000S, U19-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U19-ch1, adc0

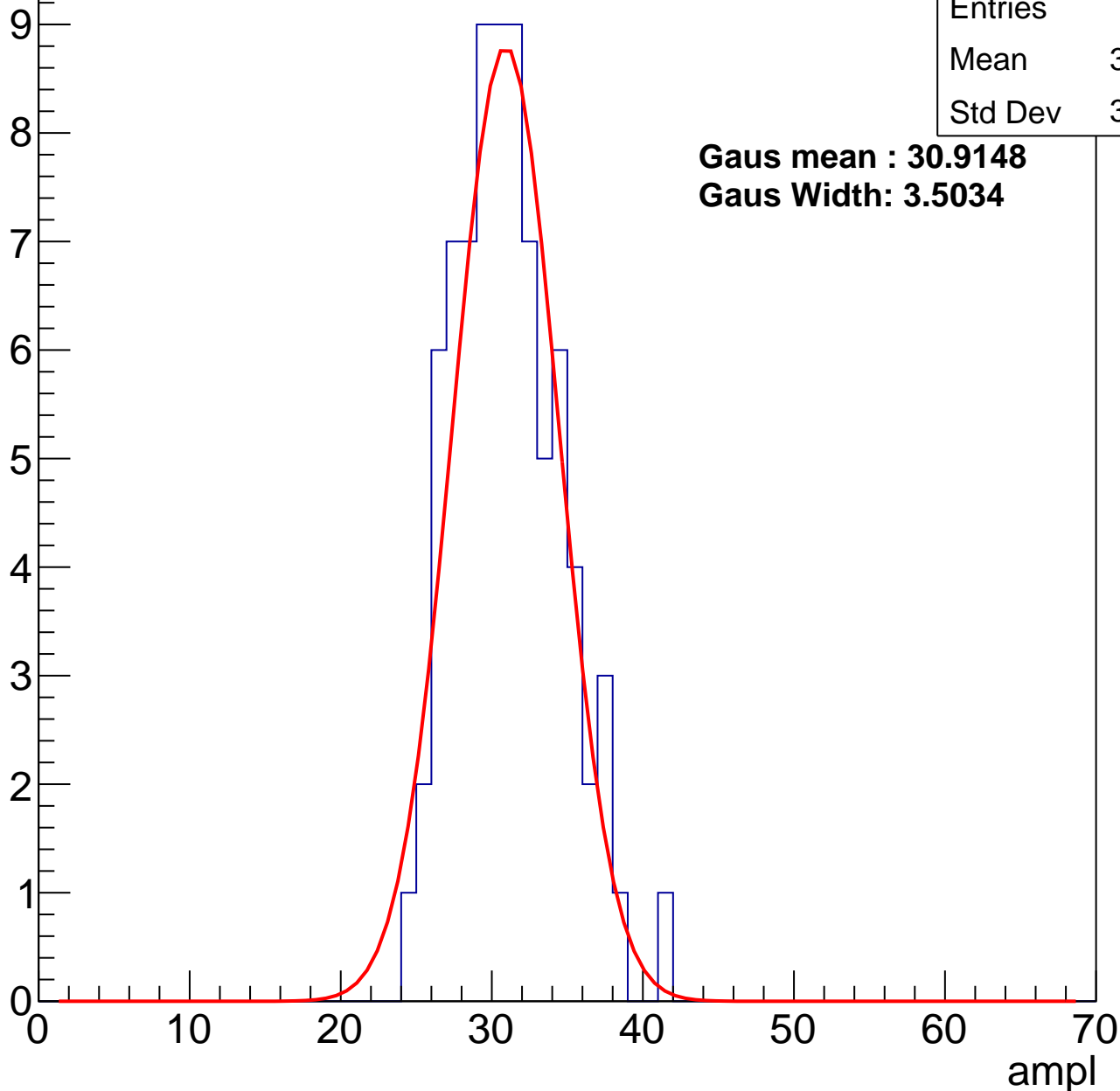
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	30.63
Std Dev	3.435

**Gaus mean : 30.9148**

**Gaus Width: 3.5034**



# B0L000S, U19-ch1, adc1

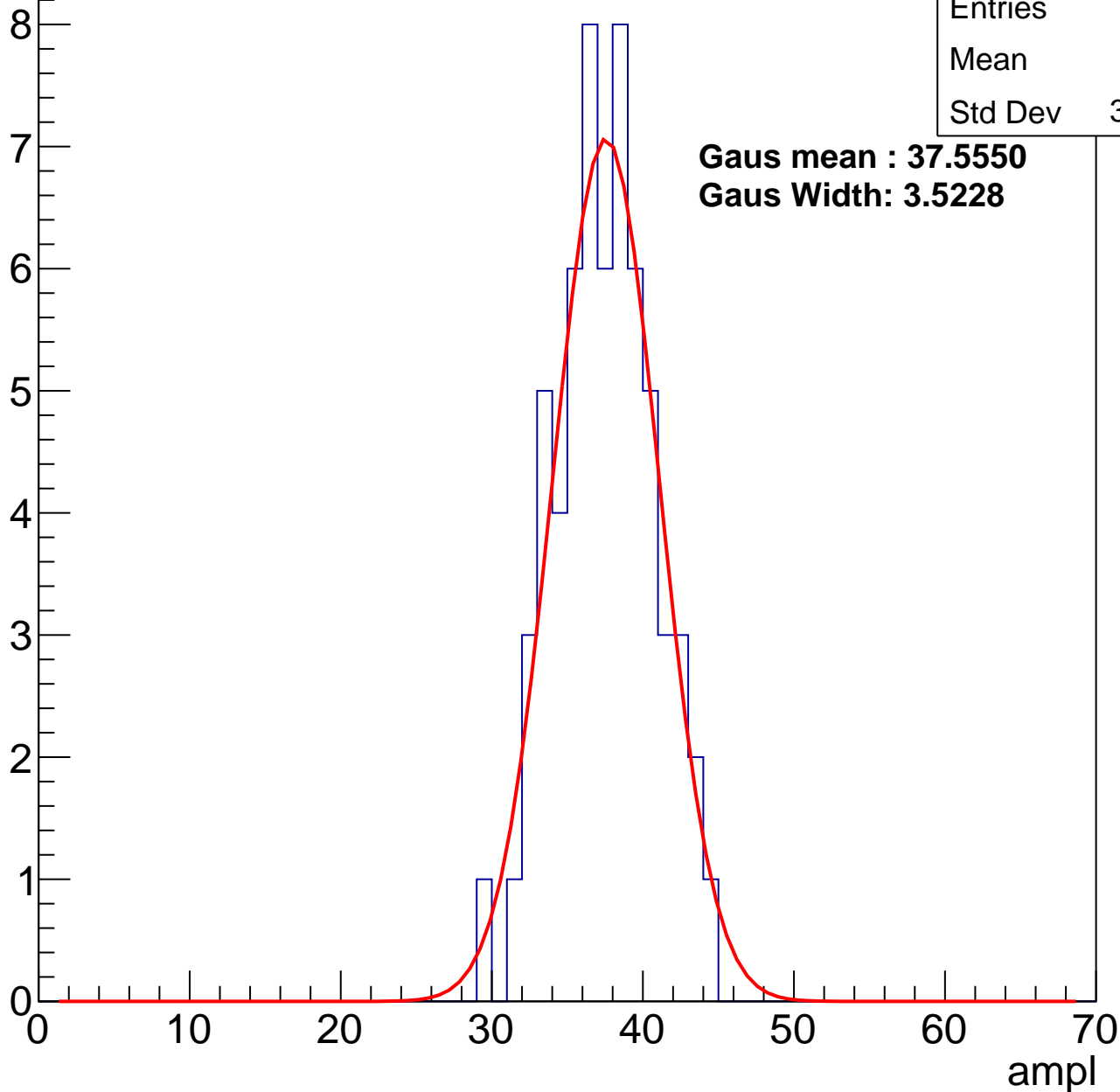
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37
Std Dev	3.223

**Gaus mean : 37.5550**

**Gaus Width: 3.5228**



# B0L000S, U19-ch1, adc2

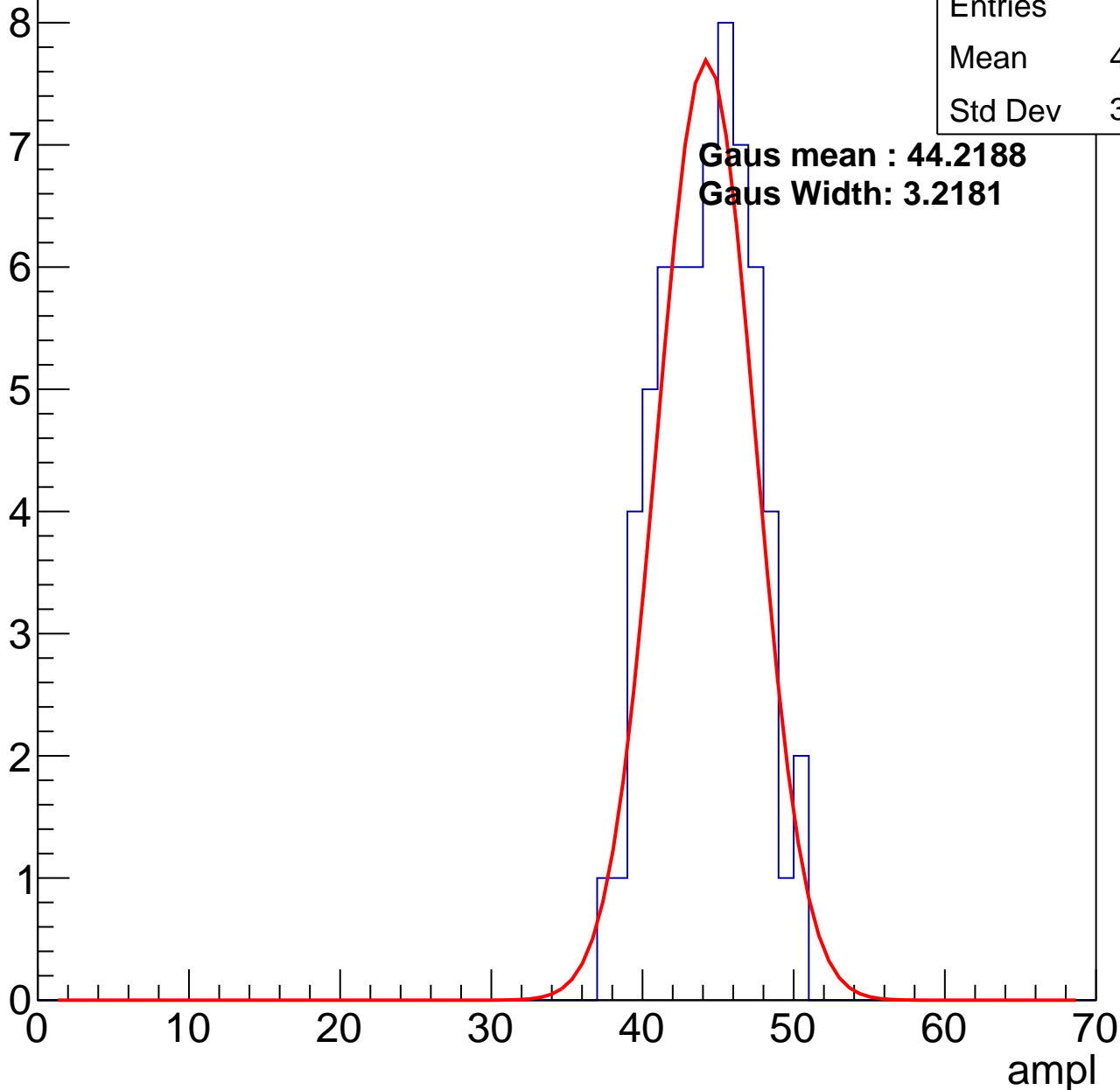
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.75
Std Dev	3.052

**Gaus mean : 44.2188**

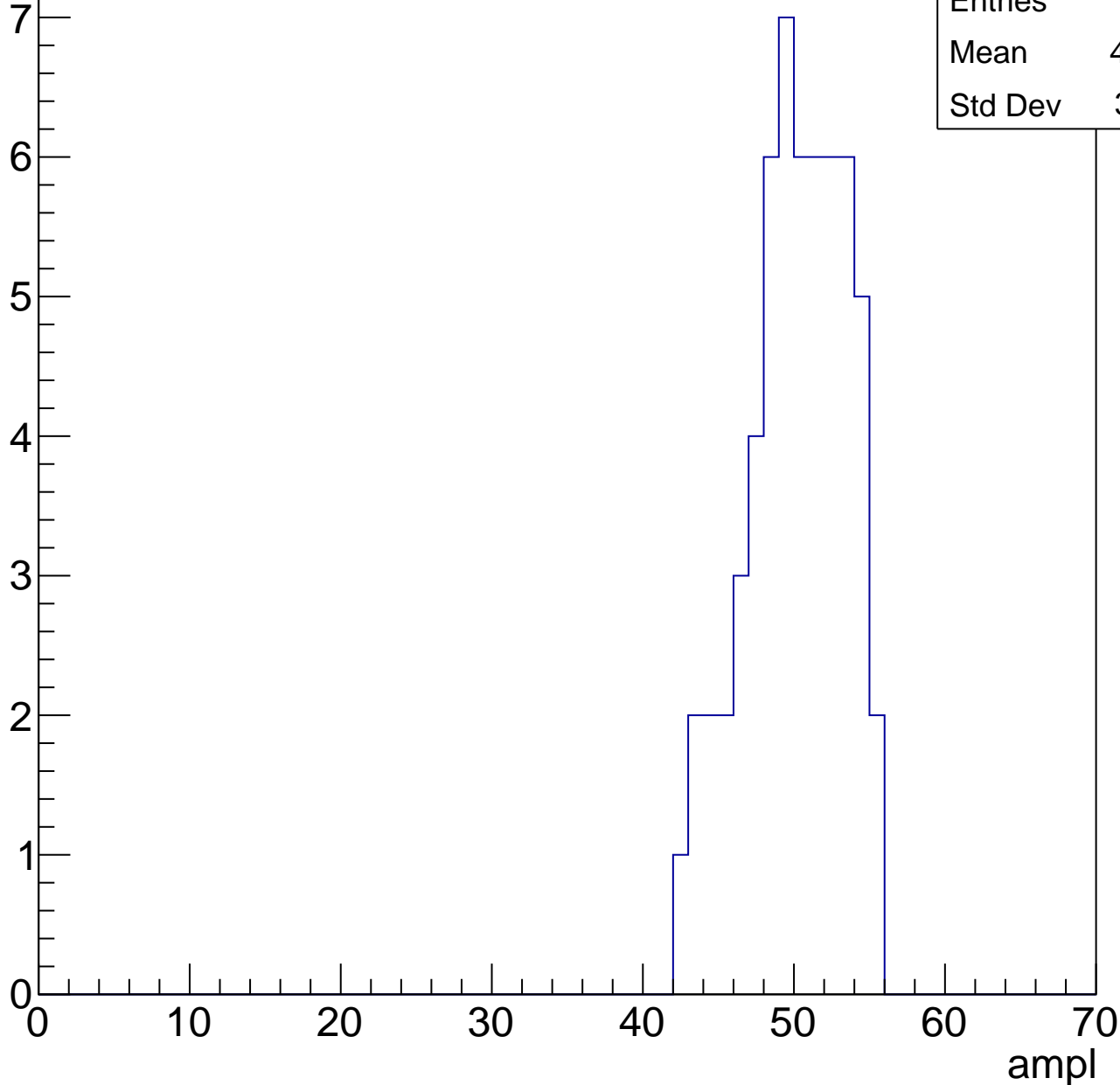
**Gaus Width: 3.2181**



# B0L000S, U19-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

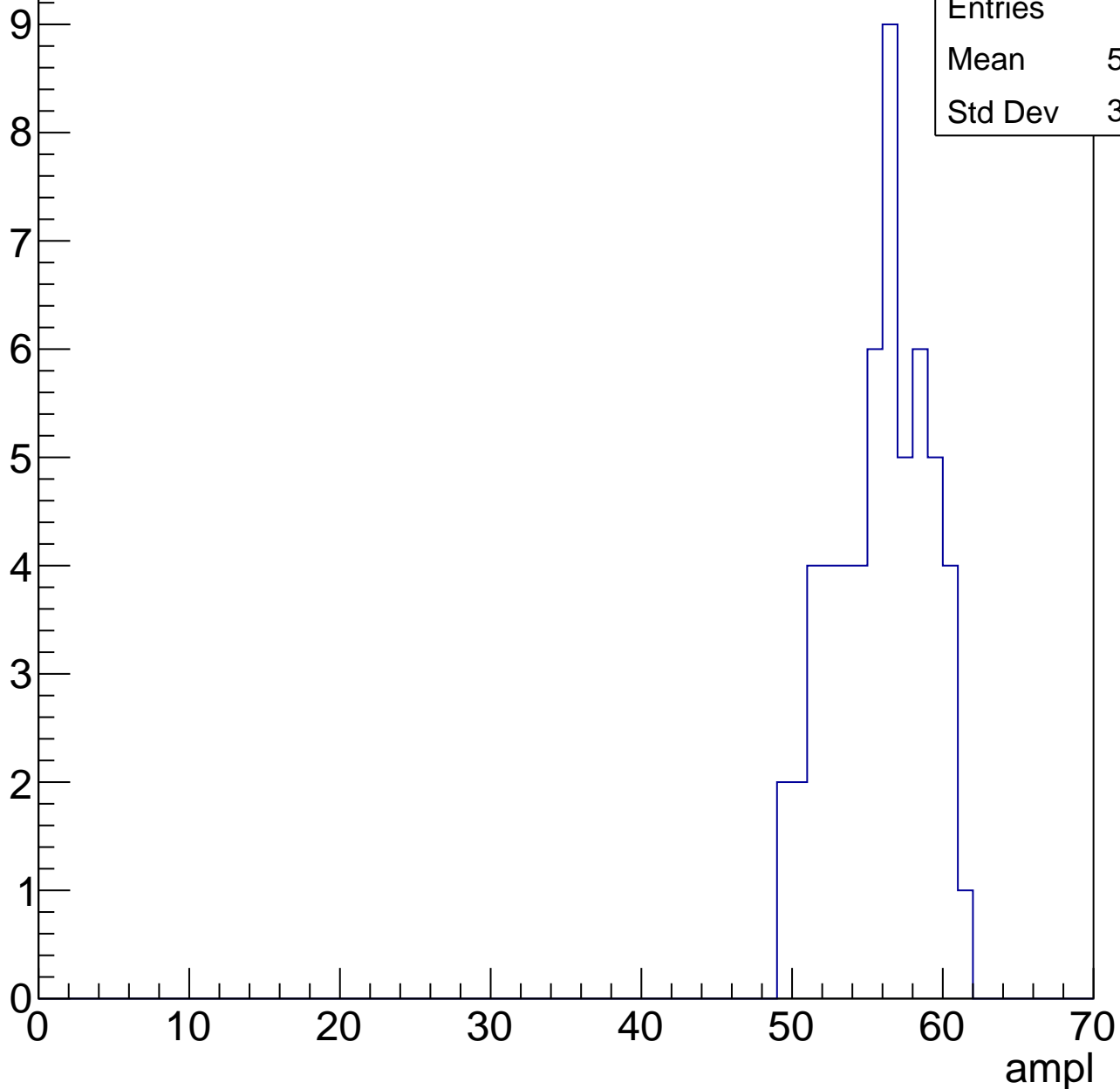


Entries	58
Mean	49.64
Std Dev	3.231

# B0L000S, U19-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	56
Mean	55.38
Std Dev	3.085

# B0L000S, U19-ch1, adc5

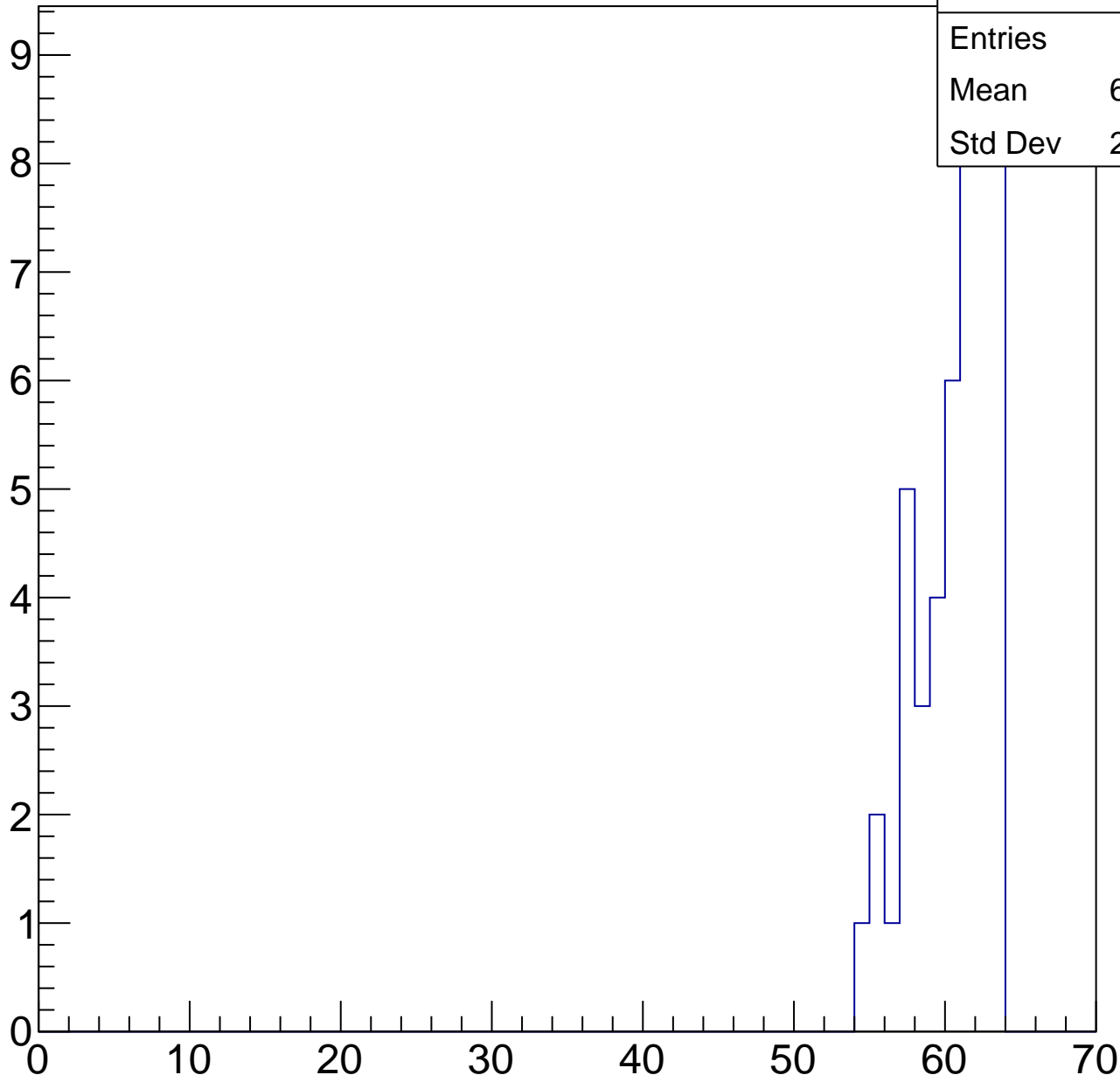
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	60.18
Std Dev	2.422

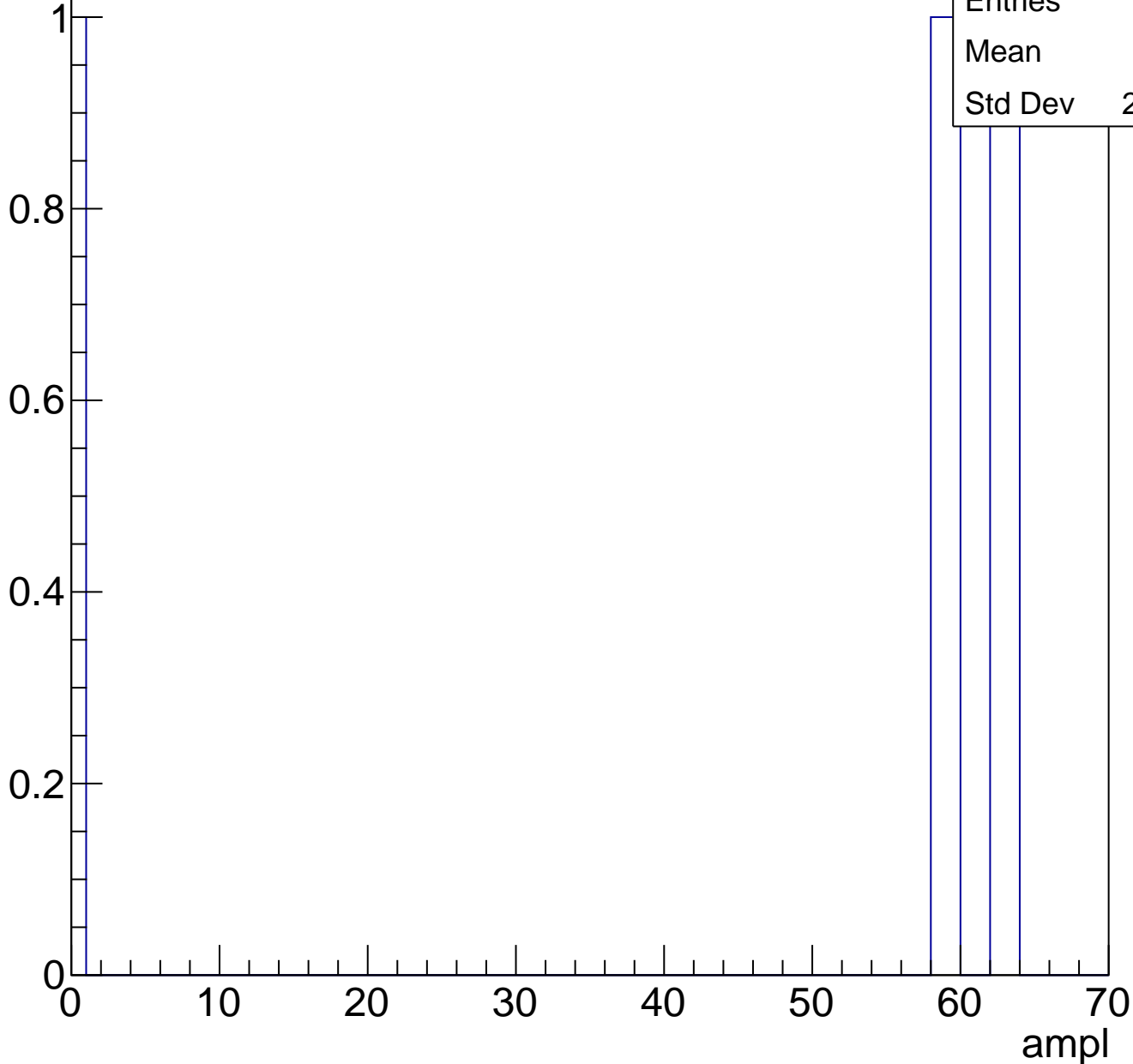
ampl



# B0L000S, U19-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch2, adc0

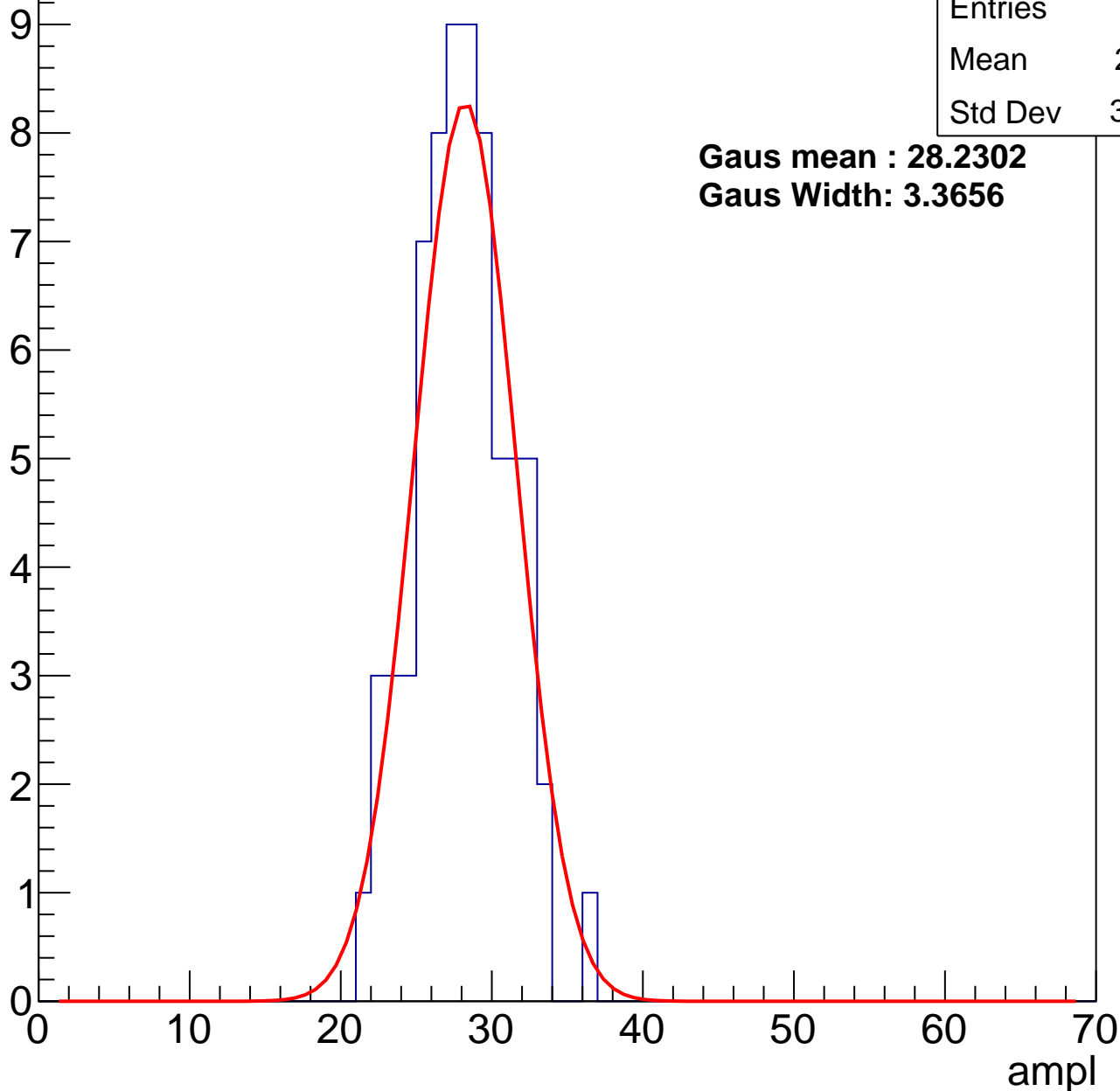
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	27.61
Std Dev	3.047

**Gaus mean : 28.2302**

**Gaus Width: 3.3656**



# B0L000S, U19-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	34.1
Std Dev	2.894

**Gaus mean : 34.4509**

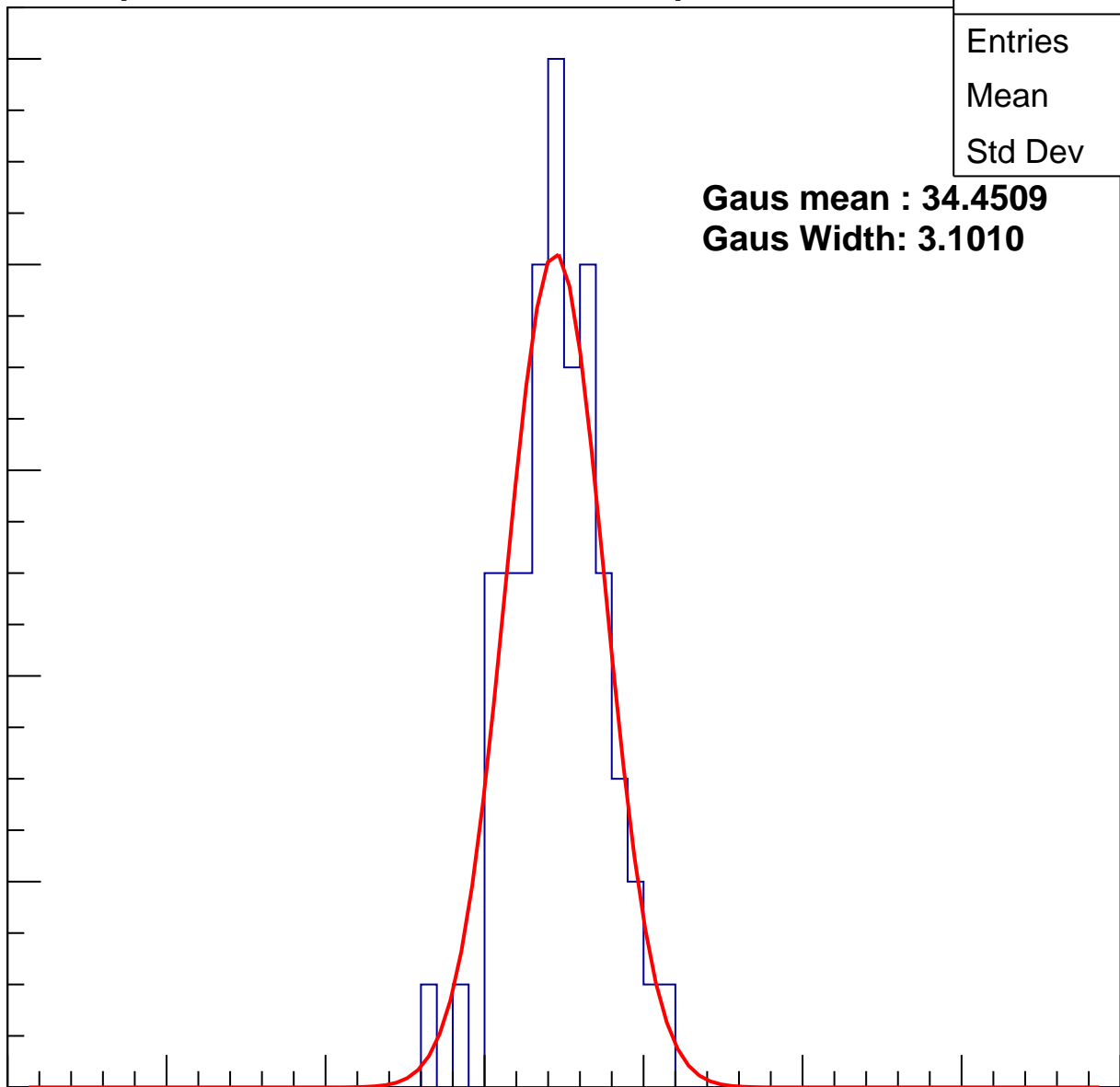
**Gaus Width: 3.1010**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch2, adc2

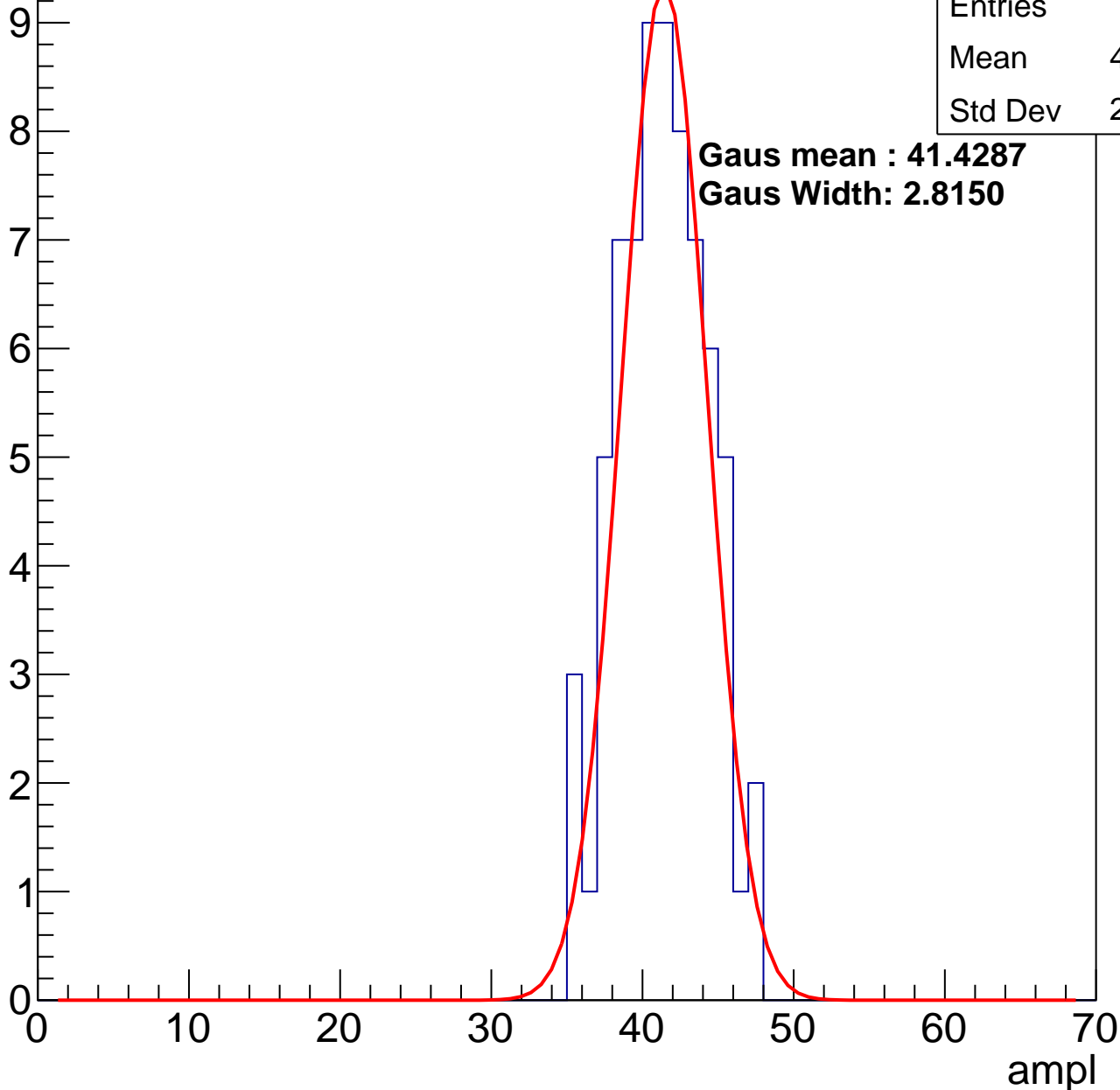
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	40.86
Std Dev	2.875

**Gaus mean : 41.4287**

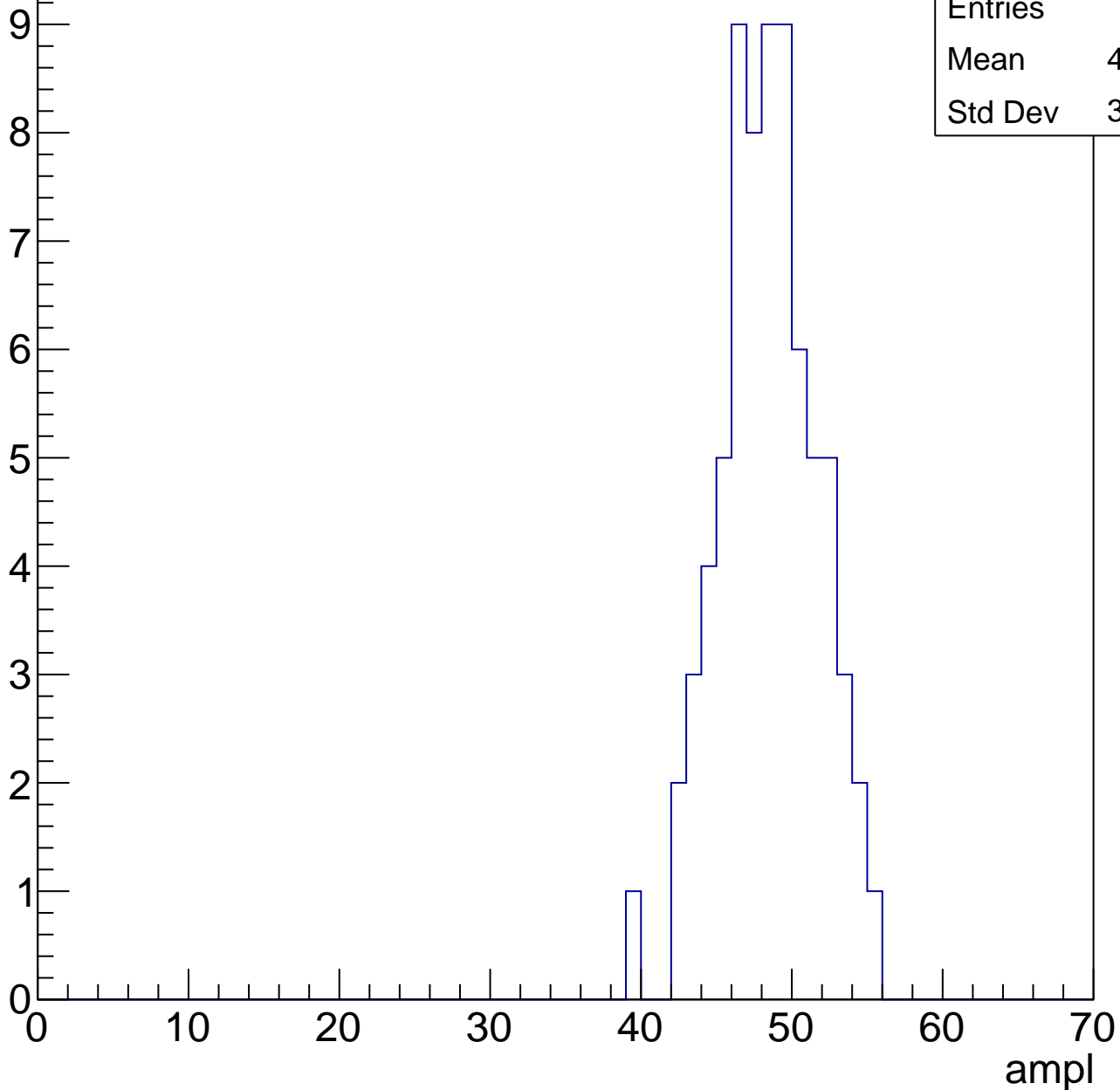
**Gaus Width: 2.8150**



# B0L000S, U19-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

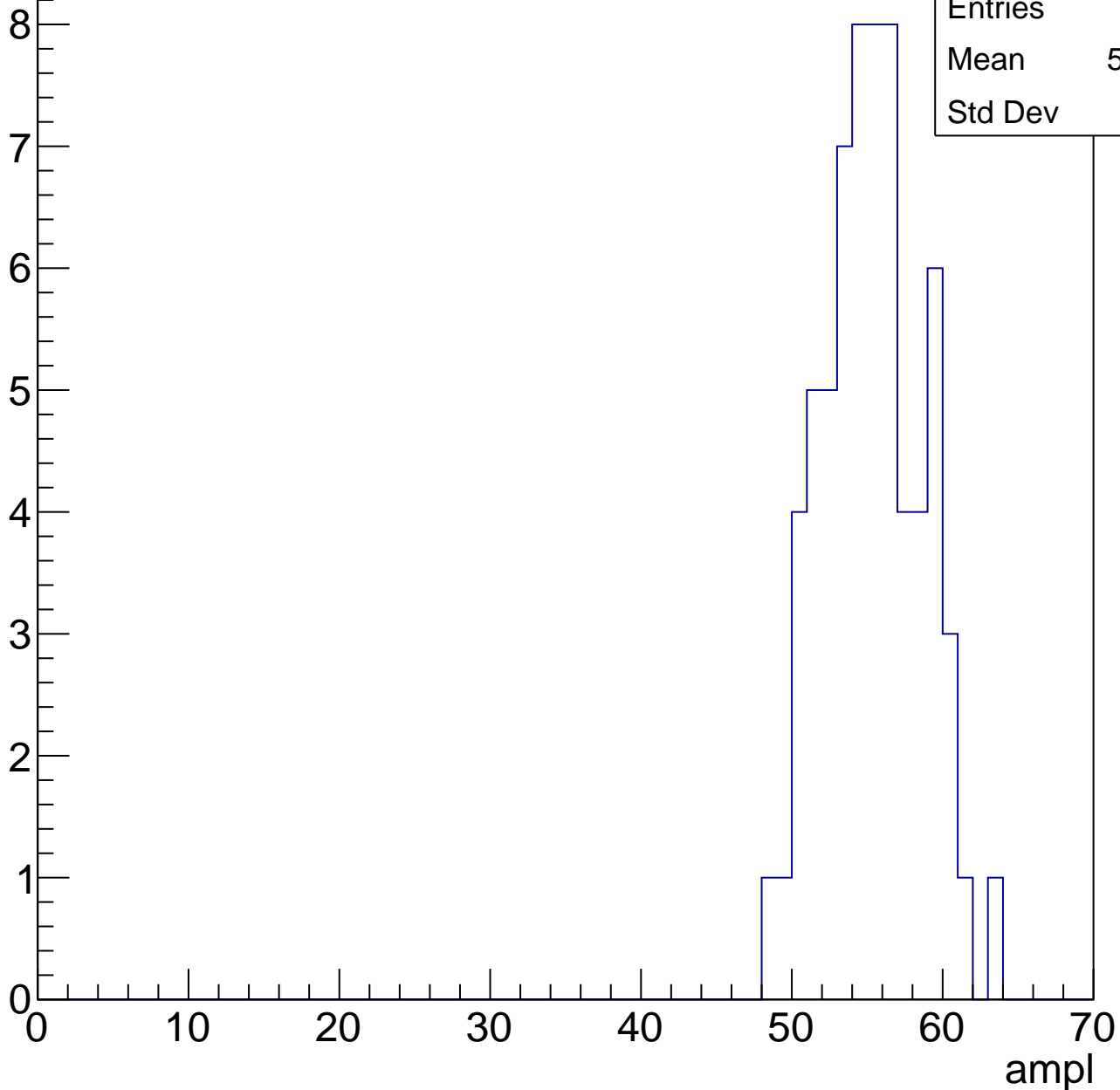


# B0L000S, U19-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	54.86
Std Dev	3.2

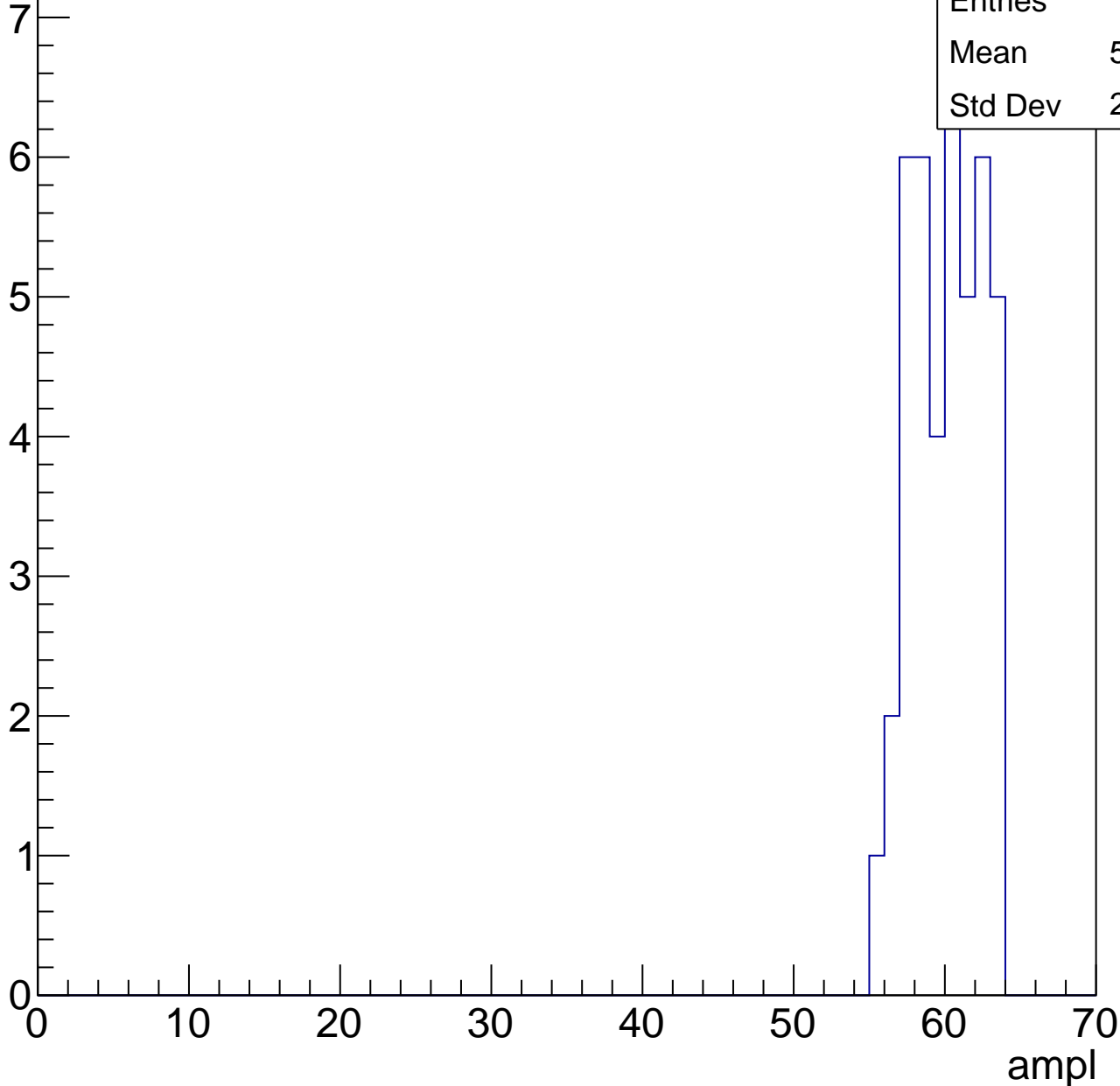


# B0L000S, U19-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

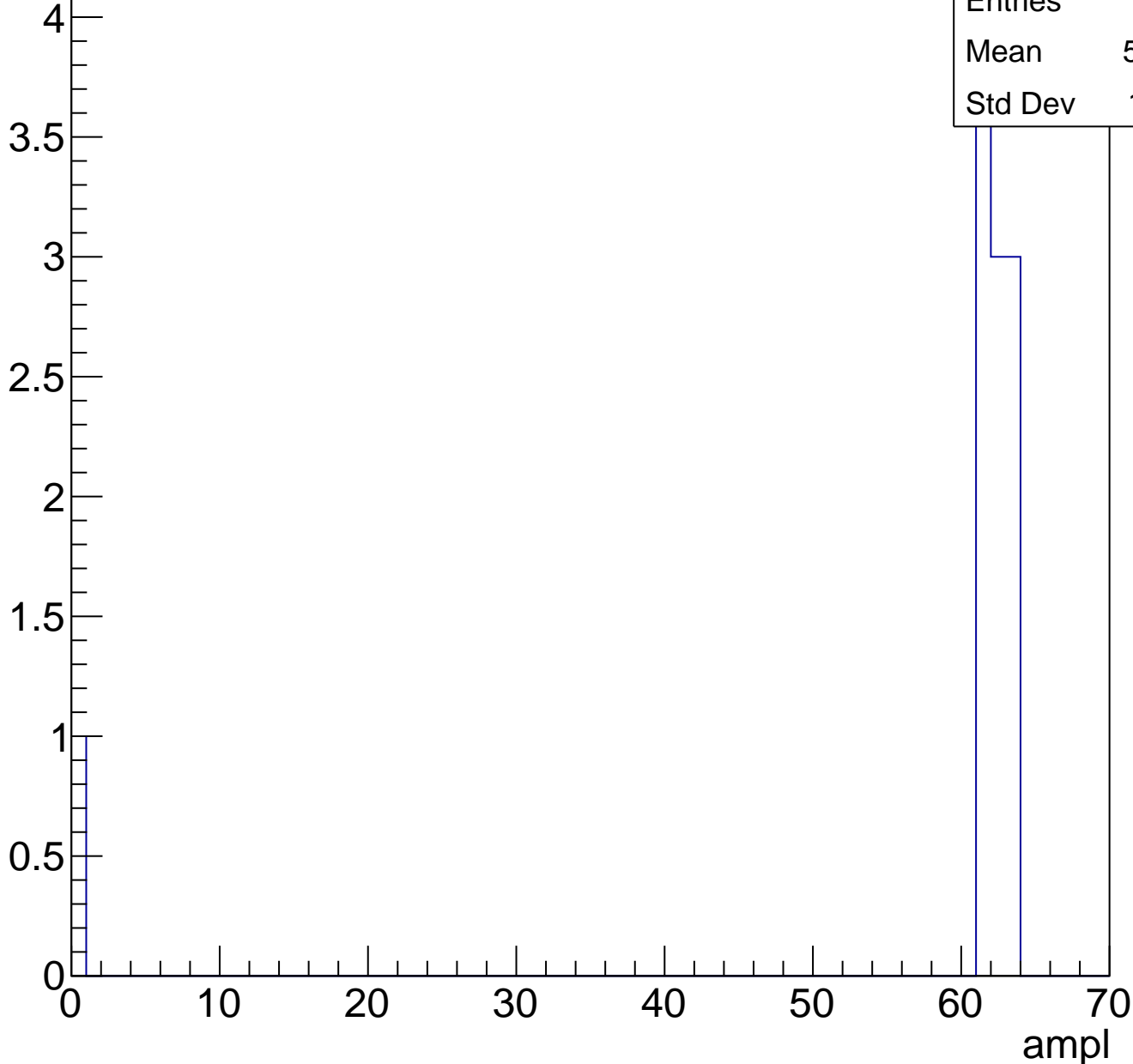
Entries	42
Mean	59.64
Std Dev	2.223



# B0L000S, U19-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch3, adc0

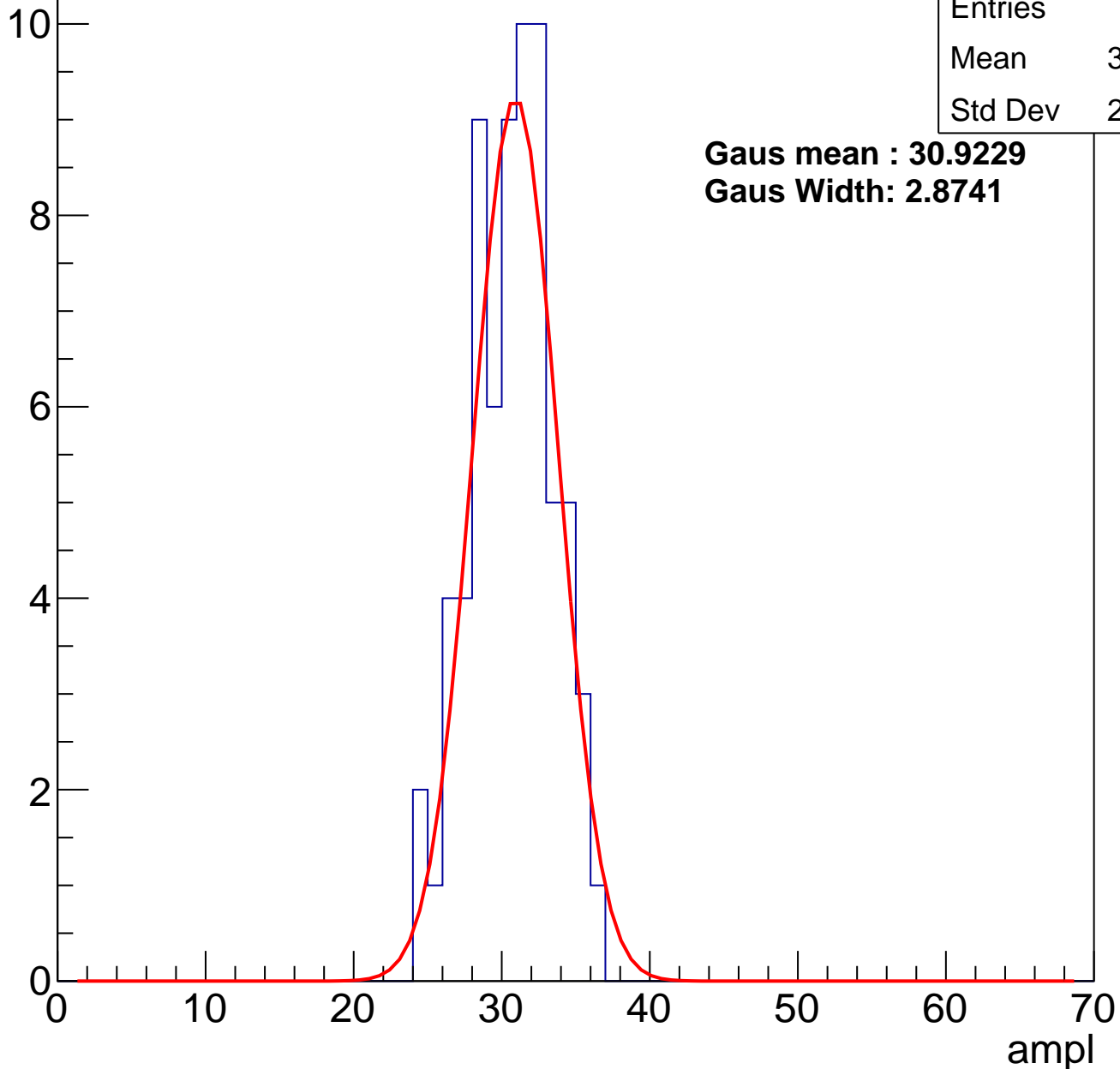
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	30.25
Std Dev	2.747

**Gaus mean : 30.9229**

**Gaus Width: 2.8741**

Entry



# B0L000S, U19-ch3, adc1

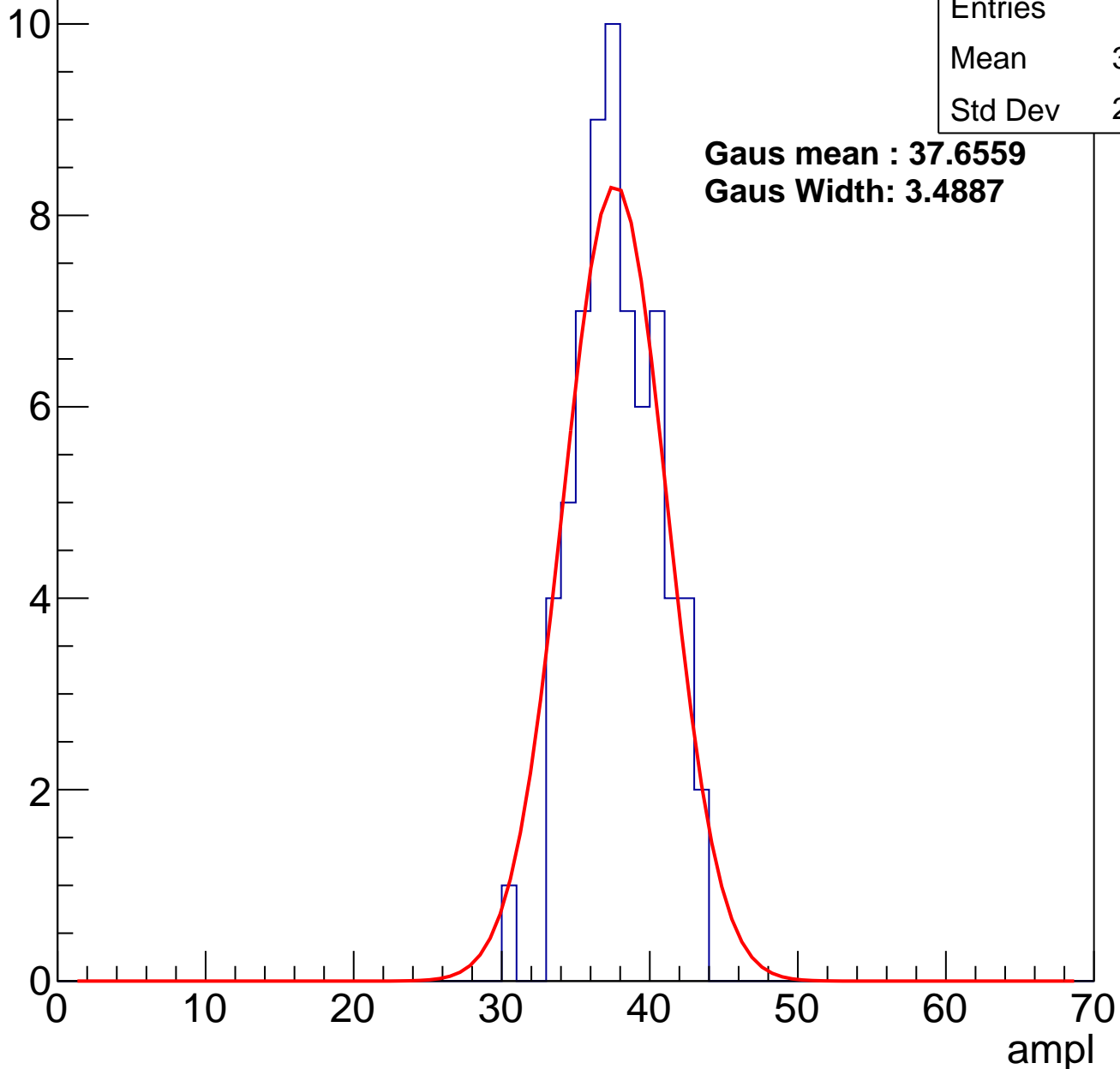
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	37.41
Std Dev	2.791

**Gaus mean : 37.6559**

**Gaus Width: 3.4887**

Entry



# B0L000S, U19-ch3, adc2

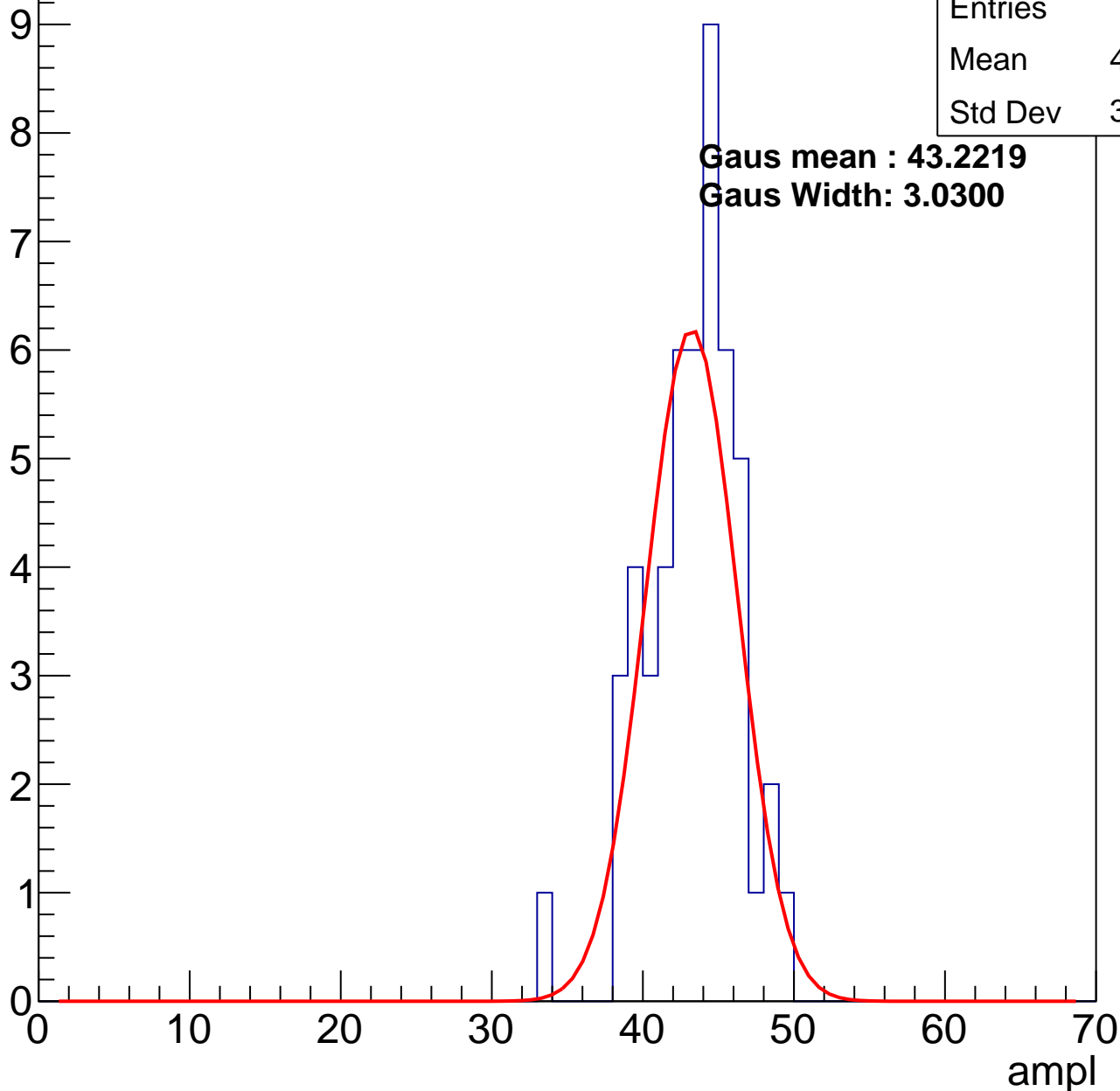
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	42.84
Std Dev	3.025

**Gaus mean : 43.2219**

**Gaus Width: 3.0300**

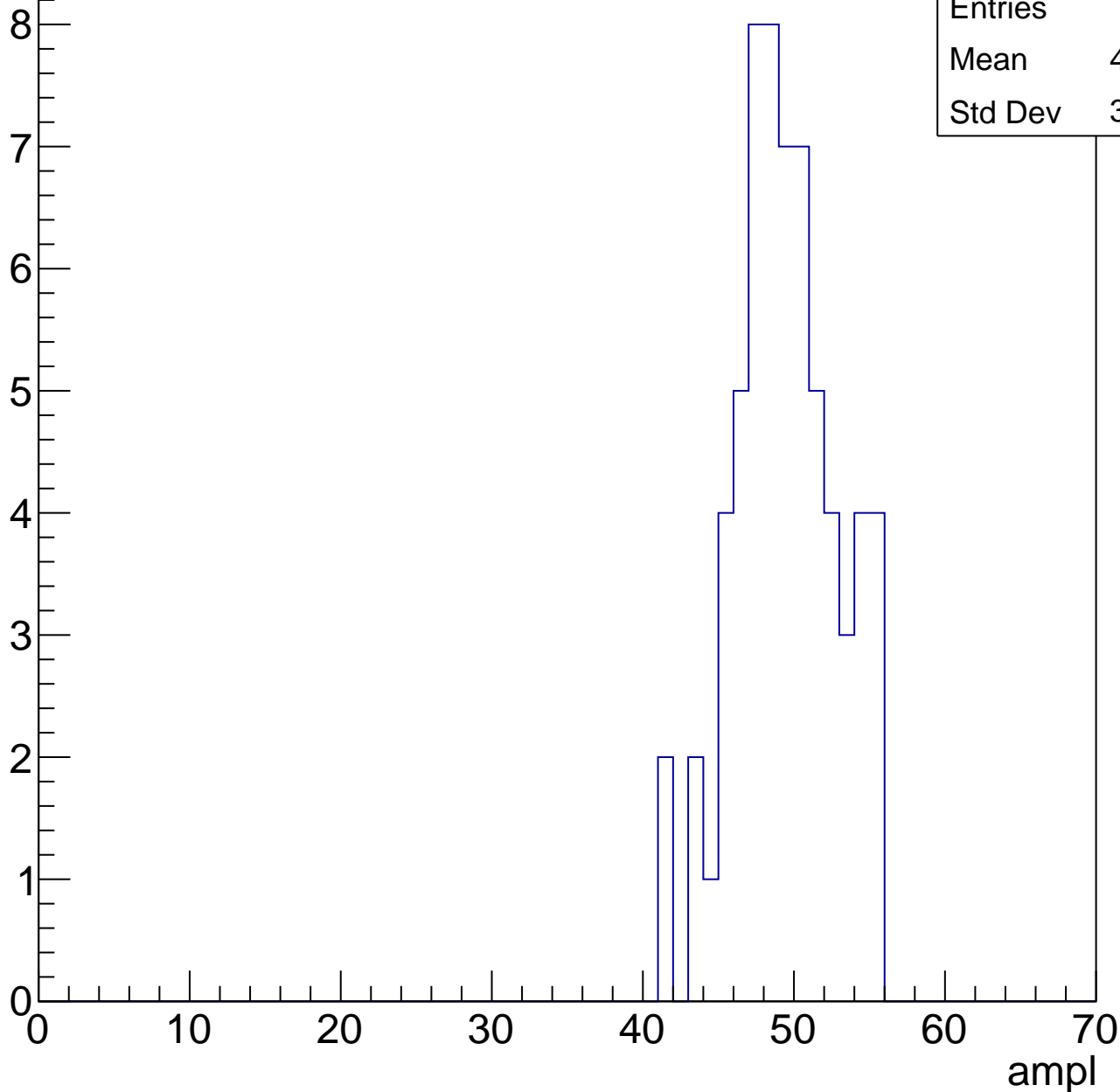


# B0L000S, U19-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	48.95
Std Dev	3.375

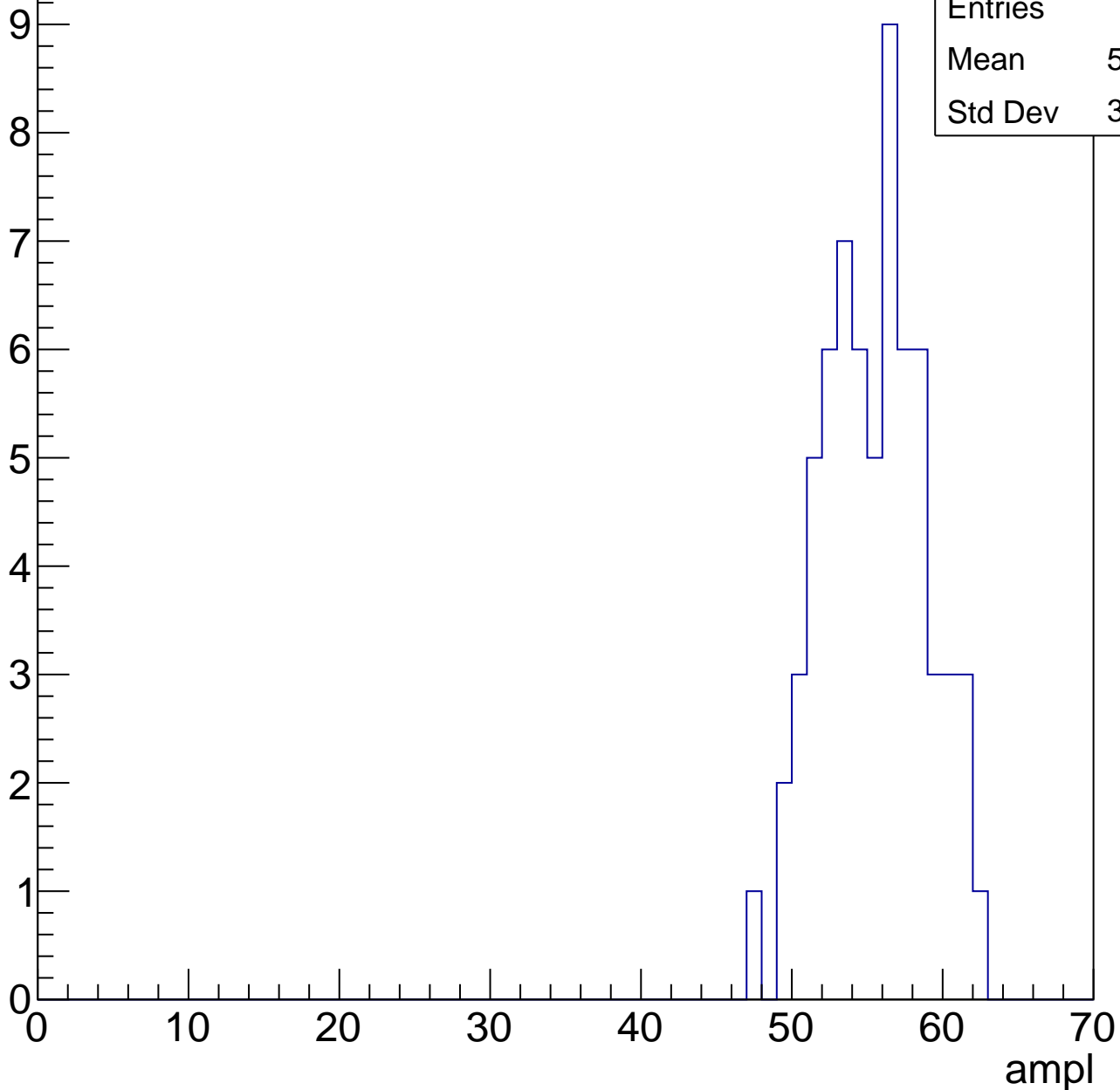


# B0L000S, U19-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	54.97
Std Dev	3.362

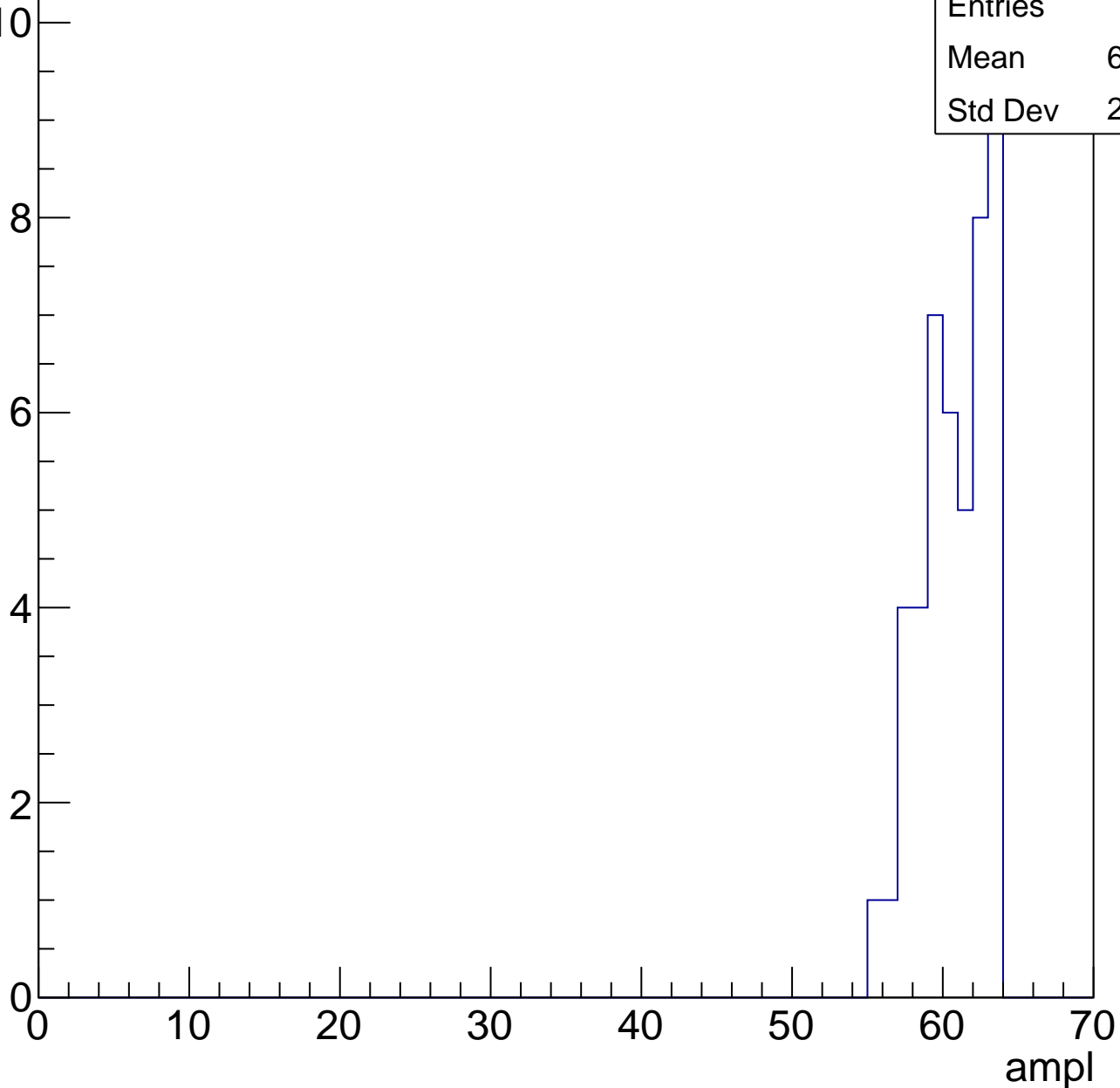


# B0L000S, U19-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

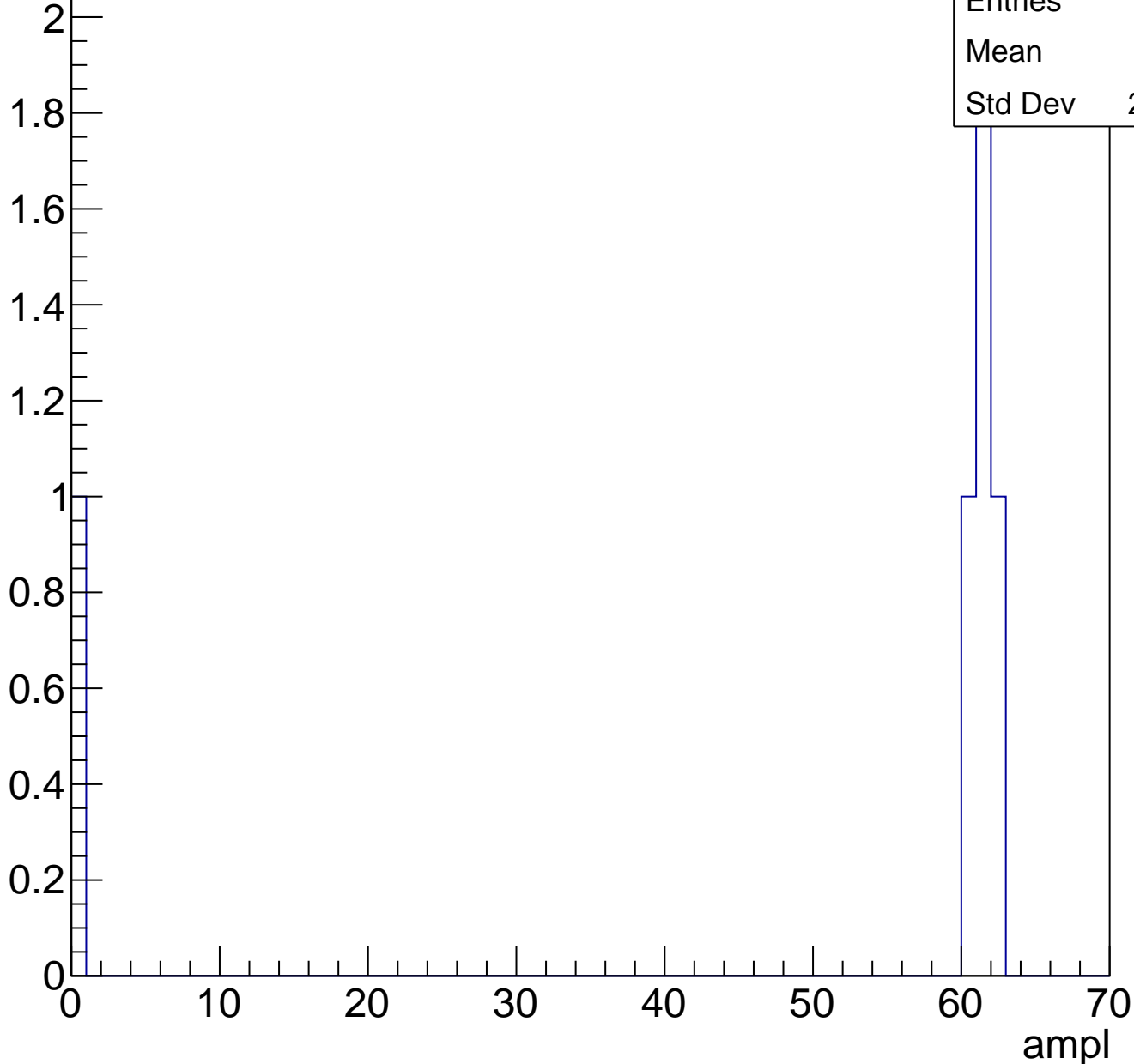
Entries	46
Mean	60.33
Std Dev	2.197



# B0L000S, U19-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	48.8
Std Dev	24.41



# B0L000S, U19-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U19-ch4, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	29.58
Std Dev	5.808

**Gaus mean : 30.7422**

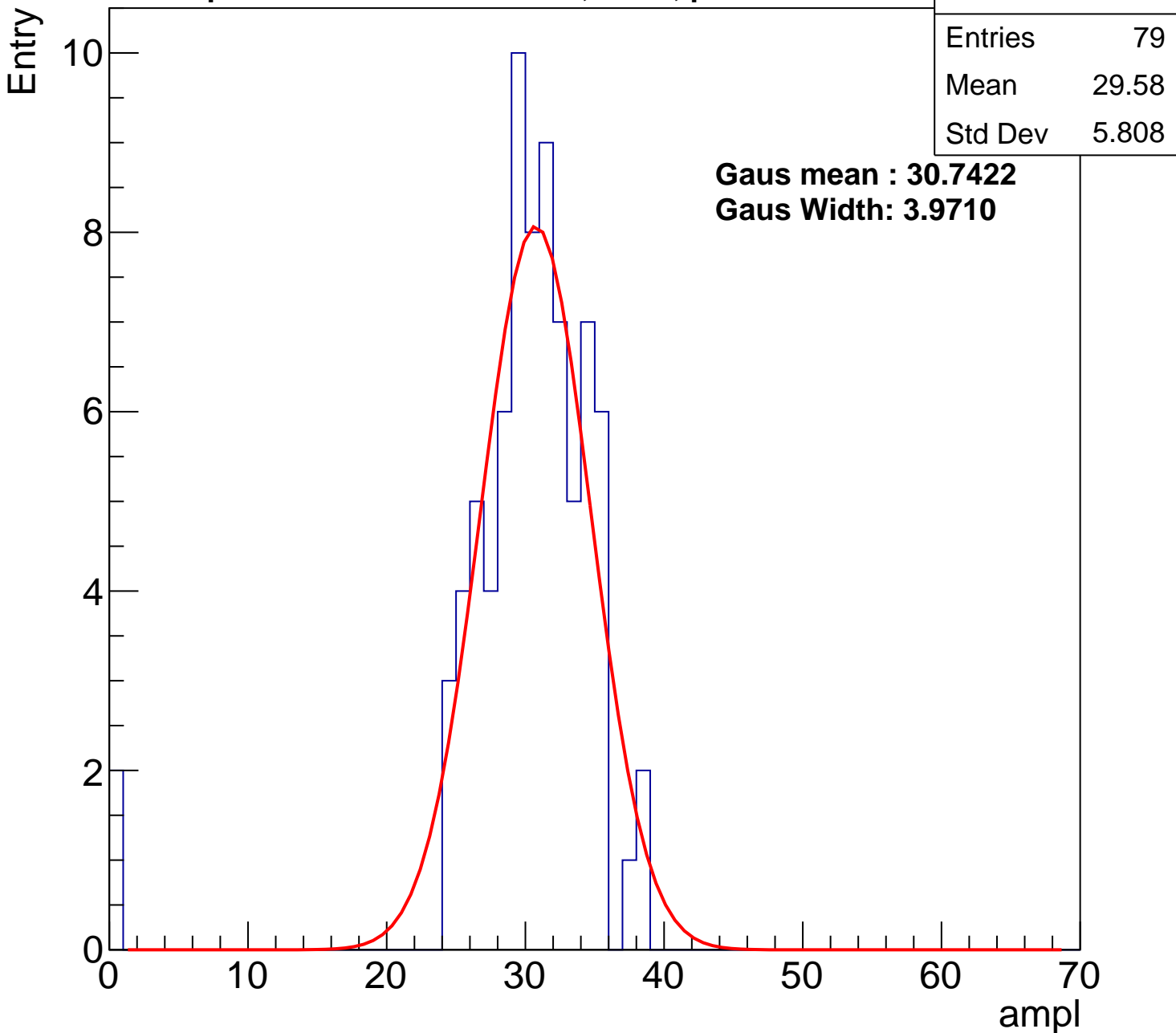
**Gaus Width: 3.9710**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	37.36
Std Dev	3.422

**Gaus mean : 38.0561**

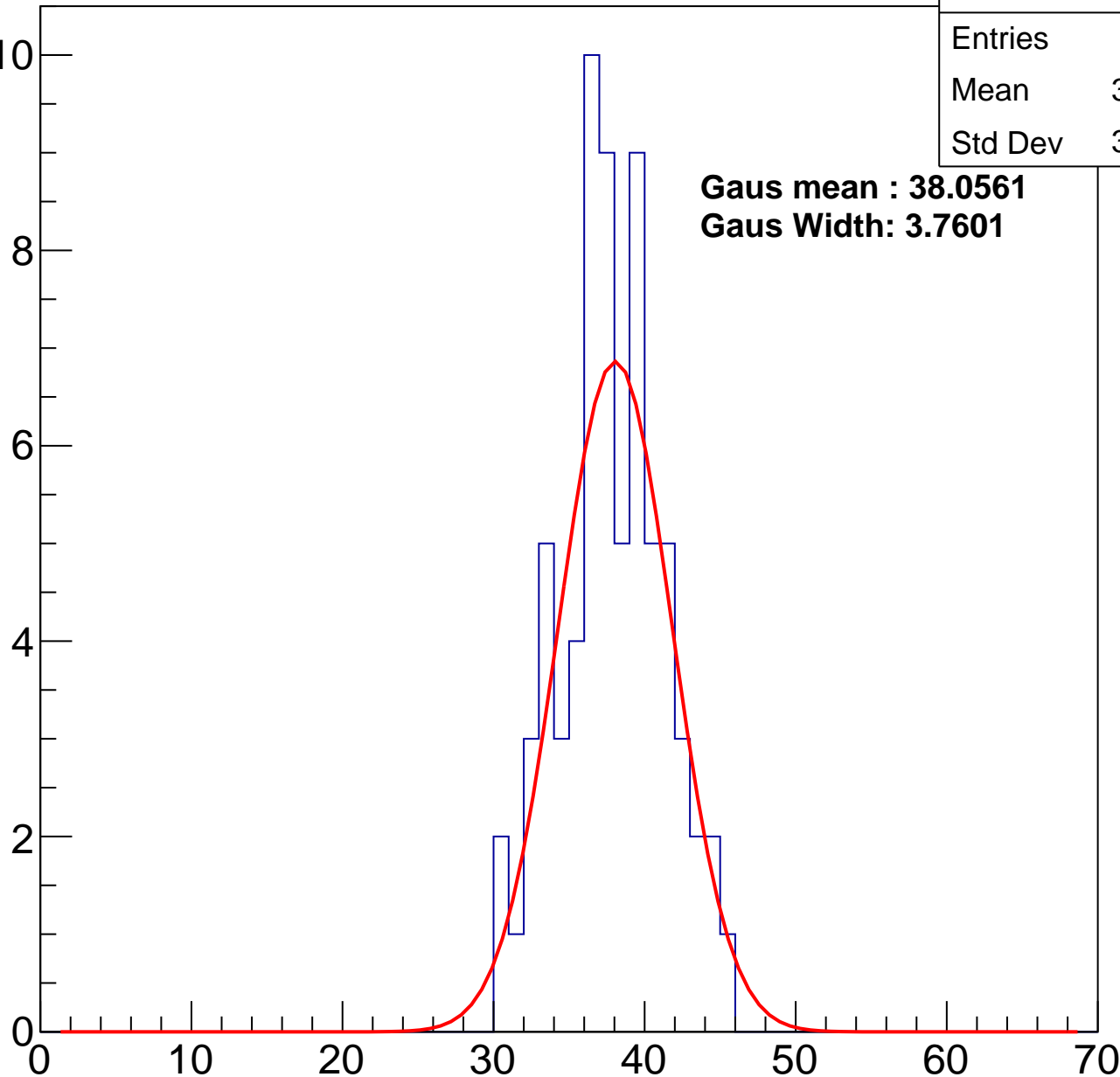
**Gaus Width: 3.7601**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



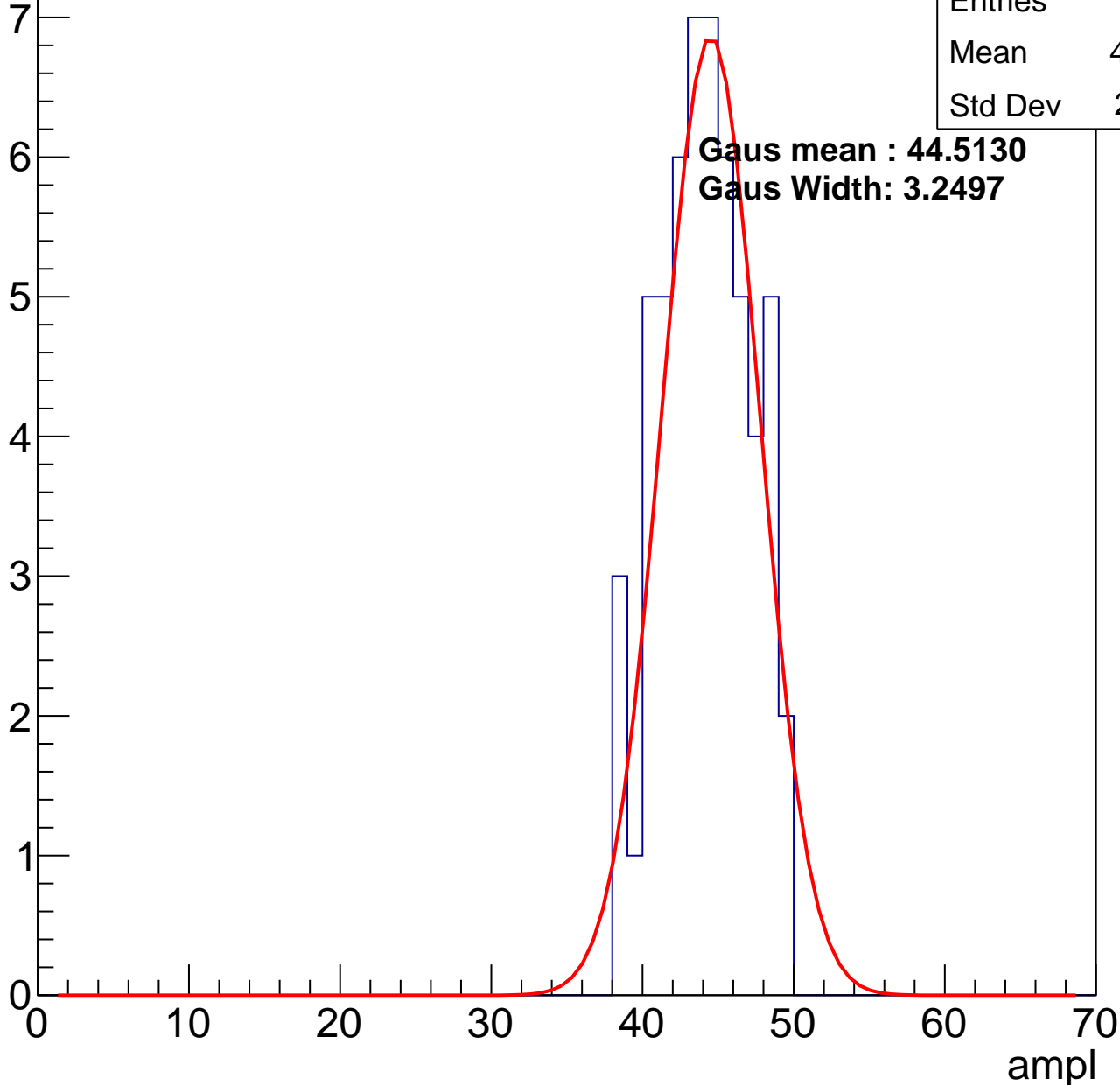
# B0L000S, U19-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43.66
Std Dev	2.911

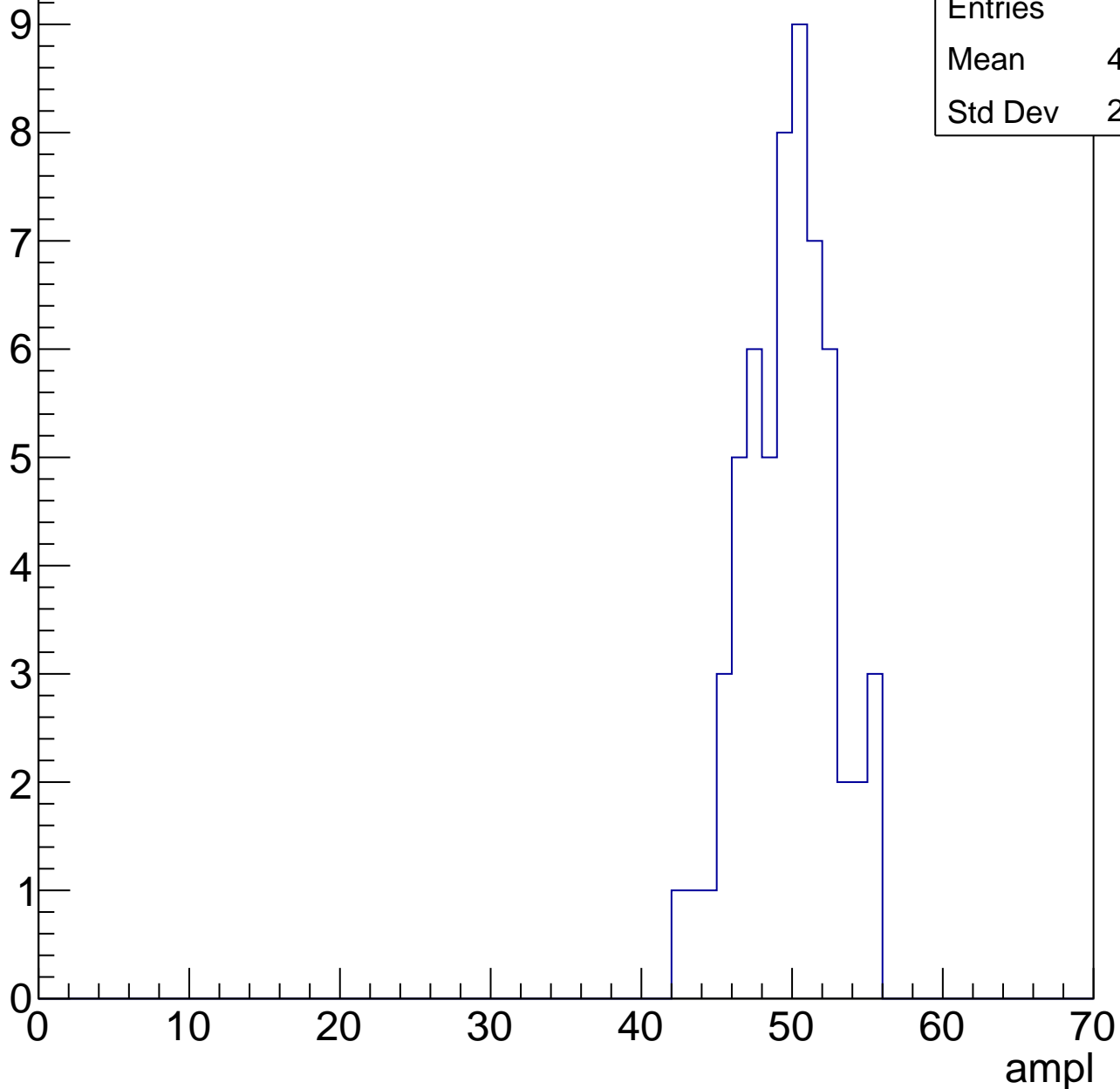
**Gaus mean : 44.5130**  
**Gaus Width: 3.2497**



# B0L000S, U19-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

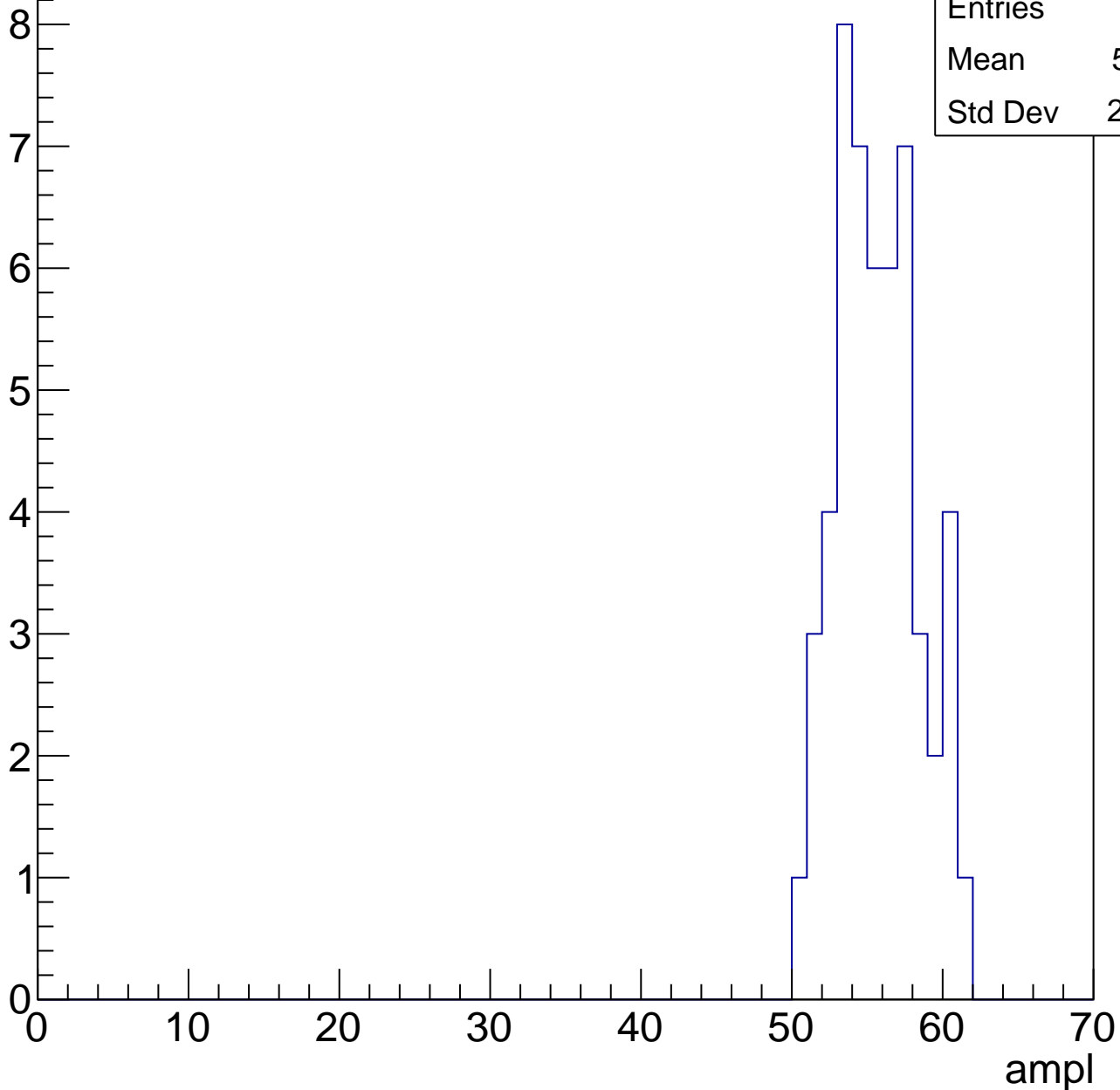


# B0L000S, U19-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

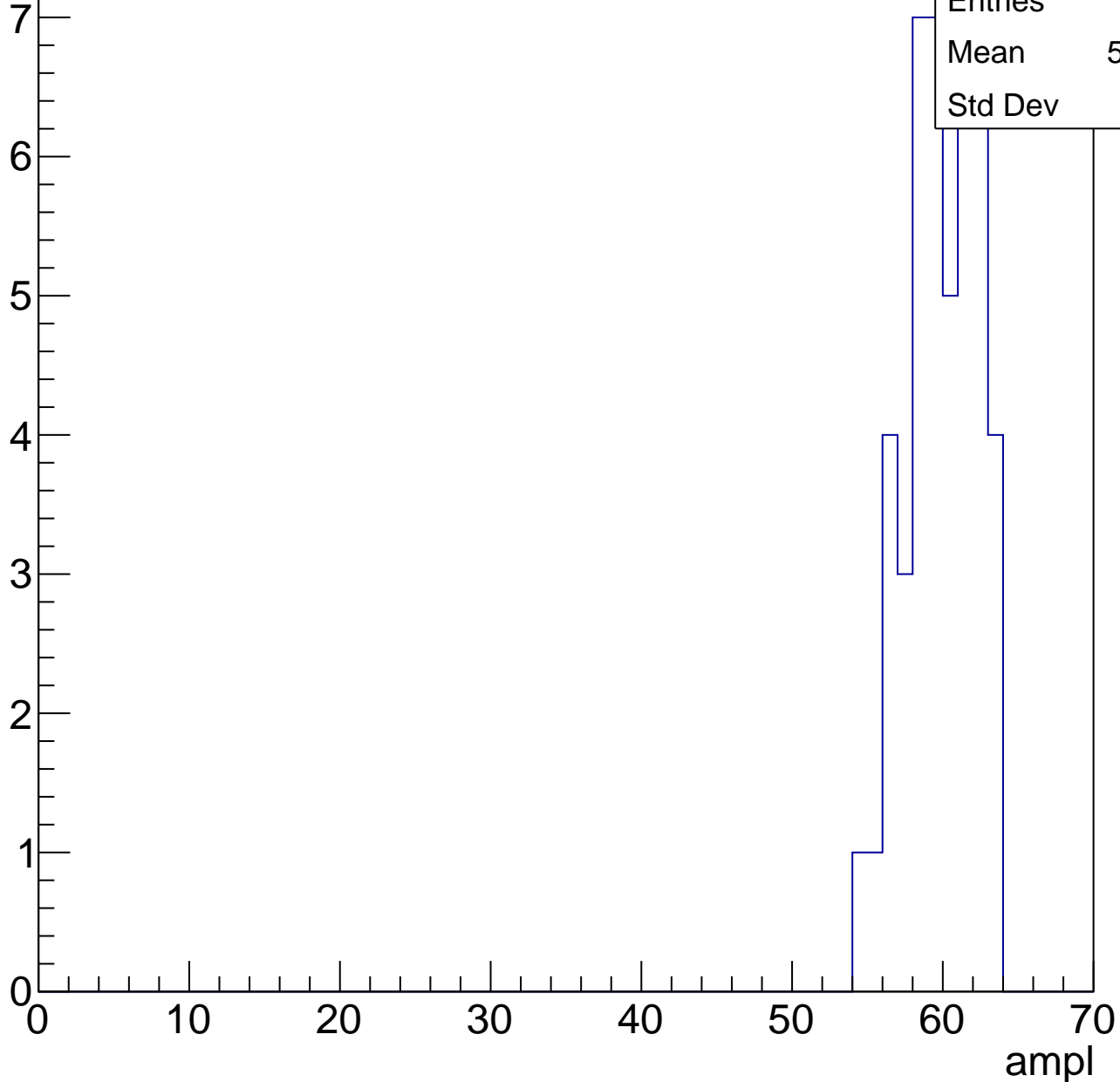
Entries	52
Mean	55.21
Std Dev	2.684



# B0L000S, U19-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



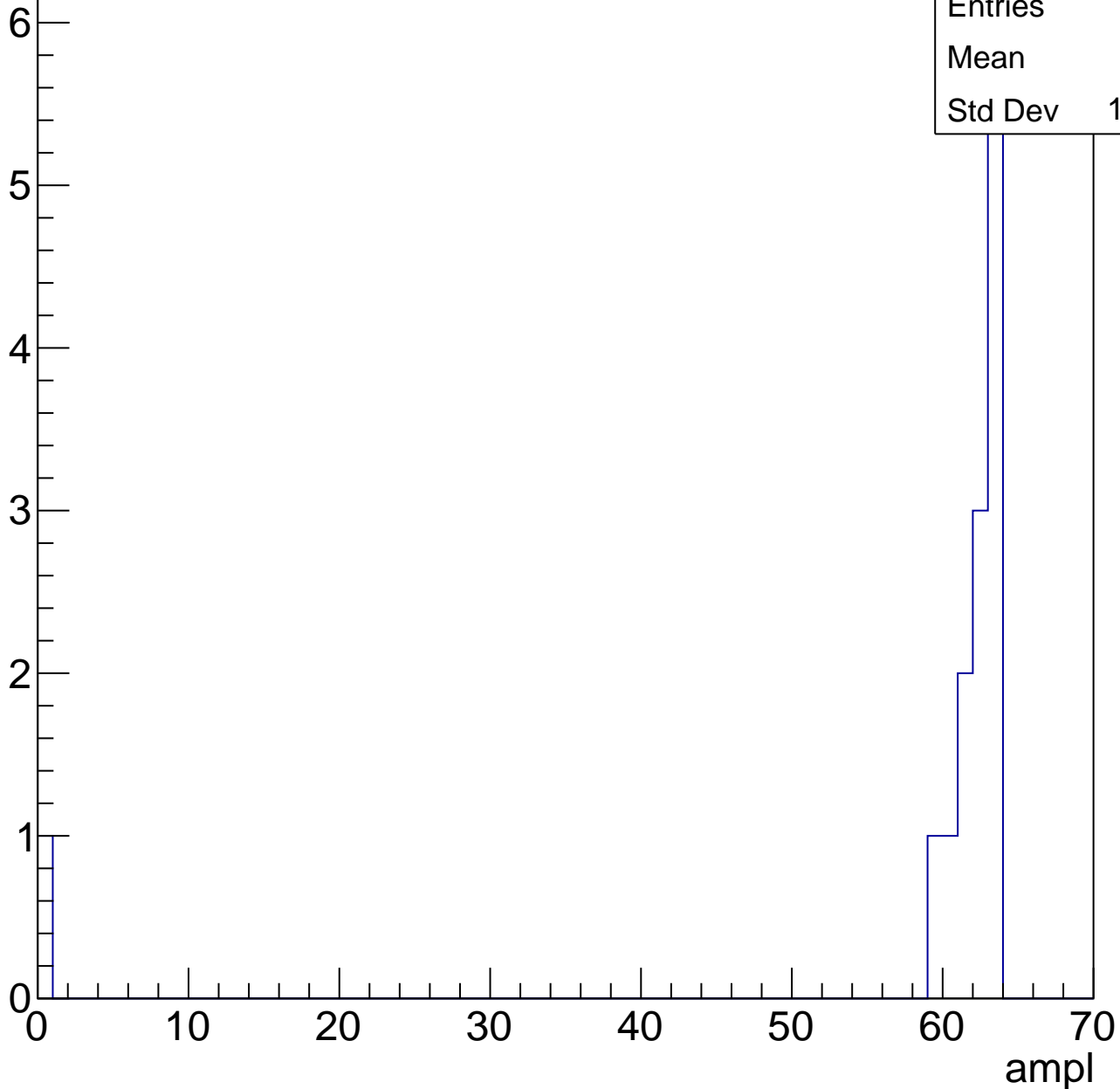
Entries	46
Mean	59.48
Std Dev	2.31

# B0L000S, U19-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	57.5
Std Dev	15.99





# B0L000S, U19-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch5, adc0

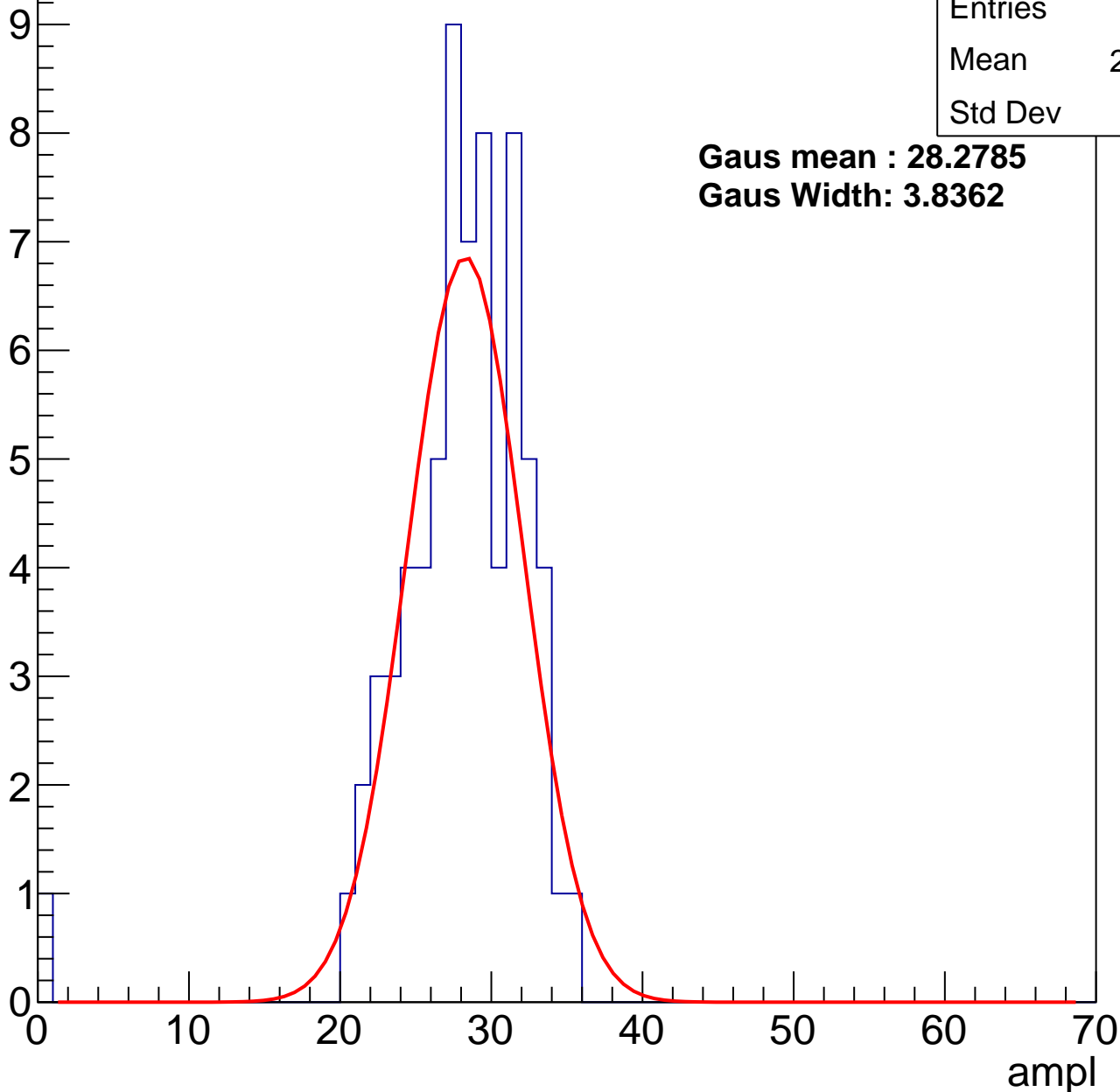
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	27.47
Std Dev	4.78

**Gaus mean : 28.2785**

**Gaus Width: 3.8362**



# B0L000S, U19-ch5, adc1

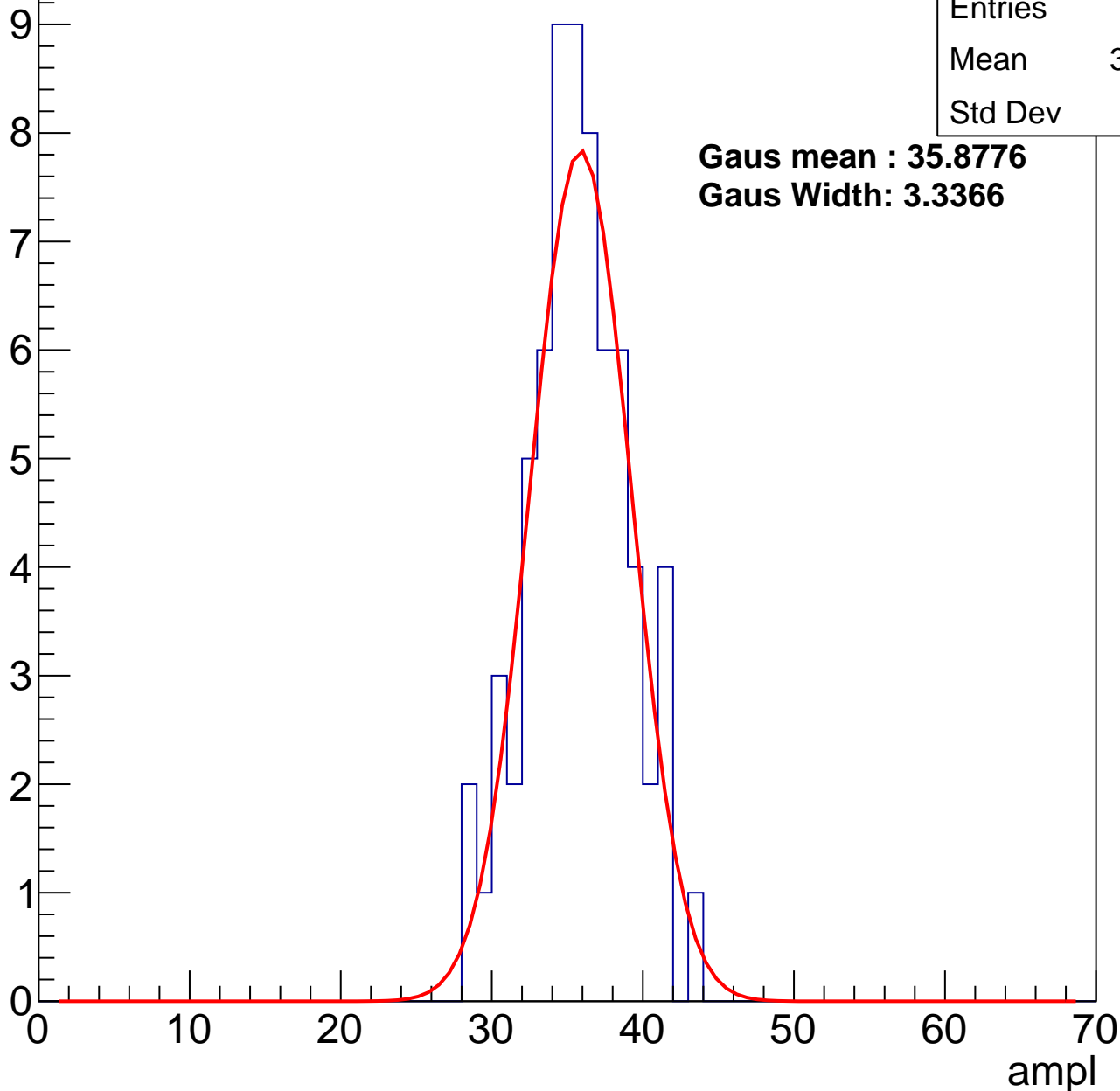
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	35.25
Std Dev	3.26

**Gaus mean : 35.8776**

**Gaus Width: 3.3366**



# B0L000S, U19-ch5, adc2

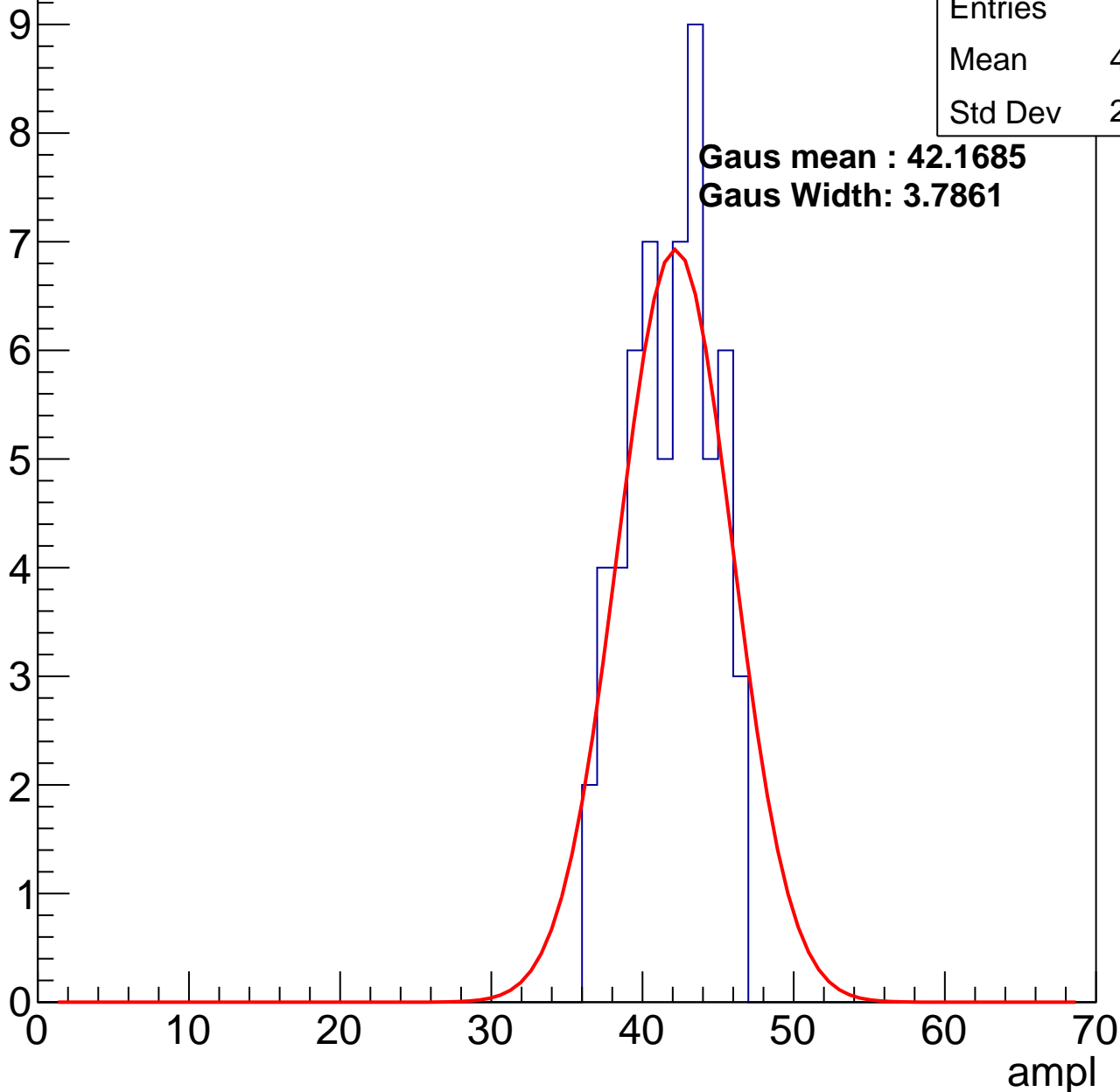
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	41.38
Std Dev	2.728

**Gaus mean : 42.1685**

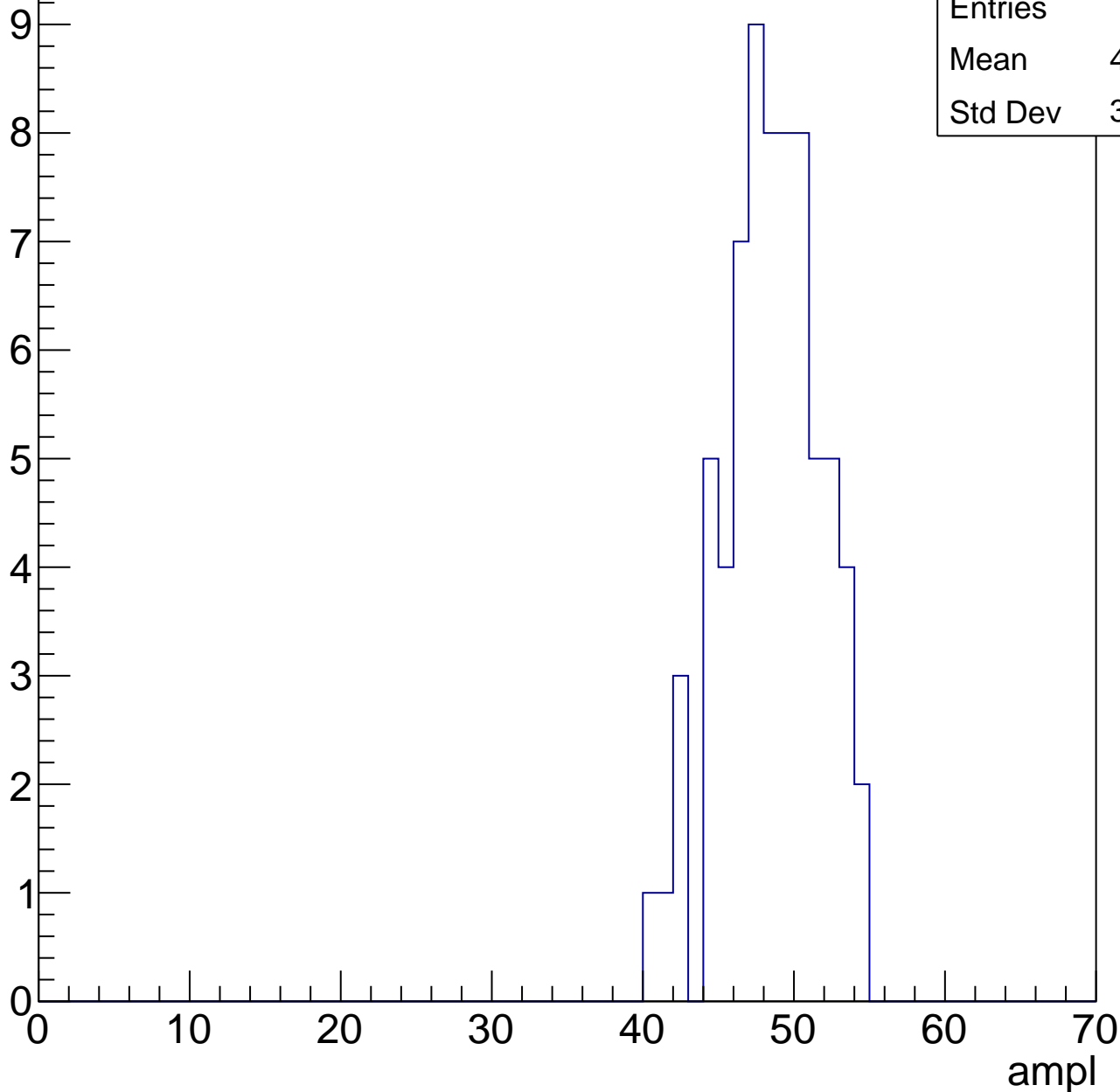
**Gaus Width: 3.7861**



# B0L000S, U19-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

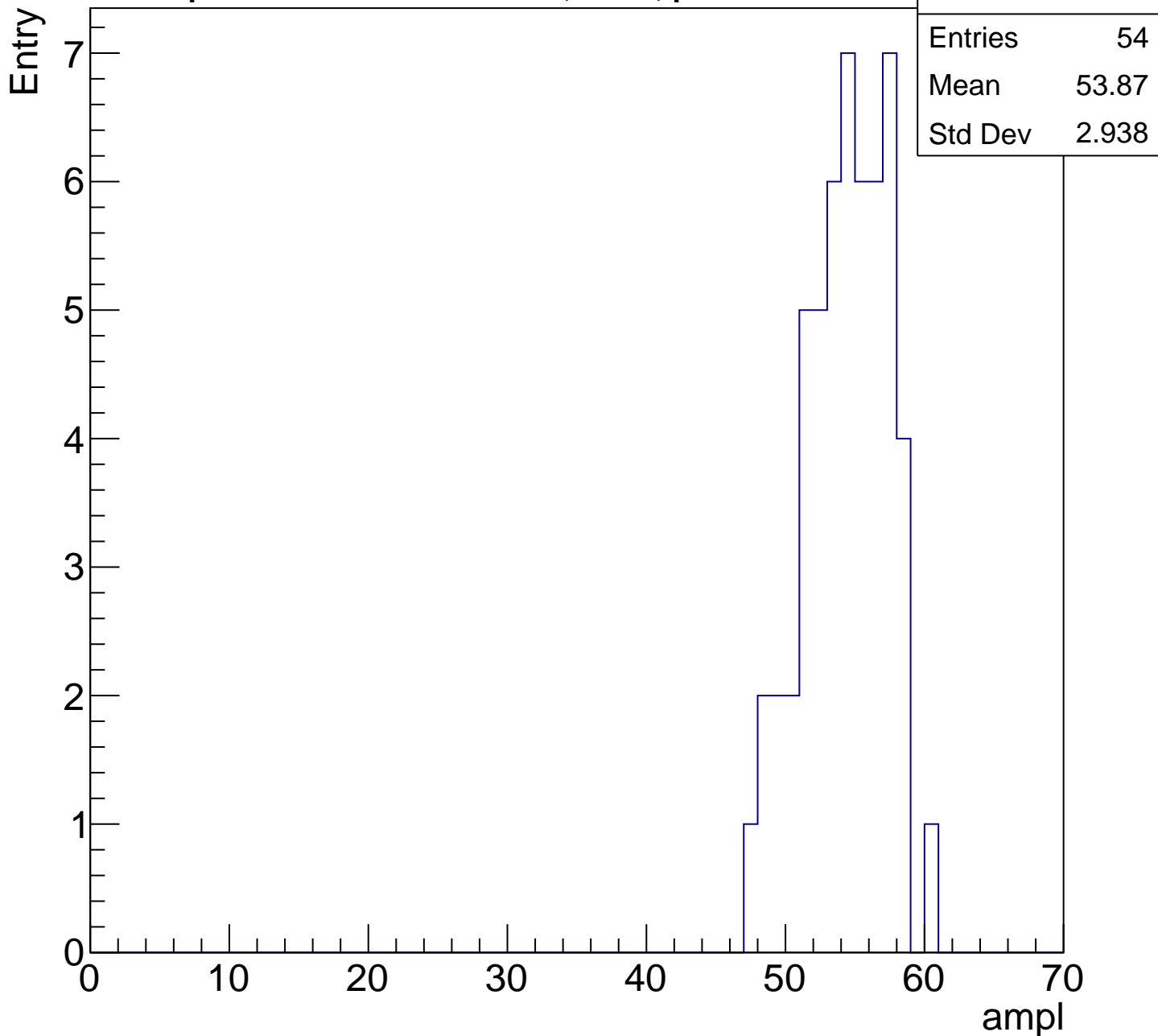
Entry



Entries	70
Mean	48.04
Std Dev	3.187

# B0L000S, U19-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U19-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

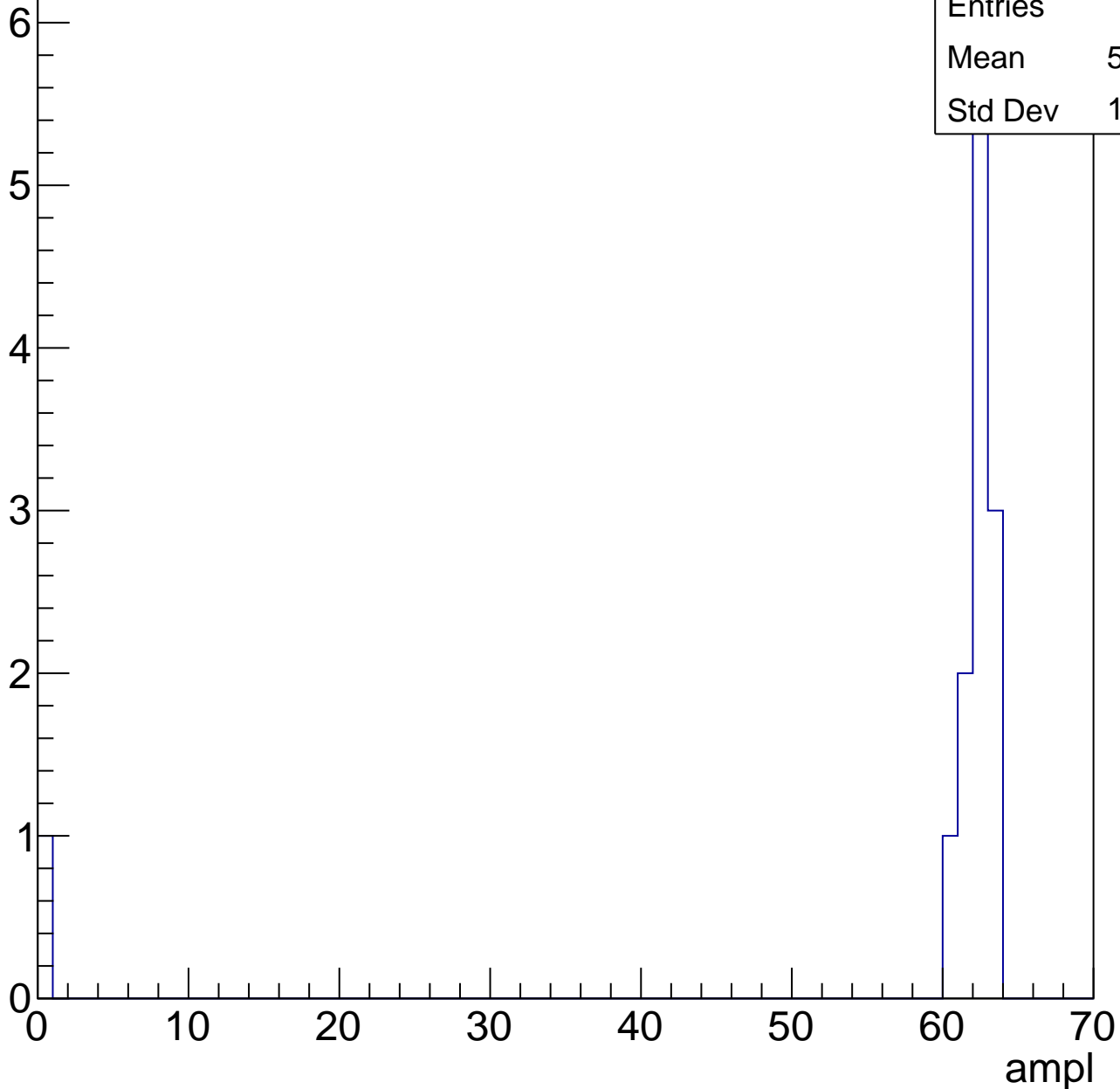
Entries	54
Mean	59.28
Std Dev	2.399

# B0L000S, U19-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	57.15
Std Dev	16.52





# B0L000S, U19-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch6, adc0

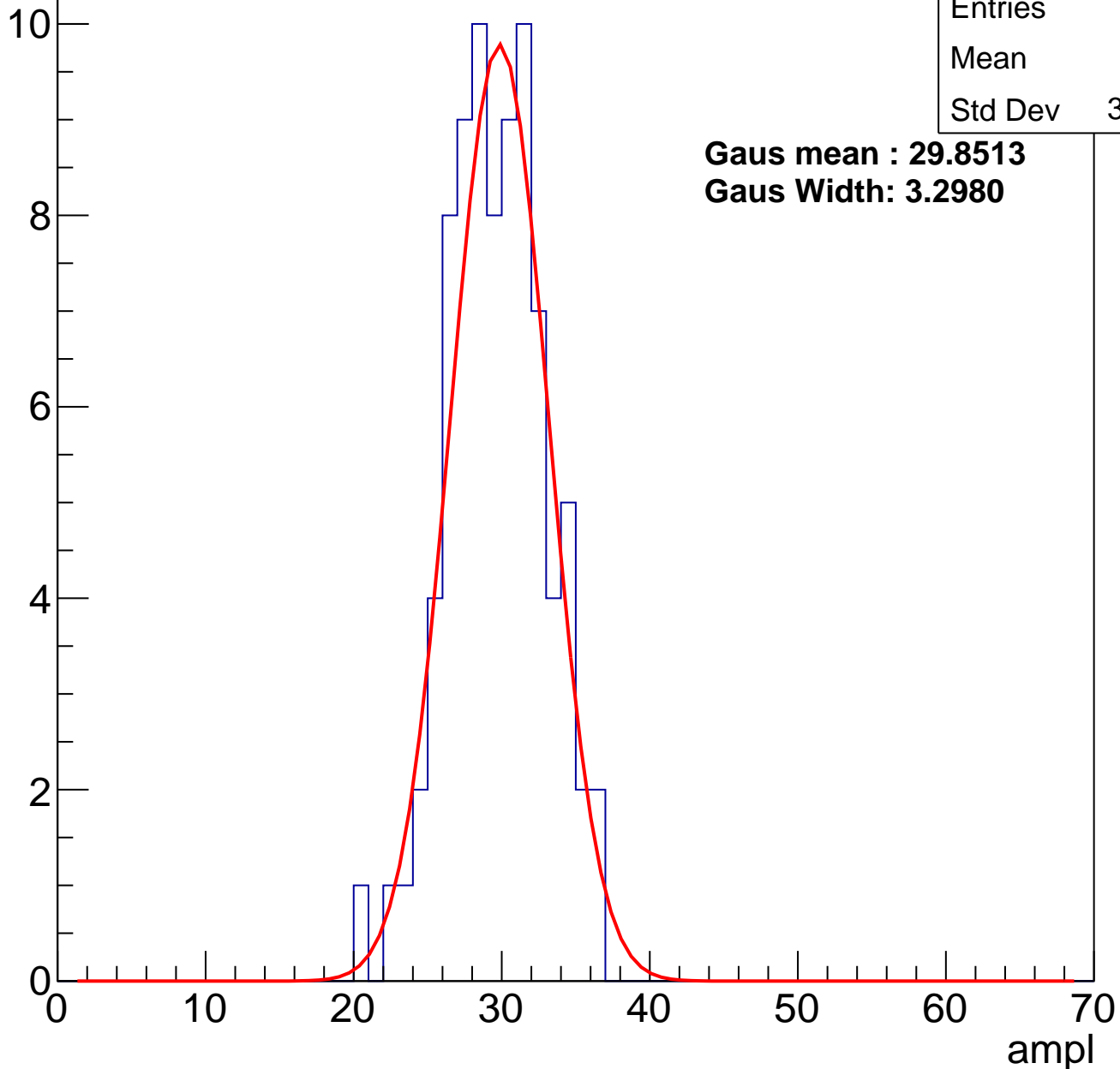
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	29.2
Std Dev	3.229

**Gaus mean : 29.8513**

**Gaus Width: 3.2980**

Entry



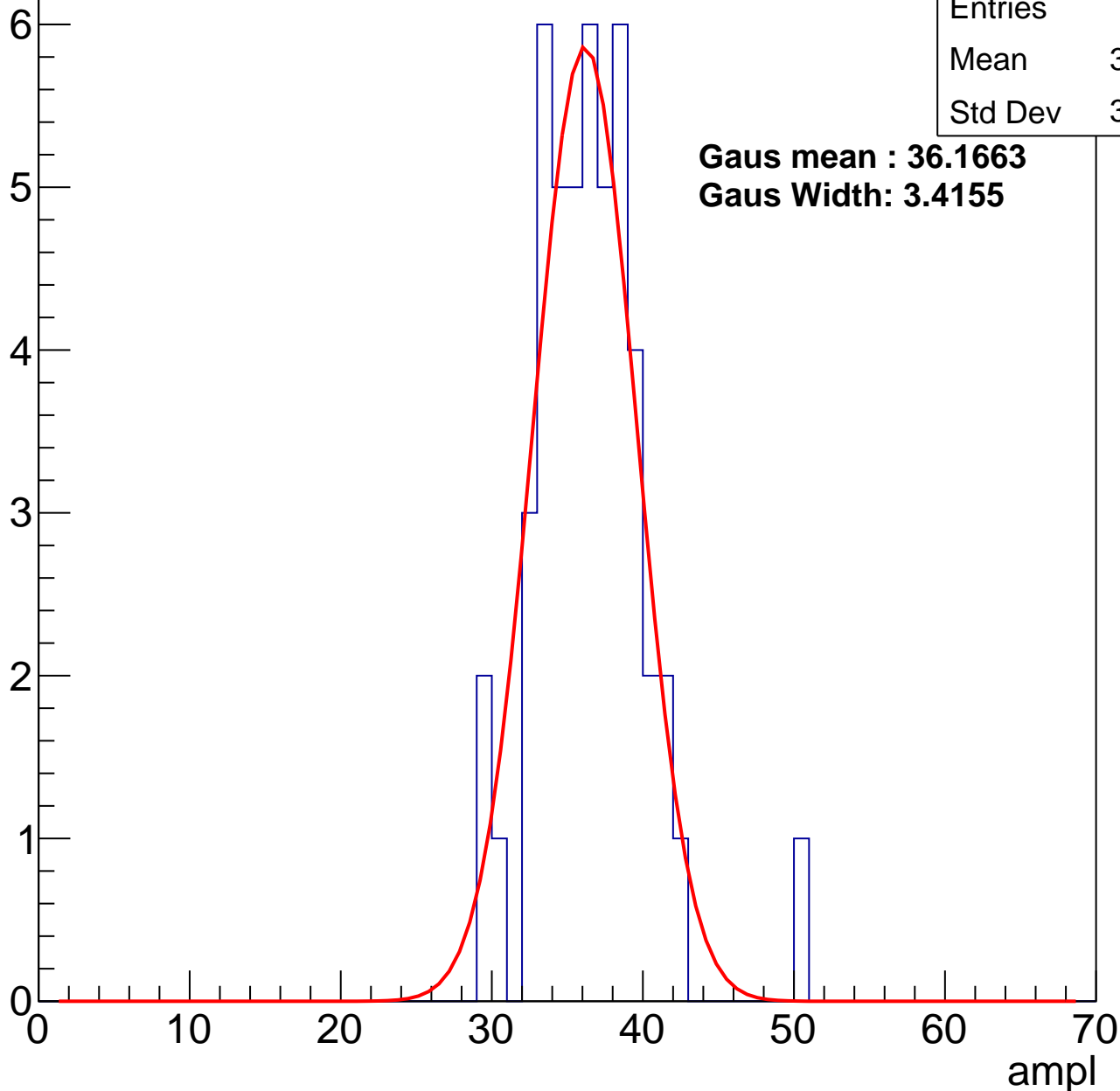
# B0L000S, U19-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.04
Std Dev	3.614

**Gaus mean : 36.1663**  
**Gaus Width: 3.4155**



# B0L000S, U19-ch6, adc2

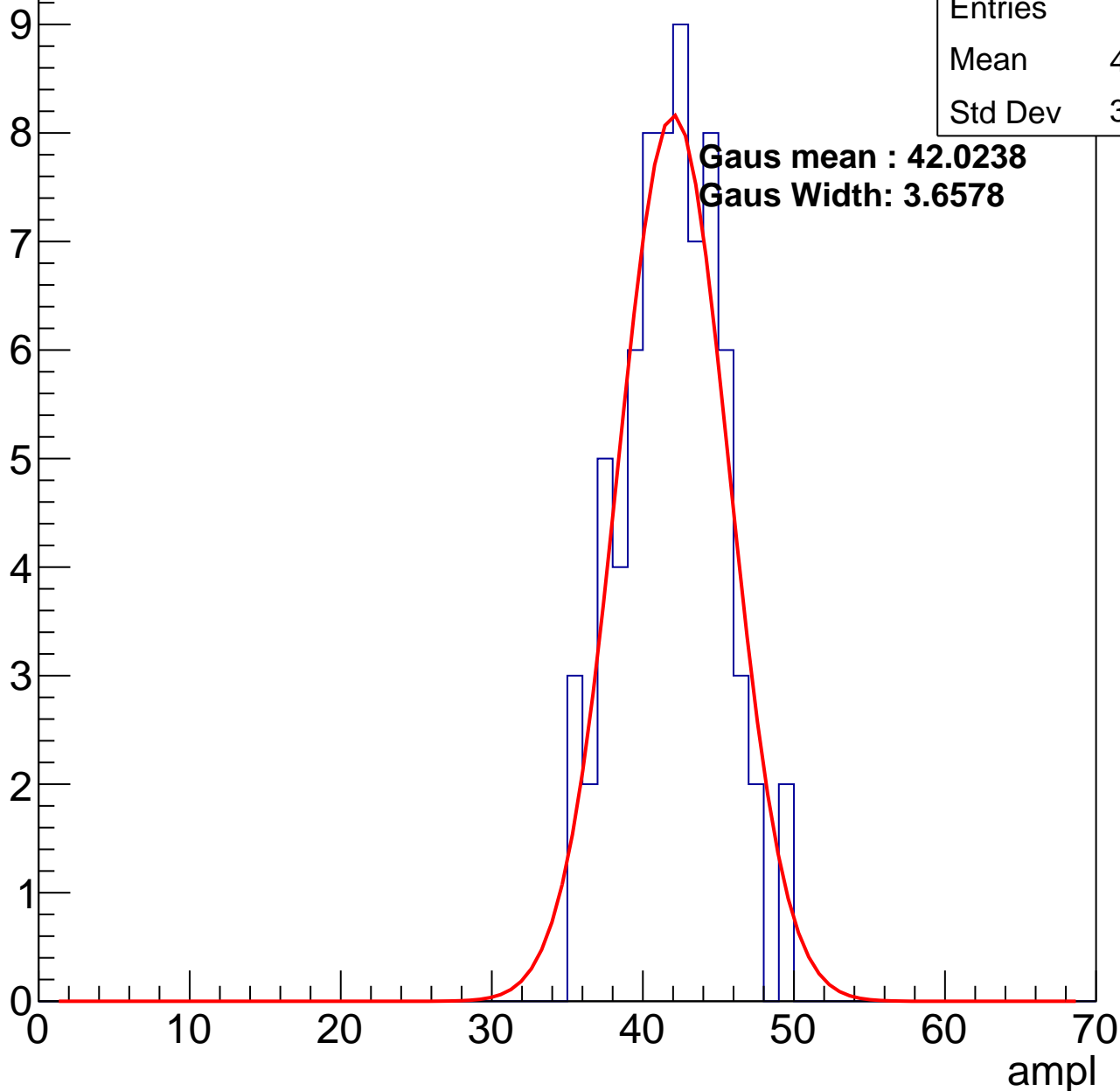
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	41.47
Std Dev	3.248

**Gaus mean : 42.0238**

**Gaus Width: 3.6578**

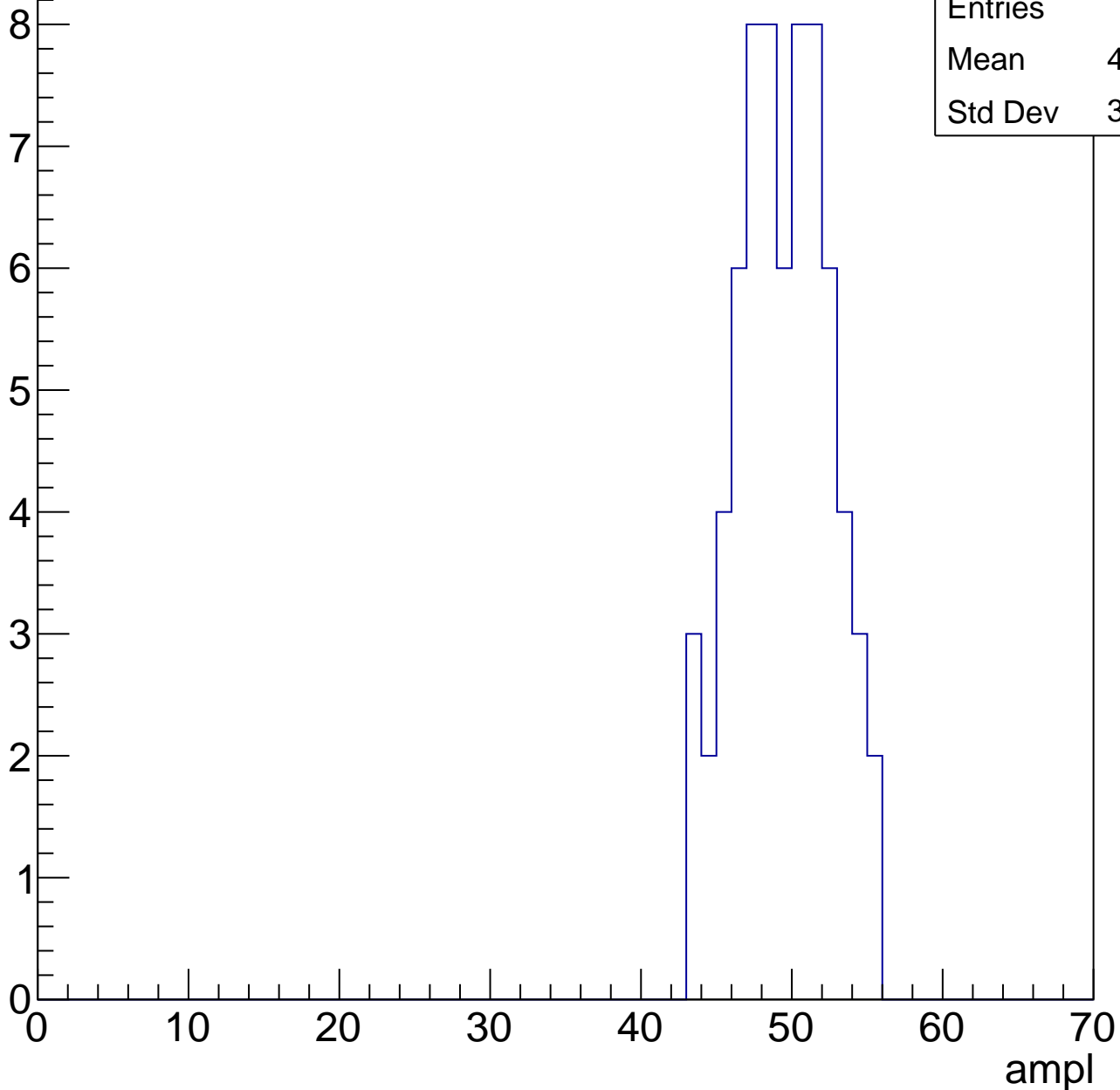


# B0L000S, U19-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	48.99
Std Dev	3.022

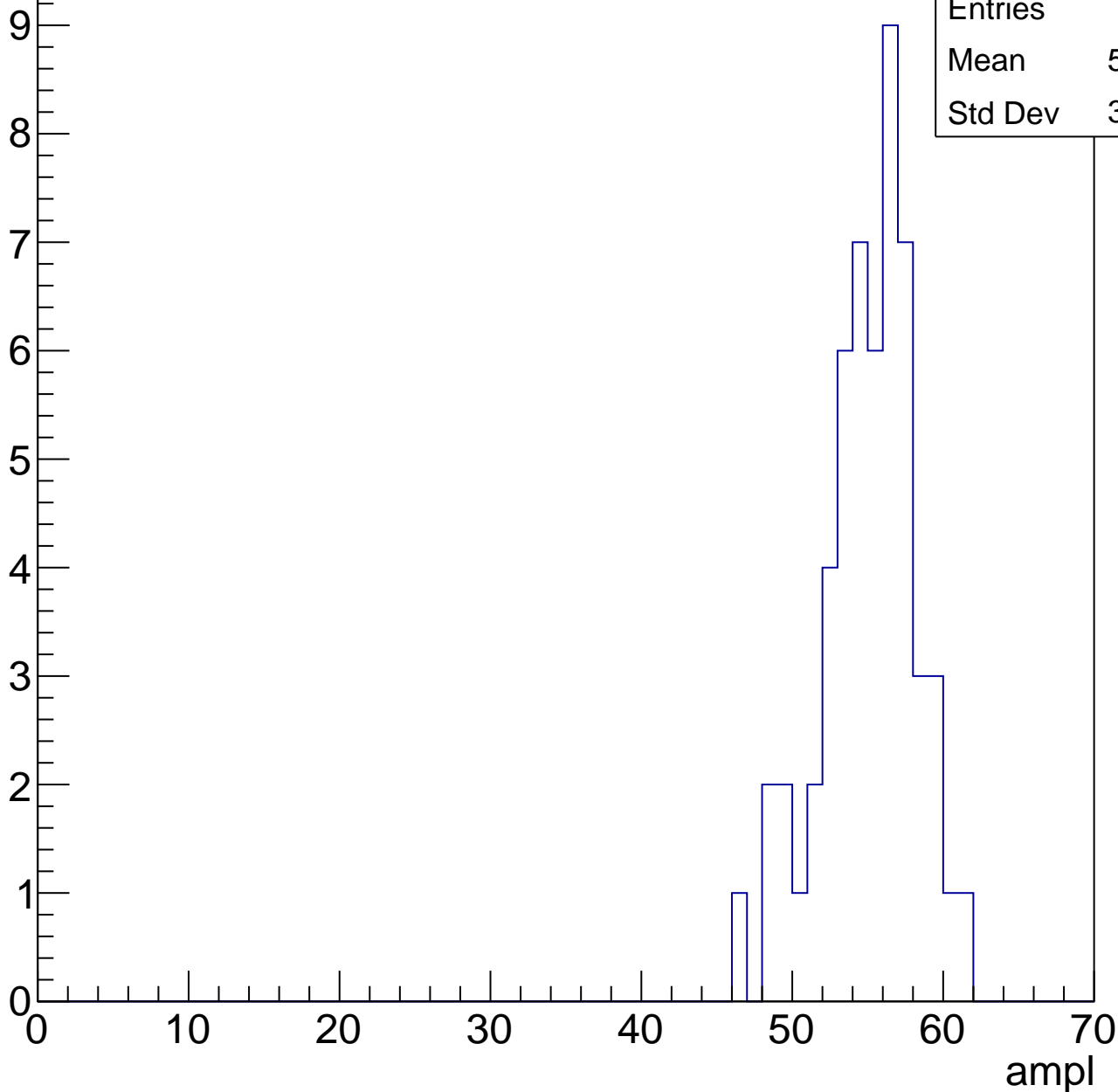


# B0L000S, U19-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

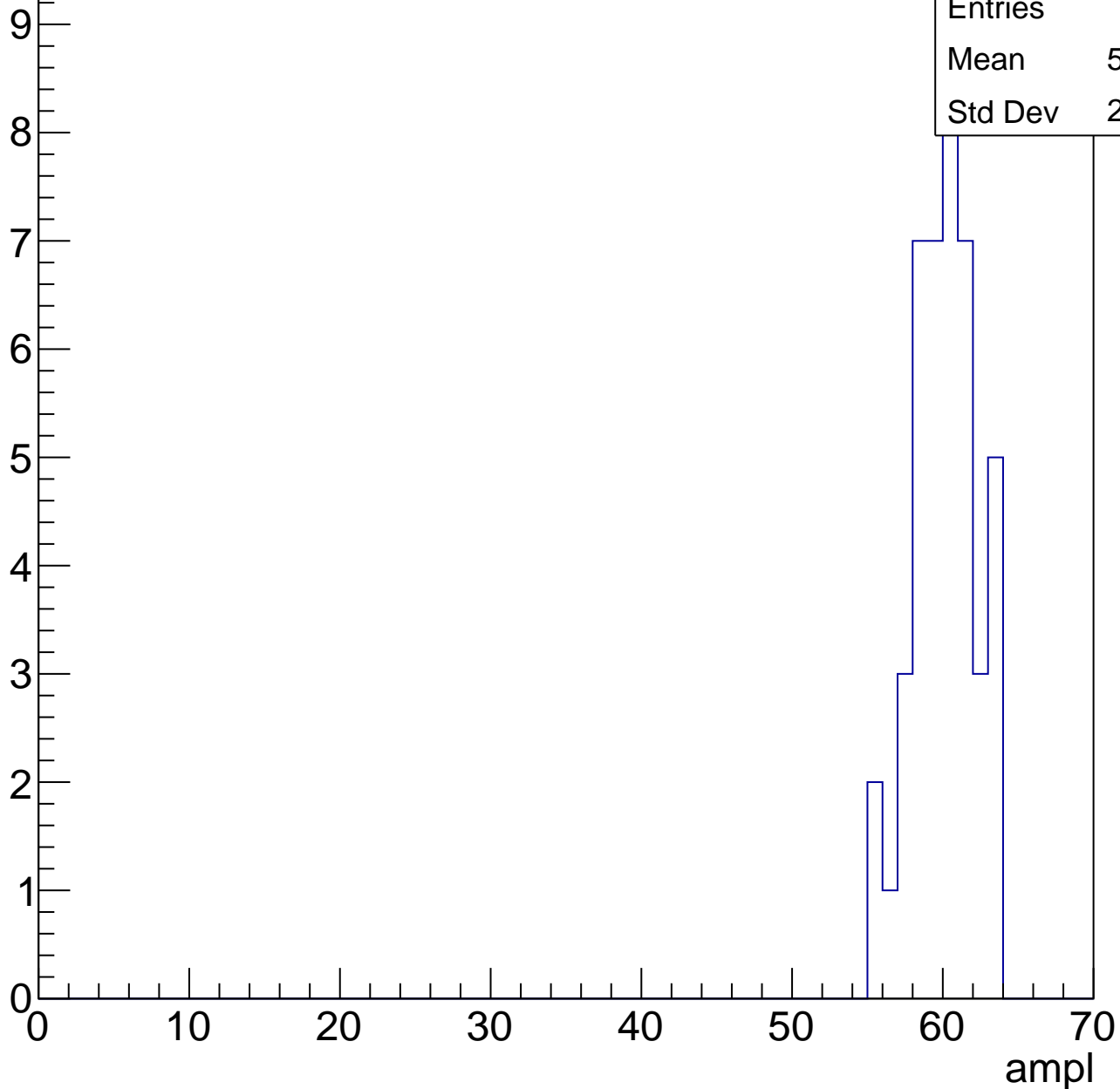
Entries	55
Mean	54.56
Std Dev	3.126



# B0L000S, U19-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

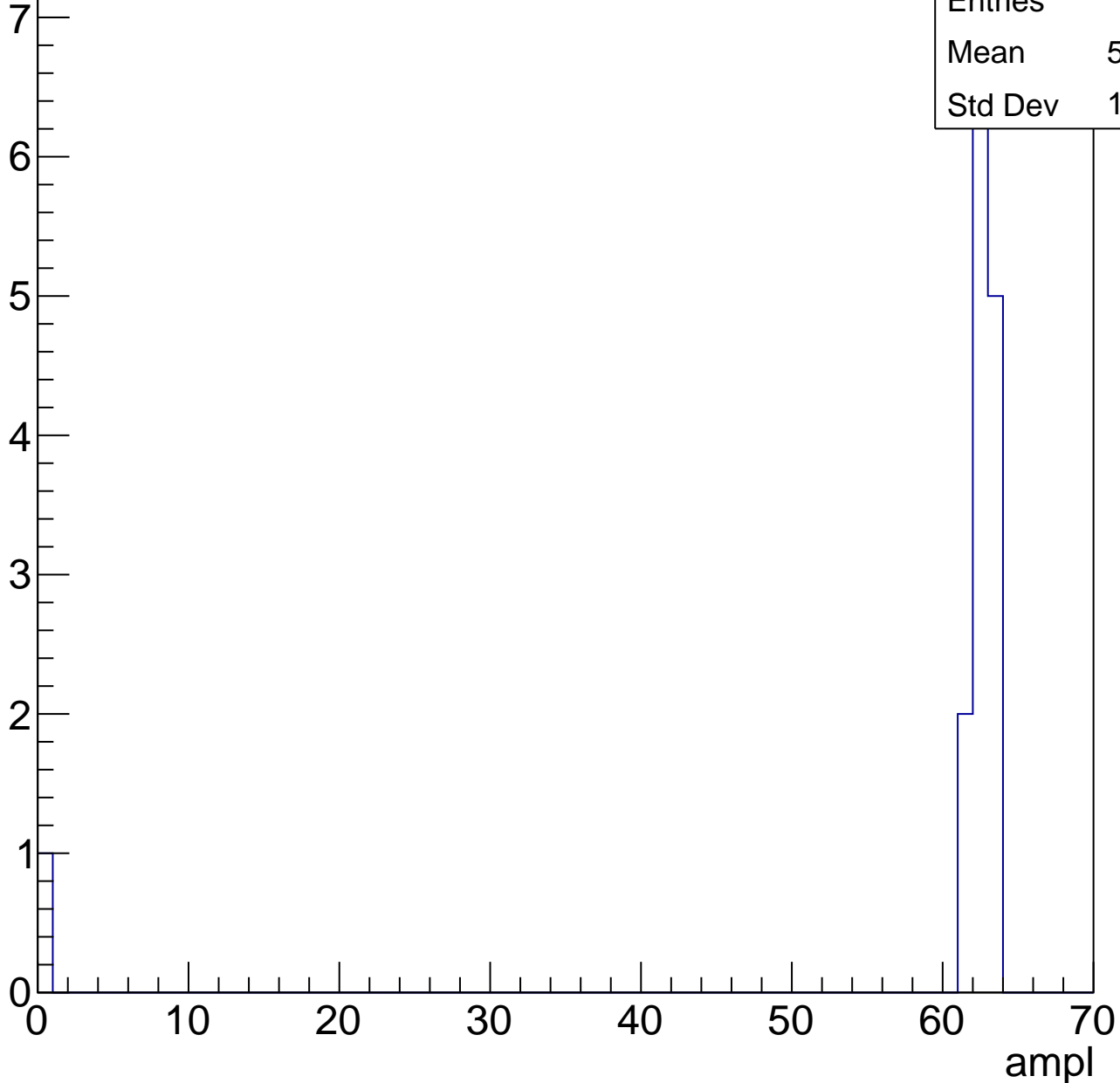


# B0L000S, U19-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	58.07
Std Dev	15.53





# B0L000S, U19-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch7, adc0

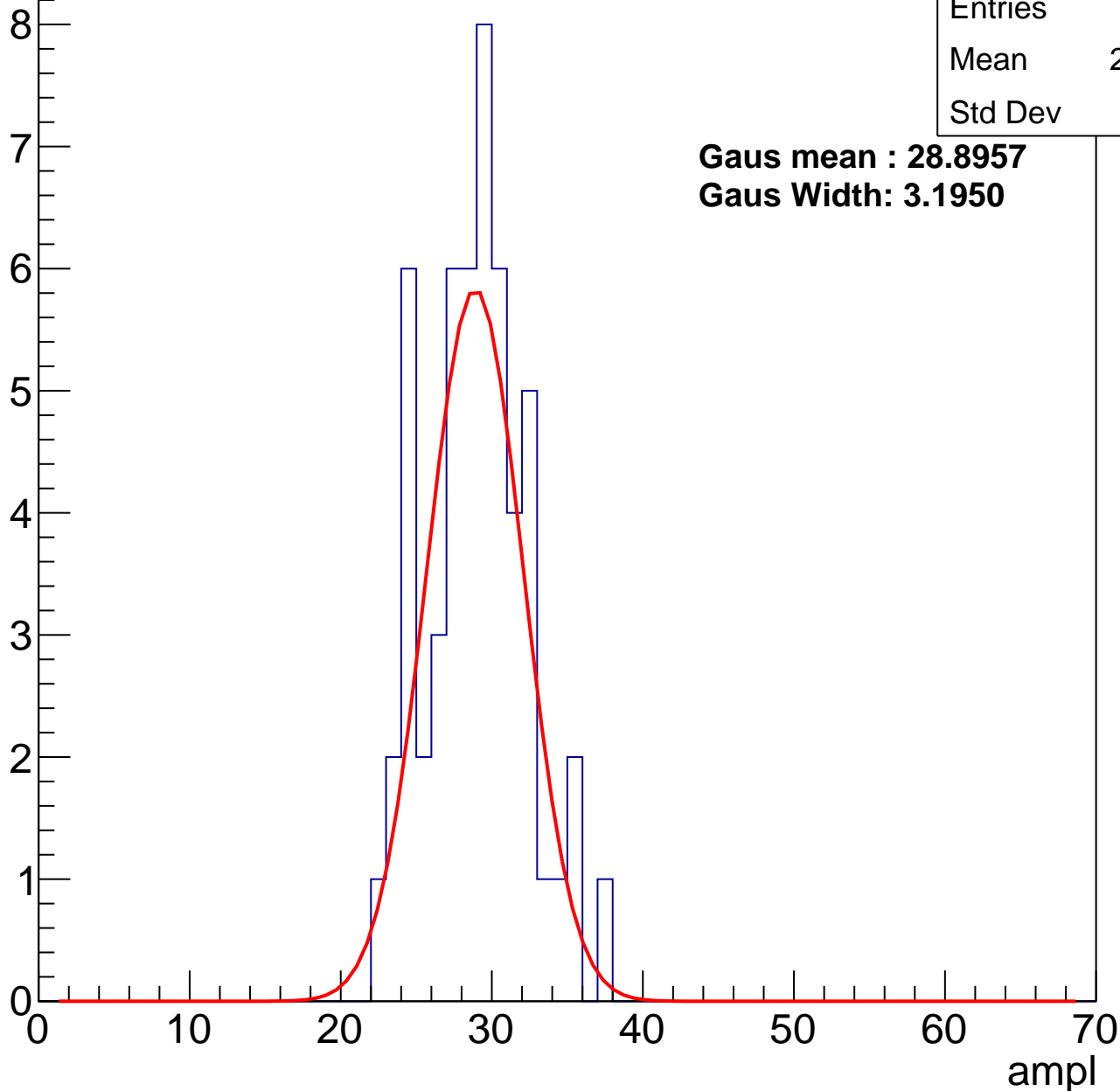
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	28.52
Std Dev	3.31

**Gaus mean : 28.8957**

**Gaus Width: 3.1950**



# B0L000S, U19-ch7, adc1

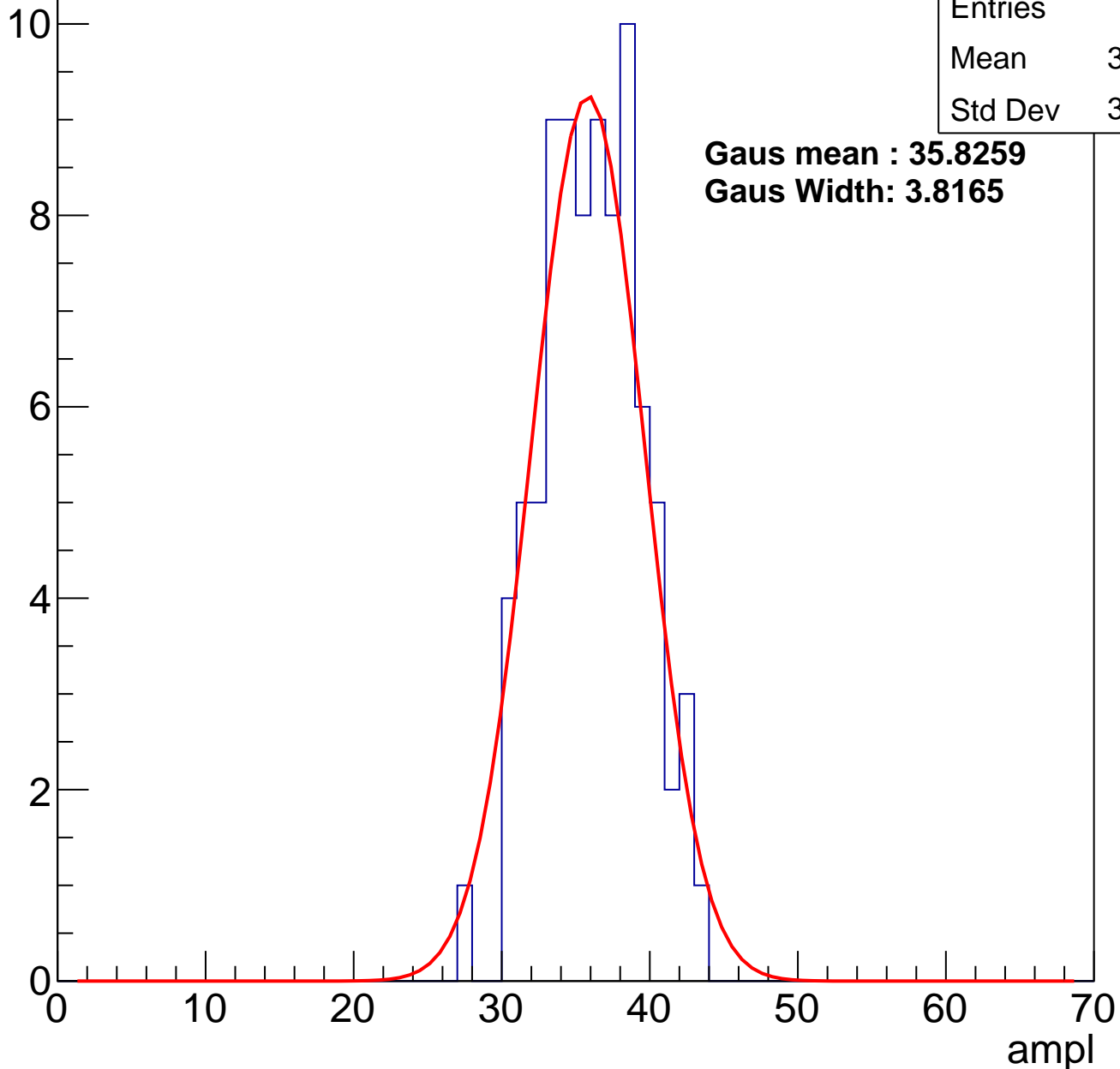
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	35.65
Std Dev	3.314

**Gaus mean : 35.8259**

**Gaus Width: 3.8165**

Entry



# B0L000S, U19-ch7, adc2

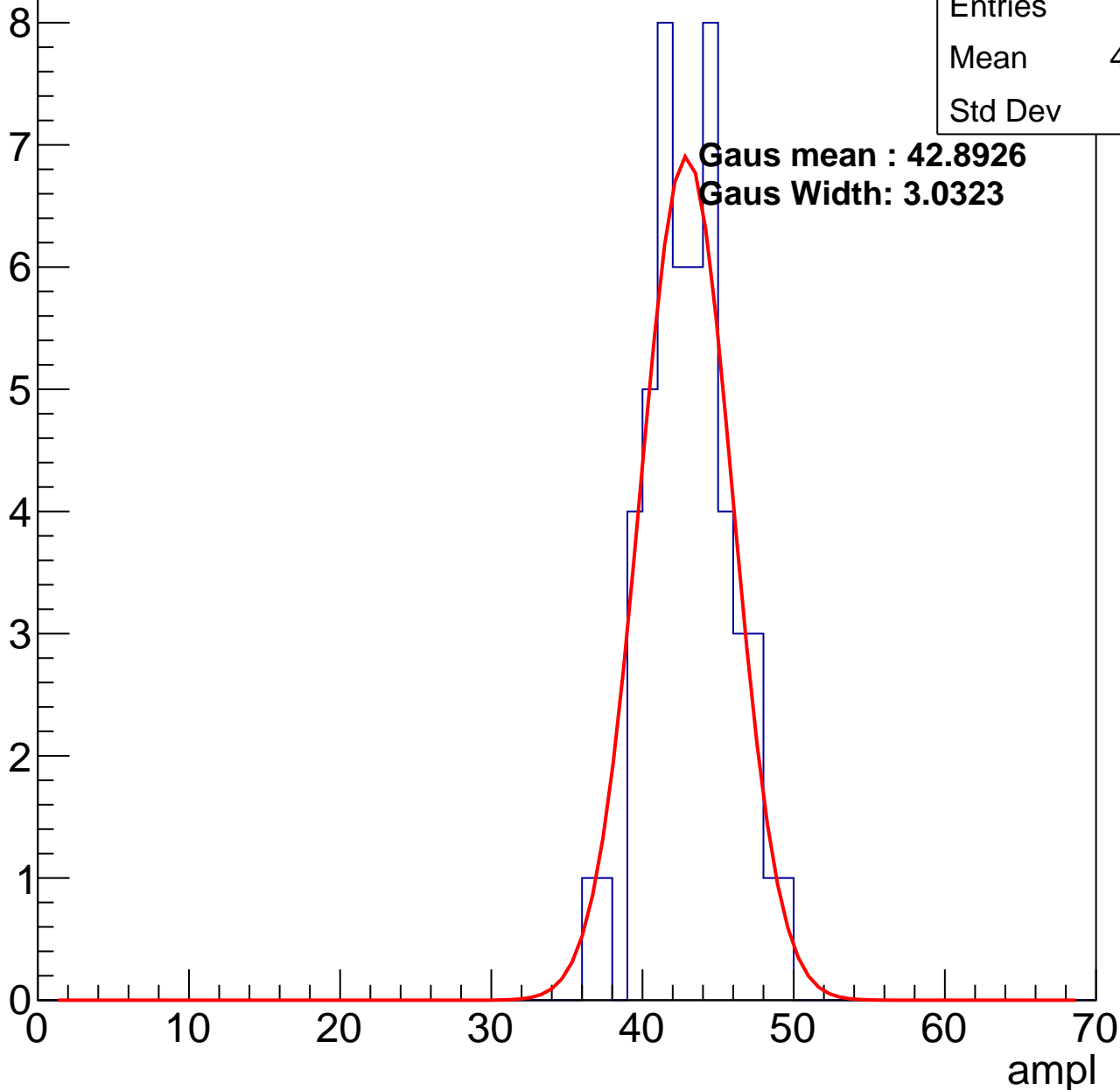
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	42.65
Std Dev	2.75

**Gaus mean : 42.8926**

**Gaus Width: 3.0323**

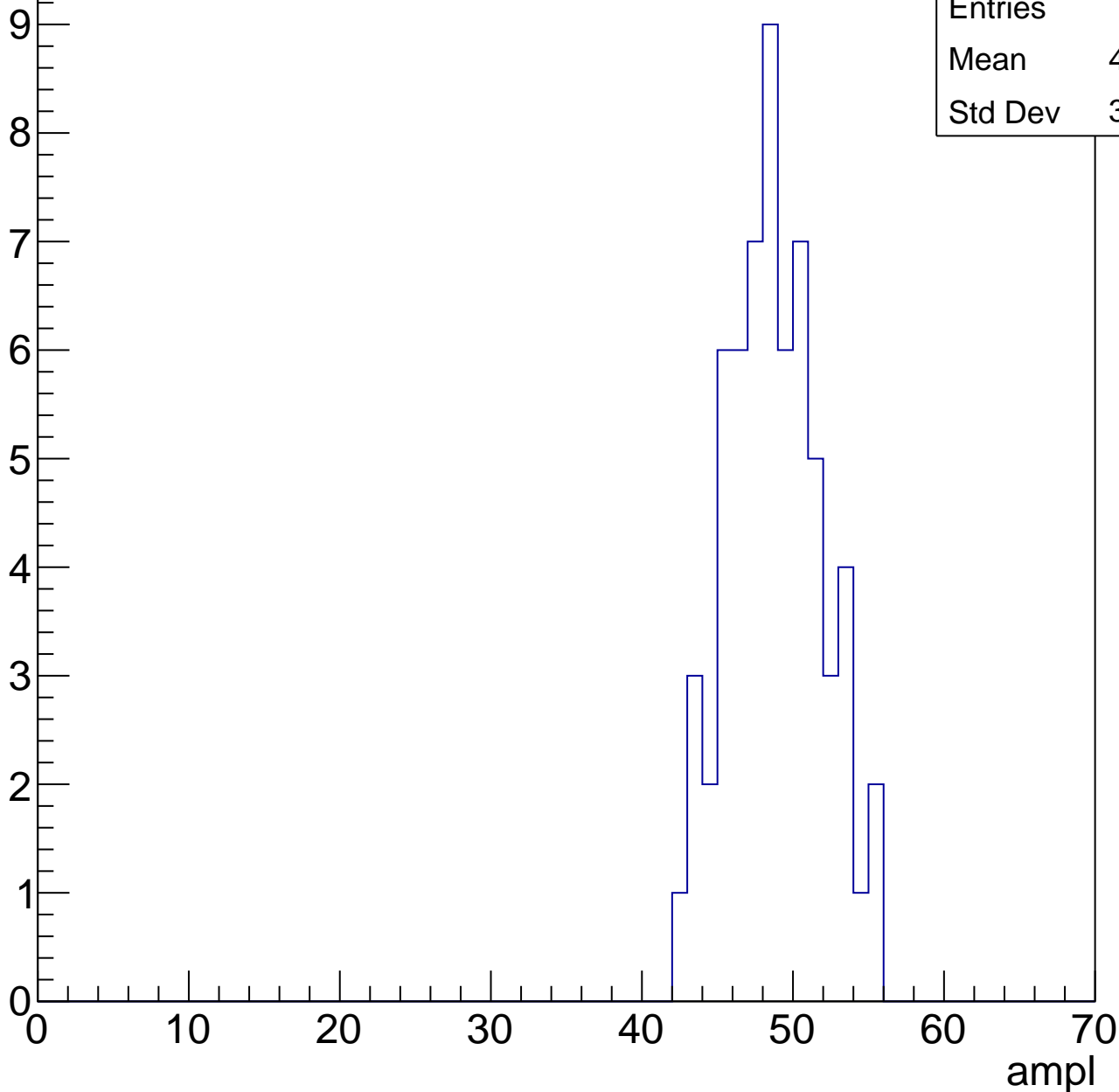


# B0L000S, U19-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	48.34
Std Dev	3.064

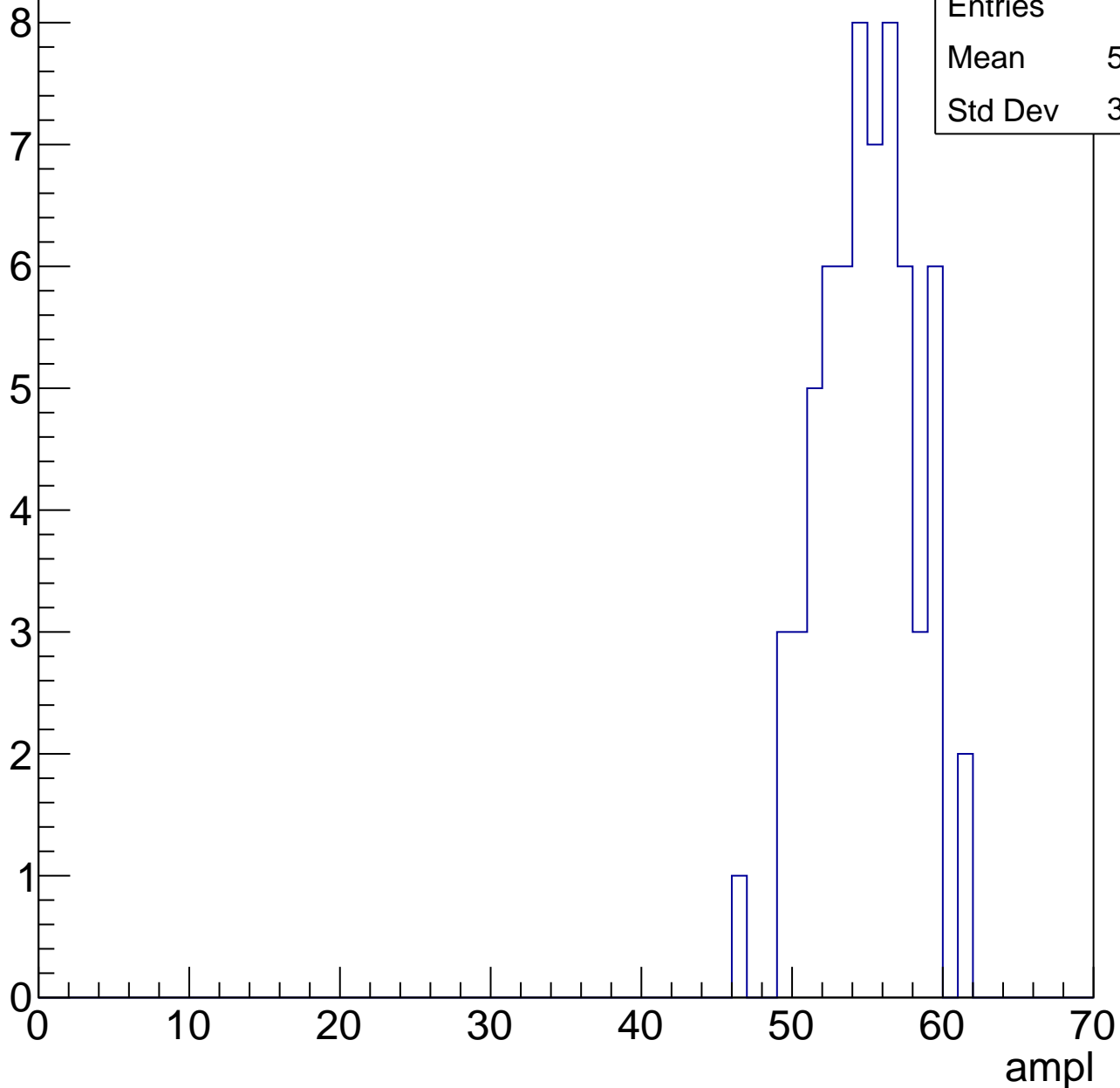


# B0L000S, U19-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.45
Std Dev	3.157

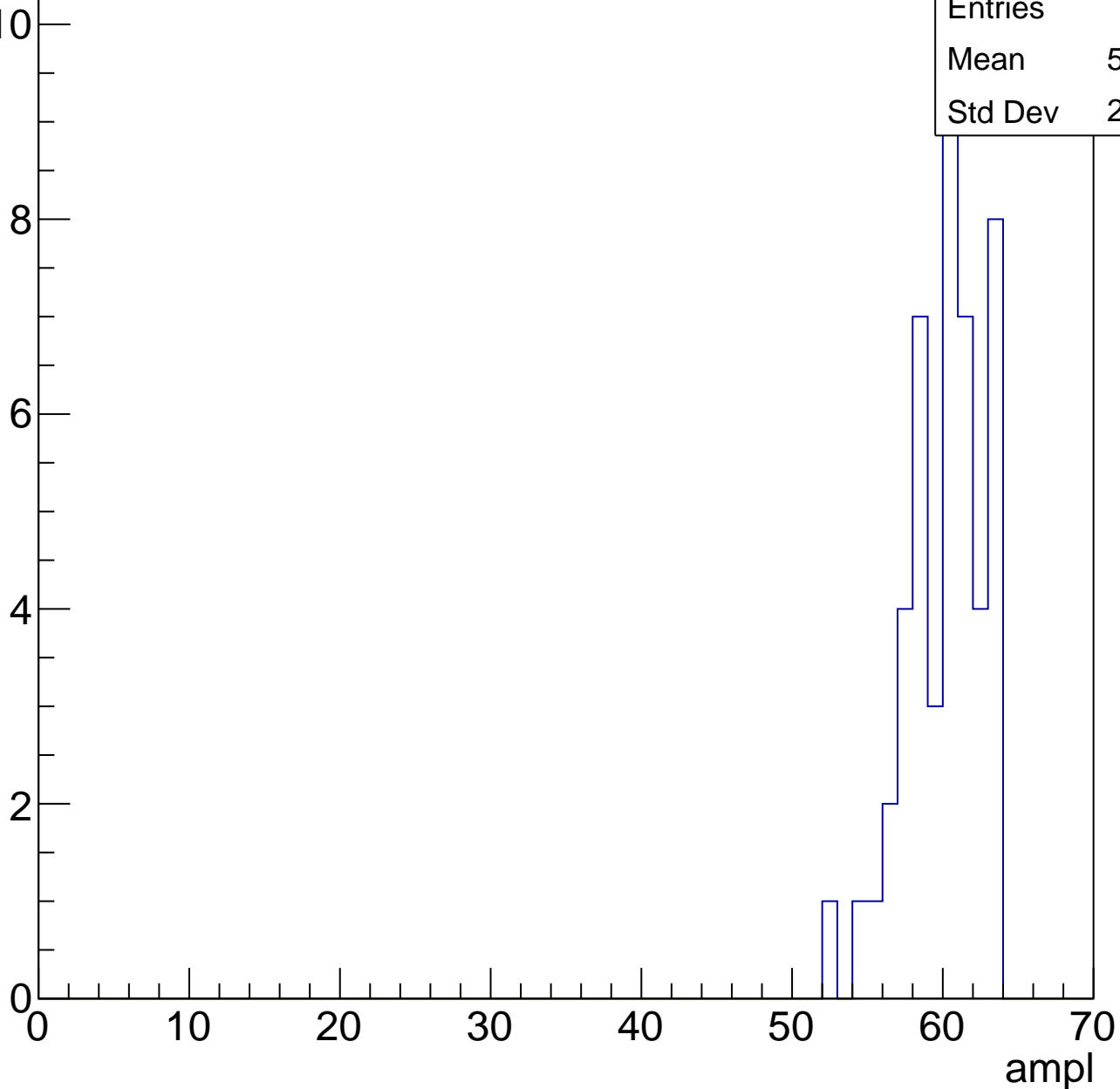


# B0L000S, U19-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	59.65
Std Dev	2.554

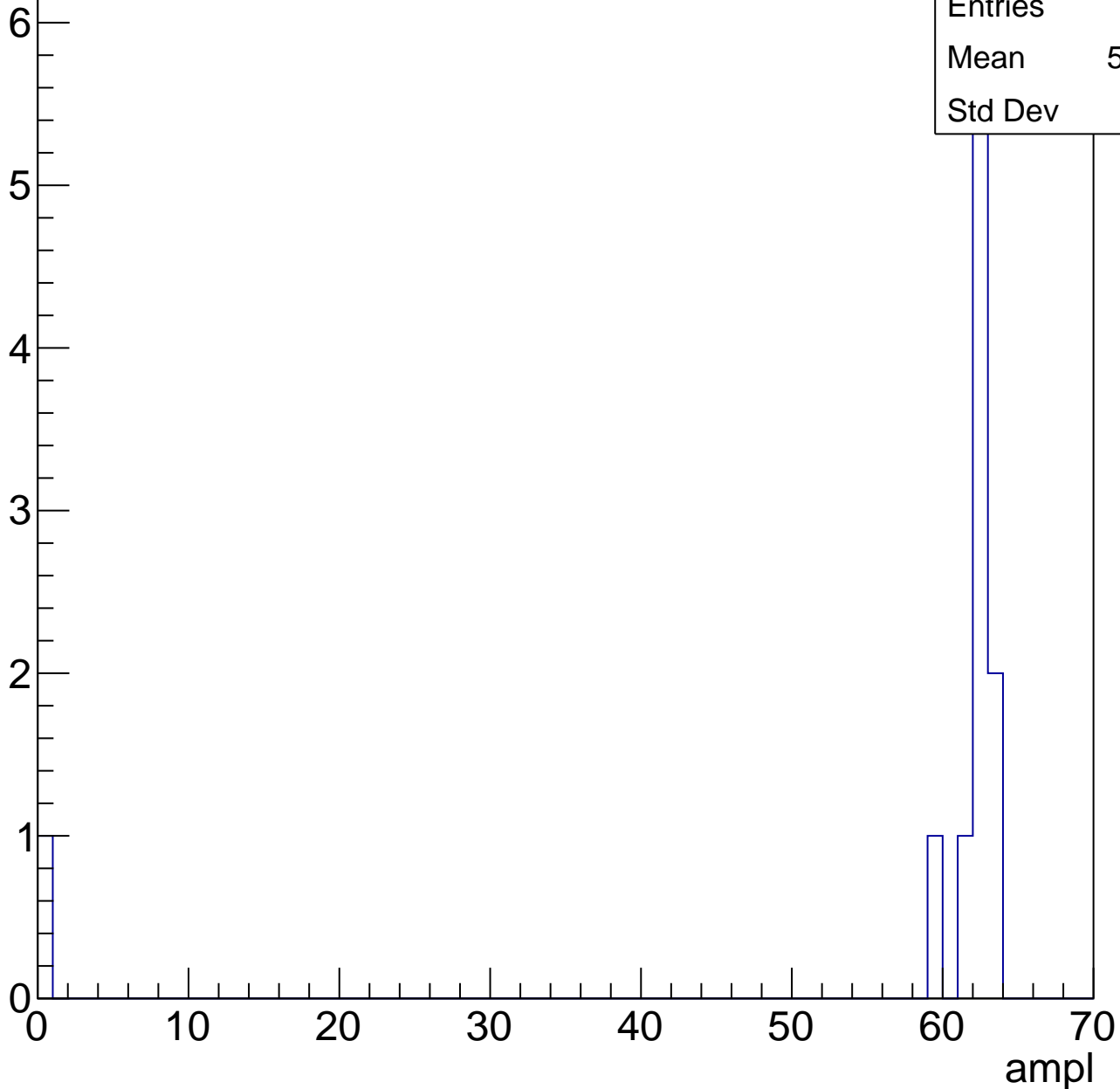


# B0L000S, U19-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	56.18
Std Dev	17.8





# B0L000S, U19-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch8, adc0

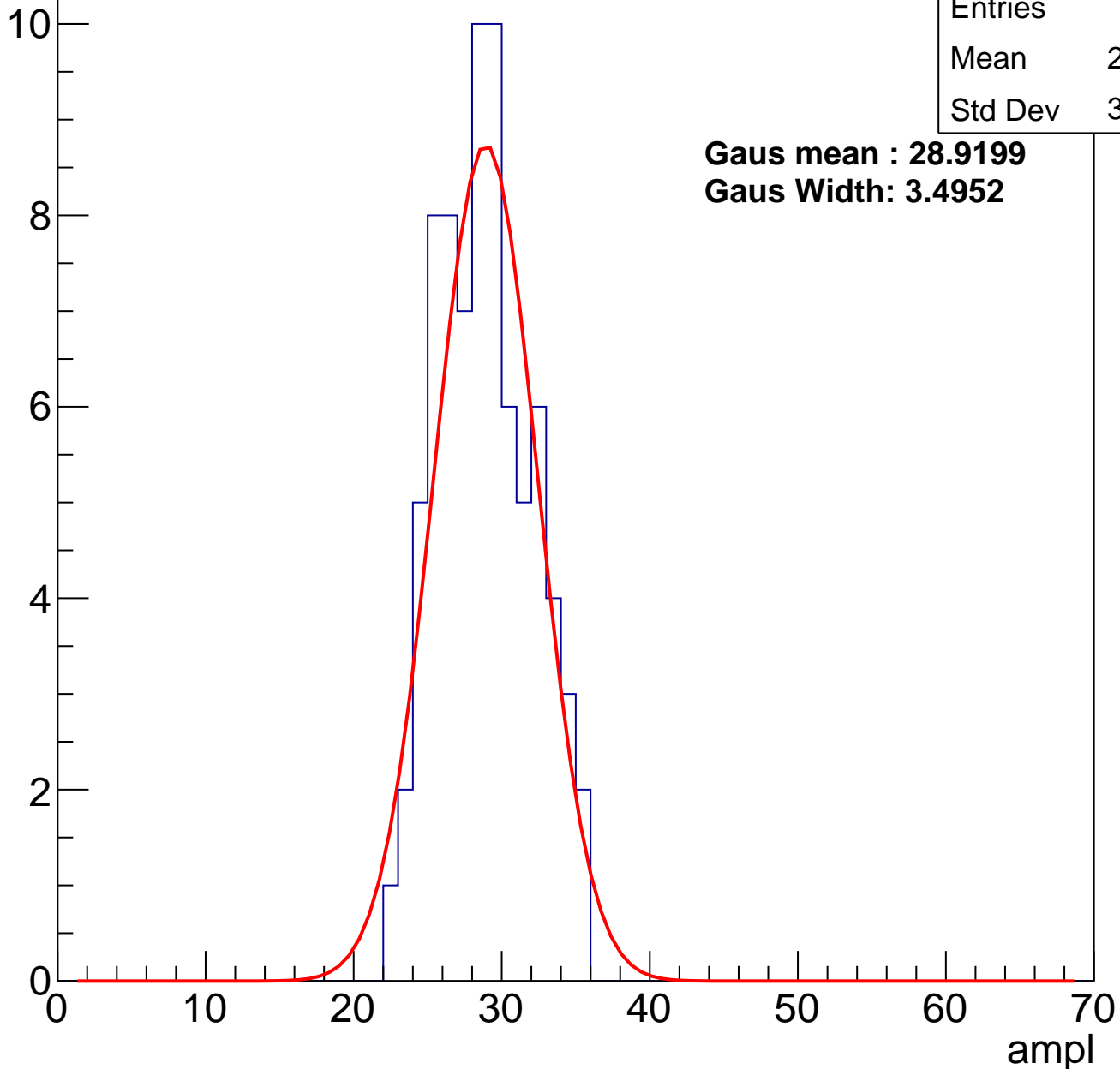
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	28.39
Std Dev	3.113

**Gaus mean : 28.9199**

**Gaus Width: 3.4952**

Entry



# B0L000S, U19-ch8, adc1

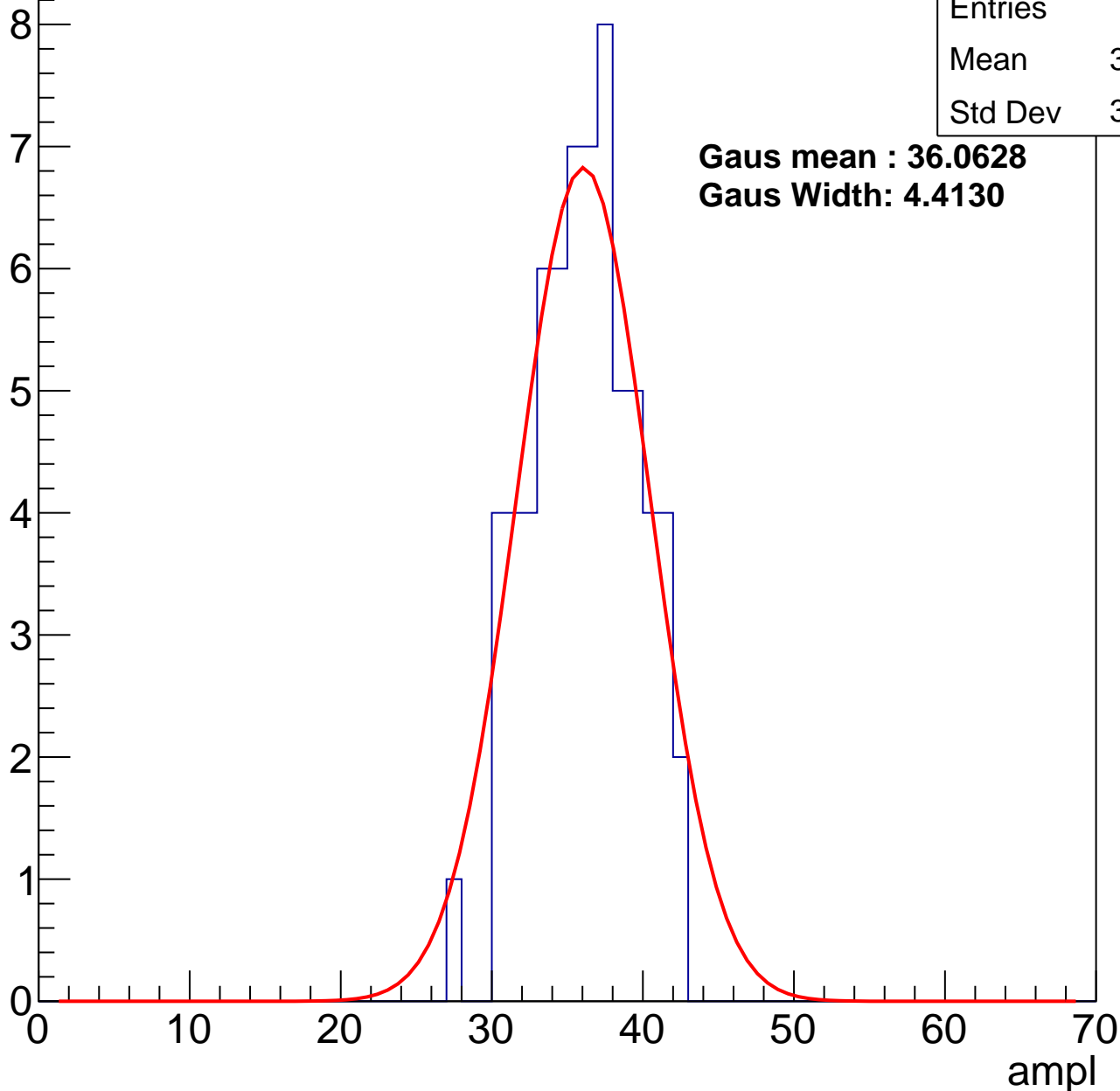
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.63
Std Dev	3.398

**Gaus mean : 36.0628**

**Gaus Width: 4.4130**



# B0L000S, U19-ch8, adc2

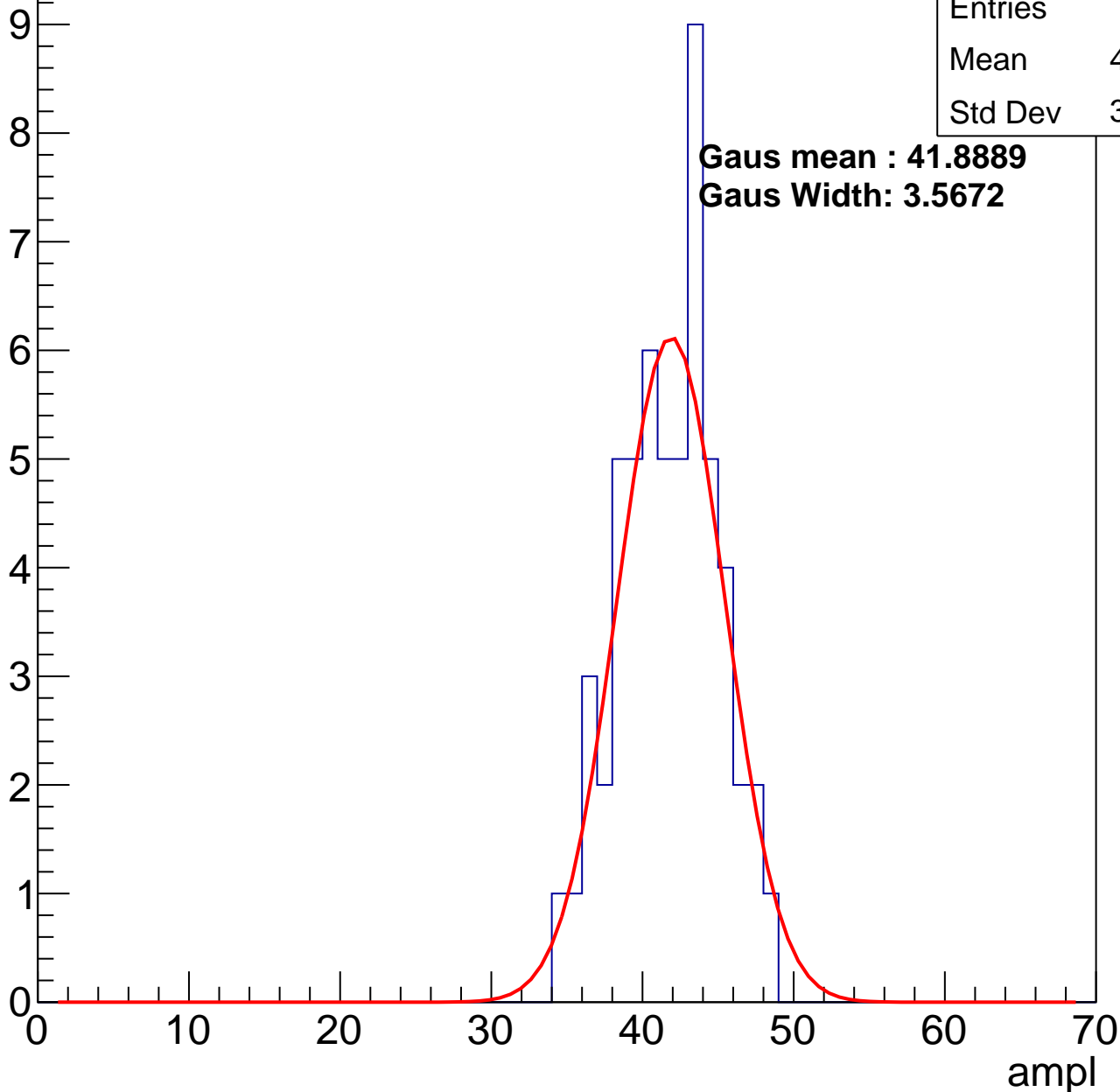
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	41.29
Std Dev	3.217

**Gaus mean : 41.8889**

**Gaus Width: 3.5672**

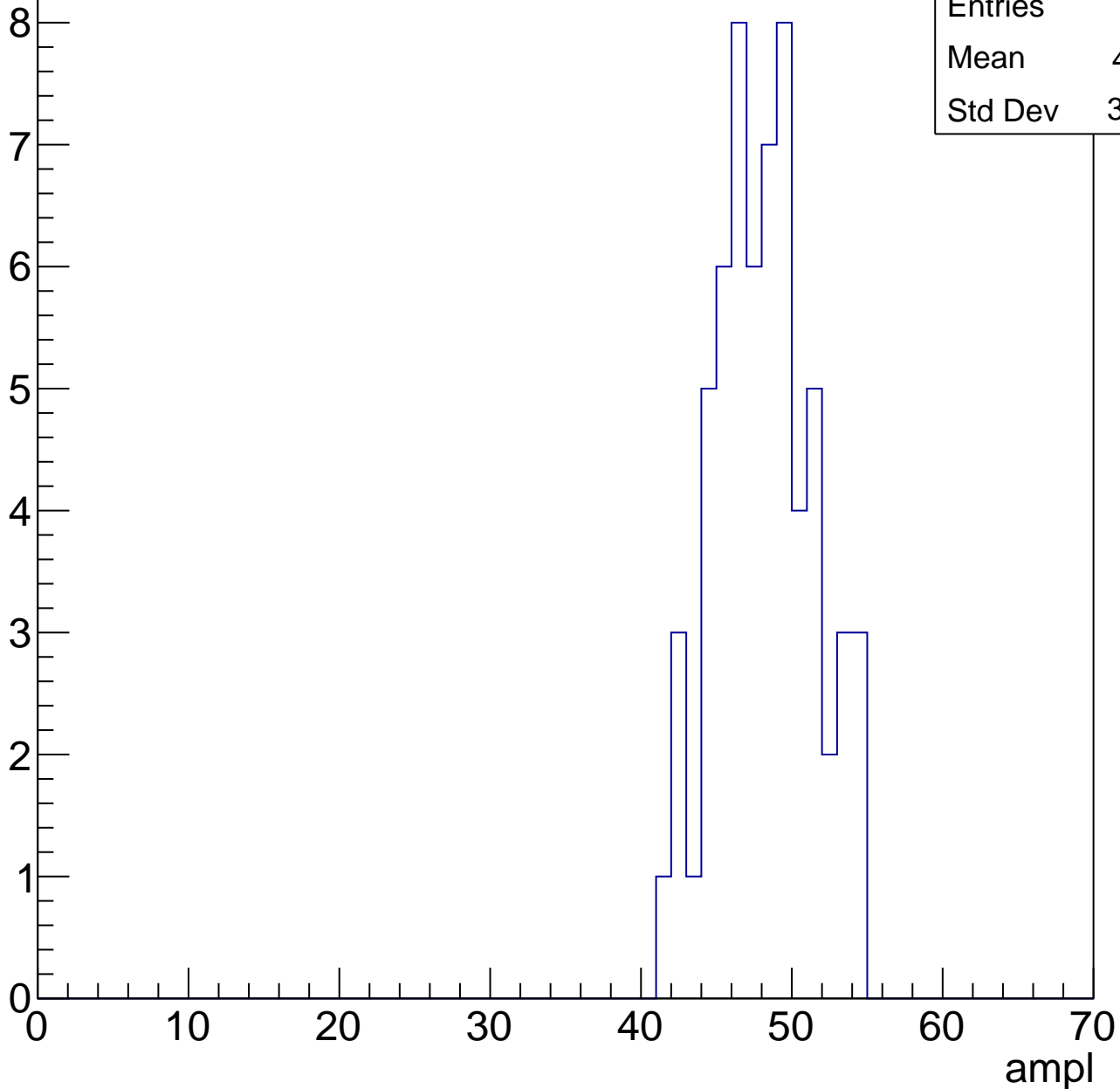


# B0L000S, U19-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

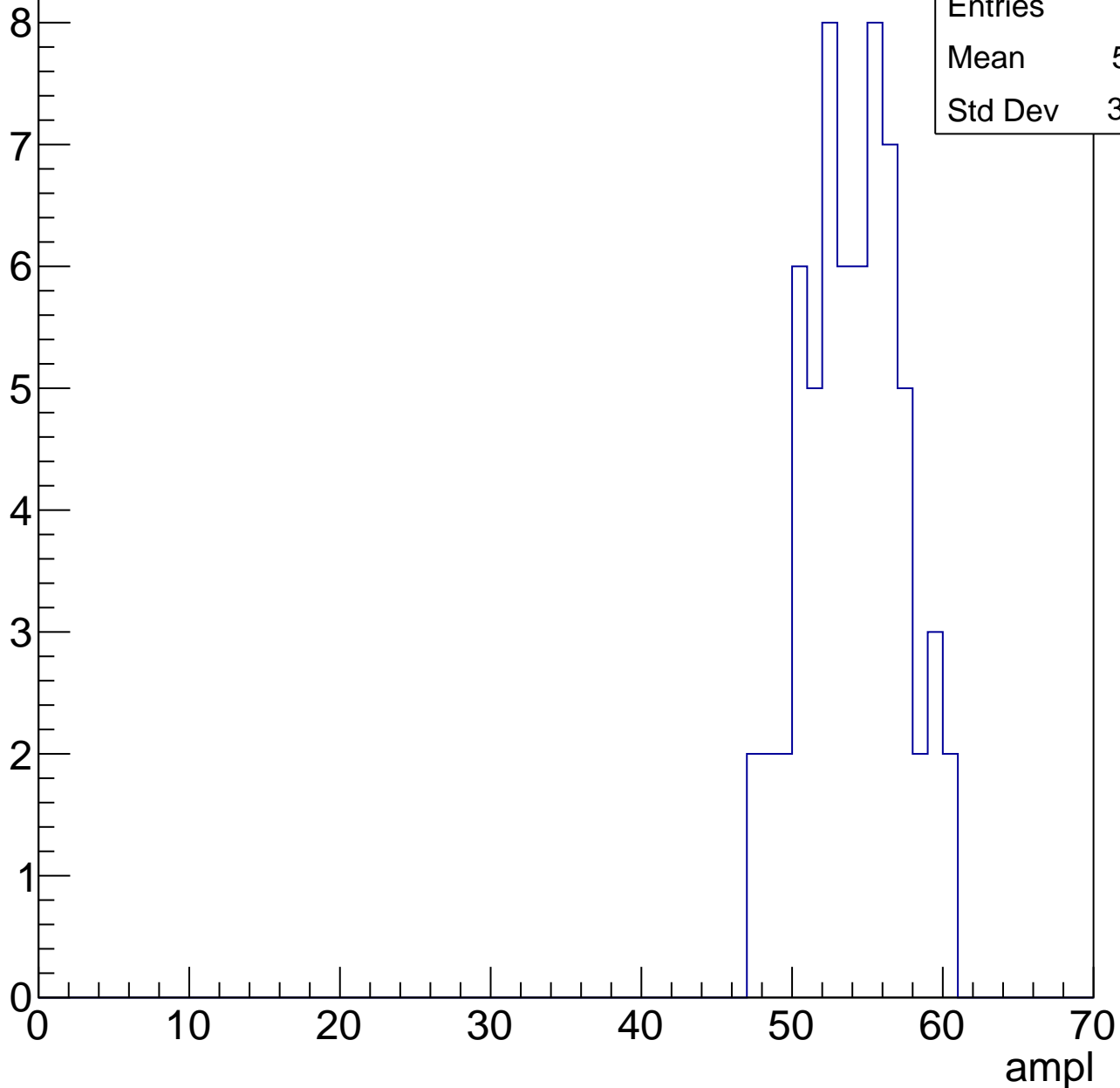
Entries	62
Mean	47.71
Std Dev	3.195



# B0L000S, U19-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

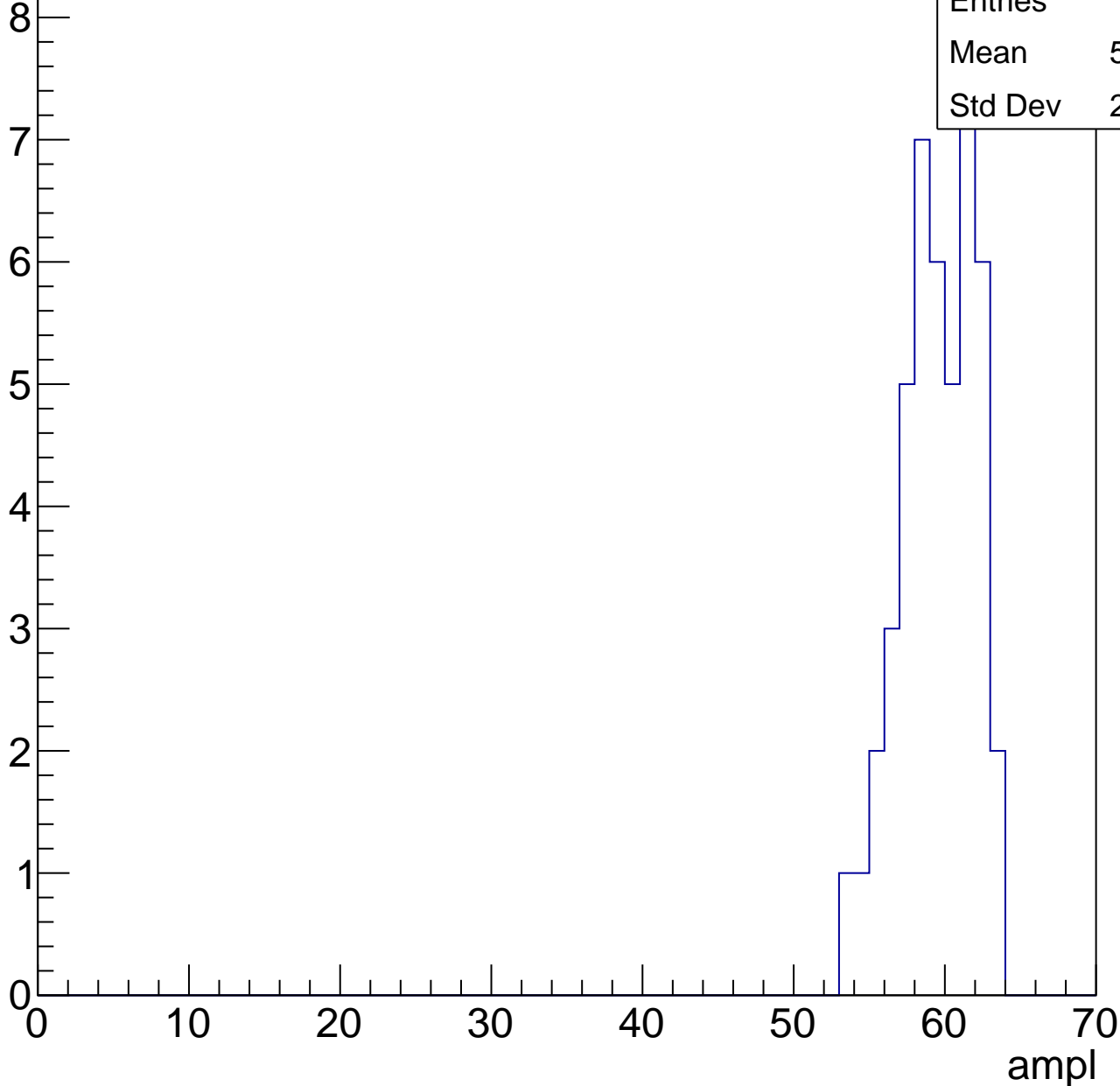


# B0L000S, U19-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	59.04
Std Dev	2.422

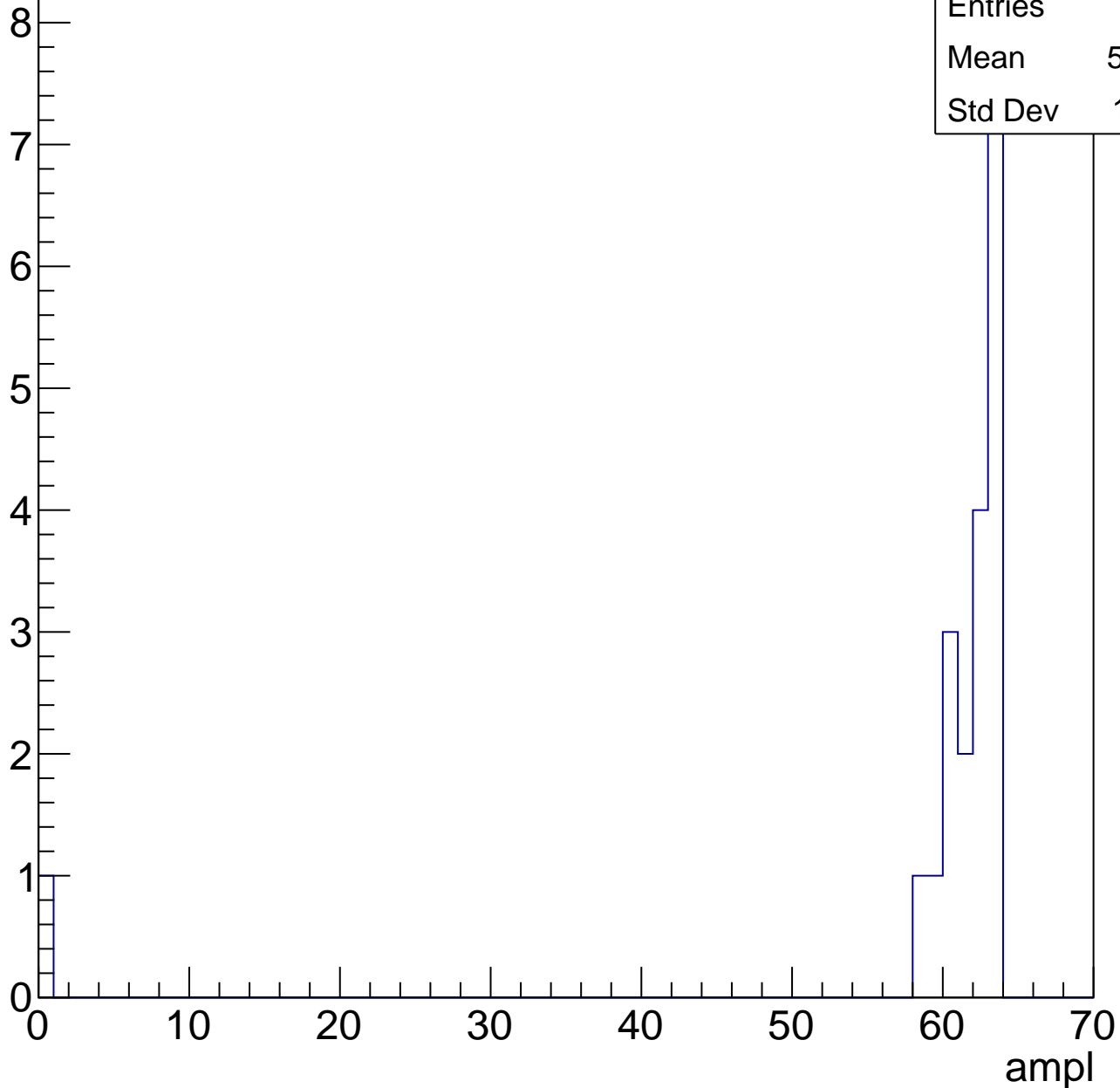


# B0L000S, U19-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	58.55
Std Dev	13.51





# B0L000S, U19-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch9, adc0

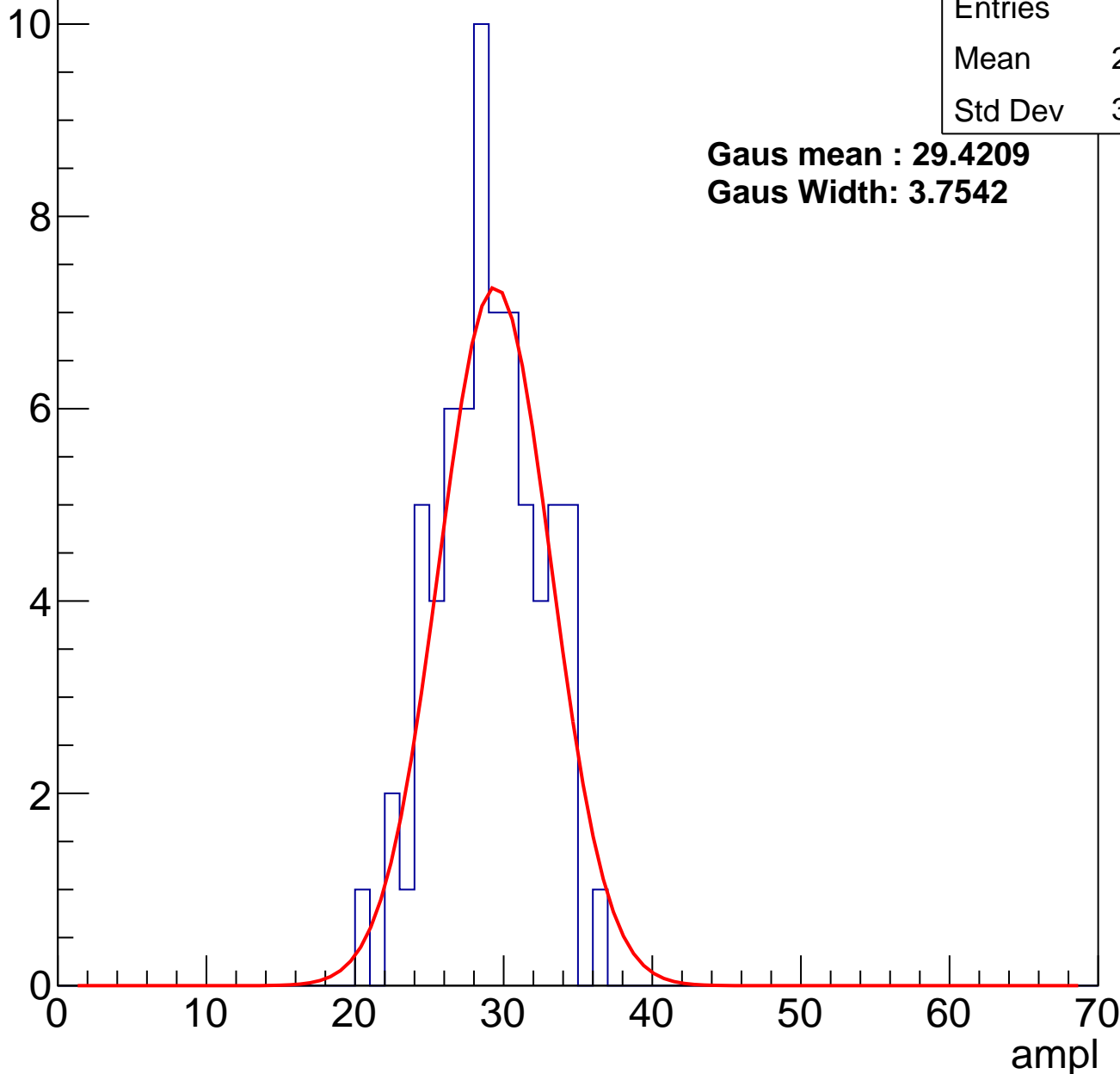
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	28.58
Std Dev	3.398

**Gaus mean : 29.4209**

**Gaus Width: 3.7542**

Entry



# B0L000S, U19-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	36.07
Std Dev	3.55

**Gaus mean : 36.6330**

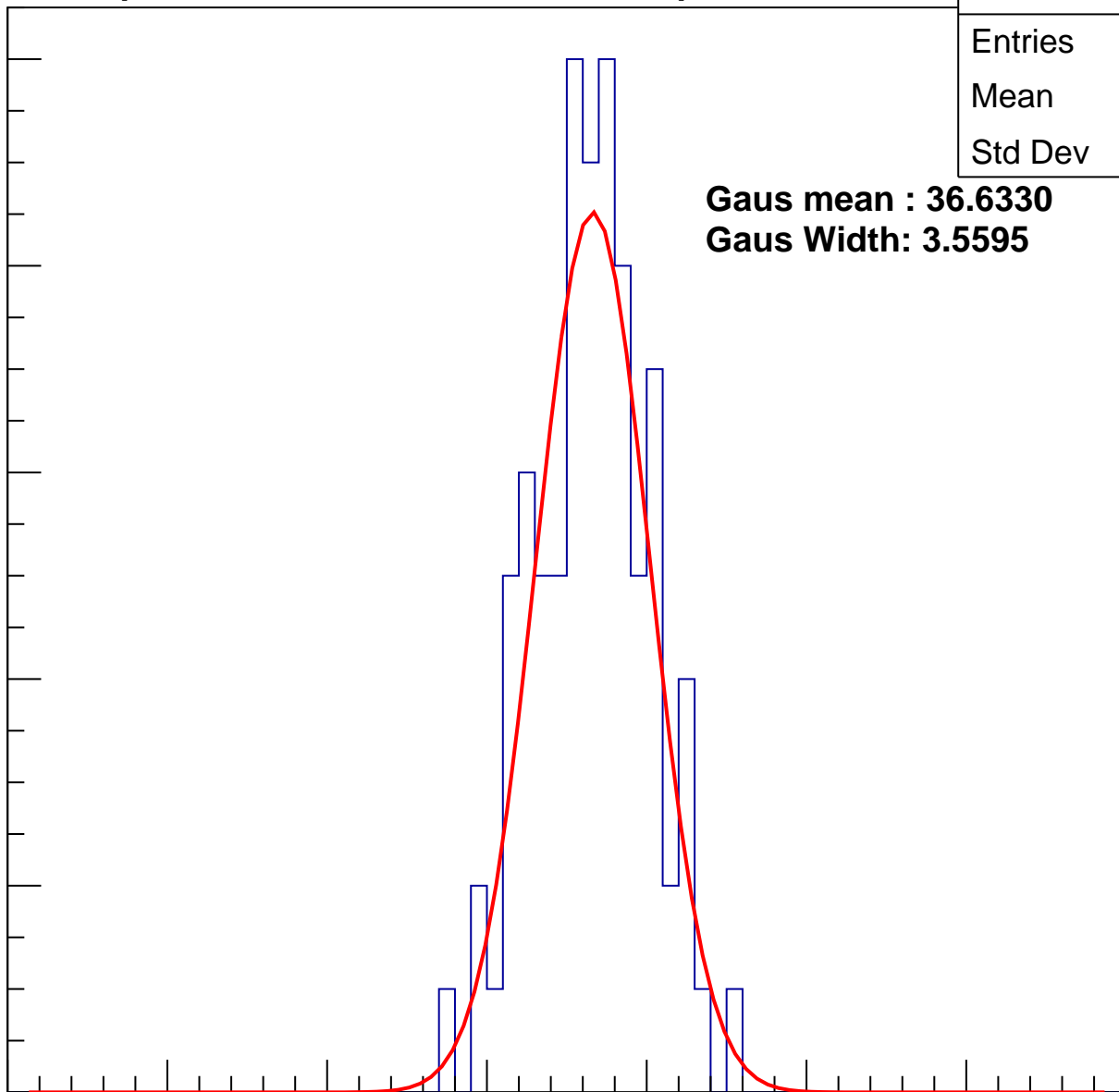
**Gaus Width: 3.5595**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch9, adc2

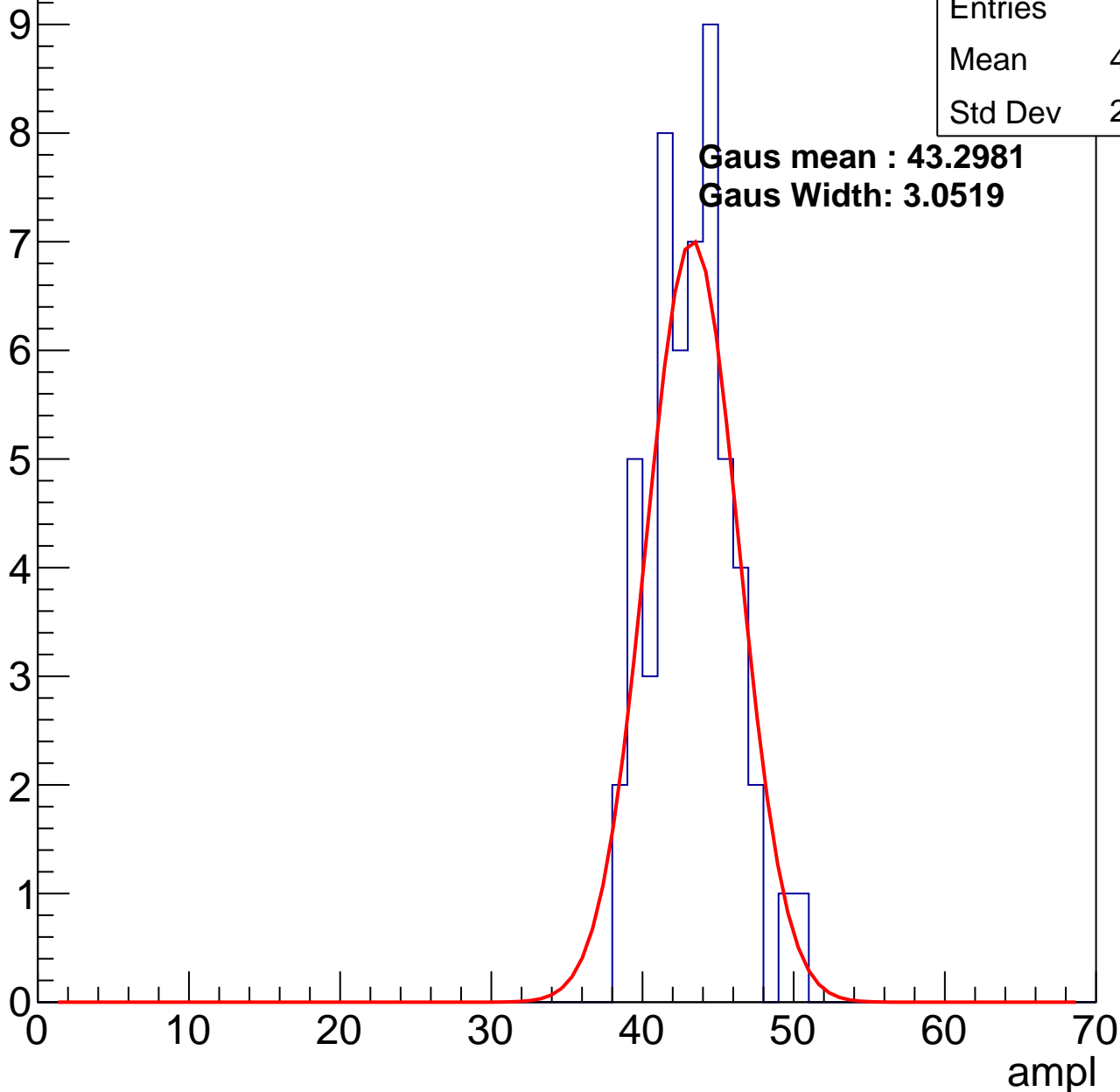
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	42.83
Std Dev	2.662

**Gaus mean : 43.2981**

**Gaus Width: 3.0519**



# B0L000S, U19-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	49.07
Std Dev	3.006

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

2

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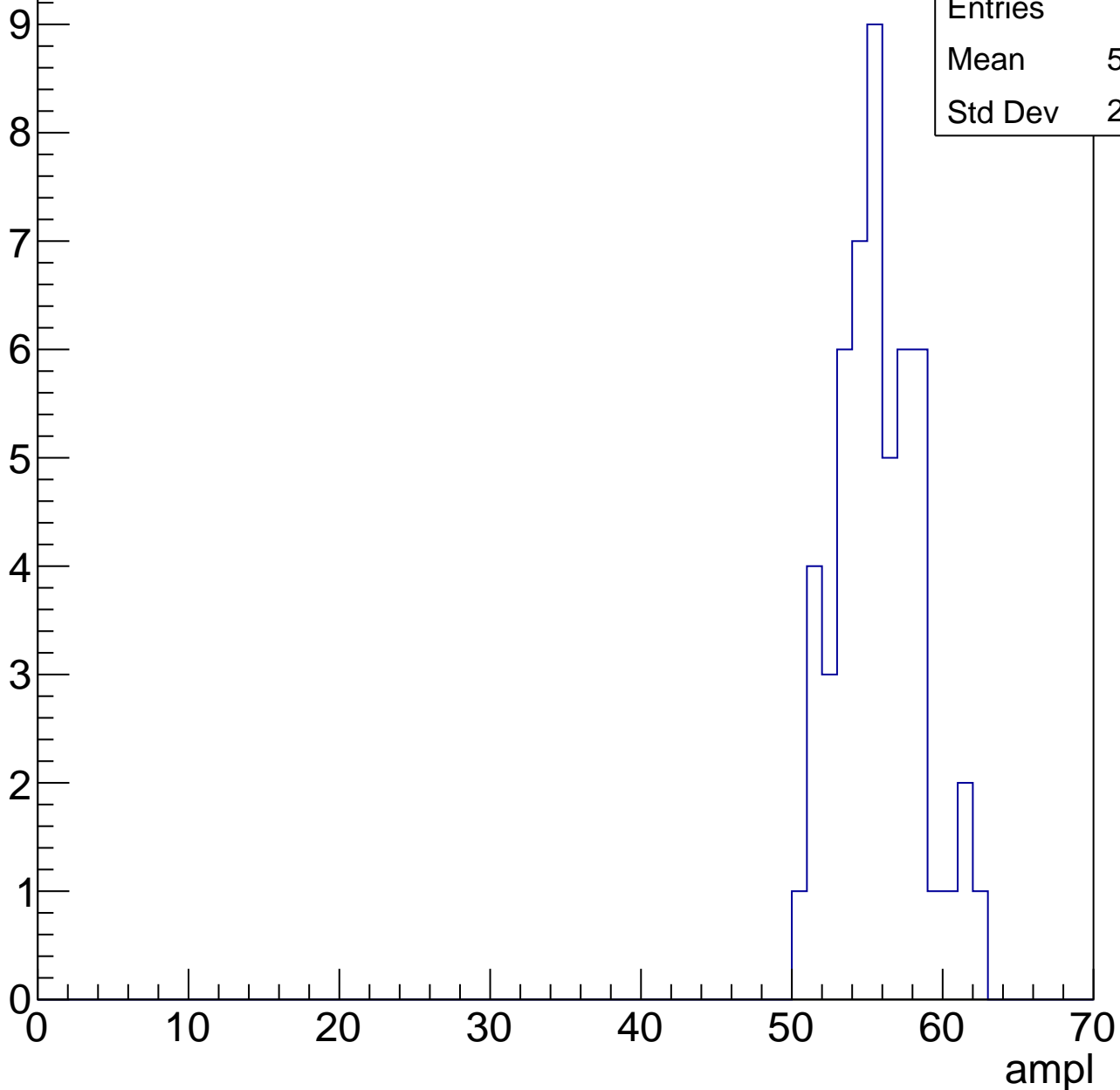
1

# B0L000S, U19-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	55.27
Std Dev	2.732



# B0L000S, U19-ch9, adc5

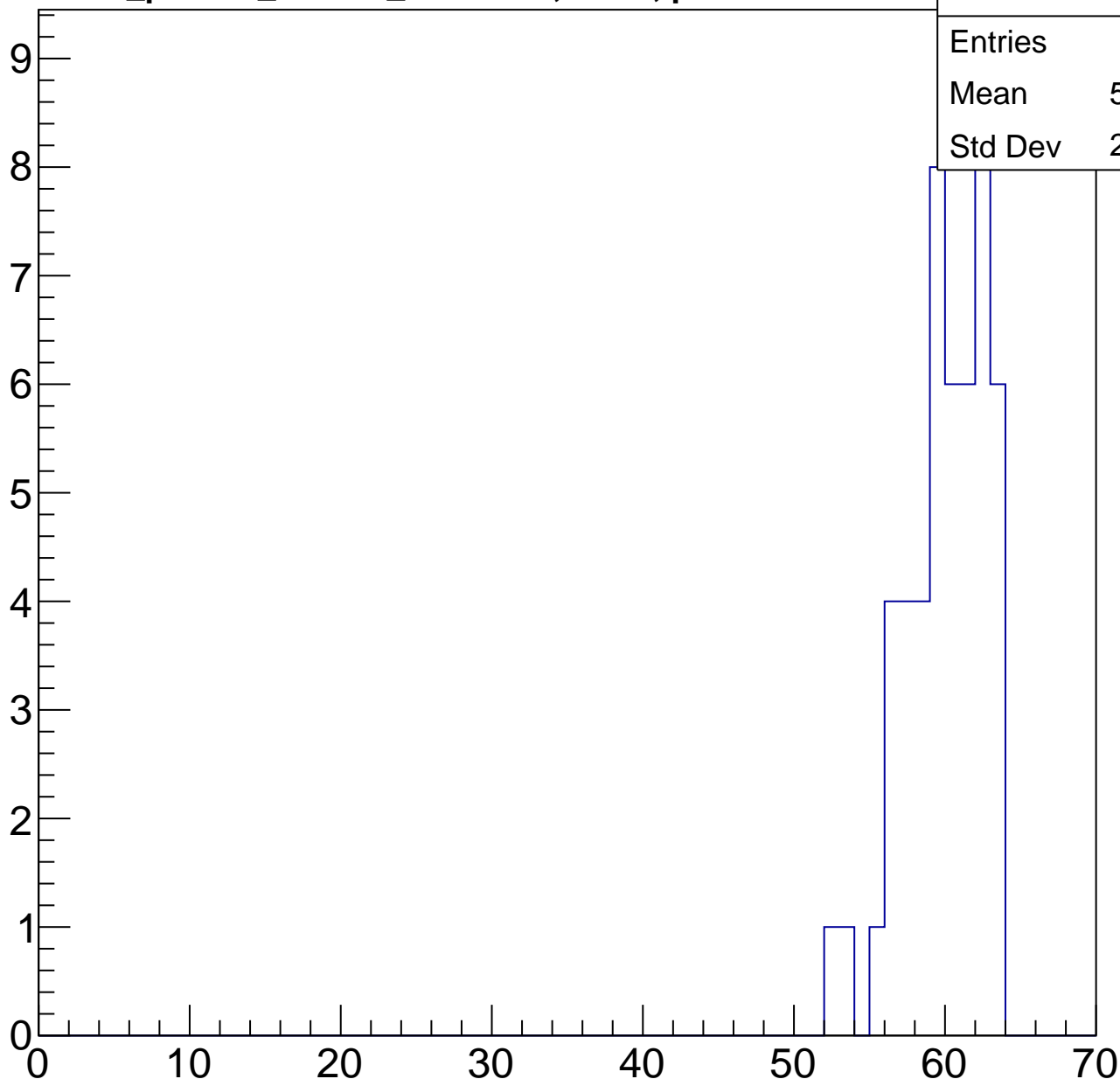
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	59.56
Std Dev	2.639

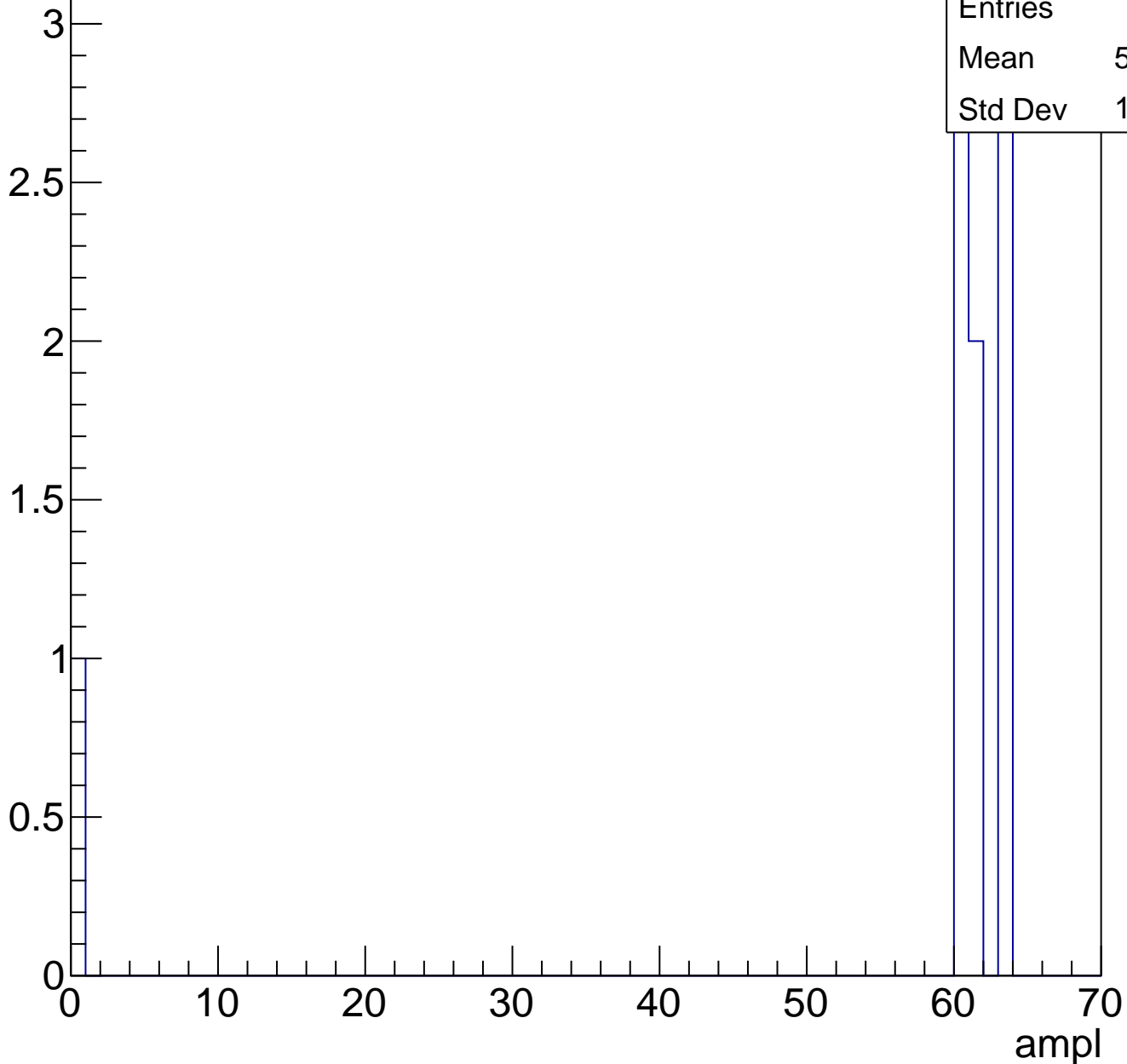
ampl



# B0L000S, U19-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch10, adc0

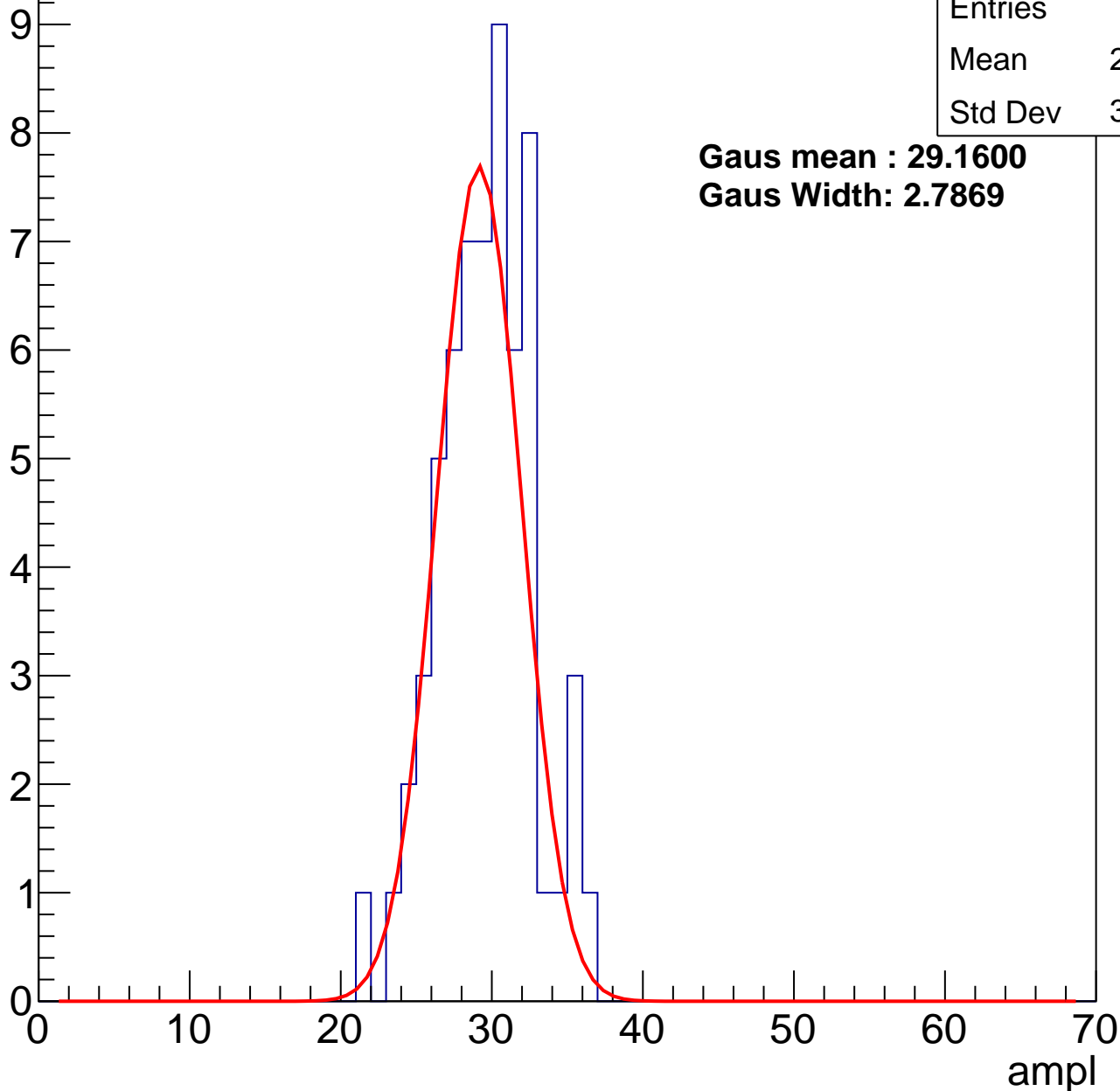
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.15
Std Dev	3.072

**Gaus mean : 29.1600**

**Gaus Width: 2.7869**

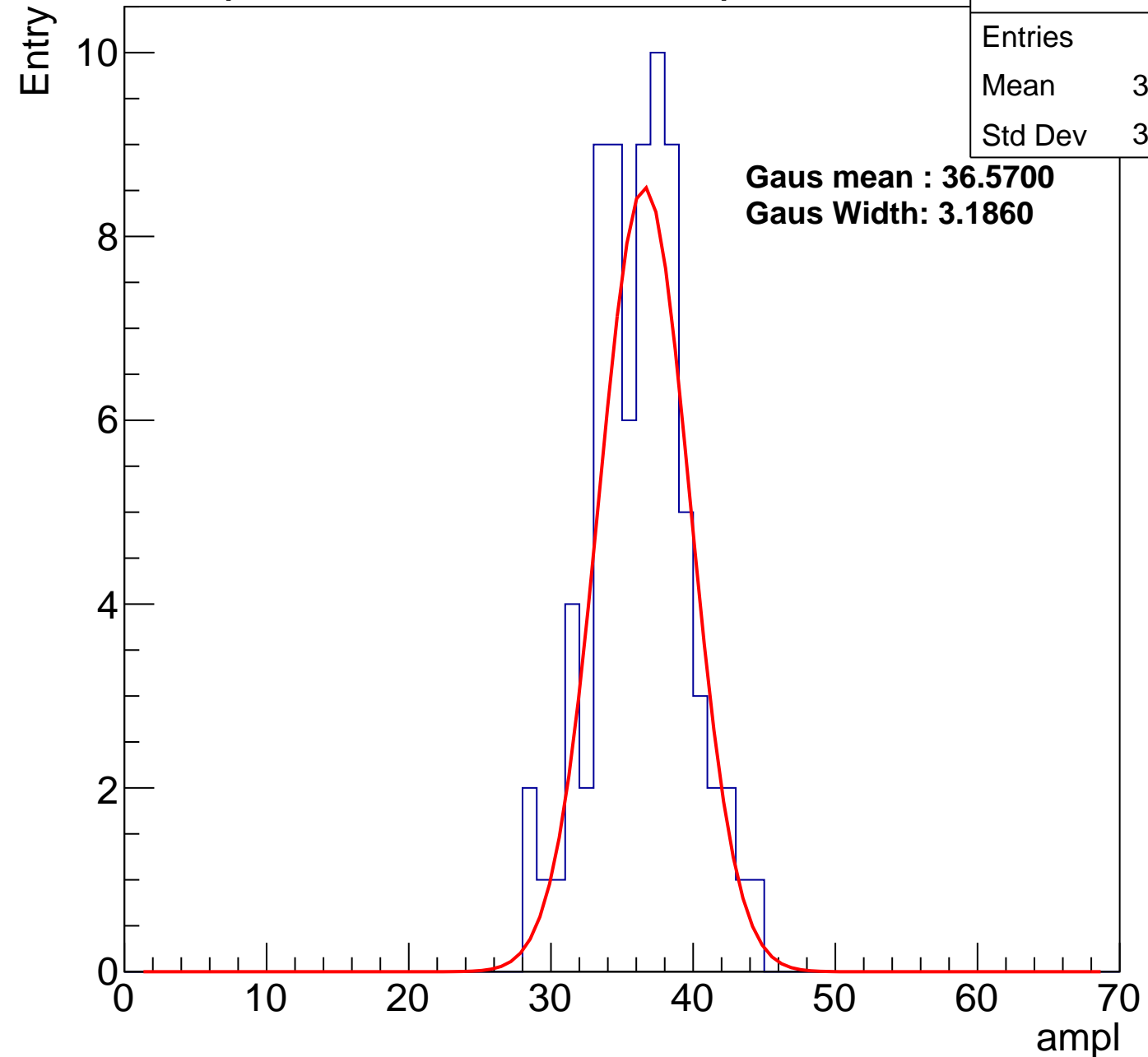


# B0L000S, U19-ch10, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	35.79
Std Dev	3.322

**Gaus mean : 36.5700**  
**Gaus Width: 3.1860**



# B0L000S, U19-ch10, adc2

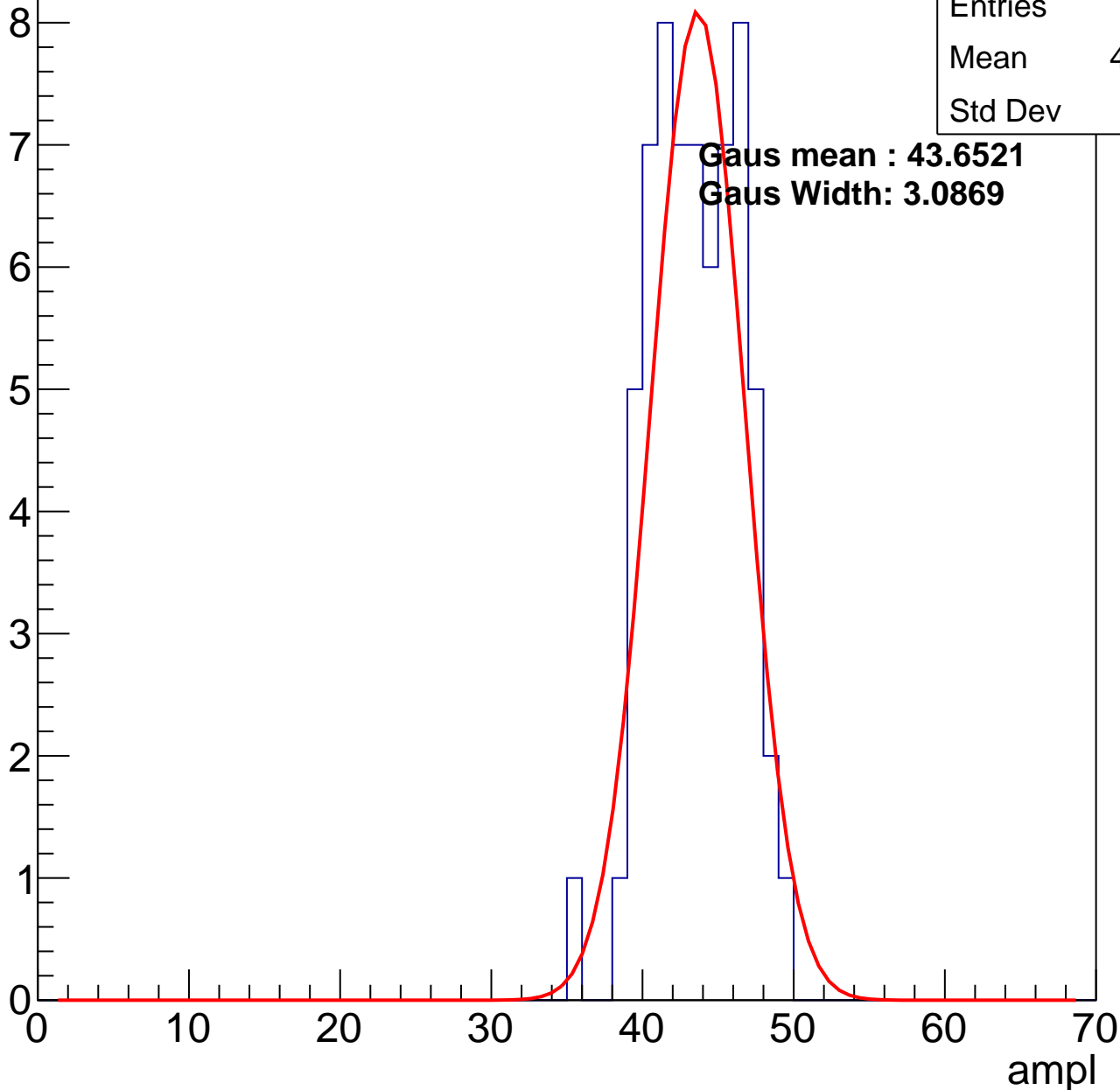
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.05
Std Dev	2.89

**Gaus mean : 43.6521**

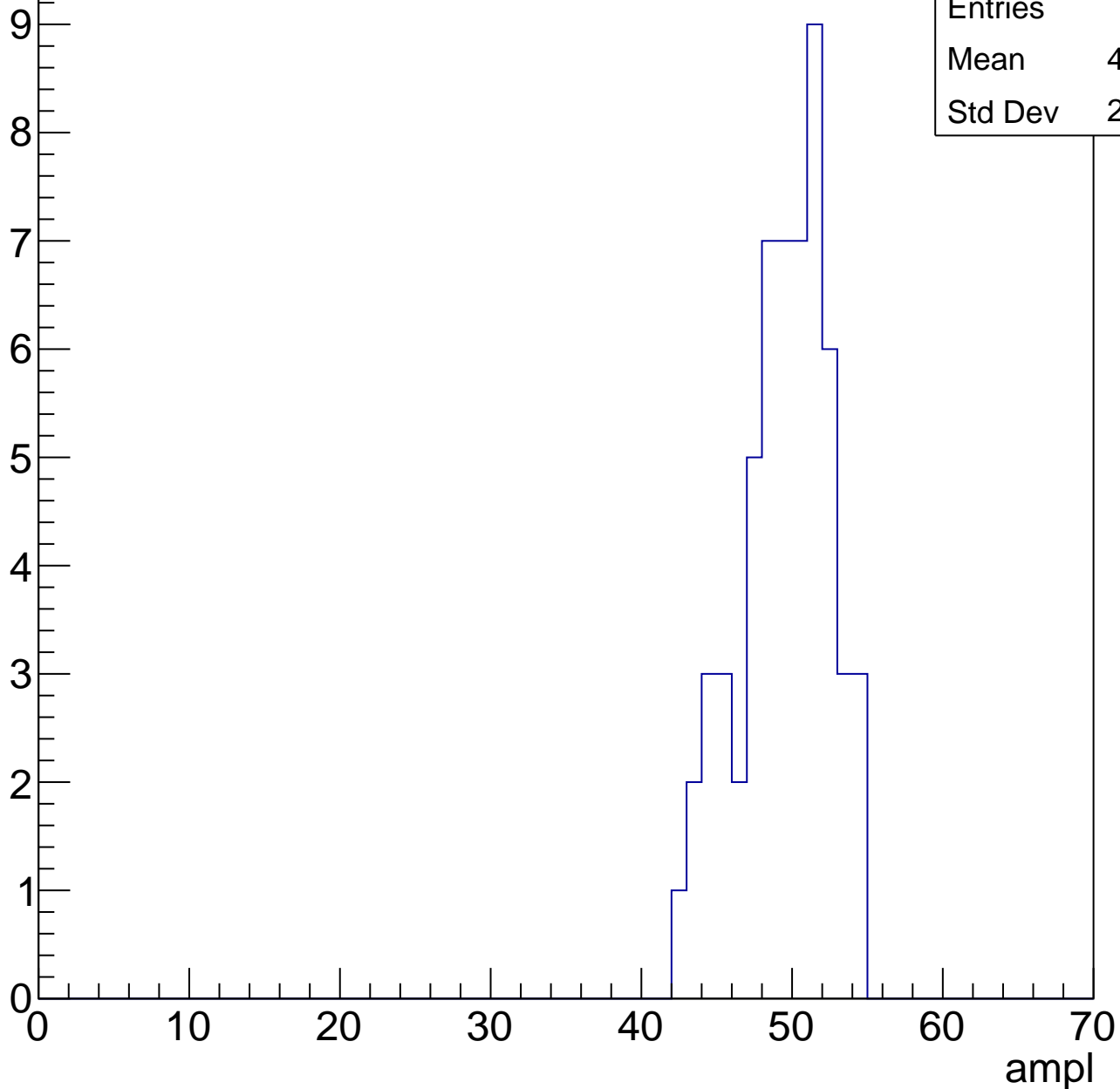
**Gaus Width: 3.0869**



# B0L000S, U19-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

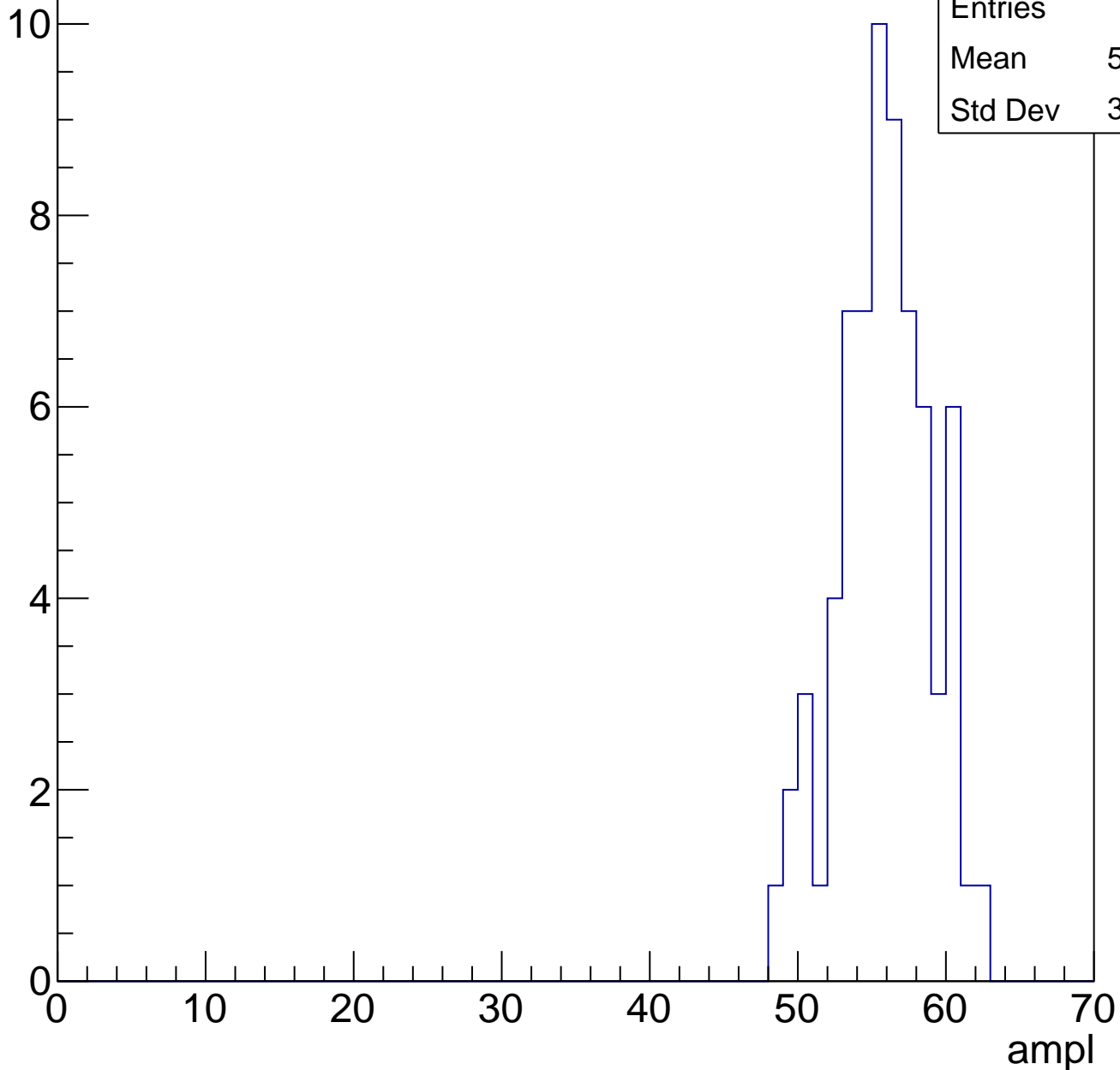


# B0L000S, U19-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	55.37
Std Dev	3.087

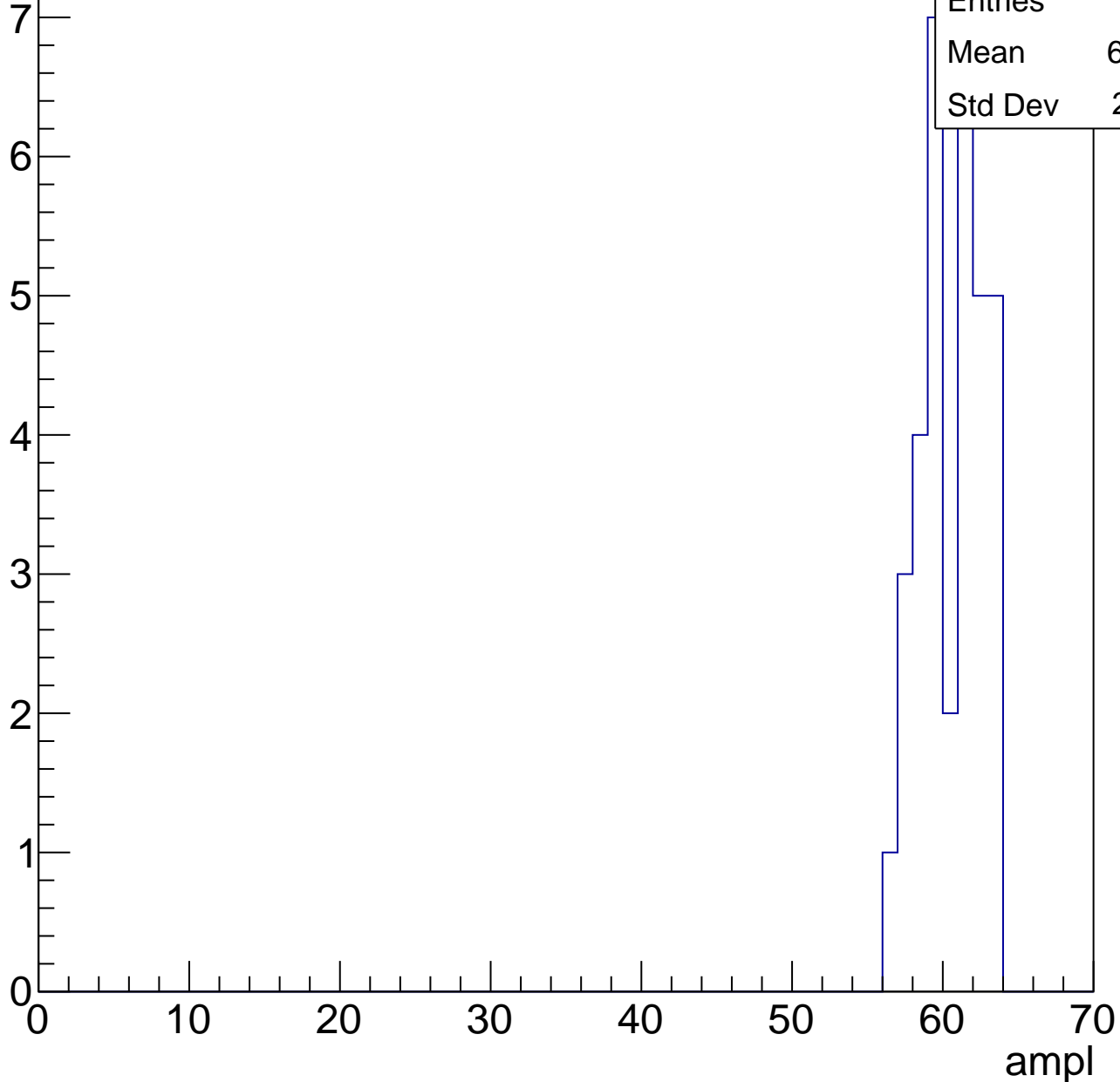
Entry



# B0L000S, U19-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

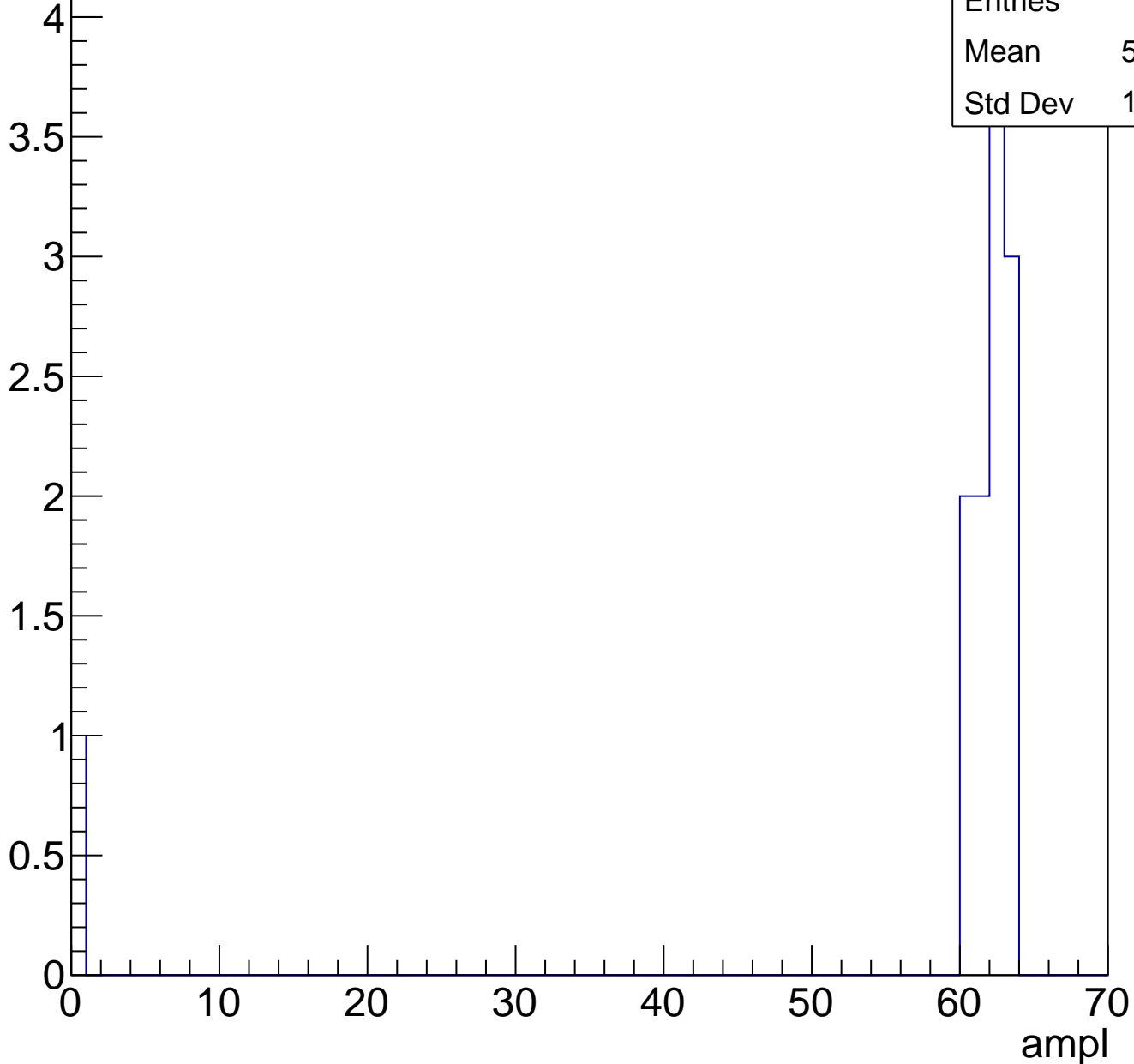
Entry



# B0L000S, U19-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch11, adc0

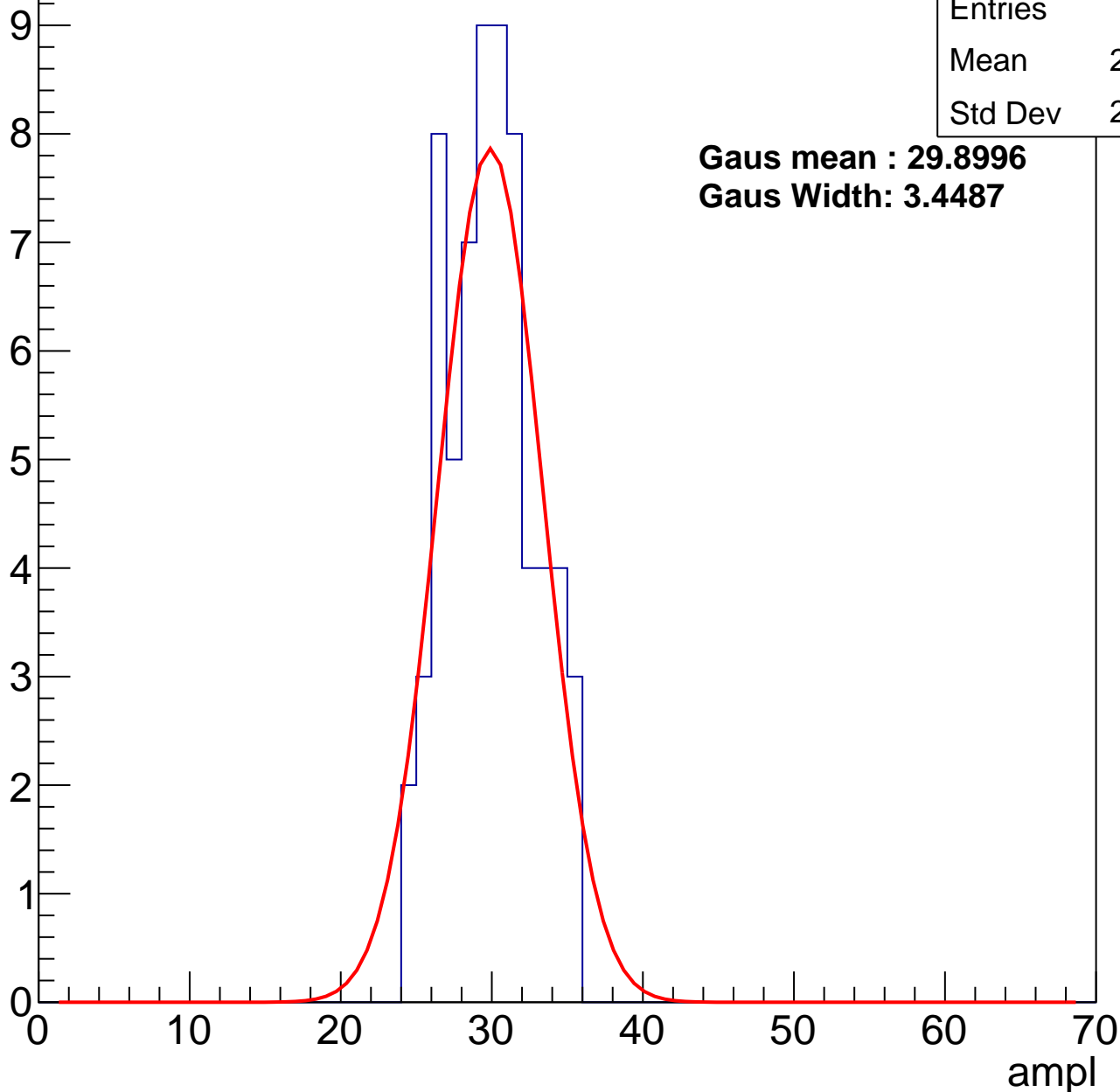
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.42
Std Dev	2.845

**Gaus mean : 29.8996**

**Gaus Width: 3.4487**



# B0L000S, U19-ch11, adc1

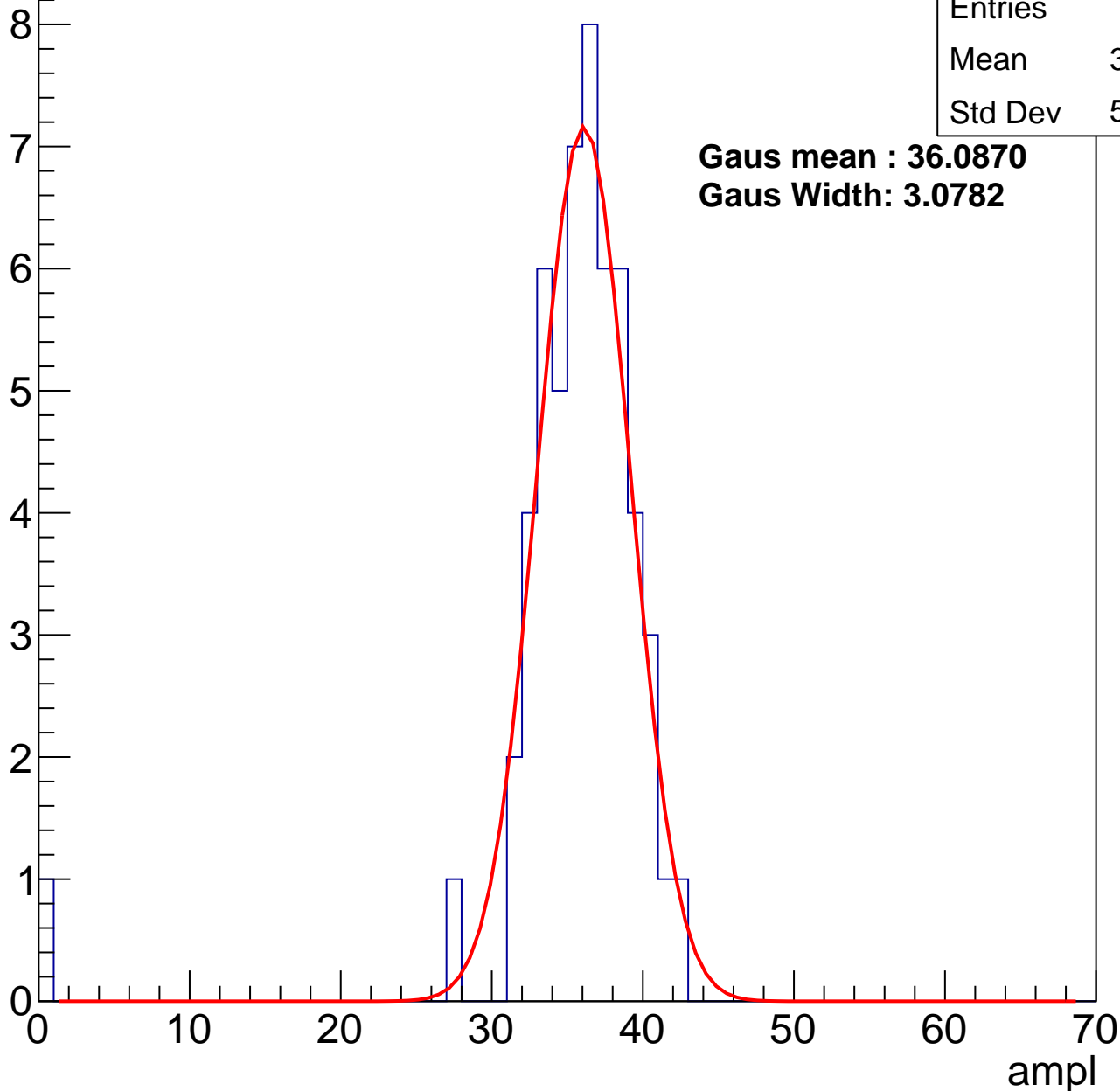
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	35.04
Std Dev	5.553

**Gaus mean : 36.0870**

**Gaus Width: 3.0782**



# B0L000S, U19-ch11, adc2

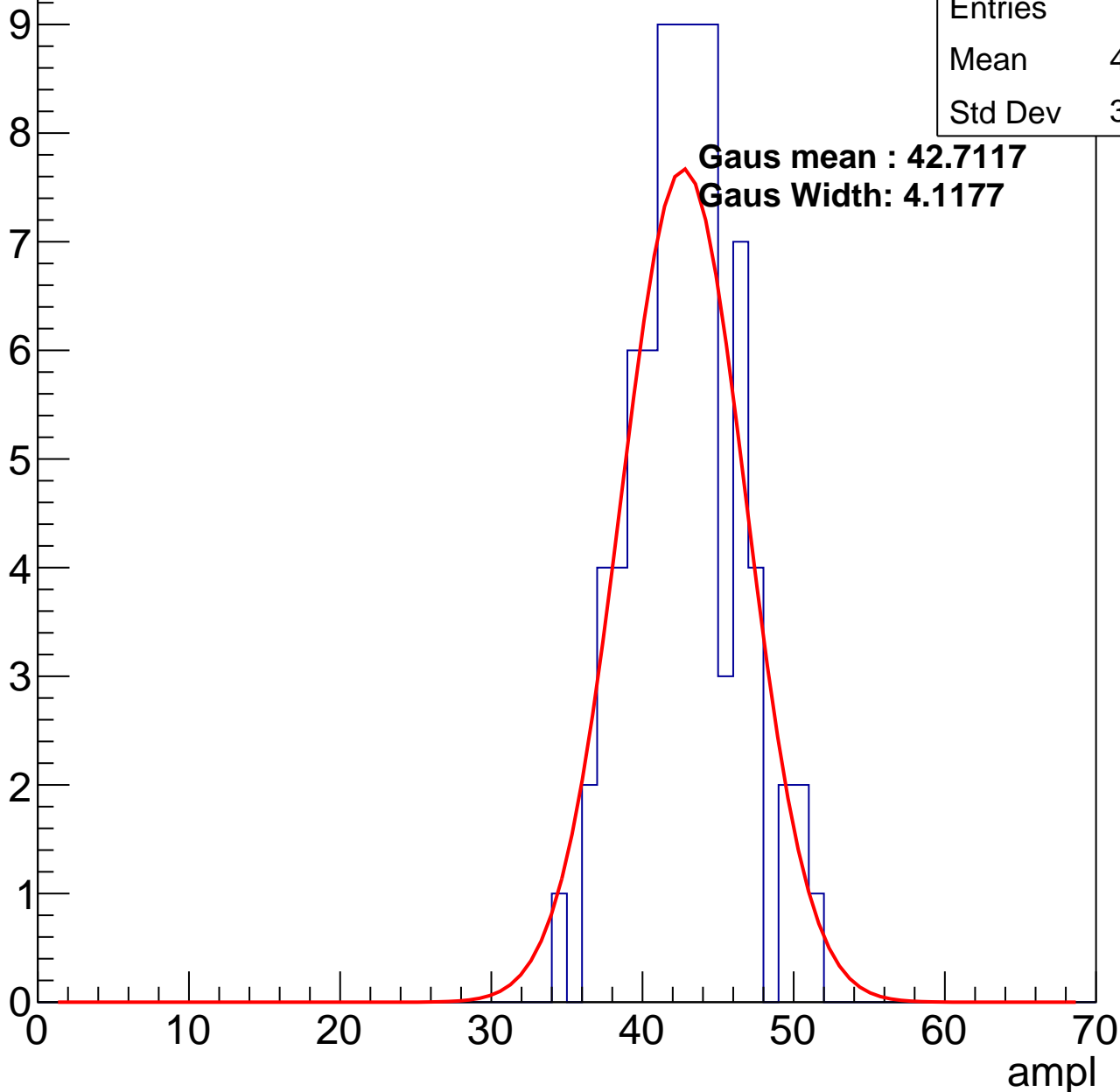
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	42.36
Std Dev	3.523

**Gaus mean : 42.7117**

**Gaus Width: 4.1177**

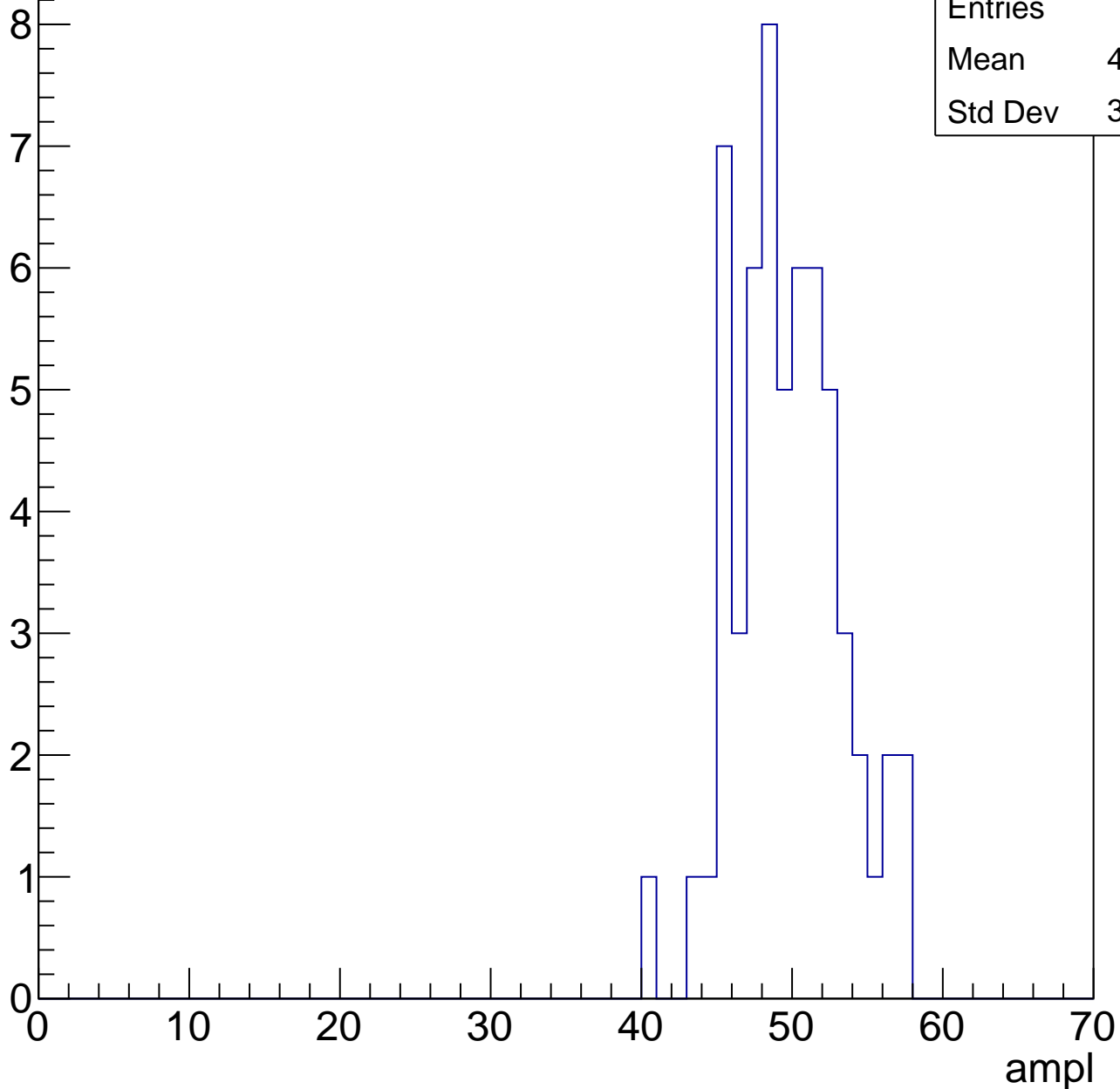


# B0L000S, U19-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.24
Std Dev	3.553

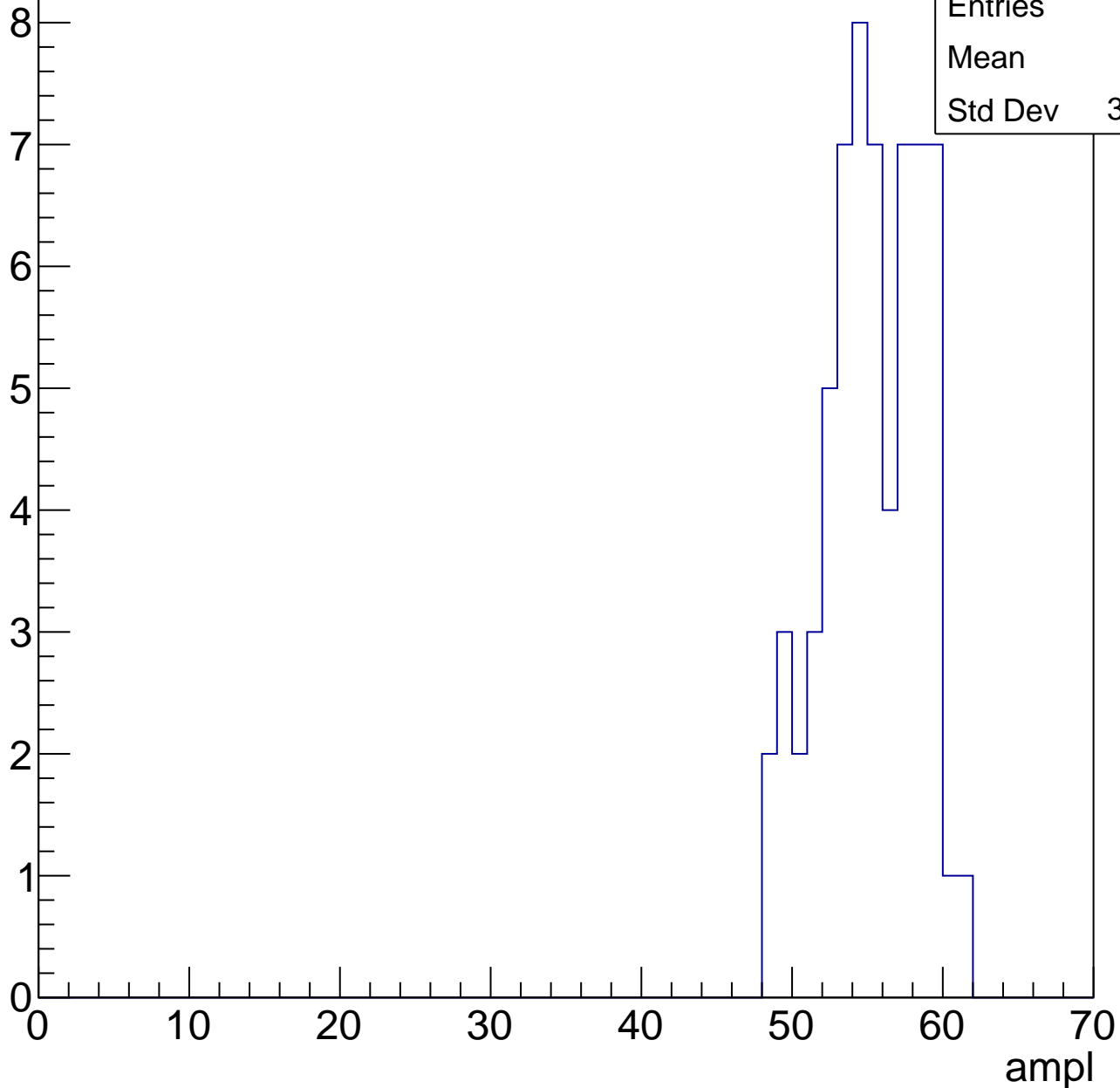


# B0L000S, U19-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.8
Std Dev	3.188

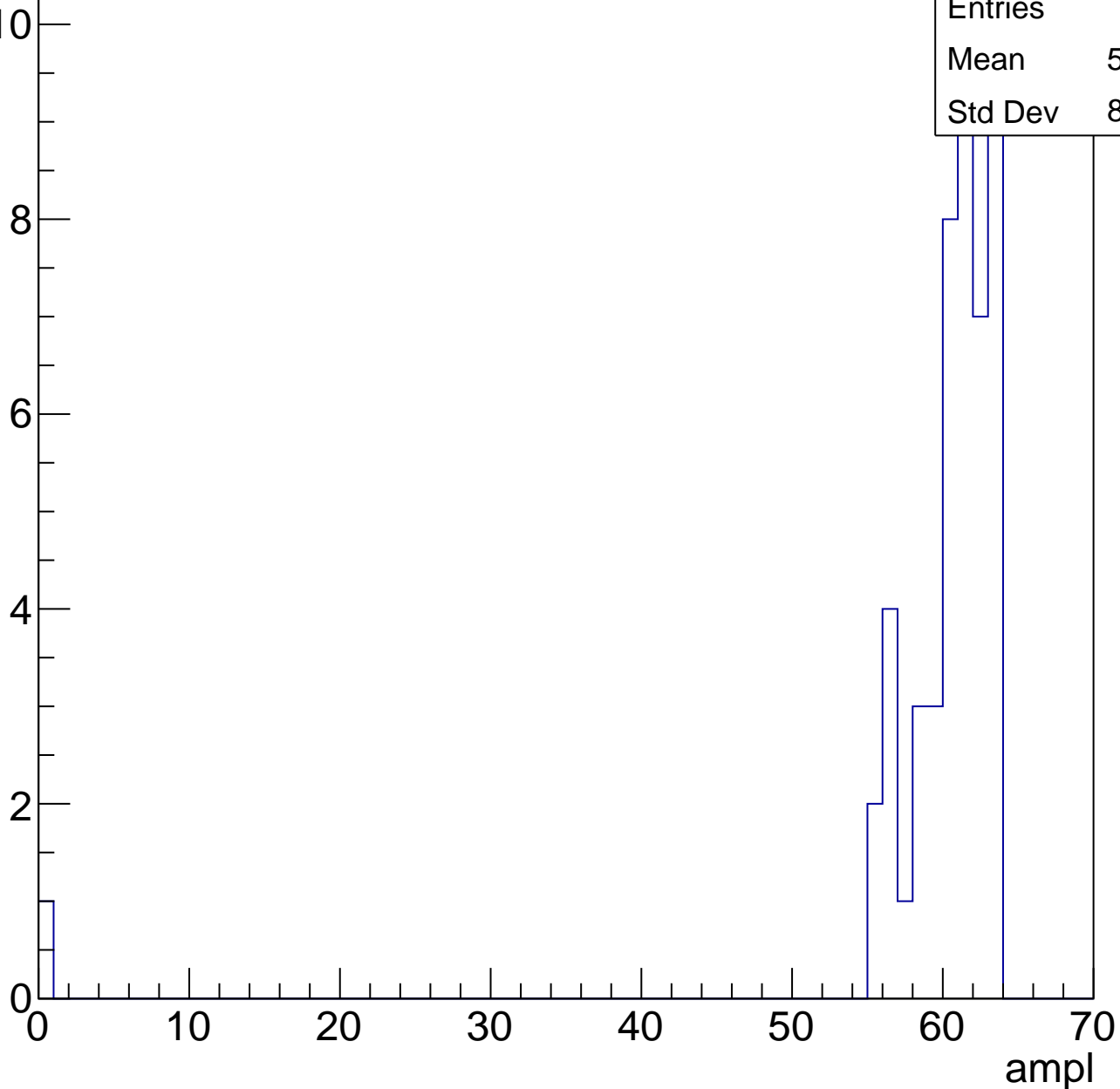


# B0L000S, U19-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

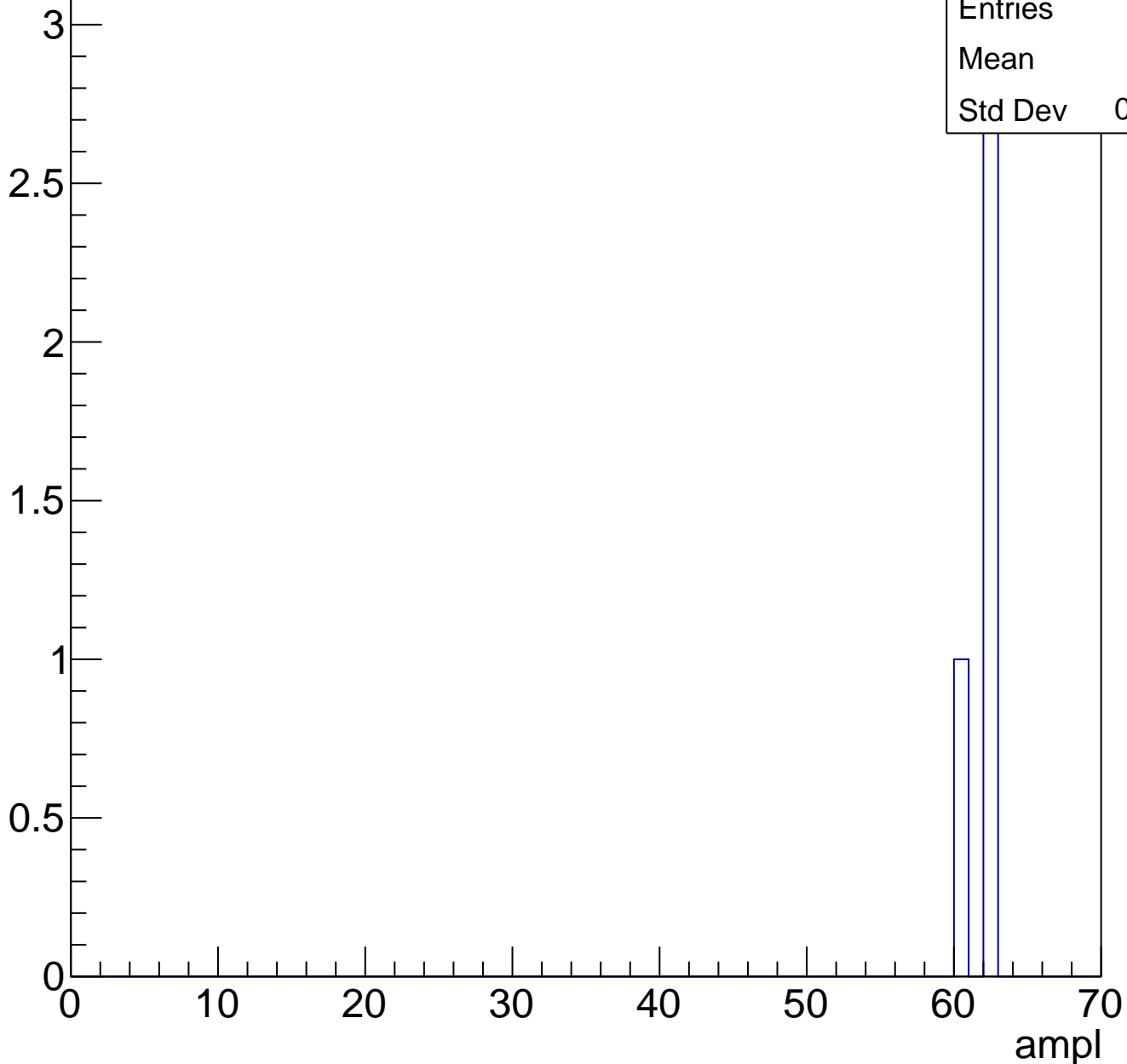
Entries	48
Mean	59.06
Std Dev	8.924



# B0L000S, U19-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B0L000S, U19-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	30.03
Std Dev	3.171

**Gaus mean : 30.4143**

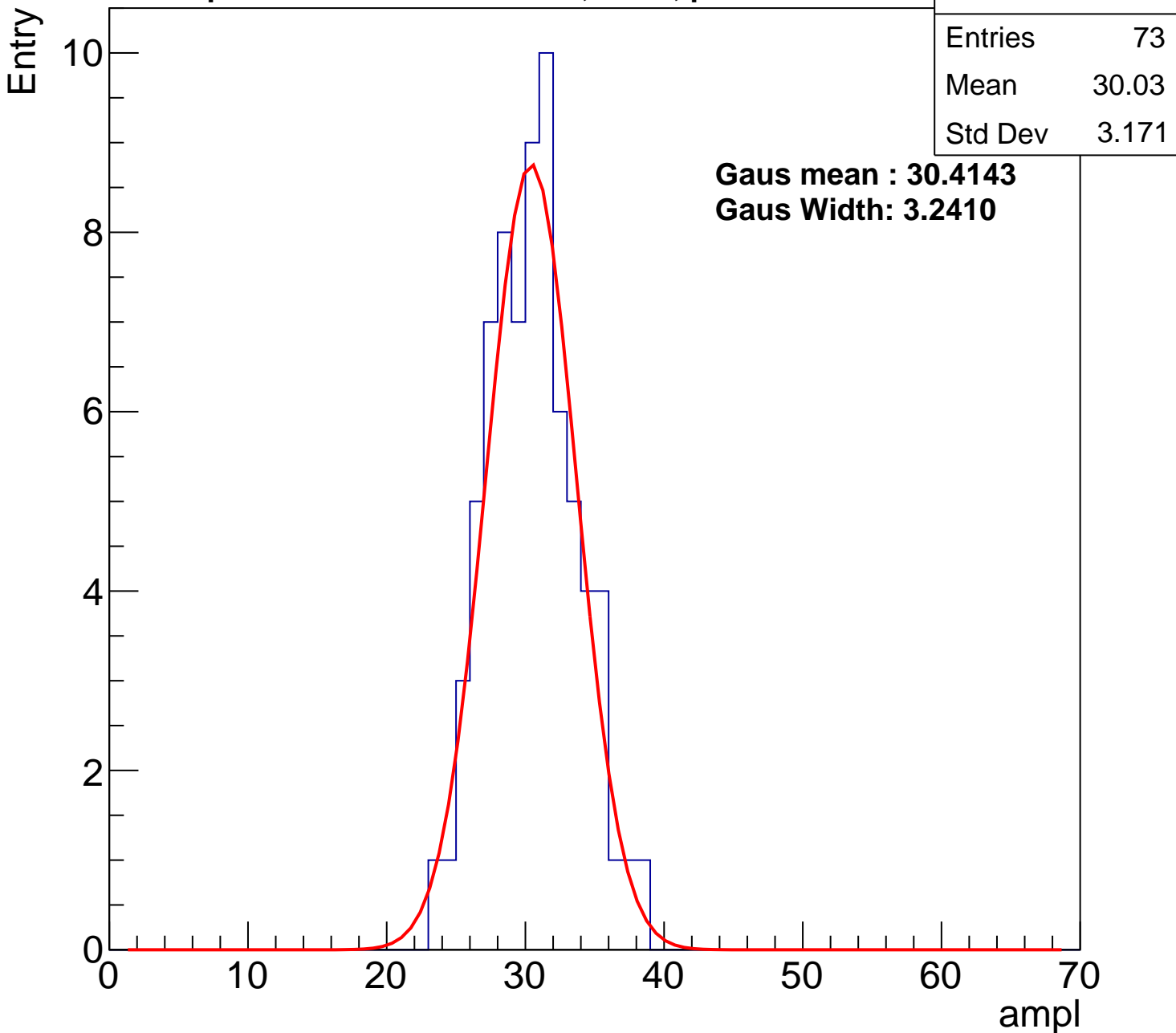
**Gaus Width: 3.2410**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch12, adc1

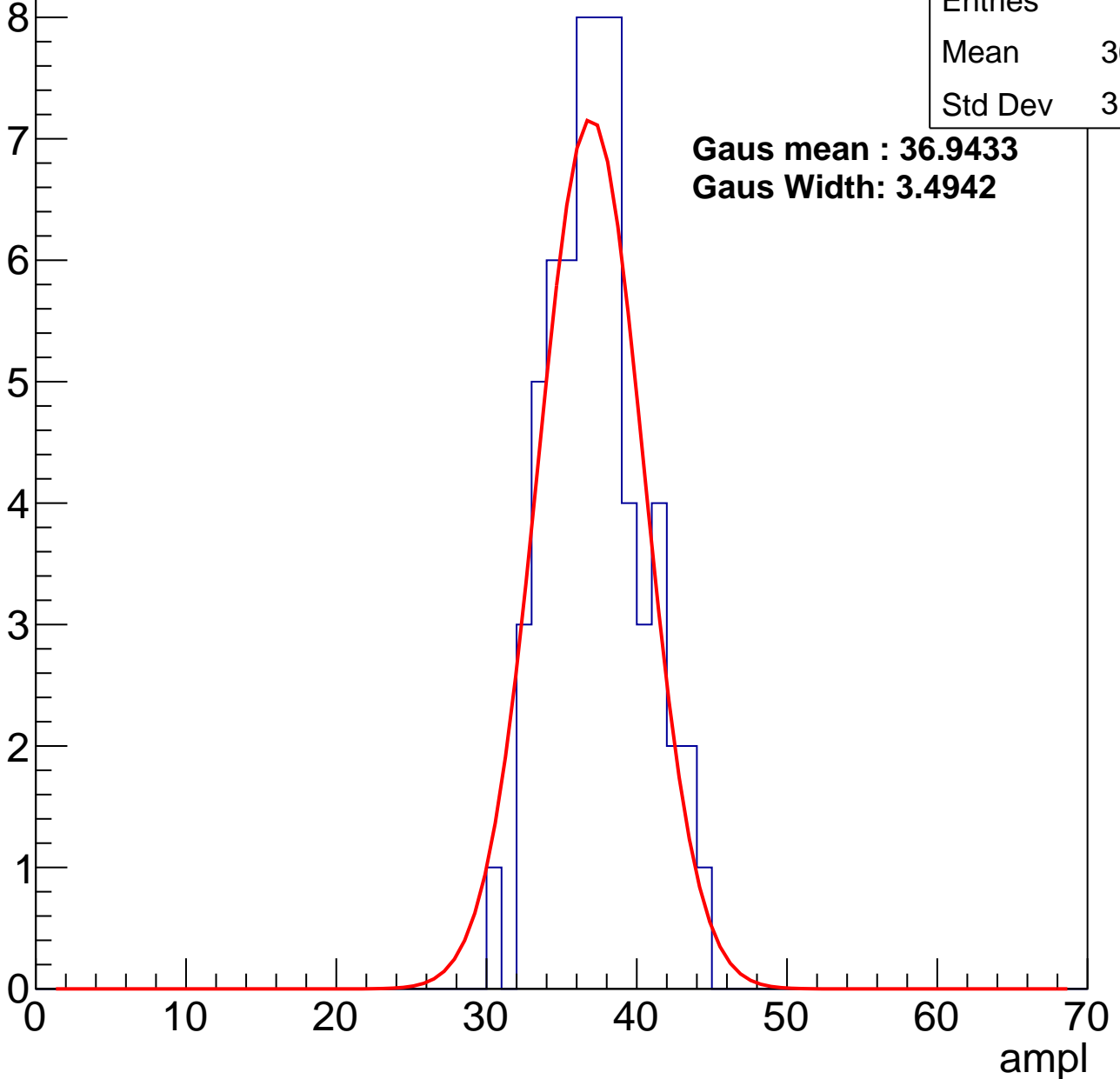
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.84
Std Dev	3.069

**Gaus mean : 36.9433**

**Gaus Width: 3.4942**



# B0L000S, U19-ch12, adc2

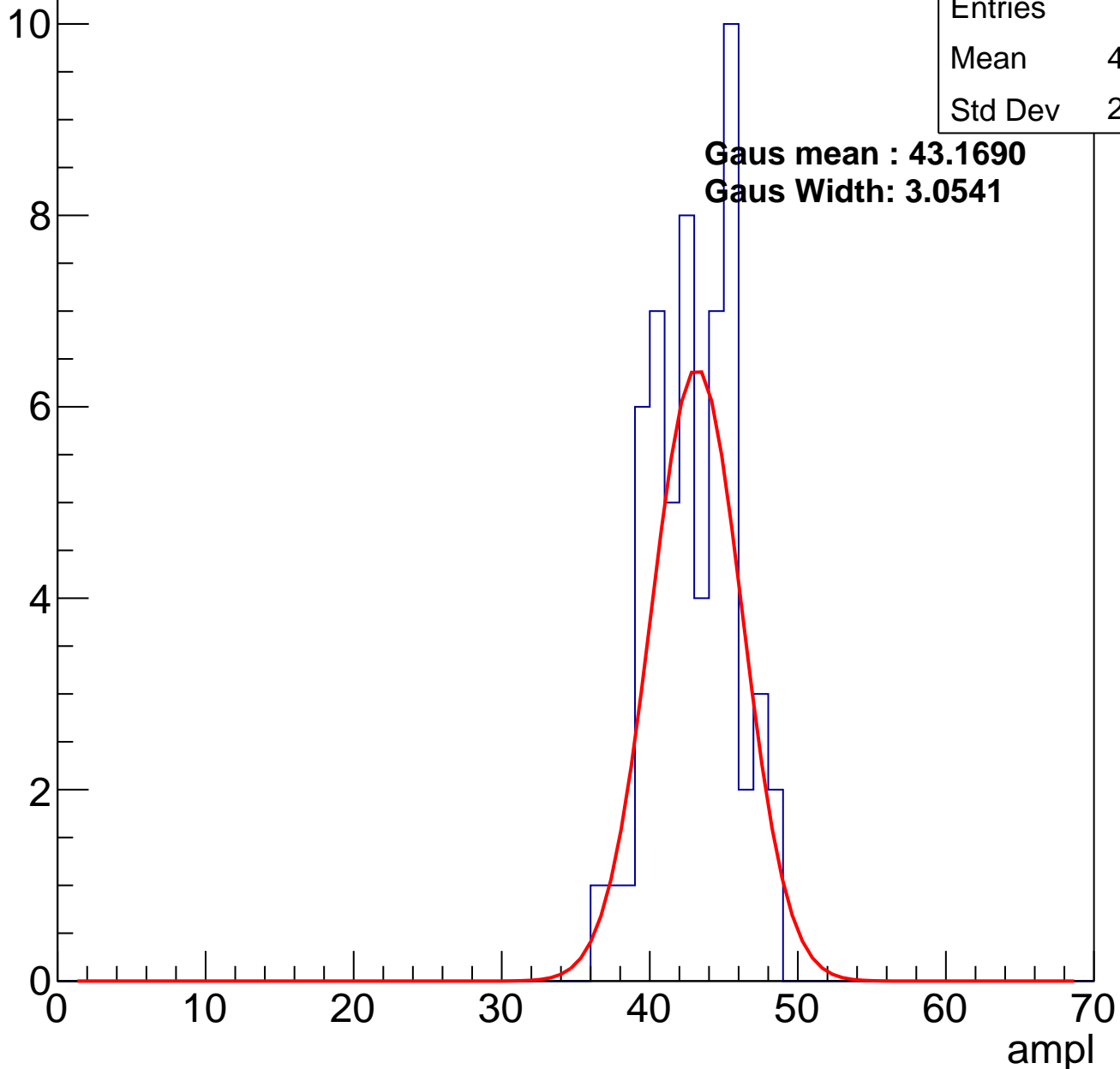
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	42.54
Std Dev	2.804

**Gaus mean : 43.1690**

**Gaus Width: 3.0541**

Entry

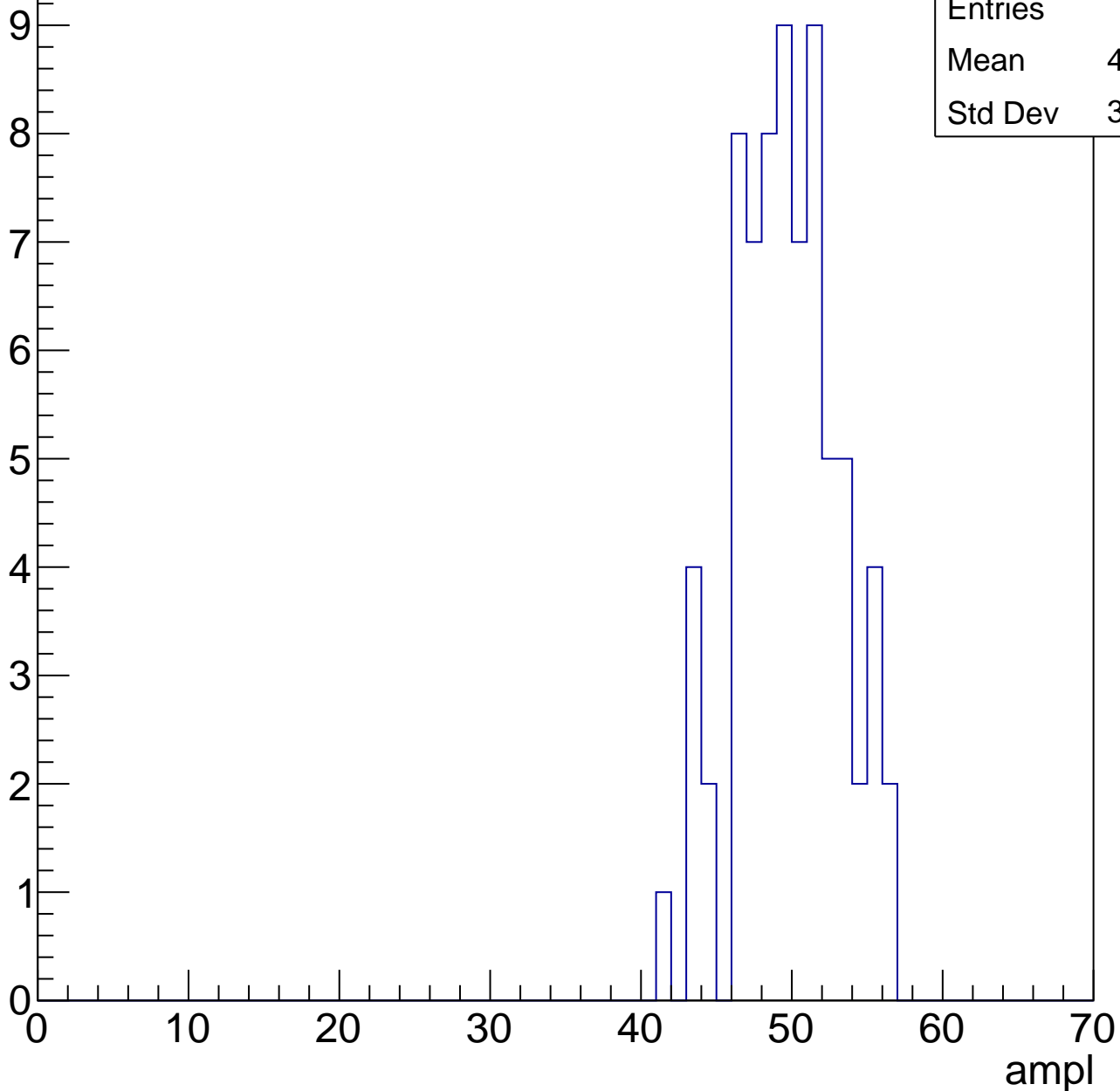


# B0L000S, U19-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	49.27
Std Dev	3.353

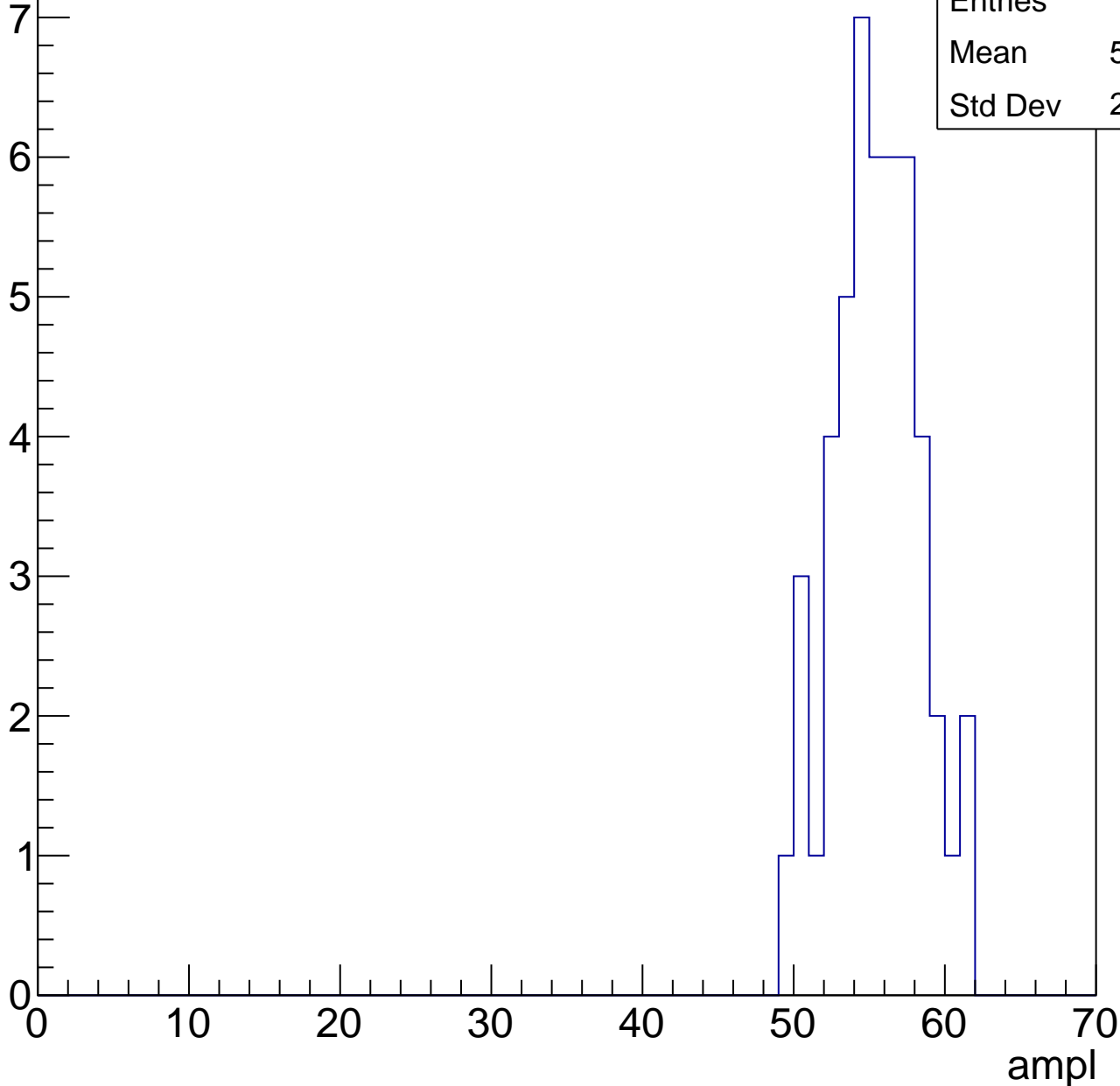


# B0L000S, U19-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

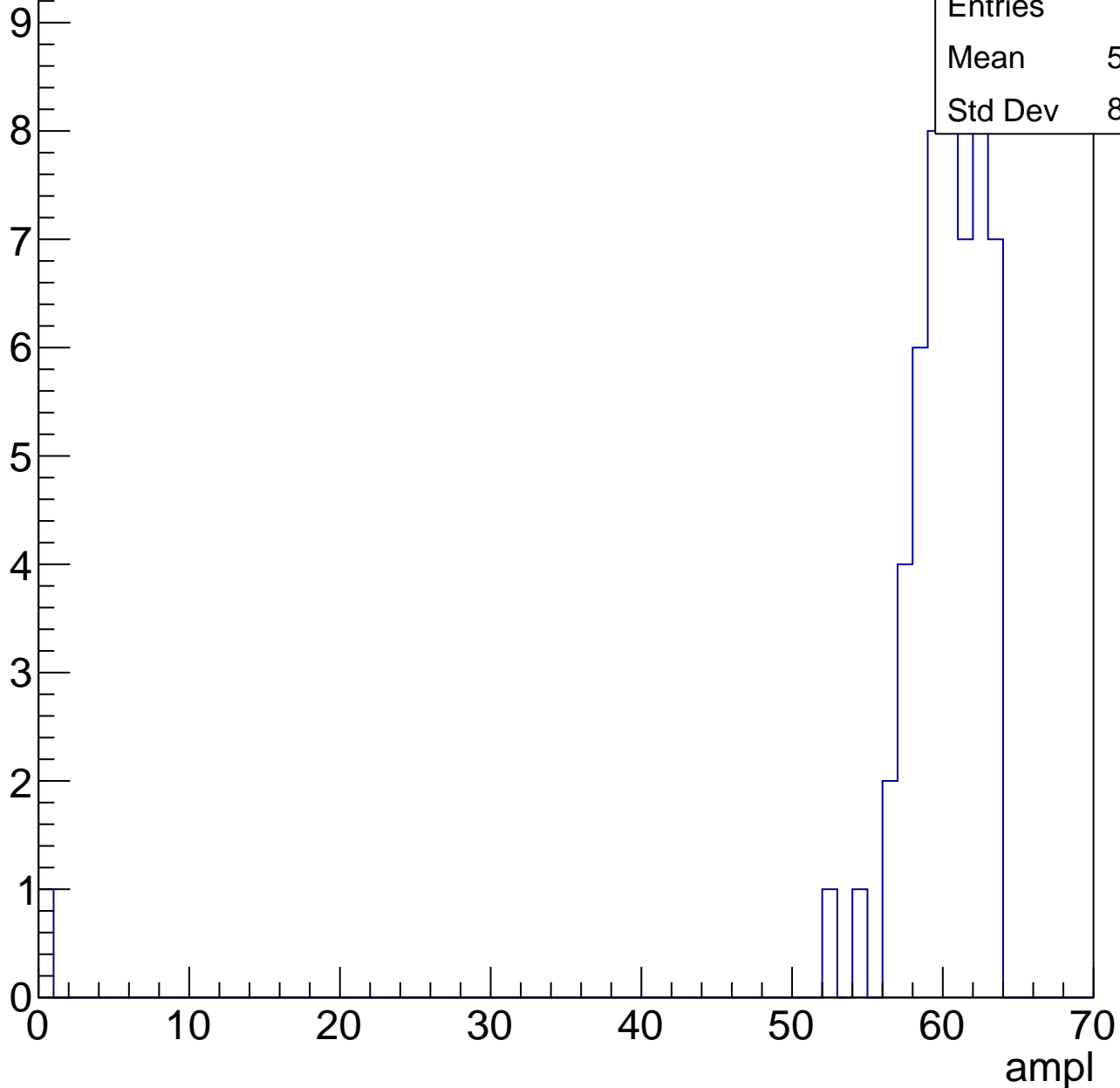
Entries	48
Mean	55.02
Std Dev	2.832



# B0L000S, U19-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



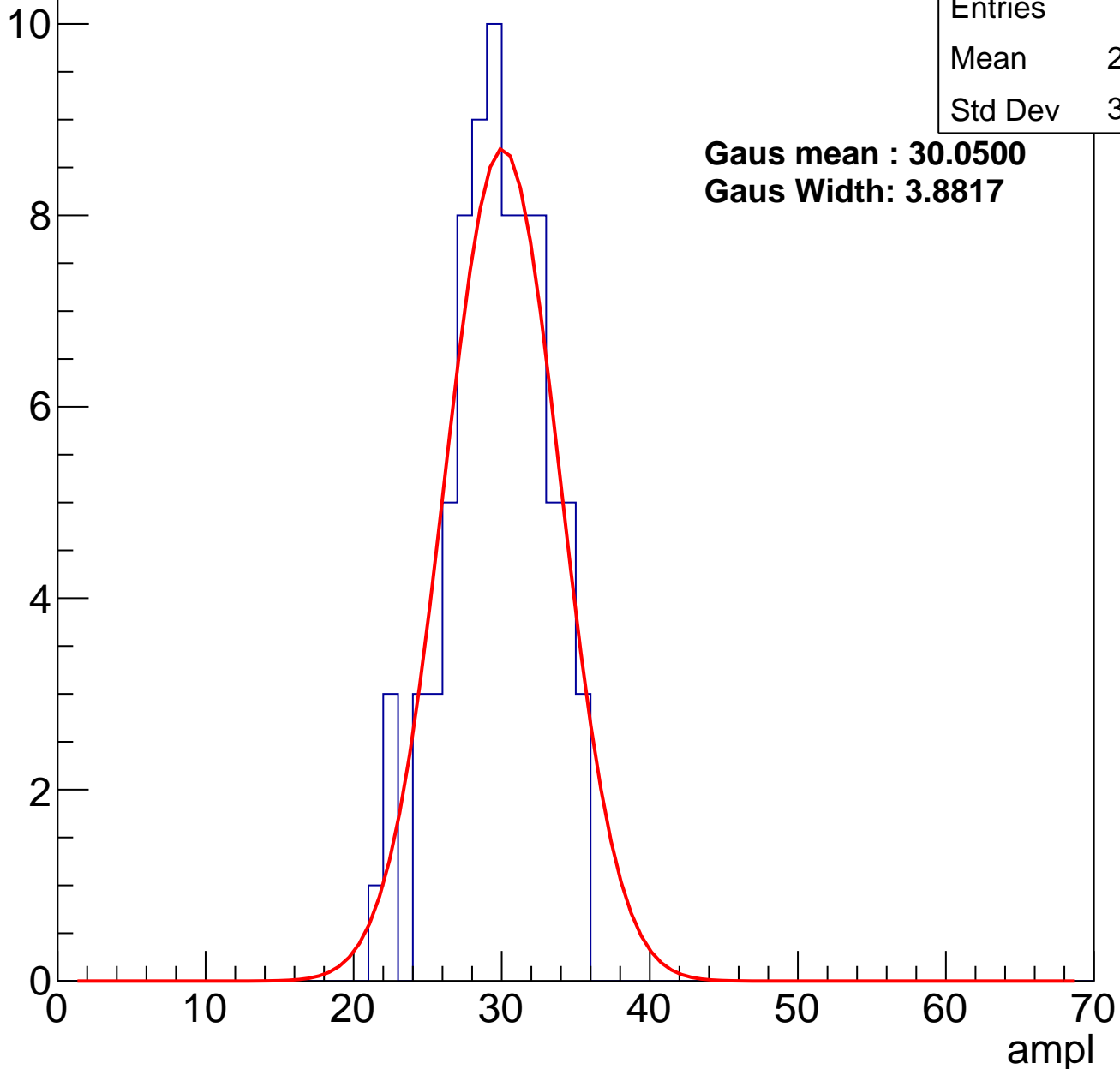
# B0L000S, U19-ch13, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	29.19
Std Dev	3.265

**Gaus mean : 30.0500**  
**Gaus Width: 3.8817**

Entry



# B0L000S, U19-ch13, adc1

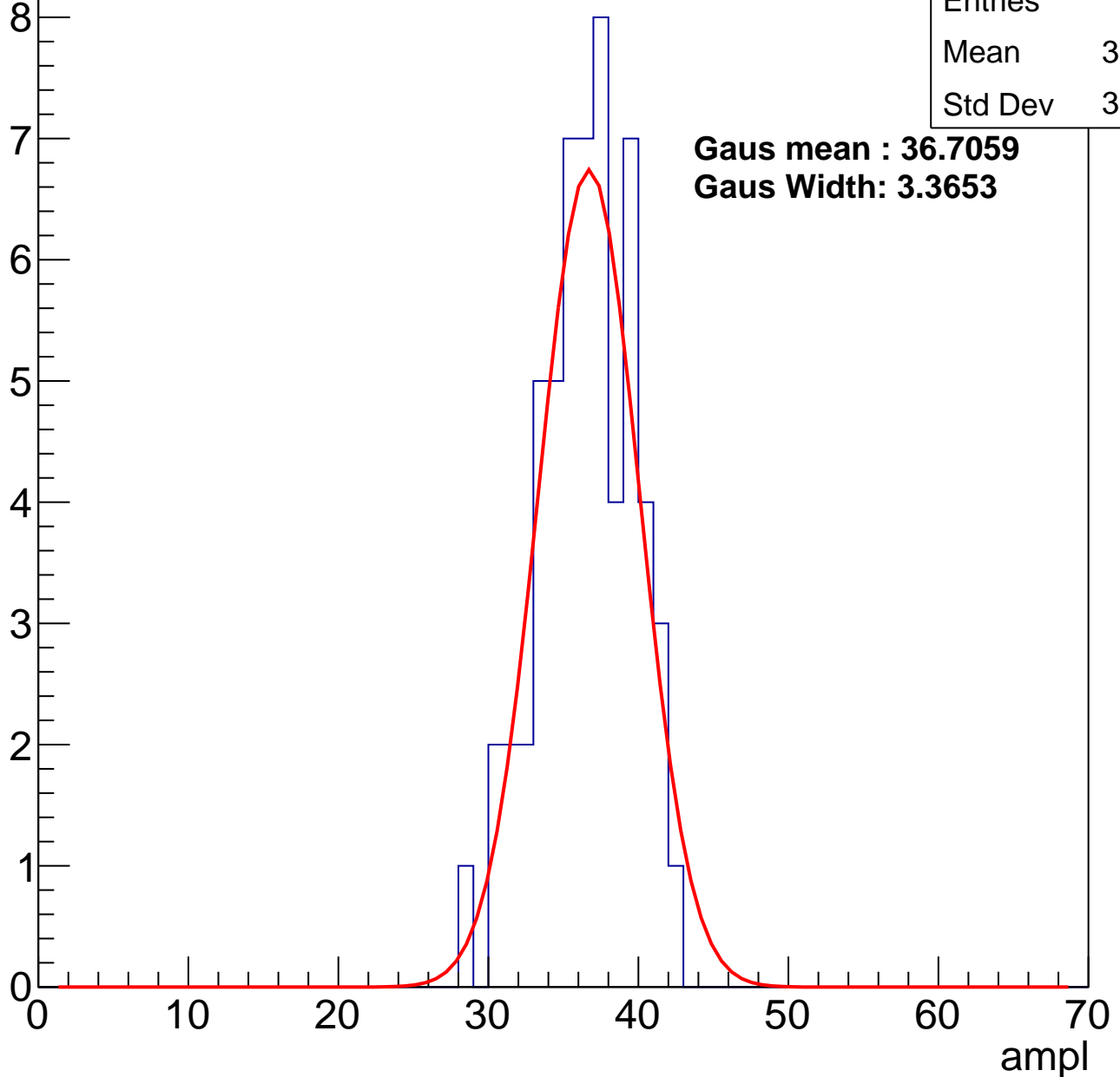
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	36.07
Std Dev	3.084

**Gaus mean : 36.7059**

**Gaus Width: 3.3653**



# B0L000S, U19-ch13, adc2

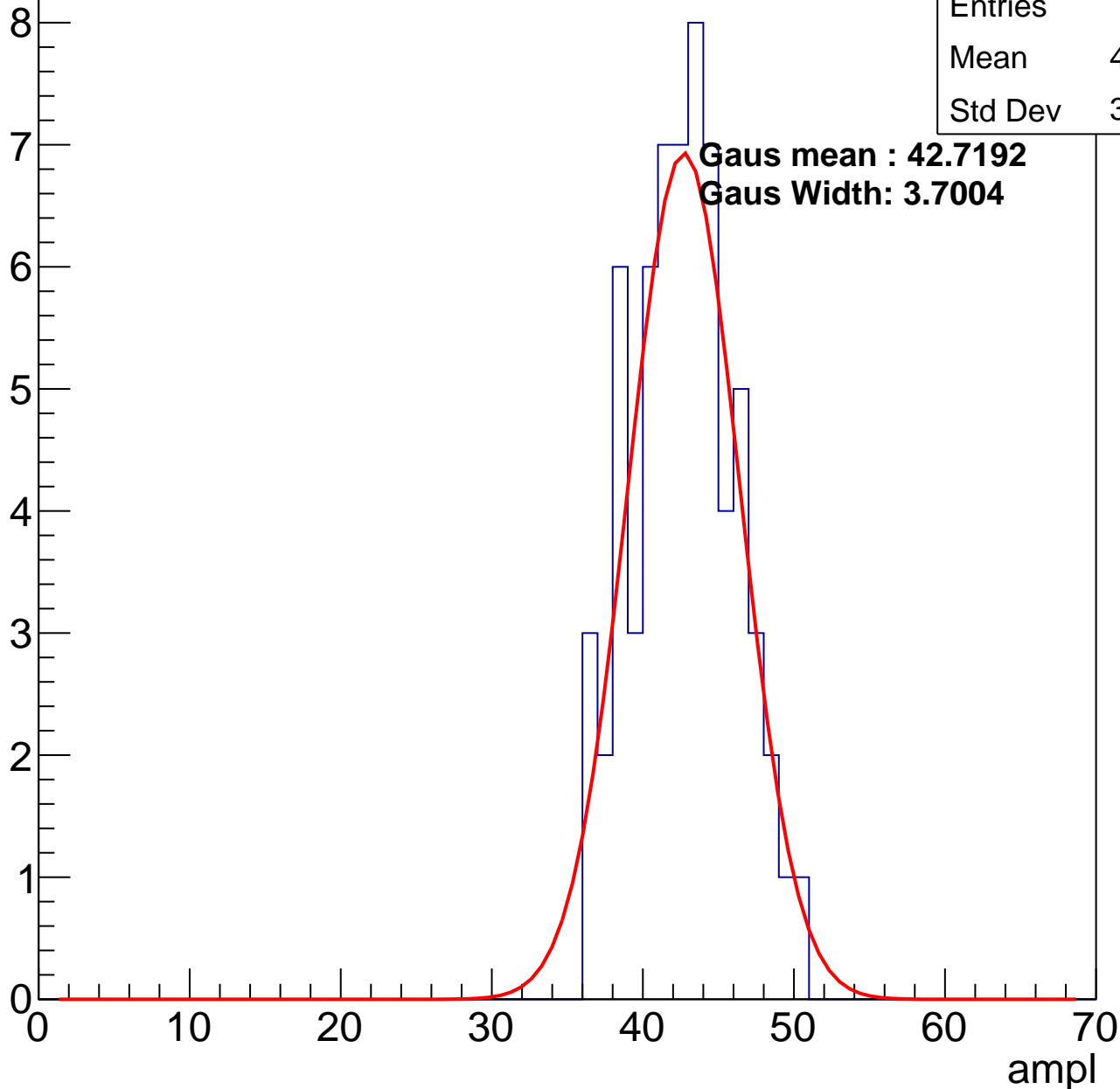
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.25
Std Dev	3.328

**Gaus mean : 42.7192**

**Gaus Width: 3.7004**

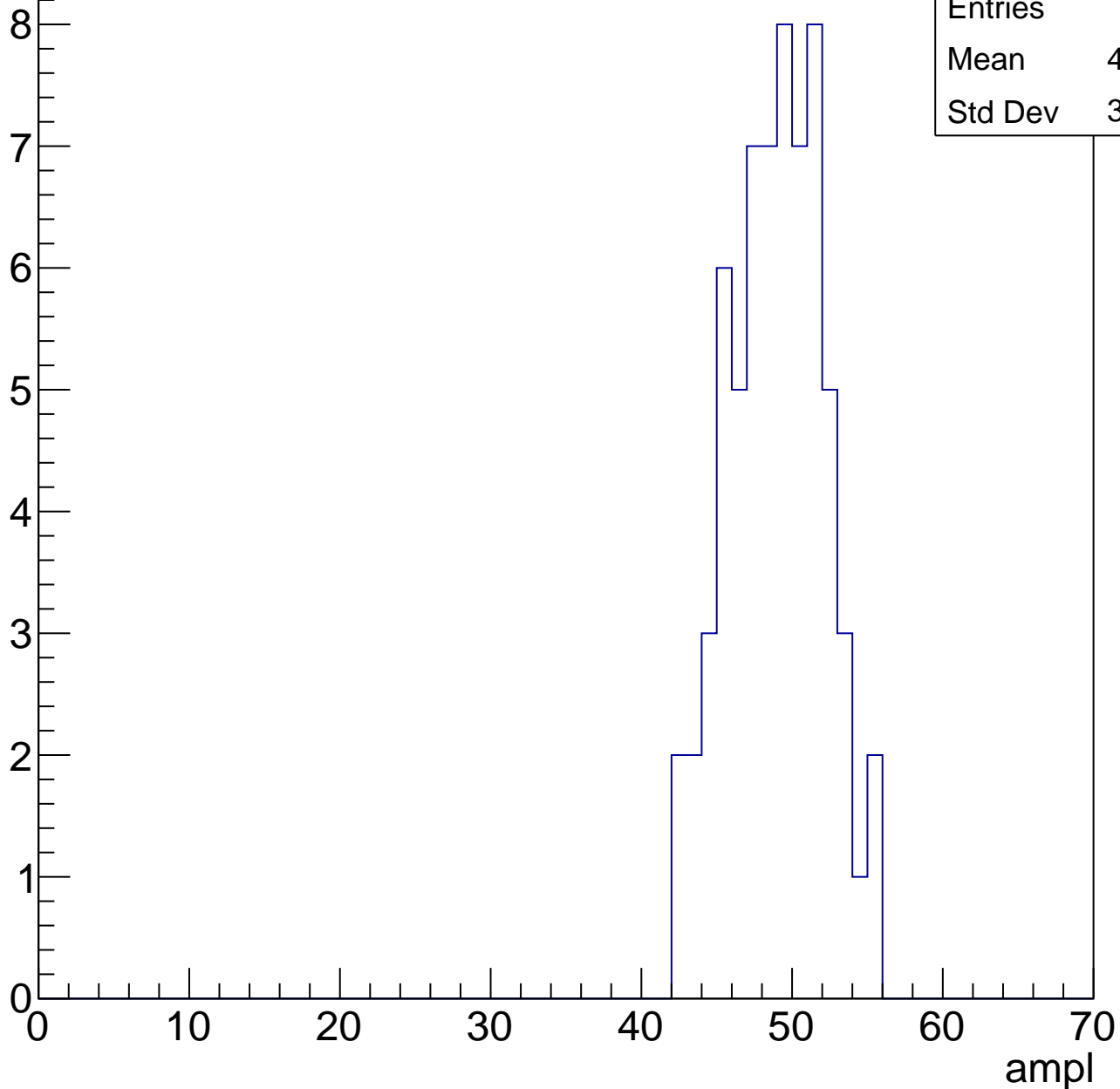


# B0L000S, U19-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	48.48
Std Dev	3.096



# B0L000S, U19-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	55.66
Std Dev	3.22

Entry

10

8

6

4

2

0

0

10

20

30

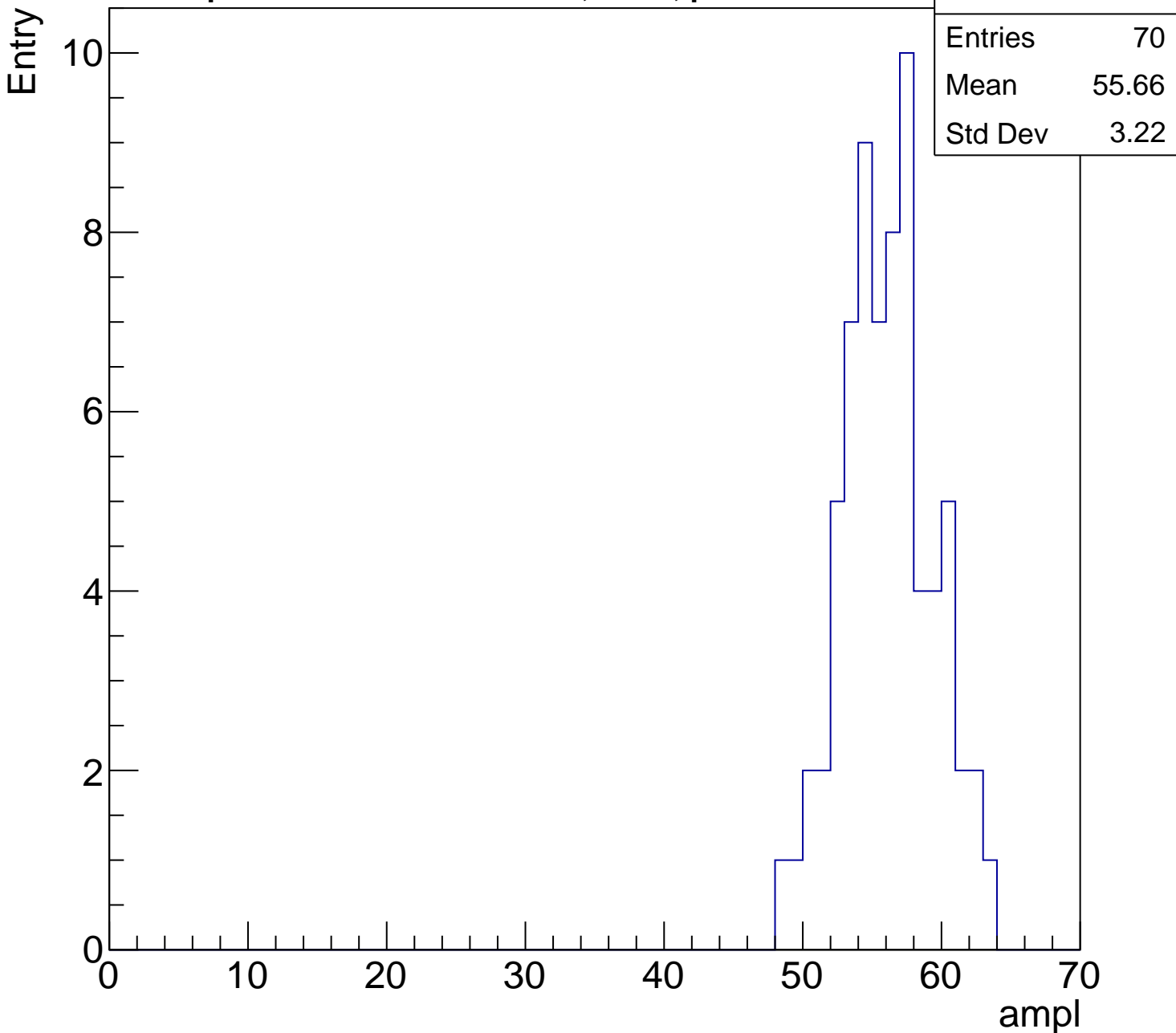
40

50

60

ampl

70

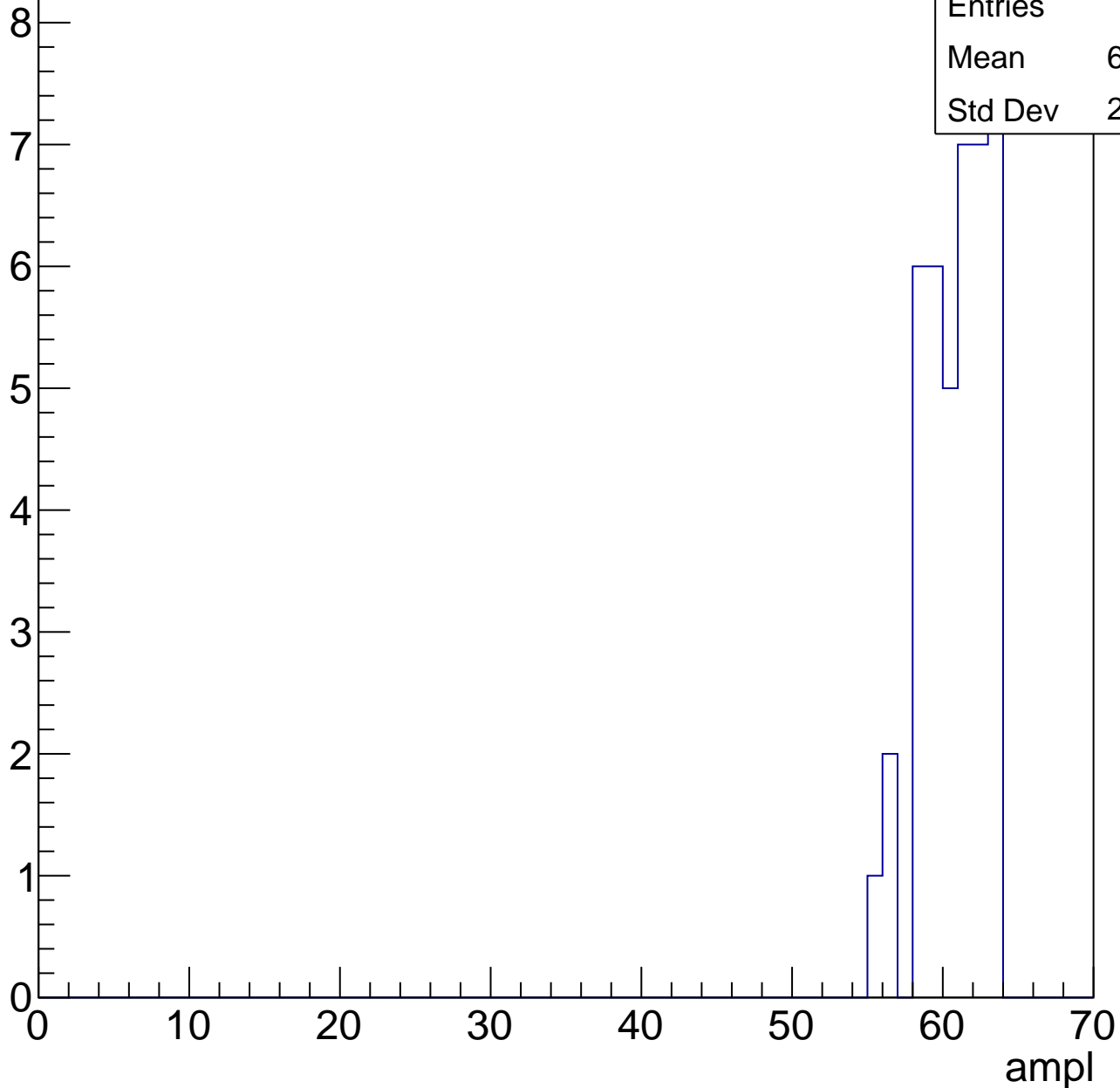


# B0L000S, U19-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	60.33
Std Dev	2.123



# B0L000S, U19-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch14, adc0

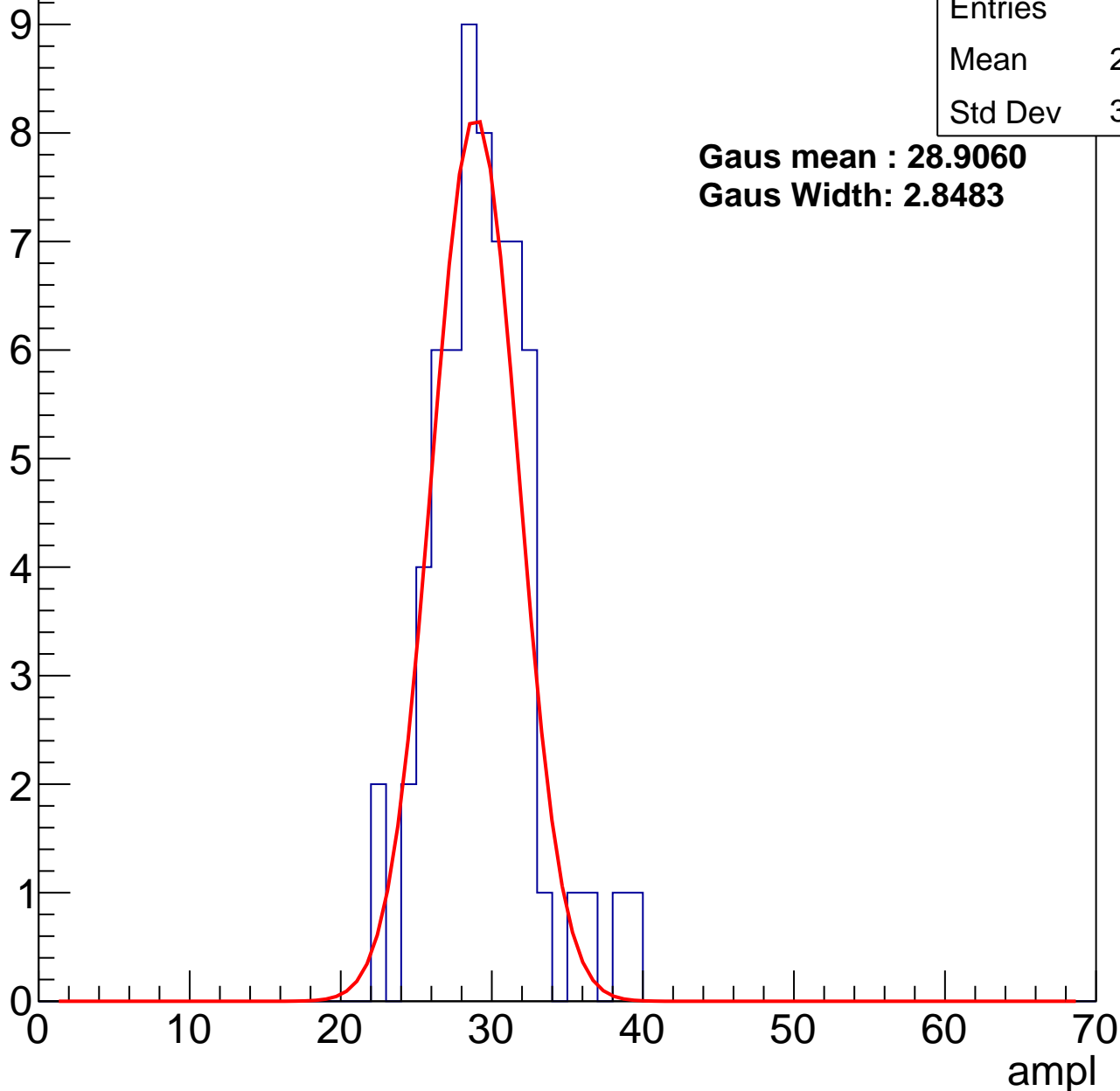
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	28.94
Std Dev	3.287

**Gaus mean : 28.9060**

**Gaus Width: 2.8483**



# B0L000S, U19-ch14, adc1

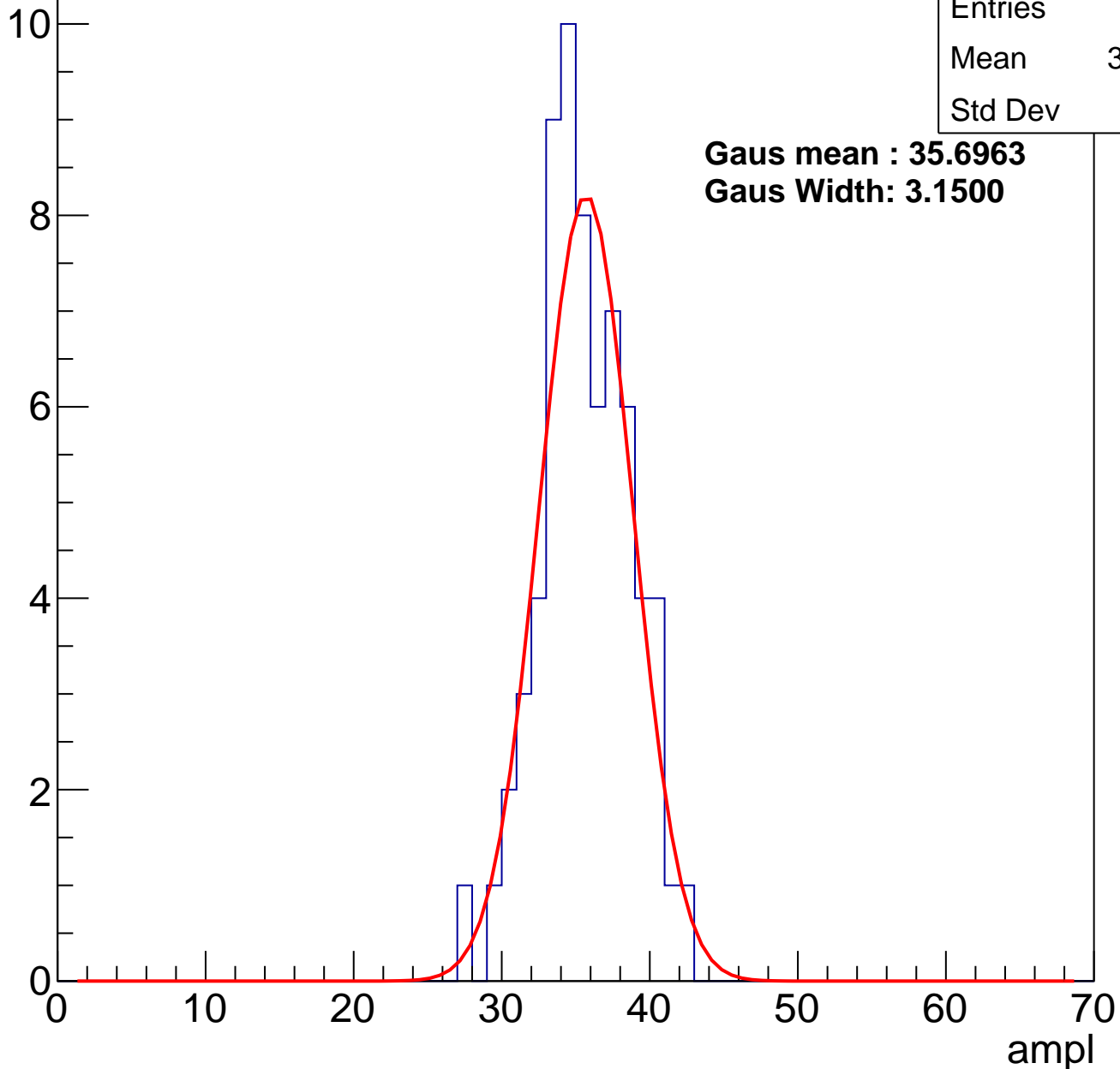
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	35.16
Std Dev	3.03

**Gaus mean : 35.6963**

**Gaus Width: 3.1500**

Entry



# B0L000S, U19-ch14, adc2

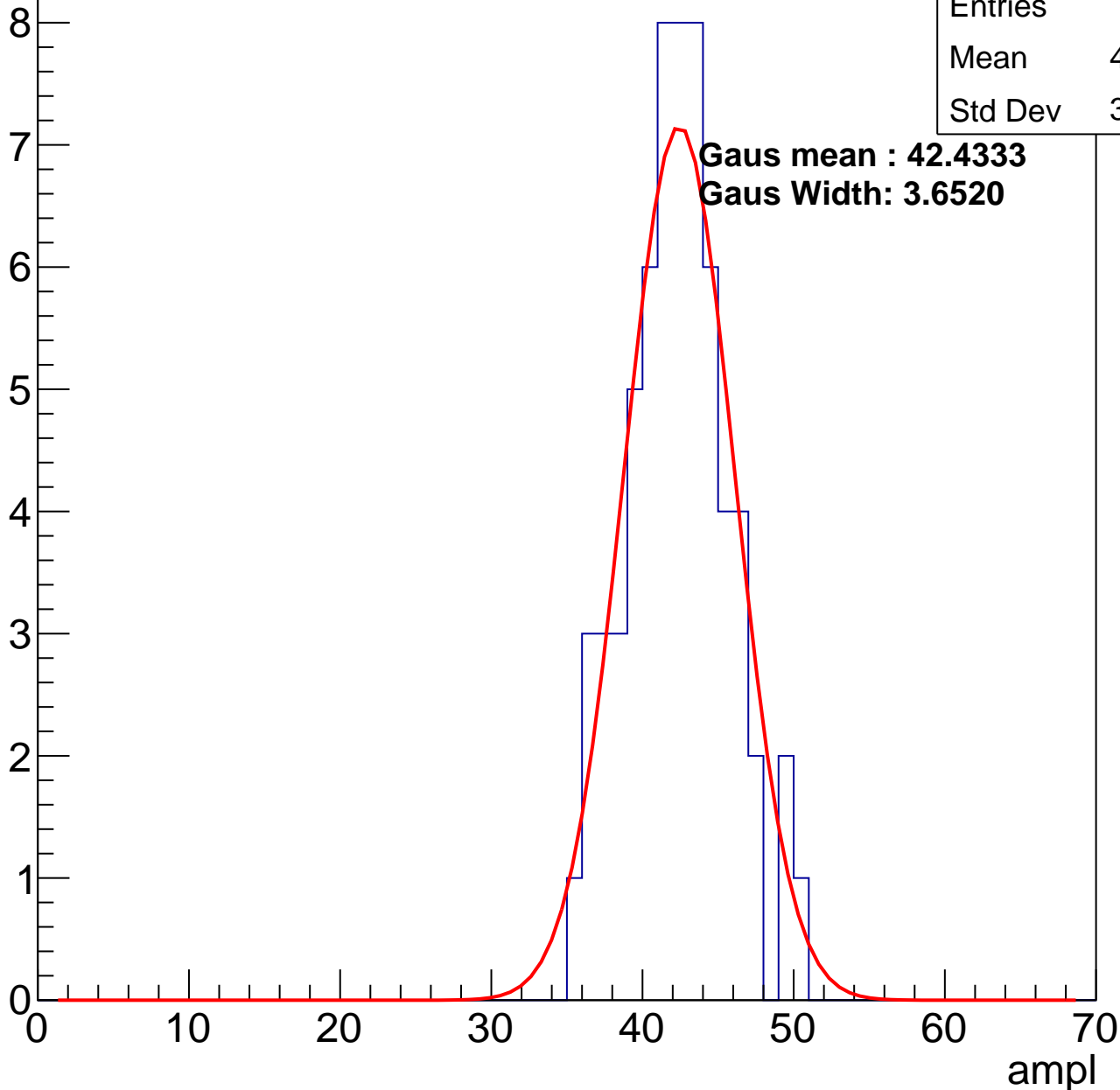
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.89
Std Dev	3.308

**Gaus mean : 42.4333**

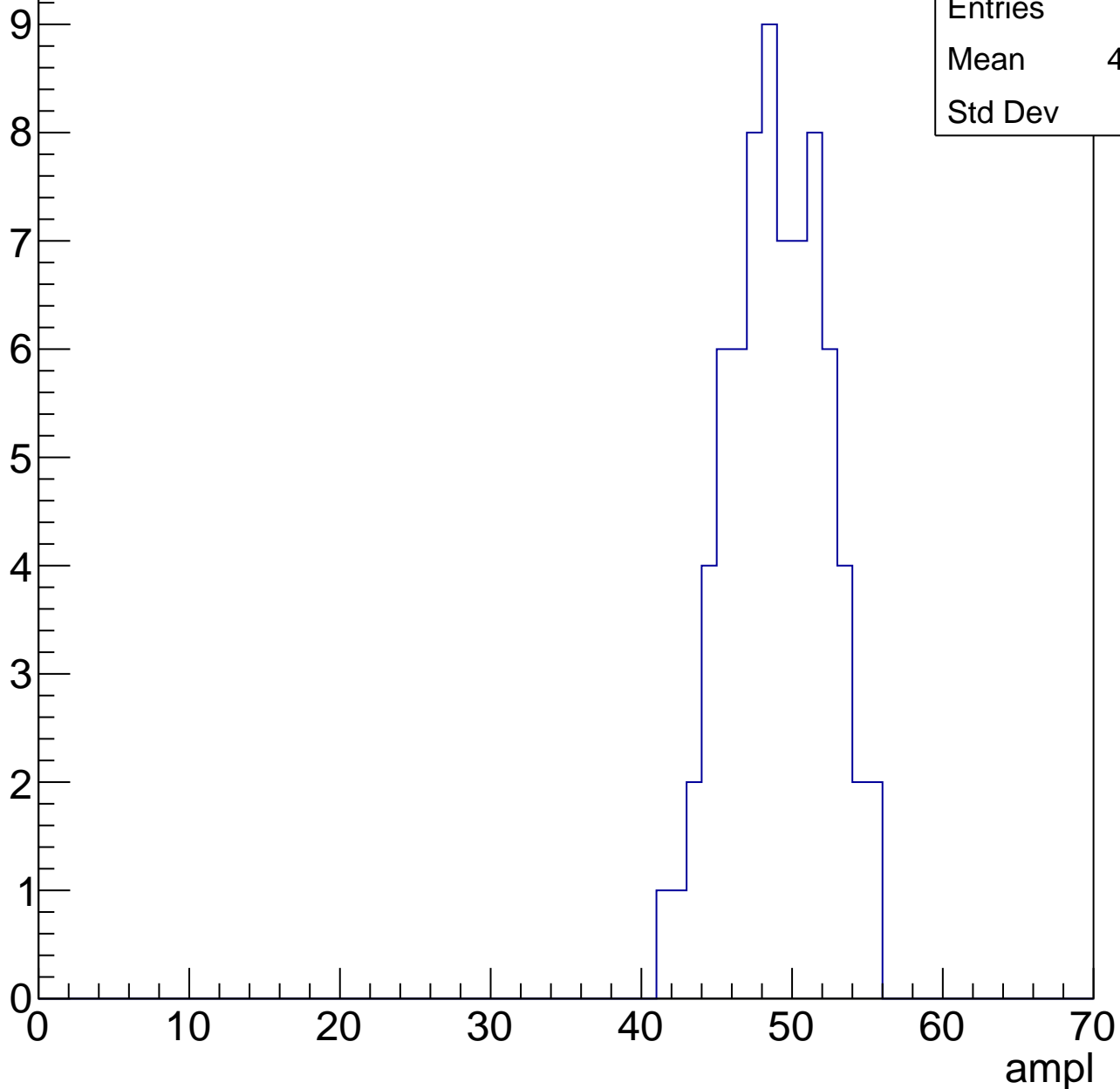
**Gaus Width: 3.6520**



# B0L000S, U19-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



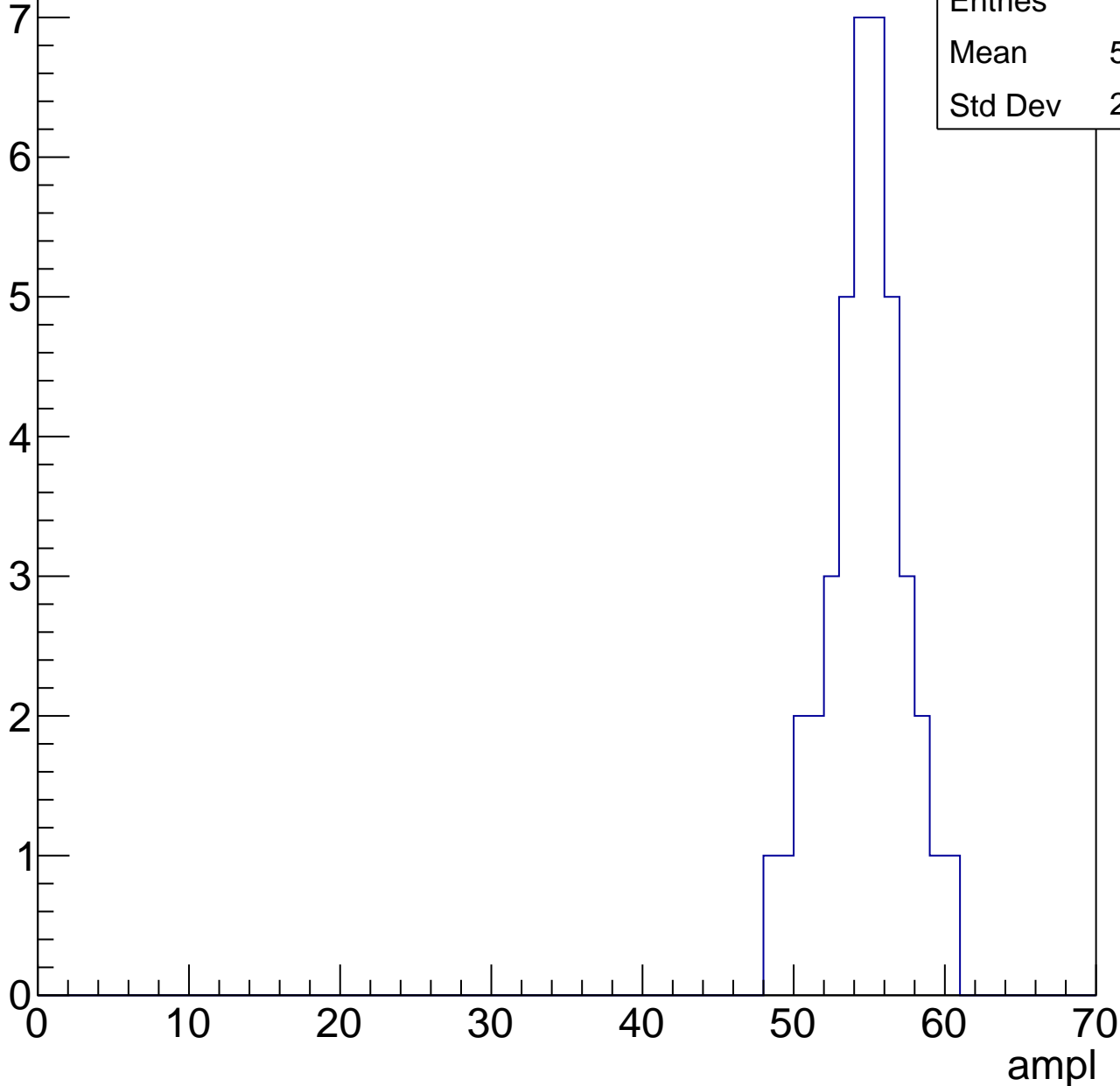
Entries	73
Mean	48.52
Std Dev	3.18

# B0L000S, U19-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	54.23
Std Dev	2.612

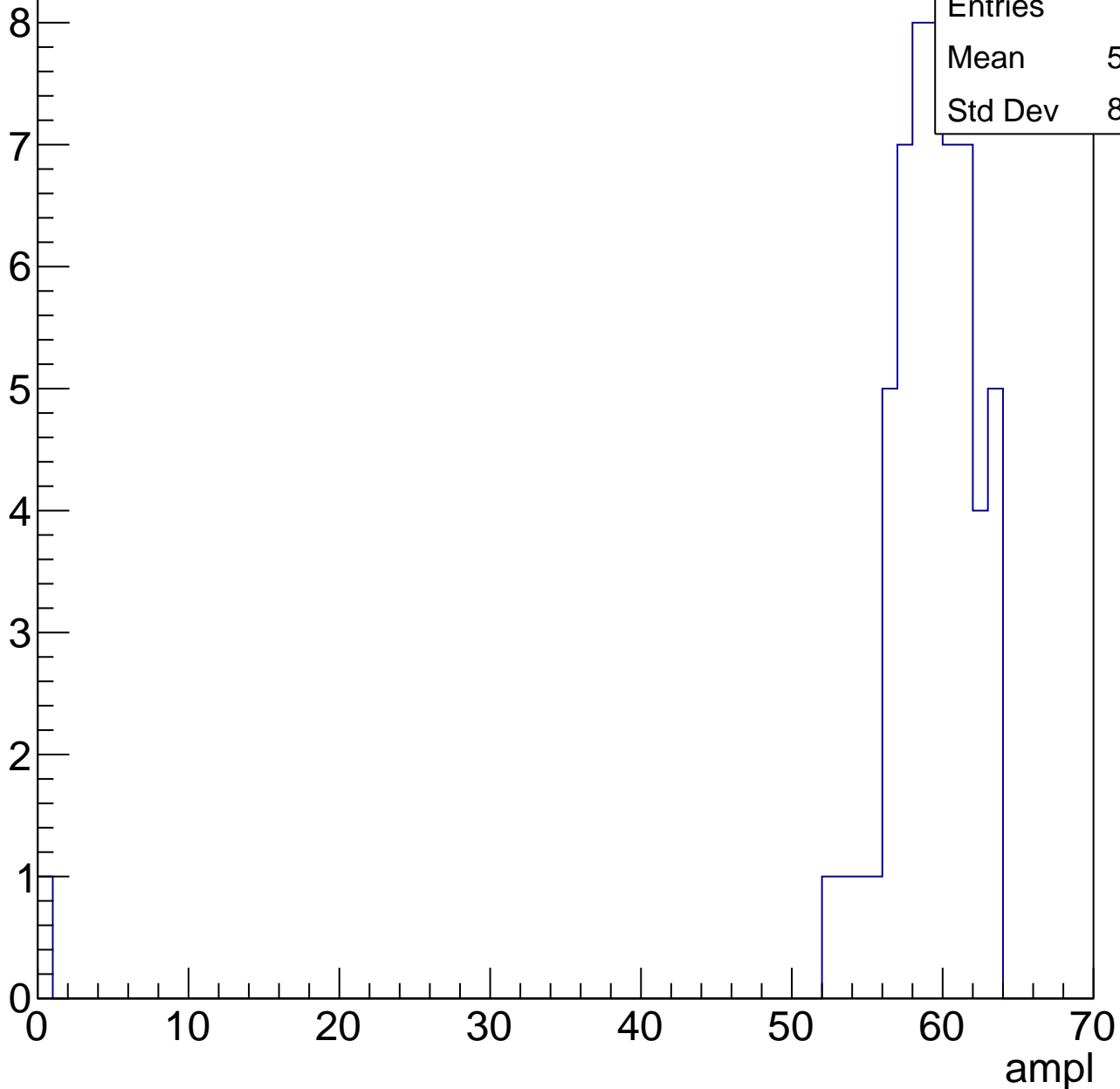


# B0L000S, U19-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	57.84
Std Dev	8.198

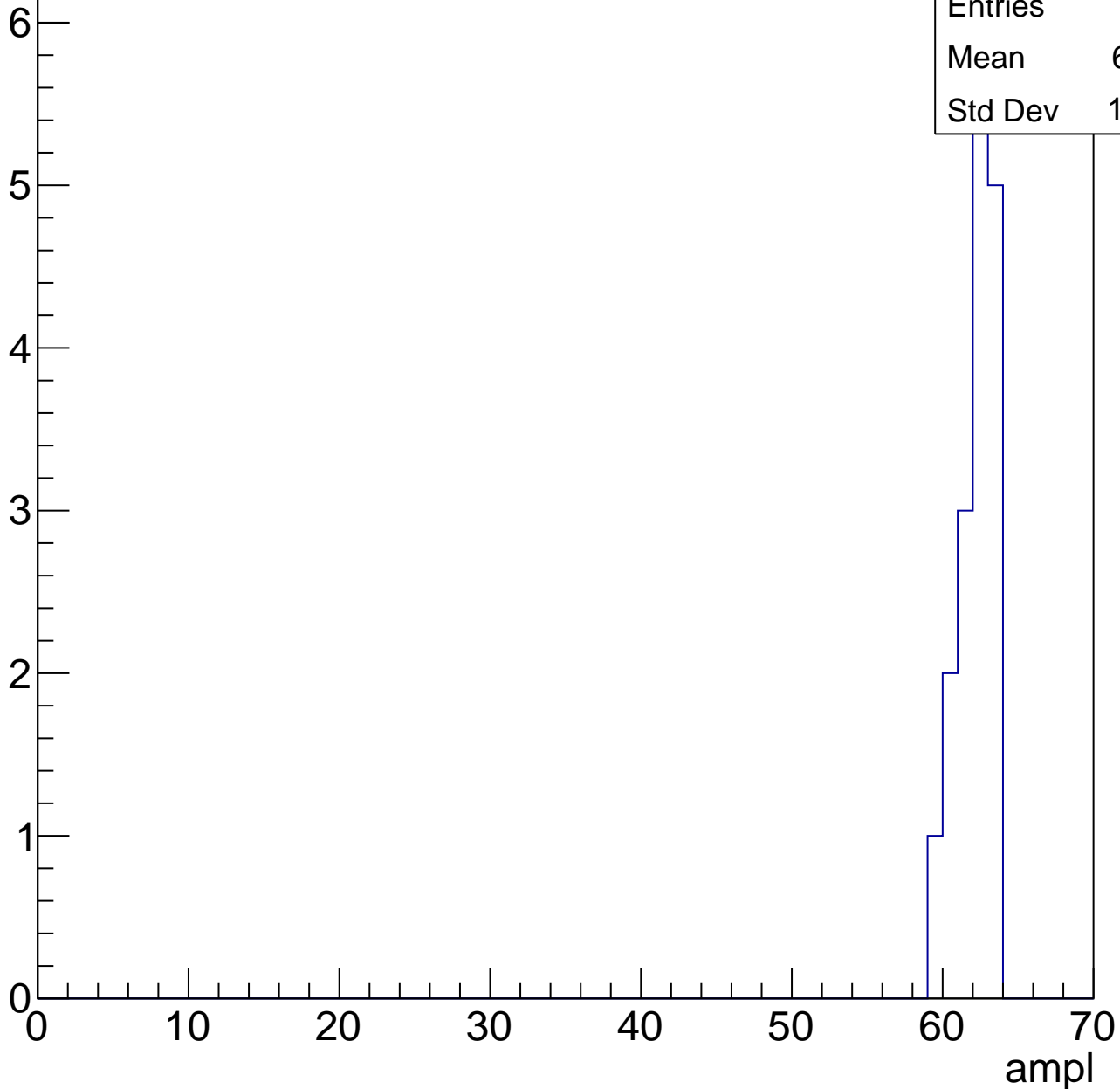


# B0L000S, U19-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61.71
Std Dev	1.176





# B0L000S, U19-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch15, adc0

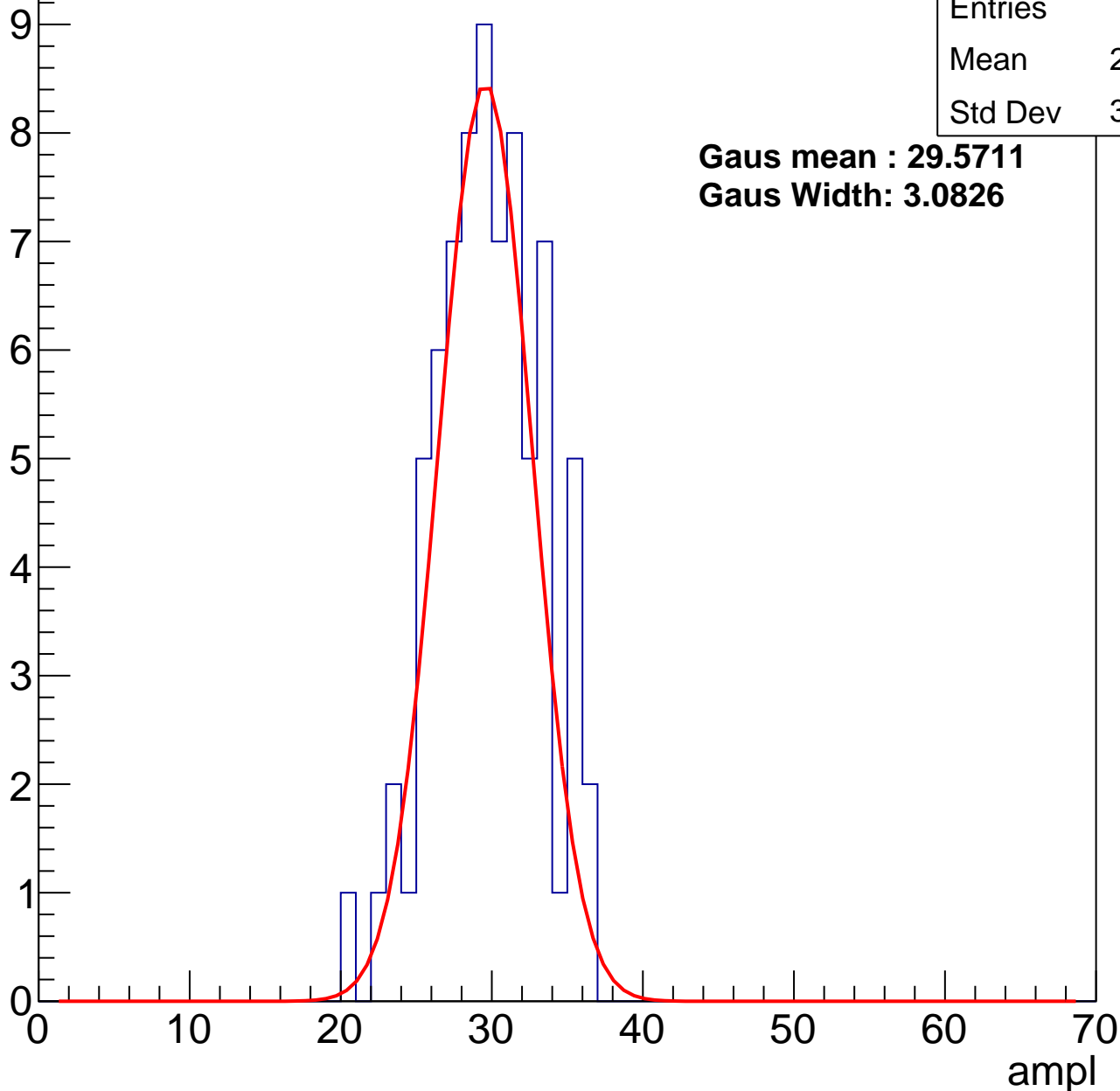
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.29
Std Dev	3.444

**Gaus mean : 29.5711**

**Gaus Width: 3.0826**



# B0L000S, U19-ch15, adc1

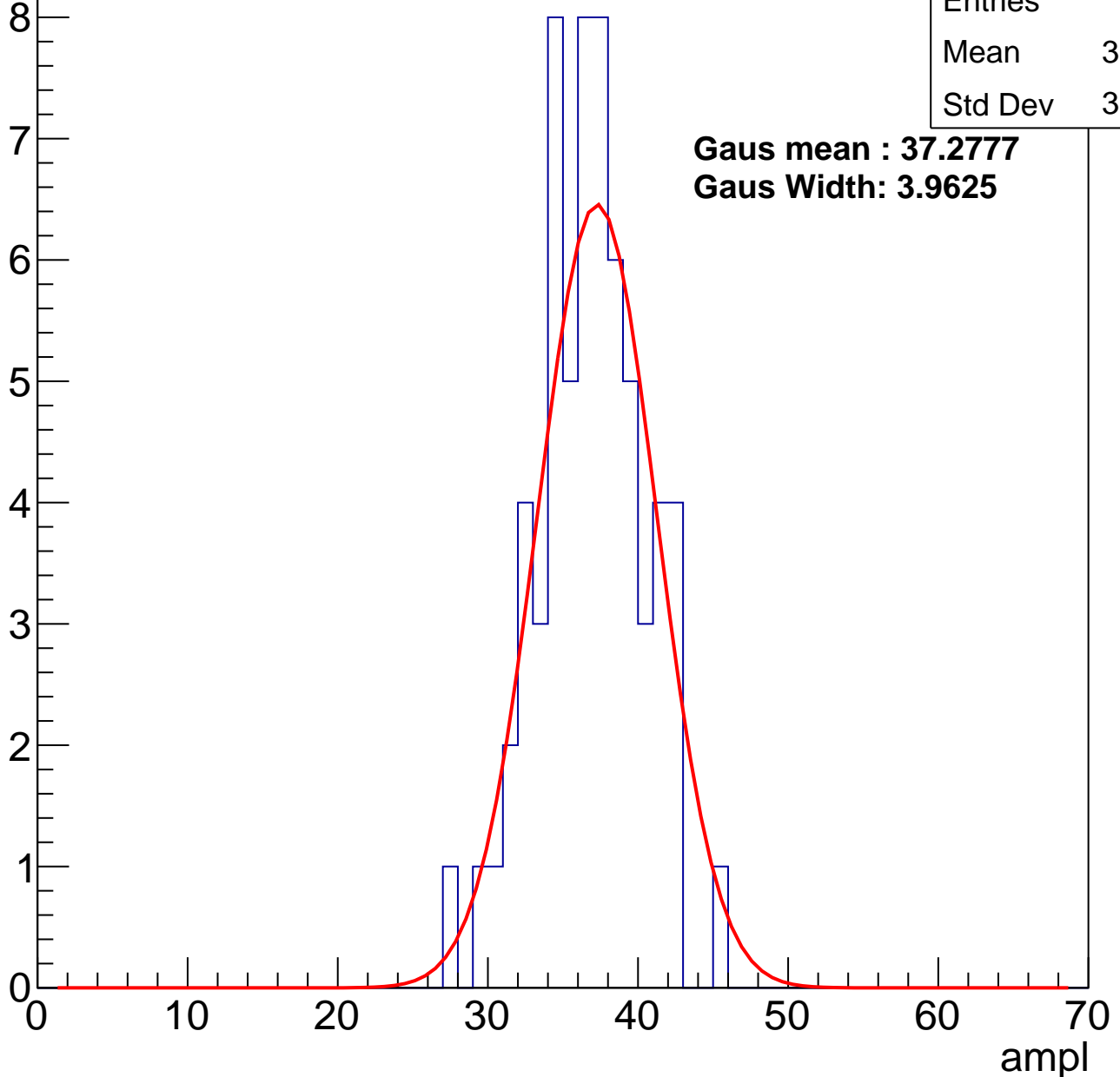
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.34
Std Dev	3.505

**Gaus mean : 37.2777**

**Gaus Width: 3.9625**



# B0L000S, U19-ch15, adc2

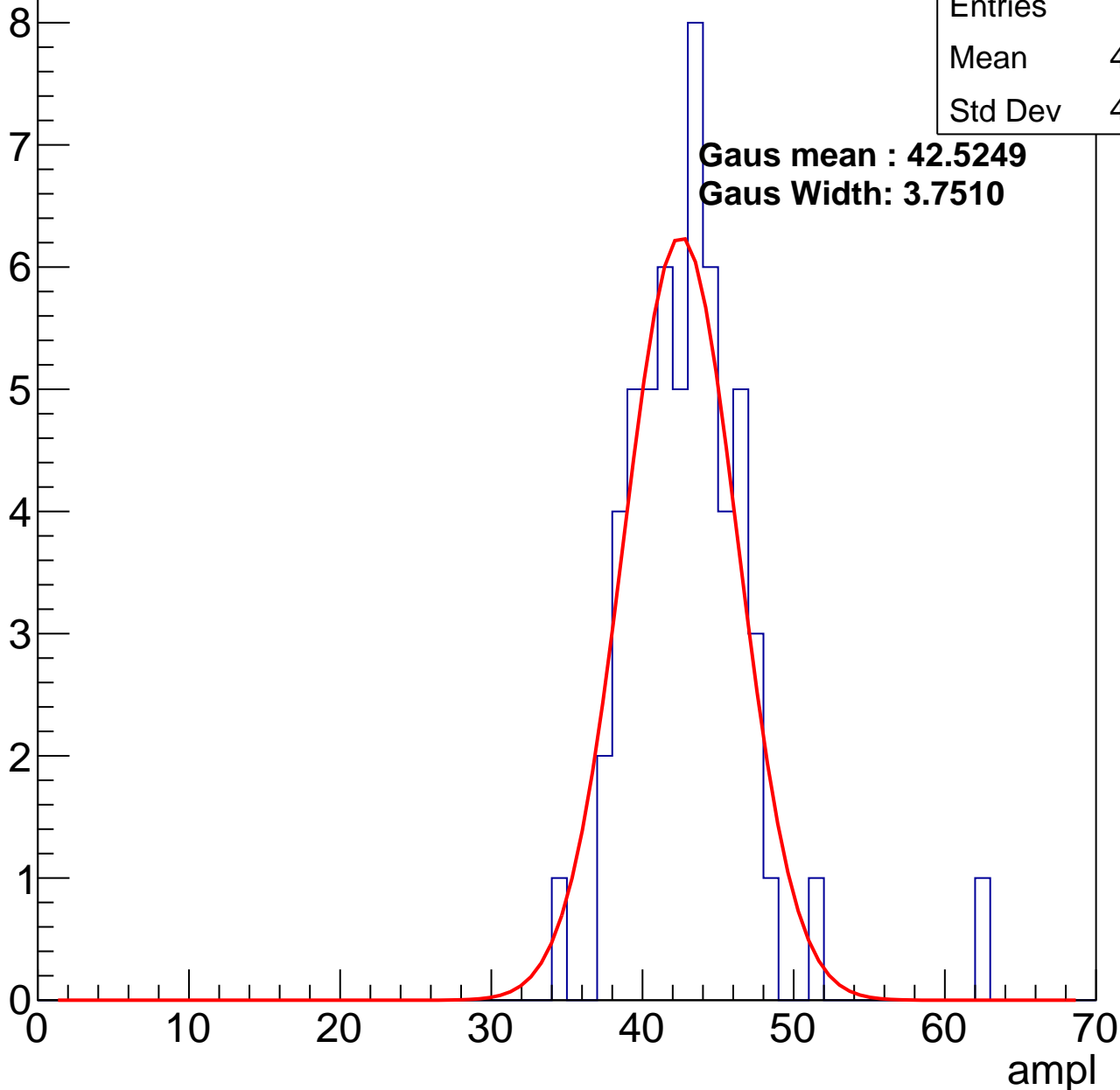
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.65
Std Dev	4.115

**Gaus mean : 42.5249**

**Gaus Width: 3.7510**

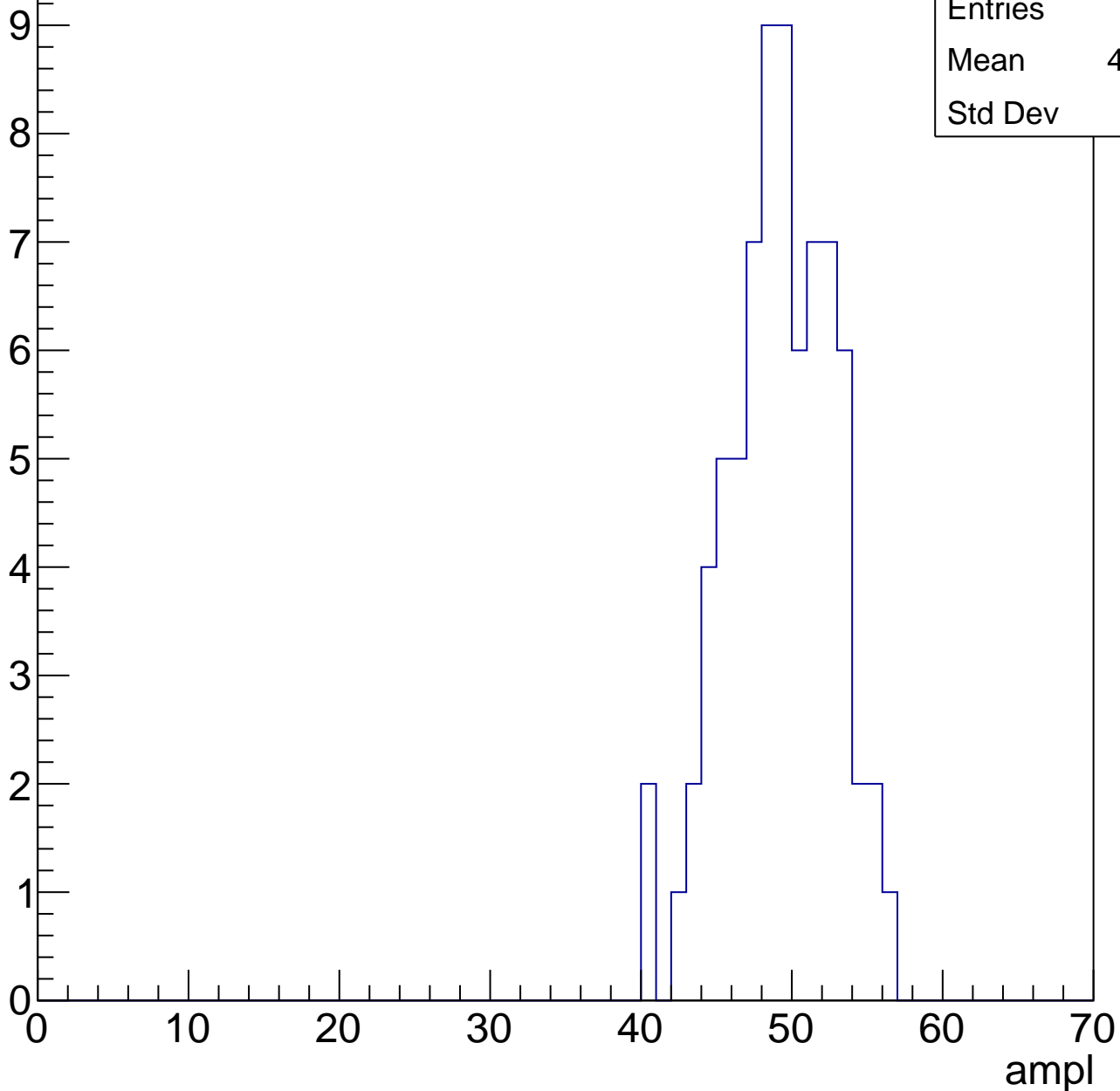


# B0L000S, U19-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	48.72
Std Dev	3.47

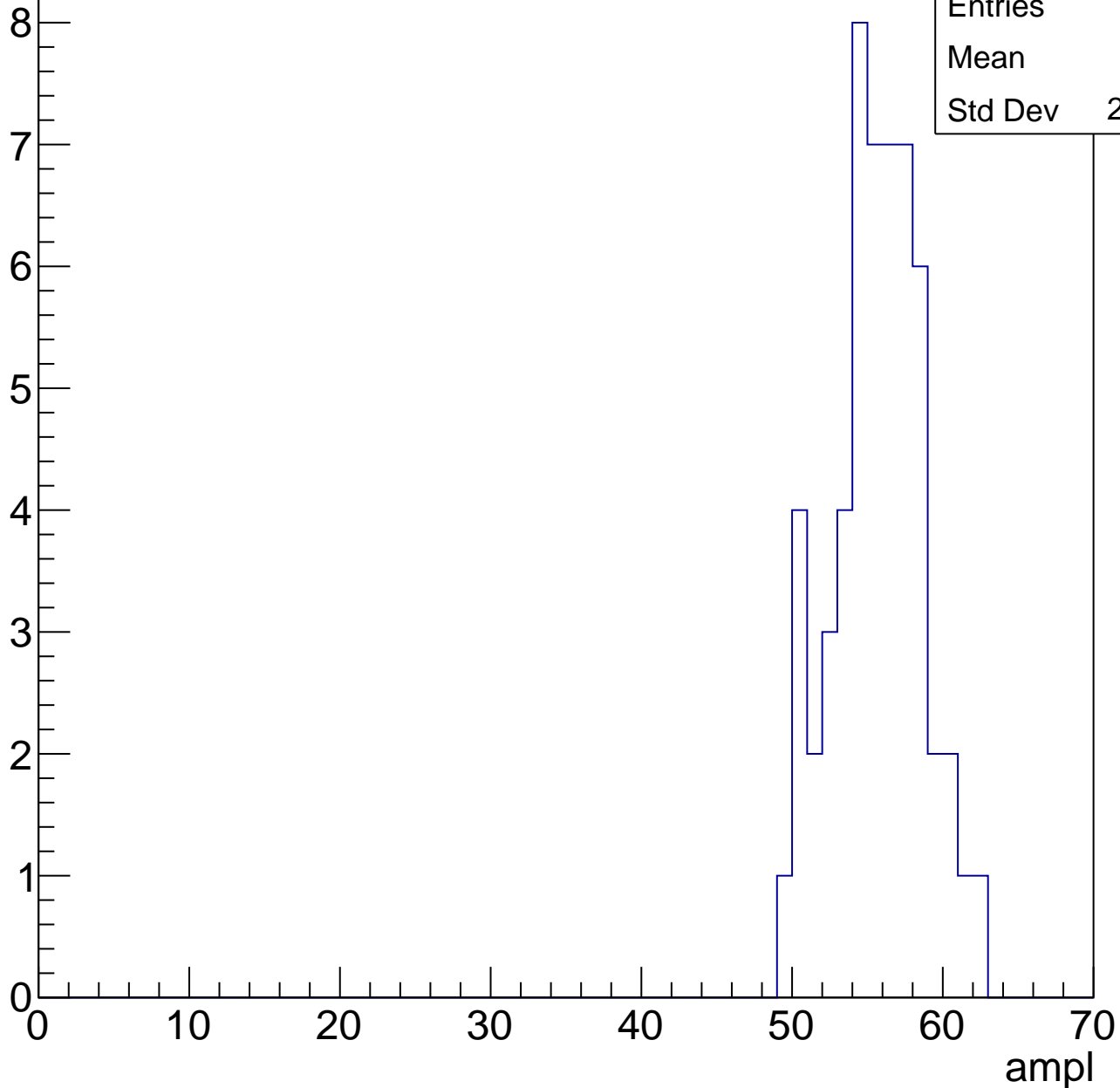


# B0L000S, U19-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.2
Std Dev	2.932



# B0L000S, U19-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

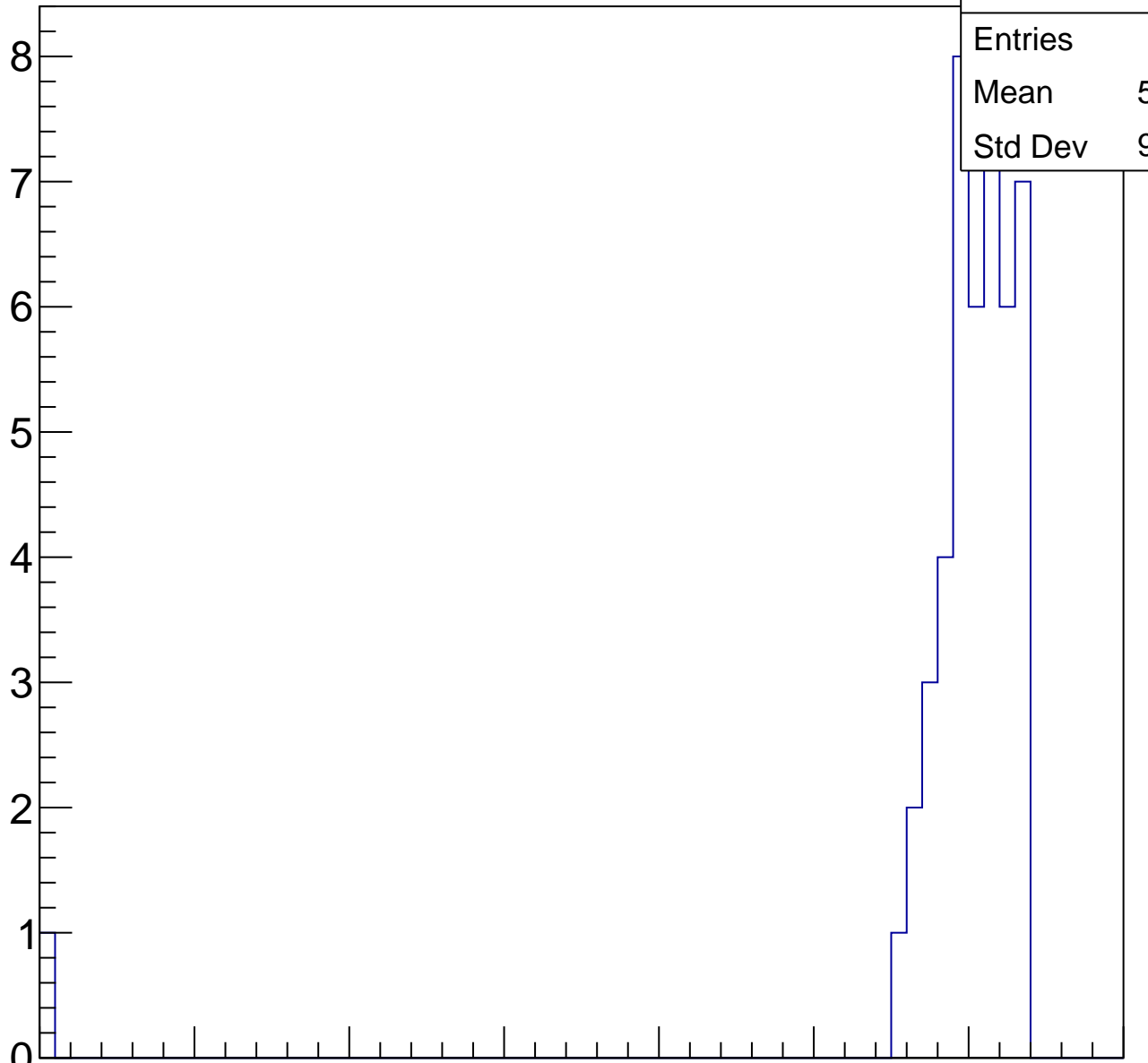
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	58.76
Std Dev	9.008

ampl

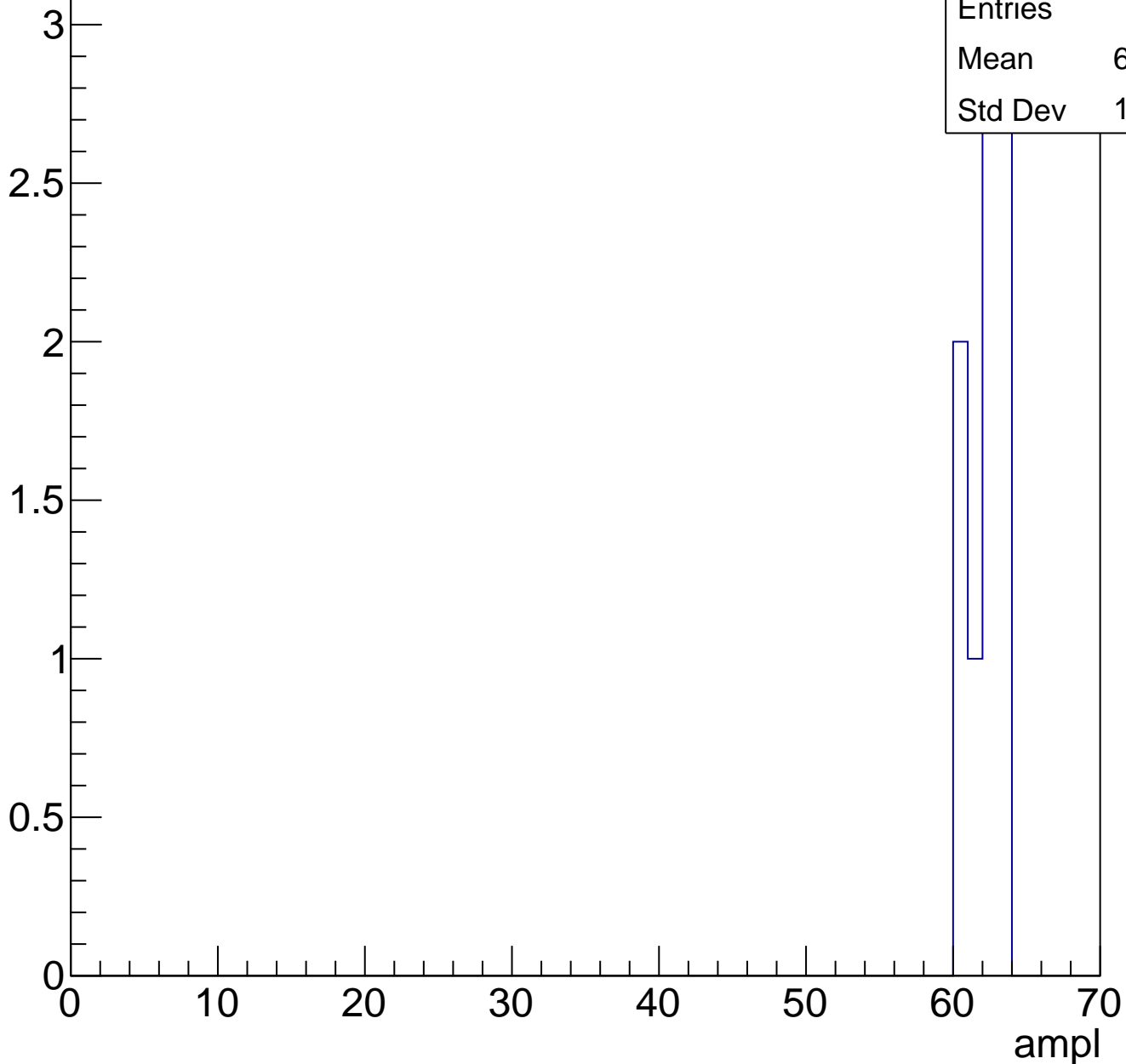
0 10 20 30 40 50 60 70



# B0L000S, U19-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch16, adc0

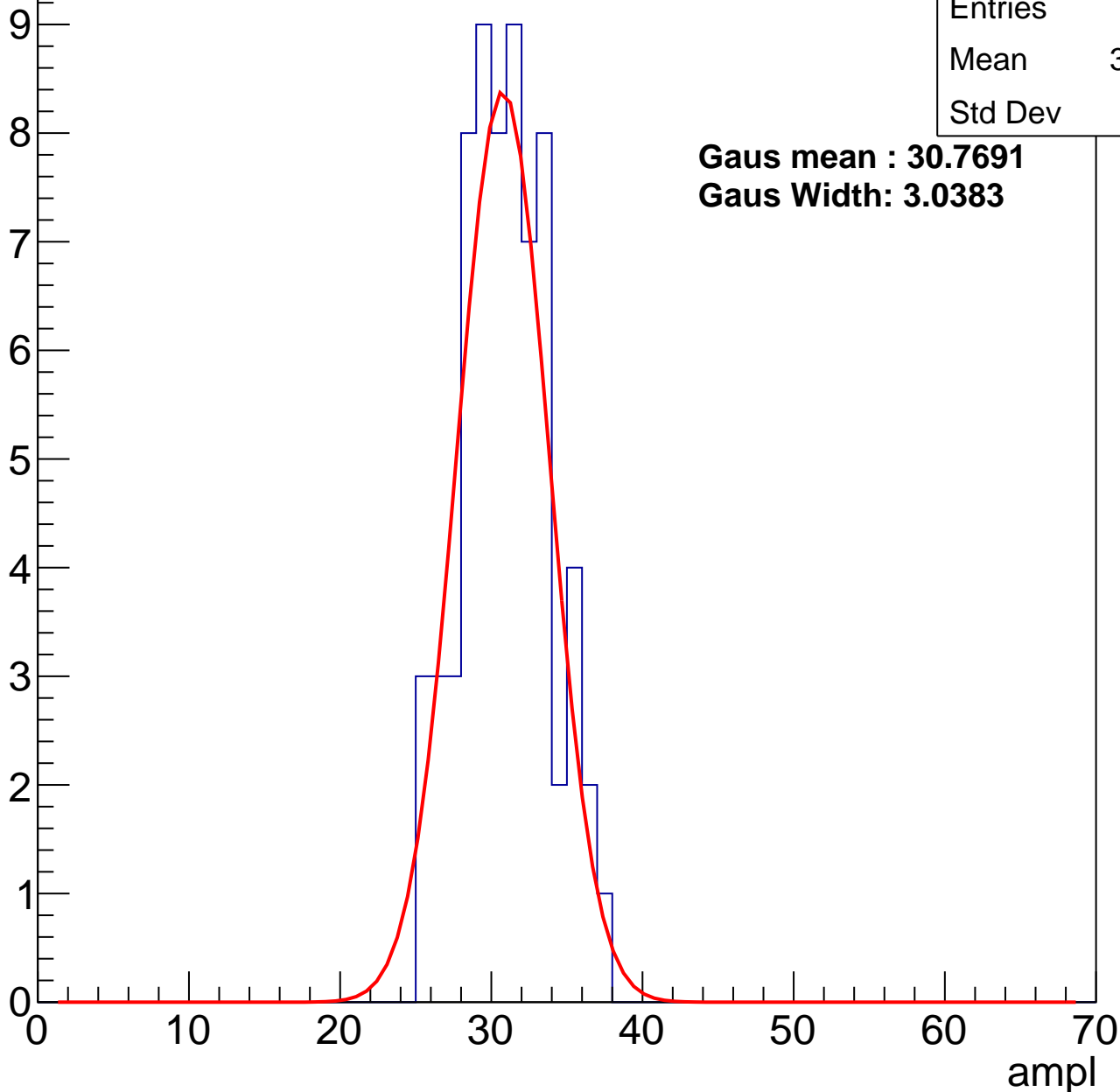
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.49
Std Dev	2.83

**Gaus mean : 30.7691**

**Gaus Width: 3.0383**



# B0L000S, U19-ch16, adc1

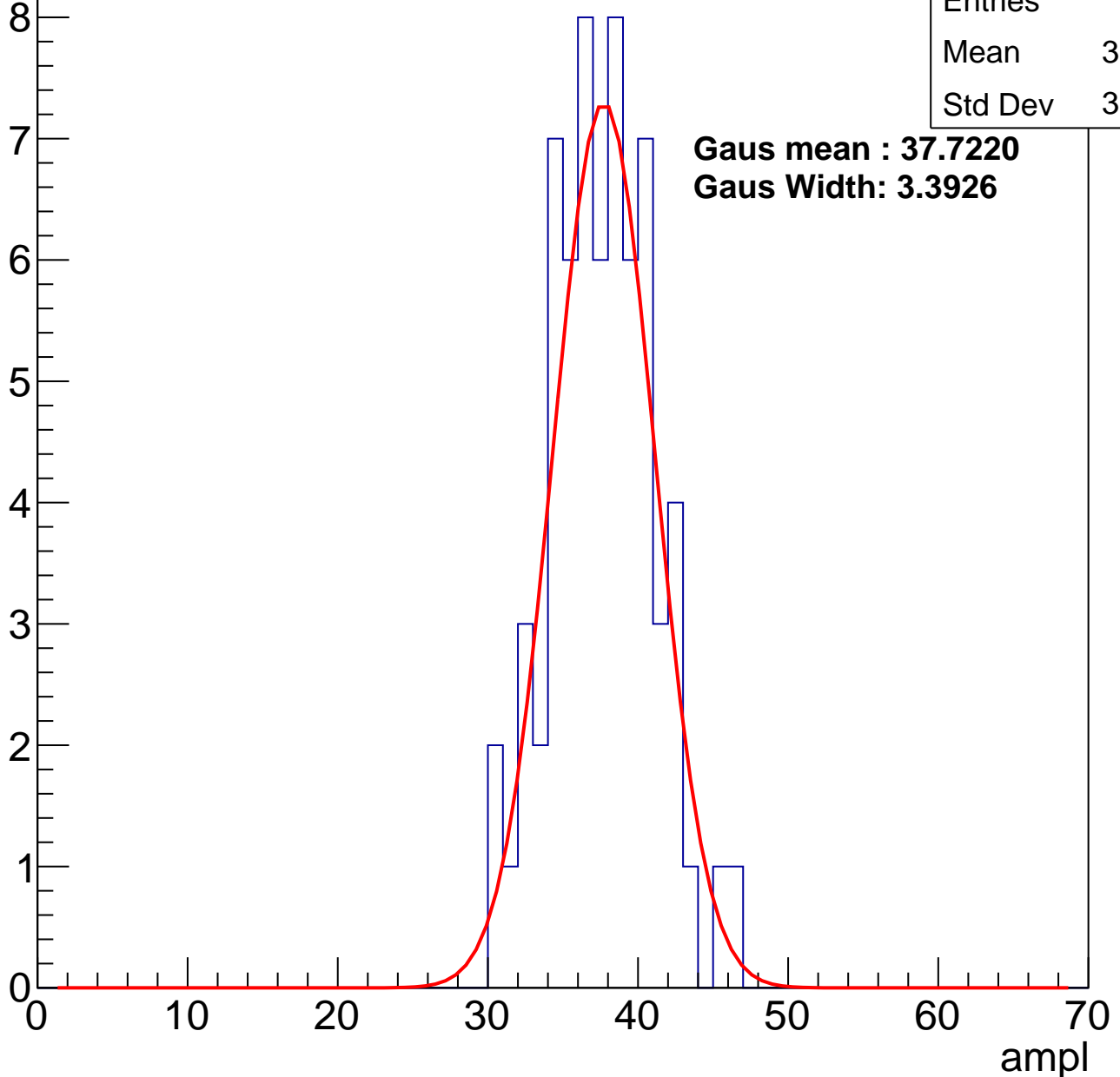
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.18
Std Dev	3.389

**Gaus mean : 37.7220**

**Gaus Width: 3.3926**



# B0L000S, U19-ch16, adc2

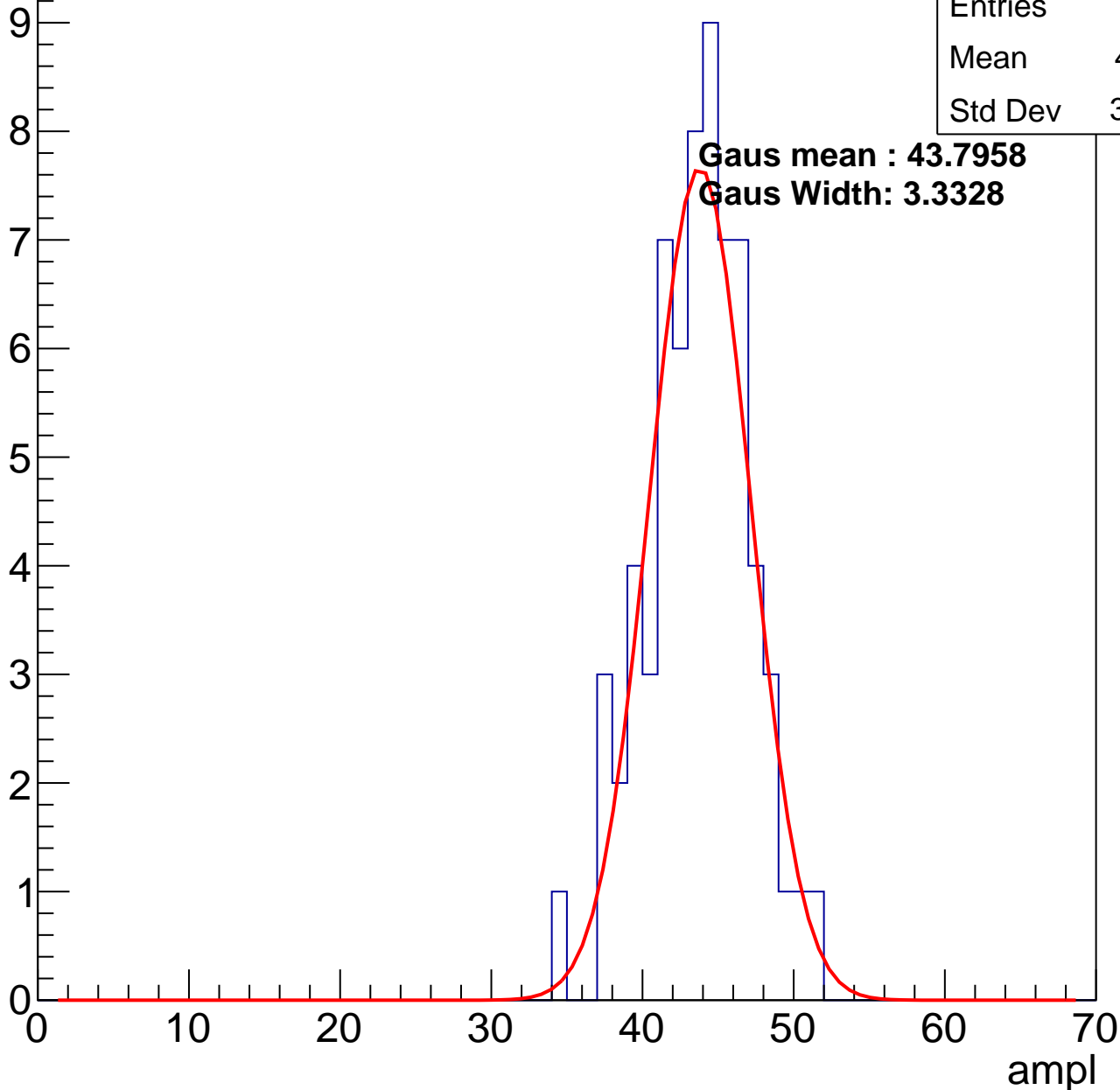
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	43.21
Std Dev	3.344

**Gaus mean : 43.7958**

**Gaus Width: 3.3328**

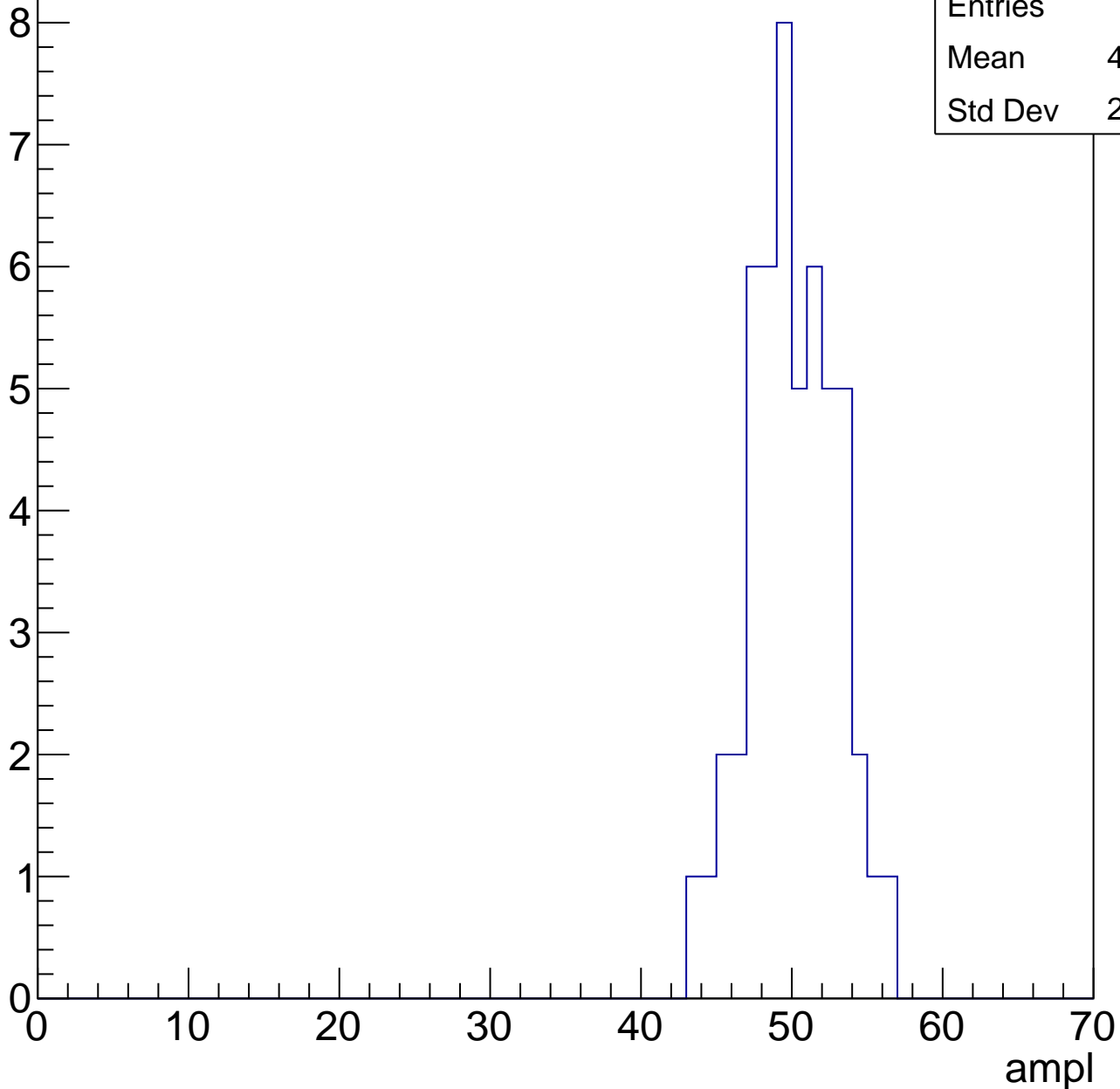


# B0L000S, U19-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

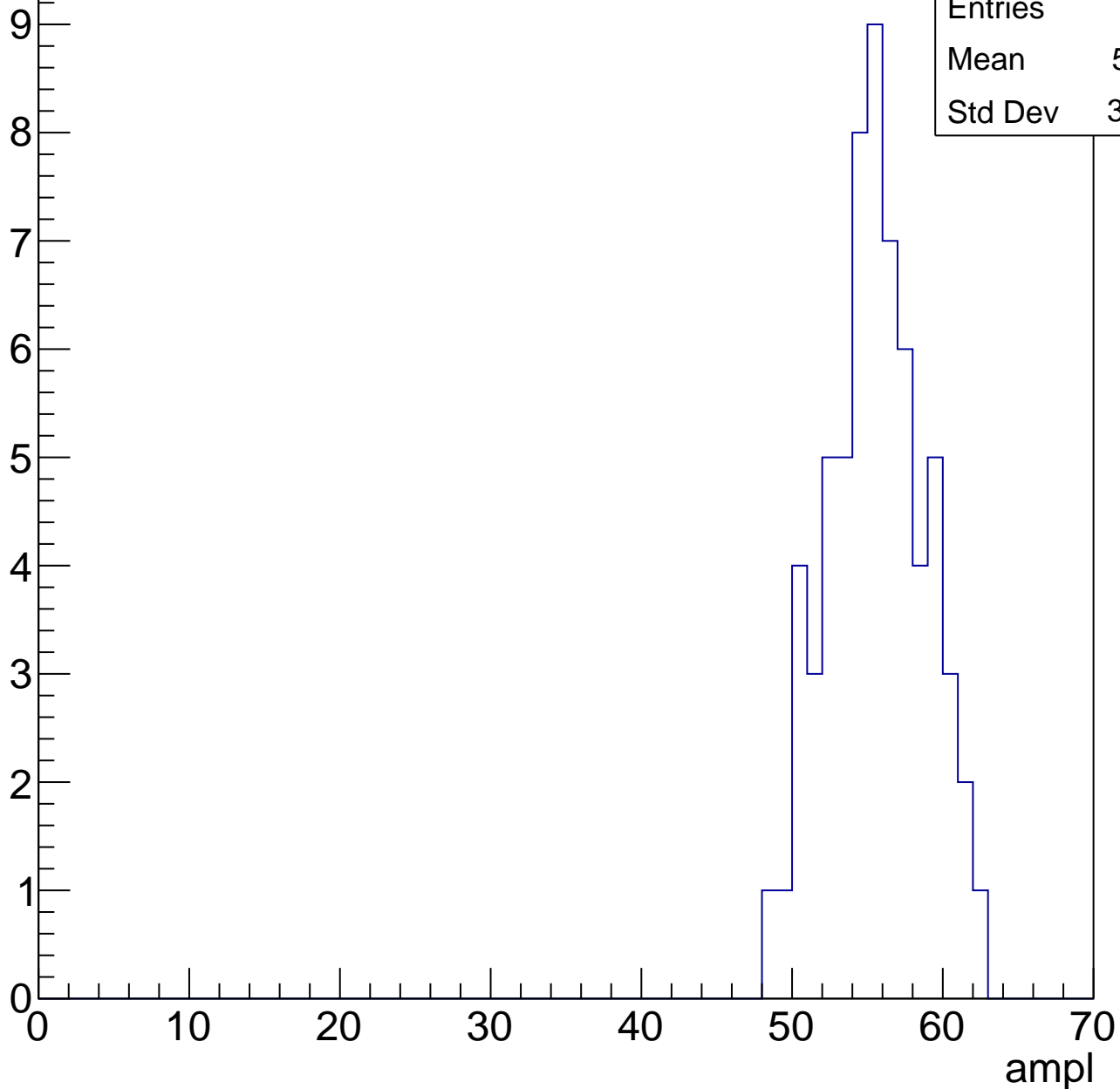
Entries	51
Mean	49.63
Std Dev	2.835



# B0L000S, U19-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

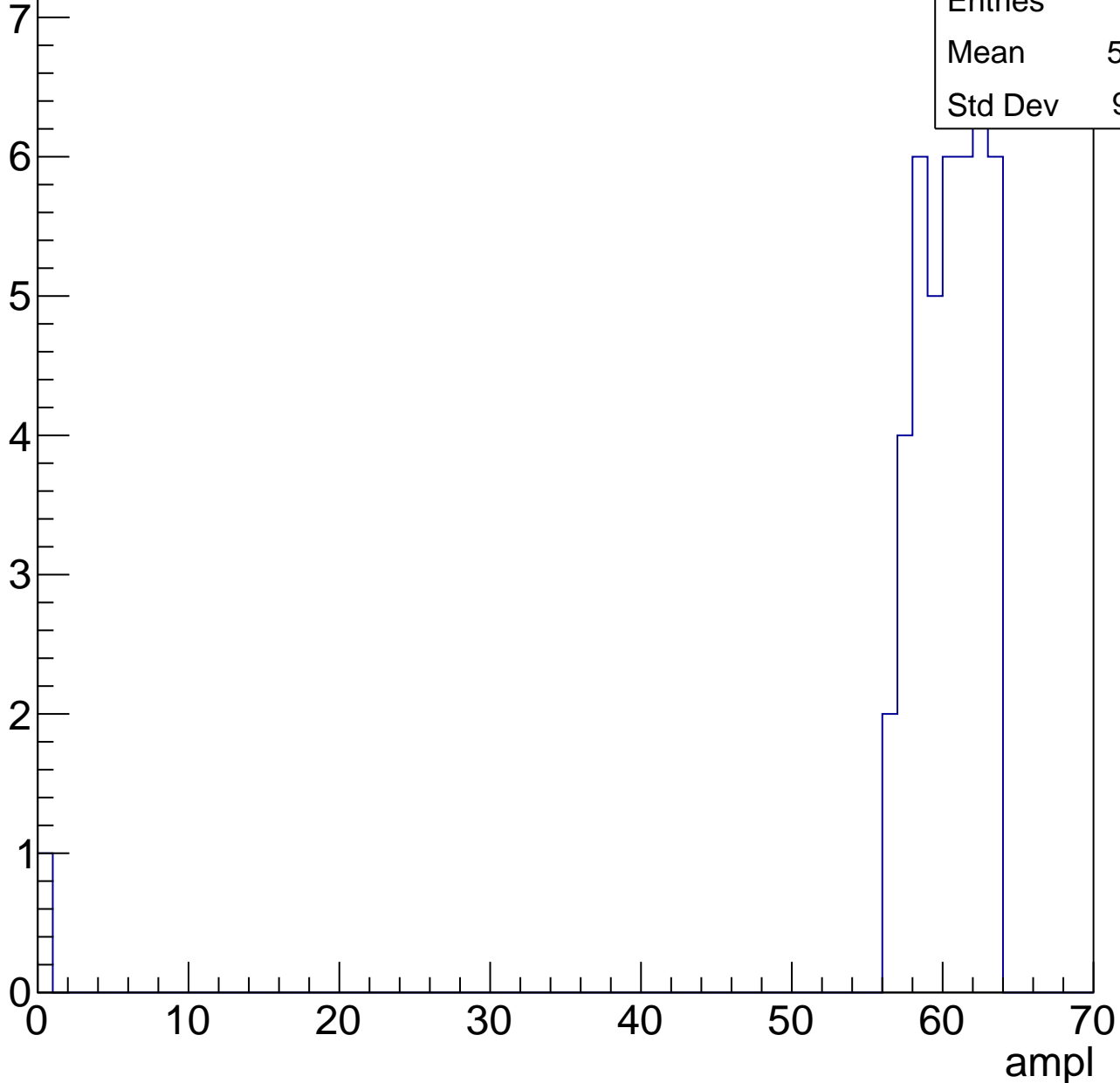


# B0L000S, U19-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

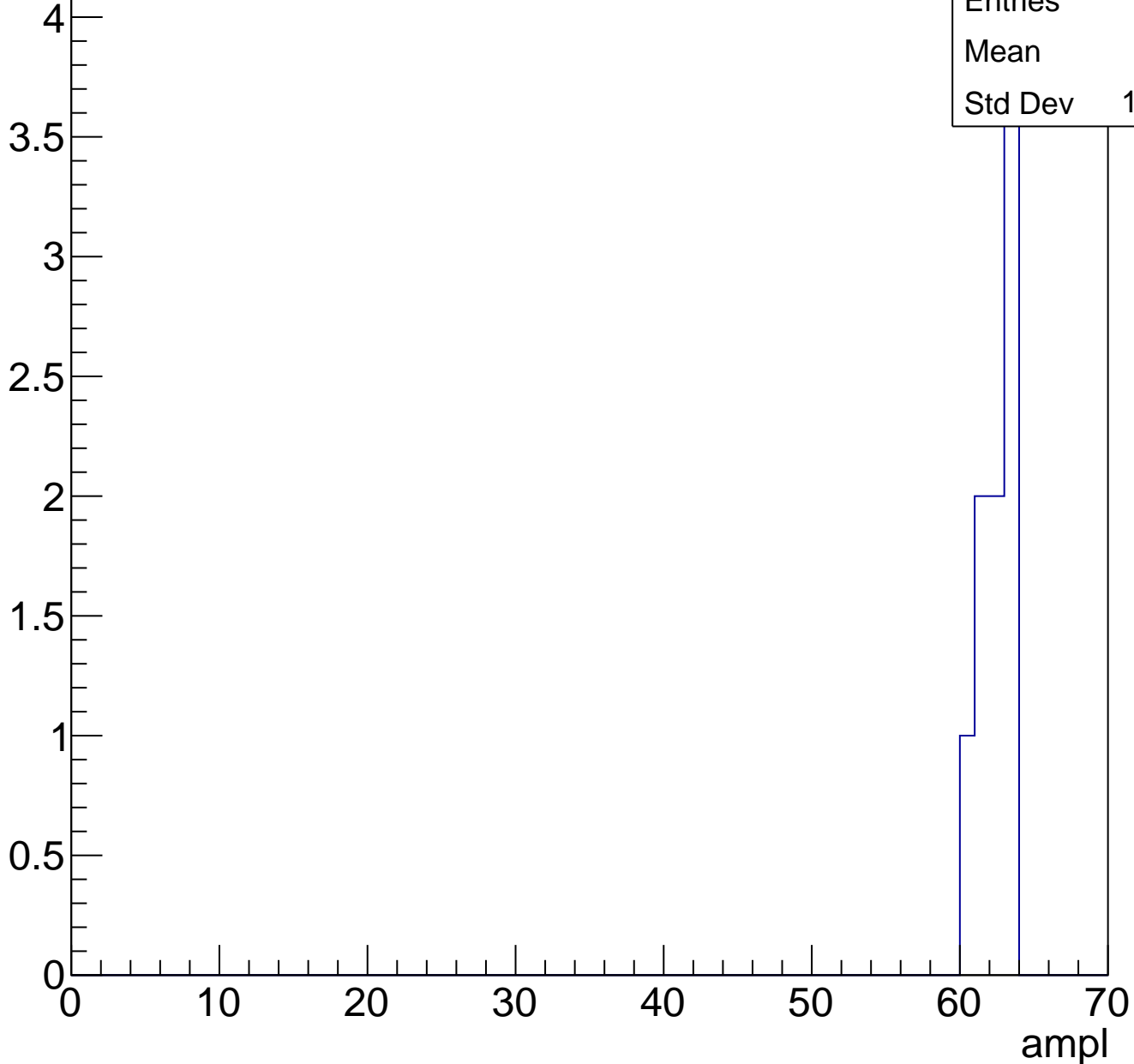
Entries	43
Mean	58.63
Std Dev	9.281



# B0L000S, U19-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl



# B0L000S, U19-ch17, adc0

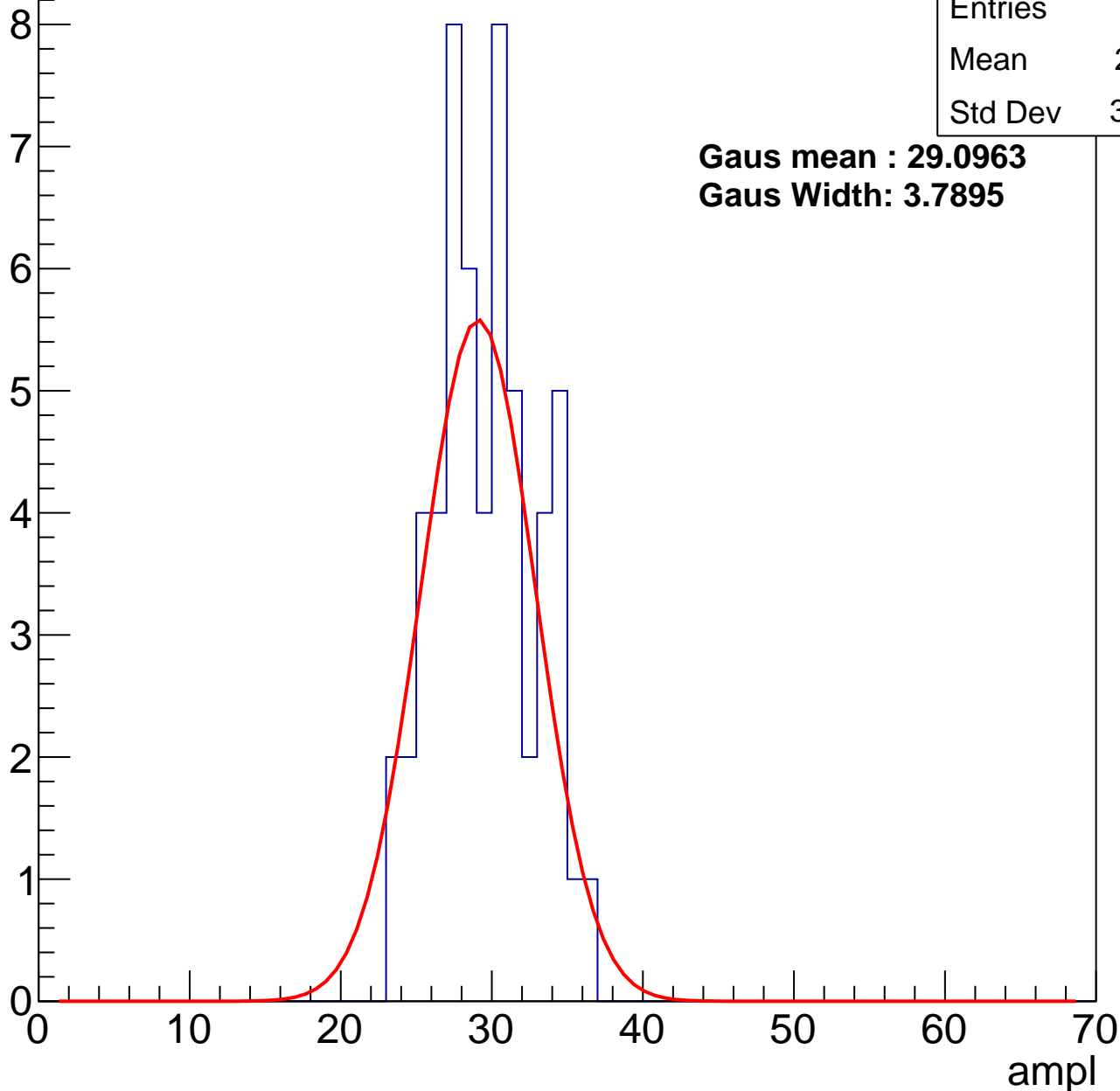
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.11
Std Dev	3.216

**Gaus mean : 29.0963**

**Gaus Width: 3.7895**



# B0L000S, U19-ch17, adc1

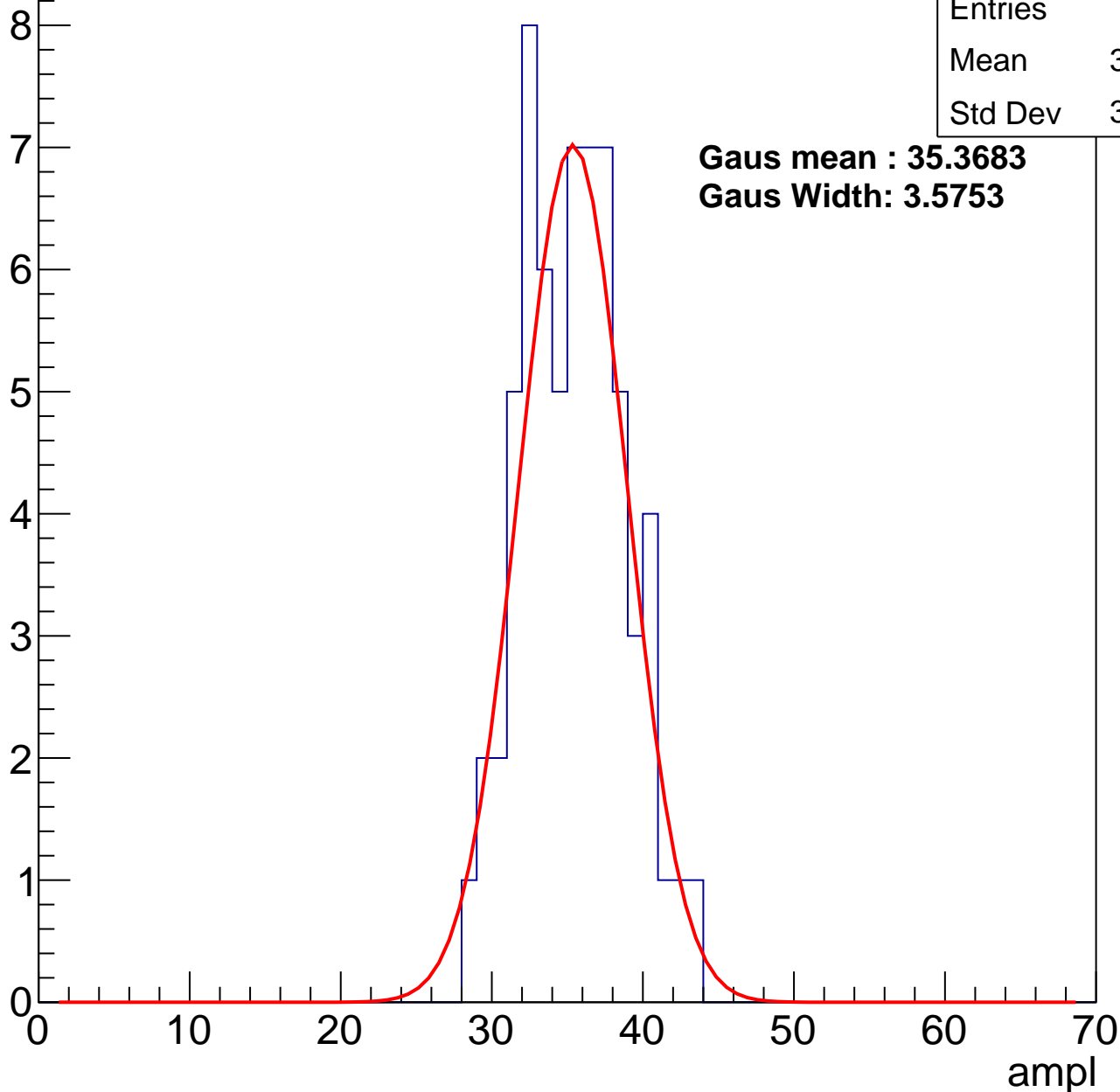
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	34.98
Std Dev	3.349

**Gaus mean : 35.3683**

**Gaus Width: 3.5753**



# B0L000S, U19-ch17, adc2

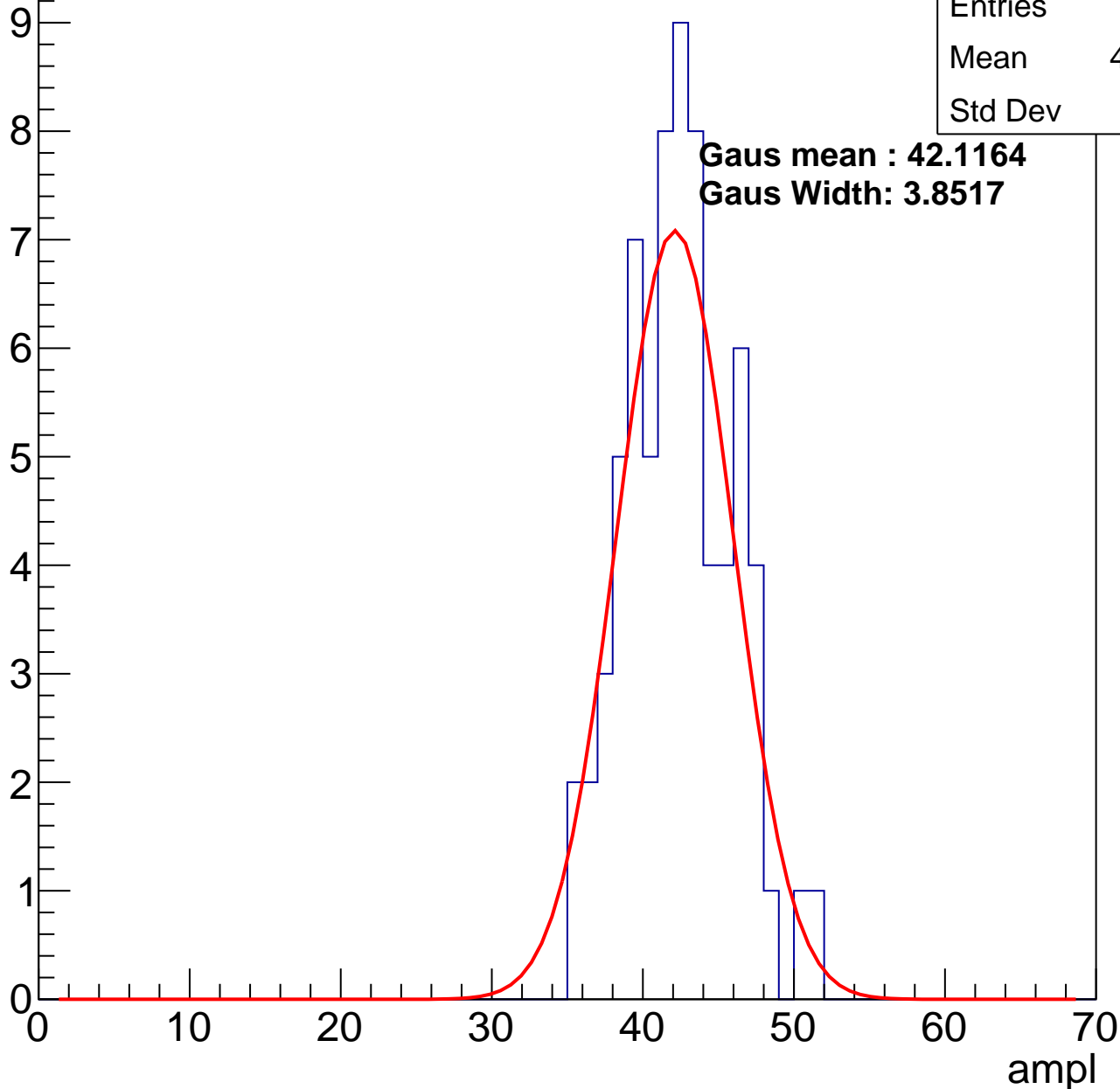
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.93
Std Dev	3.49

**Gaus mean : 42.1164**

**Gaus Width: 3.8517**

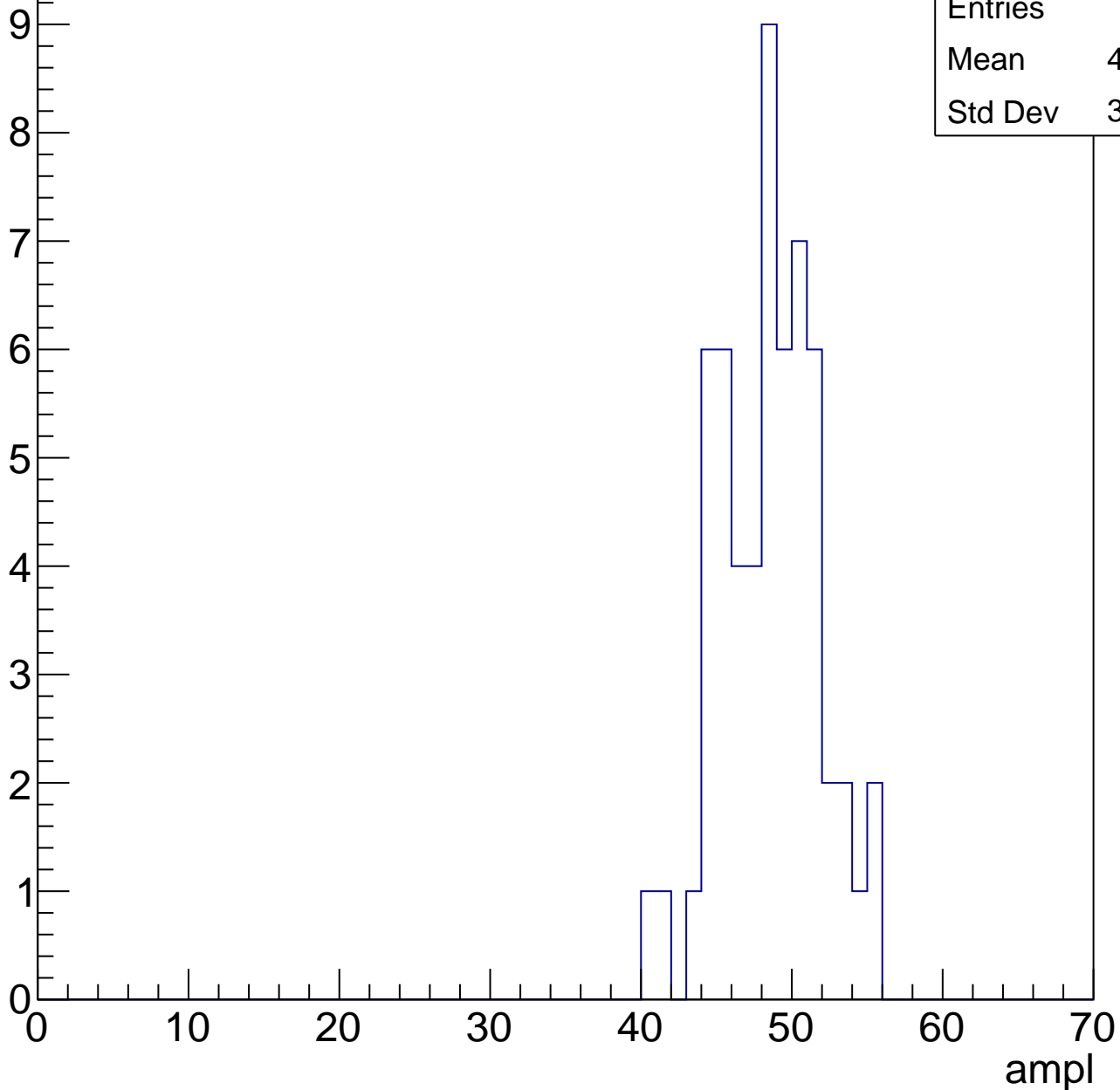


# B0L000S, U19-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	48.03
Std Dev	3.248

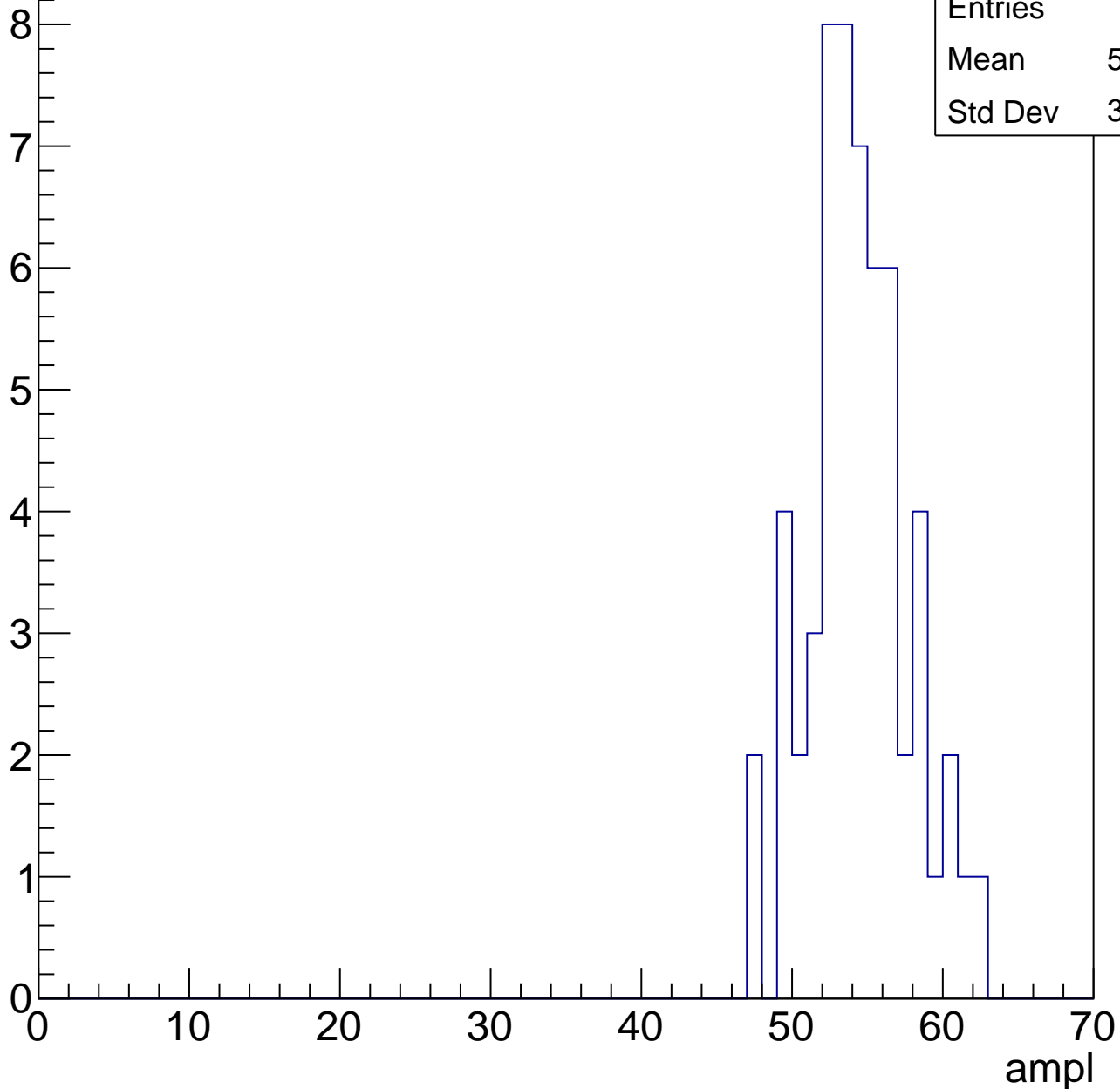


# B0L000S, U19-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

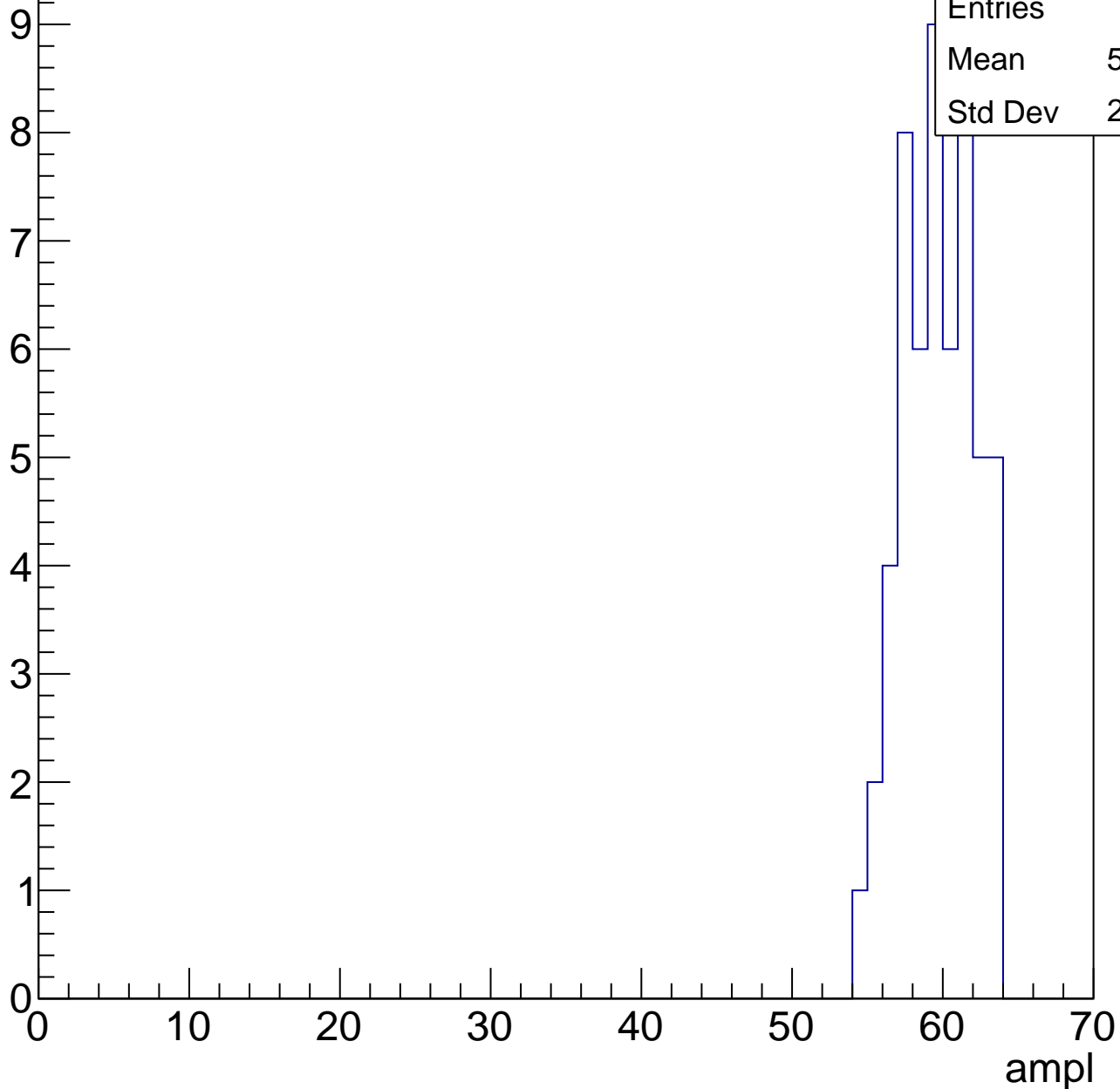
Entries	57
Mean	53.95
Std Dev	3.295



# B0L000S, U19-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



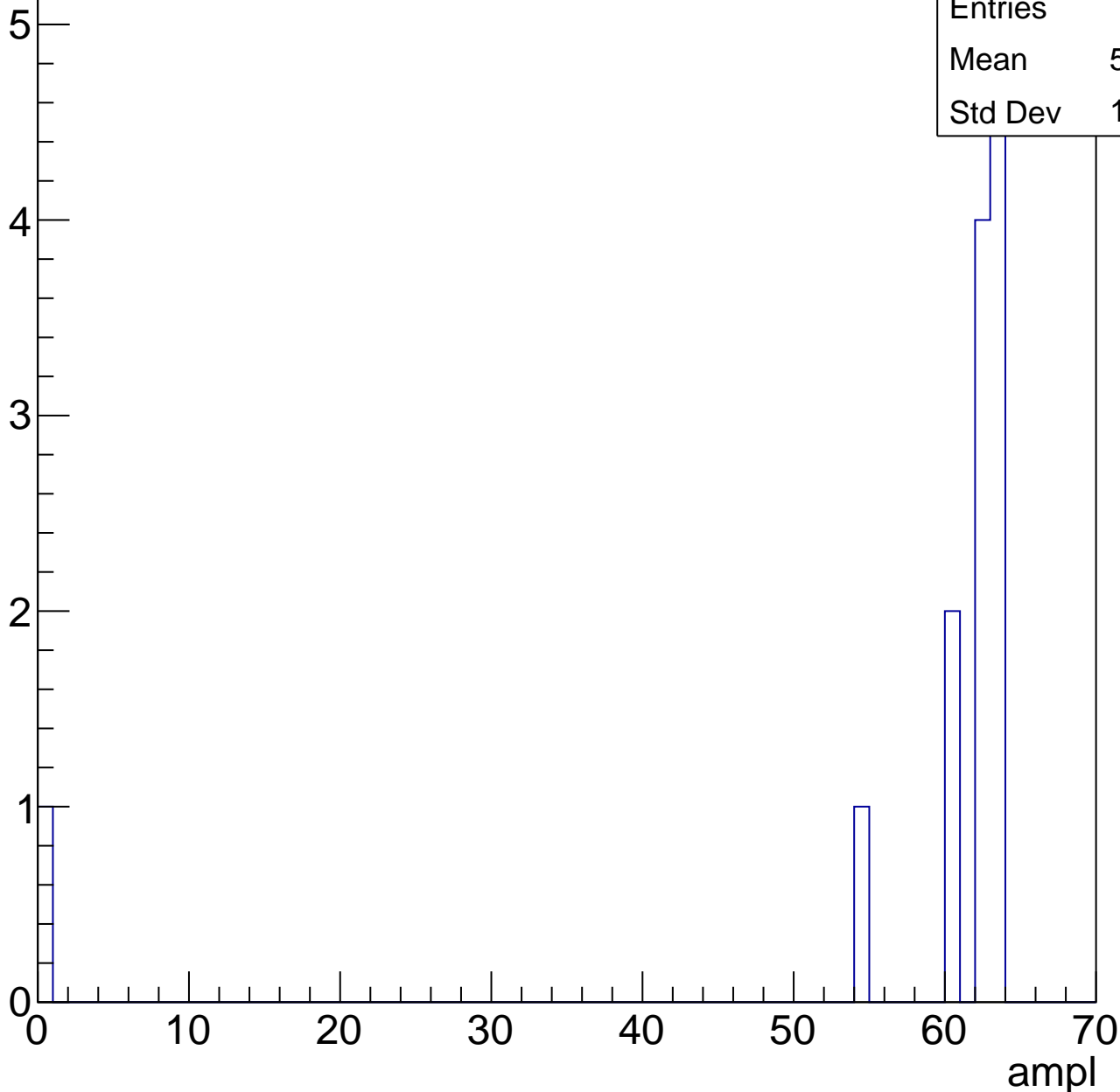
Entries	55
Mean	59.22
Std Dev	2.317

# B0L000S, U19-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	56.69
Std Dev	16.54





# B0L000S, U19-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U19-ch18, adc0

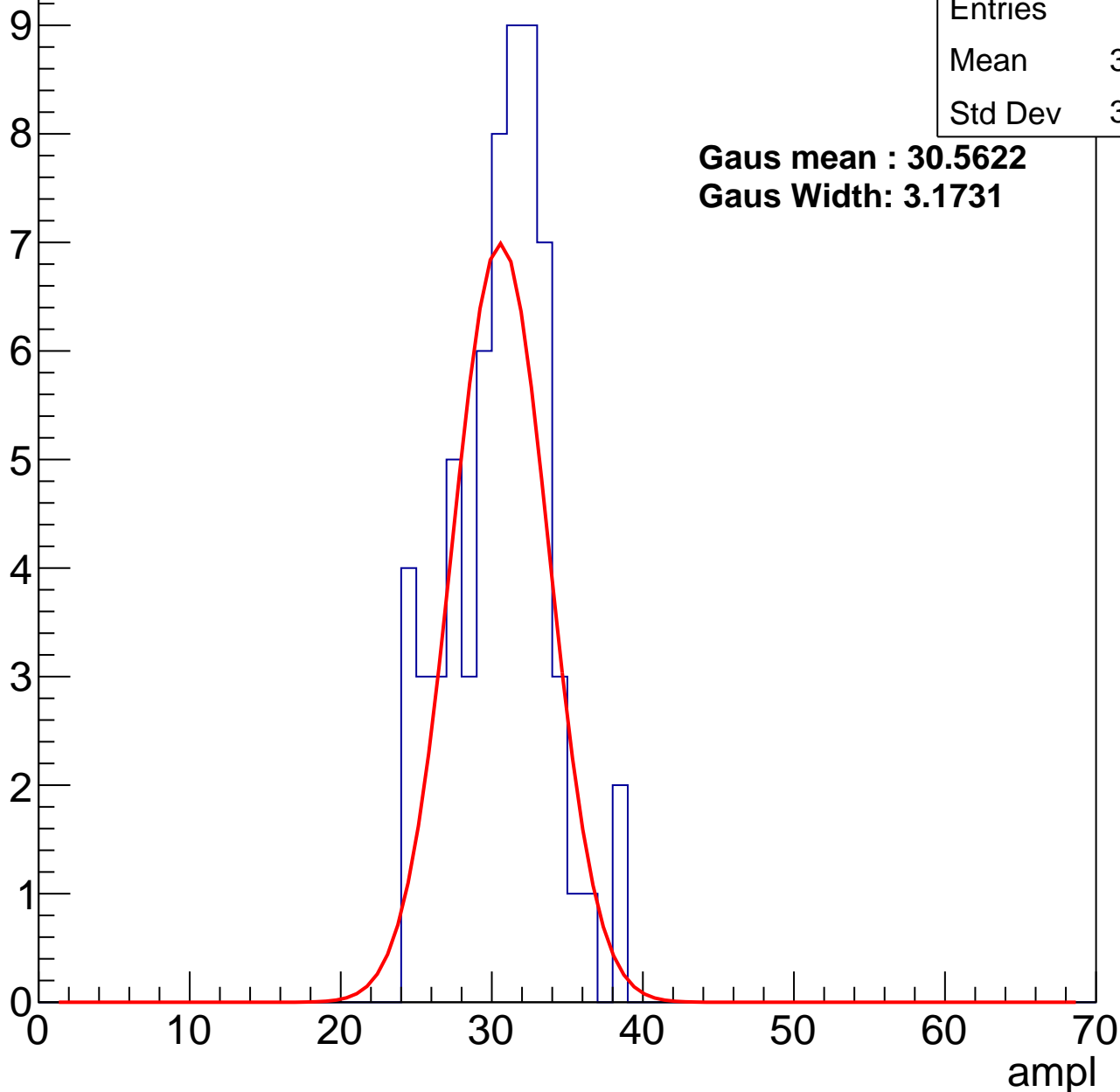
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.14
Std Dev	3.245

**Gaus mean : 30.5622**

**Gaus Width: 3.1731**



# B0L000S, U19-ch18, adc1

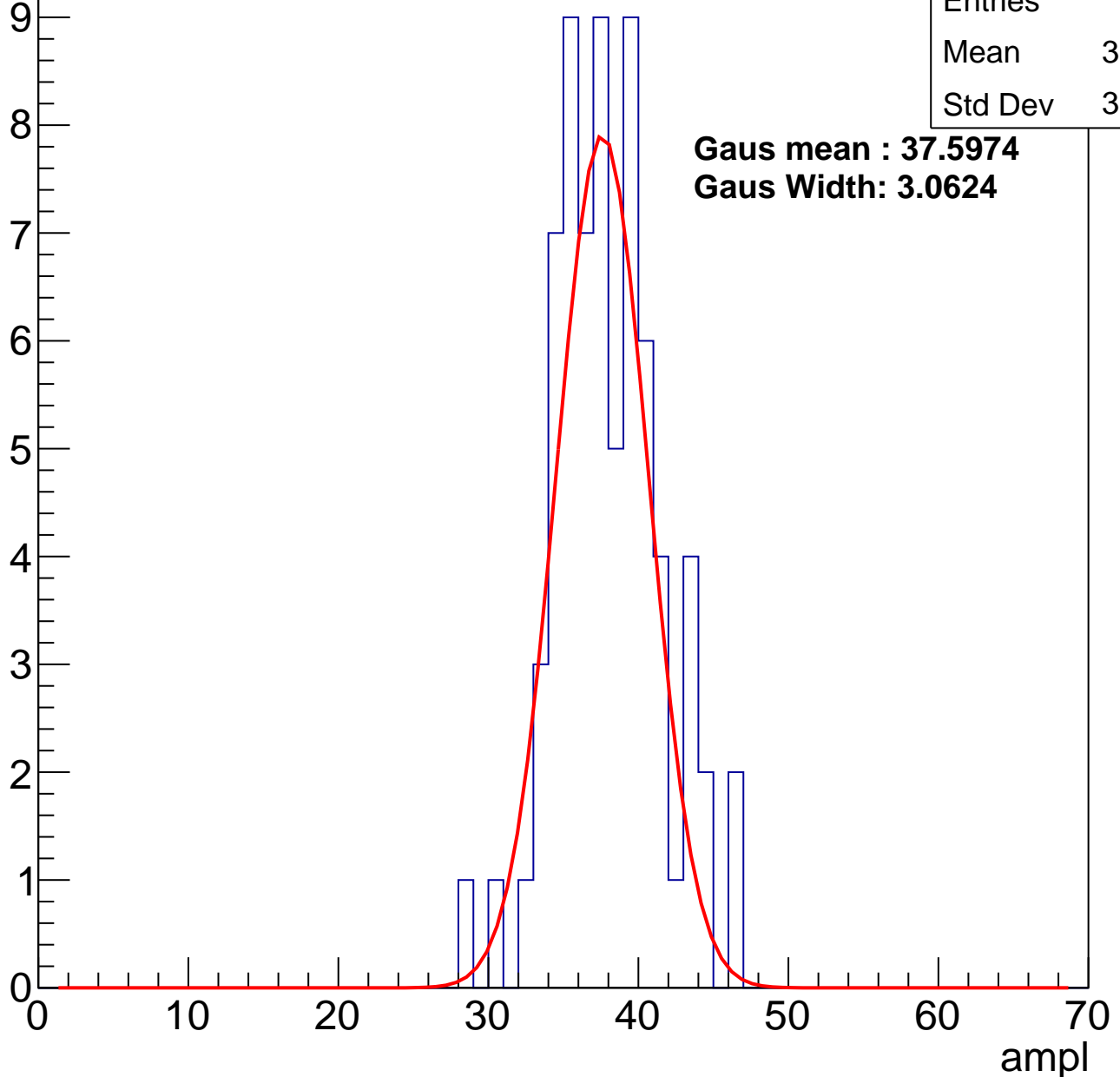
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.55
Std Dev	3.512

**Gaus mean : 37.5974**

**Gaus Width: 3.0624**



# B0L000S, U19-ch18, adc2

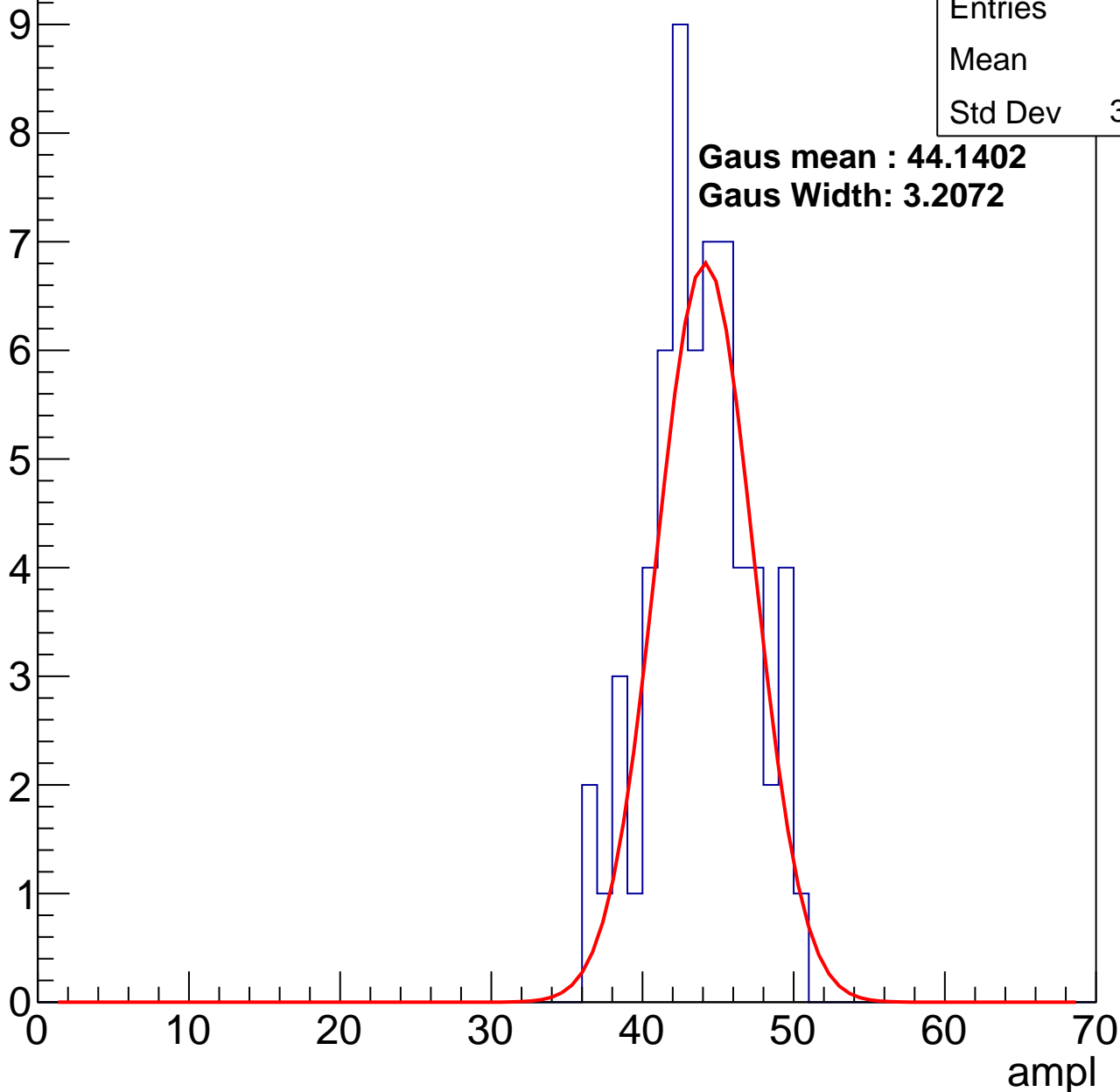
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.3
Std Dev	3.306

**Gaus mean : 44.1402**

**Gaus Width: 3.2072**

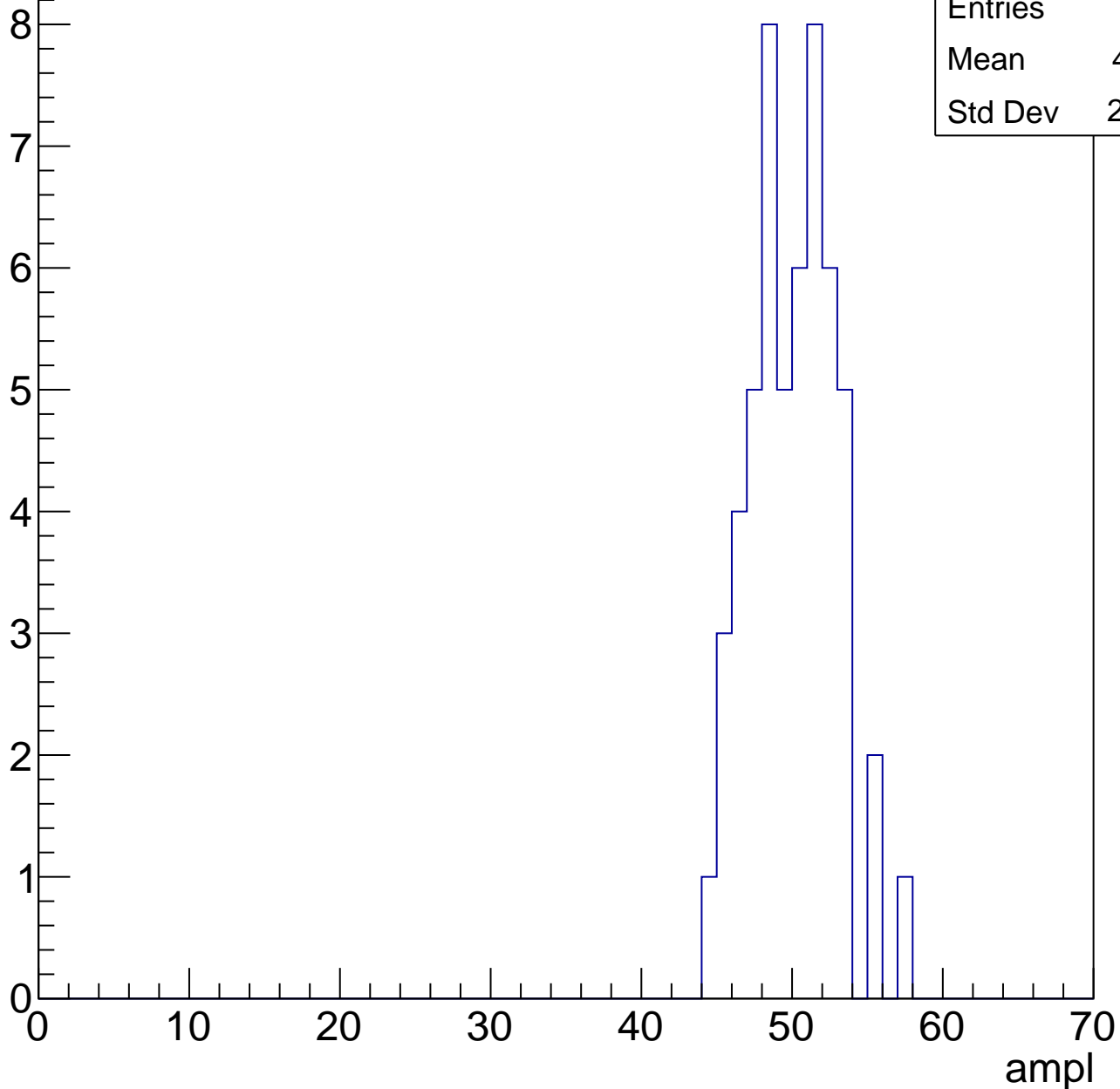


# B0L000S, U19-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	49.61
Std Dev	2.805

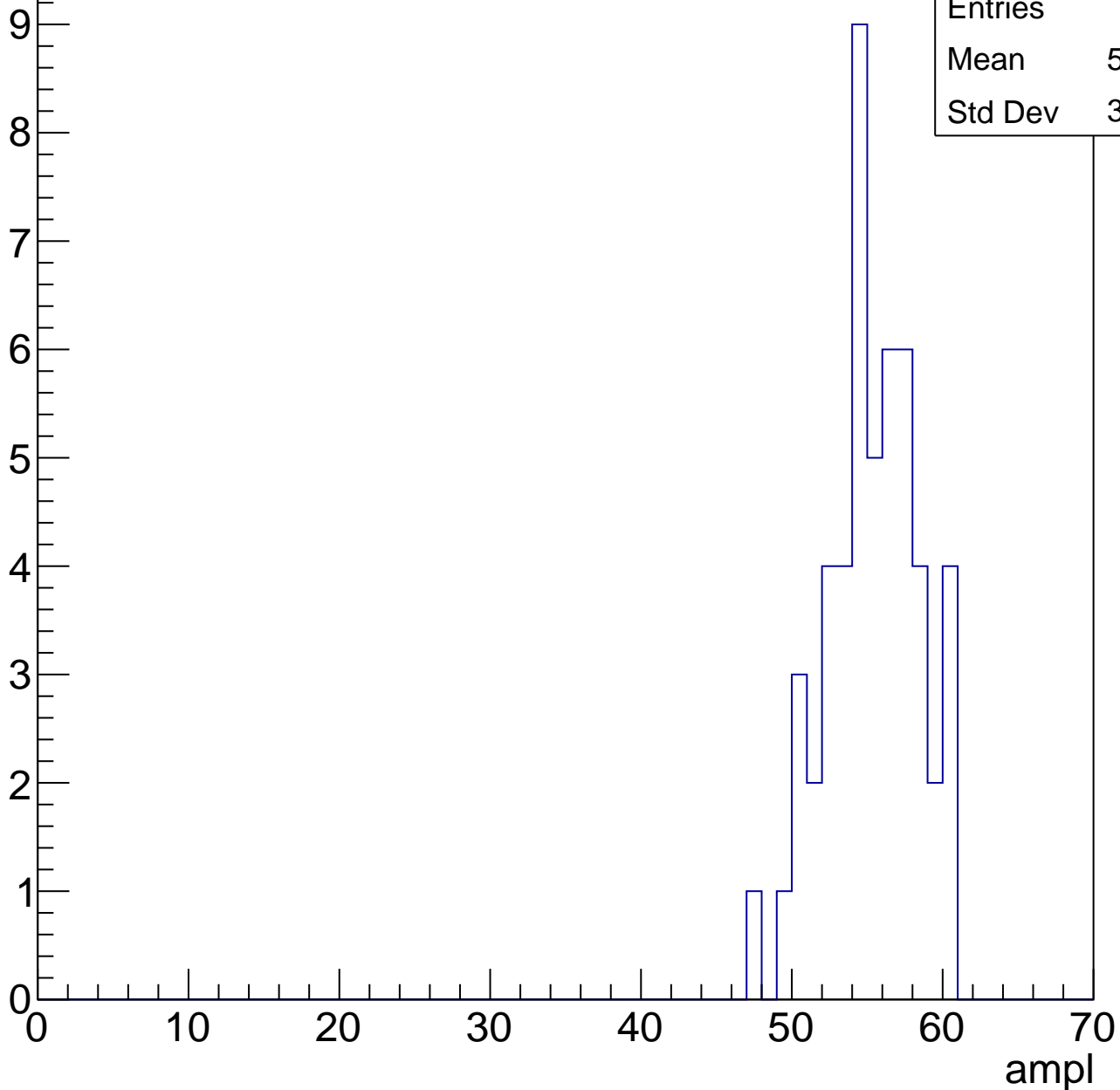


# B0L000S, U19-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	54.84
Std Dev	3.019

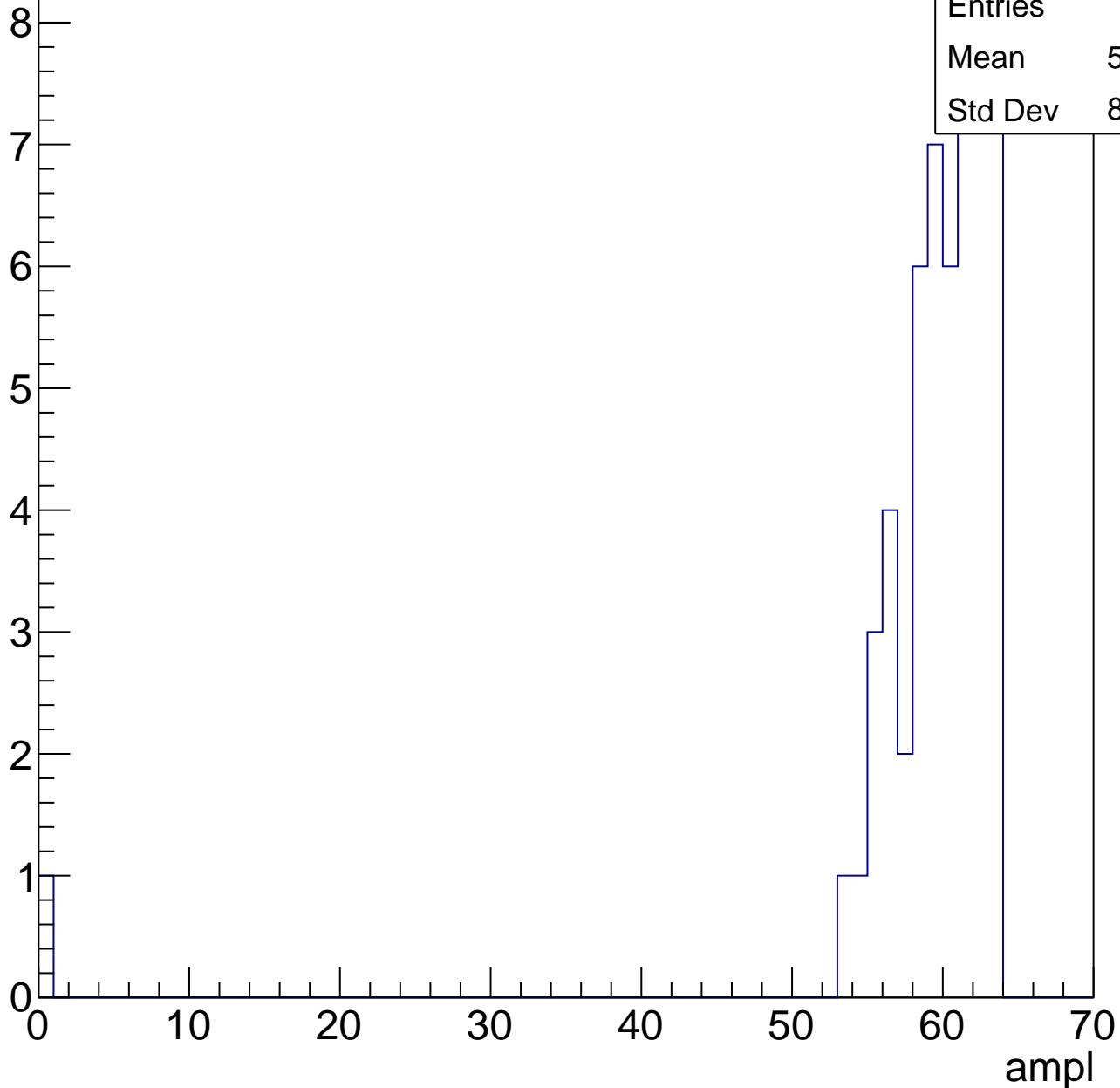


# B0L000S, U19-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

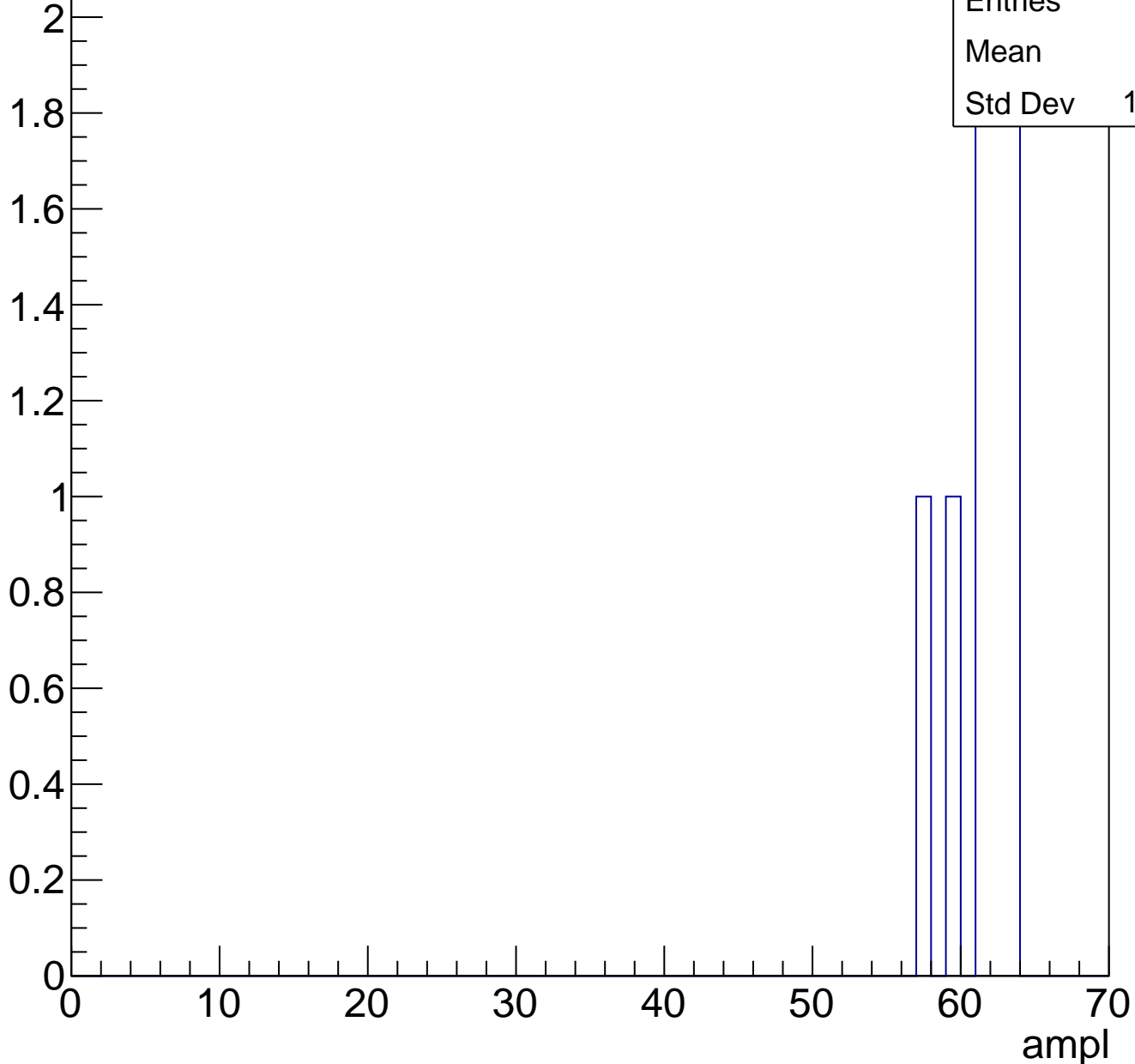
Entries	55
Mean	58.53
Std Dev	8.384



# B0L000S, U19-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

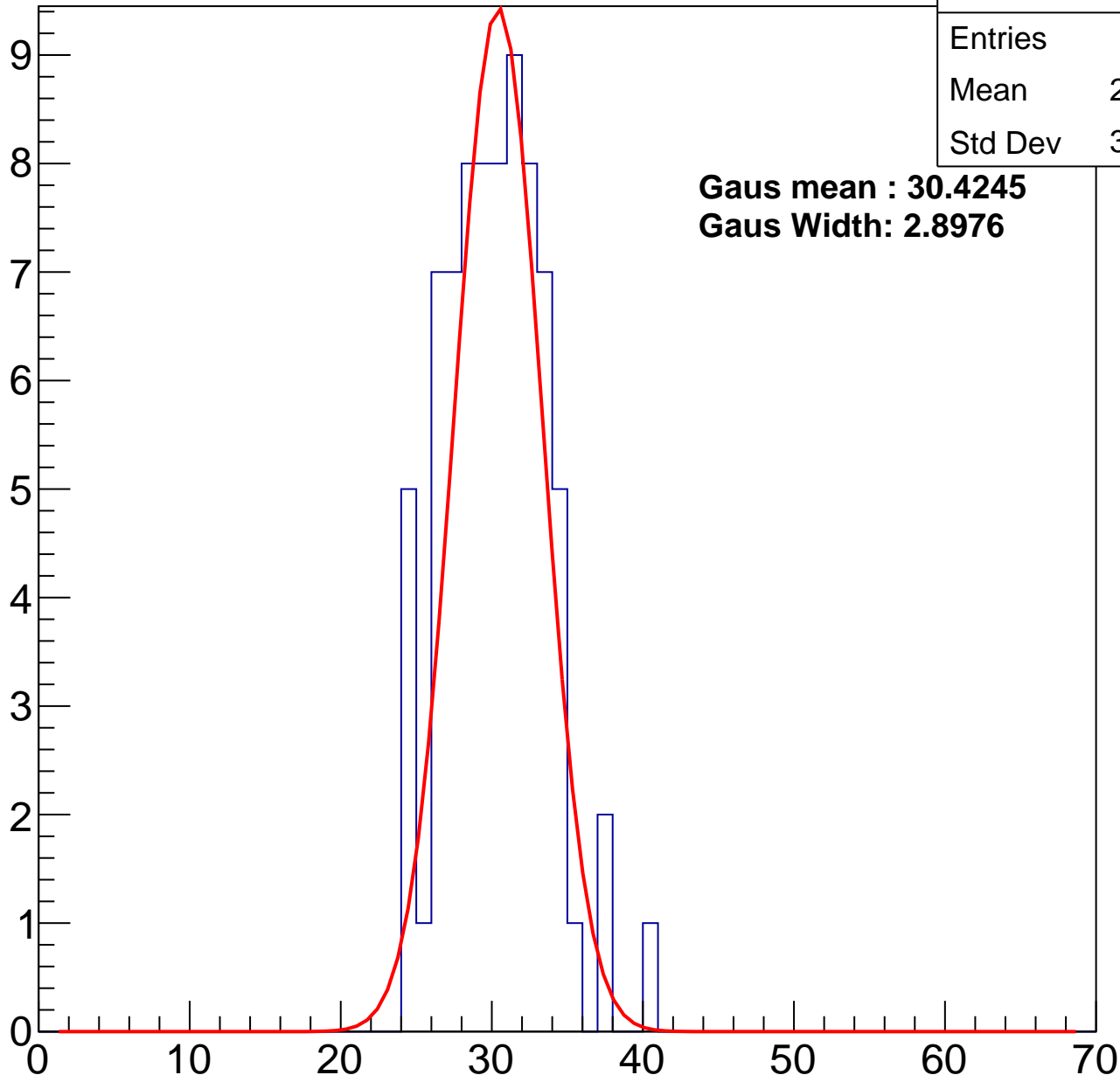
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	77
Mean	29.83
Std Dev	3.273

**Gaus mean : 30.4245**

**Gaus Width: 2.8976**

ampl



# B0L000S, U19-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	36.59
Std Dev	2.871

**Gaus mean : 37.1028**

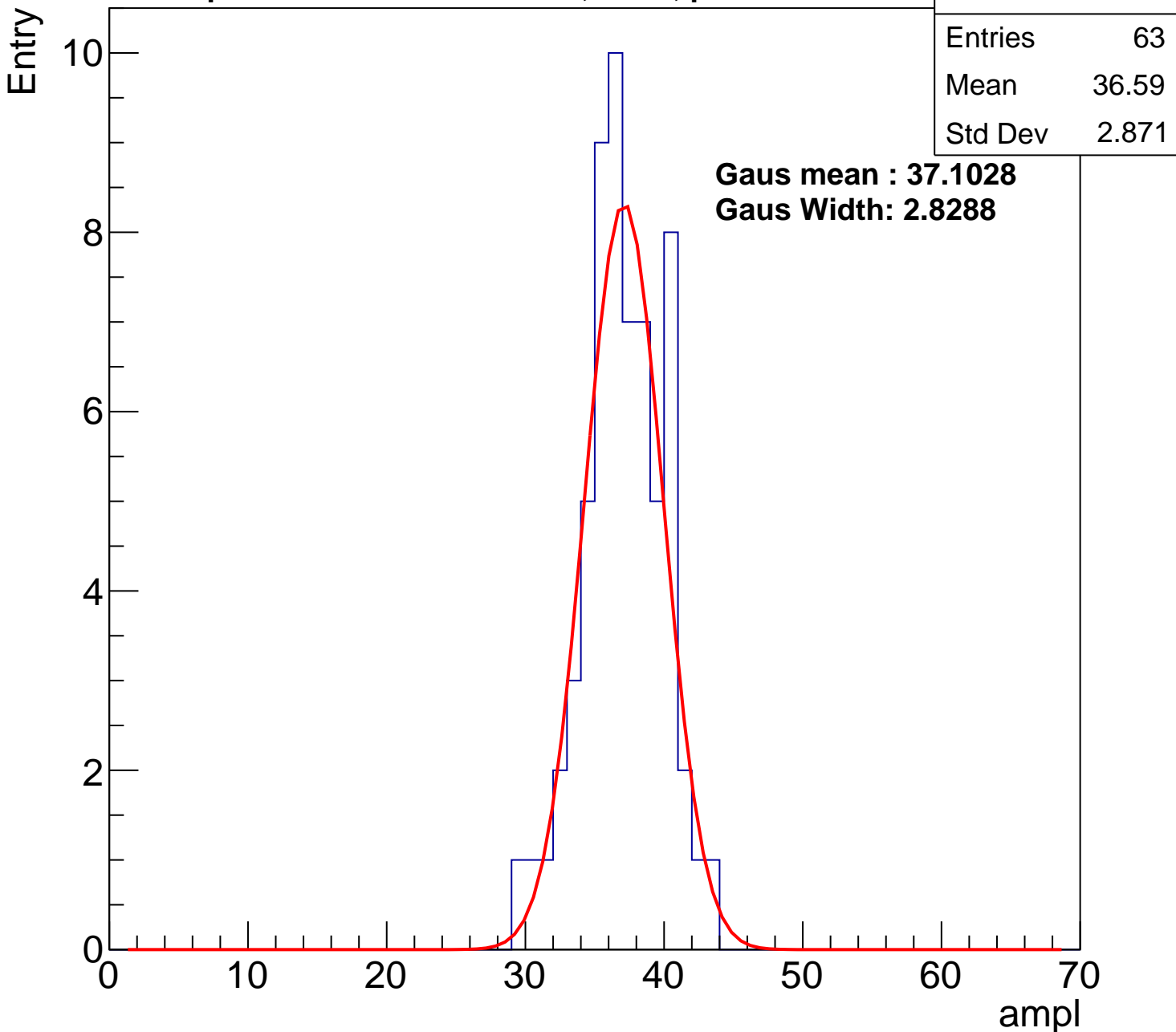
**Gaus Width: 2.8288**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch19, adc2

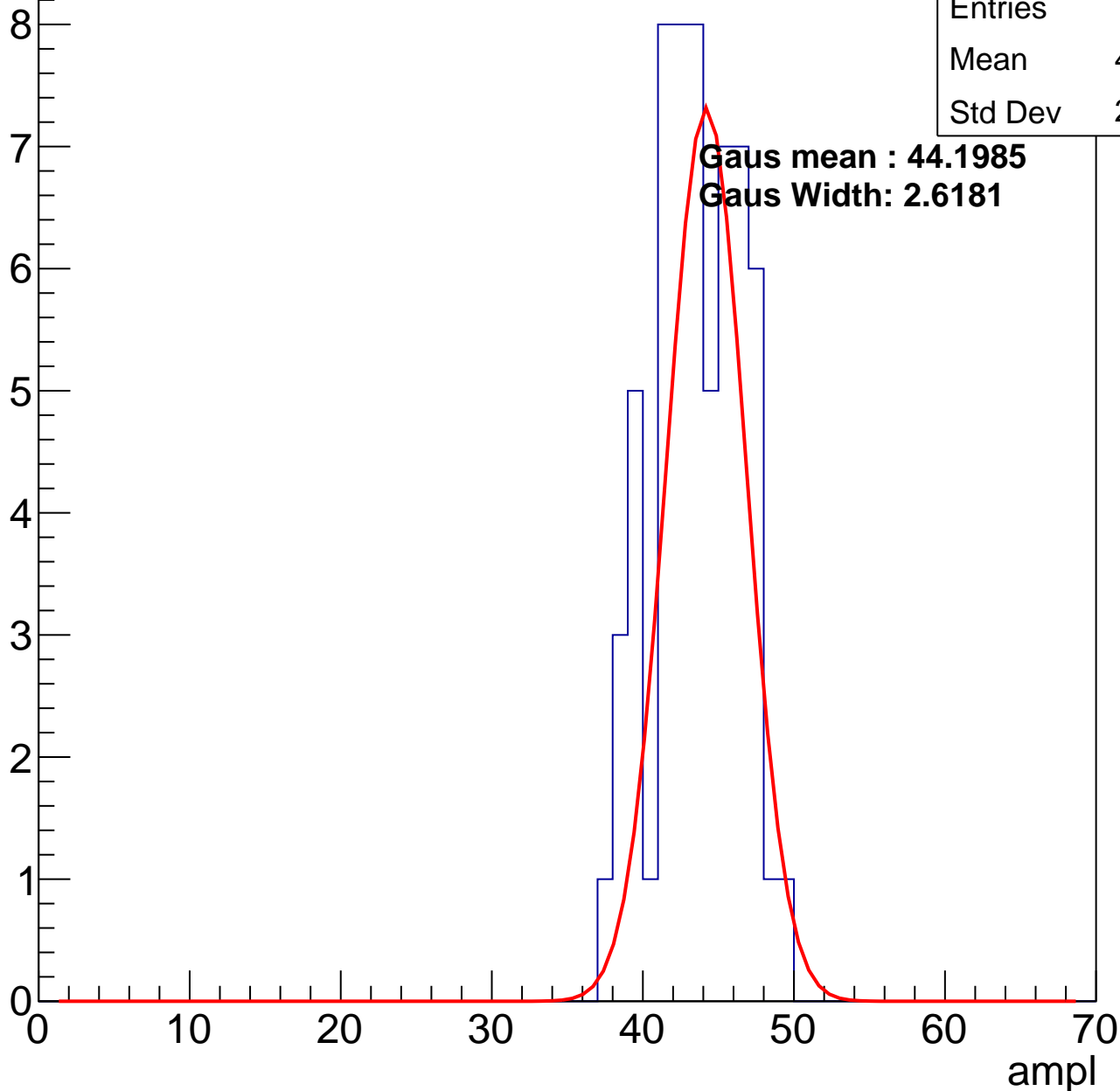
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.11
Std Dev	2.841

**Gaus mean : 44.1985**

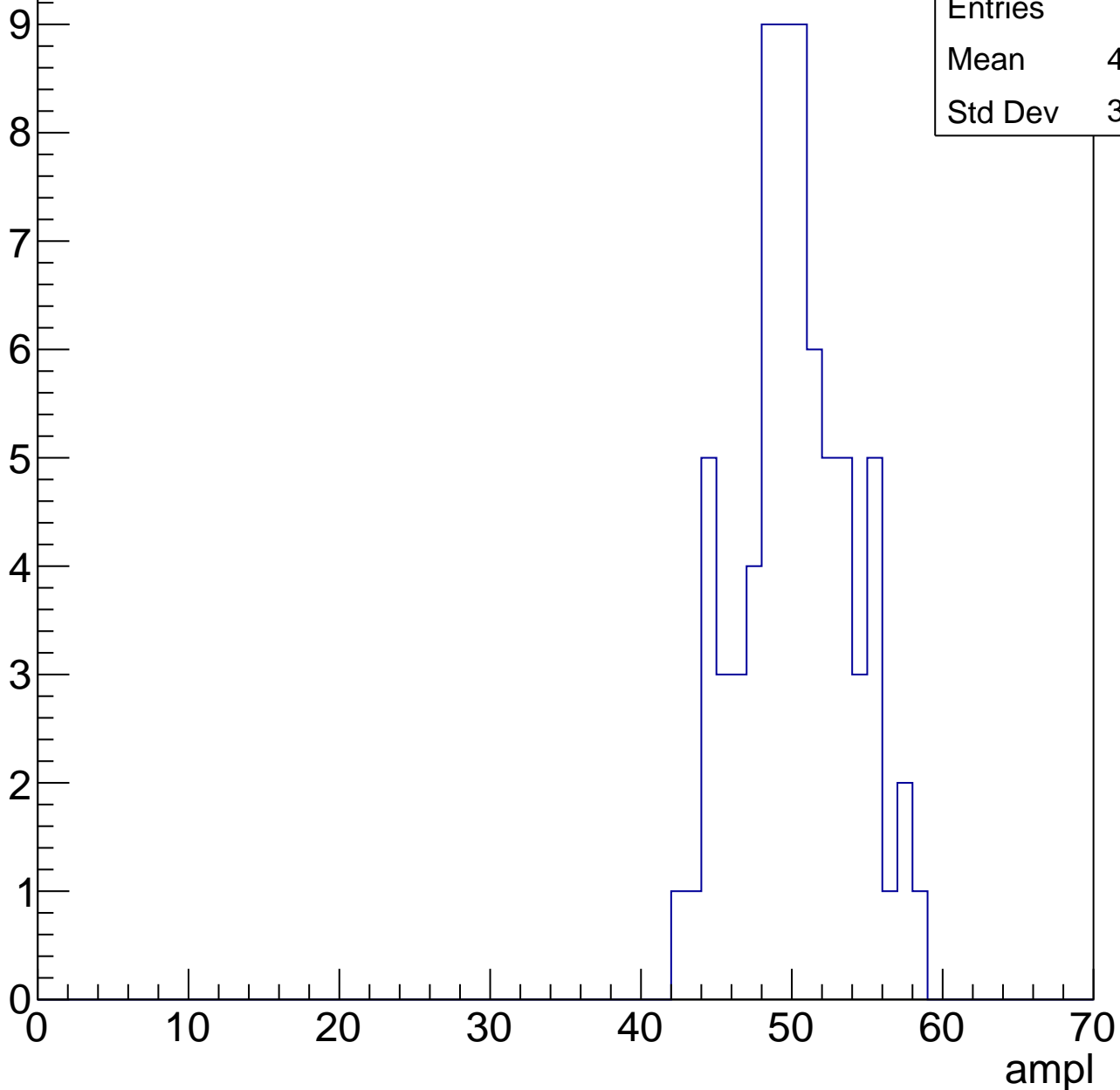
**Gaus Width: 2.6181**



# B0L000S, U19-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



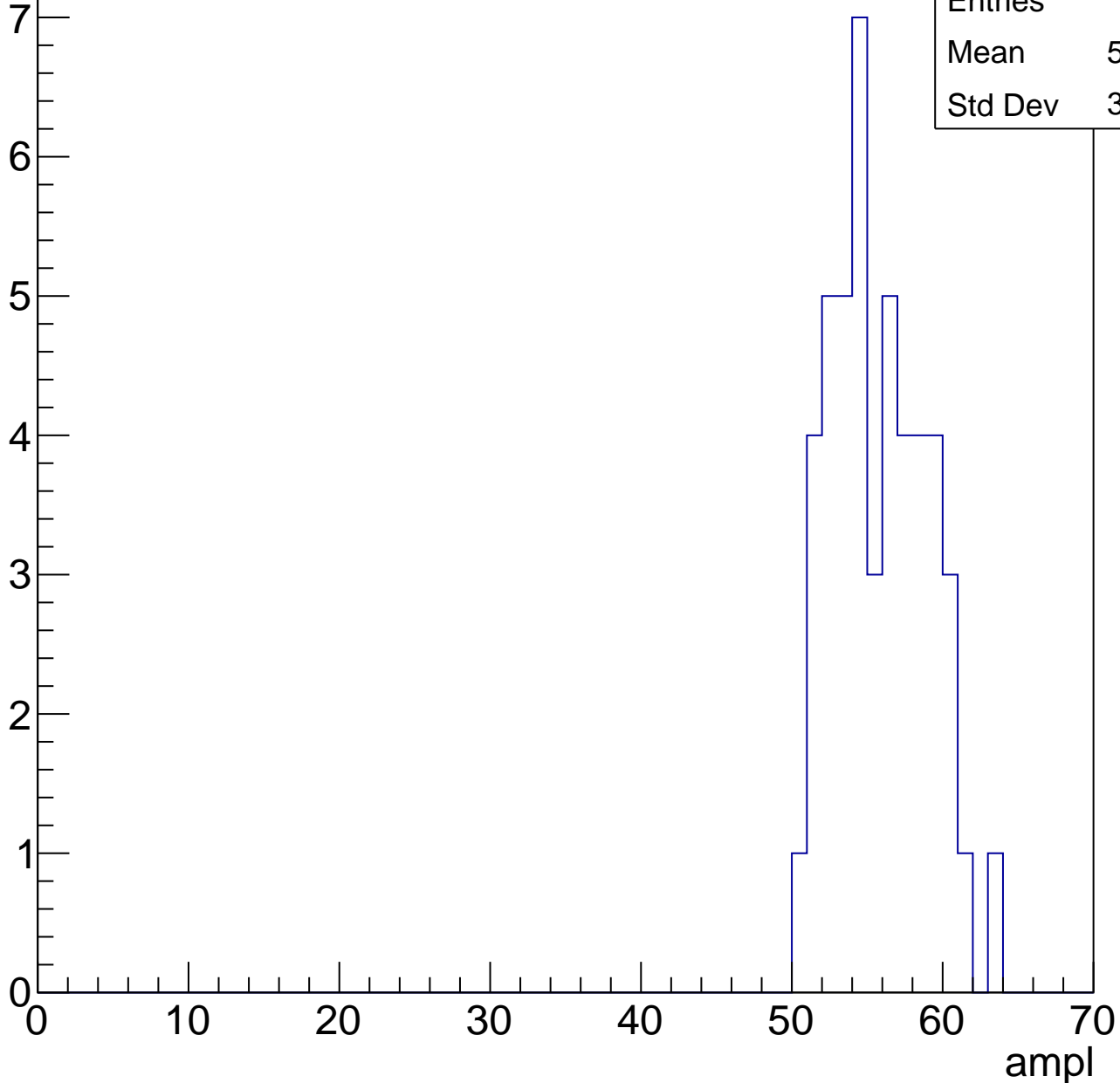
Entries	72
Mean	49.79
Std Dev	3.605

# B0L000S, U19-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	55.36
Std Dev	3.097

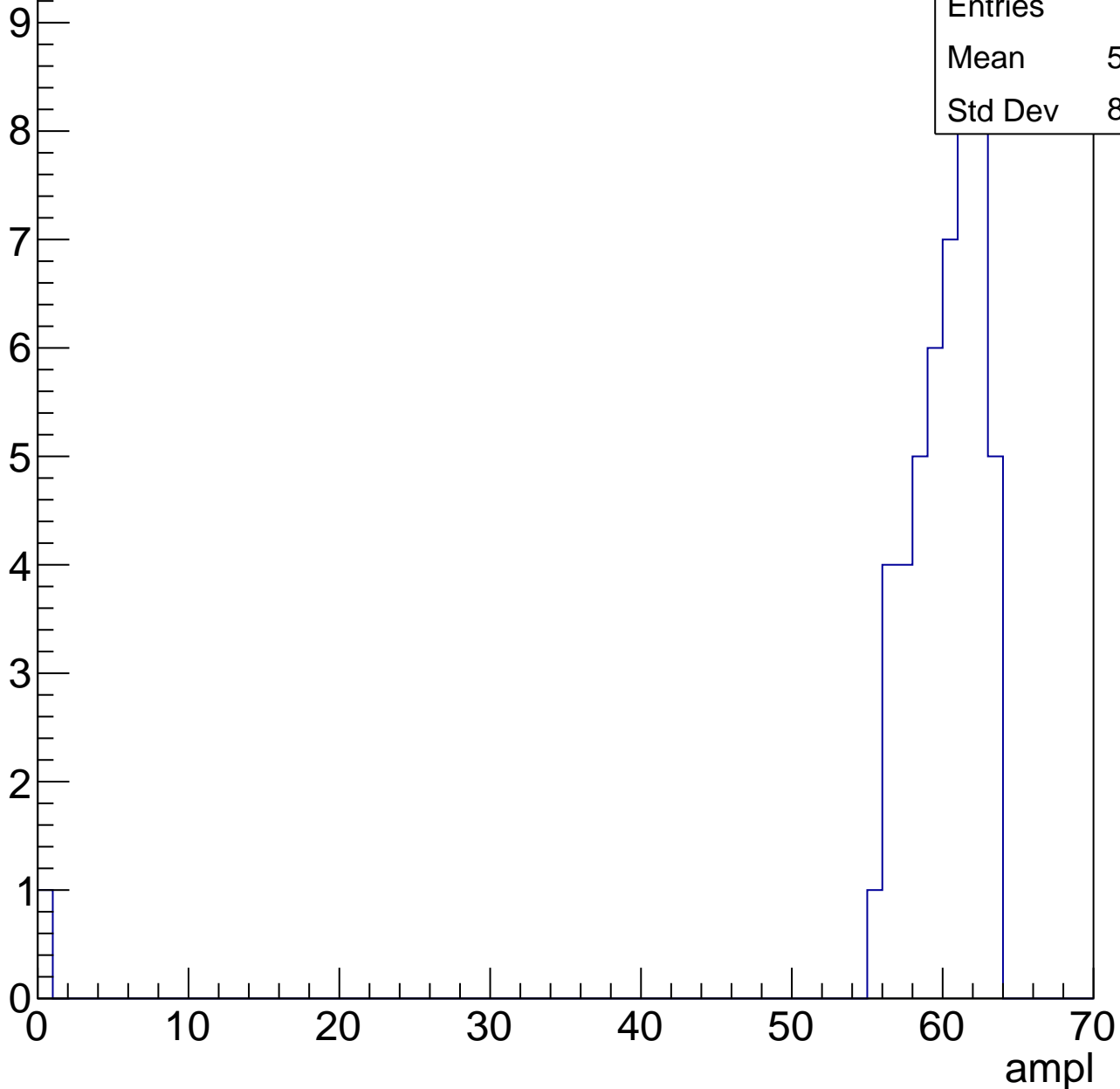


# B0L000S, U19-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

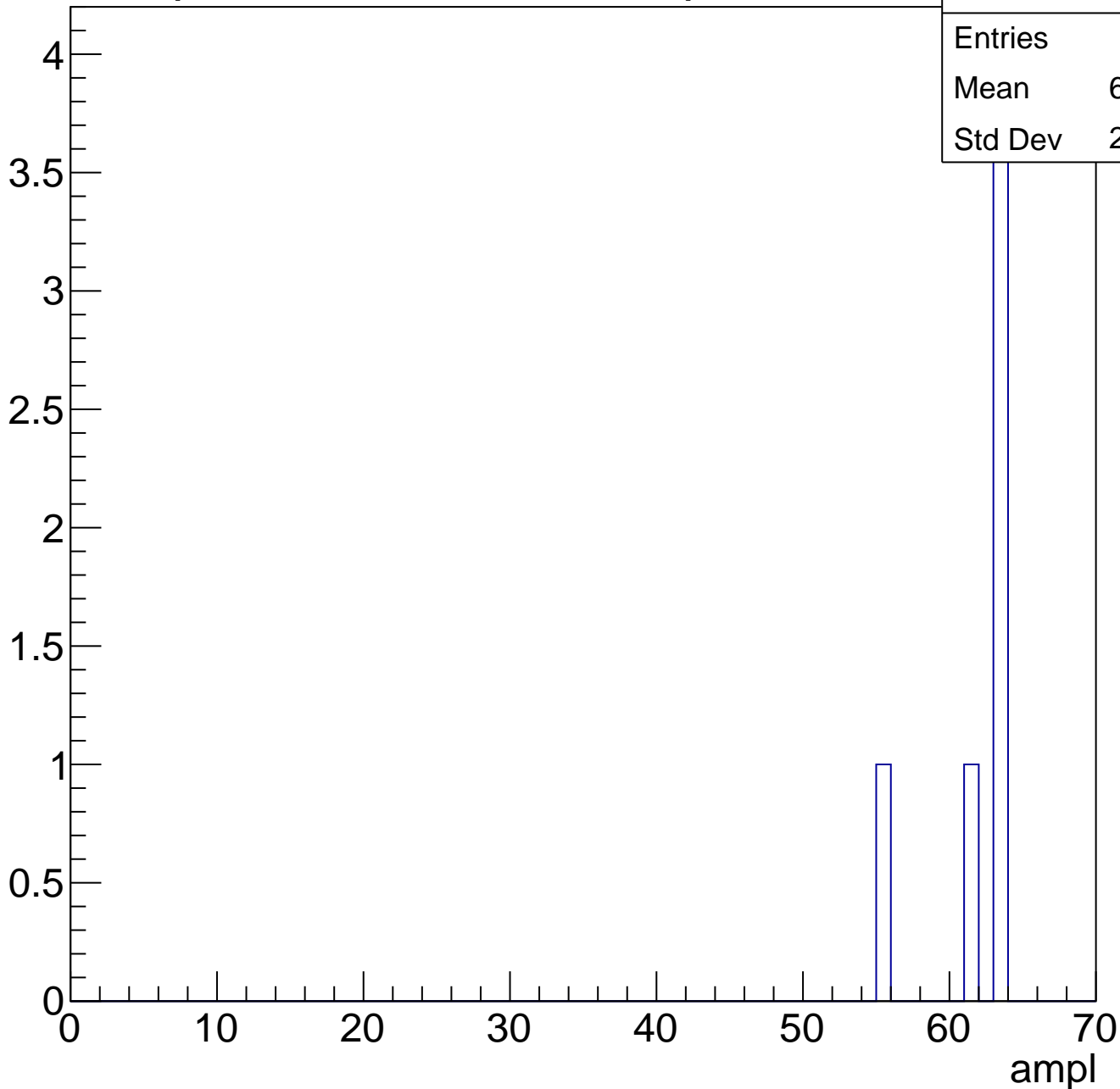
Entries	50
Mean	58.64
Std Dev	8.657



# B0L000S, U19-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch20, adc0

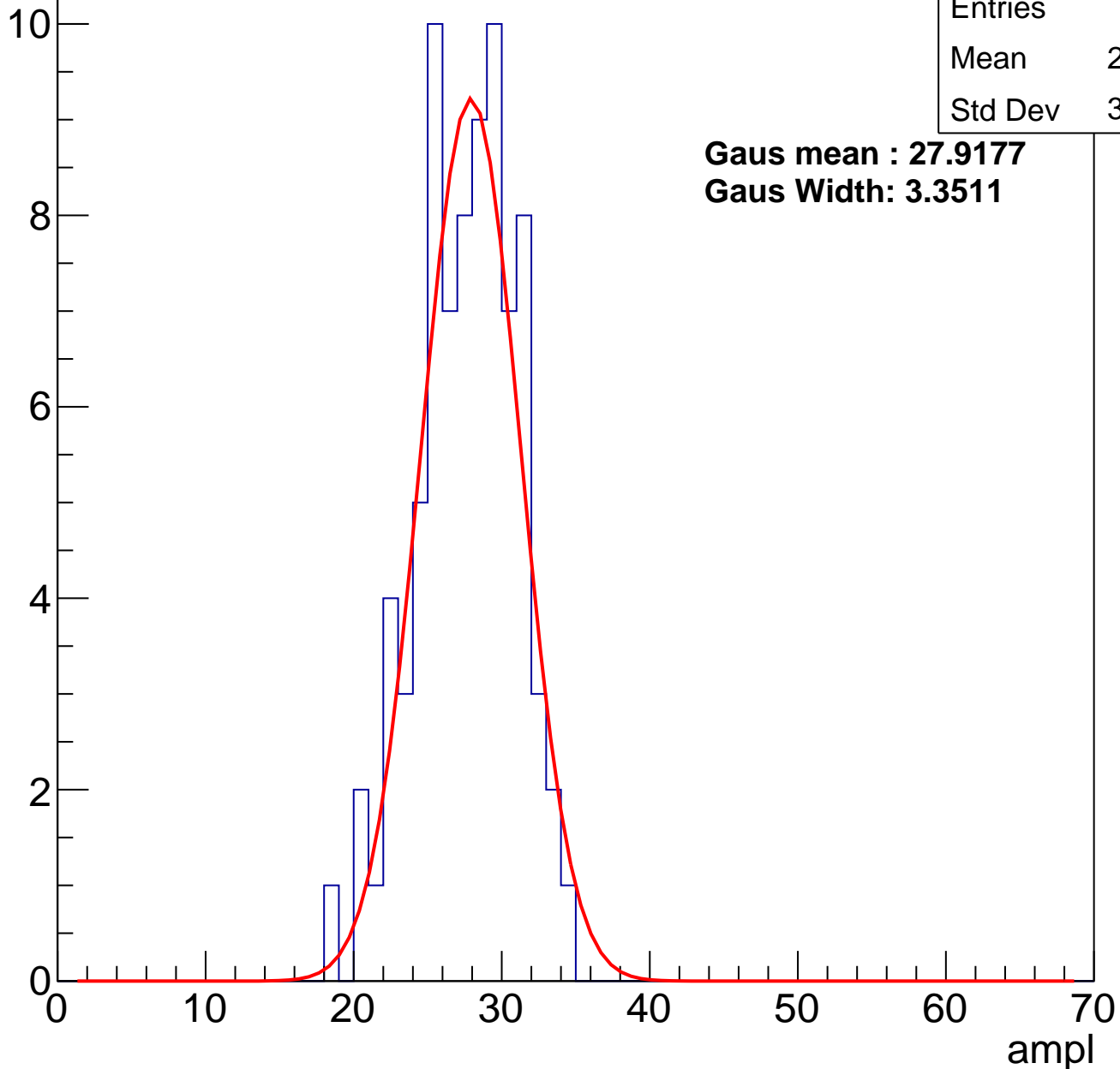
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	27.16
Std Dev	3.313

**Gaus mean : 27.9177**

**Gaus Width: 3.3511**

Entry



# B0L000S, U19-ch20, adc1

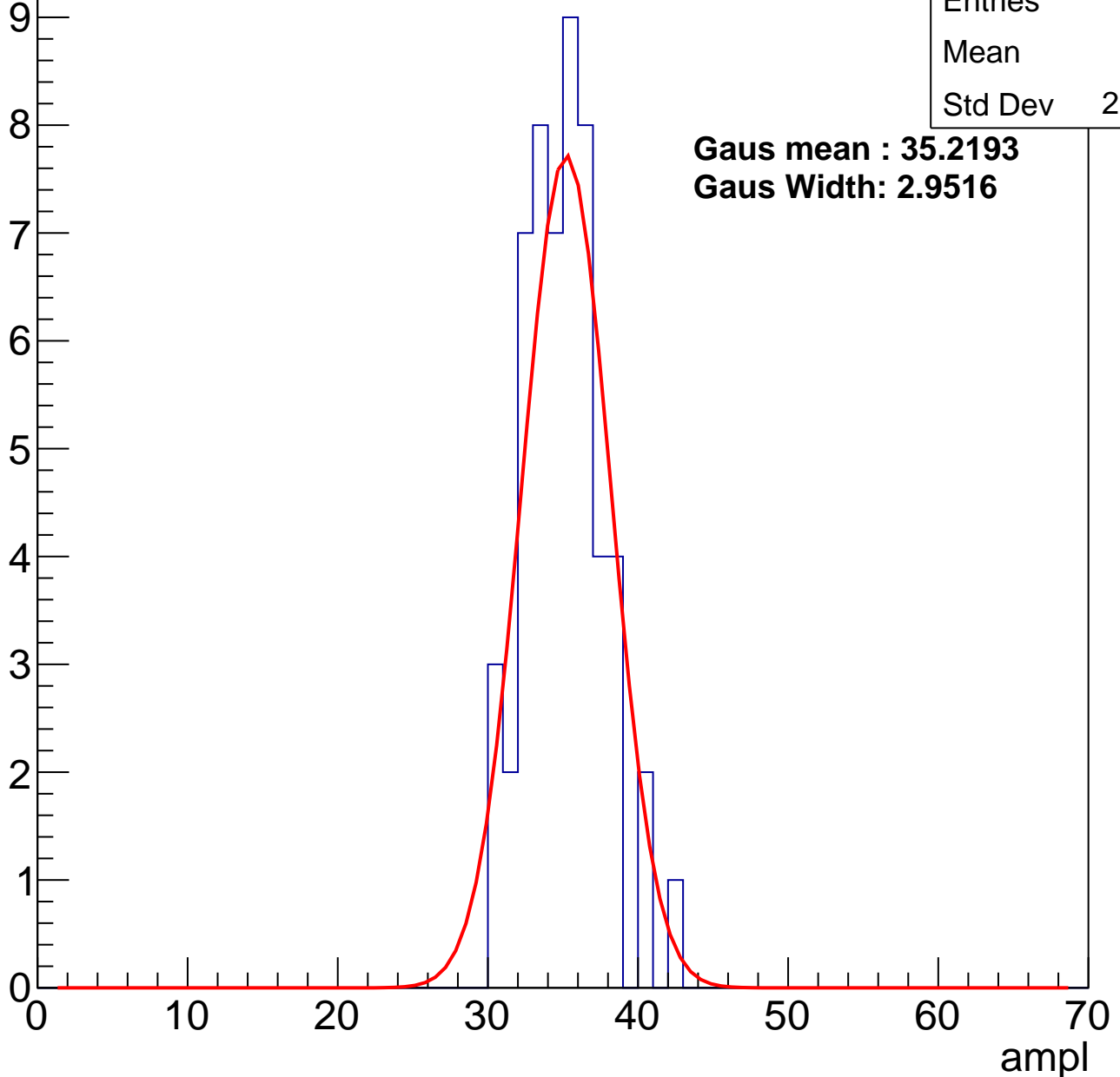
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	34.6
Std Dev	2.556

**Gaus mean : 35.2193**

**Gaus Width: 2.9516**



# B0L000S, U19-ch20, adc2

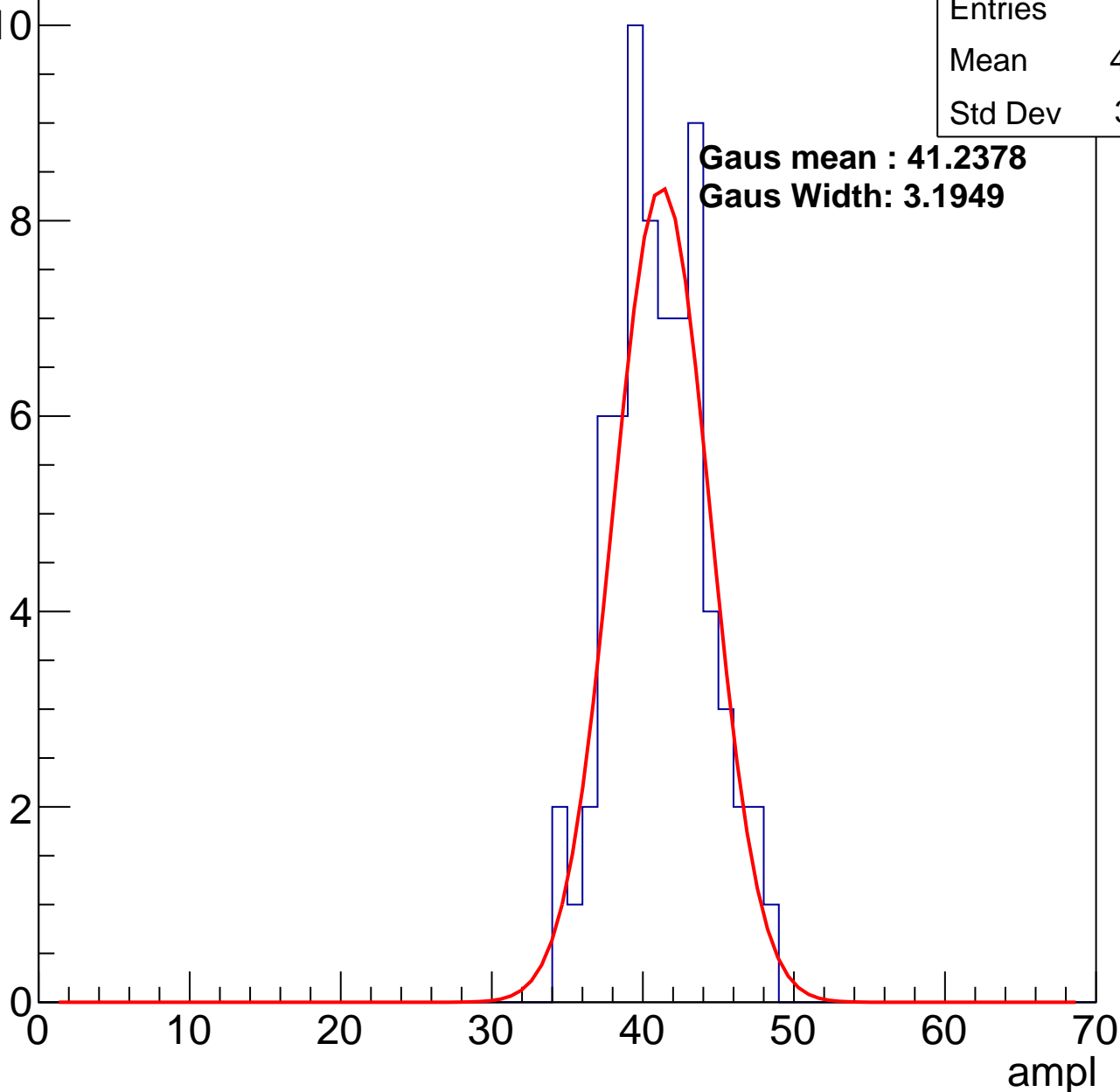
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	40.69
Std Dev	3.101

**Gaus mean : 41.2378**

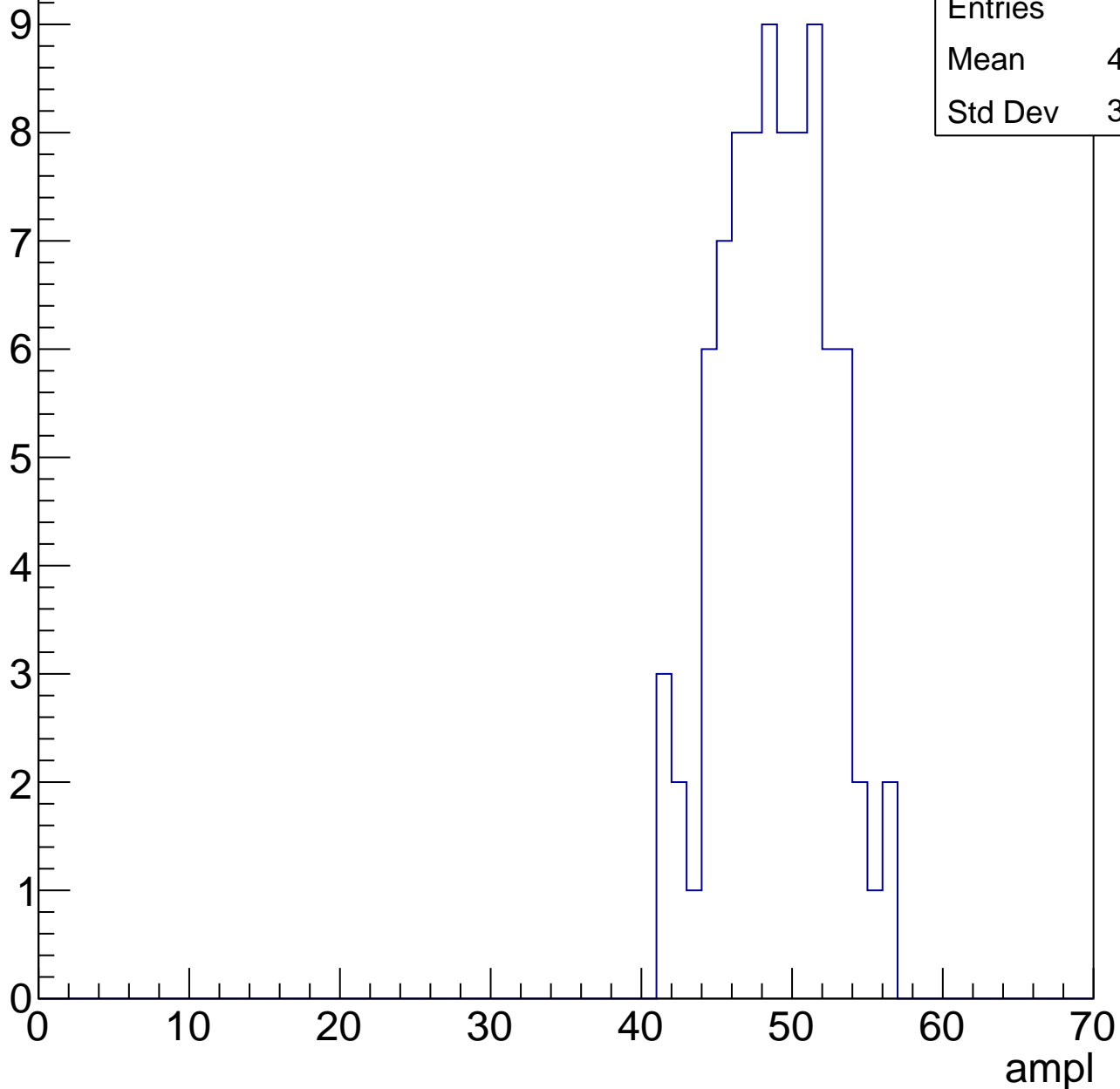
**Gaus Width: 3.1949**



# B0L000S, U19-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



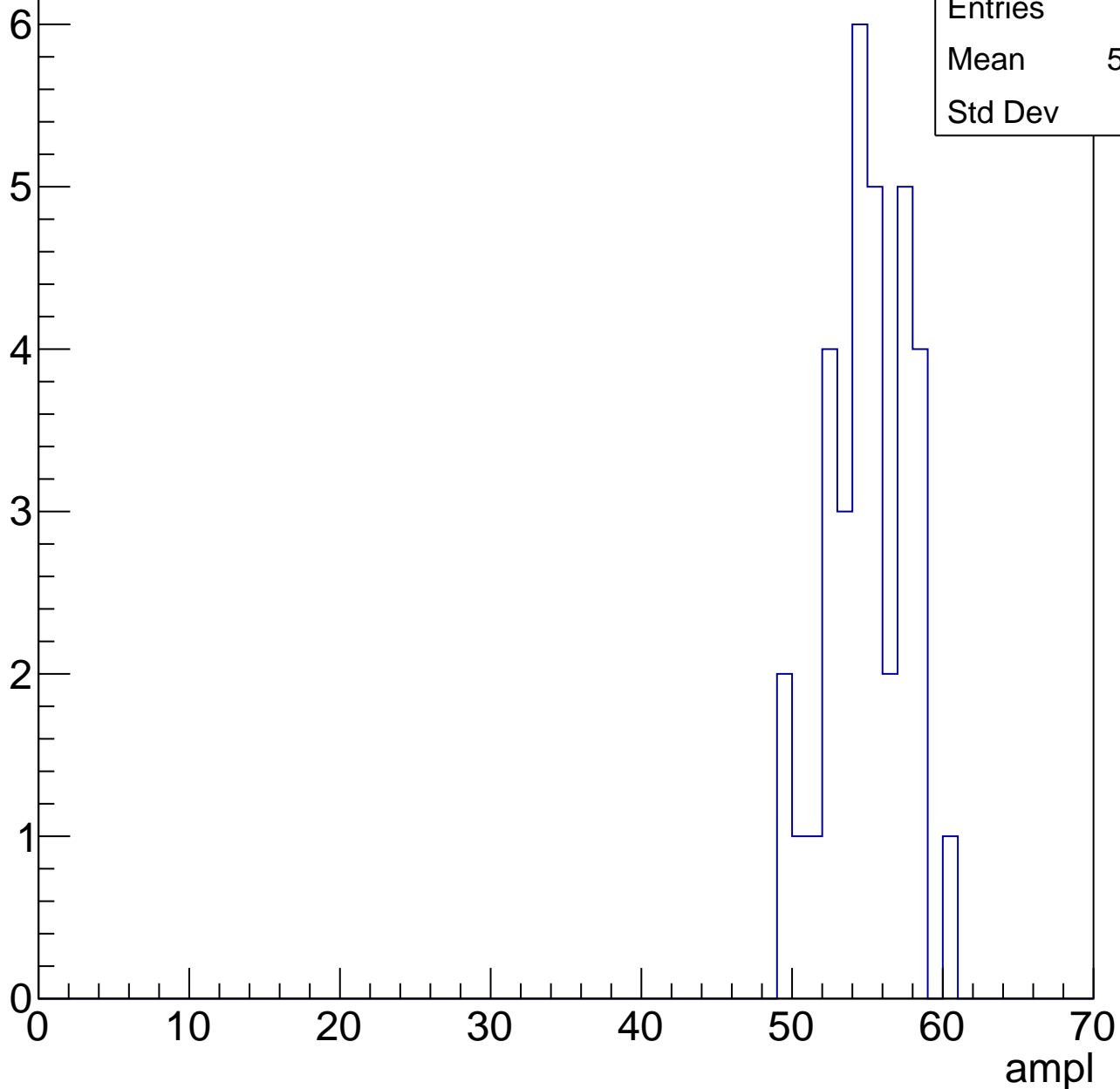
Entries	86
Mean	48.38
Std Dev	3.488

# B0L000S, U19-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

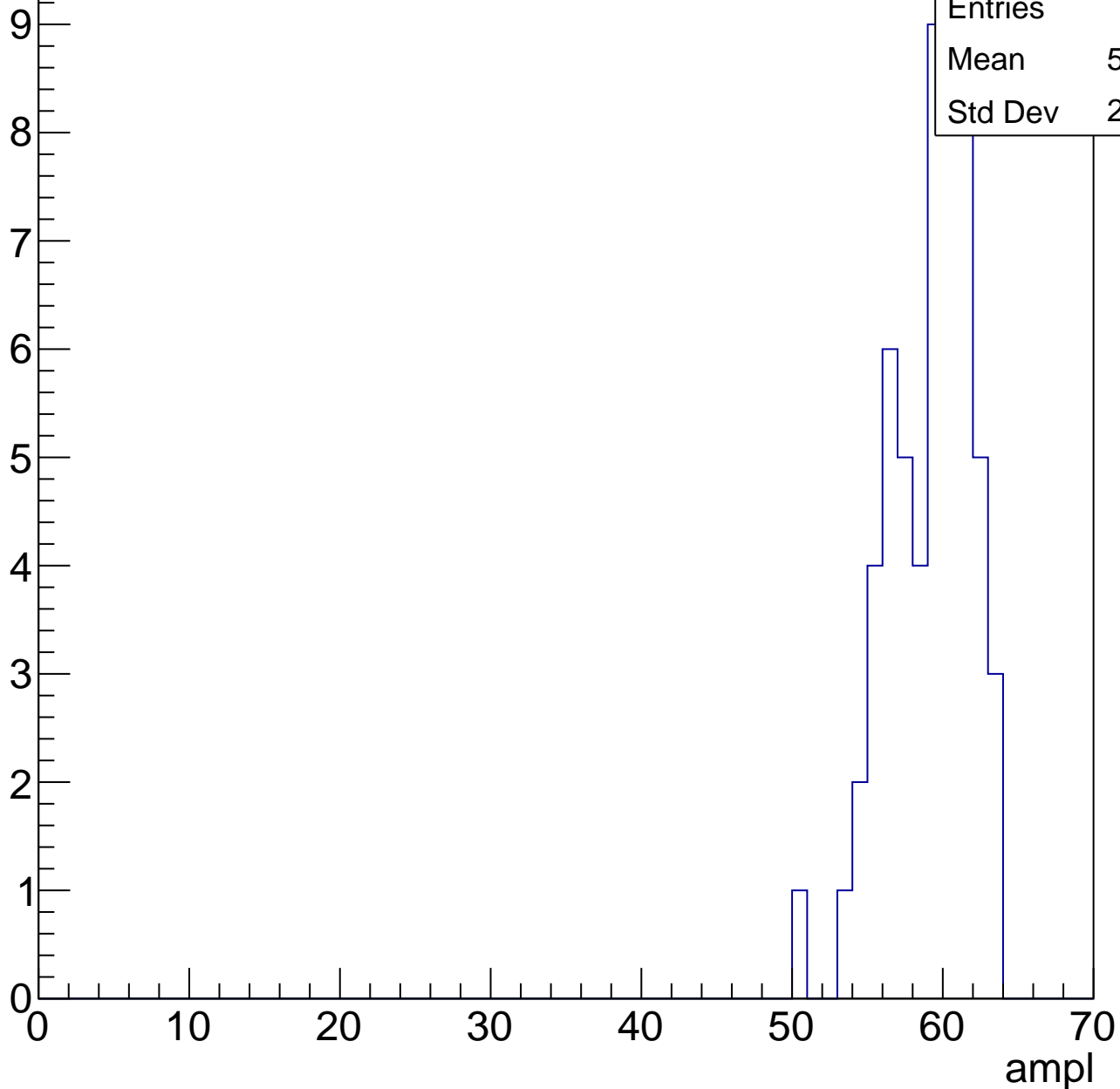
Entries	34
Mean	54.53
Std Dev	2.67



# B0L000S, U19-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

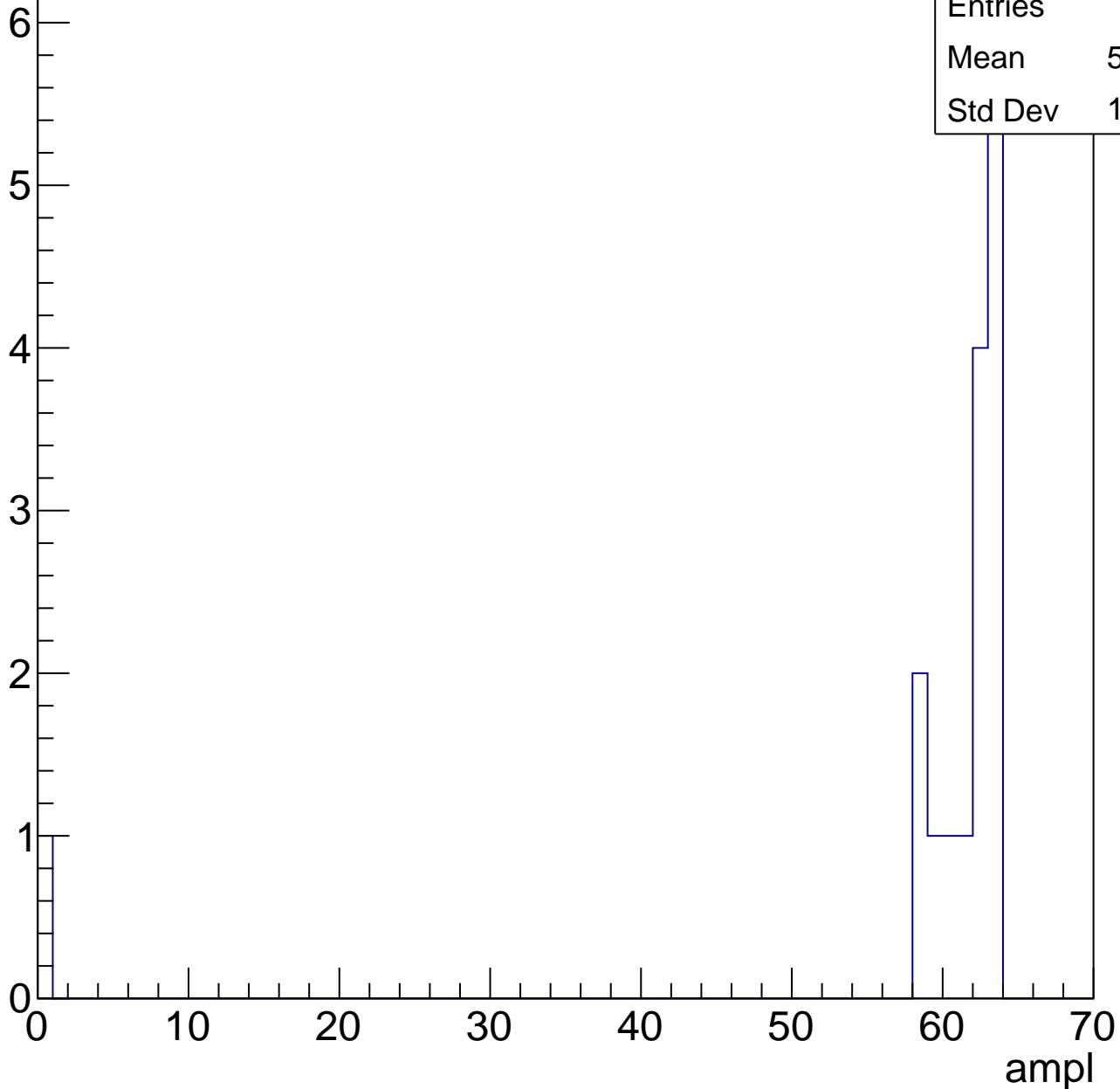


# B0L000S, U19-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.62
Std Dev	14.98





# B0L000S, U19-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch21, adc0

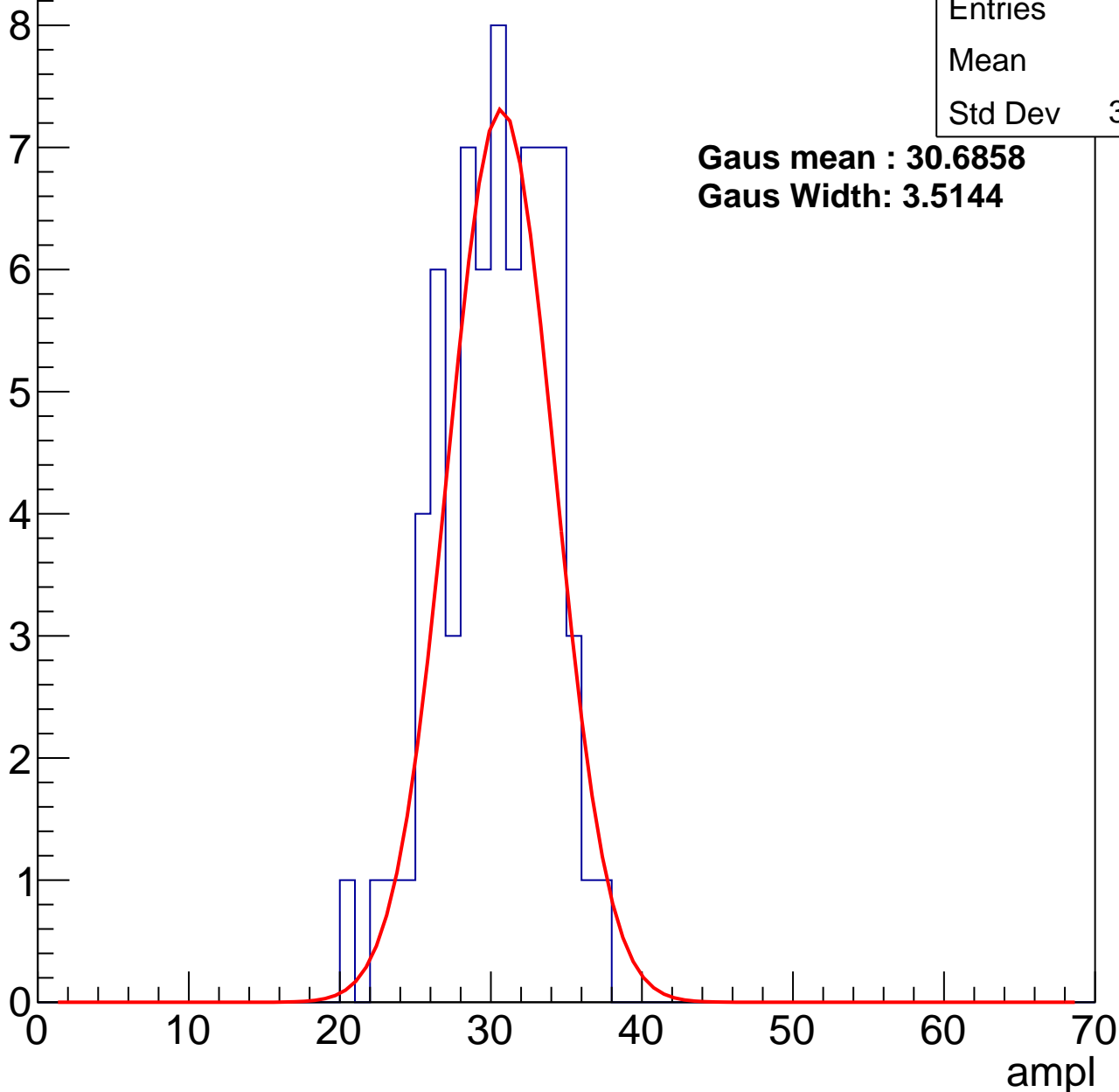
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.9
Std Dev	3.534

**Gaus mean : 30.6858**

**Gaus Width: 3.5144**



# B0L000S, U19-ch21, adc1

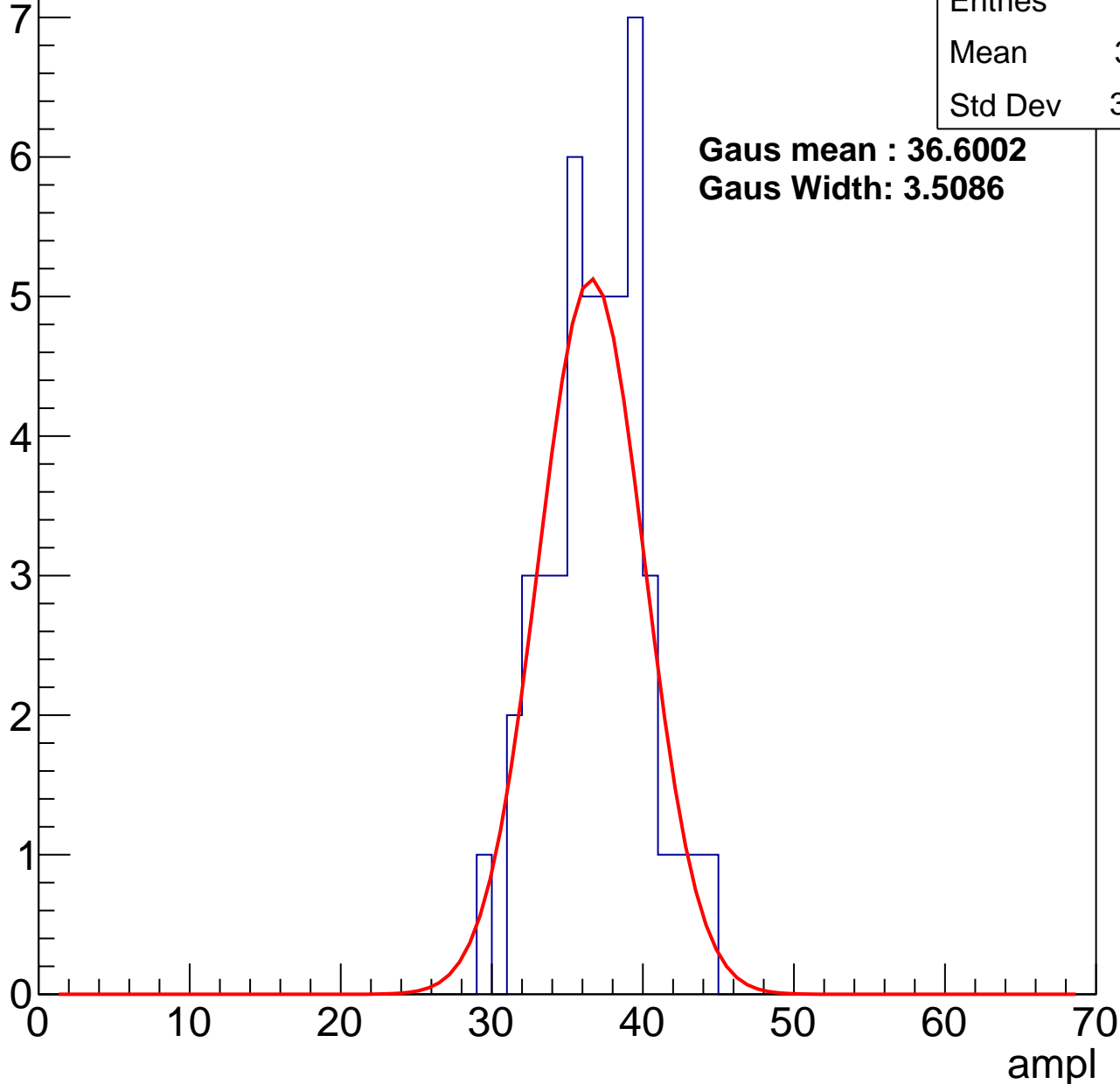
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	36.51
Std Dev	3.228

**Gaus mean : 36.6002**

**Gaus Width: 3.5086**



# B0L000S, U19-ch21, adc2

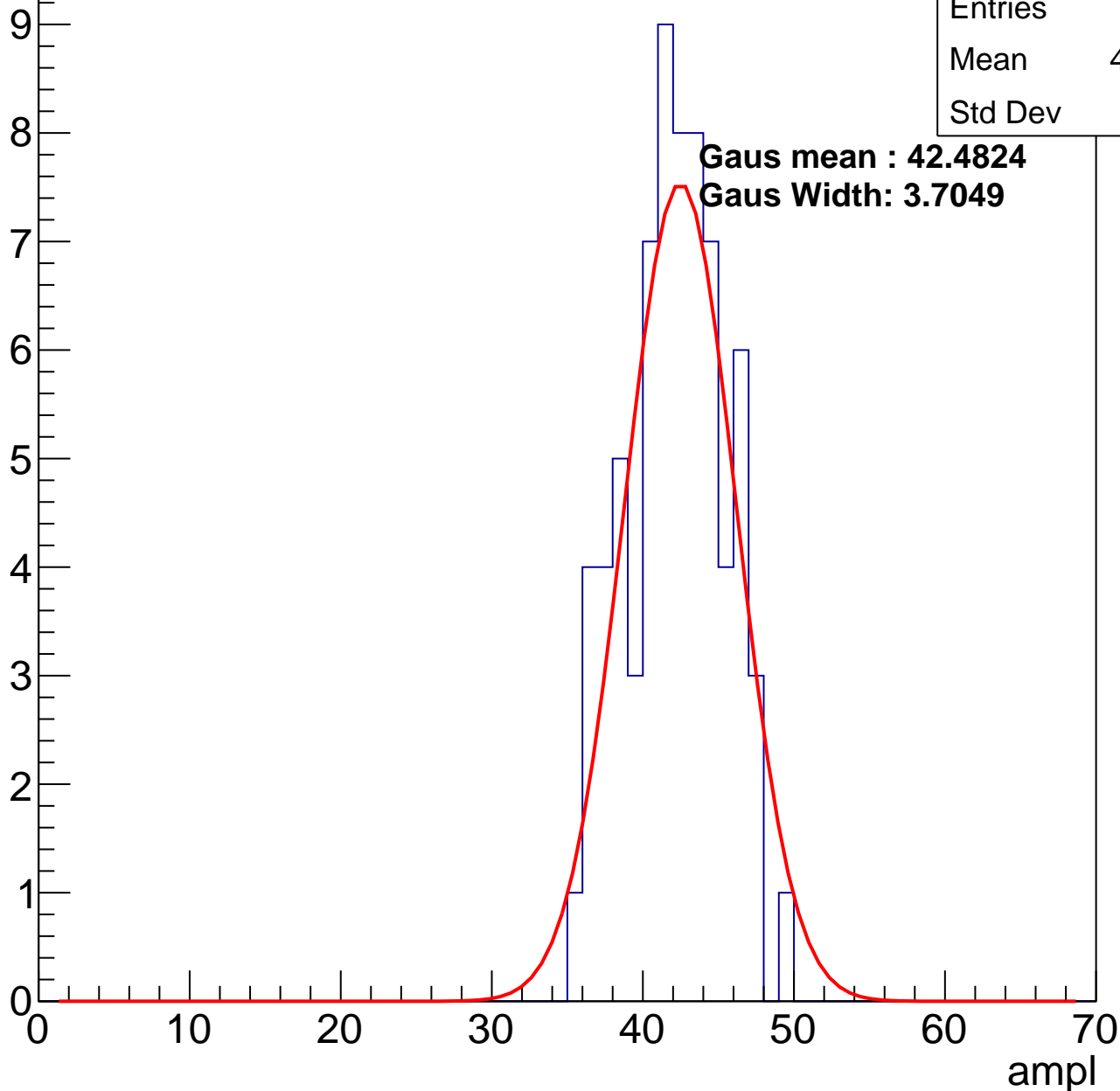
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.67
Std Dev	3.21

**Gaus mean : 42.4824**

**Gaus Width: 3.7049**

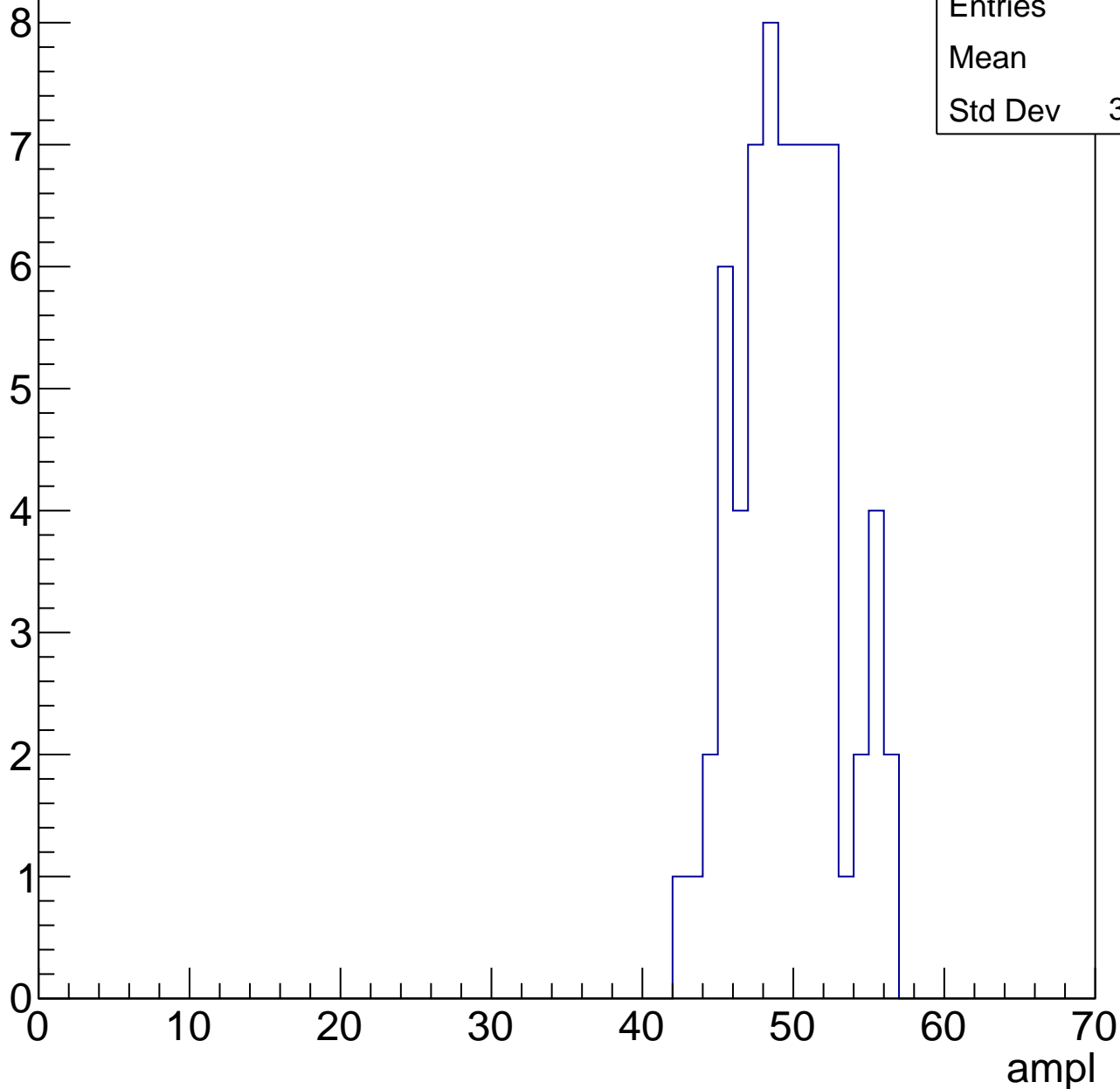


# B0L000S, U19-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

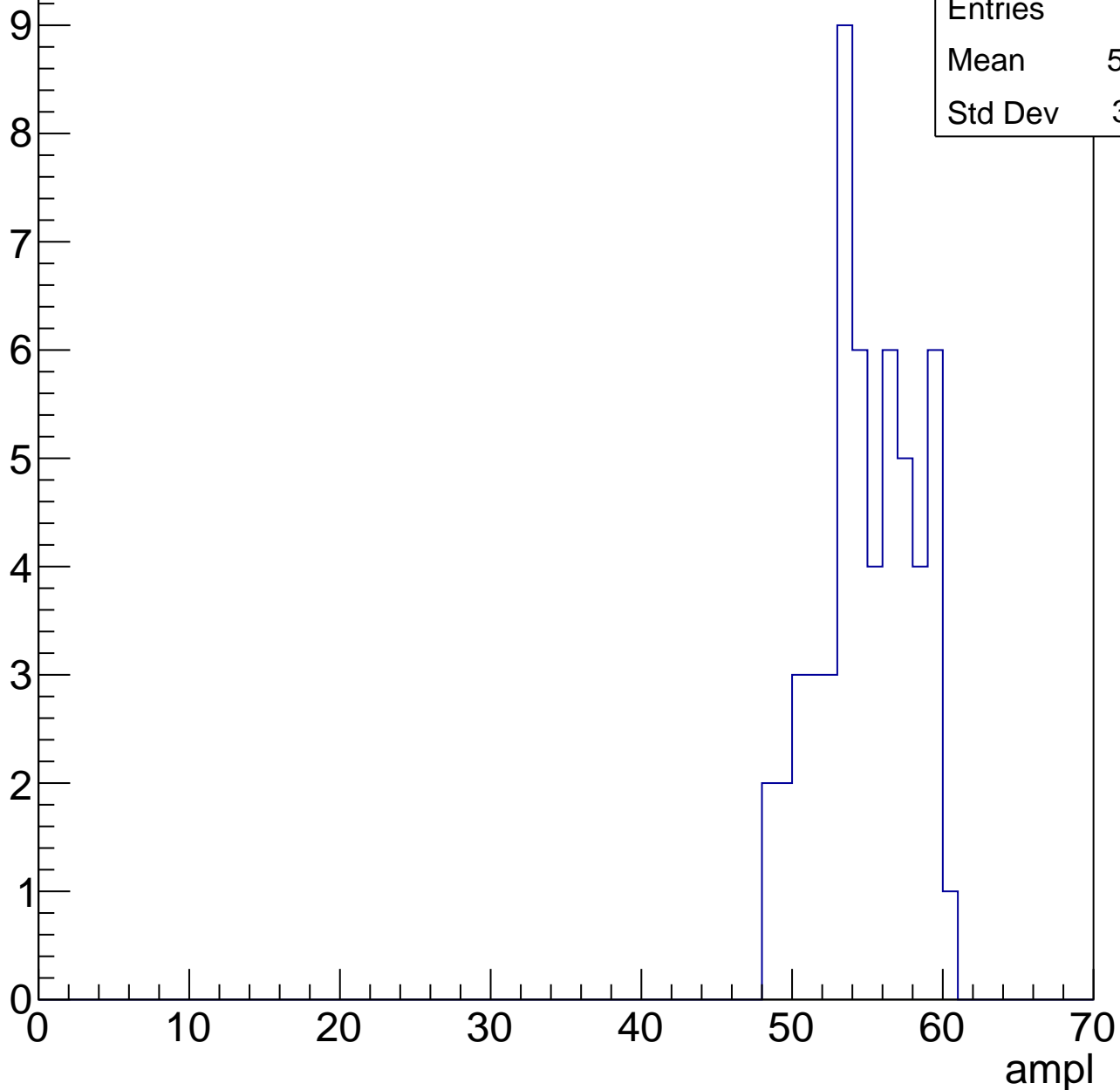
Entries	66
Mean	49.2
Std Dev	3.272



# B0L000S, U19-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



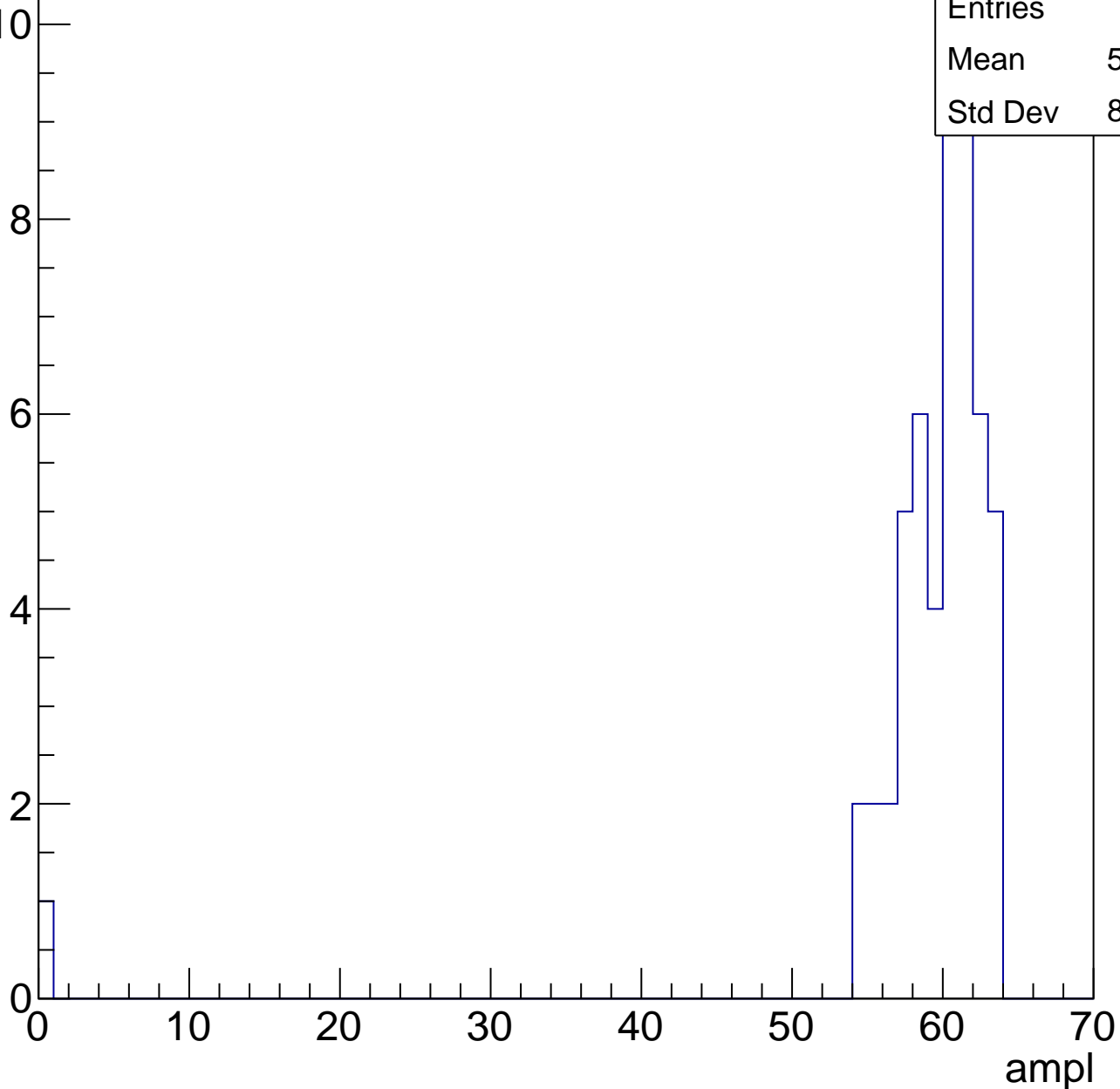
Entries	54
Mean	54.46
Std Dev	3.131

# B0L000S, U19-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.38
Std Dev	8.515

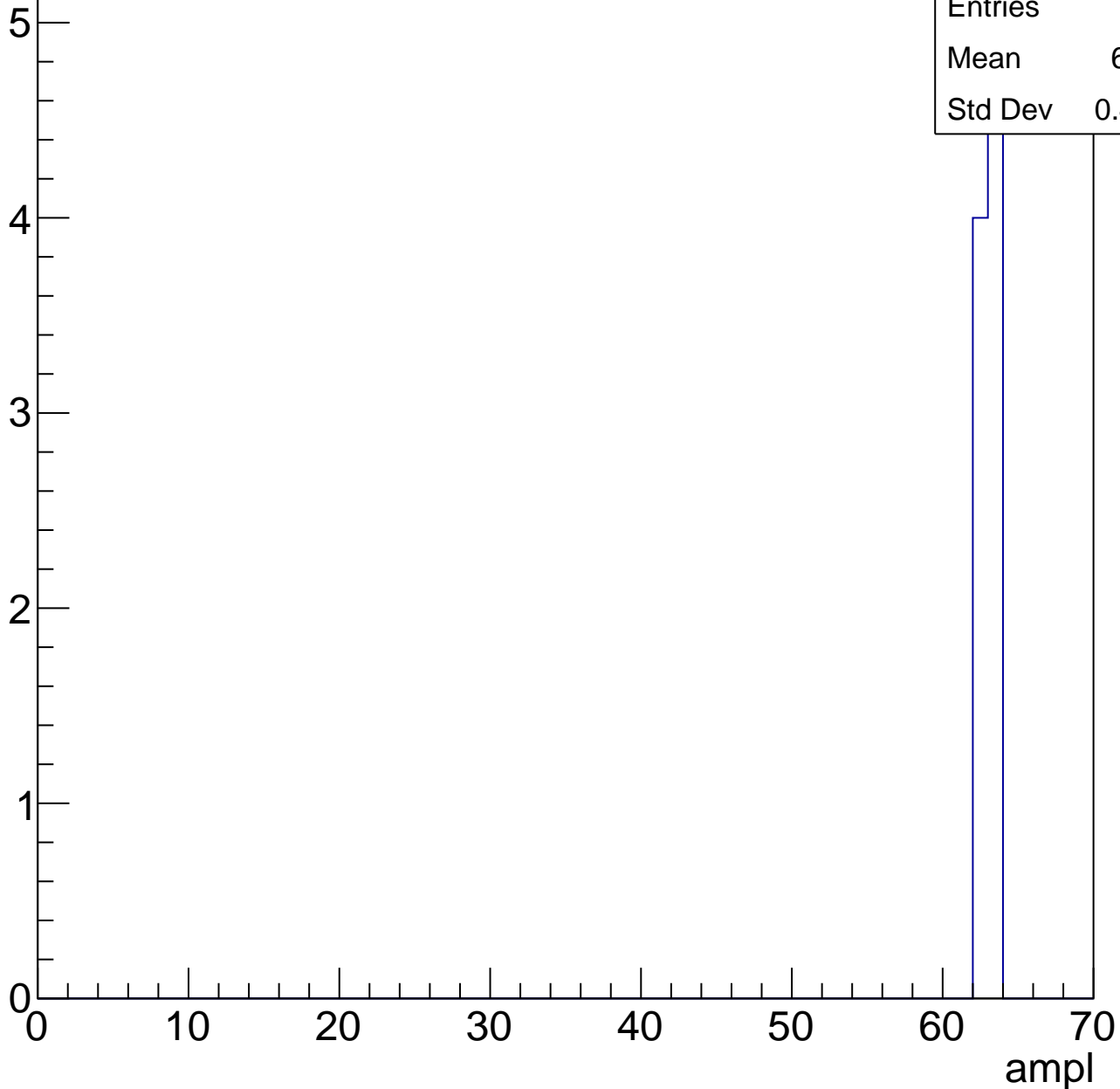


# B0L000S, U19-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.56
Std Dev	0.4969





# B0L000S, U19-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch22, adc0

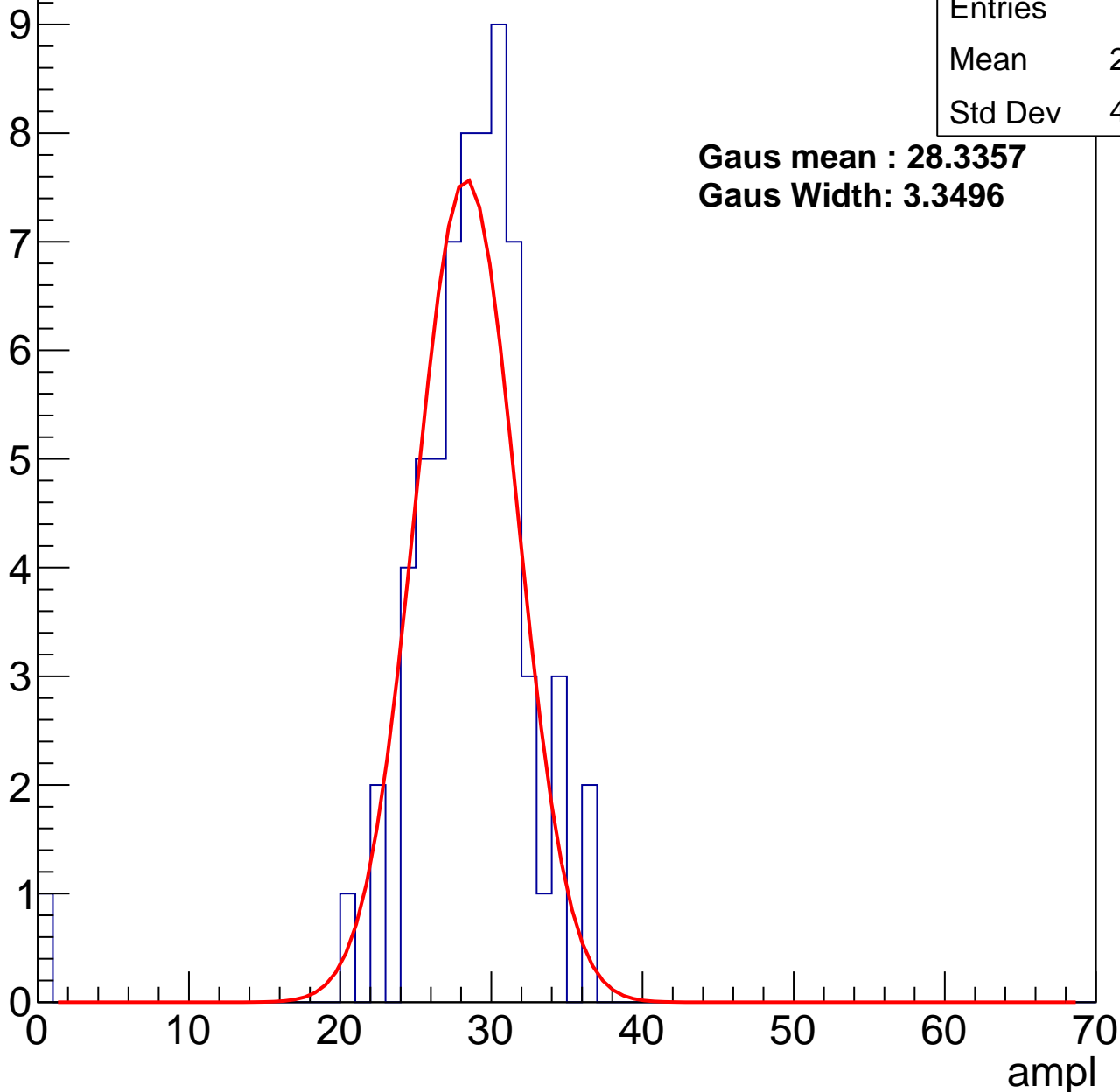
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.03
Std Dev	4.723

**Gaus mean : 28.3357**

**Gaus Width: 3.3496**



# B0L000S, U19-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	35.79
Std Dev	3.488

**Gaus mean : 36.8809**

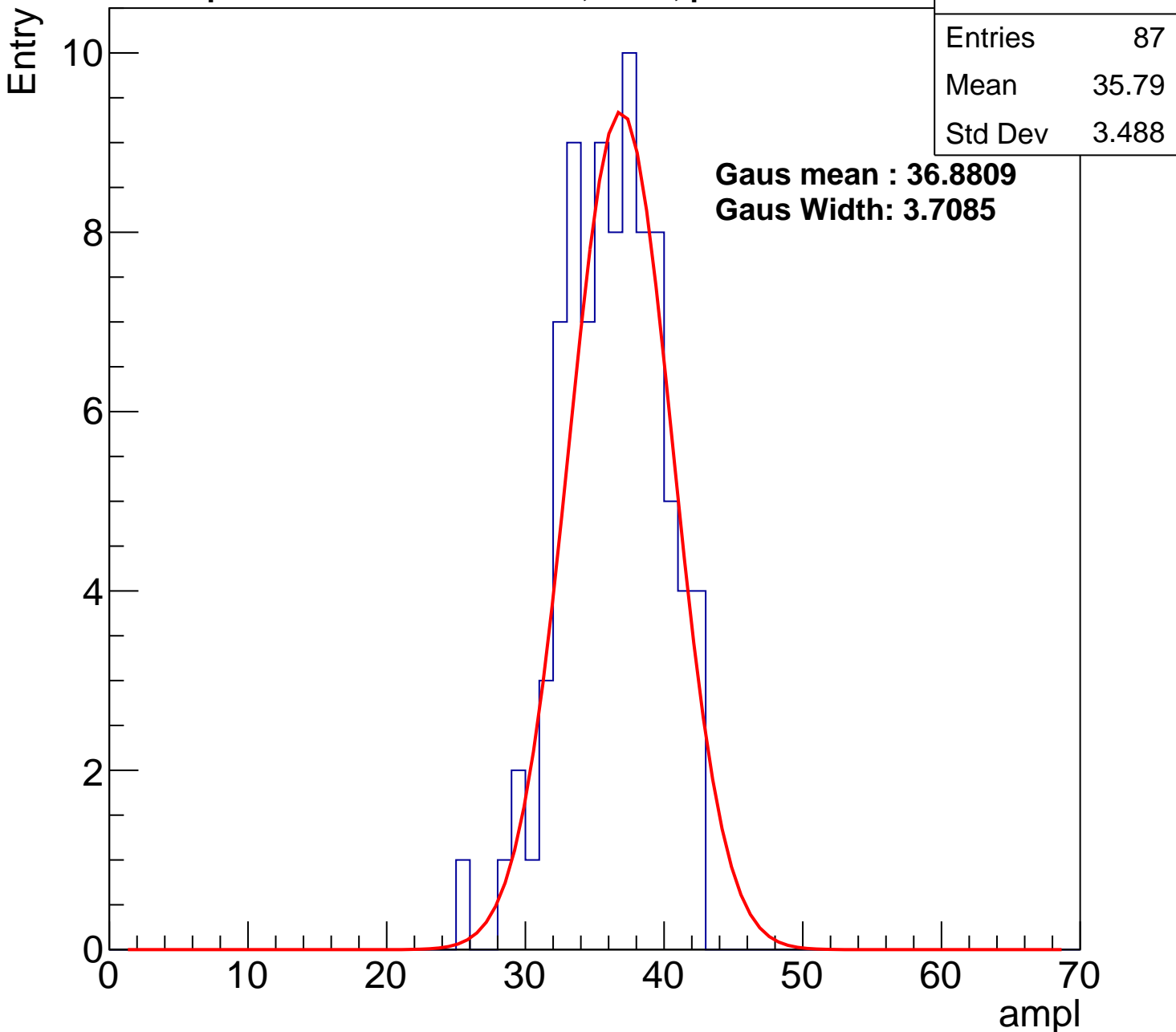
**Gaus Width: 3.7085**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch22, adc2

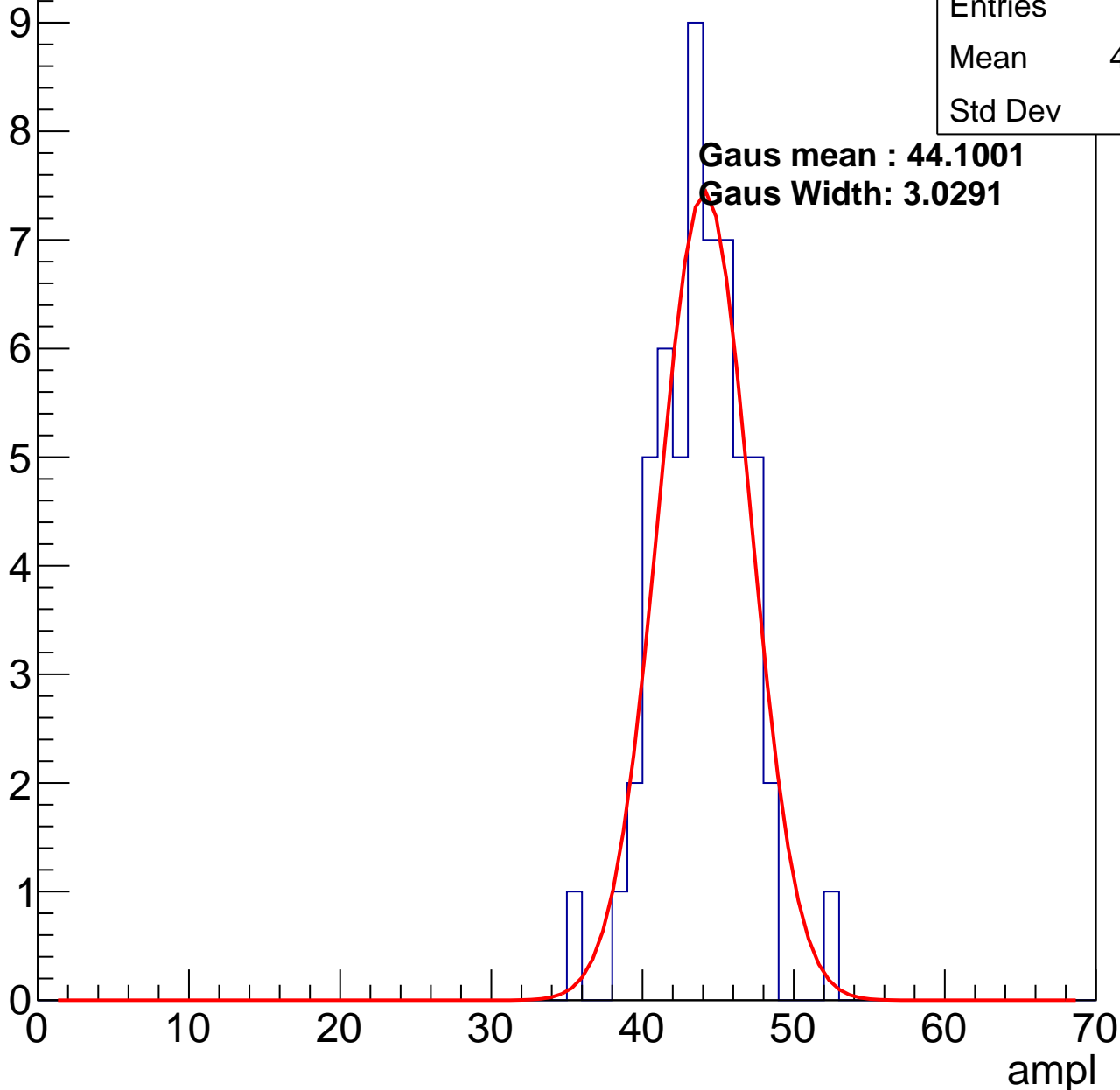
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43.39
Std Dev	2.92

**Gaus mean : 44.1001**

**Gaus Width: 3.0291**

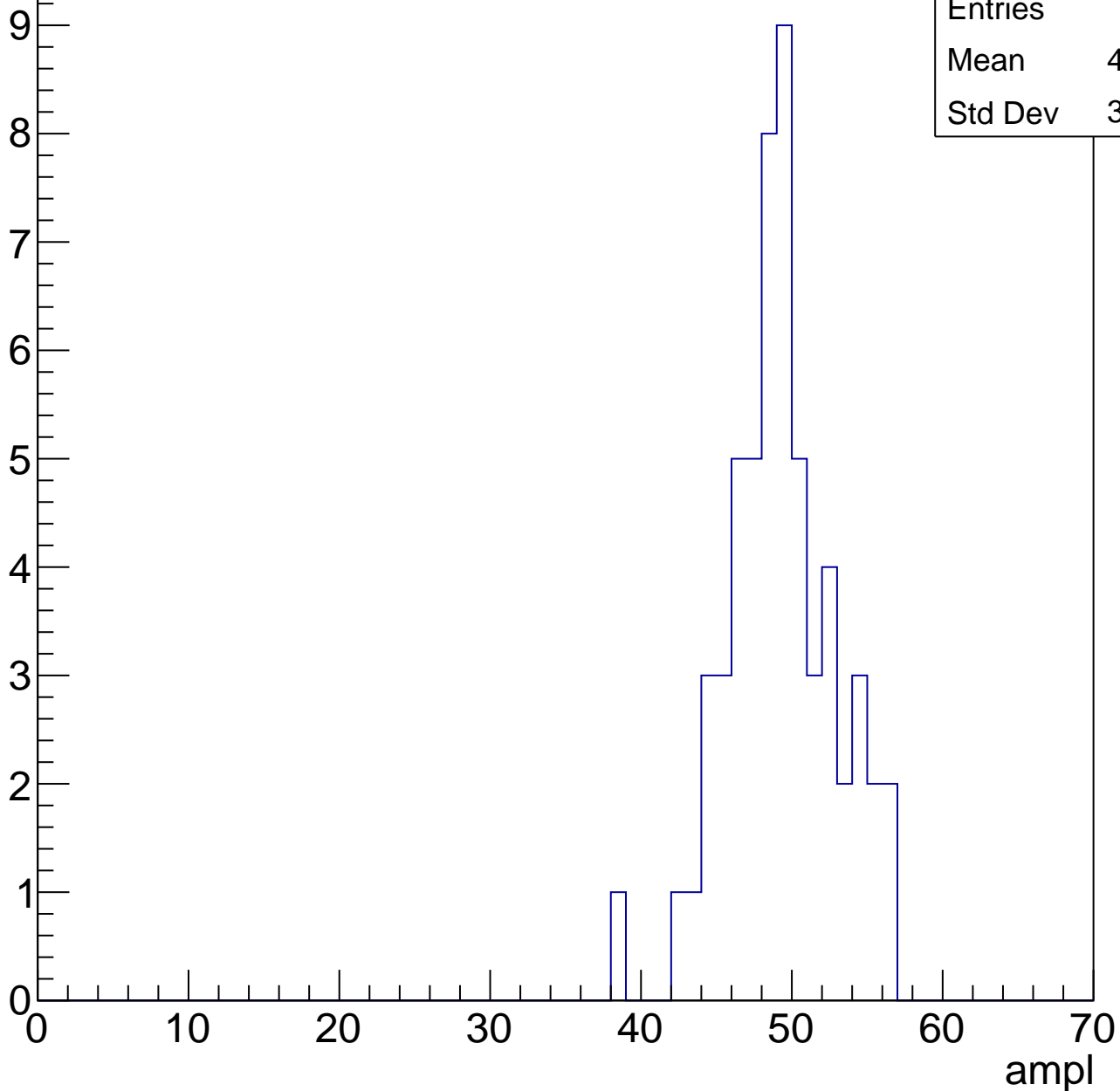


# B0L000S, U19-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

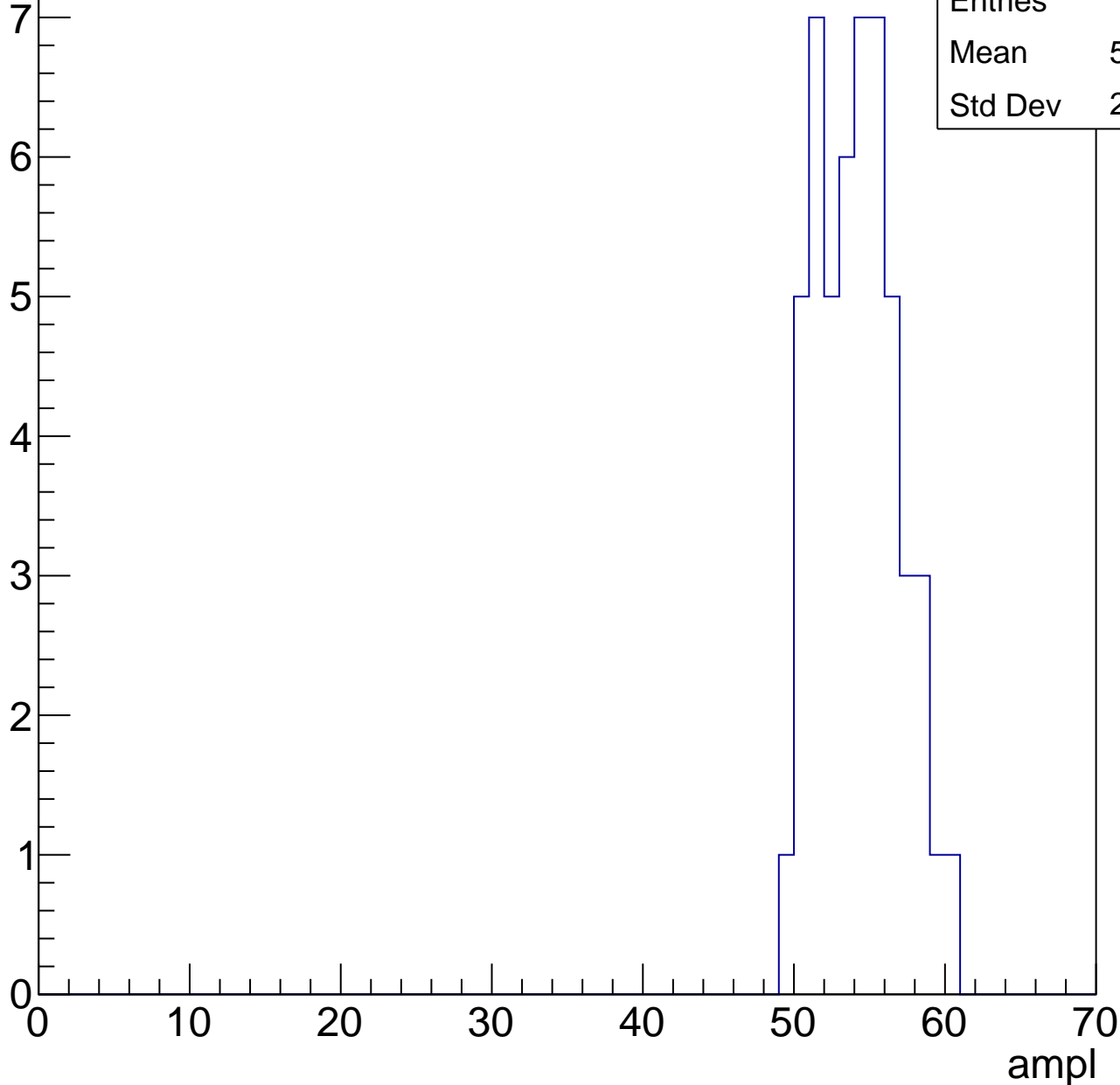
Entries	57
Mean	48.79
Std Dev	3.577



# B0L000S, U19-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



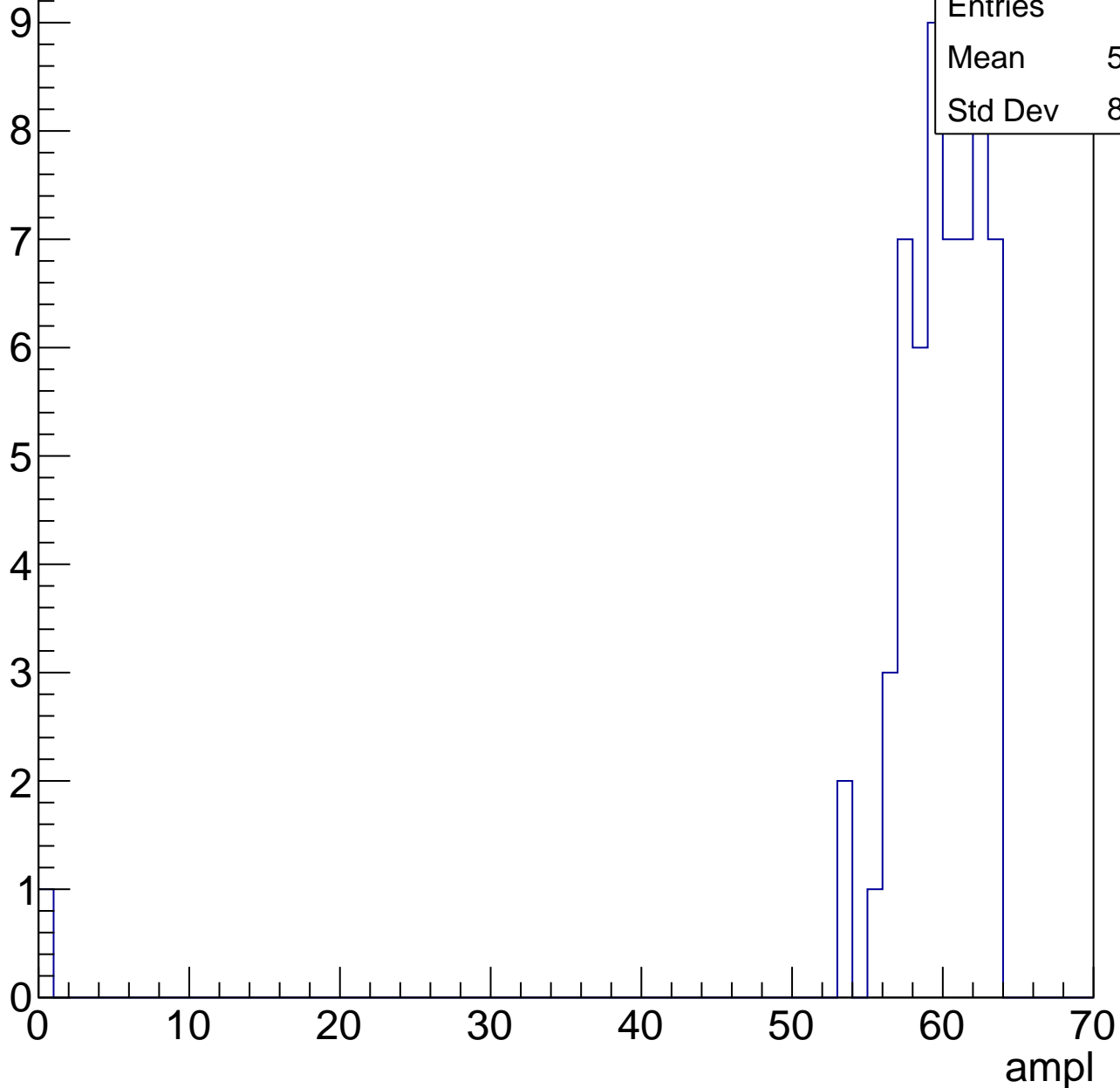
Entries	51
Mean	53.75
Std Dev	2.633

# B0L000S, U19-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	58.53
Std Dev	8.072



# B0L000S, U19-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	10
Mean	61.2
Std Dev	1.536



# B0L000S, U19-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch23, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	59
Mean	28.59
Std Dev	4.651

**Gaus mean : 29.8197**

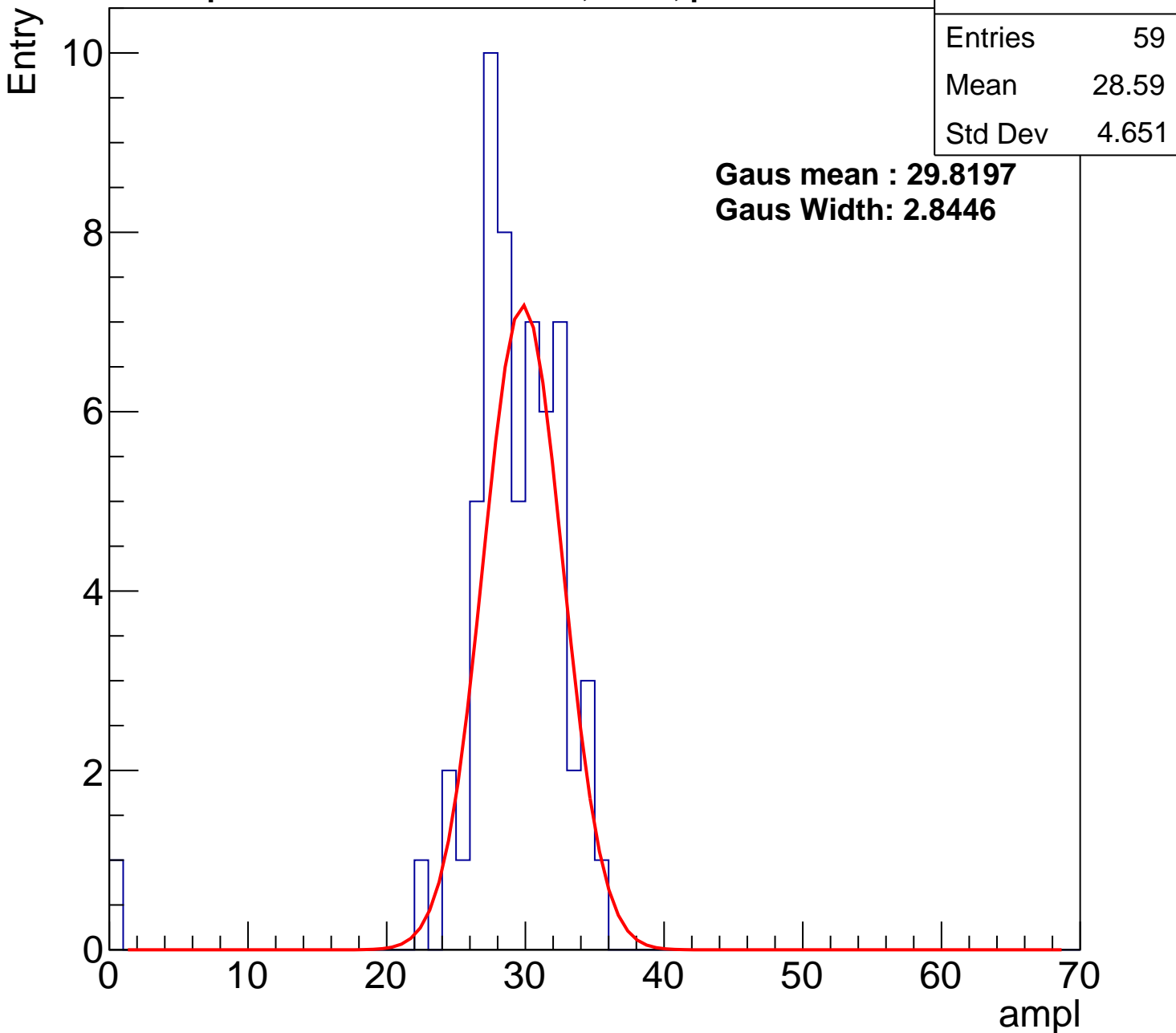
**Gaus Width: 2.8446**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch23, adc1

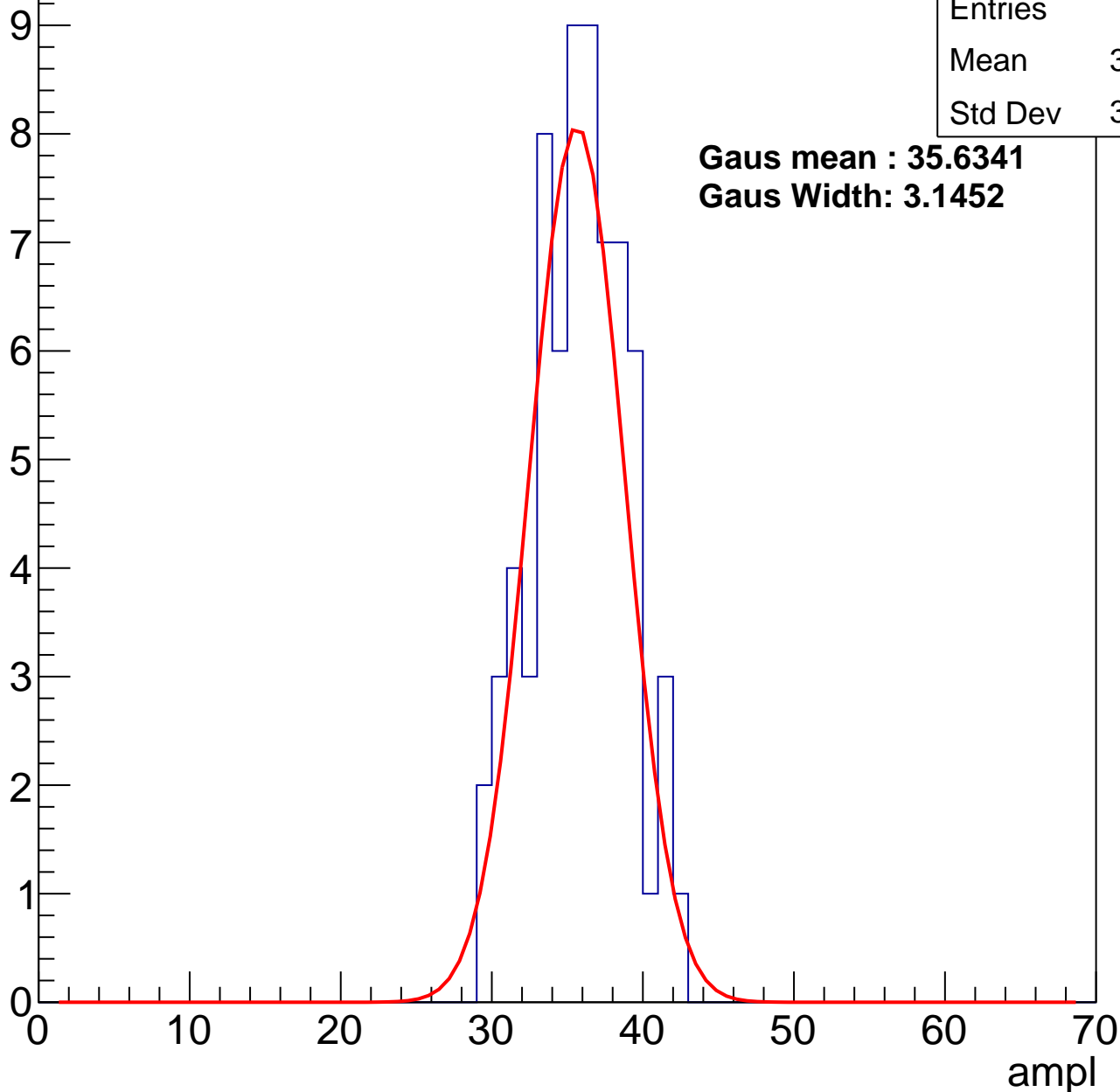
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	35.35
Std Dev	3.059

**Gaus mean : 35.6341**

**Gaus Width: 3.1452**



# B0L000S, U19-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	42
Std Dev	3.101

**Gaus mean : 42.1304**

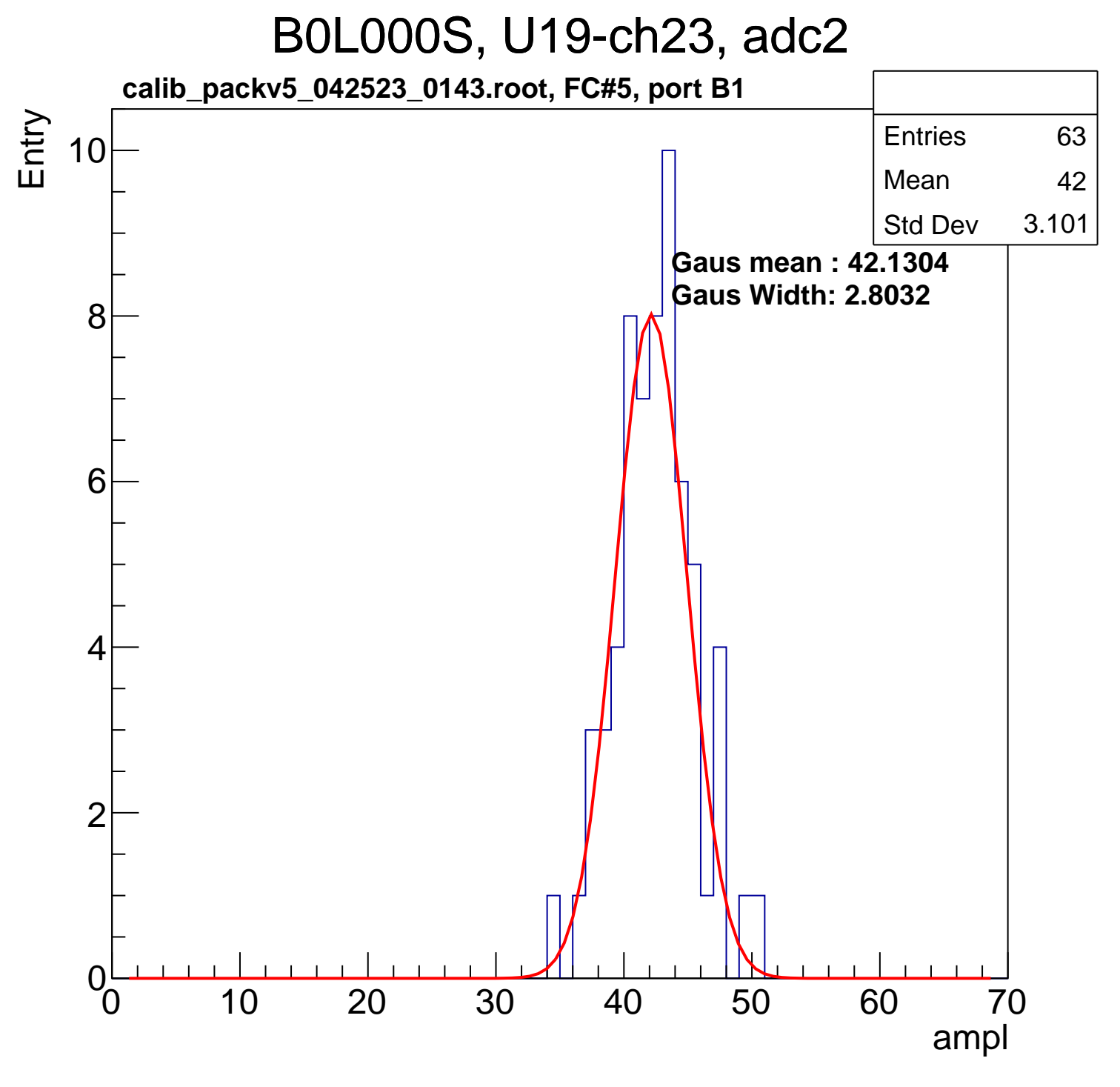
**Gaus Width: 2.8032**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

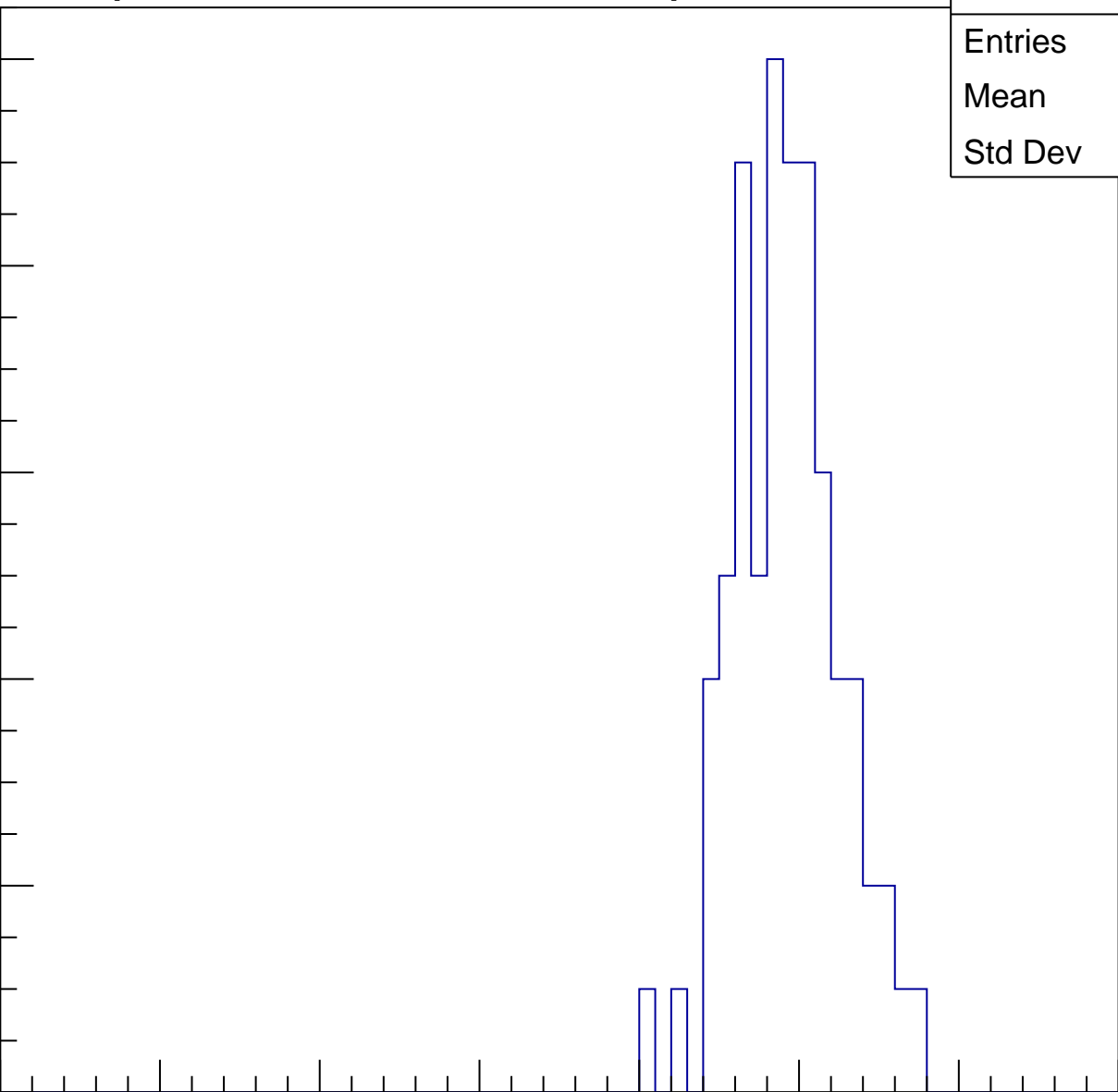
Entries	73
Mean	48.77
Std Dev	3.279

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

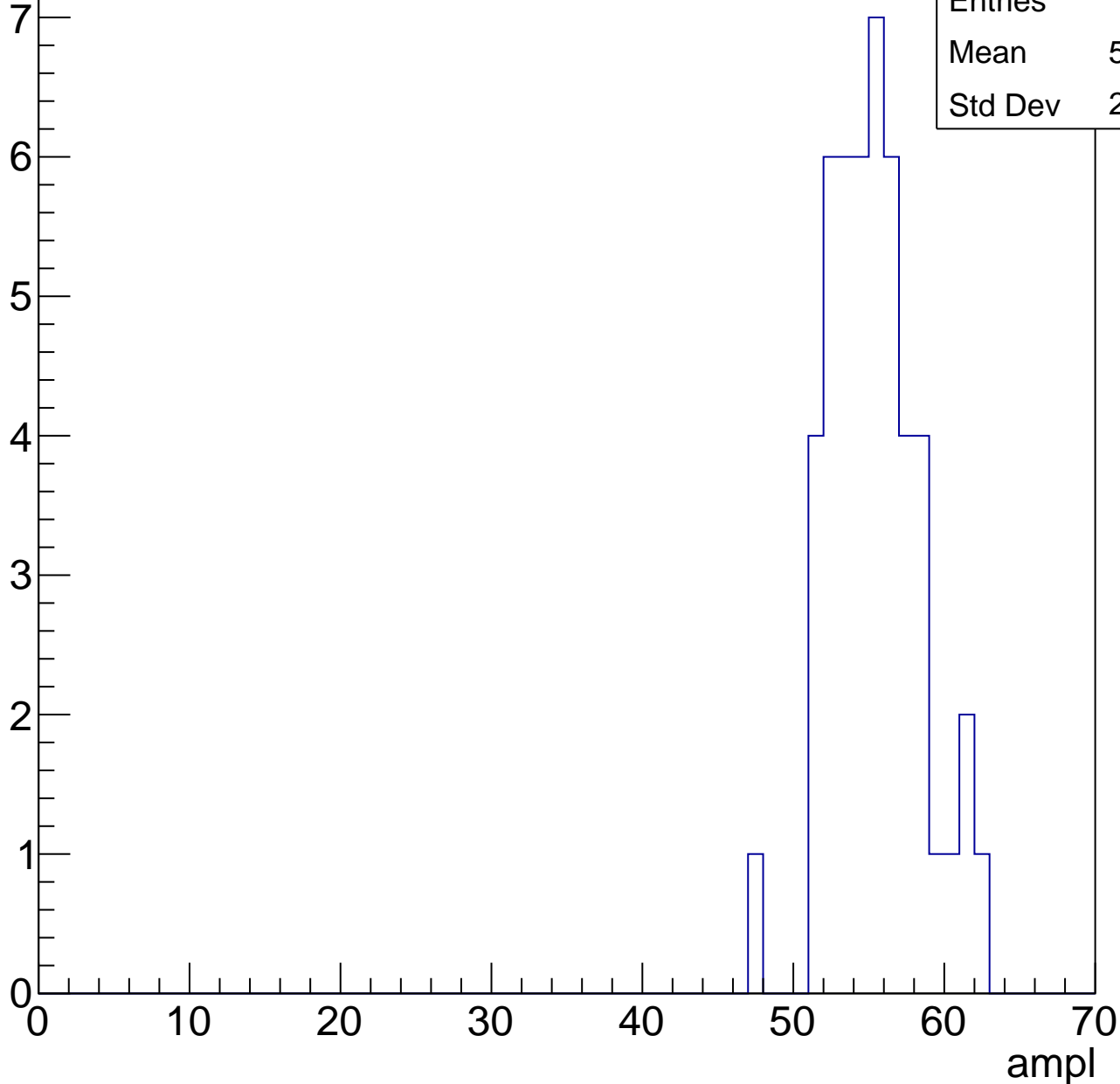


# B0L000S, U19-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	54.88
Std Dev	2.967

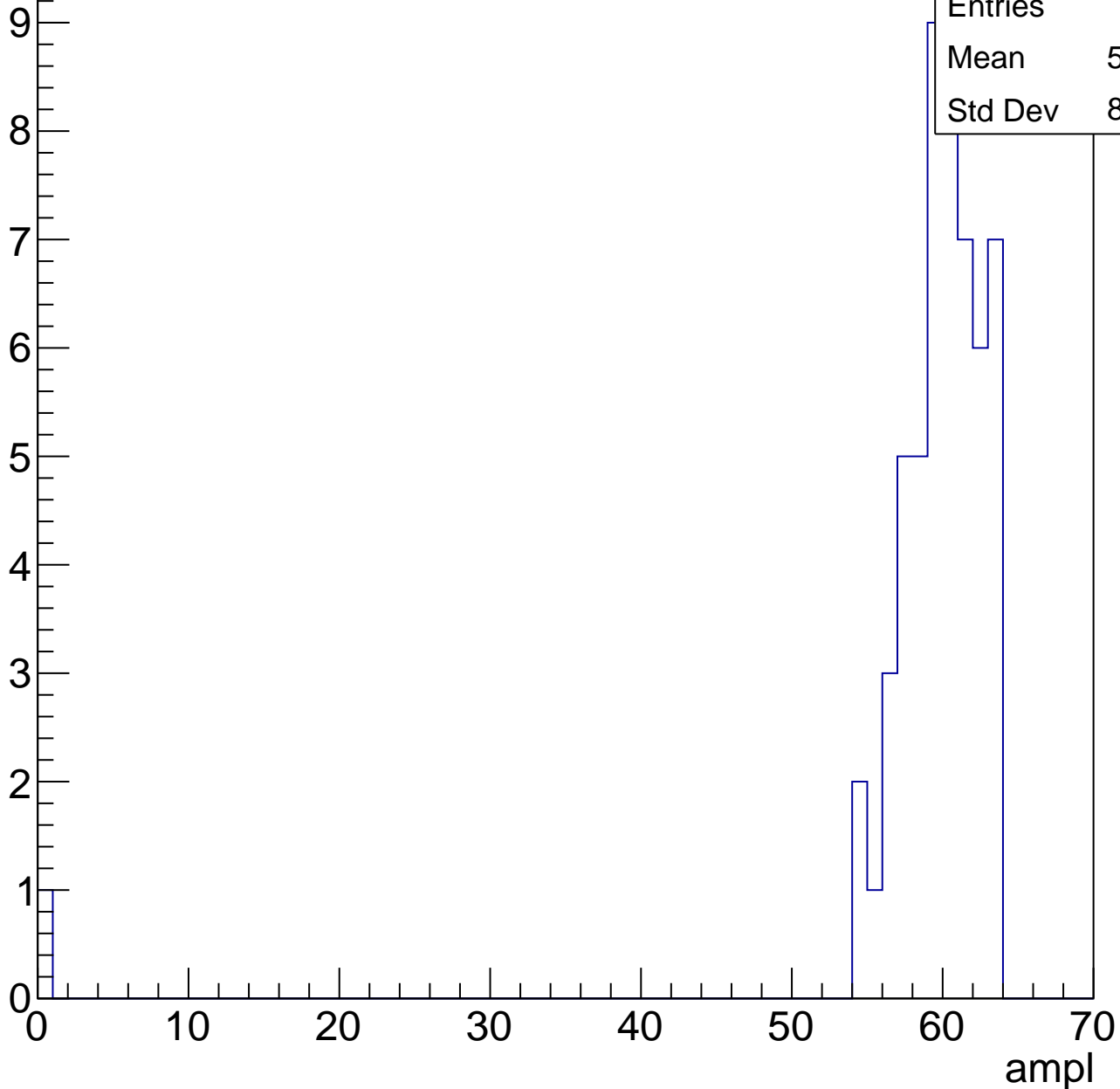


# B0L000S, U19-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

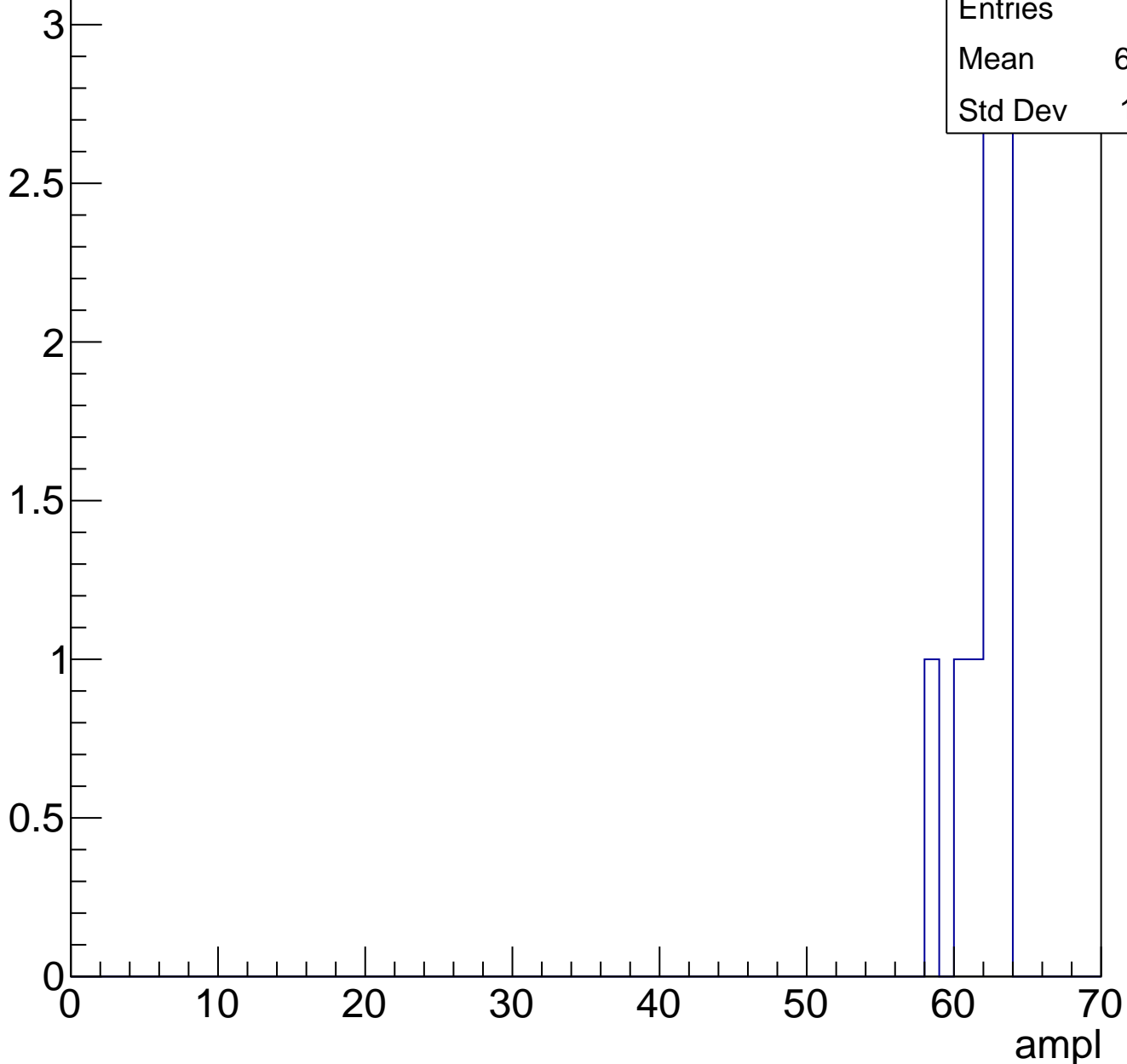
Entries	54
Mean	58.46
Std Dev	8.373



# B0L000S, U19-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch24, adc0

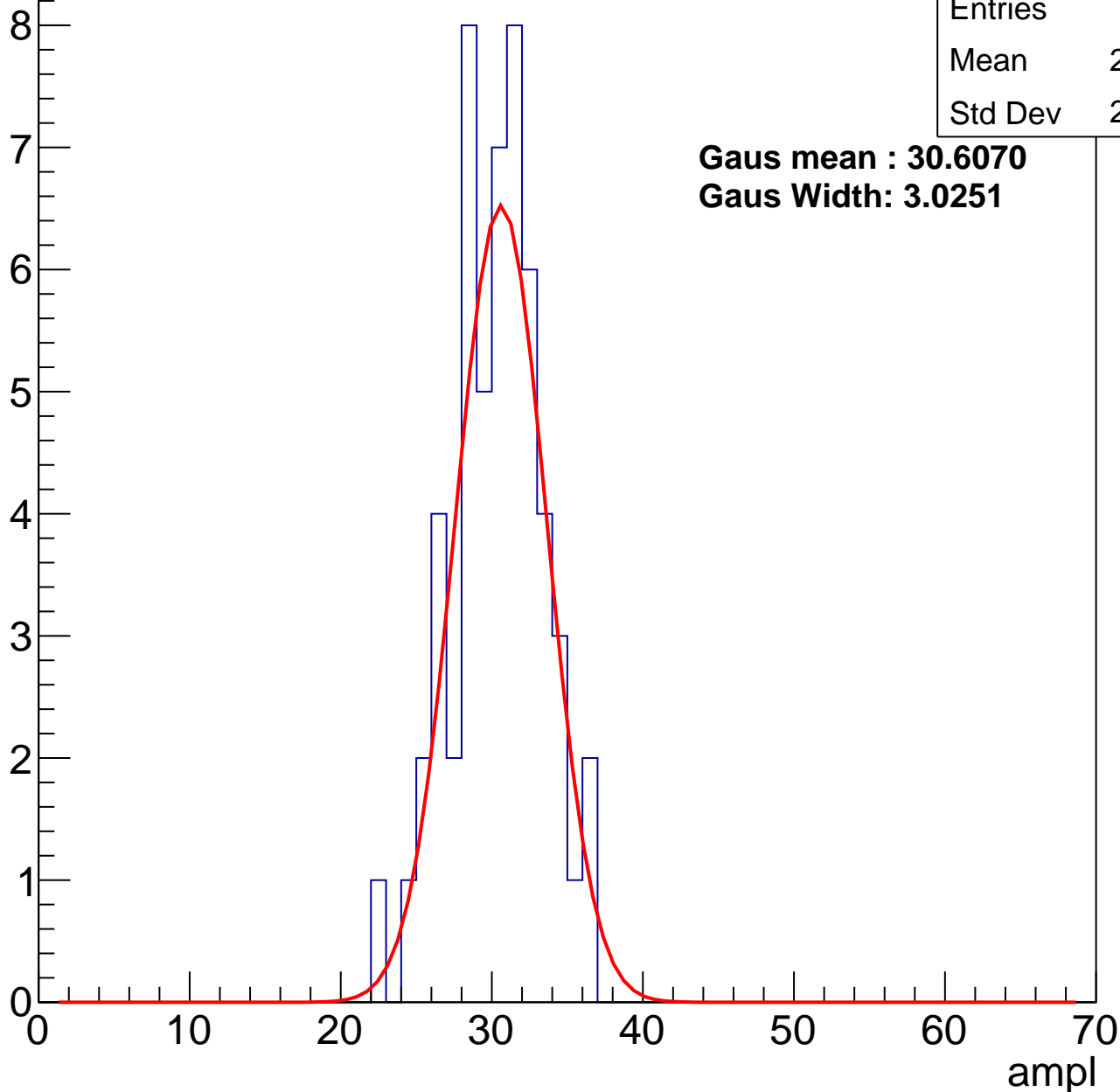
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	29.89
Std Dev	2.986

**Gaus mean : 30.6070**

**Gaus Width: 3.0251**



# B0L000S, U19-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	36.07
Std Dev	3.216

**Gaus mean : 36.5160**

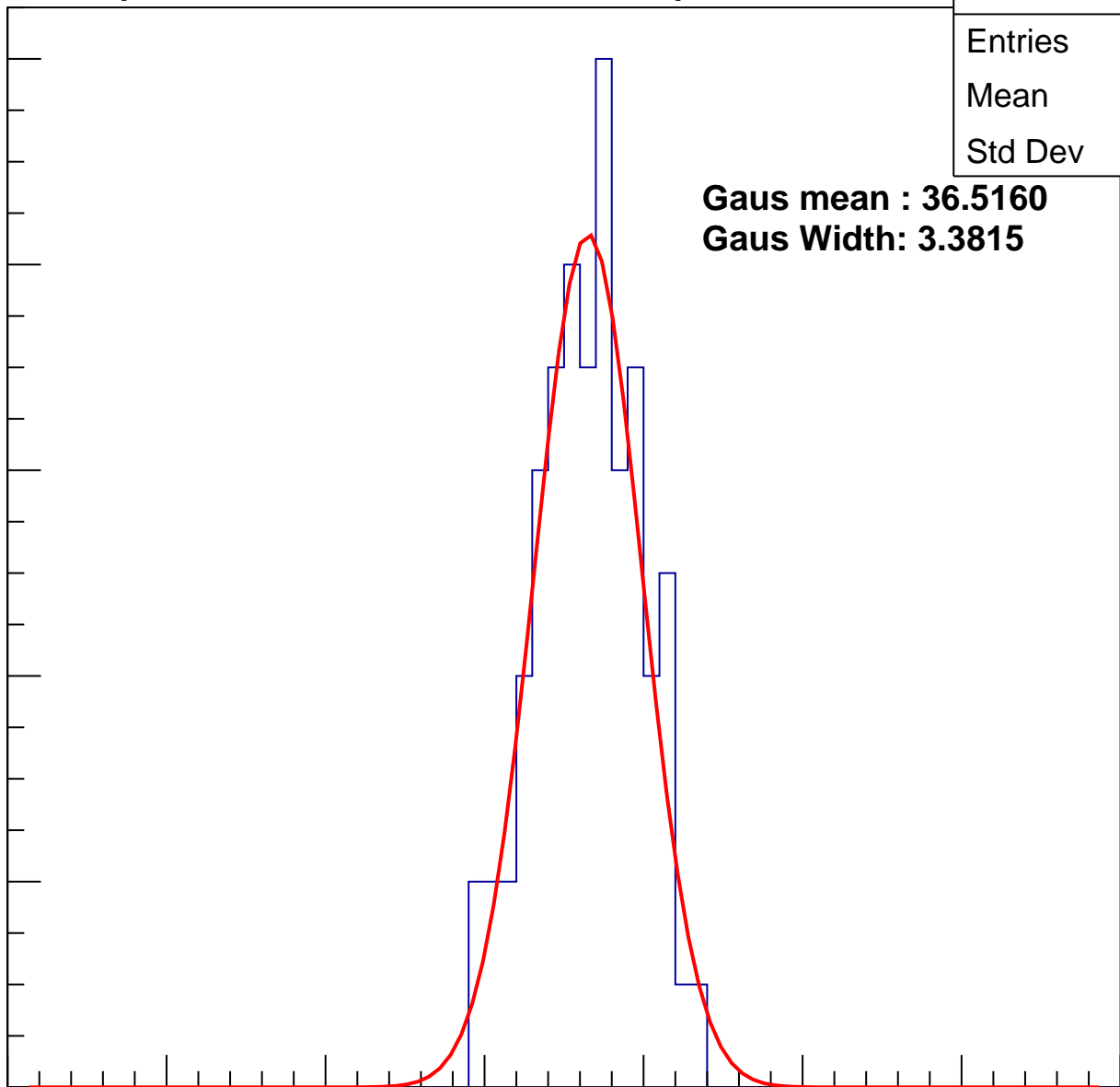
**Gaus Width: 3.3815**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch24, adc2

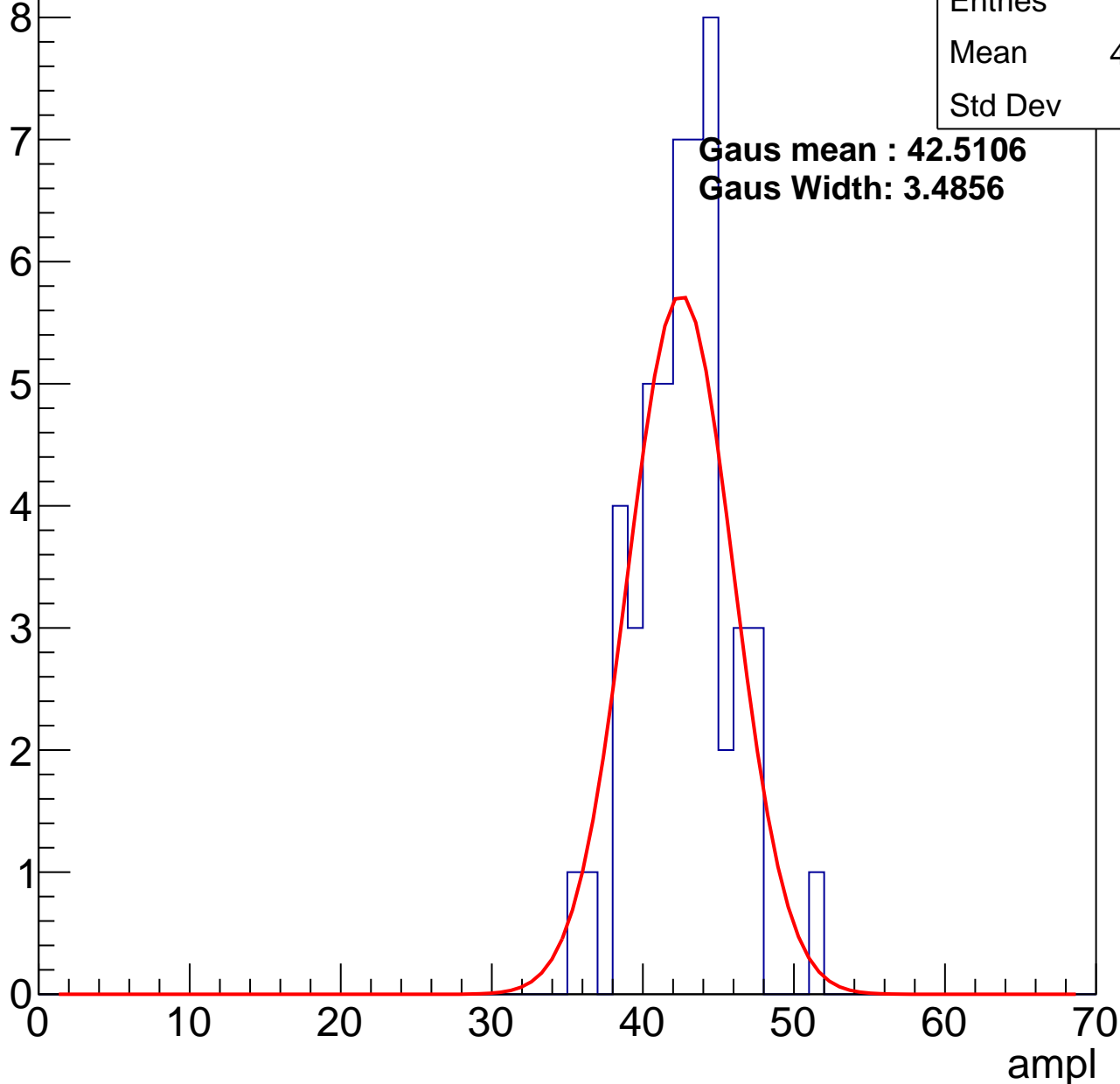
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	42.24
Std Dev	3.03

**Gaus mean : 42.5106**

**Gaus Width: 3.4856**



# B0L000S, U19-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	47.91
Std Dev	3.046

Entry

10

8

6

4

2

0

0

10

20

30

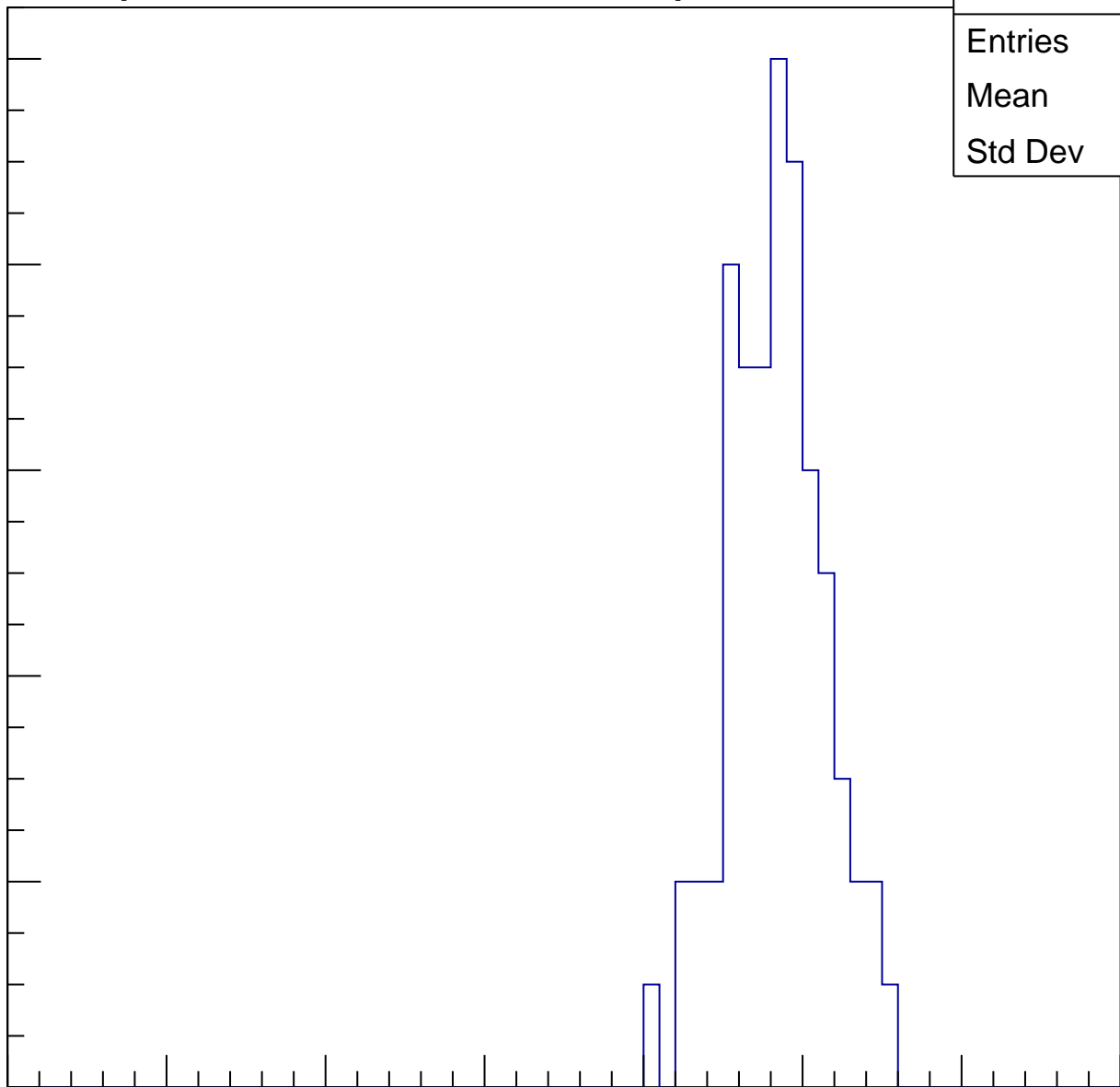
40

50

60

70

ampl

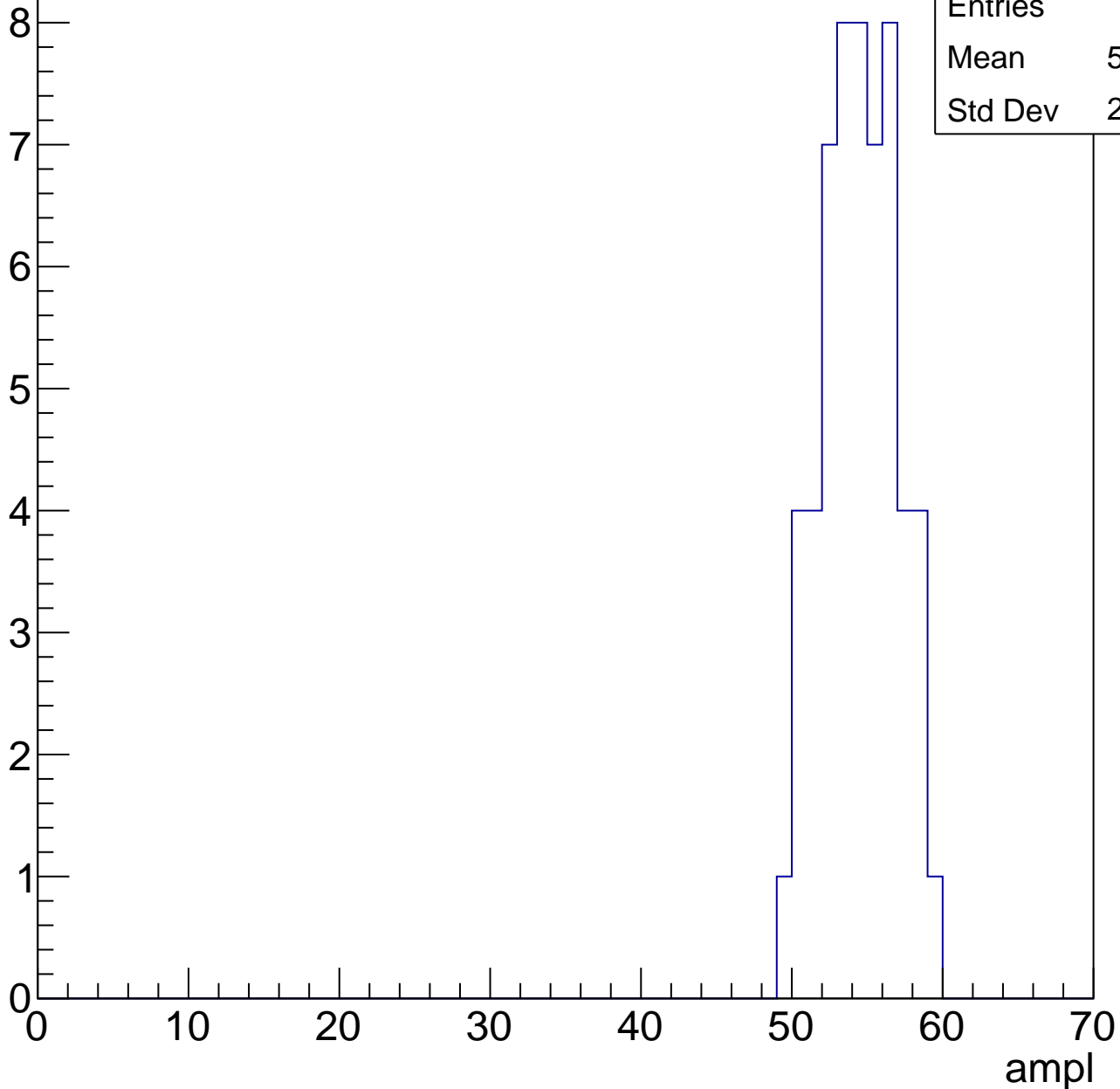


# B0L000S, U19-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	54.02
Std Dev	2.409



# B0L000S, U19-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

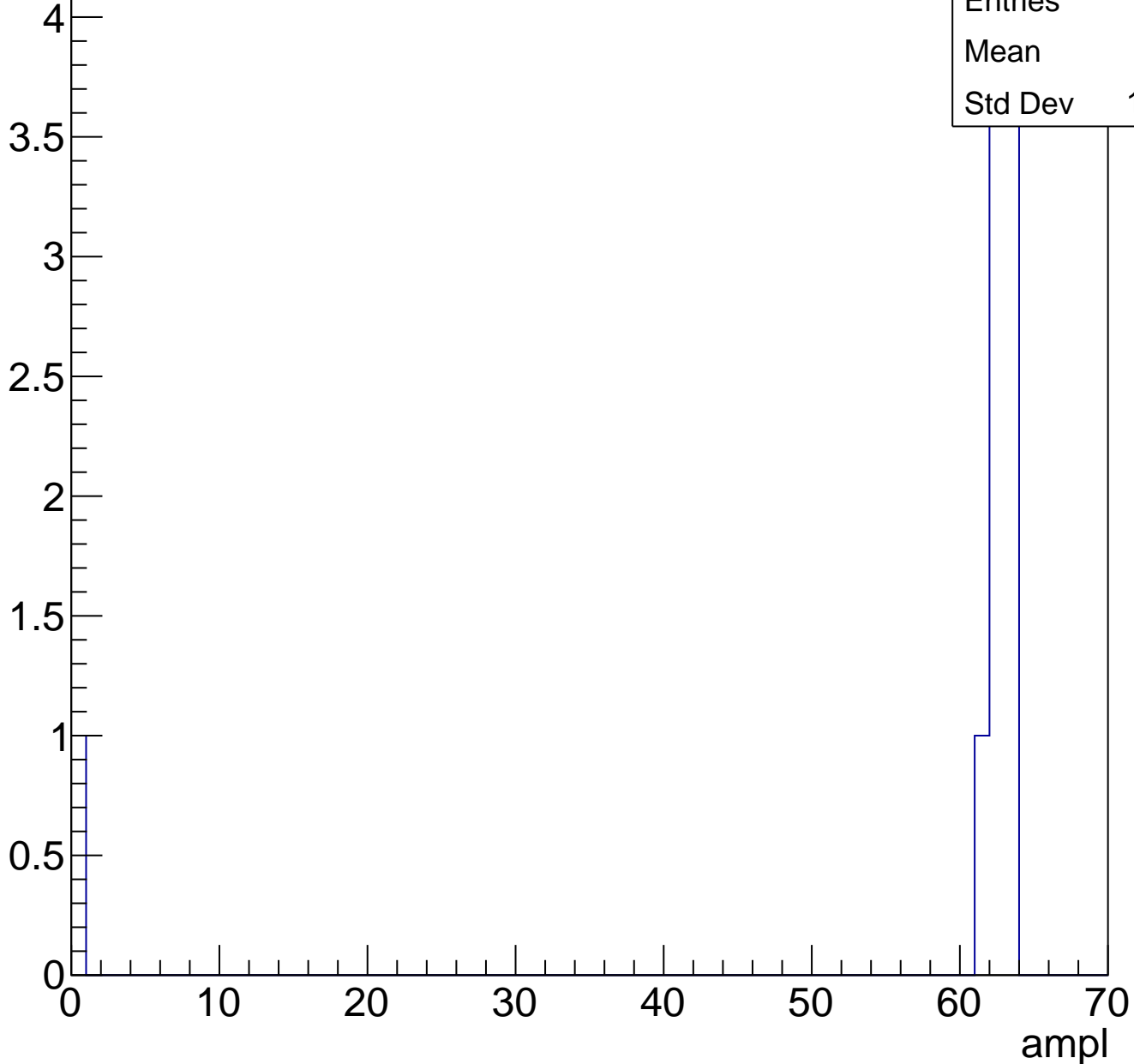
ampl

Entries	56
Mean	59.68
Std Dev	2.114

# B0L000S, U19-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U19-ch25, adc0

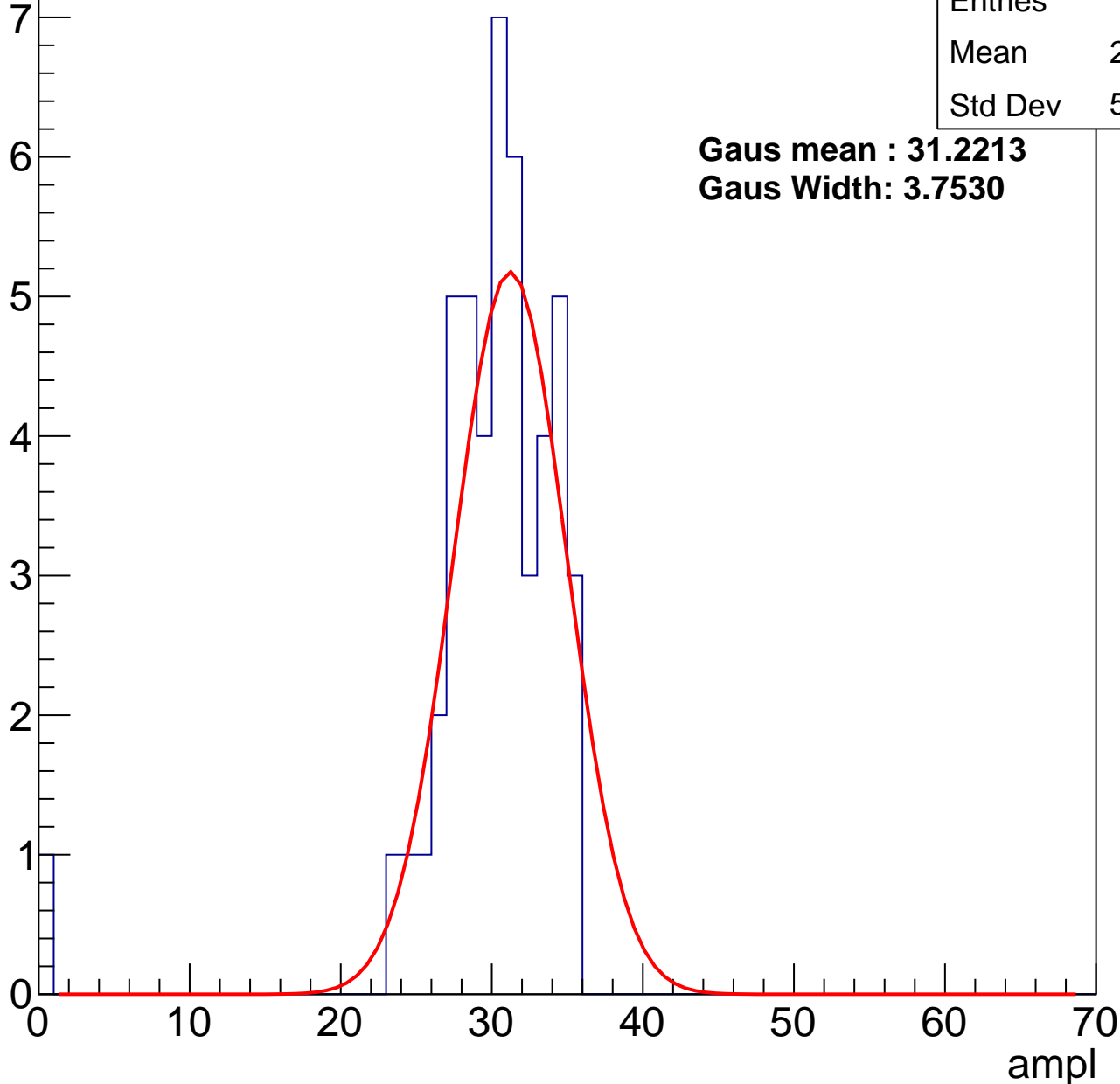
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	29.46
Std Dev	5.216

**Gaus mean : 31.2213**

**Gaus Width: 3.7530**



# B0L000S, U19-ch25, adc1

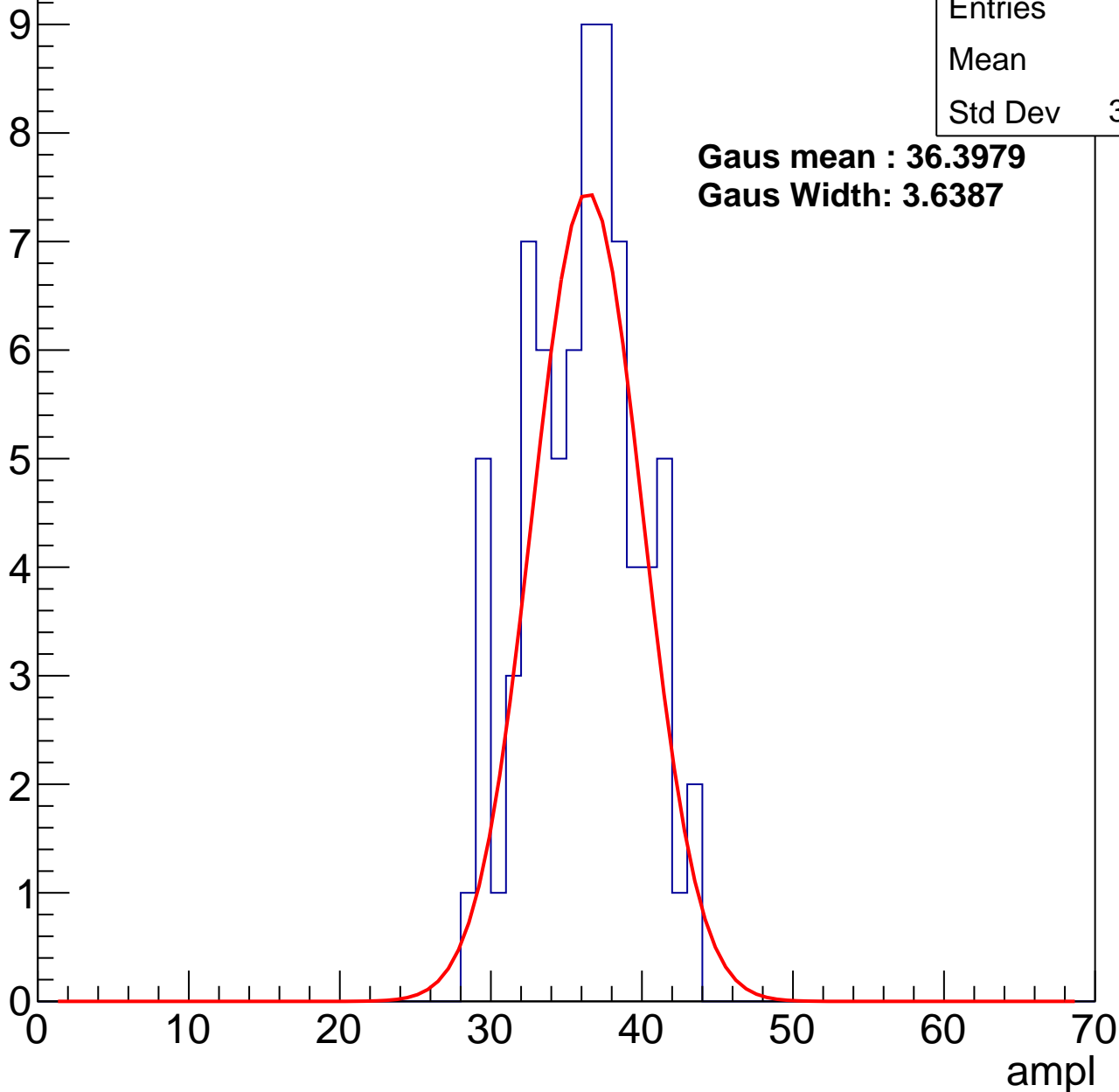
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	35.6
Std Dev	3.644

**Gaus mean : 36.3979**

**Gaus Width: 3.6387**



# B0L000S, U19-ch25, adc2

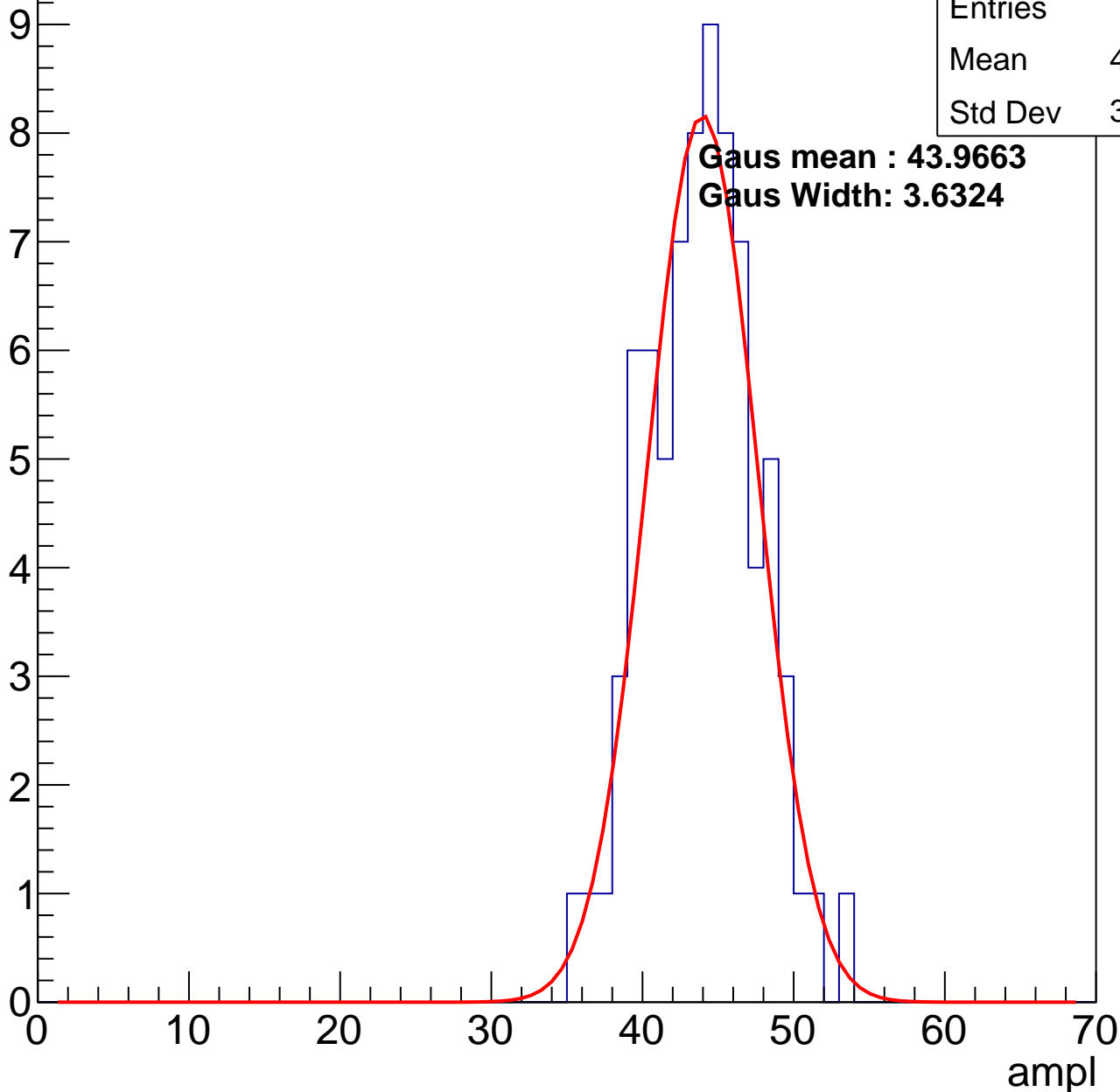
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	43.45
Std Dev	3.602

**Gaus mean : 43.9663**

**Gaus Width: 3.6324**

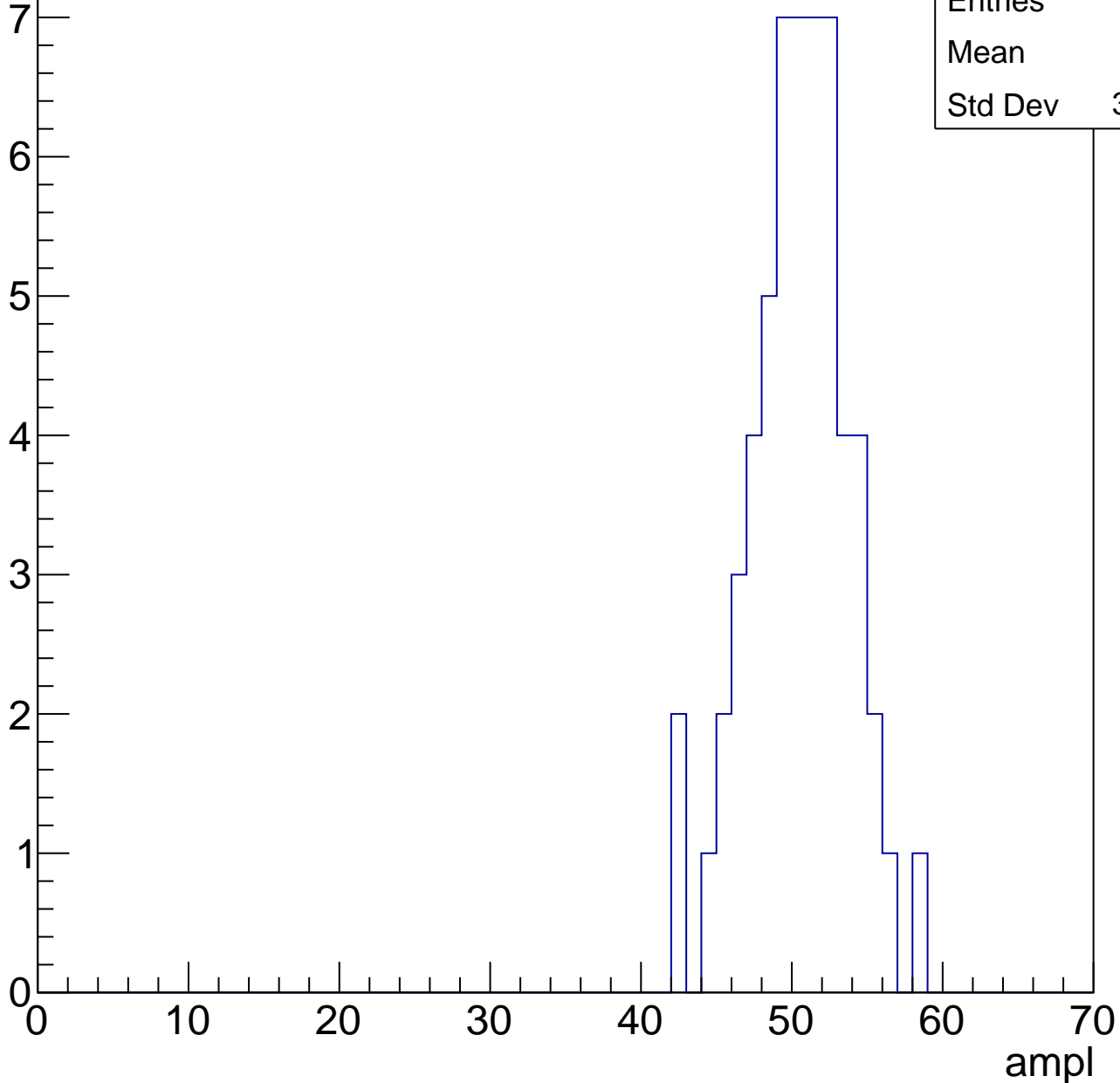


# B0L000S, U19-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	50
Std Dev	3.271

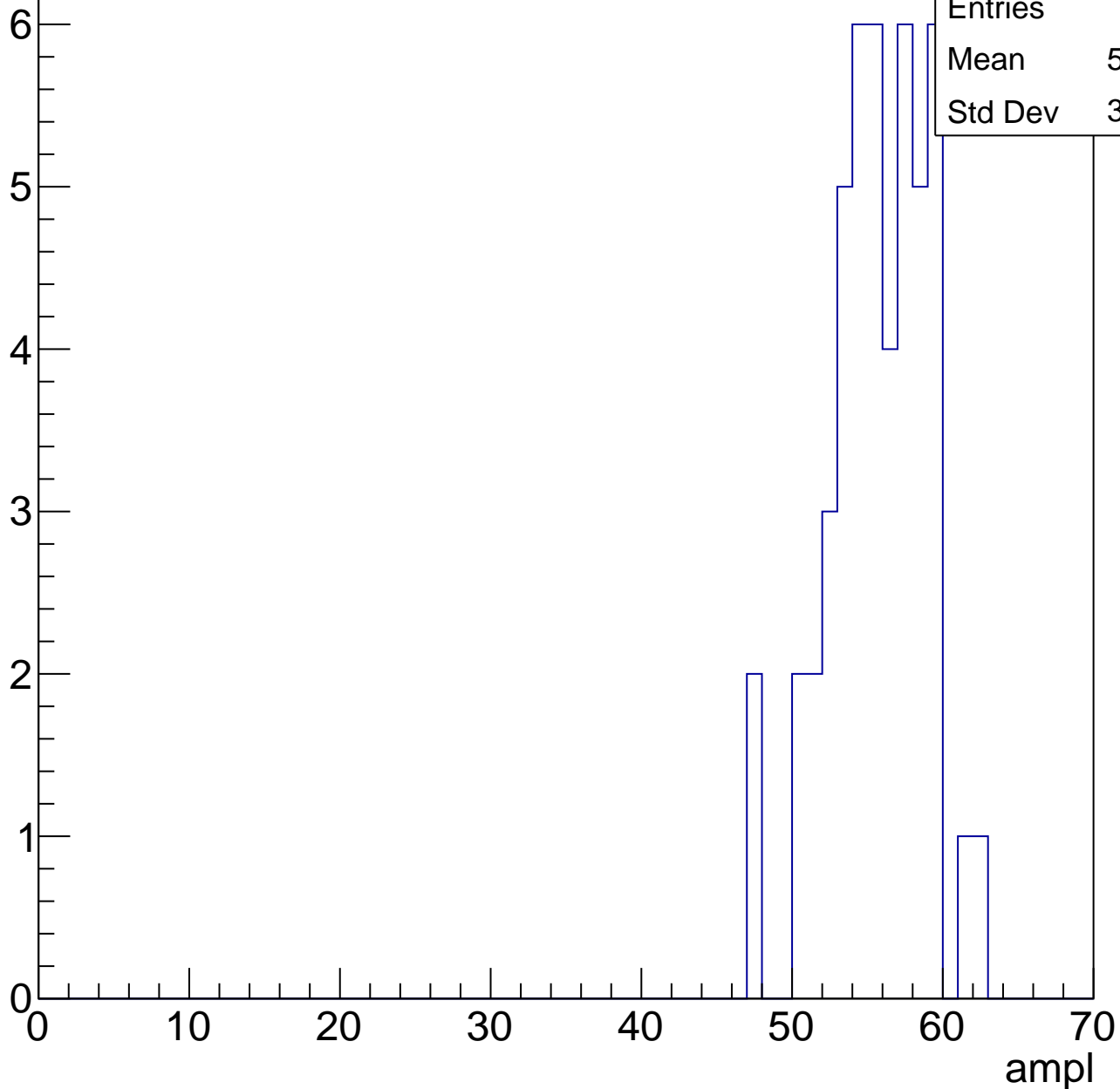


# B0L000S, U19-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.18
Std Dev	3.243



# B0L000S, U19-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

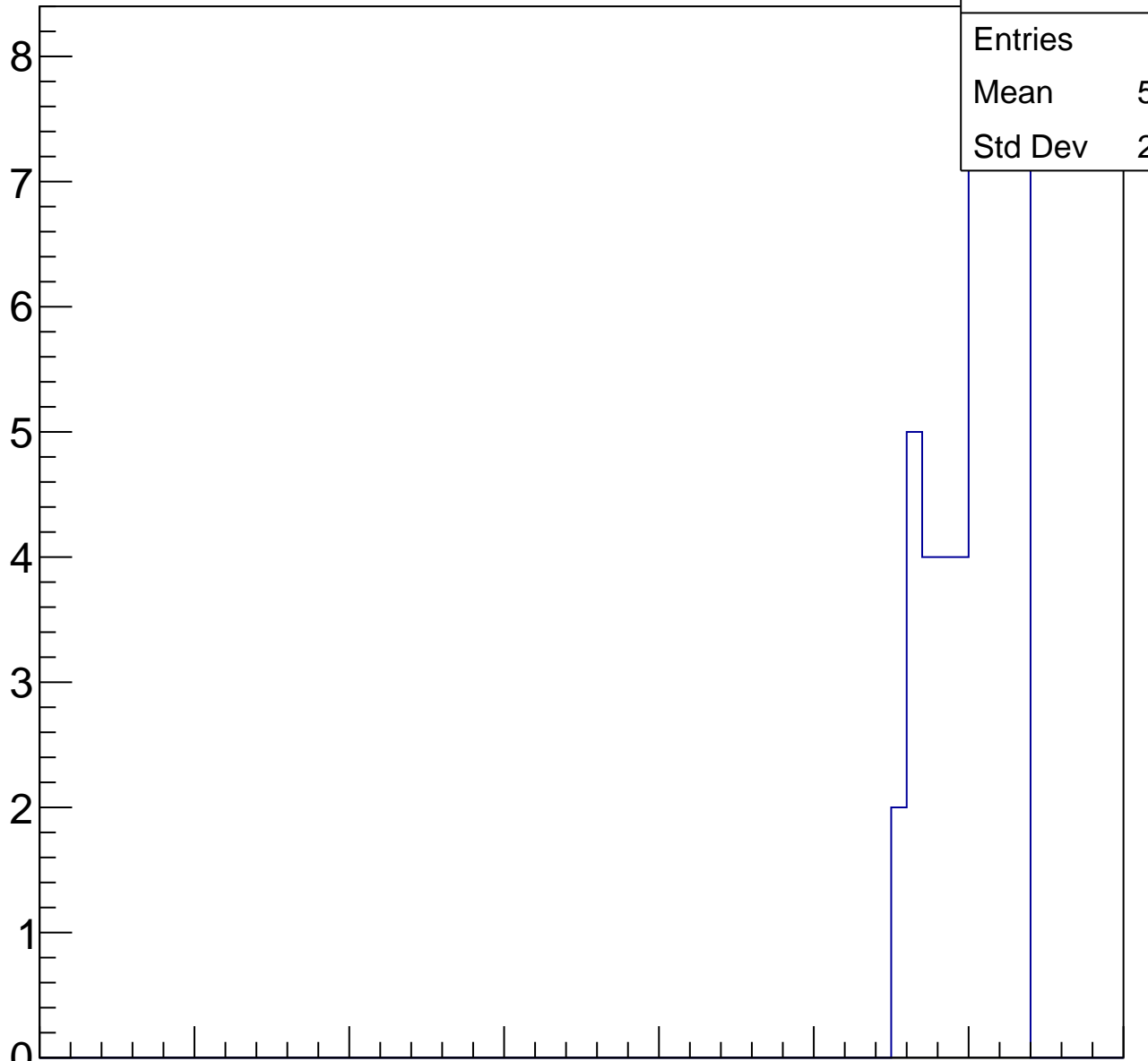
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.88
Std Dev	2.414

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

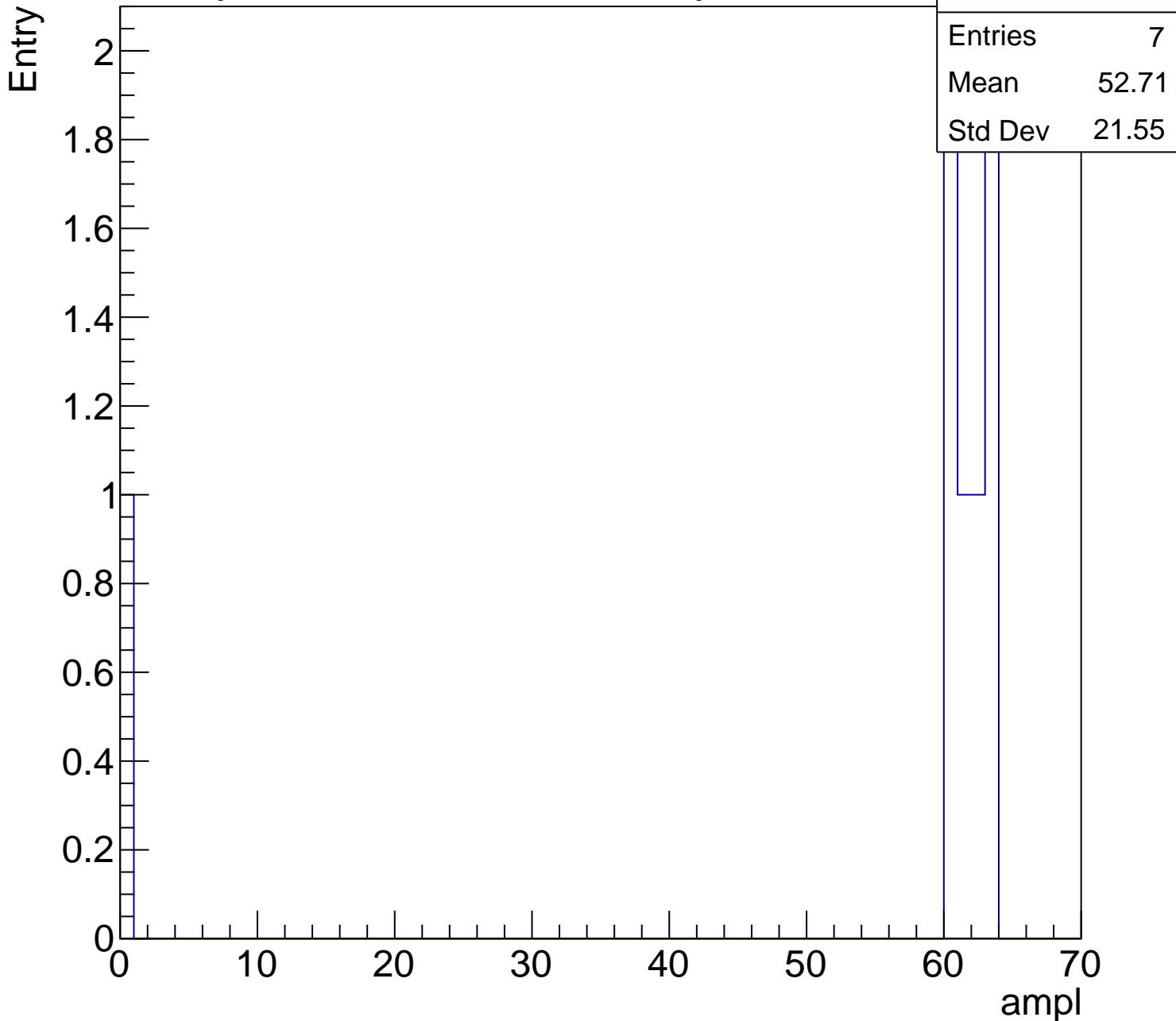
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.71
Std Dev	21.55

0 10 20 30 40 50 60 70

ampl





# B0L000S, U19-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch26, adc0

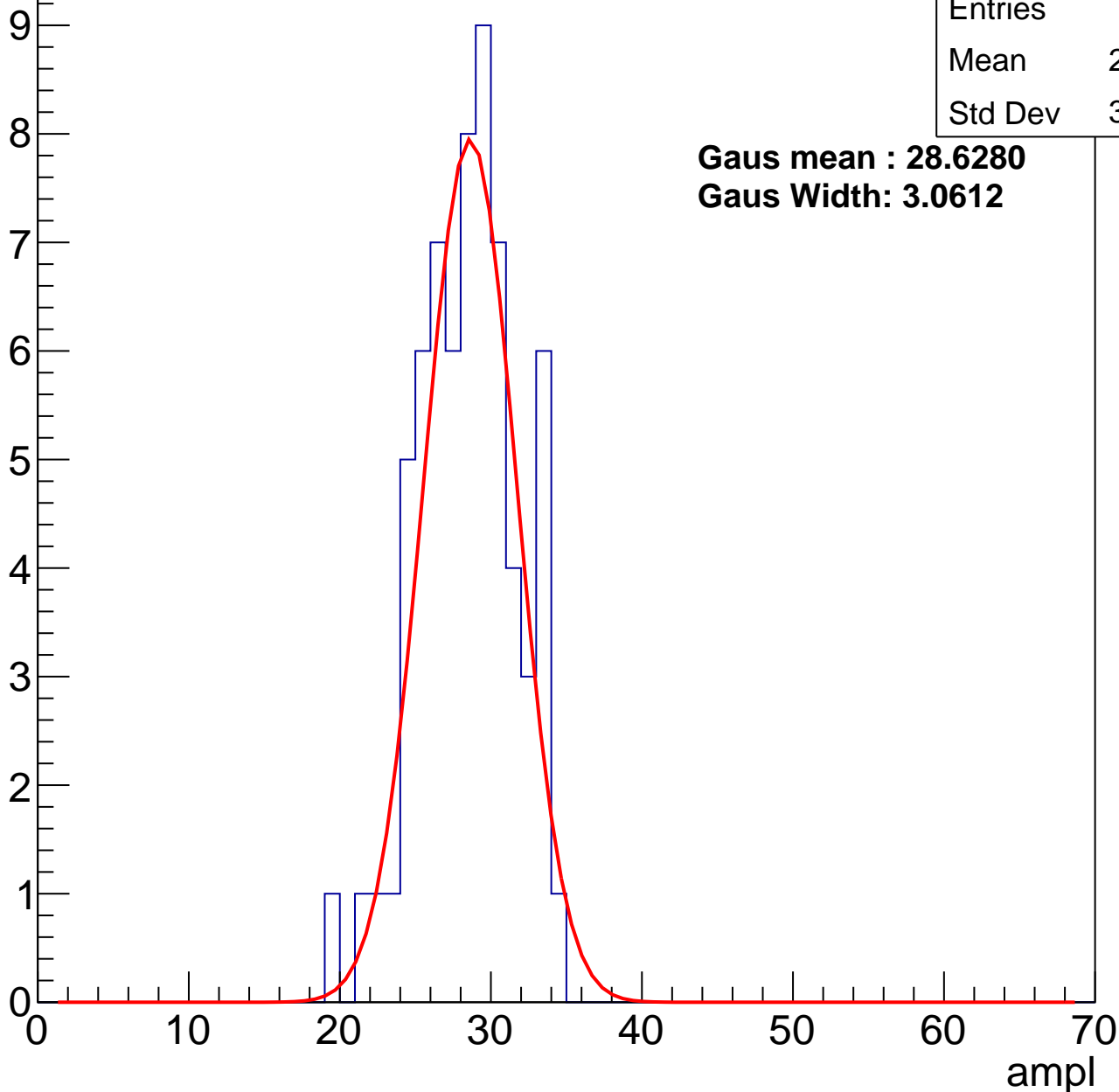
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	27.97
Std Dev	3.176

**Gaus mean : 28.6280**

**Gaus Width: 3.0612**



# B0L000S, U19-ch26, adc1

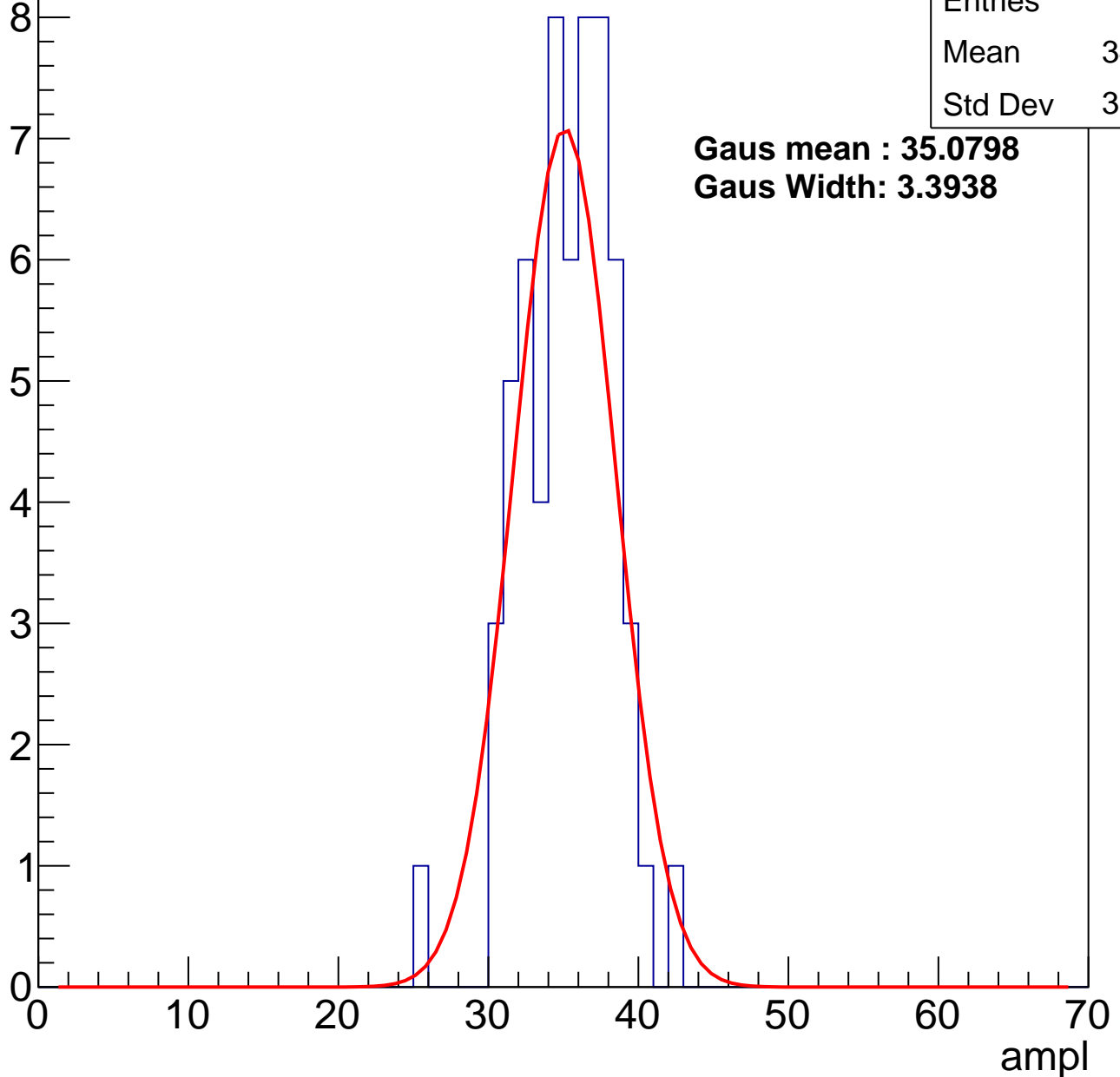
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	34.78
Std Dev	3.017

**Gaus mean : 35.0798**

**Gaus Width: 3.3938**



# B0L000S, U19-ch26, adc2

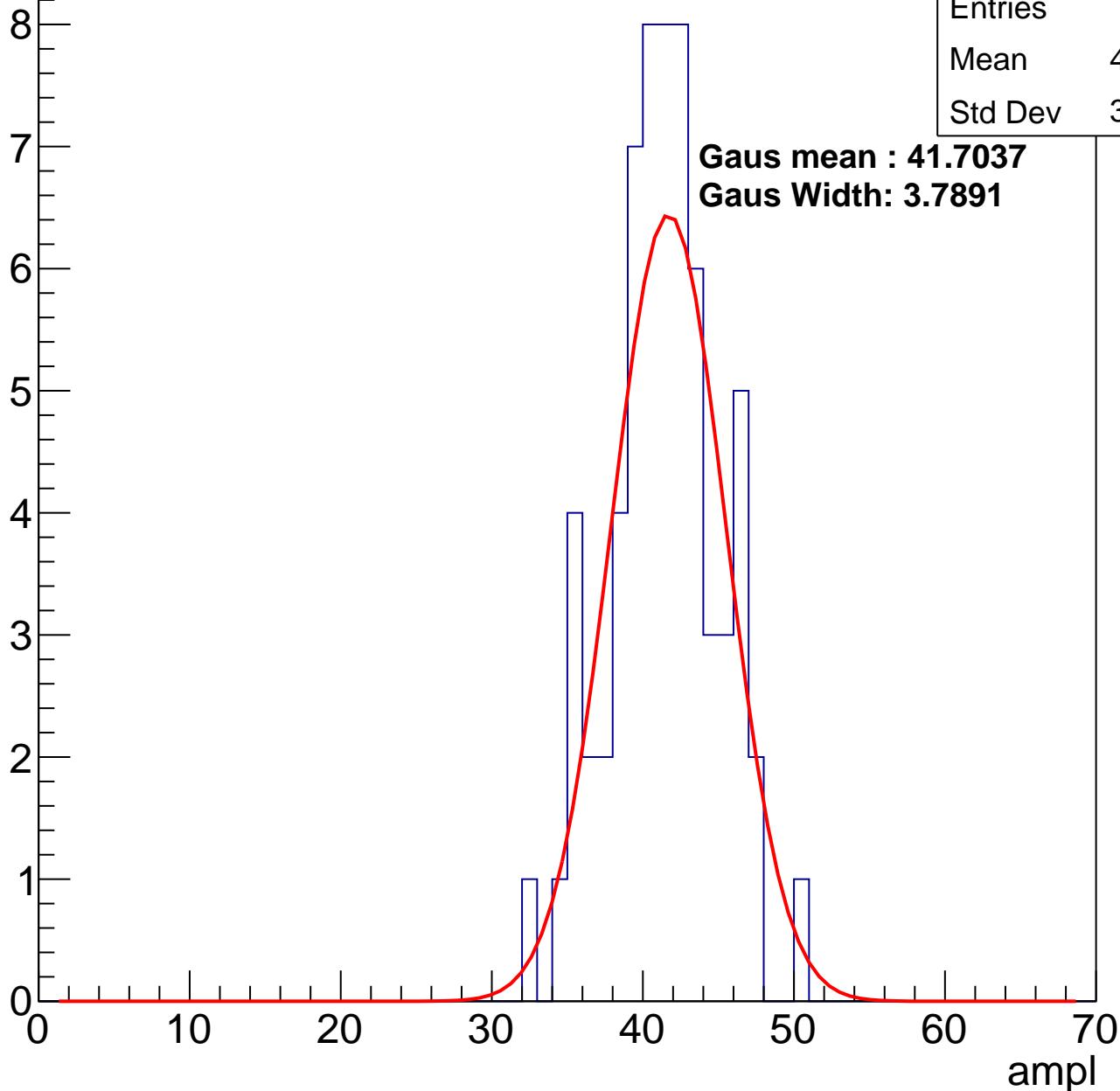
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	40.92
Std Dev	3.536

**Gaus mean : 41.7037**

**Gaus Width: 3.7891**

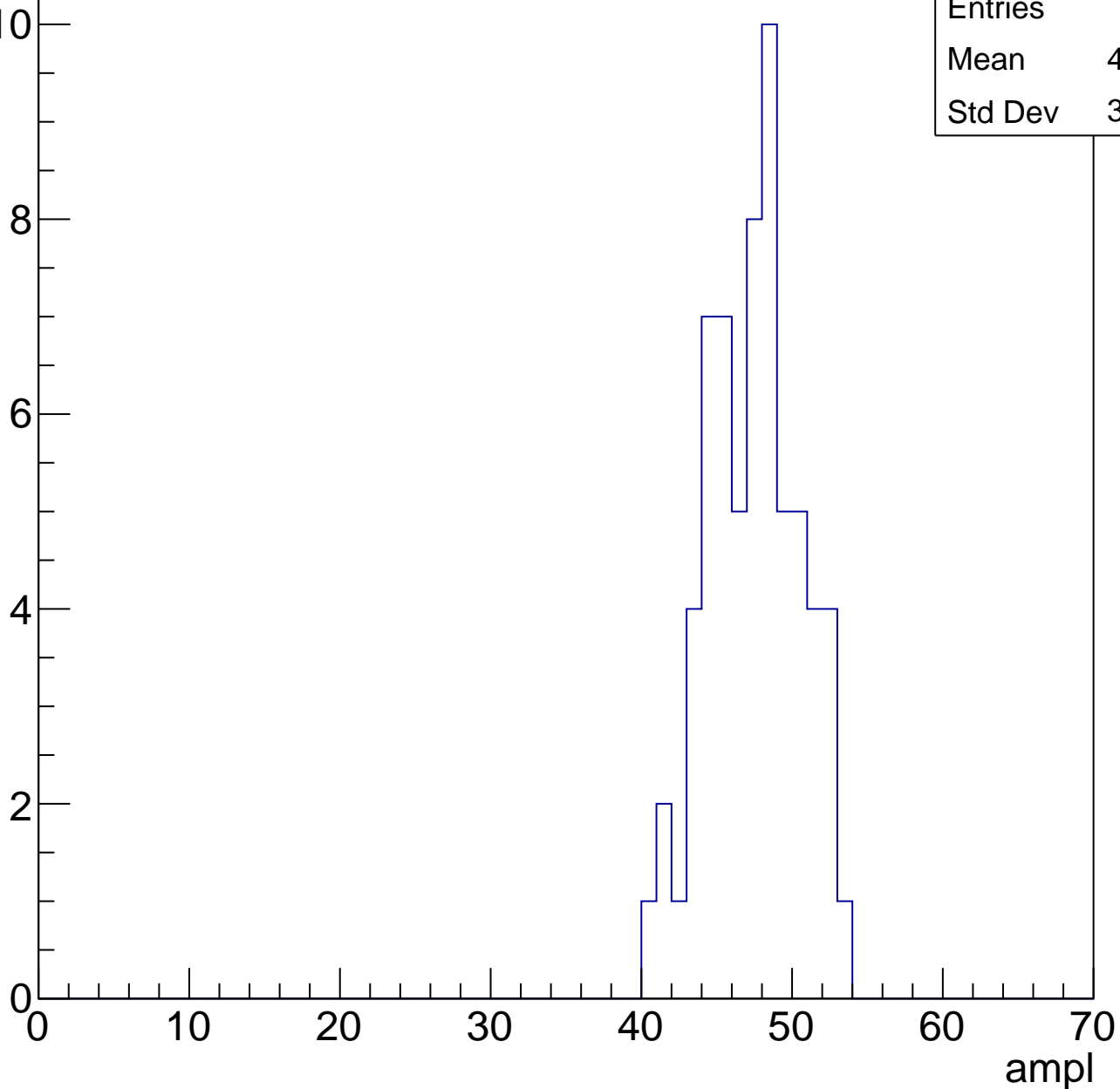


# B0L000S, U19-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	46.95
Std Dev	3.013

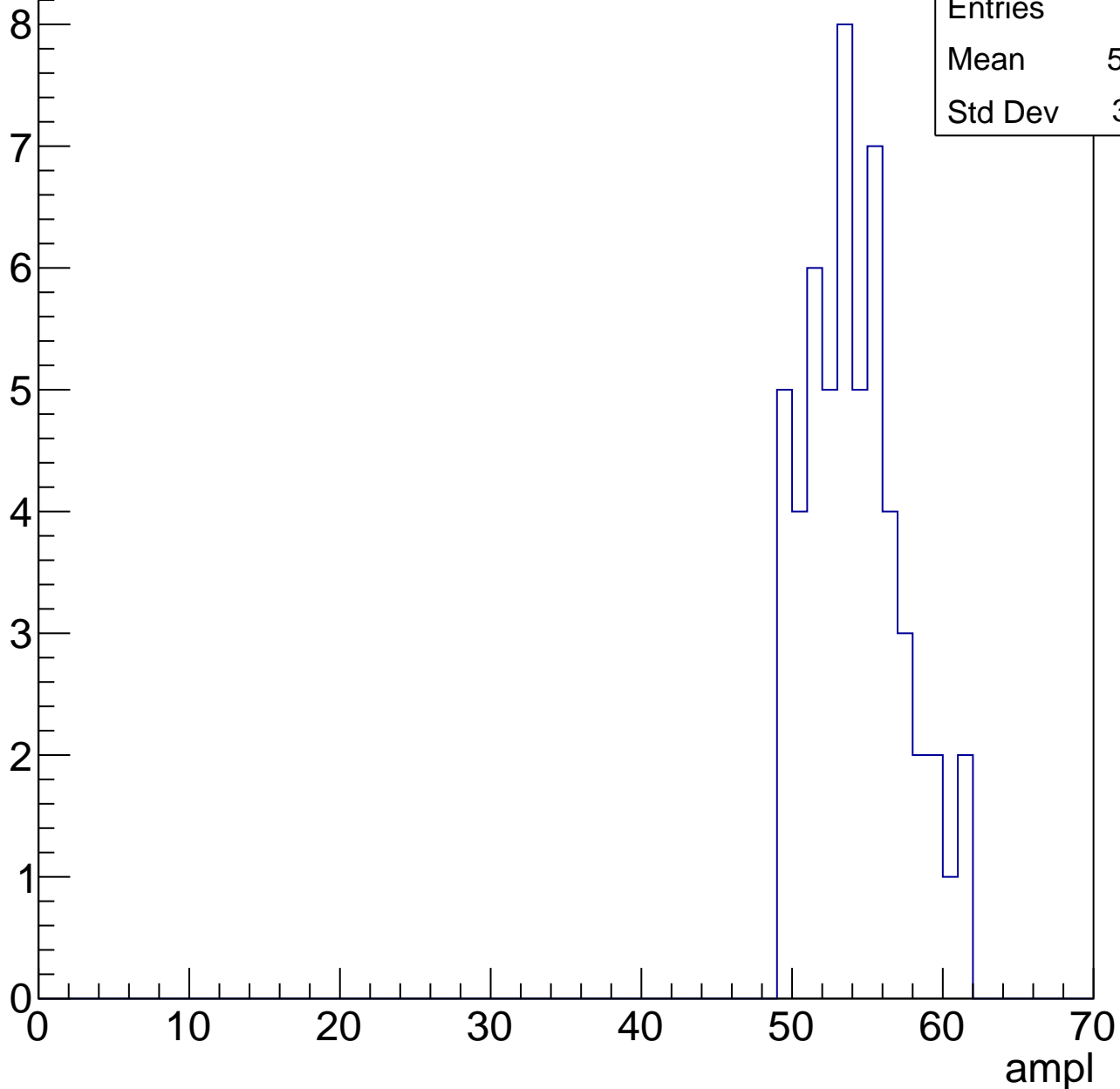


# B0L000S, U19-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	53.72
Std Dev	3.141

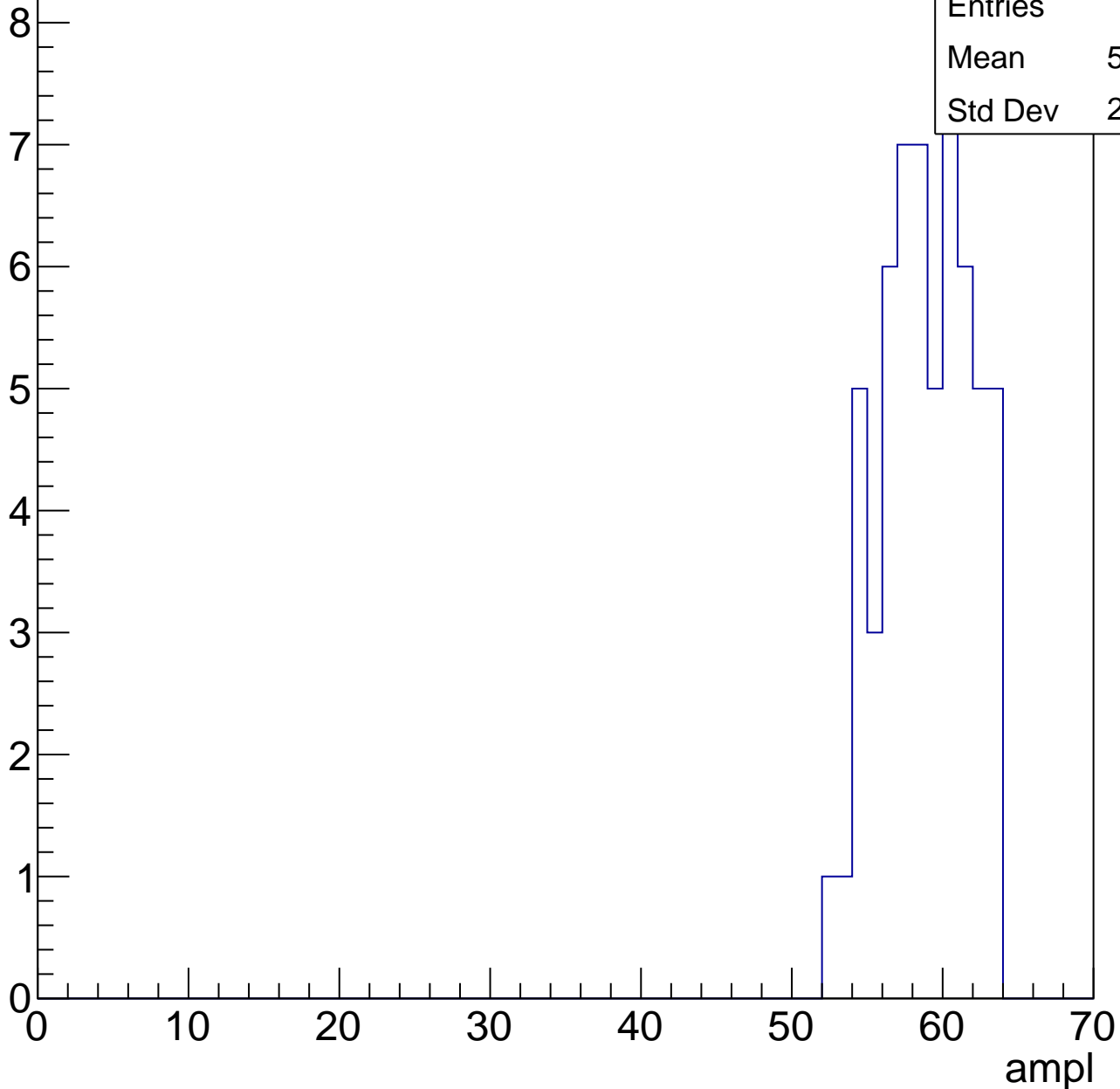


# B0L000S, U19-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	58.42
Std Dev	2.865

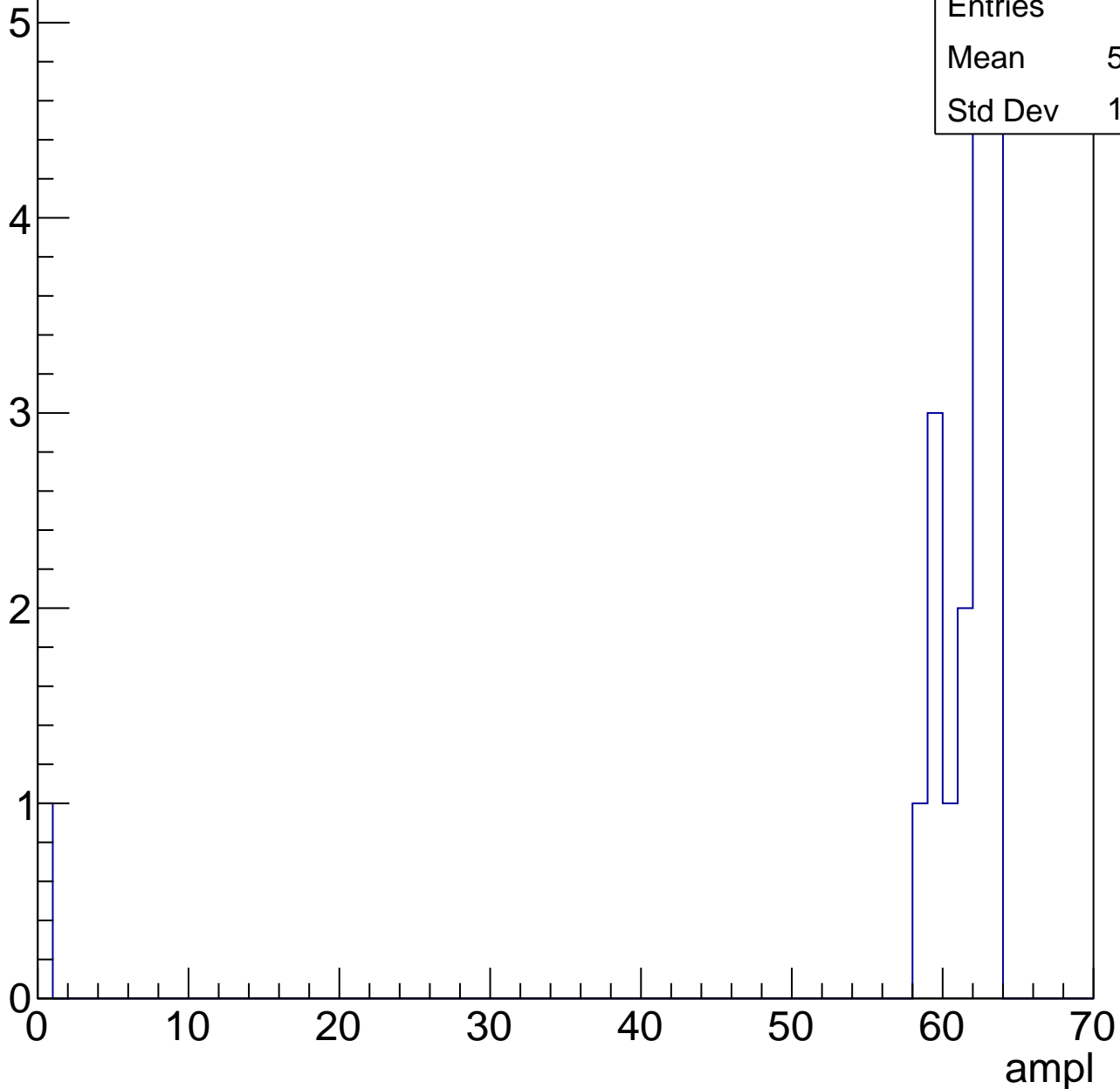


# B0L000S, U19-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	18
Mean	57.89
Std Dev	14.13





# B0L000S, U19-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	29.3
Std Dev	4.869

**Gaus mean : 29.7632**

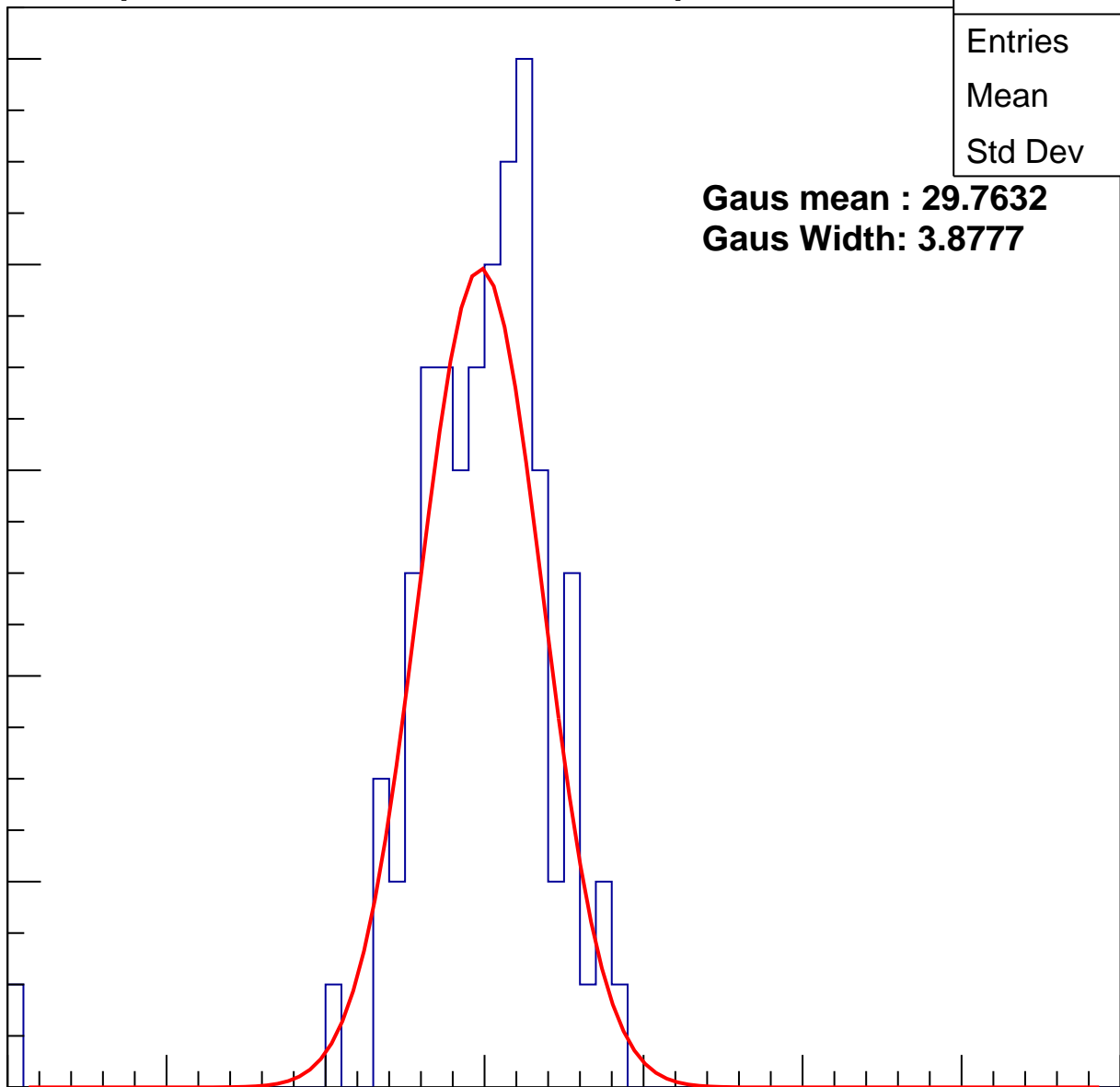
**Gaus Width: 3.8777**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch27, adc1

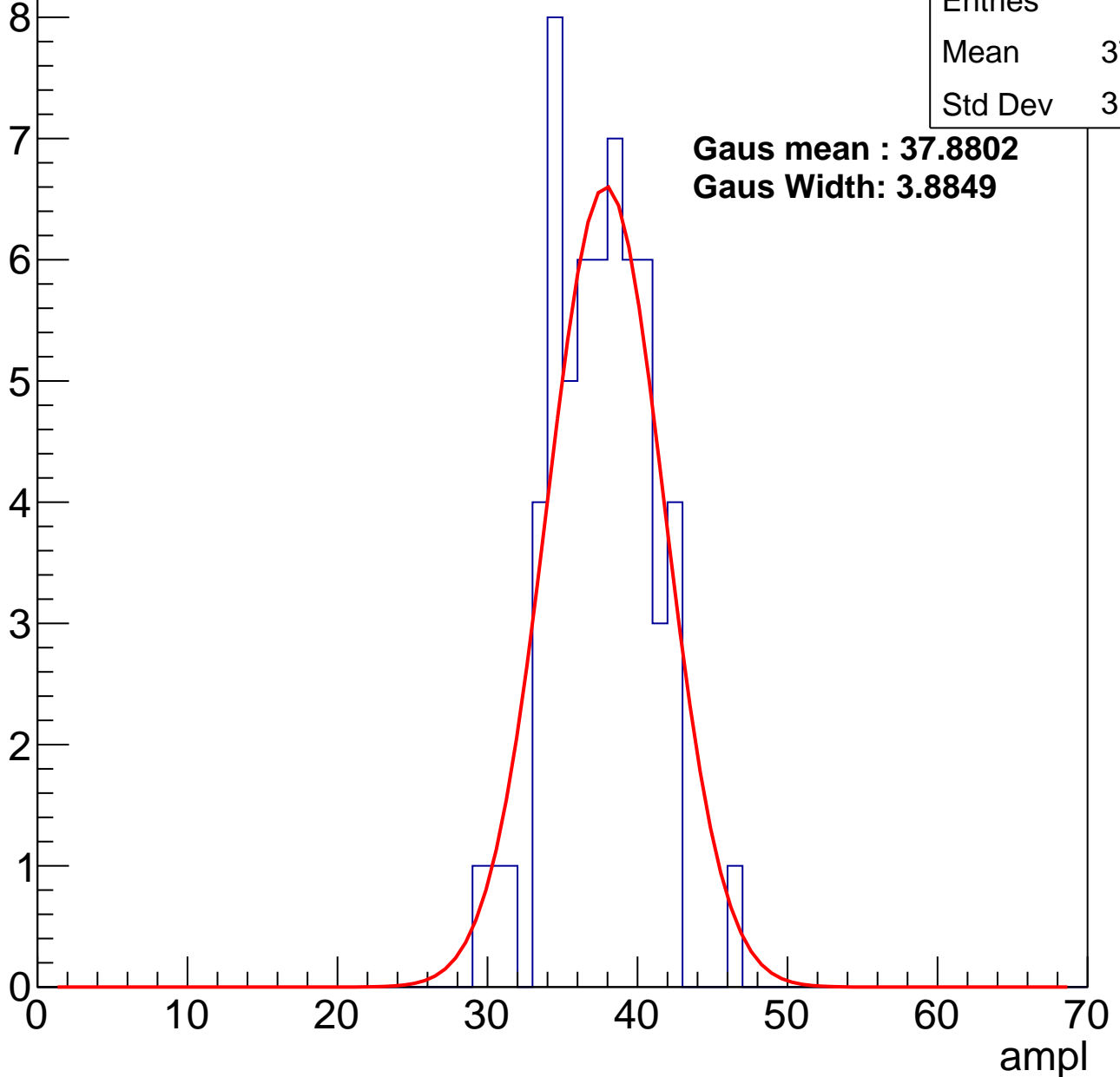
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.02
Std Dev	3.255

**Gaus mean : 37.8802**

**Gaus Width: 3.8849**



# B0L000S, U19-ch27, adc2

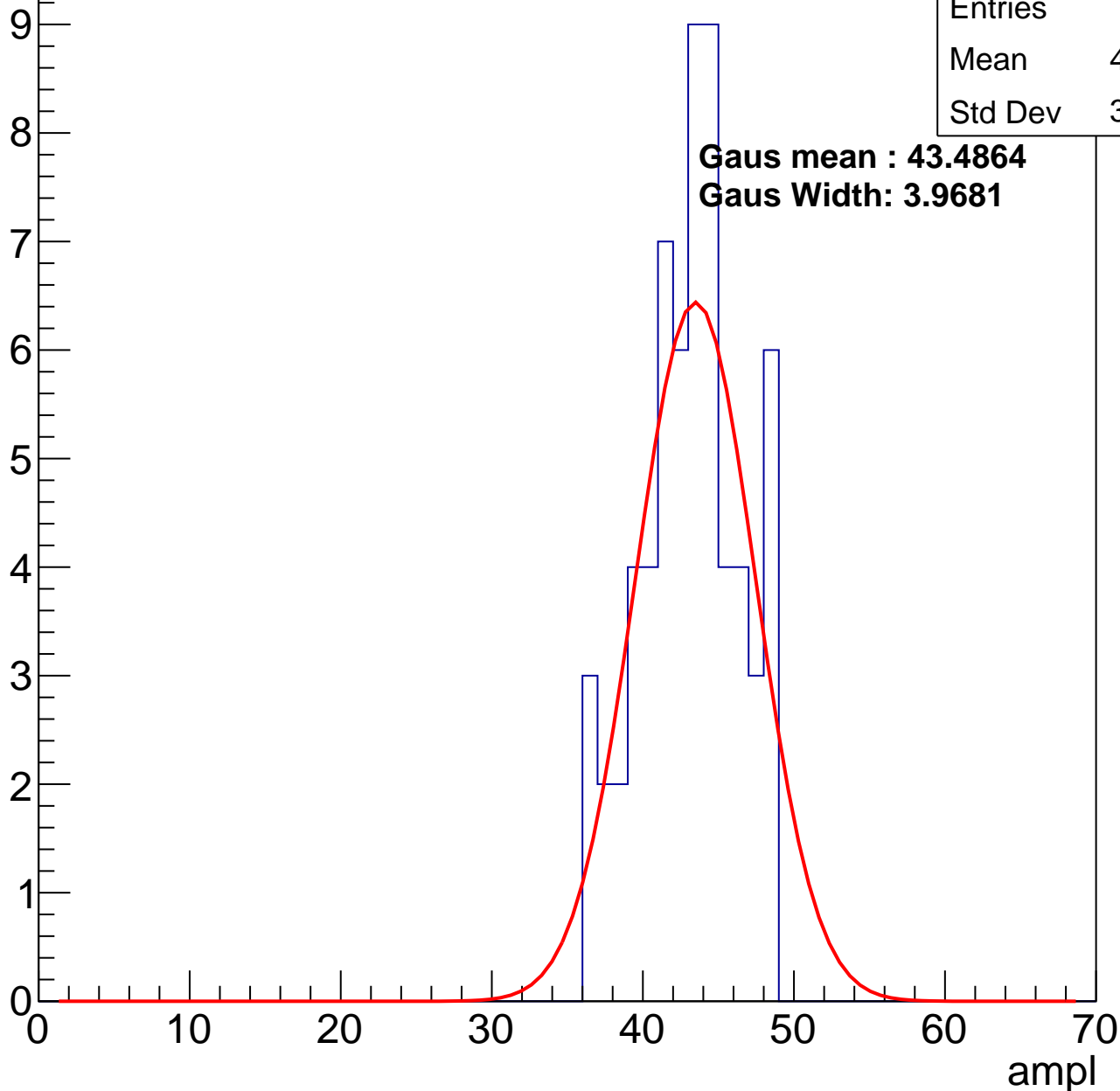
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.68
Std Dev	3.226

**Gaus mean : 43.4864**

**Gaus Width: 3.9681**

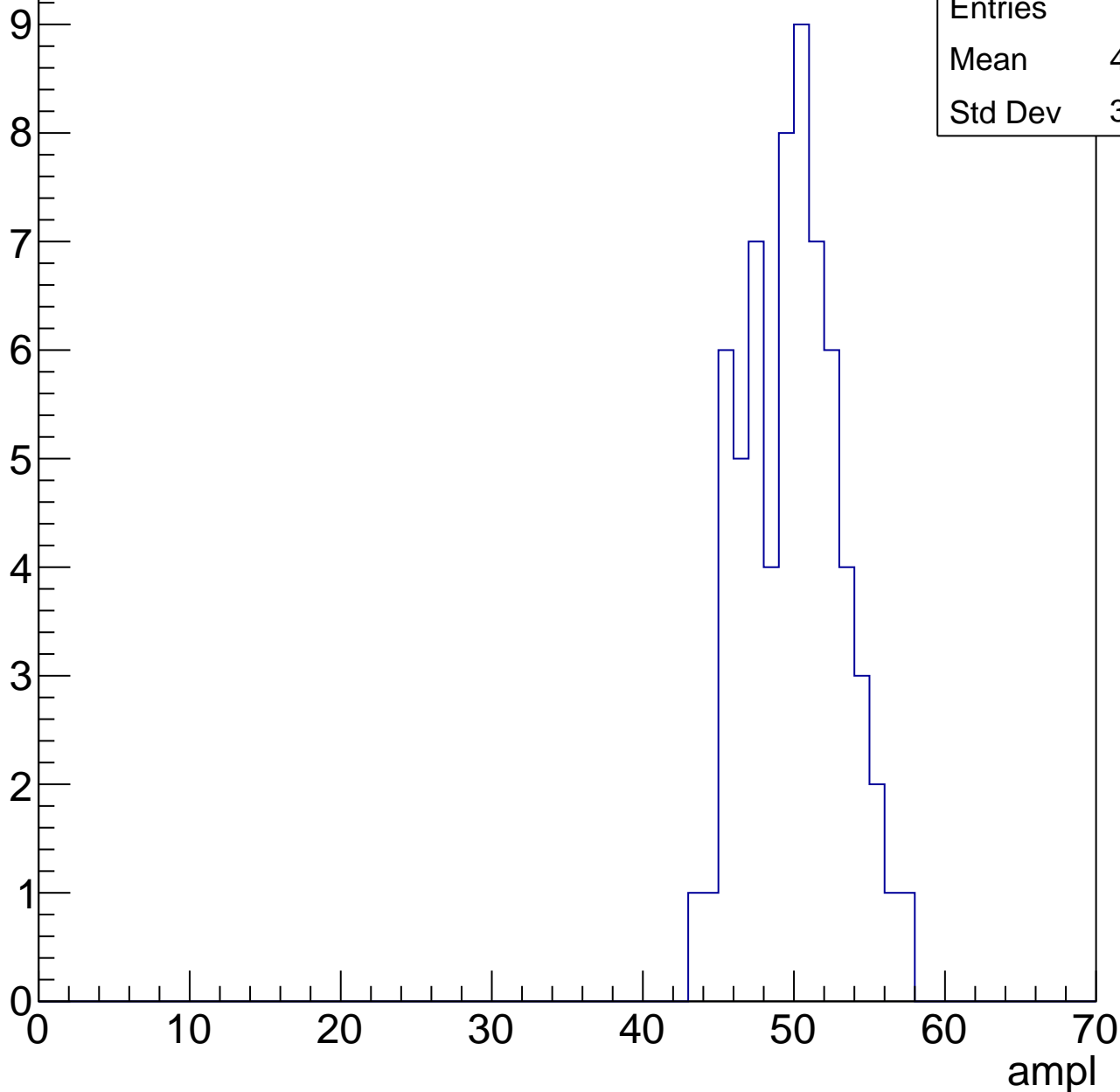


# B0L000S, U19-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

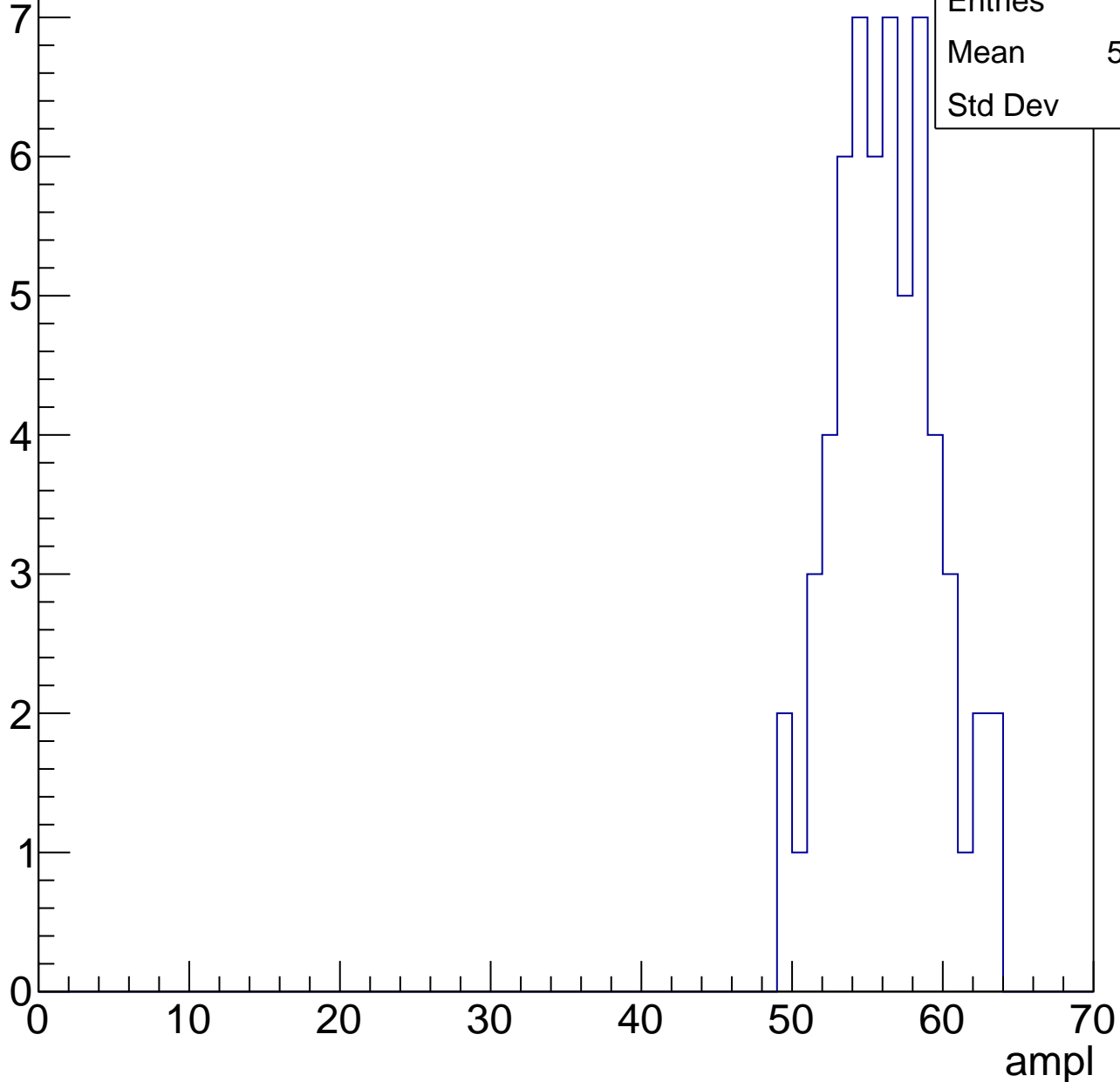
Entries	65
Mean	49.48
Std Dev	3.124



# B0L000S, U19-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

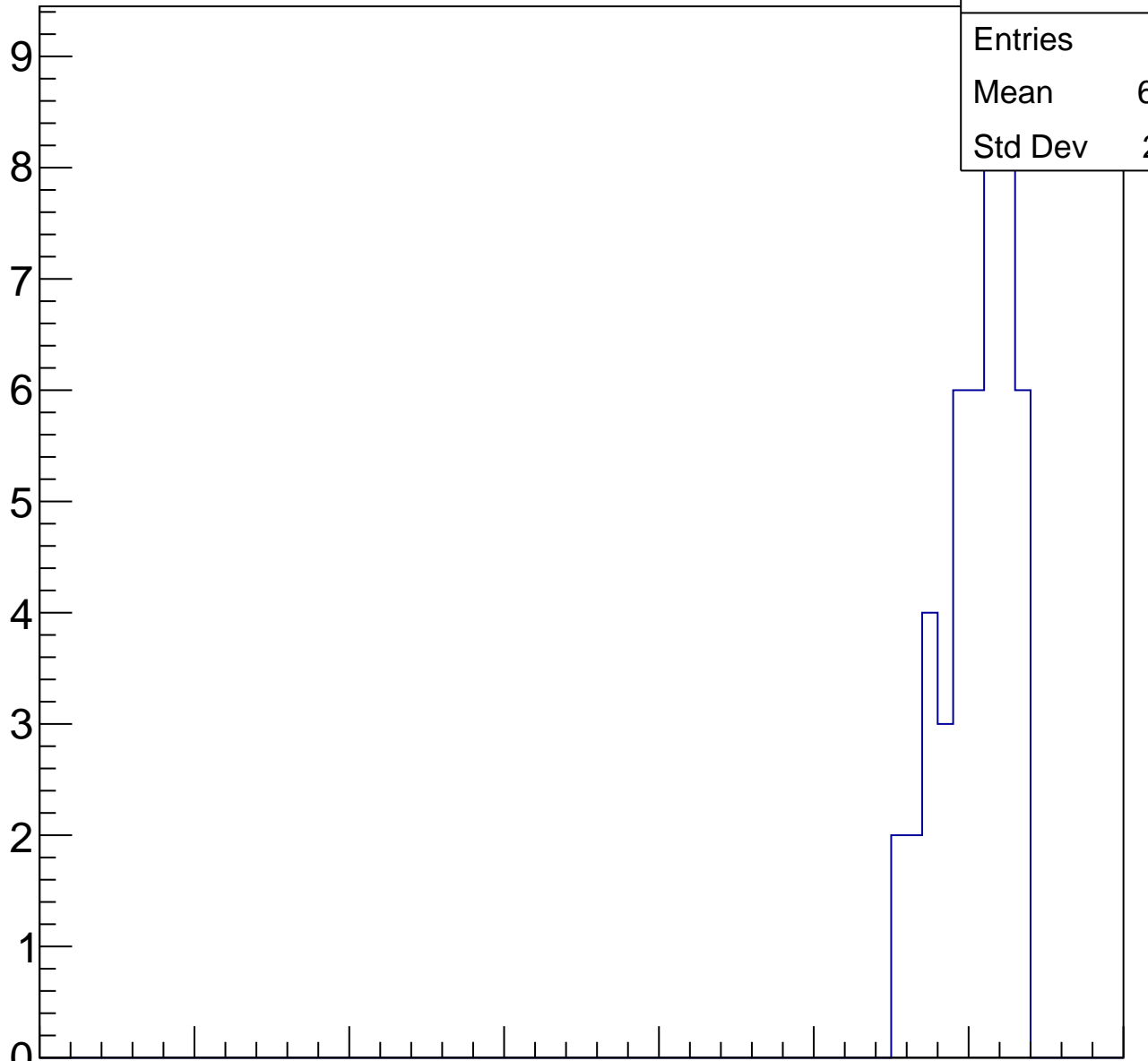
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	60.02
Std Dev	2.241

ampl

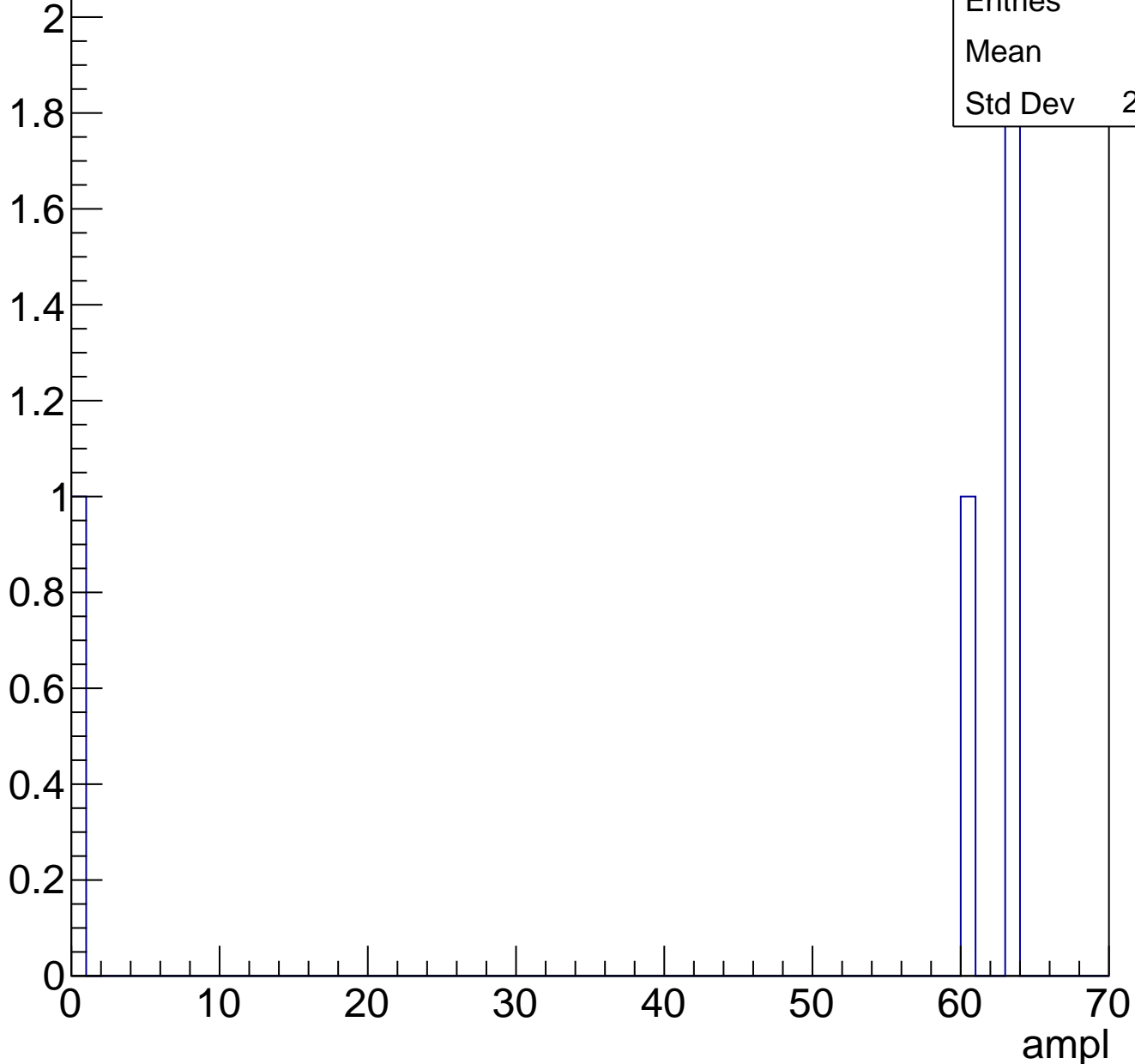
0 10 20 30 40 50 60 70



# B0L000S, U19-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch28, adc0

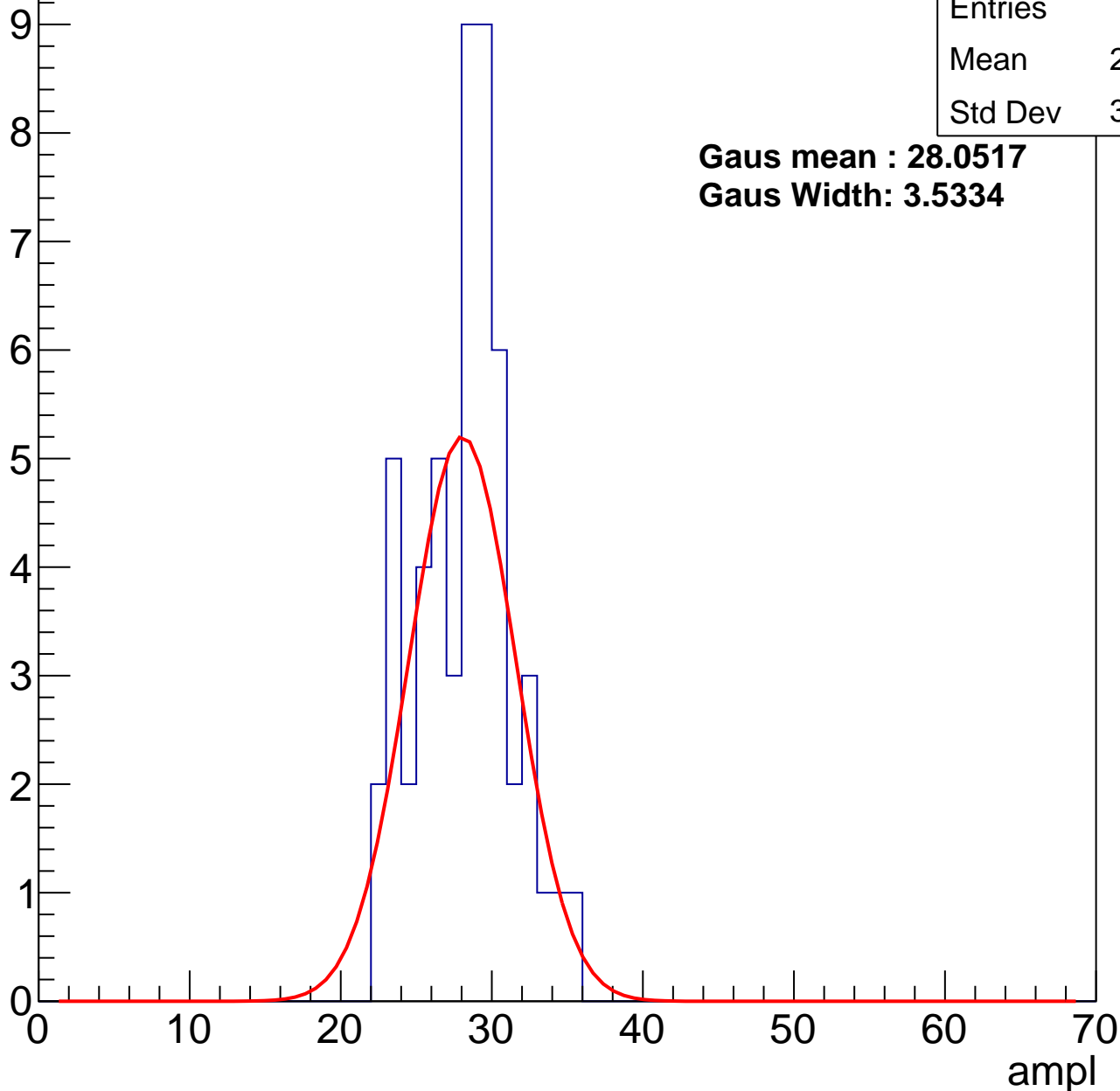
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	27.75
Std Dev	3.052

**Gaus mean : 28.0517**

**Gaus Width: 3.5334**



# B0L000S, U19-ch28, adc1

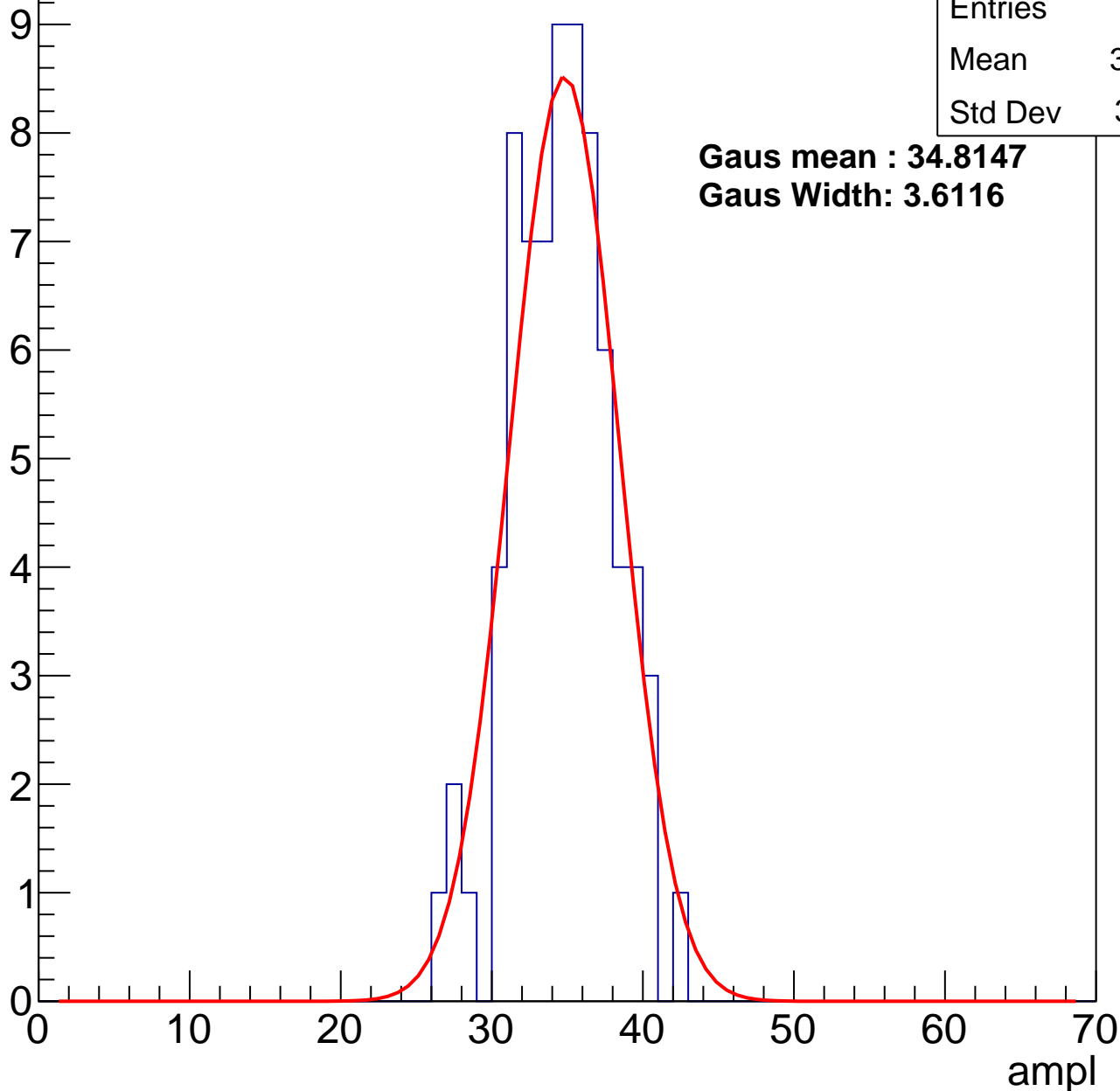
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	34.22
Std Dev	3.281

**Gaus mean : 34.8147**

**Gaus Width: 3.6116**



# B0L000S, U19-ch28, adc2

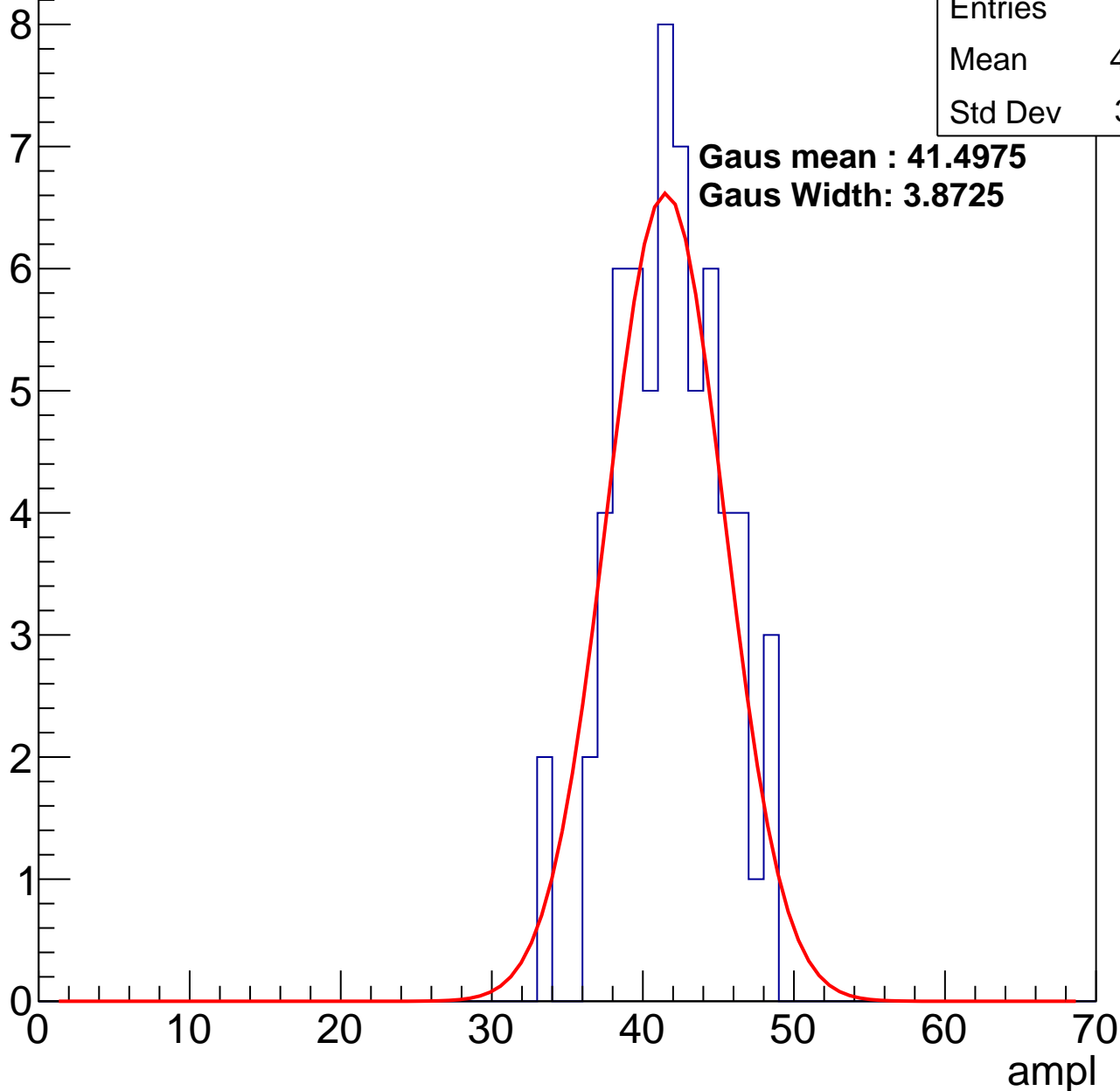
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	41.33
Std Dev	3.441

**Gaus mean : 41.4975**

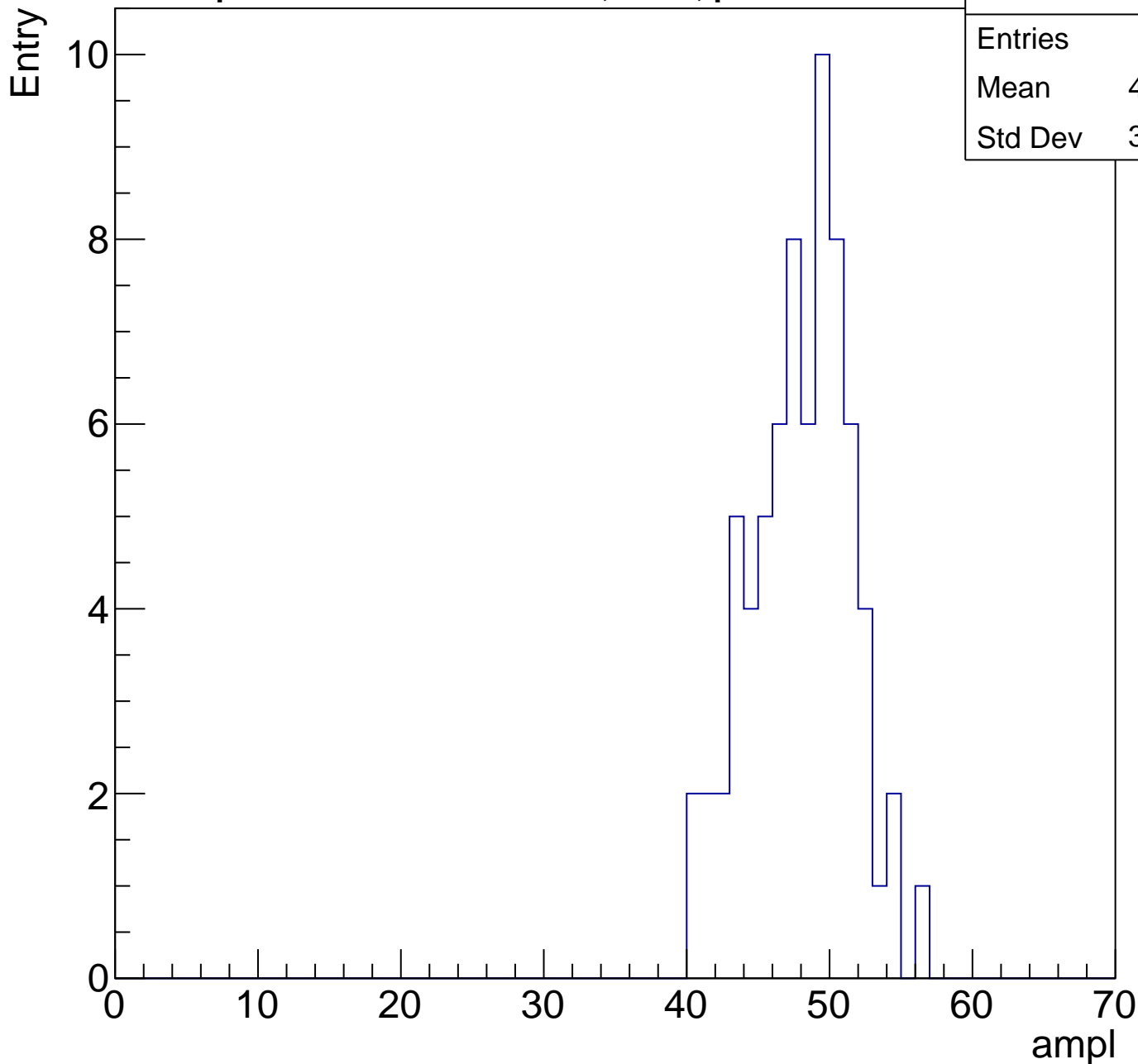
**Gaus Width: 3.8725**



# B0L000S, U19-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	47.54
Std Dev	3.476

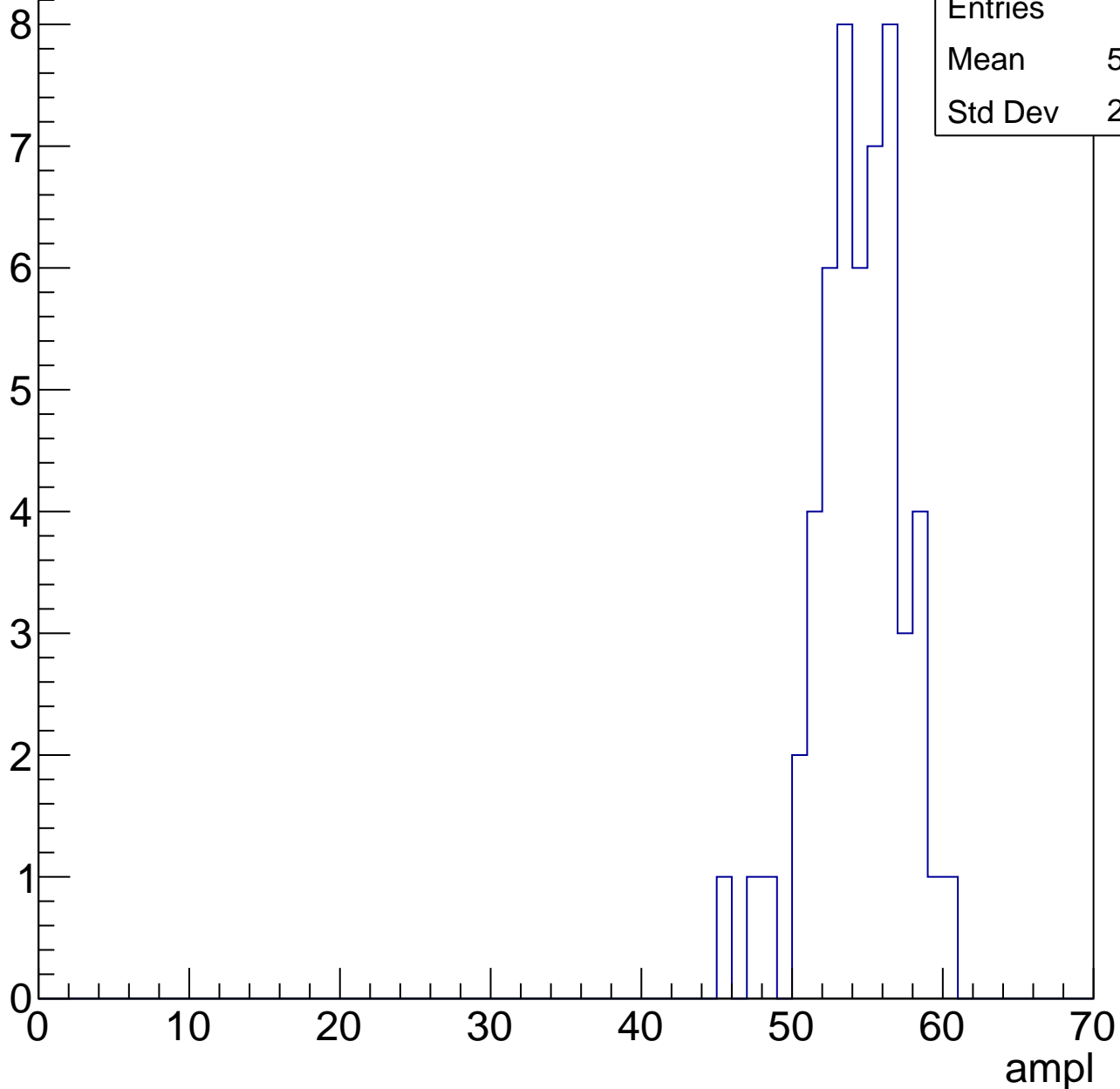


# B0L000S, U19-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

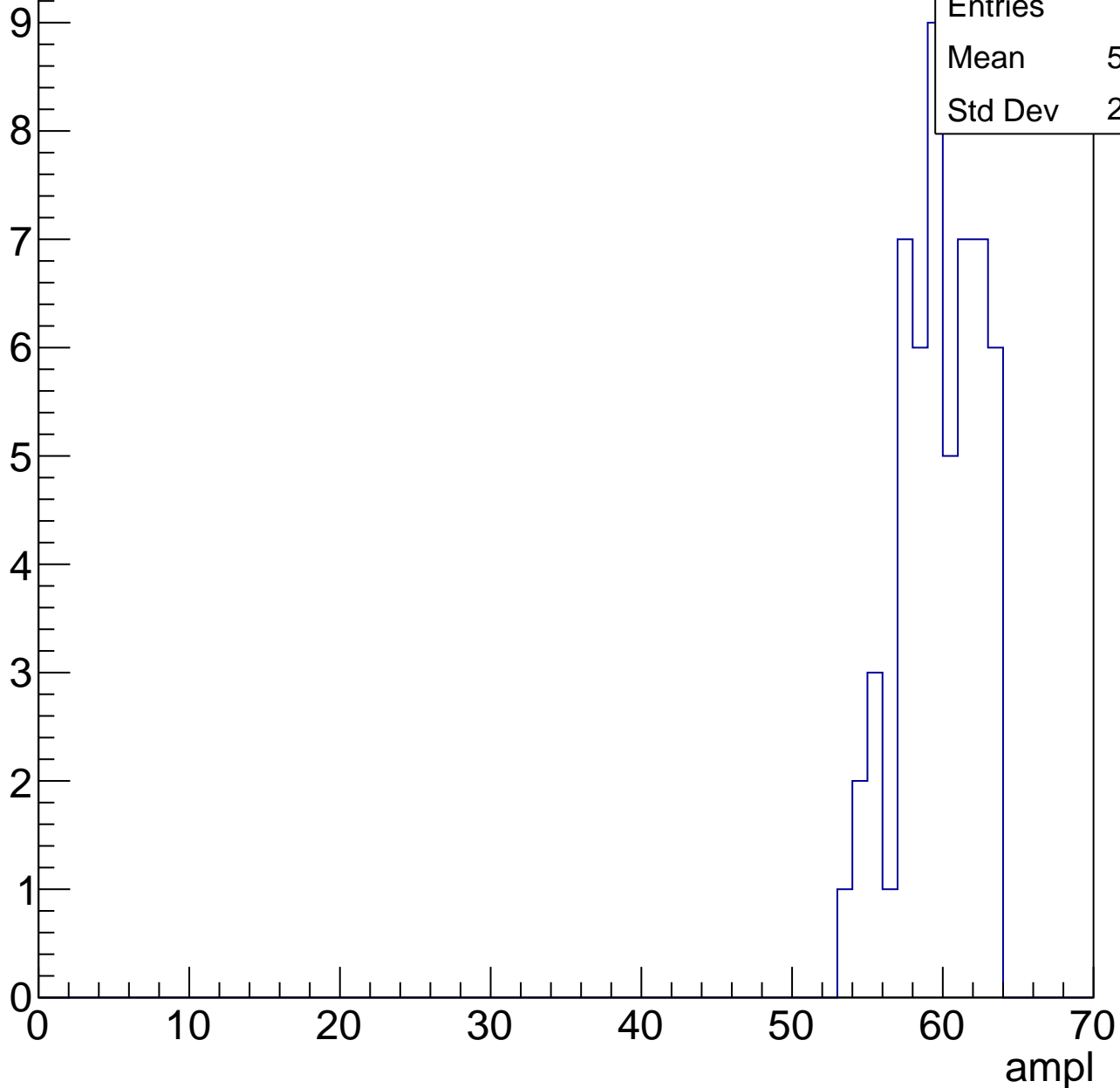
Entries	53
Mean	53.94
Std Dev	2.936



# B0L000S, U19-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

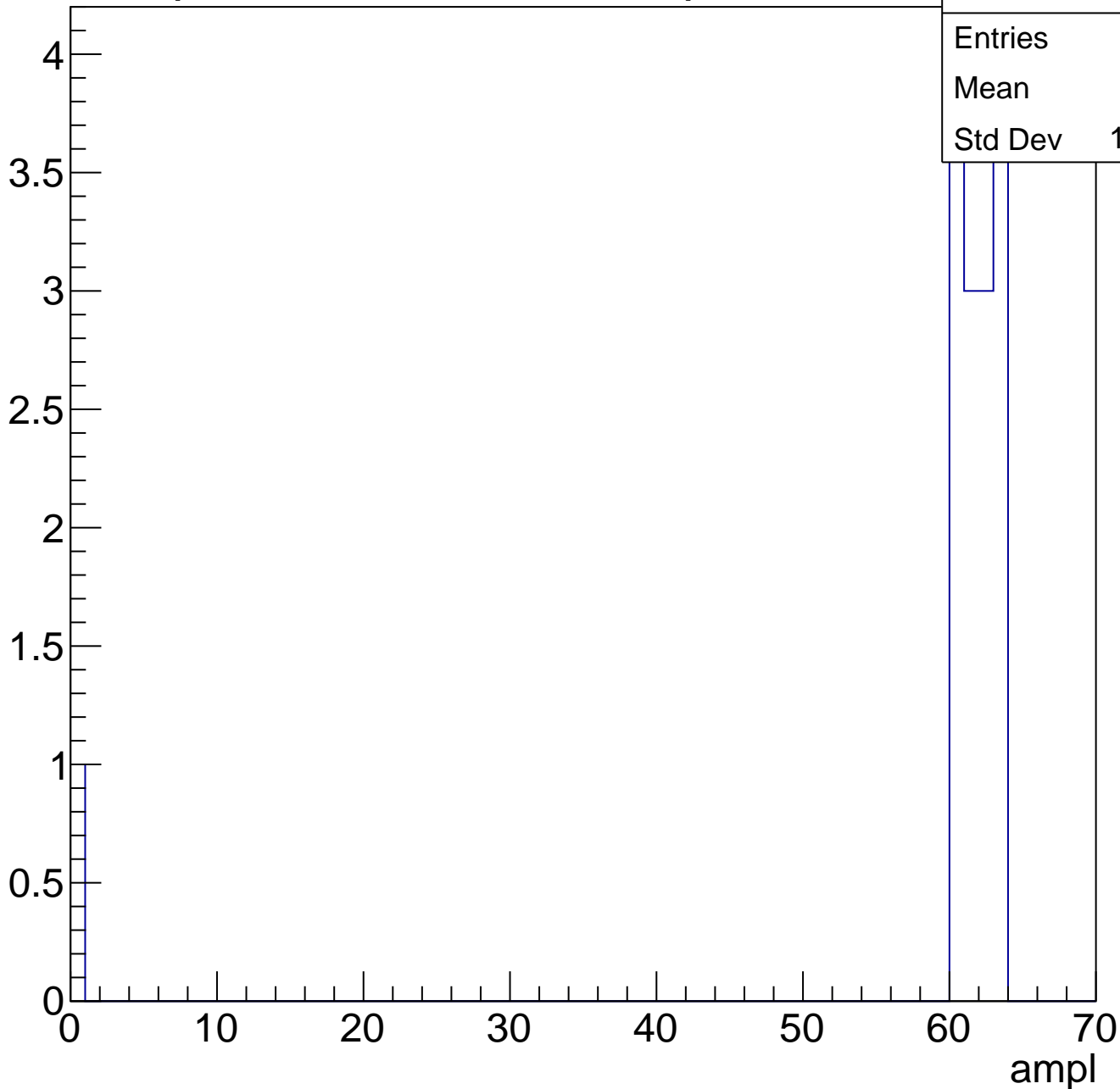
Entry



# B0L000S, U19-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



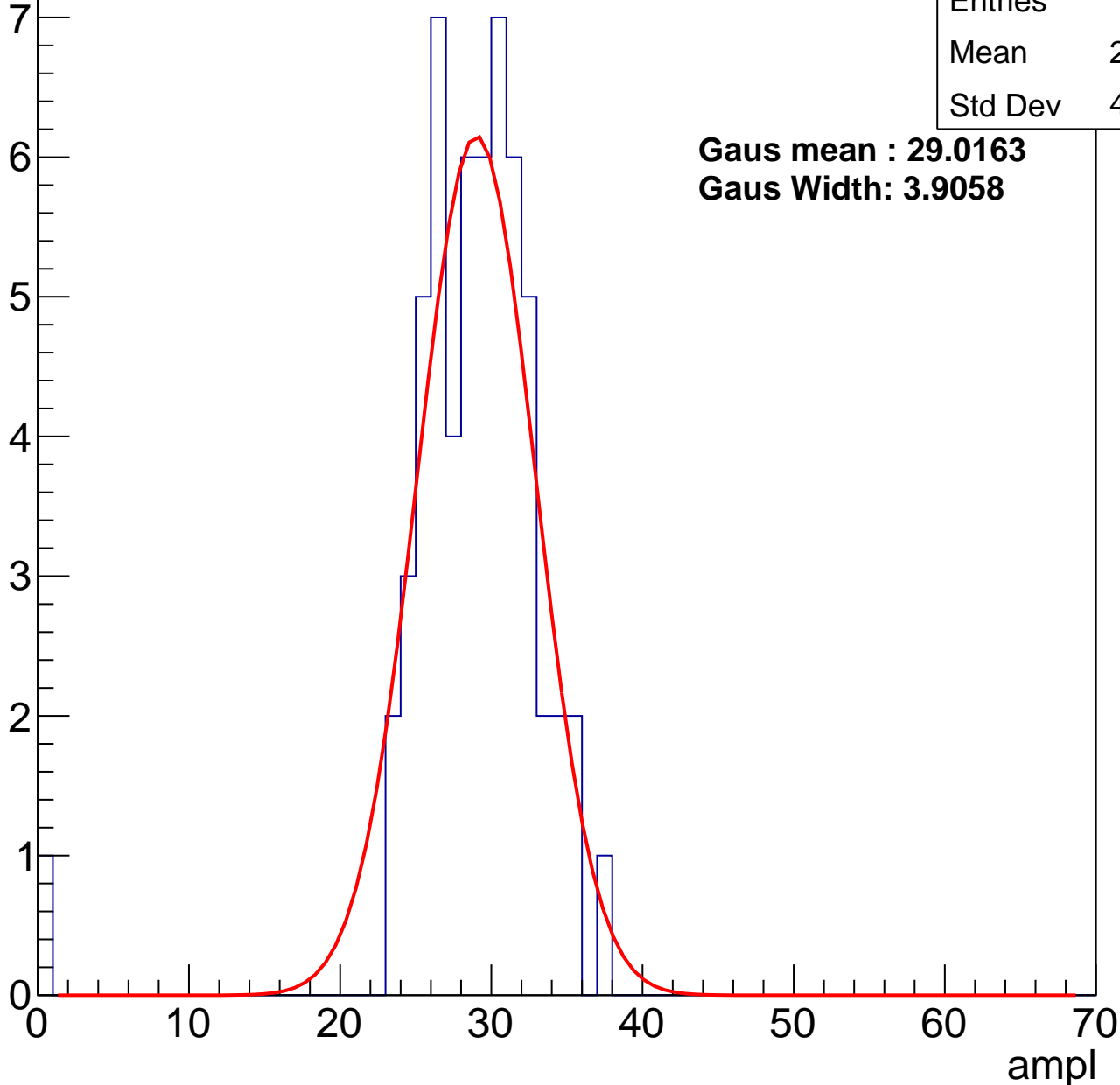
# B0L000S, U19-ch29, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	28.34
Std Dev	4.908

**Gaus mean : 29.0163**  
**Gaus Width: 3.9058**



# B0L000S, U19-ch29, adc1

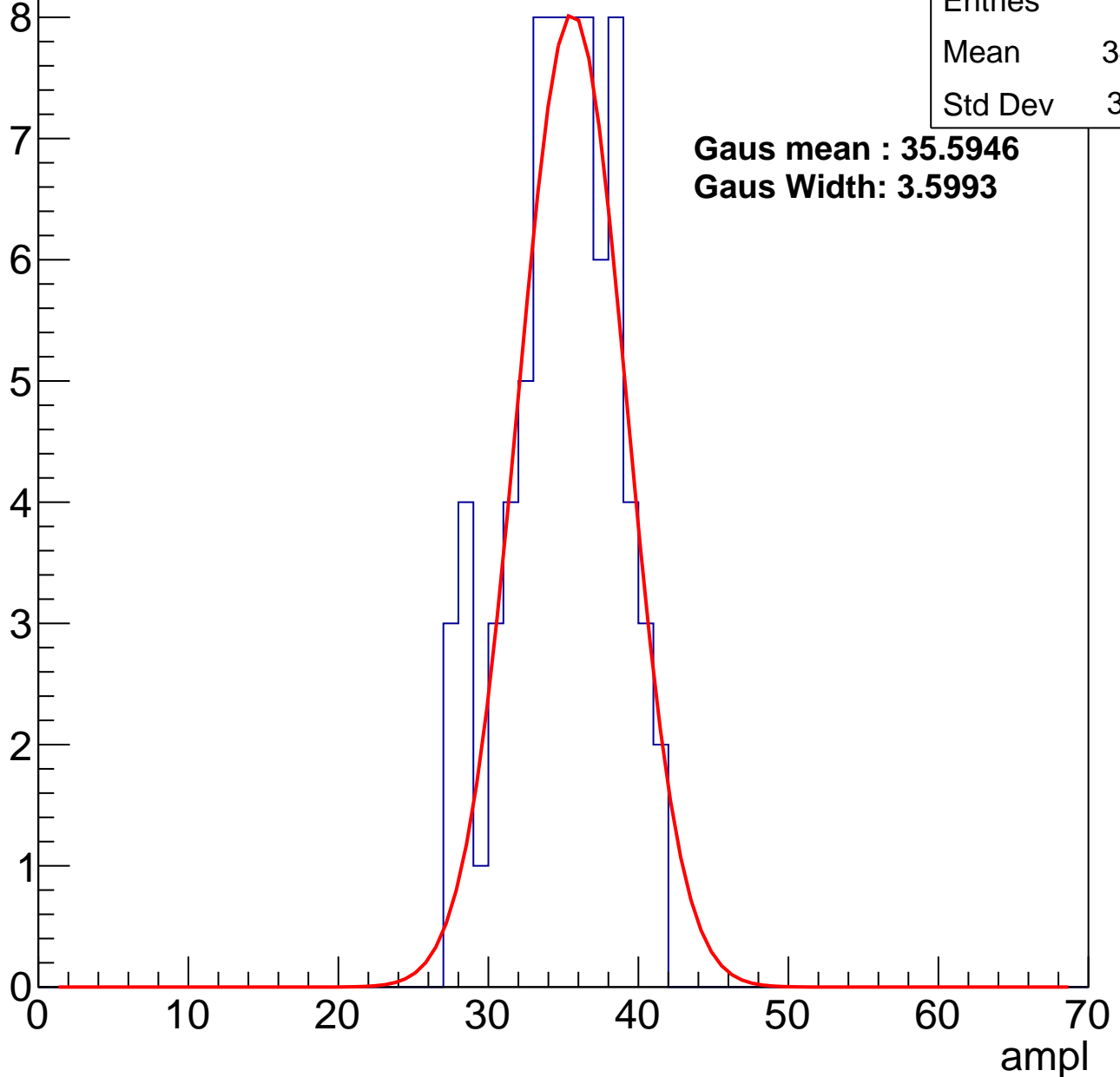
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	34.45
Std Dev	3.541

**Gaus mean : 35.5946**

**Gaus Width: 3.5993**



# B0L000S, U19-ch29, adc2

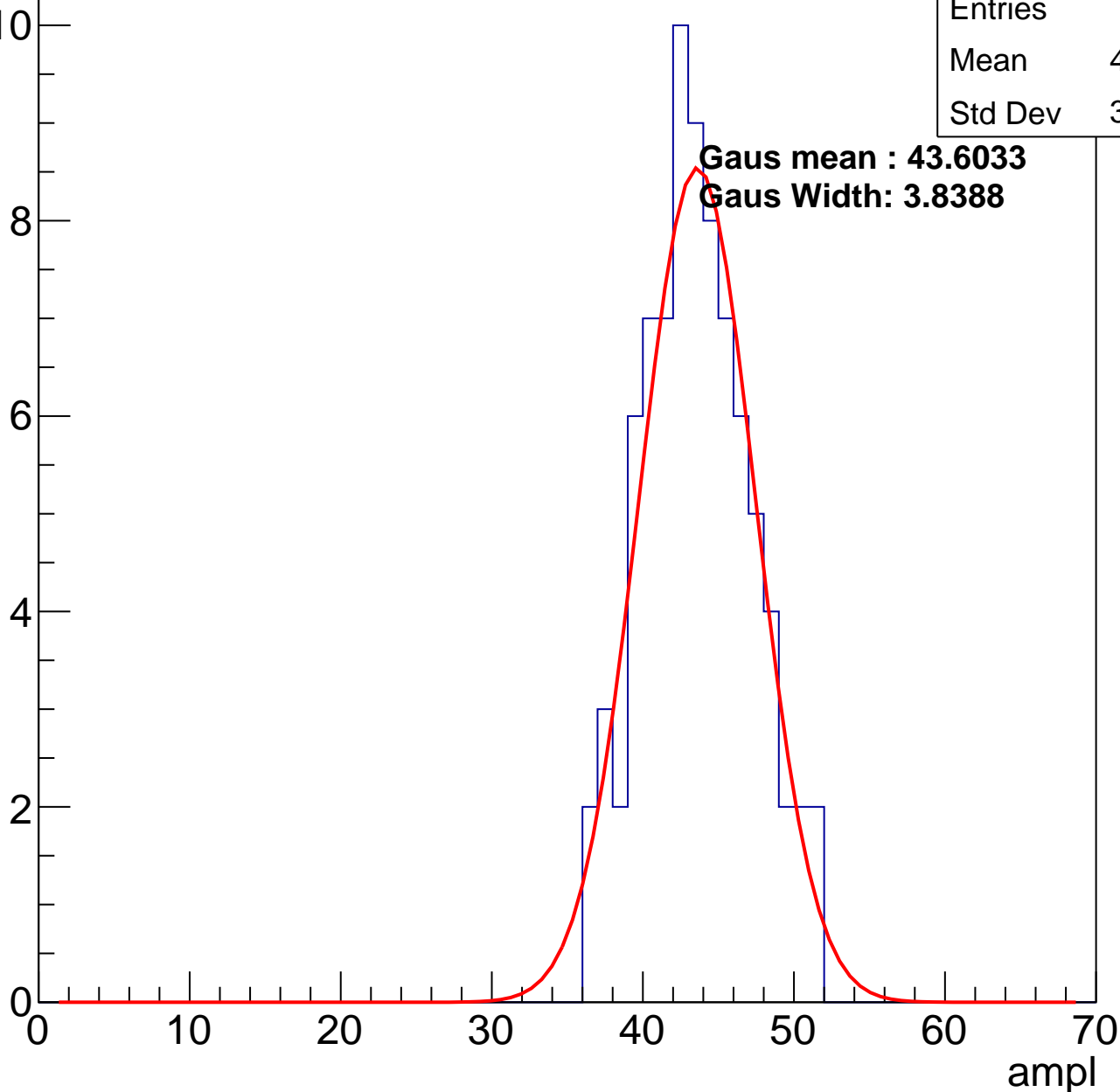
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	43.13
Std Dev	3.526

**Gaus mean : 43.6033**

**Gaus Width: 3.8388**

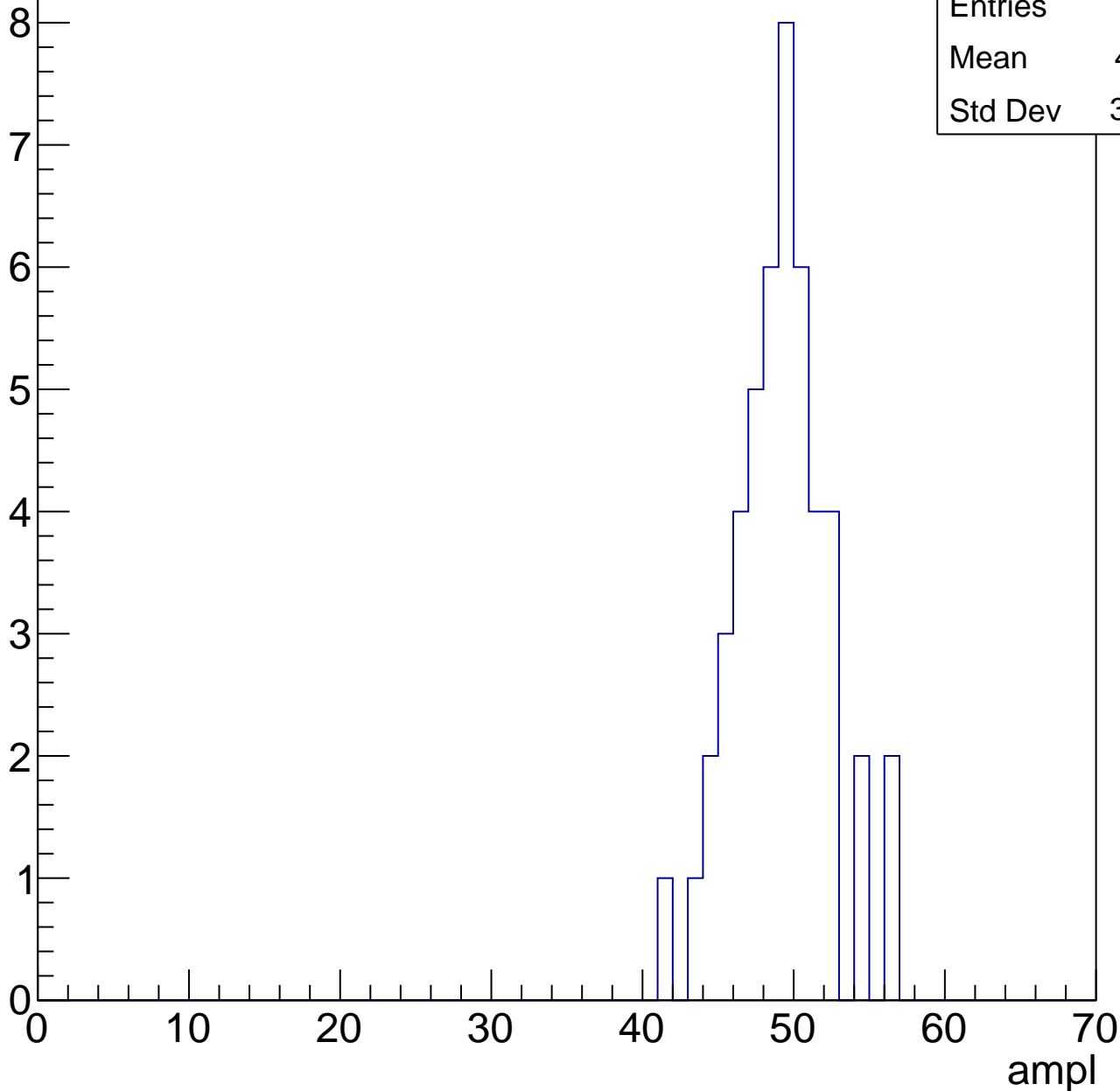


# B0L000S, U19-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	48.71
Std Dev	3.102



# B0L000S, U19-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	54
Mean	54.37
Std Dev	2.288

Entry

10

8

6

4

2

0

0

10

20

30

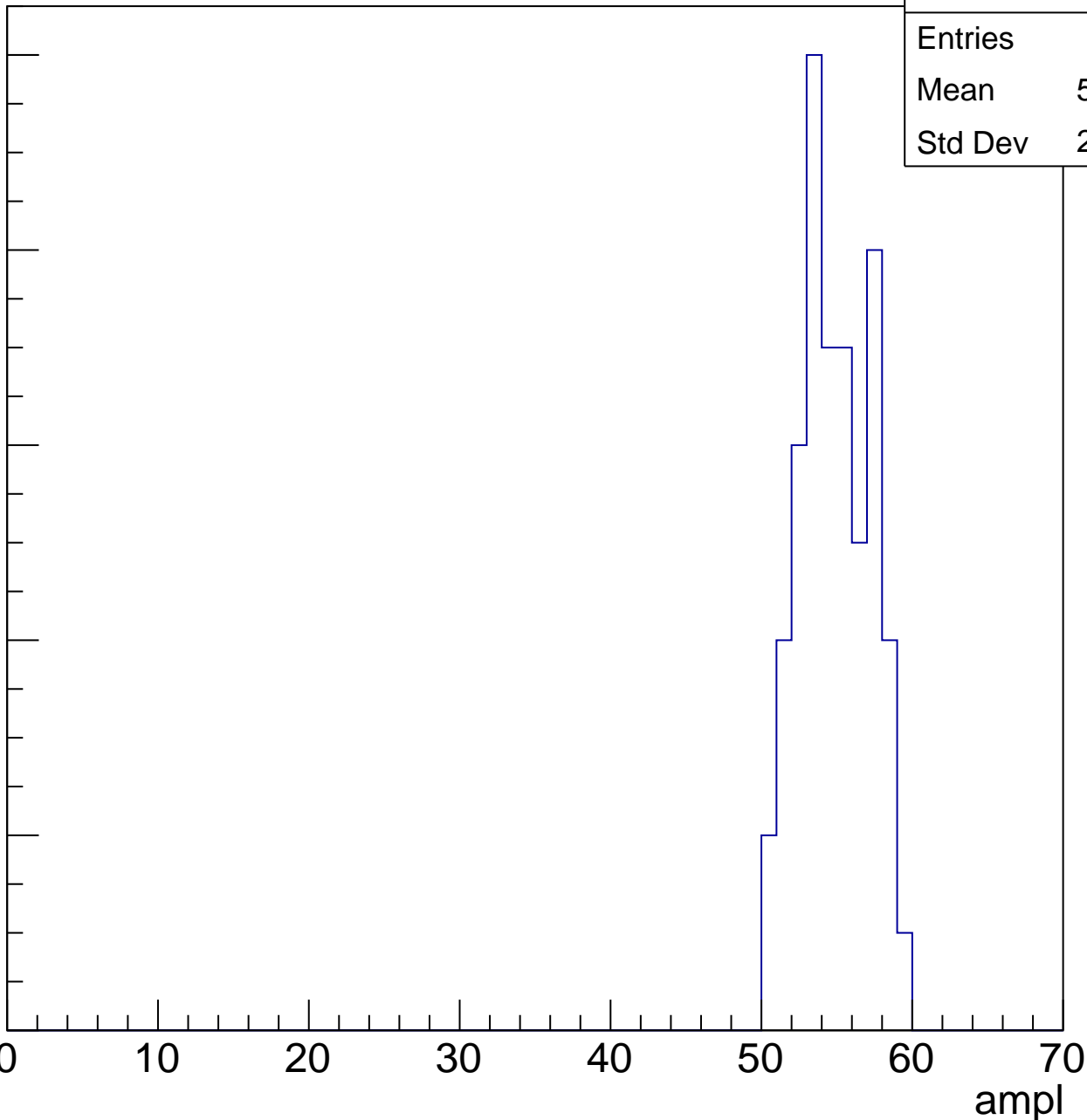
40

50

60

70

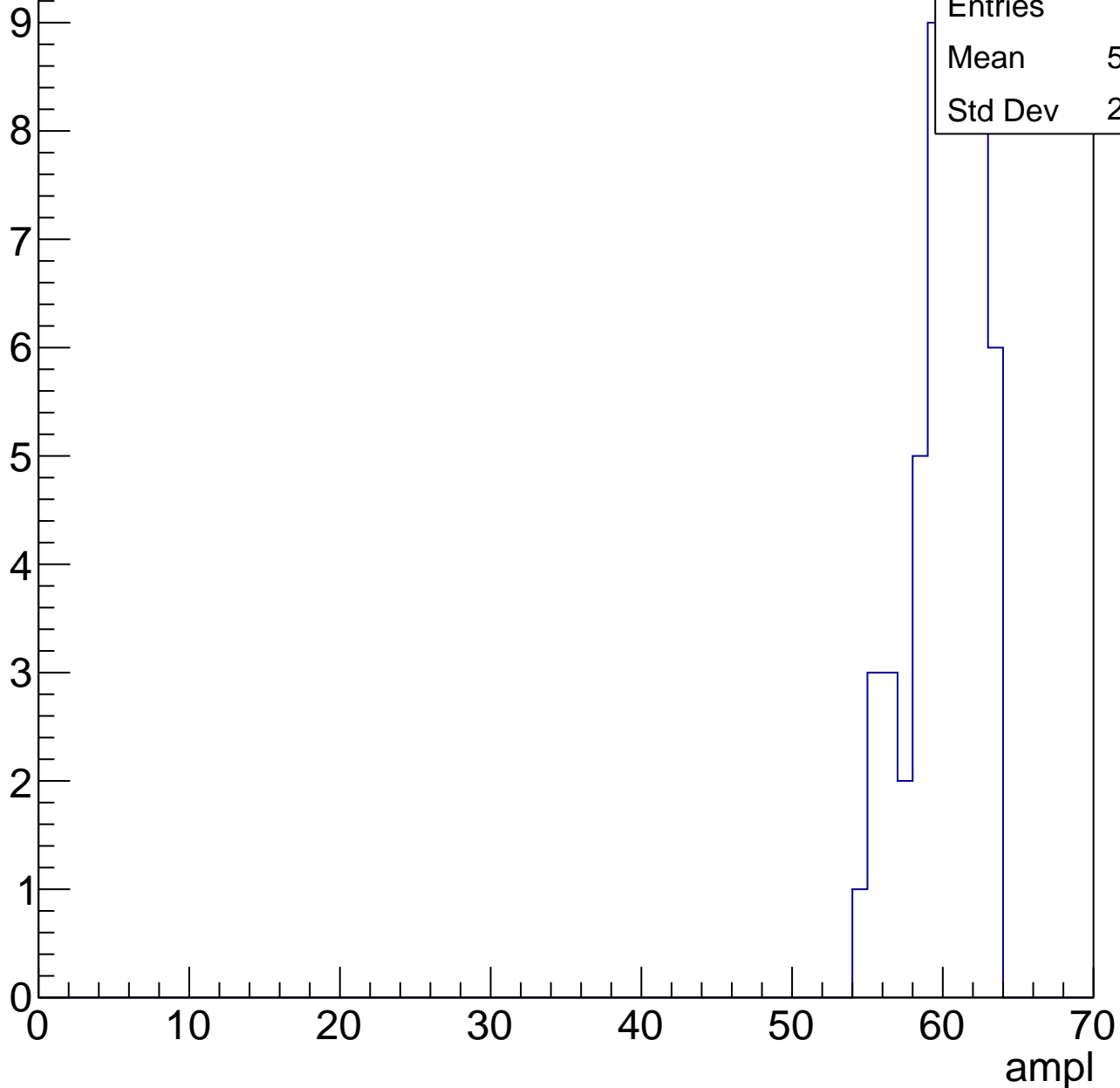
ampl



# B0L000S, U19-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

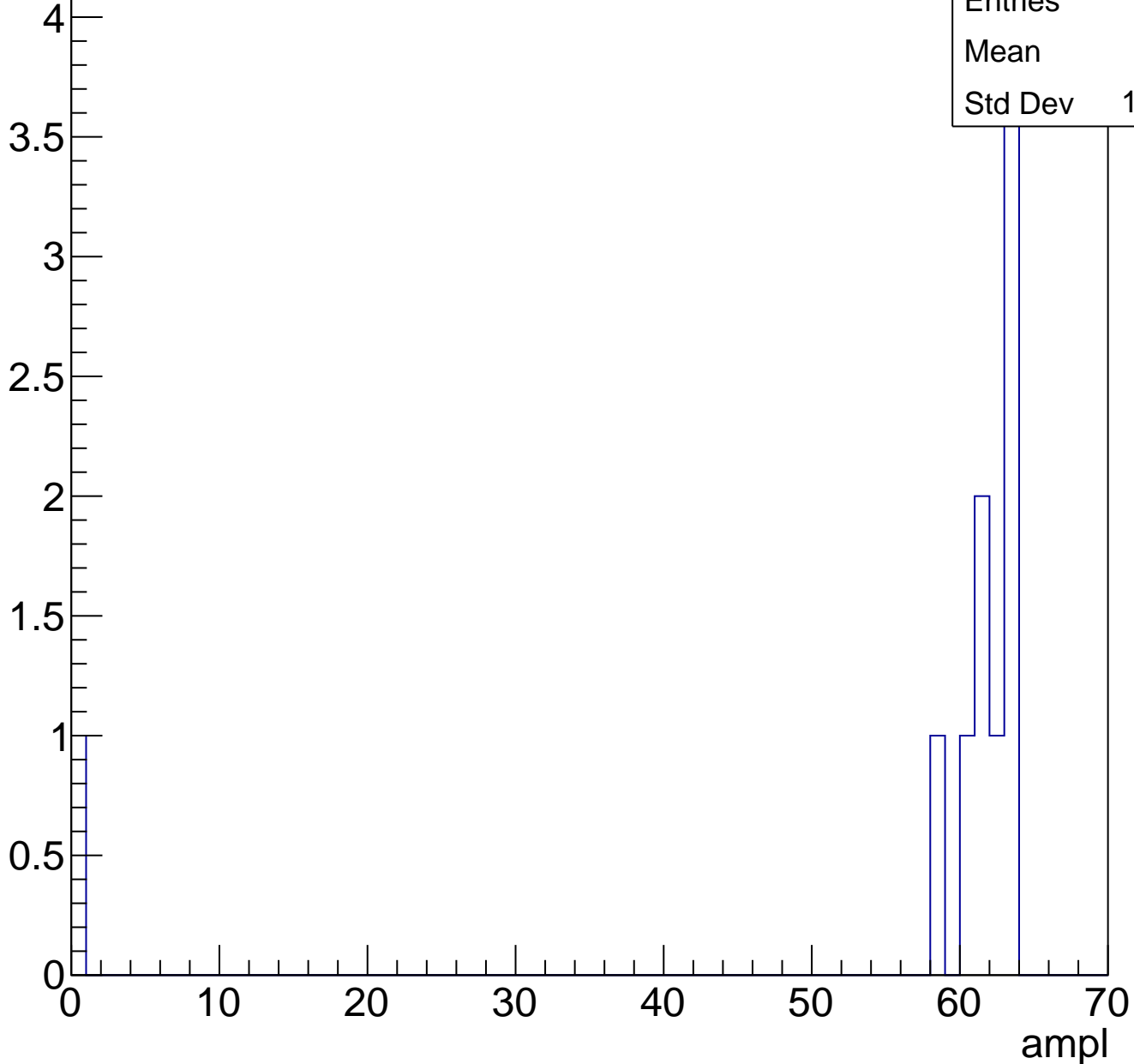


Entries	55
Mean	59.75
Std Dev	2.337

# B0L000S, U19-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

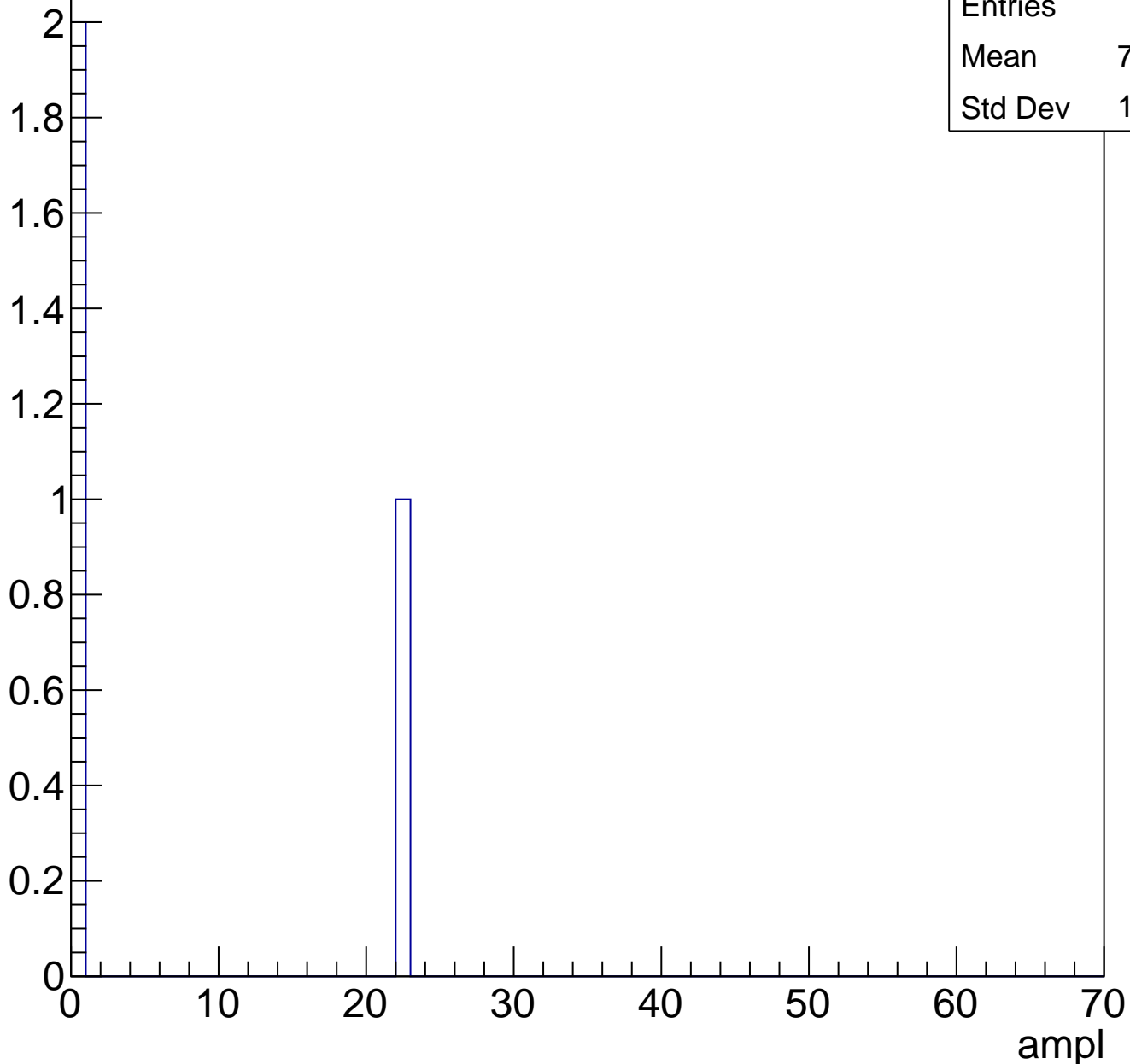




# B0L000S, U19-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U19-ch30, adc0

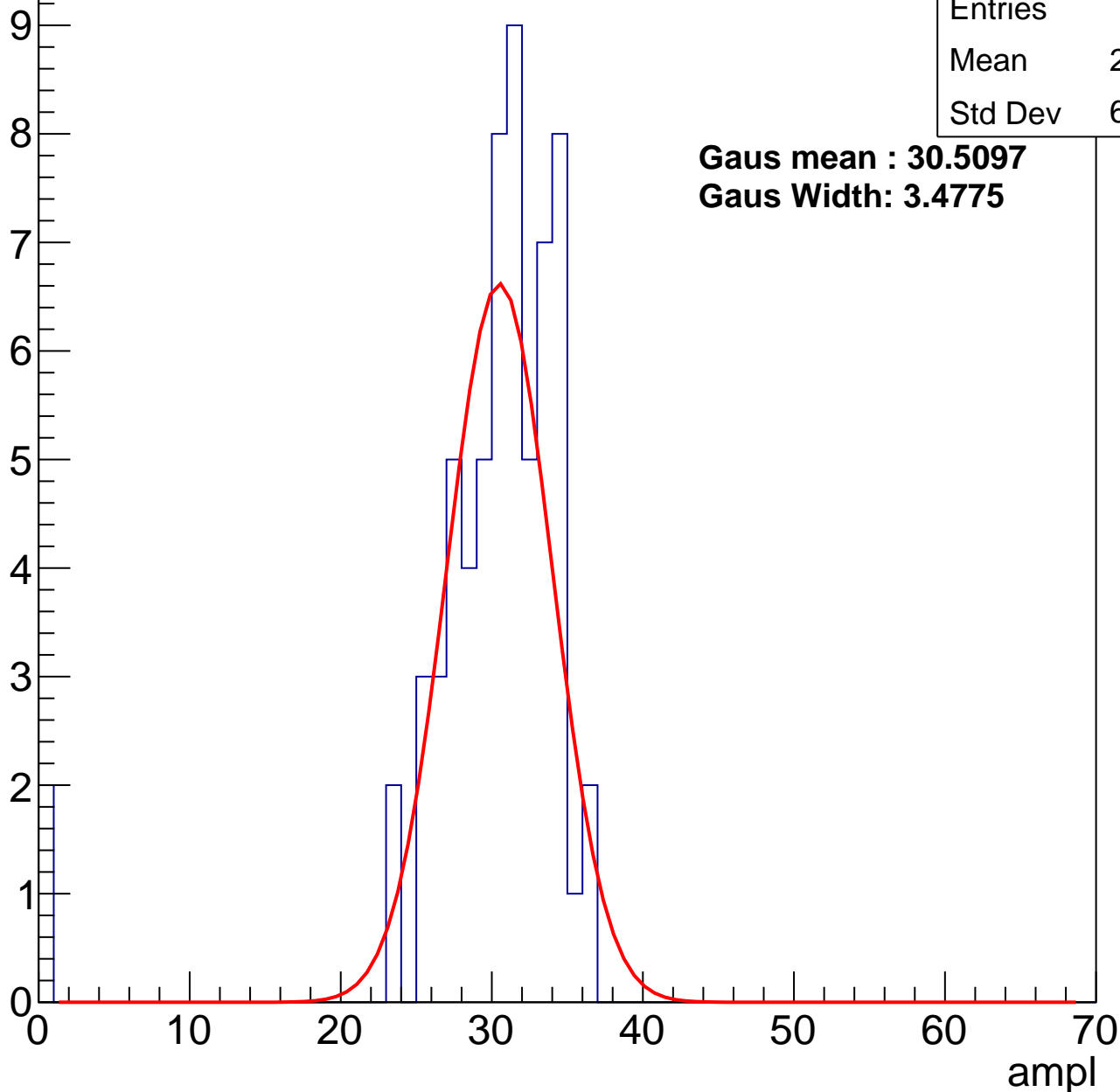
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.38
Std Dev	6.097

**Gaus mean : 30.5097**

**Gaus Width: 3.4775**



# B0L000S, U19-ch30, adc1

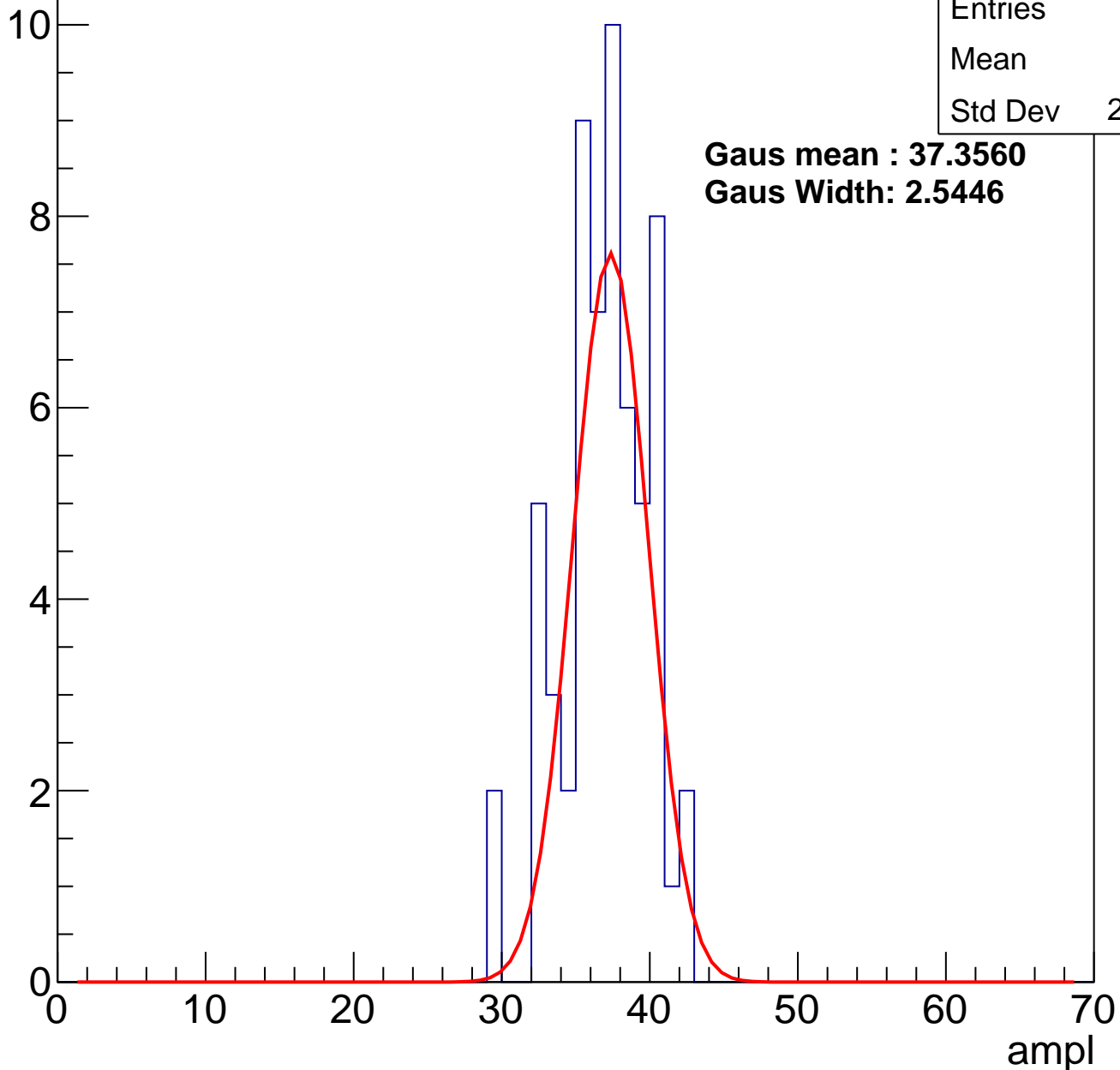
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	60
Mean	36.5
Std Dev	2.918

**Gaus mean : 37.3560**

**Gaus Width: 2.5446**

Entry



# B0L000S, U19-ch30, adc2

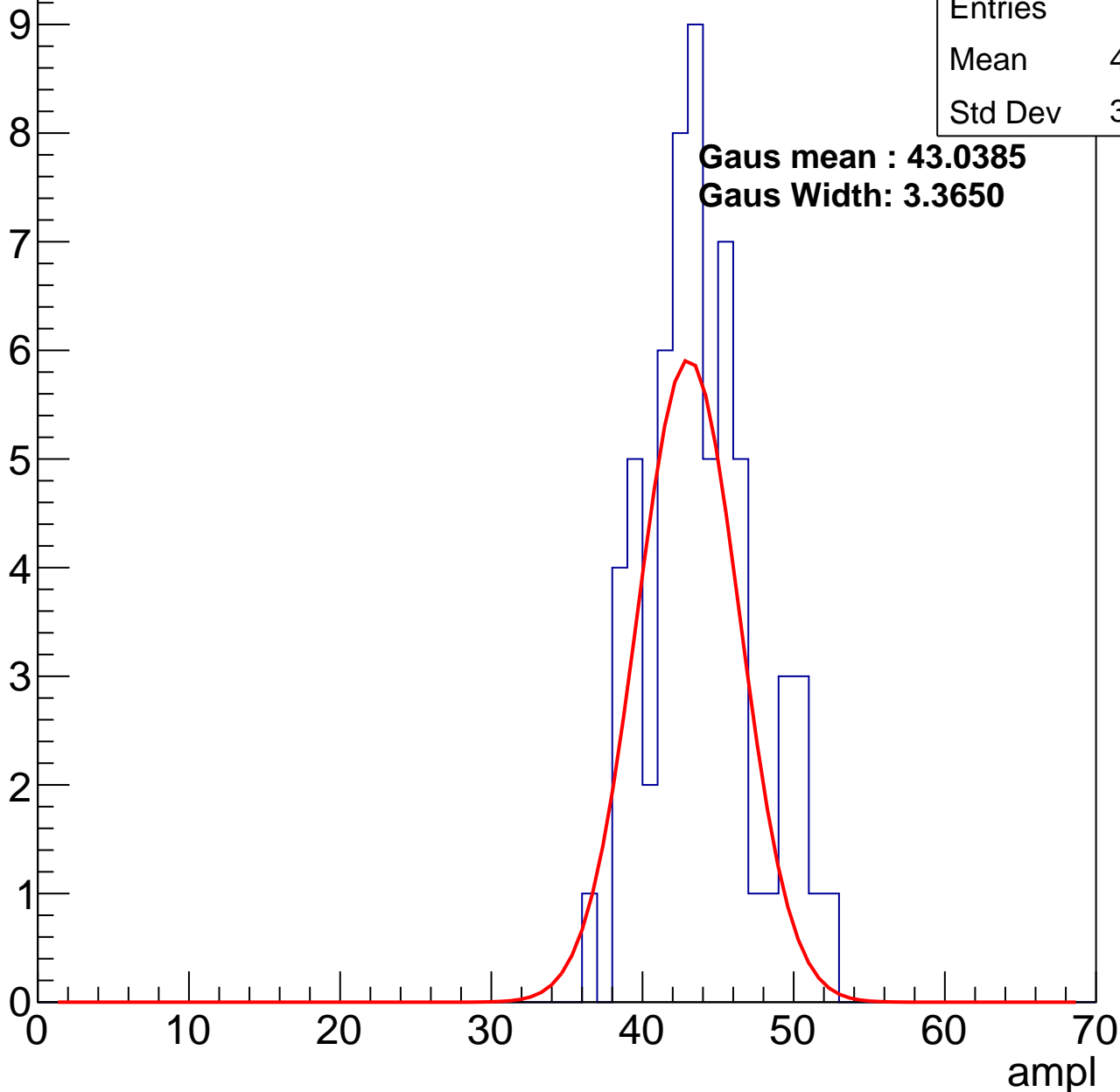
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.42
Std Dev	3.563

**Gaus mean : 43.0385**

**Gaus Width: 3.3650**

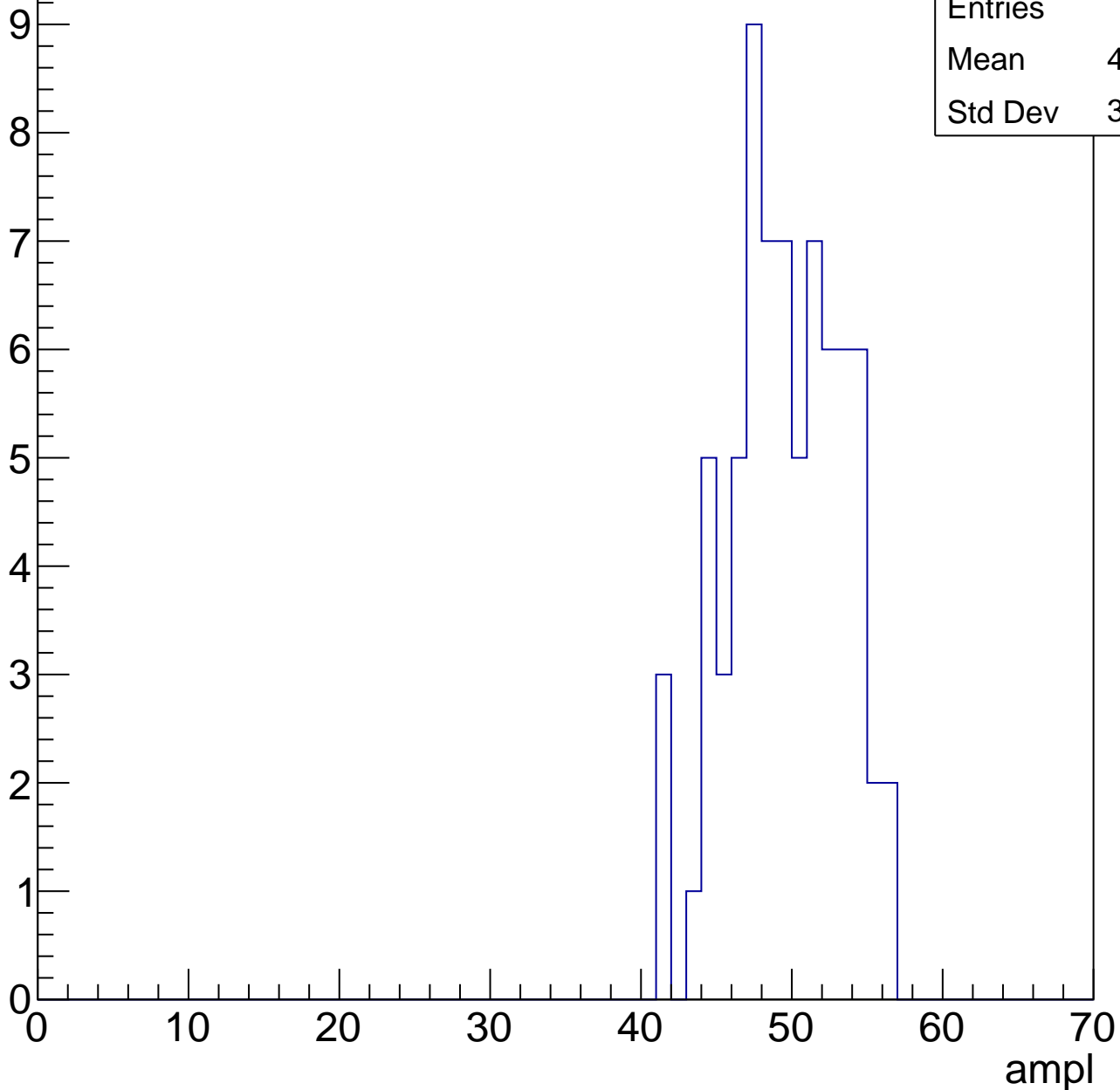


# B0L000S, U19-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	49.14
Std Dev	3.659

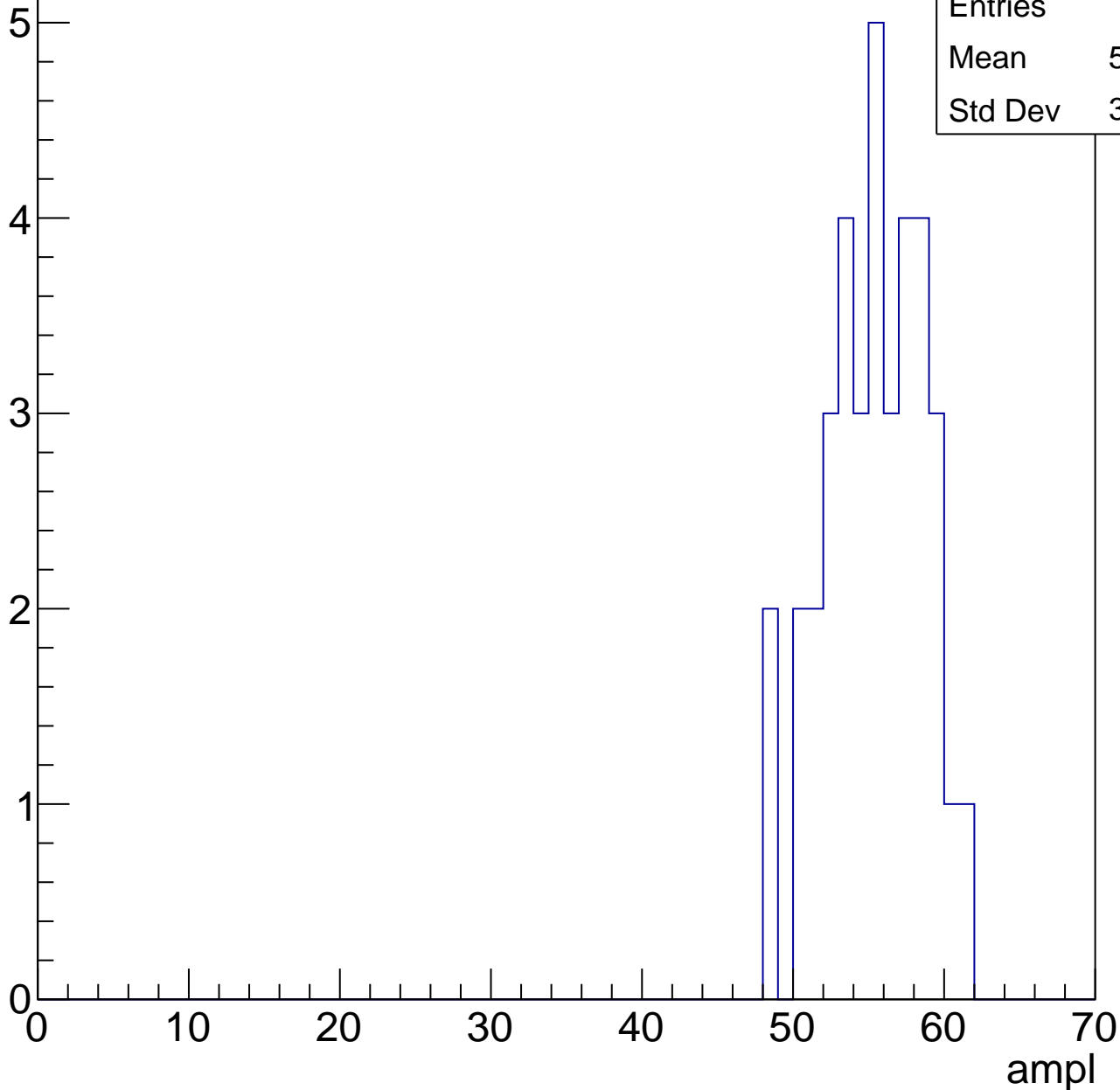


# B0L000S, U19-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	54.84
Std Dev	3.243

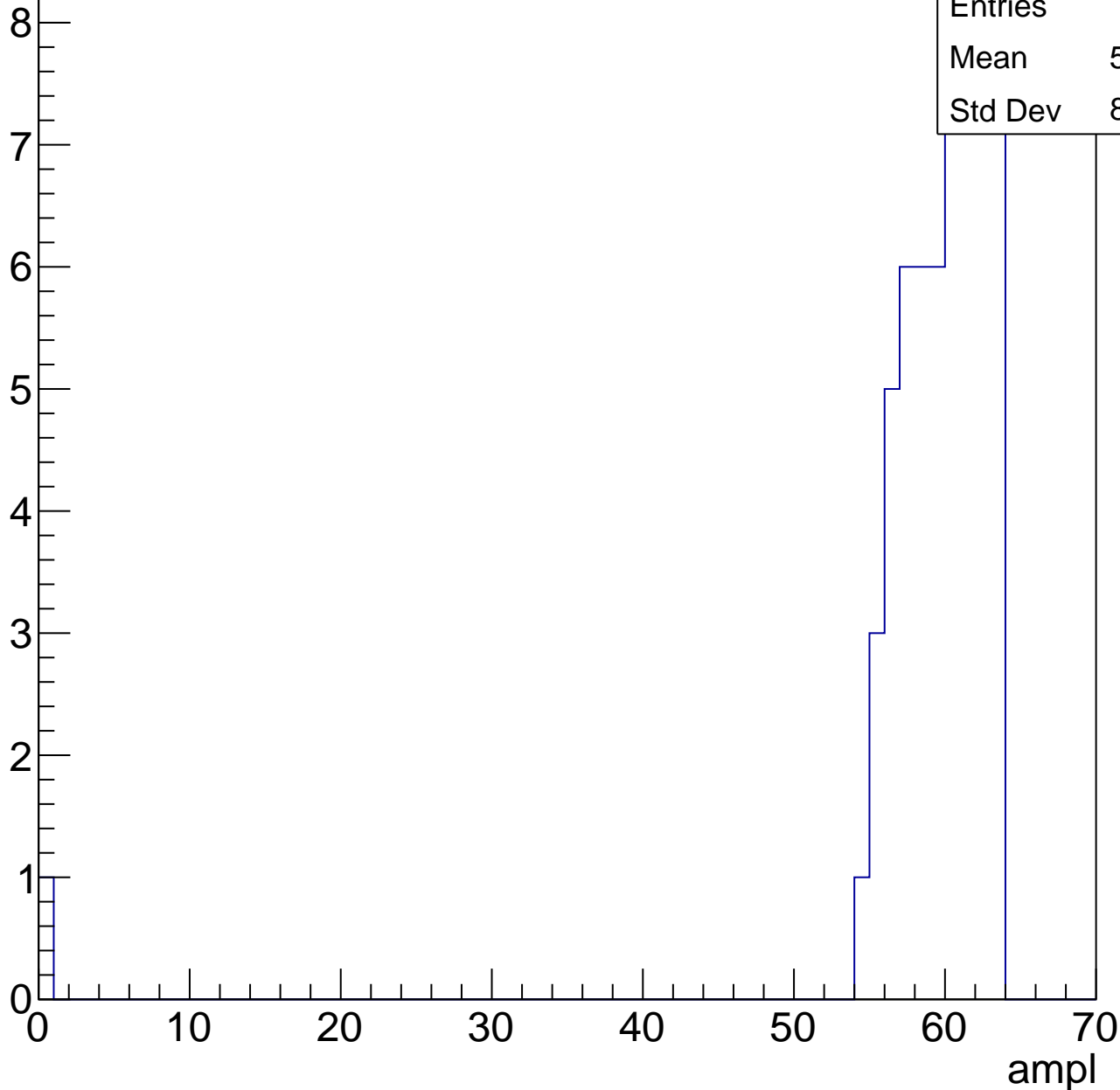


# B0L000S, U19-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	58.52
Std Dev	8.016



# B0L000S, U19-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.43
Std Dev	1.4

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U19-ch31, adc0

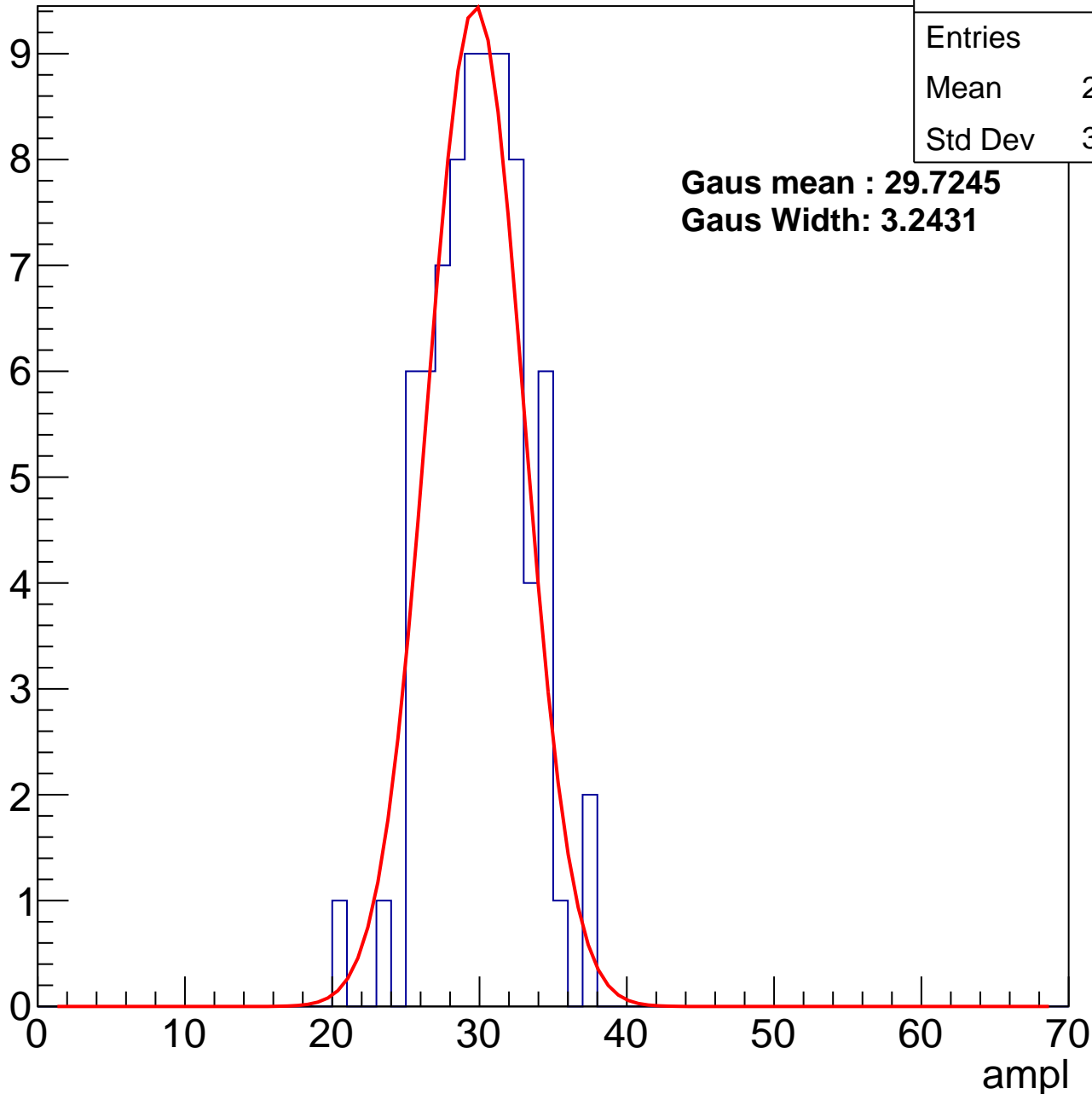
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	29.52
Std Dev	3.177

**Gaus mean : 29.7245**

**Gaus Width: 3.2431**



# B0L000S, U19-ch31, adc1

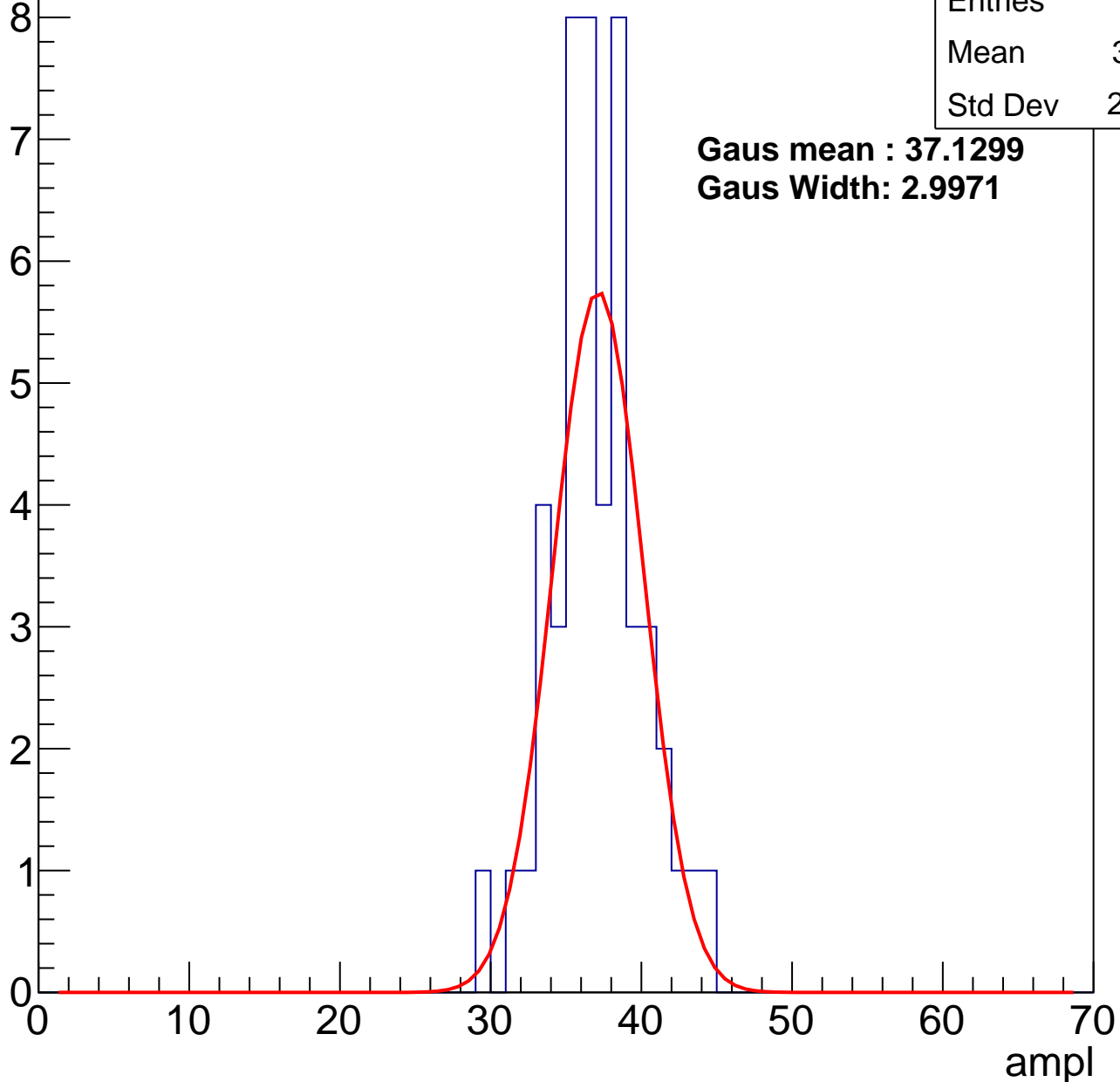
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.61
Std Dev	2.989

**Gaus mean : 37.1299**

**Gaus Width: 2.9971**



# B0L000S, U19-ch31, adc2

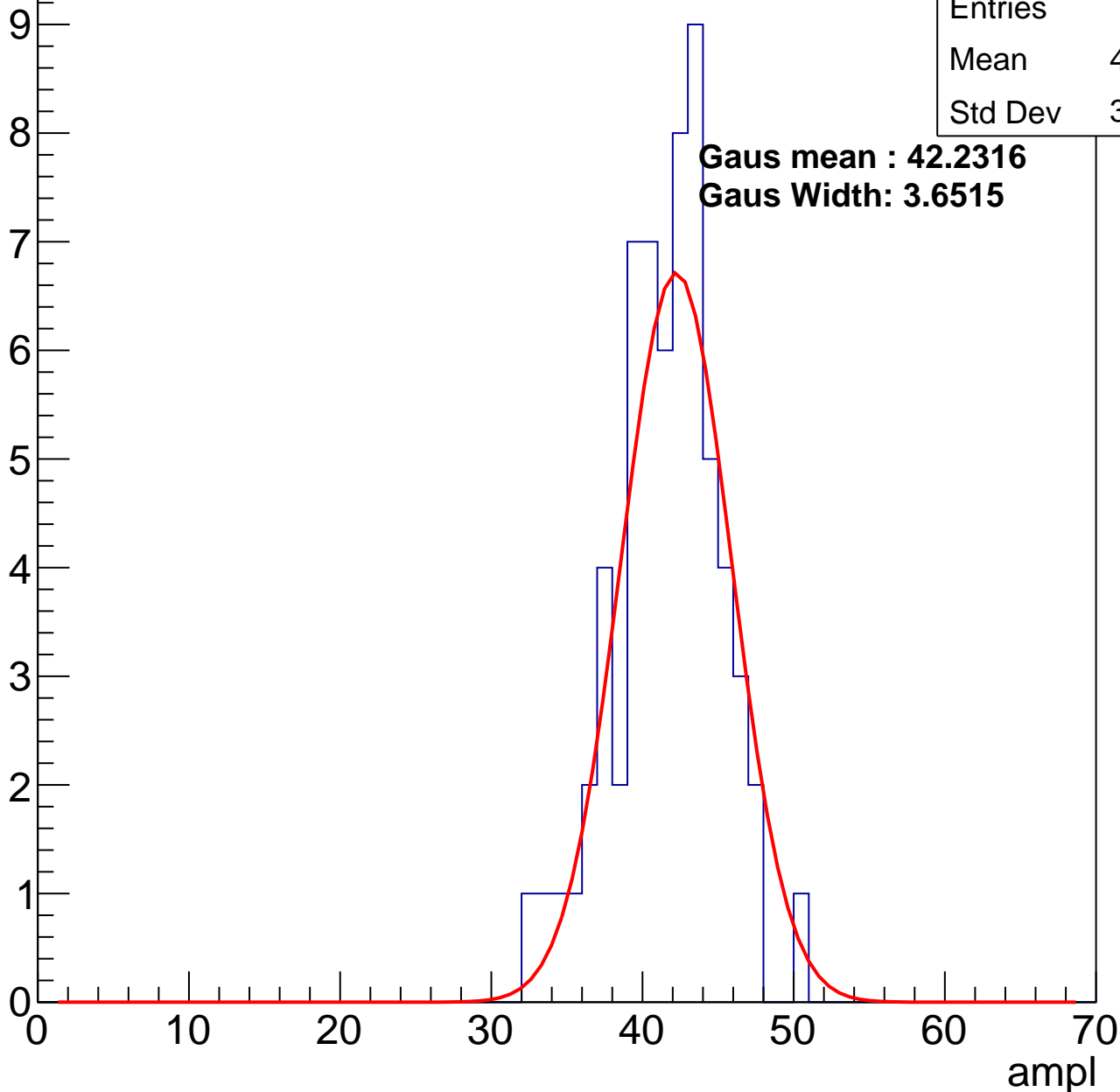
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.16
Std Dev	3.483

**Gaus mean : 42.2316**

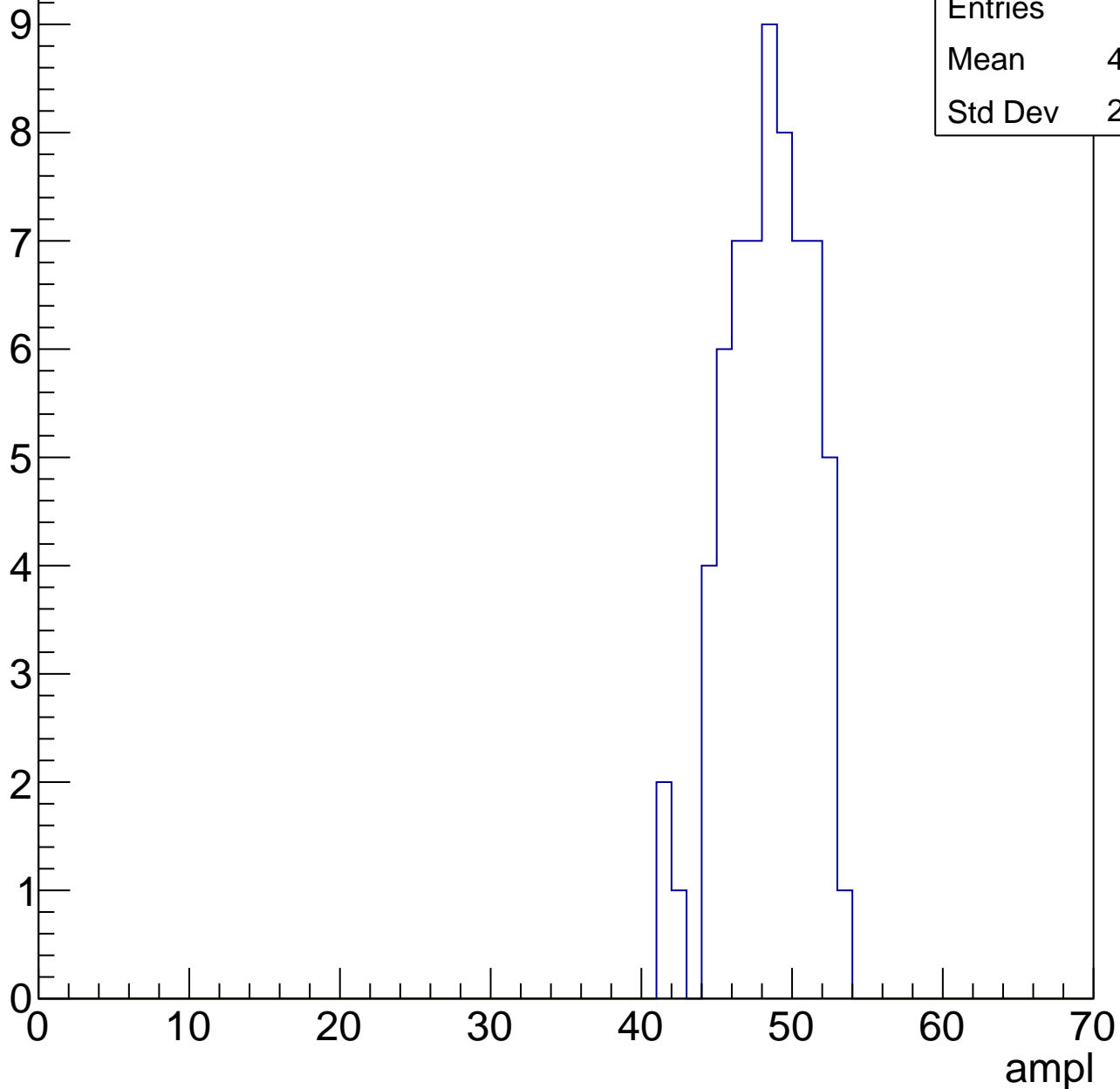
**Gaus Width: 3.6515**



# B0L000S, U19-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

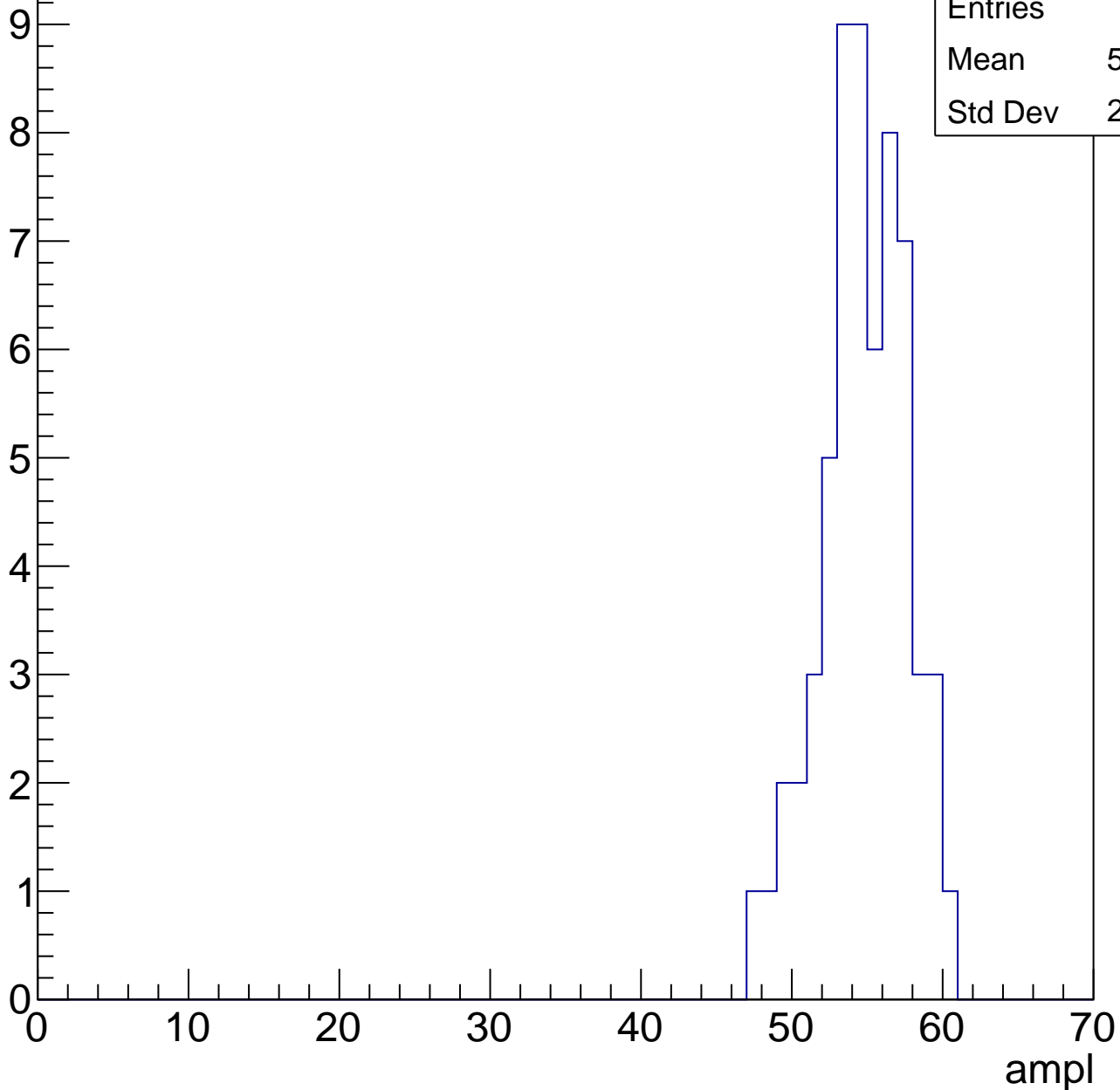


# B0L000S, U19-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.28
Std Dev	2.823

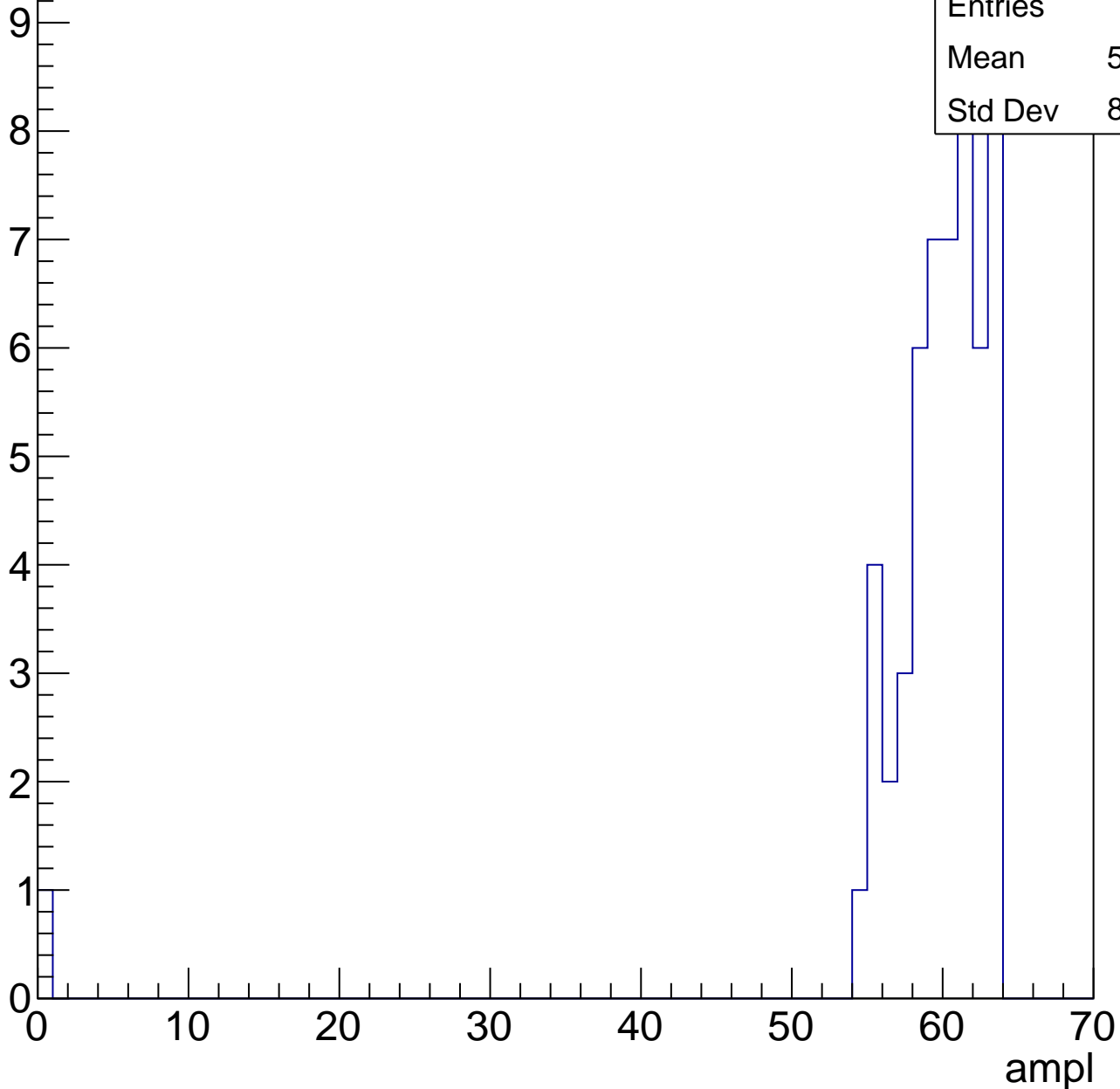


# B0L000S, U19-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

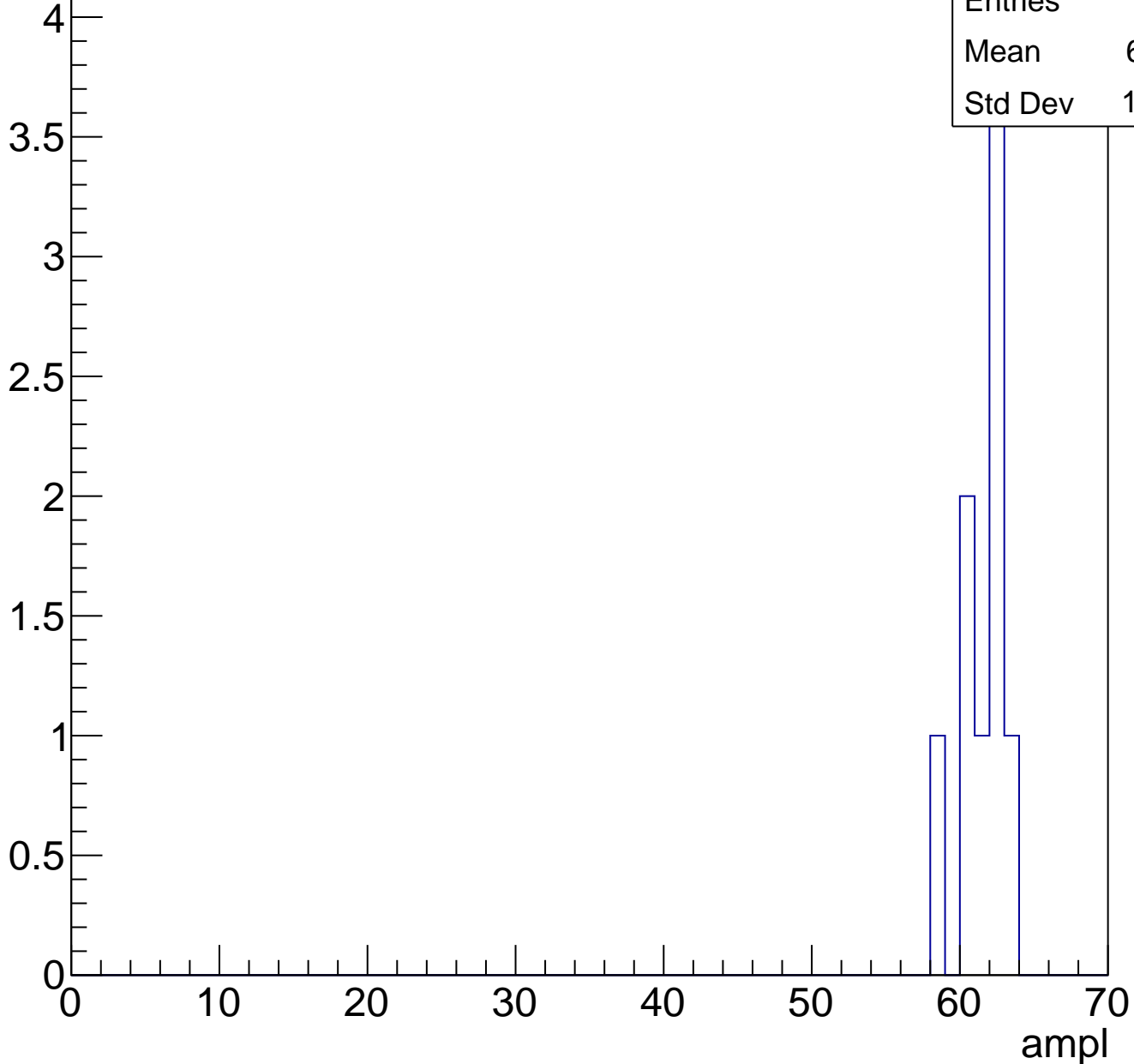
Entries	55
Mean	58.65
Std Dev	8.356



# B0L000S, U19-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch32, adc0

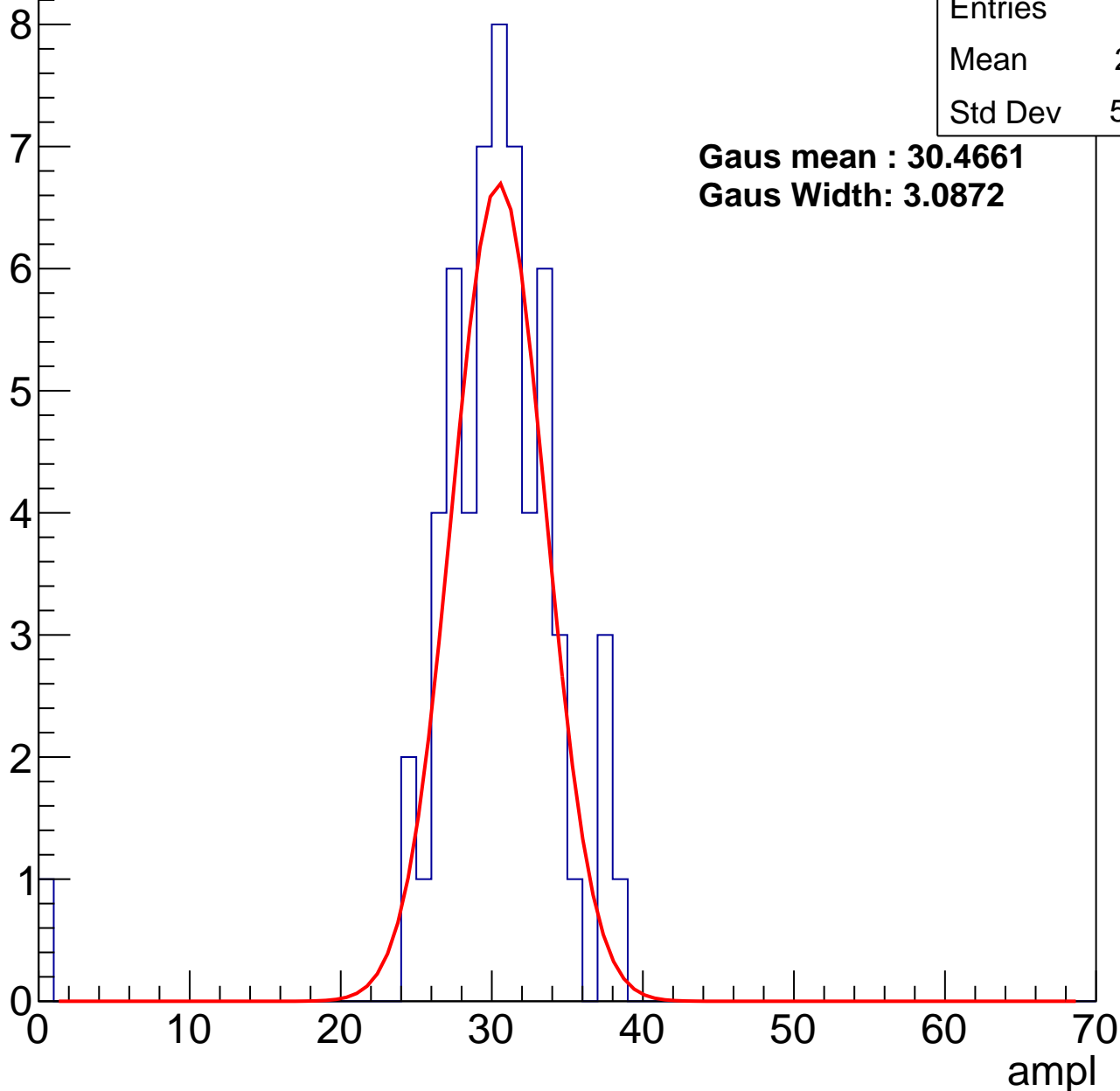
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.71
Std Dev	5.075

**Gaus mean : 30.4661**

**Gaus Width: 3.0872**



# B0L000S, U19-ch32, adc1

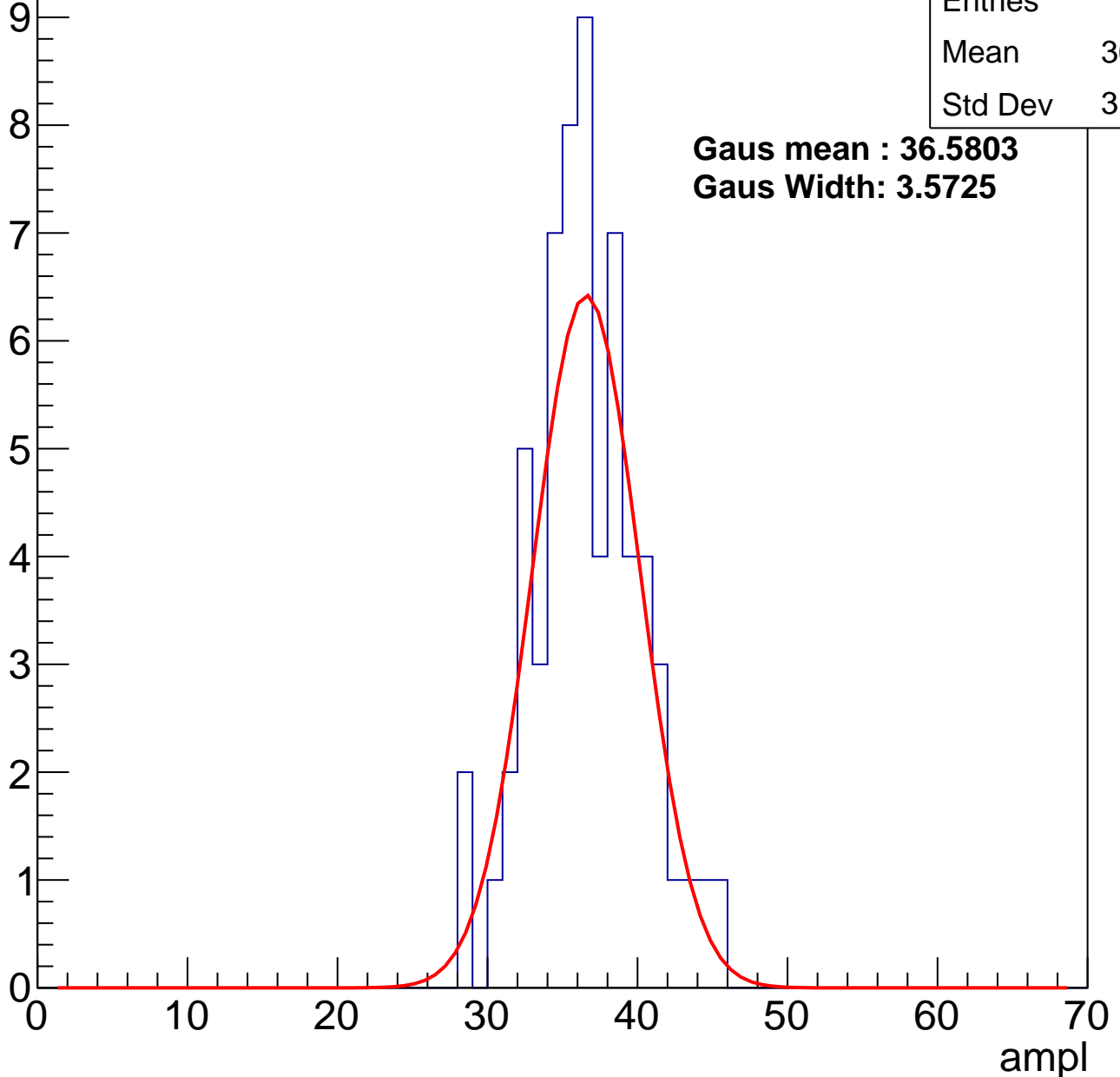
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.13
Std Dev	3.548

**Gaus mean : 36.5803**

**Gaus Width: 3.5725**



# B0L000S, U19-ch32, adc2

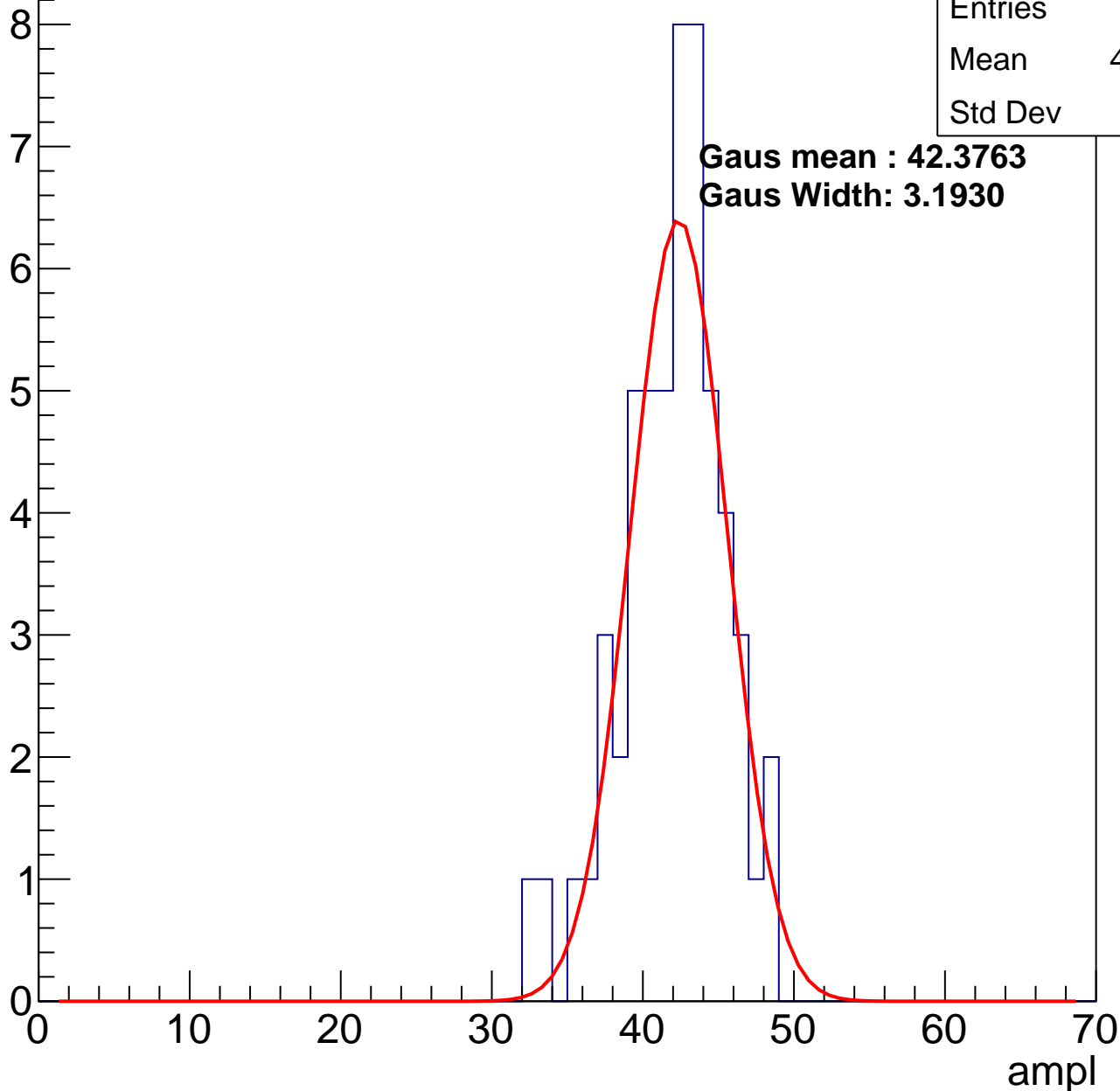
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	41.53
Std Dev	3.41

**Gaus mean : 42.3763**

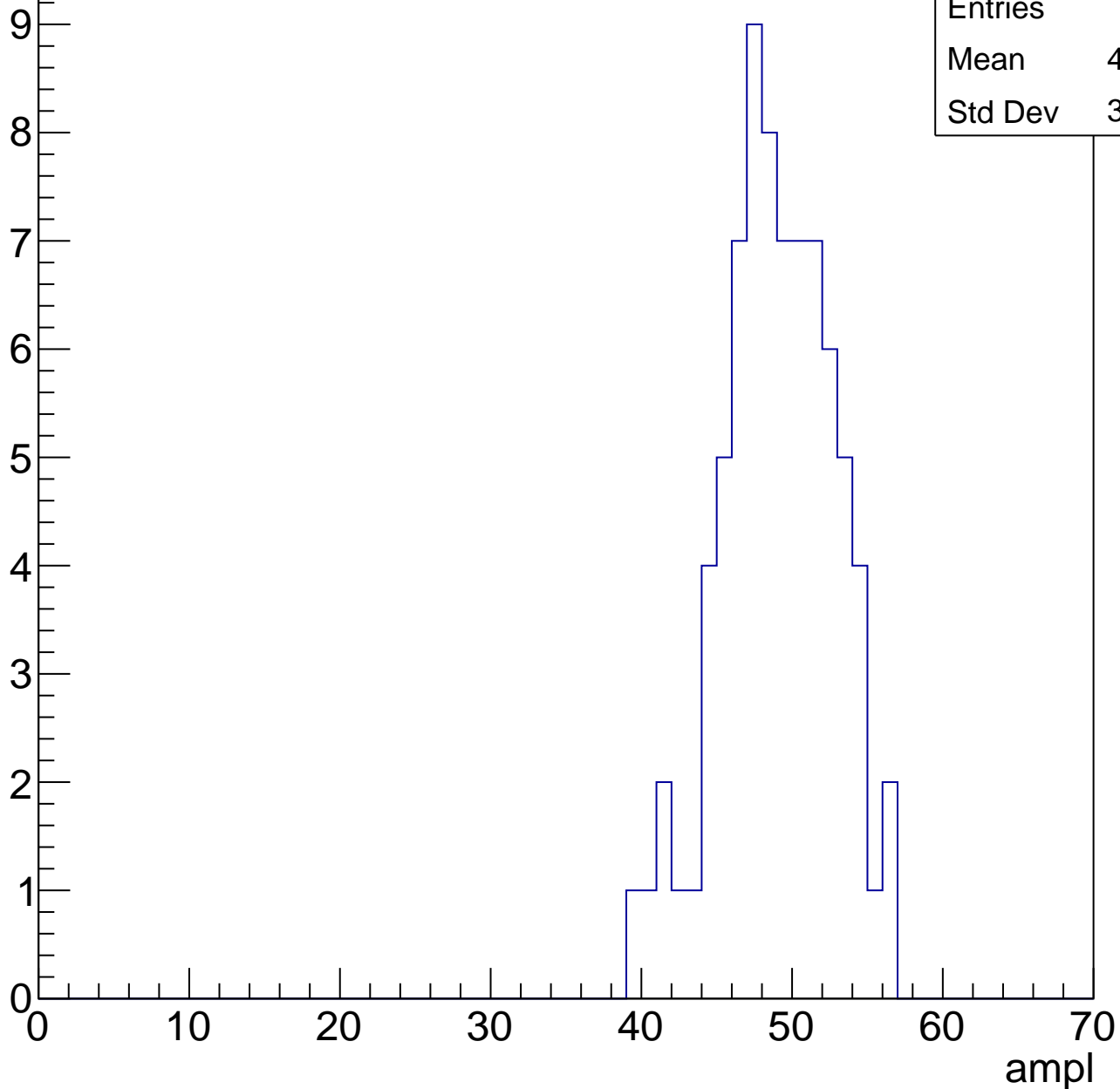
**Gaus Width: 3.1930**



# B0L000S, U19-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

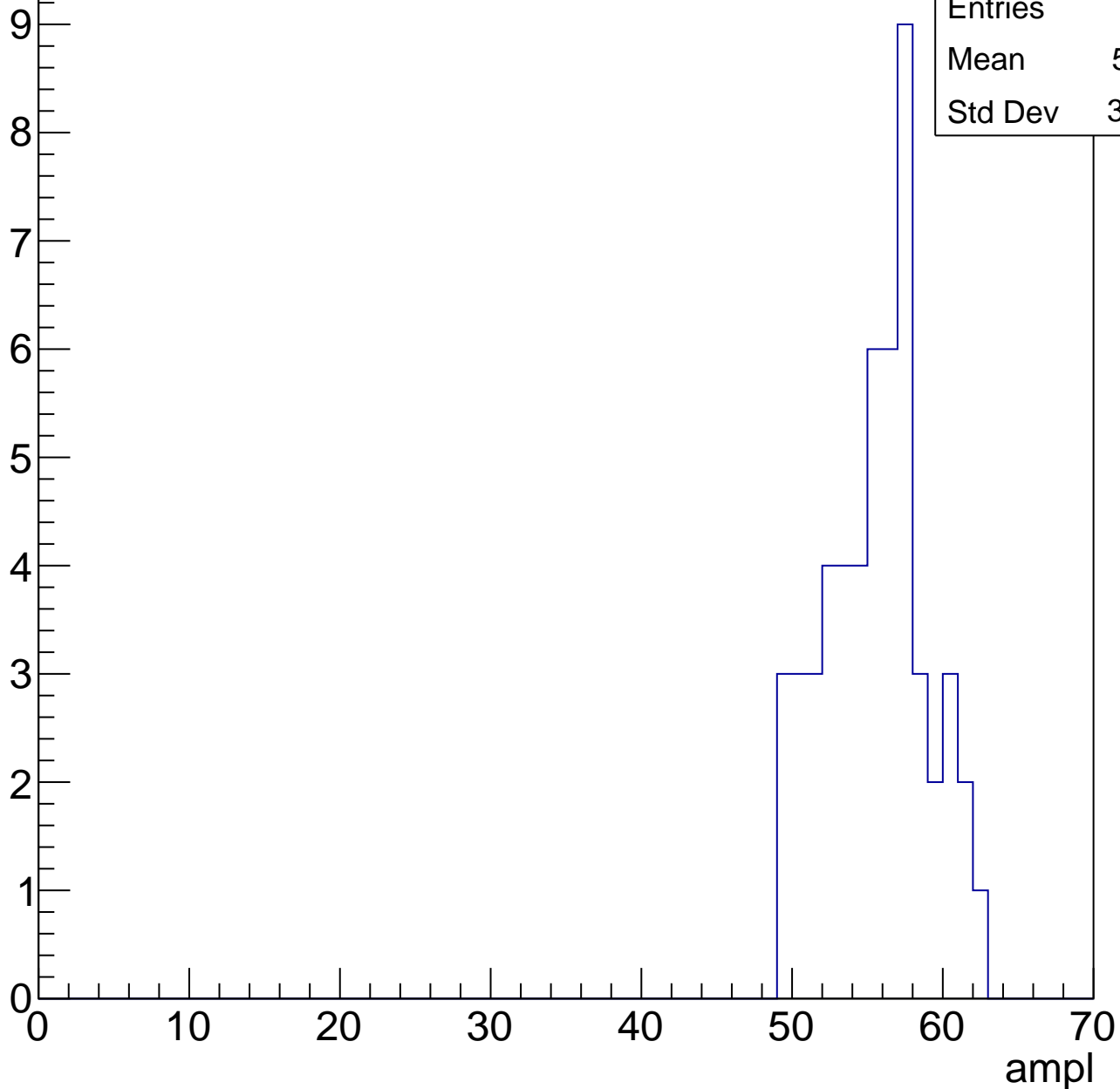


Entries	78
Mean	48.54
Std Dev	3.699

# B0L000S, U19-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

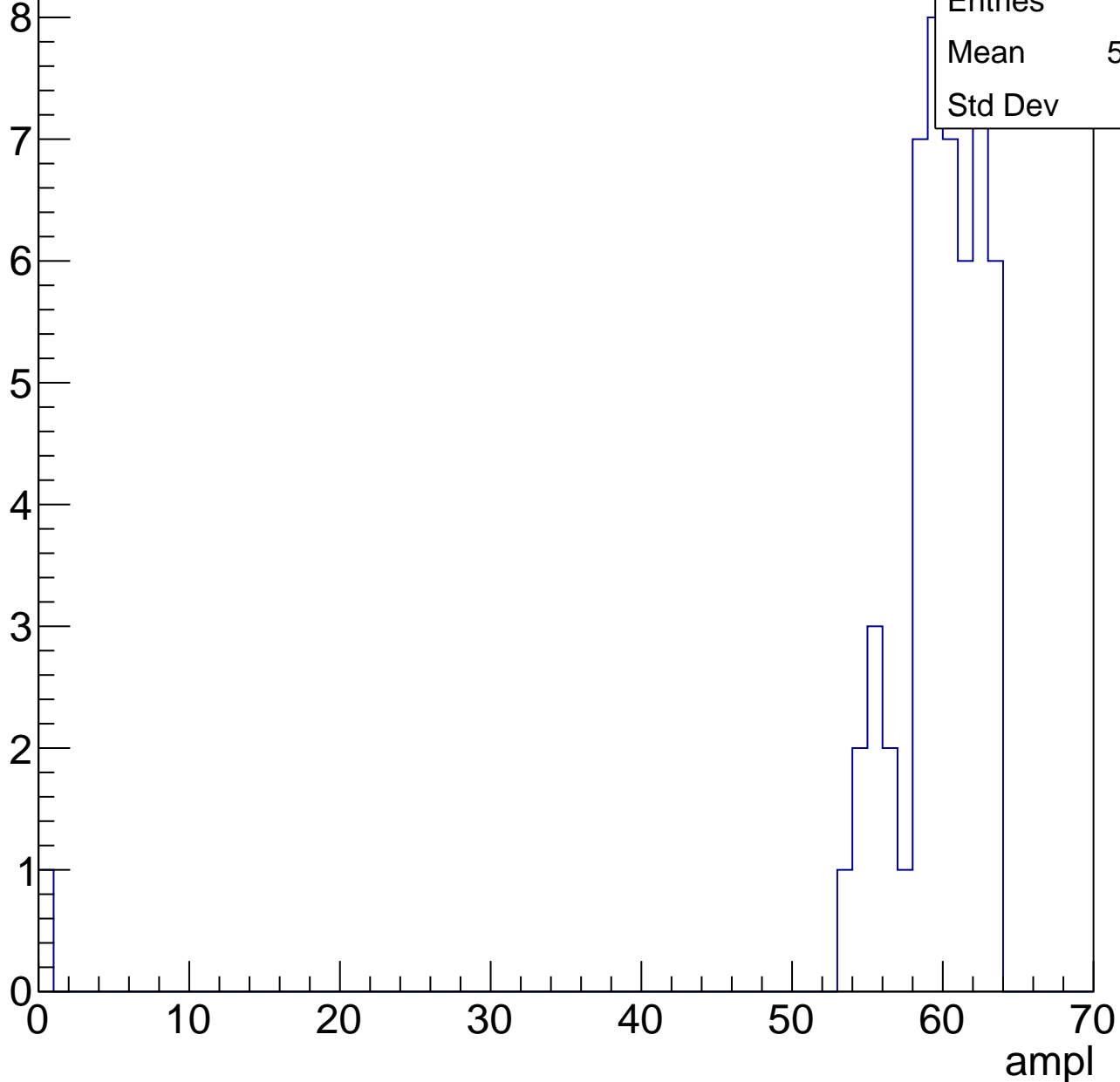
Entry



# B0L000S, U19-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

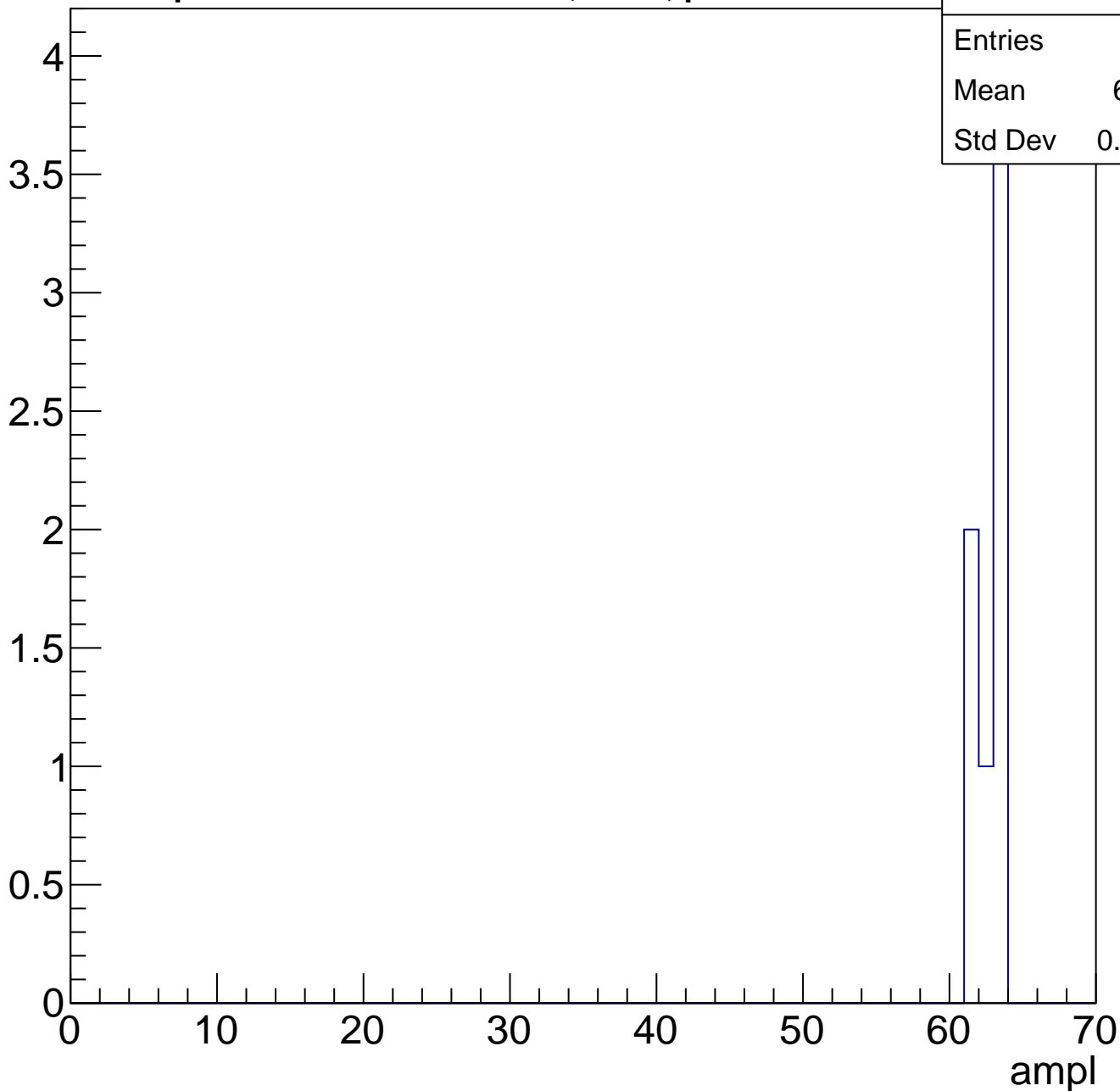
Entry



# B0L000S, U19-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	29.24
Std Dev	5.869

**Gaus mean : 30.8385**

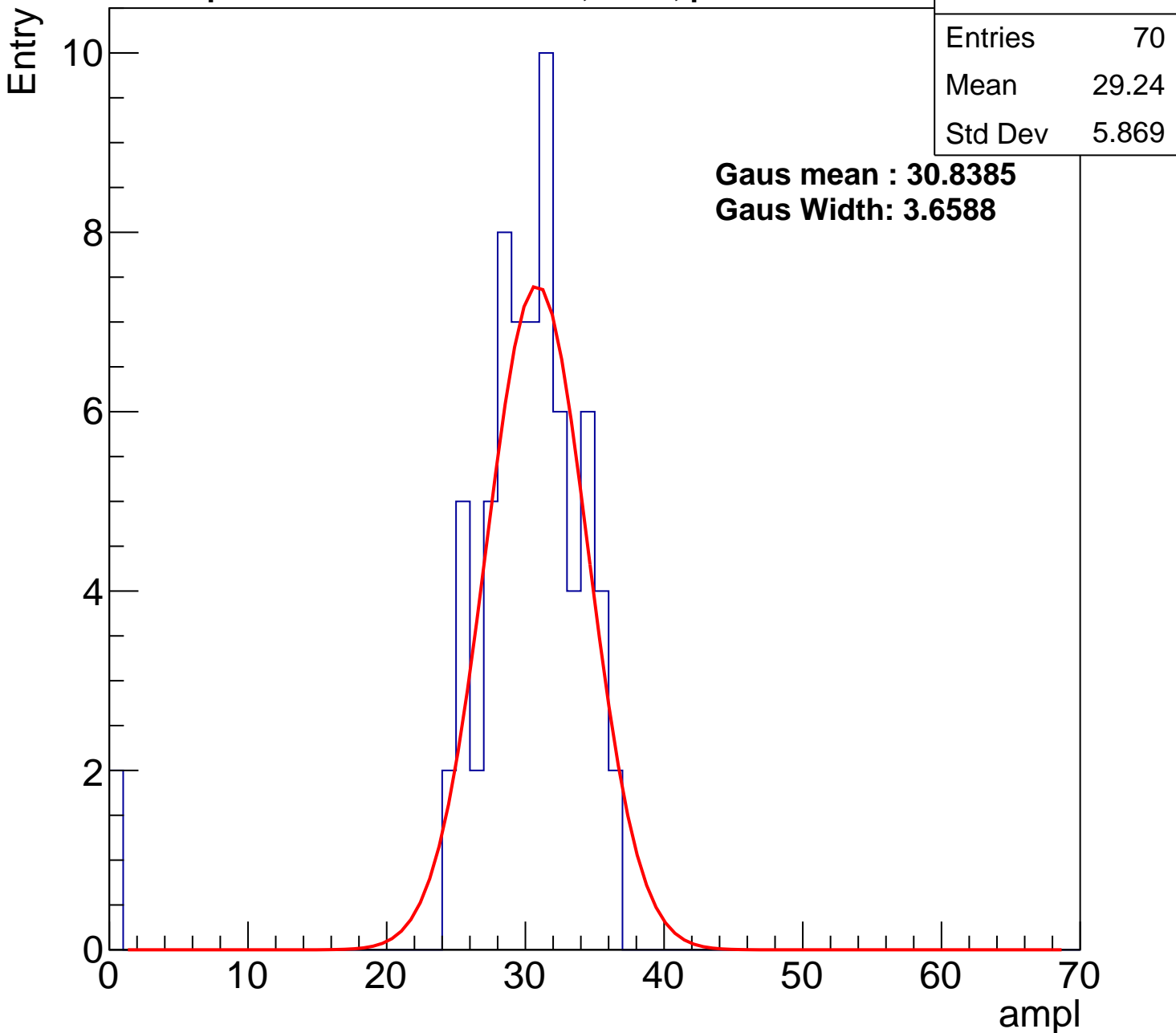
**Gaus Width: 3.6588**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch33, adc1

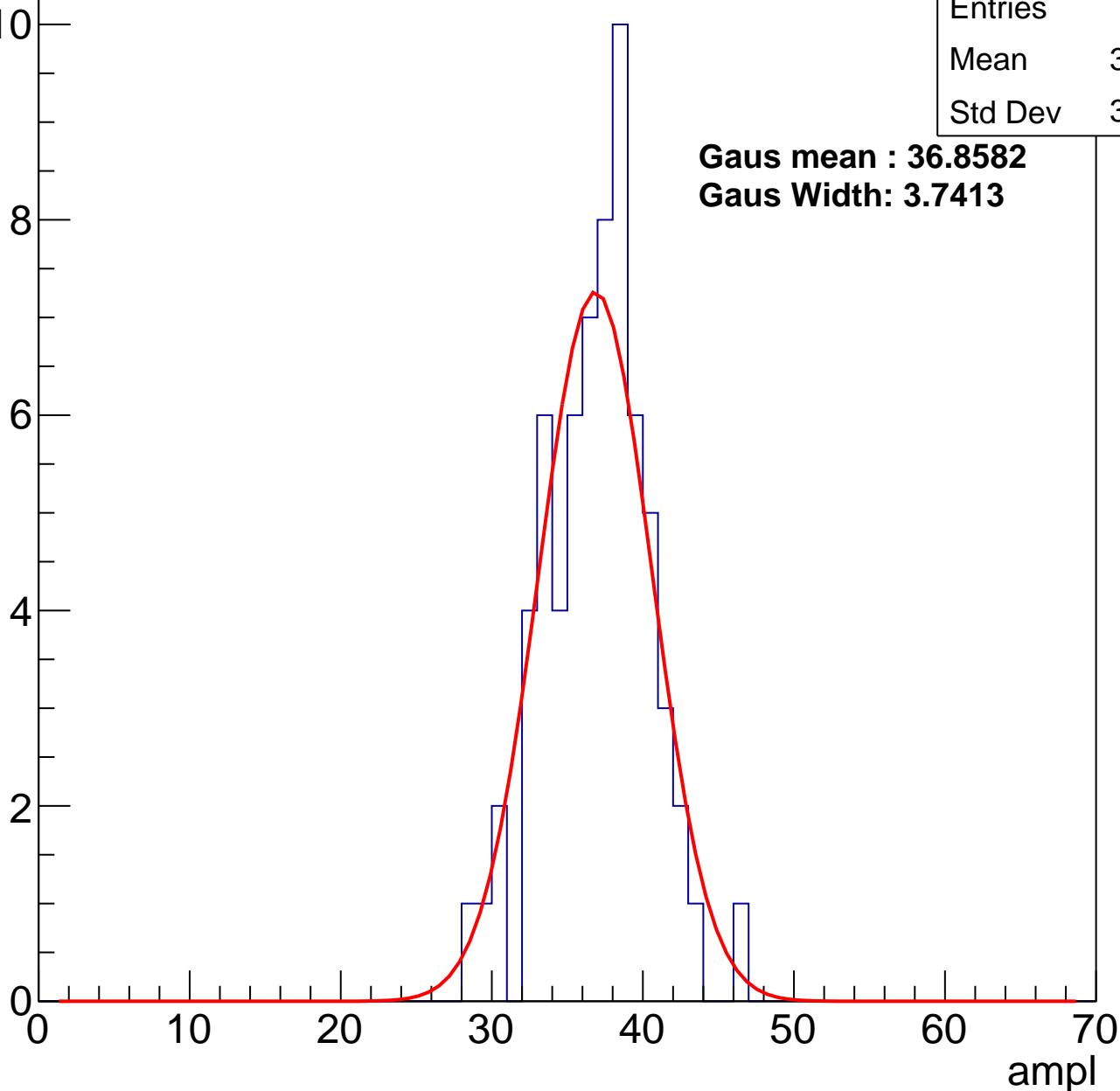
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.52
Std Dev	3.422

**Gaus mean : 36.8582**

**Gaus Width: 3.7413**



# B0L000S, U19-ch33, adc2

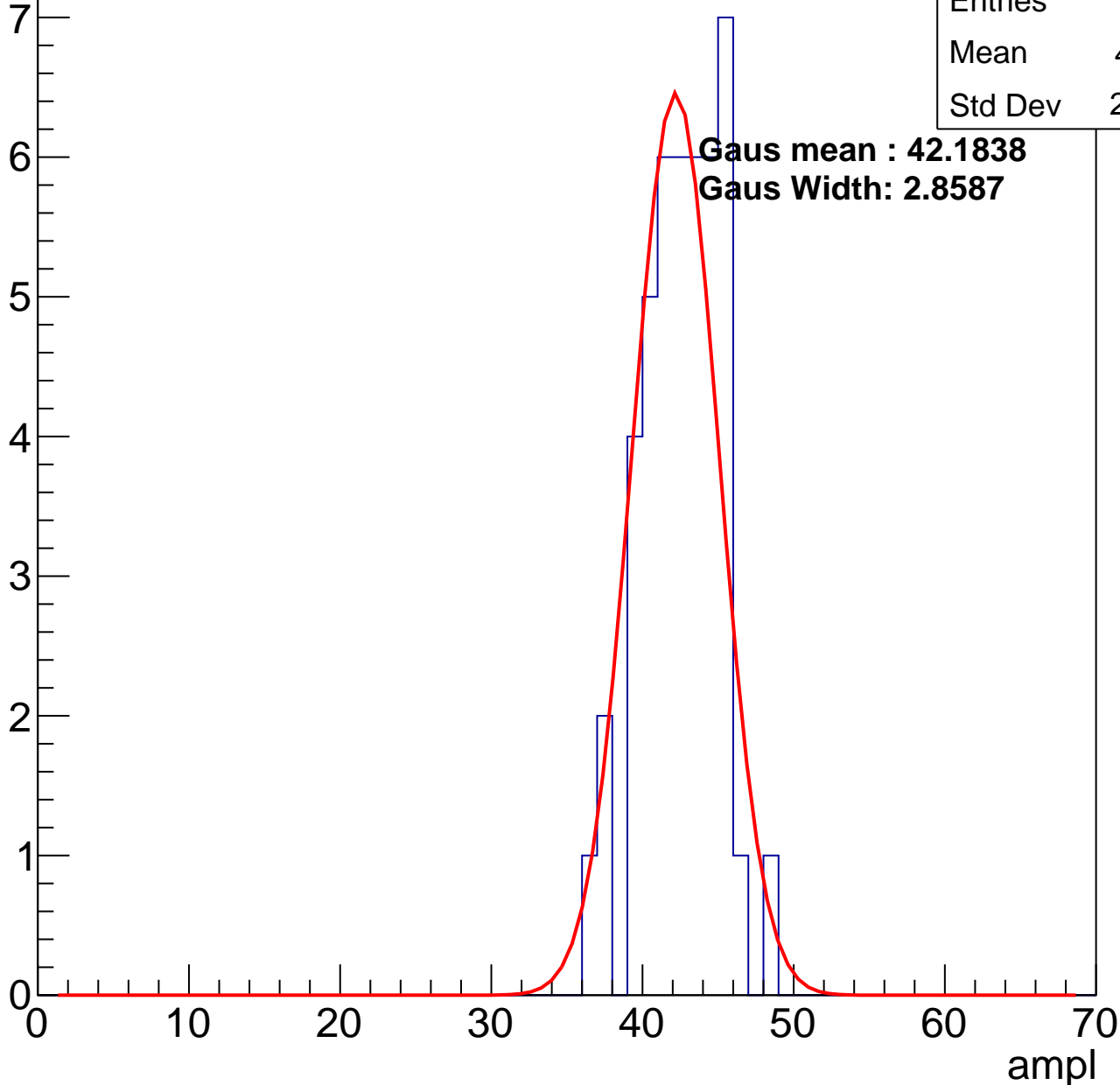
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	42.11
Std Dev	2.549

**Gaus mean : 42.1838**

**Gaus Width: 2.8587**



# B0L000S, U19-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	48.62
Std Dev	3.333

Entry

10

8

6

4

2

0

0

10

20

30

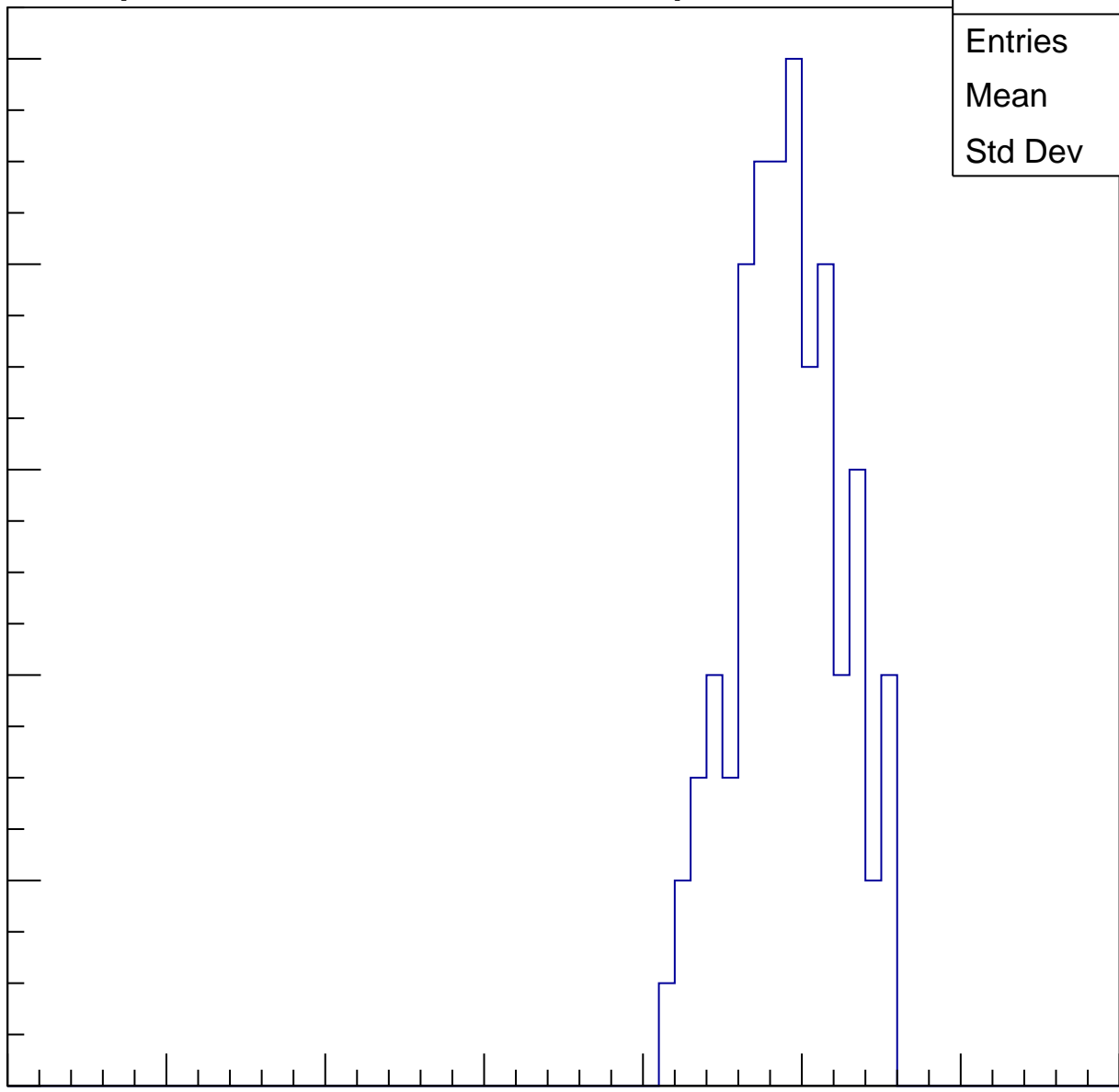
40

50

60

70

ampl

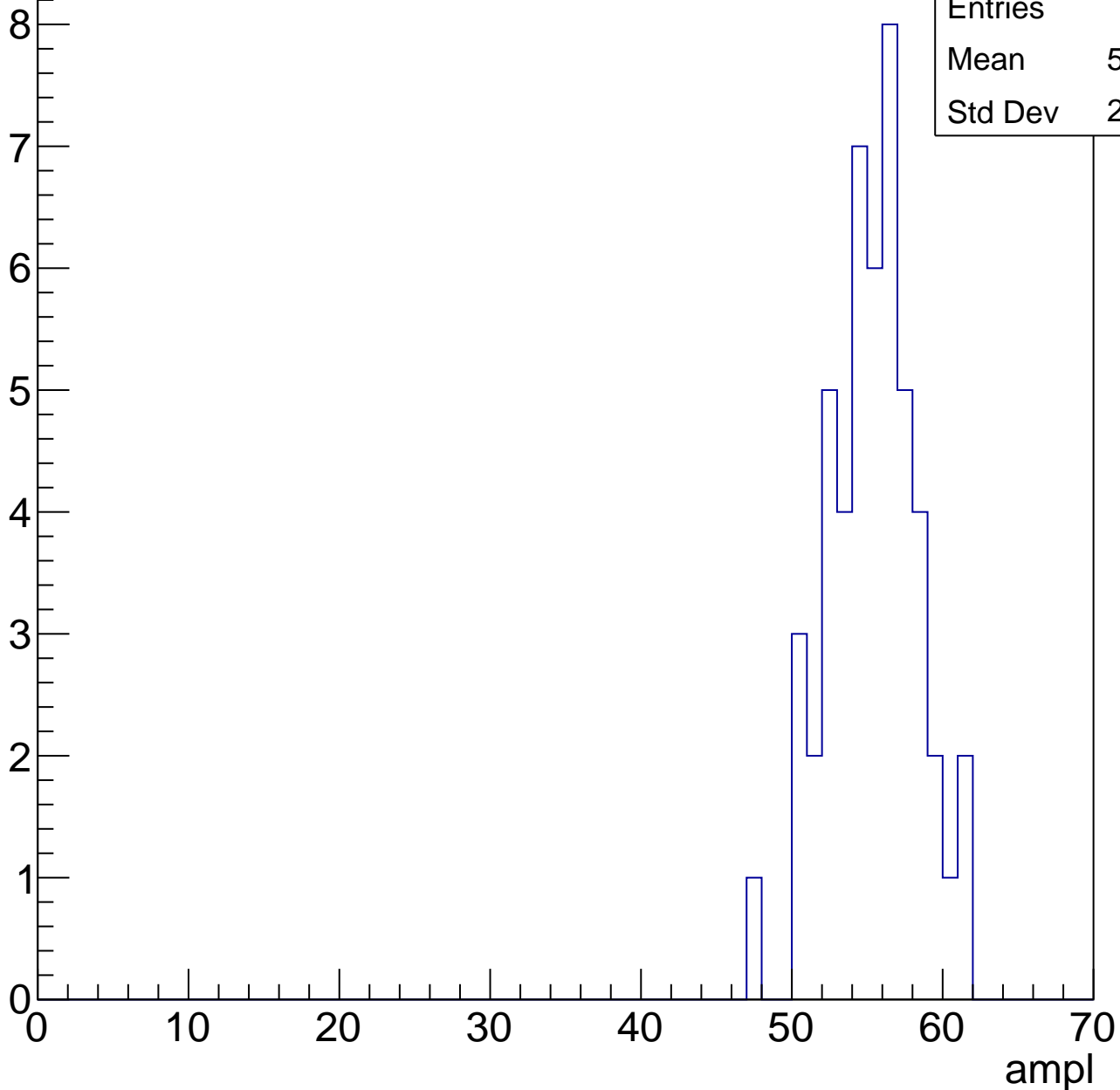


# B0L000S, U19-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	54.88
Std Dev	2.937



# B0L000S, U19-ch33, adc5

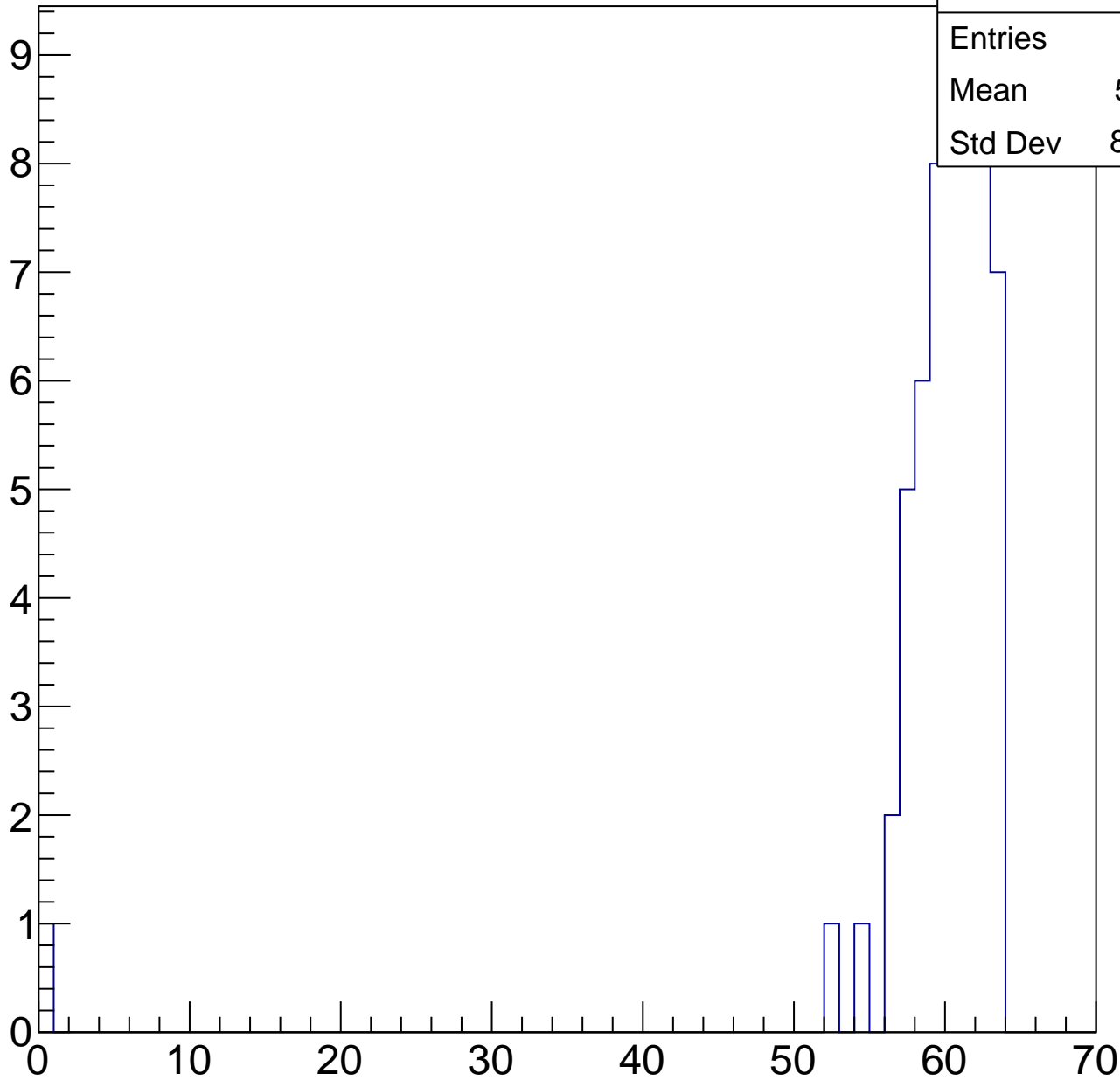
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	58.71
Std Dev	8.259

ampl



# B0L000S, U19-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	62.6
Std Dev	0.4899



# B0L000S, U19-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U19-ch34, adc0

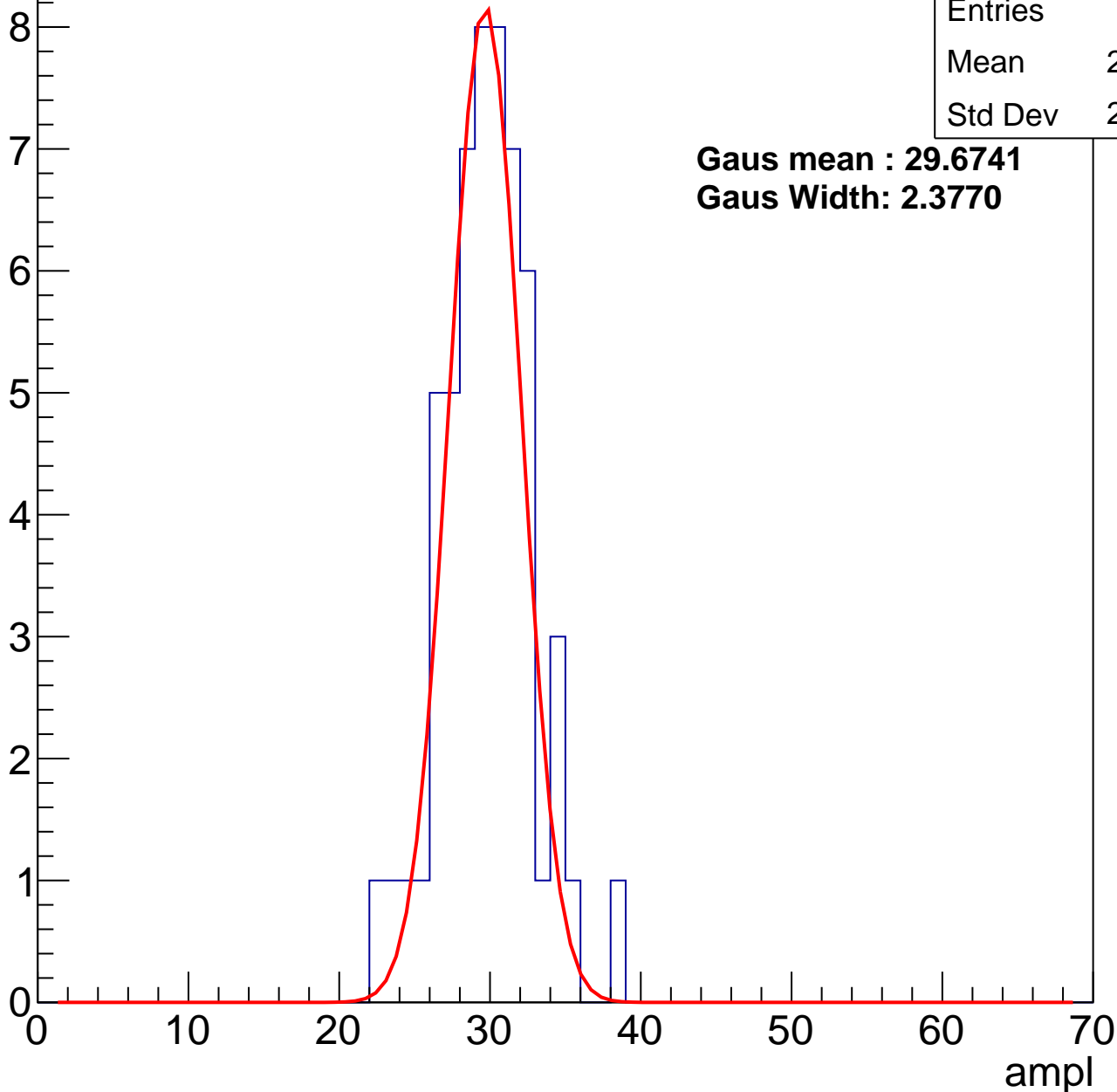
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.36
Std Dev	2.955

**Gaus mean : 29.6741**

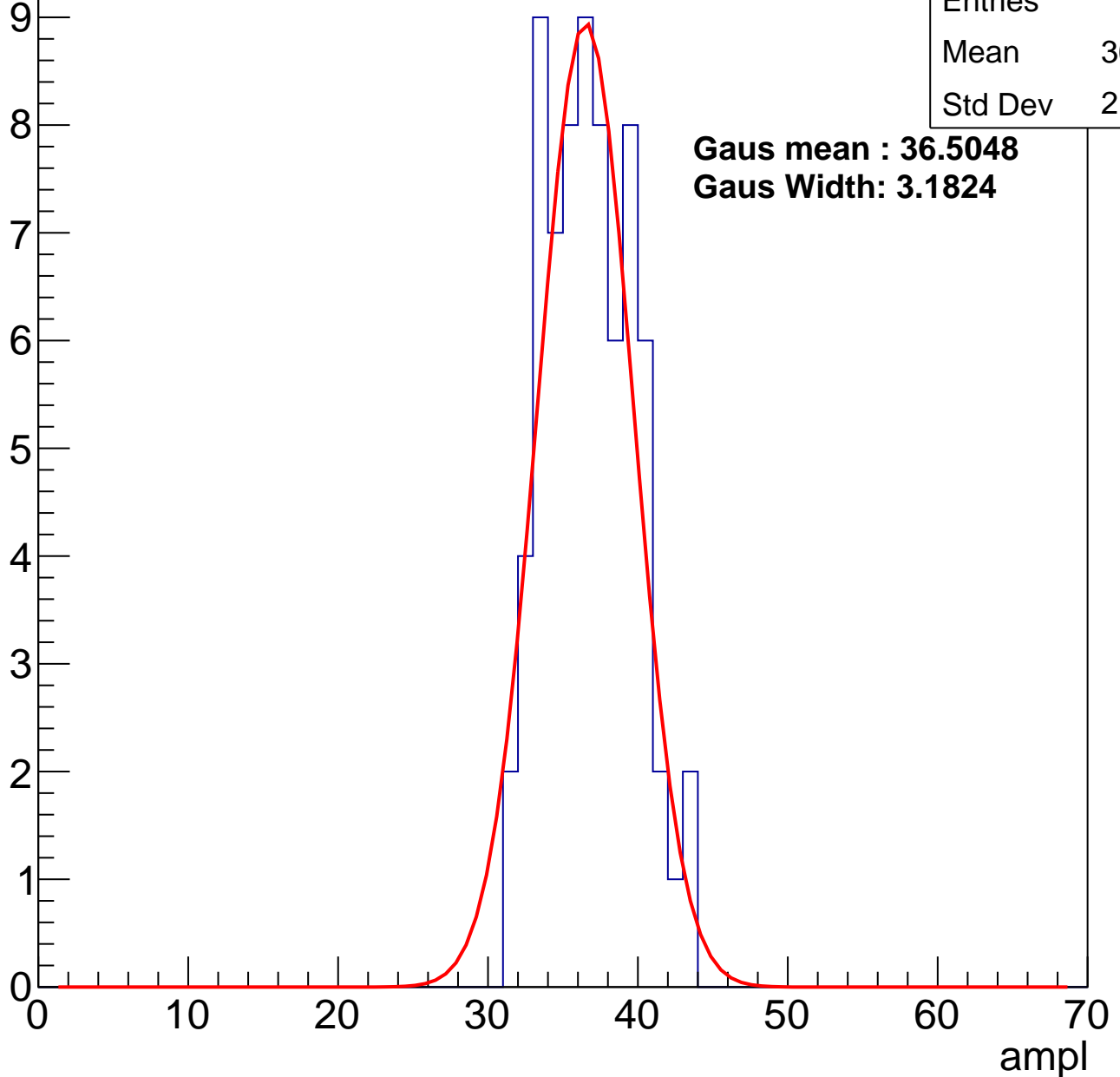
**Gaus Width: 2.3770**



# B0L000S, U19-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch34, adc2

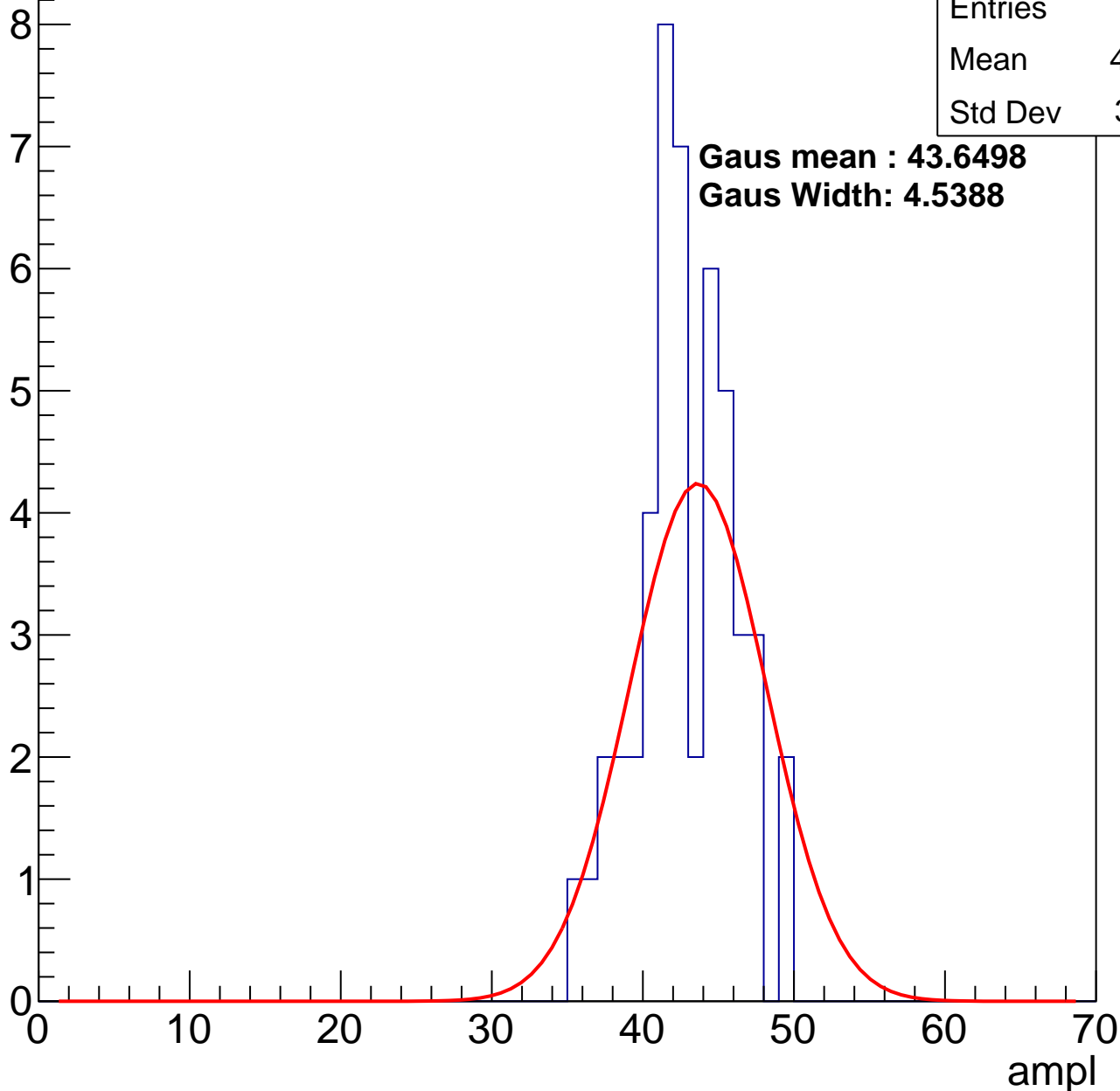
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	42.35
Std Dev	3.211

**Gaus mean : 43.6498**

**Gaus Width: 4.5388**

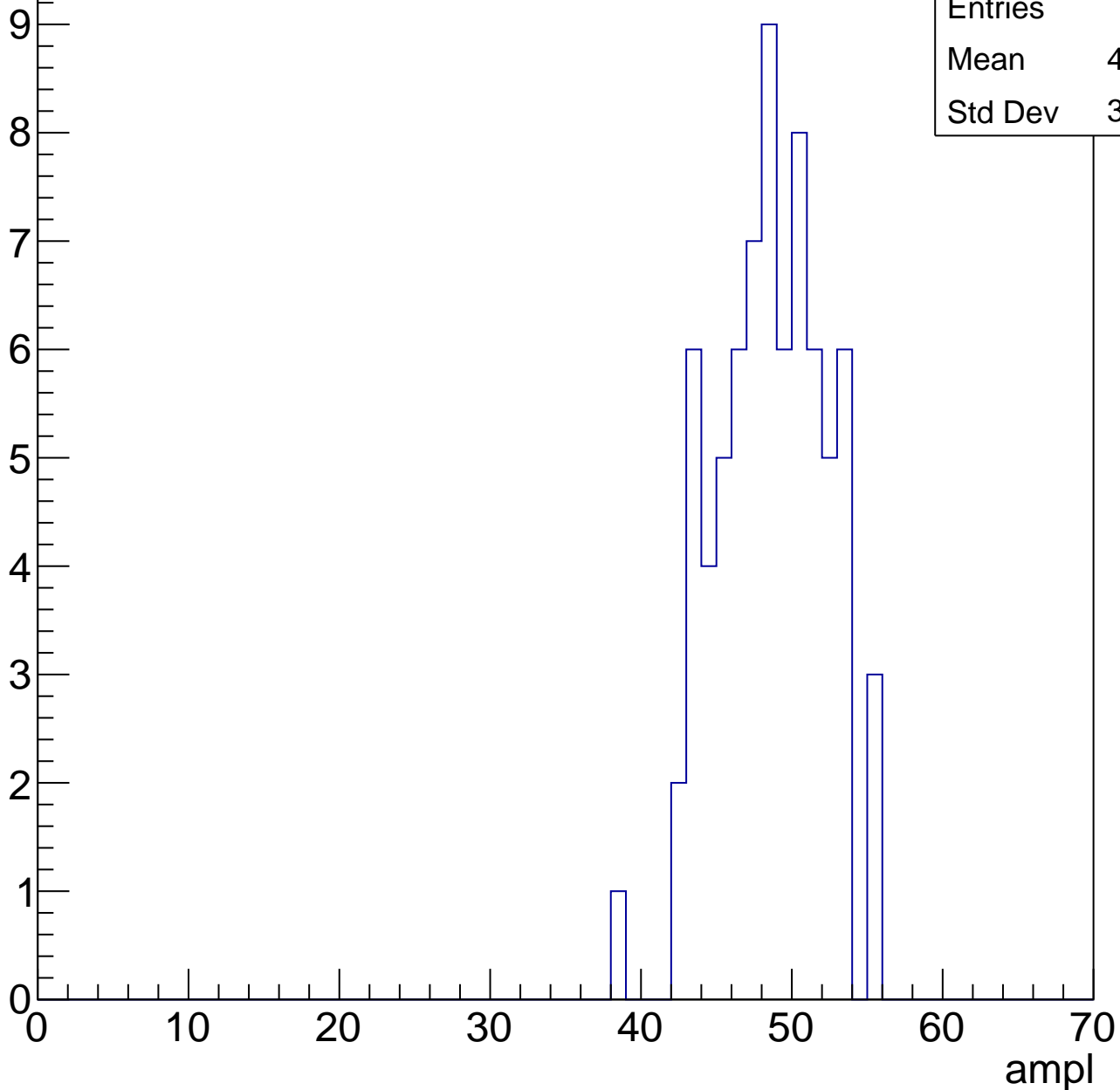


# B0L000S, U19-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	48.12
Std Dev	3.545

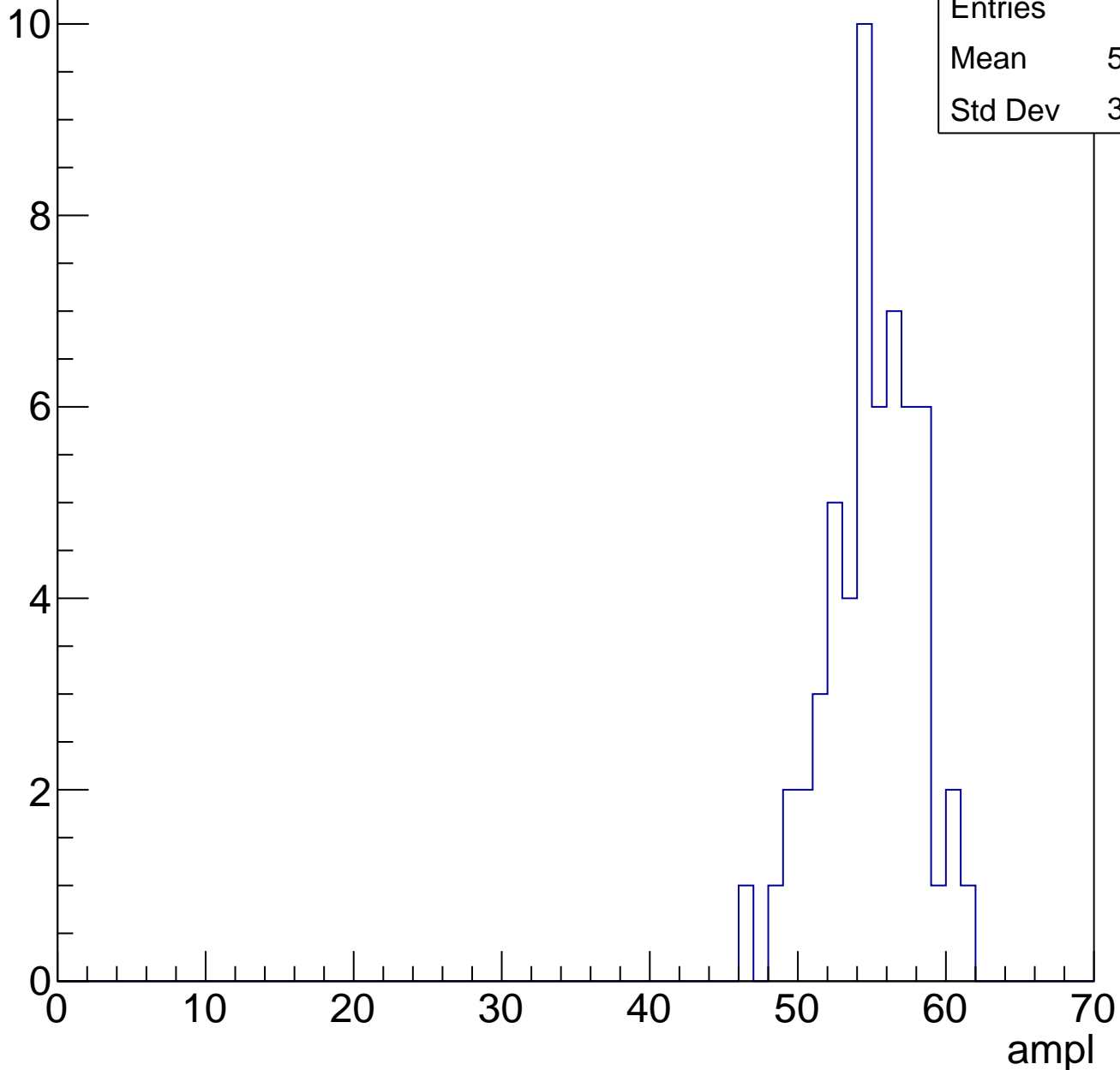


# B0L000S, U19-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	54.54
Std Dev	3.095

Entry

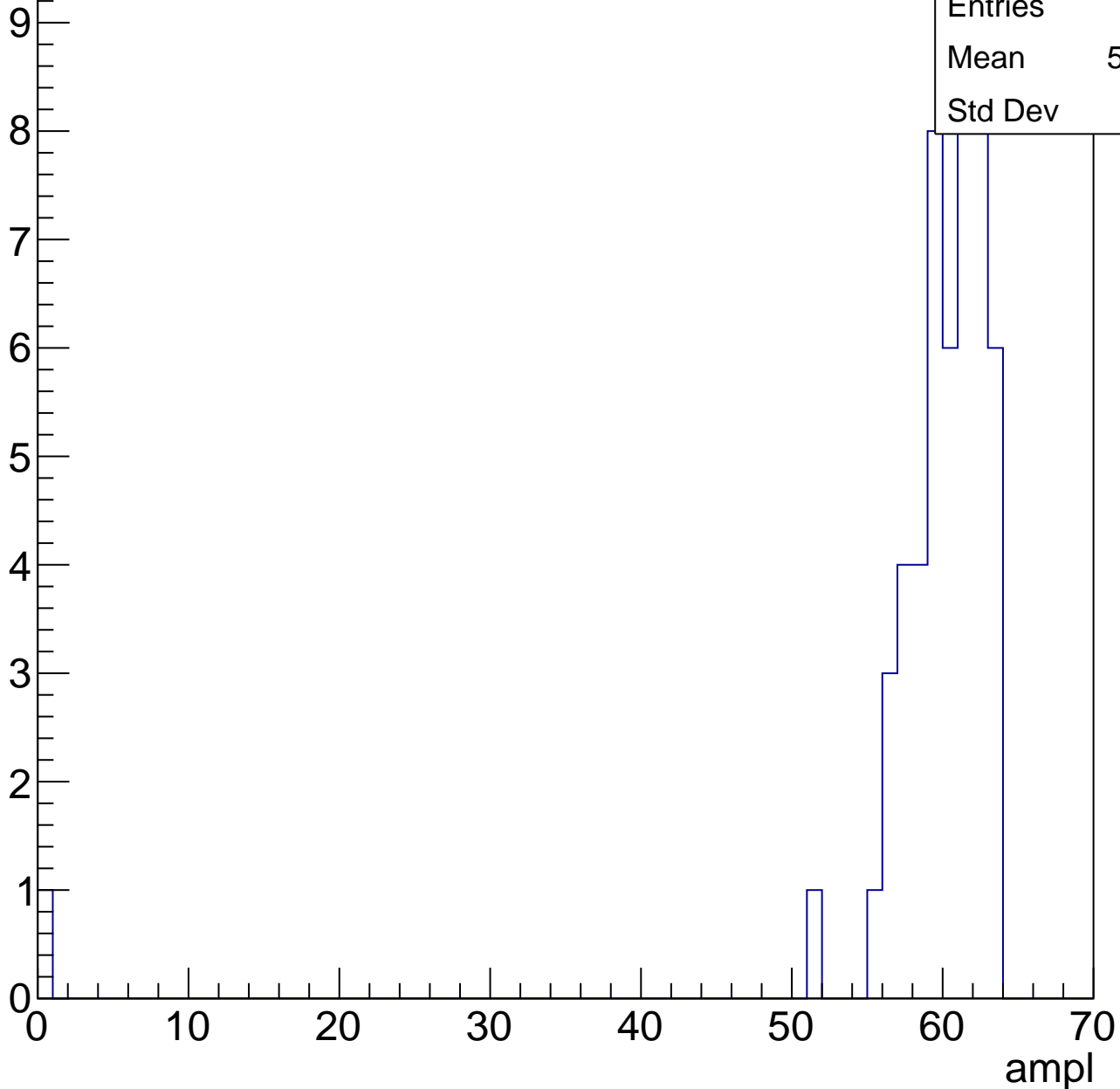


# B0L000S, U19-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

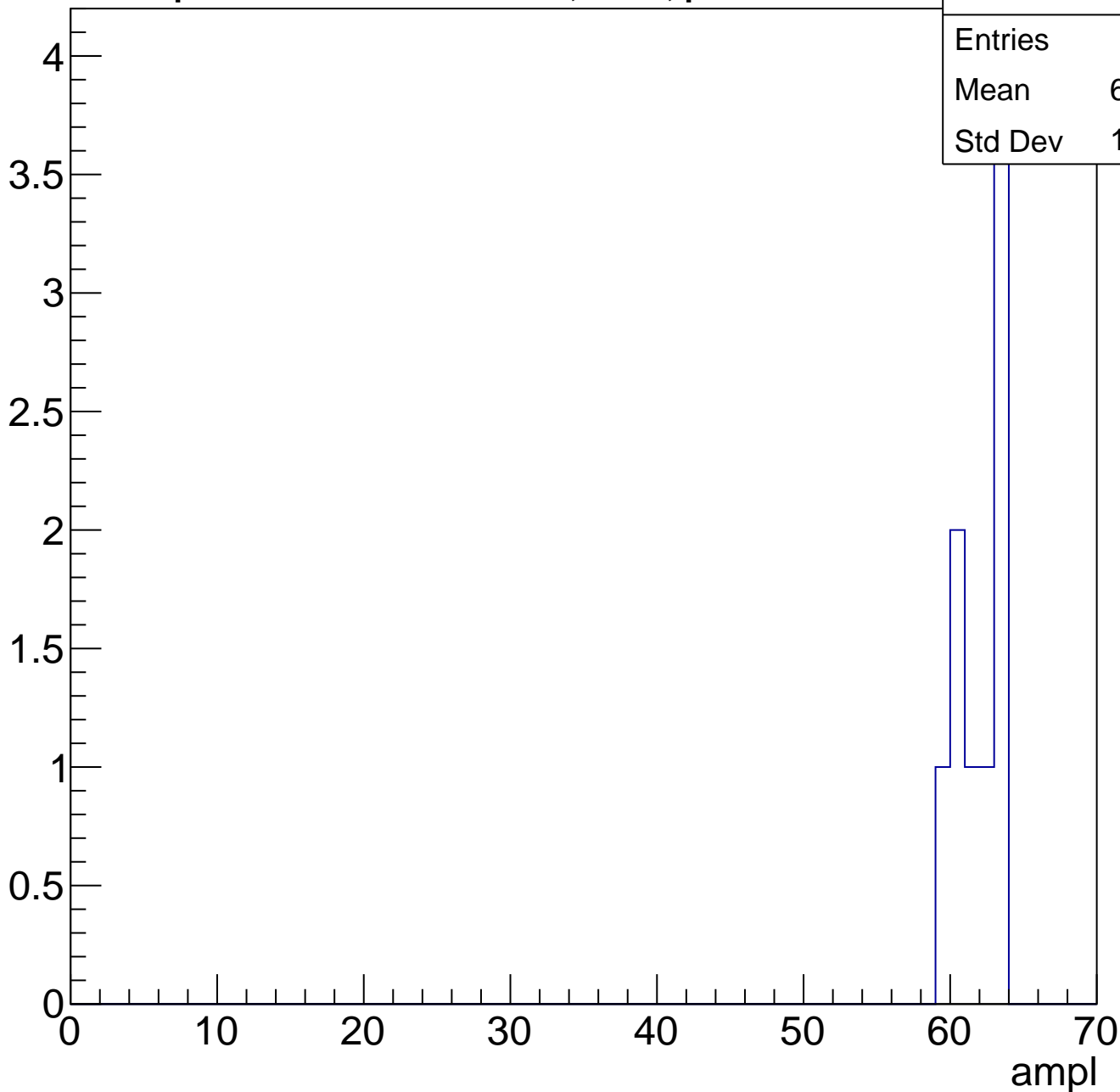
Entries	51
Mean	58.63
Std Dev	8.65



# B0L000S, U19-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U19-ch35, adc0

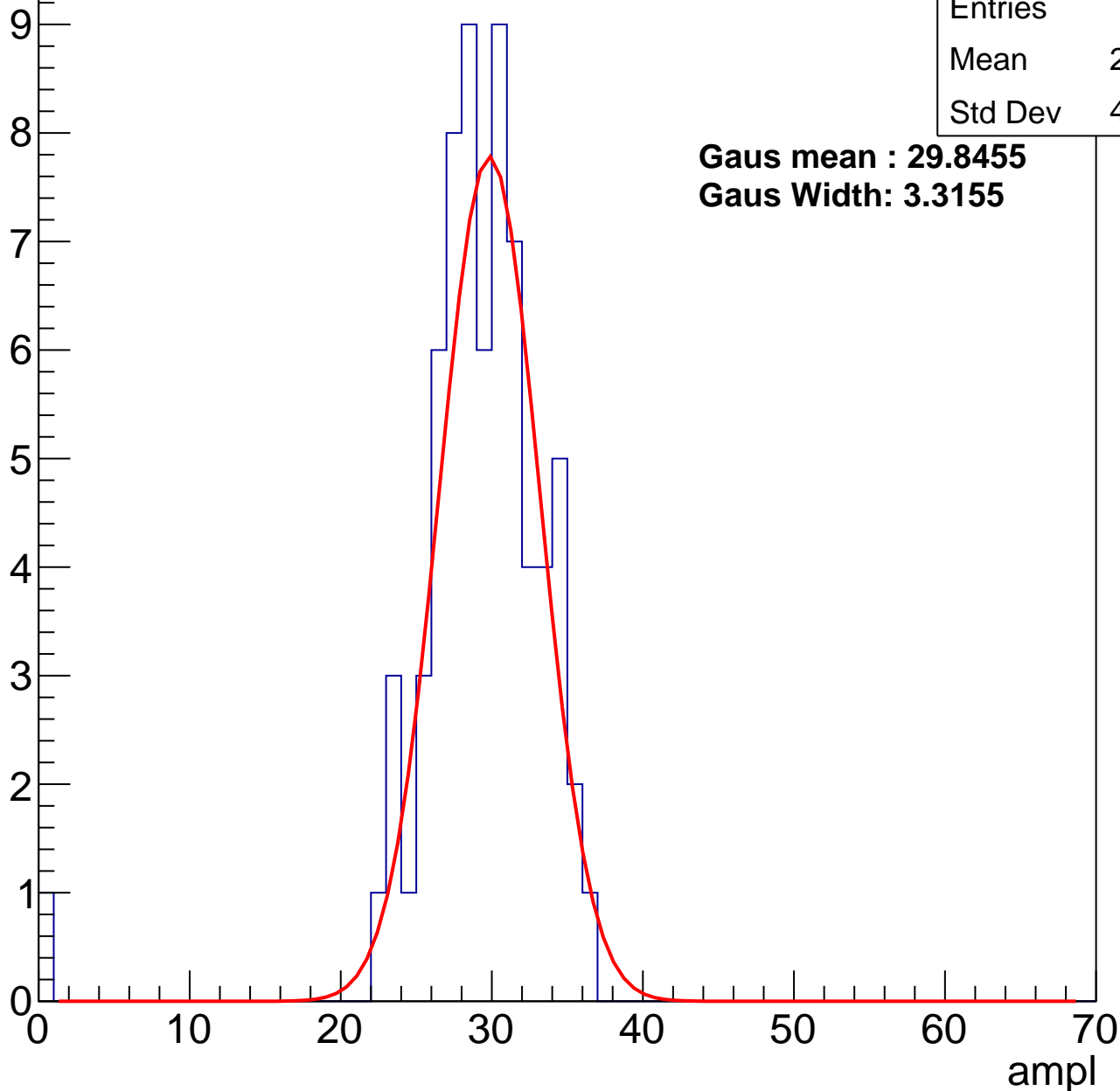
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	28.73
Std Dev	4.696

**Gaus mean : 29.8455**

**Gaus Width: 3.3155**



# B0L000S, U19-ch35, adc1

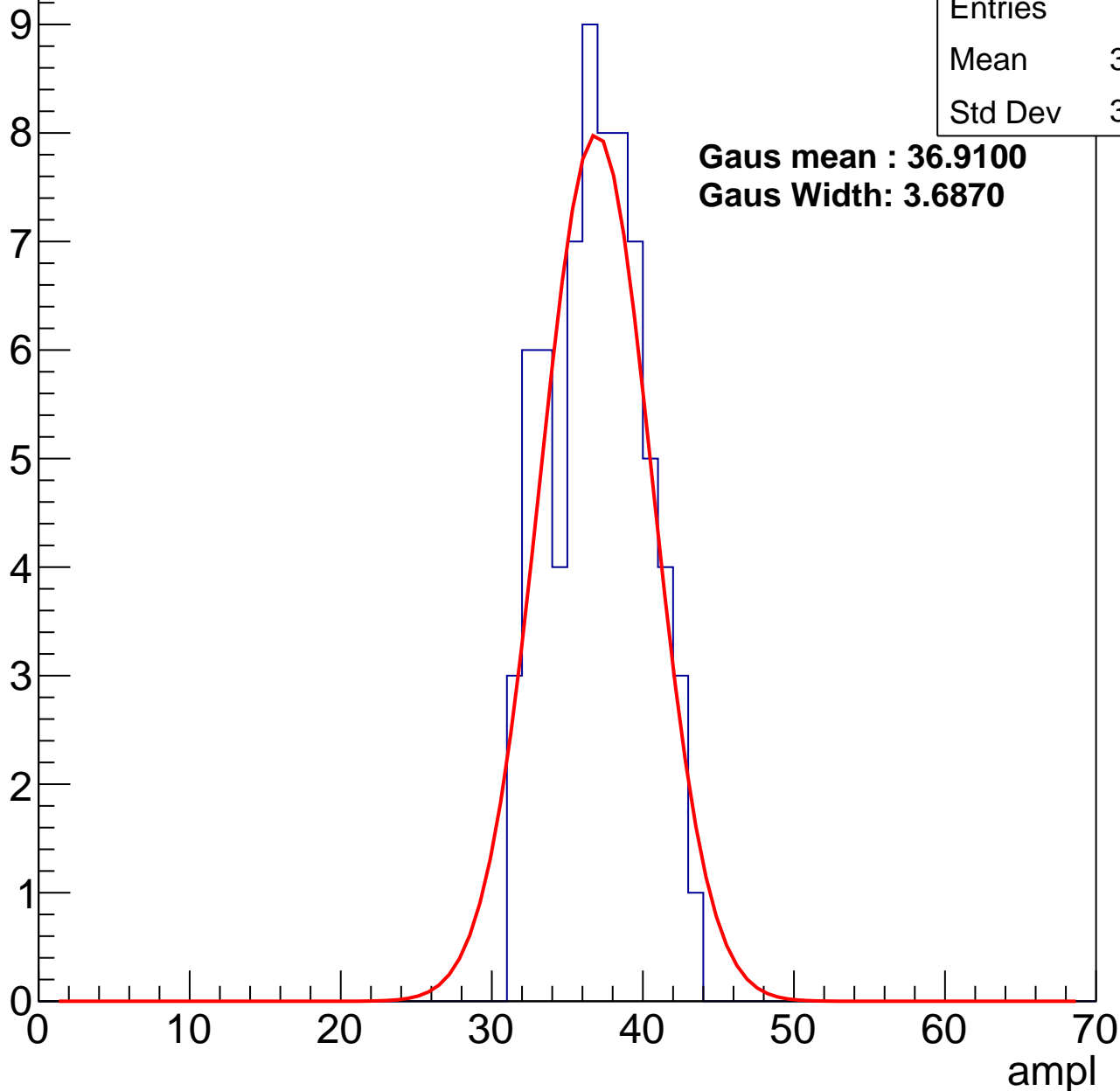
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.54
Std Dev	3.067

**Gaus mean : 36.9100**

**Gaus Width: 3.6870**

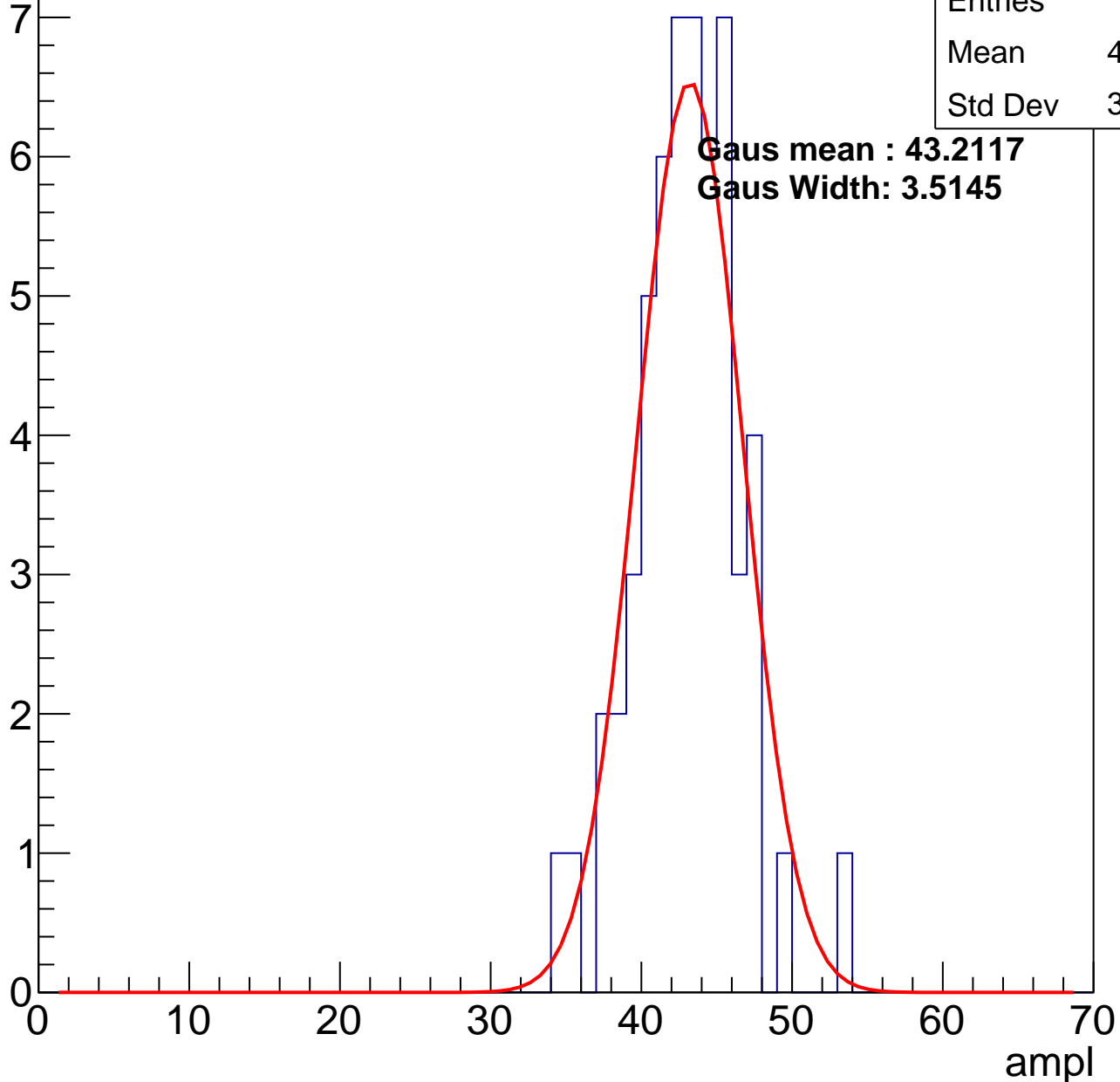


# B0L000S, U19-ch35, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.57
Std Dev	3.385

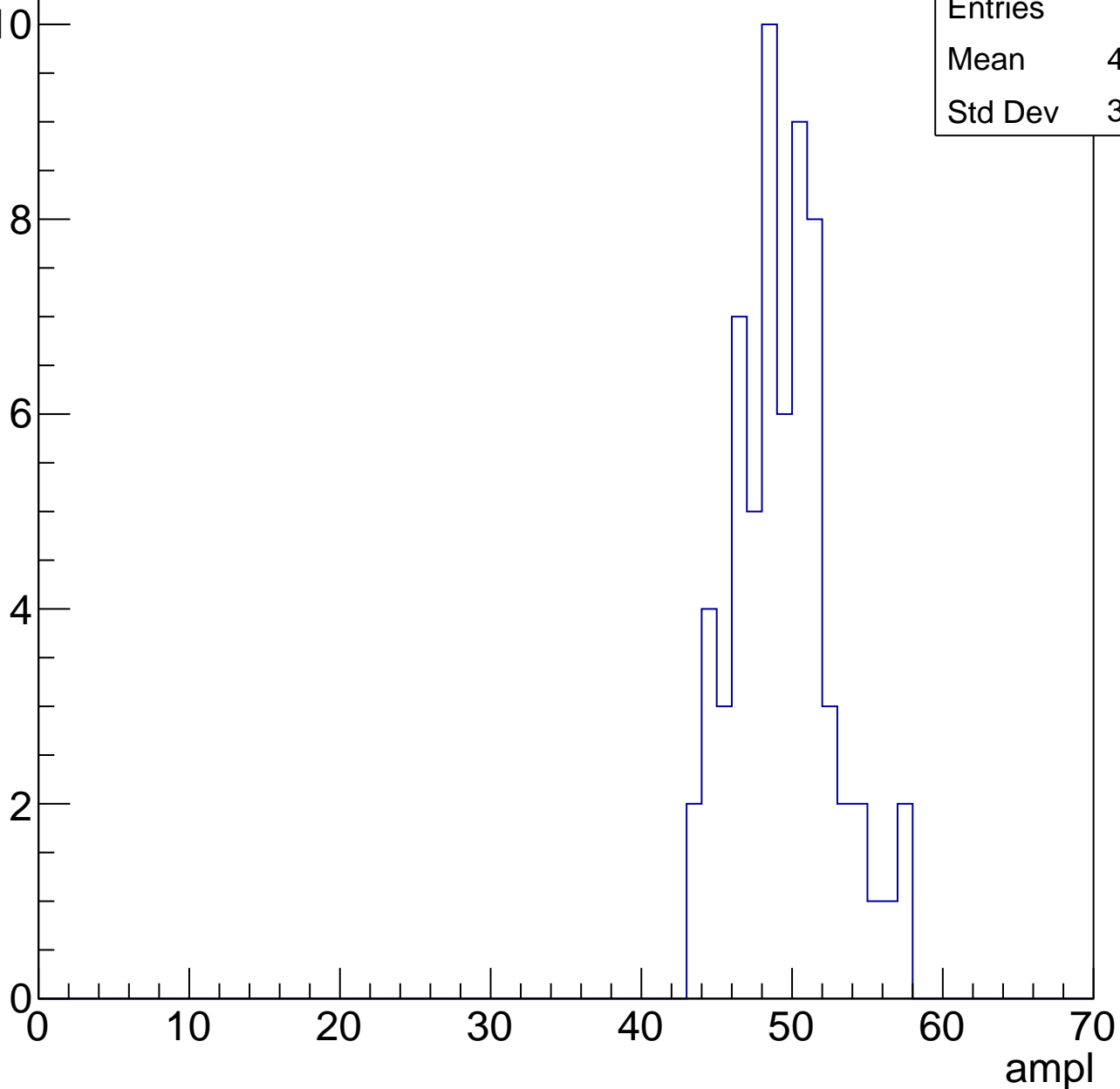


# B0L000S, U19-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.94
Std Dev	3.224

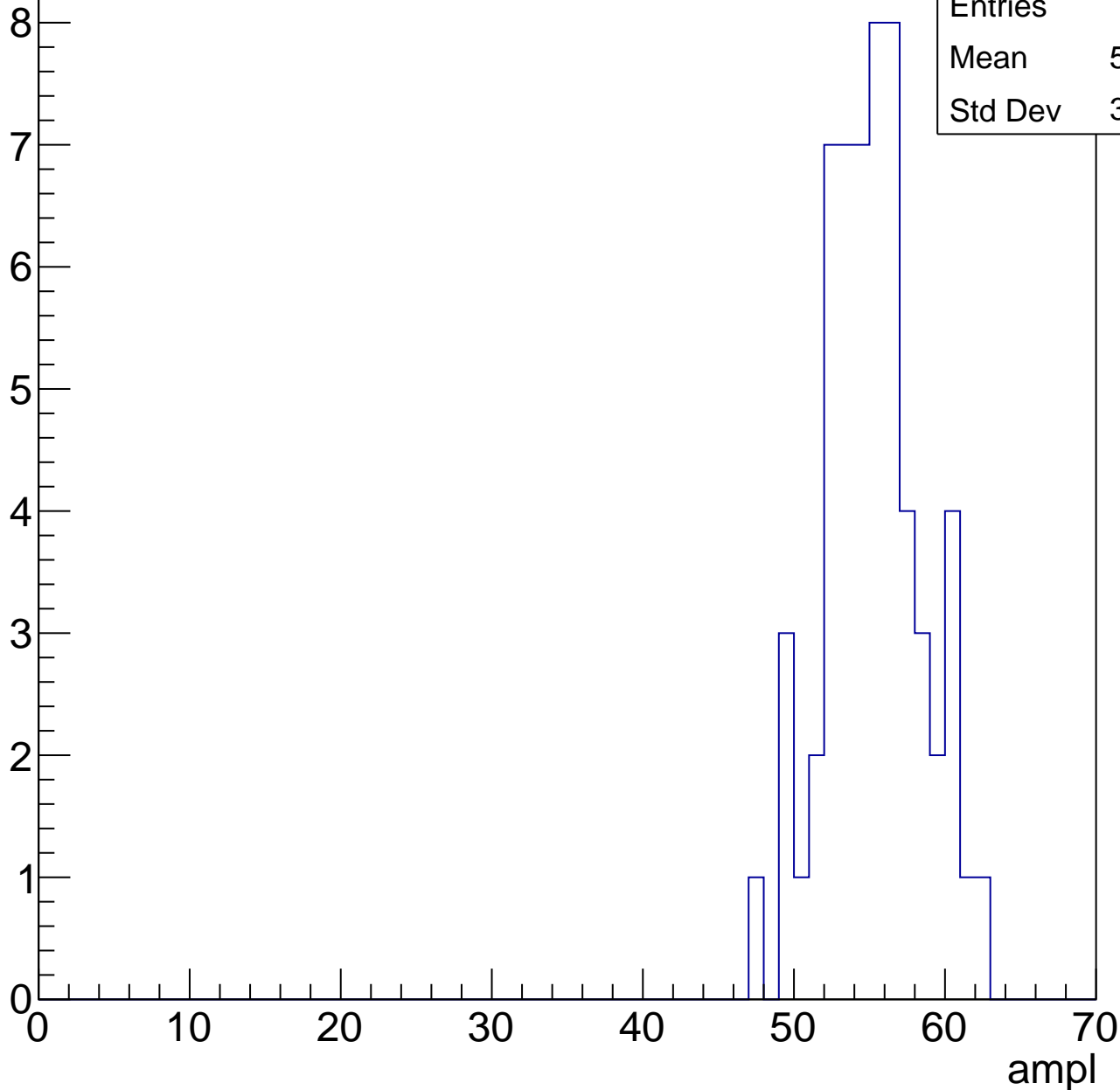


# B0L000S, U19-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54.75
Std Dev	3.165



# B0L000S, U19-ch35, adc5

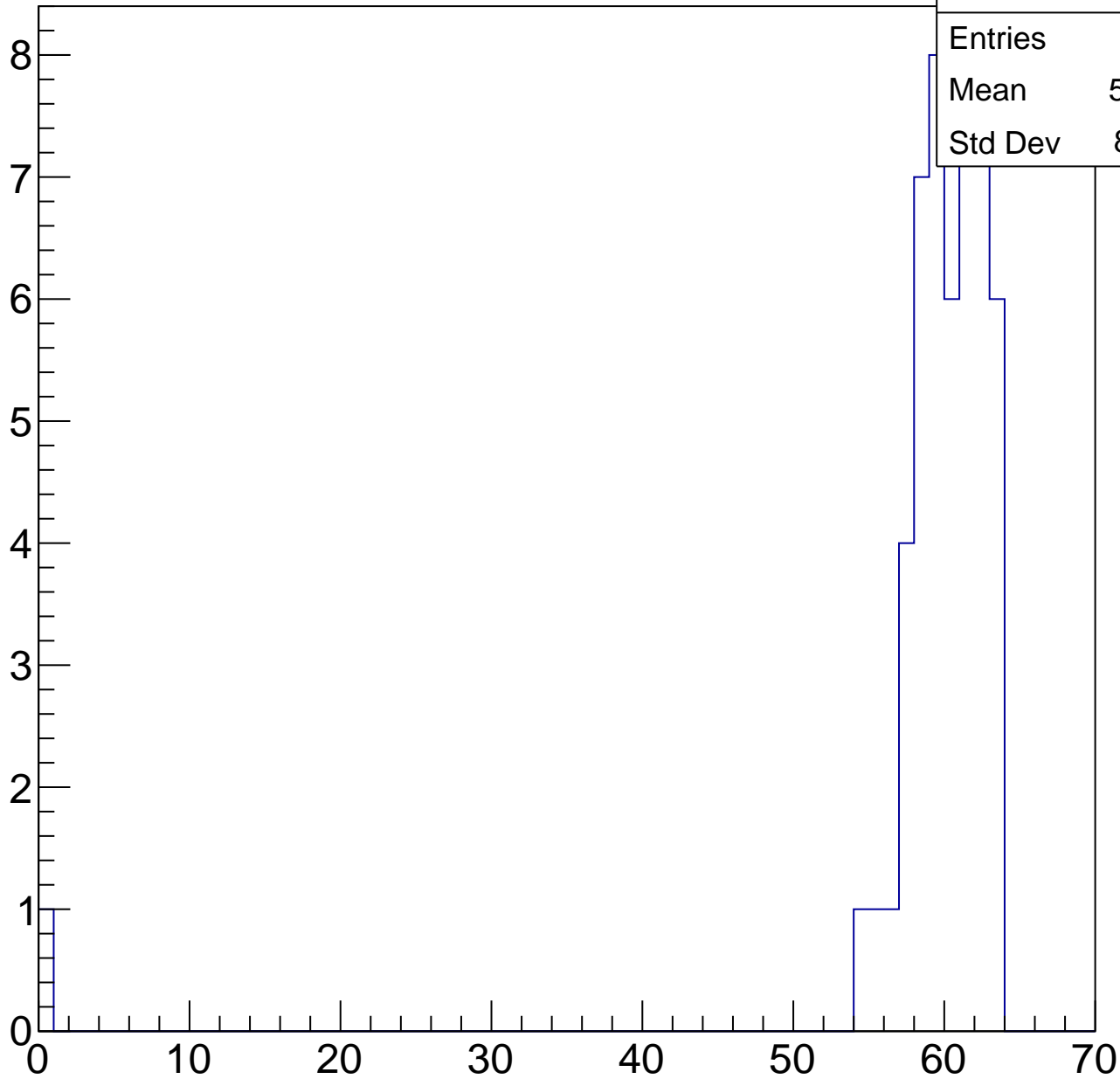
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	58.69
Std Dev	8.581

ampl



# B0L000S, U19-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch36, adc0

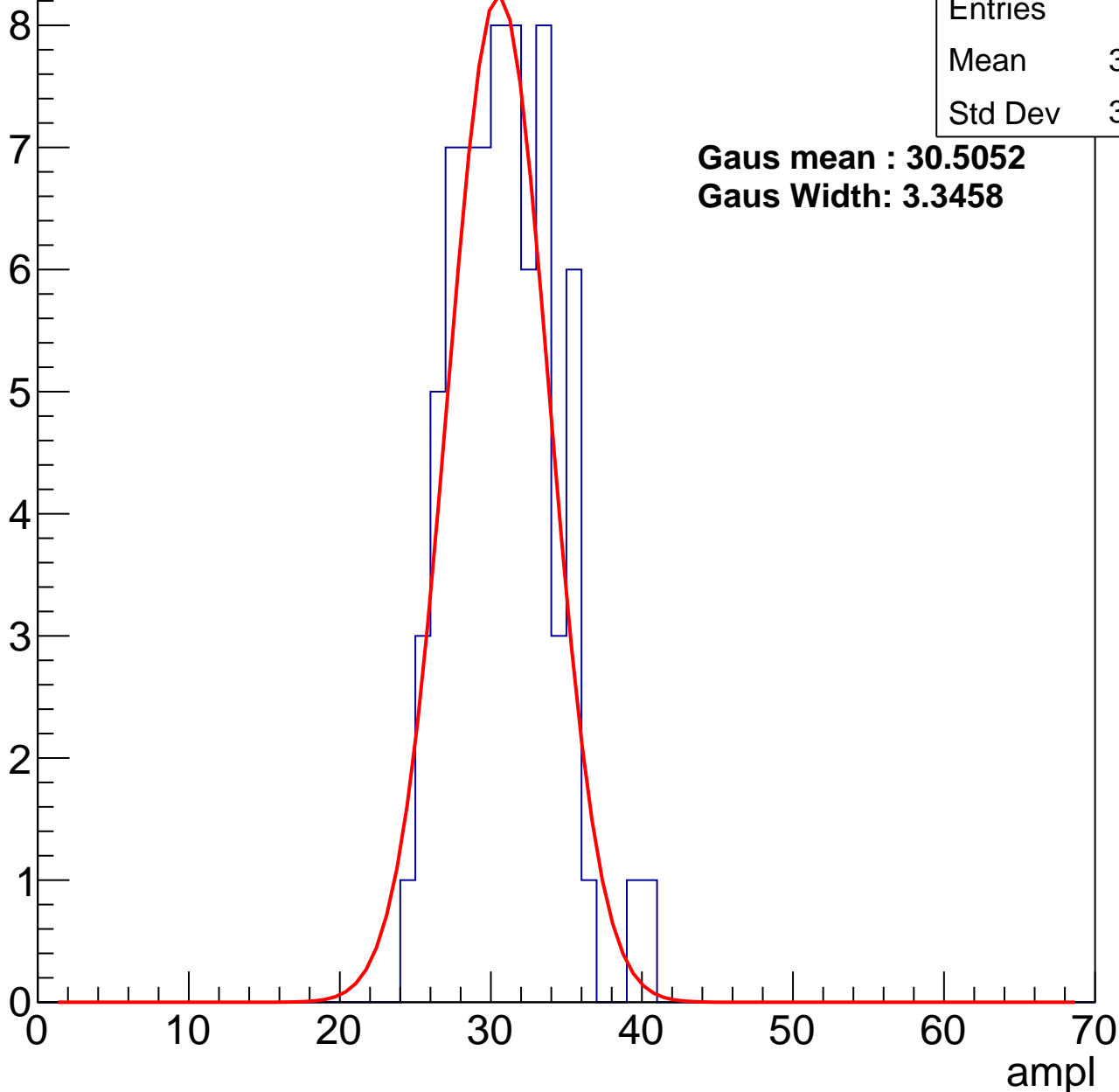
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.39
Std Dev	3.327

**Gaus mean : 30.5052**

**Gaus Width: 3.3458**



# B0L000S, U19-ch36, adc1

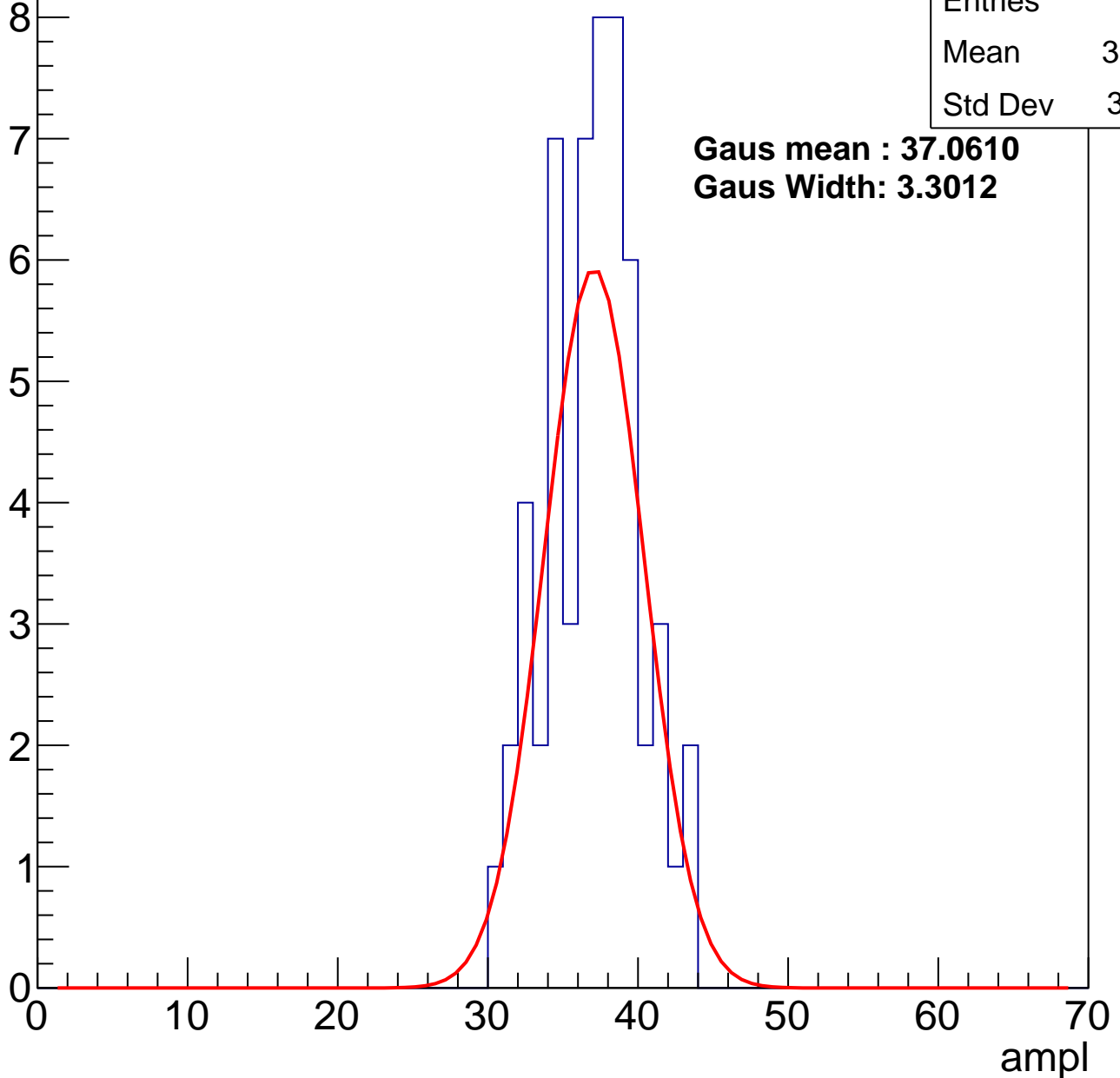
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.54
Std Dev	3.041

**Gaus mean : 37.0610**

**Gaus Width: 3.3012**

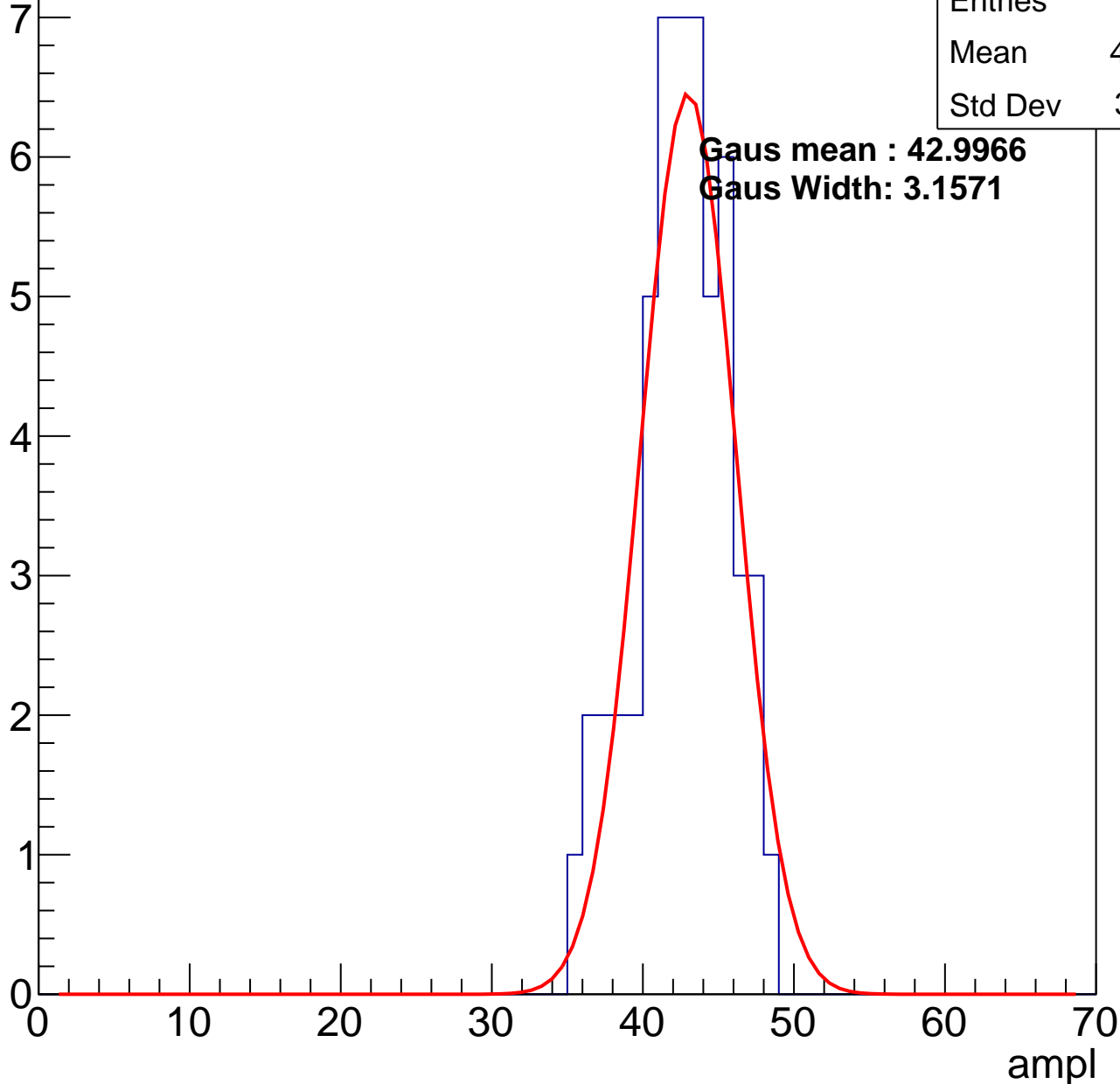


# B0L000S, U19-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	42.15
Std Dev	3.031

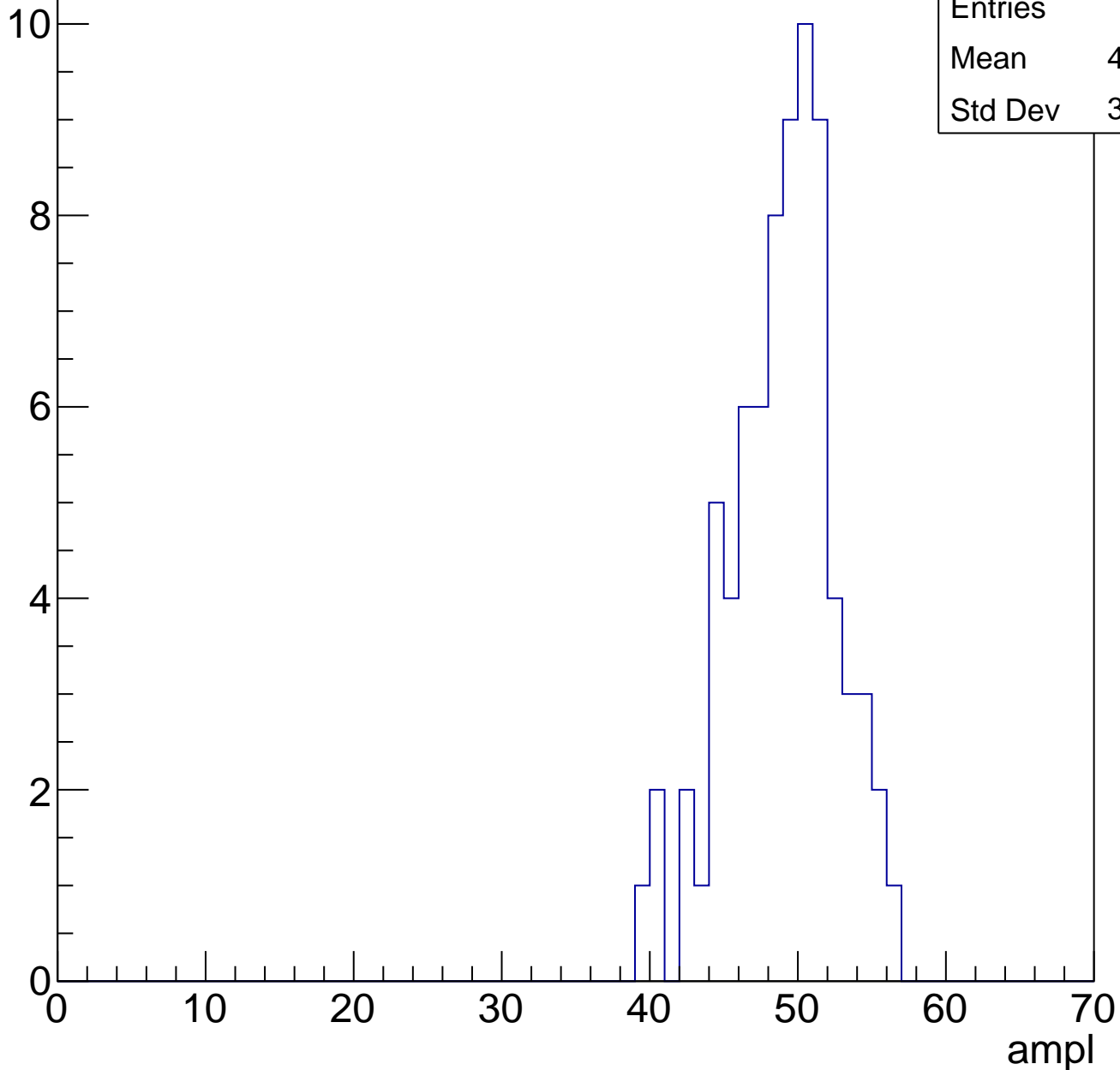


# B0L000S, U19-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

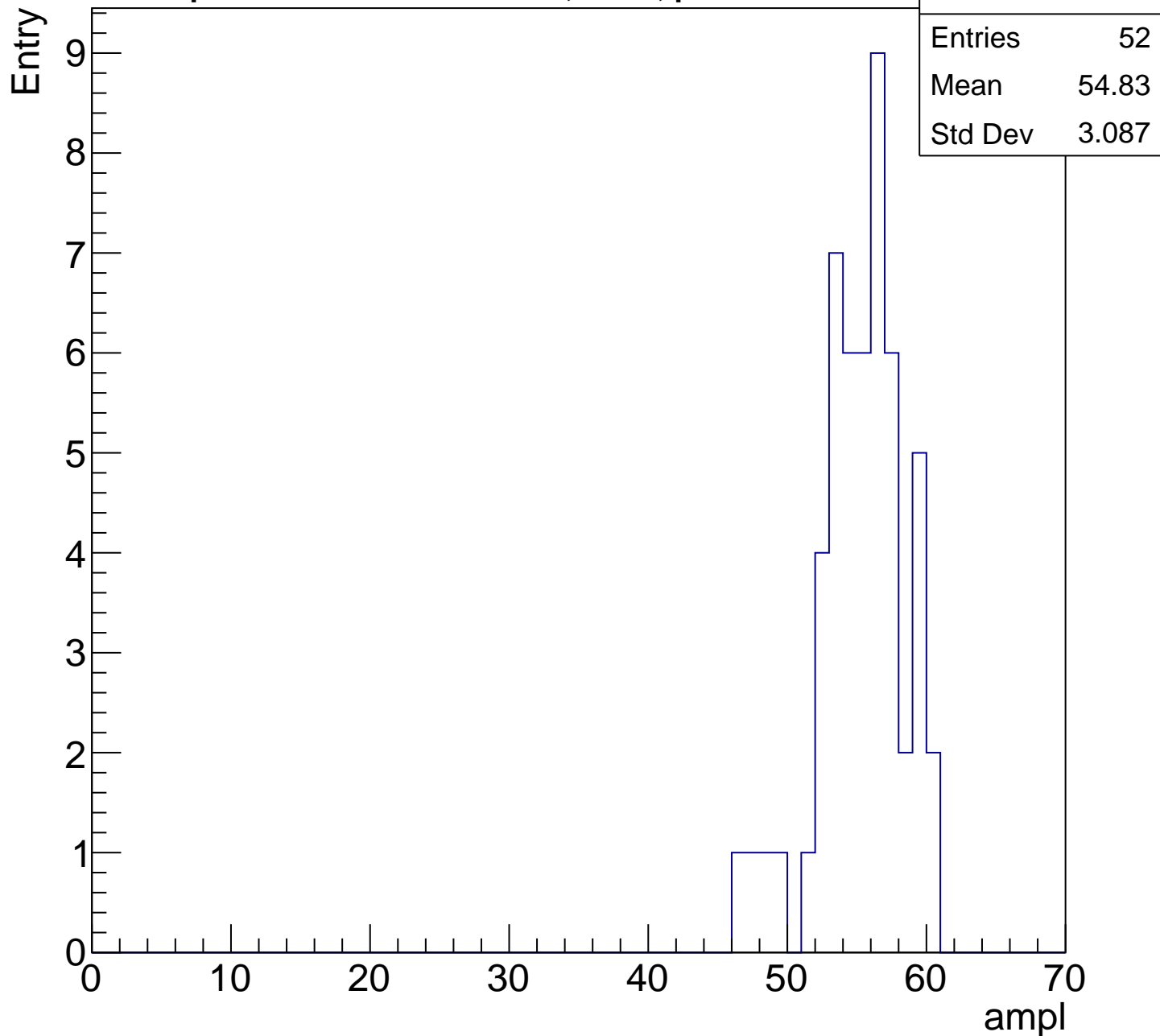
Entry

Entries	76
Mean	48.46
Std Dev	3.582



# B0L000S, U19-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

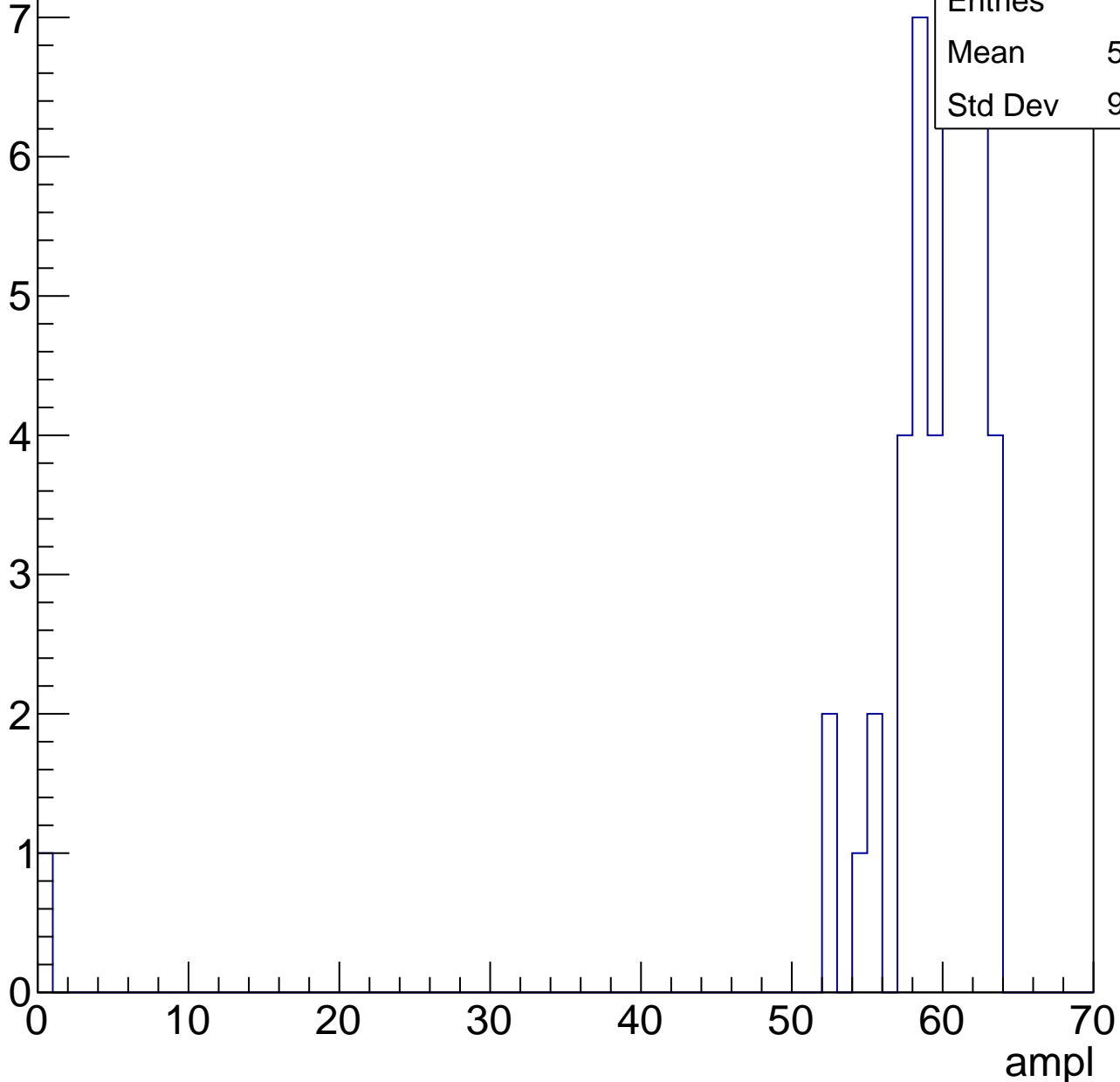


# B0L000S, U19-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

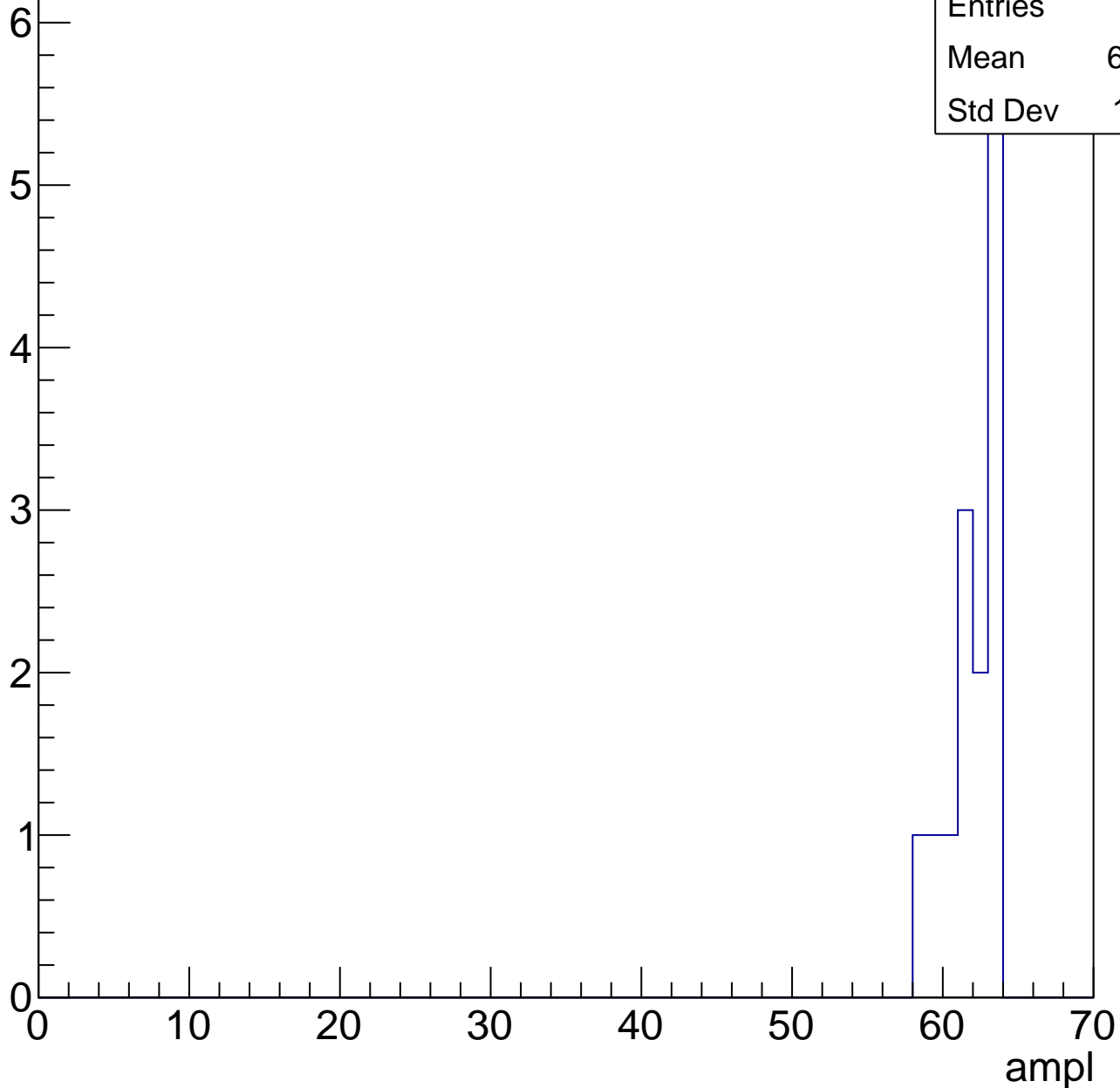
Entries	46
Mean	58.07
Std Dev	9.066



# B0L000S, U19-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

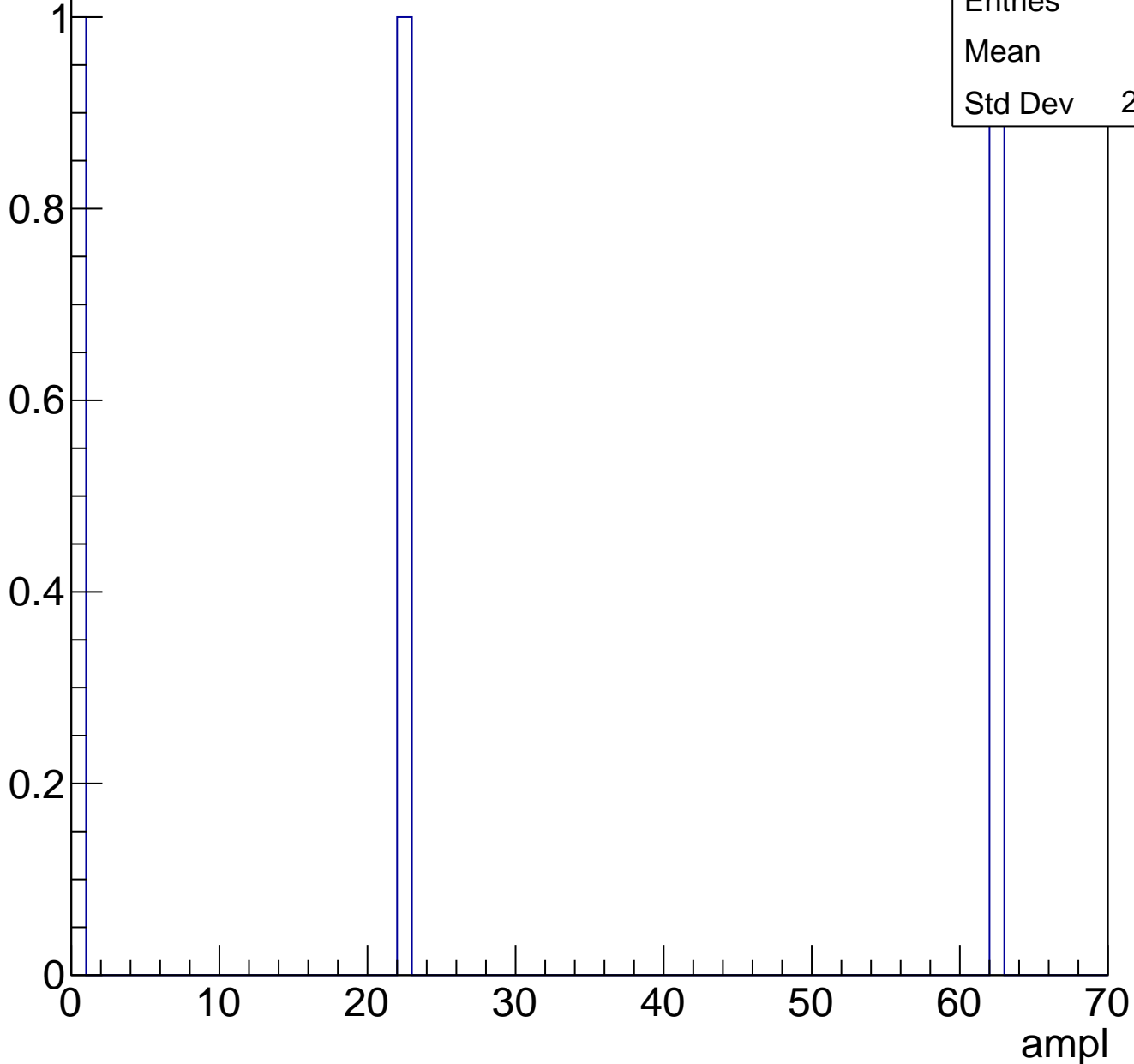




# B0L000S, U19-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28
Std Dev	25.66

# B0L000S, U19-ch37, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	27.38
Std Dev	4.519

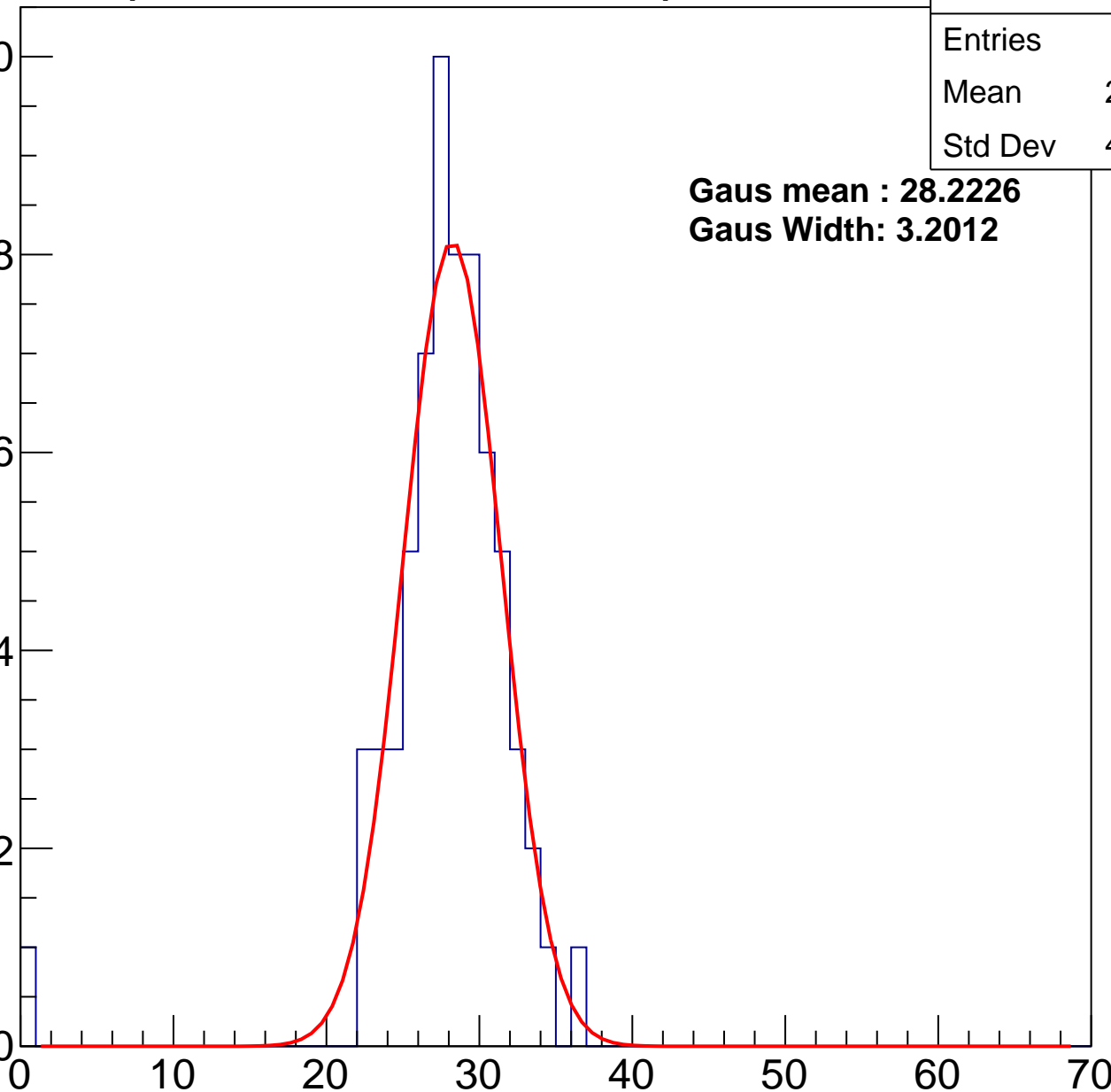
**Gaus mean : 28.2226**

**Gaus Width: 3.2012**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	34.83
Std Dev	3.236

**Gaus mean : 35.0473**

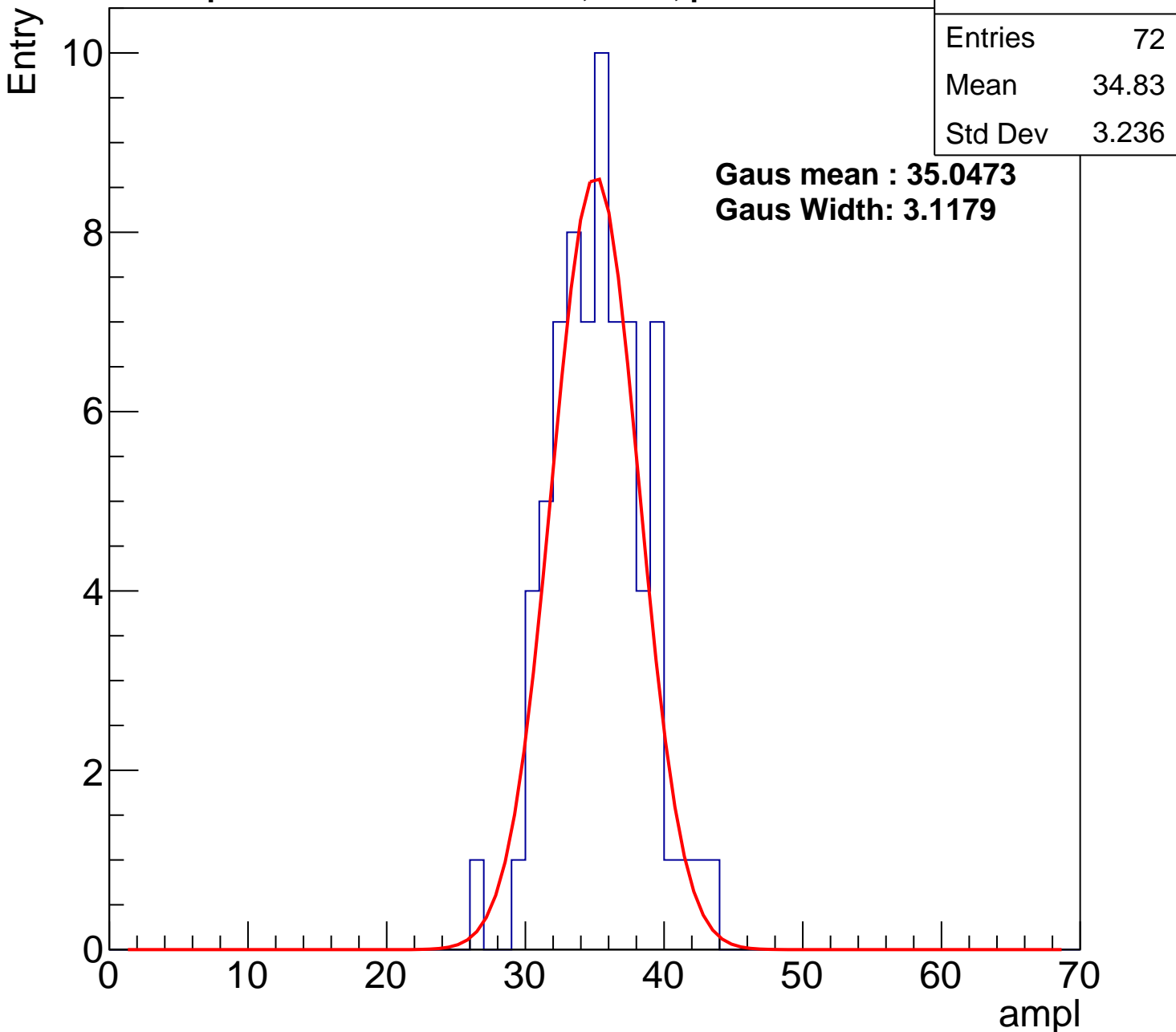
**Gaus Width: 3.1179**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch37, adc2

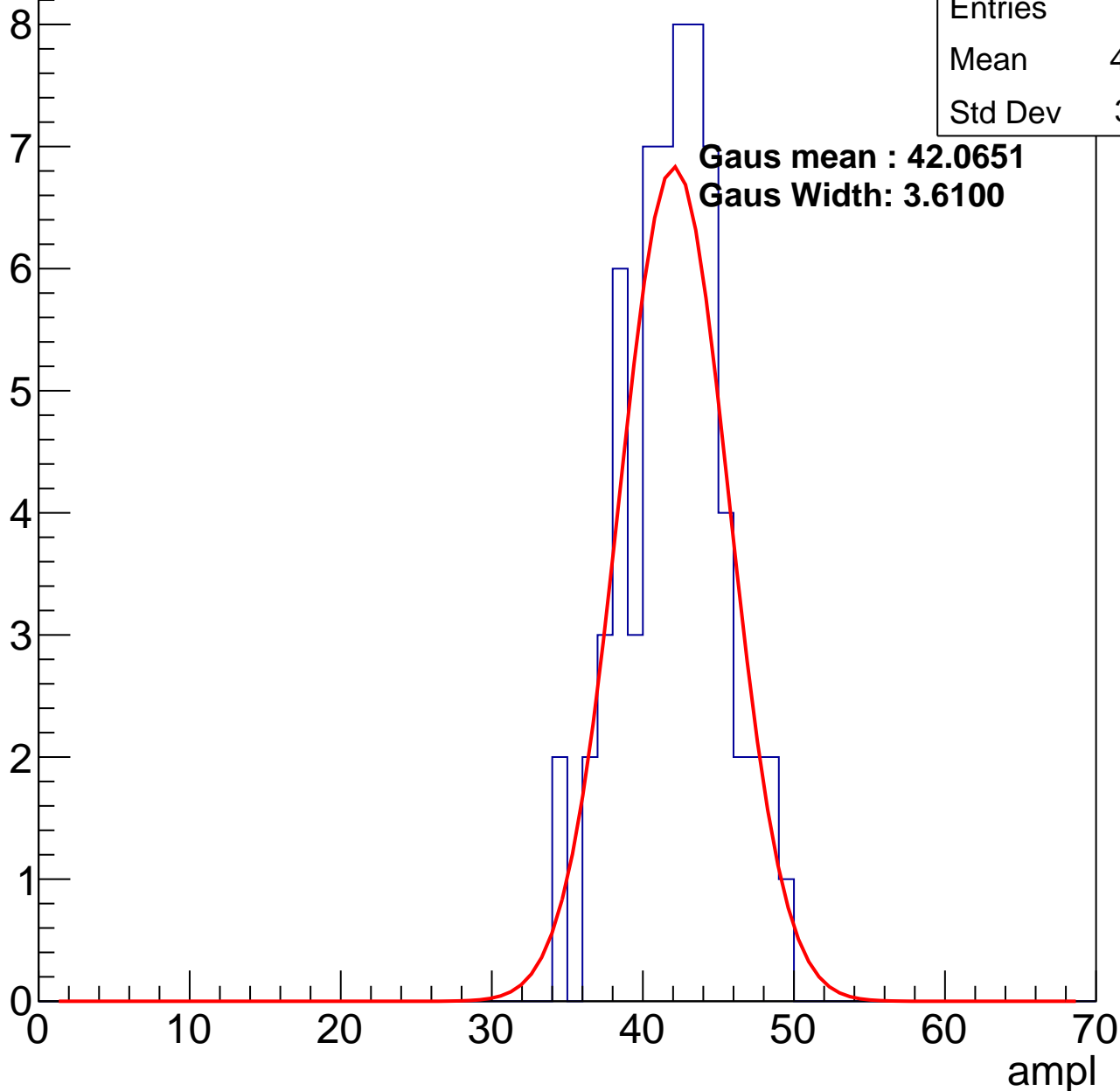
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.59
Std Dev	3.301

**Gaus mean : 42.0651**

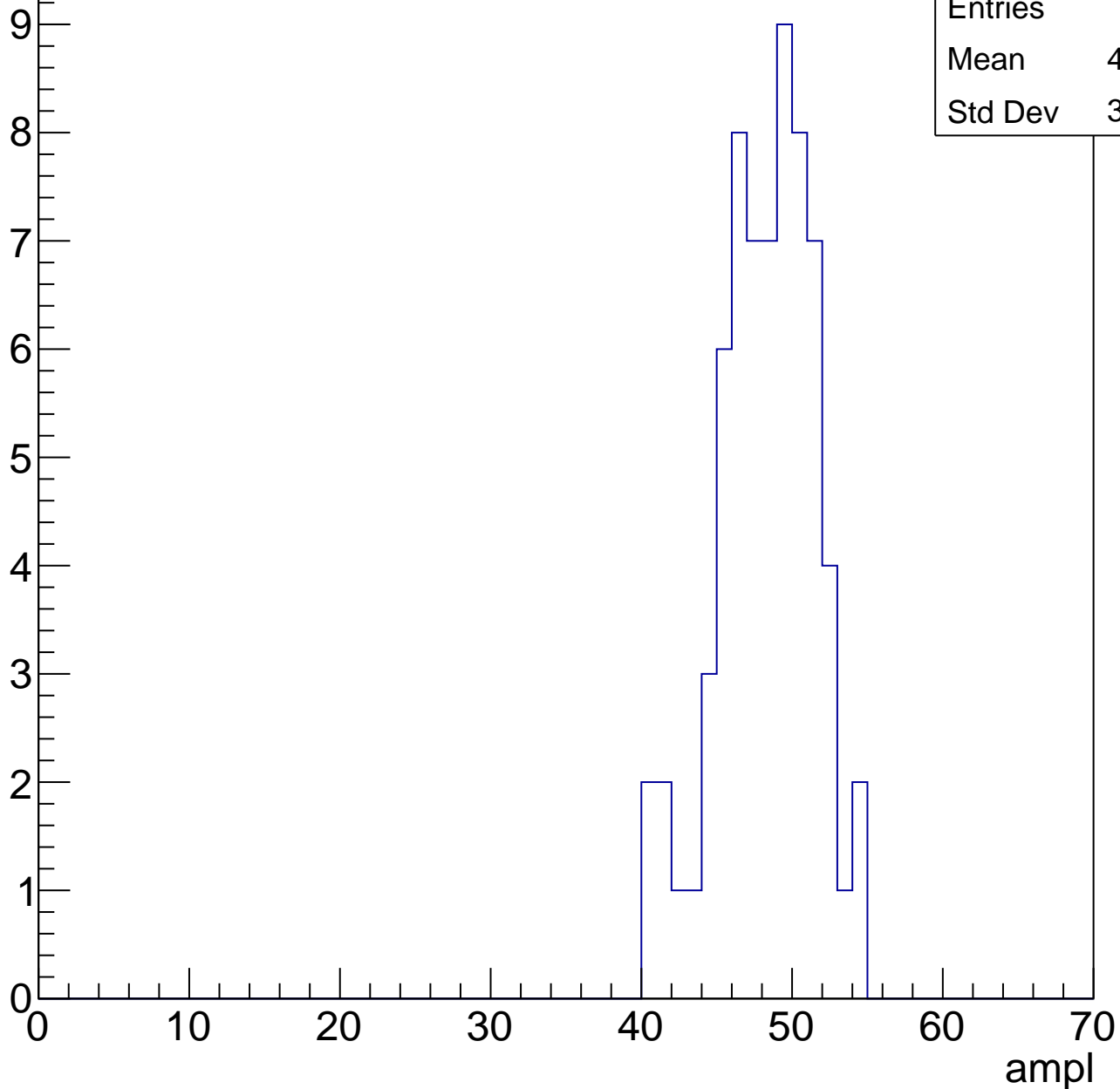
**Gaus Width: 3.6100**



# B0L000S, U19-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	55.05
Std Dev	3.092

Entry

10

8

6

4

2

0

0

10

20

30

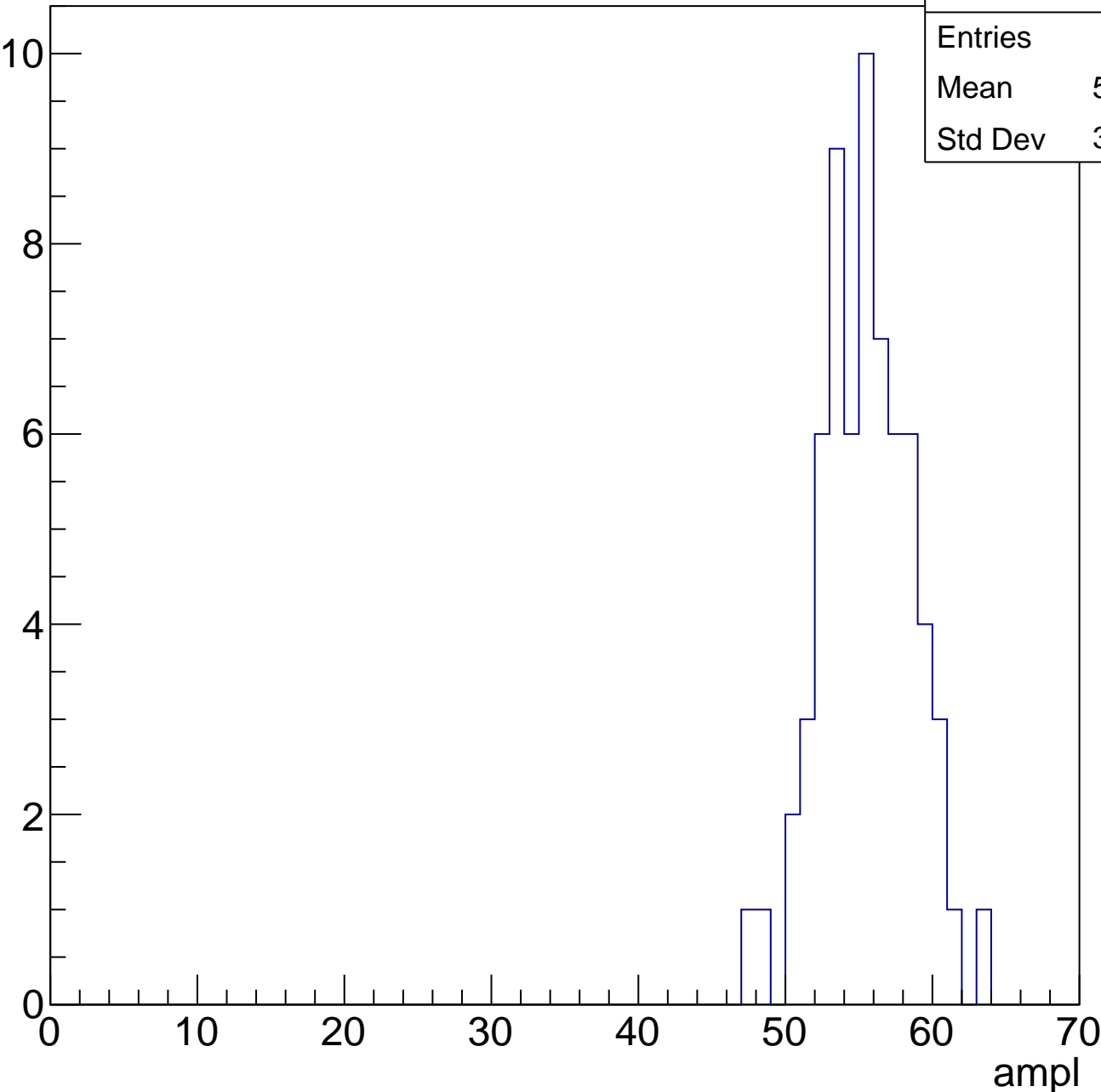
40

50

60

70

ampl

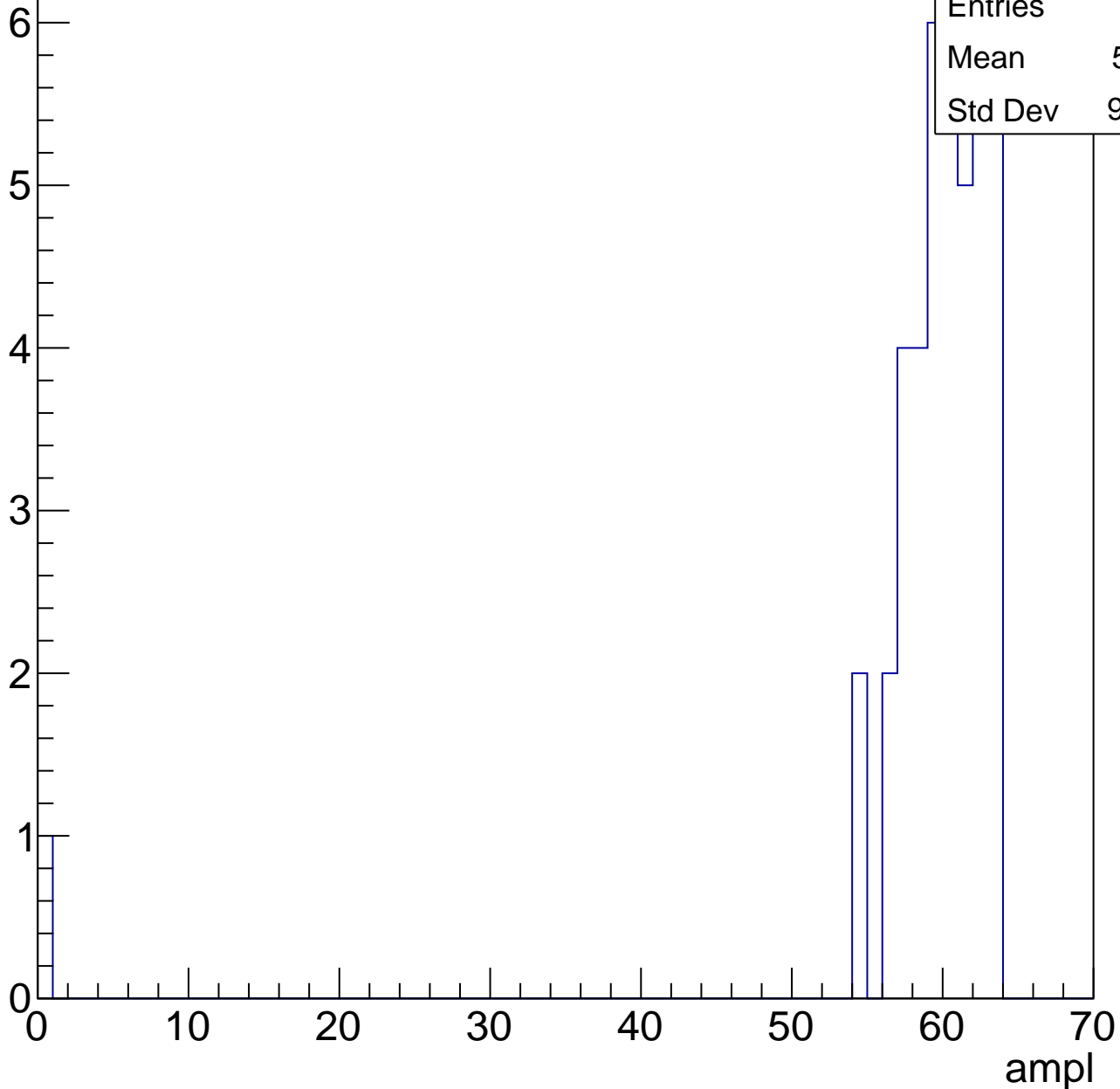


# B0L000S, U19-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

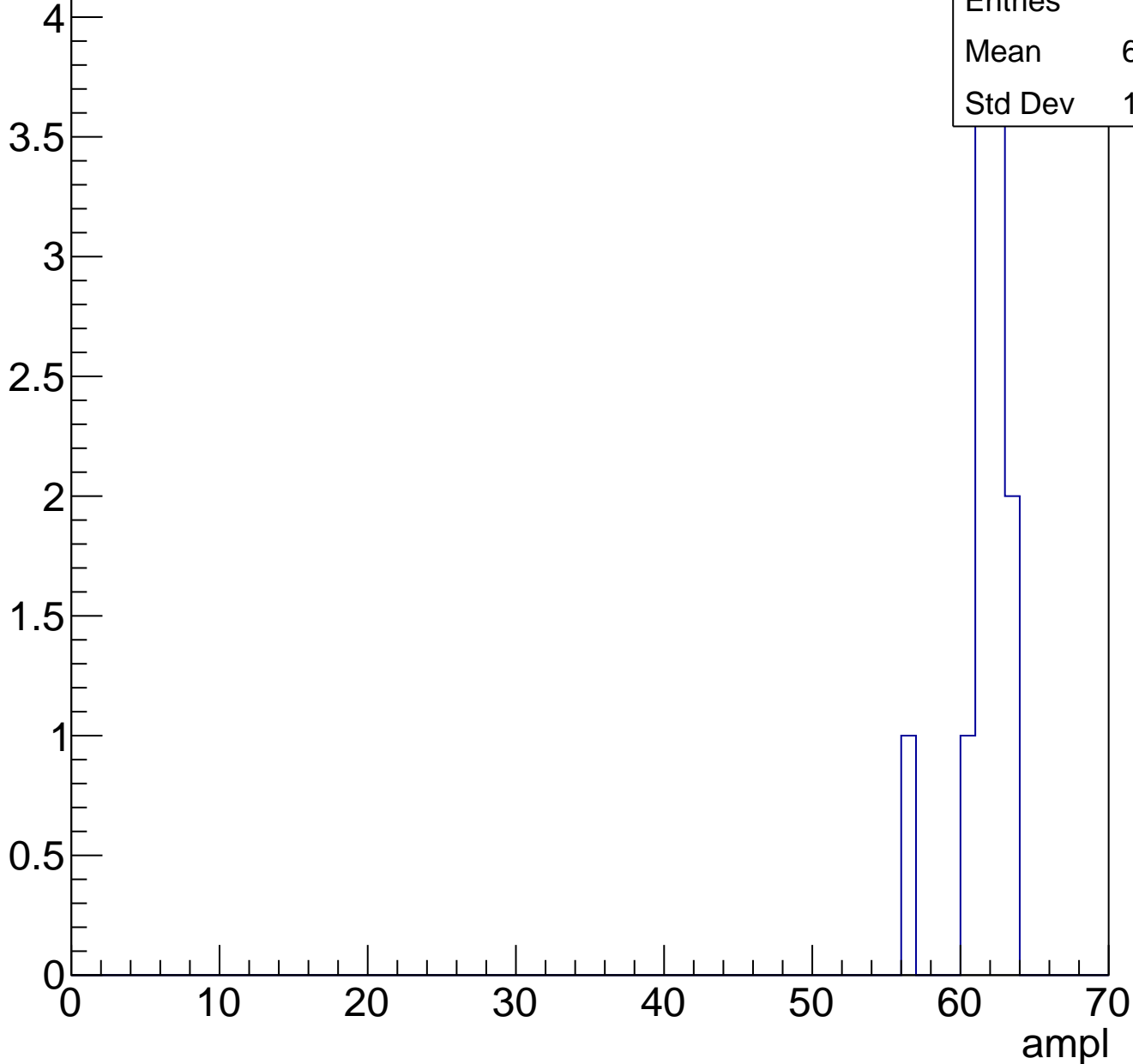
Entries	42
Mean	58.31
Std Dev	9.418



# B0L000S, U19-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch38, adc0

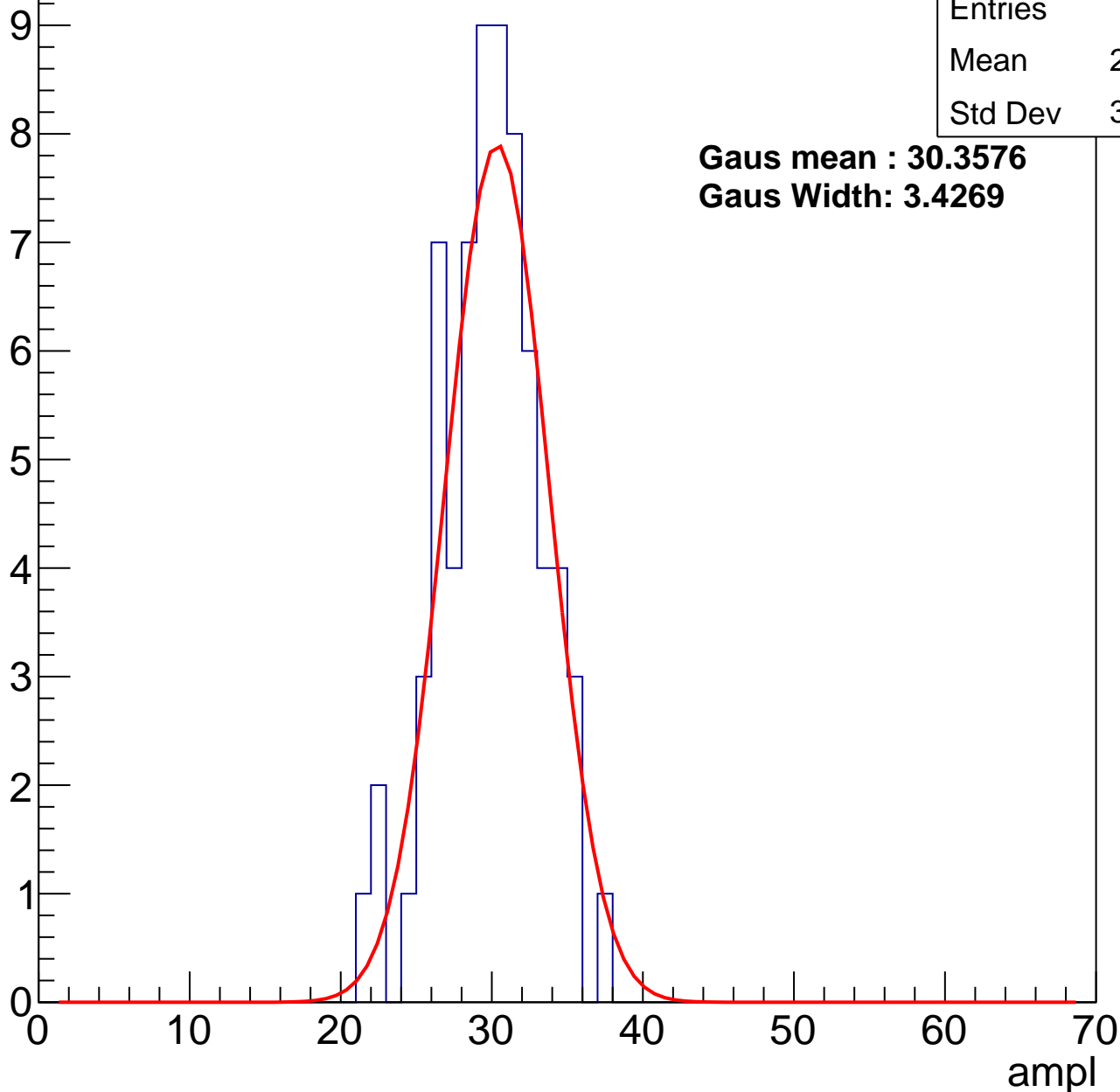
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.43
Std Dev	3.273

**Gaus mean : 30.3576**

**Gaus Width: 3.4269**



# B0L000S, U19-ch38, adc1

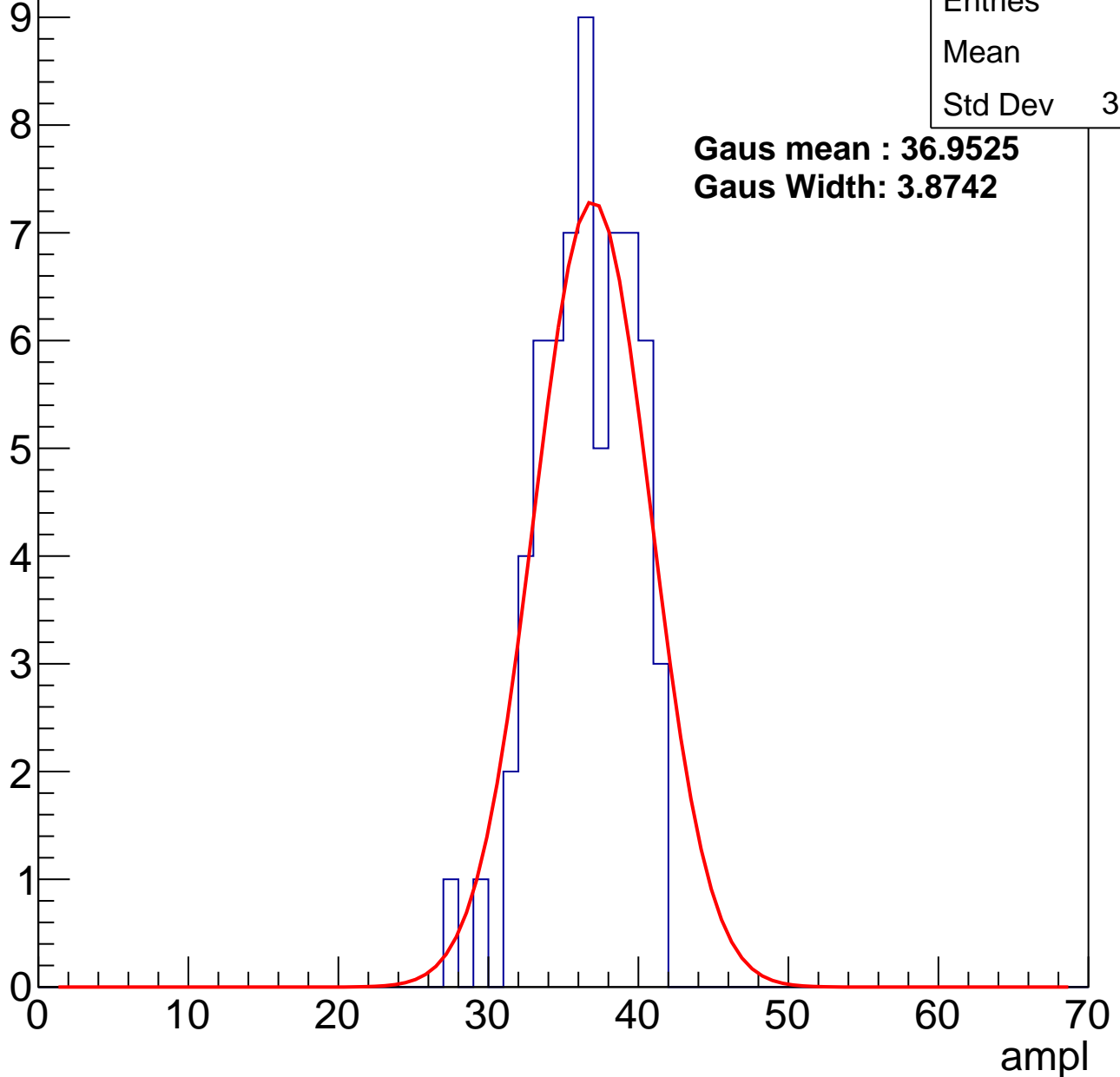
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36
Std Dev	3.052

**Gaus mean : 36.9525**

**Gaus Width: 3.8742**



# B0L000S, U19-ch38, adc2

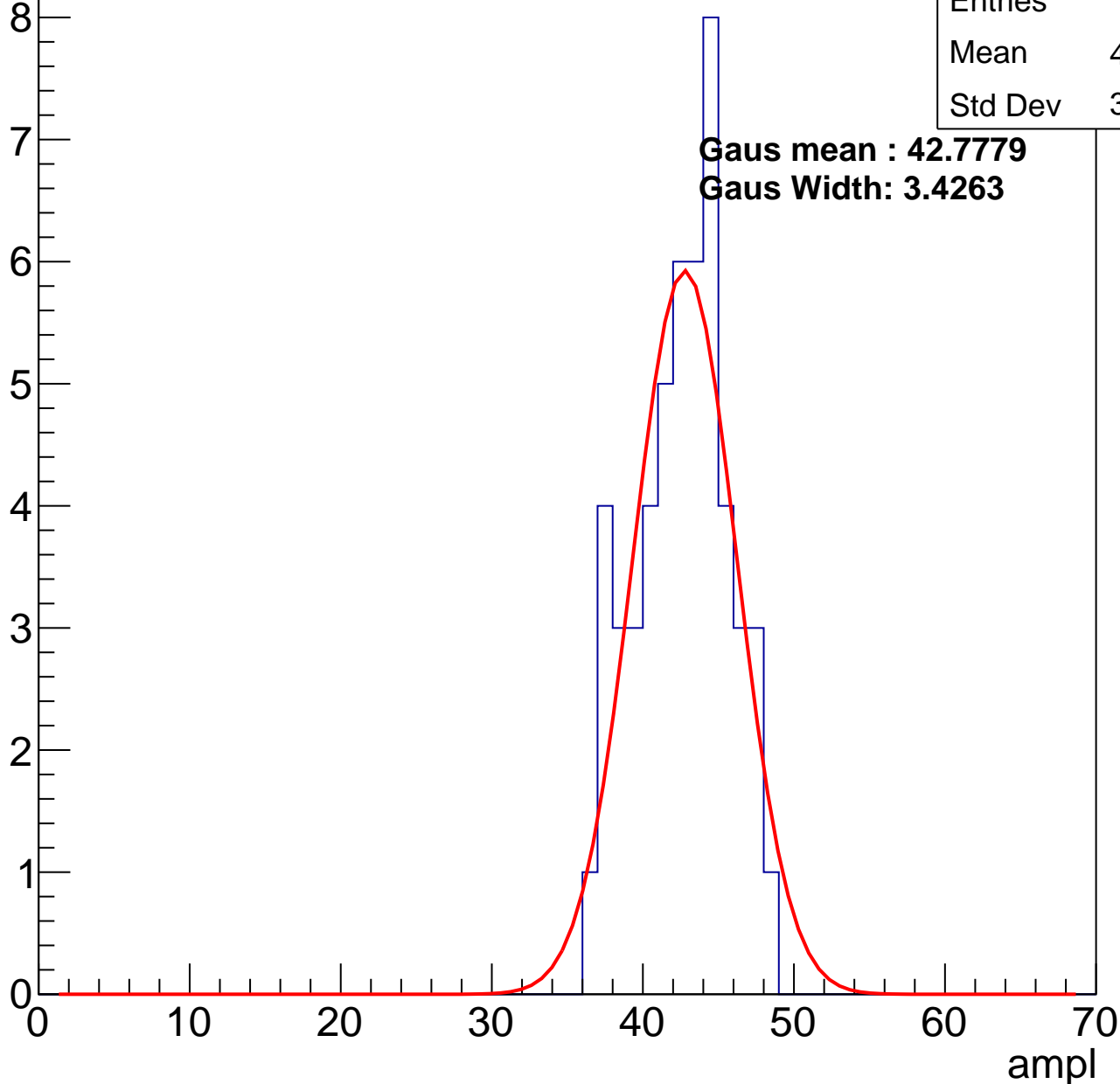
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	42.14
Std Dev	3.016

**Gaus mean : 42.7779**

**Gaus Width: 3.4263**

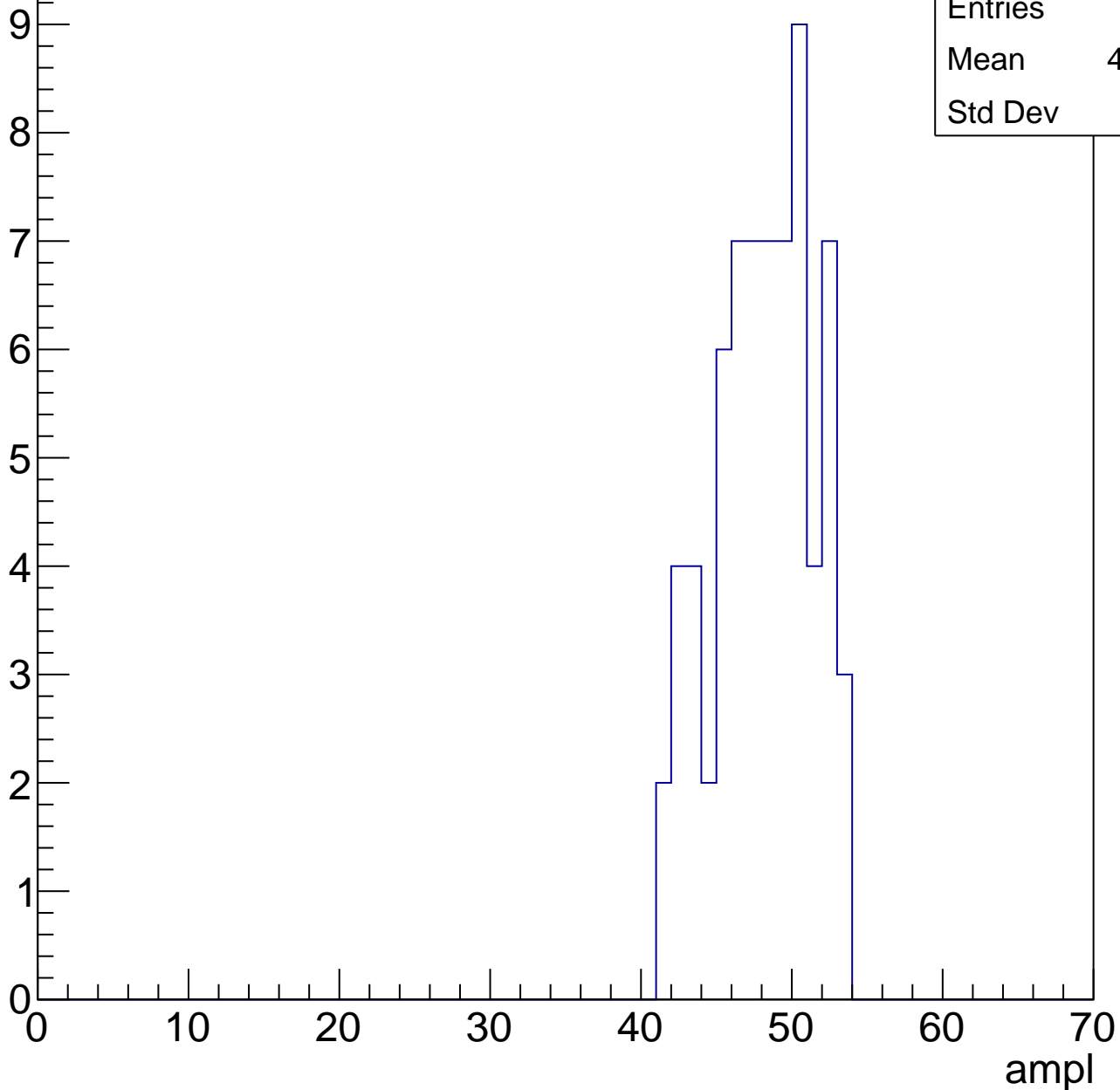


# B0L000S, U19-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

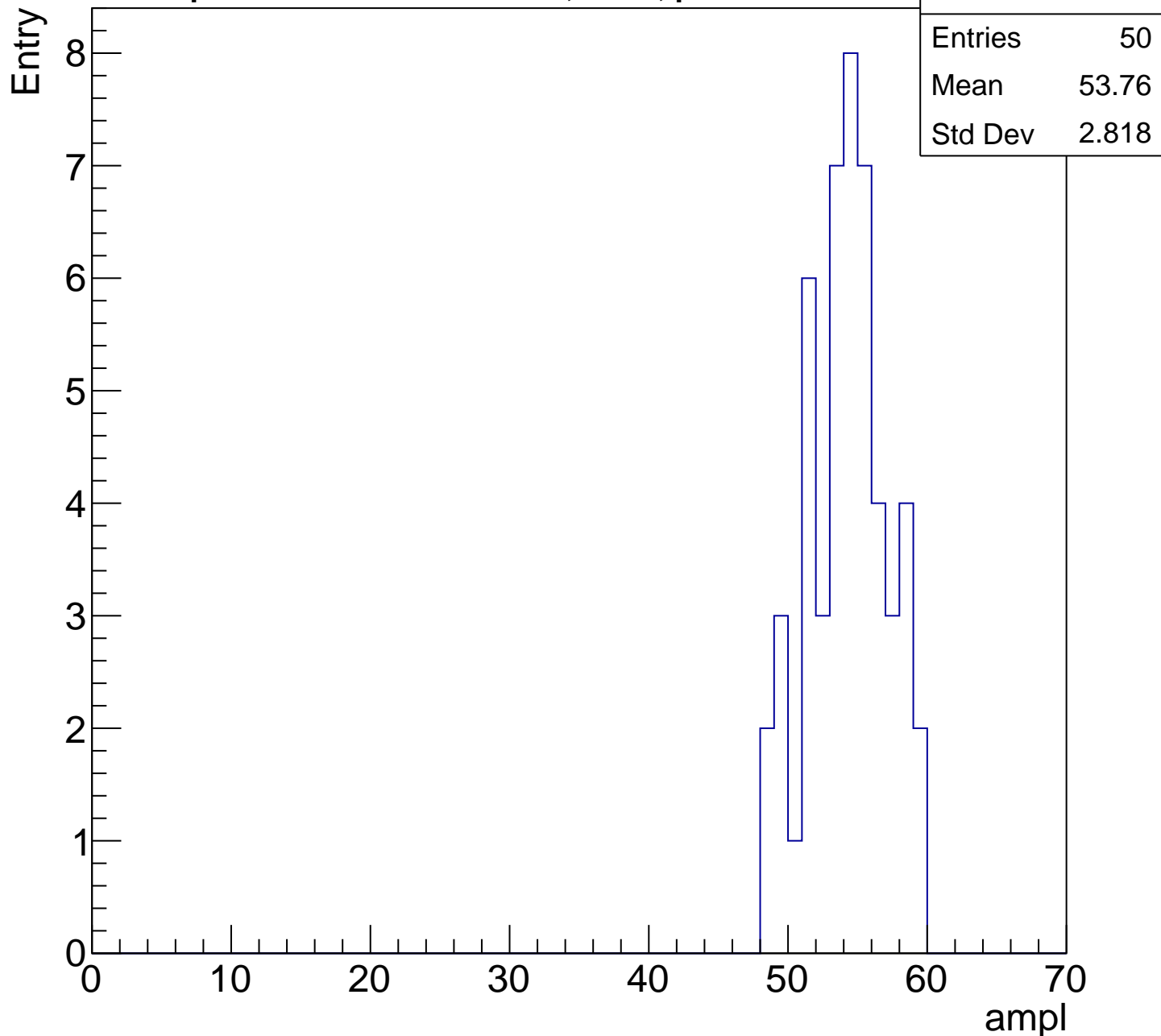
Entry

Entries	69
Mean	47.64
Std Dev	3.23



# B0L000S, U19-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

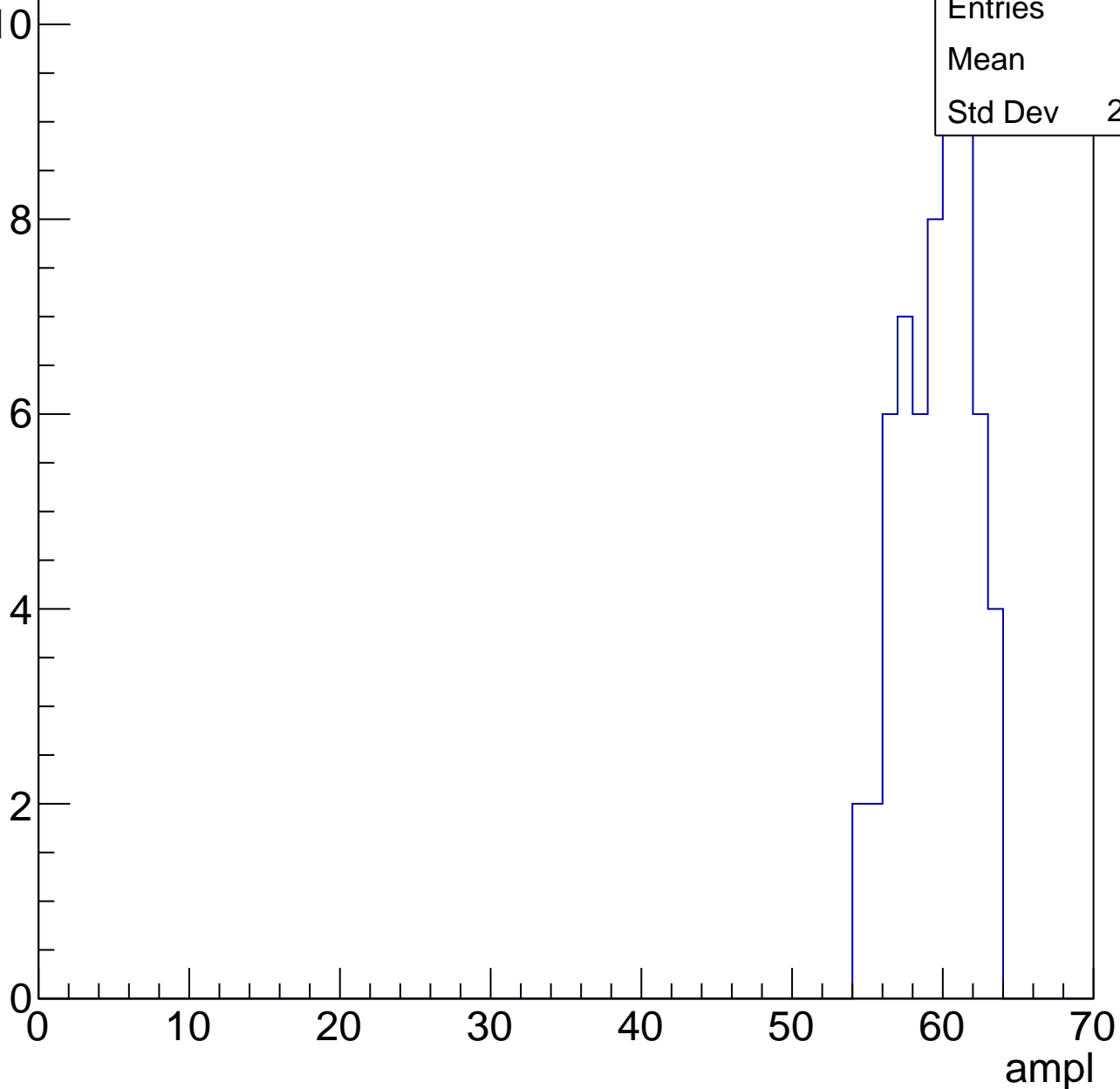


# B0L000S, U19-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	59.1
Std Dev	2.357

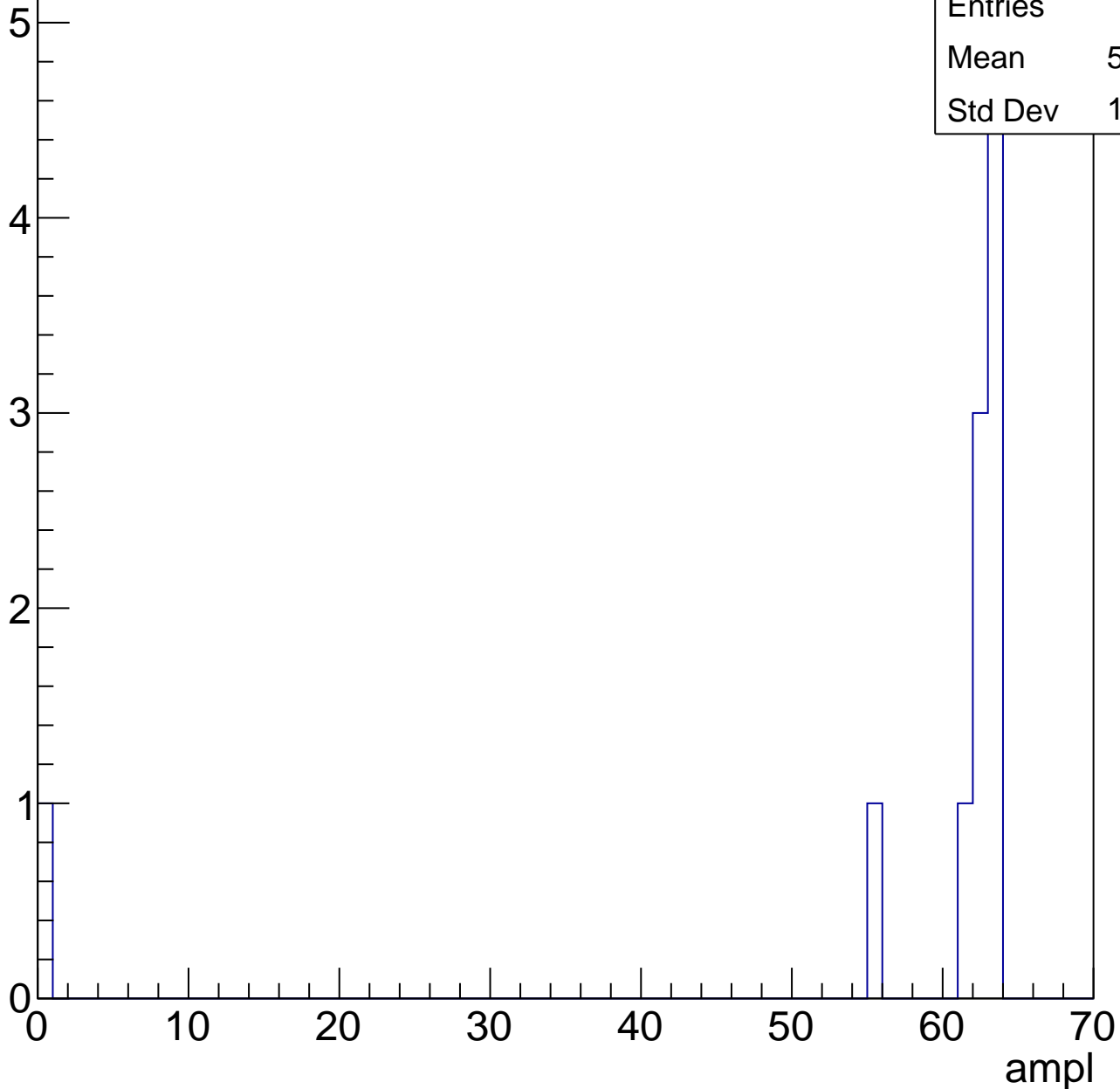


# B0L000S, U19-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	56.09
Std Dev	17.88

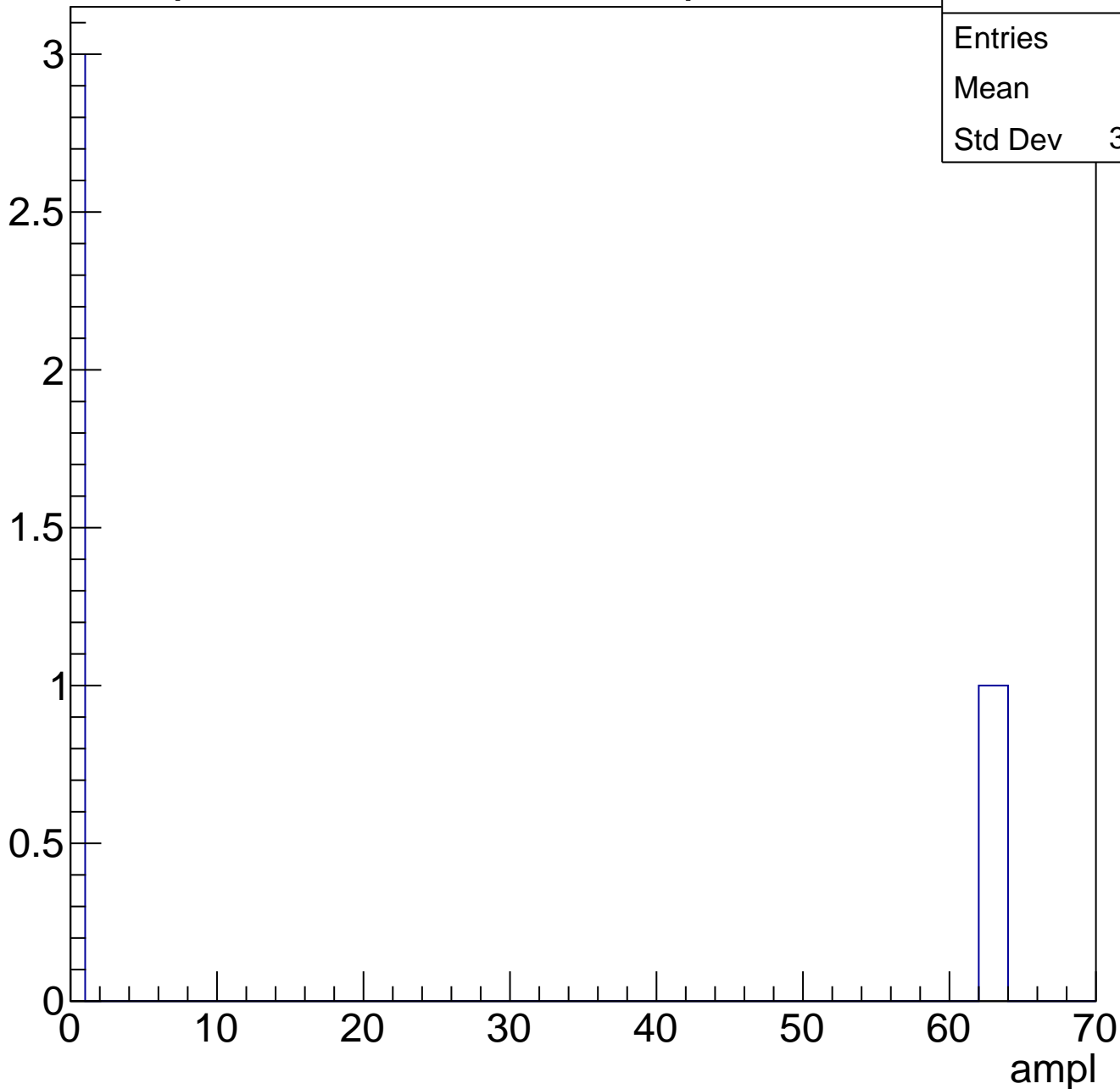




# B0L000S, U19-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	30.19
Std Dev	3.344

**Gaus mean : 30.2092**

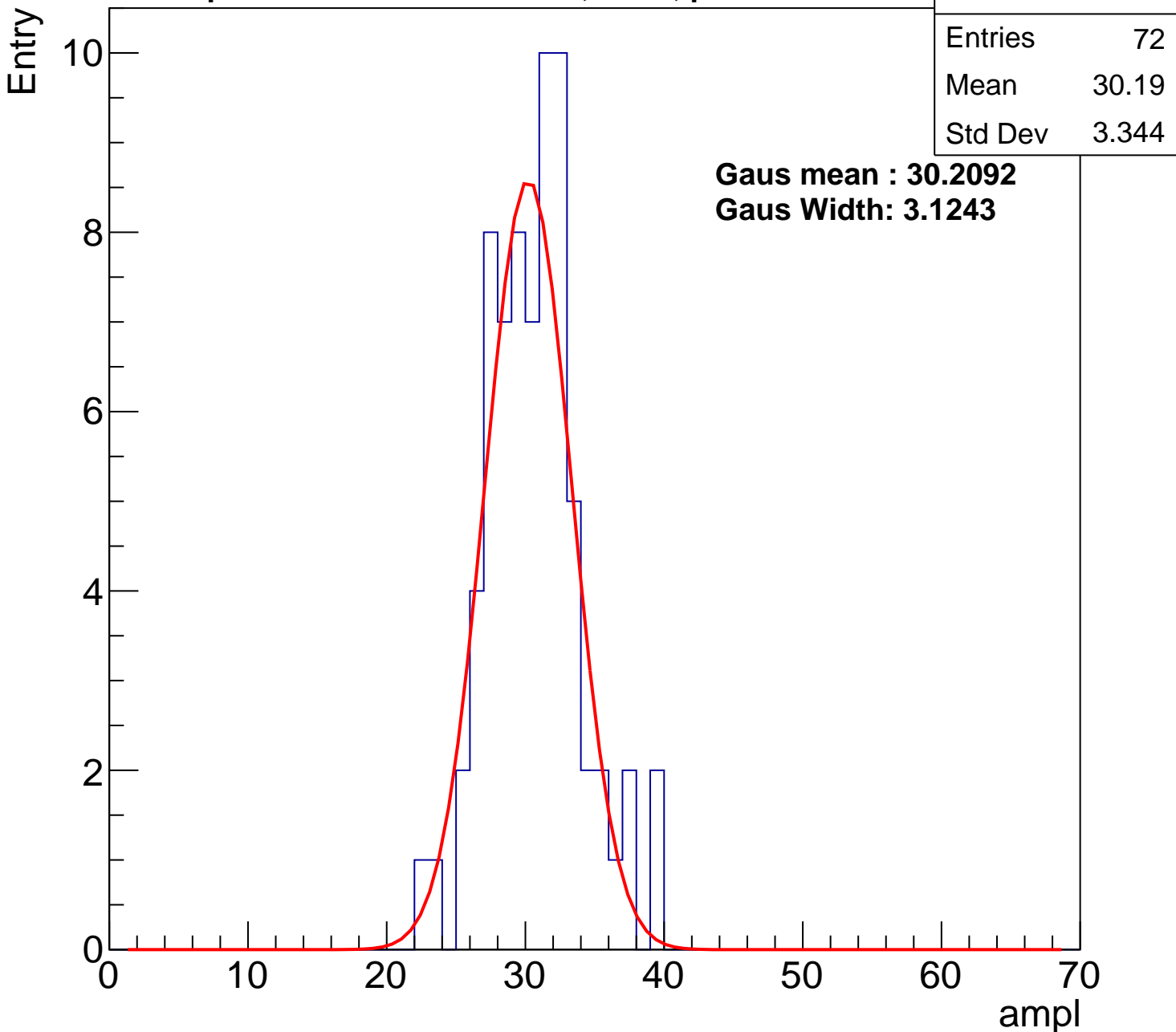
**Gaus Width: 3.1243**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch39, adc1

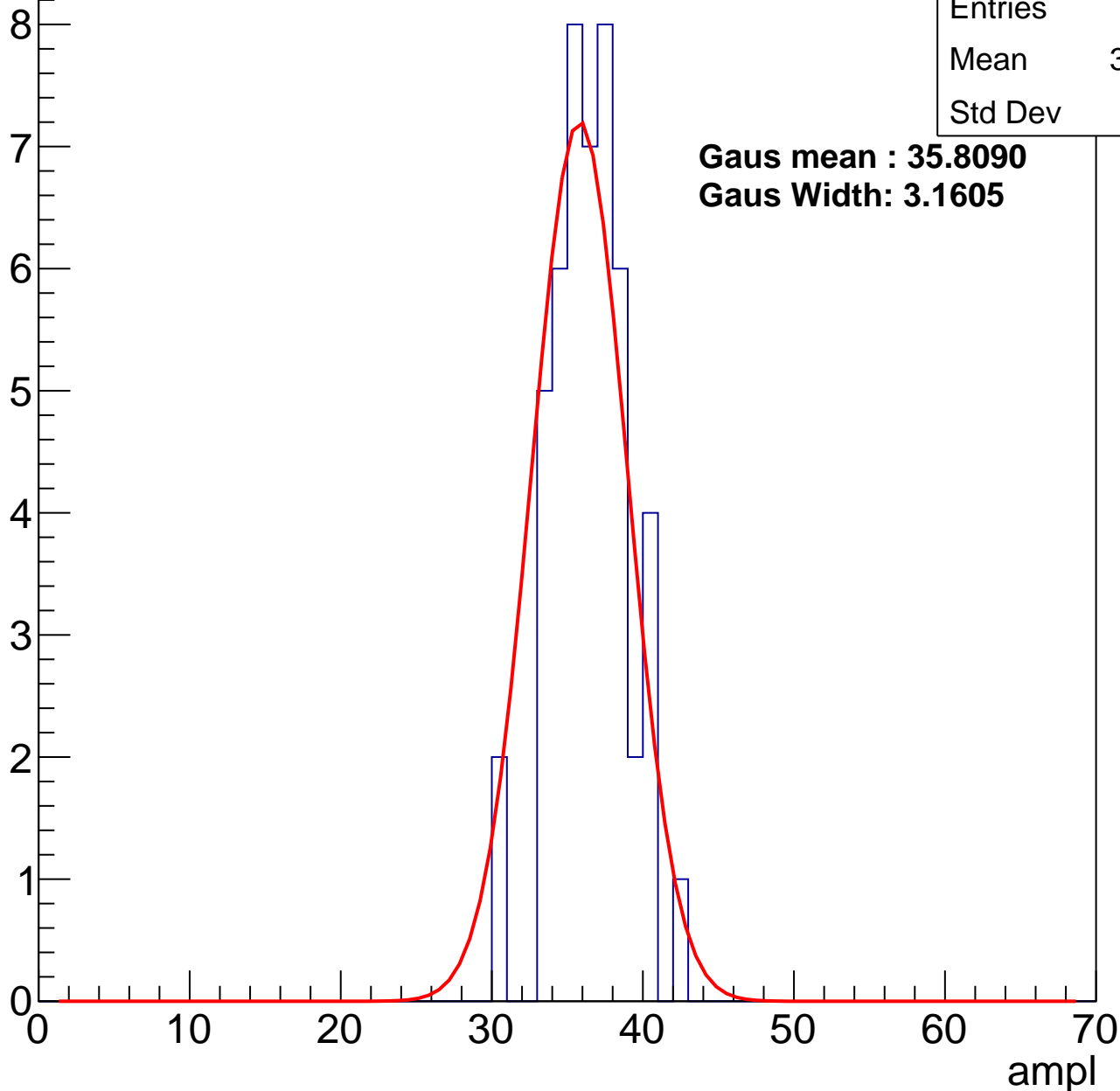
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.02
Std Dev	2.47

**Gaus mean : 35.8090**

**Gaus Width: 3.1605**



# B0L000S, U19-ch39, adc2

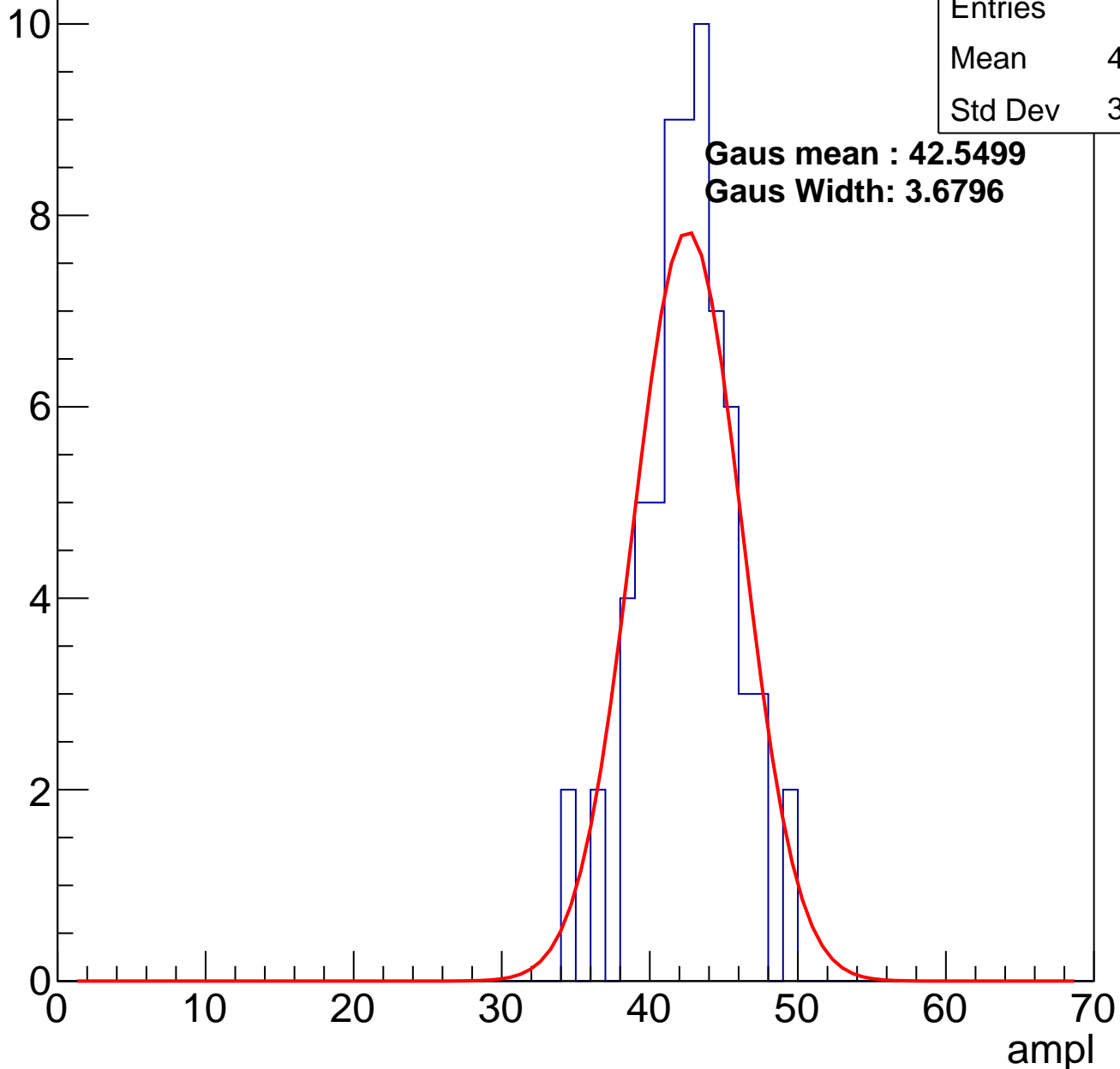
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	42.07
Std Dev	3.116

**Gaus mean : 42.5499**

**Gaus Width: 3.6796**

Entry

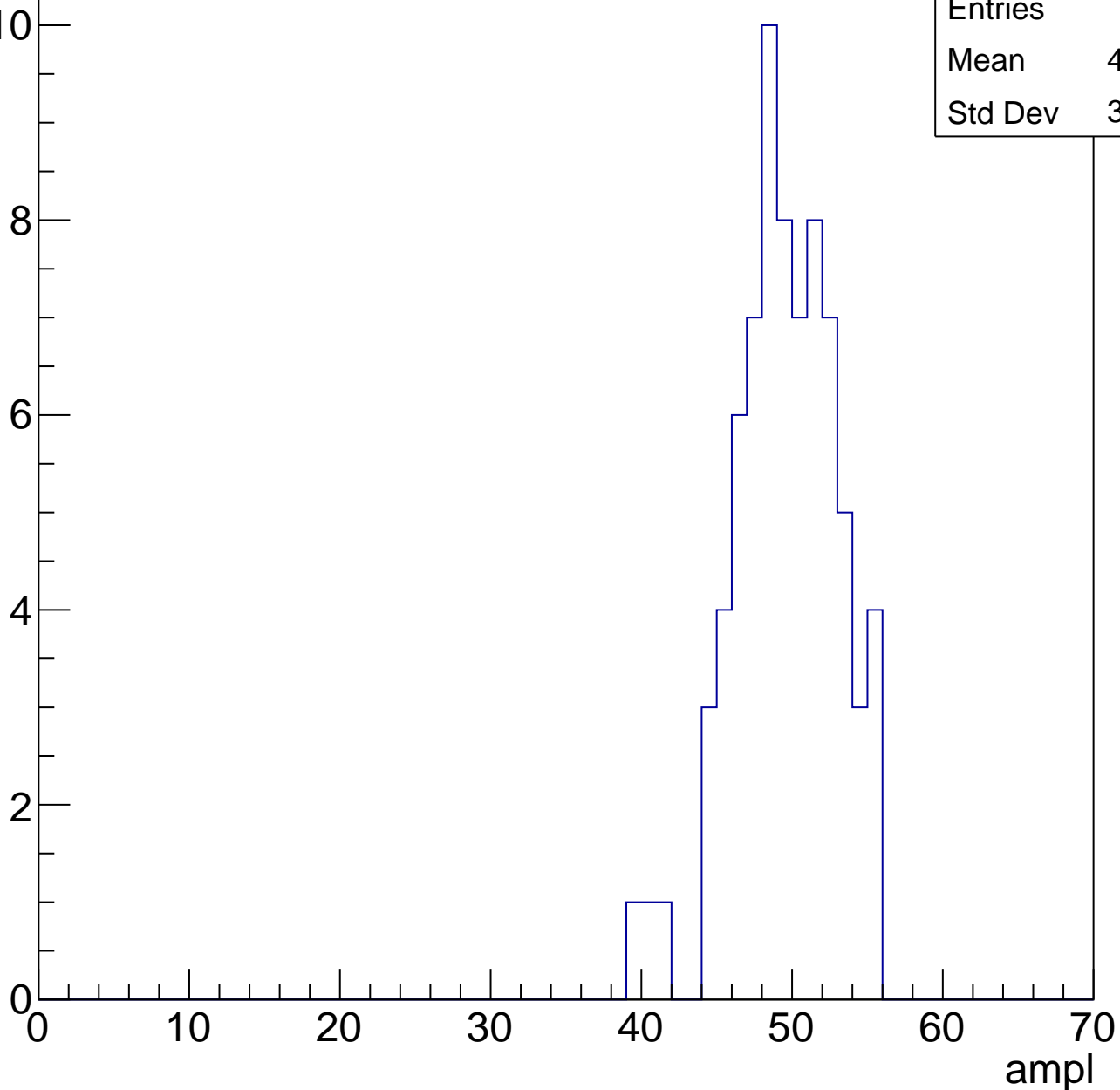


# B0L000S, U19-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	49.04
Std Dev	3.419

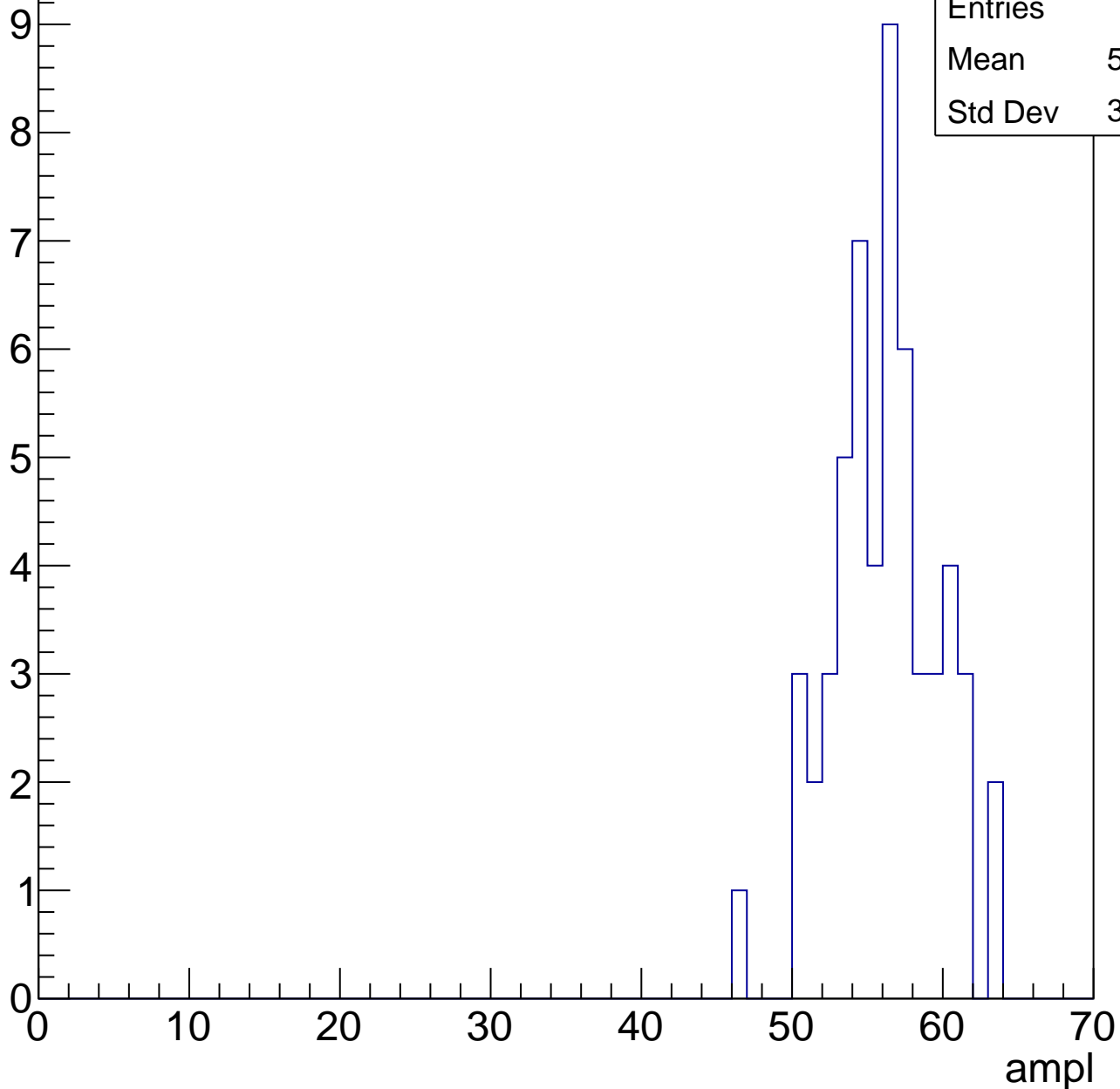


# B0L000S, U19-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

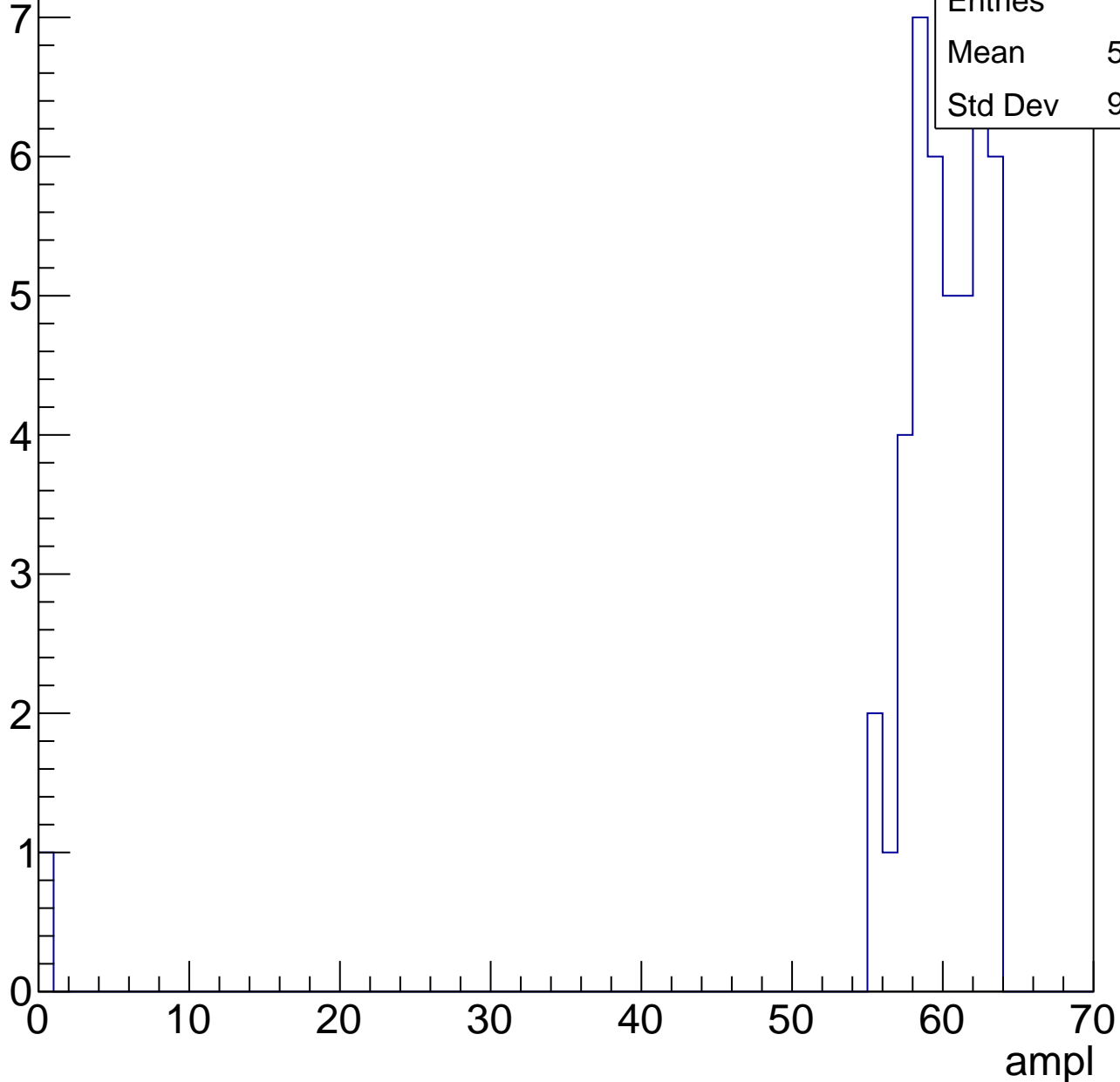
Entries	55
Mean	55.69
Std Dev	3.463



# B0L000S, U19-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

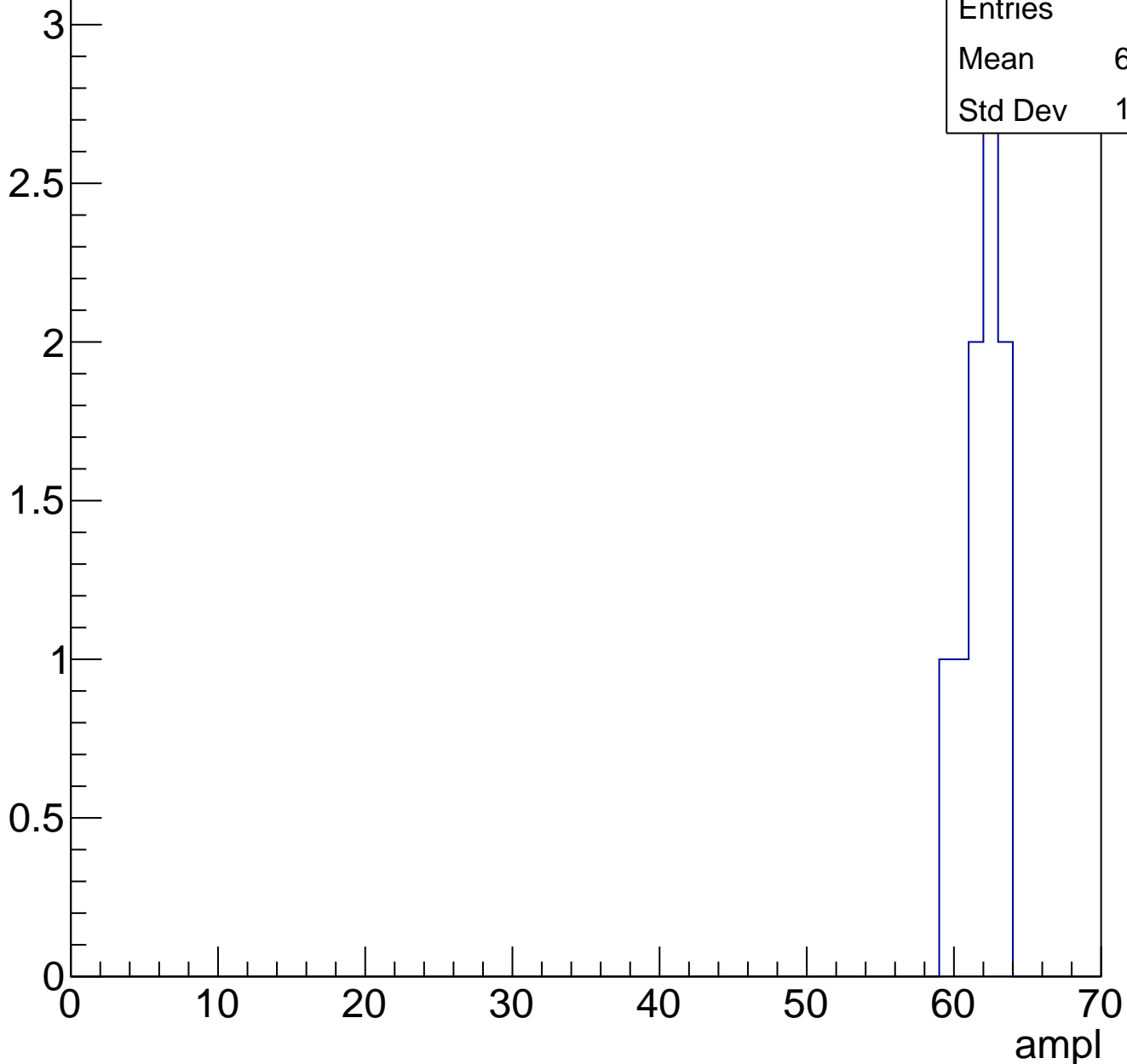
Entry



# B0L000S, U19-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch40, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	49
Mean	28.63
Std Dev	2.919

**Gaus mean : 29.3366**

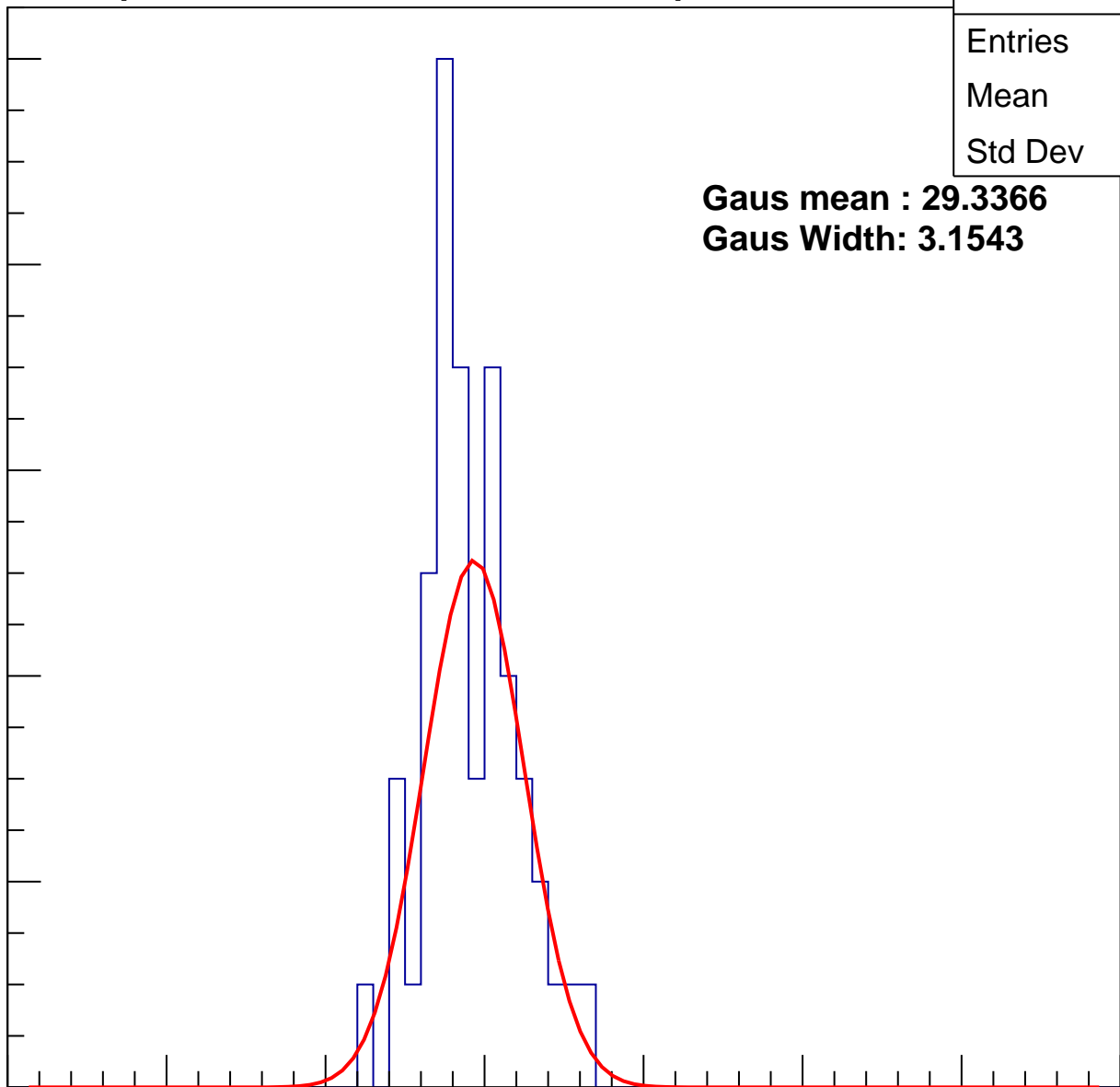
**Gaus Width: 3.1543**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



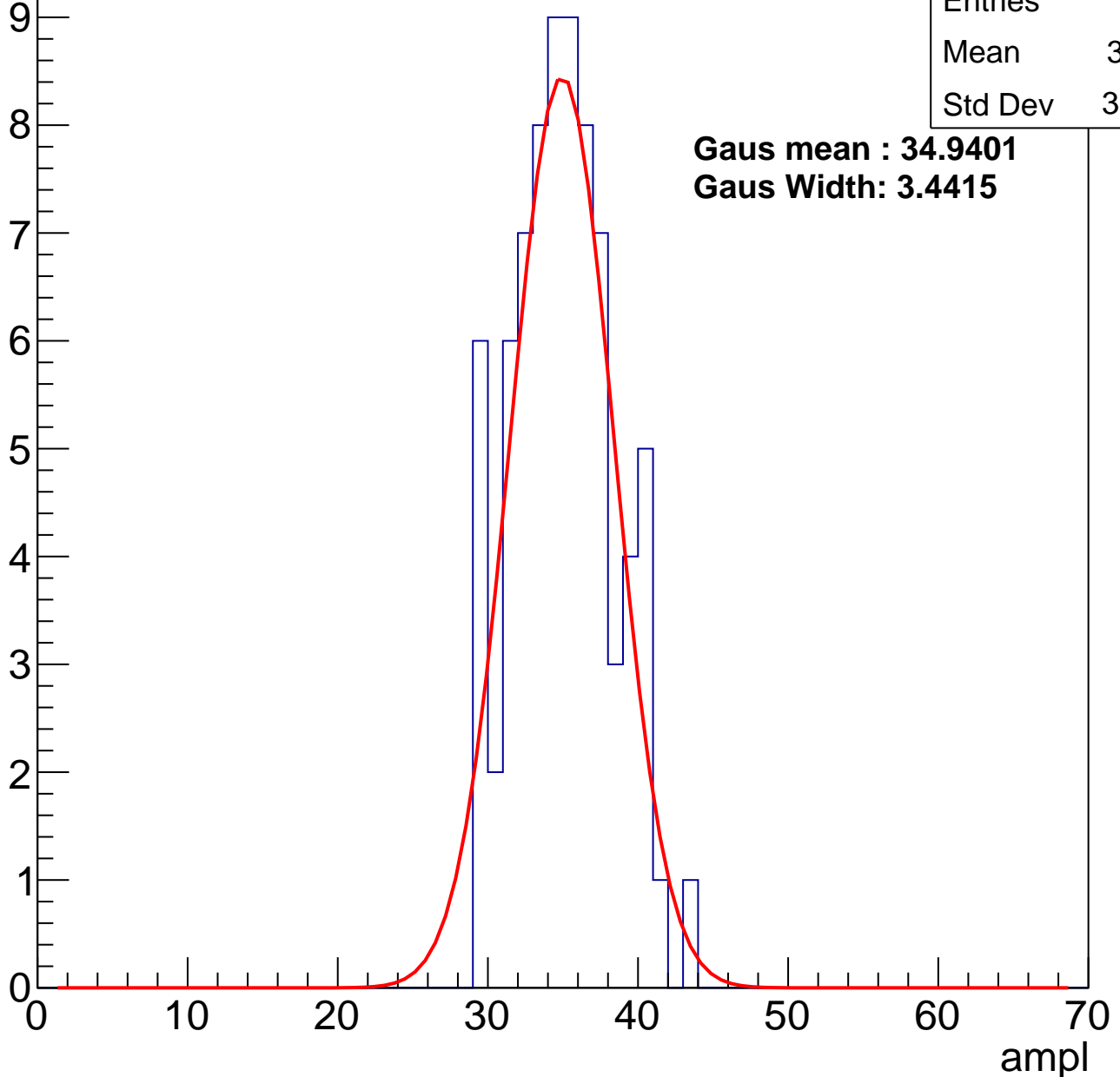
# B0L000S, U19-ch40, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	34.61
Std Dev	3.257

**Gaus mean : 34.9401**  
**Gaus Width: 3.4415**



# B0L000S, U19-ch40, adc2

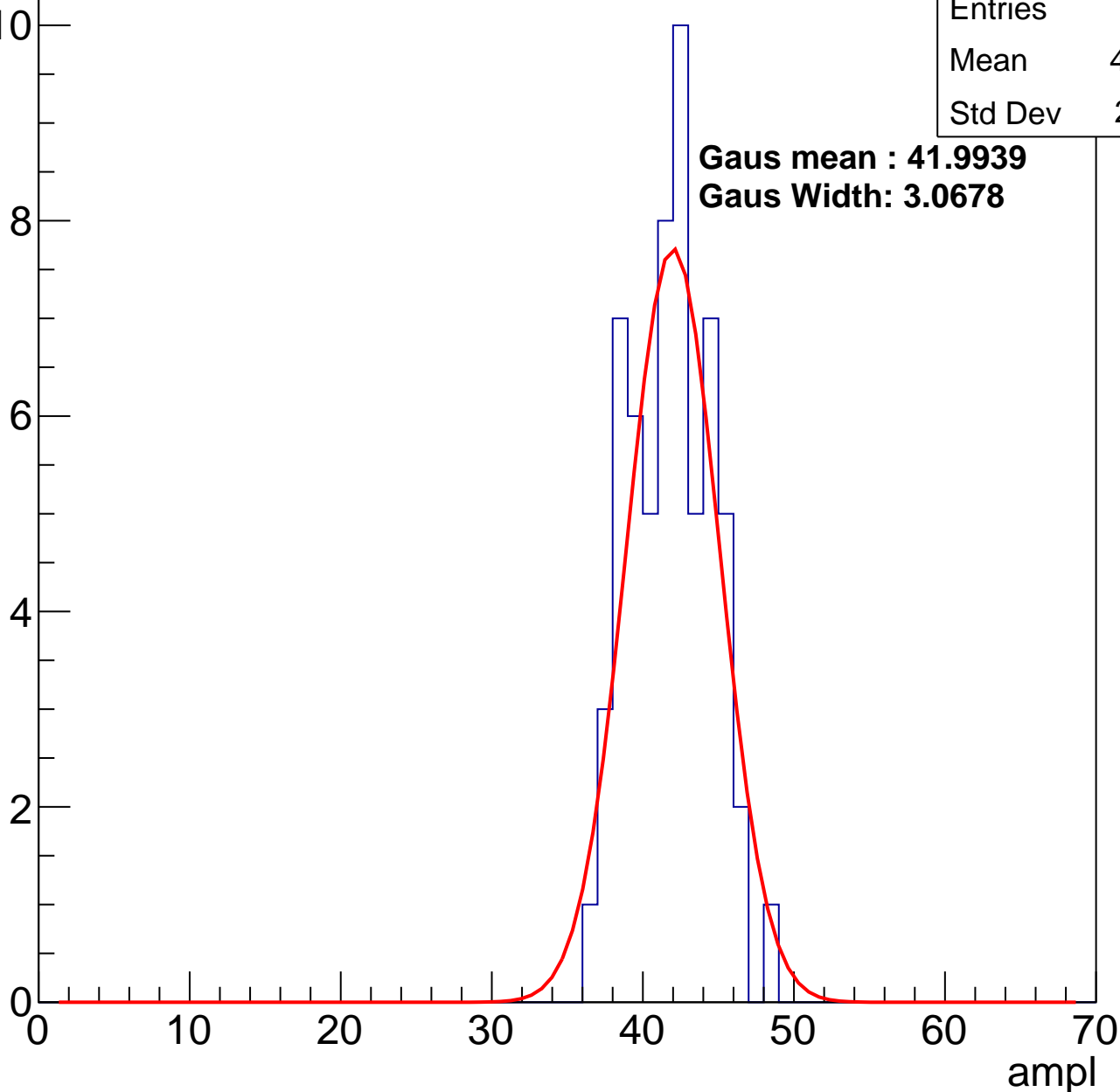
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.38
Std Dev	2.671

**Gaus mean : 41.9939**

**Gaus Width: 3.0678**

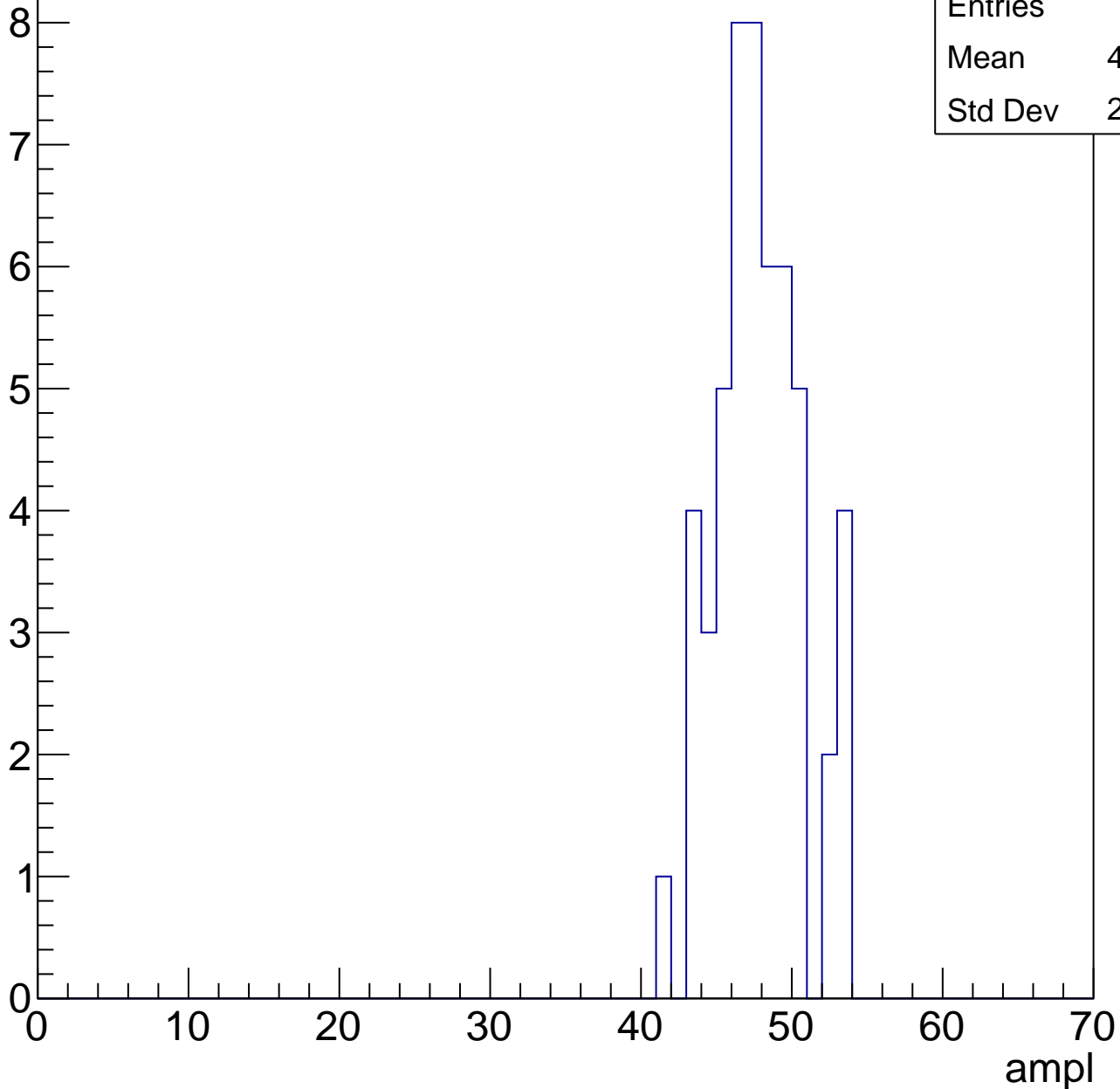


# B0L000S, U19-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	47.35
Std Dev	2.834

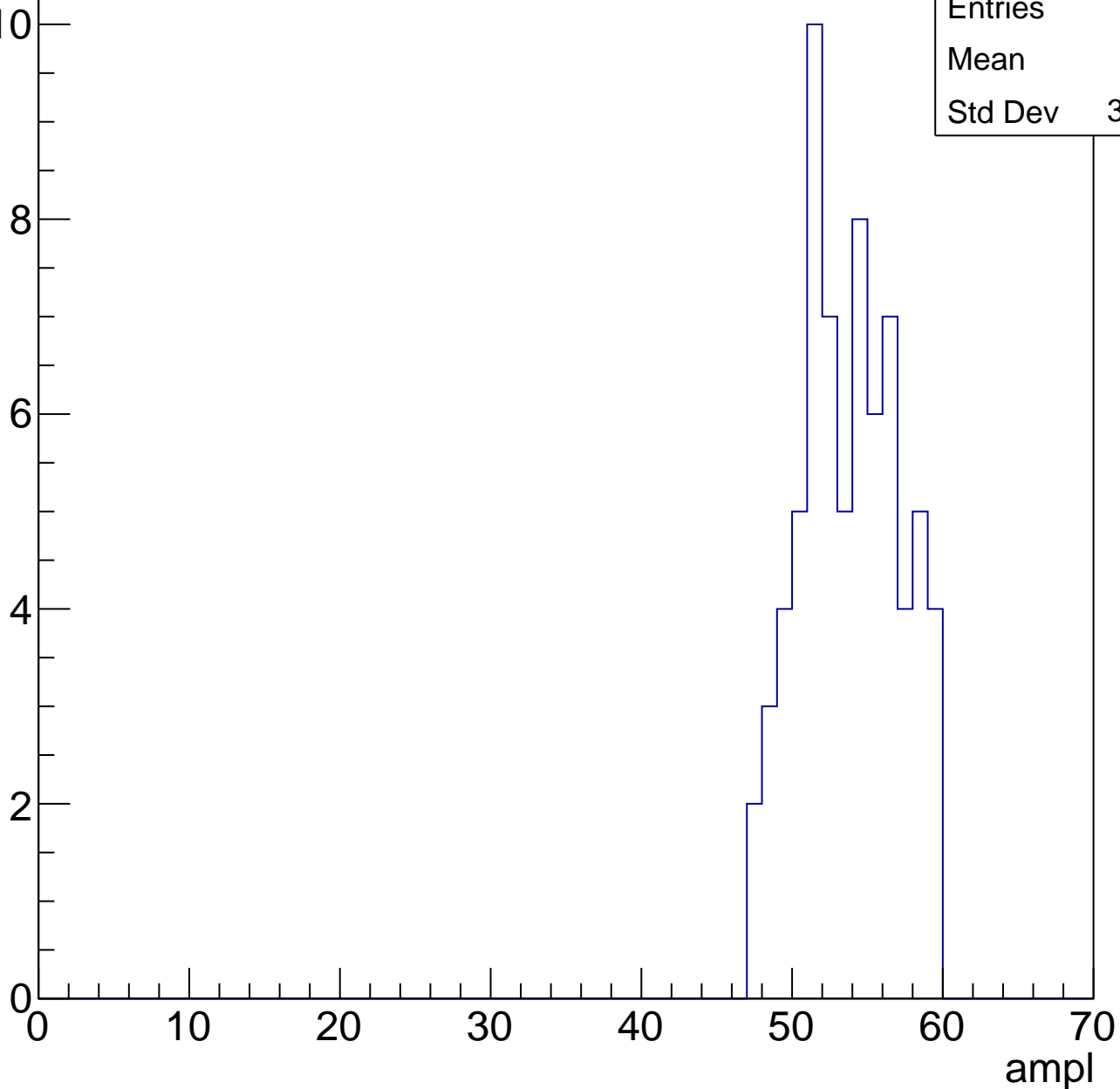


# B0L000S, U19-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	53.3
Std Dev	3.218



# B0L000S, U19-ch40, adc5

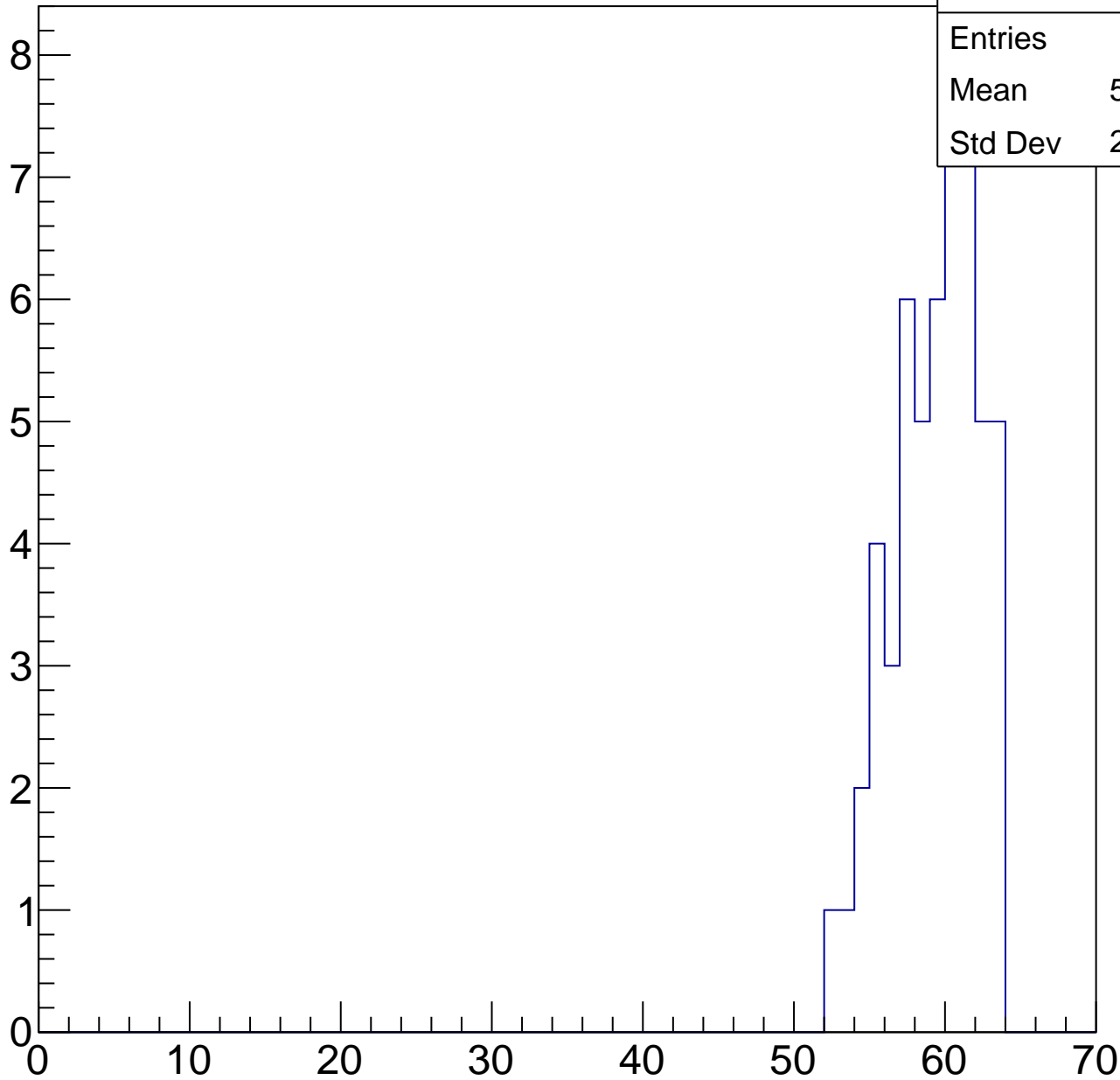
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.89
Std Dev	2.787

ampl

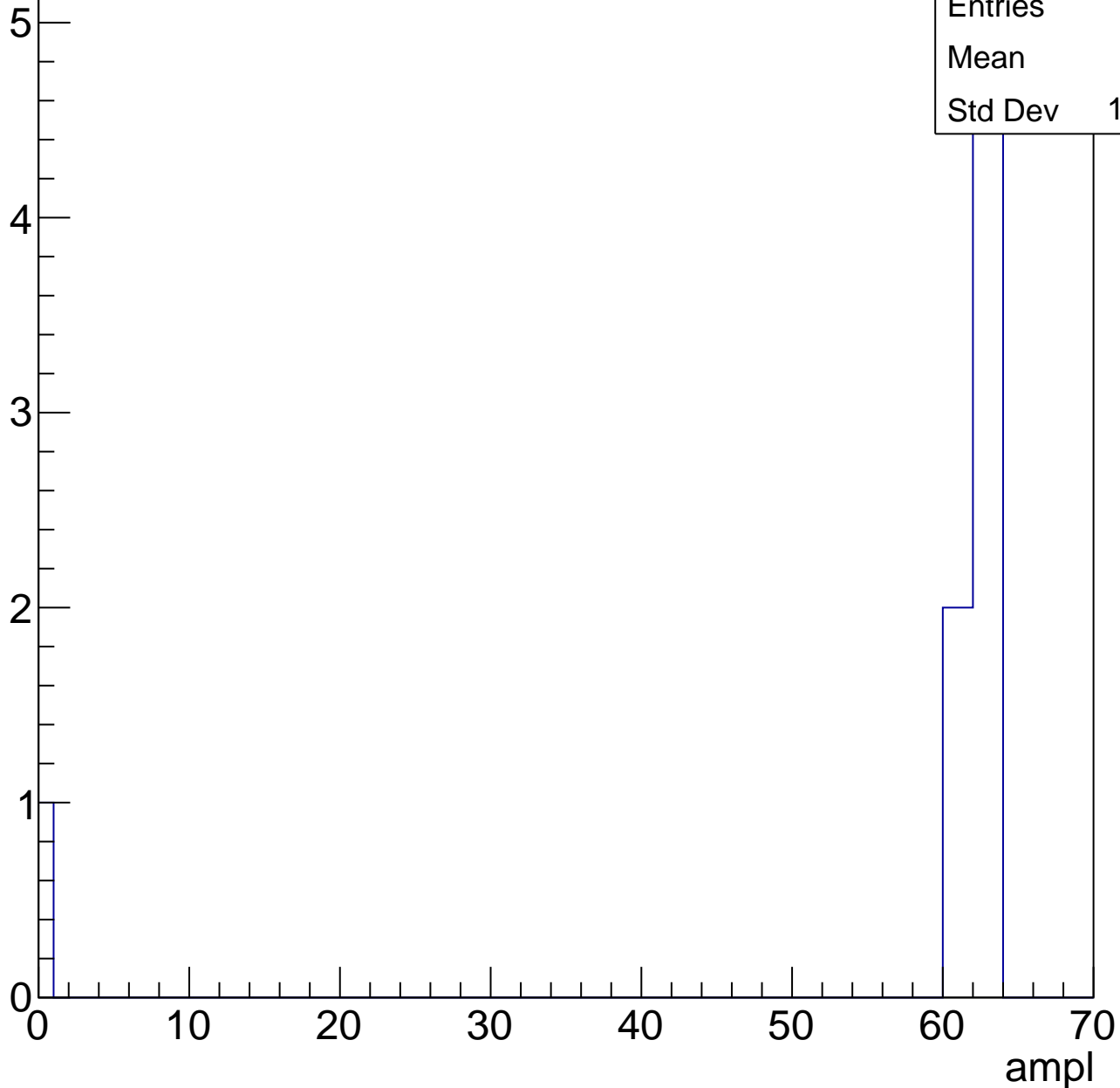


# B0L000S, U19-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	57.8
Std Dev	15.48





# B0L000S, U19-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch41, adc0

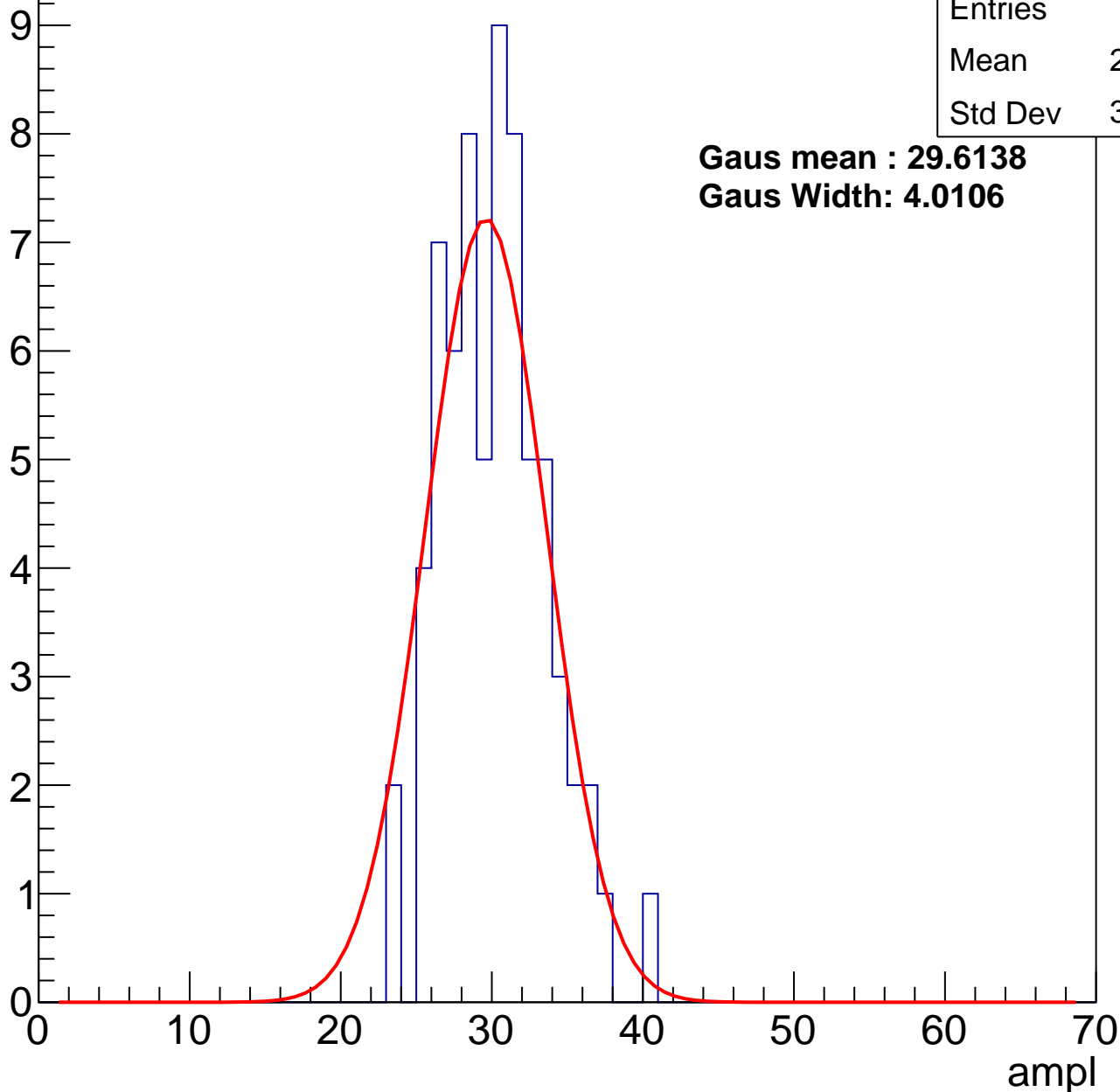
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	29.75
Std Dev	3.406

**Gaus mean : 29.6138**

**Gaus Width: 4.0106**



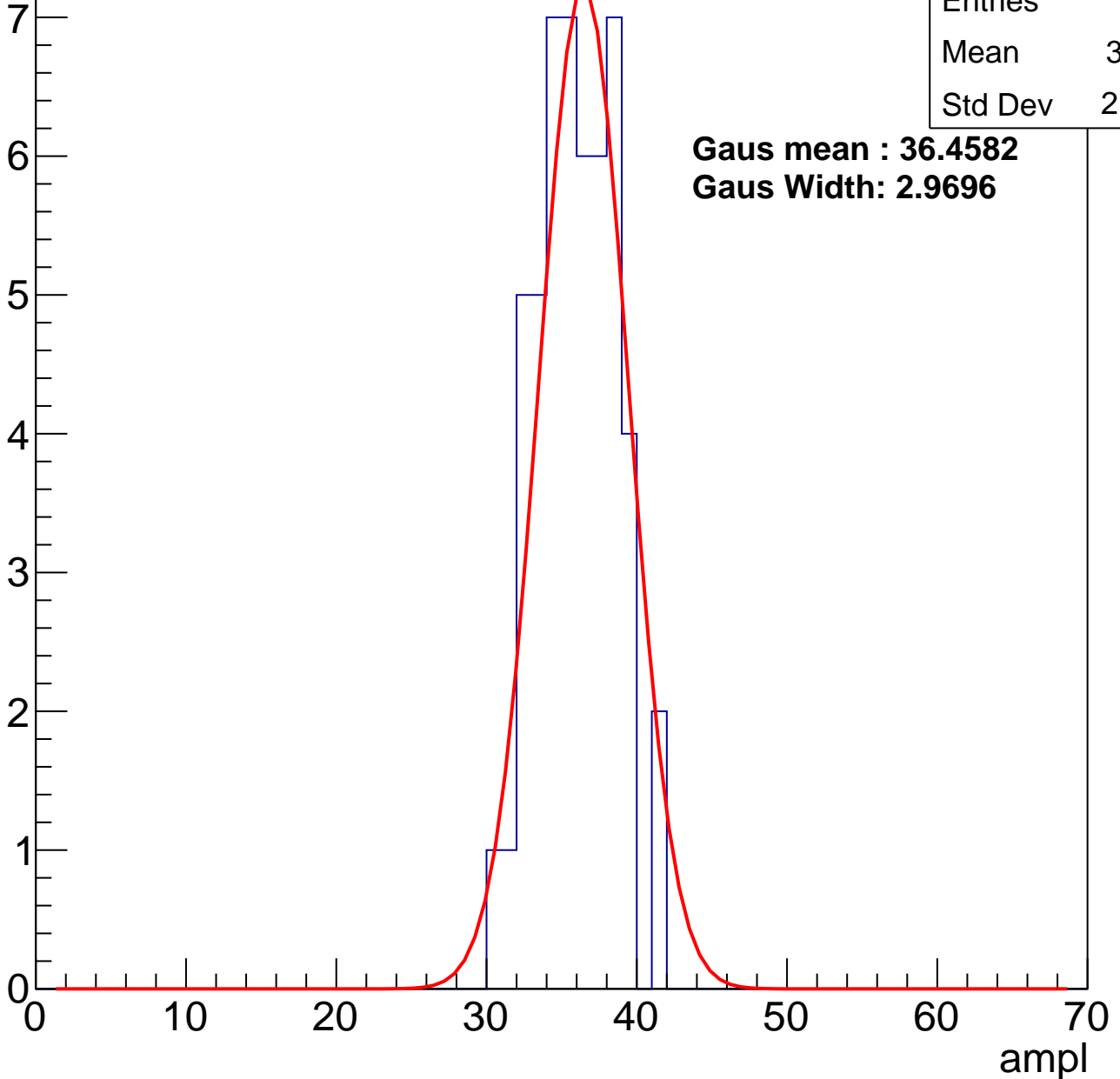
# B0L000S, U19-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	35.51
Std Dev	2.539

**Gaus mean : 36.4582**  
**Gaus Width: 2.9696**



# B0L000S, U19-ch41, adc2

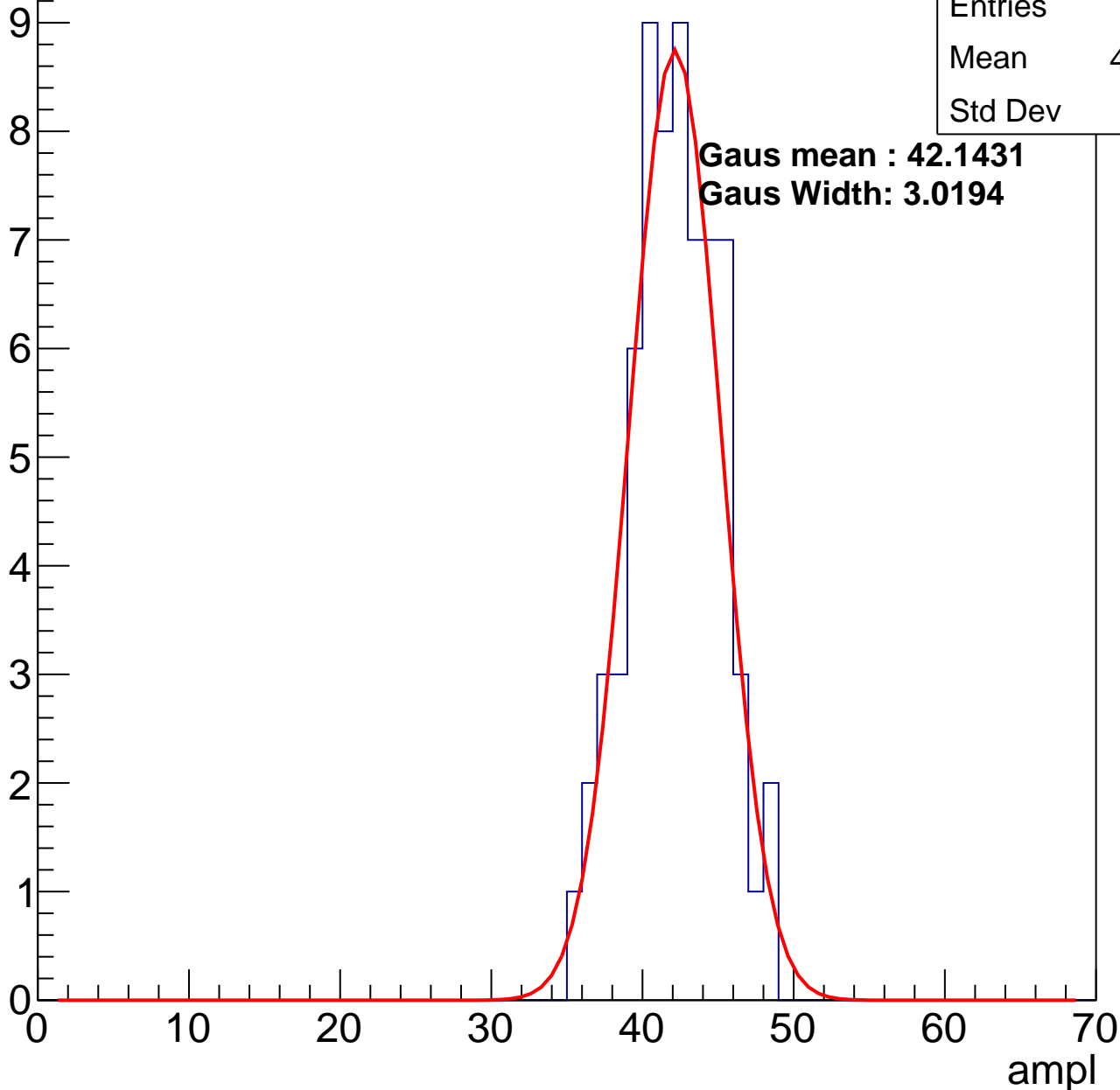
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	41.72
Std Dev	2.92

**Gaus mean : 42.1431**

**Gaus Width: 3.0194**



# B0L000S, U19-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

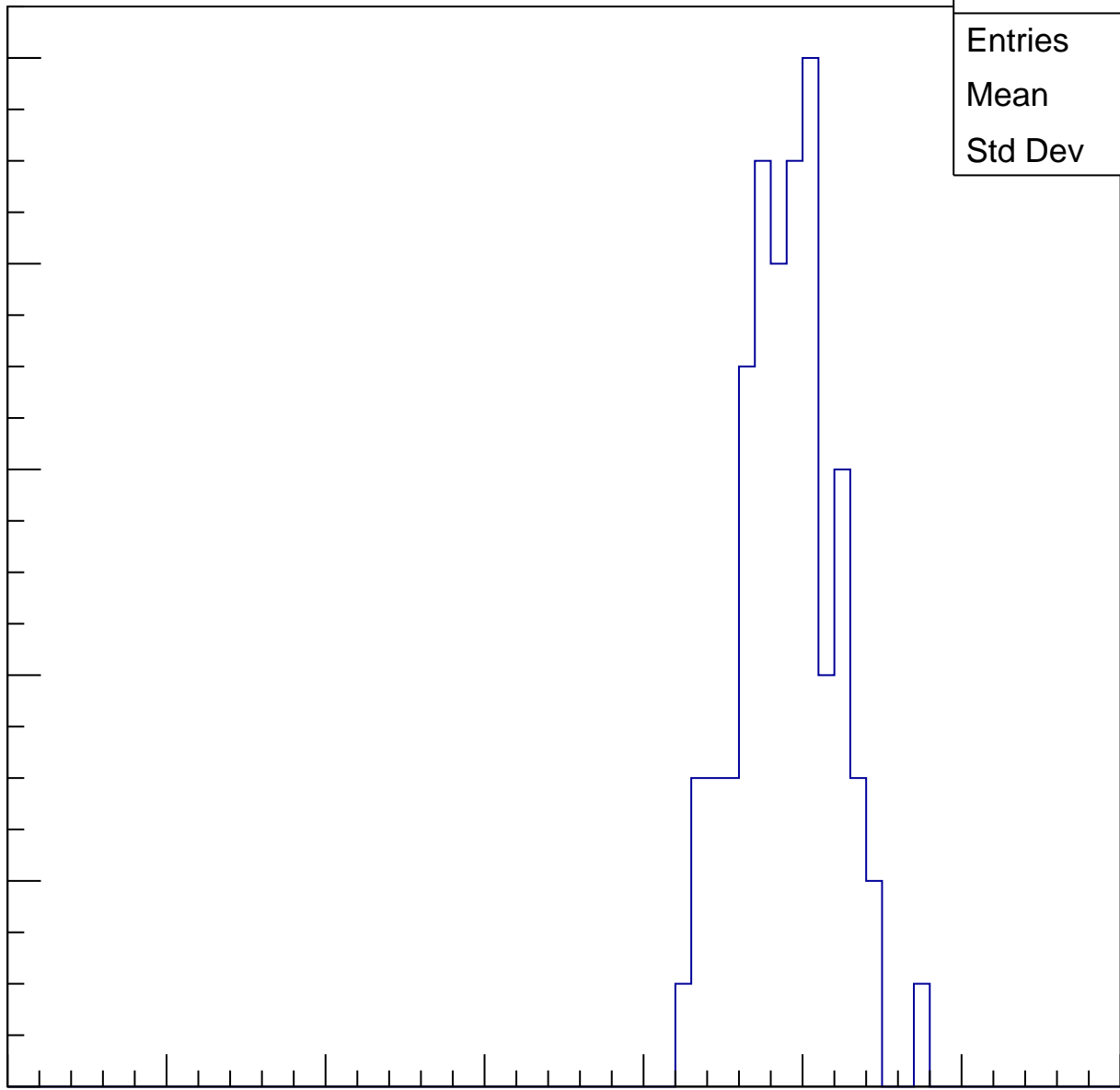
Entries	69
Mean	48.52
Std Dev	2.981

Entry

10  
8  
6  
4  
2  
0

ampl

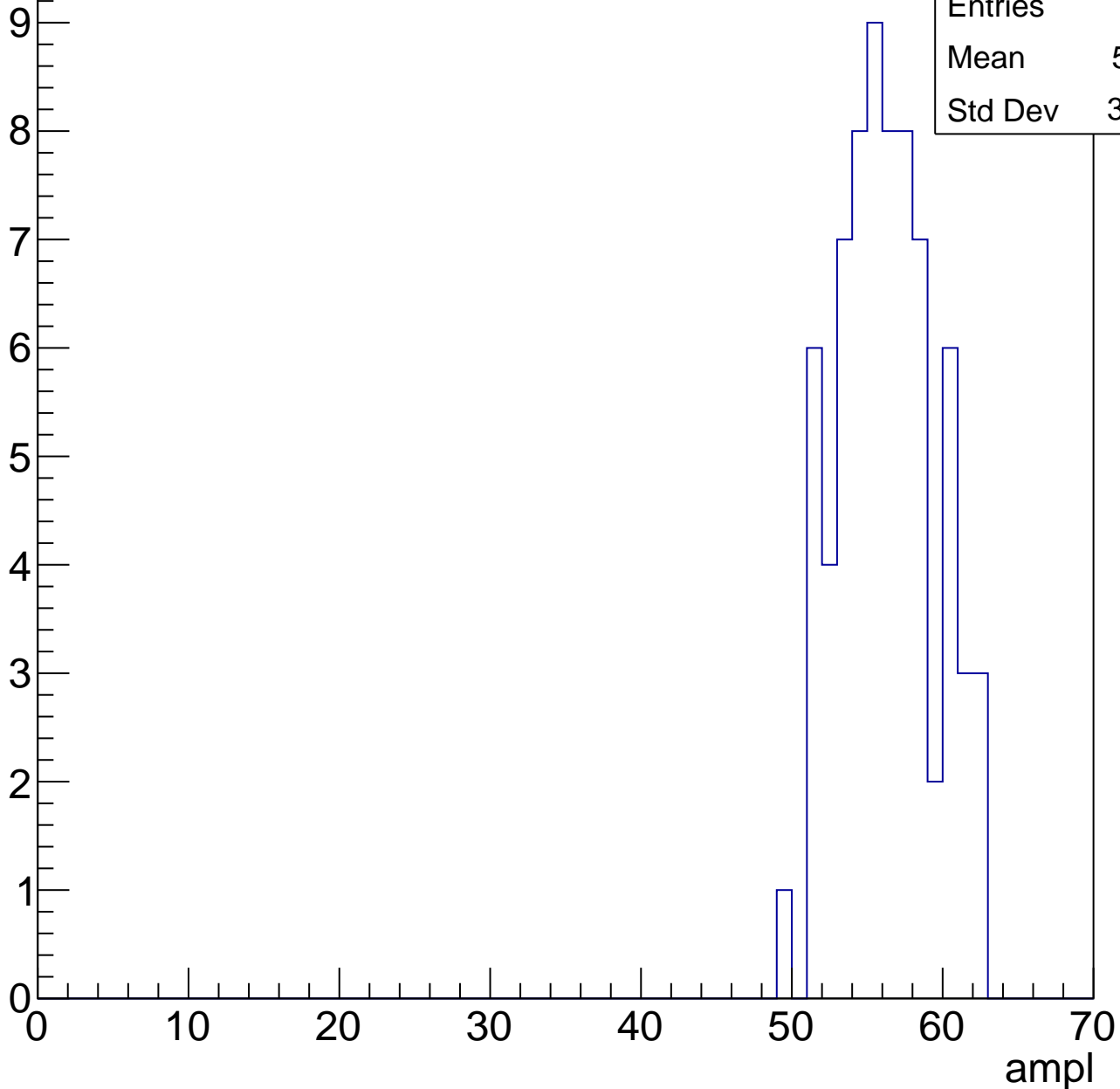
0 10 20 30 40 50 60 70



# B0L000S, U19-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

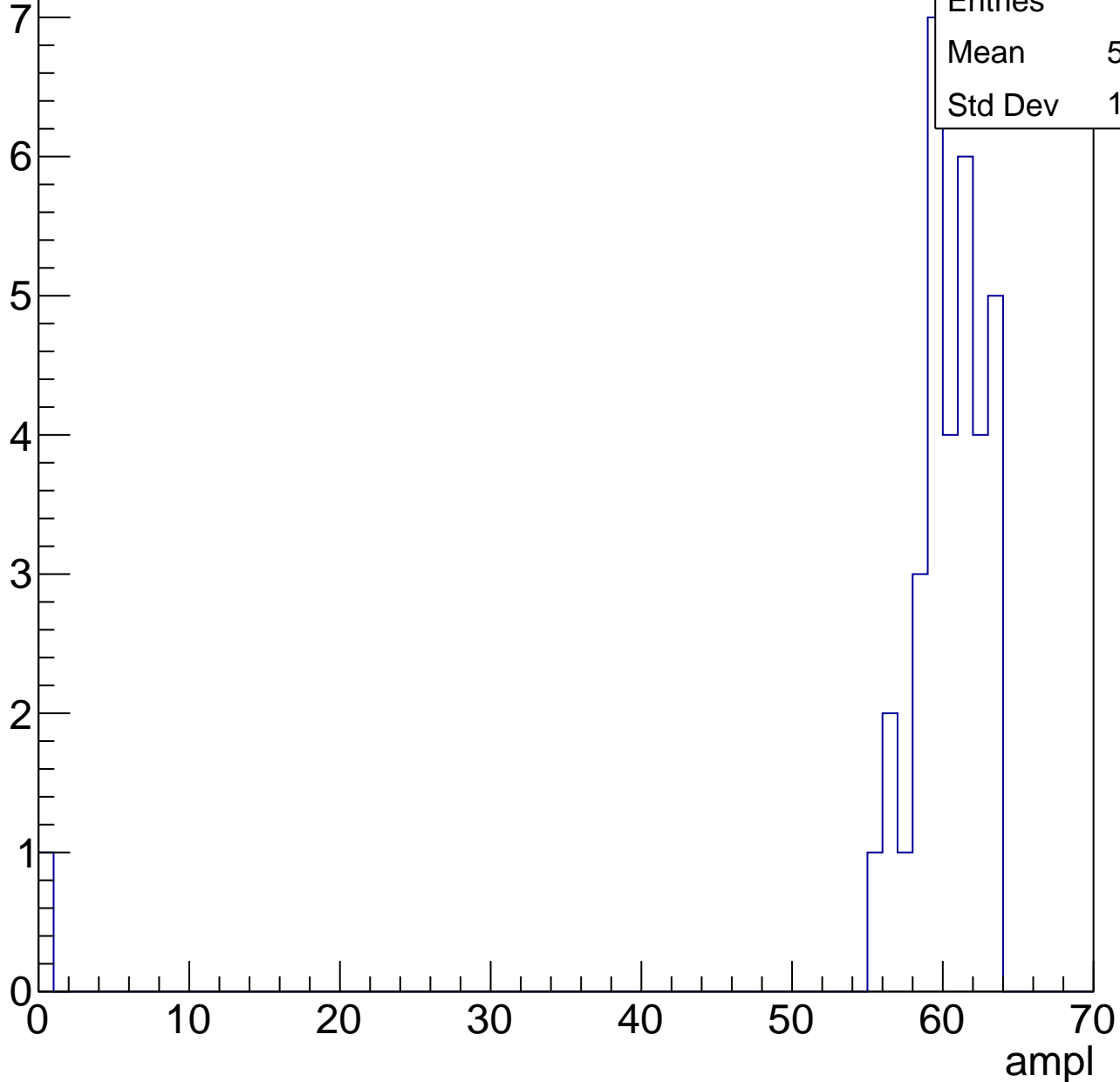


Entries	72
Mean	55.81
Std Dev	3.112

# B0L000S, U19-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

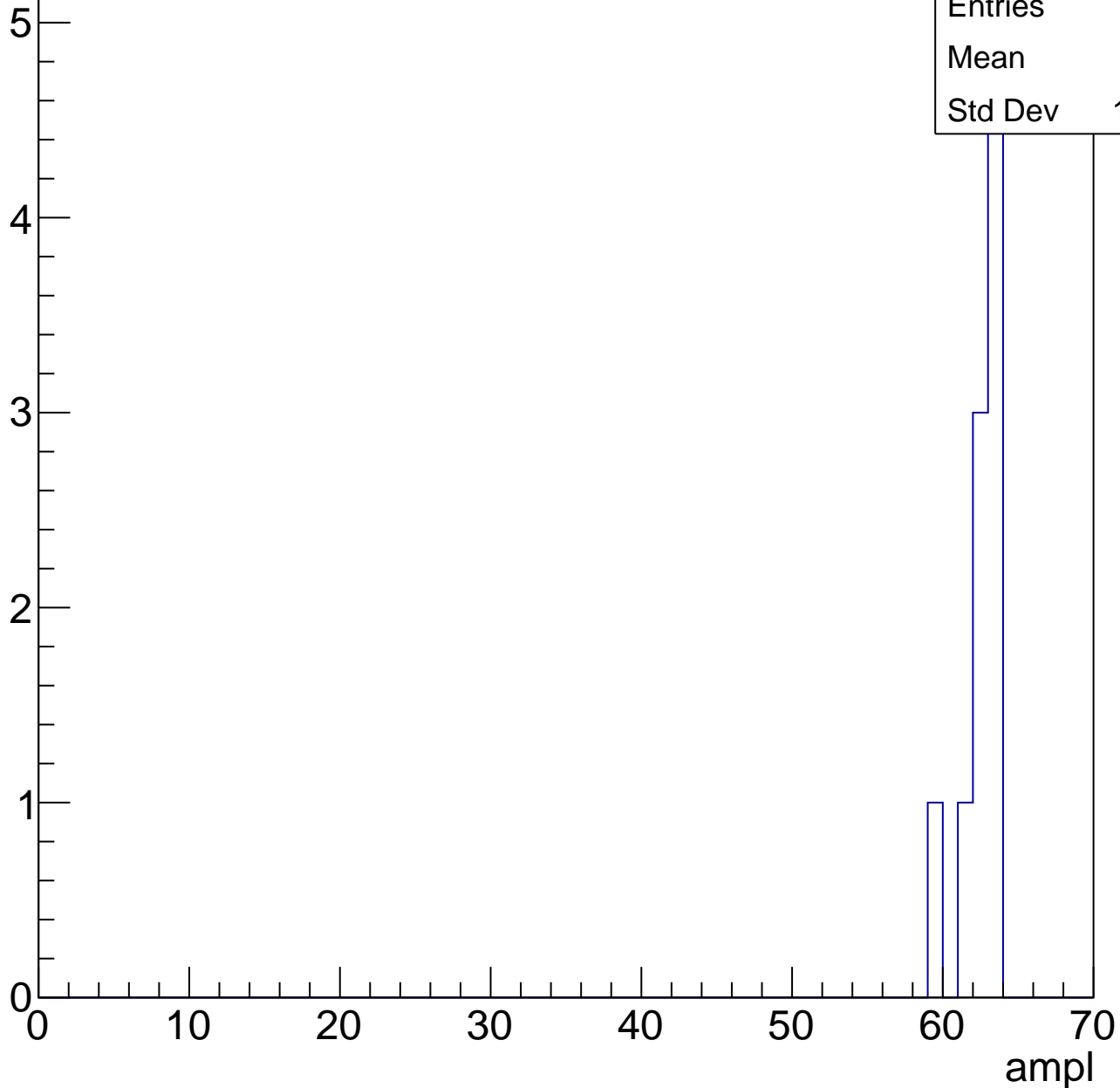


# B0L000S, U19-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62.1
Std Dev	1.221





# B0L000S, U19-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch42, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

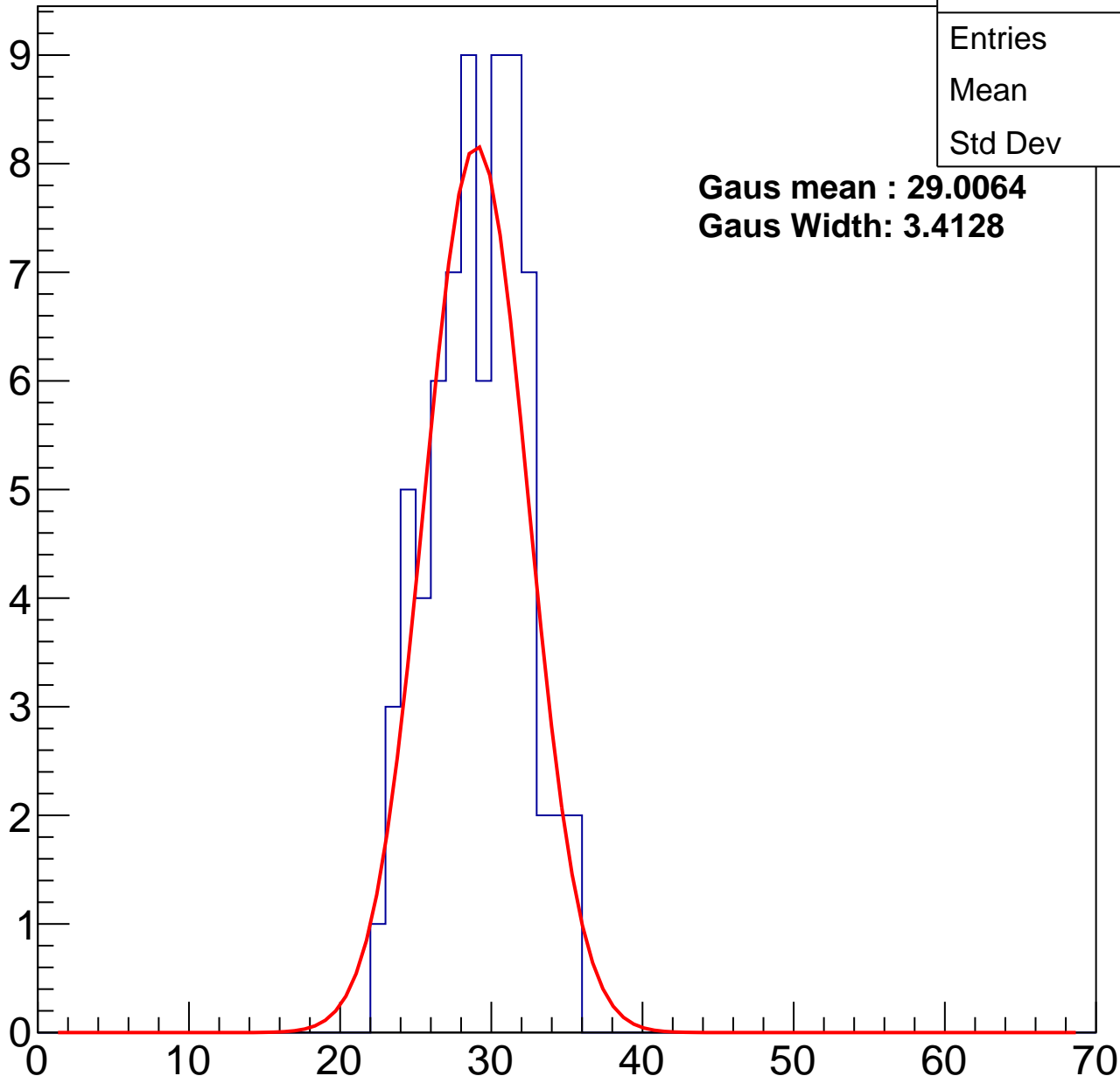
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	72
Mean	28.6
Std Dev	3.09

**Gaus mean : 29.0064**

**Gaus Width: 3.4128**

ampl



# B0L000S, U19-ch42, adc1

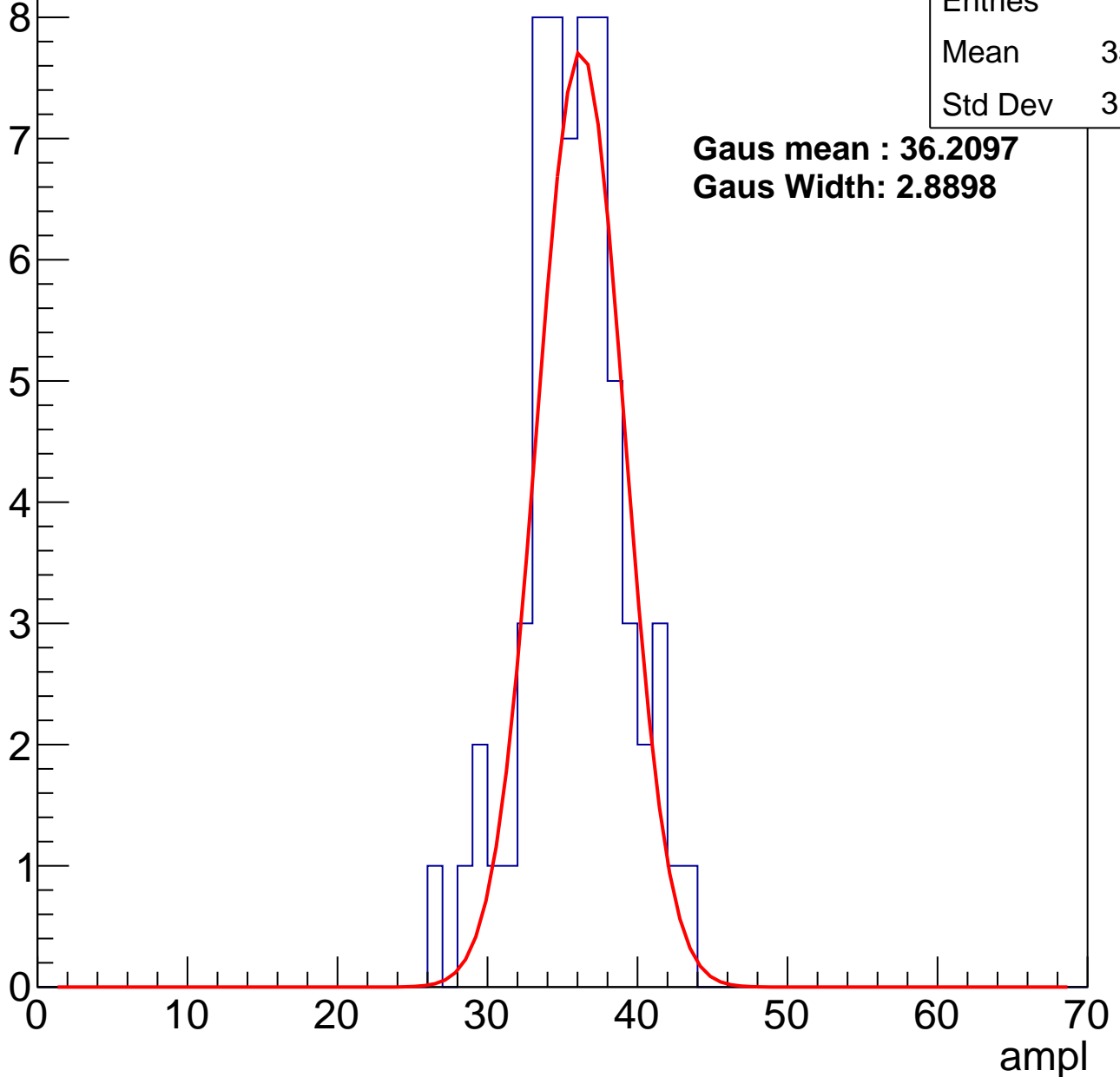
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	35.38
Std Dev	3.345

**Gaus mean : 36.2097**

**Gaus Width: 2.8898**



# B0L000S, U19-ch42, adc2

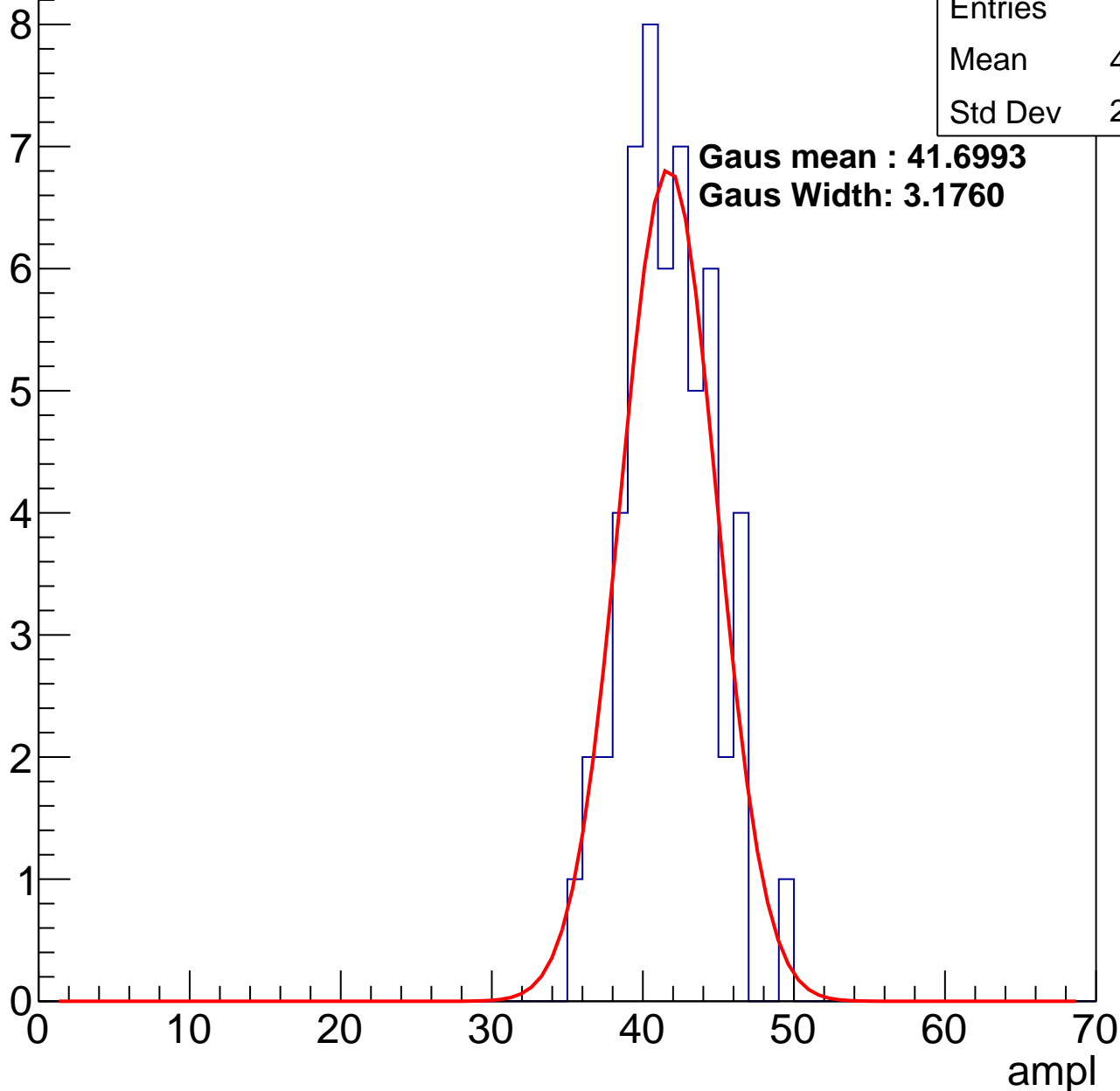
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	41.24
Std Dev	2.904

**Gaus mean : 41.6993**

**Gaus Width: 3.1760**

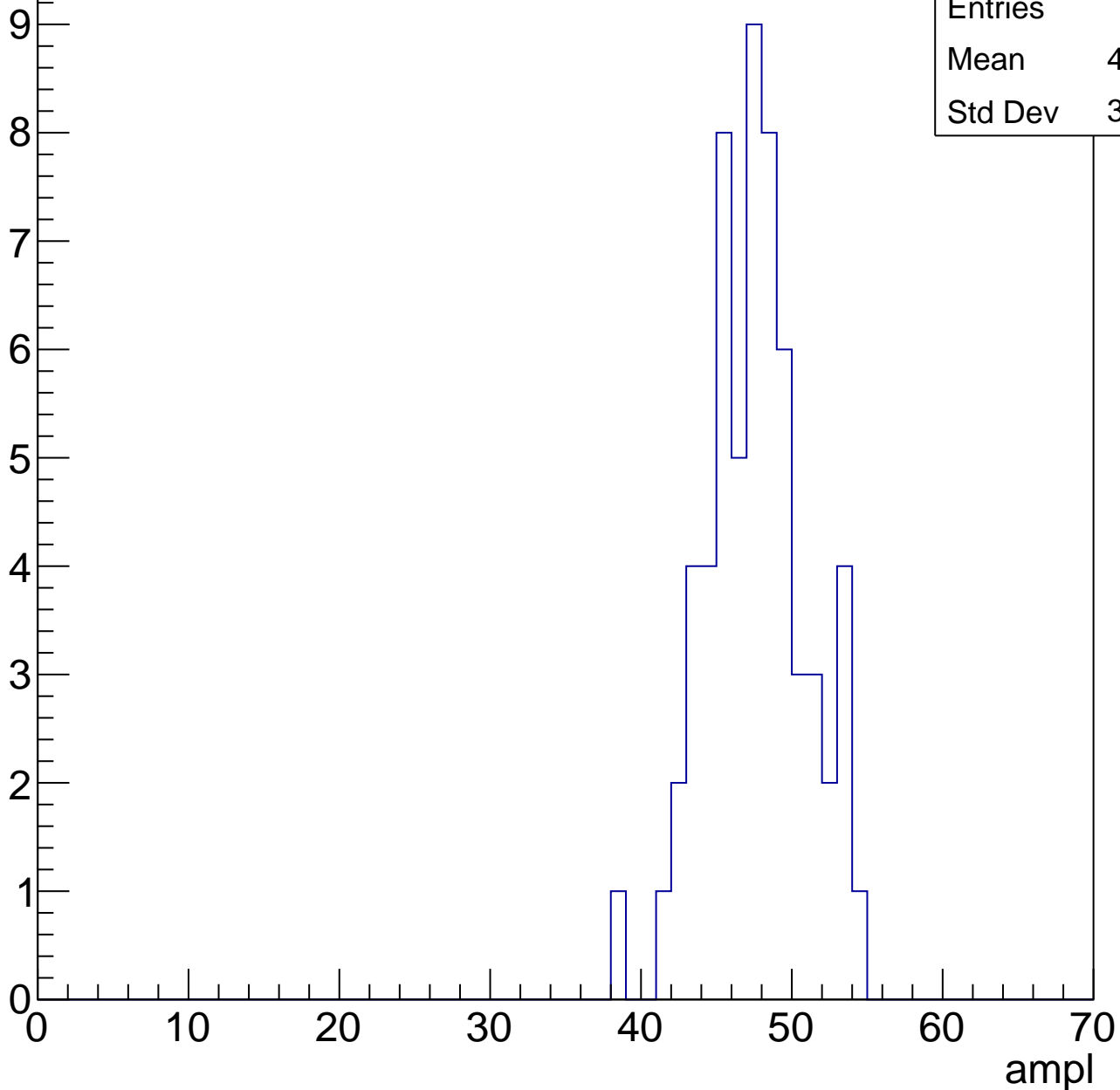


# B0L000S, U19-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	47.13
Std Dev	3.272

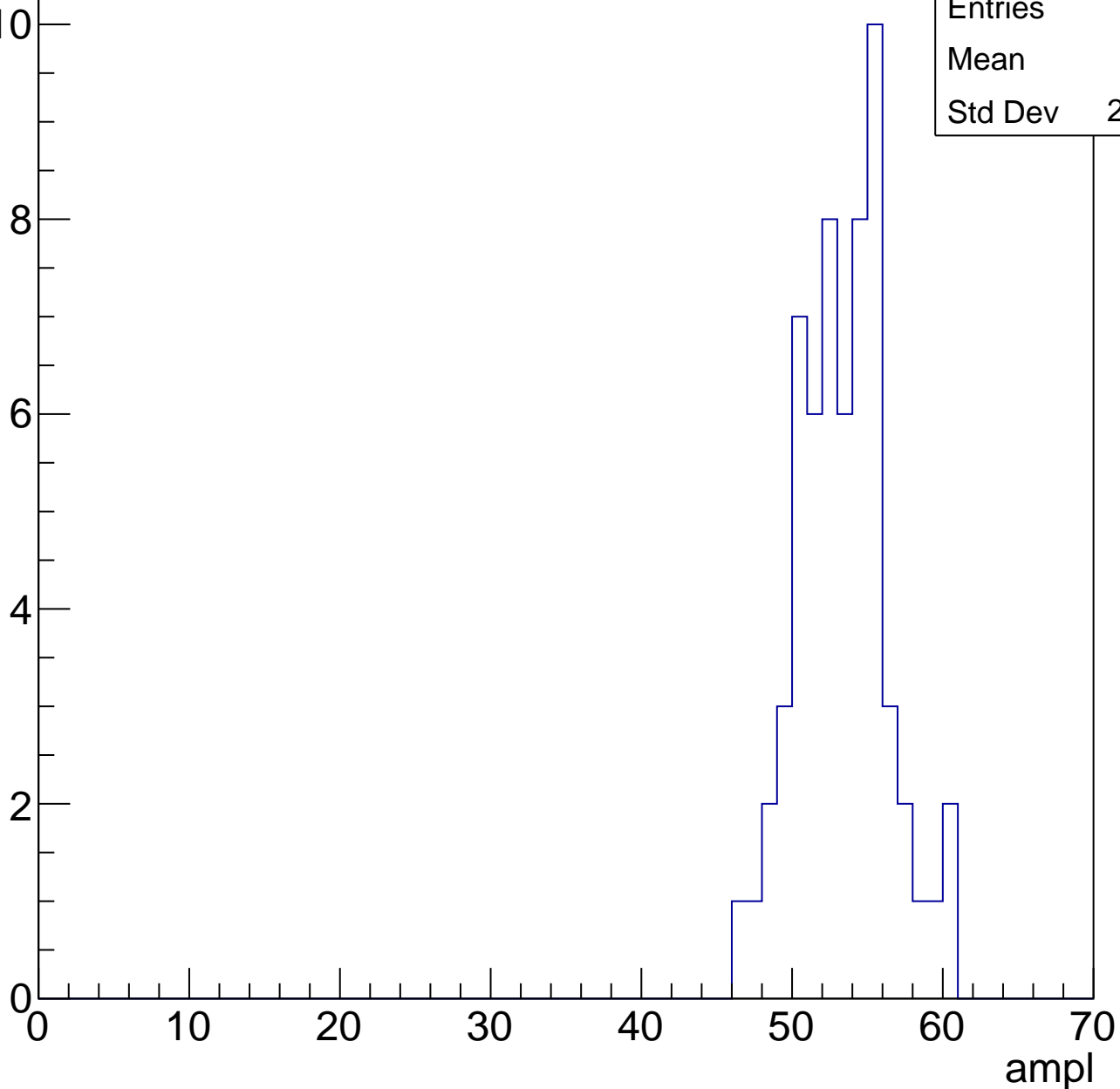


# B0L000S, U19-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	52.9
Std Dev	2.985

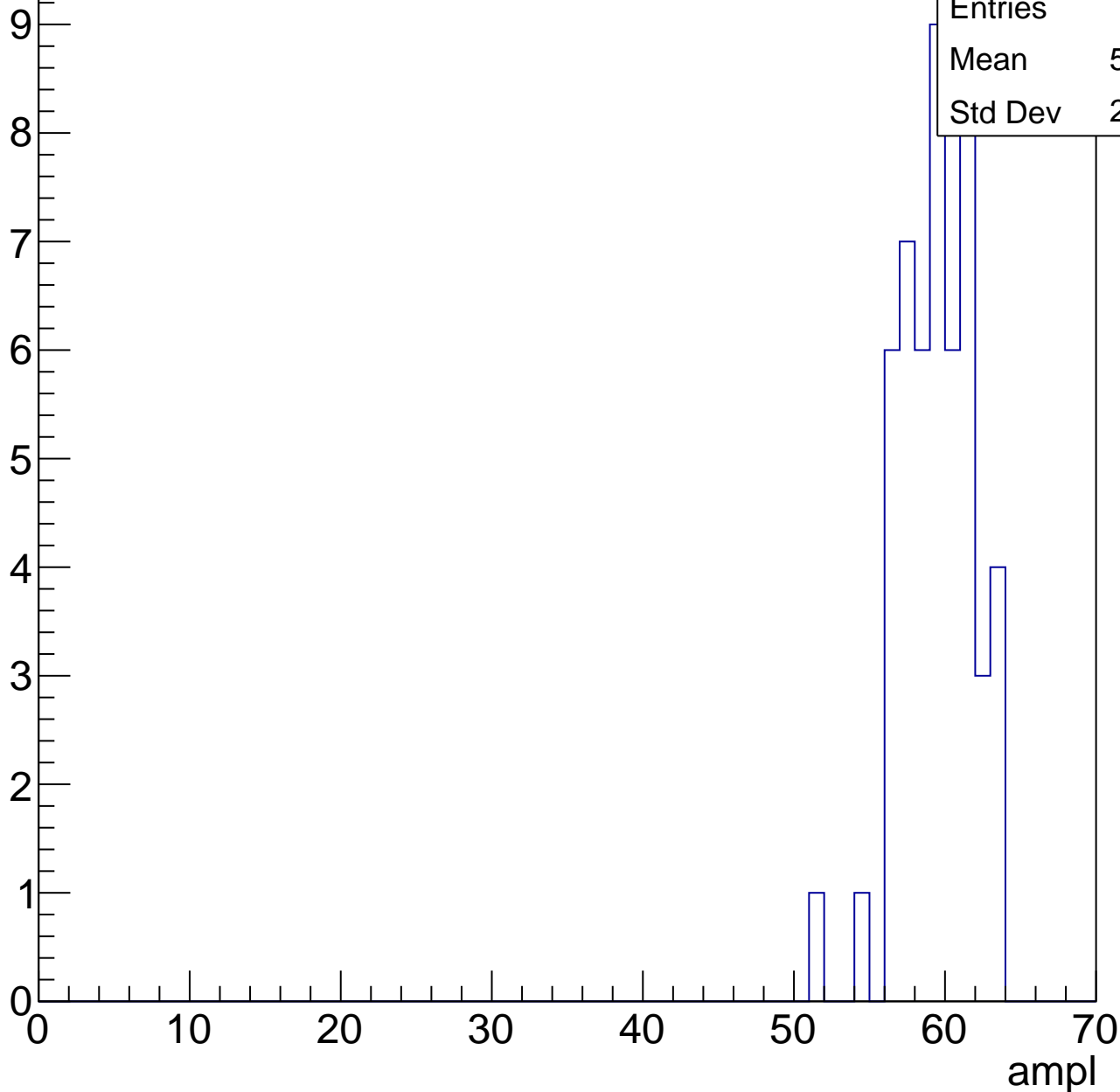


# B0L000S, U19-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

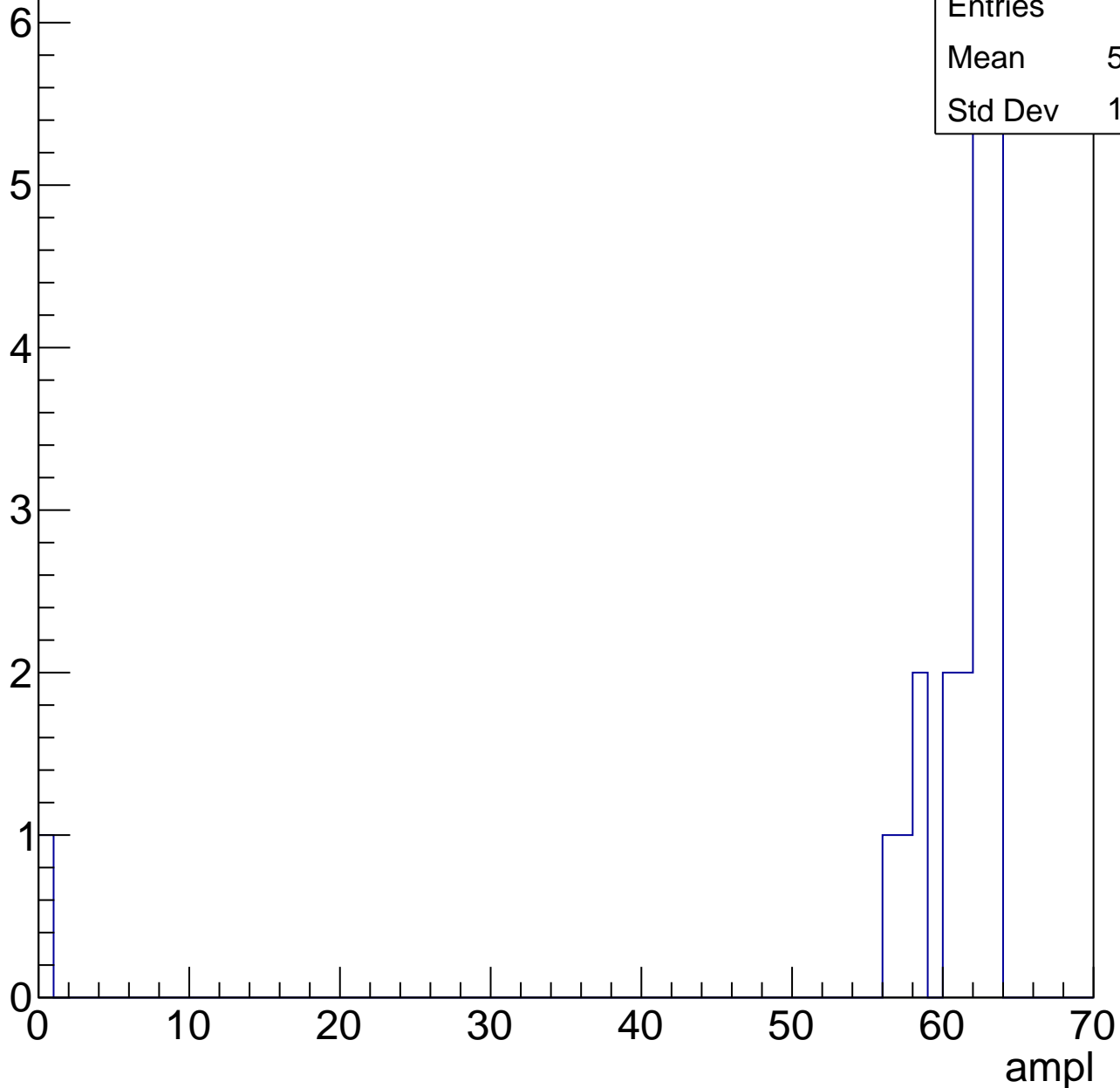
Entries	51
Mean	58.92
Std Dev	2.448



# B0L000S, U19-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

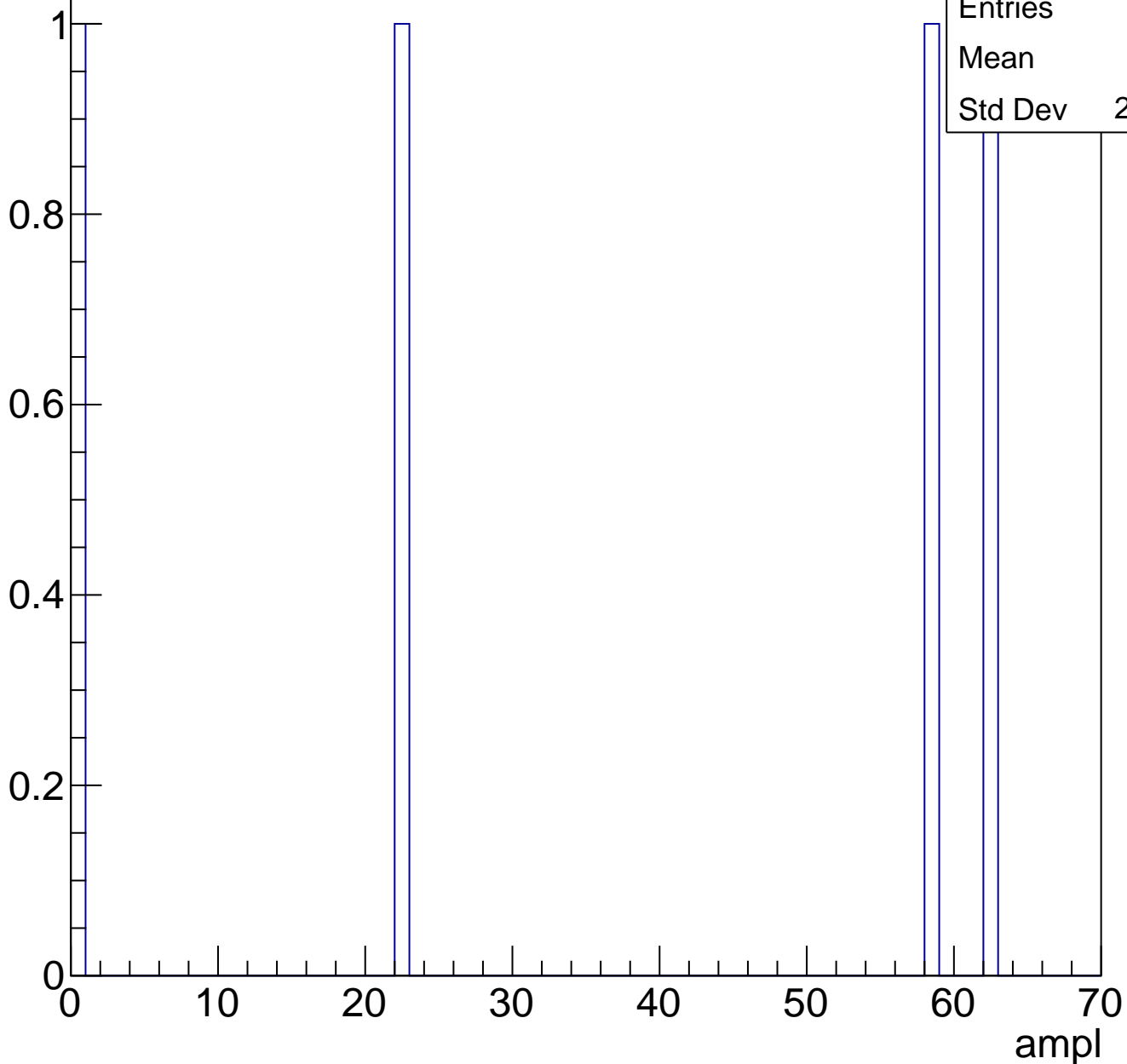




# B0L000S, U19-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch43, adc0

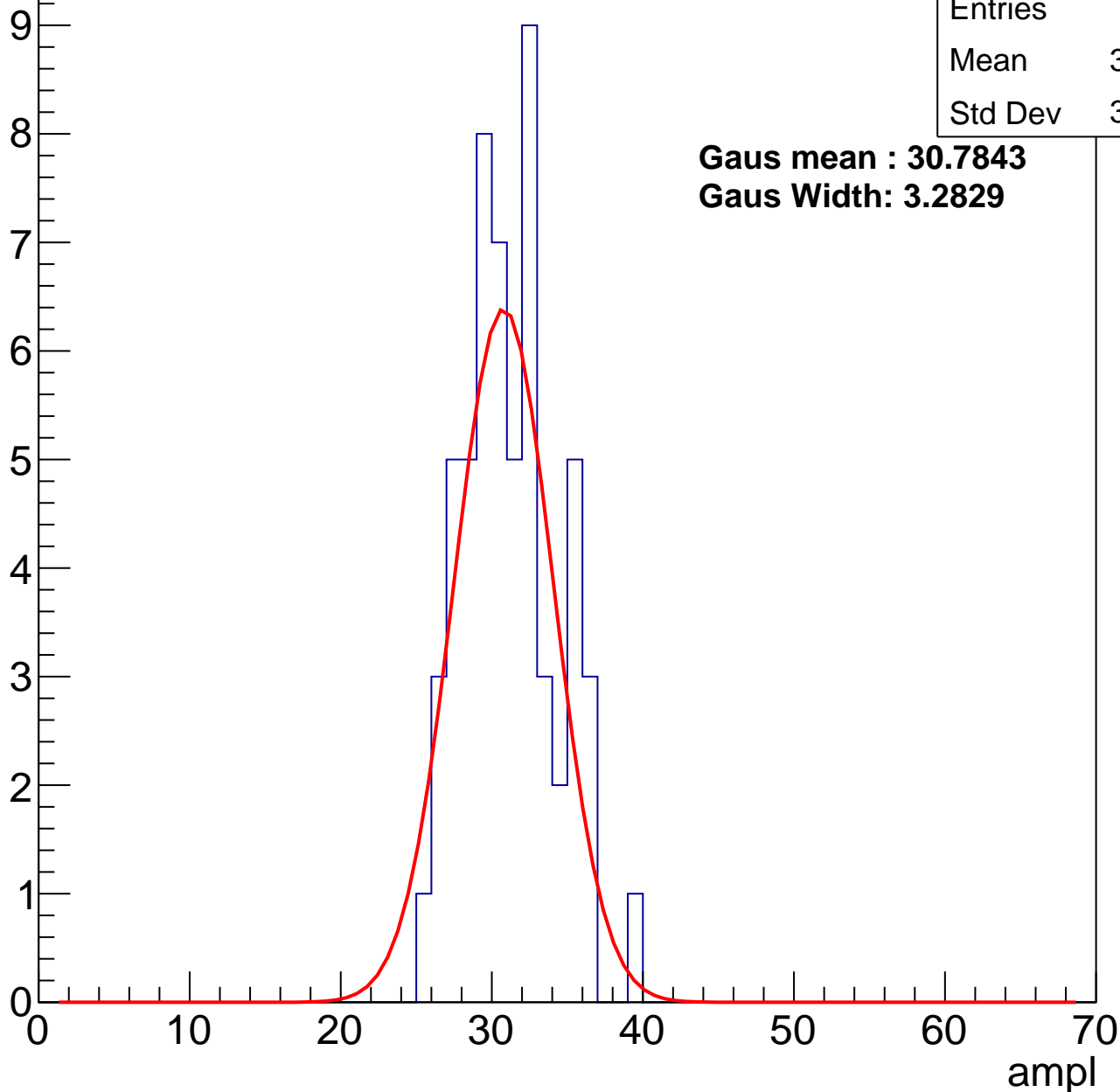
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	30.74
Std Dev	3.047

**Gaus mean : 30.7843**

**Gaus Width: 3.2829**



# B0L000S, U19-ch43, adc1

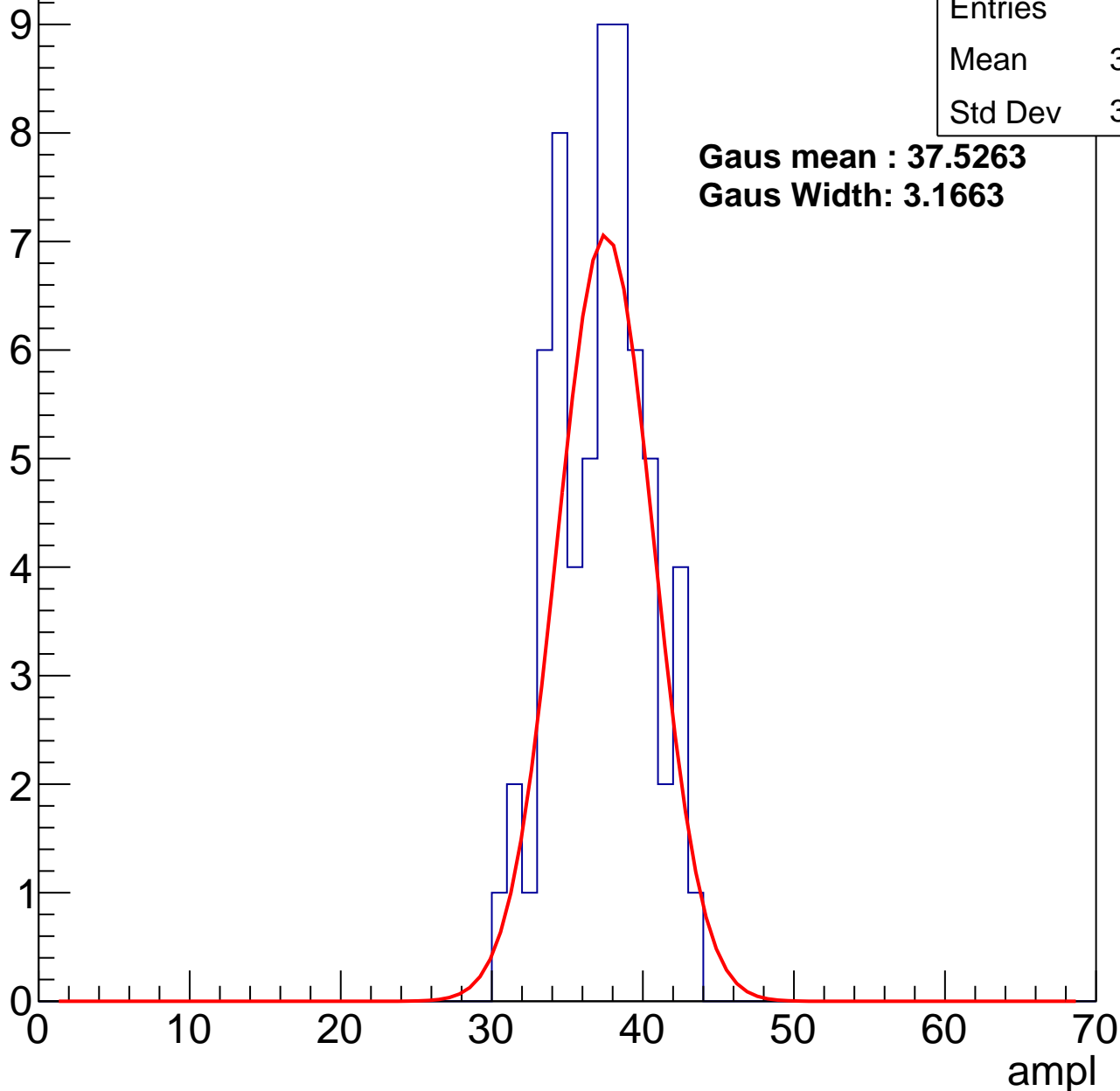
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.76
Std Dev	3.027

**Gaus mean : 37.5263**

**Gaus Width: 3.1663**



# B0L000S, U19-ch43, adc2

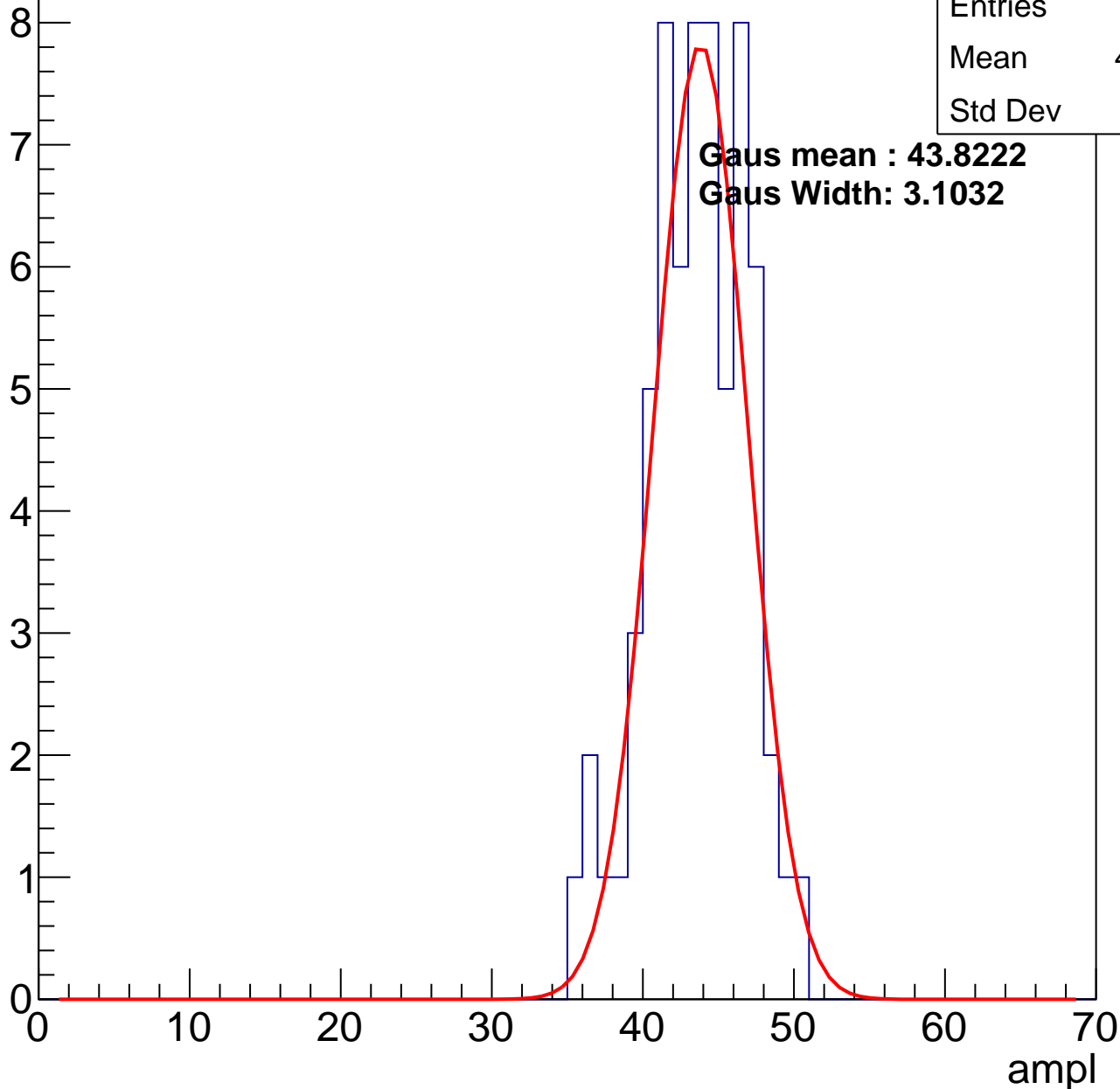
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.11
Std Dev	3.22

**Gaus mean : 43.8222**

**Gaus Width: 3.1032**

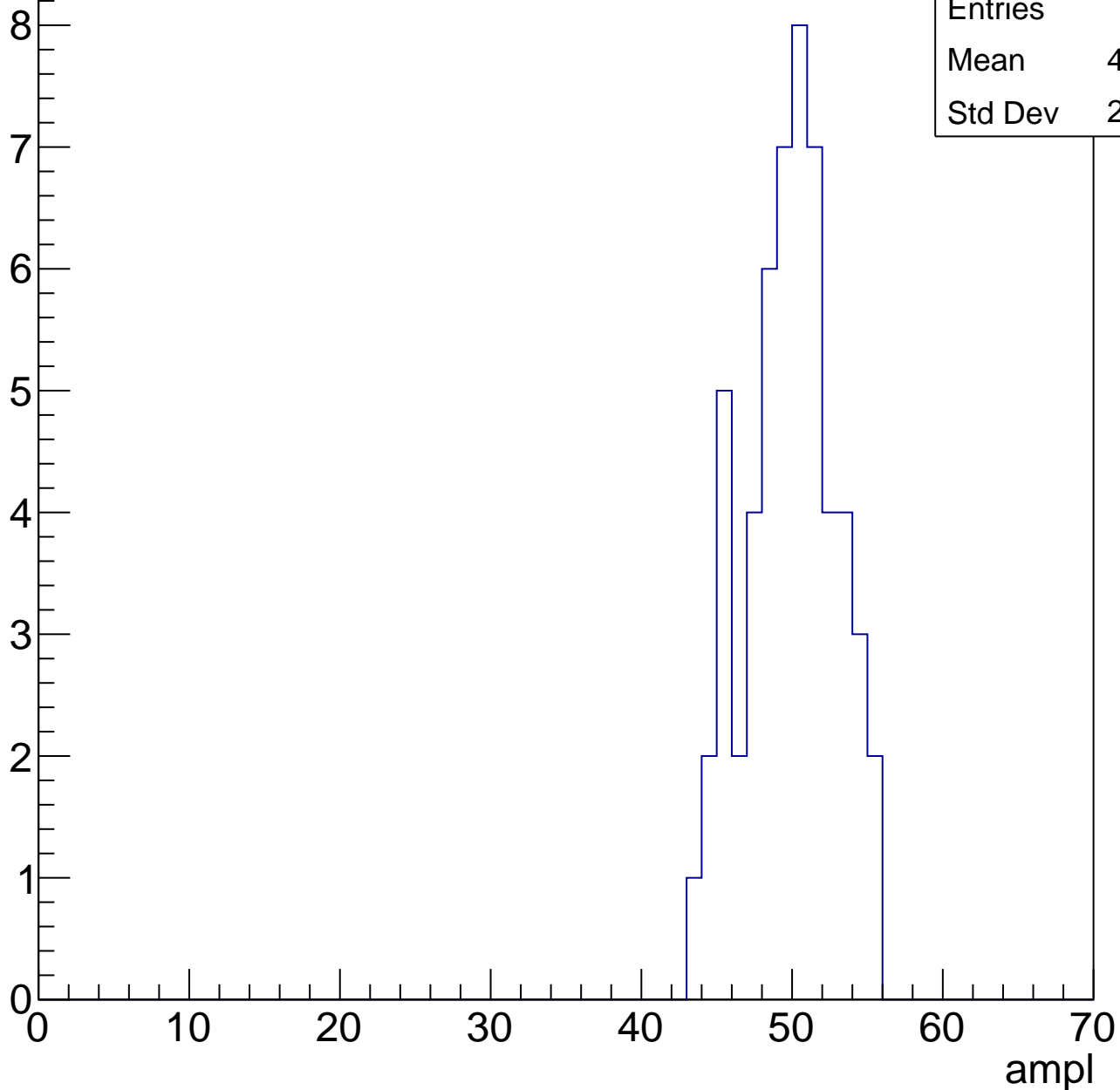


# B0L000S, U19-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

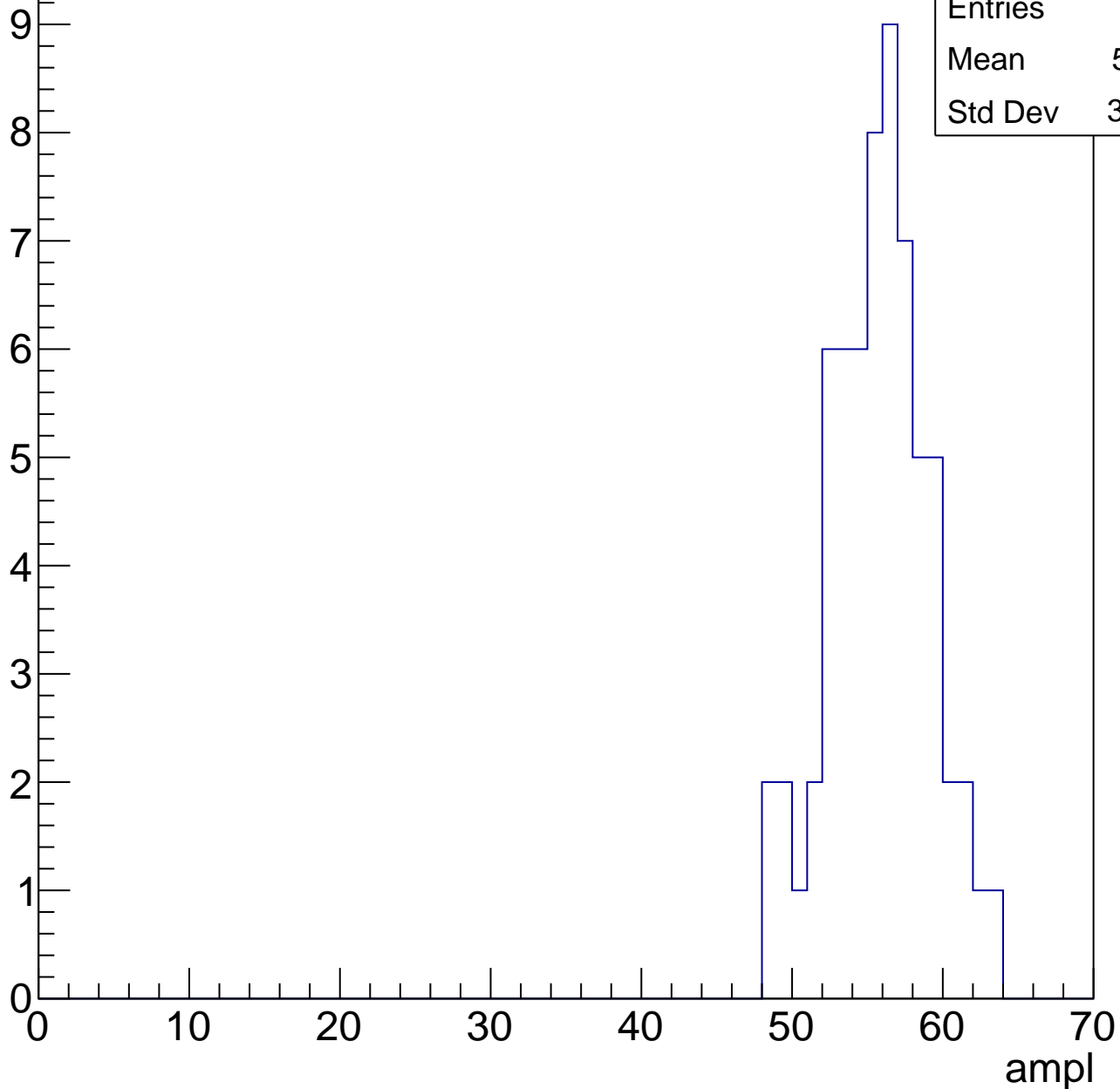
Entries	55
Mean	49.38
Std Dev	2.957



# B0L000S, U19-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

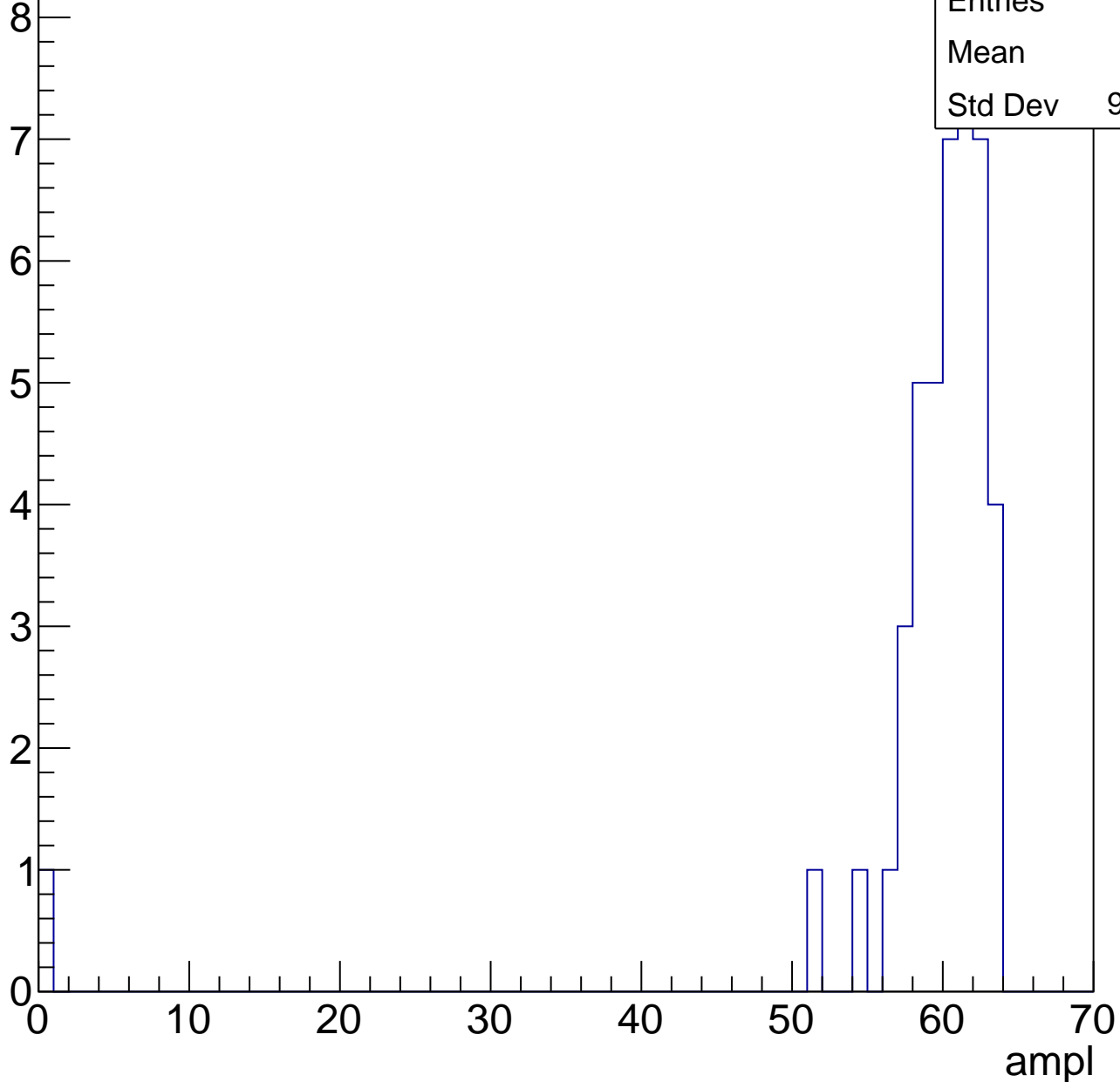


# B0L000S, U19-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.4
Std Dev	9.334

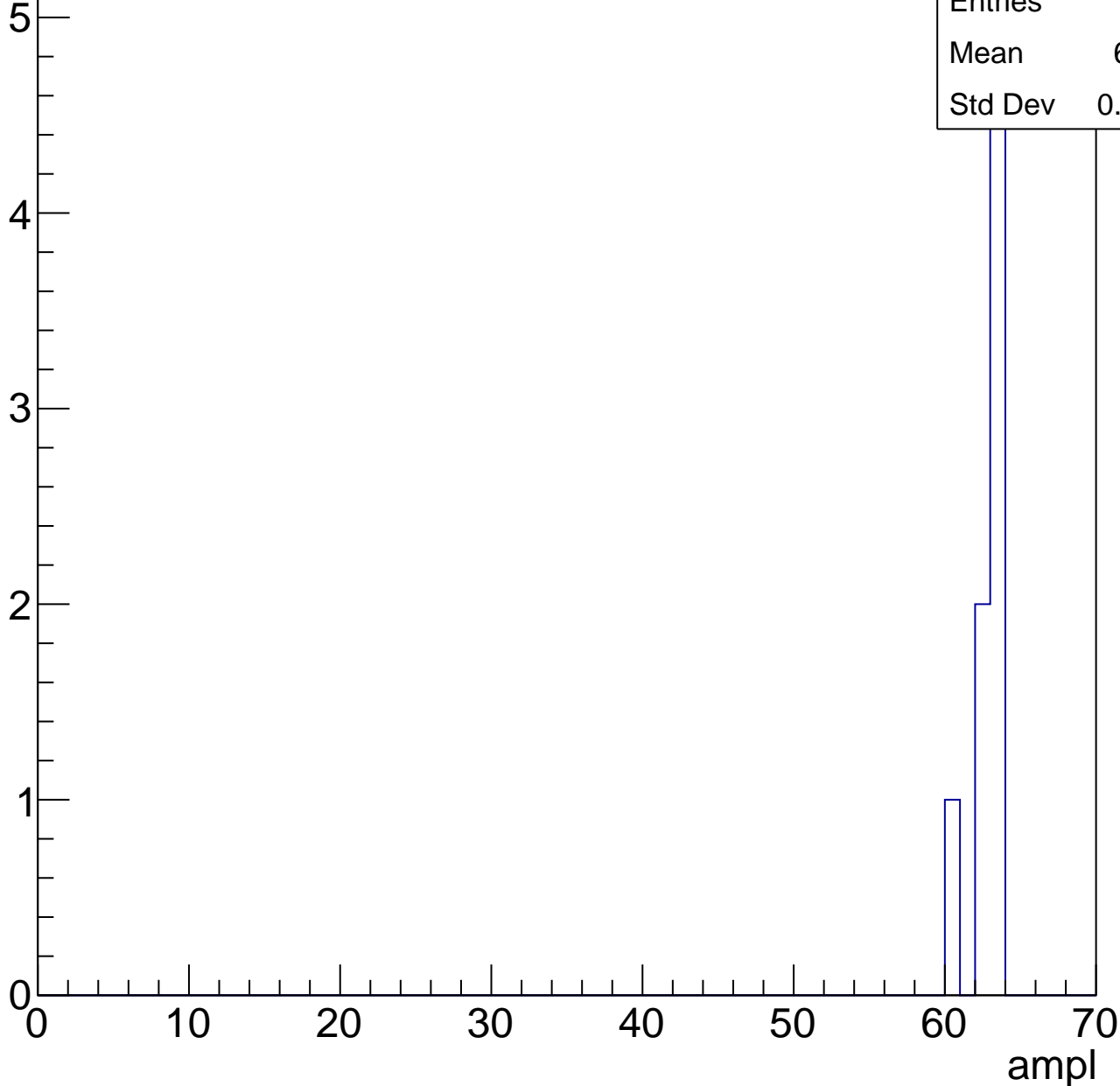


# B0L000S, U19-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	8
Mean	62.38
Std Dev	0.9922





# B0L000S, U19-ch43, adc7

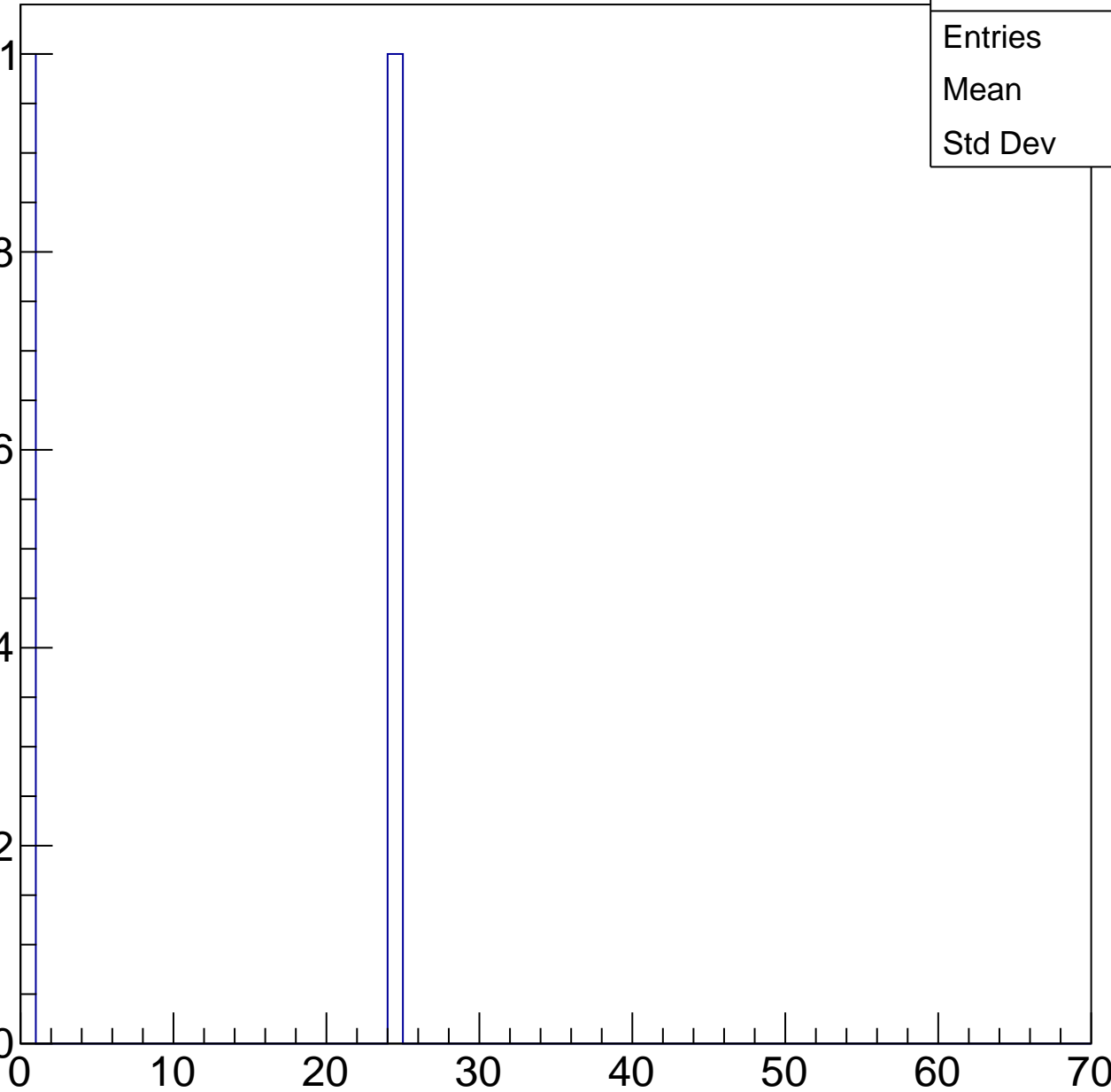
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	12
Std Dev	12

ampl



# B0L000S, U19-ch44, adc0

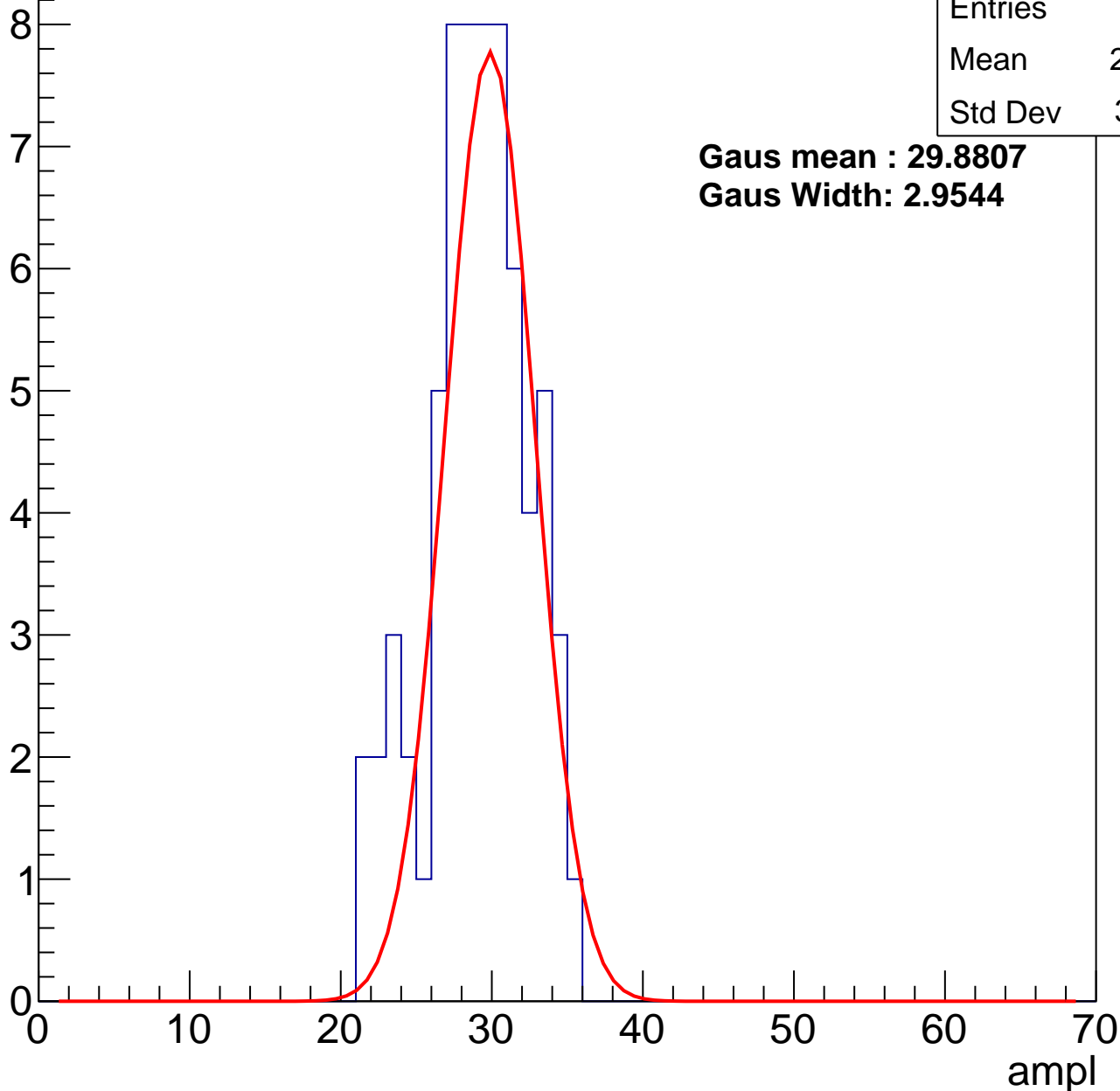
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.58
Std Dev	3.331

**Gaus mean : 29.8807**

**Gaus Width: 2.9544**



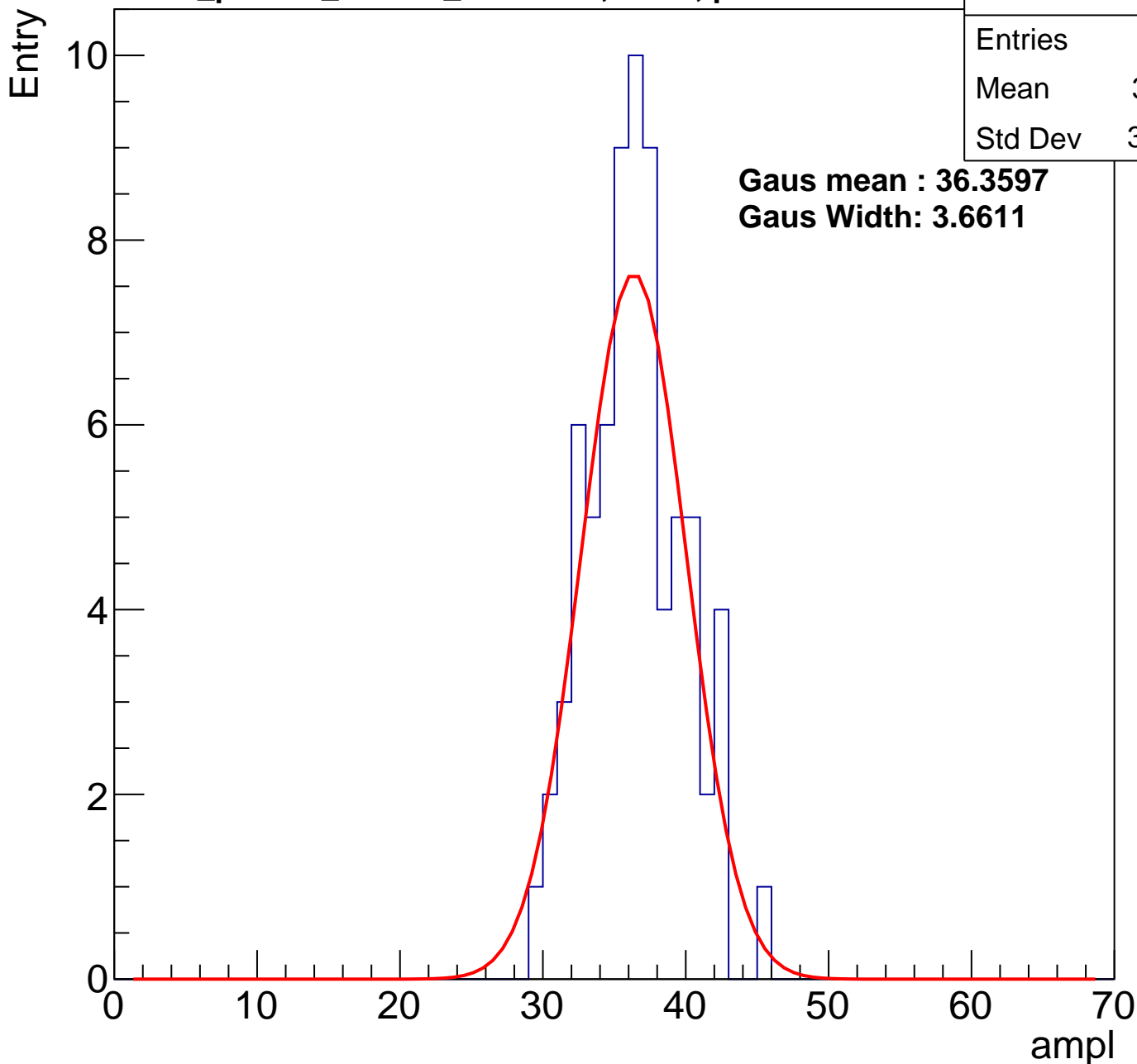
# B0L000S, U19-ch44, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	36.01
Std Dev	3.323

**Gaus mean : 36.3597**

**Gaus Width: 3.6611**



# B0L000S, U19-ch44, adc2

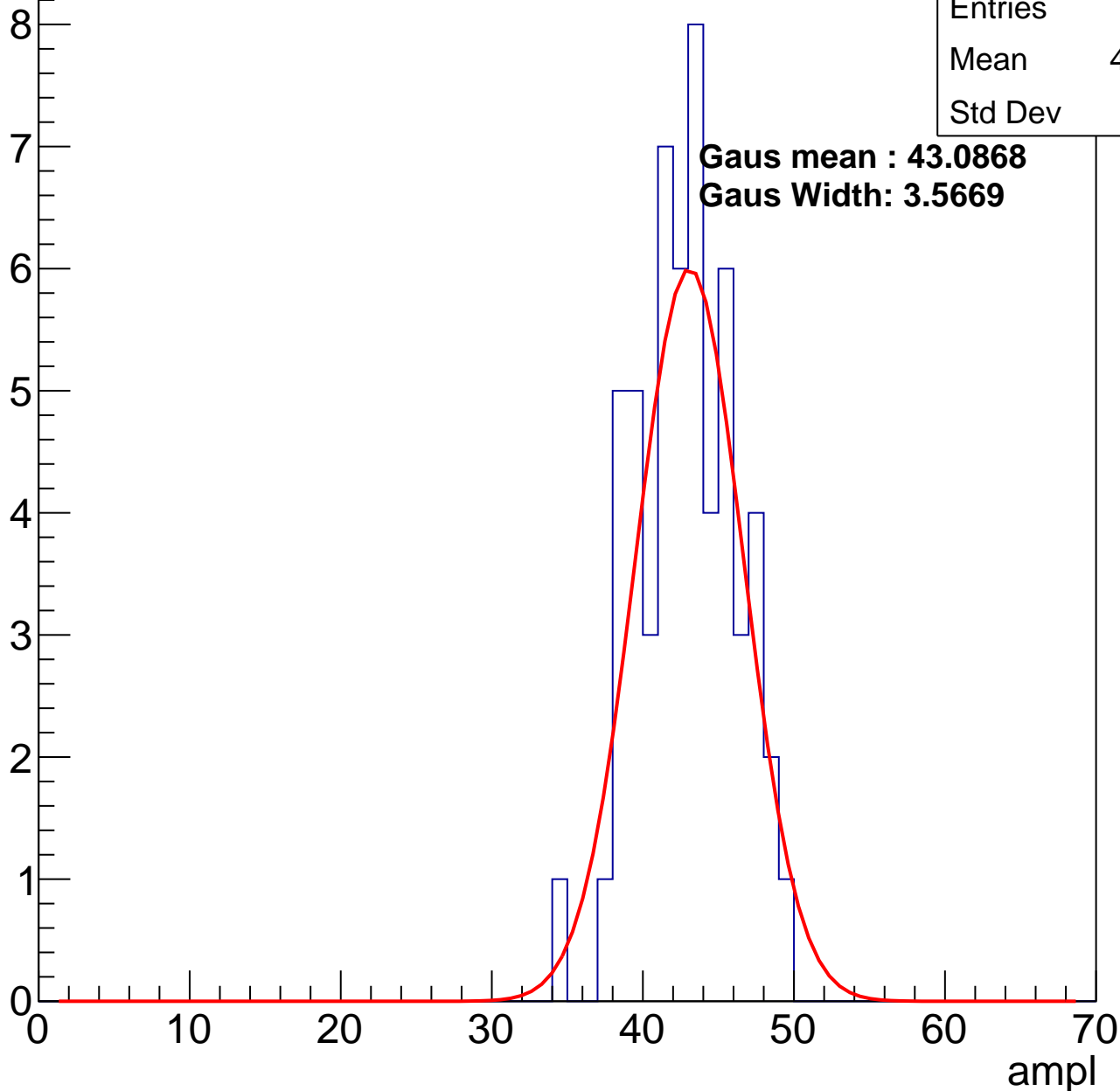
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.43
Std Dev	3.19

**Gaus mean : 43.0868**

**Gaus Width: 3.5669**

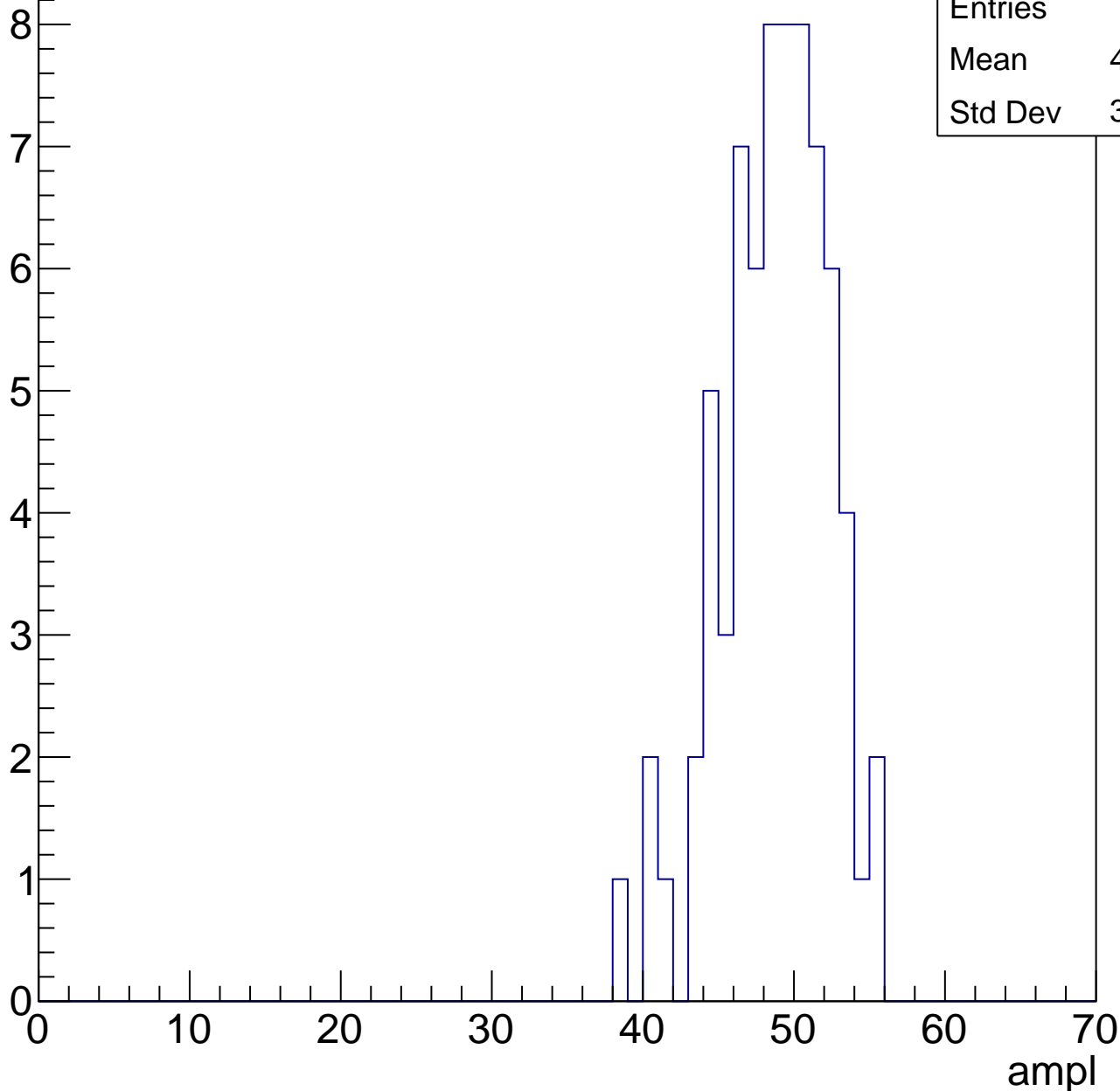


# B0L000S, U19-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	48.24
Std Dev	3.554

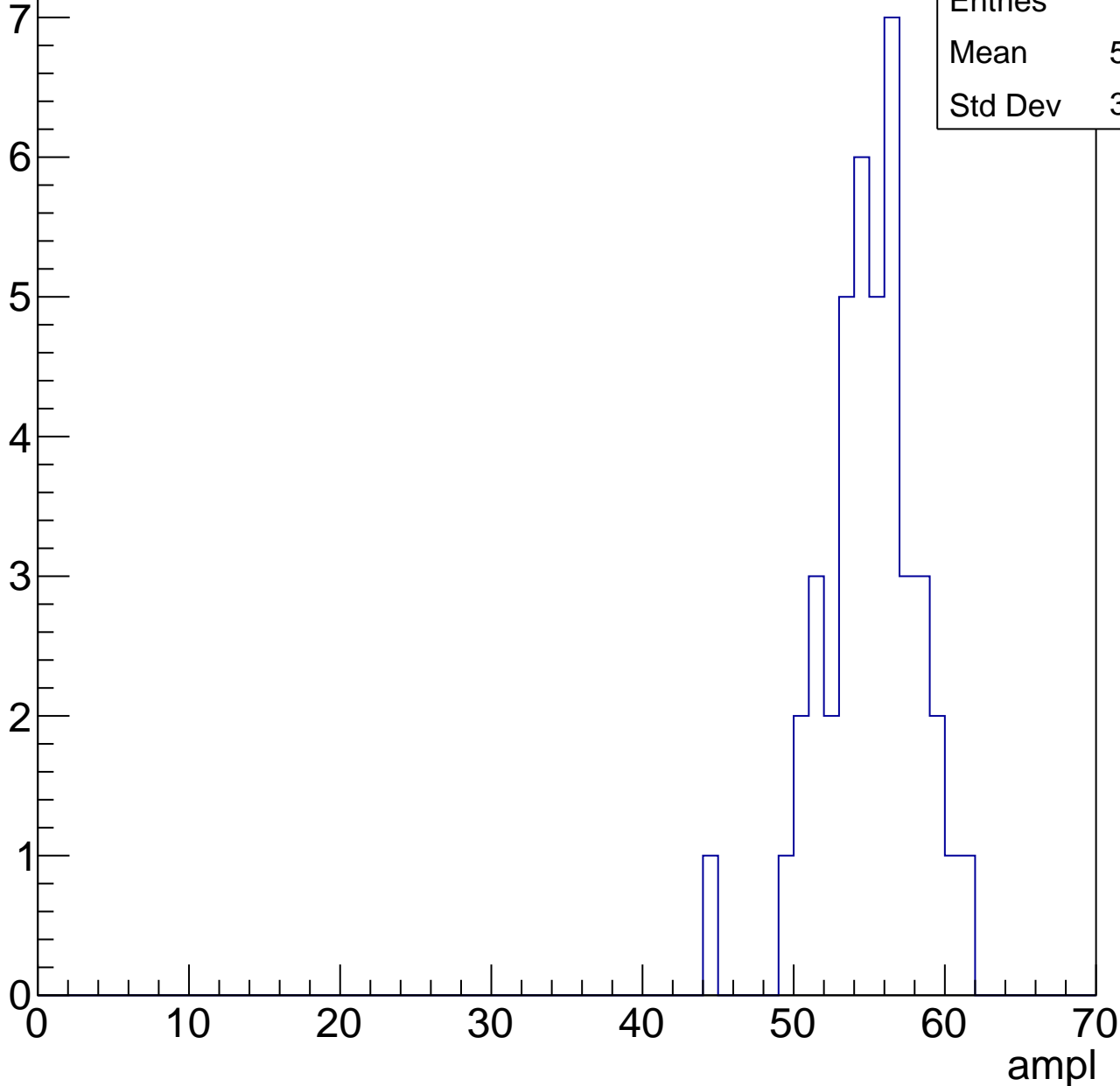


# B0L000S, U19-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

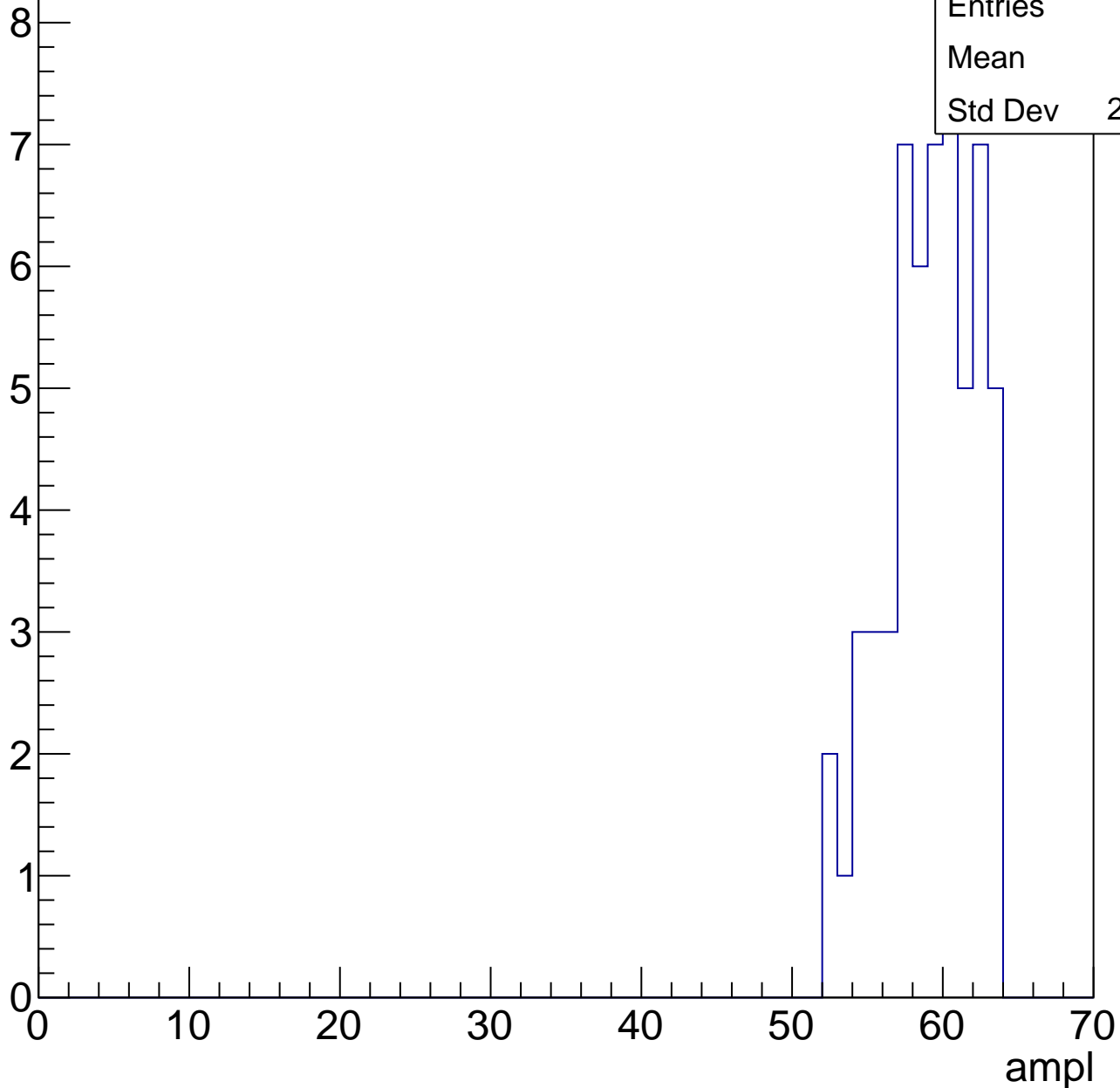
Entries	42
Mean	54.52
Std Dev	3.194



# B0L000S, U19-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



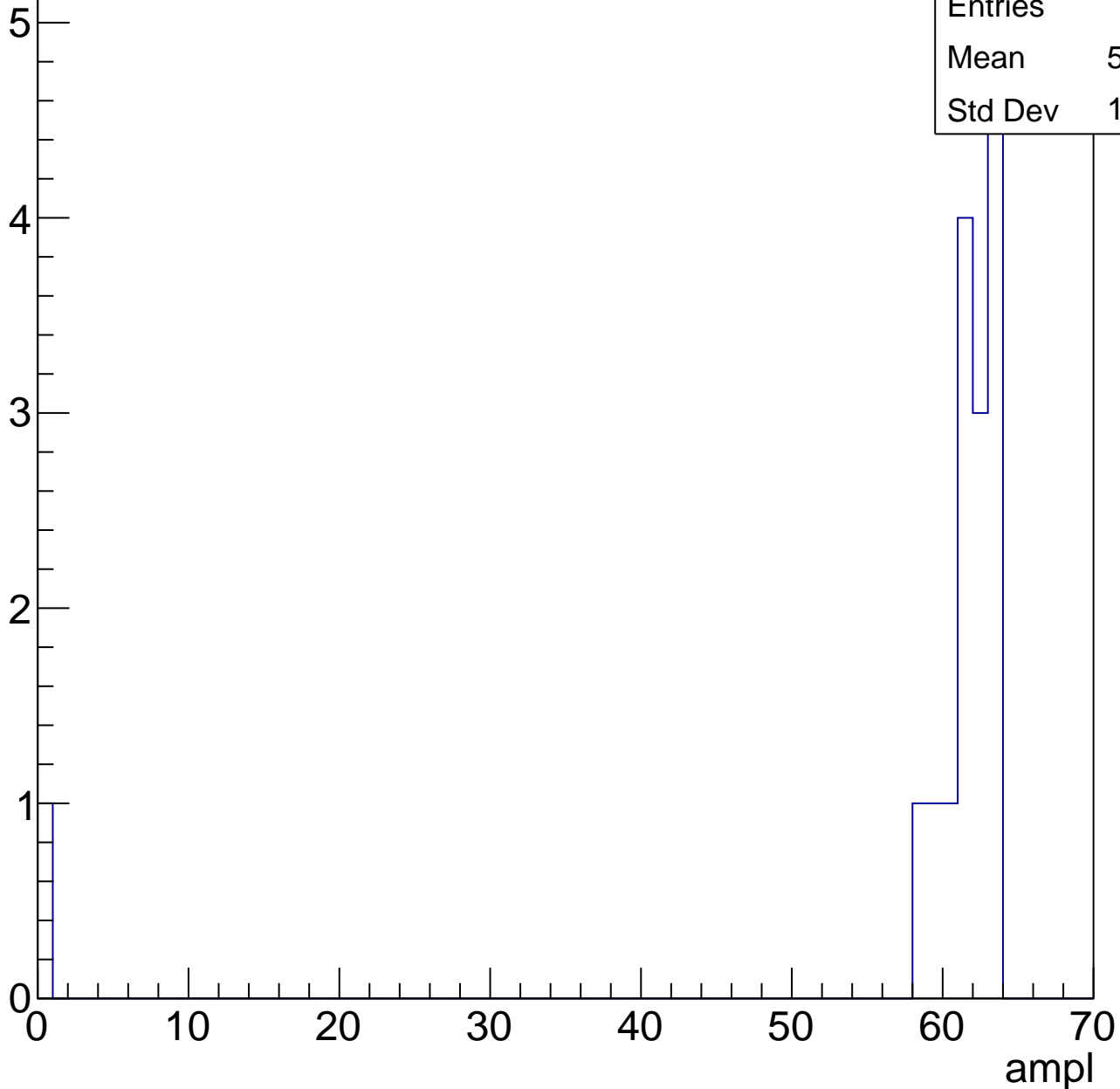
Entries	57
Mean	58.7
Std Dev	2.914

# B0L000S, U19-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.62
Std Dev	14.95





# B0L000S, U19-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch45, adc0

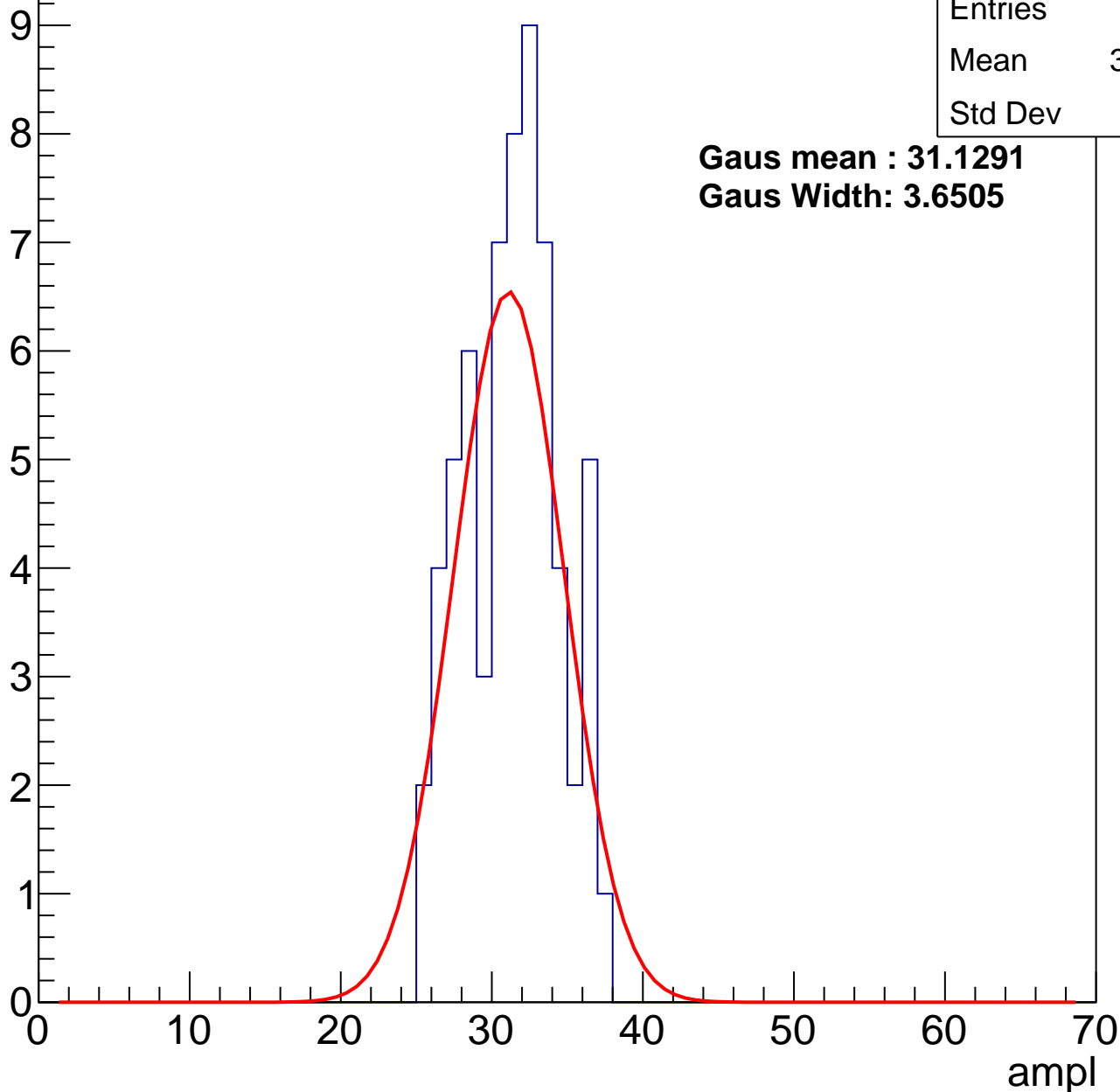
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.86
Std Dev	3.06

**Gaus mean : 31.1291**

**Gaus Width: 3.6505**



# B0L000S, U19-ch45, adc1

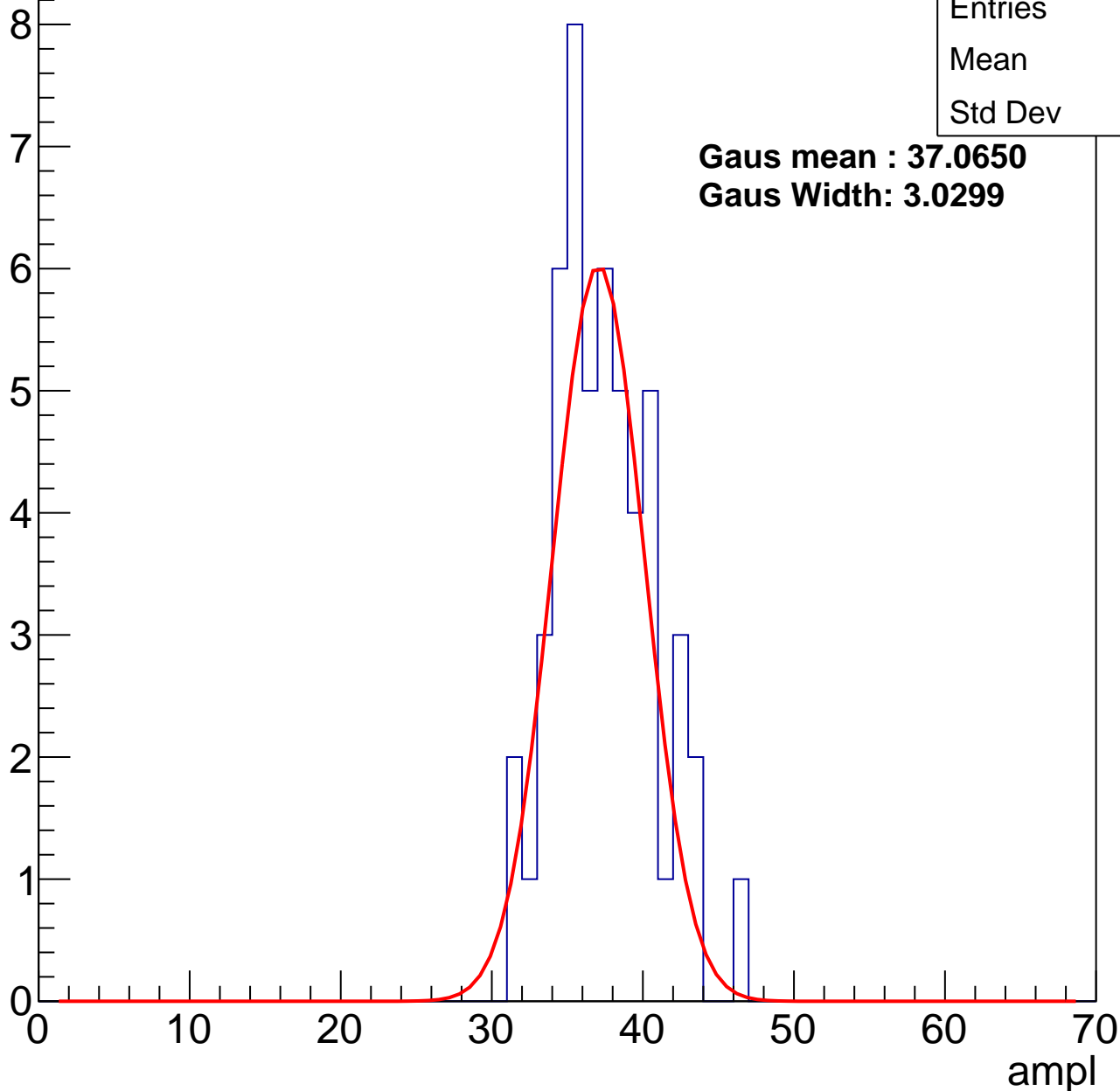
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	37
Std Dev	3.24

**Gaus mean : 37.0650**

**Gaus Width: 3.0299**



# B0L000S, U19-ch45, adc2

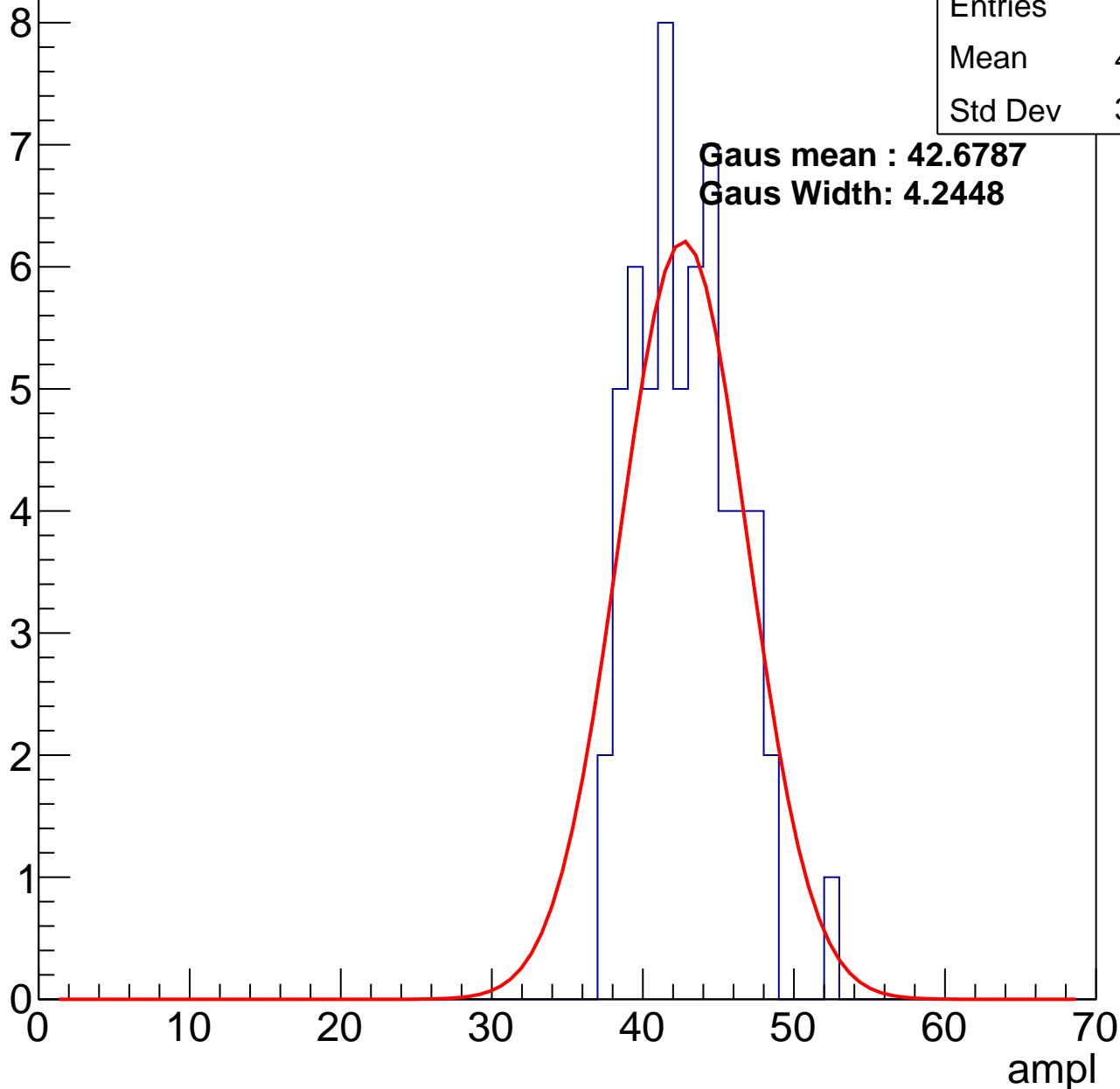
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.41
Std Dev	3.211

**Gaus mean : 42.6787**

**Gaus Width: 4.2448**

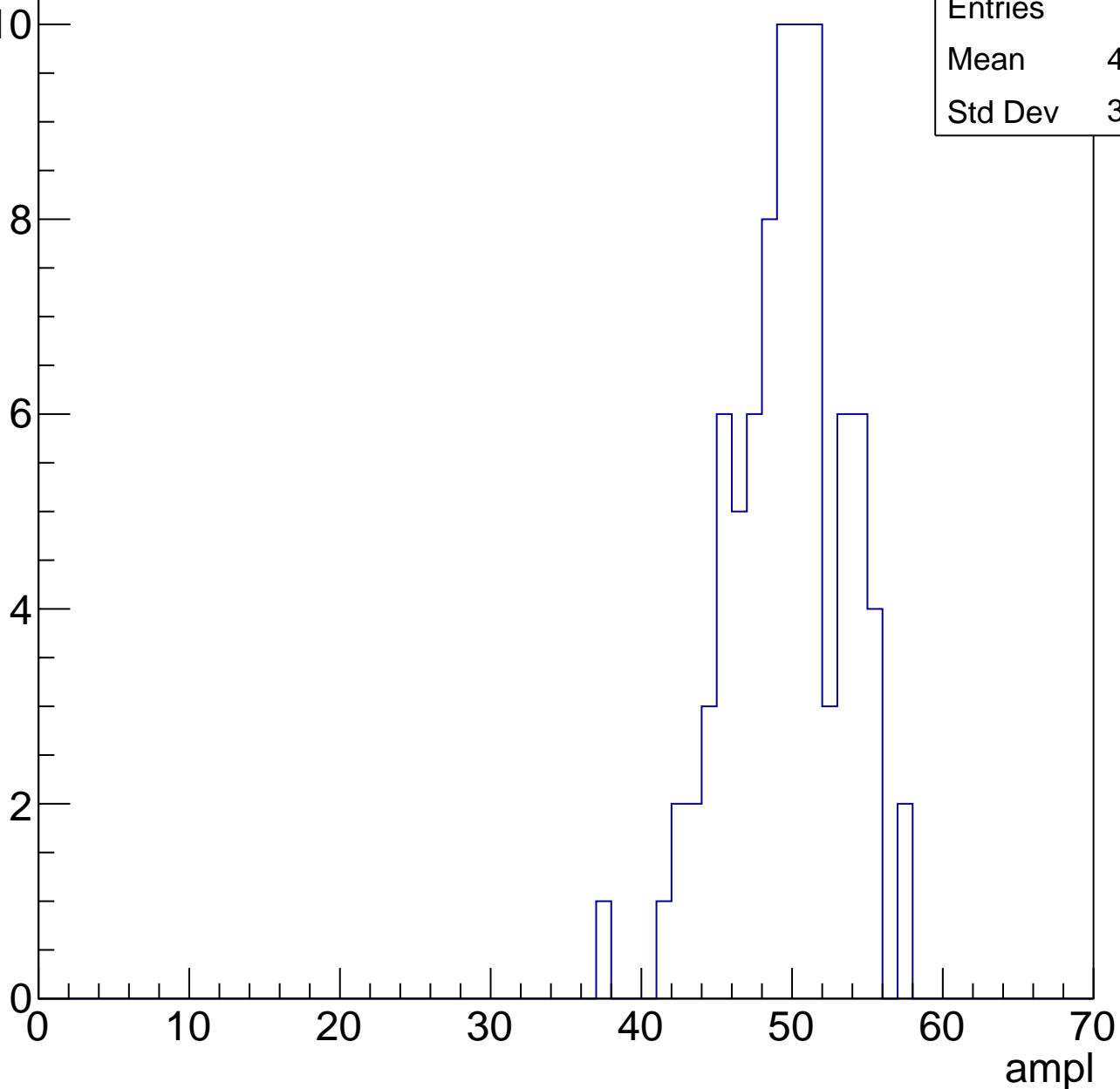


# B0L000S, U19-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

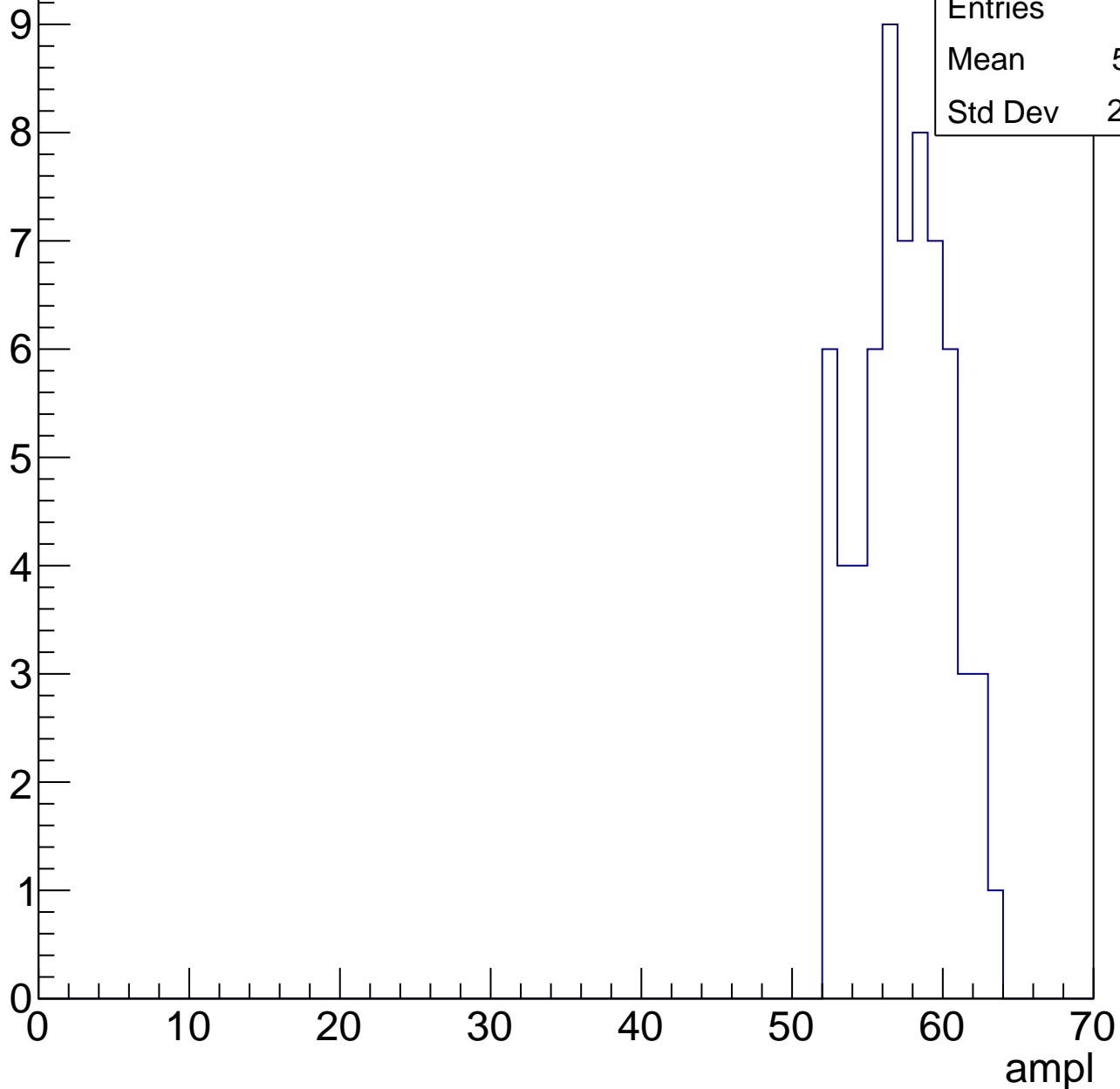
Entries	85
Mean	49.15
Std Dev	3.787



# B0L000S, U19-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



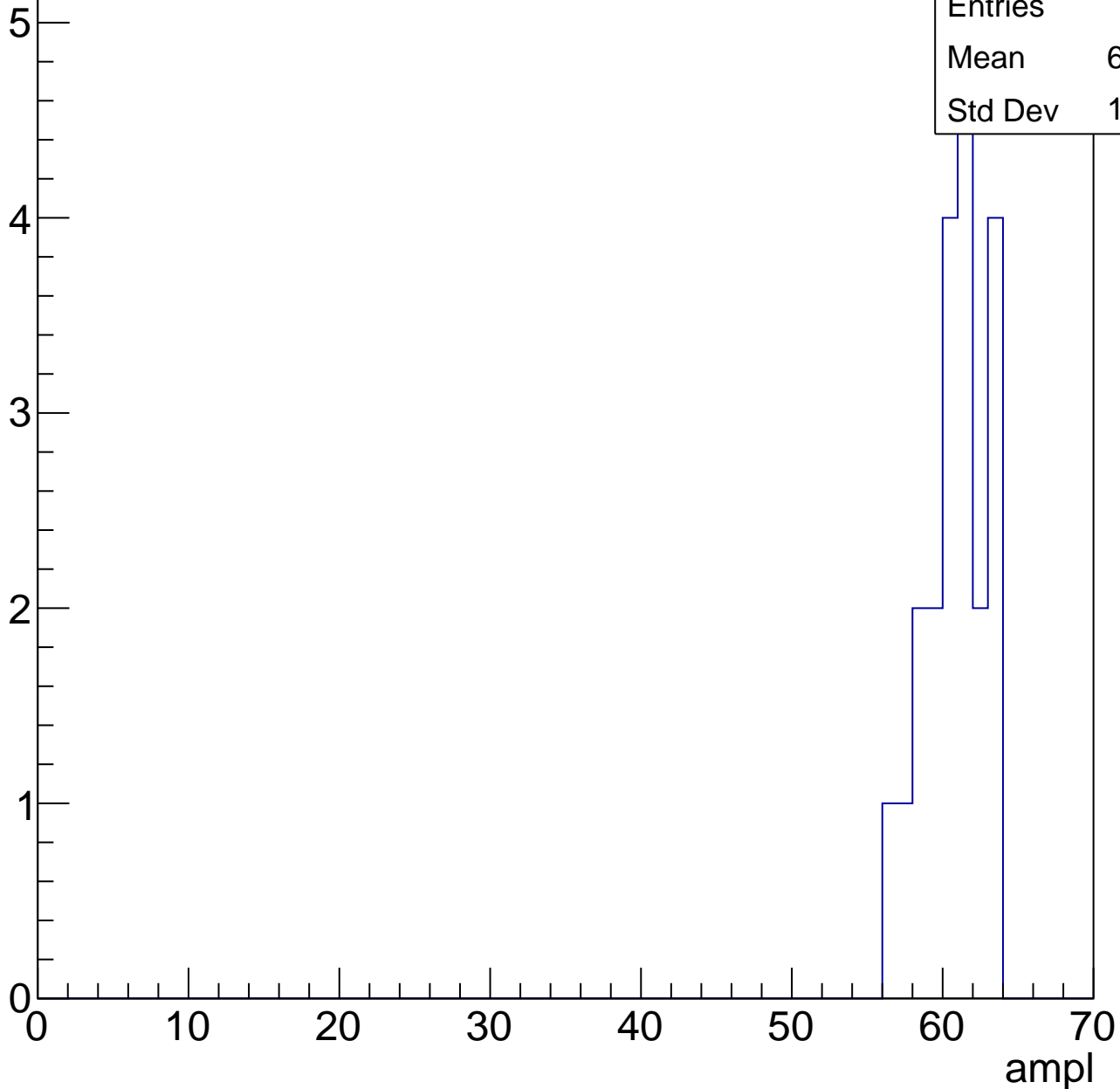
Entries	64
Mean	56.91
Std Dev	2.882

# B0L000S, U19-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	21
Mean	60.38
Std Dev	1.963

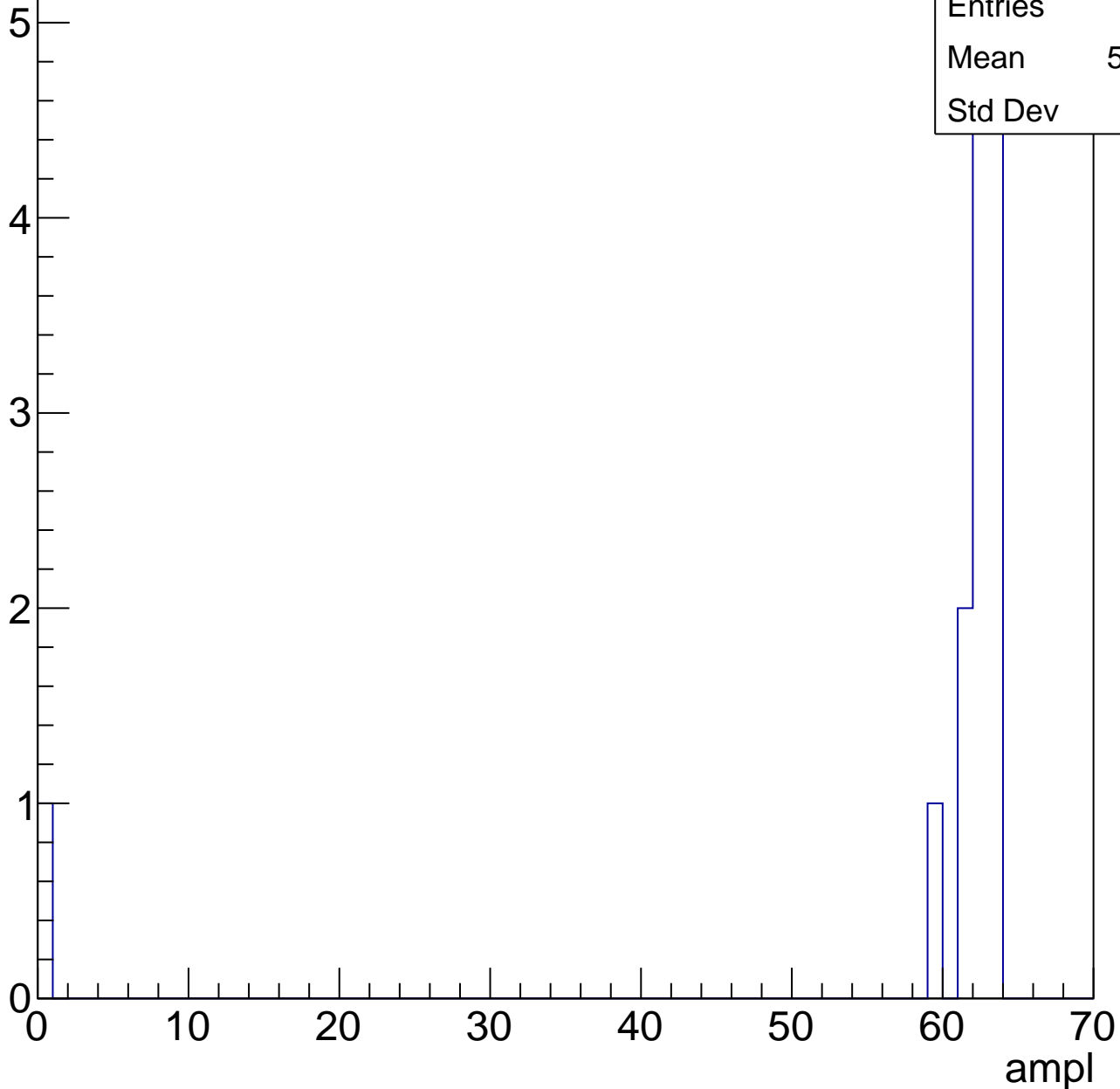


# B0L000S, U19-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	57.57
Std Dev	16





# B0L000S, U19-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch46, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	29.26
Std Dev	5.563

**Gaus mean : 30.3143**

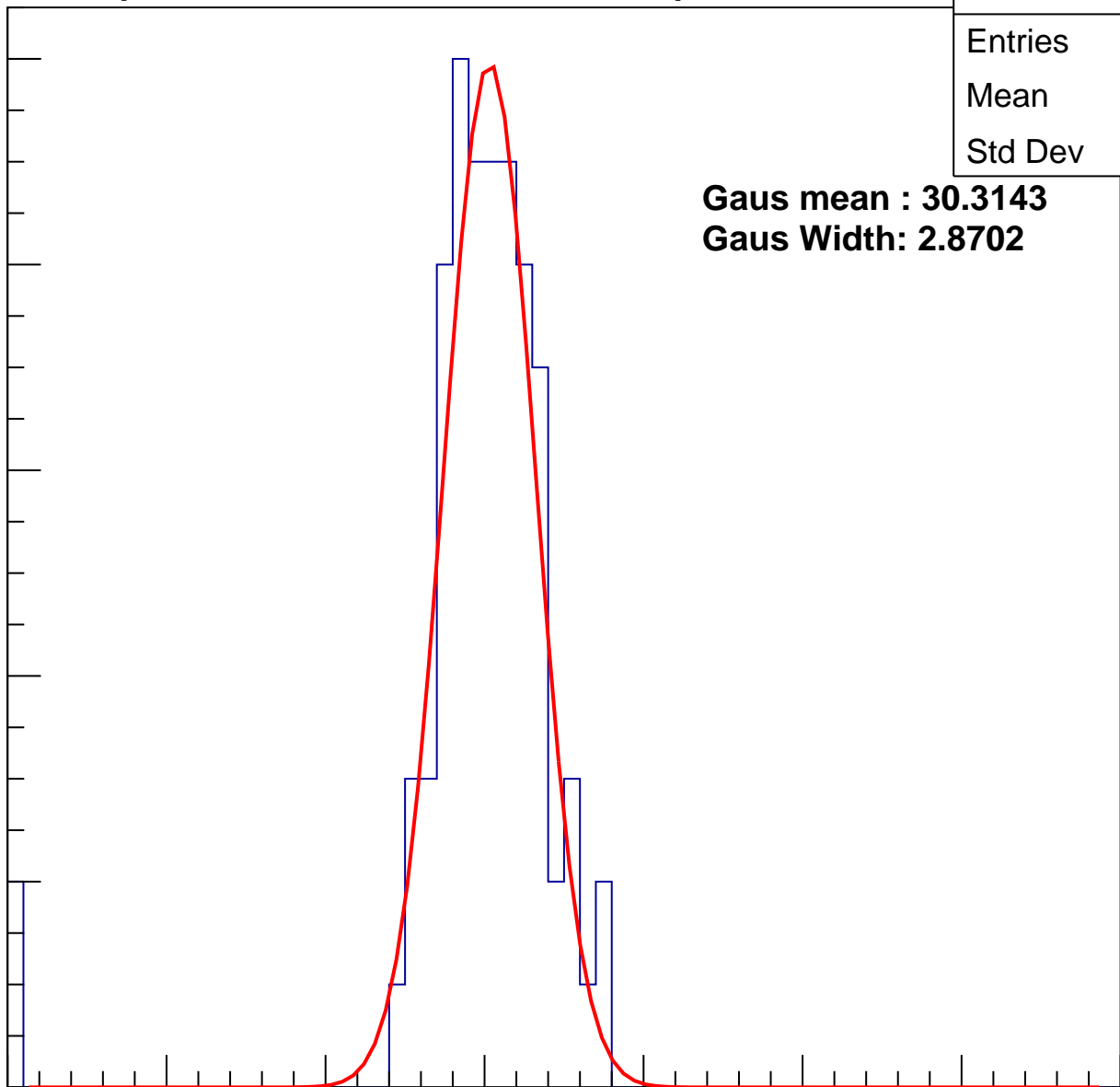
**Gaus Width: 2.8702**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch46, adc1

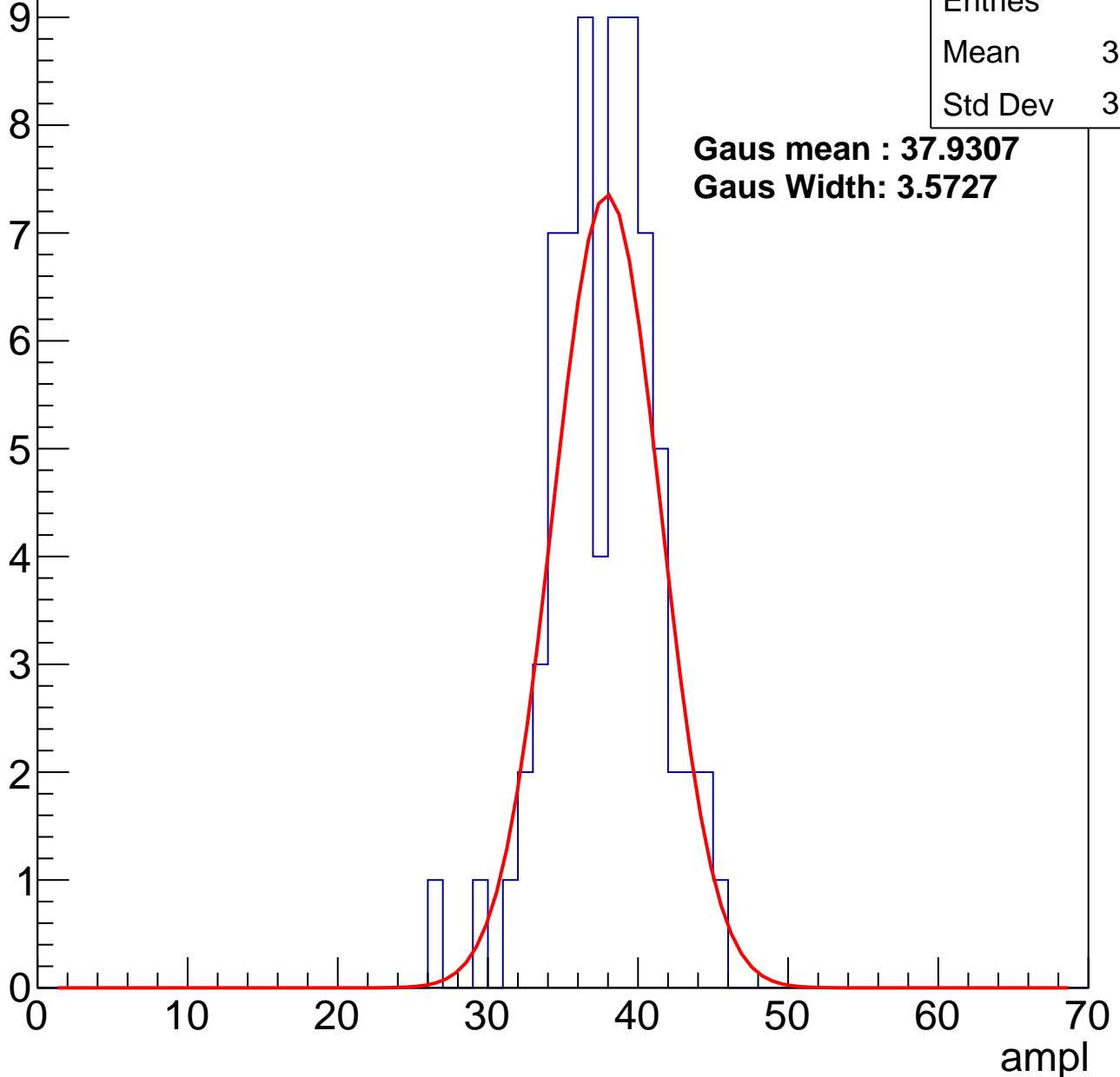
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	37.29
Std Dev	3.506

**Gaus mean : 37.9307**

**Gaus Width: 3.5727**



# B0L000S, U19-ch46, adc2

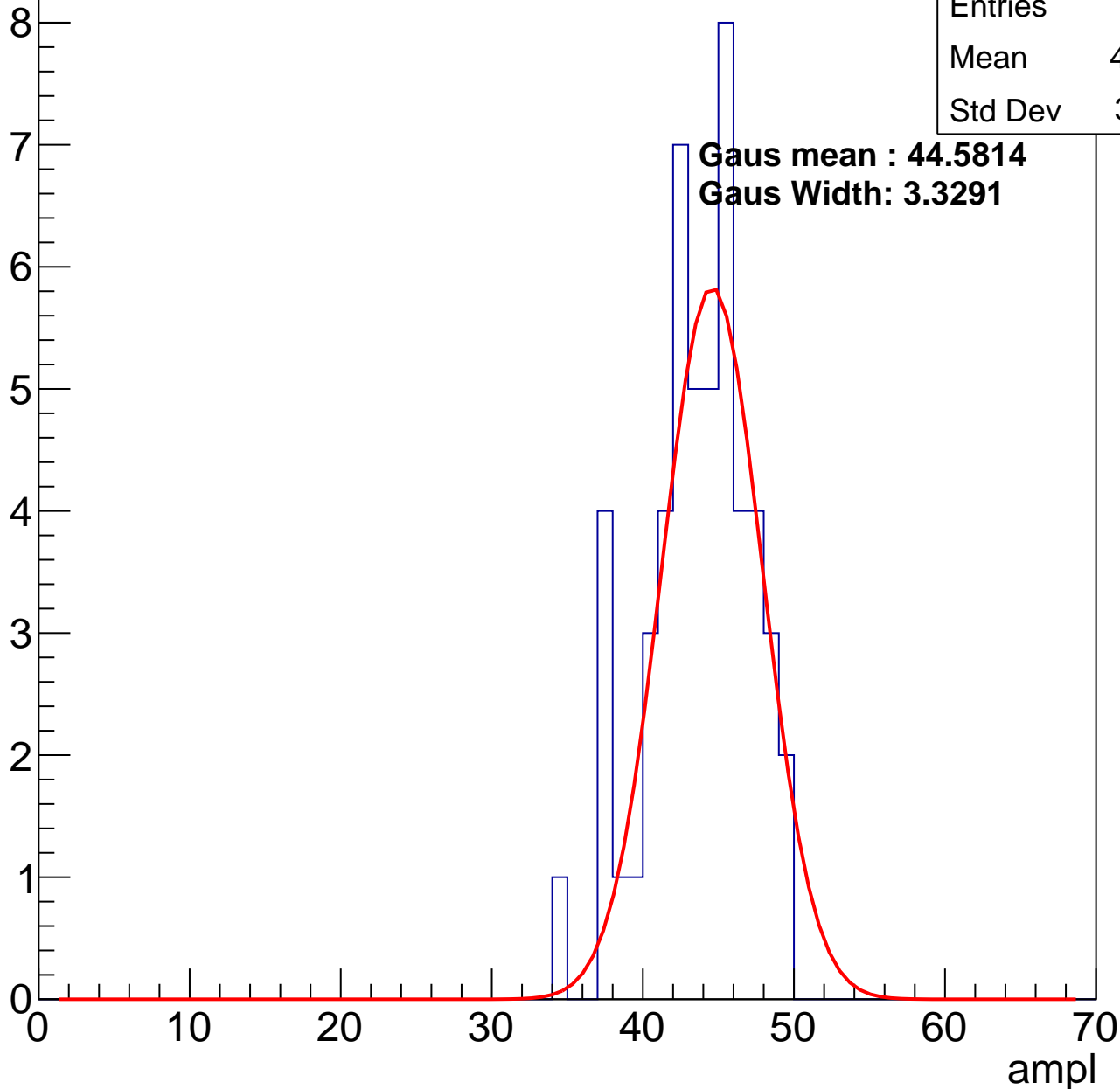
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	43.19
Std Dev	3.391

**Gaus mean : 44.5814**

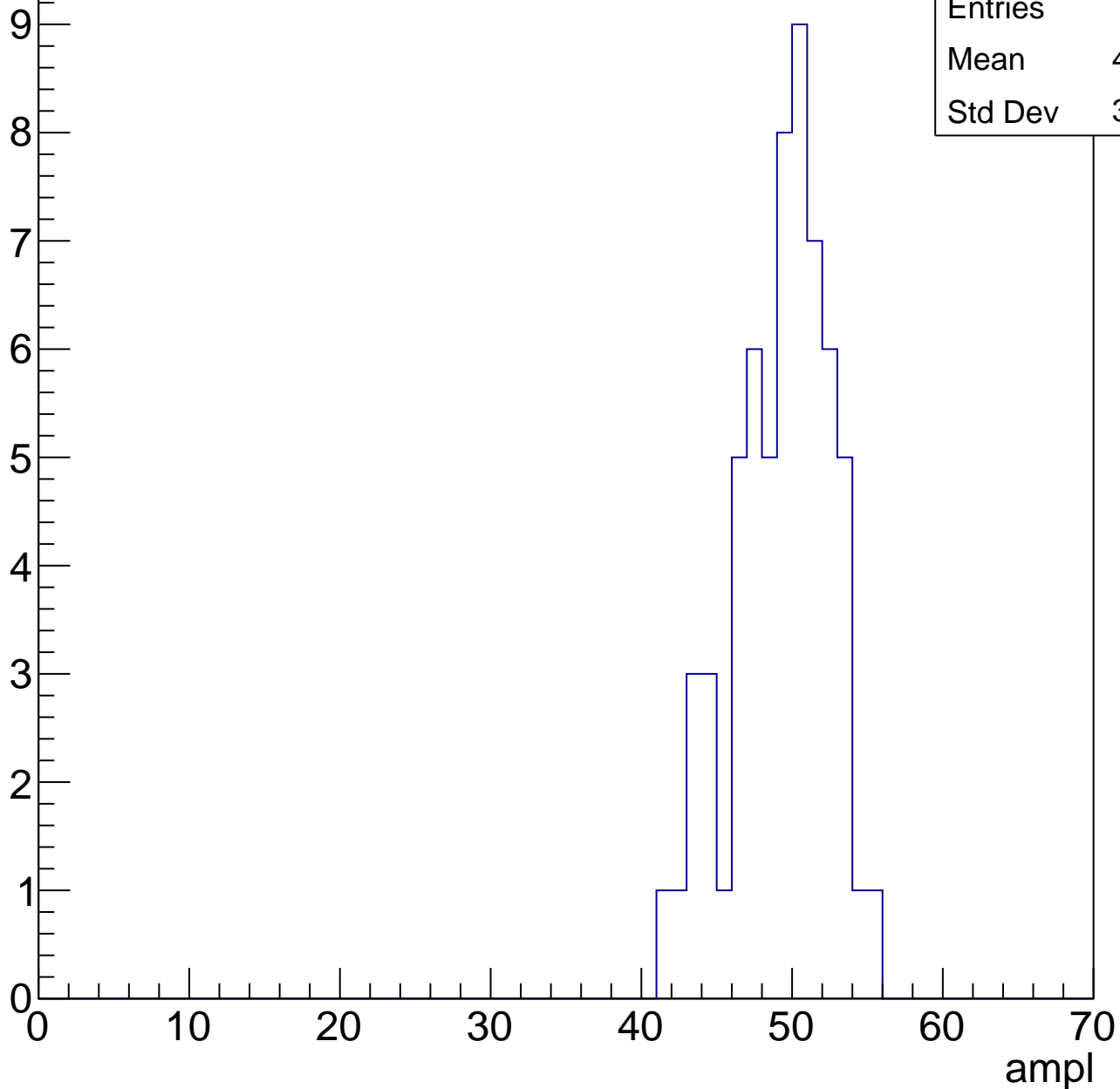
**Gaus Width: 3.3291**



# B0L000S, U19-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



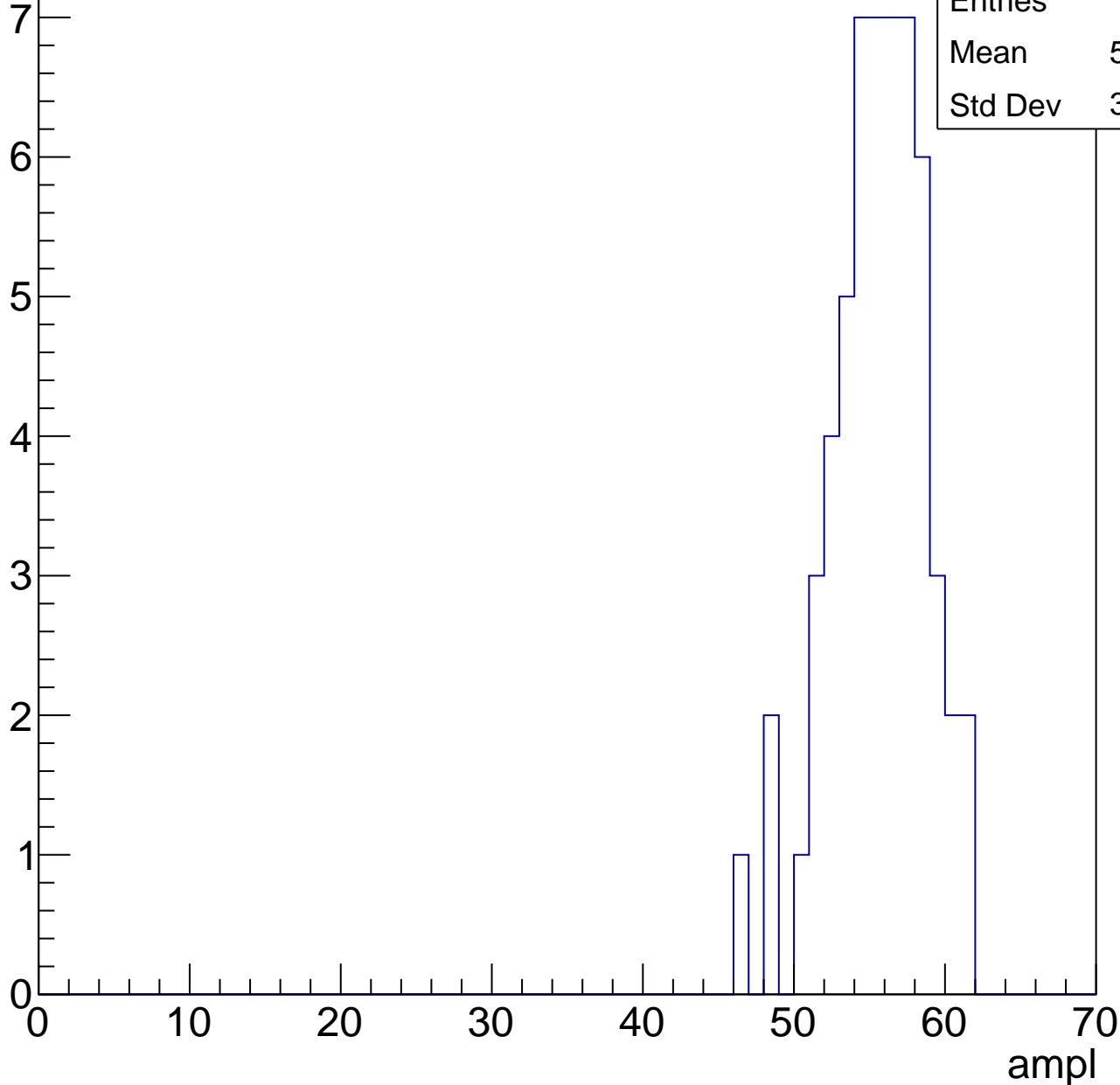
Entries	62
Mean	48.81
Std Dev	3.151

# B0L000S, U19-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

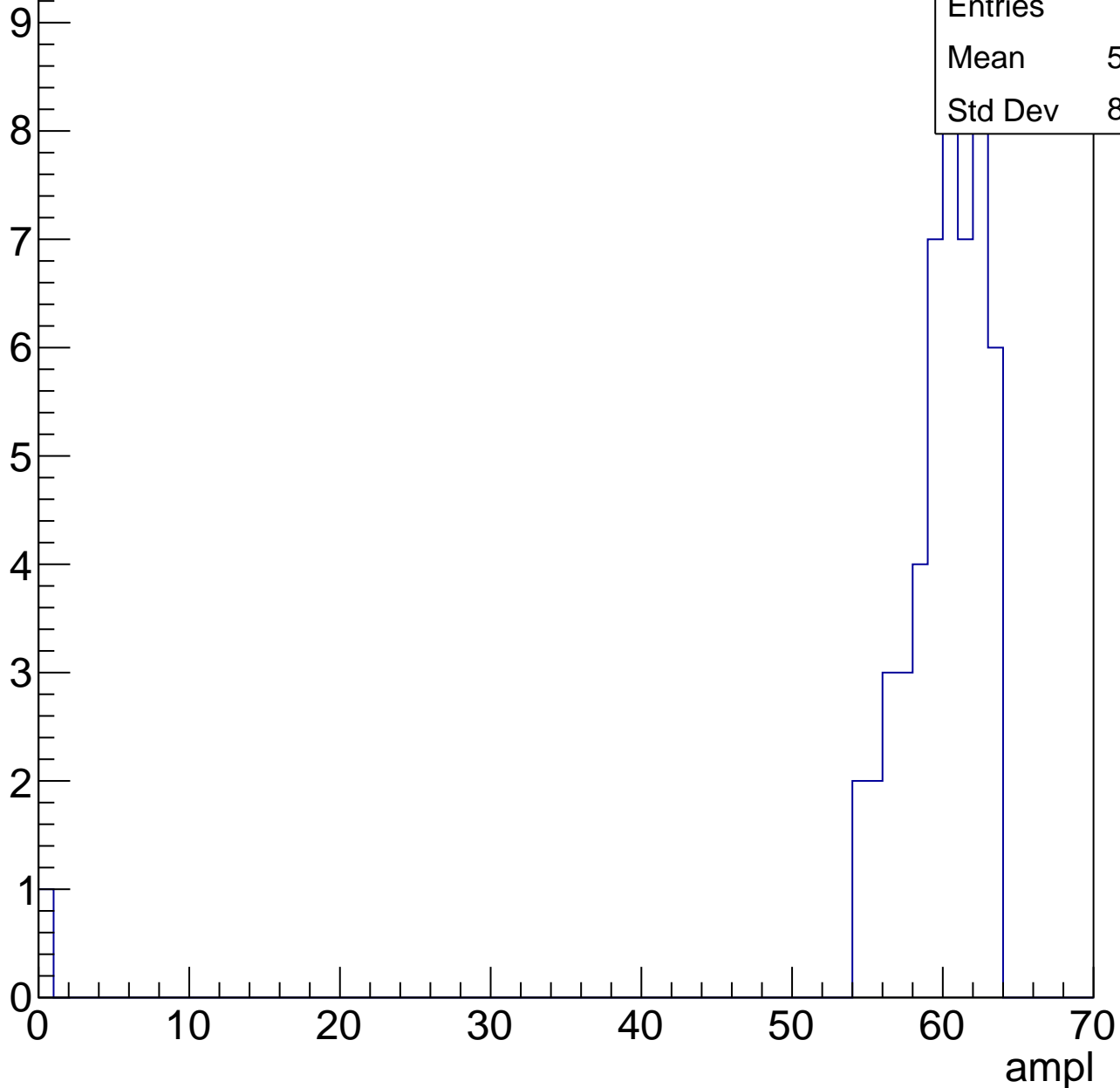
Entries	57
Mean	55.07
Std Dev	3.173



# B0L000S, U19-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

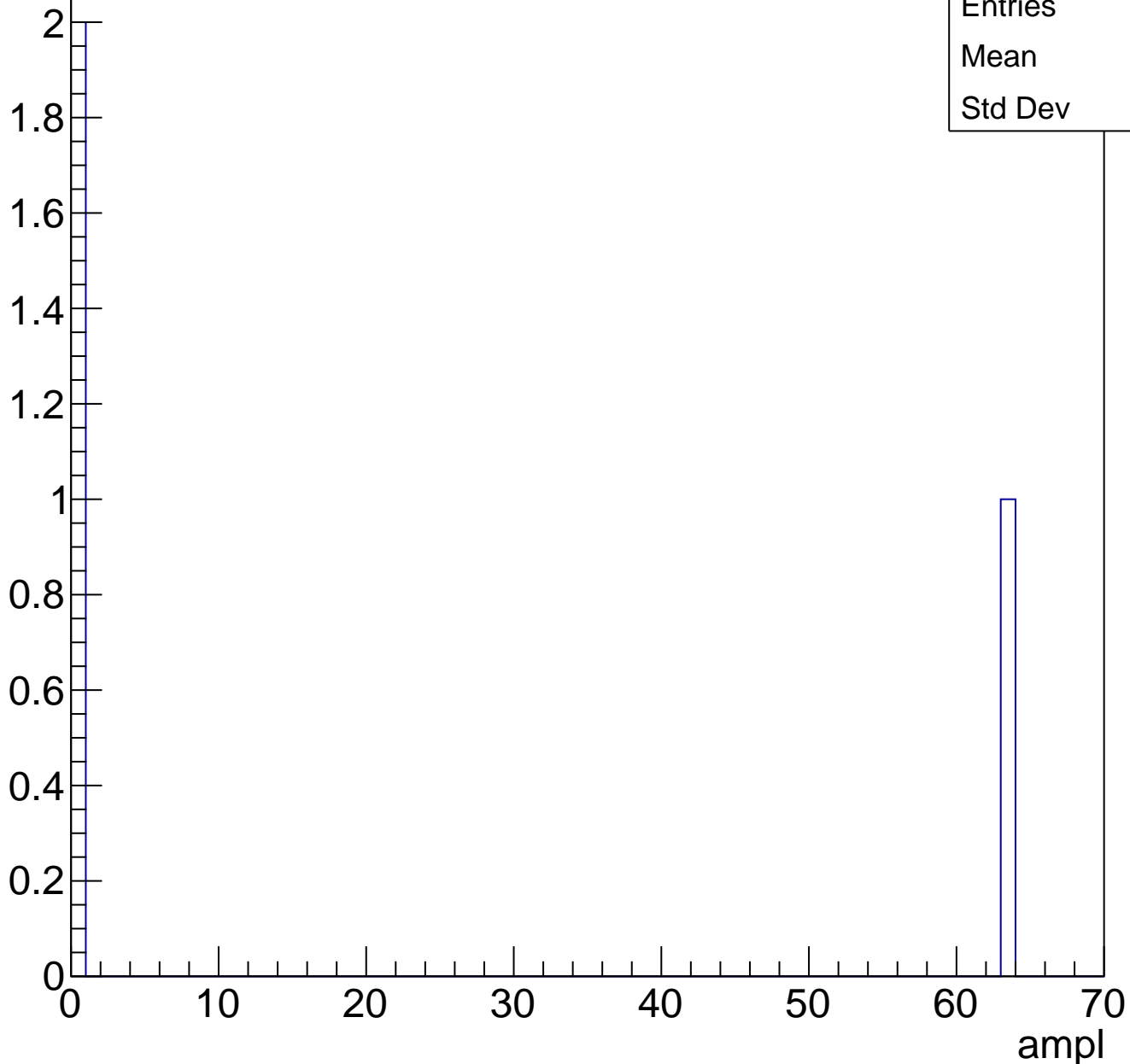




# B0L000S, U19-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

# B0L000S, U19-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	27.92
Std Dev	4.384

**Gaus mean : 29.2626**

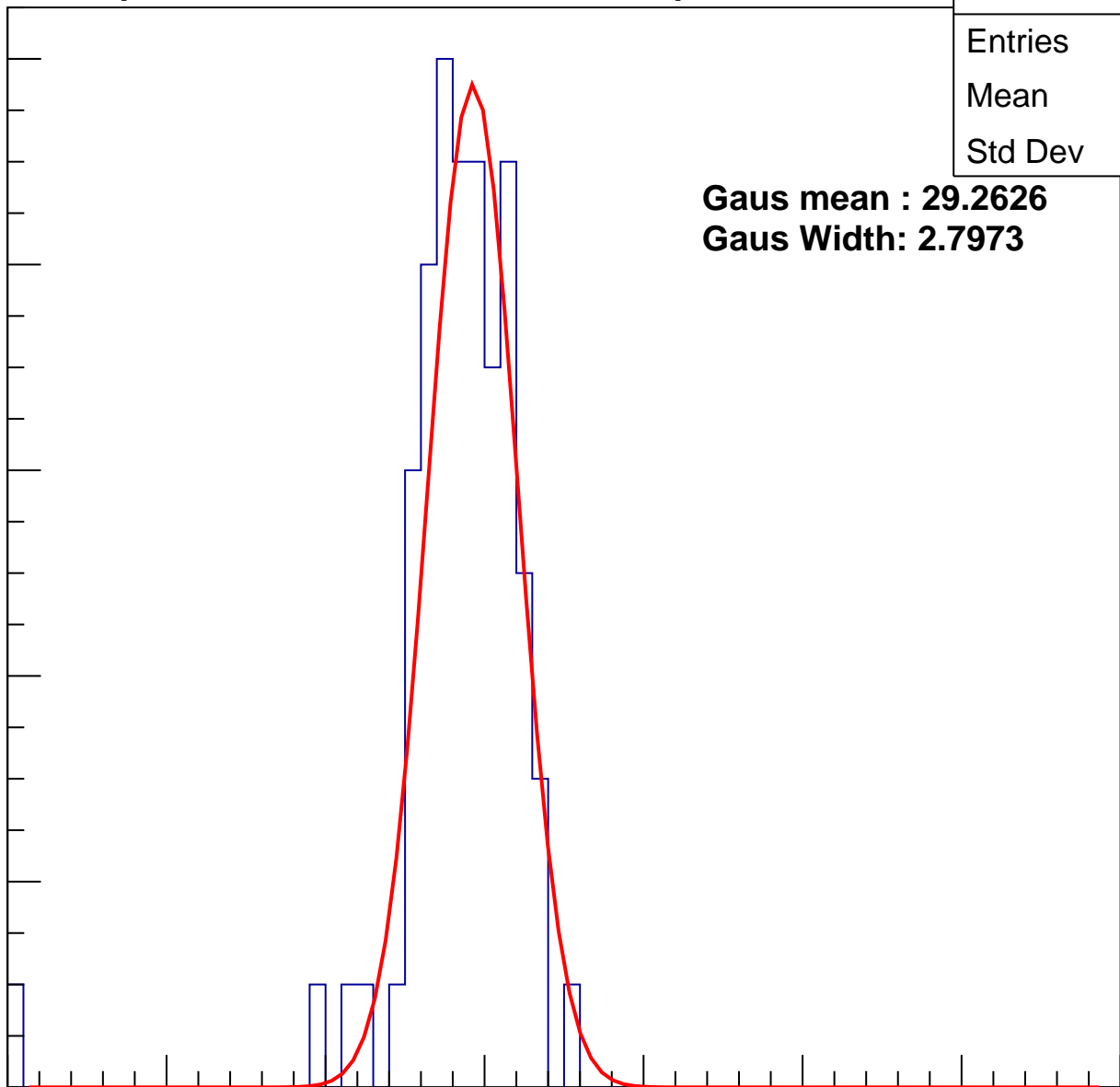
**Gaus Width: 2.7973**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



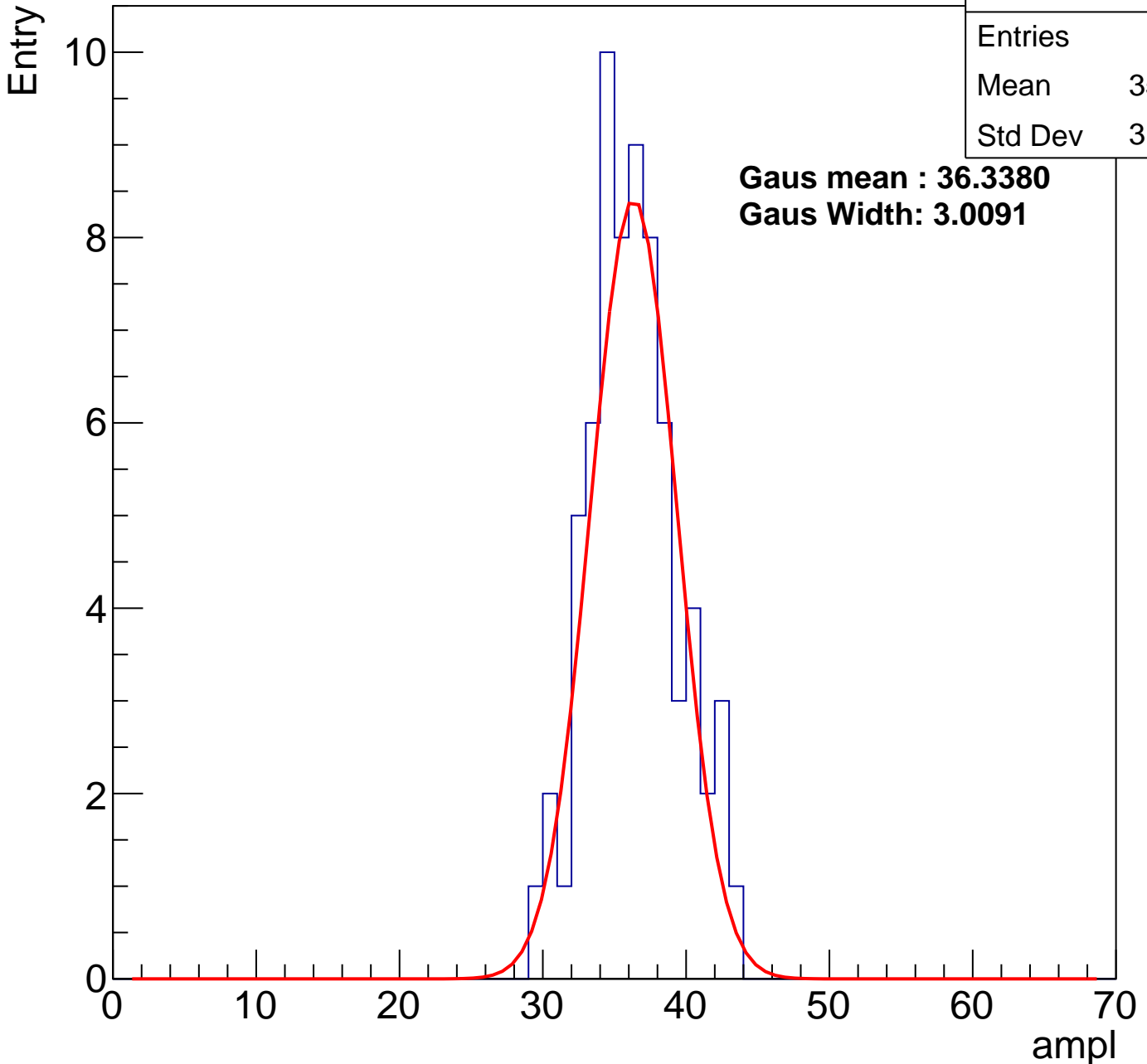
# B0L000S, U19-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	35.86
Std Dev	3.085

**Gaus mean : 36.3380**

**Gaus Width: 3.0091**



# B0L000S, U19-ch47, adc2

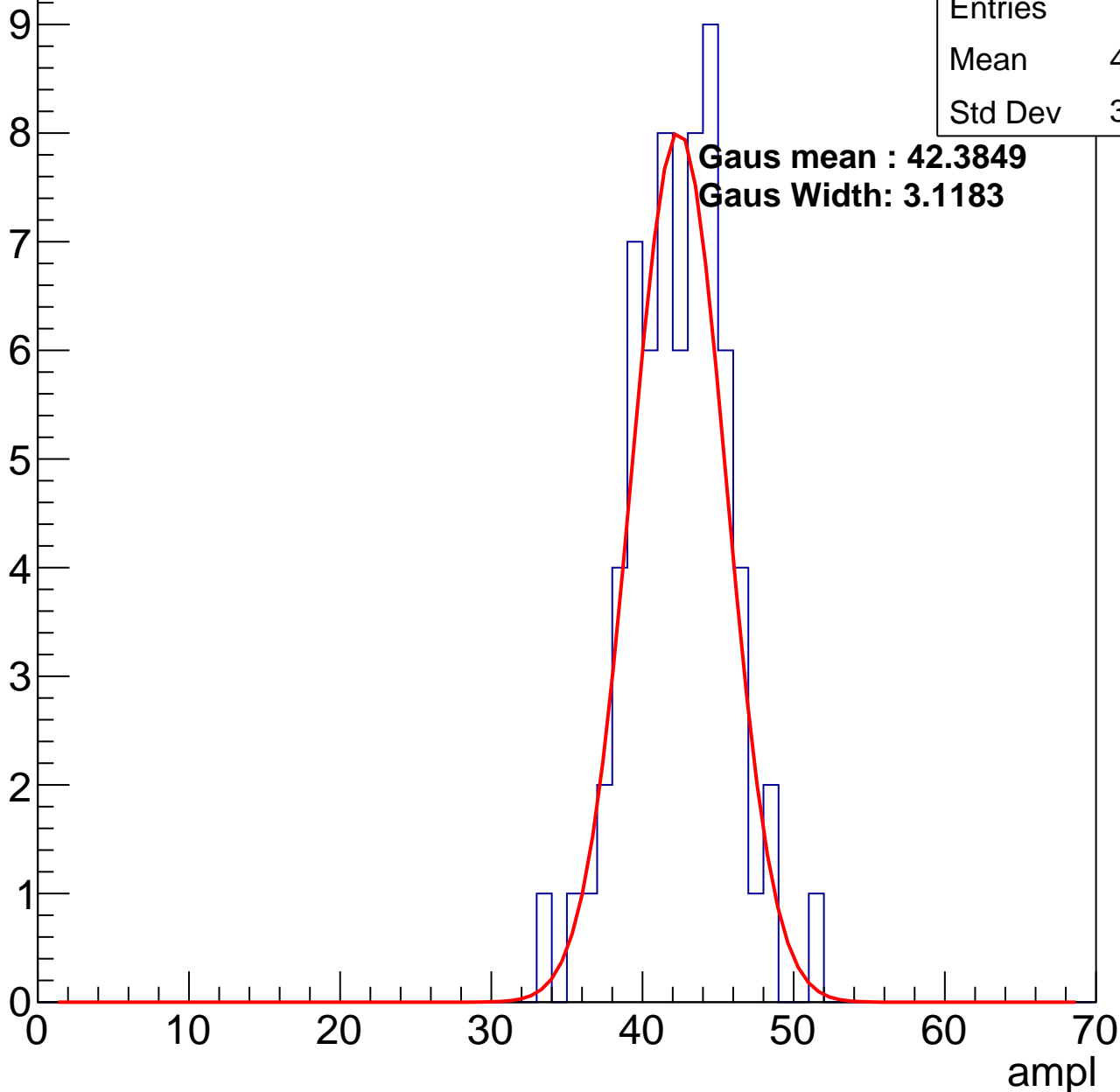
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	41.96
Std Dev	3.266

**Gaus mean : 42.3849**

**Gaus Width: 3.1183**

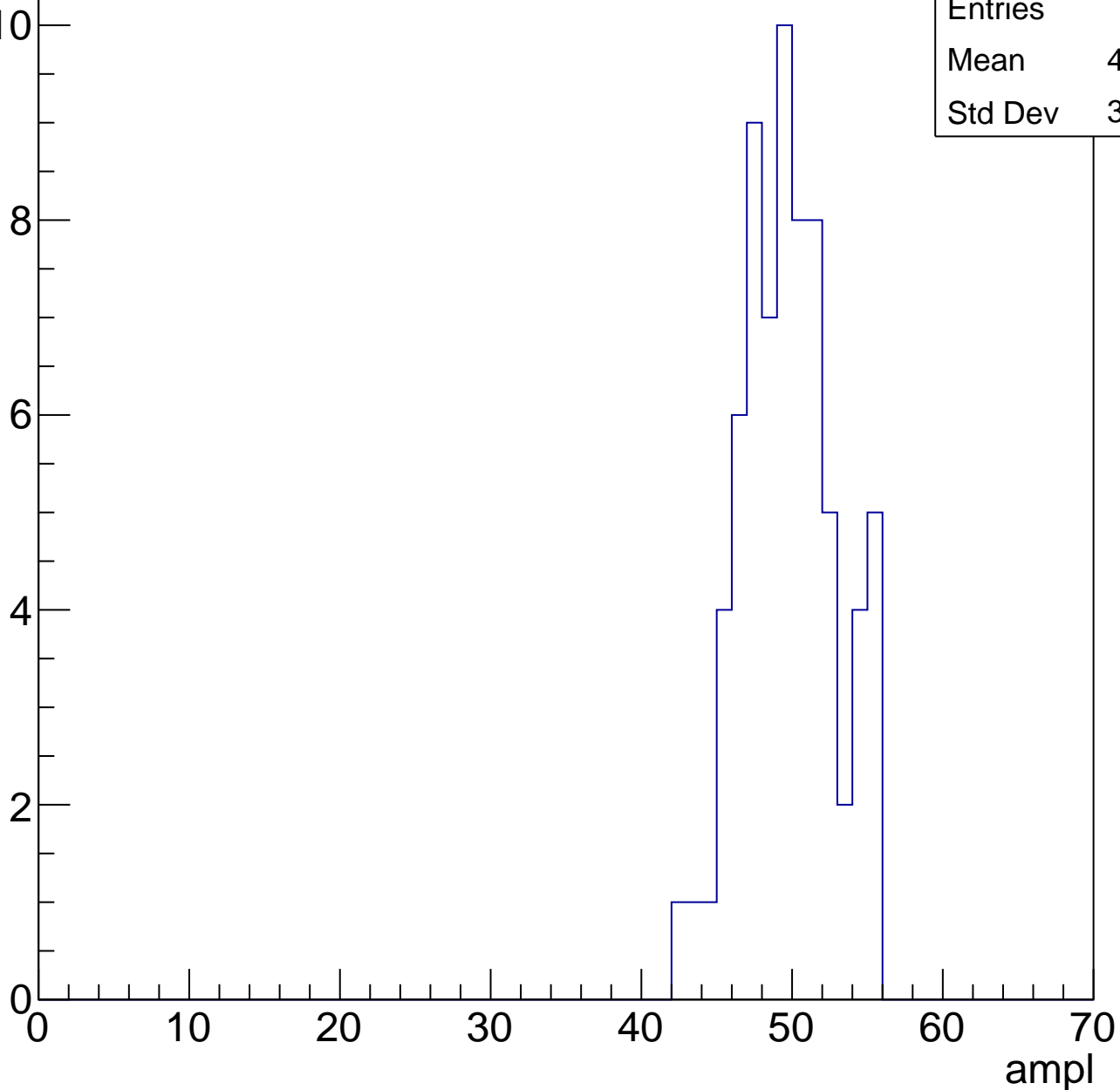


# B0L000S, U19-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

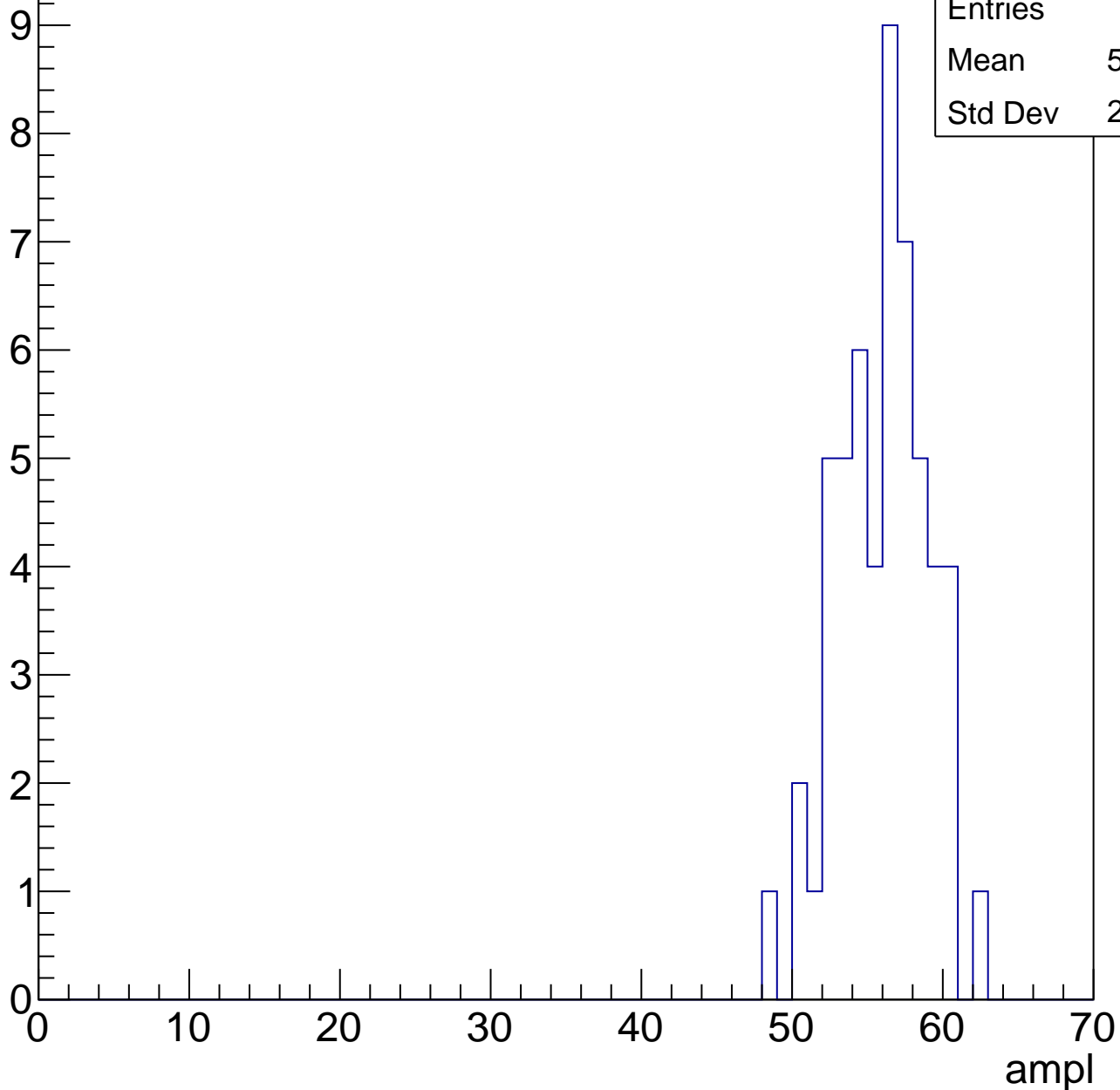
Entries	71
Mean	49.28
Std Dev	3.054



# B0L000S, U19-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



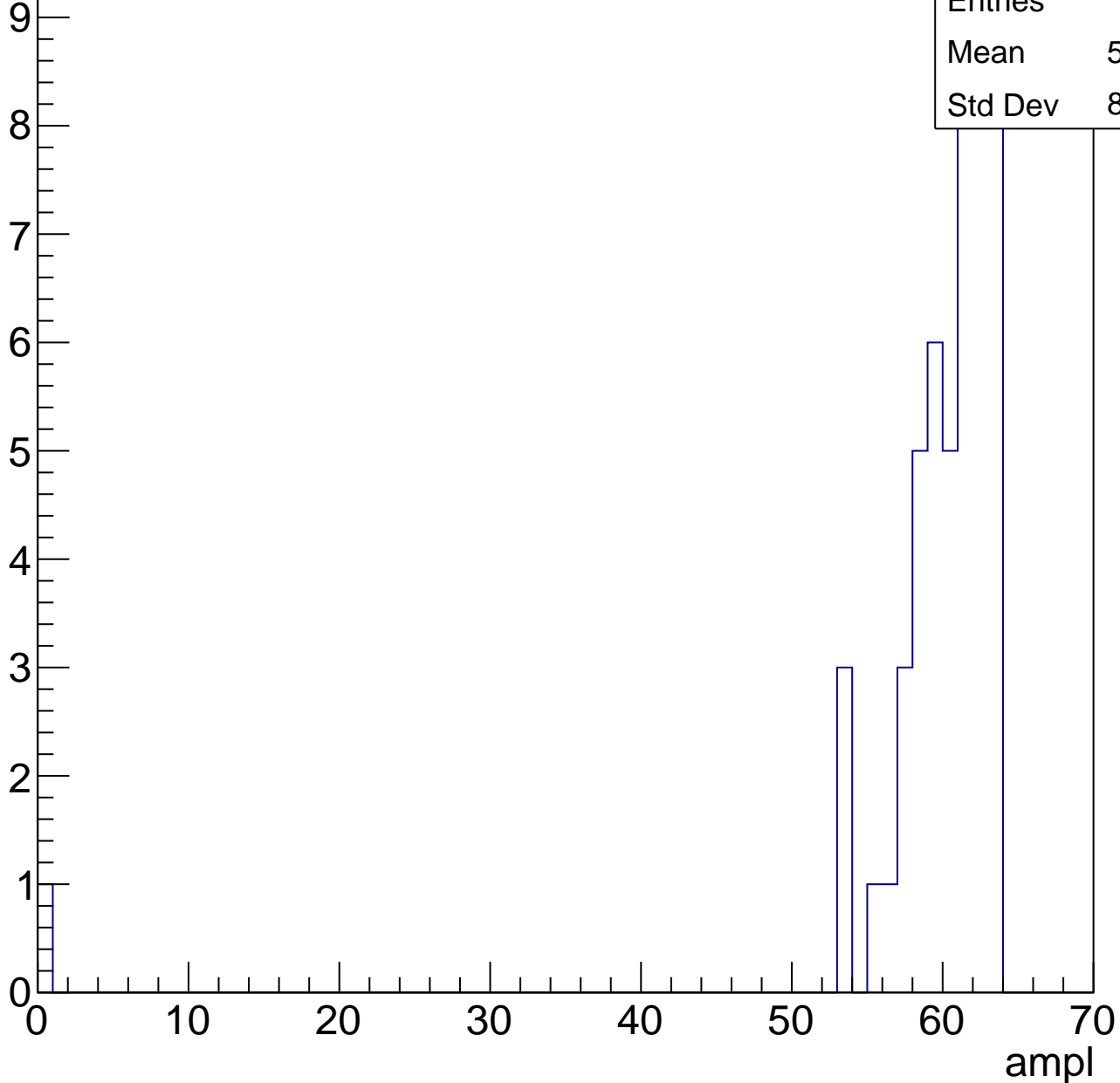
Entries	54
Mean	55.54
Std Dev	2.936

# B0L000S, U19-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

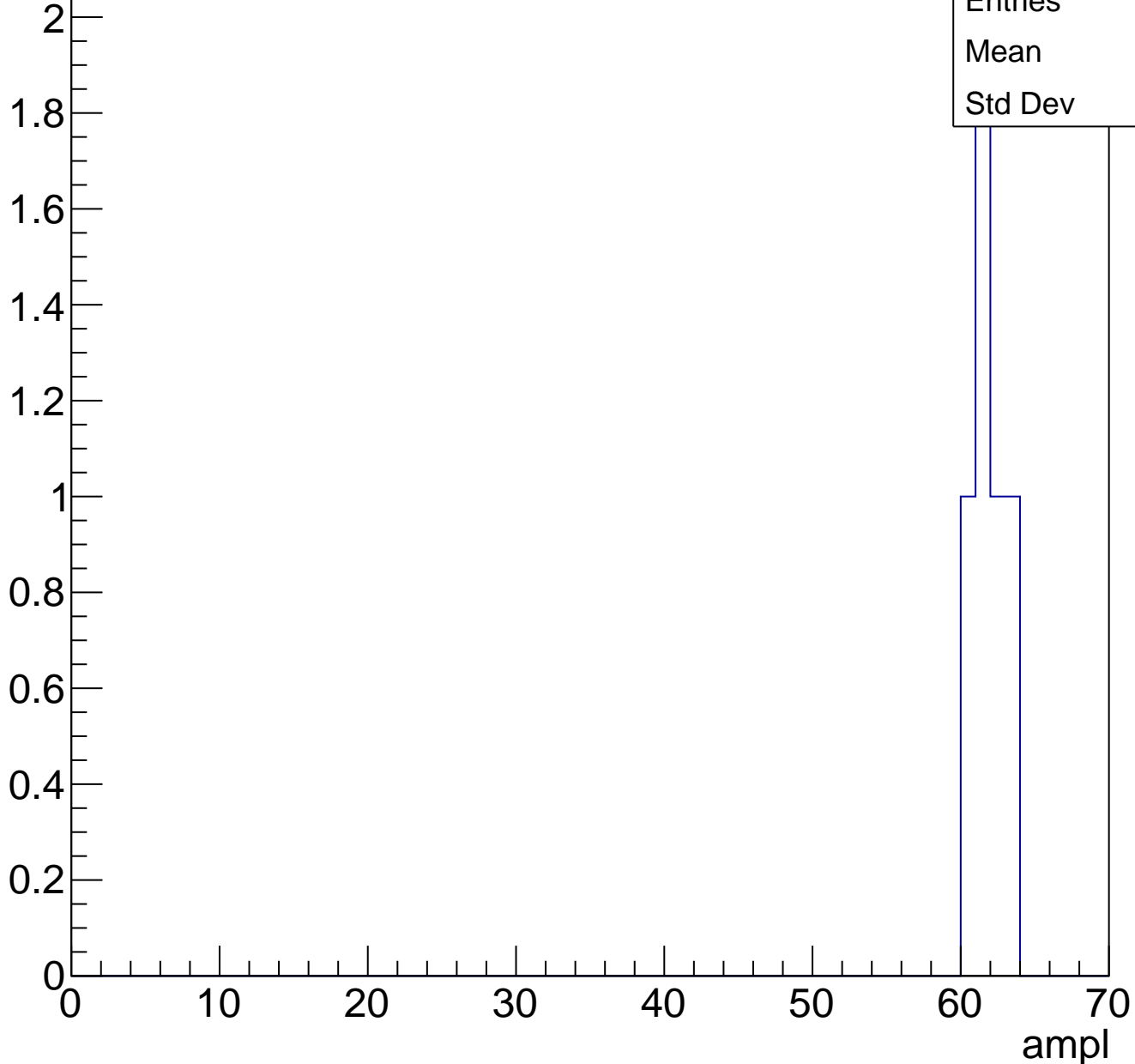
Entries	50
Mean	58.72
Std Dev	8.809



# B0L000S, U19-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	61.4
Std Dev	1.02



# B0L000S, U19-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch48, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	29.41
Std Dev	3.153

**Gaus mean : 29.9669**

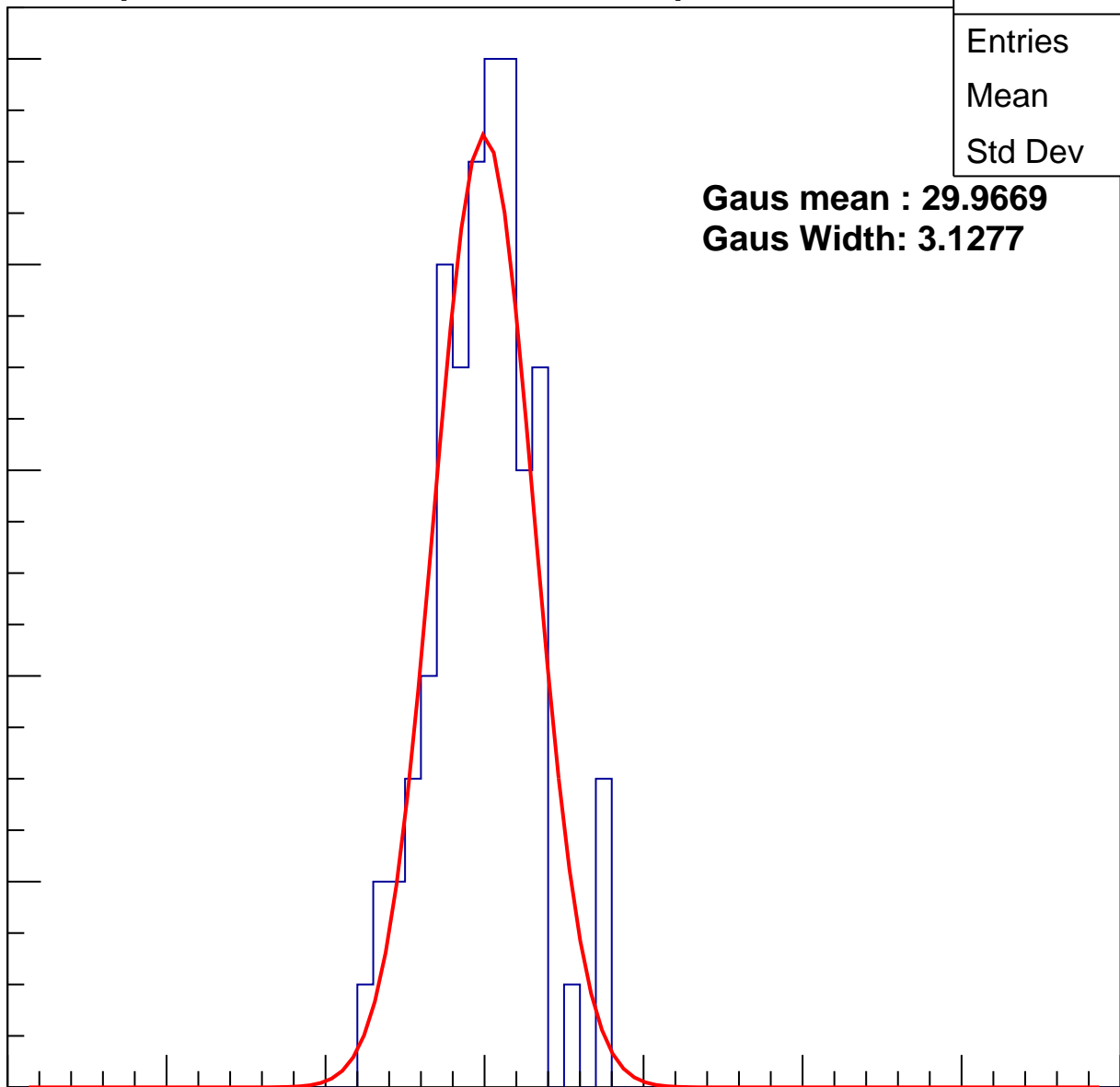
**Gaus Width: 3.1277**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch48, adc1

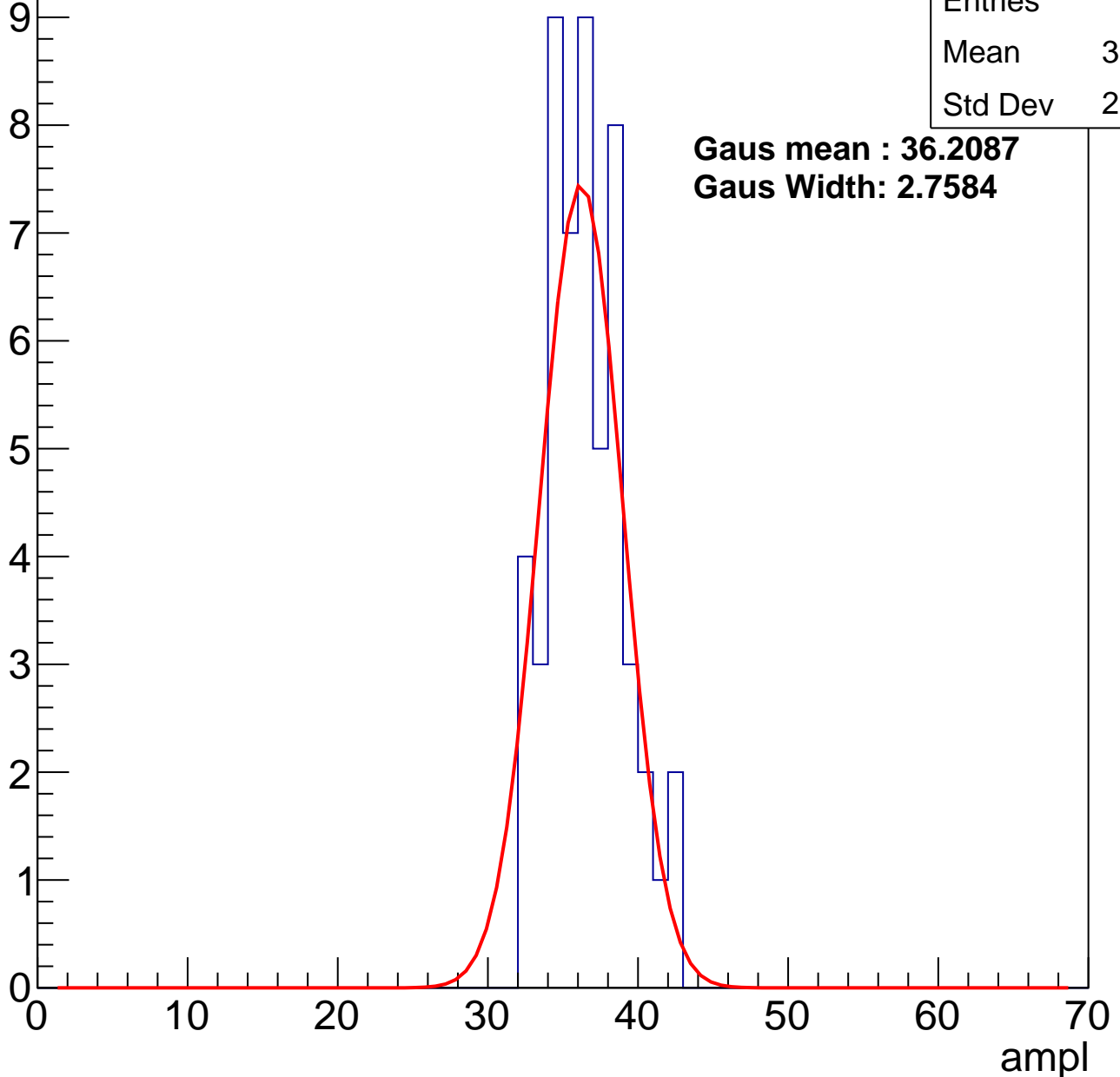
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.09
Std Dev	2.482

**Gaus mean : 36.2087**

**Gaus Width: 2.7584**



# B0L000S, U19-ch48, adc2

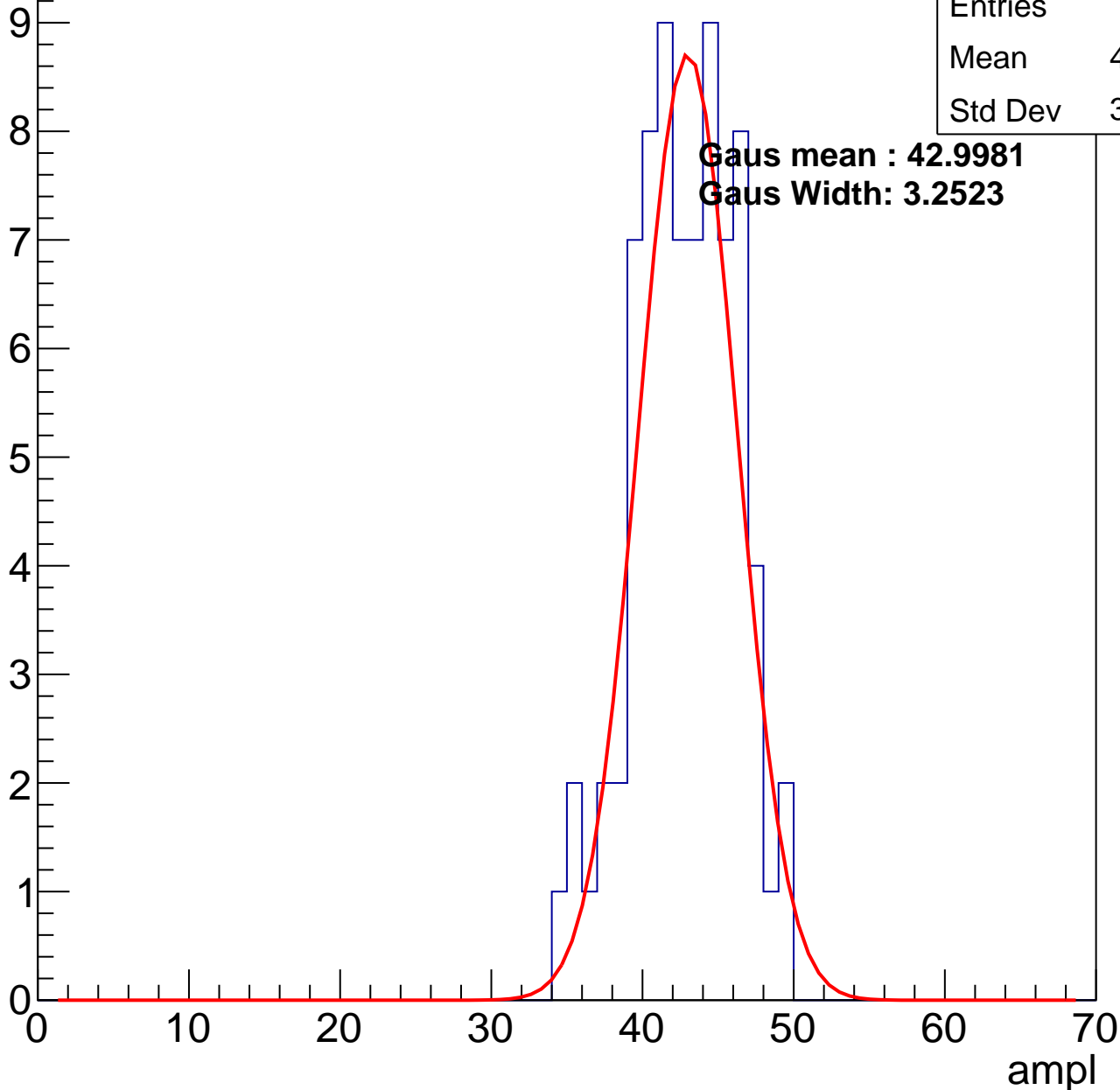
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	42.34
Std Dev	3.309

**Gaus mean : 42.9981**

**Gaus Width: 3.2523**

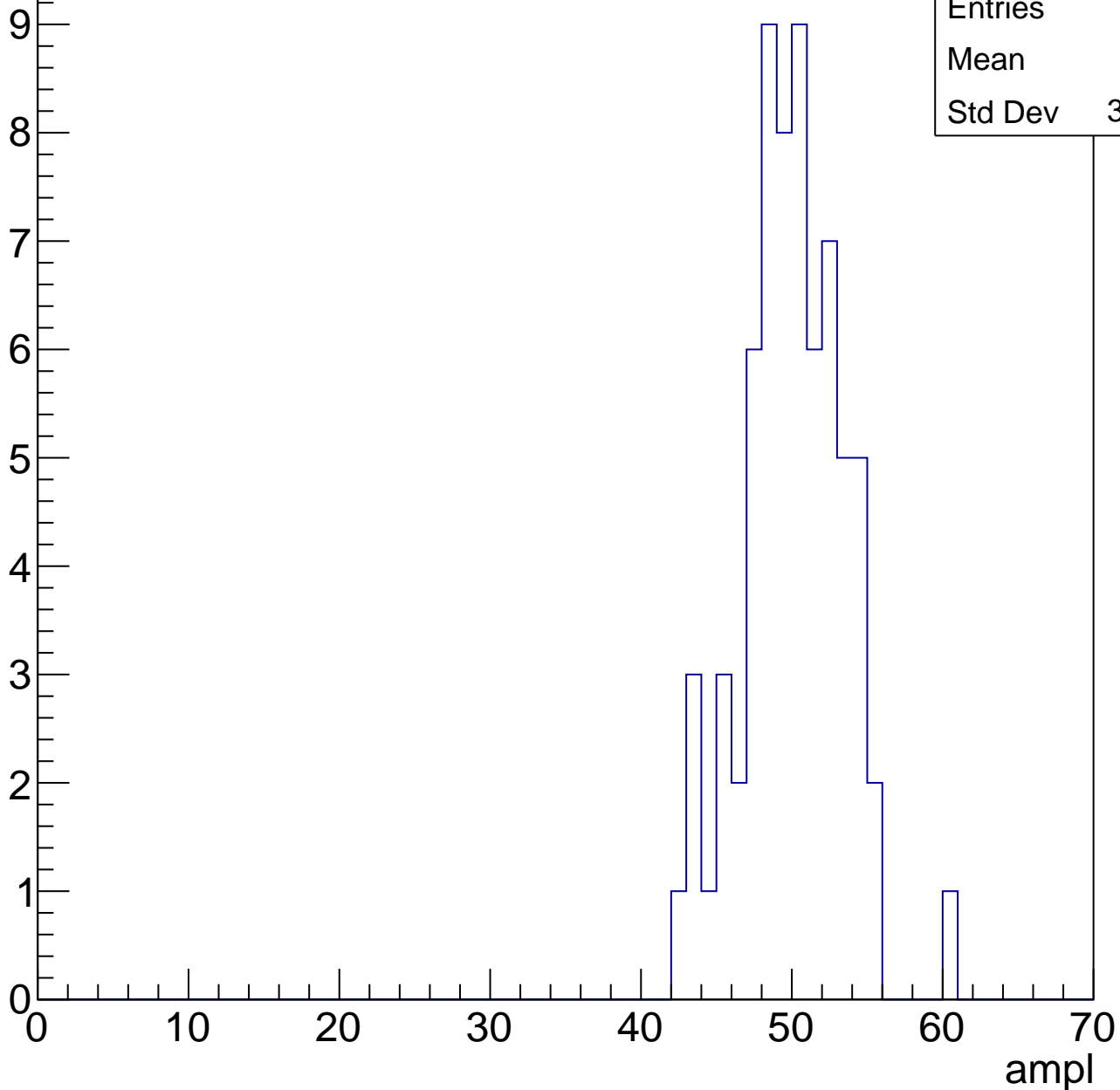


# B0L000S, U19-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.6
Std Dev	3.322

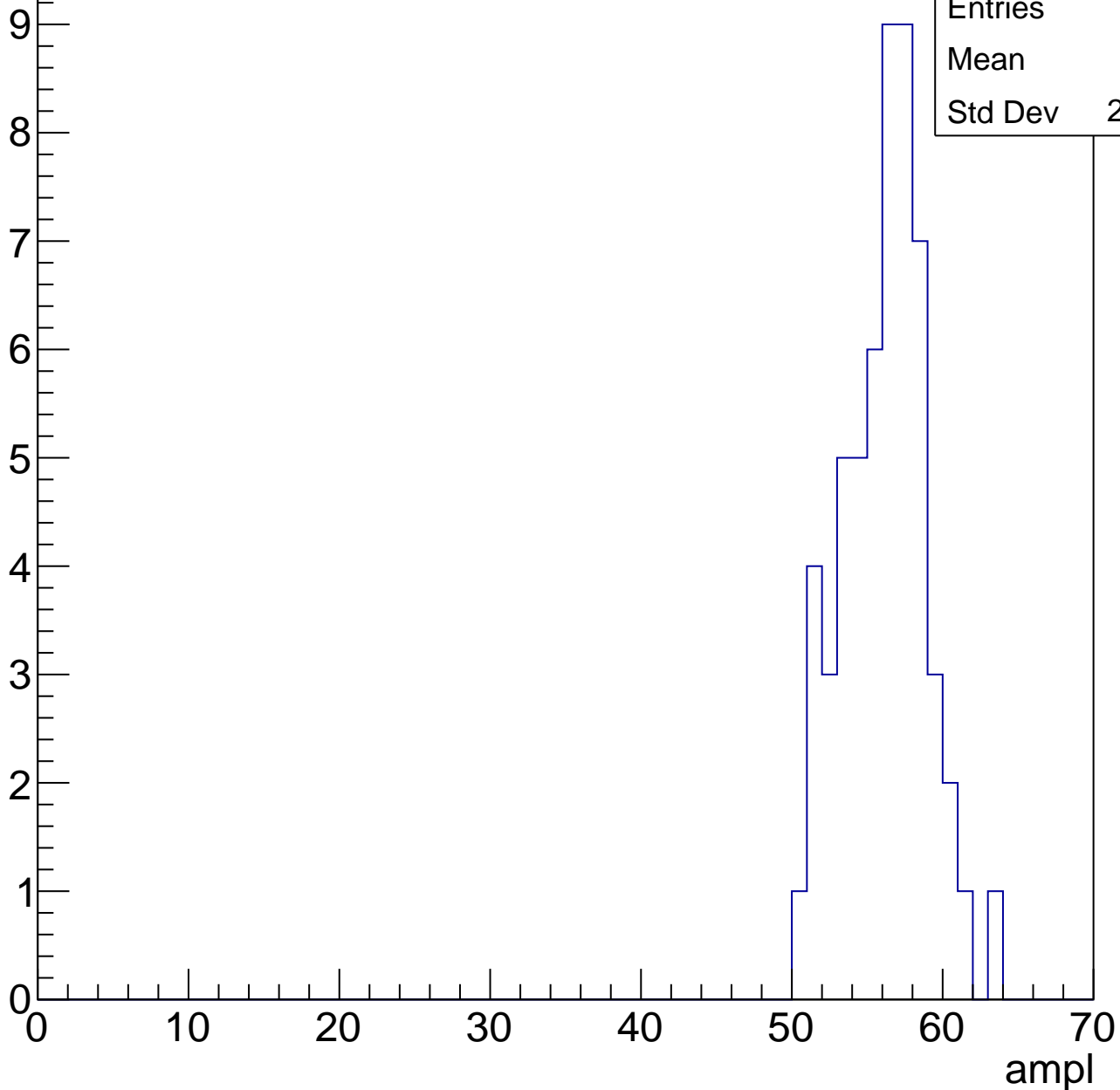


# B0L000S, U19-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.7
Std Dev	2.738



# B0L000S, U19-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

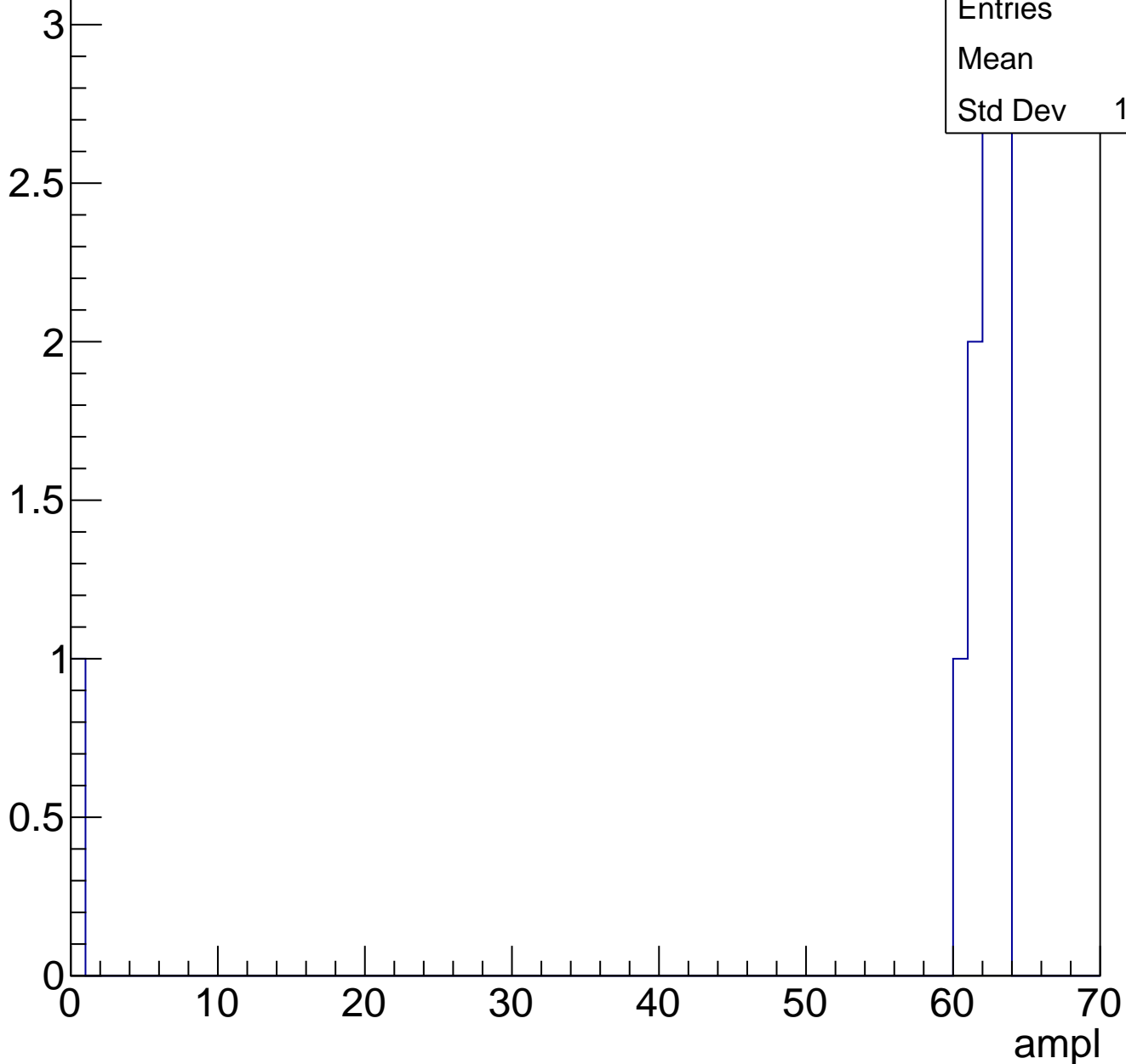
ampl

Entries	40
Mean	60.23
Std Dev	2.127

# B0L000S, U19-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	10
Mean	55.7
Std Dev	18.59



# B0L000S, U19-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch49, adc0

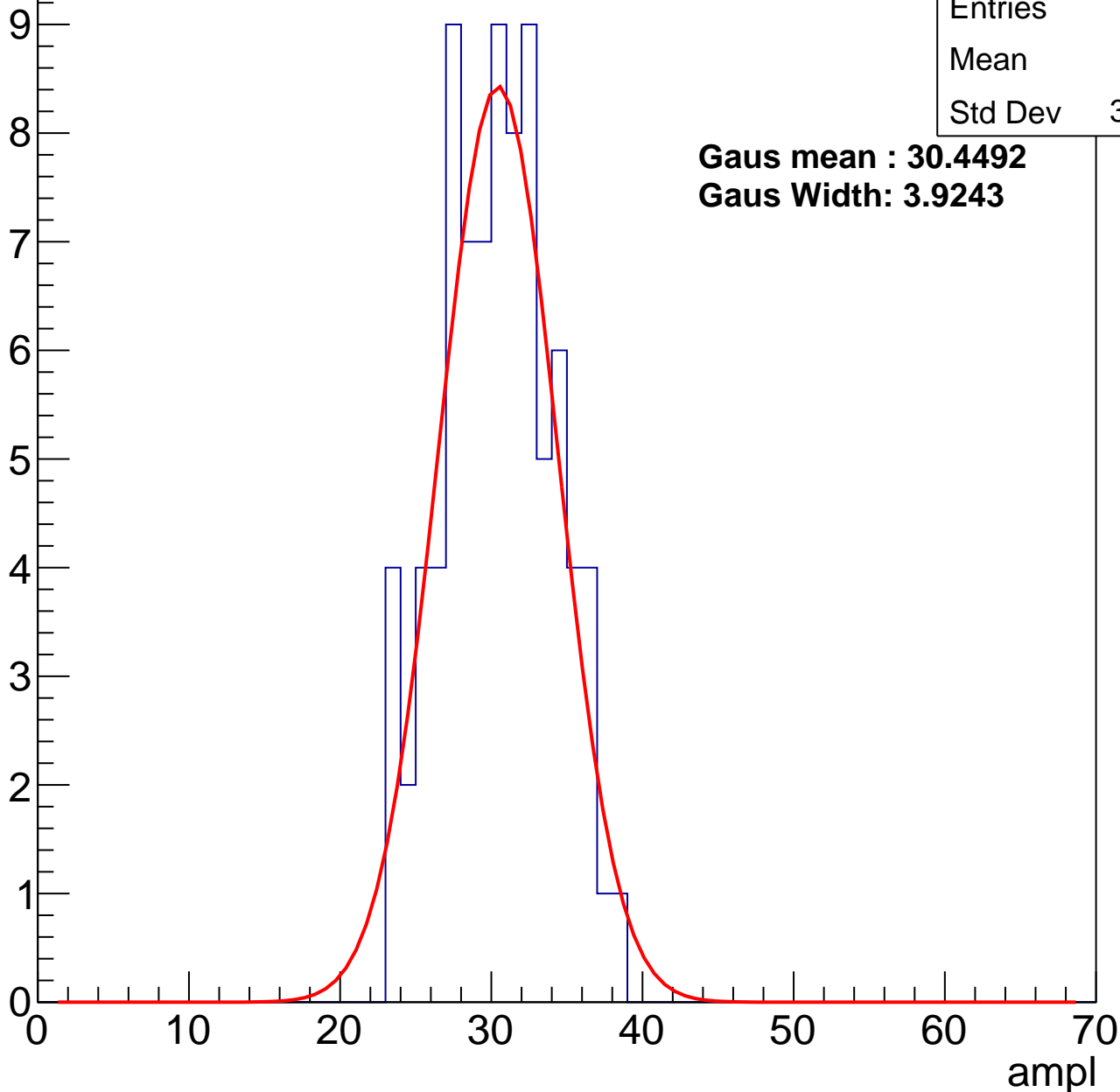
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	84
Mean	30
Std Dev	3.602

**Gaus mean : 30.4492**

**Gaus Width: 3.9243**



# B0L000S, U19-ch49, adc1

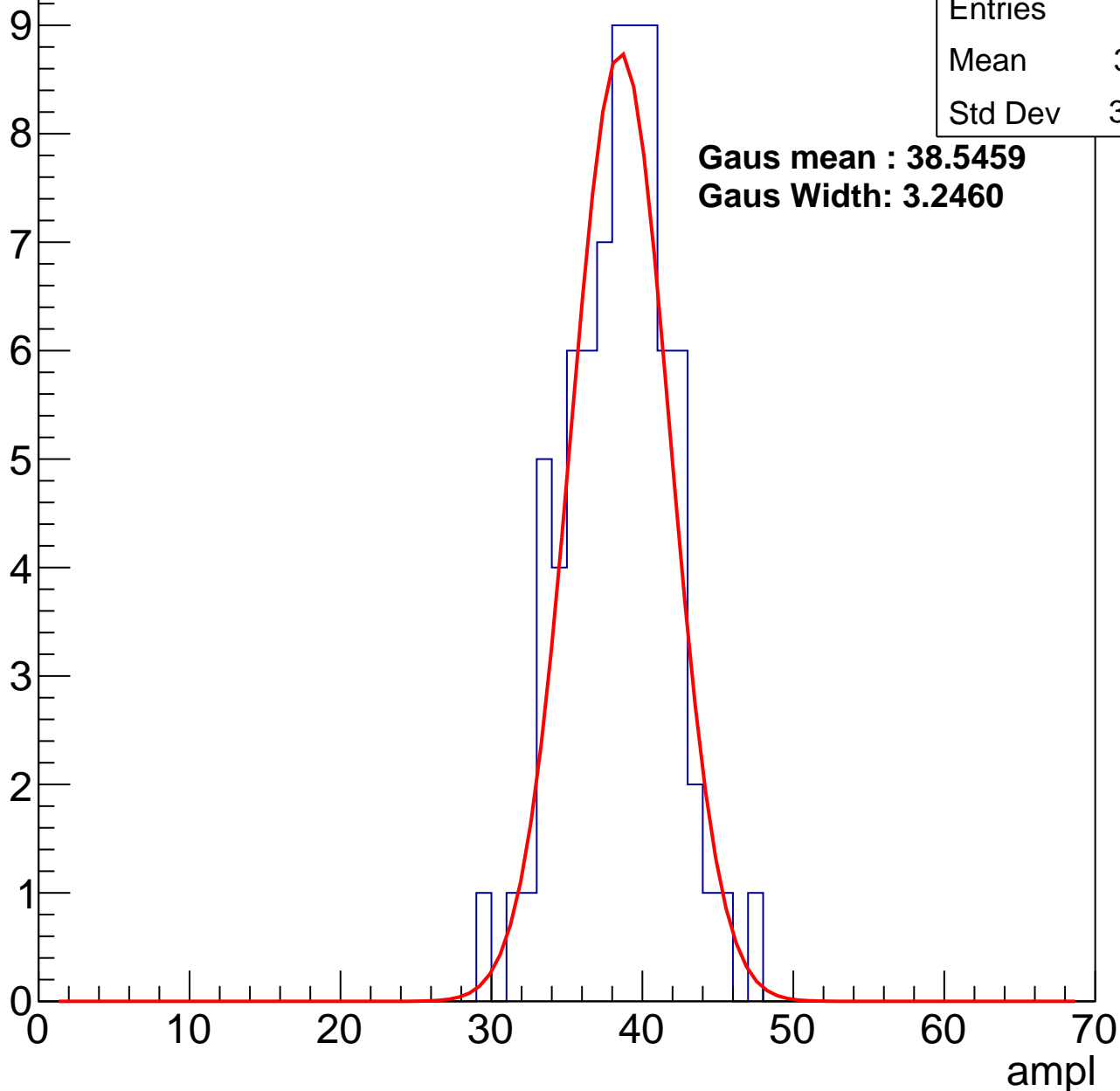
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	38.01
Std Dev	3.368

**Gaus mean : 38.5459**

**Gaus Width: 3.2460**



# B0L000S, U19-ch49, adc2

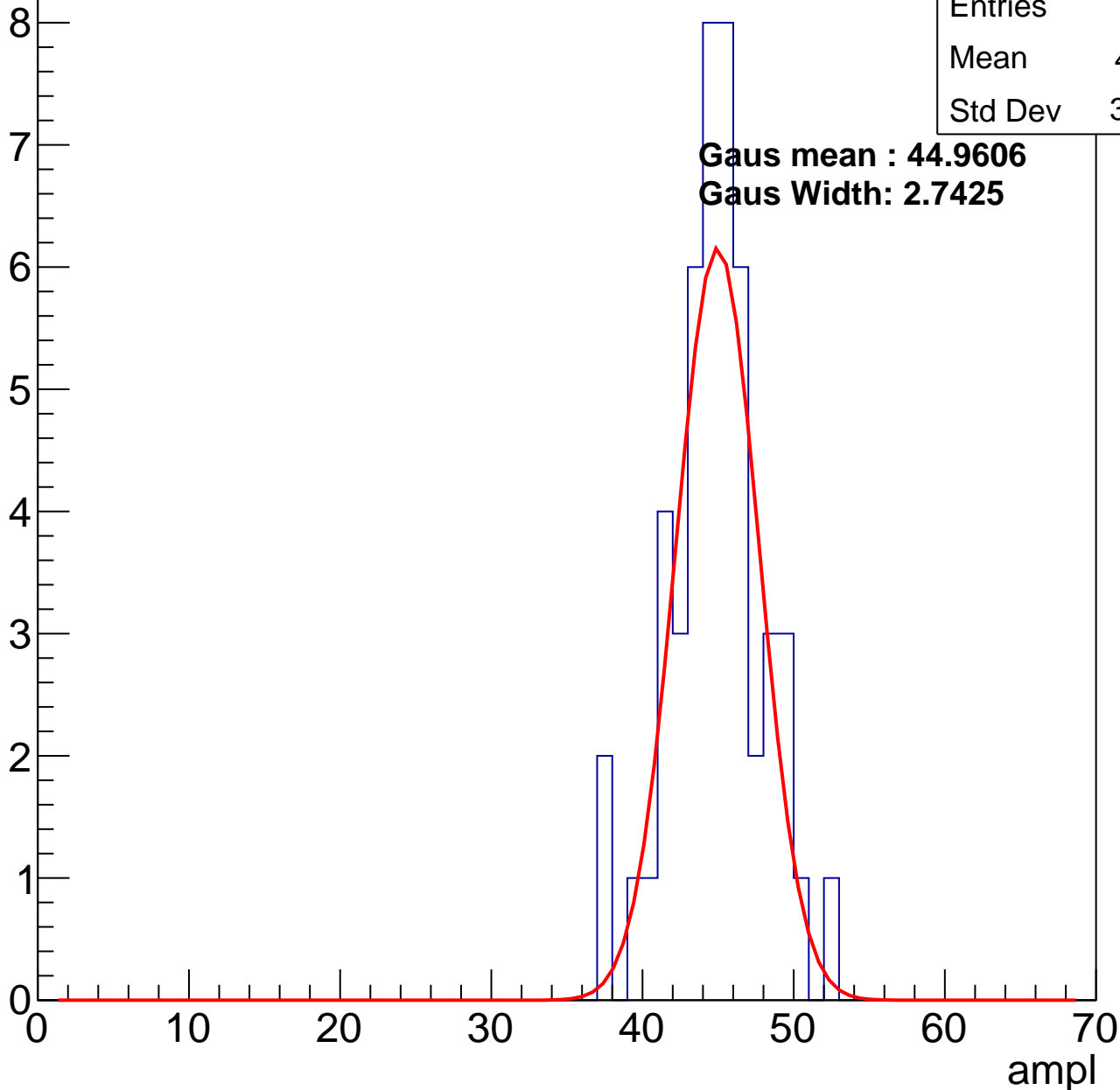
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	44.41
Std Dev	3.057

**Gaus mean : 44.9606**

**Gaus Width: 2.7425**

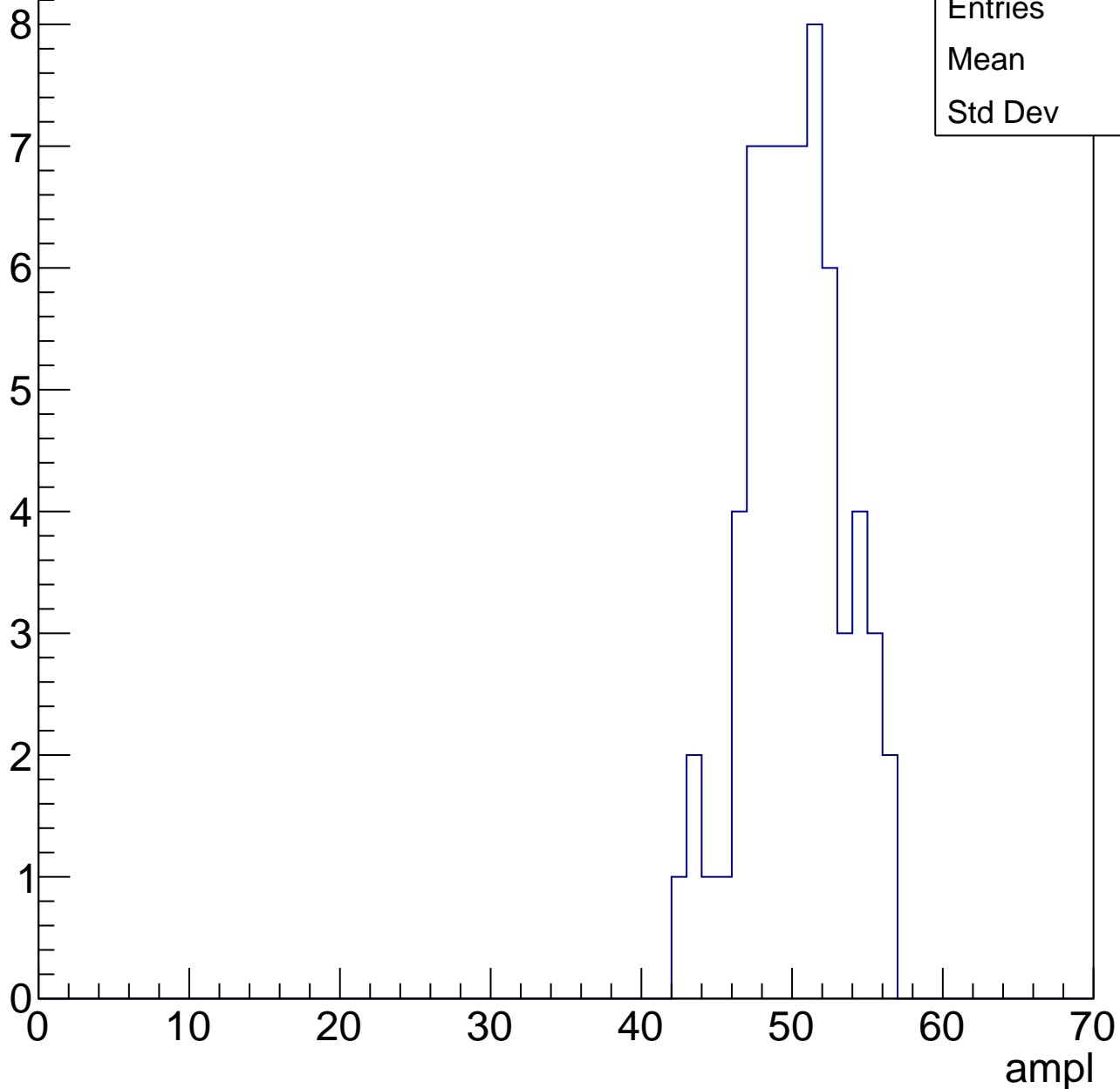


# B0L000S, U19-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	49.7
Std Dev	3.21

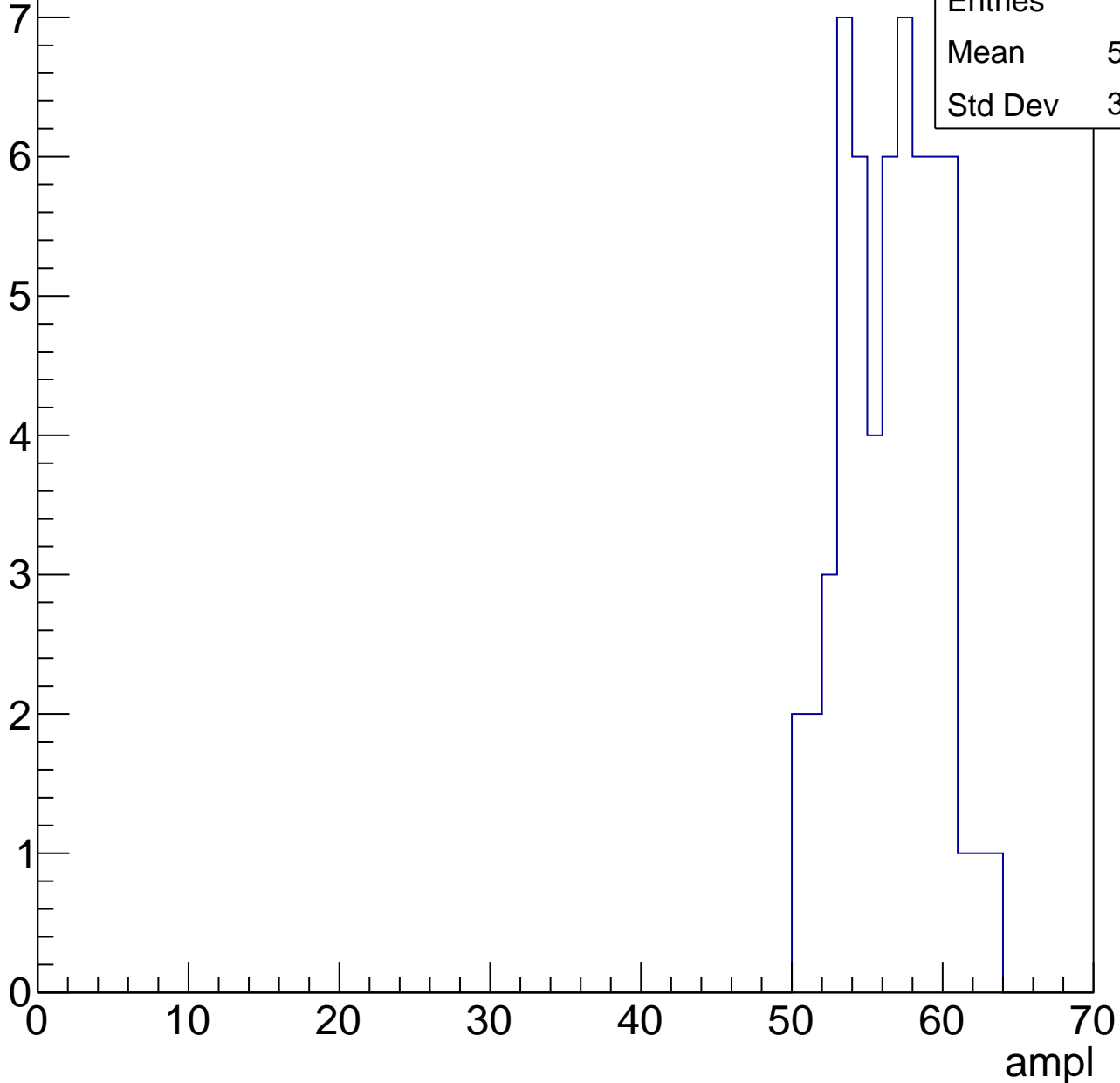


# B0L000S, U19-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

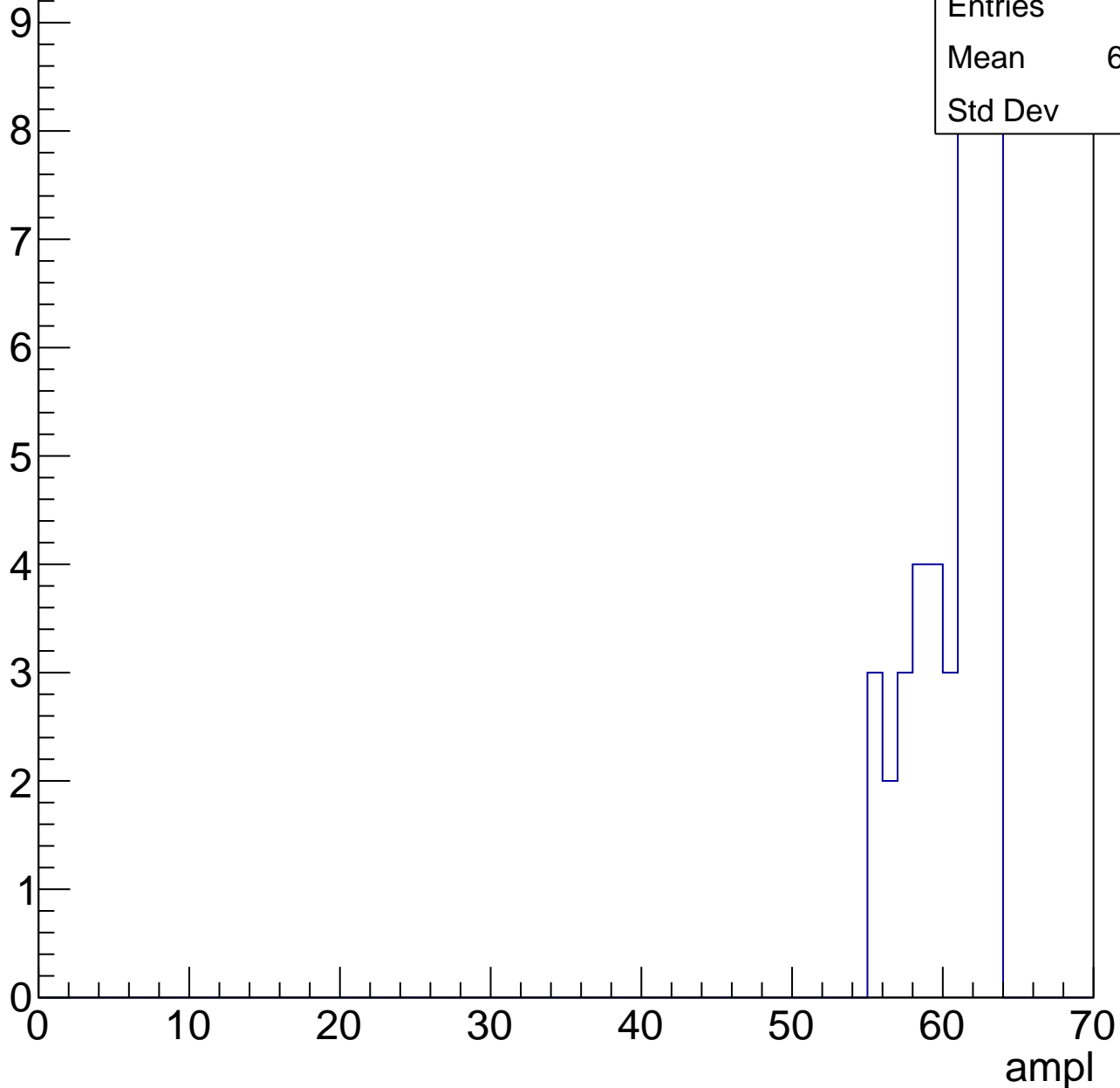
Entries	58
Mean	56.14
Std Dev	3.082



# B0L000S, U19-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

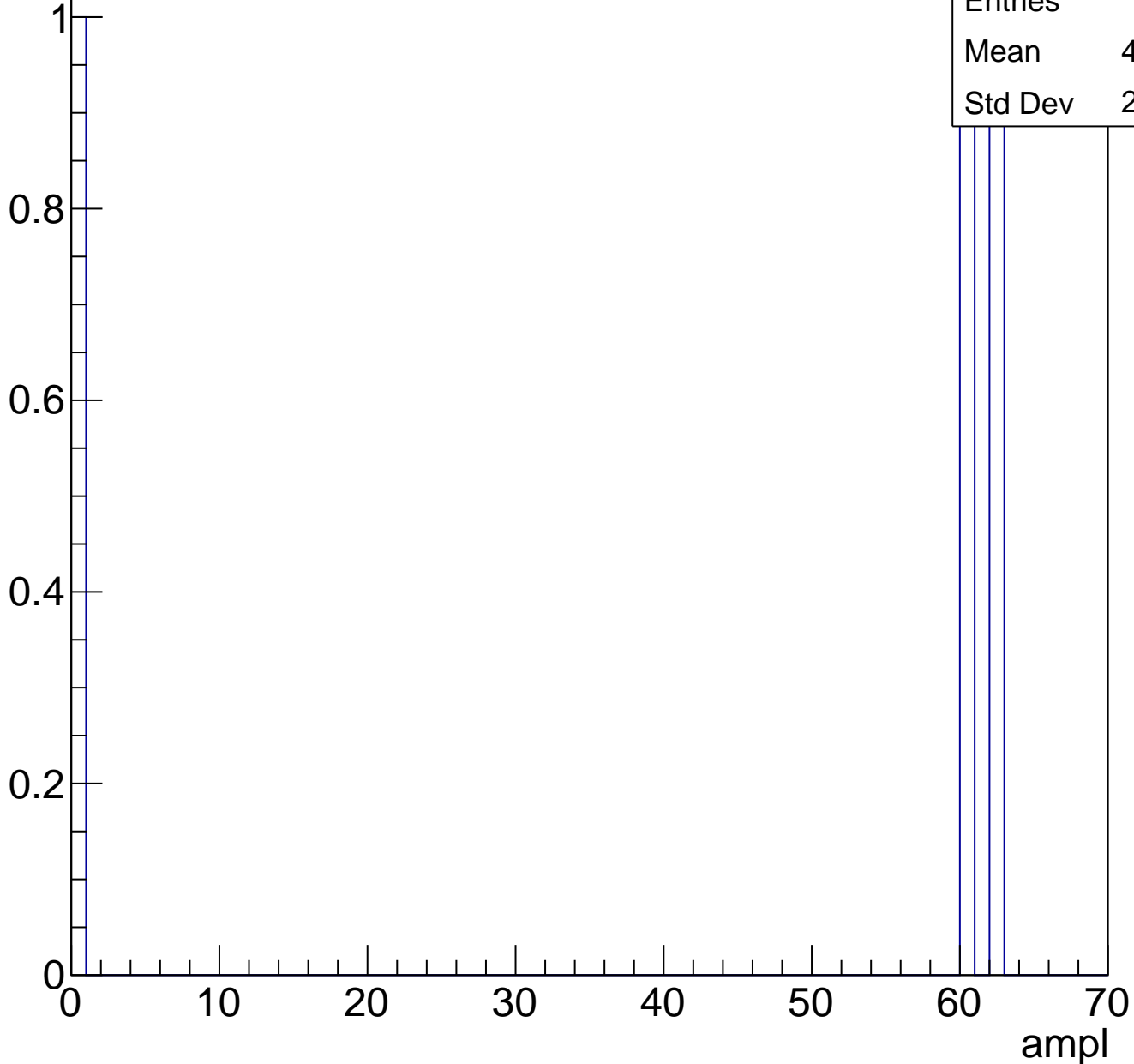
Entry



# B0L000S, U19-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L000S, U19-ch50, adc0

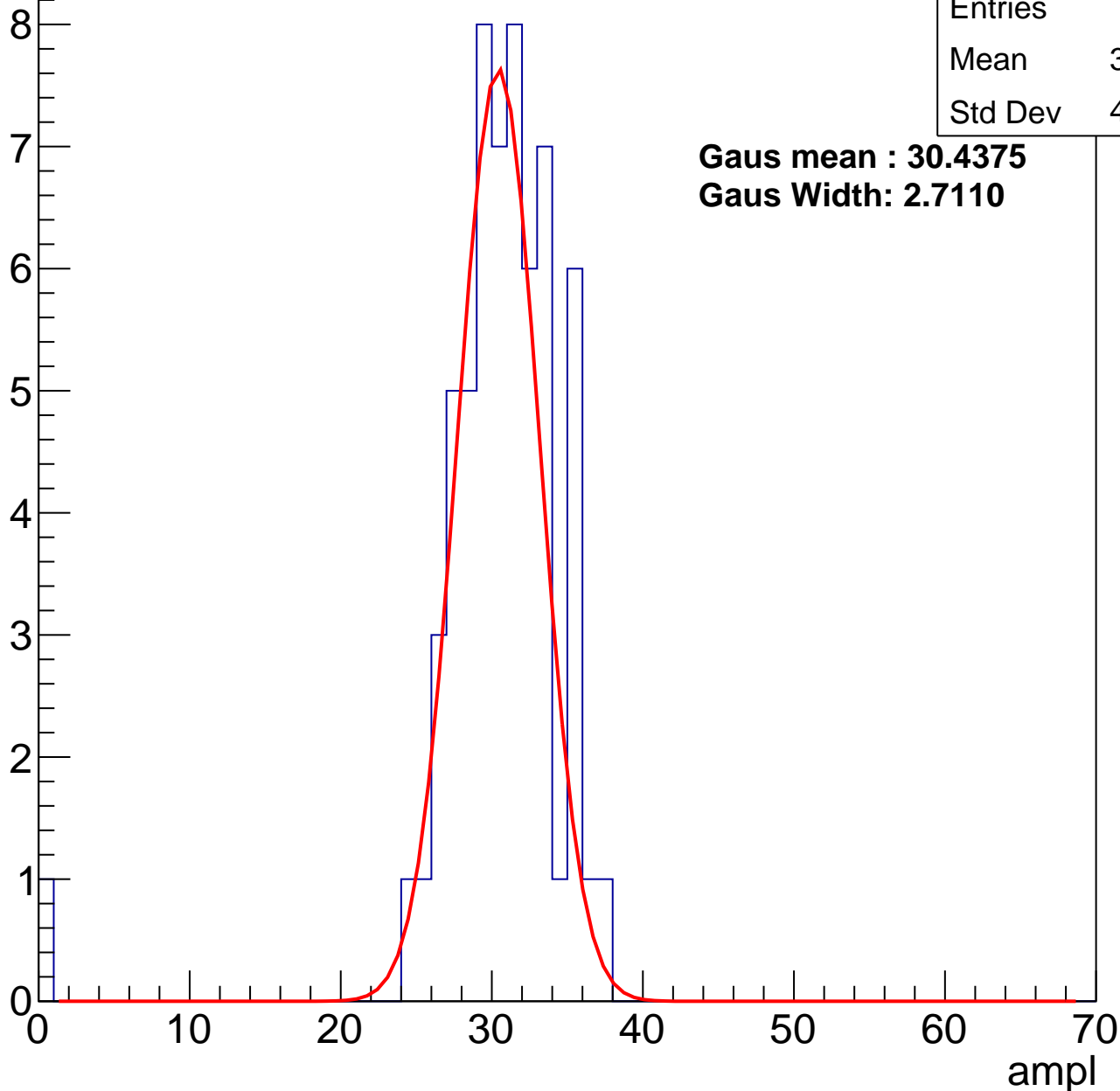
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	30.03
Std Dev	4.838

**Gaus mean : 30.4375**

**Gaus Width: 2.7110**



# B0L000S, U19-ch50, adc1

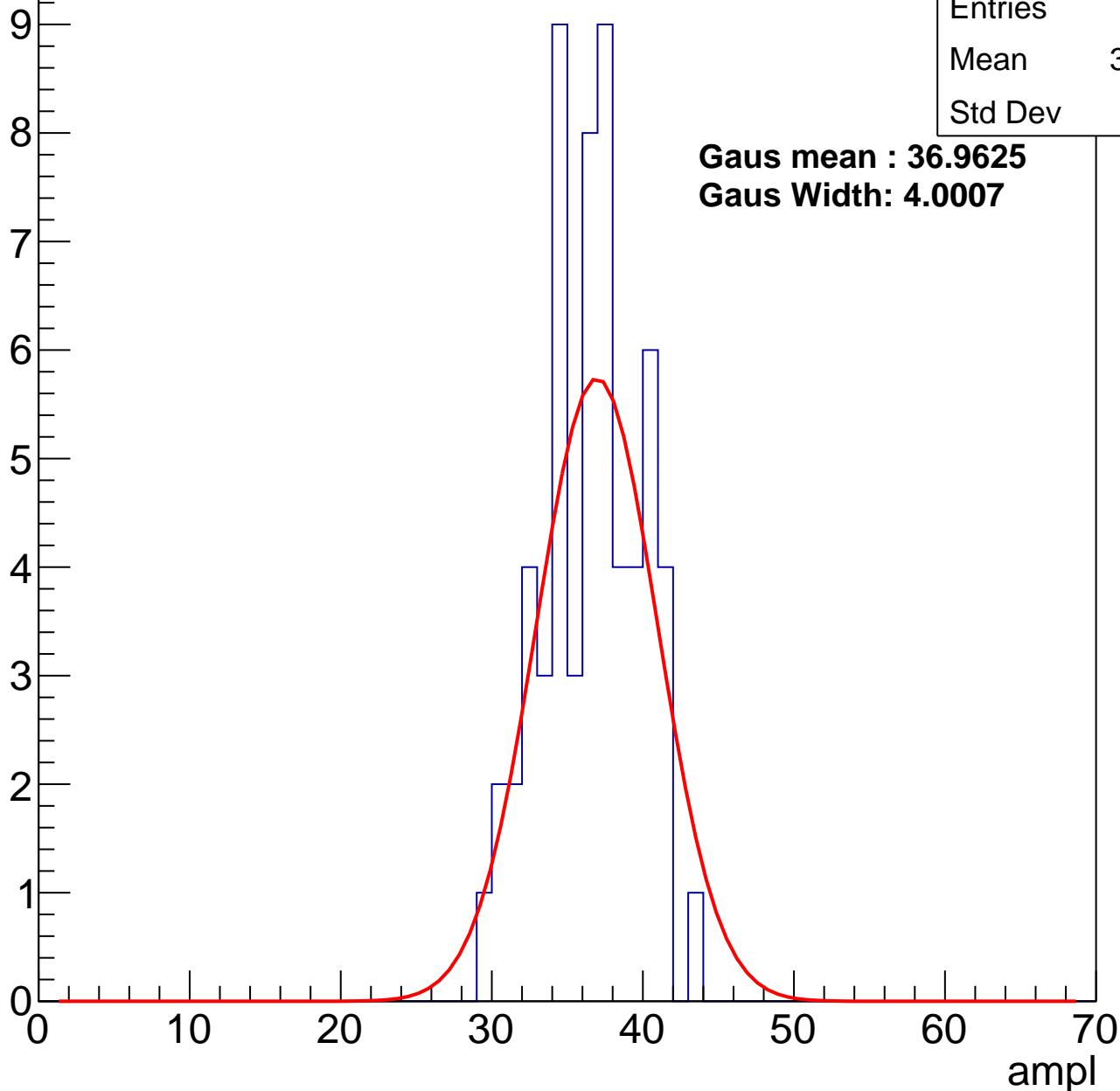
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.08
Std Dev	3.18

**Gaus mean : 36.9625**

**Gaus Width: 4.0007**



# B0L000S, U19-ch50, adc2

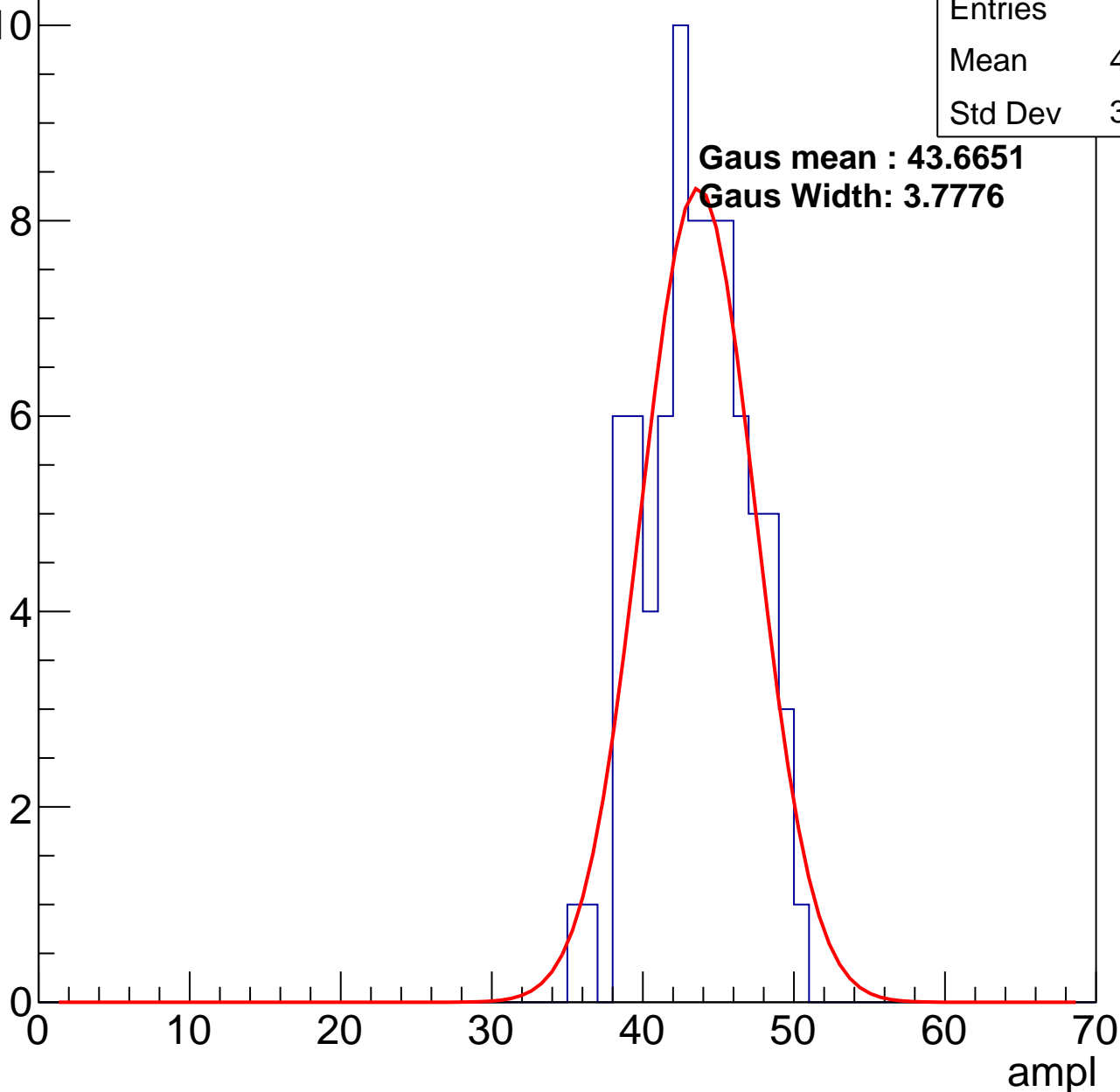
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	43.12
Std Dev	3.366

**Gaus mean : 43.6651**

**Gaus Width: 3.7776**

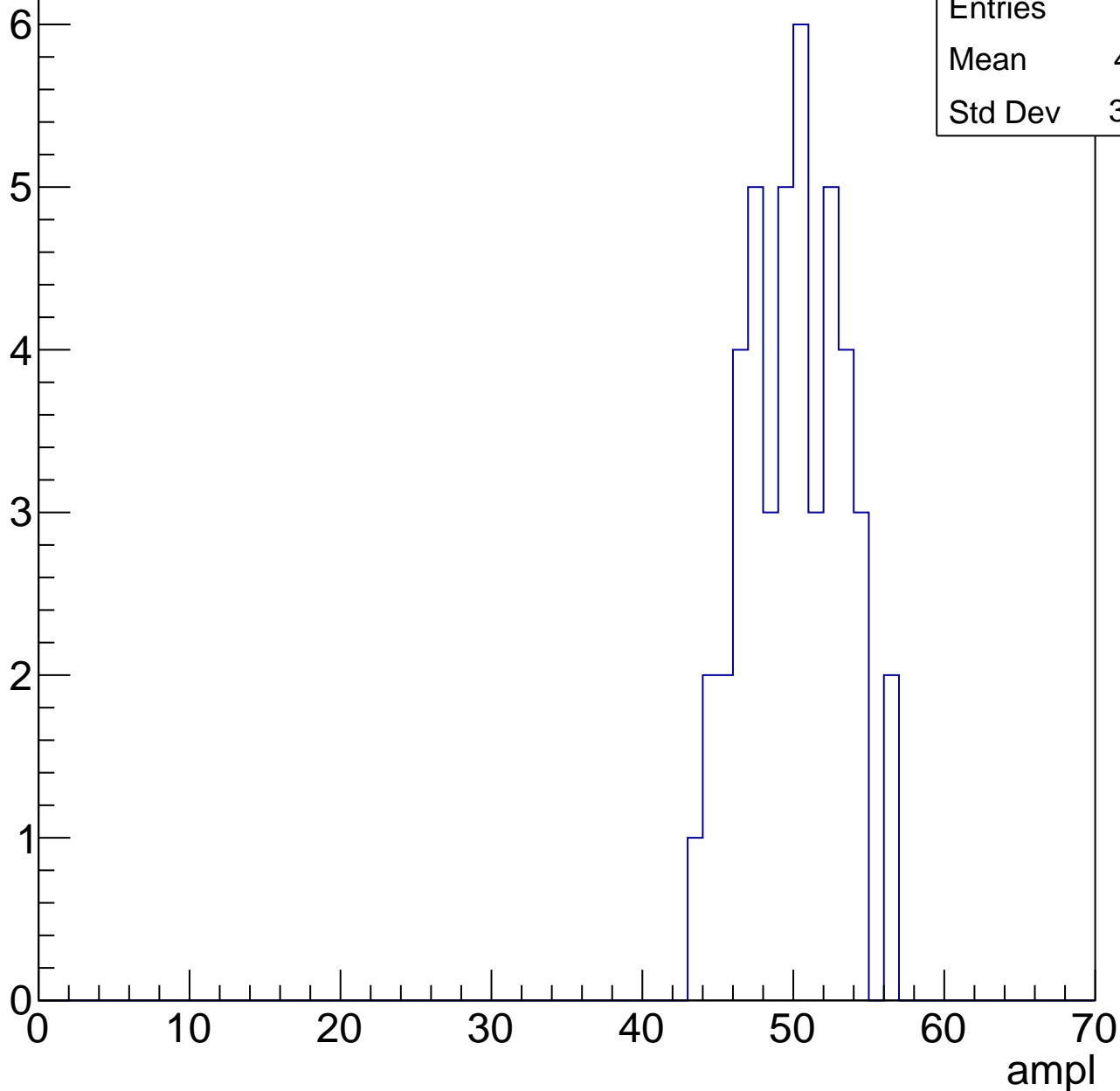


# B0L000S, U19-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	49.51
Std Dev	3.215

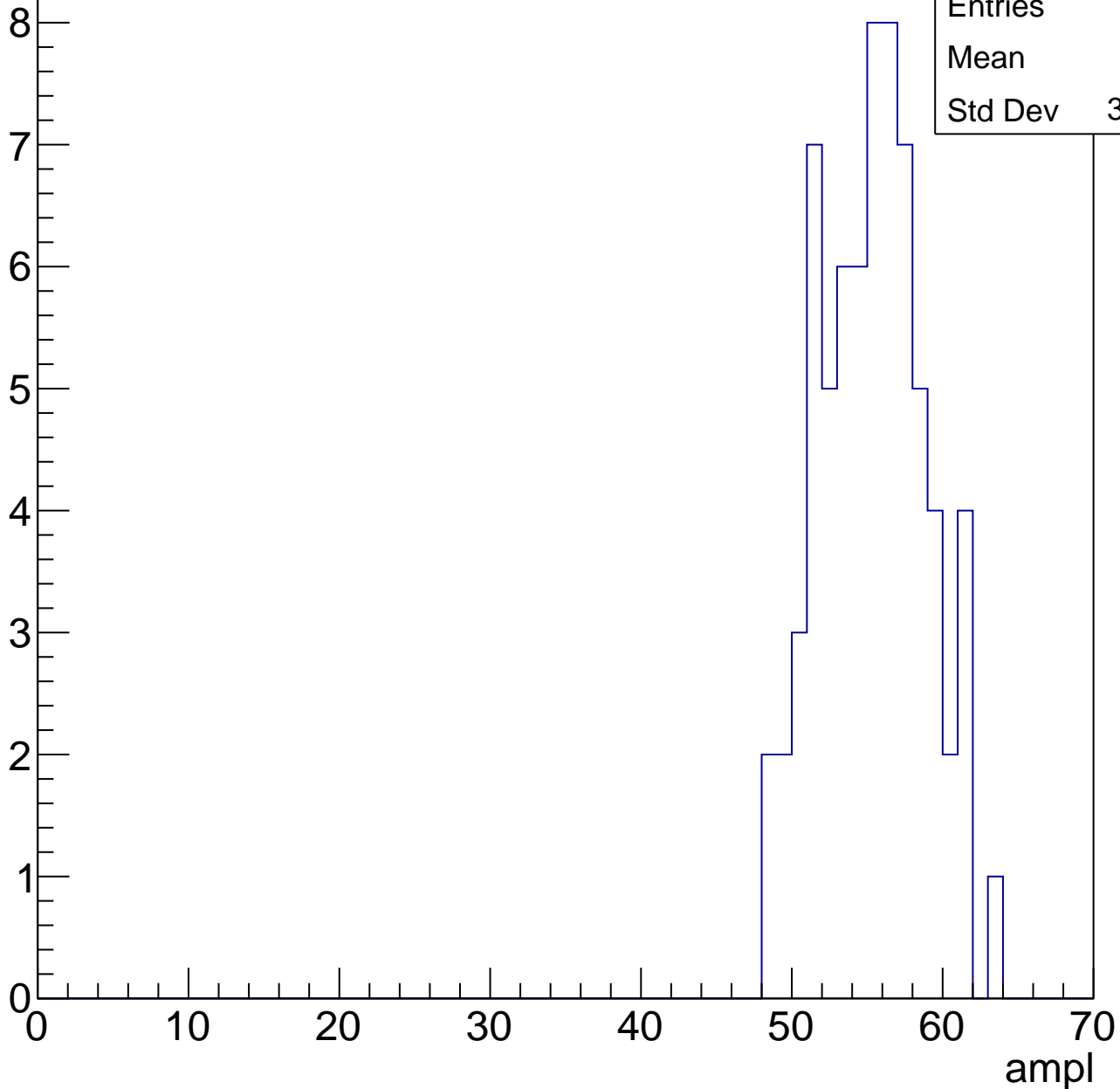


# B0L000S, U19-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	54.9
Std Dev	3.452

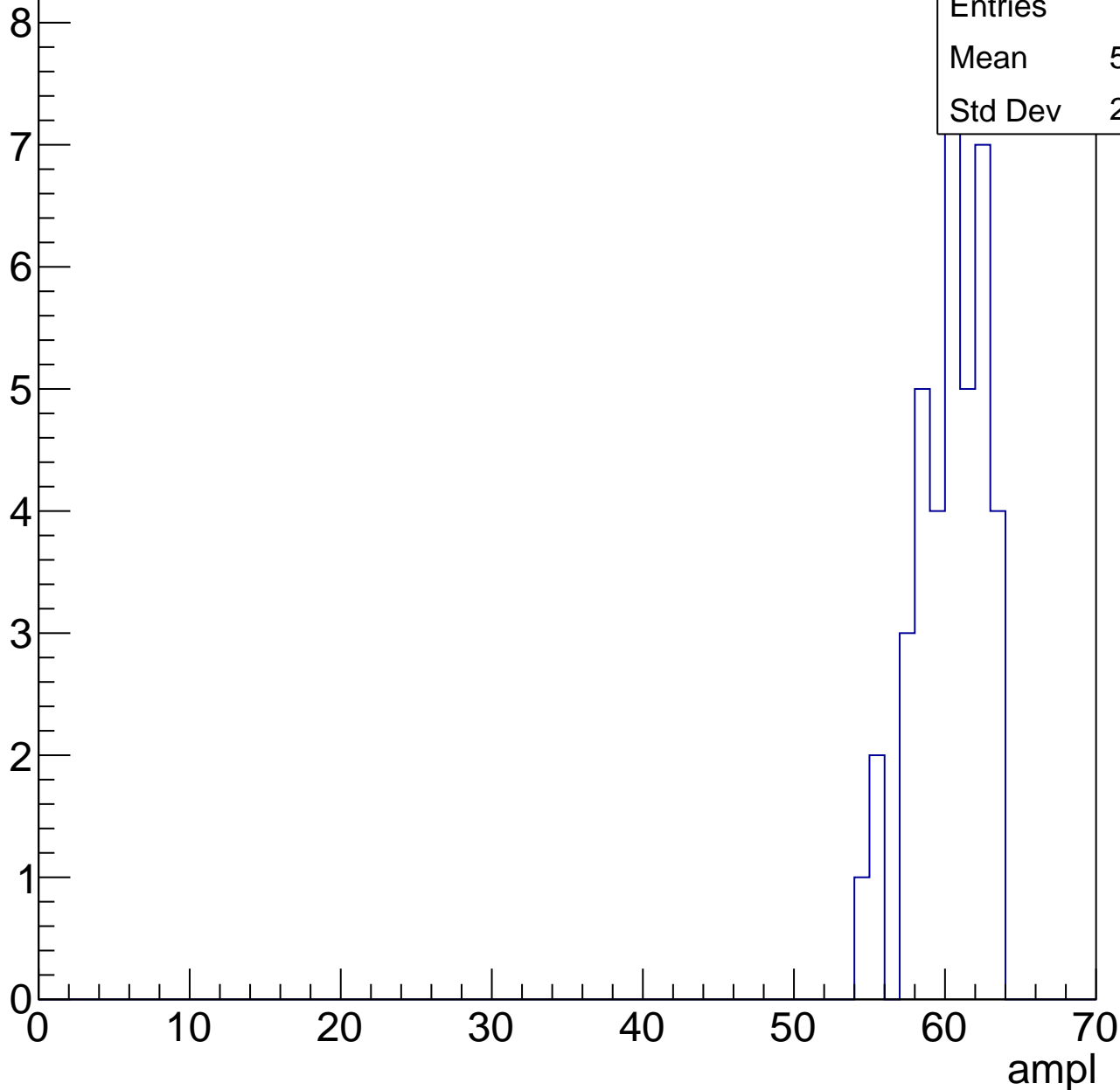


# B0L000S, U19-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

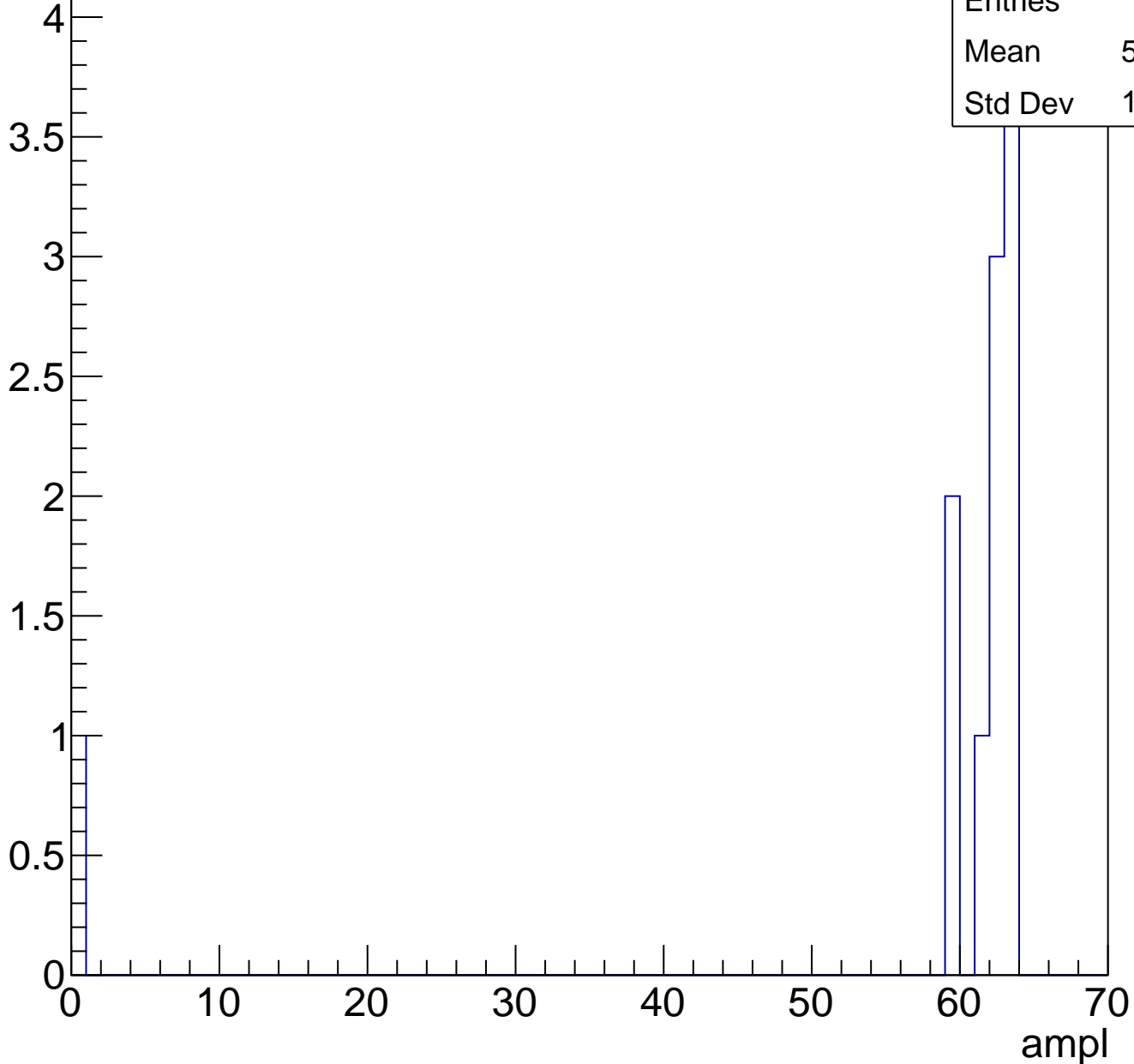
Entries	39
Mean	59.79
Std Dev	2.289



# B0L000S, U19-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch51, adc0

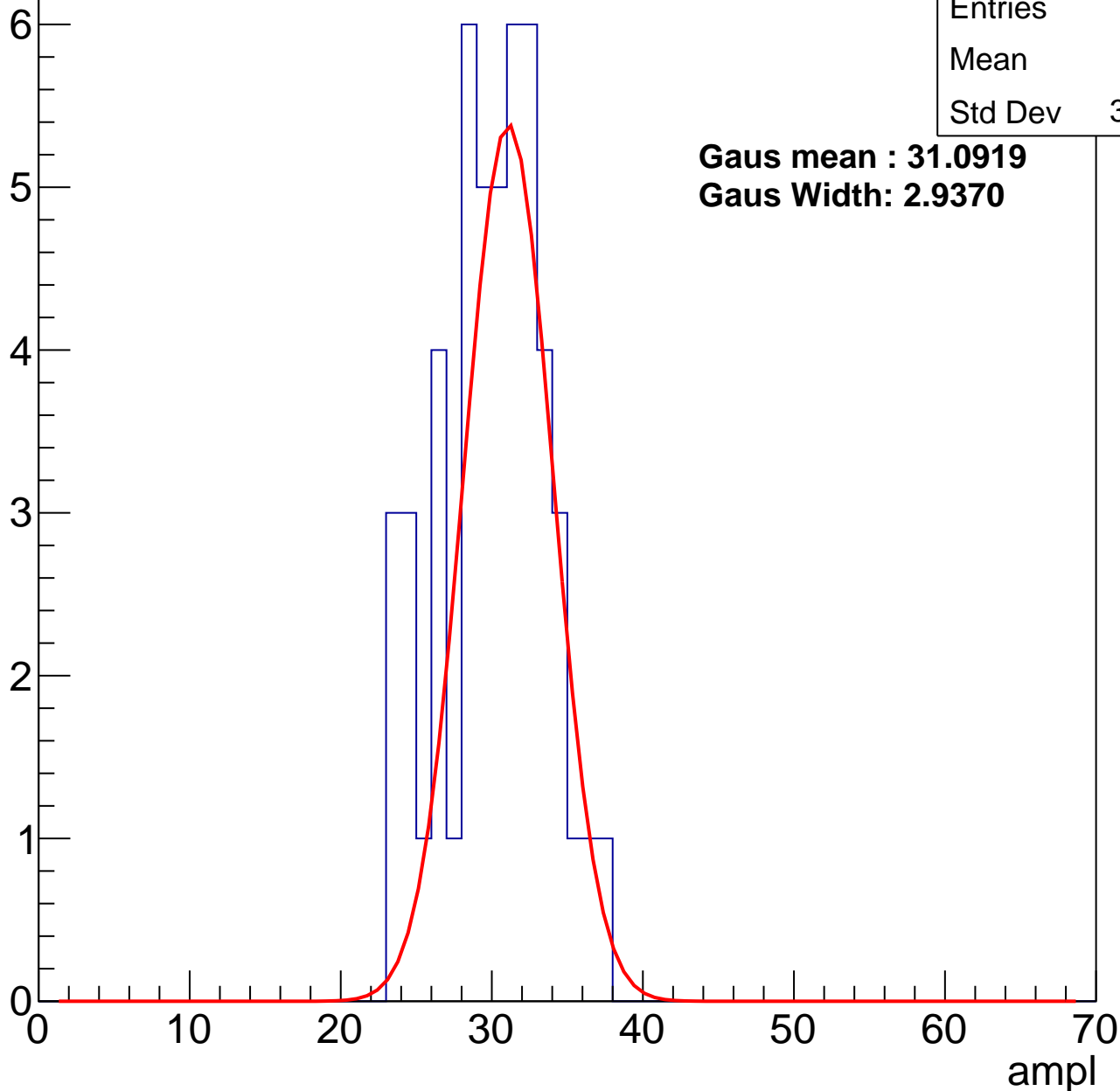
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	29.6
Std Dev	3.458

**Gaus mean : 31.0919**

**Gaus Width: 2.9370**



# B0L000S, U19-ch51, adc1

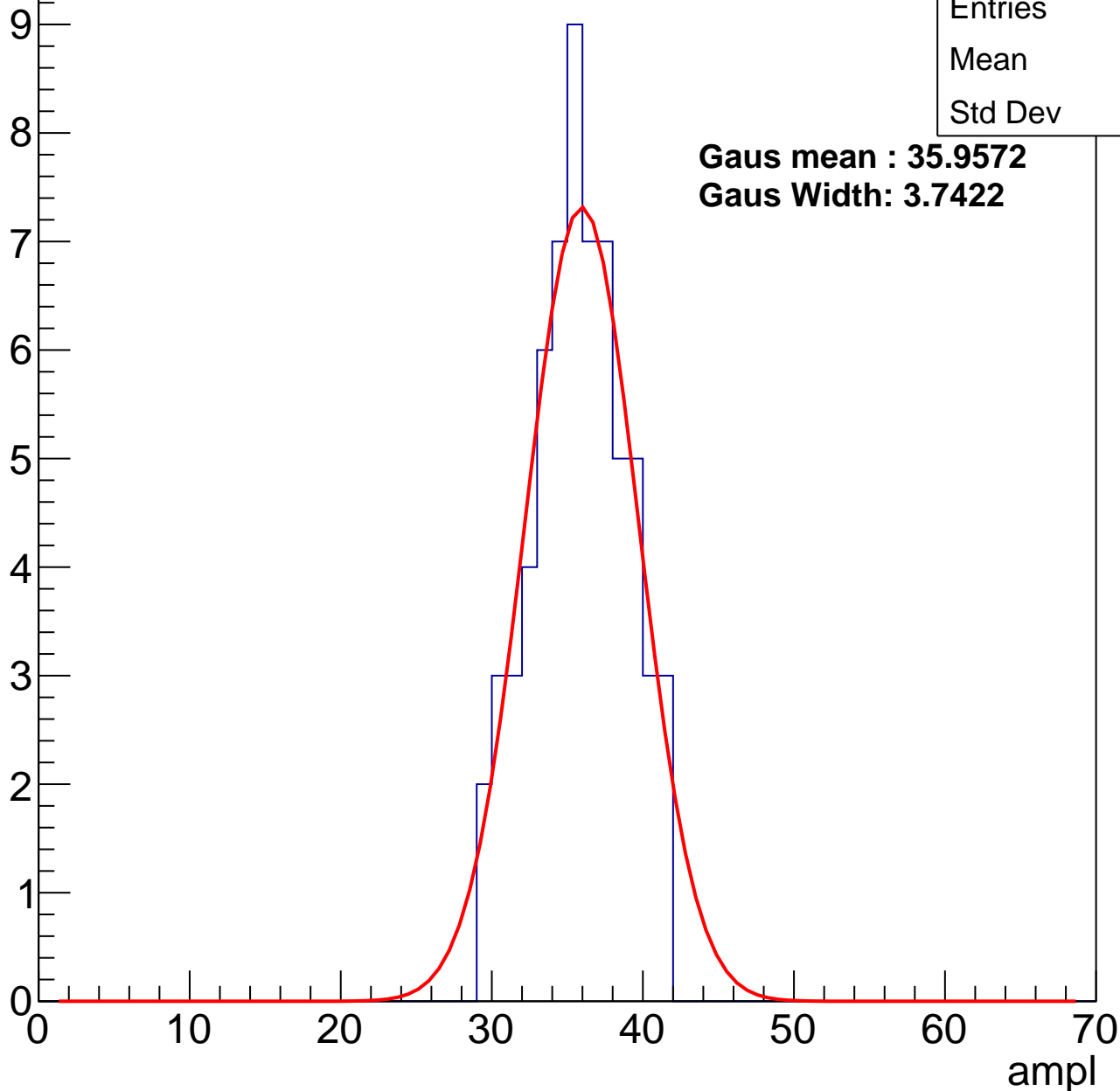
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	35.3
Std Dev	3.06

**Gaus mean : 35.9572**

**Gaus Width: 3.7422**



# B0L000S, U19-ch51, adc2

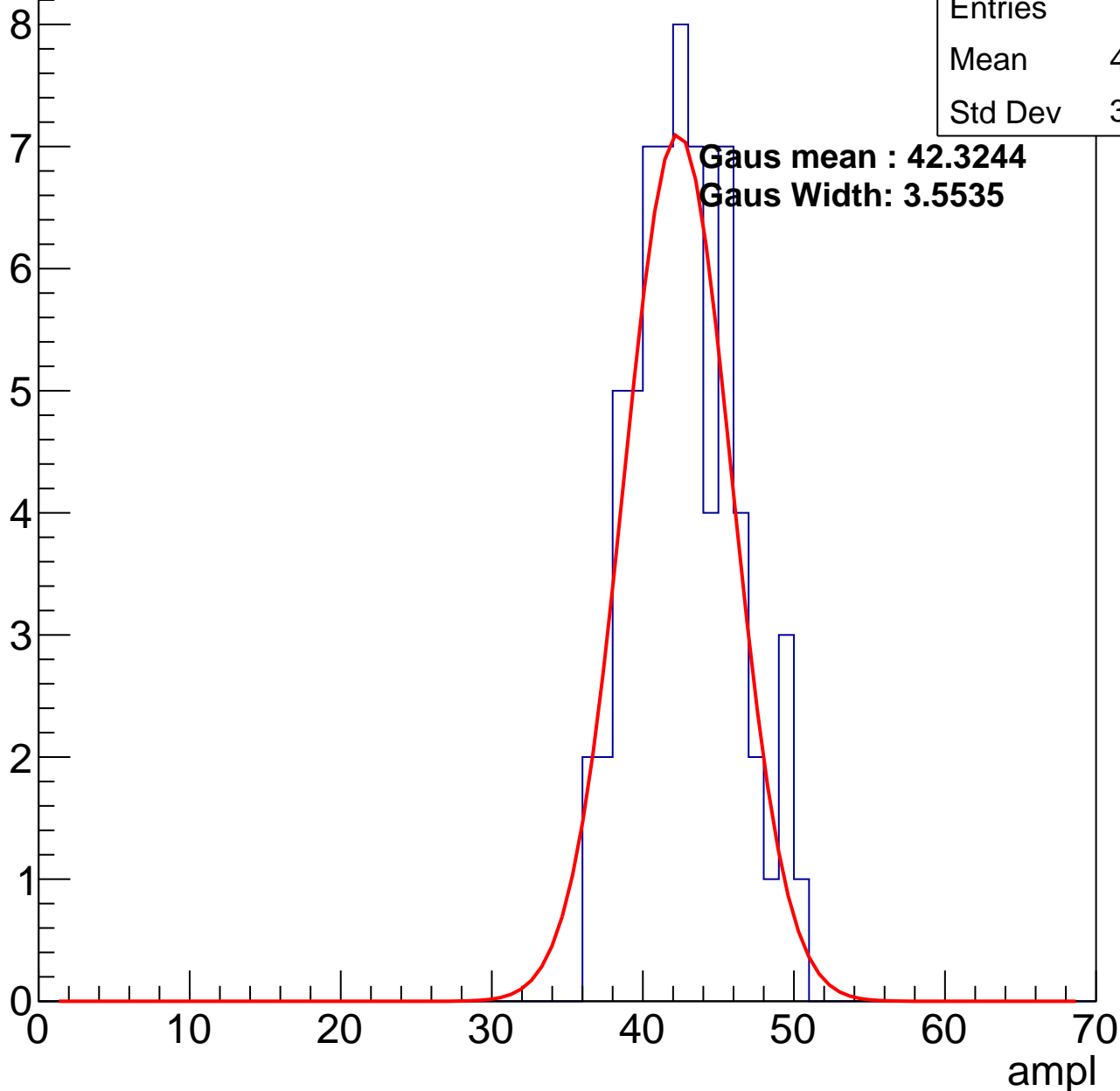
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.29
Std Dev	3.336

**Gaus mean : 42.3244**

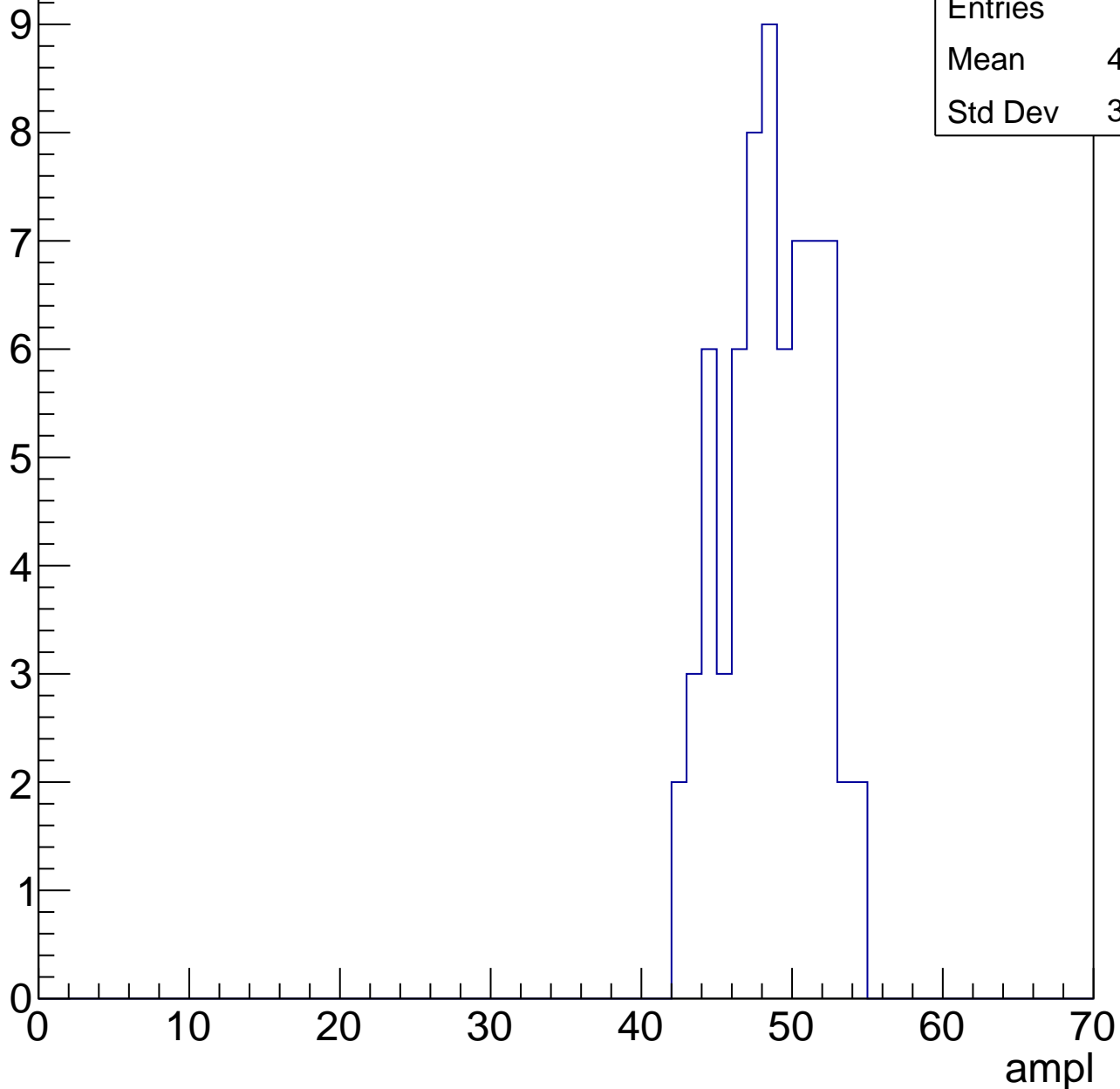
**Gaus Width: 3.5535**



# B0L000S, U19-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	68
Mean	48.16
Std Dev	3.047

# B0L000S, U19-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

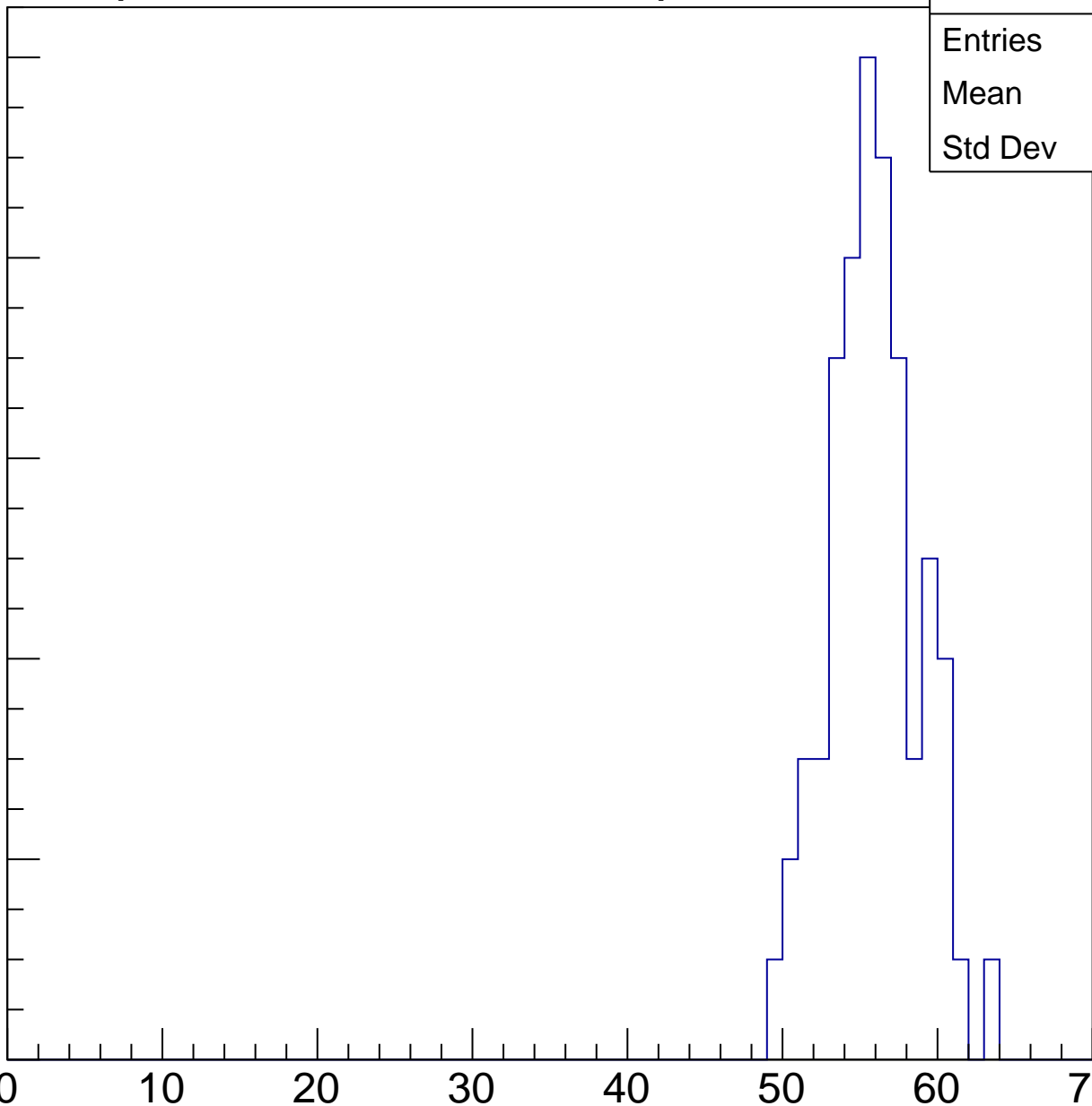
Entries	64
Mean	55.42
Std Dev	2.877

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

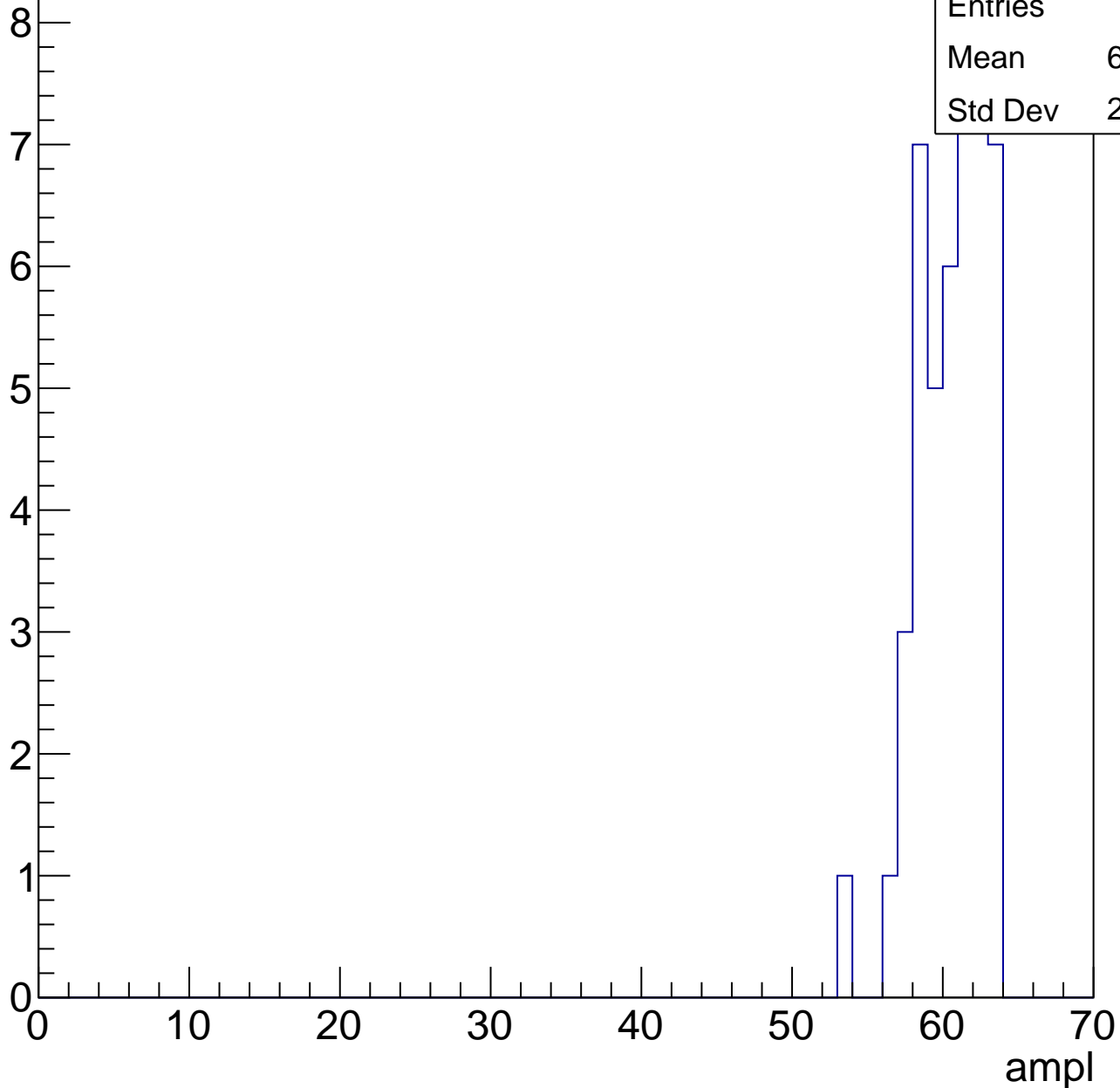


# B0L000S, U19-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	60.13
Std Dev	2.223



# B0L000S, U19-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch52, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	29.83
Std Dev	4.825

**Gaus mean : 30.7051**

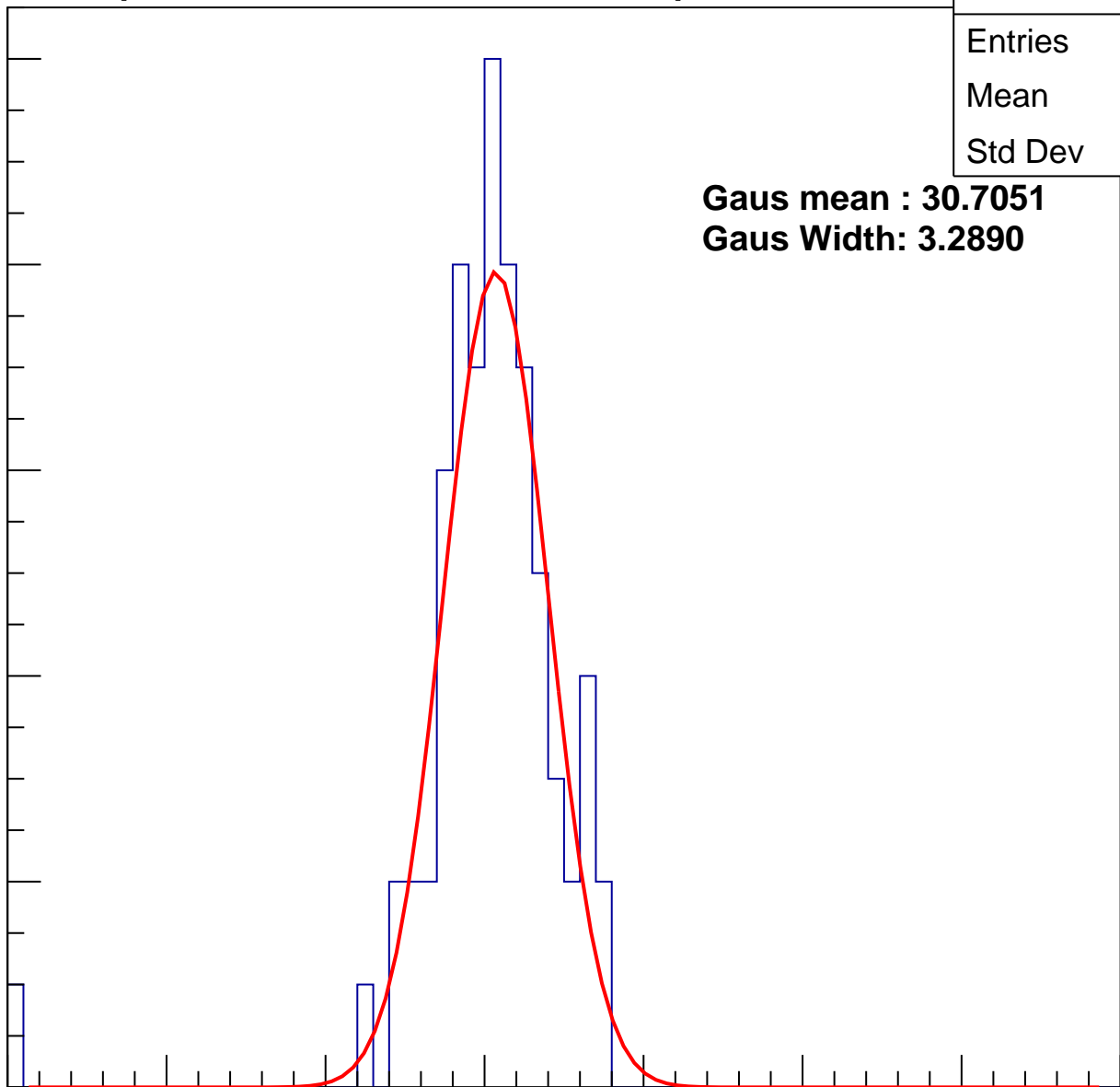
**Gaus Width: 3.2890**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch52, adc1

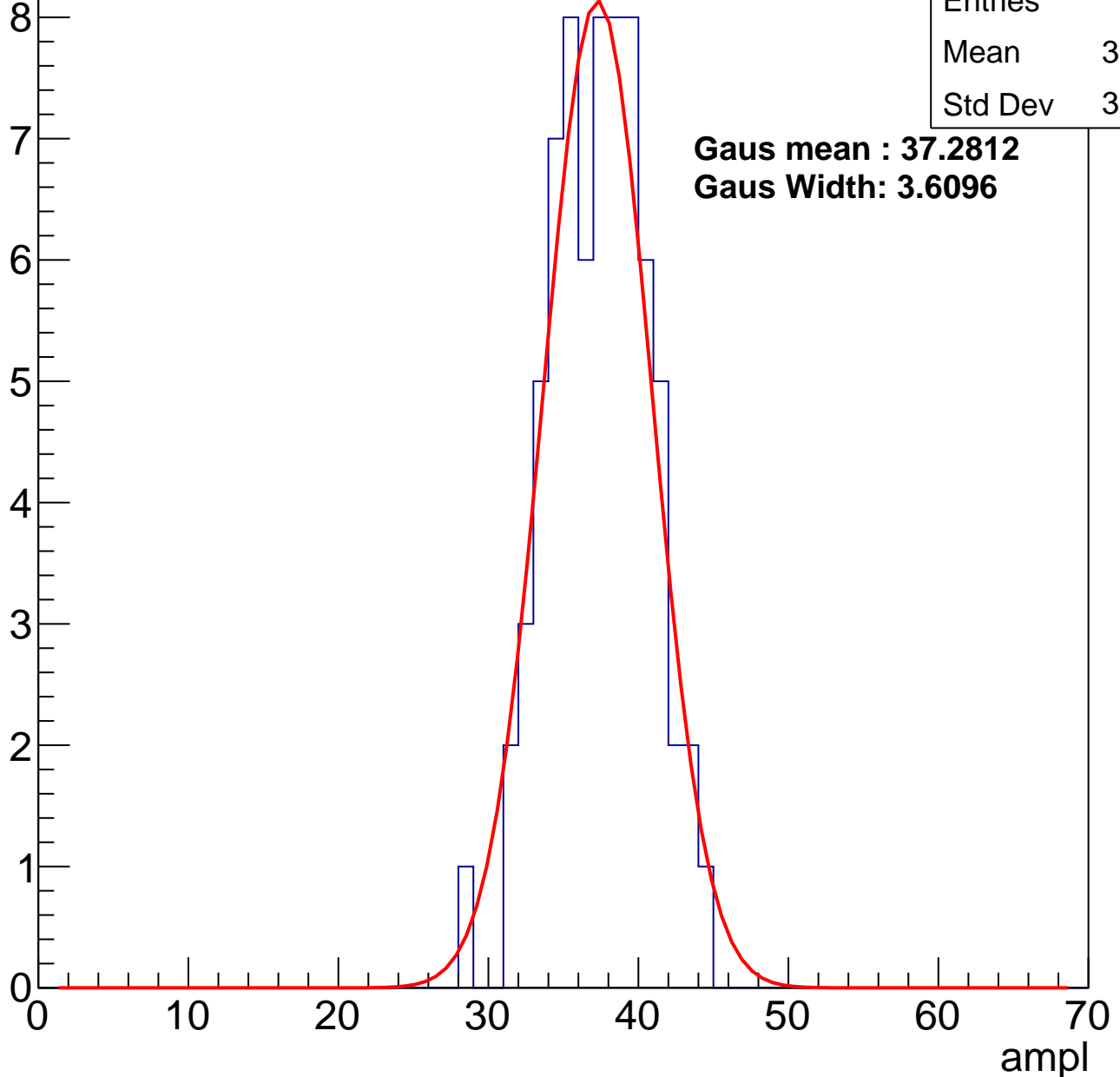
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.89
Std Dev	3.234

**Gaus mean : 37.2812**

**Gaus Width: 3.6096**



# B0L000S, U19-ch52, adc2

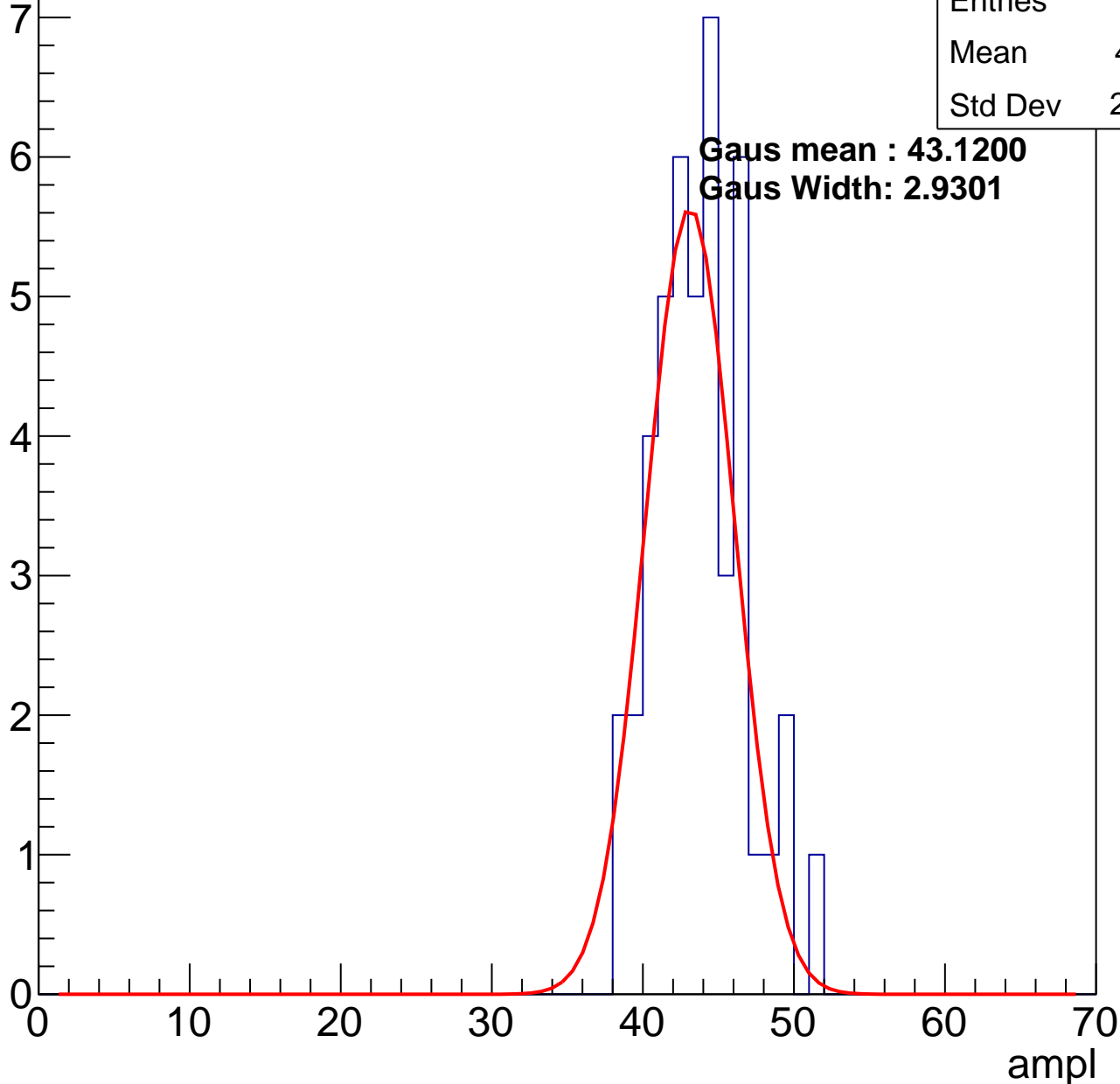
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	43.31
Std Dev	2.943

**Gaus mean : 43.1200**

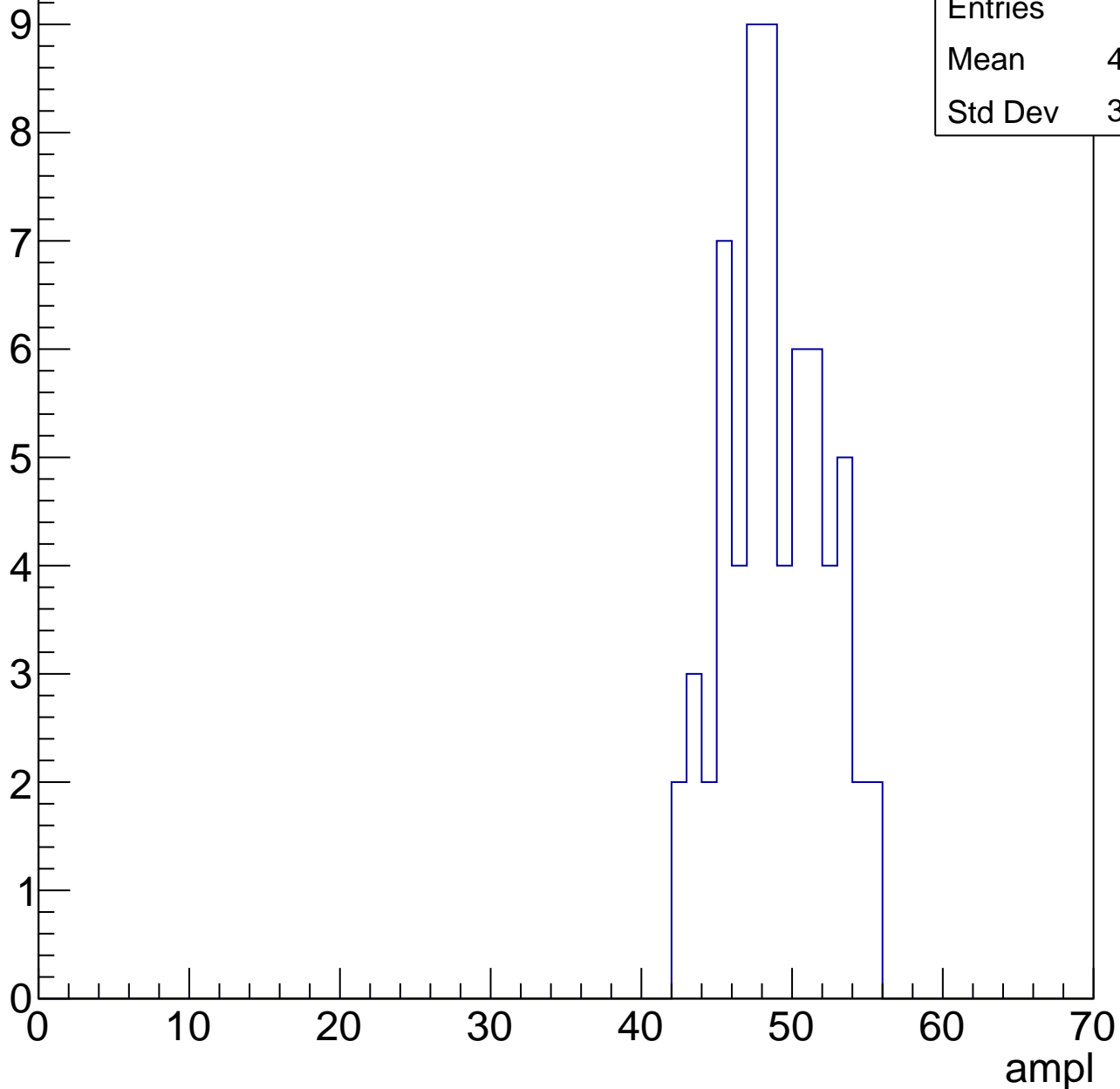
**Gaus Width: 2.9301**



# B0L000S, U19-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

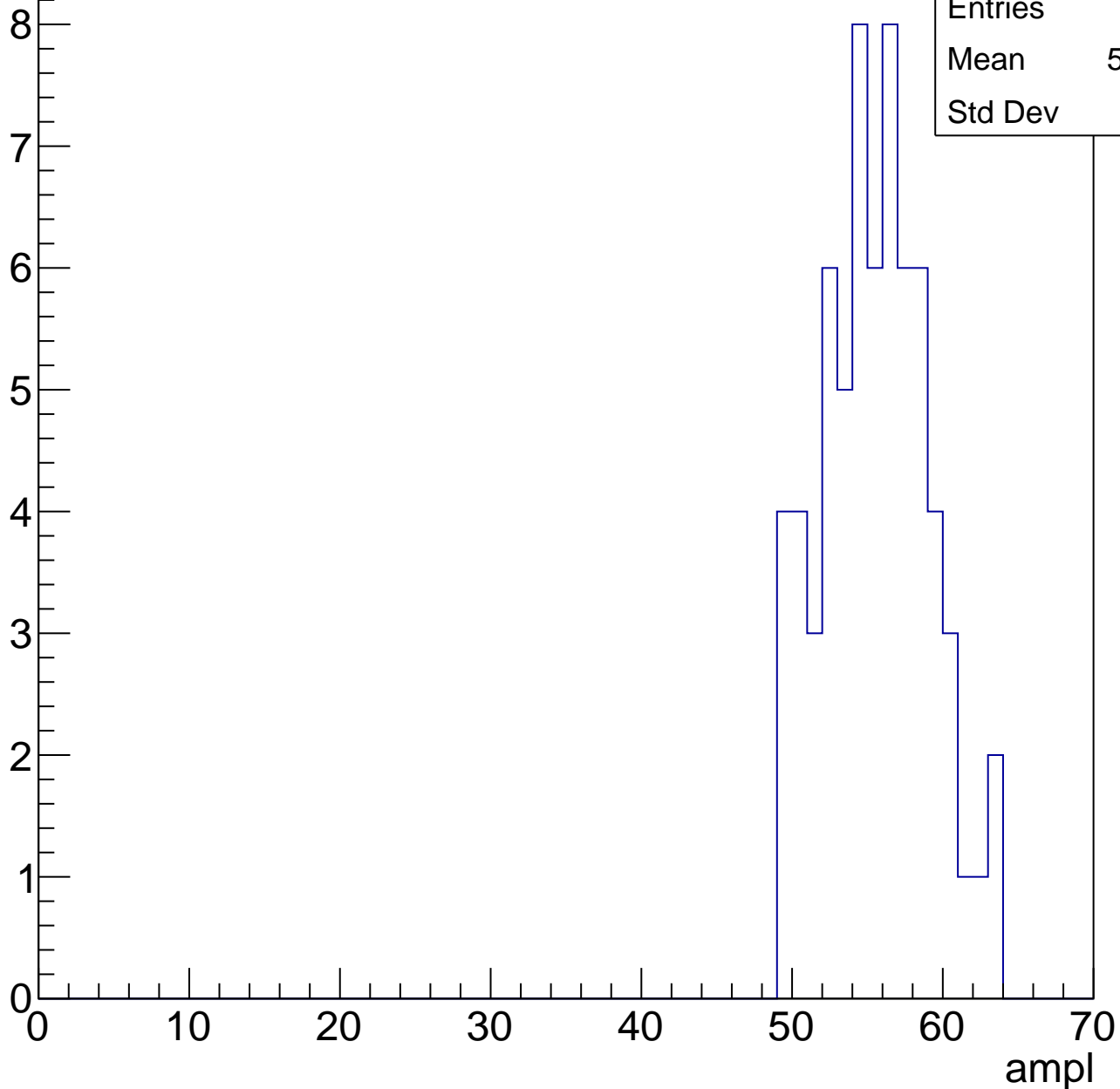


# B0L000S, U19-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	55.09
Std Dev	3.48

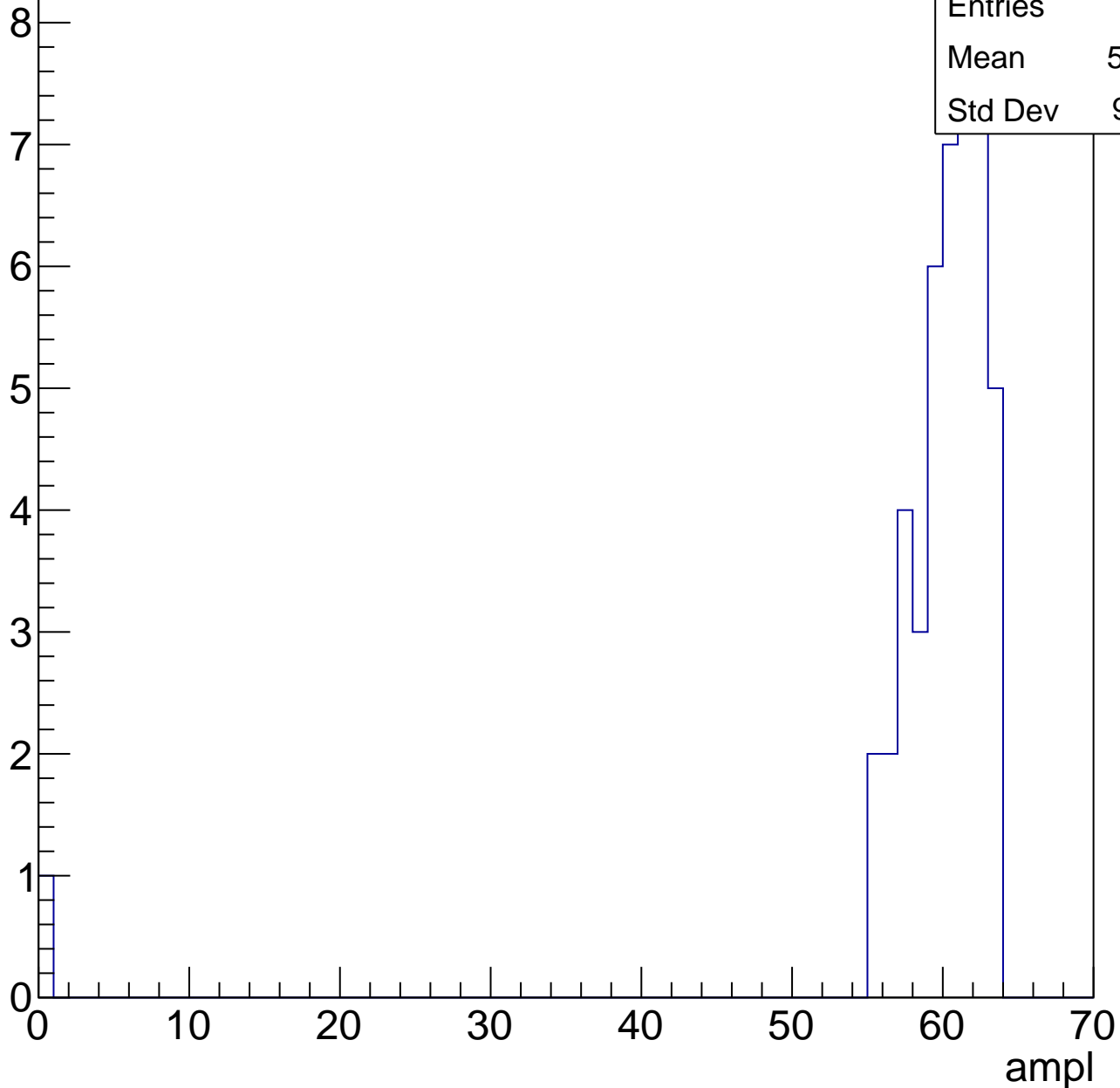


# B0L000S, U19-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

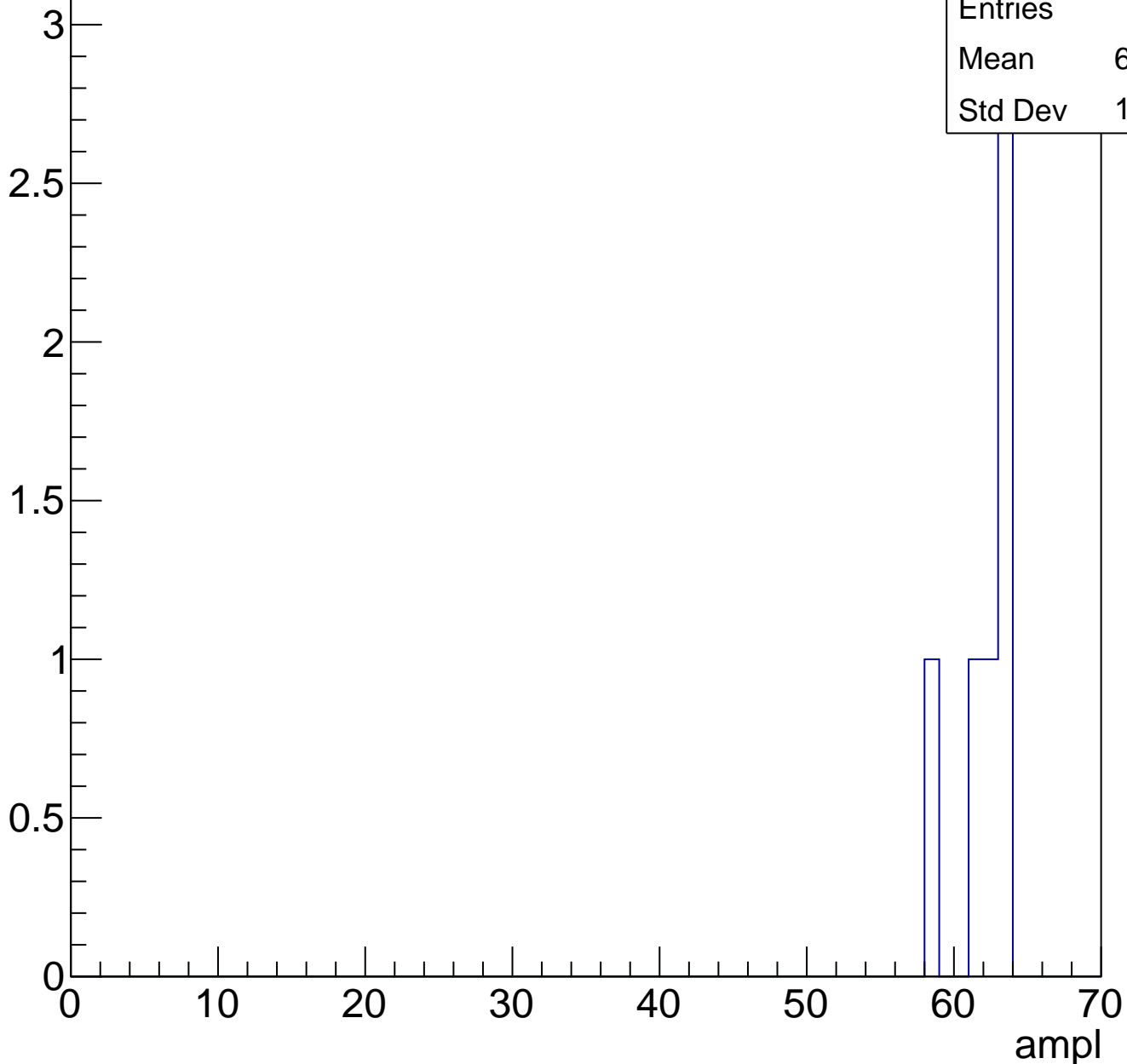
Entries	46
Mean	58.63
Std Dev	9.011



# B0L000S, U19-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch53, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	28.41
Std Dev	4.453

**Gaus mean : 29.2793**

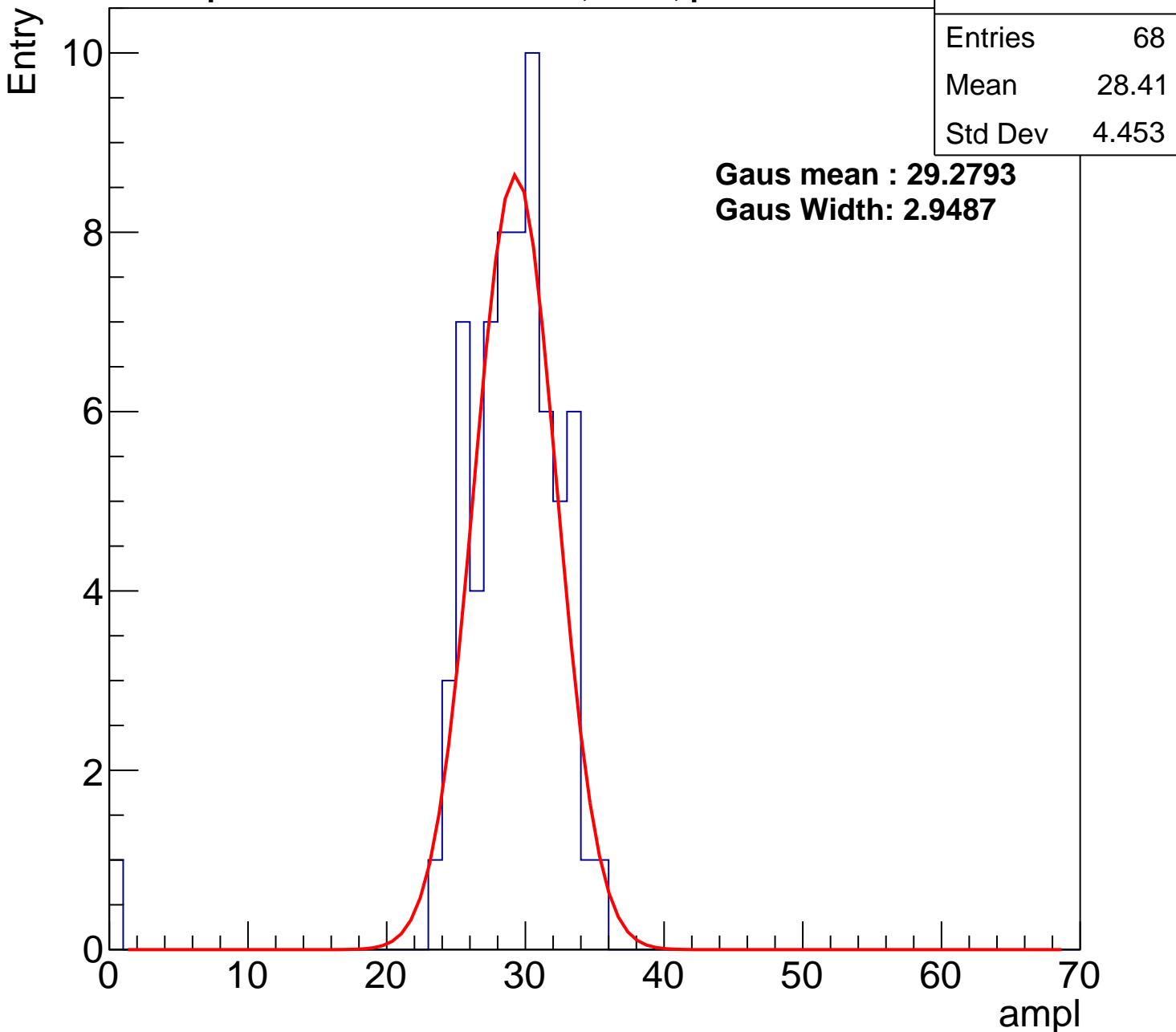
**Gaus Width: 2.9487**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

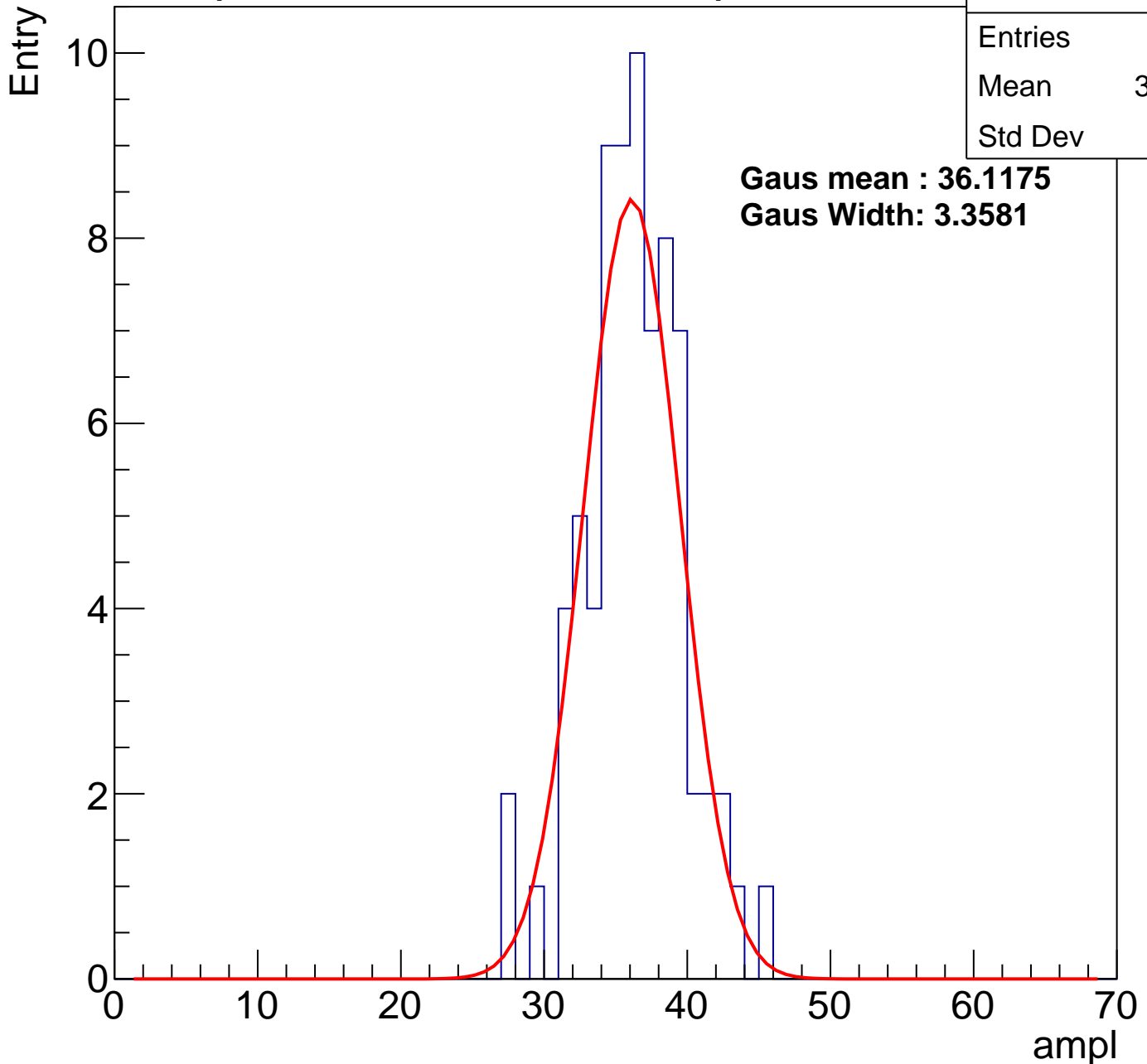


# B0L000S, U19-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	35.81
Std Dev	3.4

**Gaus mean : 36.1175**  
**Gaus Width: 3.3581**



# B0L000S, U19-ch53, adc2

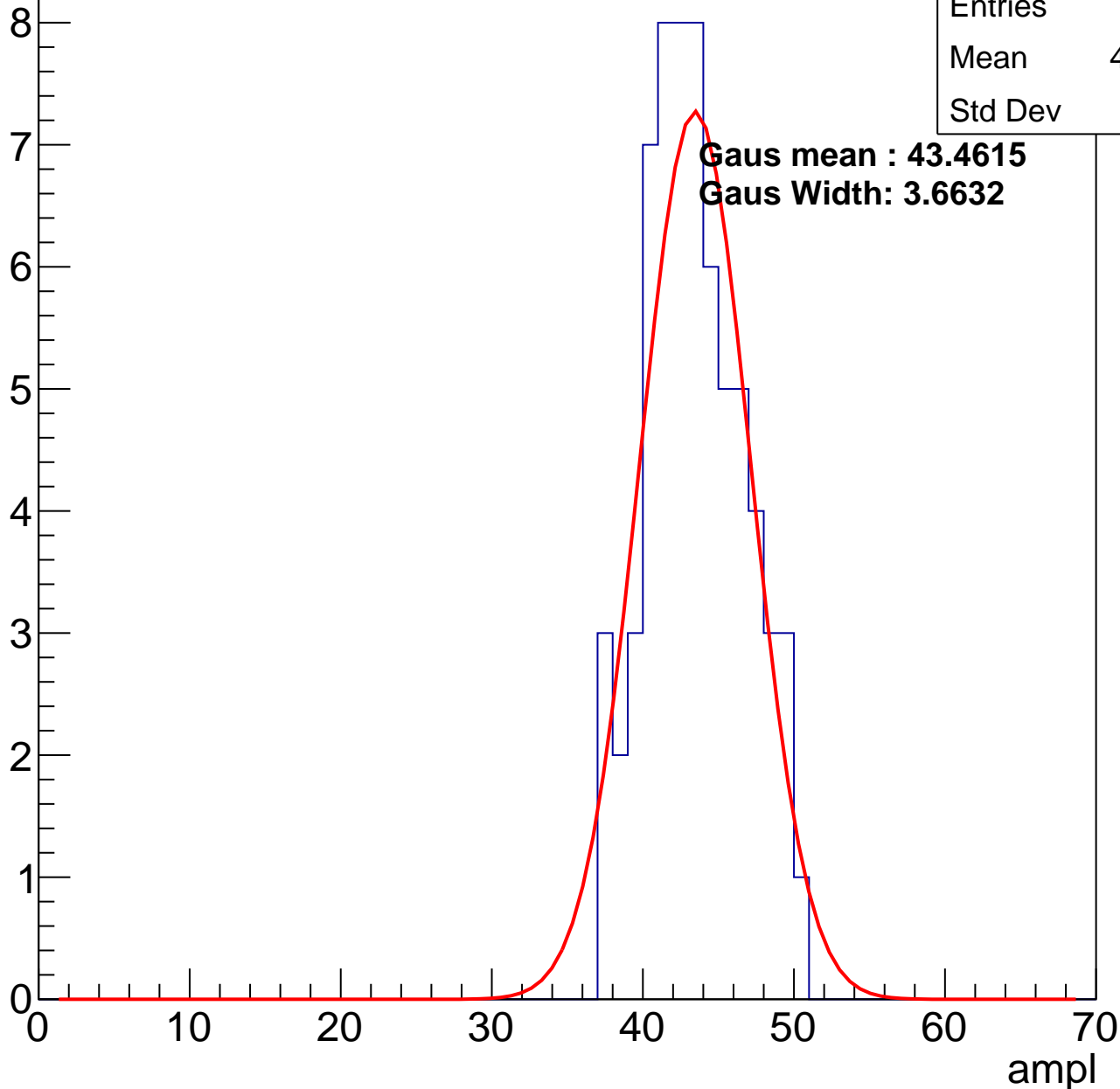
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.03
Std Dev	3.2

**Gaus mean : 43.4615**

**Gaus Width: 3.6632**

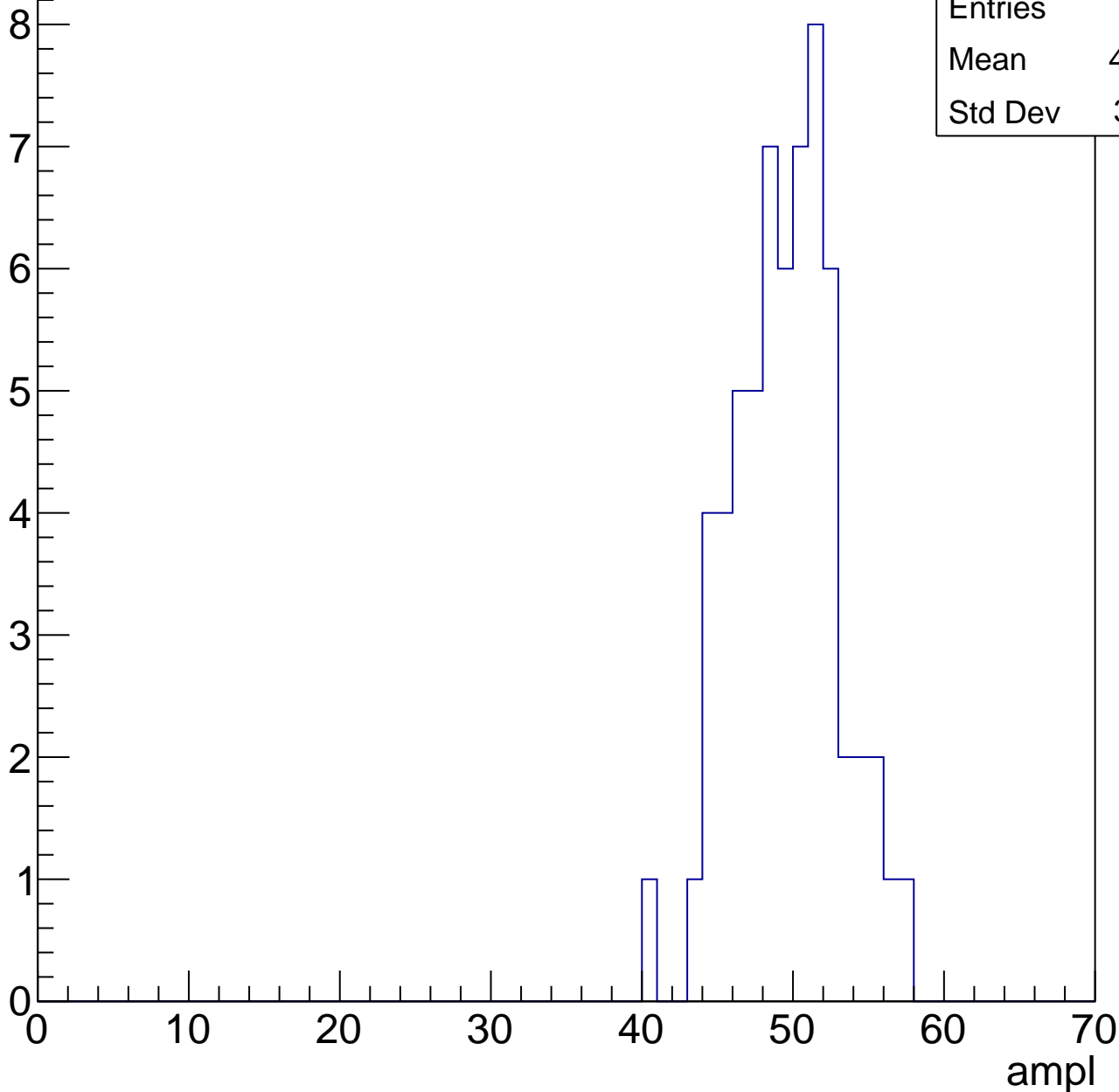


# B0L000S, U19-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	49.05
Std Dev	3.391

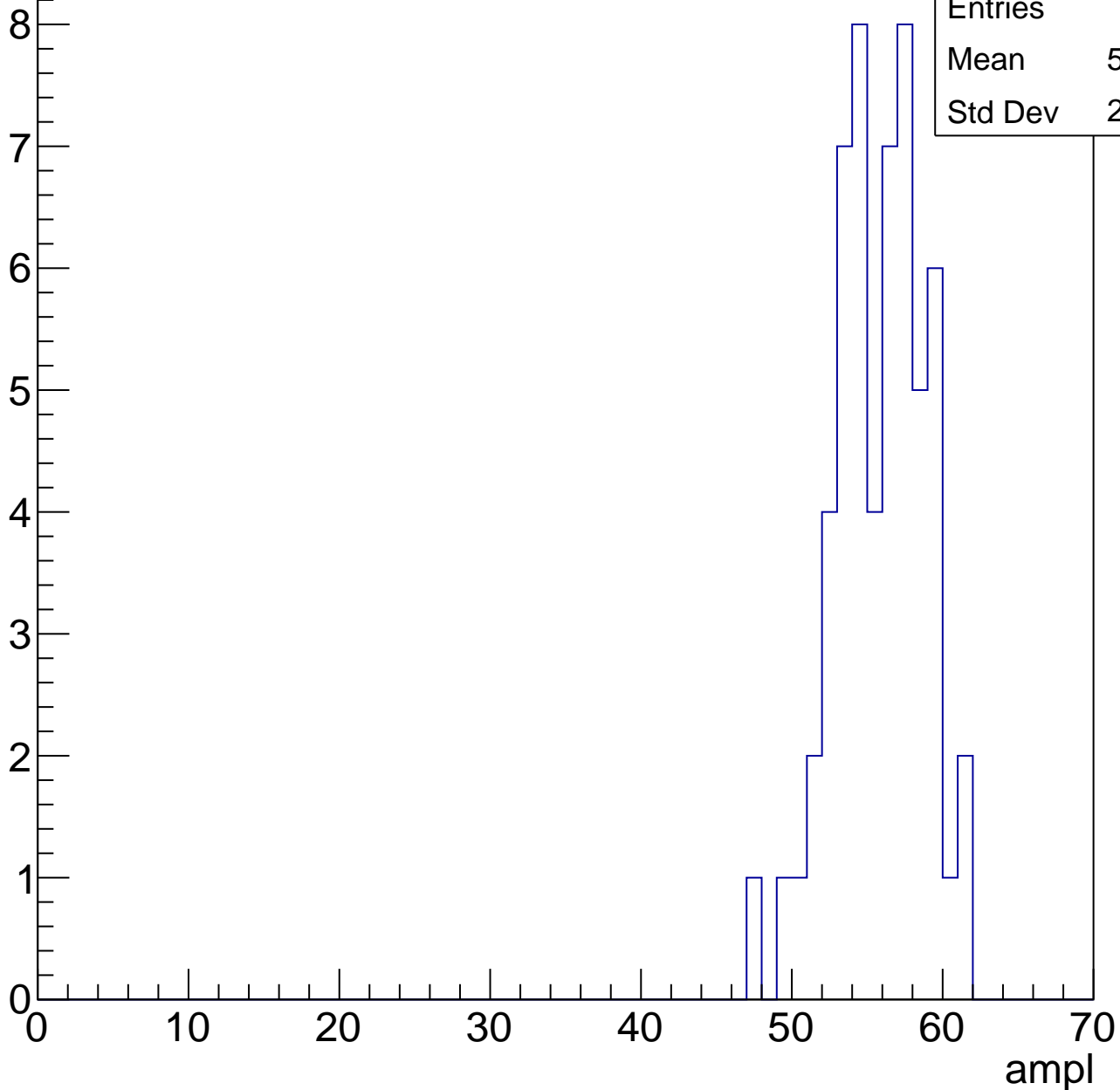


# B0L000S, U19-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	55.32
Std Dev	2.963



# B0L000S, U19-ch53, adc5

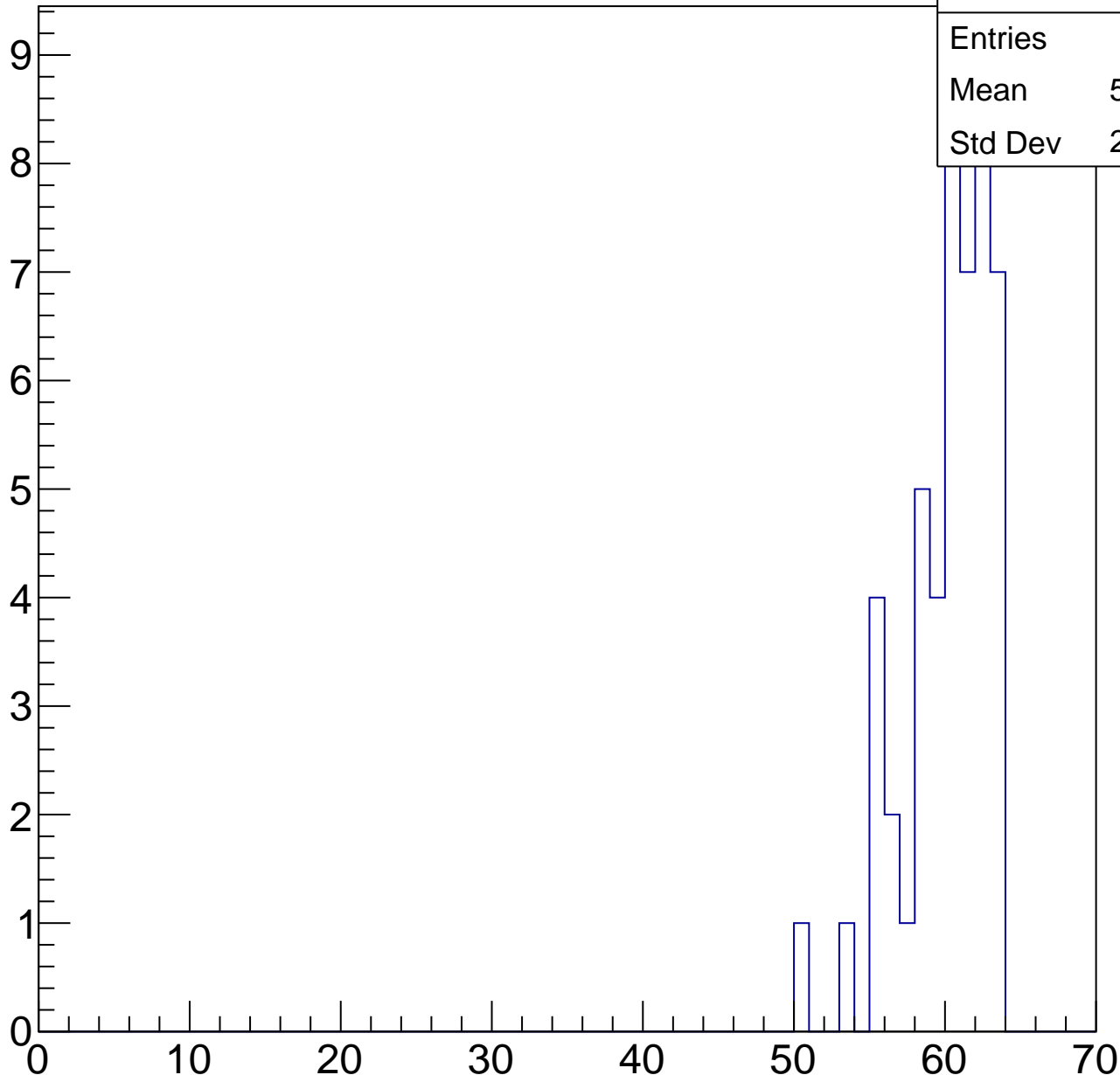
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	59.67
Std Dev	2.909

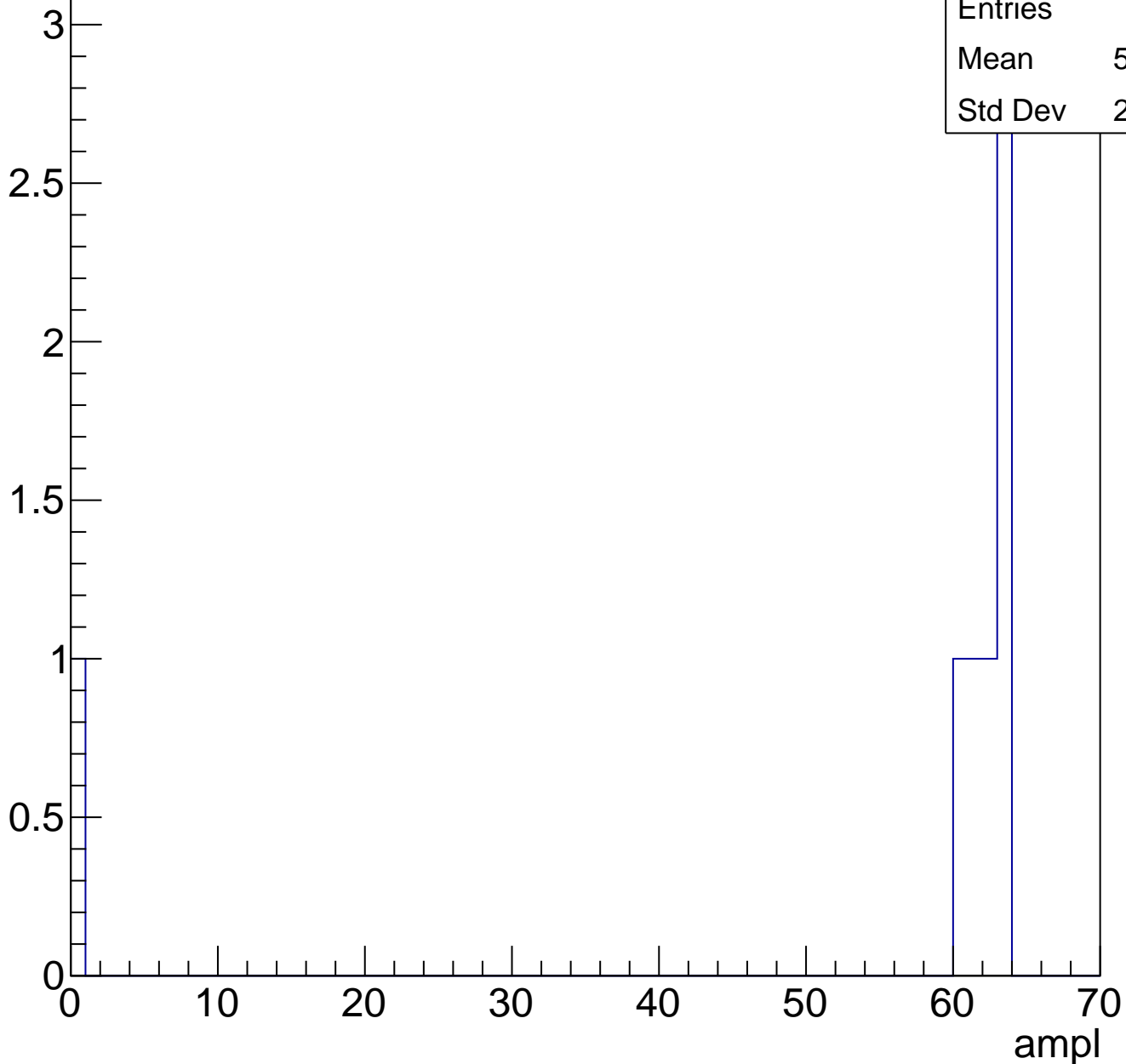
ampl



# B0L000S, U19-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch54, adc0

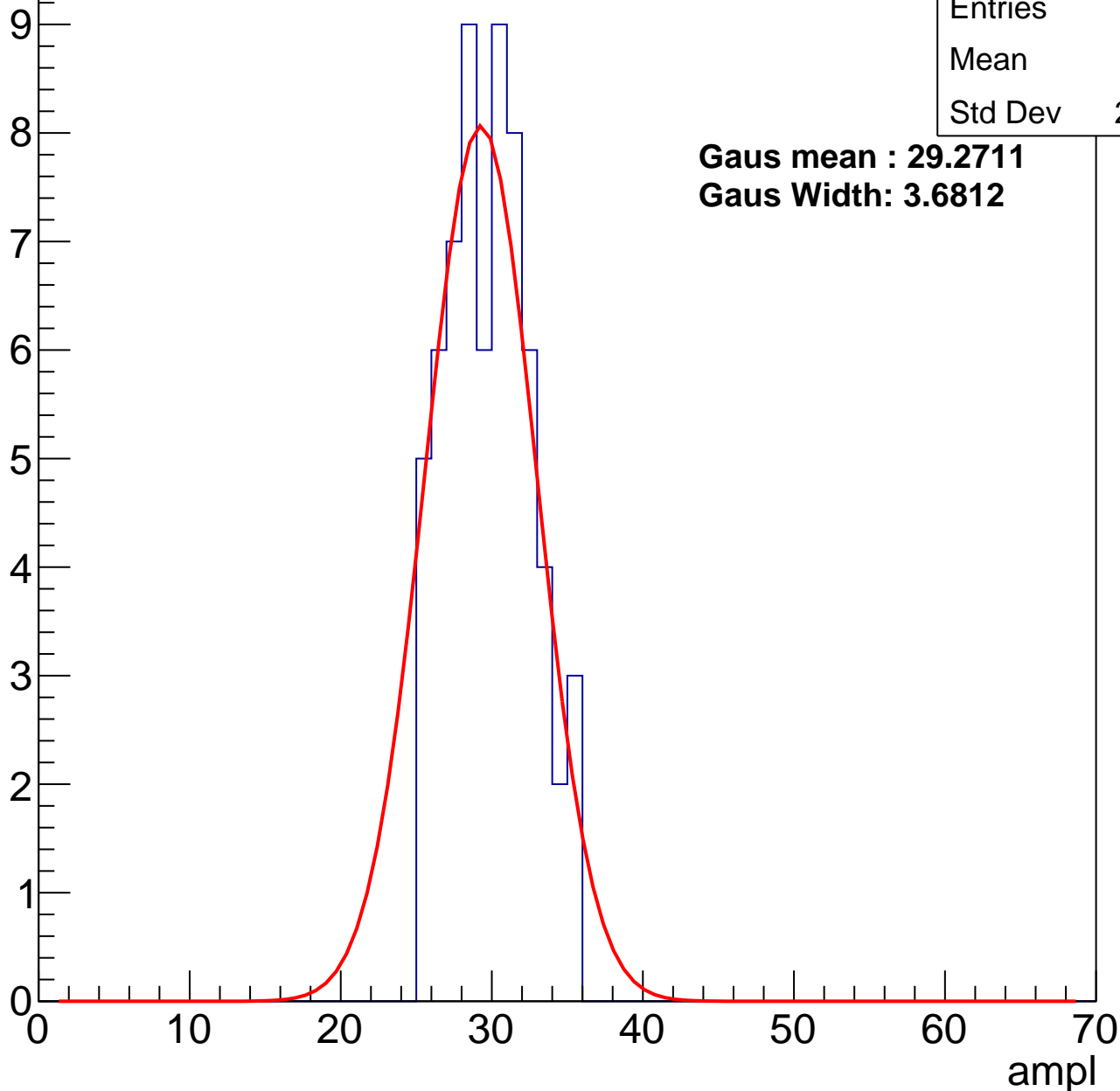
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.4
Std Dev	2.711

**Gaus mean : 29.2711**

**Gaus Width: 3.6812**



# B0L000S, U19-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	35.88
Std Dev	3.279

**Gaus mean : 36.4468**

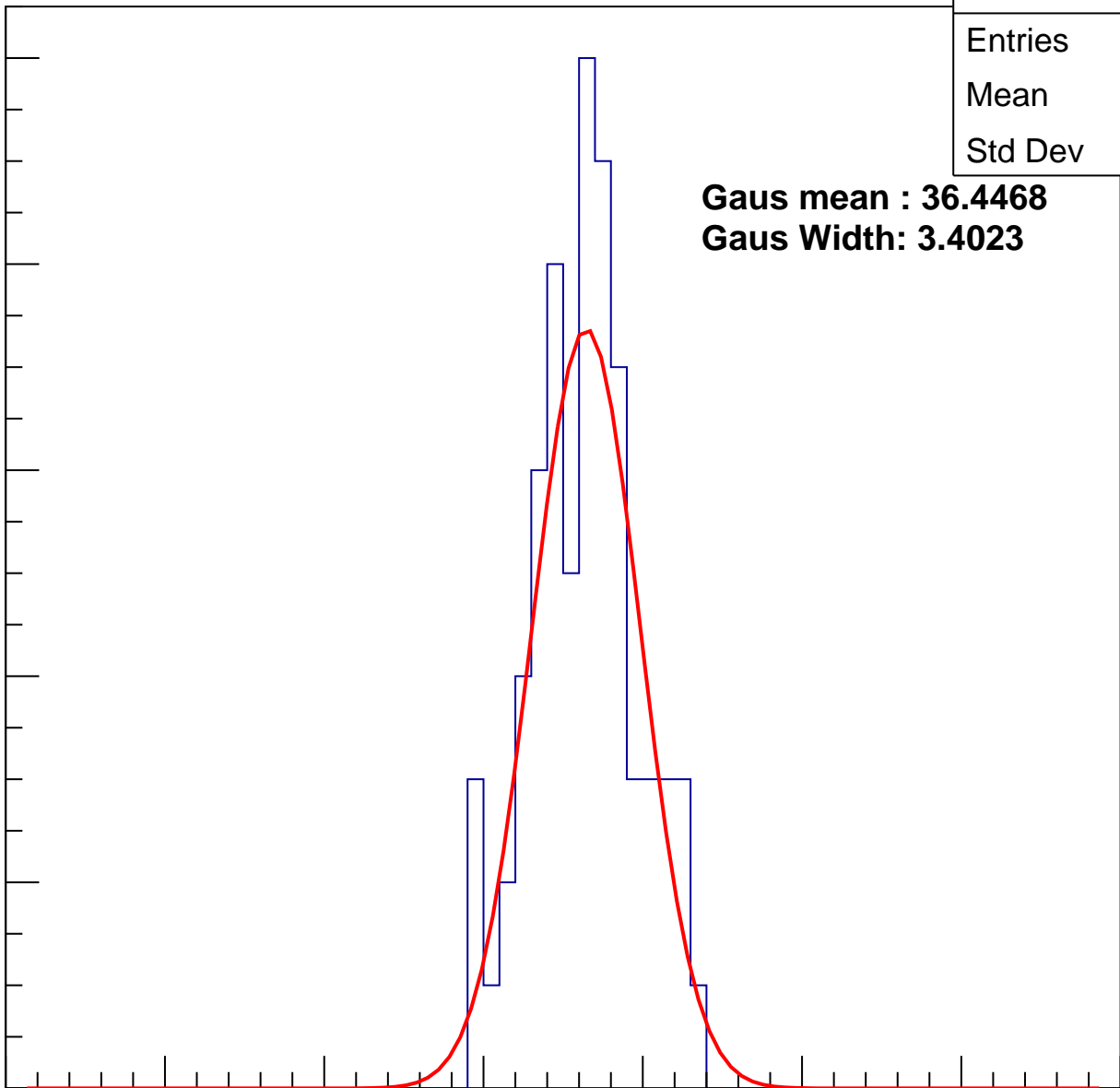
**Gaus Width: 3.4023**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch54, adc2

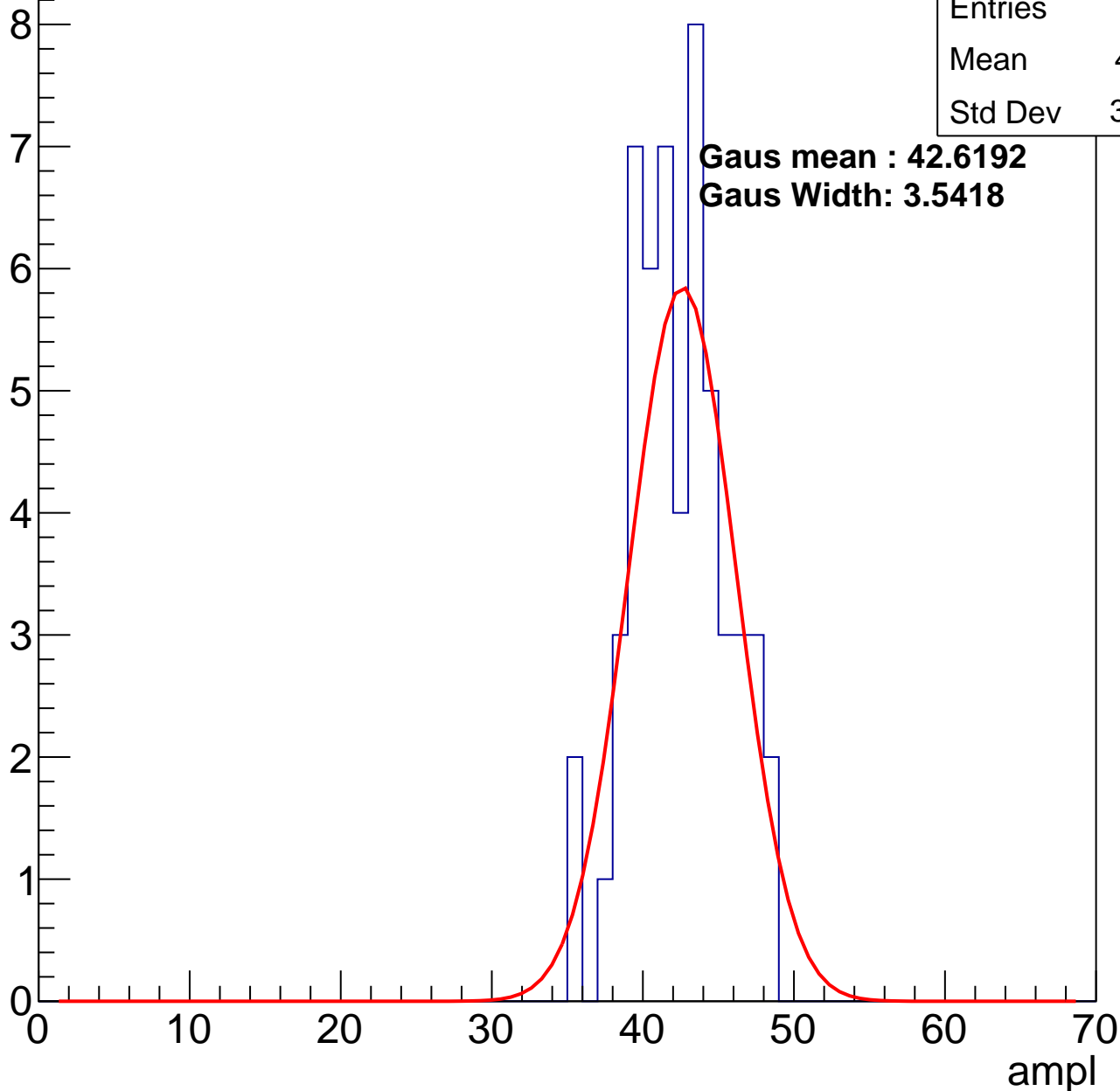
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	41.91
Std Dev	3.087

**Gaus mean : 42.6192**

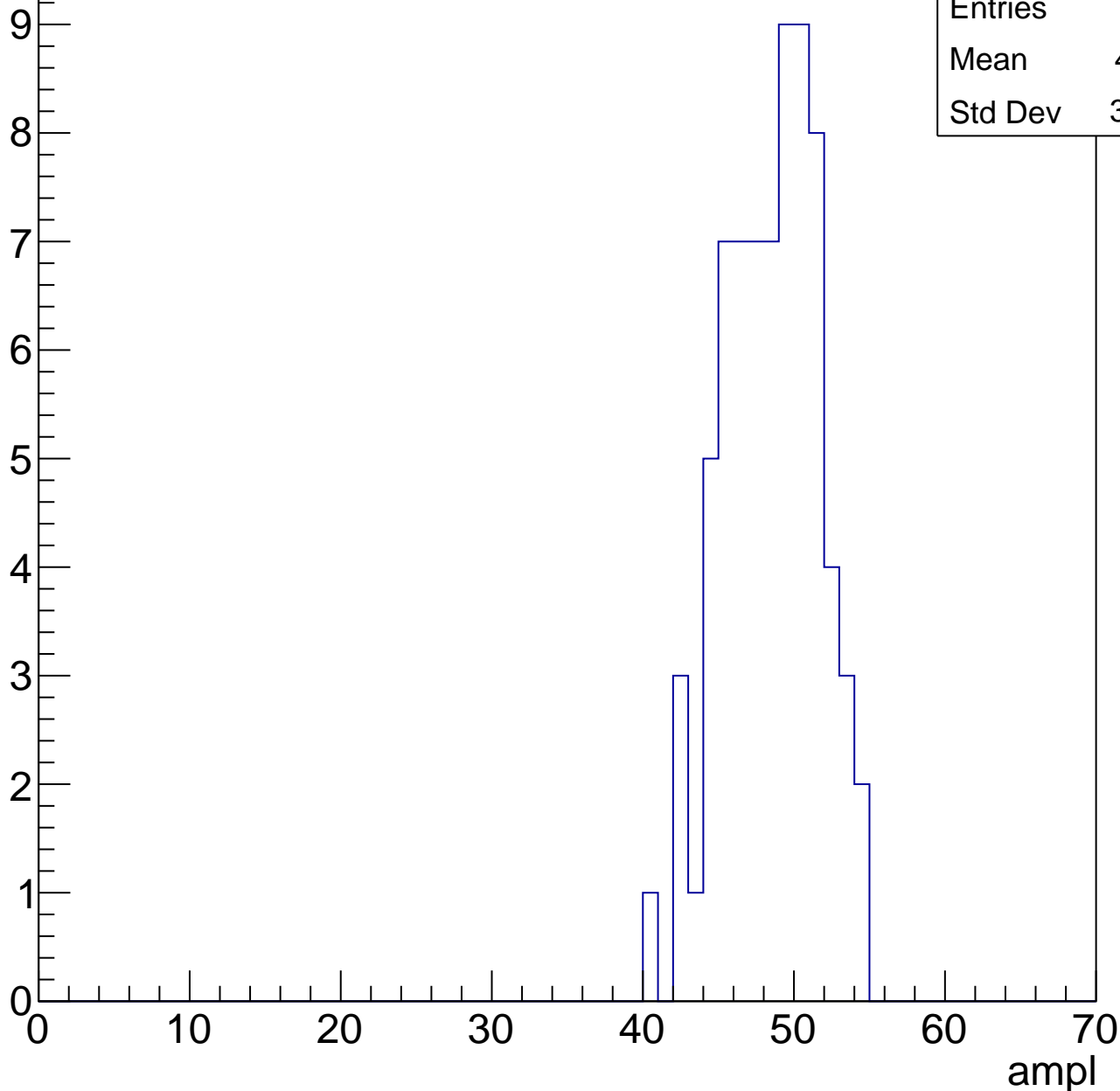
**Gaus Width: 3.5418**



# B0L000S, U19-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



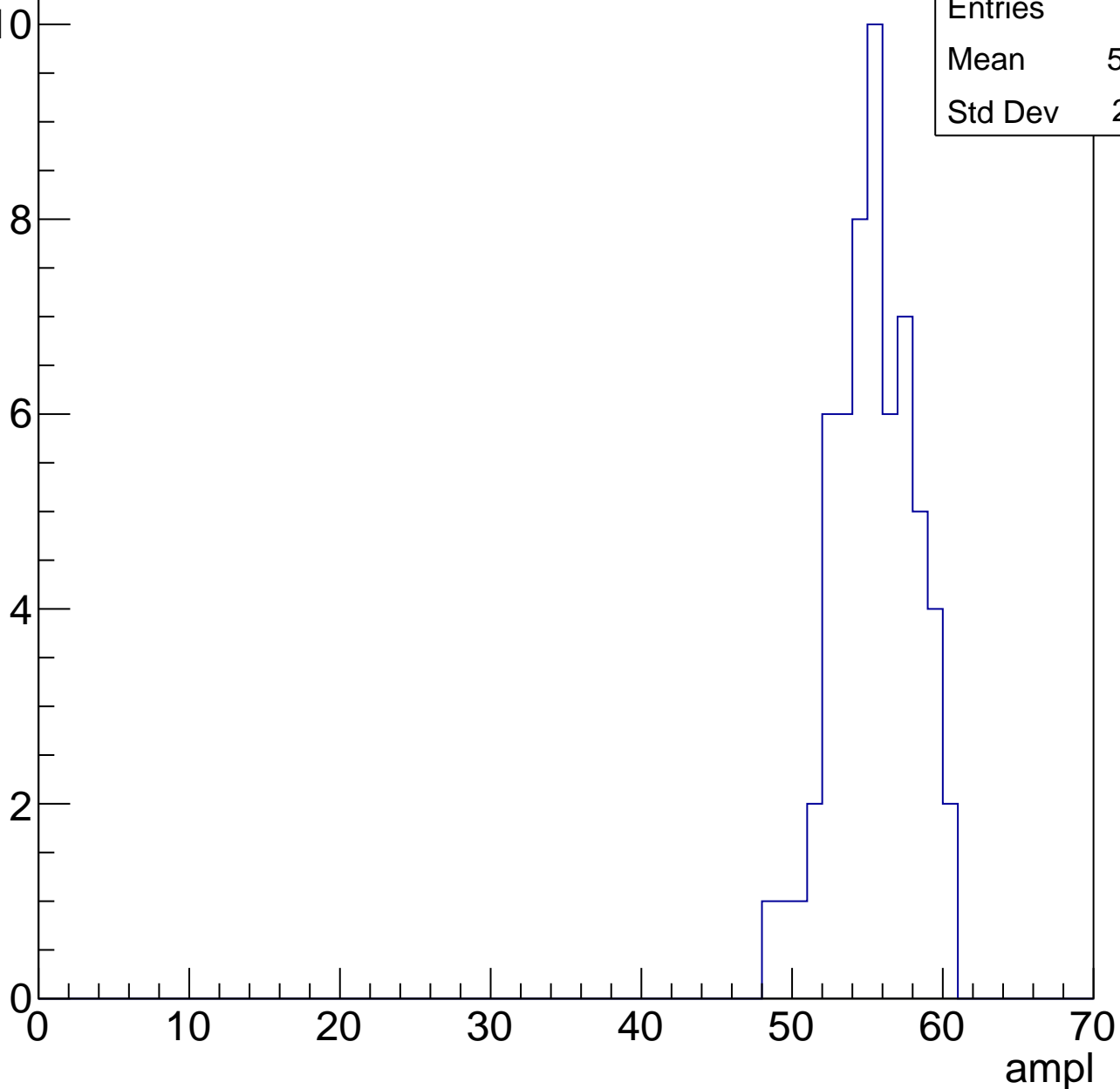
Entries	73
Mean	48.01
Std Dev	3.103

# B0L000S, U19-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54.95
Std Dev	2.671

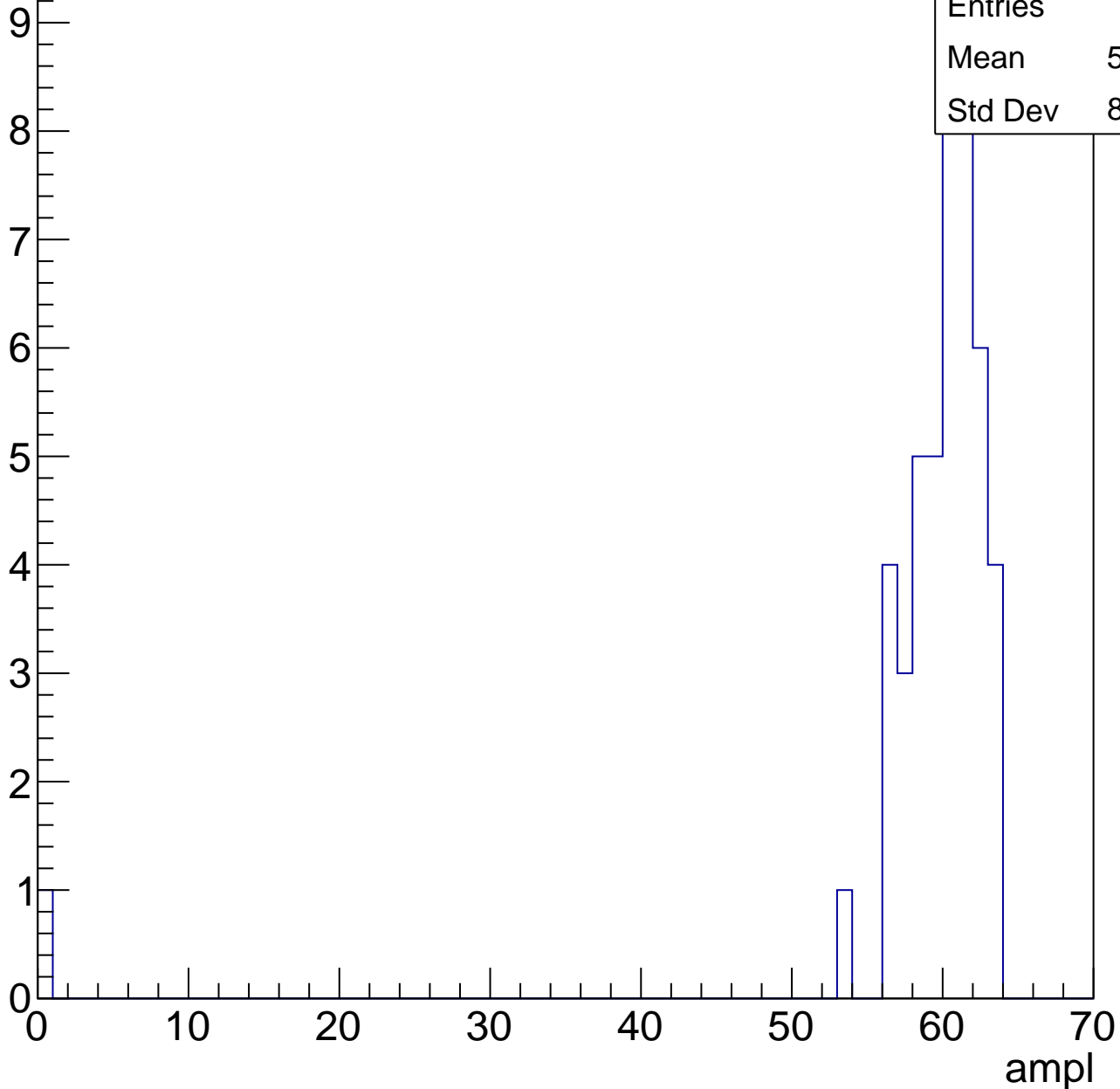


# B0L000S, U19-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.39
Std Dev	8.987

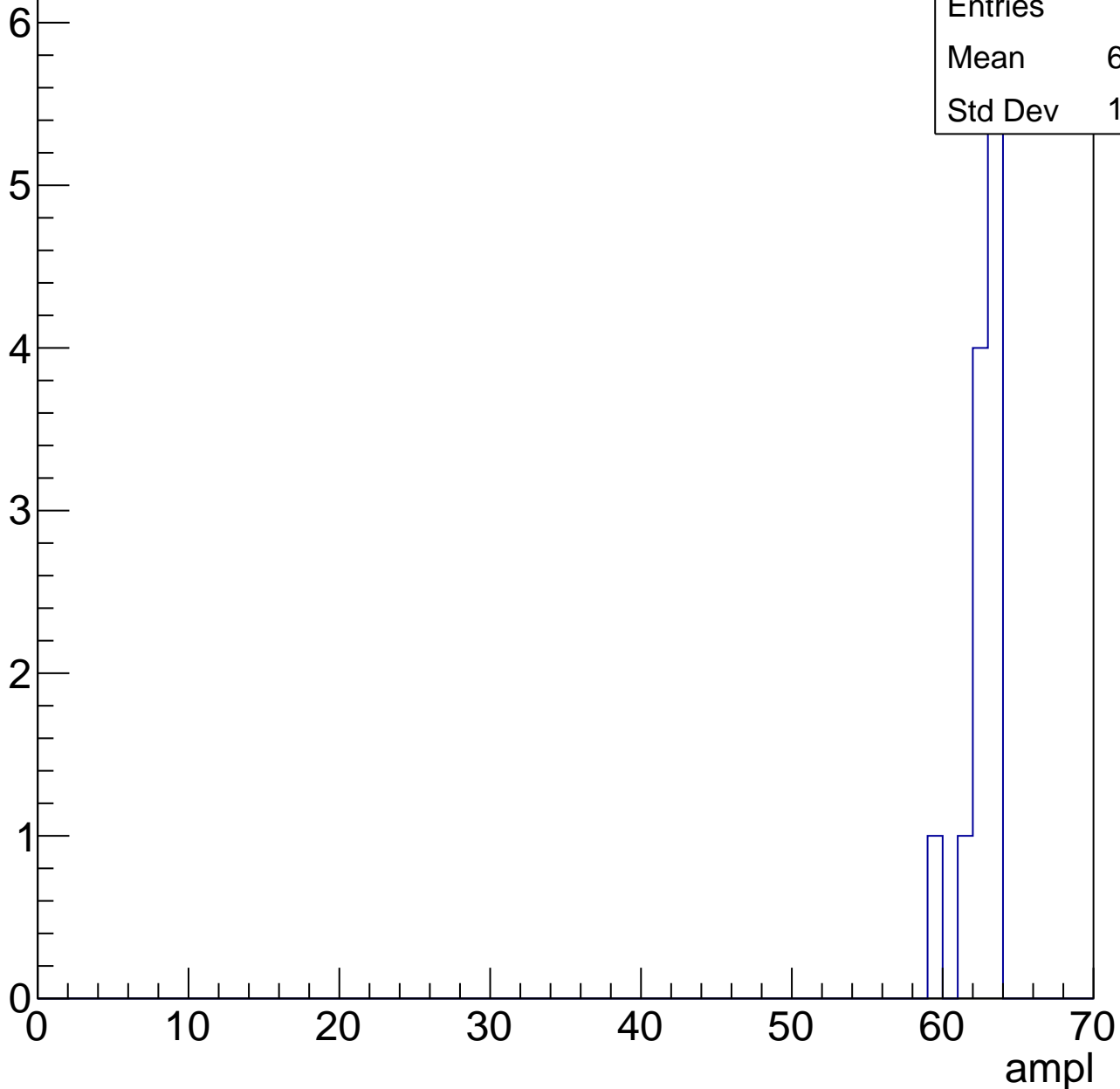


# B0L000S, U19-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	62.17
Std Dev	1.143





# B0L000S, U19-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch55, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	28.56
Std Dev	6.64

**Gaus mean : 30.2168**

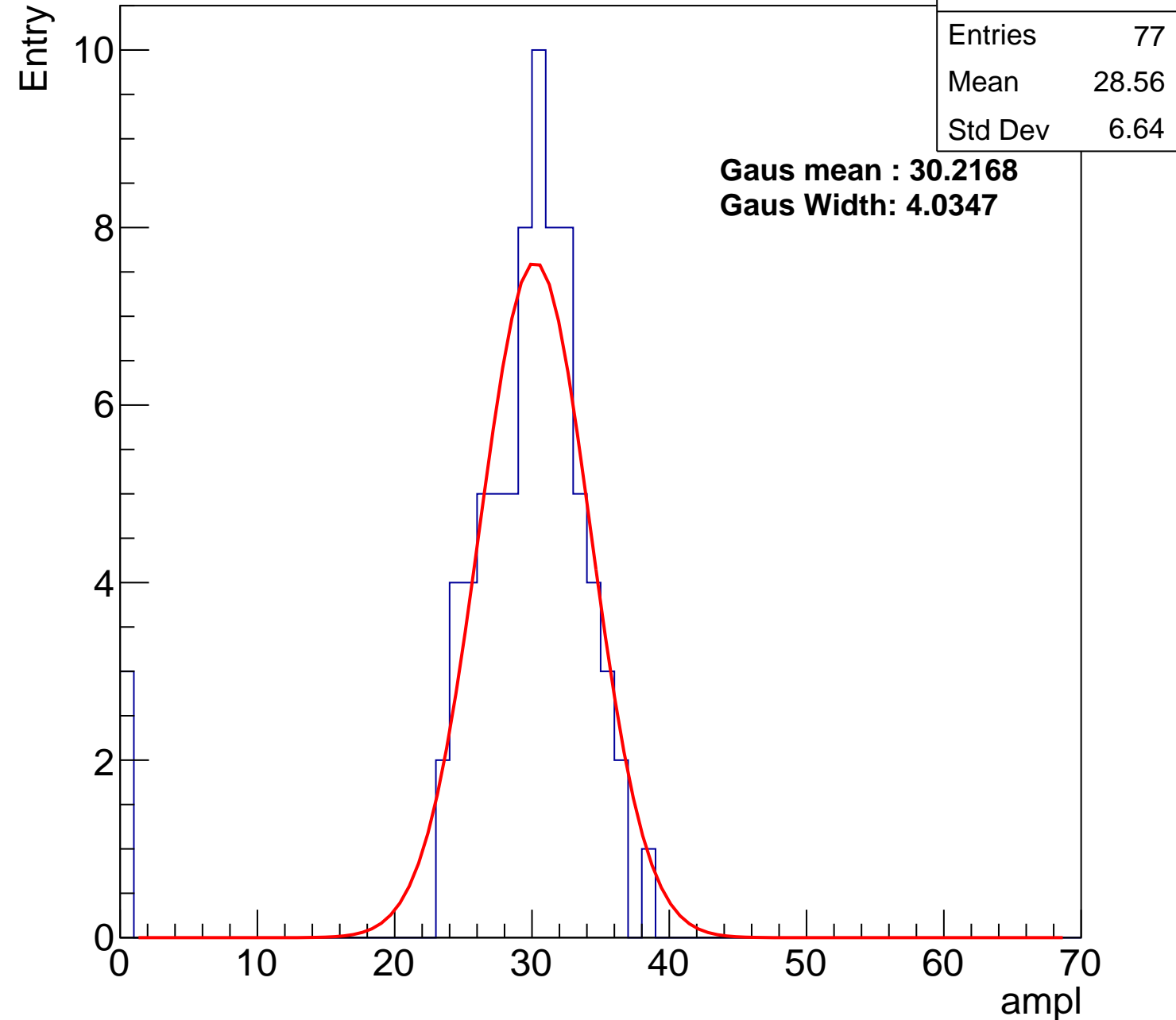
**Gaus Width: 4.0347**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch55, adc1

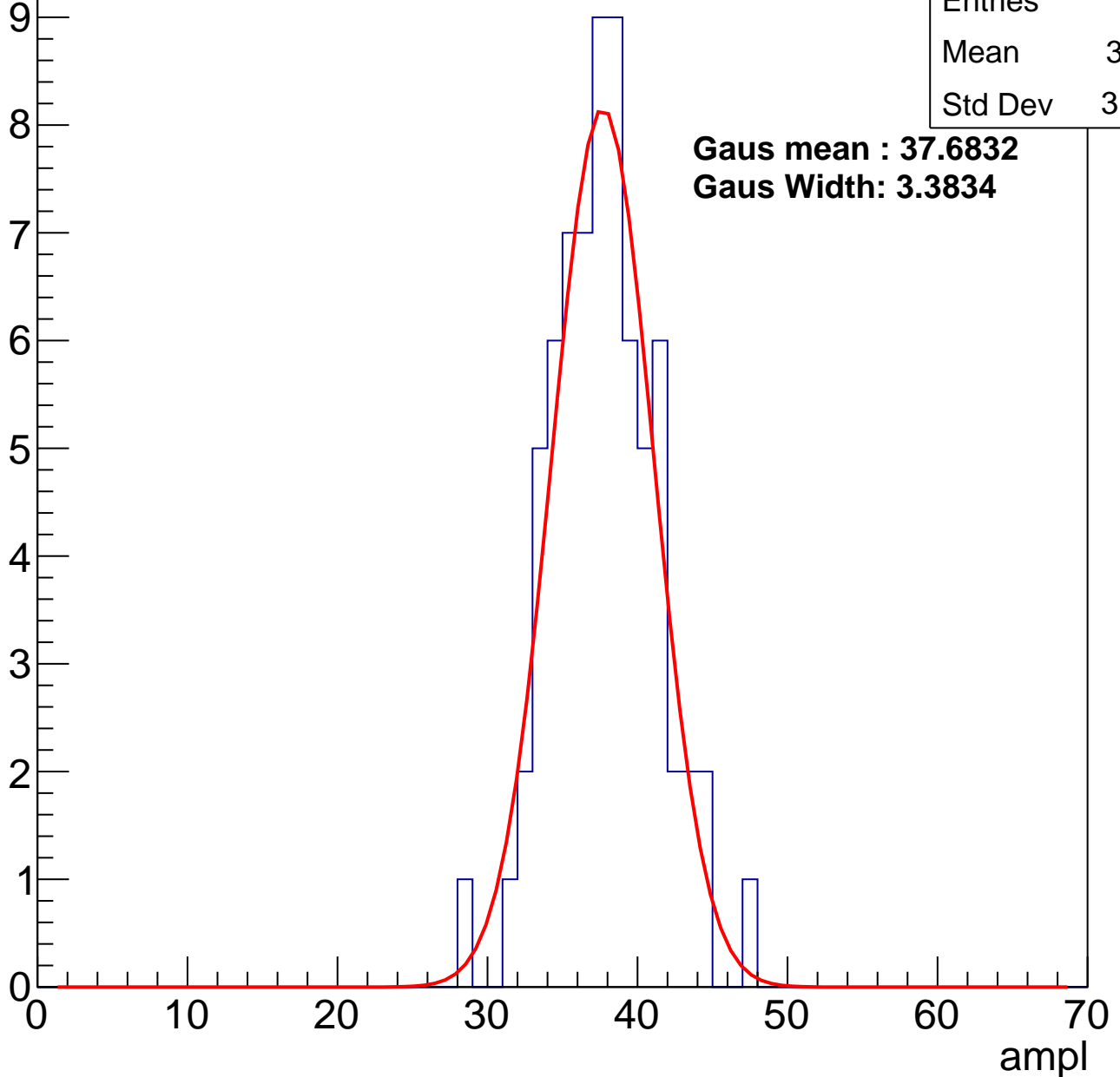
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.31
Std Dev	3.405

**Gaus mean : 37.6832**

**Gaus Width: 3.3834**



# B0L000S, U19-ch55, adc2

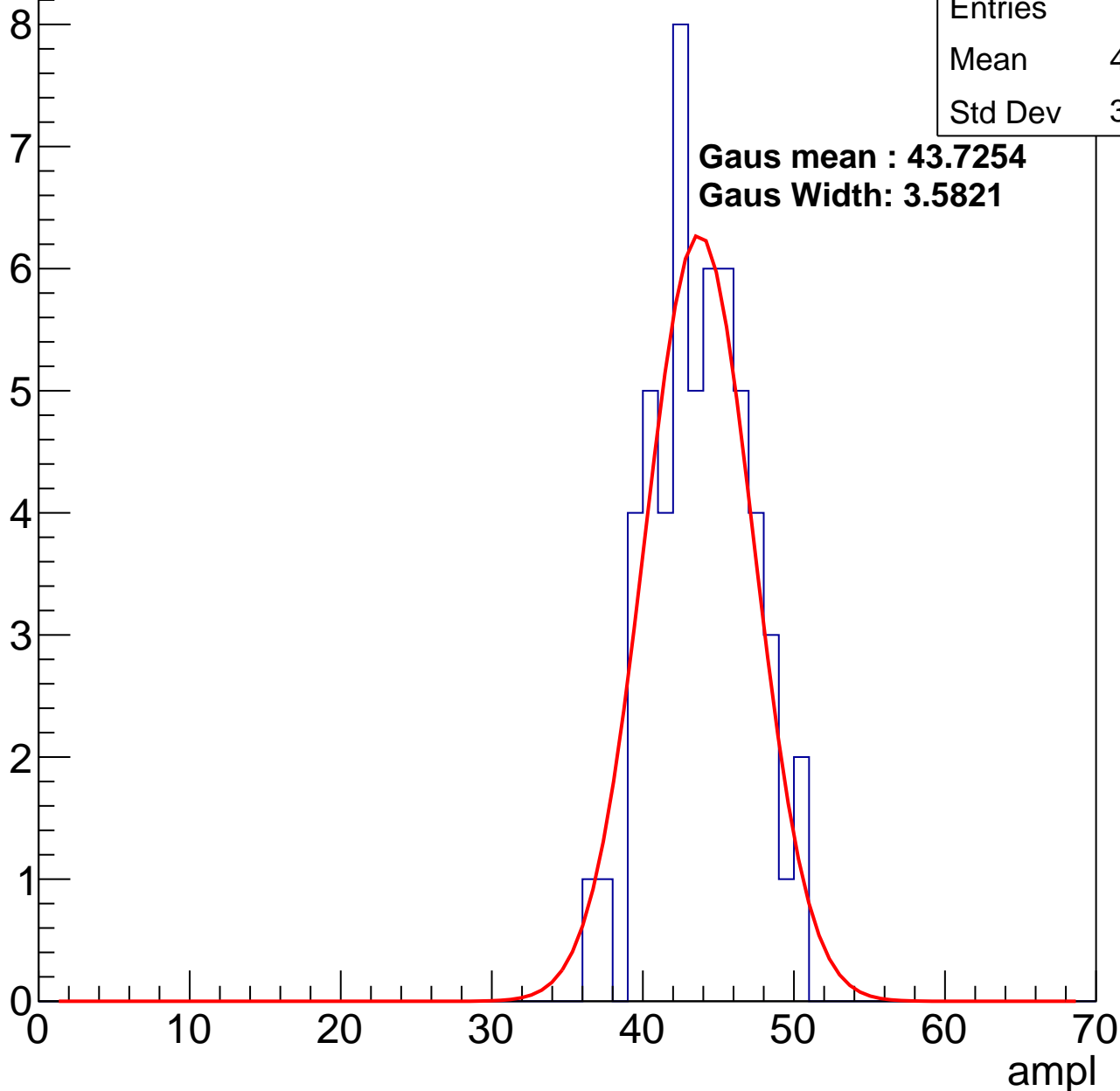
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	43.44
Std Dev	3.178

**Gaus mean : 43.7254**

**Gaus Width: 3.5821**

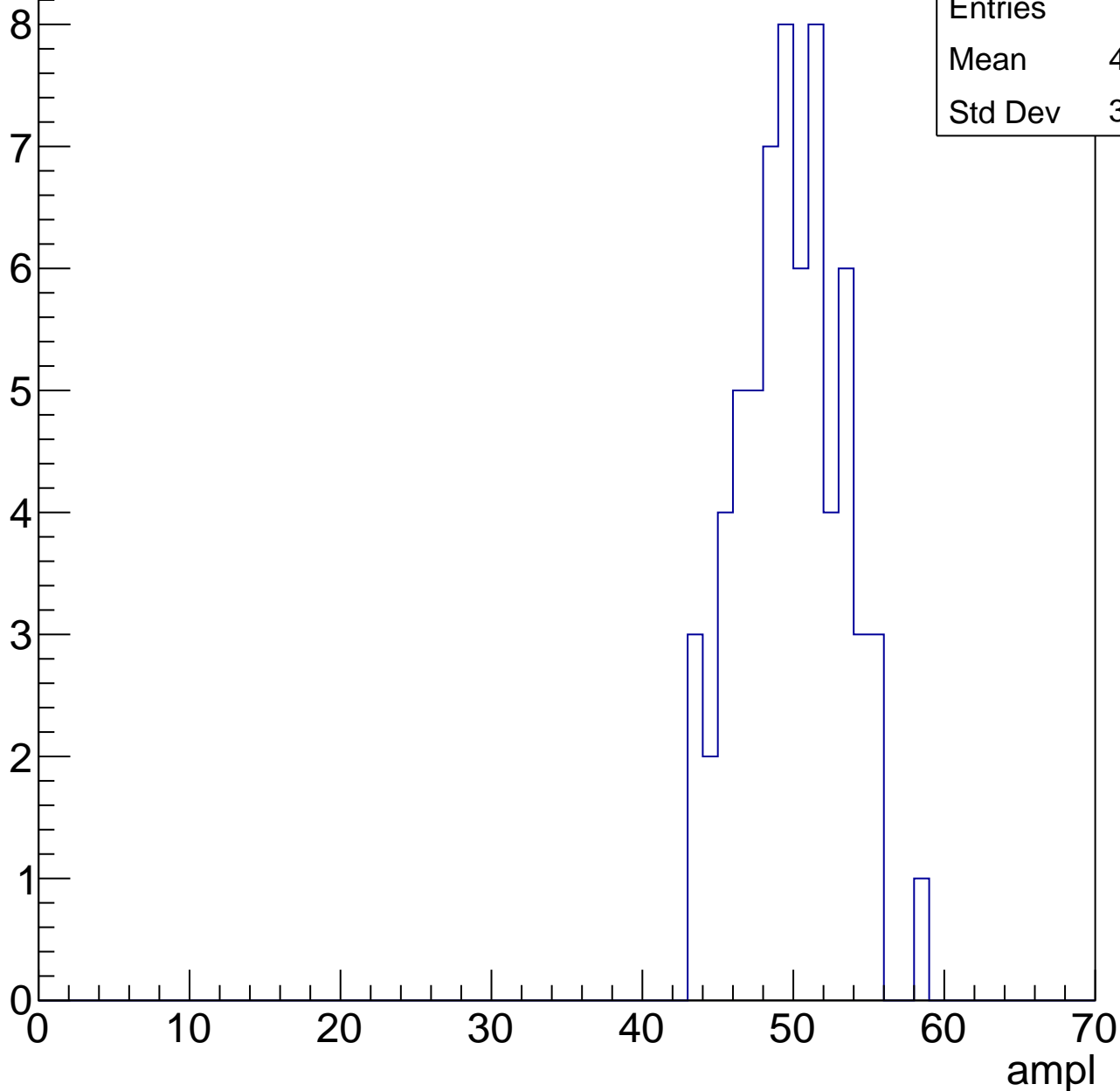


# B0L000S, U19-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

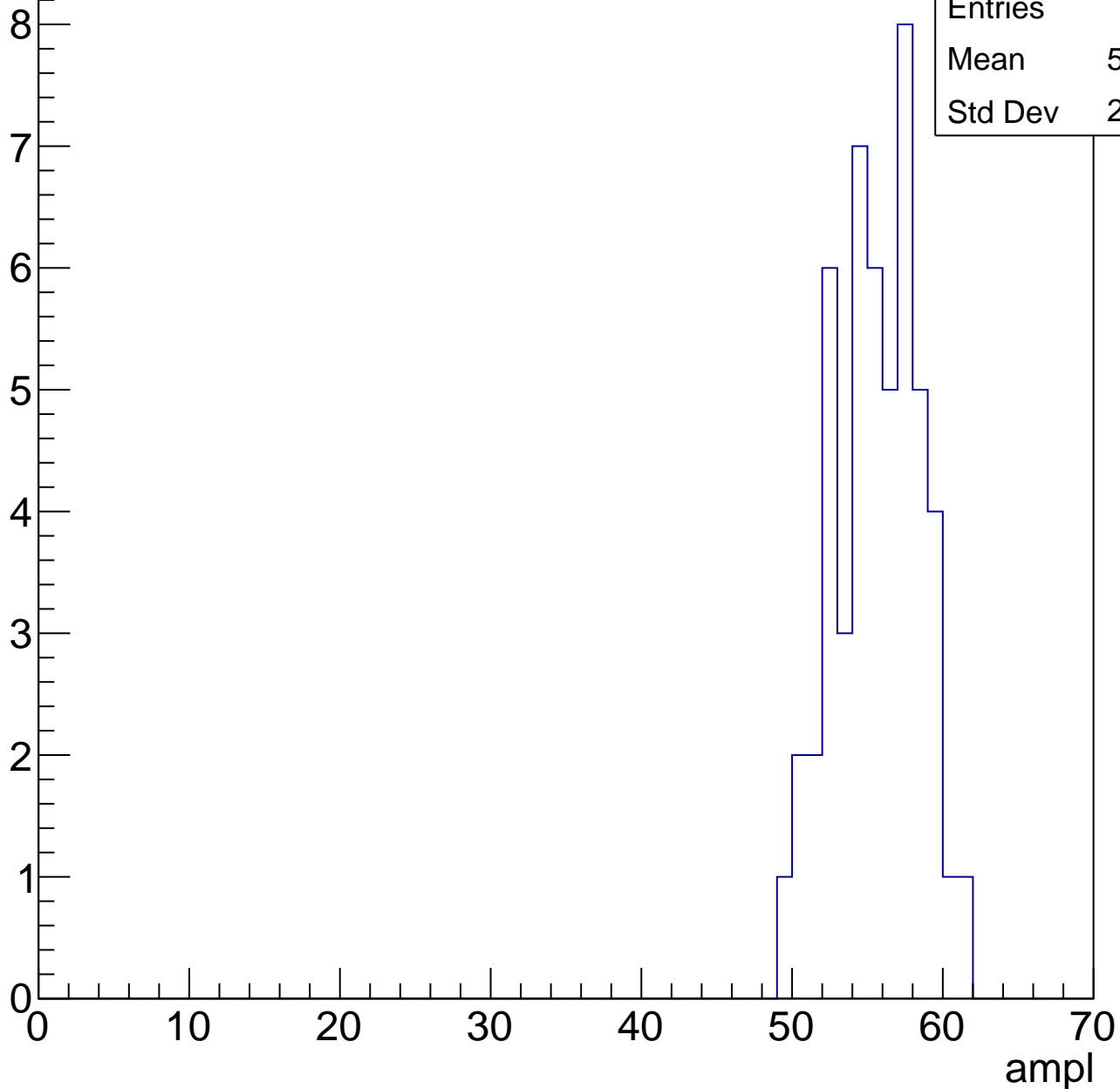
Entries	65
Mean	49.37
Std Dev	3.326



# B0L000S, U19-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

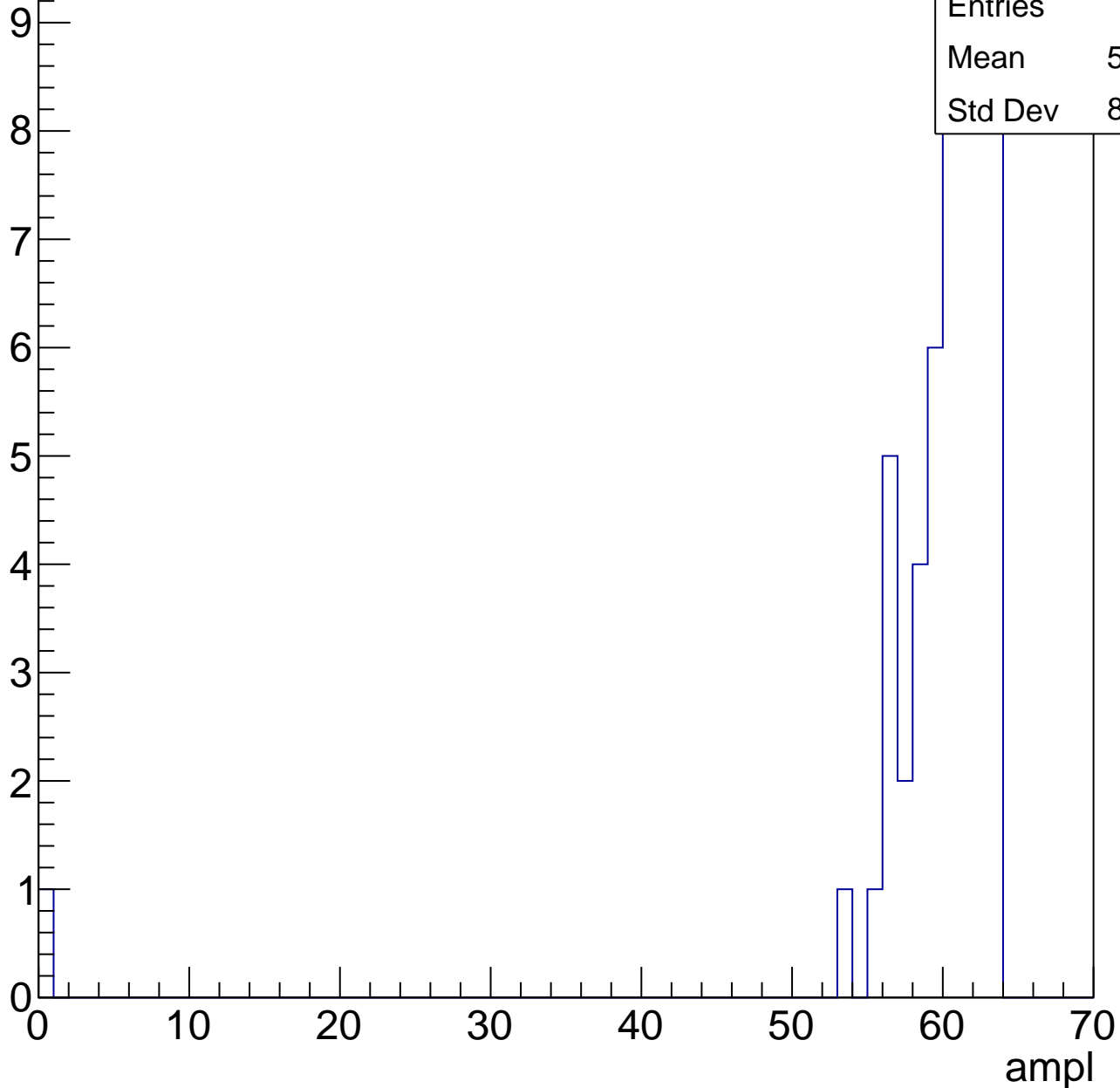
Entry



# B0L000S, U19-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

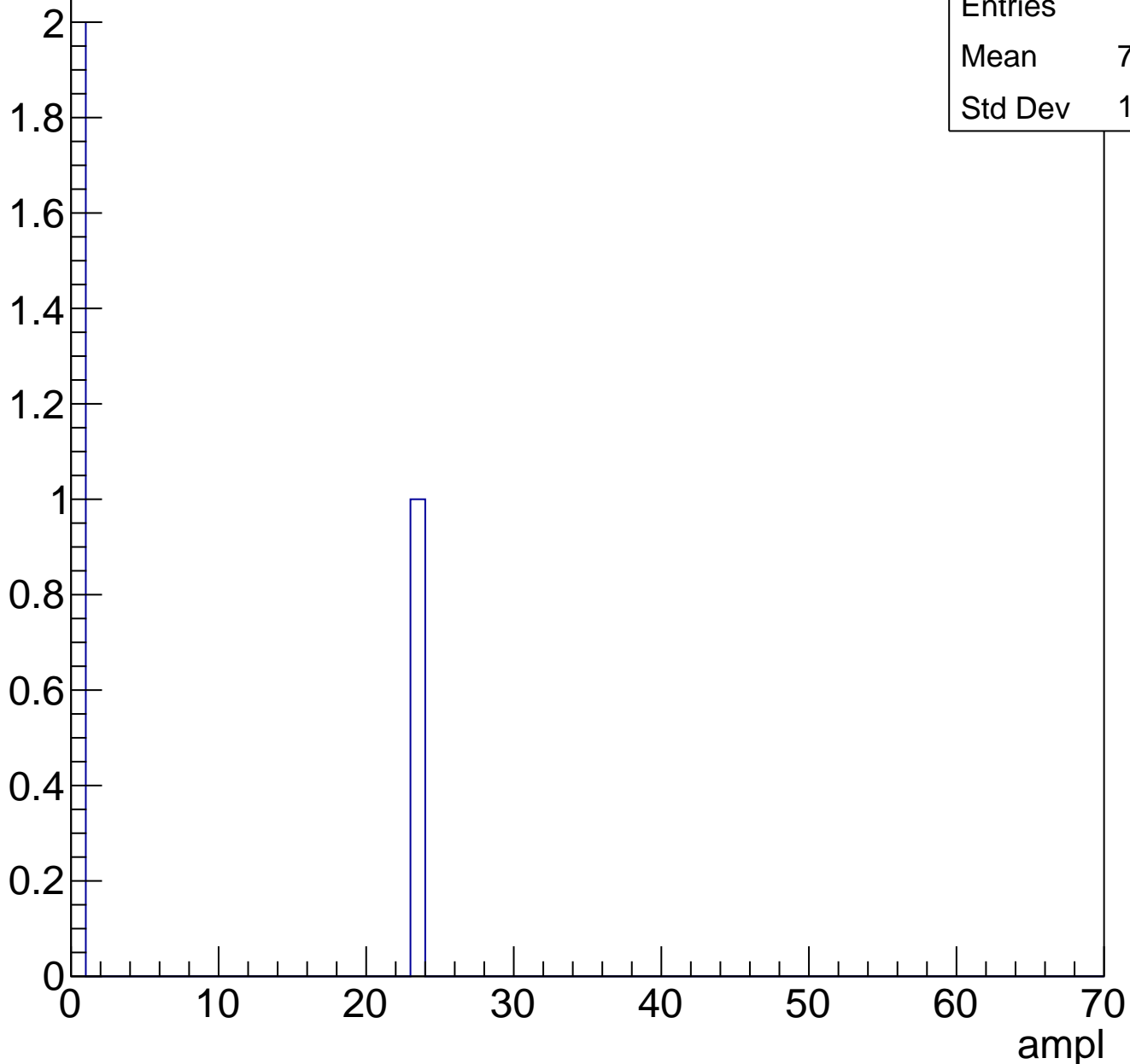




# B0L000S, U19-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U19-ch56, adc0

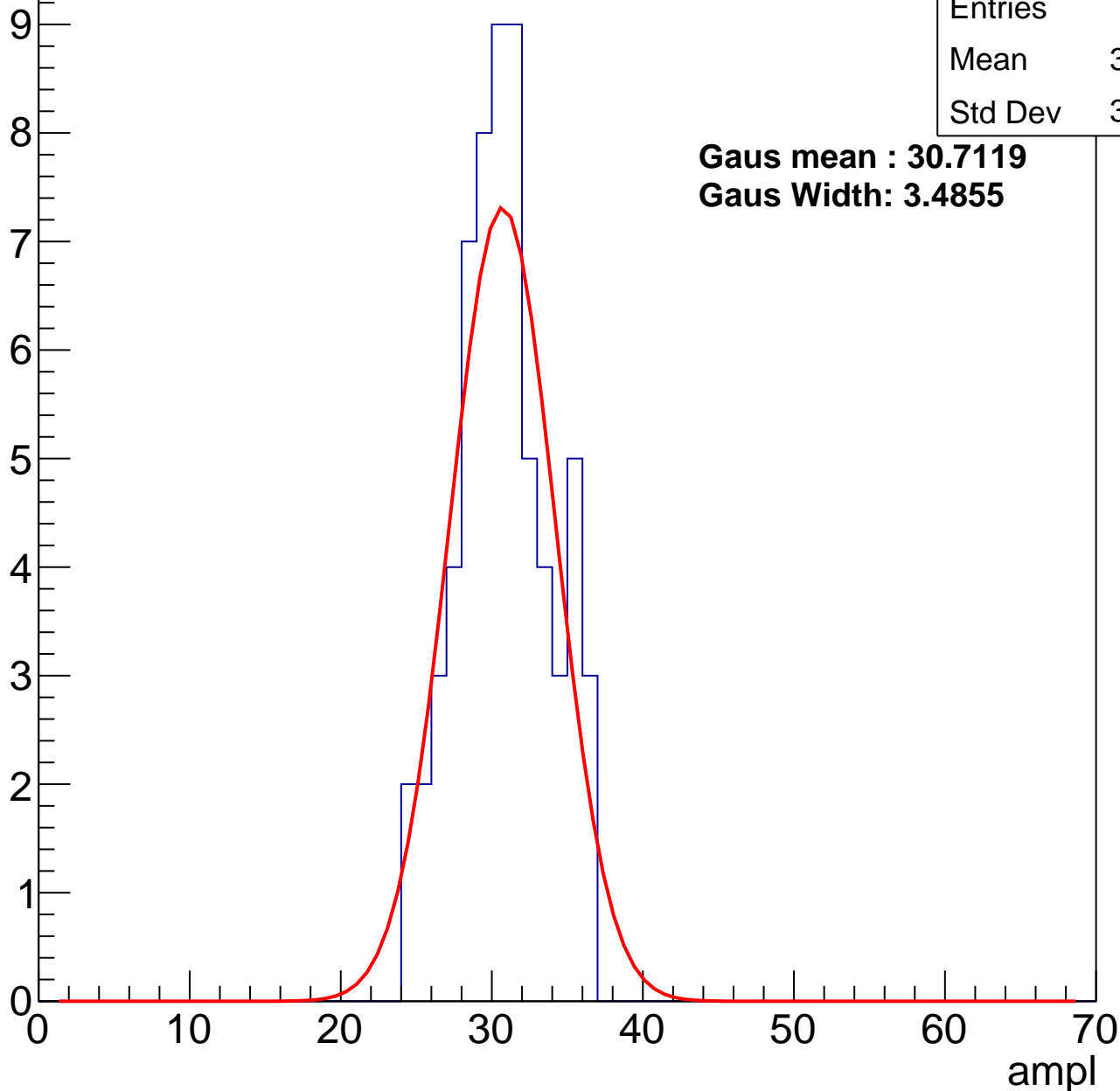
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.28
Std Dev	3.018

**Gaus mean : 30.7119**

**Gaus Width: 3.4855**



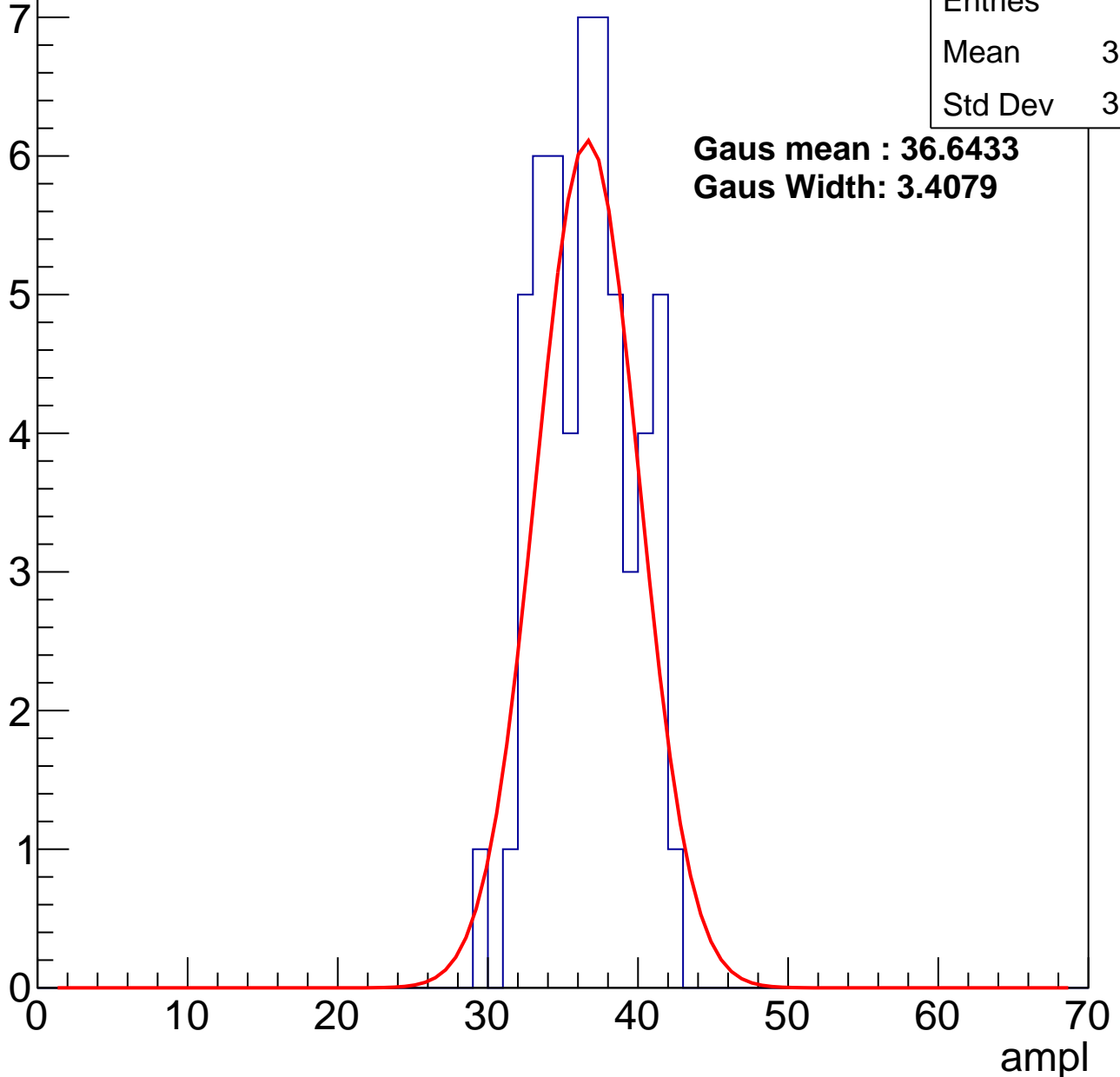
# B0L000S, U19-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	36.13
Std Dev	3.057

**Gaus mean : 36.6433**  
**Gaus Width: 3.4079**



# B0L000S, U19-ch56, adc2

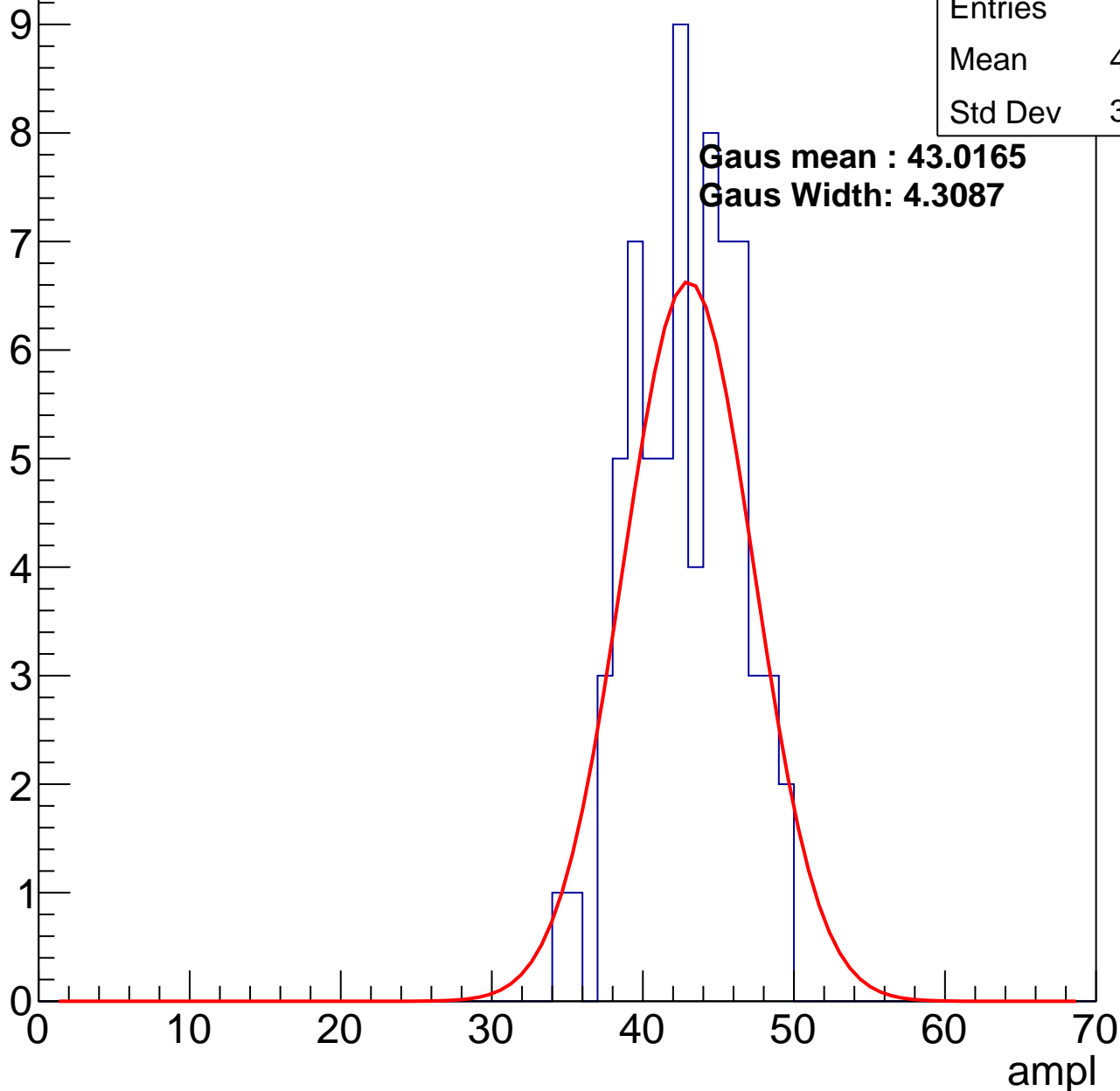
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.43
Std Dev	3.454

**Gaus mean : 43.0165**

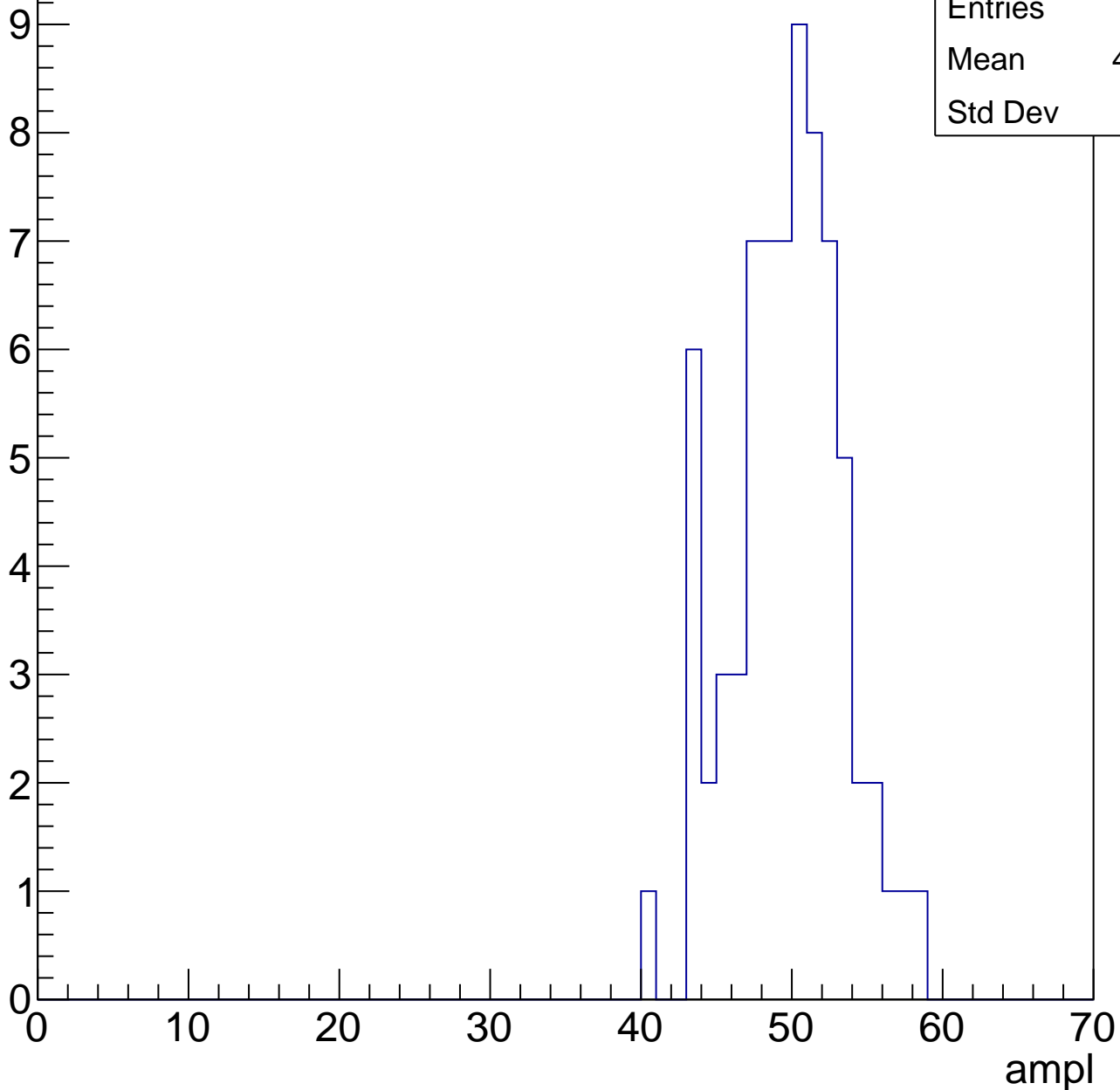
**Gaus Width: 4.3087**



# B0L000S, U19-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

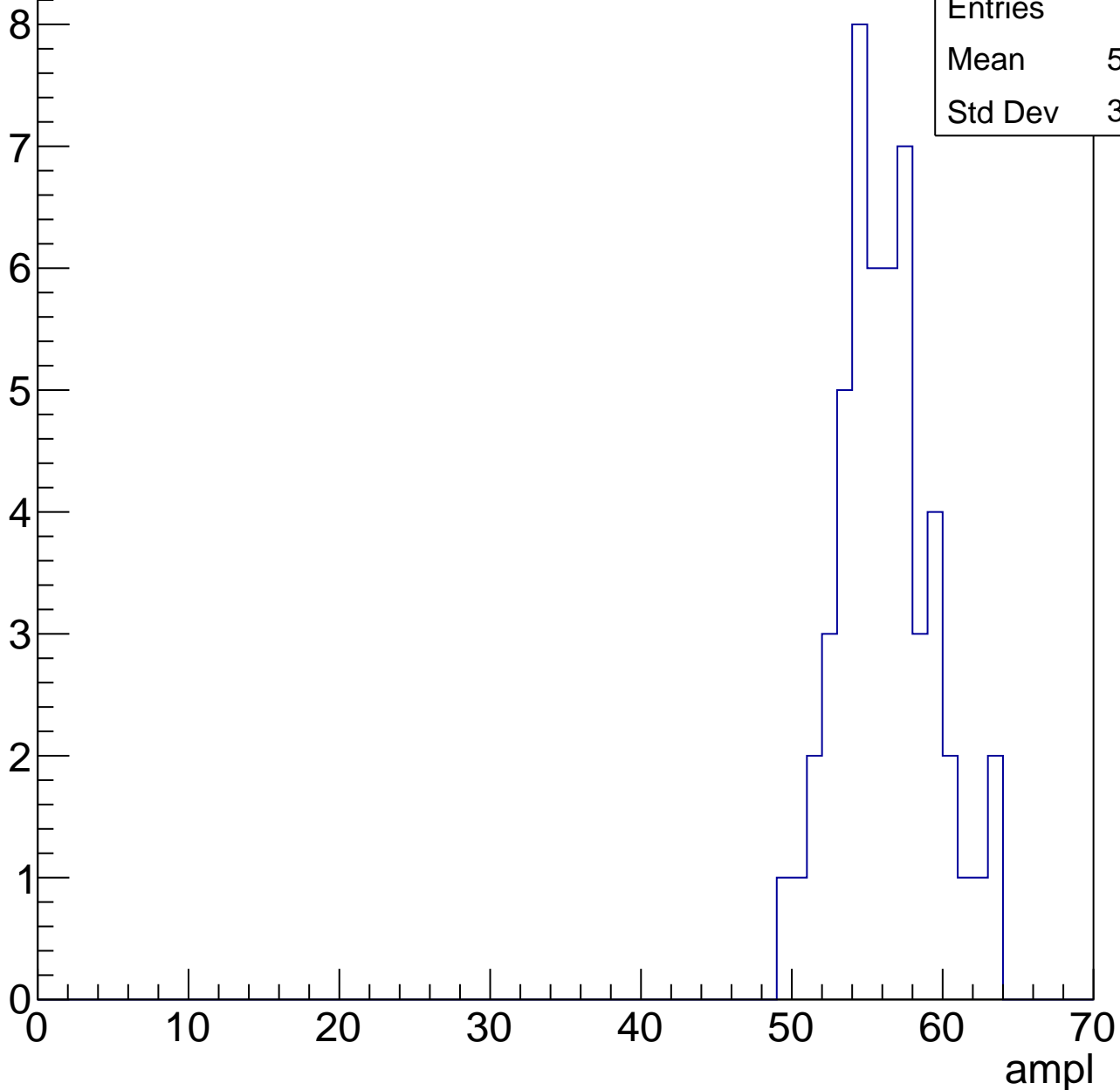


# B0L000S, U19-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	55.73
Std Dev	3.126

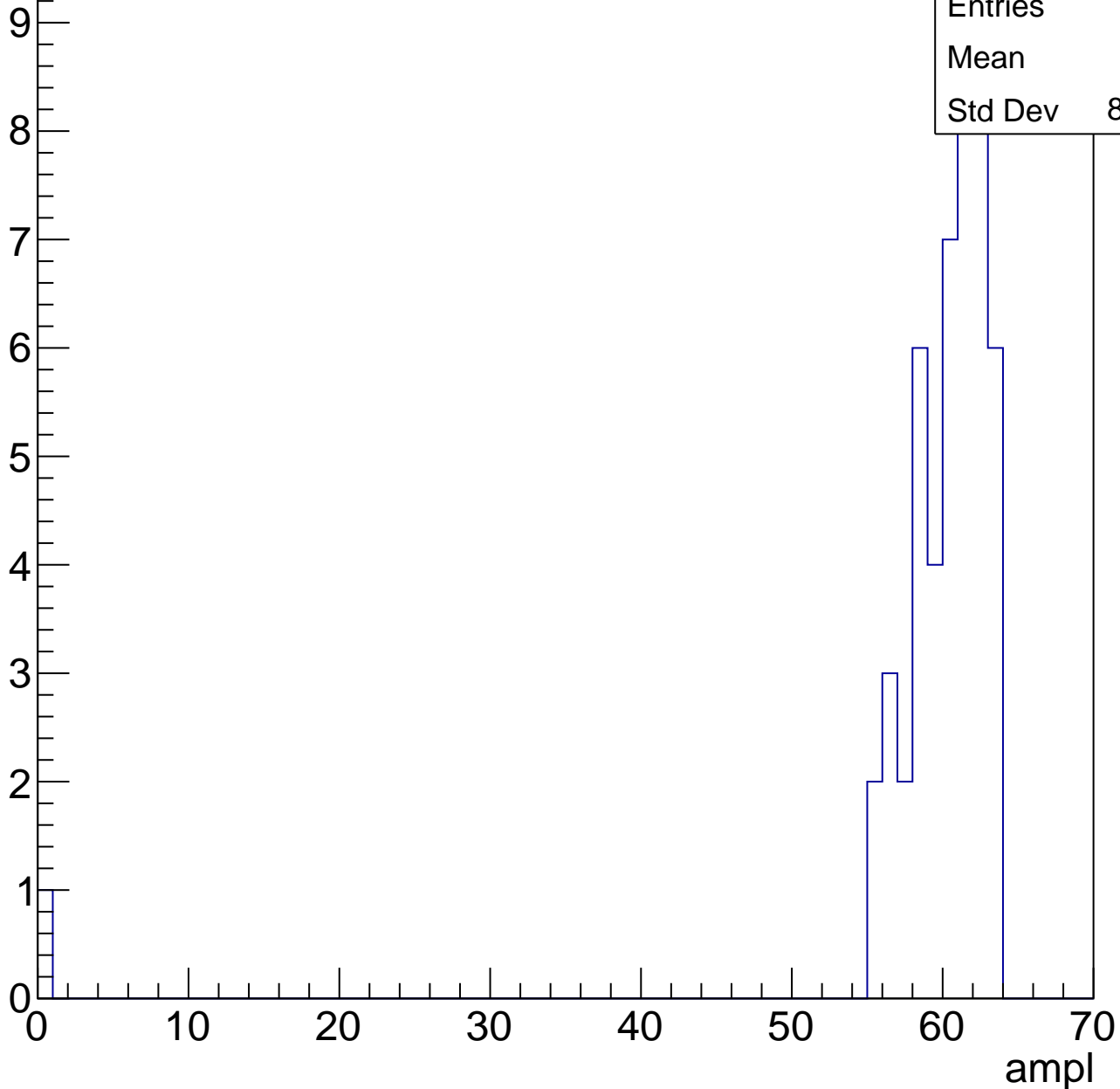


# B0L000S, U19-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	58.8
Std Dev	8.774



# B0L000S, U19-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

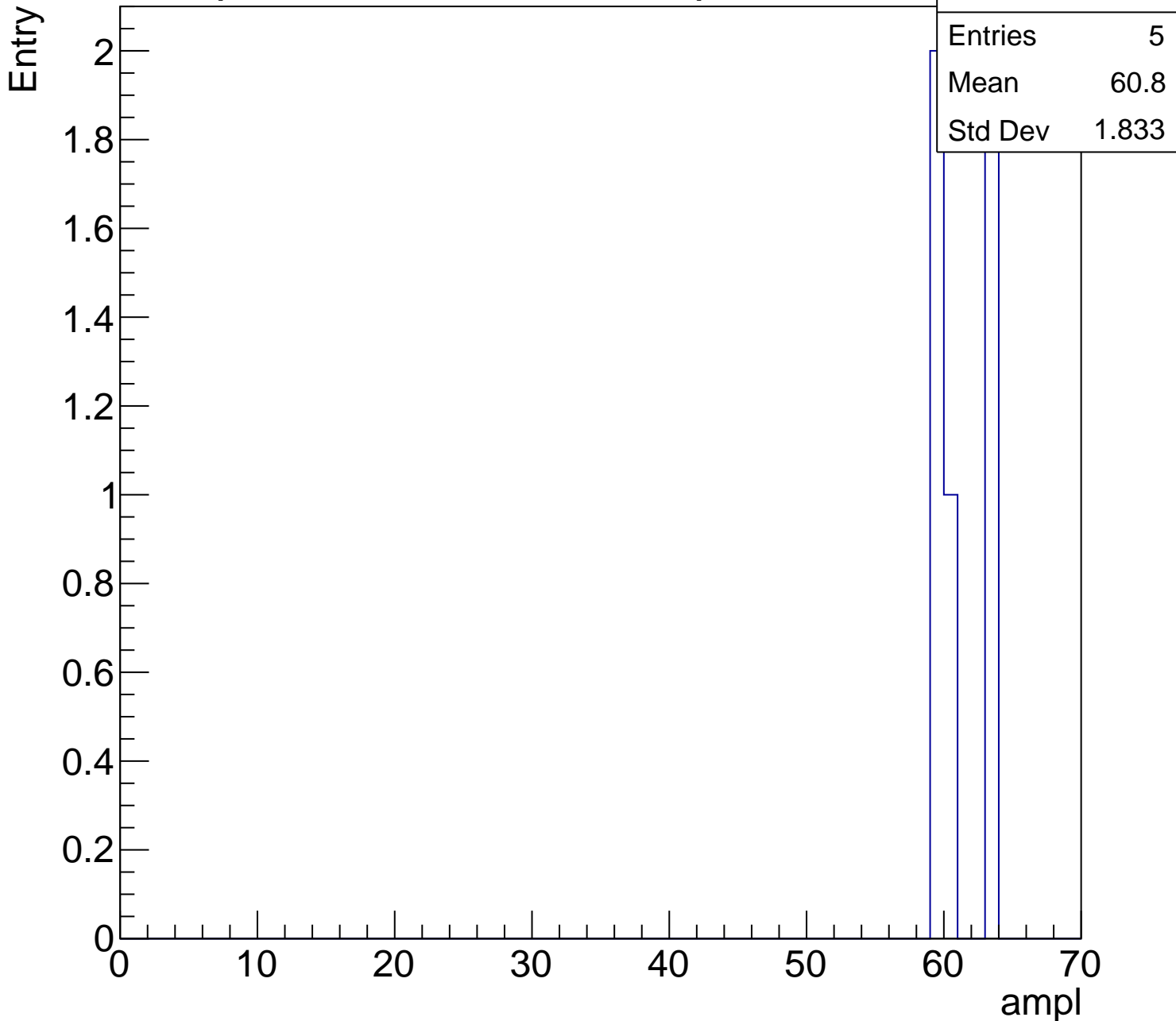
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	60.8
Std Dev	1.833

0 10 20 30 40 50 60 70

ampl





# B0L000S, U19-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch57, adc0

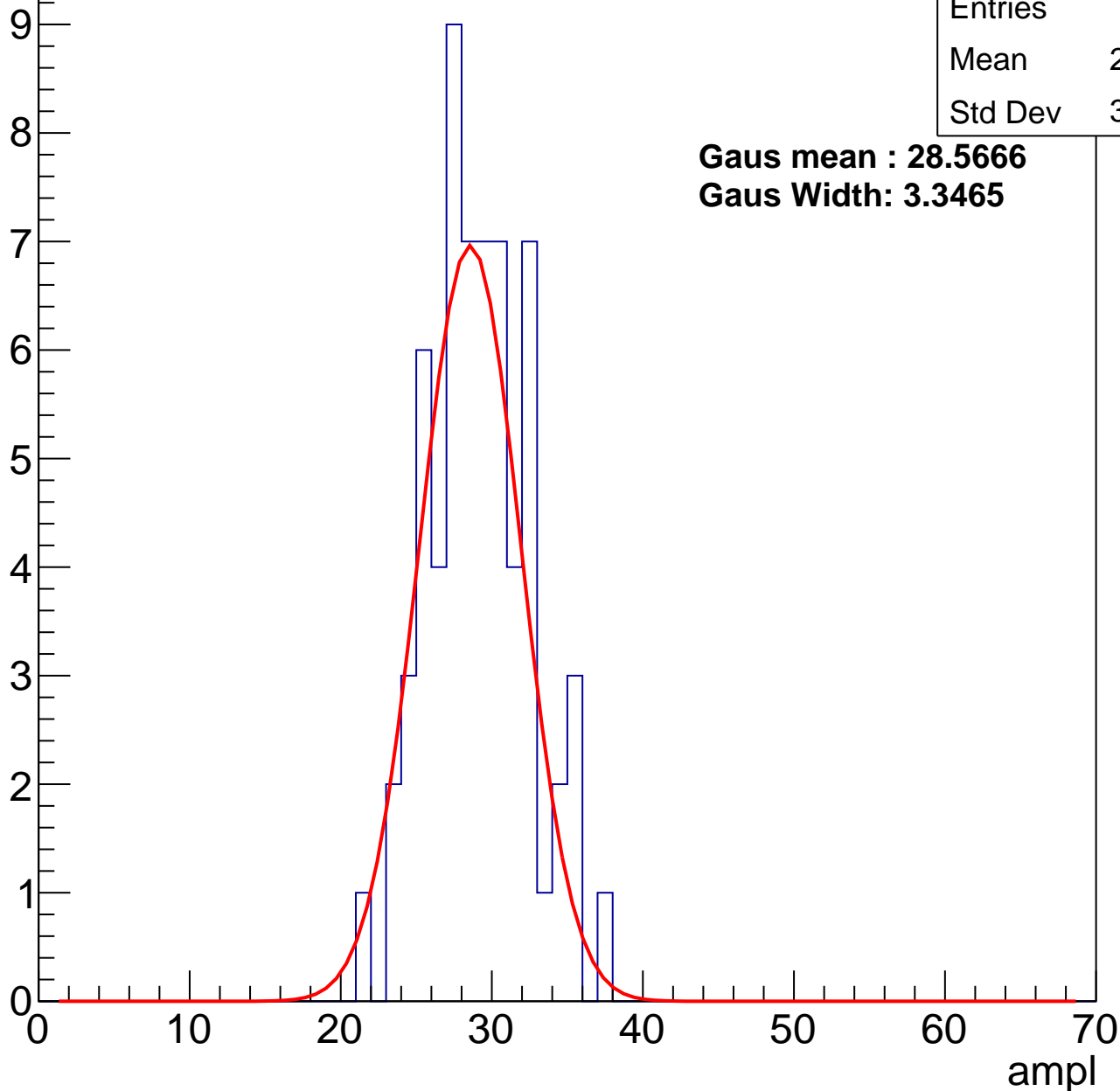
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	28.69
Std Dev	3.316

**Gaus mean : 28.5666**

**Gaus Width: 3.3465**



# B0L000S, U19-ch57, adc1

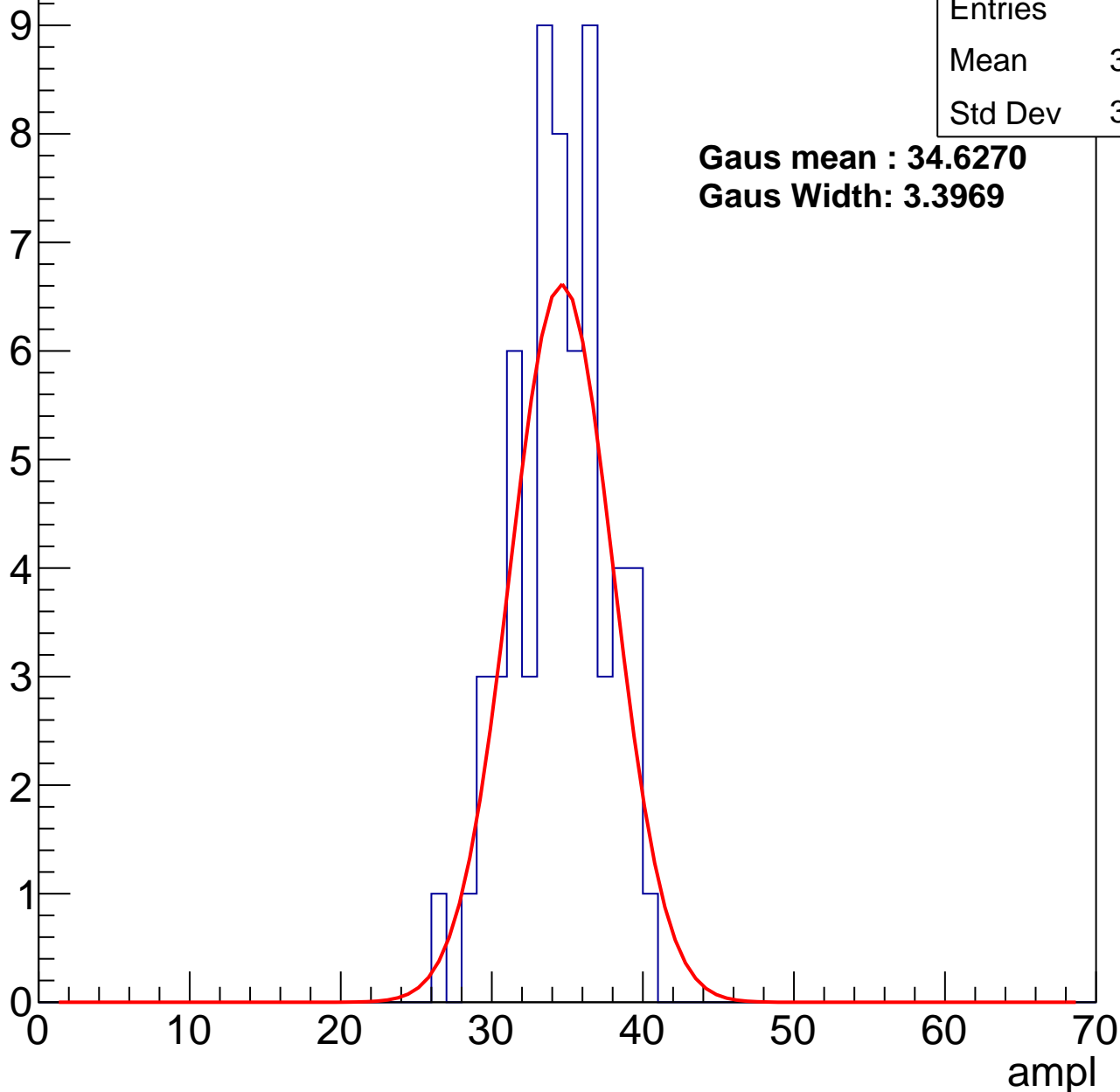
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	34.02
Std Dev	3.049

**Gaus mean : 34.6270**

**Gaus Width: 3.3969**



# B0L000S, U19-ch57, adc2

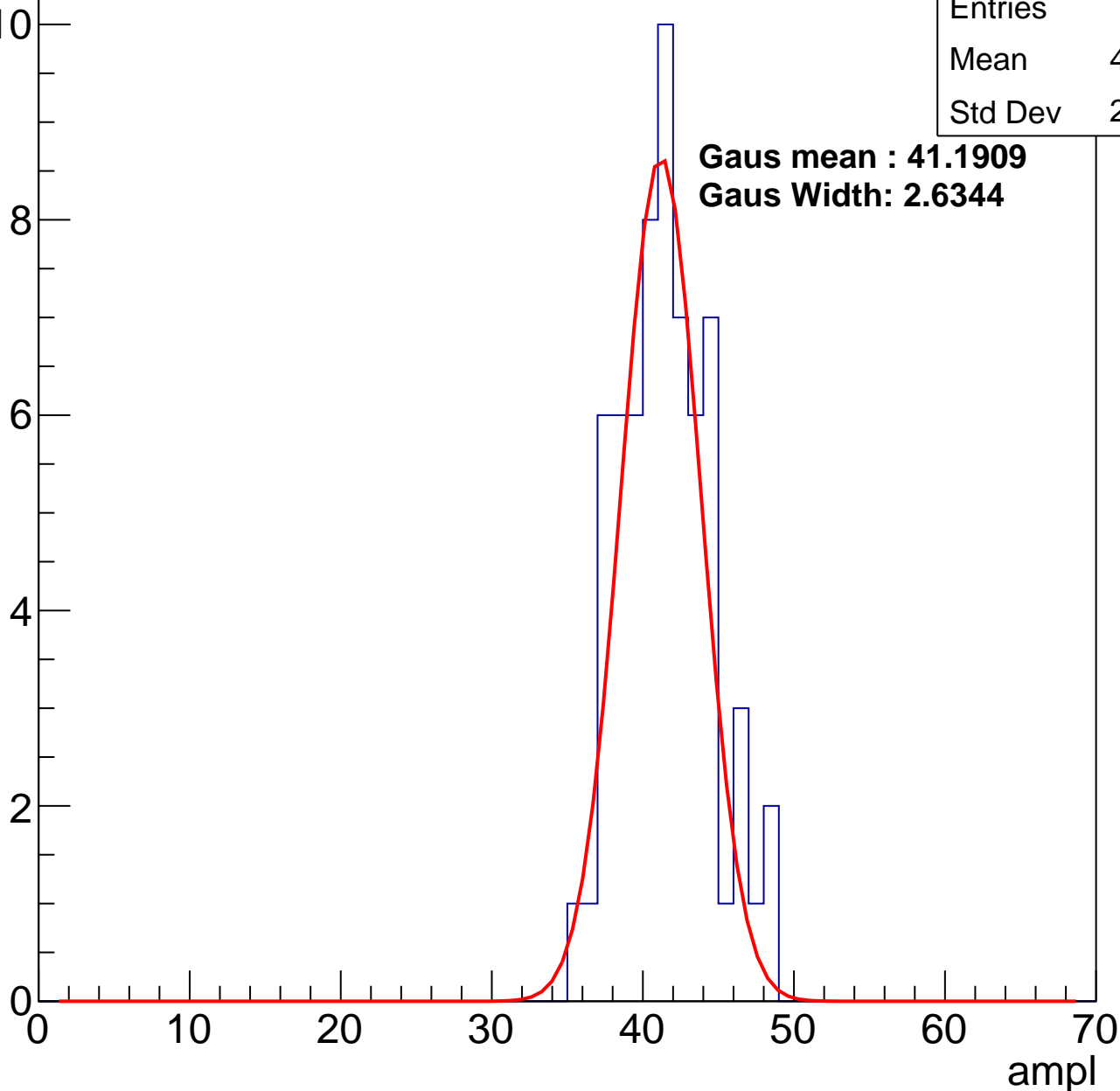
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	41.09
Std Dev	2.939

**Gaus mean : 41.1909**

**Gaus Width: 2.6344**

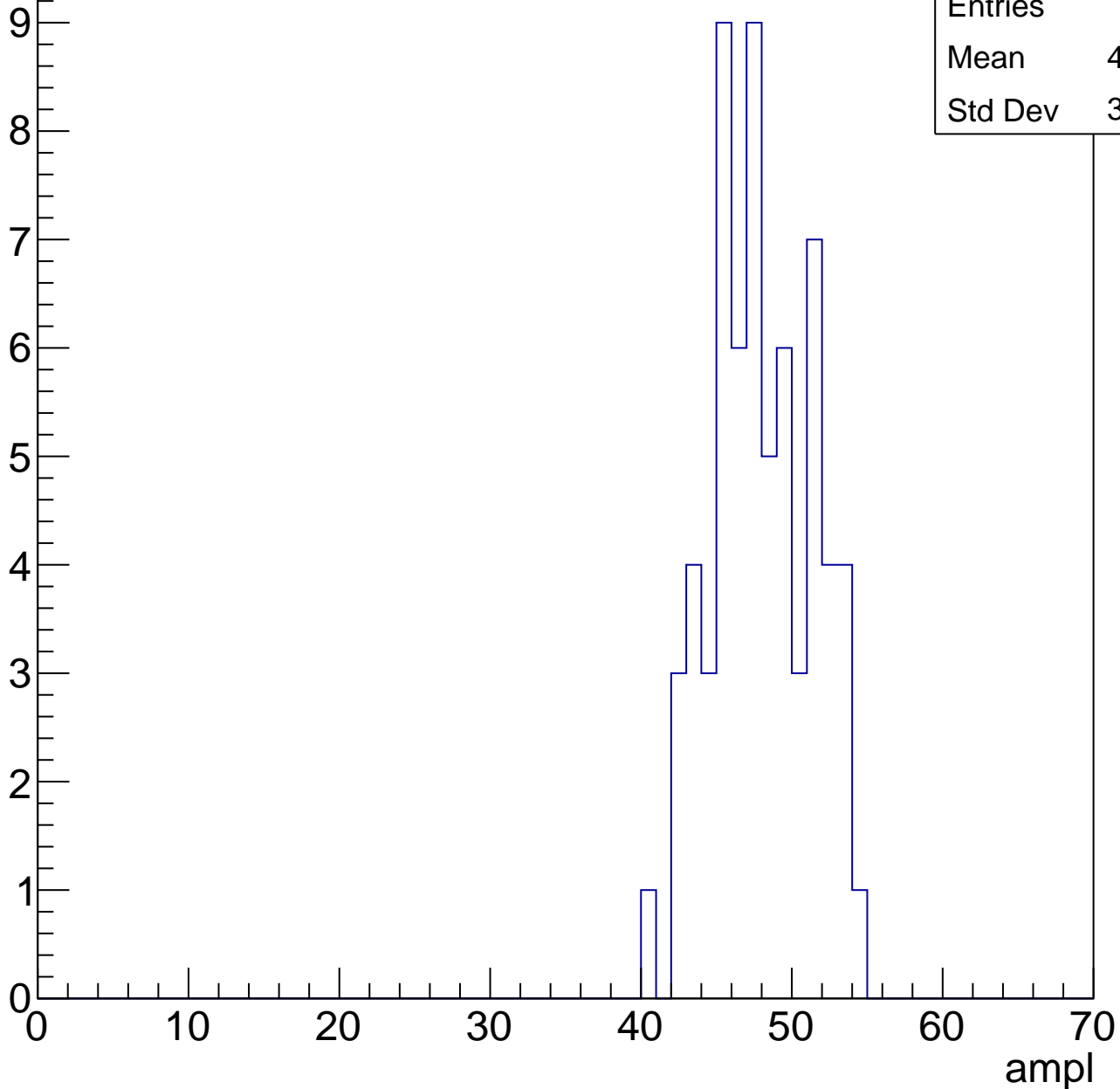


# B0L000S, U19-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	47.52
Std Dev	3.282

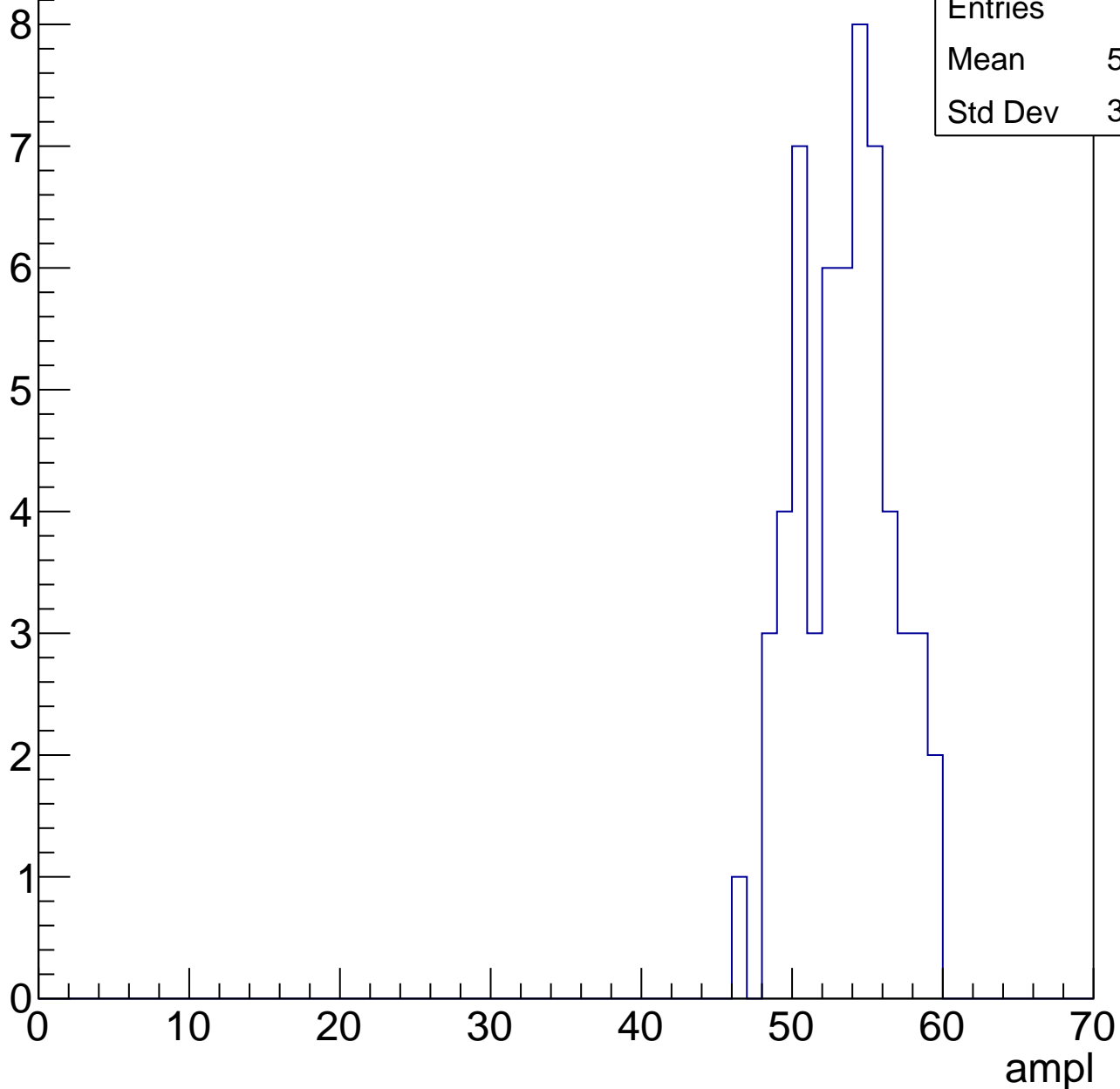


# B0L000S, U19-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	53.04
Std Dev	3.066

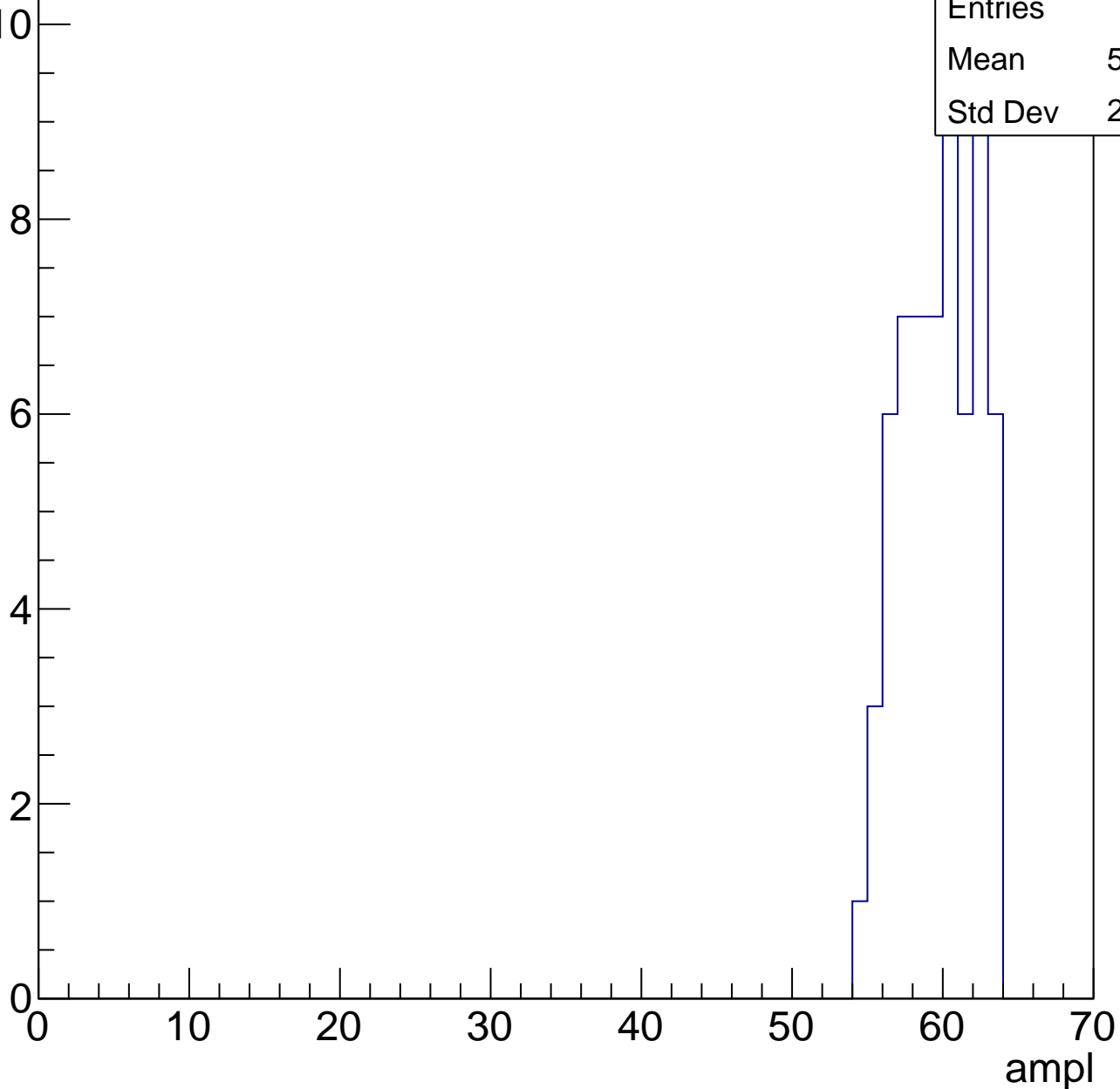


# B0L000S, U19-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

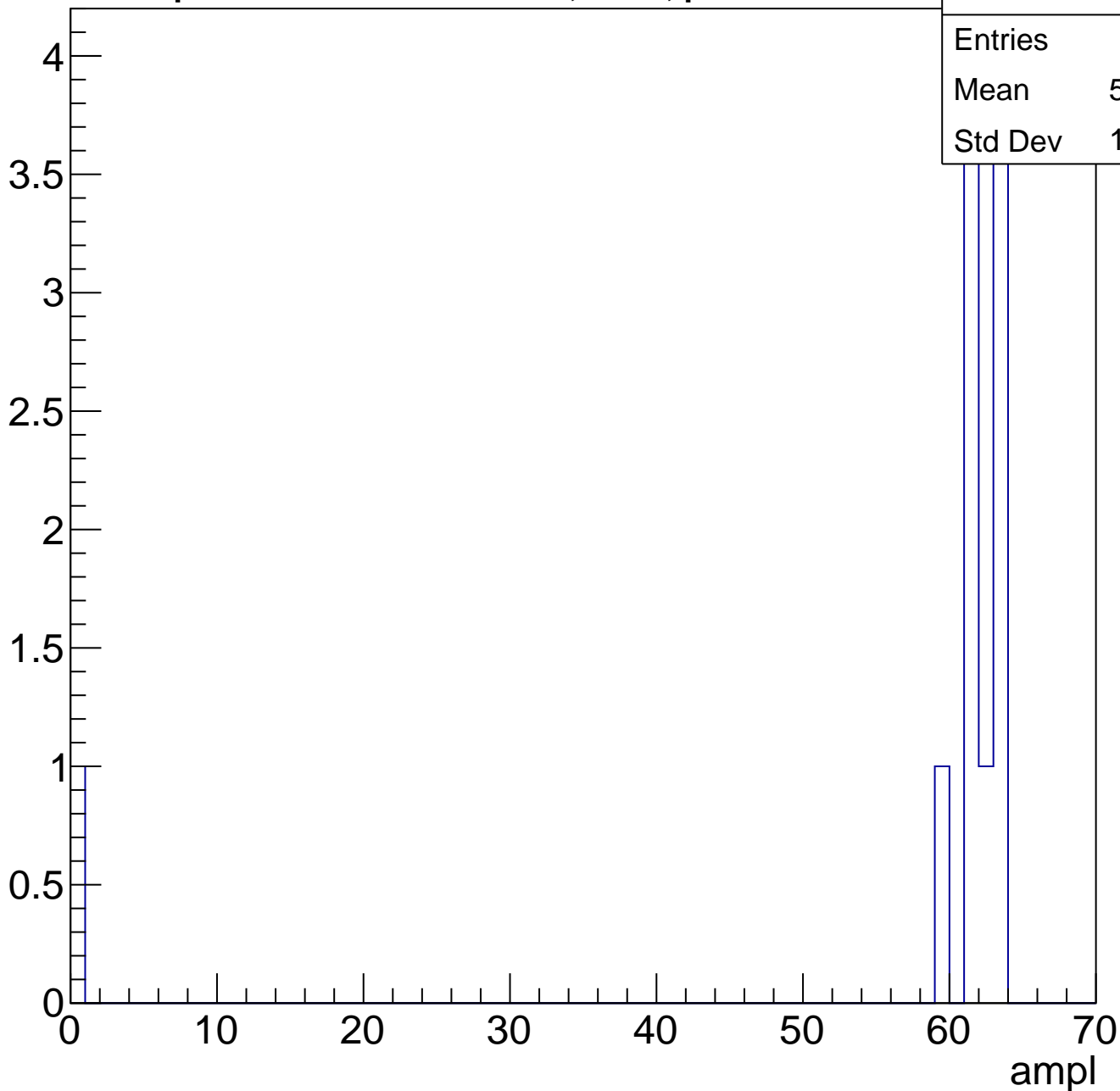
Entries	62
Mean	59.27
Std Dev	2.437



# B0L000S, U19-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch58, adc0

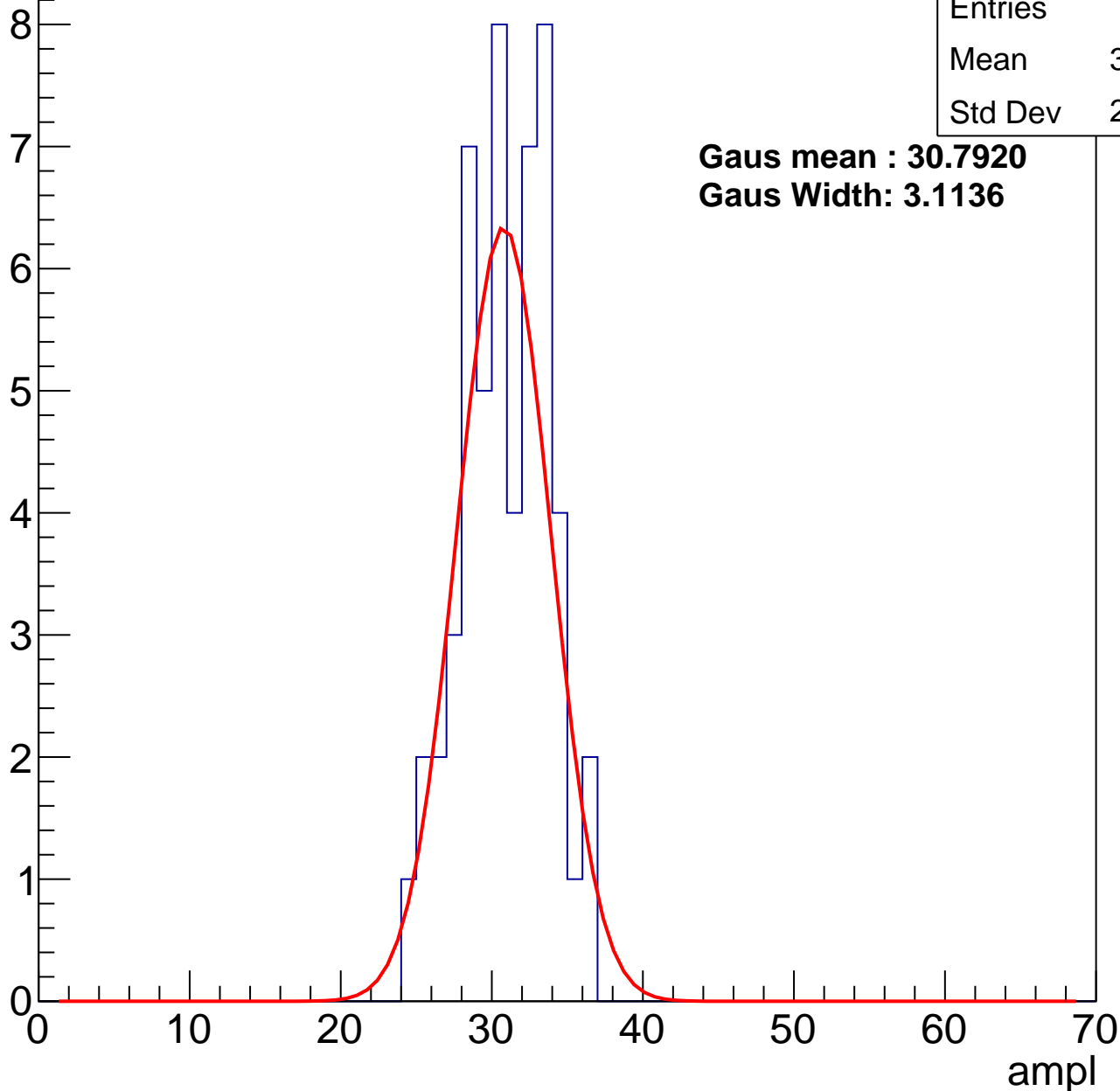
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	30.43
Std Dev	2.832

**Gaus mean : 30.7920**

**Gaus Width: 3.1136**



# B0L000S, U19-ch58, adc1

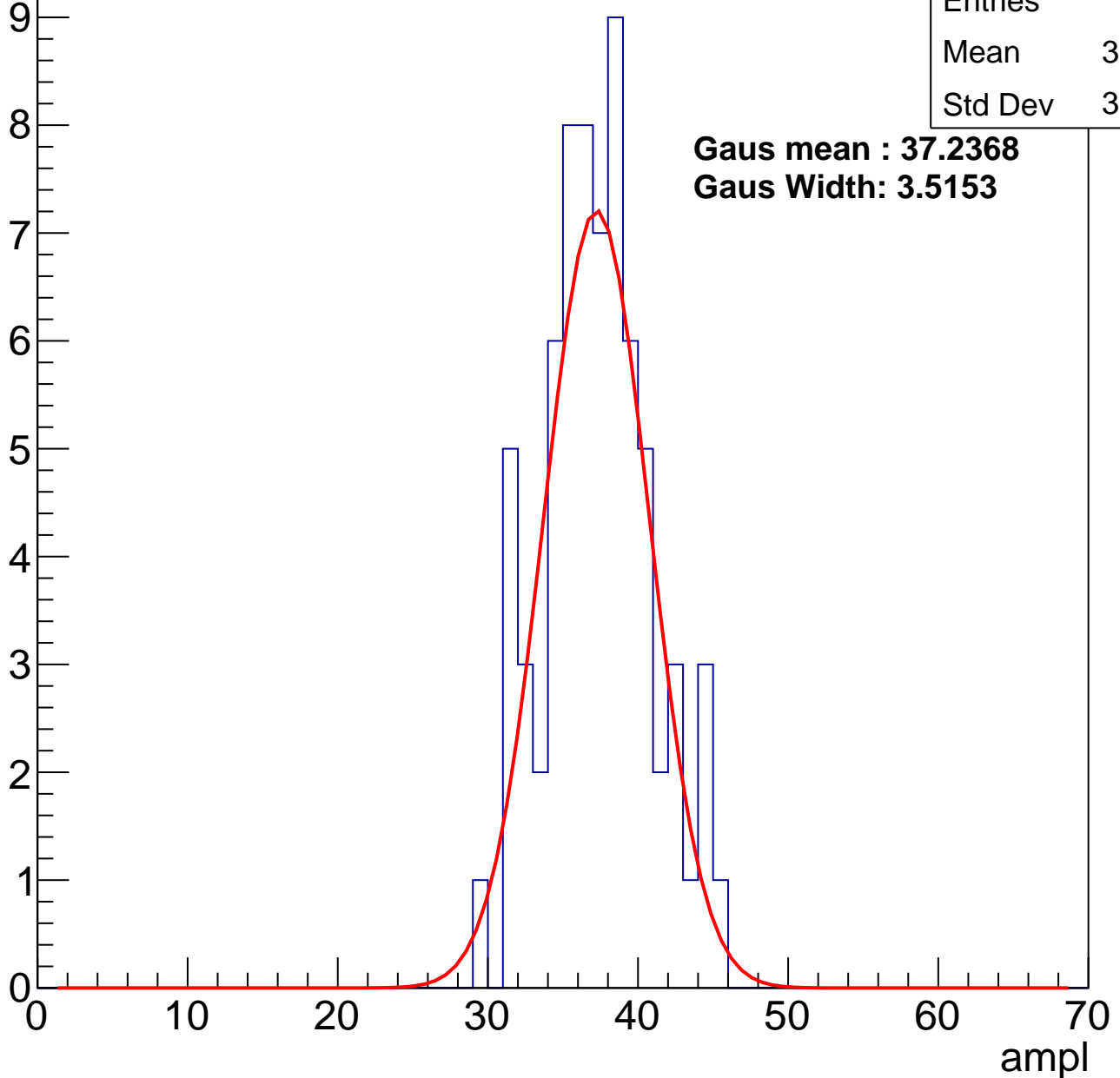
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.87
Std Dev	3.537

**Gaus mean : 37.2368**

**Gaus Width: 3.5153**



# B0L000S, U19-ch58, adc2

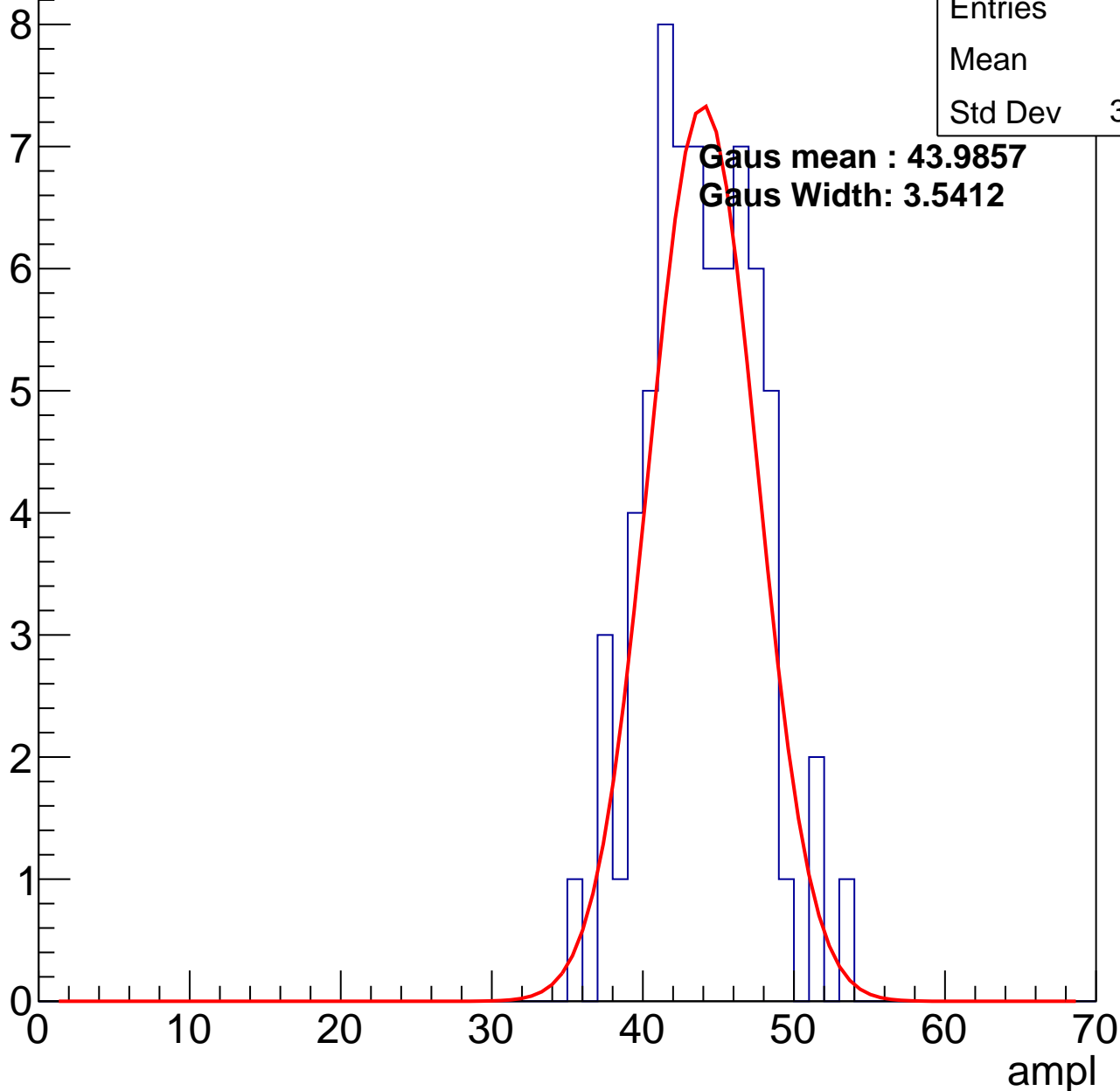
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.5
Std Dev	3.597

**Gaus mean : 43.9857**

**Gaus Width: 3.5412**

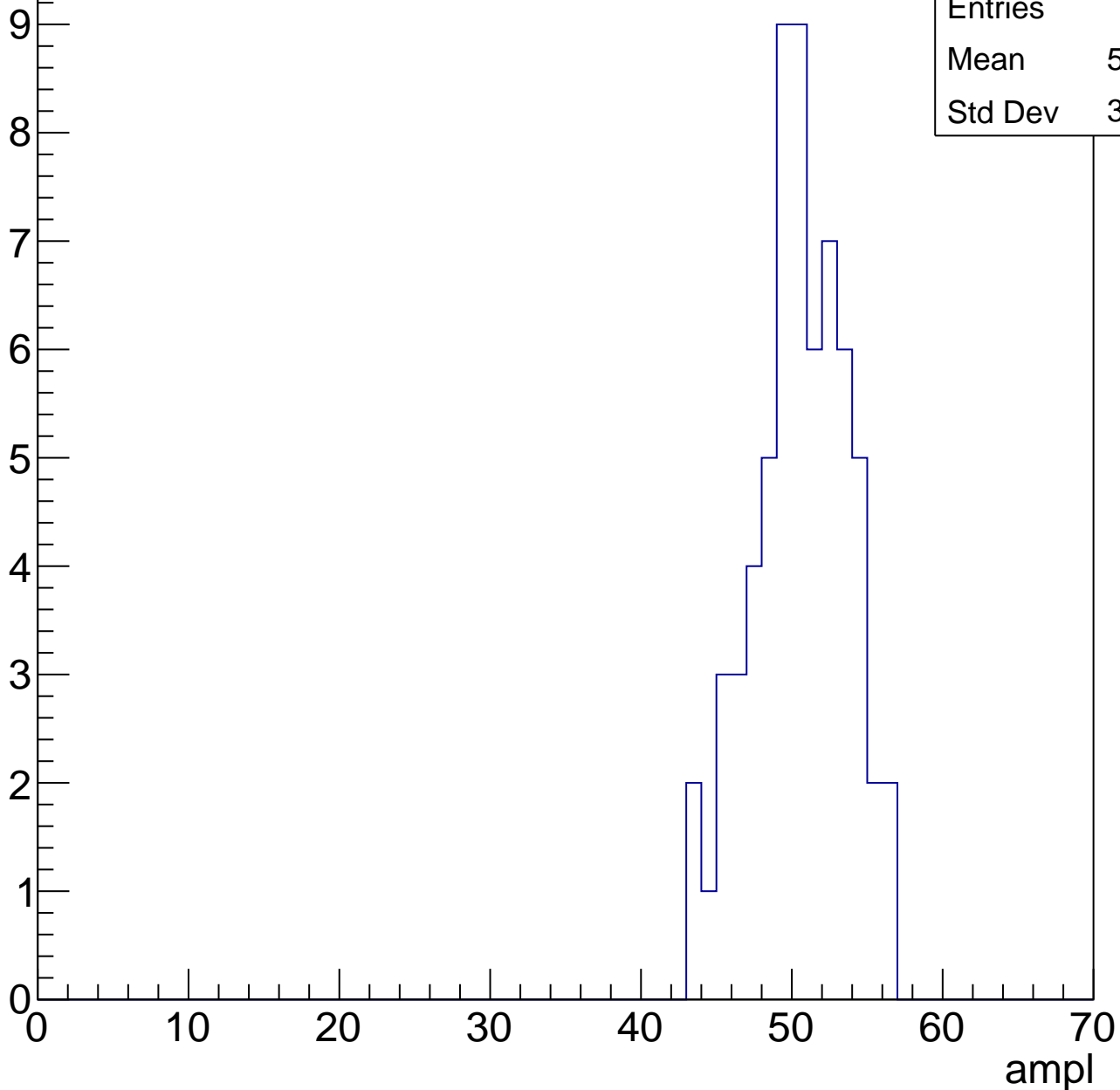


# B0L000S, U19-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

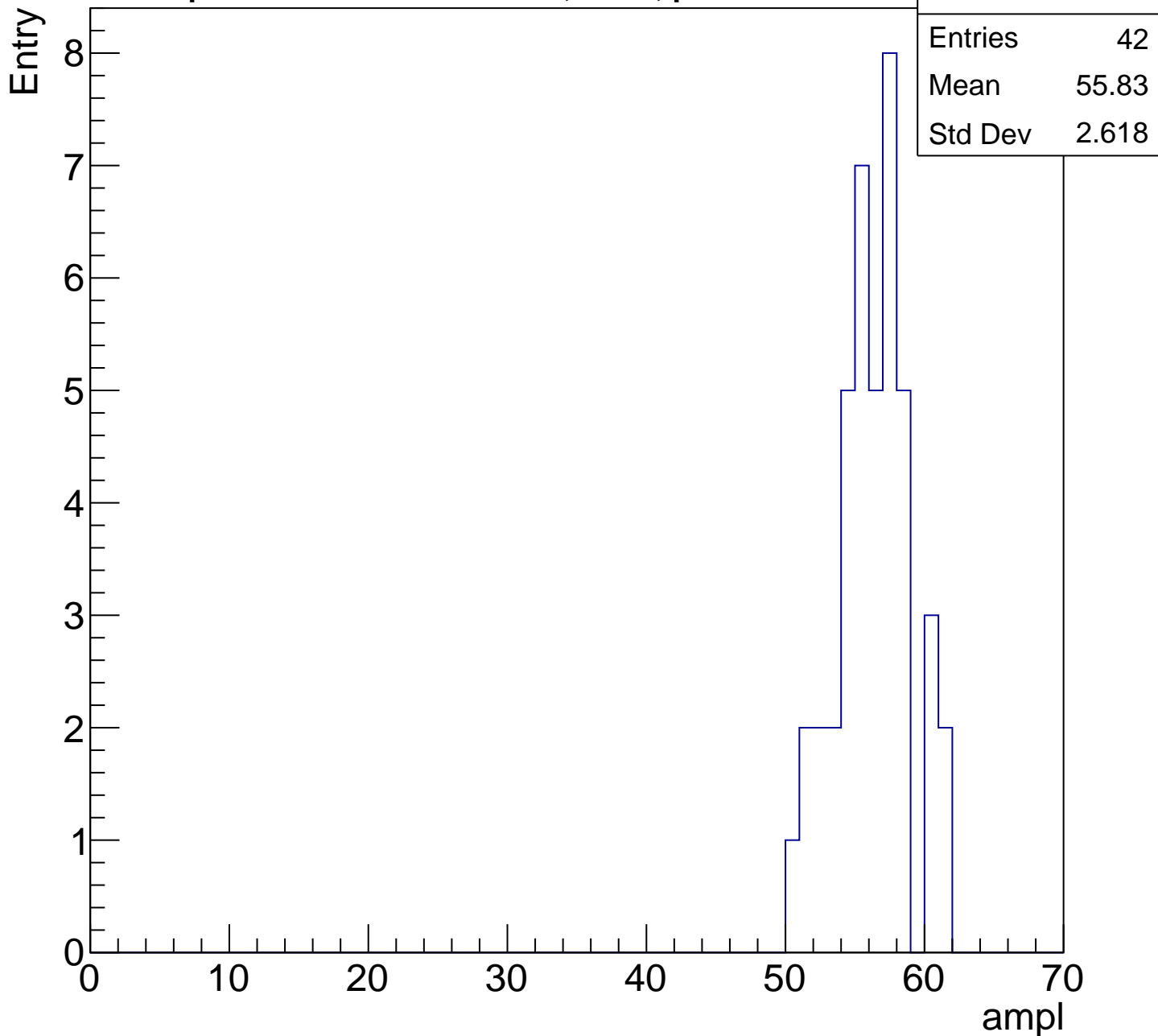
Entry

Entries	64
Mean	50.03
Std Dev	3.092



# B0L000S, U19-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U19-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	45
Mean	59.47
Std Dev	2.587

Entry

10

8

6

4

2

0

0

10

20

30

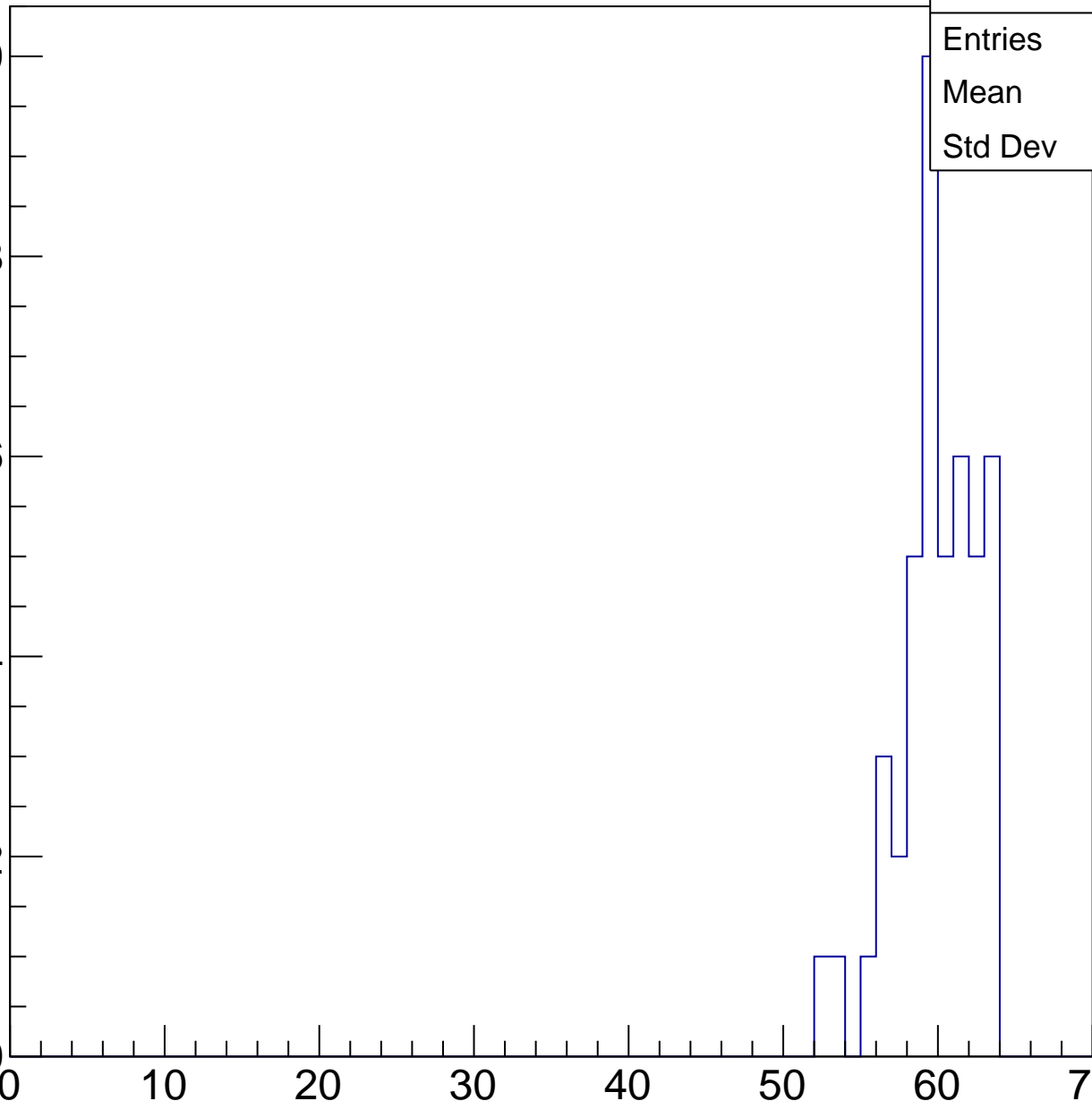
40

50

60

70

ampl

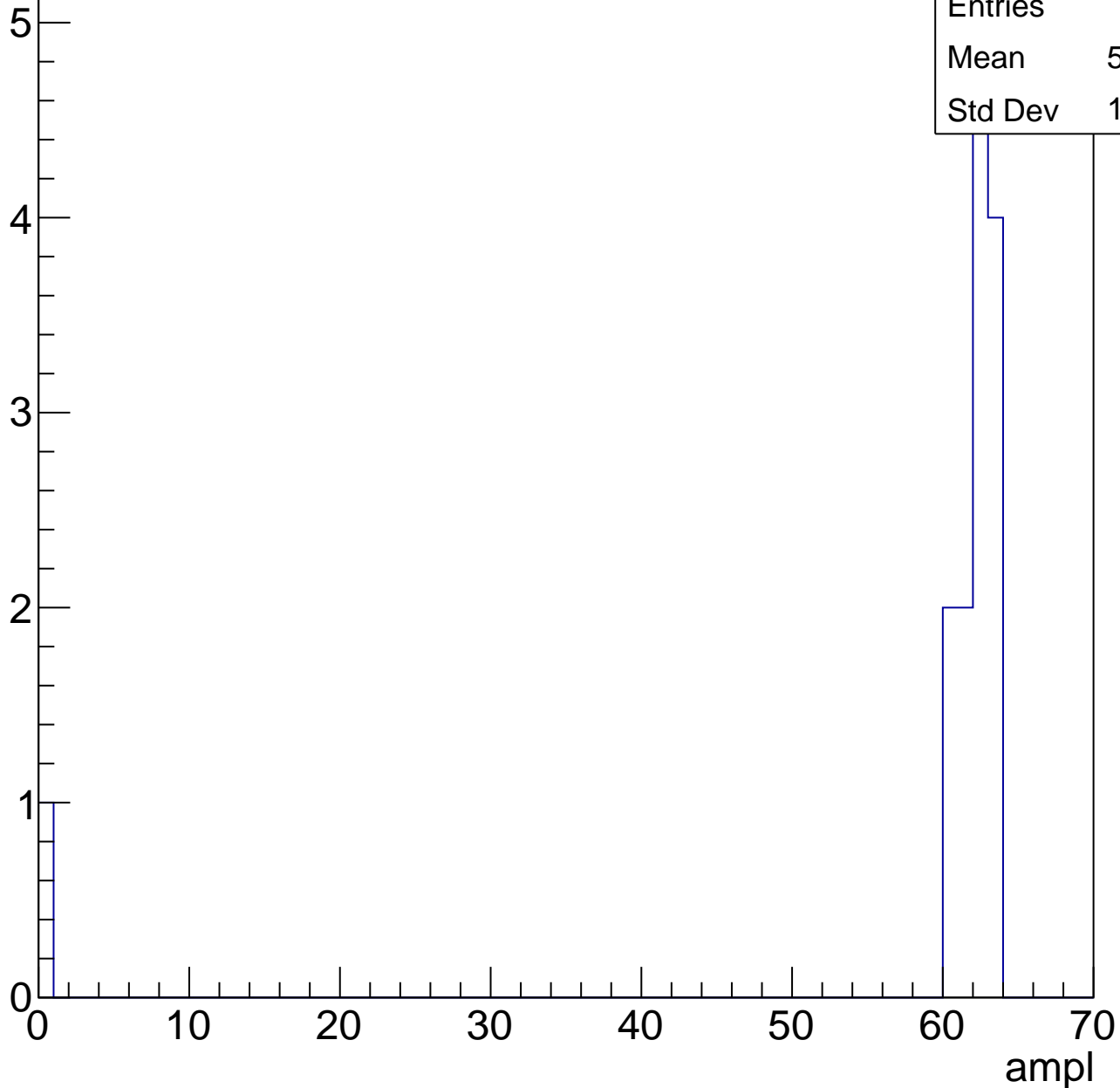


# B0L000S, U19-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	57.43
Std Dev	15.96





# B0L000S, U19-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	29.9
Std Dev	3.075

**Gaus mean : 29.8894**

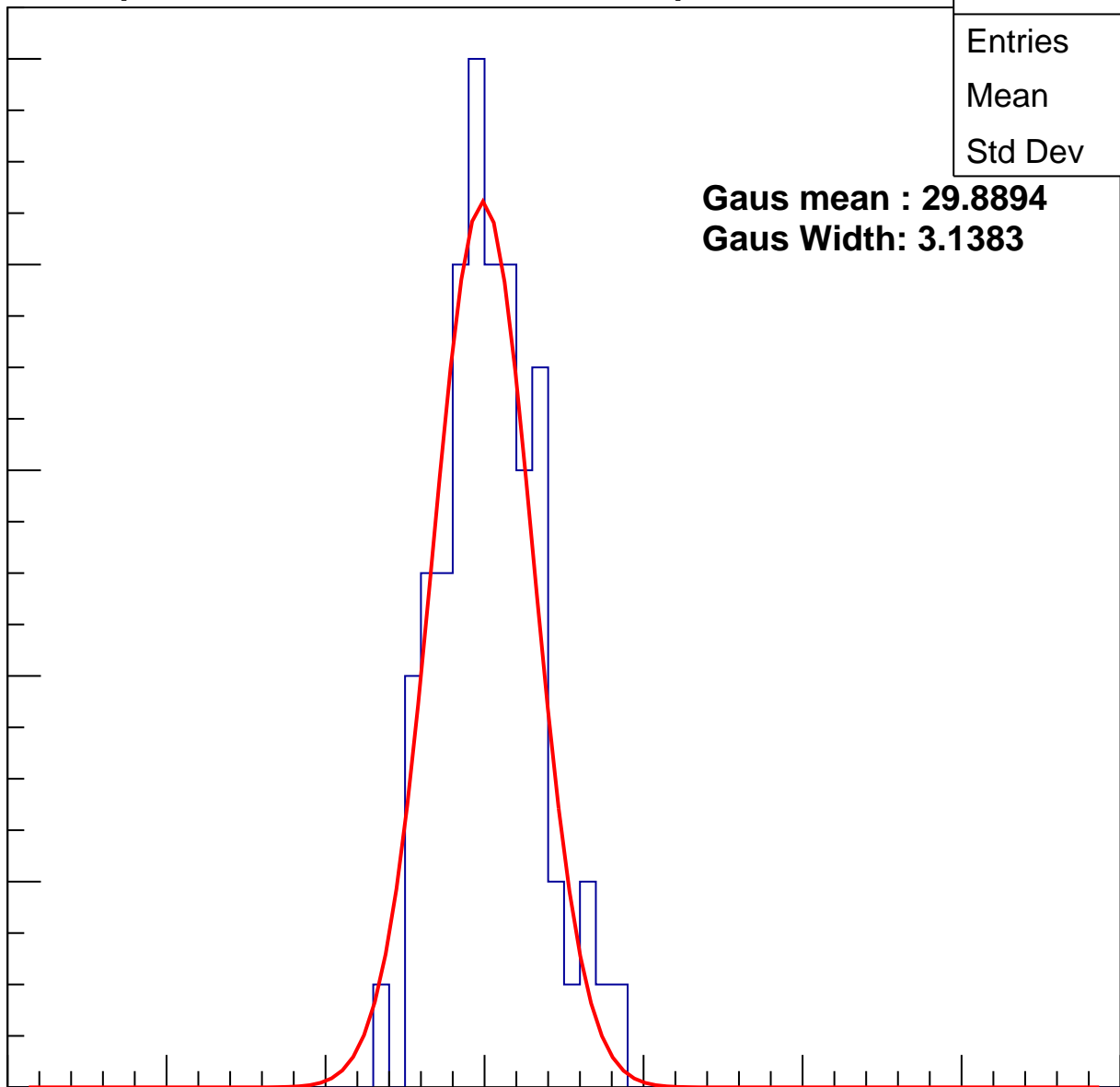
**Gaus Width: 3.1383**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch59, adc1

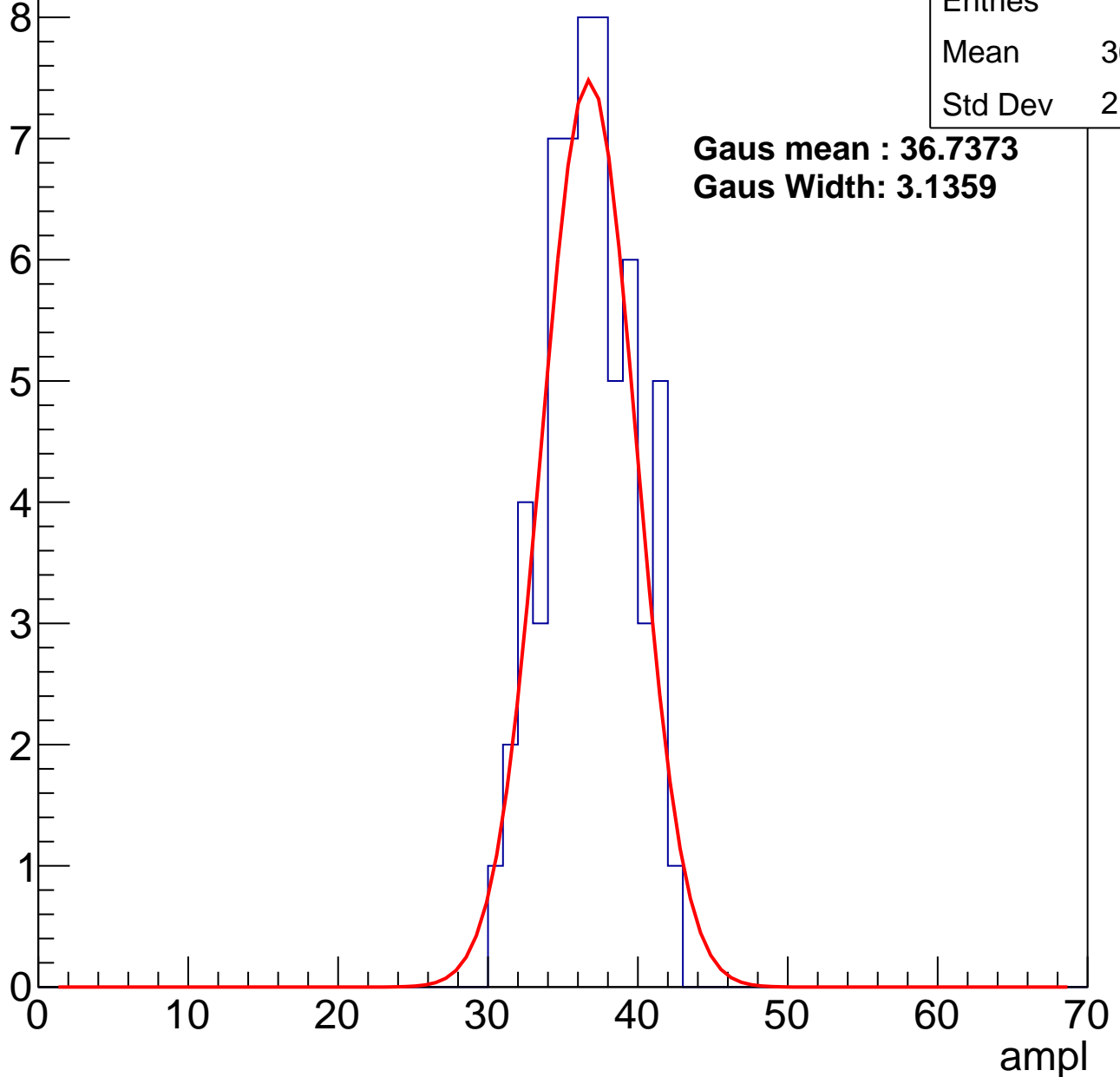
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.28
Std Dev	2.882

**Gaus mean : 36.7373**

**Gaus Width: 3.1359**



# B0L000S, U19-ch59, adc2

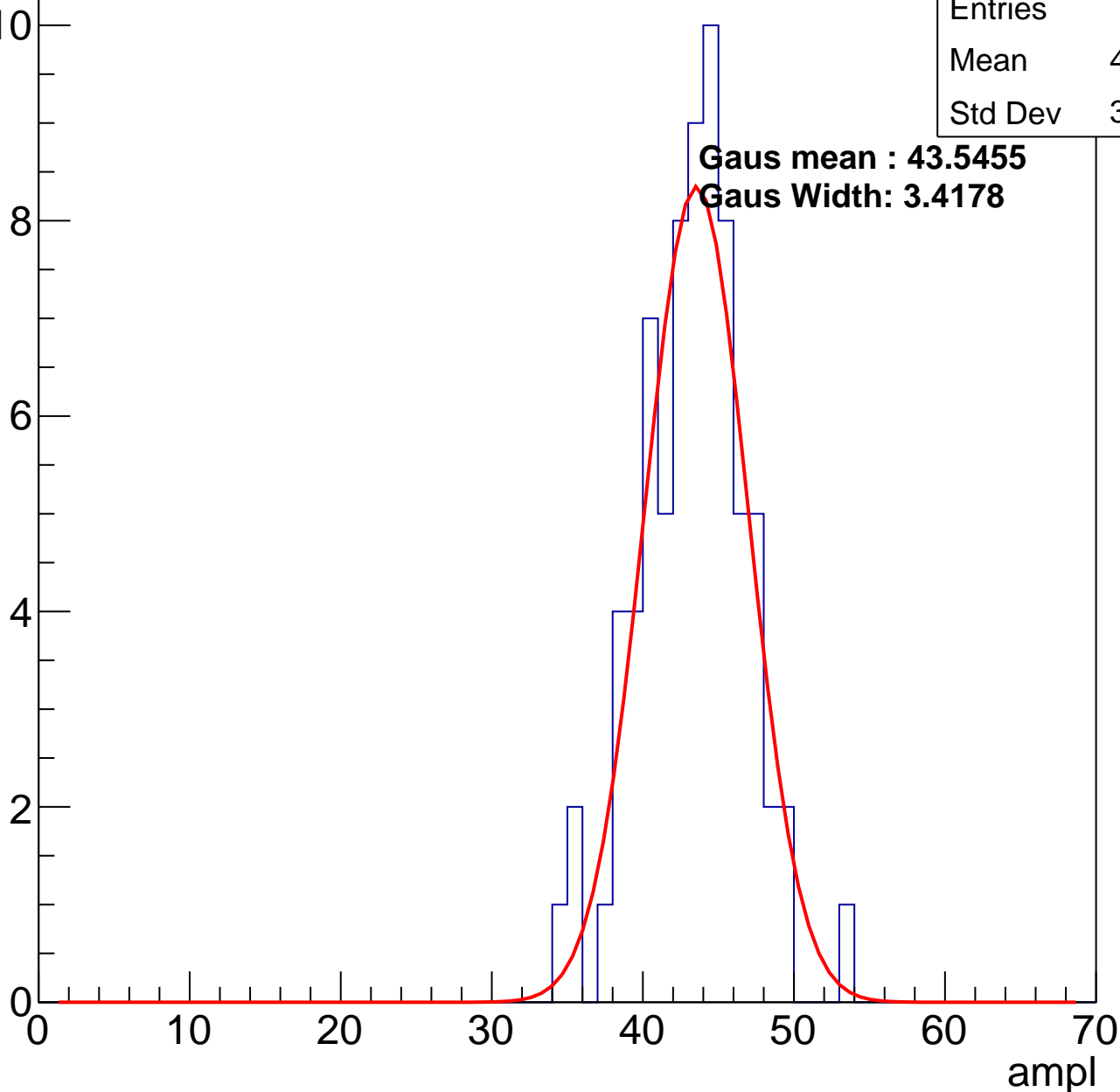
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.82
Std Dev	3.462

**Gaus mean : 43.5455**

**Gaus Width: 3.4178**

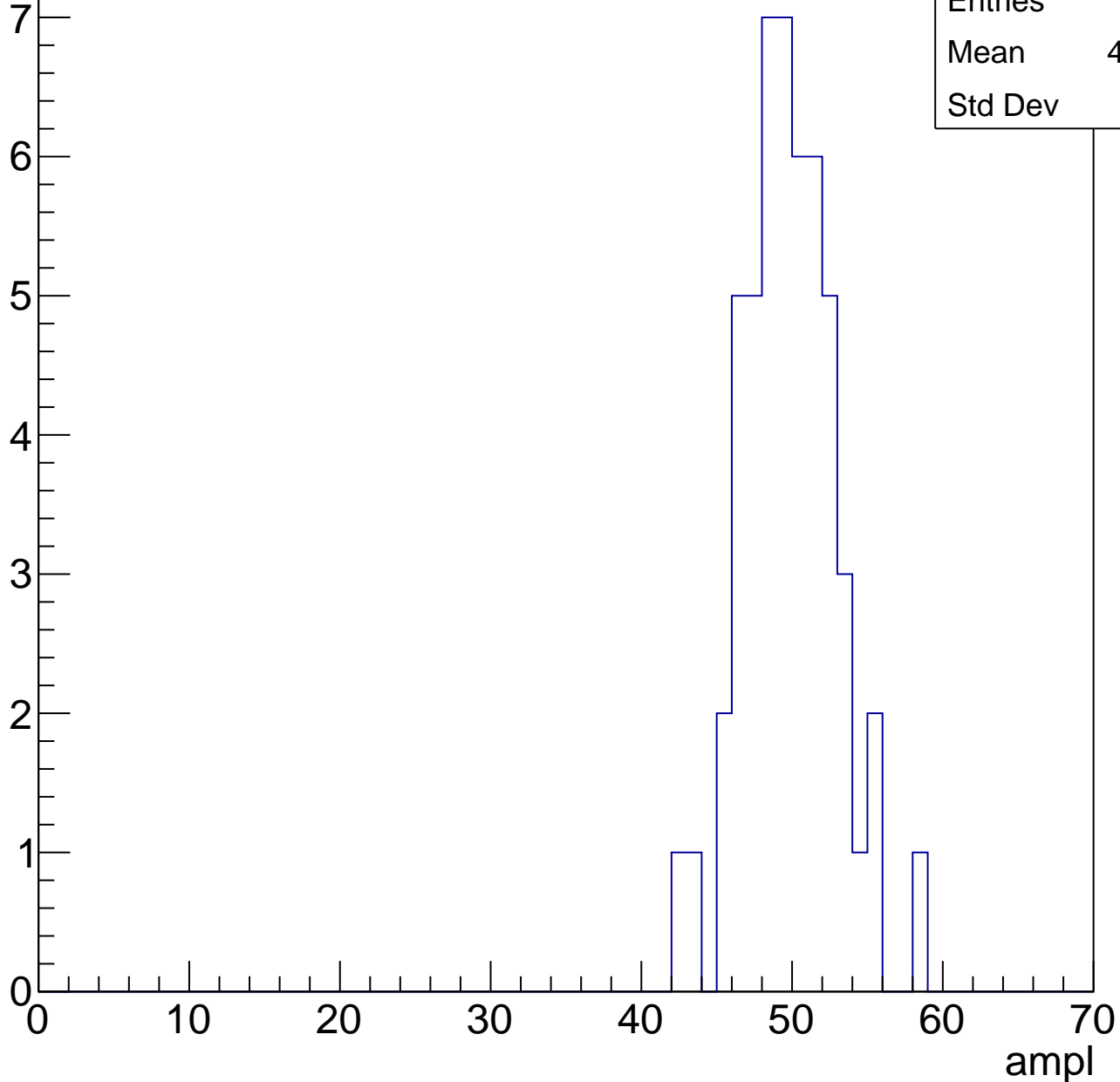


# B0L000S, U19-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	49.35
Std Dev	3.05

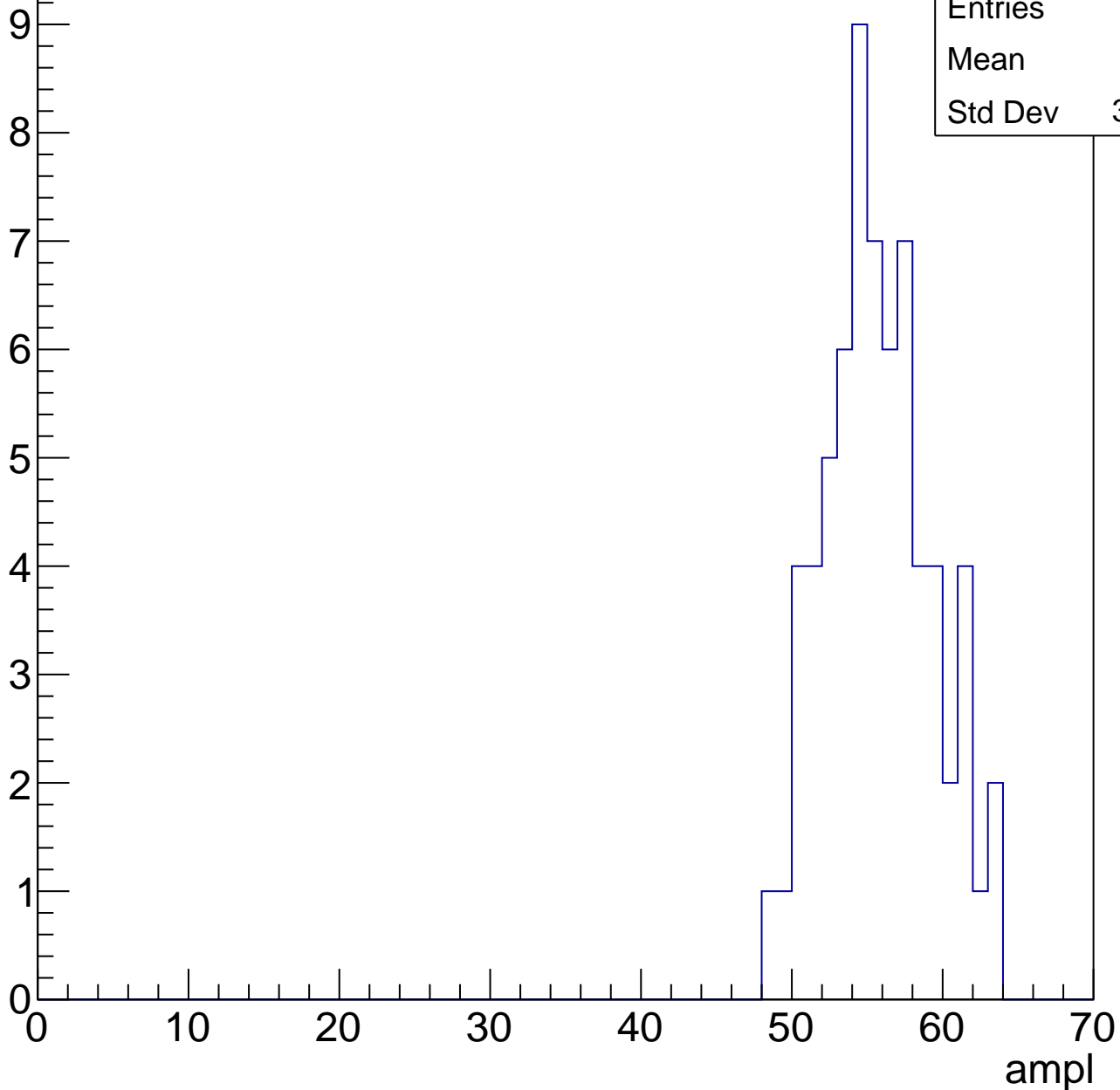


# B0L000S, U19-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	55.3
Std Dev	3.511



# B0L000S, U19-ch59, adc5

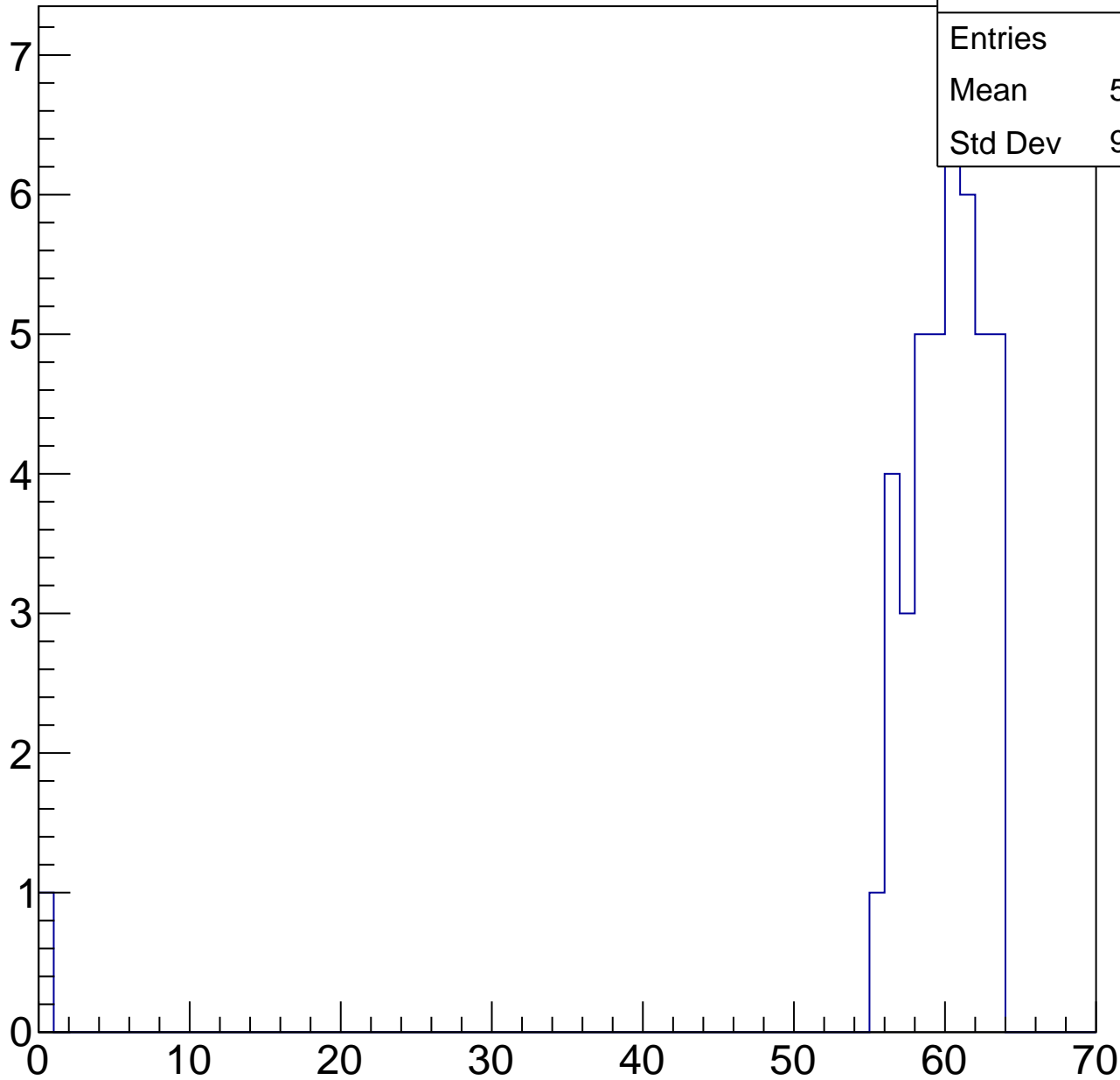
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	42
Mean	58.24
Std Dev	9.363

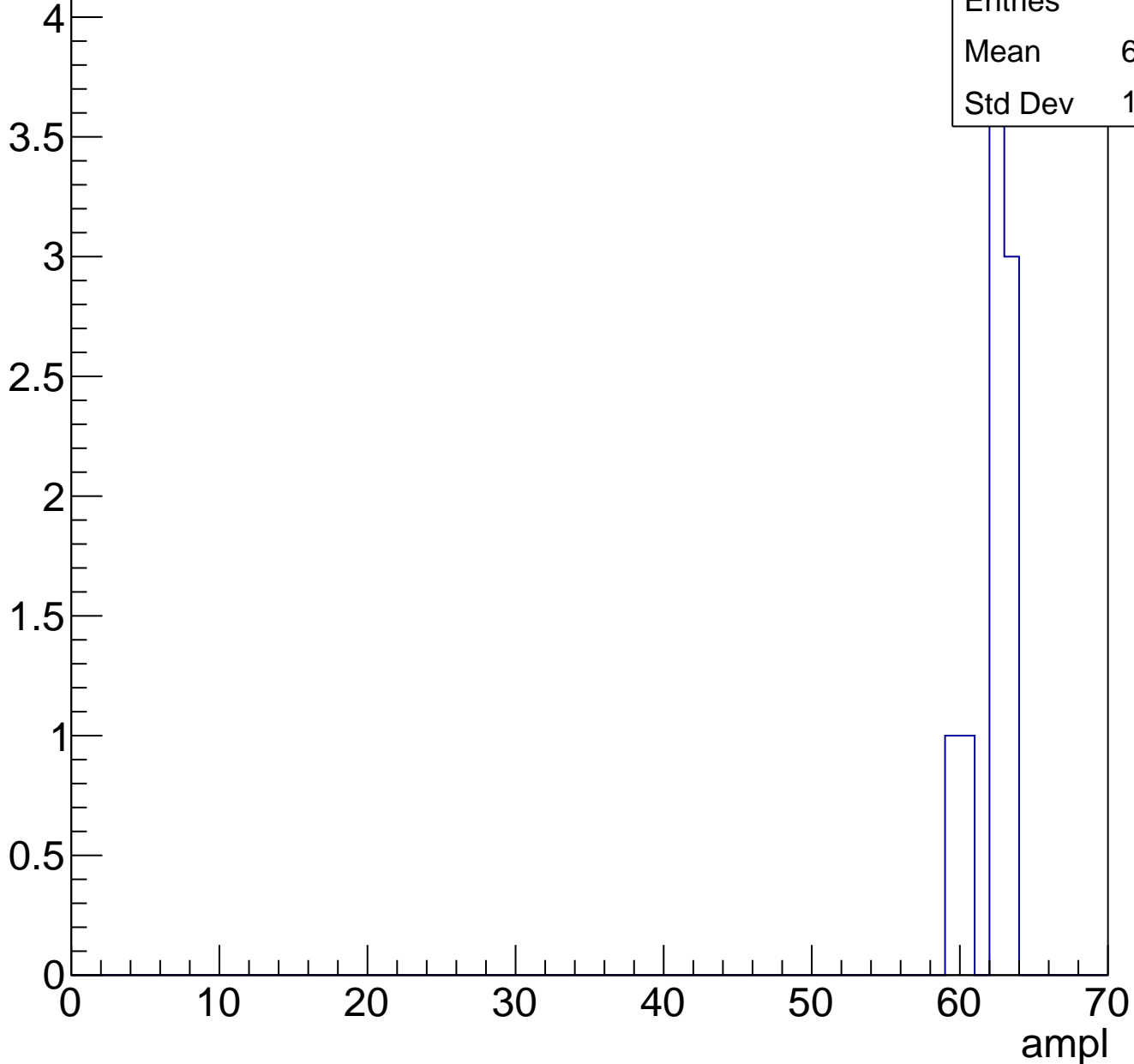
ampl



# B0L000S, U19-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch60, adc0

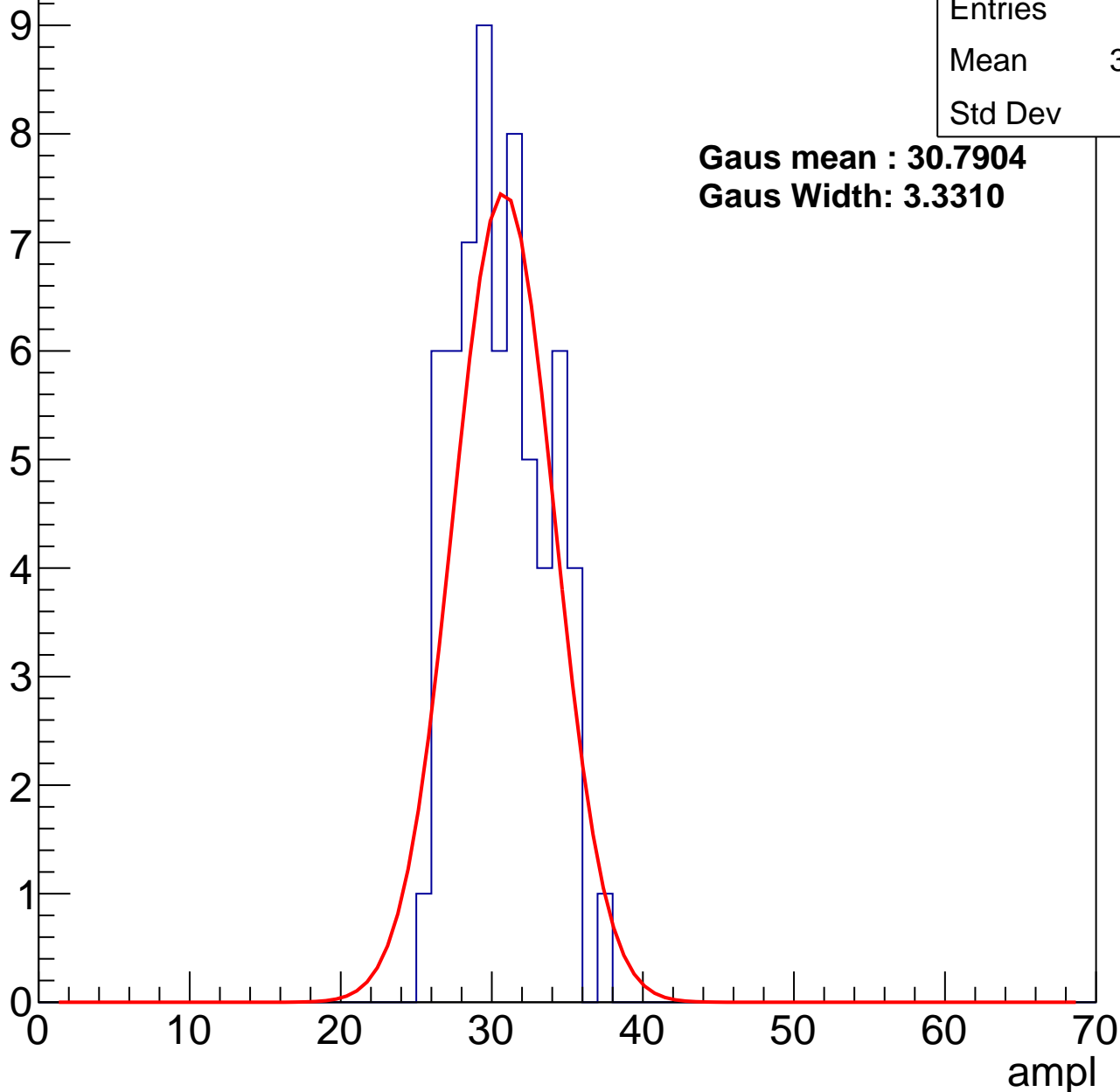
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.17
Std Dev	2.87

**Gaus mean : 30.7904**

**Gaus Width: 3.3310**



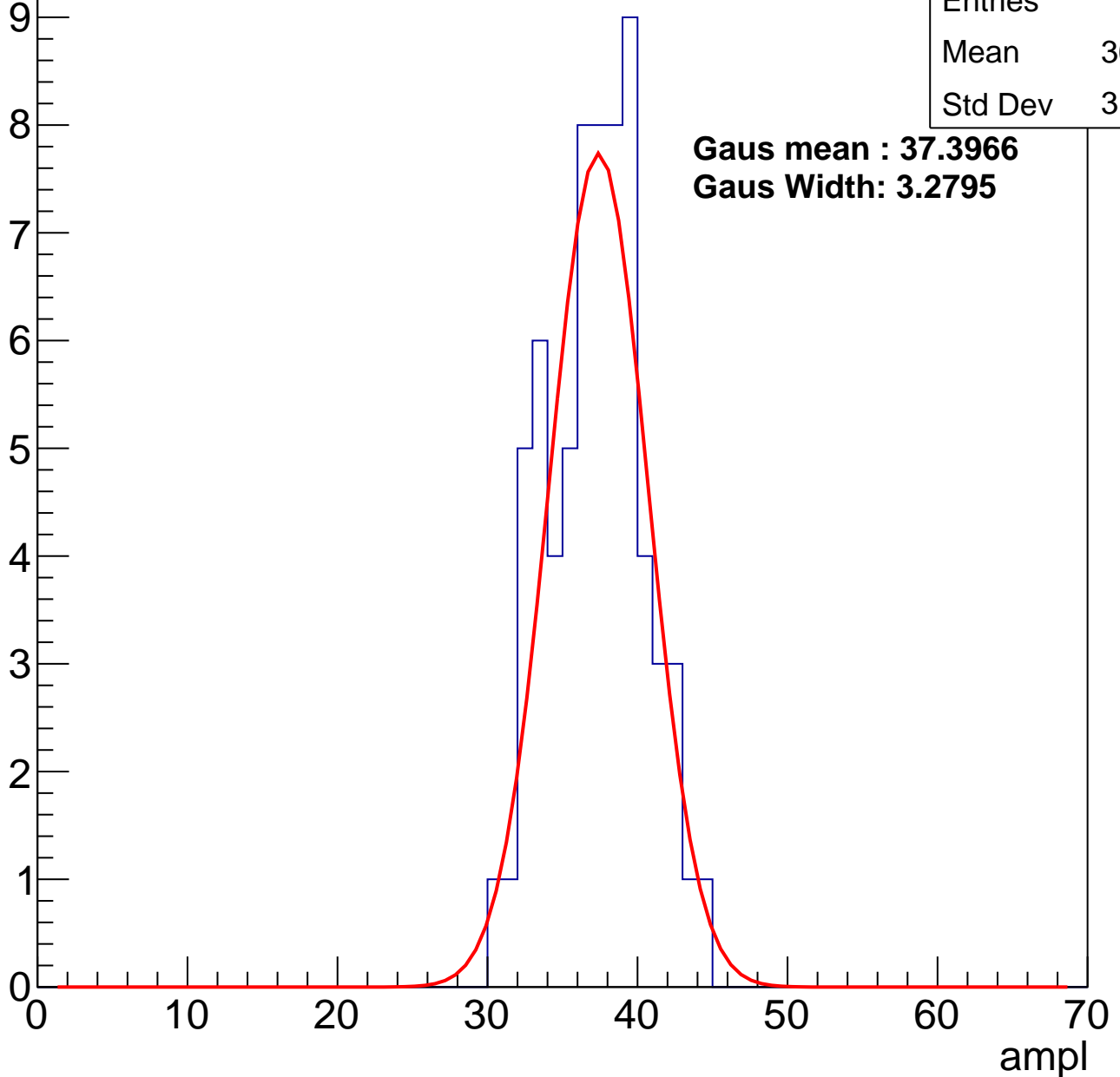
# B0L000S, U19-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.79
Std Dev	3.127

**Gaus mean : 37.3966**  
**Gaus Width: 3.2795**



# B0L000S, U19-ch60, adc2

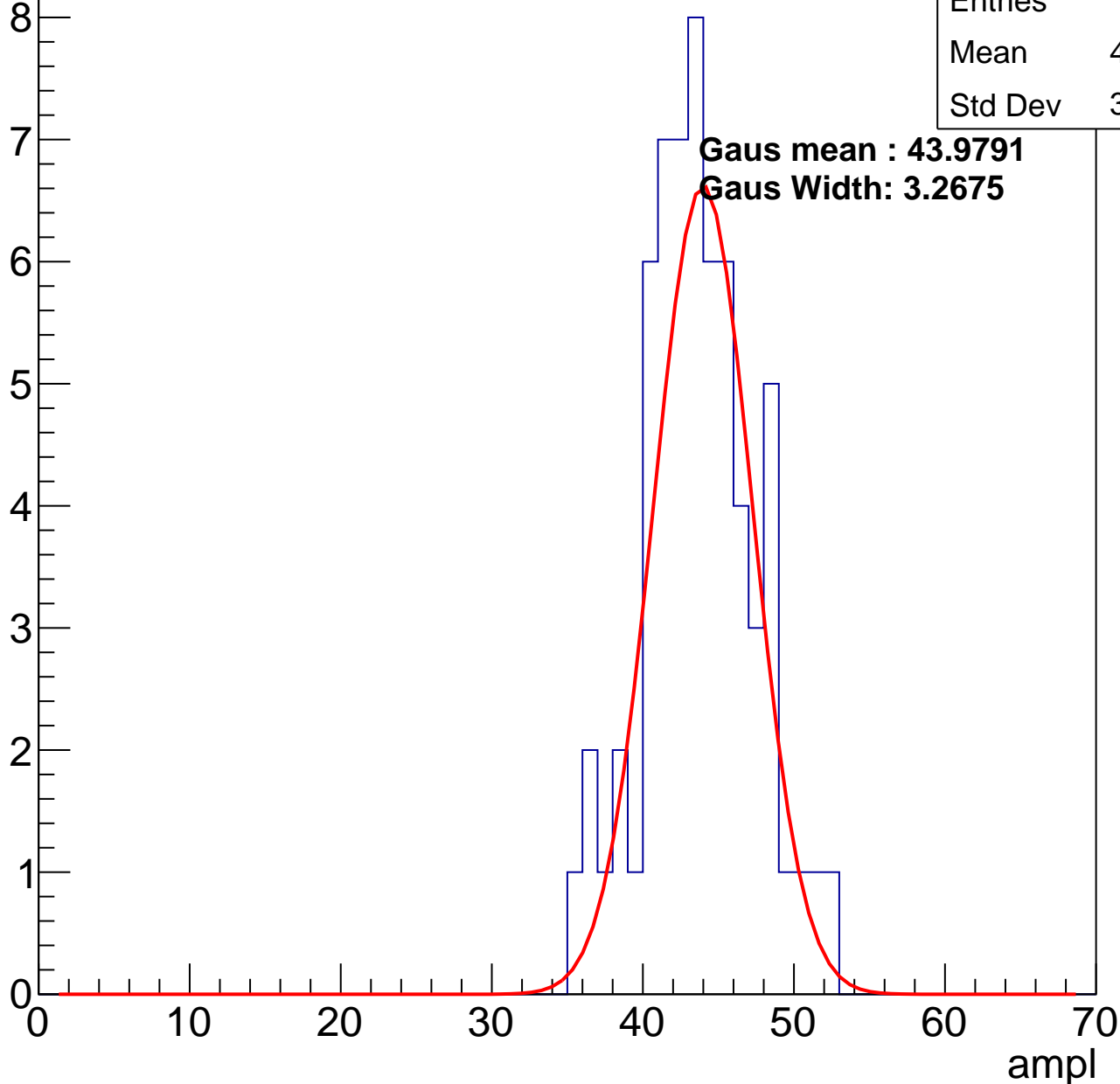
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	43.25
Std Dev	3.603

**Gaus mean : 43.9791**

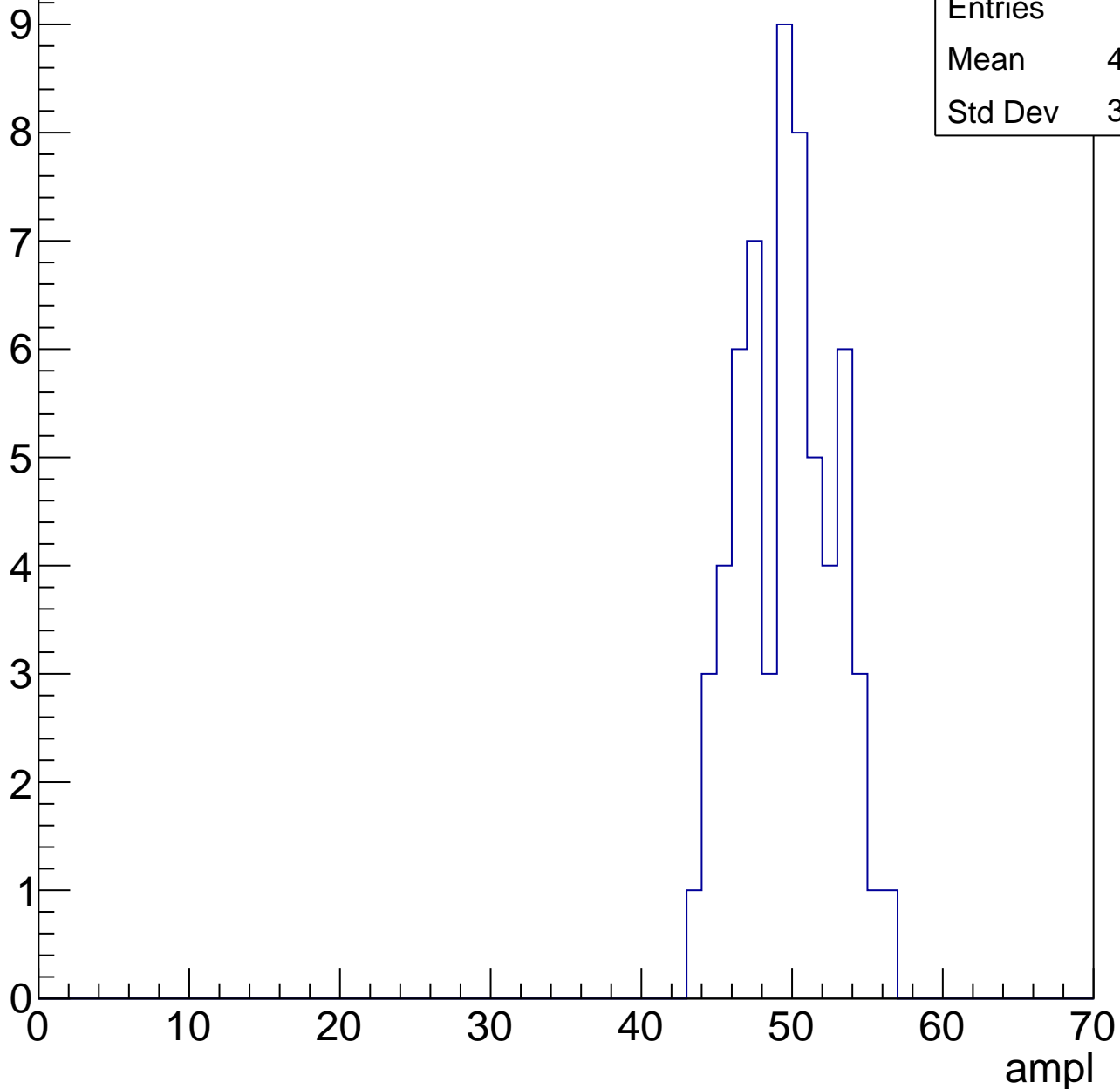
**Gaus Width: 3.2675**



# B0L000S, U19-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



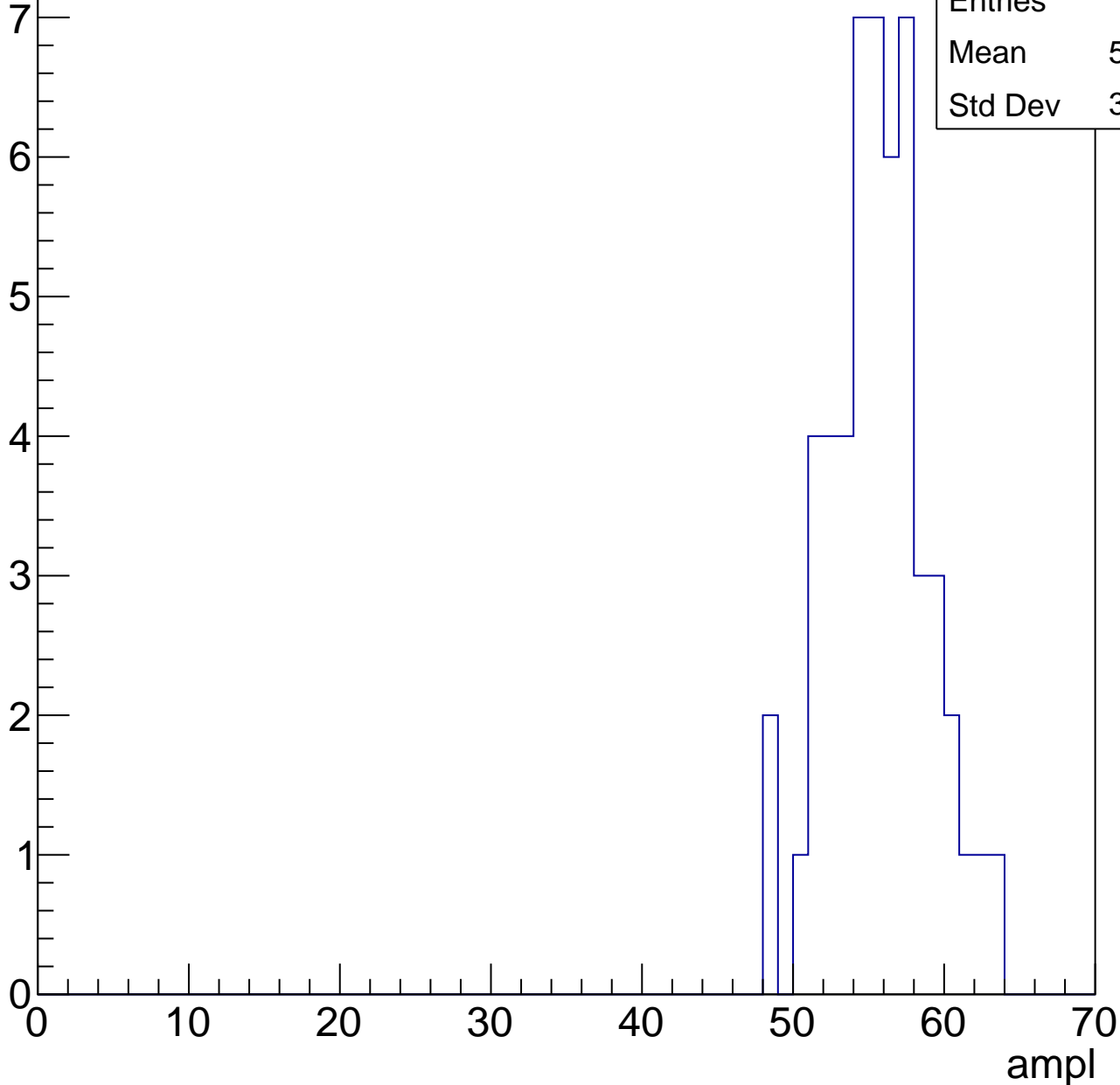
Entries	61
Mean	49.16
Std Dev	3.079

# B0L000S, U19-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	55.19
Std Dev	3.228

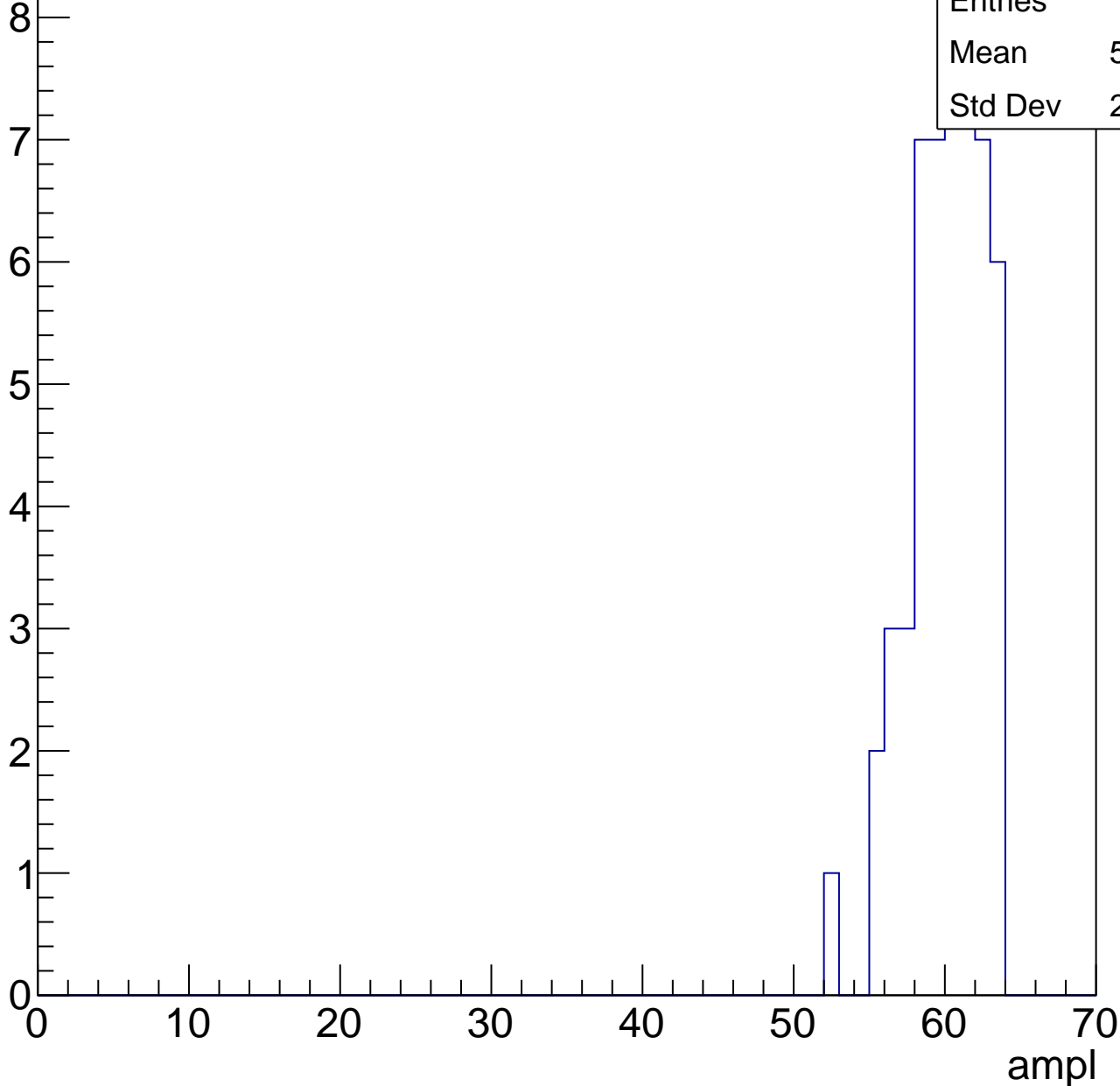


# B0L000S, U19-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	59.62
Std Dev	2.427



# B0L000S, U19-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch61, adc0

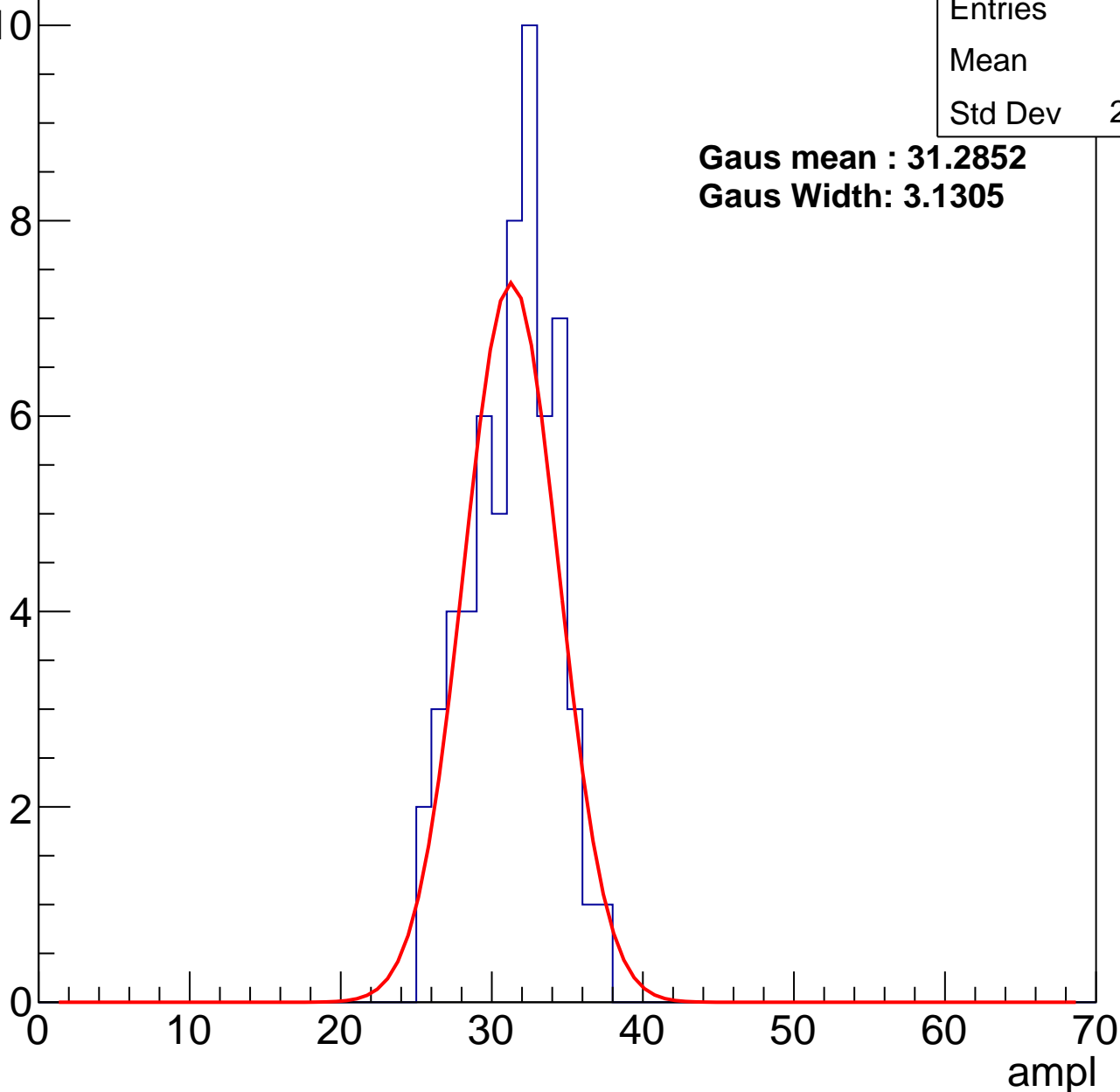
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.9
Std Dev	2.833

**Gaus mean : 31.2852**

**Gaus Width: 3.1305**



# B0L000S, U19-ch61, adc1

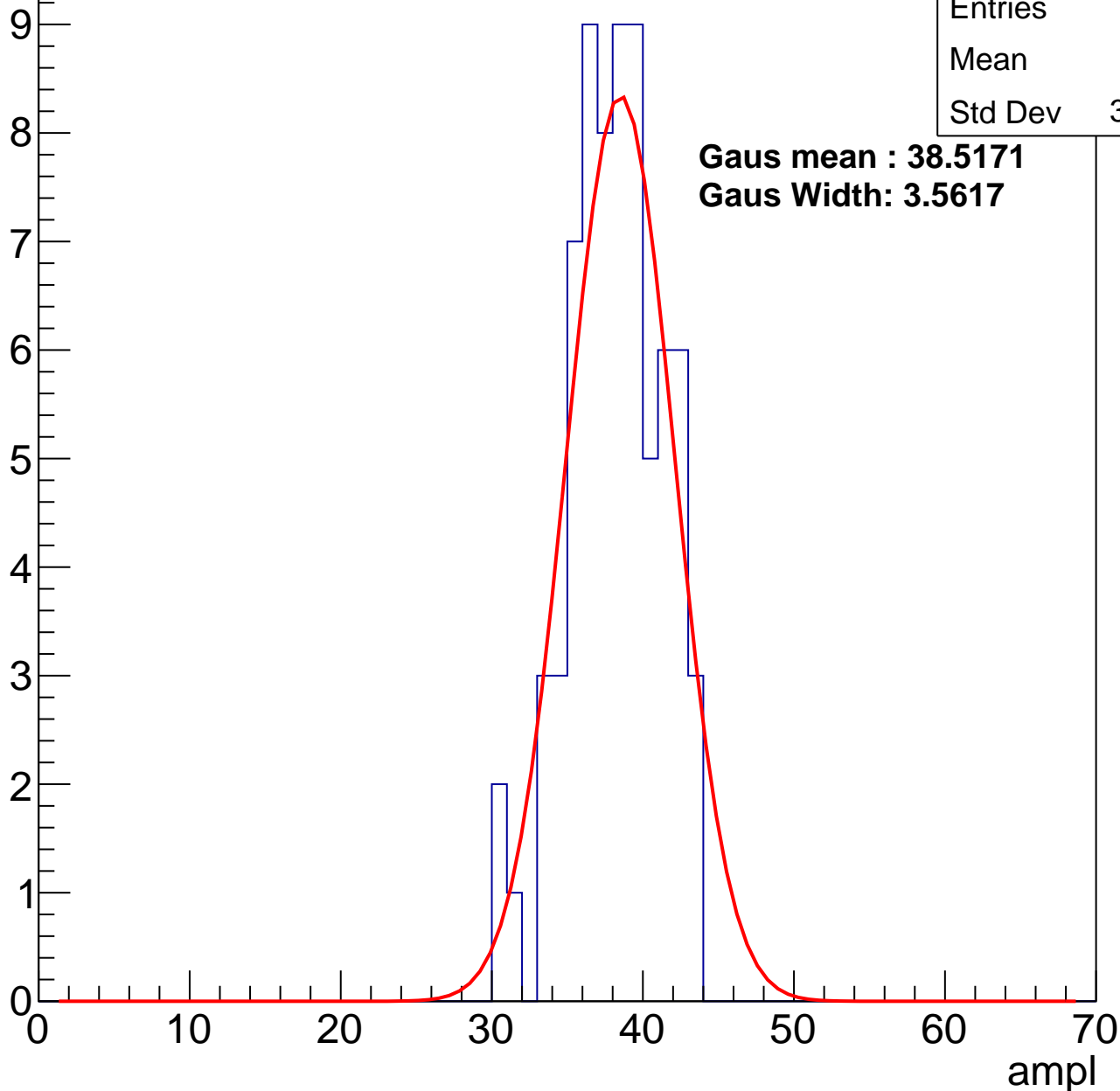
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.7
Std Dev	3.037

**Gaus mean : 38.5171**

**Gaus Width: 3.5617**



# B0L000S, U19-ch61, adc2

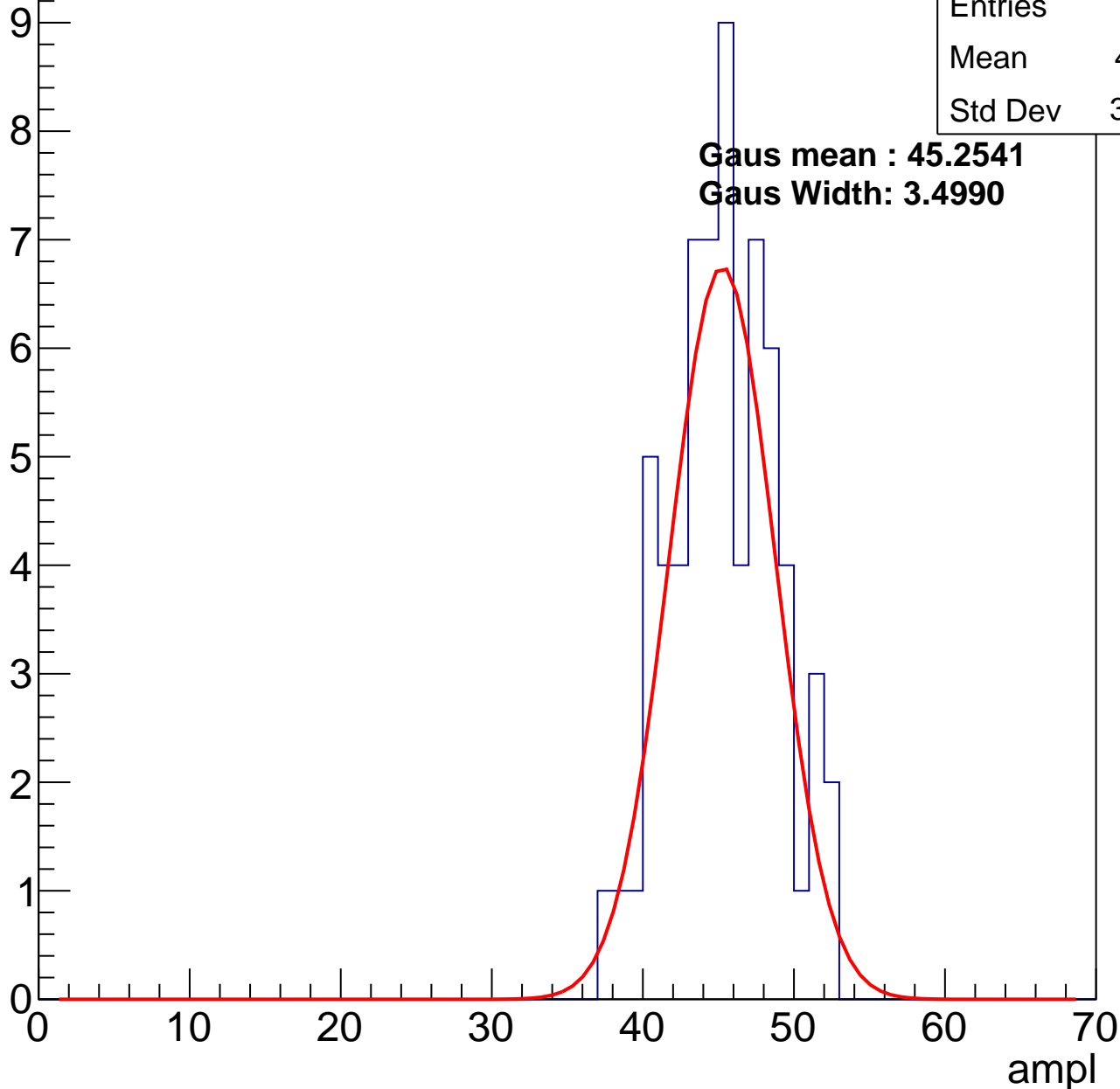
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.91
Std Dev	3.459

**Gaus mean : 45.2541**

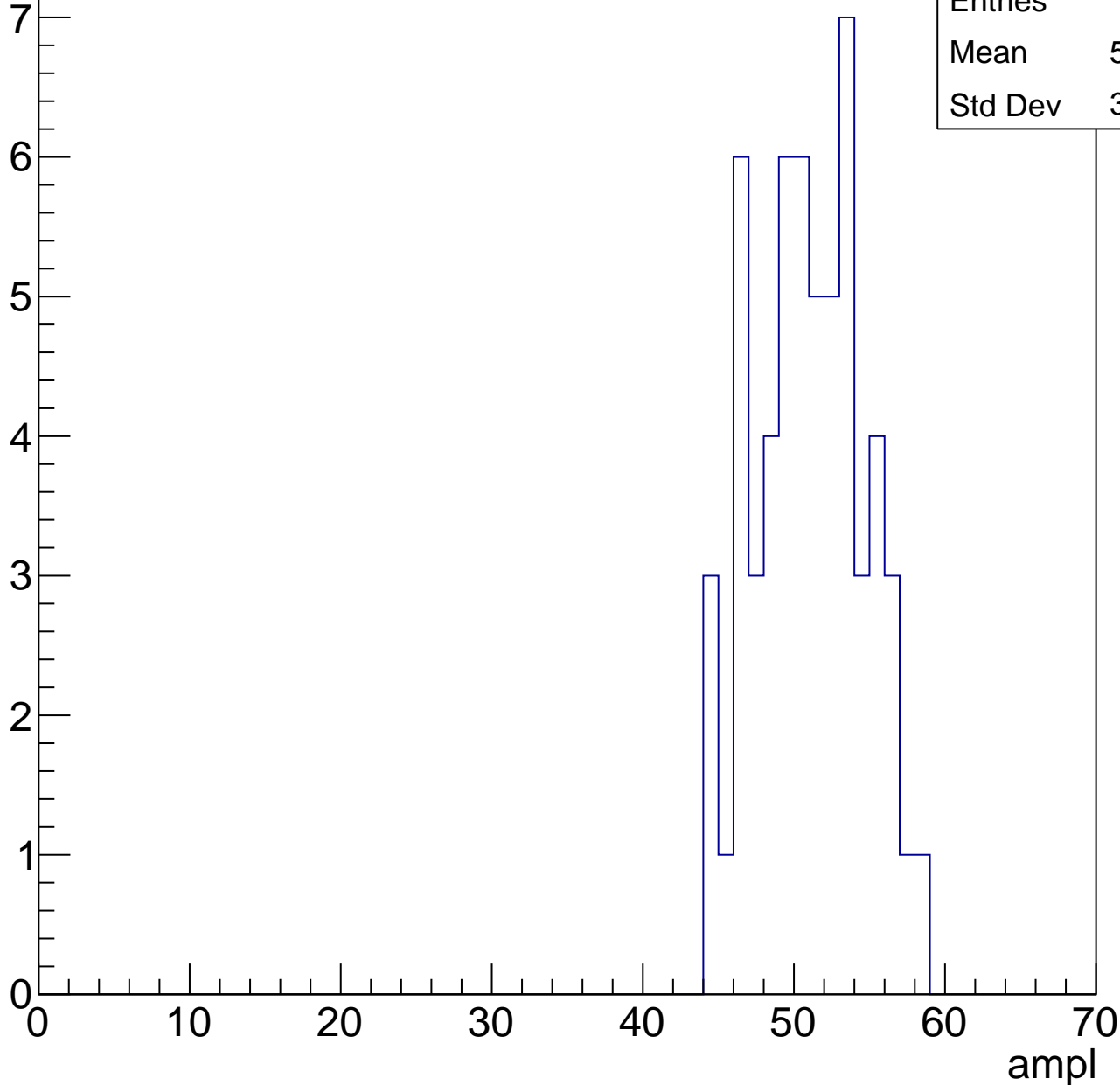
**Gaus Width: 3.4990**



# B0L000S, U19-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



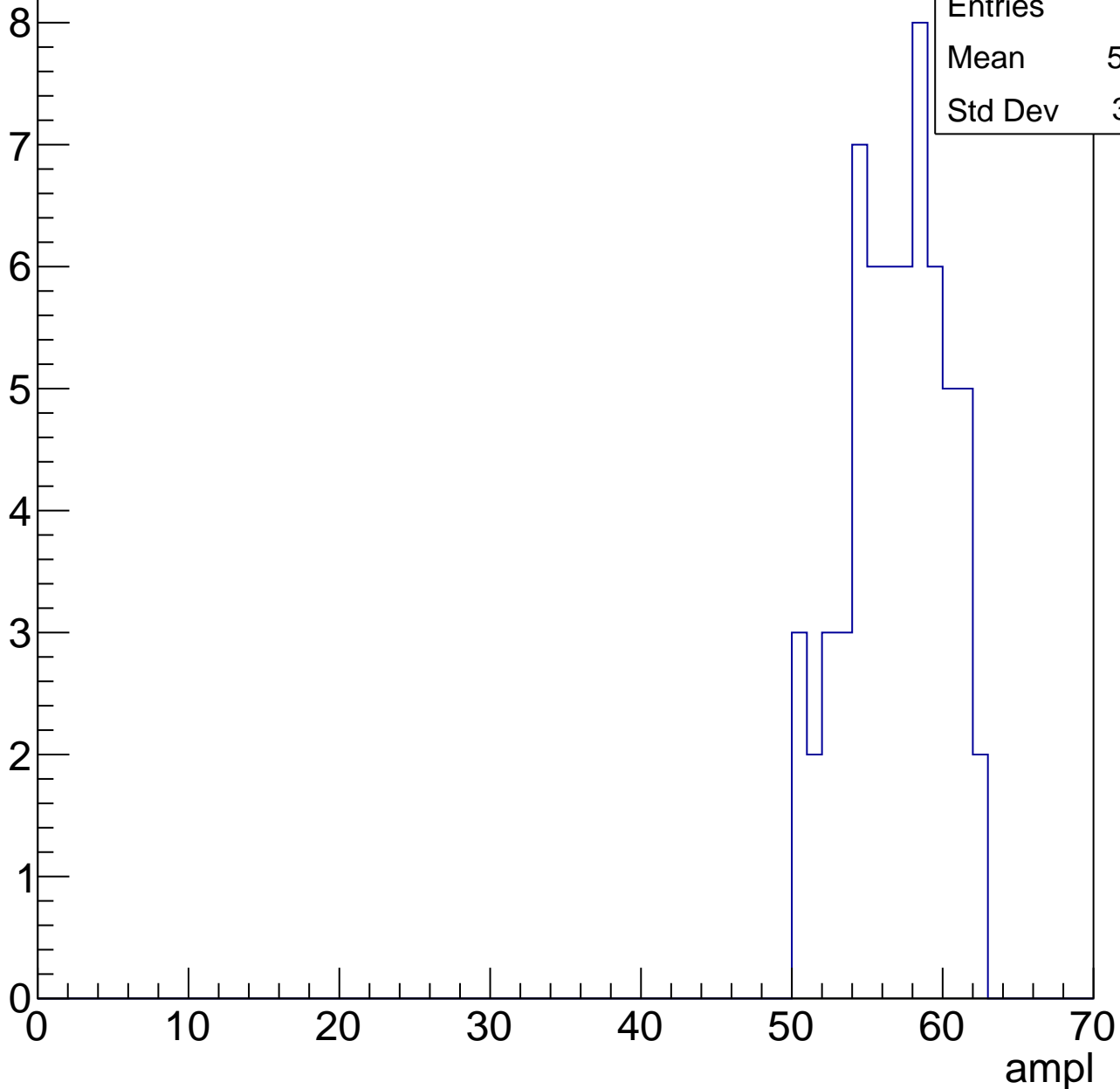
Entries	58
Mean	50.53
Std Dev	3.519

# B0L000S, U19-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	56.45
Std Dev	3.171

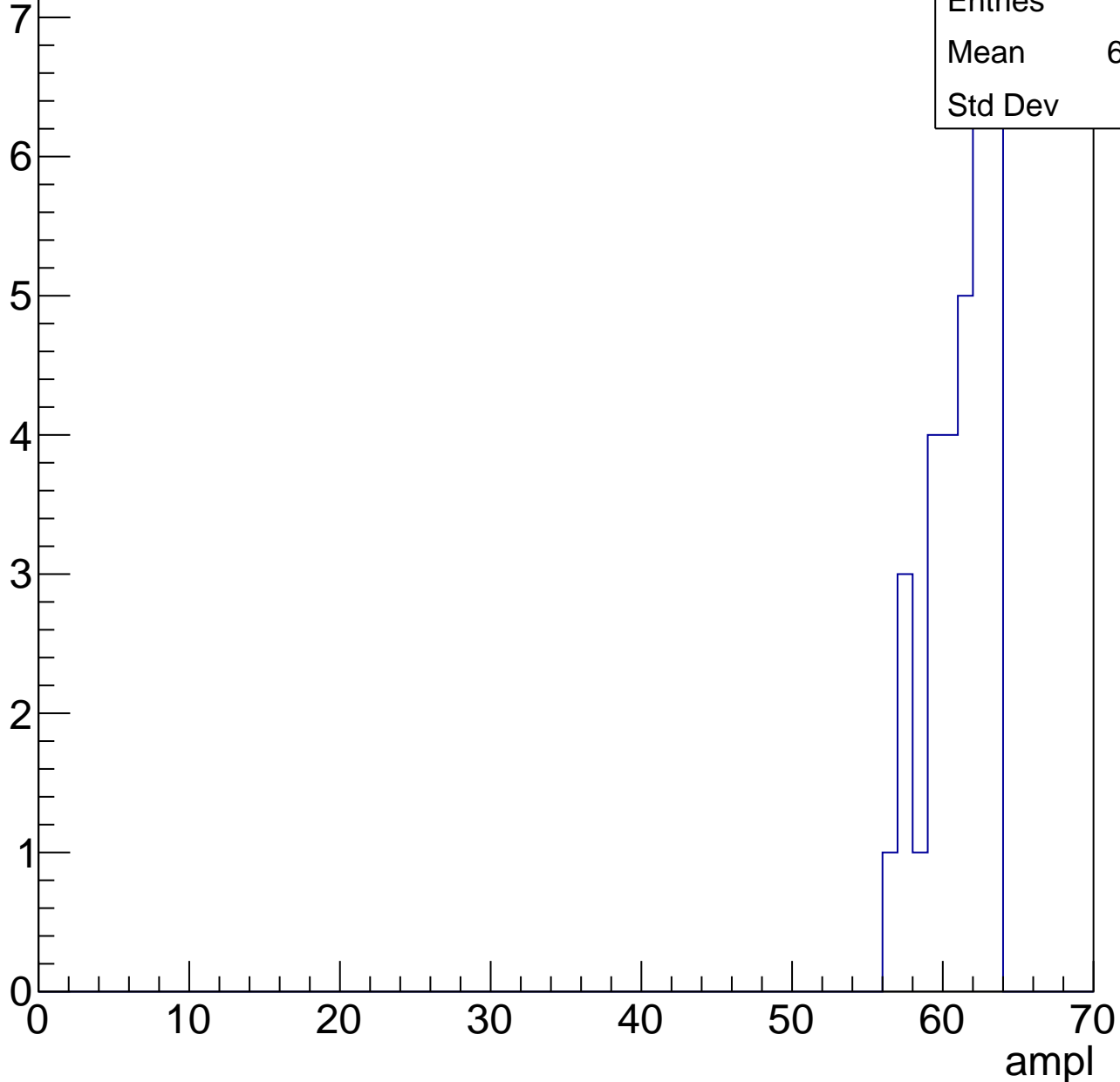


# B0L000S, U19-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

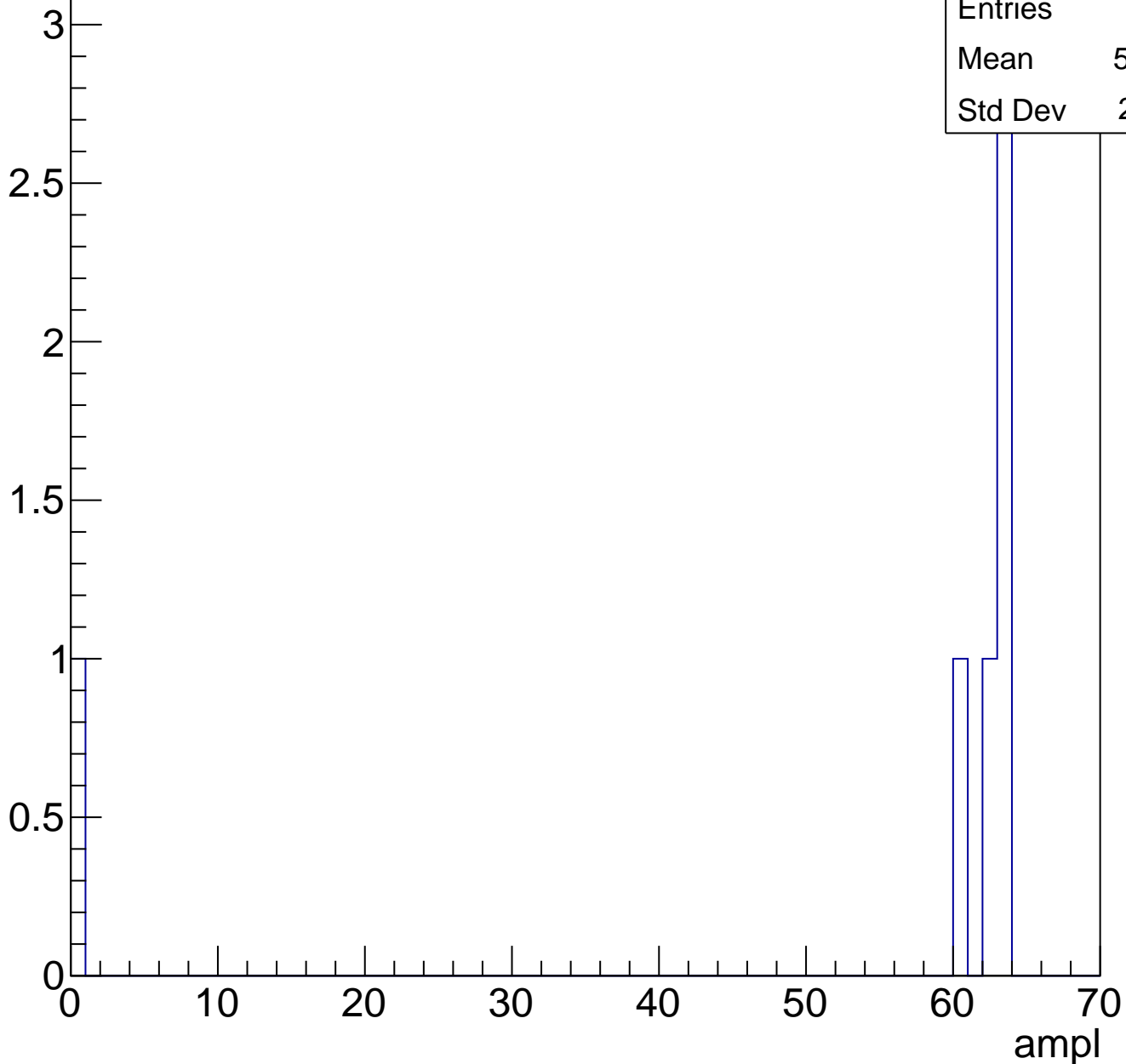
Entries	32
Mean	60.66
Std Dev	2.04



# B0L000S, U19-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B0L000S, U19-ch62, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	29.57
Std Dev	3.041

**Gaus mean : 30.0979**

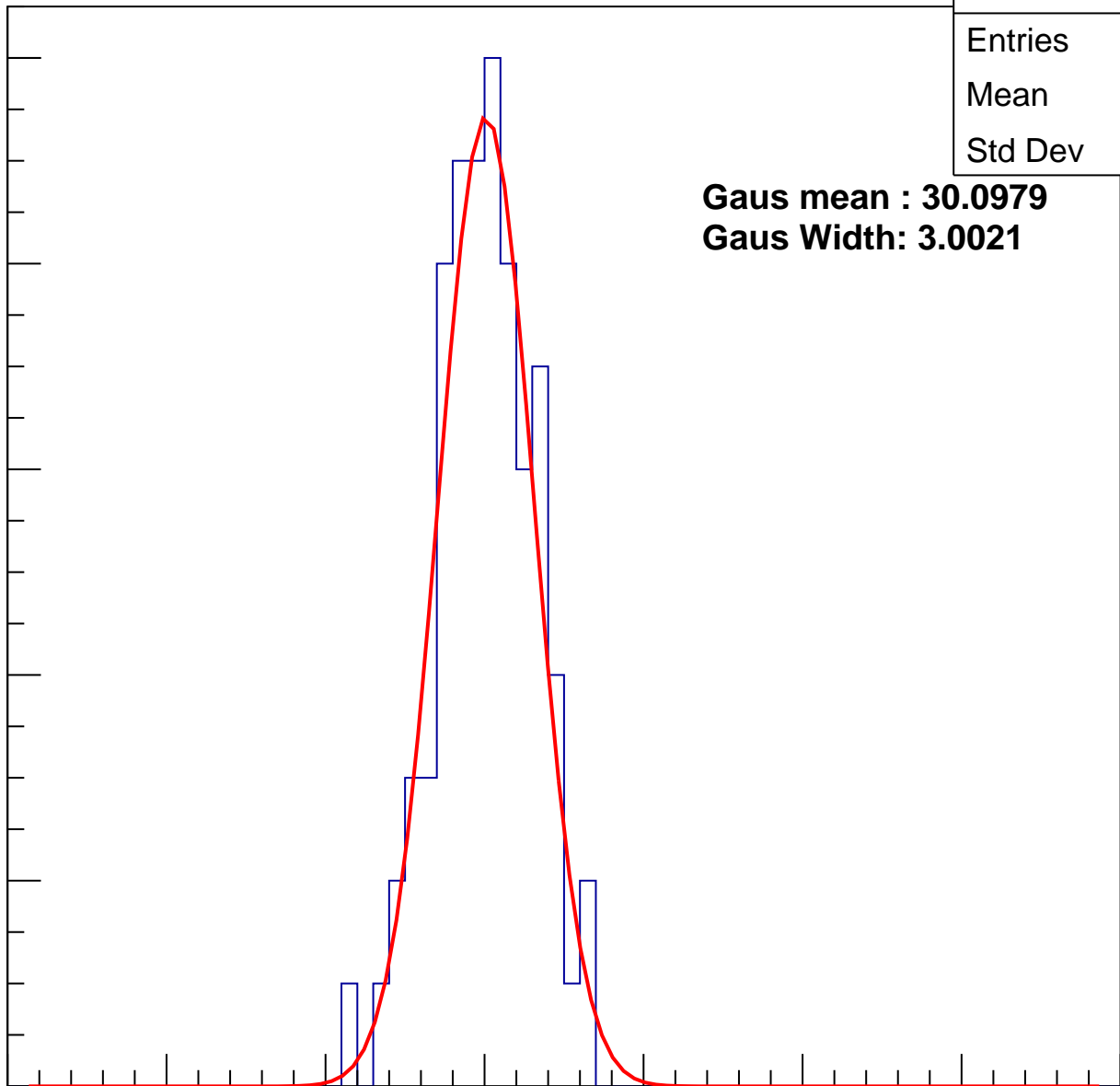
**Gaus Width: 3.0021**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch62, adc1

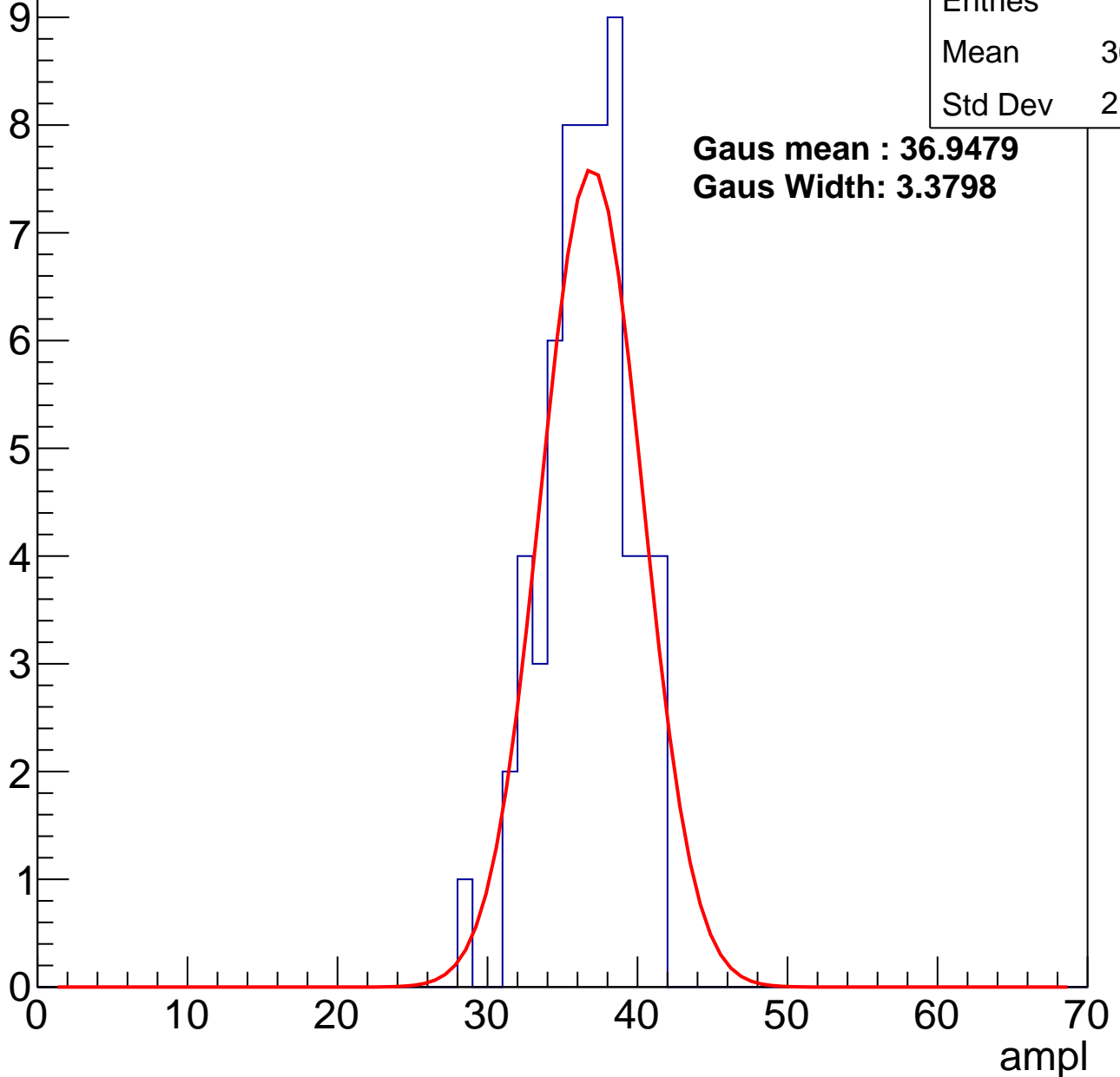
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.18
Std Dev	2.802

**Gaus mean : 36.9479**

**Gaus Width: 3.3798**



# B0L000S, U19-ch62, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	43.01
Std Dev	3.136

**Gaus mean : 43.6825**

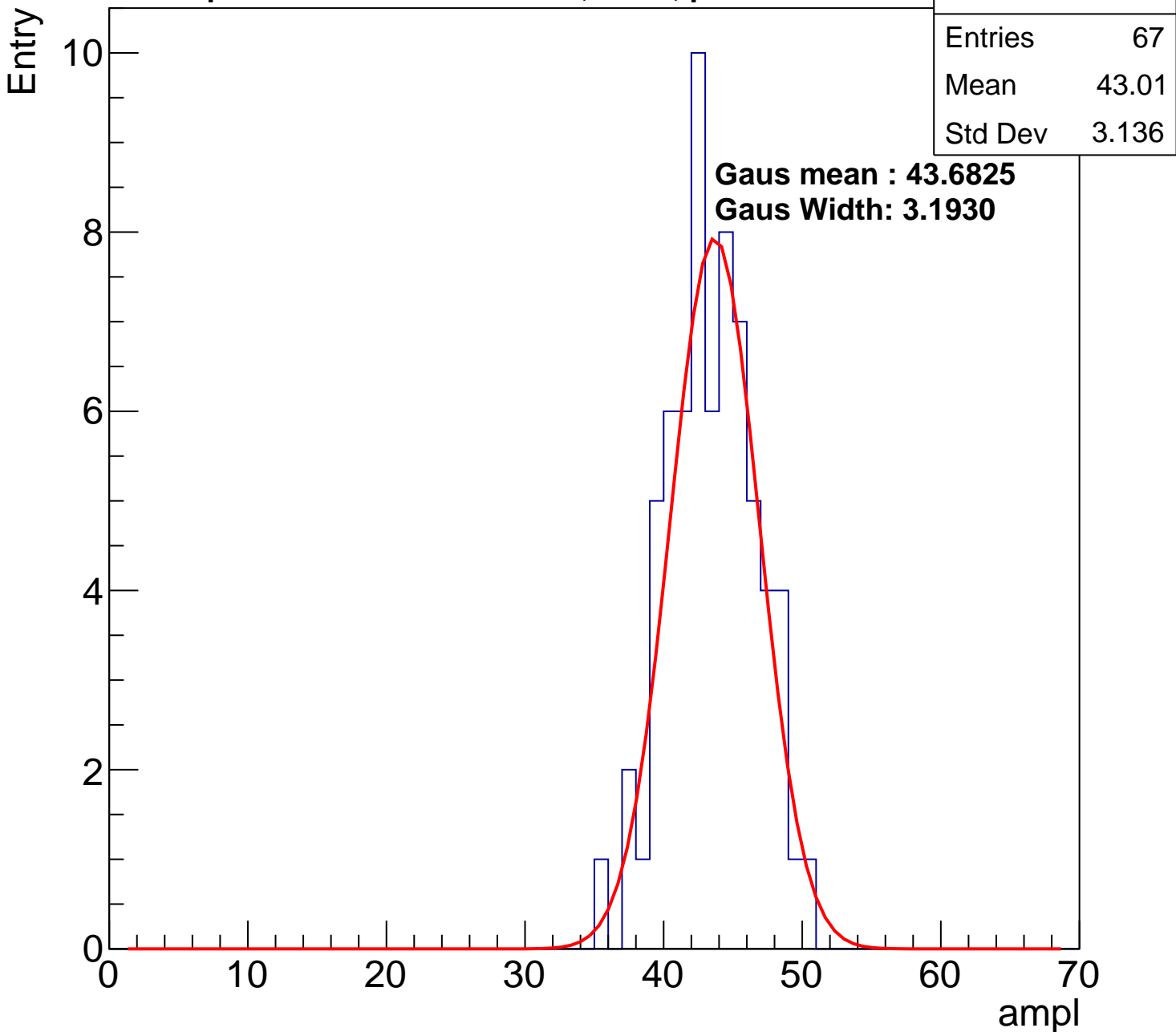
**Gaus Width: 3.1930**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

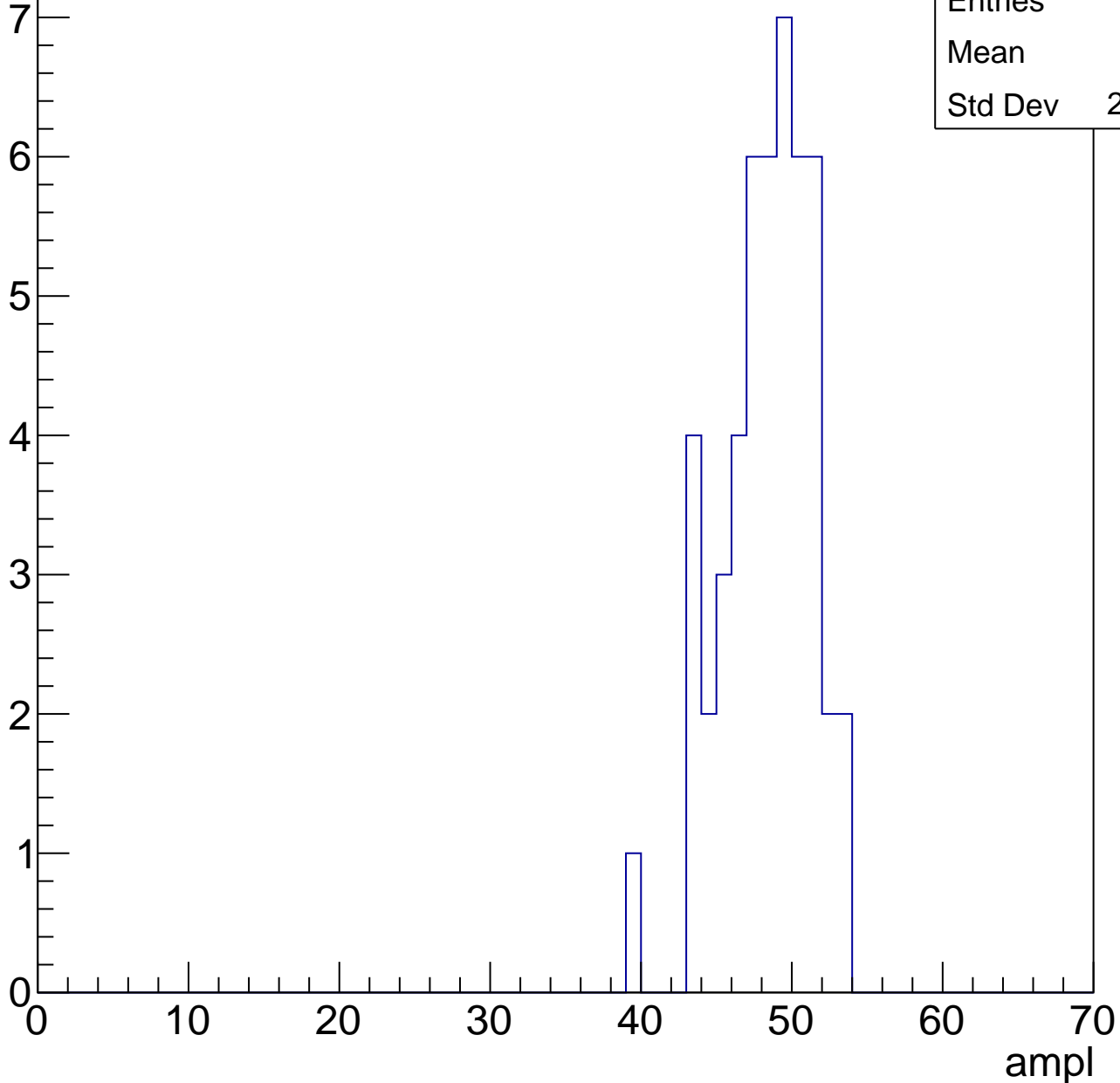


# B0L000S, U19-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

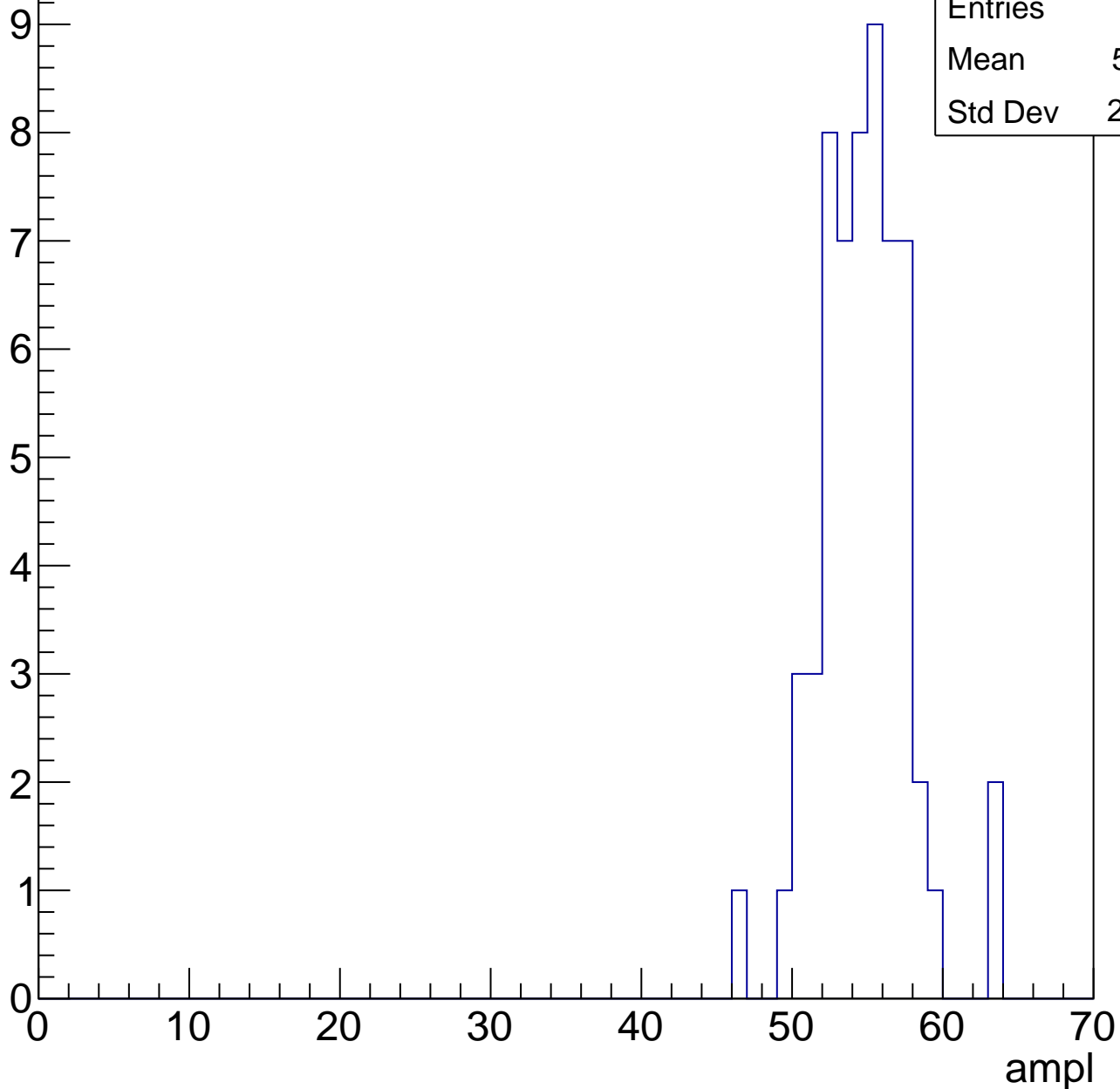
Entries	49
Mean	47.9
Std Dev	2.957



# B0L000S, U19-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

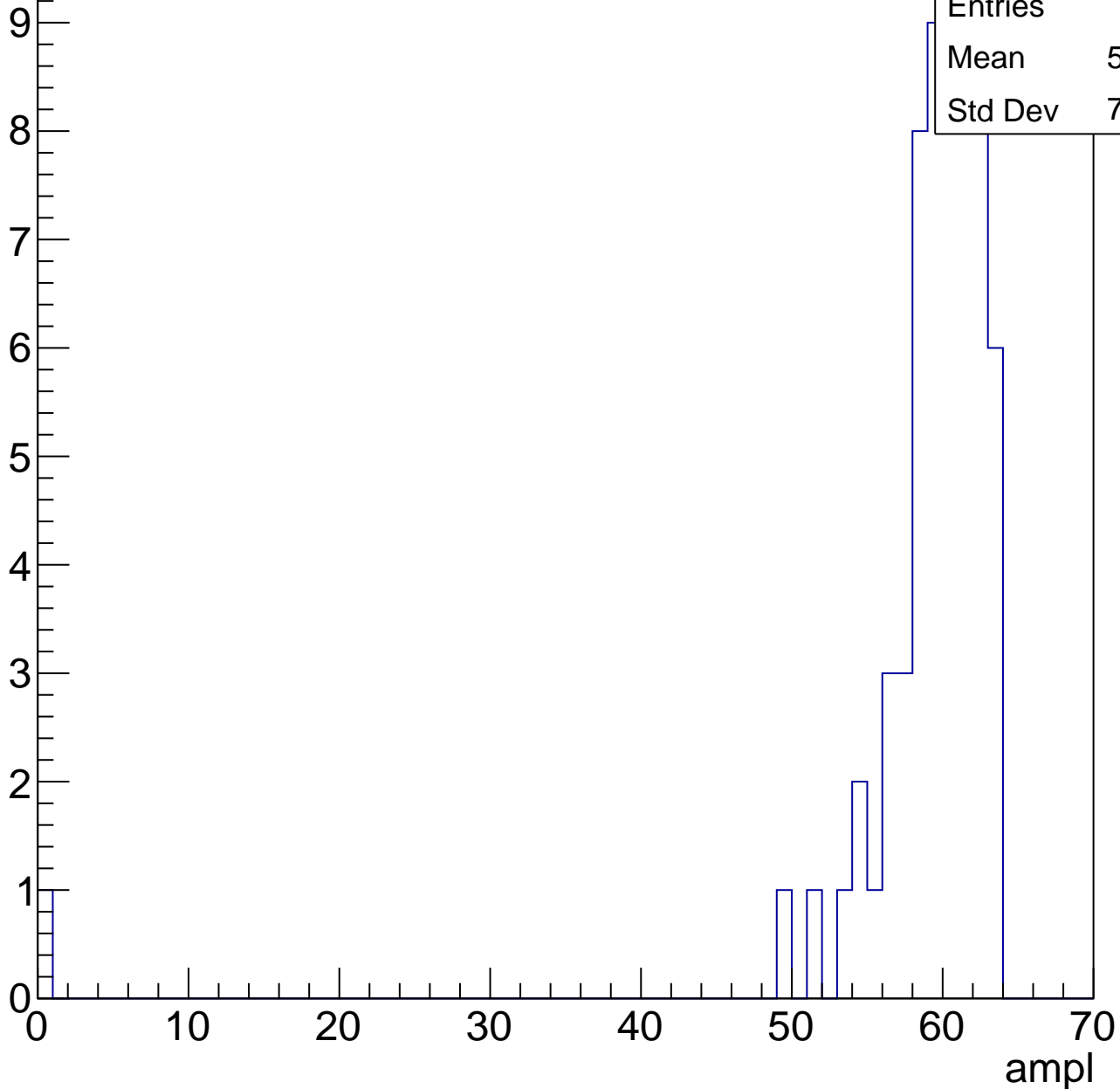


# B0L000S, U19-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	58.33
Std Dev	7.956



# B0L000S, U19-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch63, adc0

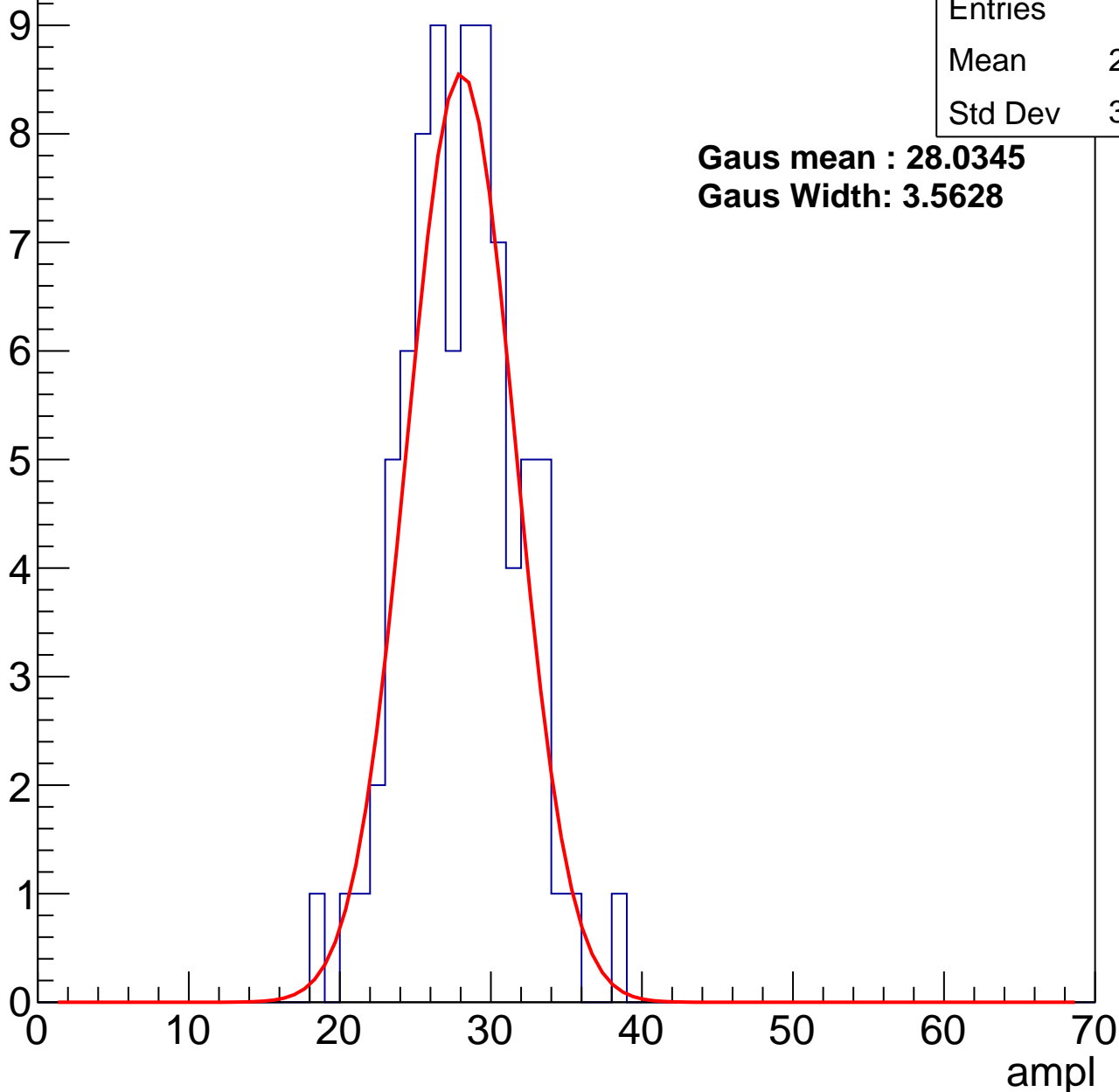
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	27.62
Std Dev	3.636

**Gaus mean : 28.0345**

**Gaus Width: 3.5628**



# B0L000S, U19-ch63, adc1

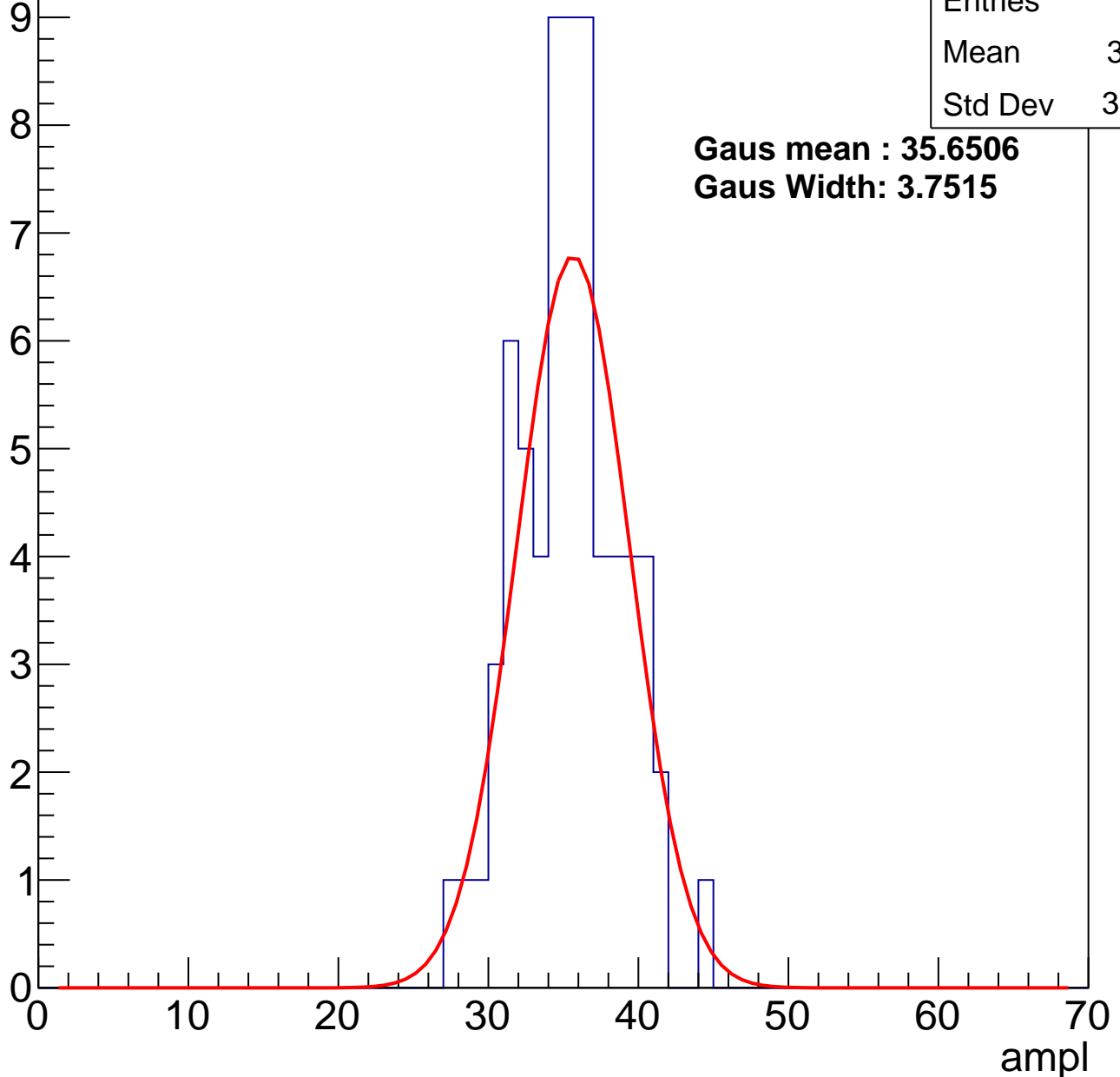
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	34.91
Std Dev	3.384

**Gaus mean : 35.6506**

**Gaus Width: 3.7515**

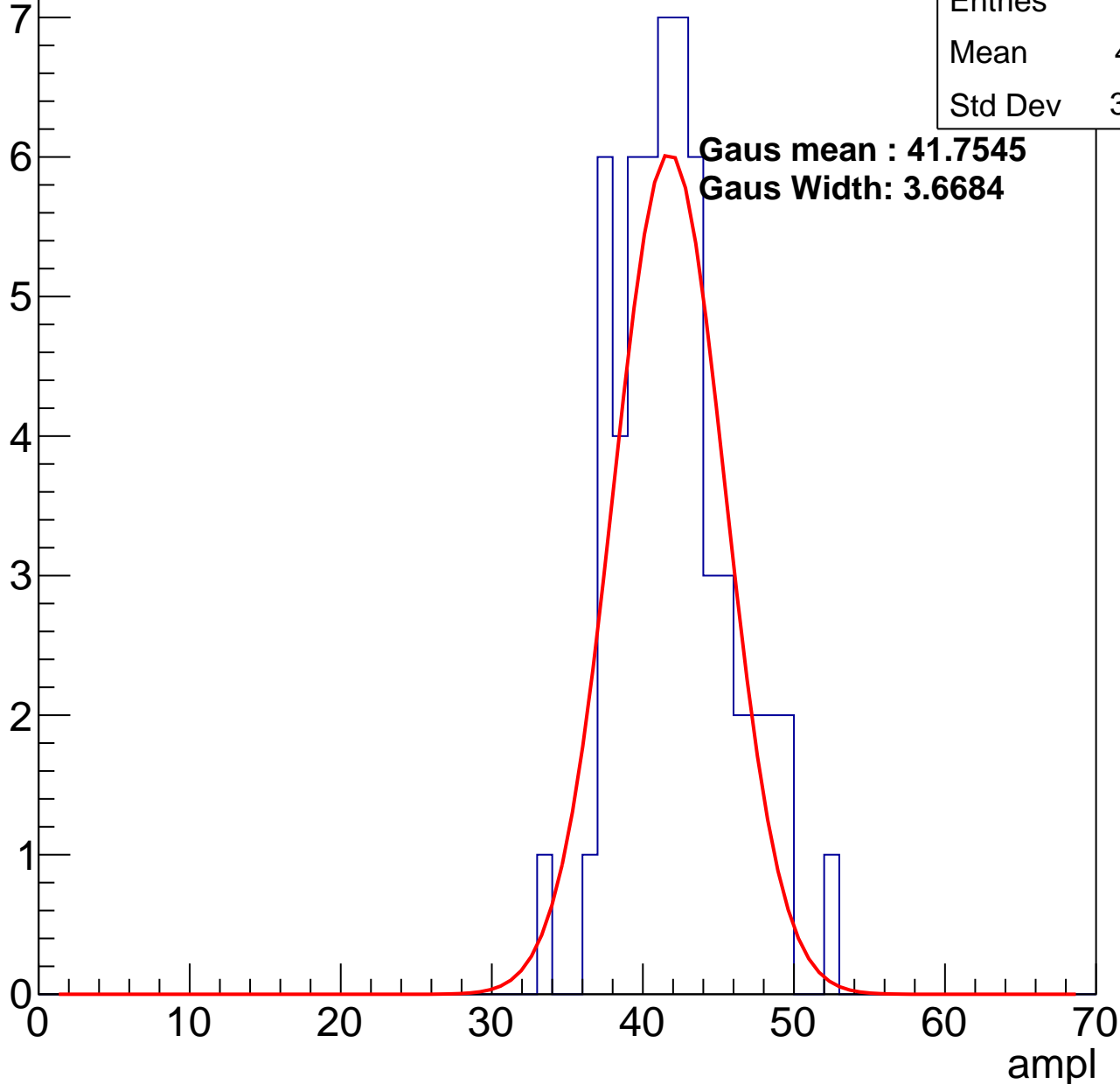


# B0L000S, U19-ch63, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	41.61
Std Dev	3.692

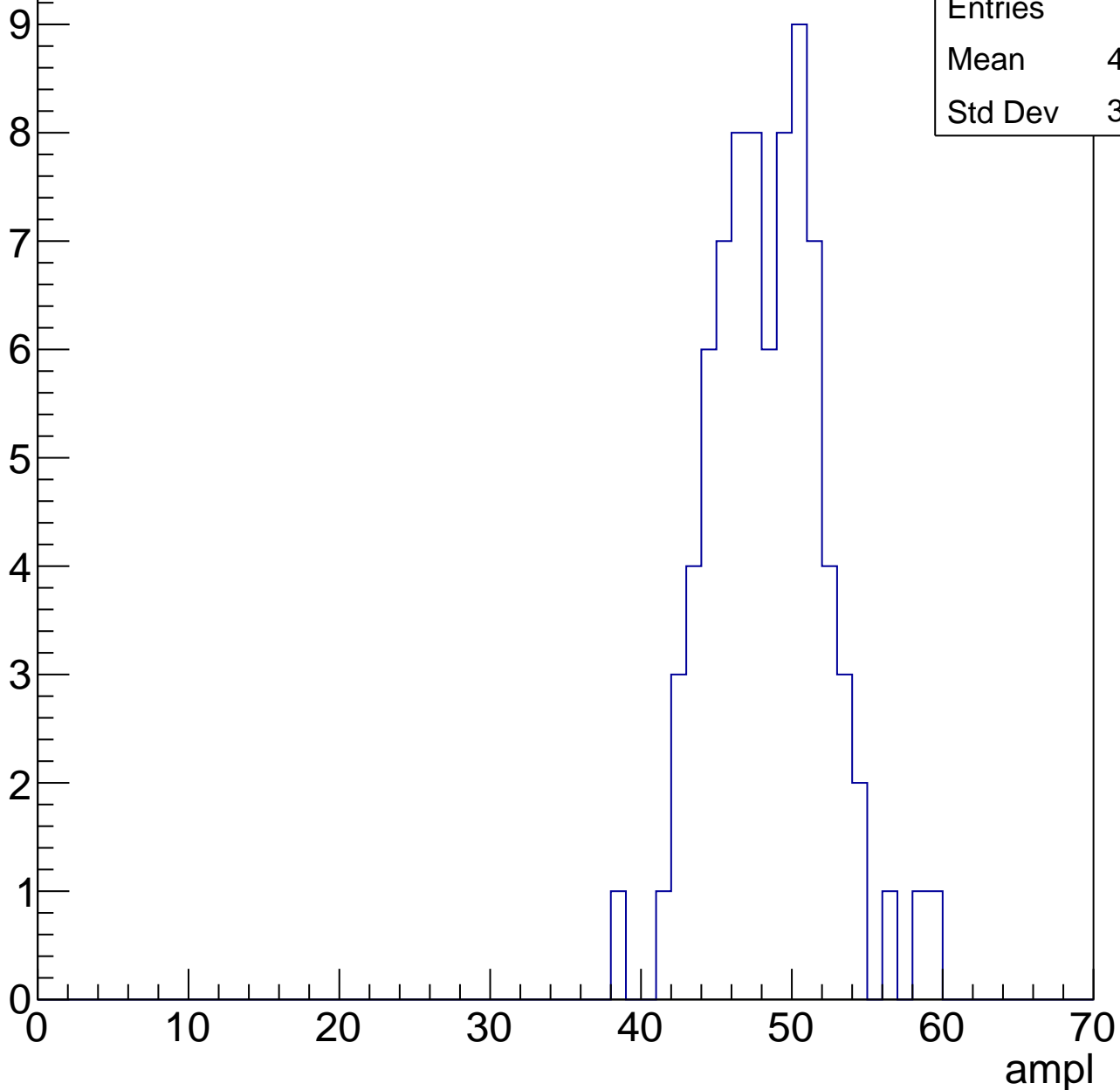


# B0L000S, U19-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	47.94
Std Dev	3.806



# B0L000S, U19-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	47
Mean	54.21
Std Dev	2.649

Entry

10

8

6

4

2

0

0

10

20

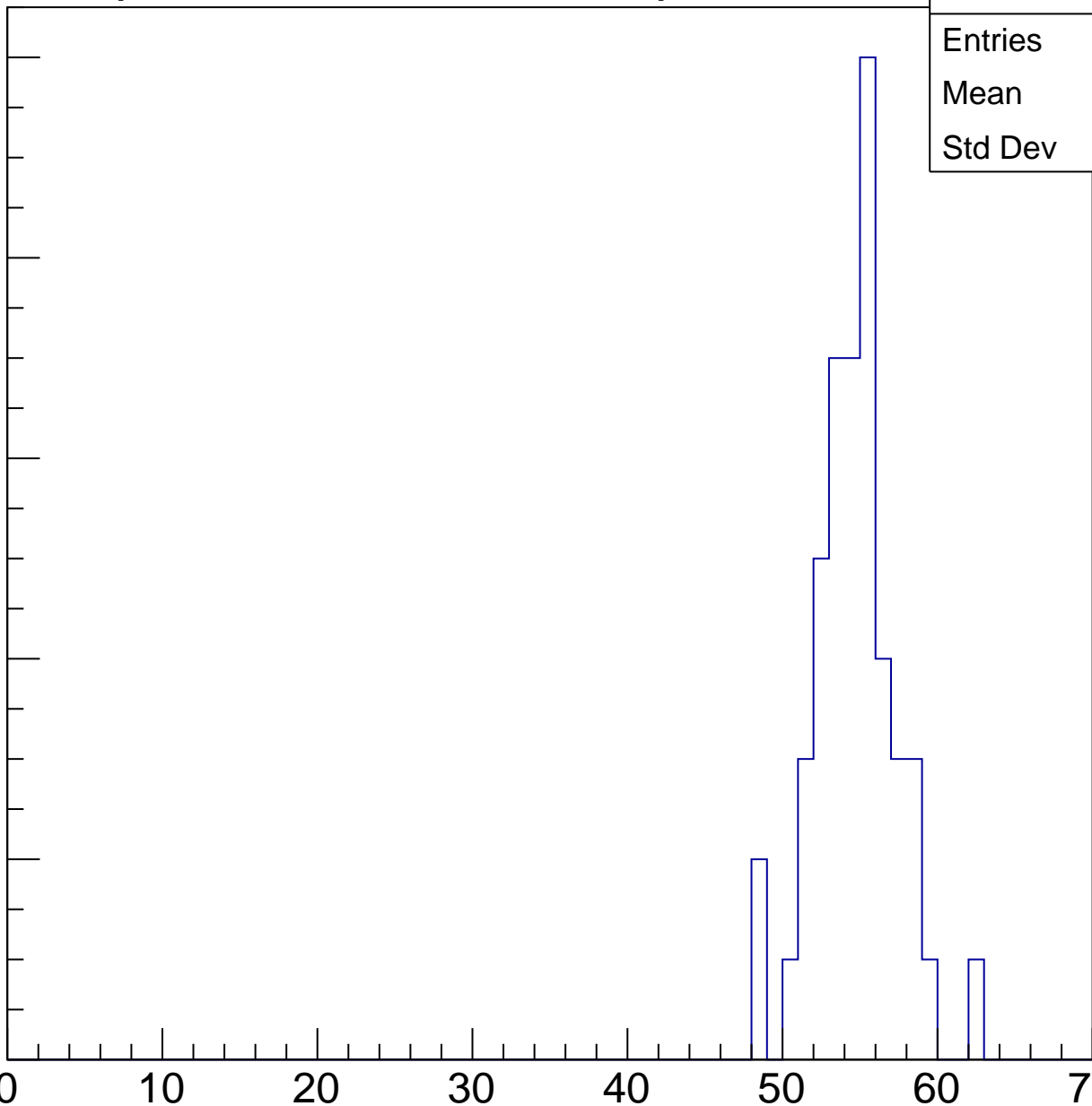
30

40

50

60

ampl

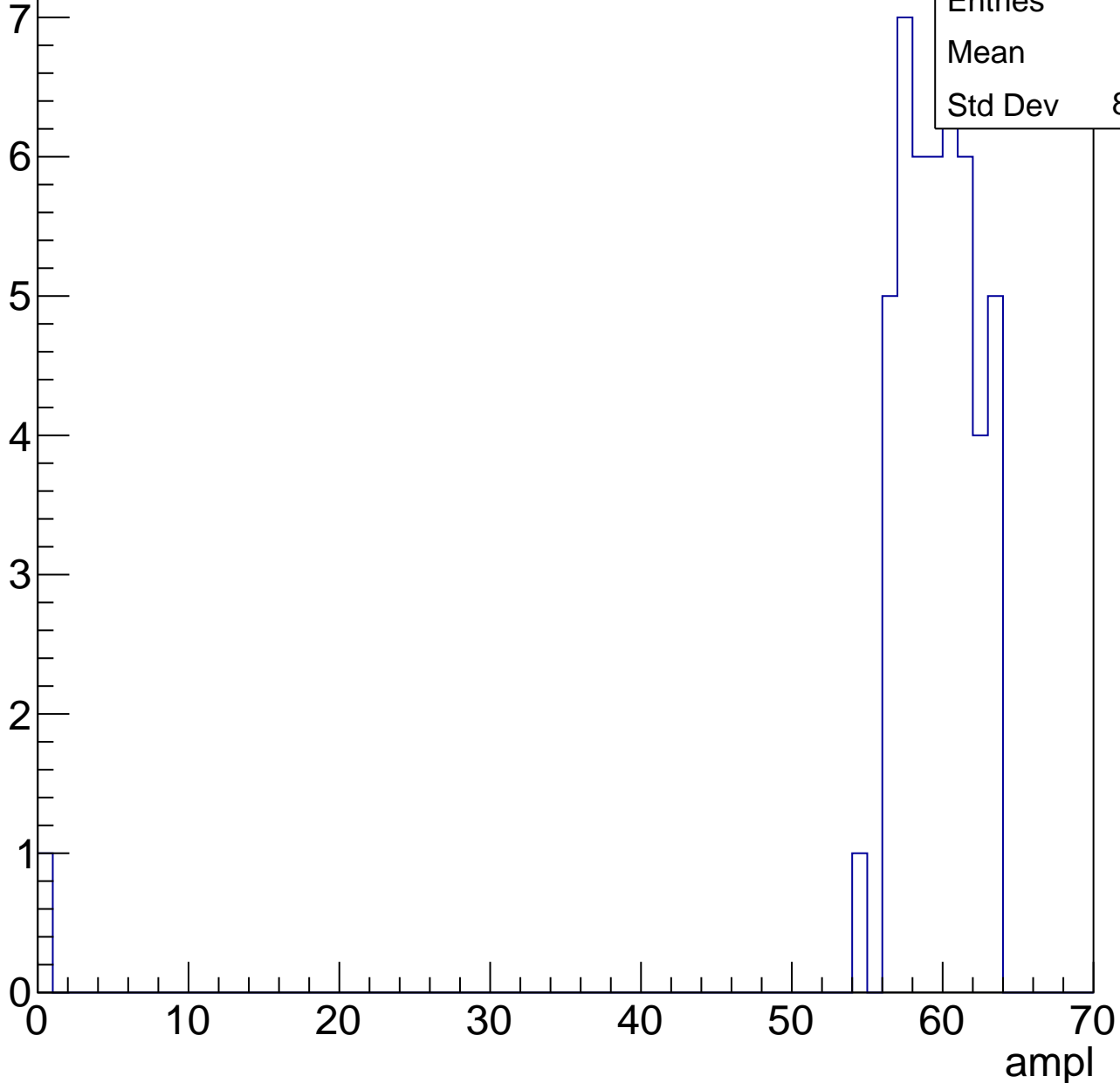


# B0L000S, U19-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	58
Std Dev	8.761

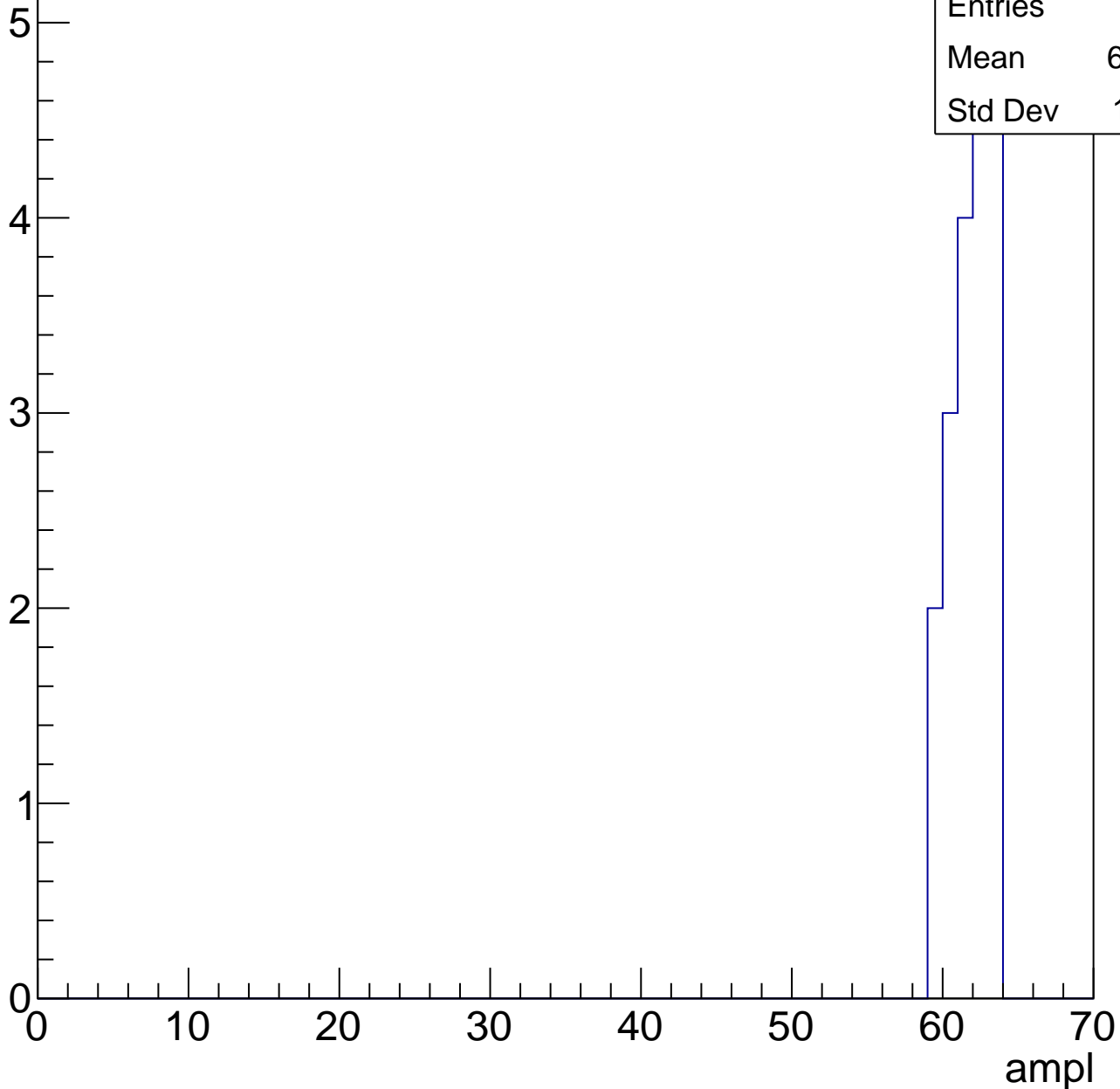


# B0L000S, U19-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	19
Mean	61.42
Std Dev	1.311





# B0L000S, U19-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch64, adc0

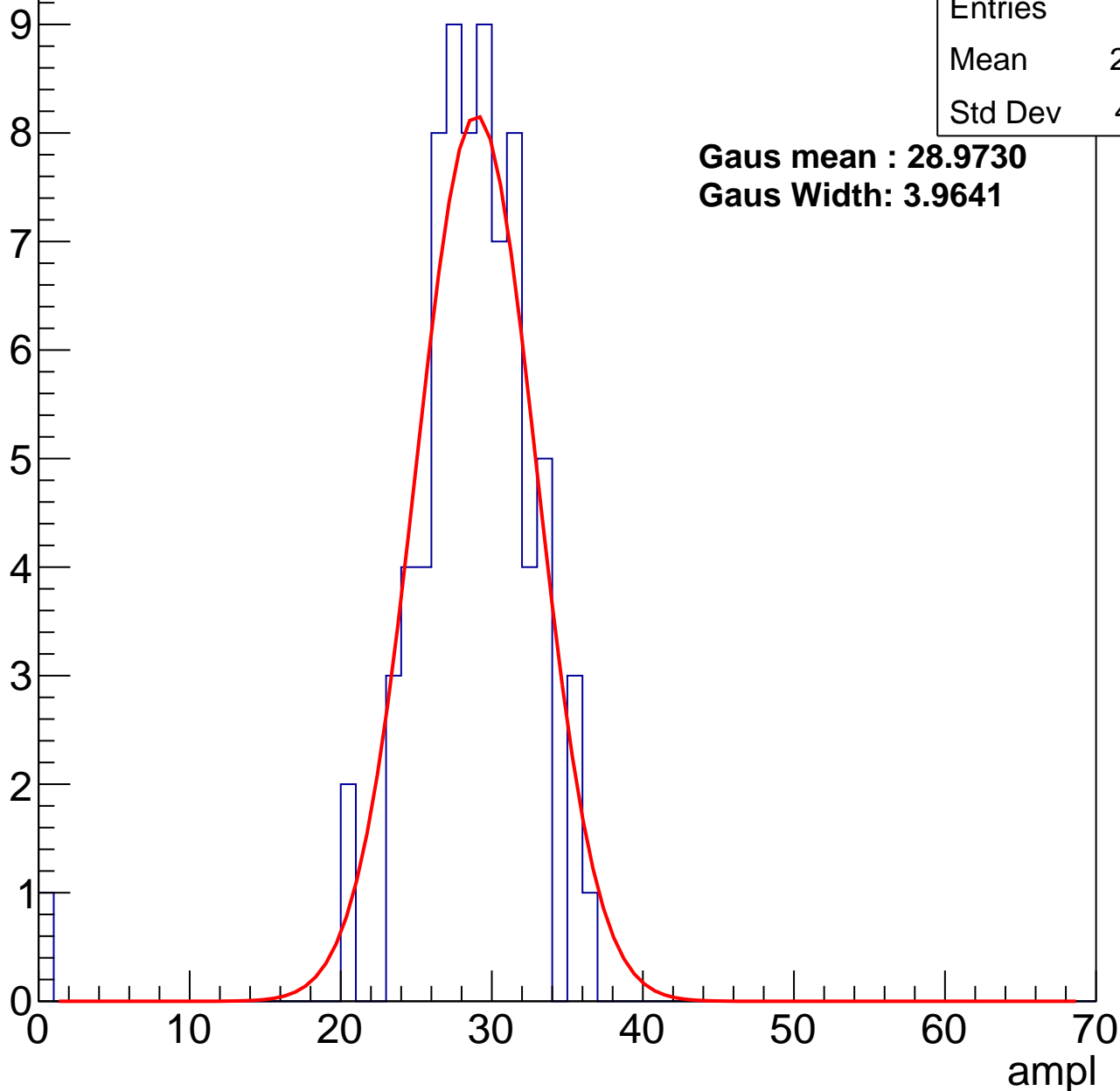
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	28.07
Std Dev	4.641

**Gaus mean : 28.9730**

**Gaus Width: 3.9641**



# B0L000S, U19-ch64, adc1

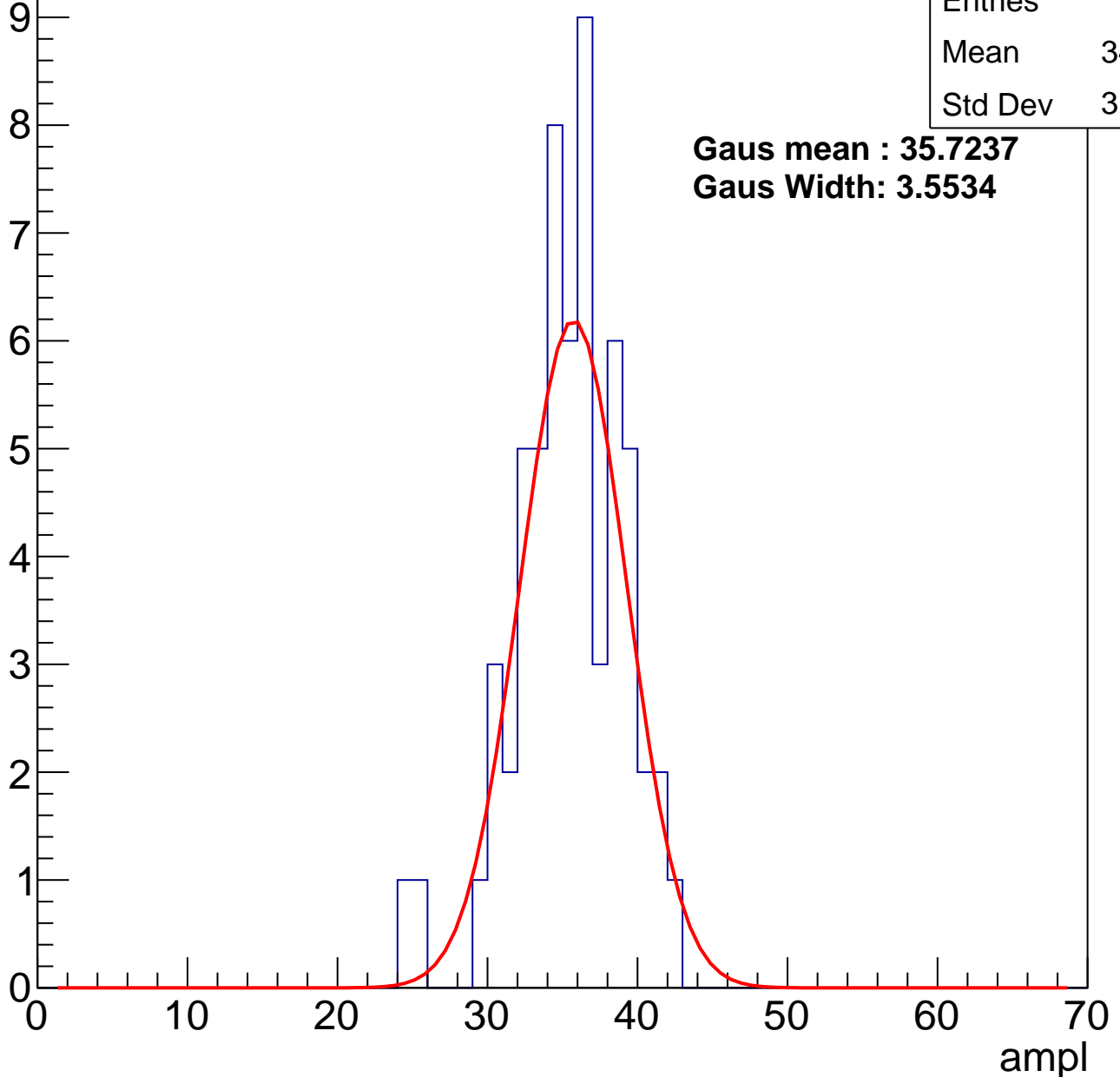
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	34.98
Std Dev	3.566

**Gaus mean : 35.7237**

**Gaus Width: 3.5534**



# B0L000S, U19-ch64, adc2

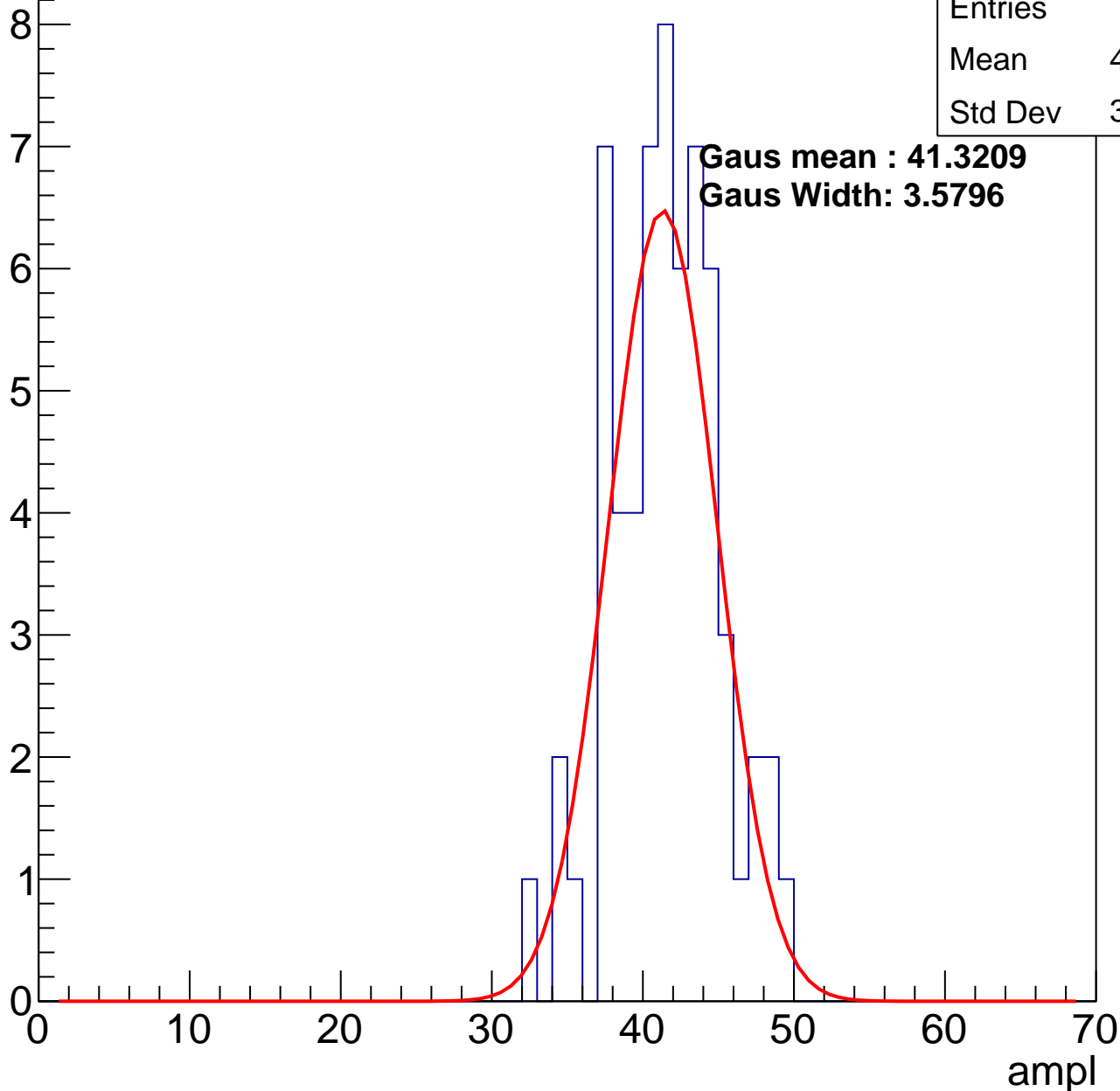
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	41.08
Std Dev	3.548

**Gaus mean : 41.3209**

**Gaus Width: 3.5796**

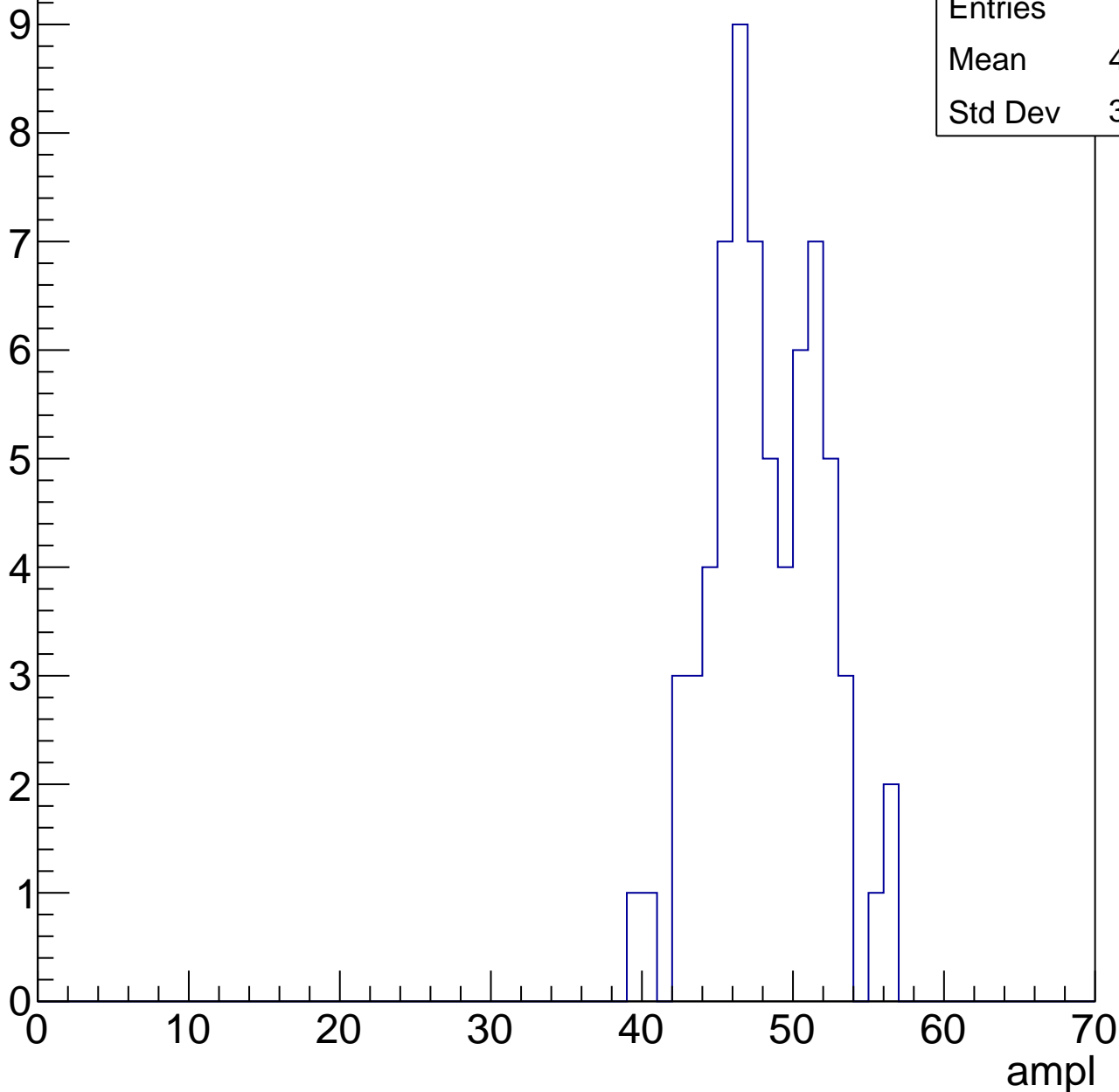


# B0L000S, U19-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	47.75
Std Dev	3.672

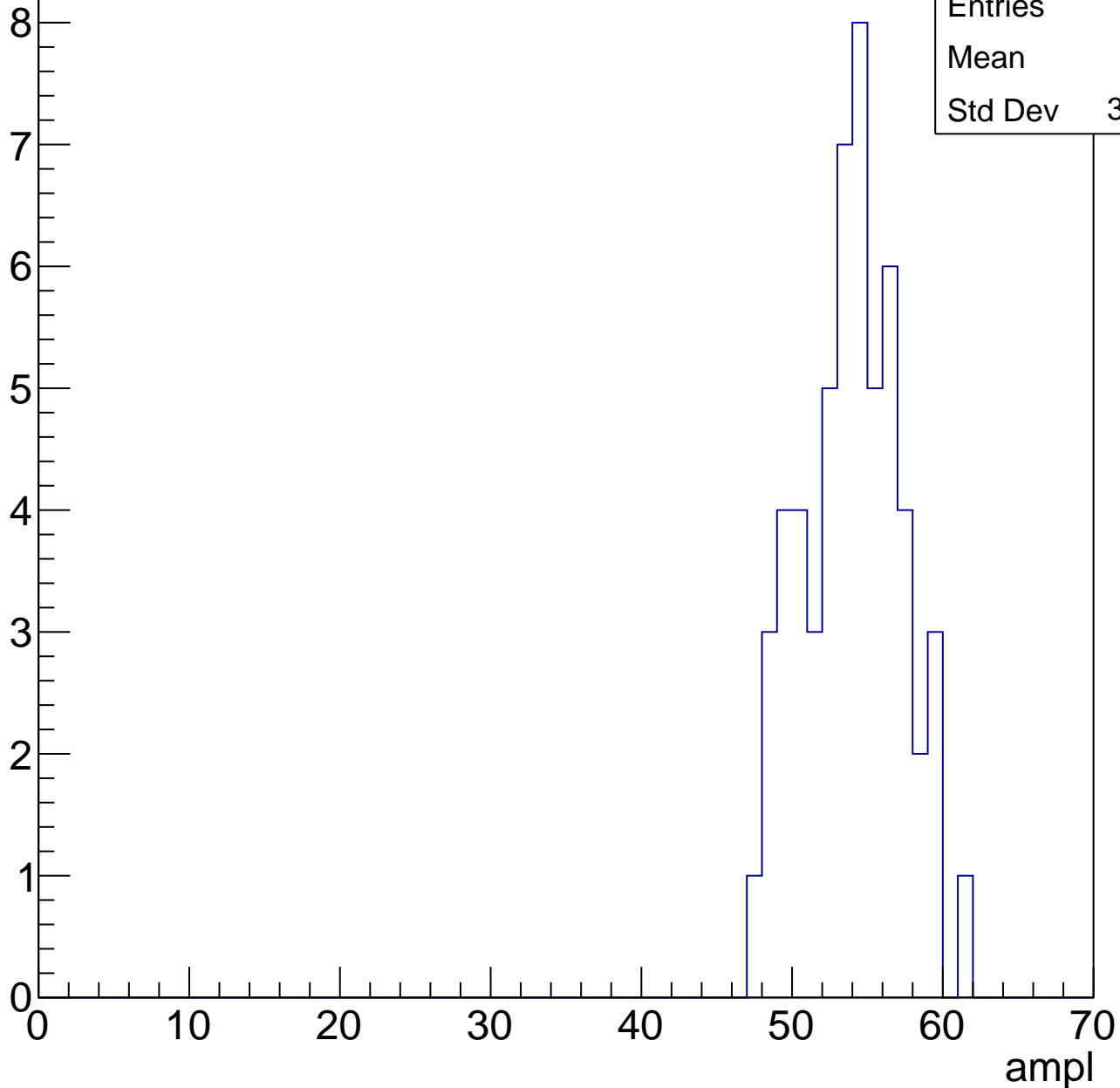


# B0L000S, U19-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

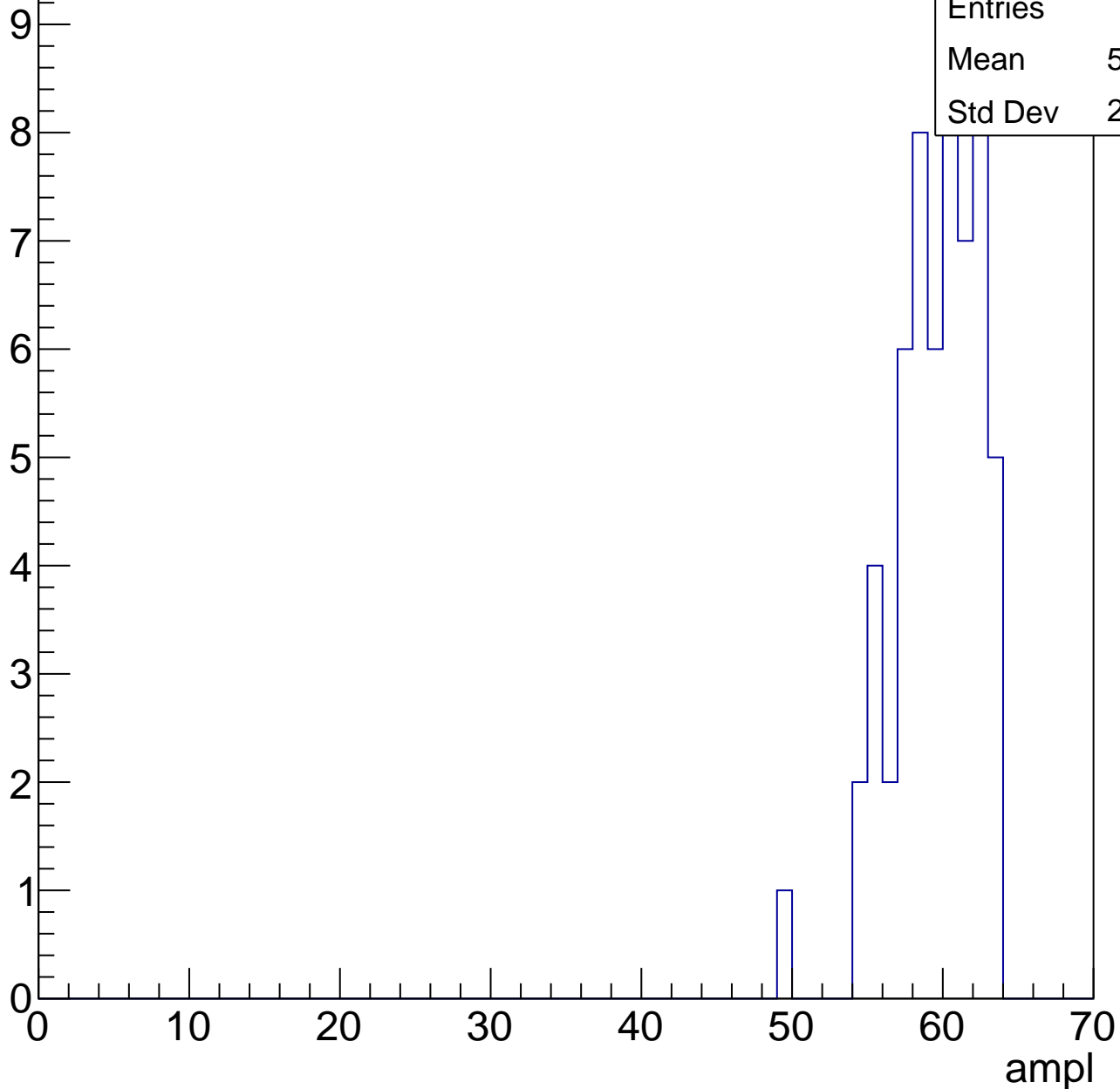
Entries	56
Mean	53.5
Std Dev	3.224



# B0L000S, U19-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

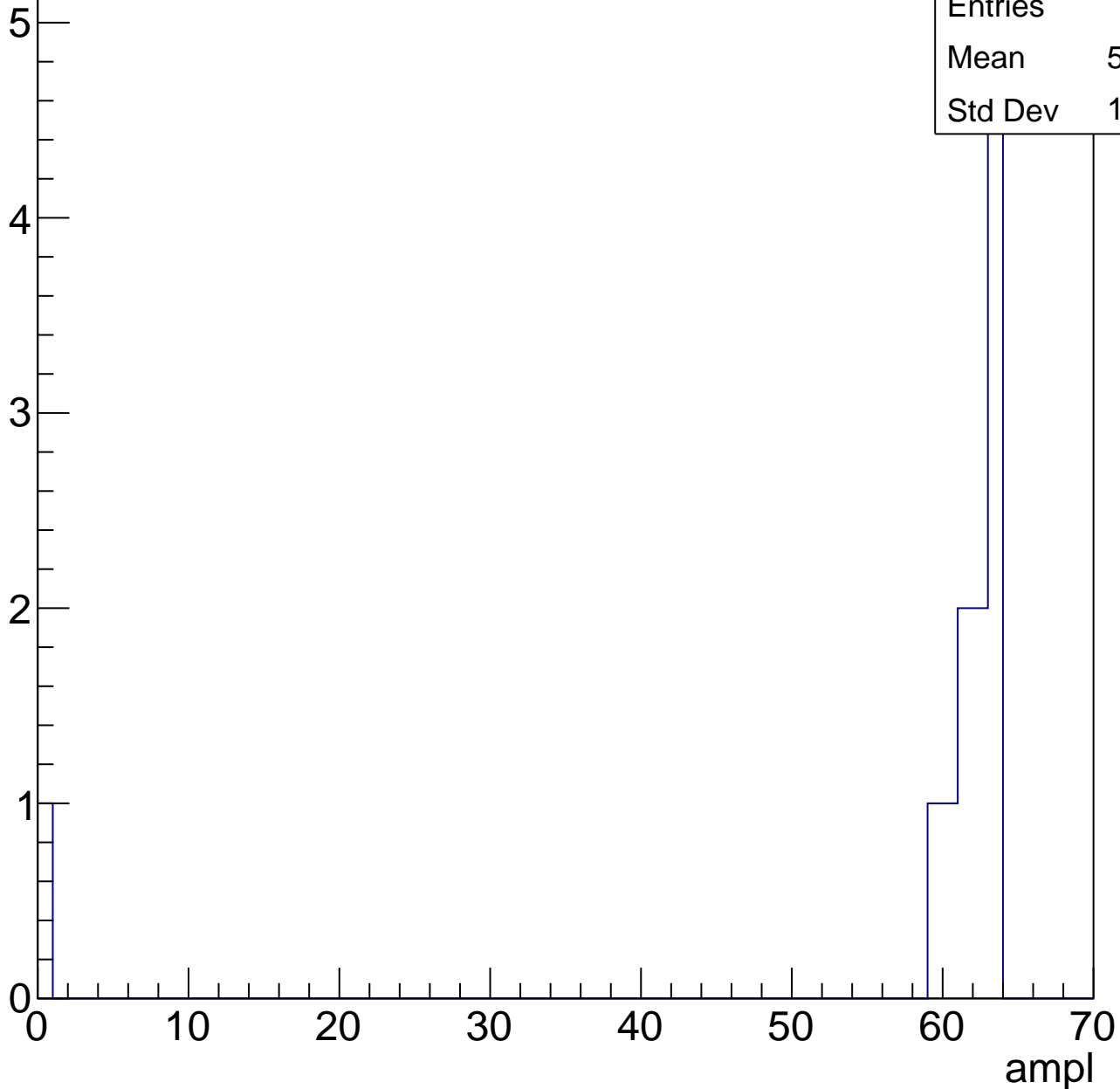


# B0L000S, U19-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	56.67
Std Dev	17.13





# B0L000S, U19-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch65, adc0

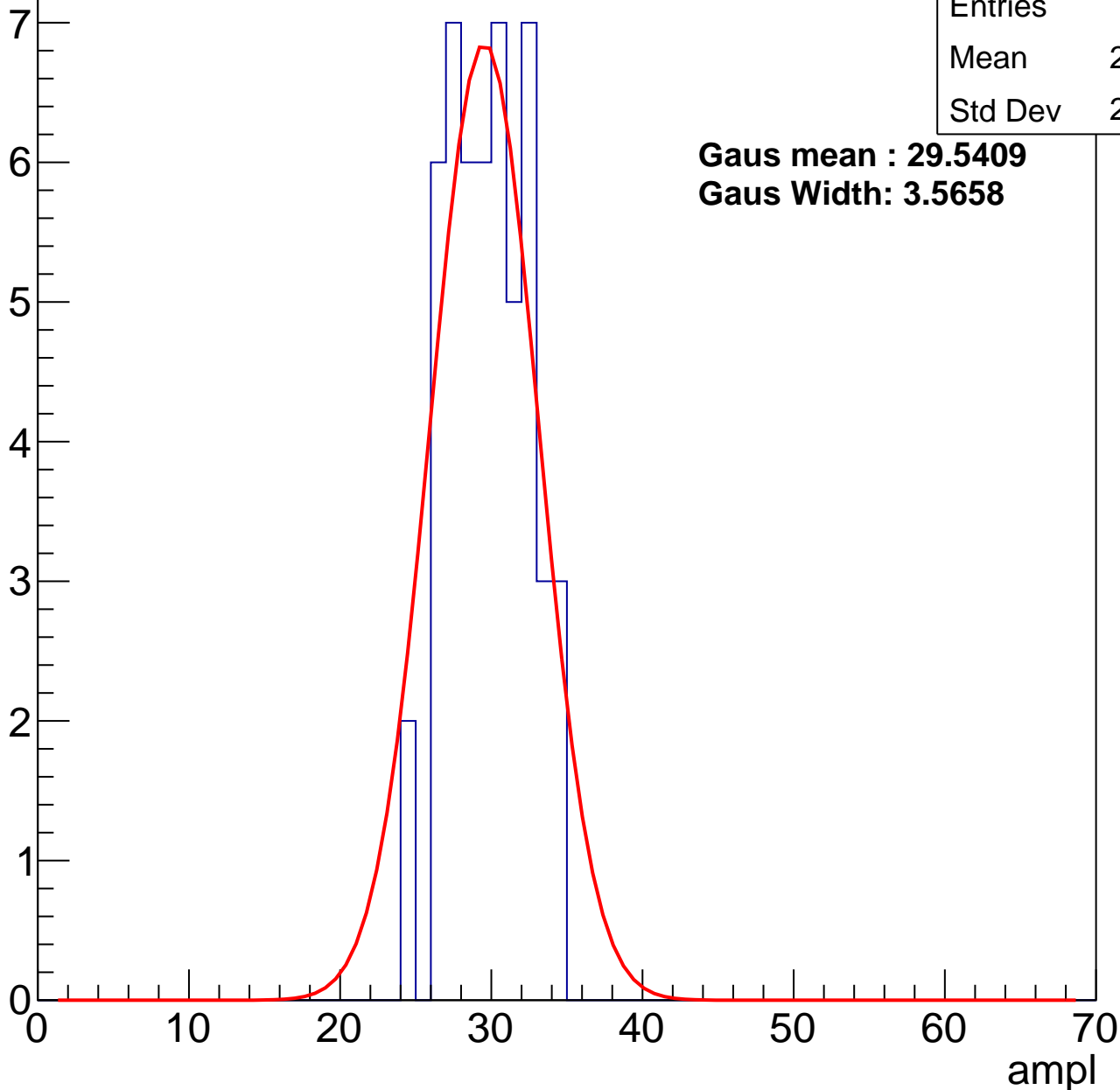
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	29.33
Std Dev	2.577

**Gaus mean : 29.5409**

**Gaus Width: 3.5658**



# B0L000S, U19-ch65, adc1

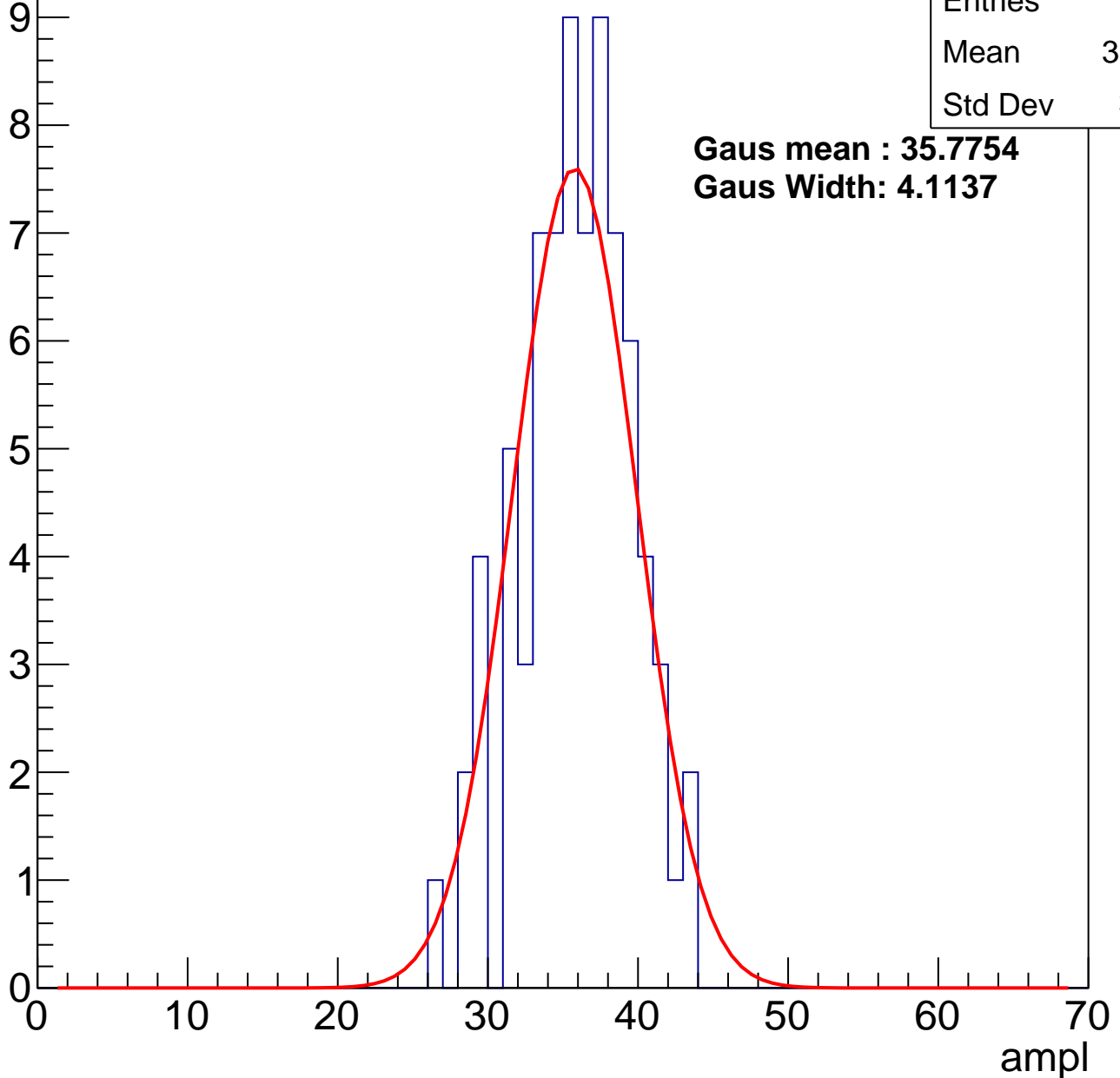
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	35.44
Std Dev	3.67

**Gaus mean : 35.7754**

**Gaus Width: 4.1137**

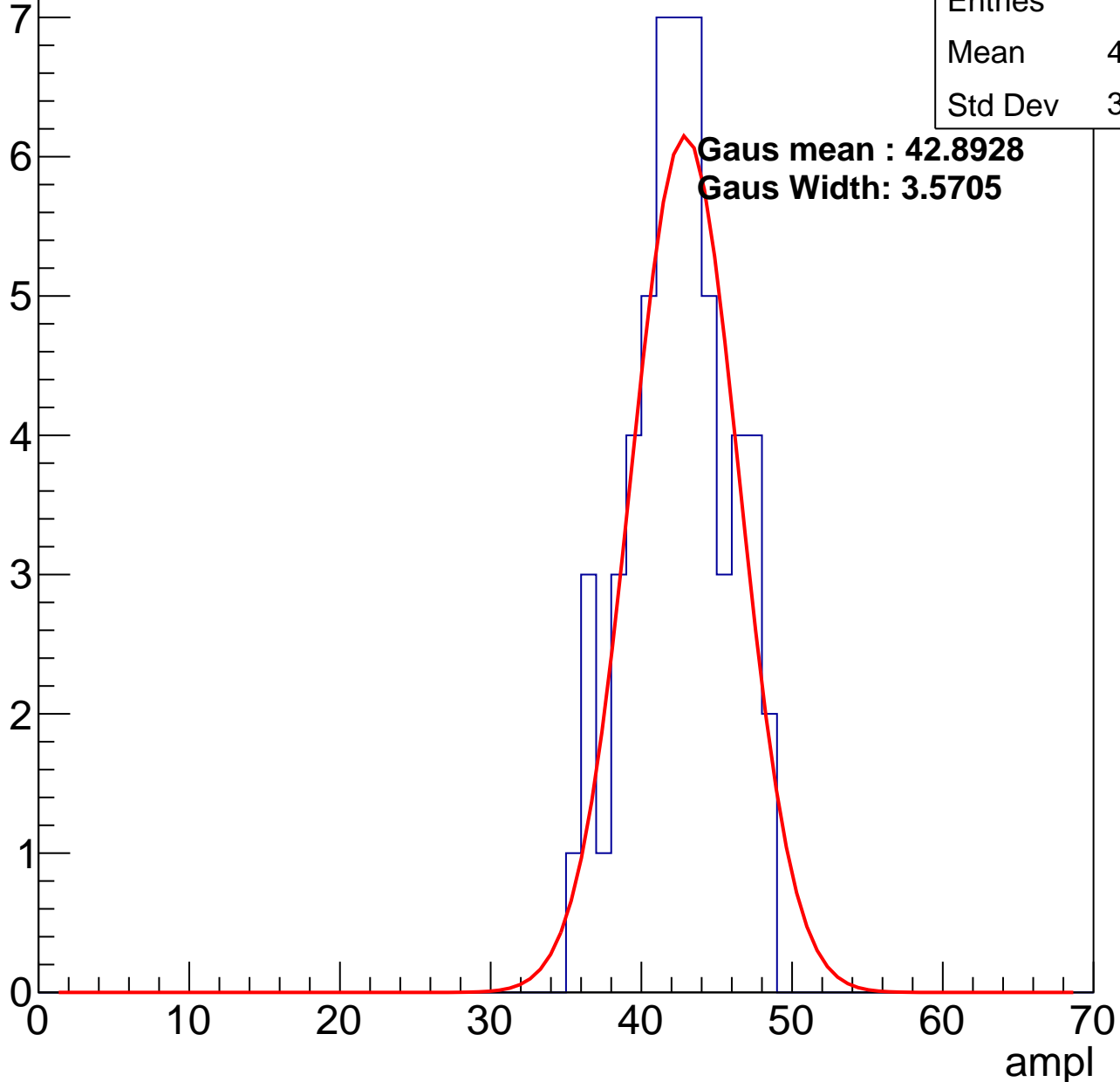


# B0L000S, U19-ch65, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.05
Std Dev	3.226

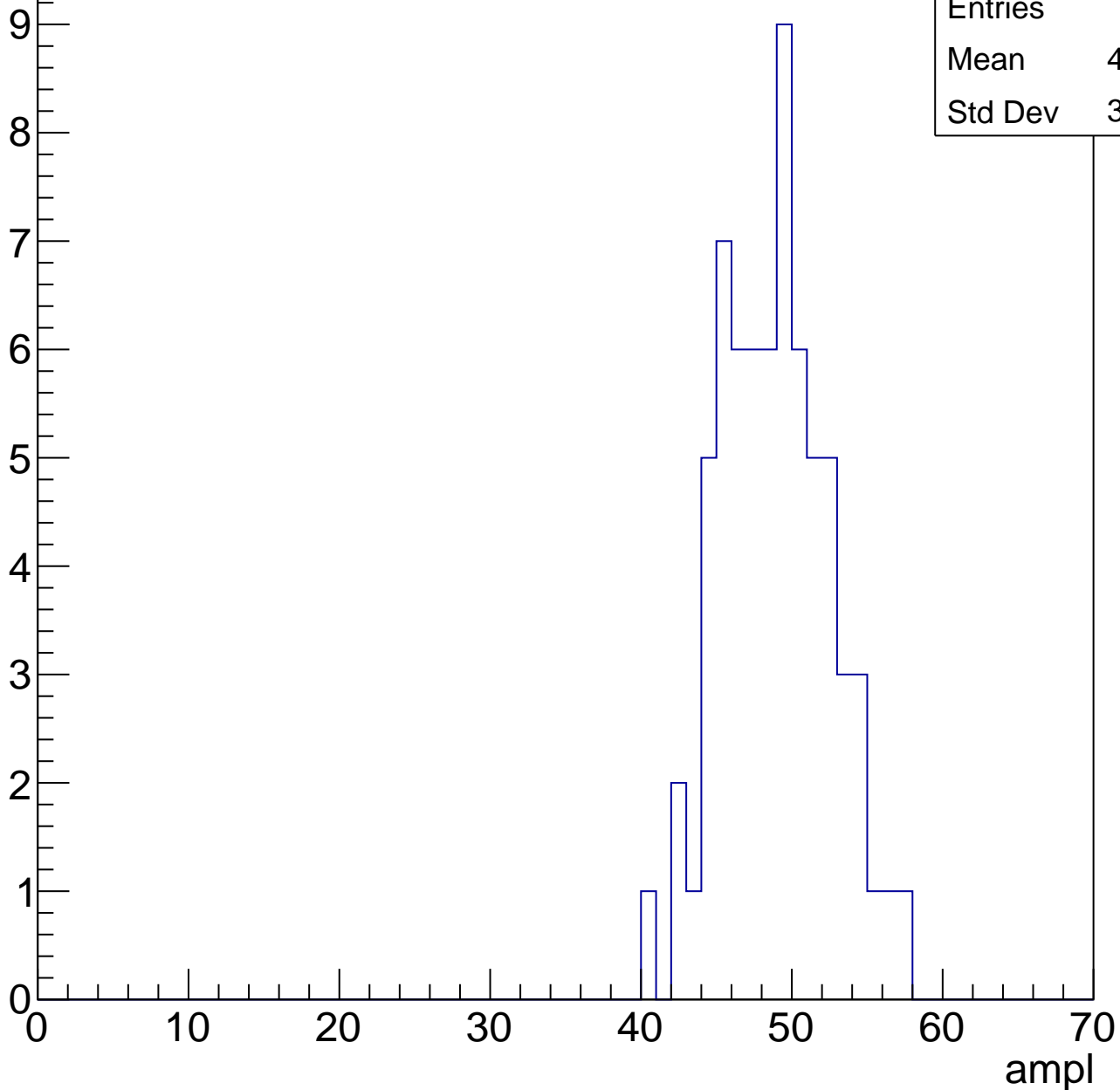


# B0L000S, U19-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

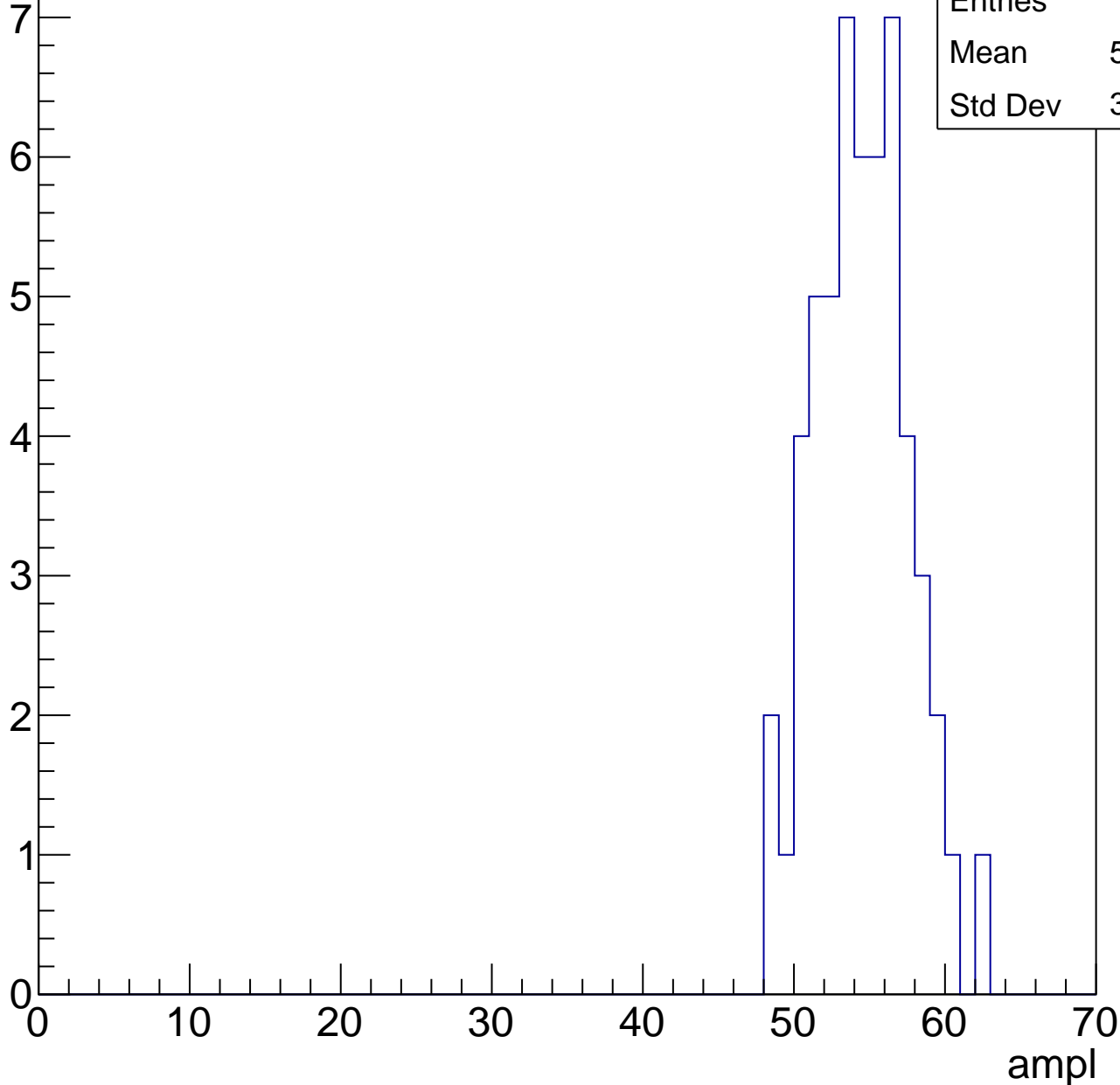
Entries	68
Mean	48.43
Std Dev	3.549



# B0L000S, U19-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

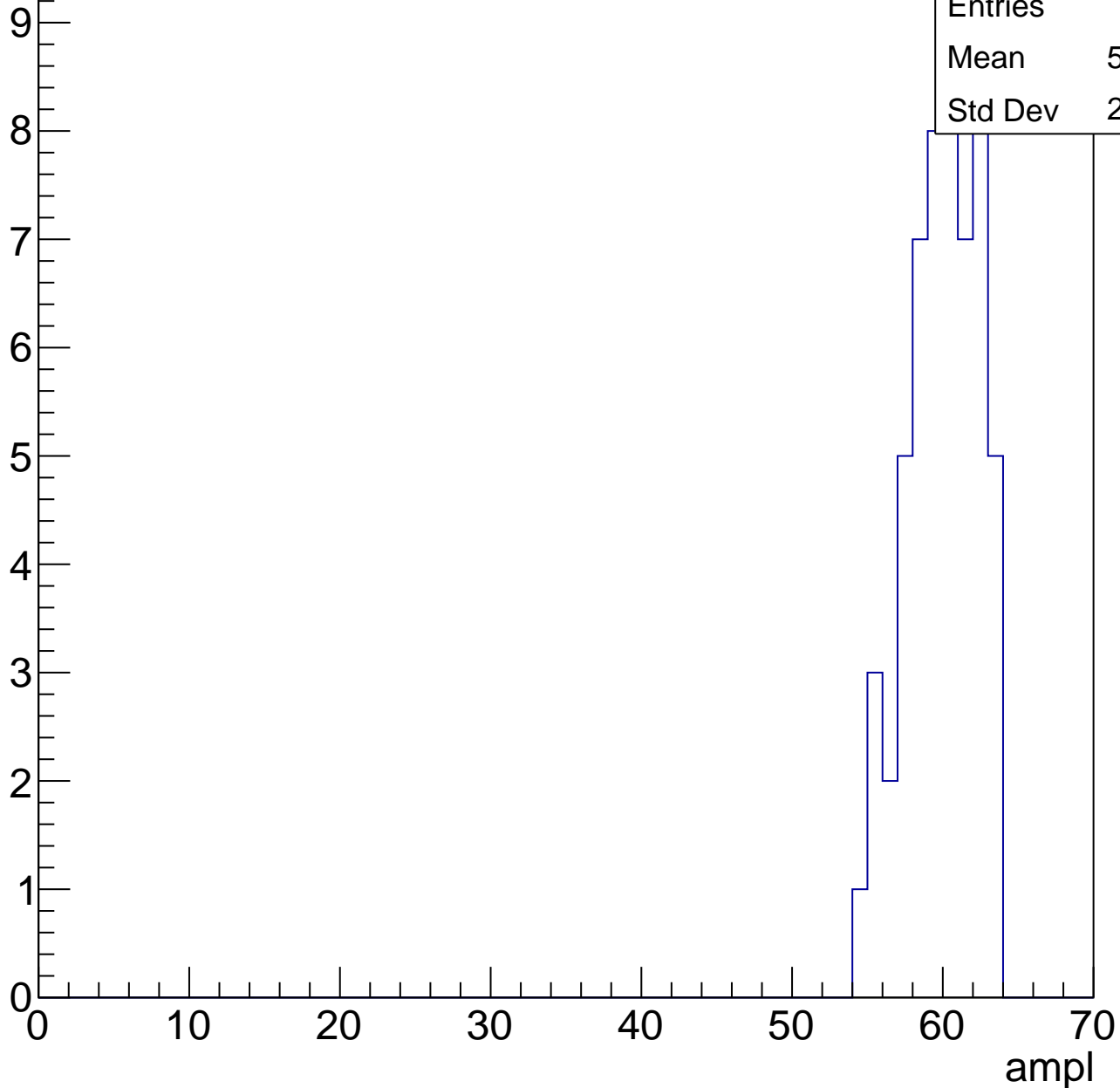


Entries	54
Mean	54.06
Std Dev	3.045

# B0L000S, U19-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

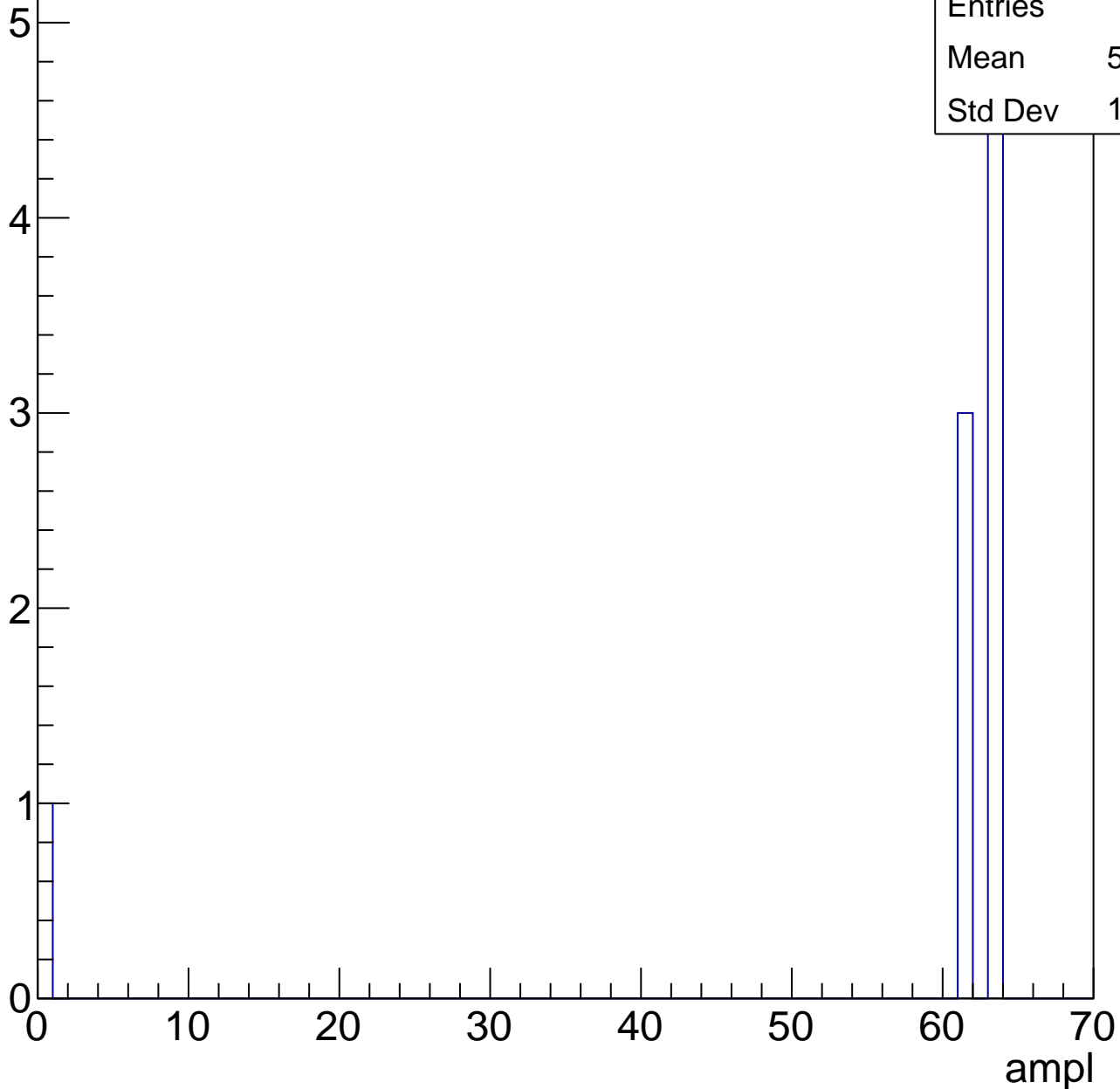


# B0L000S, U19-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	55.33
Std Dev	19.58

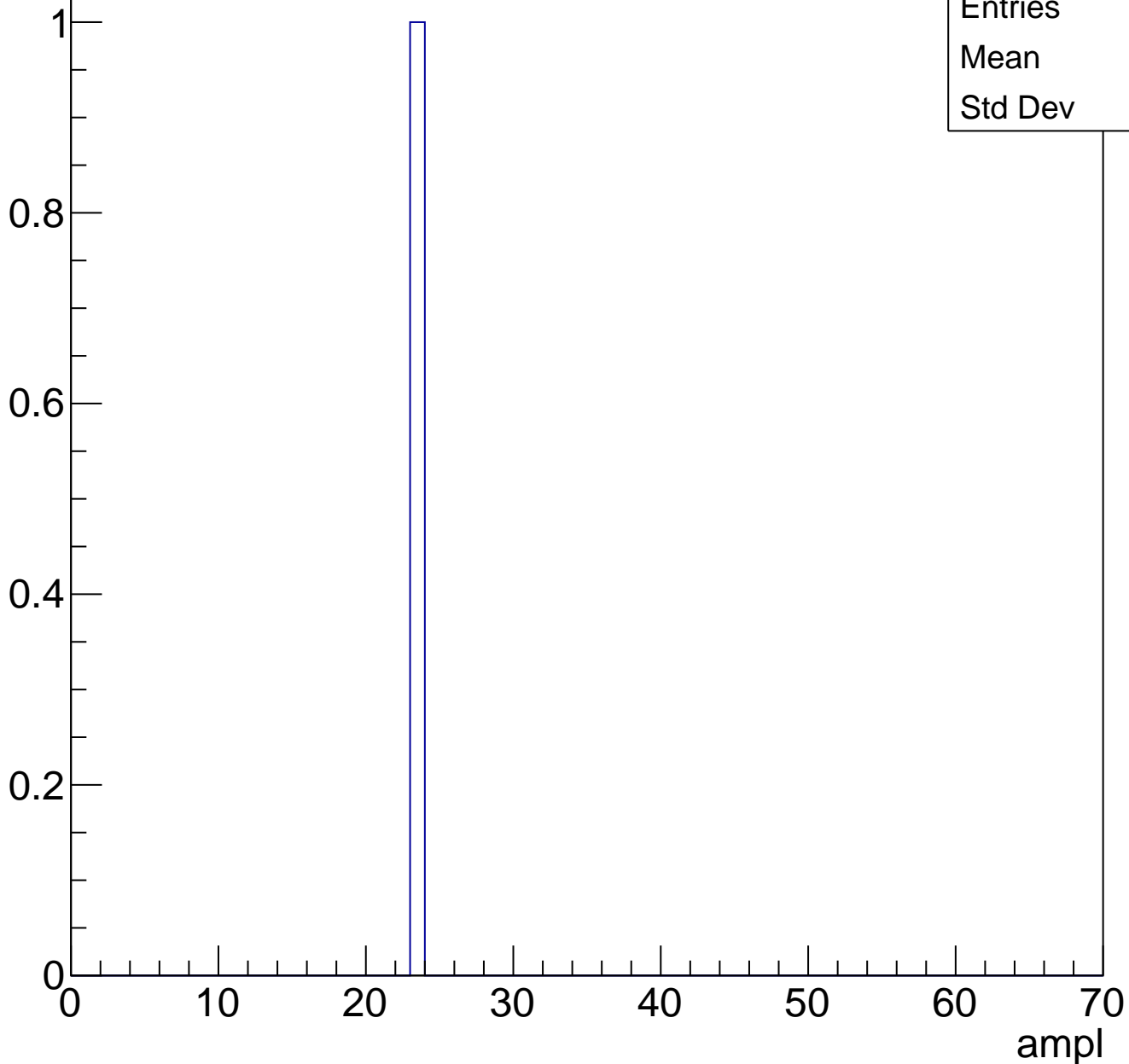




# B0L000S, U19-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch66, adc0

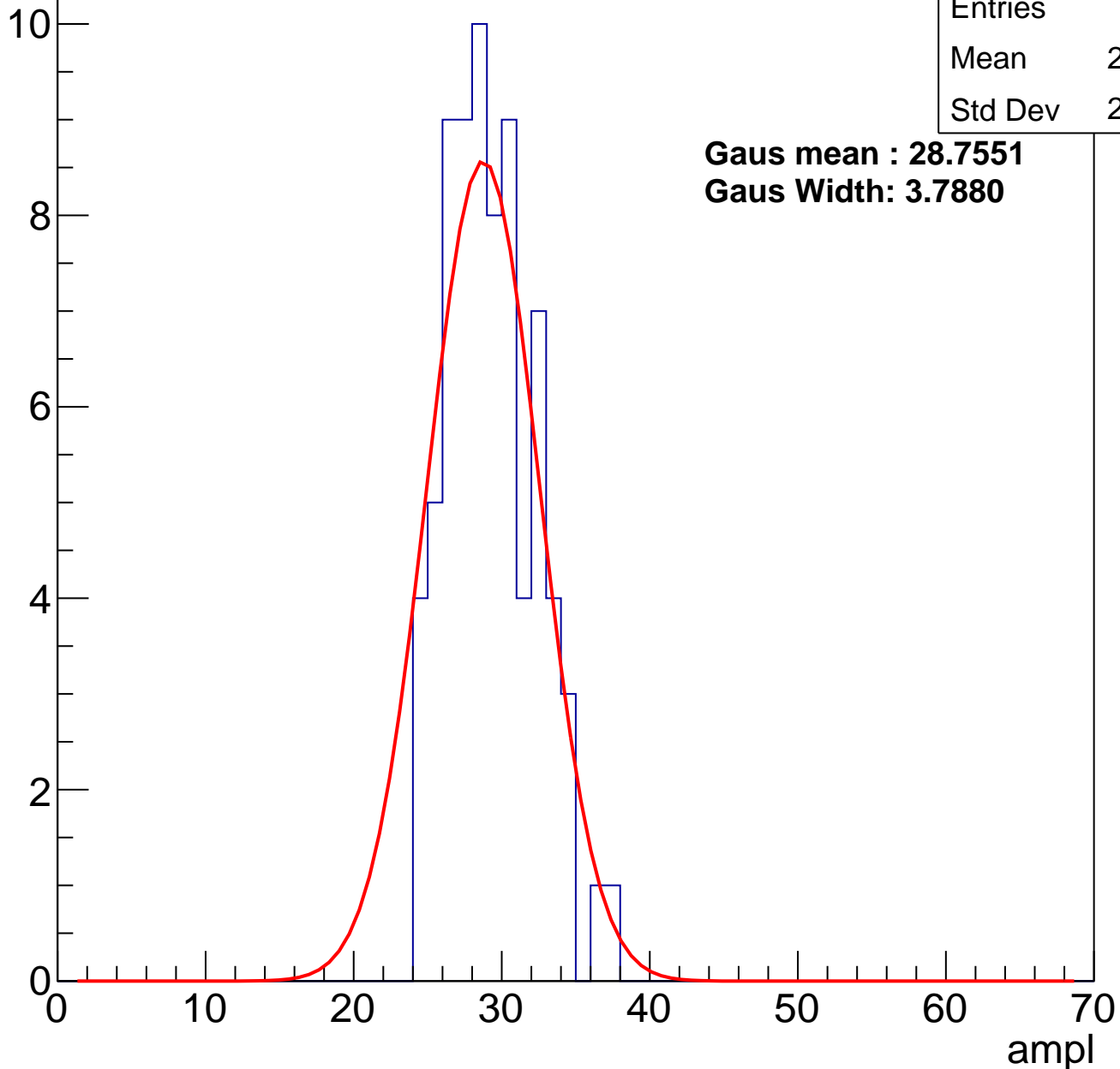
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	28.85
Std Dev	2.953

**Gaus mean : 28.7551**

**Gaus Width: 3.7880**

Entry



# B0L000S, U19-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	35.24
Std Dev	3.017

**Gaus mean : 35.9373**

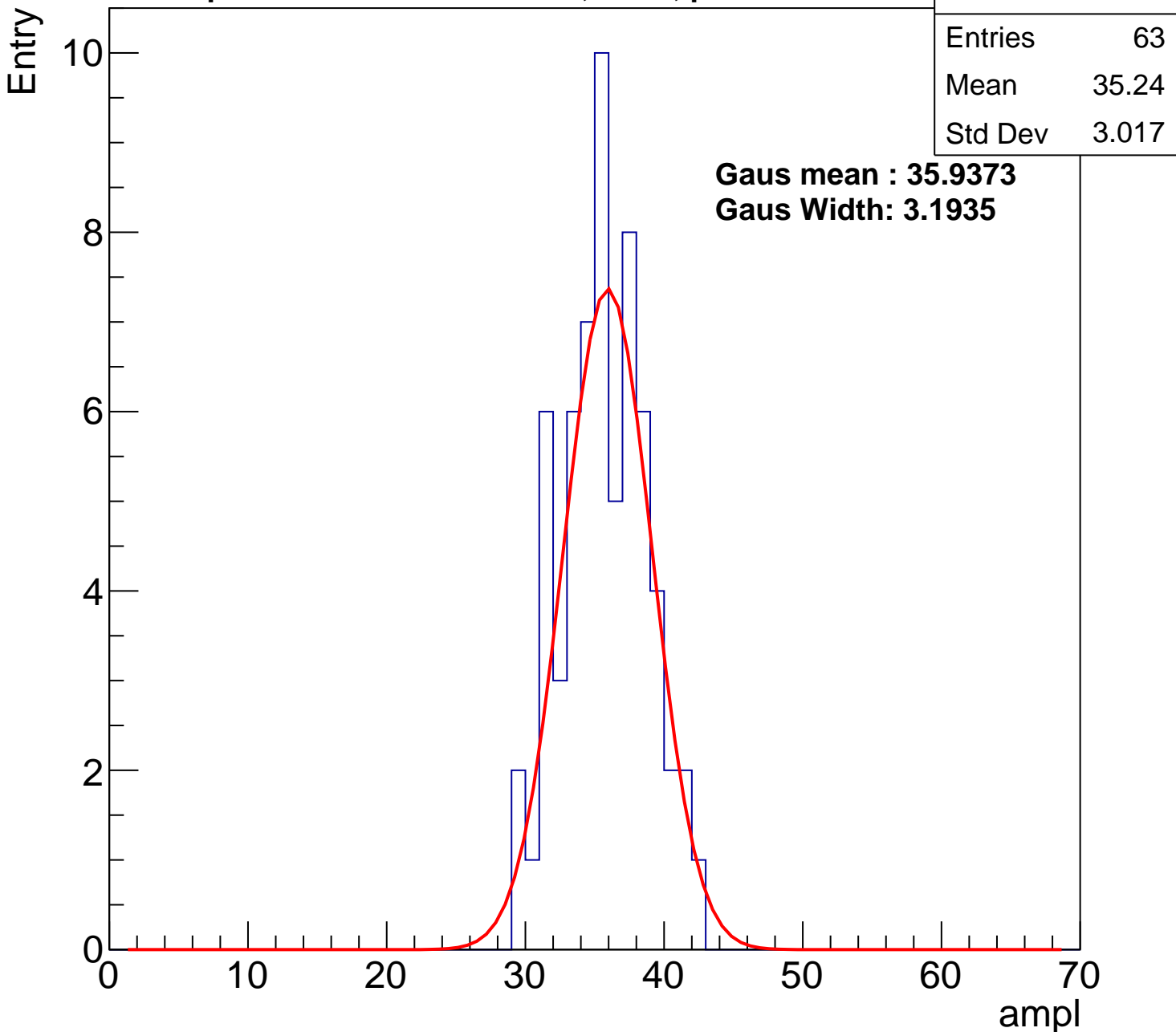
**Gaus Width: 3.1935**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch66, adc2

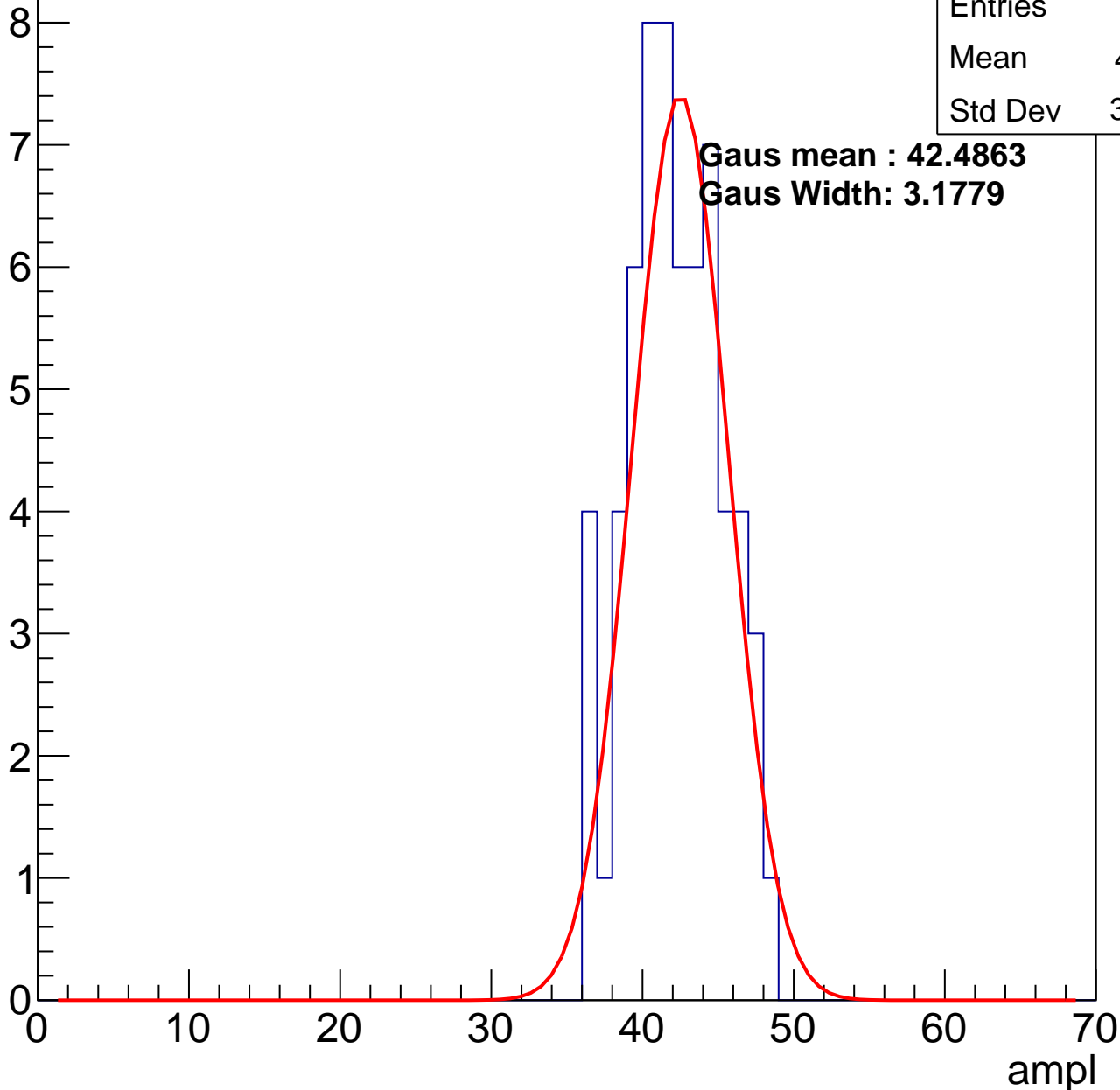
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	41.71
Std Dev	3.023

**Gaus mean : 42.4863**

**Gaus Width: 3.1779**

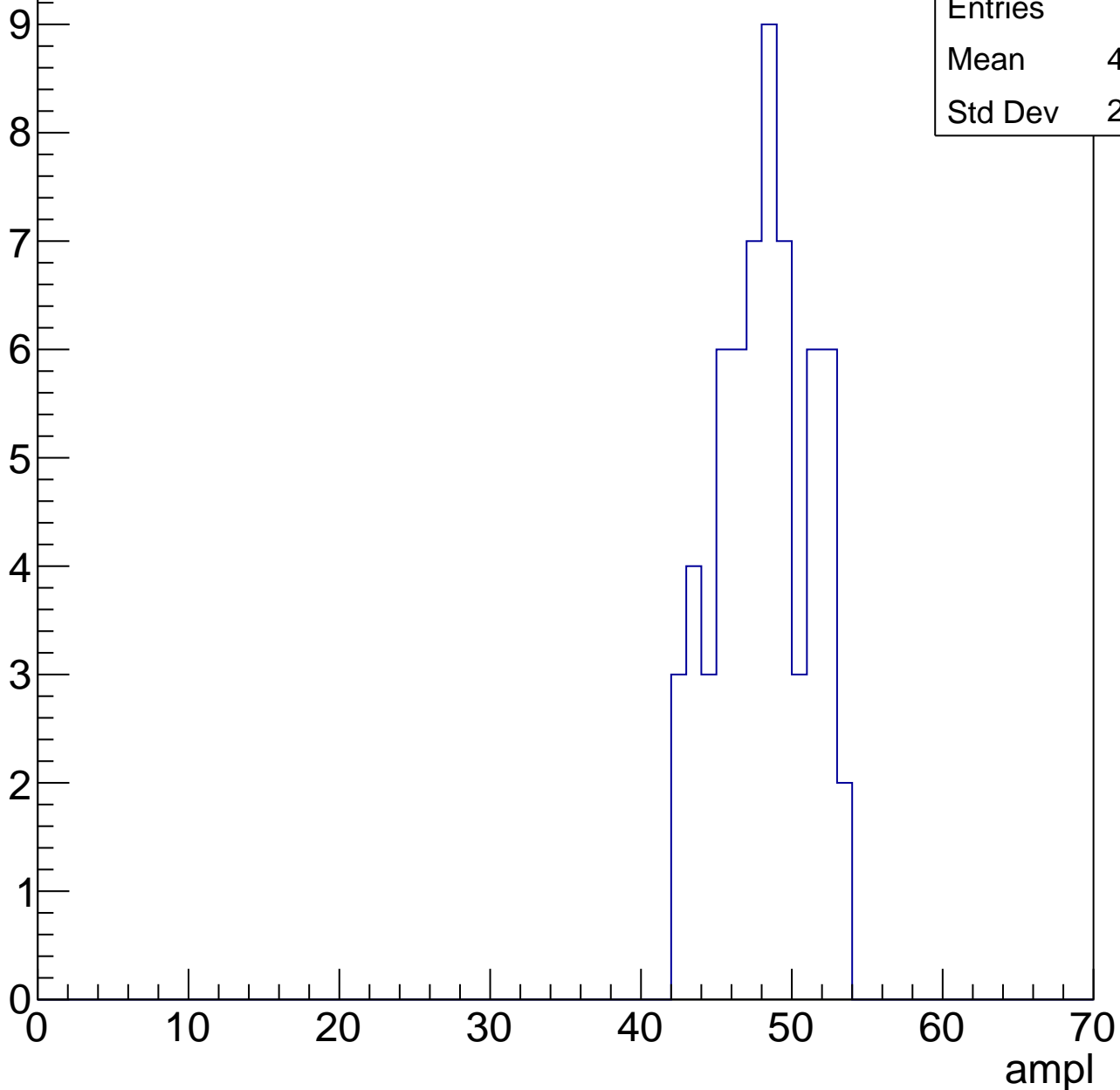


# B0L000S, U19-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	47.65
Std Dev	2.984

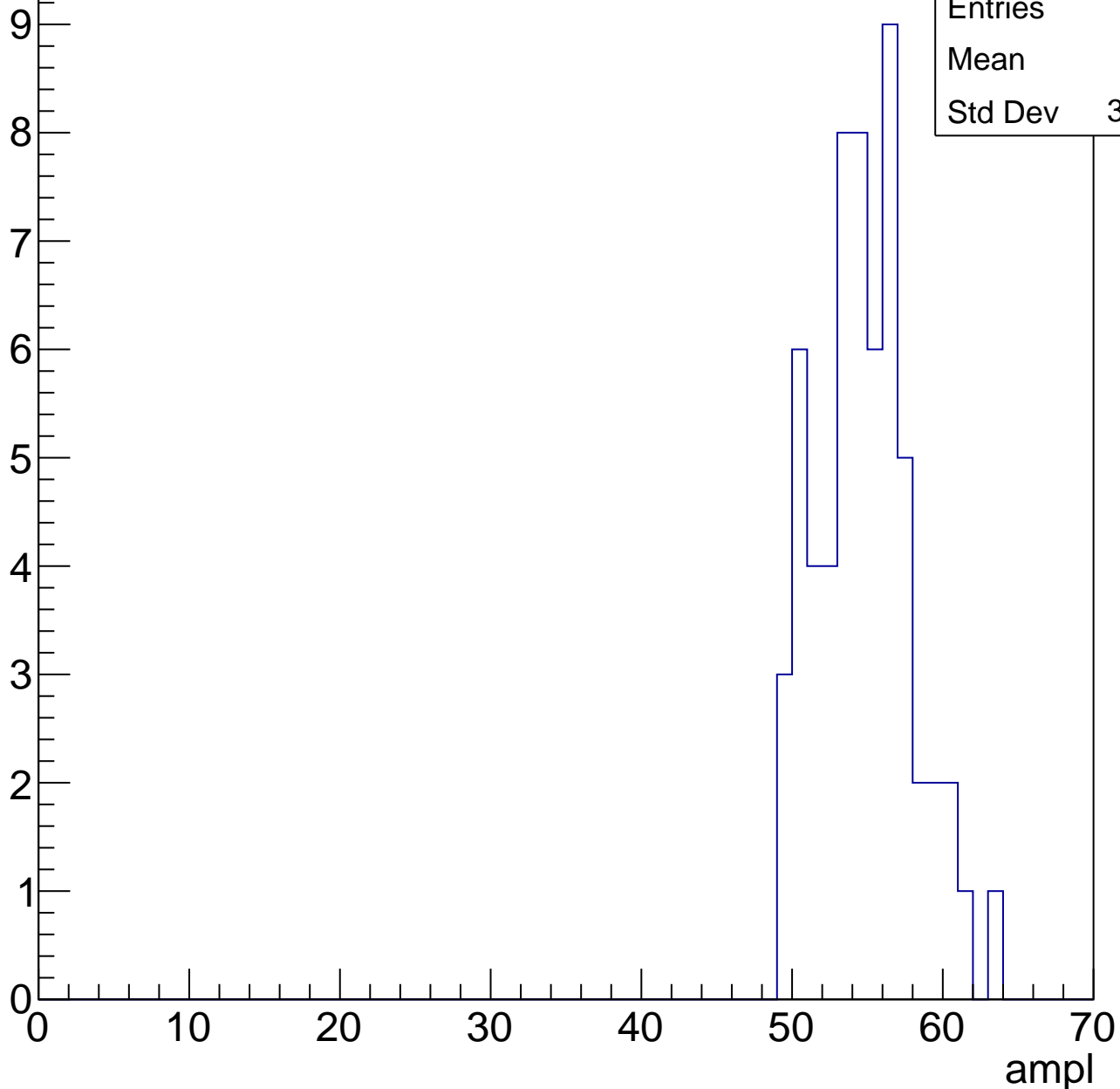


# B0L000S, U19-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	54.3
Std Dev	3.128

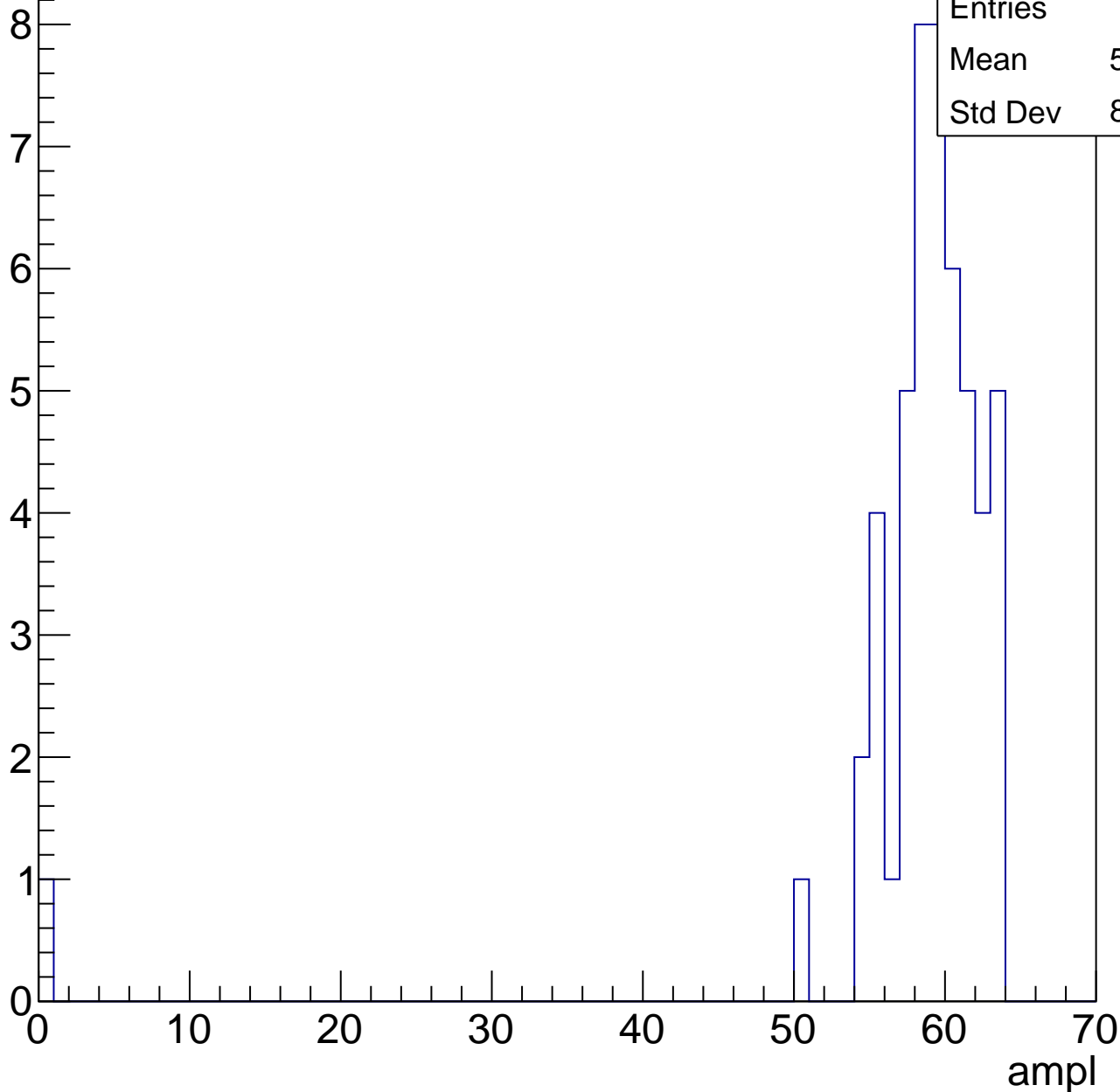


# B0L000S, U19-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	57.66
Std Dev	8.678

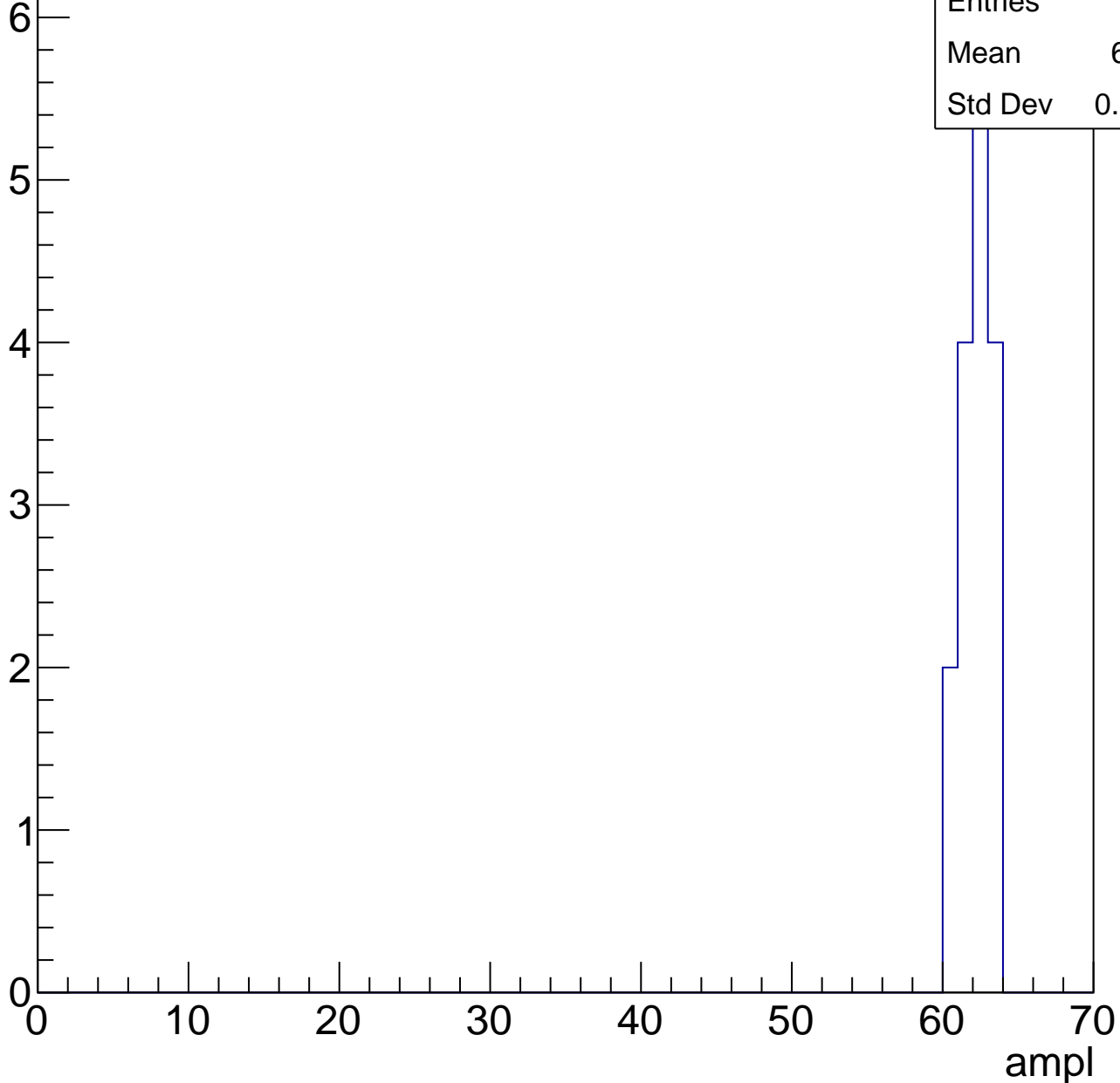


# B0L000S, U19-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.75
Std Dev	0.9682

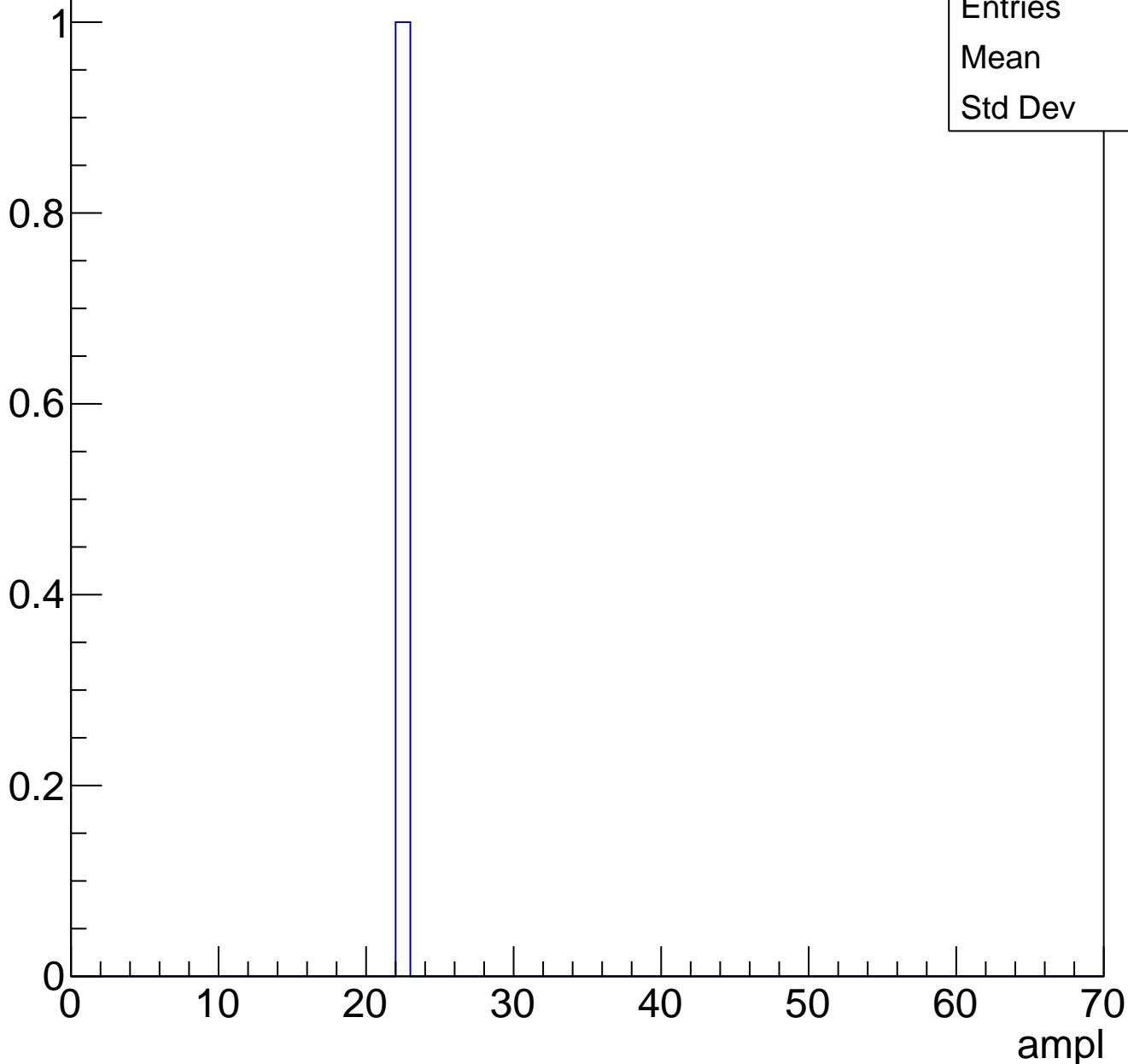




# B0L000S, U19-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch67, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	29.97
Std Dev	4.676

**Gaus mean : 30.8248**

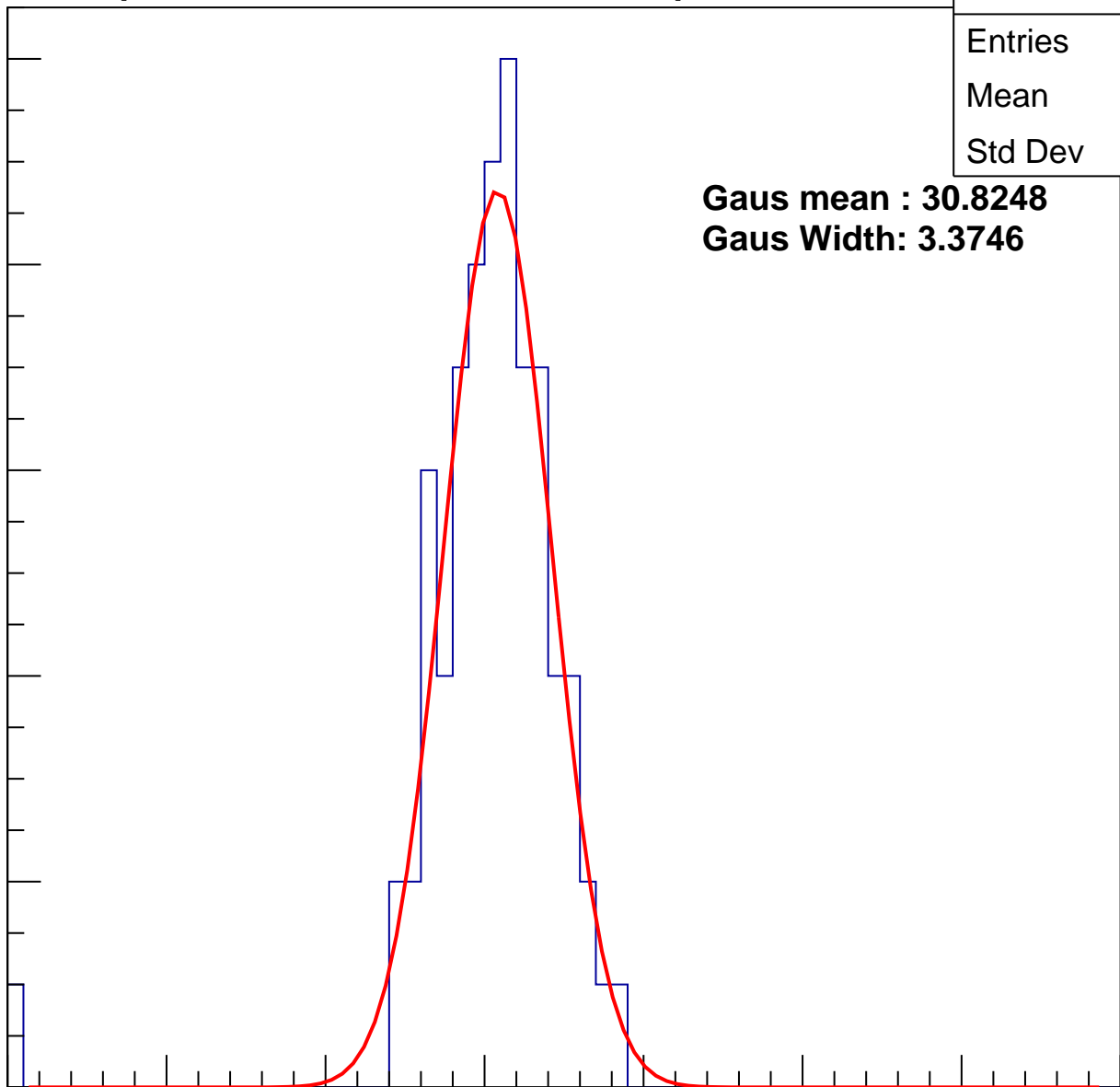
**Gaus Width: 3.3746**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch67, adc1

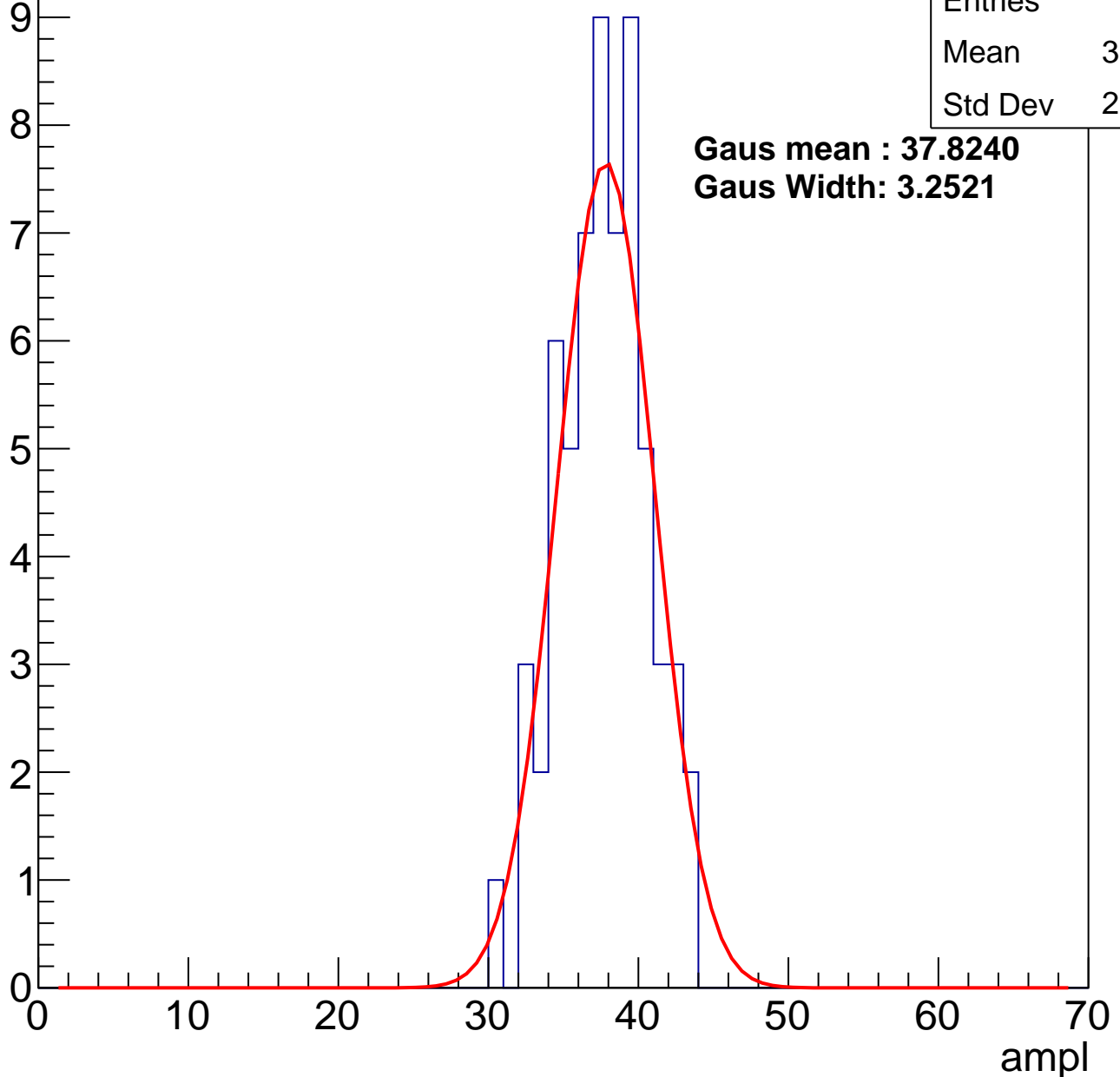
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.23
Std Dev	2.887

**Gaus mean : 37.8240**

**Gaus Width: 3.2521**



# B0L000S, U19-ch67, adc2

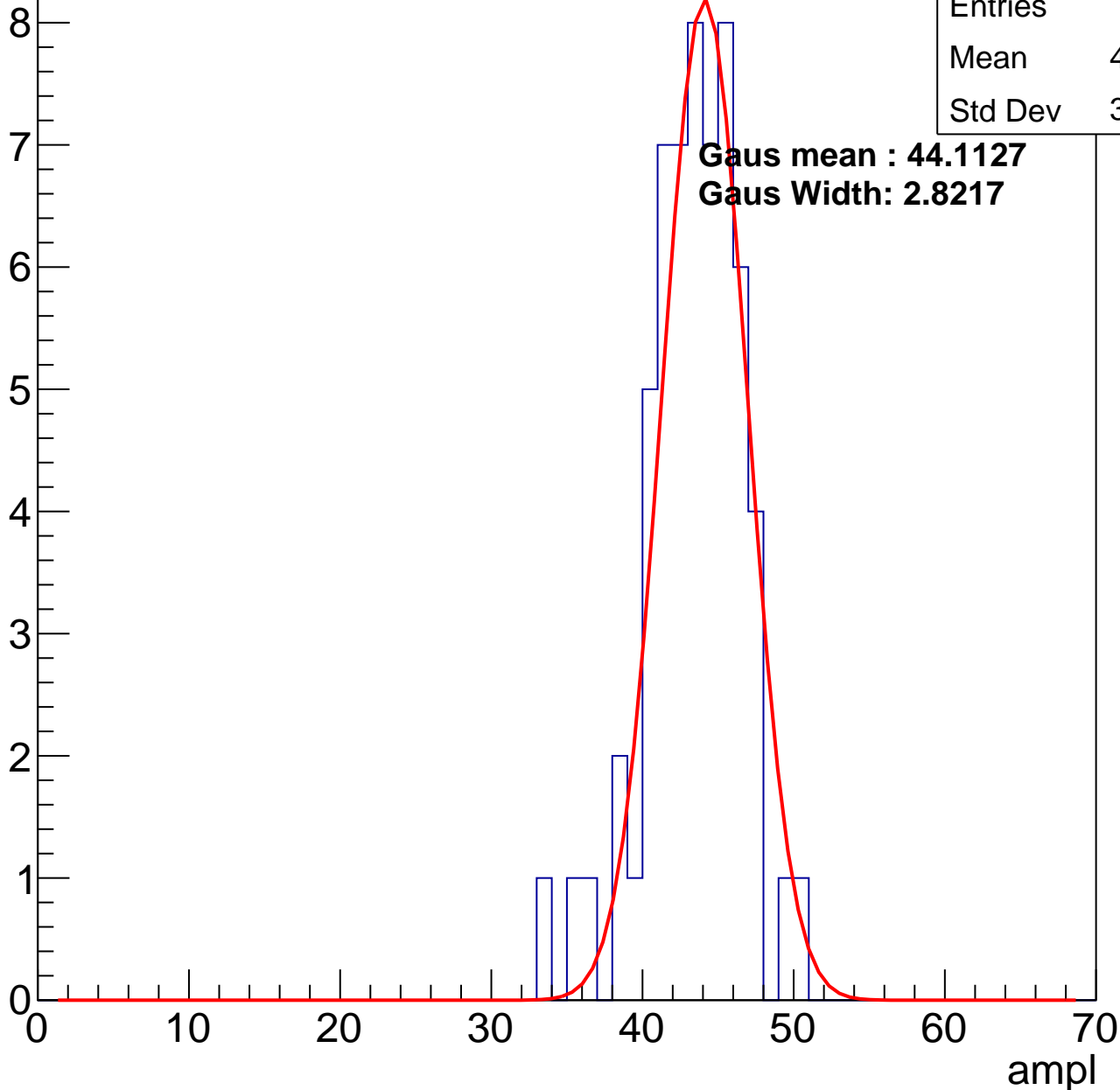
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.92
Std Dev	3.164

**Gaus mean : 44.1127**

**Gaus Width: 2.8217**



# B0L000S, U19-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	49.85
Std Dev	2.752

Entry

10

8

6

4

2

0

0

10

20

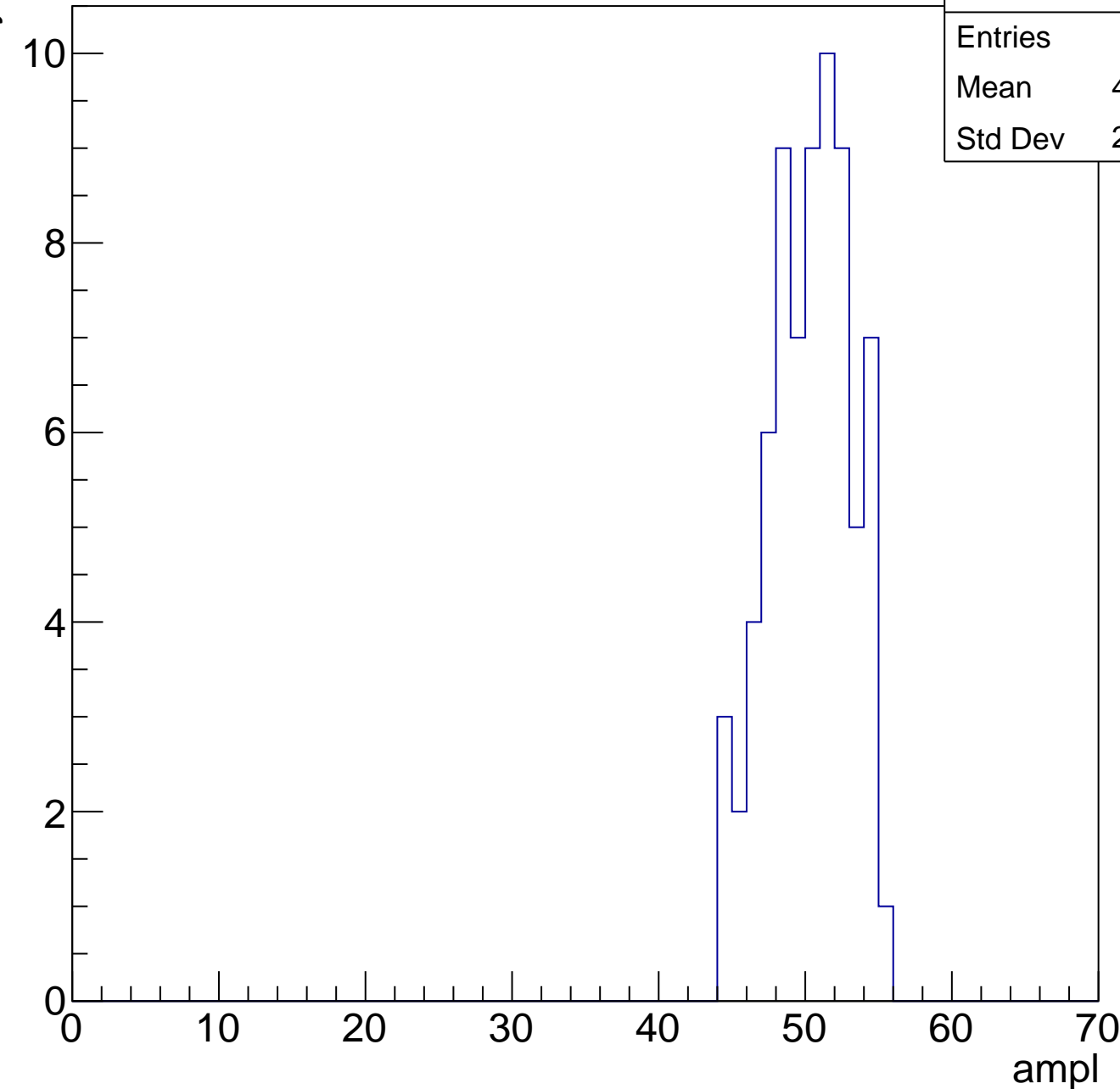
30

40

50

60

ampl

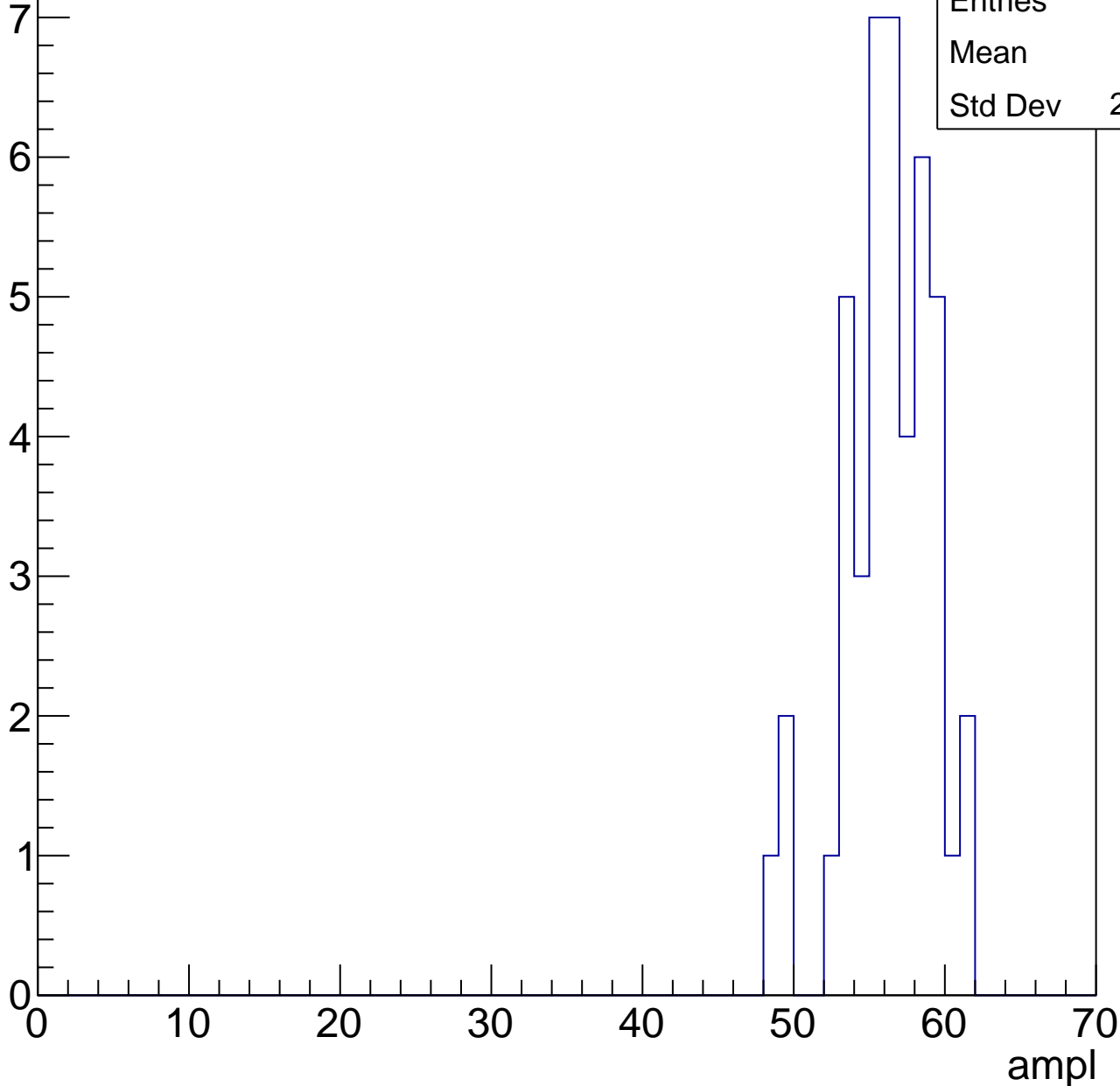


# B0L000S, U19-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	55.8
Std Dev	2.936

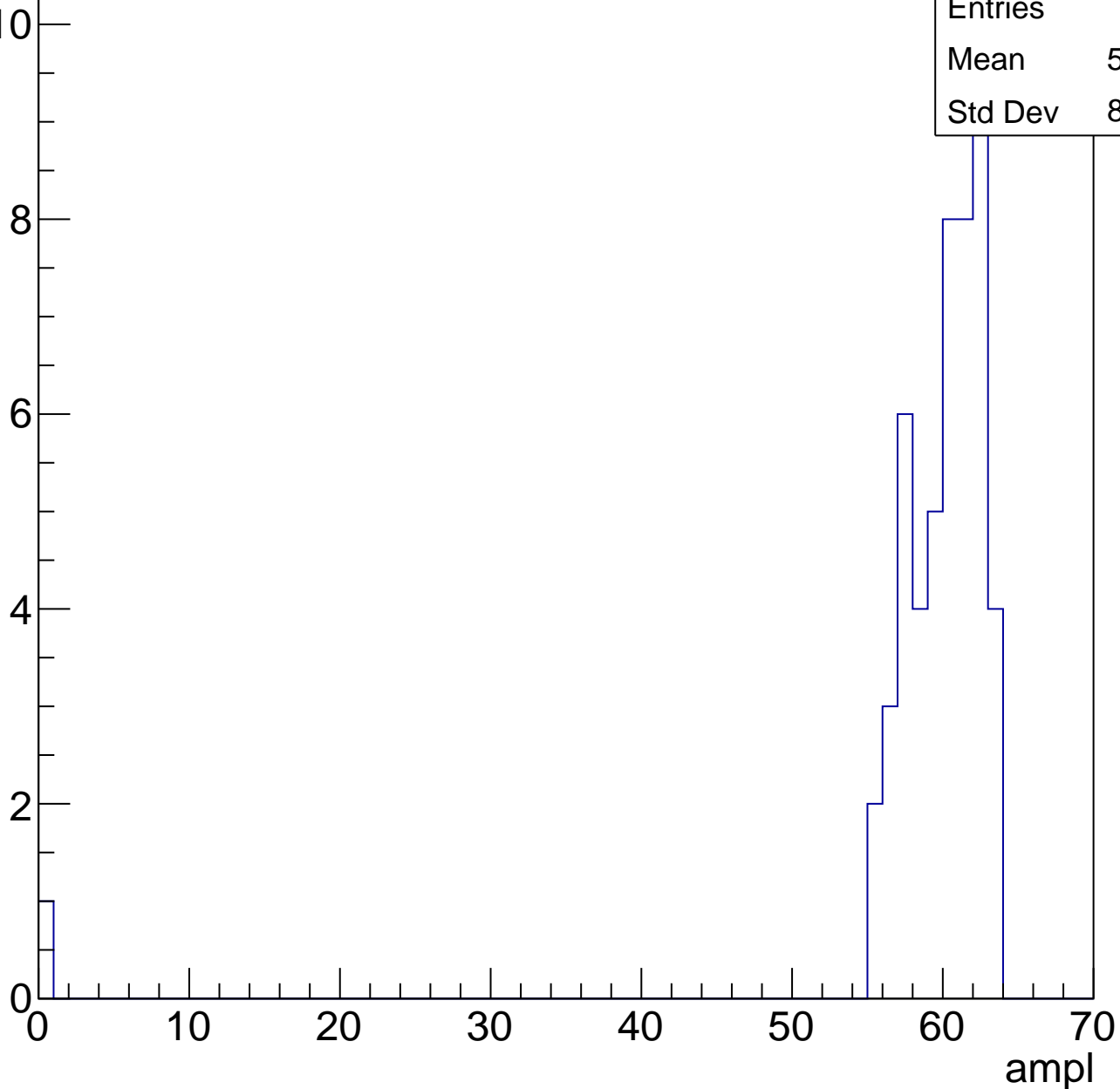


# B0L000S, U19-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	58.57
Std Dev	8.578

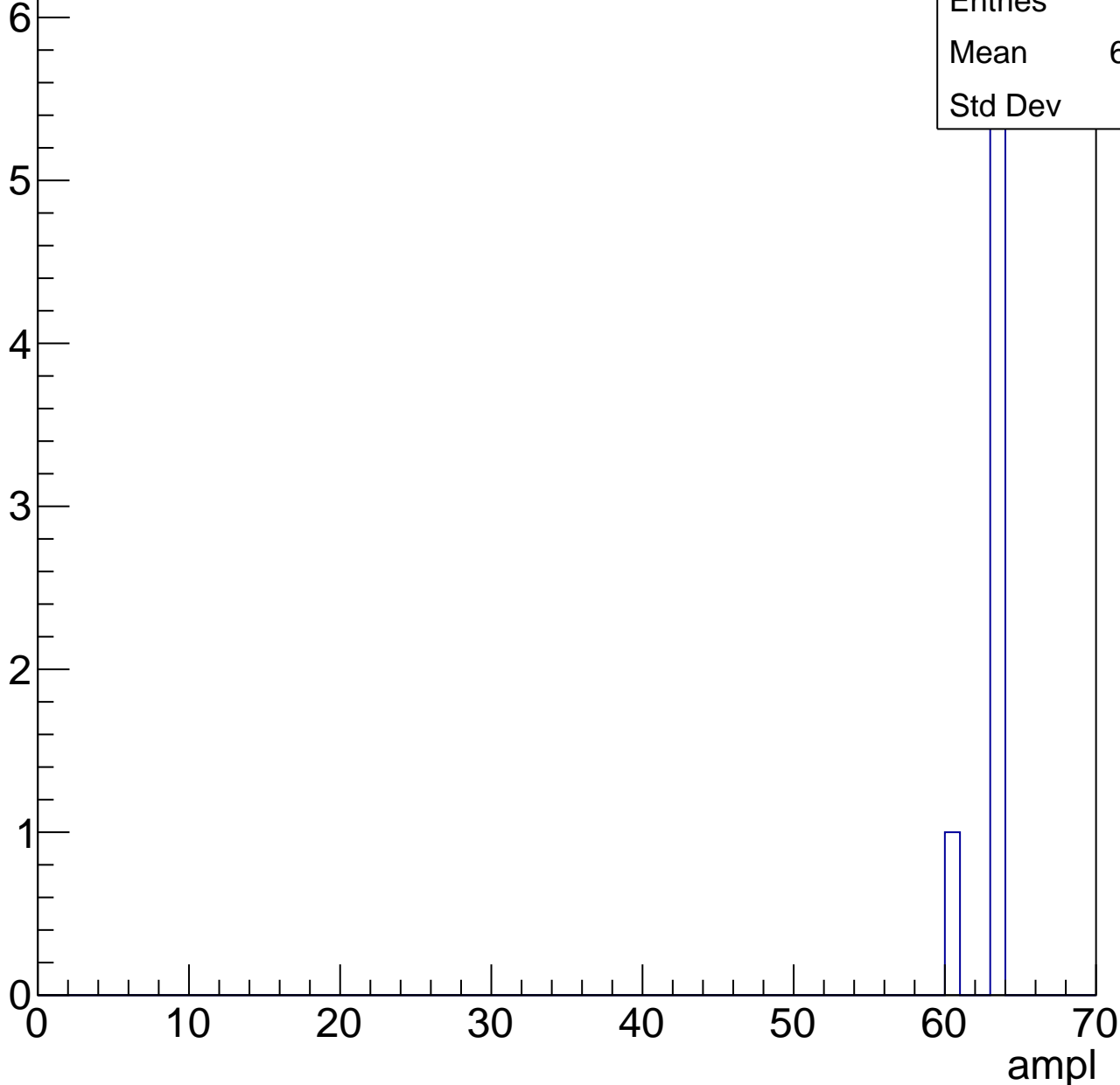


# B0L000S, U19-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	7
Mean	62.57
Std Dev	1.05





# B0L000S, U19-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U19-ch68, adc0

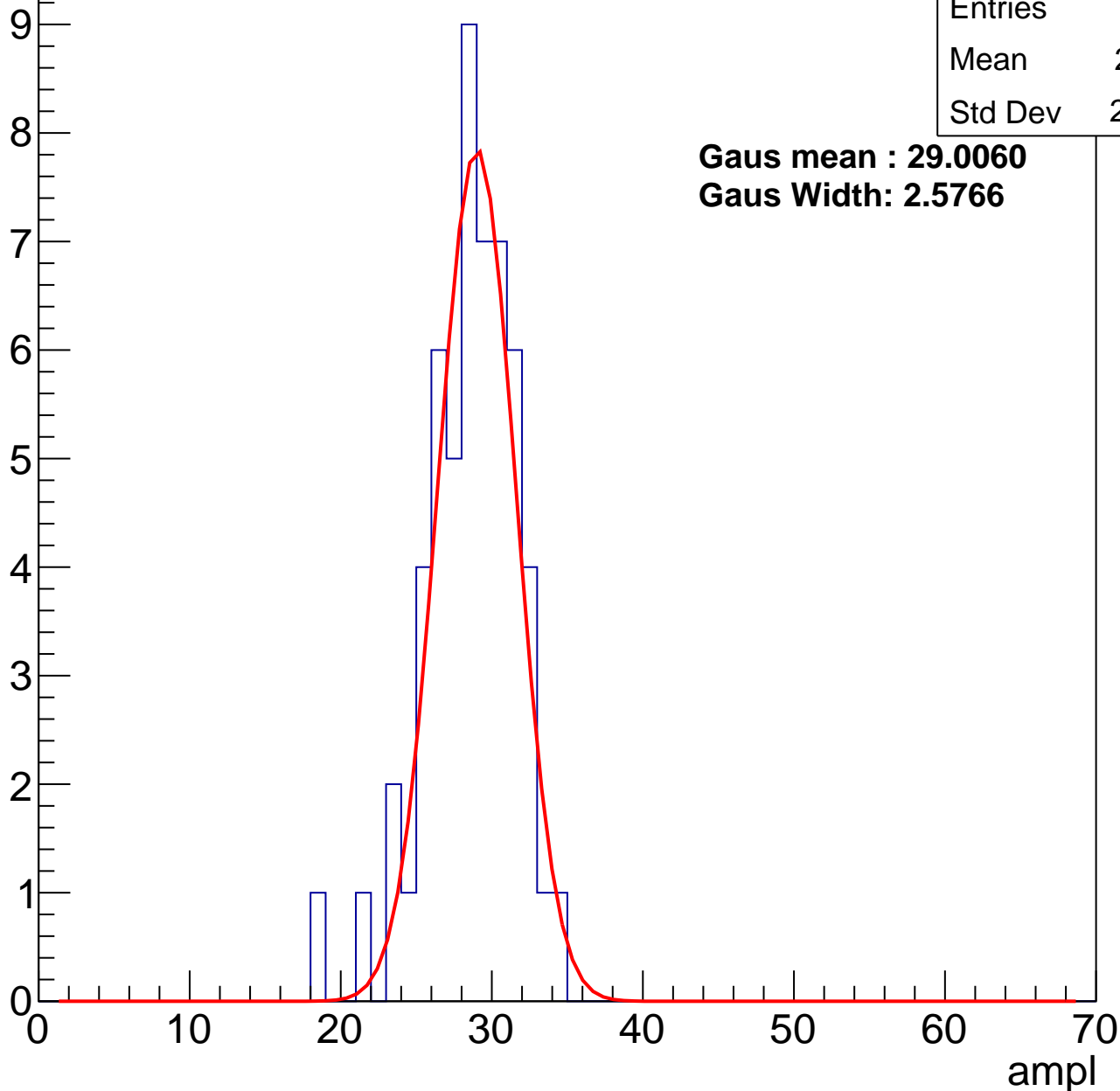
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	28.11
Std Dev	2.995

**Gaus mean : 29.0060**

**Gaus Width: 2.5766**



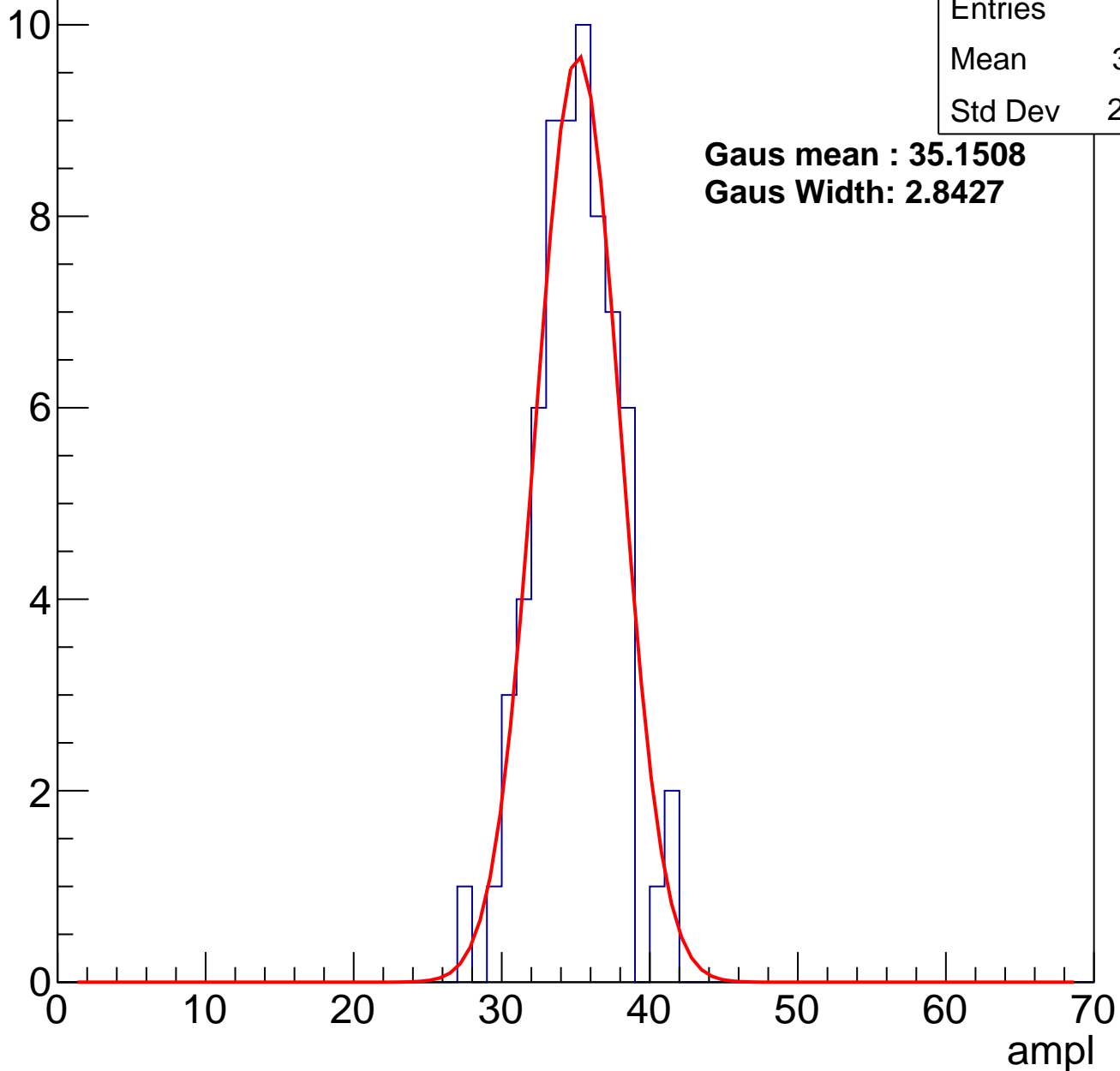
# B0L000S, U19-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	34.51
Std Dev	2.756

**Gaus mean : 35.1508**  
**Gaus Width: 2.8427**

Entry



# B0L000S, U19-ch68, adc2

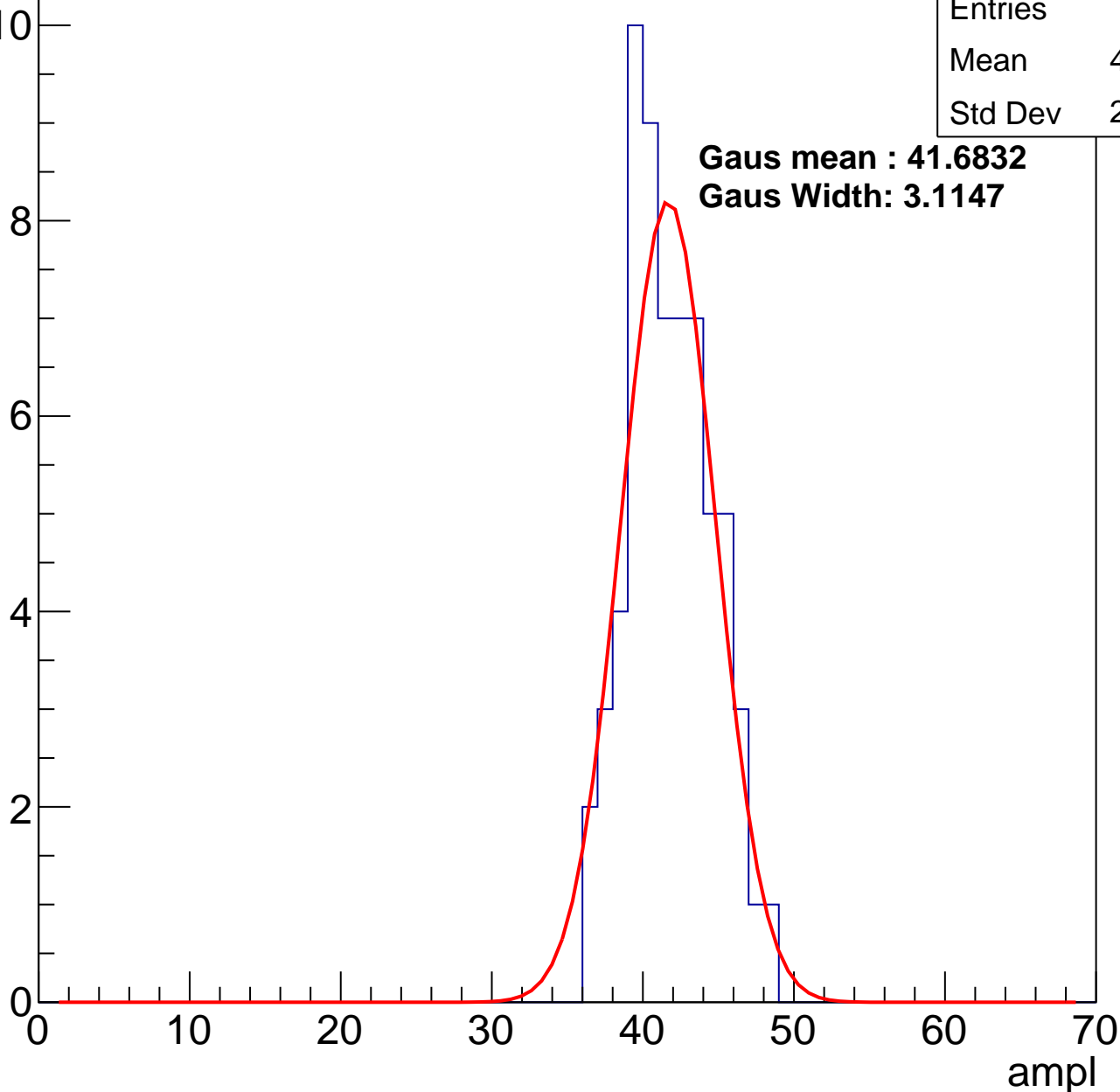
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.33
Std Dev	2.784

**Gaus mean : 41.6832**

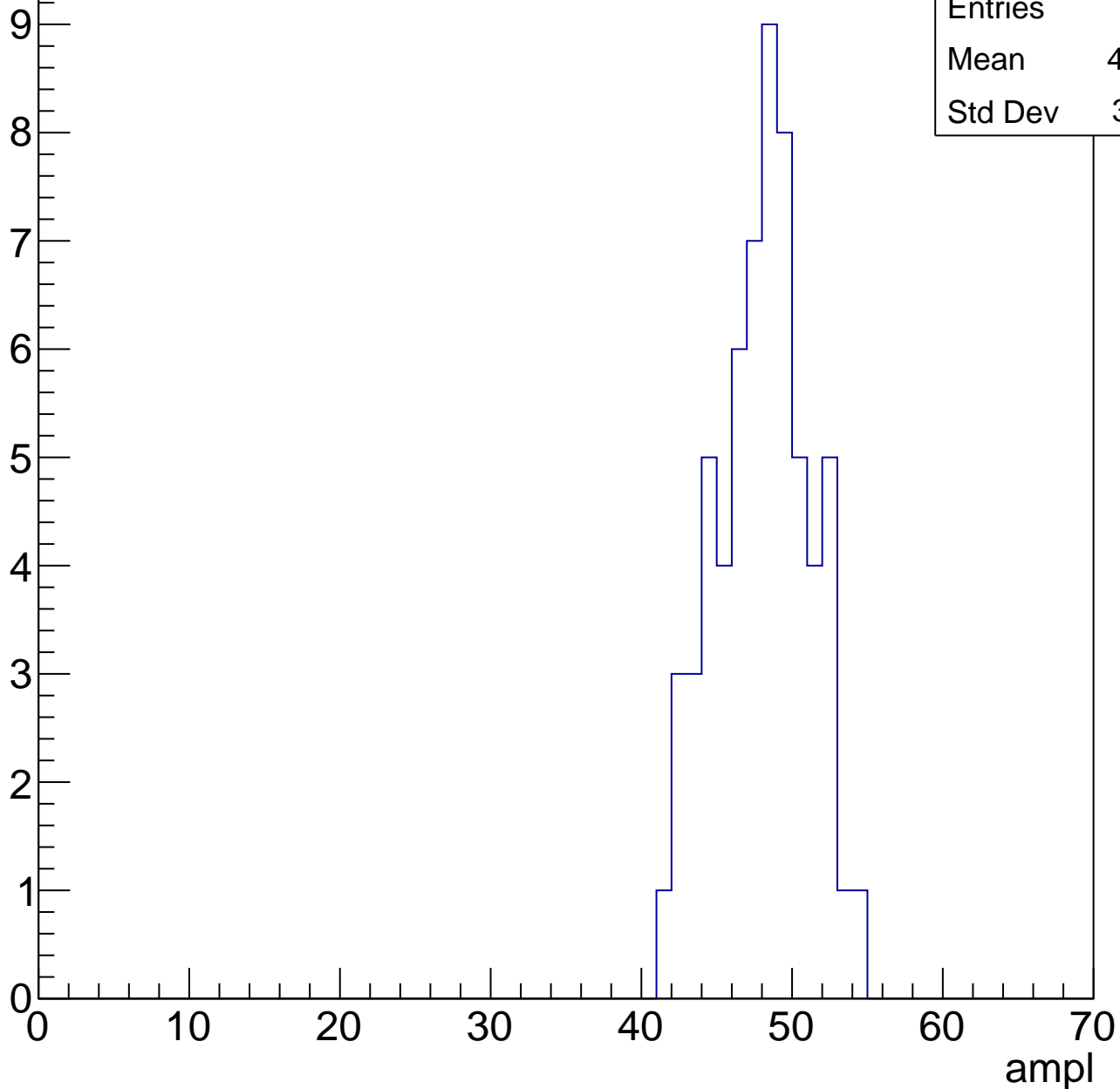
**Gaus Width: 3.1147**



# B0L000S, U19-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



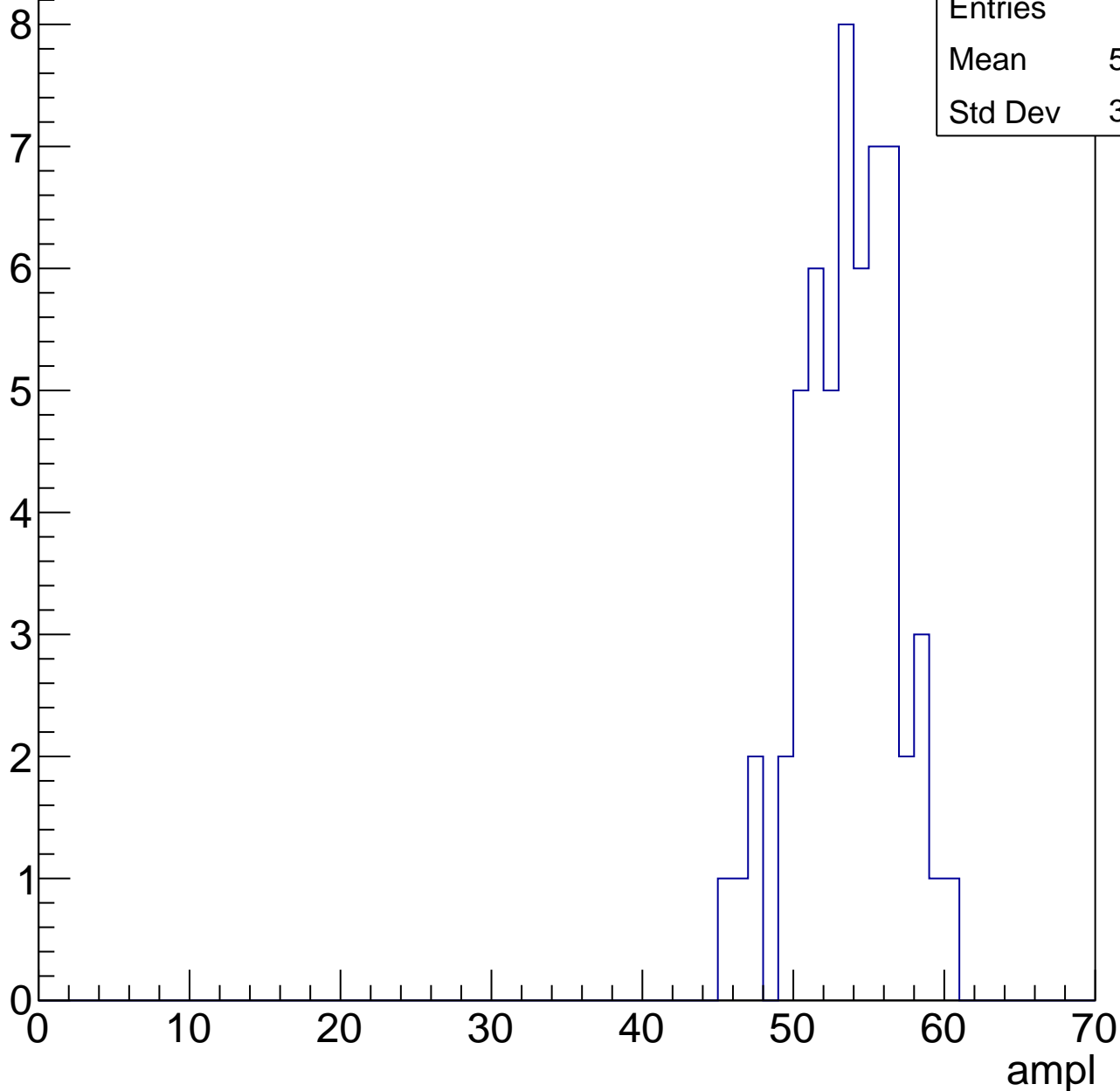
Entries	62
Mean	47.52
Std Dev	3.031

# B0L000S, U19-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

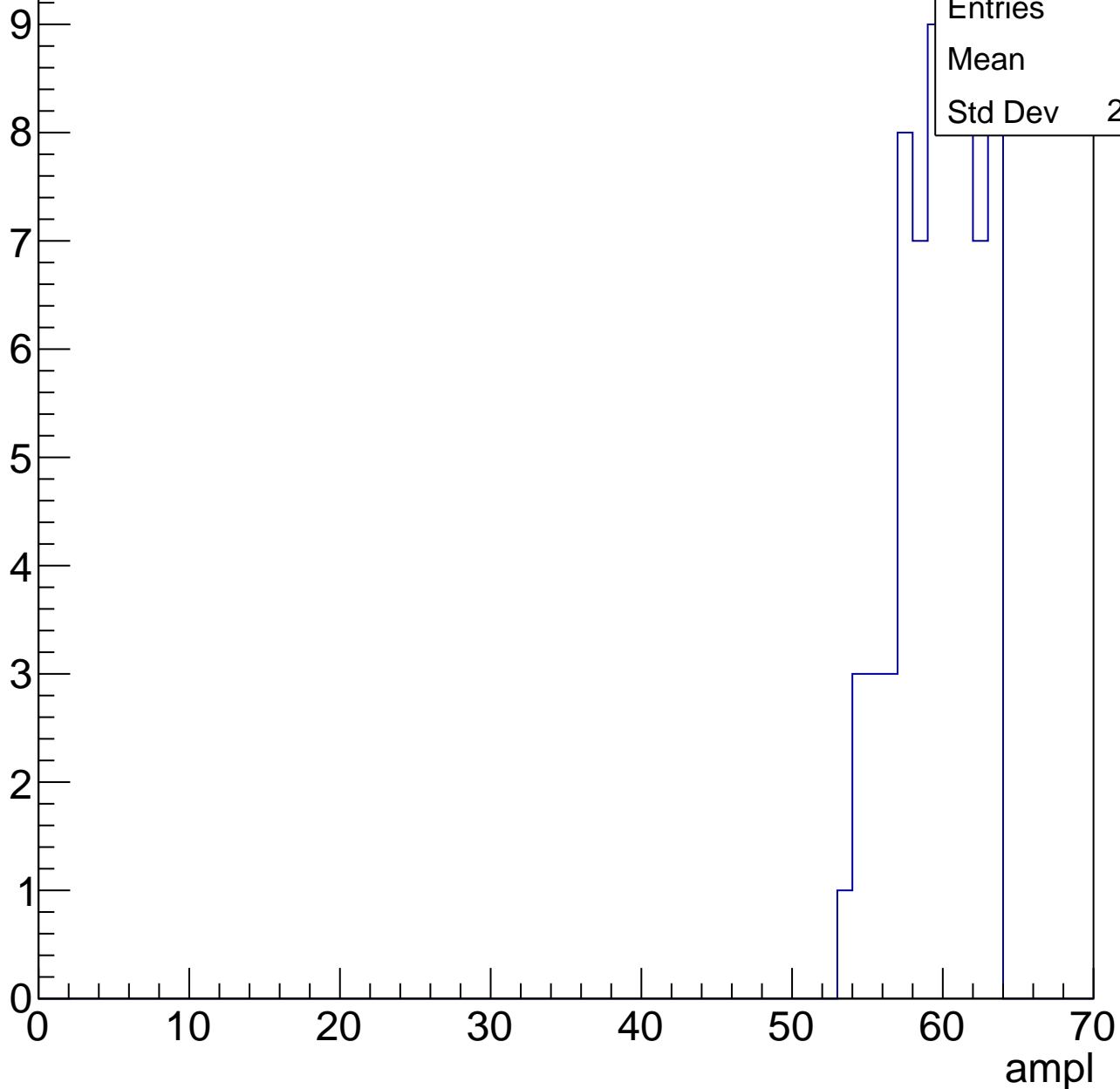
Entries	57
Mean	53.18
Std Dev	3.185



# B0L000S, U19-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

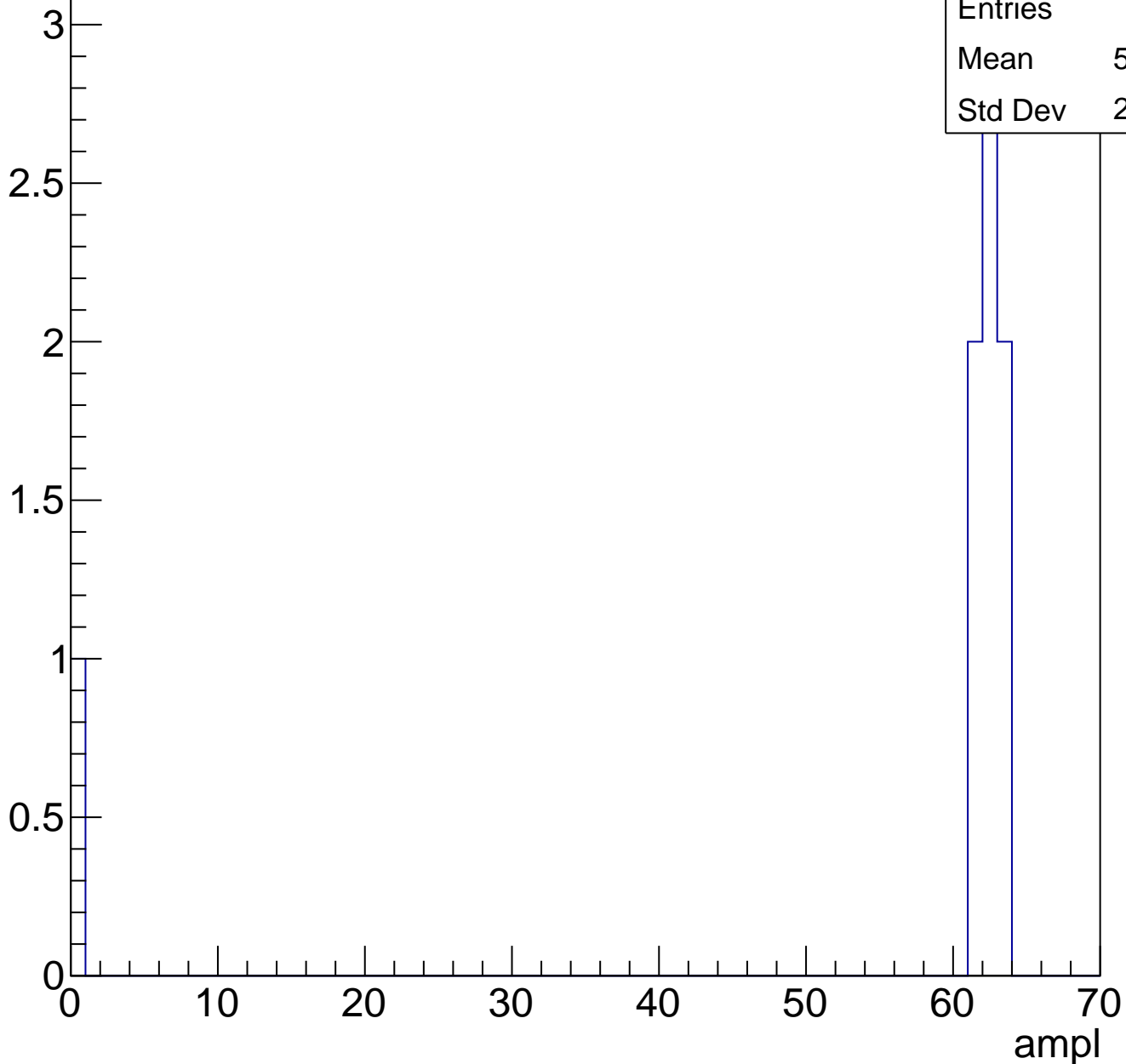
Entry



# B0L000S, U19-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch69, adc0

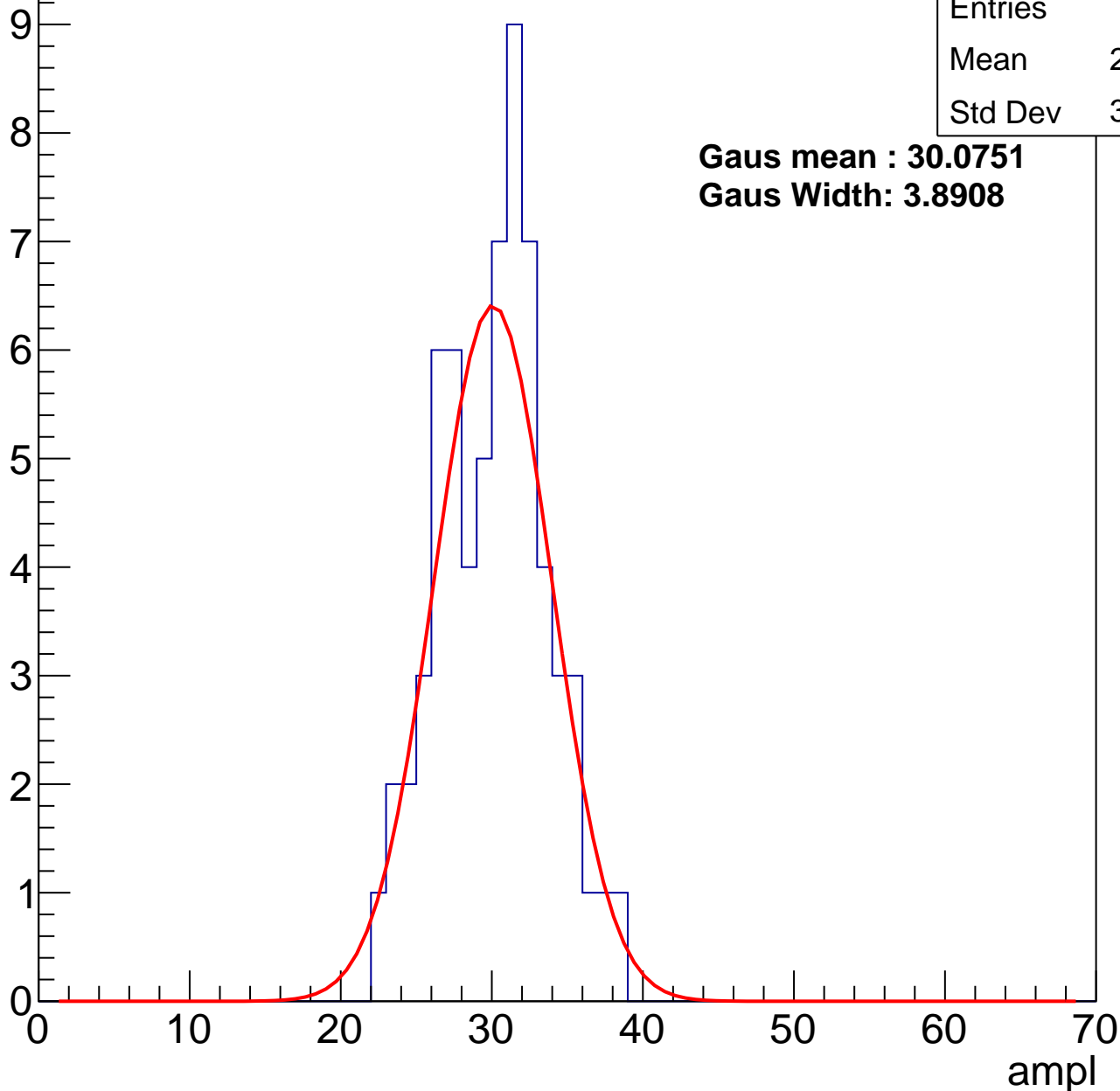
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.68
Std Dev	3.548

**Gaus mean : 30.0751**

**Gaus Width: 3.8908**



# B0L000S, U19-ch69, adc1

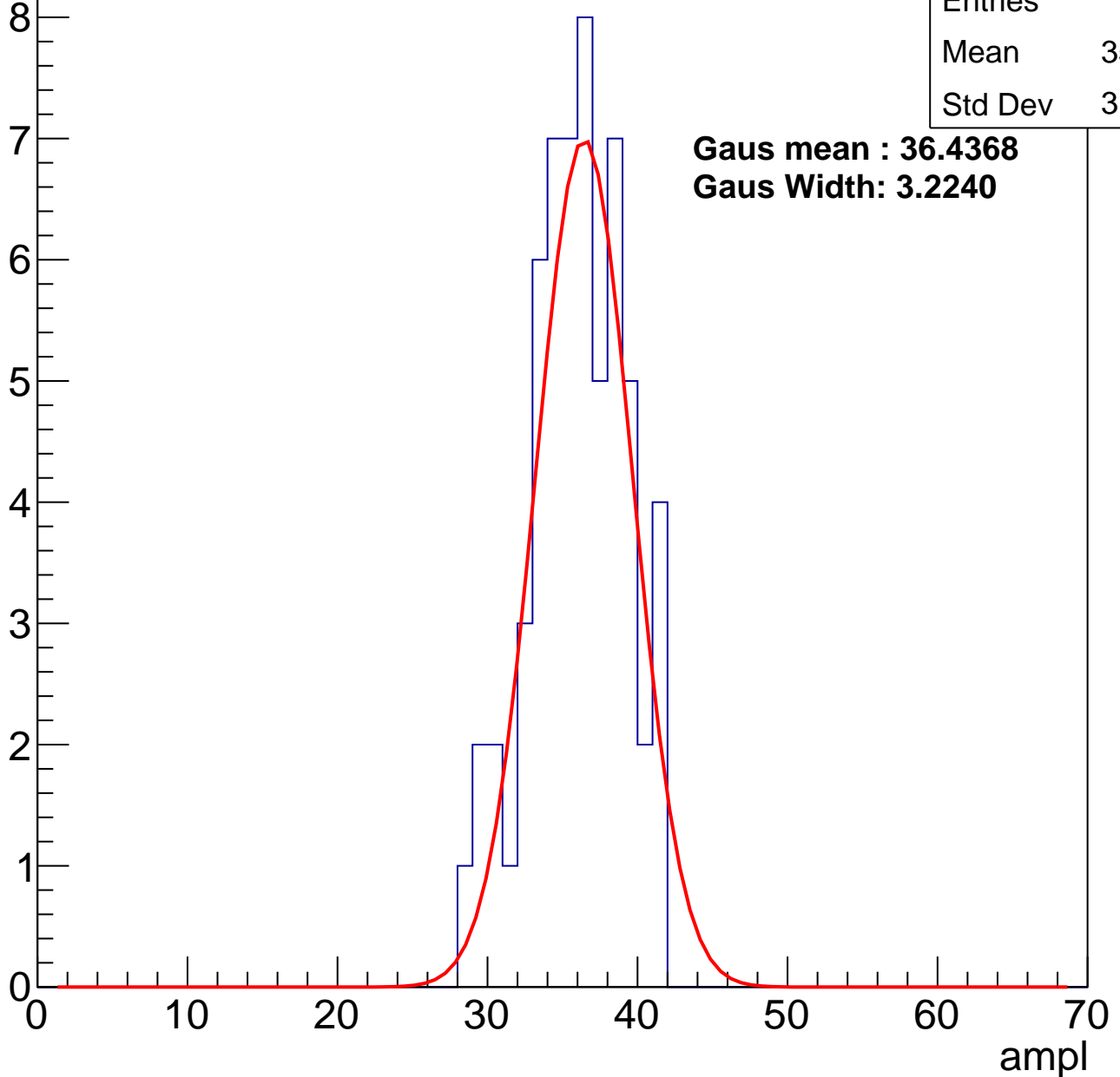
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	35.53
Std Dev	3.144

**Gaus mean : 36.4368**

**Gaus Width: 3.2240**



# B0L000S, U19-ch69, adc2

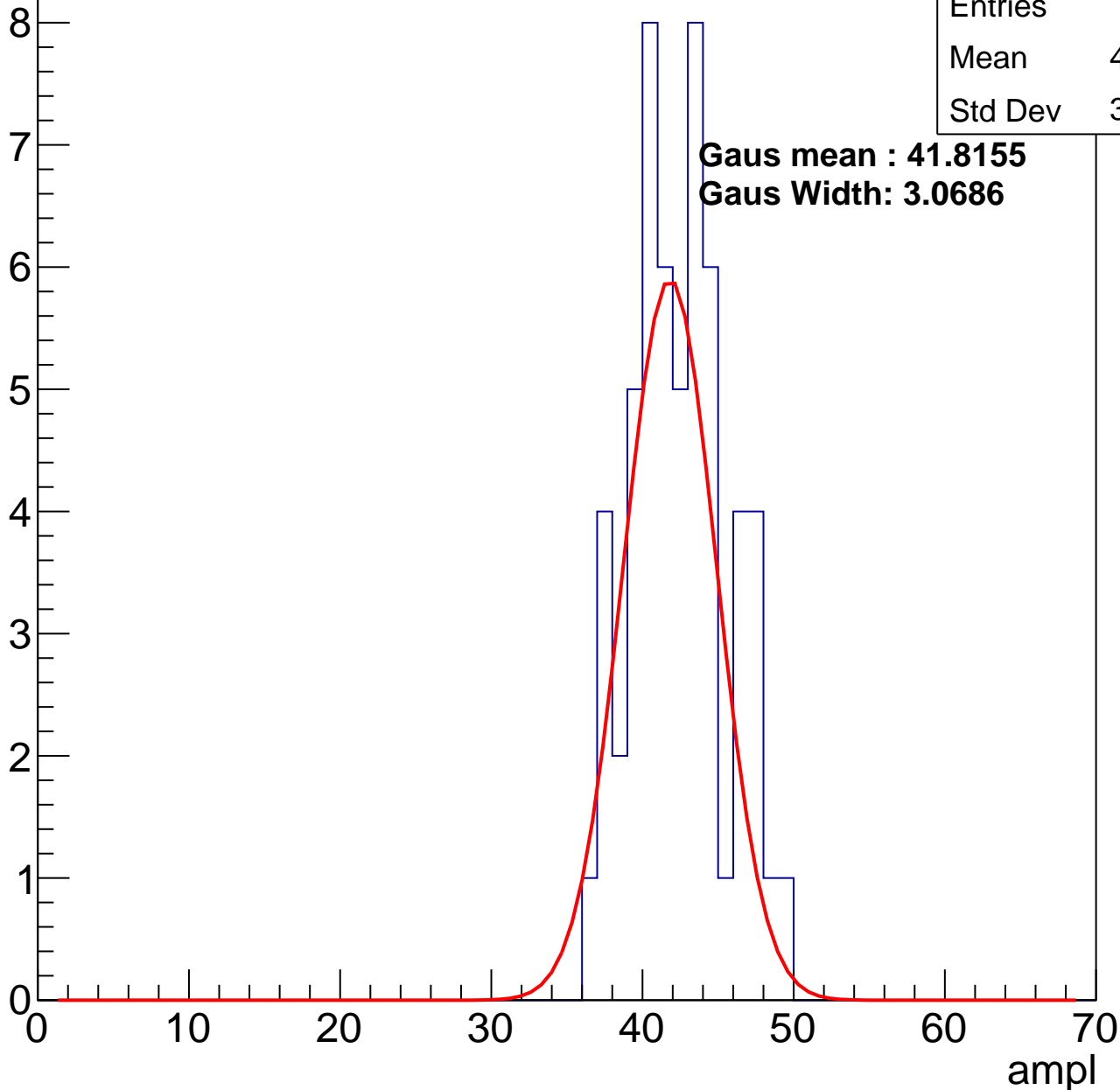
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.02
Std Dev	3.108

**Gaus mean : 41.8155**

**Gaus Width: 3.0686**

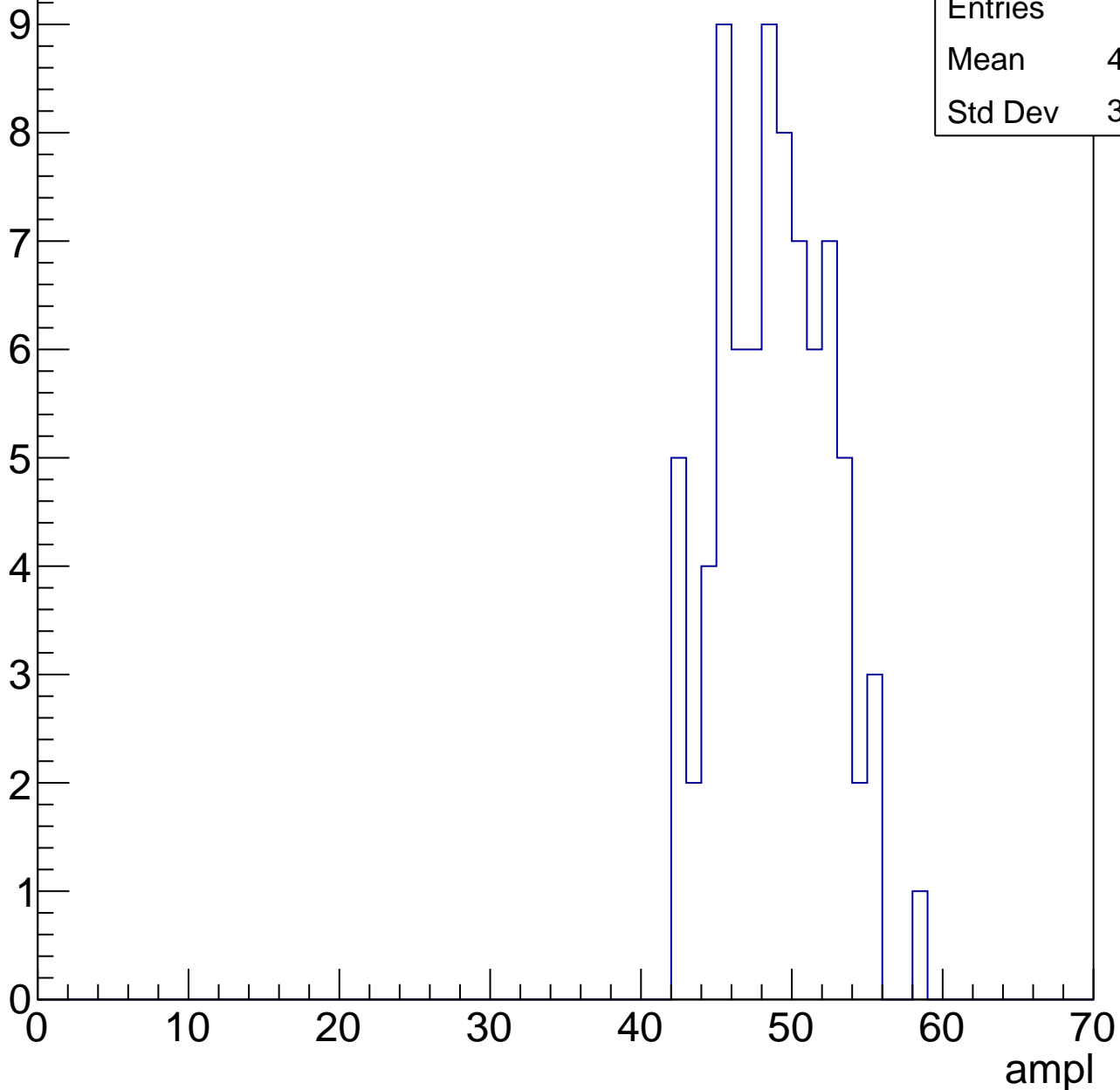


# B0L000S, U19-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

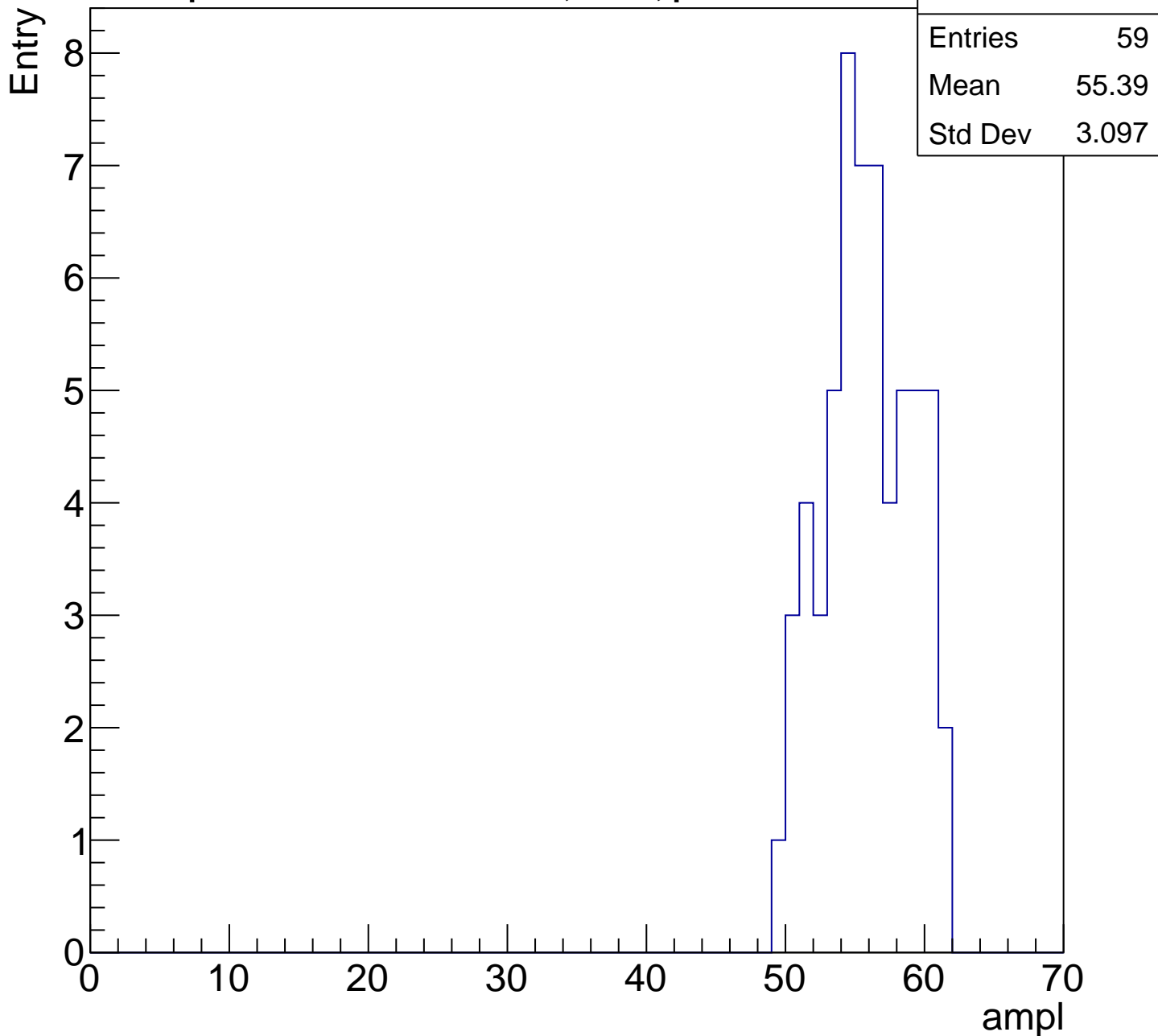
Entry

Entries	80
Mean	48.44
Std Dev	3.598



# B0L000S, U19-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

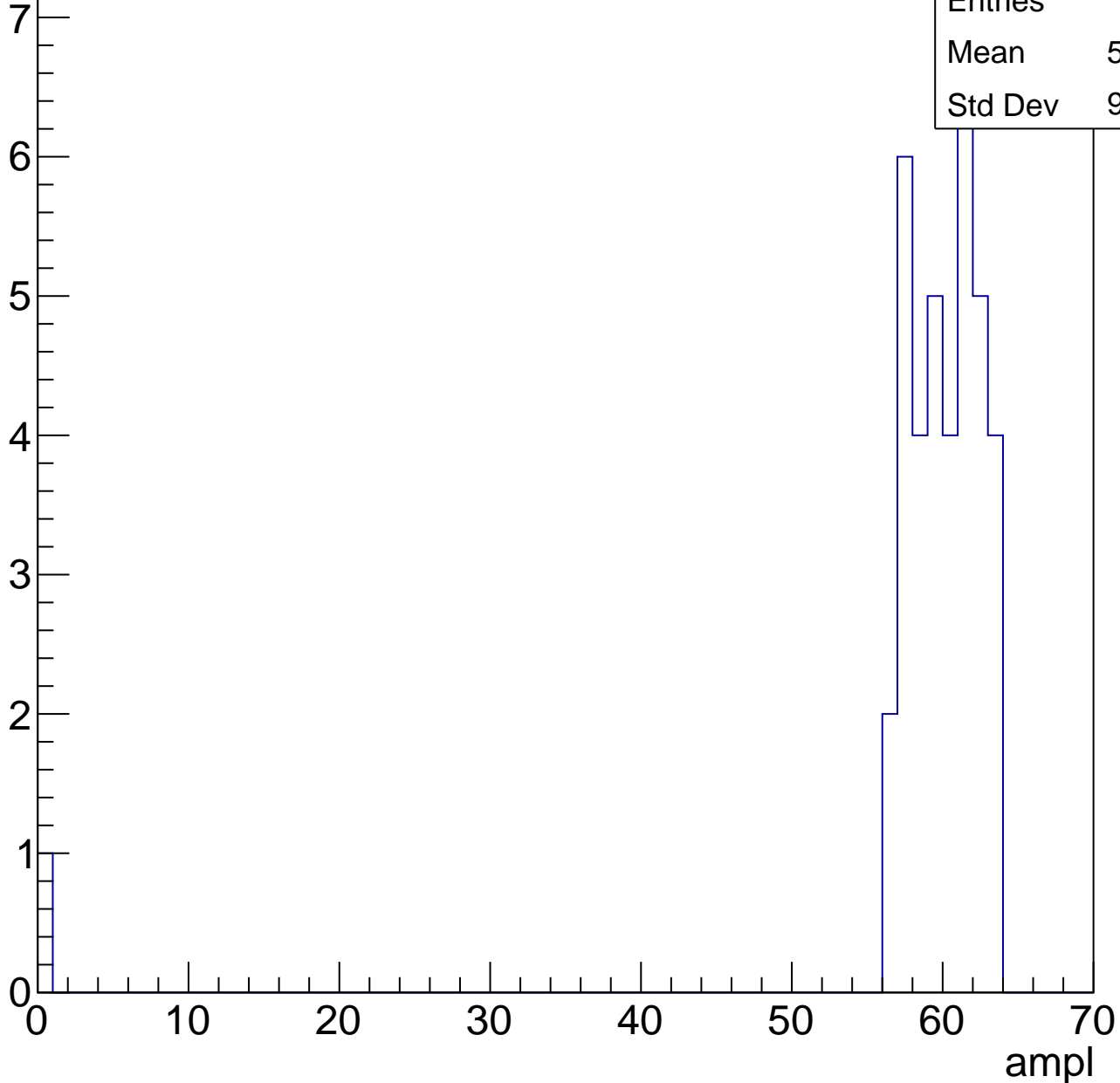


# B0L000S, U19-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

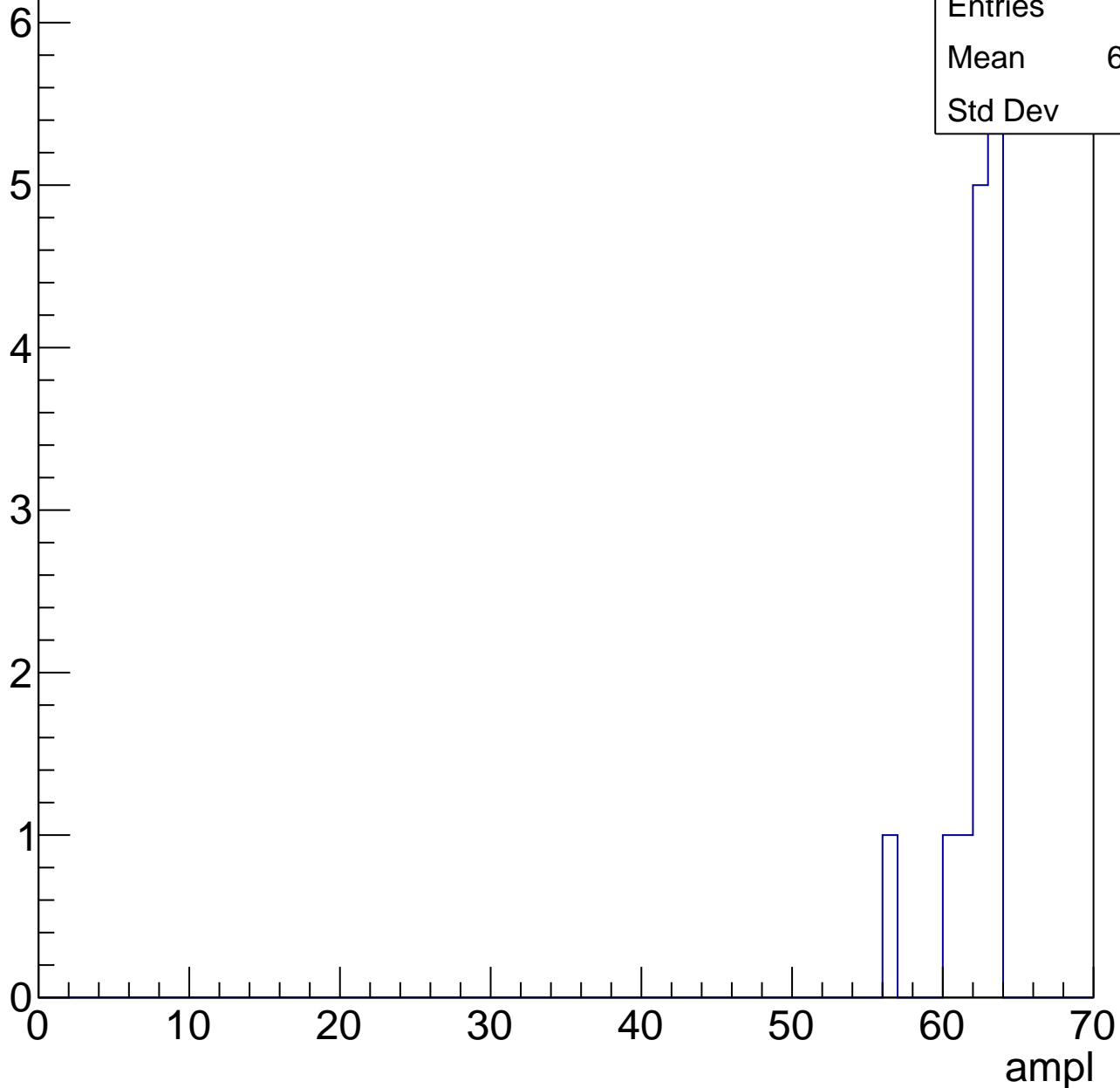
Entries	38
Mean	58.16
Std Dev	9.789



# B0L000S, U19-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch70, adc0

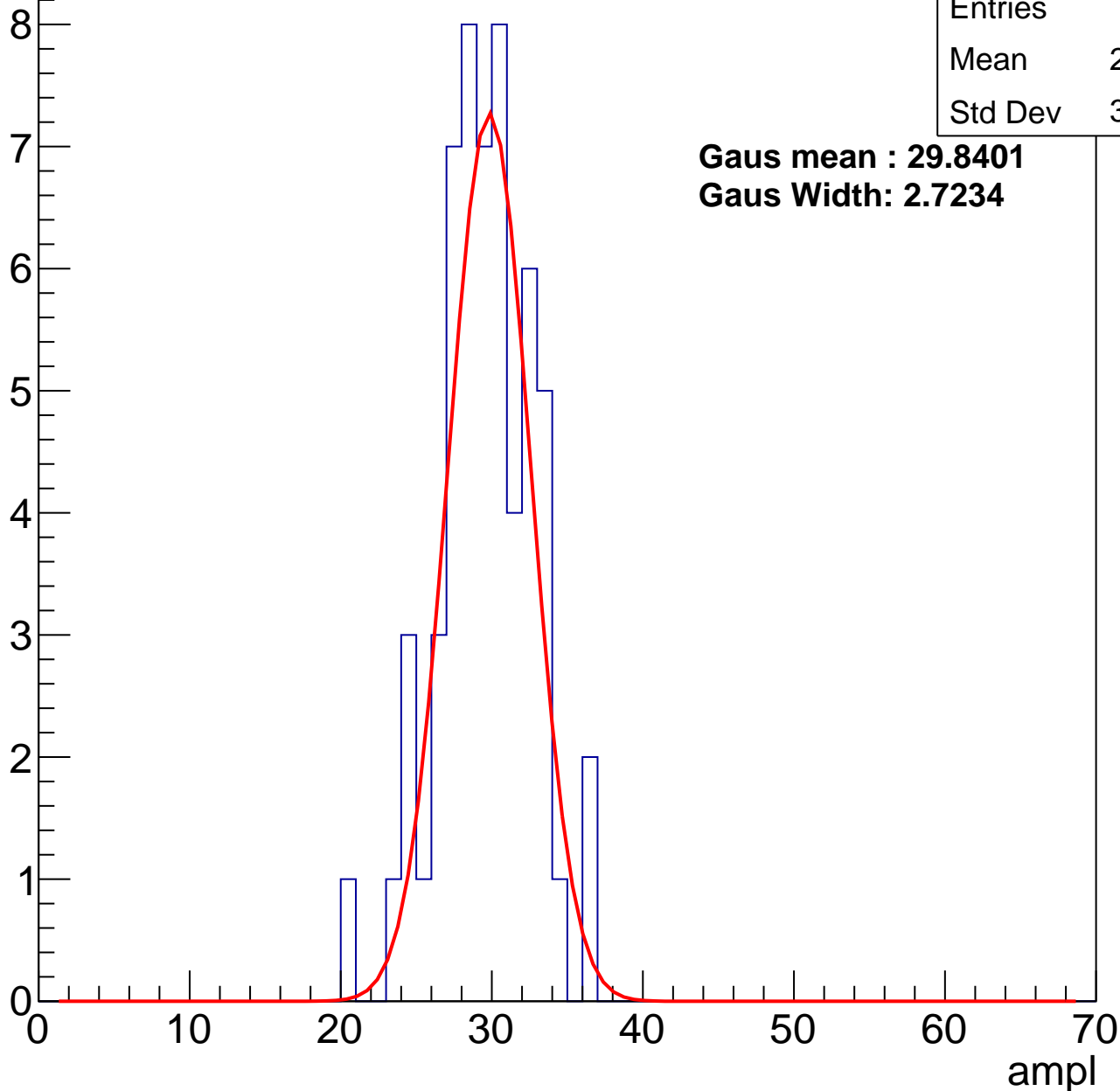
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	29.14
Std Dev	3.109

**Gaus mean : 29.8401**

**Gaus Width: 2.7234**



# B0L000S, U19-ch70, adc1

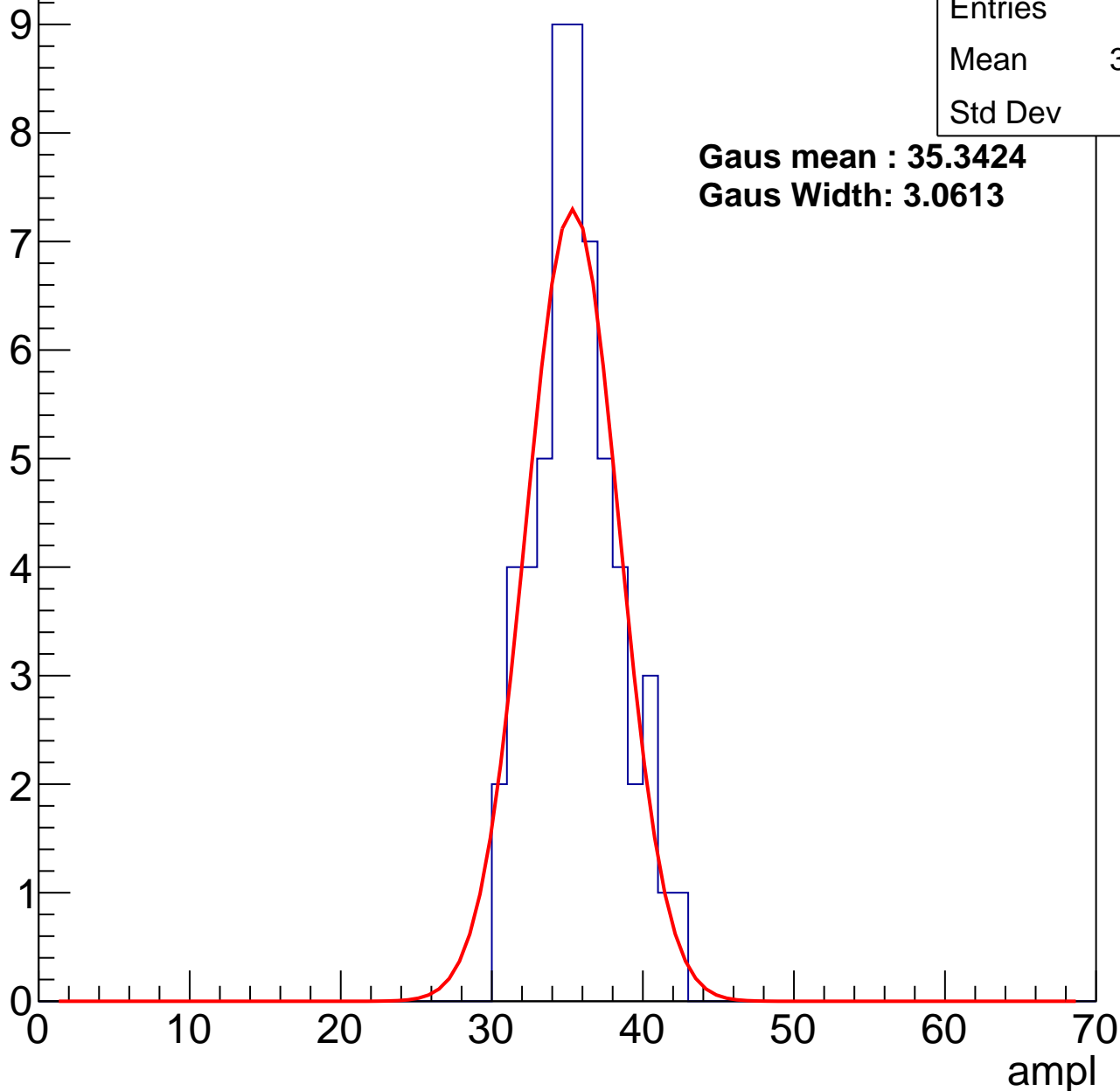
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	35.14
Std Dev	2.78

**Gaus mean : 35.3424**

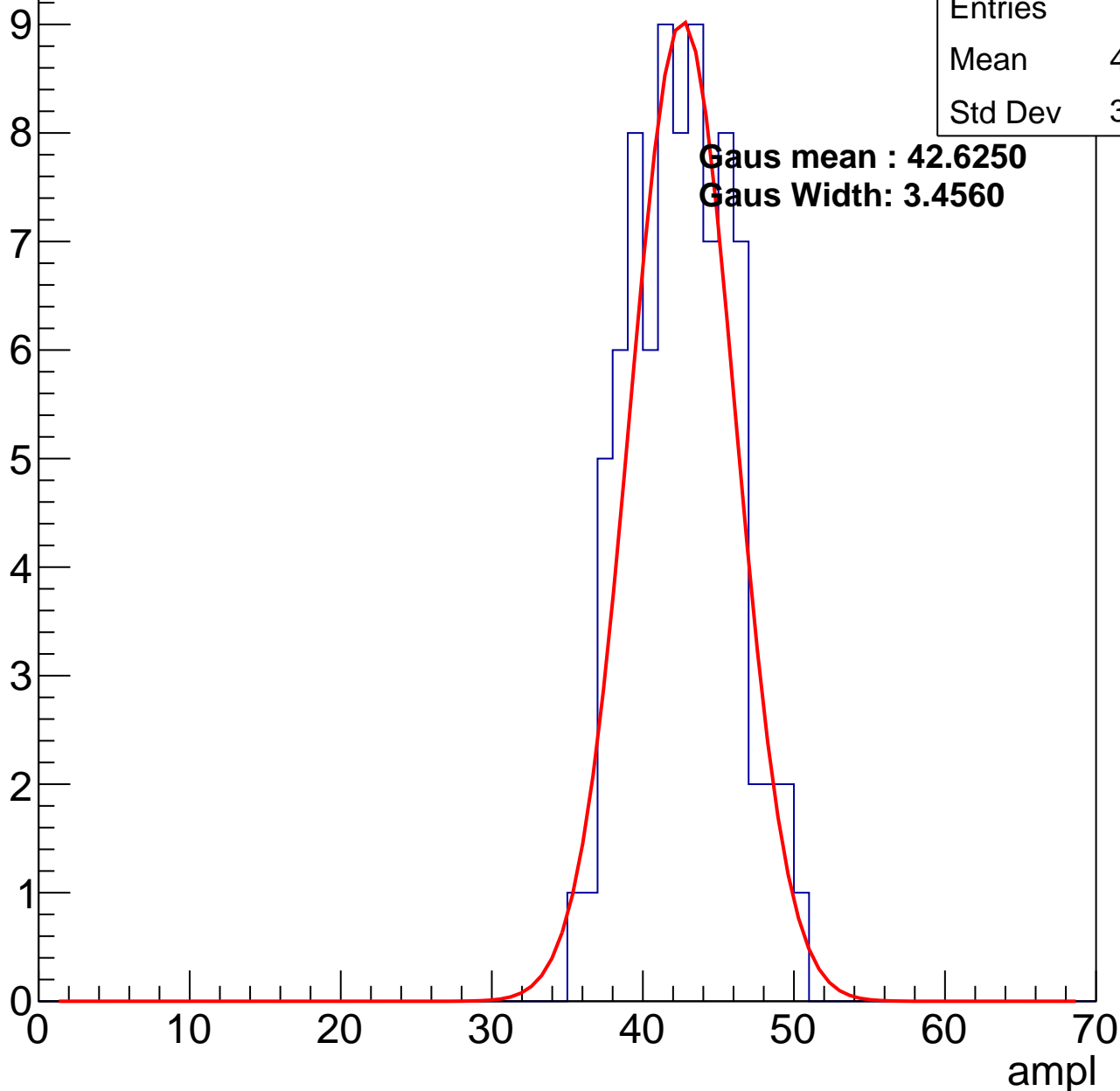
**Gaus Width: 3.0613**



# B0L000S, U19-ch70, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

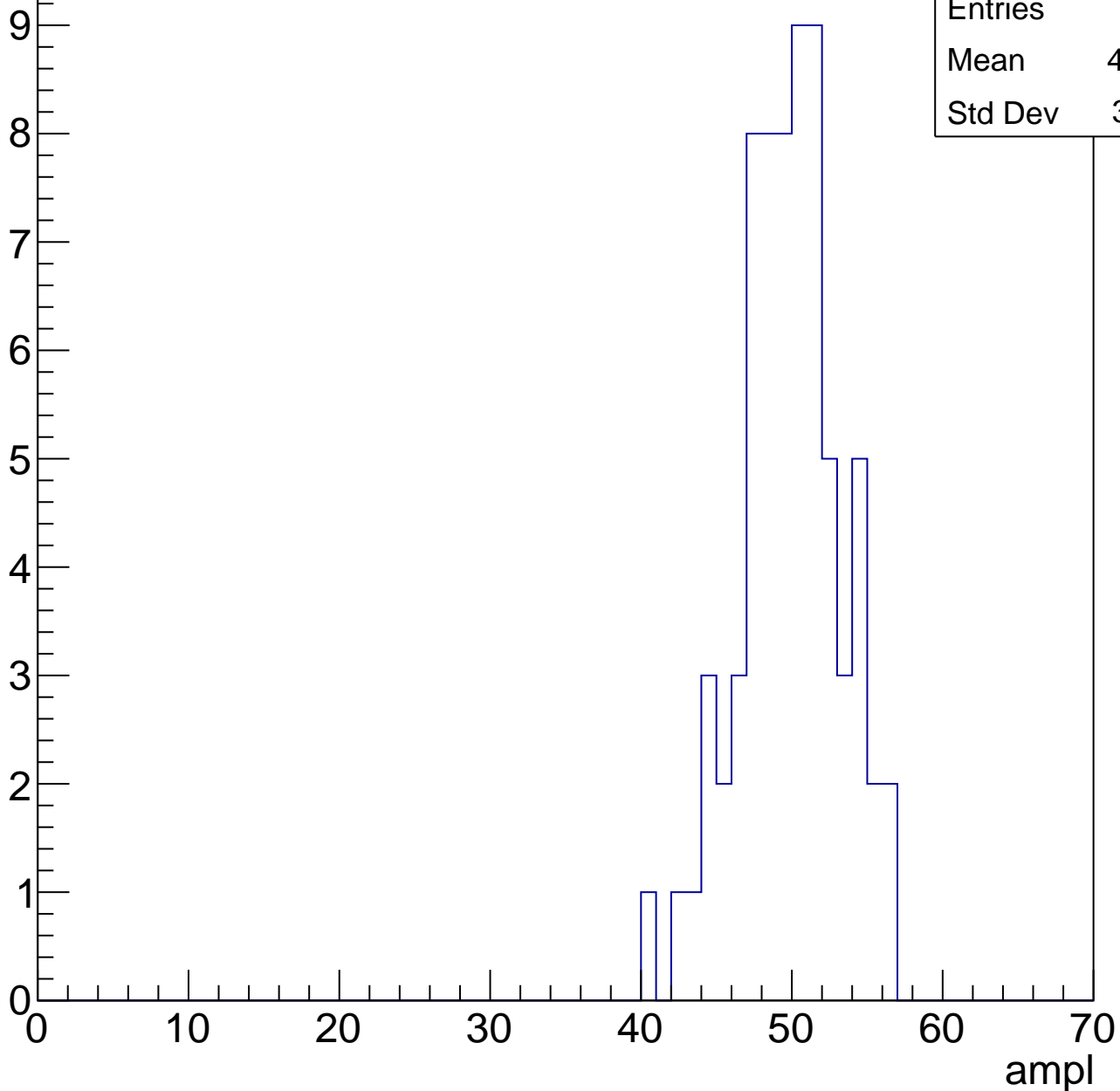


# B0L000S, U19-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.39
Std Dev	3.301

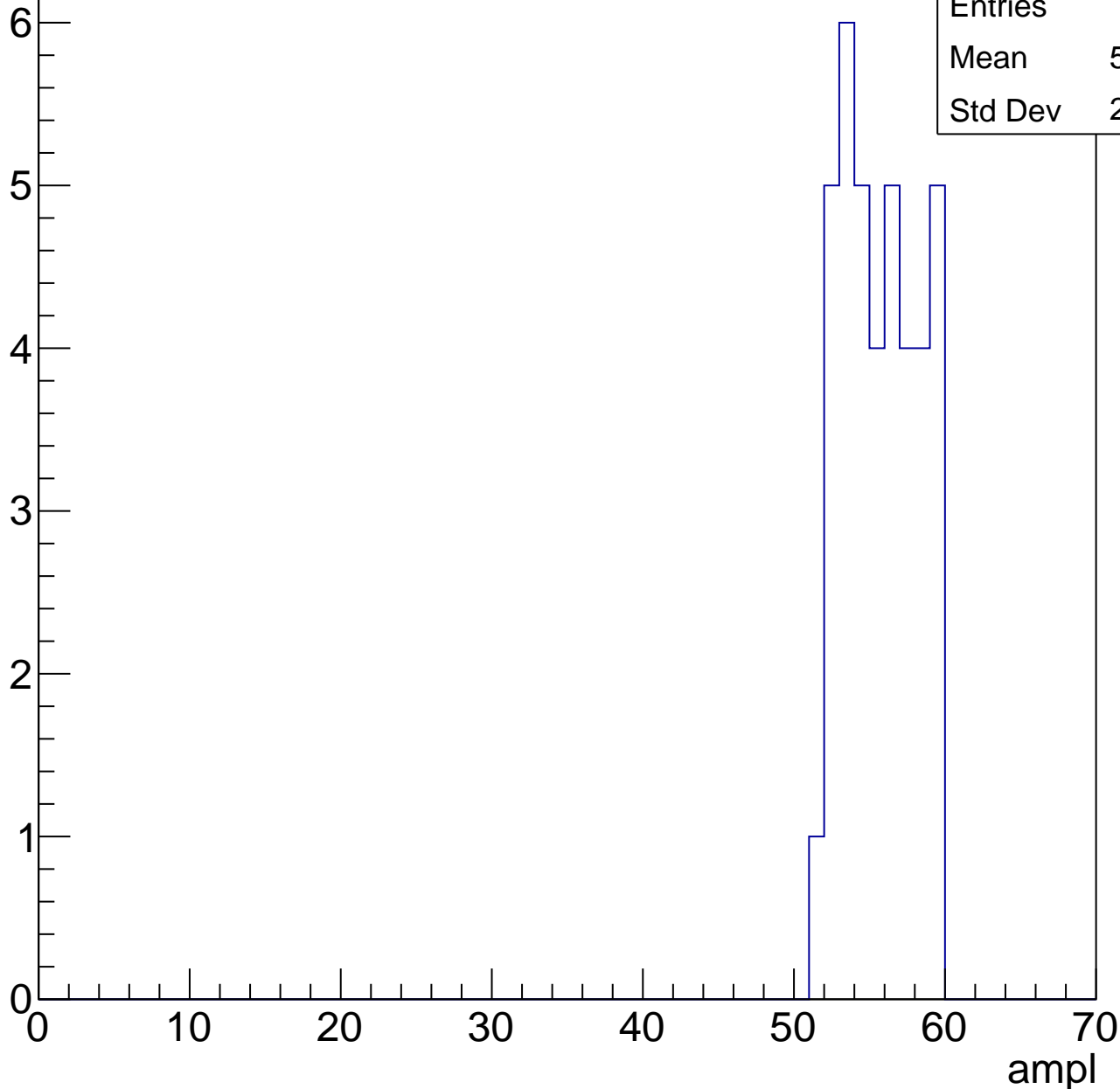


# B0L000S, U19-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	55.23
Std Dev	2.402

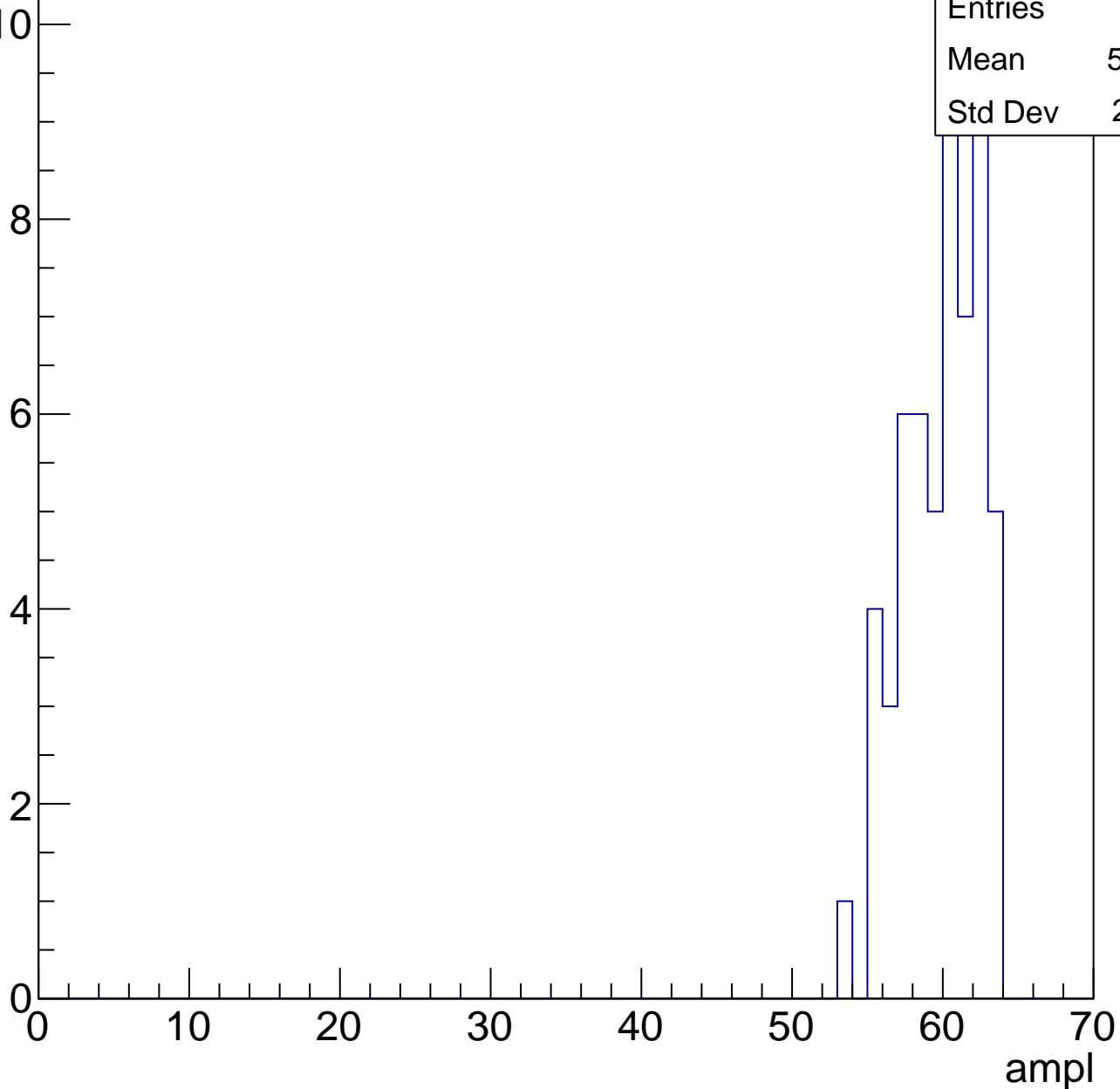


# B0L000S, U19-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	59.39
Std Dev	2.491

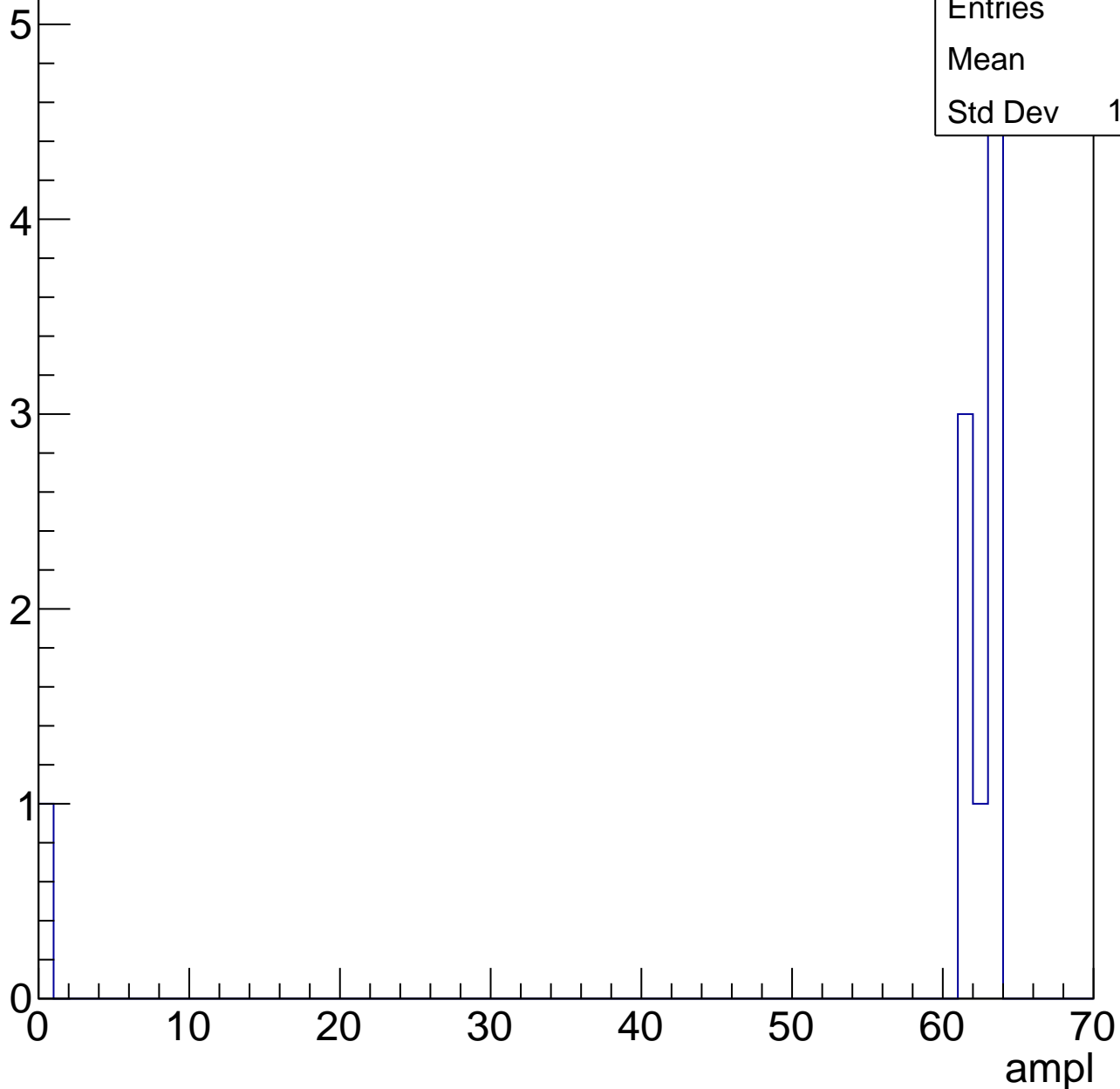


# B0L000S, U19-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	56
Std Dev	18.69





# B0L000S, U19-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch71, adc0

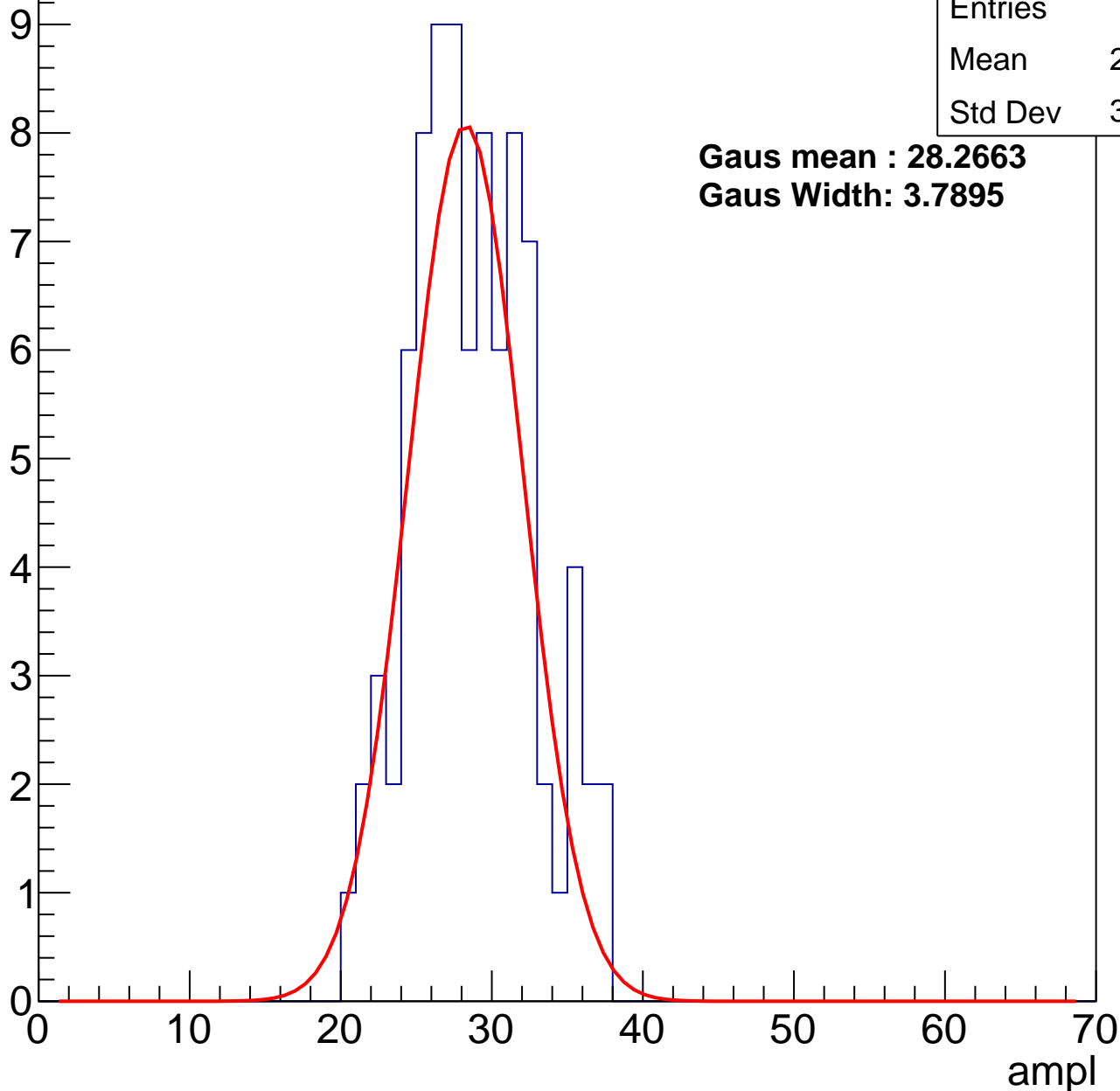
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	28.29
Std Dev	3.908

**Gaus mean : 28.2663**

**Gaus Width: 3.7895**



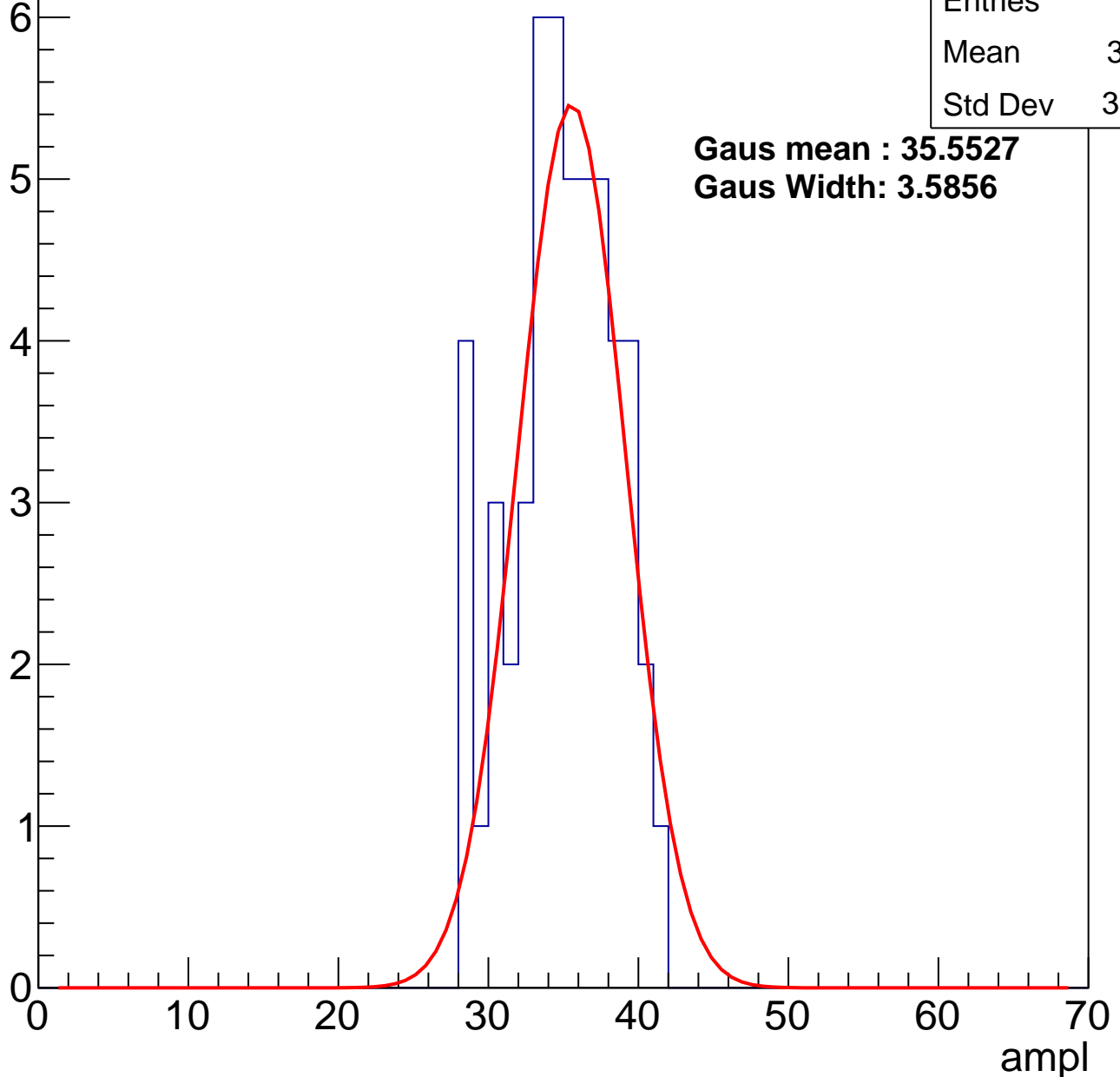
# B0L000S, U19-ch71, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	34.51
Std Dev	3.415

**Gaus mean : 35.5527**  
**Gaus Width: 3.5856**



# B0L000S, U19-ch71, adc2

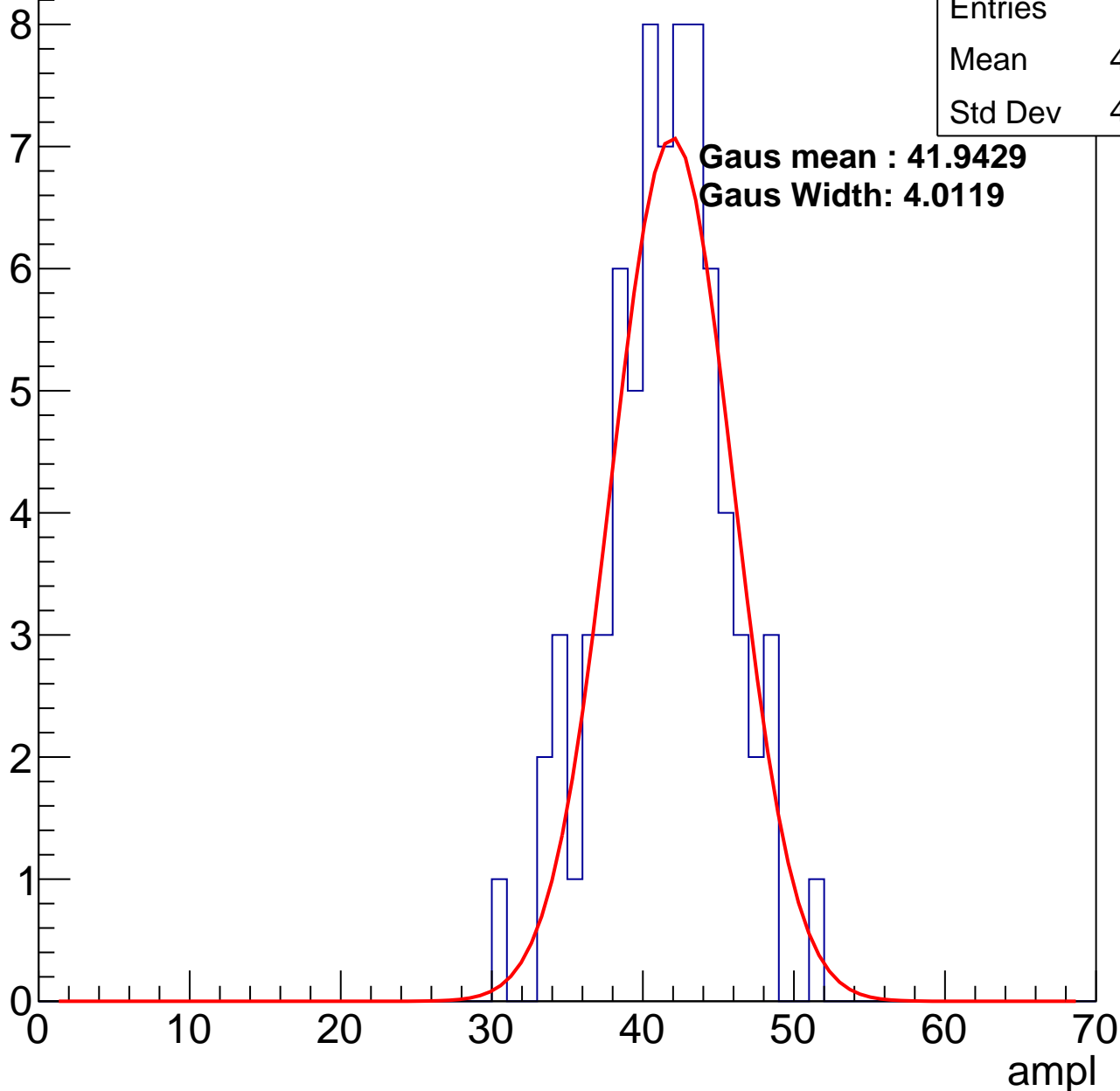
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	40.99
Std Dev	4.008

**Gaus mean : 41.9429**

**Gaus Width: 4.0119**

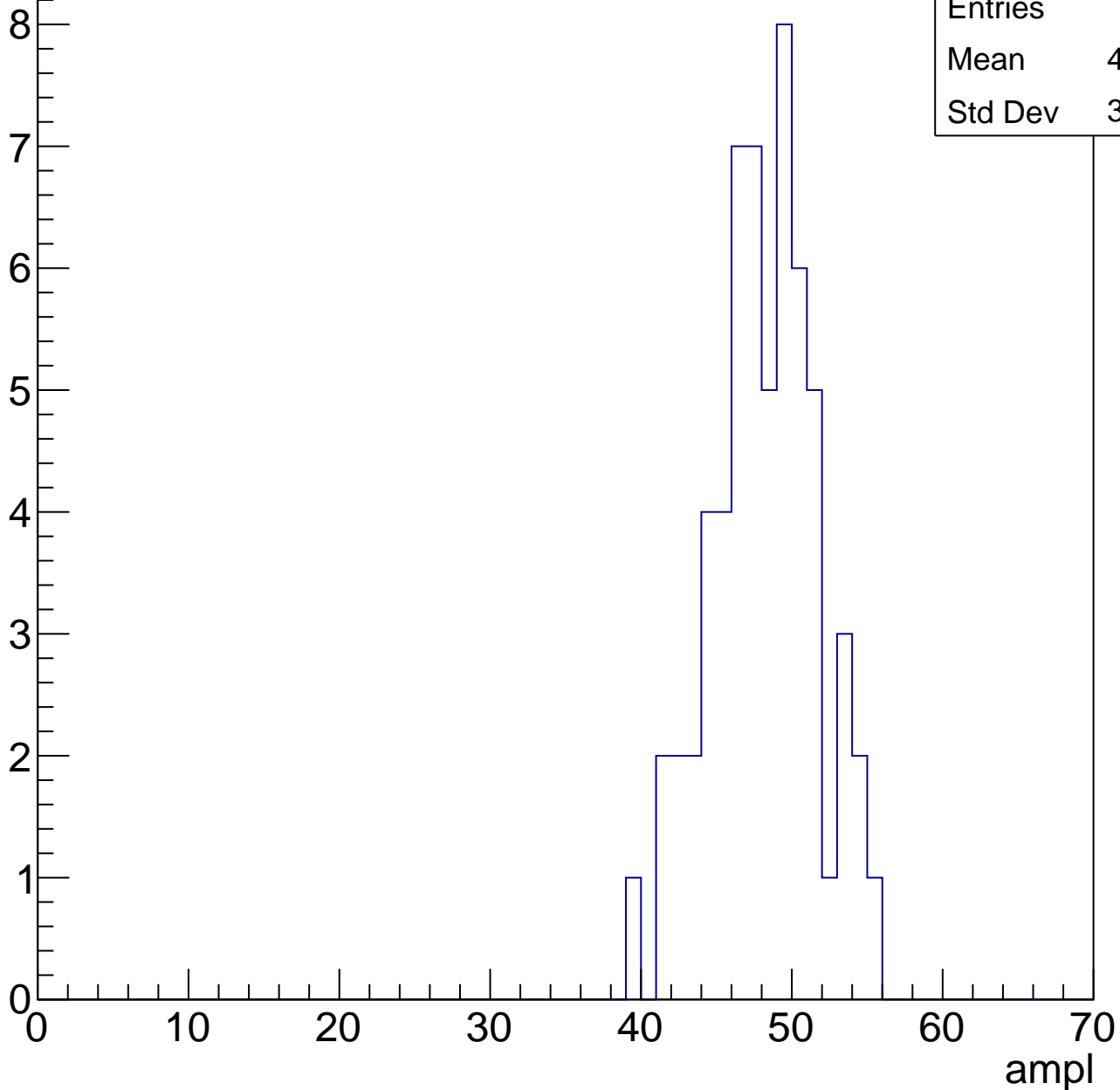


# B0L000S, U19-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	47.65
Std Dev	3.458

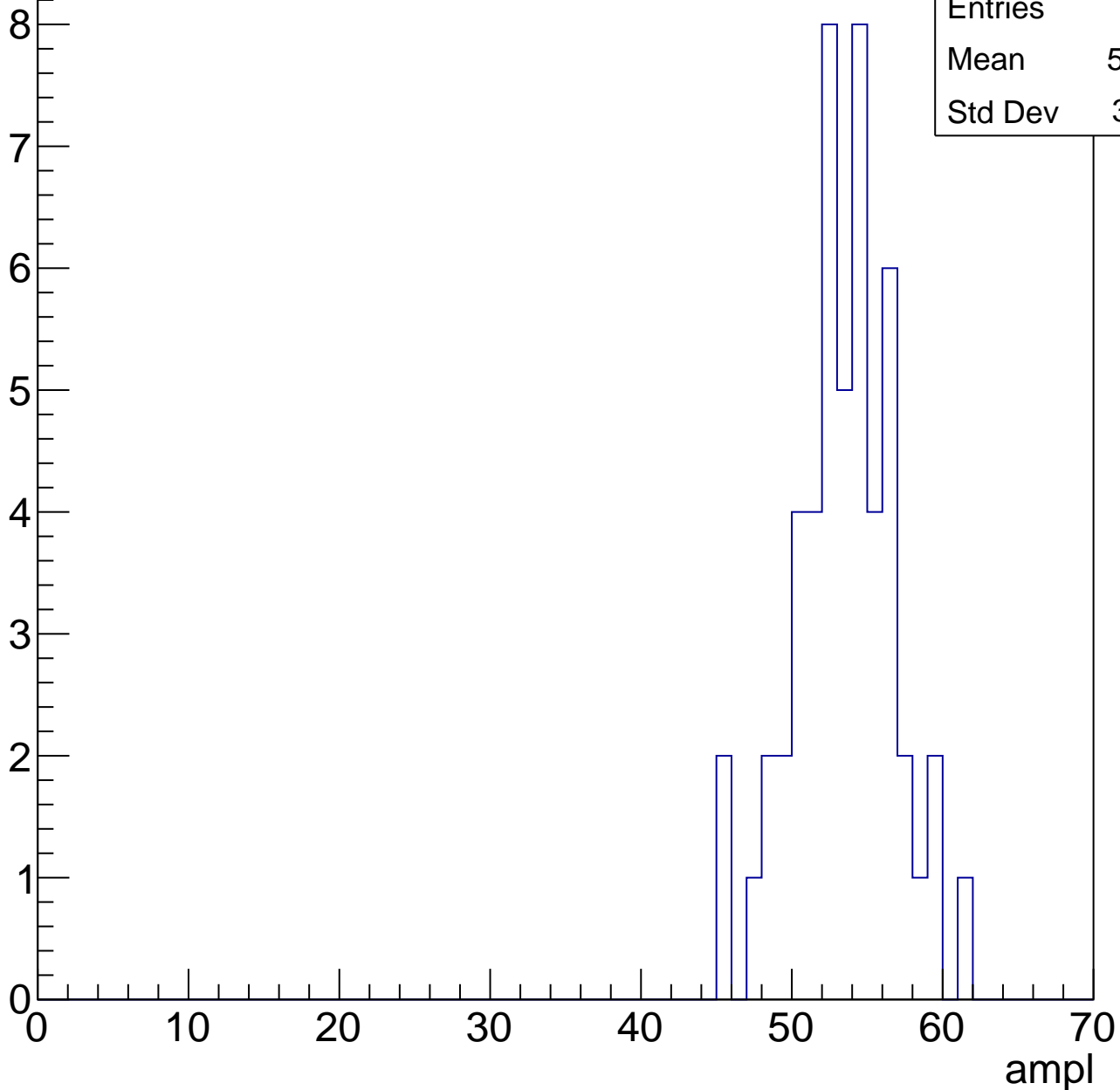


# B0L000S, U19-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	52.98
Std Dev	3.331

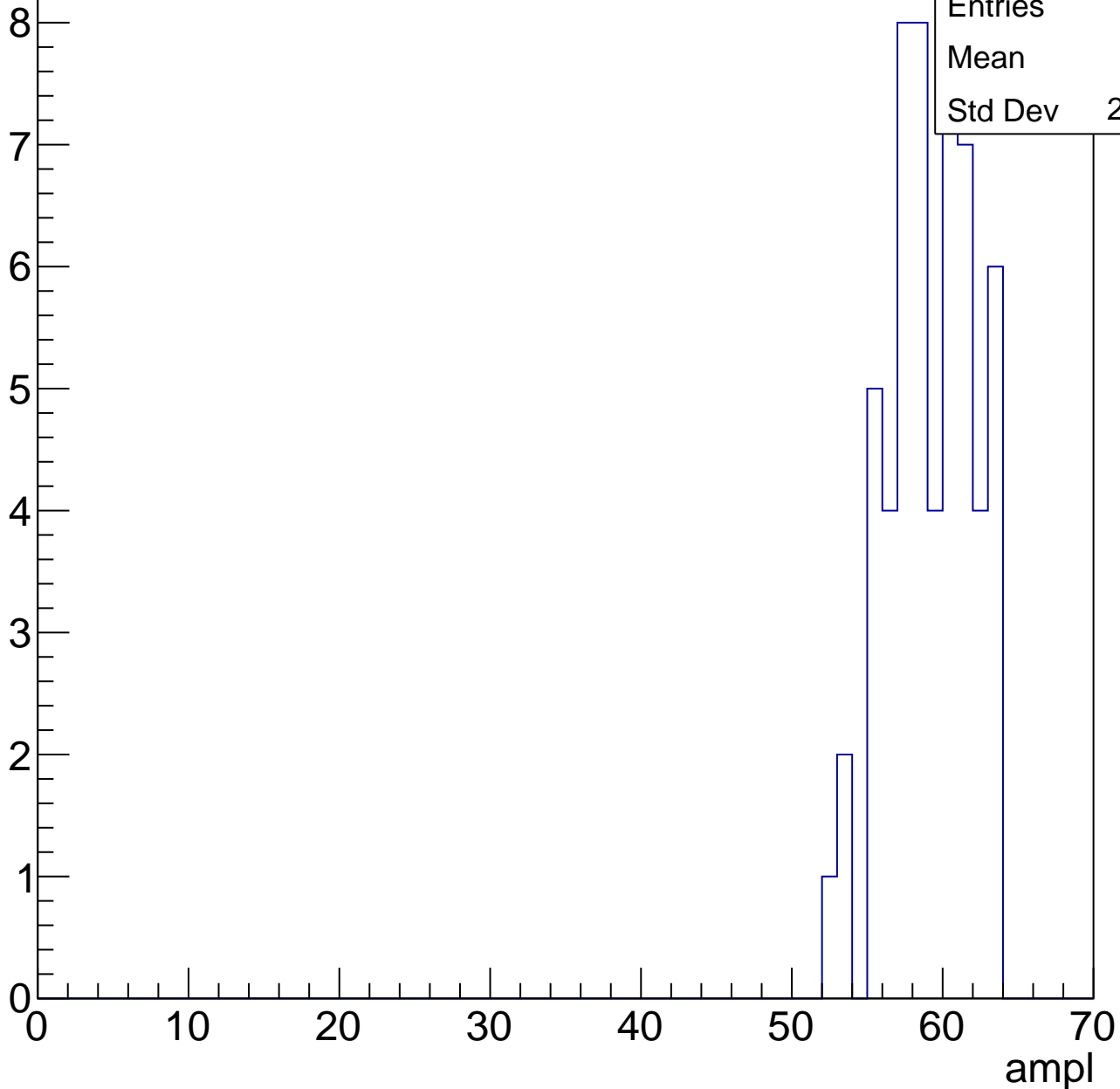


# B0L000S, U19-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	58.7
Std Dev	2.778

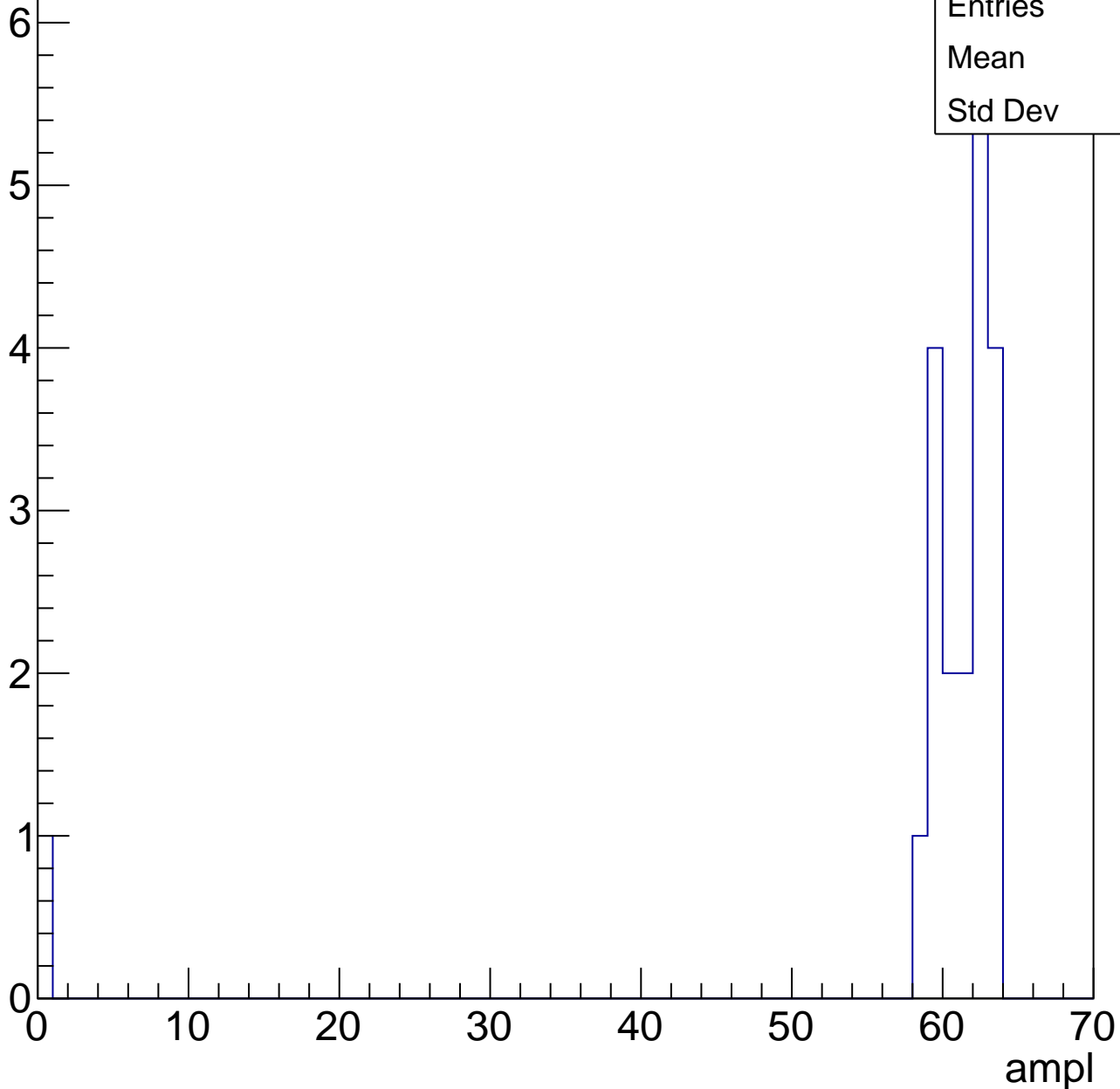


# B0L000S, U19-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	58
Std Dev	13.4





# B0L000S, U19-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch72, adc0

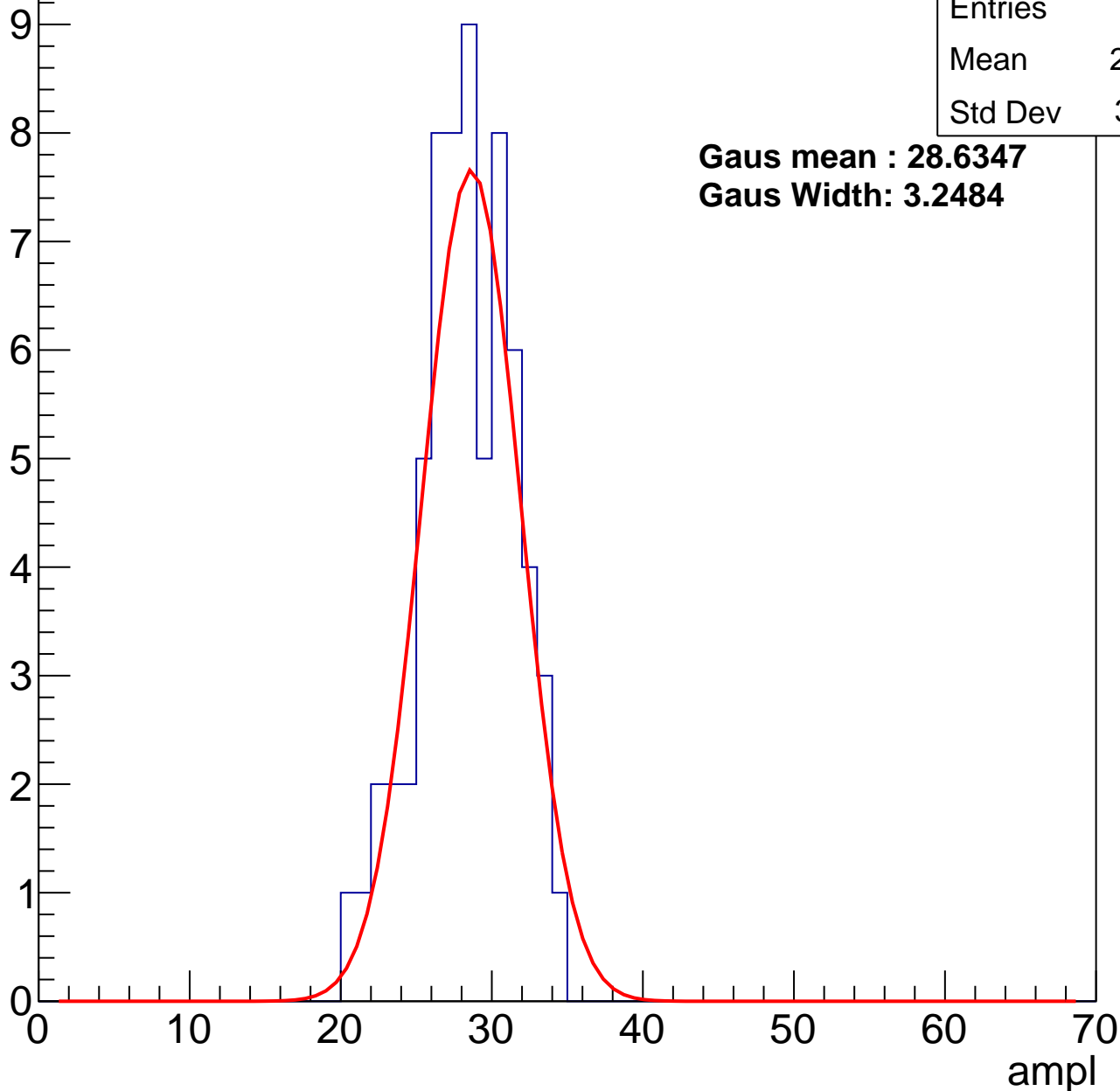
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	27.88
Std Dev	3.081

**Gaus mean : 28.6347**

**Gaus Width: 3.2484**



# B0L000S, U19-ch72, adc1

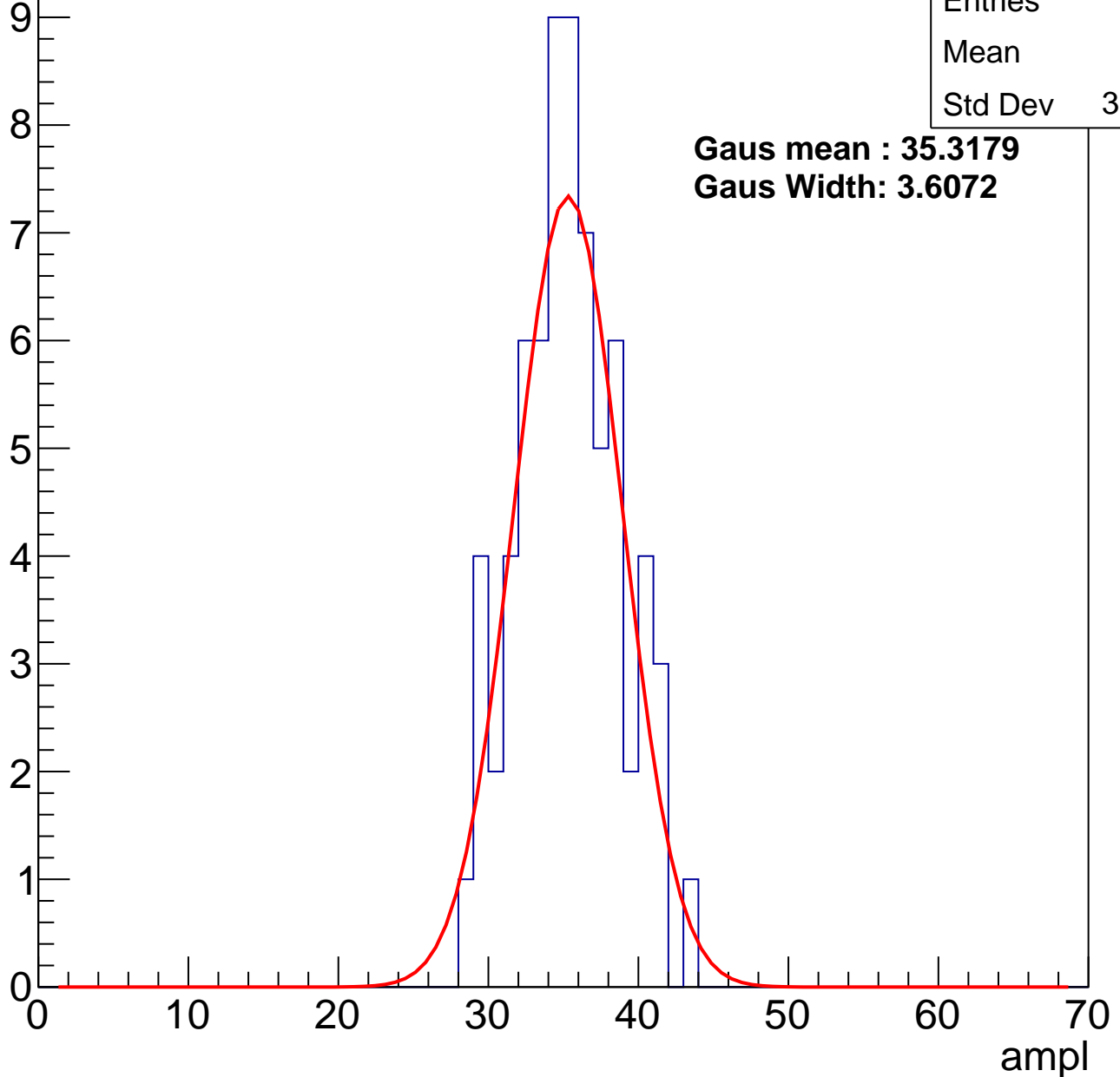
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	34.9
Std Dev	3.359

**Gaus mean : 35.3179**

**Gaus Width: 3.6072**



# B0L000S, U19-ch72, adc2

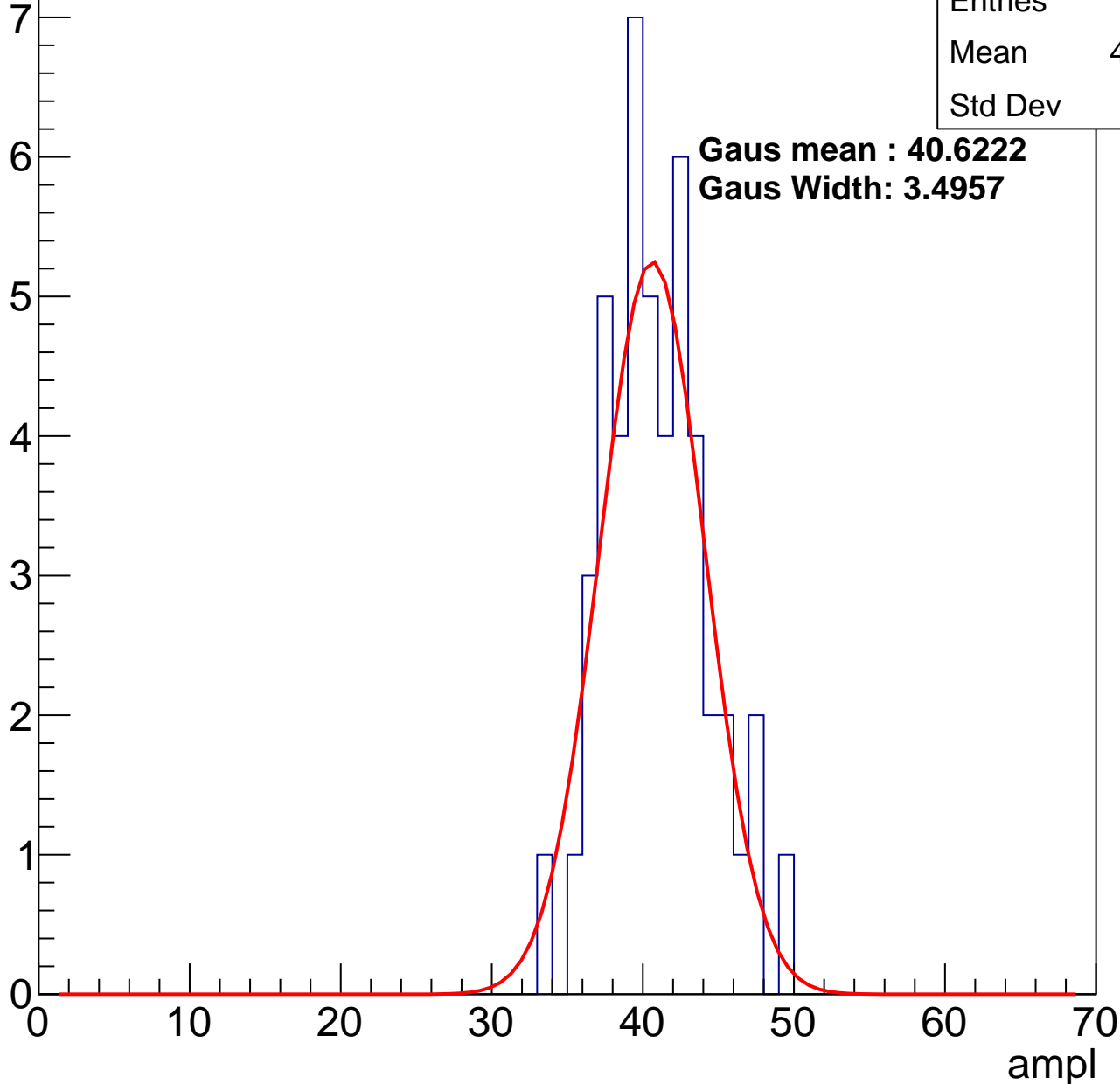
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	40.44
Std Dev	3.36

**Gaus mean : 40.6222**

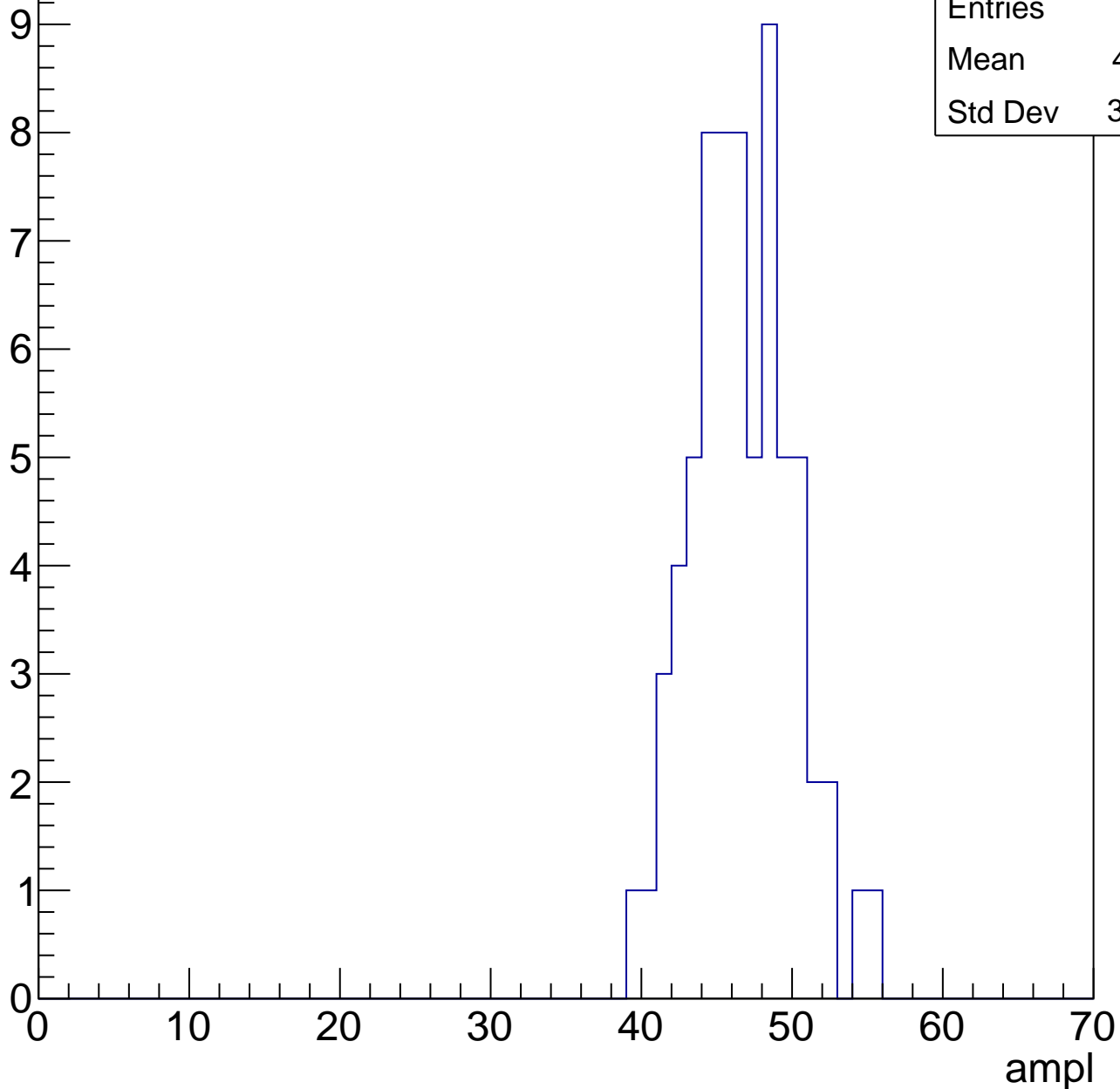
**Gaus Width: 3.4957**



# B0L000S, U19-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

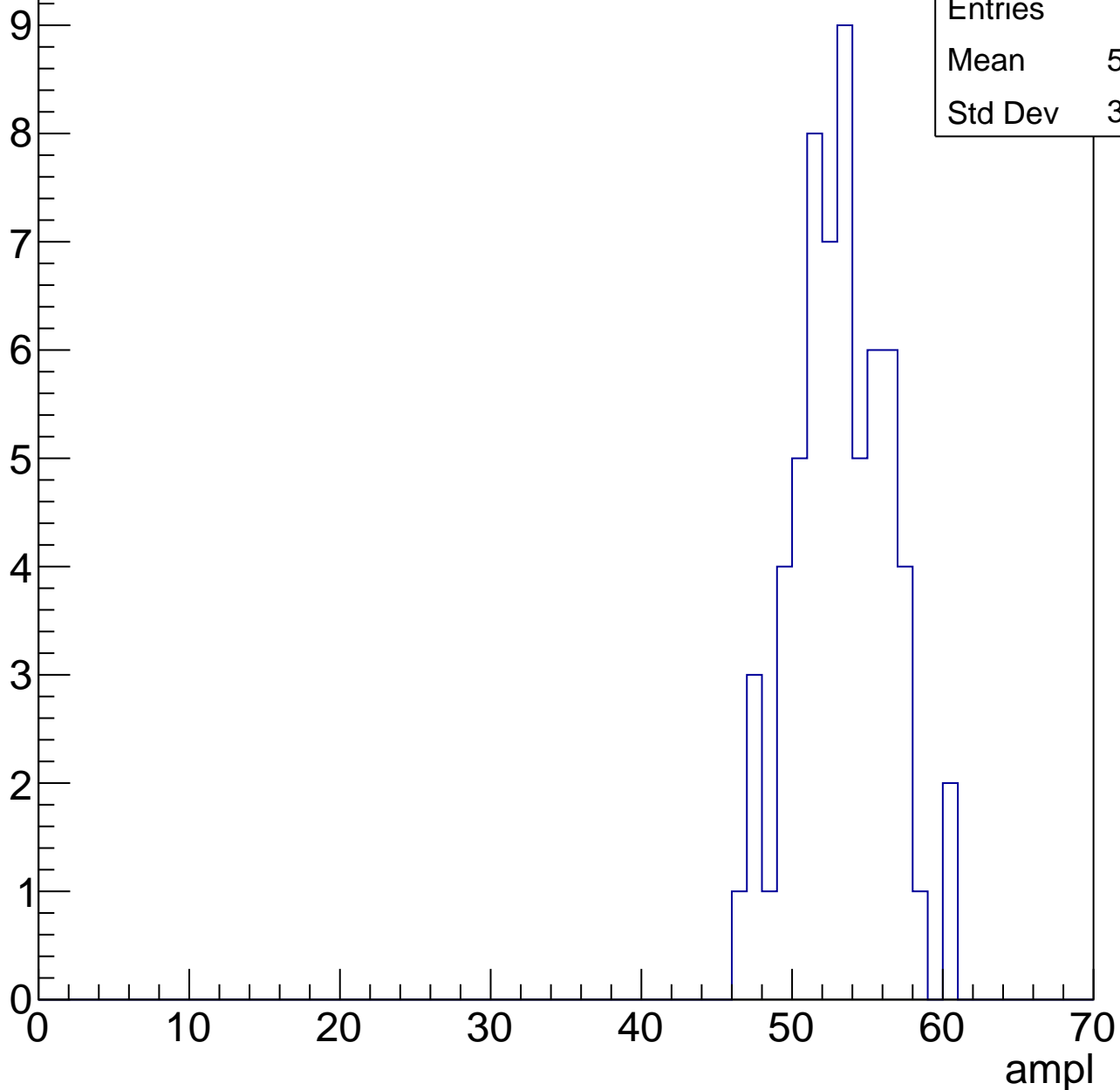


Entries	68
Mean	46.21
Std Dev	3.297

# B0L000S, U19-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

Entries 64

Mean 58.81

Std Dev 2.783

8

6

4

2

0

0

10

20

30

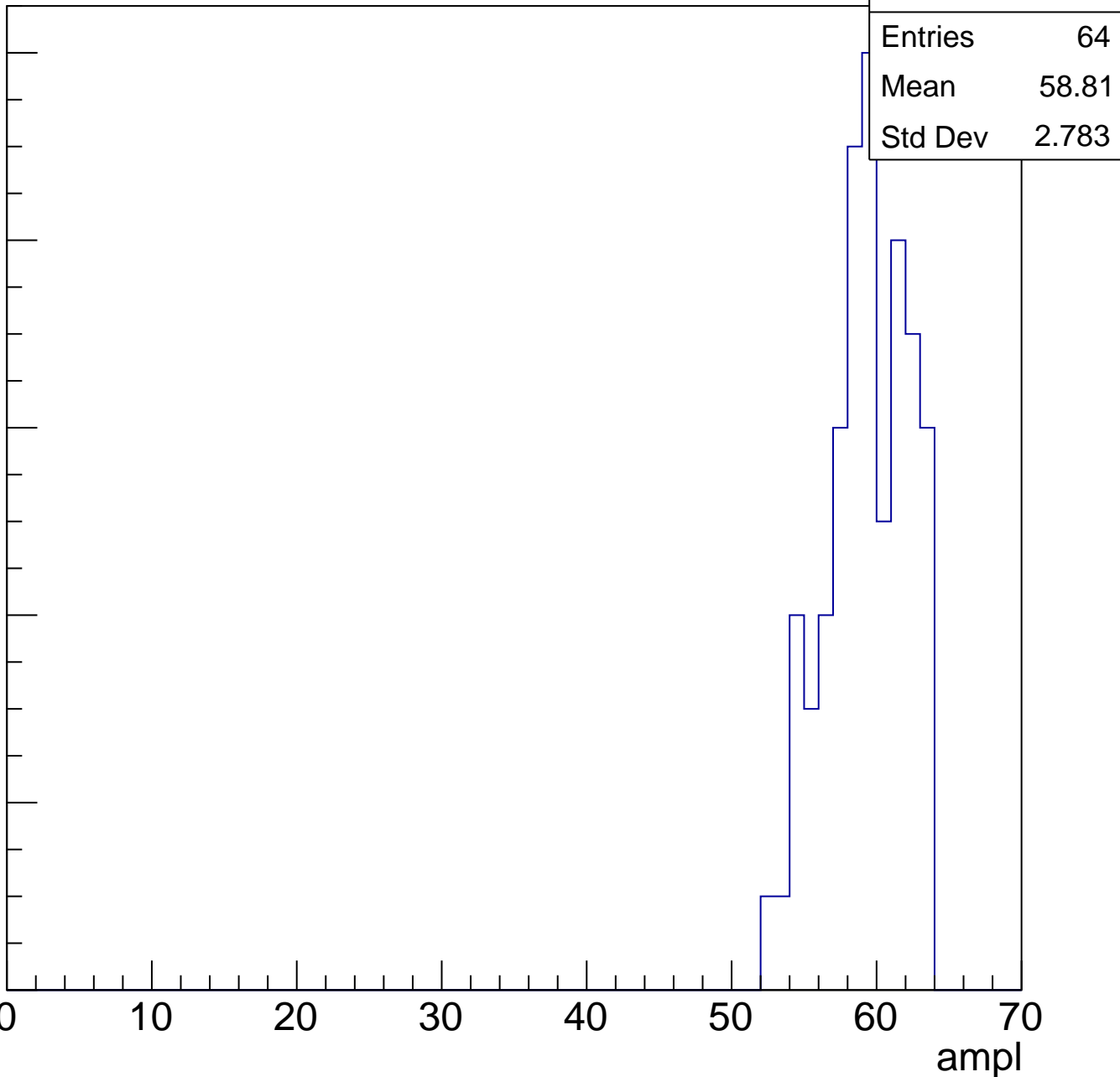
40

50

60

70

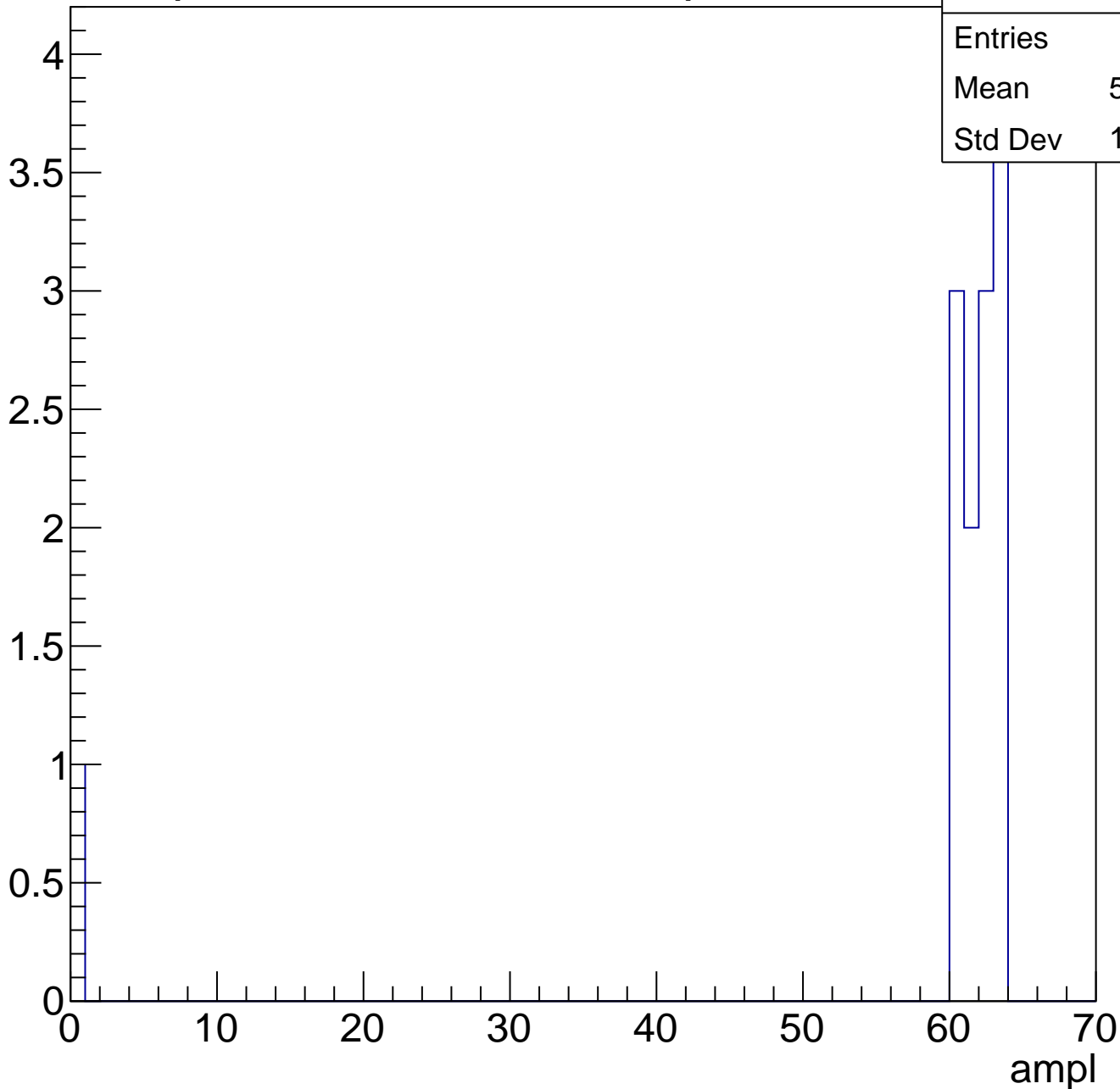
ampl



# B0L000S, U19-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L000S, U19-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	29.55
Std Dev	3.771

**Gaus mean : 29.8284**

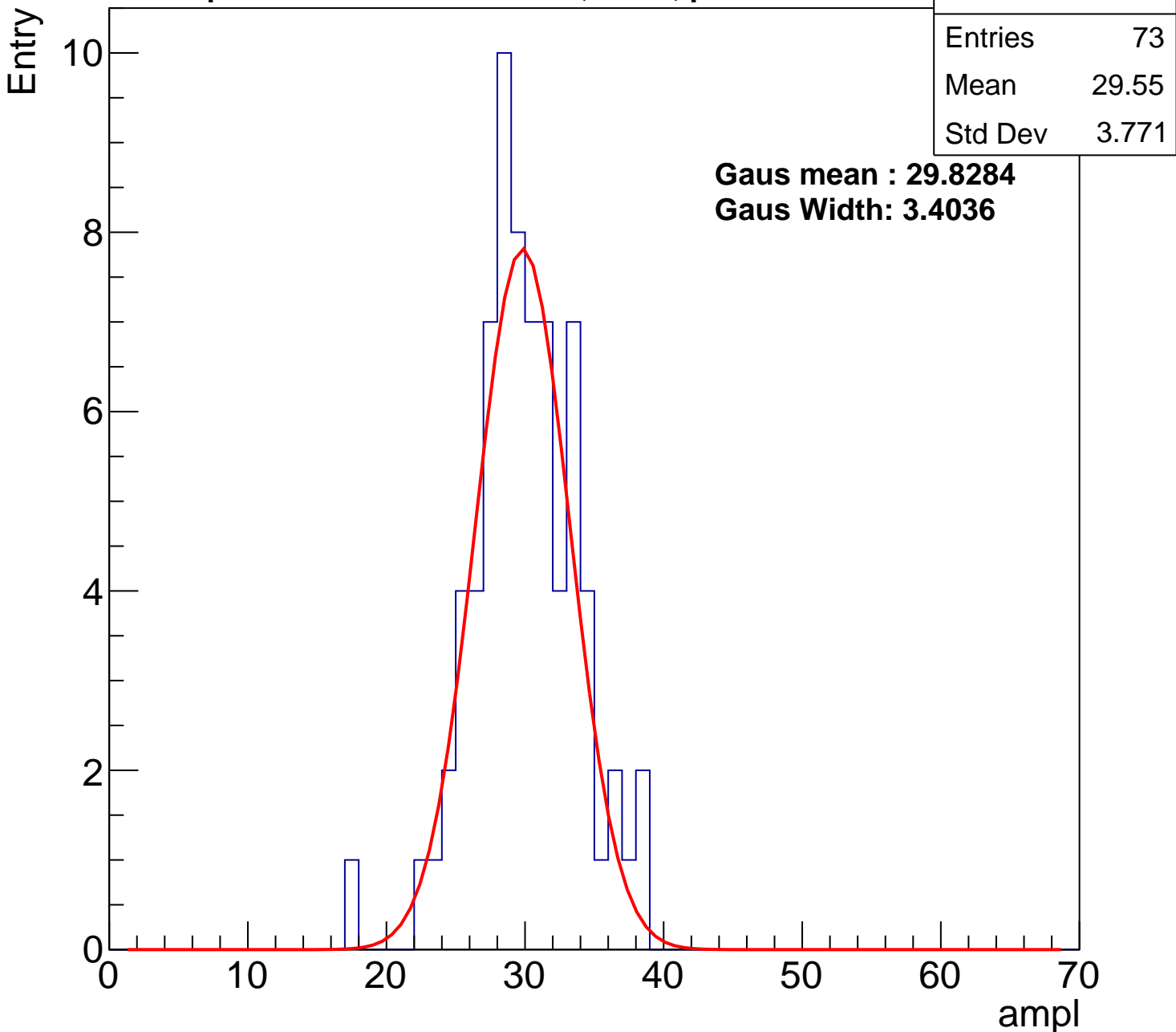
**Gaus Width: 3.4036**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch73, adc1

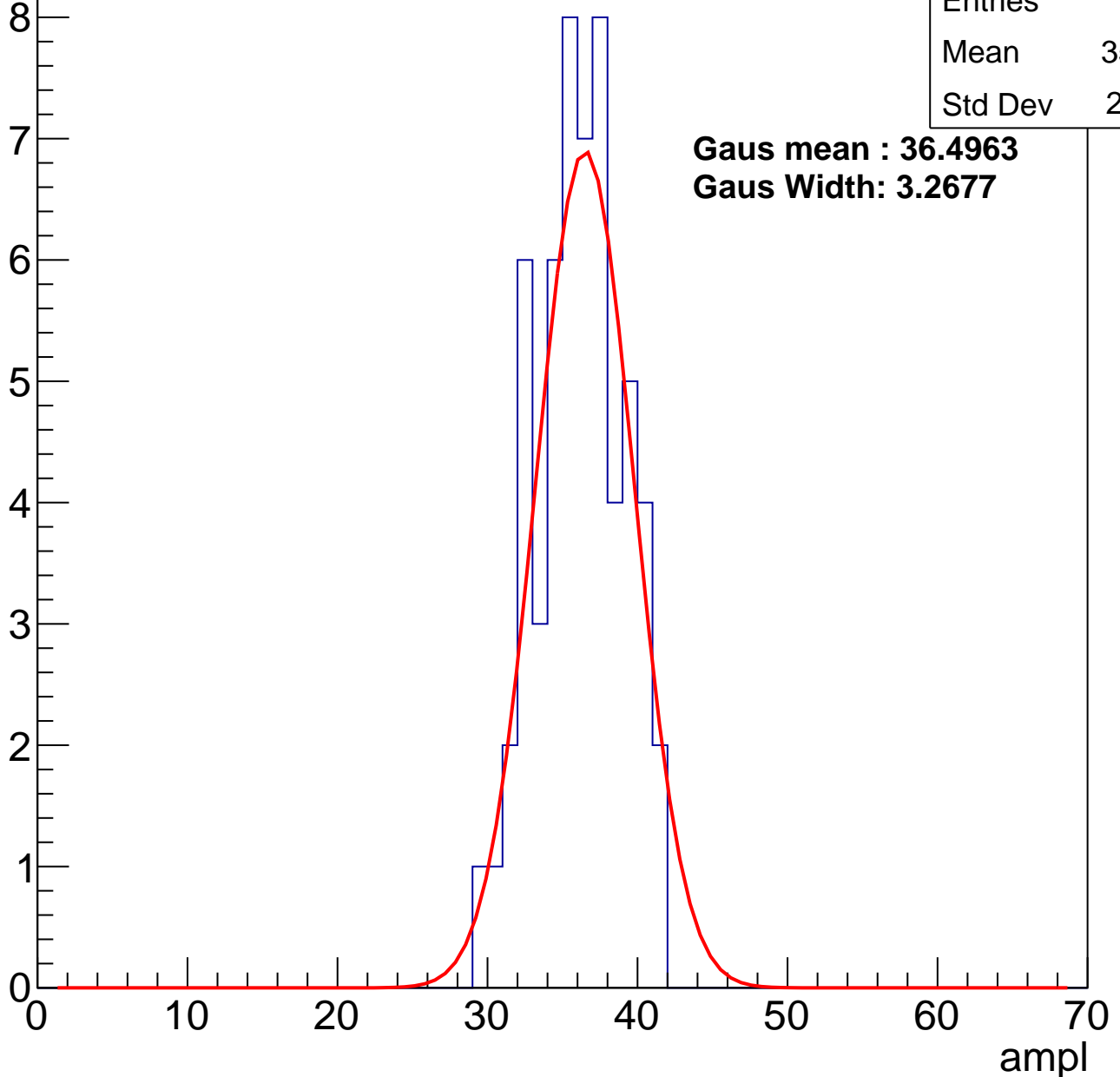
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.67
Std Dev	2.861

**Gaus mean : 36.4963**

**Gaus Width: 3.2677**



# B0L000S, U19-ch73, adc2

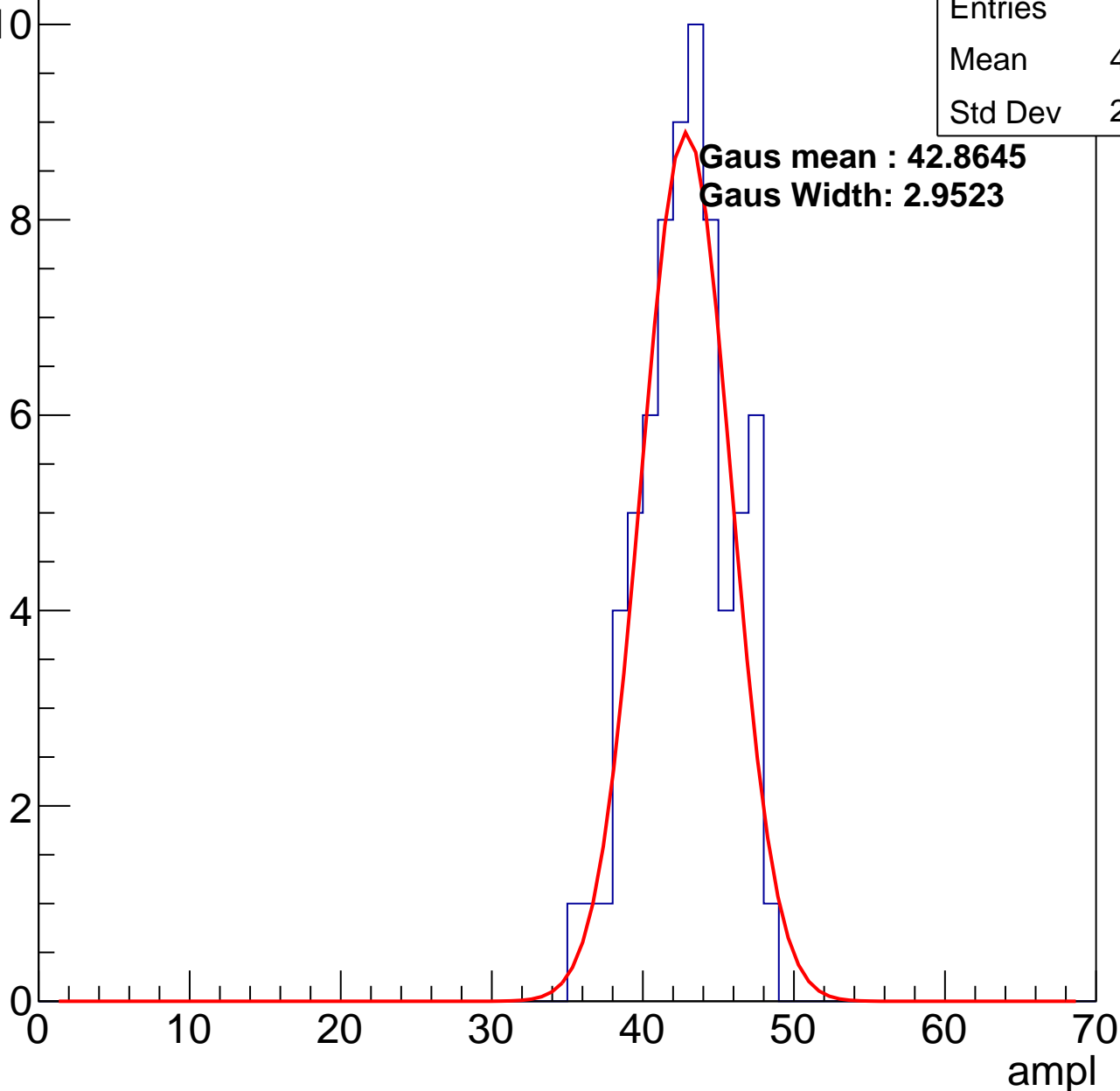
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.36
Std Dev	2.914

**Gaus mean : 42.8645**

**Gaus Width: 2.9523**

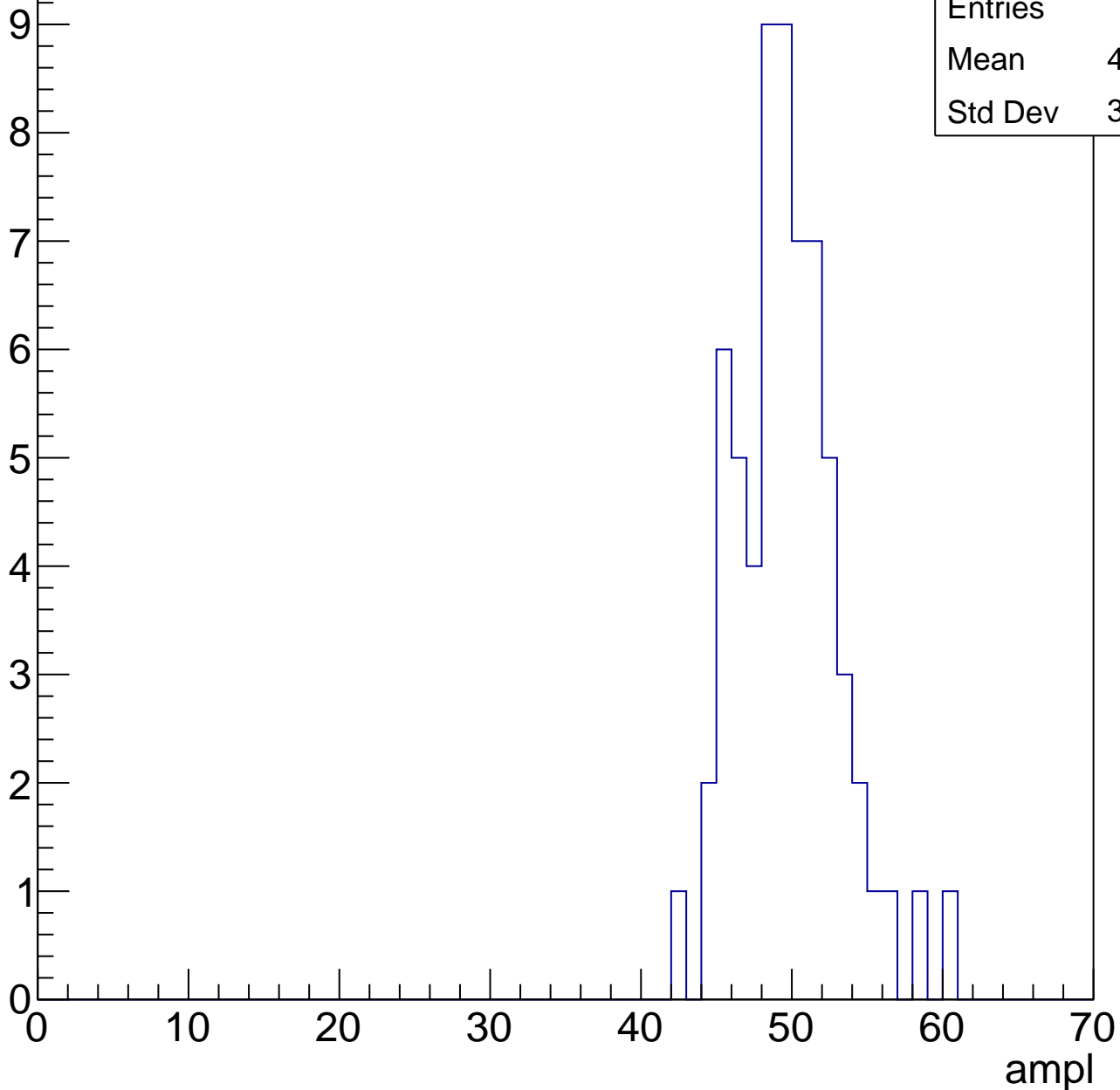


# B0L000S, U19-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.28
Std Dev	3.366

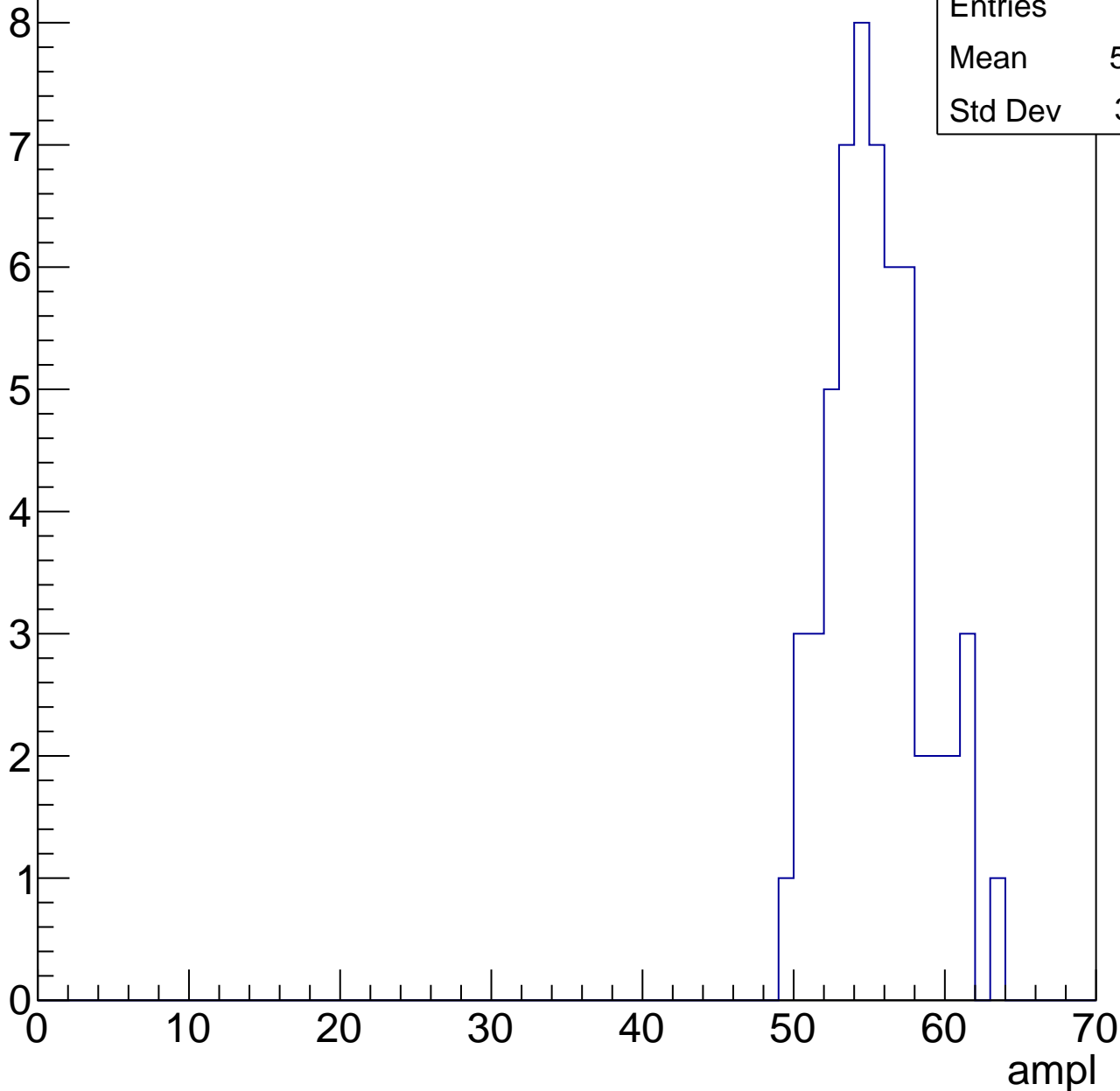


# B0L000S, U19-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

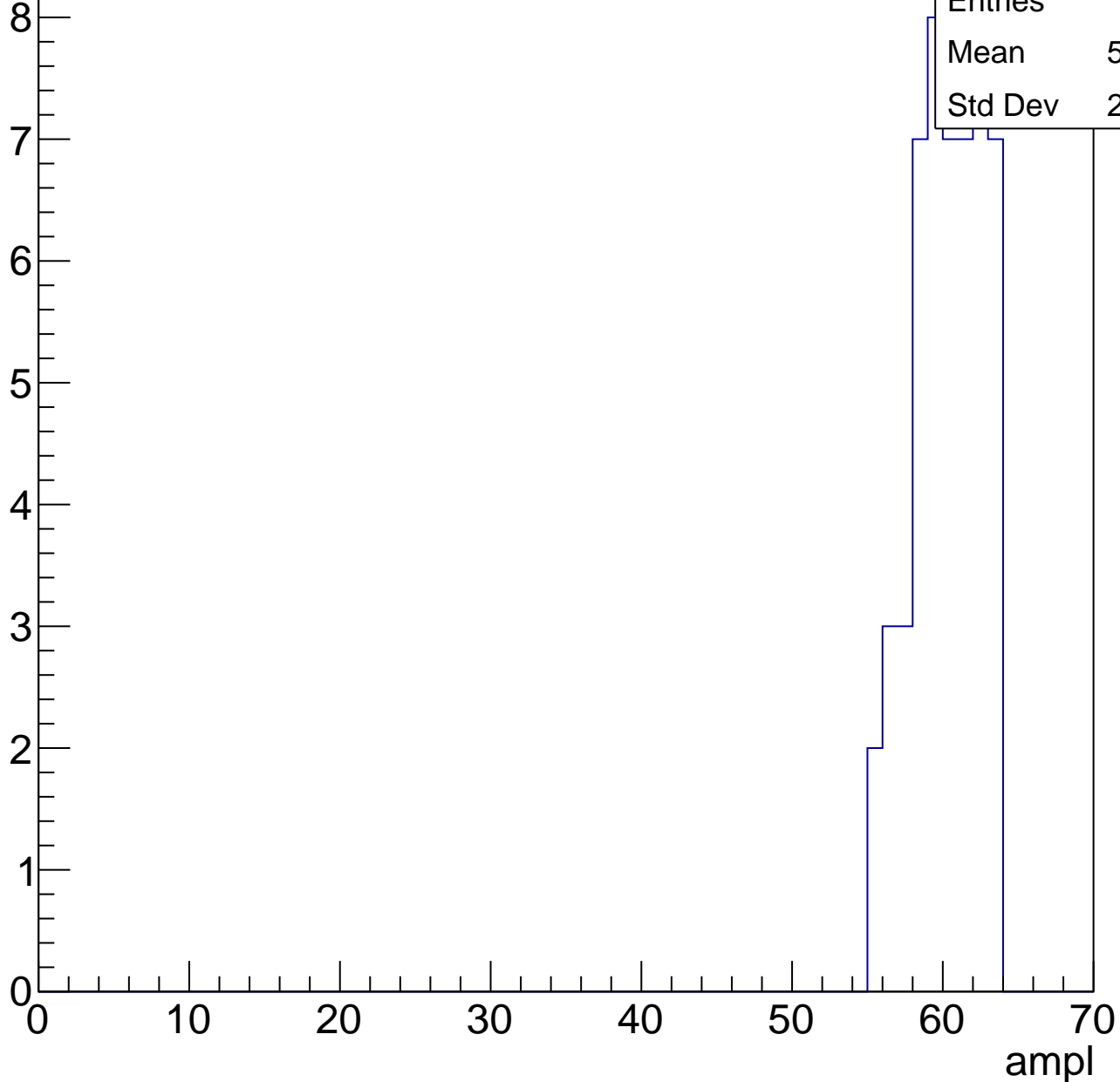
Entries	56
Mean	54.96
Std Dev	3.111



# B0L000S, U19-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

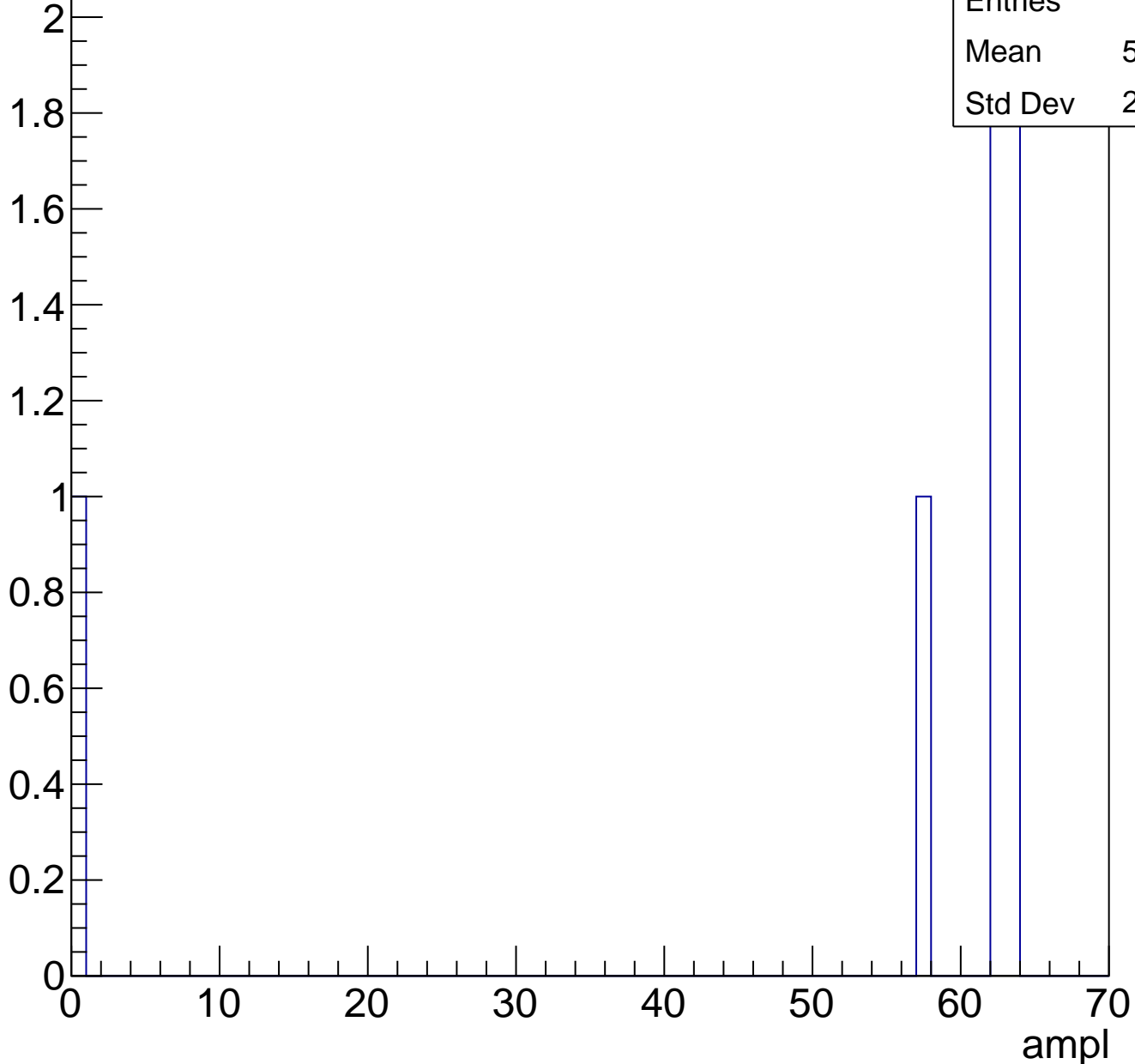
Entry



# B0L000S, U19-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	6
Mean	51.17
Std Dev	22.97



# B0L000S, U19-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch74, adc0

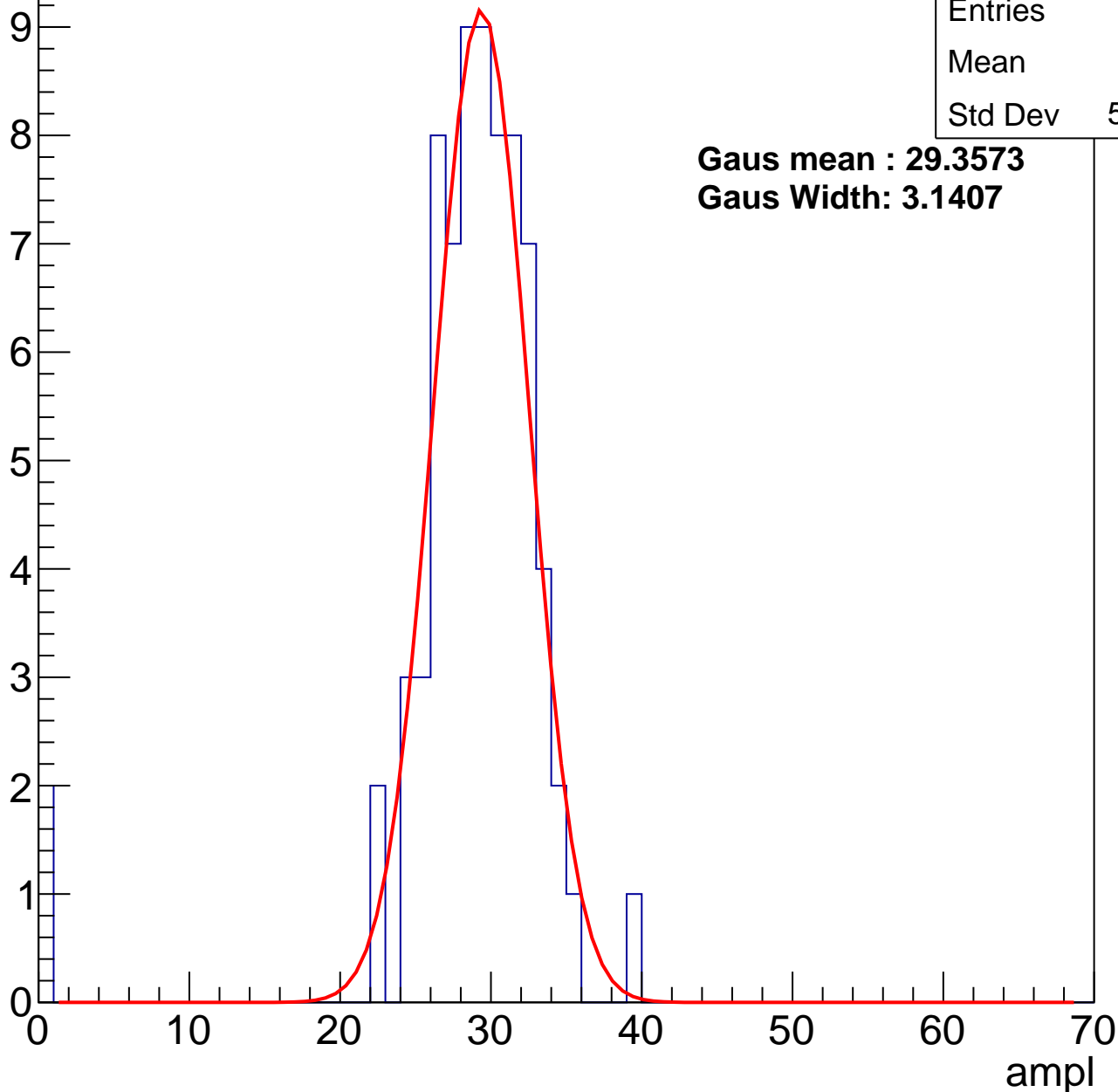
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	28.2
Std Dev	5.597

**Gaus mean : 29.3573**

**Gaus Width: 3.1407**



# B0L000S, U19-ch74, adc1

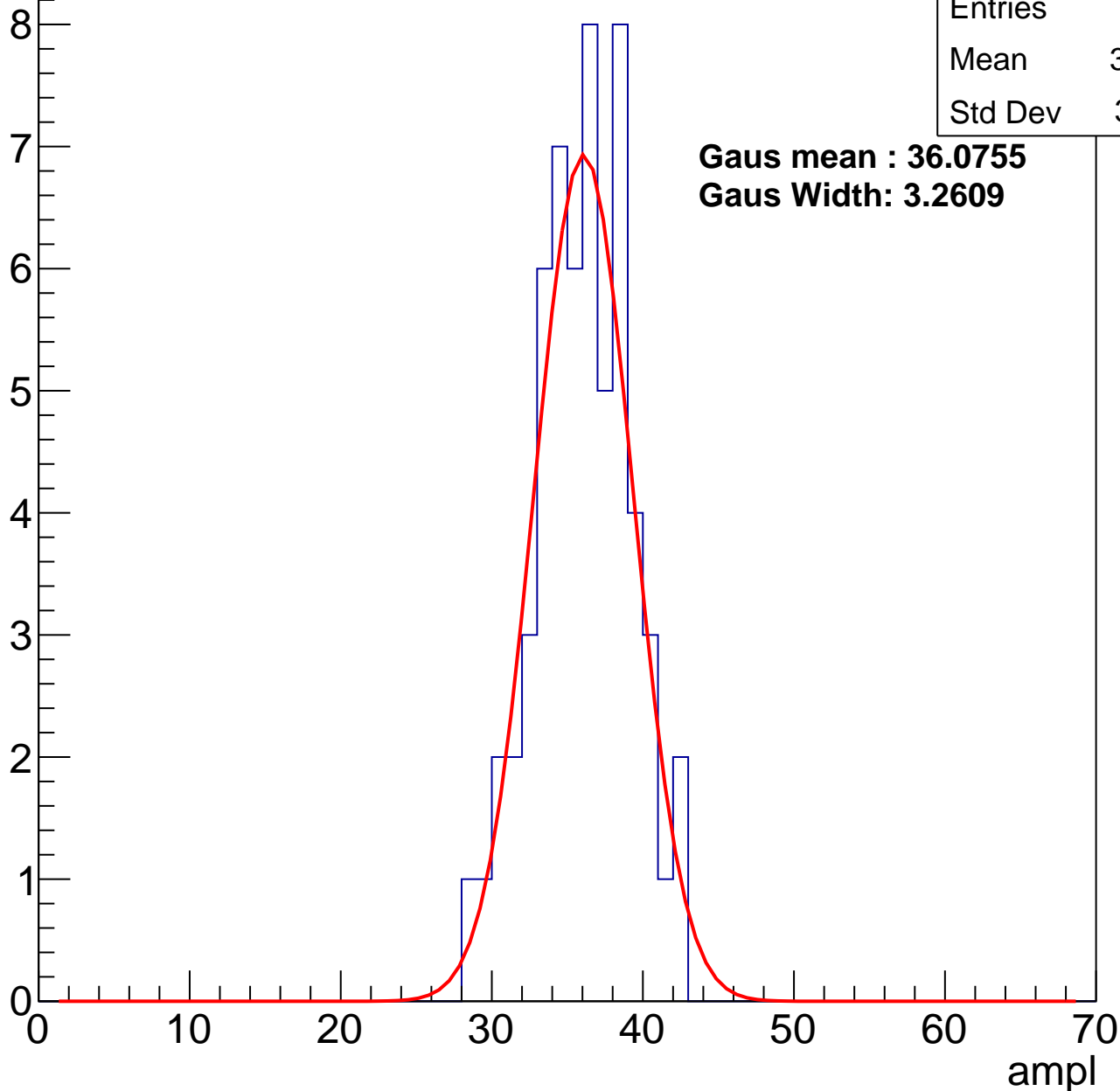
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	35.58
Std Dev	3.131

**Gaus mean : 36.0755**

**Gaus Width: 3.2609**



# B0L000S, U19-ch74, adc2

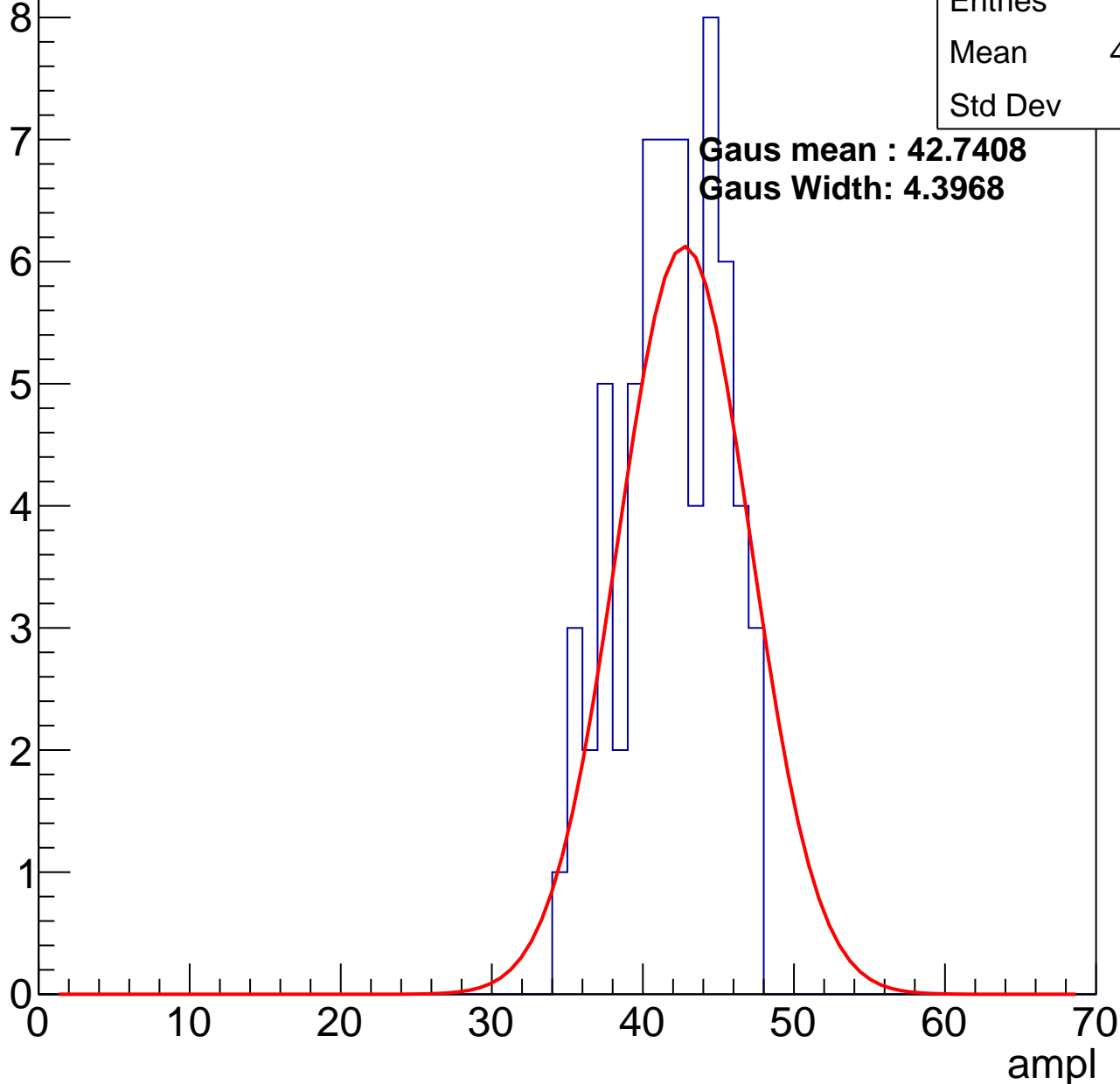
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.36
Std Dev	3.36

**Gaus mean : 42.7408**

**Gaus Width: 4.3968**

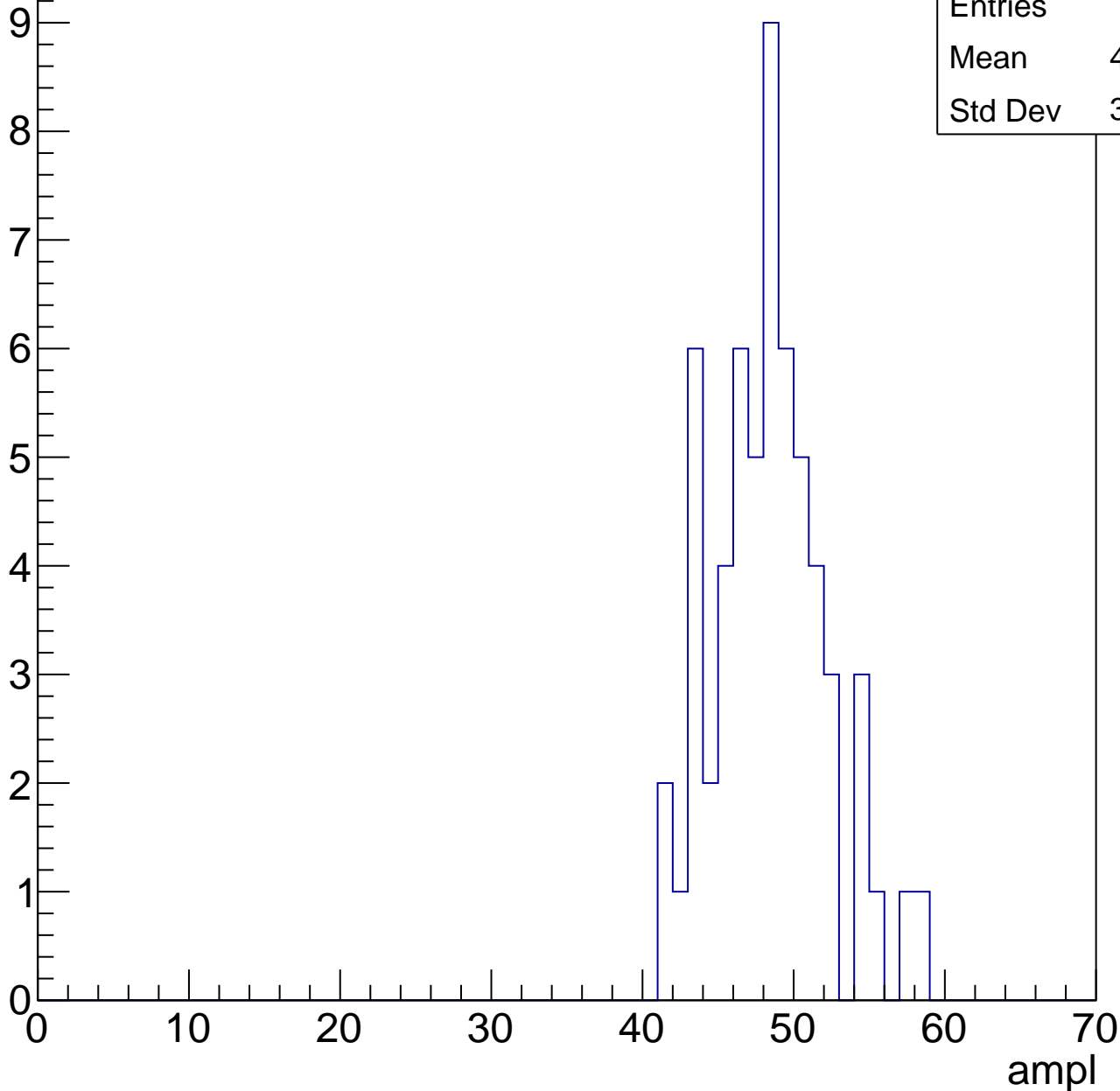


# B0L000S, U19-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

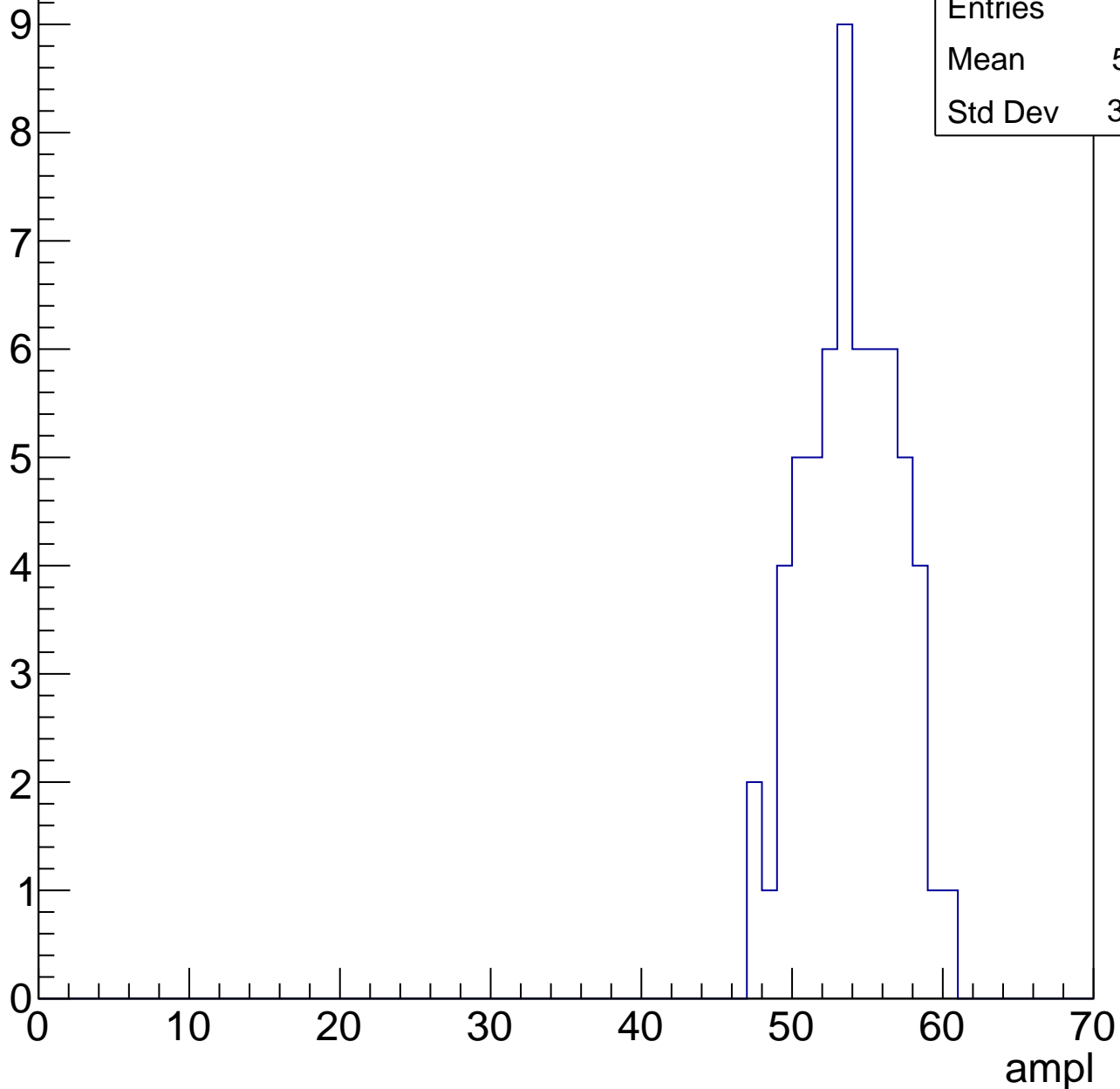
Entries	59
Mean	47.95
Std Dev	3.748



# B0L000S, U19-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

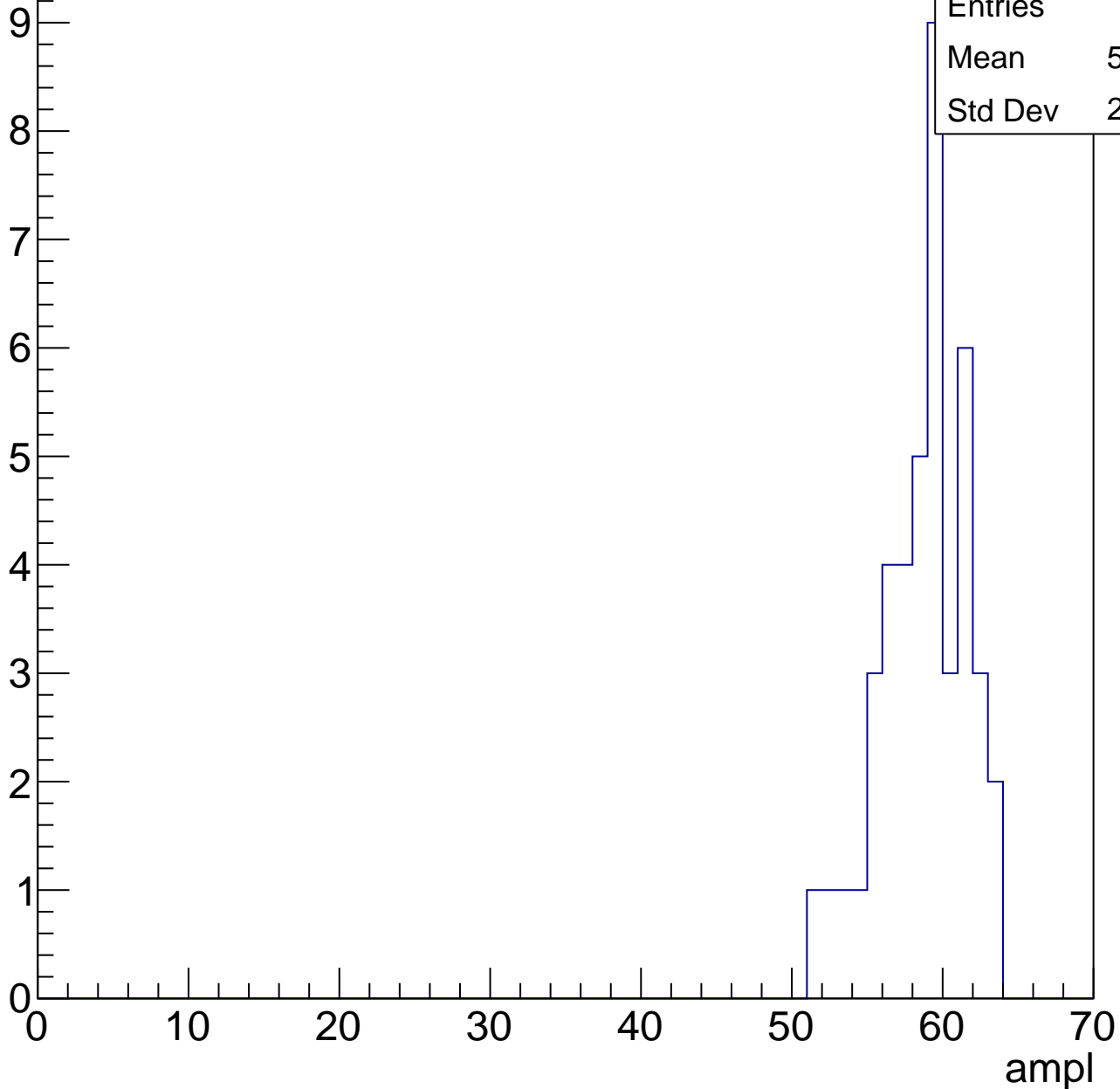
Entry



# B0L000S, U19-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



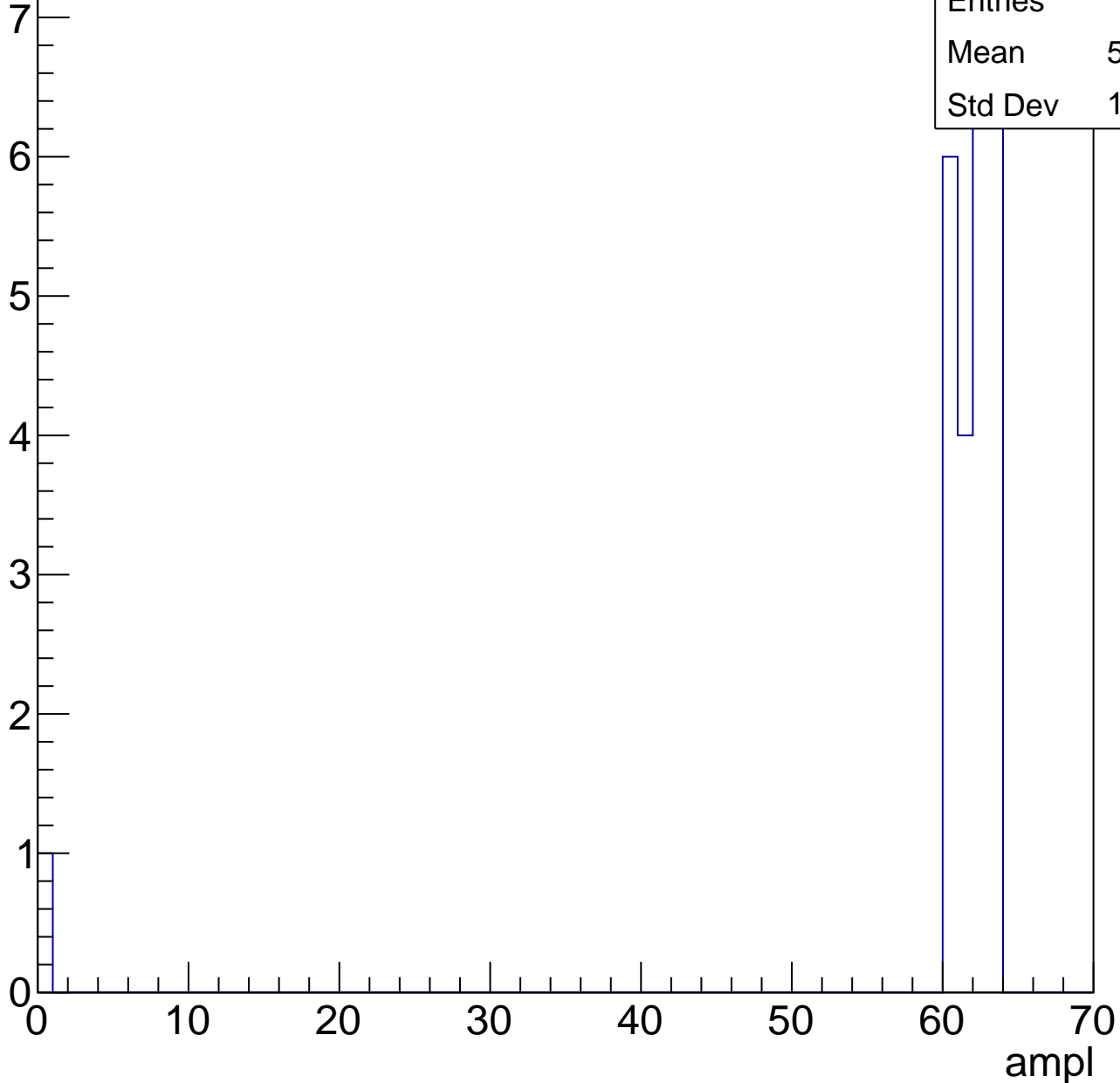
Entries	43
Mean	58.28
Std Dev	2.823

# B0L000S, U19-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	25
Mean	59.16
Std Dev	12.13





# B0L000S, U19-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	29.66
Std Dev	4.844

**Gaus mean : 30.7123**

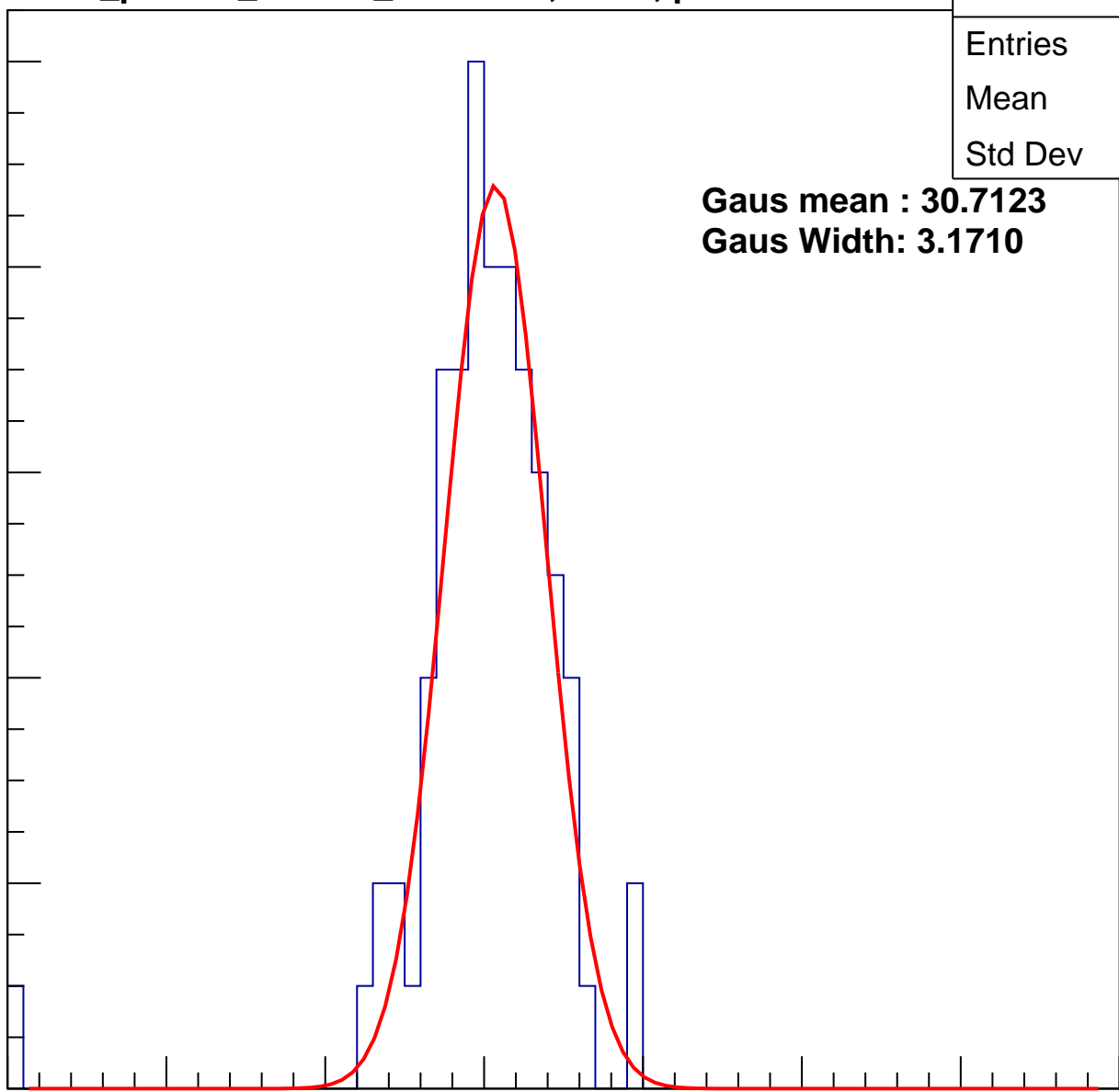
**Gaus Width: 3.1710**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch75, adc1

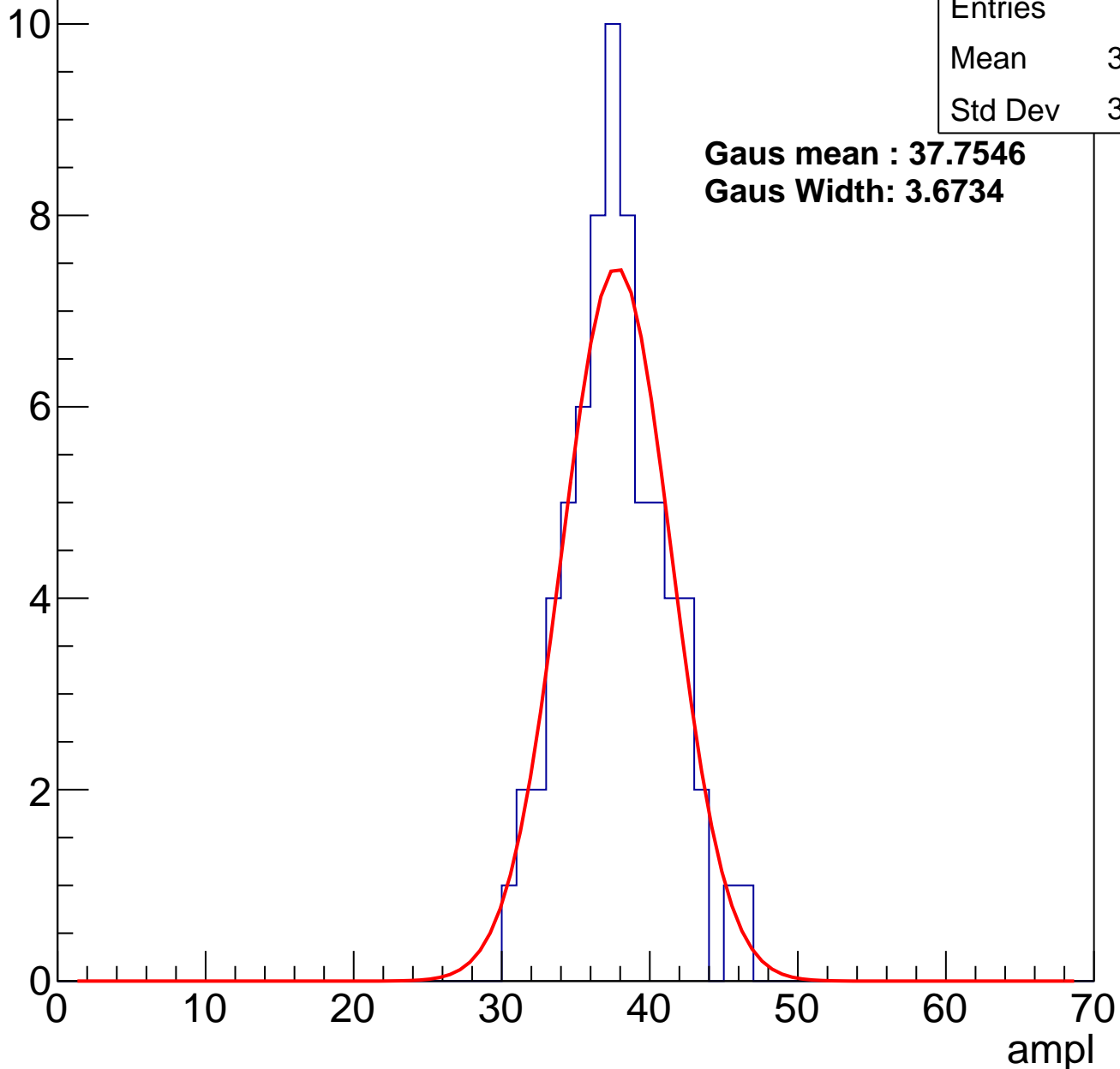
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	37.26
Std Dev	3.346

**Gaus mean : 37.7546**

**Gaus Width: 3.6734**

Entry



# B0L000S, U19-ch75, adc2

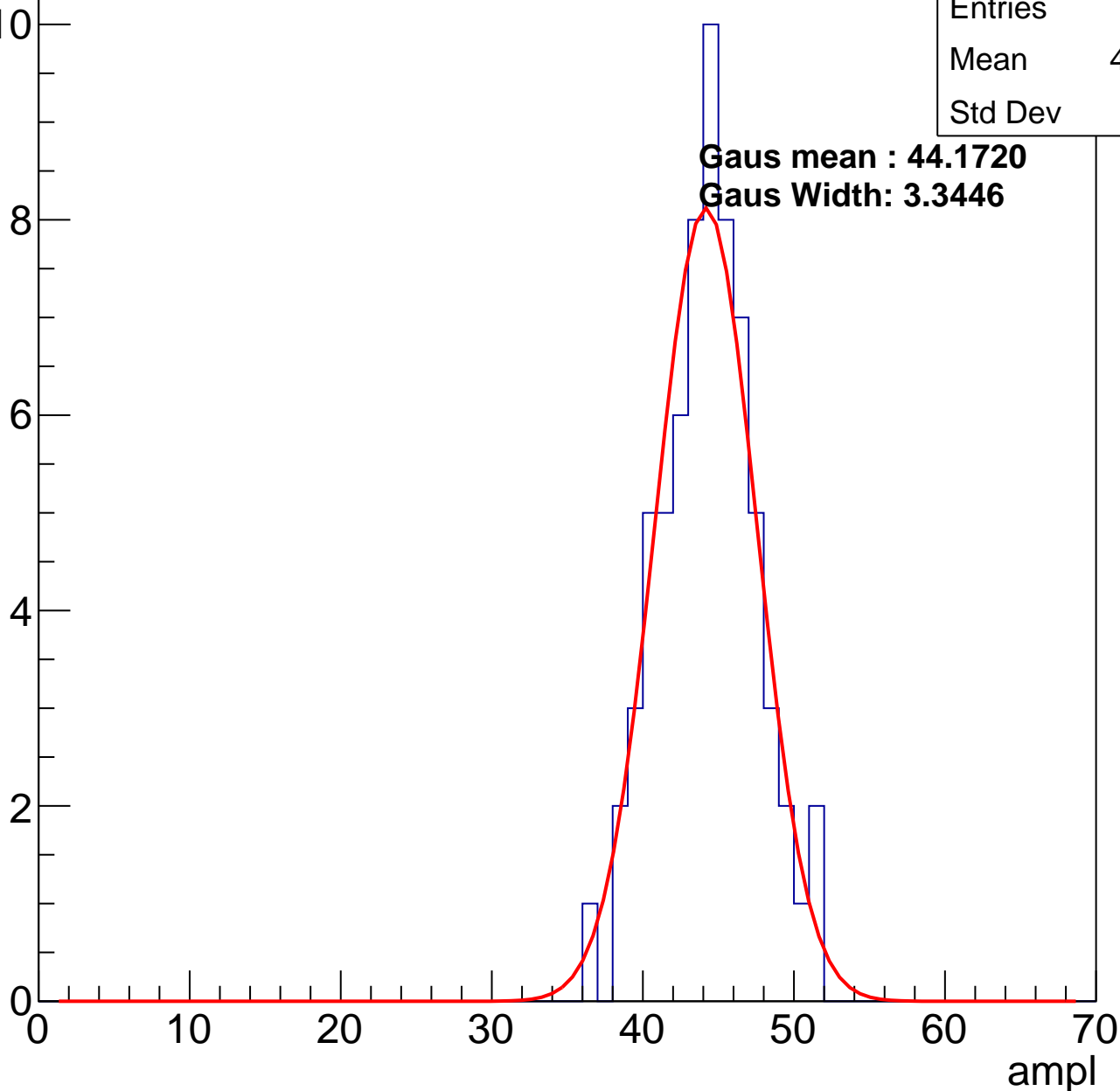
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.84
Std Dev	3.16

**Gaus mean : 44.1720**

**Gaus Width: 3.3446**

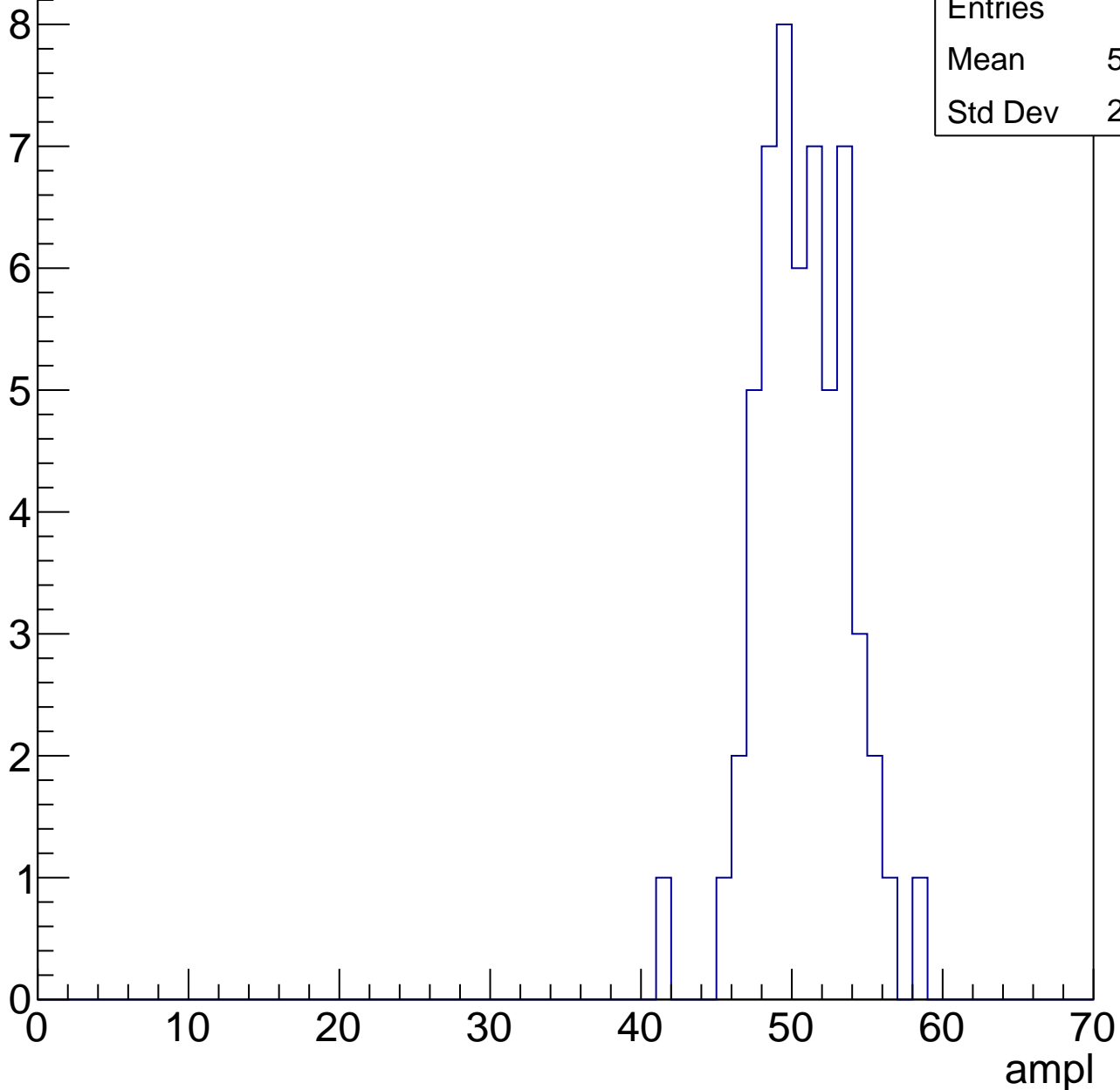


# B0L000S, U19-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	50.27
Std Dev	2.997

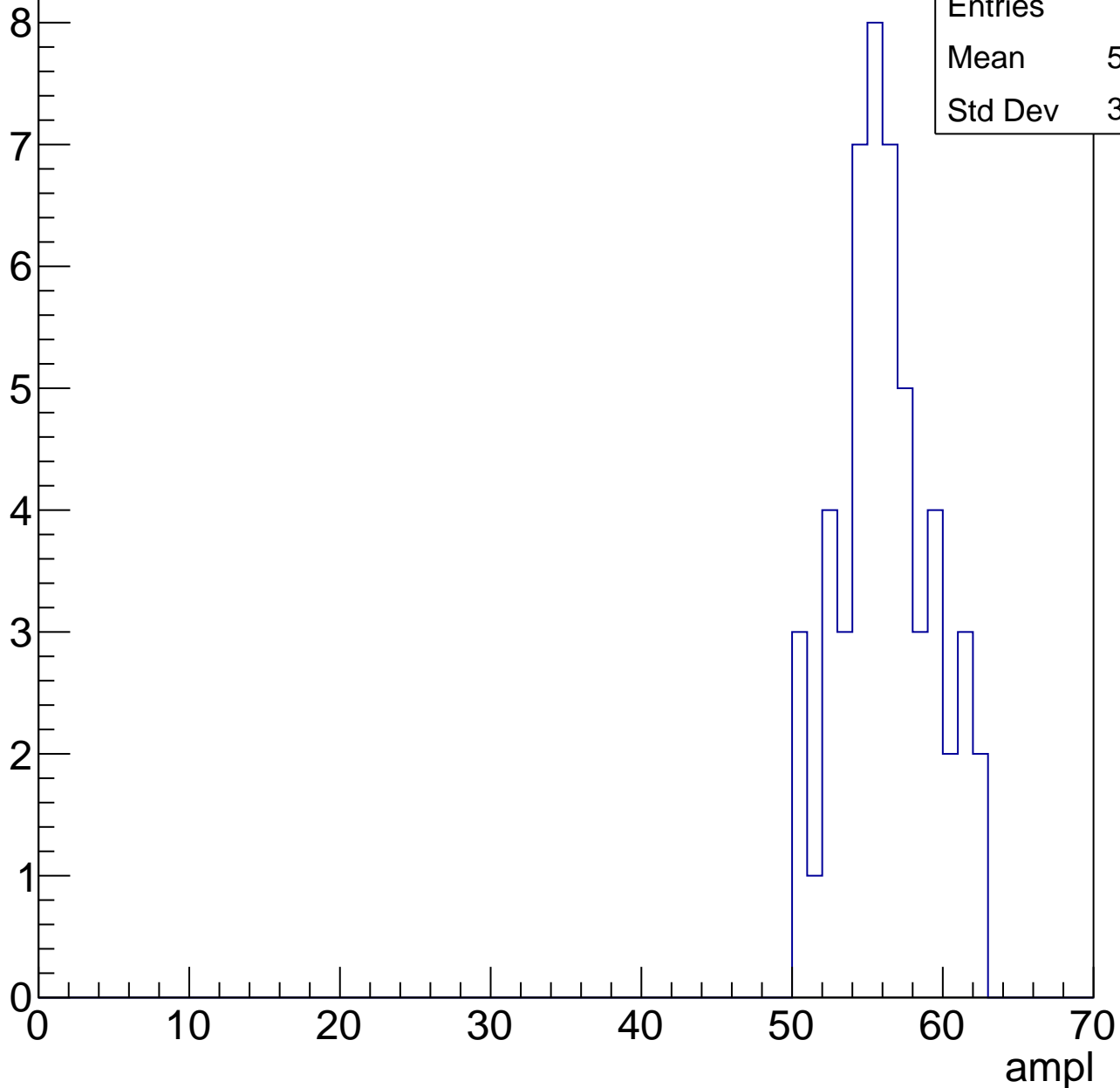


# B0L000S, U19-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

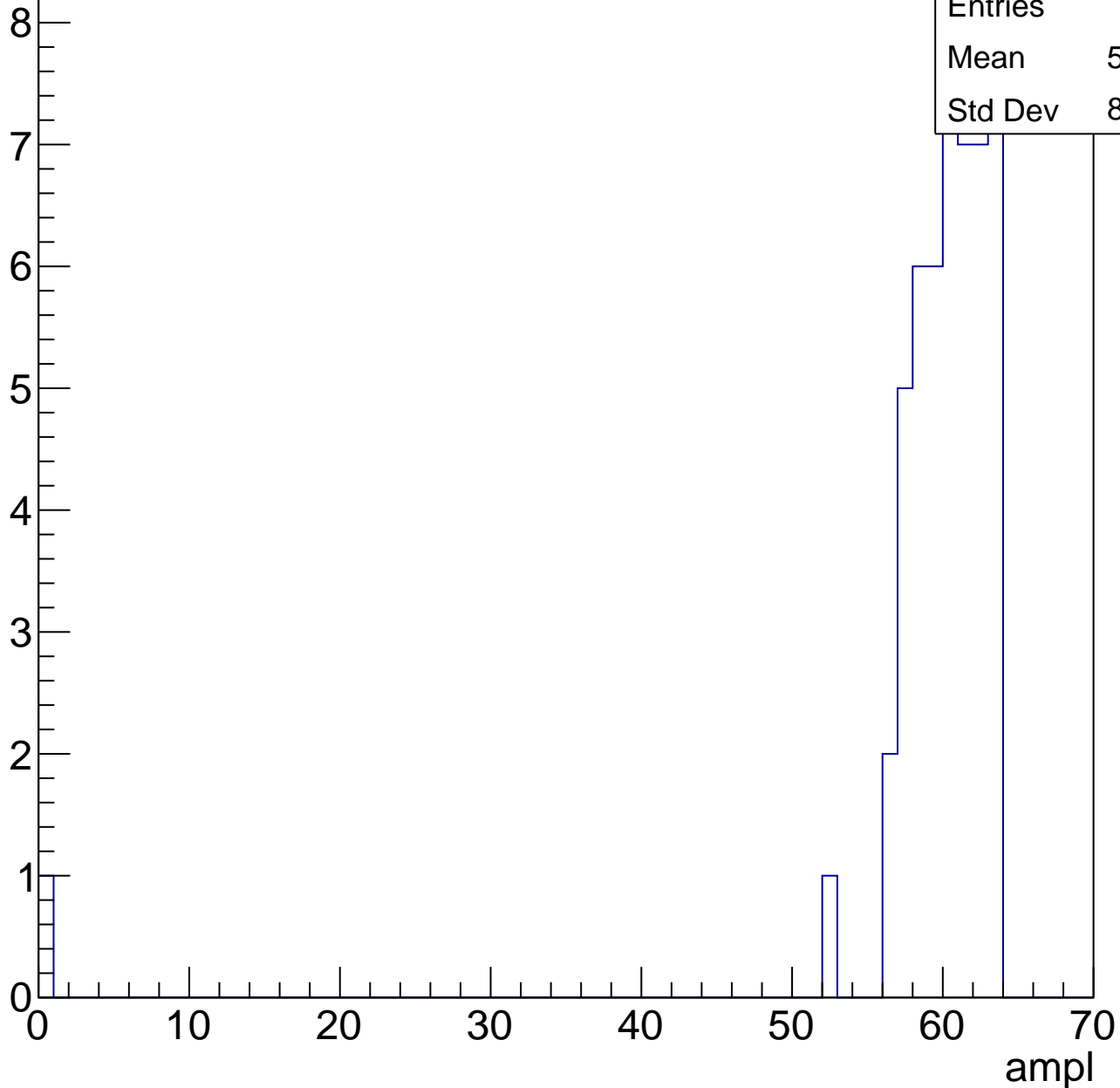
Entries	52
Mean	55.77
Std Dev	3.067



# B0L000S, U19-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	51
Mean	58.75
Std Dev	8.629

# B0L000S, U19-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U19-ch76, adc0

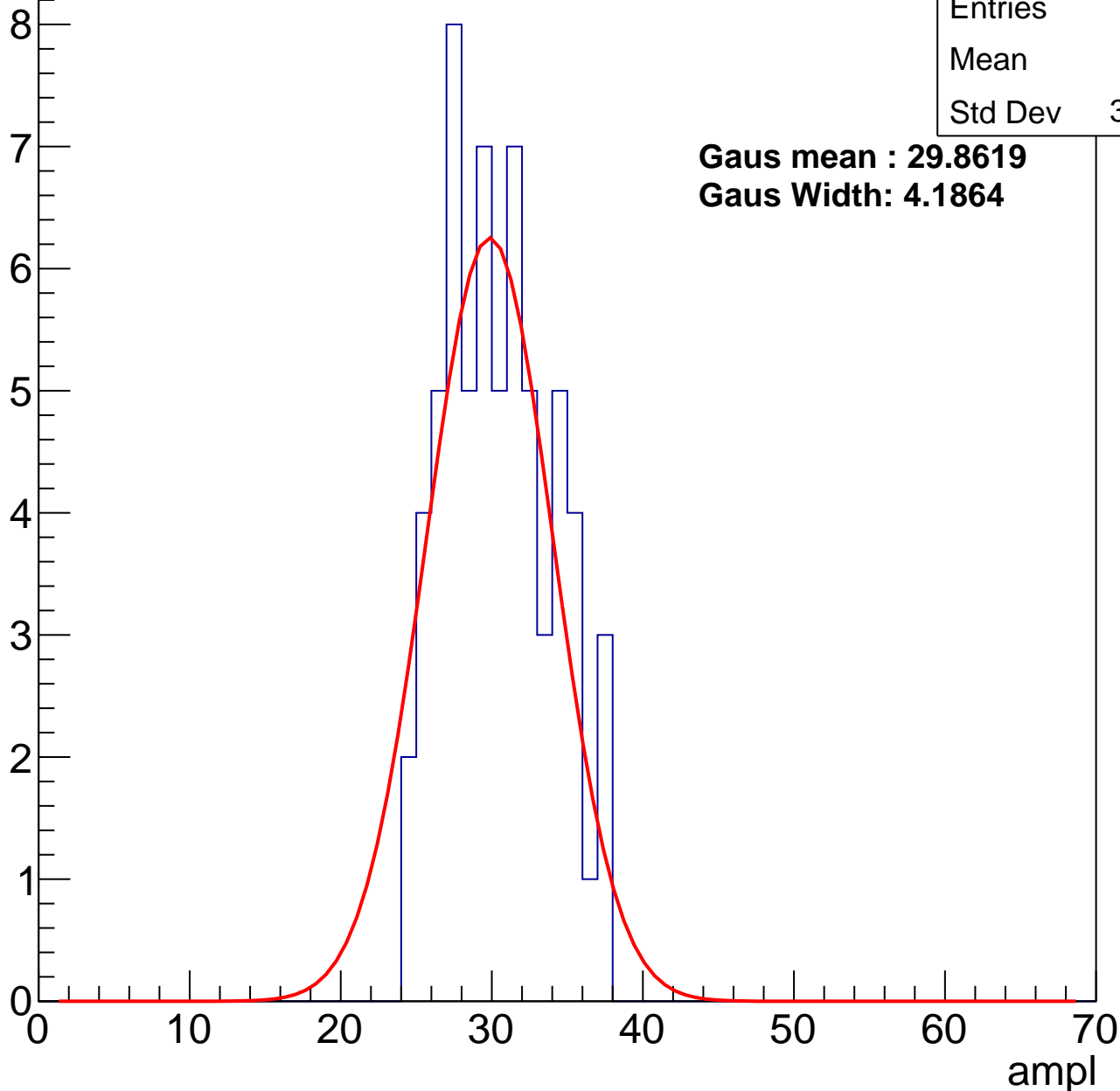
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30
Std Dev	3.464

**Gaus mean : 29.8619**

**Gaus Width: 4.1864**



# B0L000S, U19-ch76, adc1

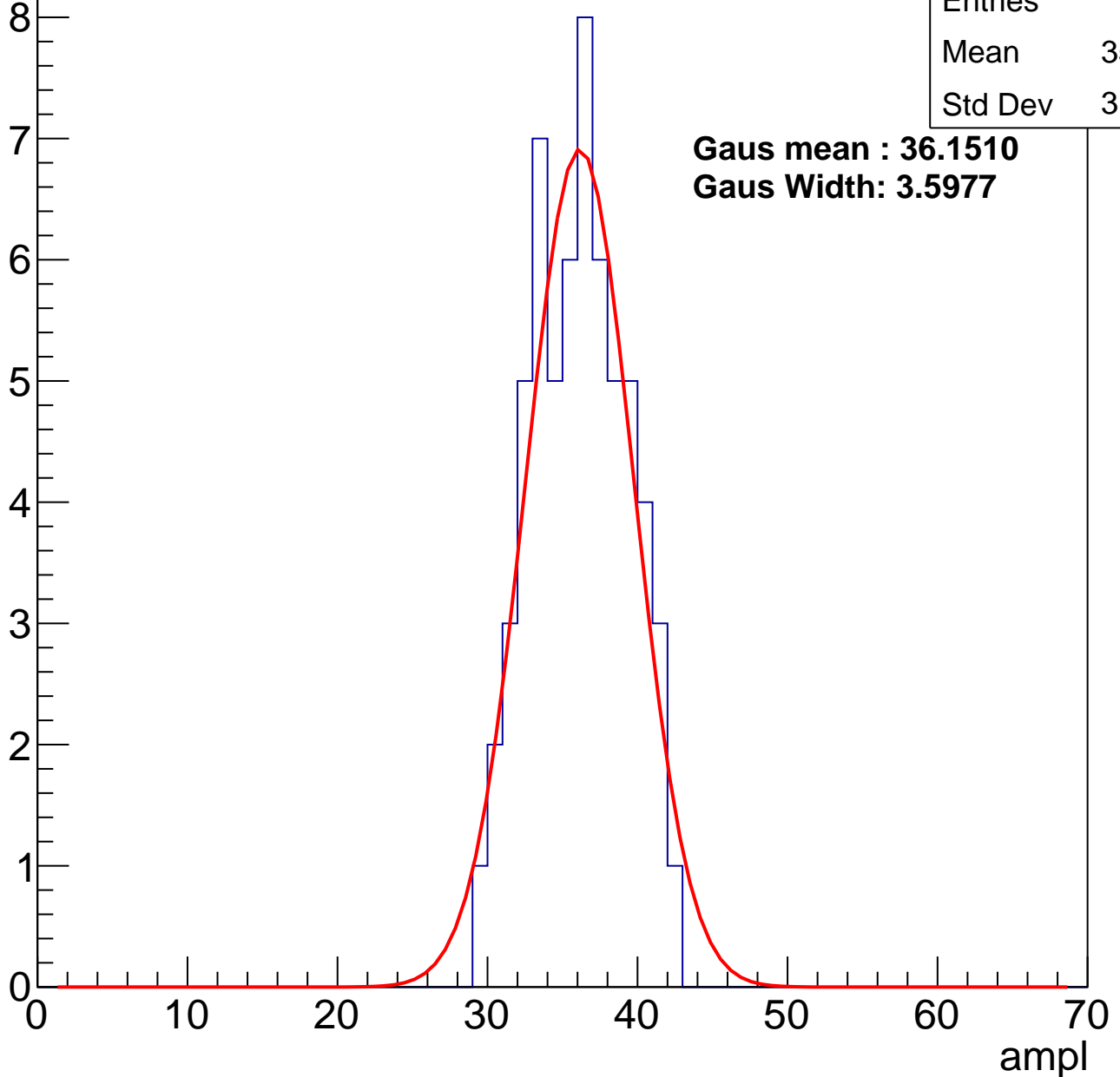
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.62
Std Dev	3.142

**Gaus mean : 36.1510**

**Gaus Width: 3.5977**



# B0L000S, U19-ch76, adc2

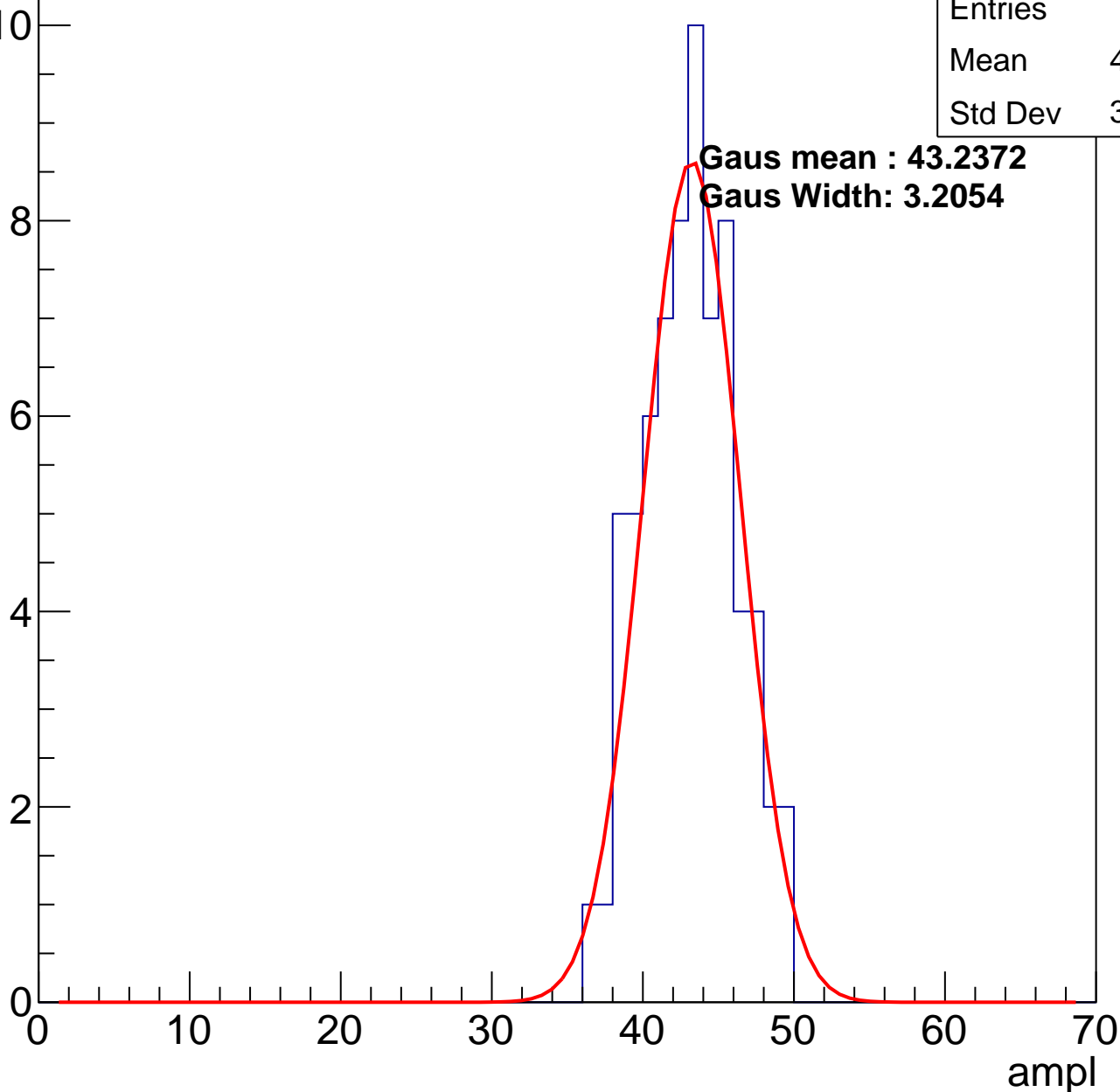
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.64
Std Dev	3.005

**Gaus mean : 43.2372**

**Gaus Width: 3.2054**

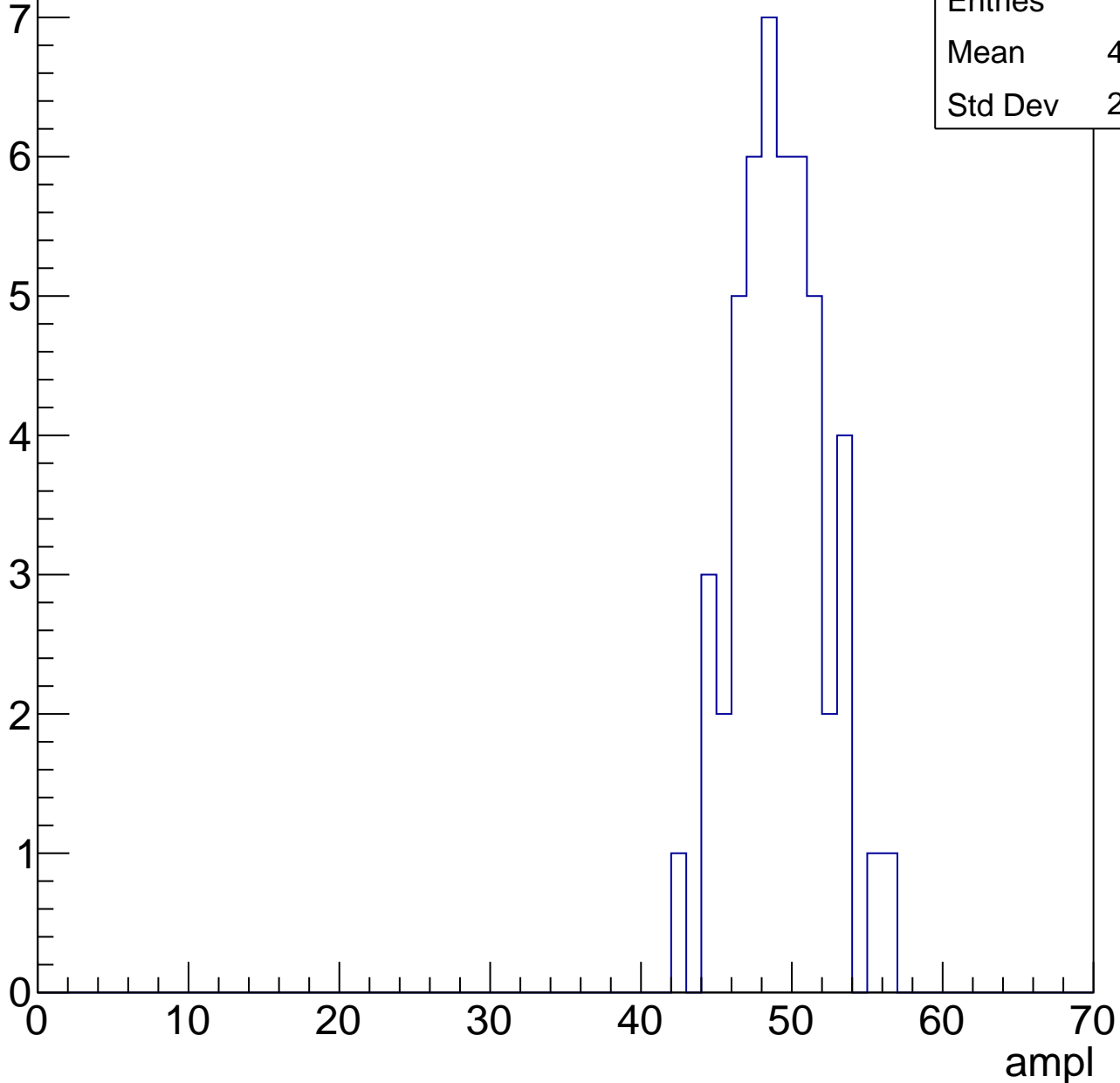


# B0L000S, U19-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	48.73
Std Dev	2.933

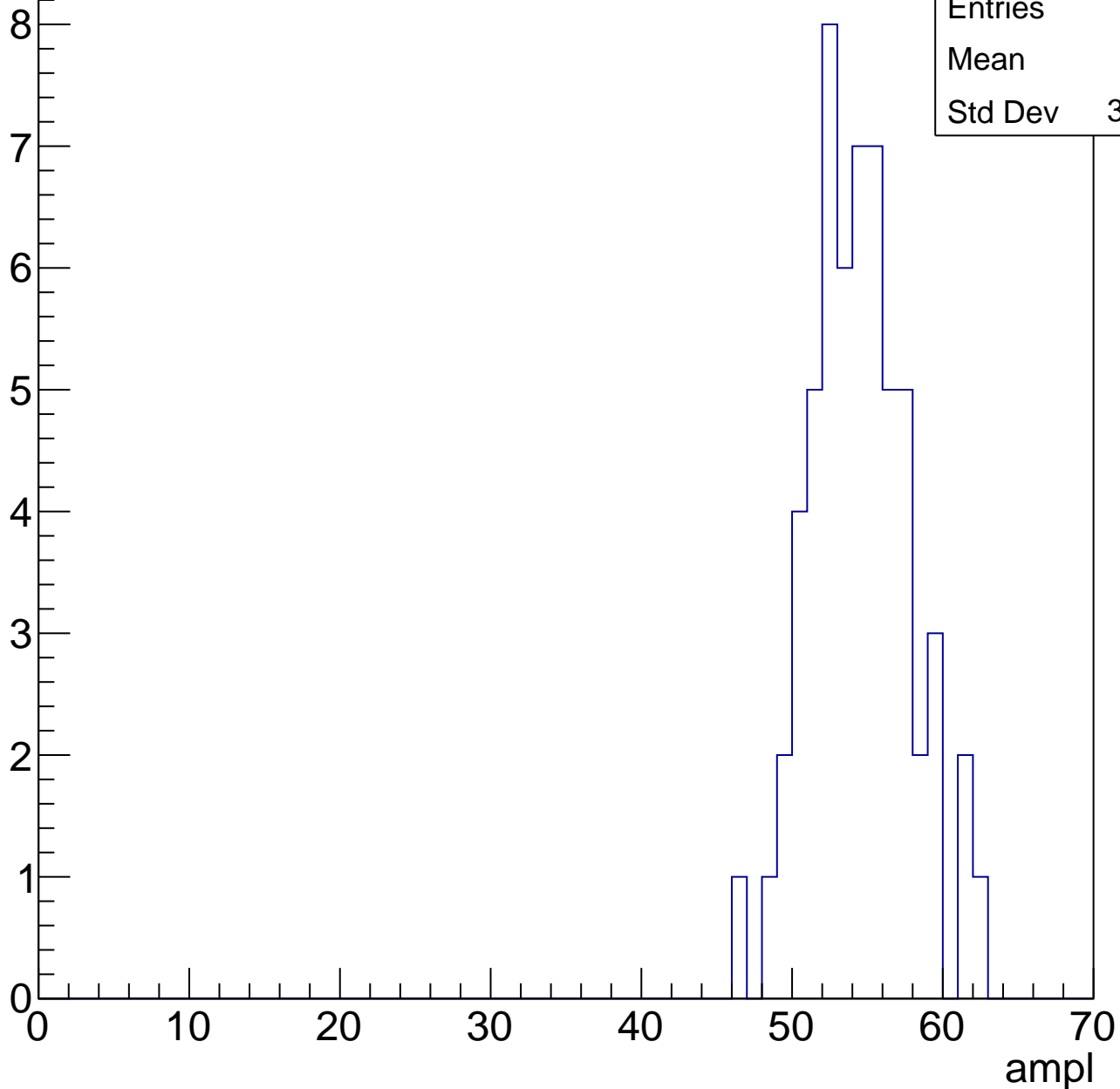


# B0L000S, U19-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54
Std Dev	3.288

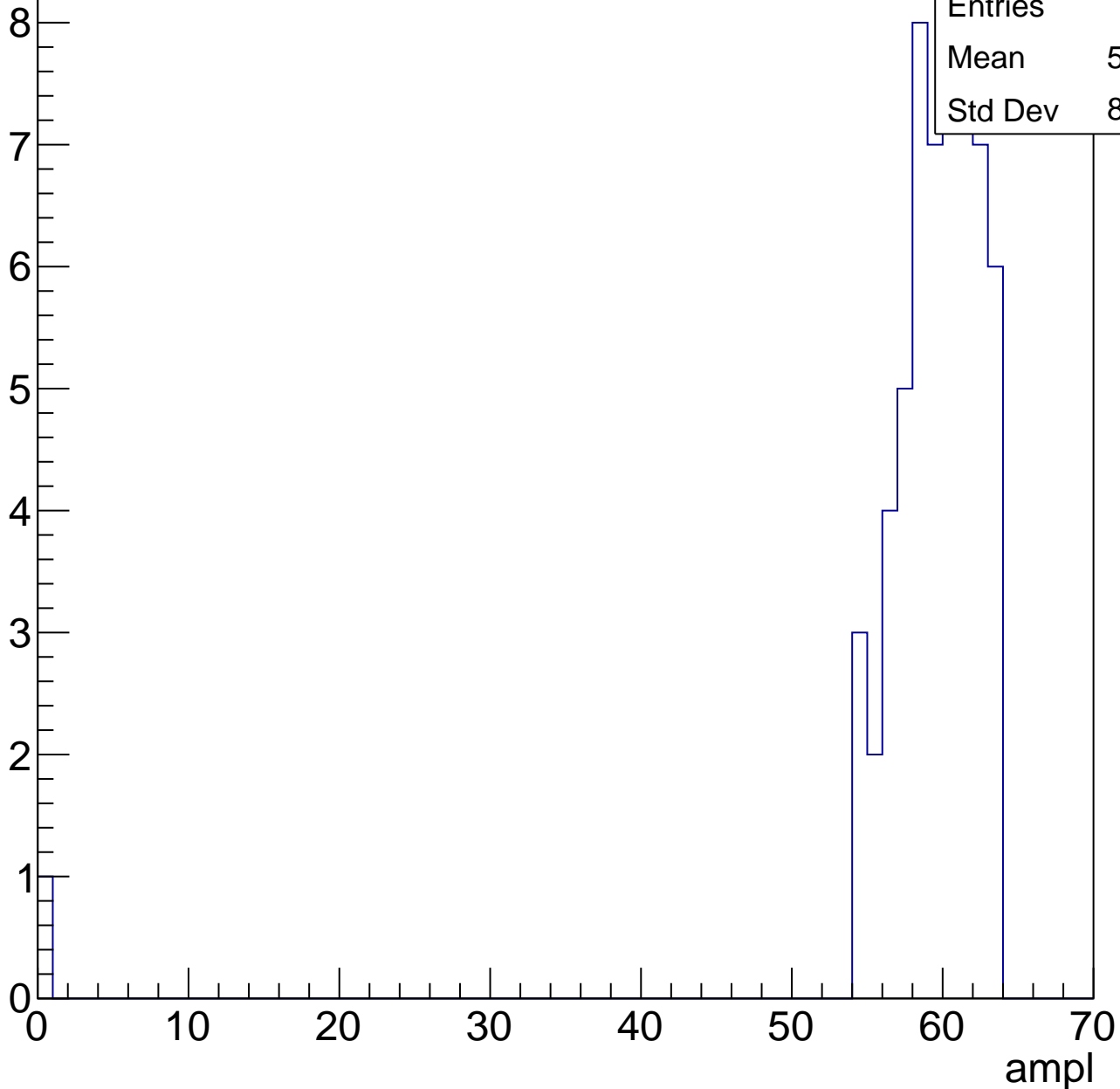


# B0L000S, U19-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

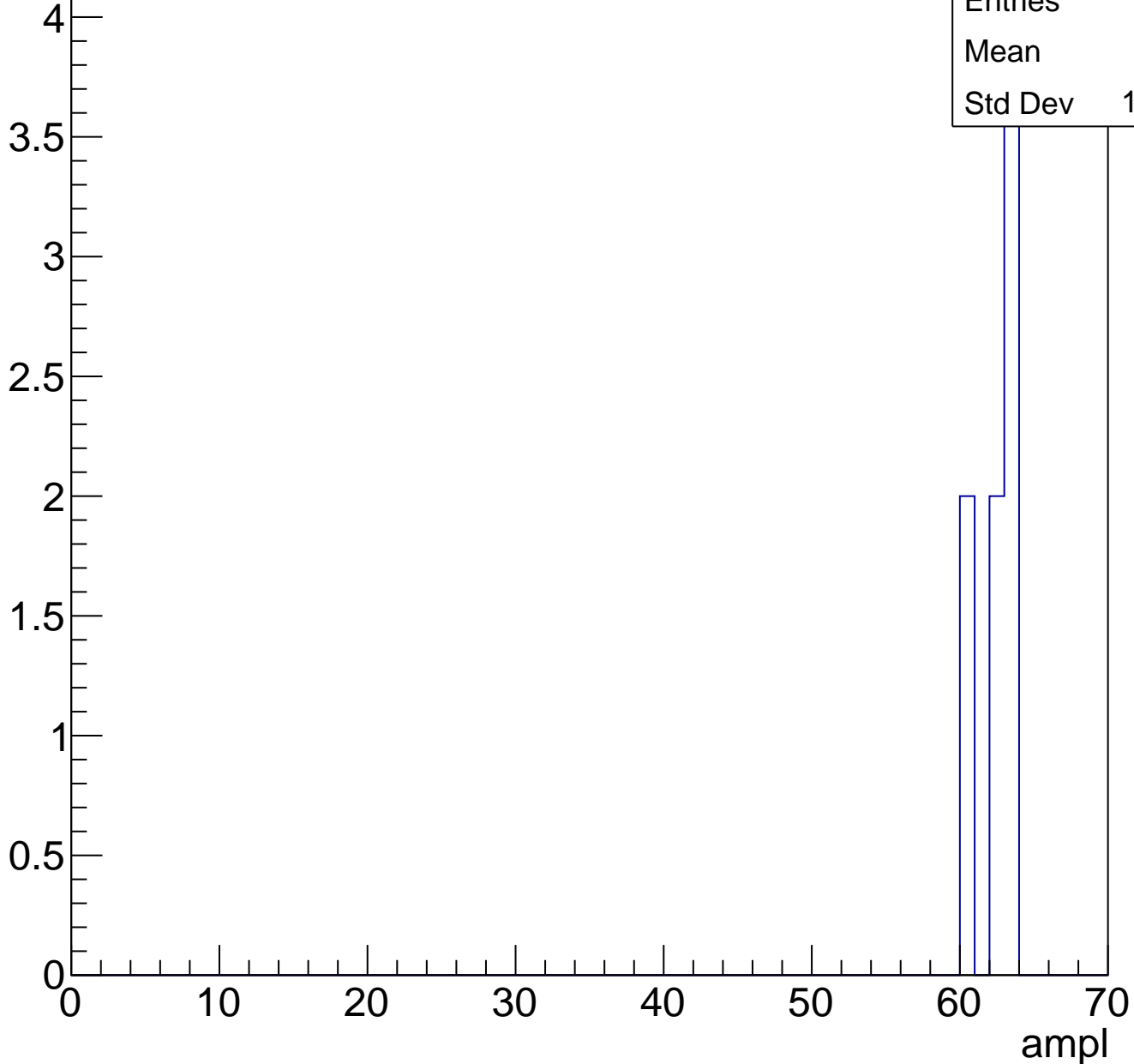
Entries	59
Mean	58.27
Std Dev	8.046



# B0L000S, U19-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch77, adc0

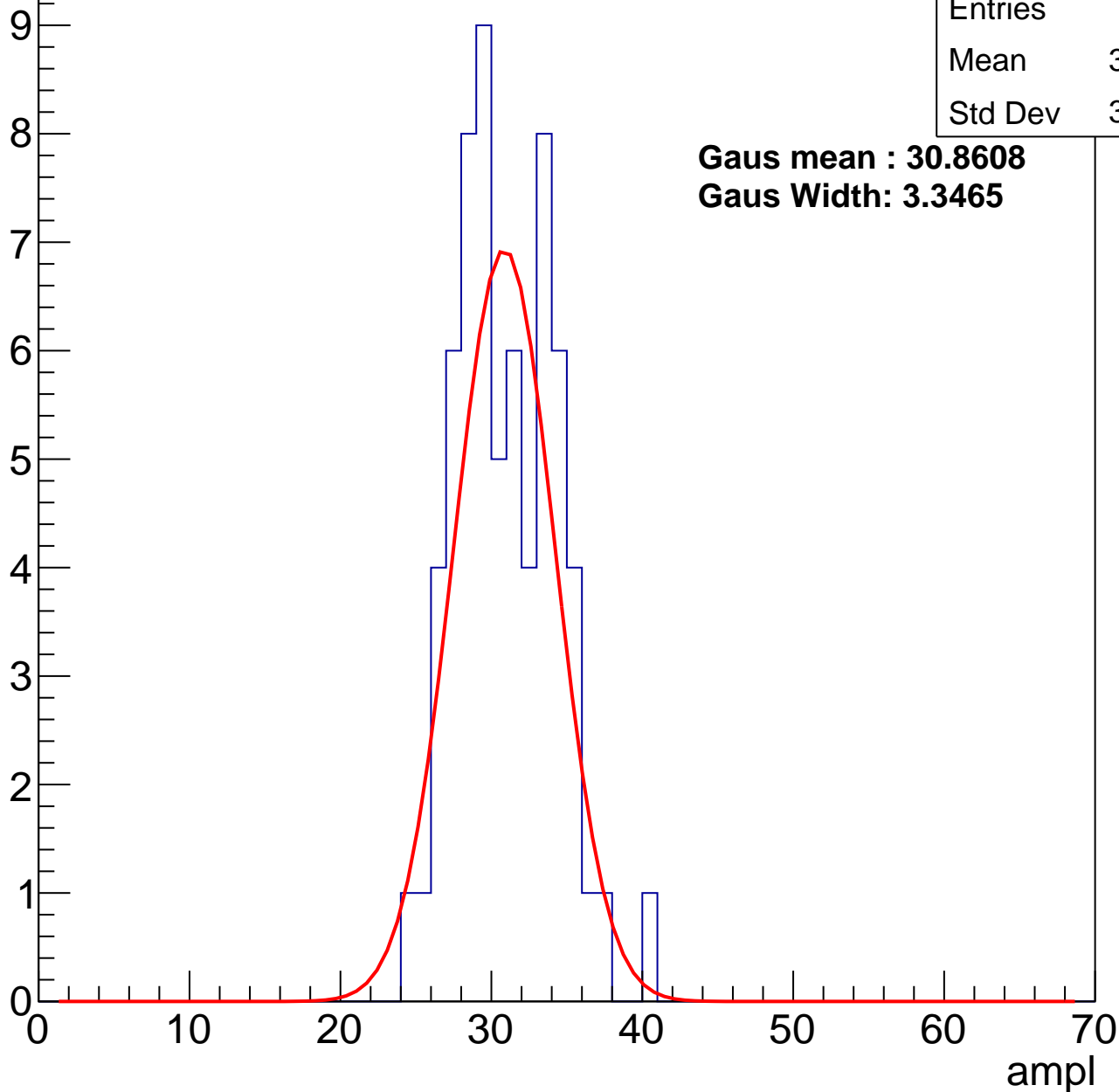
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	30.54
Std Dev	3.225

**Gaus mean : 30.8608**

**Gaus Width: 3.3465**



# B0L000S, U19-ch77, adc1

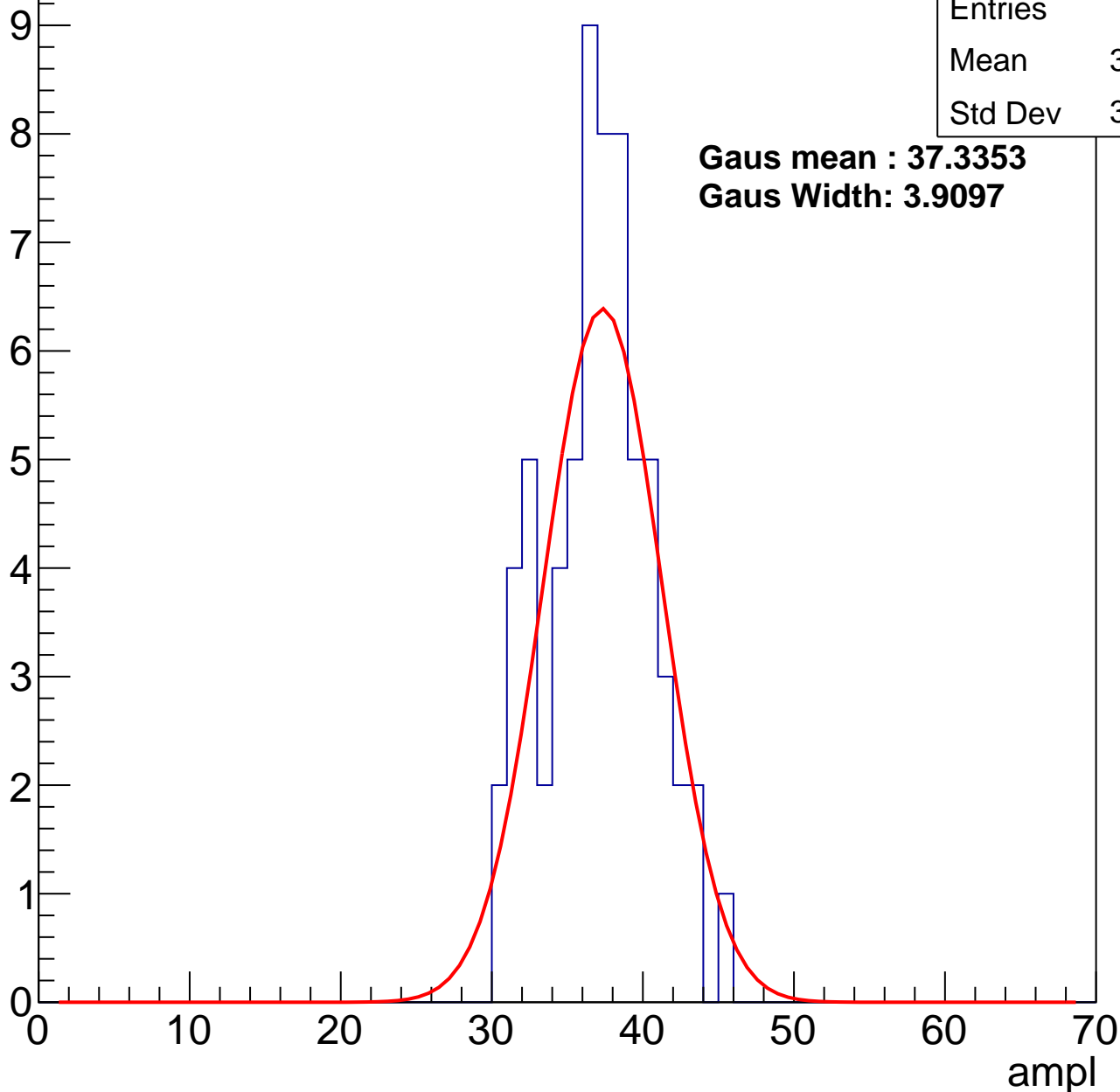
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.58
Std Dev	3.419

**Gaus mean : 37.3353**

**Gaus Width: 3.9097**



# B0L000S, U19-ch77, adc2

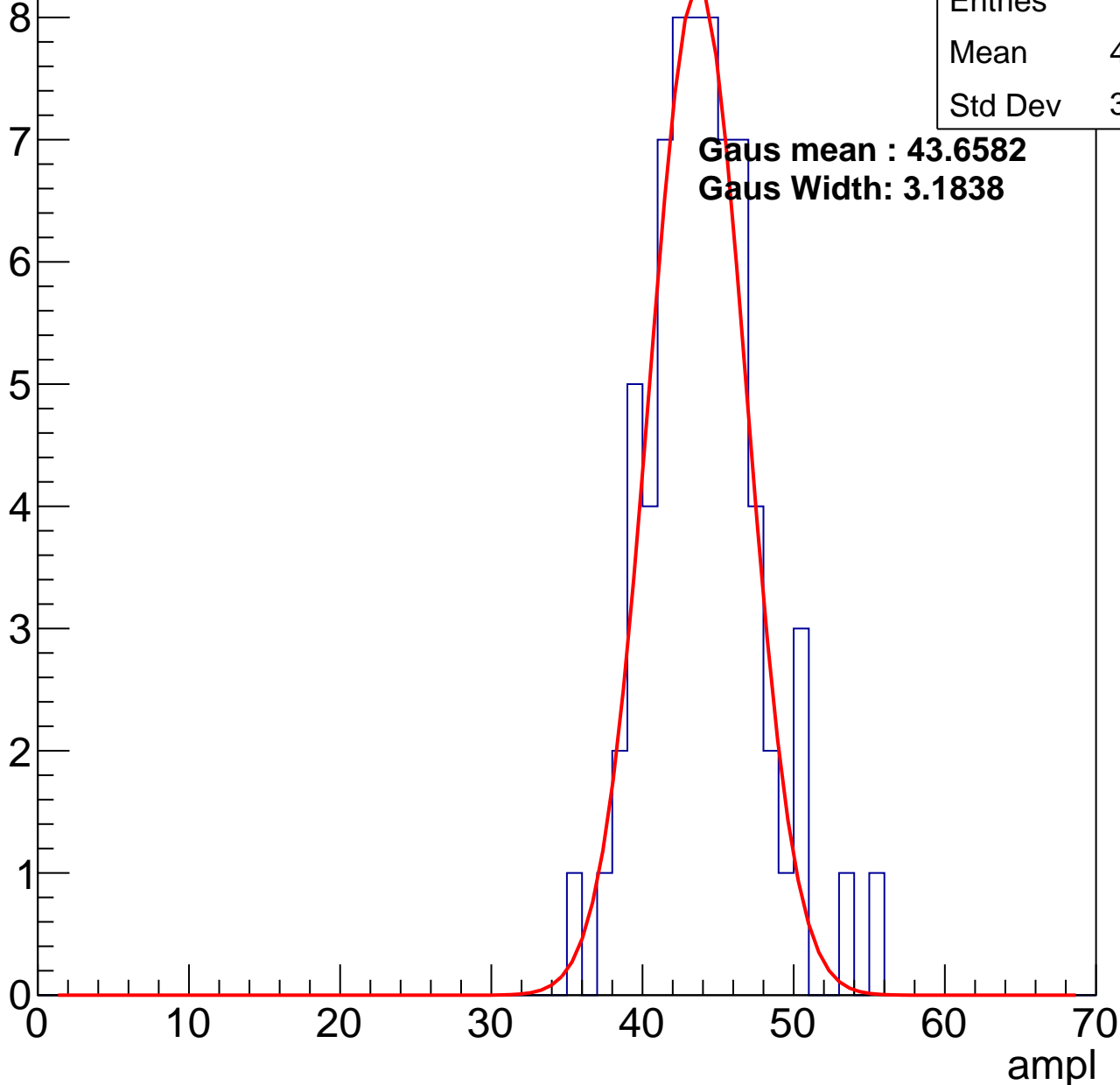
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.57
Std Dev	3.624

**Gaus mean : 43.6582**

**Gaus Width: 3.1838**

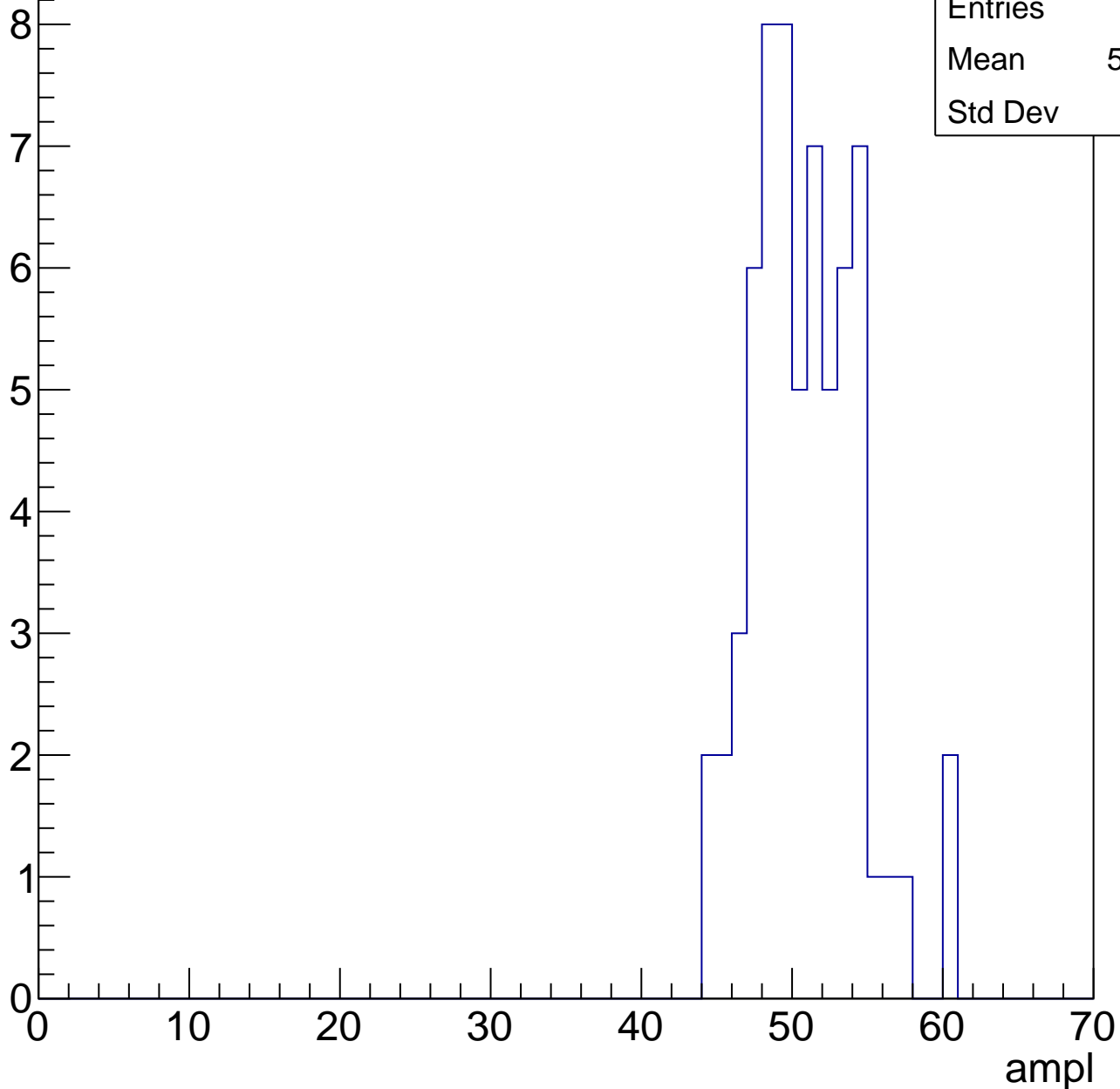


# B0L000S, U19-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	50.39
Std Dev	3.44

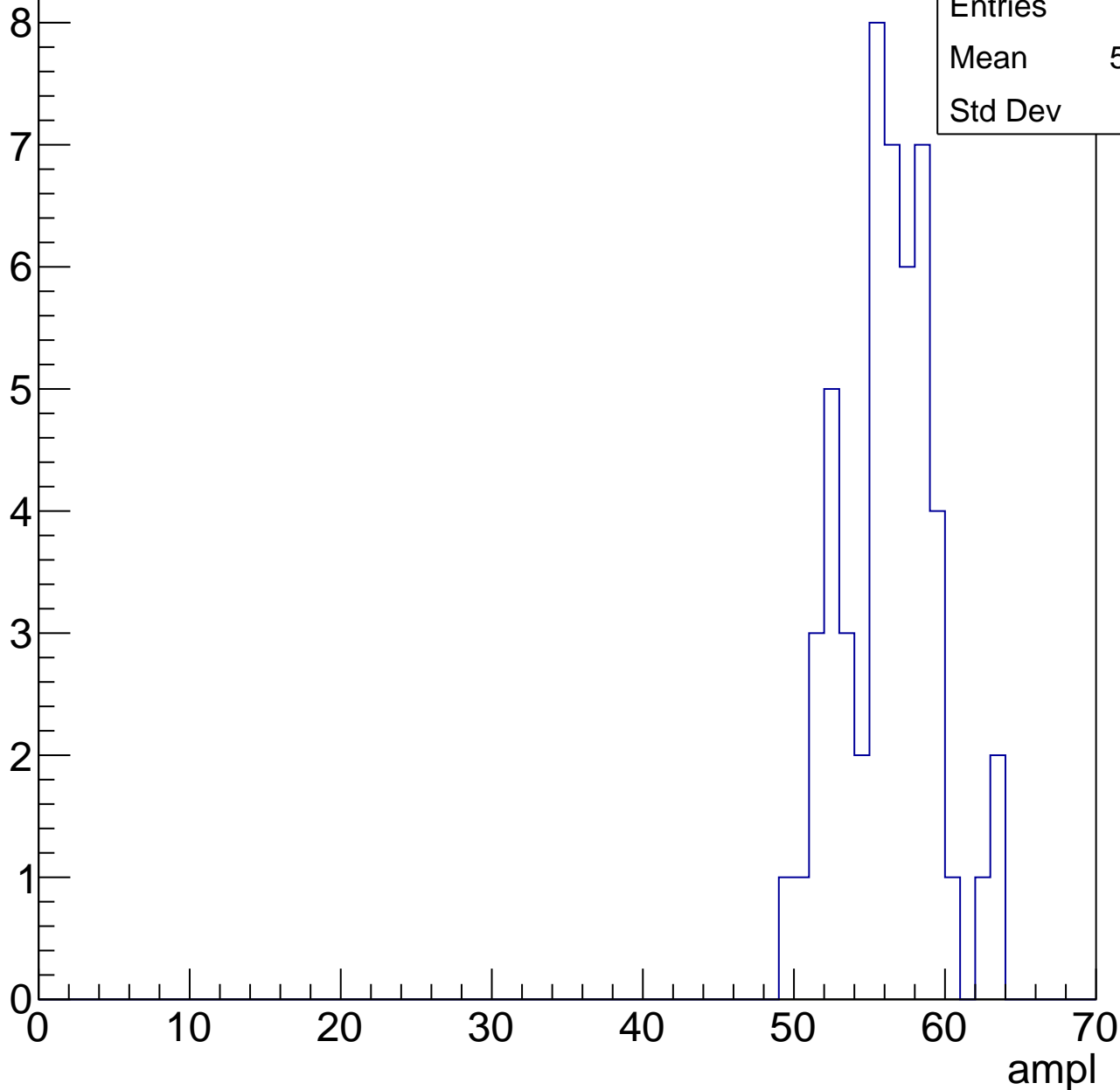


# B0L000S, U19-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	55.75
Std Dev	3.13

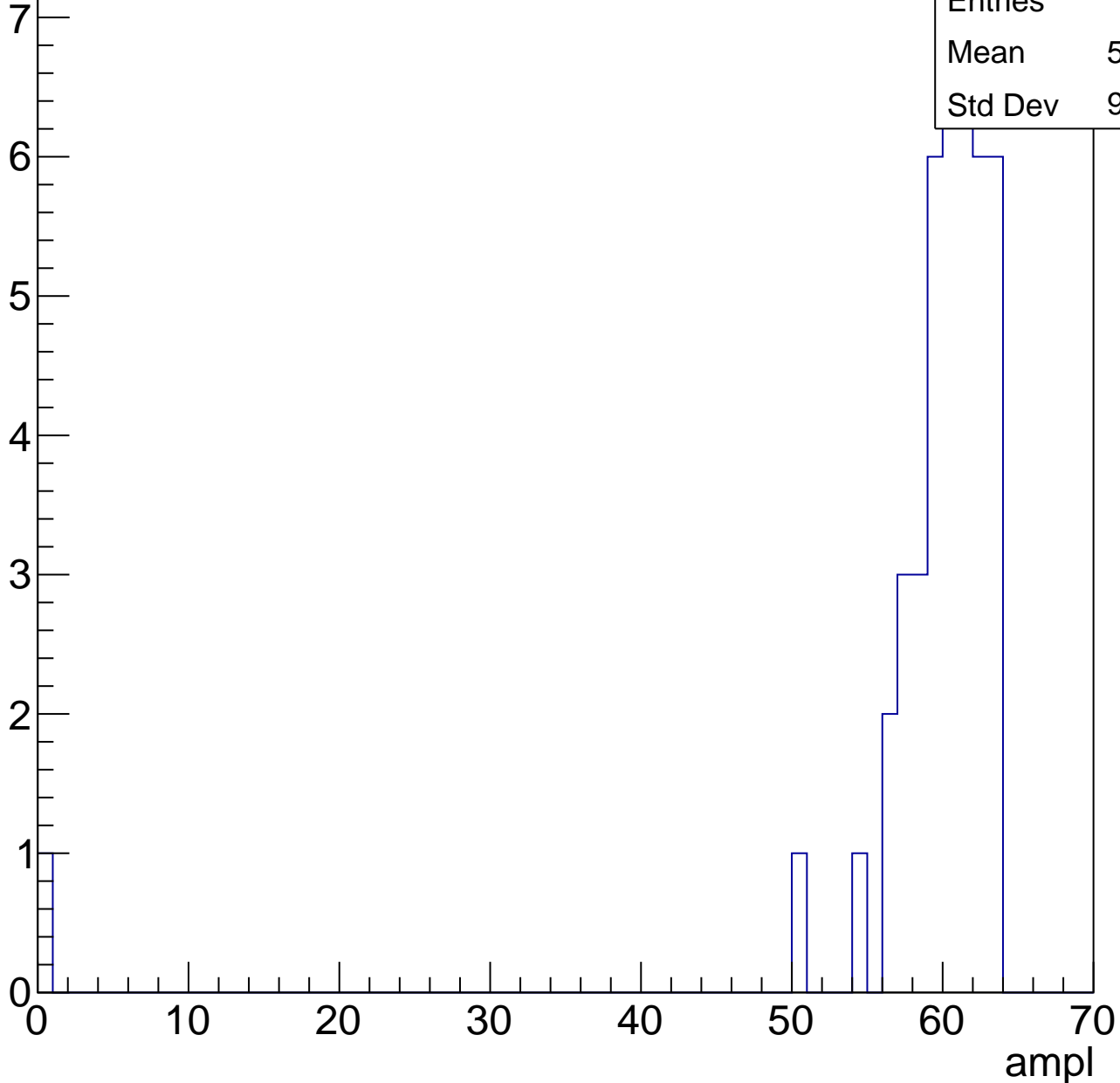


# B0L000S, U19-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.42
Std Dev	9.389



# B0L000S, U19-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.88

Std Dev

0.7806



# B0L000S, U19-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U19-ch78, adc0

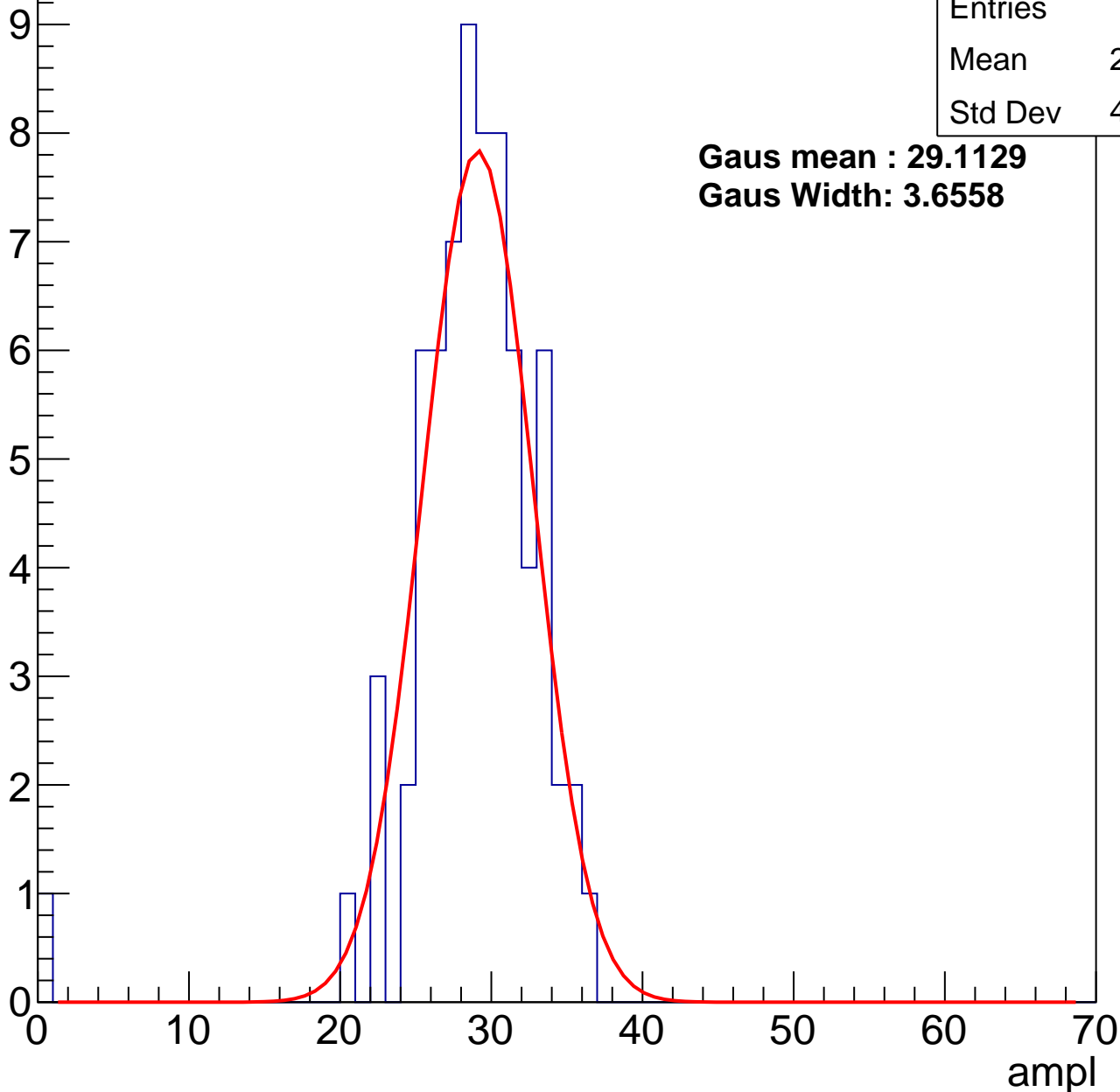
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	28.32
Std Dev	4.725

**Gaus mean : 29.1129**

**Gaus Width: 3.6558**



# B0L000S, U19-ch78, adc1

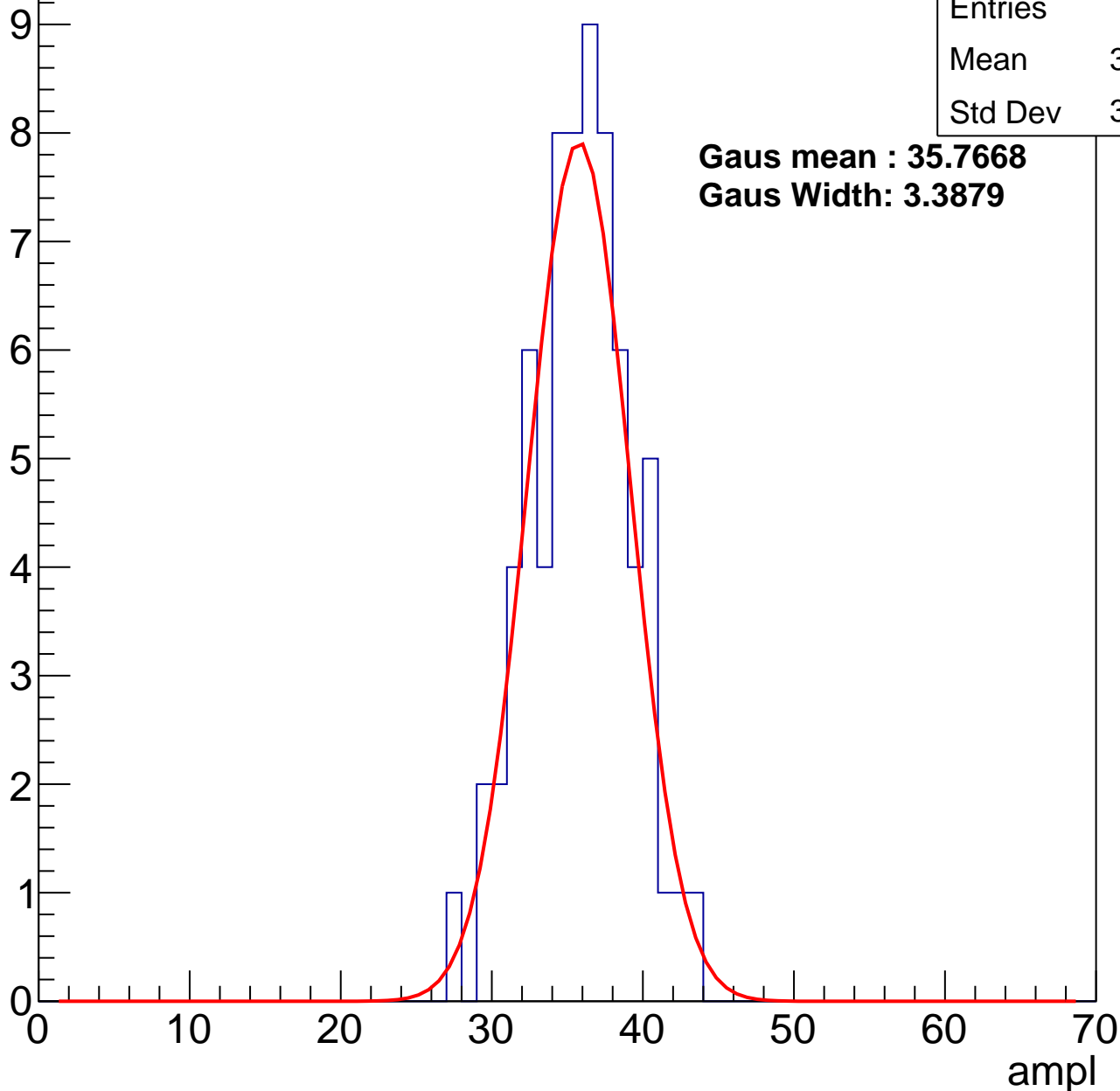
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.36
Std Dev	3.278

**Gaus mean : 35.7668**

**Gaus Width: 3.3879**



# B0L000S, U19-ch78, adc2

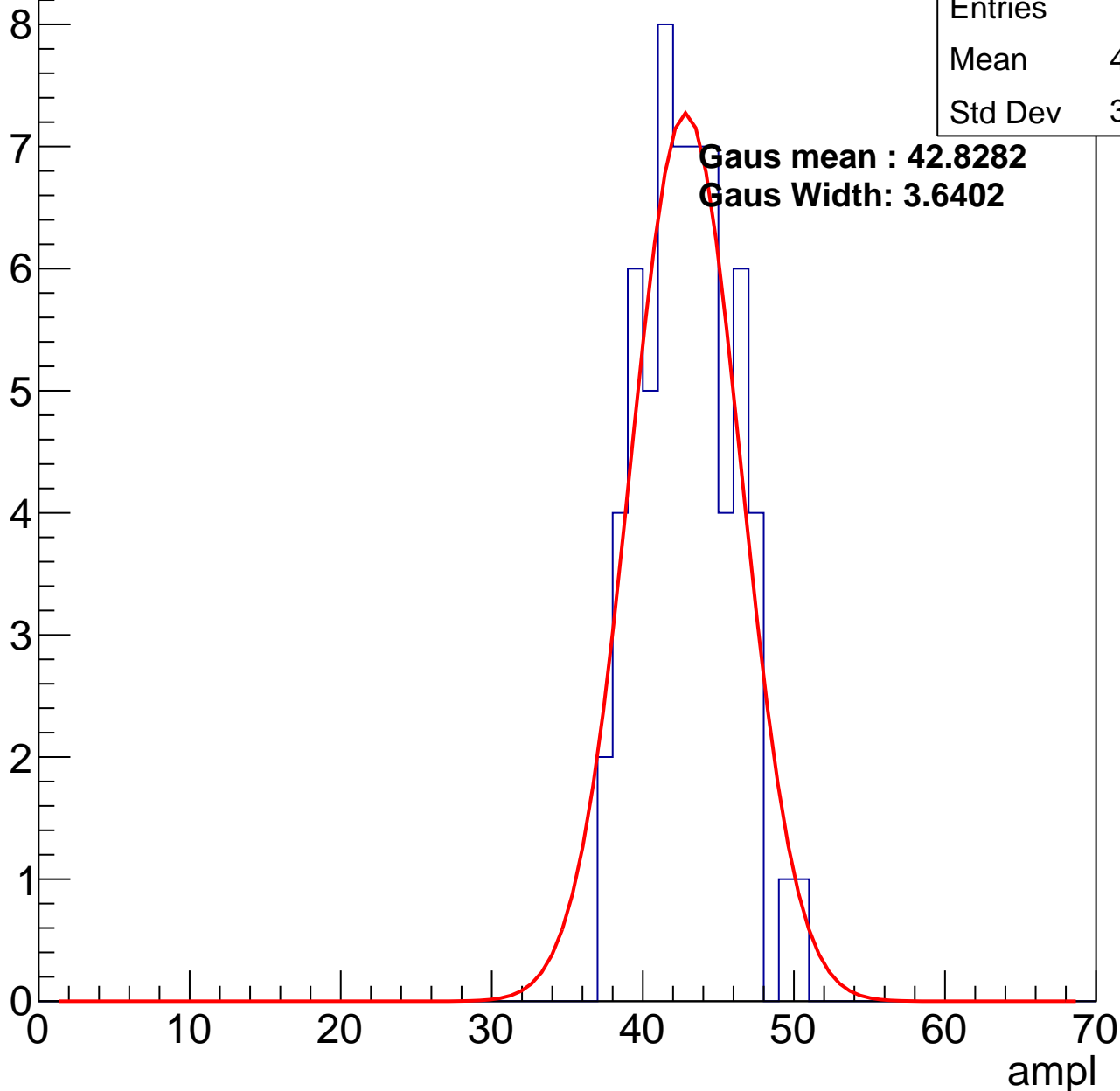
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	42.48
Std Dev	3.009

**Gaus mean : 42.8282**

**Gaus Width: 3.6402**

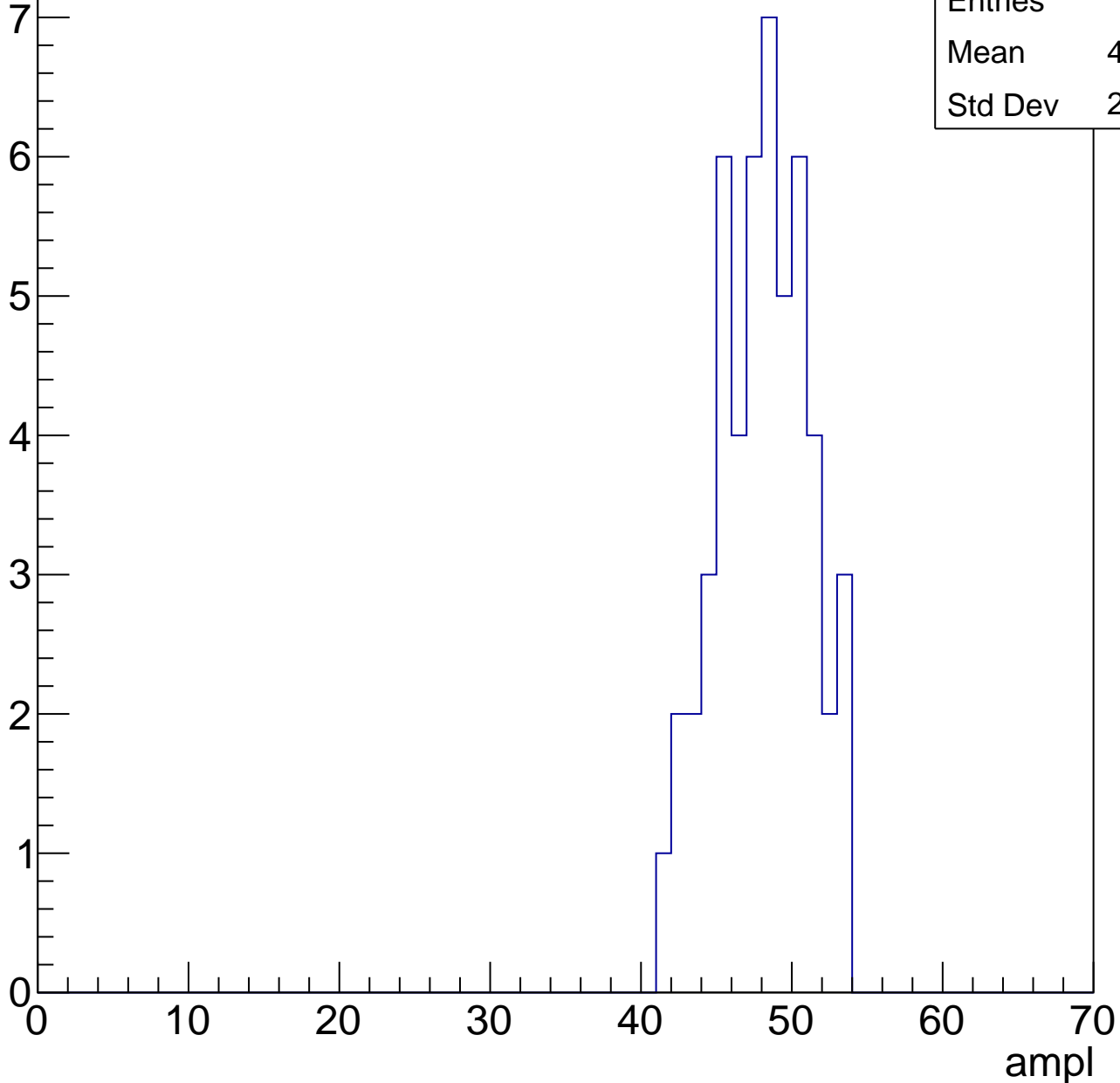


# B0L000S, U19-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	47.59
Std Dev	2.998

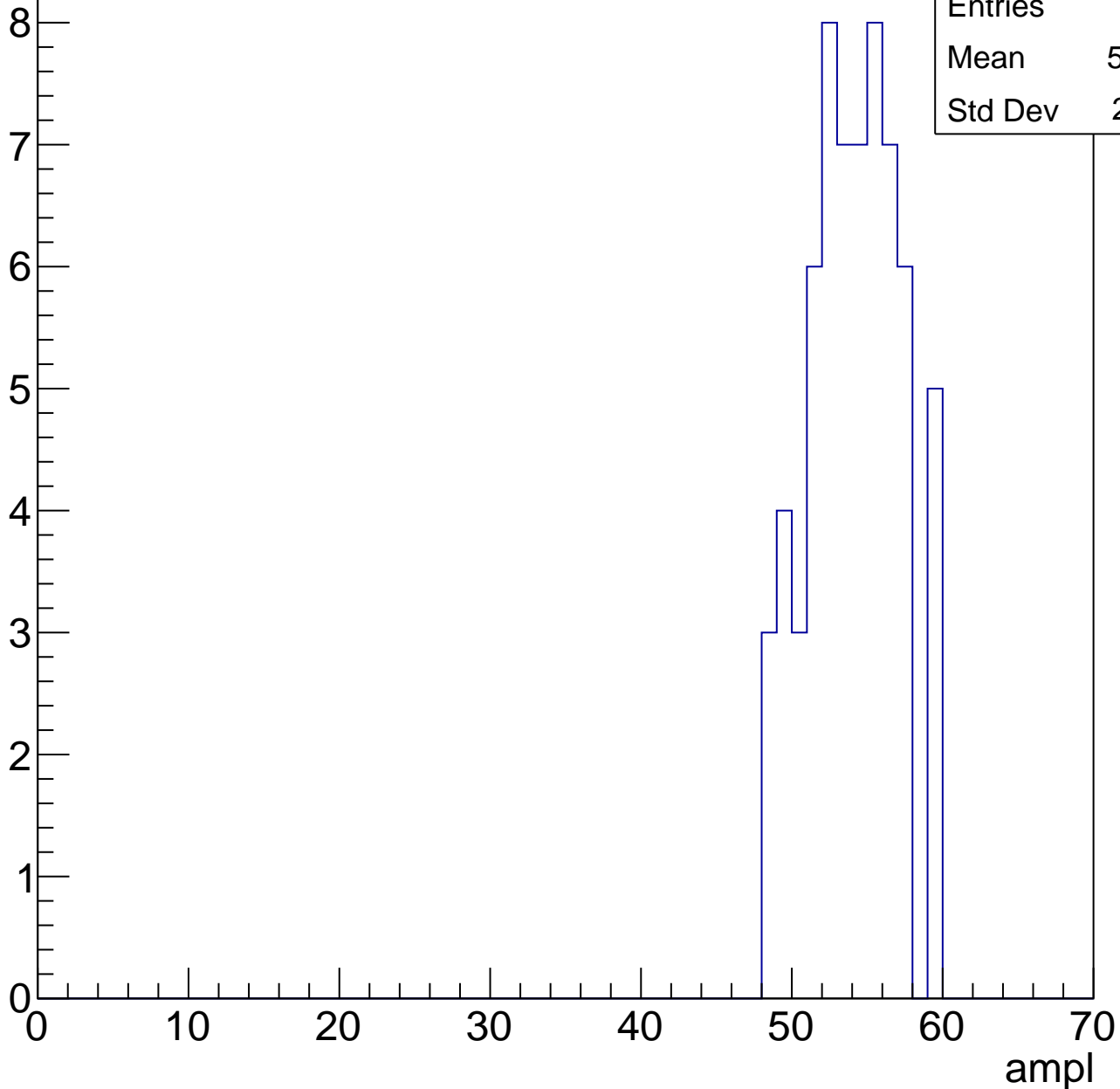


# B0L000S, U19-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	53.59
Std Dev	2.941



# B0L000S, U19-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

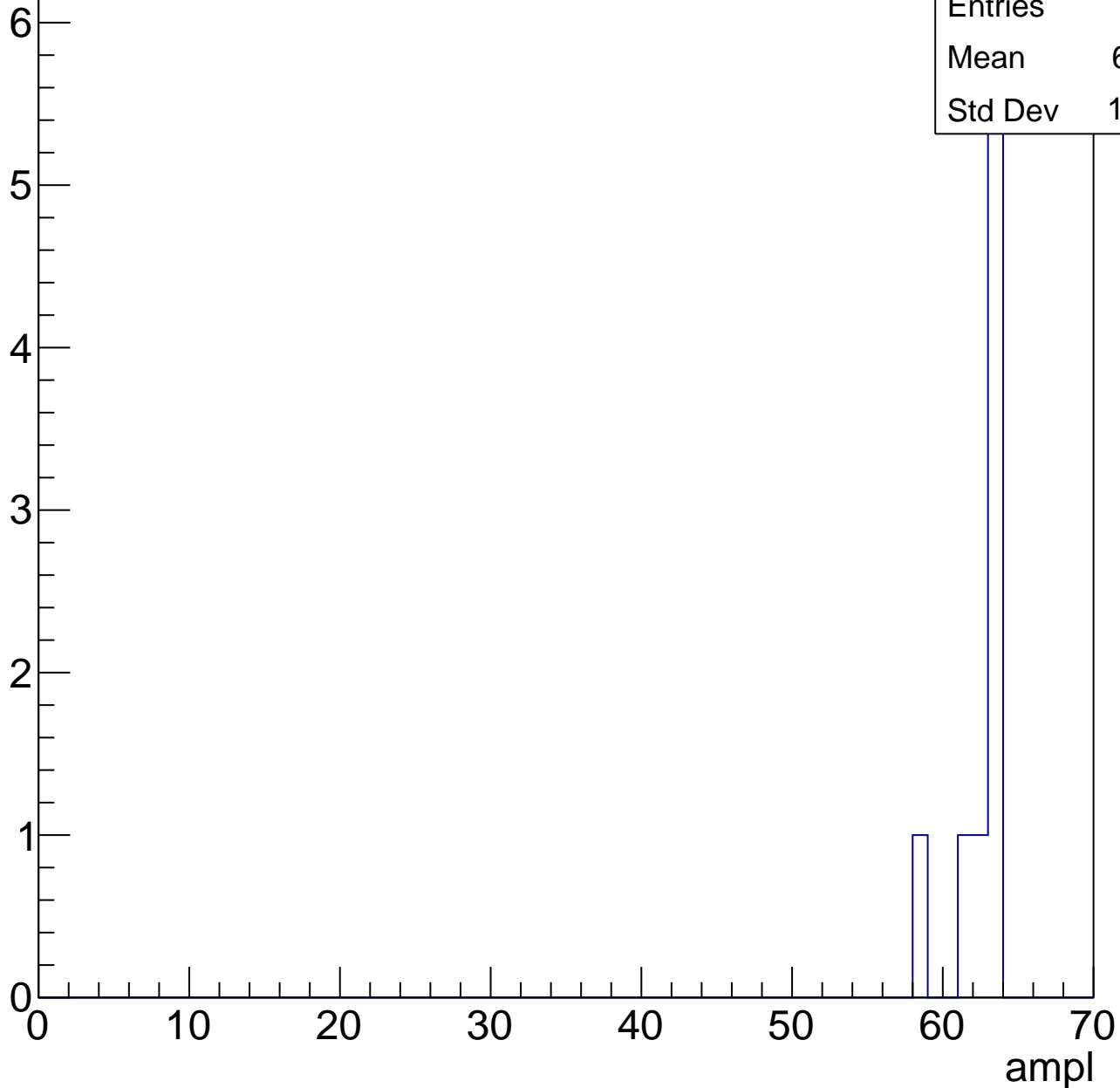
Entries	58
Mean	58.31
Std Dev	8.088

0

# B0L000S, U19-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch79, adc0

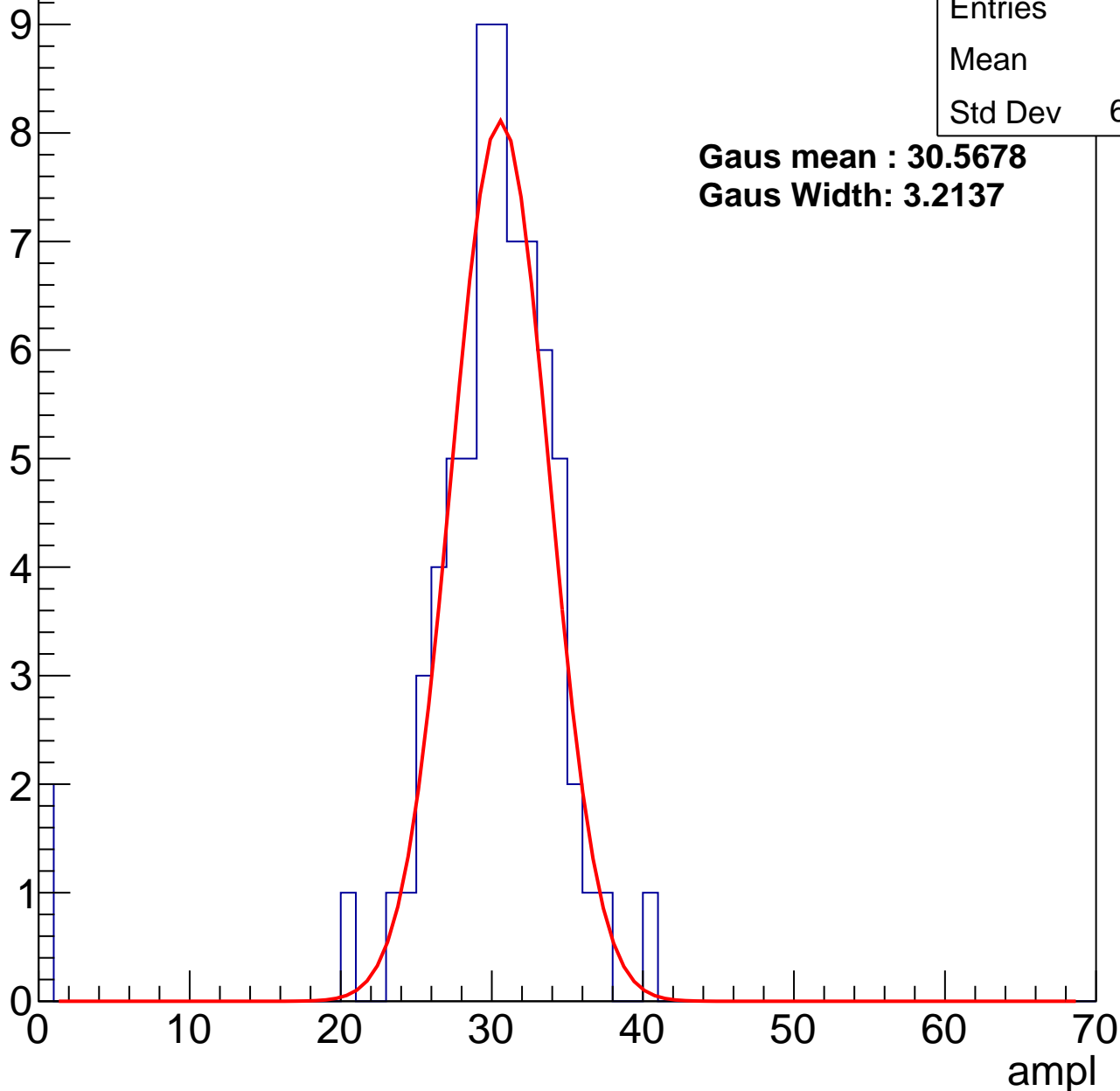
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.2
Std Dev	6.044

**Gaus mean : 30.5678**

**Gaus Width: 3.2137**



# B0L000S, U19-ch79, adc1

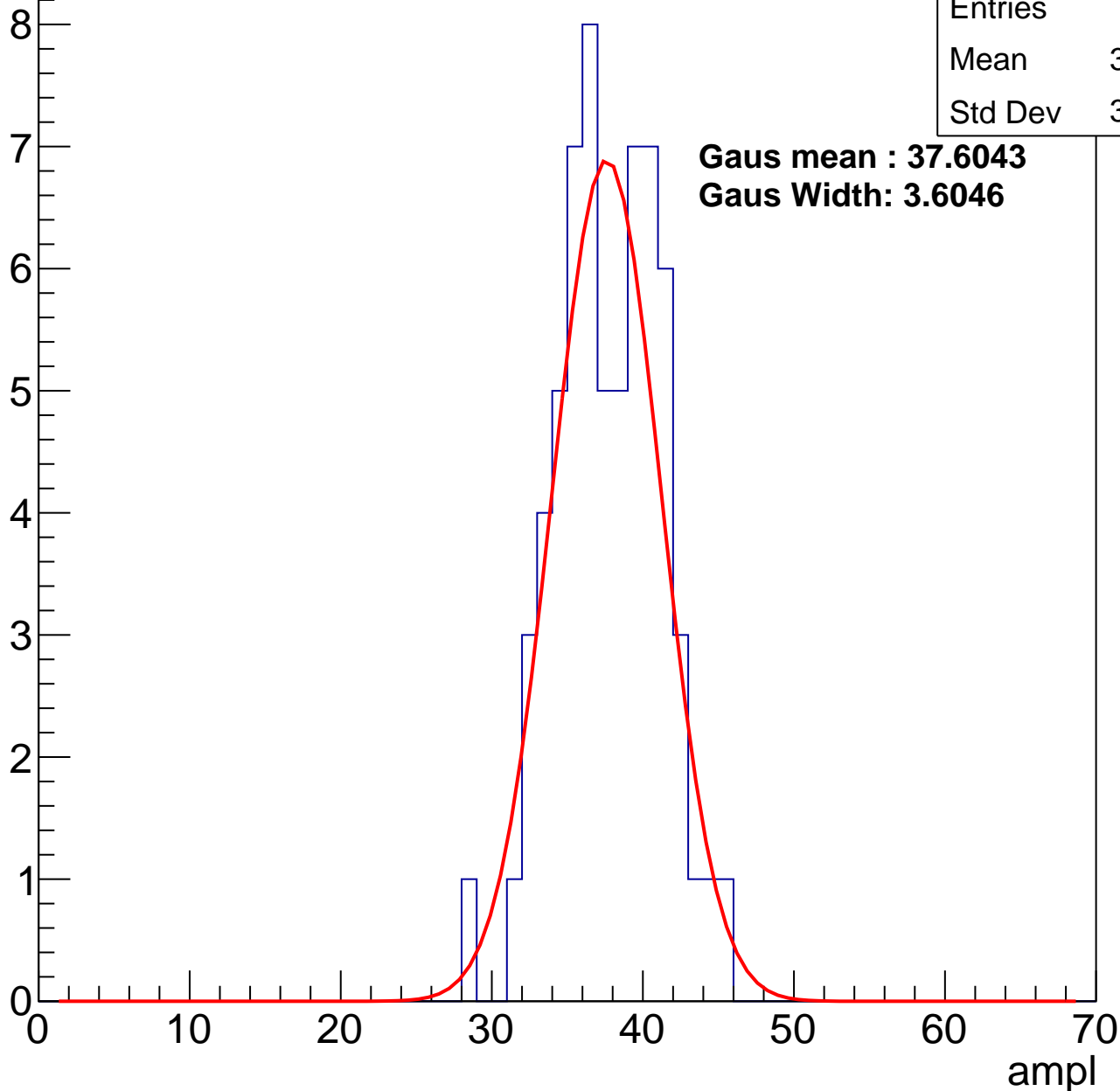
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.26
Std Dev	3.394

**Gaus mean : 37.6043**

**Gaus Width: 3.6046**



# B0L000S, U19-ch79, adc2

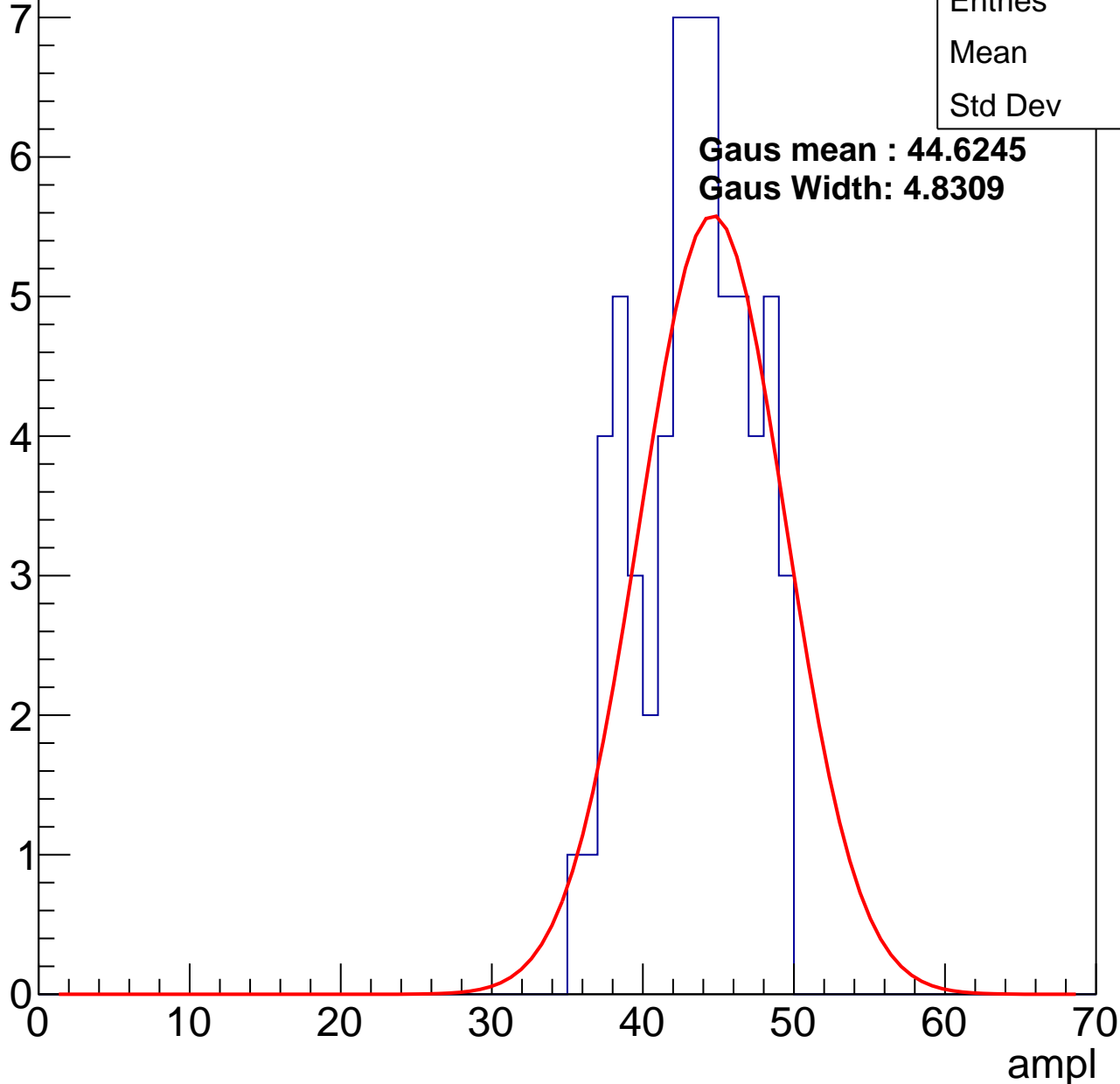
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.9
Std Dev	3.65

**Gaus mean : 44.6245**

**Gaus Width: 4.8309**

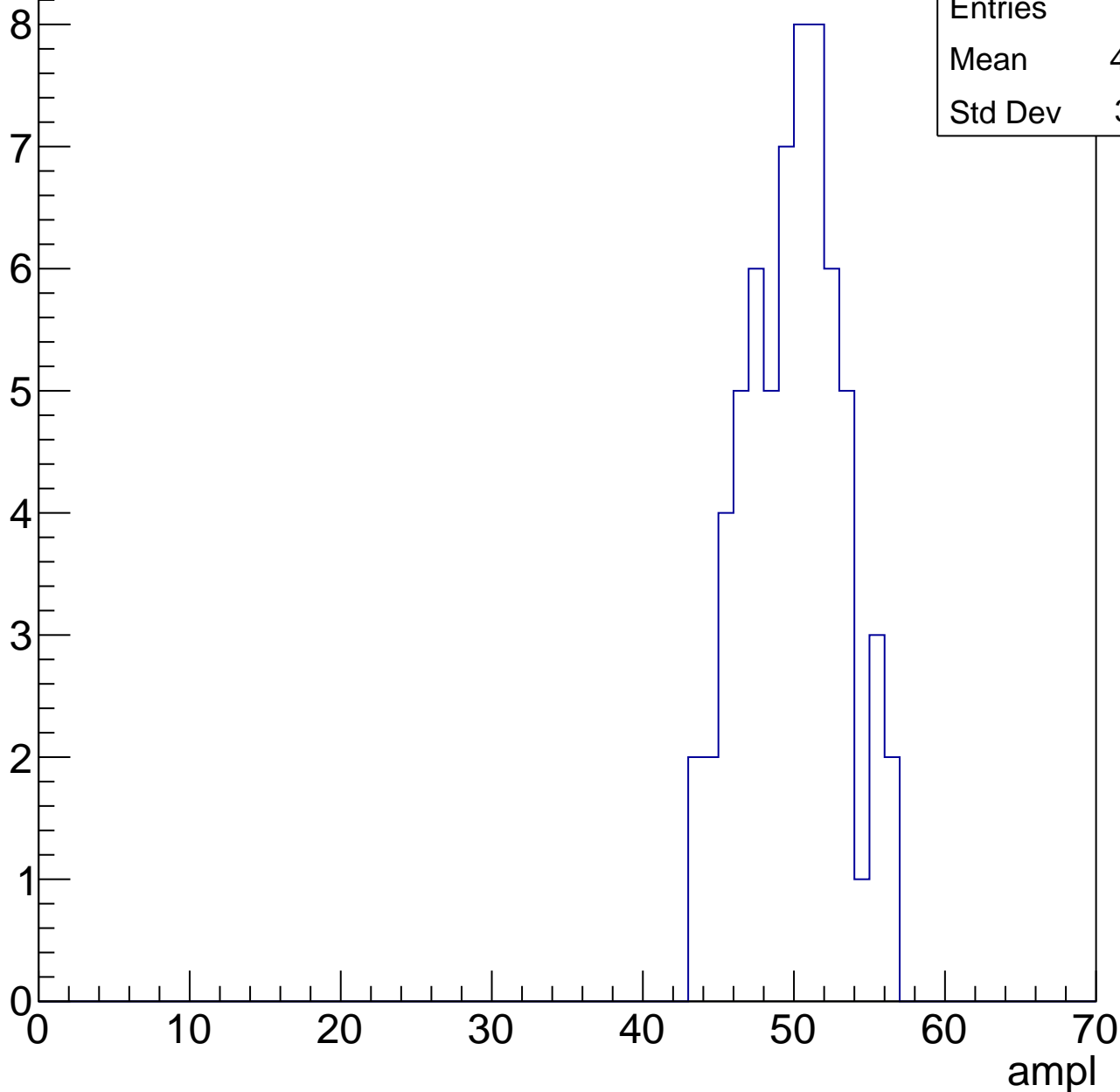


# B0L000S, U19-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.45
Std Dev	3.191

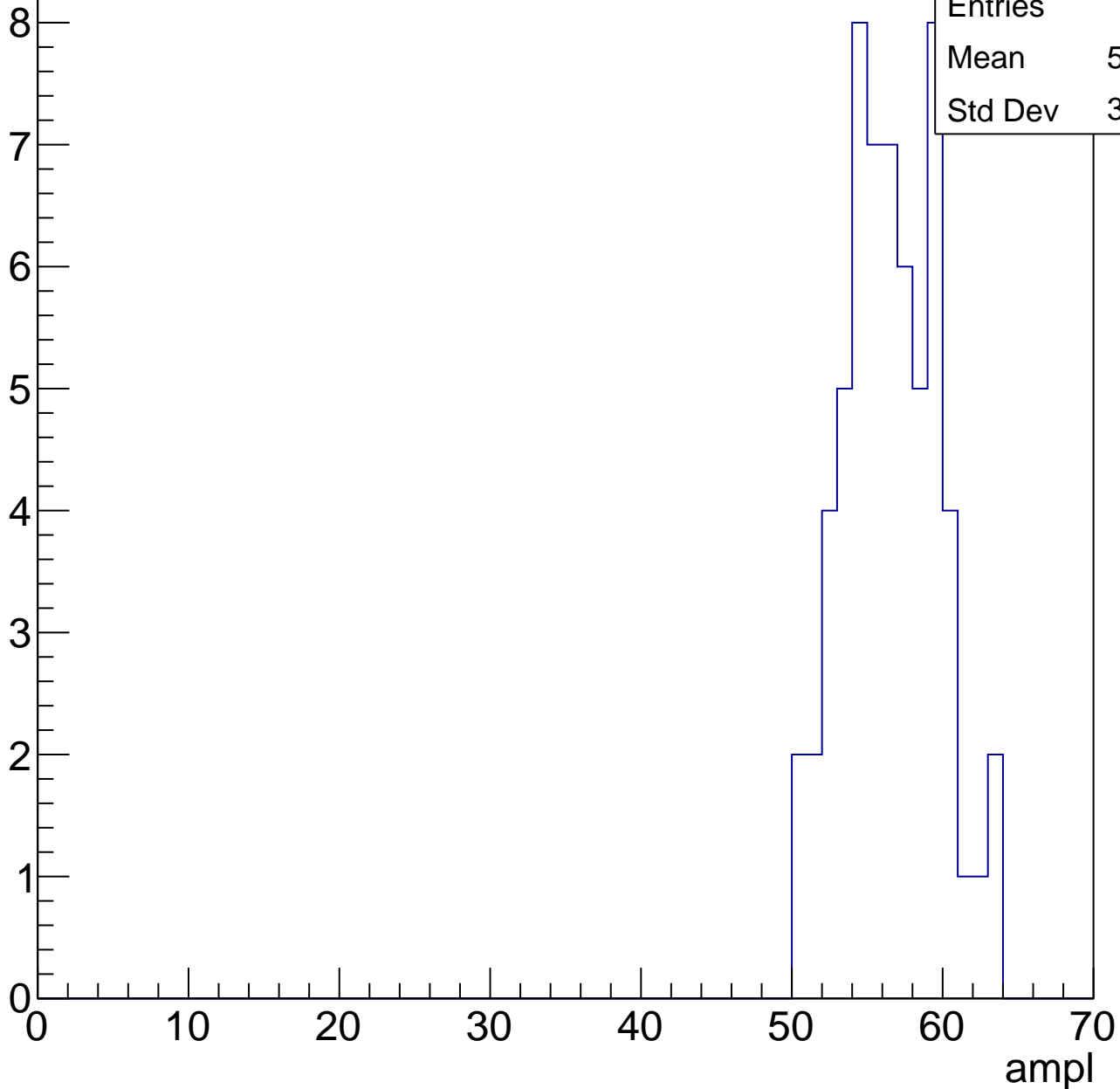


# B0L000S, U19-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	56.08
Std Dev	3.086



# B0L000S, U19-ch79, adc5

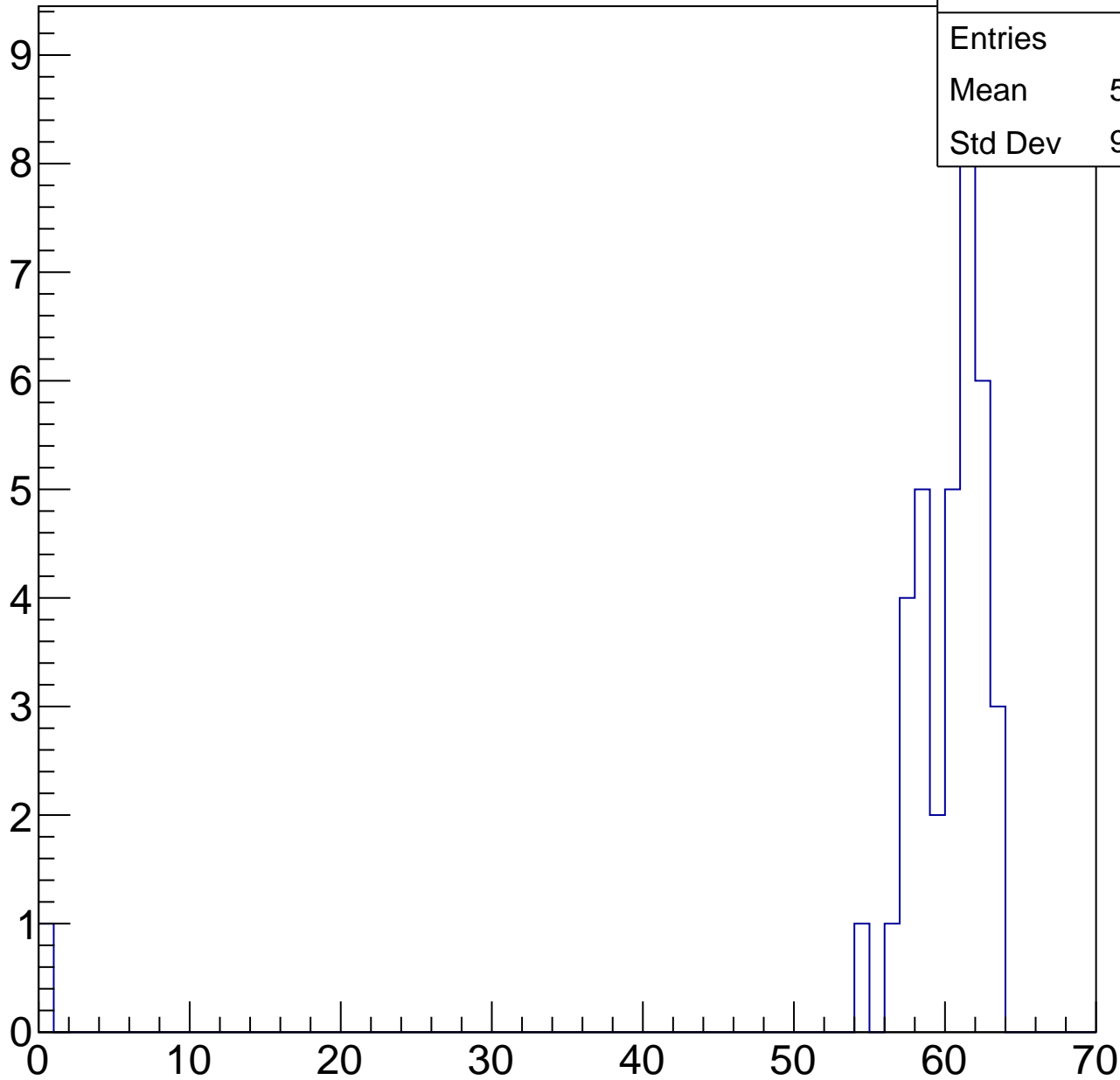
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	37
Mean	58.27
Std Dev	9.945

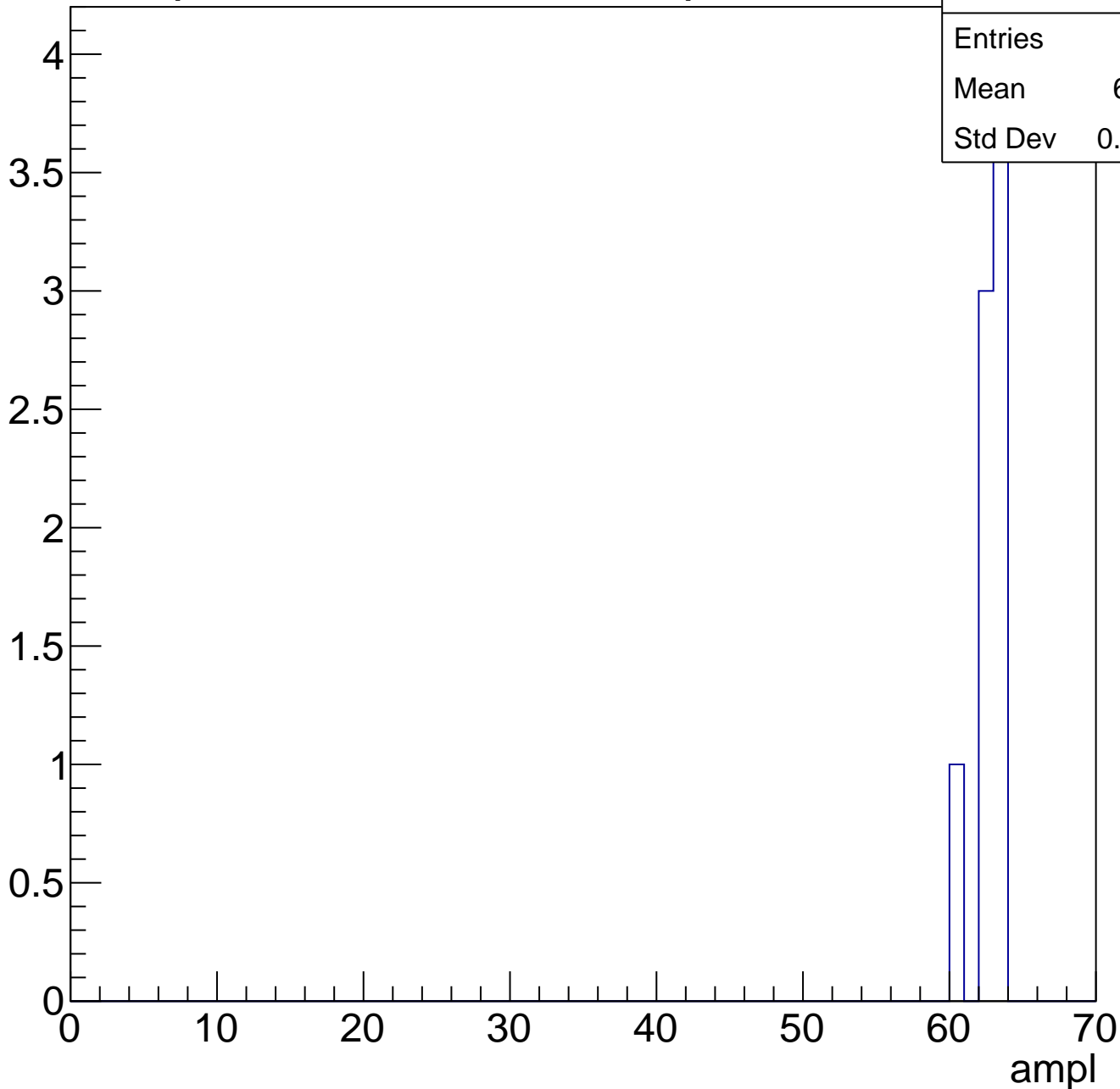
ampl



# B0L000S, U19-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

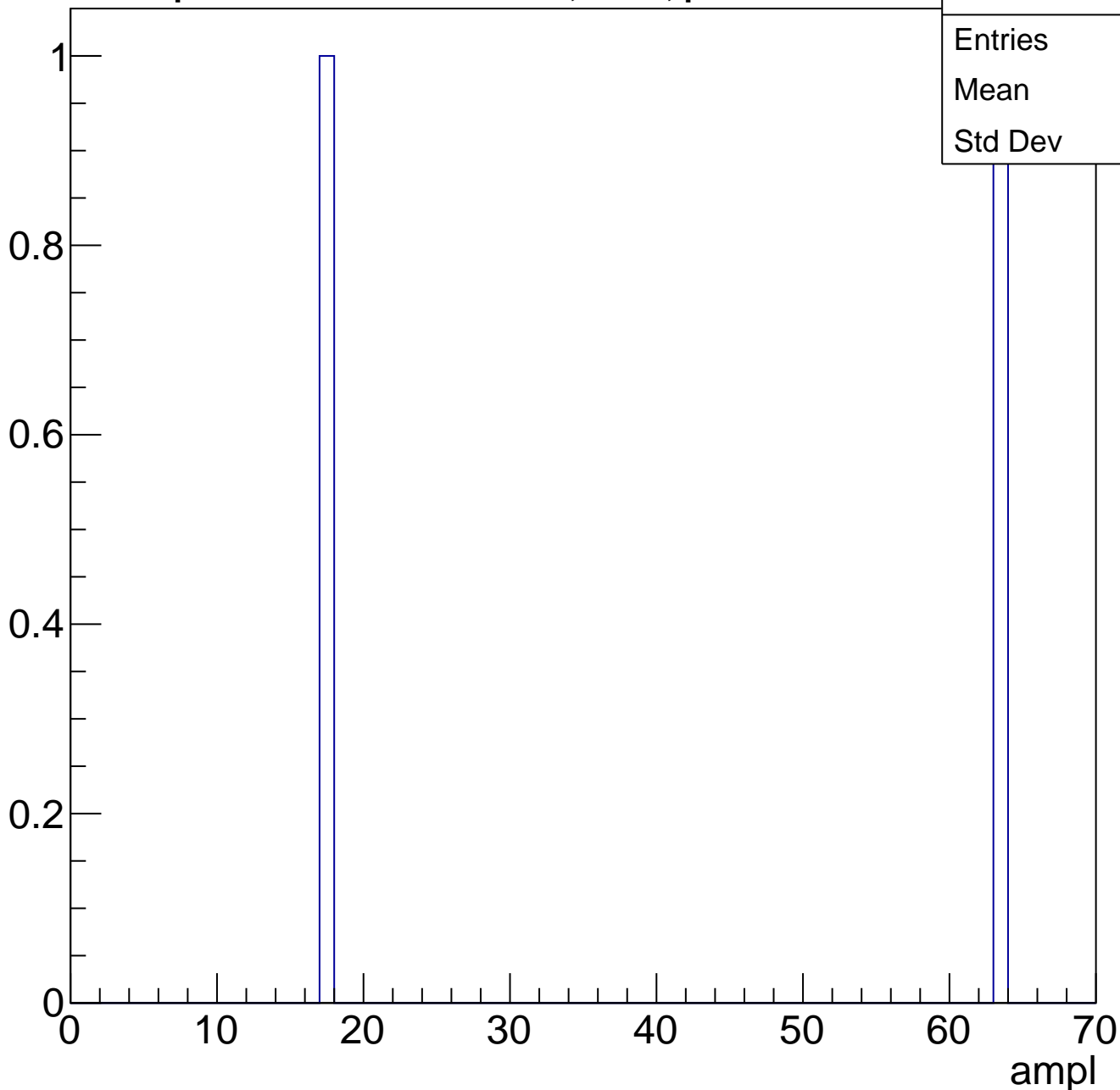




# B0L000S, U19-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	40
Std Dev	23

# B0L000S, U19-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	28.21
Std Dev	4.895

**Gaus mean : 29.1362**

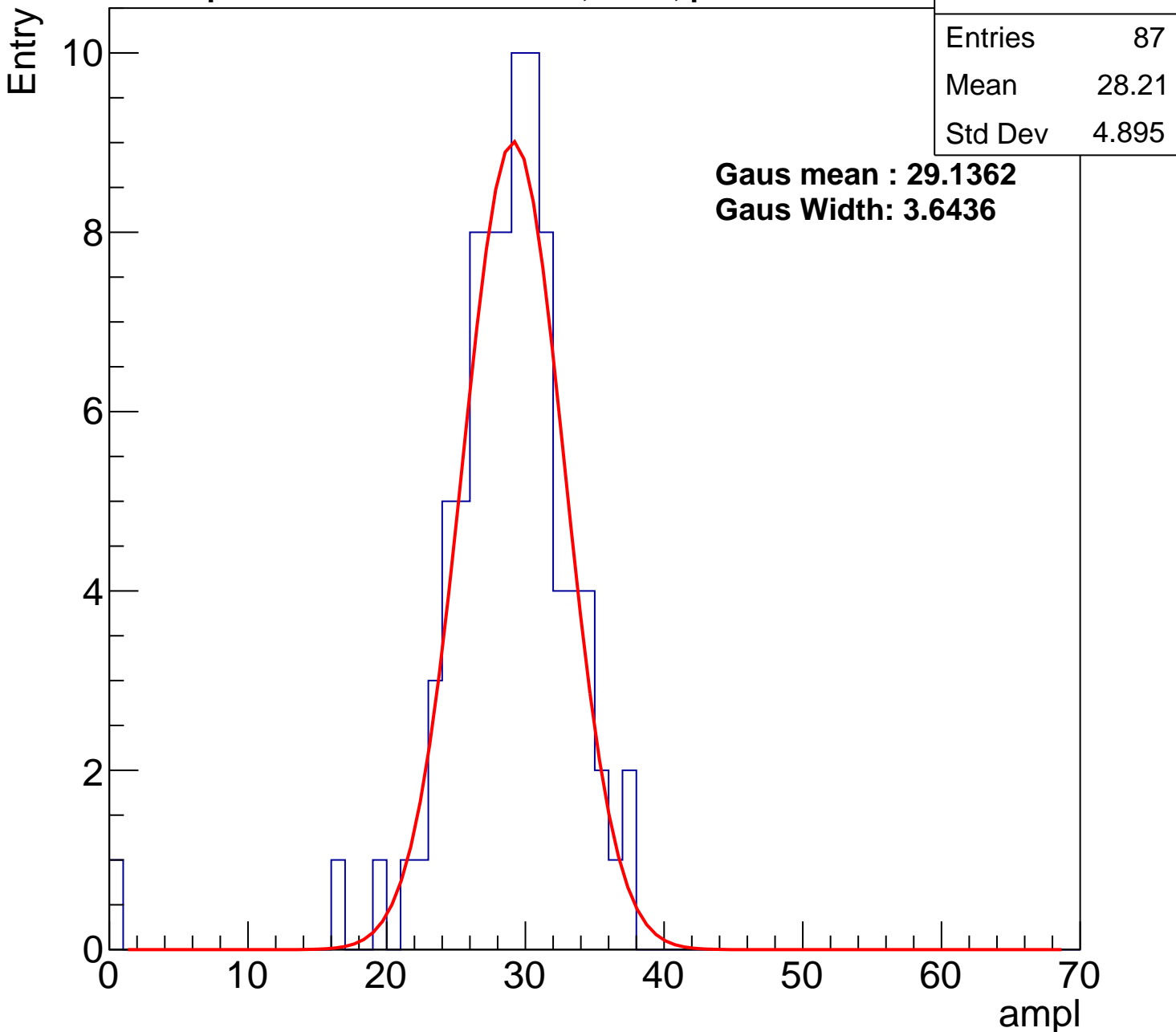
**Gaus Width: 3.6436**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



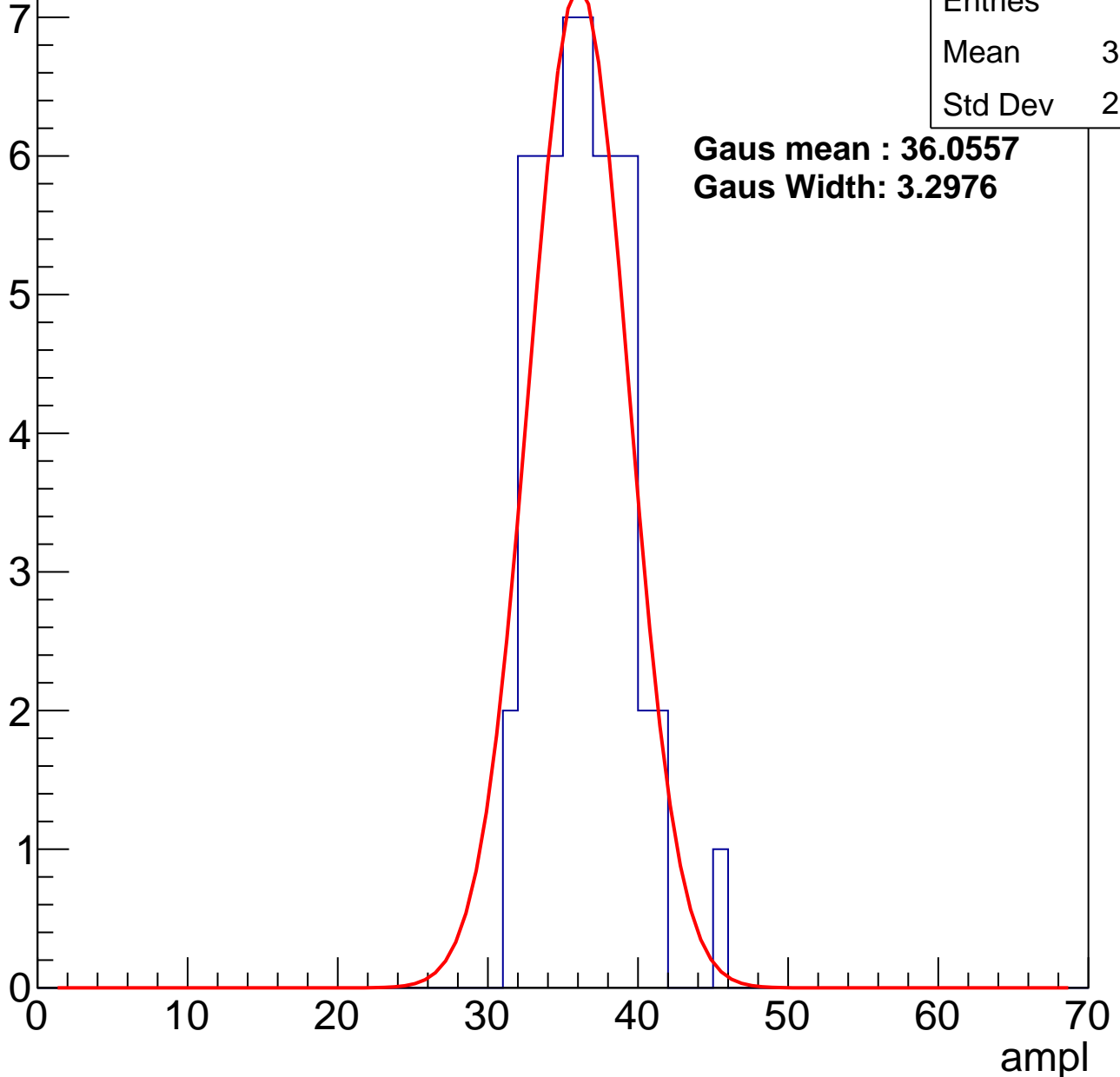
# B0L000S, U19-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.86
Std Dev	2.892

**Gaus mean : 36.0557**  
**Gaus Width: 3.2976**



# B0L000S, U19-ch80, adc2

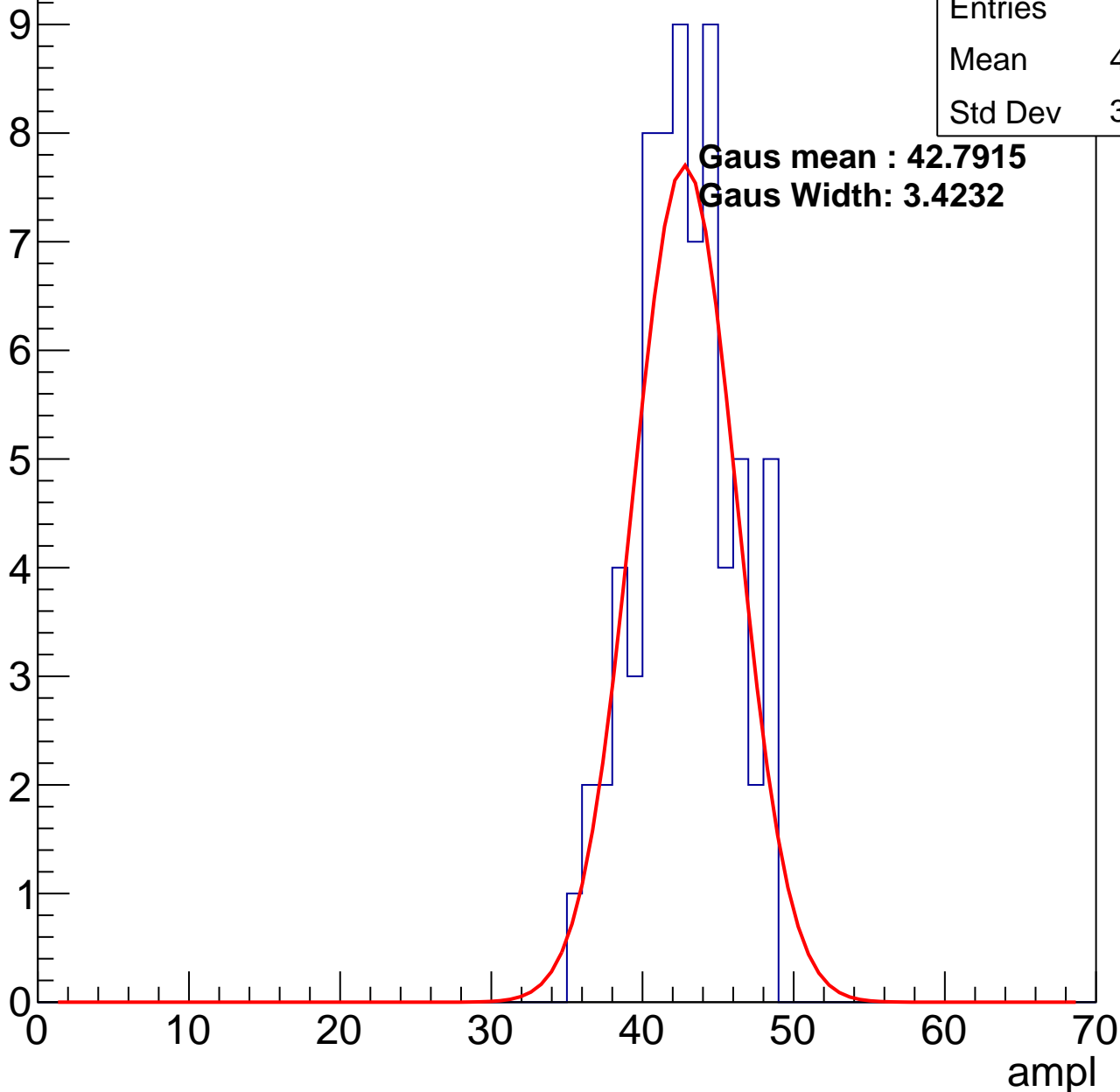
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.28
Std Dev	3.153

**Gaus mean : 42.7915**

**Gaus Width: 3.4232**

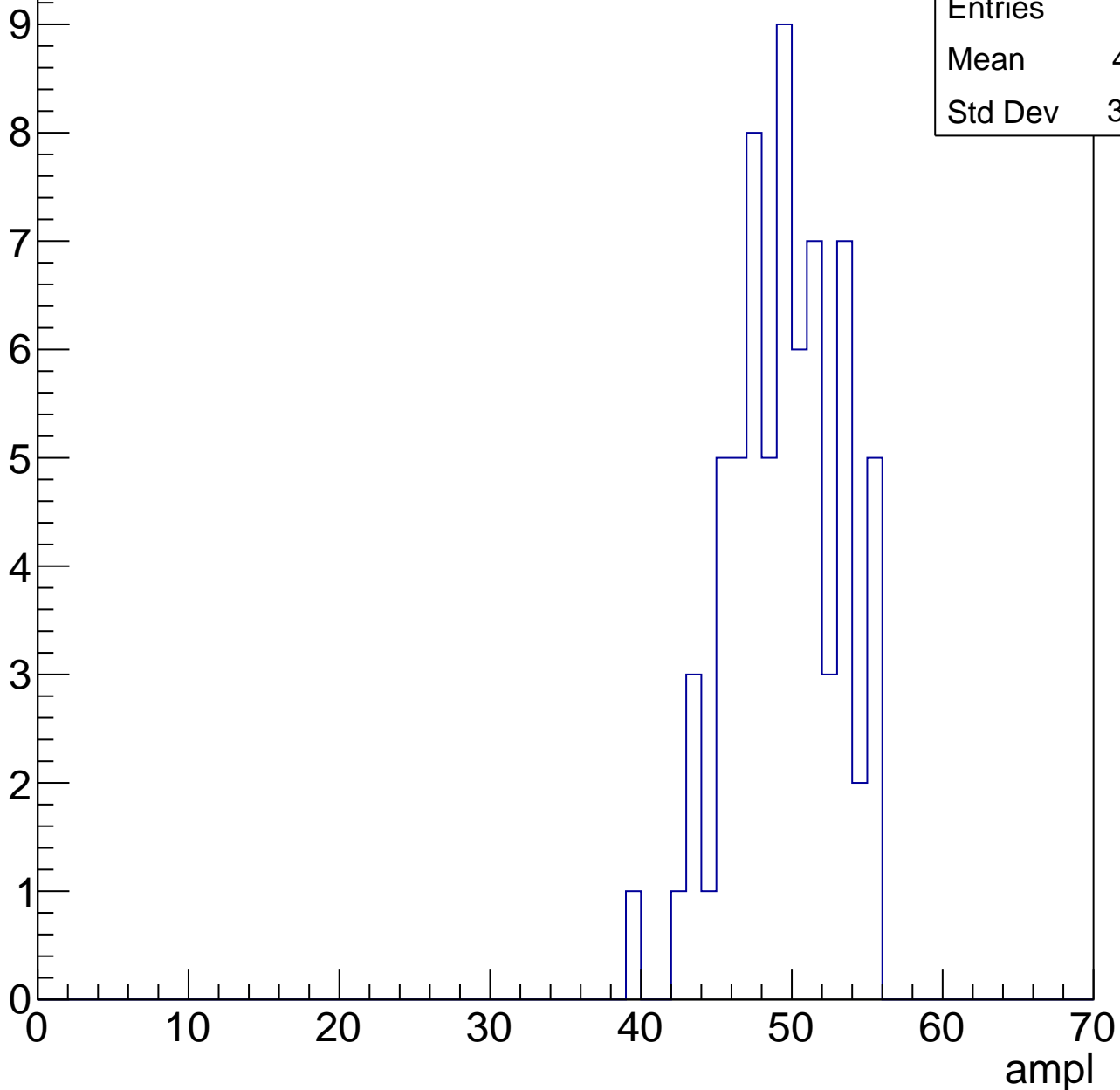


# B0L000S, U19-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.01
Std Dev	3.529

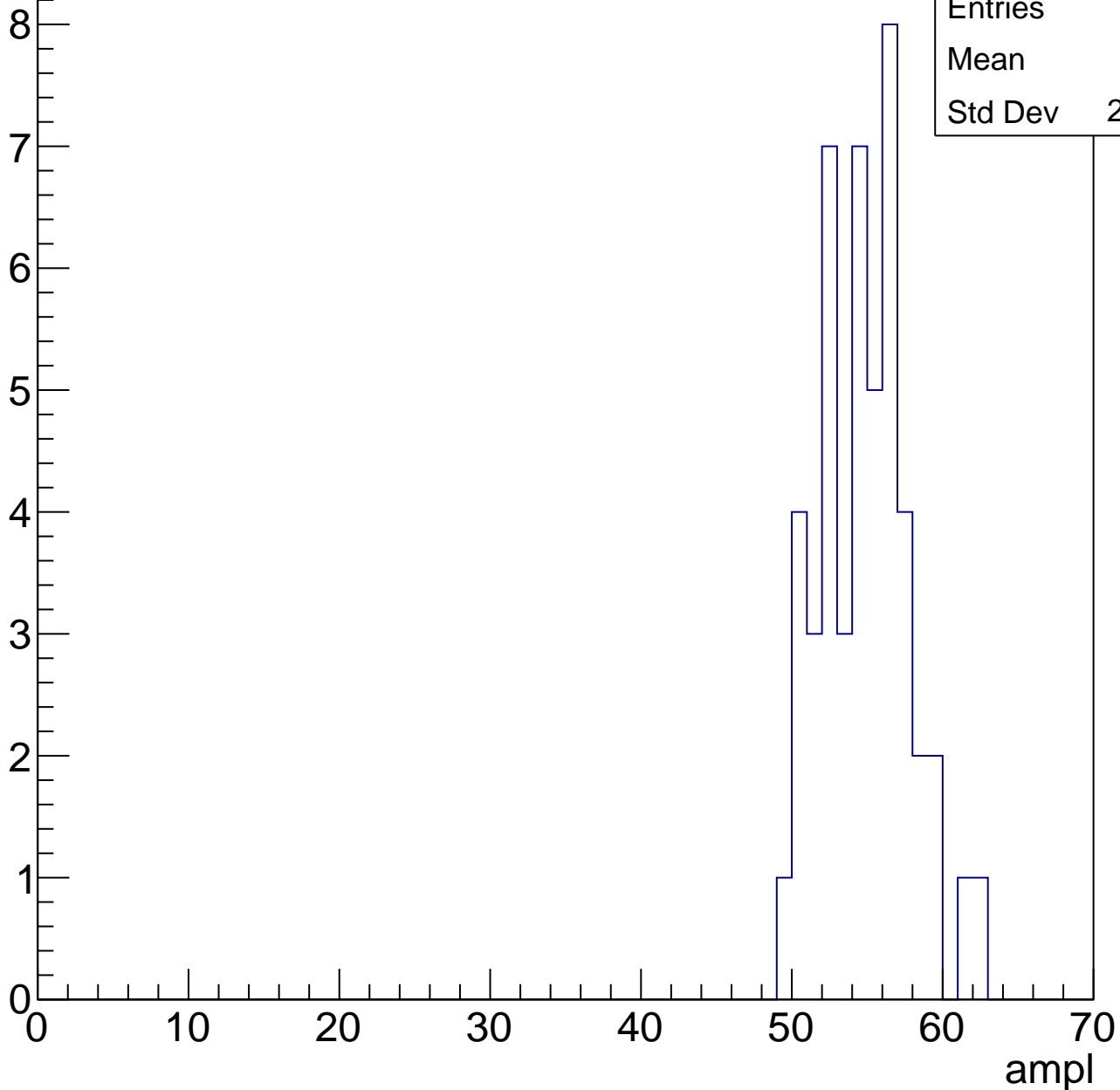


# B0L000S, U19-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

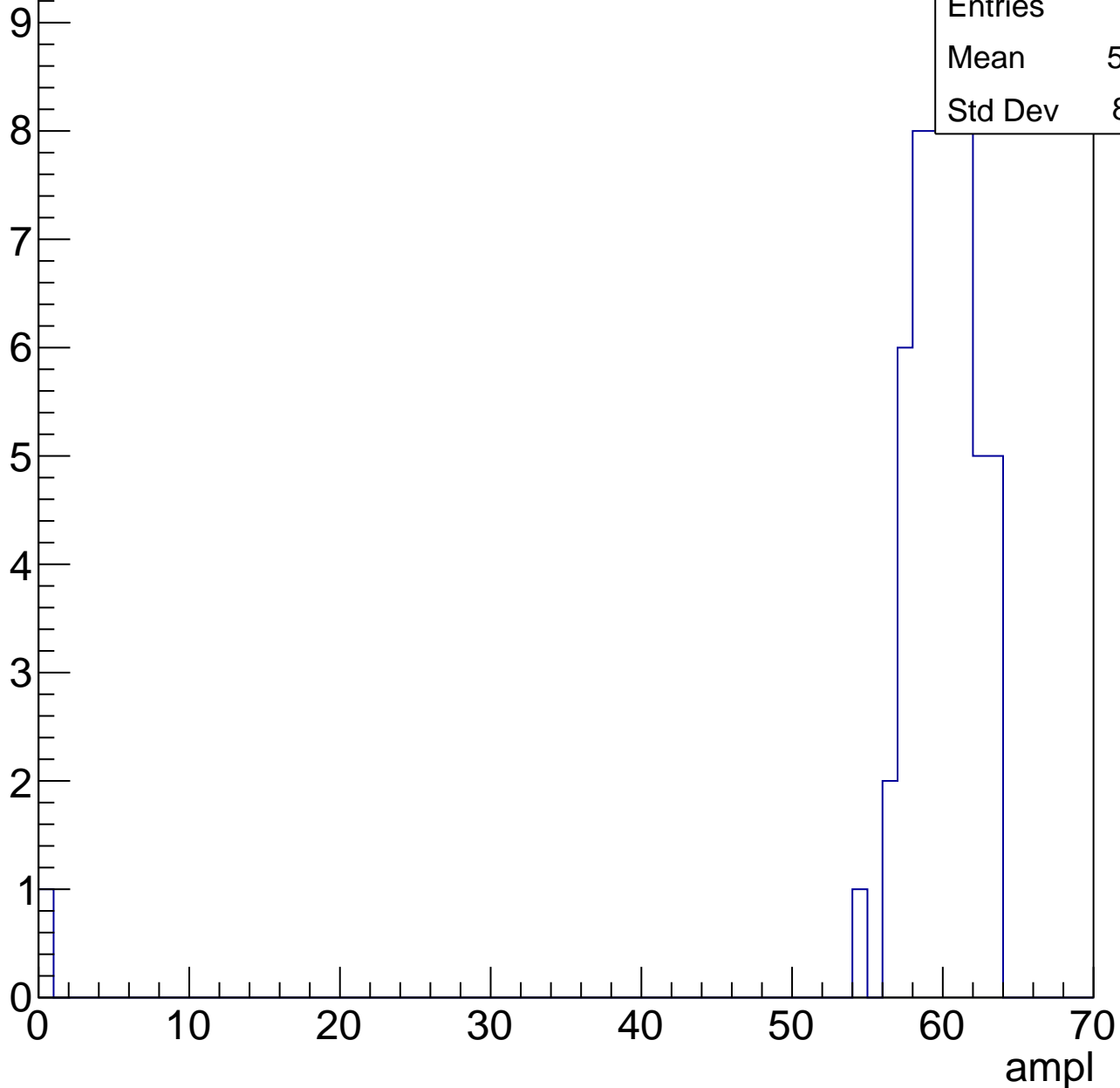
Entries	48
Mean	54.4
Std Dev	2.914



# B0L000S, U19-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

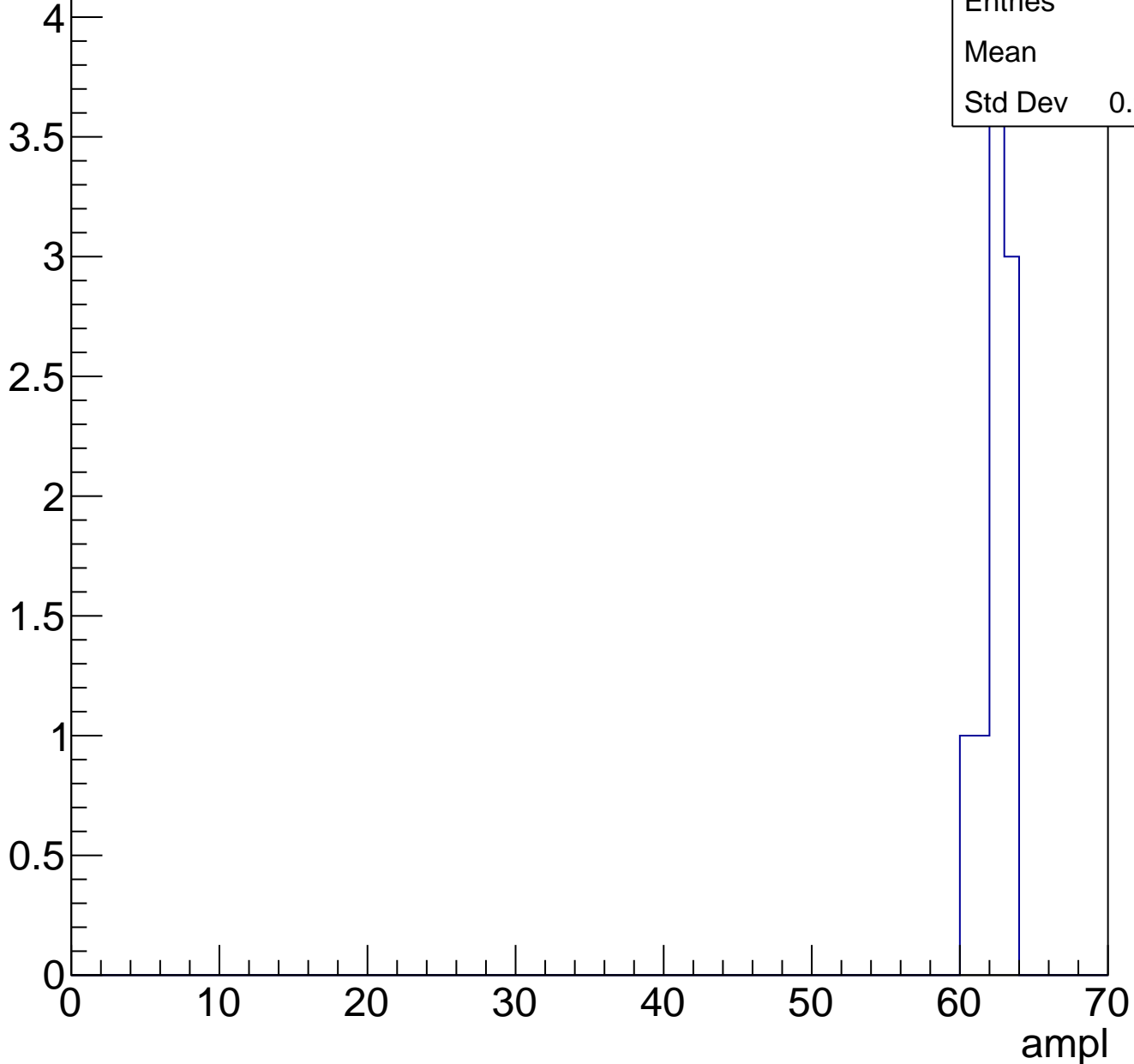
Entry



# B0L000S, U19-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	9
Mean	62
Std Dev	0.9428



# B0L000S, U19-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch81, adc0

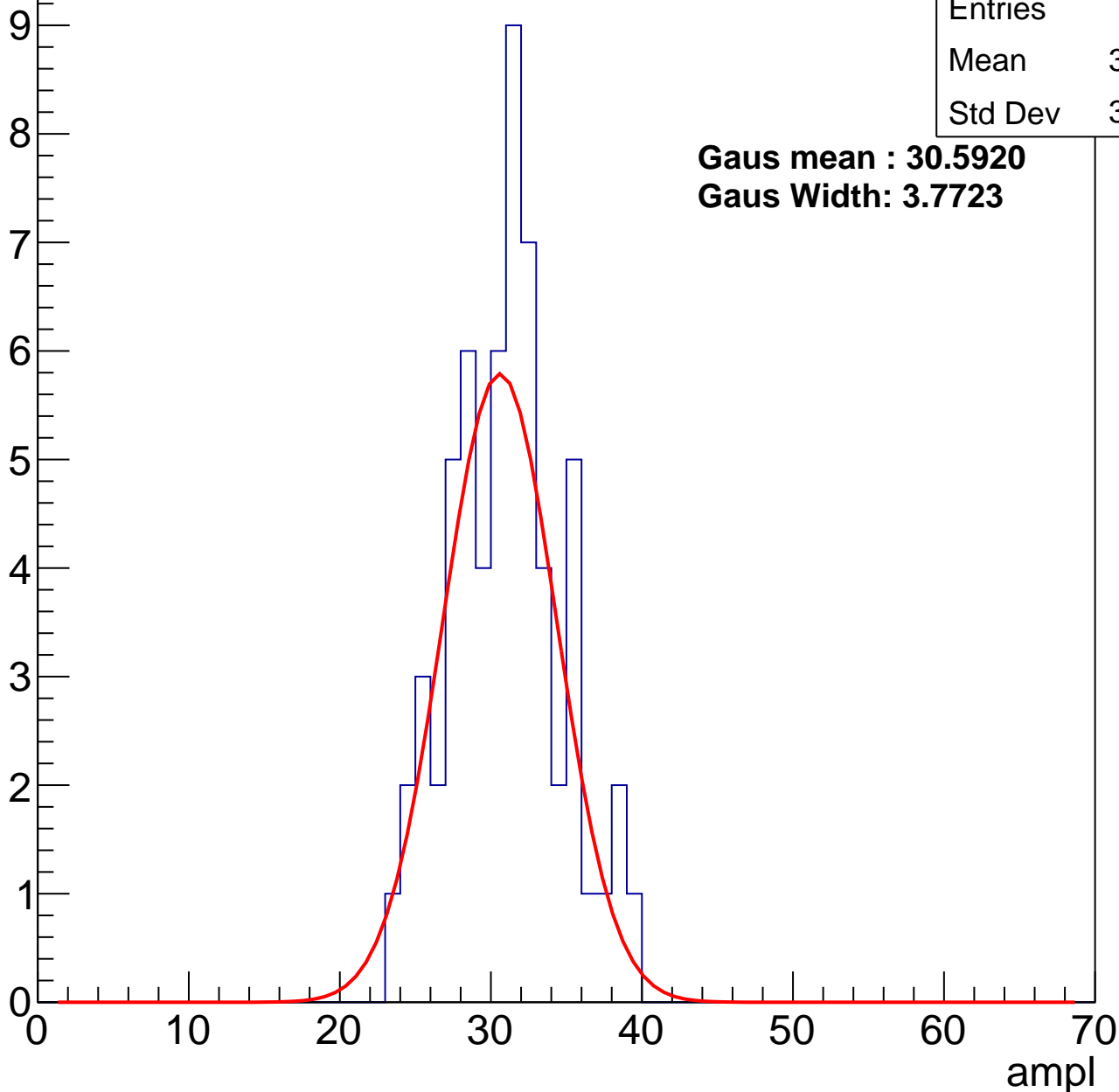
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	30.54
Std Dev	3.638

**Gaus mean : 30.5920**

**Gaus Width: 3.7723**



# B0L000S, U19-ch81, adc1

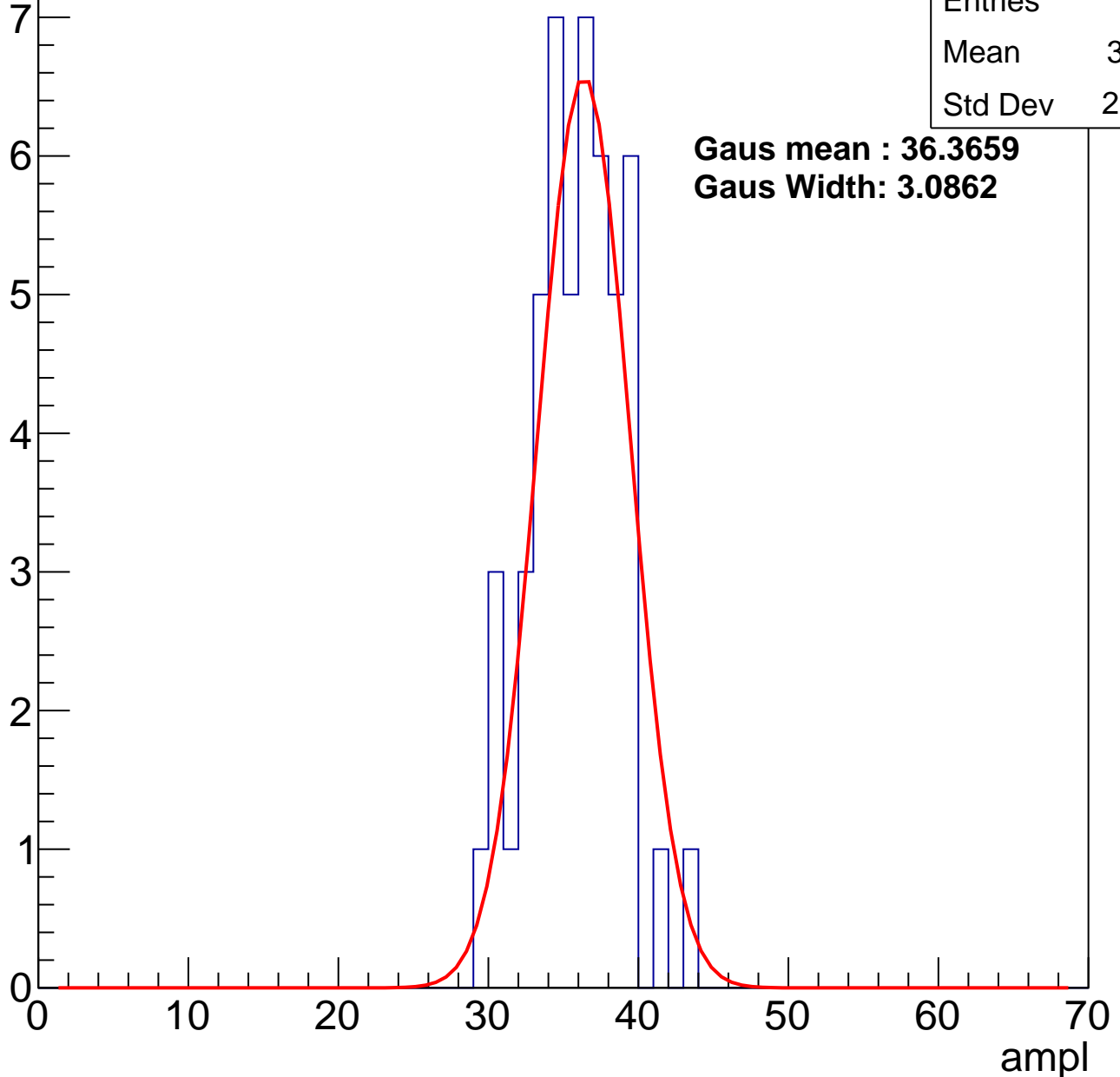
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	35.41
Std Dev	2.952

**Gaus mean : 36.3659**

**Gaus Width: 3.0862**



# B0L000S, U19-ch81, adc2

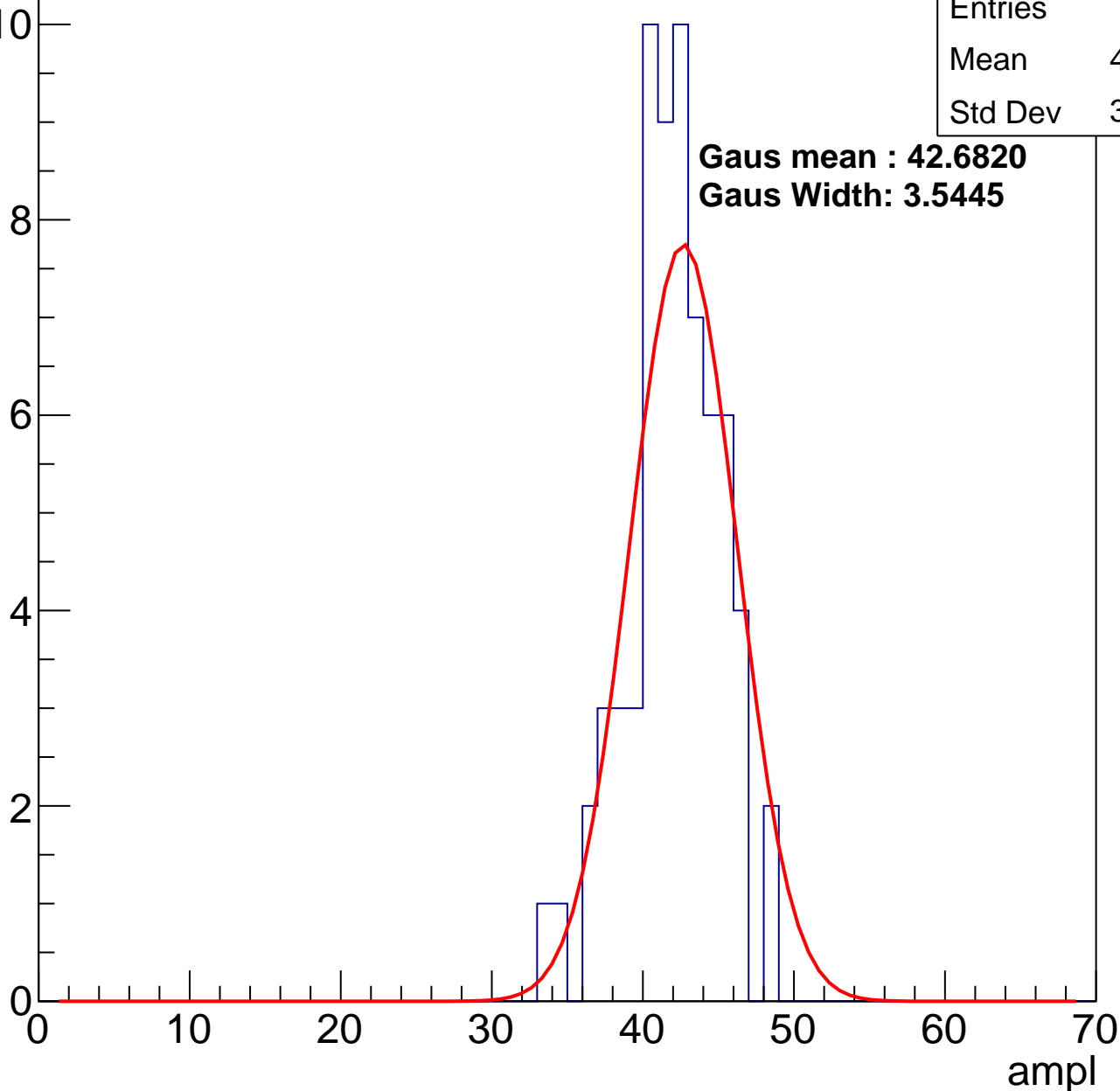
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	41.57
Std Dev	3.053

**Gaus mean : 42.6820**

**Gaus Width: 3.5445**



# B0L000S, U19-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

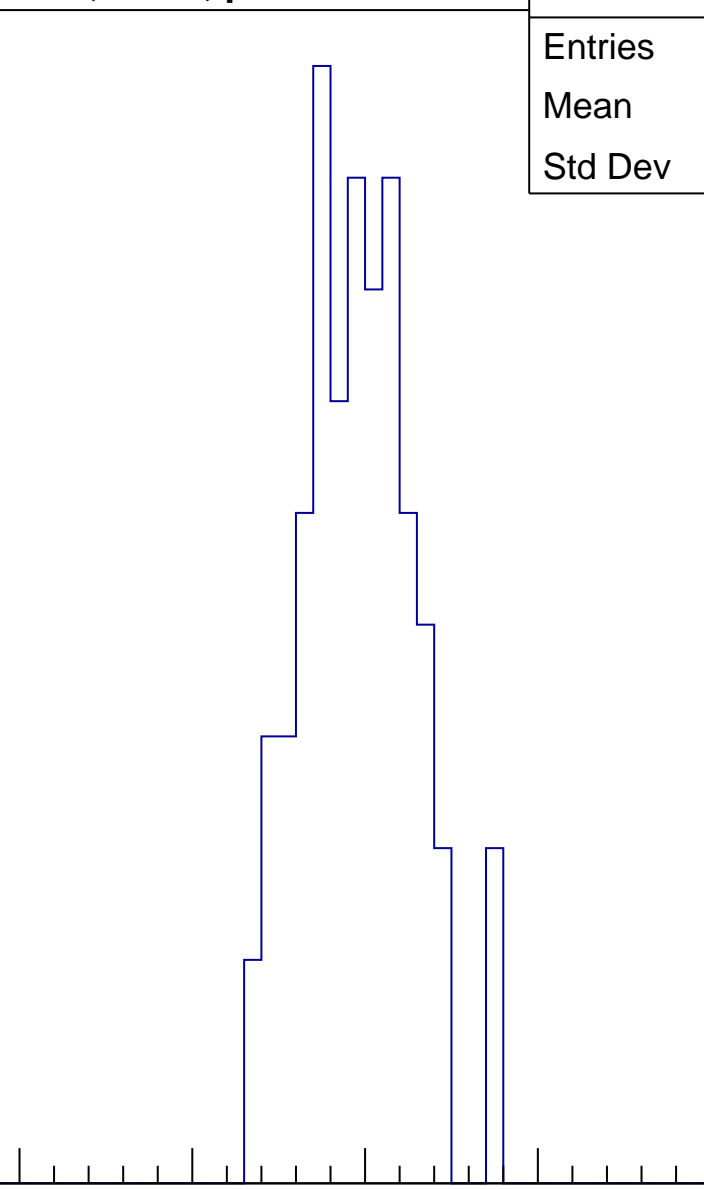
Entries	76
Mean	49.13
Std Dev	3.205

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

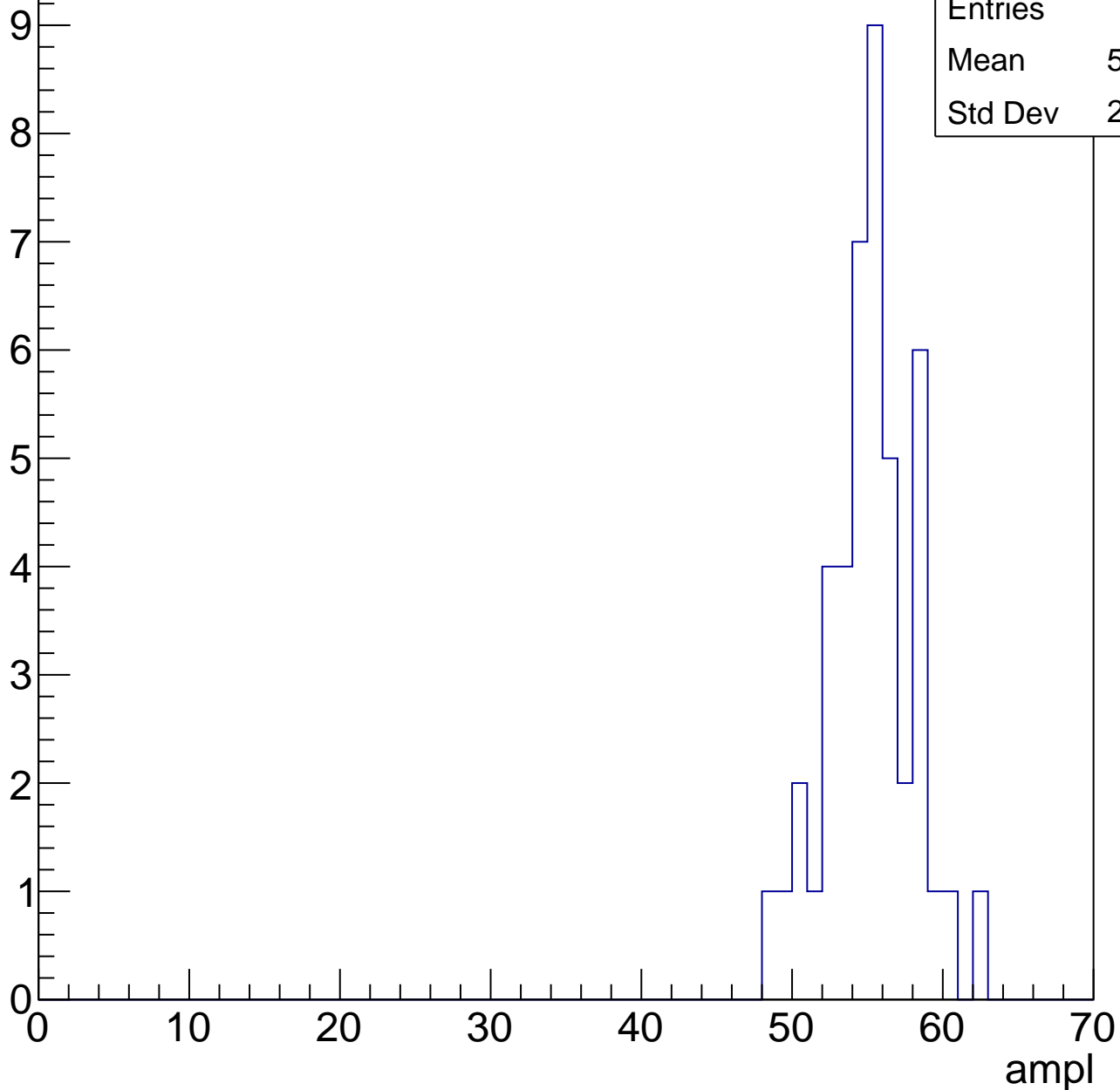
ampl



# B0L000S, U19-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

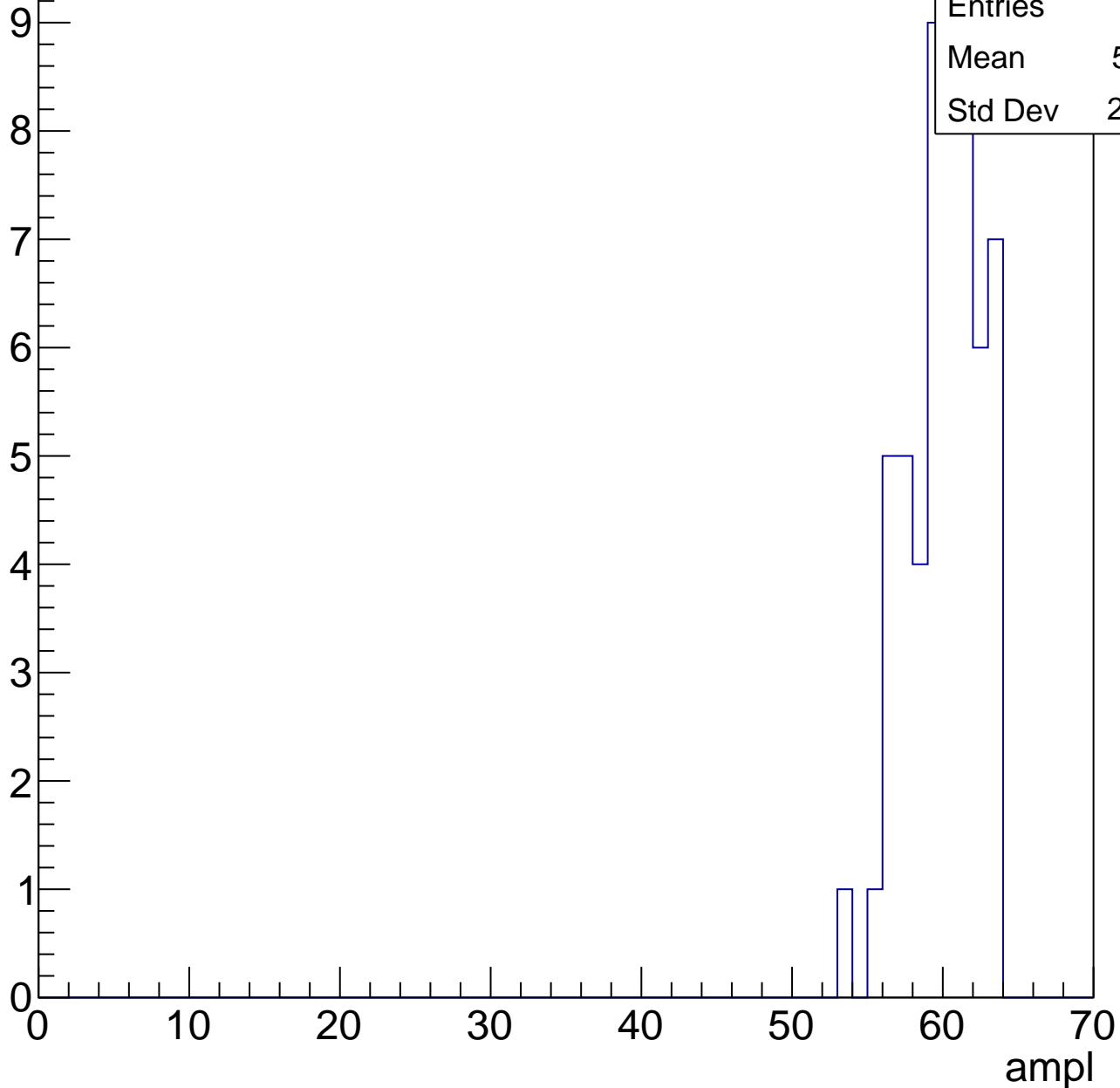
Entry



# B0L000S, U19-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

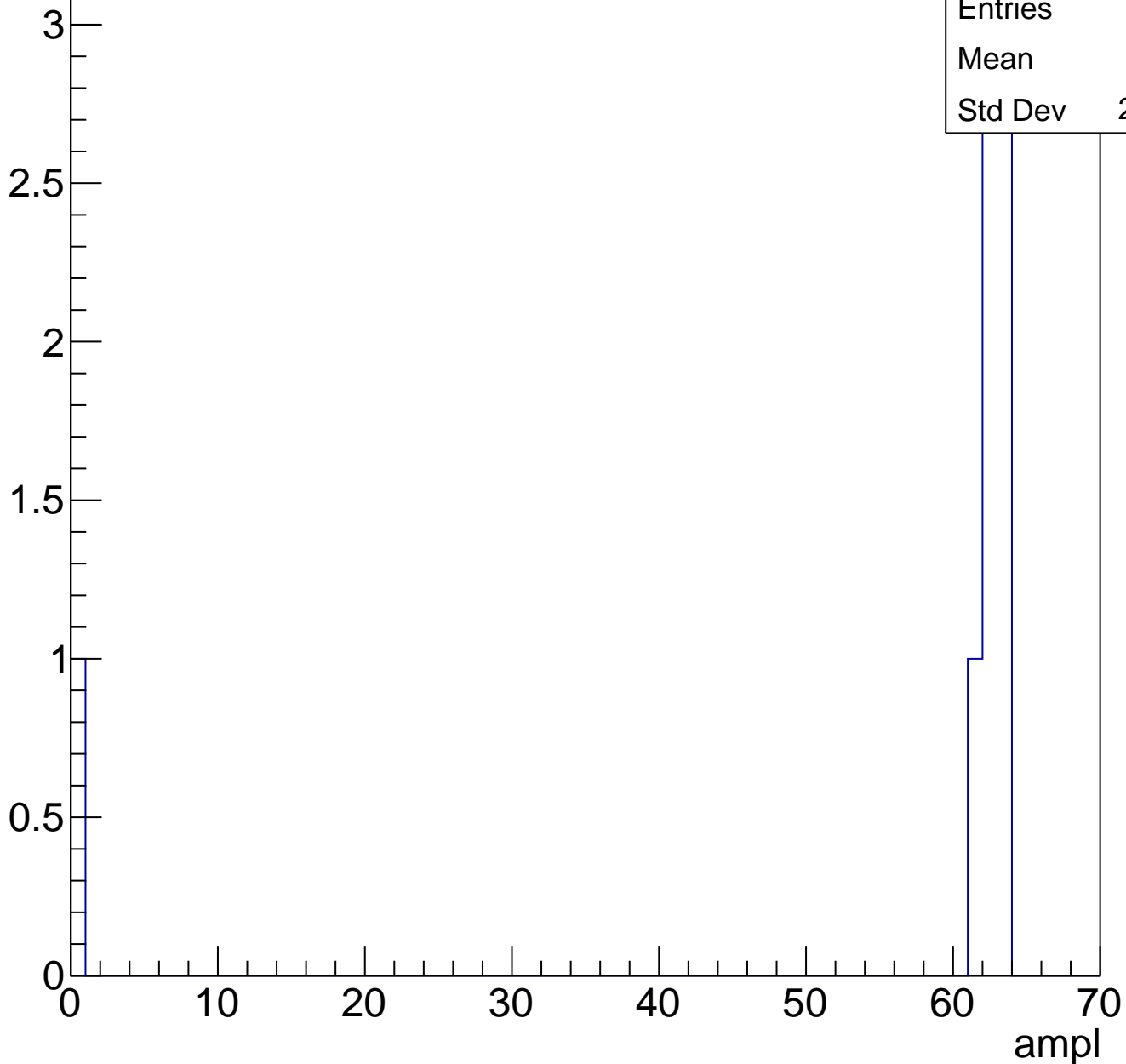


Entries	56
Mean	59.61
Std Dev	2.358

# B0L000S, U19-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	8
Mean	54.5
Std Dev	20.61



# B0L000S, U19-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch82, adc0

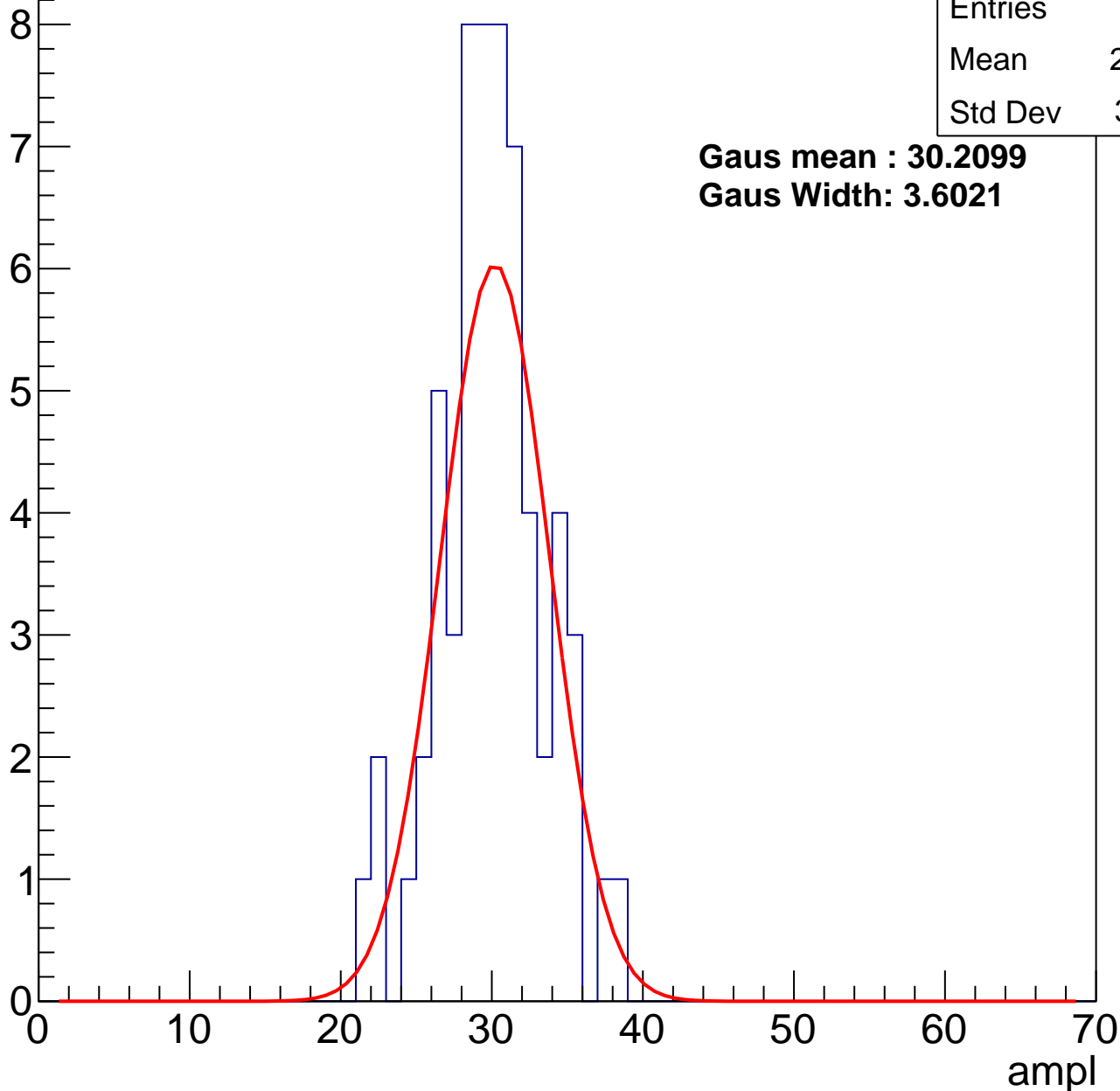
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.55
Std Dev	3.471

**Gaus mean : 30.2099**

**Gaus Width: 3.6021**



# B0L000S, U19-ch82, adc1

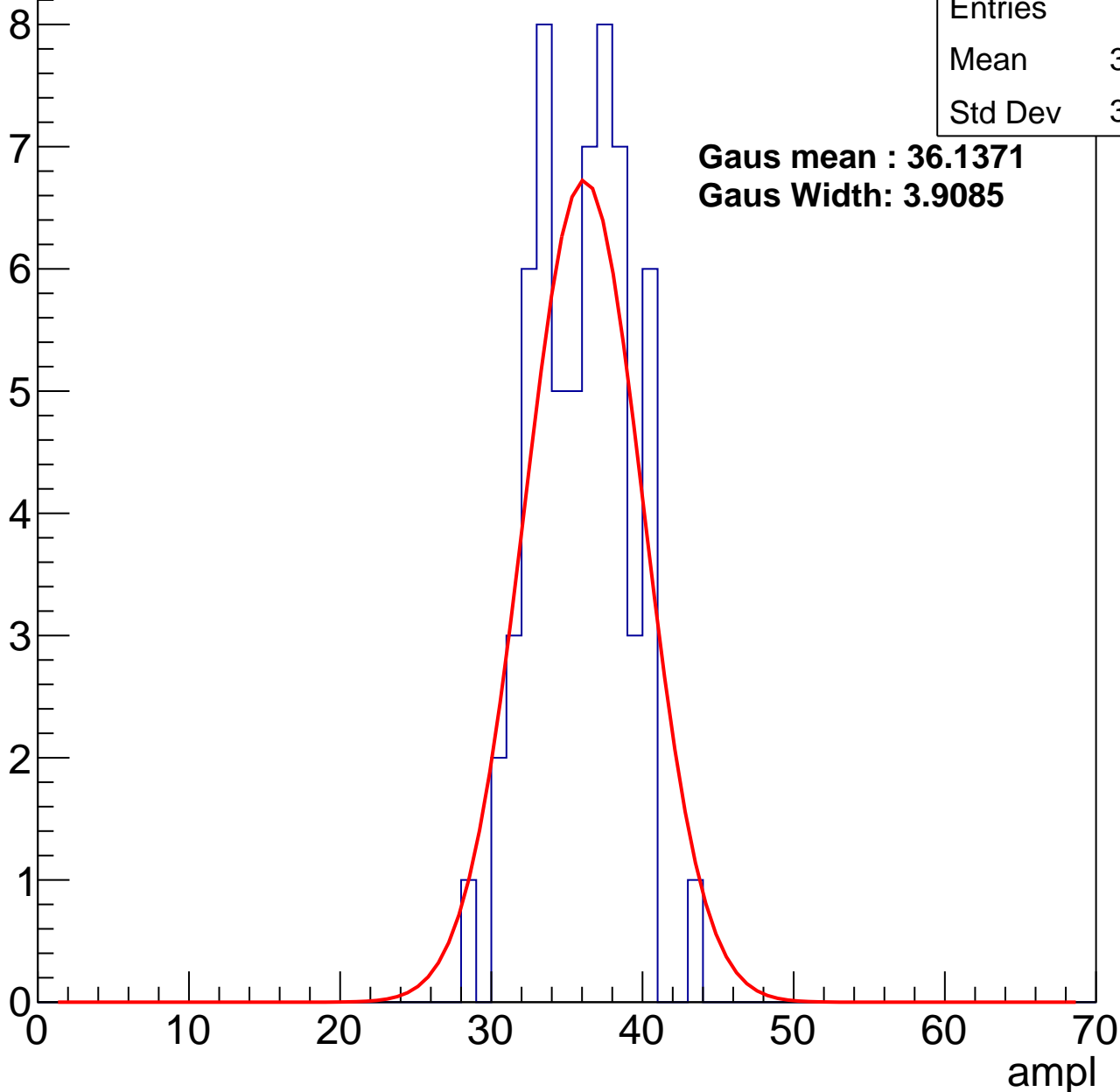
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	35.42
Std Dev	3.088

**Gaus mean : 36.1371**

**Gaus Width: 3.9085**



# B0L000S, U19-ch82, adc2

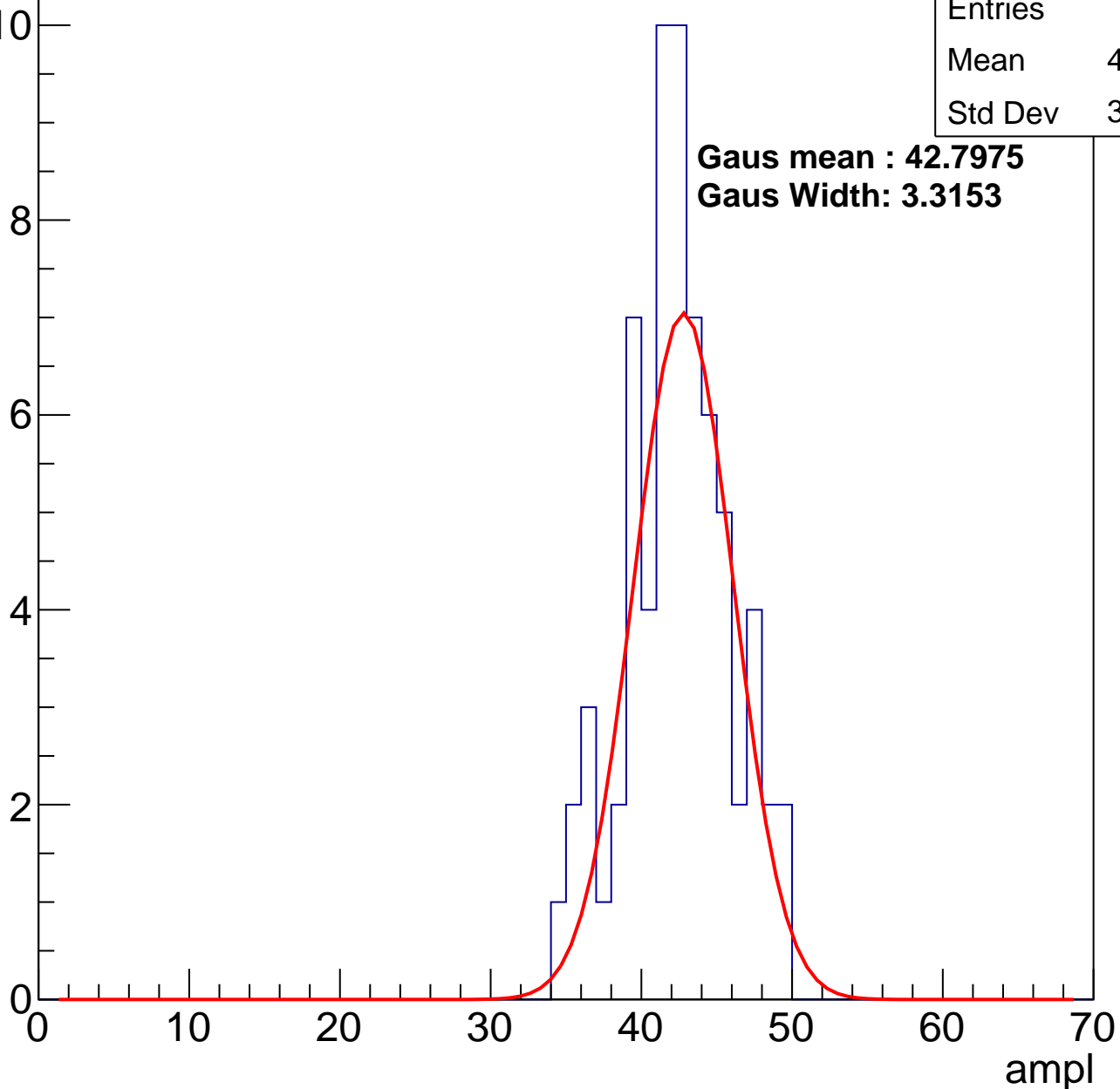
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	41.94
Std Dev	3.417

**Gaus mean : 42.7975**

**Gaus Width: 3.3153**

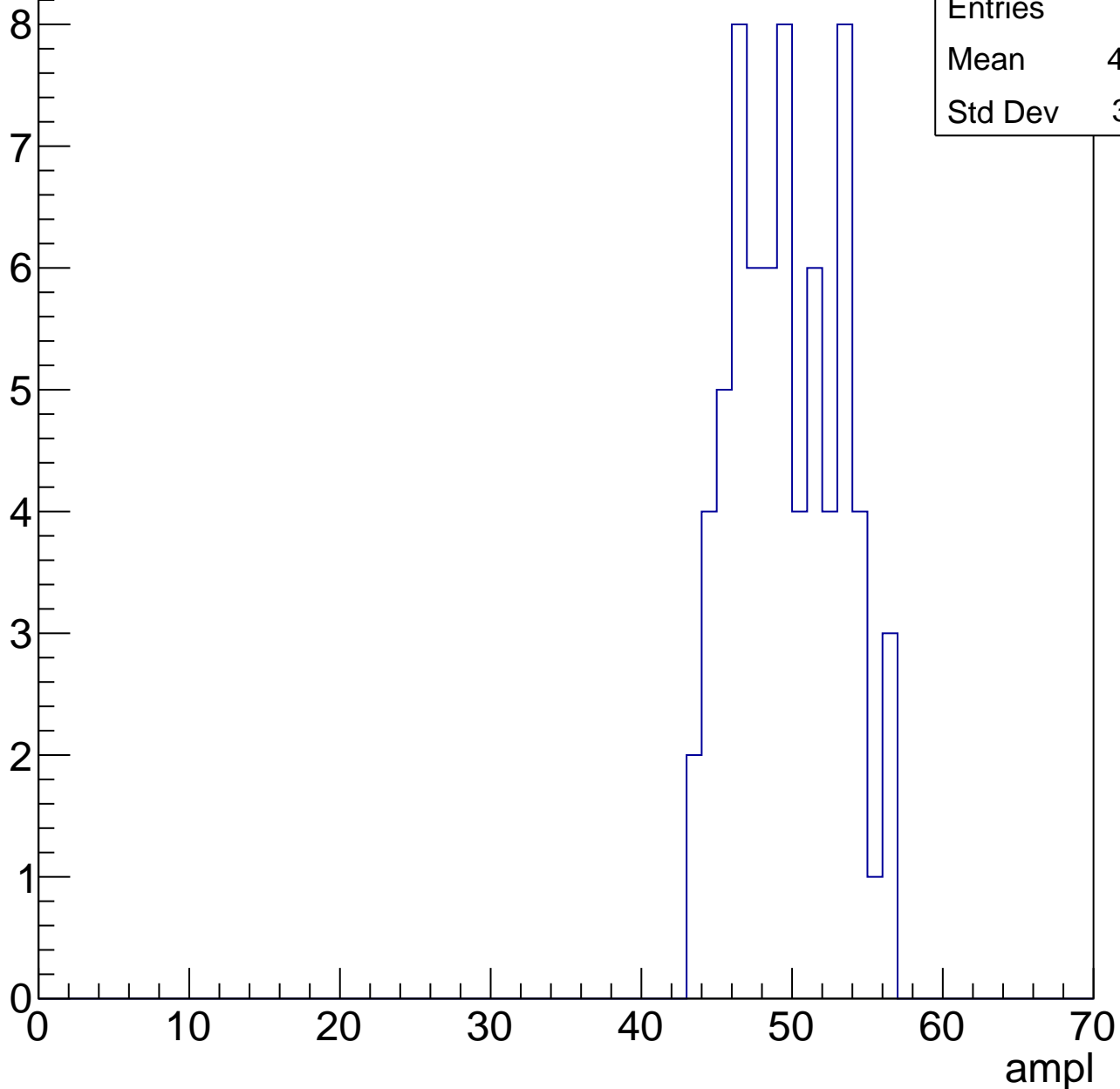


# B0L000S, U19-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

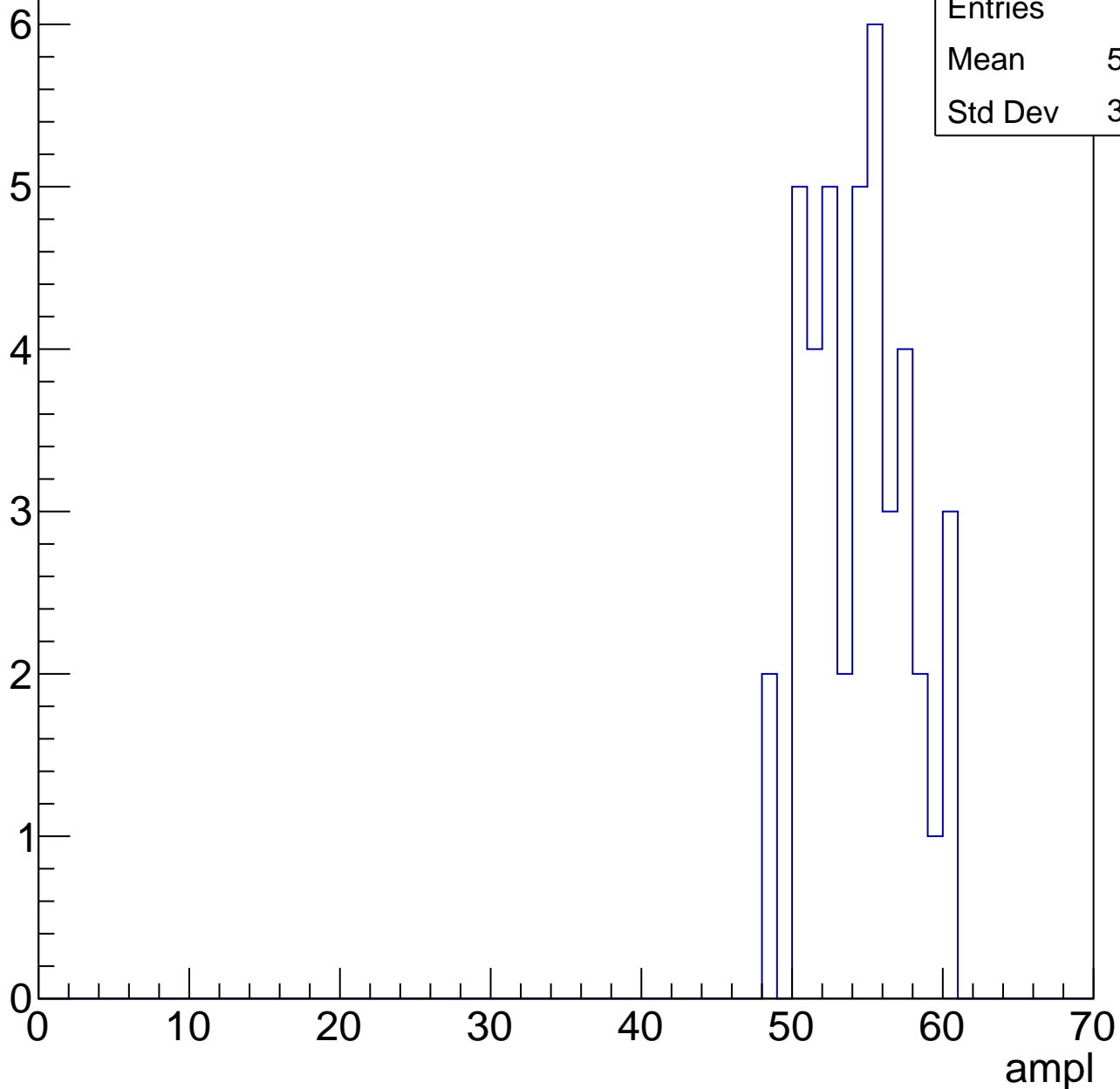
Entries	69
Mean	49.19
Std Dev	3.461



# B0L000S, U19-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

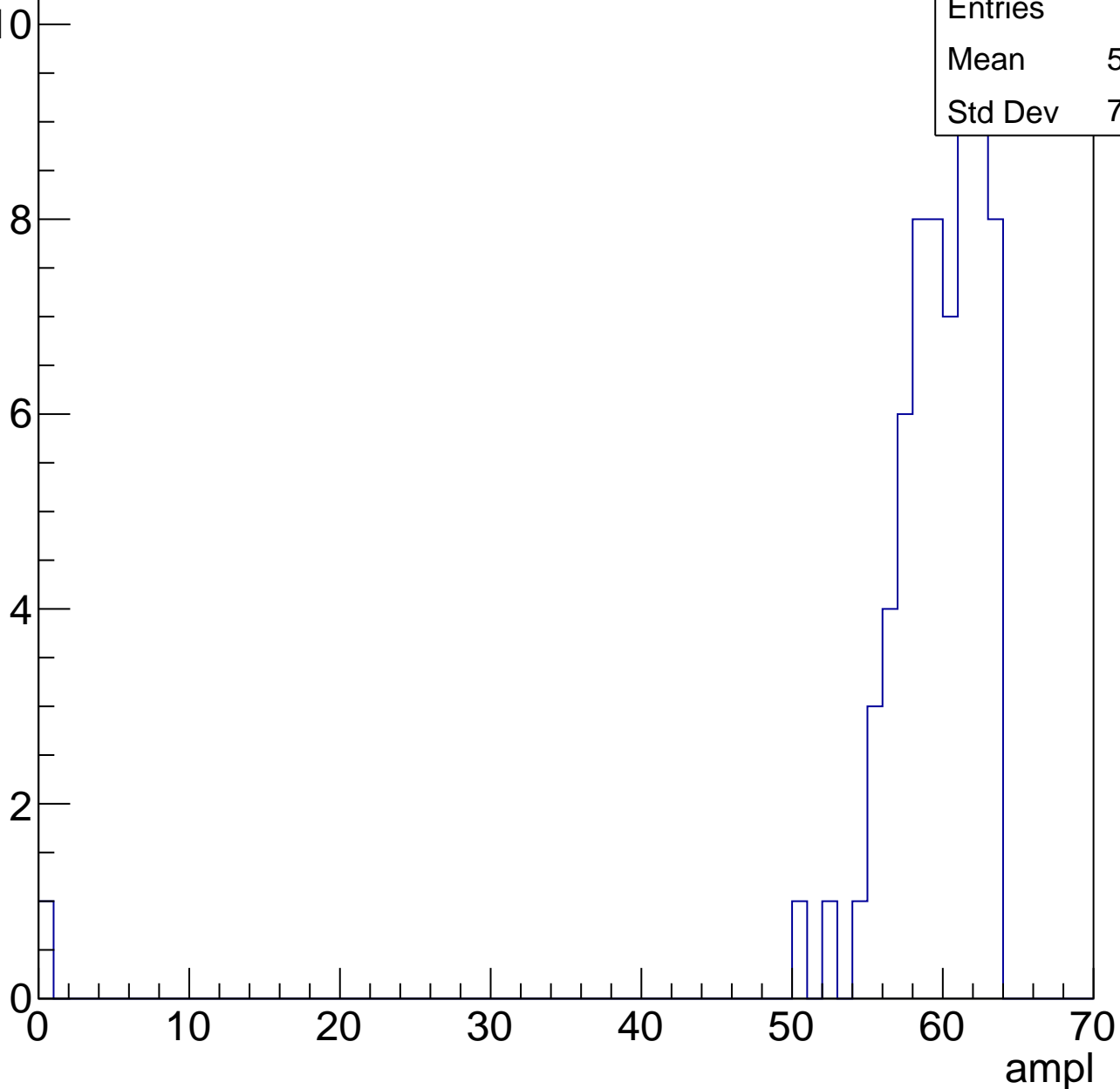


# B0L000S, U19-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

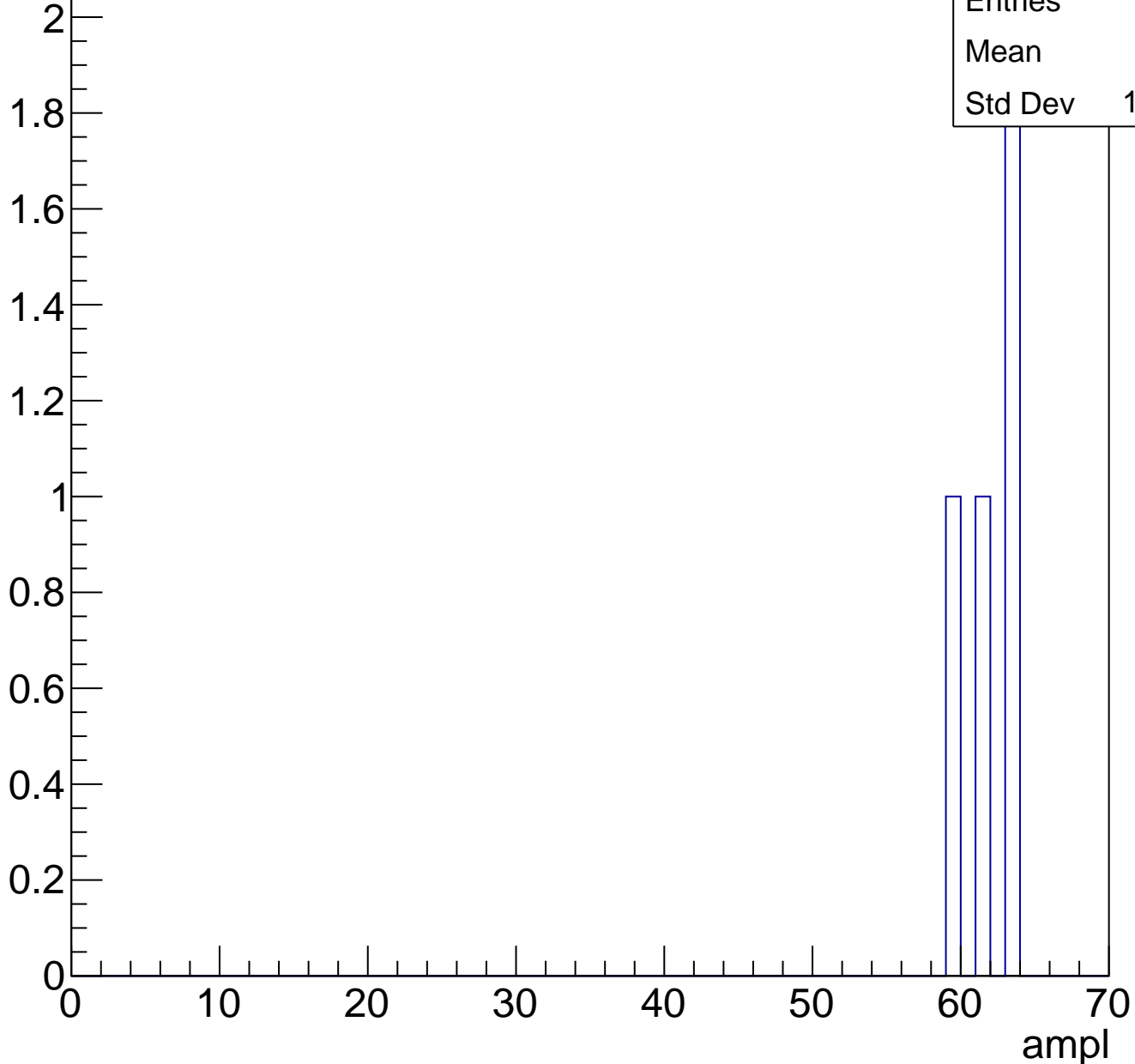
Entries	67
Mean	58.45
Std Dev	7.719



# B0L000S, U19-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	61.5
Std Dev	1.658



# B0L000S, U19-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch83, adc0

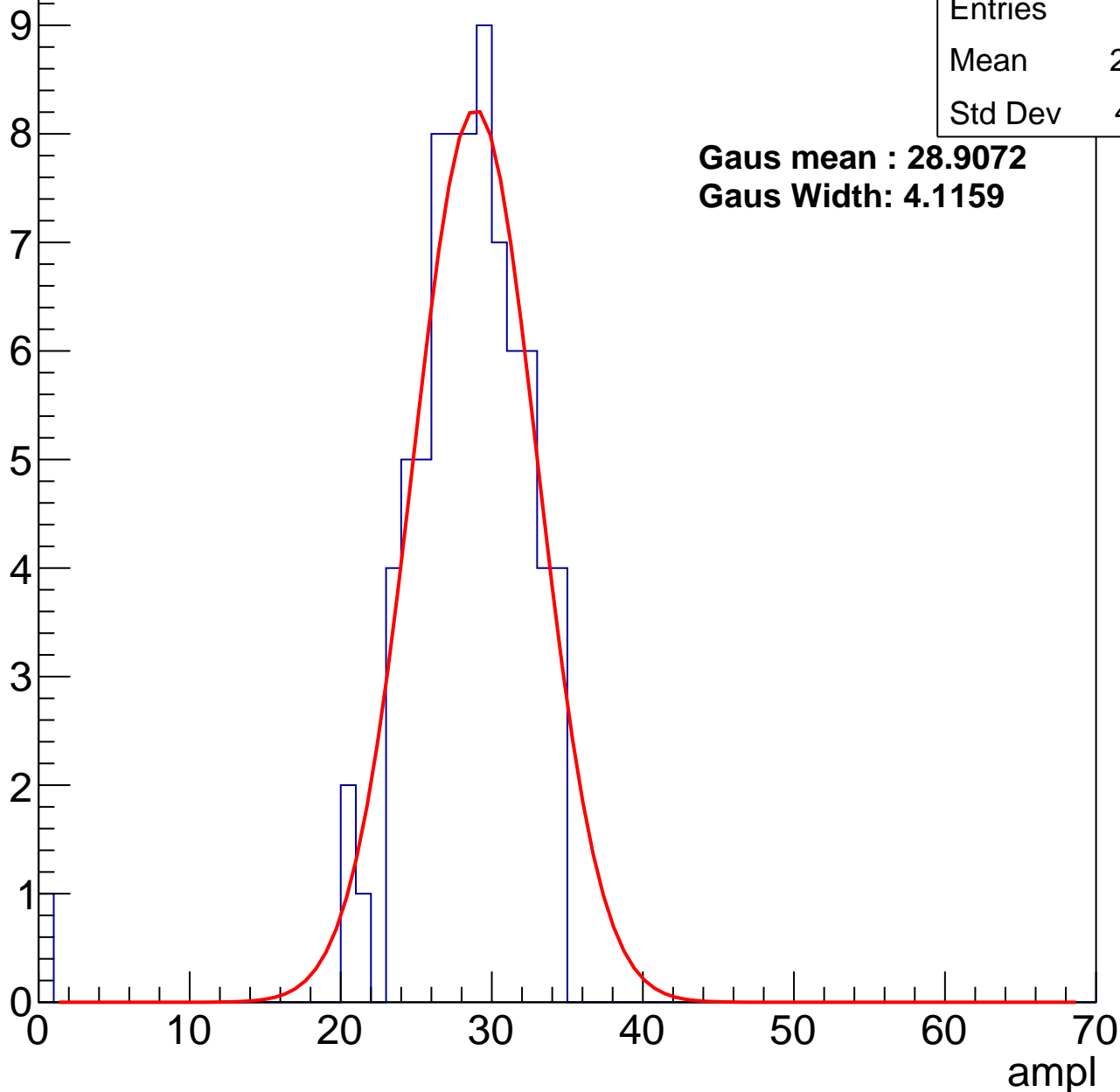
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	27.73
Std Dev	4.601

**Gaus mean : 28.9072**

**Gaus Width: 4.1159**



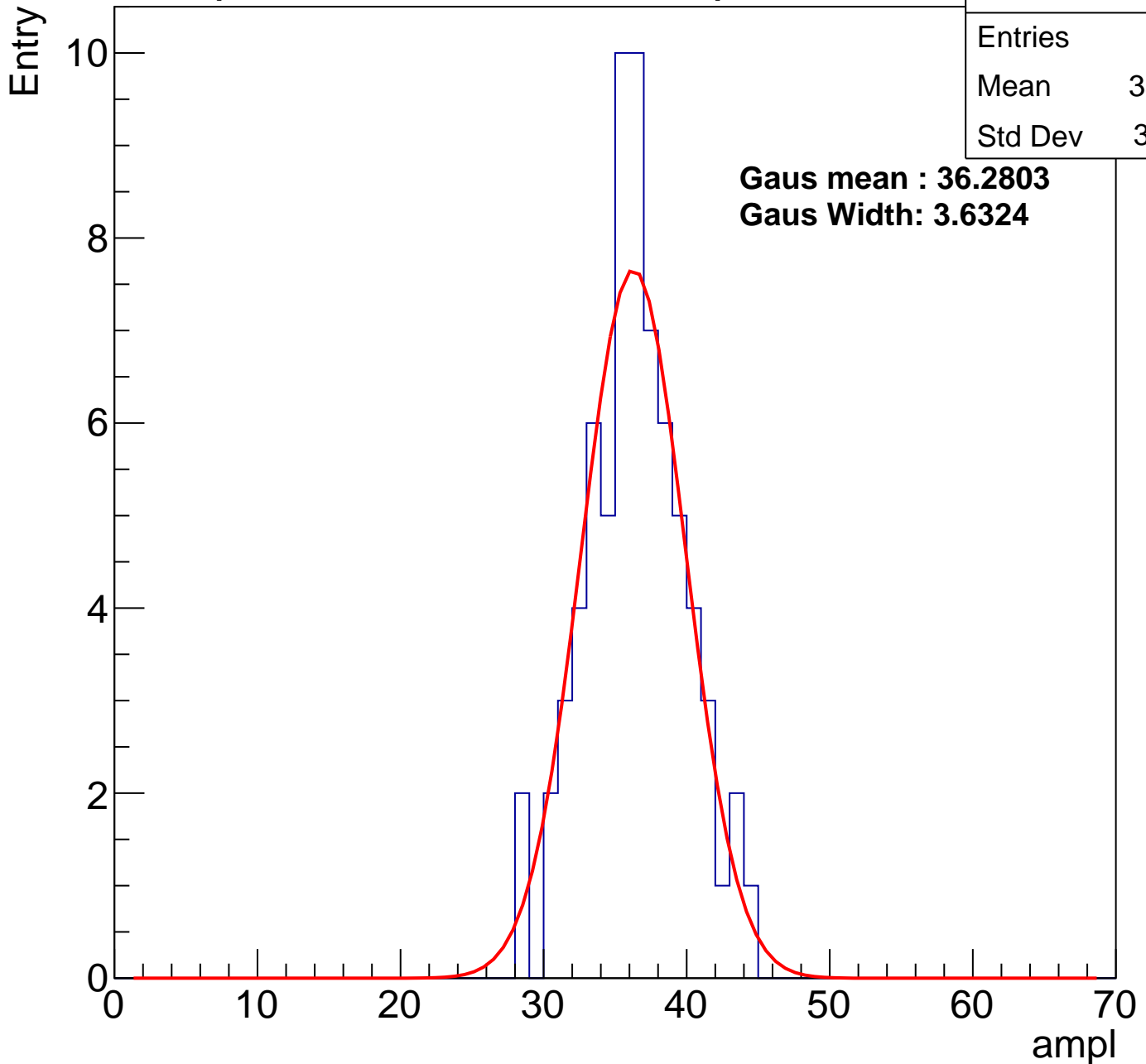
# B0L000S, U19-ch83, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	35.94
Std Dev	3.431

**Gaus mean : 36.2803**

**Gaus Width: 3.6324**



# B0L000S, U19-ch83, adc2

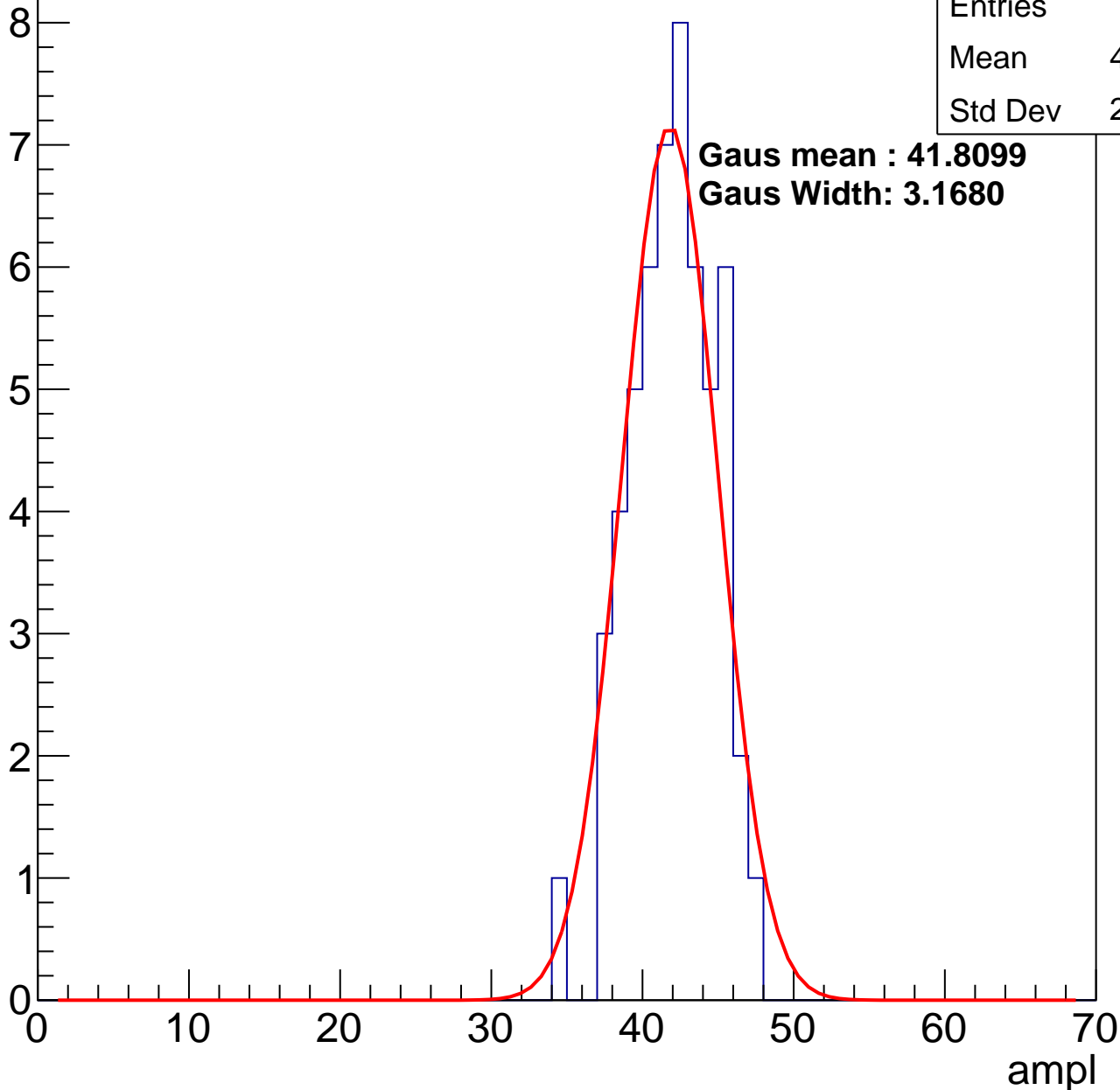
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	41.52
Std Dev	2.733

**Gaus mean : 41.8099**

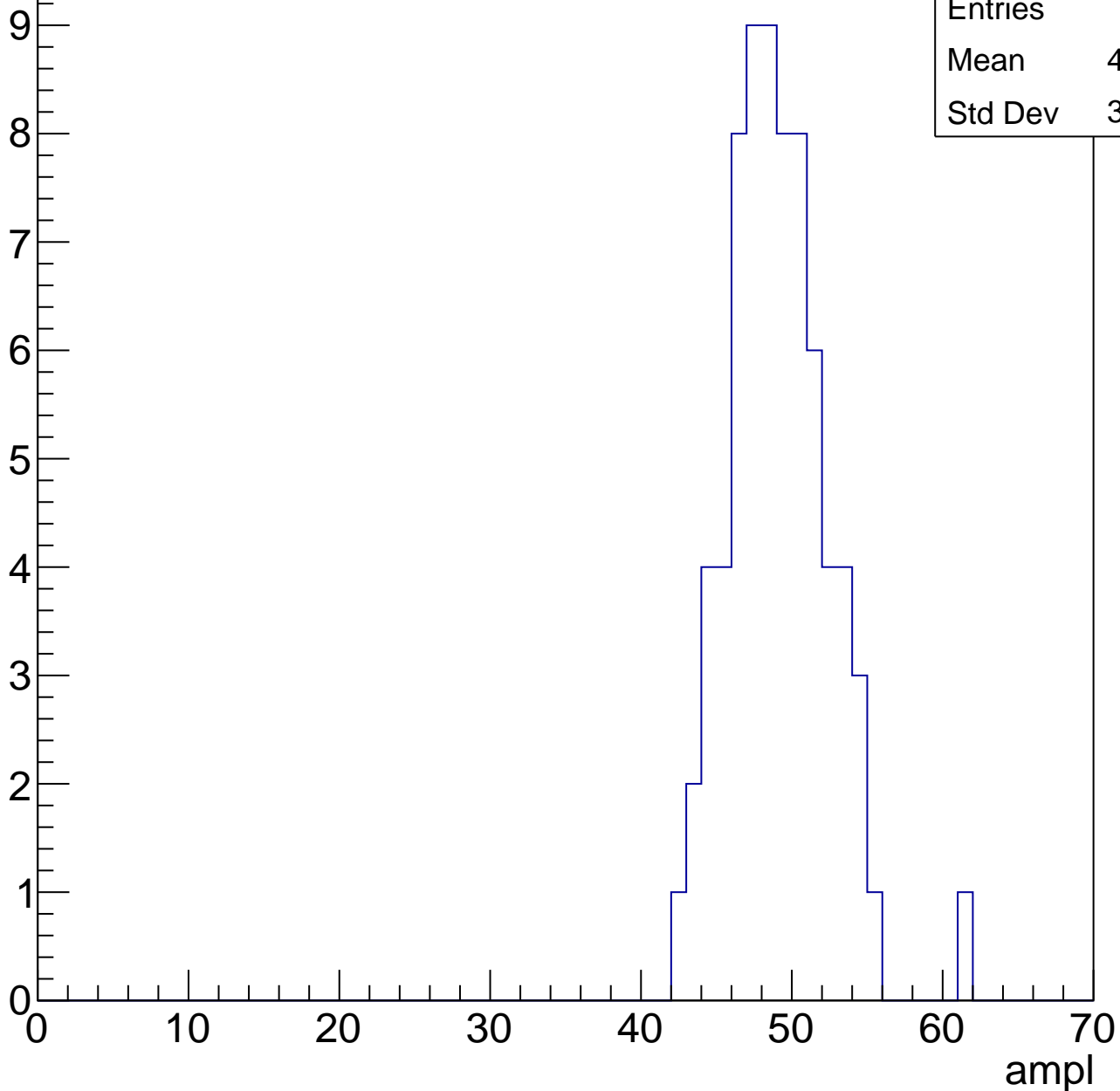
**Gaus Width: 3.1680**



# B0L000S, U19-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

6

5

4

3

2

1

0

Entries	43
Mean	54
Std Dev	2.668

0

10

20

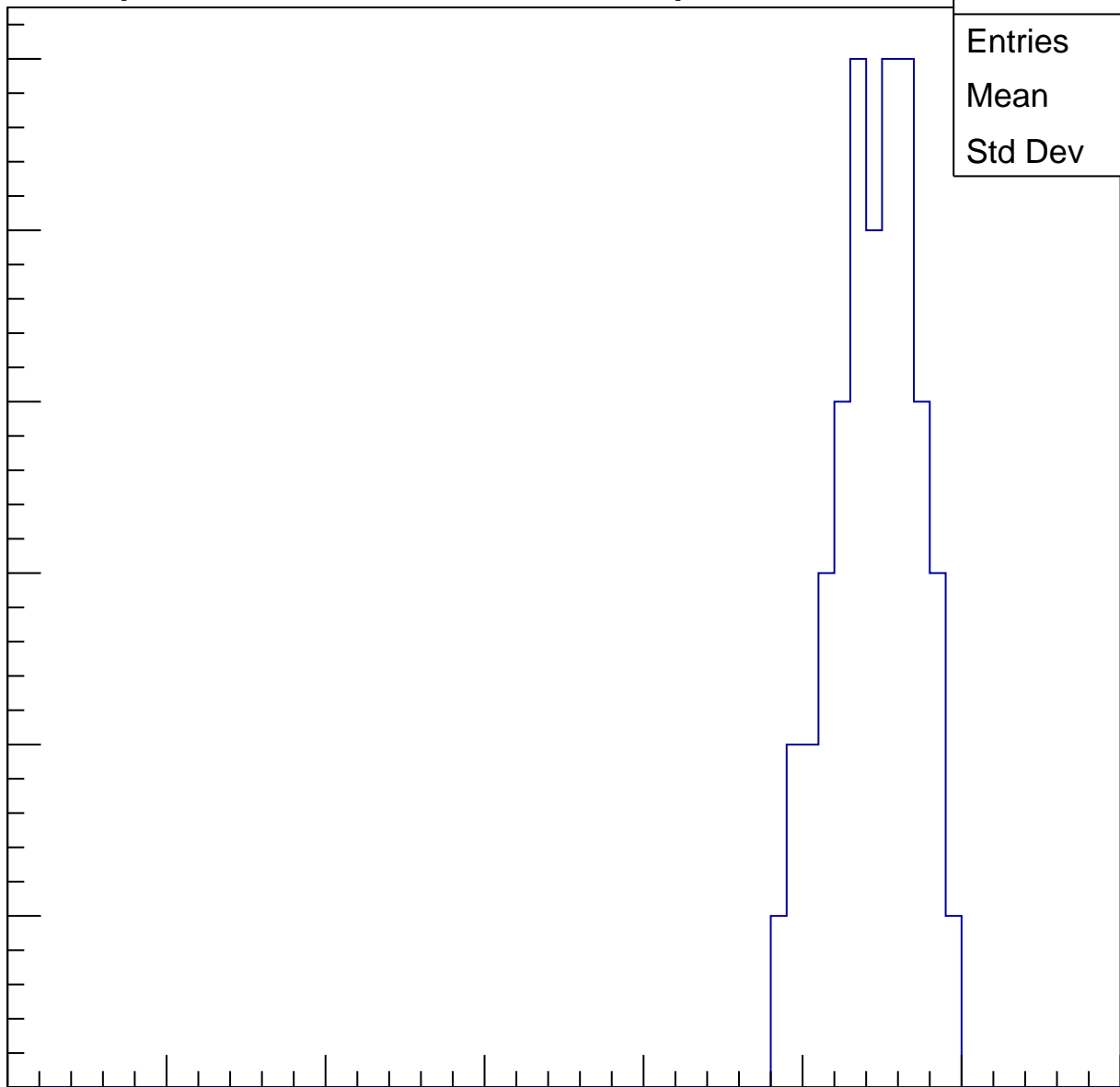
30

40

50

60

ampl

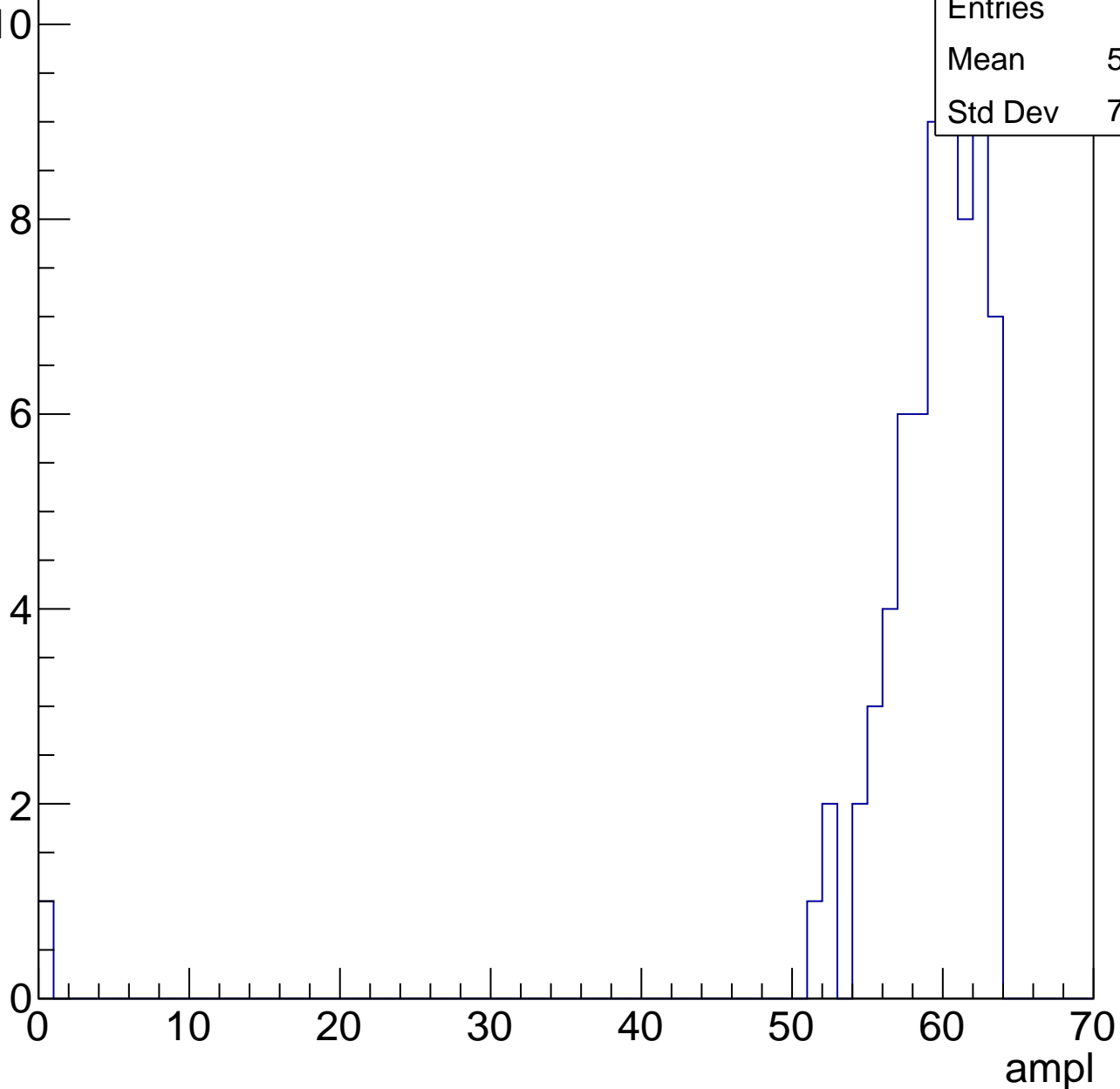


# B0L000S, U19-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

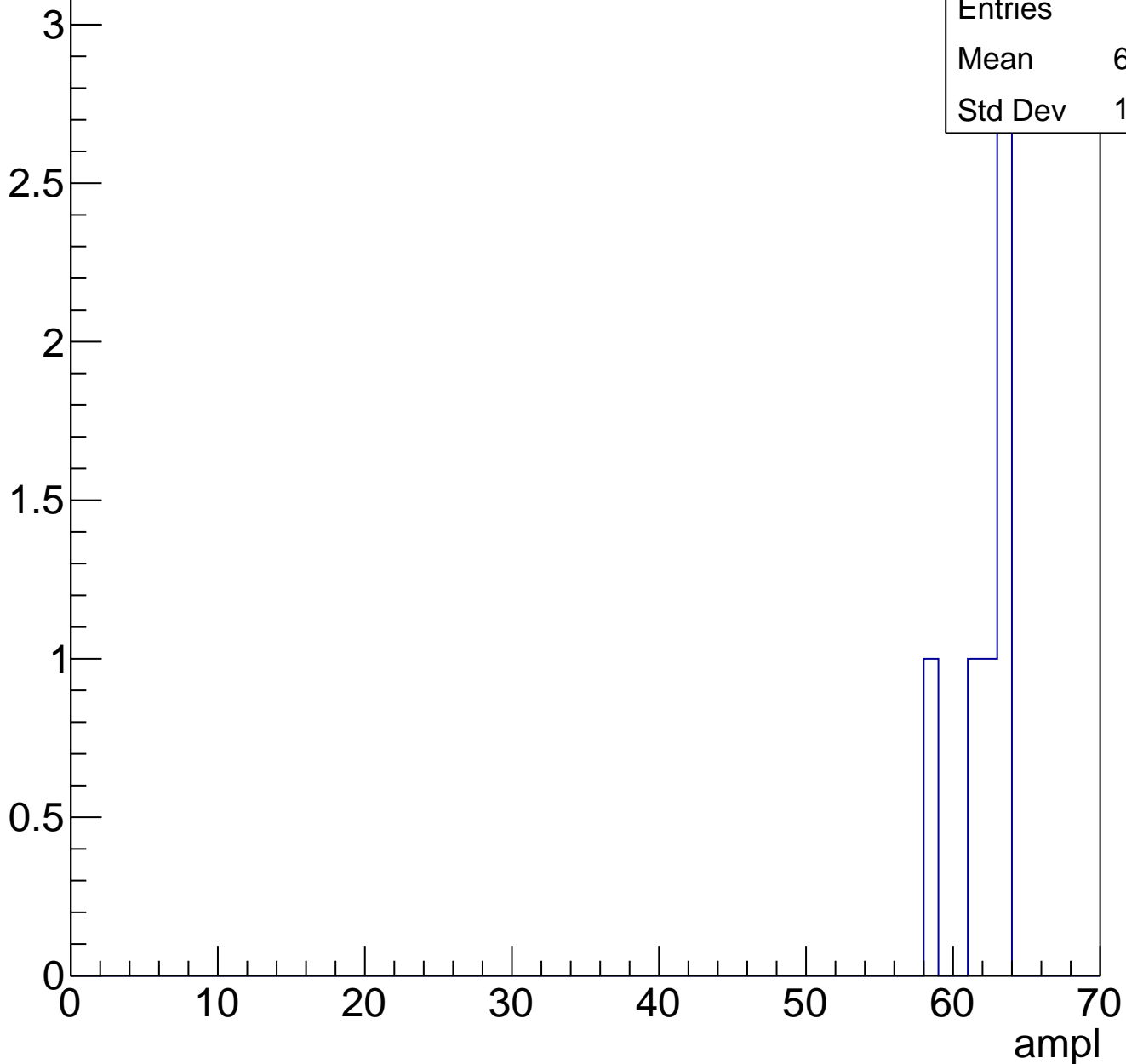
Entries	68
Mean	58.24
Std Dev	7.672



# B0L000S, U19-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch84, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	28.57
Std Dev	5.583

**Gaus mean : 29.4806**

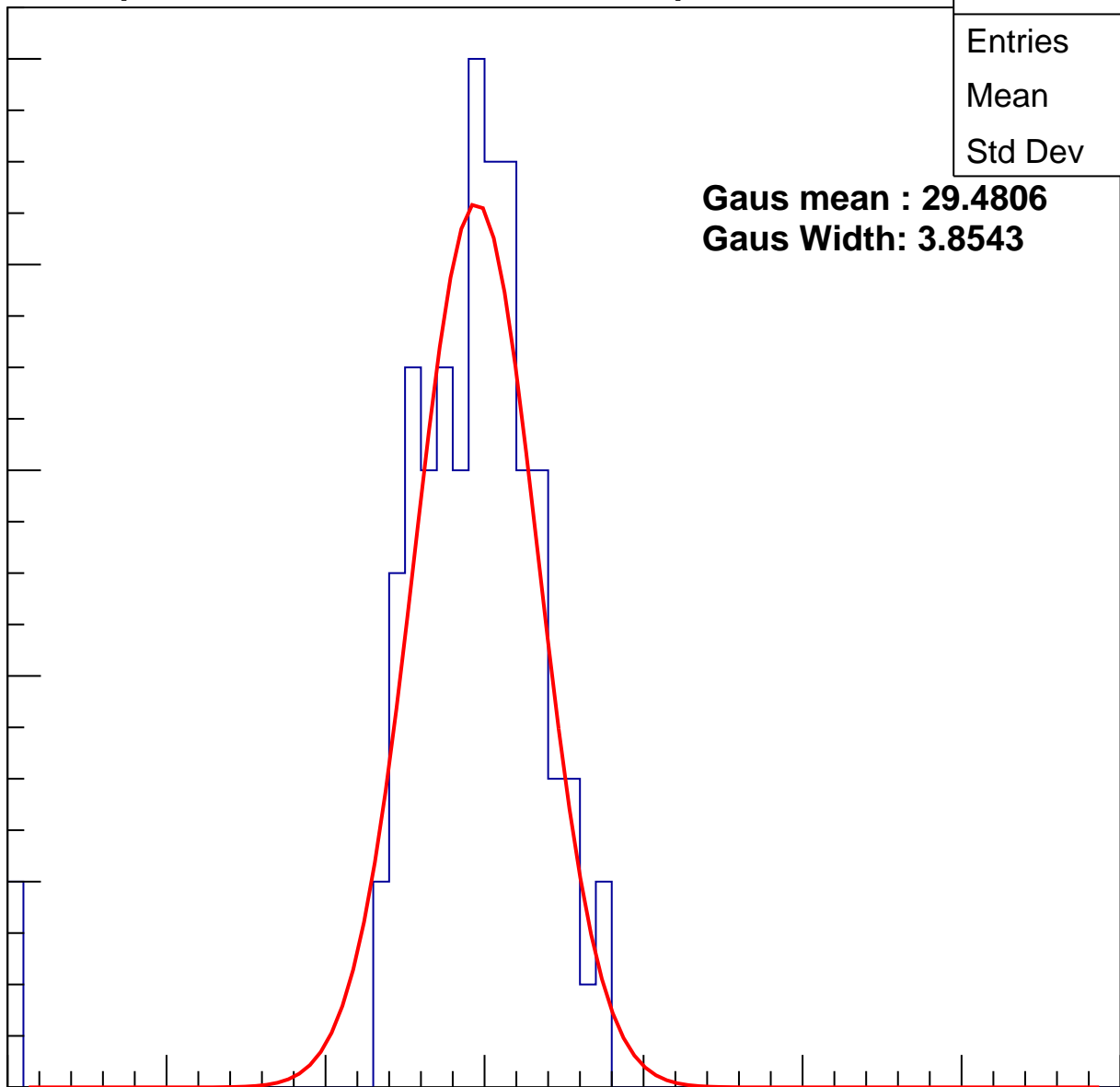
**Gaus Width: 3.8543**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch84, adc1

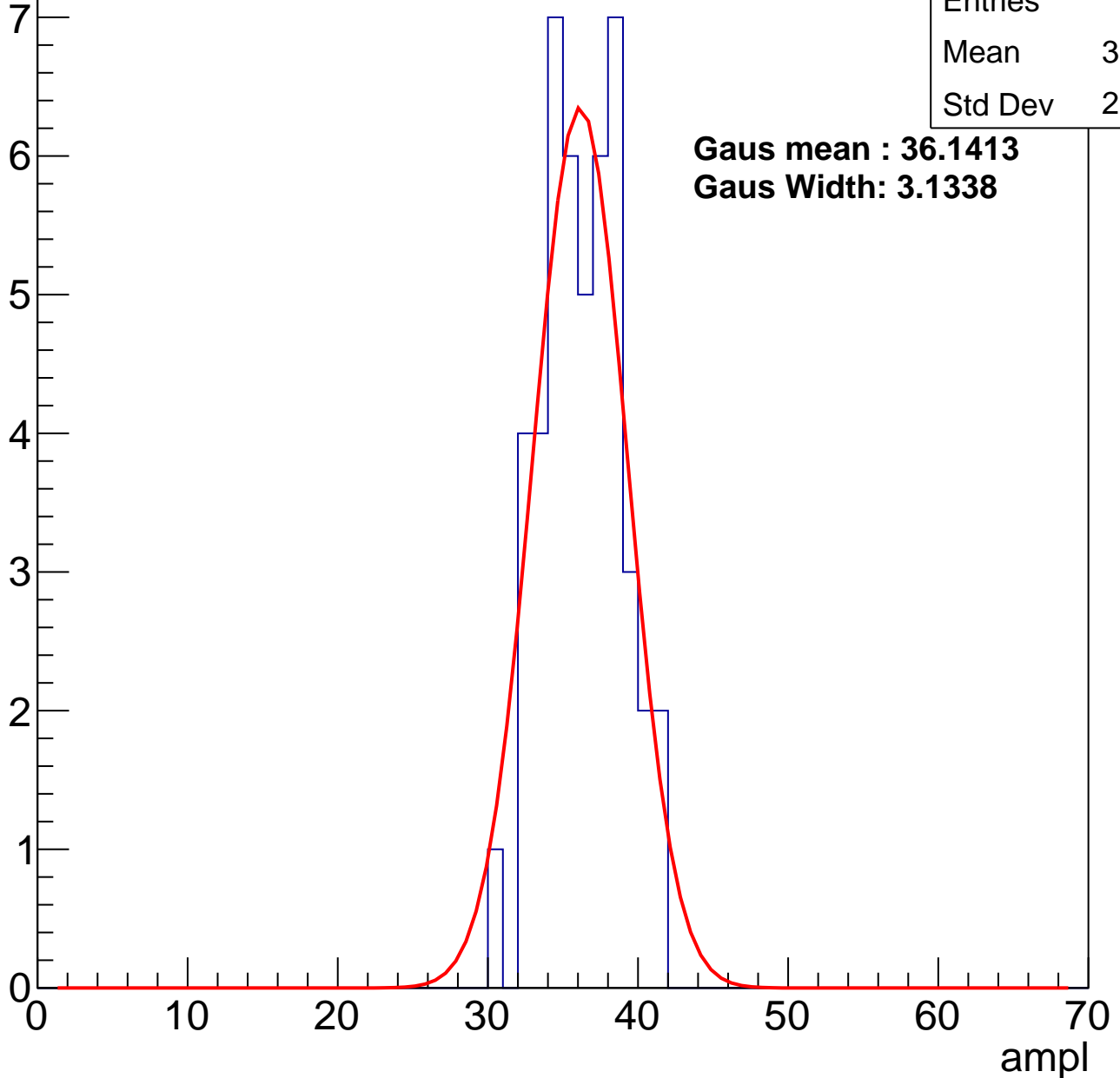
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	35.85
Std Dev	2.576

**Gaus mean : 36.1413**

**Gaus Width: 3.1338**



# B0L000S, U19-ch84, adc2

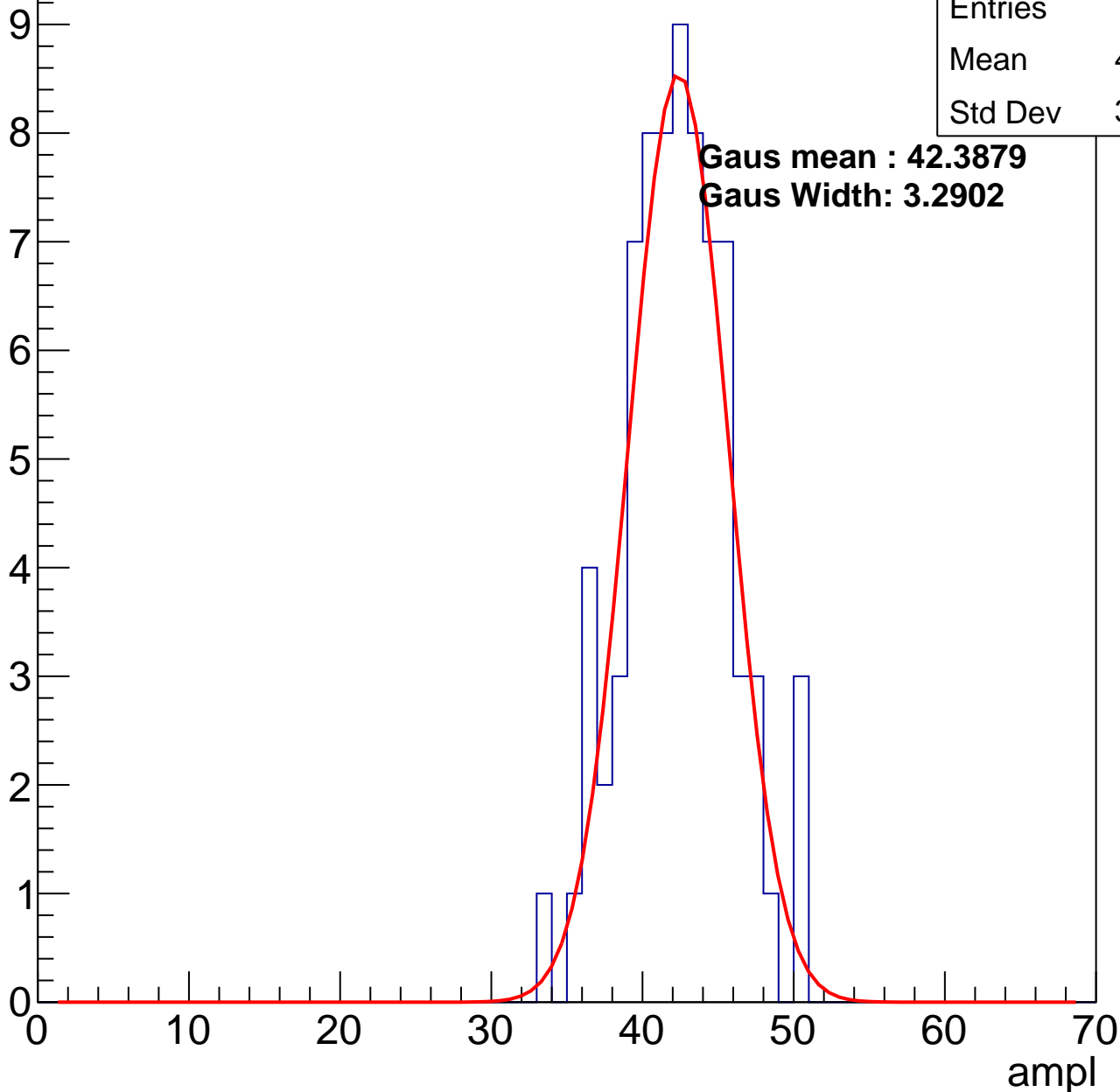
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	41.91
Std Dev	3.511

**Gaus mean : 42.3879**

**Gaus Width: 3.2902**

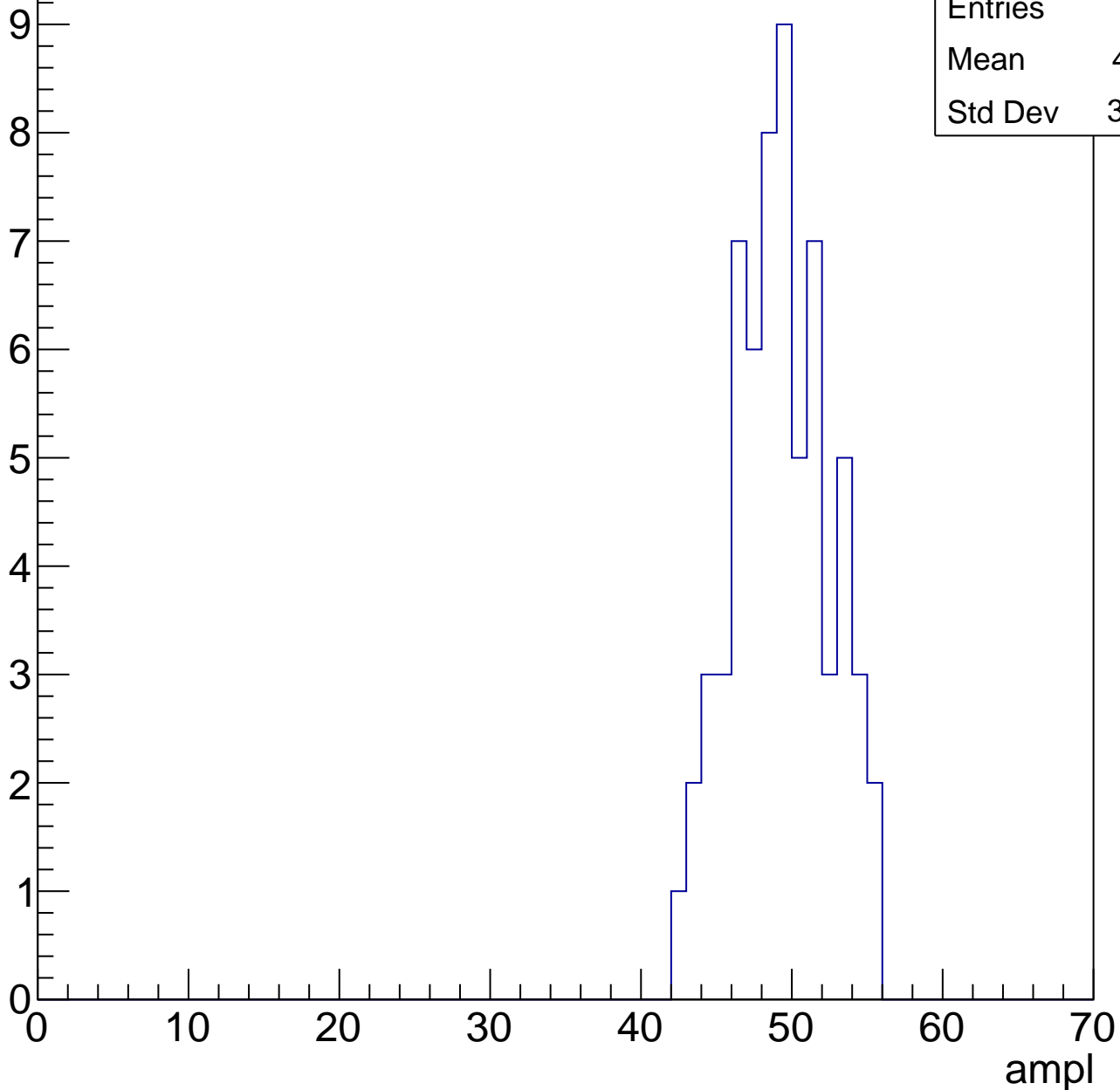


# B0L000S, U19-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	48.81
Std Dev	3.122

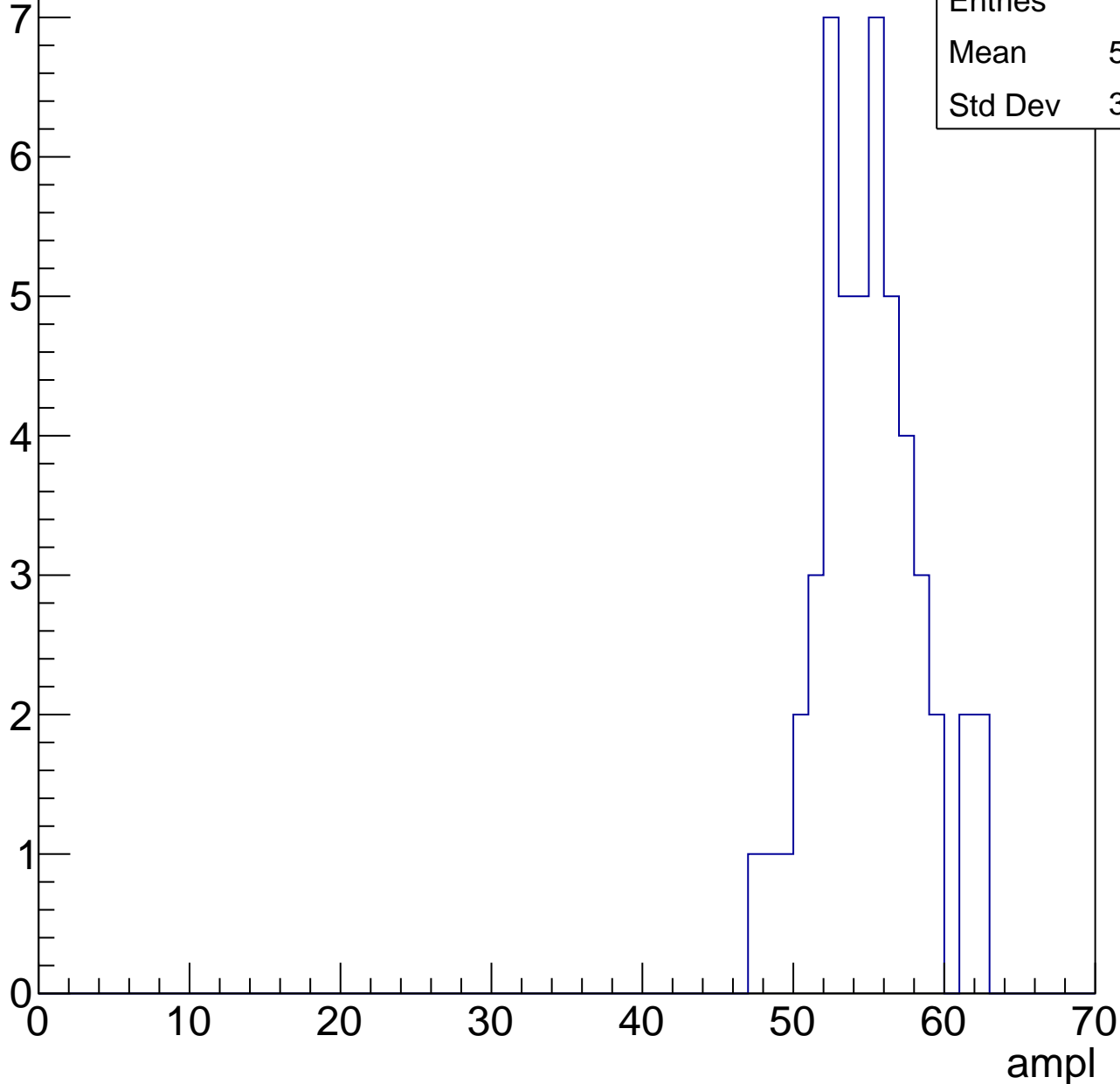


# B0L000S, U19-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

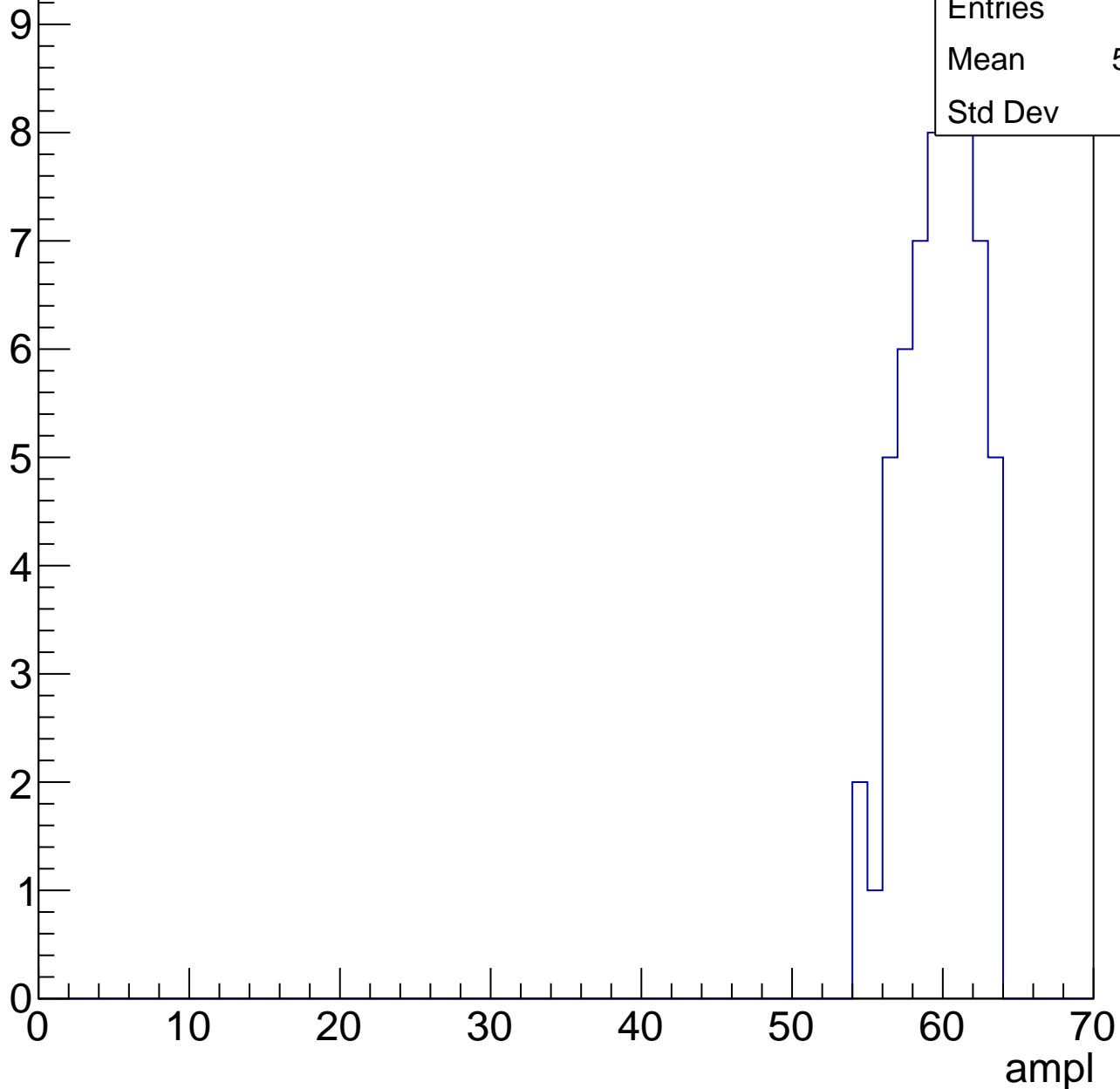
Entries	50
Mean	54.54
Std Dev	3.389



# B0L000S, U19-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

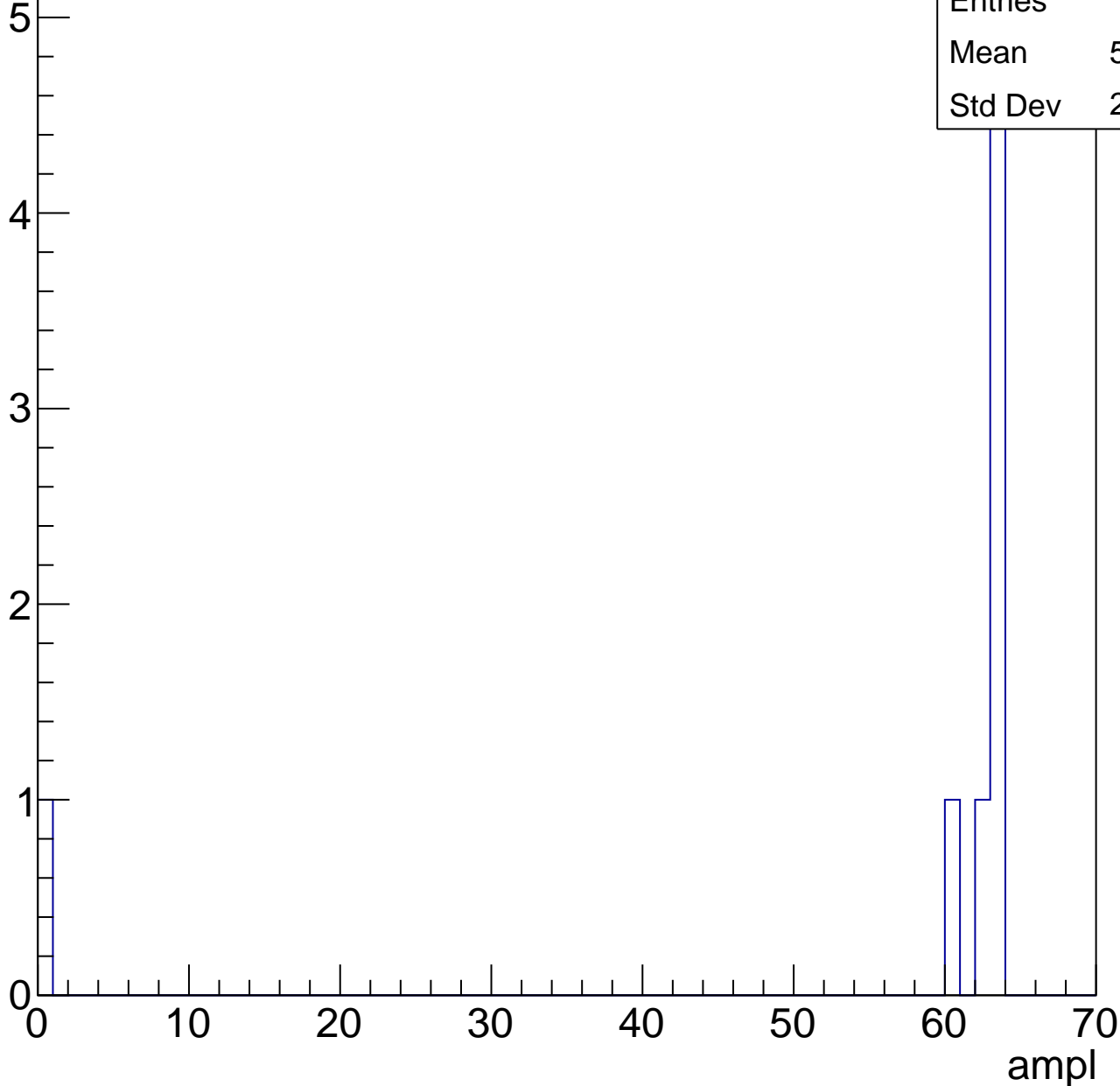


# B0L000S, U19-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	8
Mean	54.62
Std Dev	20.67





# B0L000S, U19-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch85, adc0

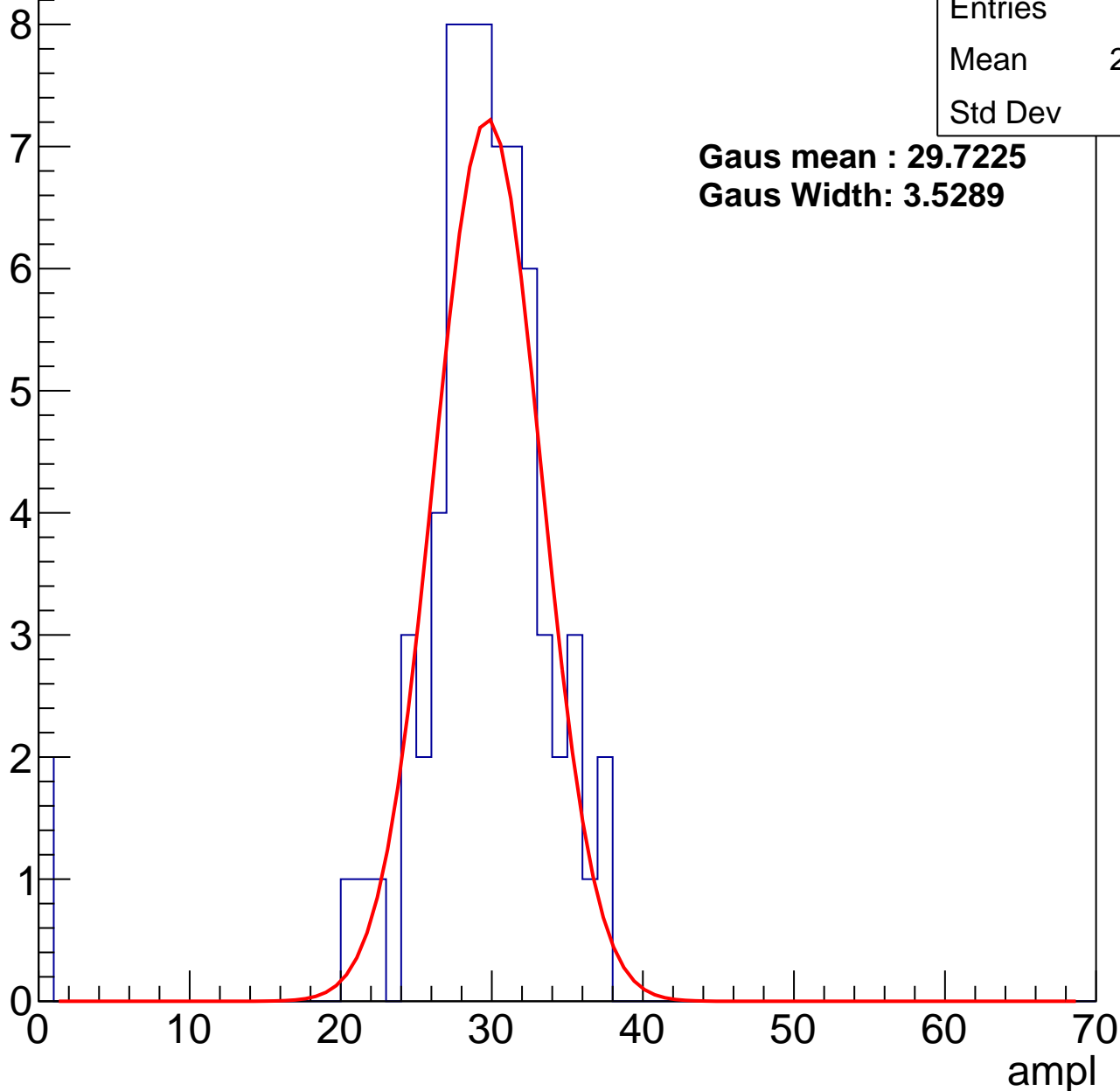
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	28.43
Std Dev	6.03

**Gaus mean : 29.7225**

**Gaus Width: 3.5289**



# B0L000S, U19-ch85, adc1

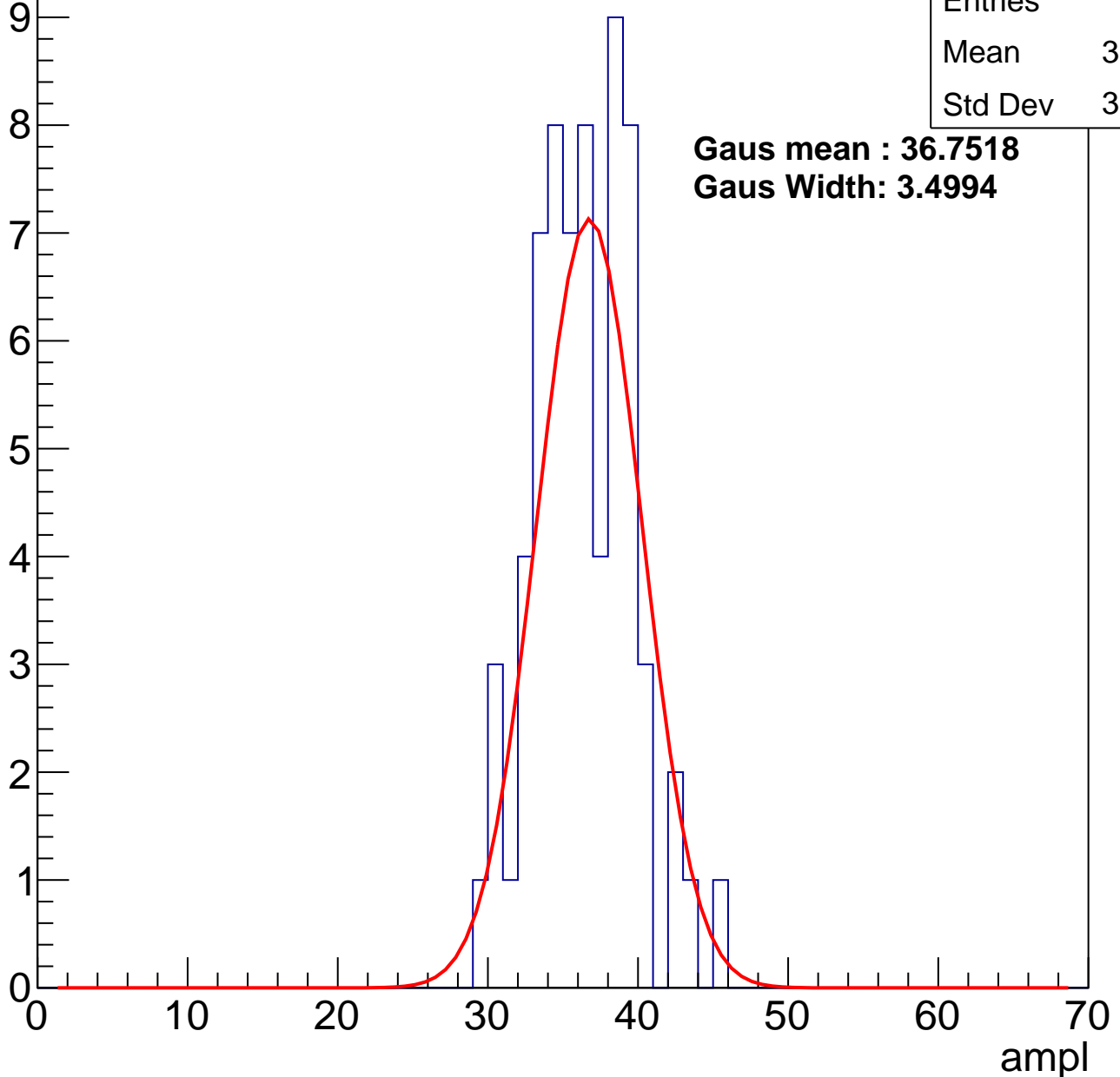
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.94
Std Dev	3.255

**Gaus mean : 36.7518**

**Gaus Width: 3.4994**



# B0L000S, U19-ch85, adc2

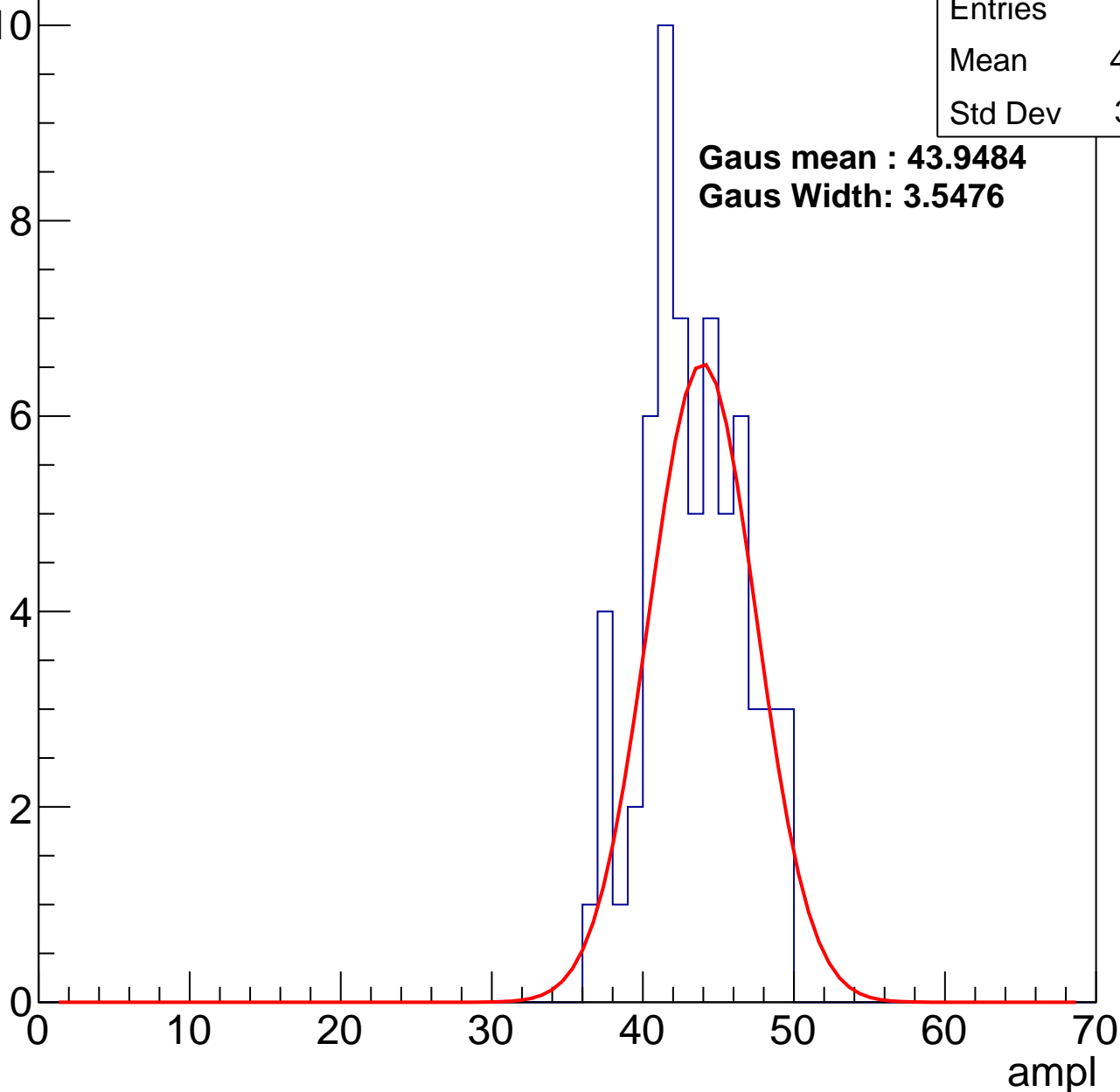
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.86
Std Dev	3.241

**Gaus mean : 43.9484**

**Gaus Width: 3.5476**

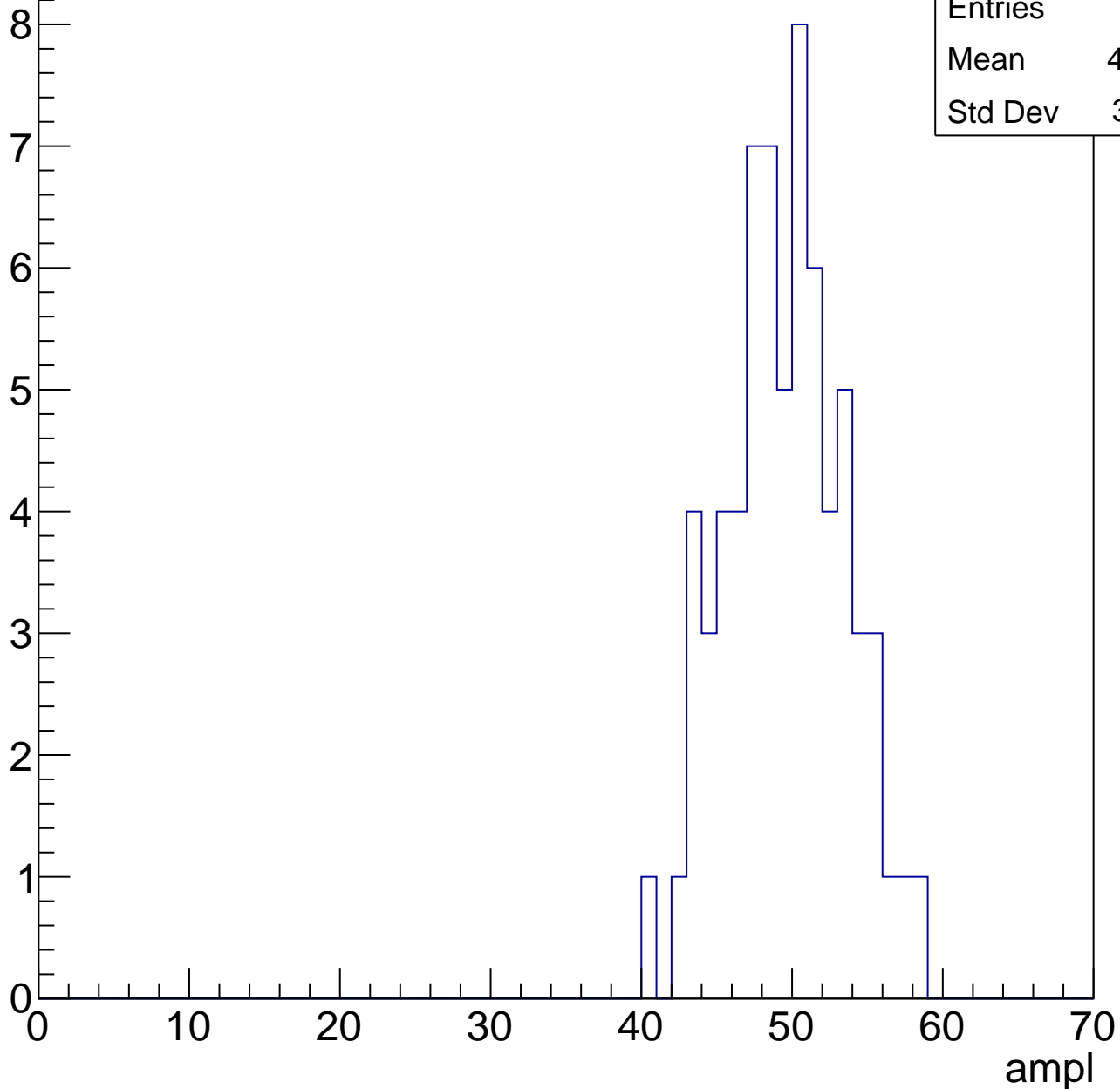


# B0L000S, U19-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.07
Std Dev	3.851

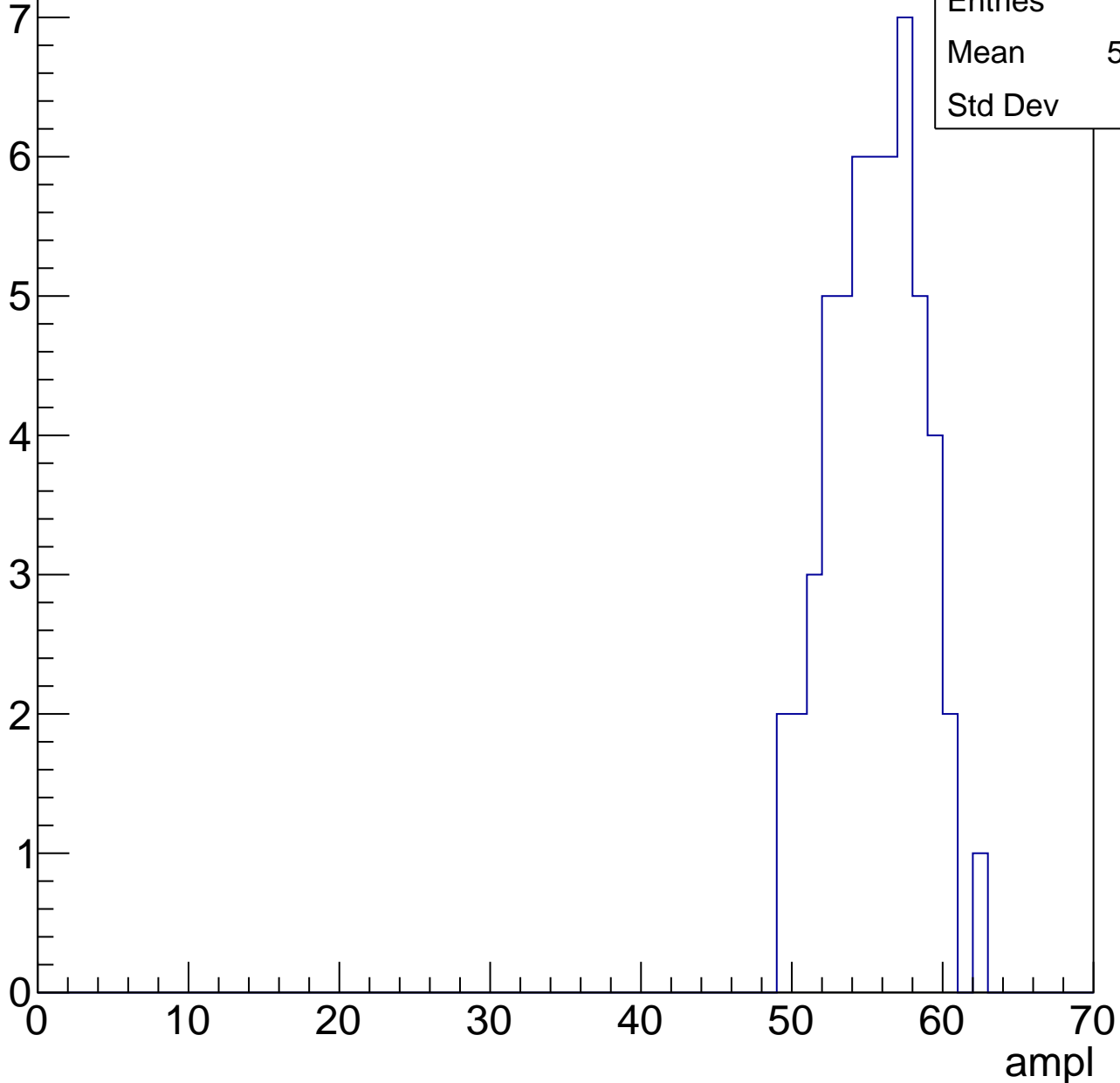


# B0L000S, U19-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

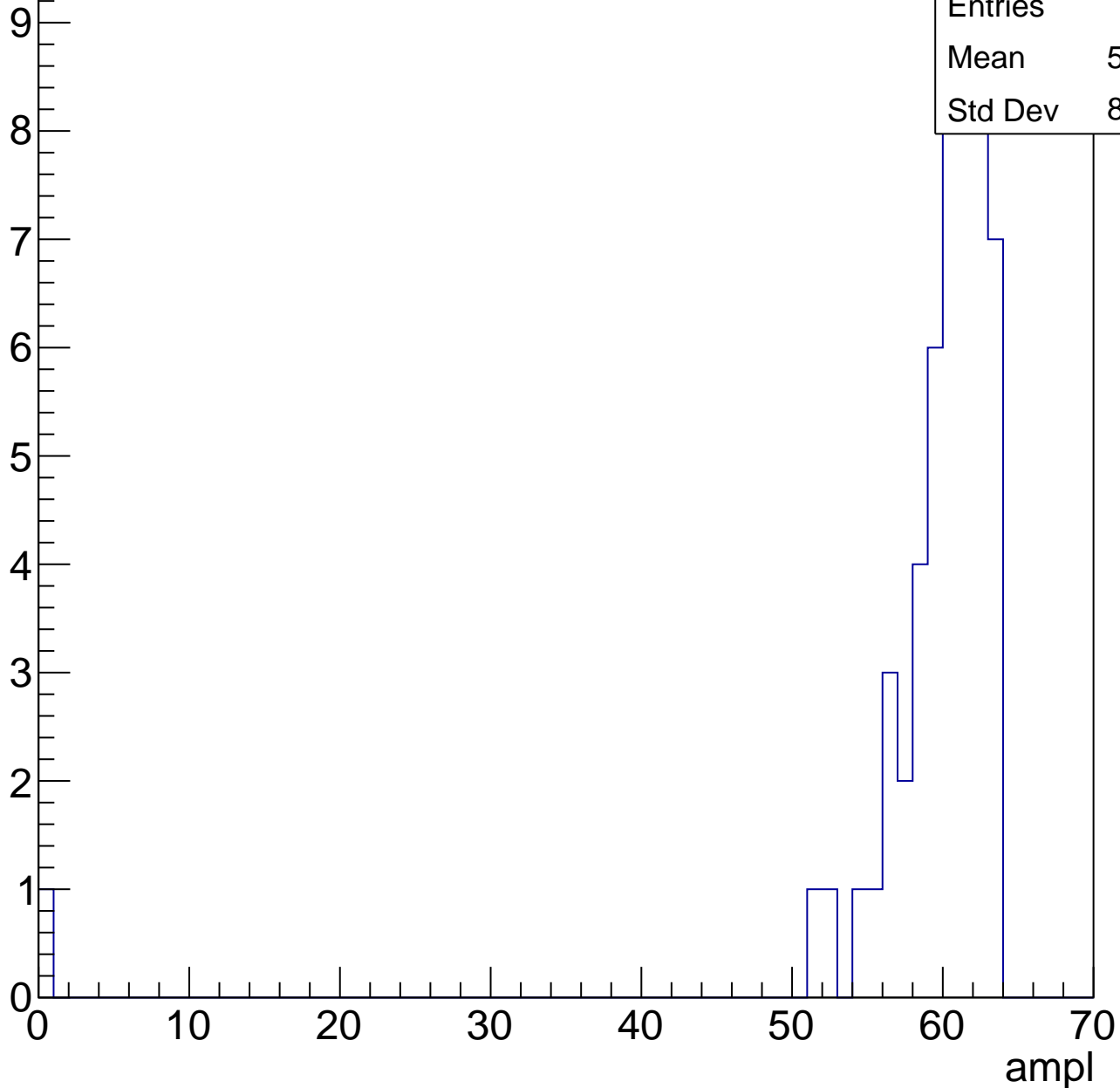
Entries	54
Mean	55.06
Std Dev	2.99



# B0L000S, U19-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	62.4
Std Dev	0.8



# B0L000S, U19-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch86, adc0

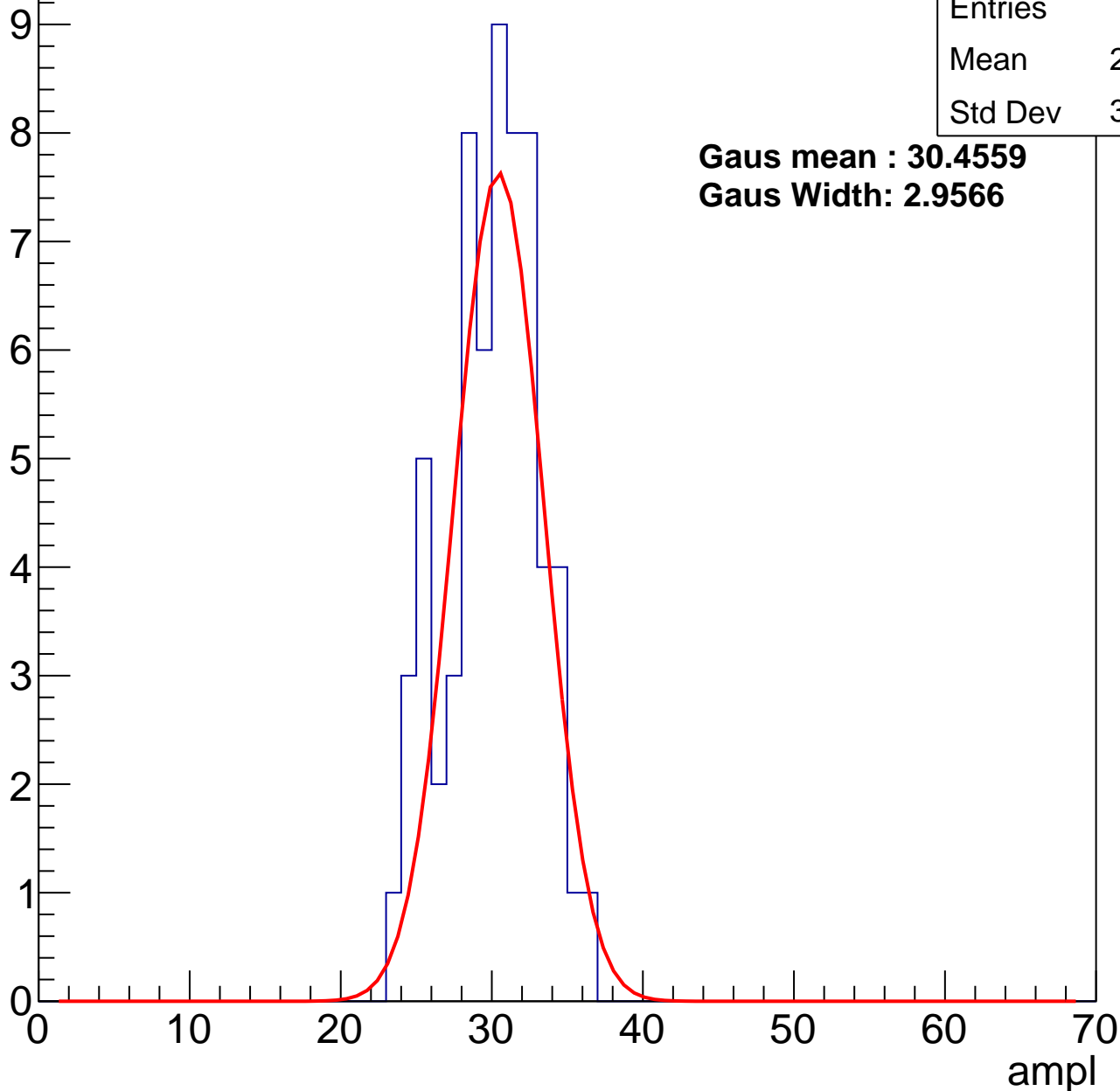
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.59
Std Dev	3.006

**Gaus mean : 30.4559**

**Gaus Width: 2.9566**



# B0L000S, U19-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	36.64
Std Dev	2.905

**Gaus mean : 36.9827**

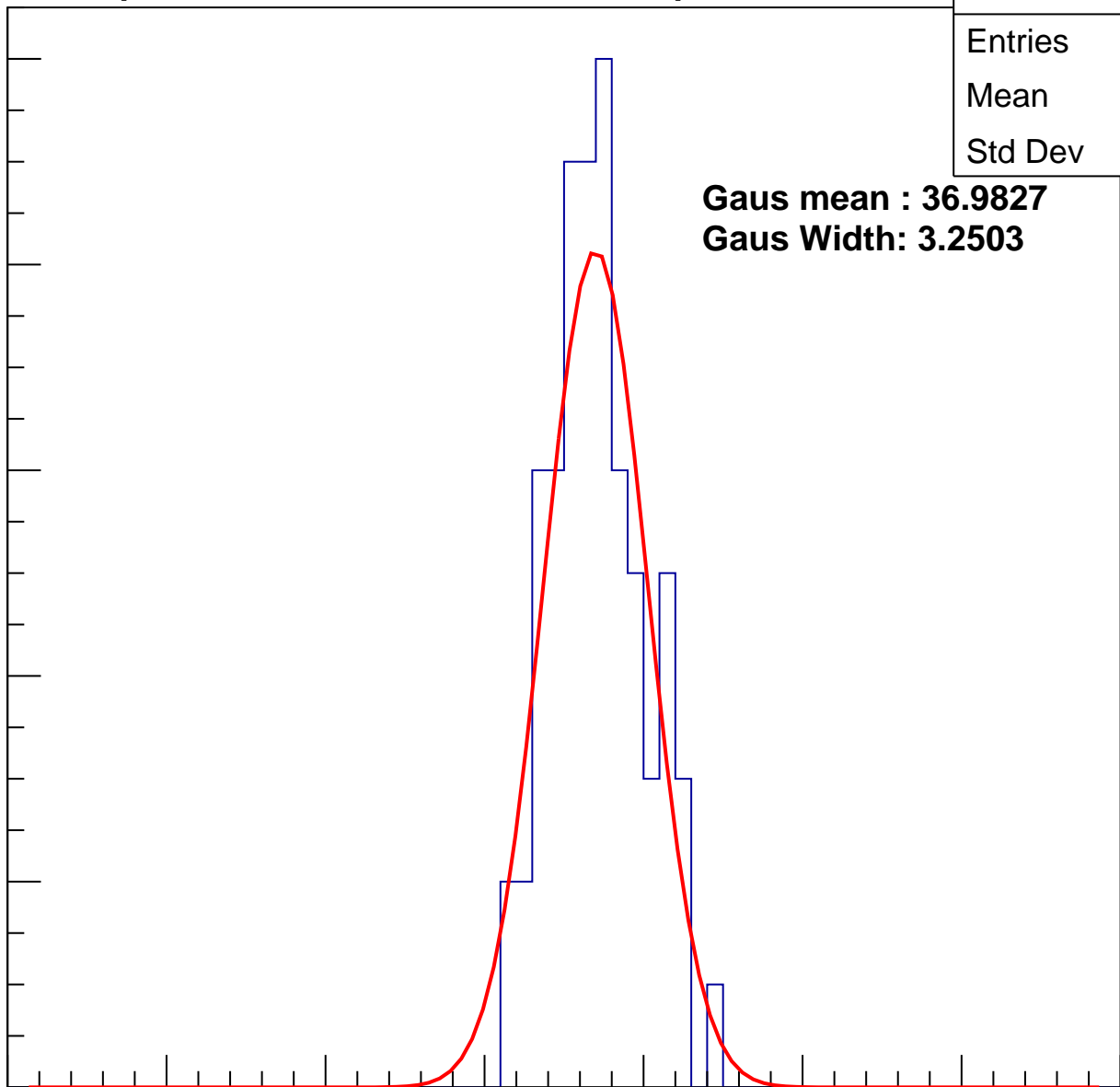
**Gaus Width: 3.2503**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

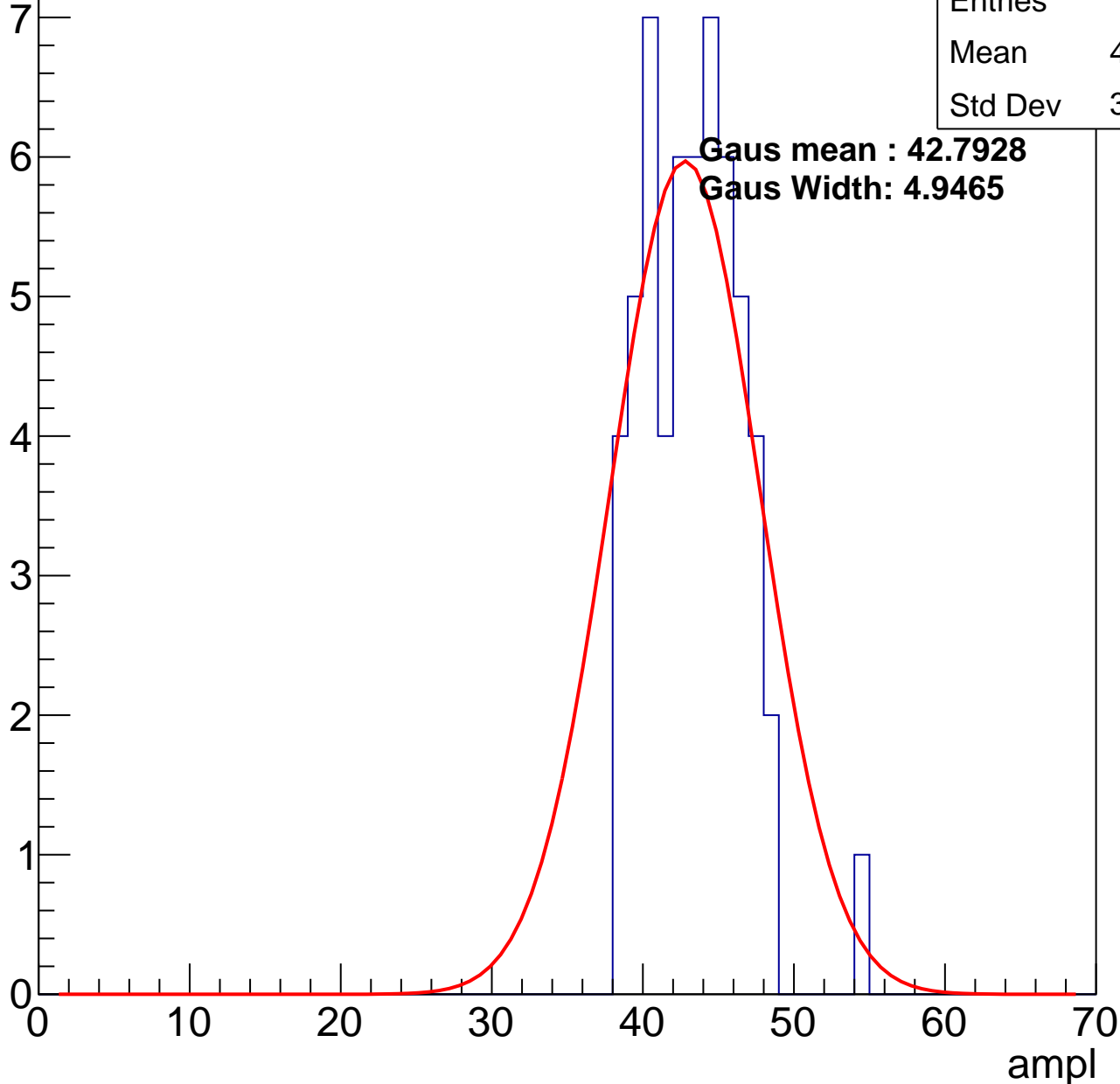


# B0L000S, U19-ch86, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.93
Std Dev	3.178



# B0L000S, U19-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

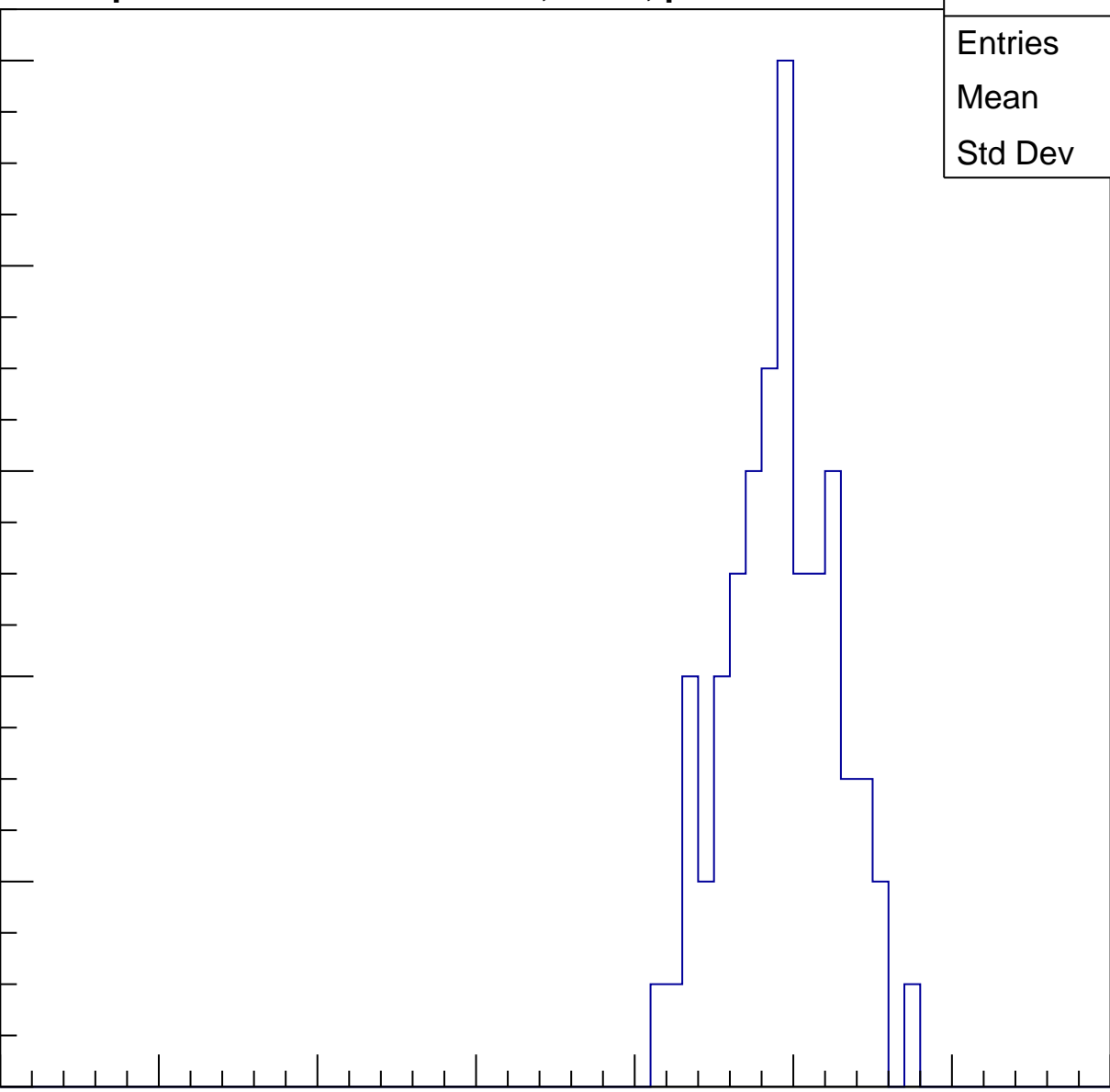
Entries	65
Mean	48.71
Std Dev	3.463

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

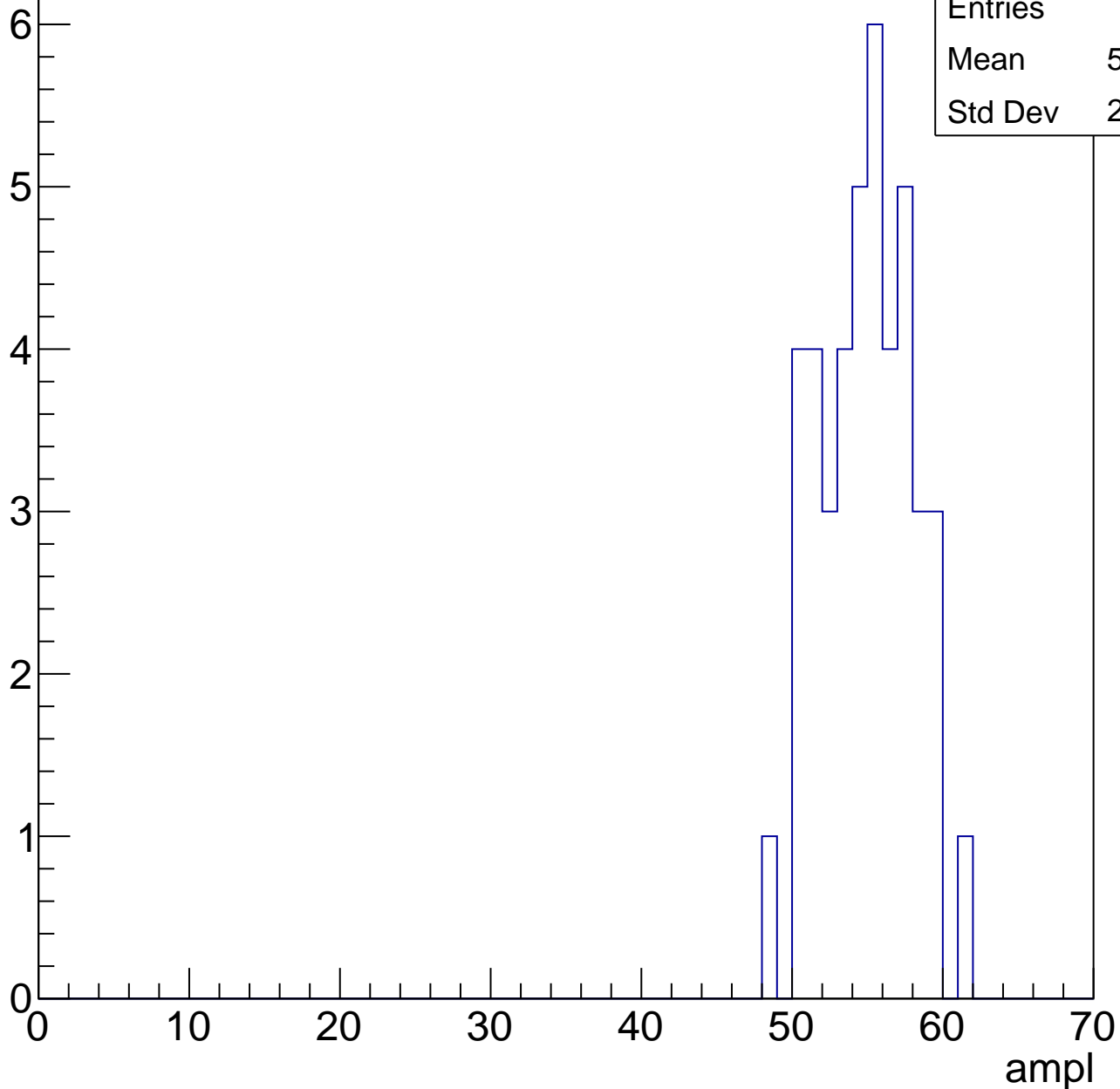


# B0L000S, U19-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

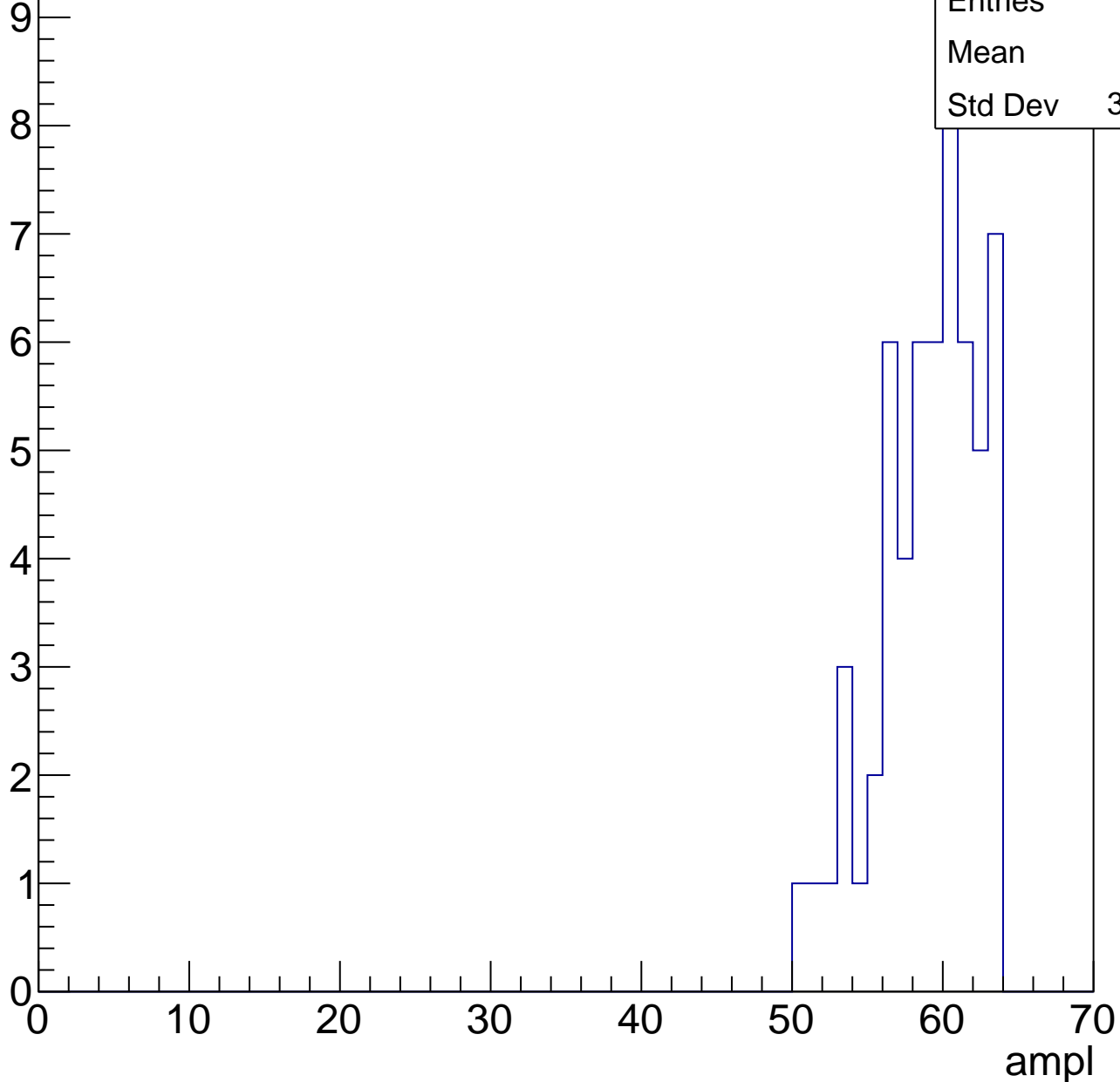
Entries	43
Mean	54.44
Std Dev	2.983



# B0L000S, U19-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



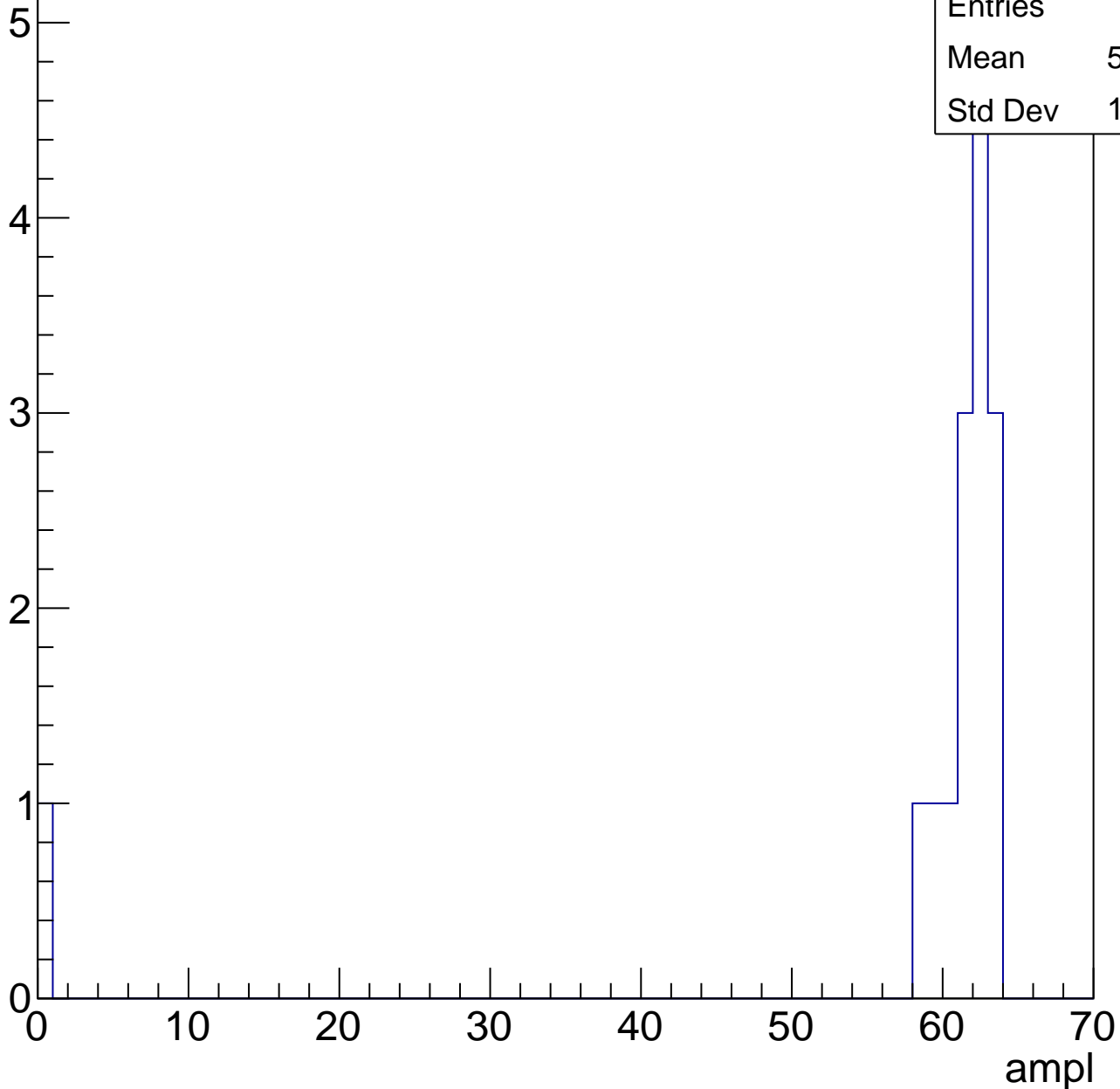
Entries	58
Mean	58.6
Std Dev	3.259

# B0L000S, U19-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	57.27
Std Dev	15.37





# B0L000S, U19-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch87, adc0

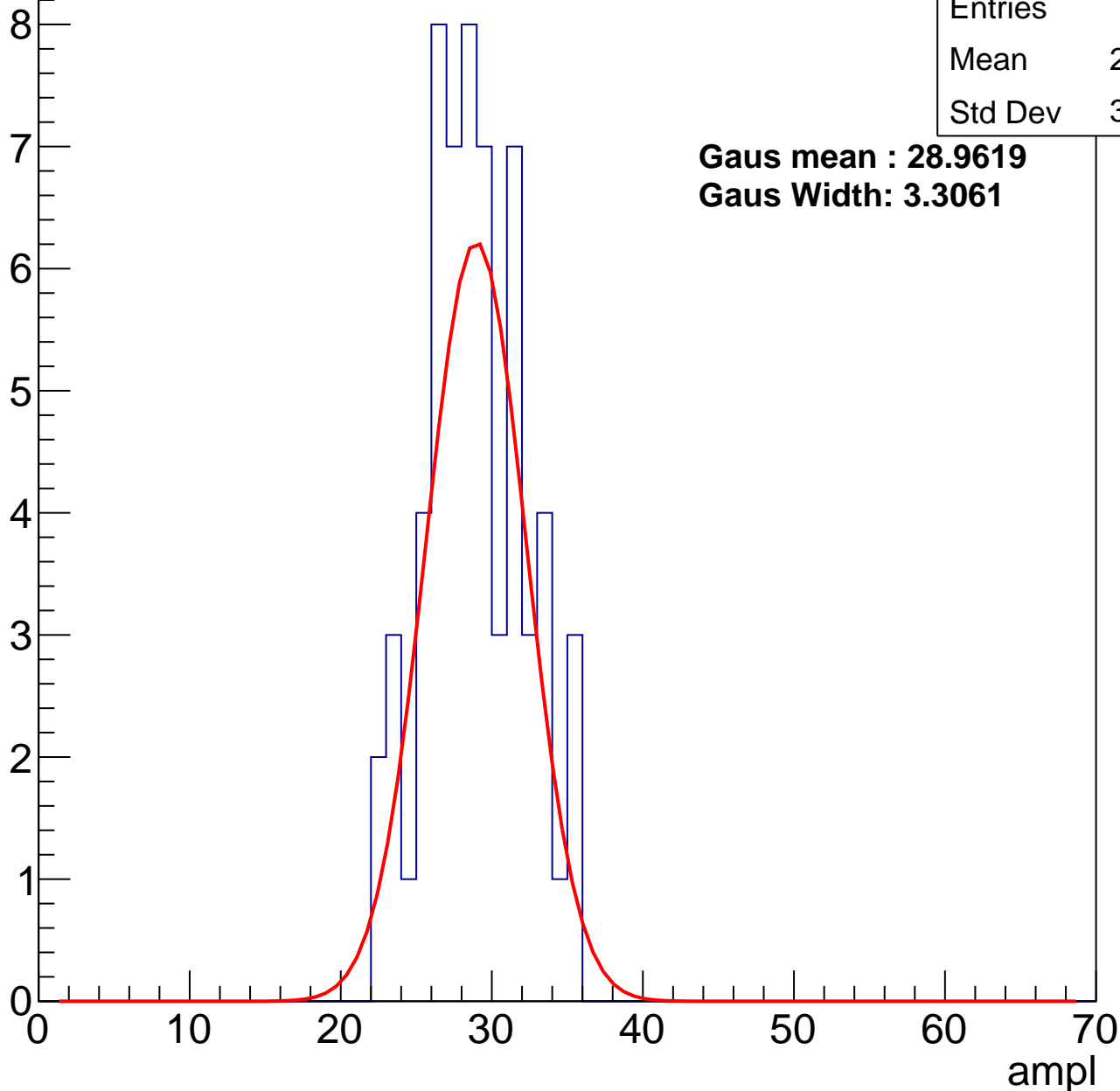
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	28.44
Std Dev	3.237

**Gaus mean : 28.9619**

**Gaus Width: 3.3061**



# B0L000S, U19-ch87, adc1

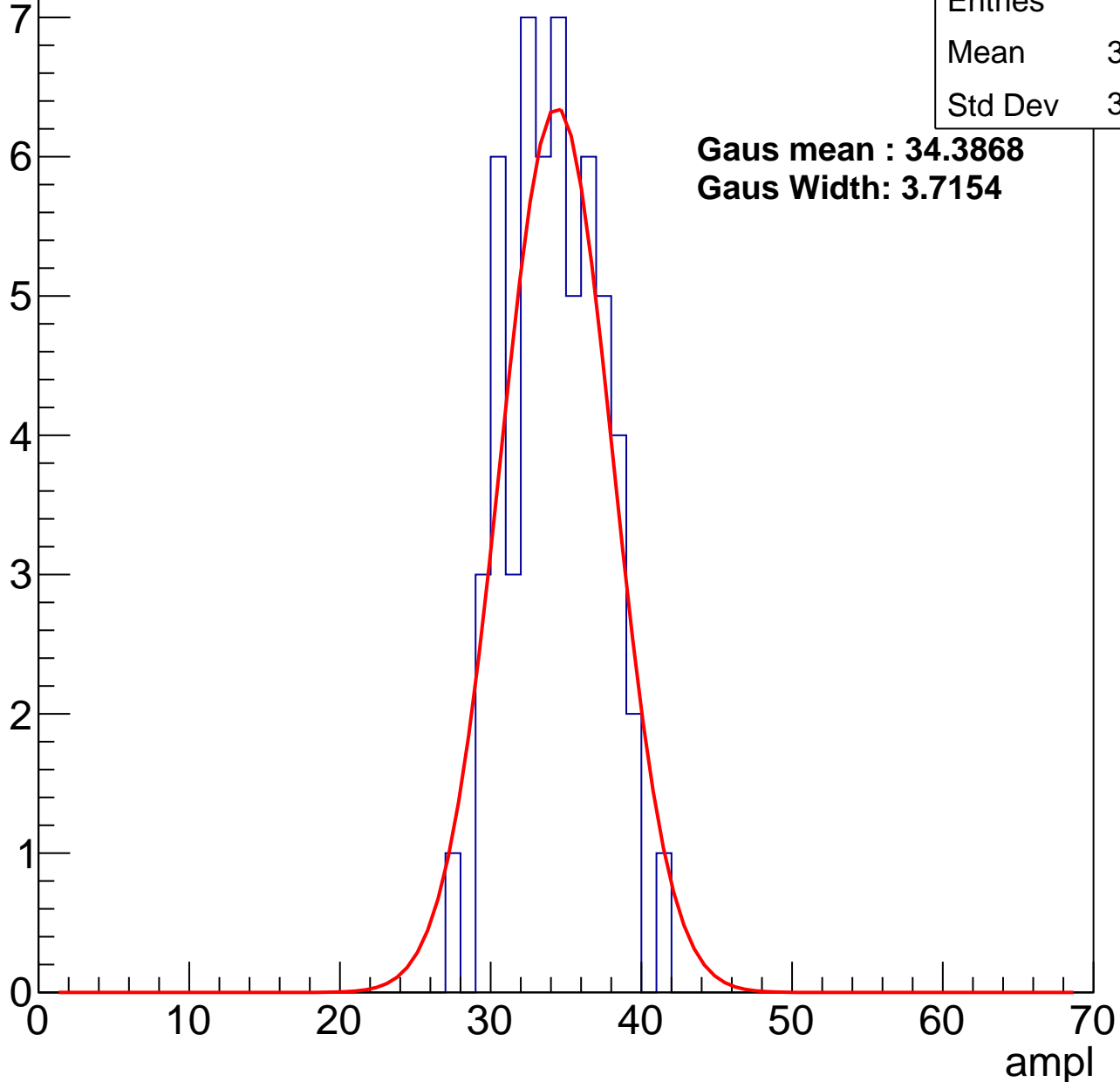
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	33.82
Std Dev	3.036

**Gaus mean : 34.3868**

**Gaus Width: 3.7154**



# B0L000S, U19-ch87, adc2

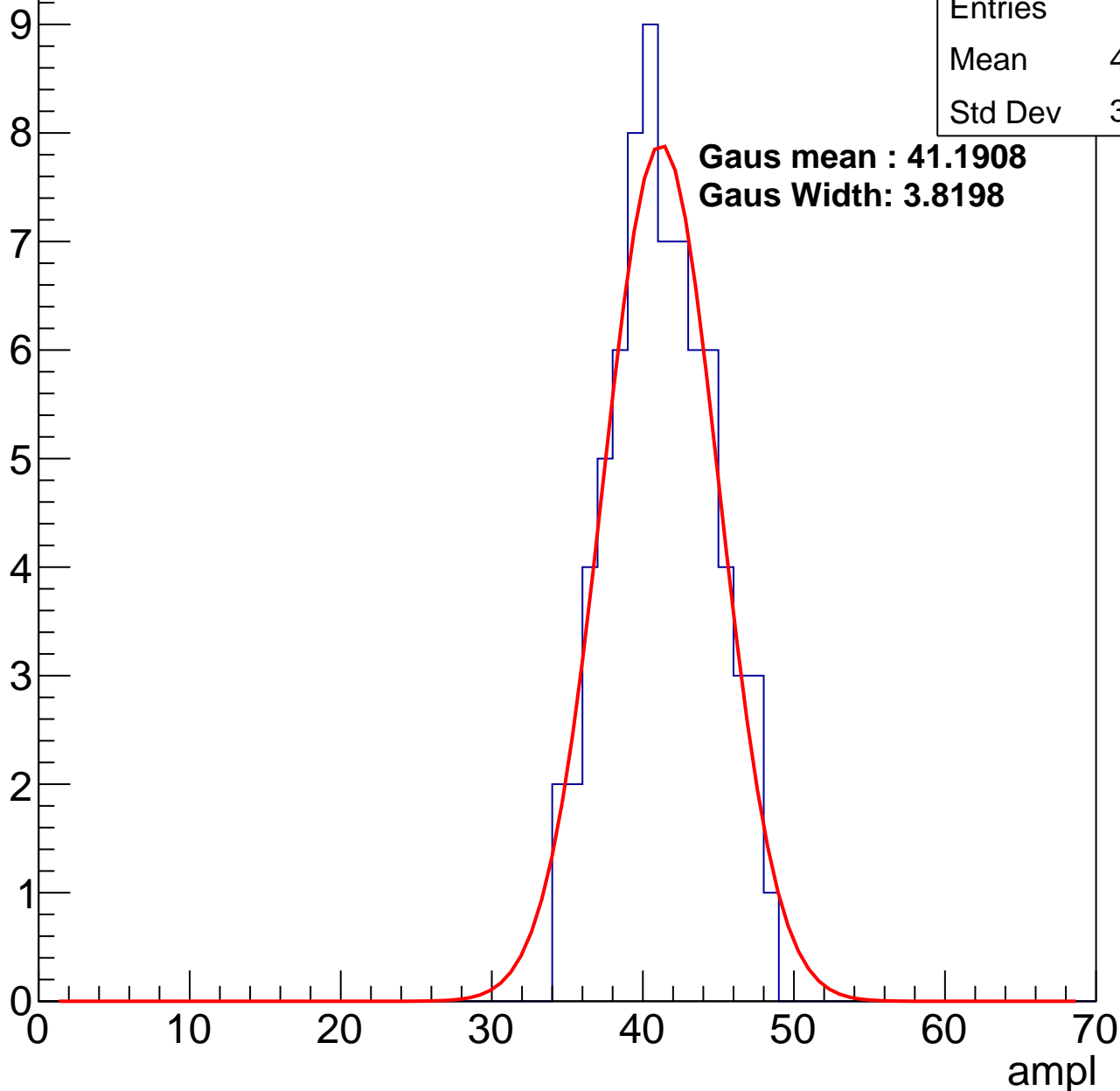
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	40.78
Std Dev	3.357

**Gaus mean : 41.1908**

**Gaus Width: 3.8198**

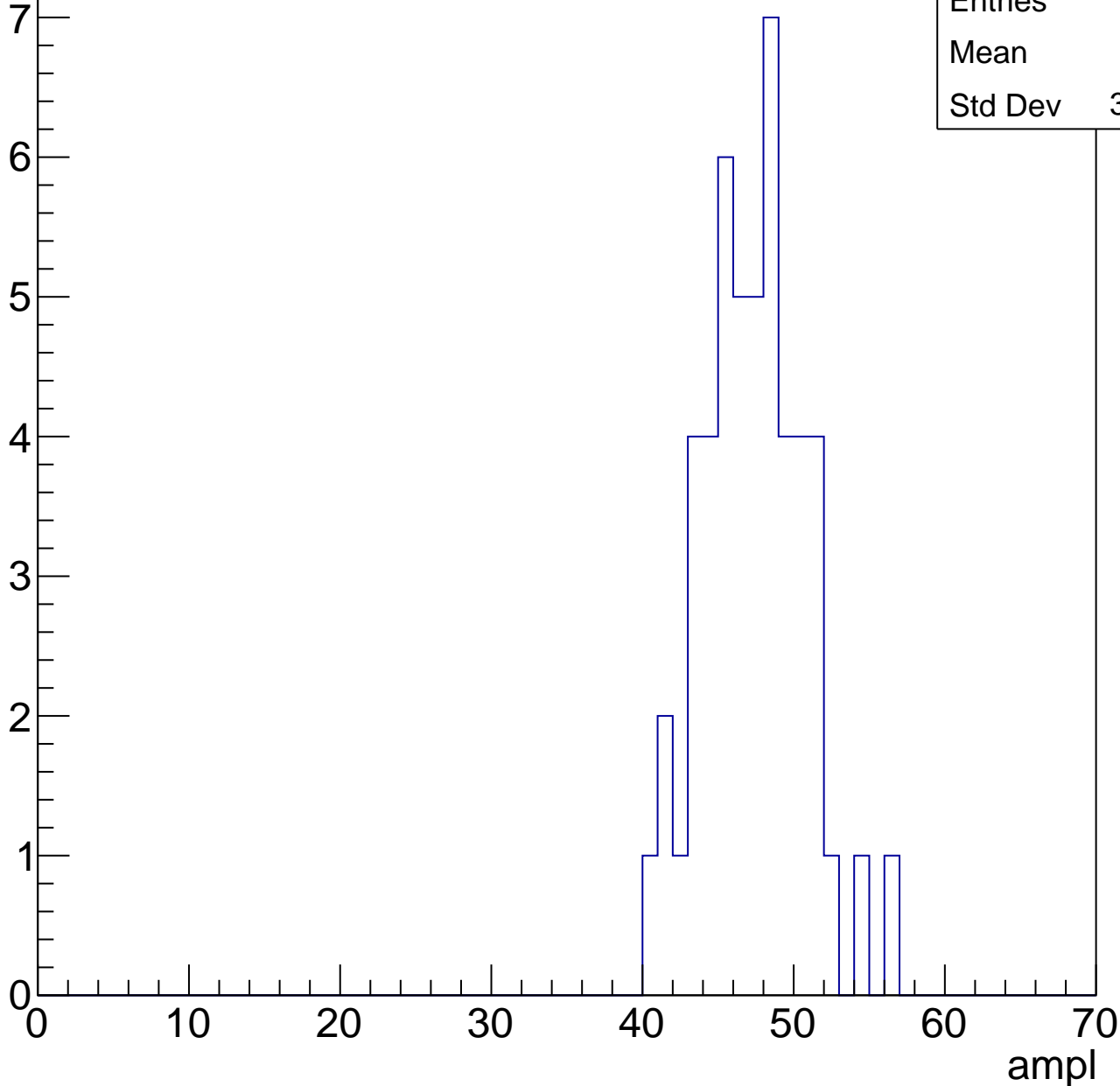


# B0L000S, U19-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	46.9
Std Dev	3.324

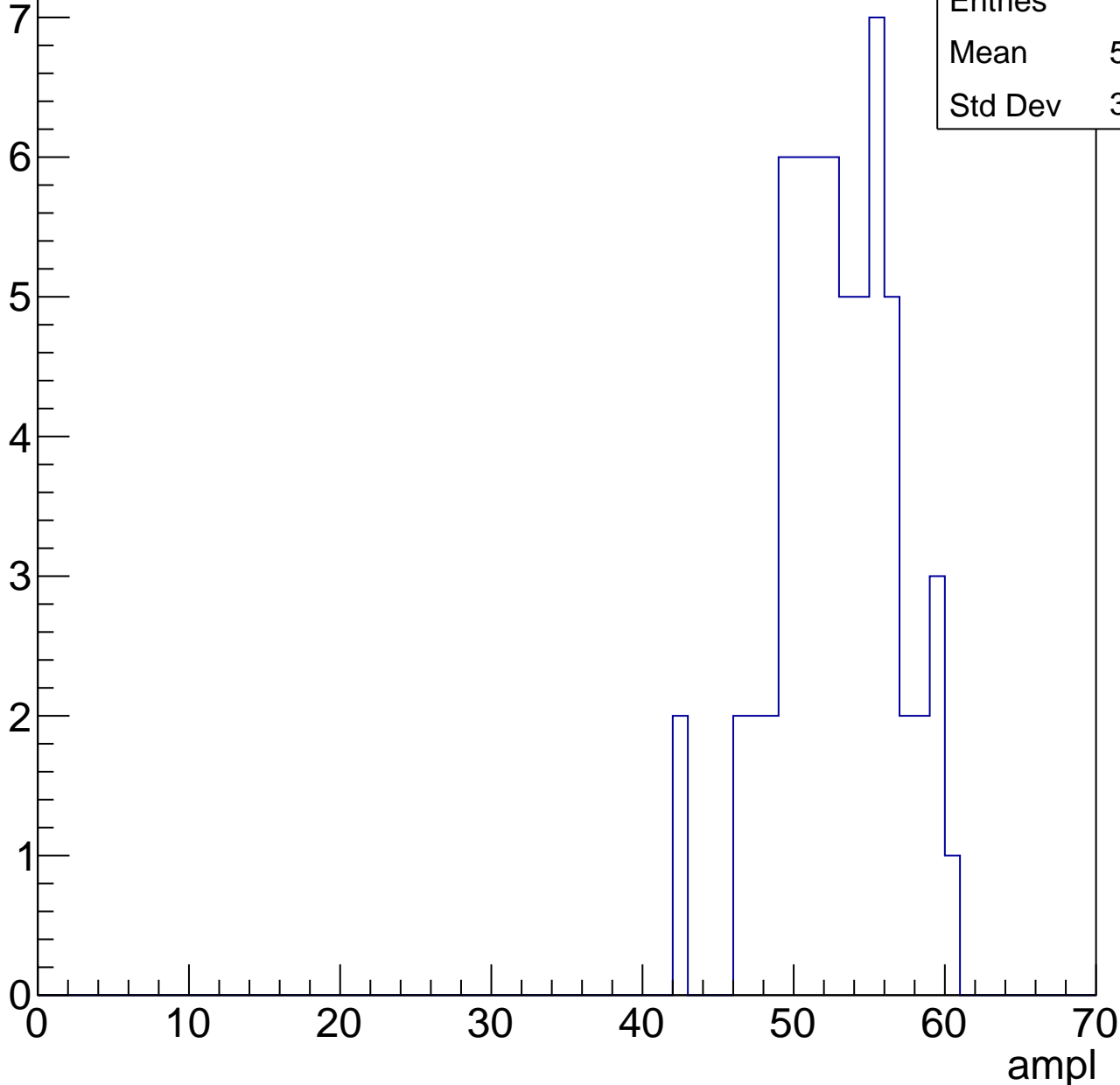


# B0L000S, U19-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

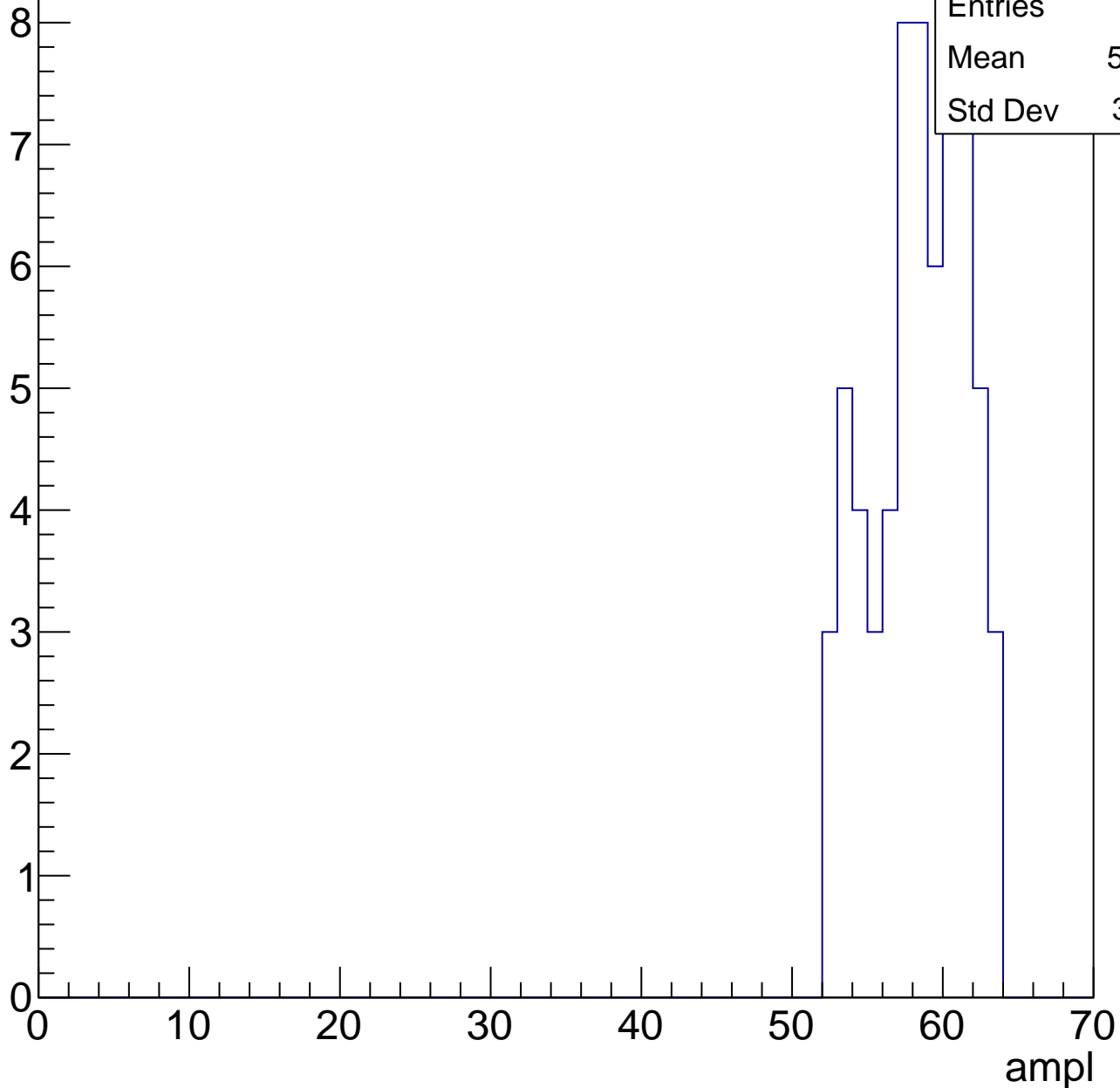
Entries	62
Mean	52.34
Std Dev	3.877



# B0L000S, U19-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

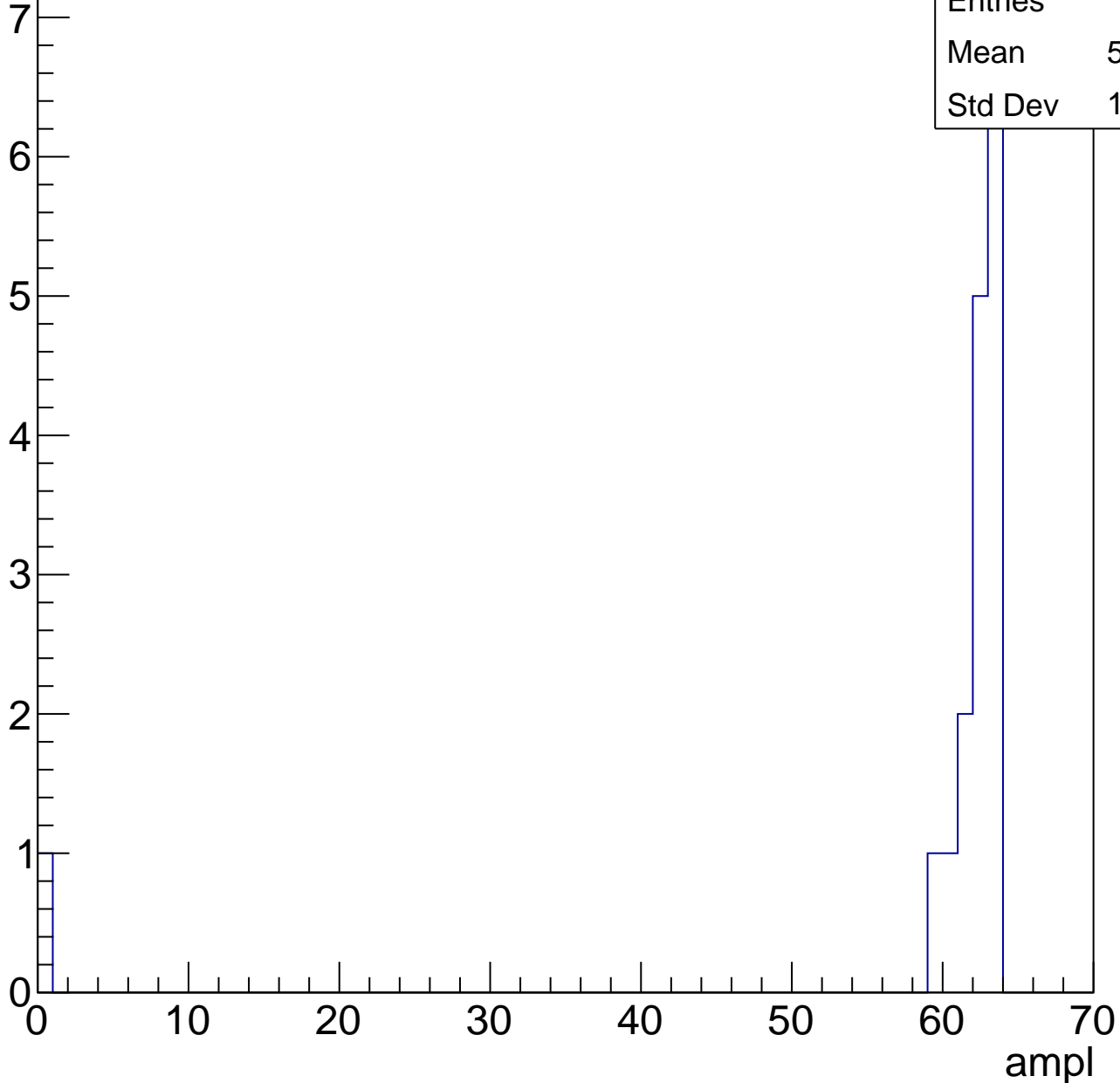


# B0L000S, U19-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	58.35
Std Dev	14.63





# B0L000S, U19-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl

# B0L000S, U19-ch88, adc0

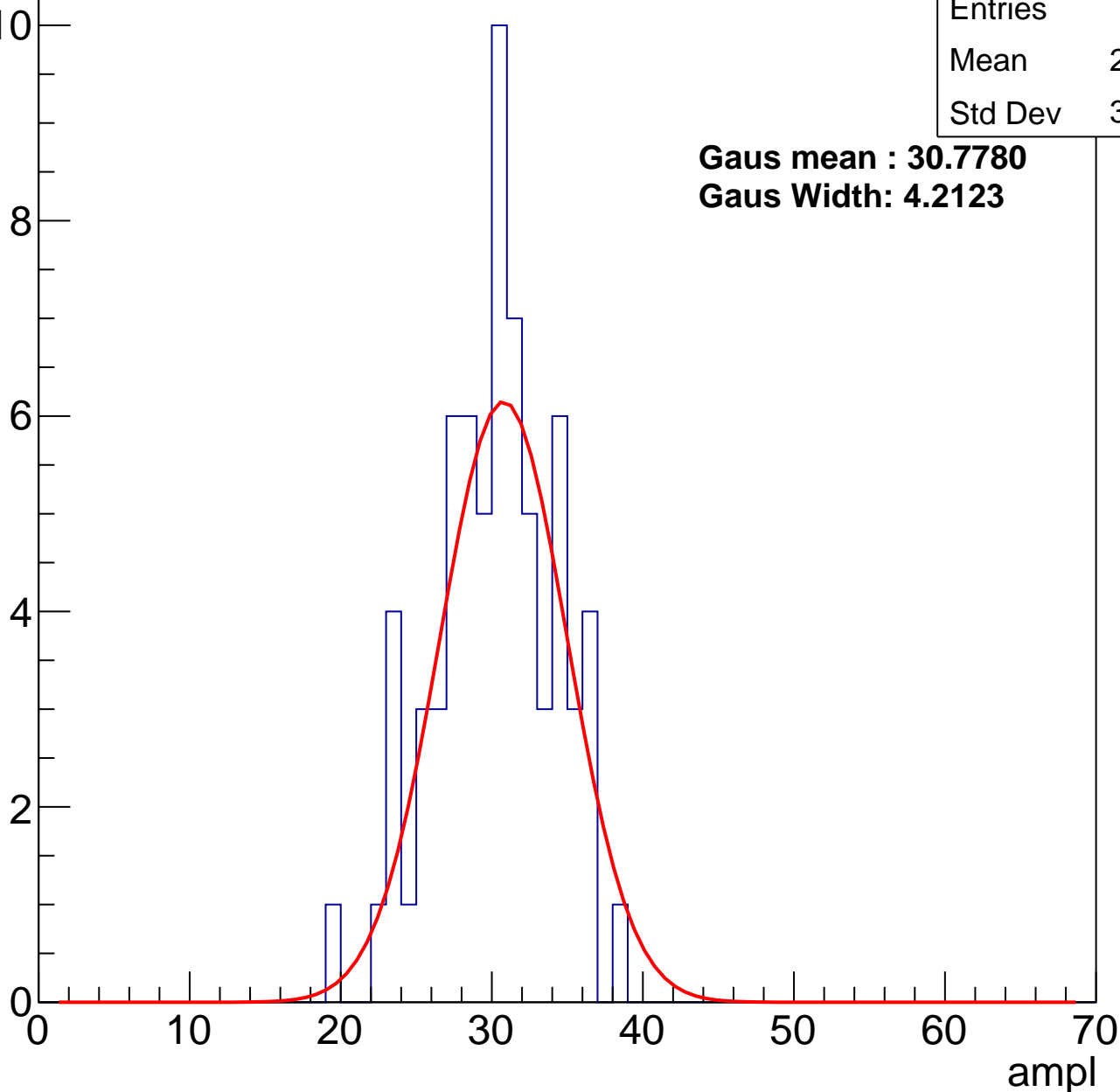
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.74
Std Dev	3.903

**Gaus mean : 30.7780**

**Gaus Width: 4.2123**



# B0L000S, U19-ch88, adc1

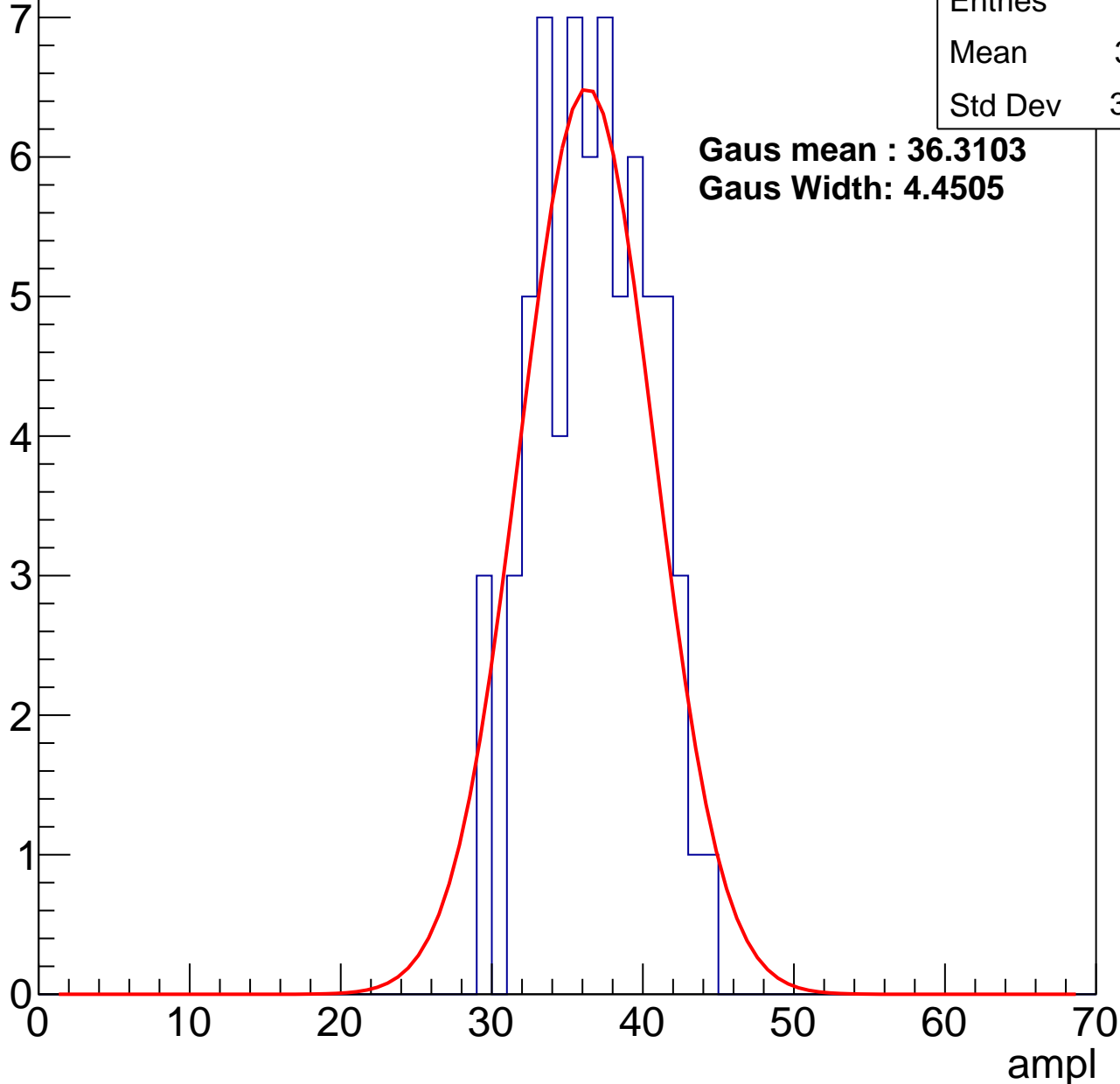
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.31
Std Dev	3.615

**Gaus mean : 36.3103**

**Gaus Width: 4.4505**



# B0L000S, U19-ch88, adc2

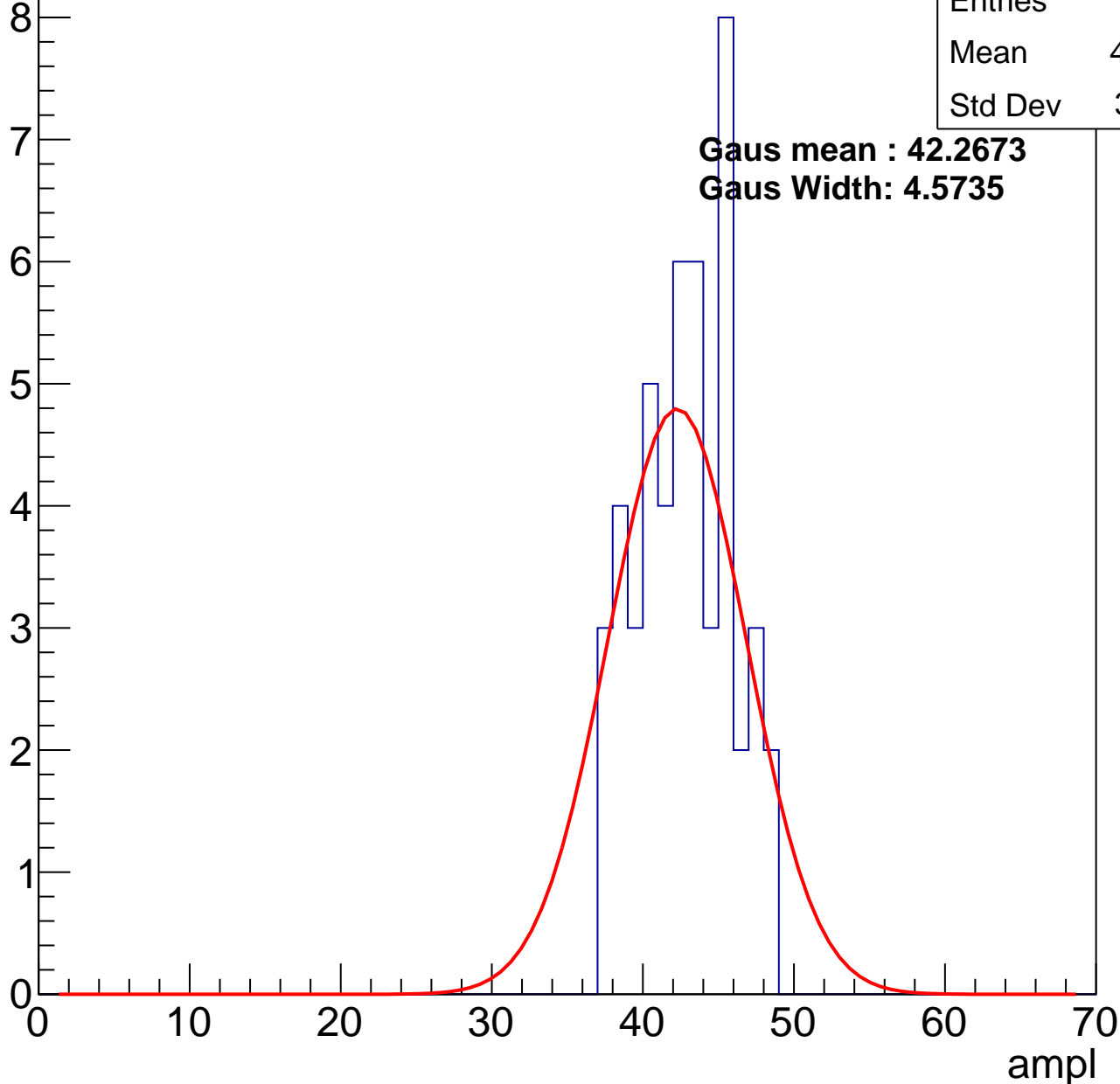
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	42.35
Std Dev	3.041

**Gaus mean : 42.2673**

**Gaus Width: 4.5735**

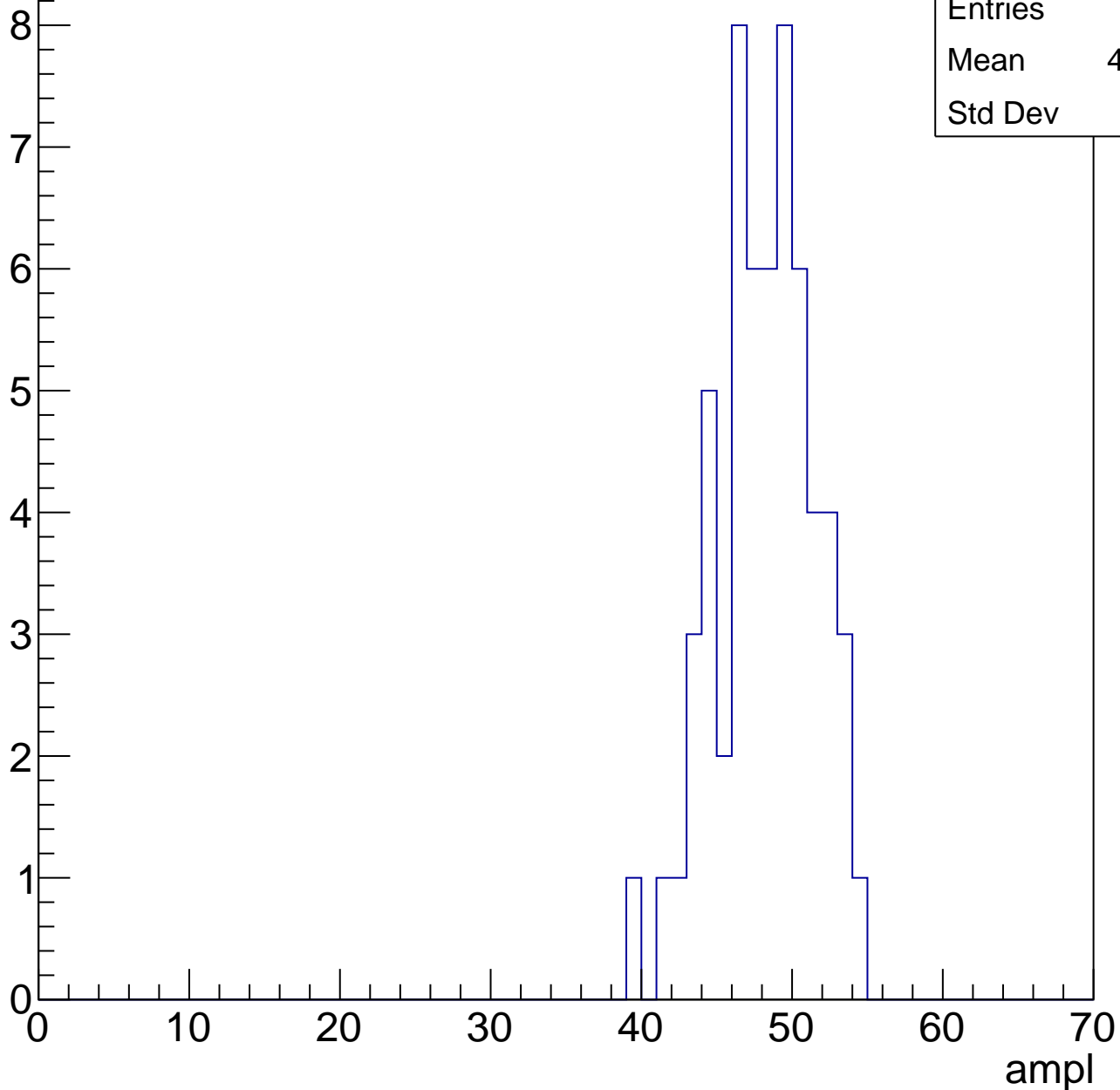


# B0L000S, U19-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	47.73
Std Dev	3.23

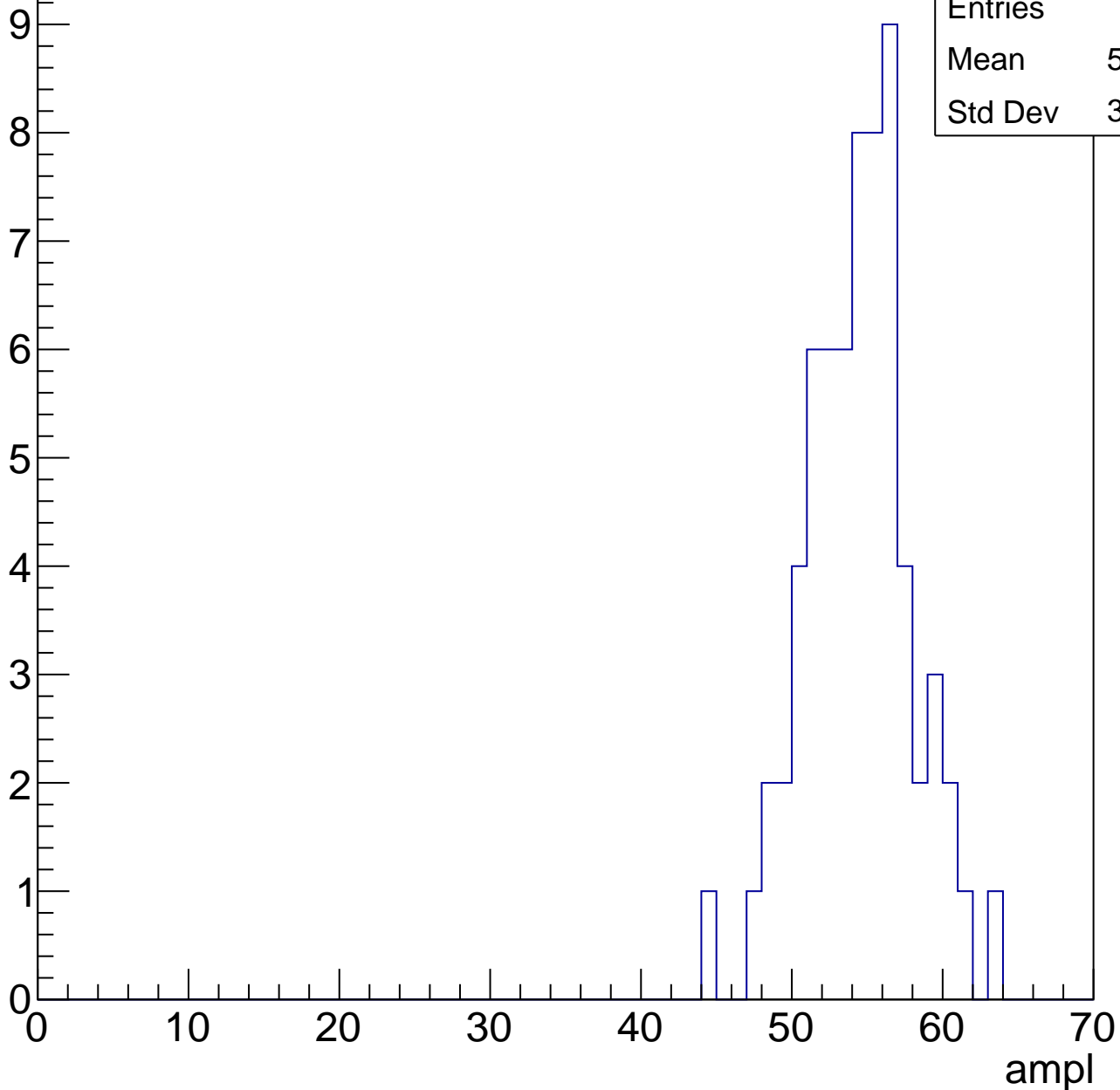


# B0L000S, U19-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

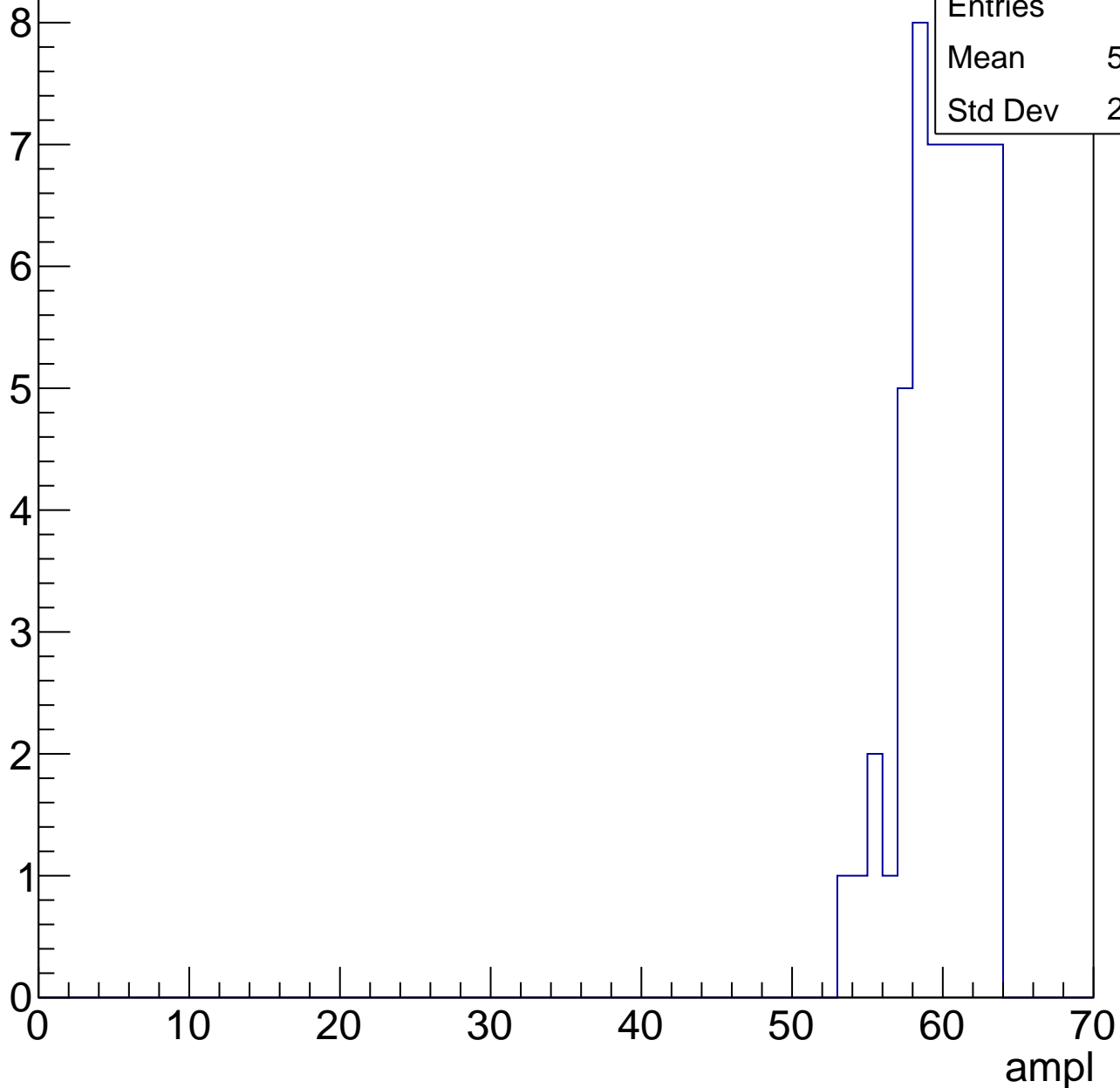
Entries	66
Mean	53.97
Std Dev	3.499



# B0L000S, U19-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

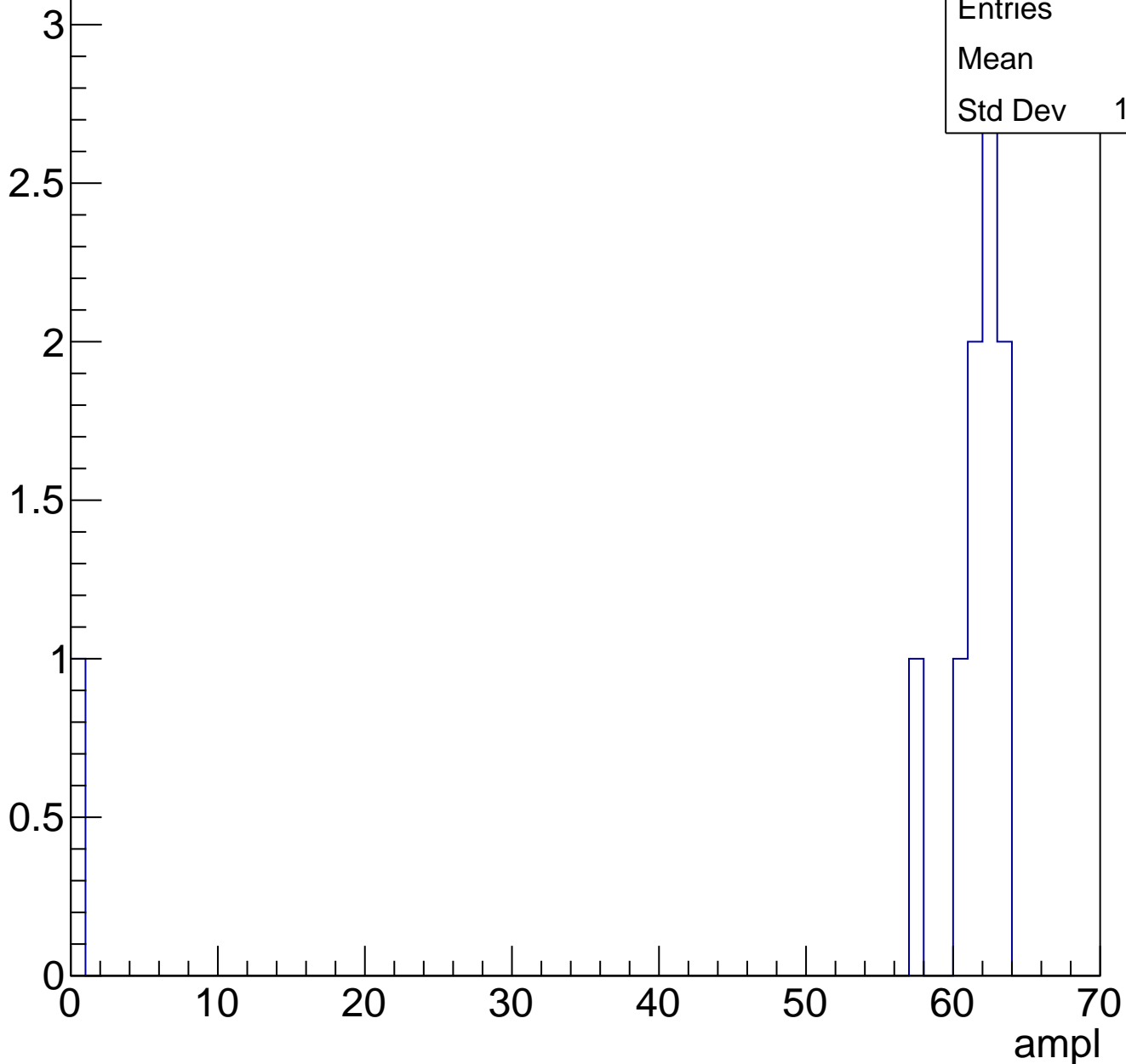


Entries	53
Mean	59.57
Std Dev	2.469

# B0L000S, U19-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	10
Mean	55.1
Std Dev	18.44



# B0L000S, U19-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch89, adc0

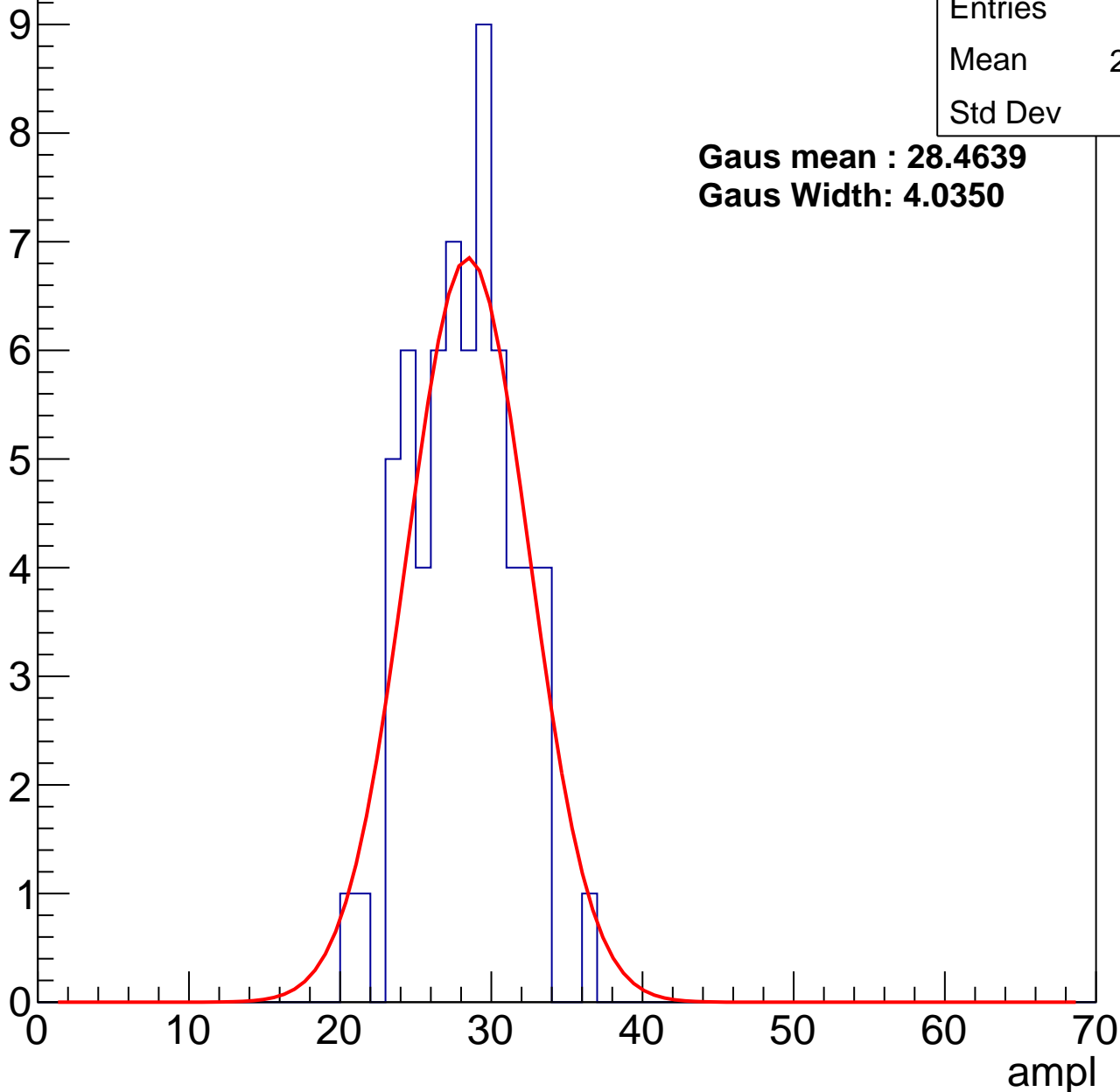
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	27.72
Std Dev	3.29

**Gaus mean : 28.4639**

**Gaus Width: 4.0350**



# B0L000S, U19-ch89, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	59
Mean	34.41
Std Dev	3.092

**Gaus mean : 34.2024**

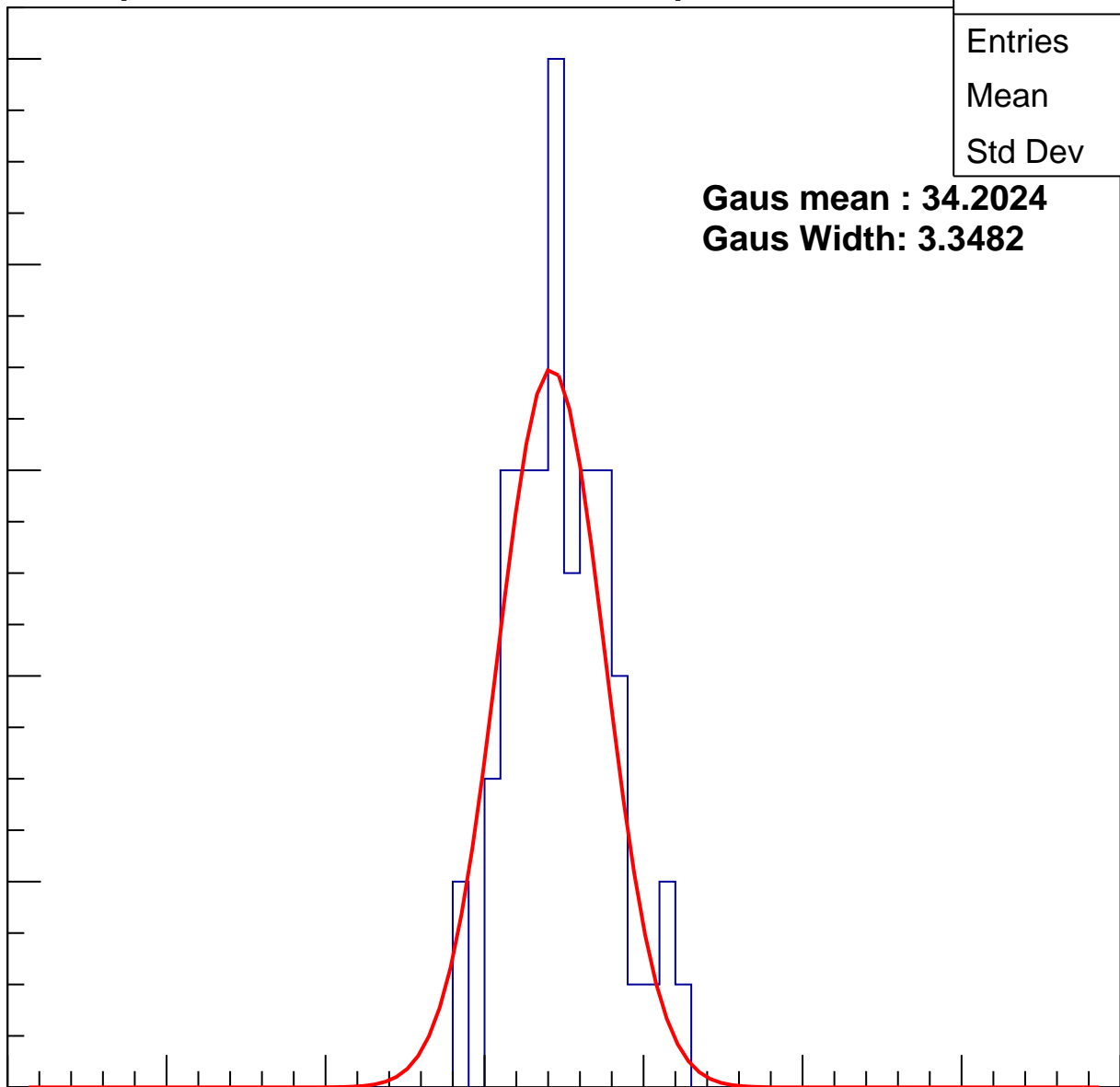
**Gaus Width: 3.3482**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch89, adc2

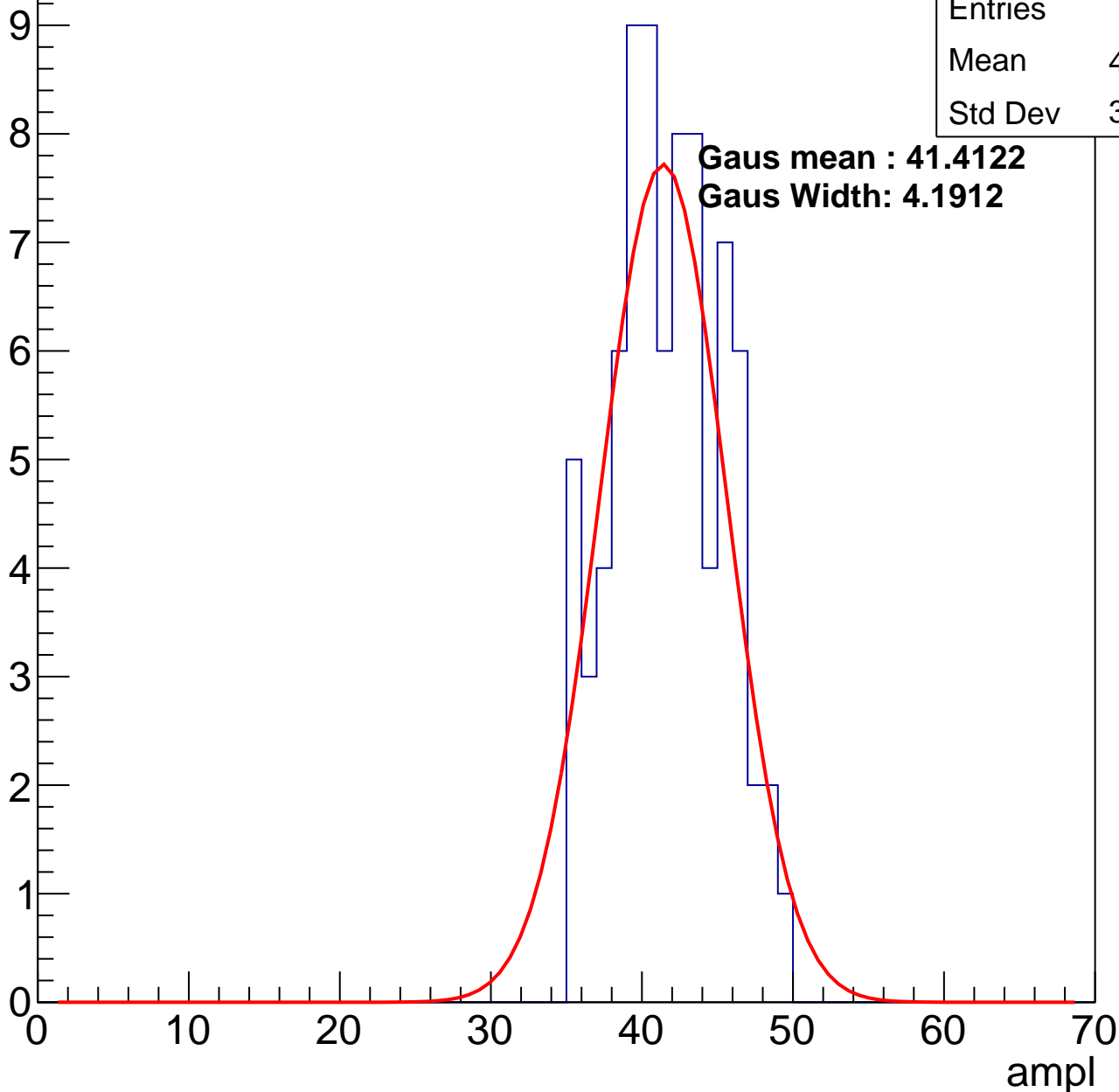
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	41.27
Std Dev	3.507

**Gaus mean : 41.4122**

**Gaus Width: 4.1912**

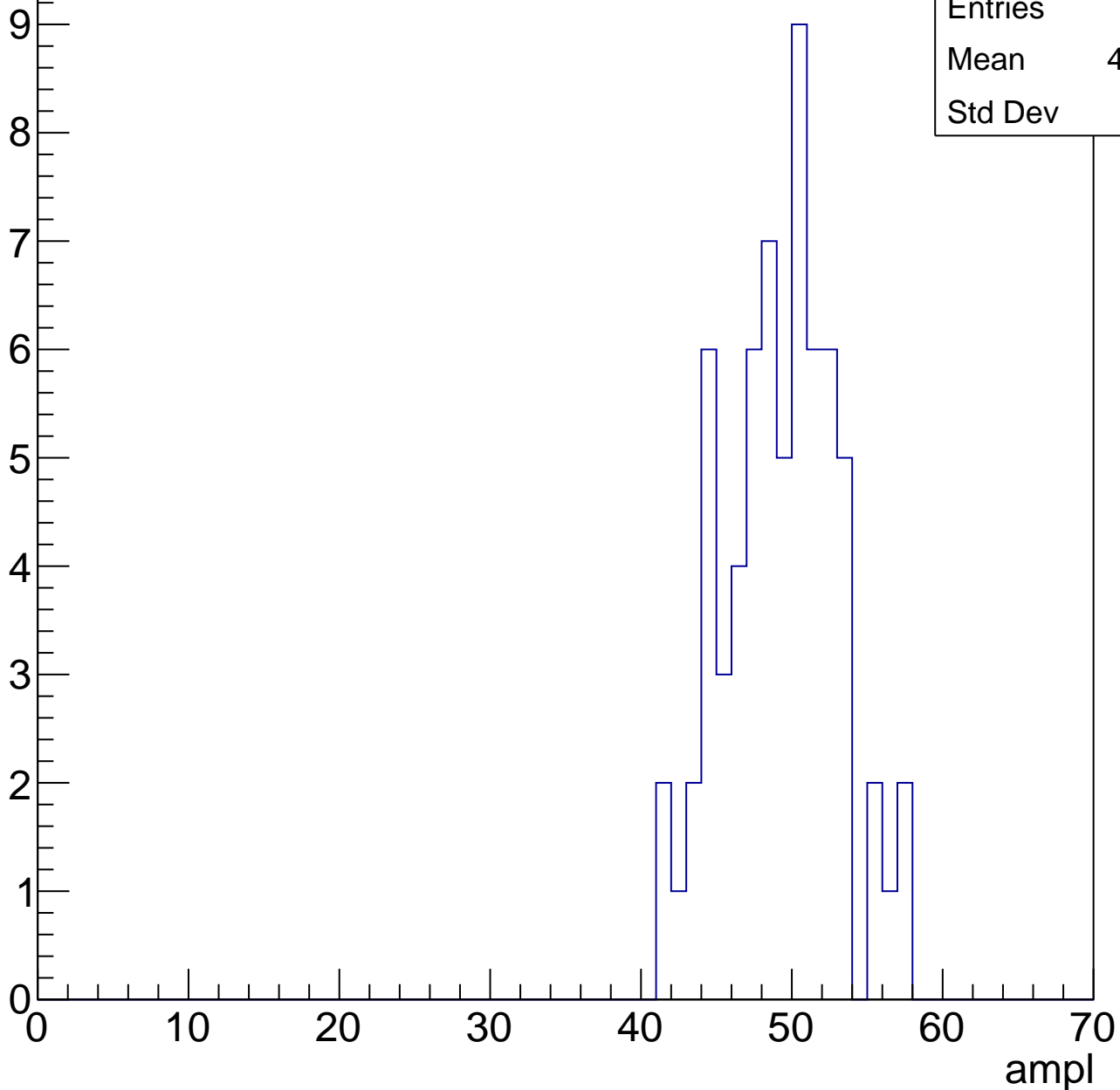


# B0L000S, U19-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.79
Std Dev	3.72



# B0L000S, U19-ch89, adc4

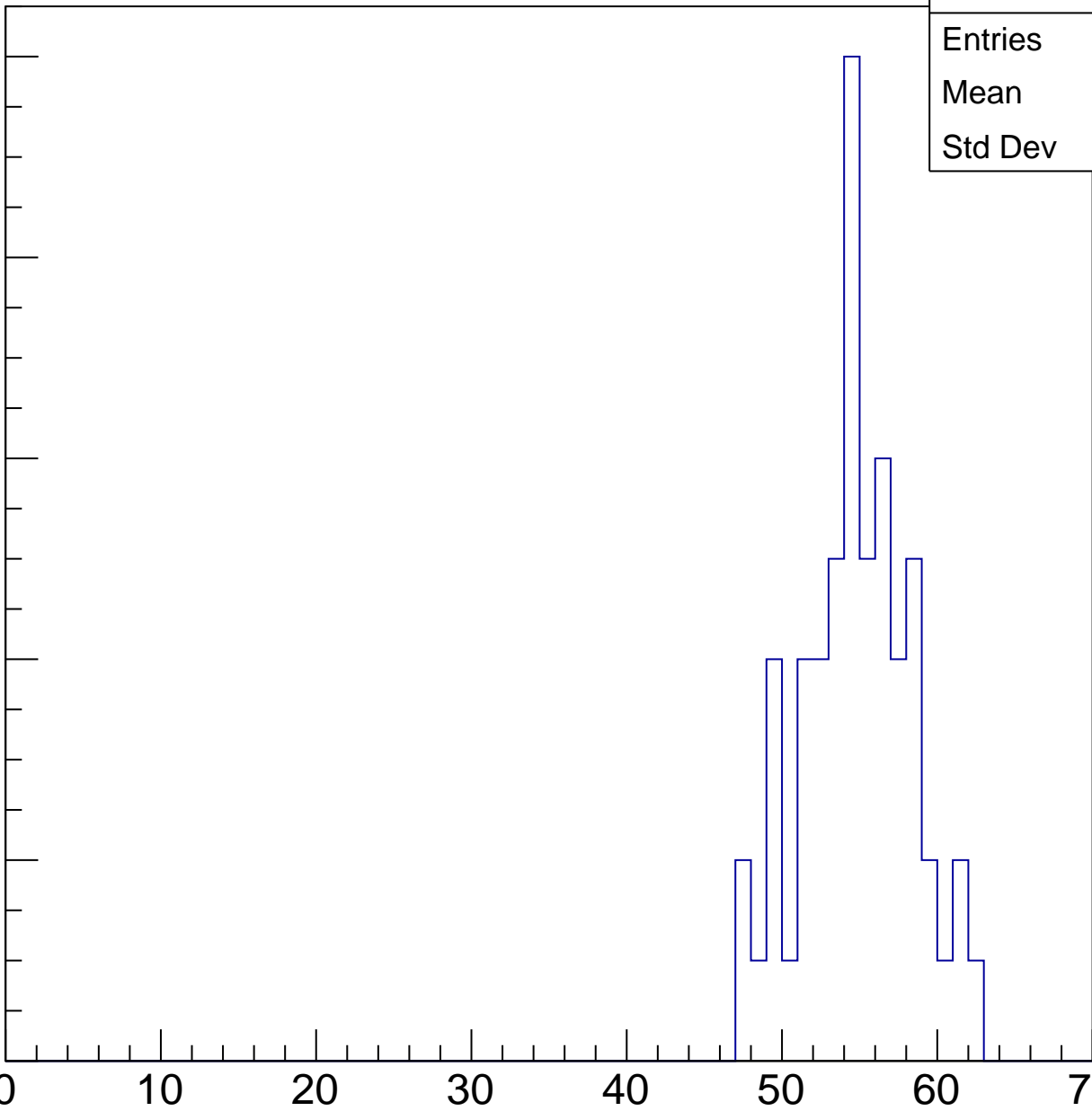
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	54.32
Std Dev	3.49

Entry

10  
8  
6  
4  
2  
0

ampl

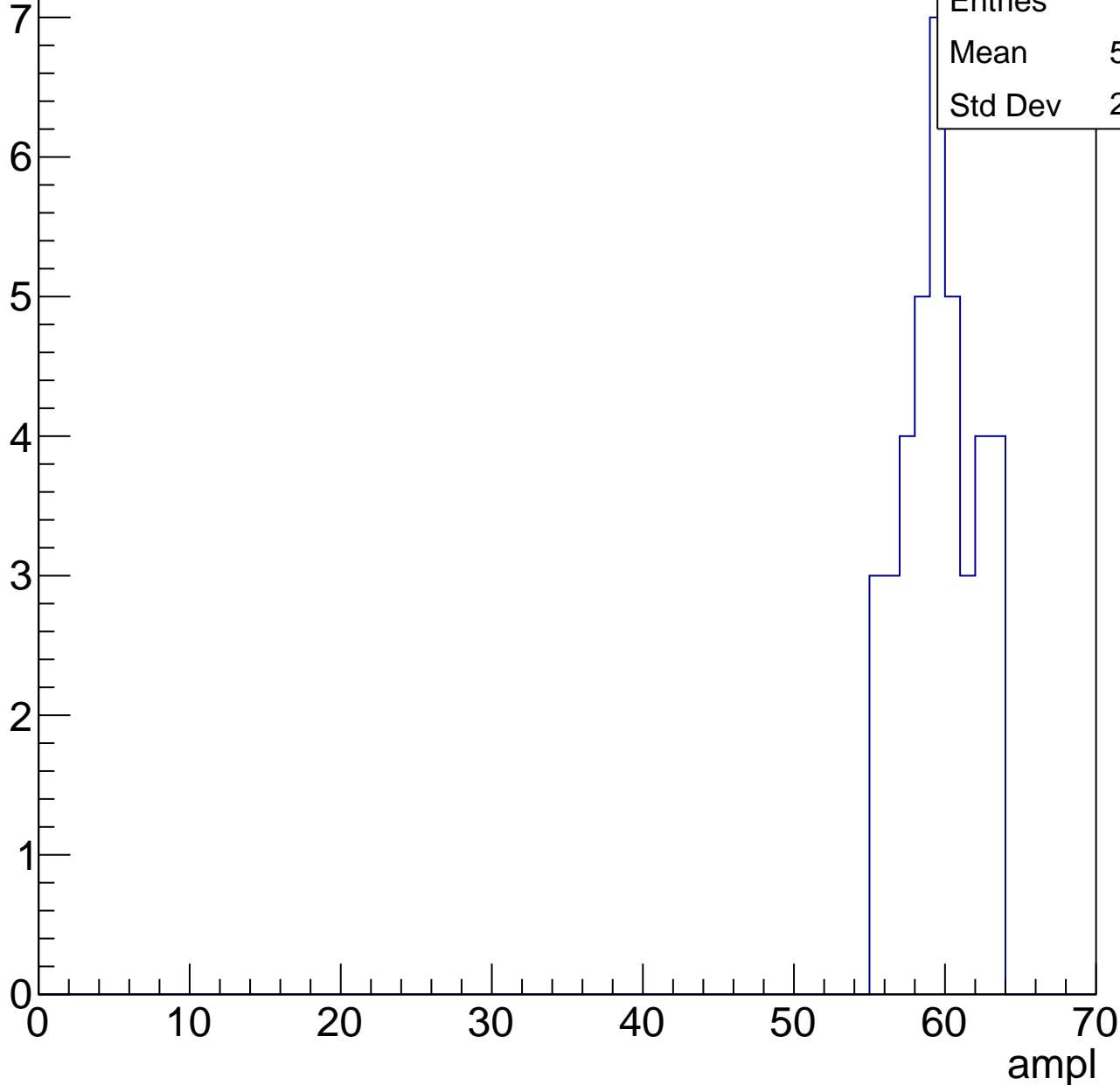


# B0L000S, U19-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

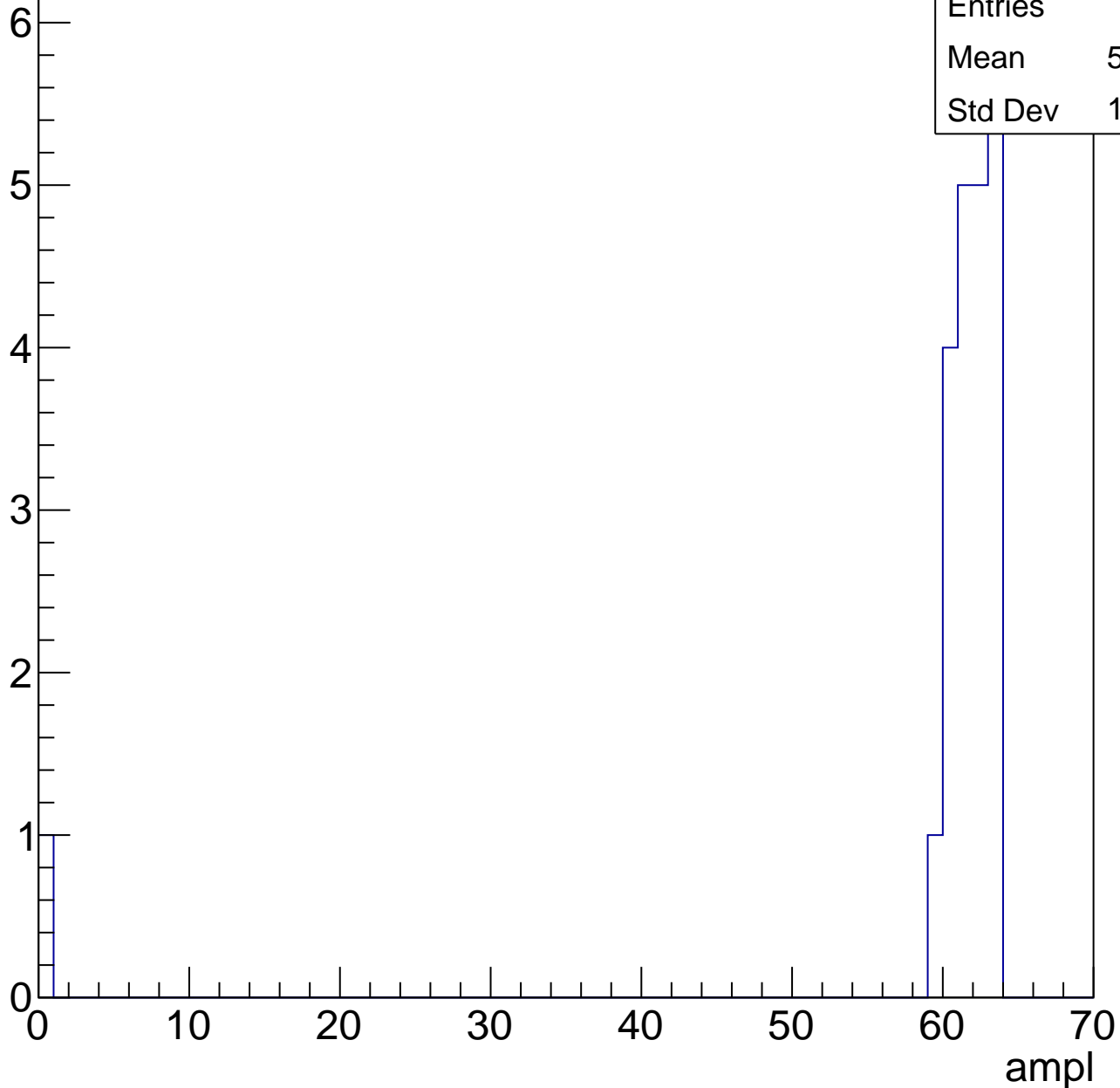
Entries	38
Mean	59.13
Std Dev	2.364



# B0L000S, U19-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	22
Mean	58.73
Std Dev	12.87



# B0L000S, U19-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U19-ch90, adc0

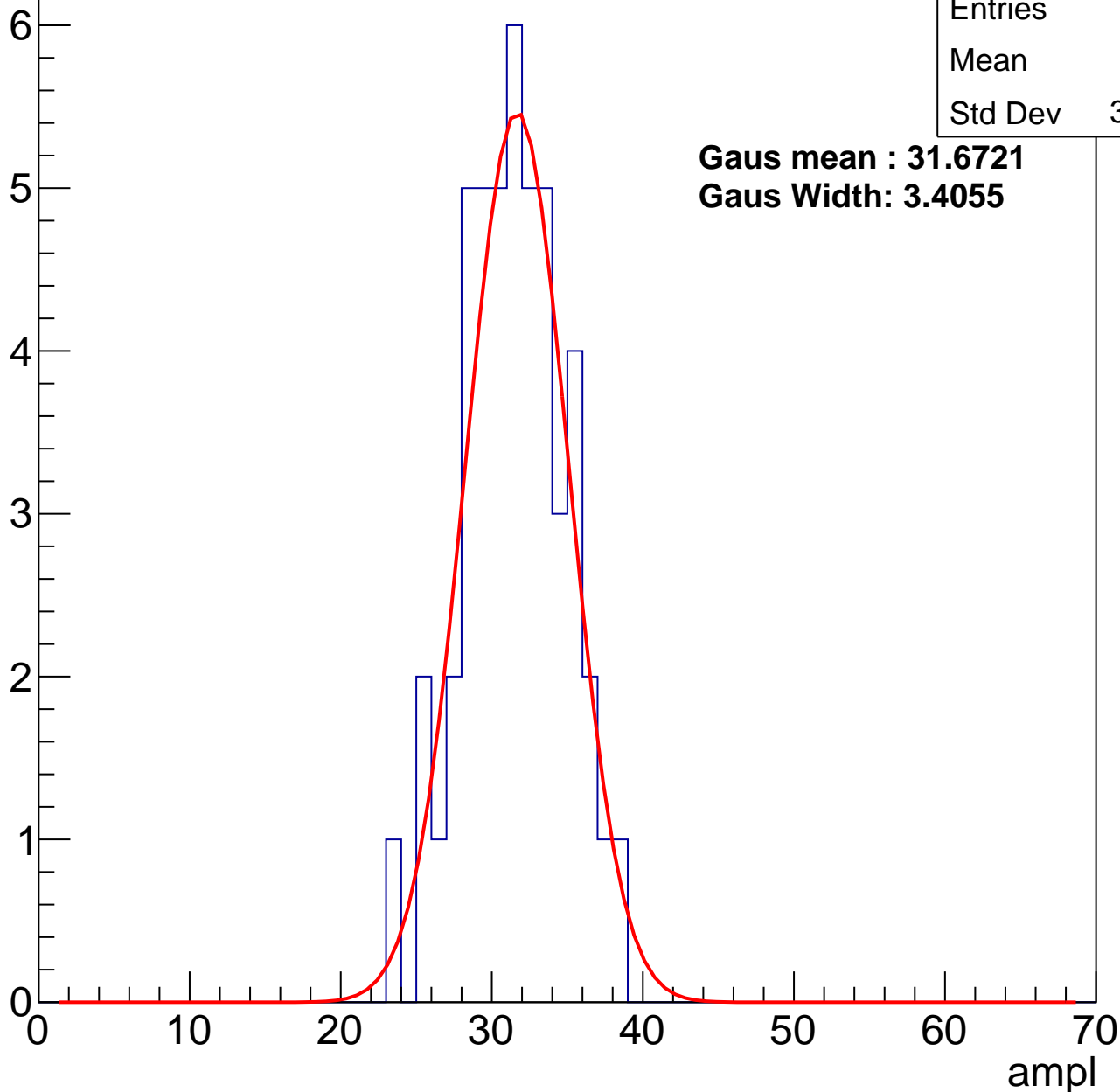
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	31
Std Dev	3.272

**Gaus mean : 31.6721**

**Gaus Width: 3.4055**



# B0L000S, U19-ch90, adc1

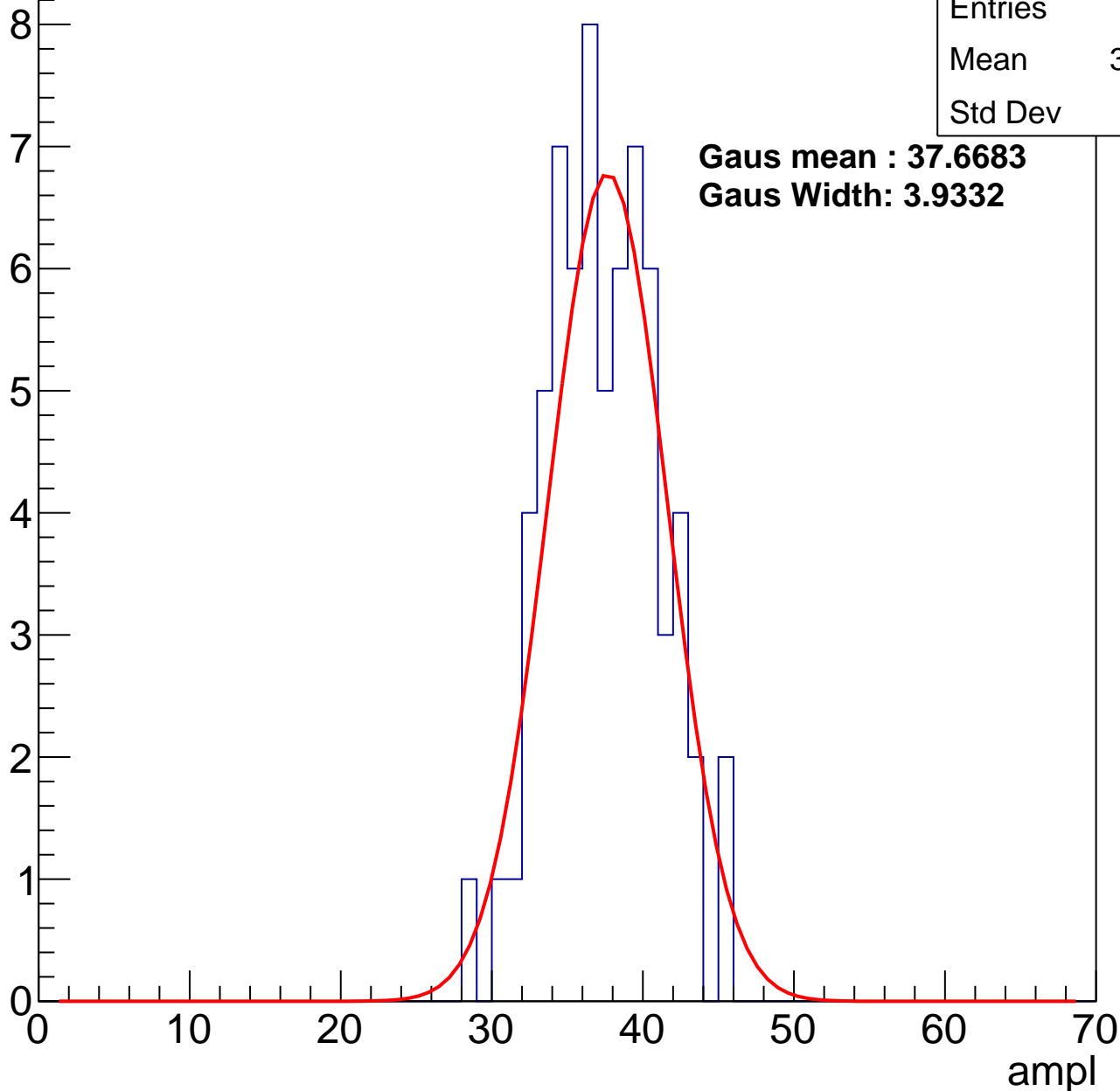
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.93
Std Dev	3.59

**Gaus mean : 37.6683**

**Gaus Width: 3.9332**



# B0L000S, U19-ch90, adc2

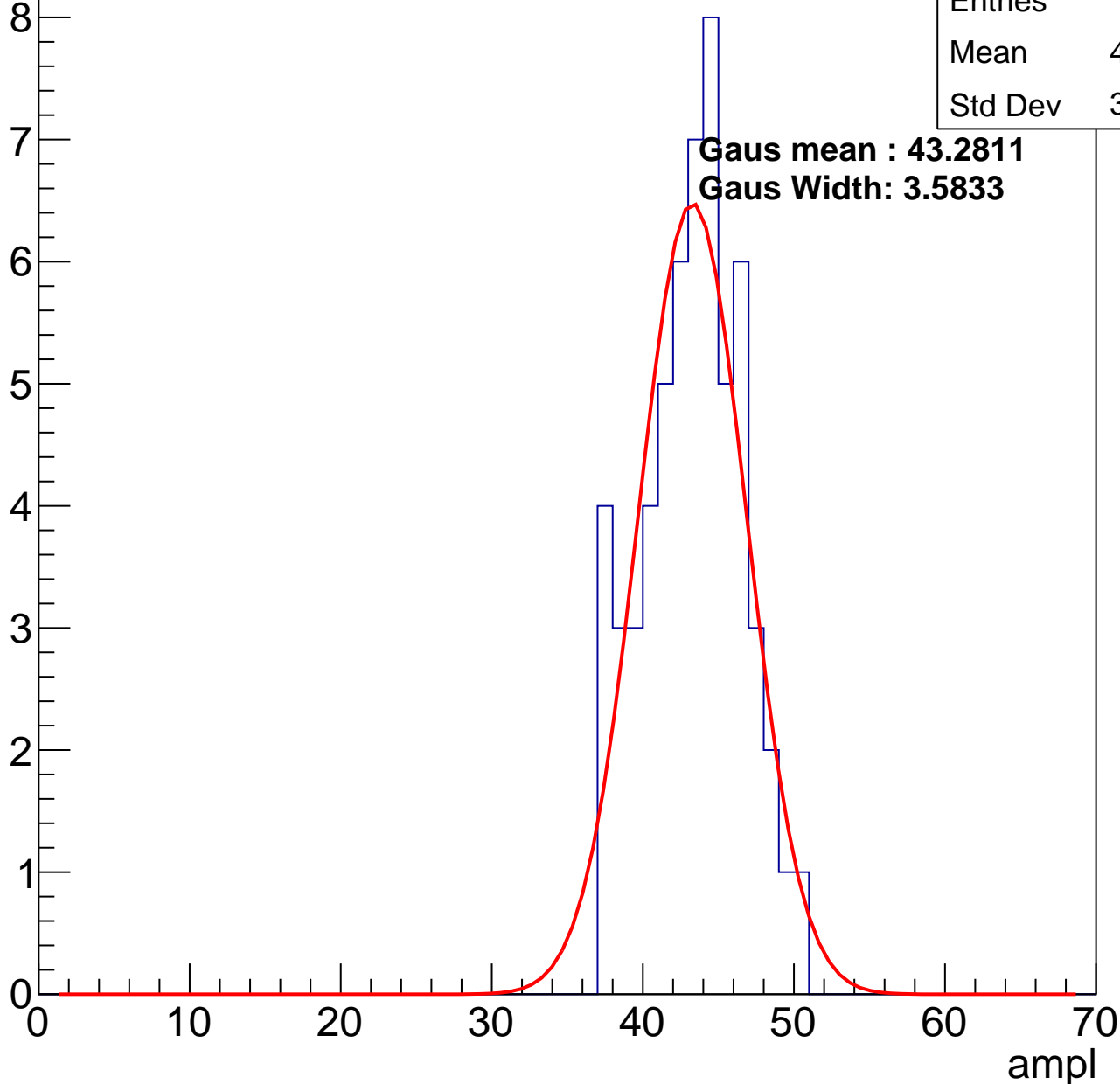
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.86
Std Dev	3.197

**Gaus mean : 43.2811**

**Gaus Width: 3.5833**

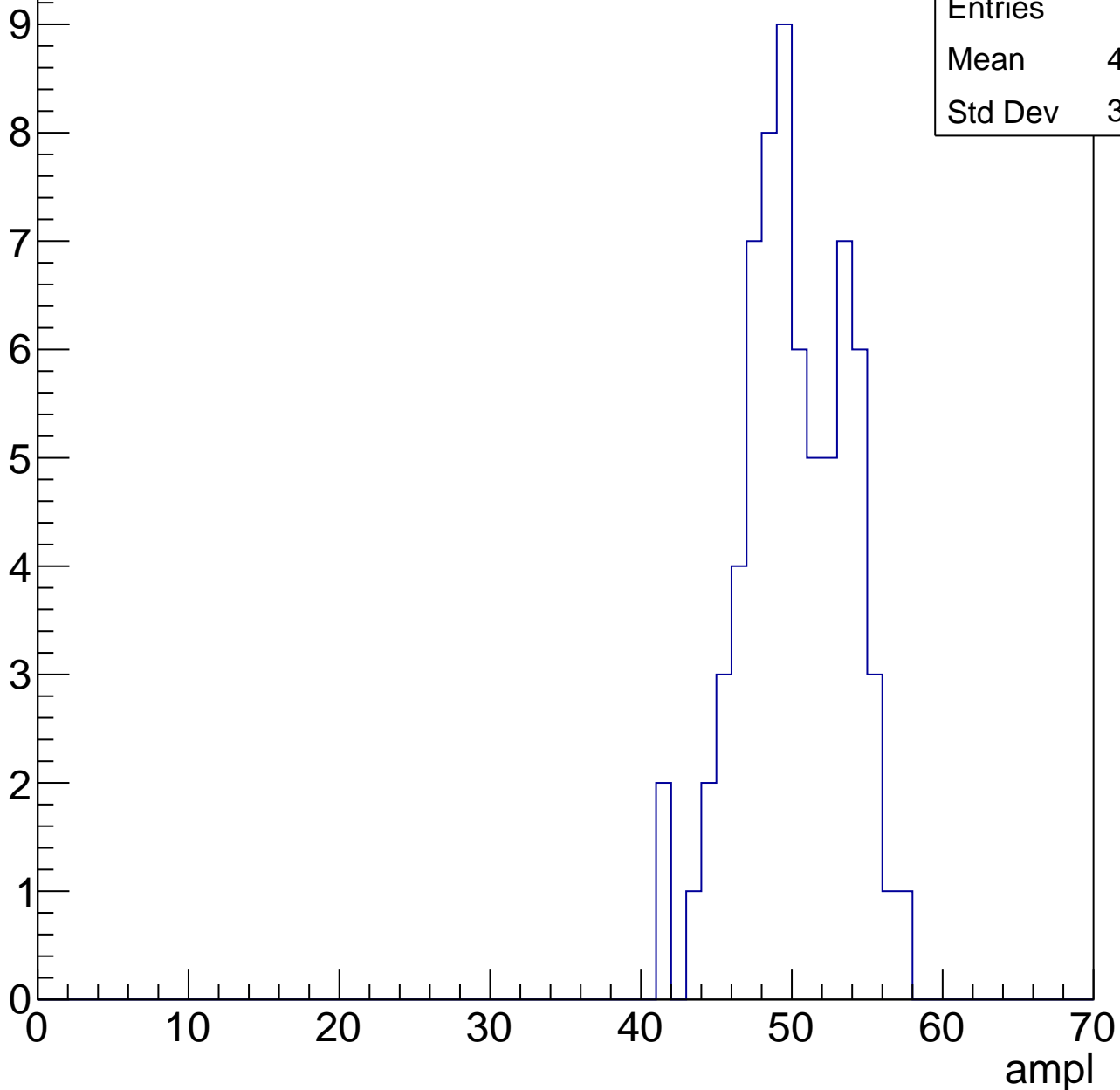


# B0L000S, U19-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.63
Std Dev	3.506

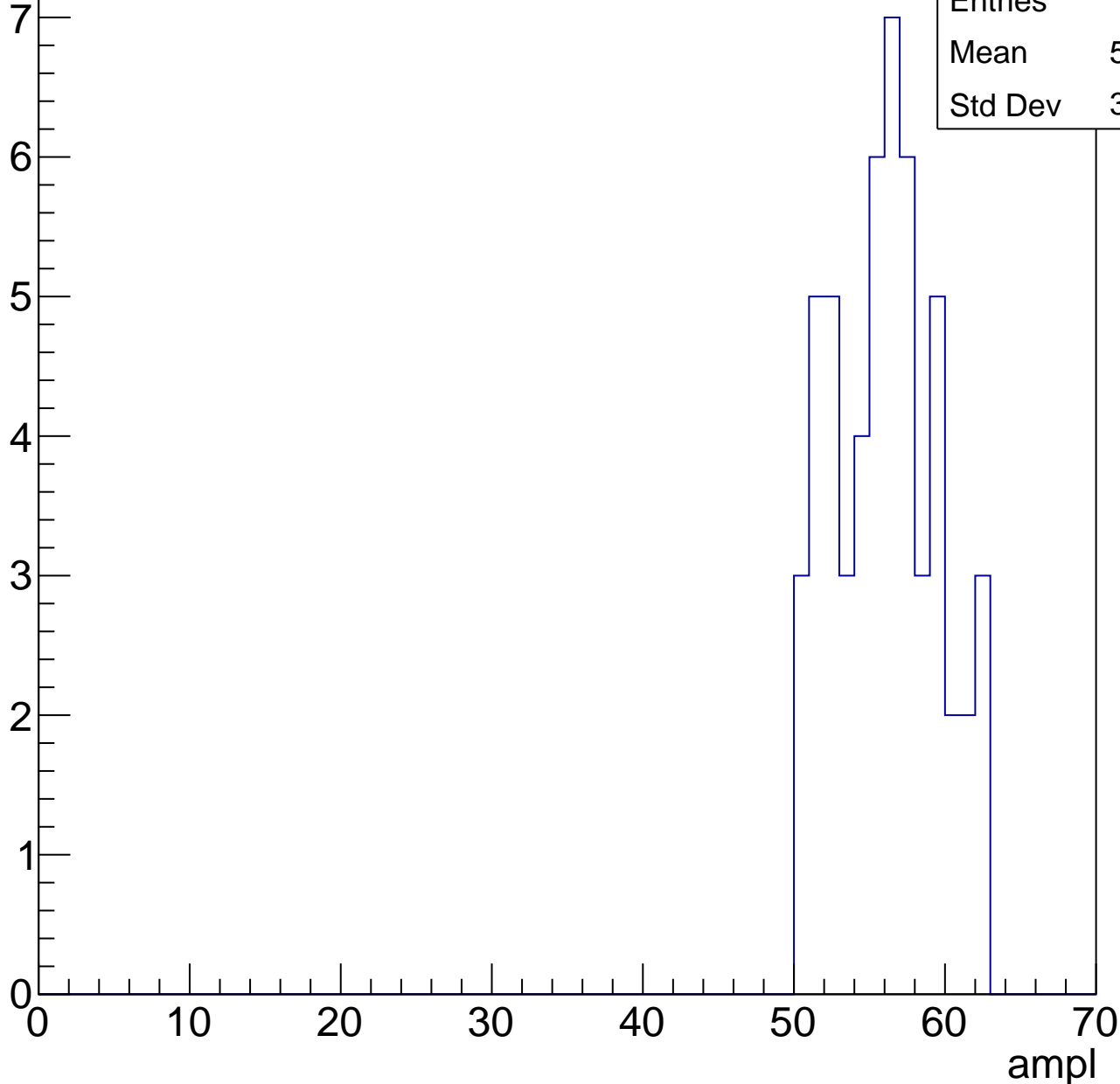


# B0L000S, U19-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.57
Std Dev	3.348

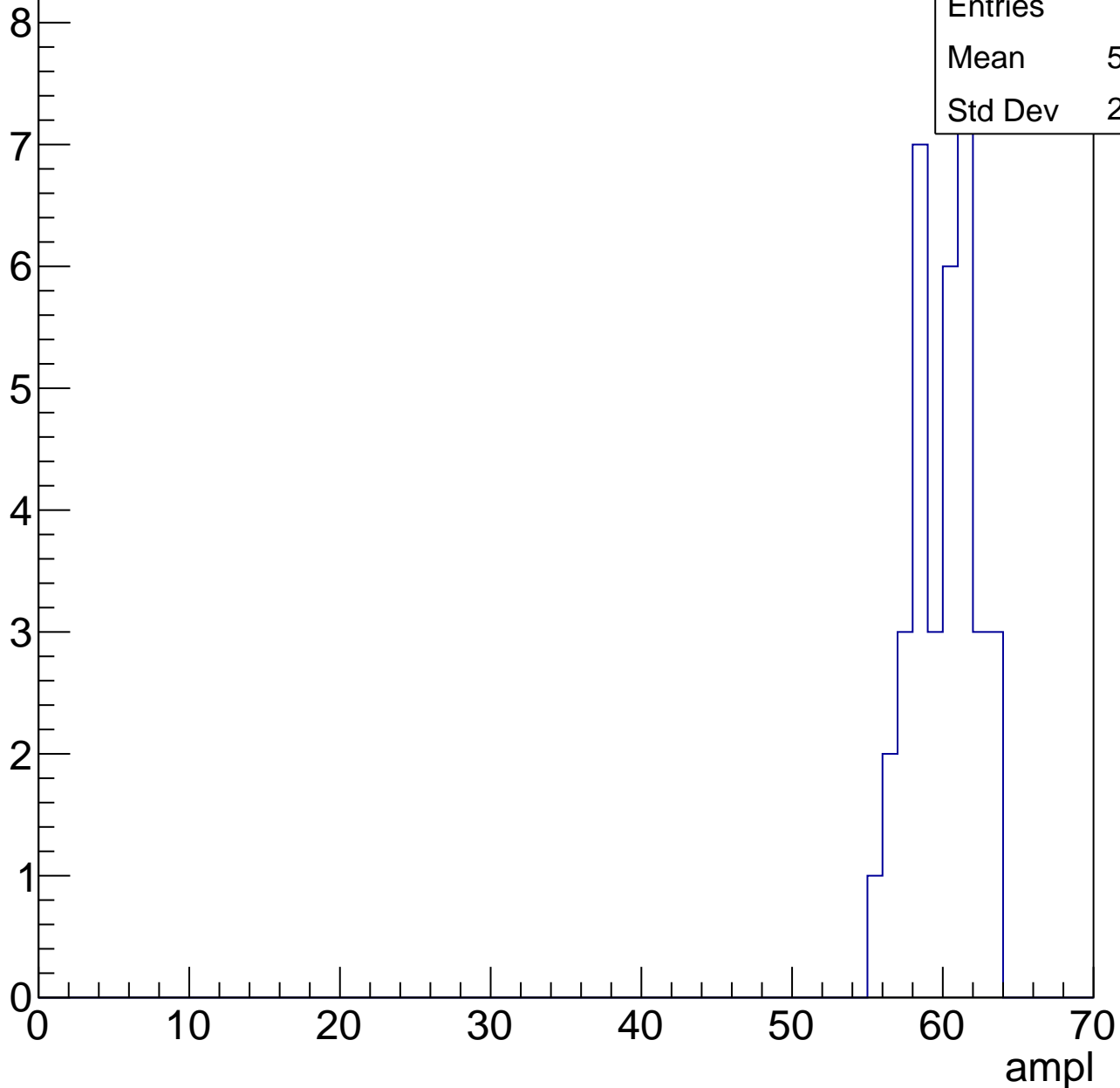


# B0L000S, U19-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	59.56
Std Dev	2.074

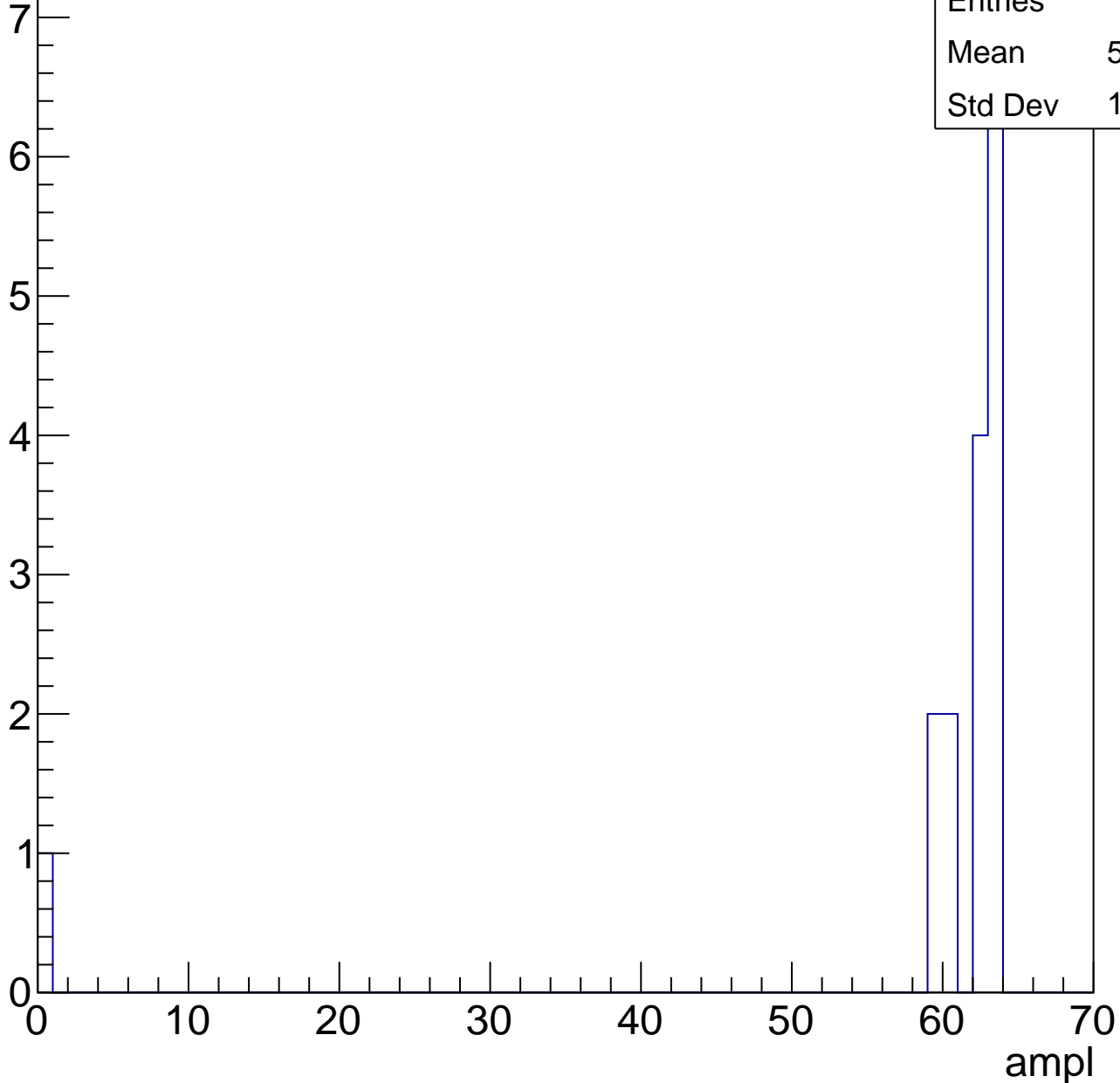


# B0L000S, U19-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.94
Std Dev	15.03





# B0L000S, U19-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch91, adc0

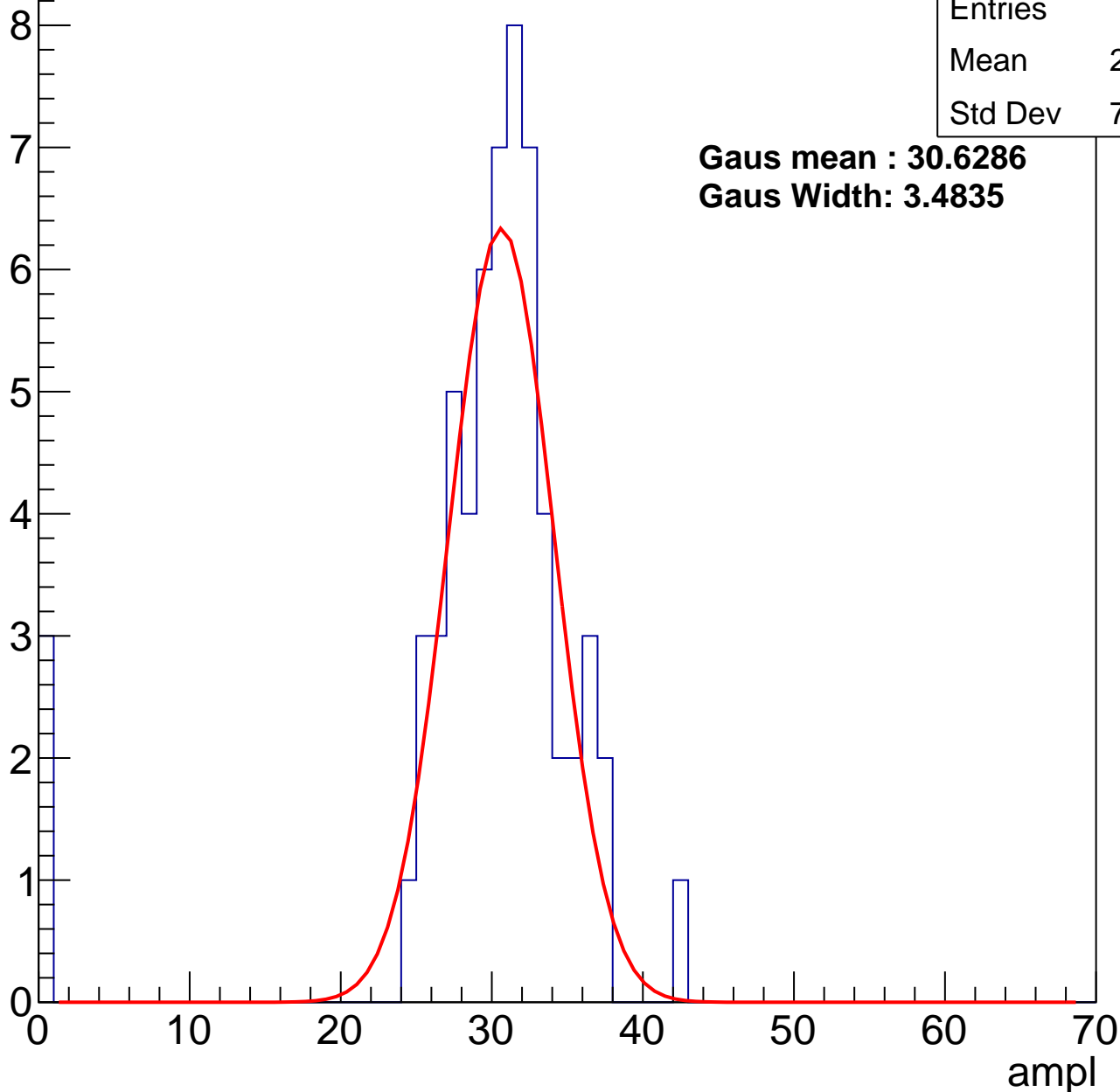
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.08
Std Dev	7.442

**Gaus mean : 30.6286**

**Gaus Width: 3.4835**



# B0L000S, U19-ch91, adc1

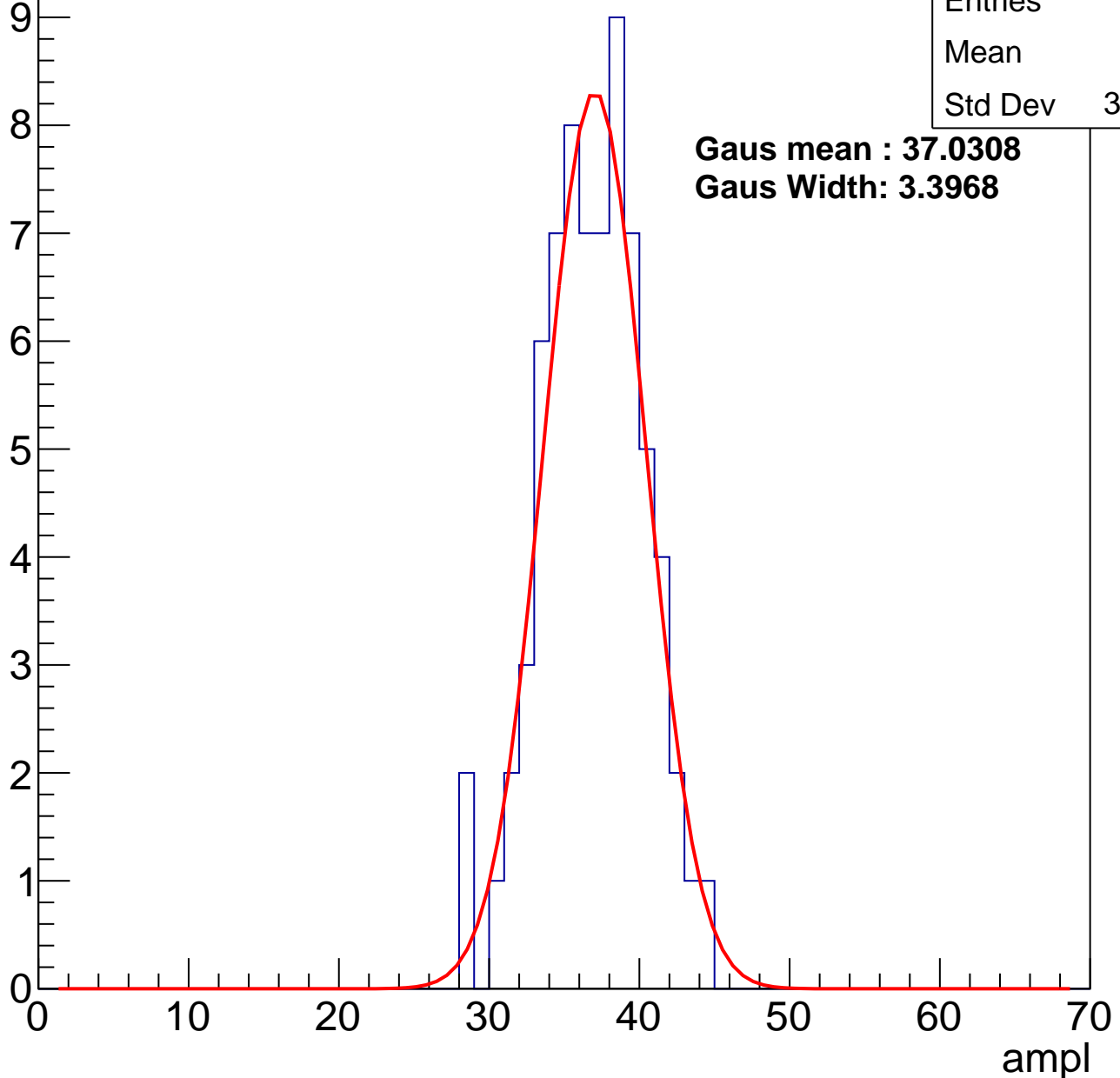
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.4
Std Dev	3.357

**Gaus mean : 37.0308**

**Gaus Width: 3.3968**



# B0L000S, U19-ch91, adc2

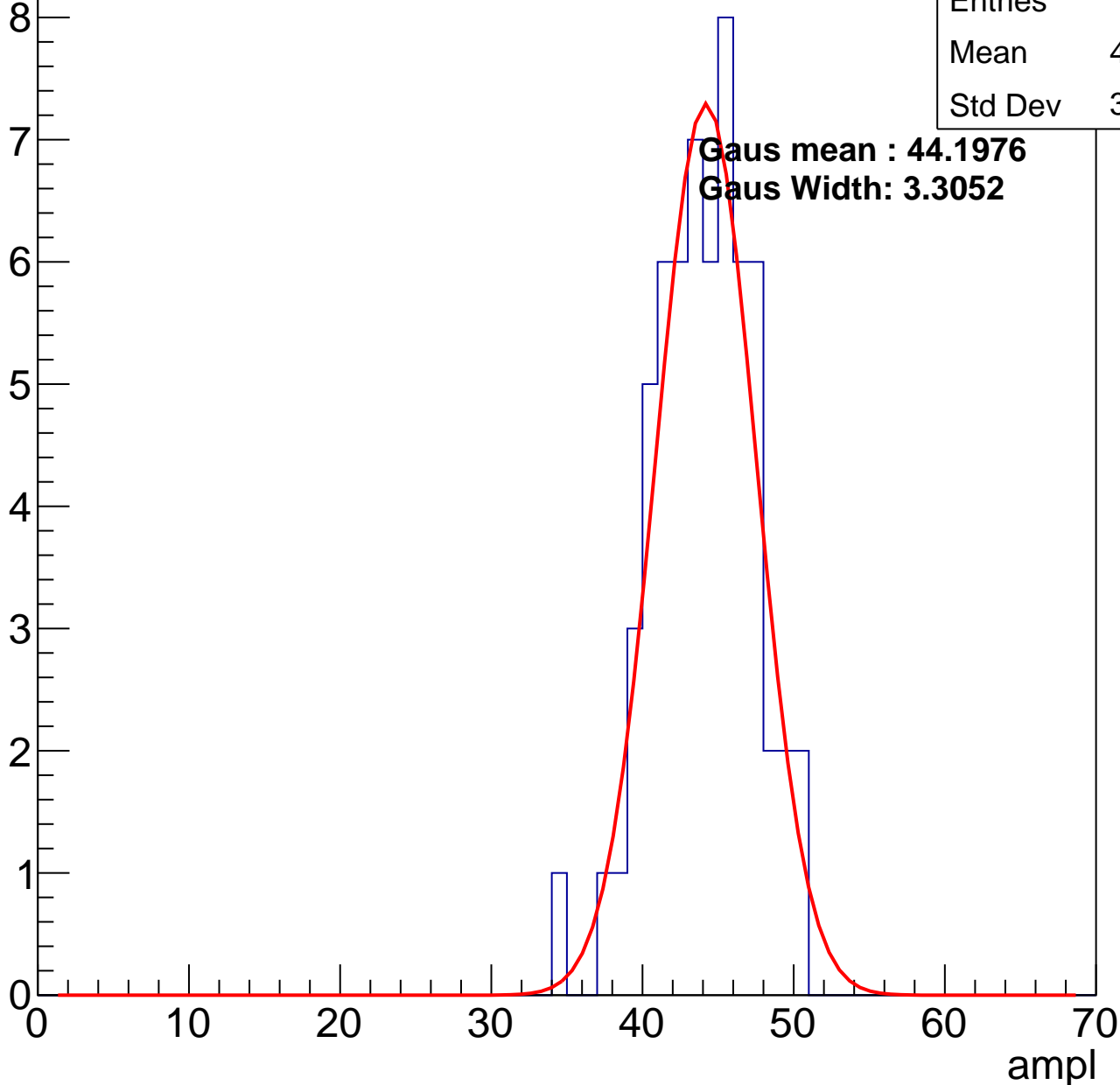
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.56
Std Dev	3.246

**Gaus mean : 44.1976**

**Gaus Width: 3.3052**

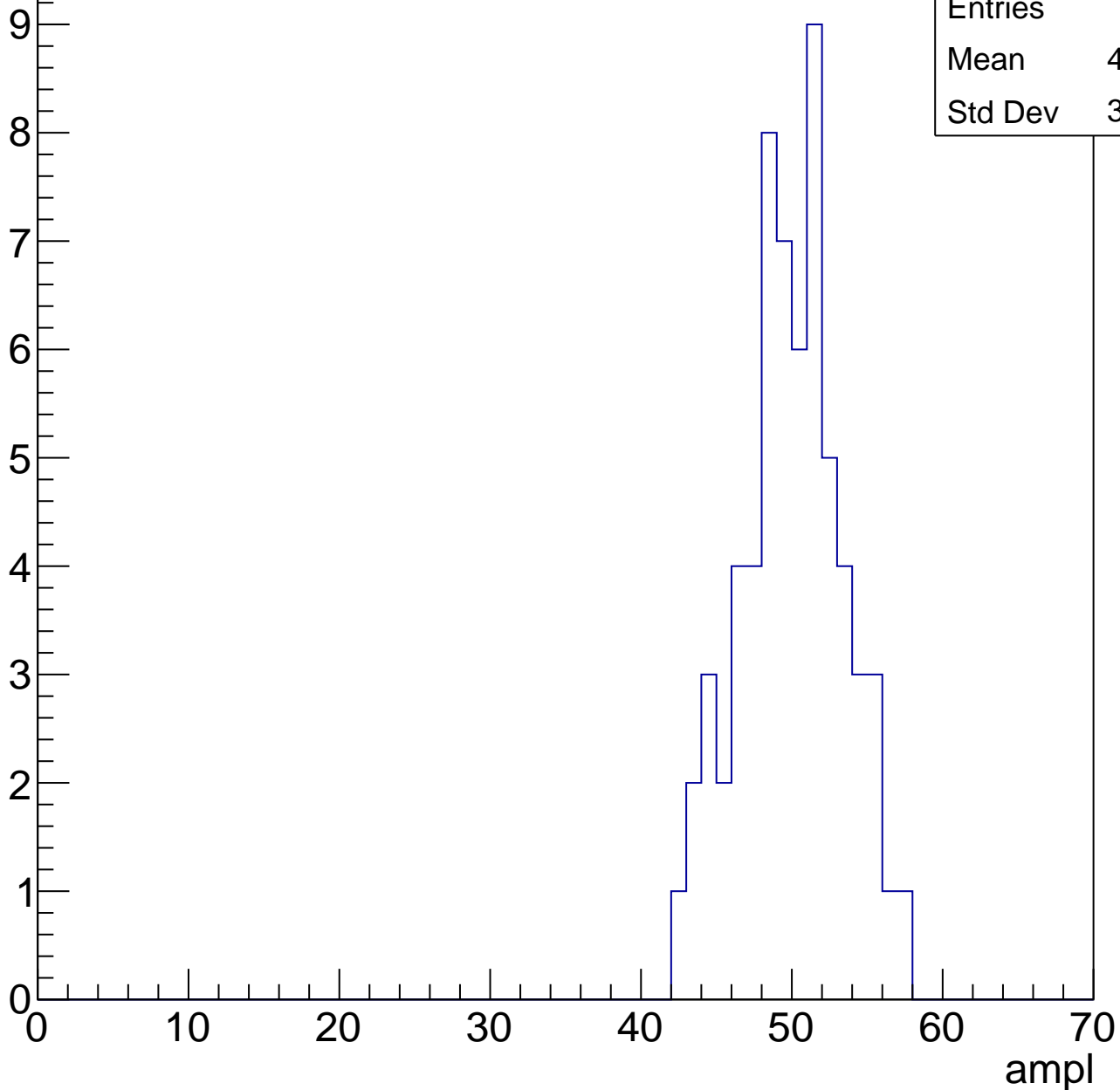


# B0L000S, U19-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

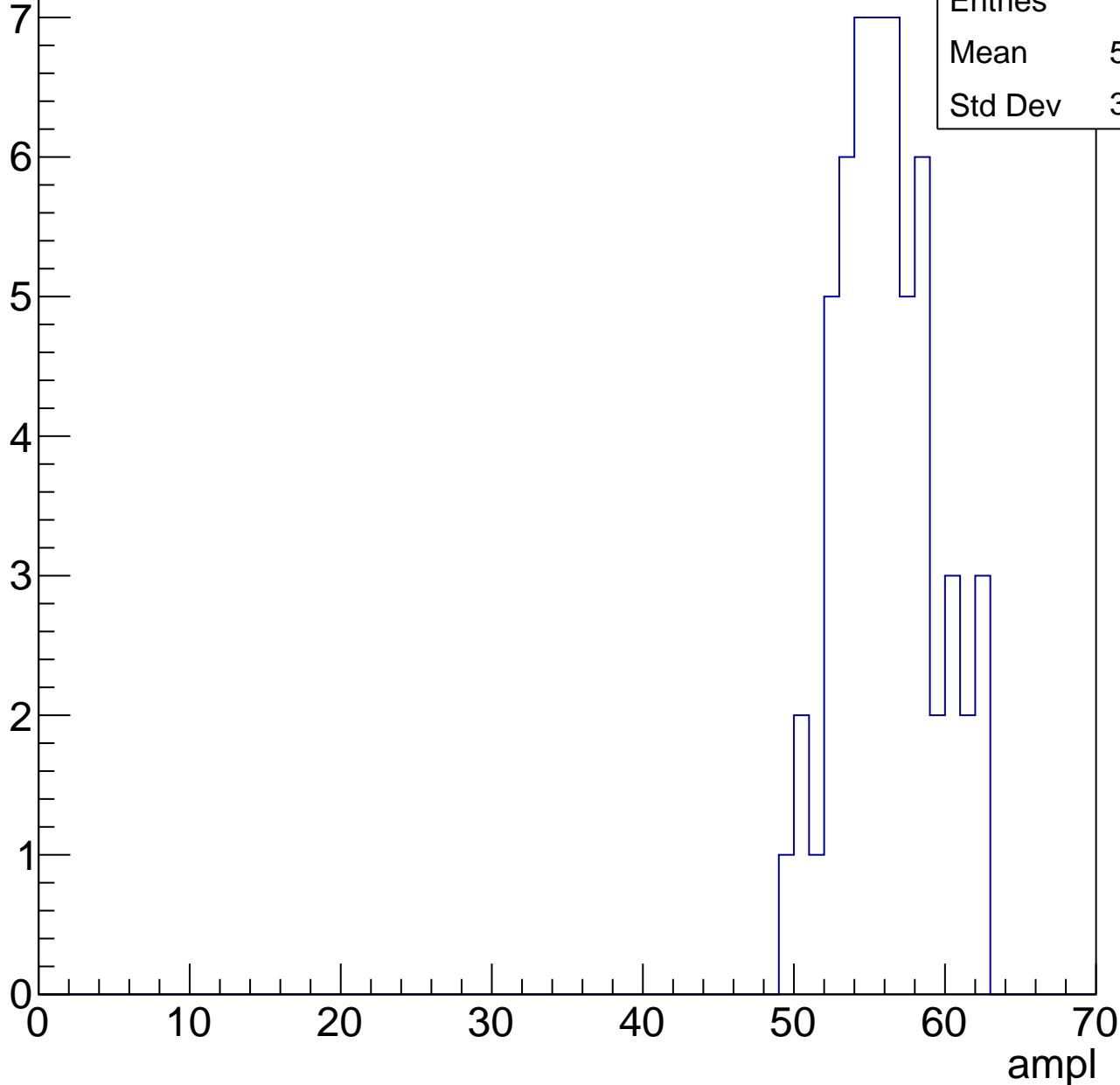
Entries	63
Mean	49.52
Std Dev	3.375



# B0L000S, U19-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

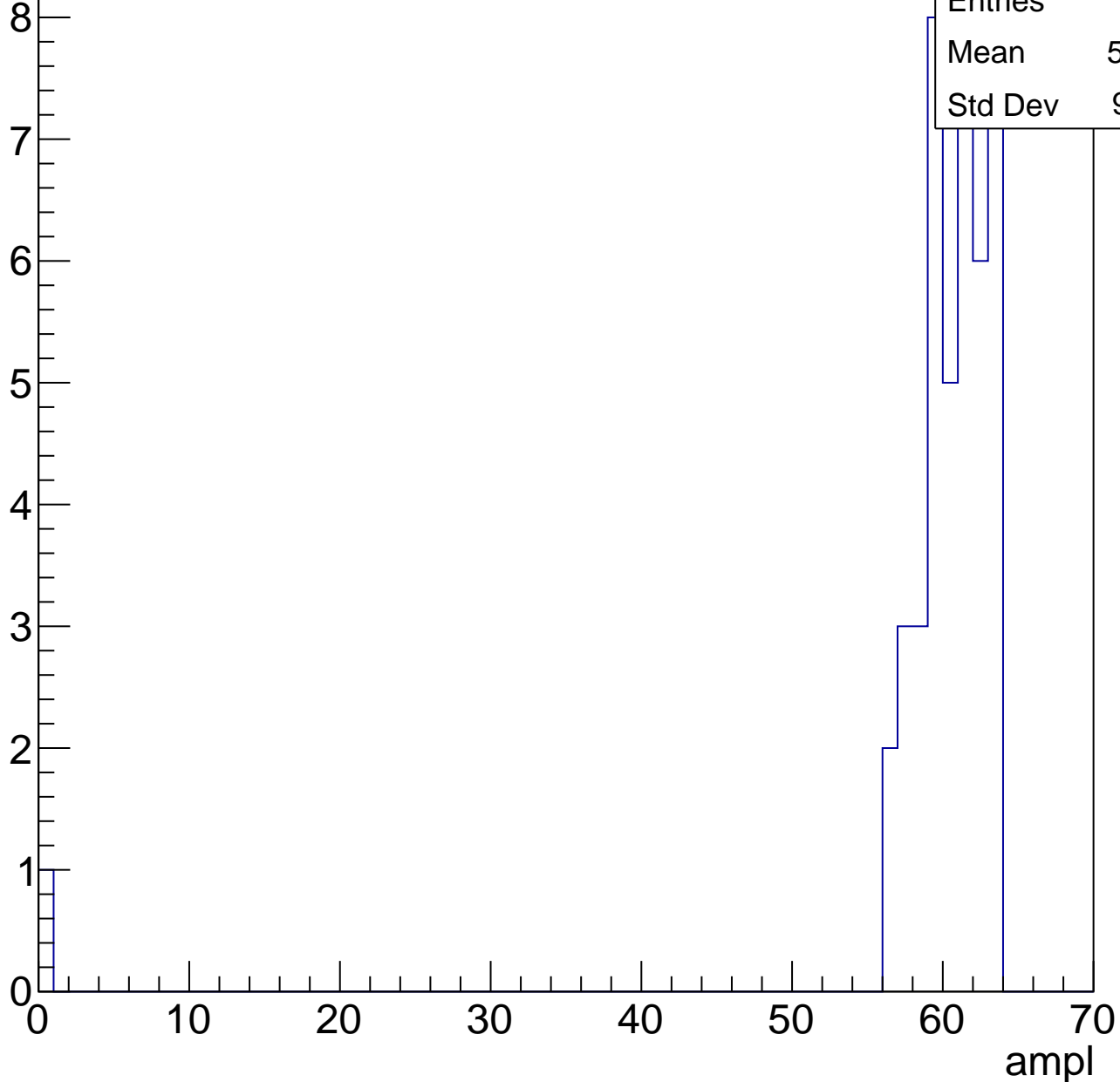


Entries	57
Mean	55.65
Std Dev	3.137

# B0L000S, U19-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

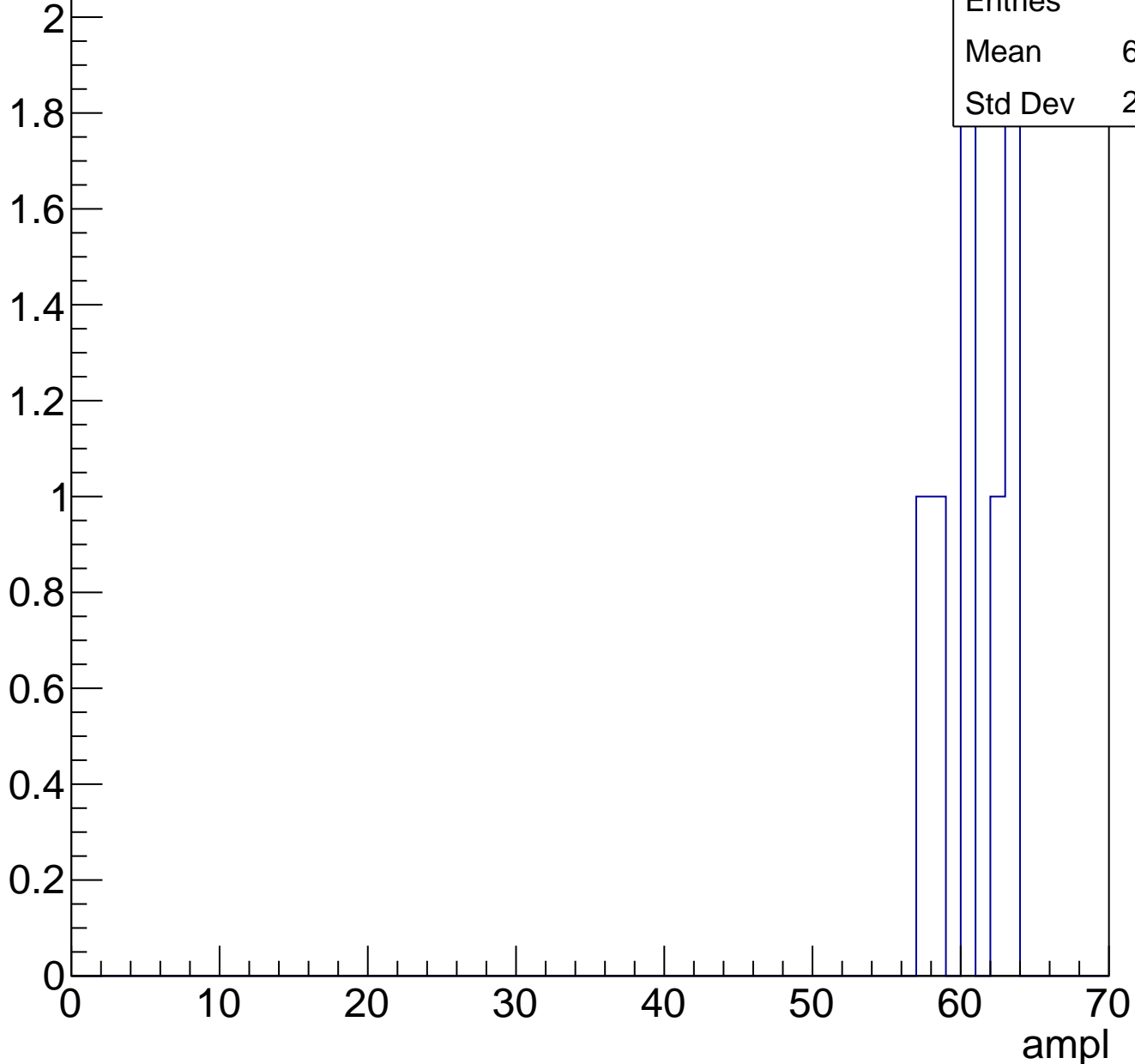
Entry



# B0L000S, U19-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	7
Mean	60.43
Std Dev	2.195



# B0L000S, U19-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch92, adc0

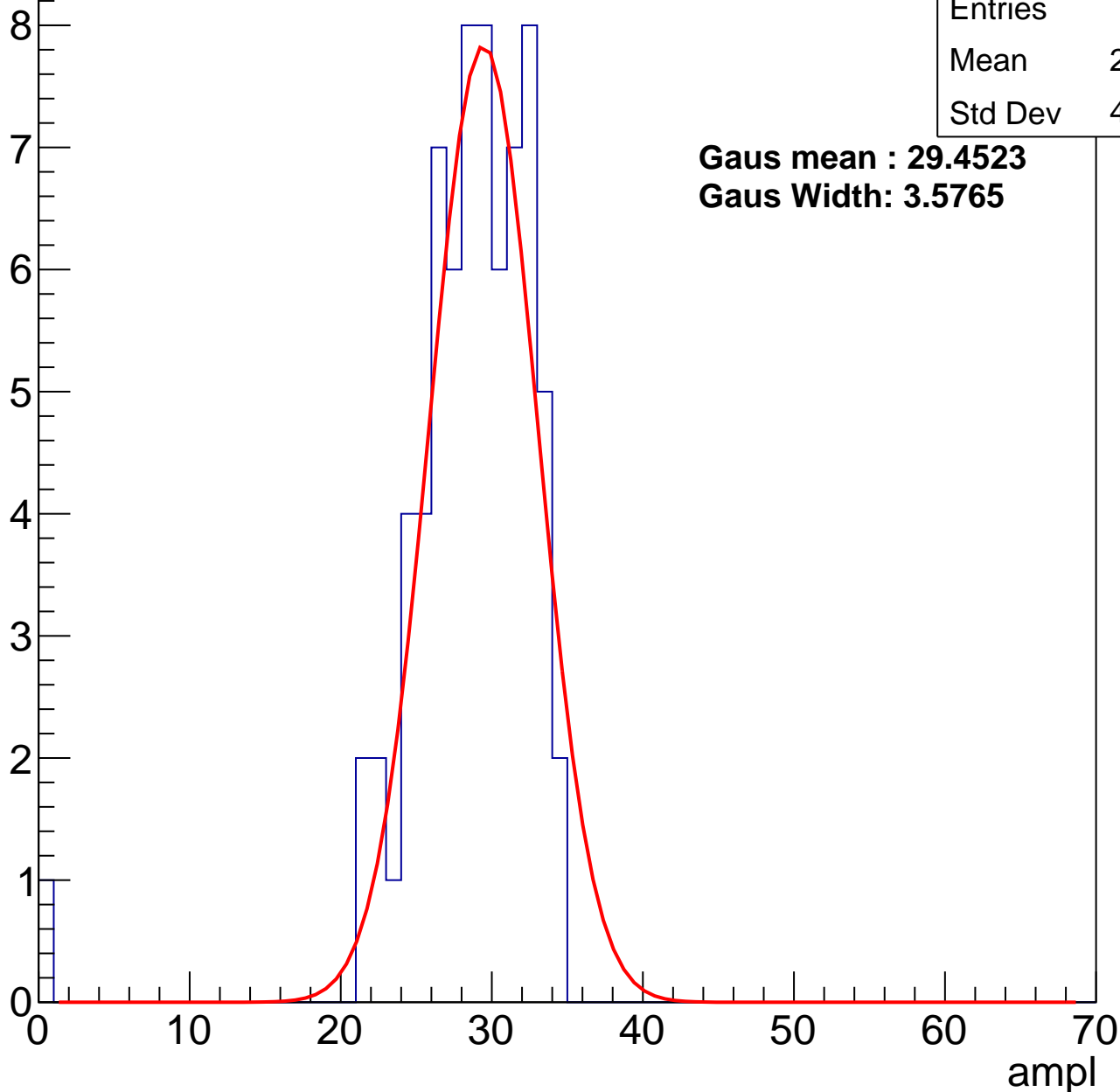
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	28.04
Std Dev	4.646

**Gaus mean : 29.4523**

**Gaus Width: 3.5765**



# B0L000S, U19-ch92, adc1

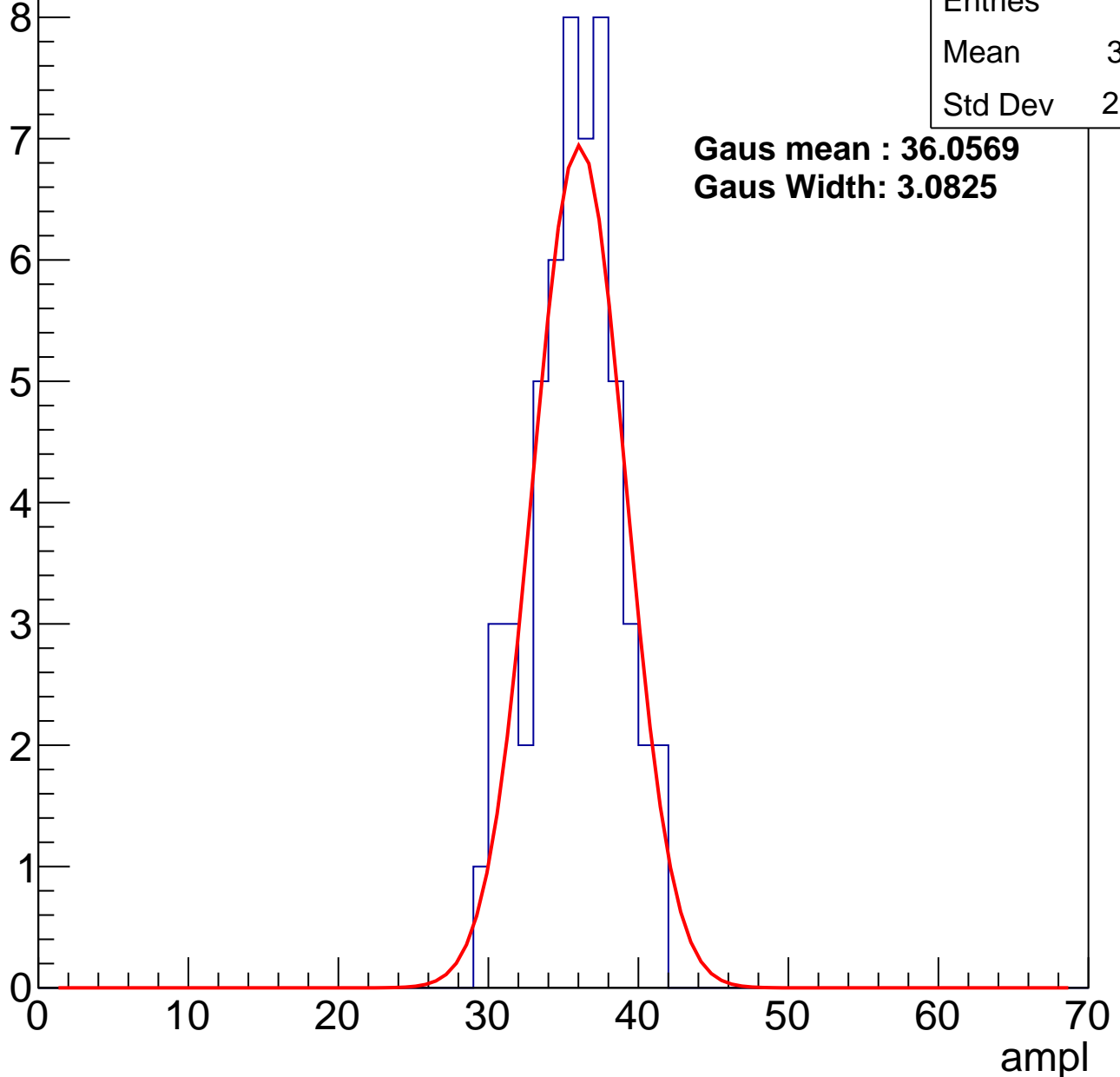
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	35.31
Std Dev	2.866

**Gaus mean : 36.0569**

**Gaus Width: 3.0825**



# B0L000S, U19-ch92, adc2

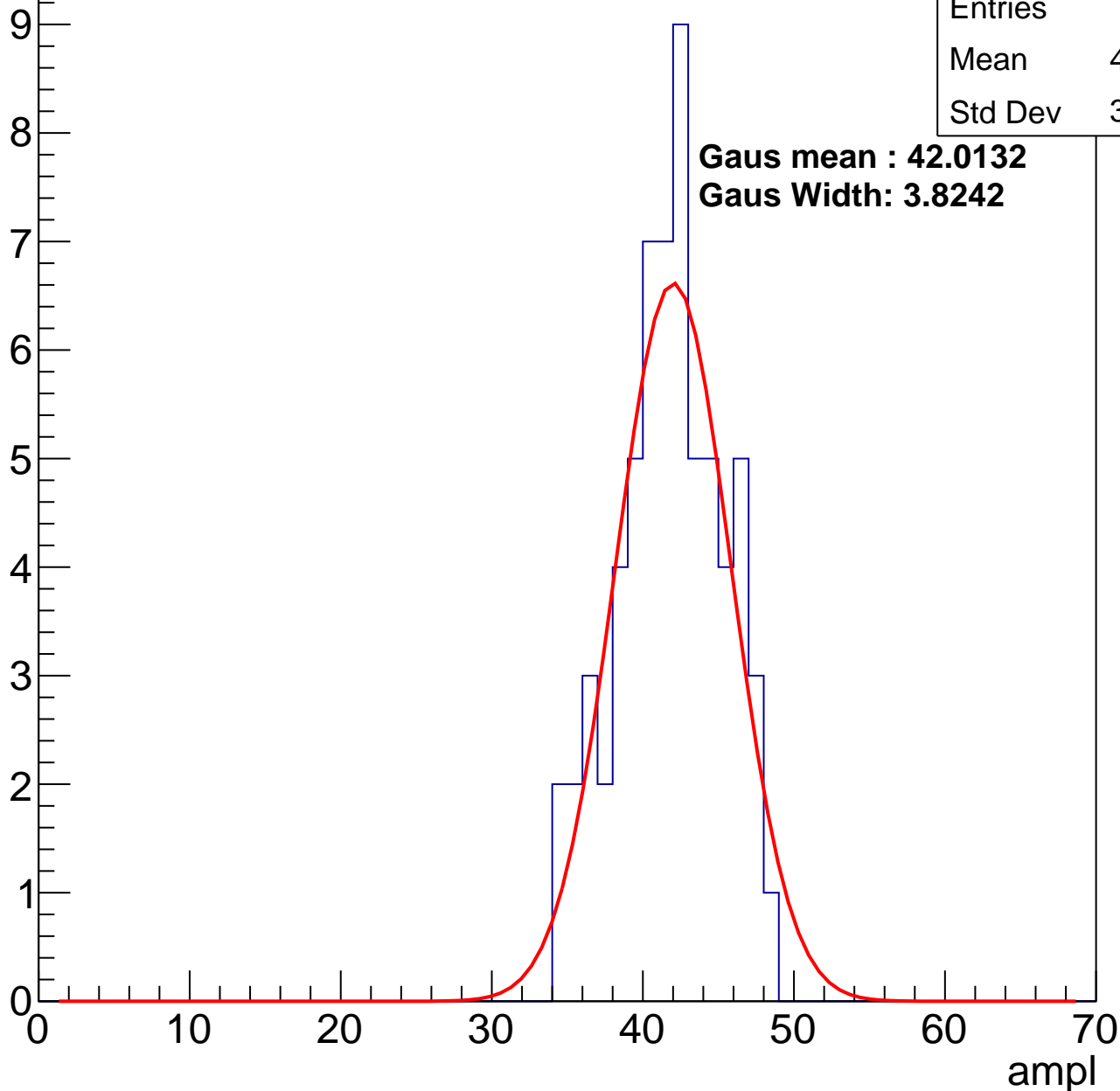
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.34
Std Dev	3.429

**Gaus mean : 42.0132**

**Gaus Width: 3.8242**

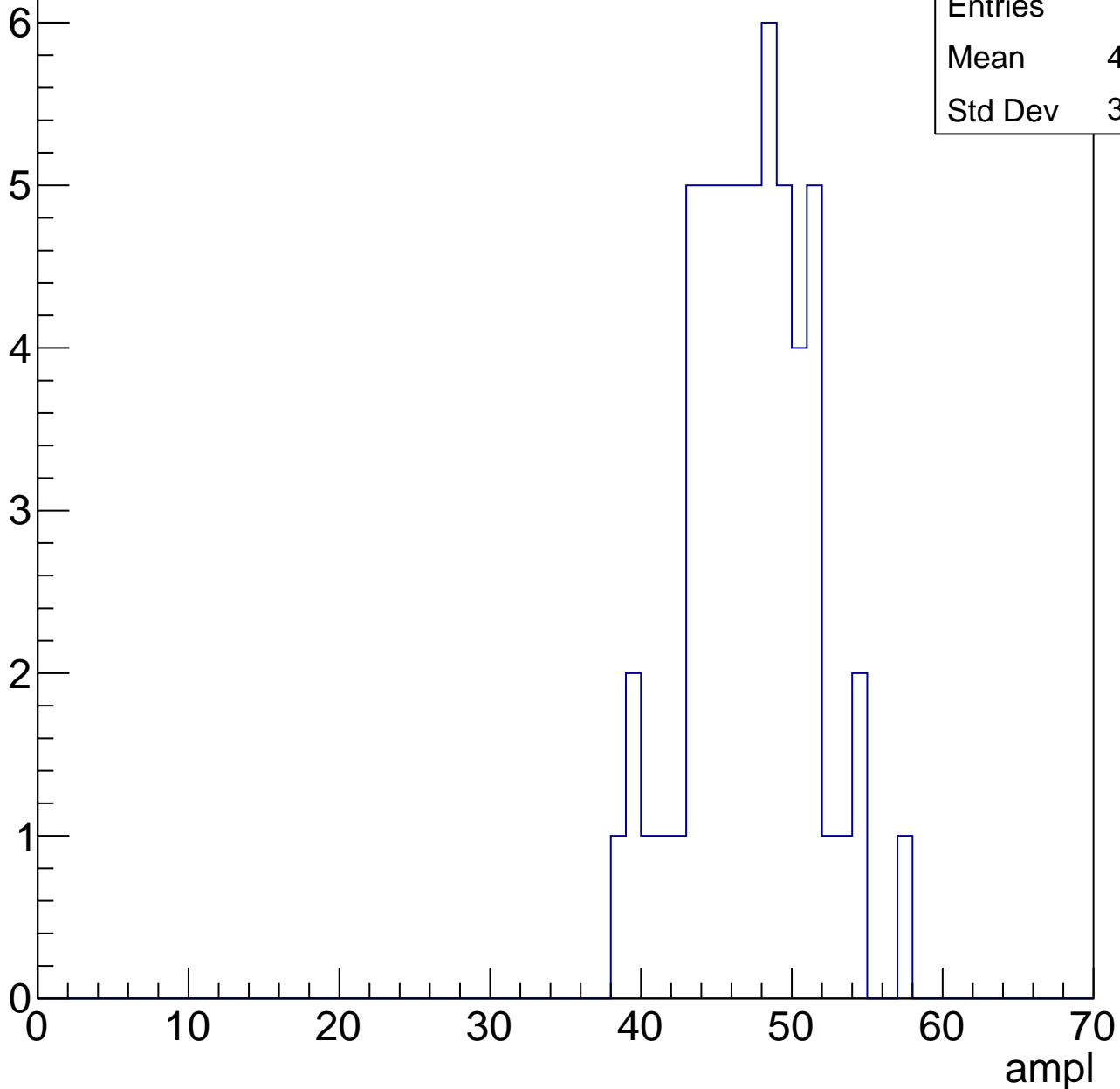


# B0L000S, U19-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	46.82
Std Dev	3.937

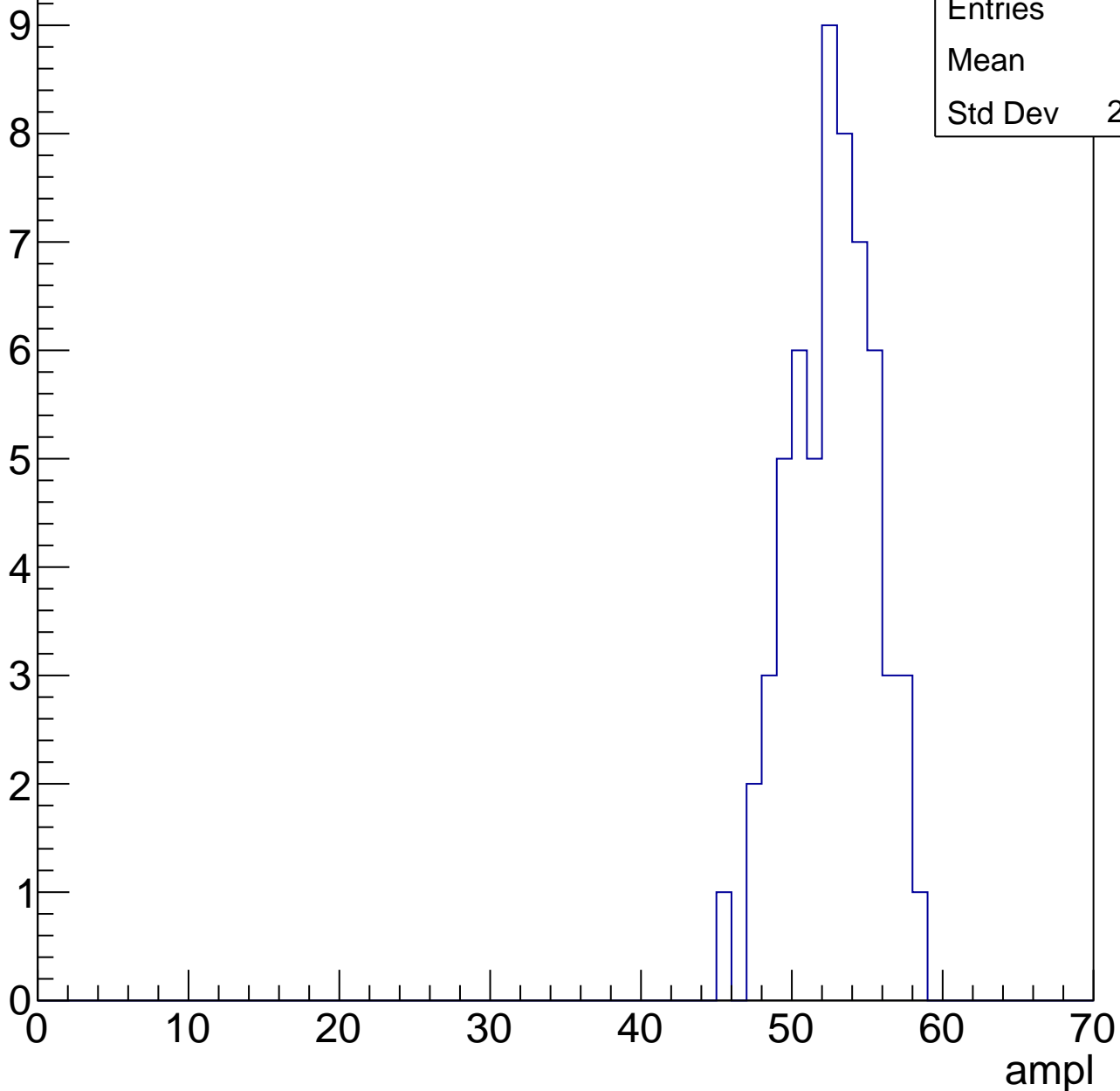


# B0L000S, U19-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	52.2
Std Dev	2.815

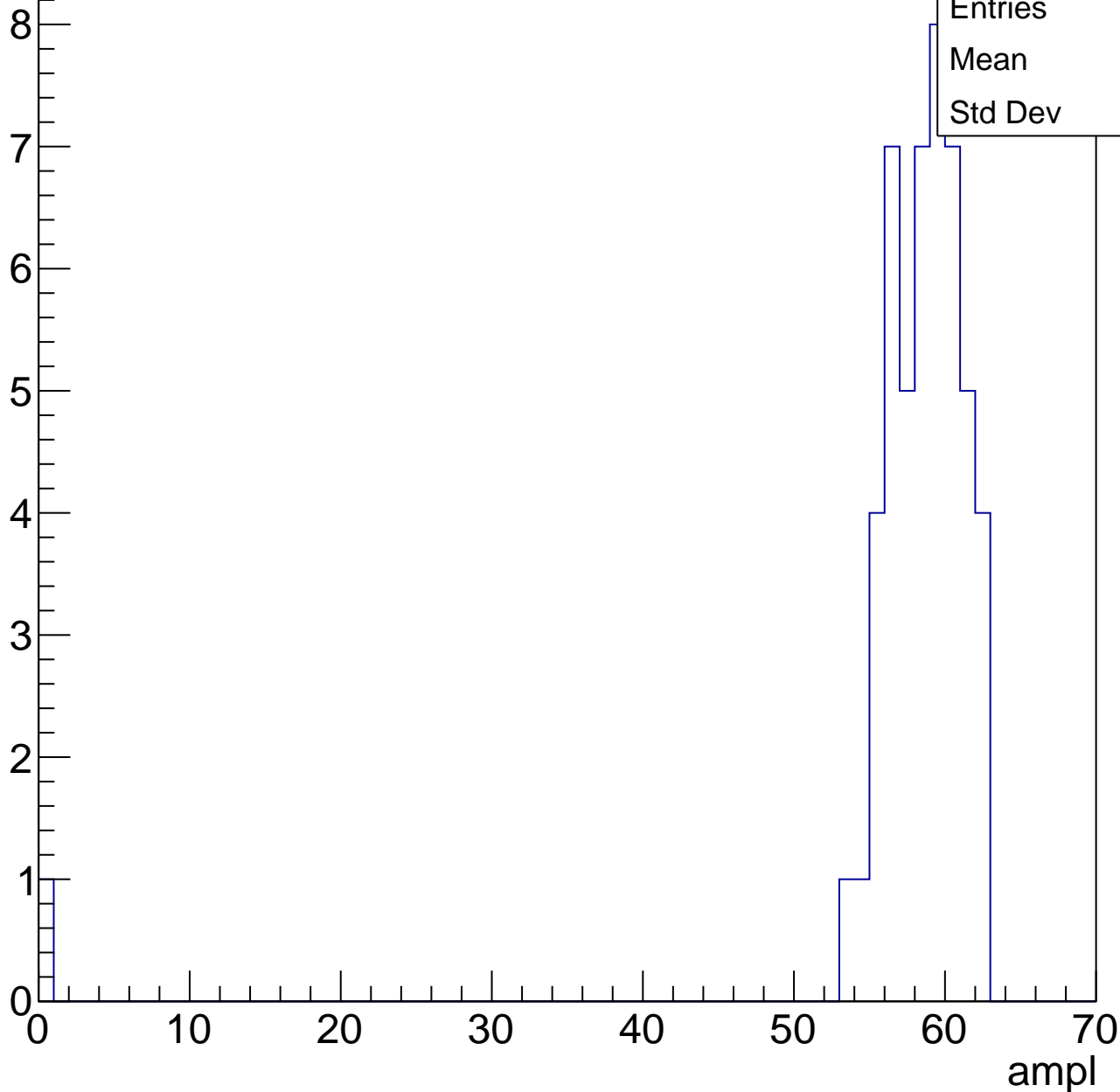


# B0L000S, U19-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	57.1
Std Dev	8.46

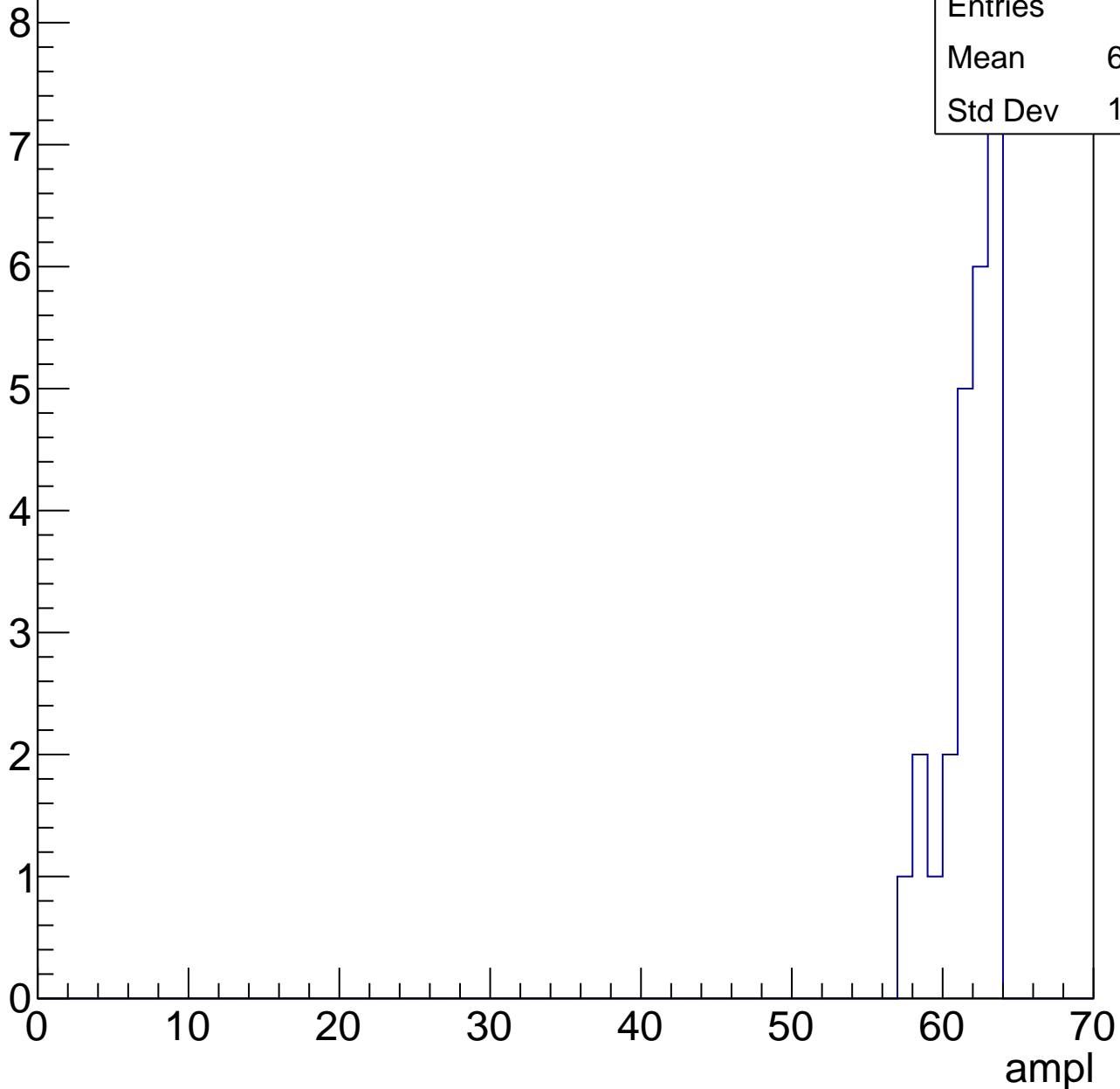


# B0L000S, U19-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	25
Mean	61.32
Std Dev	1.737

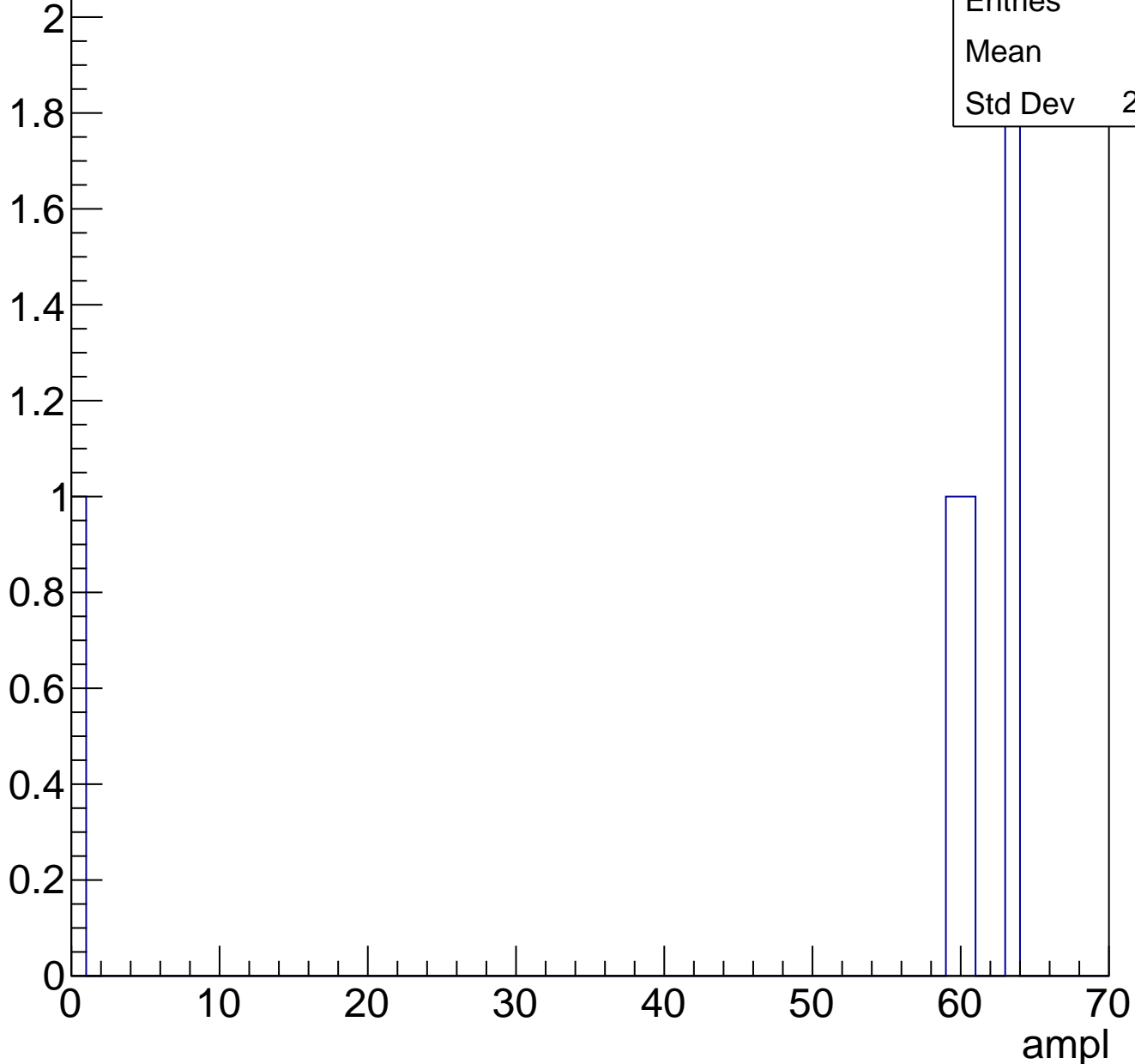




# B0L000S, U19-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch93, adc0

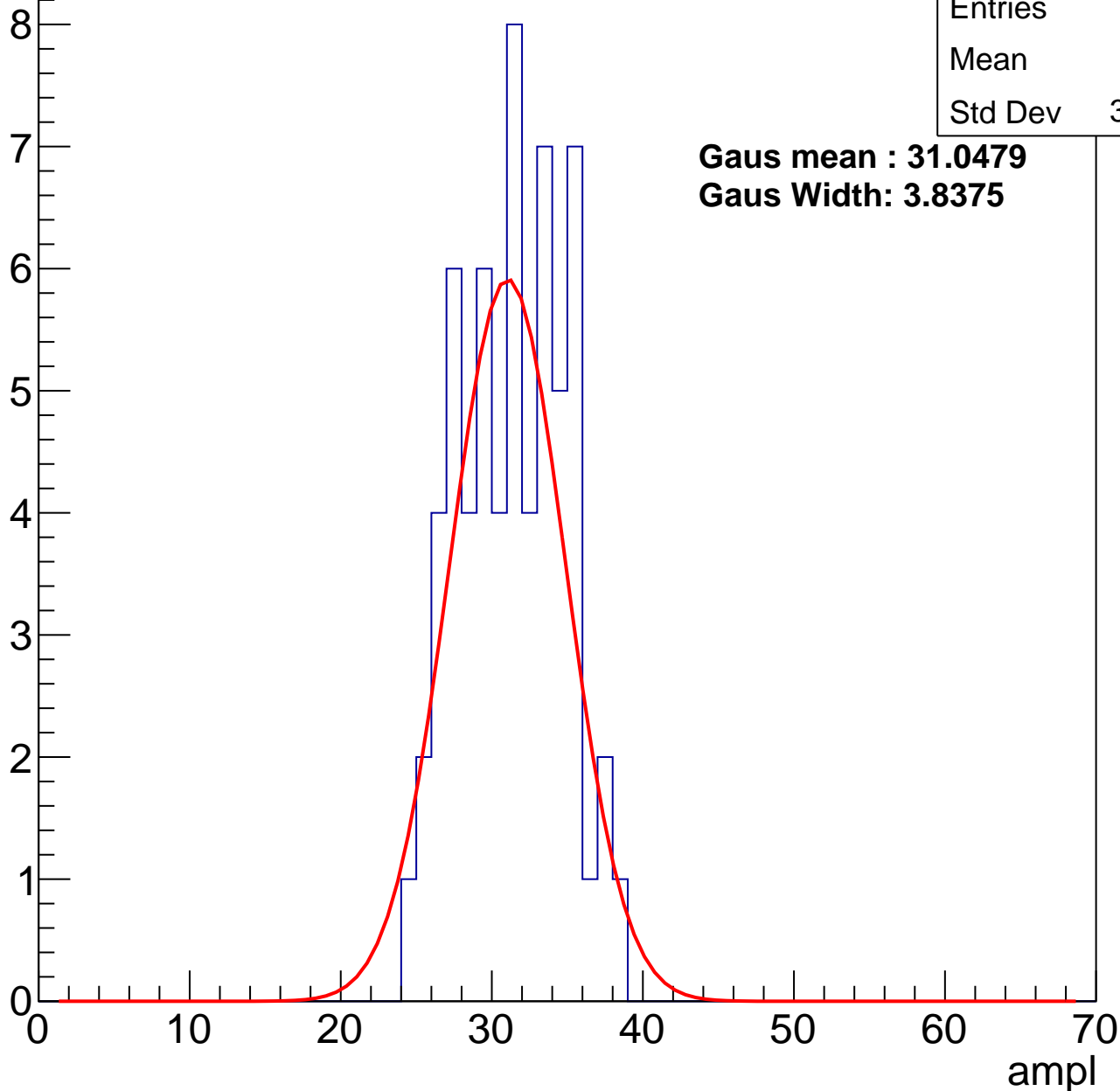
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.9
Std Dev	3.397

**Gaus mean : 31.0479**

**Gaus Width: 3.8375**



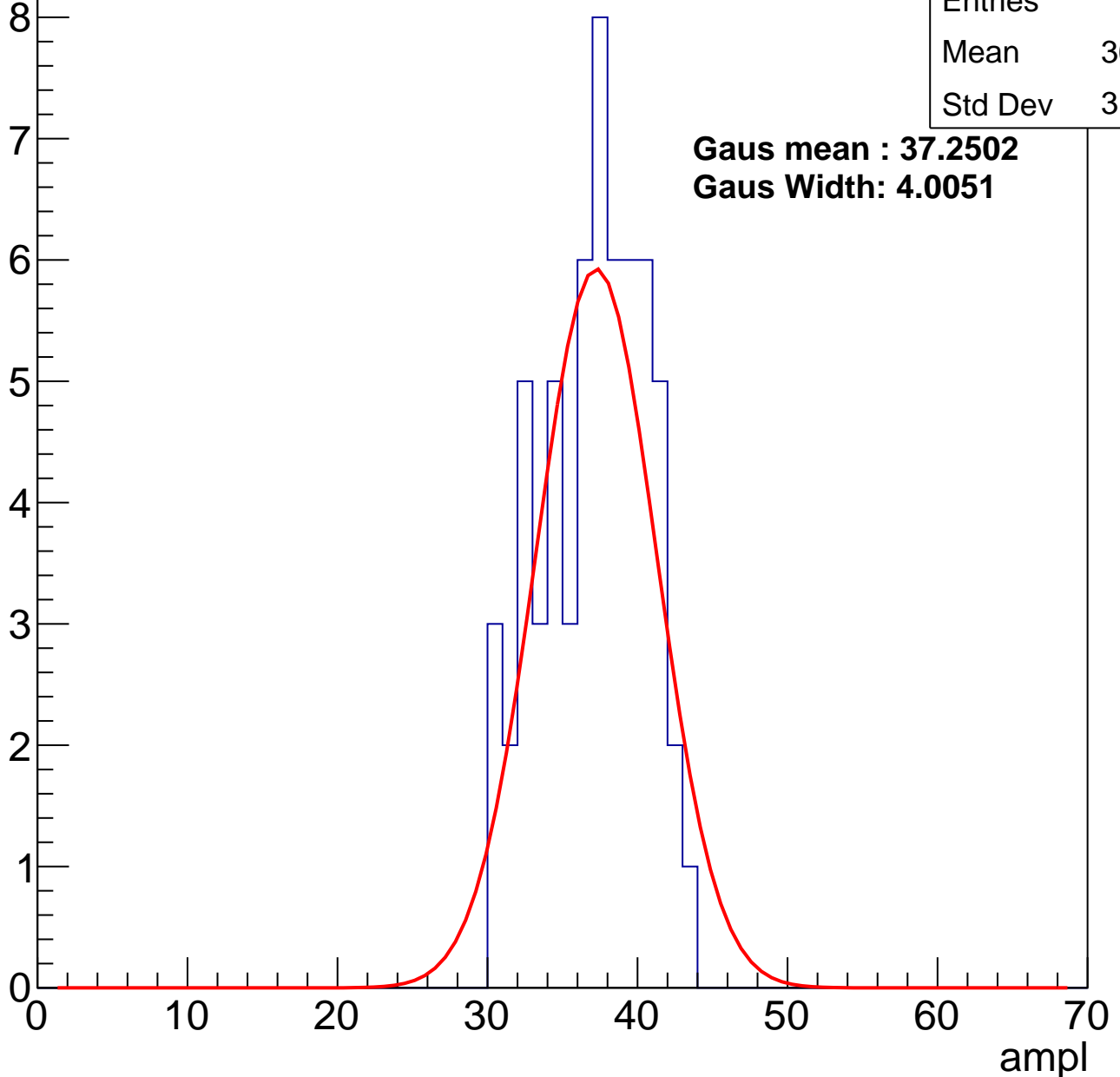
# B0L000S, U19-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.59
Std Dev	3.375

**Gaus mean : 37.2502**  
**Gaus Width: 4.0051**



# B0L000S, U19-ch93, adc2

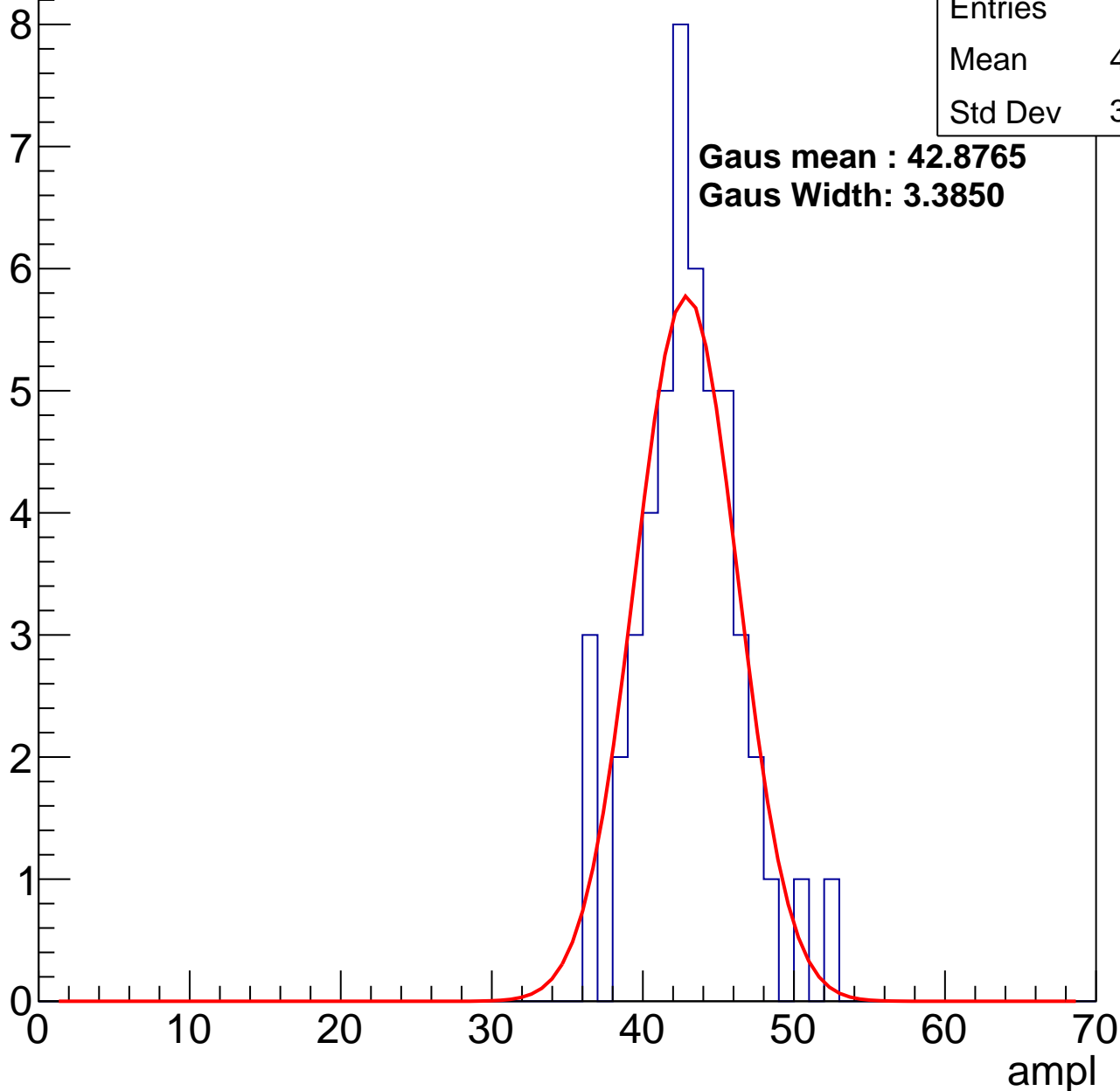
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	42.59
Std Dev	3.319

**Gaus mean : 42.8765**

**Gaus Width: 3.3850**

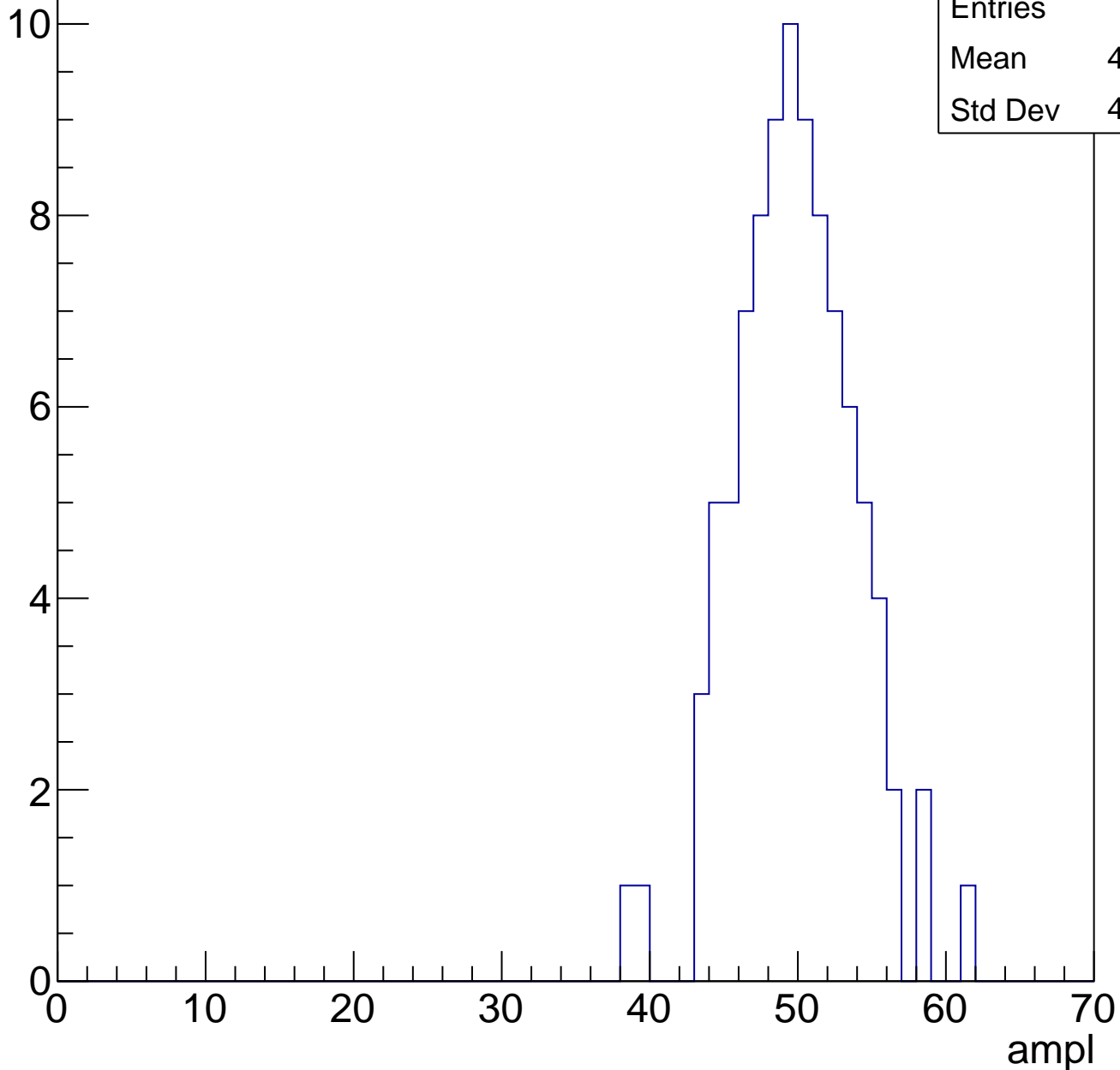


# B0L000S, U19-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	93
Mean	49.35
Std Dev	4.015

Entry

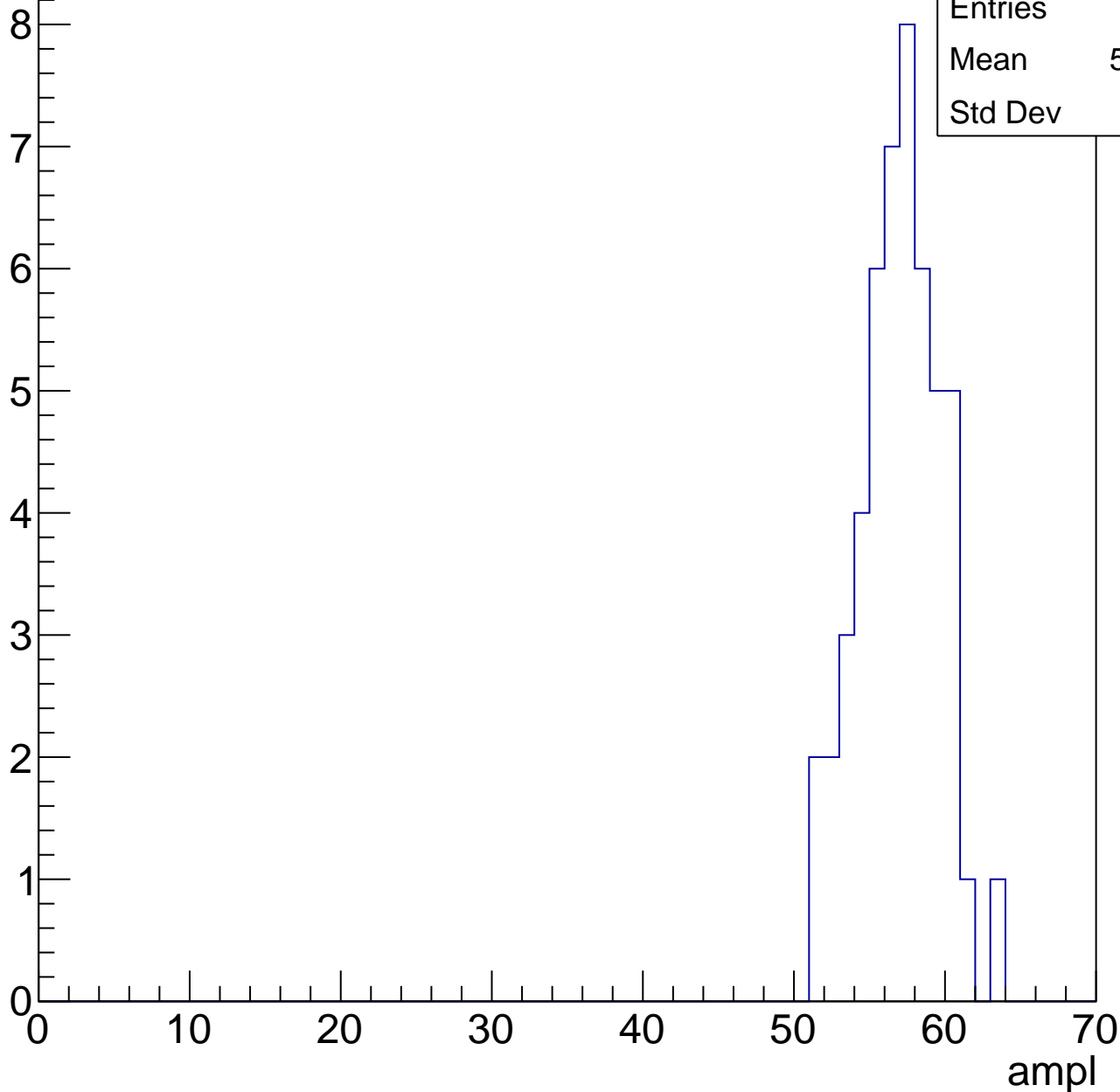


# B0L000S, U19-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.52
Std Dev	2.64

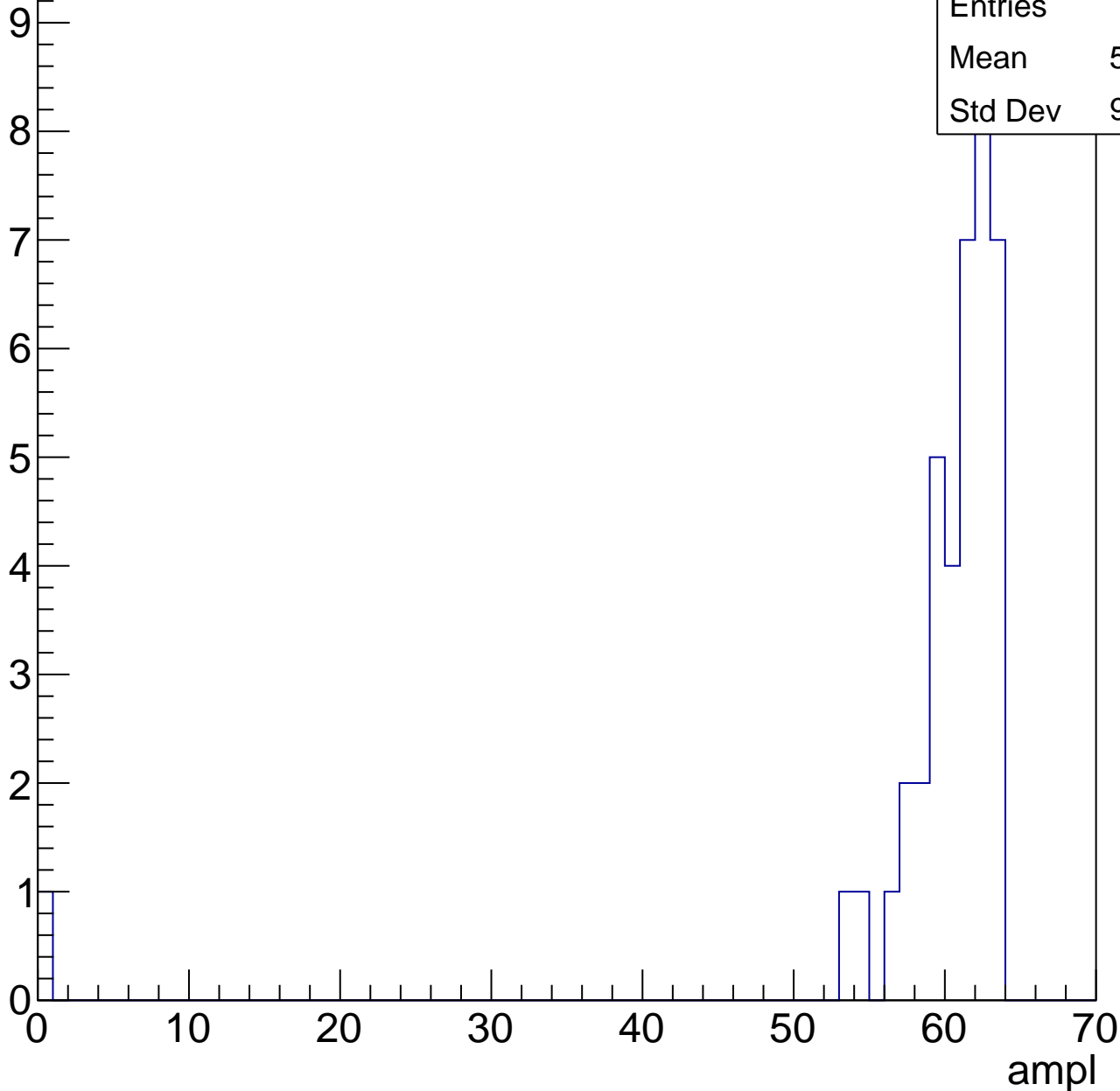


# B0L000S, U19-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.85
Std Dev	9.728



# B0L000S, U19-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch94, adc0

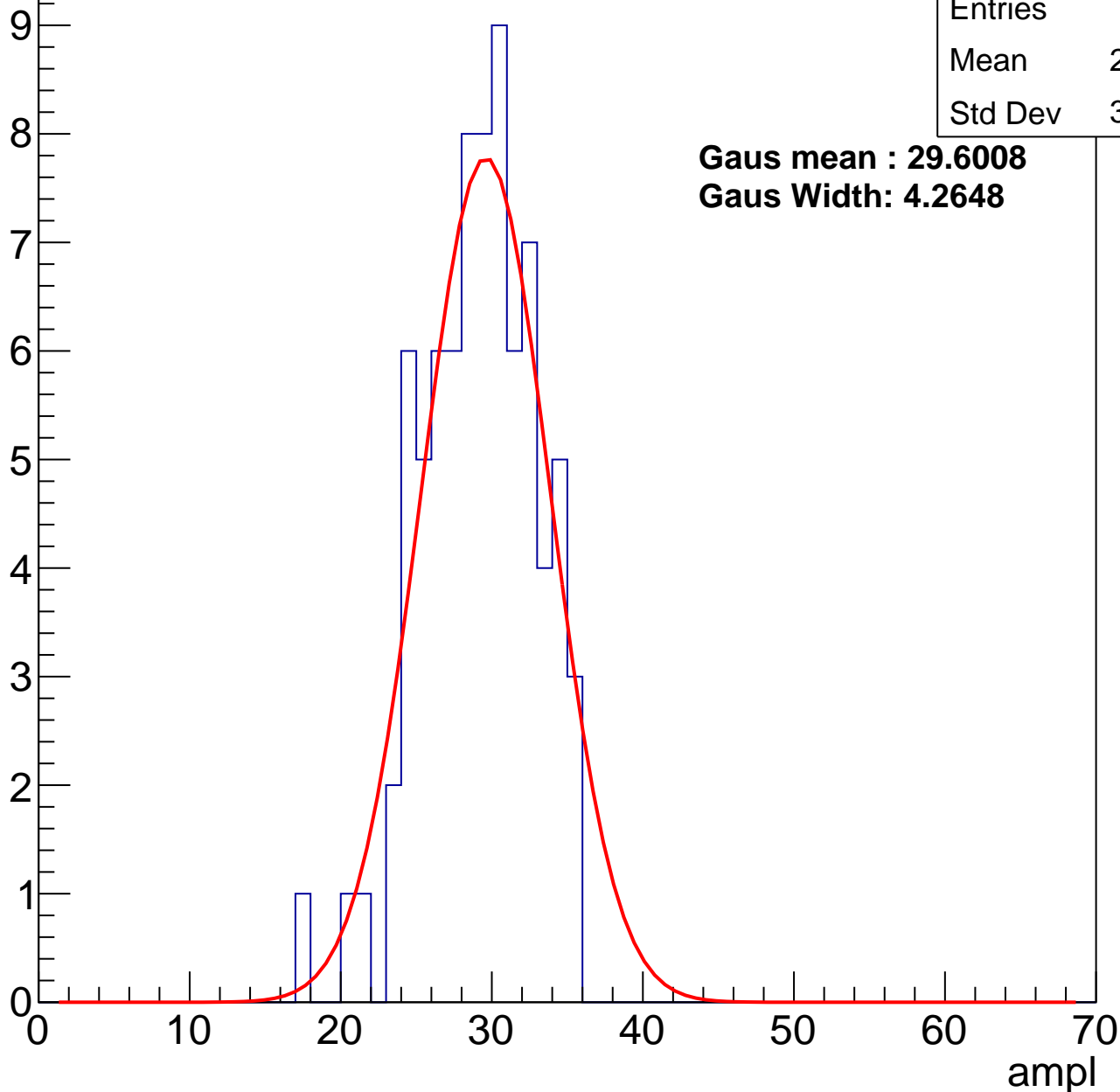
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.64
Std Dev	3.686

**Gaus mean : 29.6008**

**Gaus Width: 4.2648**



# B0L000S, U19-ch94, adc1

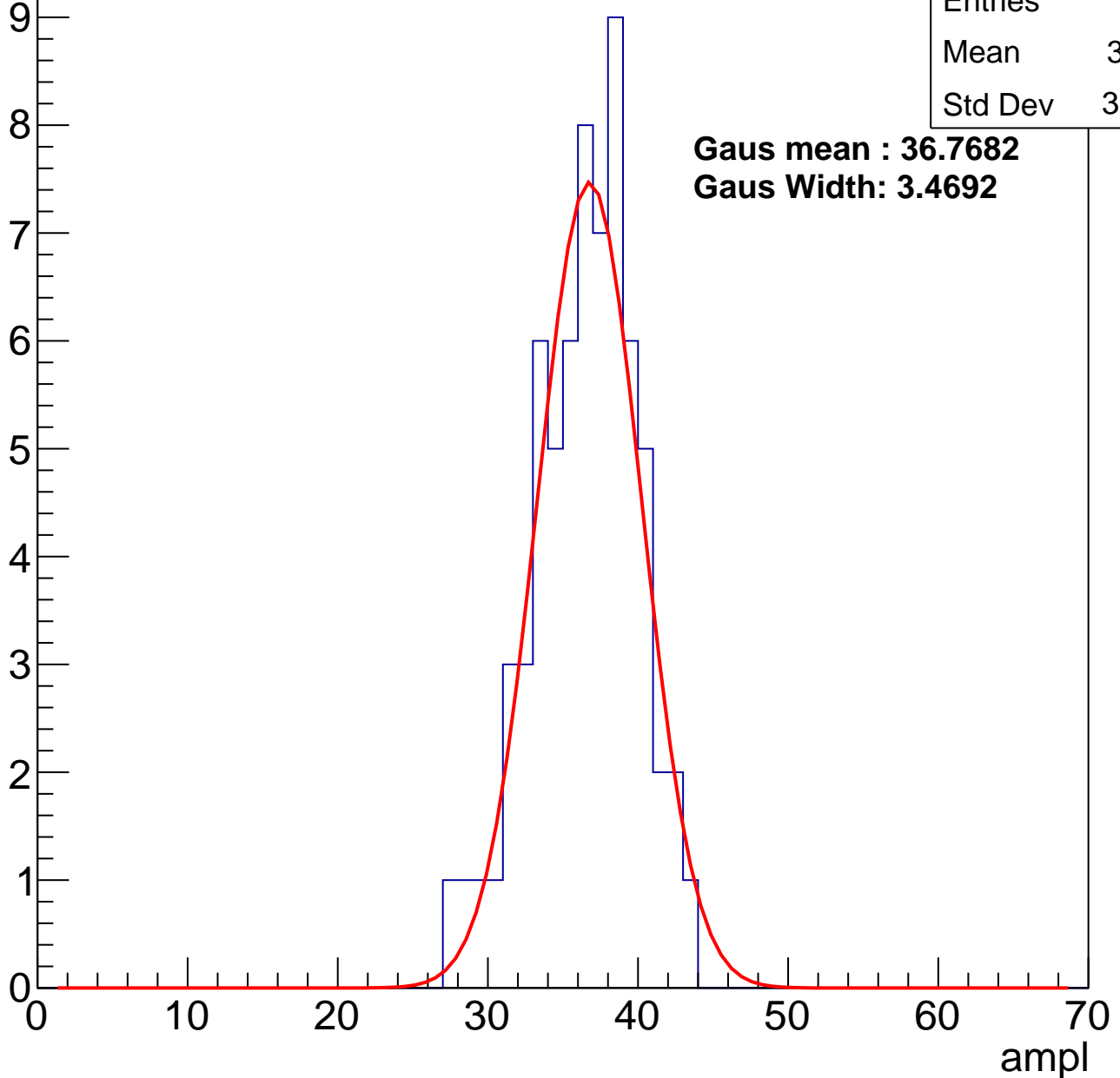
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.01
Std Dev	3.414

**Gaus mean : 36.7682**

**Gaus Width: 3.4692**



# B0L000S, U19-ch94, adc2

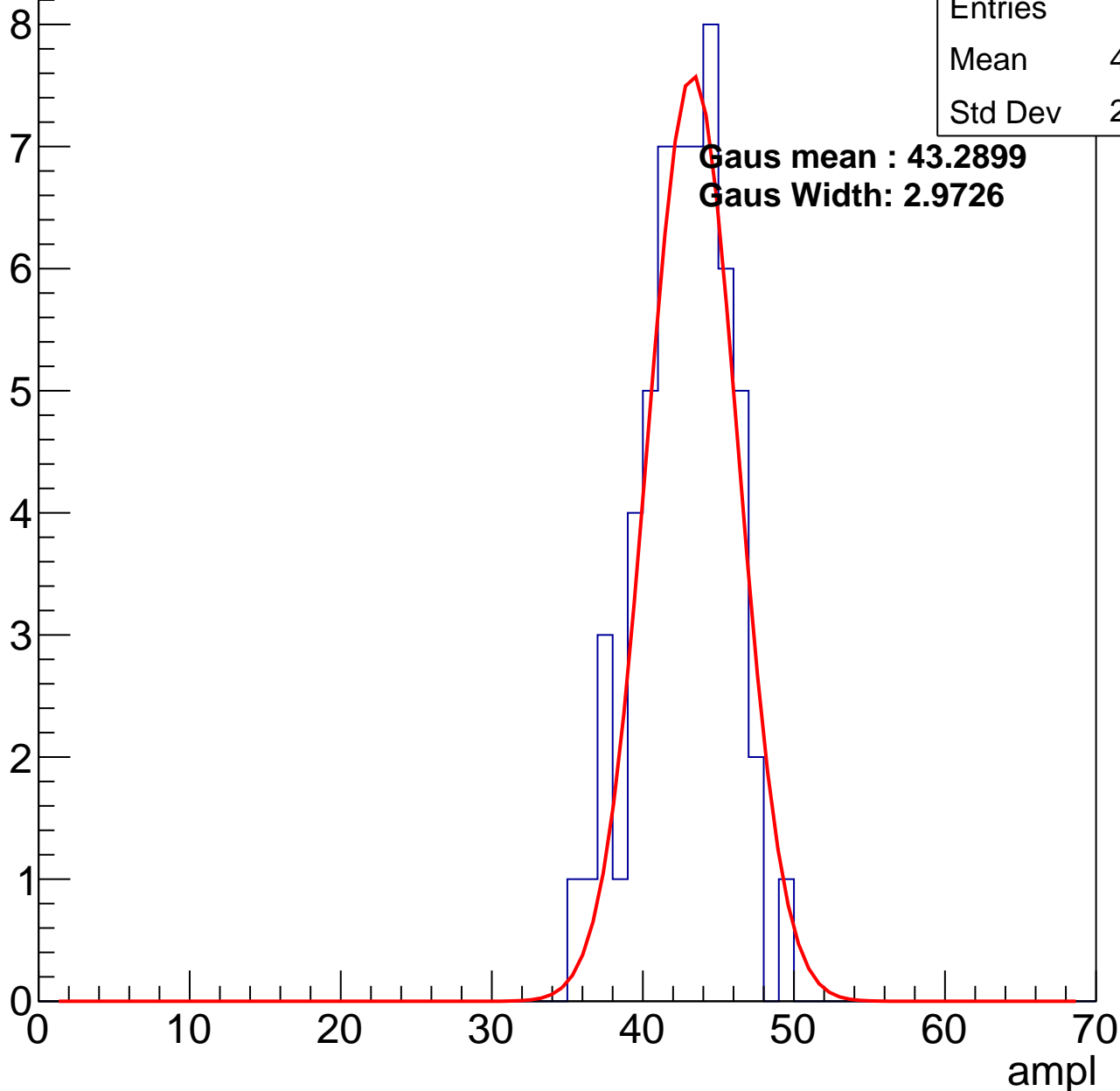
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.29
Std Dev	2.954

**Gaus mean : 43.2899**

**Gaus Width: 2.9726**

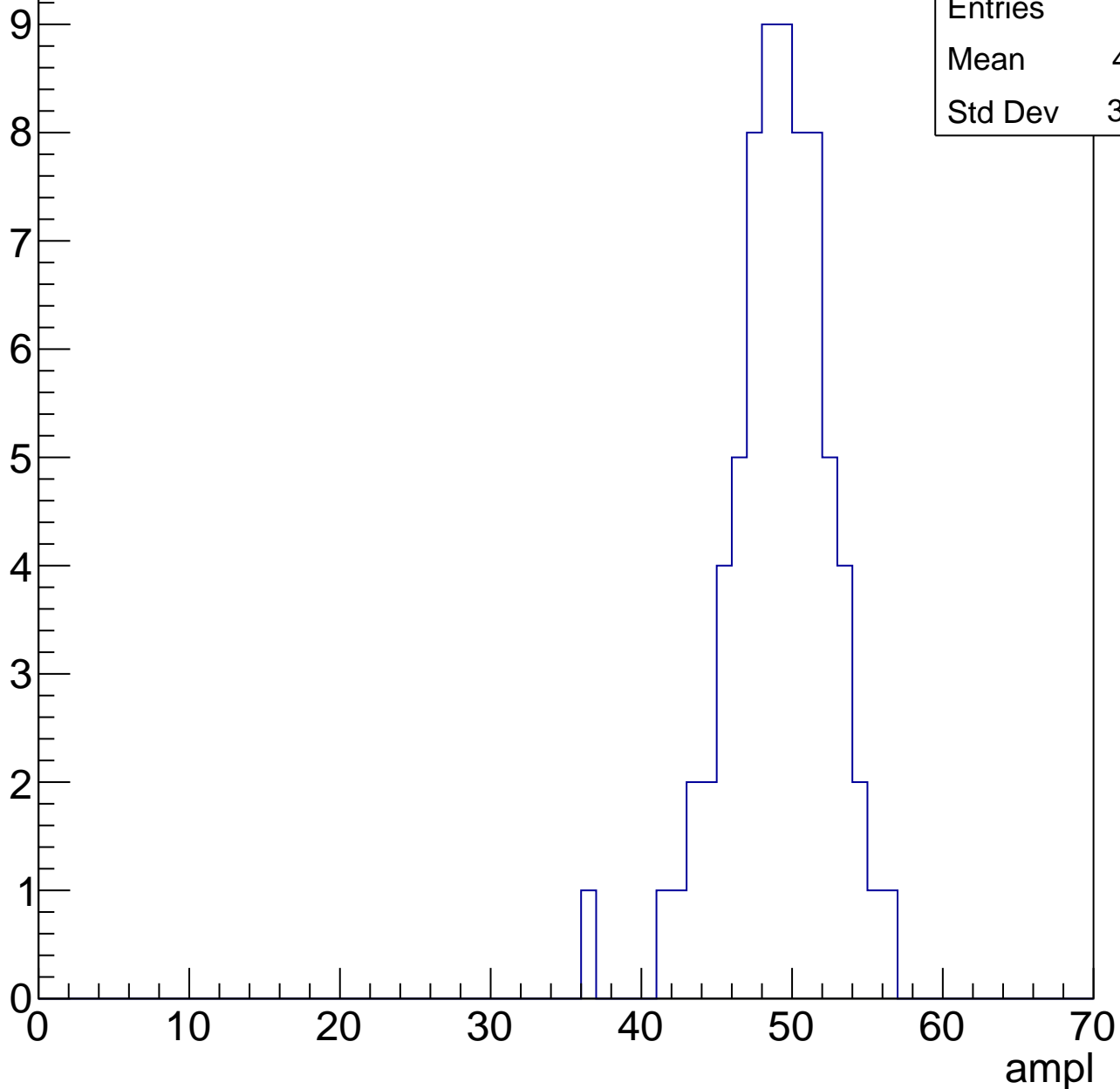


# B0L000S, U19-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	48.61
Std Dev	3.413

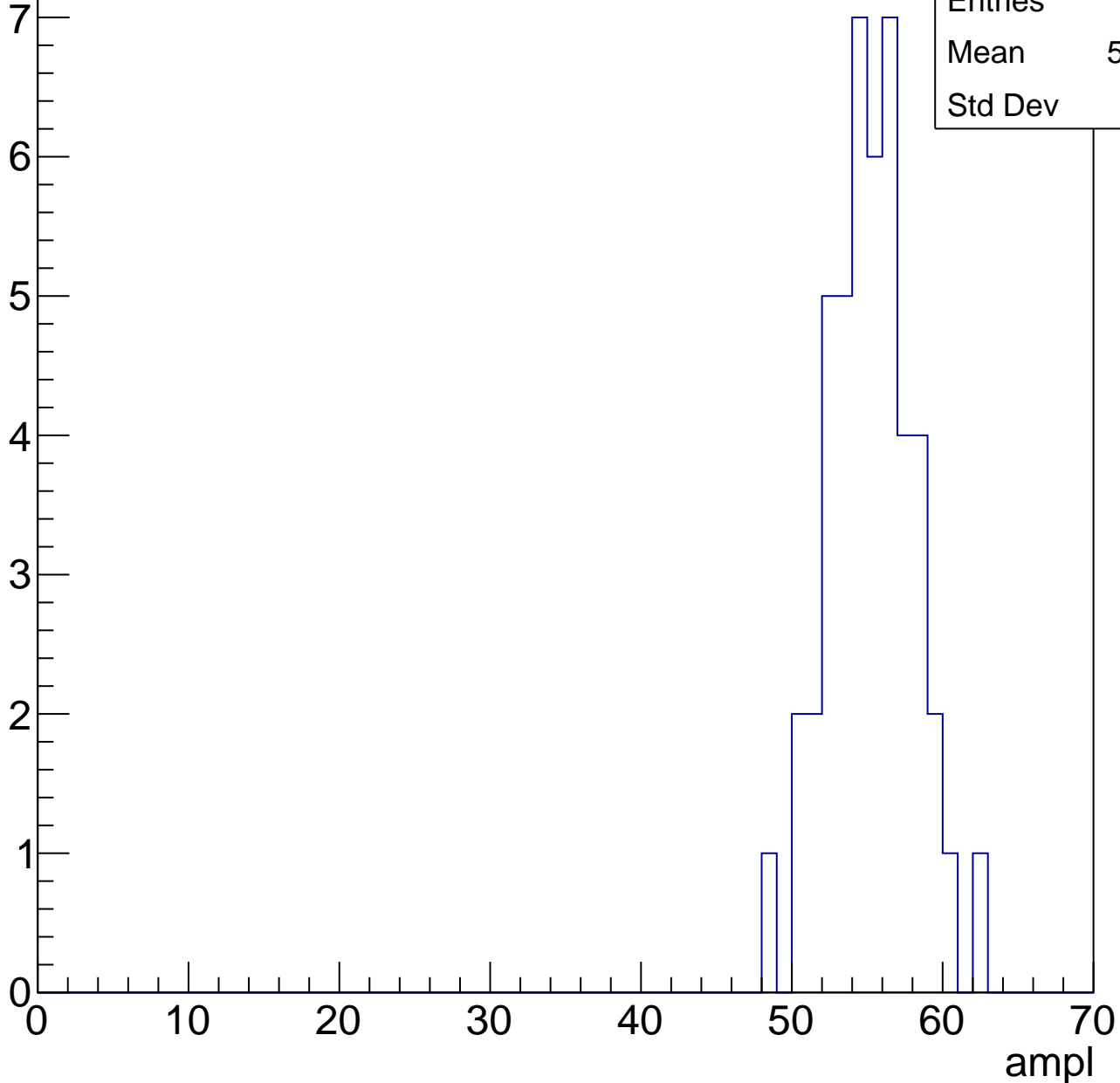


# B0L000S, U19-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	54.79
Std Dev	2.79



# B0L000S, U19-ch94, adc5

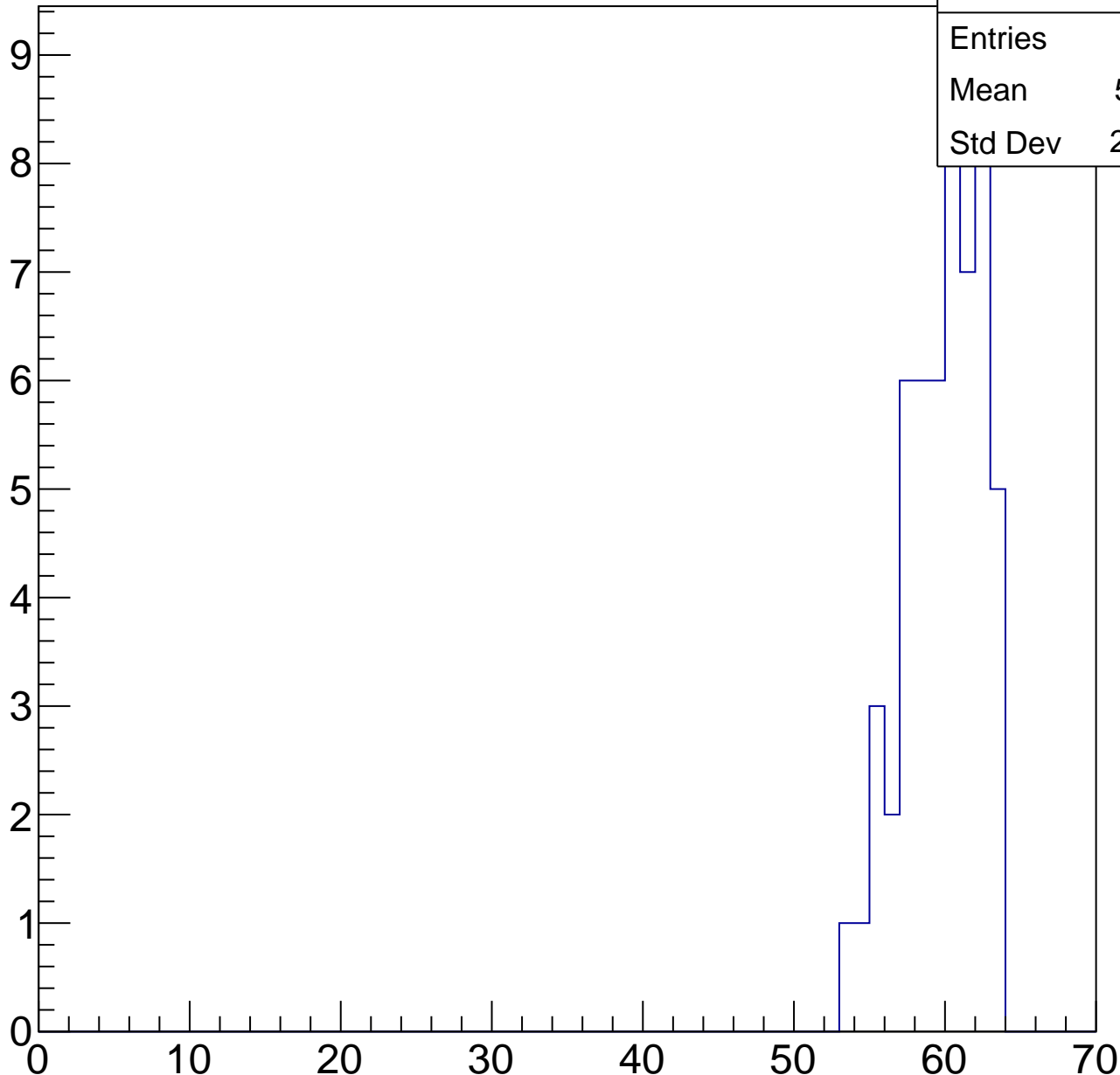
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	59.41
Std Dev	2.528

ampl

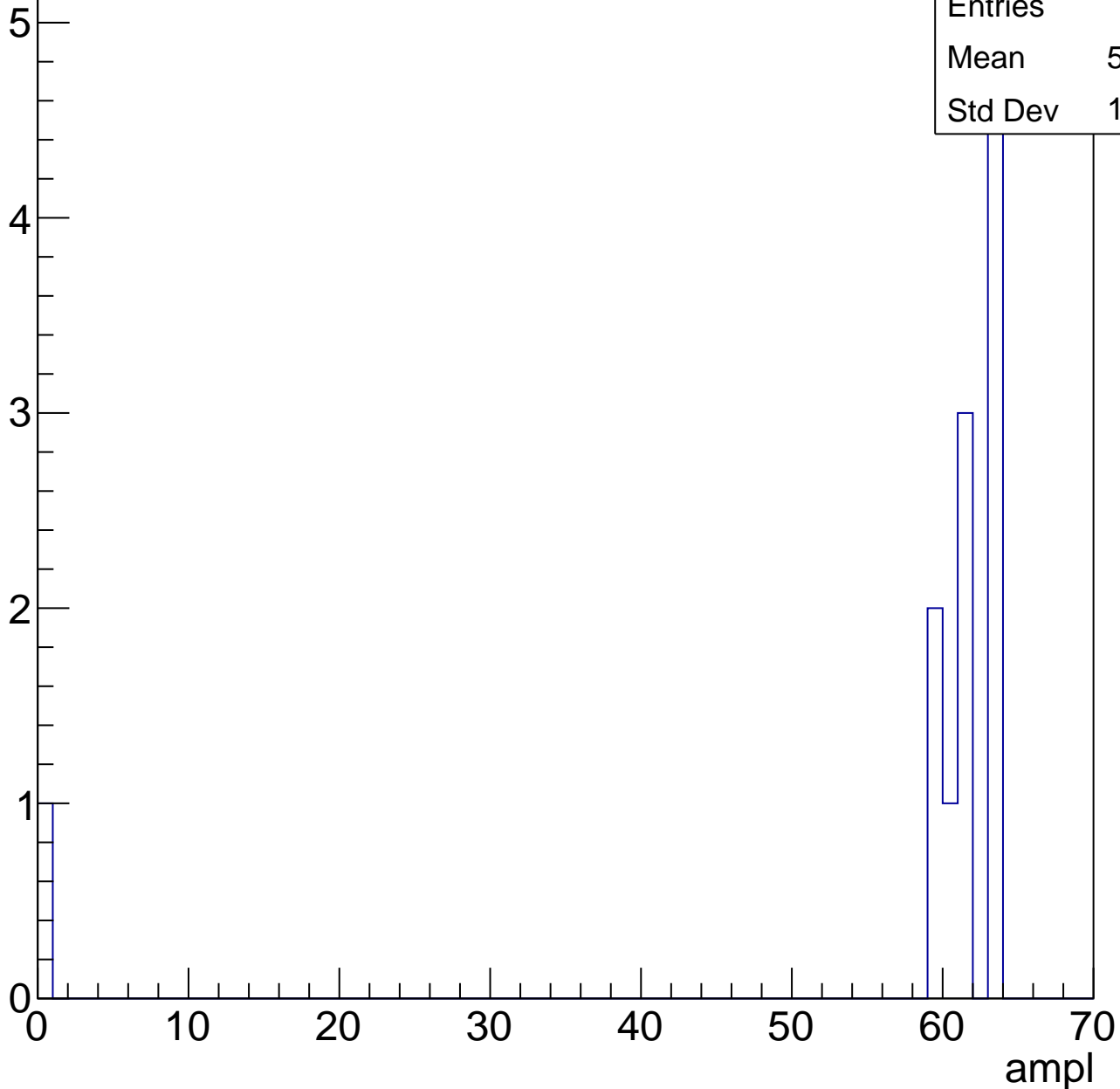


# B0L000S, U19-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	56.33
Std Dev	17.05





# B0L000S, U19-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch95, adc0

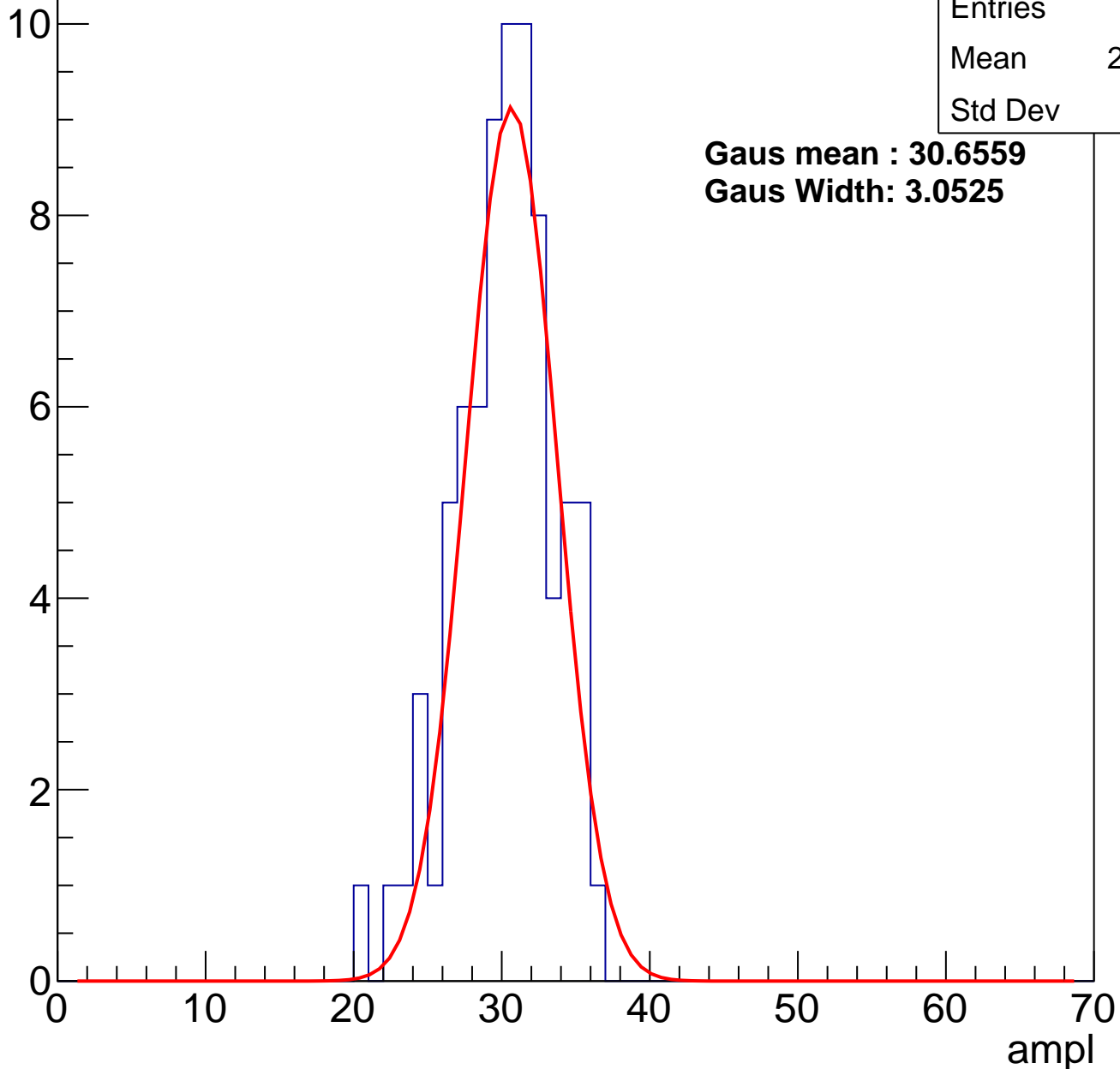
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	29.76
Std Dev	3.3

**Gaus mean : 30.6559**

**Gaus Width: 3.0525**

Entry



# B0L000S, U19-ch95, adc1

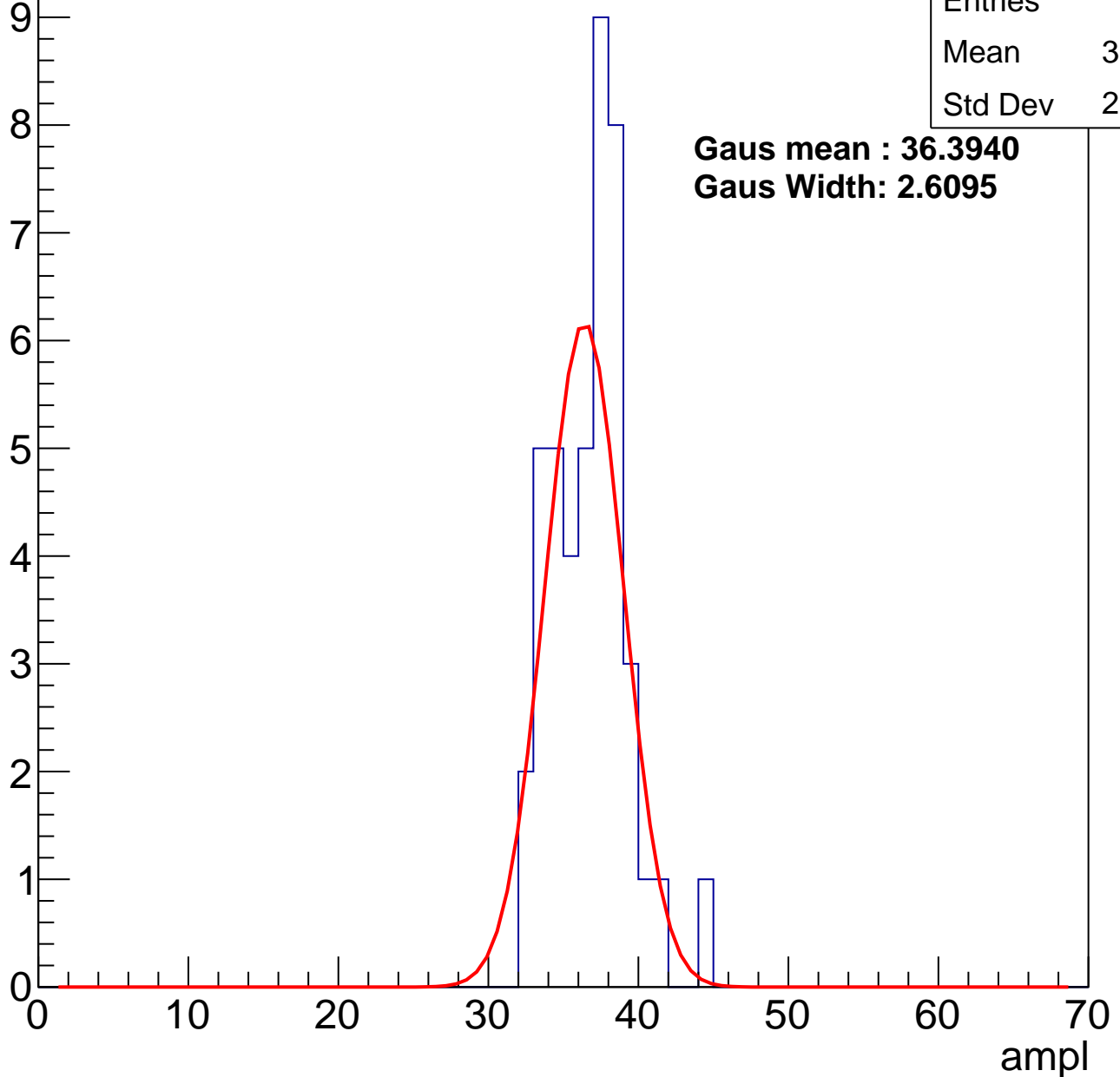
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	36.32
Std Dev	2.475

**Gaus mean : 36.3940**

**Gaus Width: 2.6095**



# B0L000S, U19-ch95, adc2

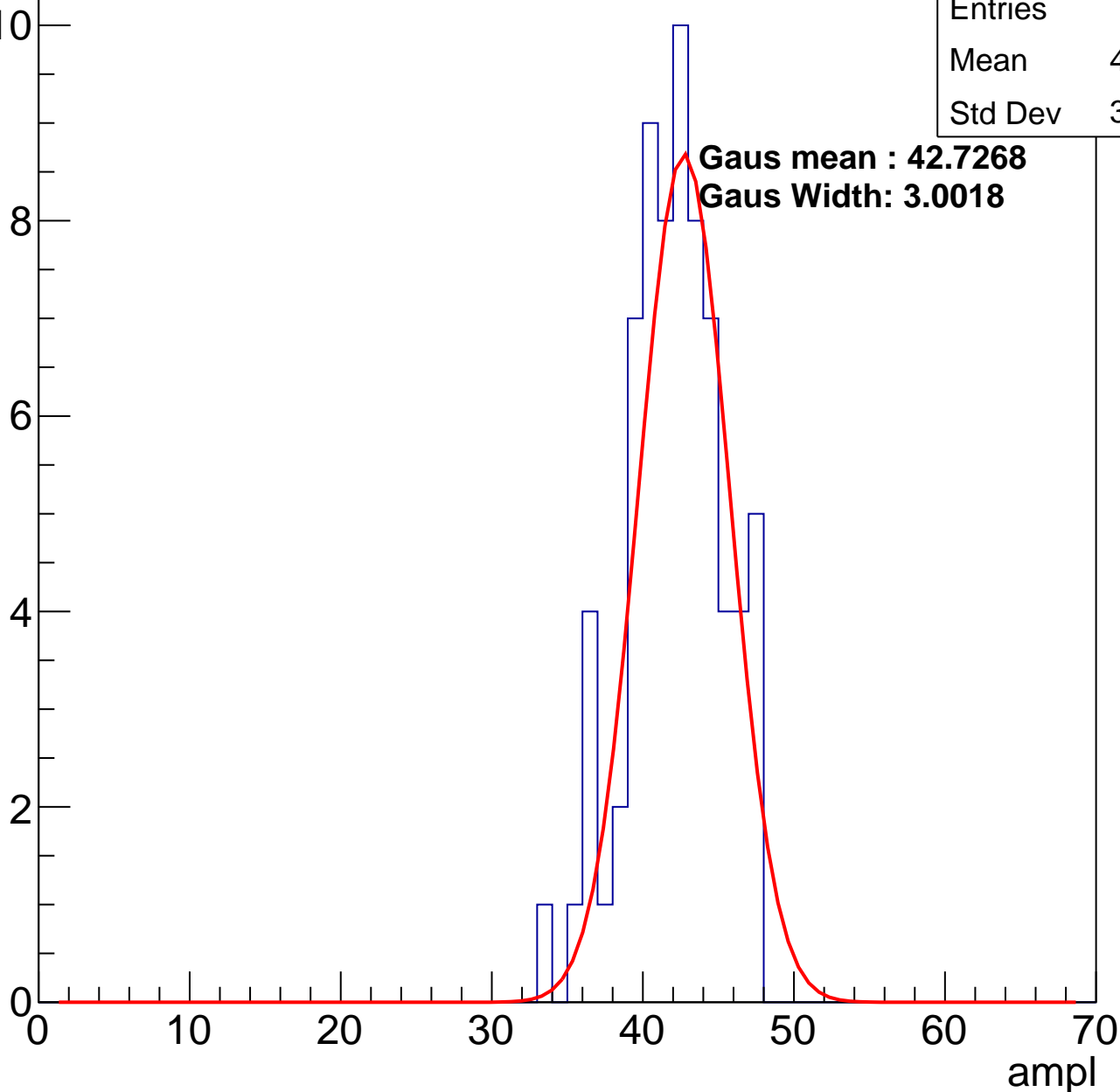
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	41.65
Std Dev	3.118

**Gaus mean : 42.7268**

**Gaus Width: 3.0018**



# B0L000S, U19-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

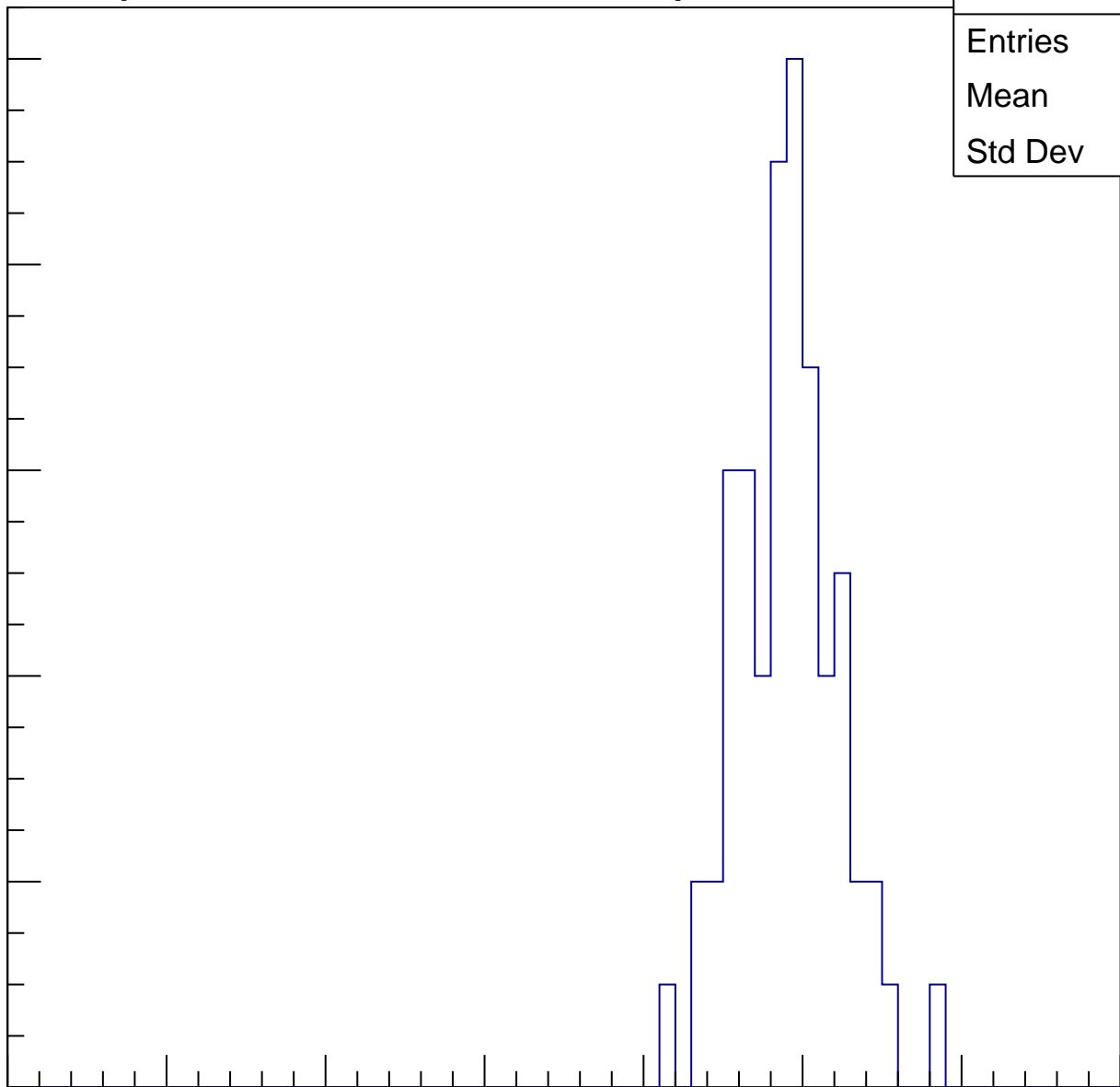
Entries	62
Mean	48.58
Std Dev	3.155

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

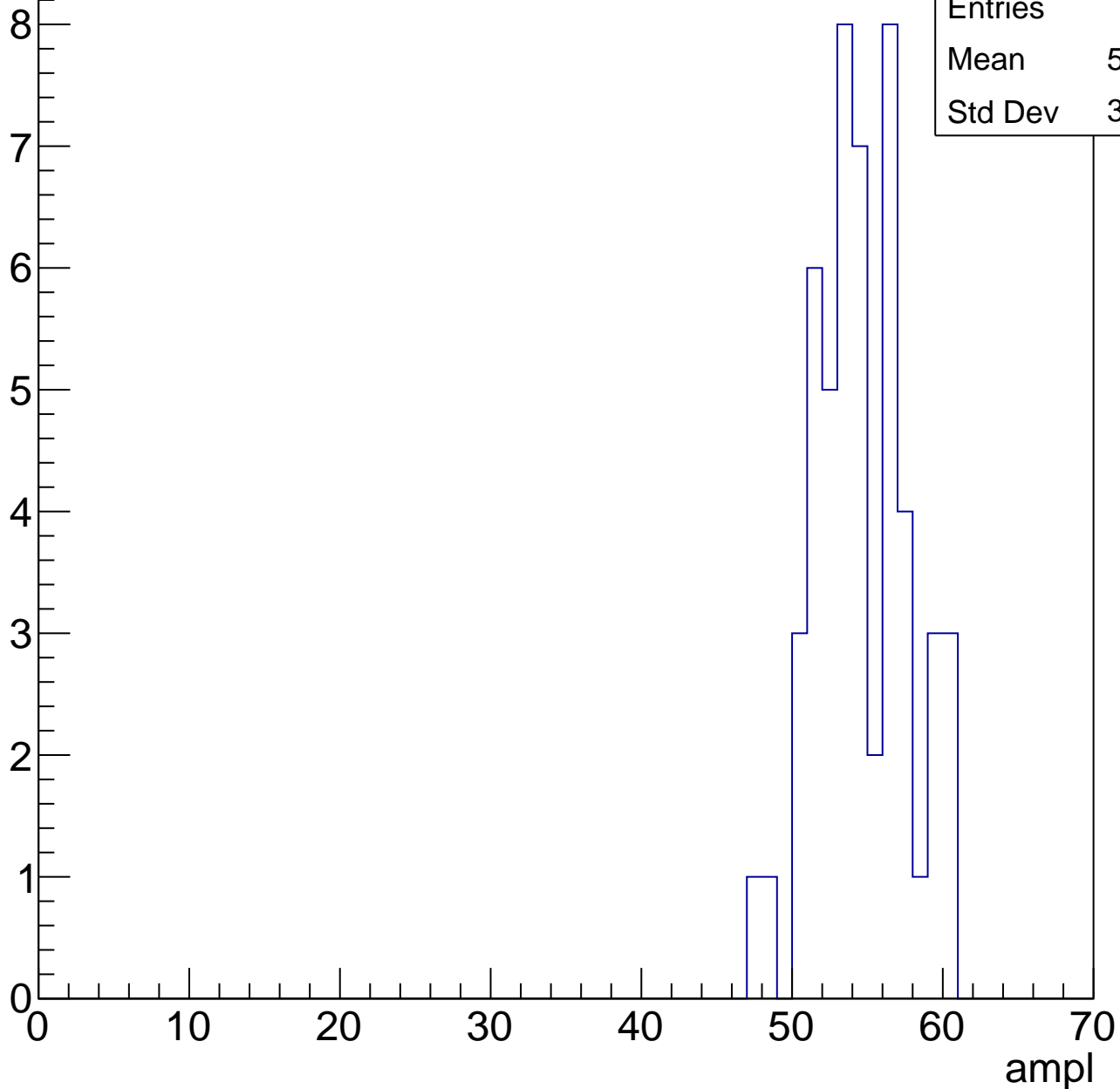


# B0L000S, U19-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	54.12
Std Dev	3.049

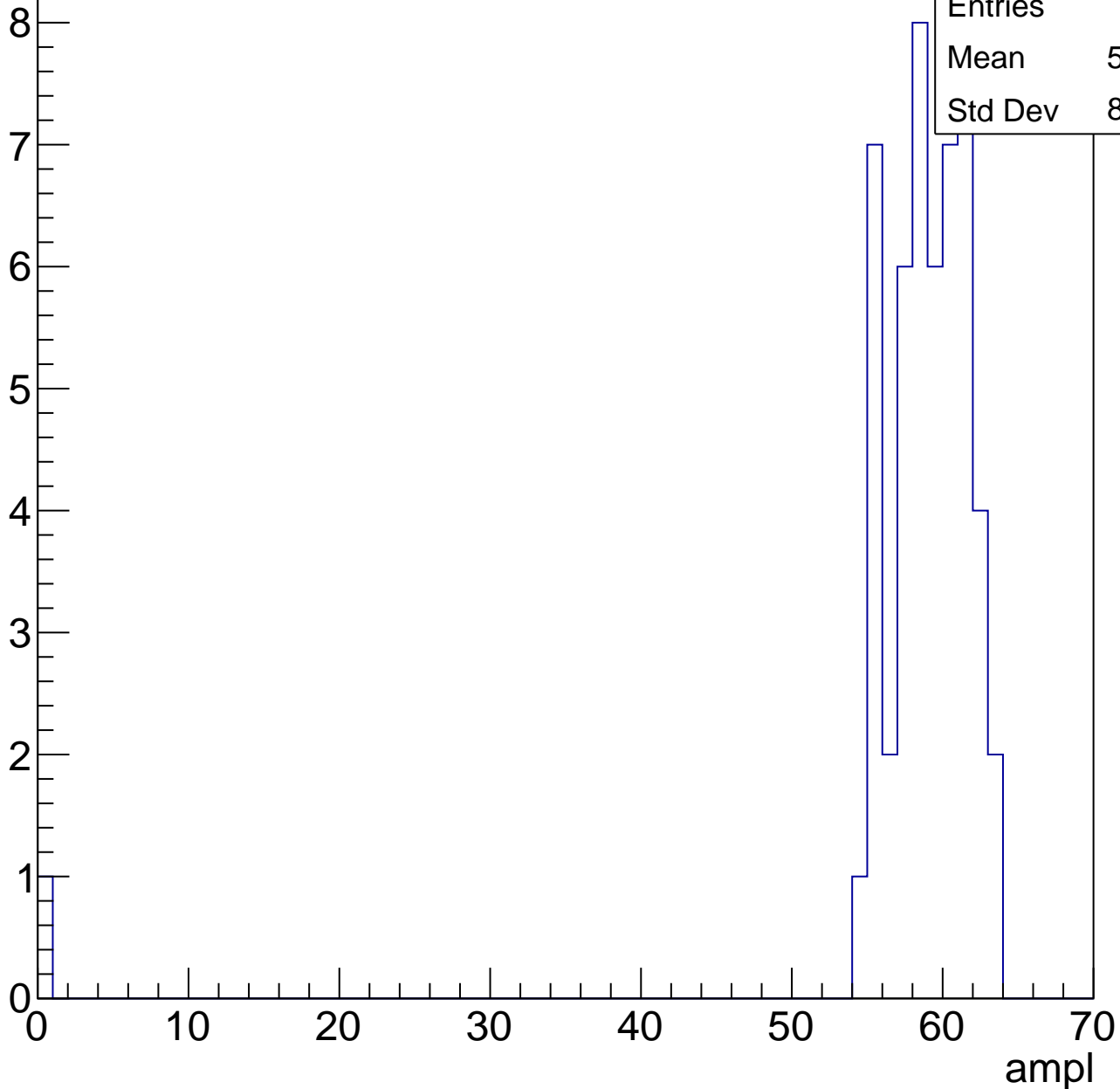


# B0L000S, U19-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	57.56
Std Dev	8.397

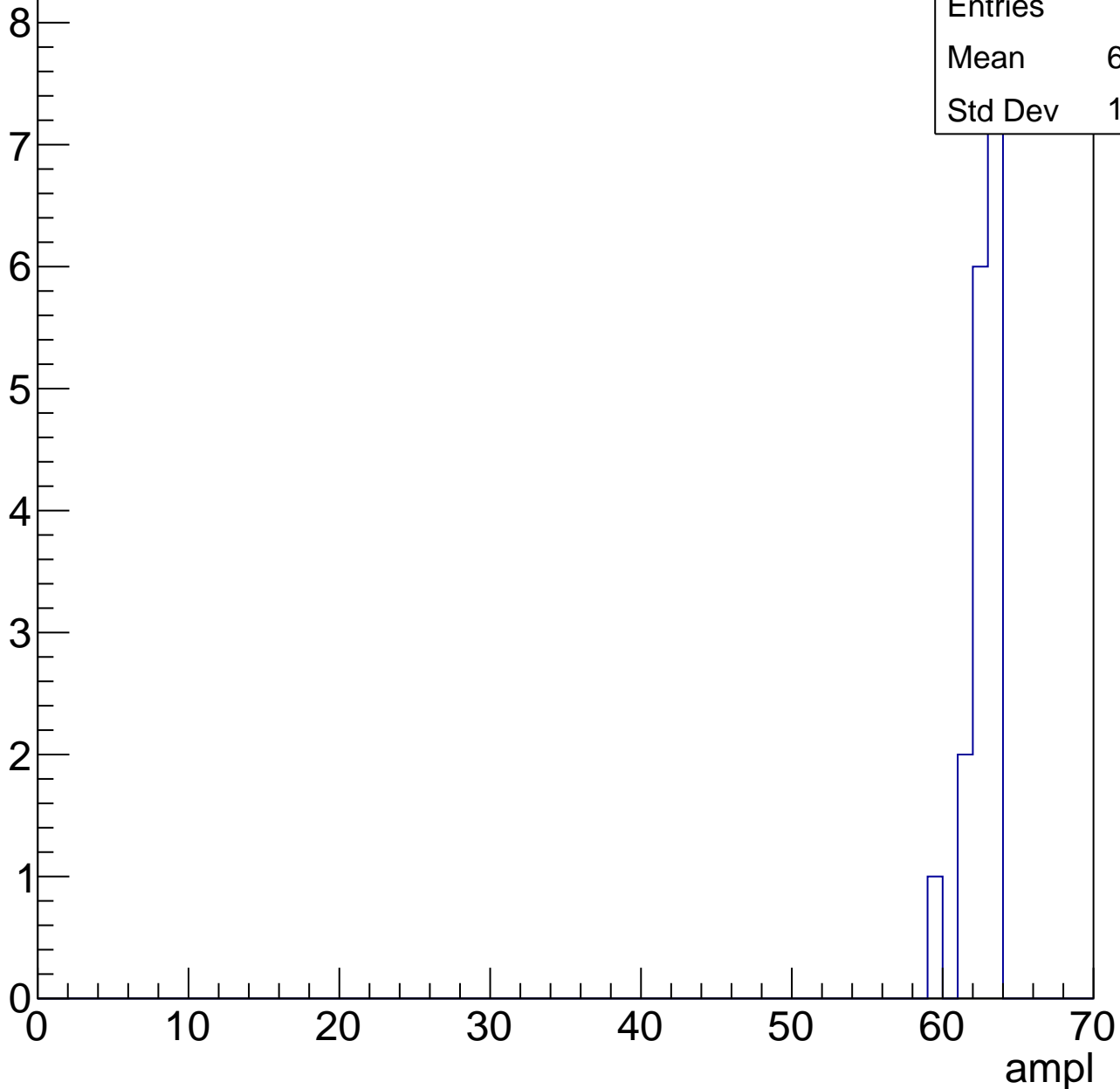


# B0L000S, U19-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	62.18
Std Dev	1.042





# B0L000S, U19-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch96, adc0

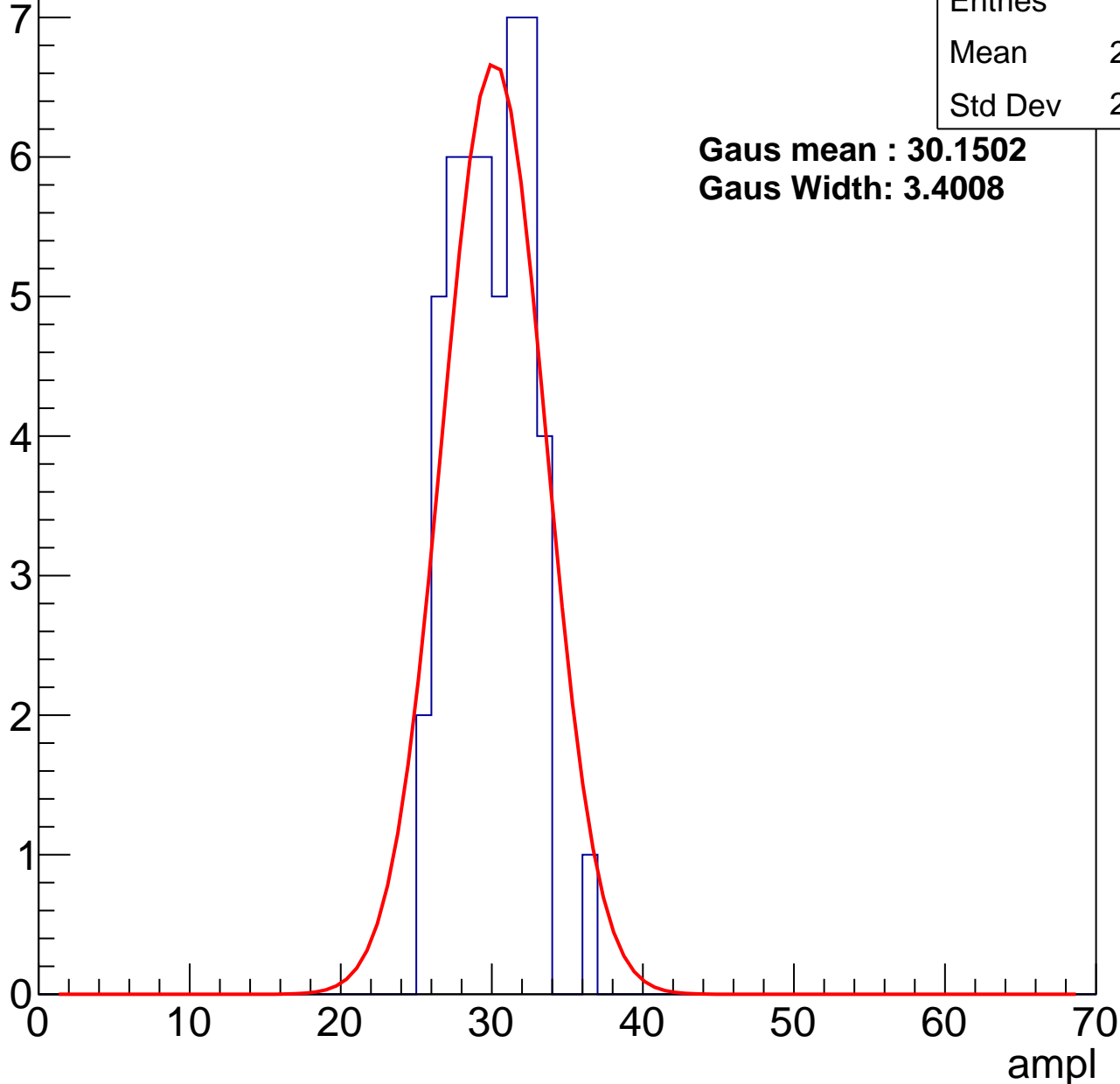
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	29.45
Std Dev	2.499

**Gaus mean : 30.1502**

**Gaus Width: 3.4008**



# B0L000S, U19-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	35.82
Std Dev	3.58

**Gaus mean : 36.6614**

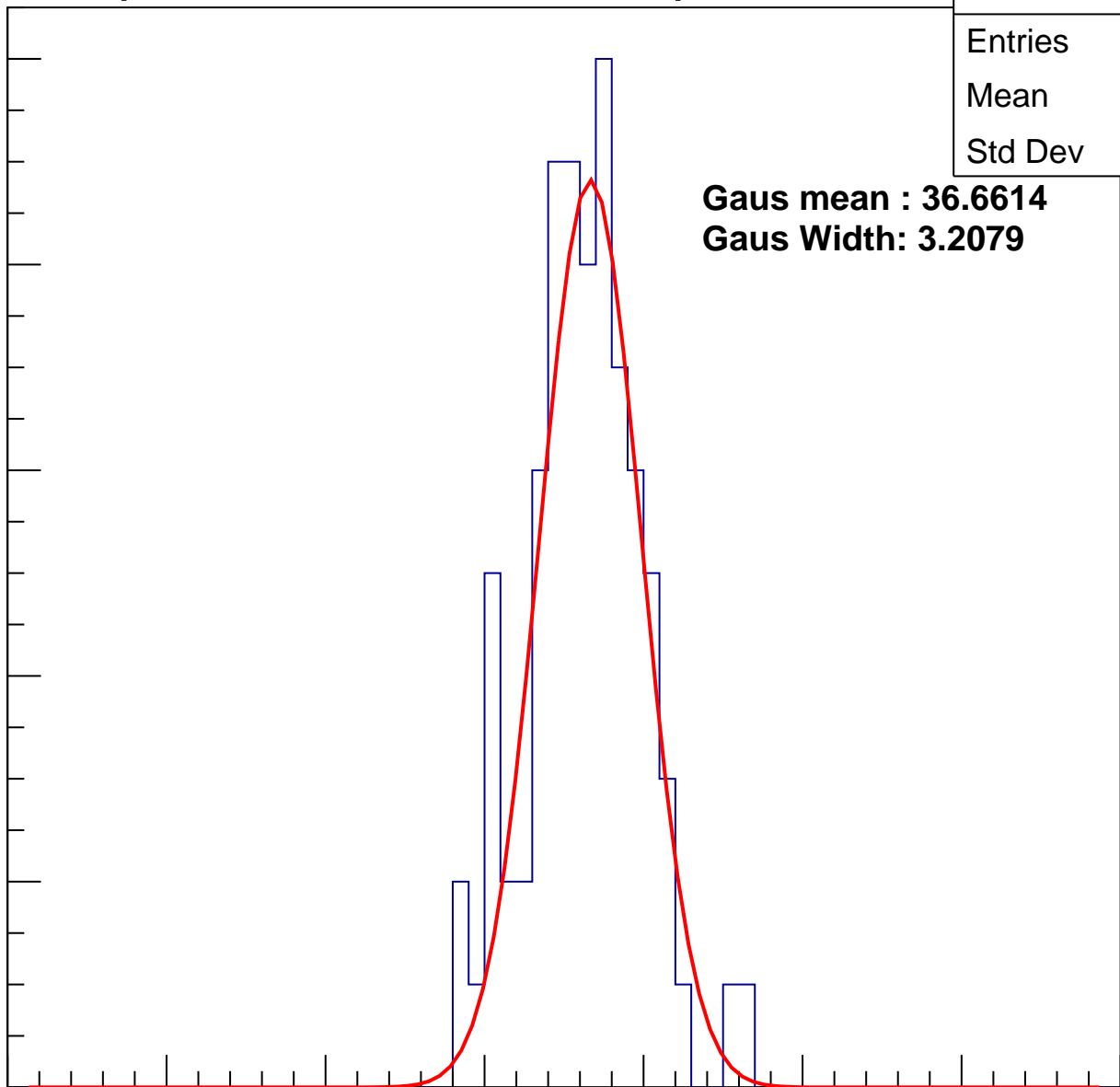
**Gaus Width: 3.2079**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch96, adc2

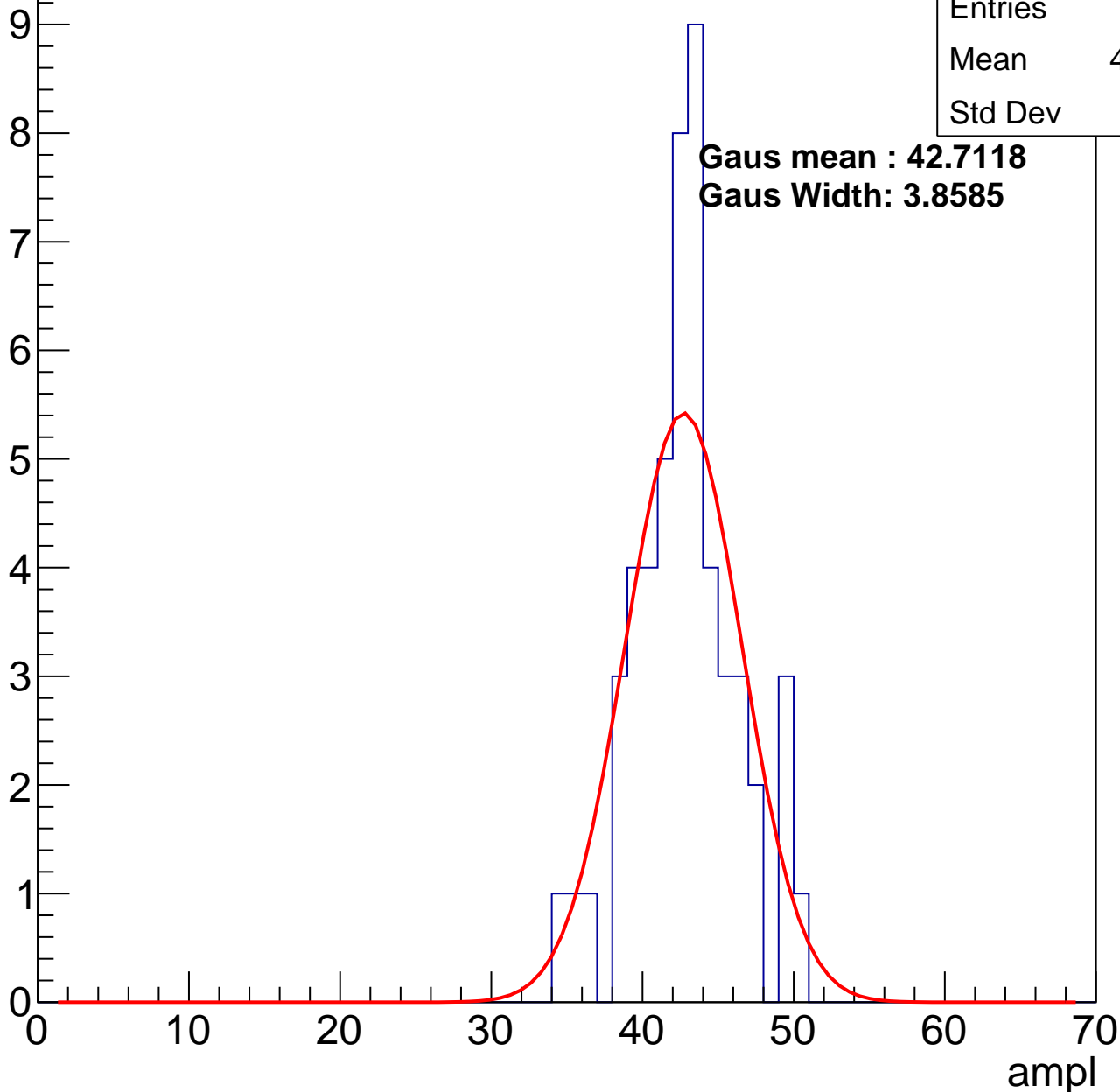
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	42.37
Std Dev	3.42

**Gaus mean : 42.7118**

**Gaus Width: 3.8585**

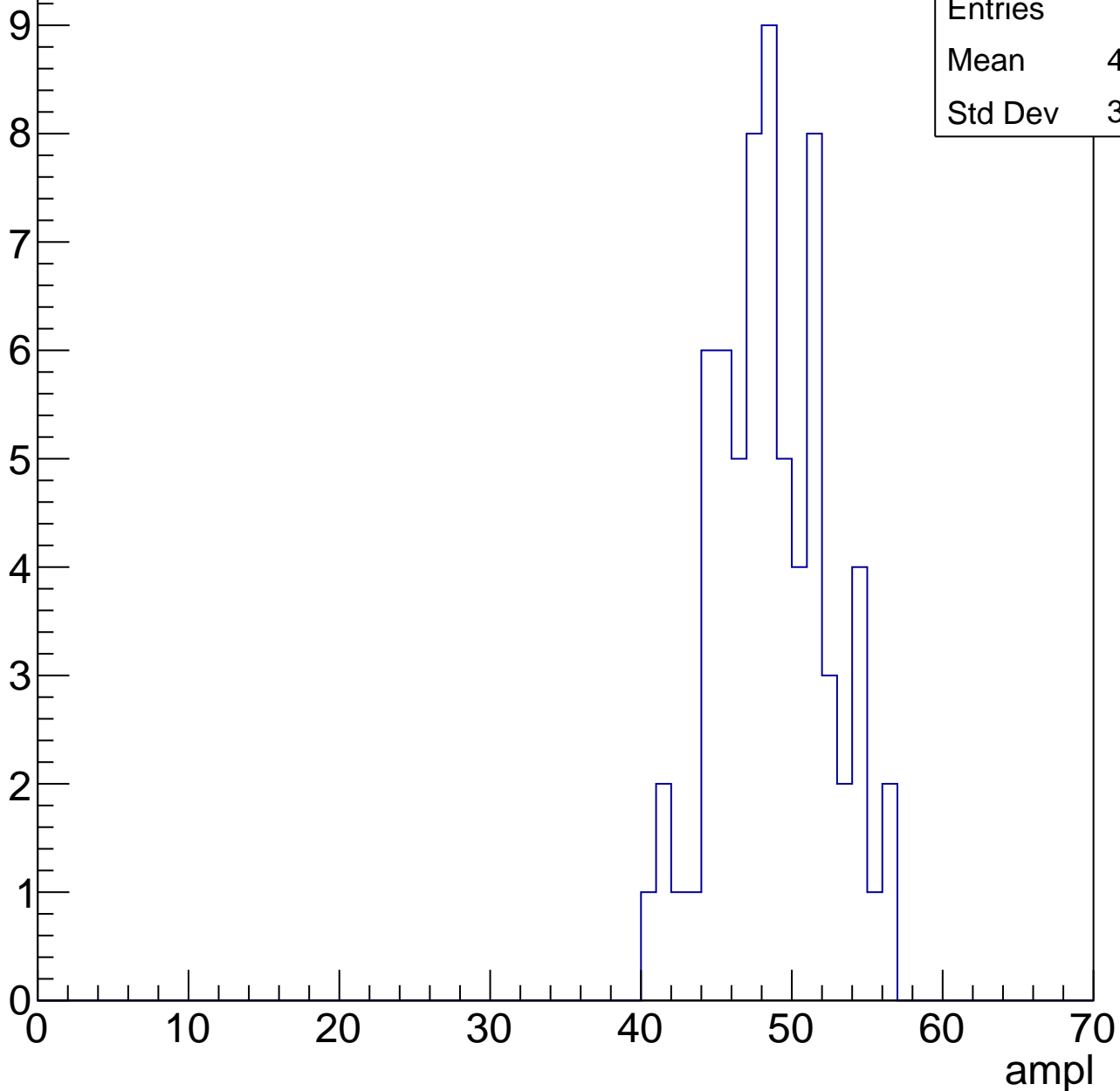


# B0L000S, U19-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

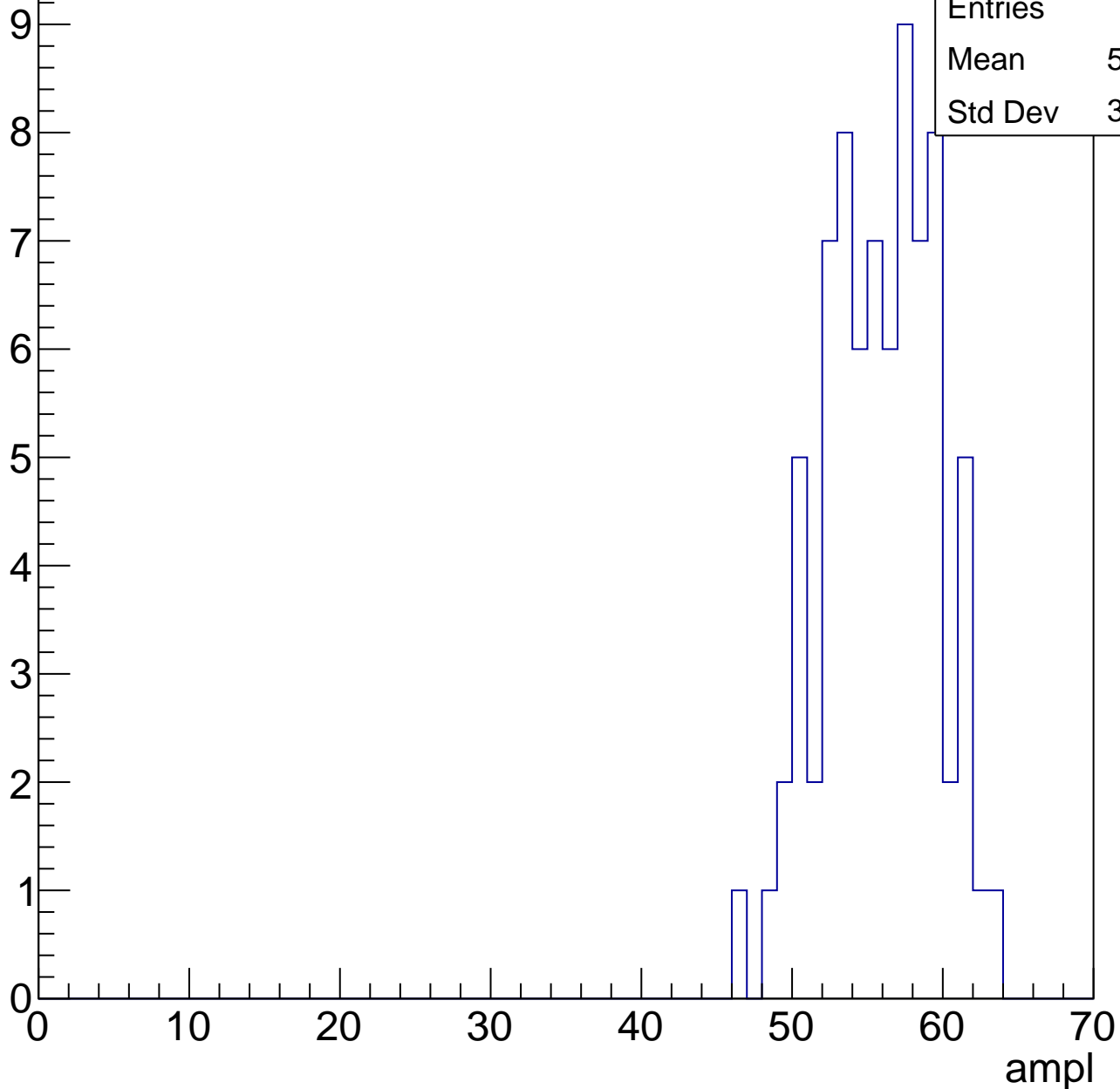
Entries	68
Mean	48.19
Std Dev	3.659



# B0L000S, U19-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

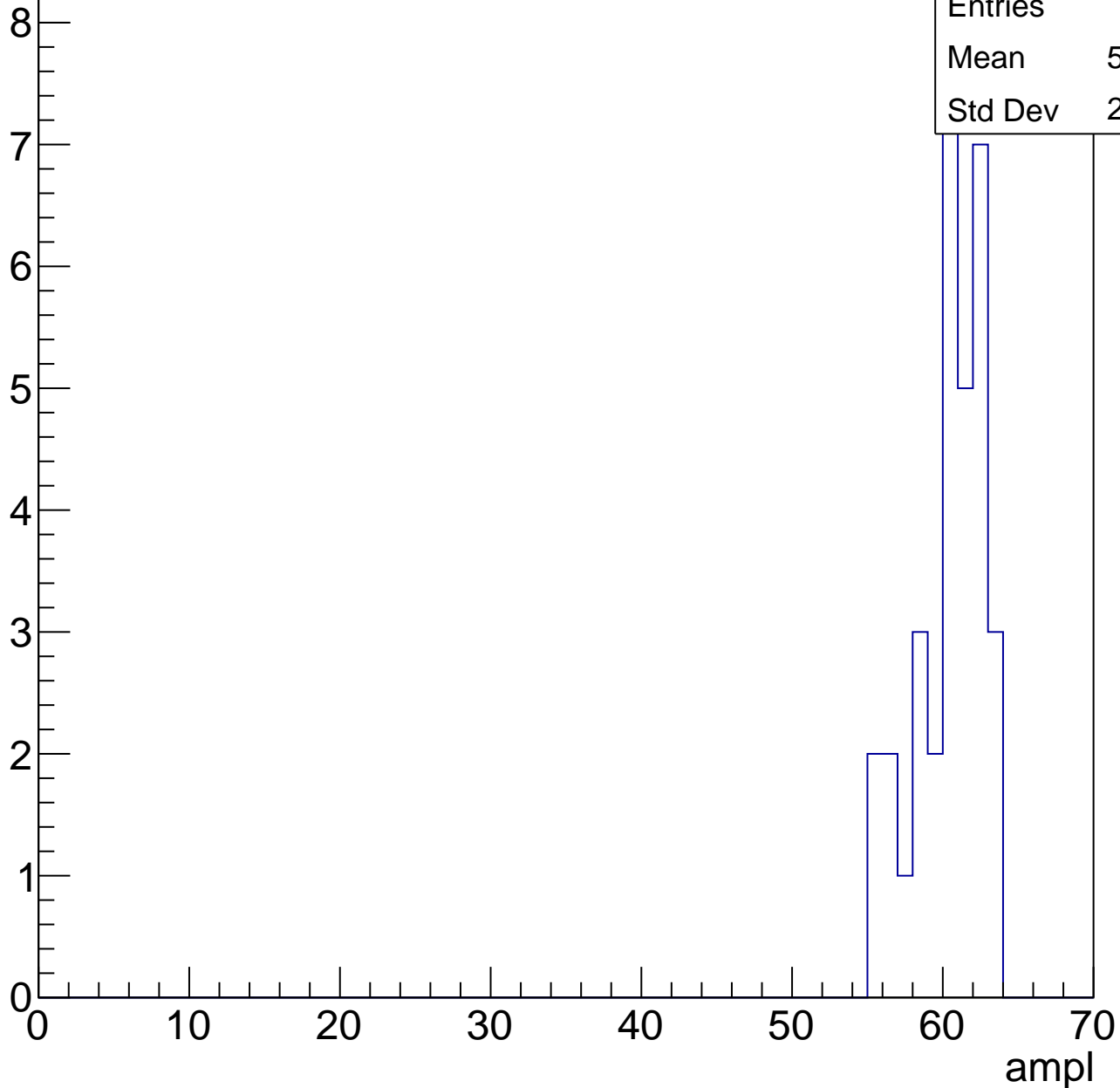


Entries	78
Mean	55.36
Std Dev	3.627

# B0L000S, U19-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

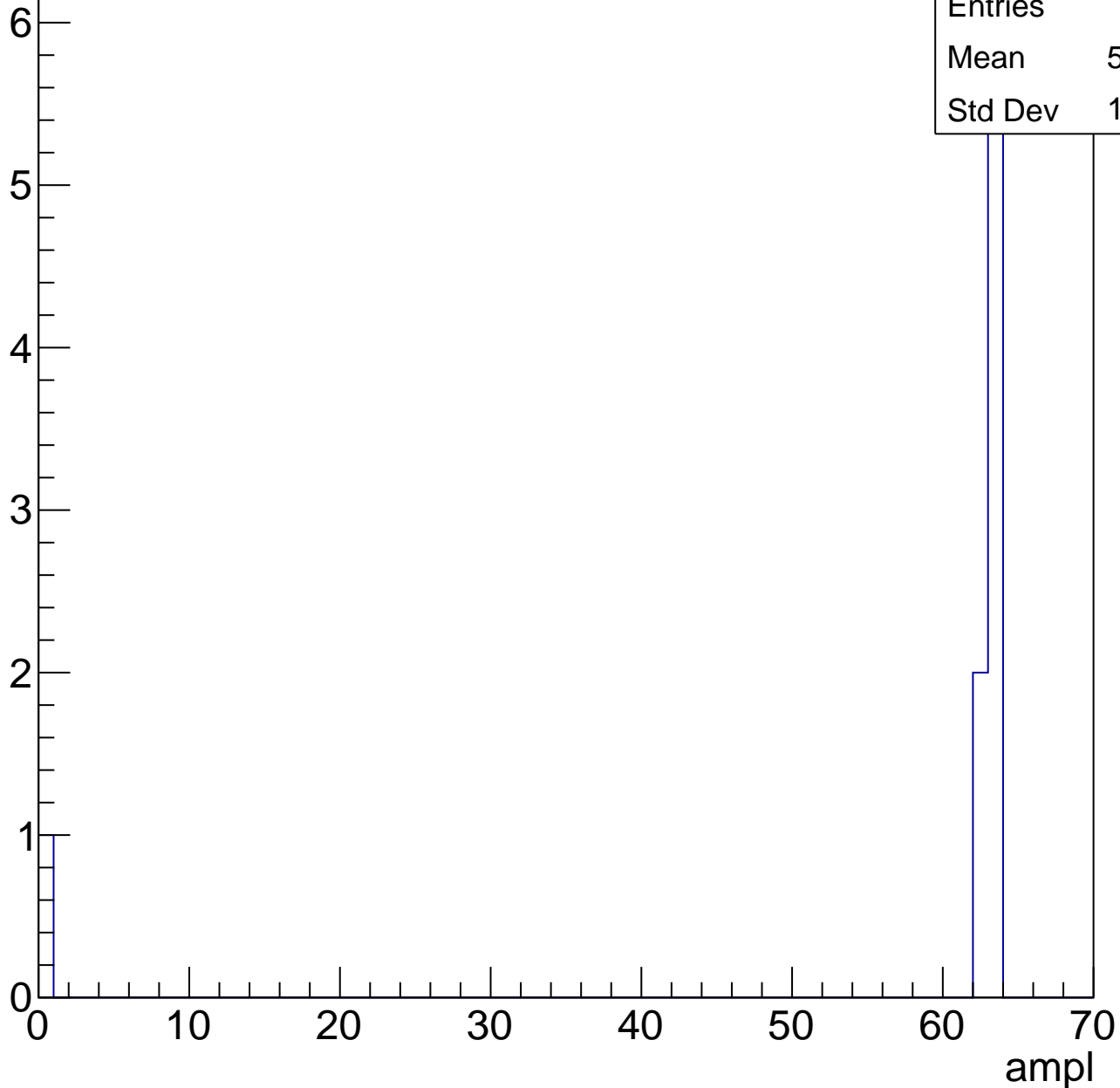
Entry



# B0L000S, U19-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



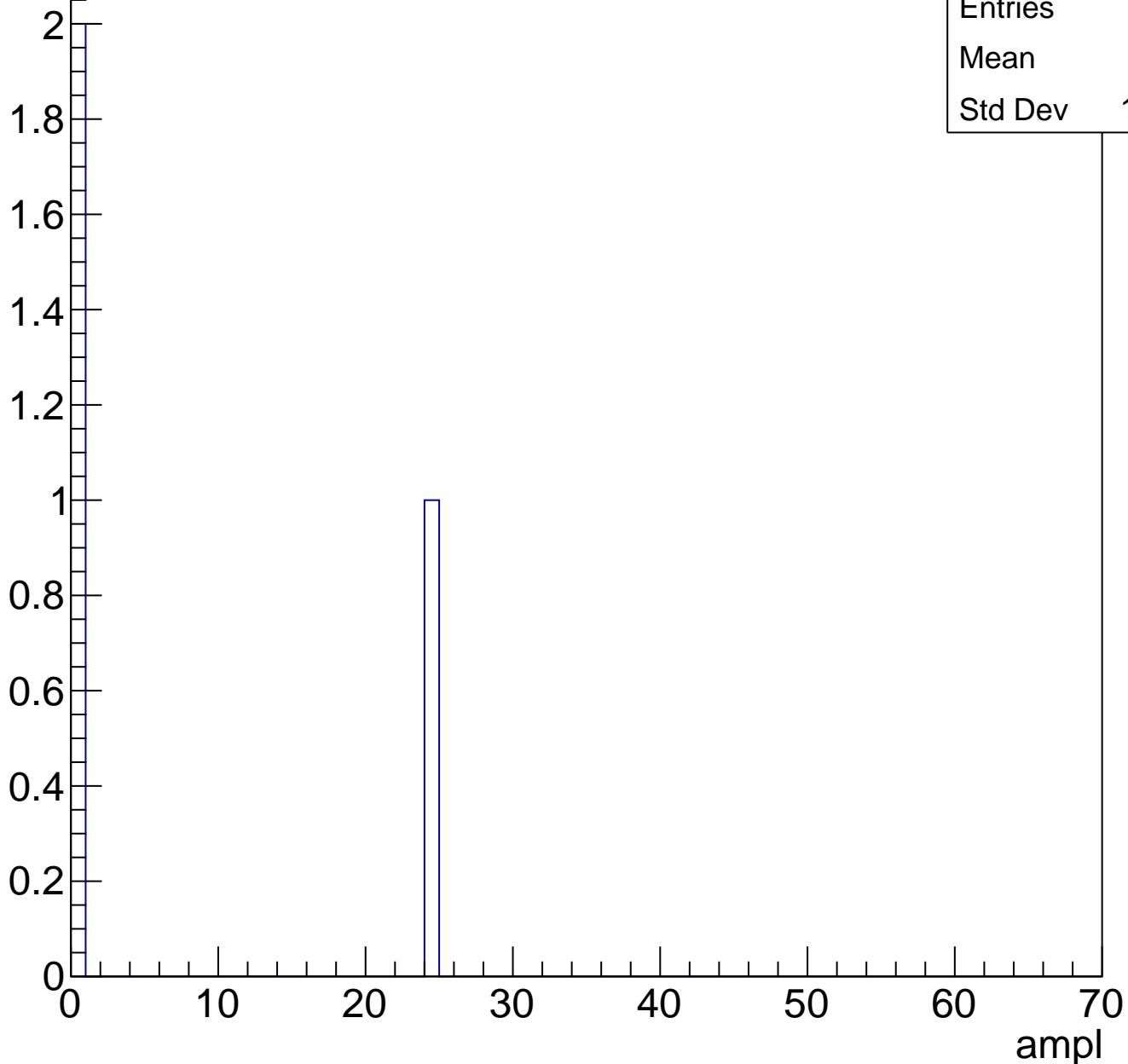
Entries	9
Mean	55.78
Std Dev	19.72



# B0L000S, U19-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L000S, U19-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	29.83
Std Dev	3.354

**Gaus mean : 30.5728**

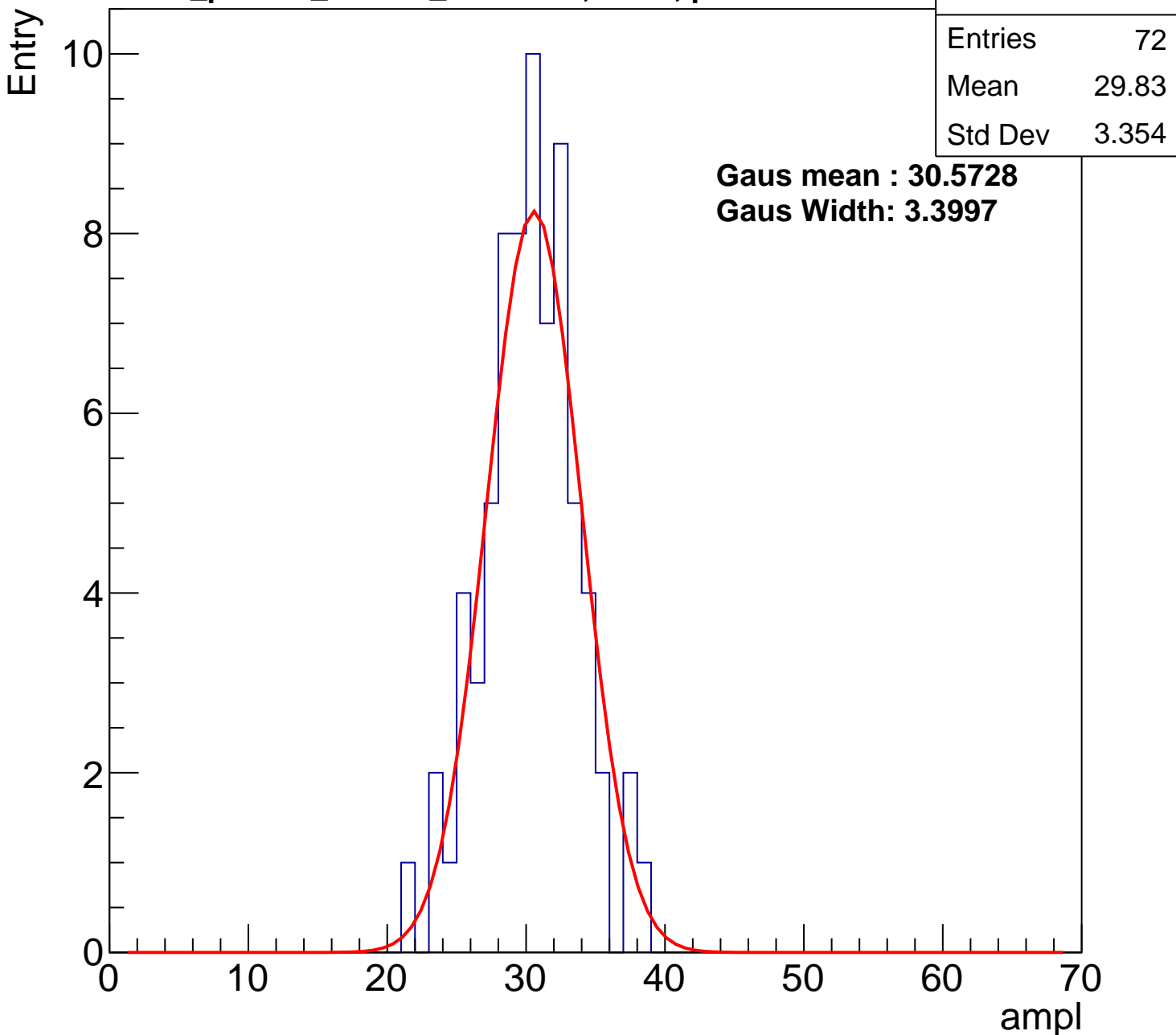
**Gaus Width: 3.3997**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch97, adc1

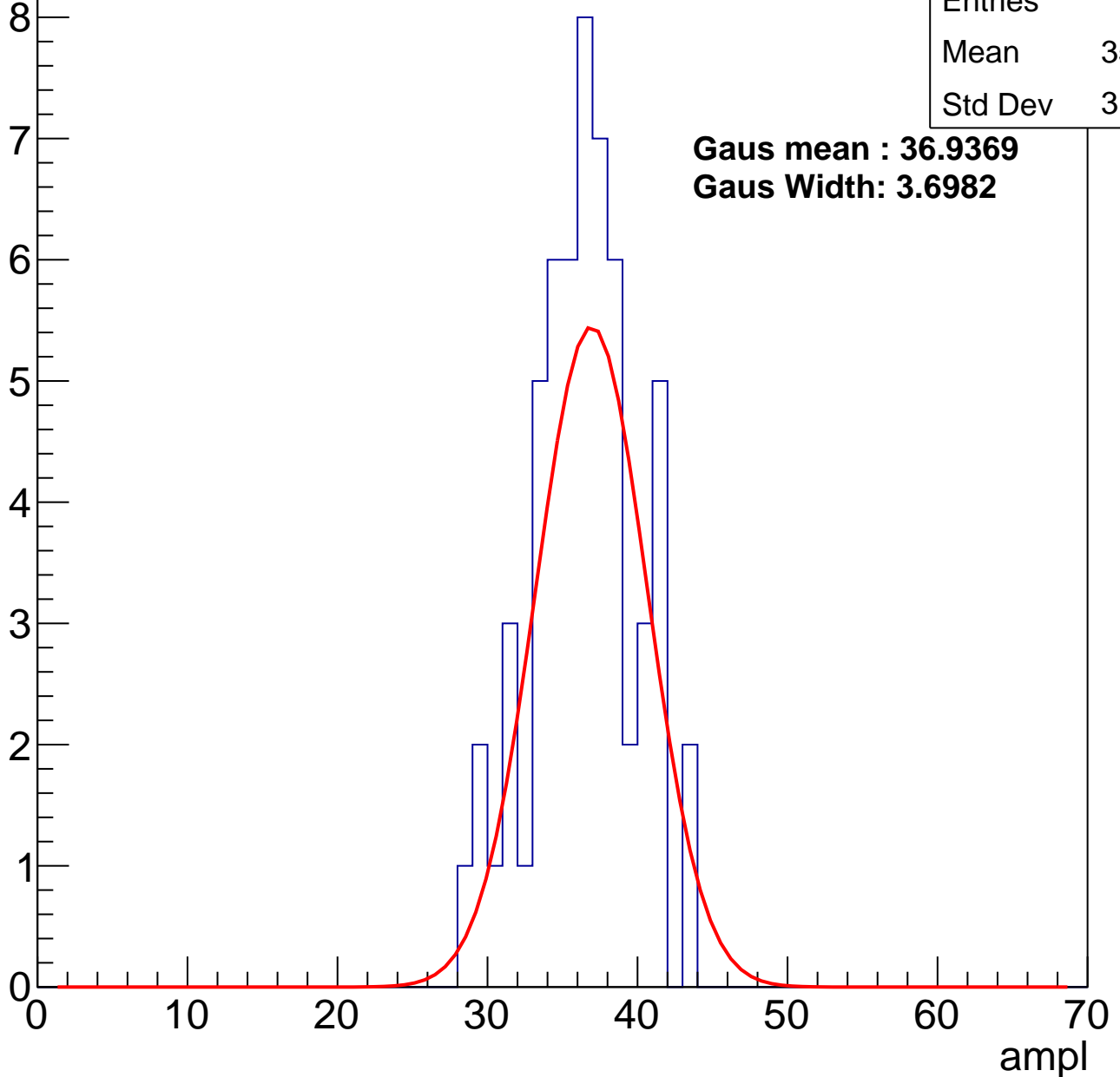
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	35.93
Std Dev	3.433

**Gaus mean : 36.9369**

**Gaus Width: 3.6982**



# B0L000S, U19-ch97, adc2

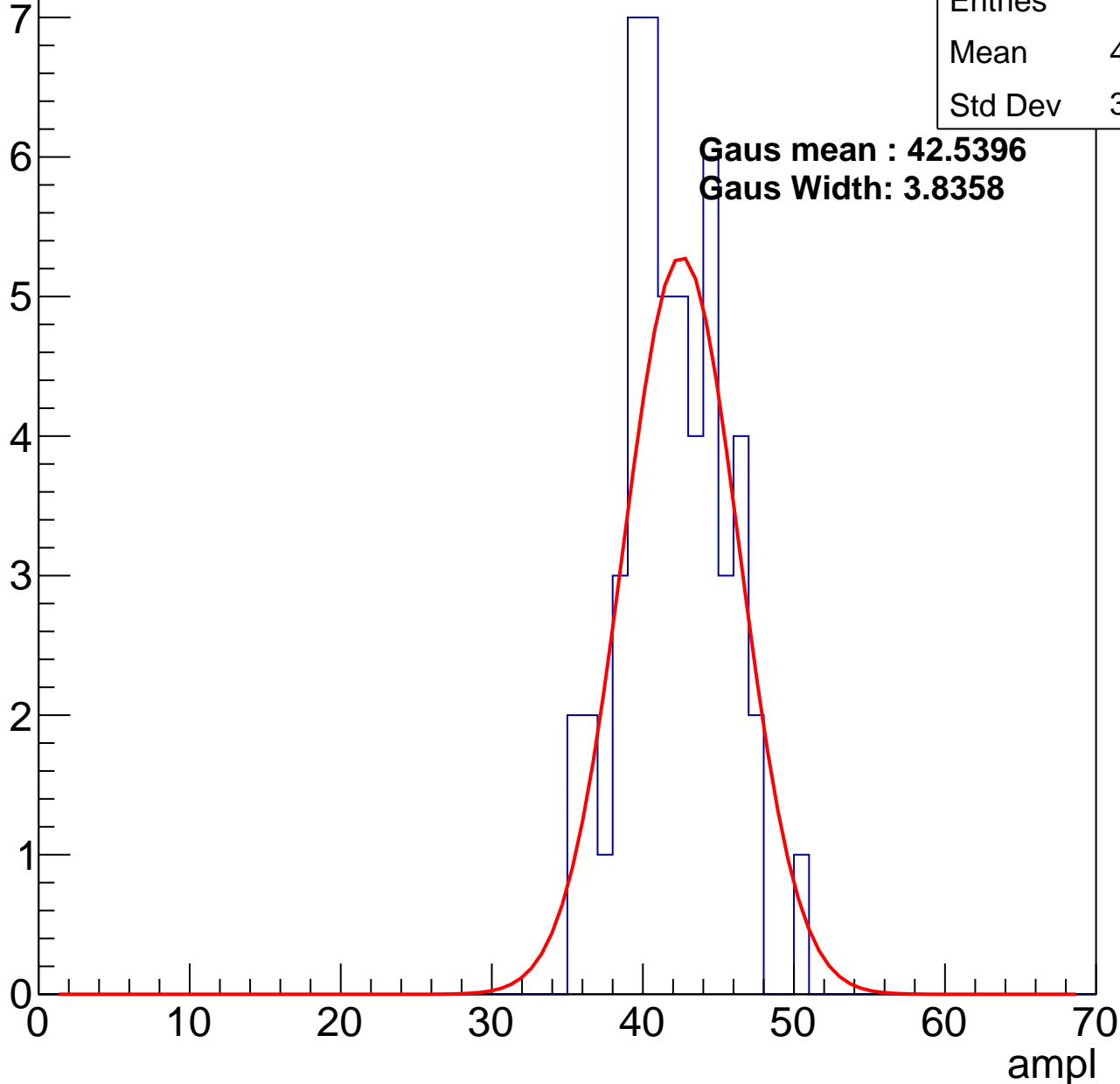
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	41.54
Std Dev	3.284

**Gaus mean : 42.5396**

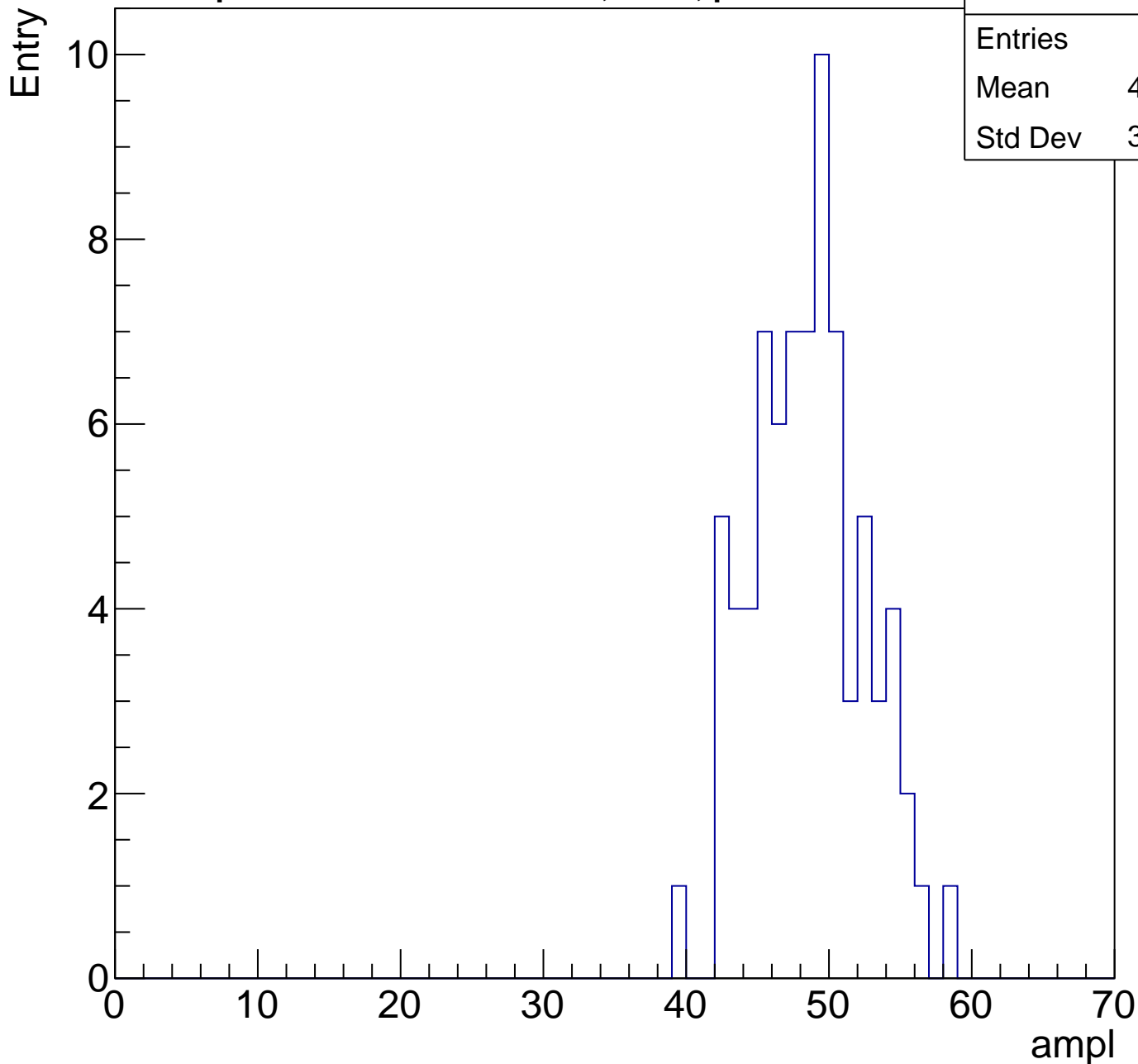
**Gaus Width: 3.8358**



# B0L000S, U19-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	48.12
Std Dev	3.854

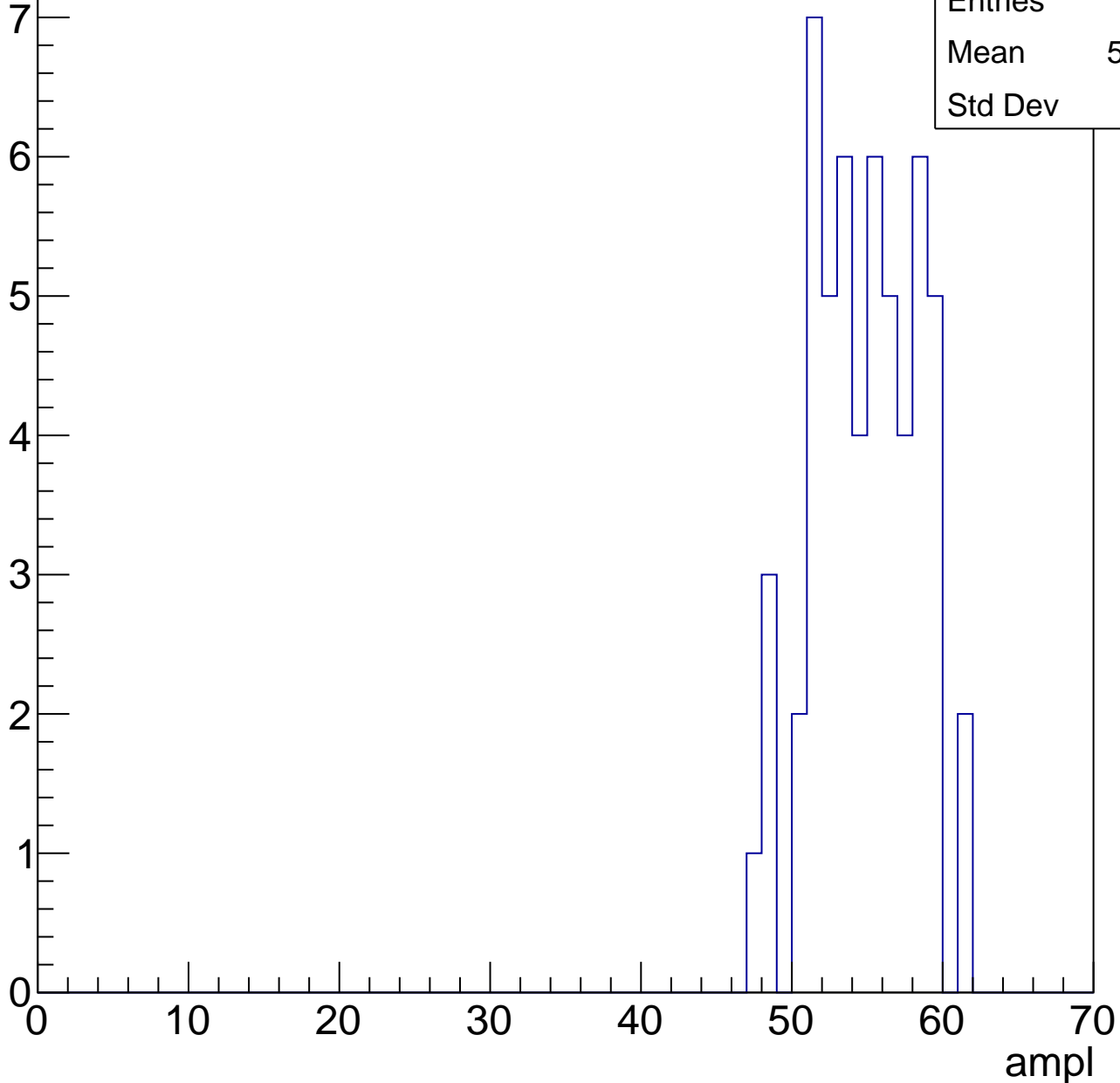


# B0L000S, U19-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	54.38
Std Dev	3.41

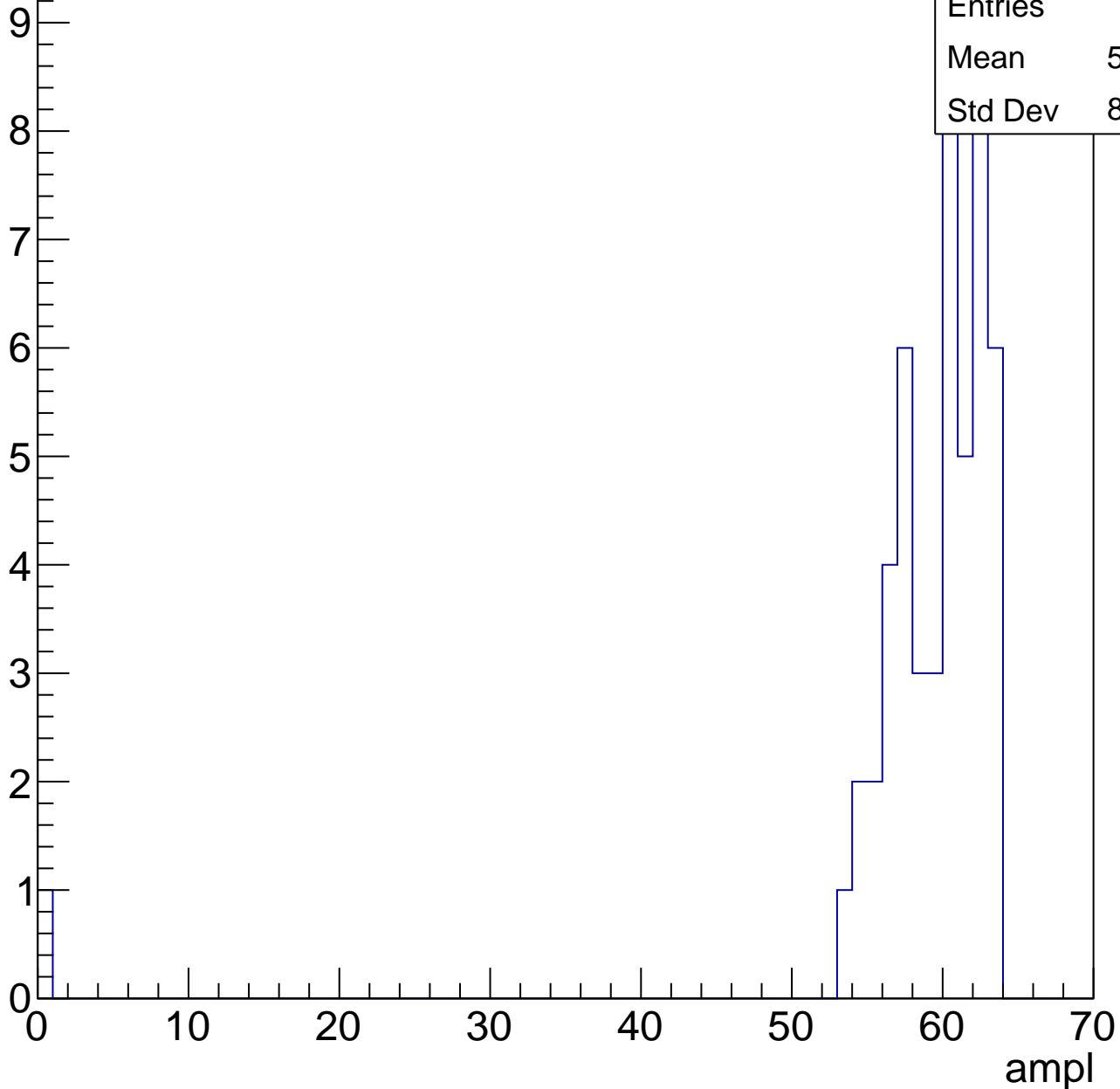


# B0L000S, U19-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

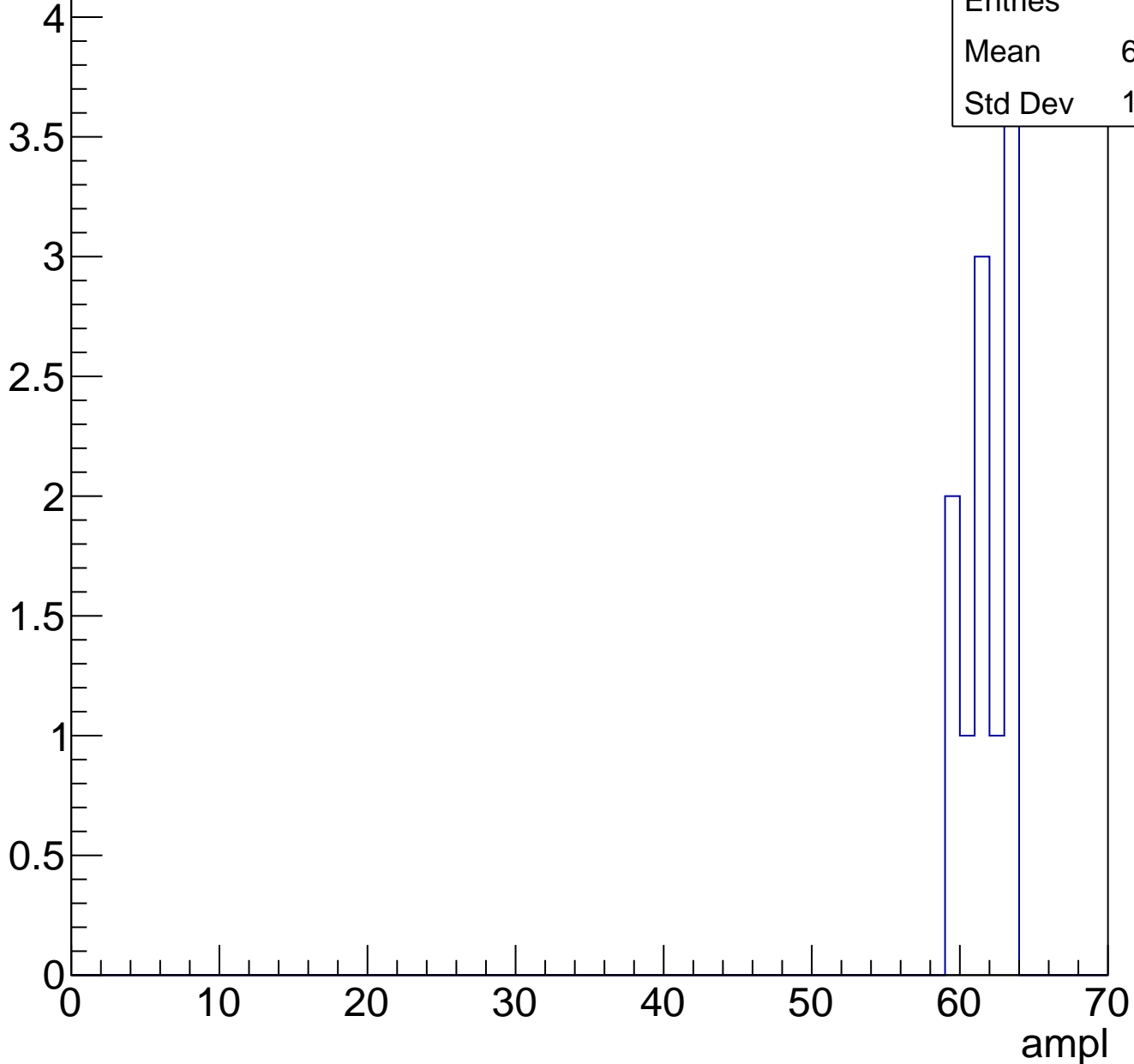
Entries	51
Mean	58.22
Std Dev	8.673



# B0L000S, U19-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	11
Mean	61.36
Std Dev	1.494



# B0L000S, U19-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch98, adc0

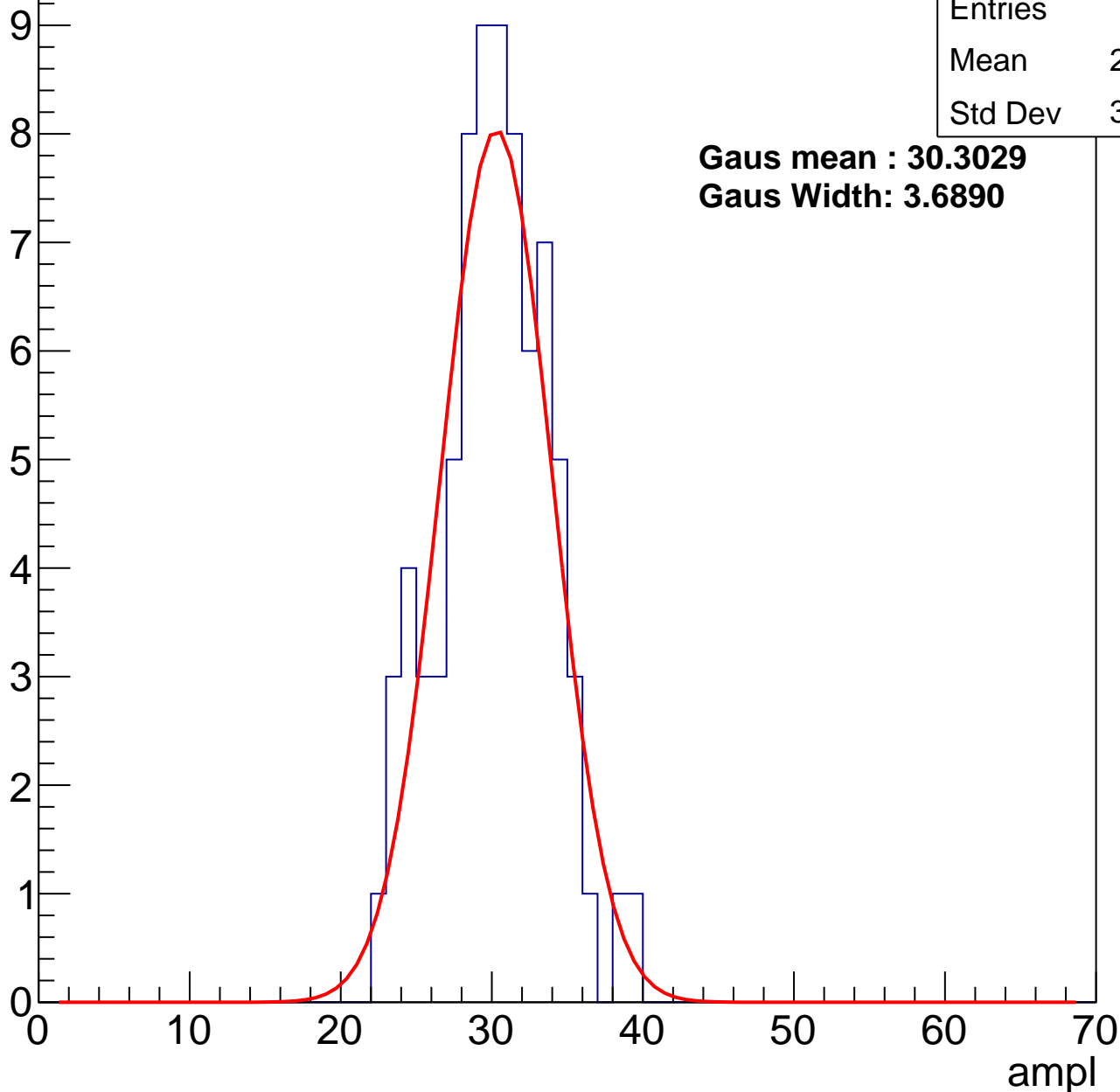
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	29.73
Std Dev	3.584

**Gaus mean : 30.3029**

**Gaus Width: 3.6890**



# B0L000S, U19-ch98, adc1

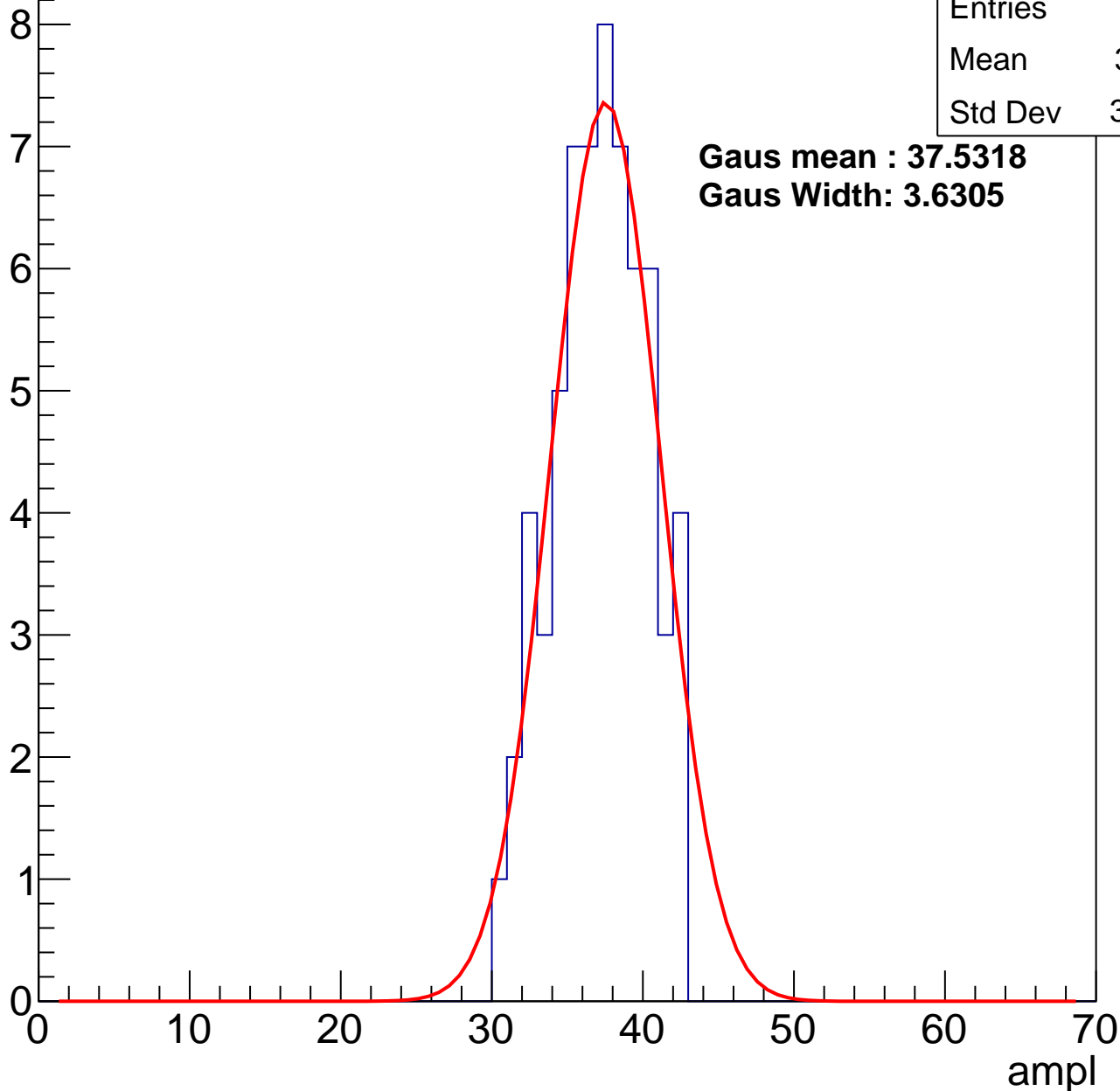
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.71
Std Dev	3.026

**Gaus mean : 37.5318**

**Gaus Width: 3.6305**



# B0L000S, U19-ch98, adc2

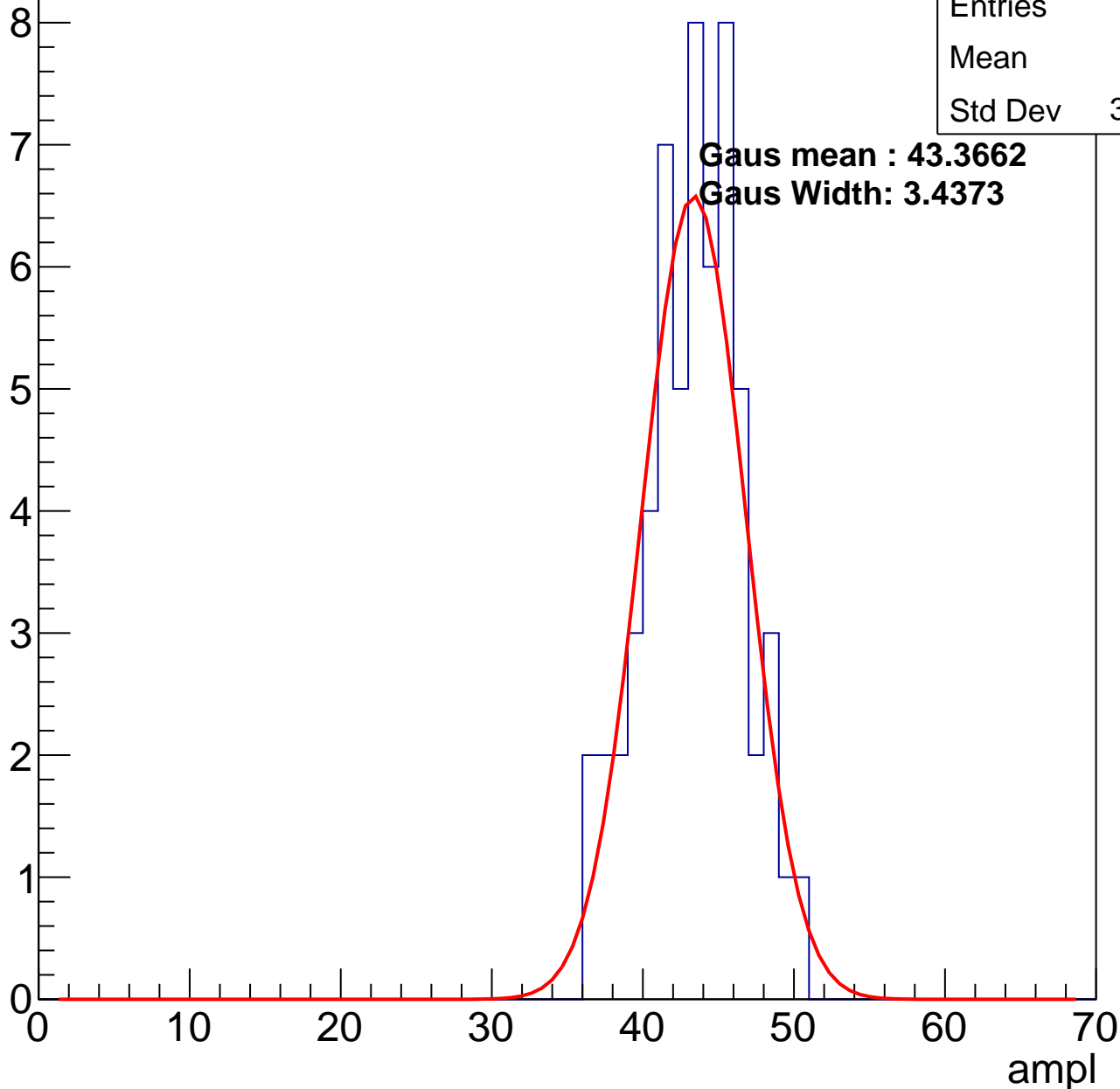
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.9
Std Dev	3.219

**Gaus mean : 43.3662**

**Gaus Width: 3.4373**

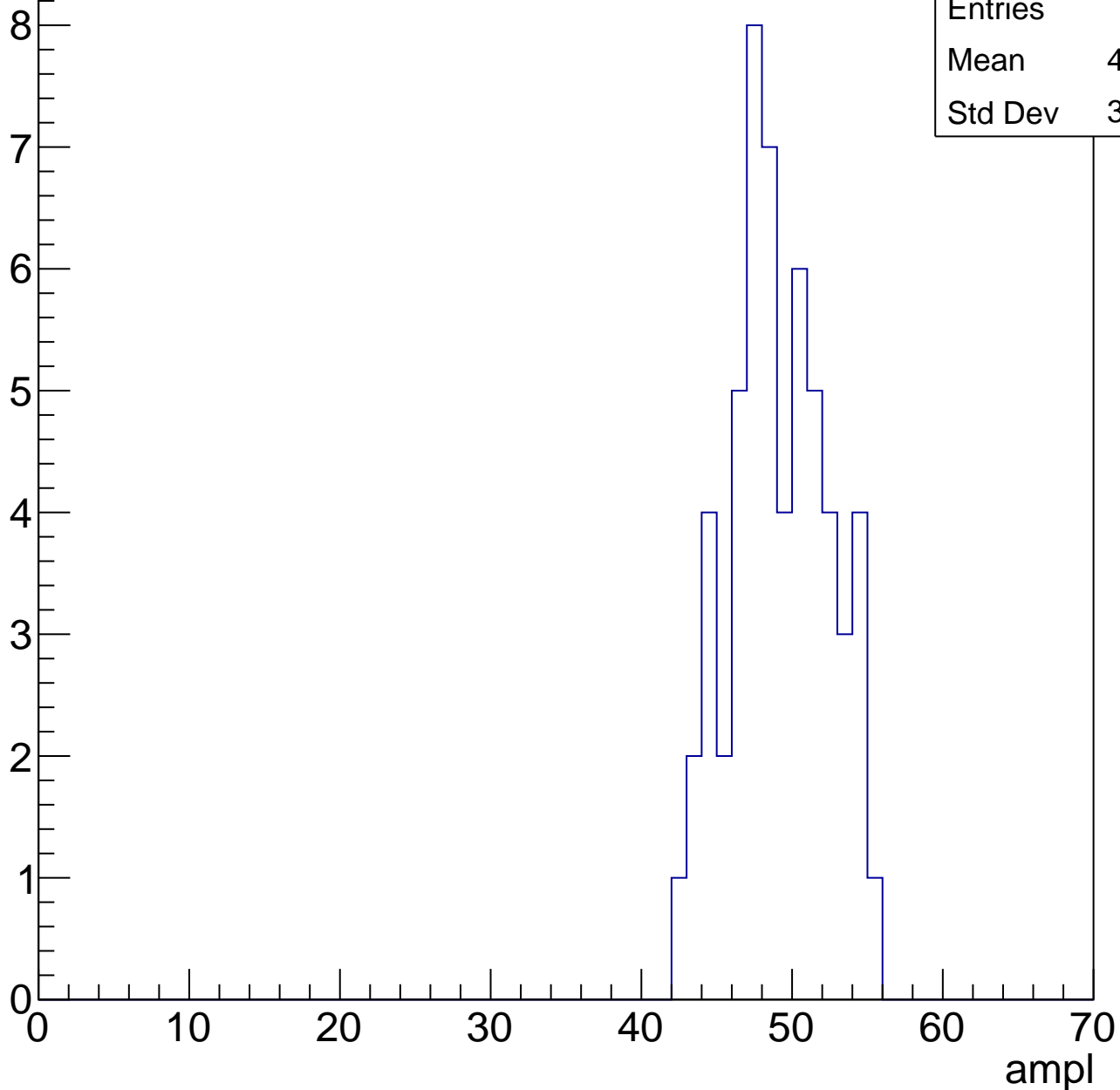


# B0L000S, U19-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

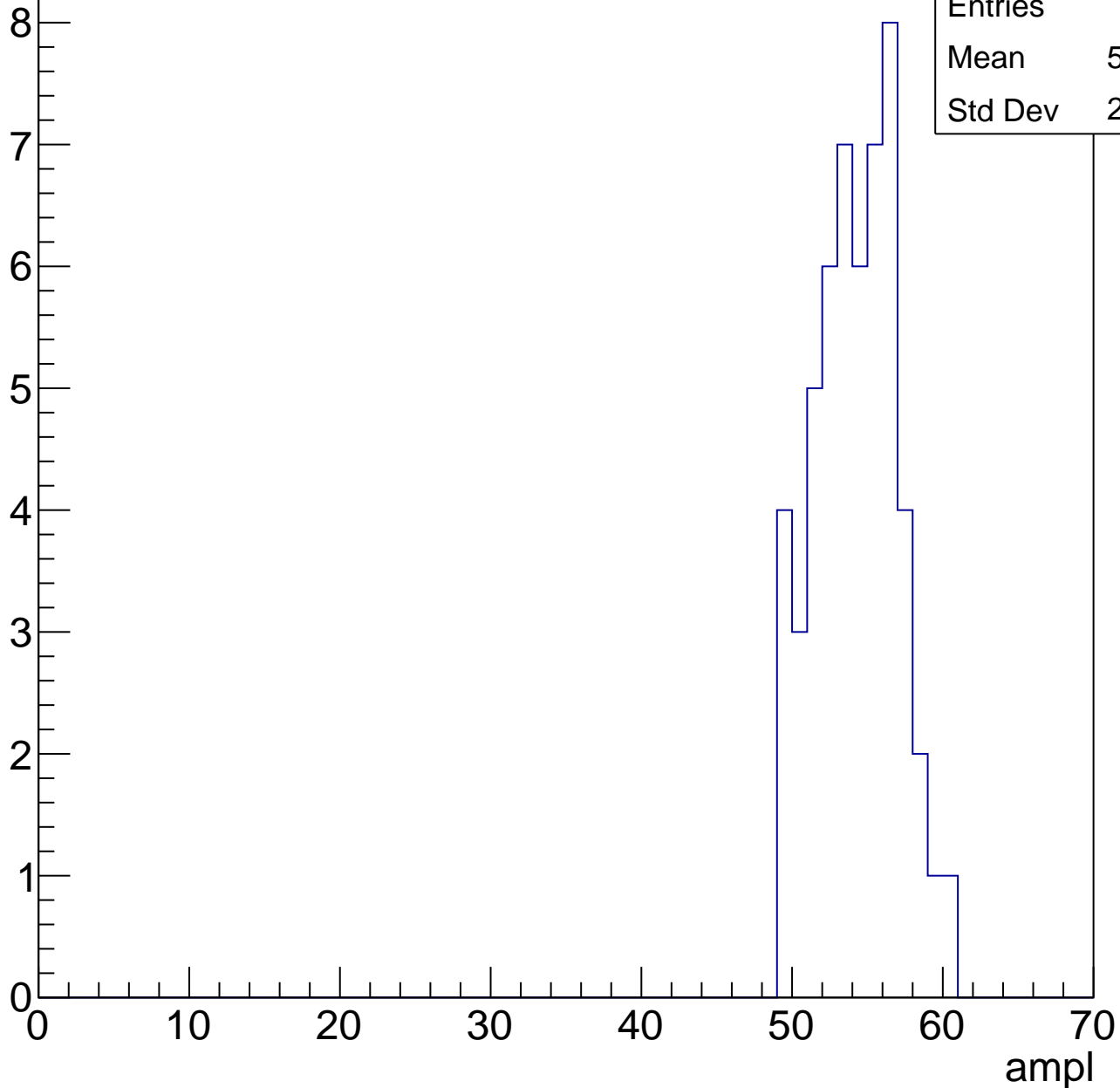
Entries	56
Mean	48.66
Std Dev	3.209



# B0L000S, U19-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

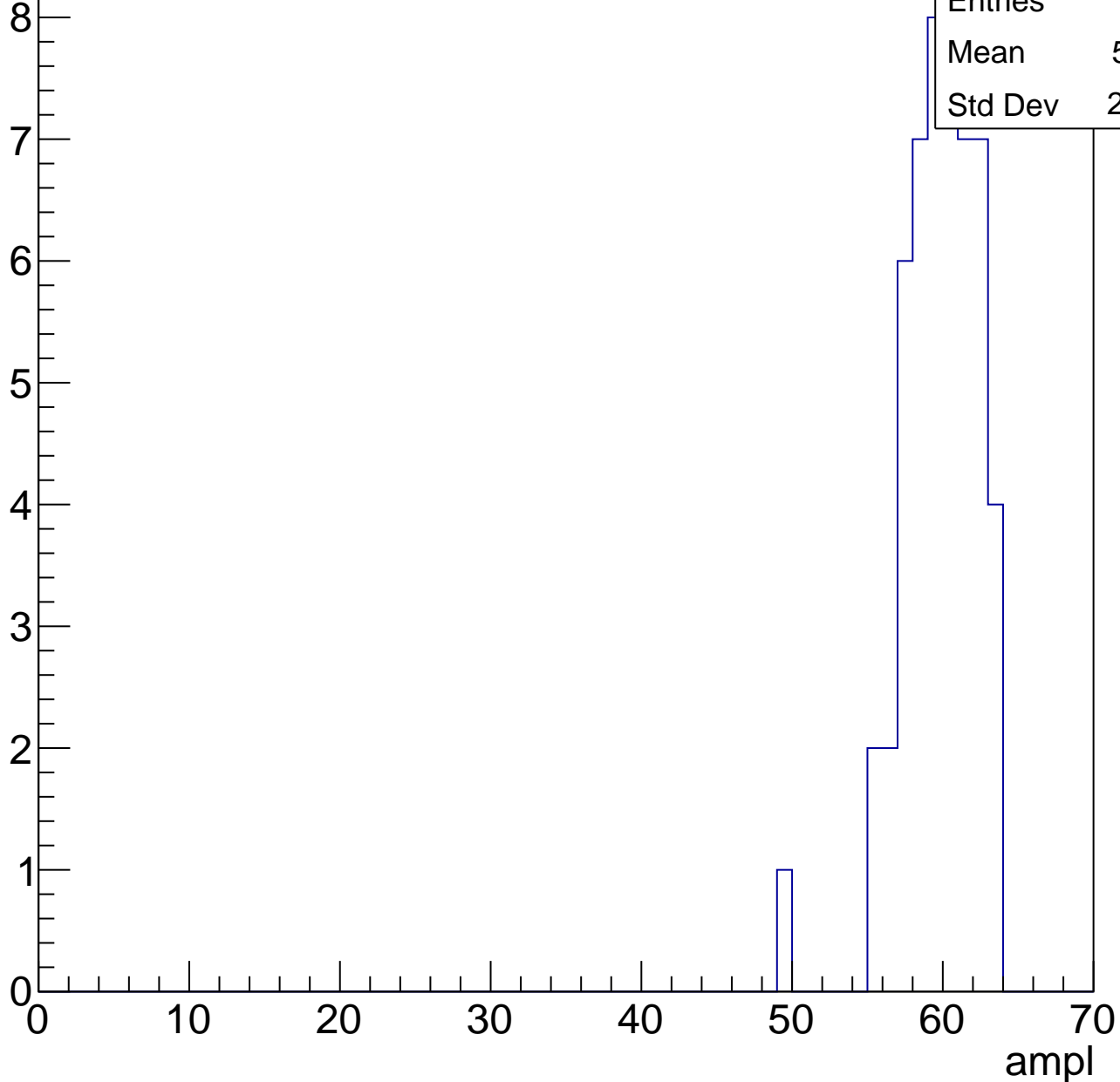
Entry



# B0L000S, U19-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

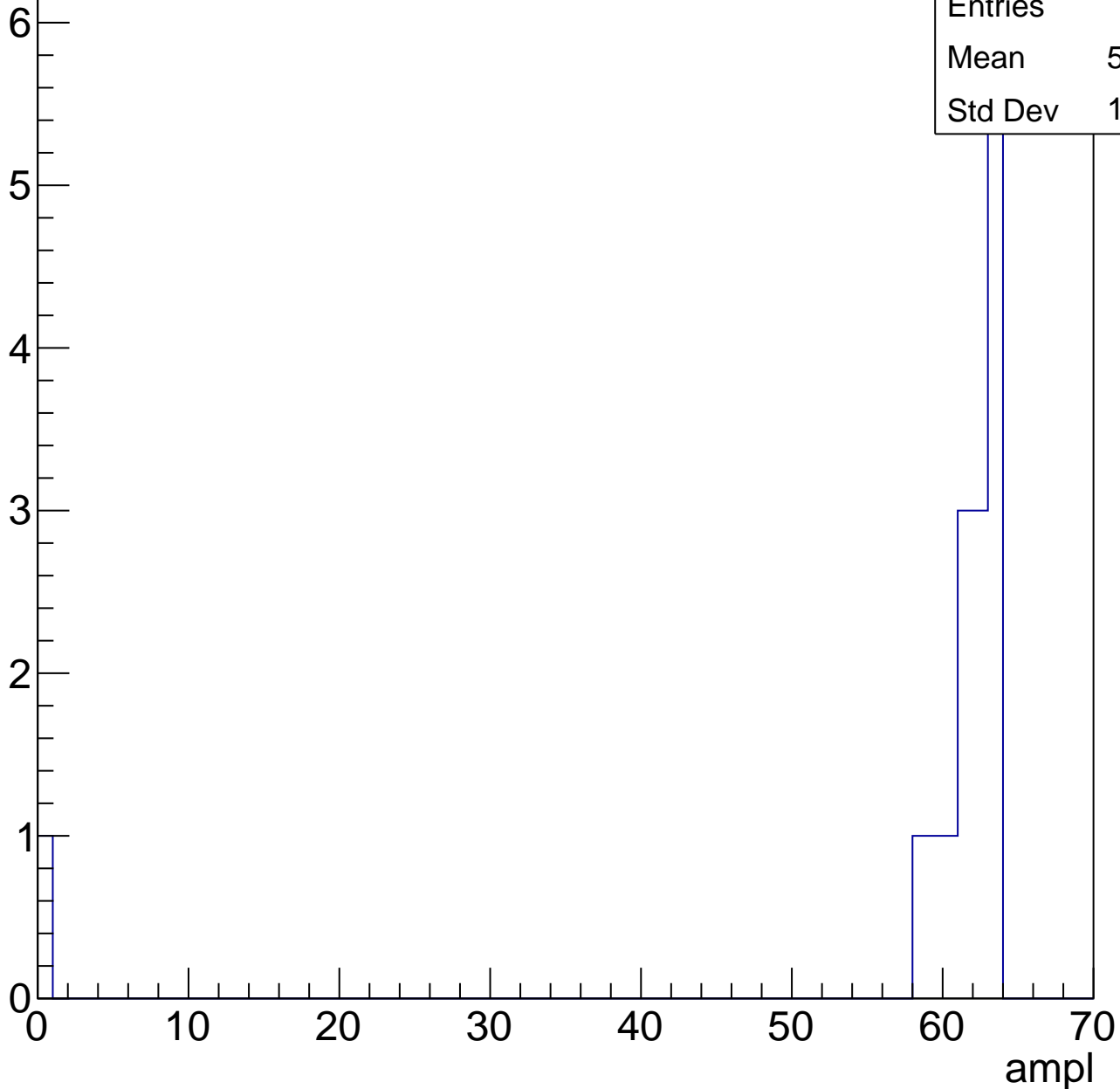


# B0L000S, U19-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.75
Std Dev	14.99





# B0L000S, U19-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B0L000S, U19-ch99, adc0

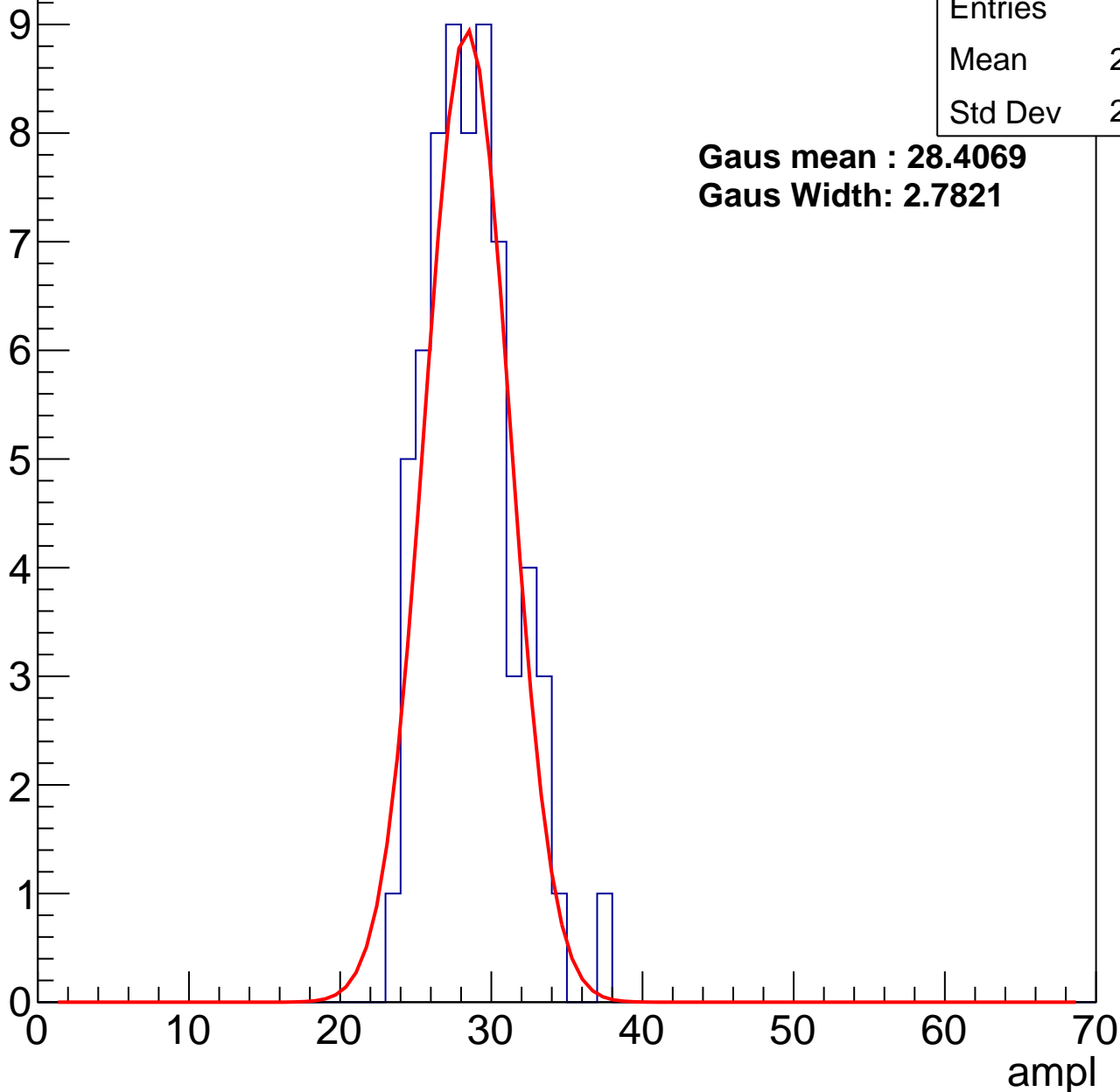
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	28.15
Std Dev	2.824

**Gaus mean : 28.4069**

**Gaus Width: 2.7821**



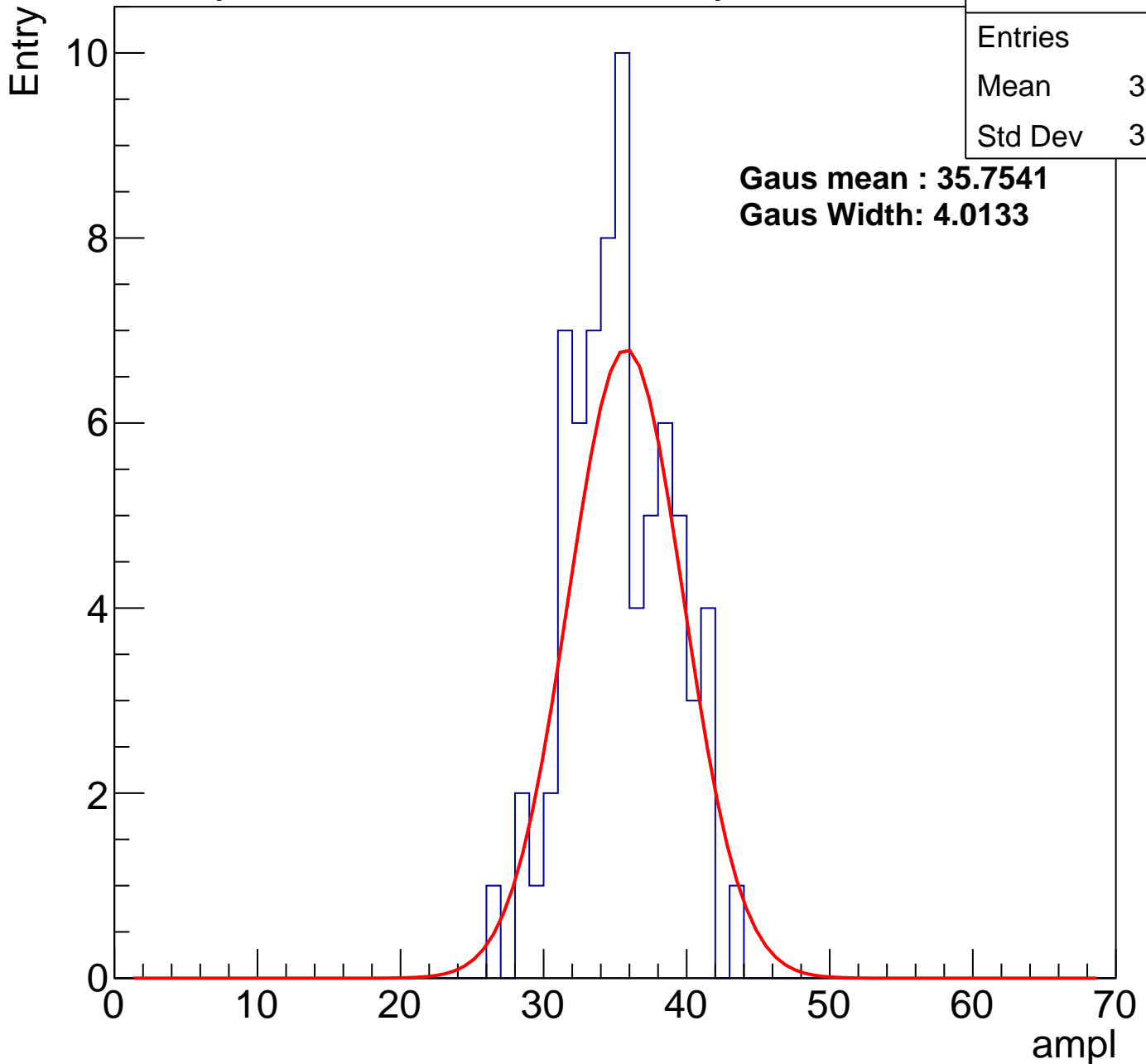
# B0L000S, U19-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	34.89
Std Dev	3.549

**Gaus mean : 35.7541**

**Gaus Width: 4.0133**



# B0L000S, U19-ch99, adc2

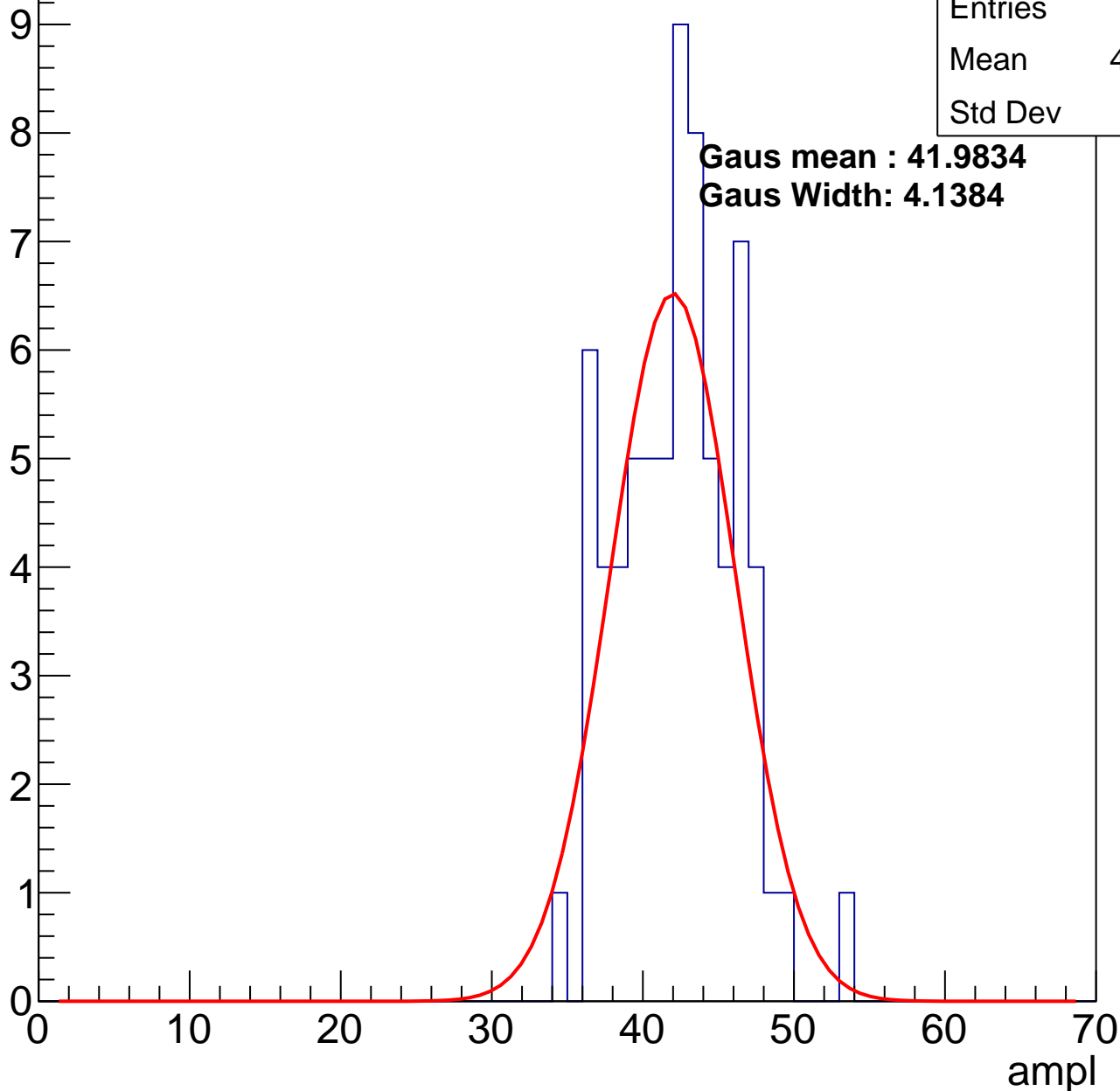
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.89
Std Dev	3.77

**Gaus mean : 41.9834**

**Gaus Width: 4.1384**

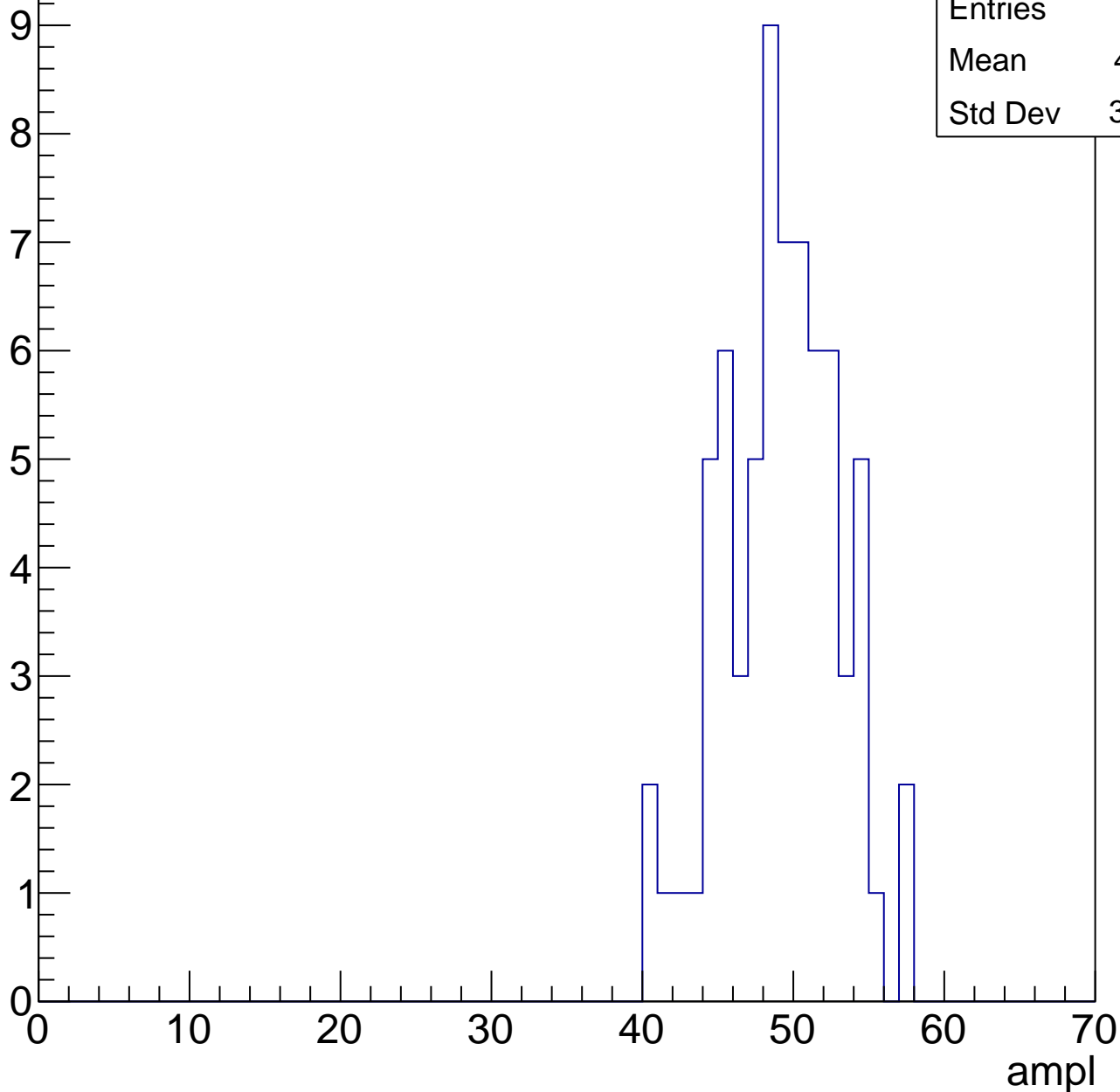


# B0L000S, U19-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	48.71
Std Dev	3.788

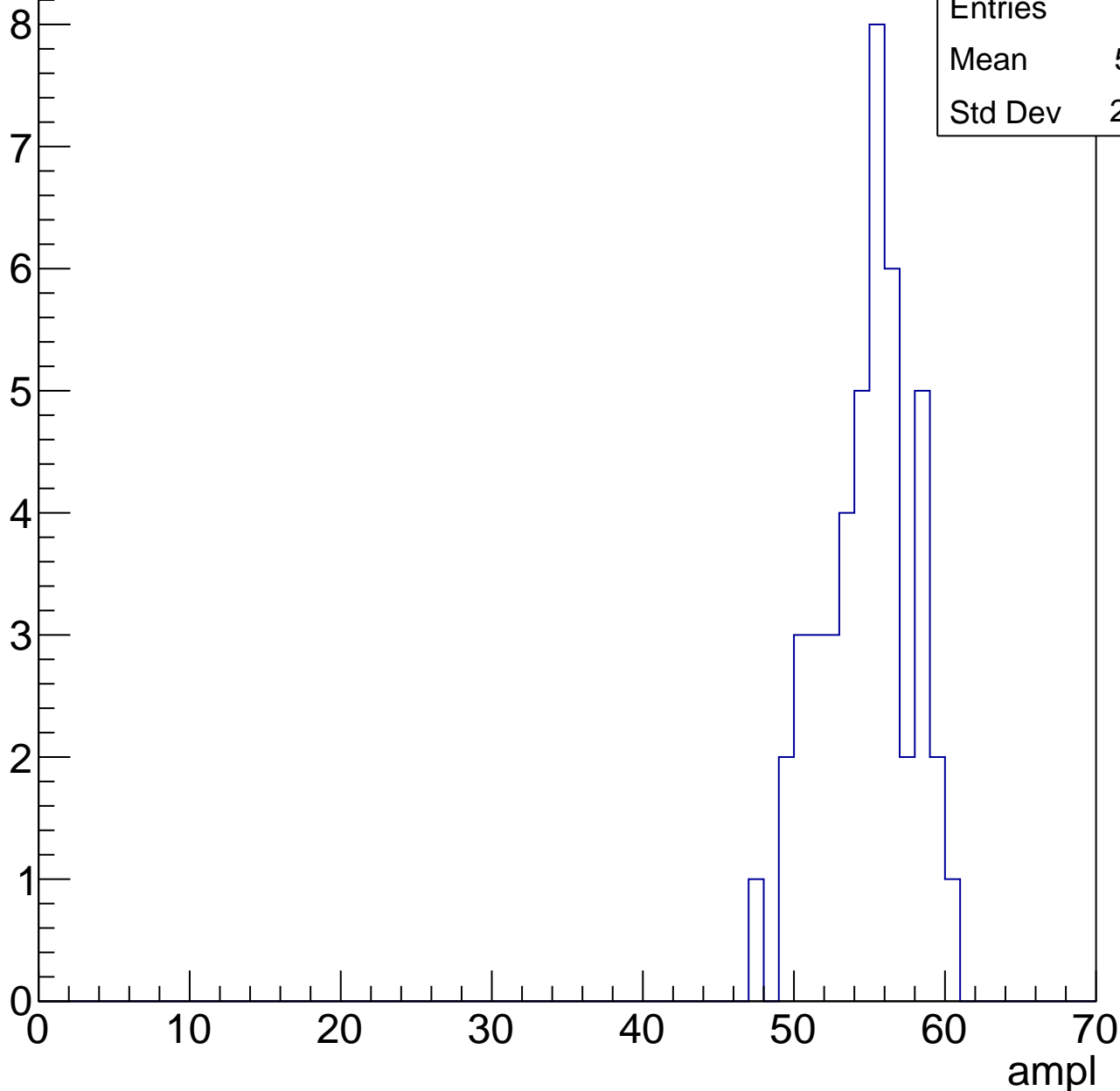


# B0L000S, U19-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

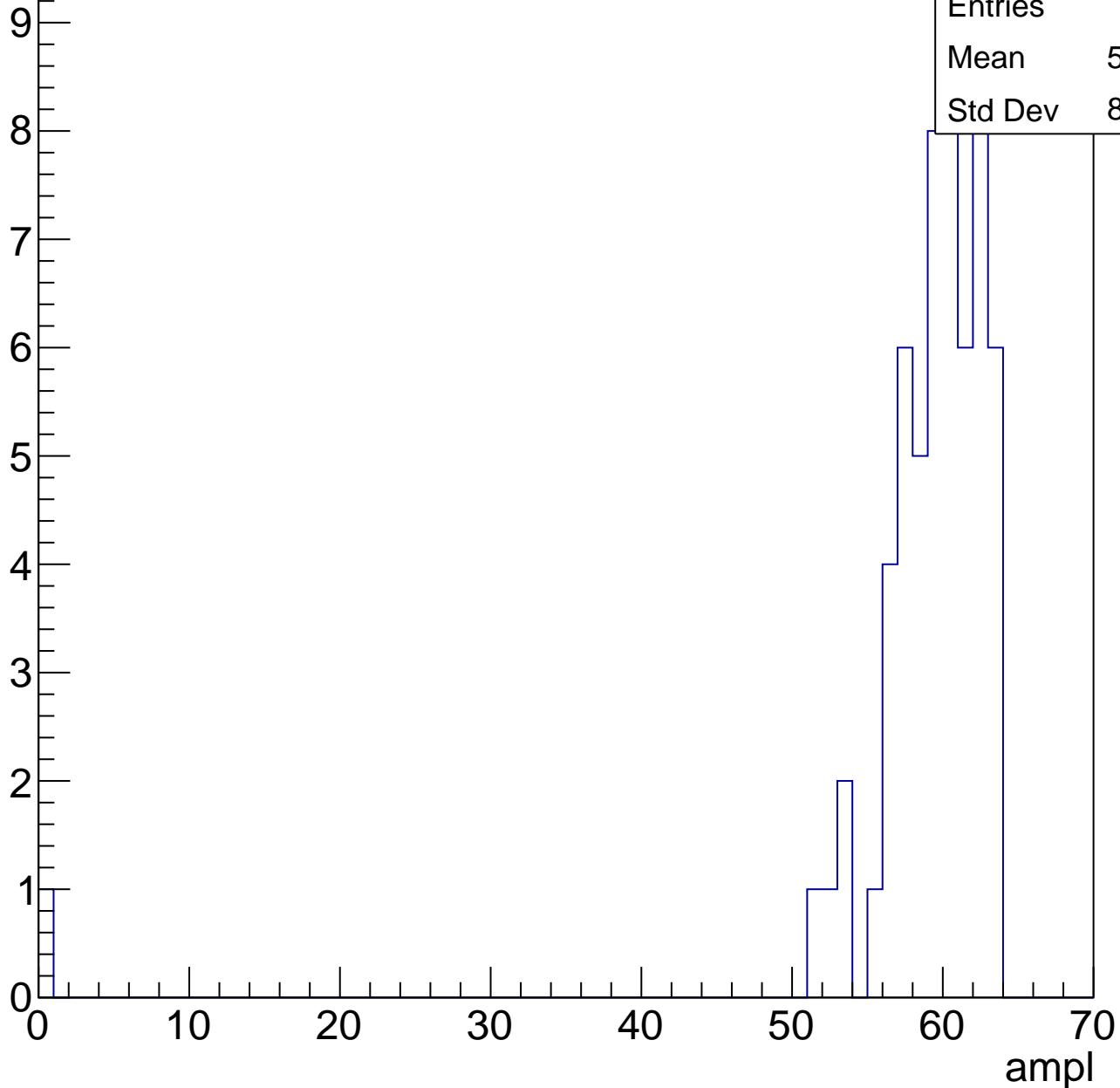
Entries	45
Mean	54.31
Std Dev	2.988



# B0L000S, U19-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

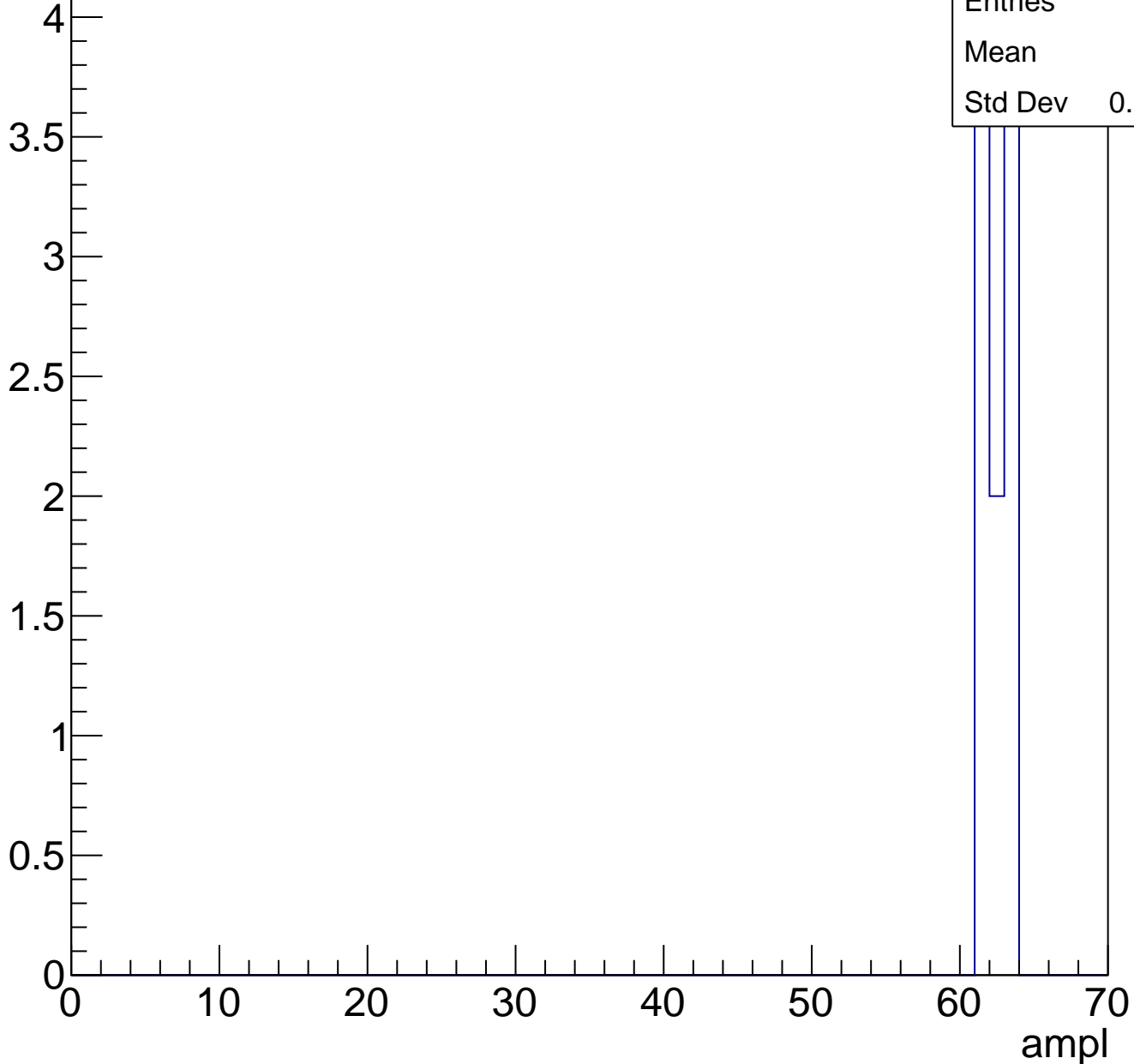
Entry



# B0L000S, U19-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	10
Mean	62
Std Dev	0.8944



# B0L000S, U19-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch100, adc0

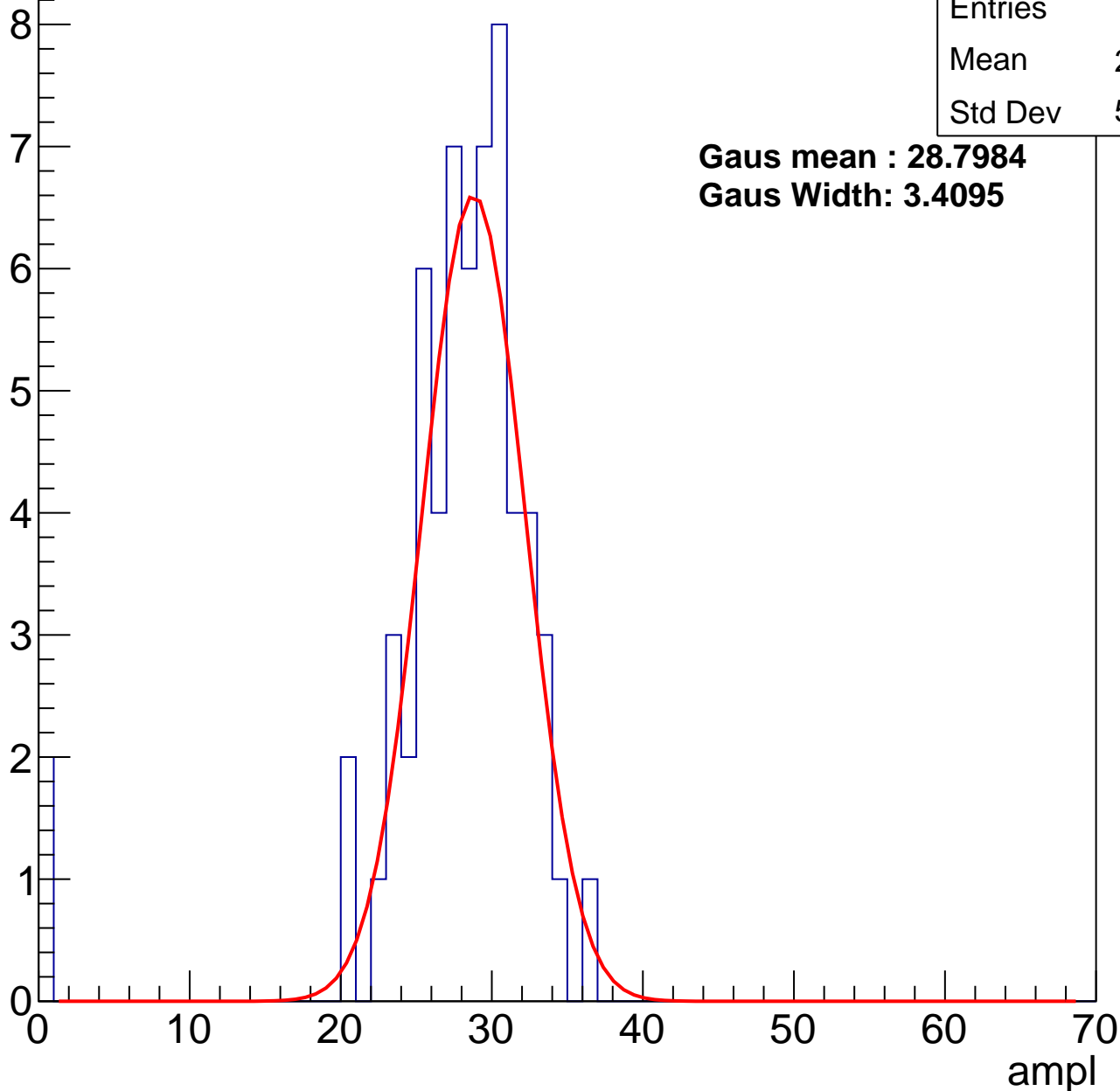
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	27.11
Std Dev	5.981

**Gaus mean : 28.7984**

**Gaus Width: 3.4095**



# B0L000S, U19-ch100, adc1

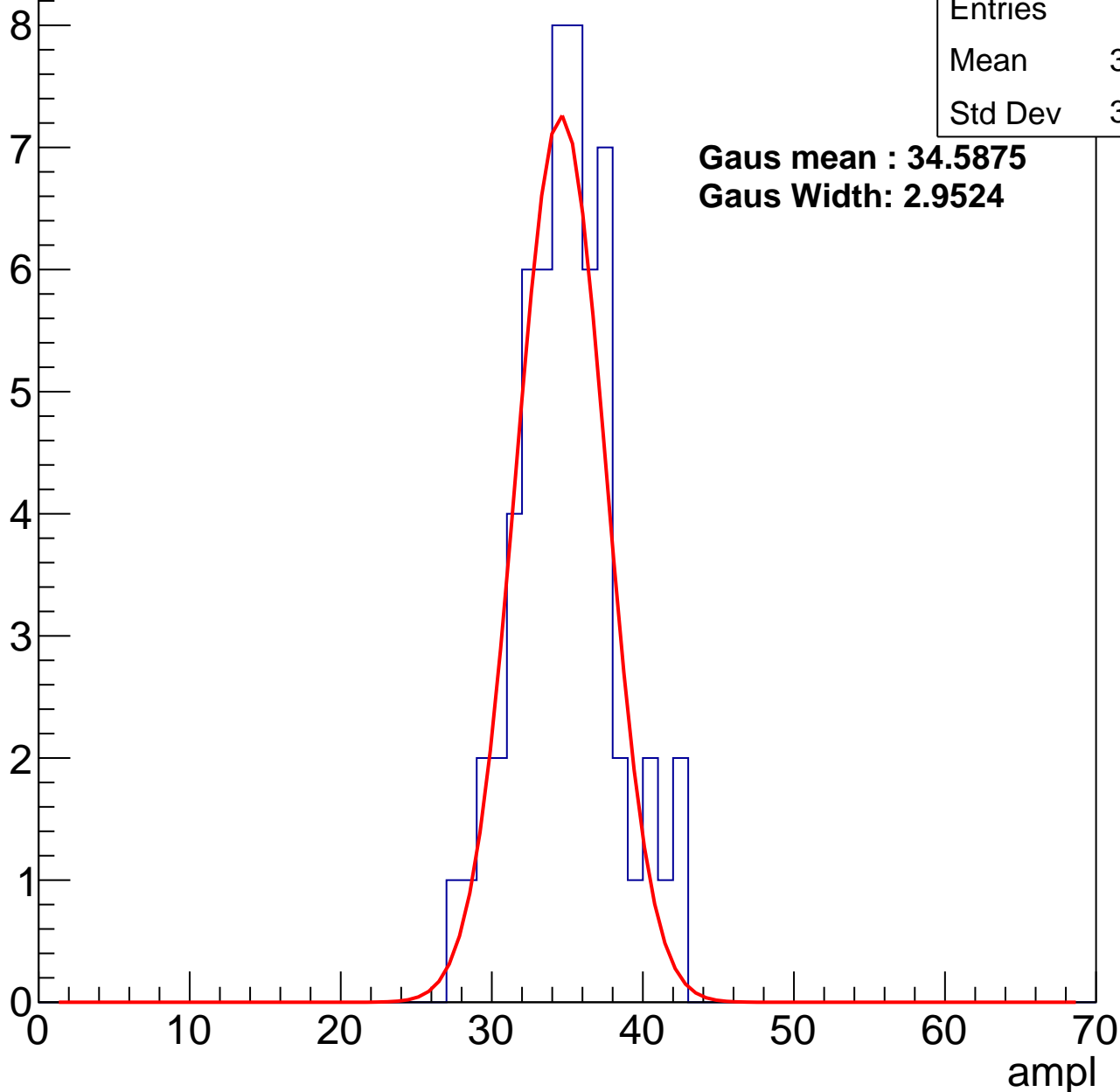
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	34.47
Std Dev	3.244

**Gaus mean : 34.5875**

**Gaus Width: 2.9524**



# B0L000S, U19-ch100, adc2

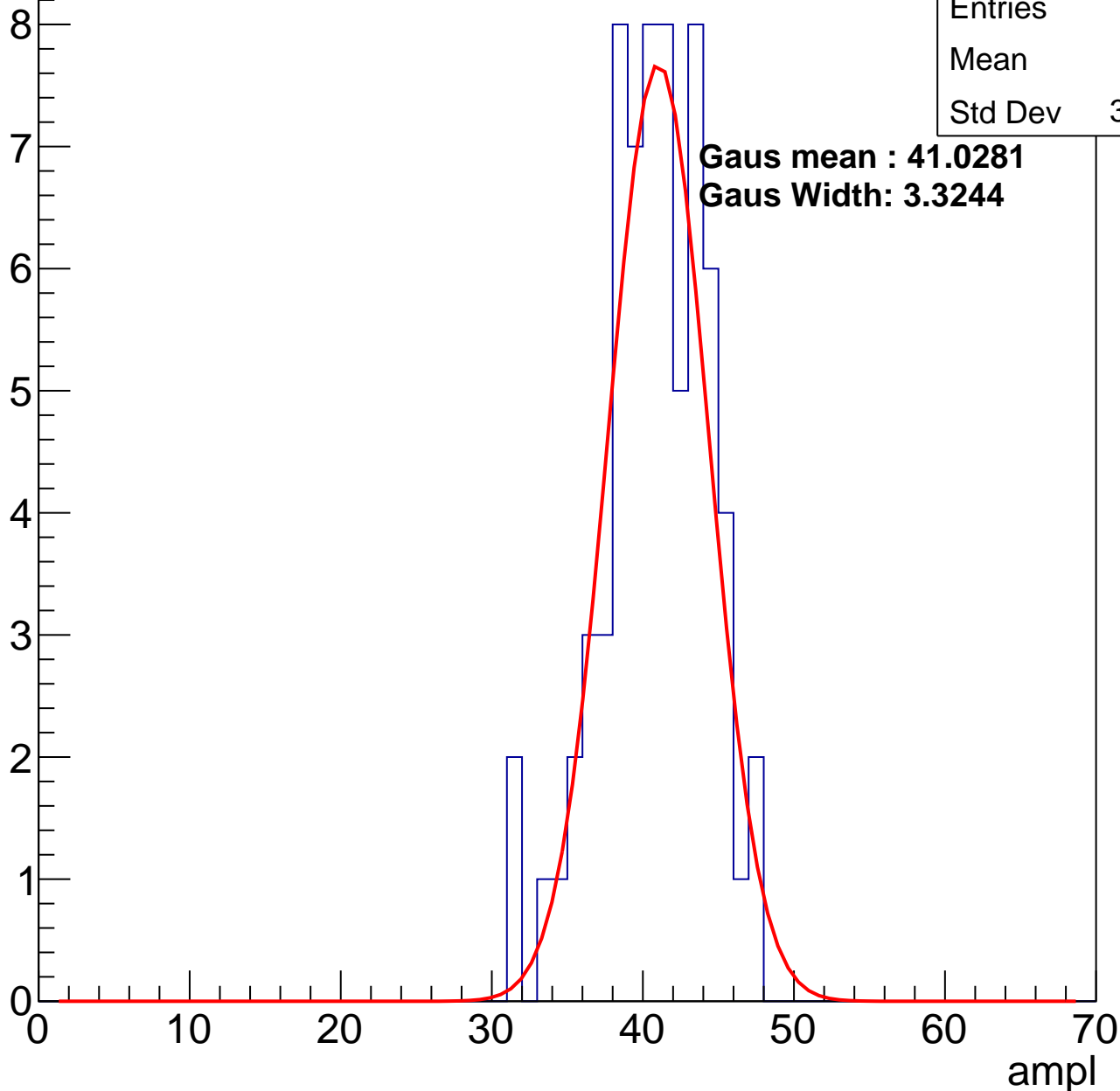
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	40.3
Std Dev	3.478

**Gaus mean : 41.0281**

**Gaus Width: 3.3244**

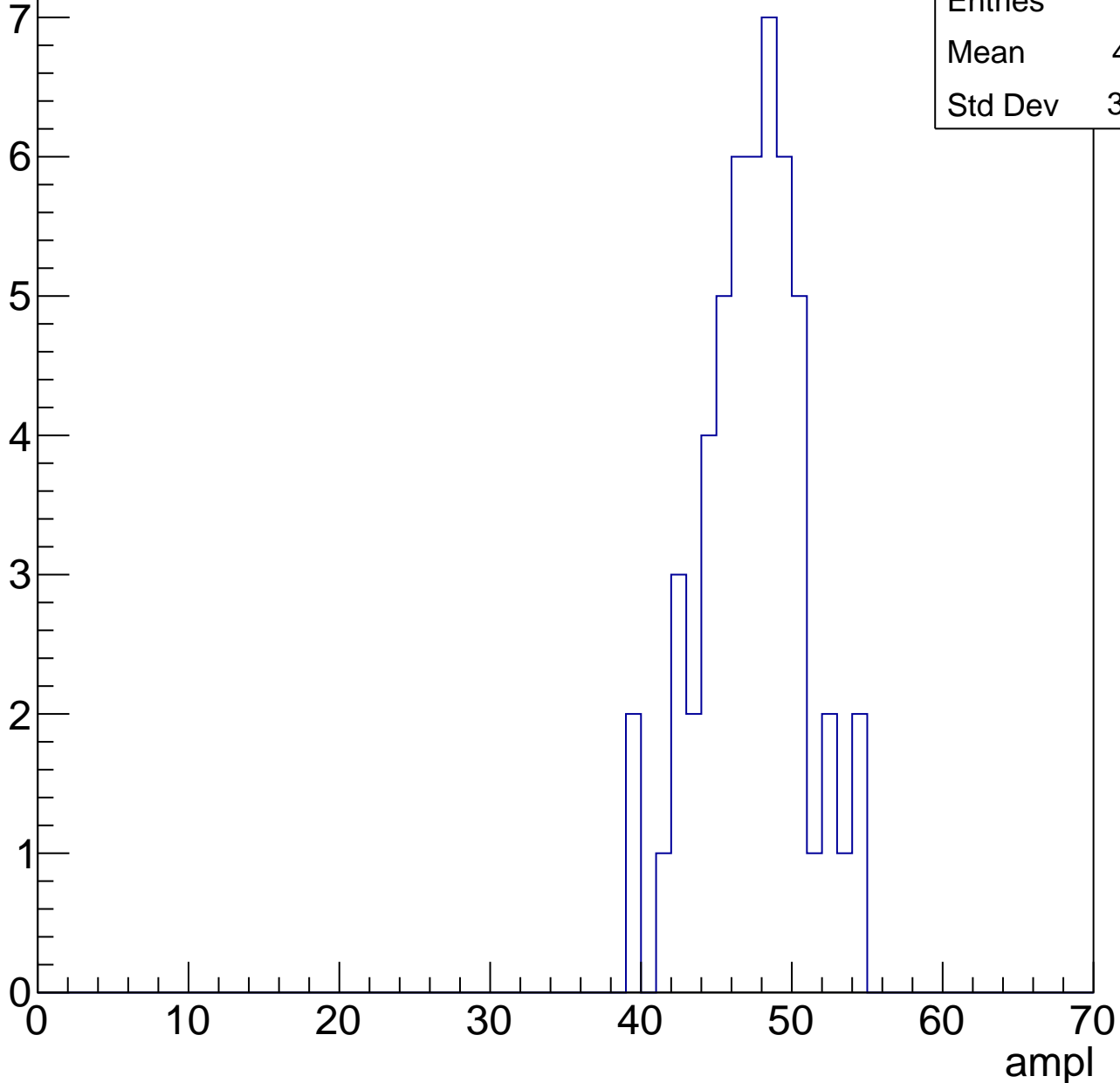


# B0L000S, U19-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	46.91
Std Dev	3.388

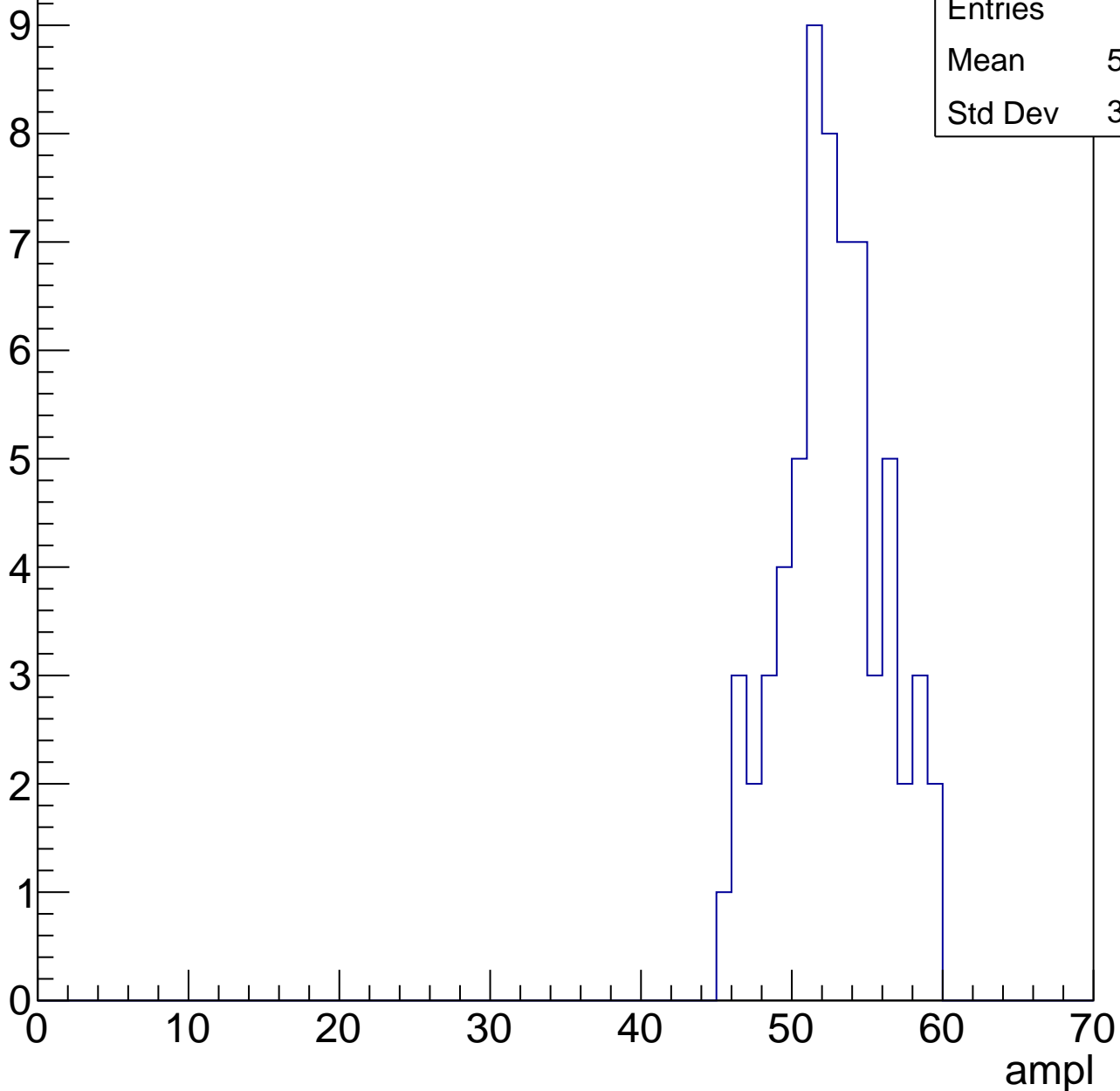


# B0L000S, U19-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	52.22
Std Dev	3.342



# B0L000S, U19-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

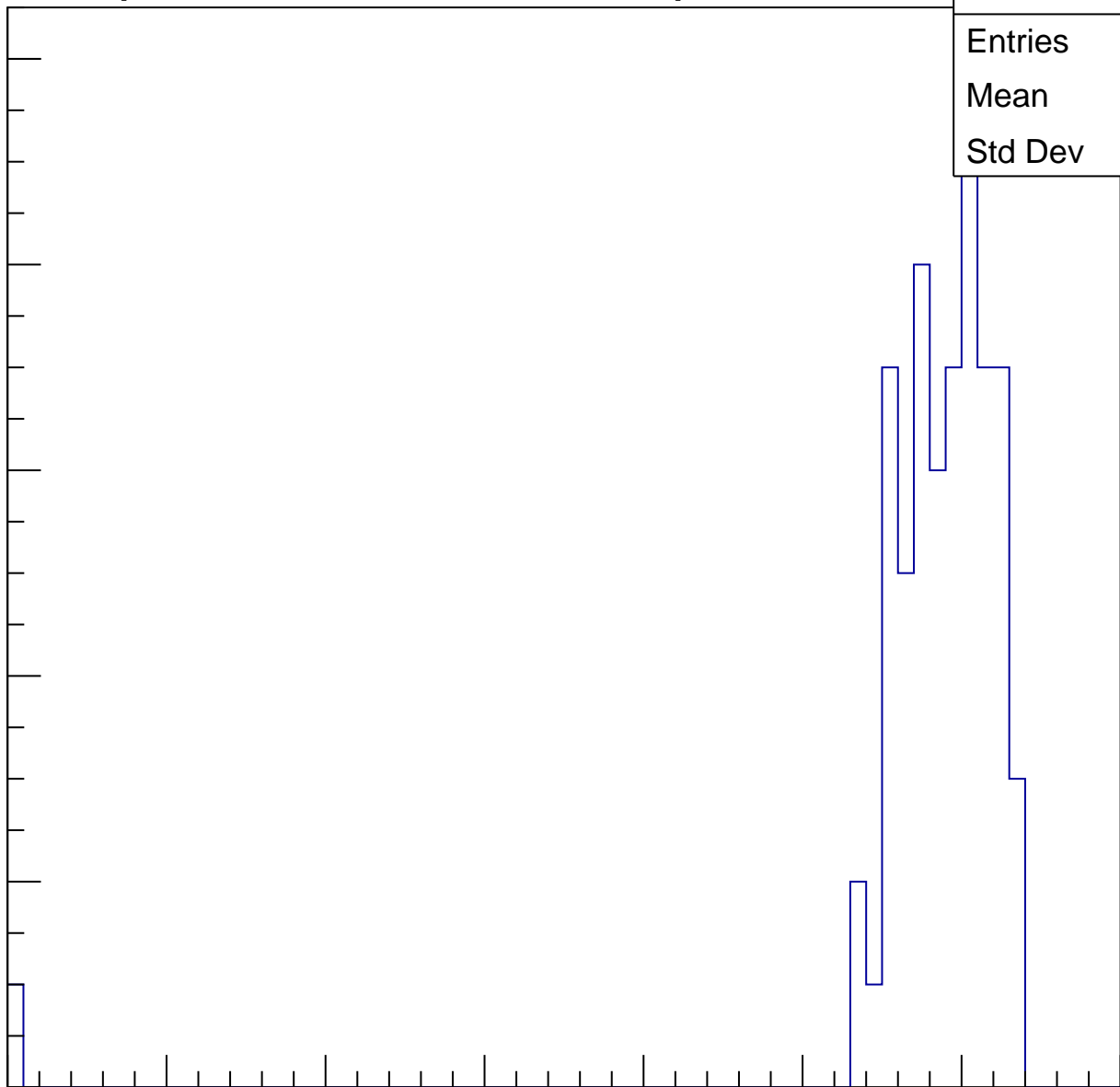
Entries	64
Mean	57.69
Std Dev	7.717

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

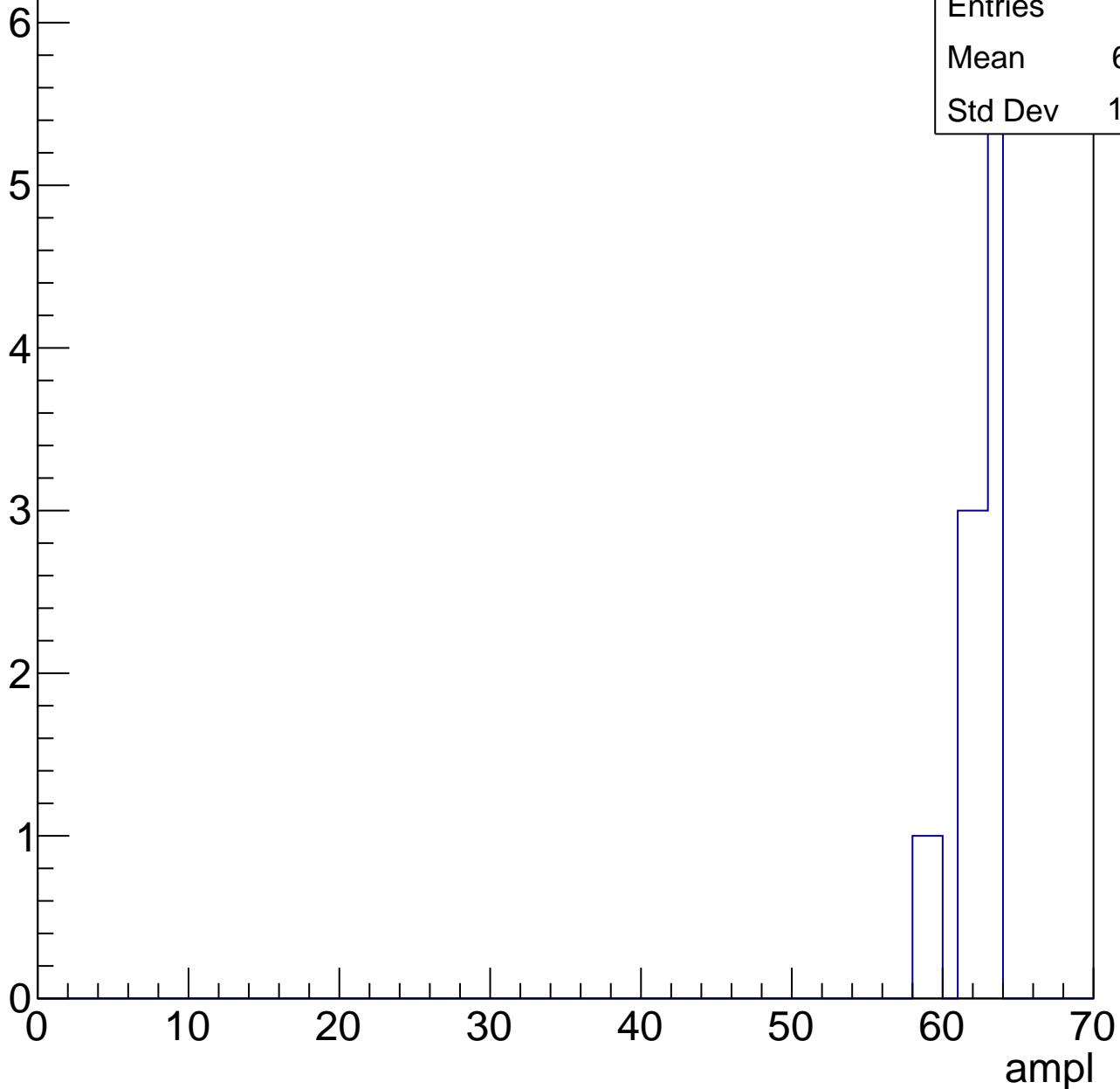


# B0L000S, U19-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	61.71
Std Dev	1.532





# B0L000S, U19-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch101, adc0

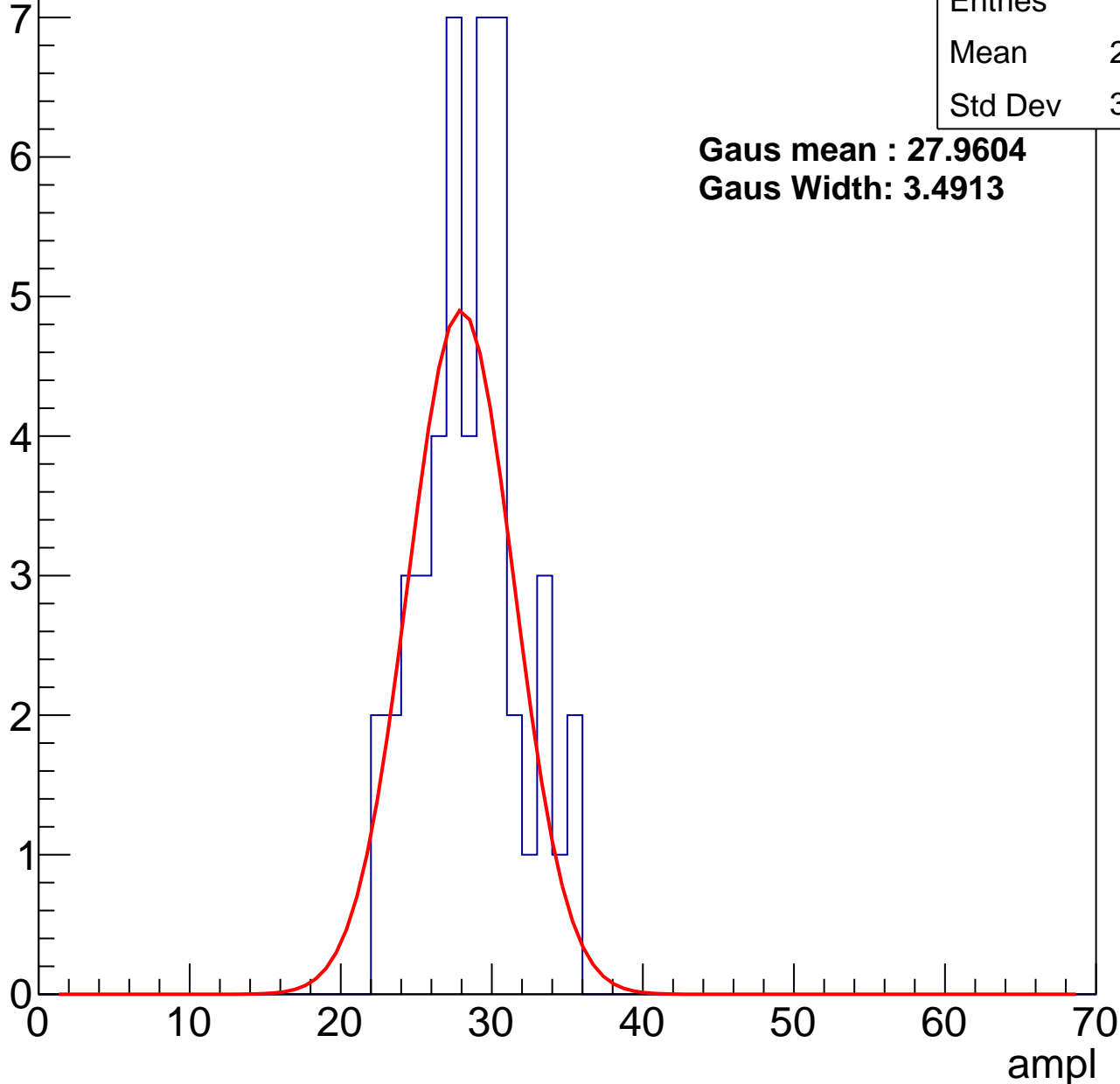
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	28.17
Std Dev	3.217

**Gaus mean : 27.9604**

**Gaus Width: 3.4913**



# B0L000S, U19-ch101, adc1

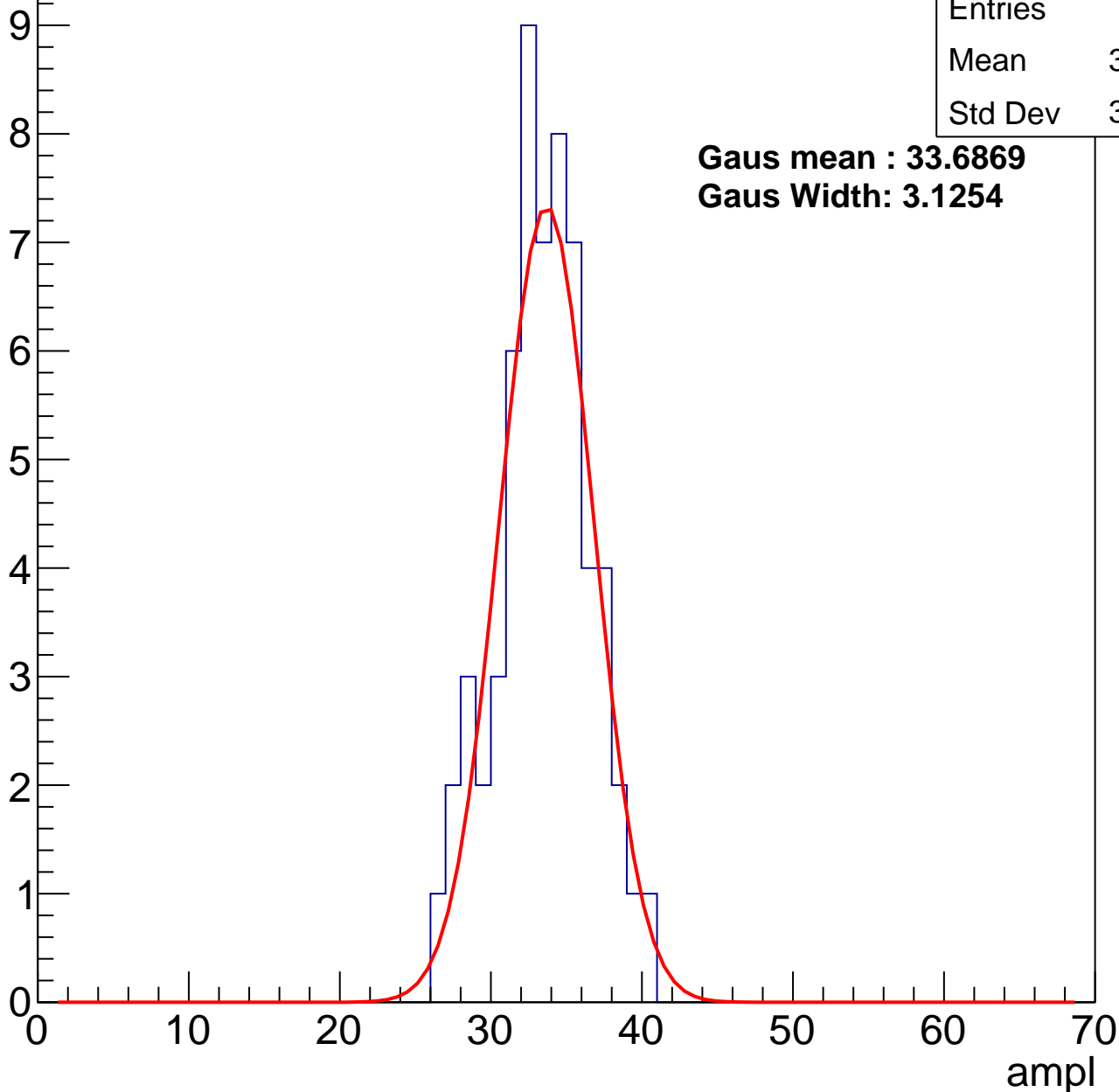
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	33.02
Std Dev	3.052

**Gaus mean : 33.6869**

**Gaus Width: 3.1254**



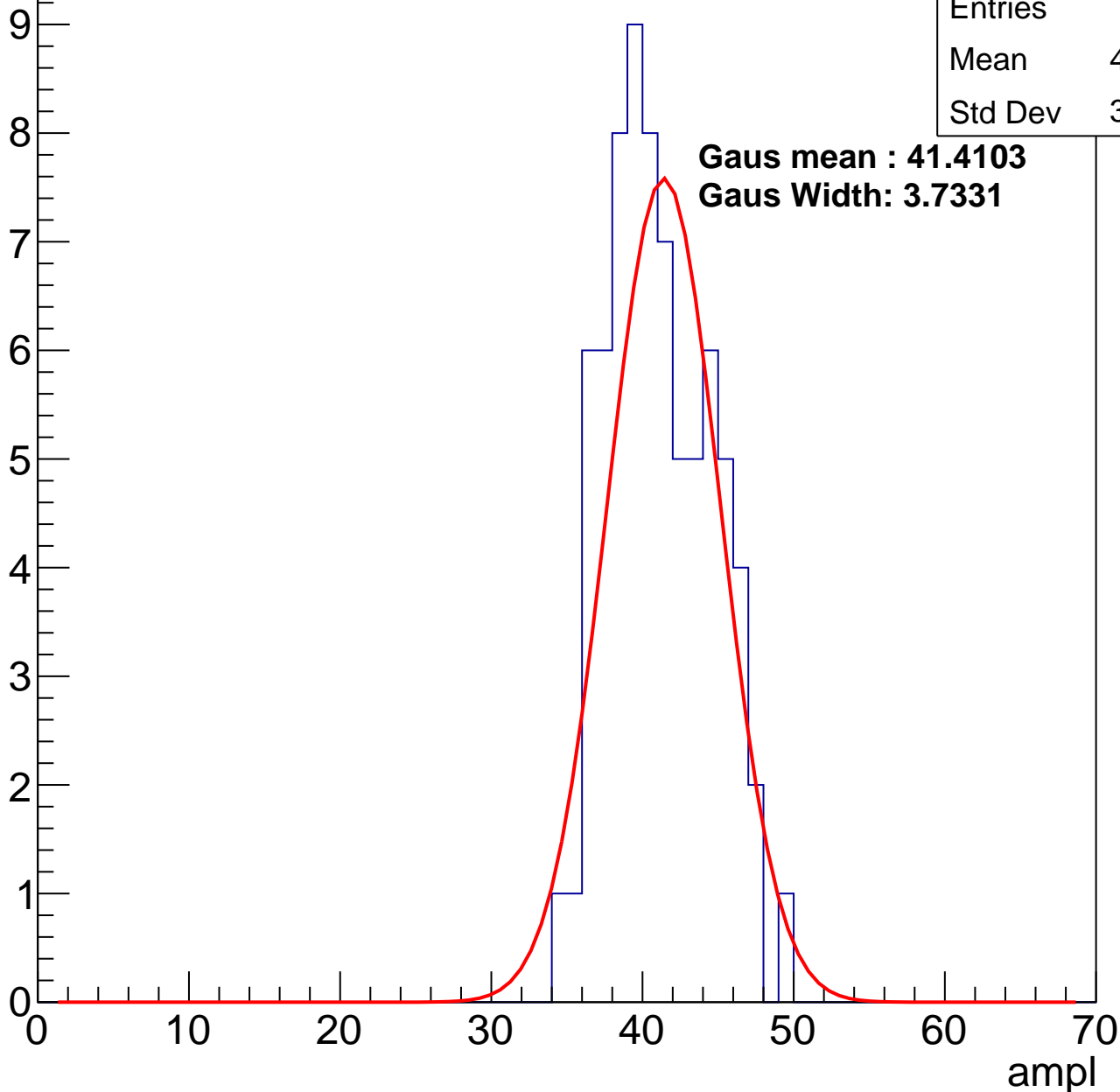
# B0L000S, U19-ch101, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	40.68
Std Dev	3.358

**Gaus mean : 41.4103**  
**Gaus Width: 3.7331**



# B0L000S, U19-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

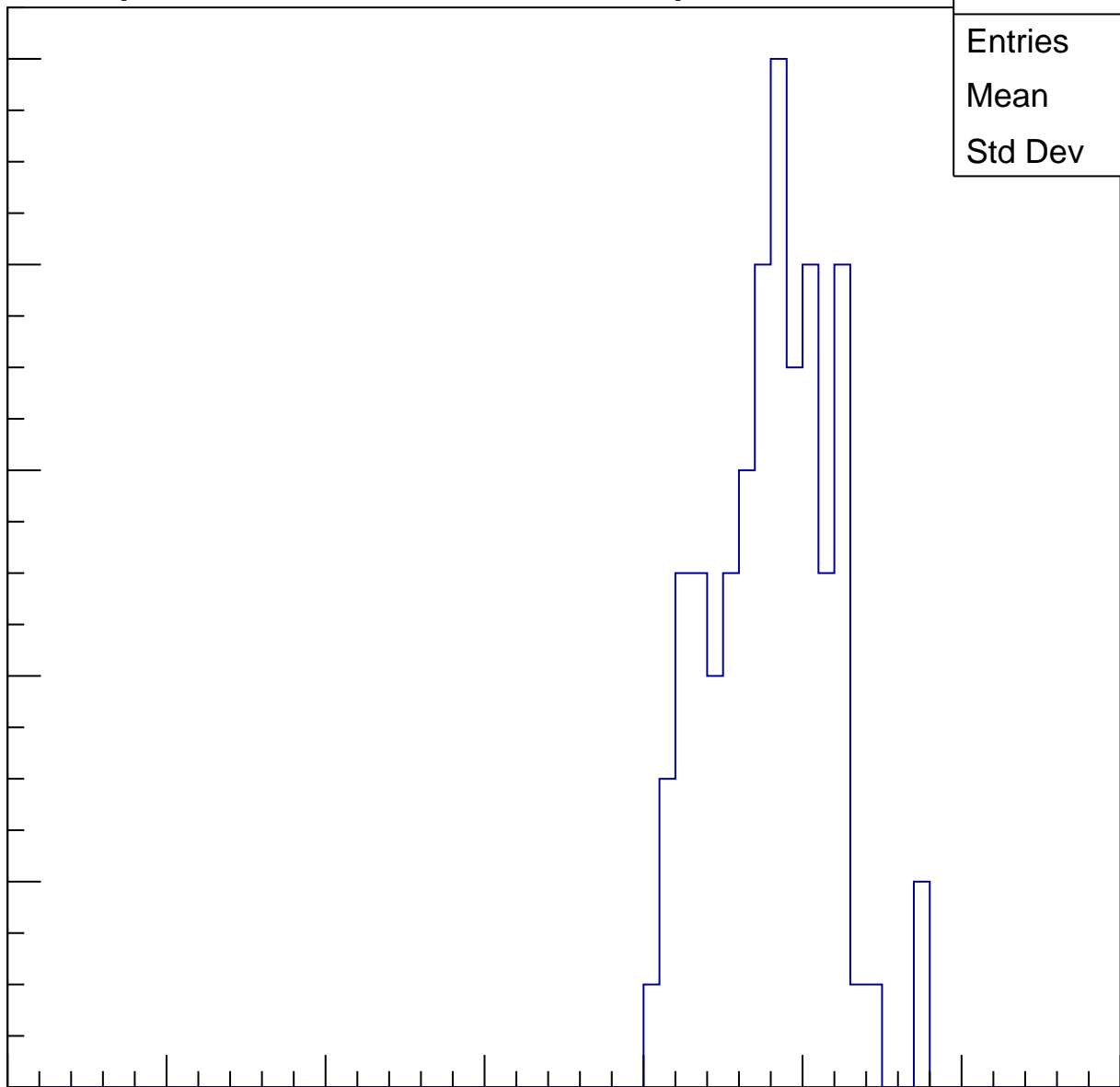
Entries	79
Mean	47.54
Std Dev	3.69

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch101, adc4

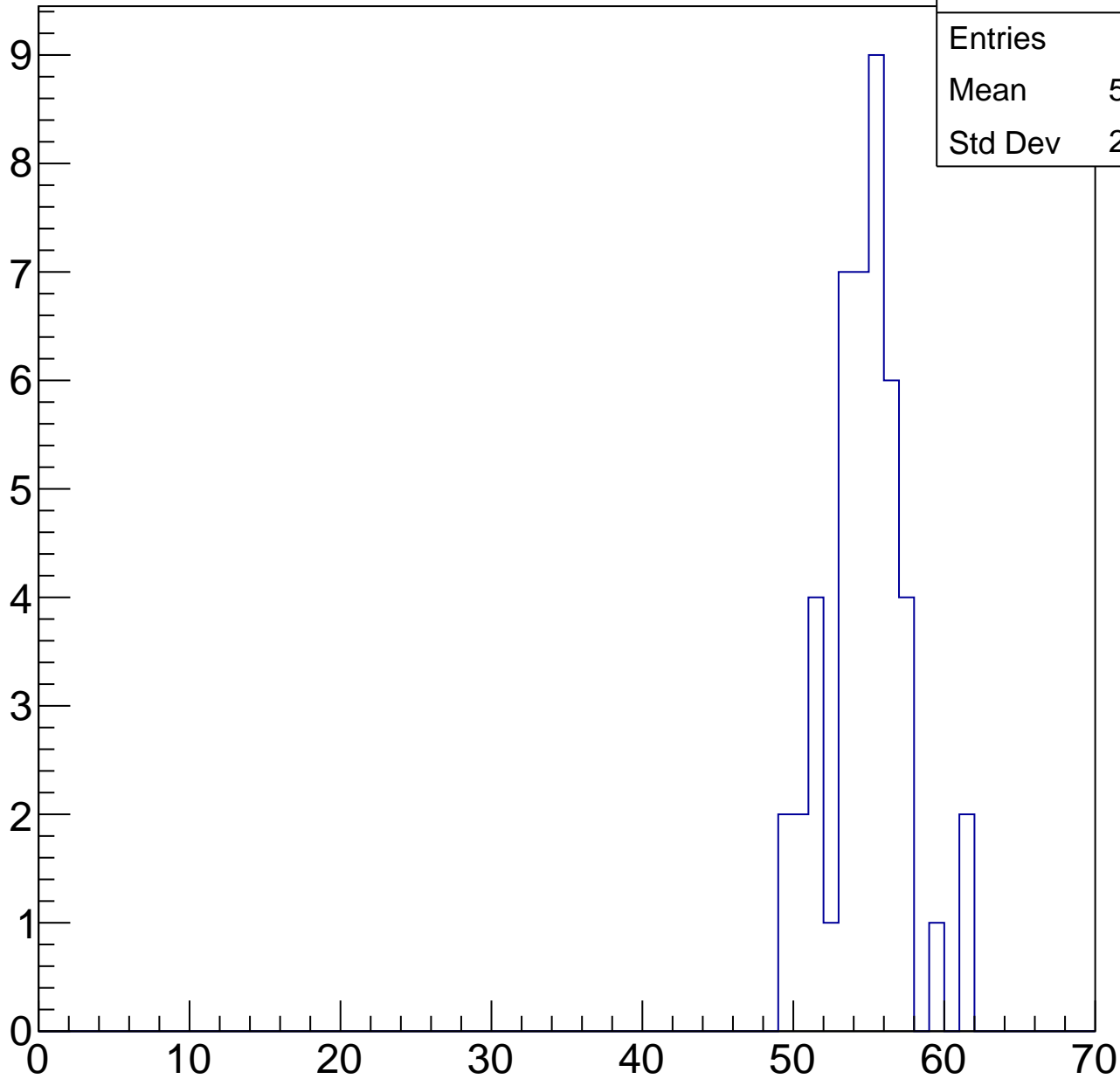
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	54.29
Std Dev	2.655

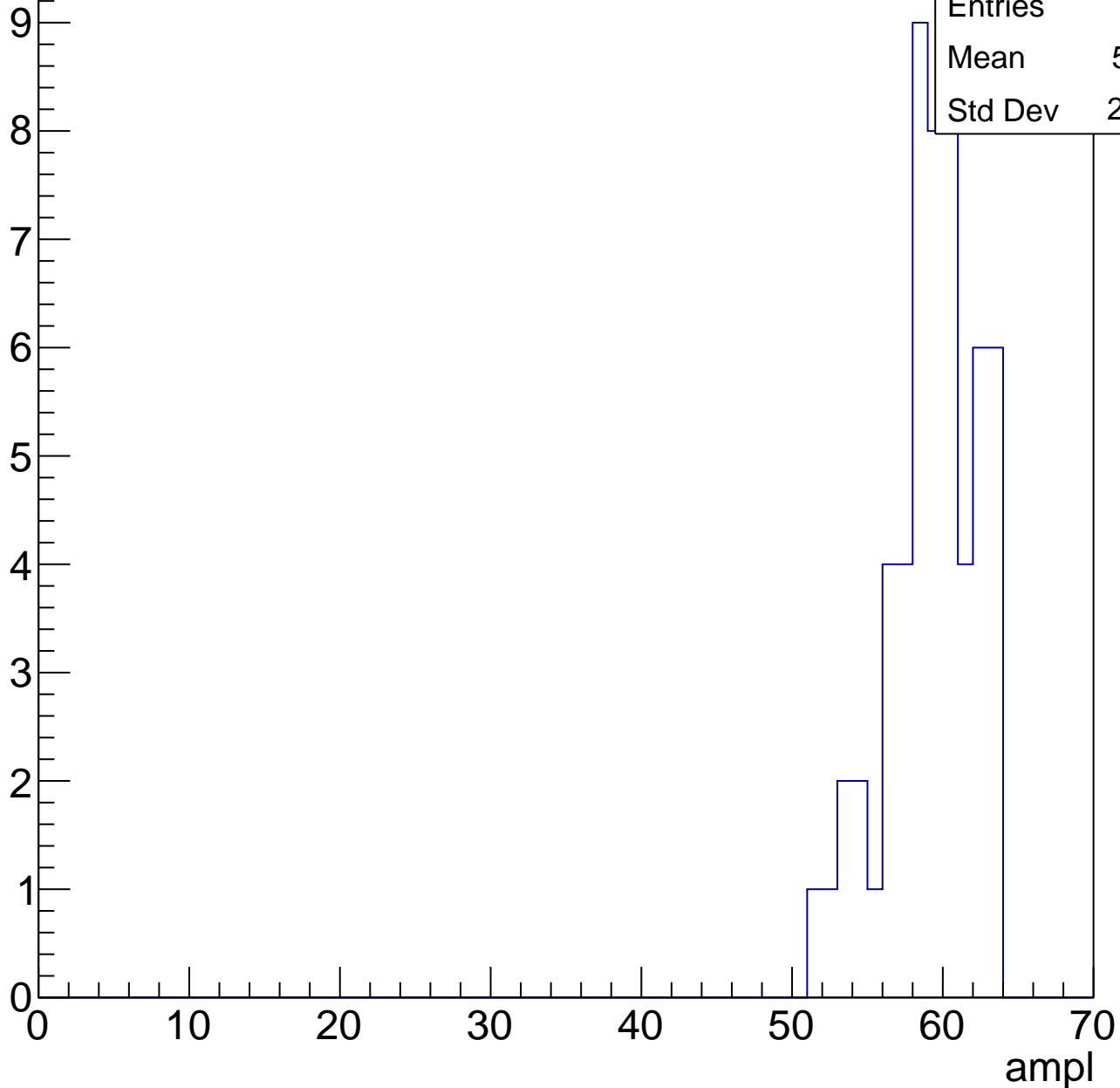
ampl



# B0L000S, U19-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

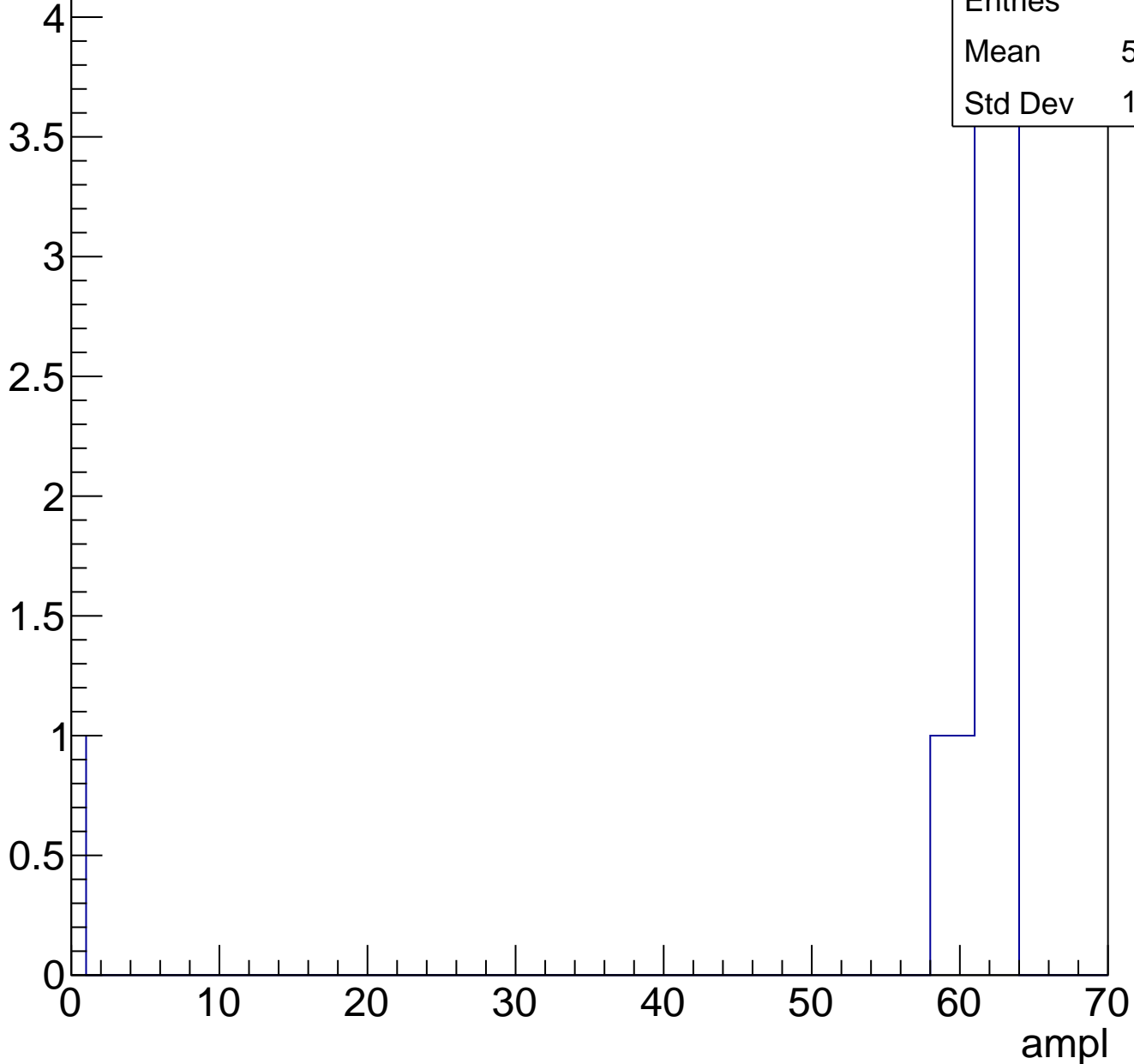
Entry



# B0L000S, U19-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch102, adc0

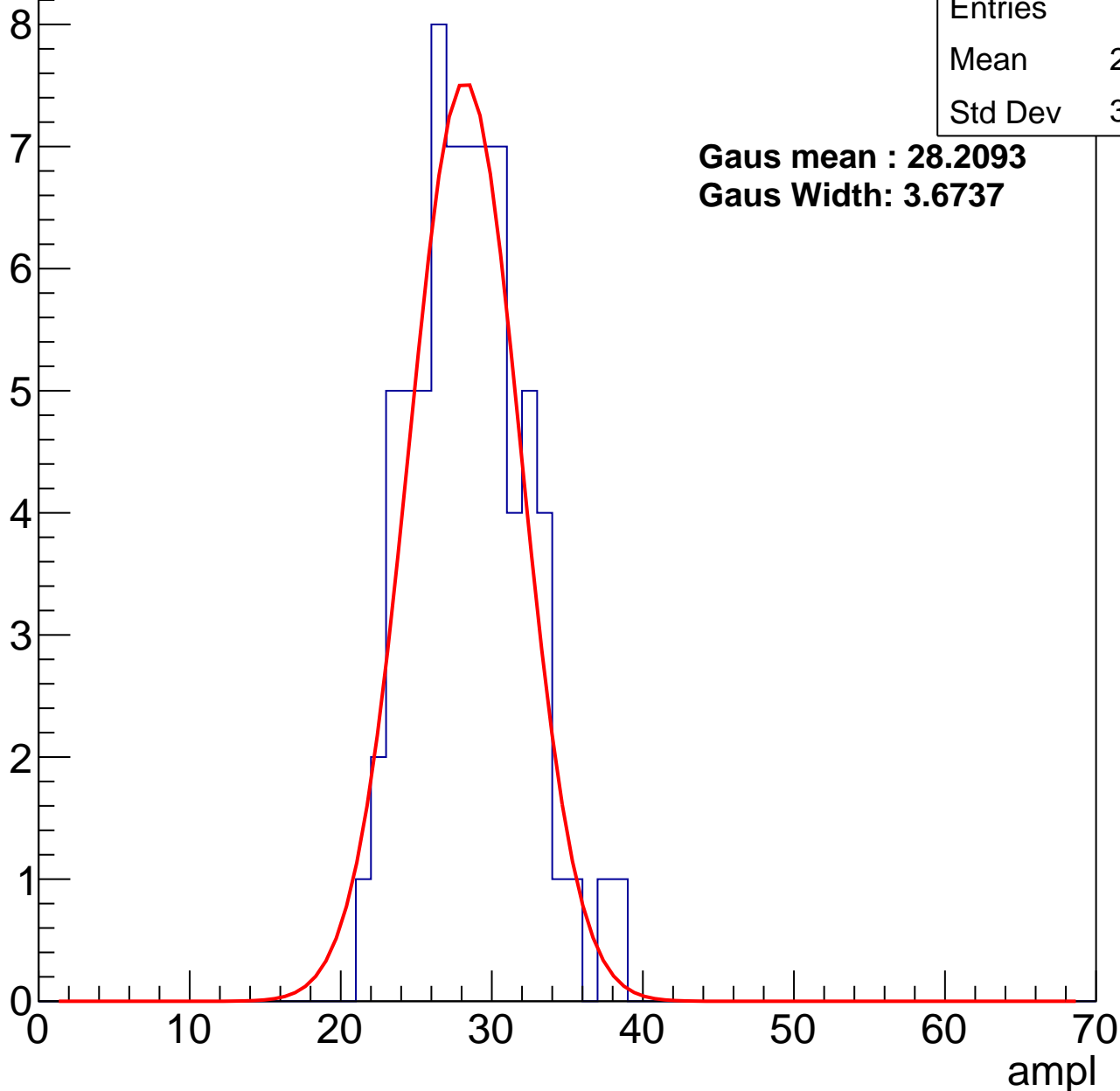
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	28.04
Std Dev	3.613

**Gaus mean : 28.2093**

**Gaus Width: 3.6737**



# B0L000S, U19-ch102, adc1

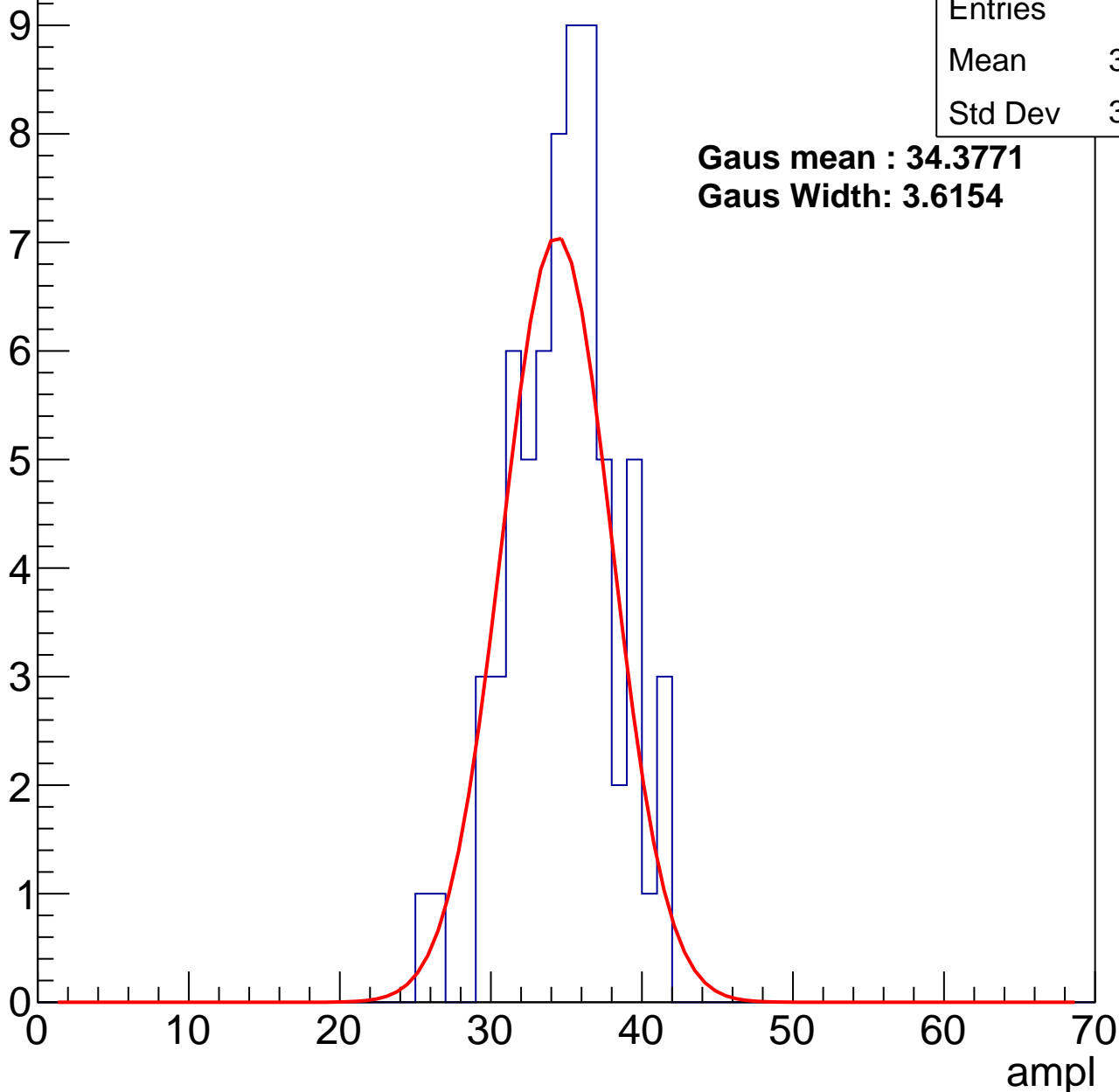
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	34.36
Std Dev	3.389

**Gaus mean : 34.3771**

**Gaus Width: 3.6154**



# B0L000S, U19-ch102, adc2

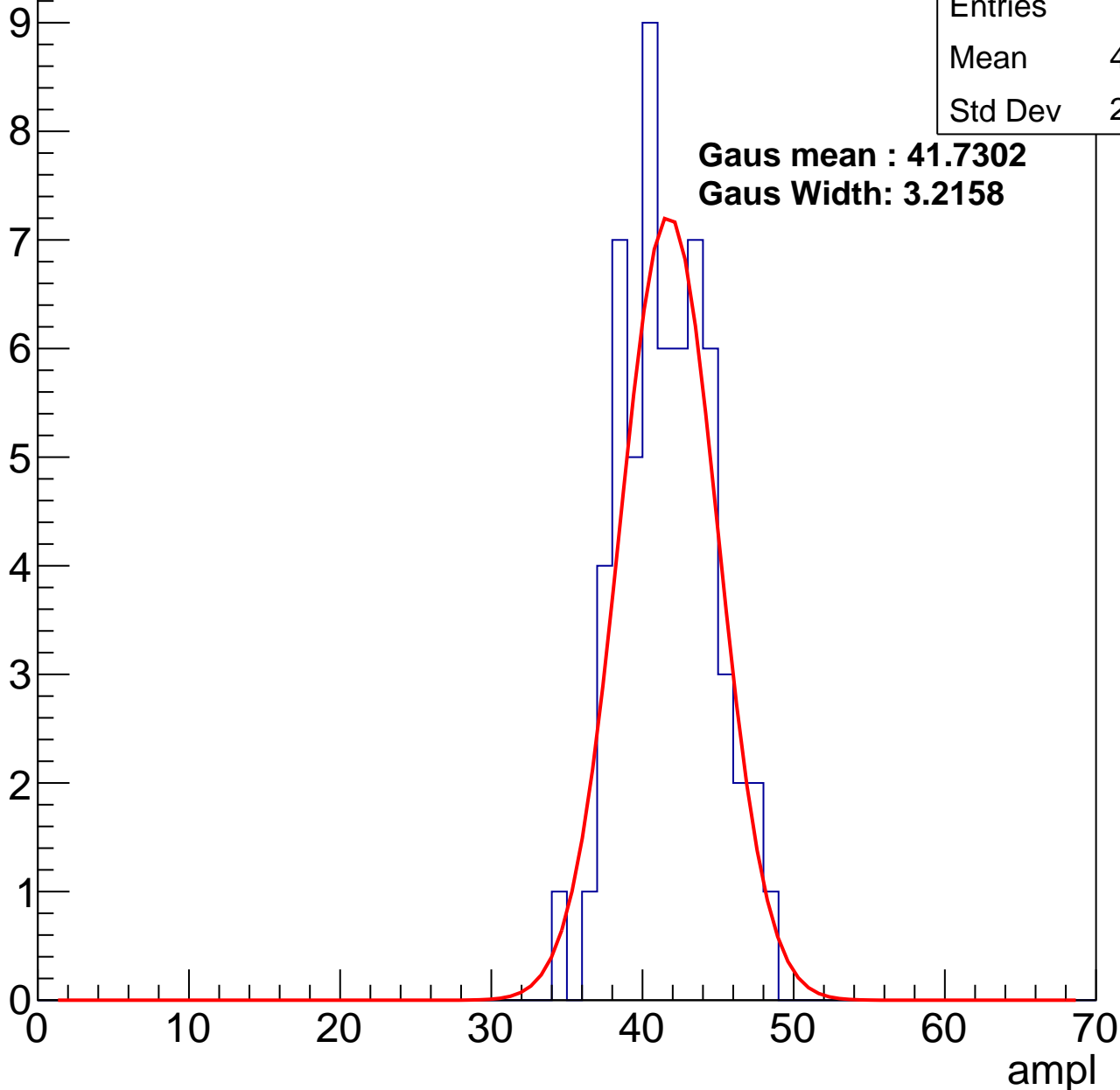
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.18
Std Dev	2.986

**Gaus mean : 41.7302**

**Gaus Width: 3.2158**

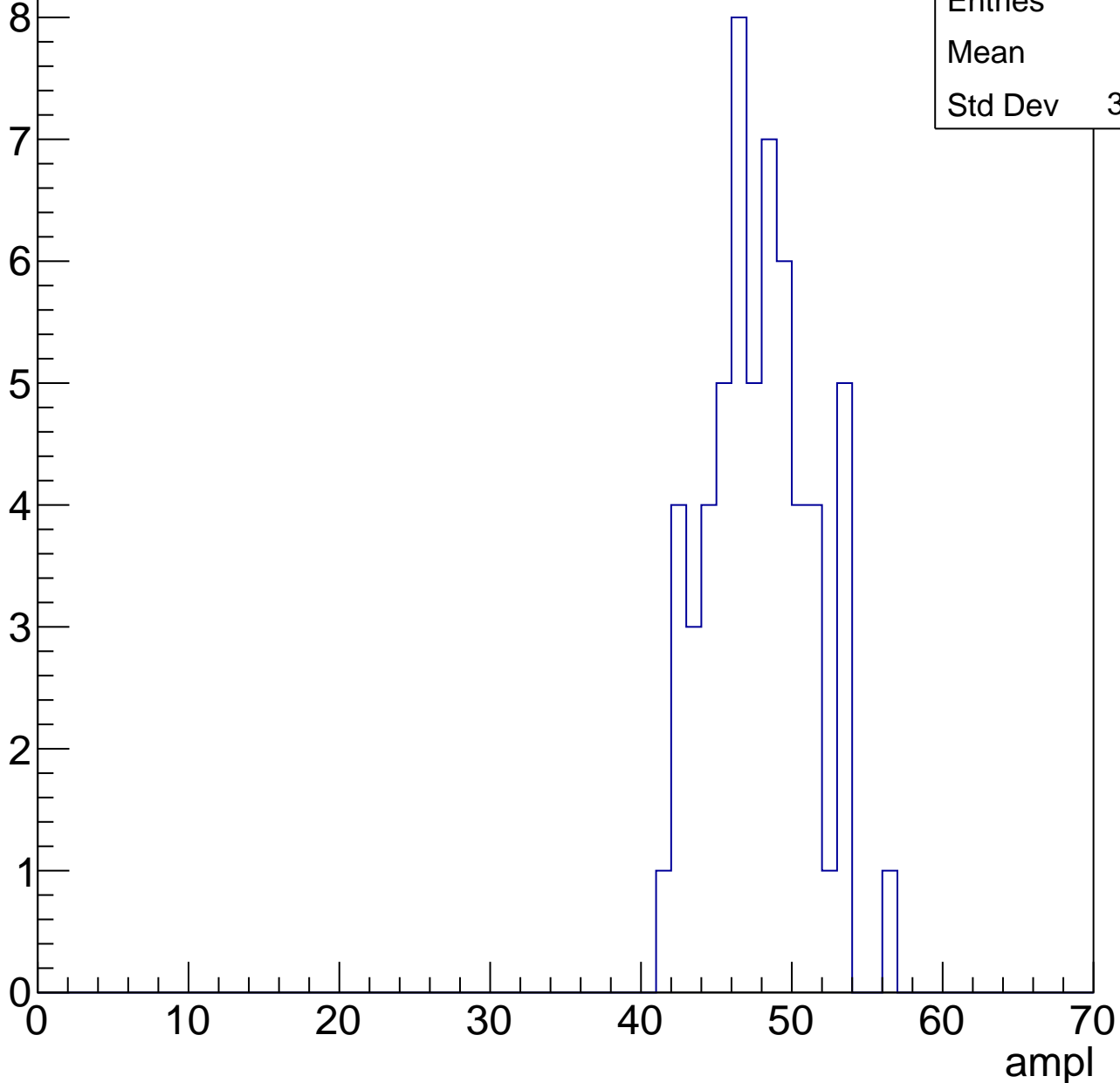


# B0L000S, U19-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	47.4
Std Dev	3.363

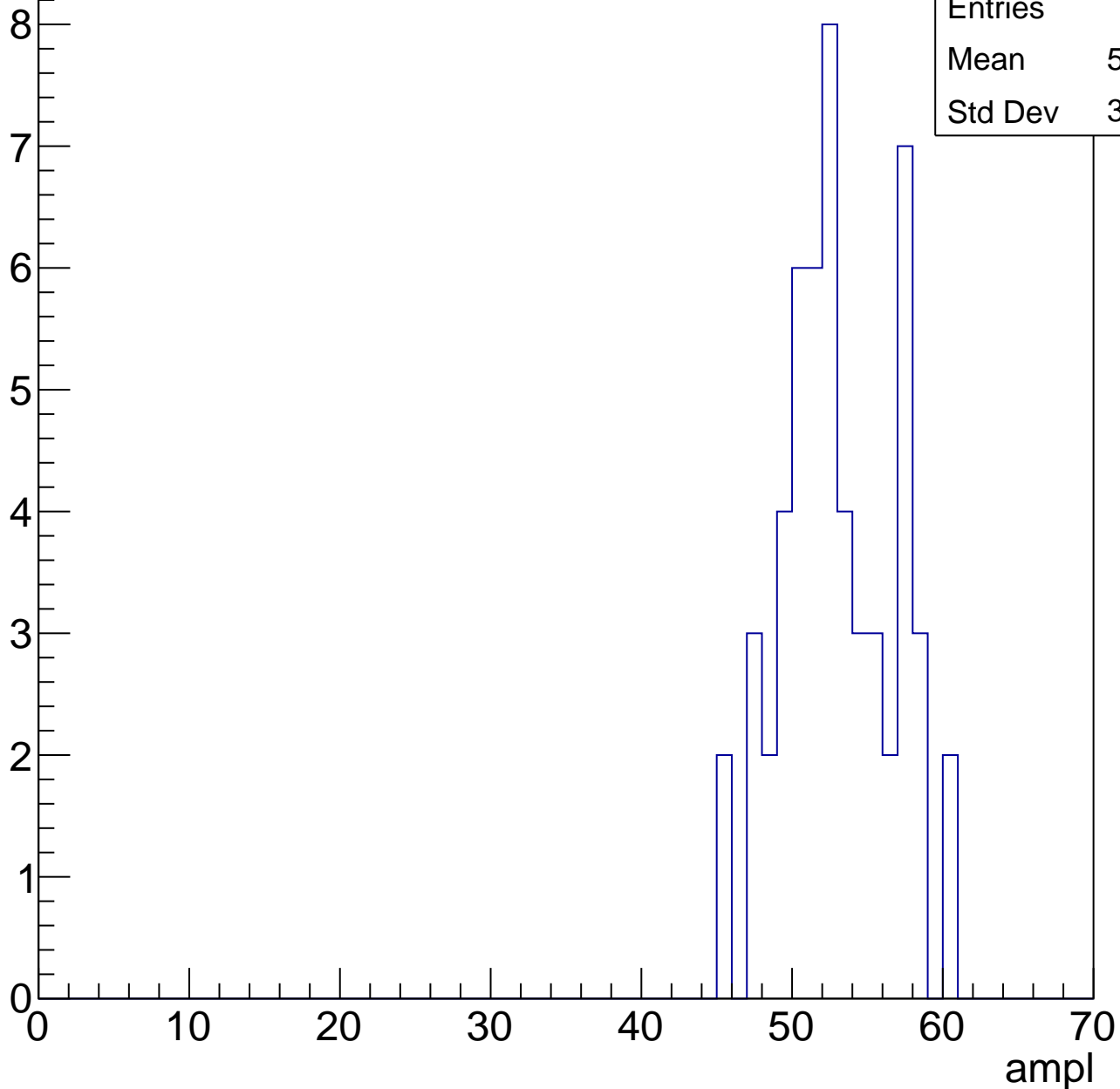


# B0L000S, U19-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	52.53
Std Dev	3.672

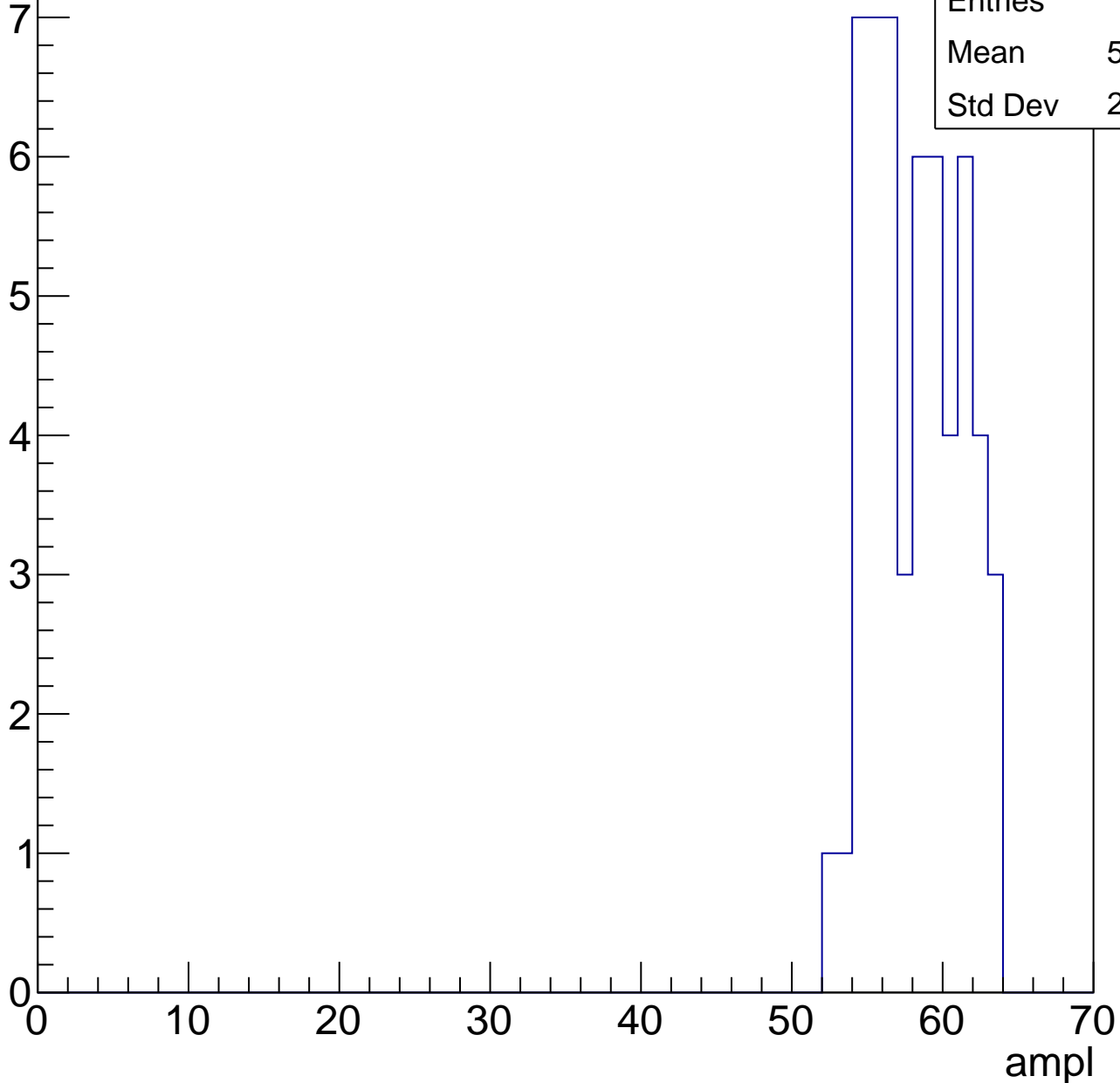


# B0L000S, U19-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

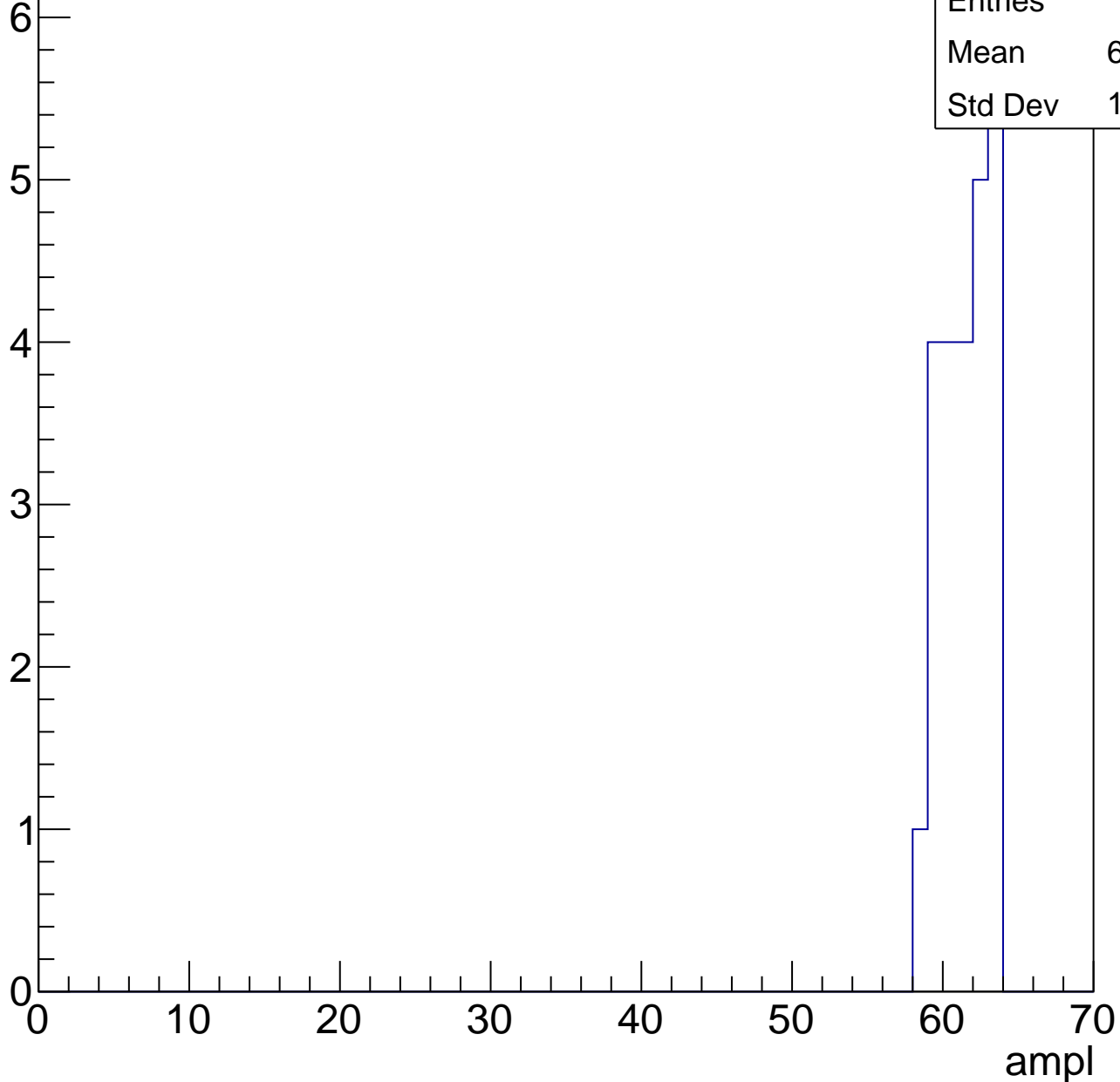
Entries	55
Mean	57.75
Std Dev	2.949



# B0L000S, U19-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

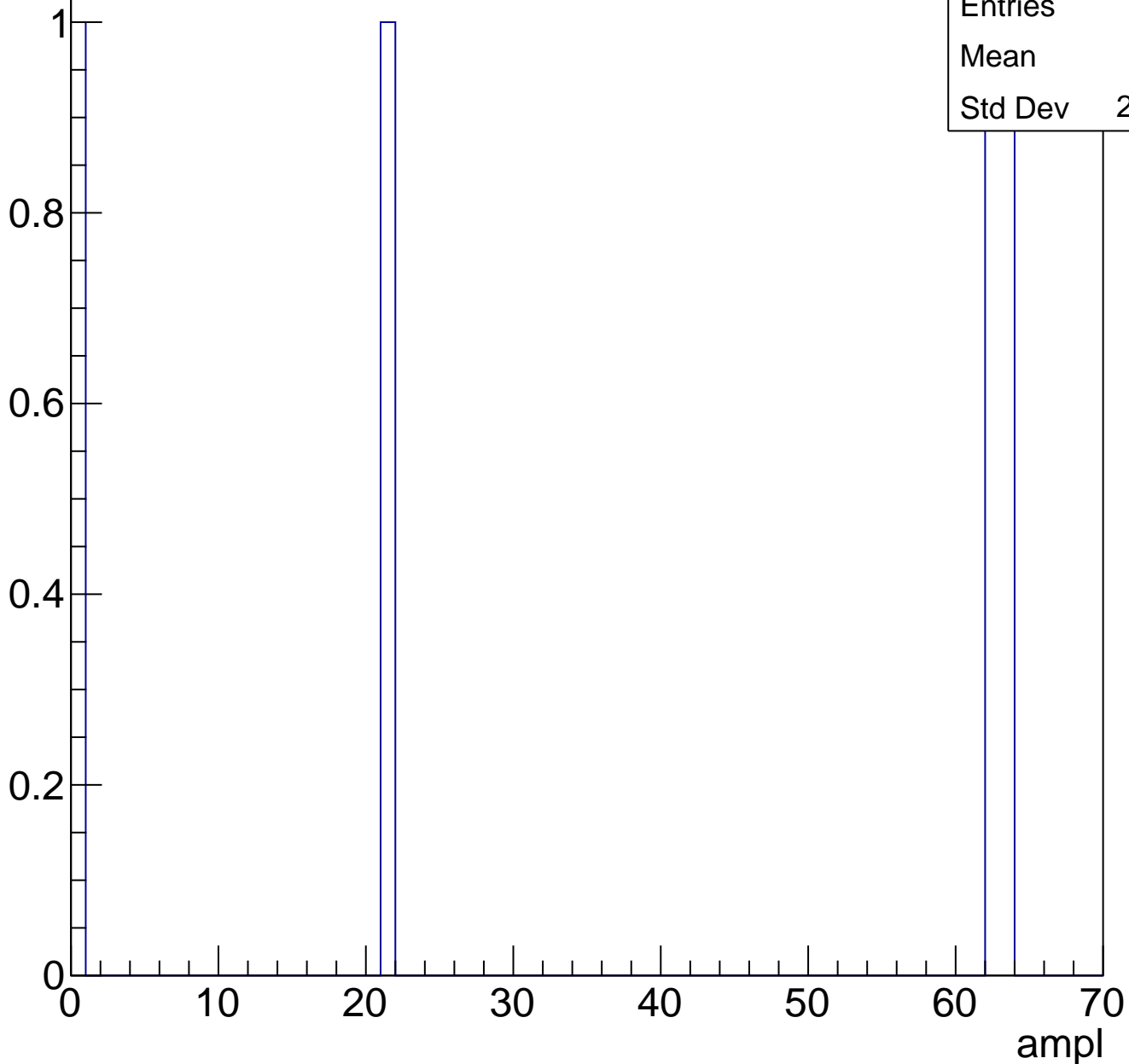




# B0L000S, U19-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch103, adc0

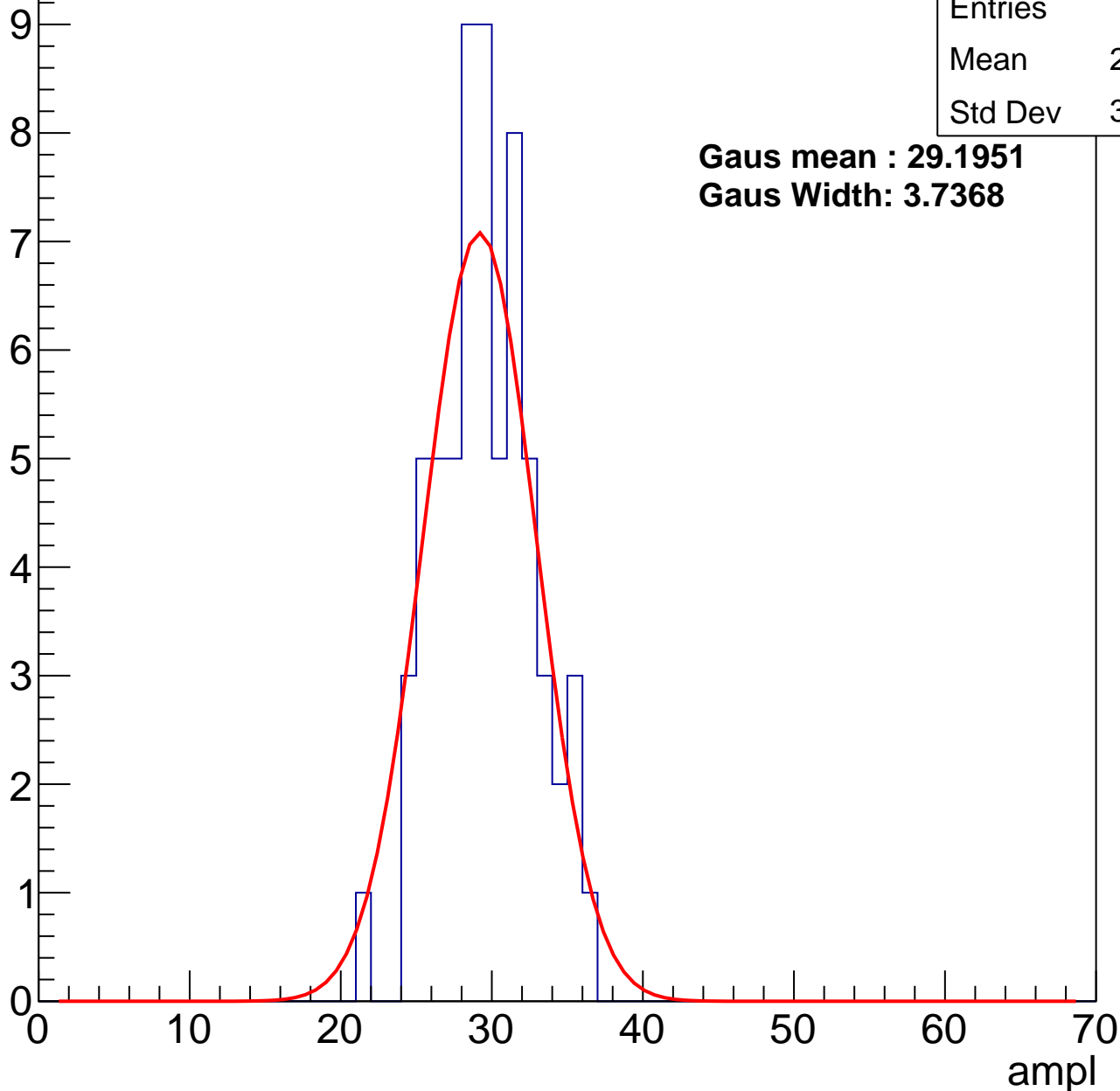
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.09
Std Dev	3.136

**Gaus mean : 29.1951**

**Gaus Width: 3.7368**



# B0L000S, U19-ch103, adc1

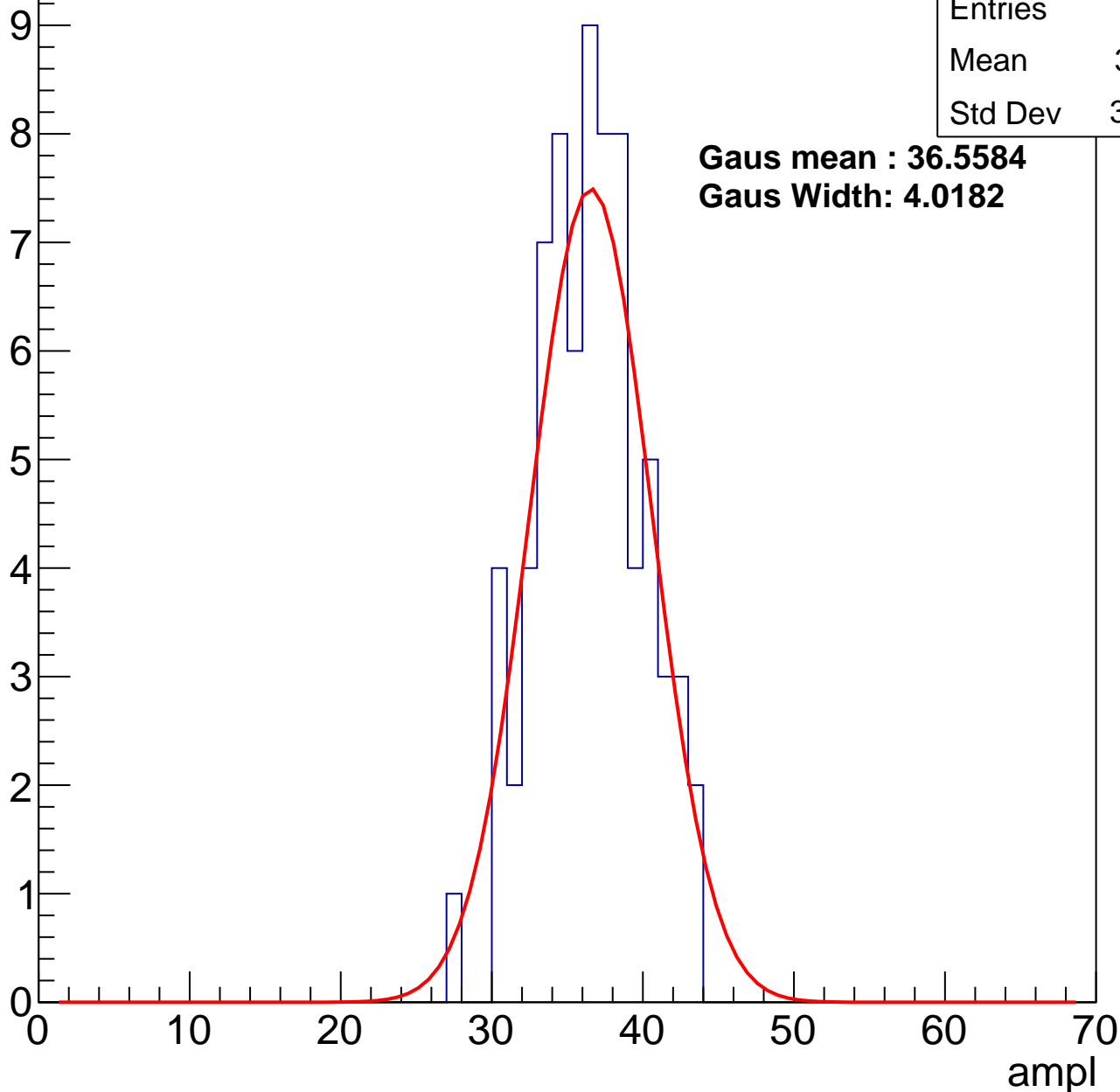
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.01
Std Dev	3.443

**Gaus mean : 36.5584**

**Gaus Width: 4.0182**

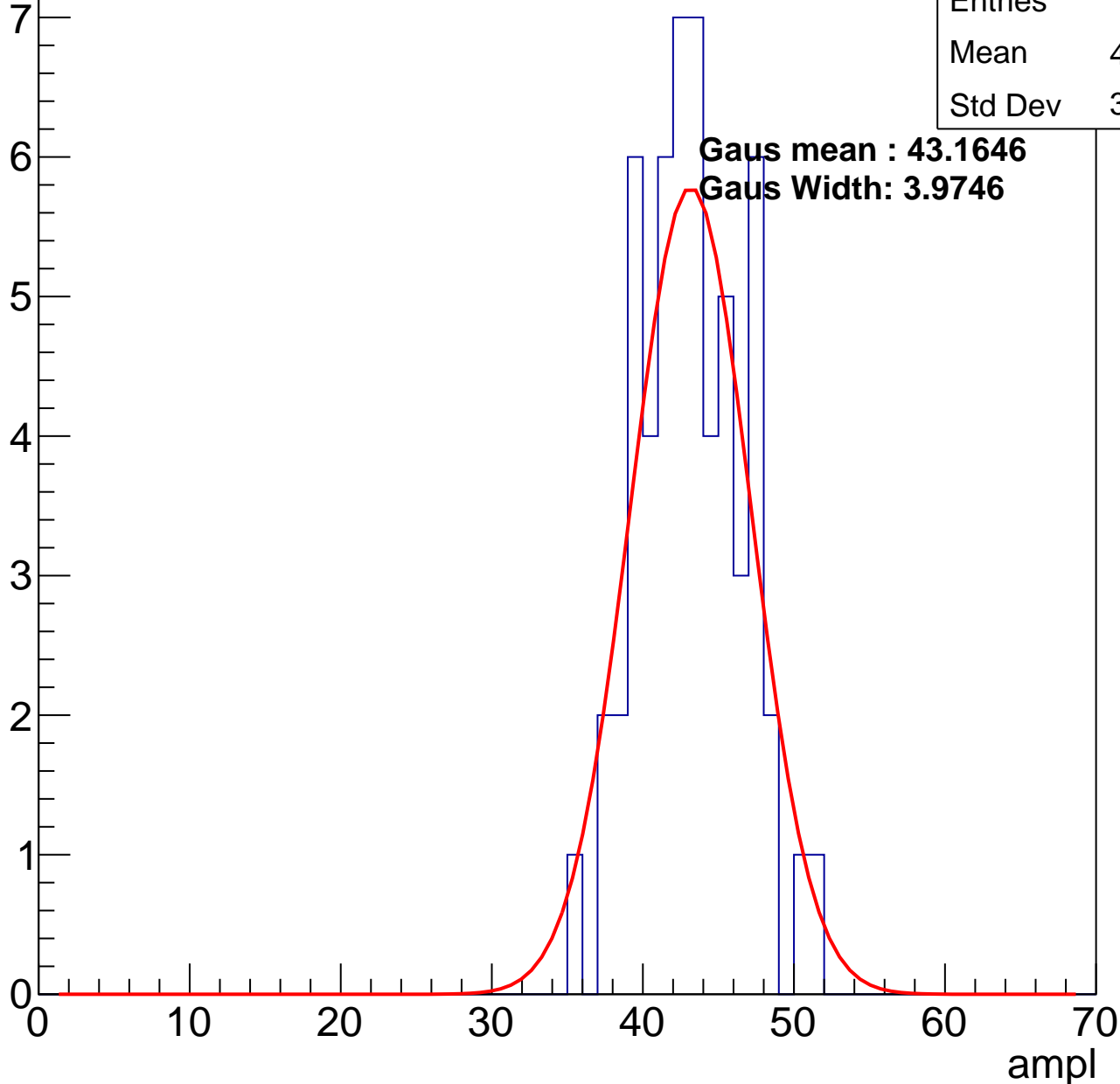


# B0L000S, U19-ch103, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.77
Std Dev	3.393

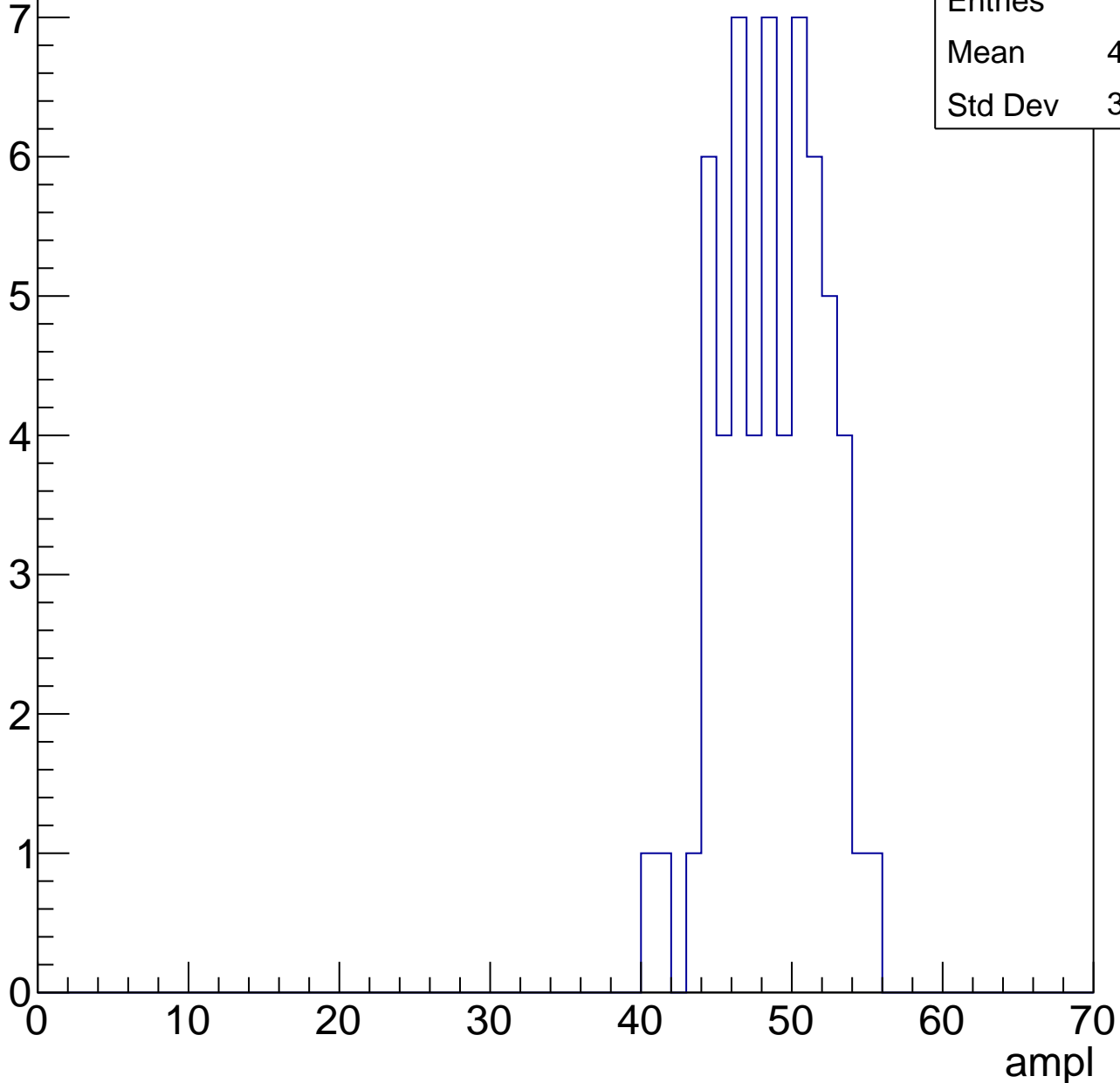


# B0L000S, U19-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	48.25
Std Dev	3.317



# B0L000S, U19-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

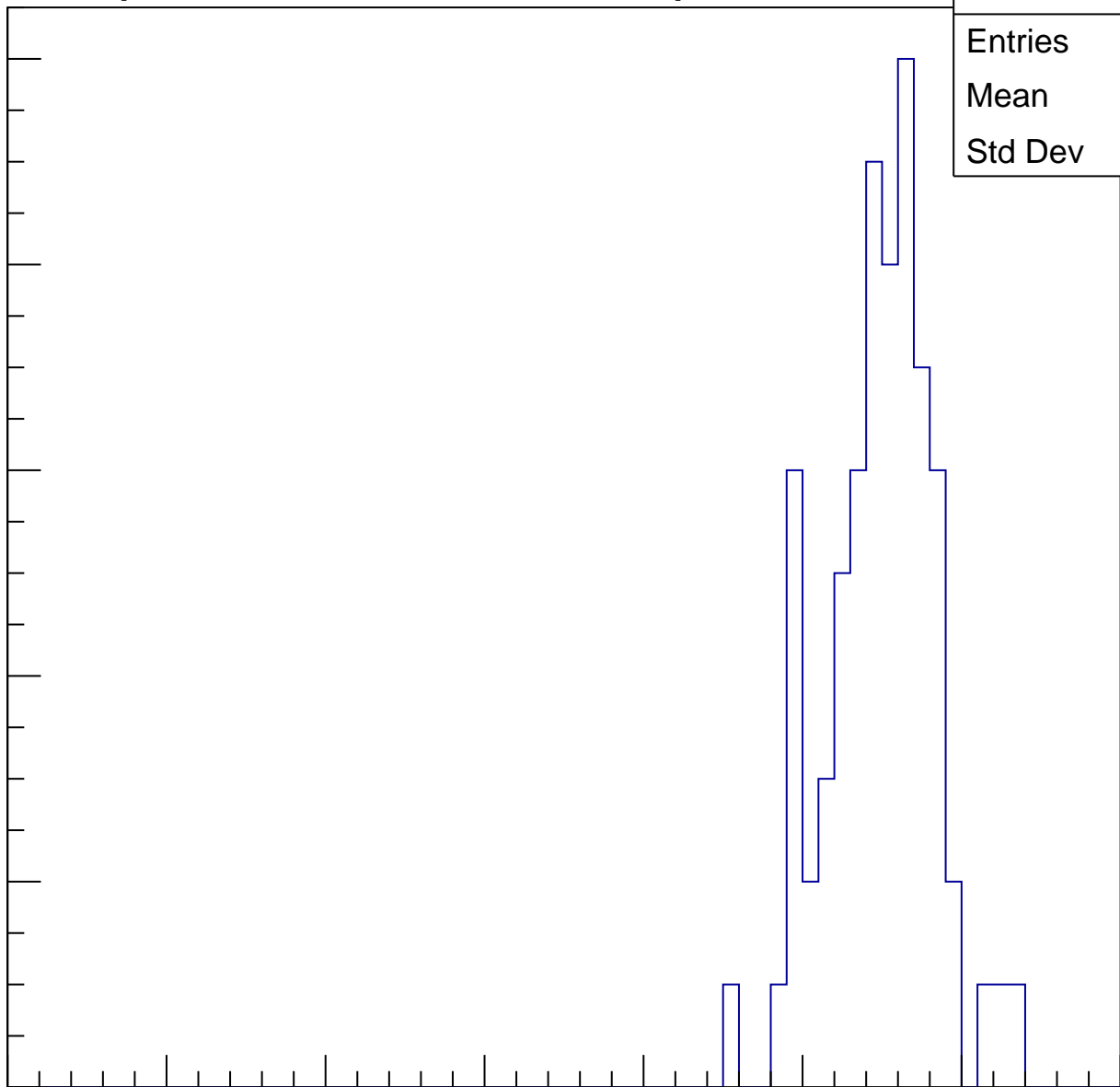
Entries	69
Mean	54.42
Std Dev	3.385

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

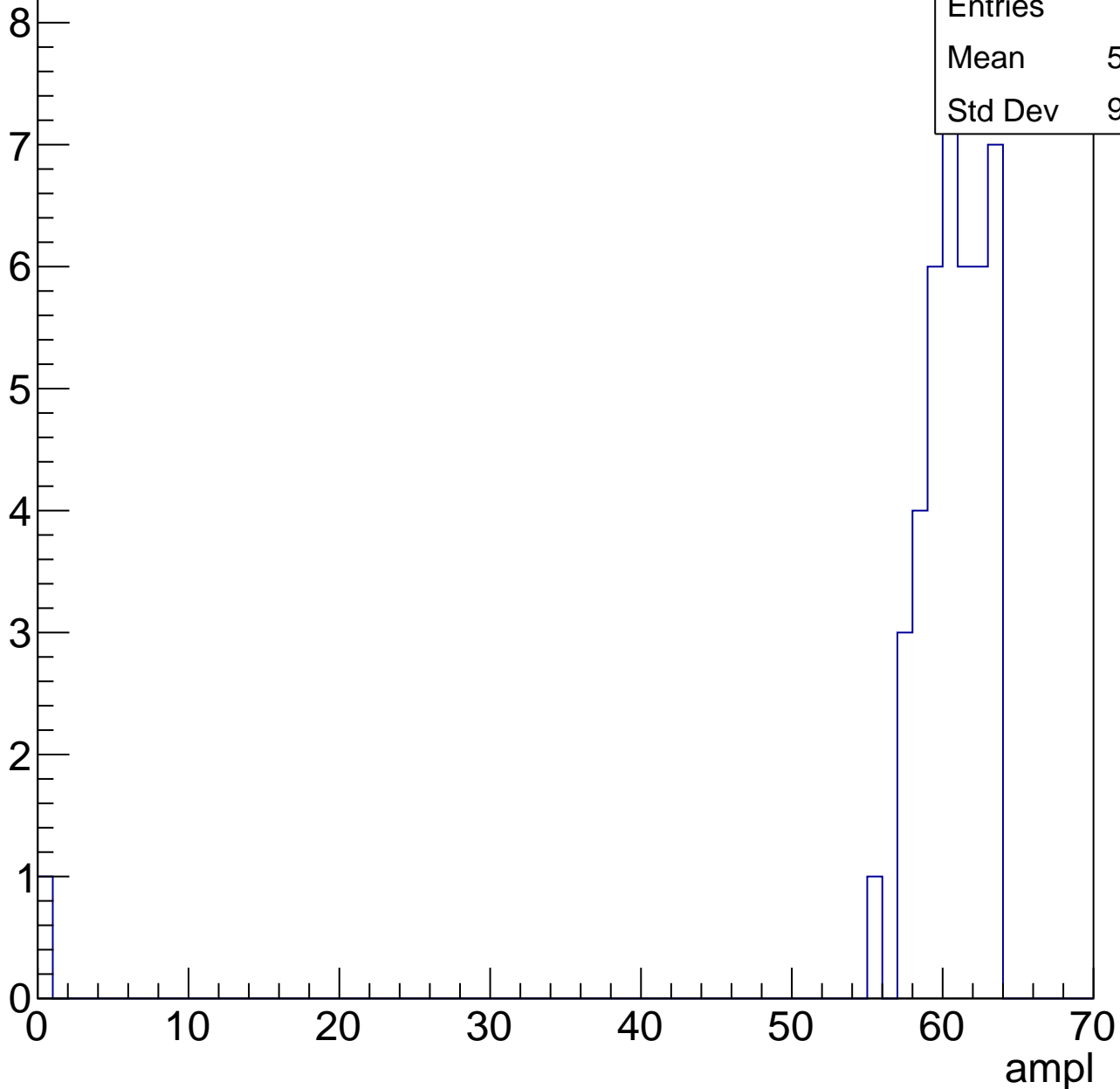


# B0L000S, U19-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

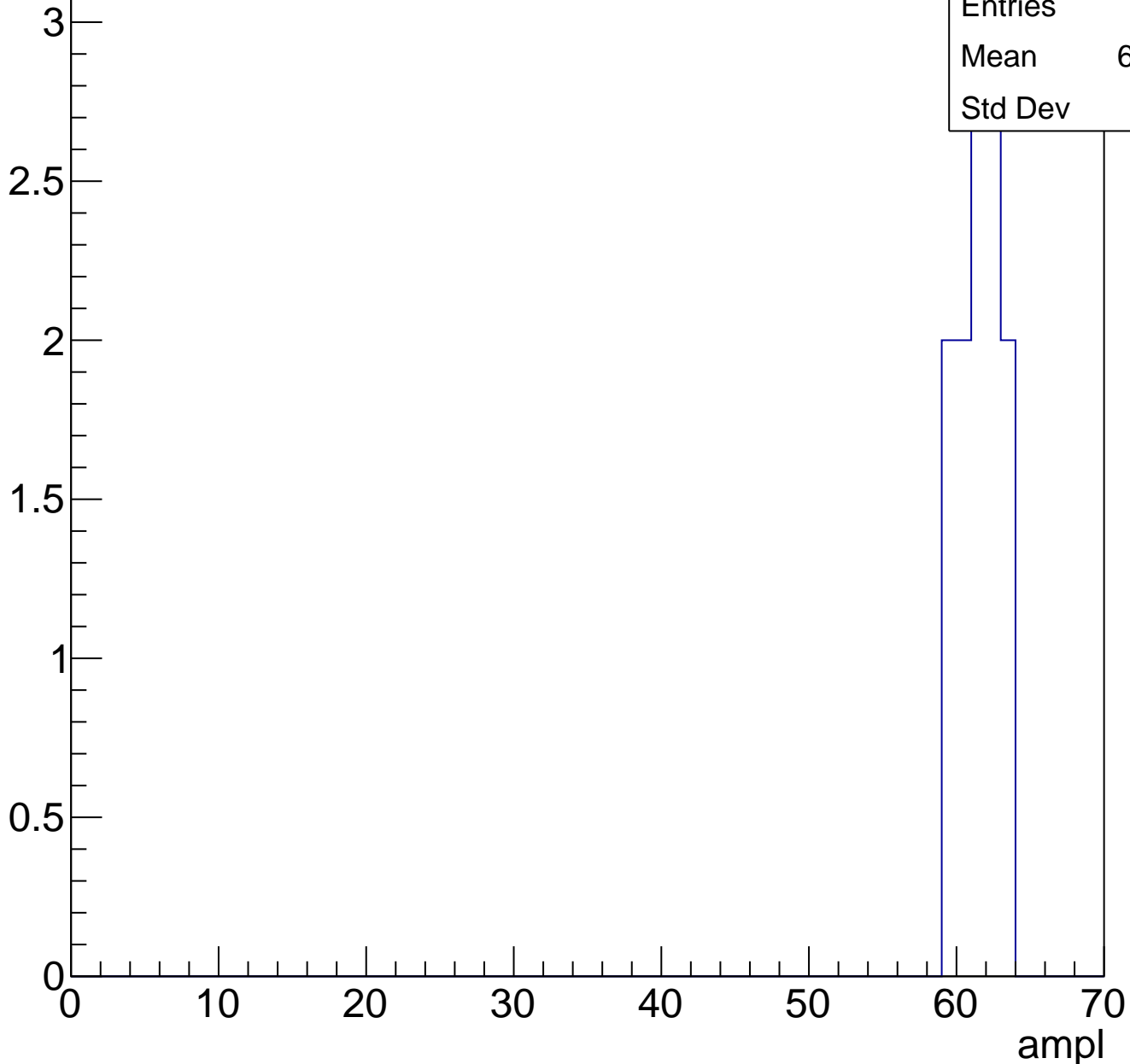
Entries	42
Mean	58.83
Std Dev	9.398



# B0L000S, U19-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



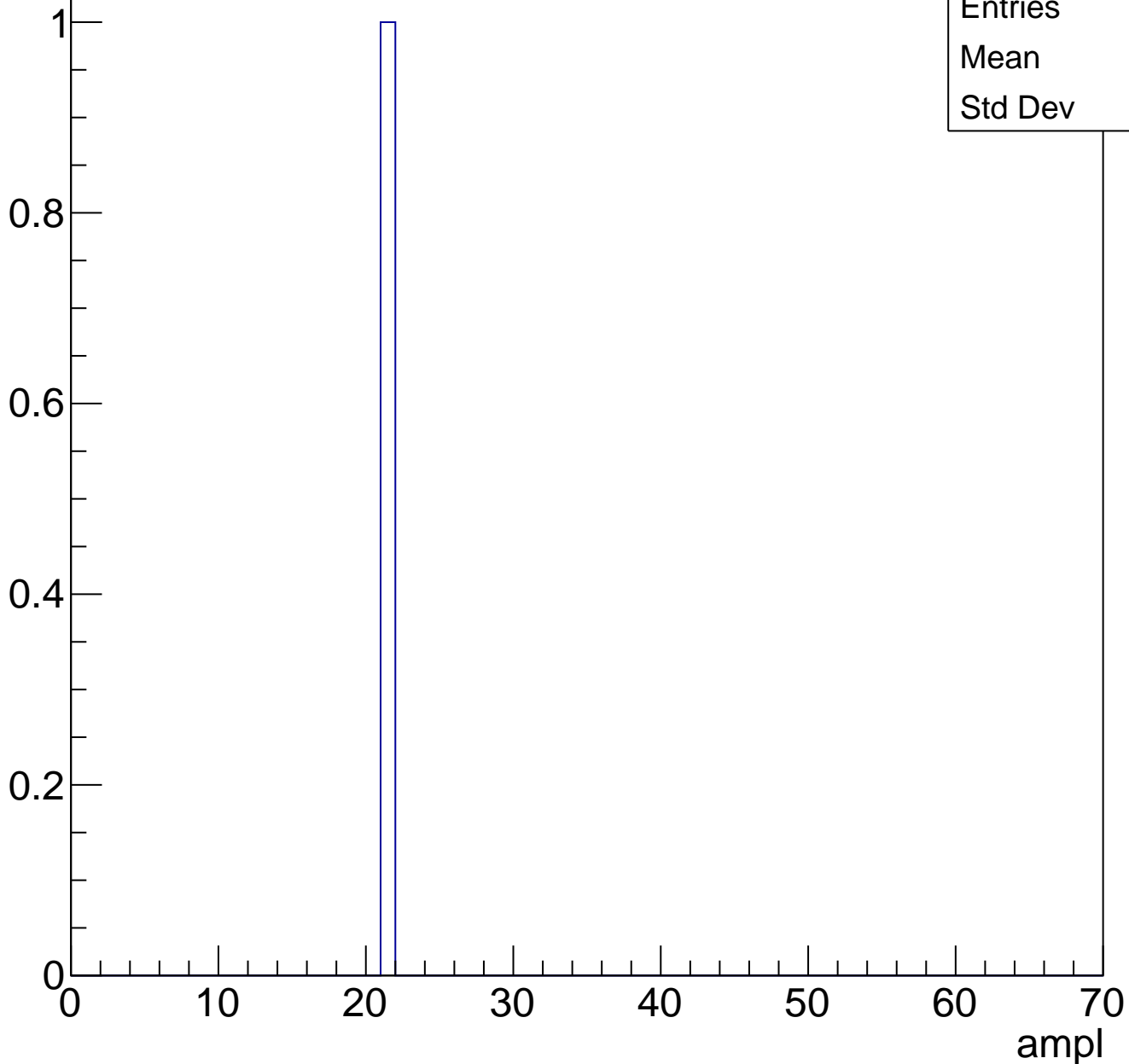
Entries	12
Mean	61.08
Std Dev	1.32



# B0L000S, U19-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch104, adc0

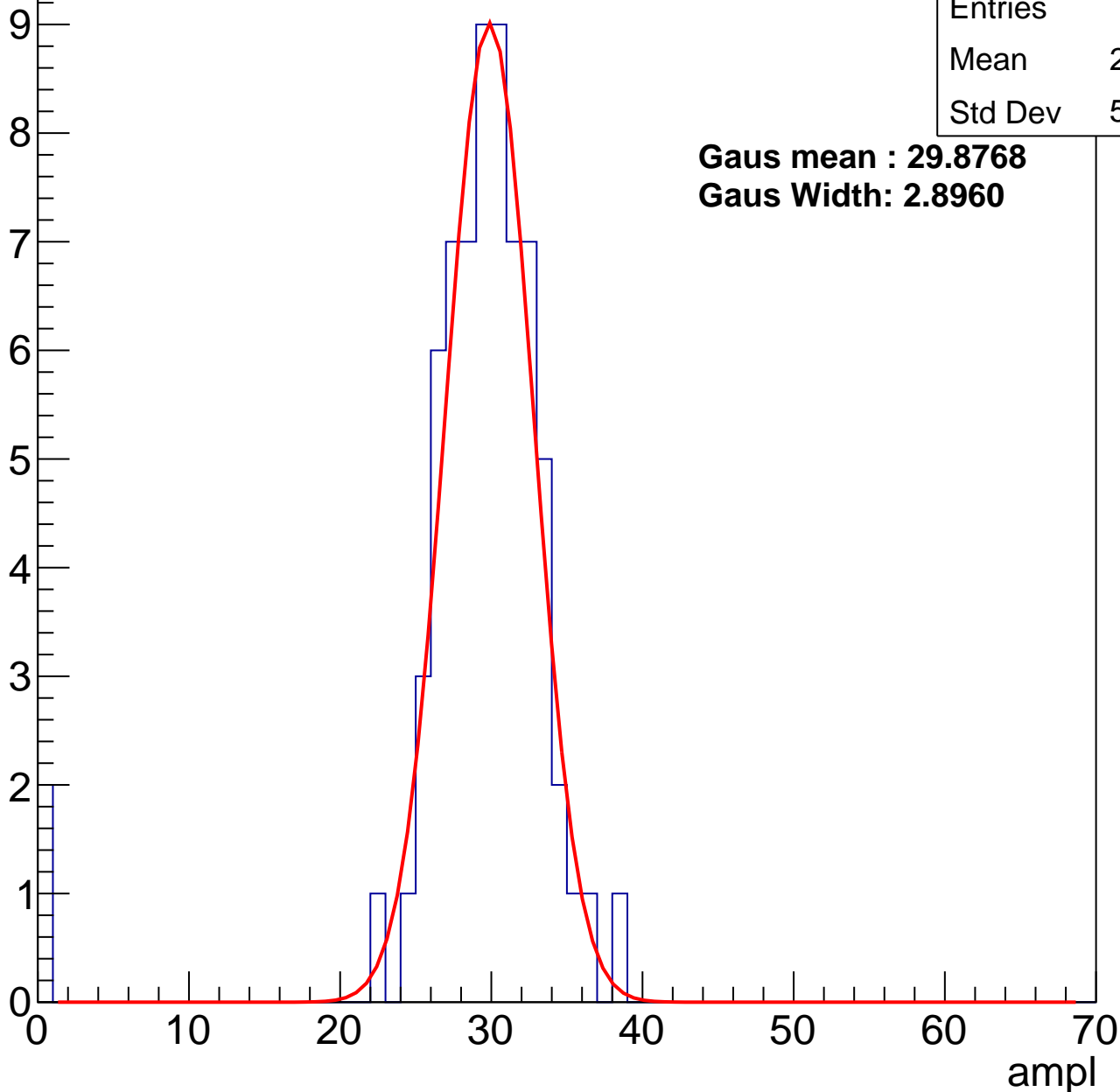
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	28.64
Std Dev	5.748

**Gaus mean : 29.8768**

**Gaus Width: 2.8960**



# B0L000S, U19-ch104, adc1

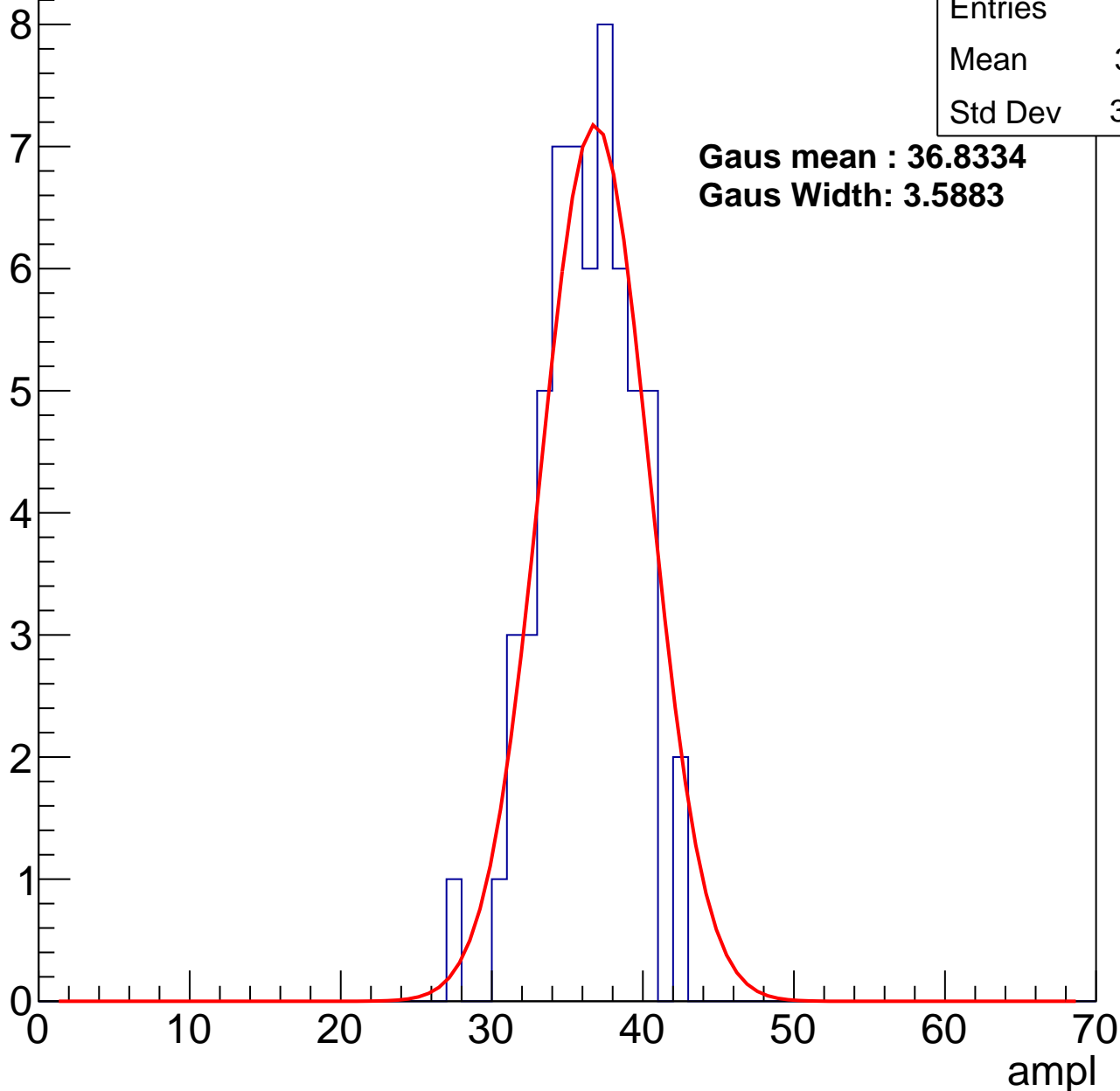
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	35.81
Std Dev	3.045

**Gaus mean : 36.8334**

**Gaus Width: 3.5883**



# B0L000S, U19-ch104, adc2

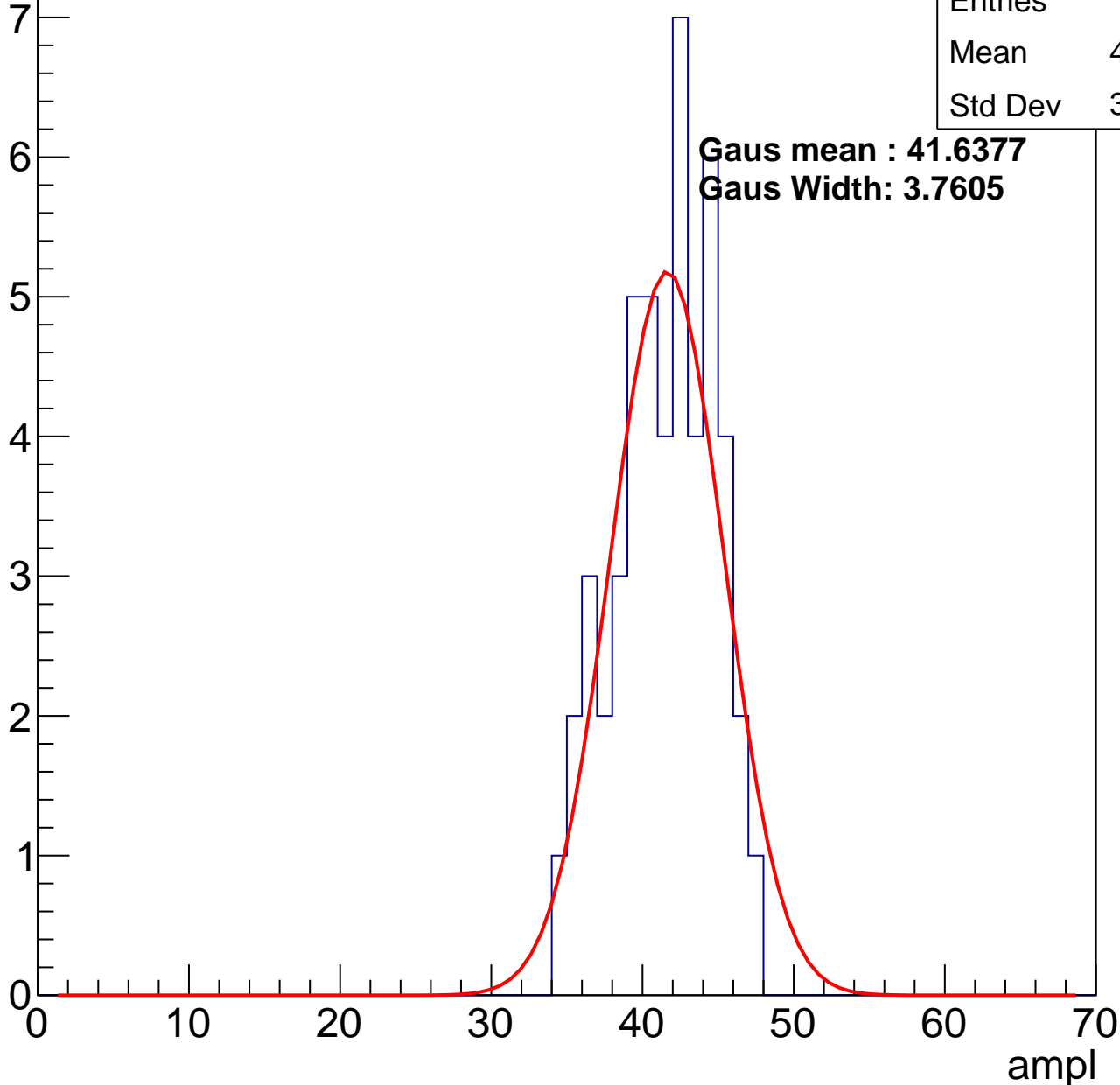
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	40.98
Std Dev	3.217

**Gaus mean : 41.6377**

**Gaus Width: 3.7605**

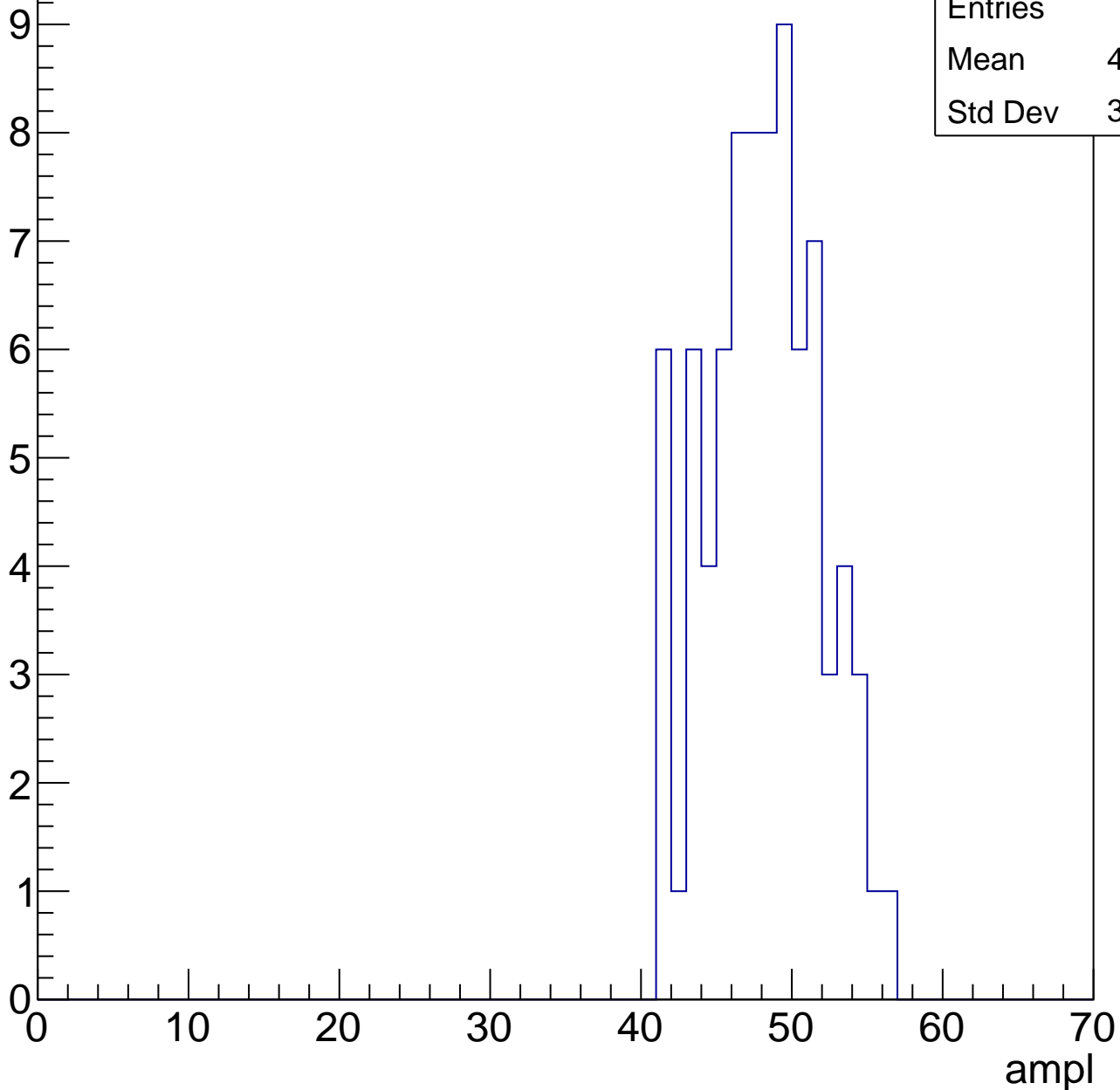


# B0L000S, U19-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	47.64
Std Dev	3.673

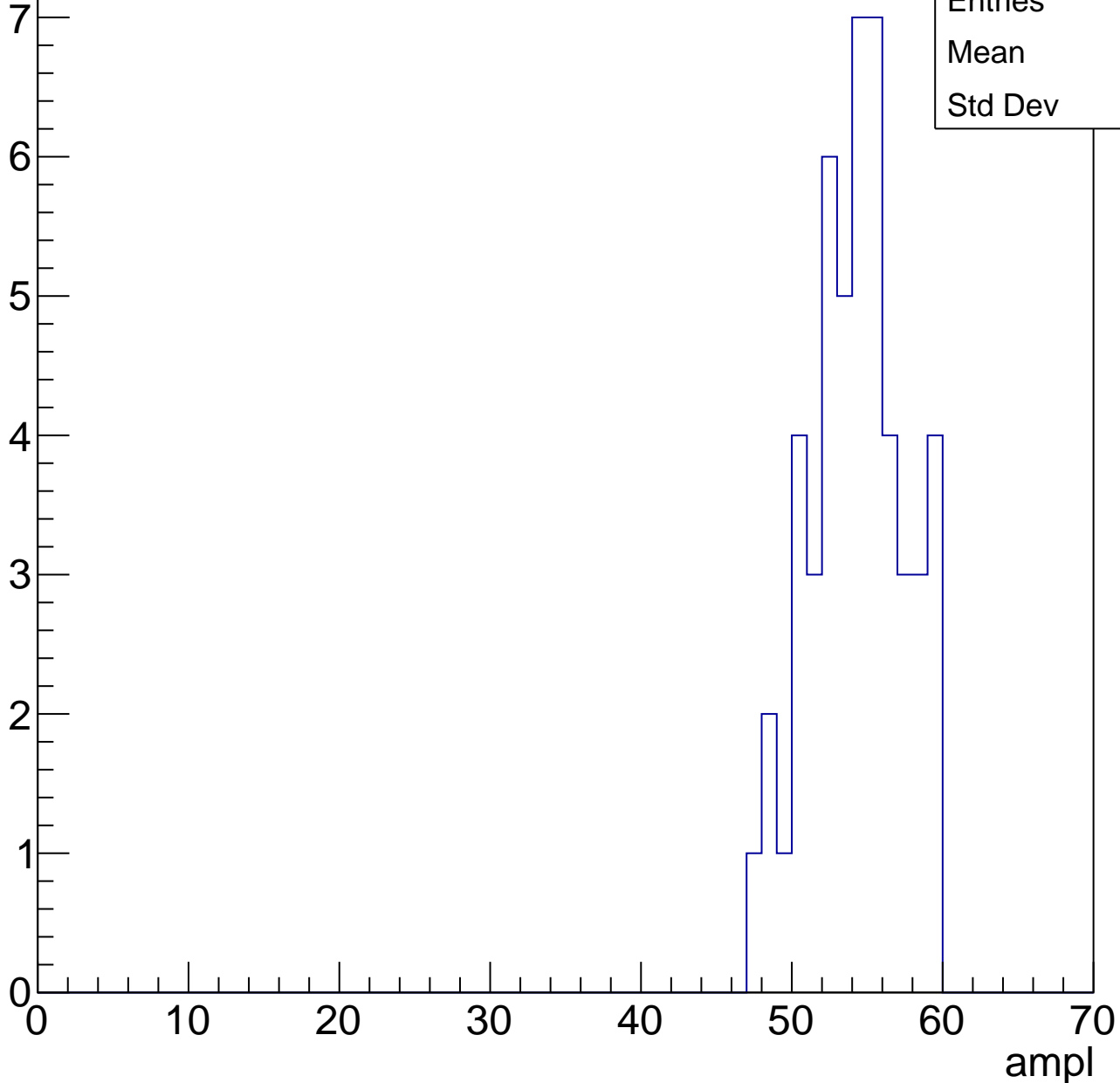


# B0L000S, U19-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	53.8
Std Dev	3.04

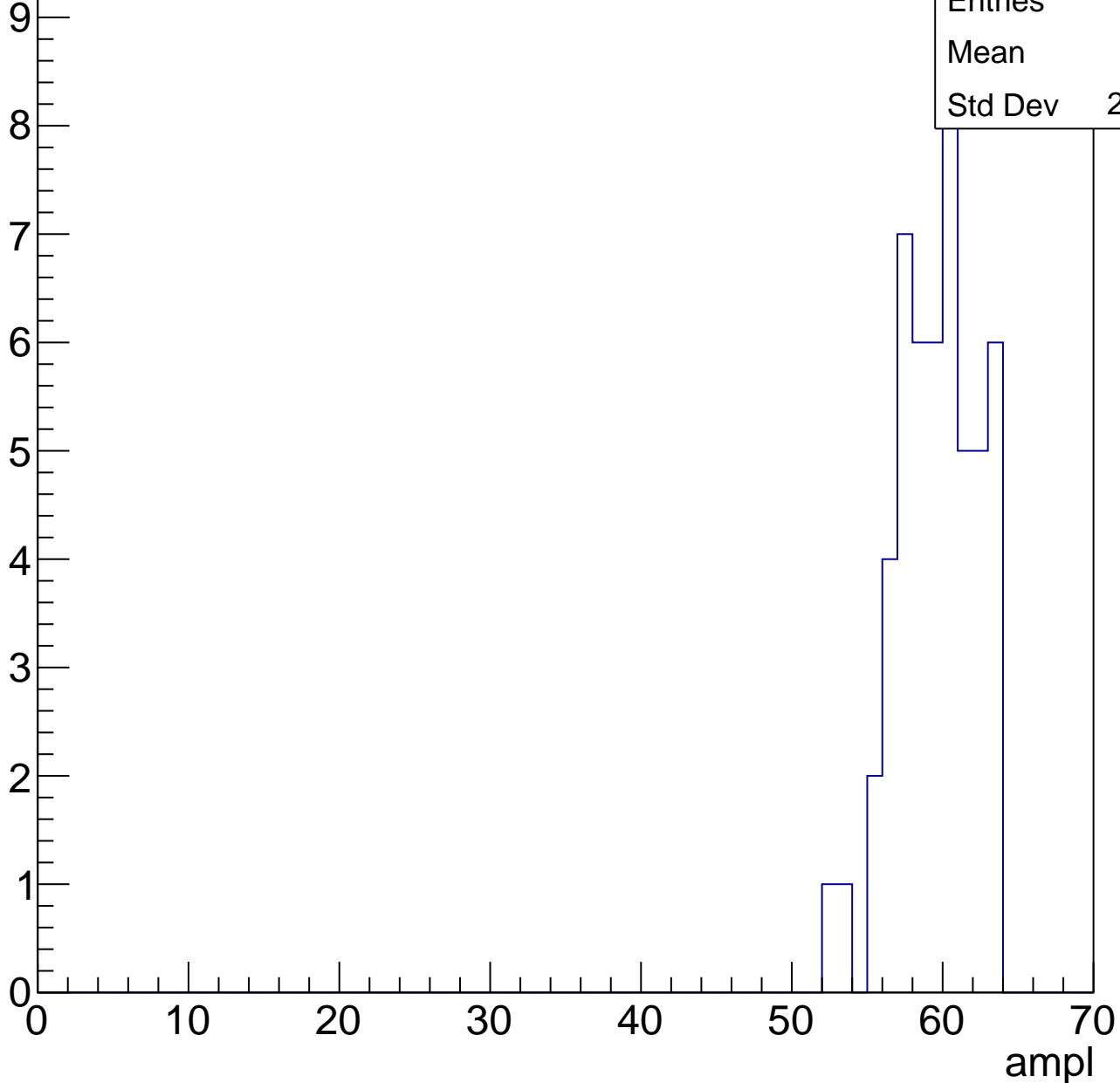


# B0L000S, U19-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	59.1
Std Dev	2.618

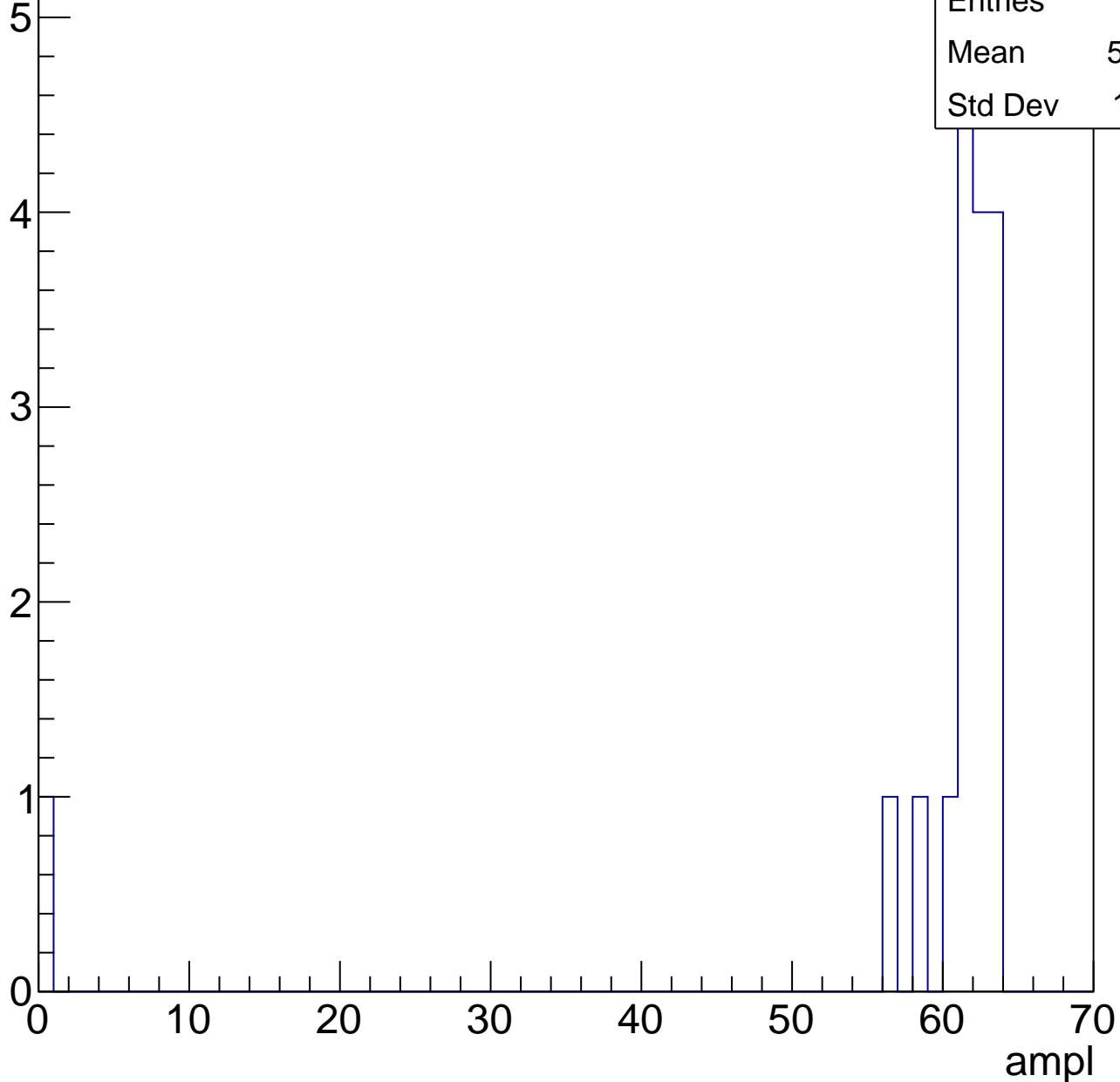


# B0L000S, U19-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	57.59
Std Dev	14.51

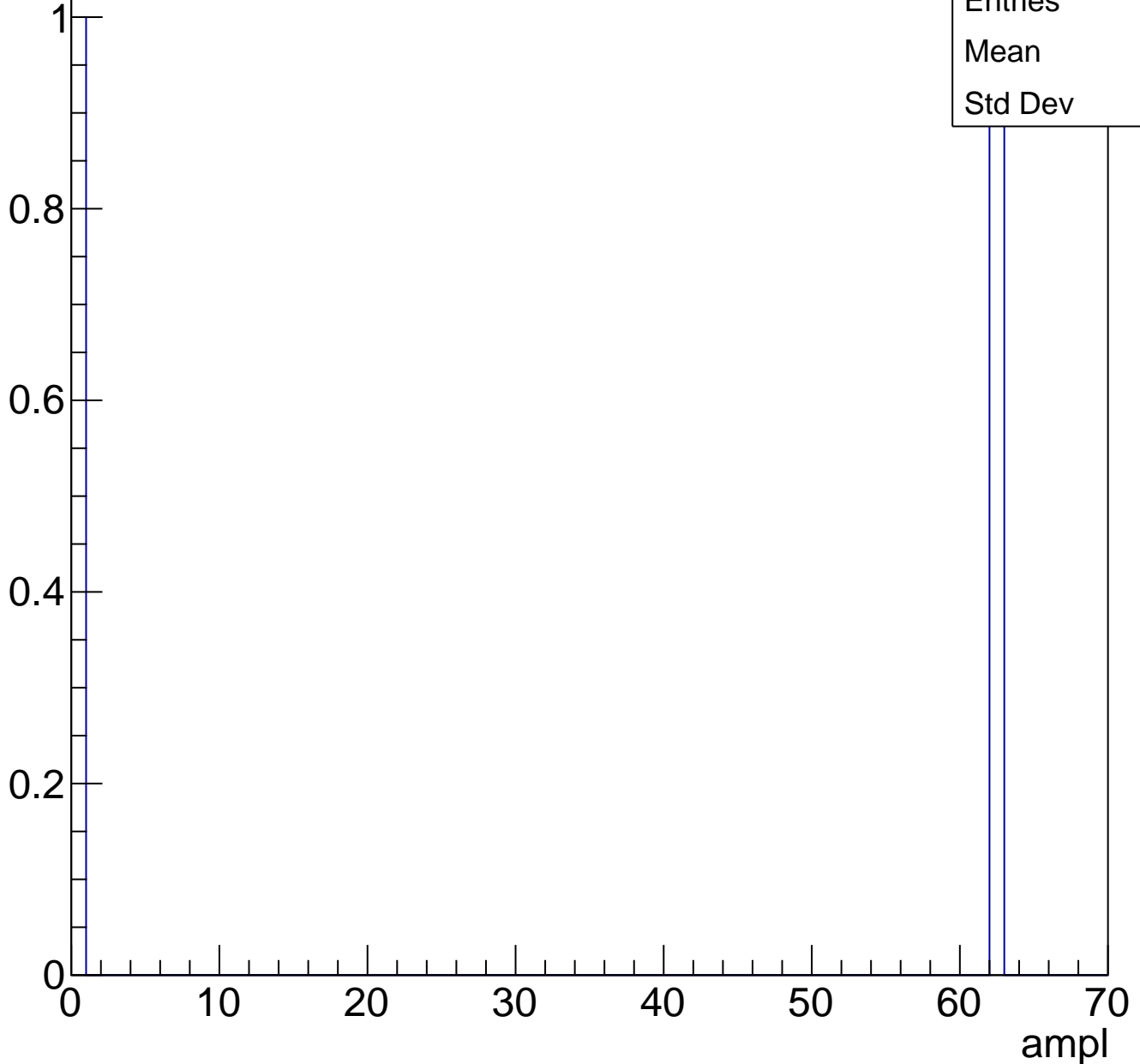




# B0L000S, U19-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch105, adc0

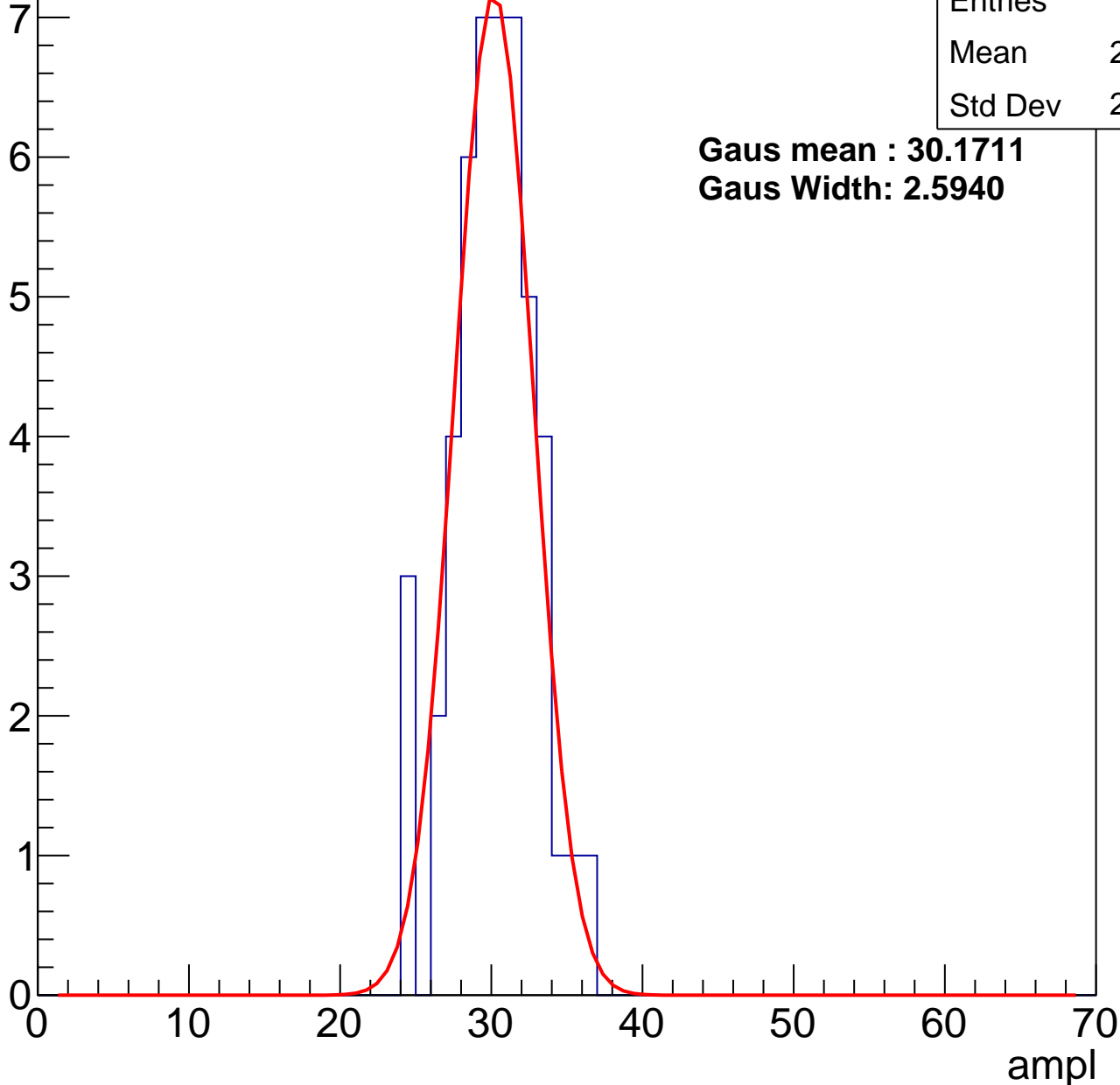
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	29.73
Std Dev	2.675

**Gaus mean : 30.1711**

**Gaus Width: 2.5940**



# B0L000S, U19-ch105, adc1

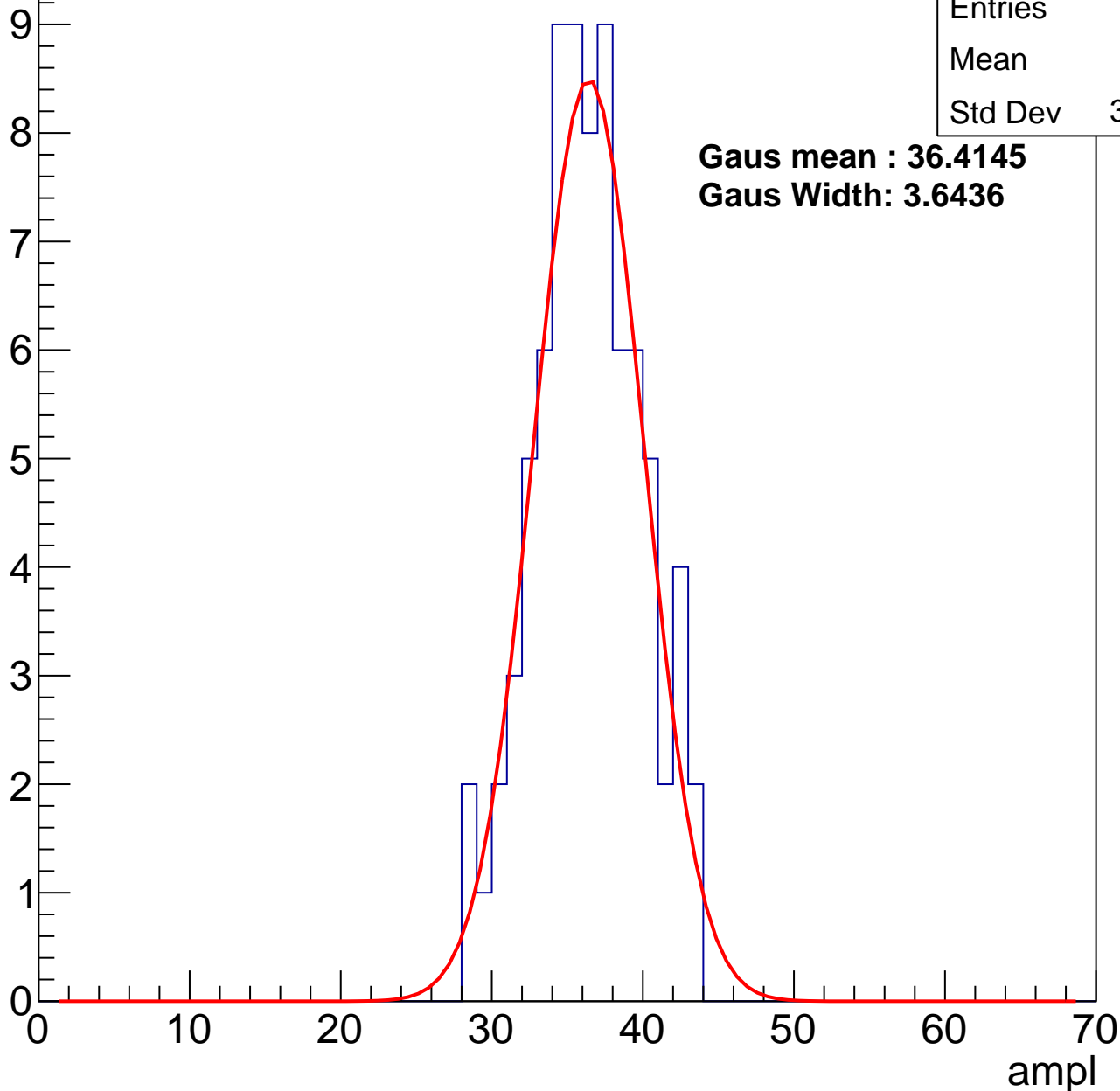
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	35.9
Std Dev	3.488

**Gaus mean : 36.4145**

**Gaus Width: 3.6436**

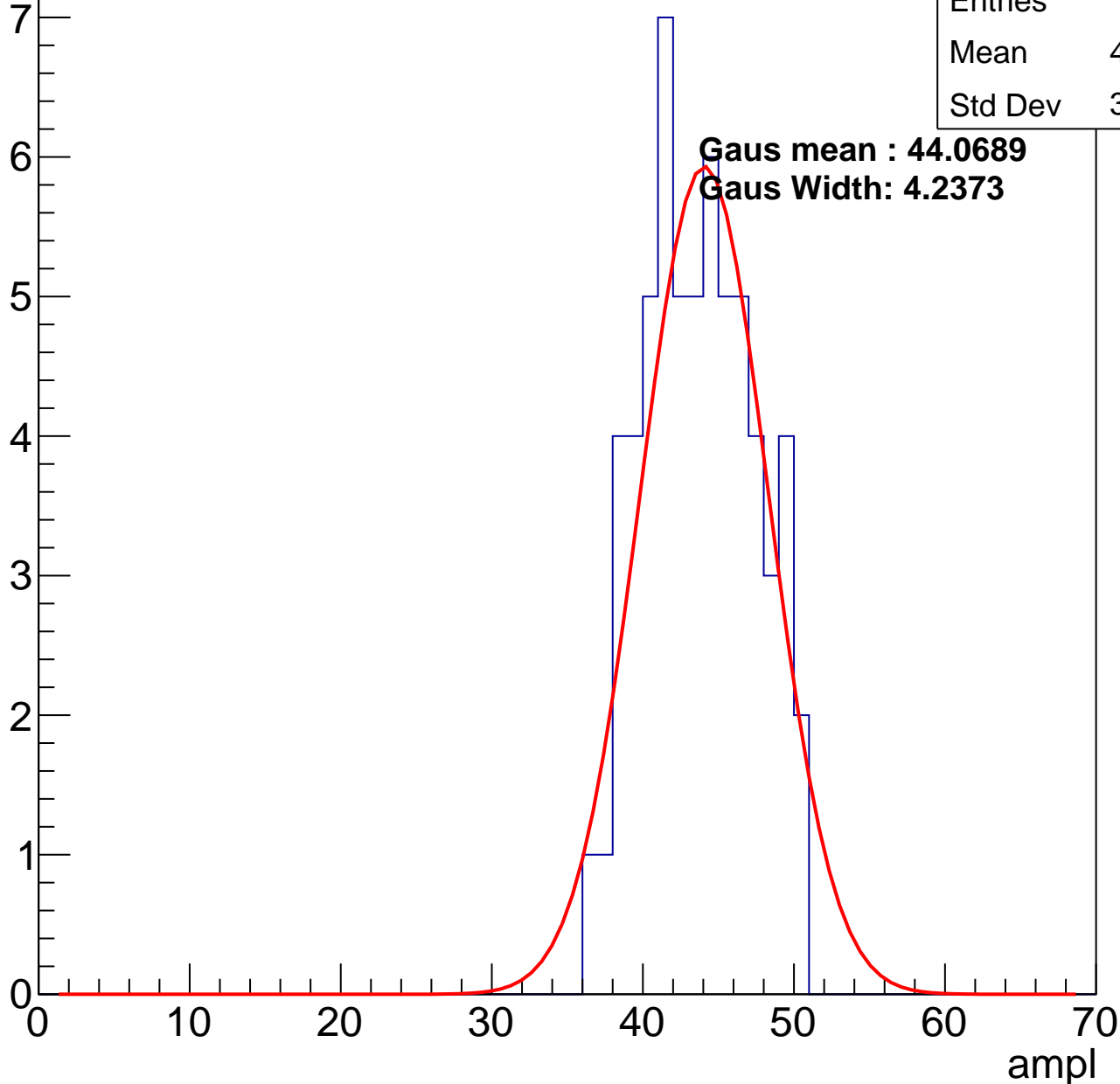


# B0L000S, U19-ch105, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.28
Std Dev	3.563

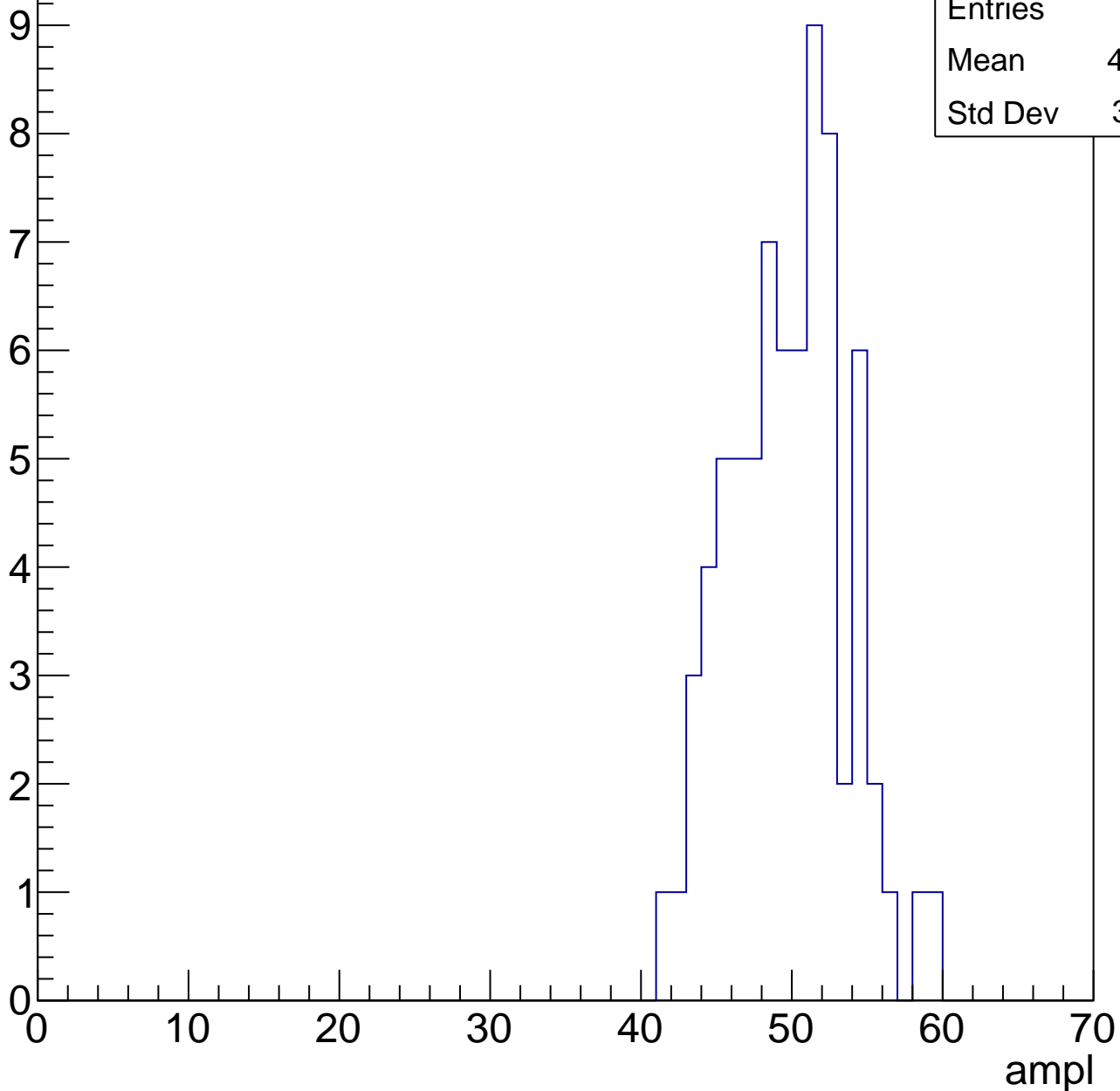


# B0L000S, U19-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	49.26
Std Dev	3.821

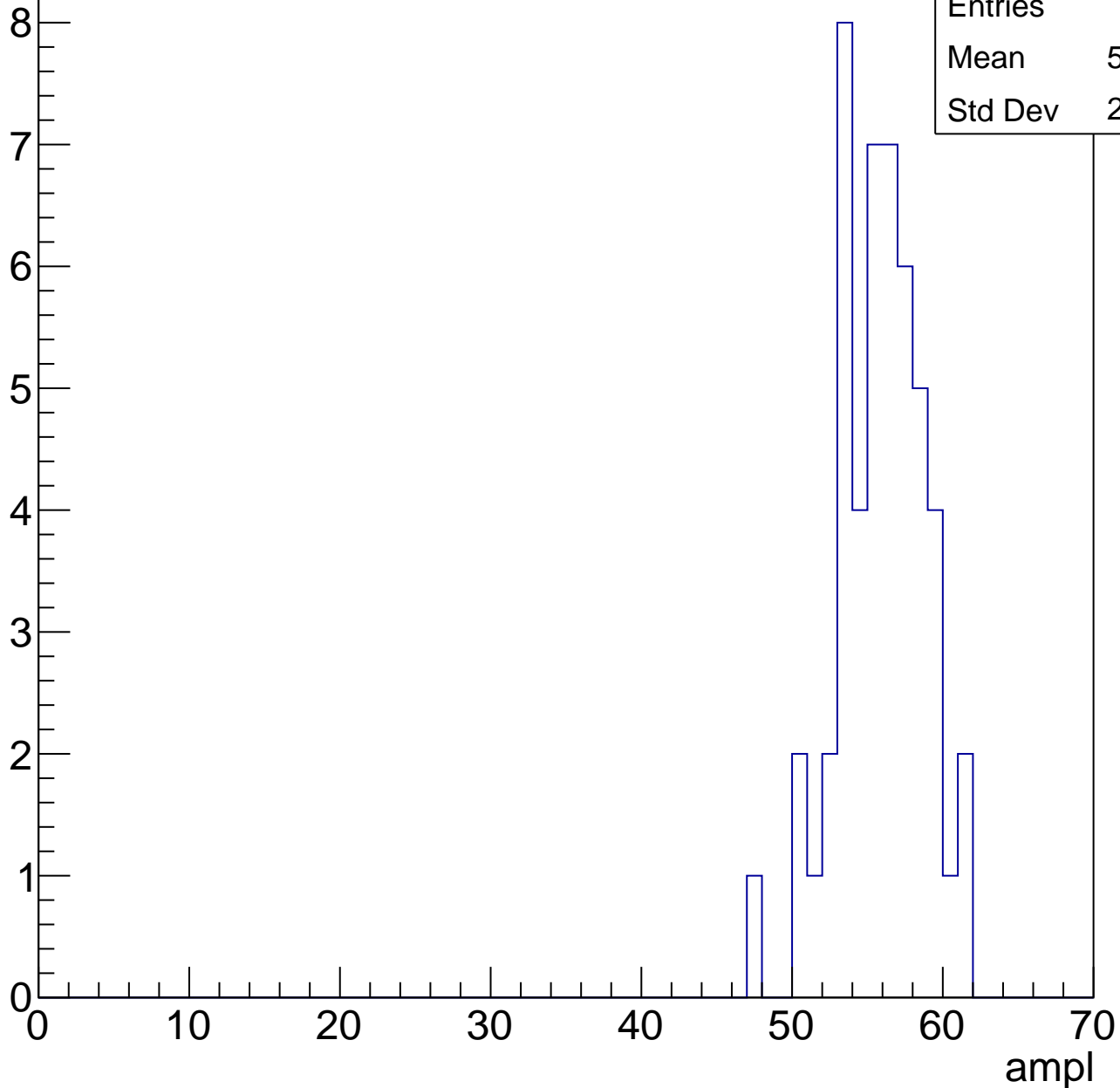


# B0L000S, U19-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

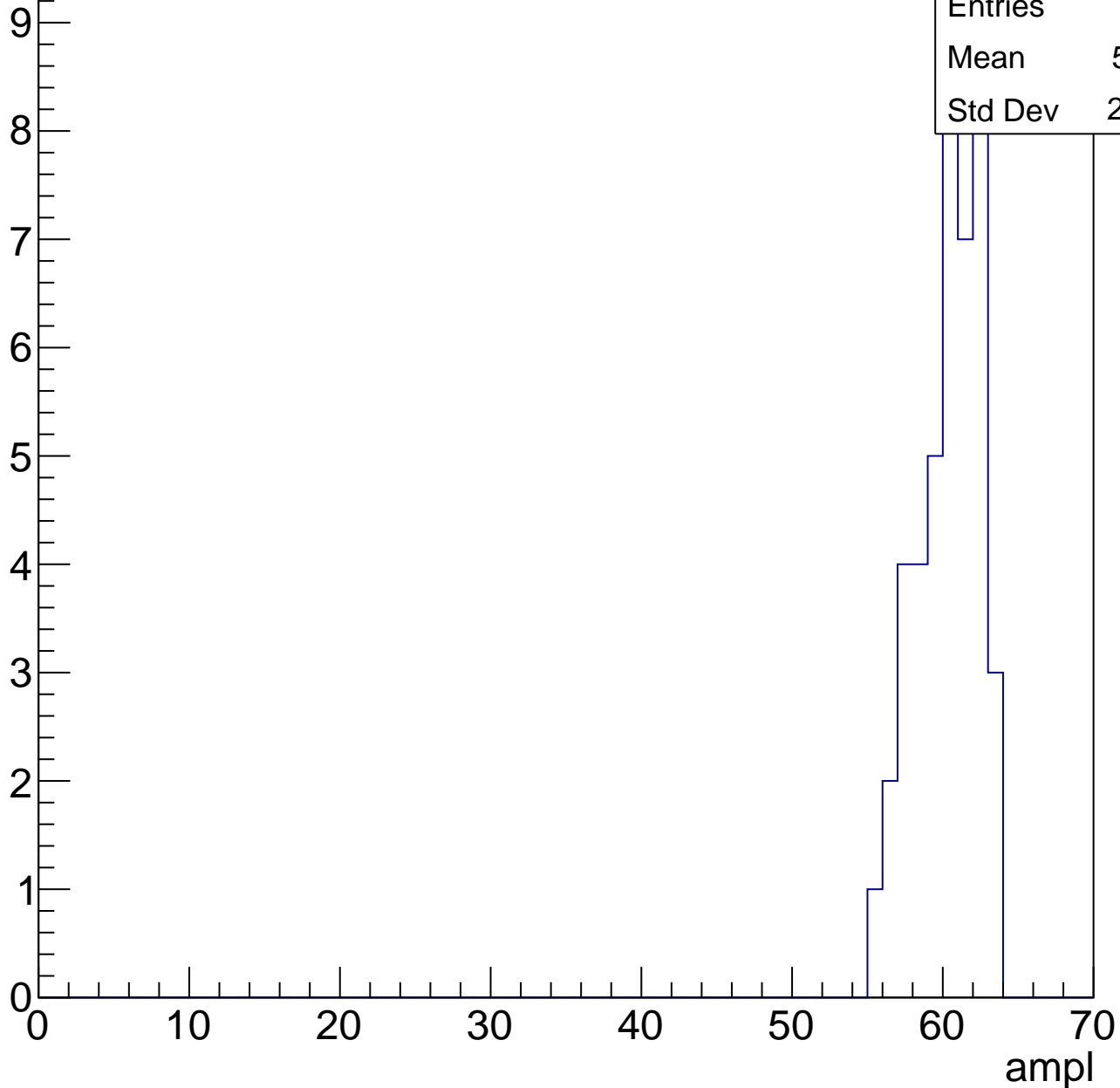
Entries	50
Mean	55.38
Std Dev	2.877



# B0L000S, U19-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

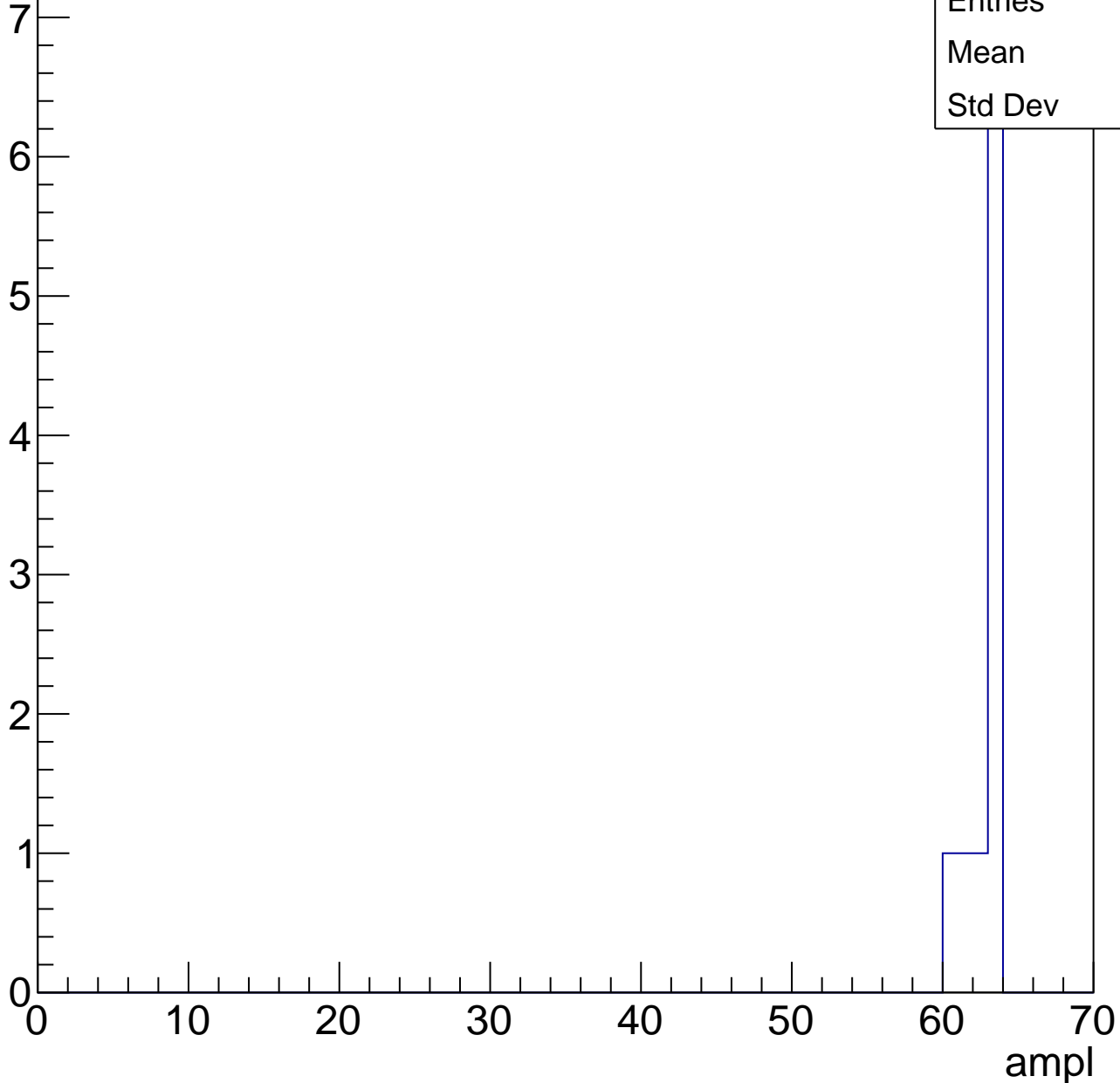


# B0L000S, U19-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62.4
Std Dev	1.02





# B0L000S, U19-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U19-ch106, adc0

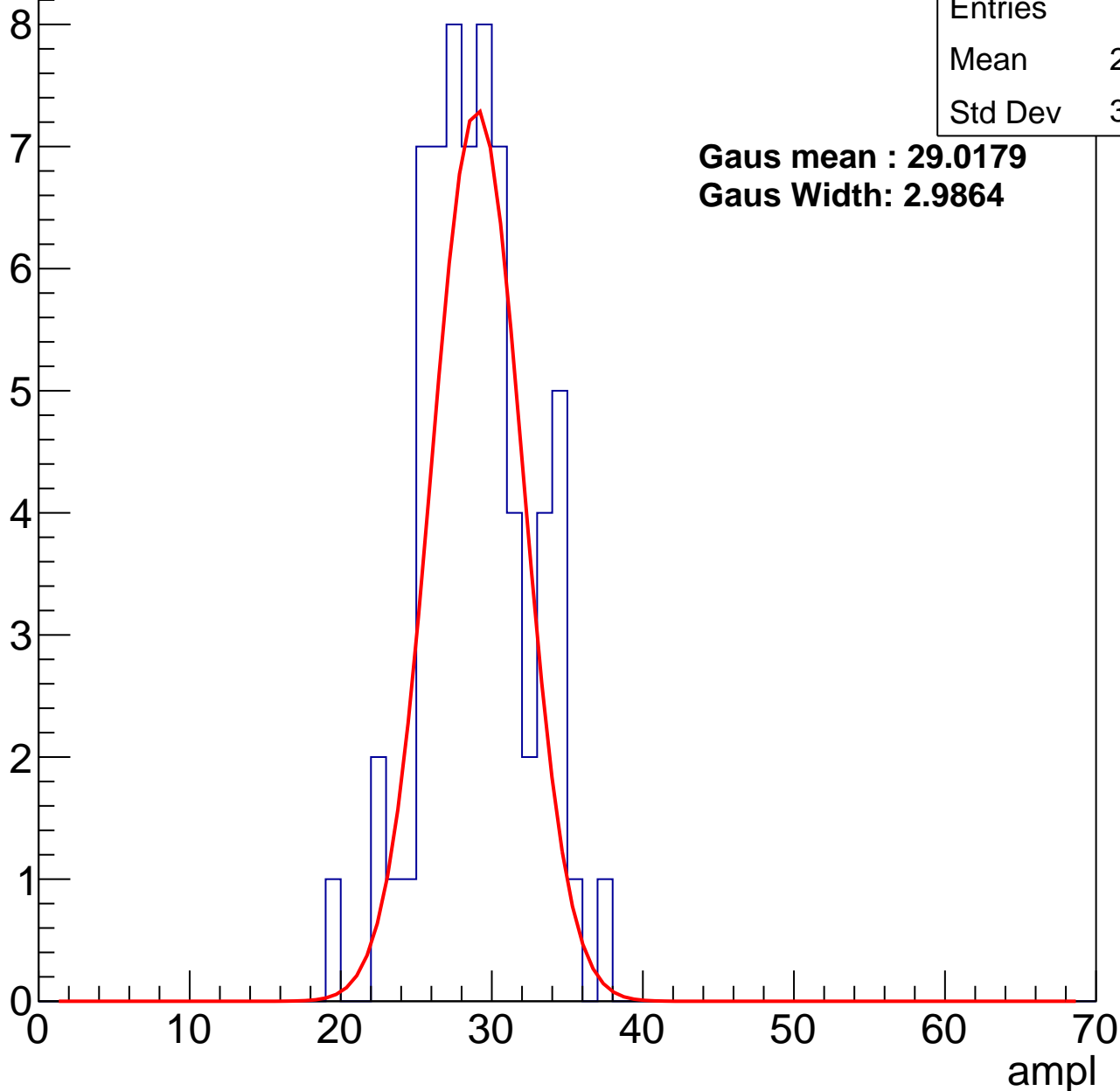
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.53
Std Dev	3.456

**Gaus mean : 29.0179**

**Gaus Width: 2.9864**



# B0L000S, U19-ch106, adc1

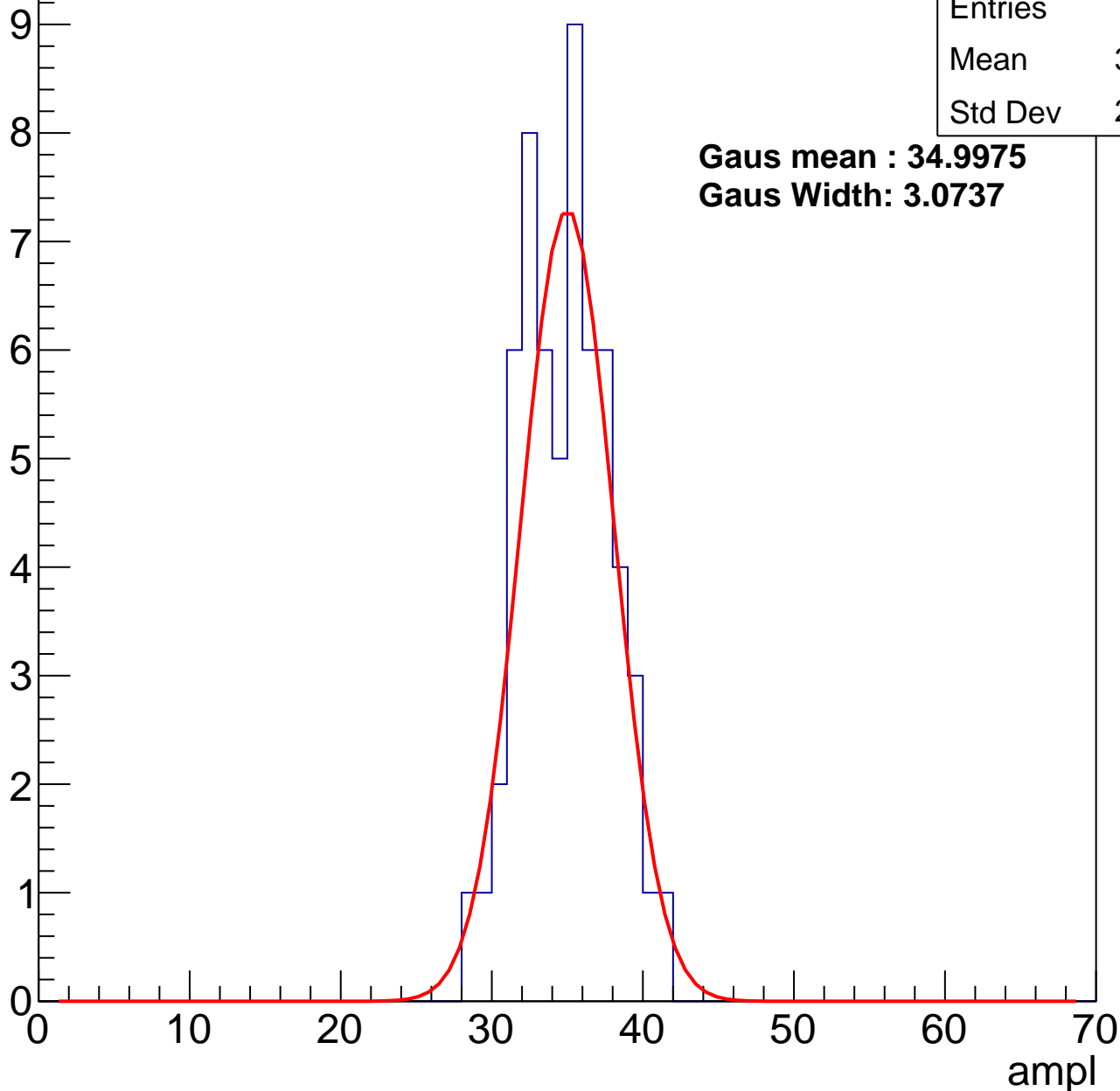
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	34.41
Std Dev	2.871

**Gaus mean : 34.9975**

**Gaus Width: 3.0737**



# B0L000S, U19-ch106, adc2

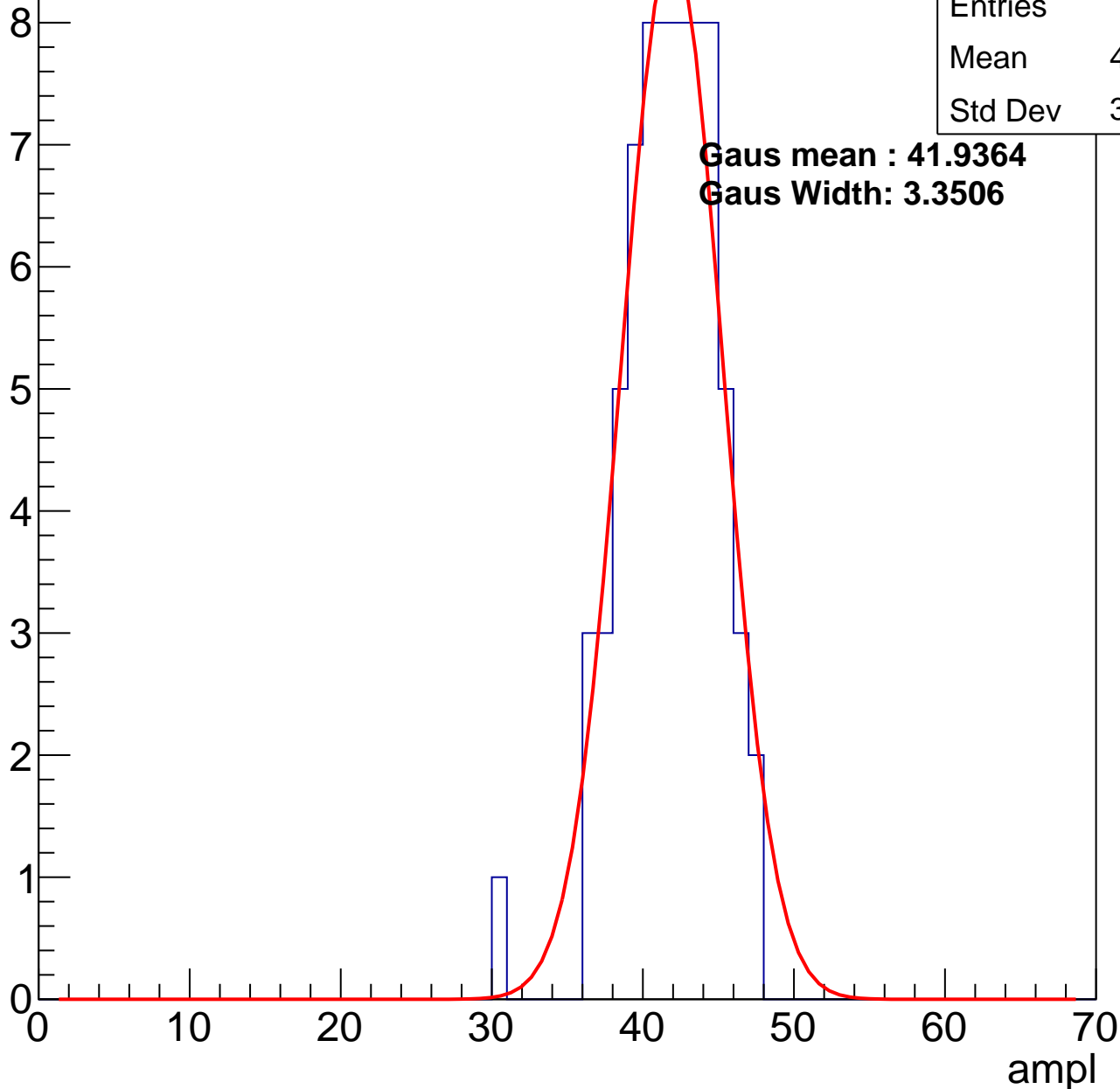
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	41.29
Std Dev	3.089

**Gaus mean : 41.9364**

**Gaus Width: 3.3506**



# B0L000S, U19-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

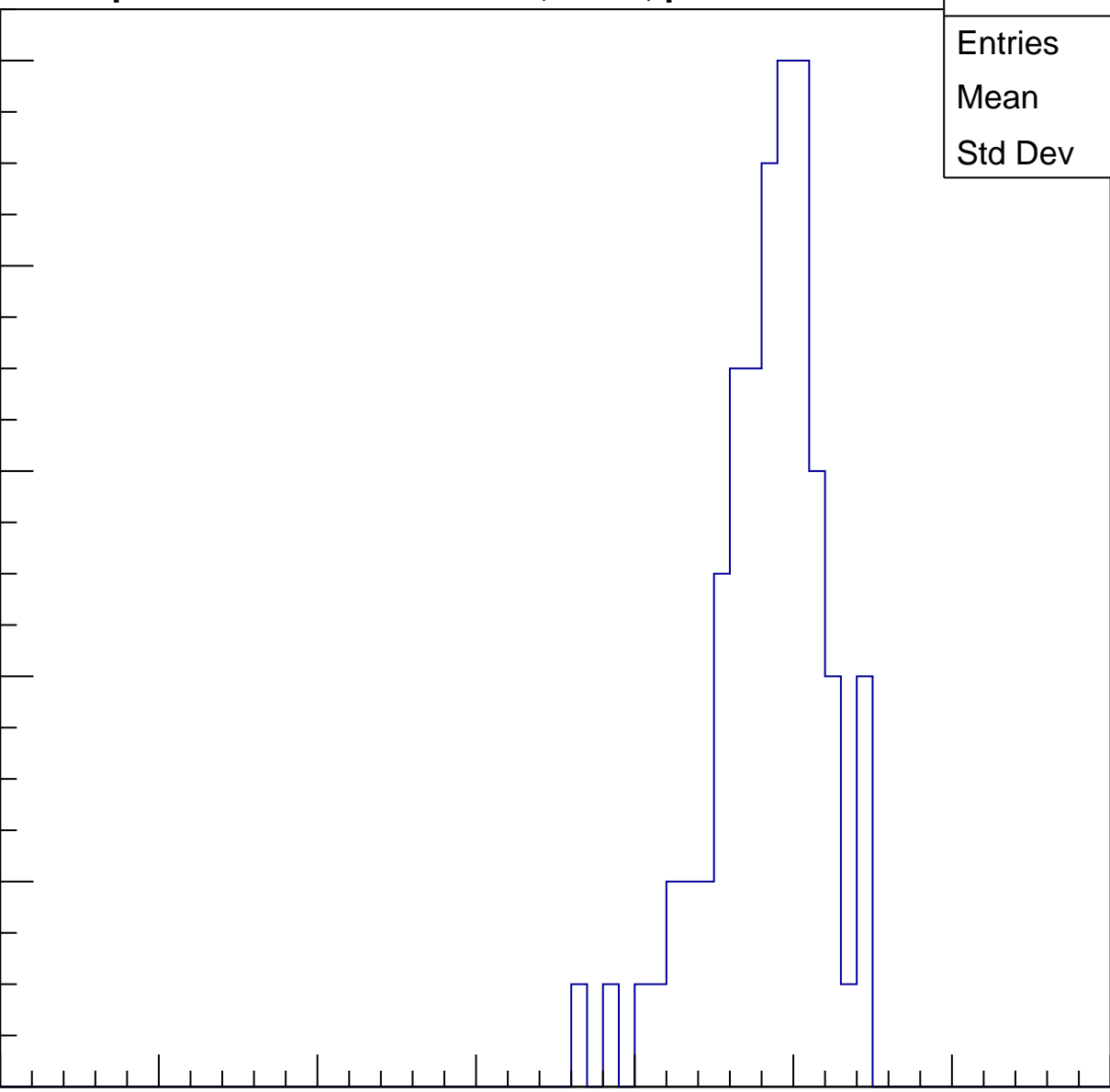
Entries	73
Mean	47.86
Std Dev	3.563

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch106, adc4

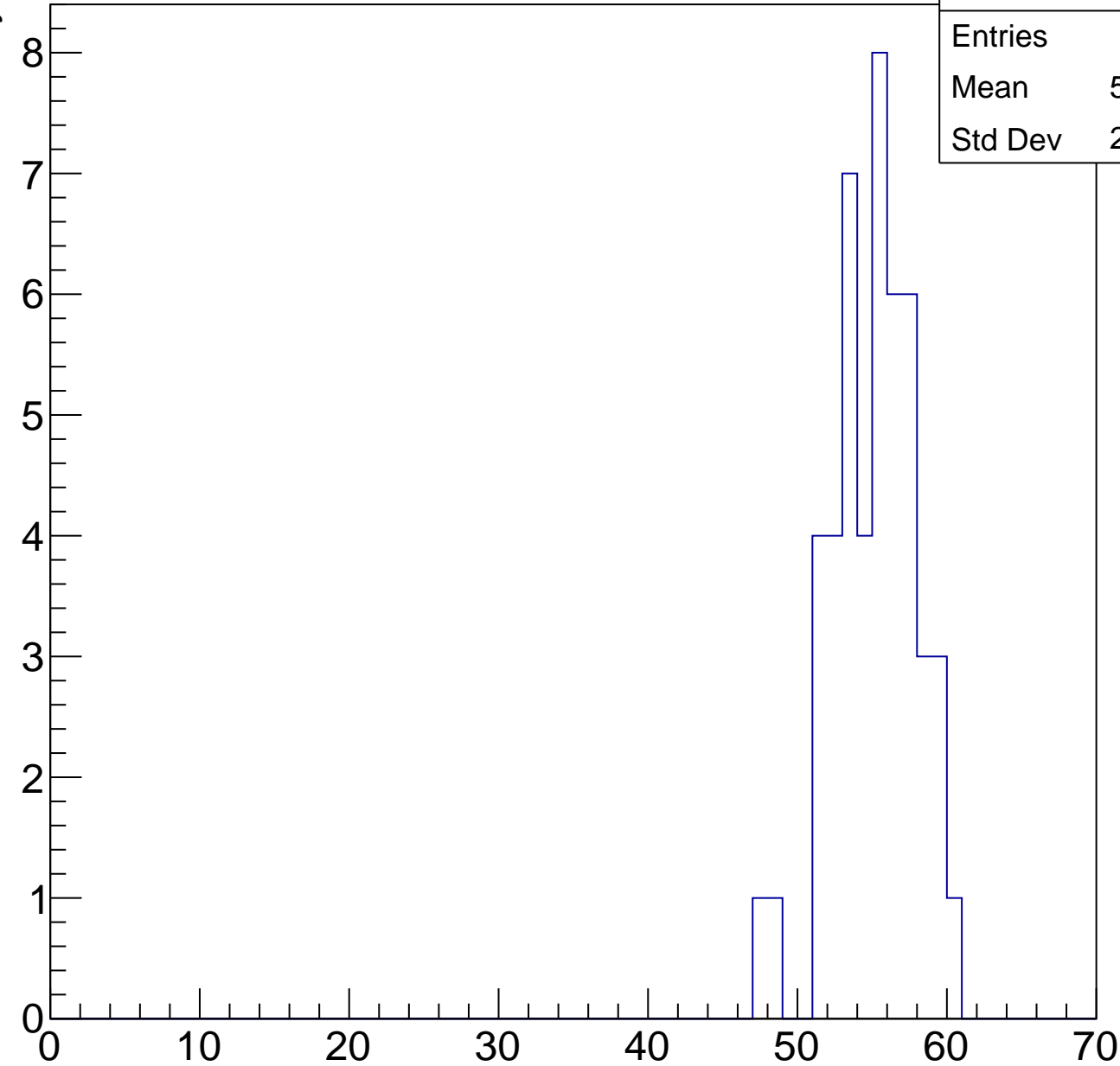
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	54.65
Std Dev	2.773

ampl

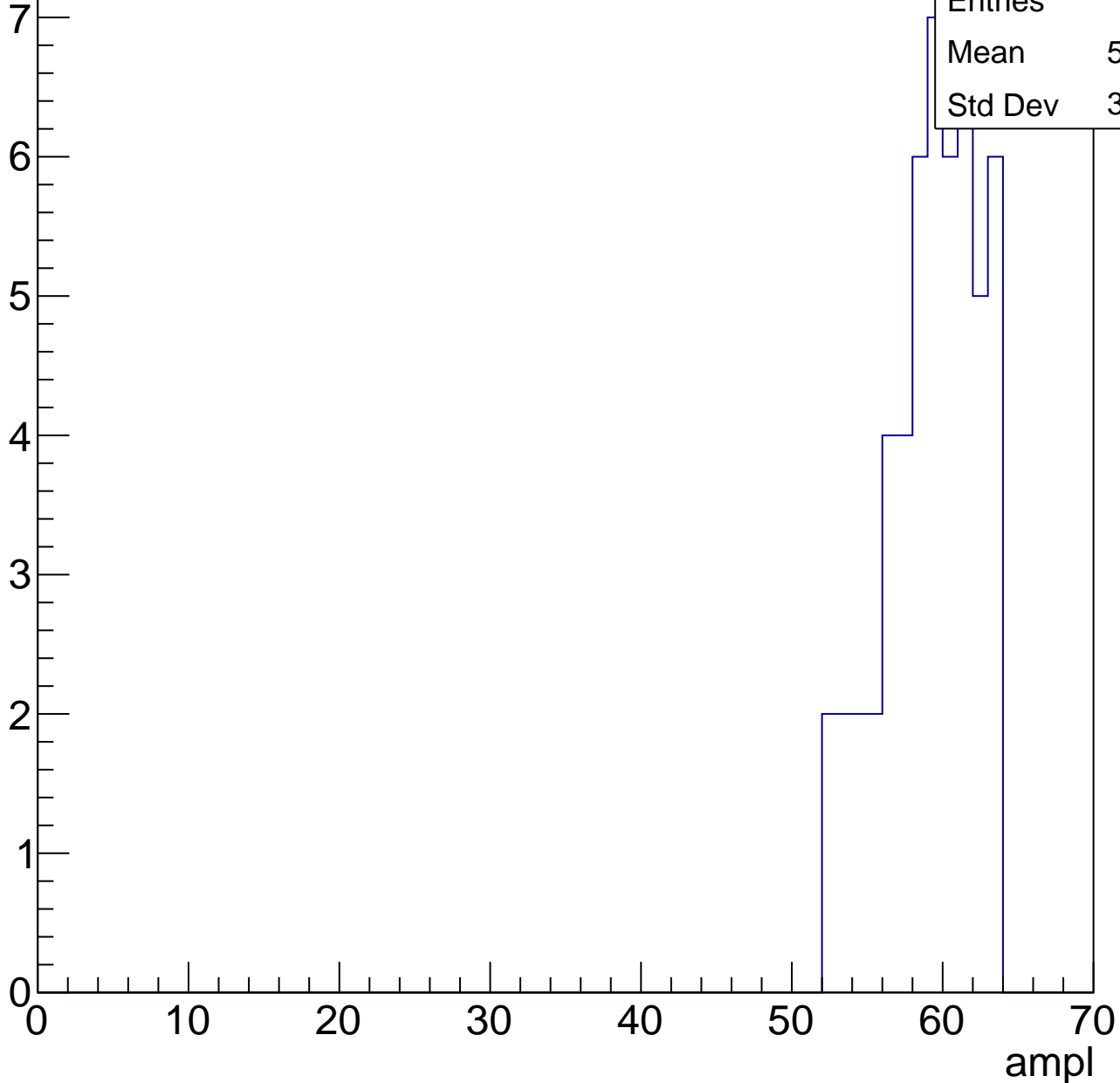


# B0L000S, U19-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	58.79
Std Dev	3.018

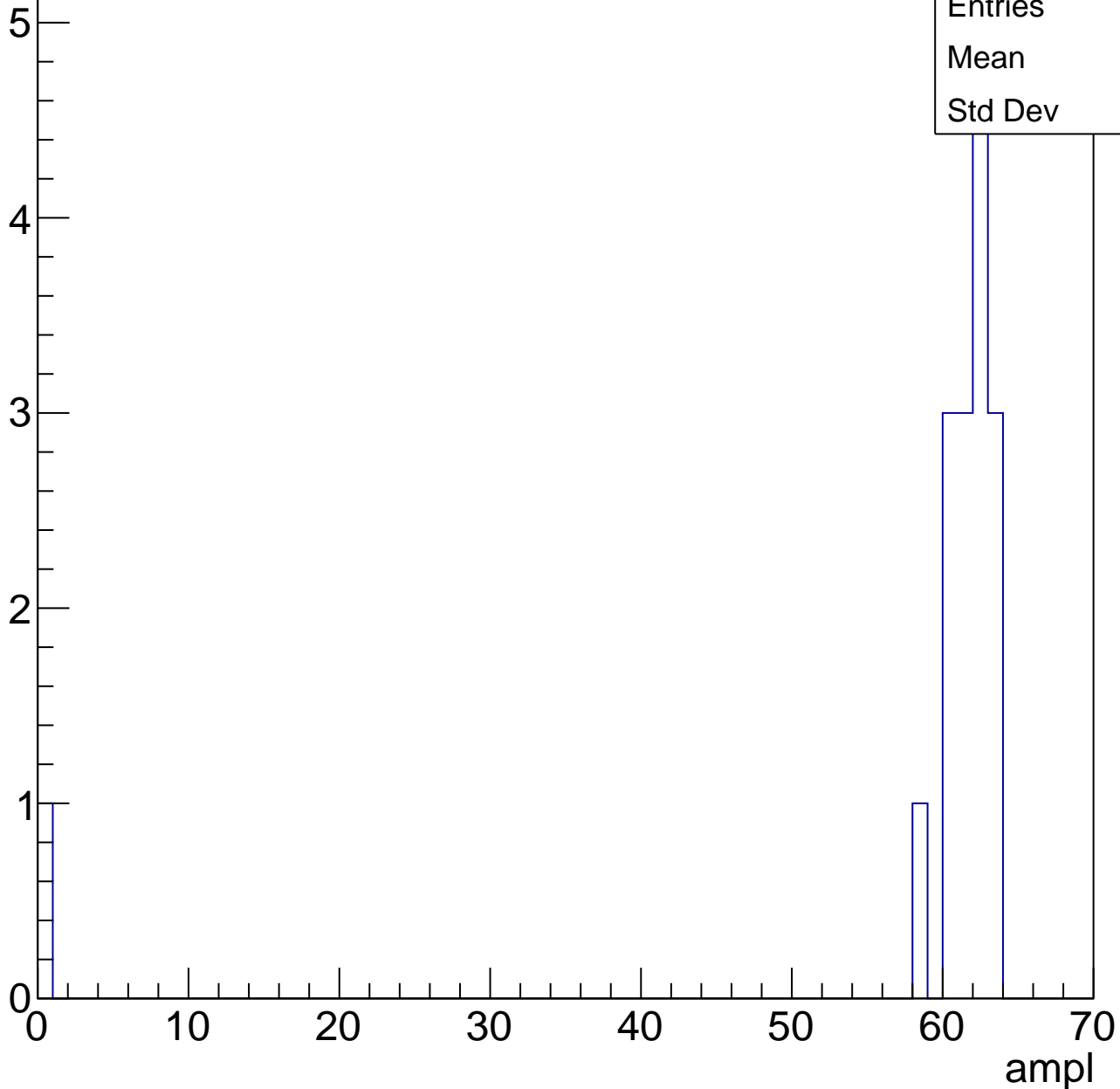


# B0L000S, U19-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.5
Std Dev	14.9





# B0L000S, U19-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0

# B0L000S, U19-ch107, adc0

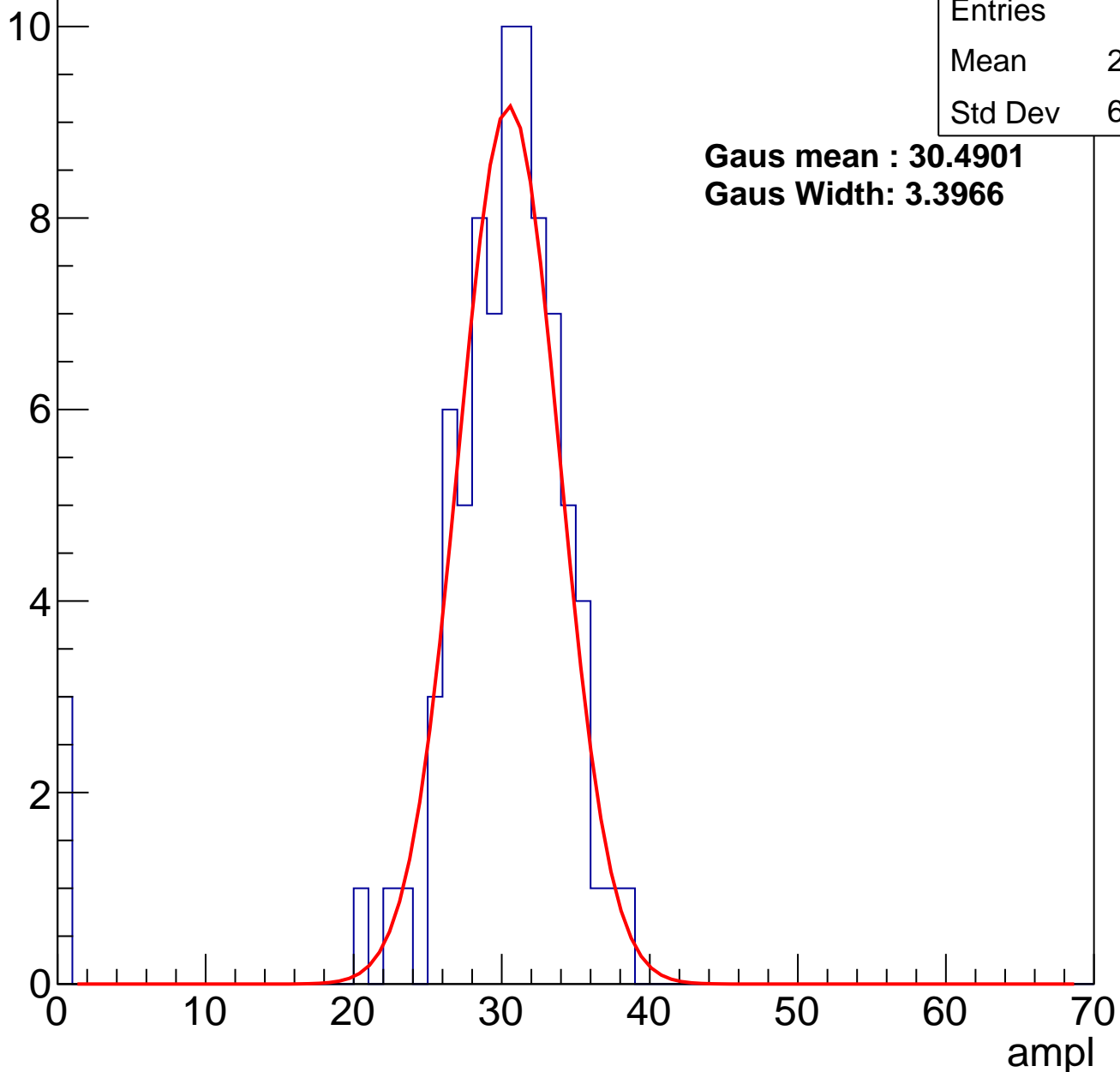
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	28.98
Std Dev	6.548

**Gaus mean : 30.4901**

**Gaus Width: 3.3966**

Entry



# B0L000S, U19-ch107, adc1

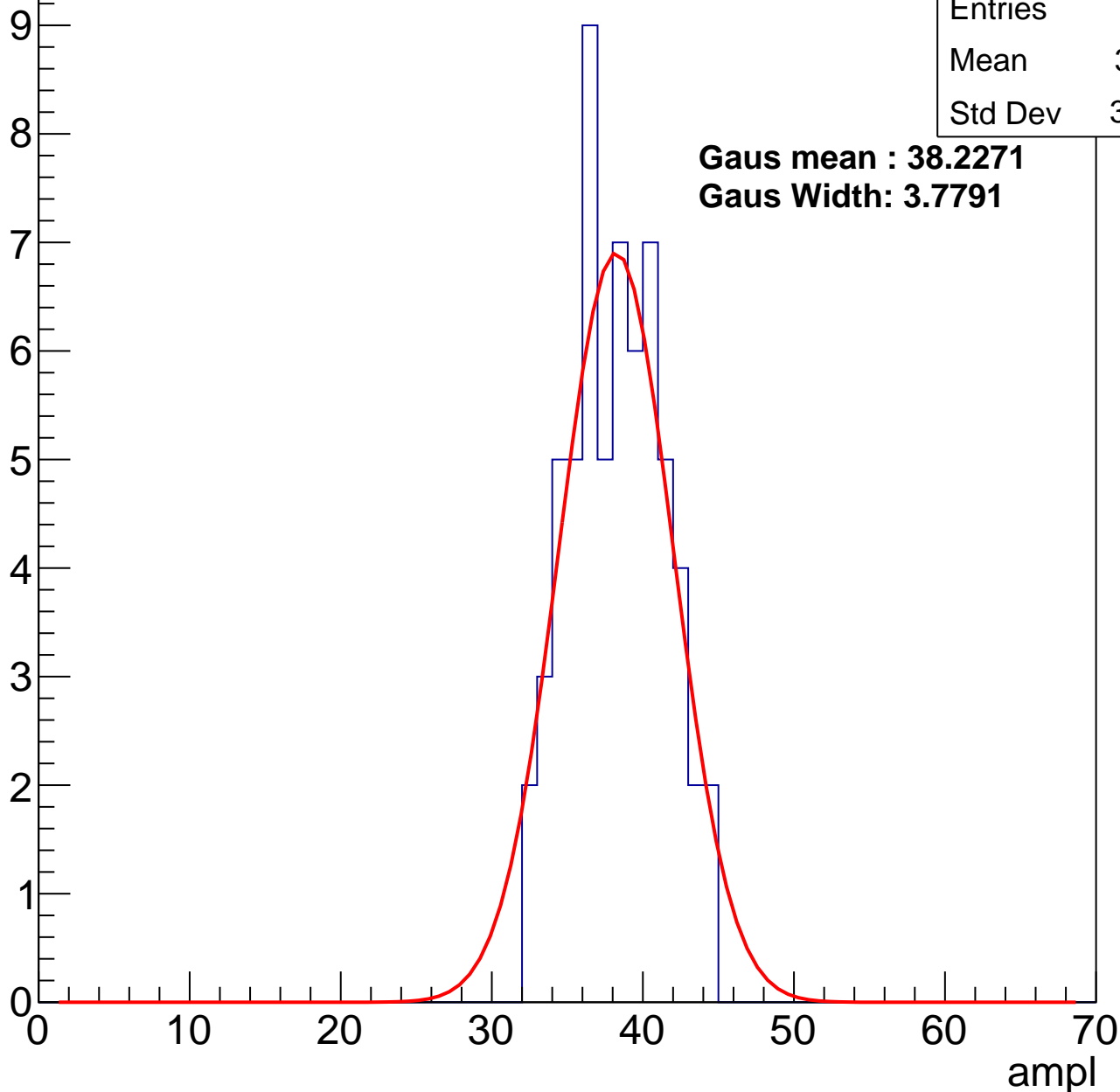
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.81
Std Dev	3.047

**Gaus mean : 38.2271**

**Gaus Width: 3.7791**



# B0L000S, U19-ch107, adc2

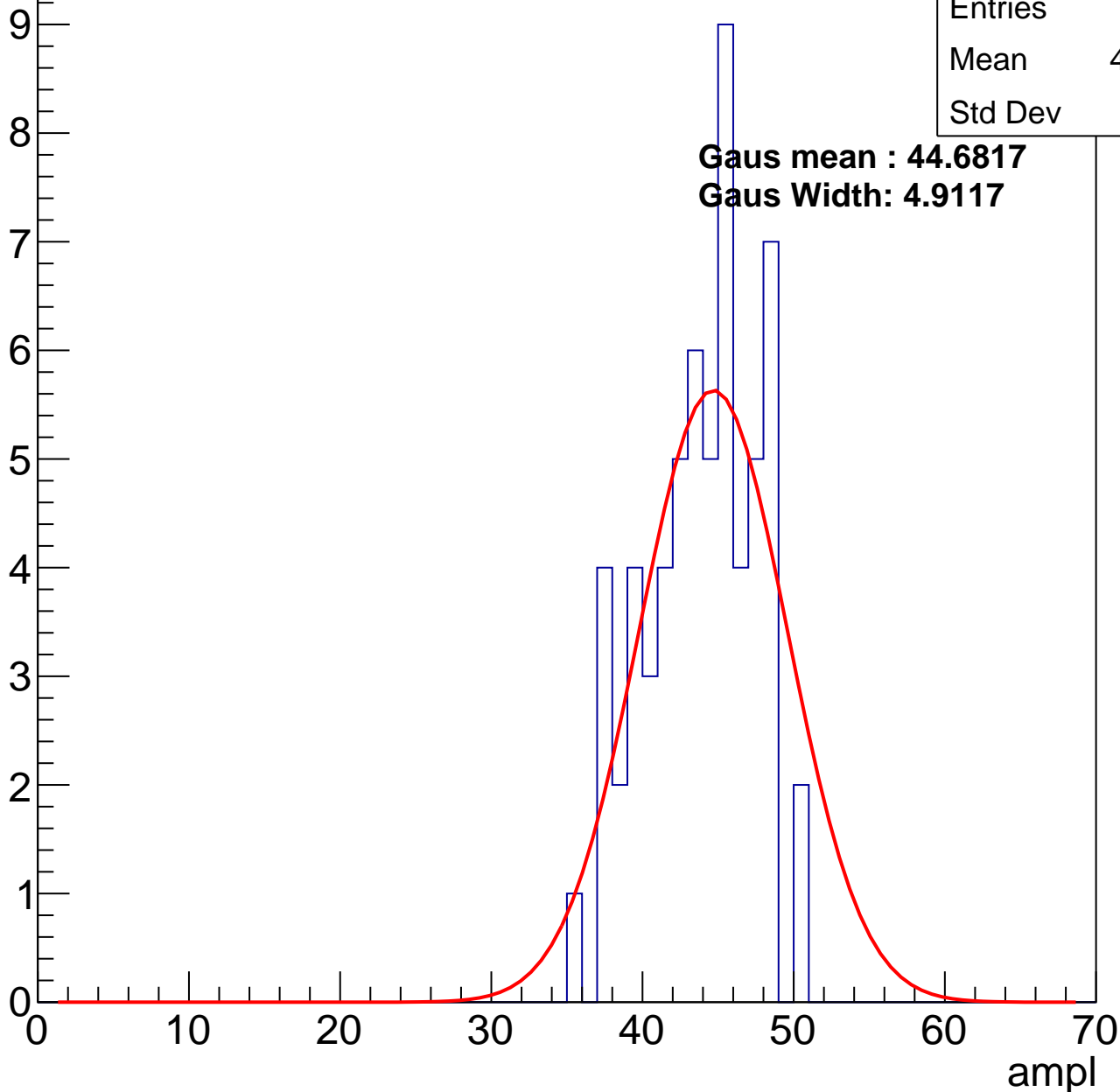
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.39
Std Dev	3.6

**Gaus mean : 44.6817**

**Gaus Width: 4.9117**

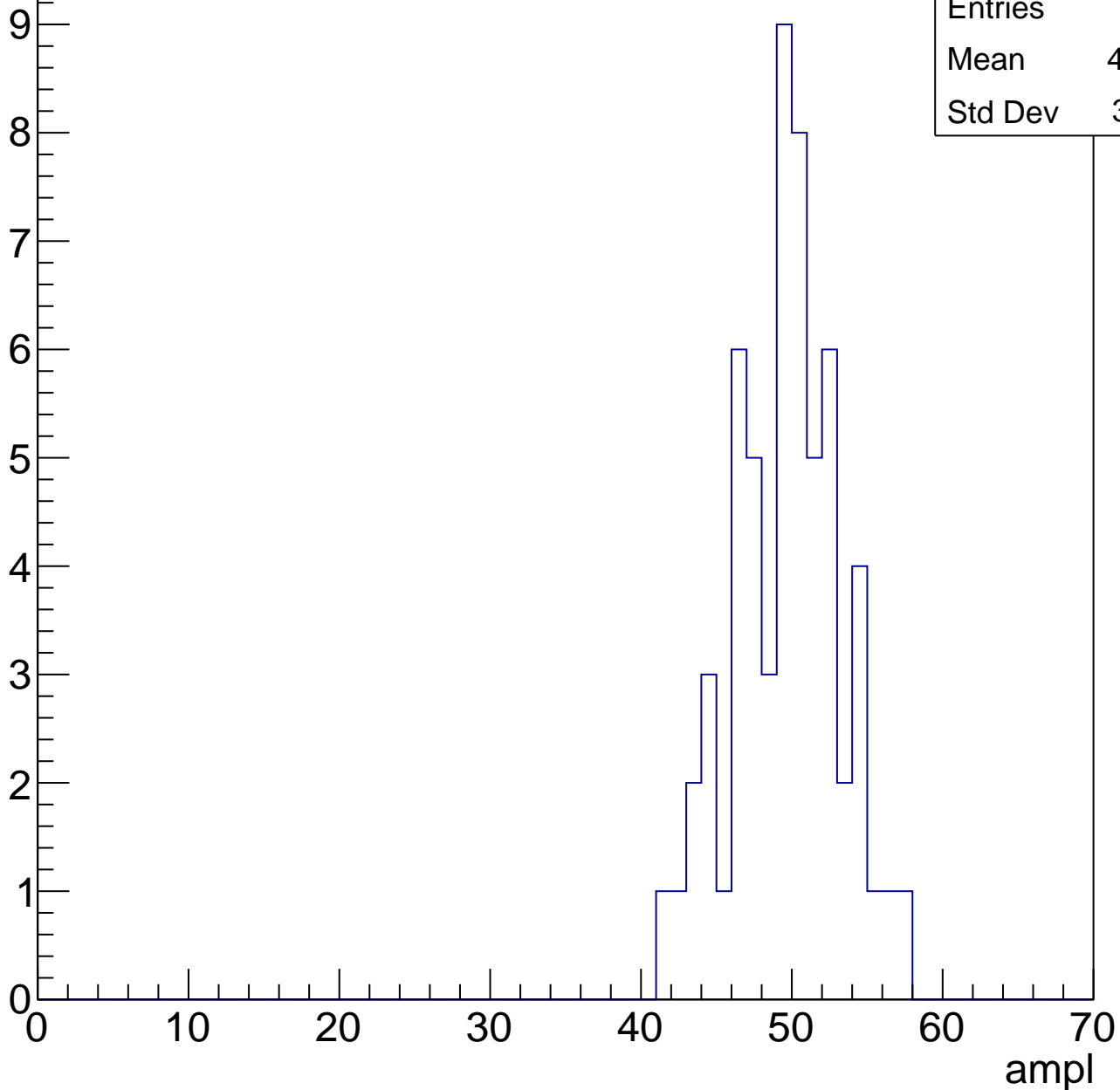


# B0L000S, U19-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.14
Std Dev	3.481

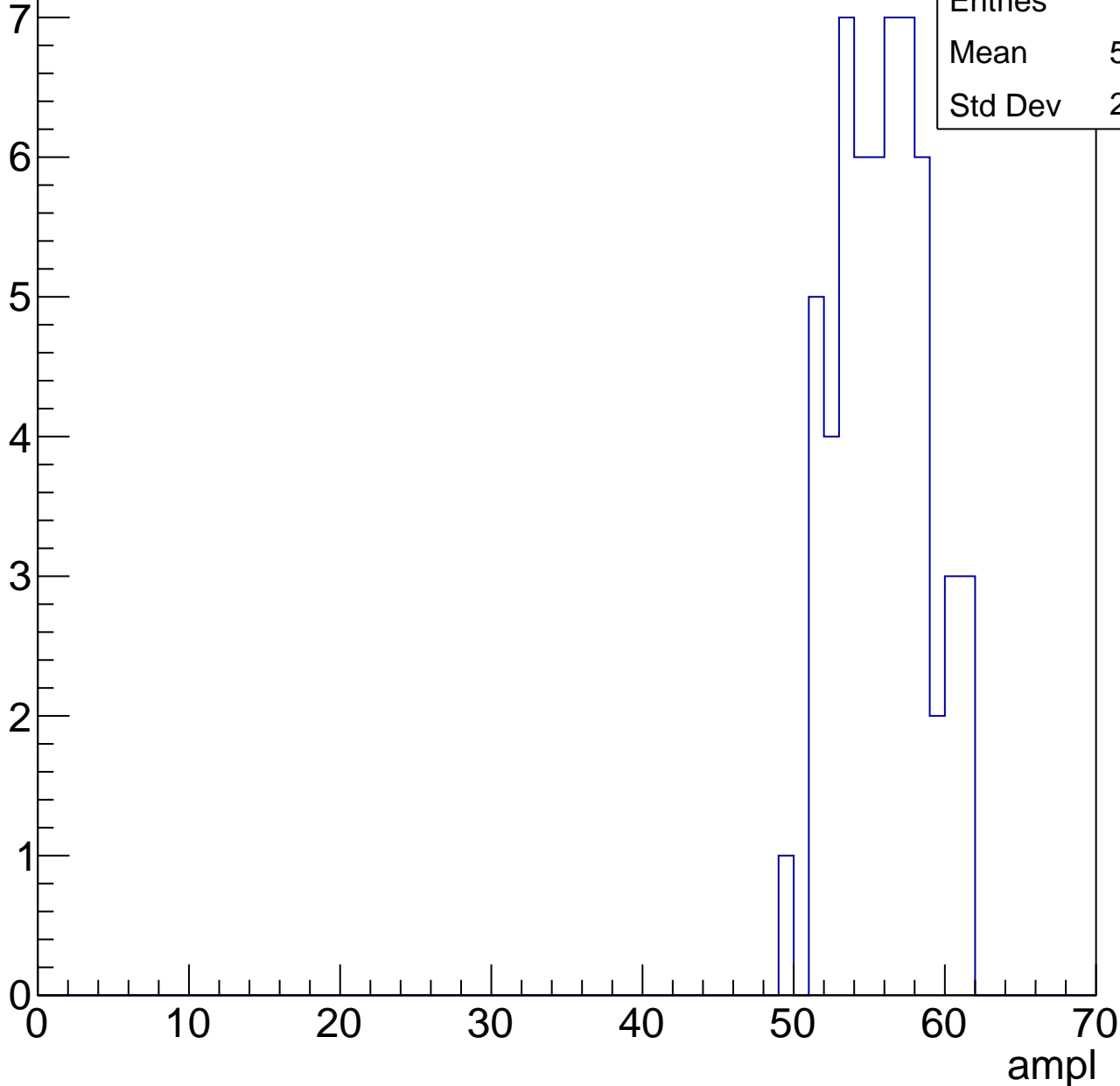


# B0L000S, U19-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	55.39
Std Dev	2.906

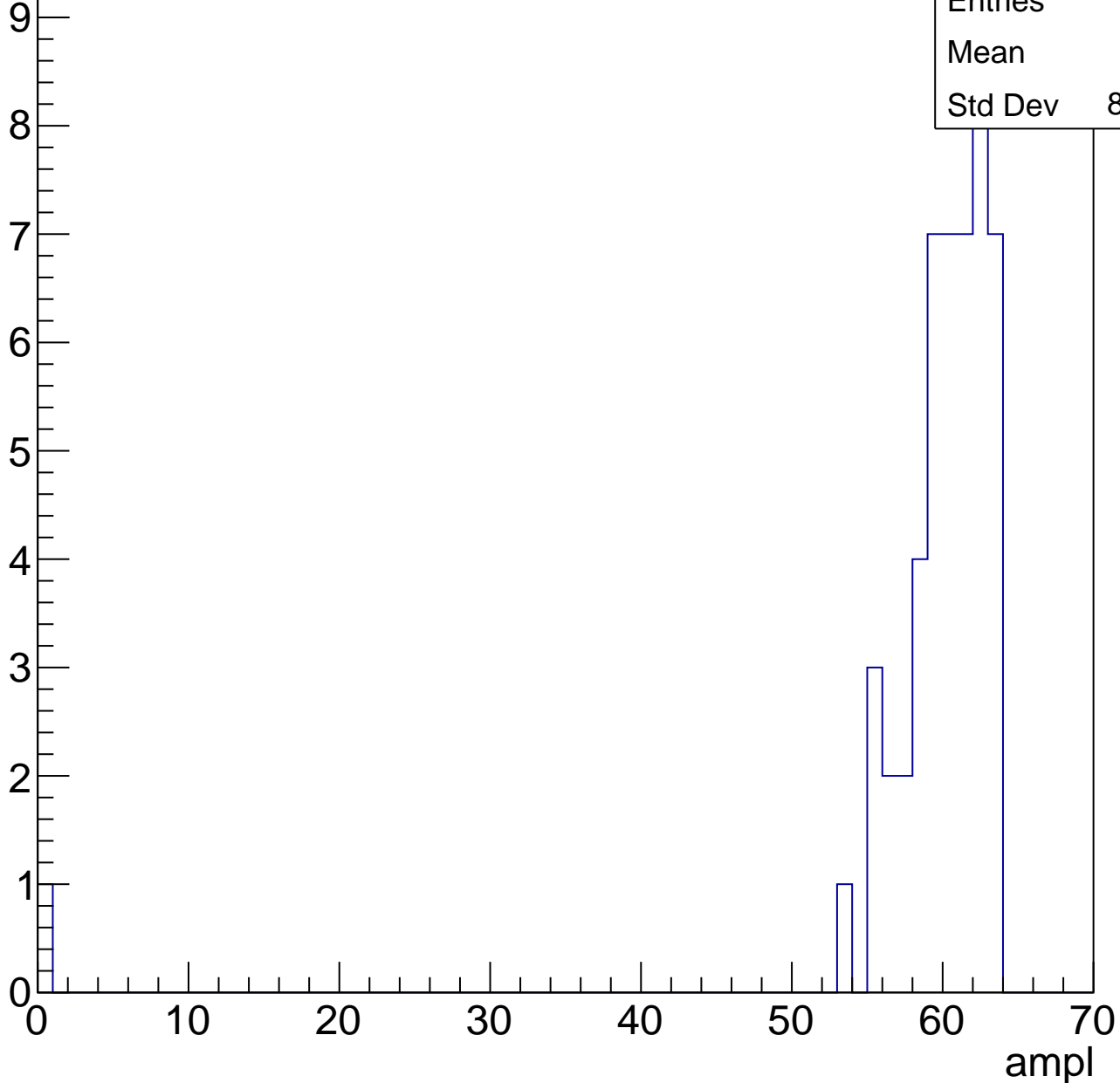


# B0L000S, U19-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	58.7
Std Dev	8.739



# B0L000S, U19-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch108, adc0

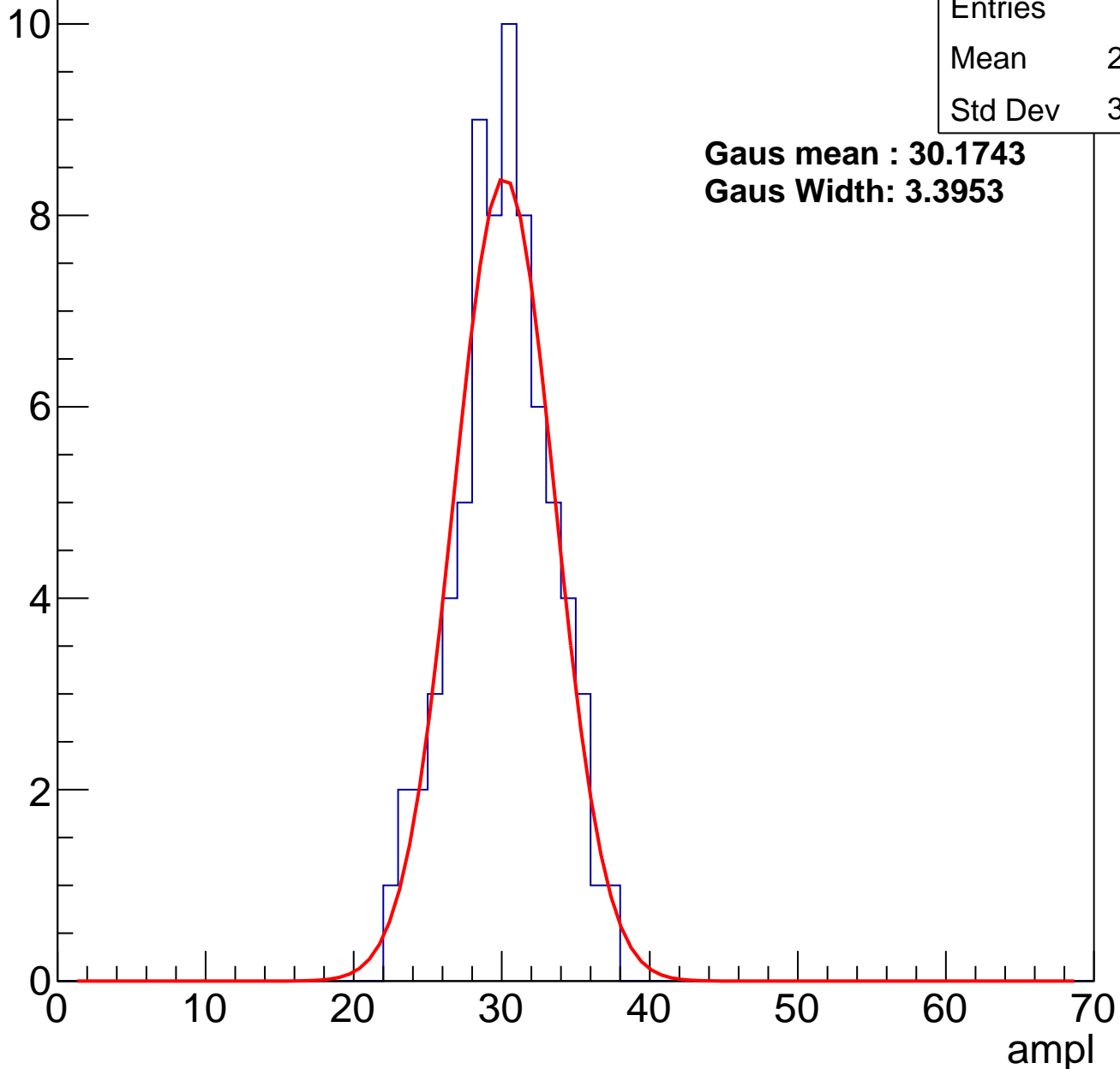
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	29.62
Std Dev	3.234

**Gaus mean : 30.1743**

**Gaus Width: 3.3953**

Entry



# B0L000S, U19-ch108, adc1

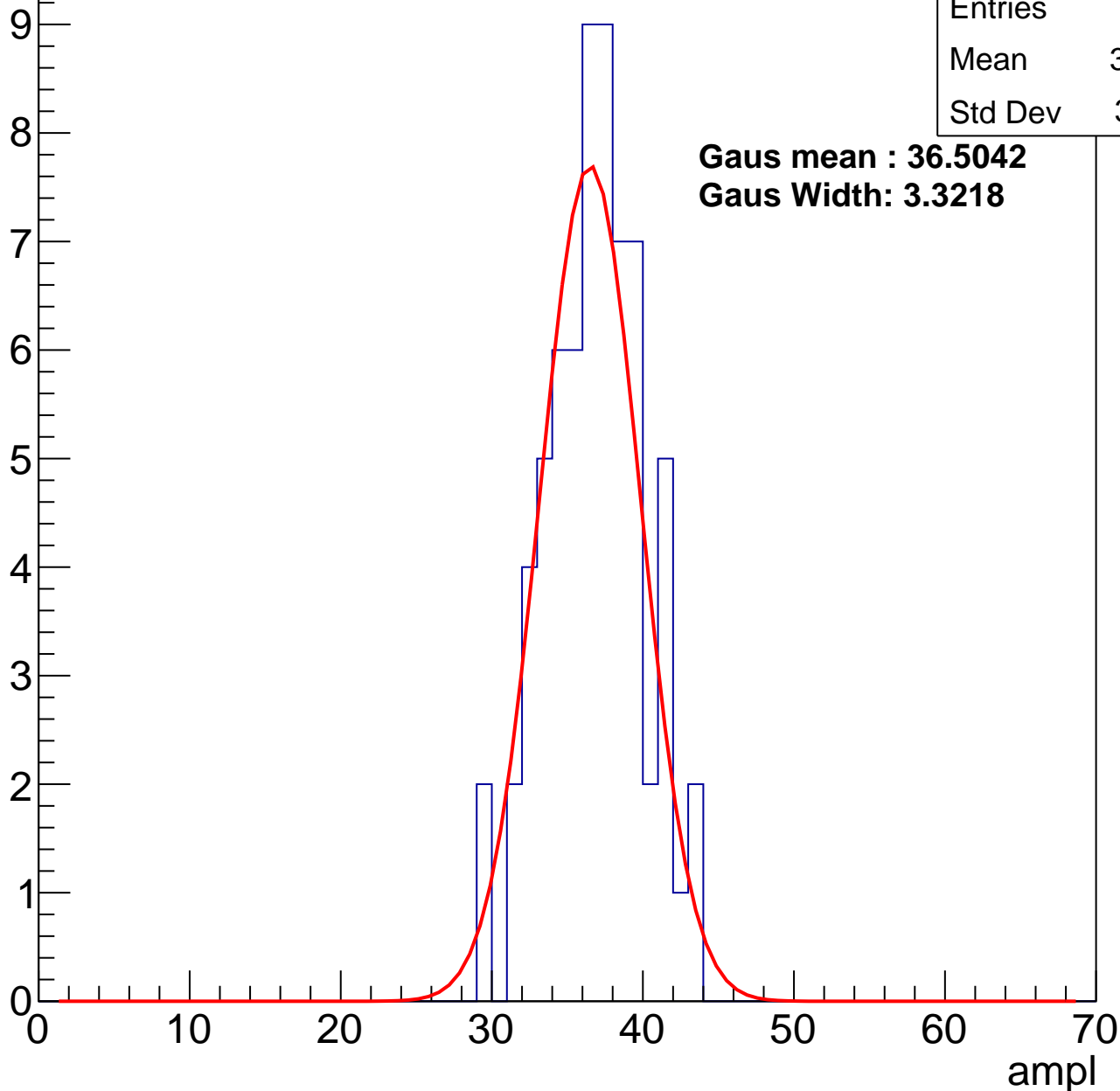
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.36
Std Dev	3.161

**Gaus mean : 36.5042**

**Gaus Width: 3.3218**



# B0L000S, U19-ch108, adc2

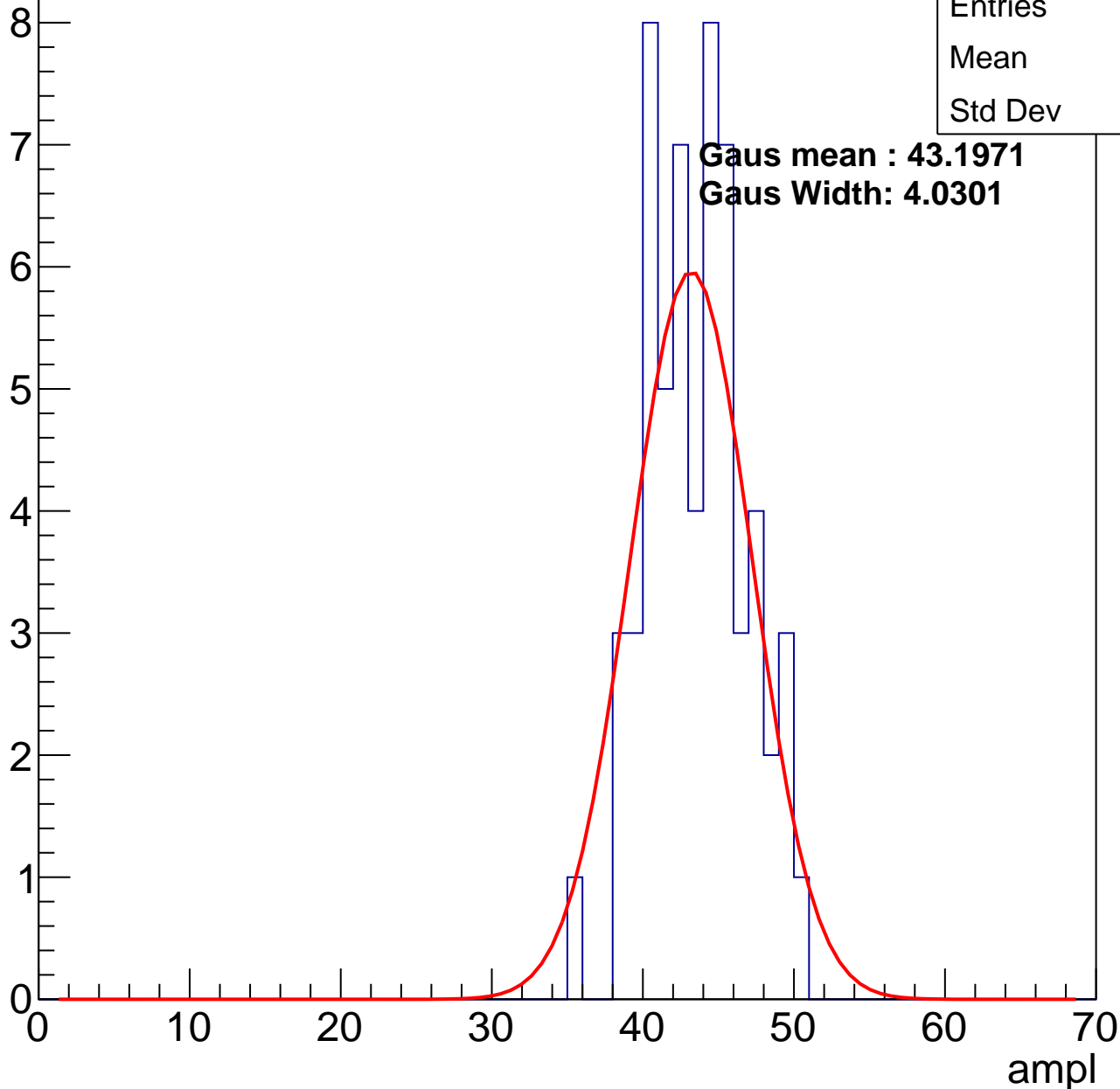
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	43.1
Std Dev	3.24

**Gaus mean : 43.1971**

**Gaus Width: 4.0301**

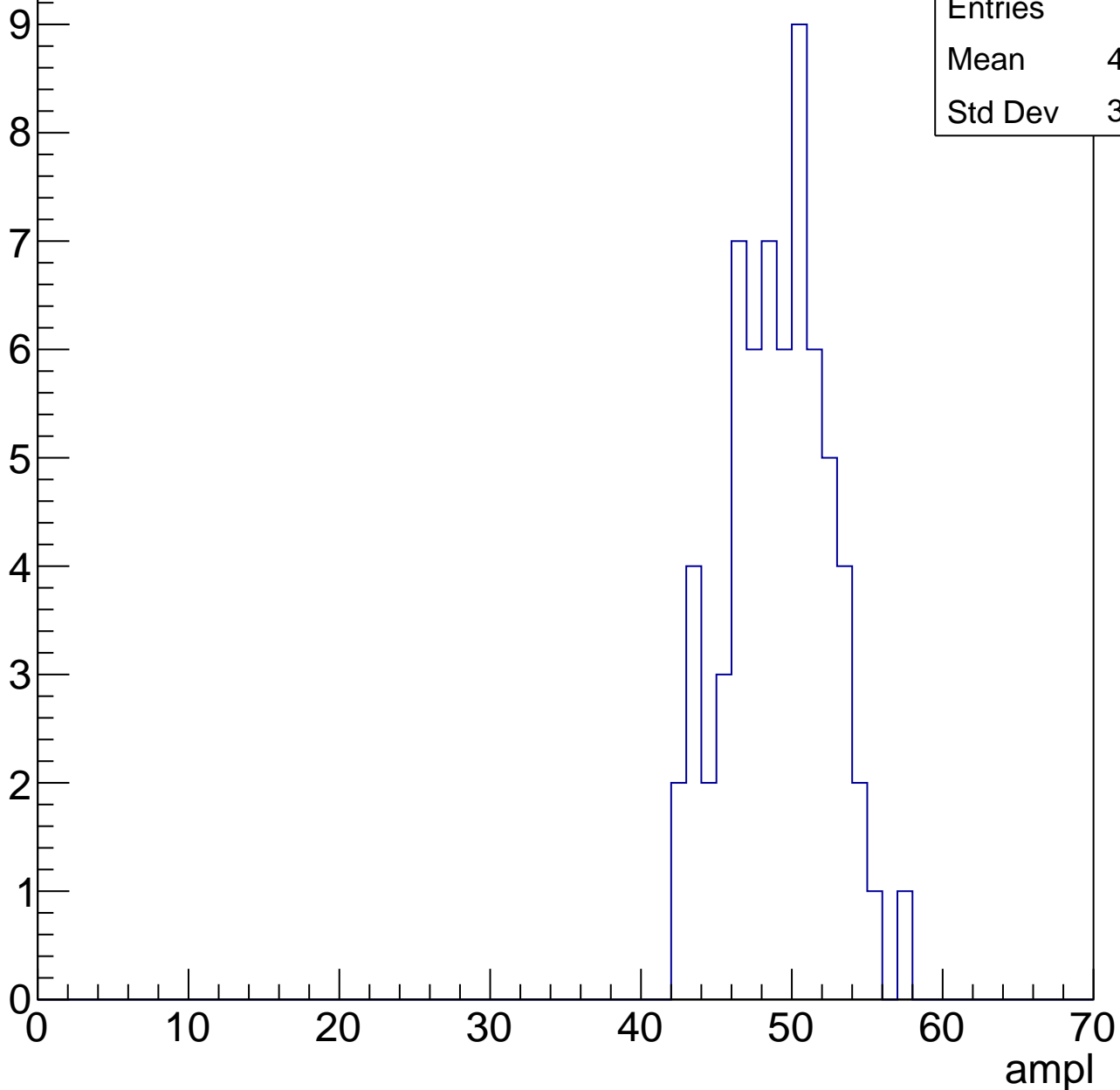


# B0L000S, U19-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.63
Std Dev	3.322

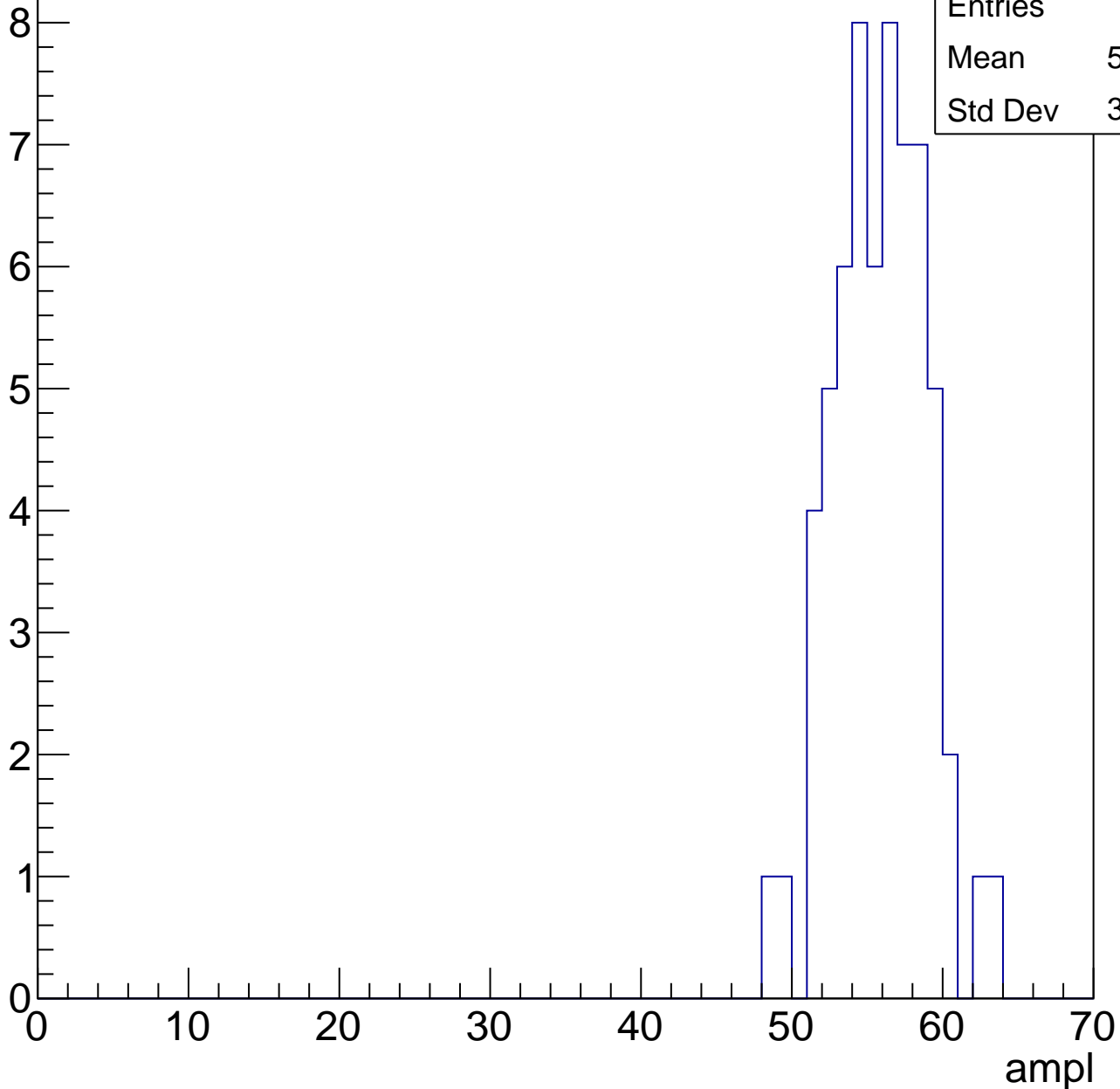


# B0L000S, U19-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.39
Std Dev	3.002

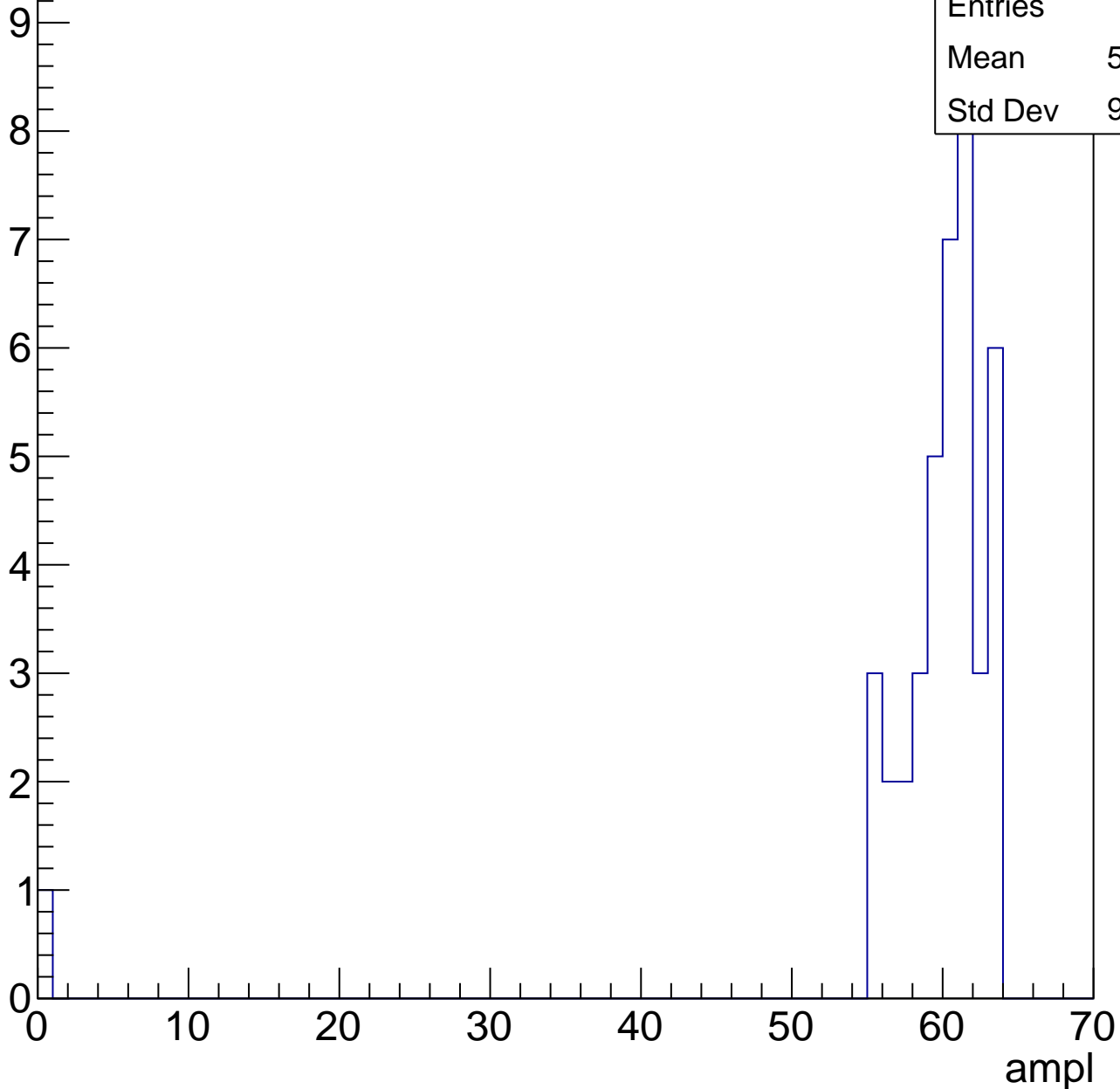


# B0L000S, U19-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.37
Std Dev	9.509

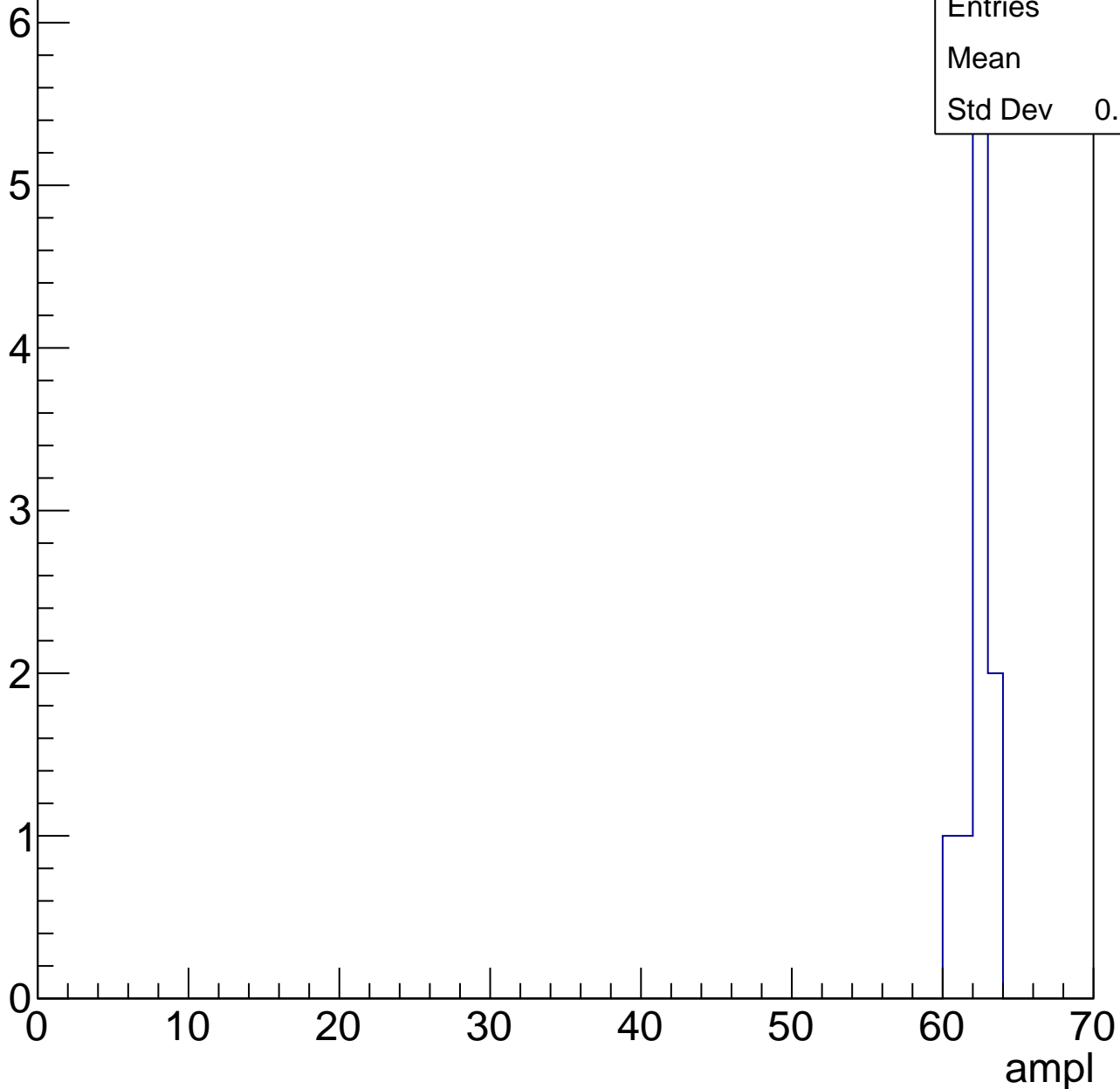


# B0L000S, U19-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	61.9
Std Dev	0.8307

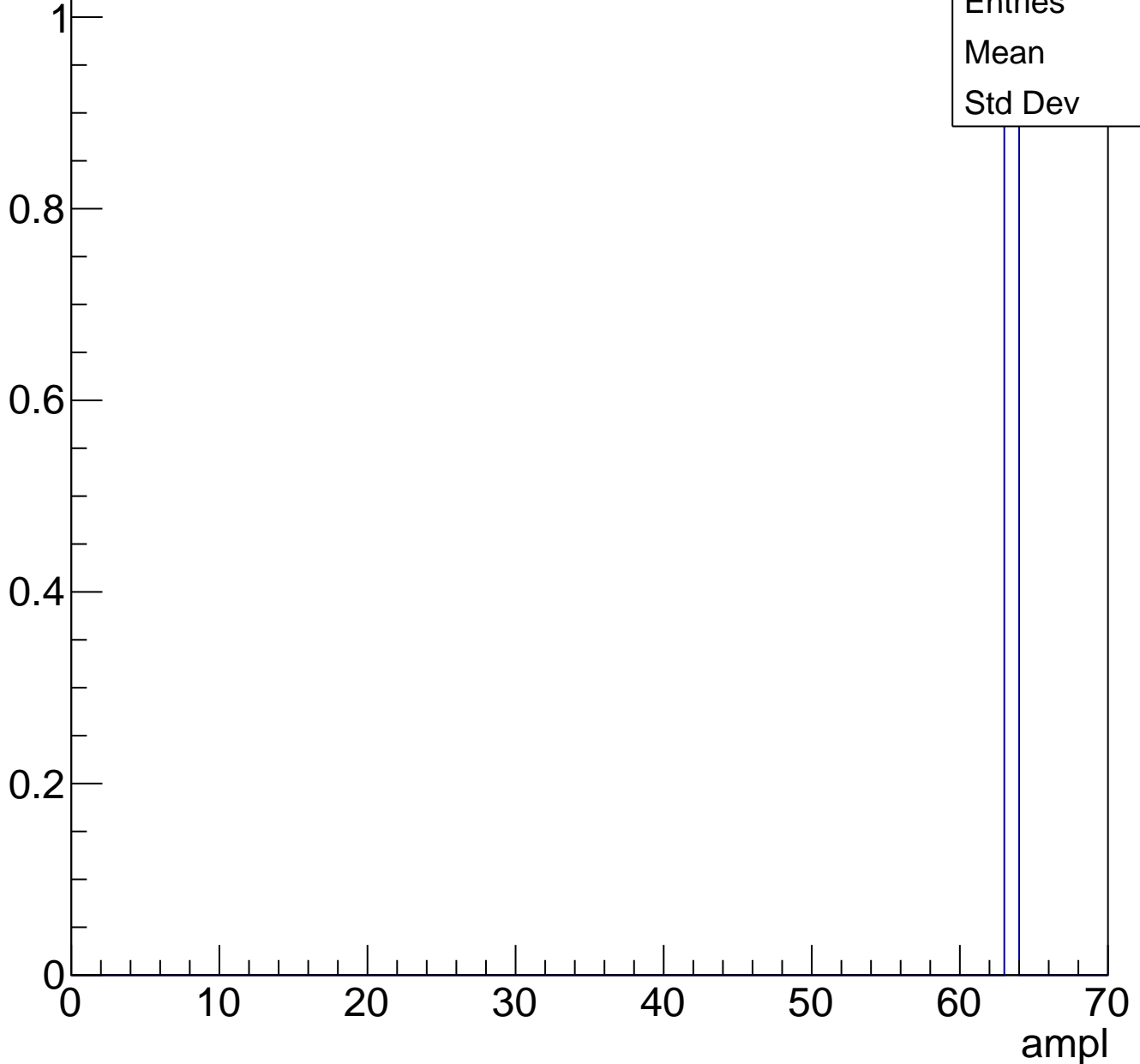




# B0L000S, U19-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0

# B0L000S, U19-ch109, adc0

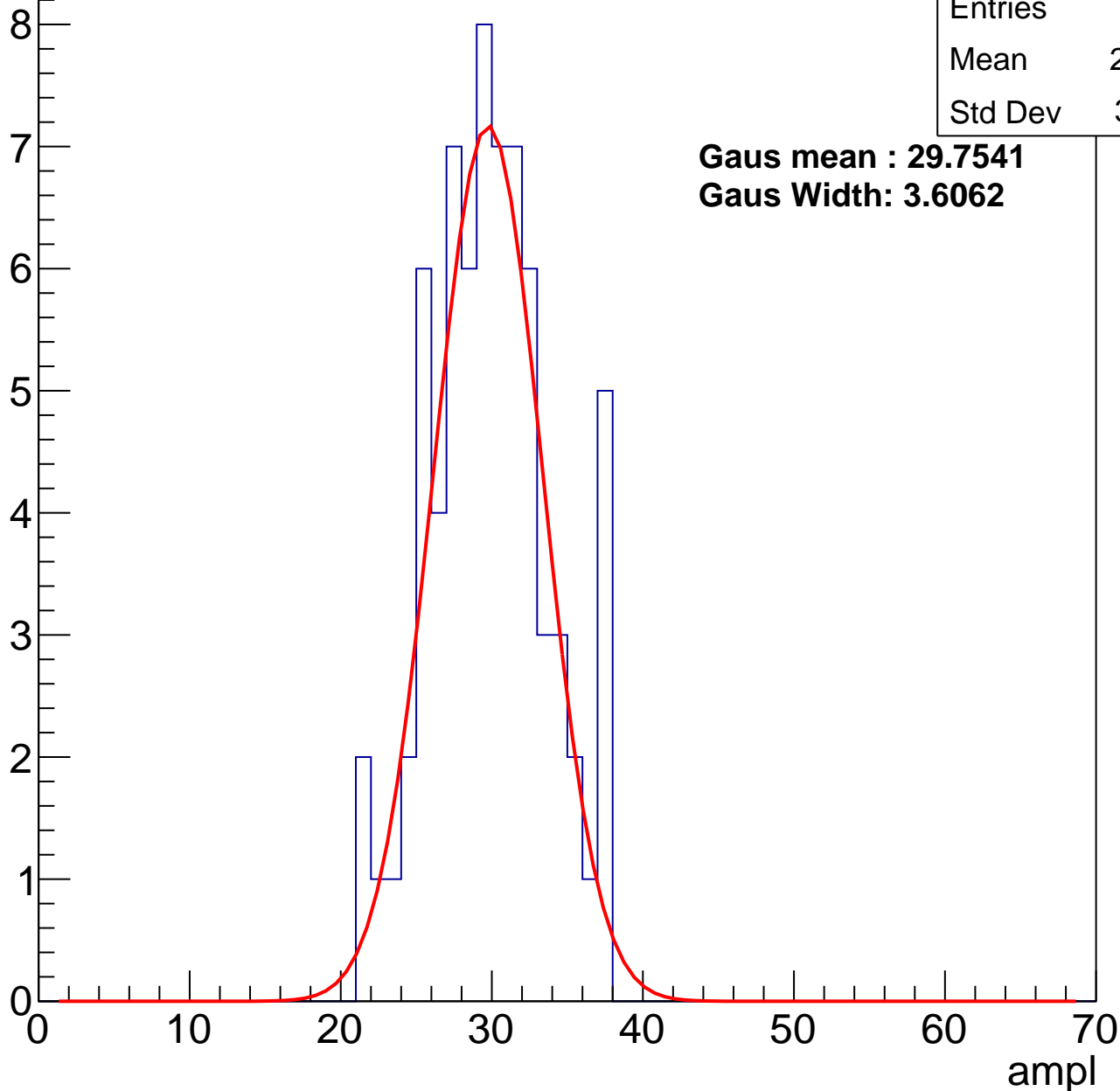
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	29.42
Std Dev	3.881

**Gaus mean : 29.7541**

**Gaus Width: 3.6062**



# B0L000S, U19-ch109, adc1

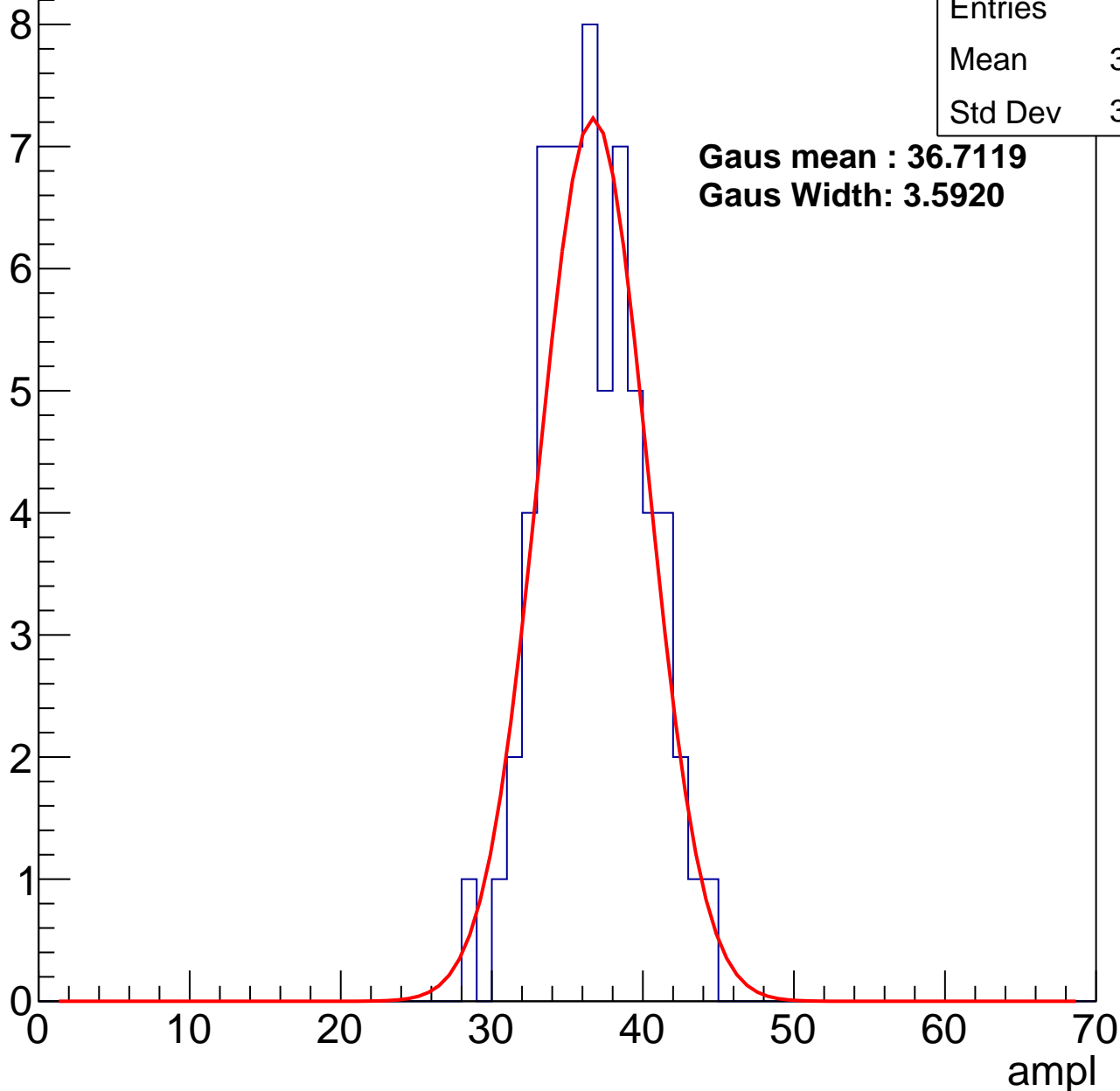
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.23
Std Dev	3.338

**Gaus mean : 36.7119**

**Gaus Width: 3.5920**



# B0L000S, U19-ch109, adc2

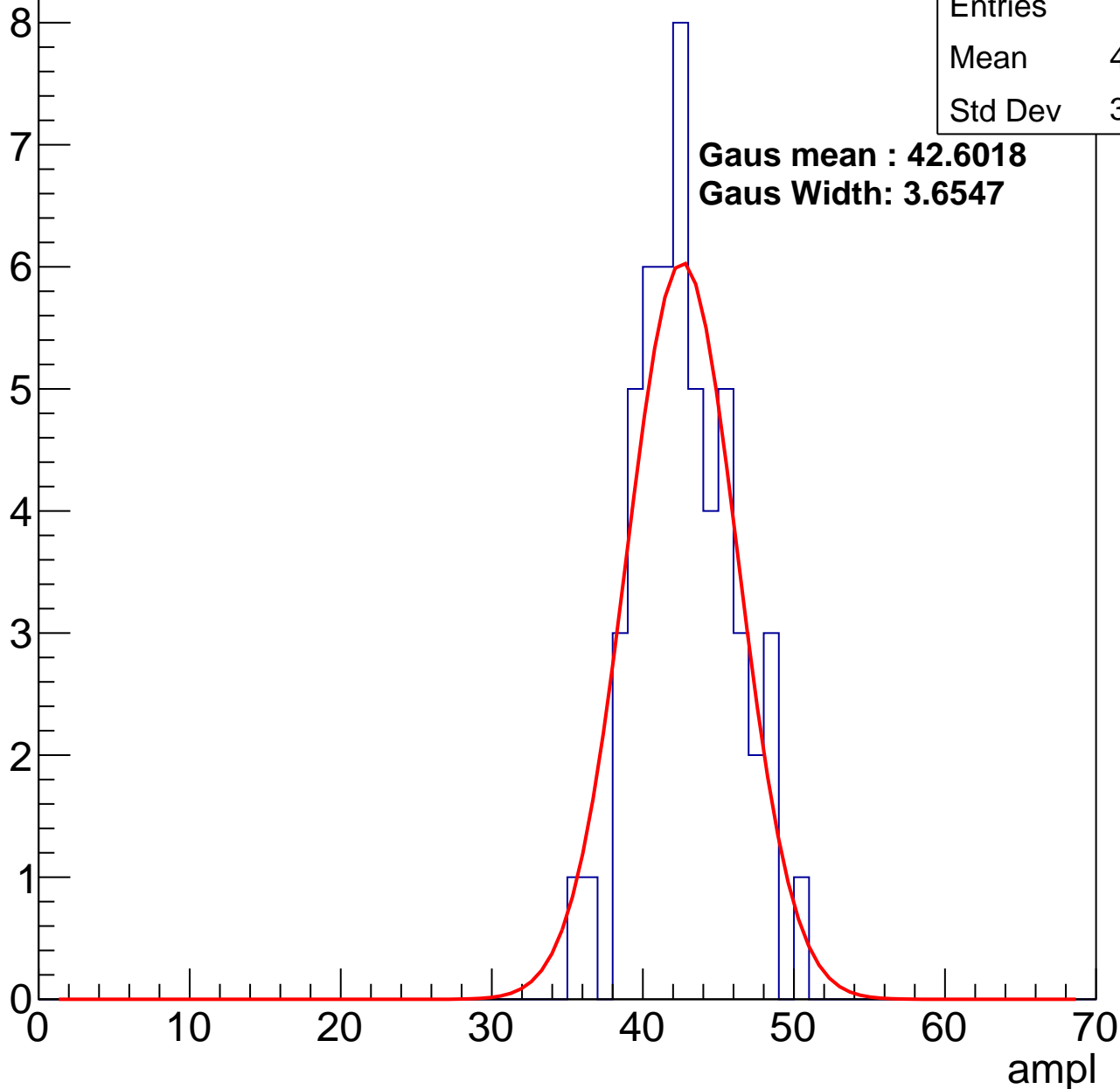
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	42.34
Std Dev	3.186

**Gaus mean : 42.6018**

**Gaus Width: 3.6547**



# B0L000S, U19-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

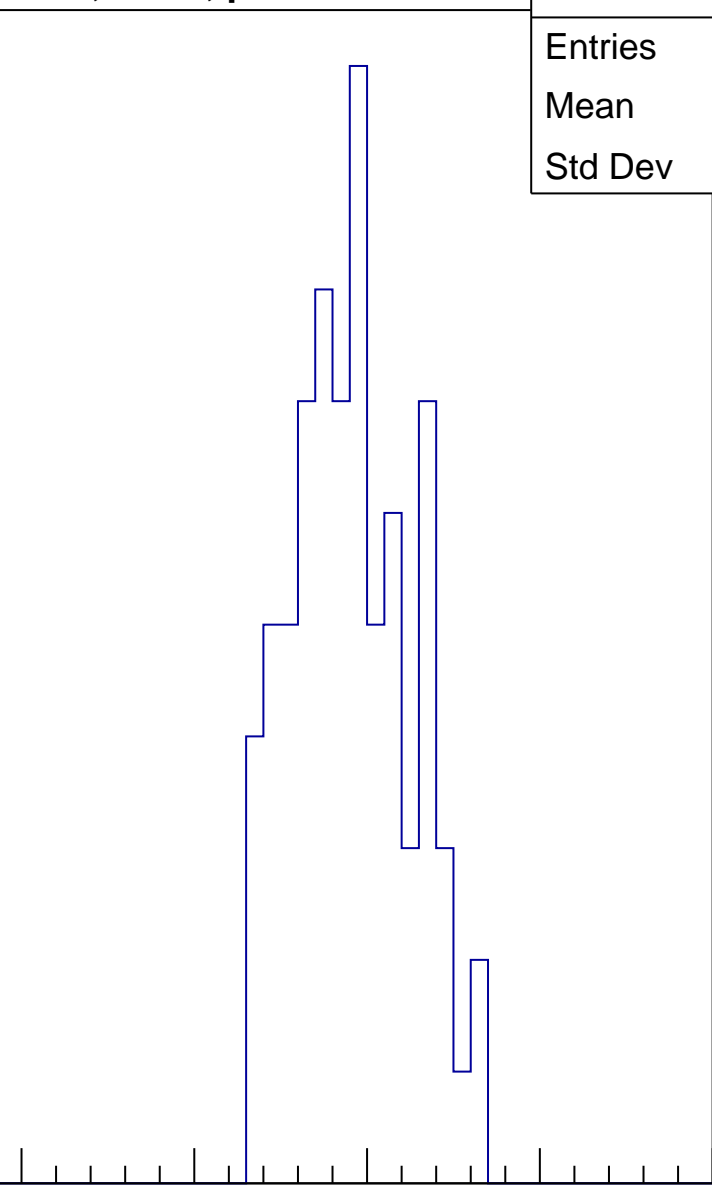
Entries	73
Mean	48.67
Std Dev	3.352

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

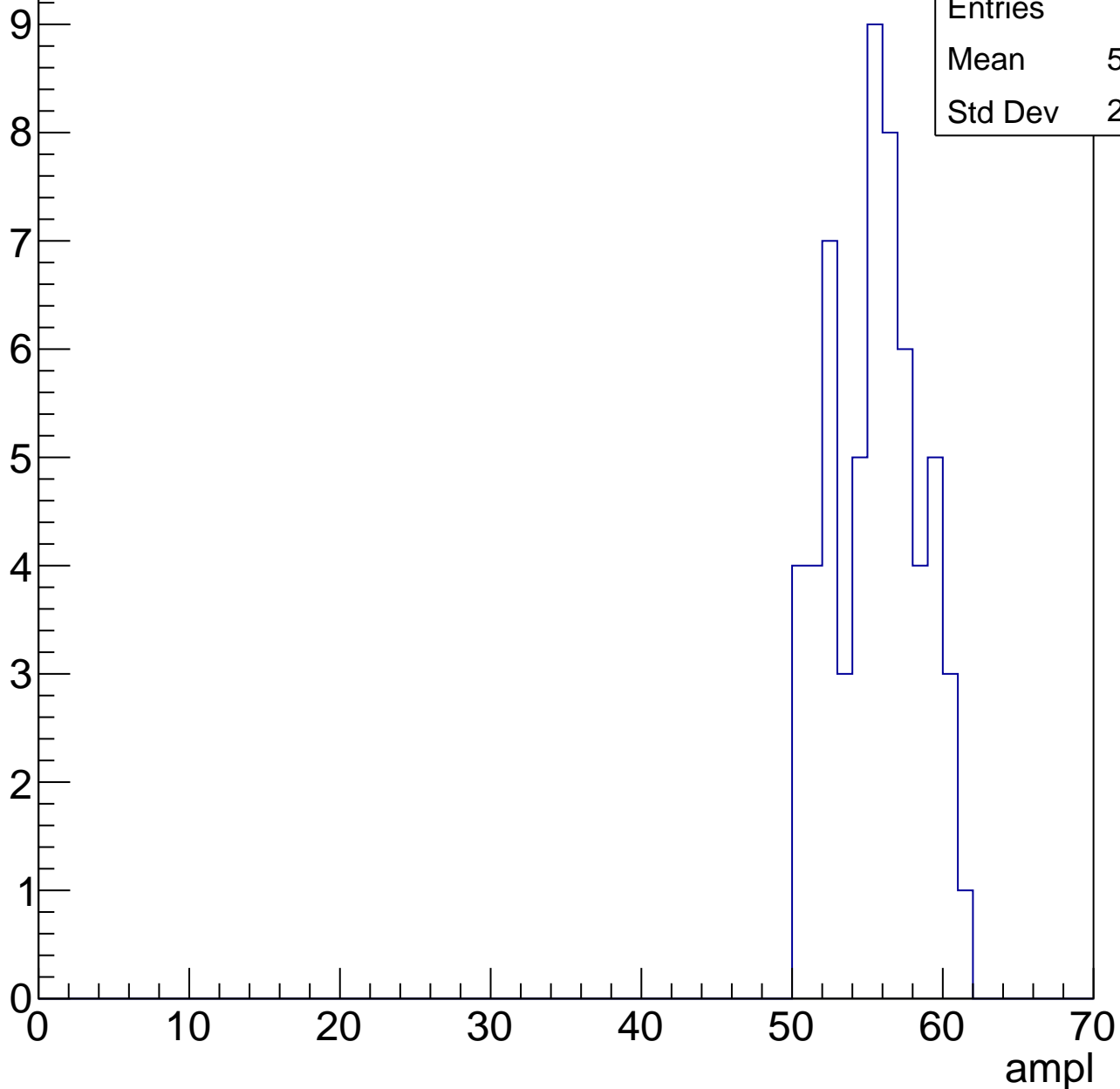


# B0L000S, U19-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.08
Std Dev	2.919

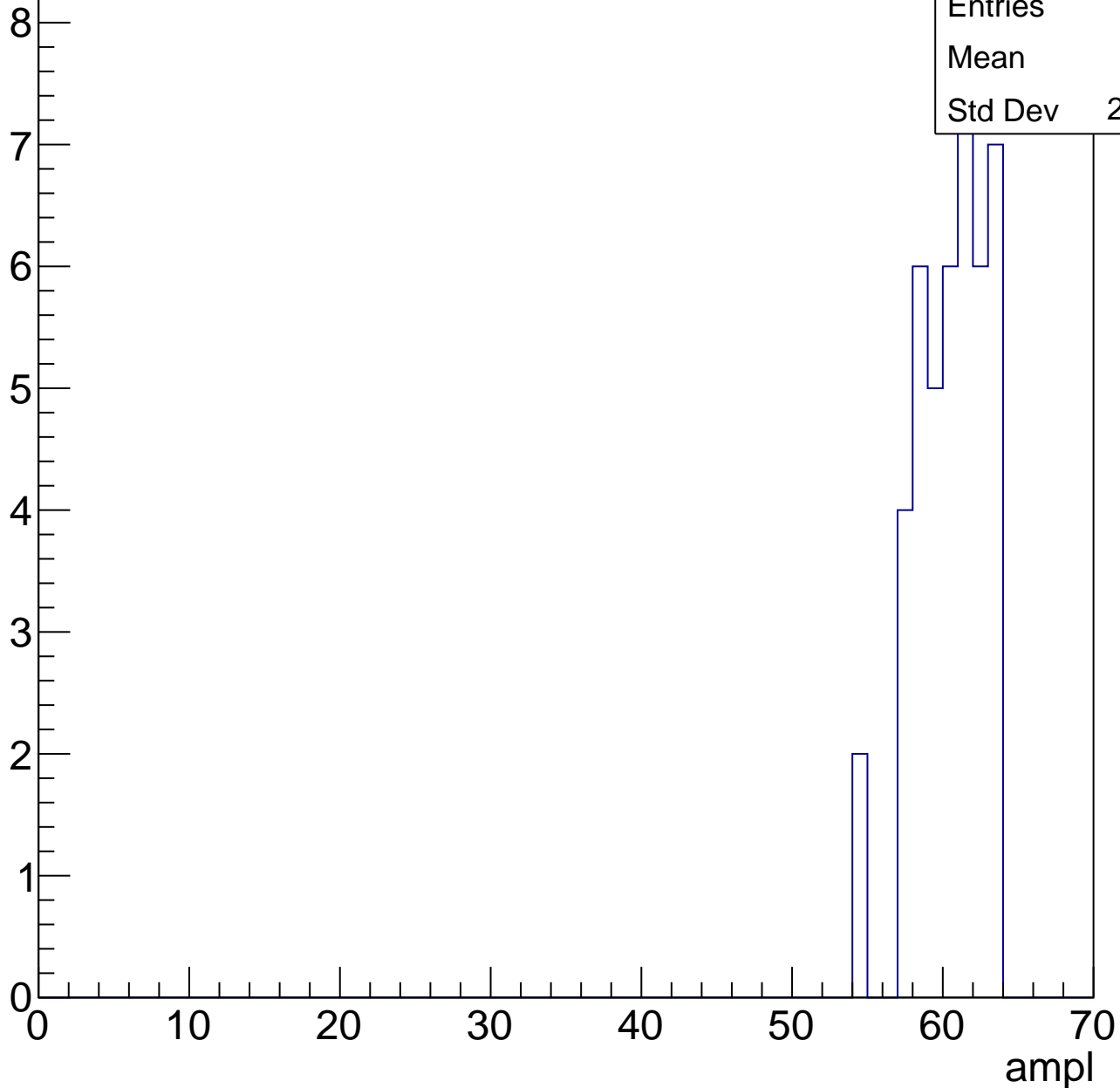


# B0L000S, U19-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

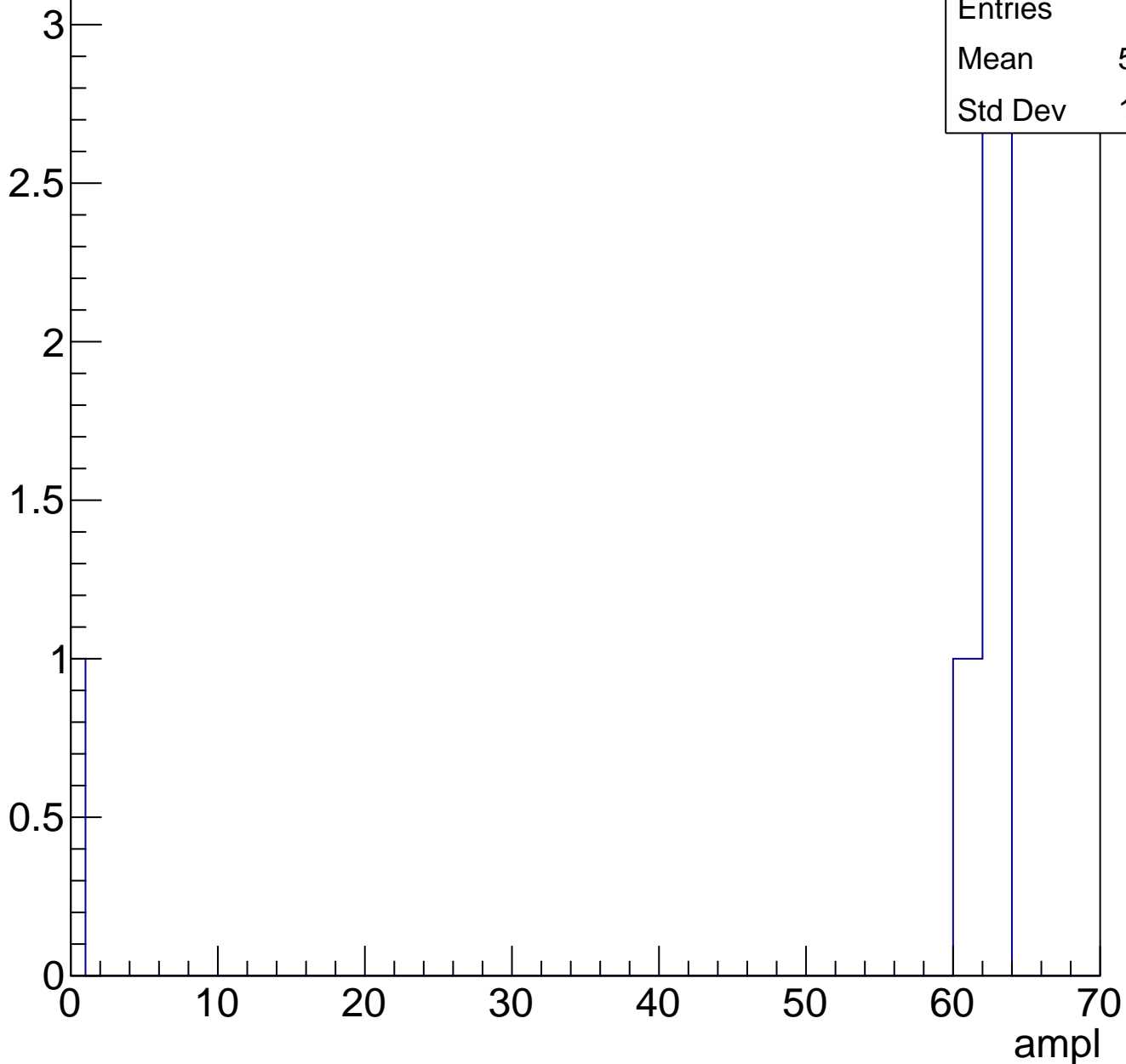
Entries	44
Mean	60
Std Dev	2.296



# B0L000S, U19-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch110, adc0

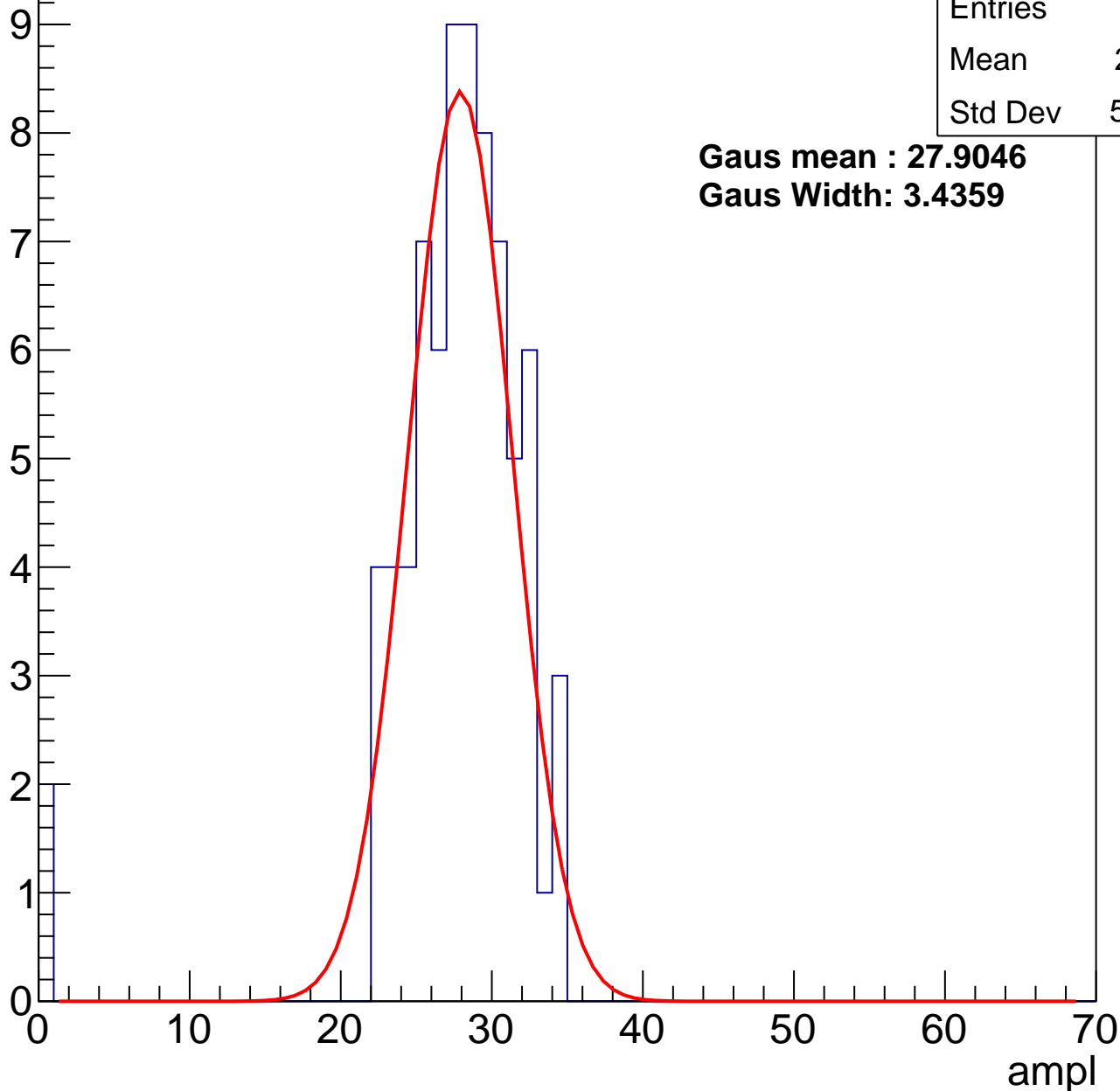
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	27.01
Std Dev	5.427

**Gaus mean : 27.9046**

**Gaus Width: 3.4359**



# B0L000S, U19-ch110, adc1

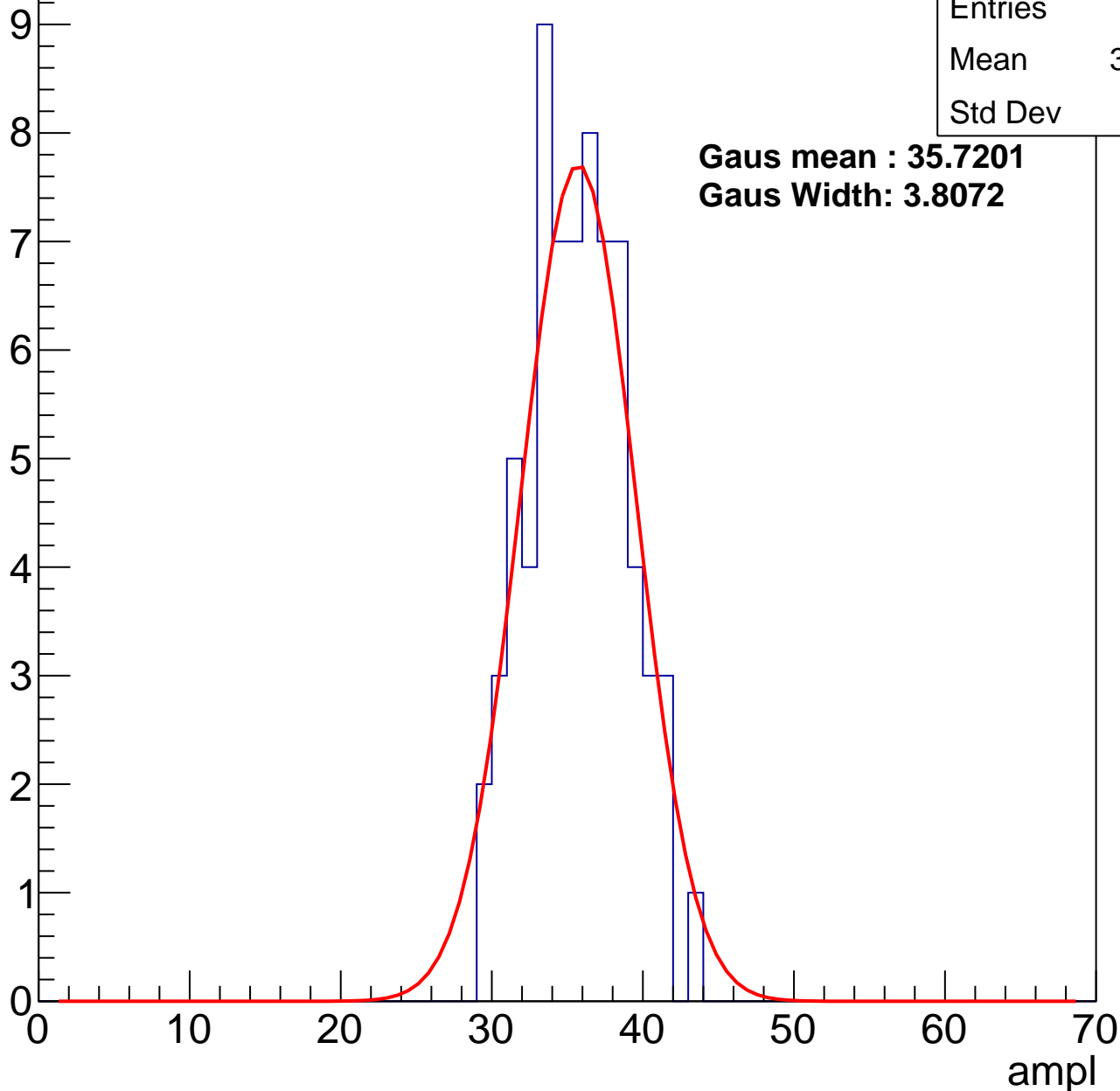
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.23
Std Dev	3.19

**Gaus mean : 35.7201**

**Gaus Width: 3.8072**



# B0L000S, U19-ch110, adc2

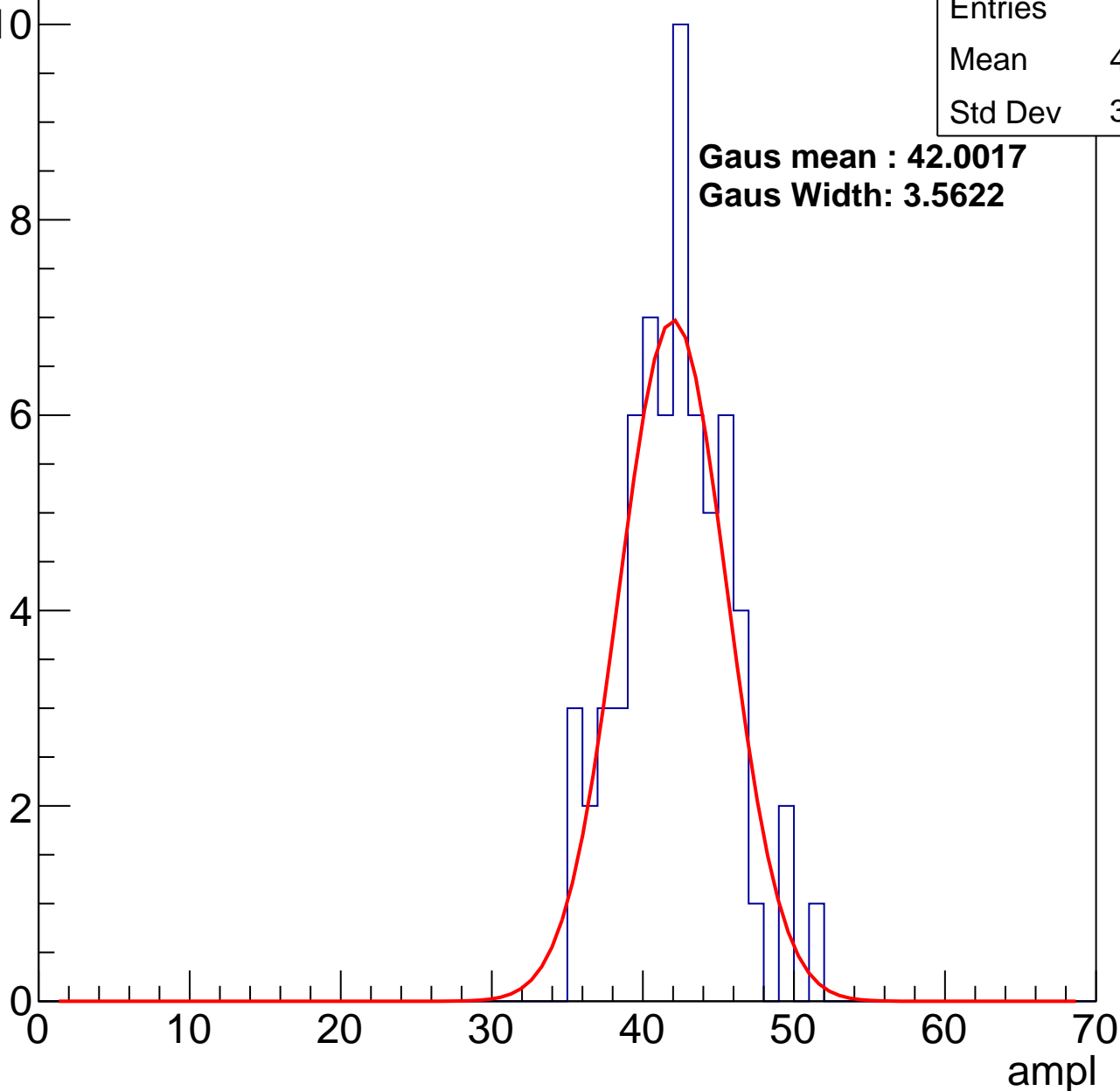
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	41.69
Std Dev	3.446

**Gaus mean : 42.0017**

**Gaus Width: 3.5622**

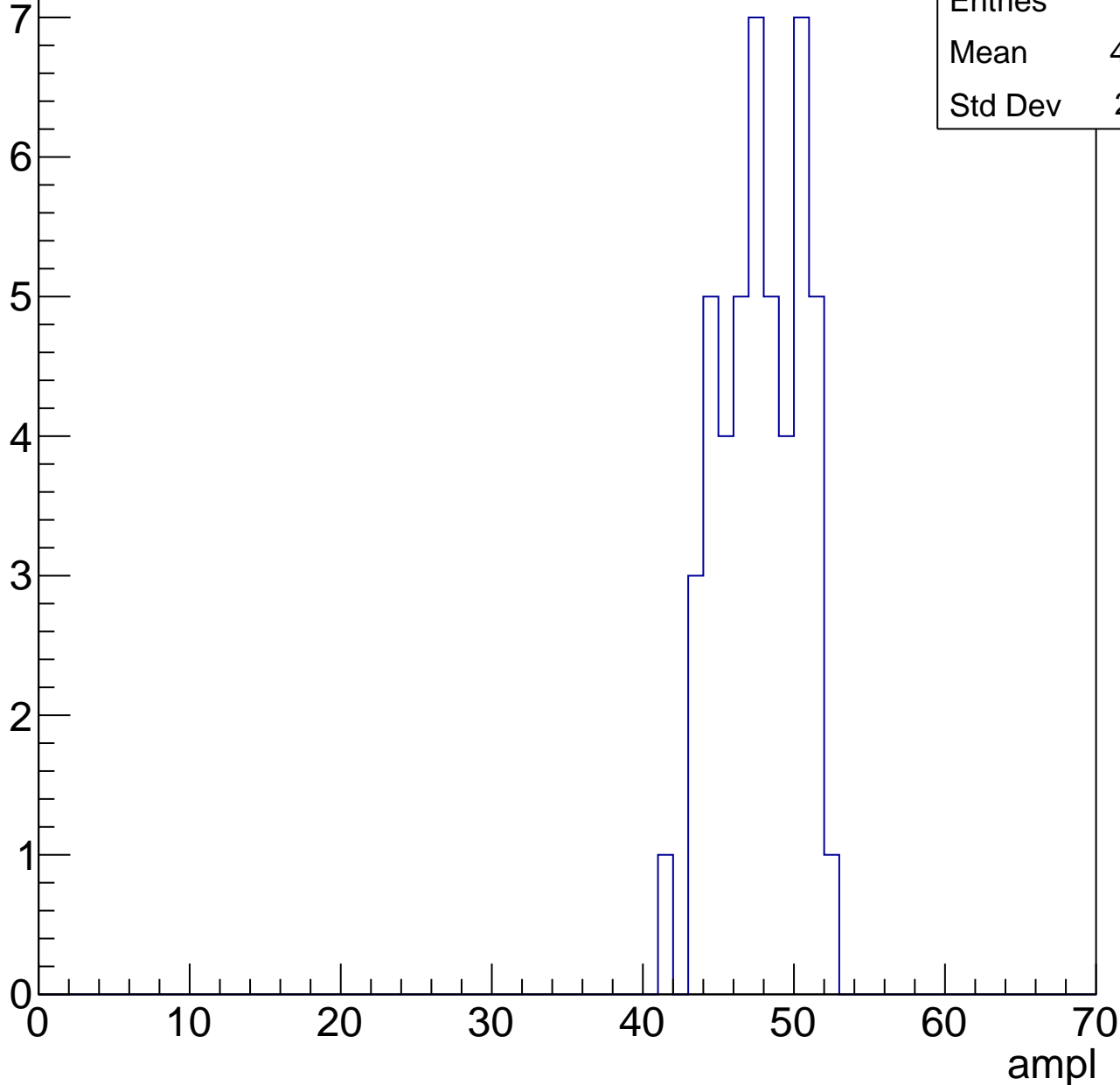


# B0L000S, U19-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	47.28
Std Dev	2.671

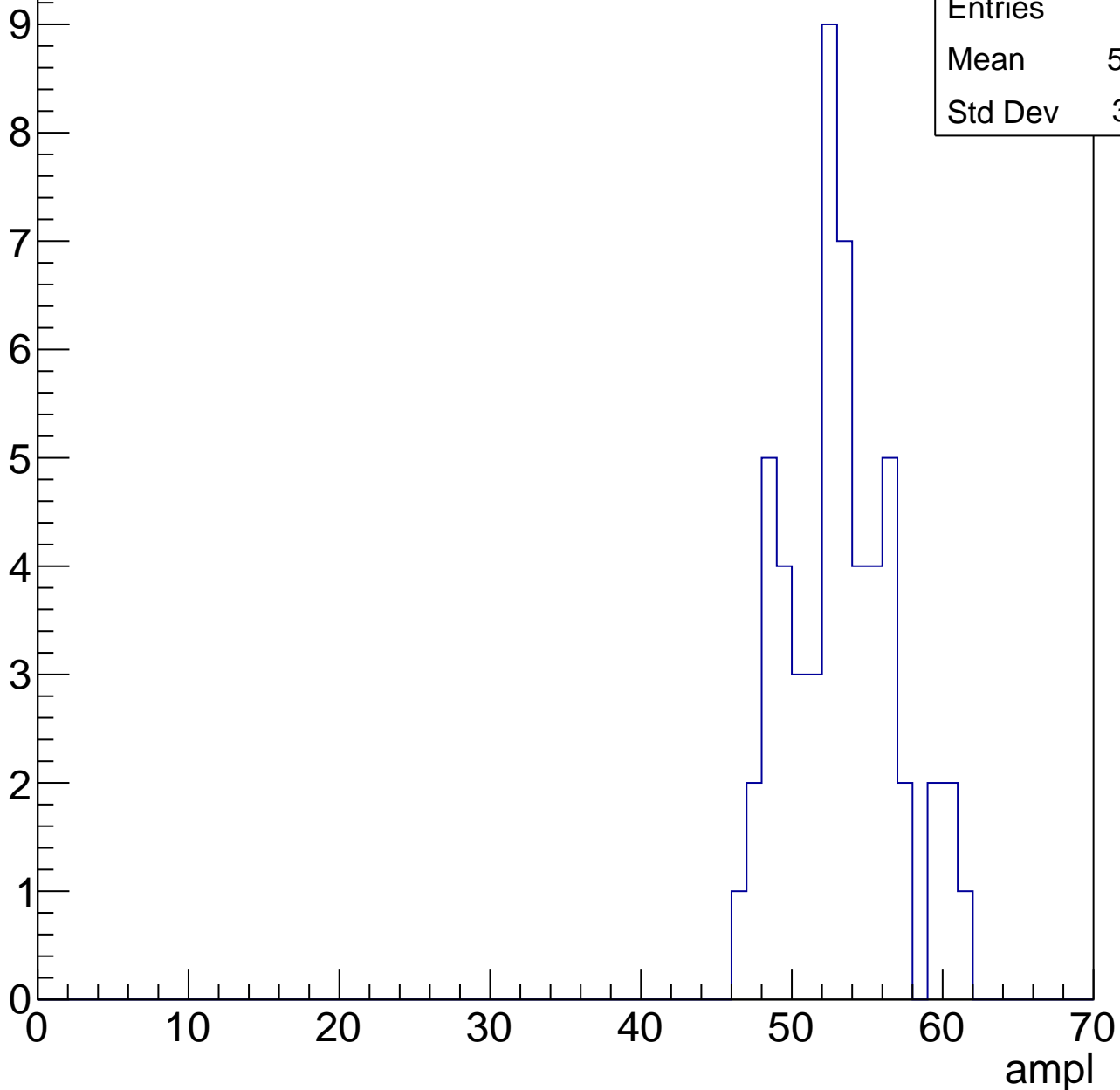


# B0L000S, U19-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	52.72
Std Dev	3.561

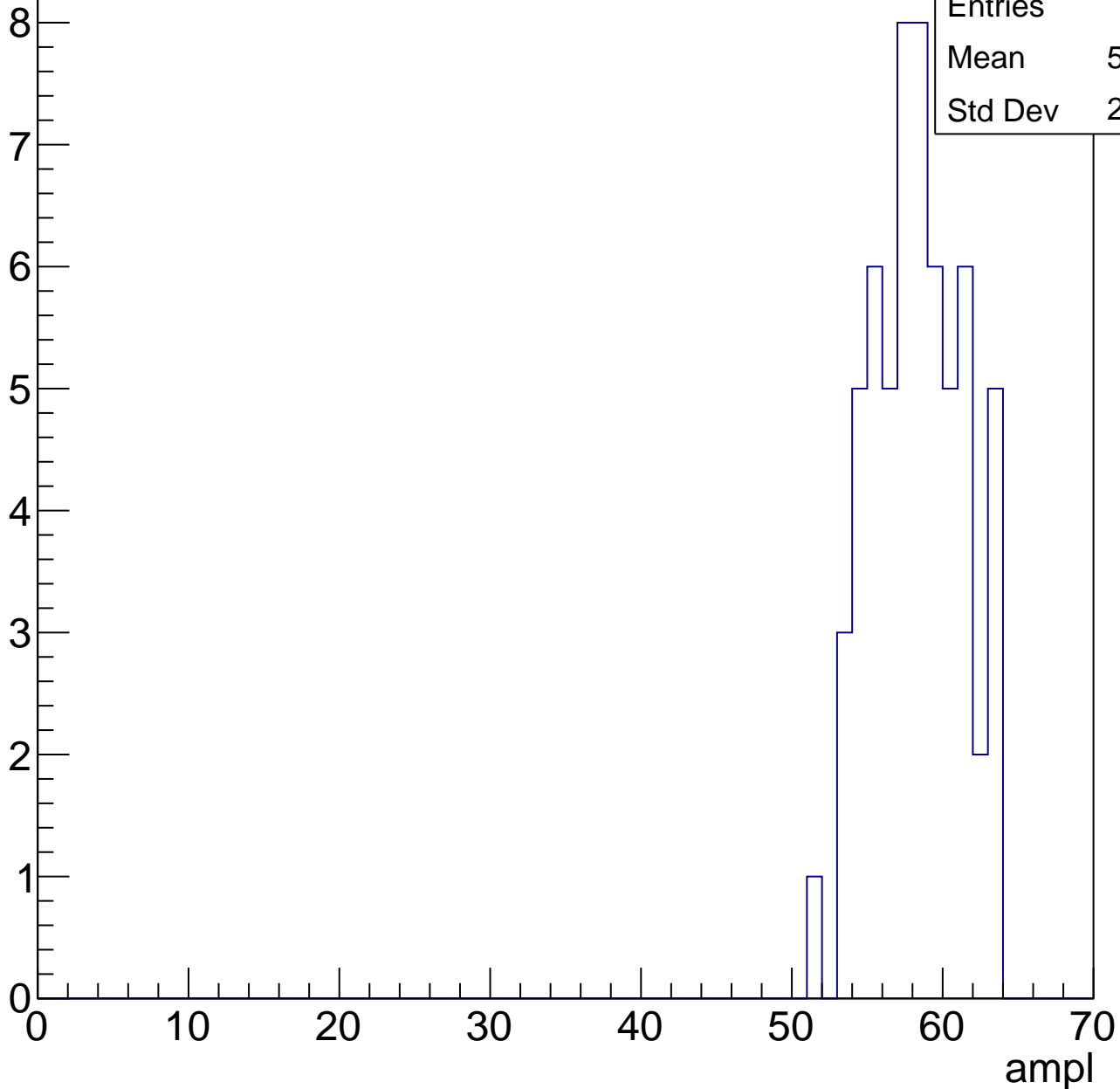


# B0L000S, U19-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57.82
Std Dev	2.947

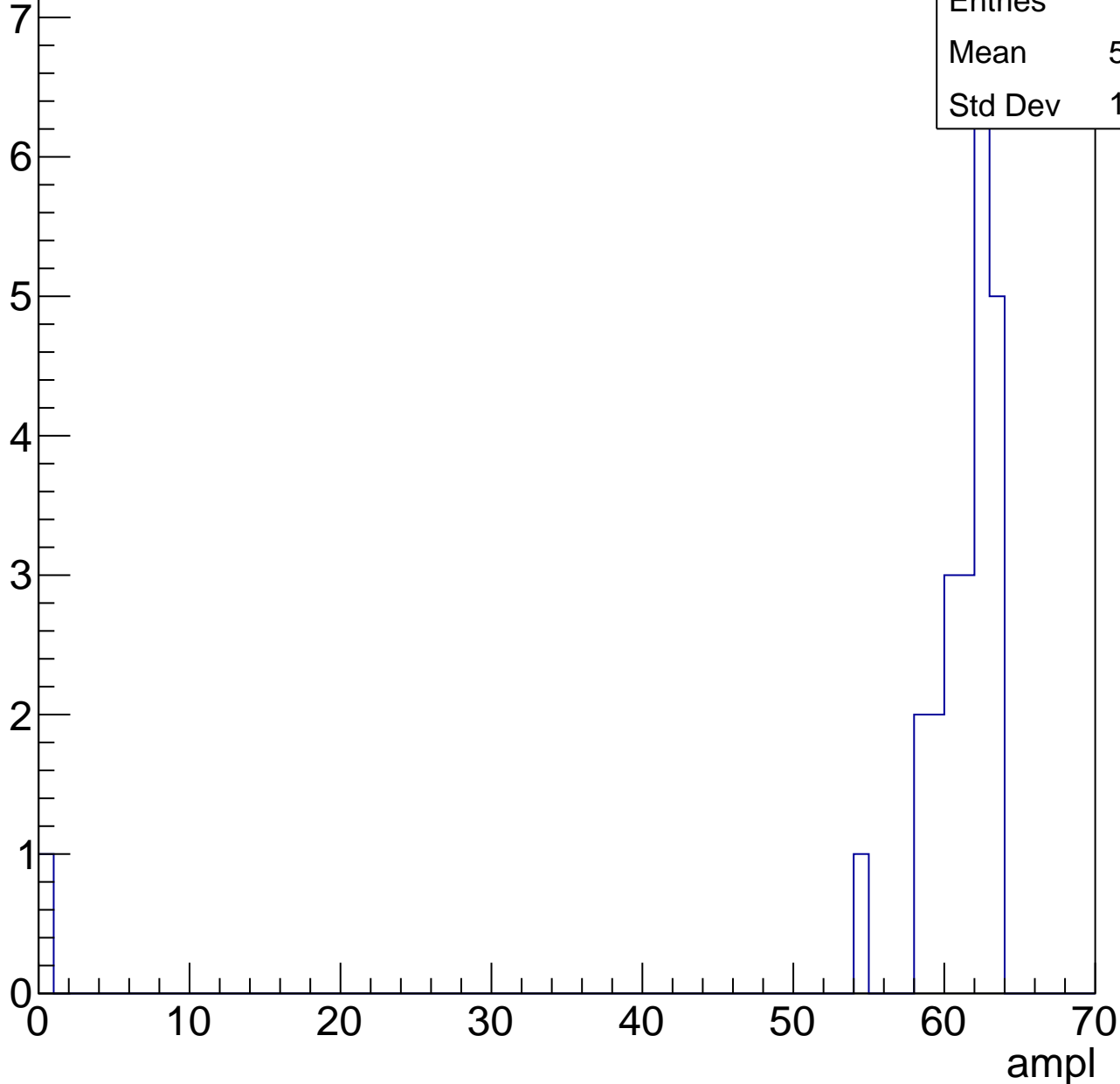


# B0L000S, U19-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	24
Mean	58.33
Std Dev	12.34

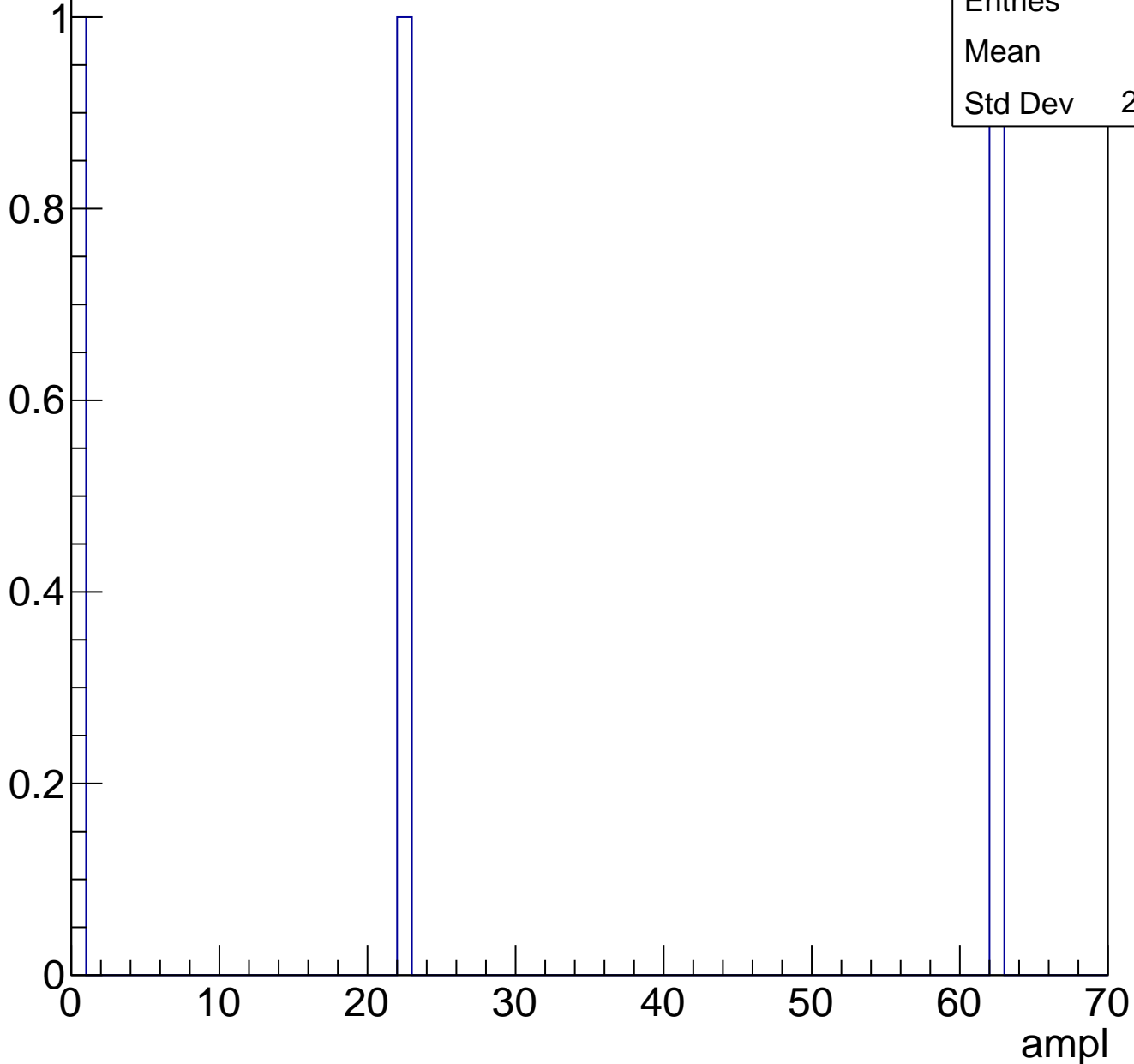




# B0L000S, U19-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28
Std Dev	25.66

# B0L000S, U19-ch111, adc0

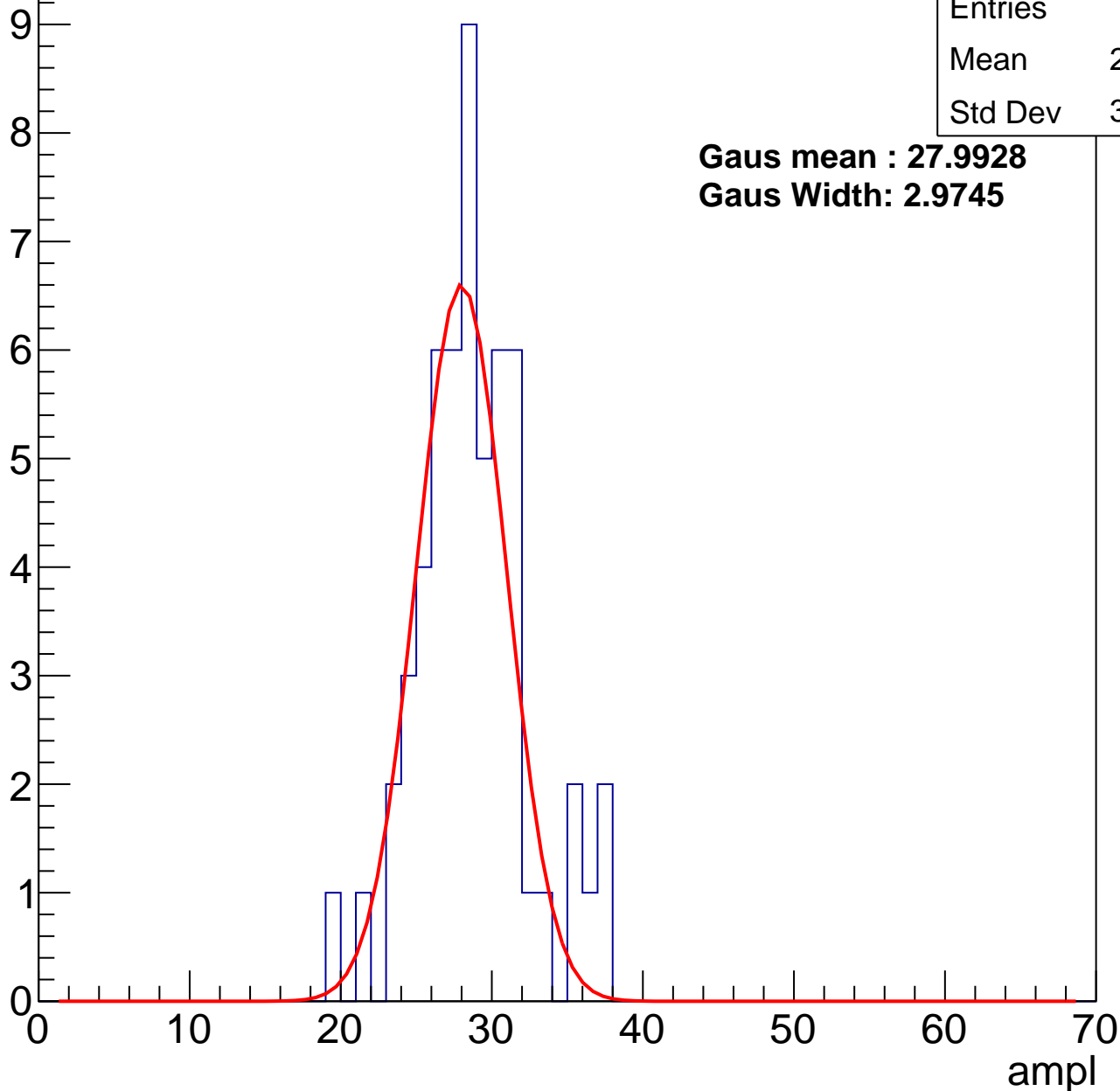
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	28.29
Std Dev	3.629

**Gaus mean : 27.9928**

**Gaus Width: 2.9745**



# B0L000S, U19-ch111, adc1

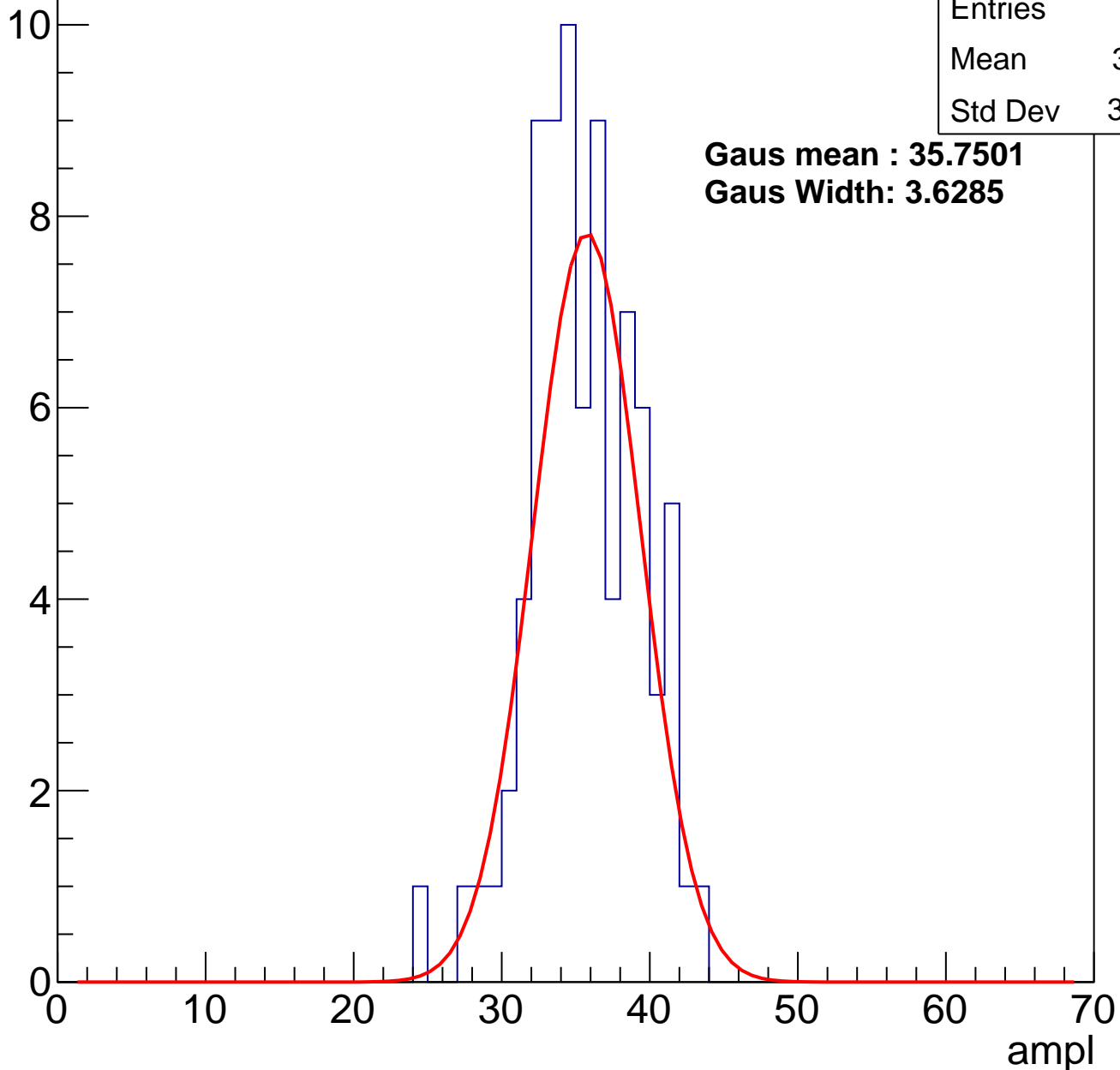
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	35.11
Std Dev	3.643

**Gaus mean : 35.7501**

**Gaus Width: 3.6285**

Entry



# B0L000S, U19-ch111, adc2

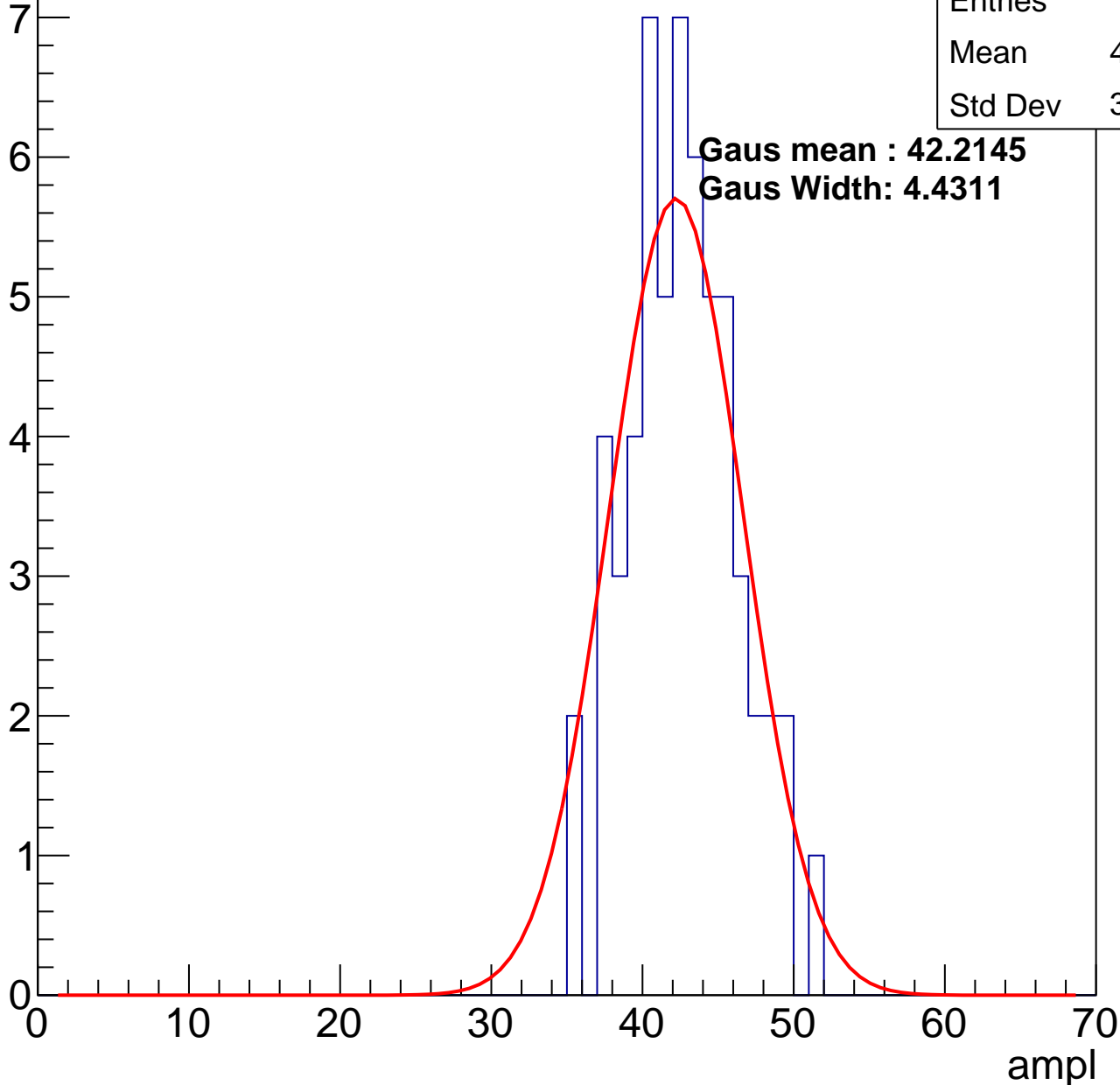
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.19
Std Dev	3.555

**Gaus mean : 42.2145**

**Gaus Width: 4.4311**

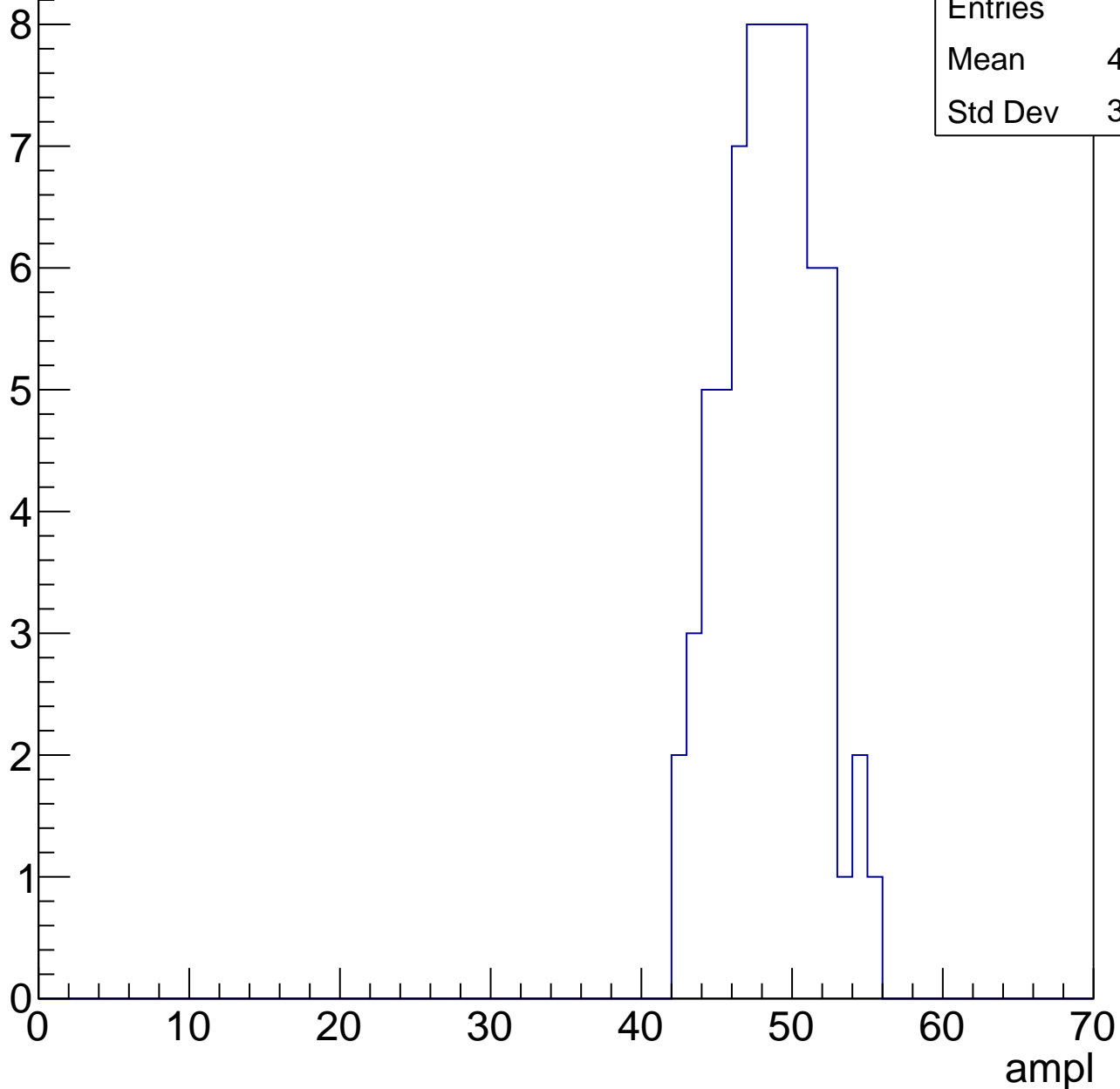


# B0L000S, U19-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	48.09
Std Dev	3.032

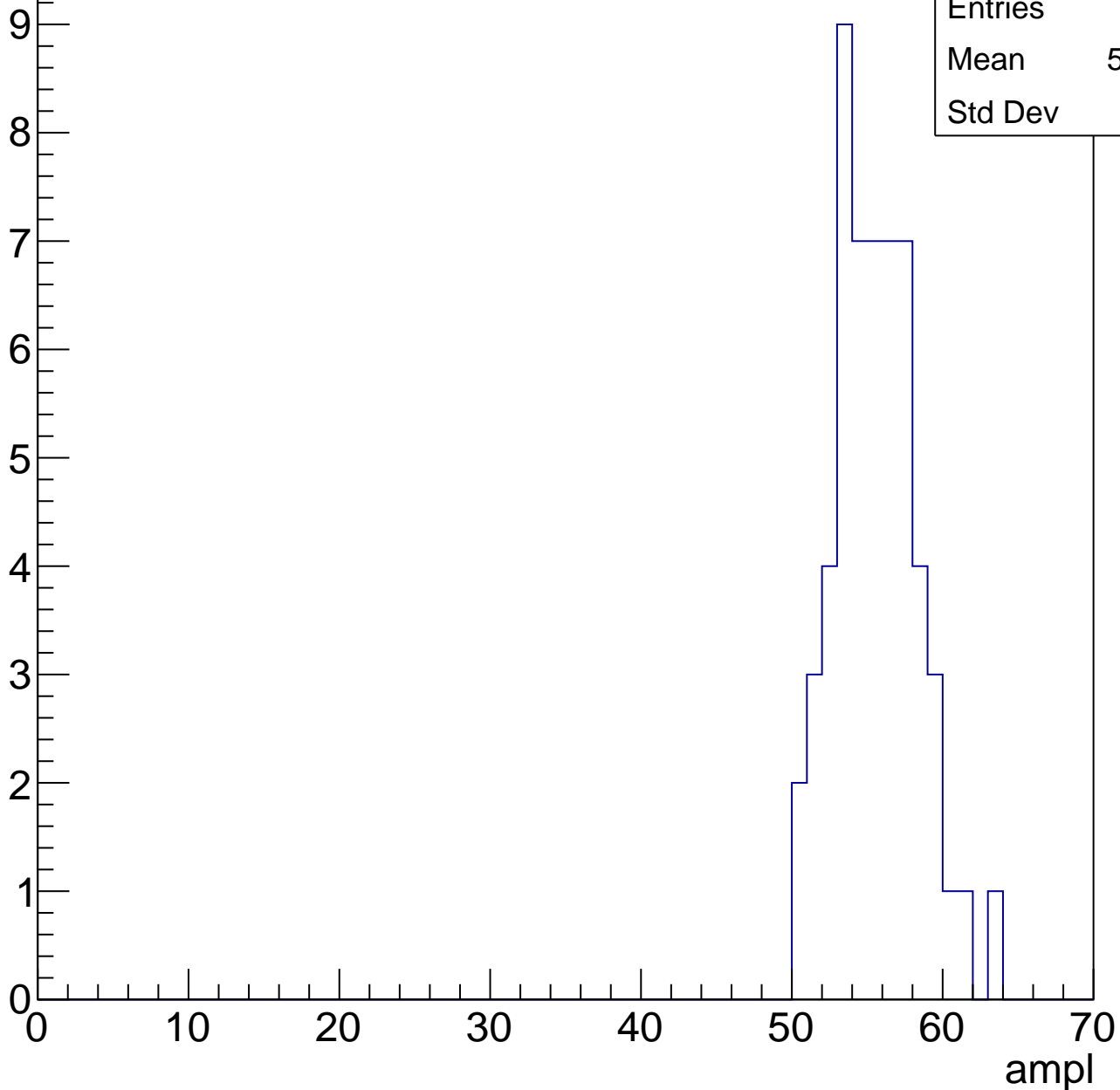


# B0L000S, U19-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.09
Std Dev	2.74

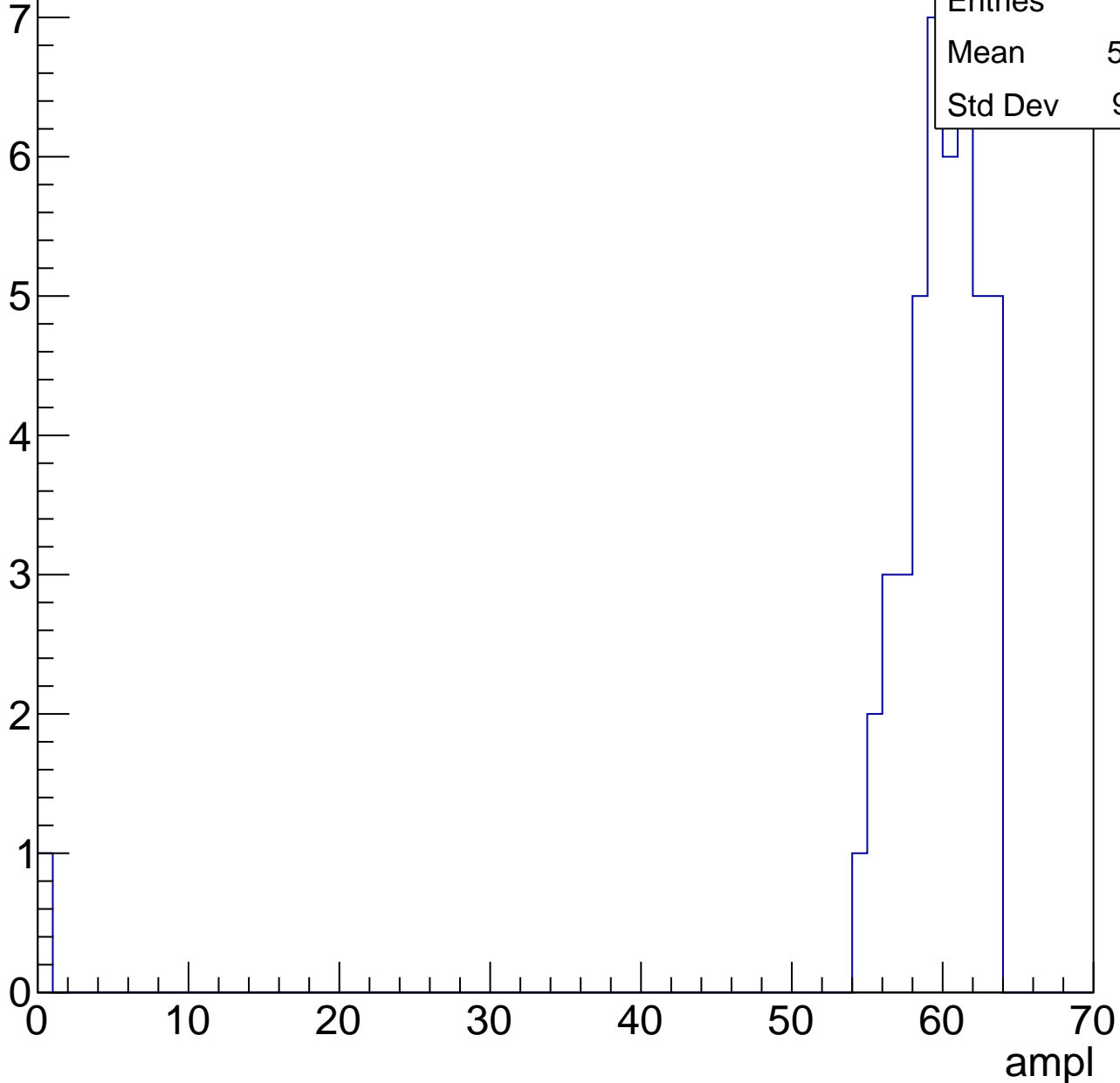


# B0L000S, U19-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.18
Std Dev	9.081

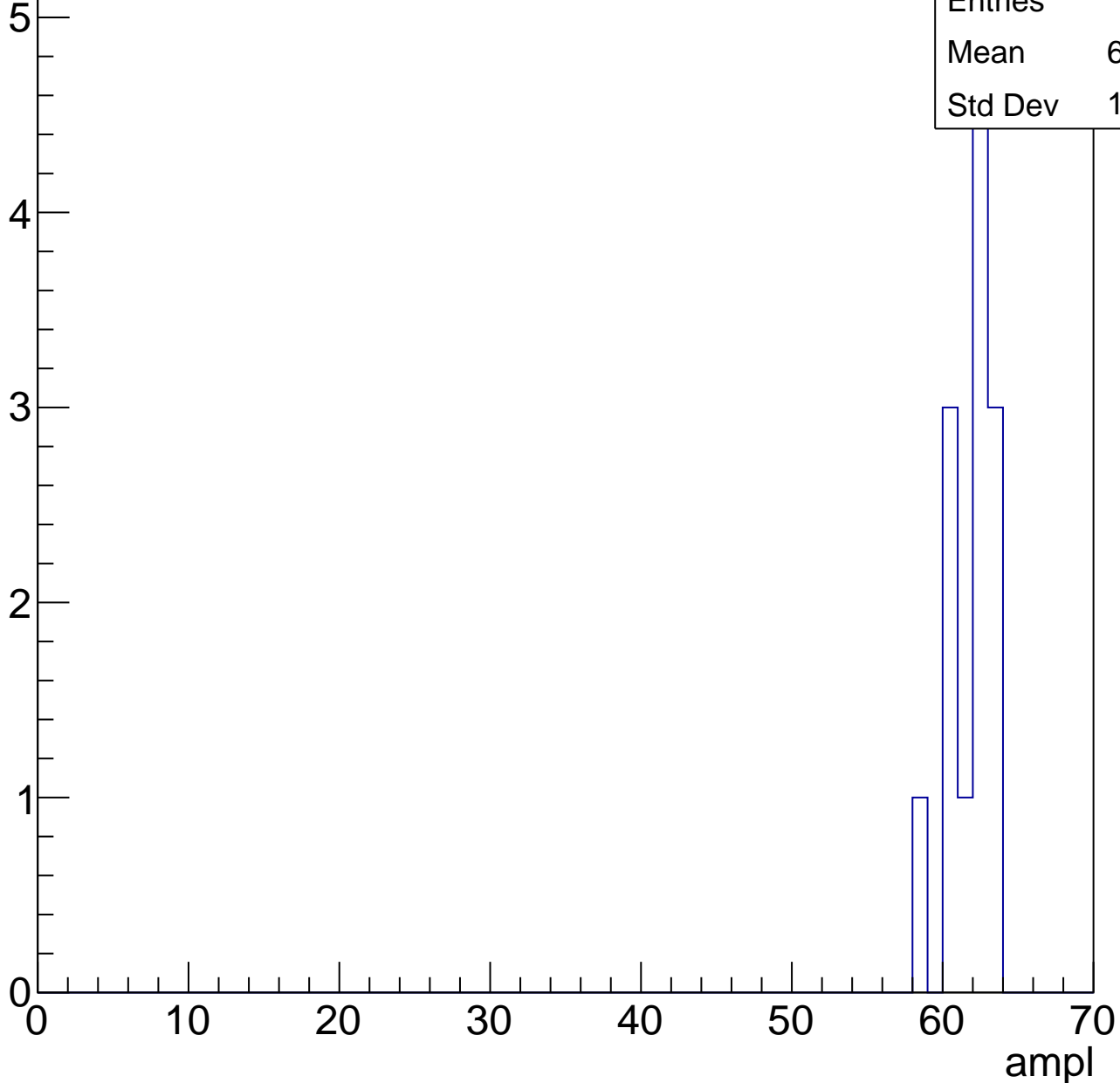


# B0L000S, U19-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	61.38
Std Dev	1.443

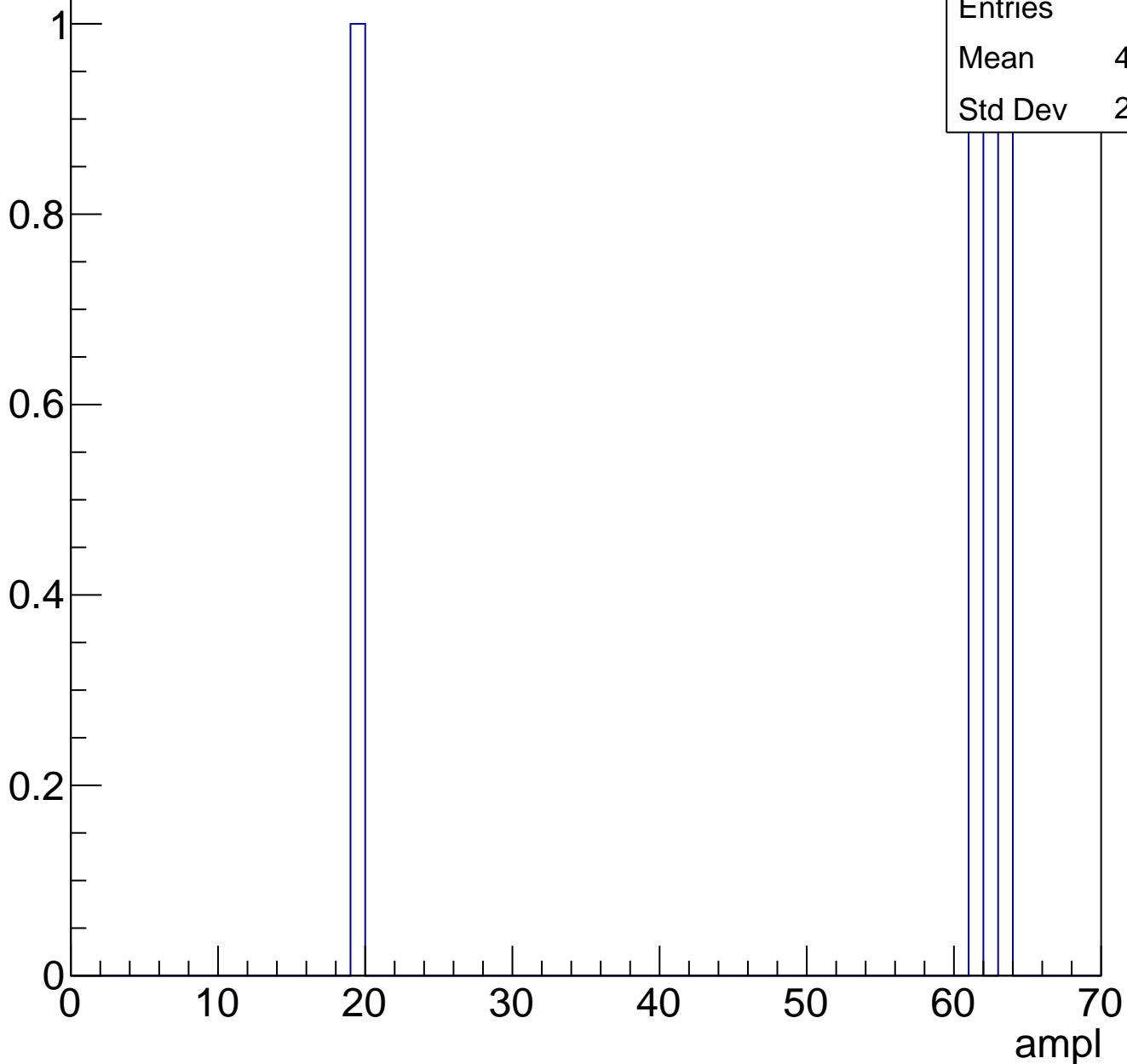




# B0L000S, U19-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch112, adc0

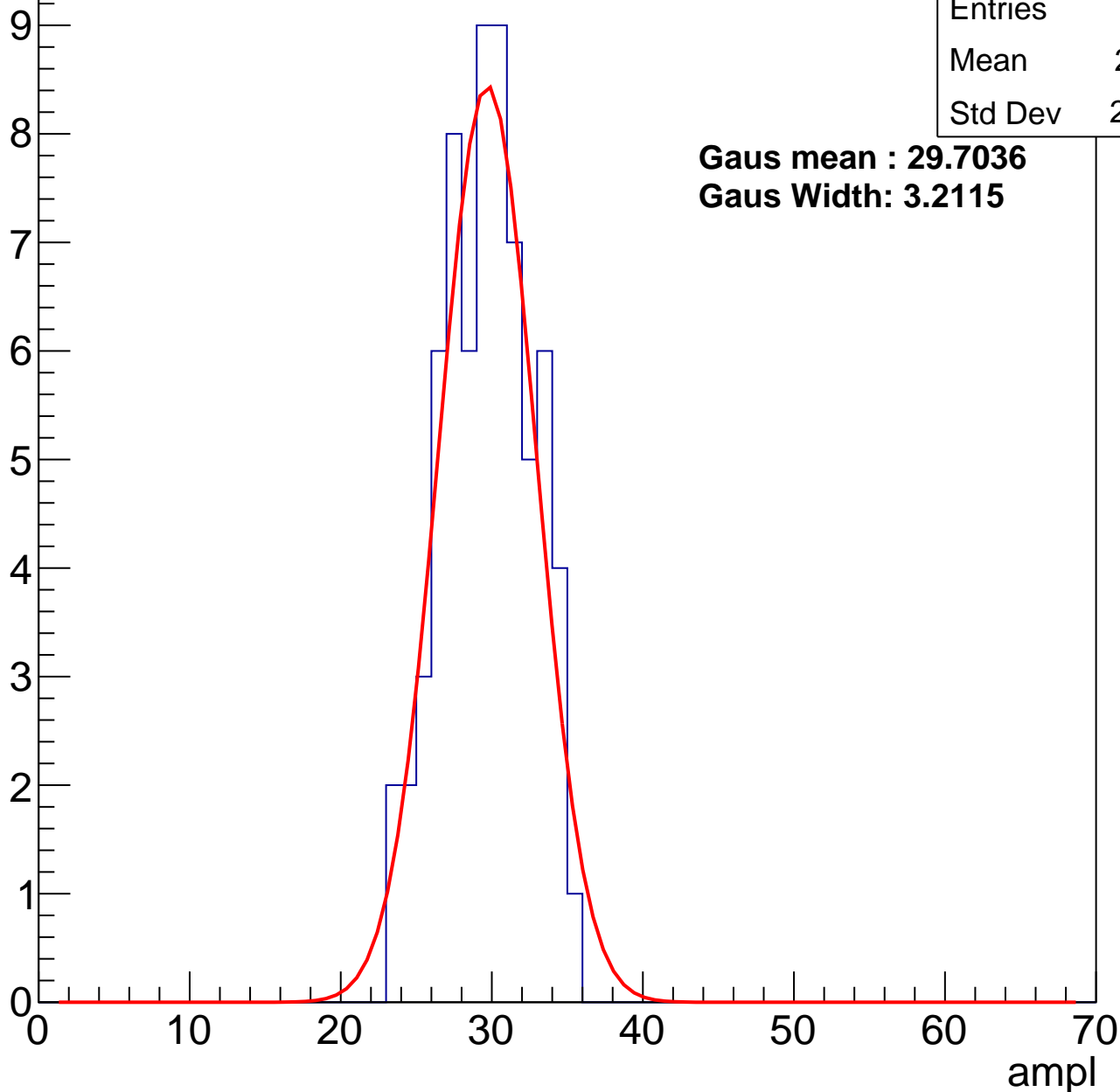
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	29.21
Std Dev	2.903

**Gaus mean : 29.7036**

**Gaus Width: 3.2115**



# B0L000S, U19-ch112, adc1

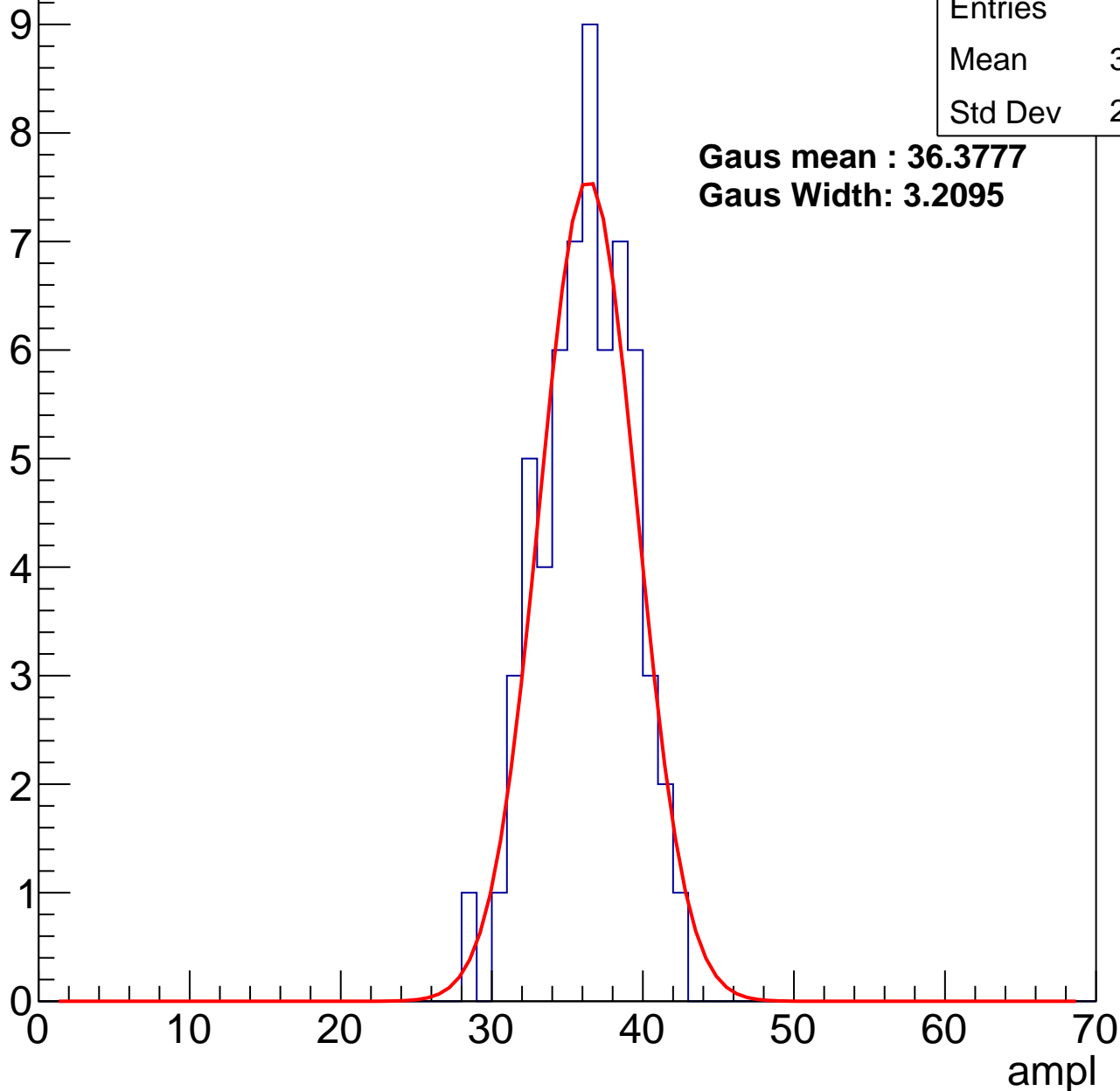
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.77
Std Dev	2.977

**Gaus mean : 36.3777**

**Gaus Width: 3.2095**



# B0L000S, U19-ch112, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	41.97
Std Dev	3.127

**Gaus mean : 42.7541**

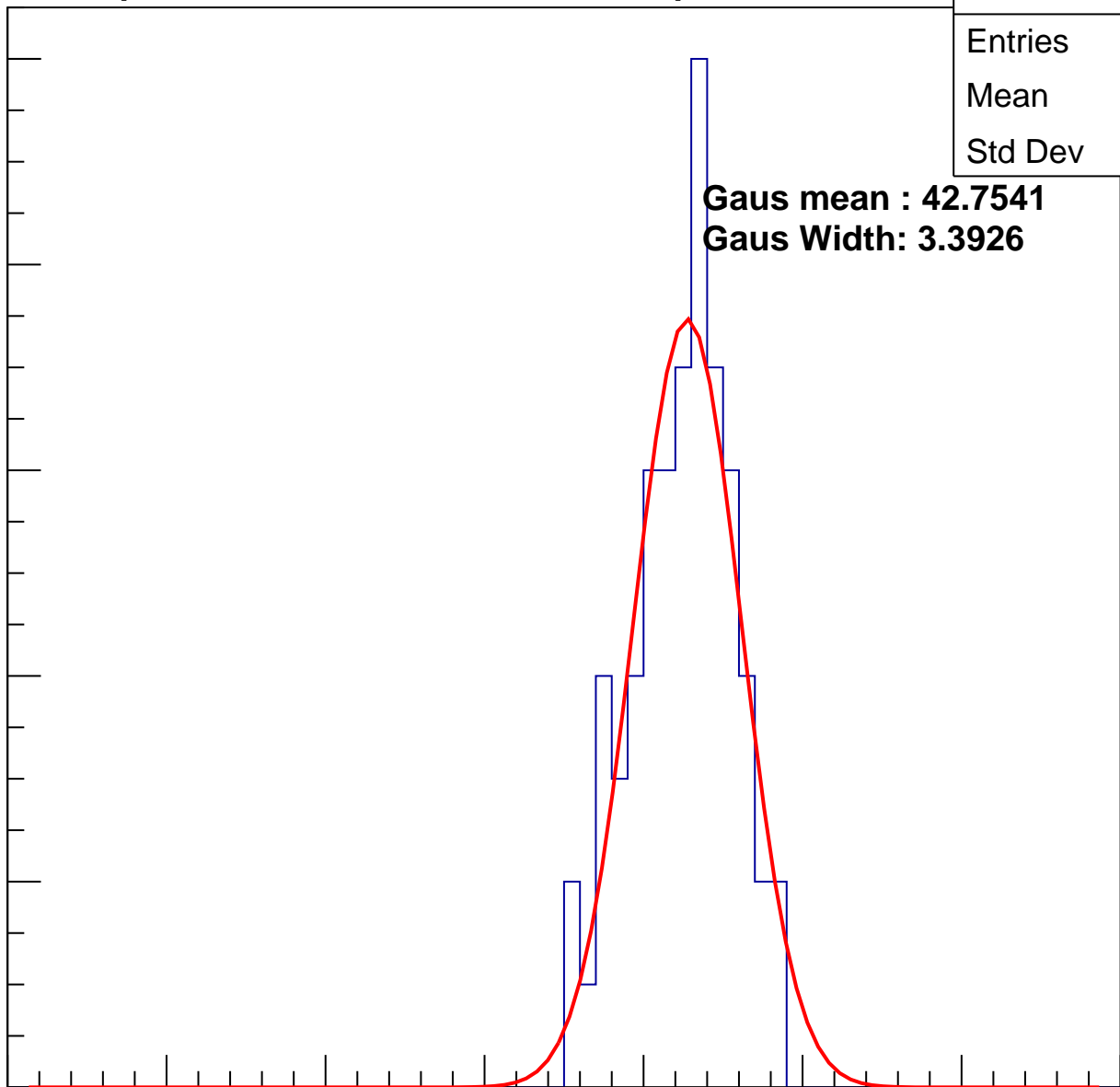
**Gaus Width: 3.3926**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

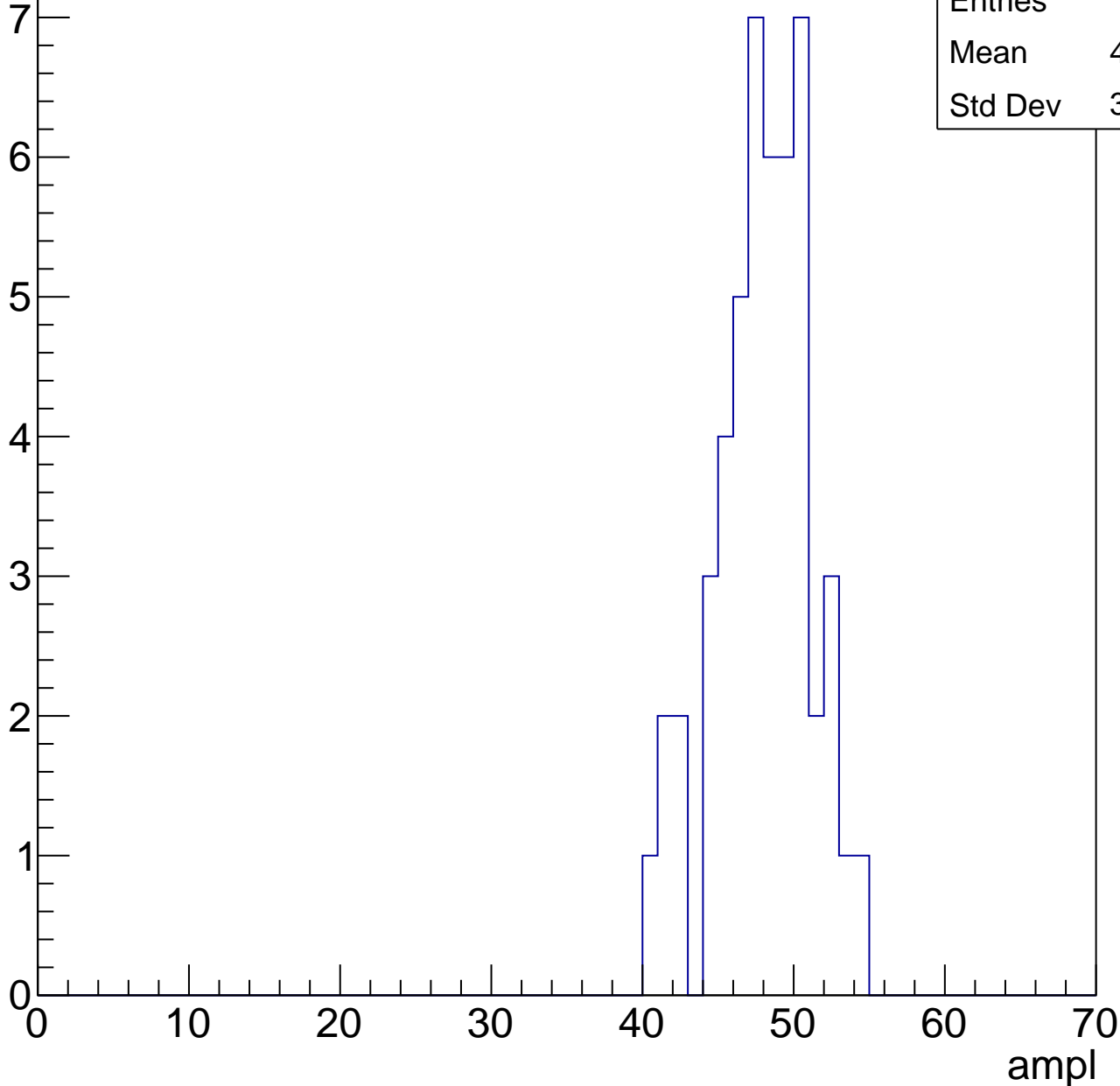


# B0L000S, U19-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	47.48
Std Dev	3.138

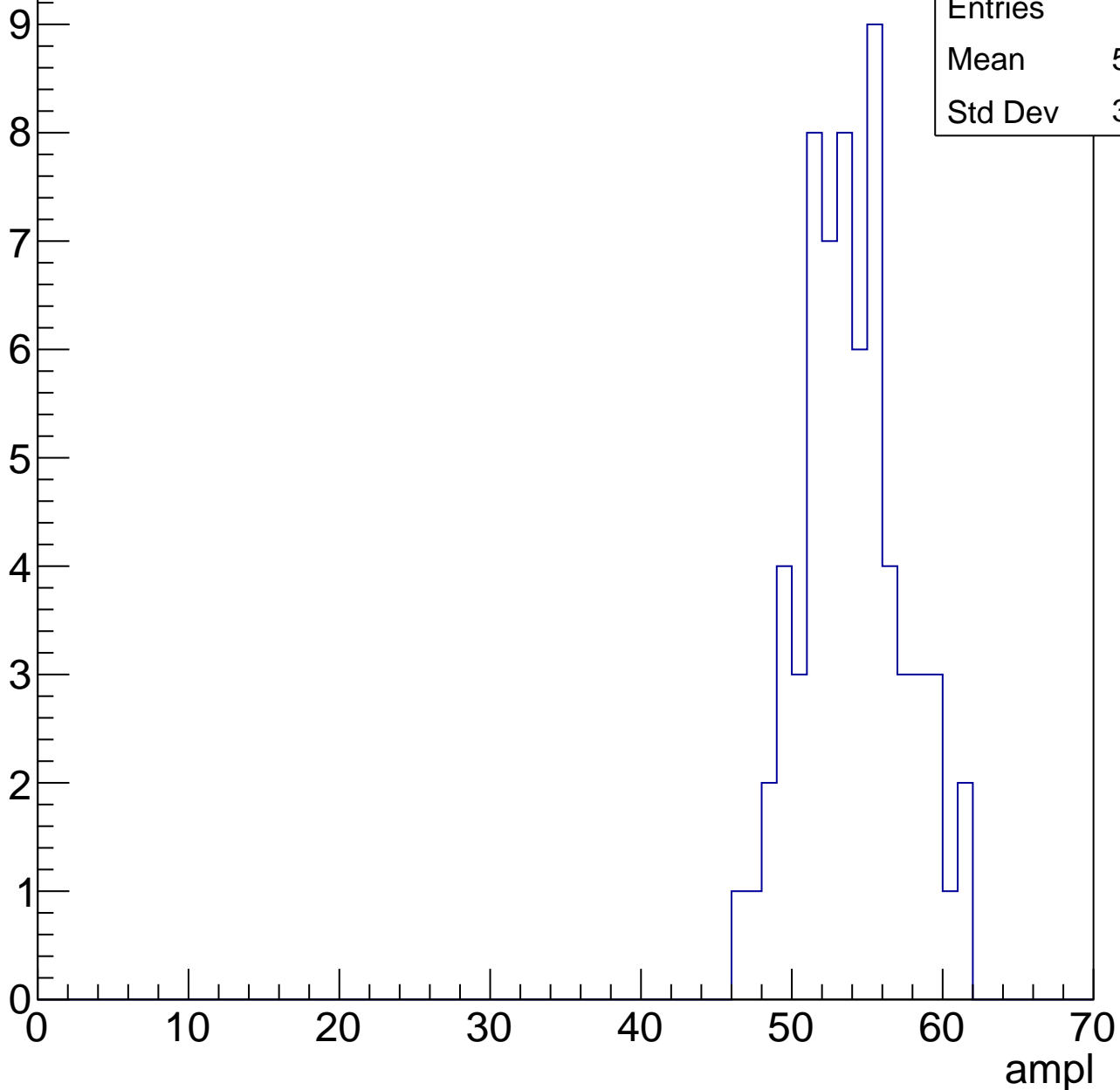


# B0L000S, U19-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

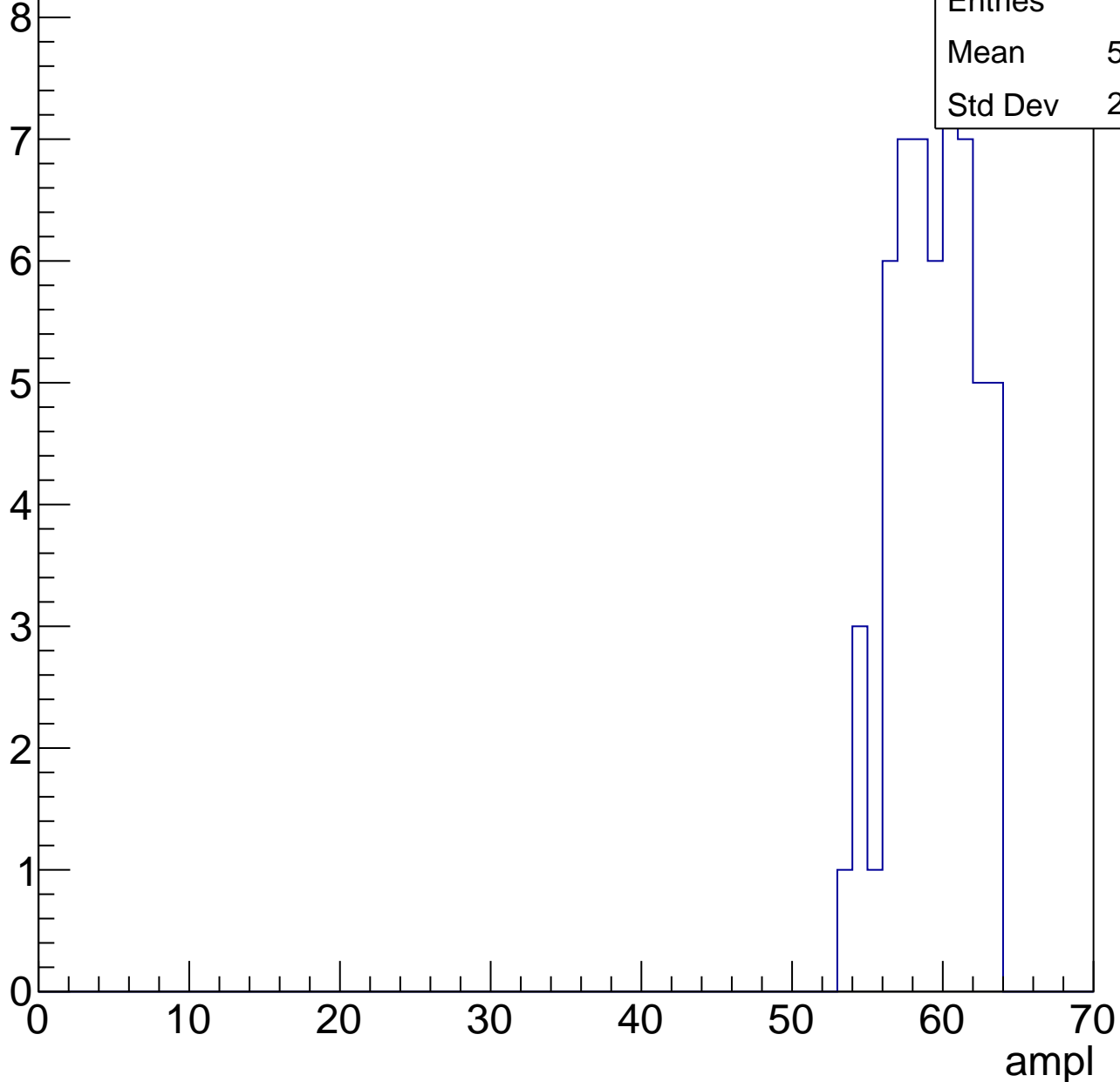
Entries	65
Mean	53.51
Std Dev	3.361



# B0L000S, U19-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

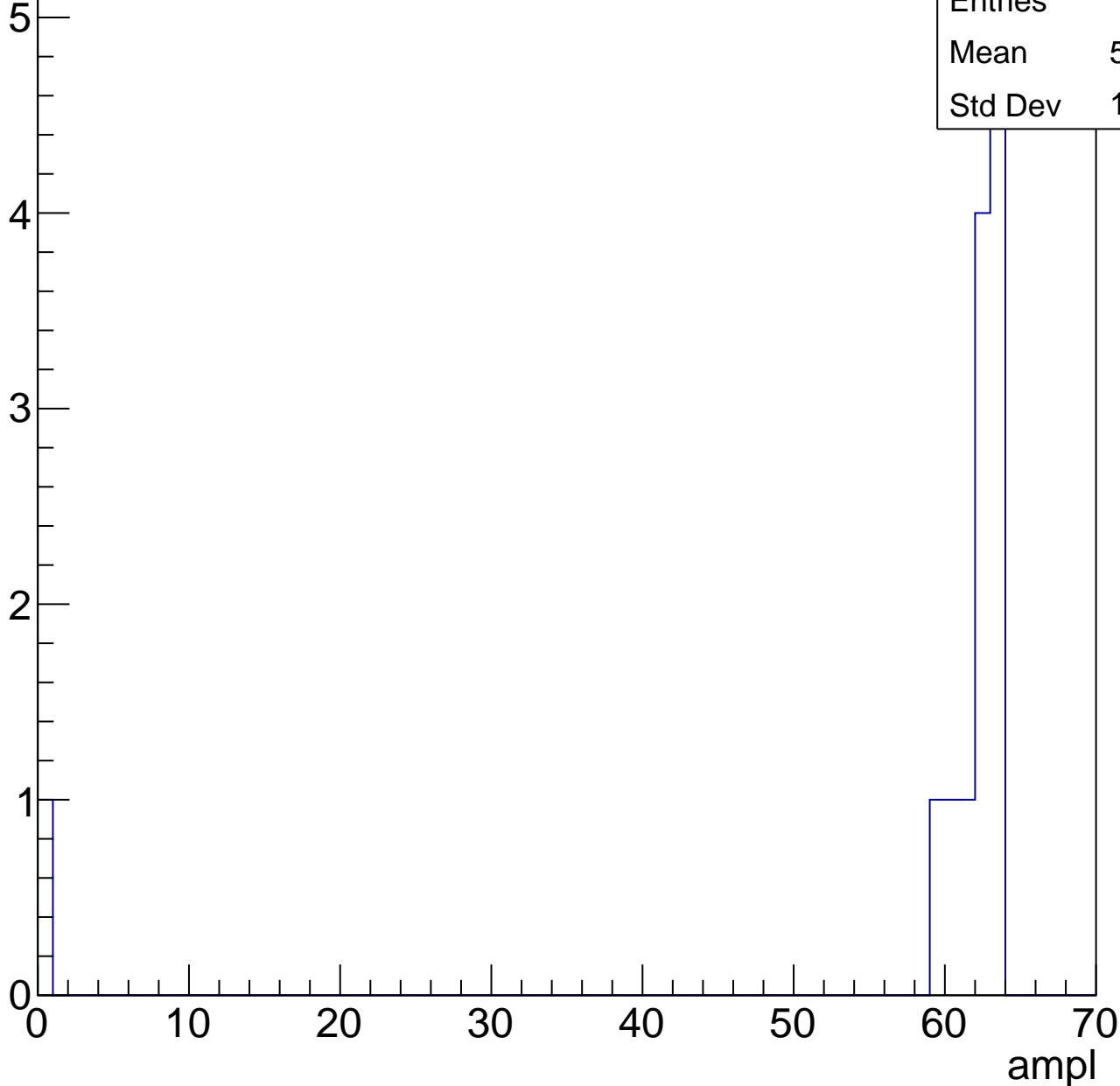


# B0L000S, U19-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	57.15
Std Dev	16.54

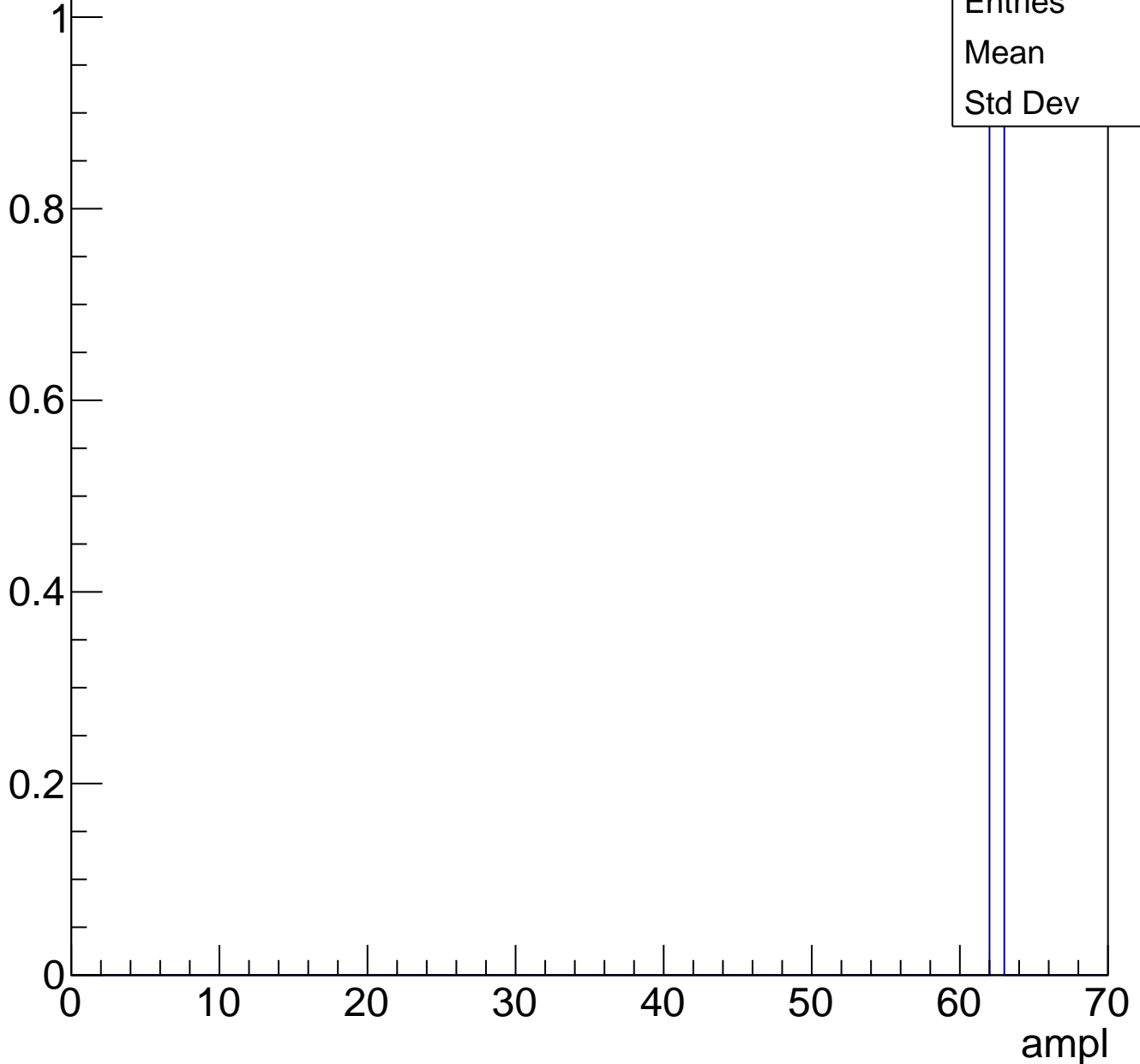




# B0L000S, U19-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	62
Std Dev	0

# B0L000S, U19-ch113, adc0

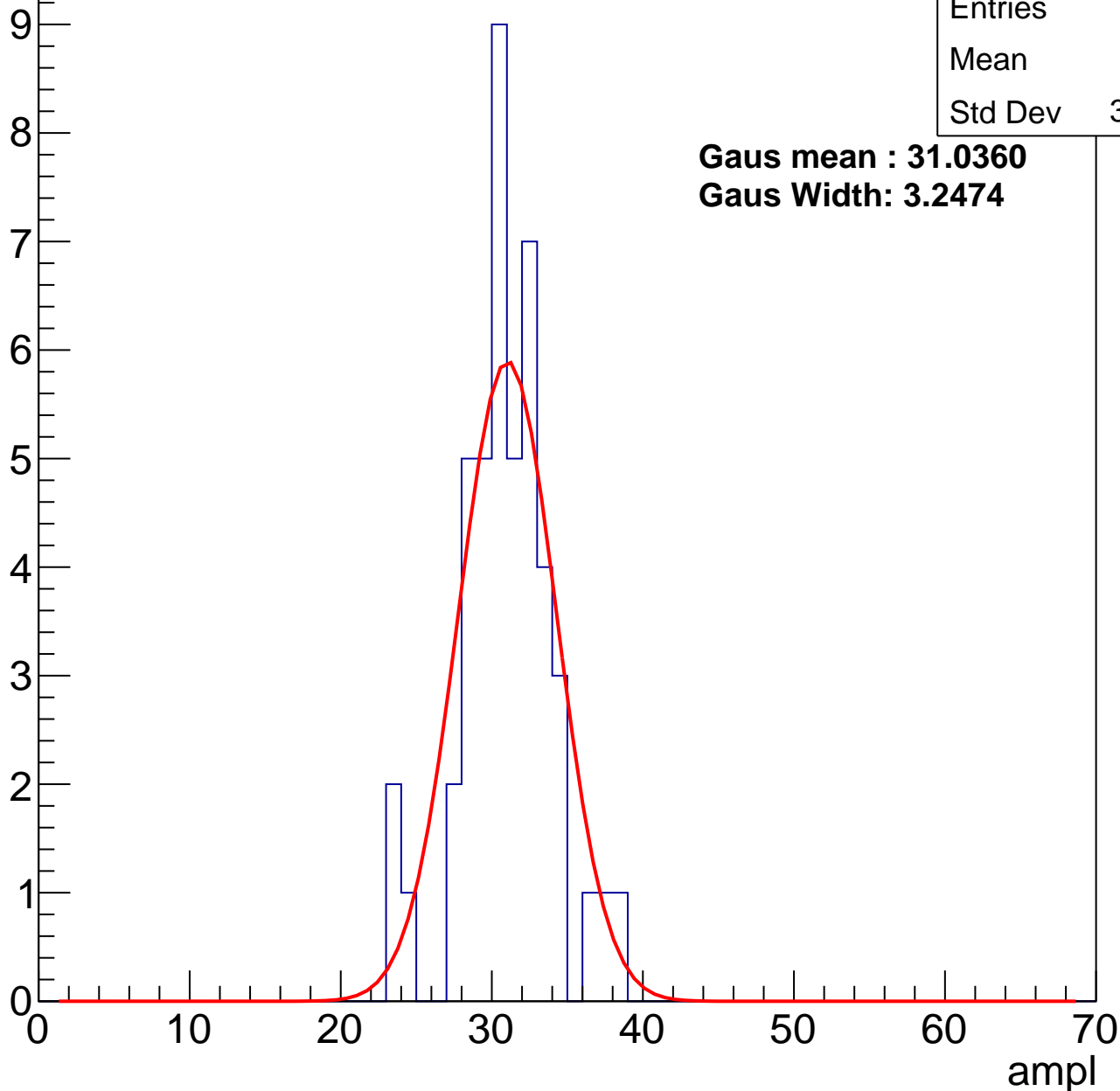
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	30.5
Std Dev	3.063

**Gaus mean : 31.0360**

**Gaus Width: 3.2474**



# B0L000S, U19-ch113, adc1

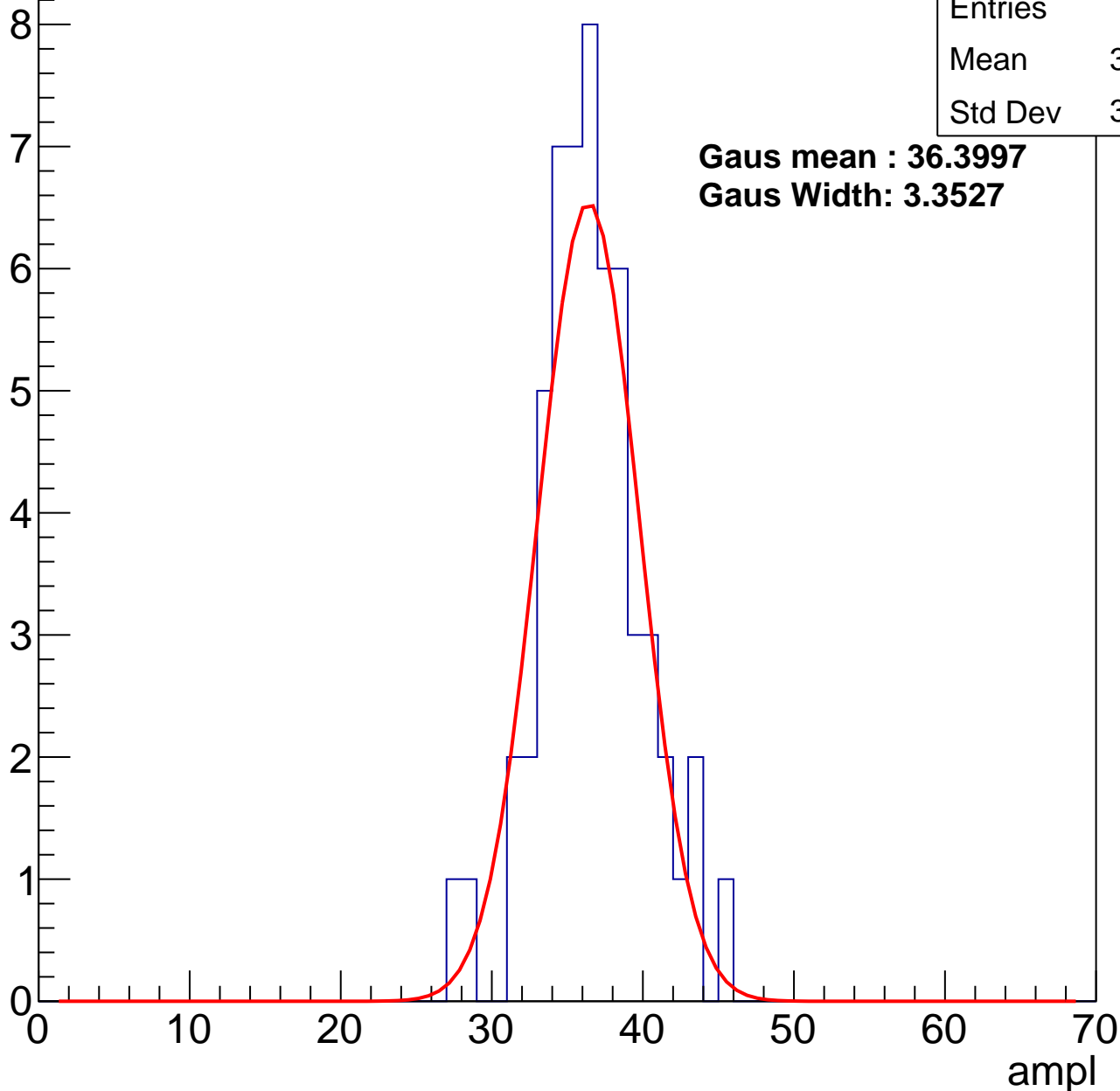
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	36.12
Std Dev	3.444

**Gaus mean : 36.3997**

**Gaus Width: 3.3527**



# B0L000S, U19-ch113, adc2

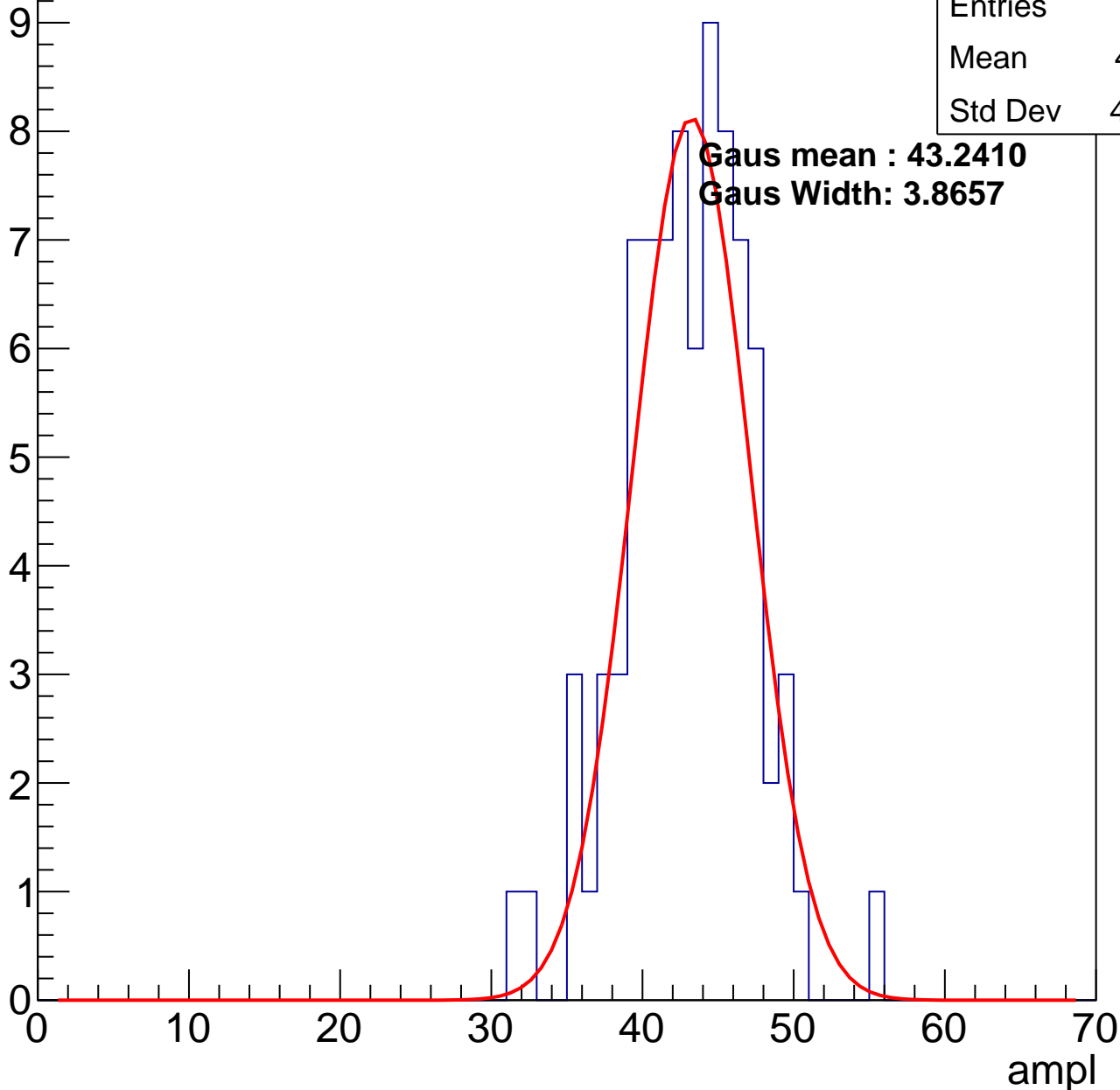
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	84
Mean	42.51
Std Dev	4.127

**Gaus mean : 43.2410**

**Gaus Width: 3.8657**

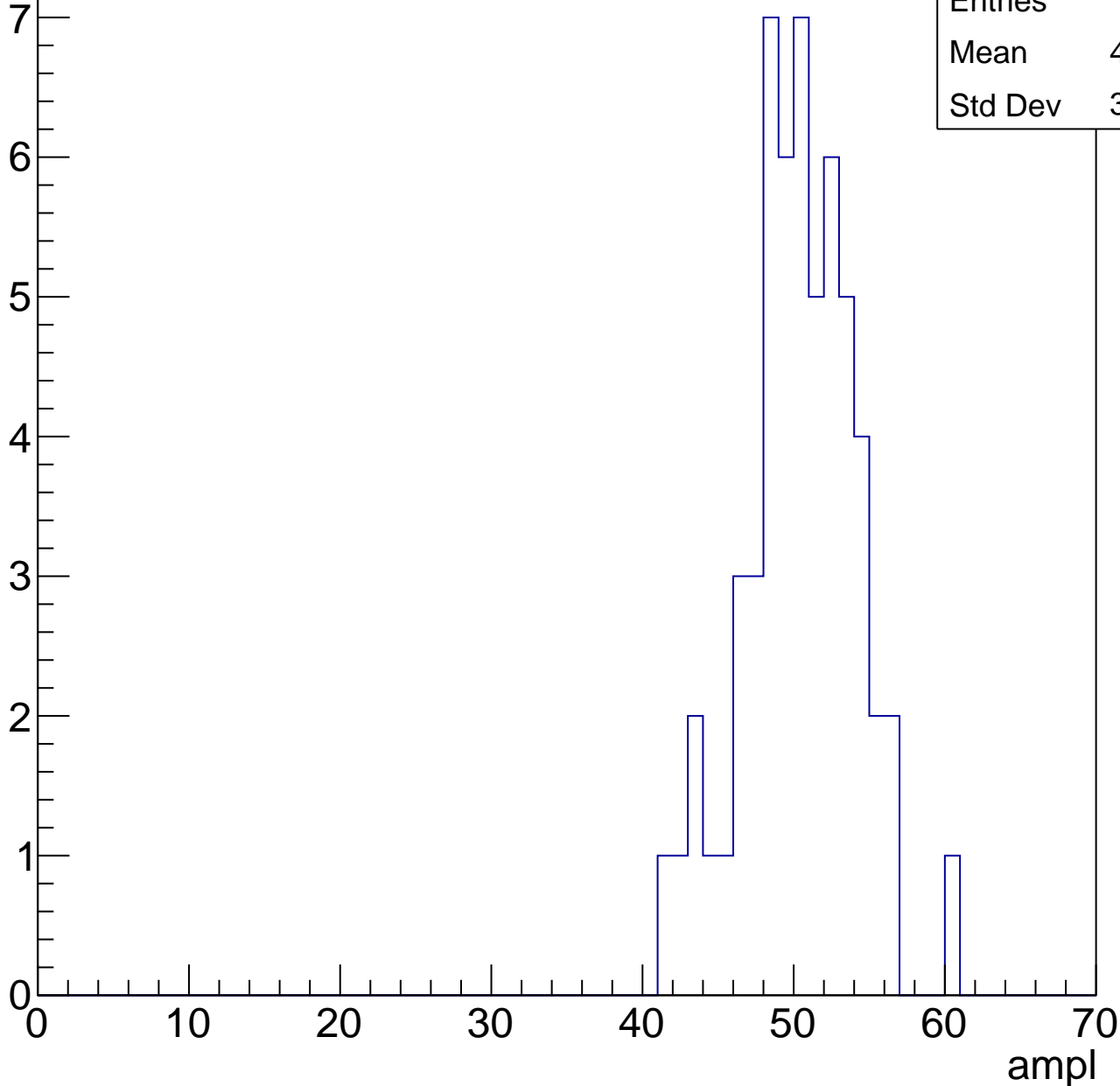


# B0L000S, U19-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	49.95
Std Dev	3.682

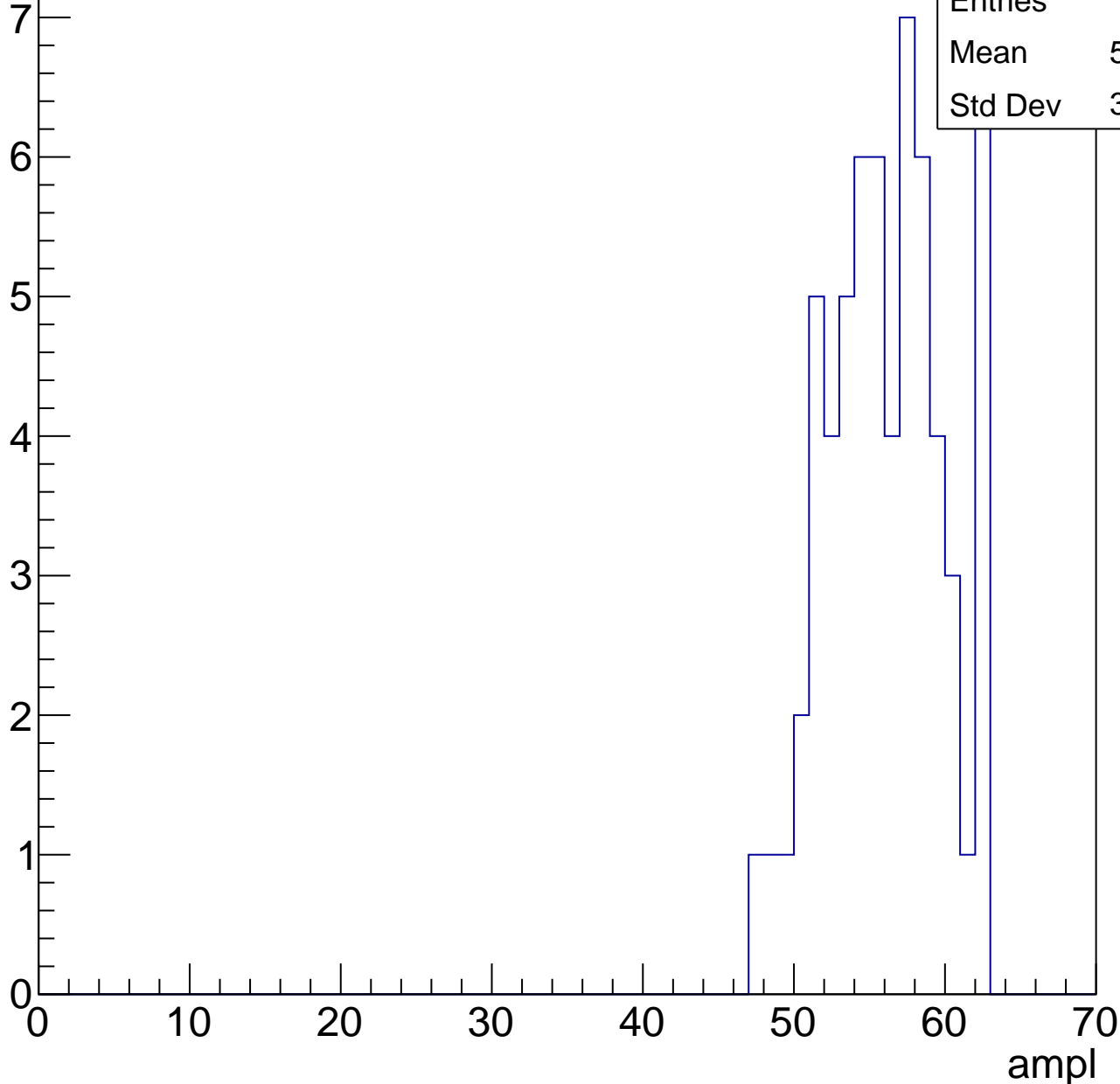


# B0L000S, U19-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	55.68
Std Dev	3.812

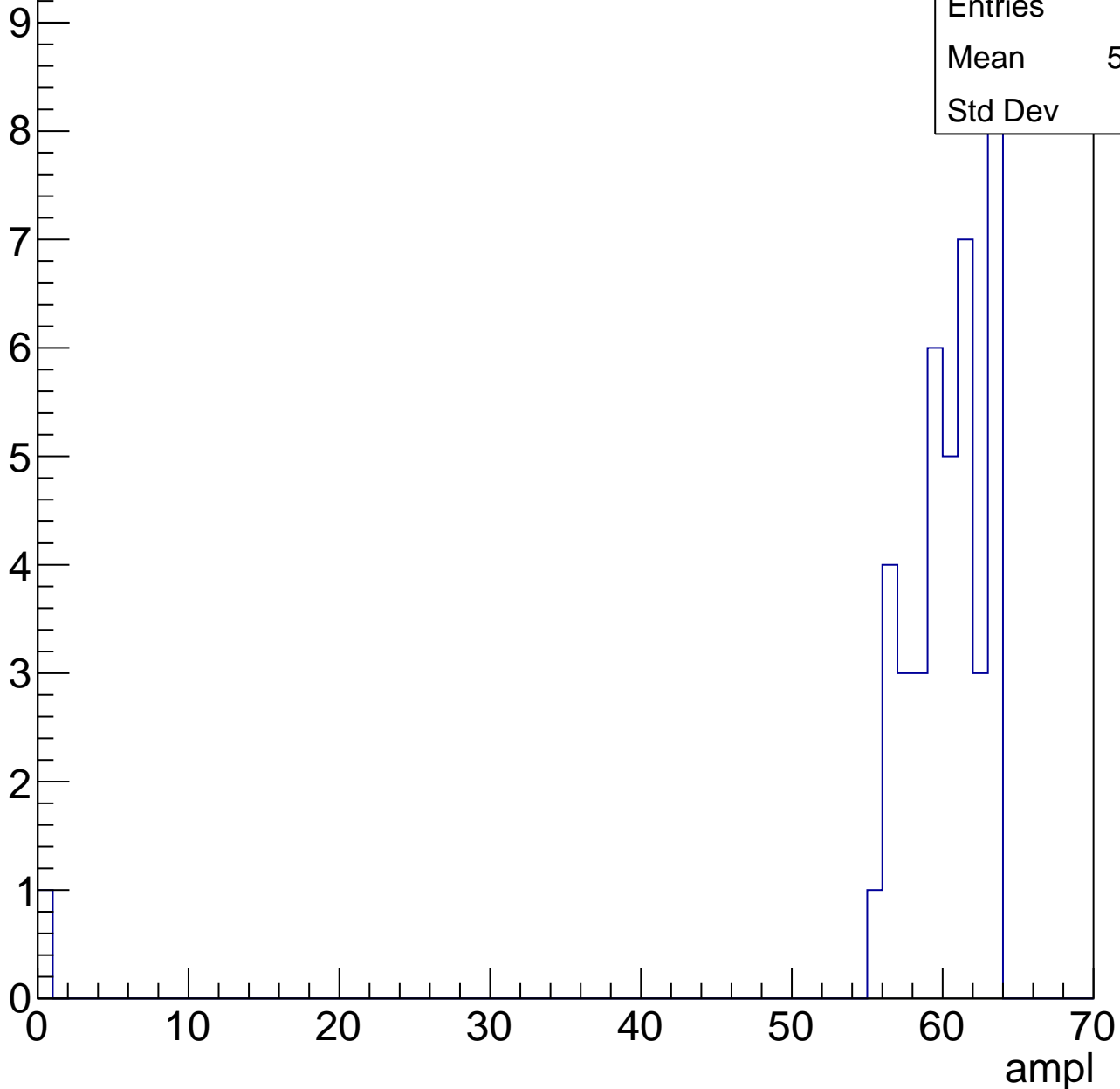


# B0L000S, U19-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.52
Std Dev	9.44



# B0L000S, U19-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

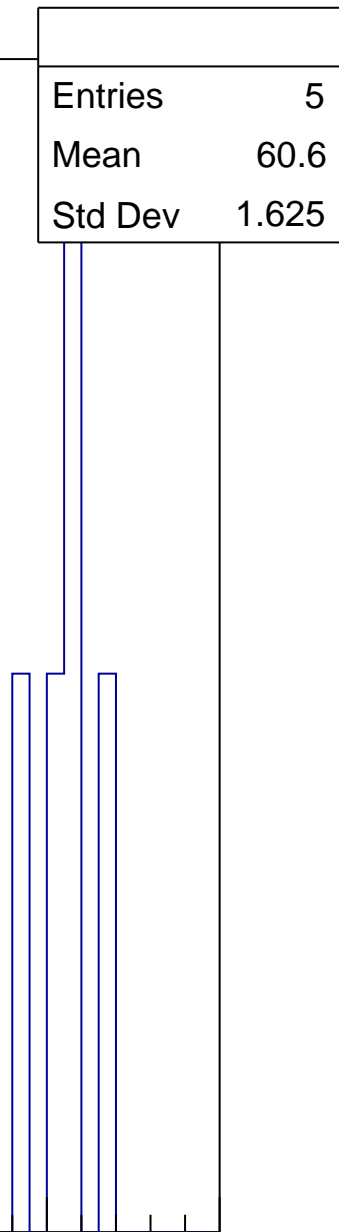
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	60.6
Std Dev	1.625

ampl

0 10 20 30 40 50 60 70





# B0L000S, U19-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch114, adc0

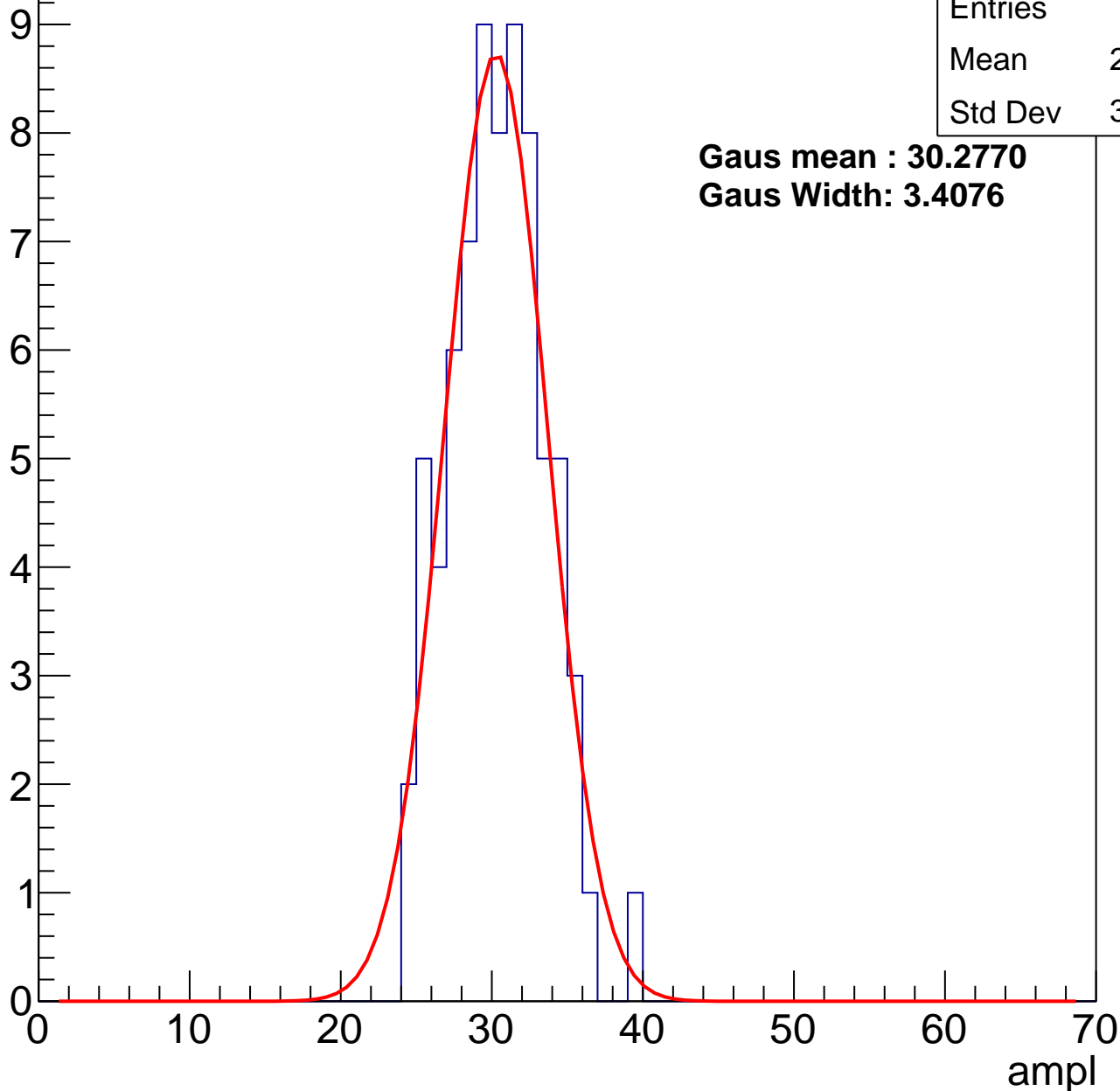
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.95
Std Dev	3.118

**Gaus mean : 30.2770**

**Gaus Width: 3.4076**



# B0L000S, U19-ch114, adc1

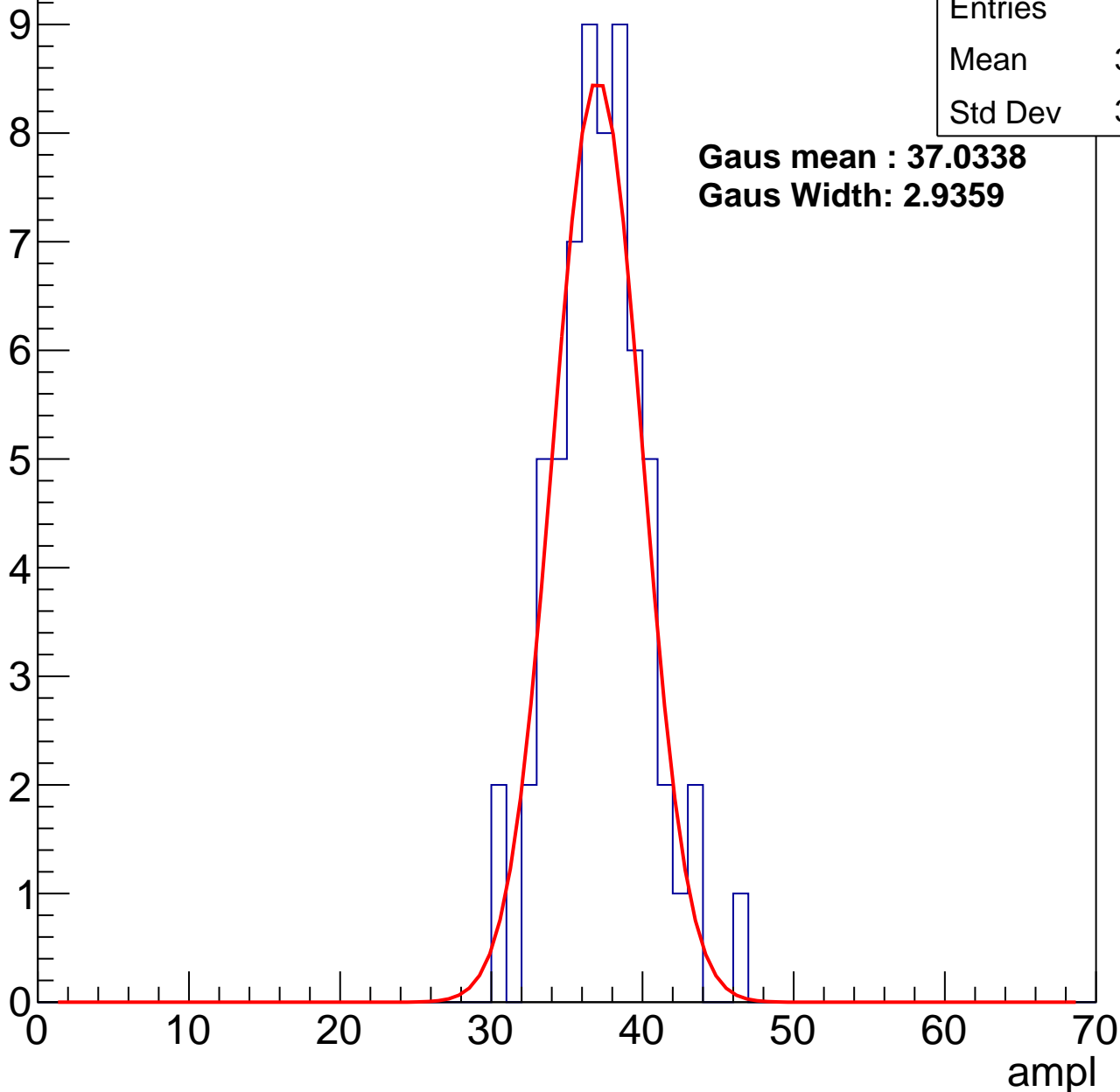
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.81
Std Dev	3.051

**Gaus mean : 37.0338**

**Gaus Width: 2.9359**



# B0L000S, U19-ch114, adc2

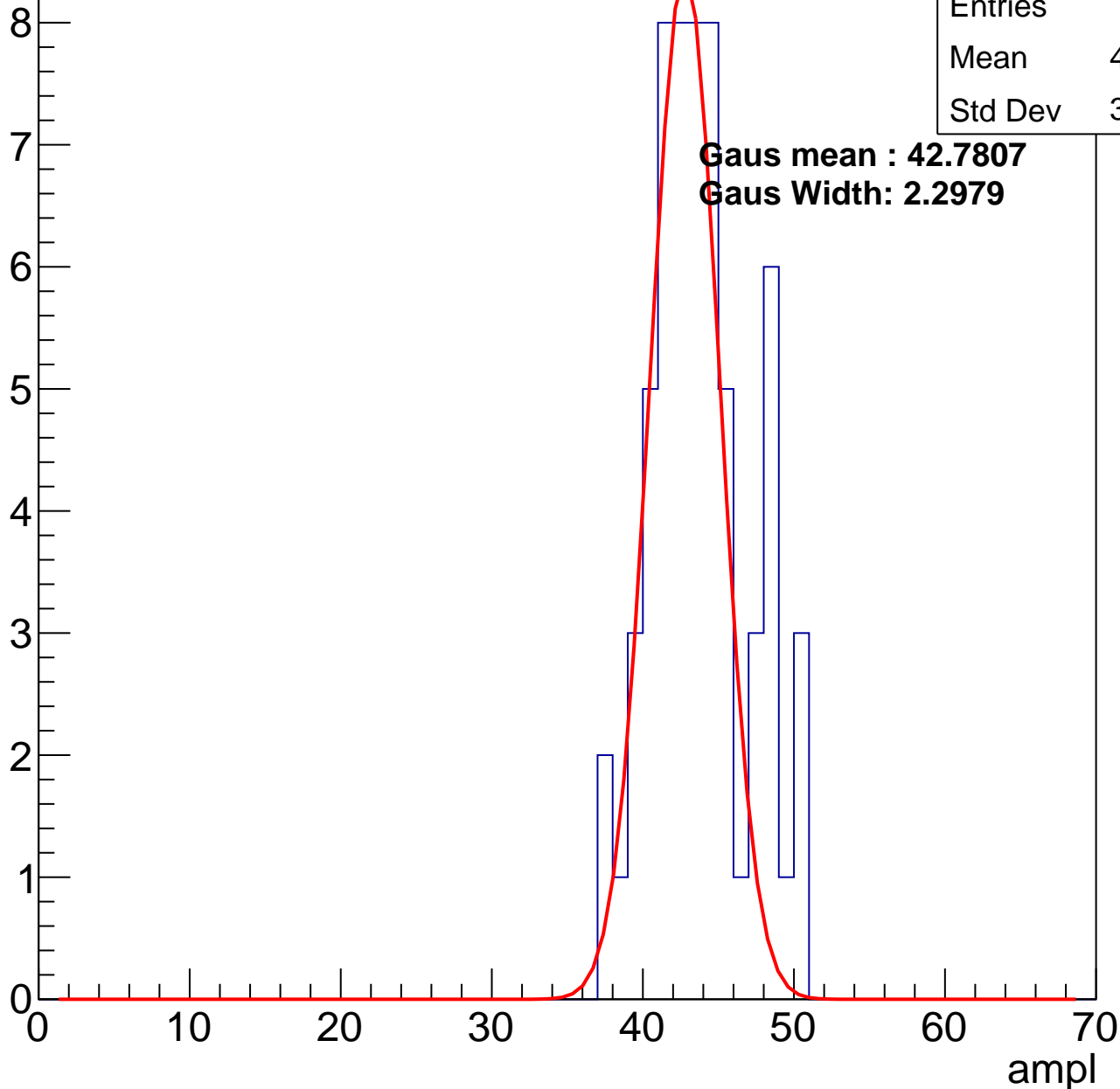
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.35
Std Dev	3.213

**Gaus mean : 42.7807**

**Gaus Width: 2.2979**

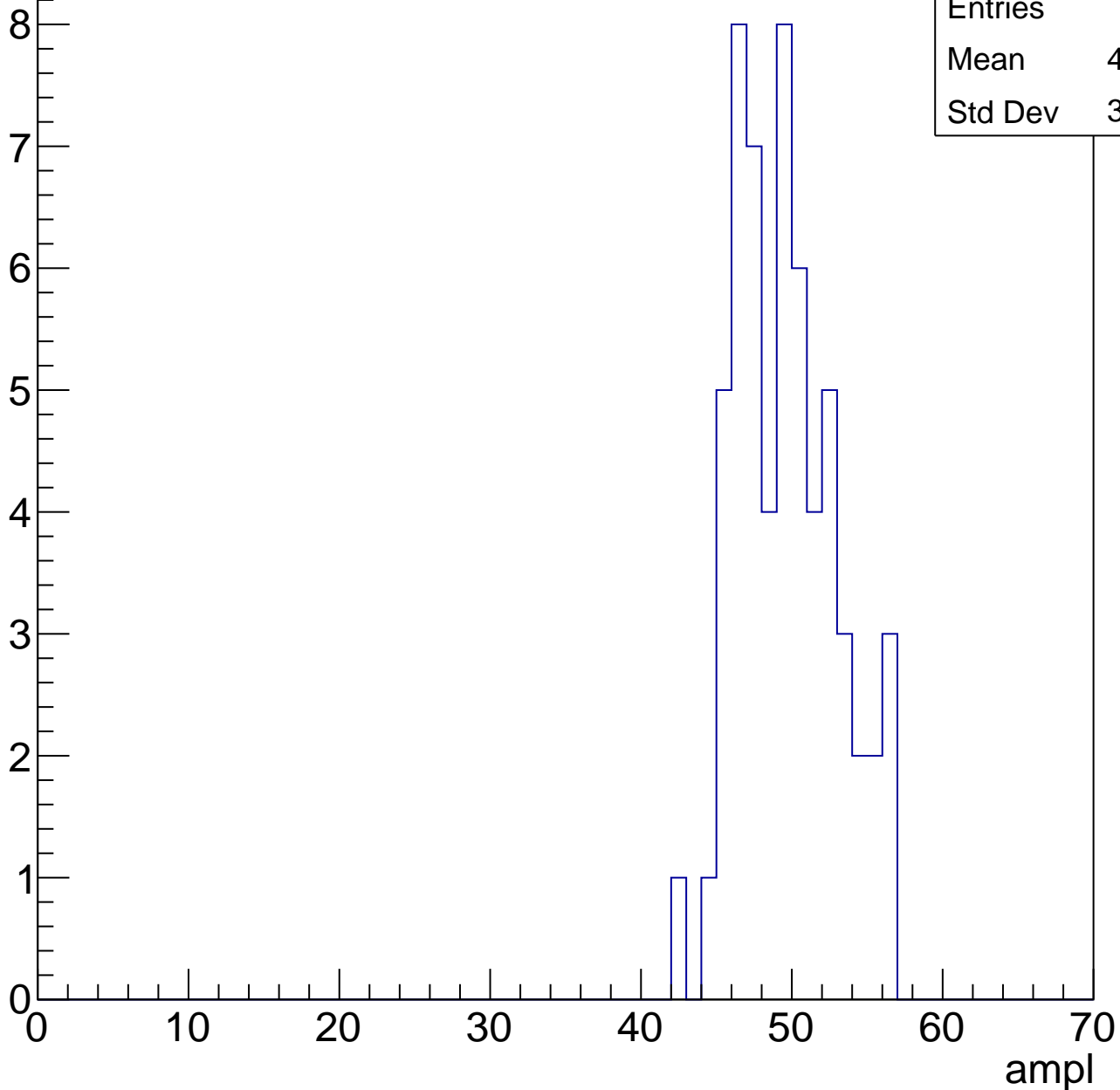


# B0L000S, U19-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.17
Std Dev	3.294

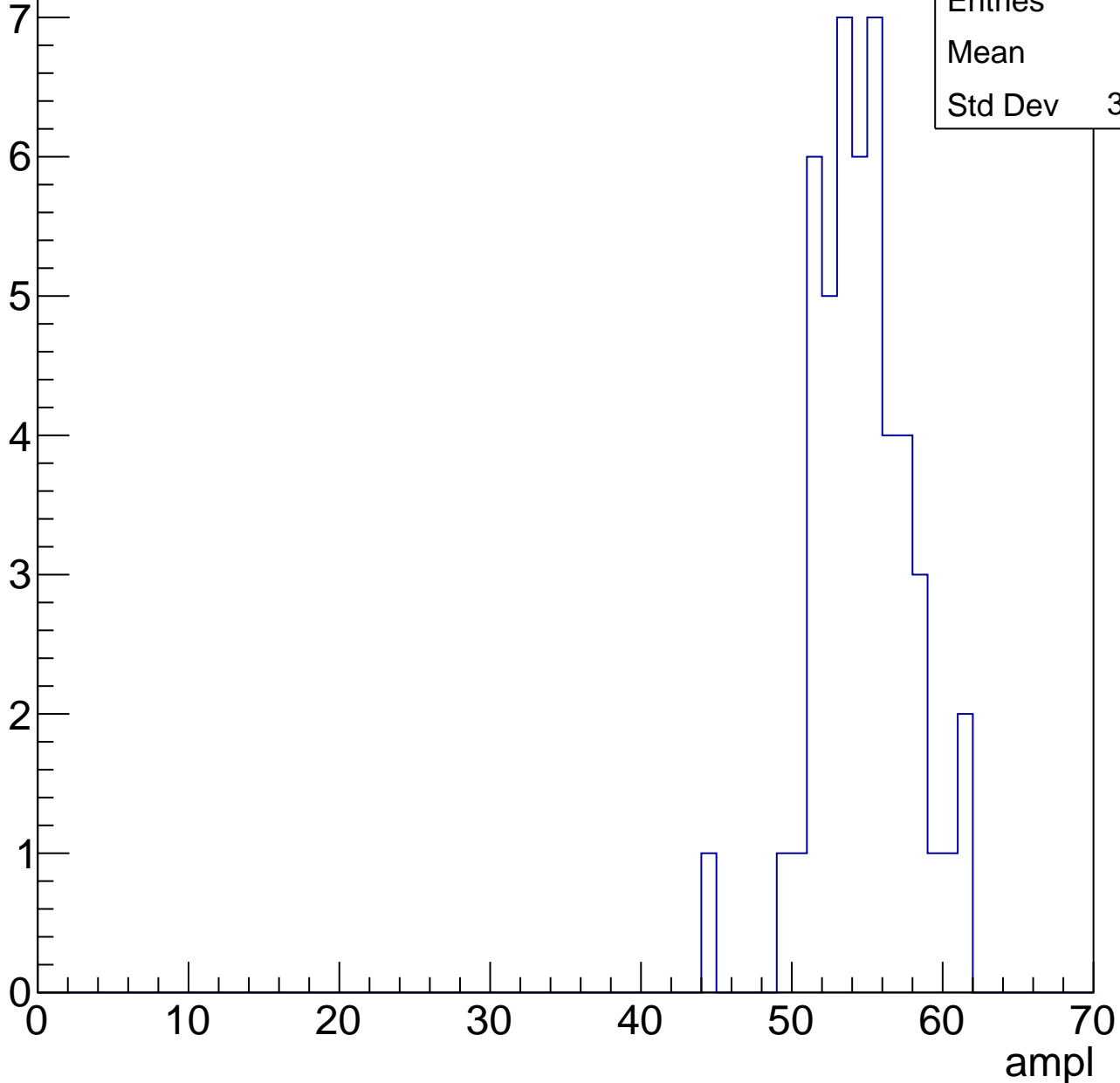


# B0L000S, U19-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	54.2
Std Dev	3.149

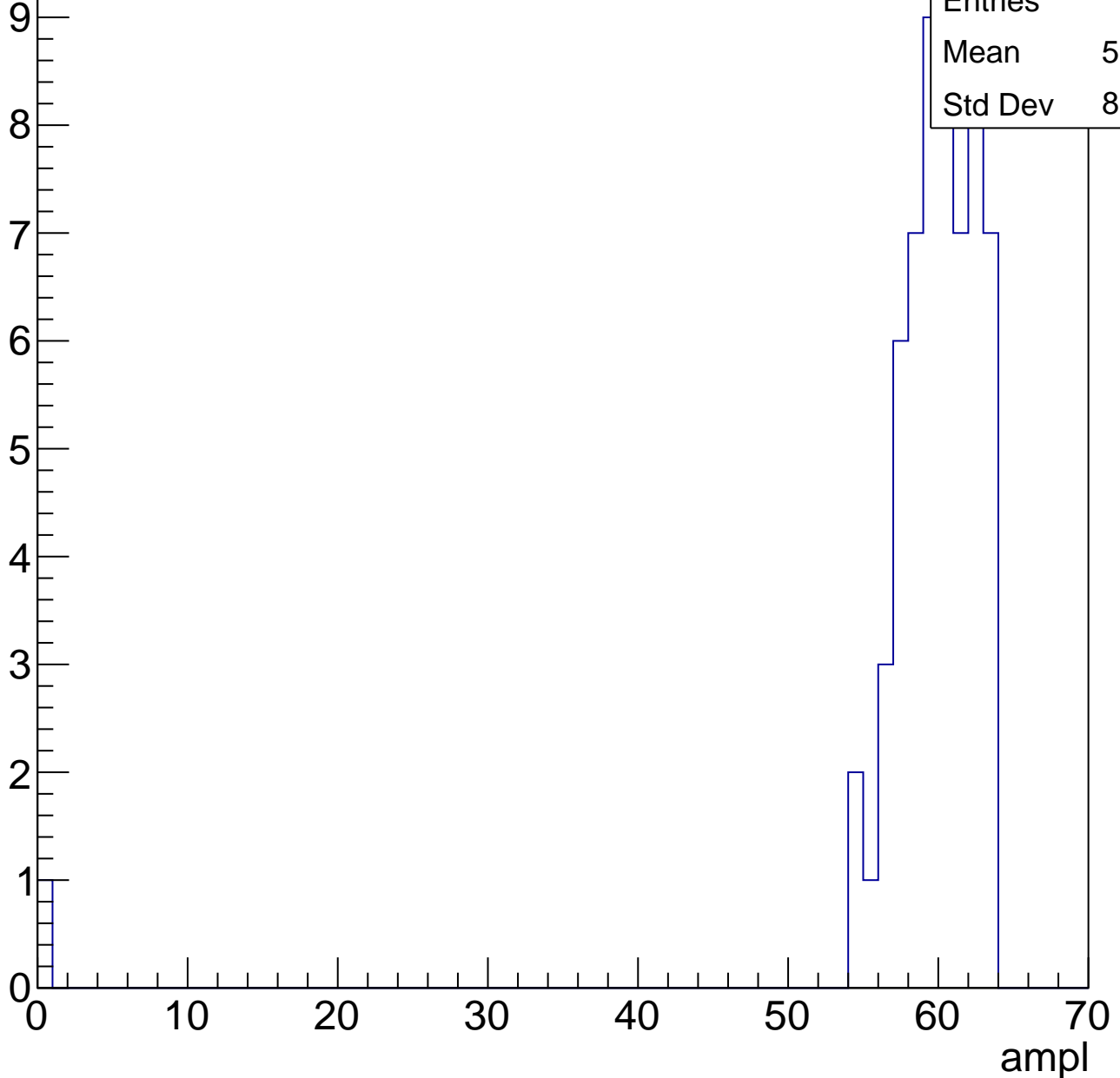


# B0L000S, U19-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	58.54
Std Dev	8.039



# B0L000S, U19-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch115, adc0

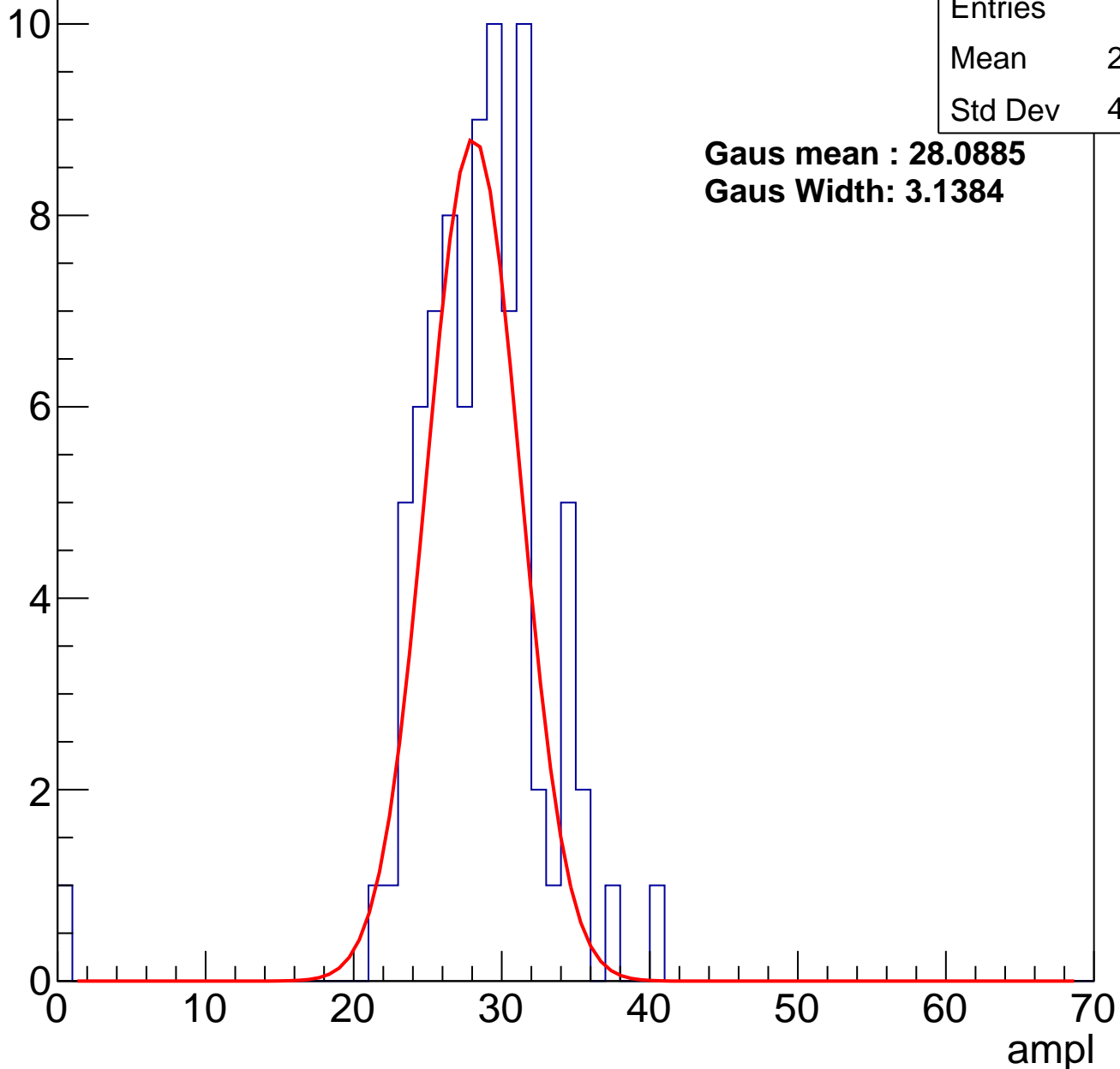
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	27.99
Std Dev	4.758

**Gaus mean : 28.0885**

**Gaus Width: 3.1384**

Entry



# B0L000S, U19-ch115, adc1

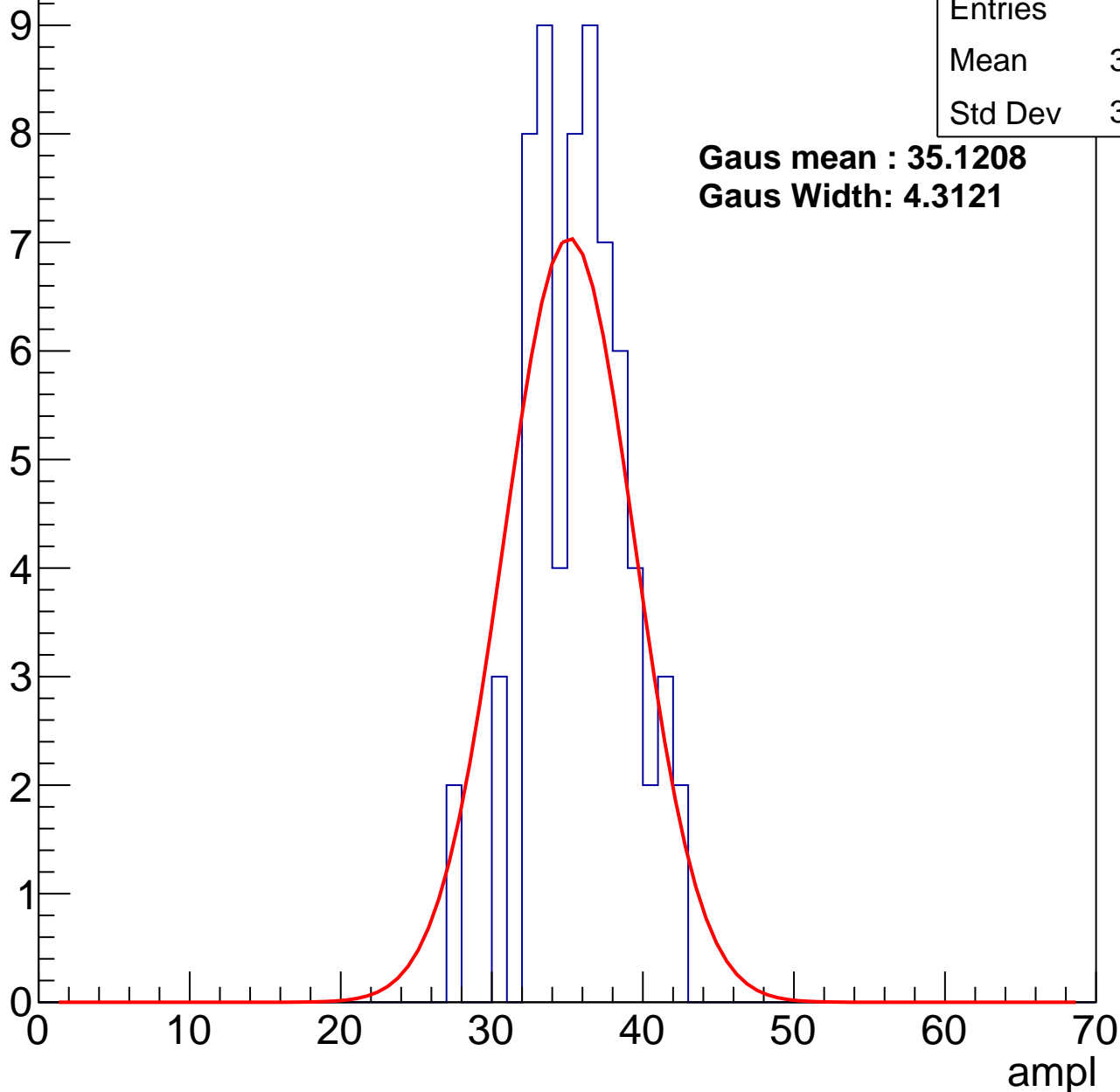
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.33
Std Dev	3.275

**Gaus mean : 35.1208**

**Gaus Width: 4.3121**



# B0L000S, U19-ch115, adc2

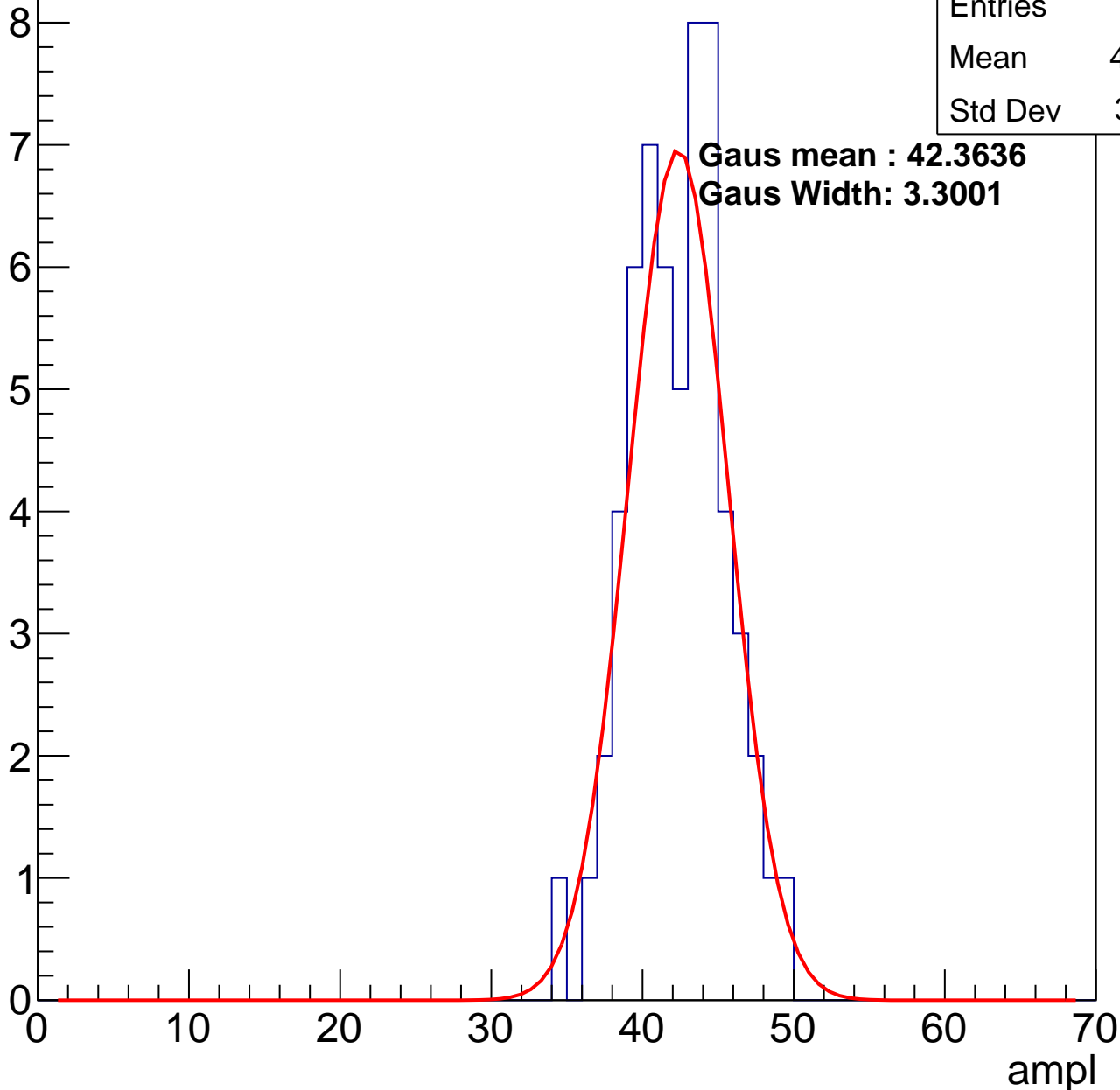
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	41.88
Std Dev	3.081

**Gaus mean : 42.3636**

**Gaus Width: 3.3001**

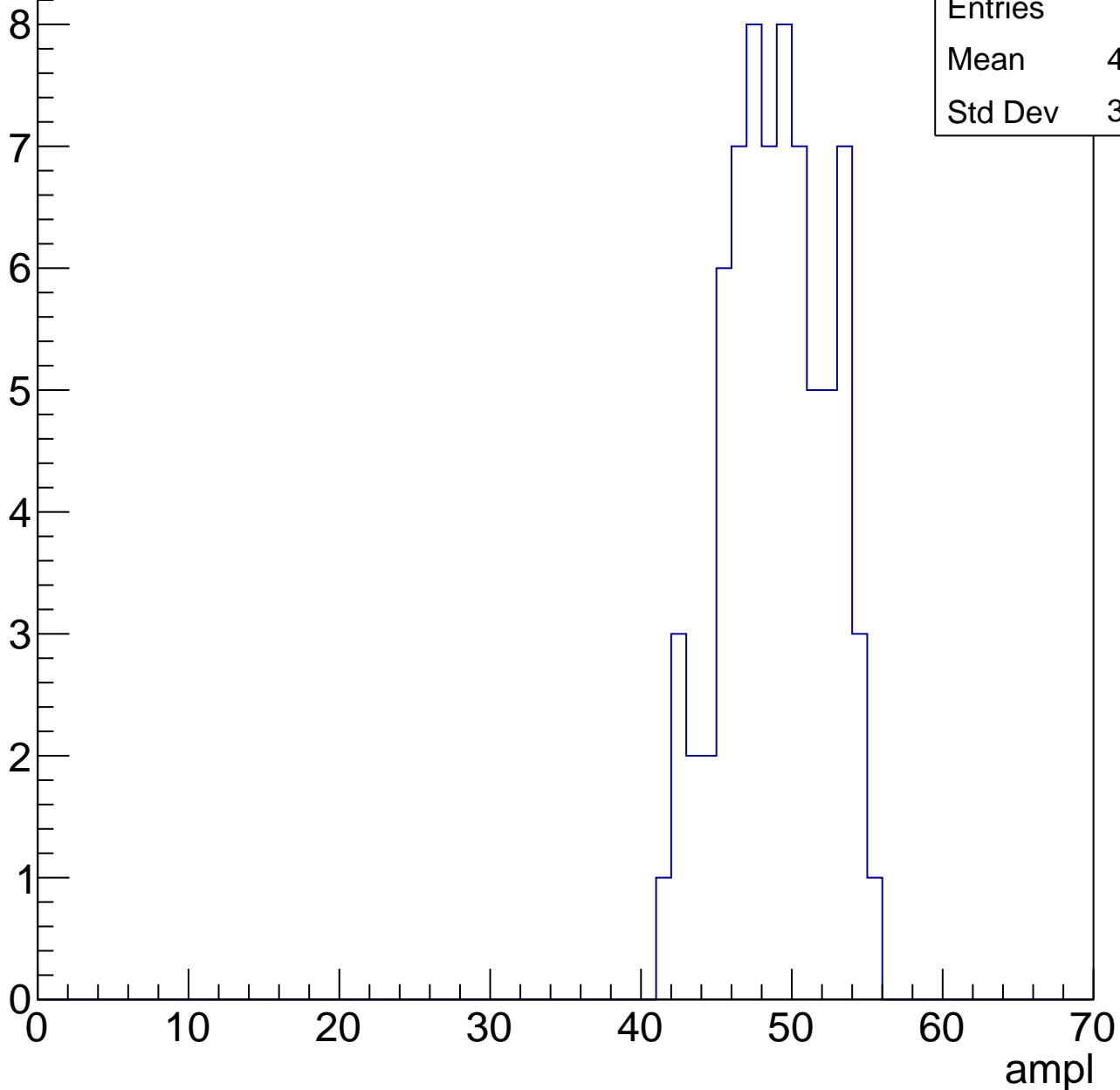


# B0L000S, U19-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	48.47
Std Dev	3.346

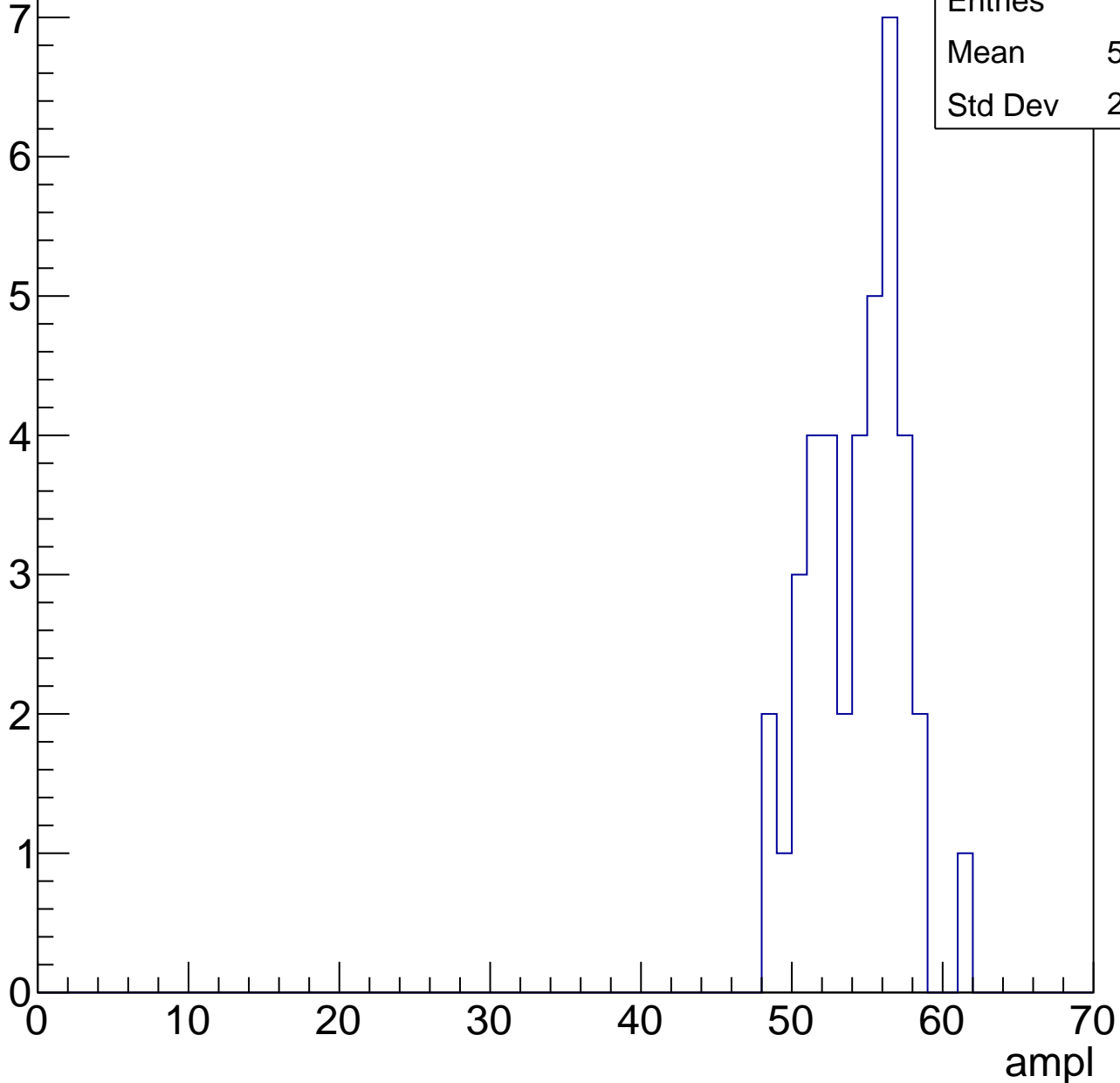


# B0L000S, U19-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	53.87
Std Dev	2.989

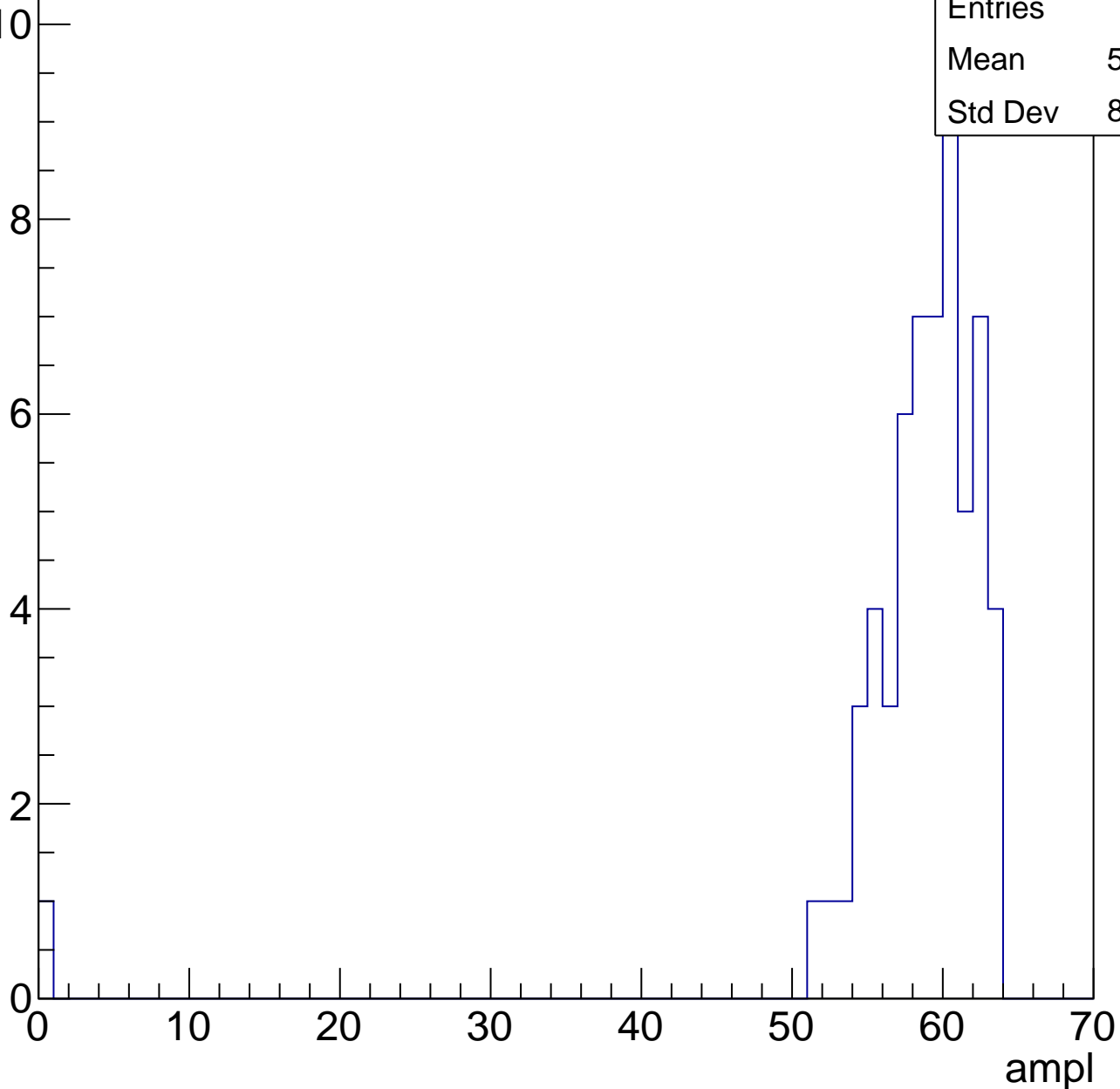


# B0L000S, U19-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57.63
Std Dev	8.033

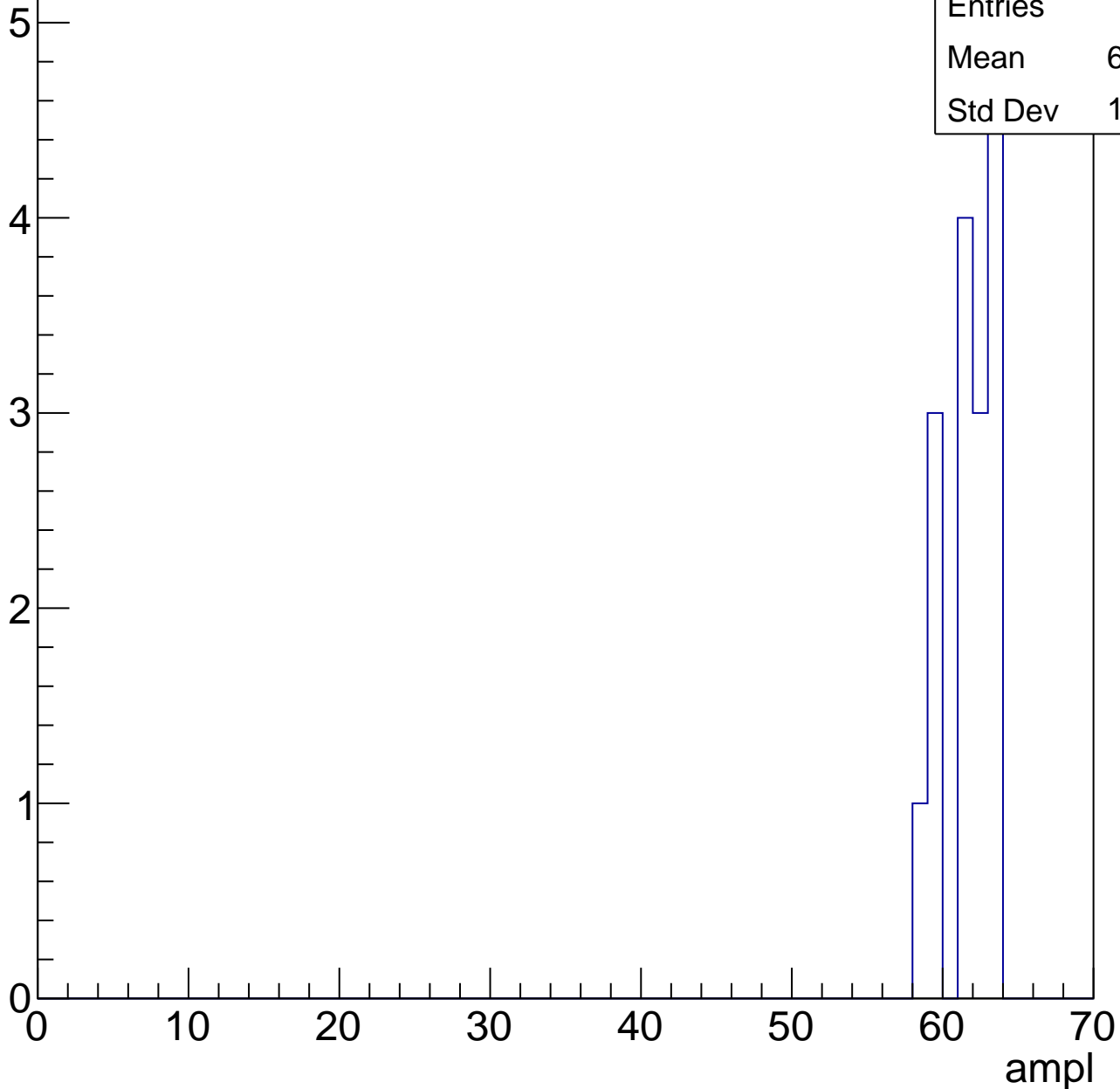


# B0L000S, U19-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.25
Std Dev	1.639





# B0L000S, U19-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch116, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	29.66
Std Dev	3.447

**Gaus mean : 30.0067**

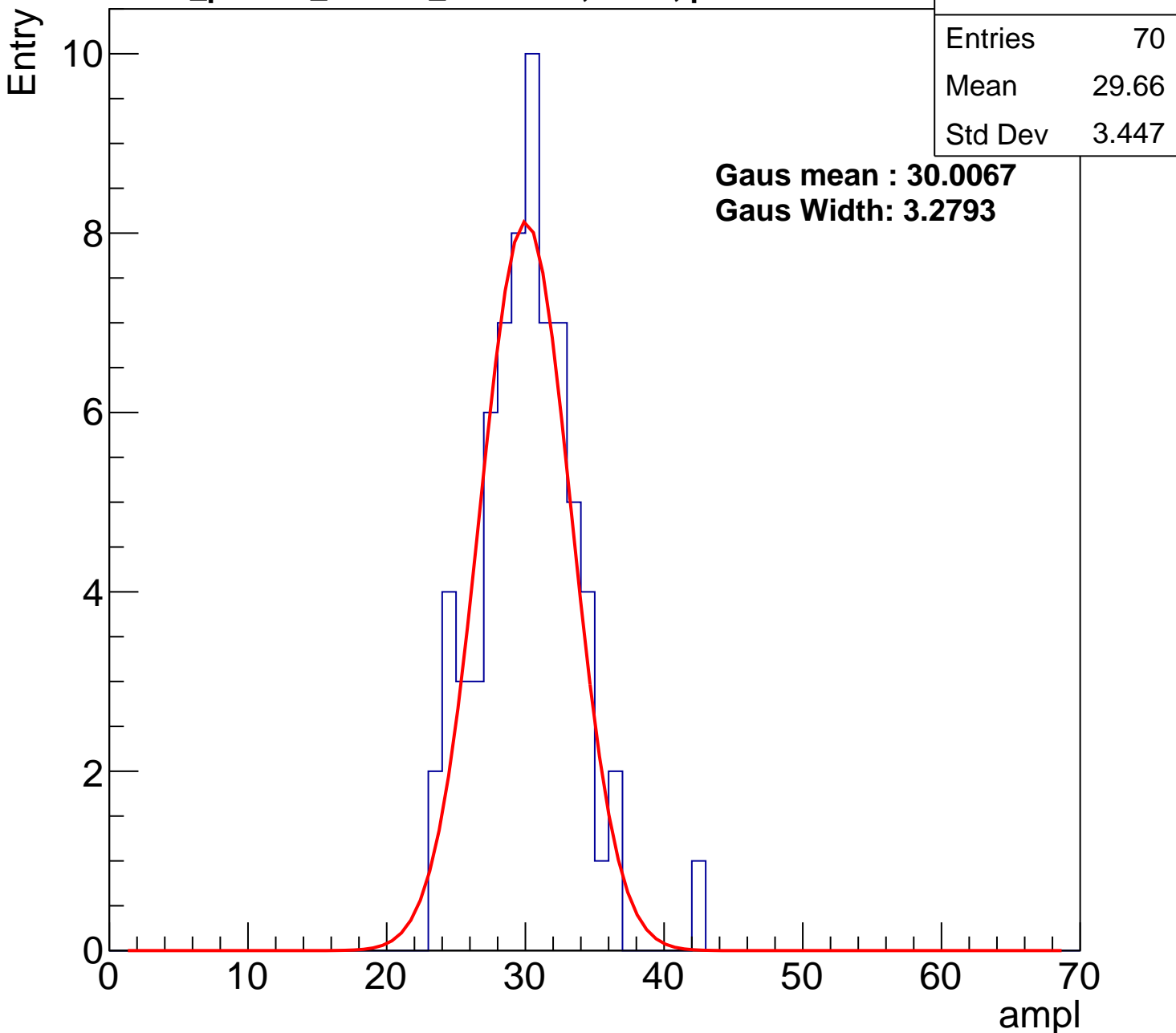
**Gaus Width: 3.2793**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch116, adc1

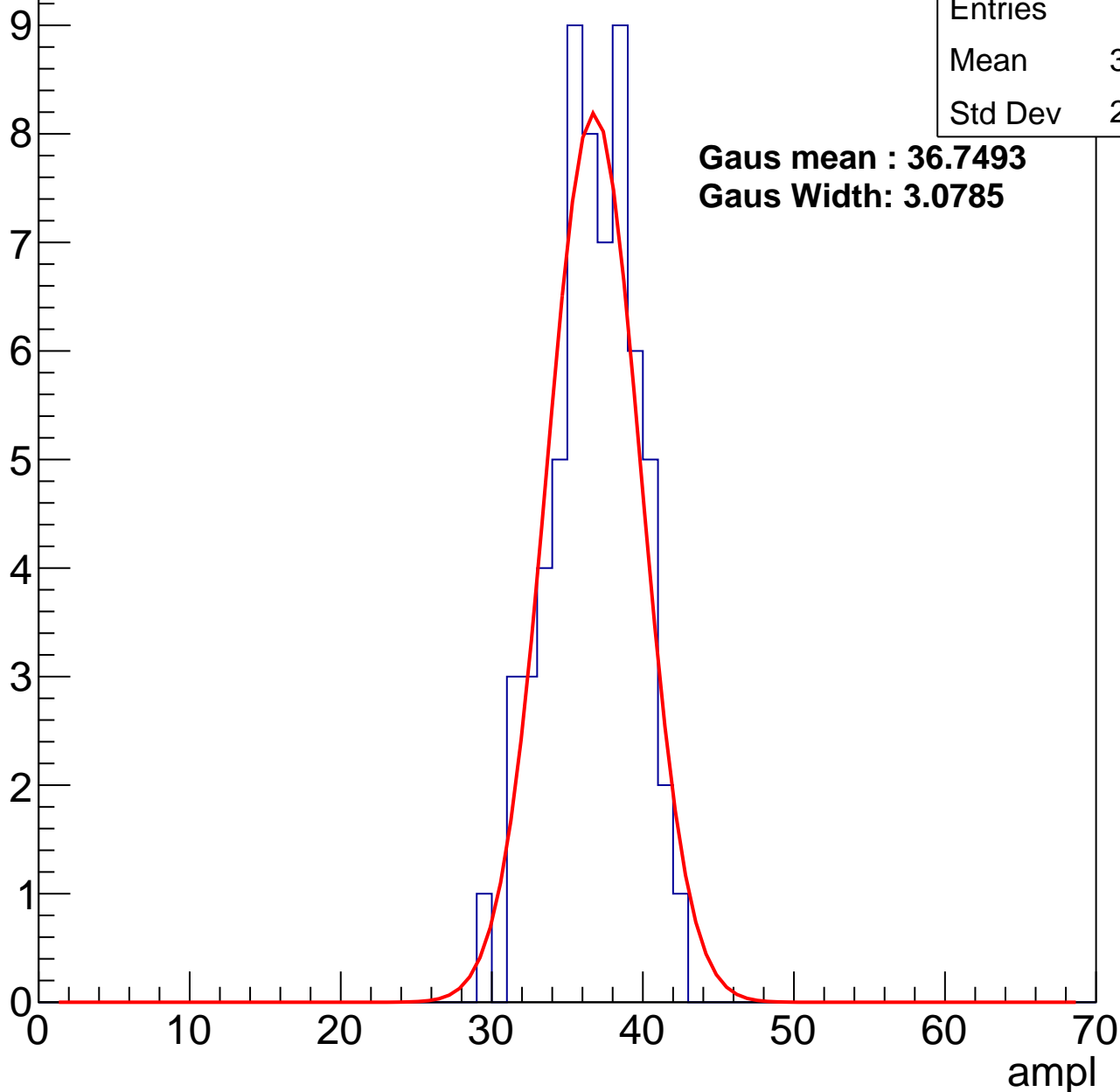
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.22
Std Dev	2.808

**Gaus mean : 36.7493**

**Gaus Width: 3.0785**



# B0L000S, U19-ch116, adc2

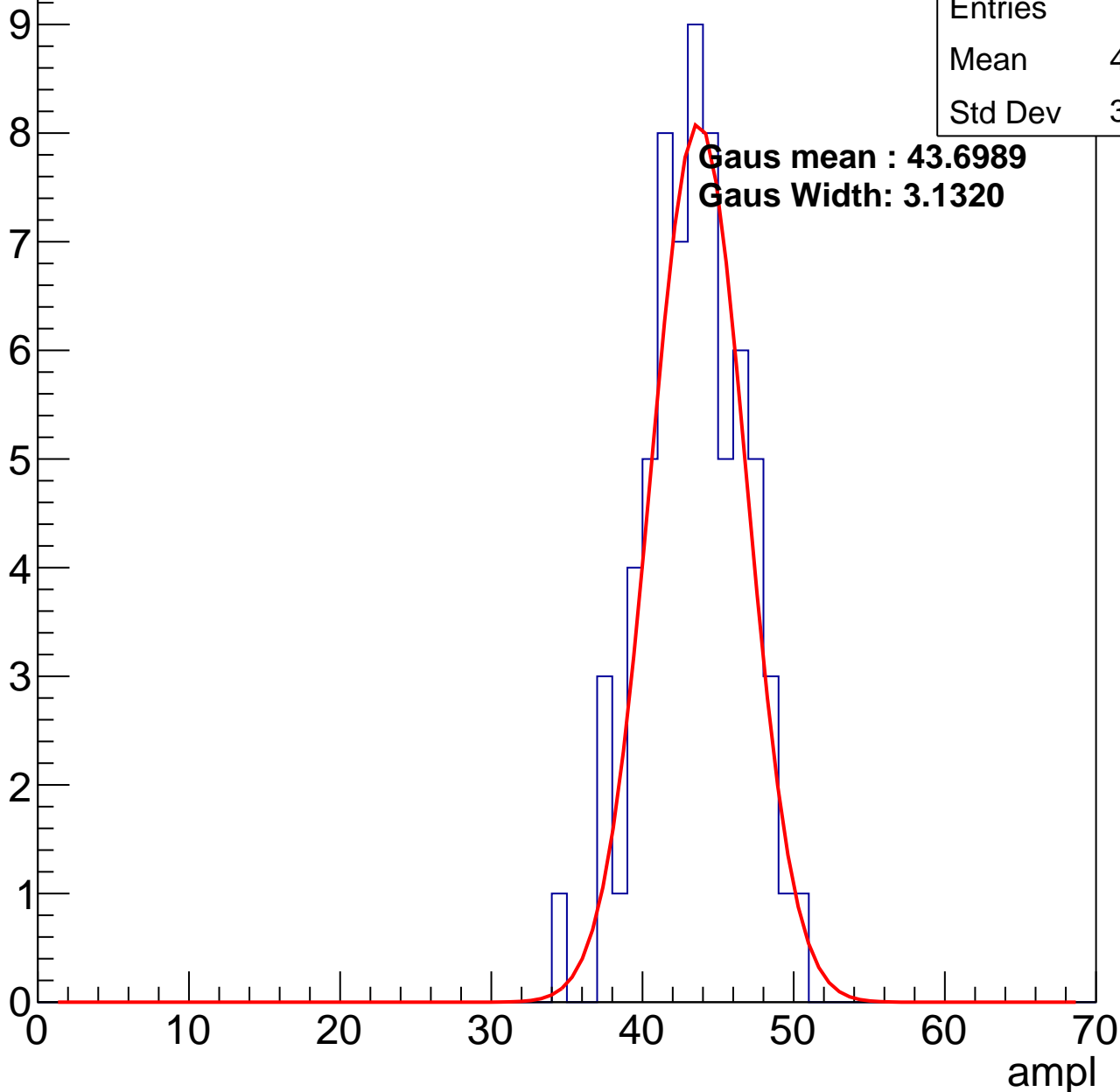
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	42.97
Std Dev	3.195

**Gaus mean : 43.6989**

**Gaus Width: 3.1320**

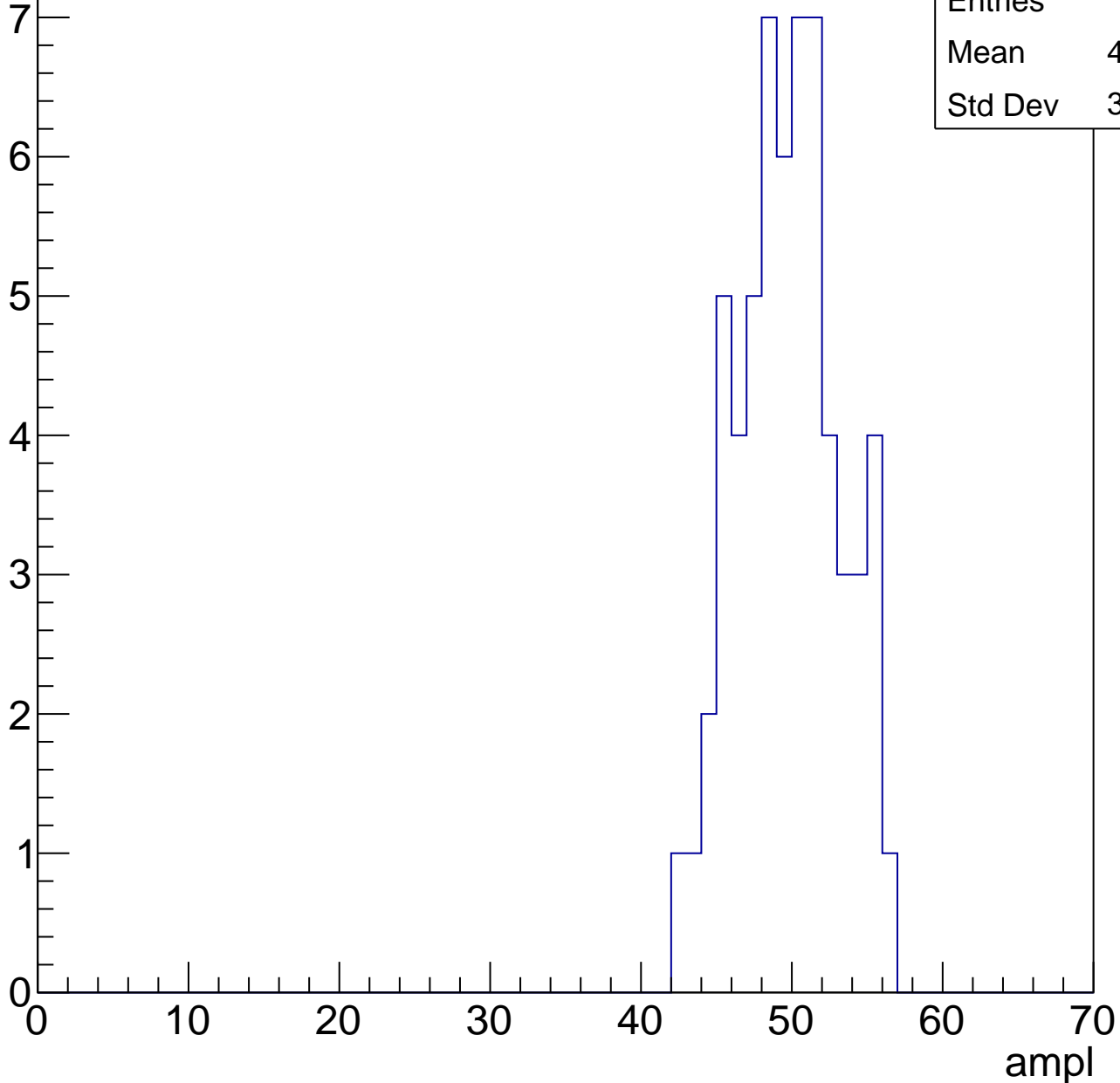


# B0L000S, U19-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

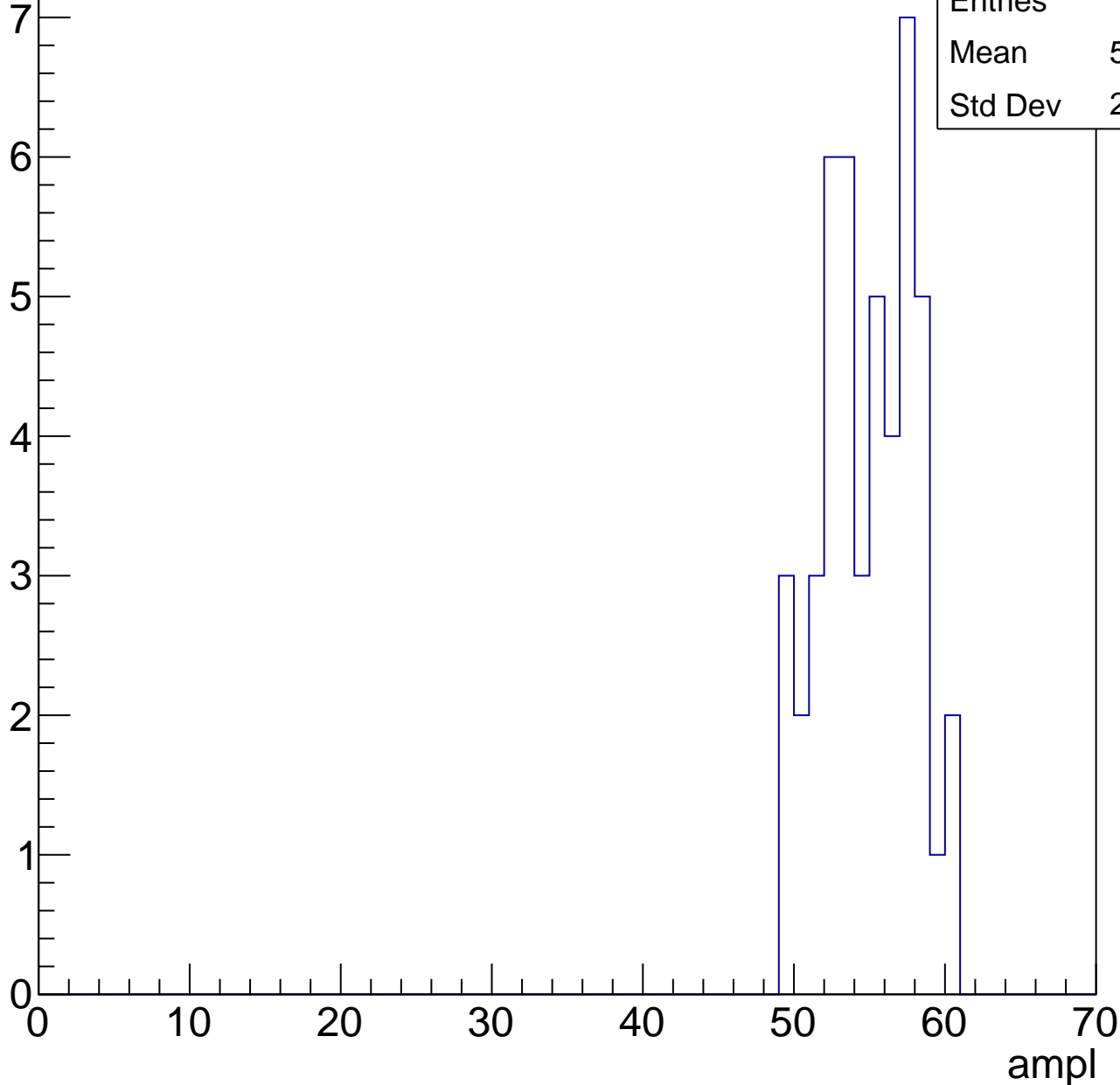
Entries	60
Mean	49.32
Std Dev	3.314



# B0L000S, U19-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



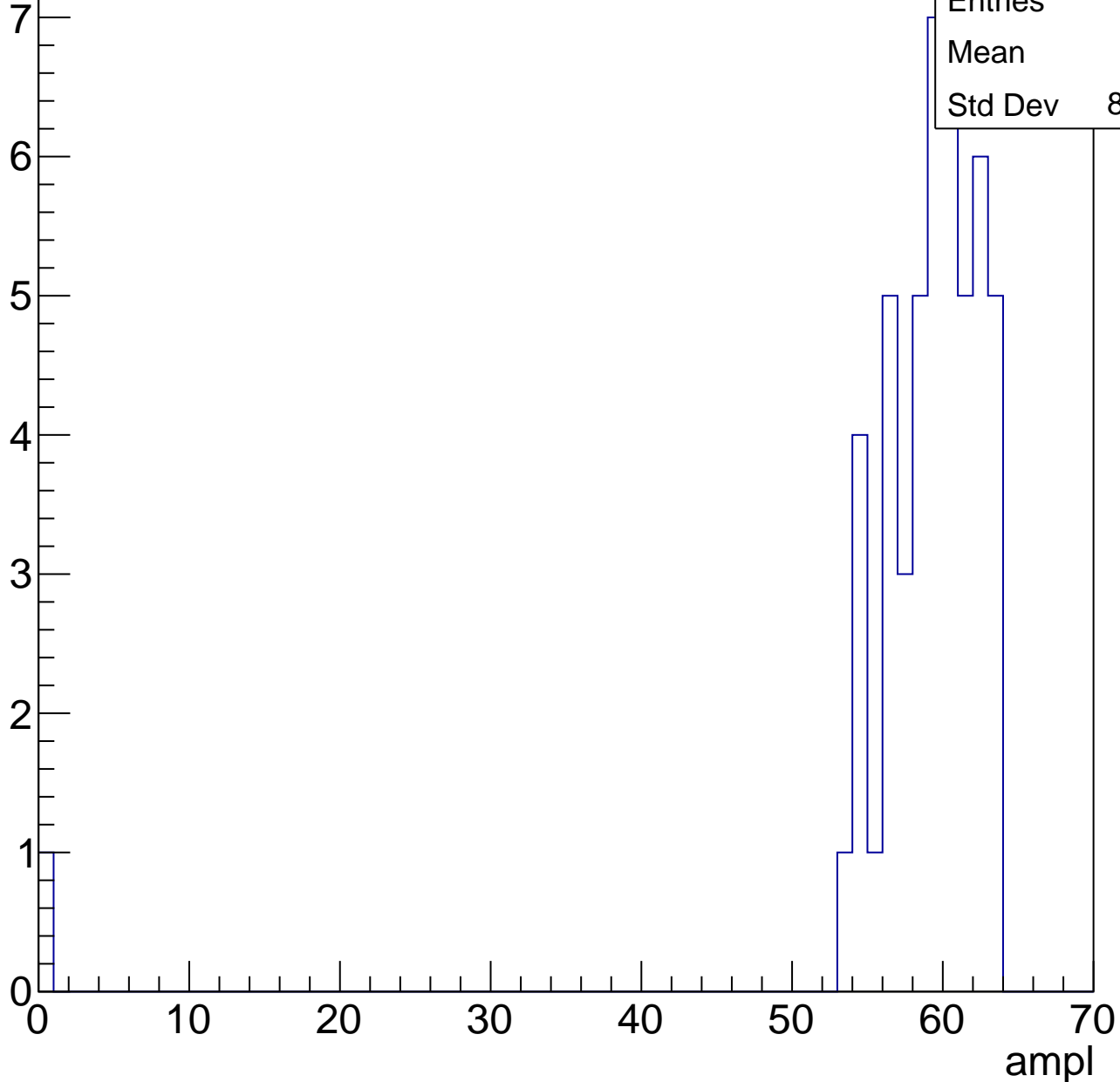
Entries	47
Mean	54.45
Std Dev	2.974

# B0L000S, U19-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	57.8
Std Dev	8.699

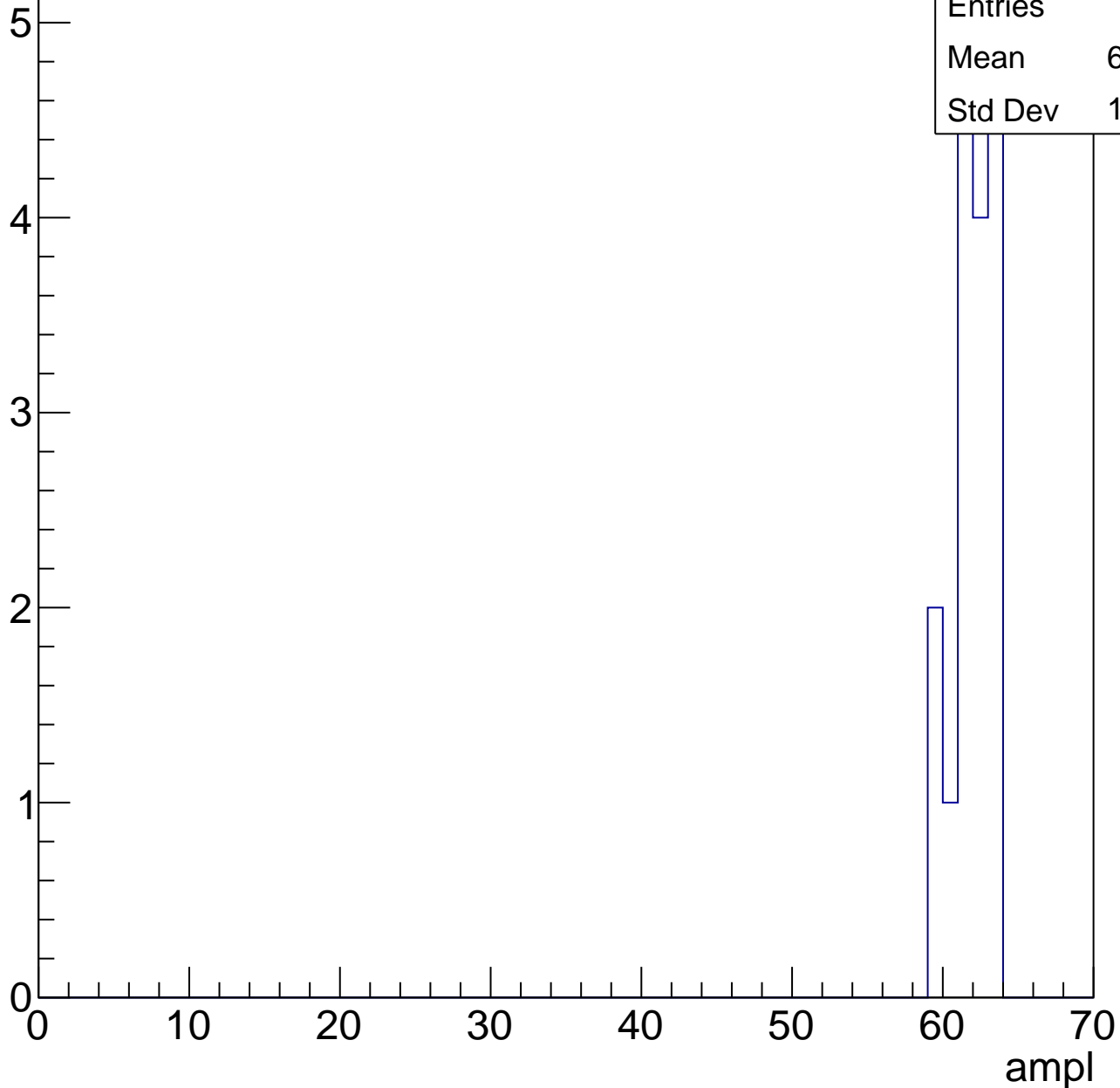


# B0L000S, U19-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61.53
Std Dev	1.289





# B0L000S, U19-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch117, adc0

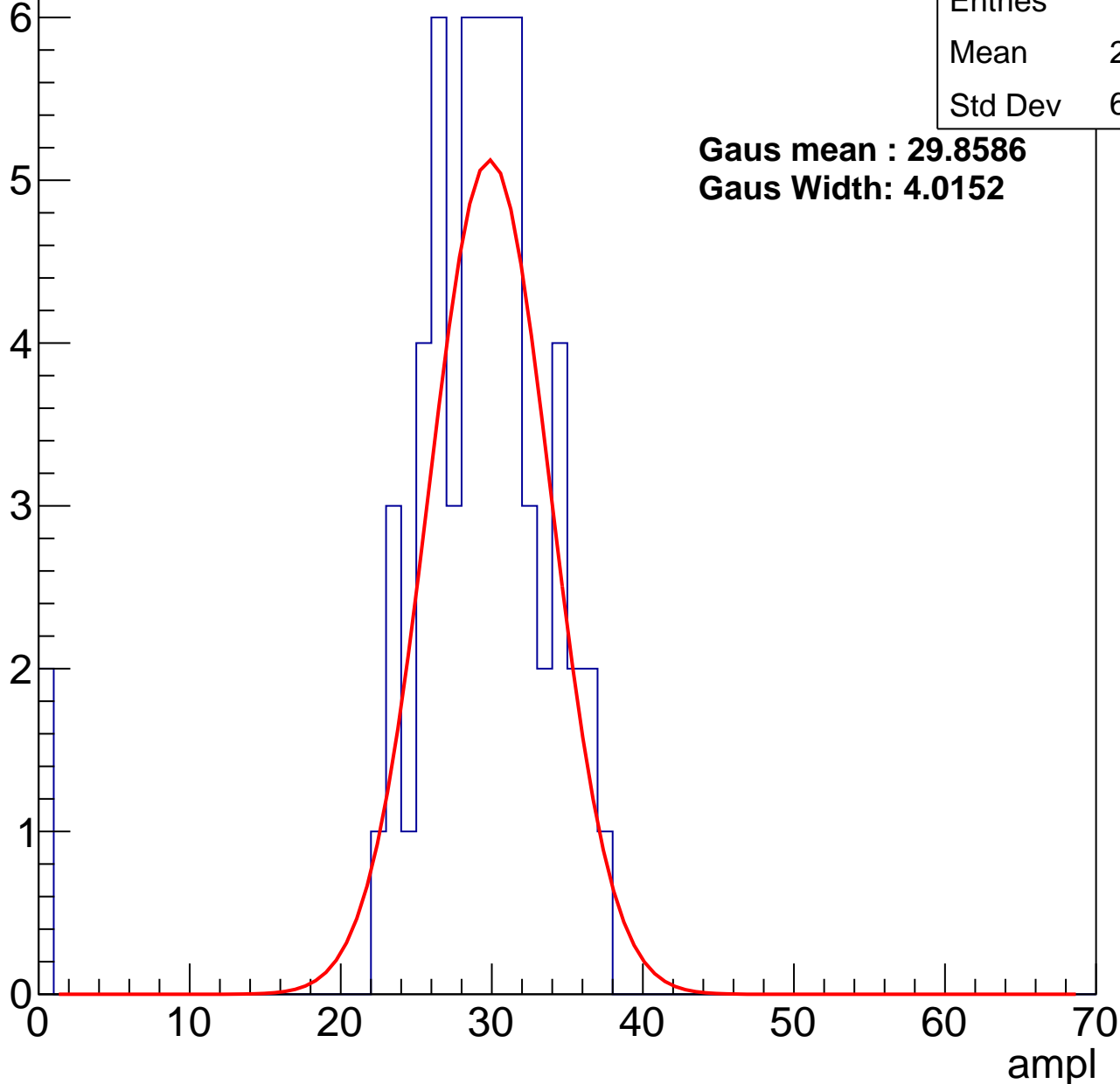
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	28.22
Std Dev	6.417

**Gaus mean : 29.8586**

**Gaus Width: 4.0152**



# B0L000S, U19-ch117, adc1

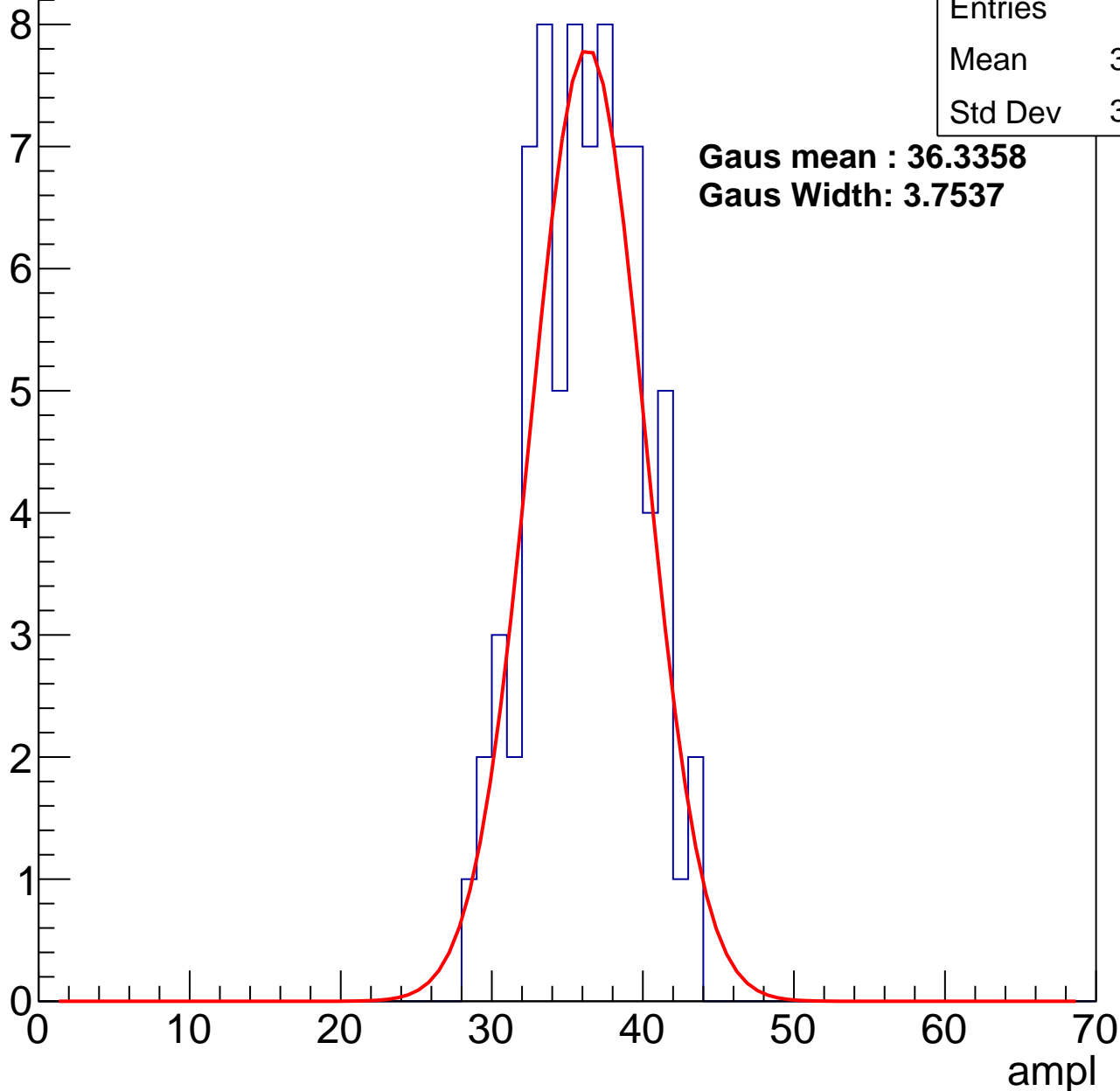
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	35.79
Std Dev	3.514

**Gaus mean : 36.3358**

**Gaus Width: 3.7537**



# B0L000S, U19-ch117, adc2

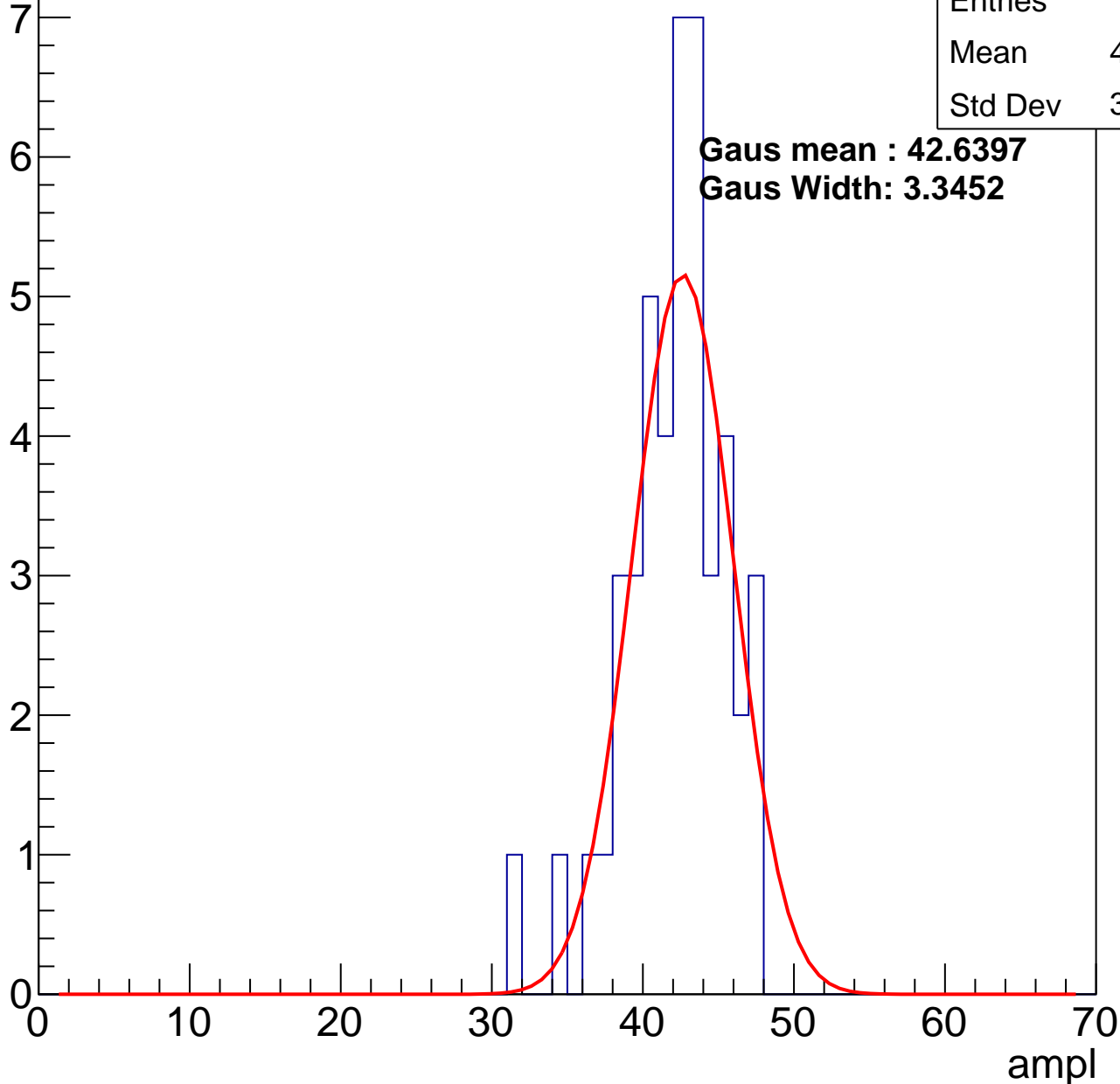
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	41.62
Std Dev	3.335

**Gaus mean : 42.6397**

**Gaus Width: 3.3452**

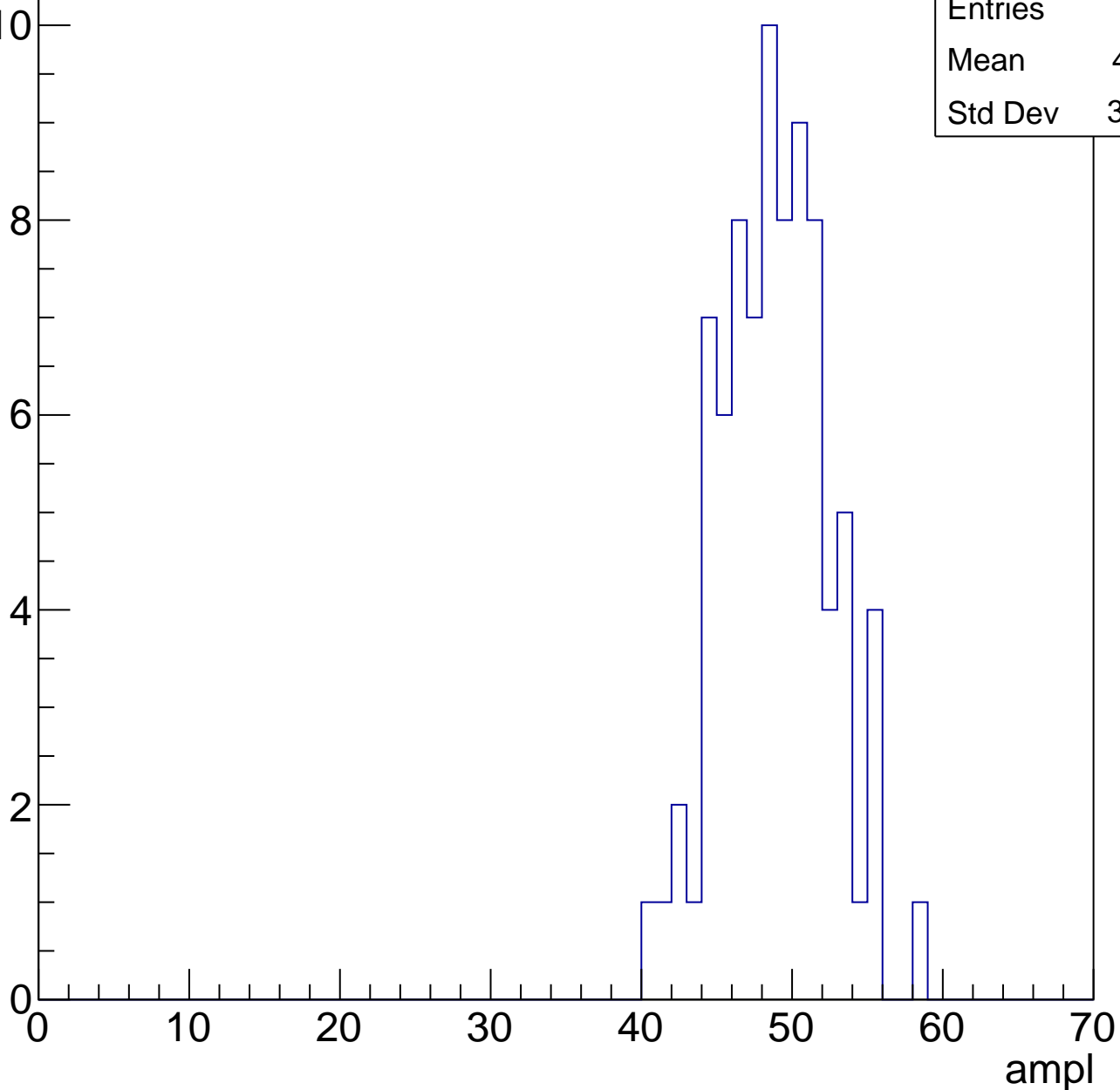


# B0L000S, U19-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	48.41
Std Dev	3.537

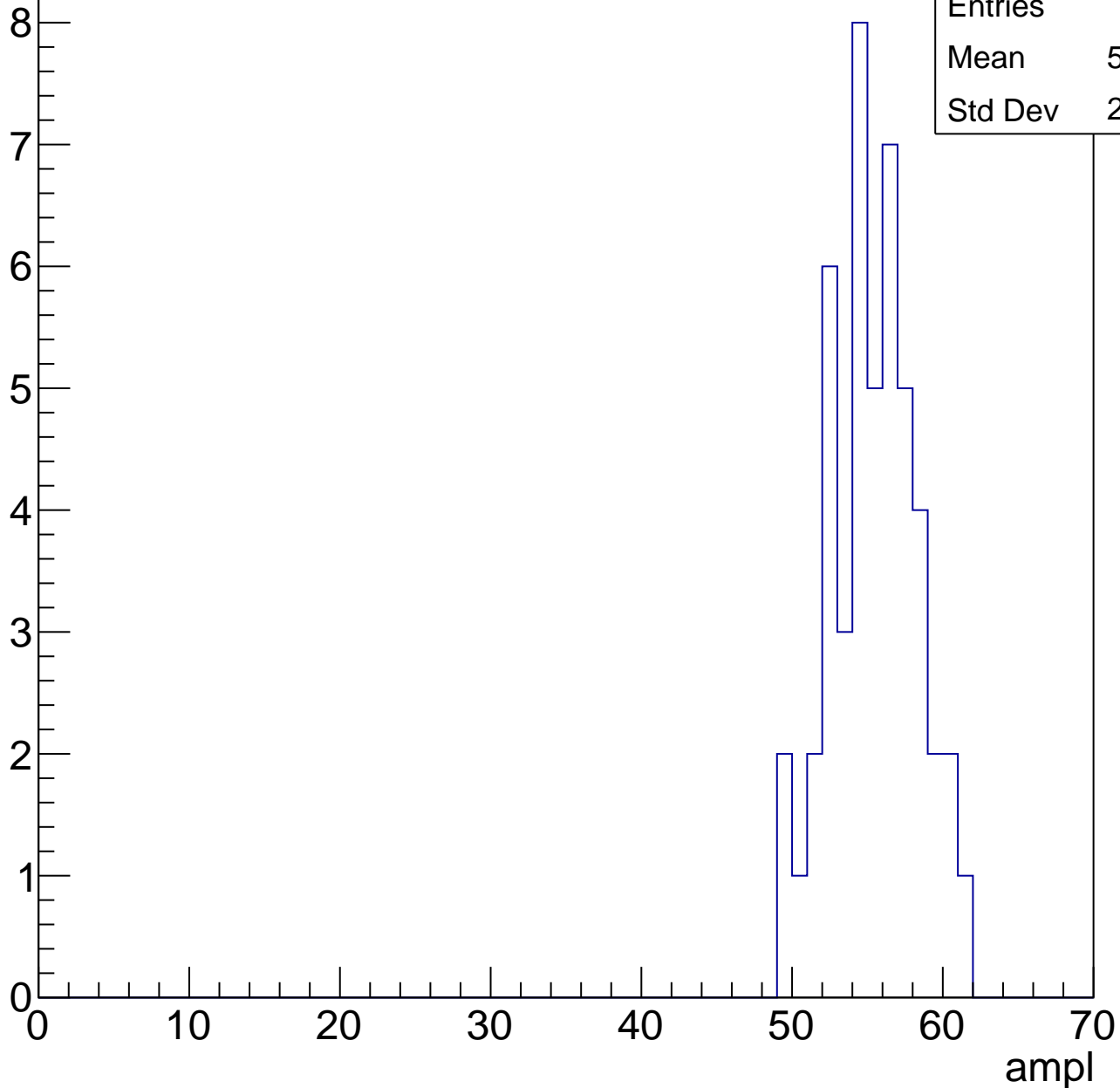


# B0L000S, U19-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	54.92
Std Dev	2.827

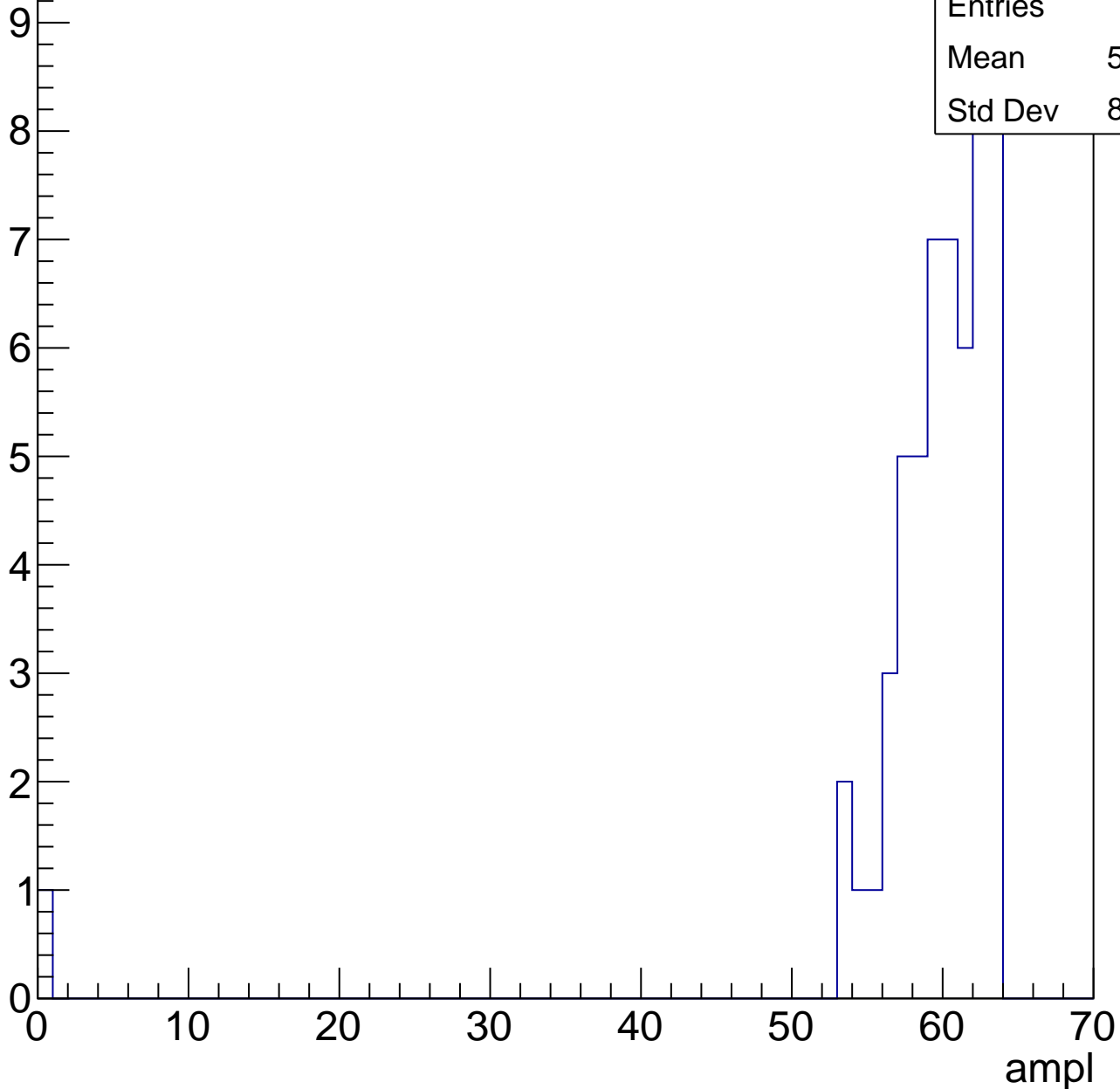


# B0L000S, U19-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.53
Std Dev	8.392



# B0L000S, U19-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

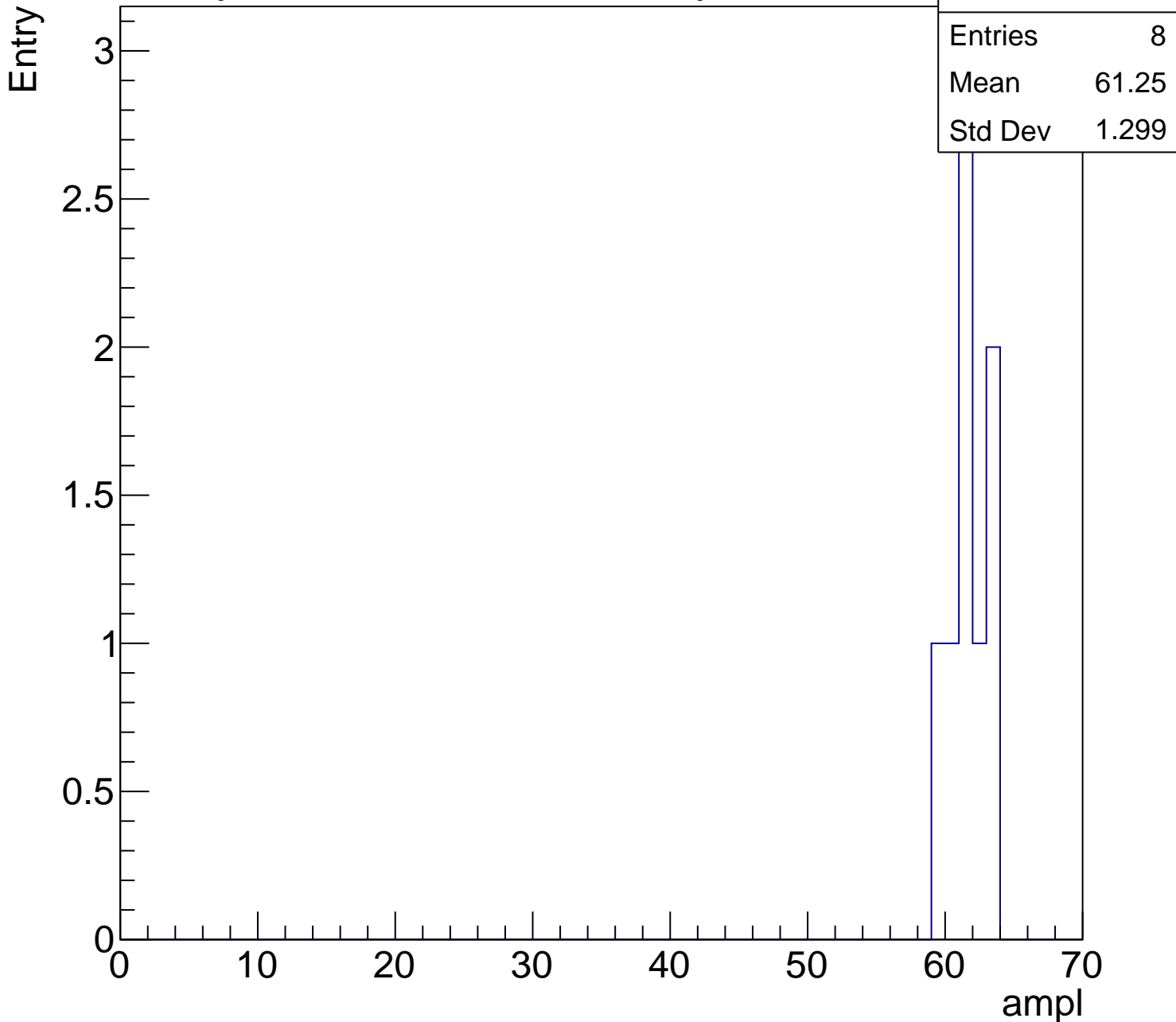
8

Mean

61.25

Std Dev

1.299





# B0L000S, U19-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch118, adc0

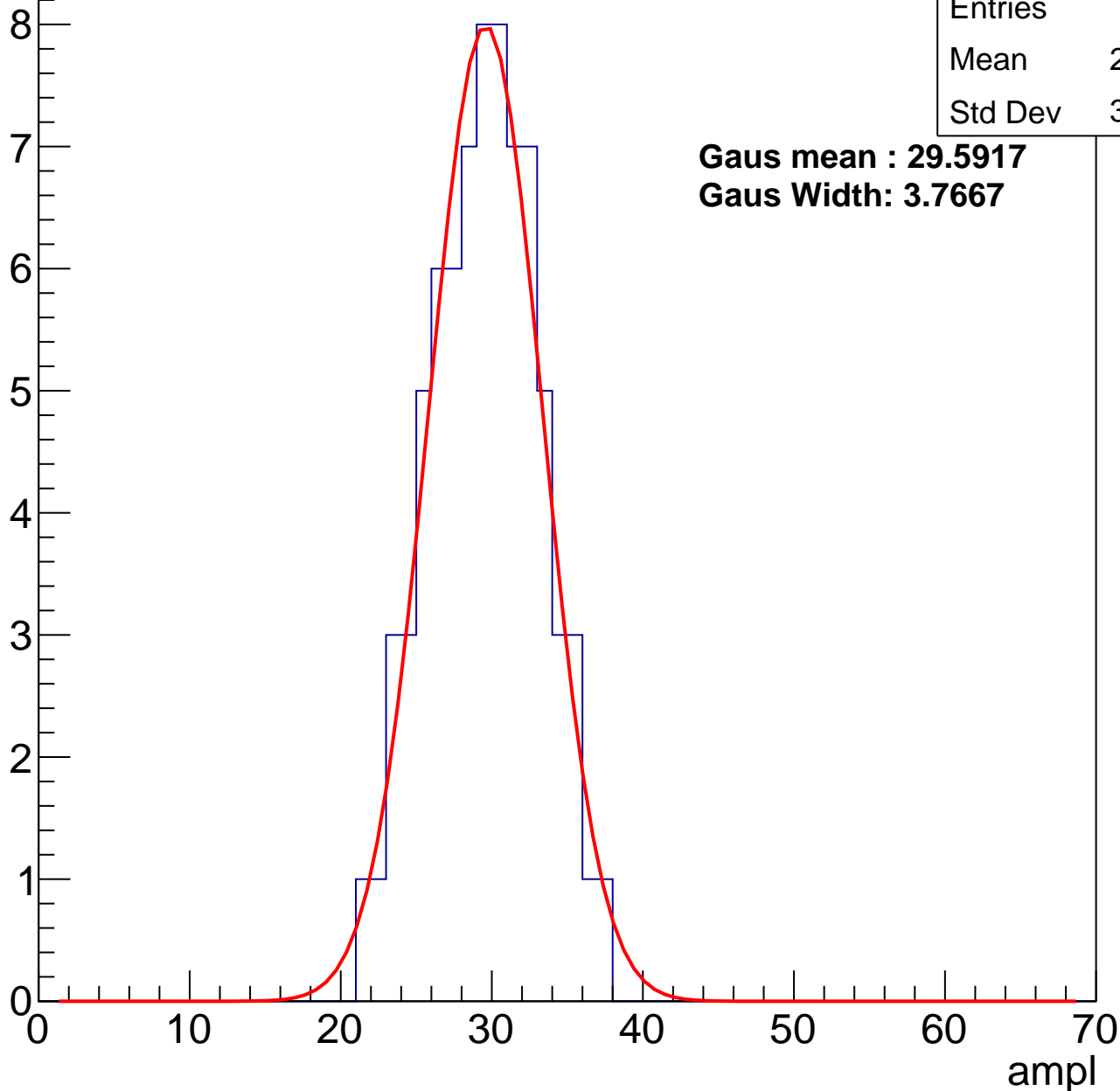
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.08
Std Dev	3.532

**Gaus mean : 29.5917**

**Gaus Width: 3.7667**



# B0L000S, U19-ch118, adc1

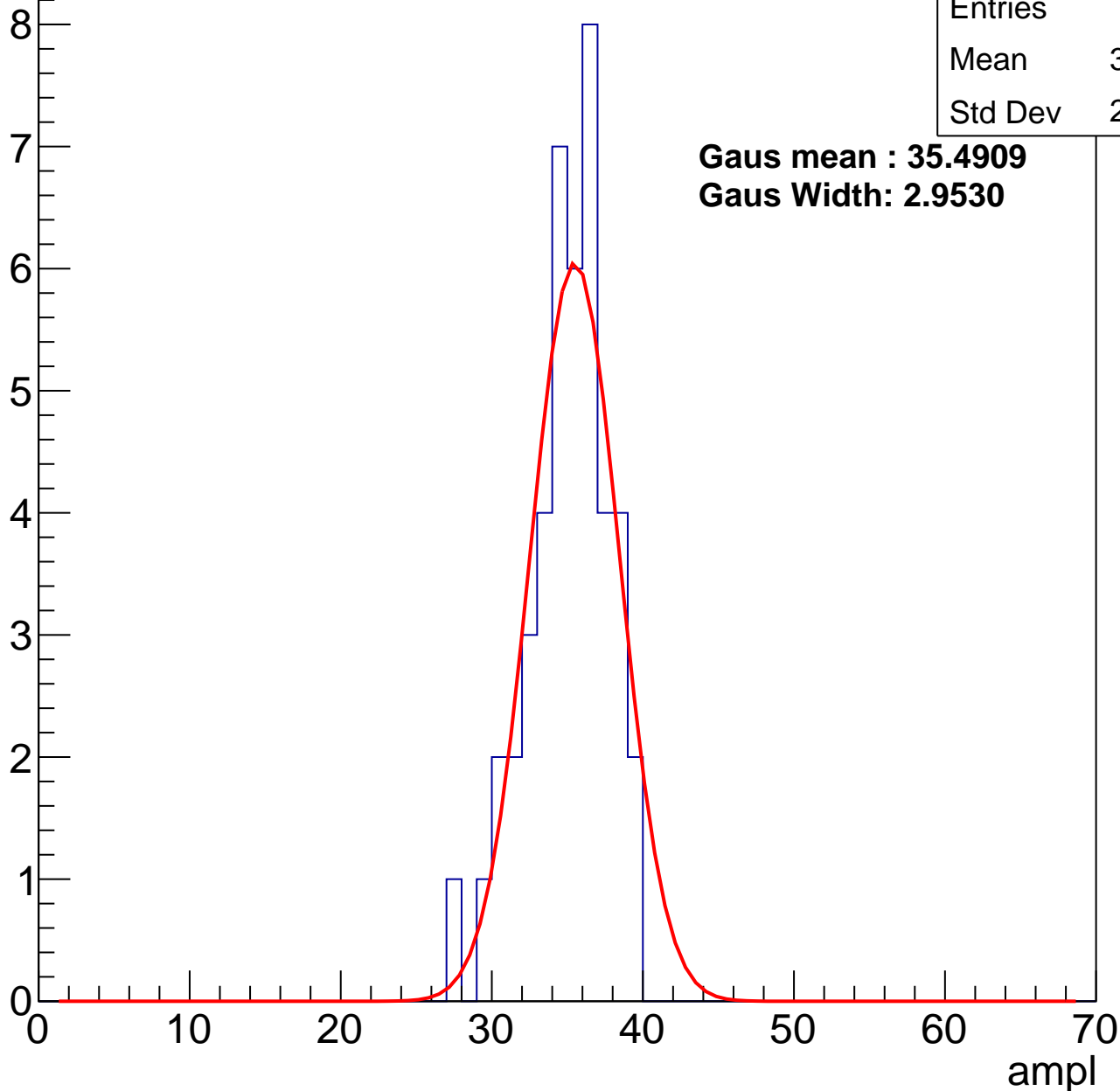
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	34.55
Std Dev	2.675

**Gaus mean : 35.4909**

**Gaus Width: 2.9530**



# B0L000S, U19-ch118, adc2

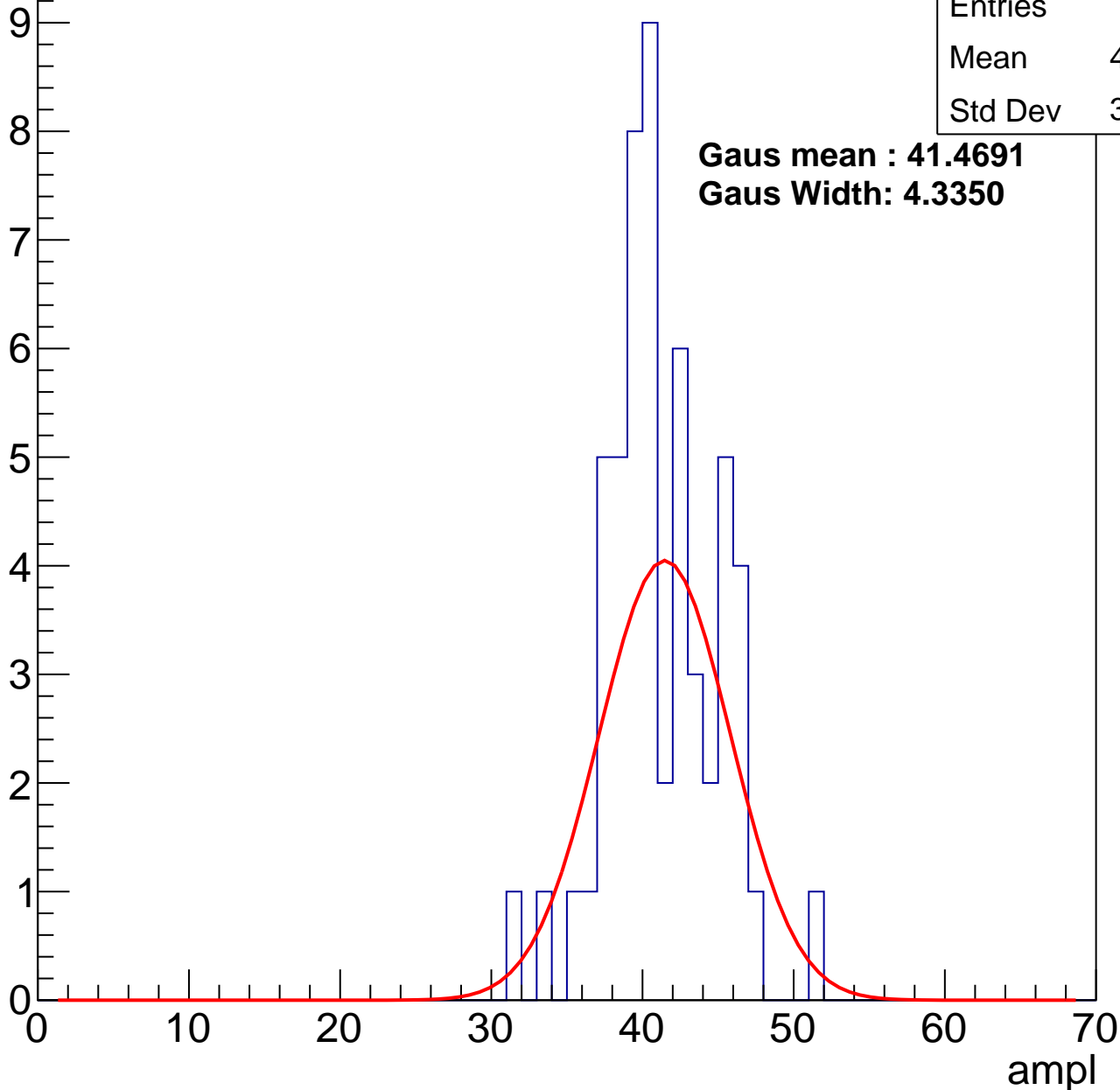
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	40.73
Std Dev	3.665

**Gaus mean : 41.4691**

**Gaus Width: 4.3350**

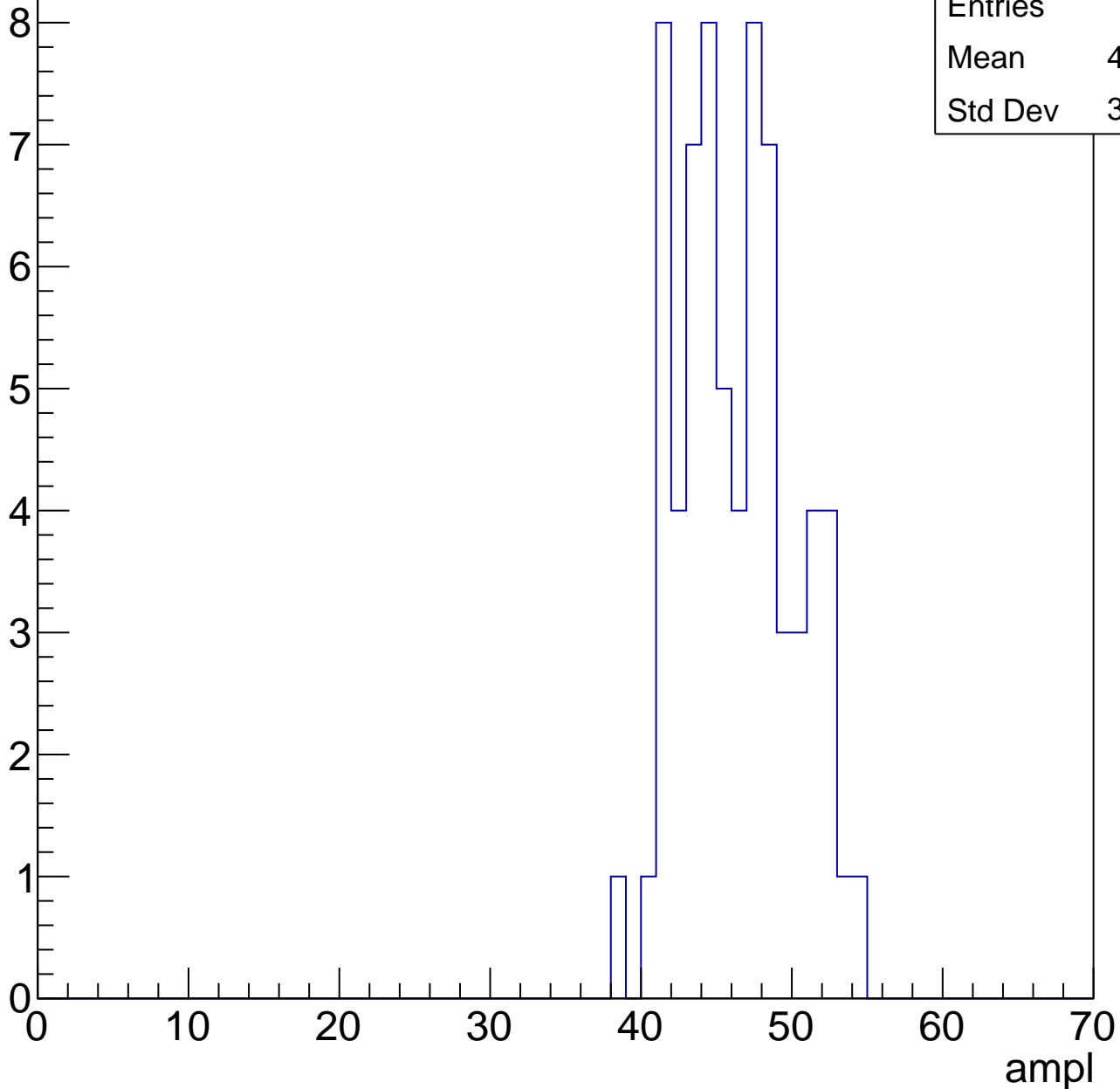


# B0L000S, U19-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	45.86
Std Dev	3.672

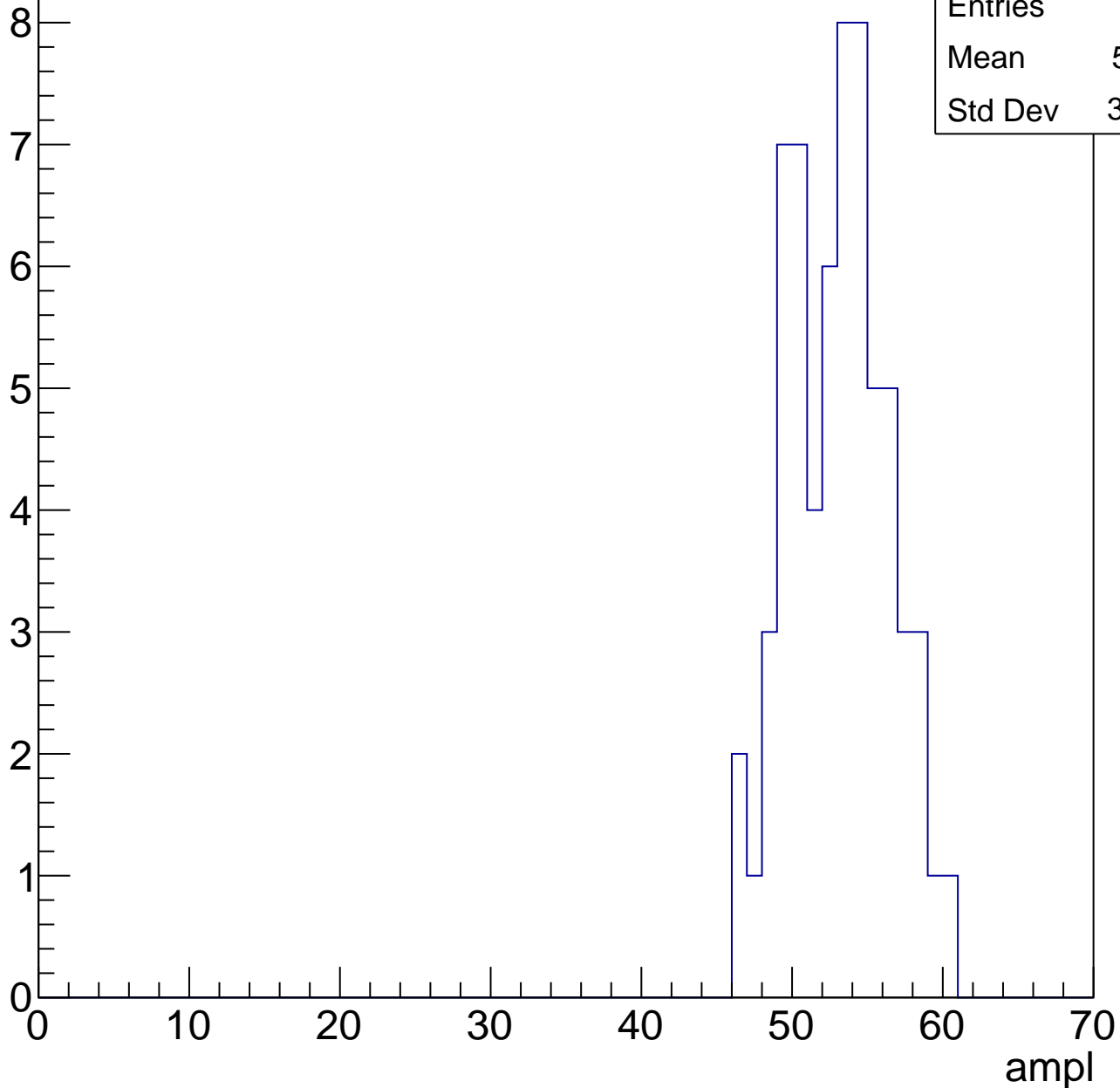


# B0L000S, U19-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	52.61
Std Dev	3.253

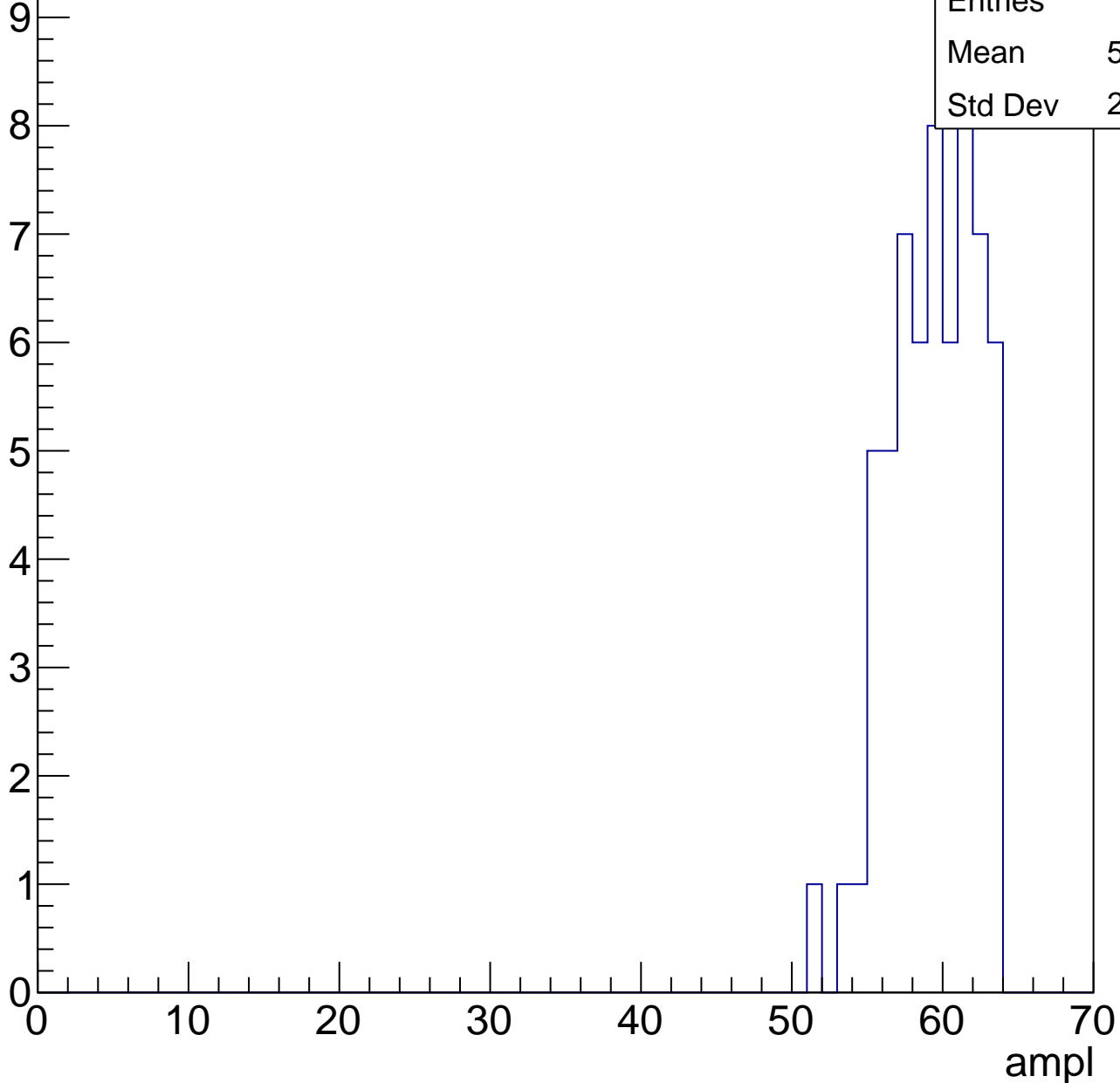


# B0L000S, U19-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

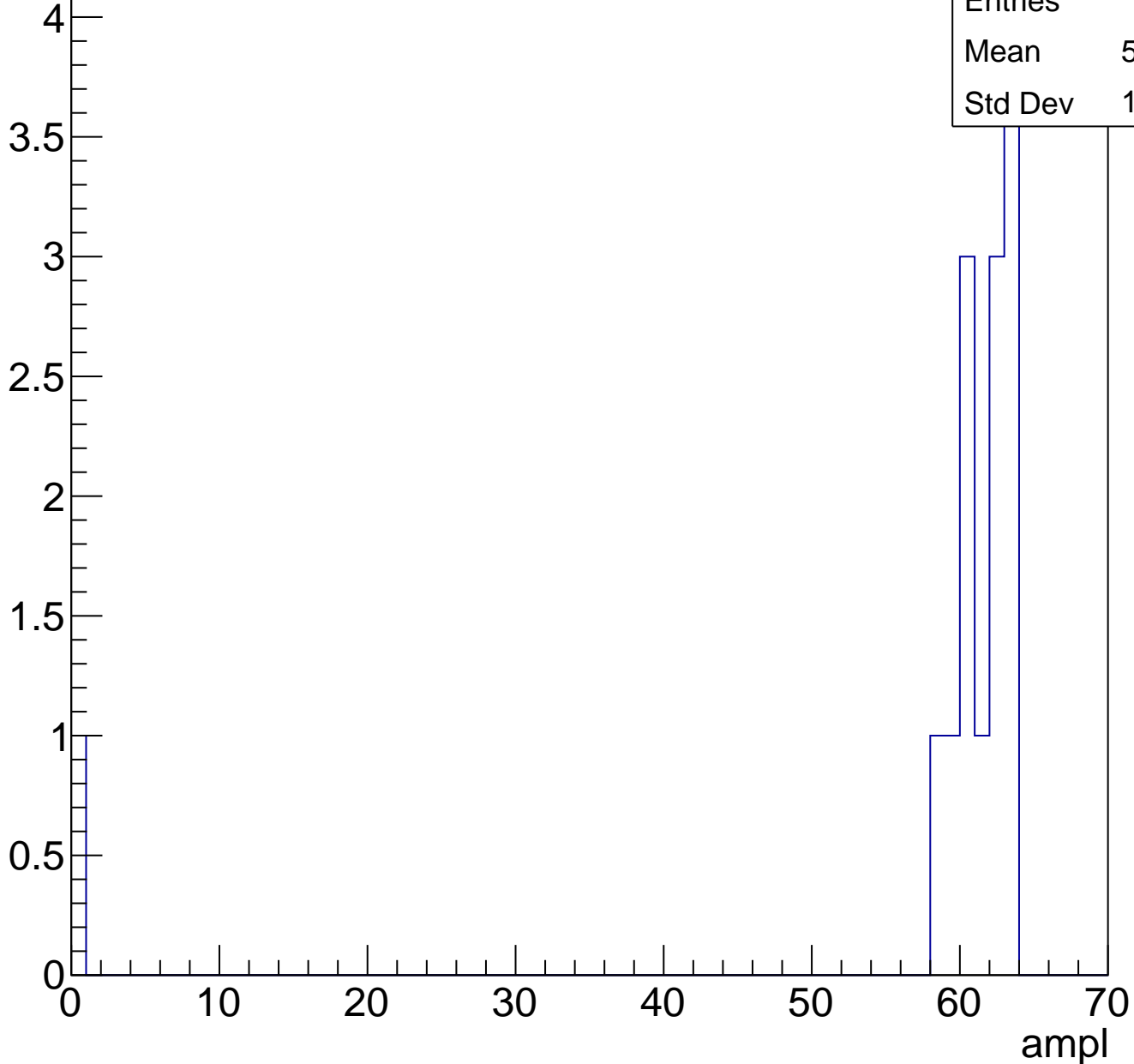
Entries	62
Mean	58.92
Std Dev	2.796



# B0L000S, U19-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

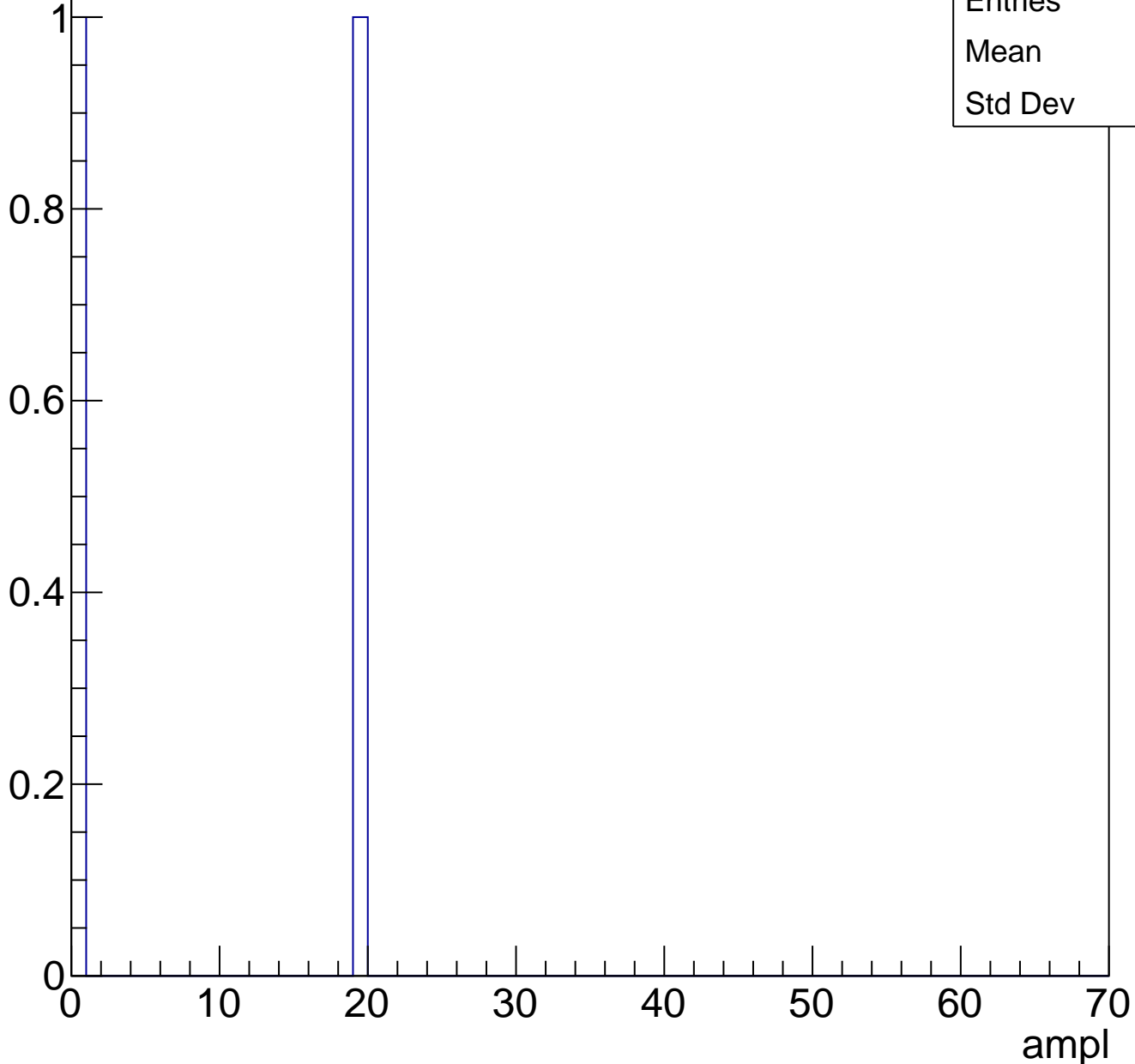




# B0L000S, U19-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B0L000S, U19-ch119, adc0

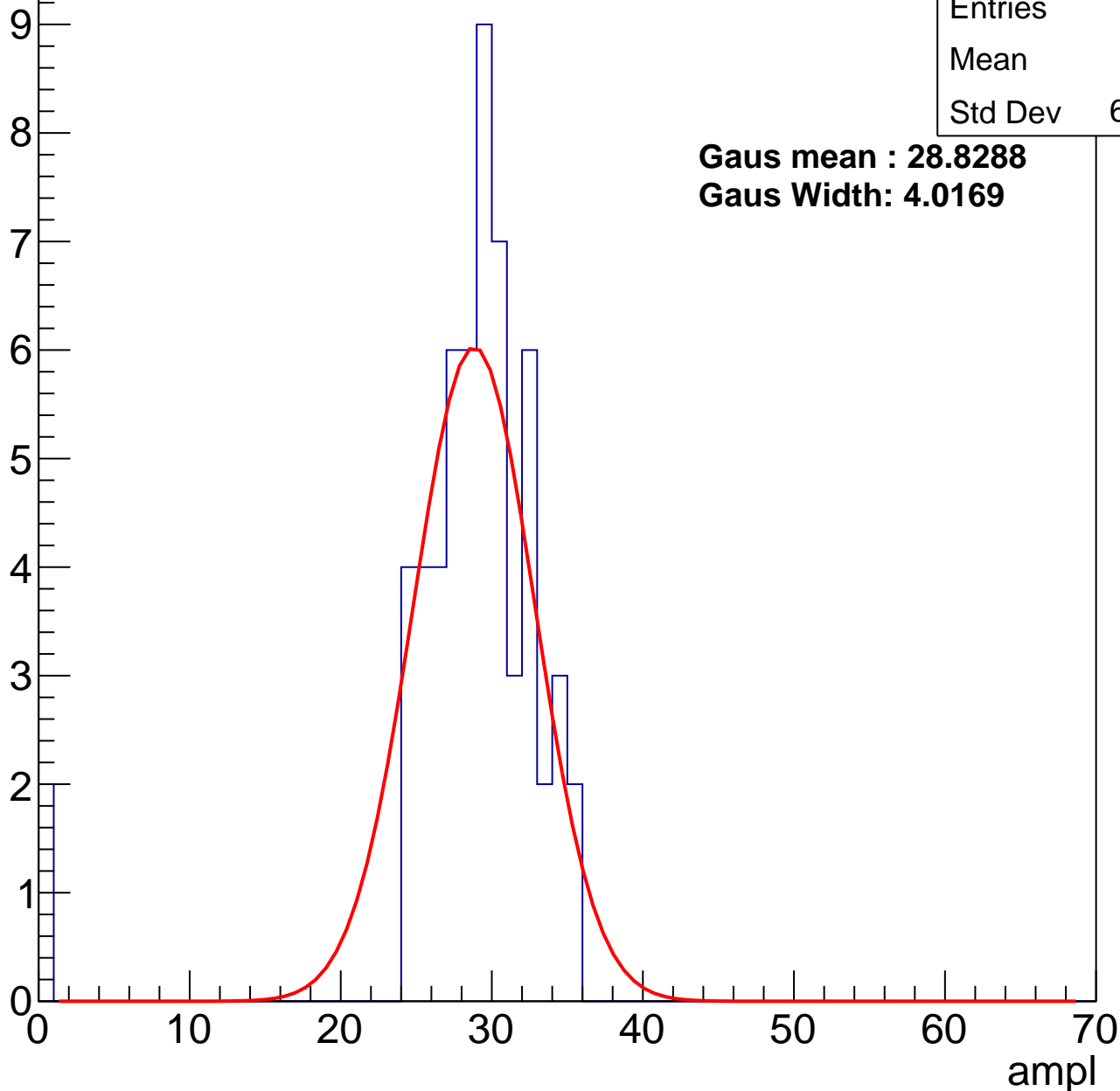
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	28
Std Dev	6.026

**Gaus mean : 28.8288**

**Gaus Width: 4.0169**

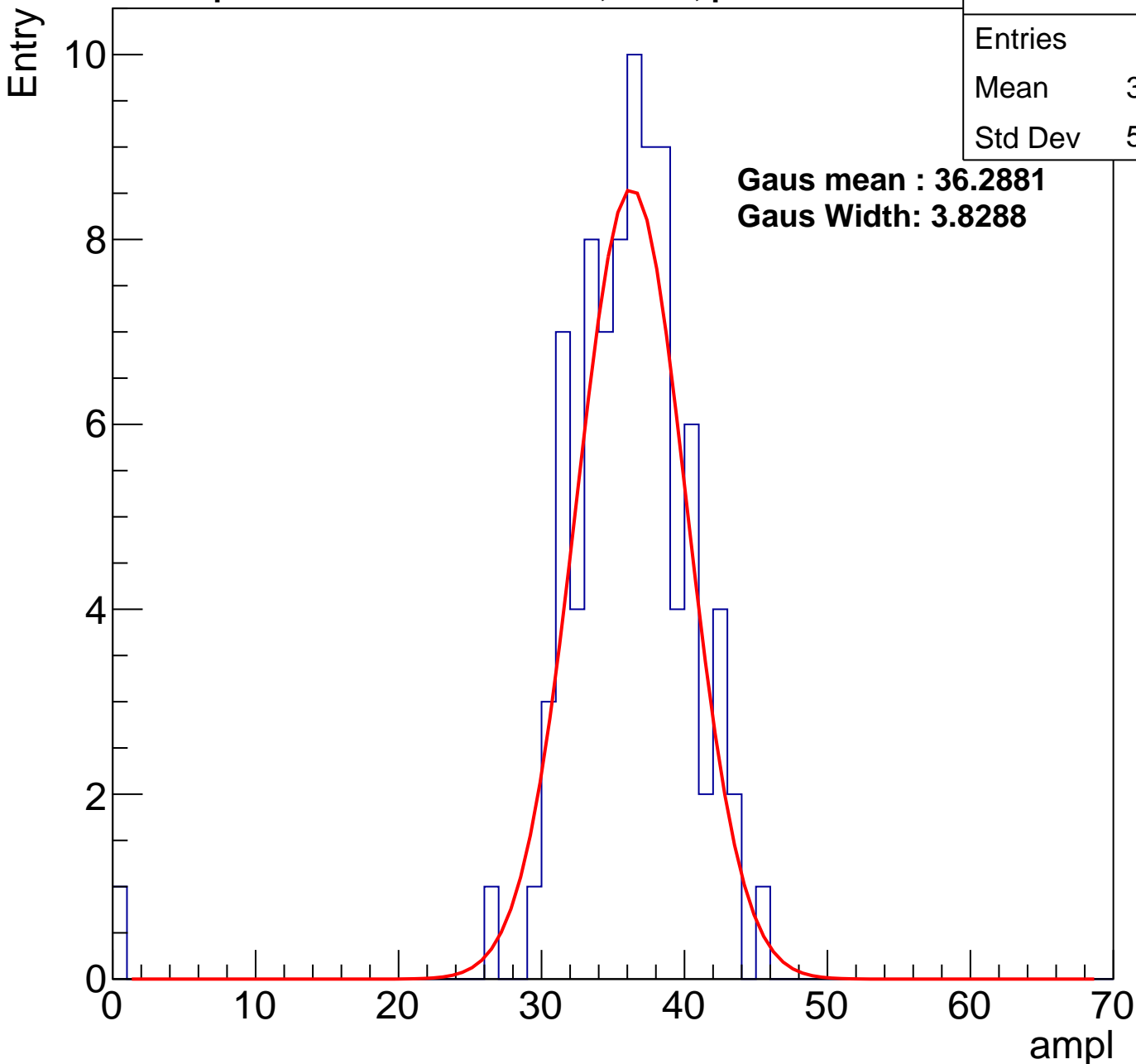


# B0L000S, U19-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	35.45
Std Dev	5.278

**Gaus mean : 36.2881**  
**Gaus Width: 3.8288**



# B0L000S, U19-ch119, adc2

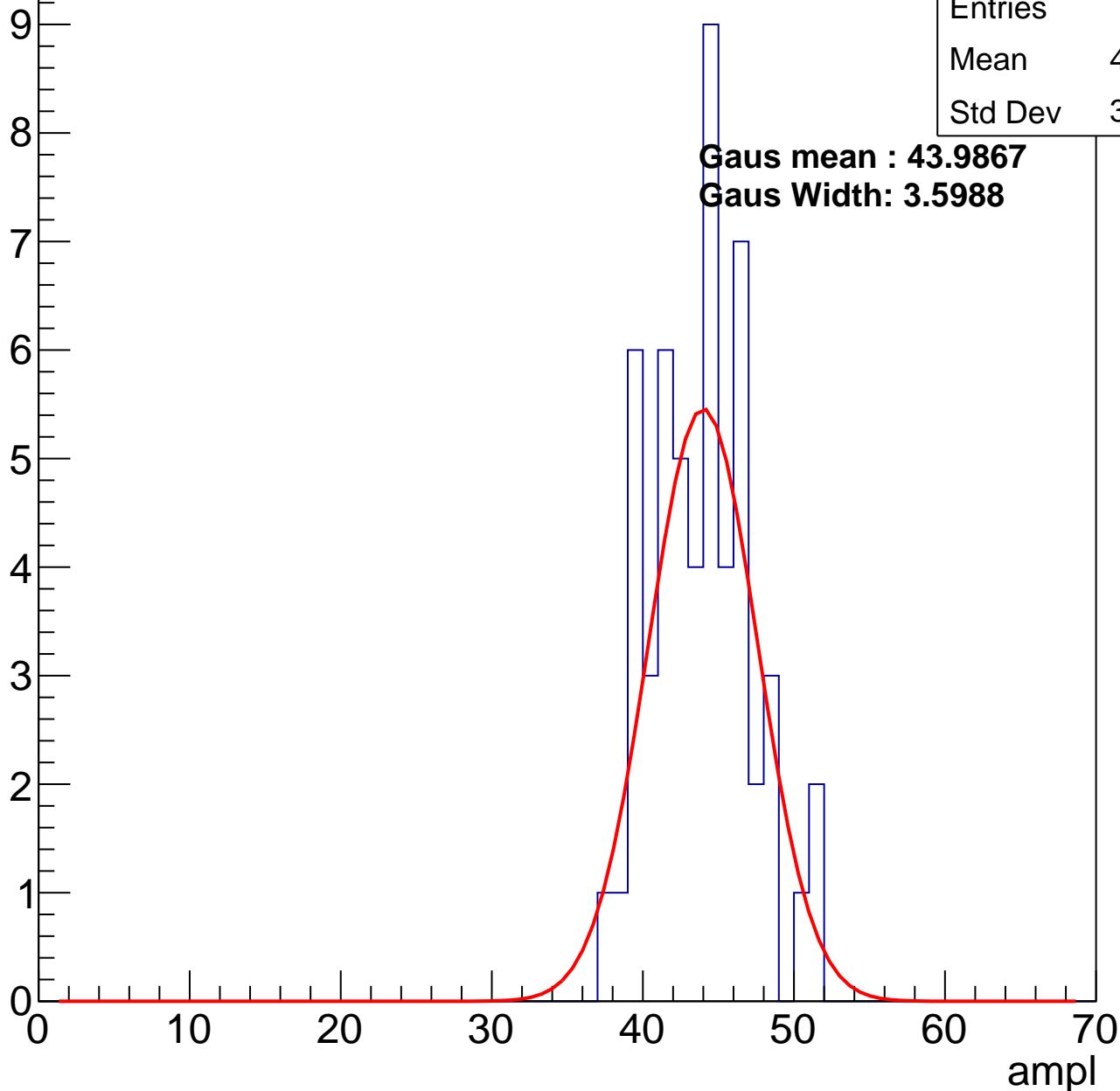
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	43.43
Std Dev	3.264

**Gaus mean : 43.9867**

**Gaus Width: 3.5988**

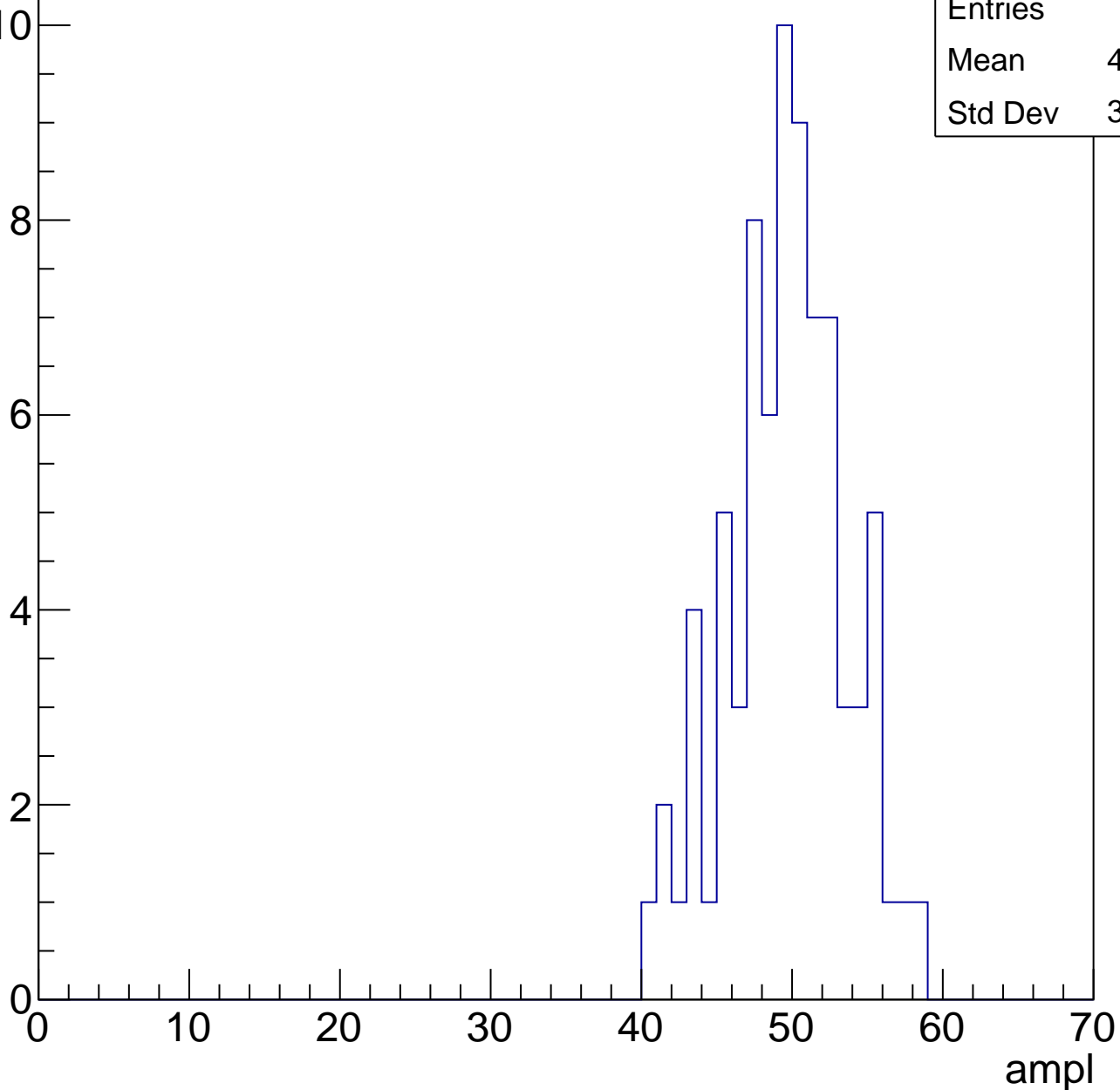


# B0L000S, U19-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	49.17
Std Dev	3.868

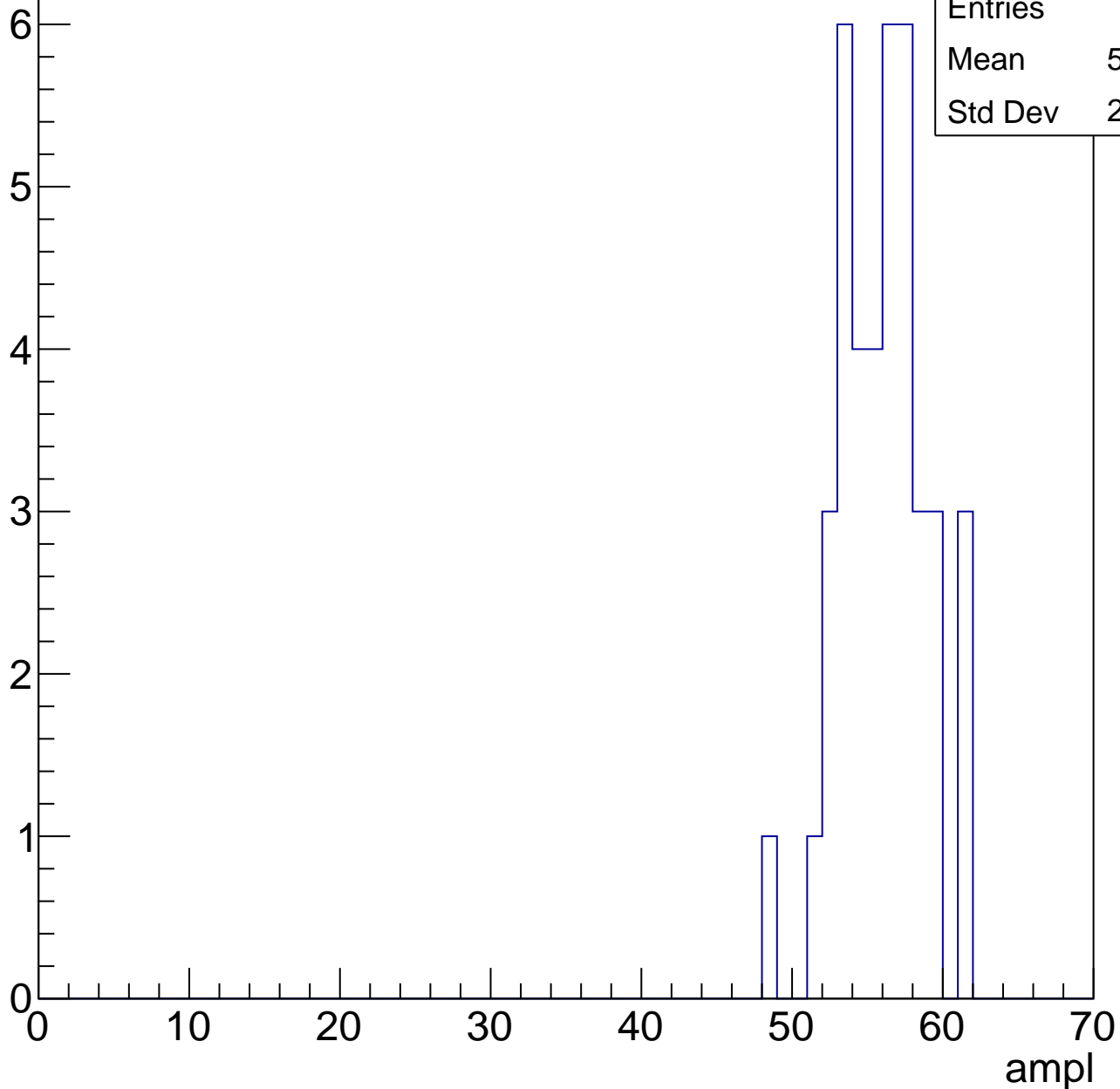


# B0L000S, U19-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	55.52
Std Dev	2.837



# B0L000S, U19-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	52
Mean	58.21
Std Dev	8.552

Entry

10

8

6

4

2

0

0

10

20

30

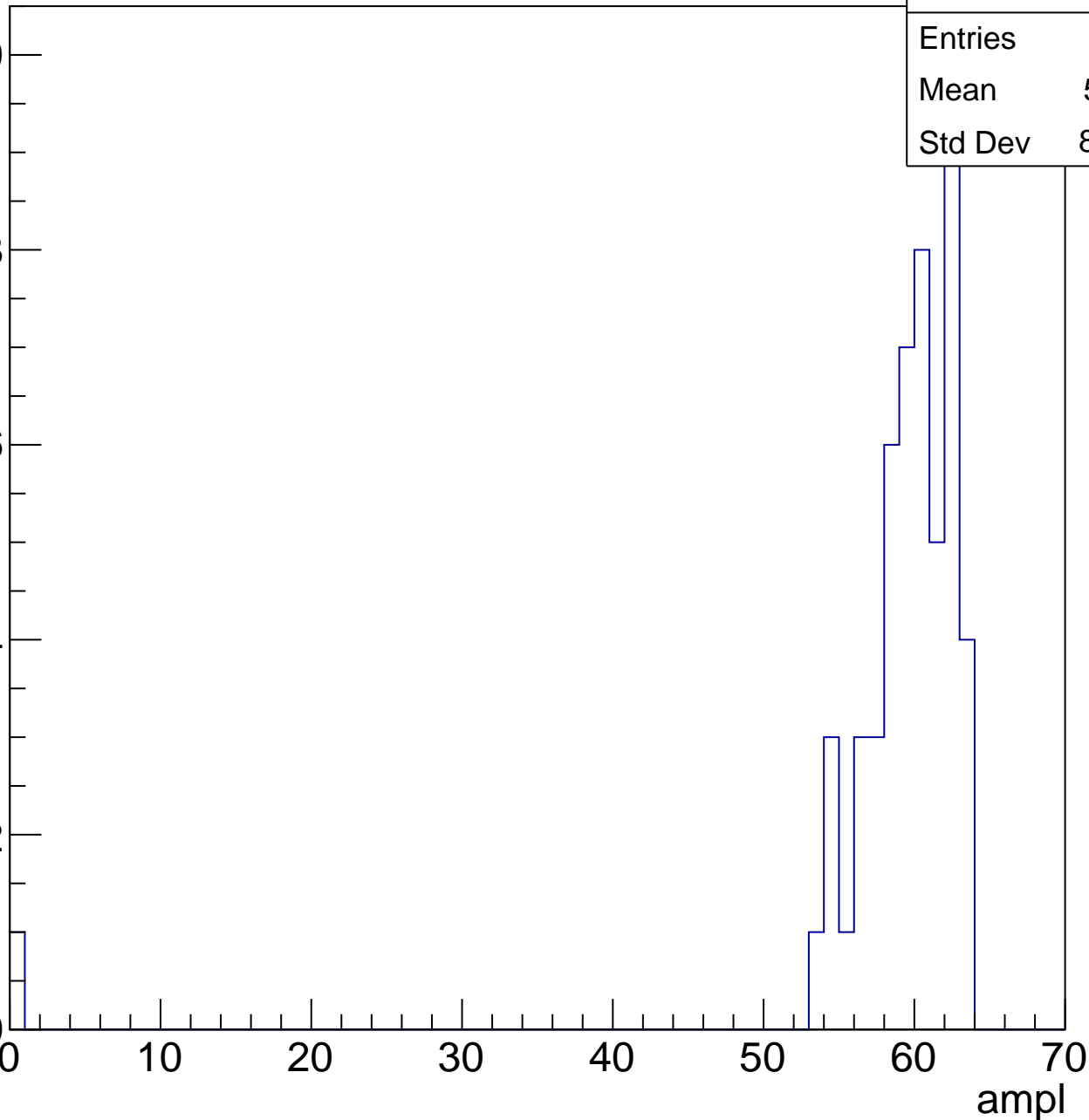
40

50

ampl

60

70

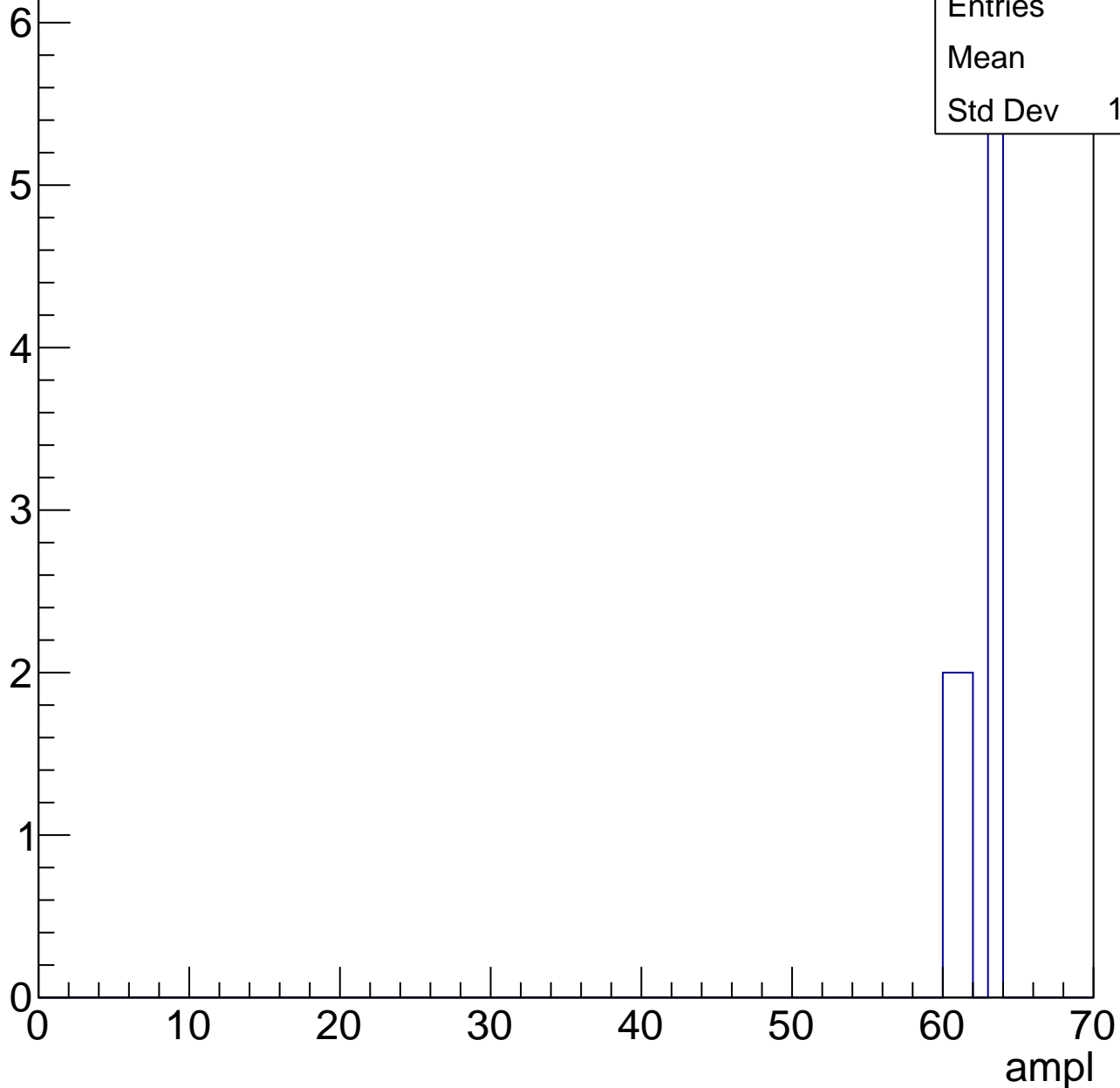


# B0L000S, U19-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62
Std Dev	1.265





# B0L000S, U19-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch120, adc0

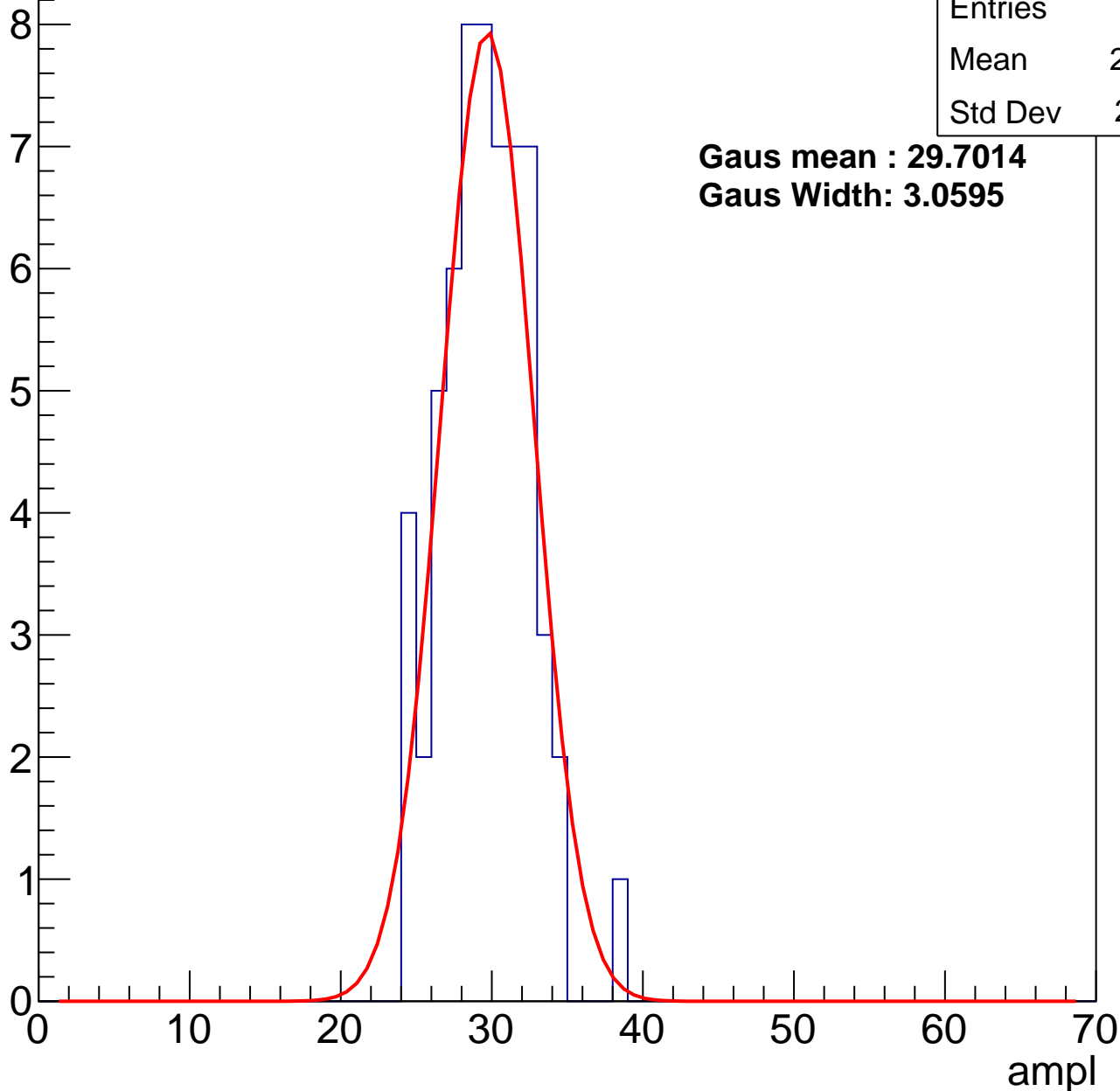
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.17
Std Dev	2.841

**Gaus mean : 29.7014**

**Gaus Width: 3.0595**



# B0L000S, U19-ch120, adc1

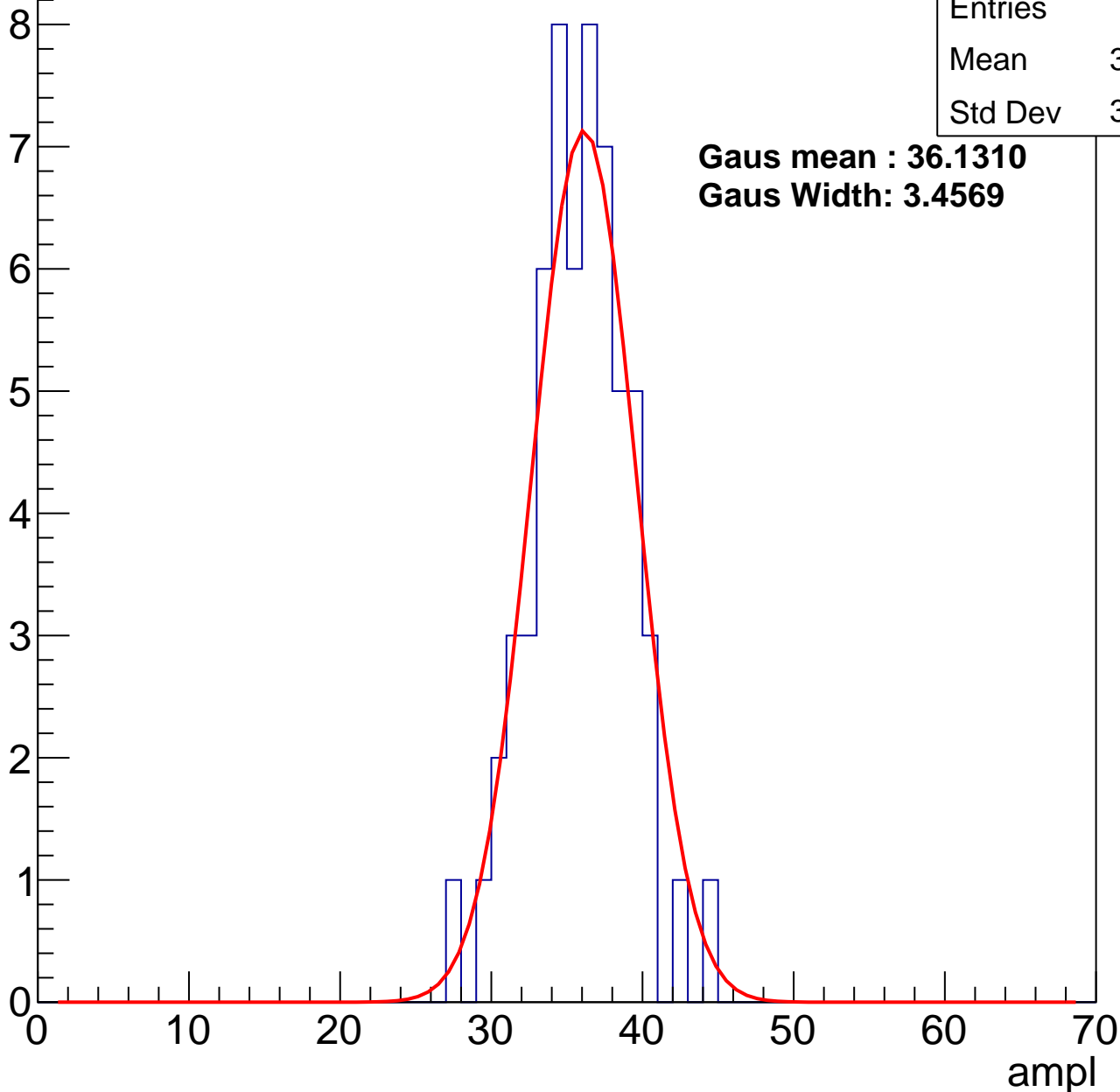
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	35.38
Std Dev	3.199

**Gaus mean : 36.1310**

**Gaus Width: 3.4569**



# B0L000S, U19-ch120, adc2

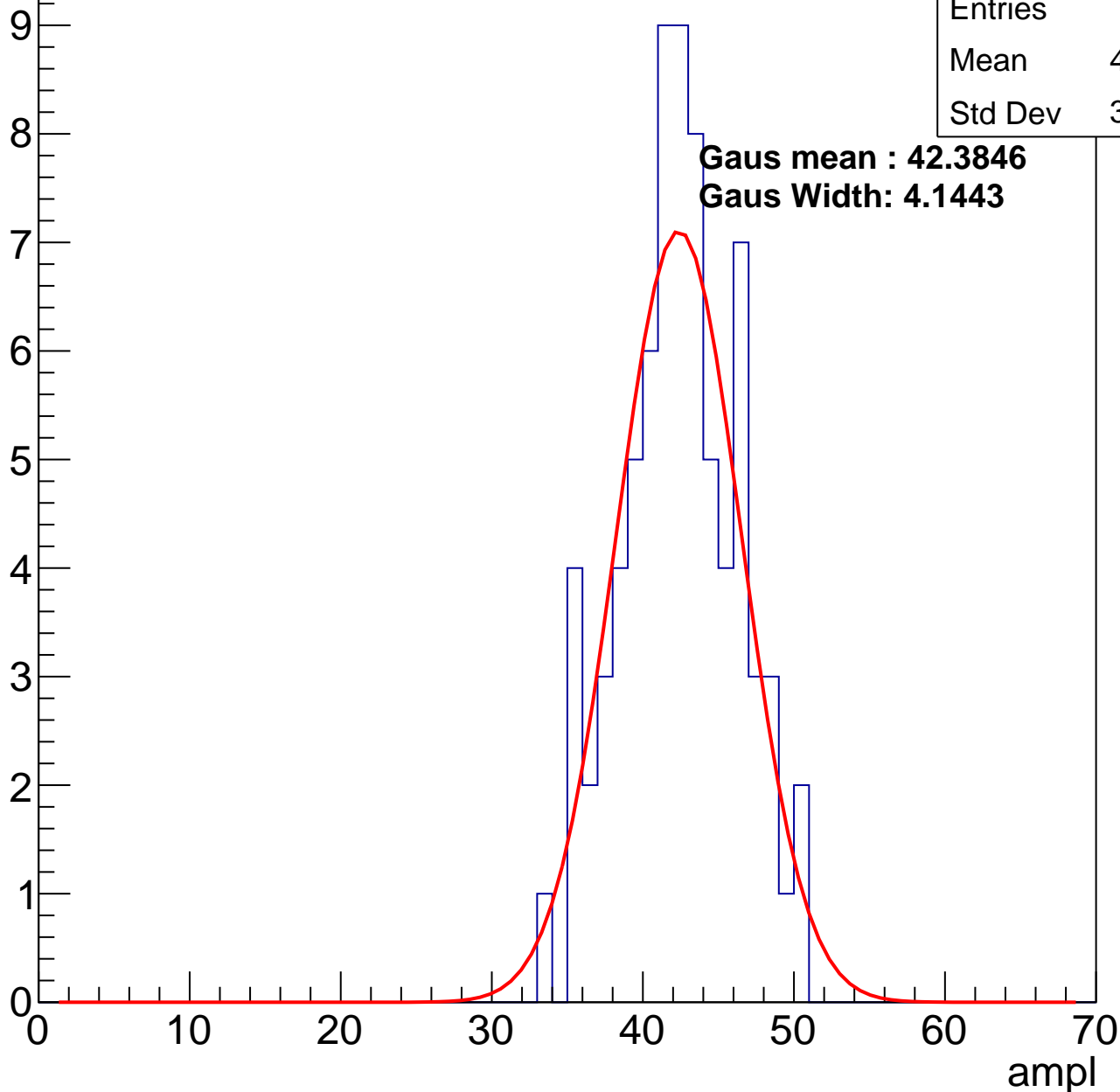
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	41.97
Std Dev	3.808

**Gaus mean : 42.3846**

**Gaus Width: 4.1443**

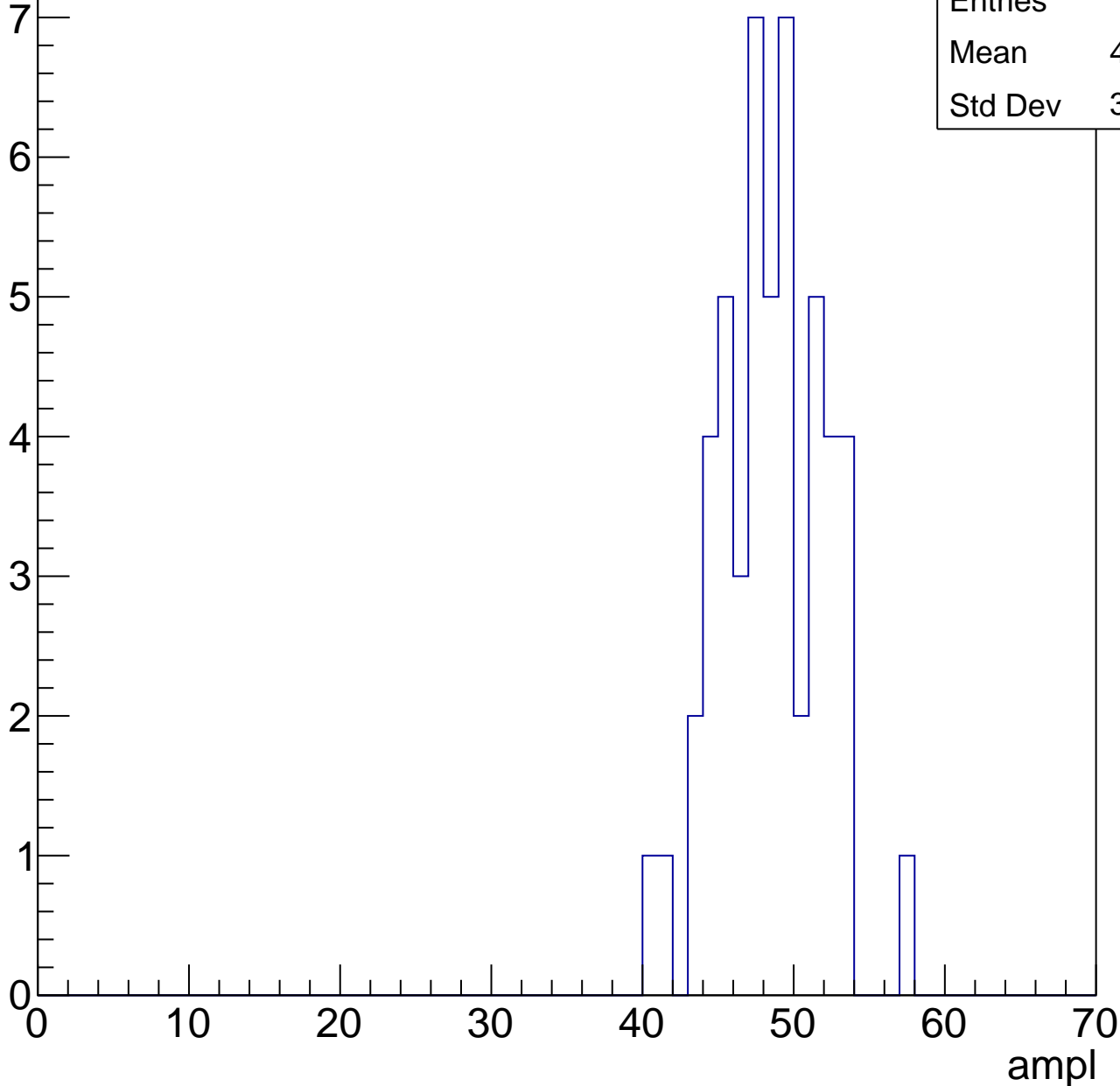


# B0L000S, U19-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	48.04
Std Dev	3.418

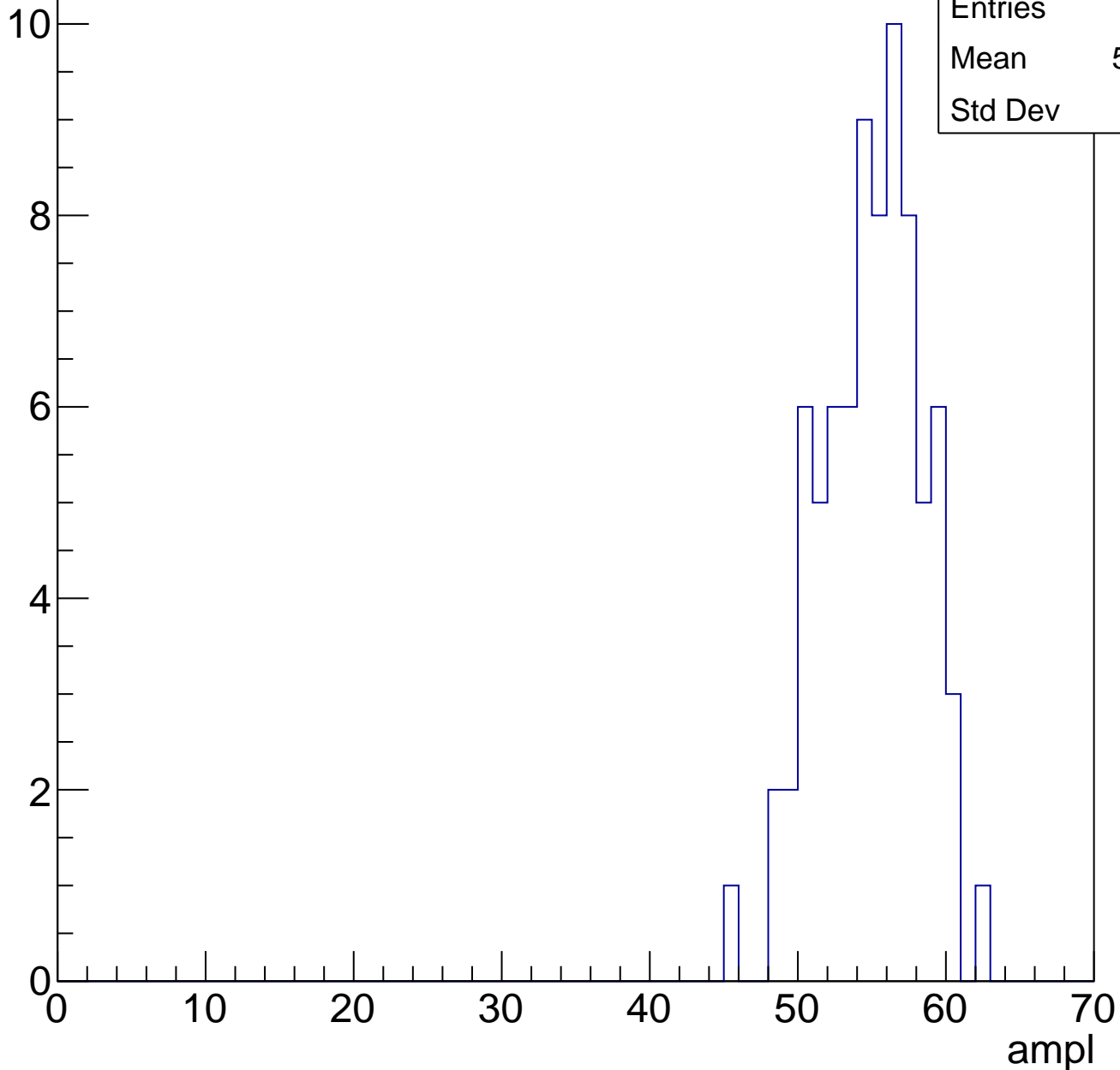


# B0L000S, U19-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	54.51
Std Dev	3.35

Entry

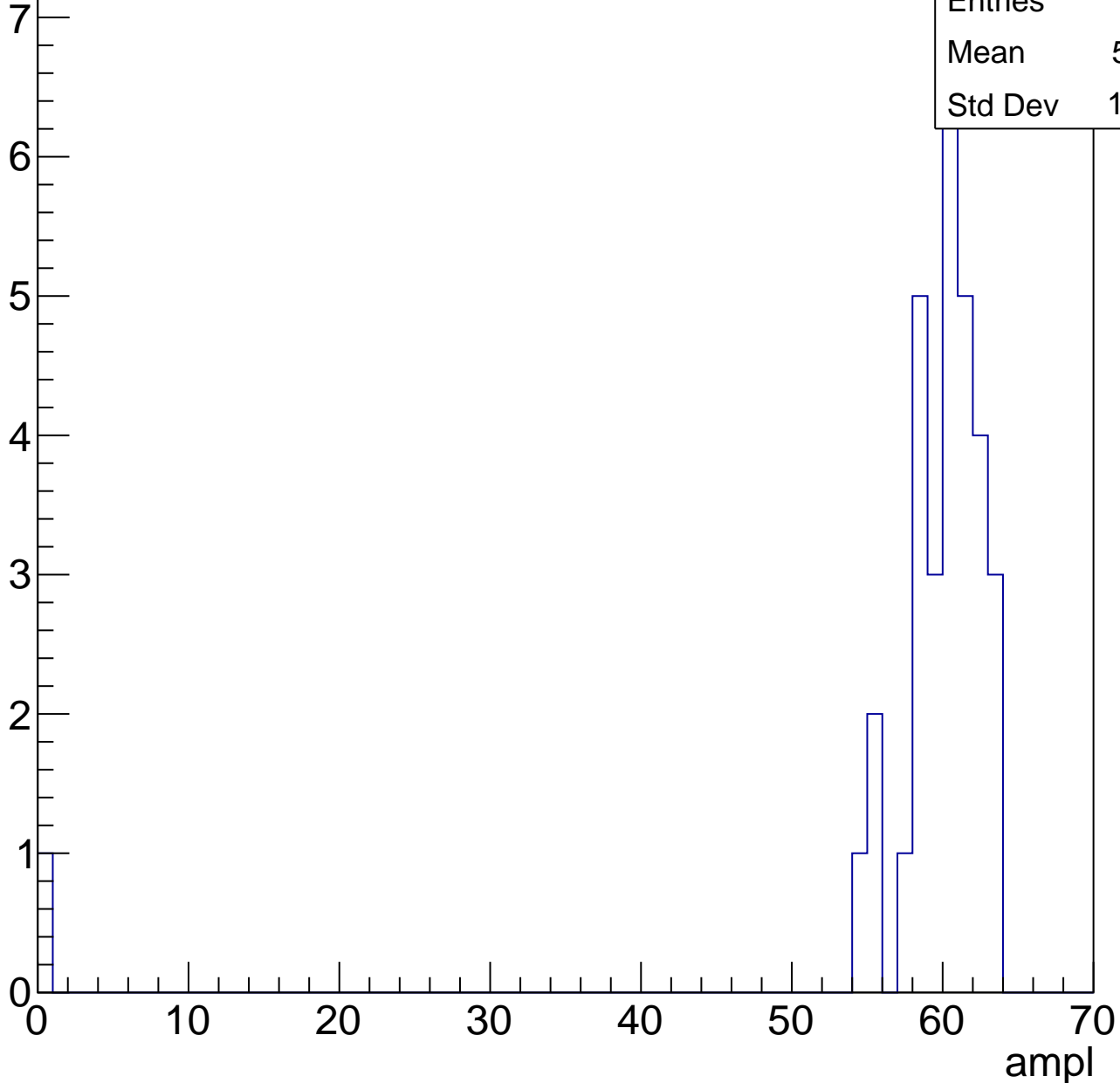


# B0L000S, U19-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	57.81
Std Dev	10.63

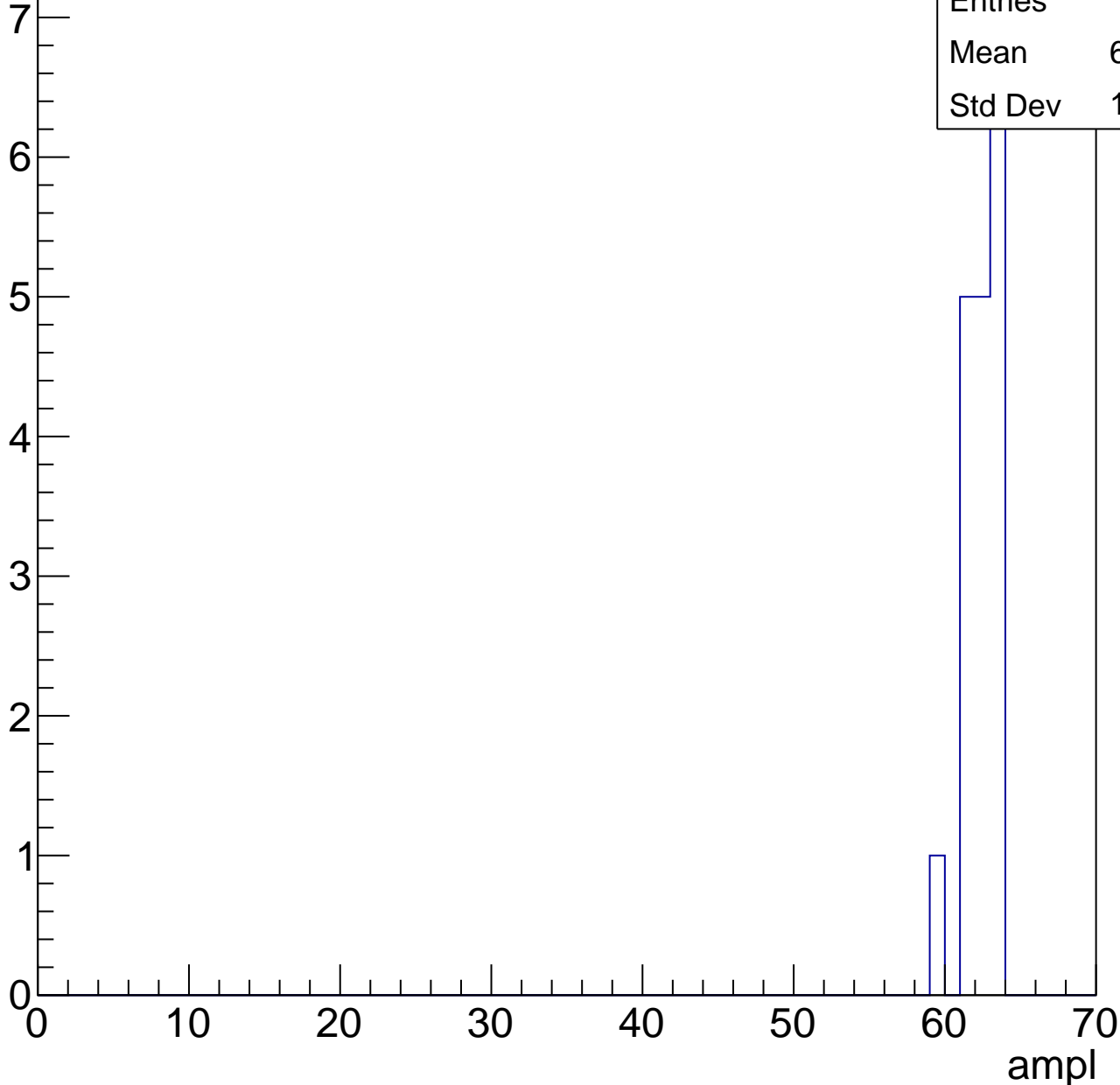


# B0L000S, U19-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	18
Mean	61.94
Std Dev	1.079





# B0L000S, U19-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	29.24
Std Dev	5.279

**Gaus mean : 29.5285**

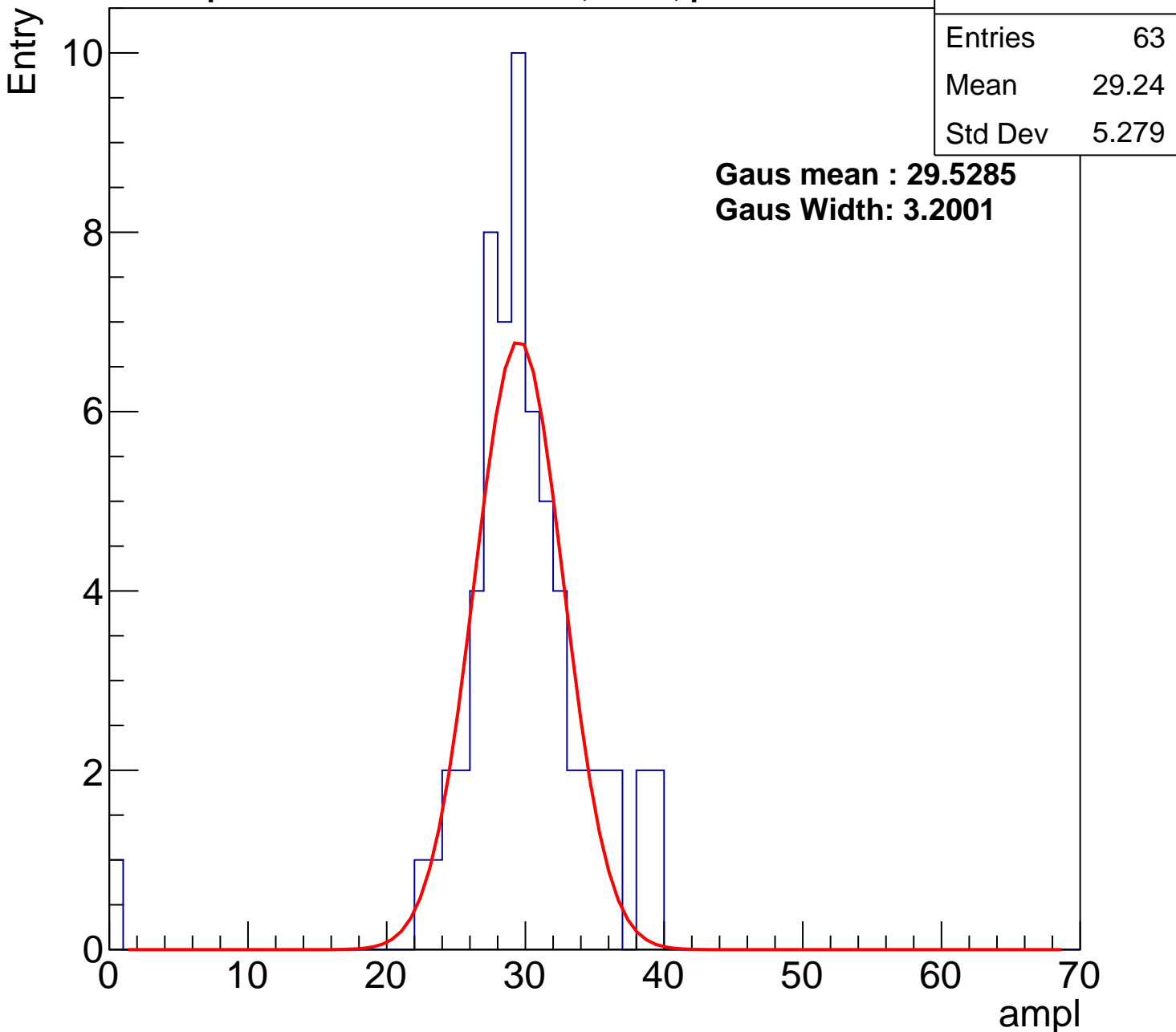
**Gaus Width: 3.2001**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch121, adc1

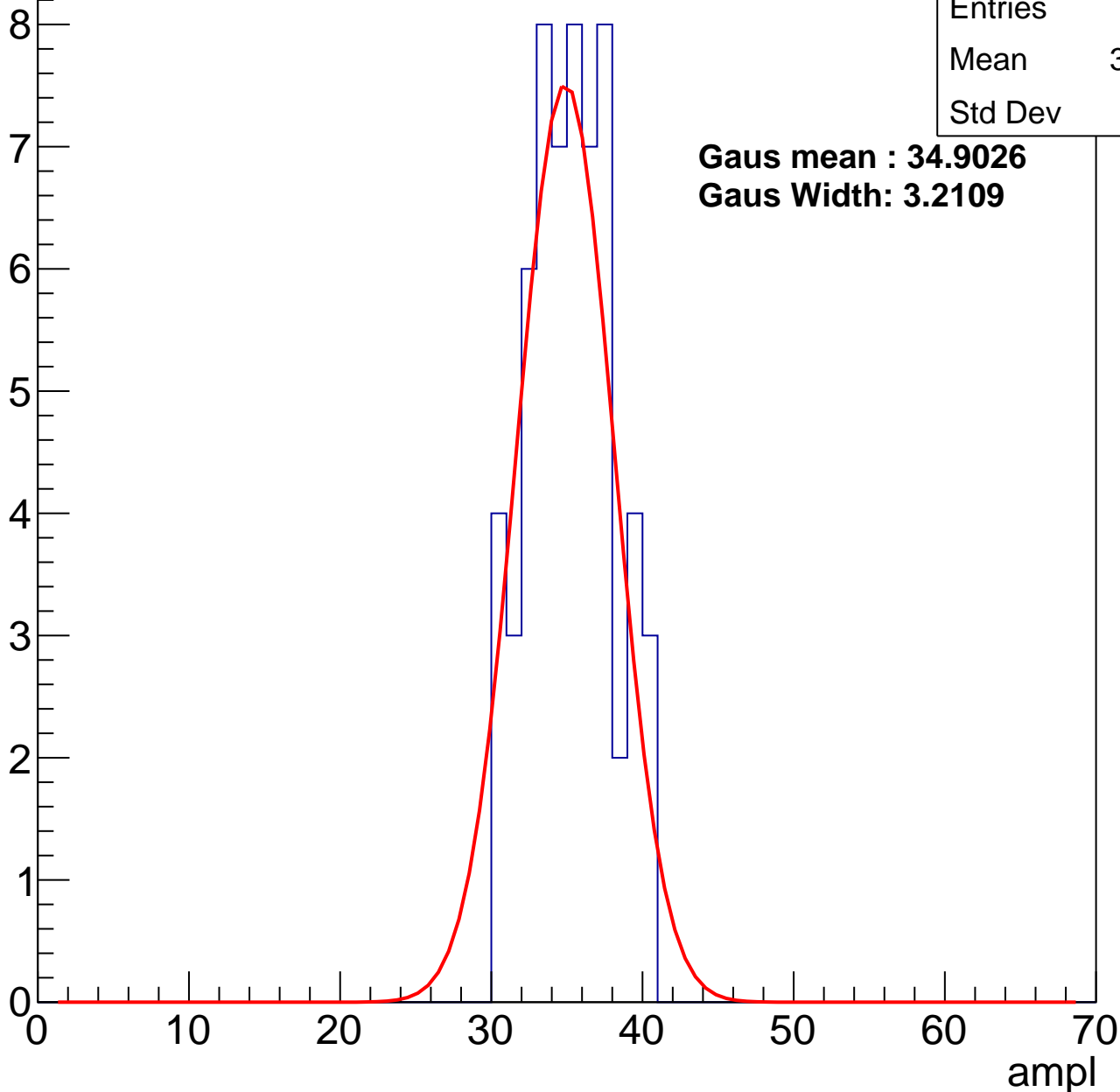
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	34.78
Std Dev	2.69

**Gaus mean : 34.9026**

**Gaus Width: 3.2109**



# B0L000S, U19-ch121, adc2

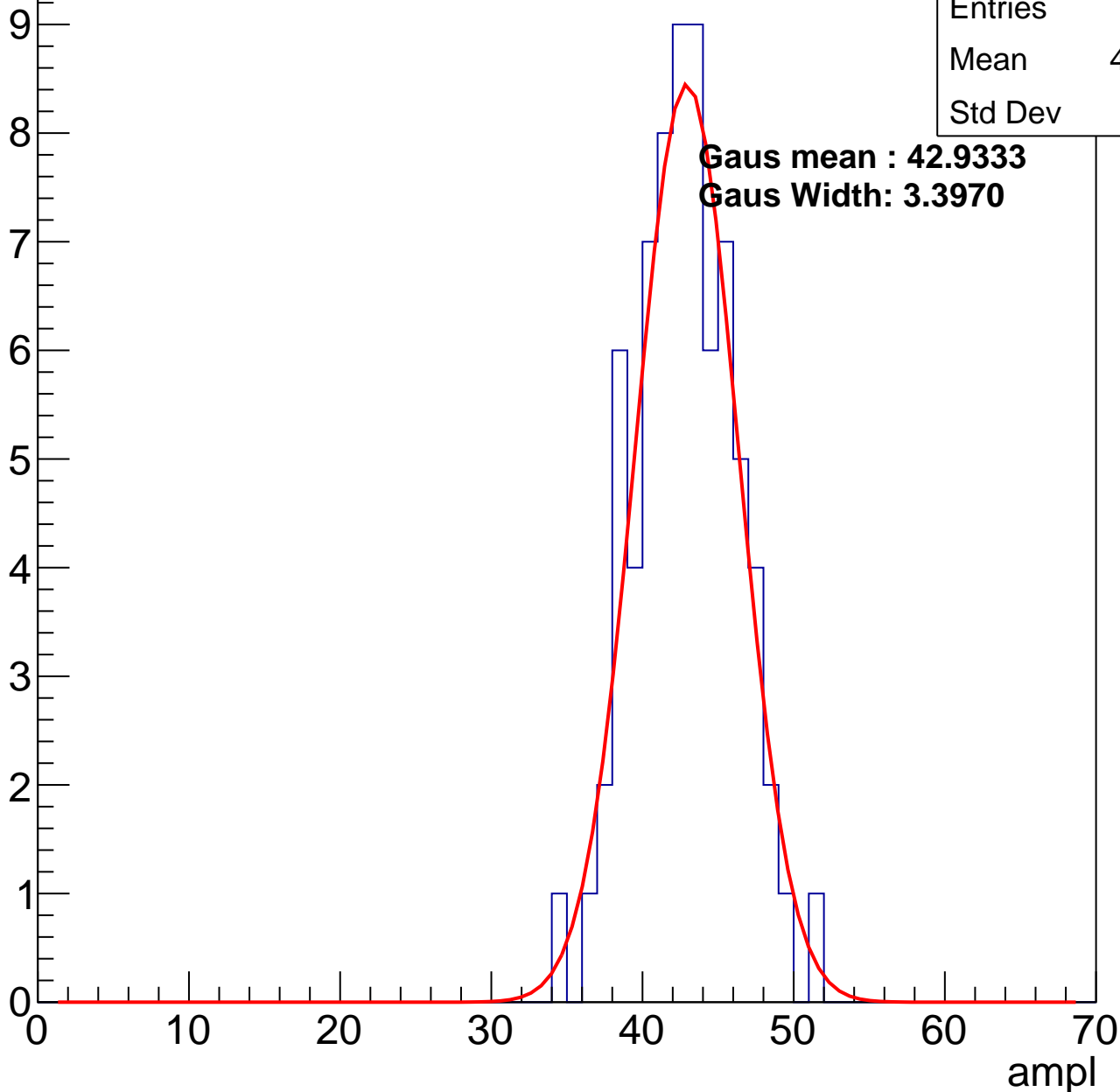
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	42.38
Std Dev	3.28

**Gaus mean : 42.9333**

**Gaus Width: 3.3970**

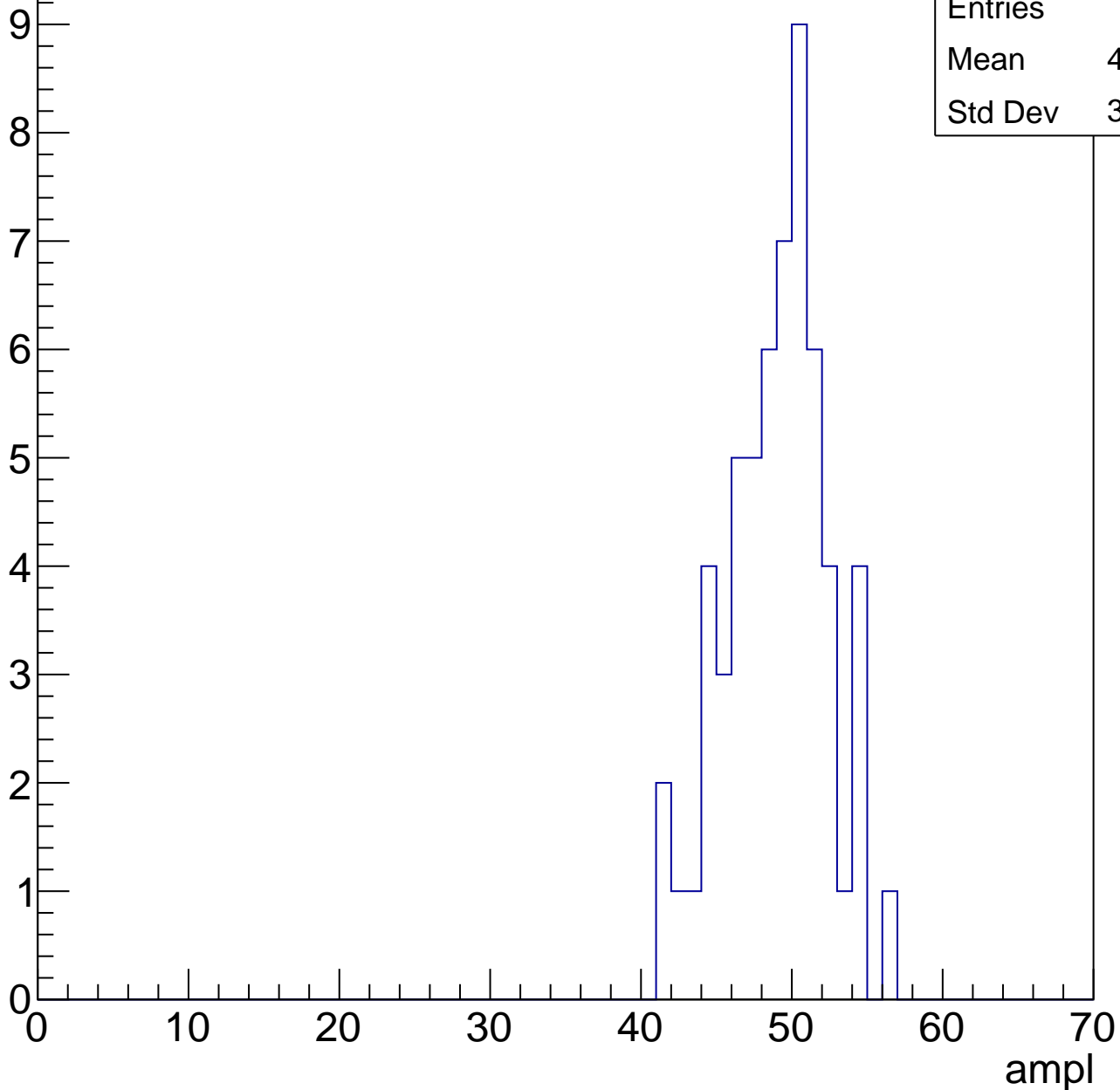


# B0L000S, U19-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

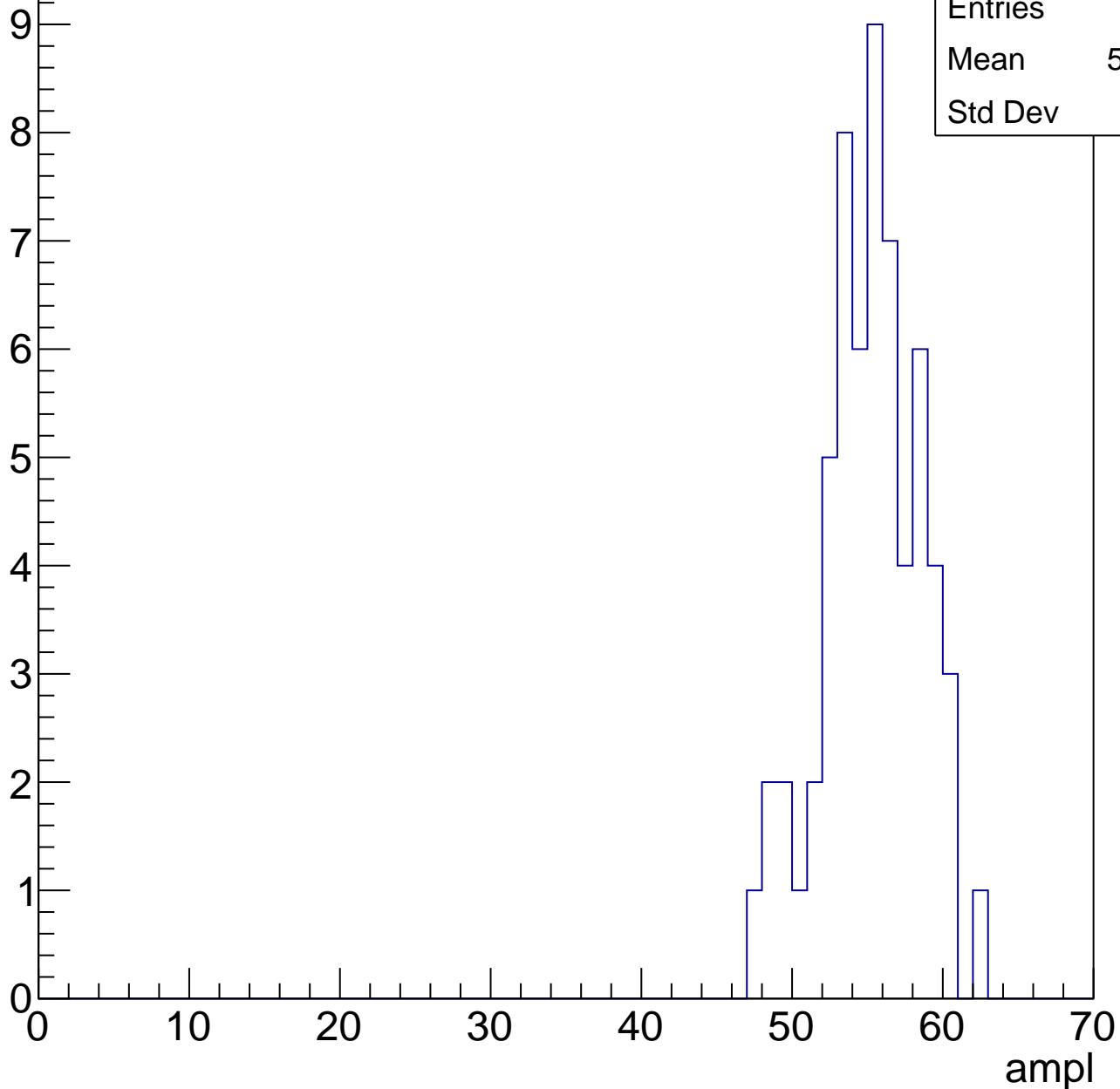
Entries	59
Mean	48.53
Std Dev	3.326



# B0L000S, U19-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

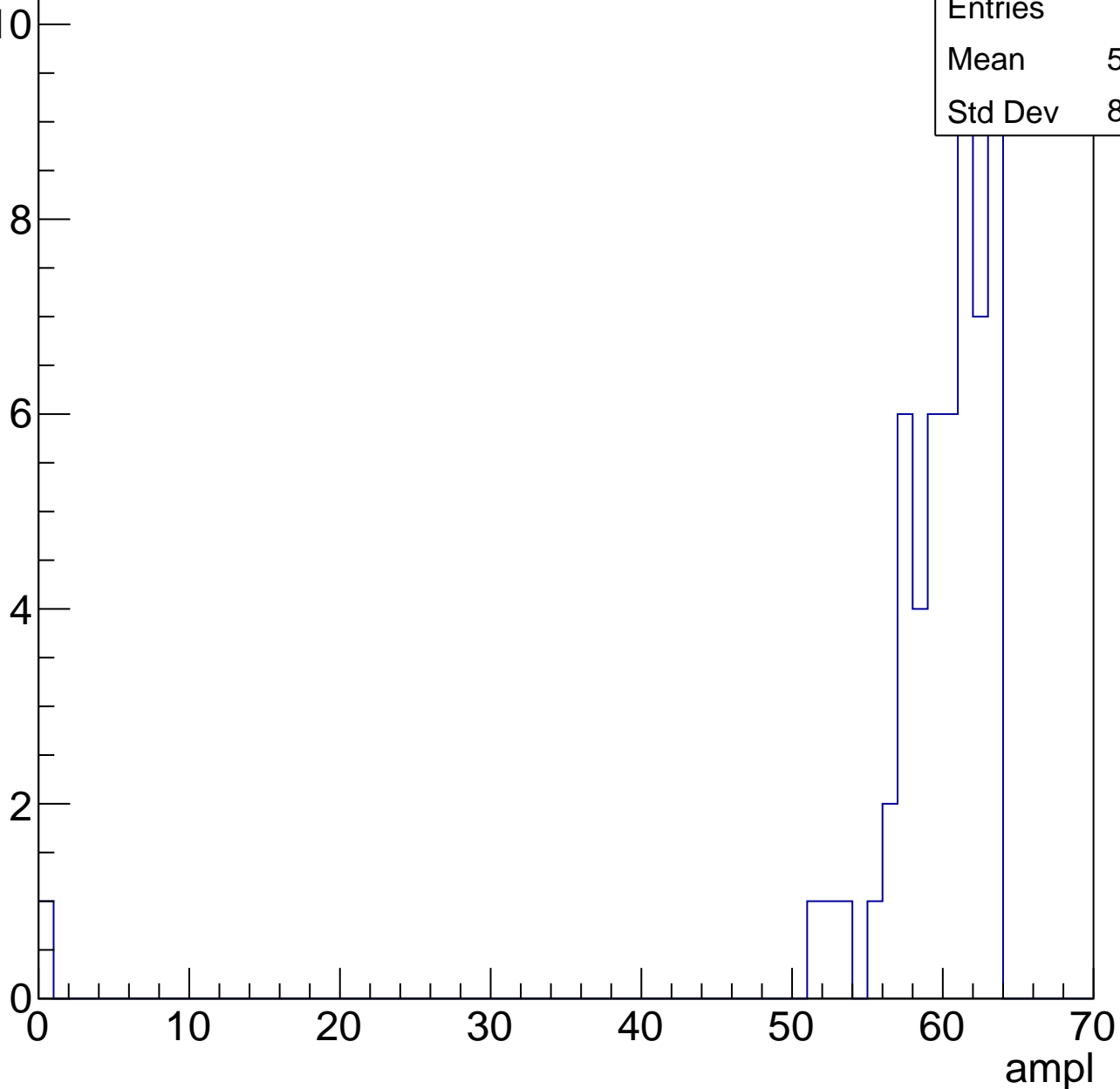


# B0L000S, U19-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.58
Std Dev	8.459



# B0L000S, U19-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

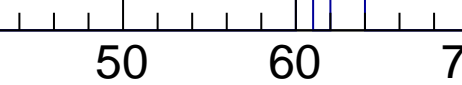
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl





# B0L000S, U19-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch122, adc0

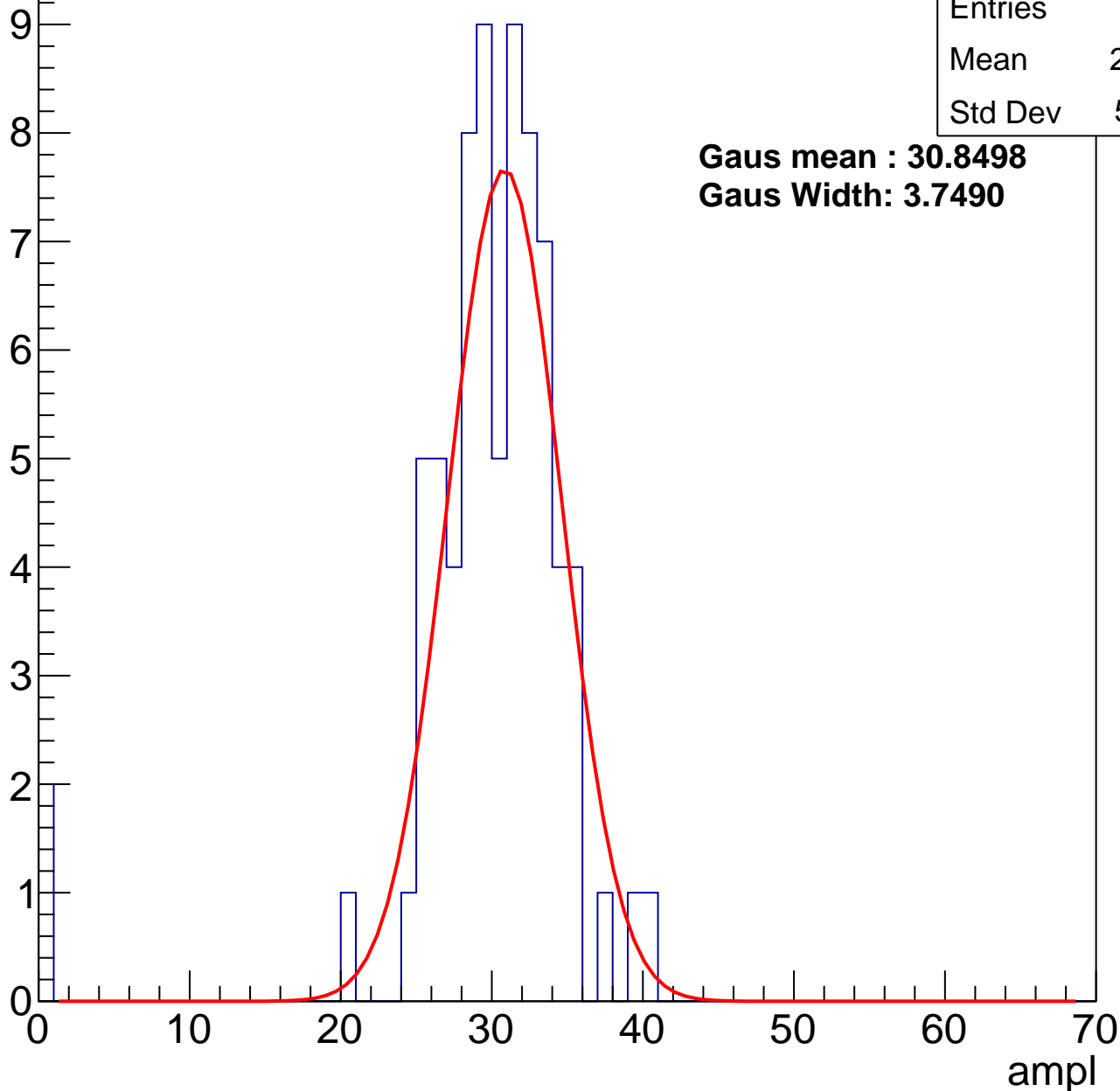
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.33
Std Dev	5.981

**Gaus mean : 30.8498**

**Gaus Width: 3.7490**



# B0L000S, U19-ch122, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	37.33
Std Dev	3.611

**Gaus mean : 37.8647**

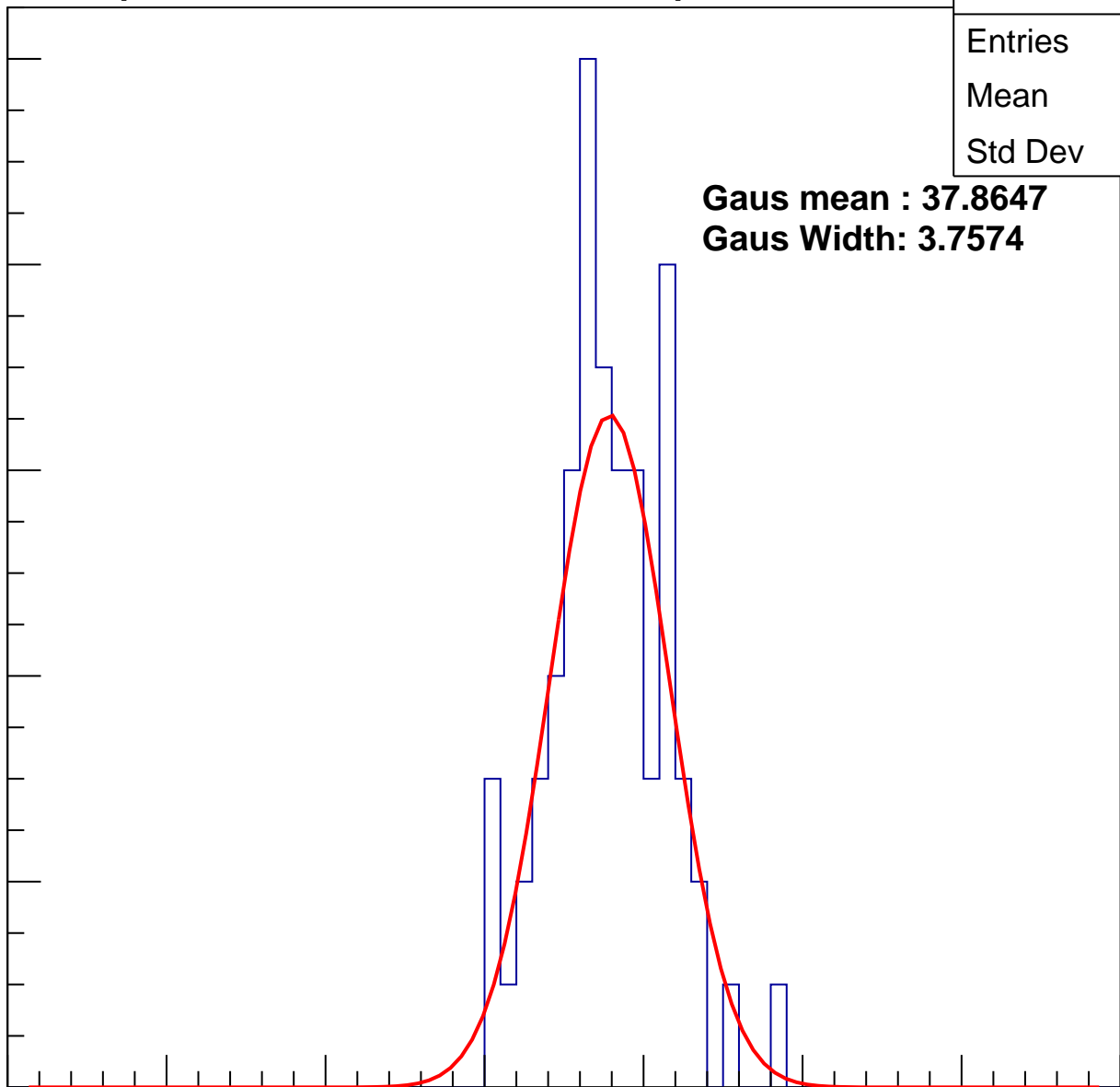
**Gaus Width: 3.7574**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch122, adc2

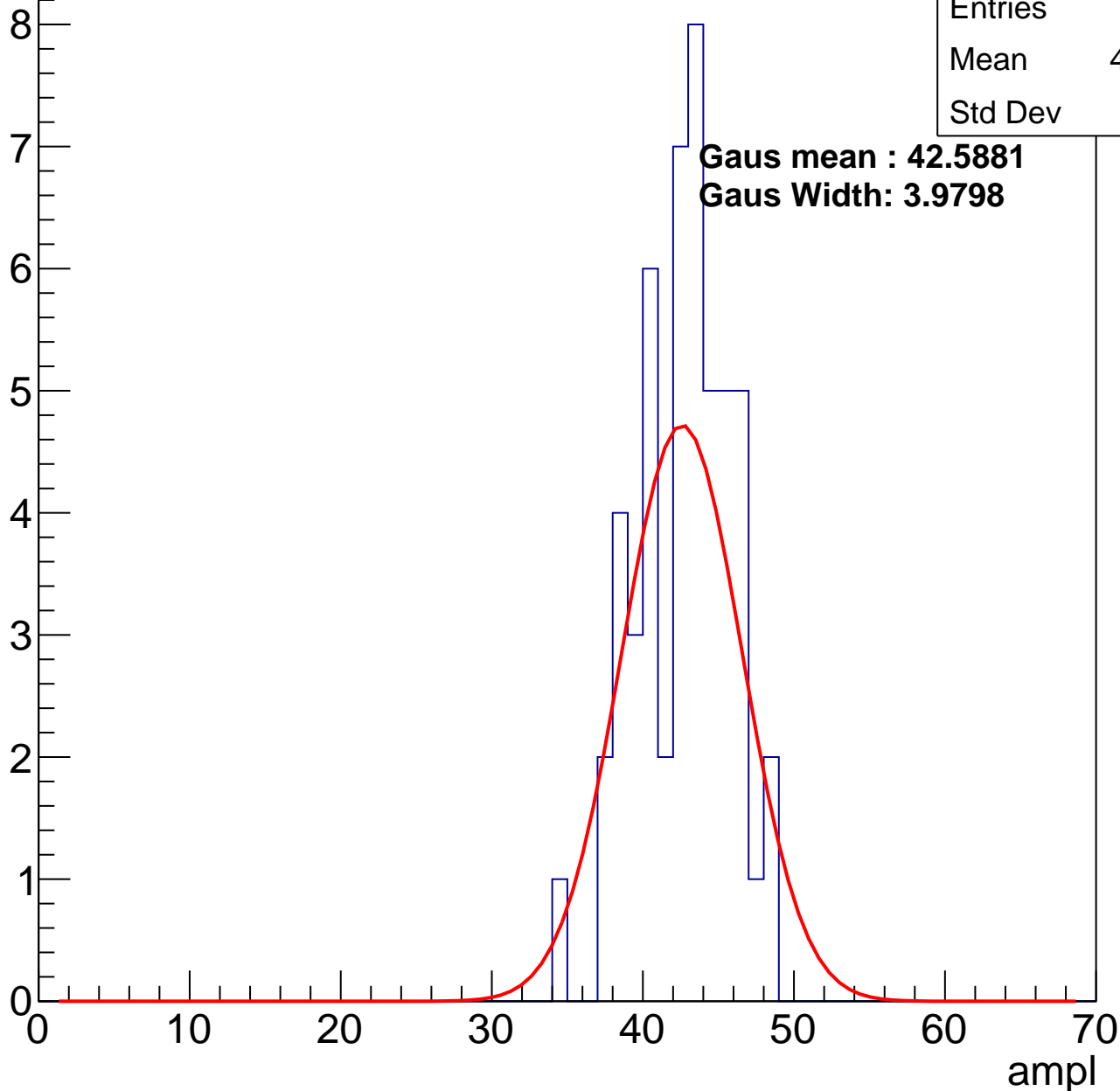
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	42.25
Std Dev	3.06

**Gaus mean : 42.5881**

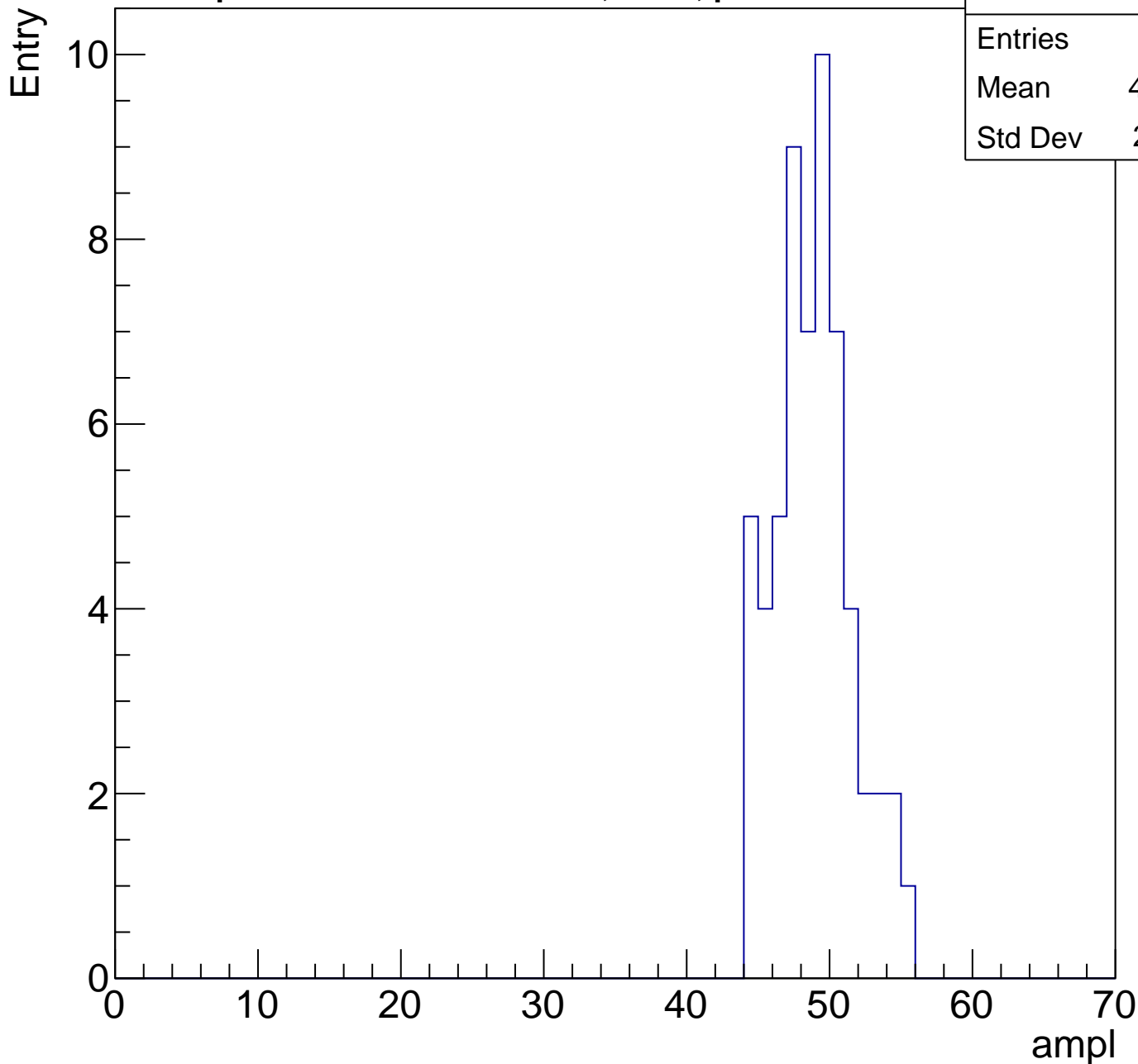
**Gaus Width: 3.9798**



# B0L000S, U19-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	58
Mean	48.38
Std Dev	2.671

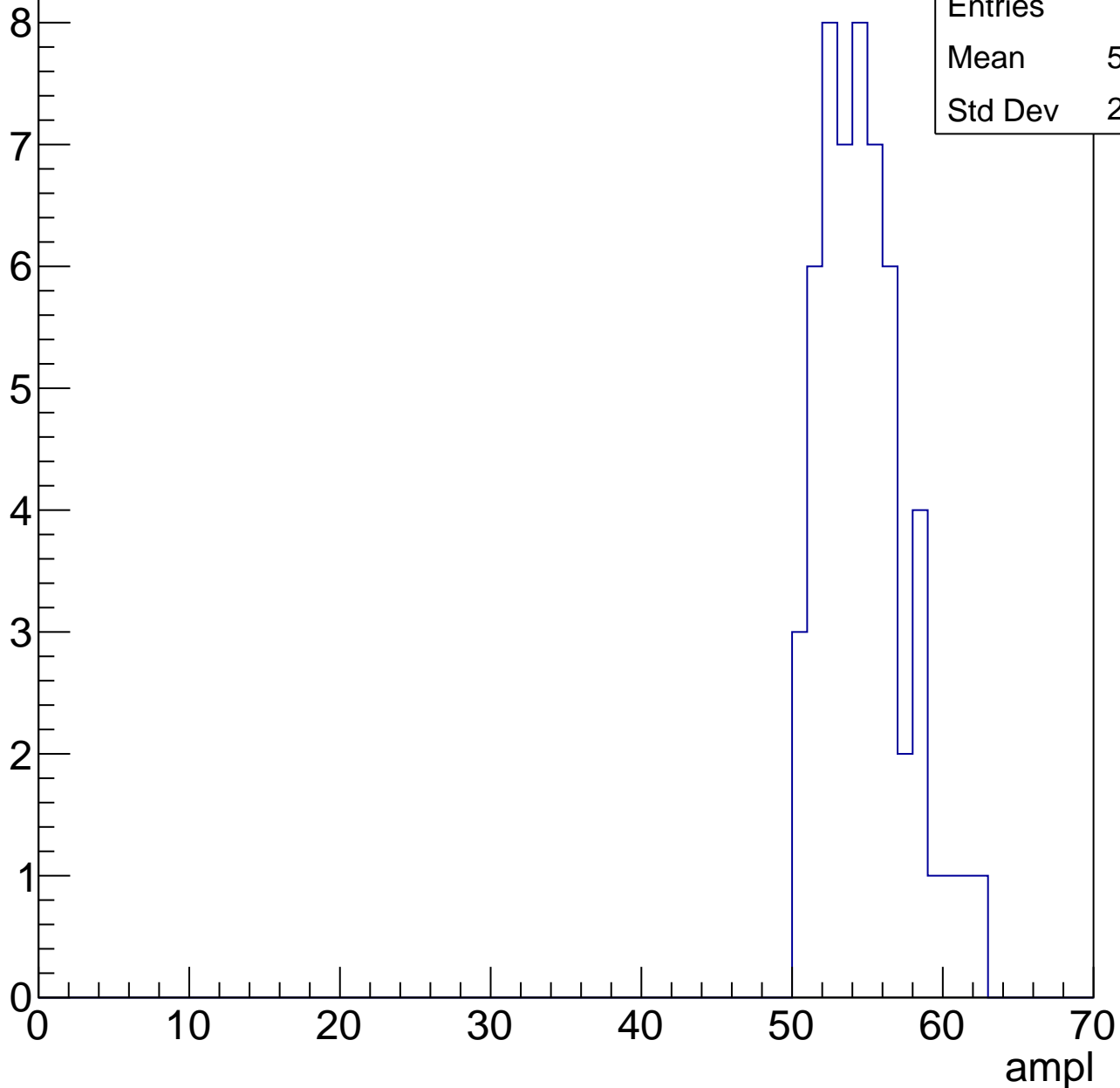


# B0L000S, U19-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	54.25
Std Dev	2.778

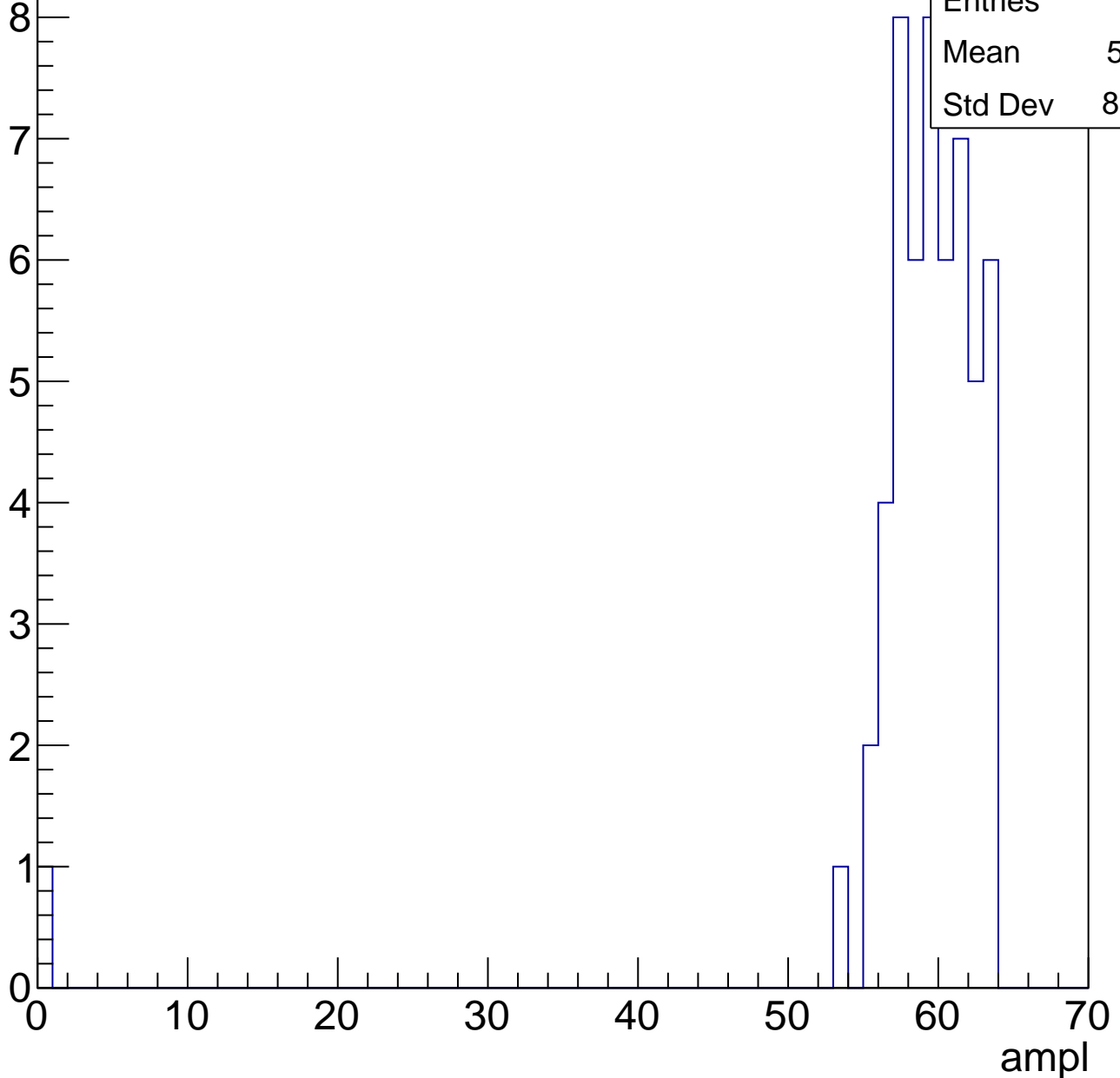


# B0L000S, U19-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

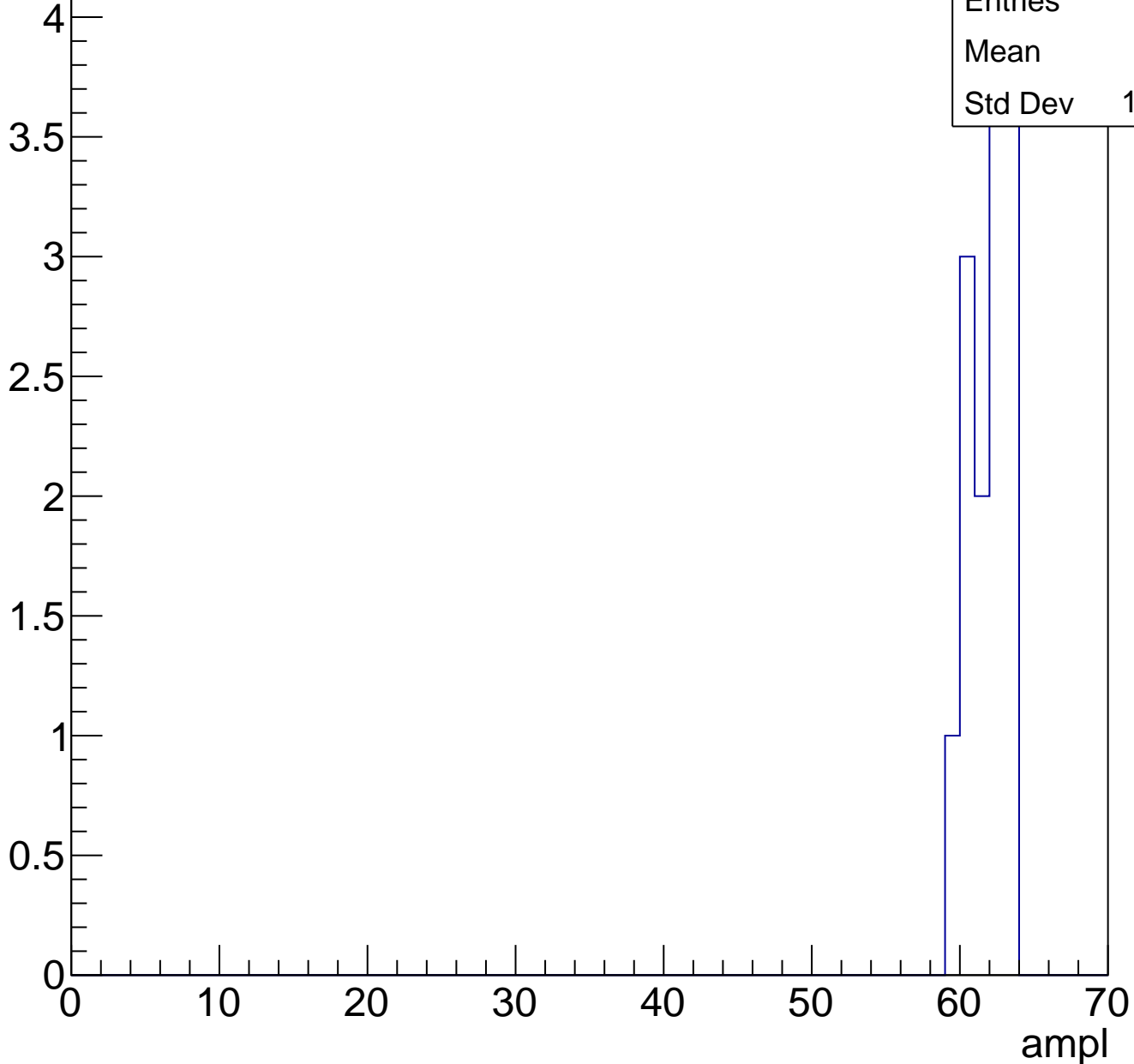
Entries	54
Mean	58.11
Std Dev	8.339



# B0L000S, U19-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	6
Std Dev	8.485

# B0L000S, U19-ch123, adc0

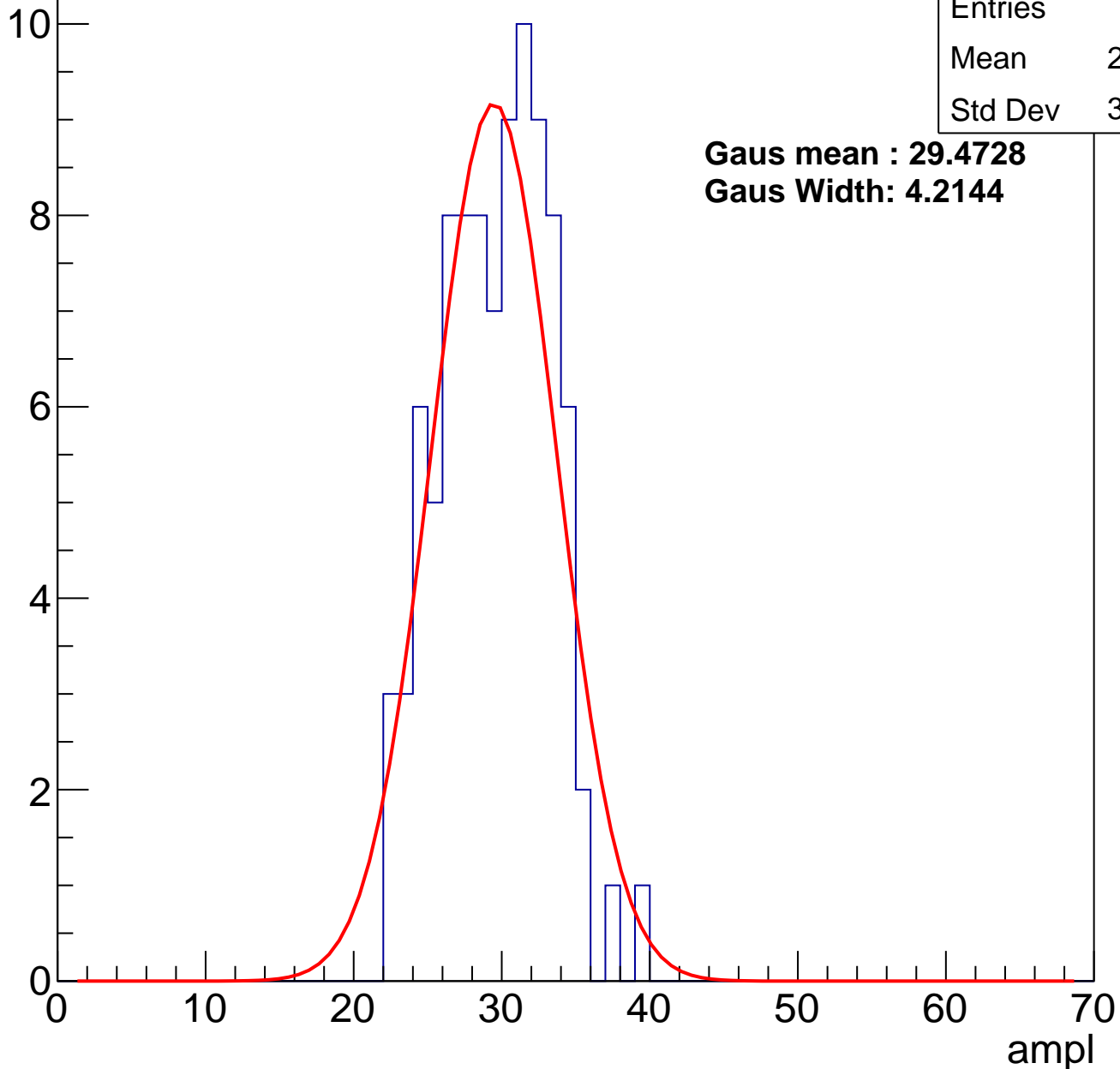
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	94
Mean	29.12
Std Dev	3.632

**Gaus mean : 29.4728**

**Gaus Width: 4.2144**

Entry



# B0L000S, U19-ch123, adc1

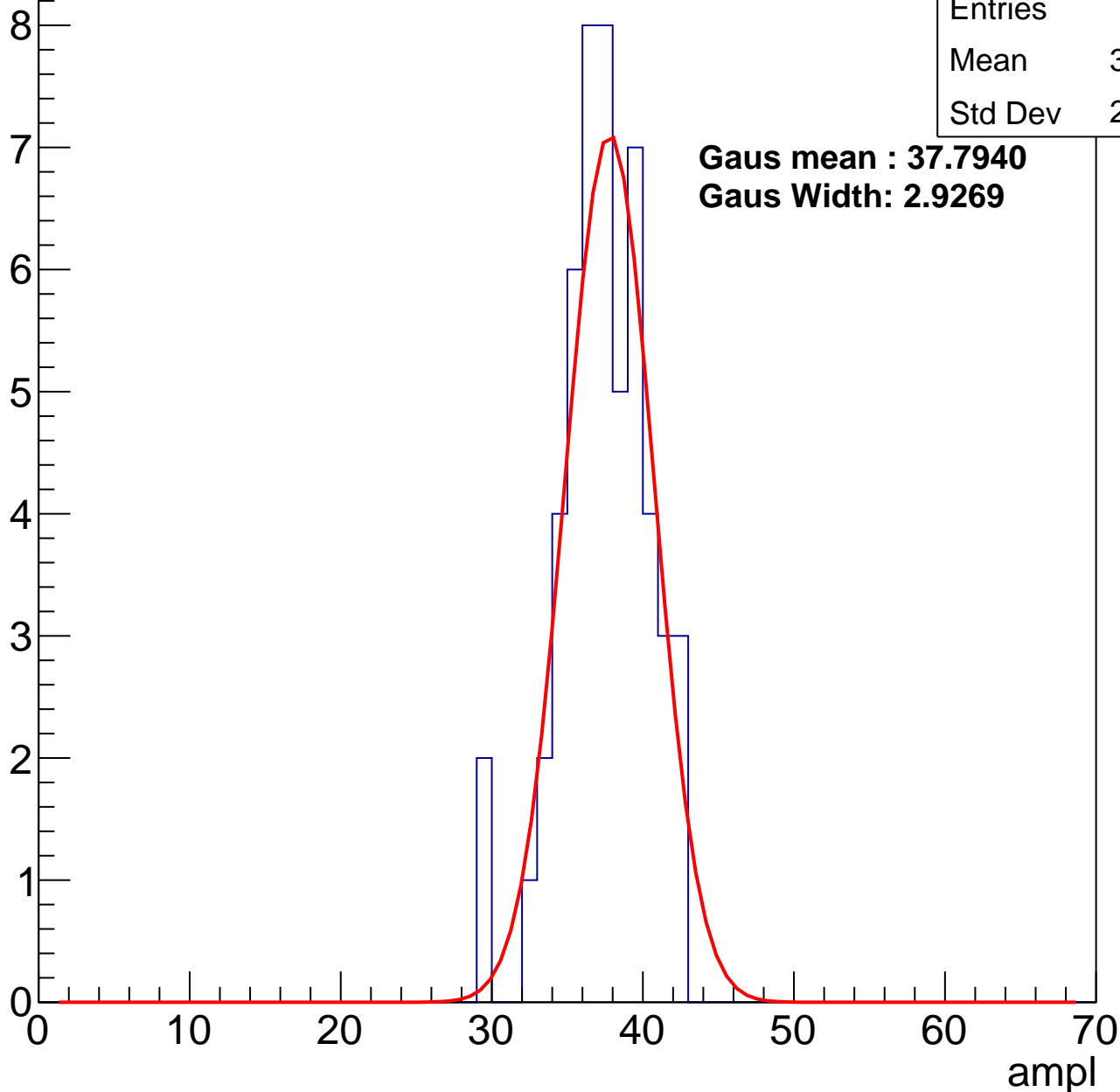
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.94
Std Dev	2.897

**Gaus mean : 37.7940**

**Gaus Width: 2.9269**



# B0L000S, U19-ch123, adc2

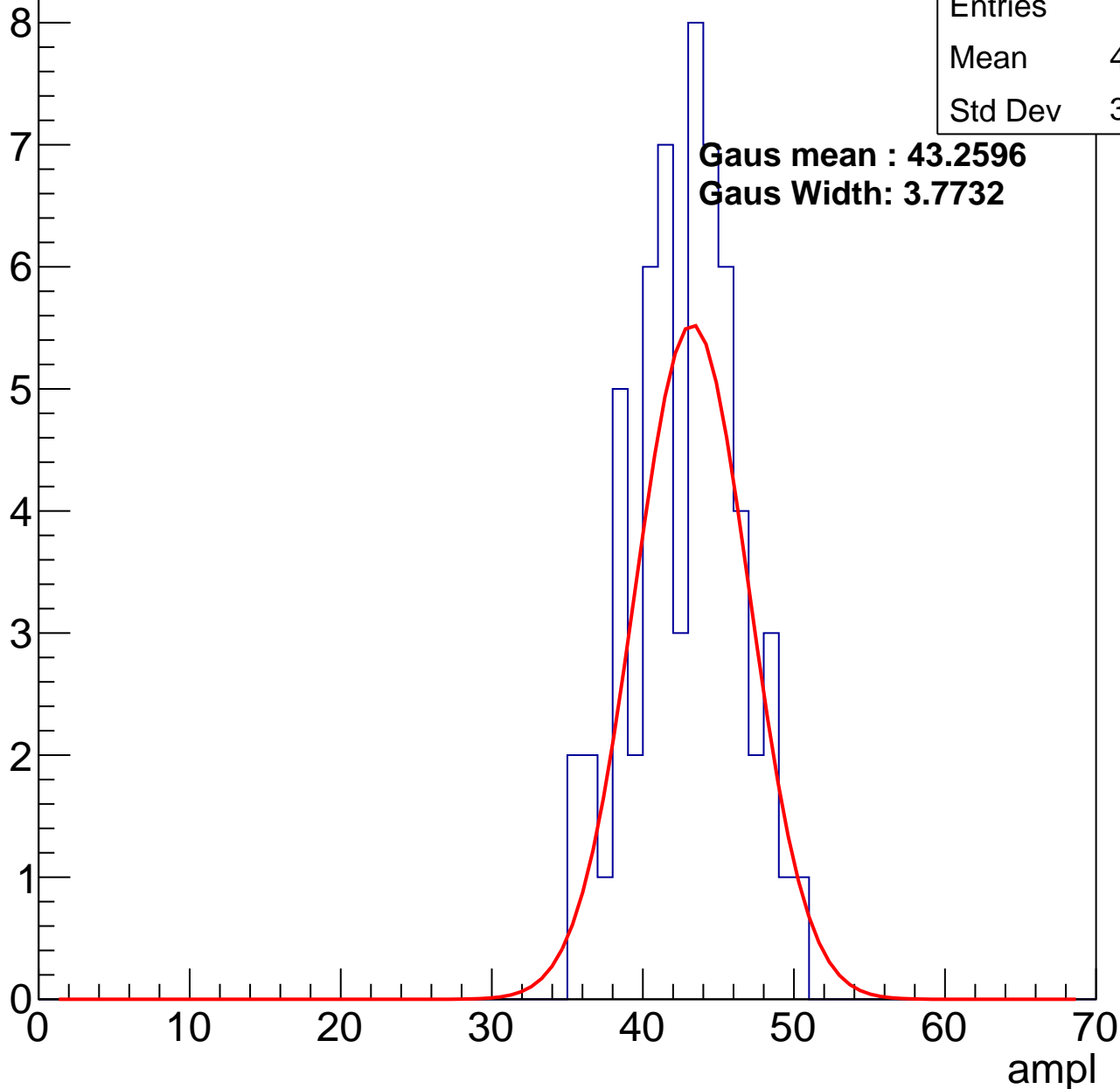
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.38
Std Dev	3.512

**Gaus mean : 43.2596**

**Gaus Width: 3.7732**

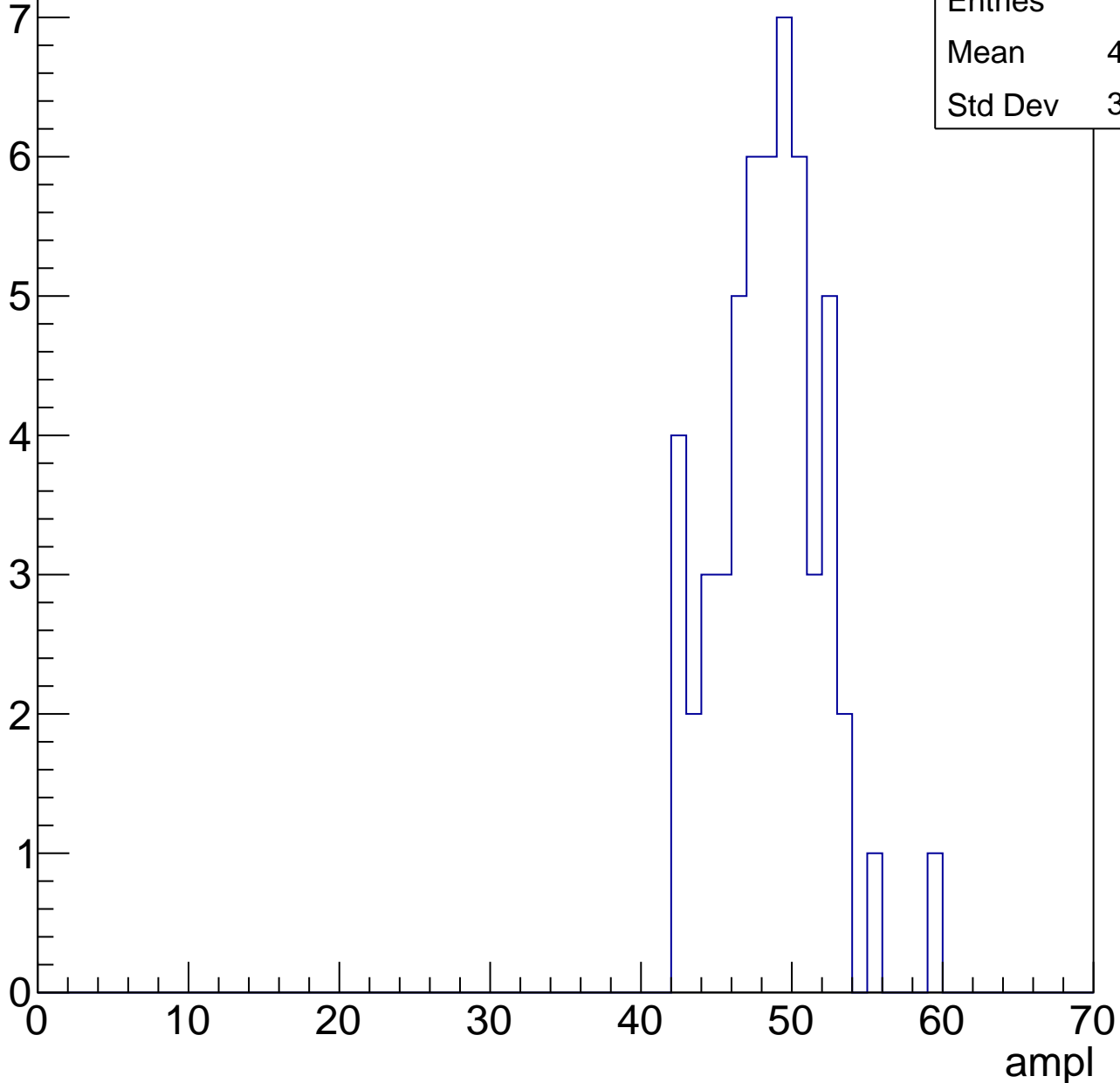


# B0L000S, U19-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	48.09
Std Dev	3.476

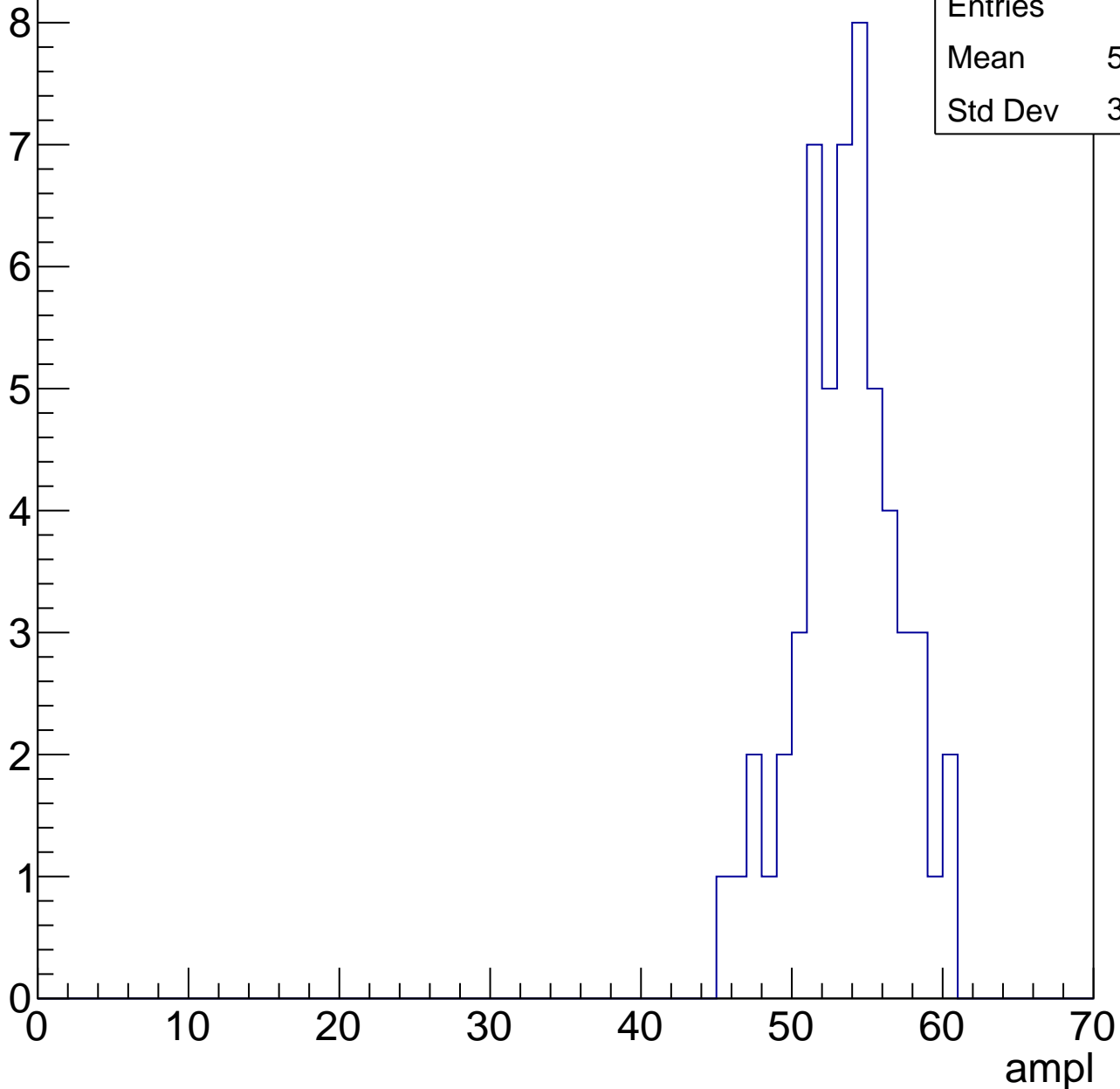


# B0L000S, U19-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	53.16
Std Dev	3.362

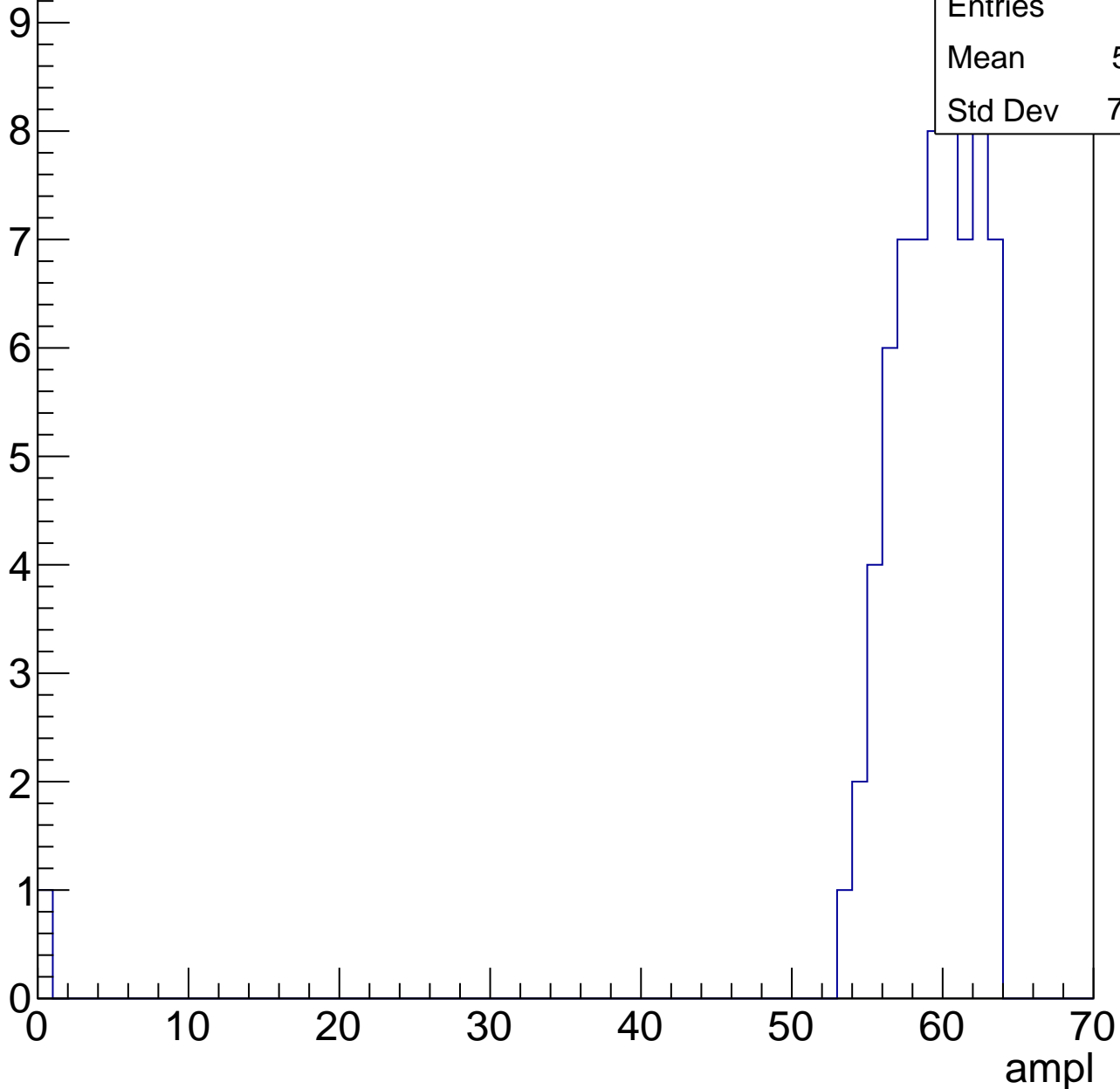


# B0L000S, U19-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

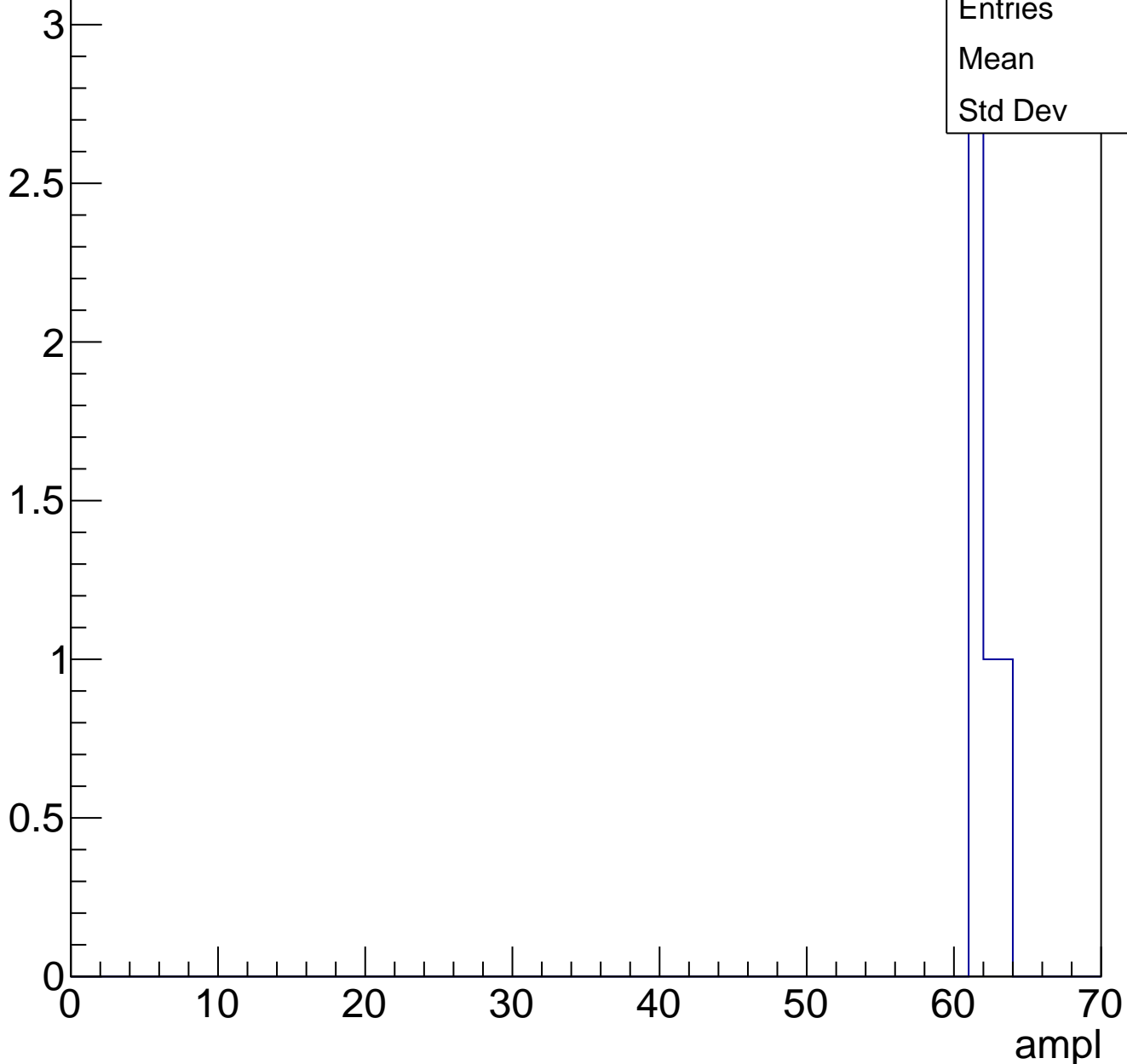
Entries	67
Mean	58.21
Std Dev	7.636



# B0L000S, U19-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	61.6
Std Dev	0.8



# B0L000S, U19-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U19-ch124, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	60
Mean	28.67
Std Dev	4.857

**Gaus mean : 29.5898**

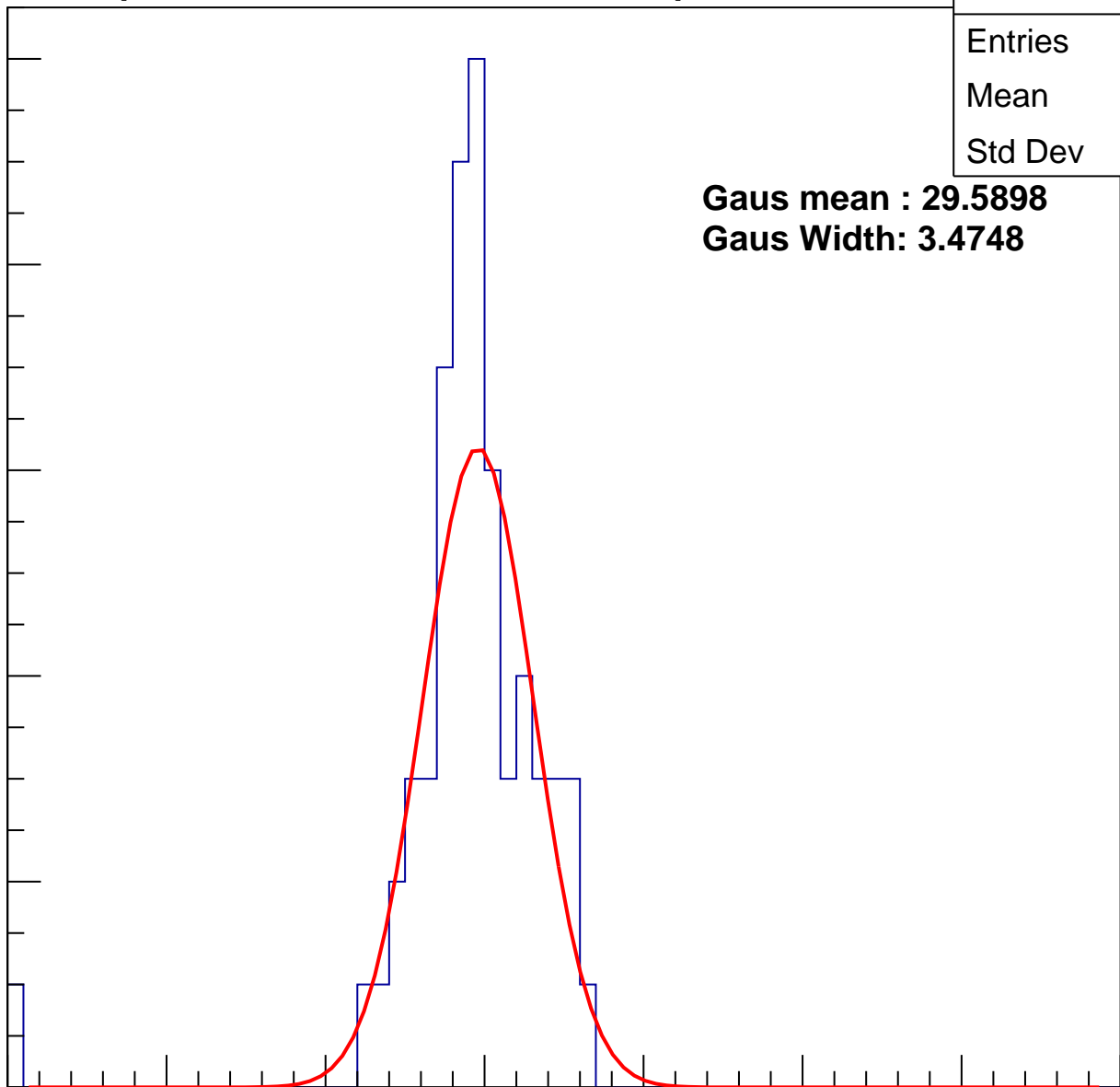
**Gaus Width: 3.4748**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch124, adc1

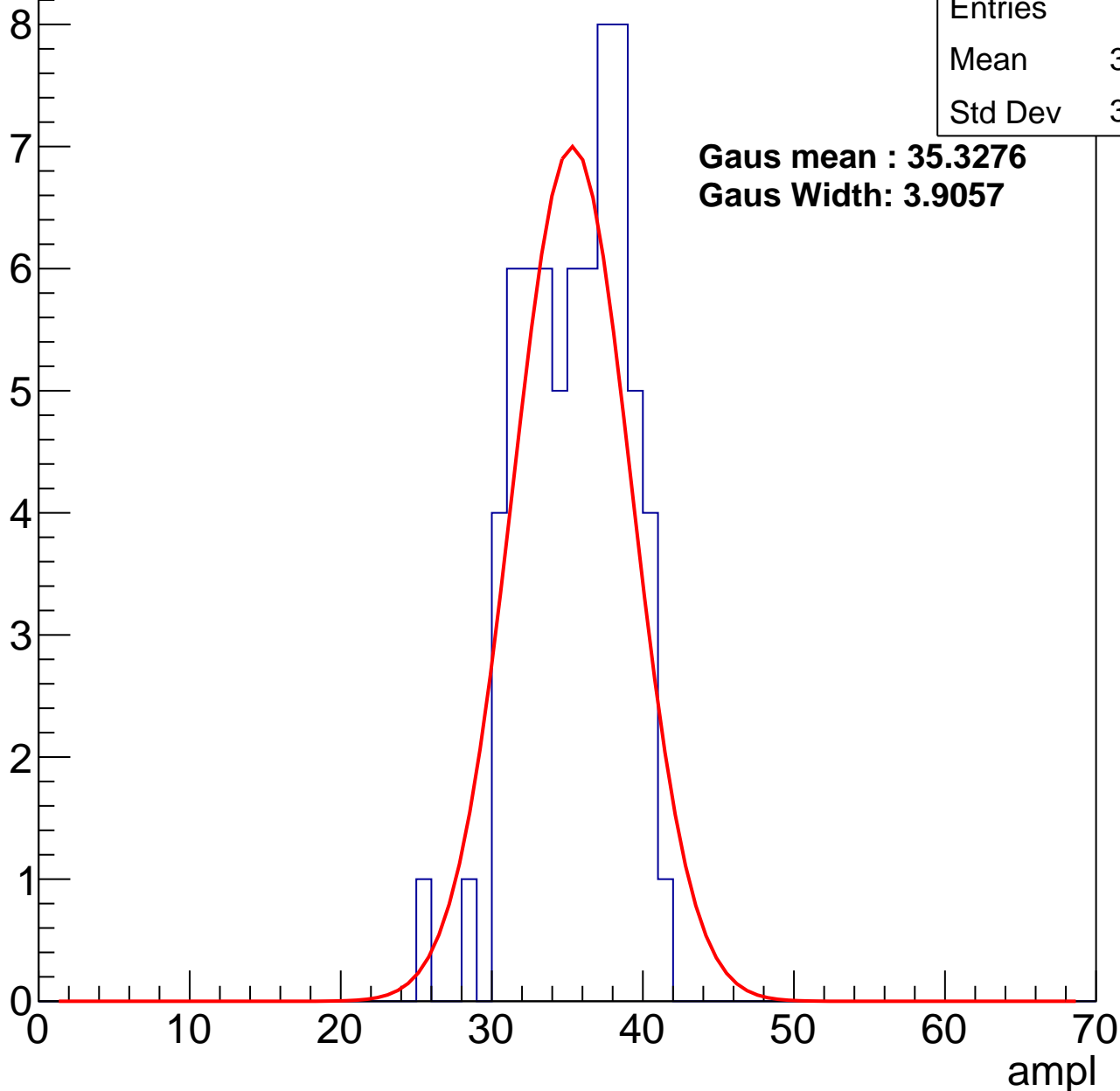
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	34.94
Std Dev	3.354

**Gaus mean : 35.3276**

**Gaus Width: 3.9057**



# B0L000S, U19-ch124, adc2

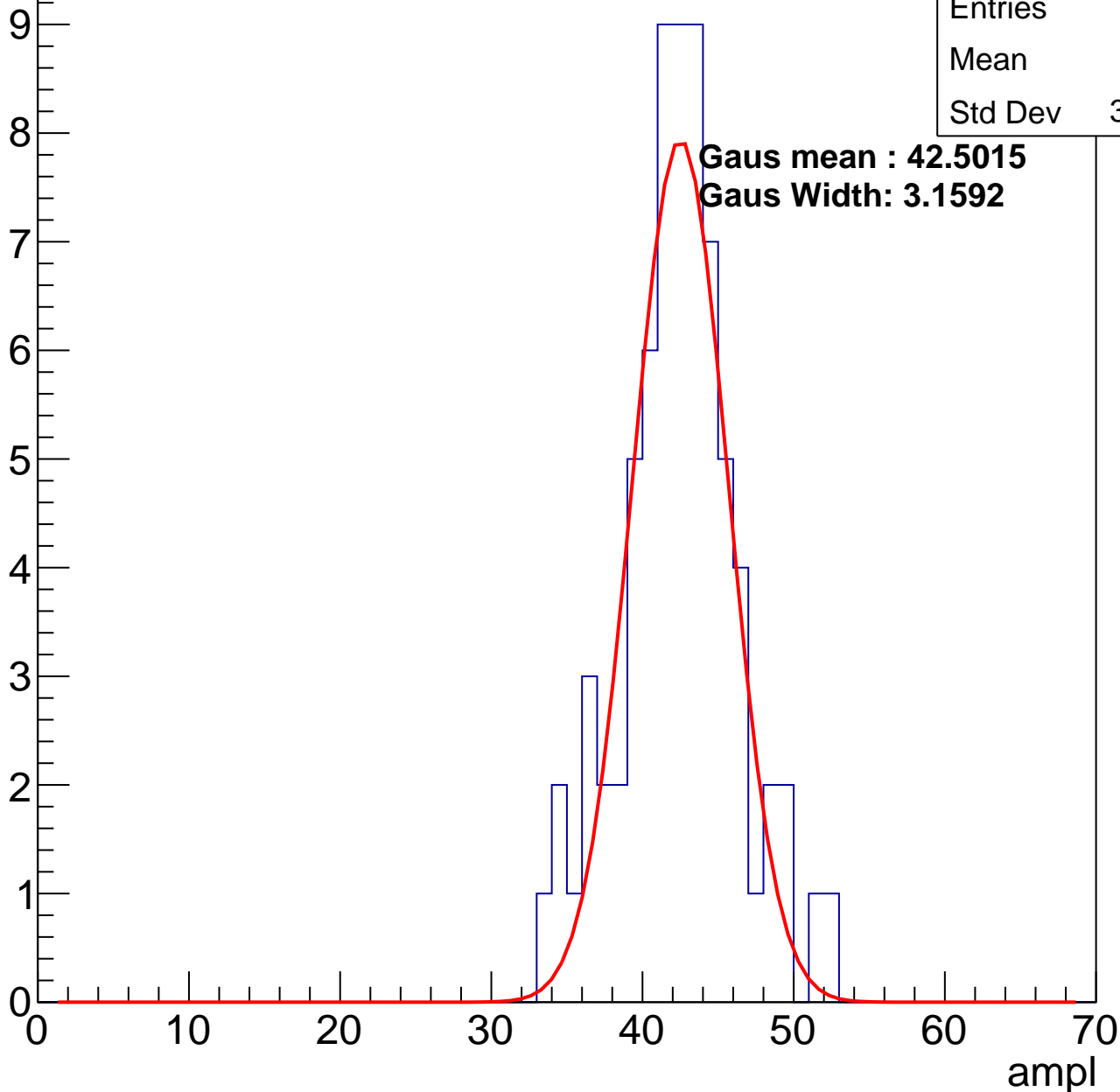
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	42
Std Dev	3.815

**Gaus mean : 42.5015**

**Gaus Width: 3.1592**

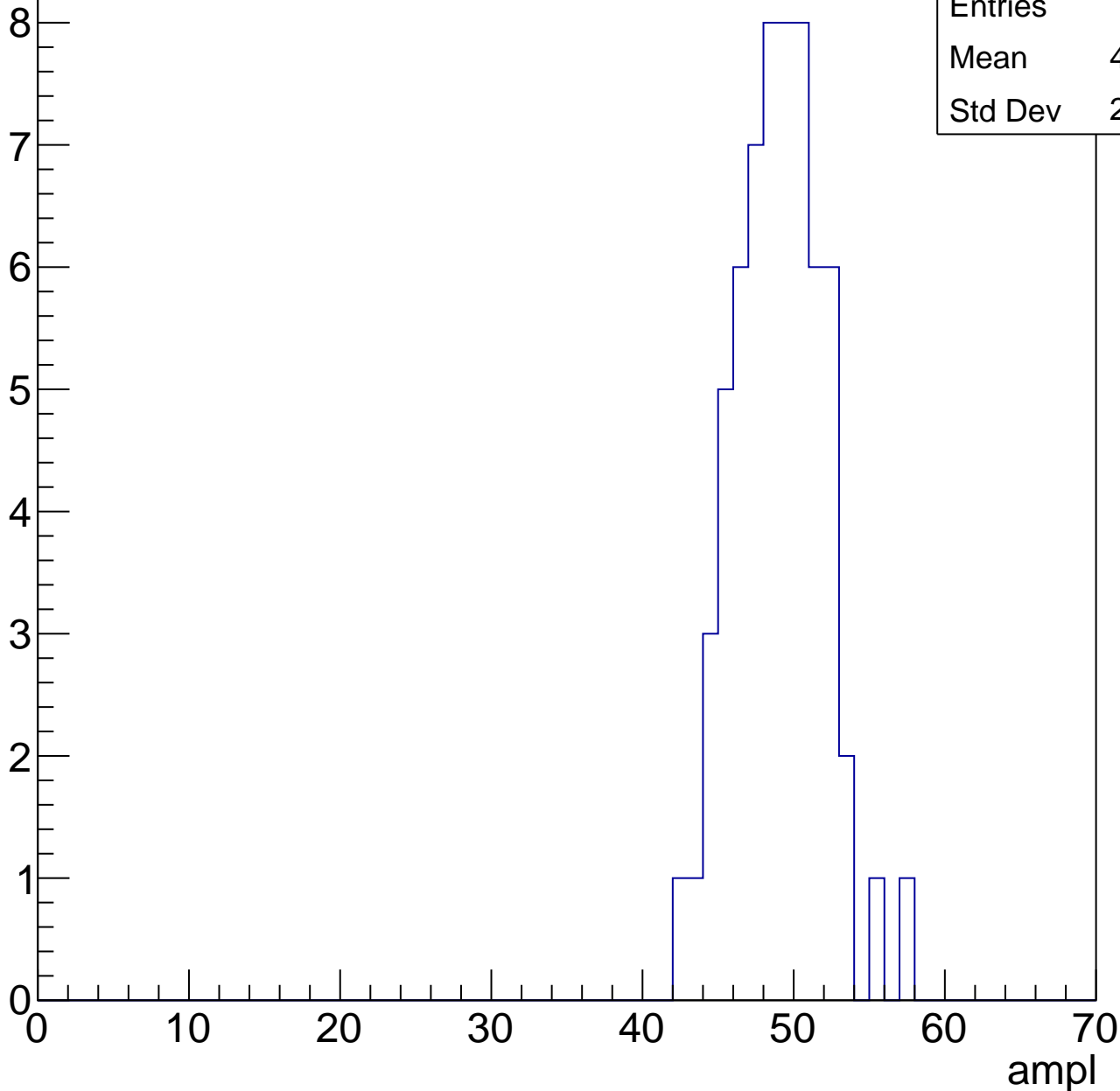


# B0L000S, U19-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	48.56
Std Dev	2.916

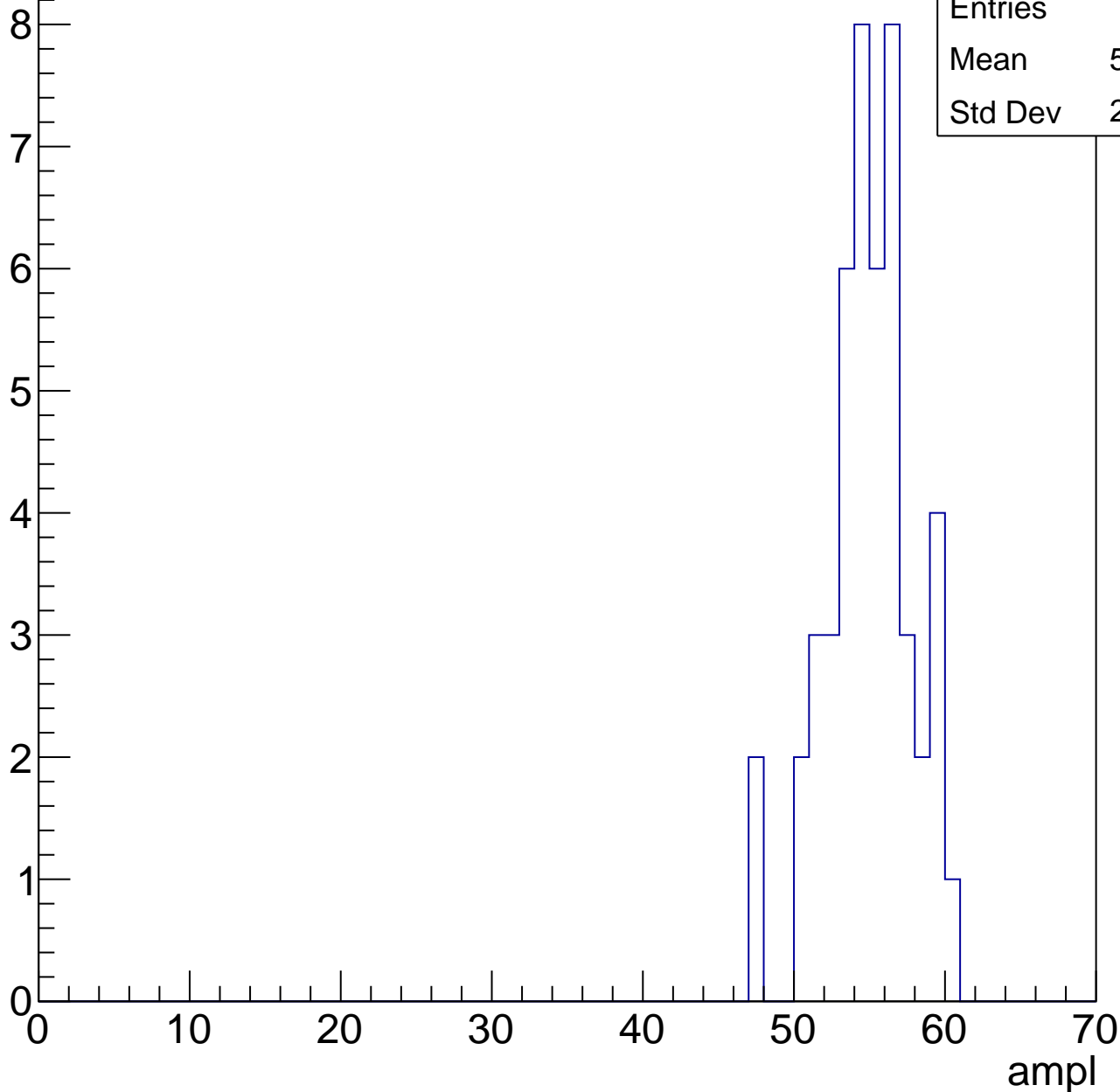


# B0L000S, U19-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	54.46
Std Dev	2.879

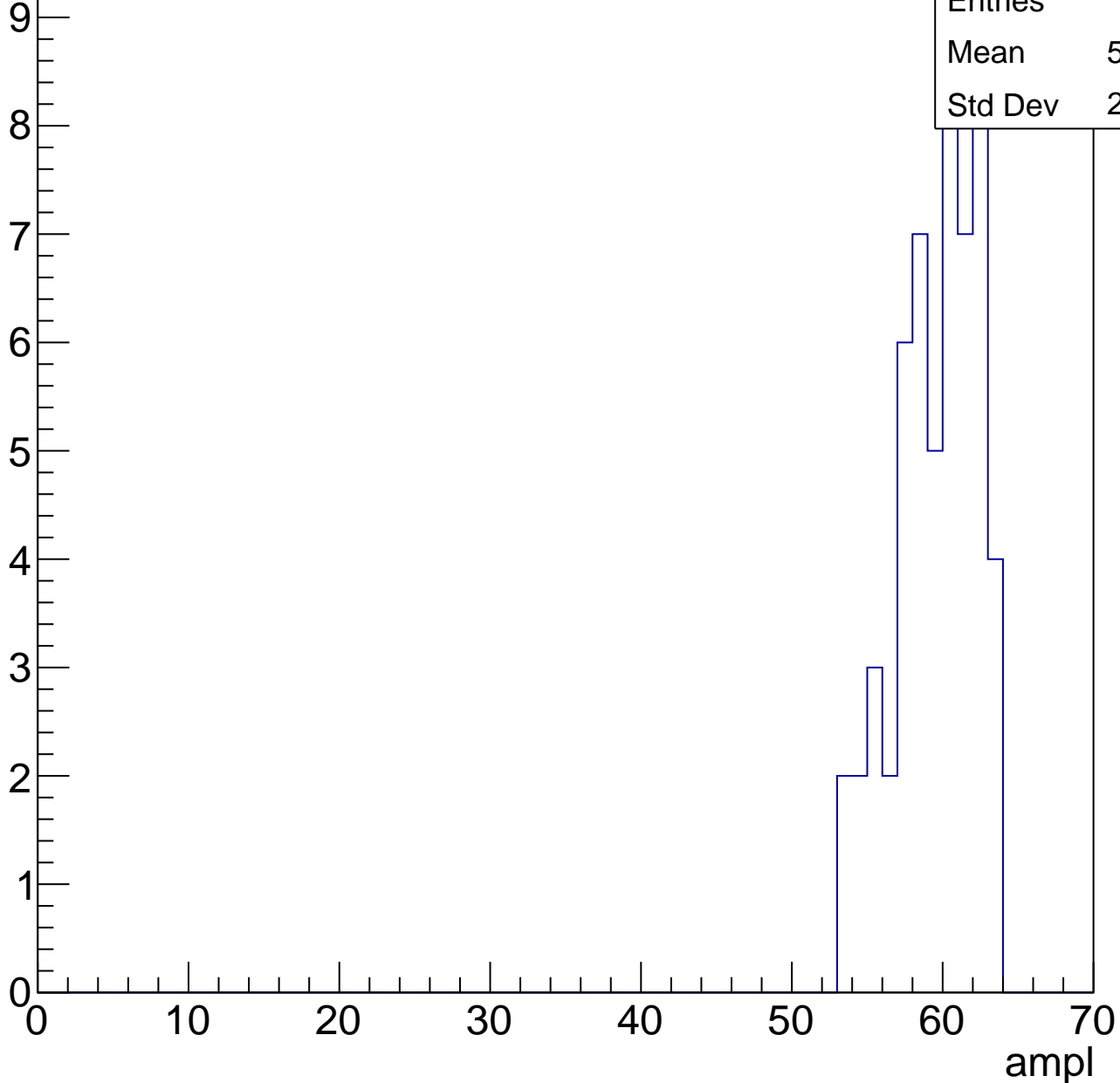


# B0L000S, U19-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	59.07
Std Dev	2.669

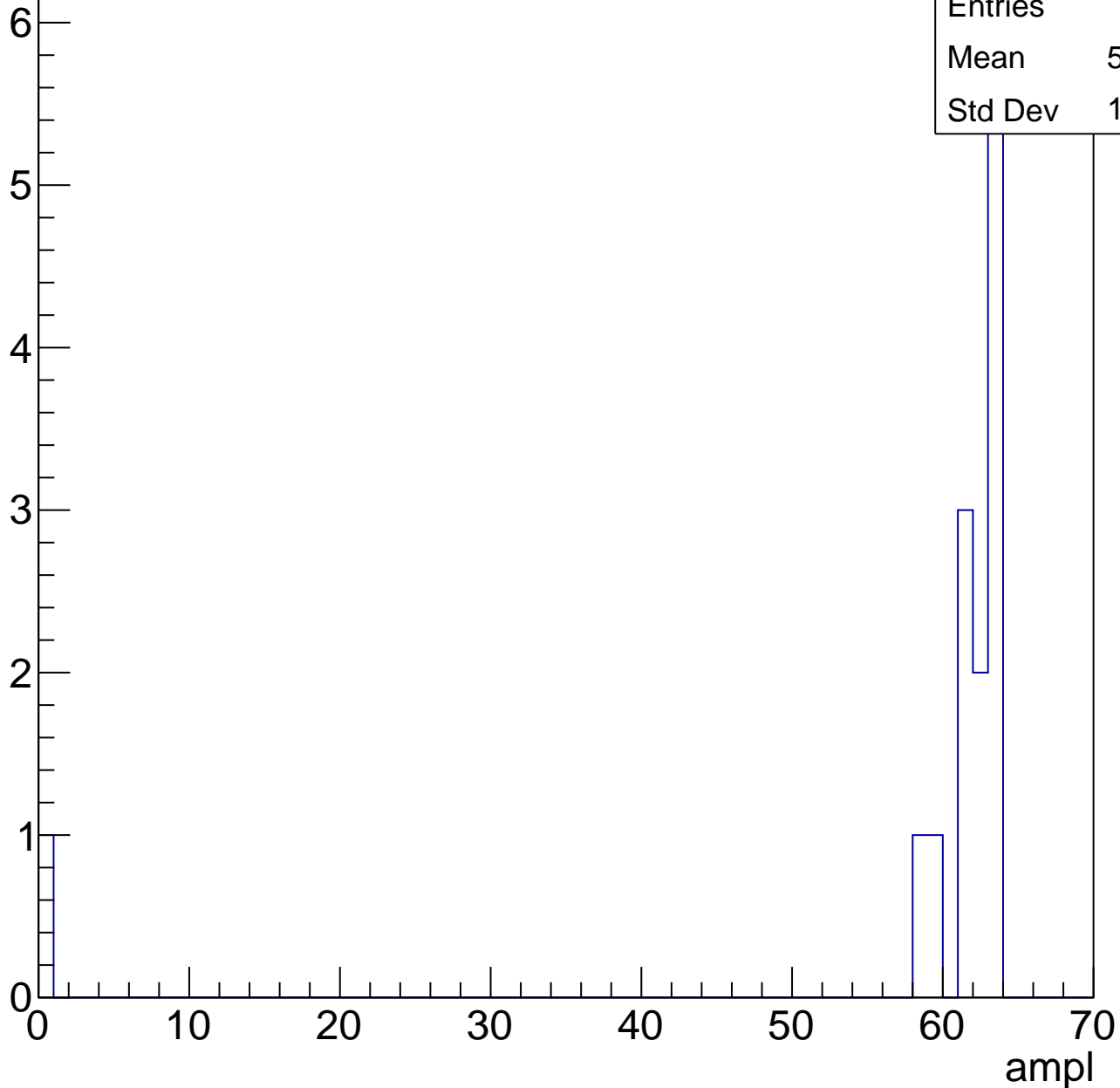


# B0L000S, U19-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	57.29
Std Dev	15.96





# B0L000S, U19-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U19-ch125, adc0

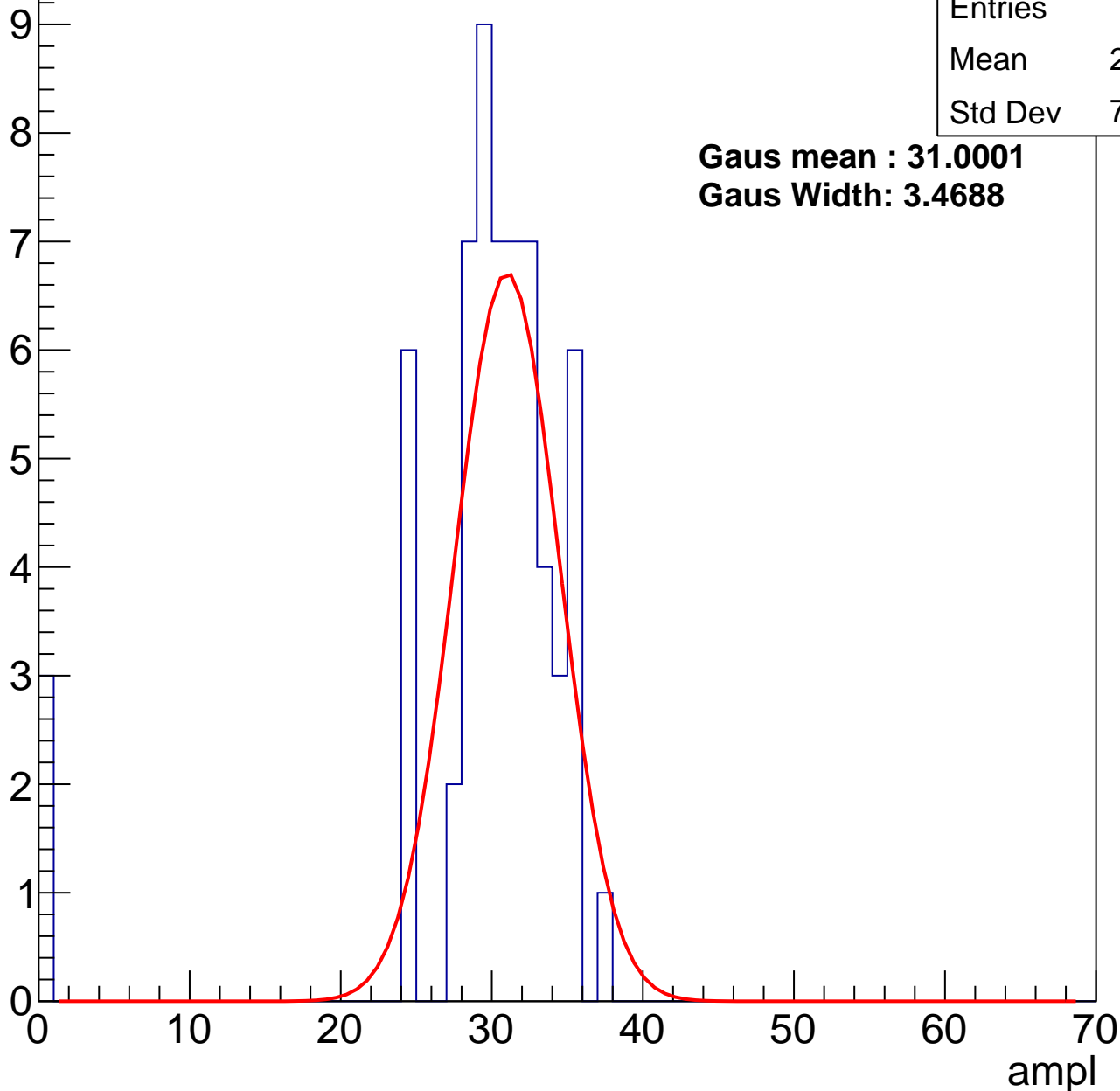
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	28.82
Std Dev	7.188

**Gaus mean : 31.0001**

**Gaus Width: 3.4688**



# B0L000S, U19-ch125, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	37.06
Std Dev	3.242

**Gaus mean : 37.7033**

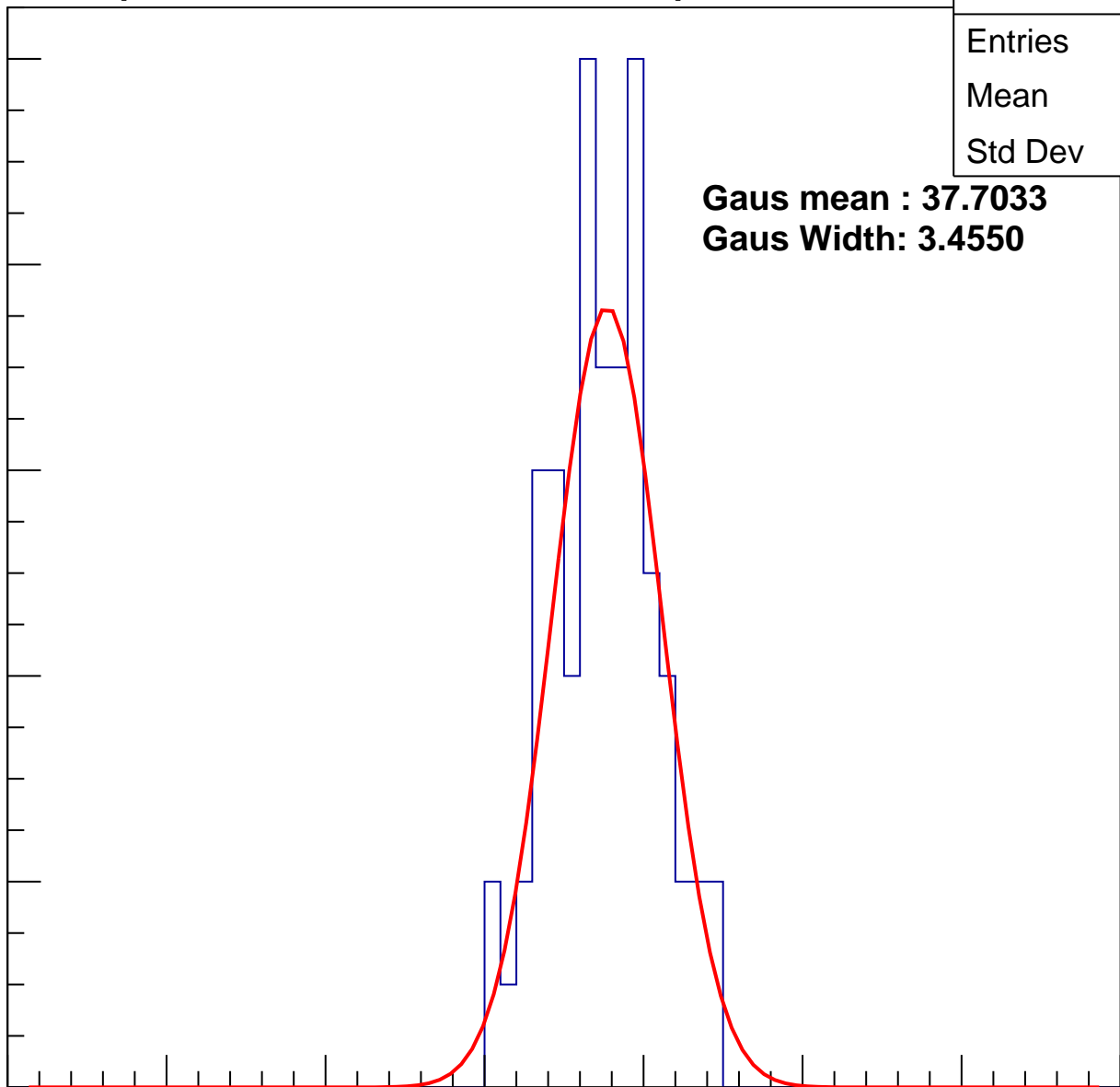
**Gaus Width: 3.4550**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U19-ch125, adc2

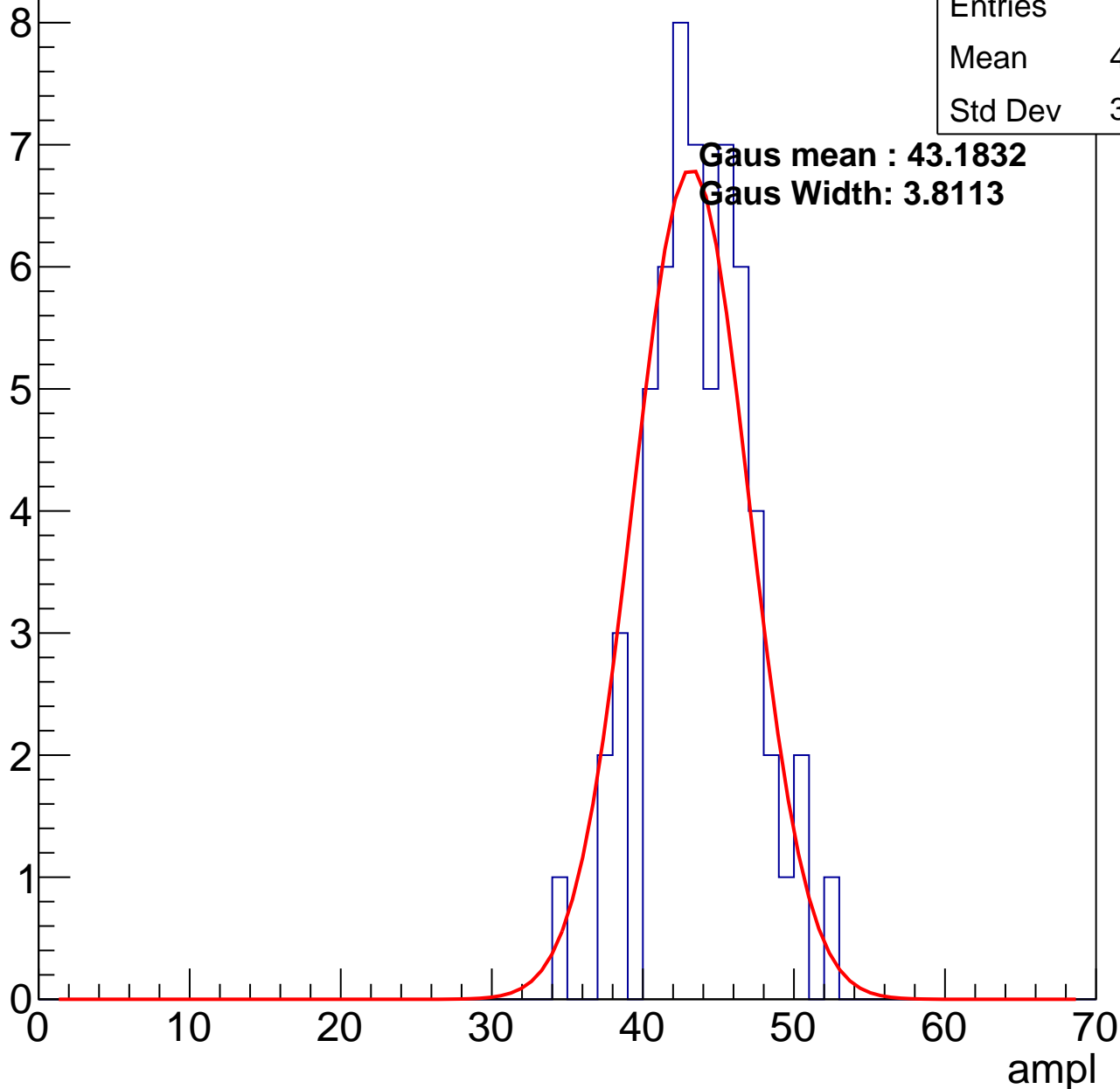
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.35
Std Dev	3.449

**Gaus mean : 43.1832**

**Gaus Width: 3.8113**

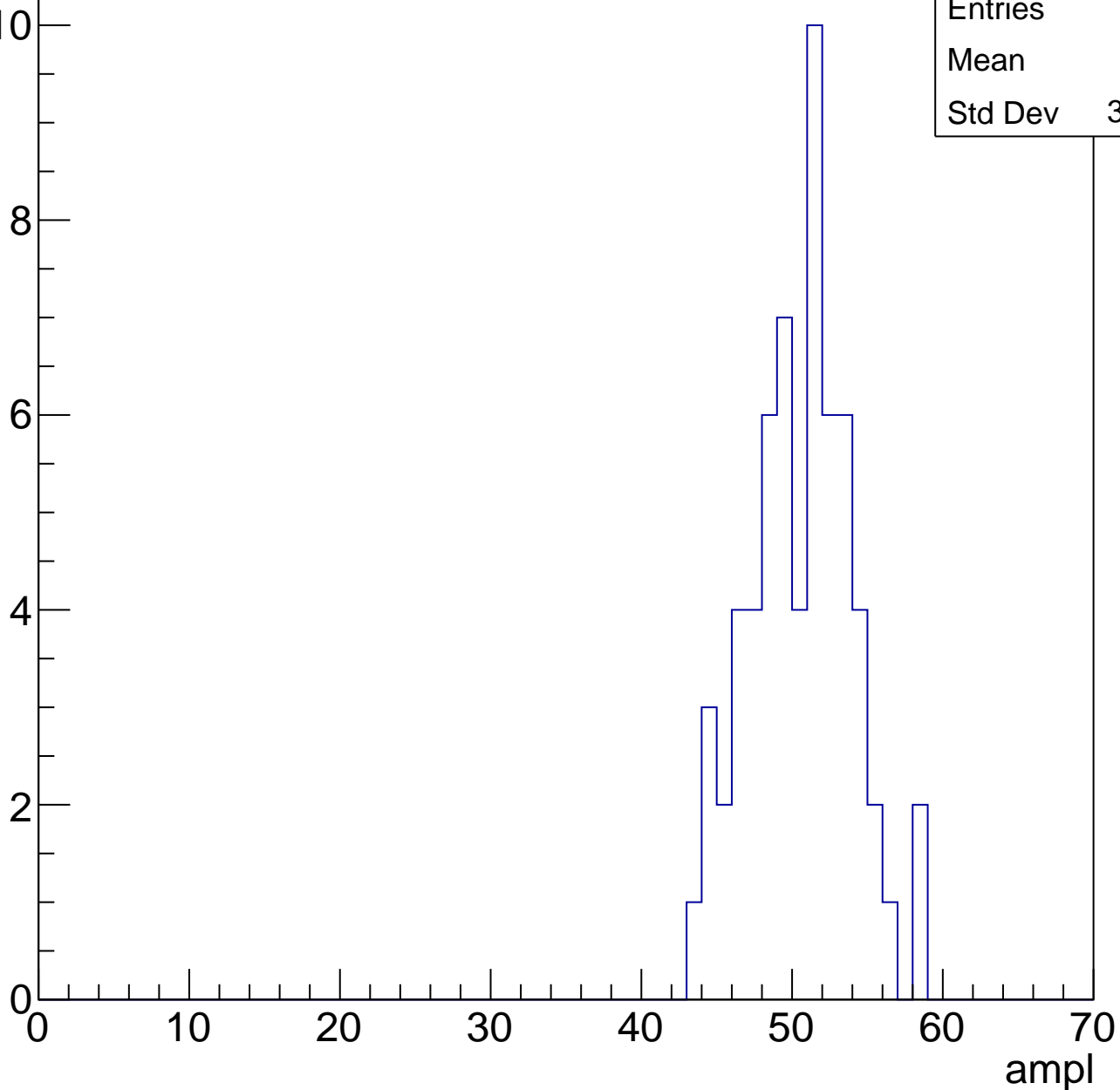


# B0L000S, U19-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.1
Std Dev	3.368

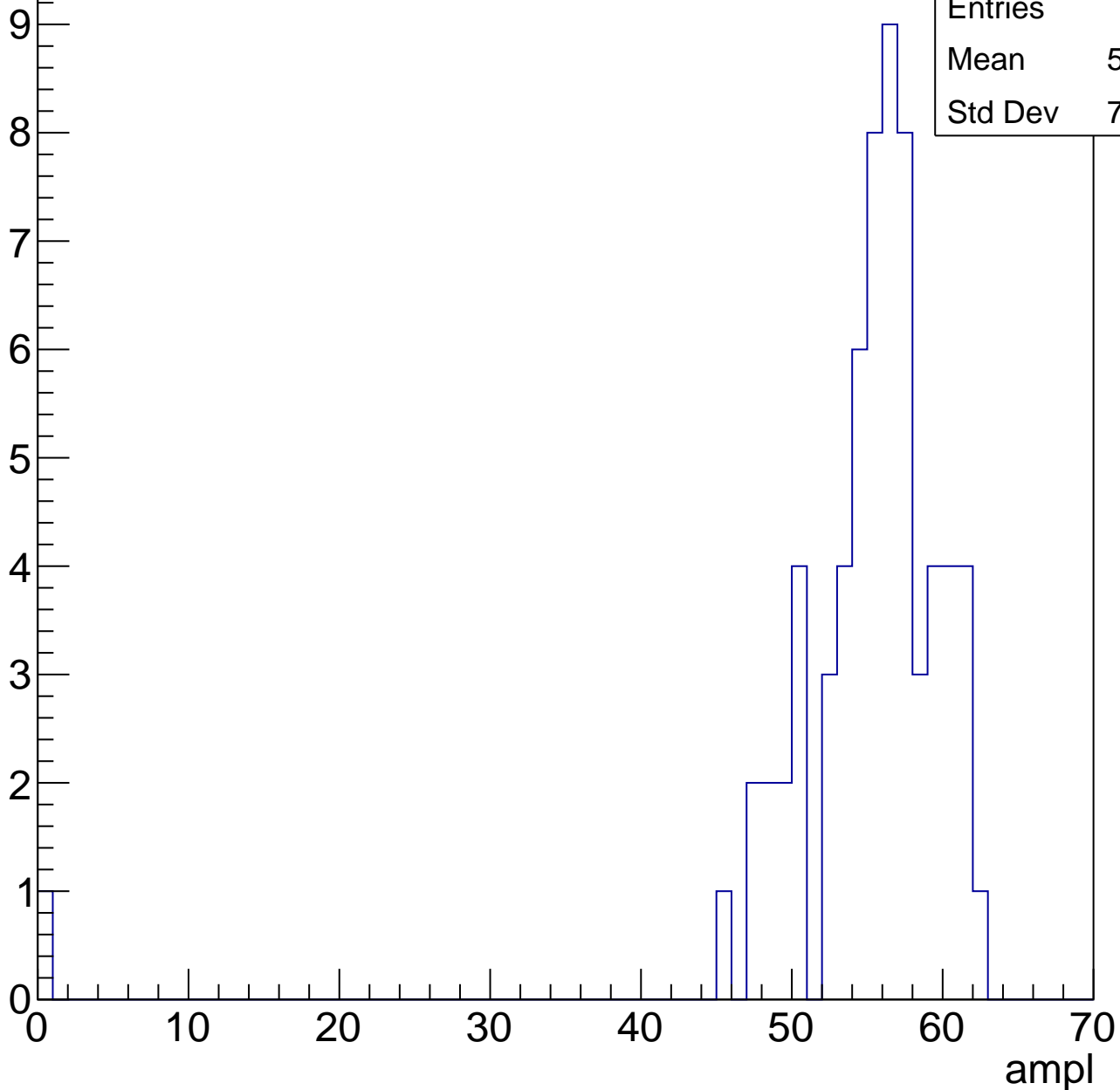


# B0L000S, U19-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	54.26
Std Dev	7.747



# B0L000S, U19-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	39
Mean	60.67
Std Dev	1.952

Entry

10

8

6

4

2

0

0

10

20

30

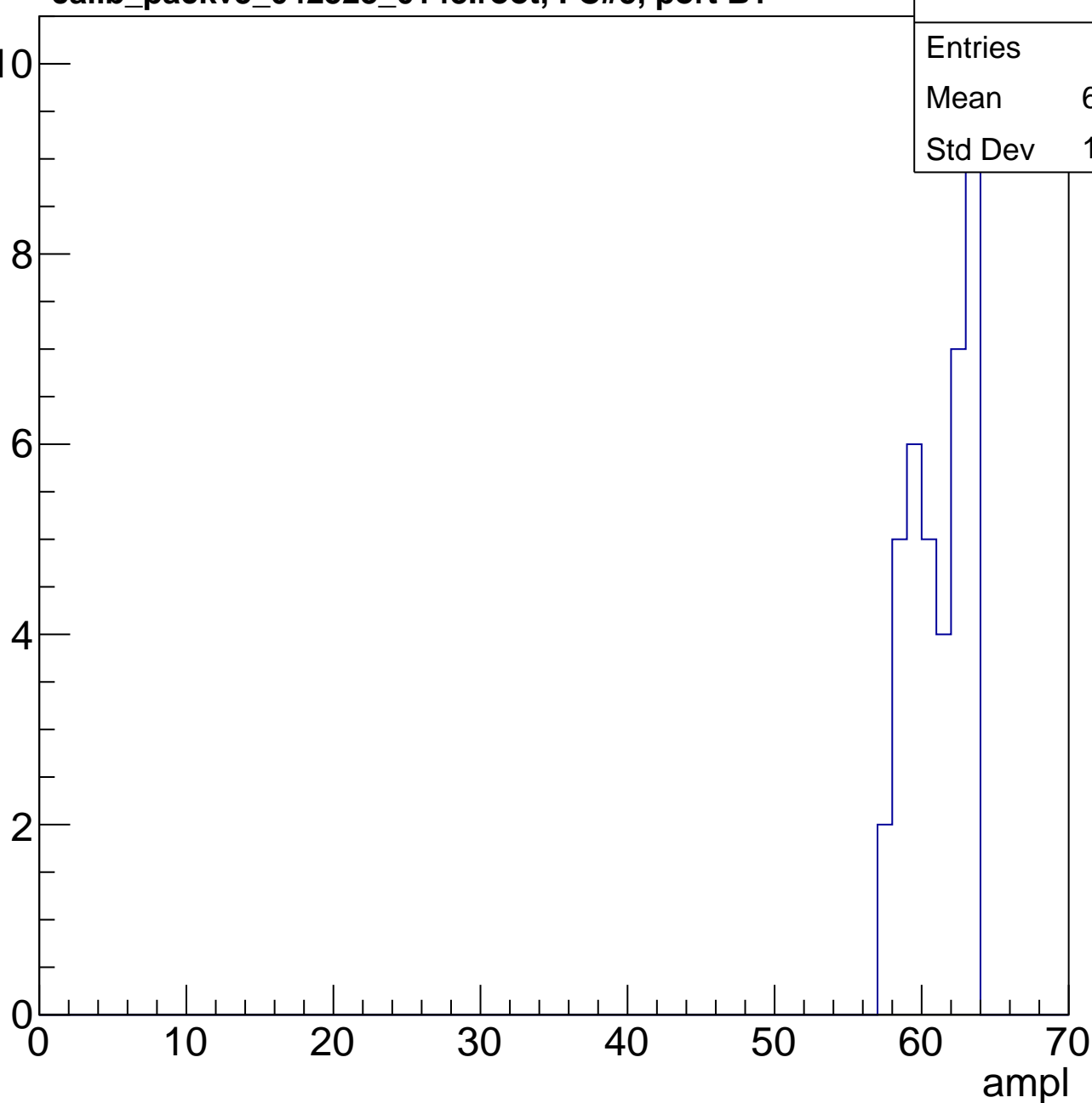
40

50

60

70

ampl



# B0L000S, U19-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch126, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	92
Mean	28.82
Std Dev	4.757

**Gaus mean : 29.5139**

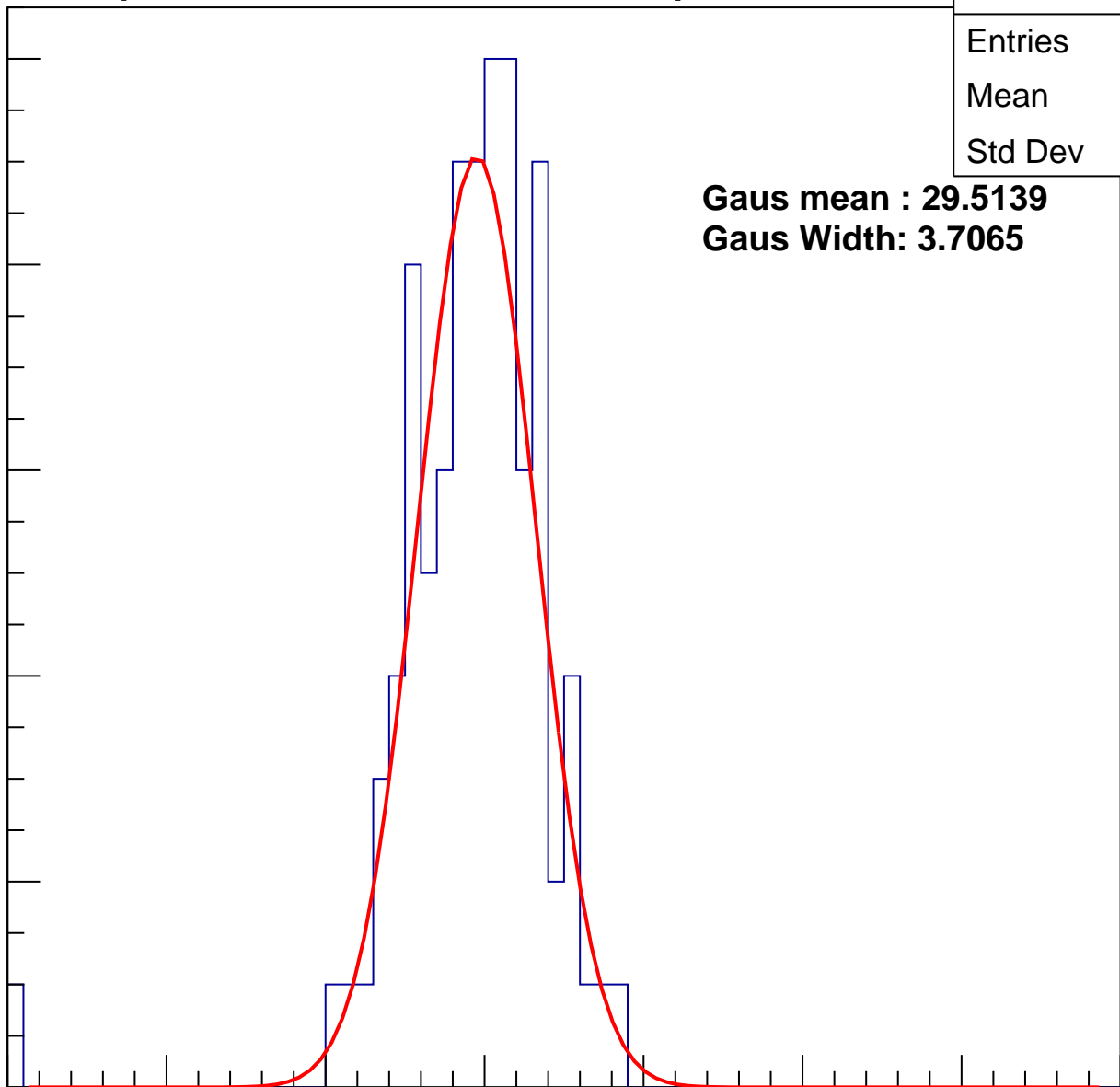
**Gaus Width: 3.7065**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch126, adc1

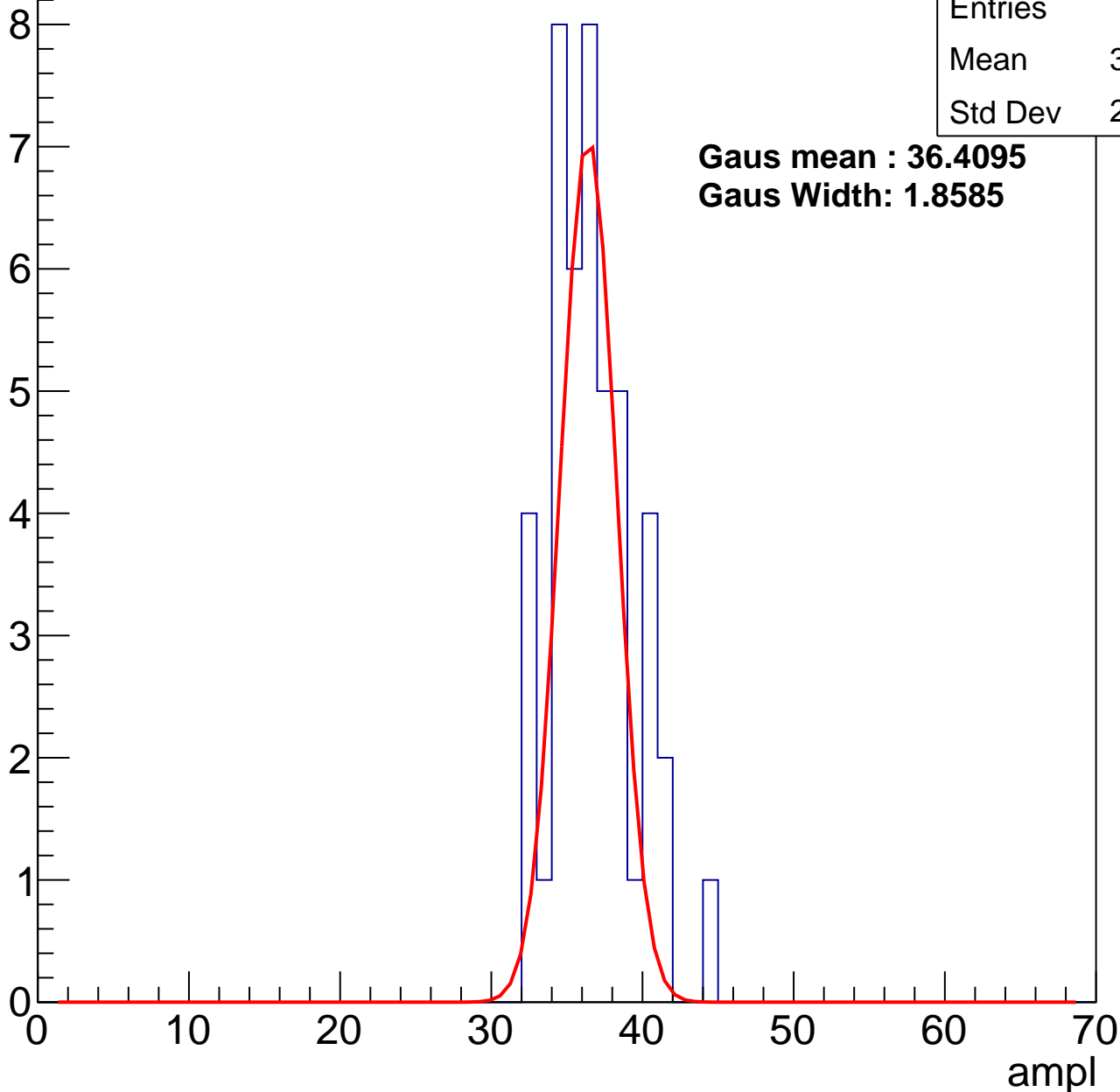
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	36.24
Std Dev	2.668

**Gaus mean : 36.4095**

**Gaus Width: 1.8585**



# B0L000S, U19-ch126, adc2

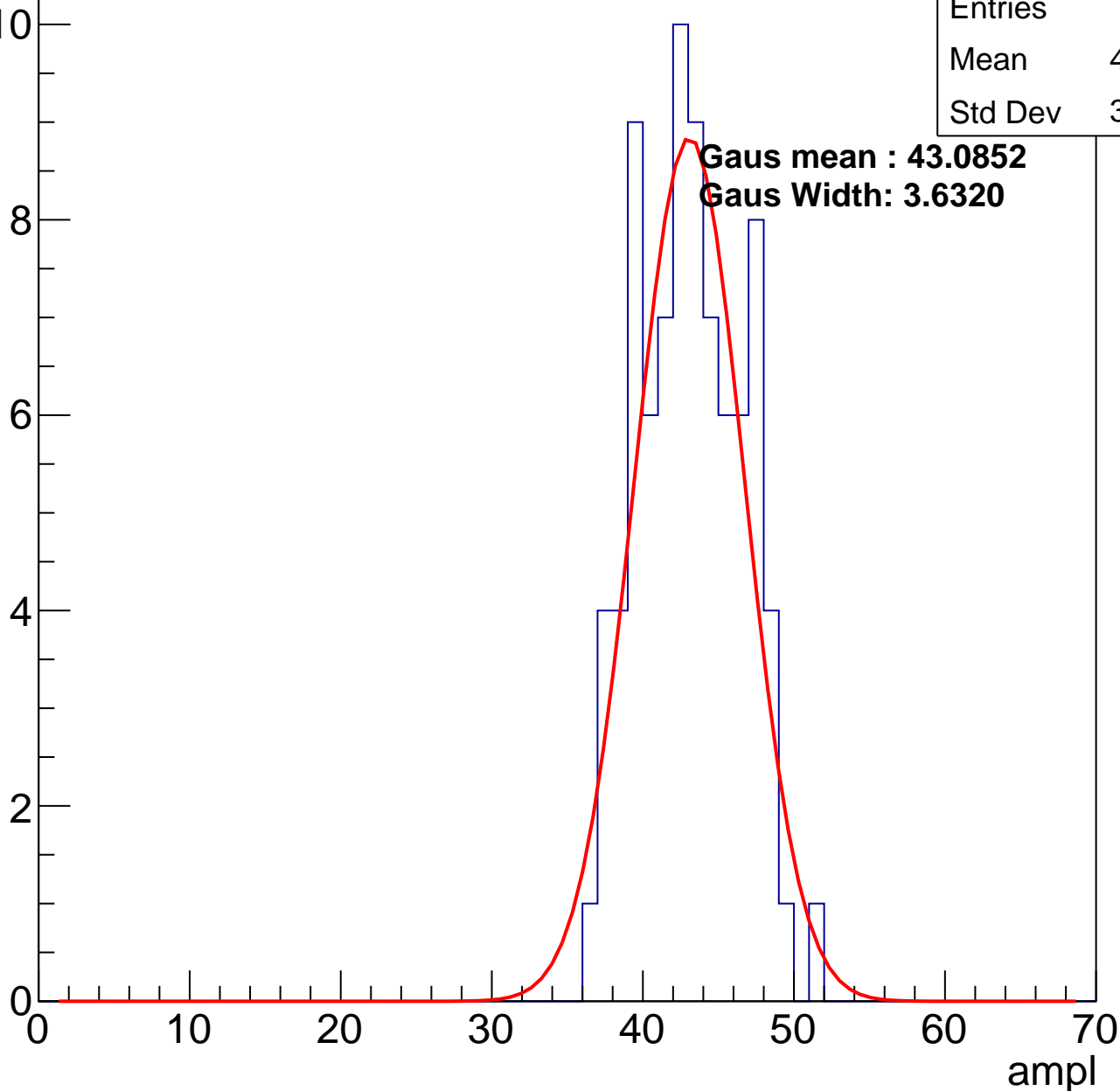
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	42.69
Std Dev	3.354

**Gaus mean : 43.0852**

**Gaus Width: 3.6320**

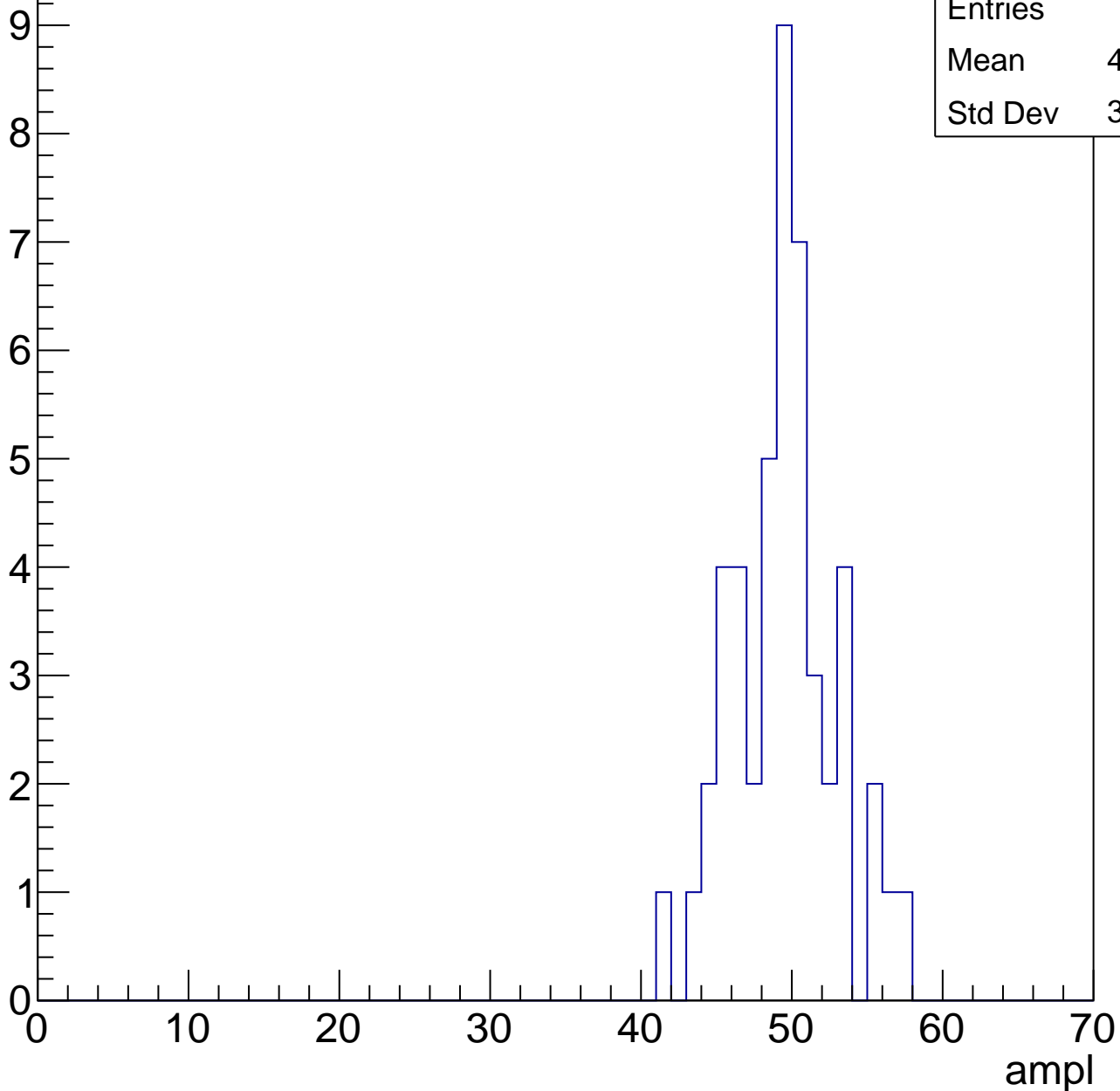


# B0L000S, U19-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	49.02
Std Dev	3.382



# B0L000S, U19-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

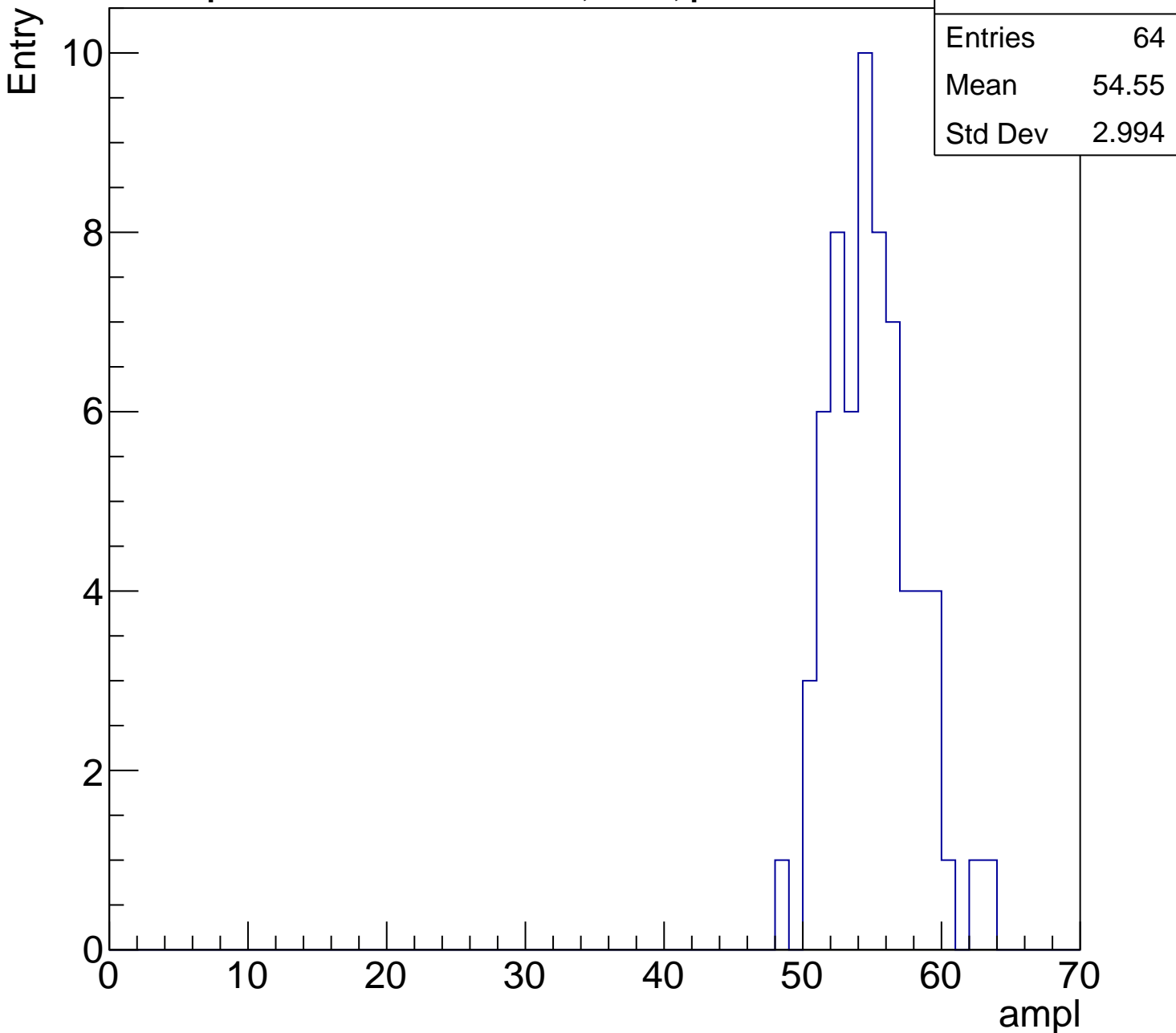
Entries	64
Mean	54.55
Std Dev	2.994

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

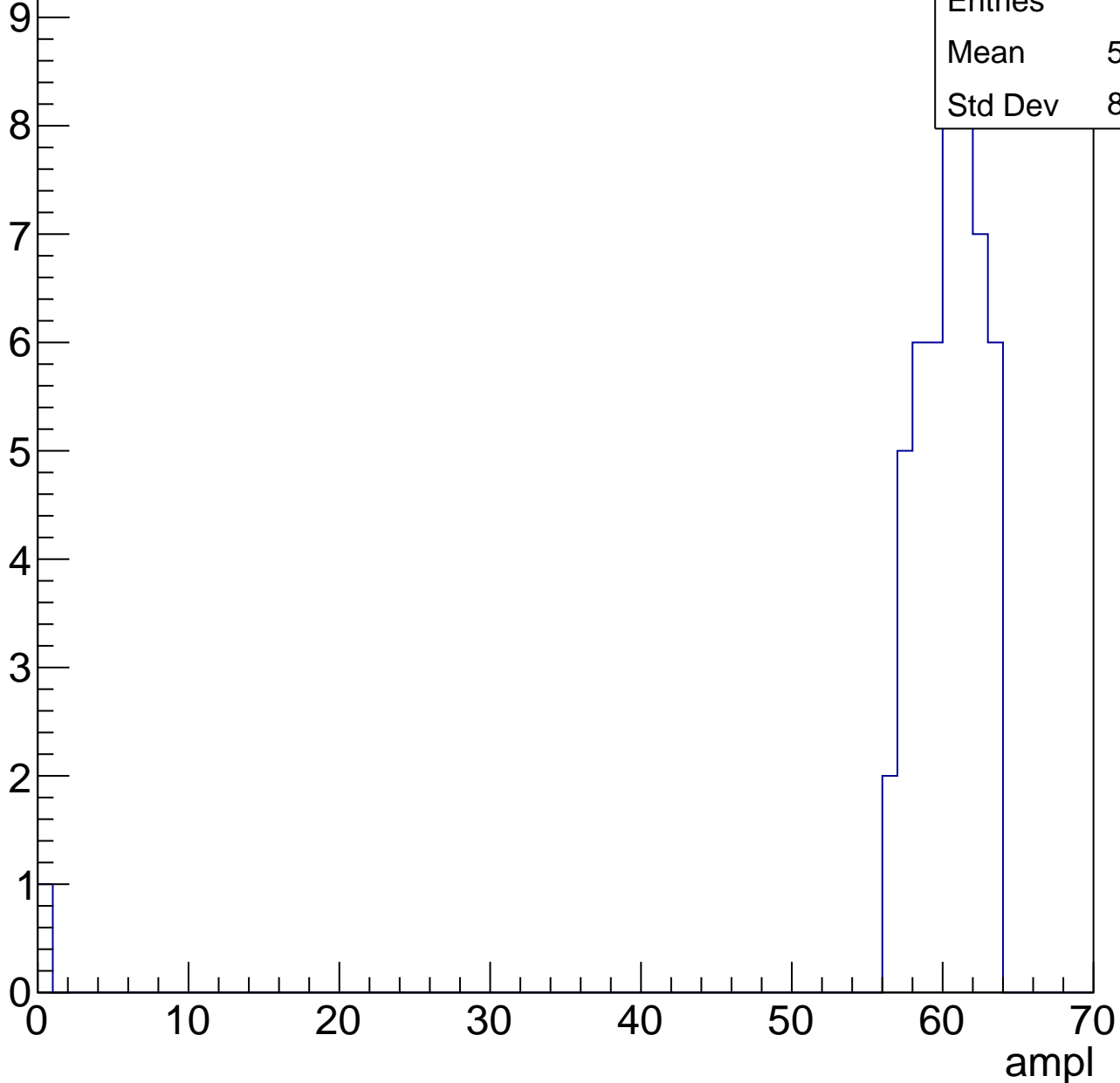


# B0L000S, U19-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	58.82
Std Dev	8.549



# B0L000S, U19-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U19-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch127, adc0

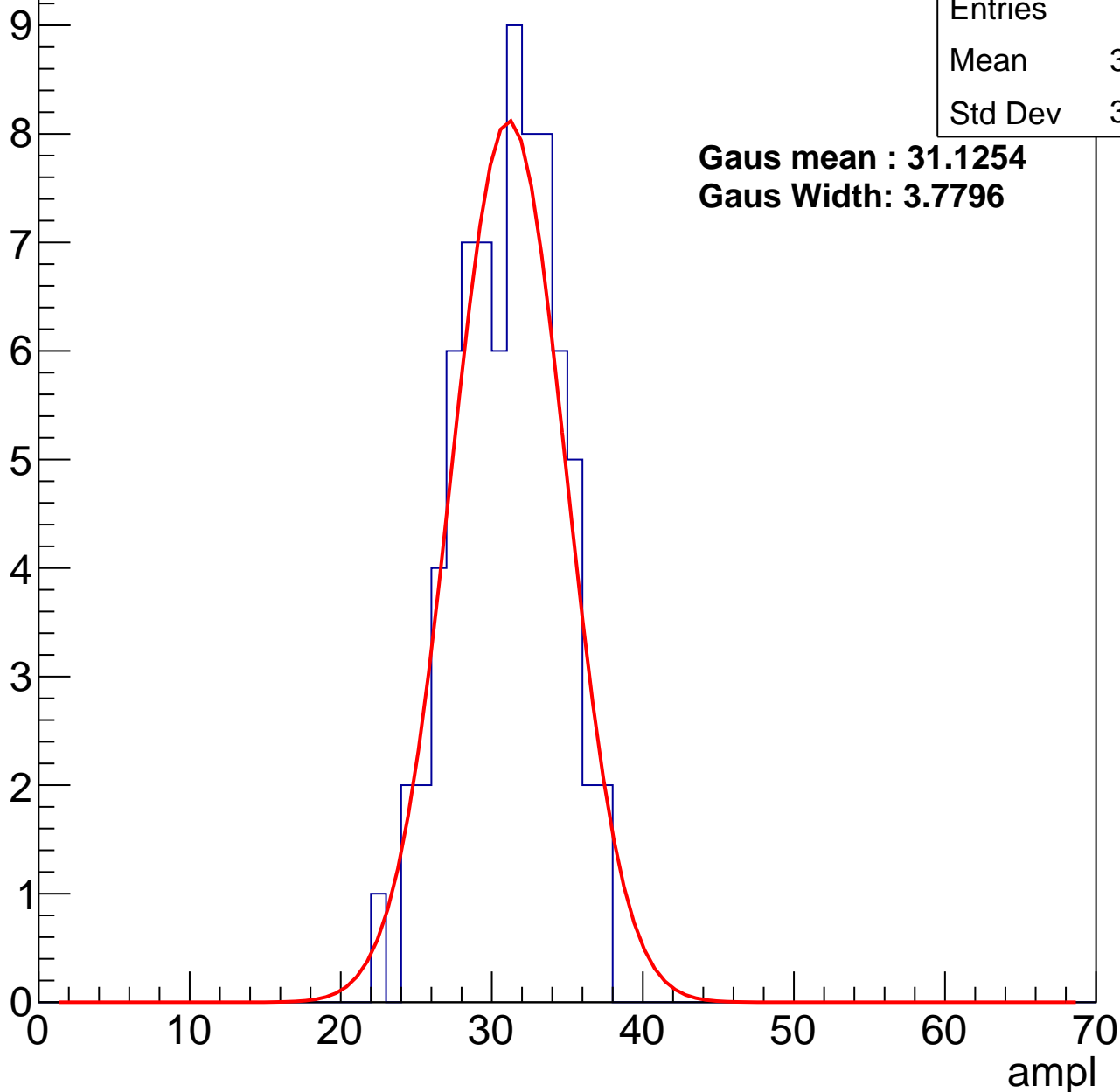
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	30.52
Std Dev	3.312

**Gaus mean : 31.1254**

**Gaus Width: 3.7796**



# B0L000S, U19-ch127, adc1

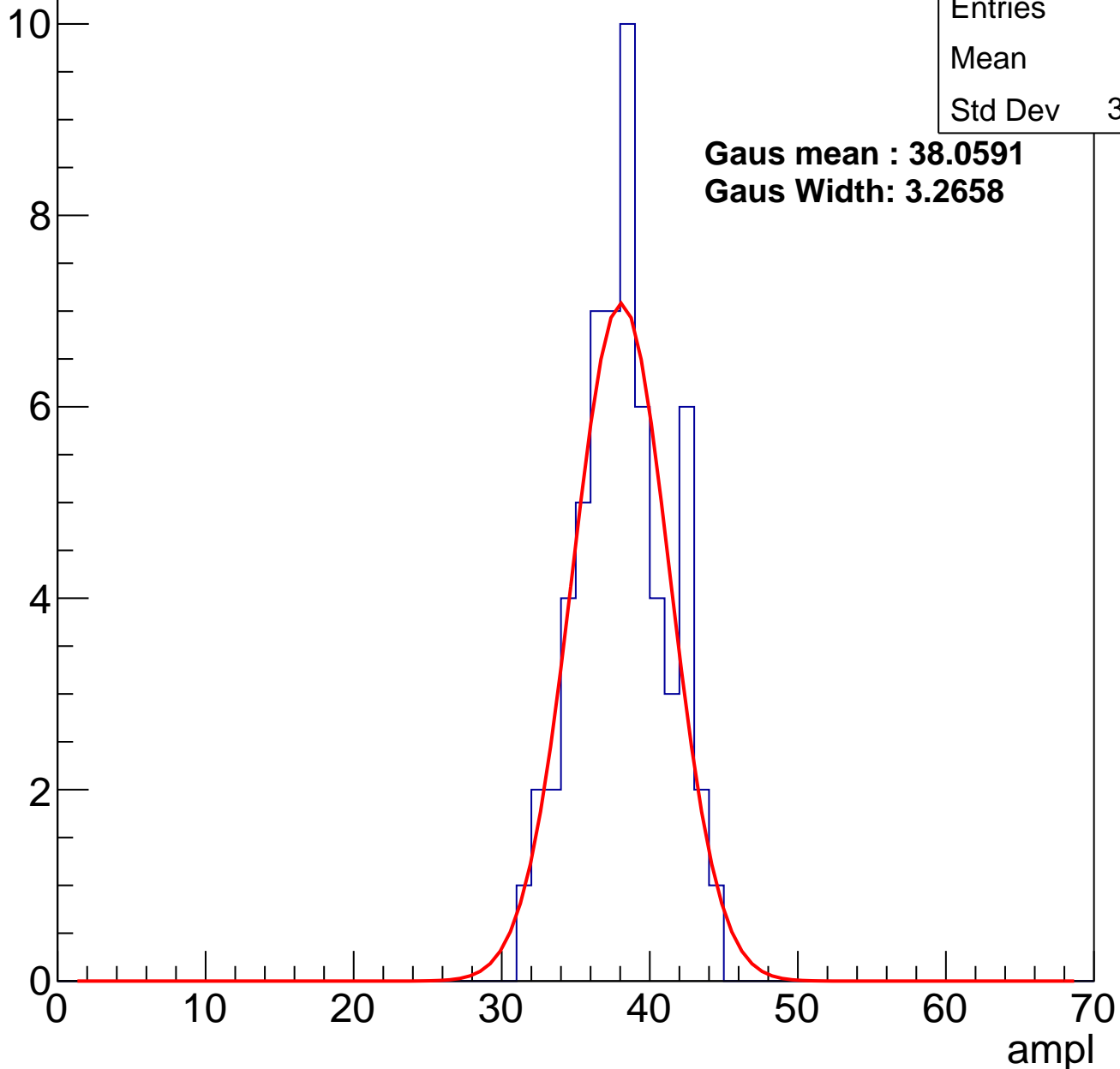
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	60
Mean	37.7
Std Dev	3.002

**Gaus mean : 38.0591**

**Gaus Width: 3.2658**

Entry

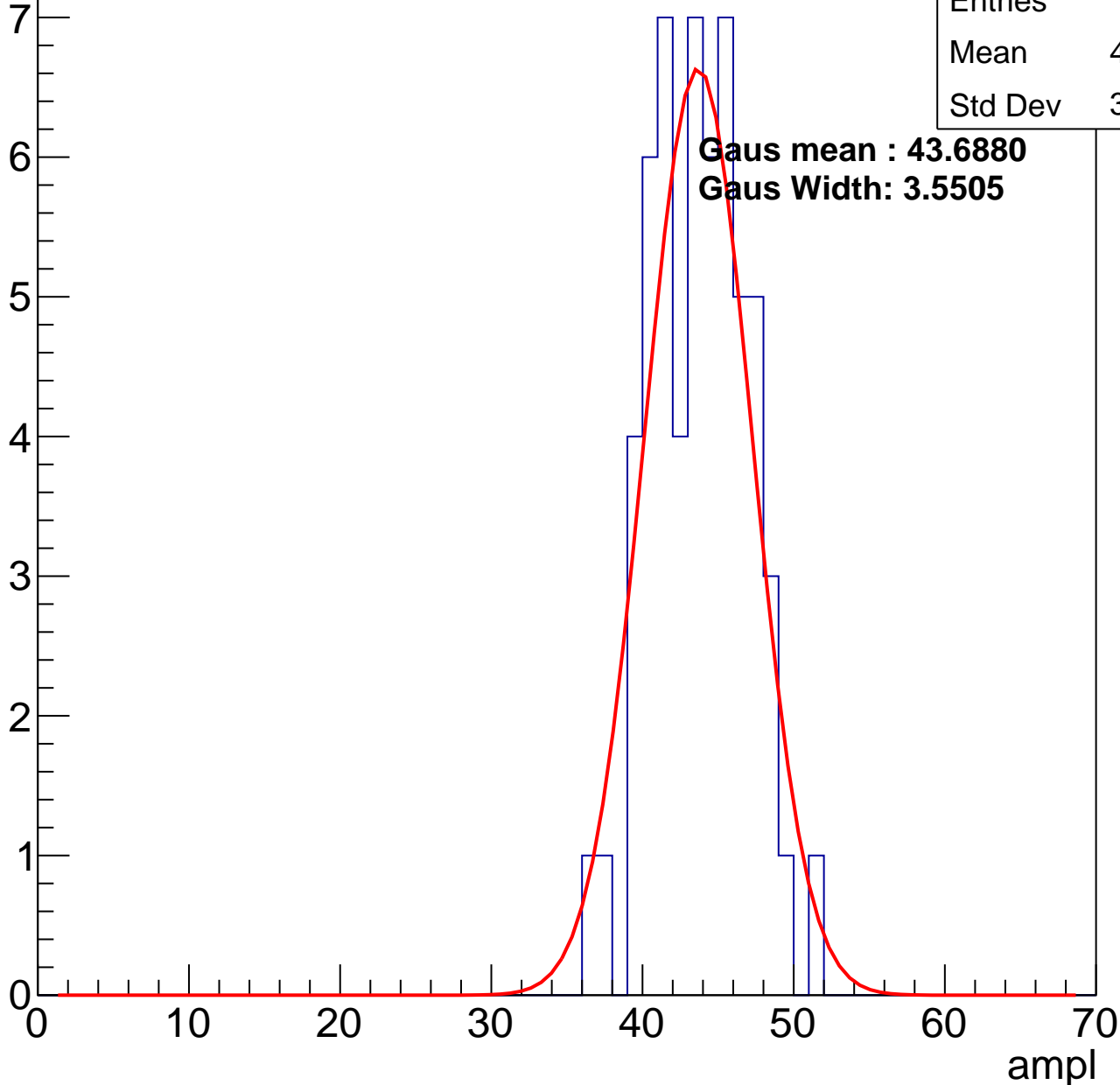


# B0L000S, U19-ch127, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.33
Std Dev	3.115

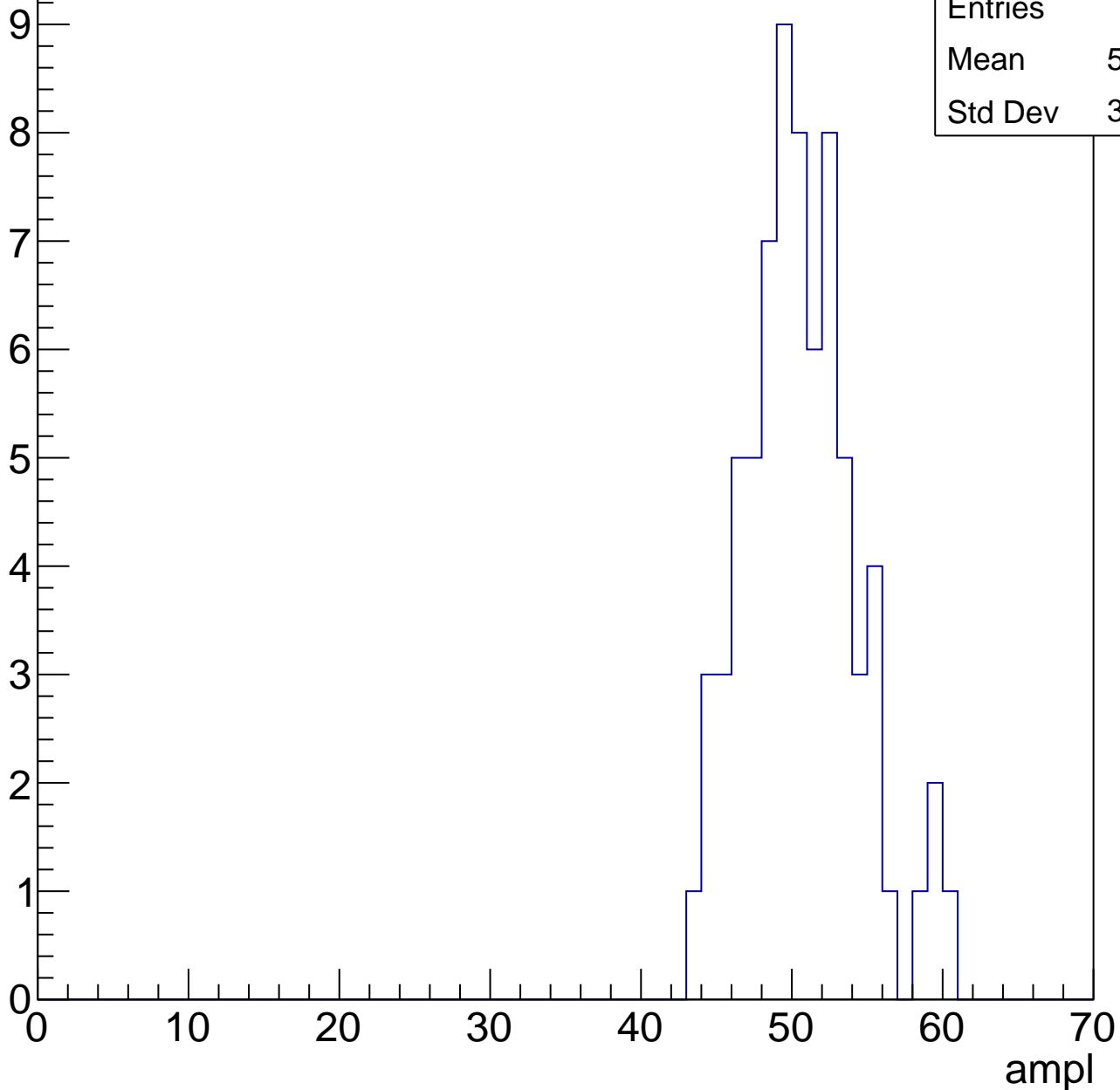


# B0L000S, U19-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	50.18
Std Dev	3.698

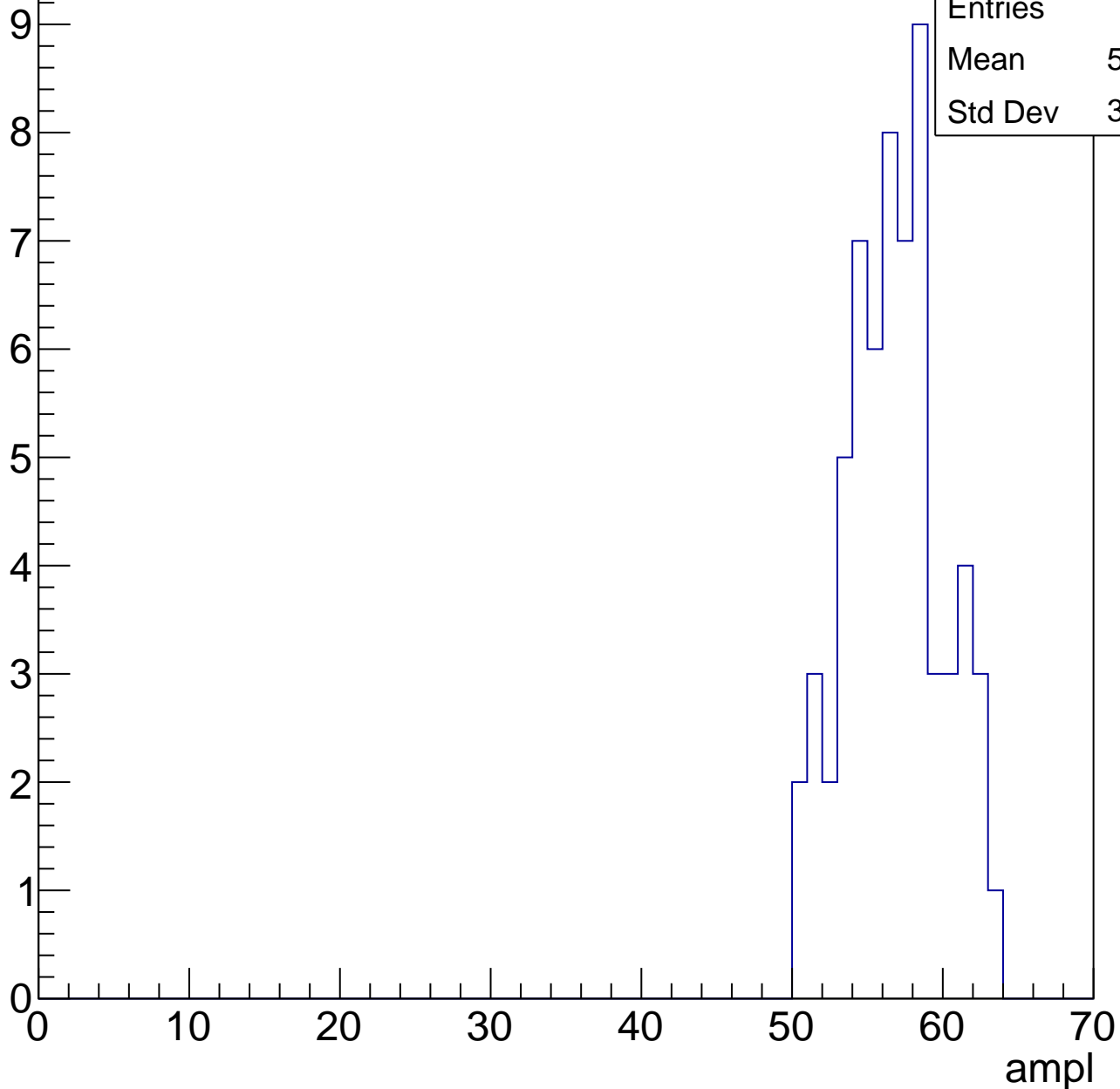


# B0L000S, U19-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	56.33
Std Dev	3.152

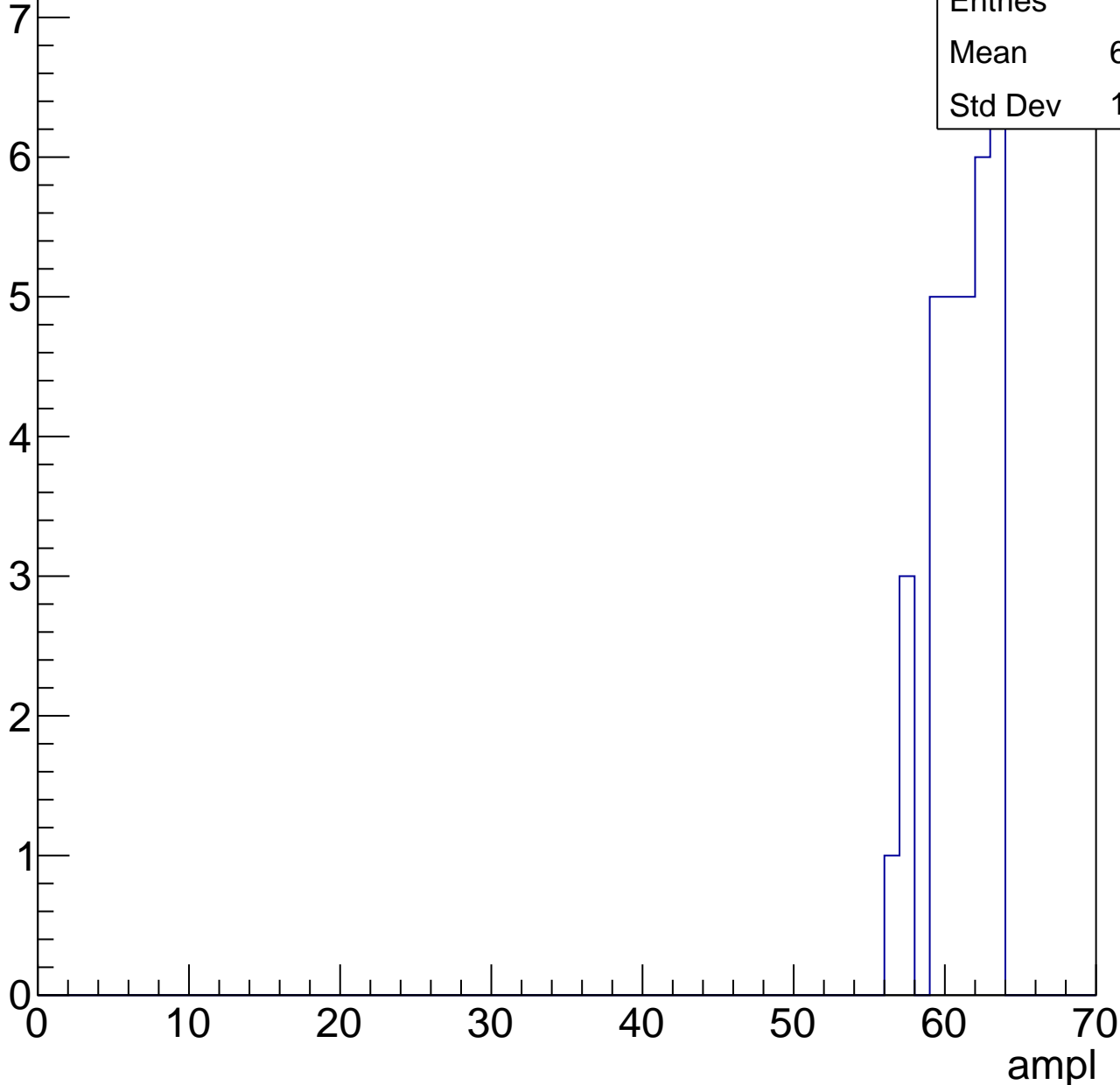


# B0L000S, U19-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	60.62
Std Dev	1.996



# B0L000S, U19-ch127, adc6

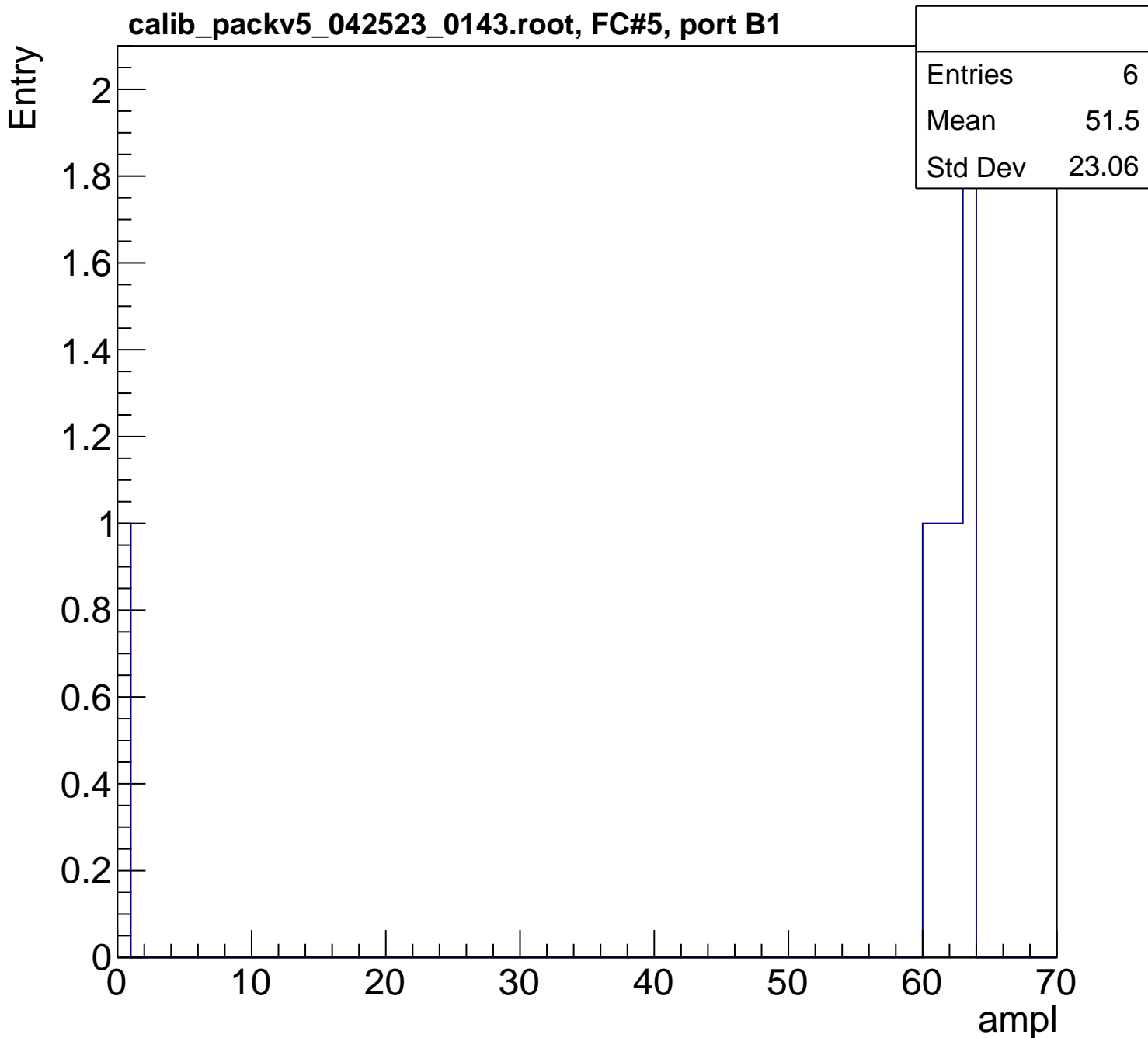
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.5
Std Dev	23.06

ampl





# B0L000S, U19-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

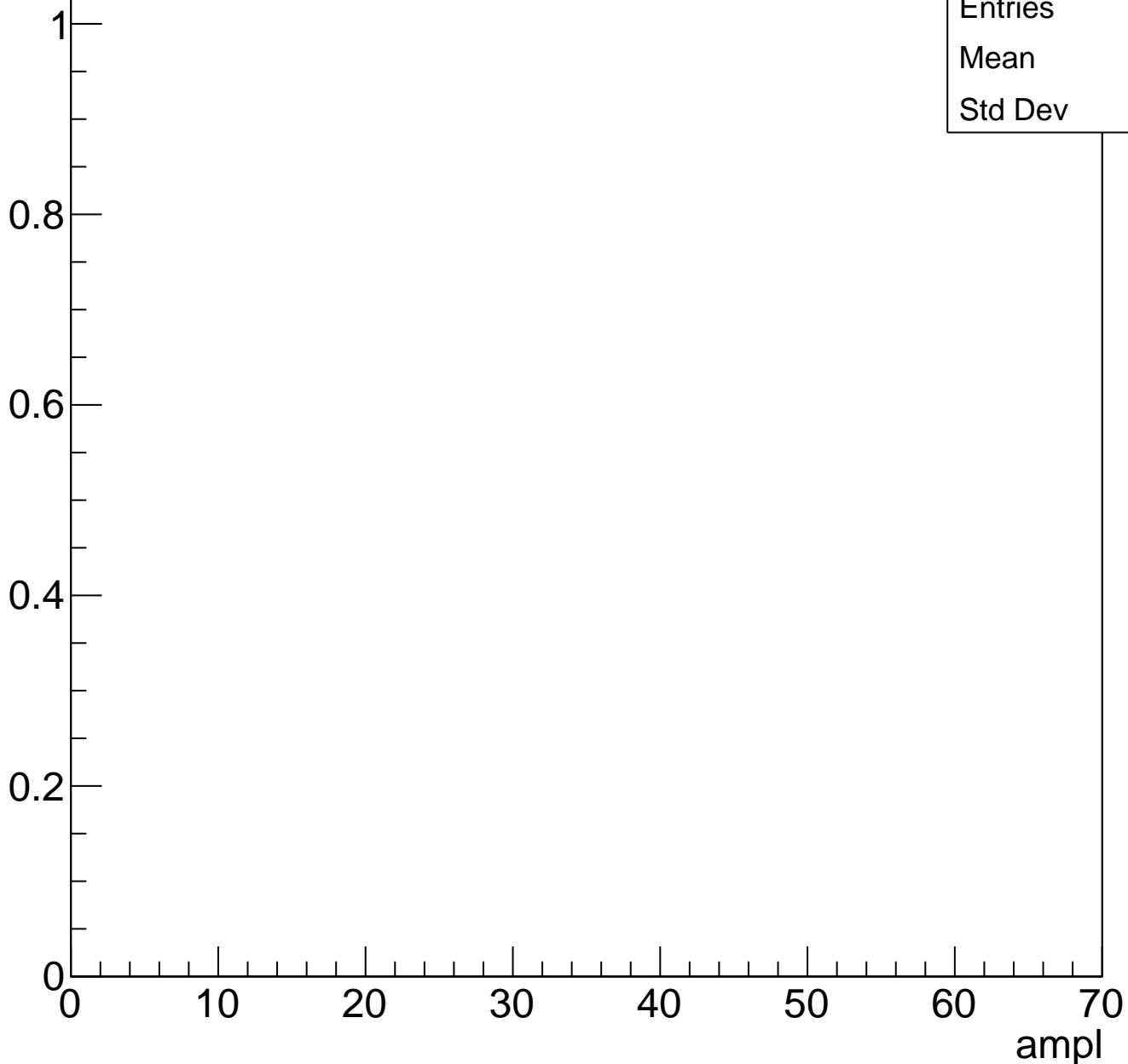


Entries	0
Mean	0
Std Dev	0

# B0L000S, U19-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0