

B0L002S, U12-ch0, adc0

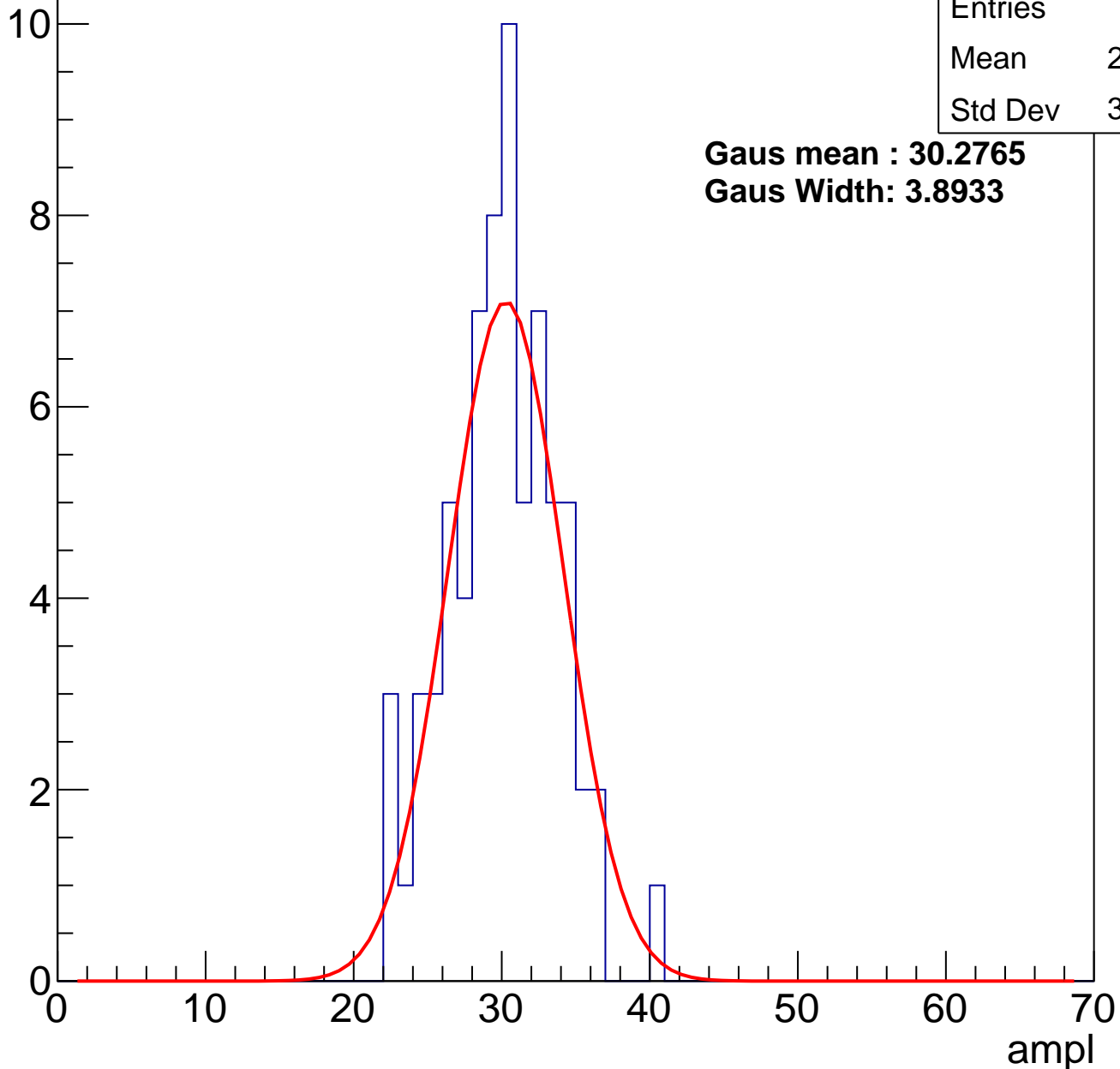
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	29.55
Std Dev	3.642

Gaus mean : 30.2765

Gaus Width: 3.8933

Entry



B0L002S, U12-ch0, adc1

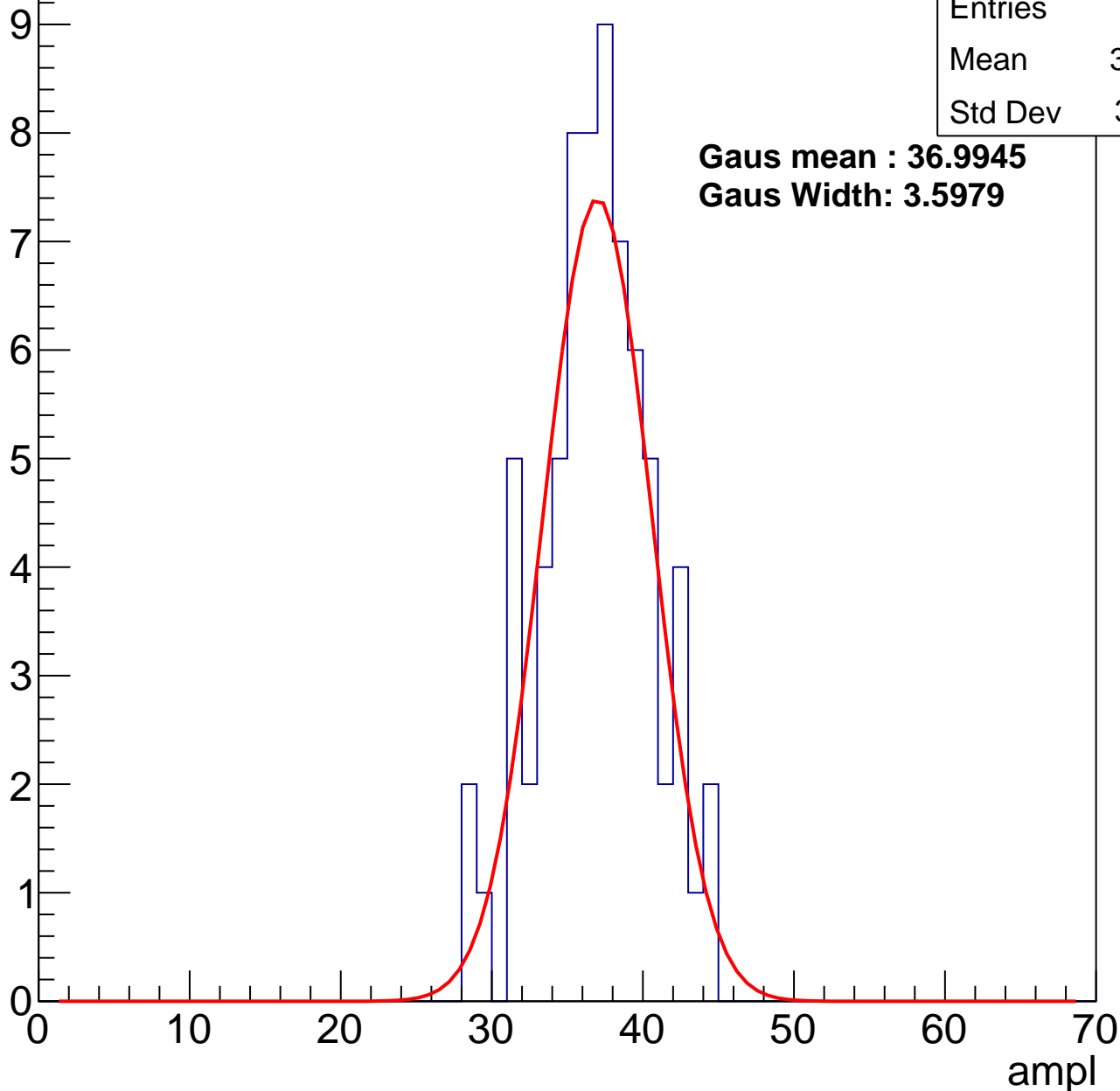
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.45
Std Dev	3.611

Gaus mean : 36.9945

Gaus Width: 3.5979



B0L002S, U12-ch0, adc2

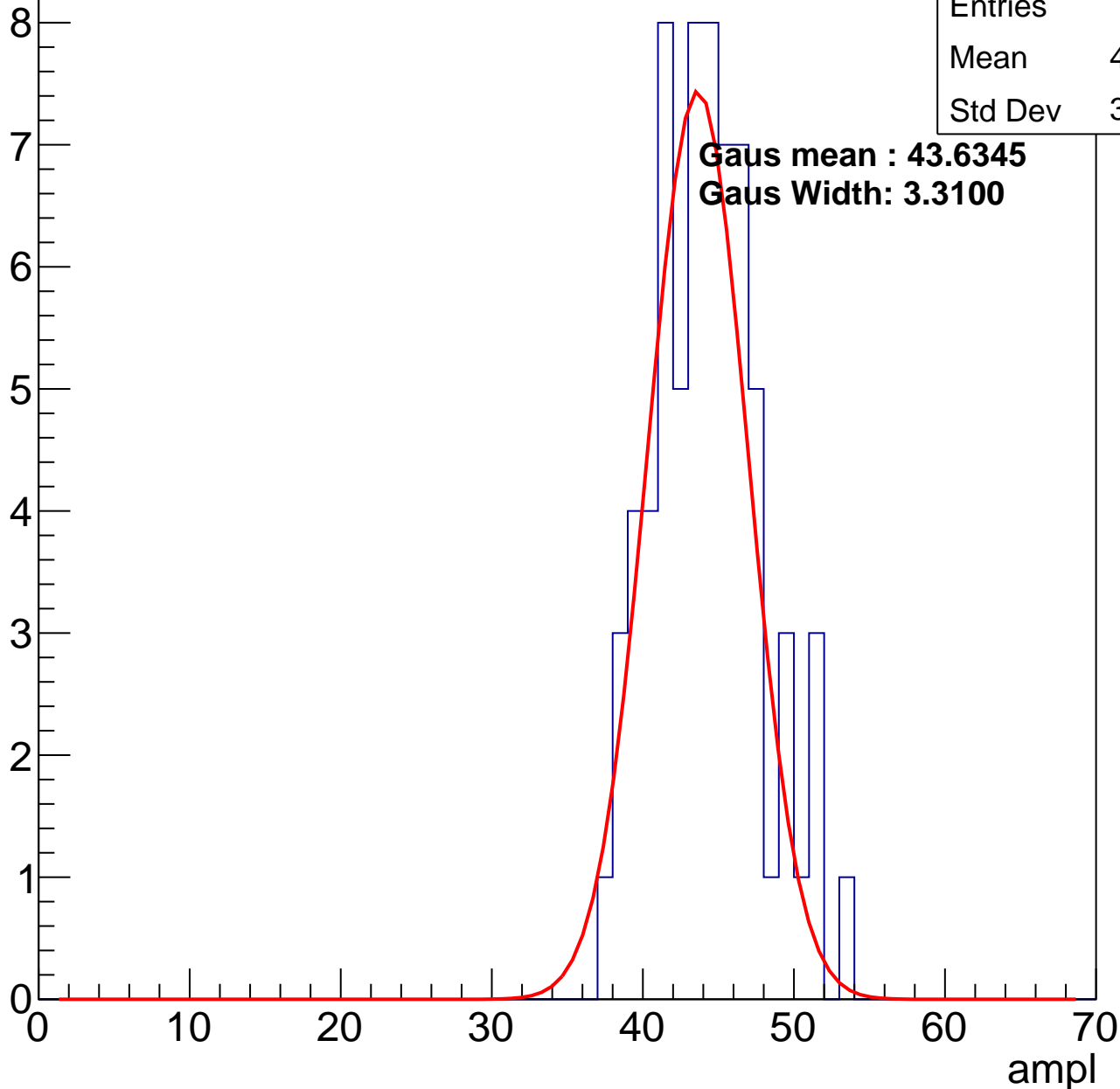
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.83
Std Dev	3.518

Gaus mean : 43.6345

Gaus Width: 3.3100

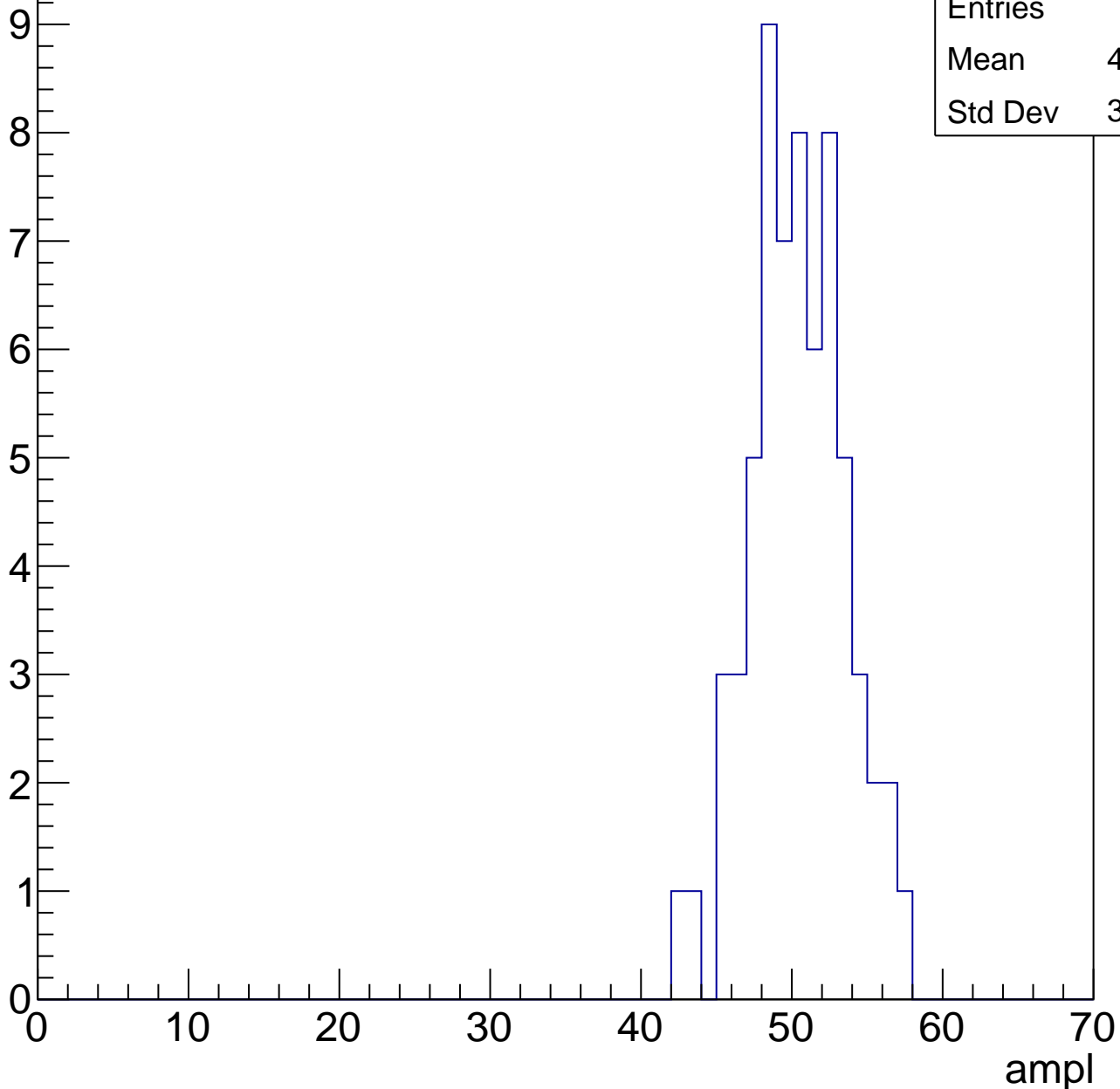


B0L002S, U12-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

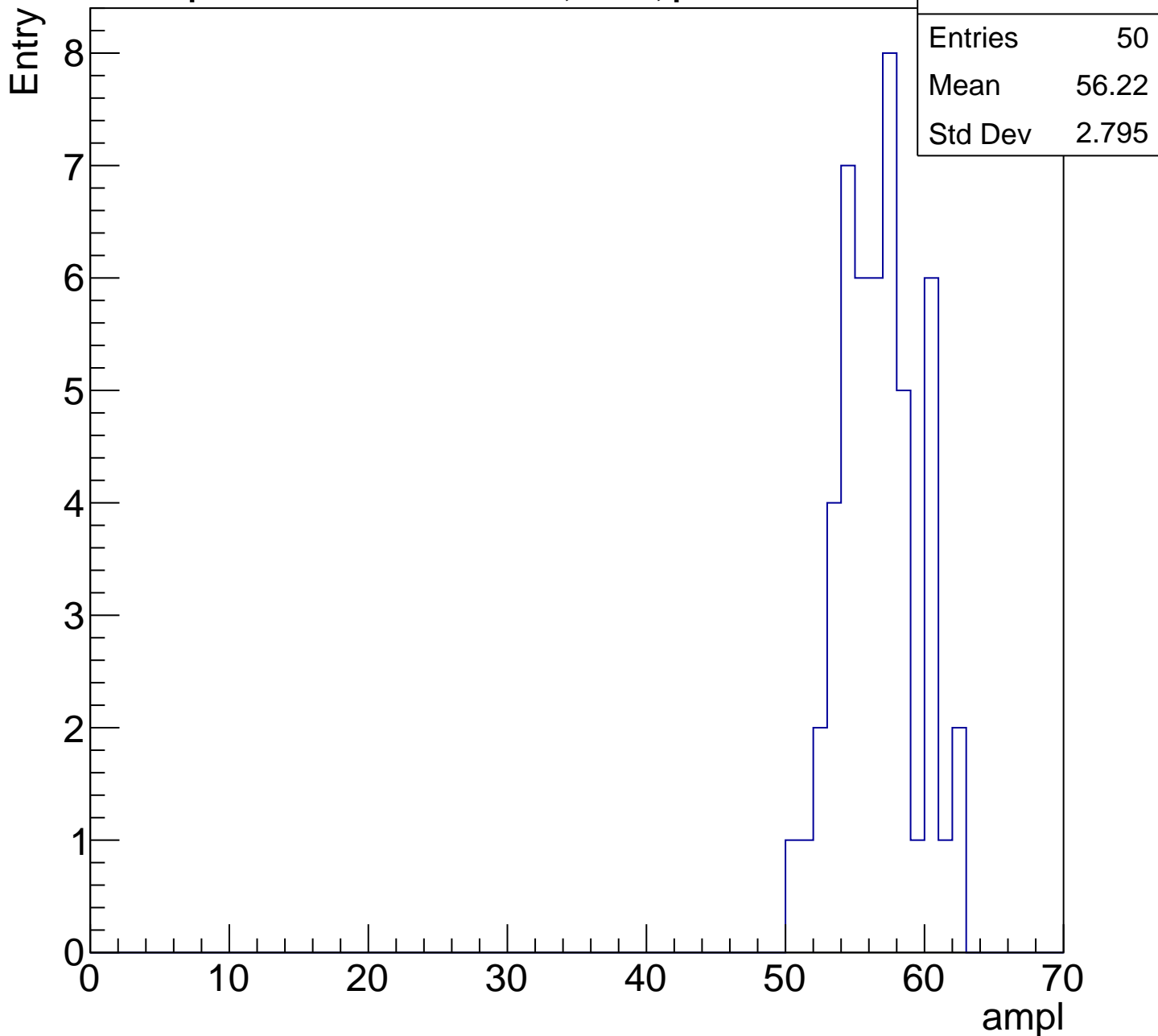
Entry

Entries	64
Mean	49.94
Std Dev	3.127



B0L002S, U12-ch0, adc4

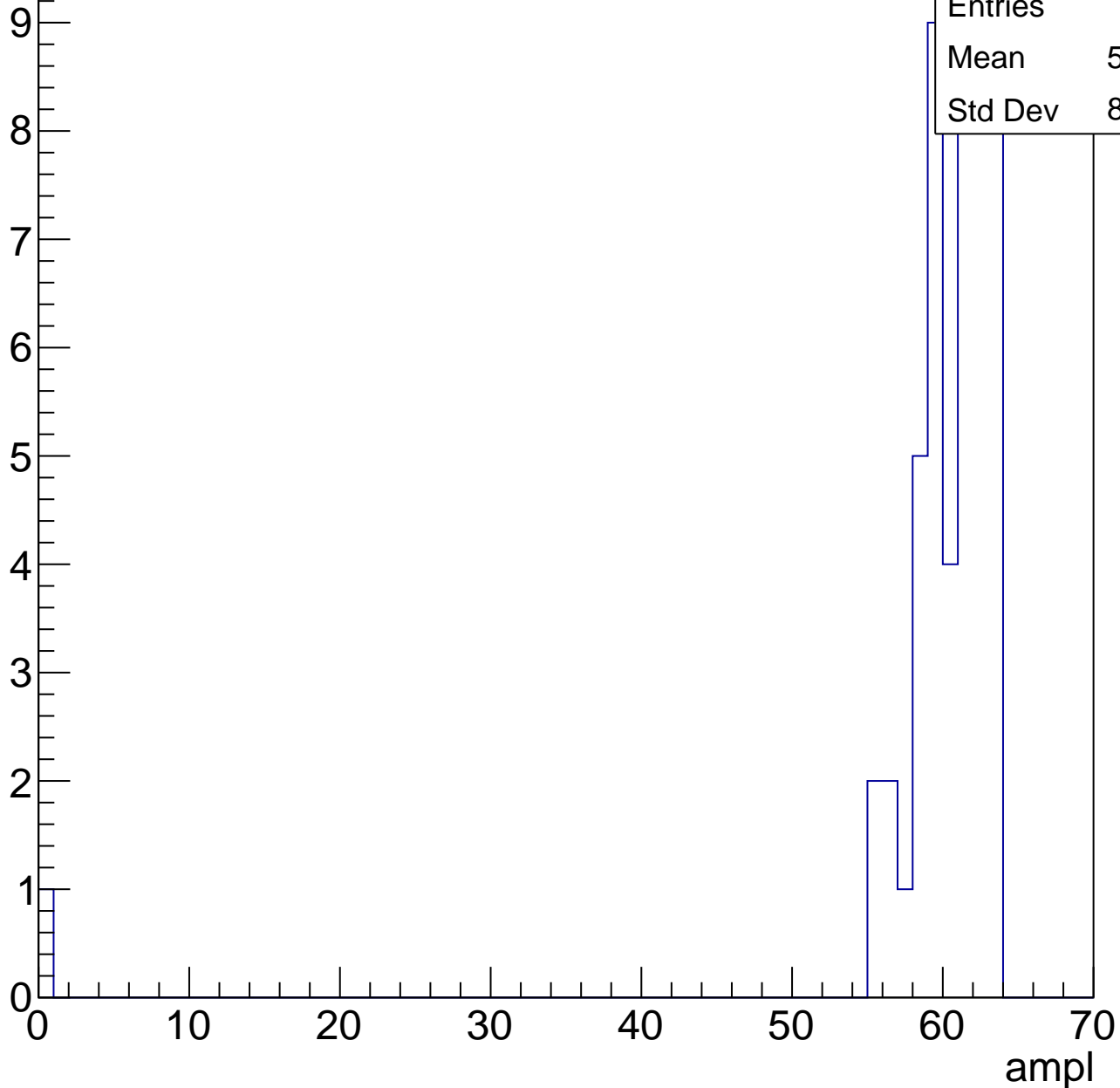
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U12-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L002S, U12-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch1, adc0

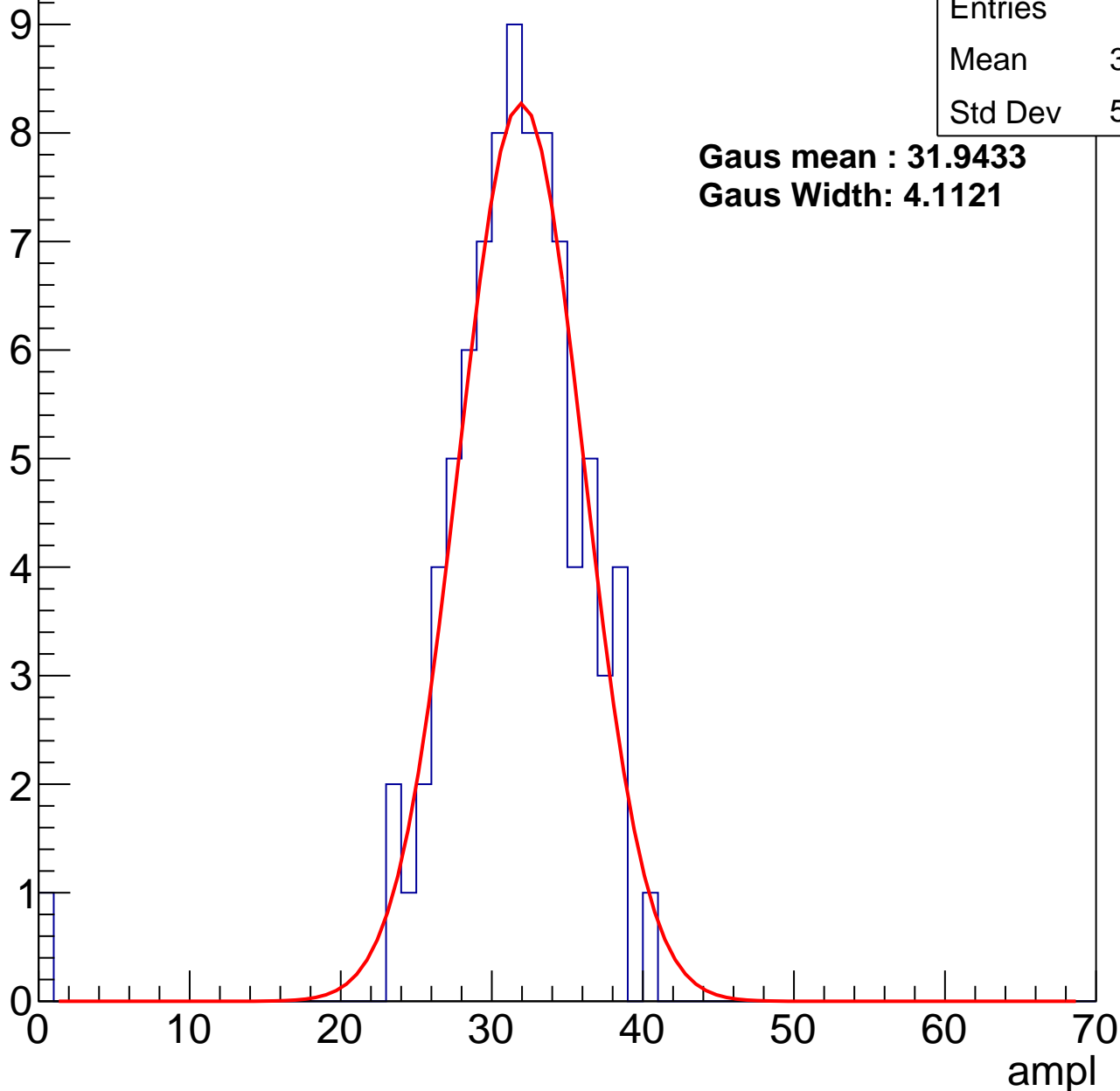
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	30.94
Std Dev	5.037

Gaus mean : 31.9433

Gaus Width: 4.1121



B0L002S, U12-ch1, adc1

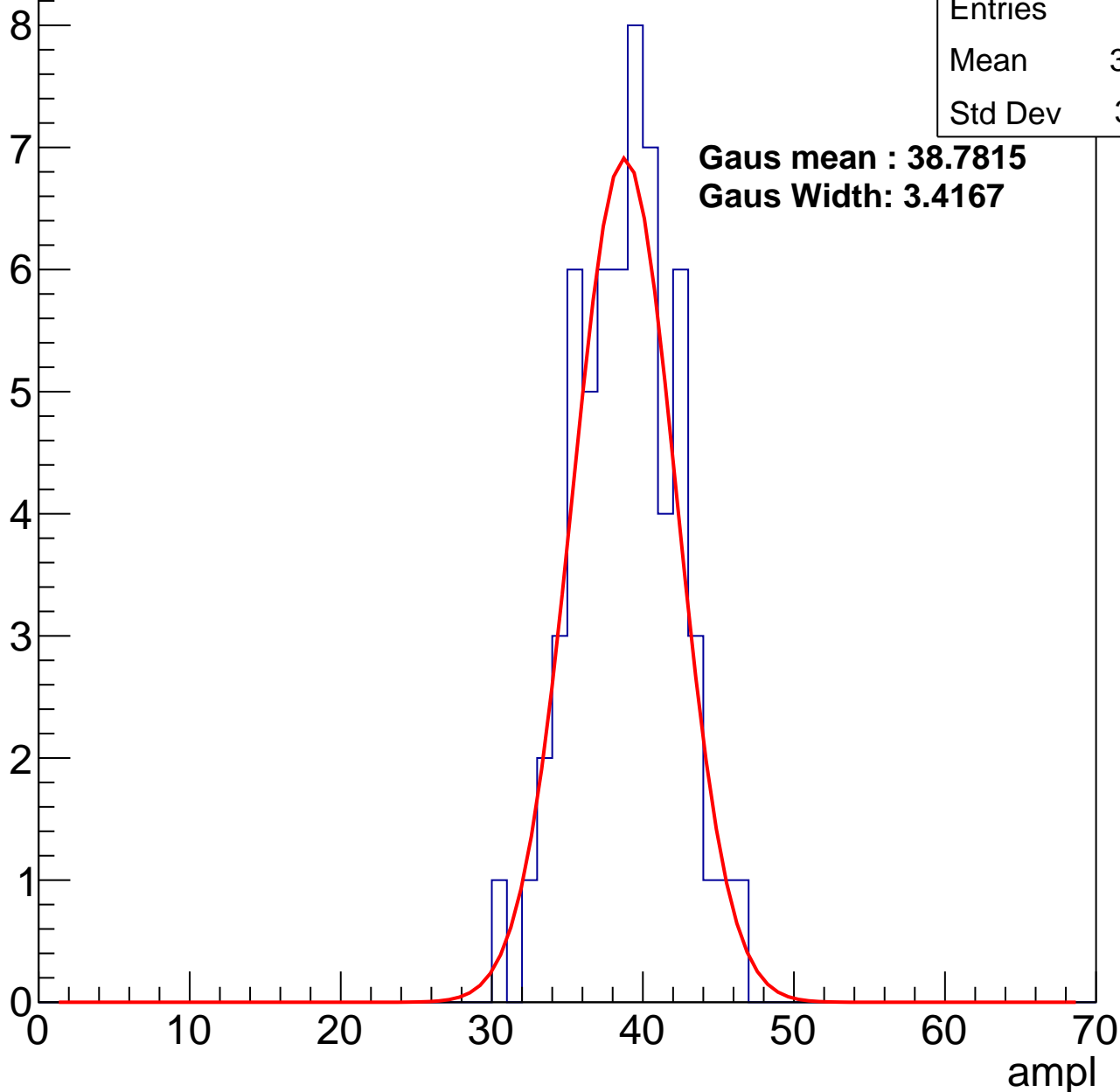
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.39
Std Dev	3.291

Gaus mean : 38.7815

Gaus Width: 3.4167



B0L002S, U12-ch1, adc2

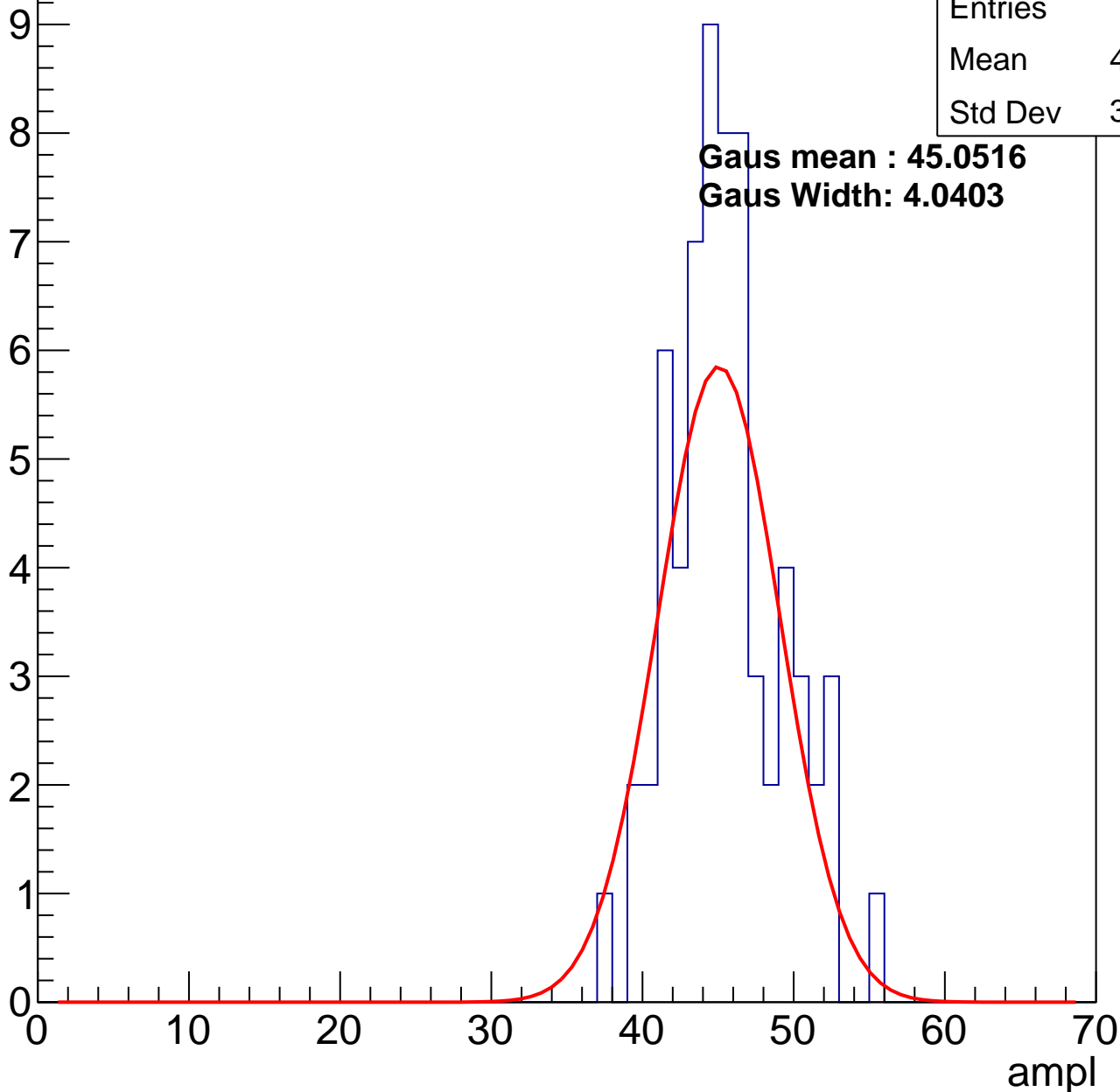
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	45.08
Std Dev	3.618

Gaus mean : 45.0516

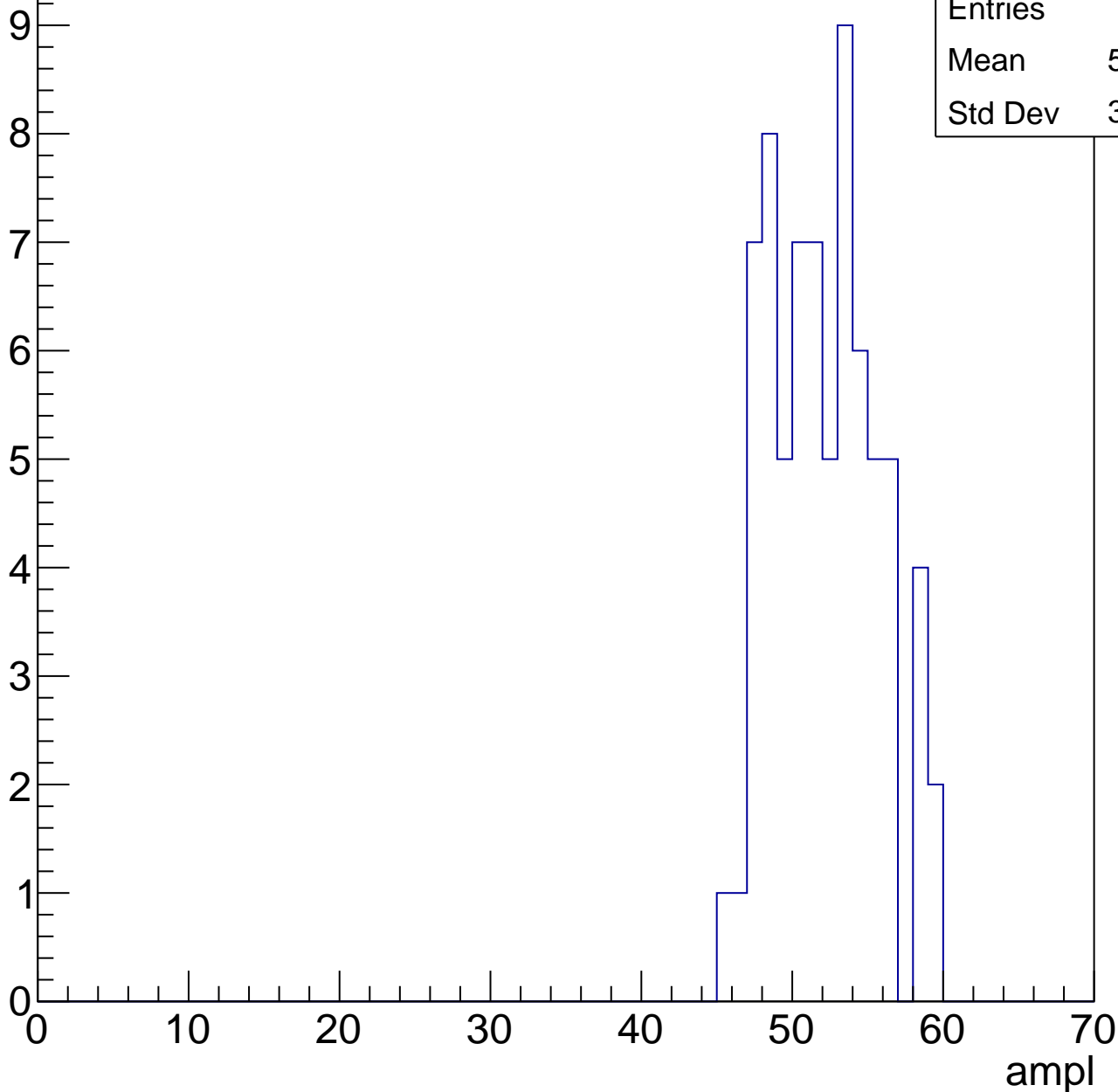
Gaus Width: 4.0403



B0L002S, U12-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

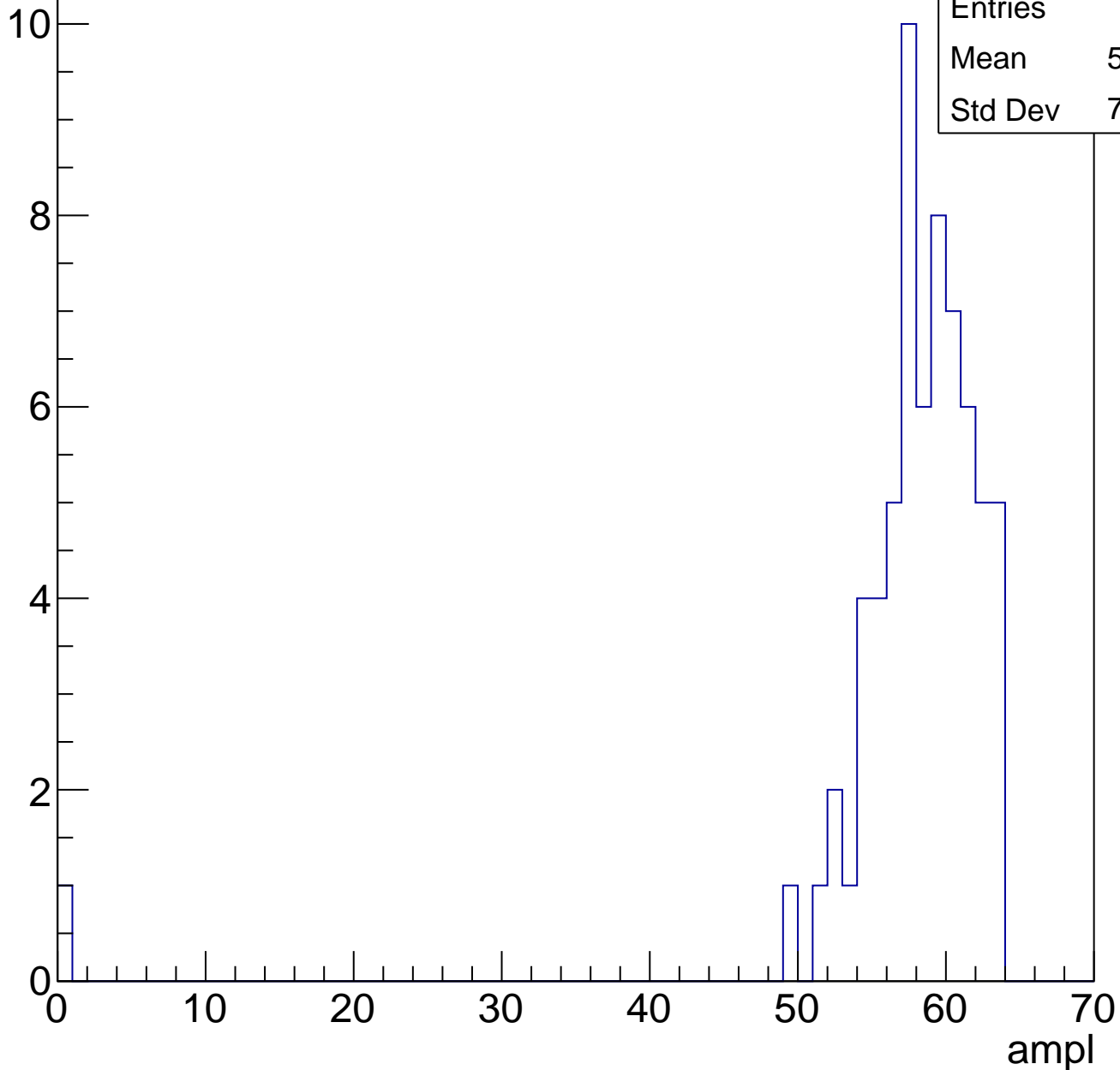


B0L002S, U12-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	57.18
Std Dev	7.758

Entry

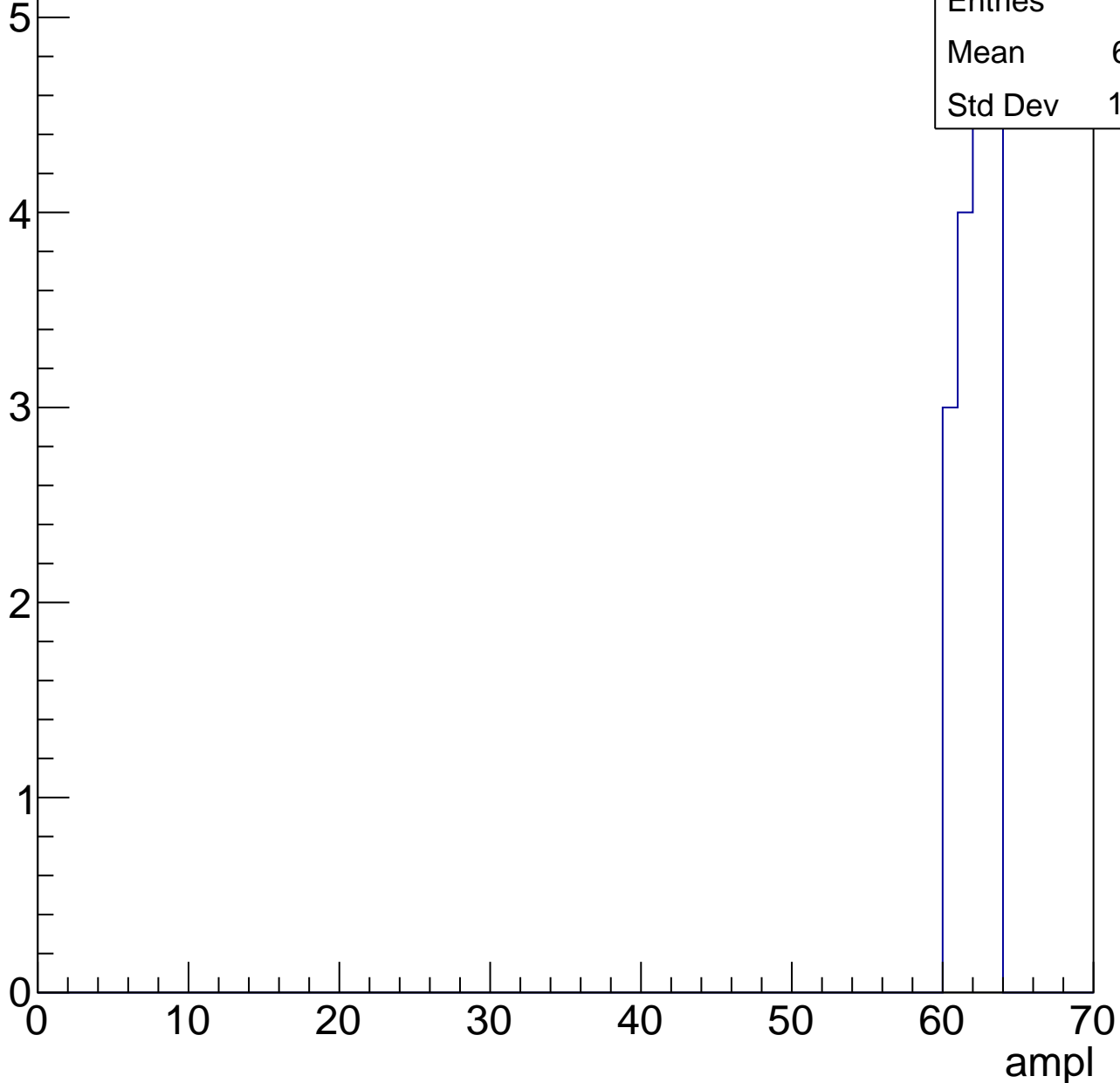


B0L002S, U12-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	61.71
Std Dev	1.072



B0L002S, U12-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch2, adc0

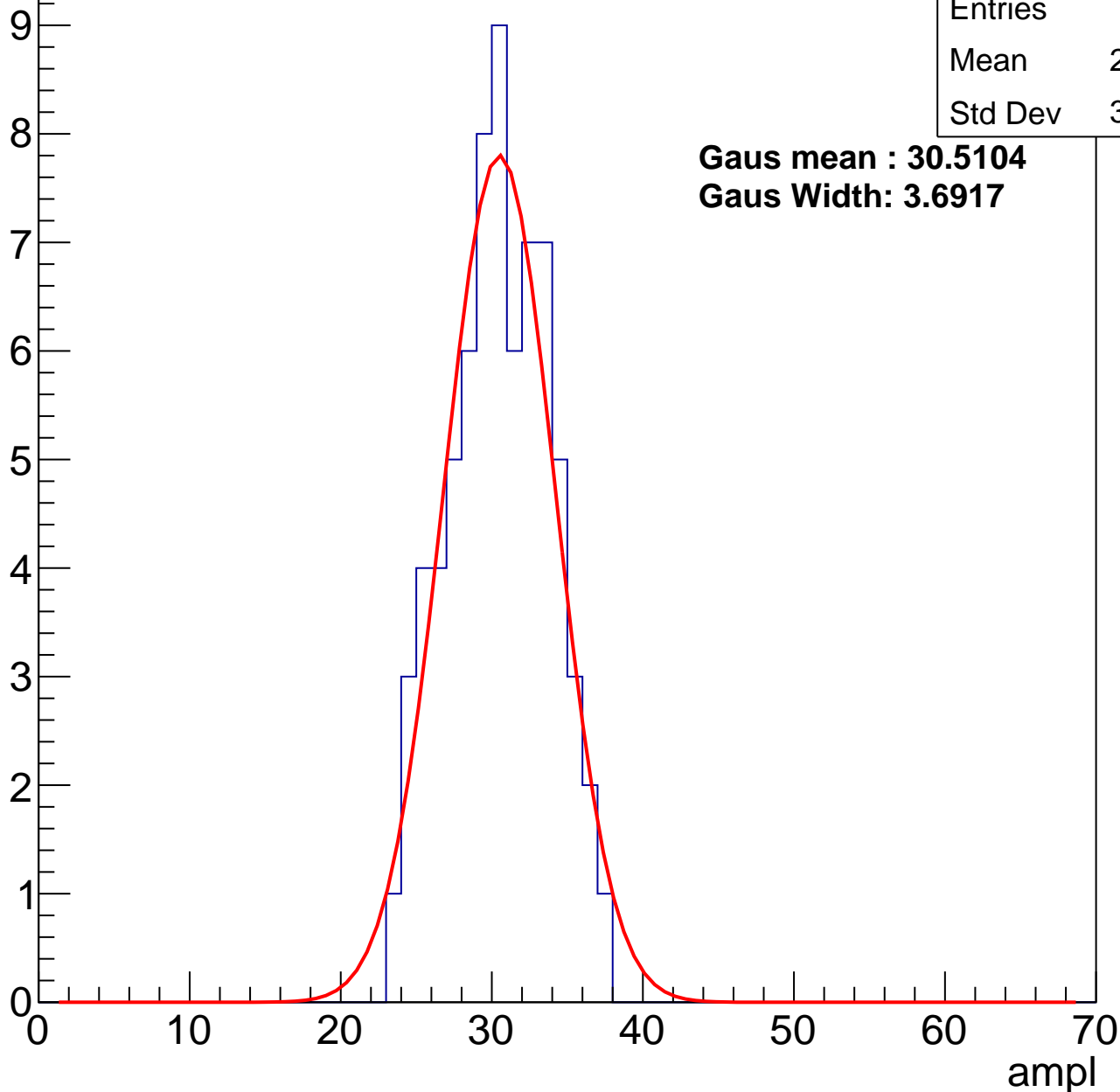
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.99
Std Dev	3.295

Gaus mean : 30.5104

Gaus Width: 3.6917



B0L002S, U12-ch2, adc1

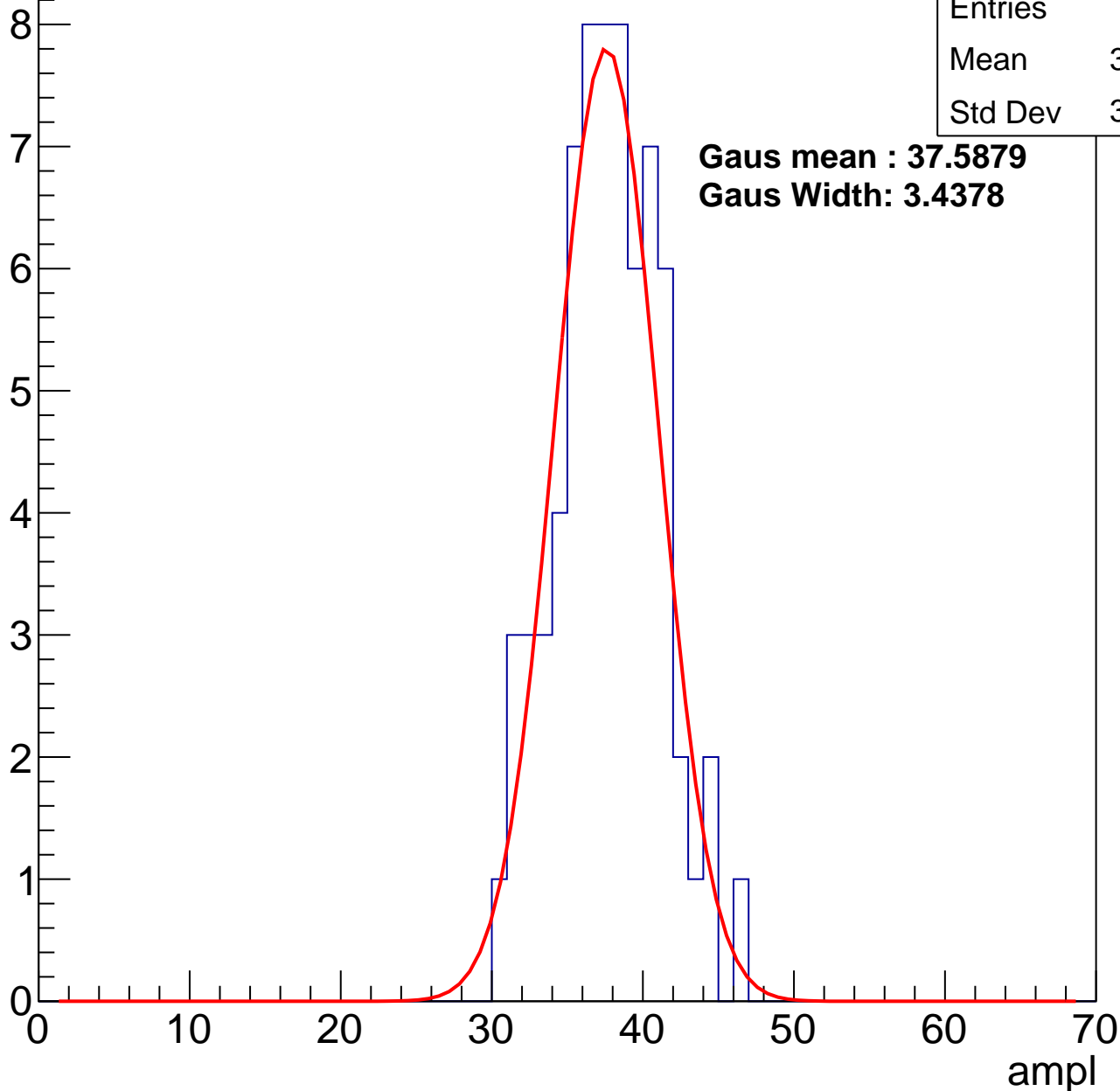
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.26
Std Dev	3.388

Gaus mean : 37.5879

Gaus Width: 3.4378



B0L002S, U12-ch2, adc2

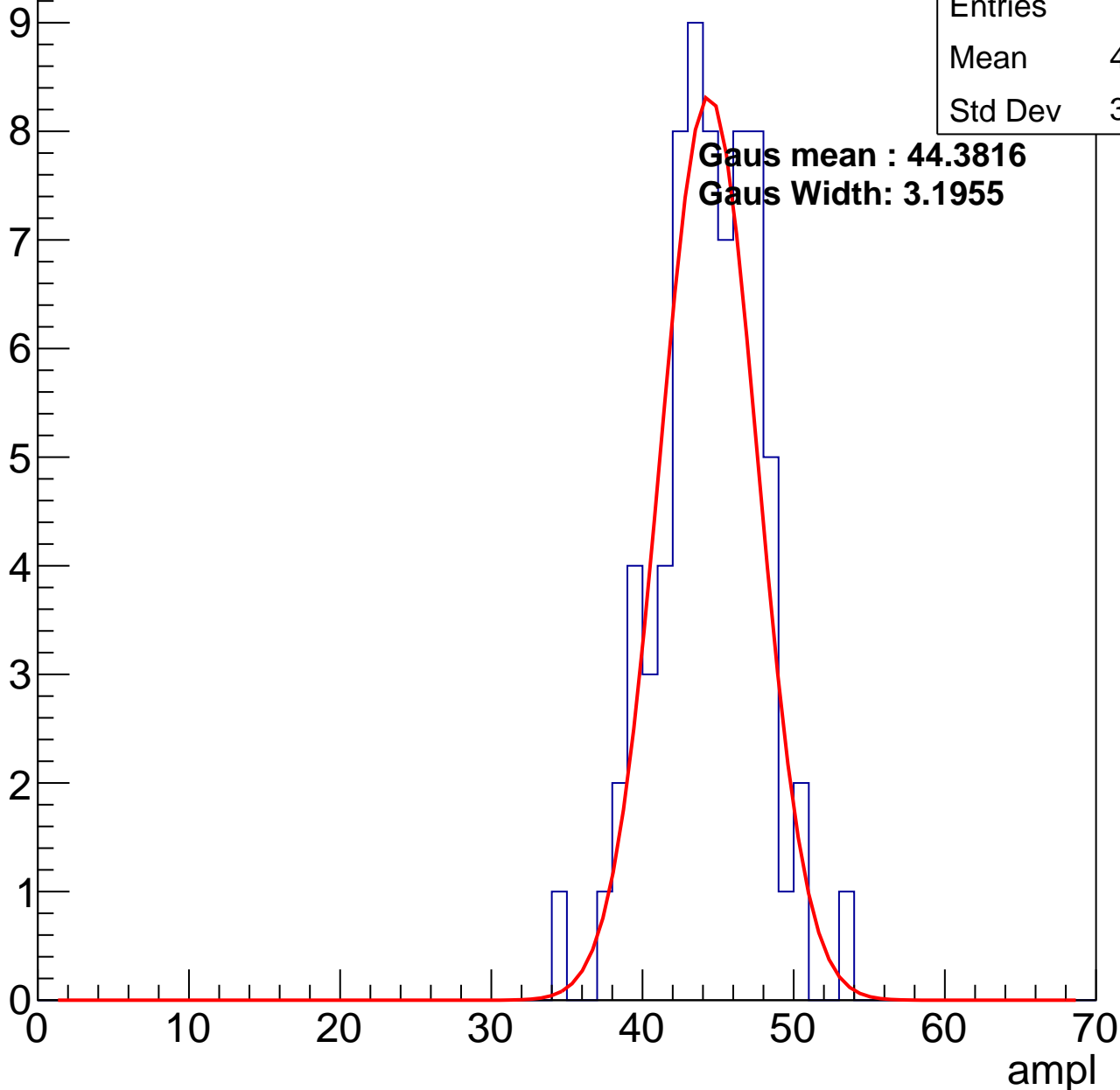
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.93
Std Dev	3.364

Gaus mean : 44.3816

Gaus Width: 3.1955

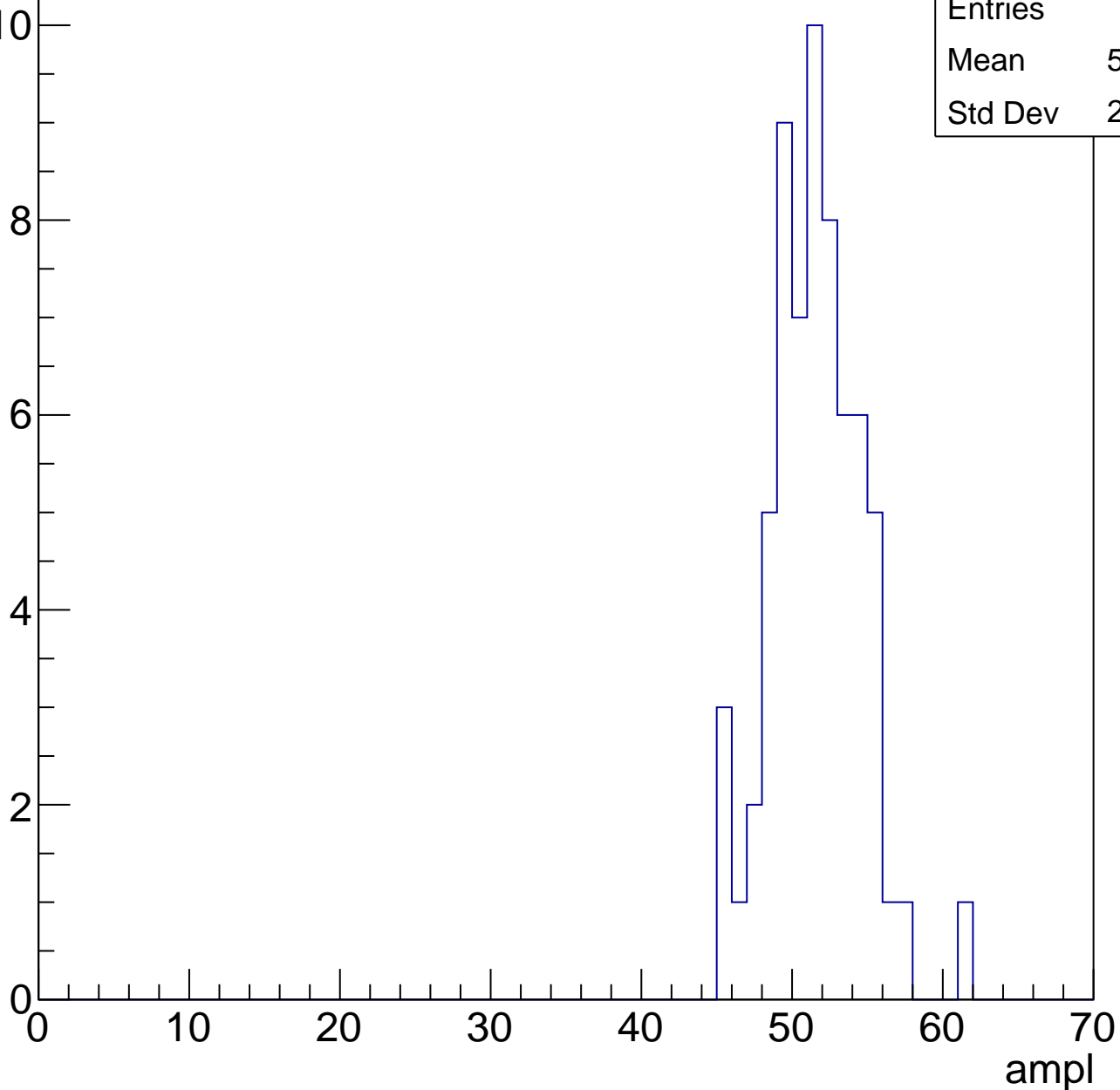


B0L002S, U12-ch2, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

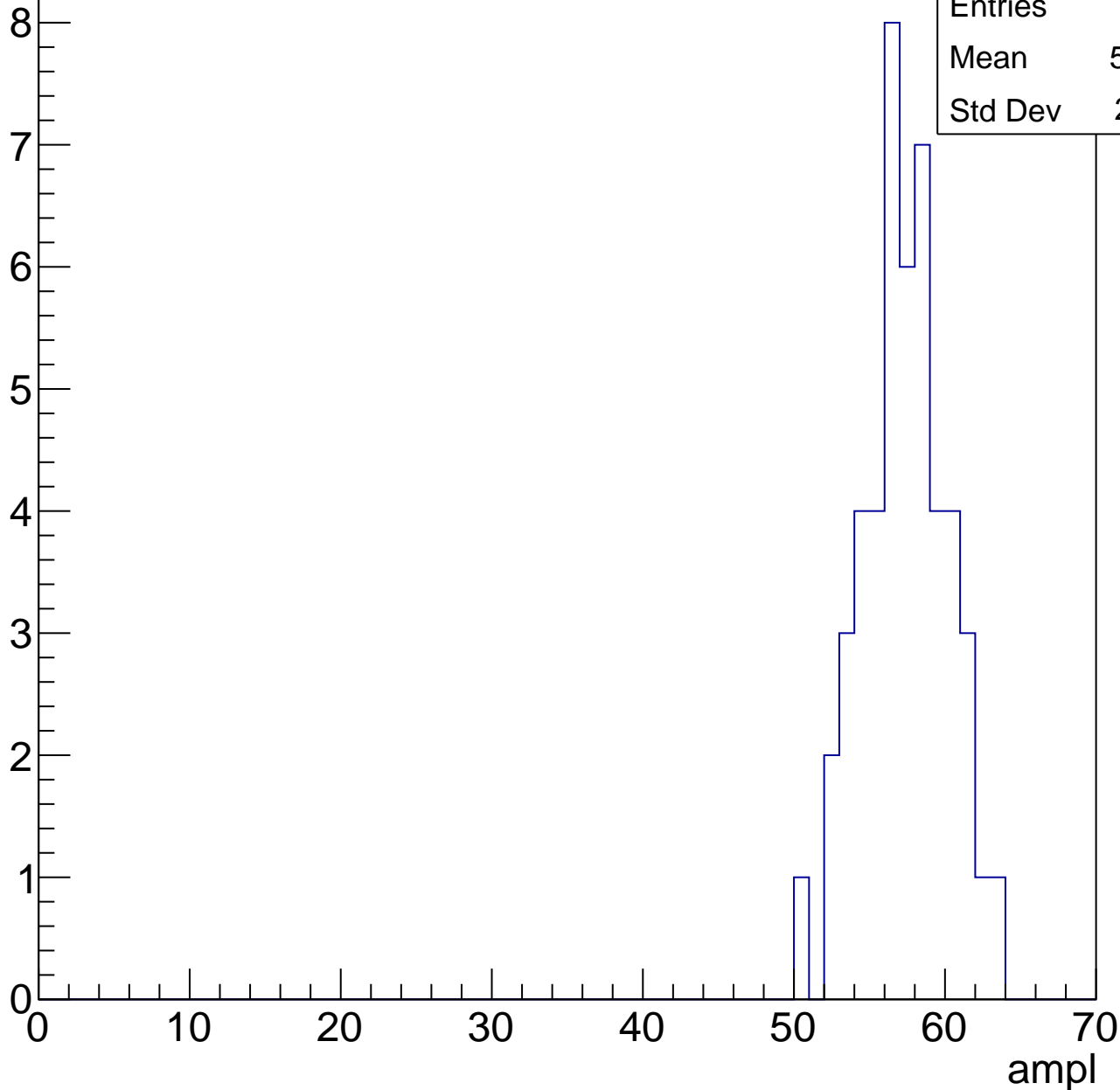
Entries	65
Mean	51.12
Std Dev	2.985



B0L002S, U12-ch2, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

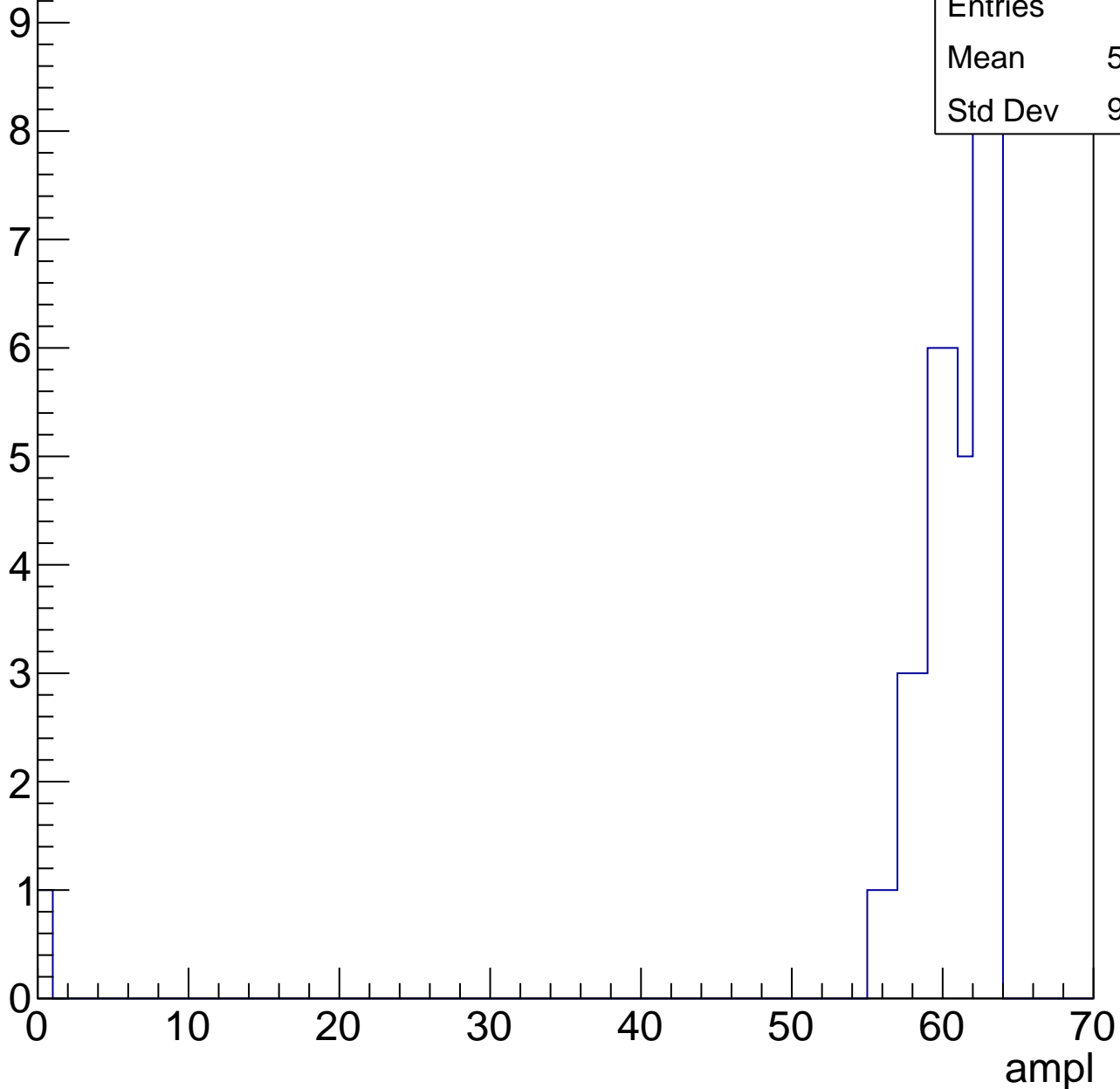


B0L002S, U12-ch2, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	59.02
Std Dev	9.355



B0L002S, U12-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch3, adc0

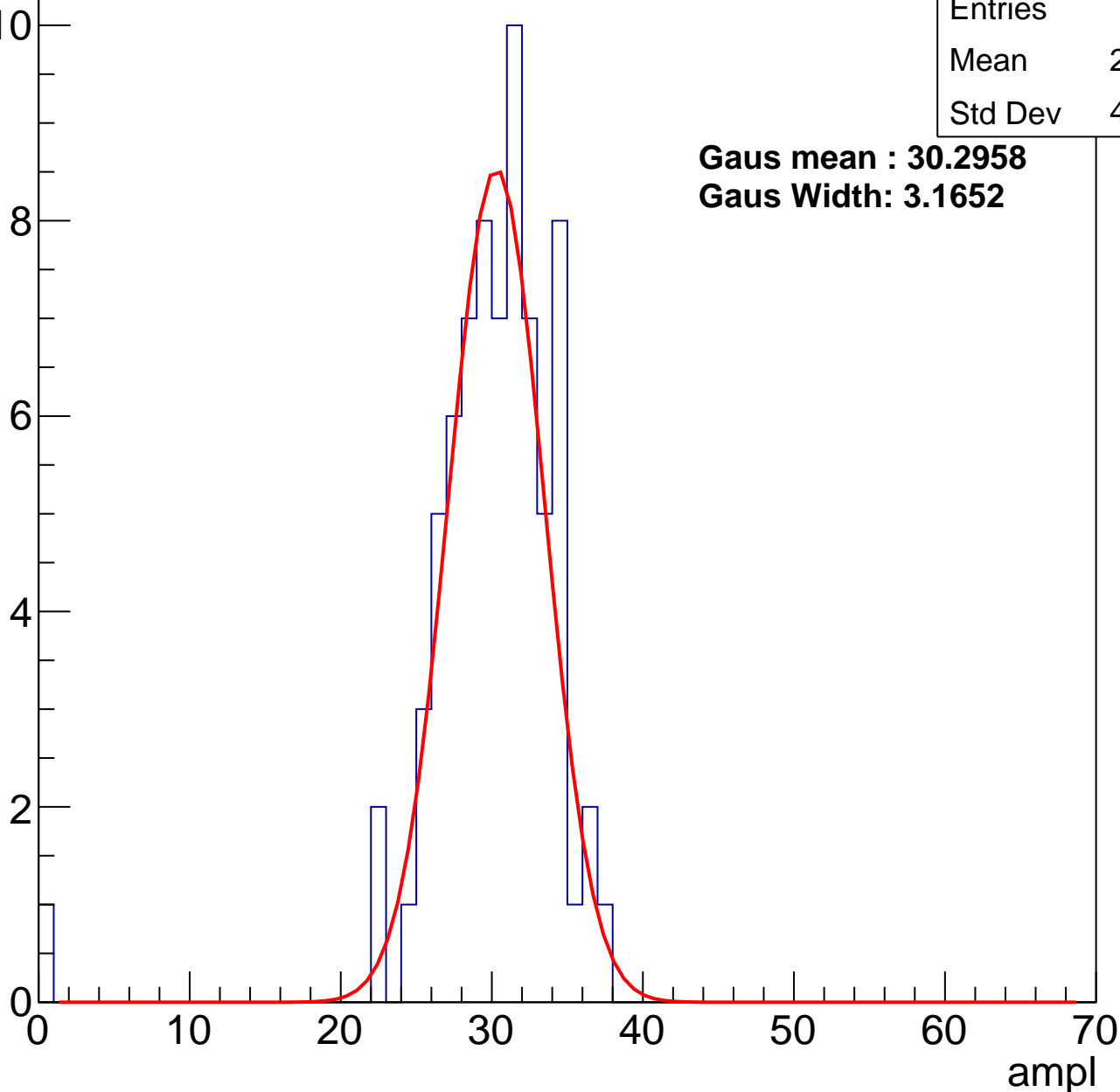
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	29.57
Std Dev	4.725

Gaus mean : 30.2958

Gaus Width: 3.1652



B0L002S, U12-ch3, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	38.16
Std Dev	3.427

Gaus mean : 38.8335

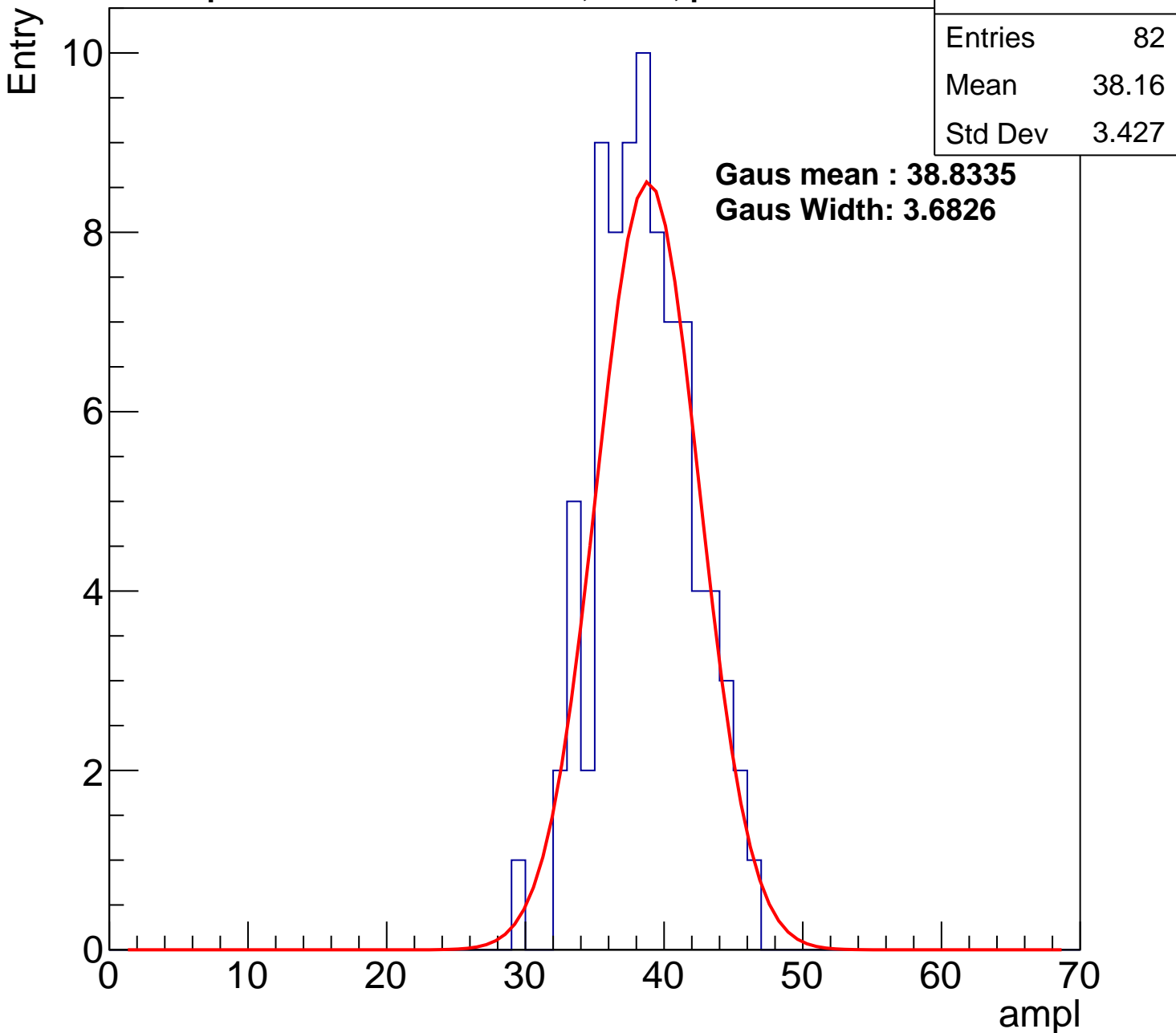
Gaus Width: 3.6826

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch3, adc2

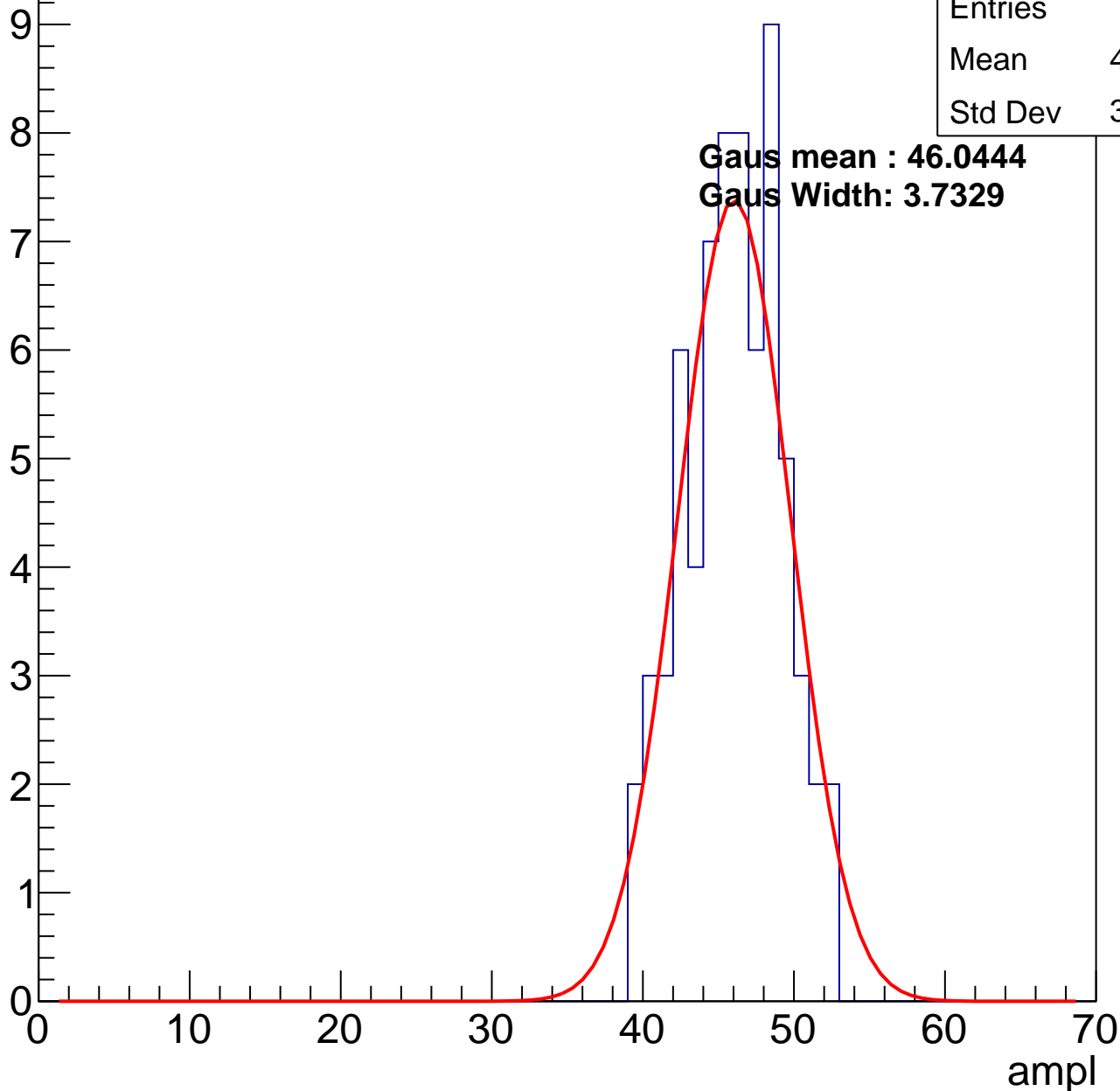
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.53
Std Dev	3.188

Gaus mean : 46.0444

Gaus Width: 3.7329

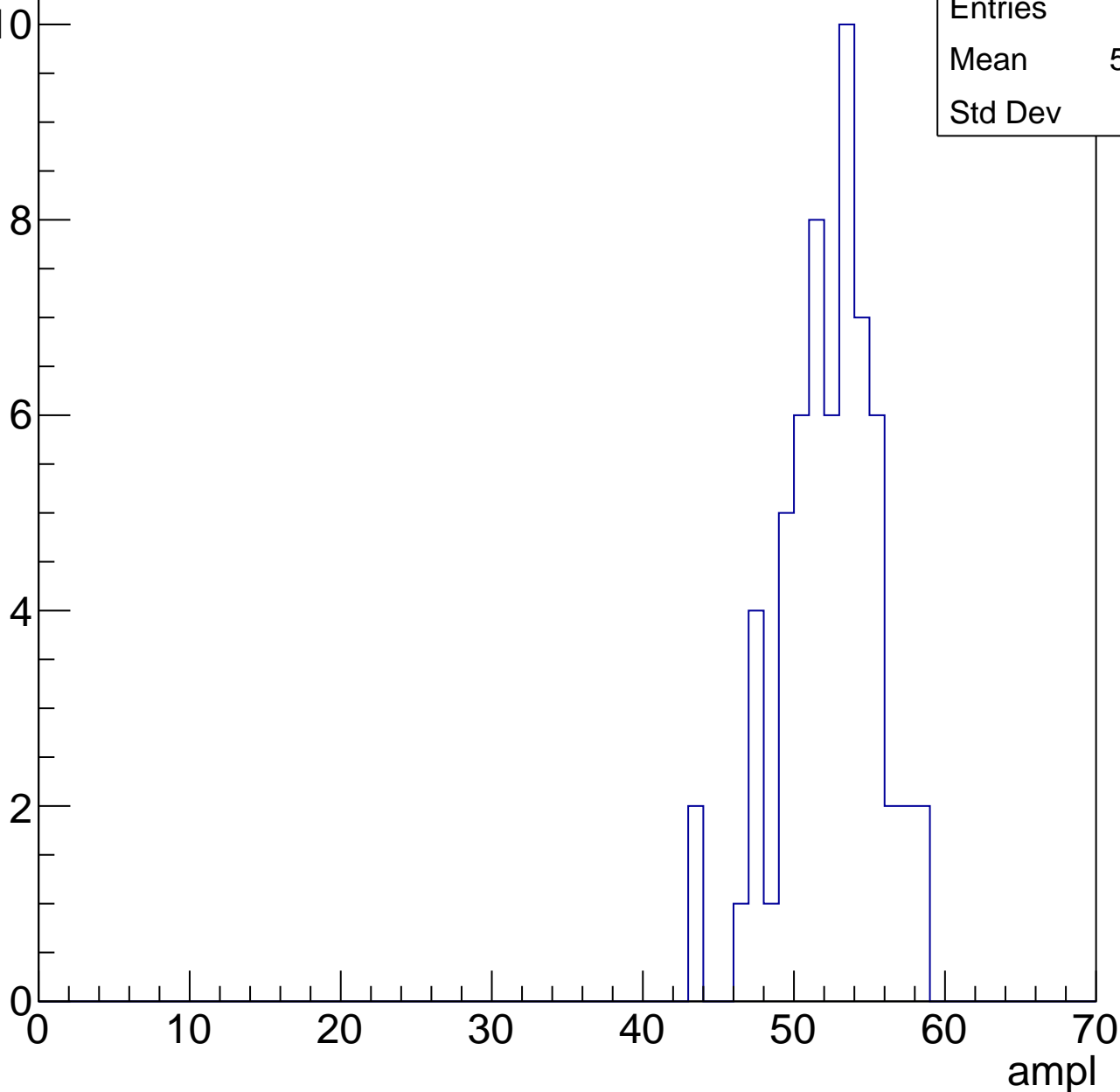


B0L002S, U12-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.82
Std Dev	3.2

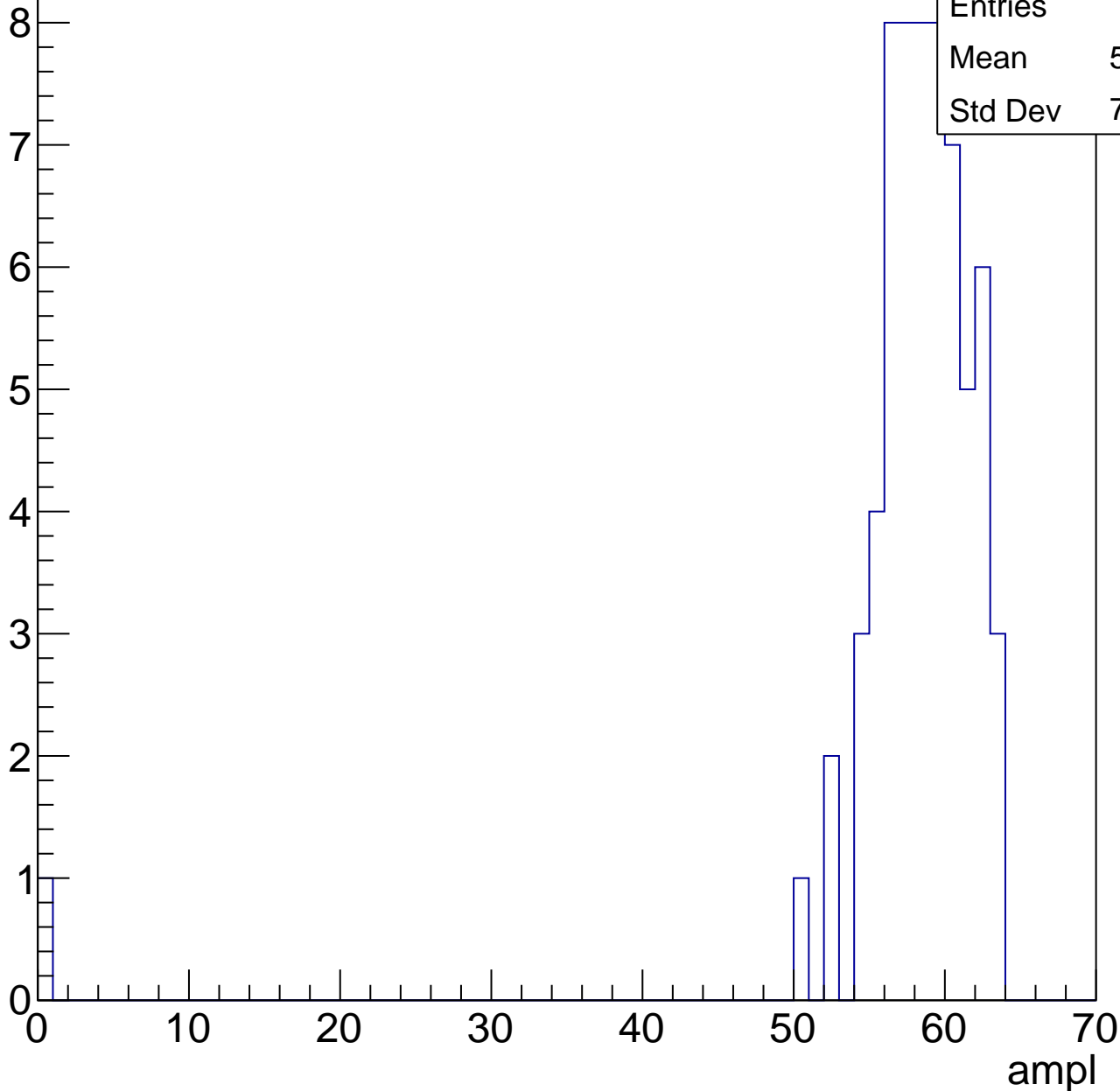


B0L002S, U12-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	57.22
Std Dev	7.743

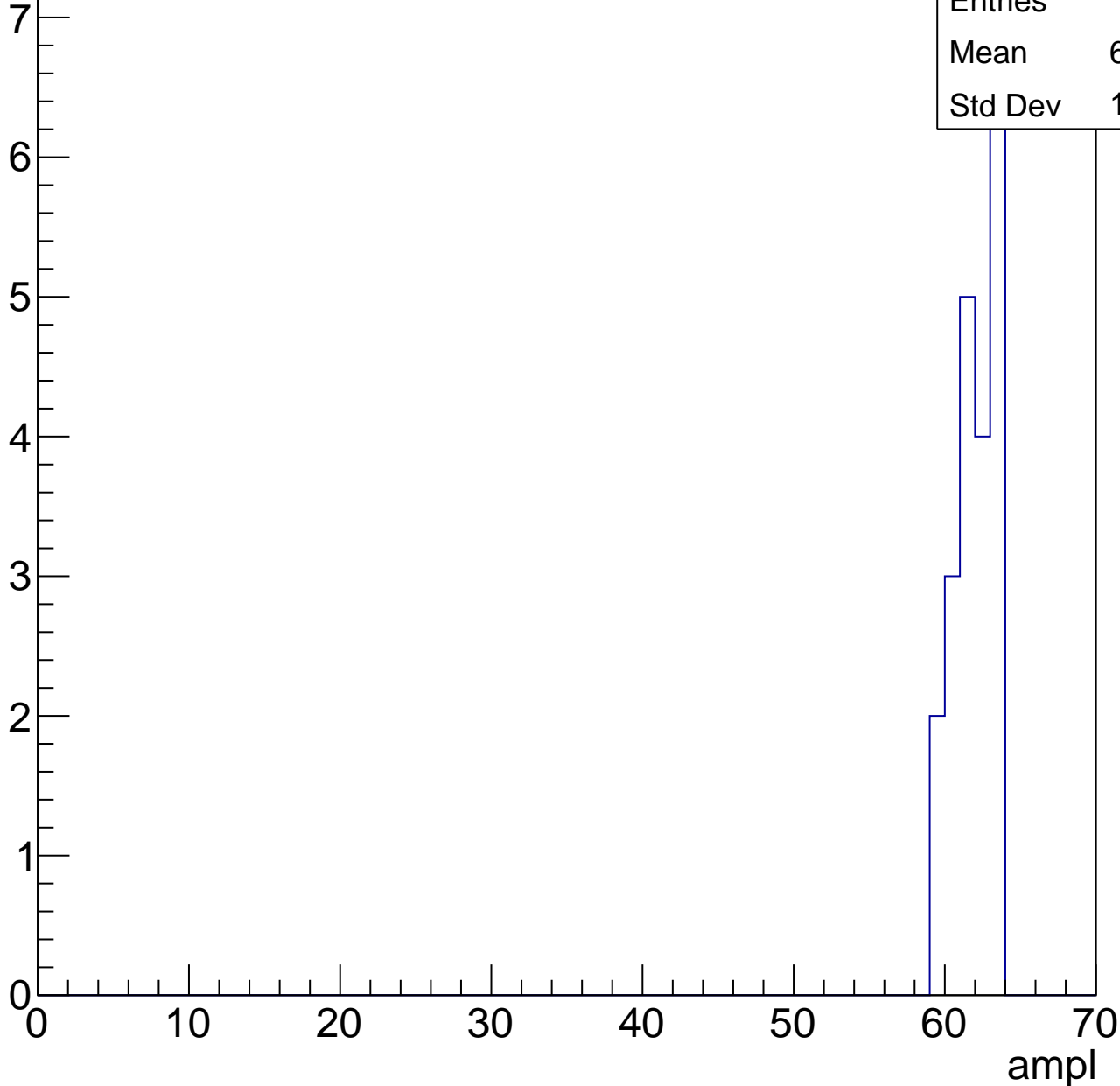


B0L002S, U12-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	61.52
Std Dev	1.332



B0L002S, U12-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch4, adc0

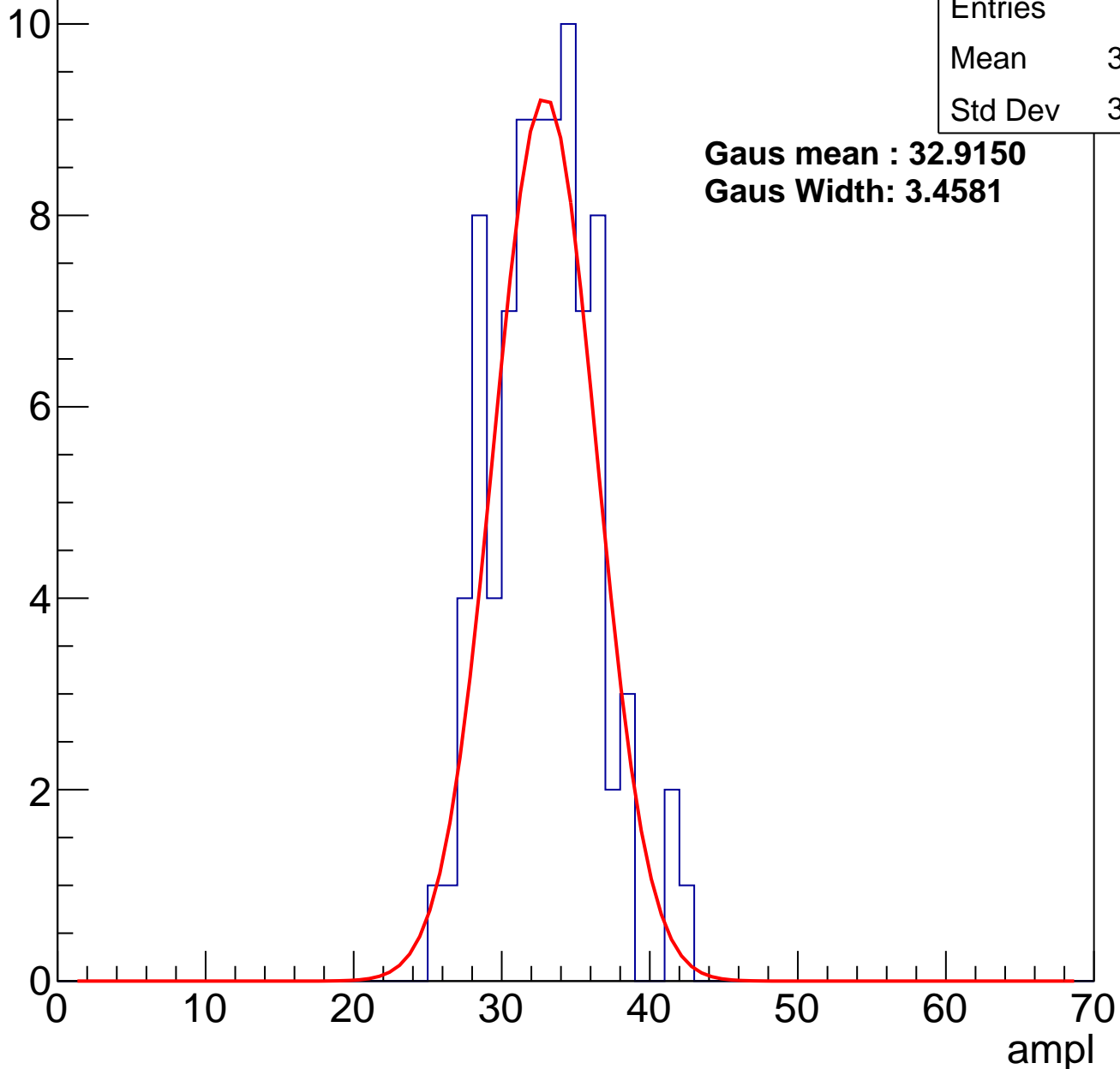
calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	32.45
Std Dev	3.479

Gaus mean : 32.9150

Gaus Width: 3.4581

Entry



B0L002S, U12-ch4, adc1

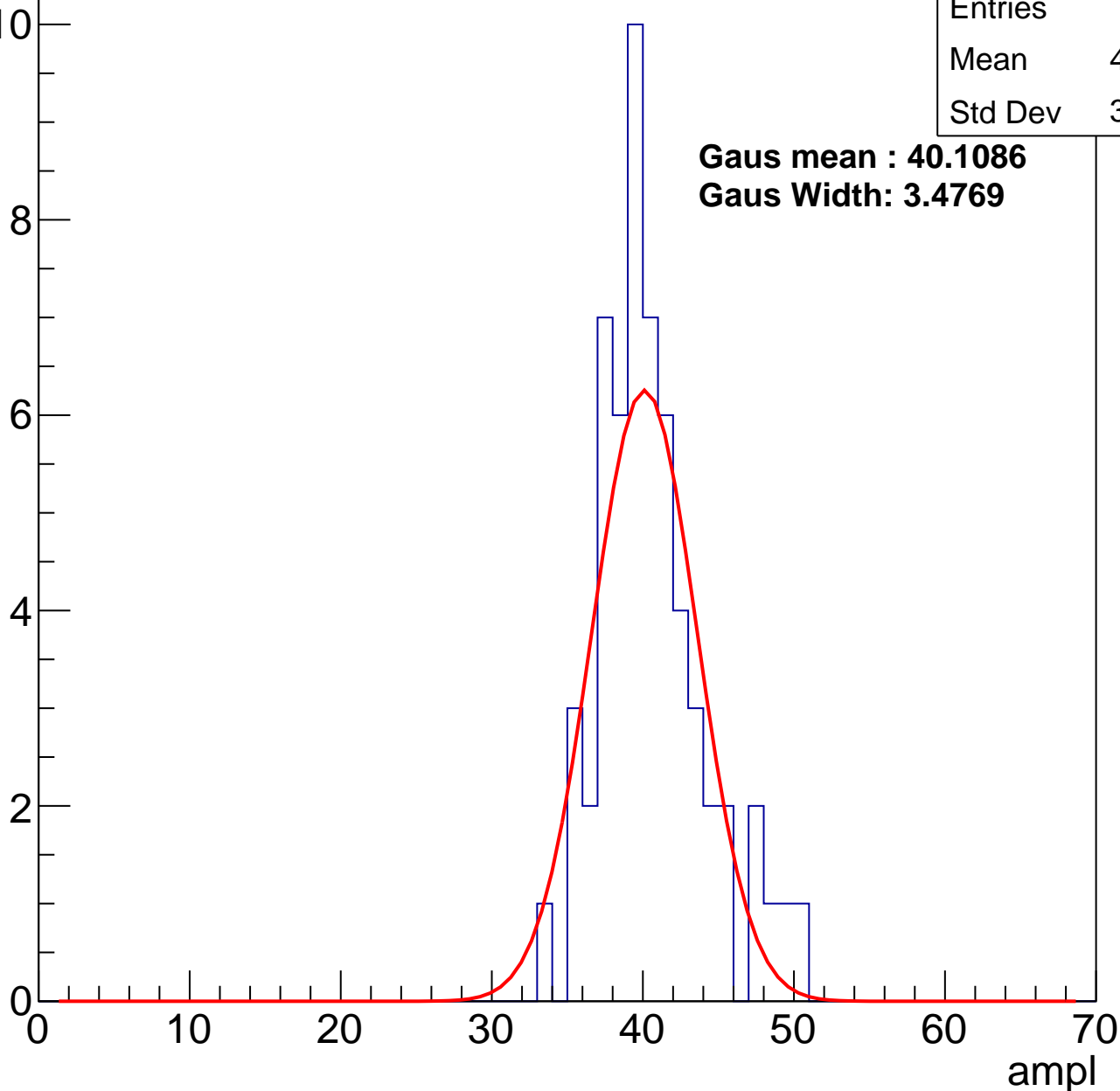
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	40.16
Std Dev	3.537

Gaus mean : 40.1086

Gaus Width: 3.4769



B0L002S, U12-ch4, adc2

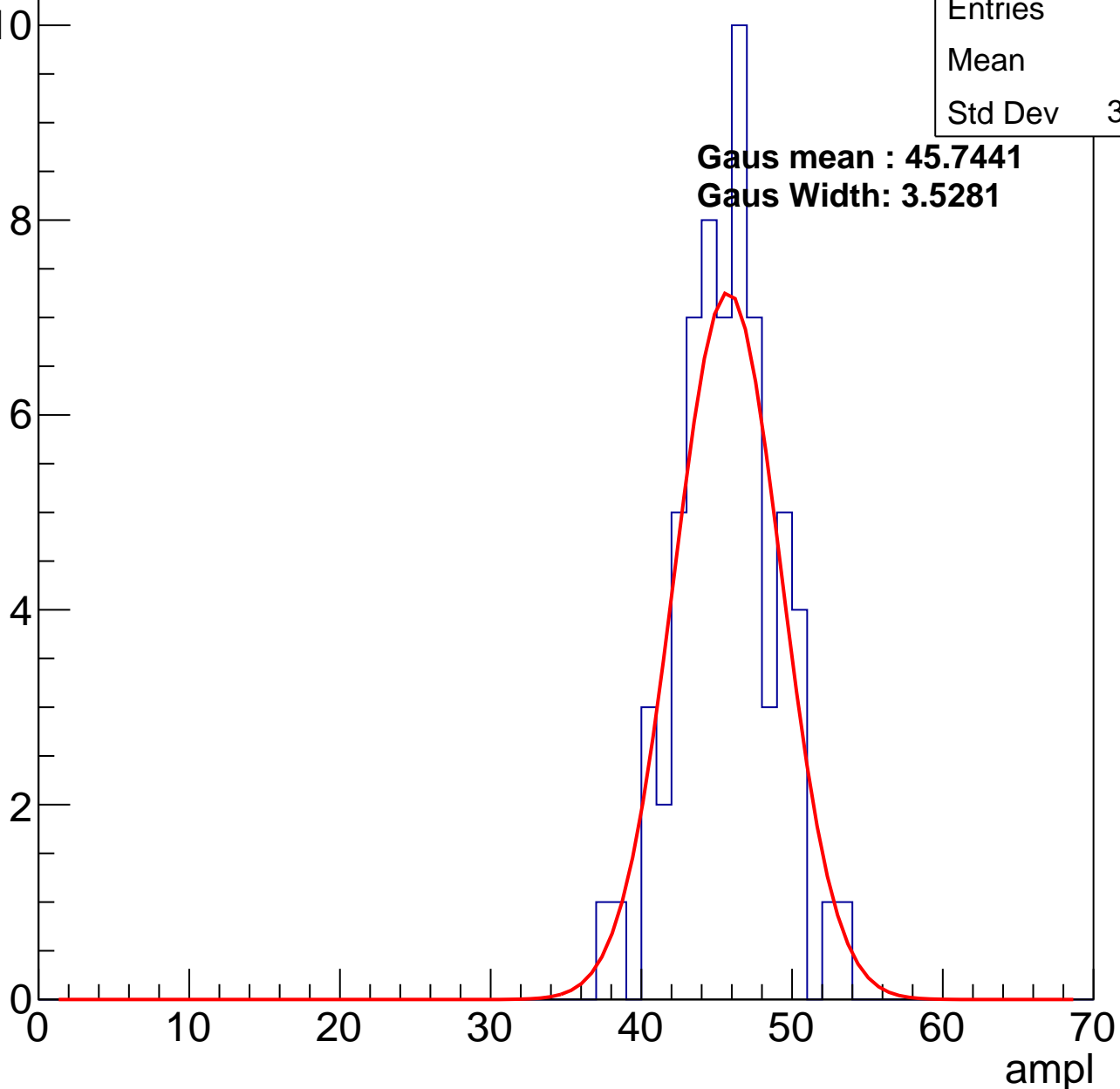
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	45.2
Std Dev	3.178

Gaus mean : 45.7441

Gaus Width: 3.5281

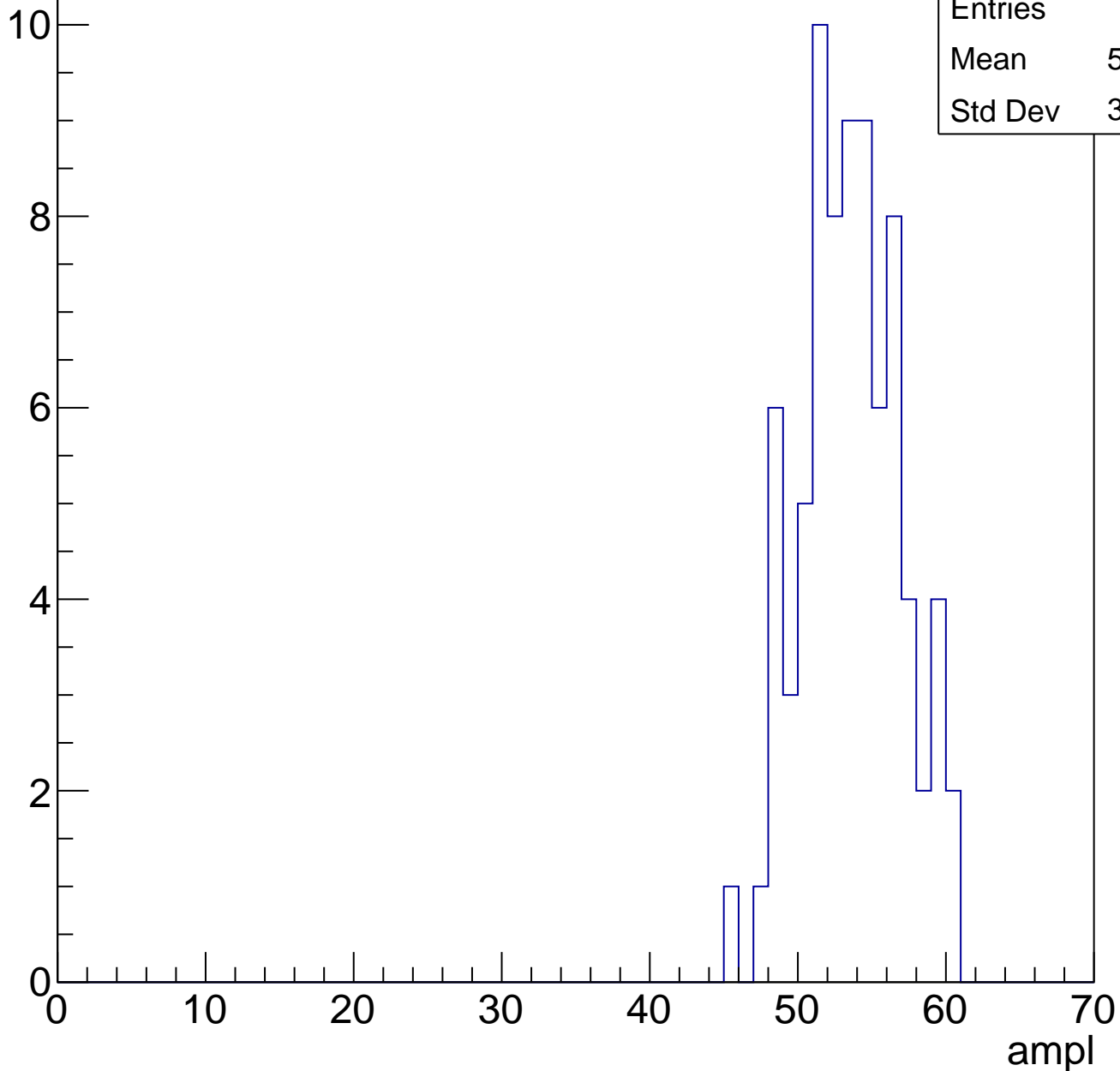


B0L002S, U12-ch4, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	53.13
Std Dev	3.303

Entry

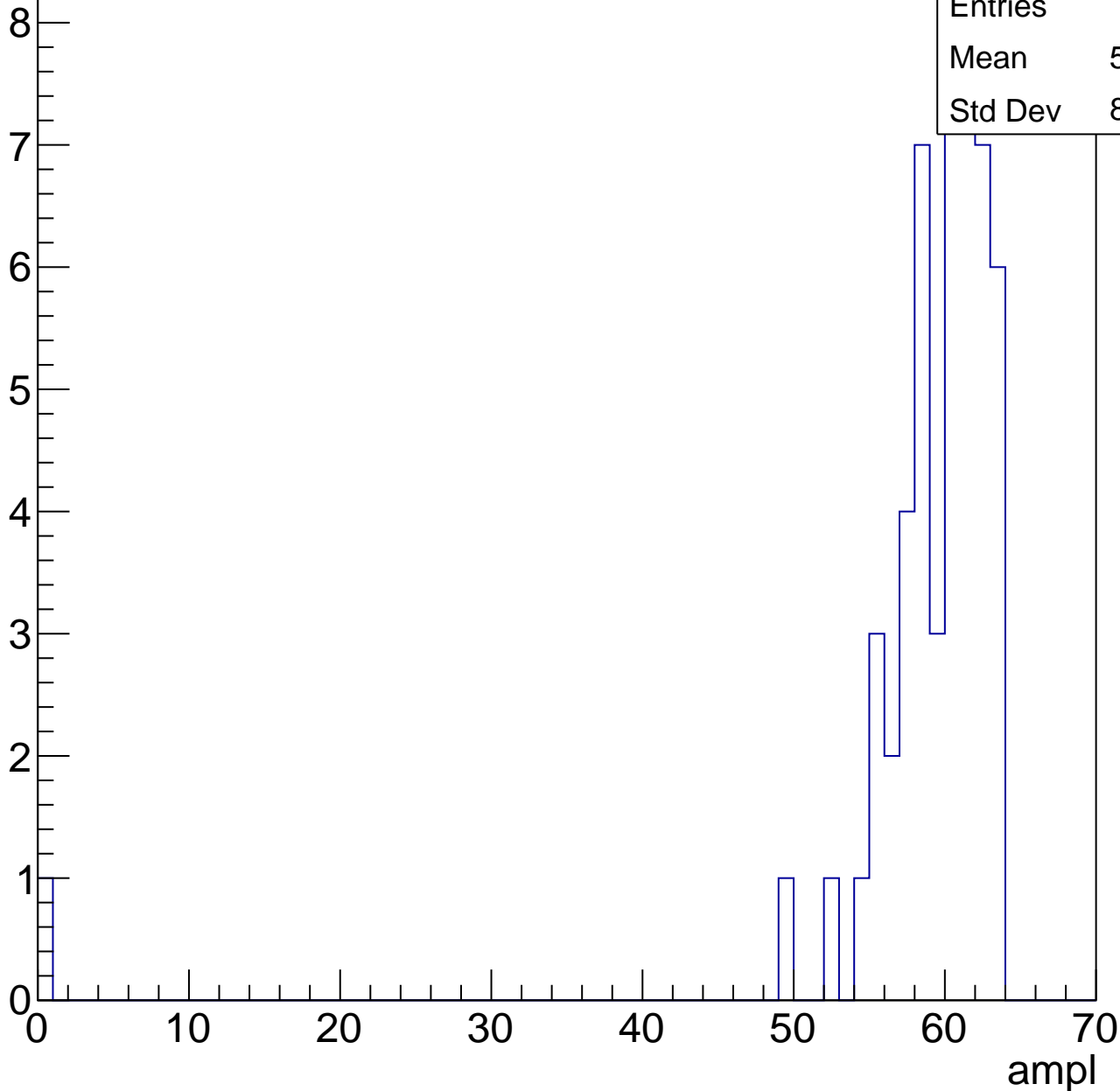


B0L002S, U12-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

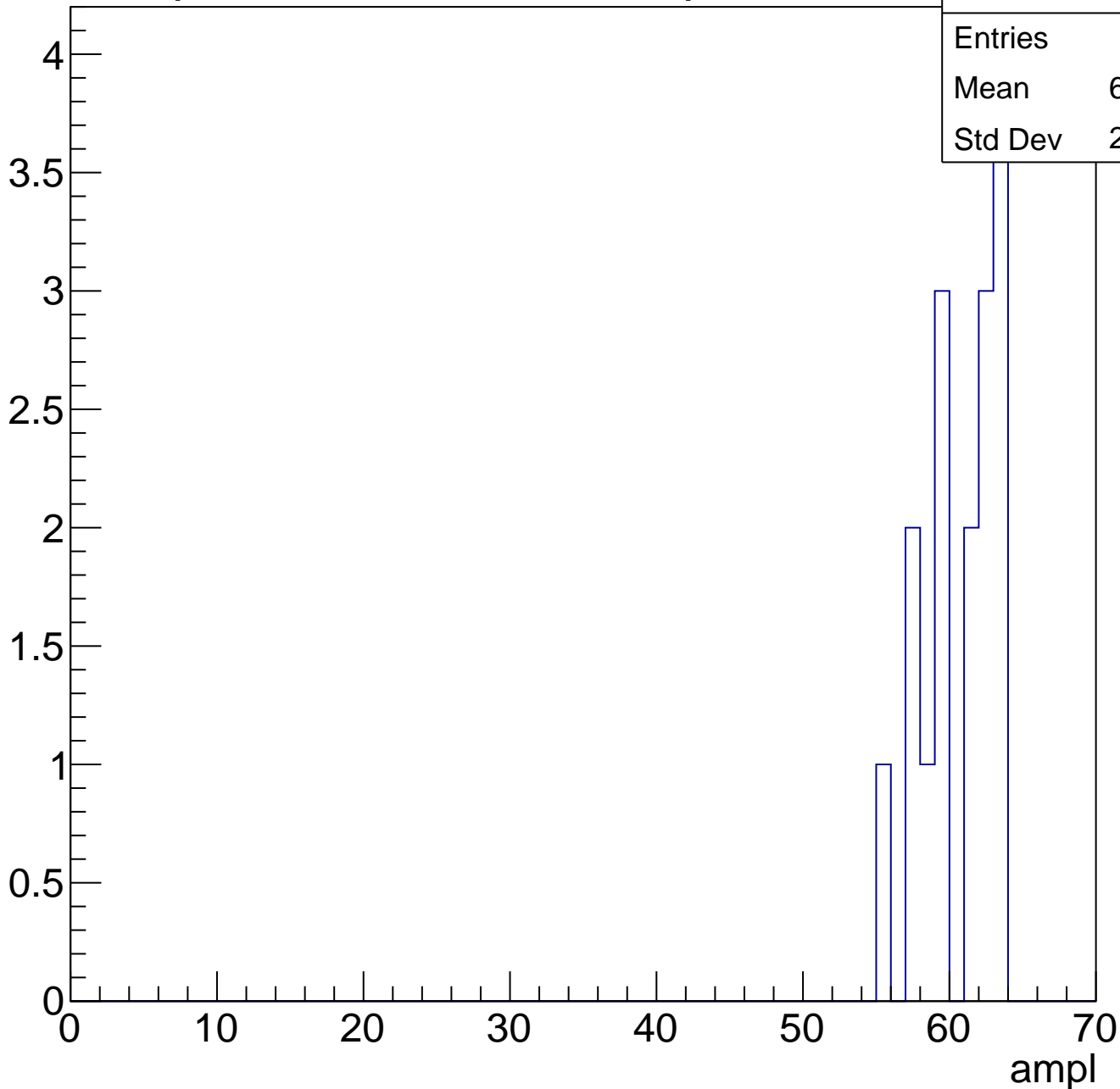
Entries	52
Mean	58.13
Std Dev	8.665



B0L002S, U12-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch5, adc0

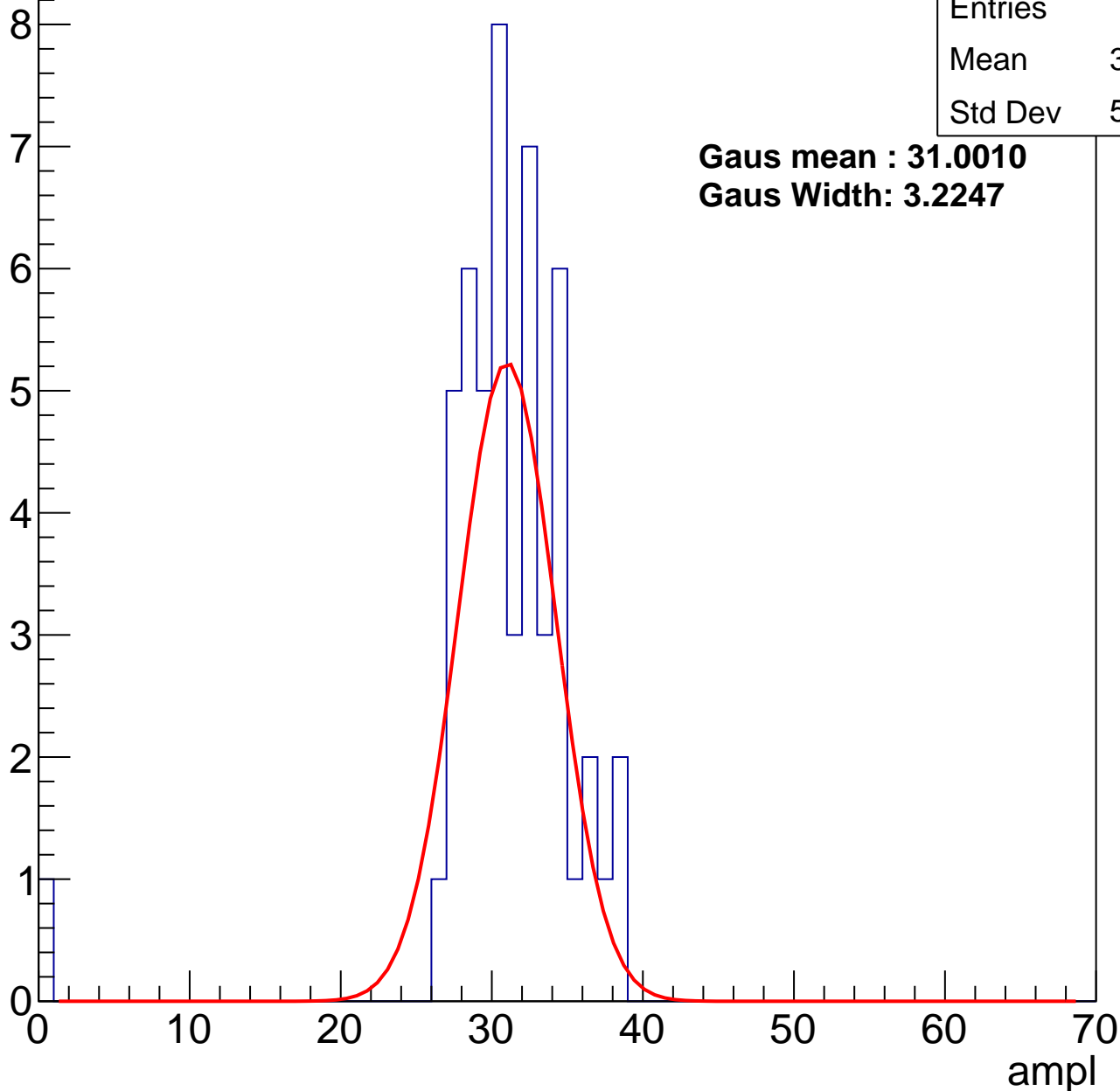
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	30.47
Std Dev	5.252

Gaus mean : 31.0010

Gaus Width: 3.2247



B0L002S, U12-ch5, adc1

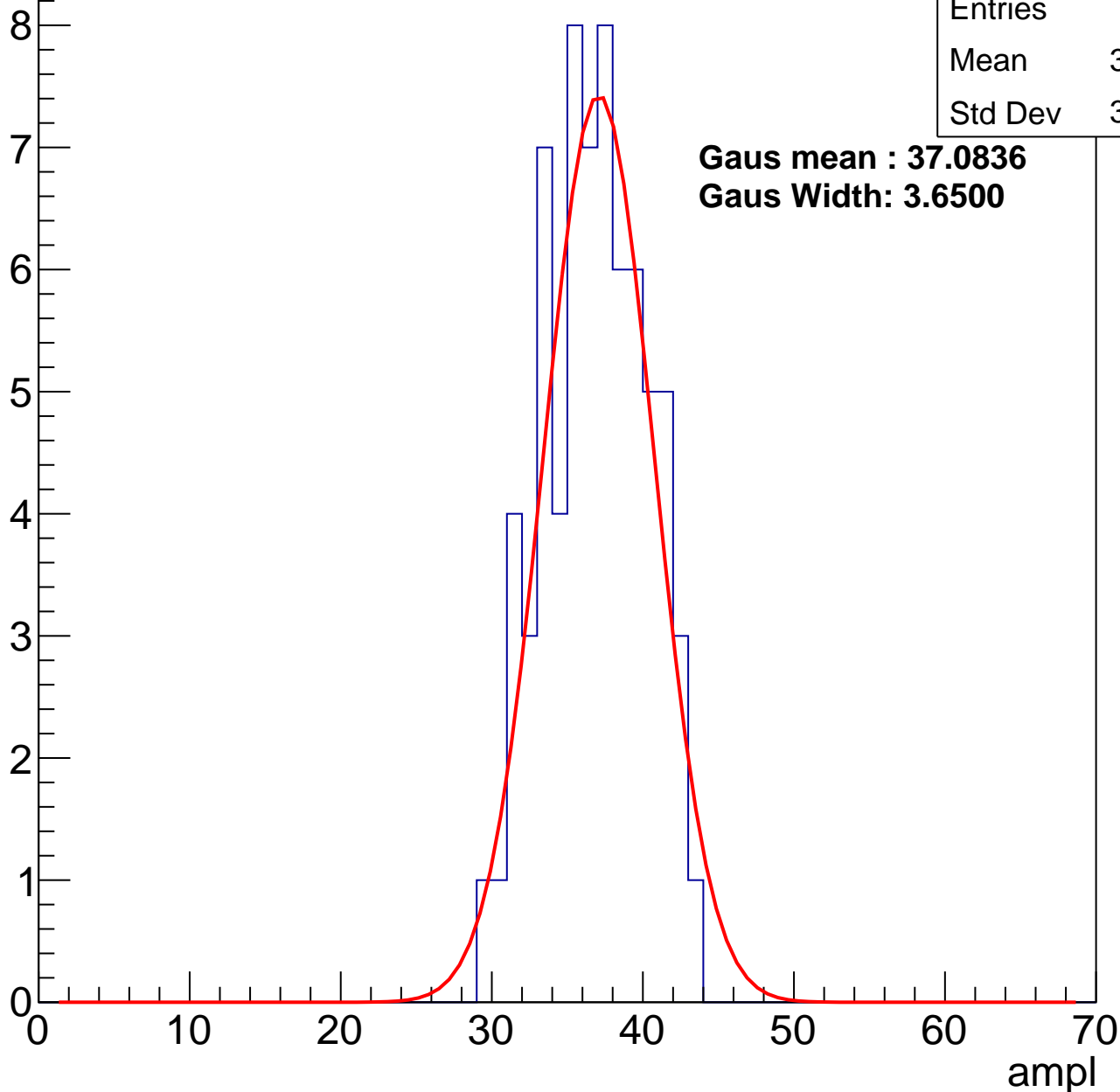
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.38
Std Dev	3.315

Gaus mean : 37.0836

Gaus Width: 3.6500



B0L002S, U12-ch5, adc2

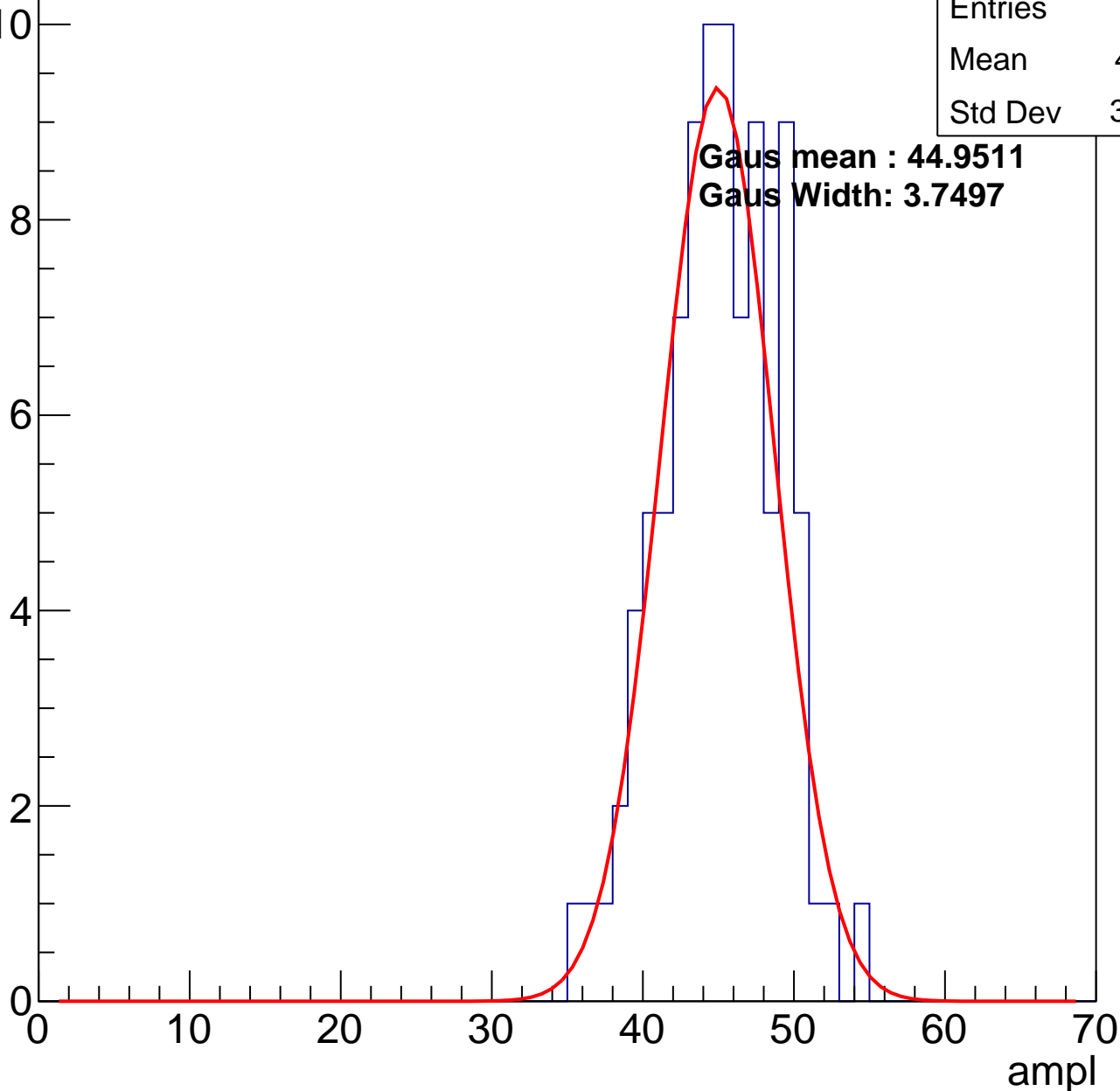
calib_packv5_042523_0143.root, FC#8, port C1

Entries	93
Mean	44.61
Std Dev	3.753

Gaus mean : 44.9511

Gaus Width: 3.7497

Entry

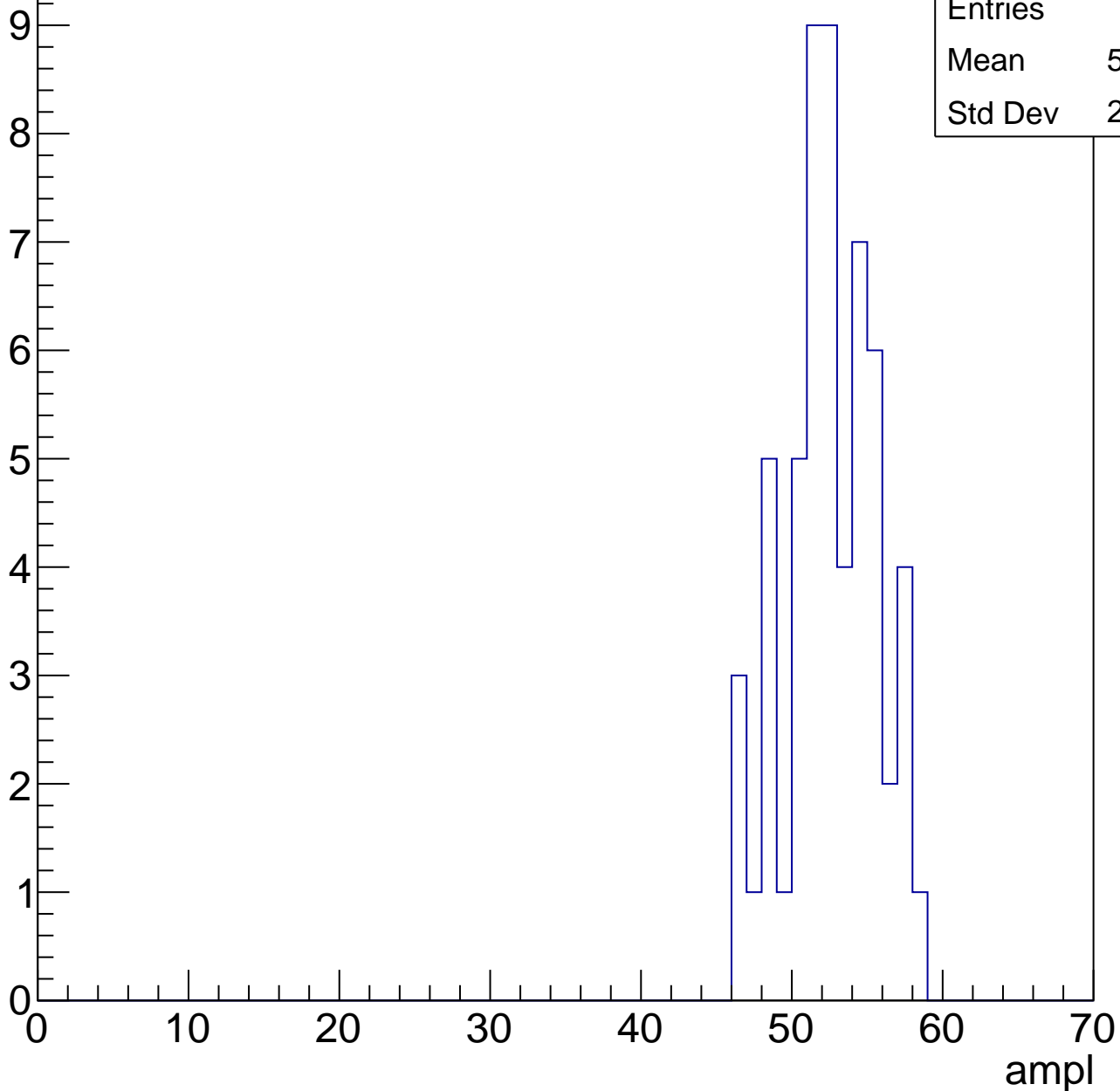


B0L002S, U12-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

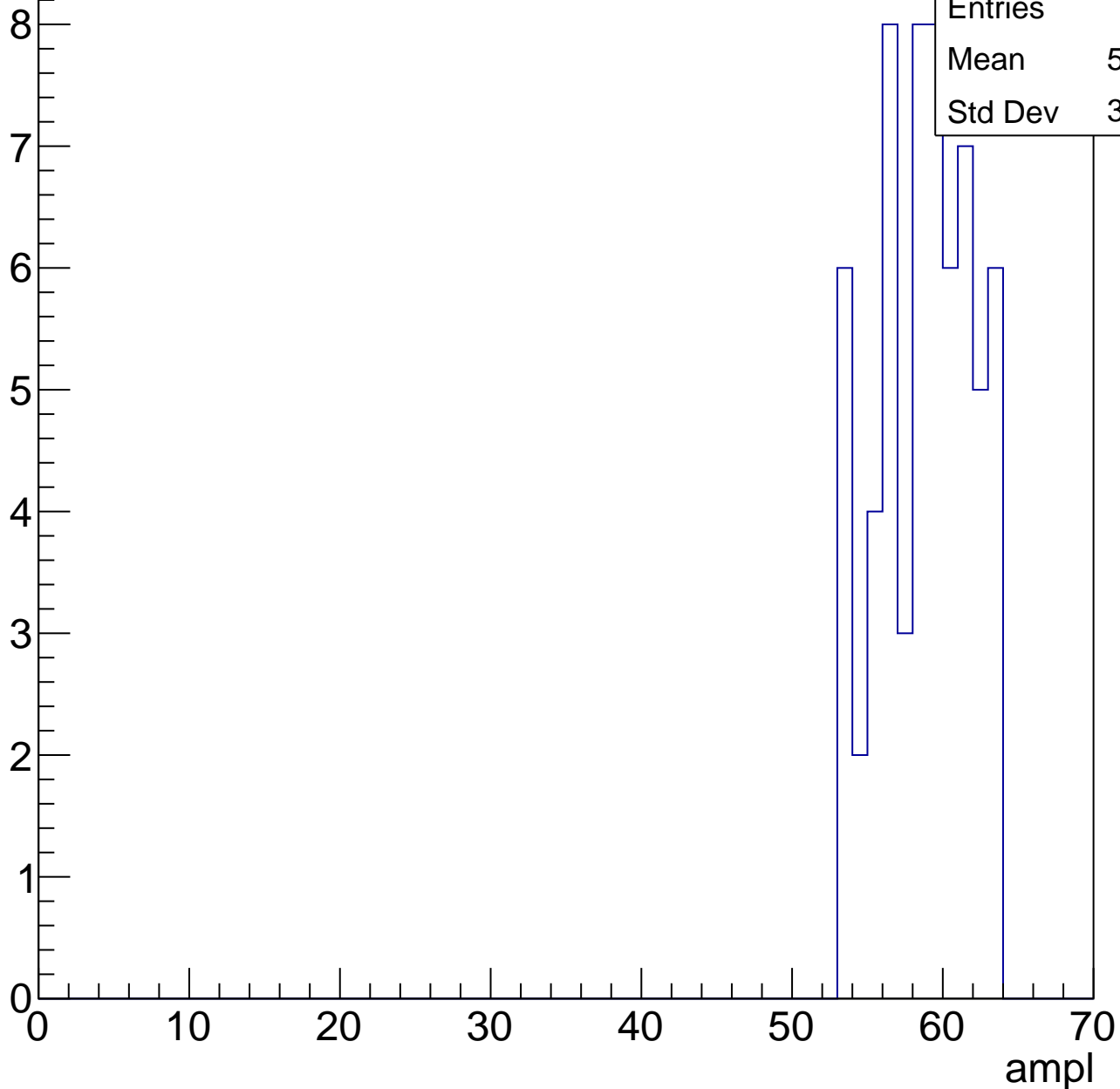
Entries	57
Mean	52.09
Std Dev	2.975



B0L002S, U12-ch5, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

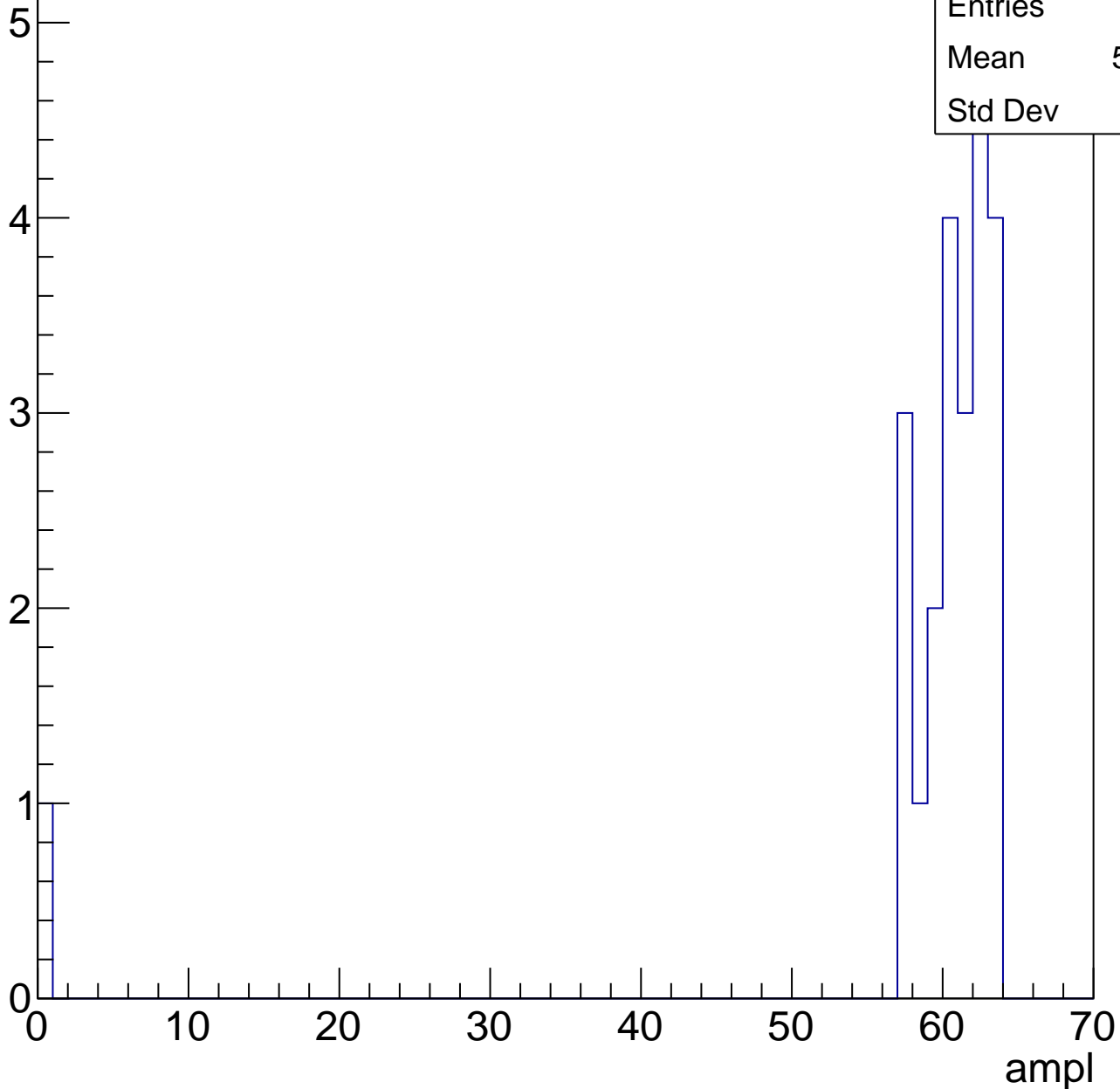


B0L002S, U12-ch5, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	57.91
Std Dev	12.5



B0L002S, U12-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U12-ch6, adc0

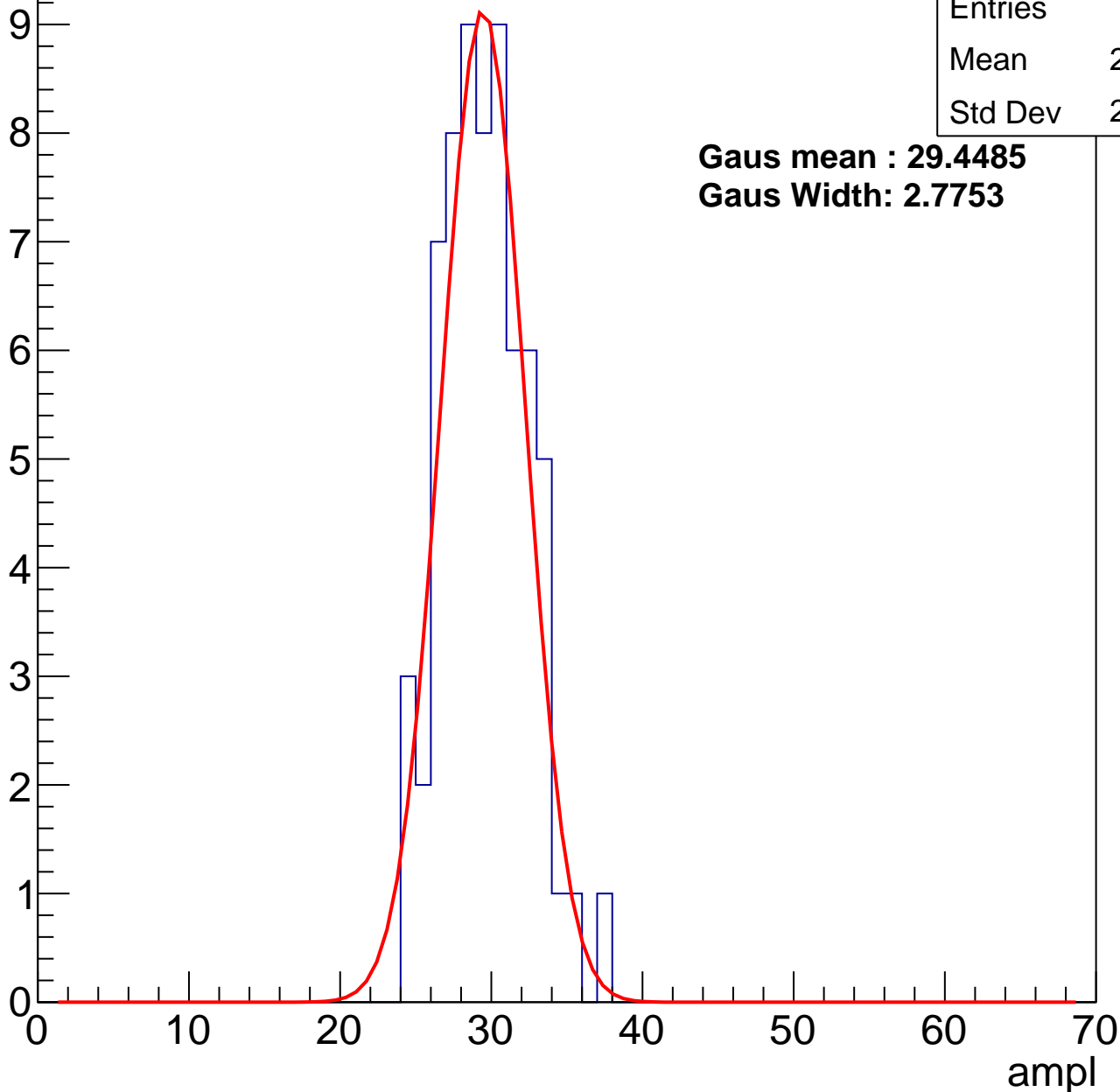
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.14
Std Dev	2.757

Gaus mean : 29.4485

Gaus Width: 2.7753



B0L002S, U12-ch6, adc1

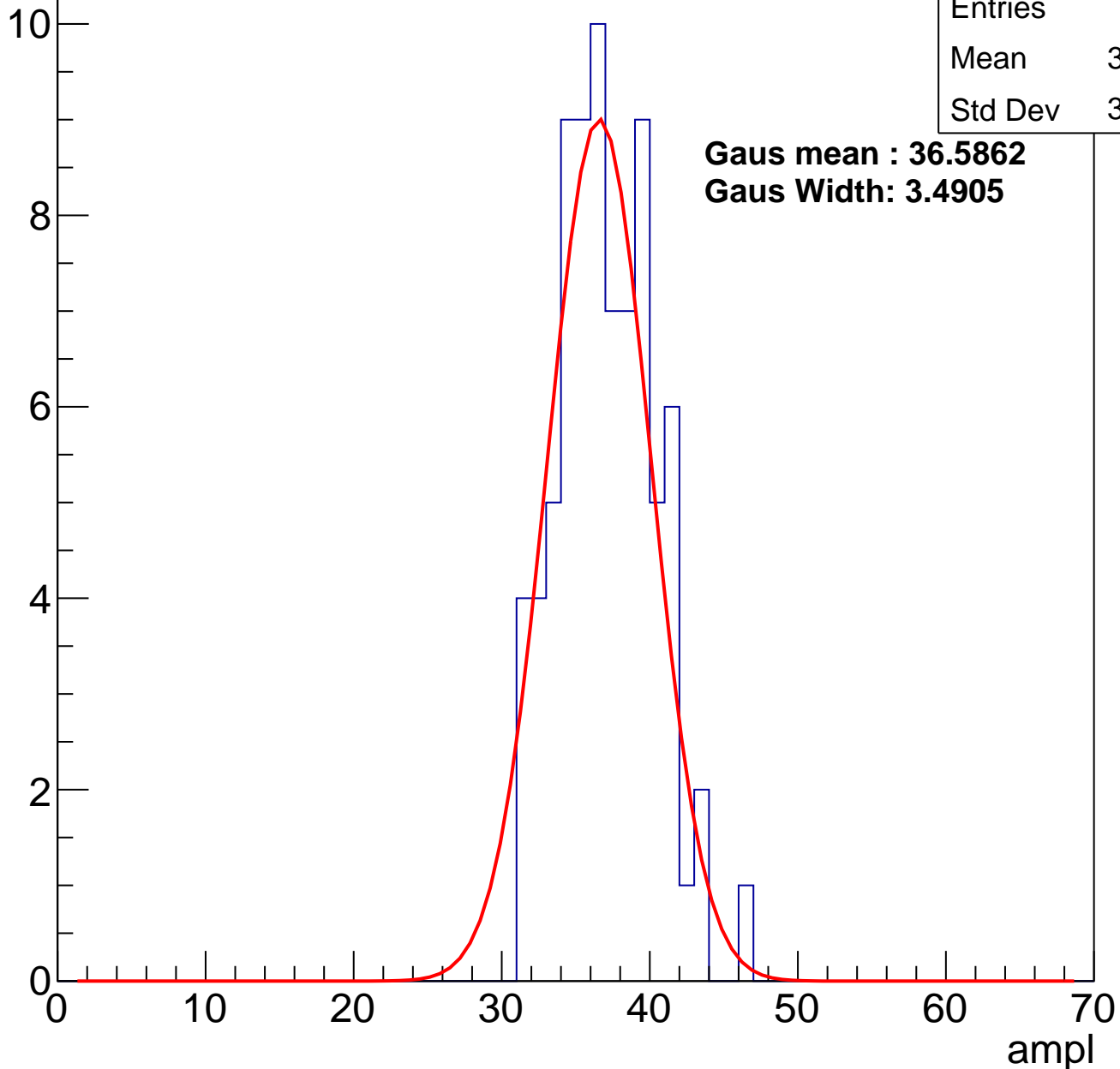
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	36.63
Std Dev	3.187

Gaus mean : 36.5862

Gaus Width: 3.4905

Entry



B0L002S, U12-ch6, adc2

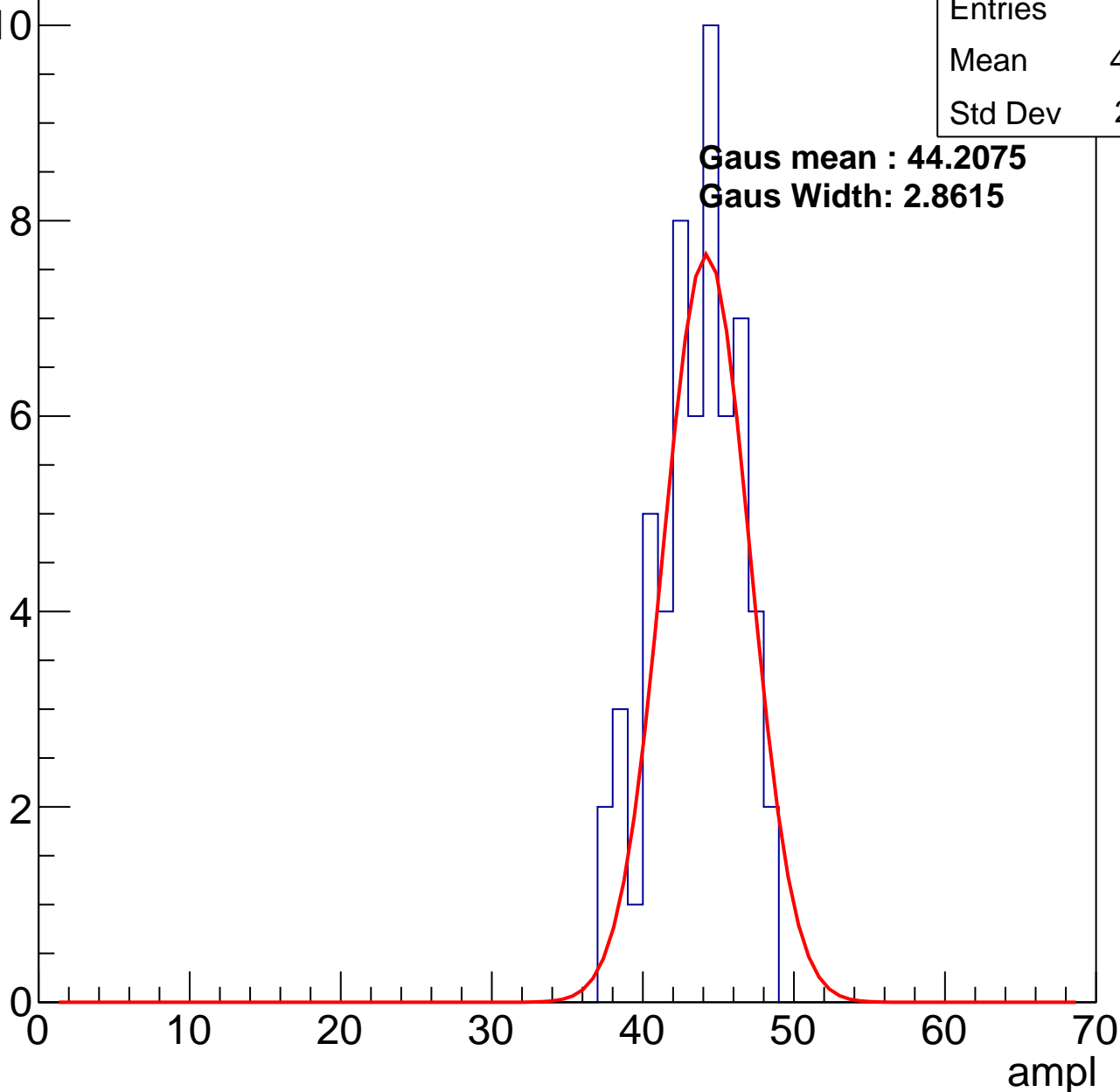
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.12
Std Dev	2.761

Gaus mean : 44.2075

Gaus Width: 2.8615

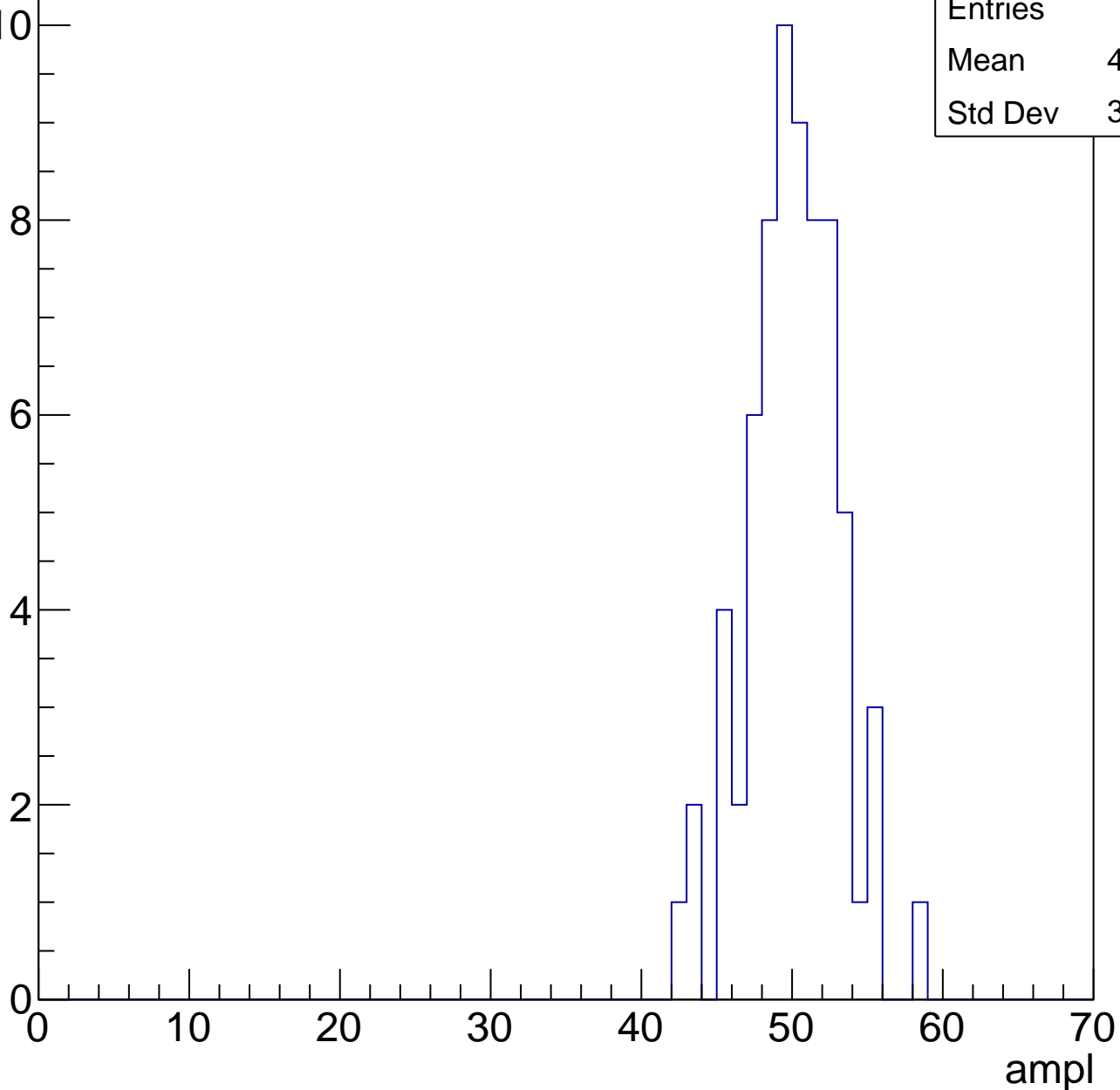


B0L002S, U12-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	49.59
Std Dev	3.016

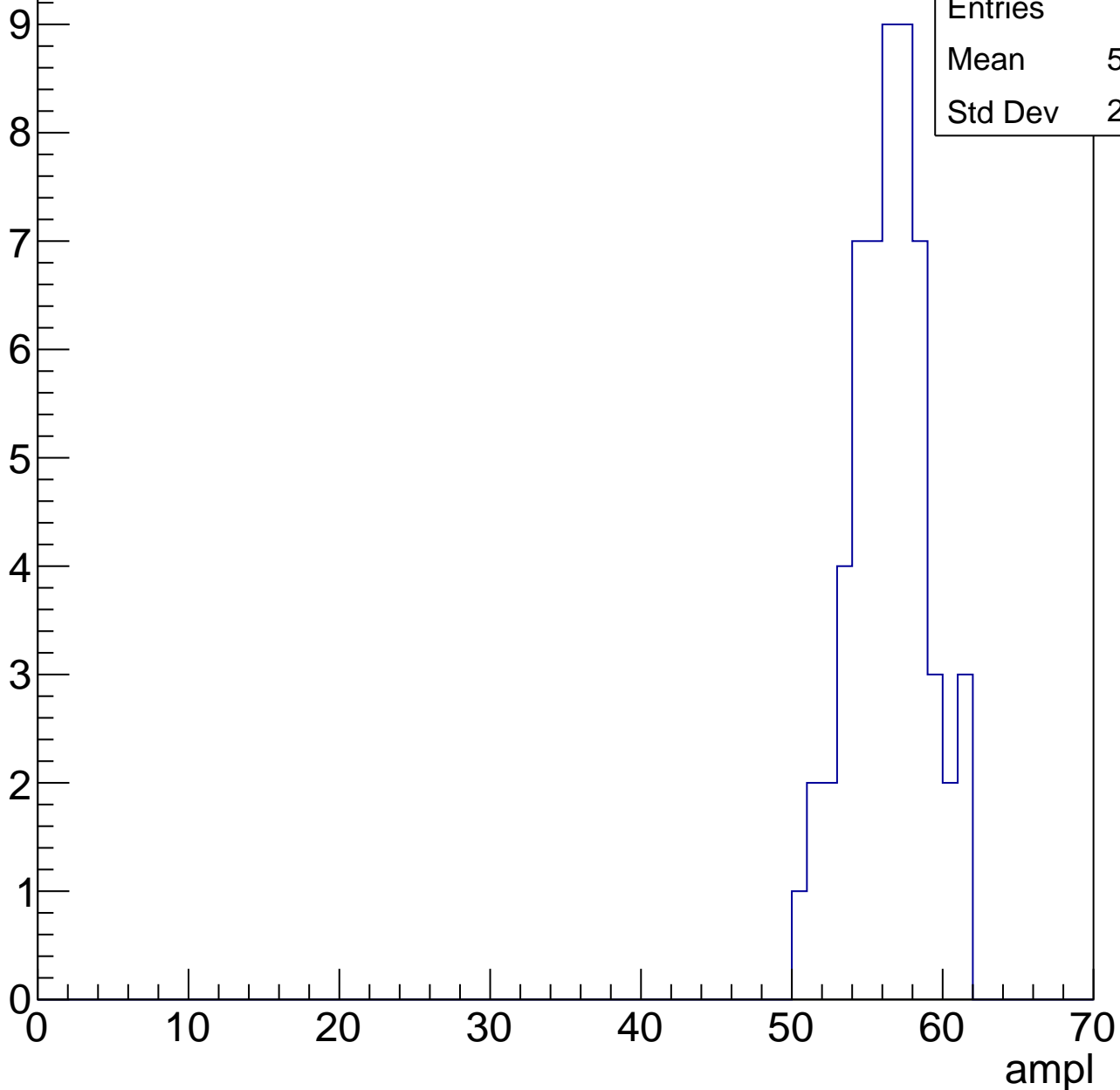


B0L002S, U12-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	55.96
Std Dev	2.535

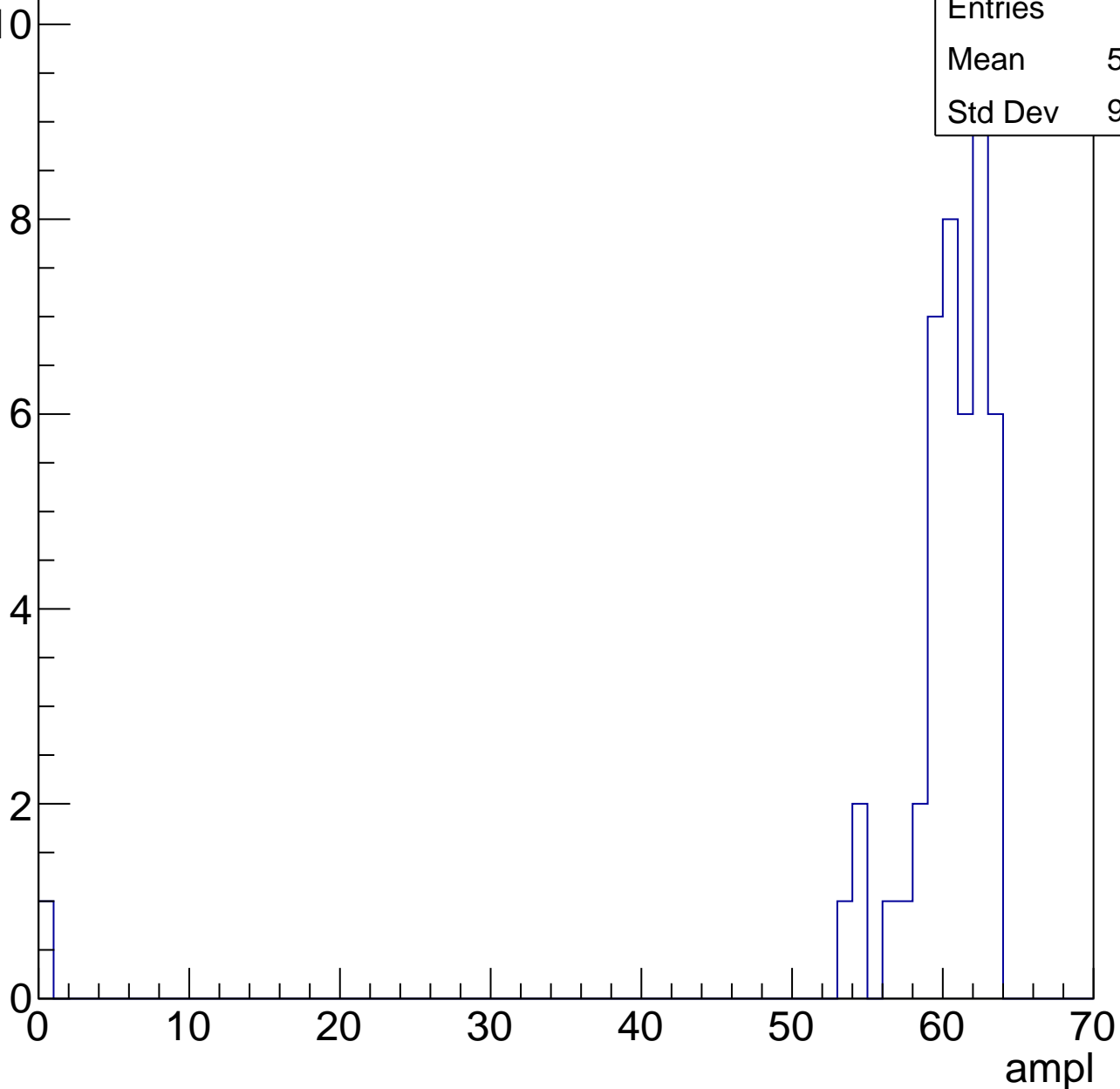


B0L002S, U12-ch6, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

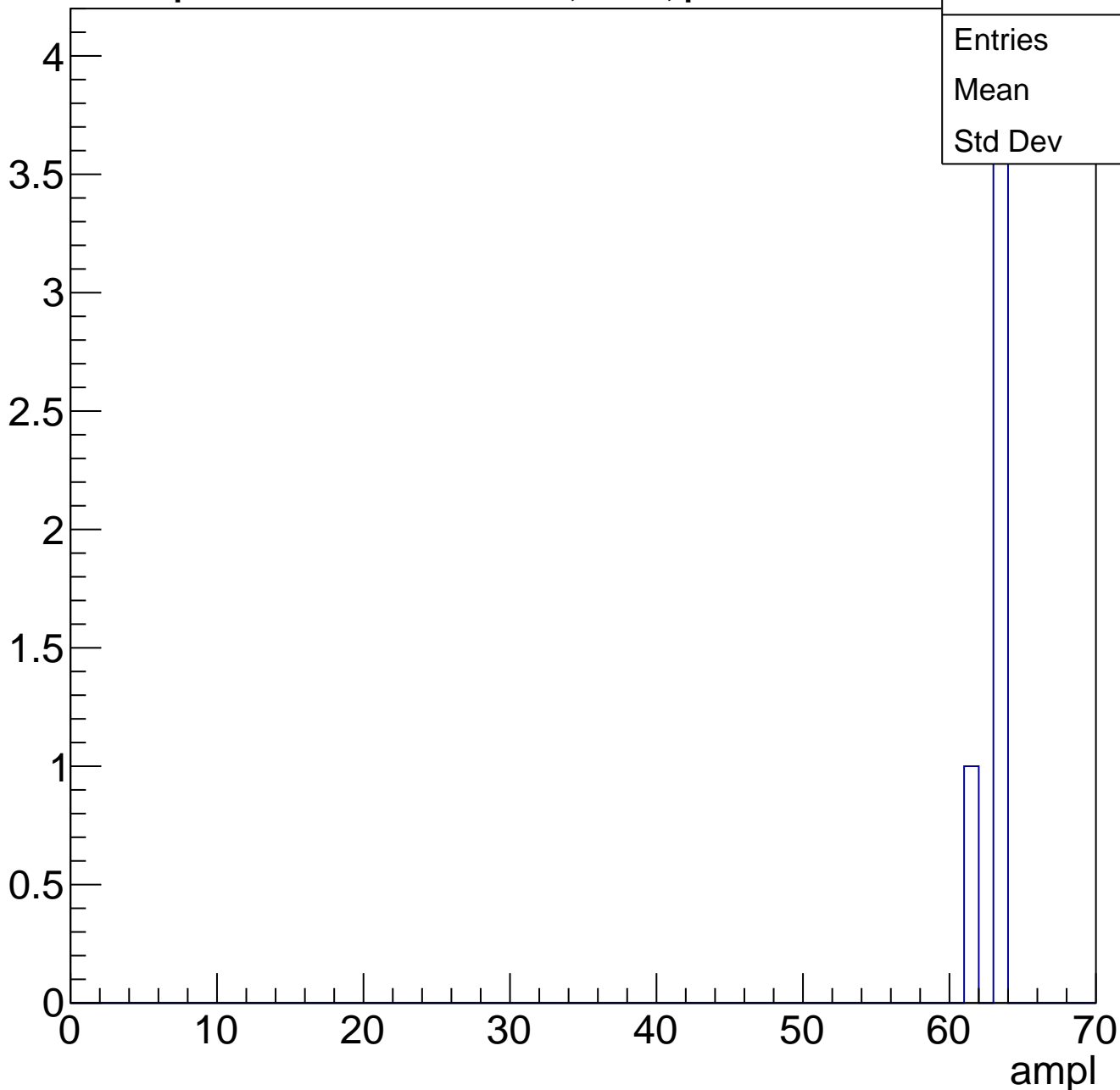
Entries	45
Mean	58.82
Std Dev	9.188



B0L002S, U12-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U12-ch7, adc0

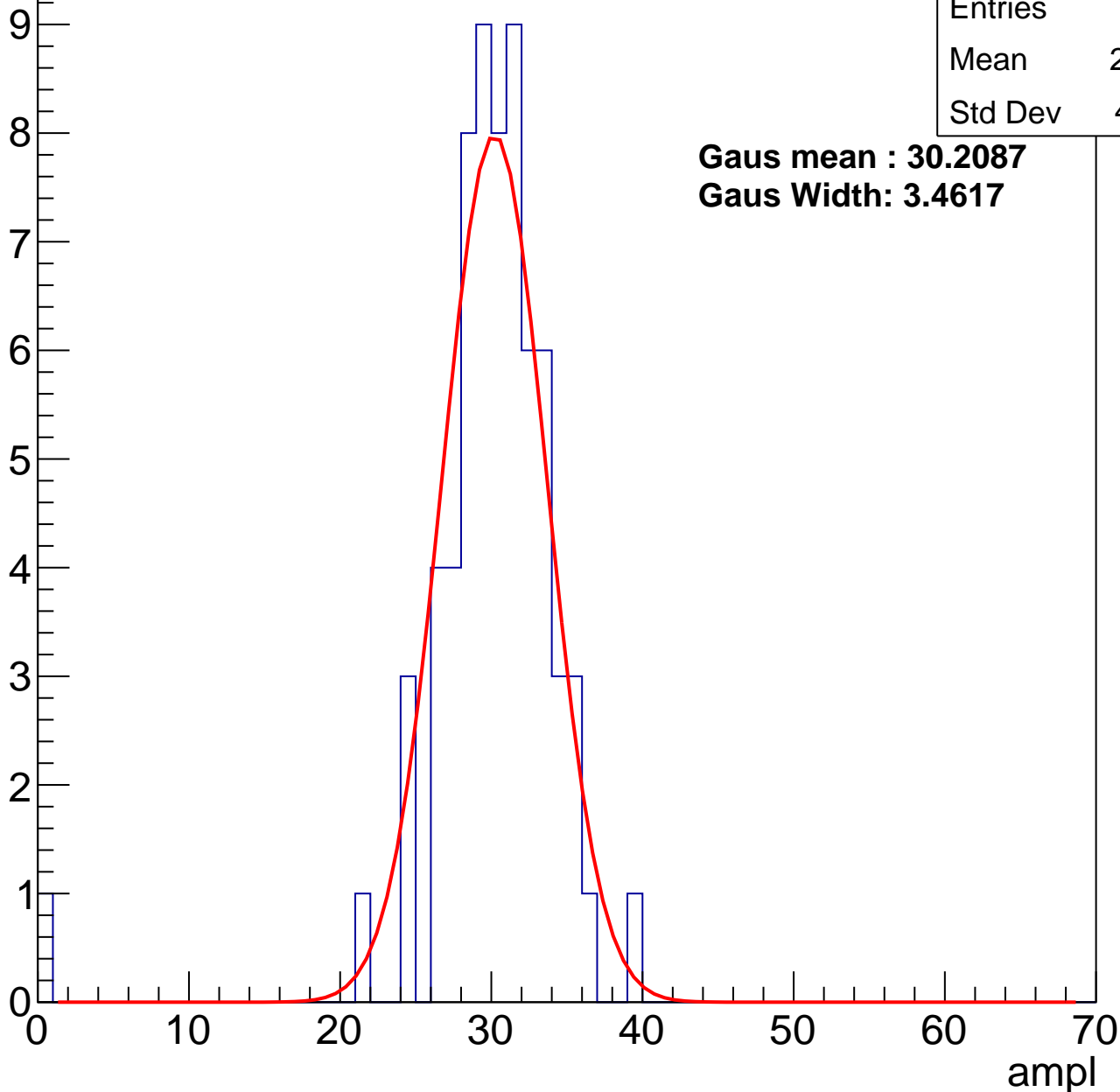
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.57
Std Dev	4.801

Gaus mean : 30.2087

Gaus Width: 3.4617



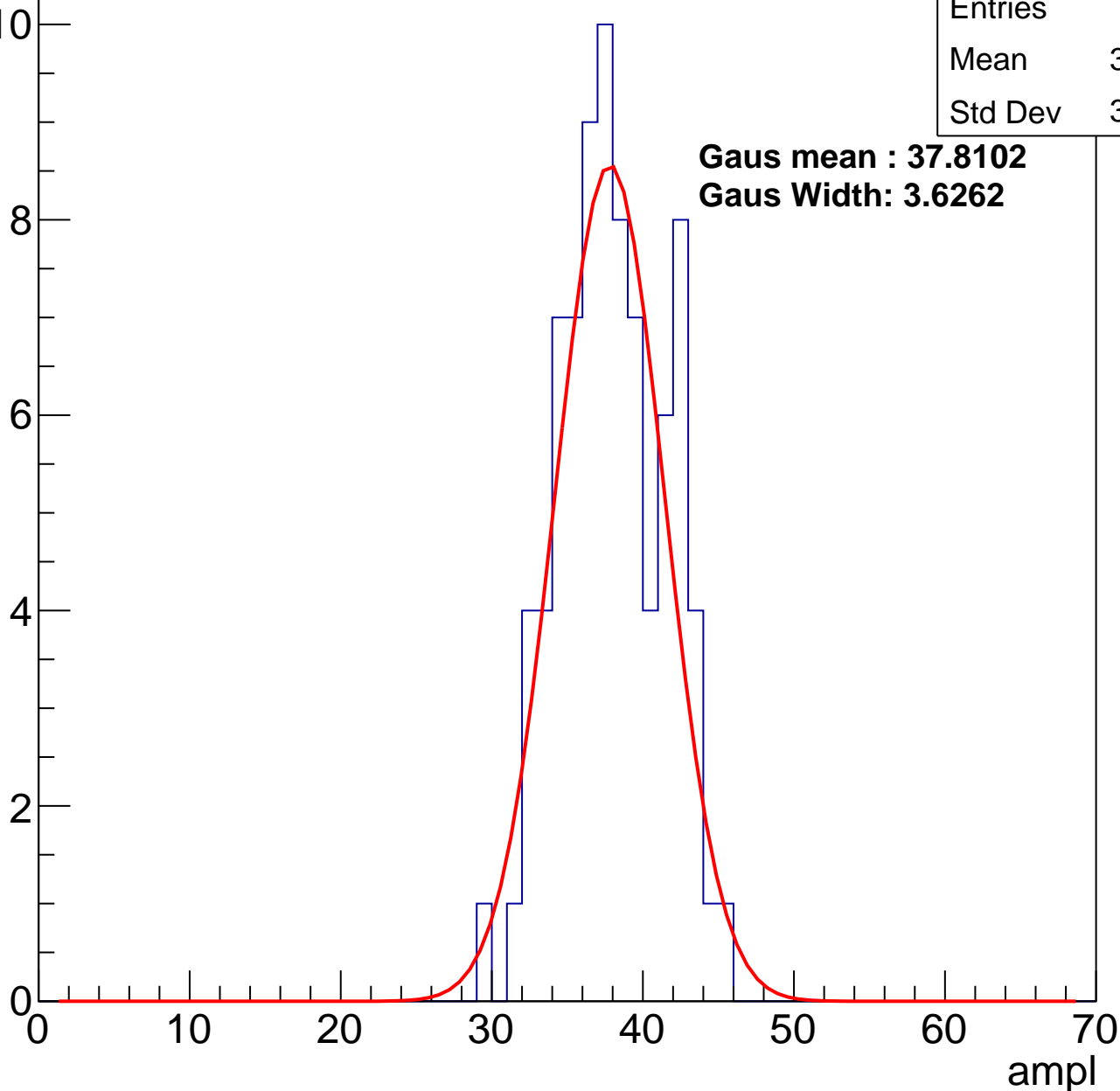
B0L002S, U12-ch7, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	37.52
Std Dev	3.433

Gaus mean : 37.8102
Gaus Width: 3.6262



B0L002S, U12-ch7, adc2

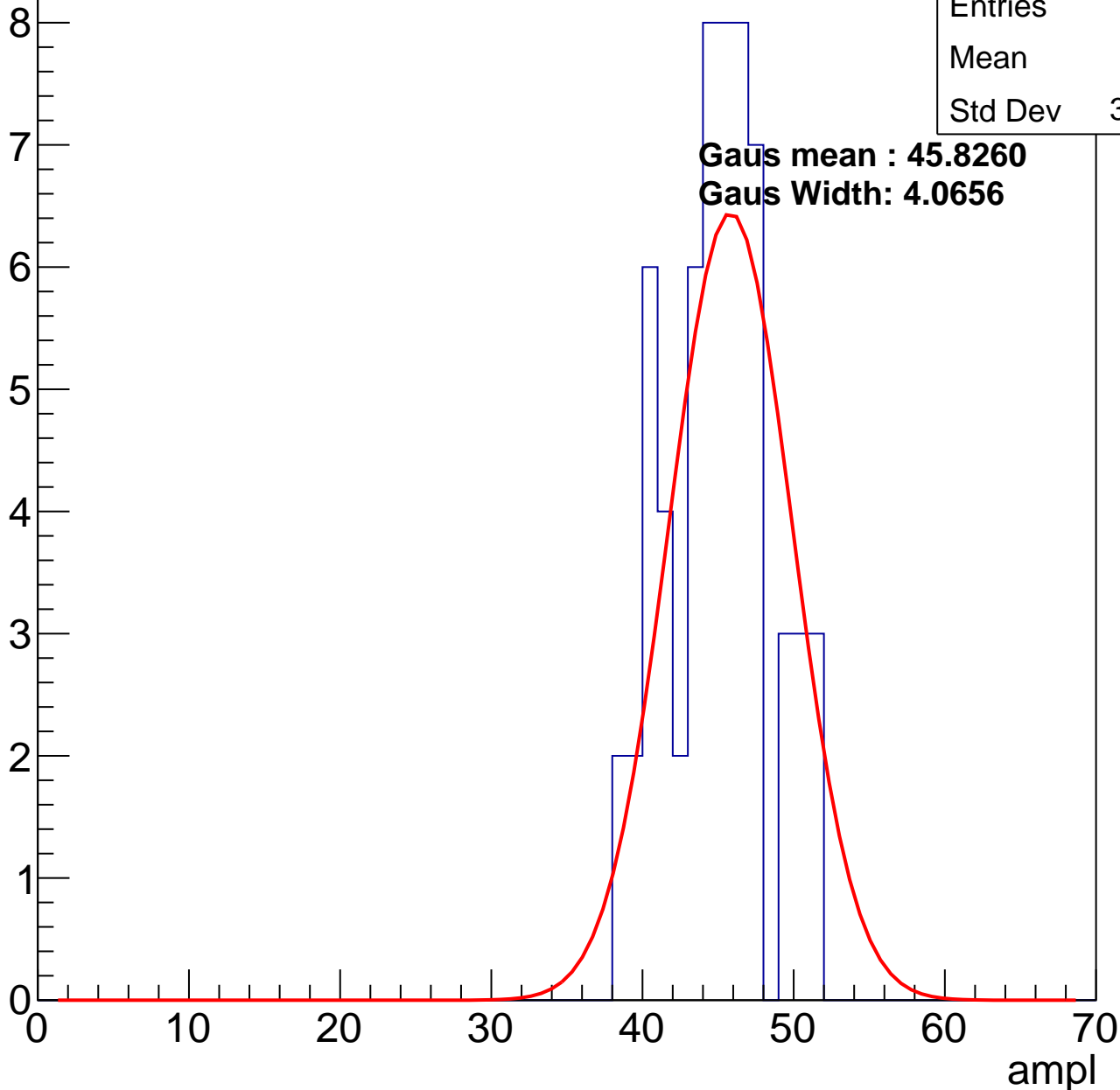
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.5
Std Dev	3.325

Gaus mean : 45.8260

Gaus Width: 4.0656



B0L002S, U12-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

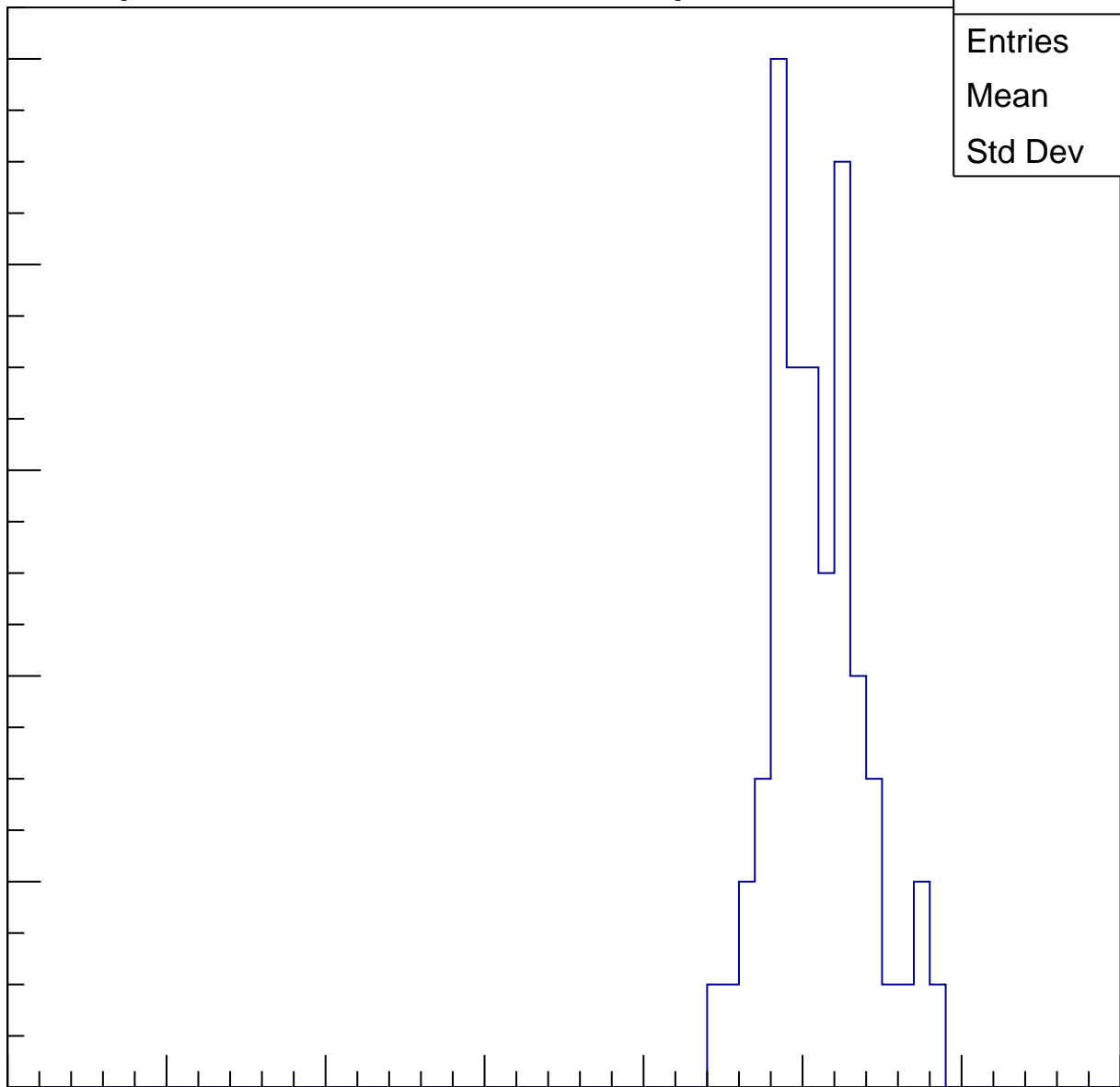
Entries	57
Mean	50.44
Std Dev	2.974

Entry

10
8
6
4
2
0

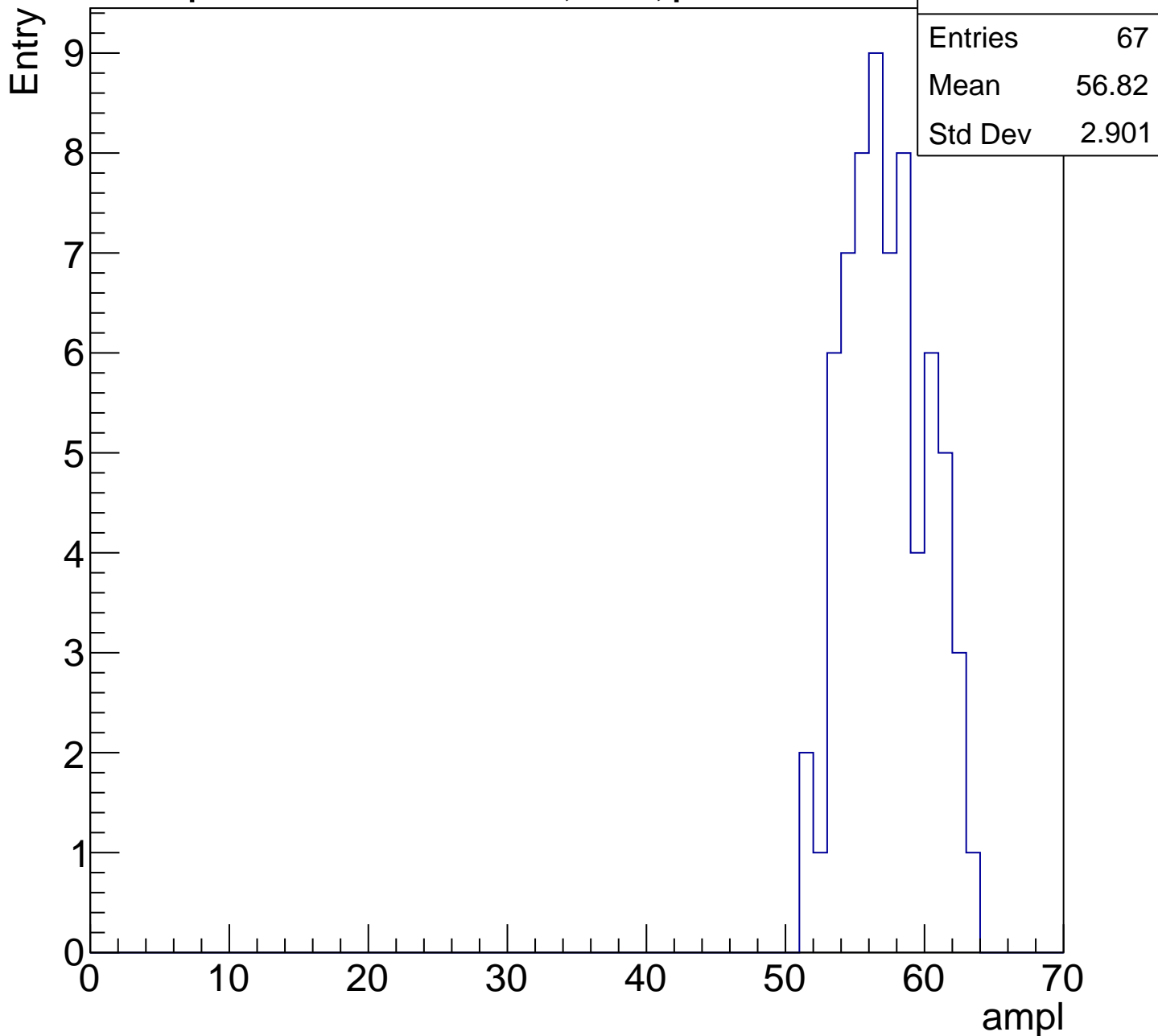
0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

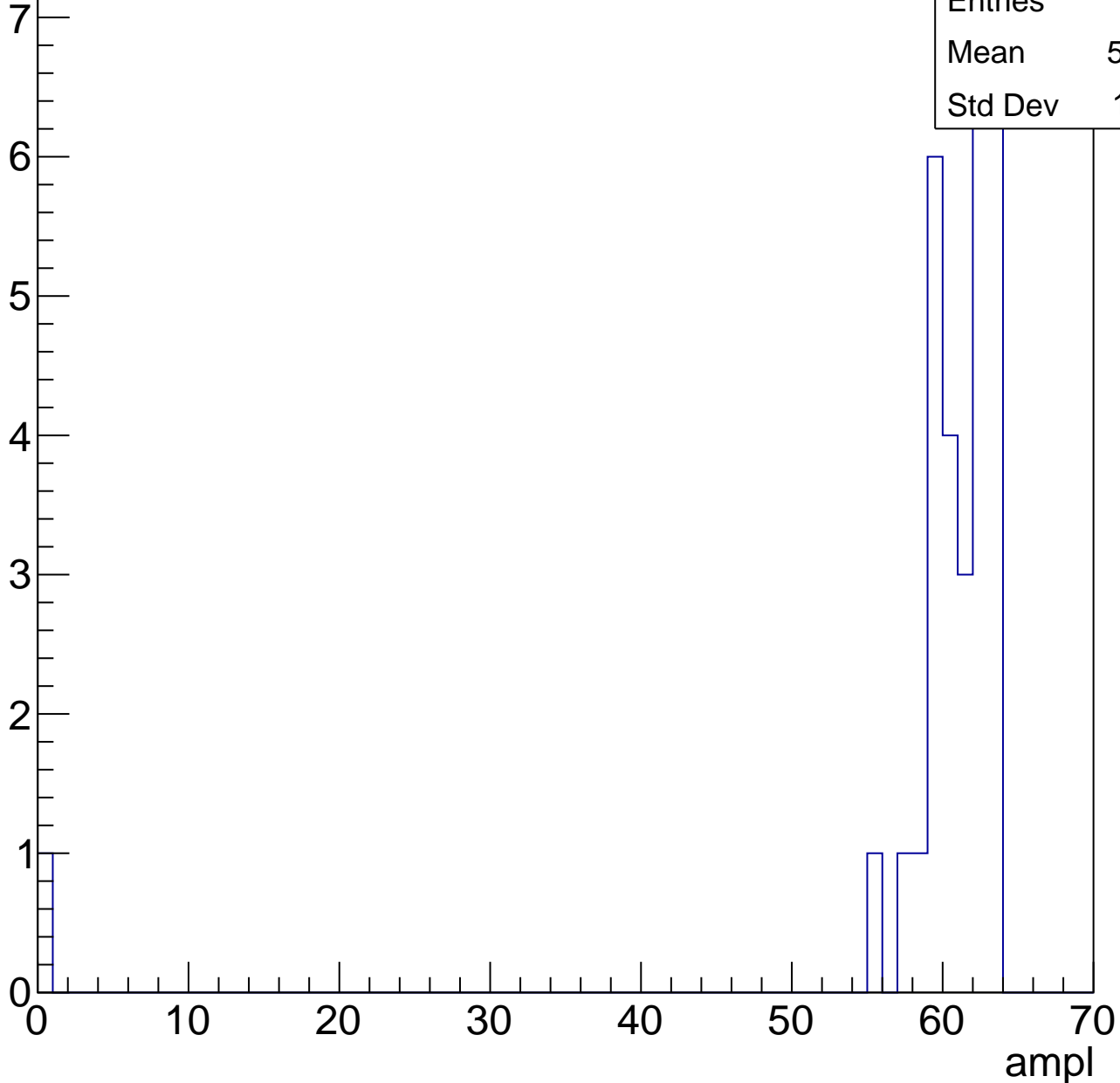


B0L002S, U12-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.77
Std Dev	10.91



B0L002S, U12-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

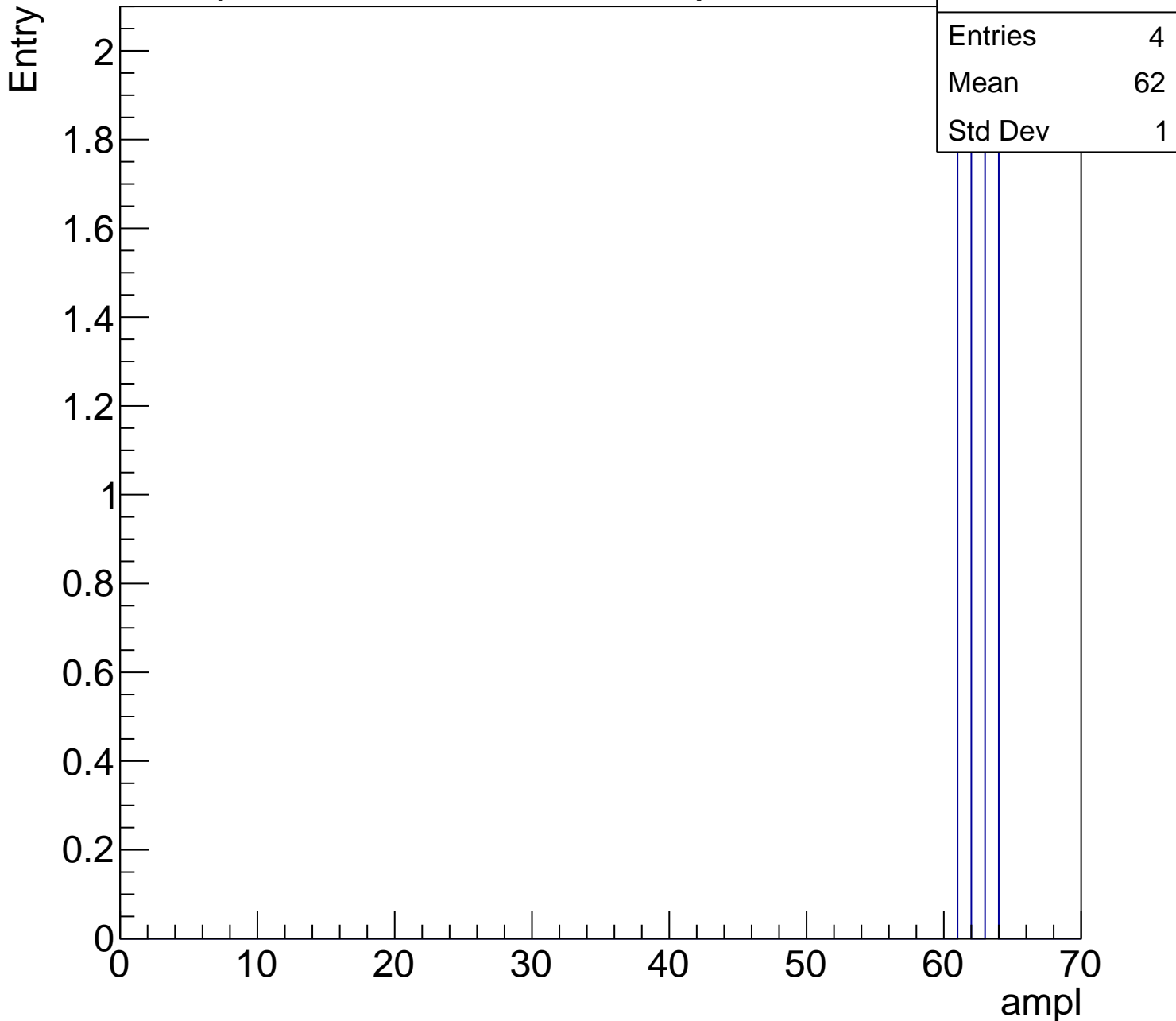
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch8, adc0

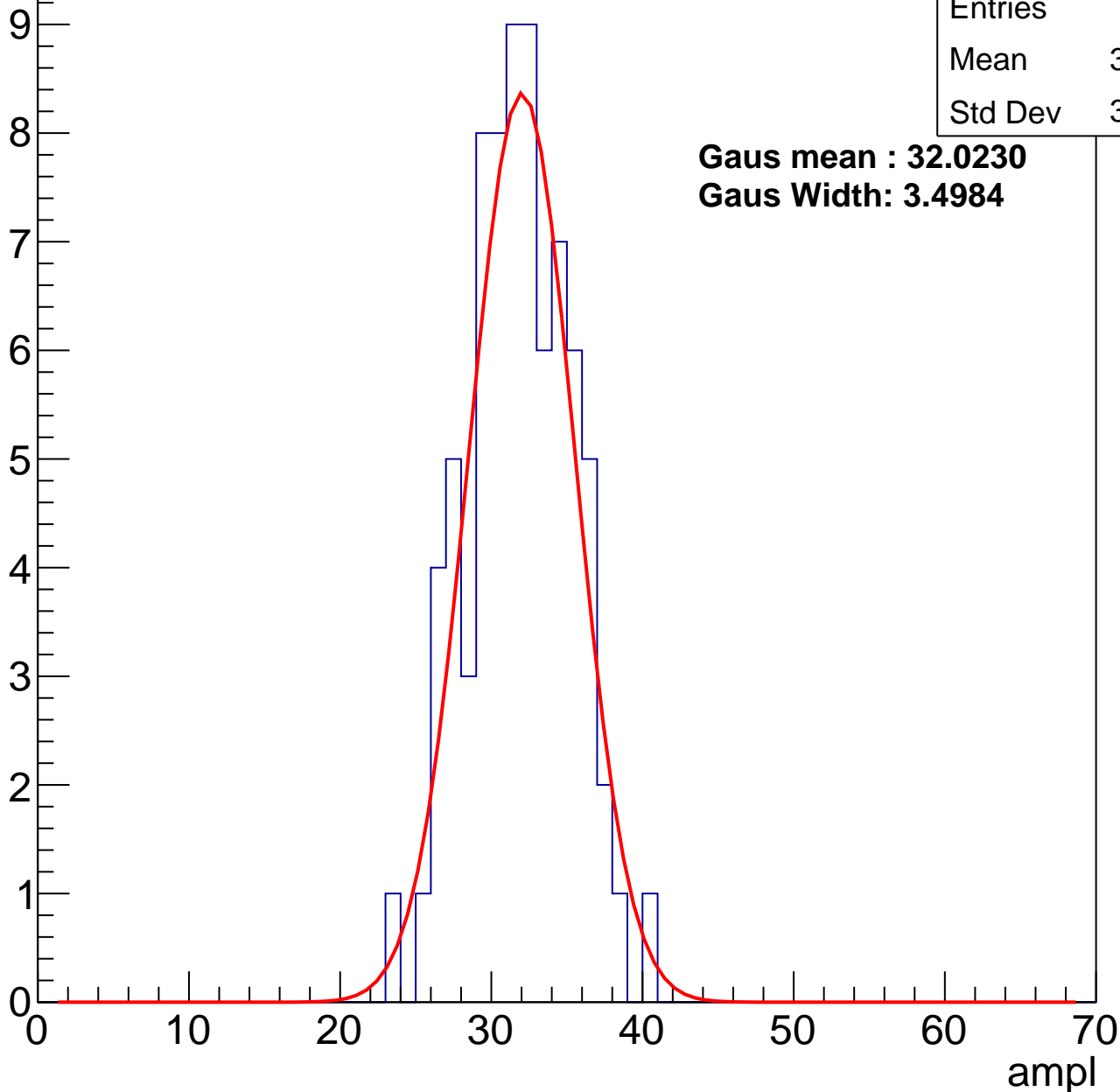
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	31.42
Std Dev	3.349

Gaus mean : 32.0230

Gaus Width: 3.4984



B0L002S, U12-ch8, adc1

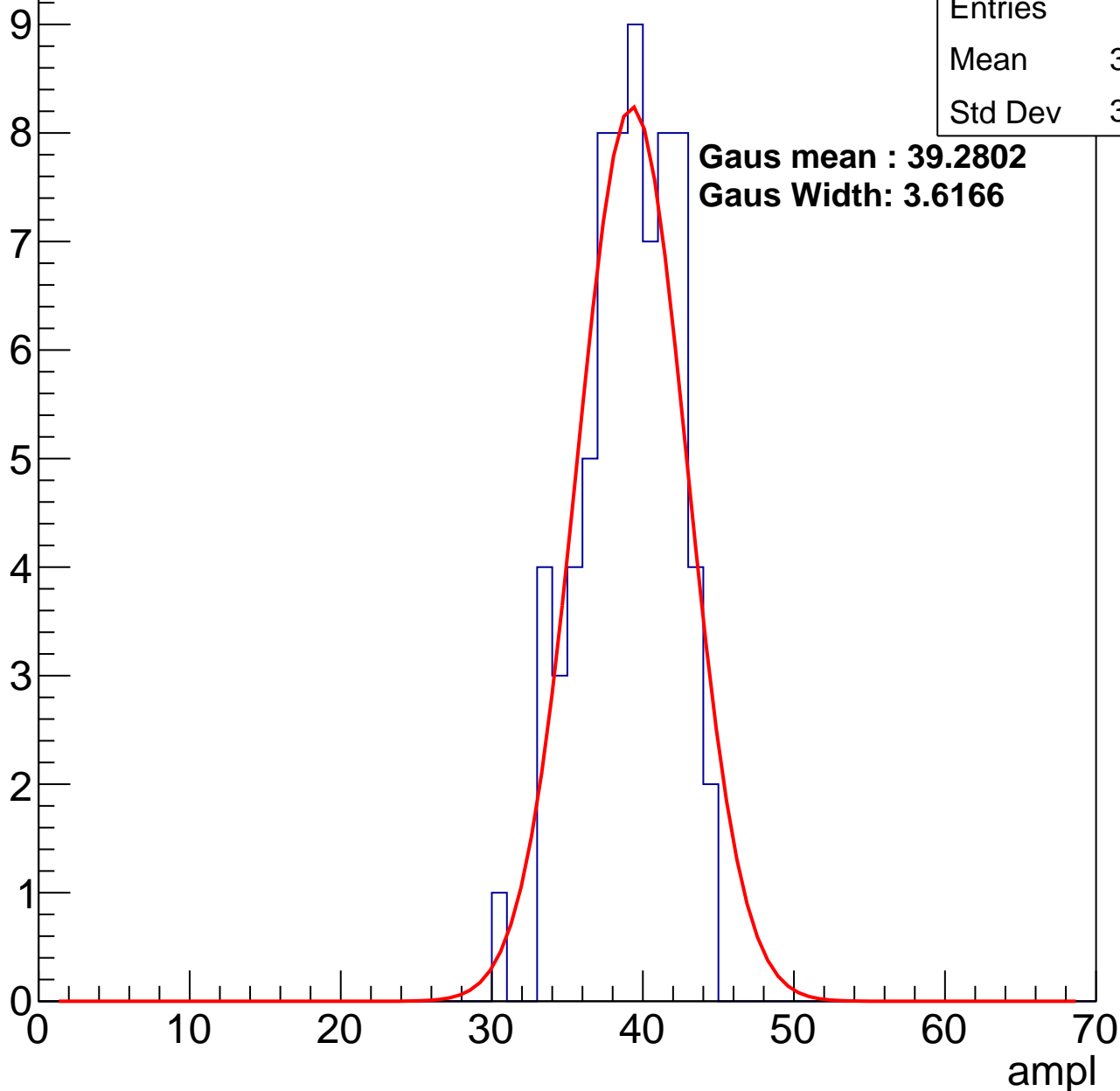
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38.58
Std Dev	3.052

Gaus mean : 39.2802

Gaus Width: 3.6166



B0L002S, U12-ch8, adc2

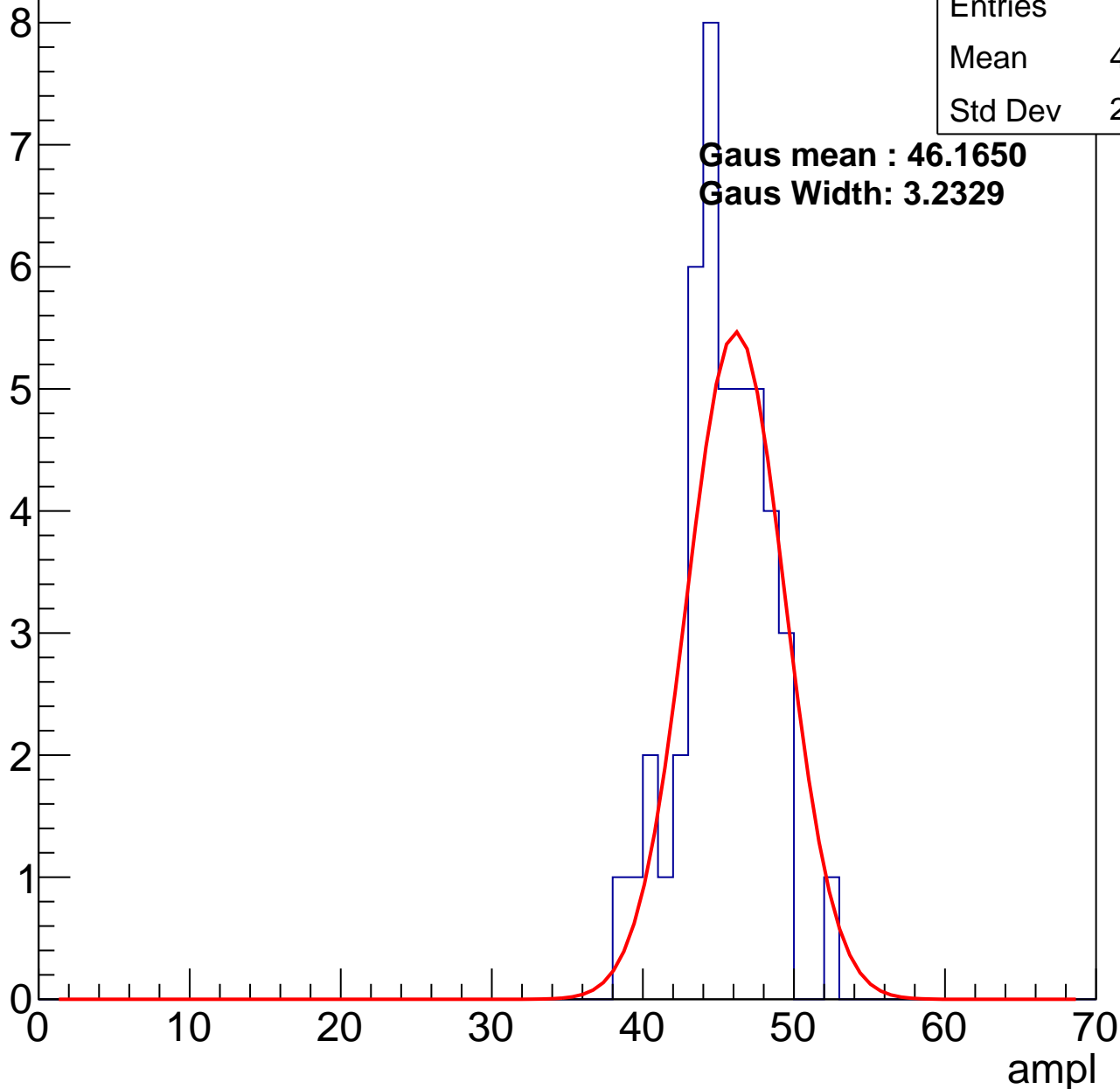
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	44.84
Std Dev	2.852

Gaus mean : 46.1650

Gaus Width: 3.2329

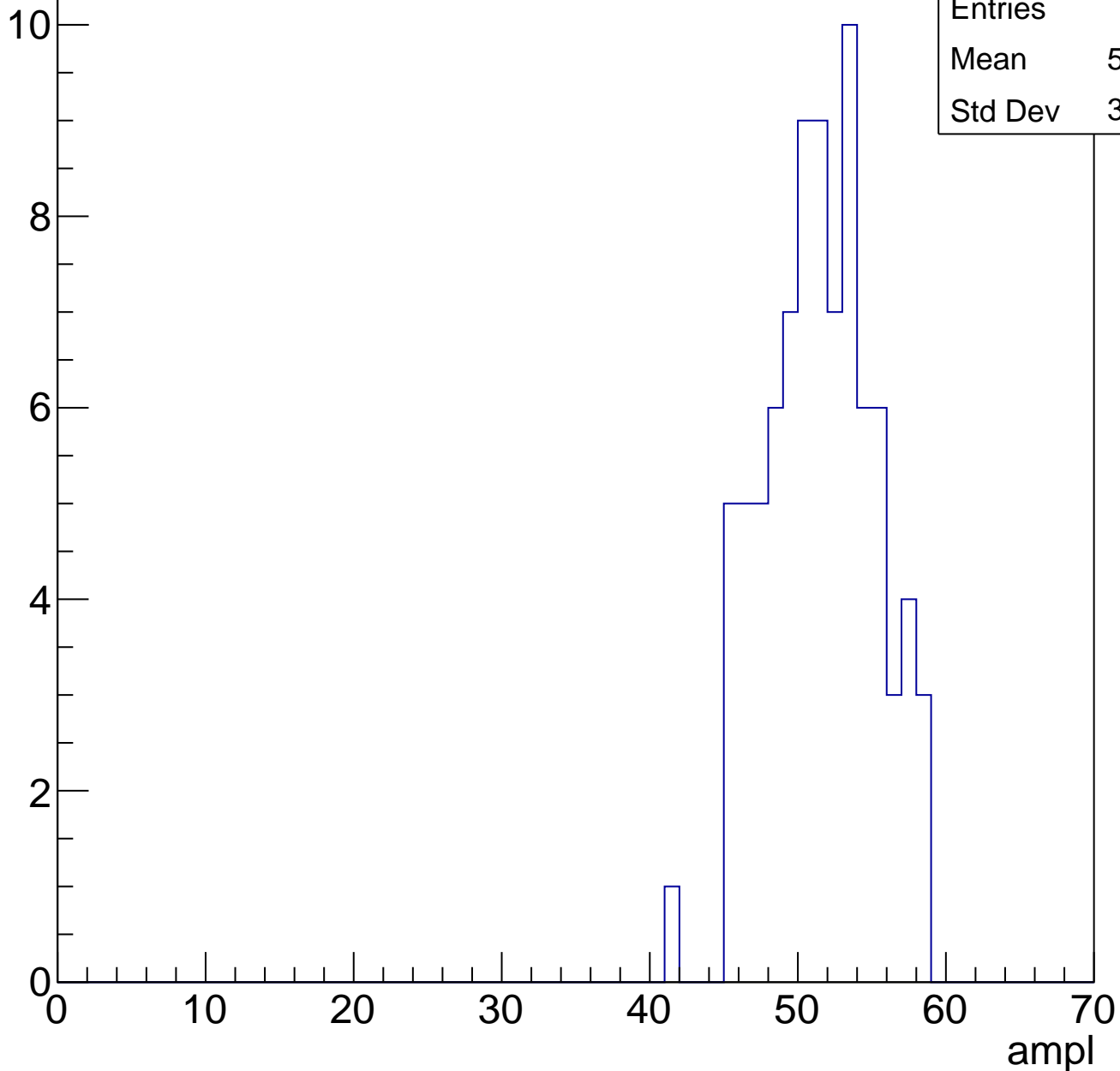


B0L002S, U12-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	86
Mean	51.03
Std Dev	3.639

Entry



B0L002S, U12-ch8, adc4

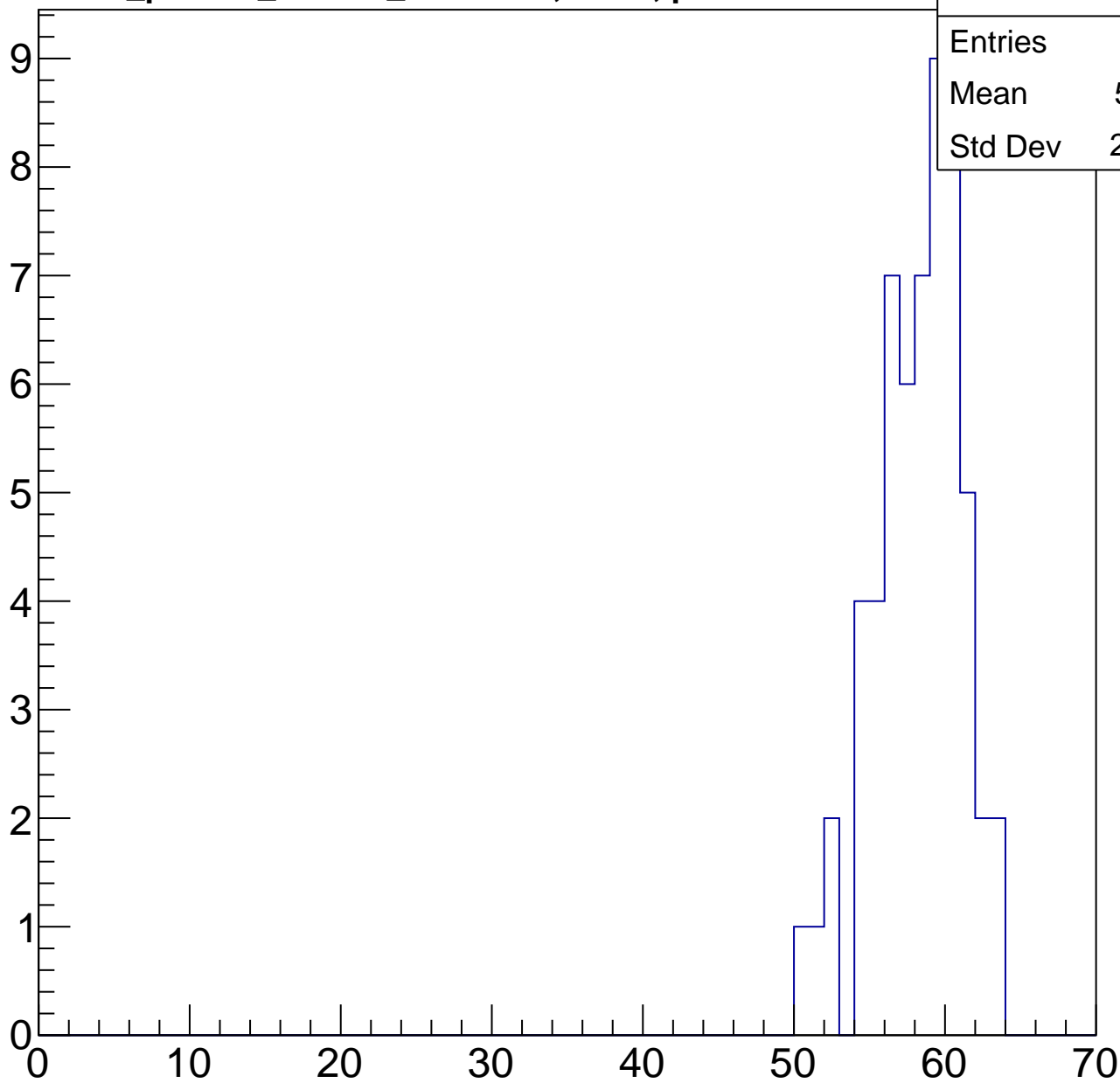
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	57.71
Std Dev	2.883

ampl

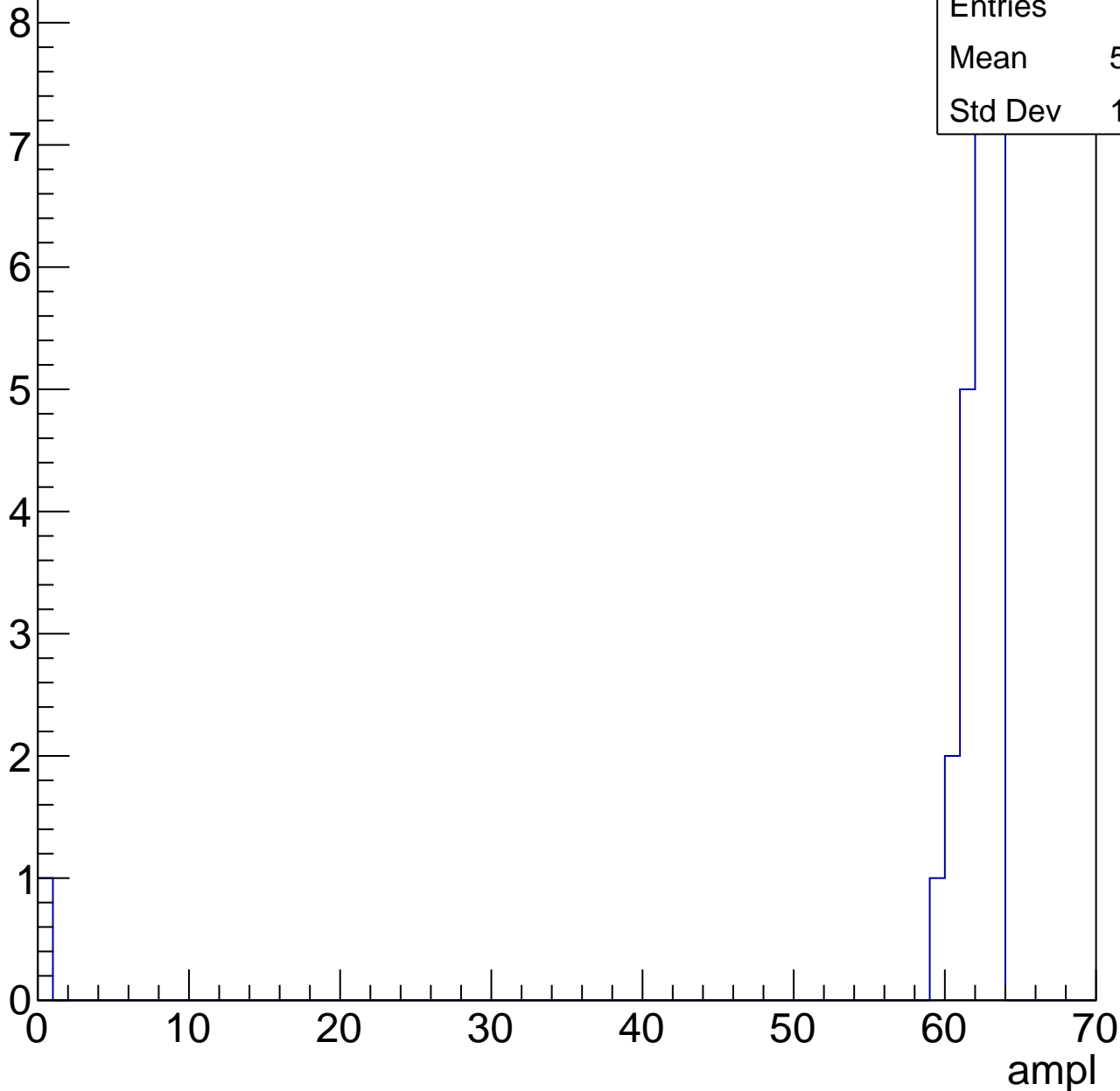


B0L002S, U12-ch8, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	59.36
Std Dev	12.17



B0L002S, U12-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U12-ch9, adc0

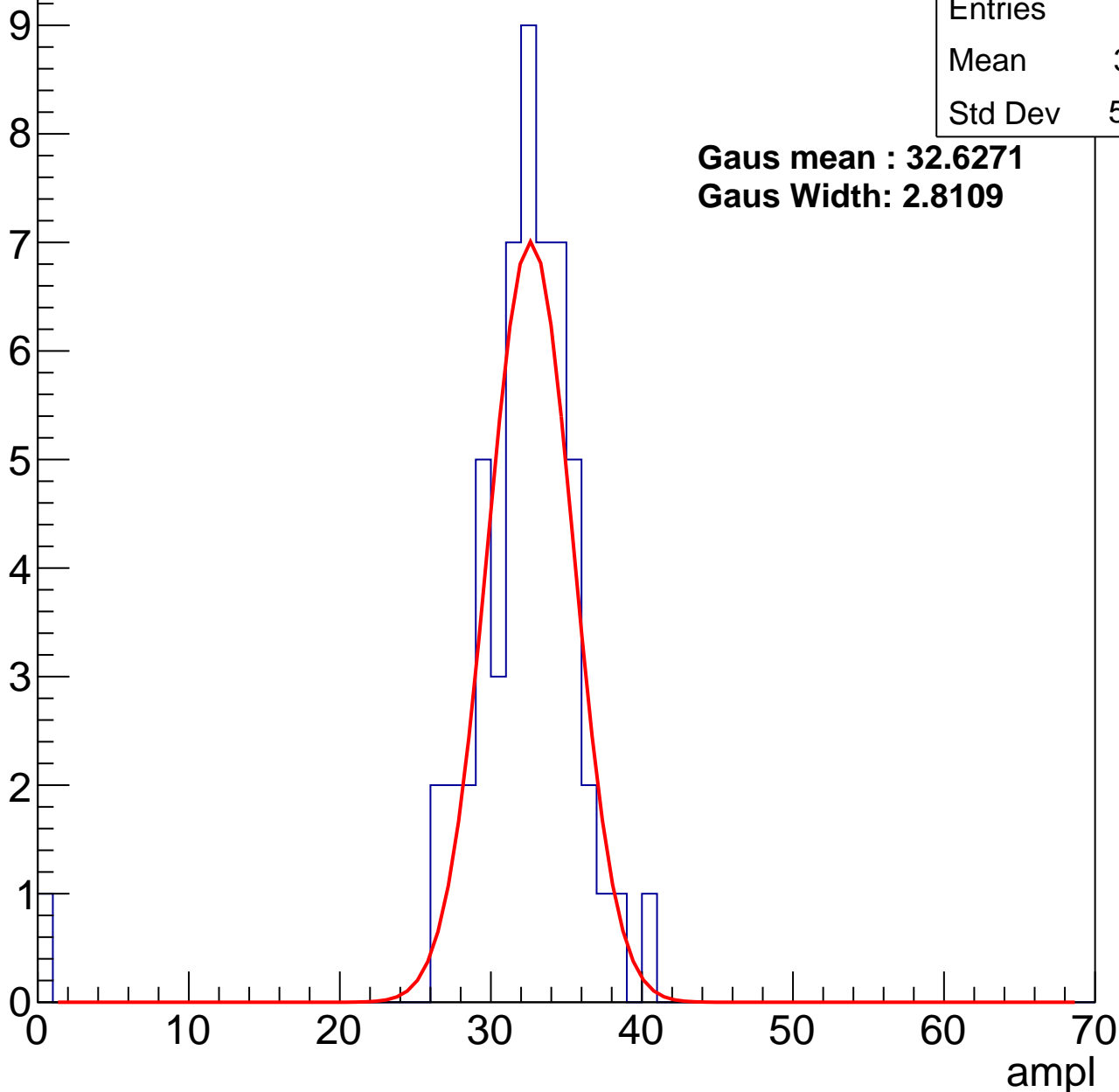
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	31.51
Std Dev	5.162

Gaus mean : 32.6271

Gaus Width: 2.8109



B0L002S, U12-ch9, adc1

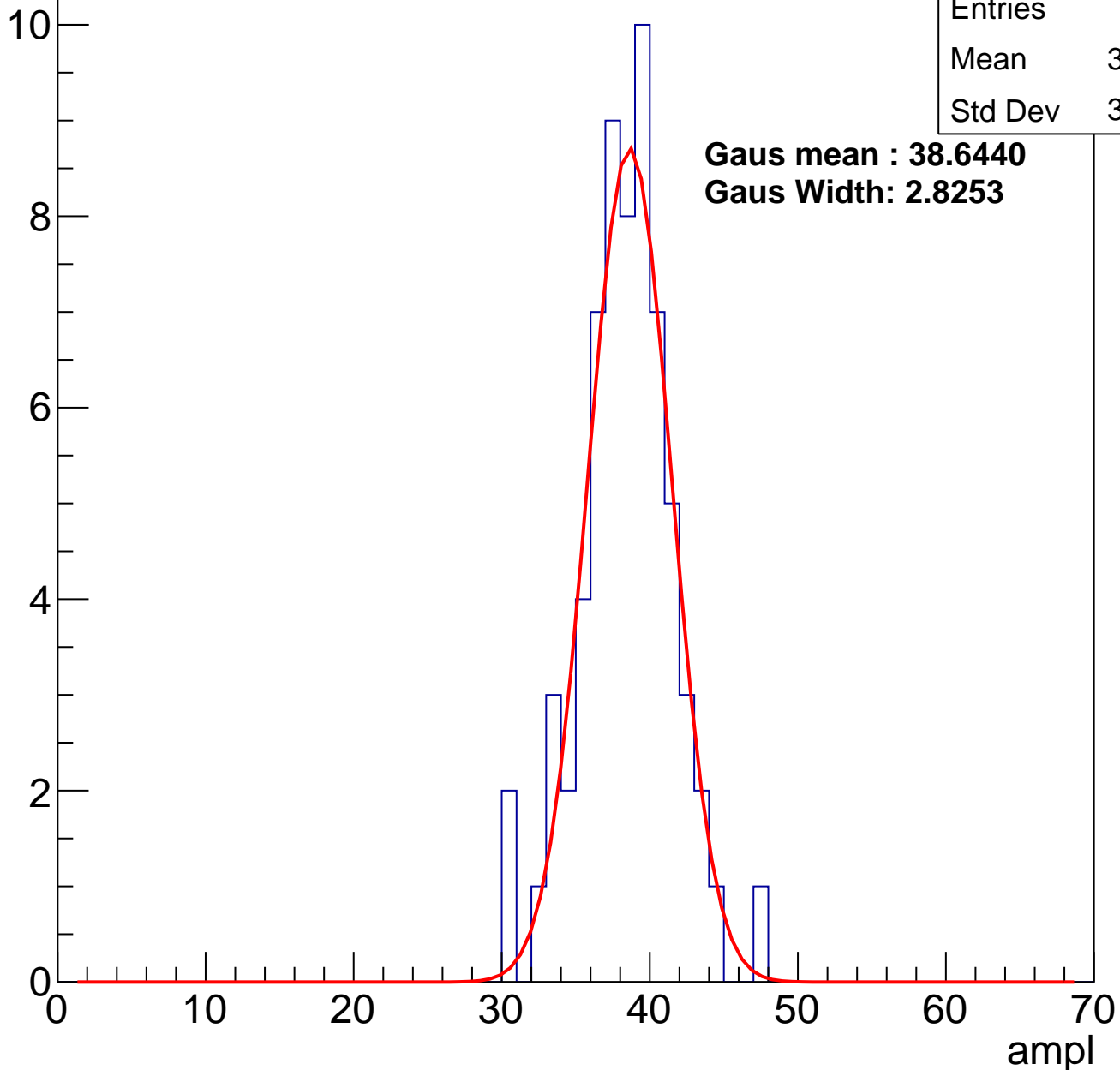
calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	37.94
Std Dev	3.142

Gaus mean : 38.6440

Gaus Width: 2.8253

Entry



B0L002S, U12-ch9, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.81
Std Dev	3.338

Gaus mean : 45.2798

Gaus Width: 3.2852

10

8

6

4

2

0

0

10

20

30

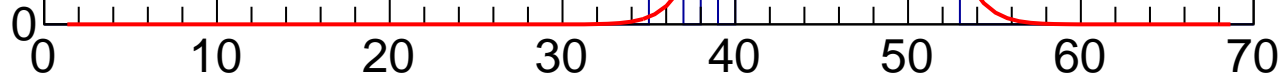
40

50

60

70

ampl



B0L002S, U12-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

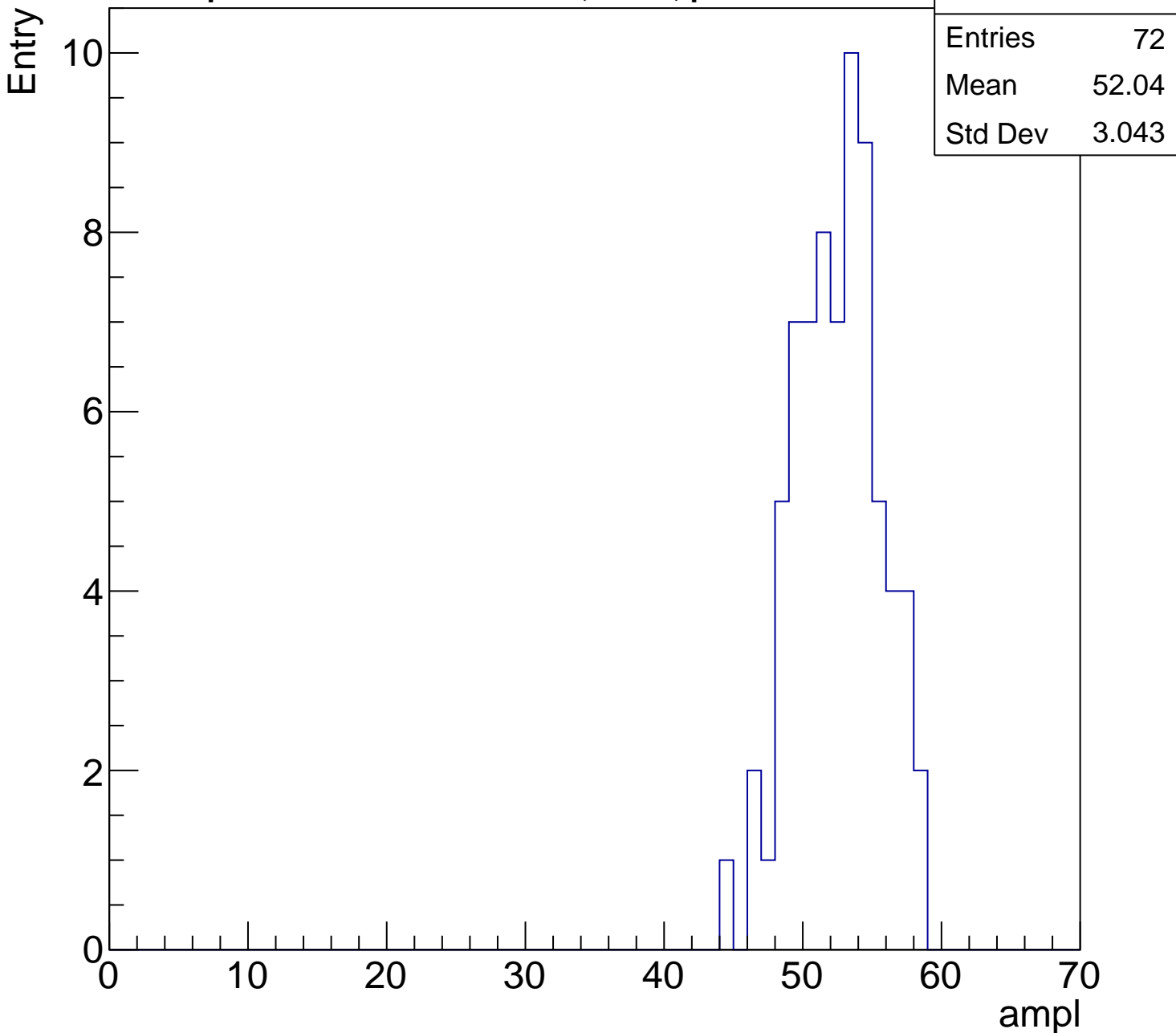
Entries	72
Mean	52.04
Std Dev	3.043

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

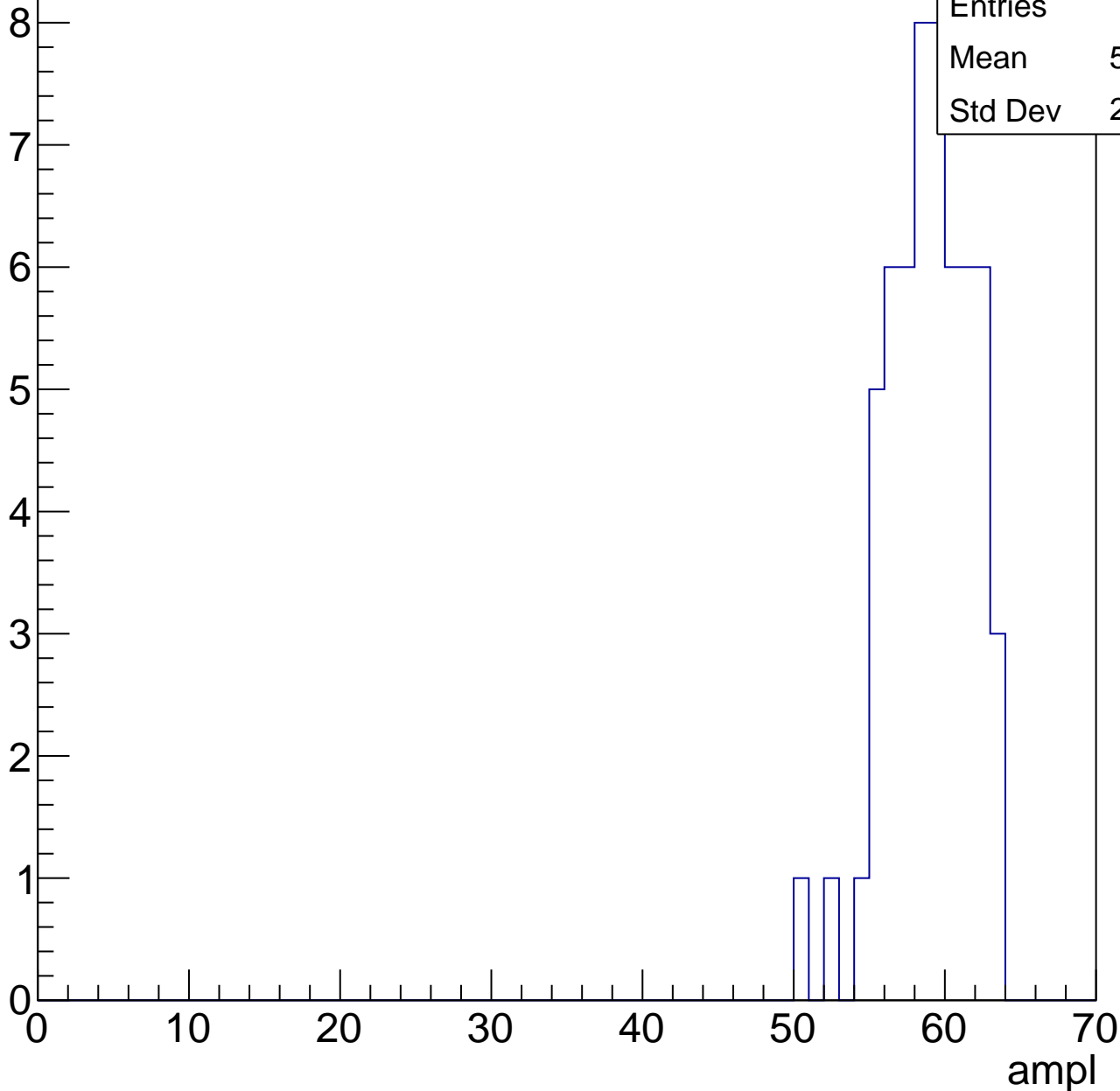


B0L002S, U12-ch9, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	58.46
Std Dev	2.766

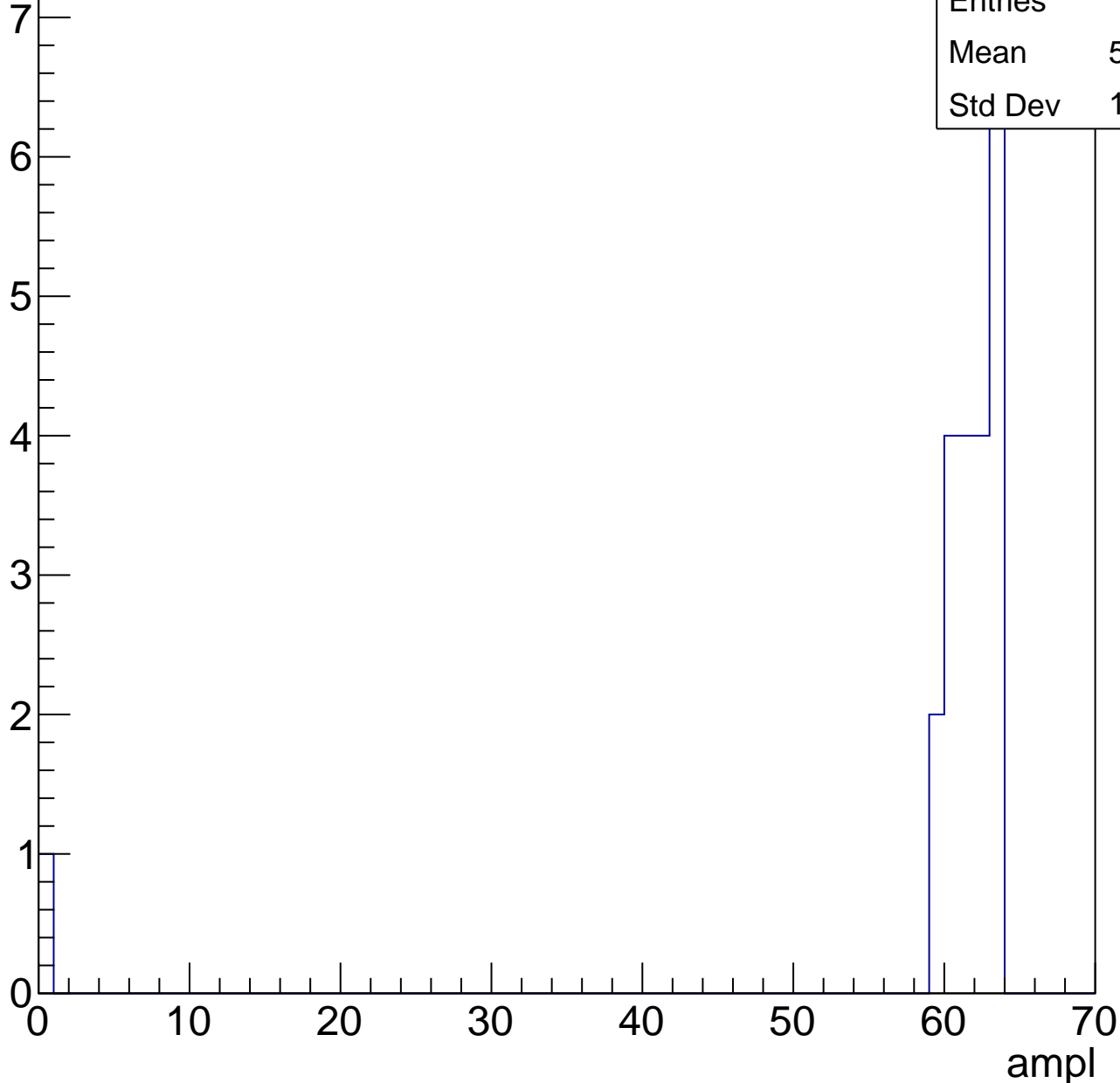


B0L002S, U12-ch9, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.68
Std Dev	12.87



B0L002S, U12-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch10, adc0

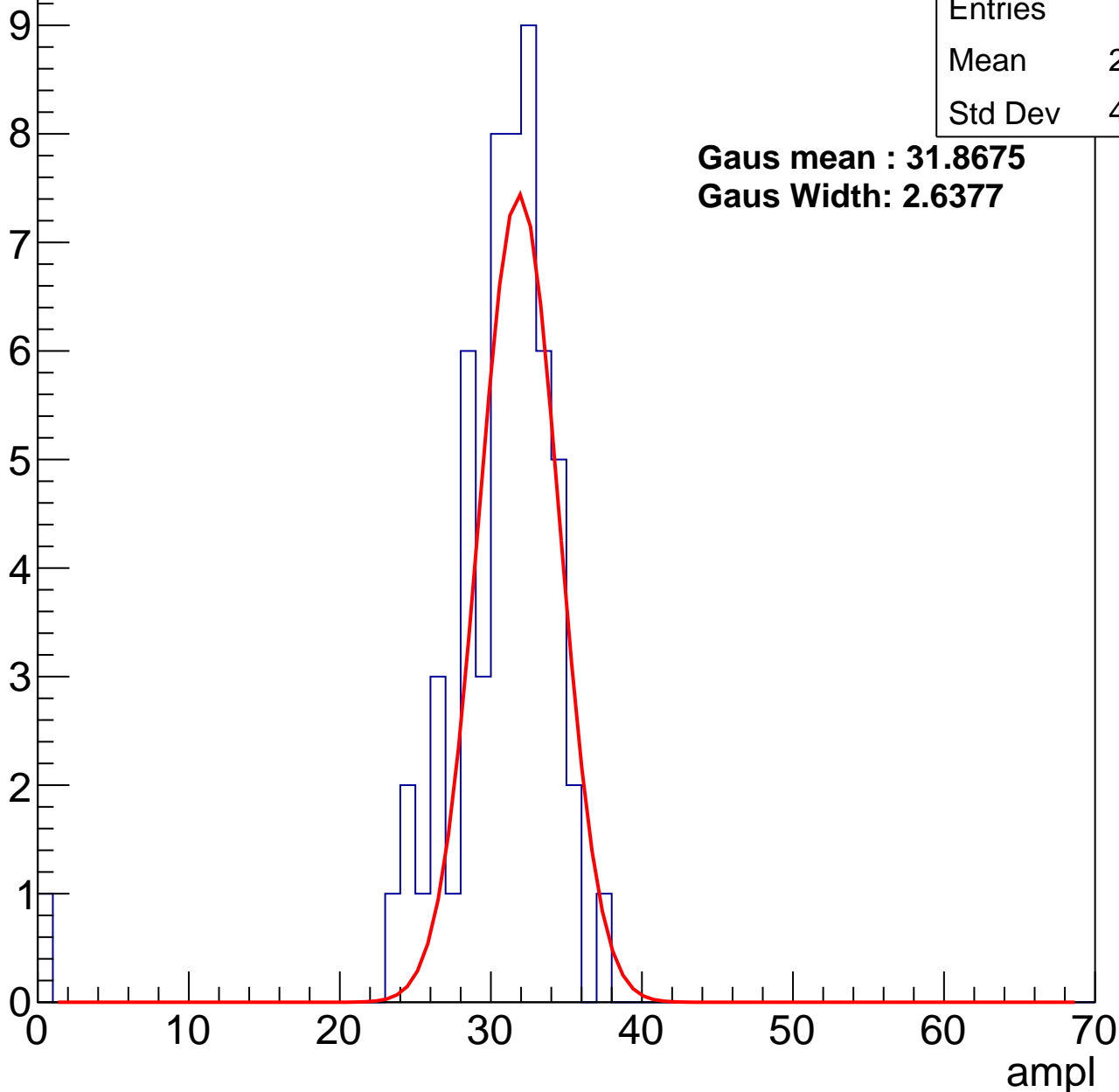
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.95
Std Dev	4.968

Gaus mean : 31.8675

Gaus Width: 2.6377

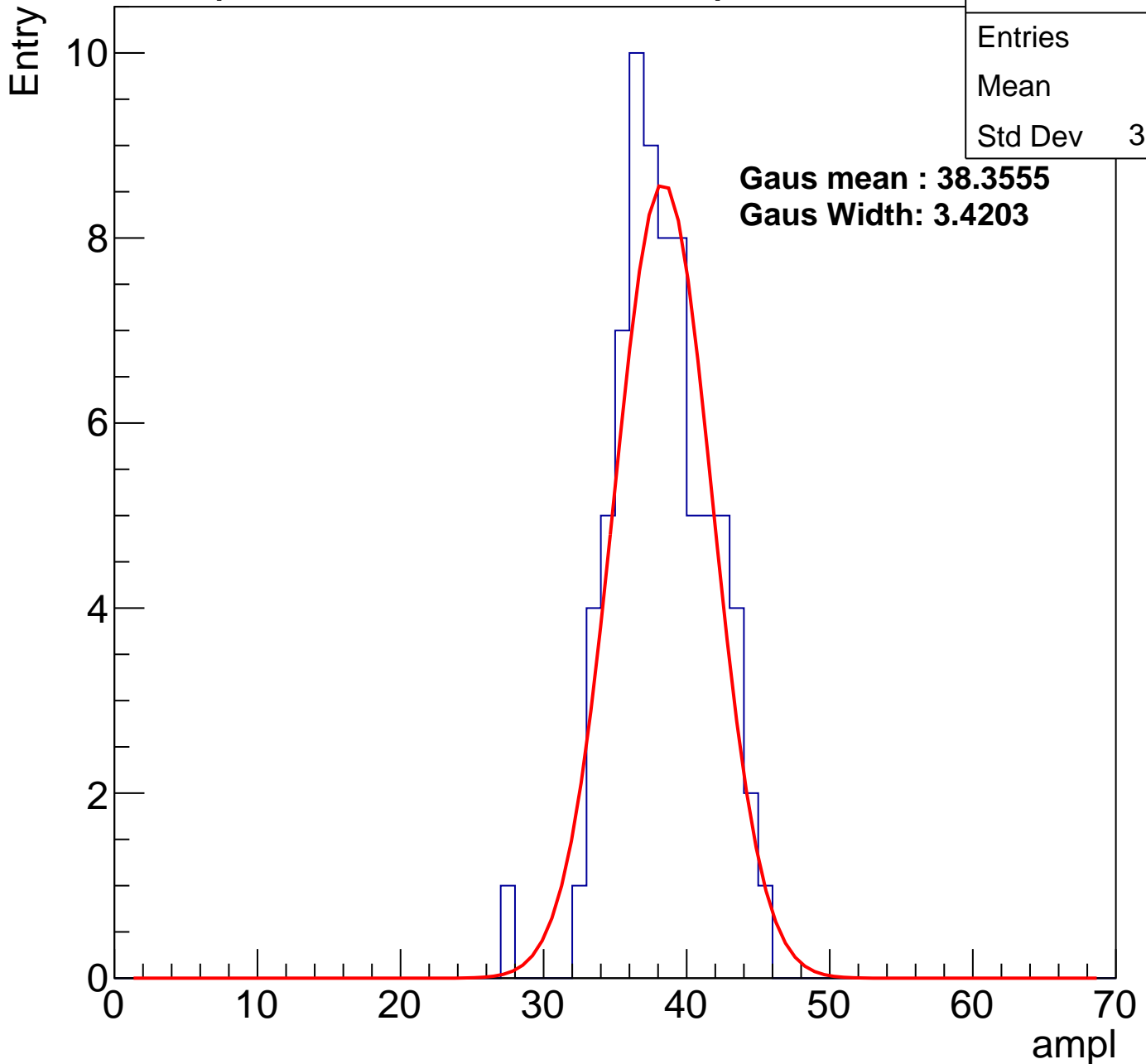


B0L002S, U12-ch10, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	37.8
Std Dev	3.307

Gaus mean : 38.3555
Gaus Width: 3.4203



B0L002S, U12-ch10, adc2

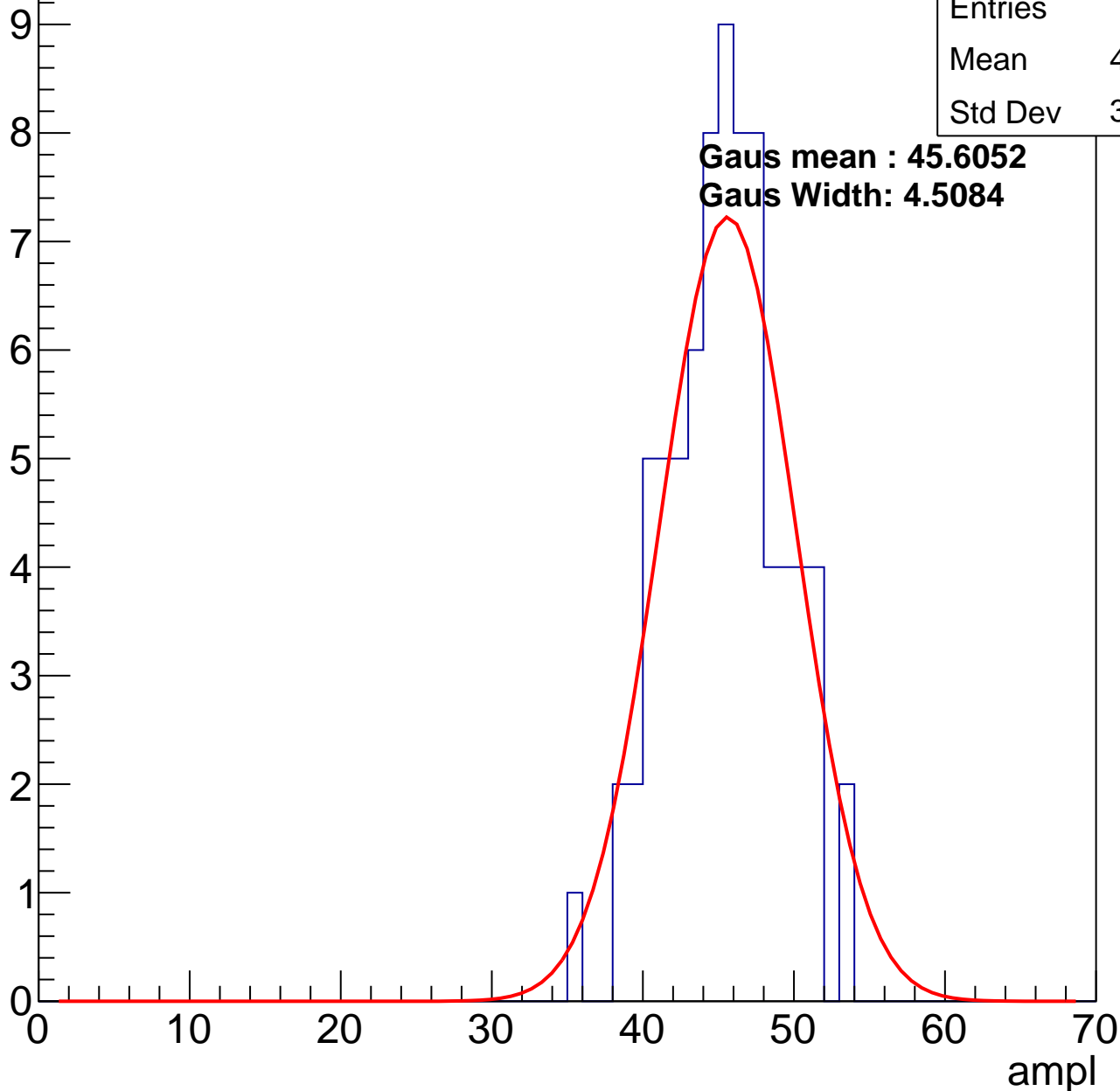
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	44.95
Std Dev	3.717

Gaus mean : 45.6052

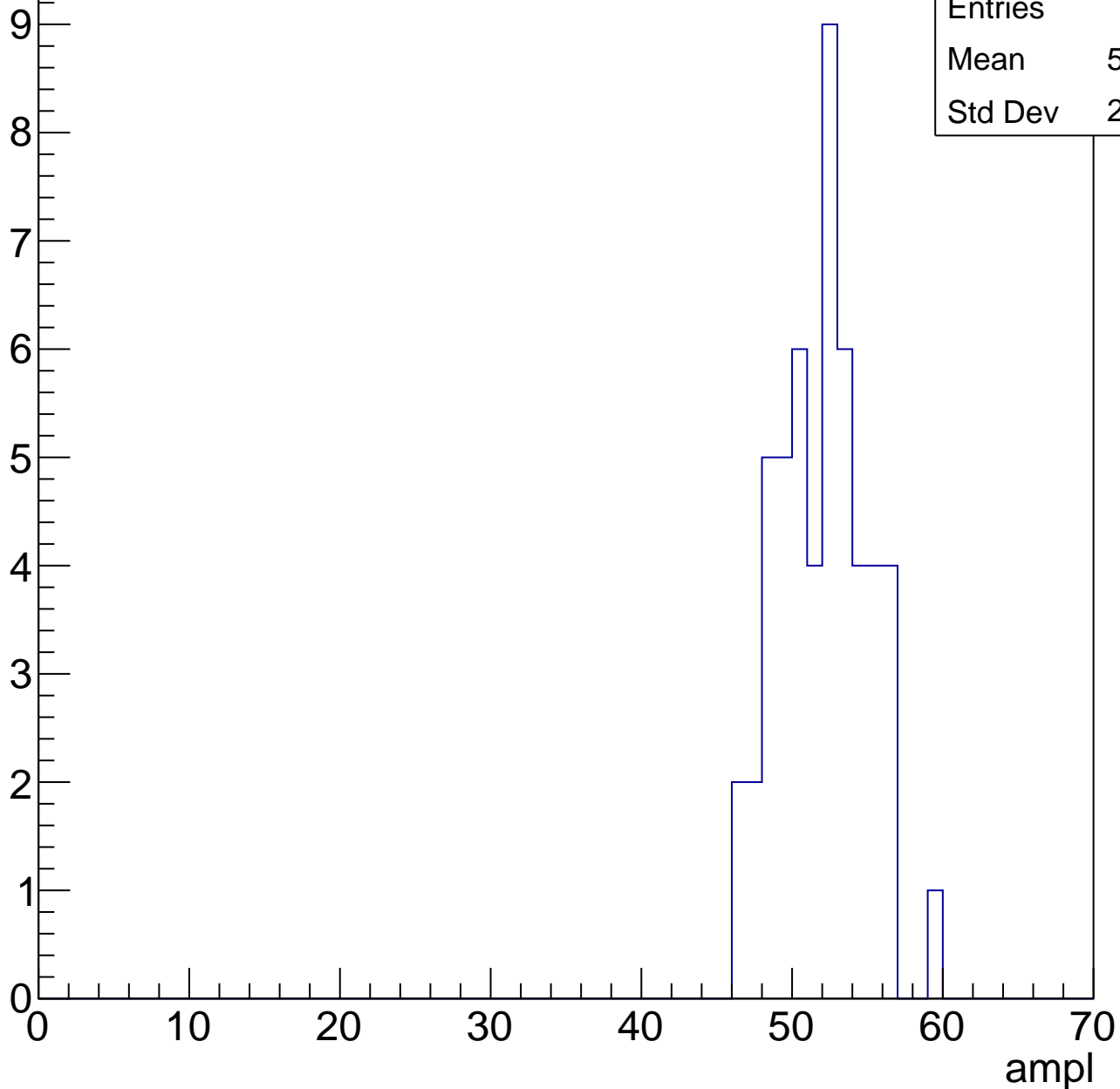
Gaus Width: 4.5084



B0L002S, U12-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



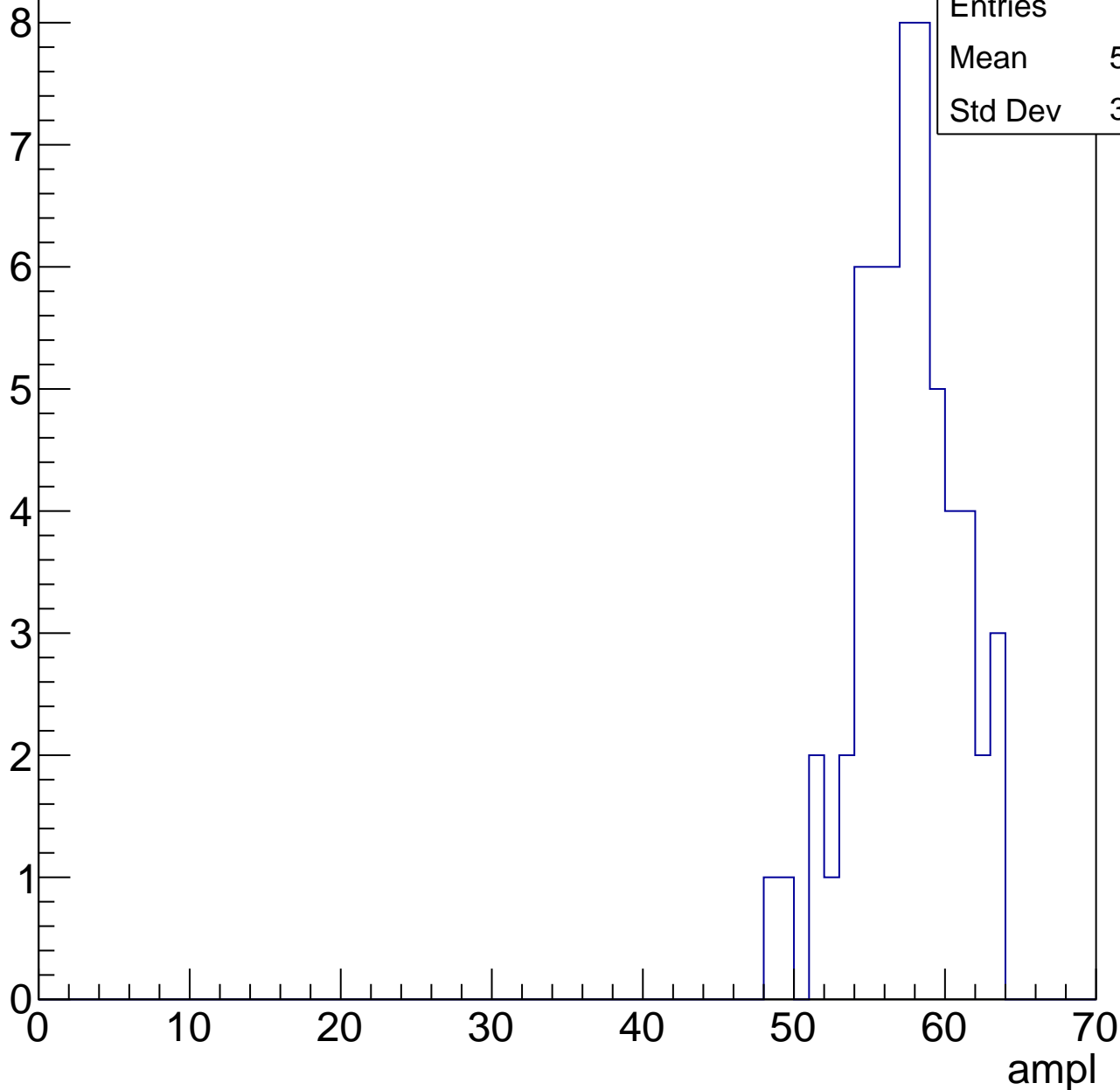
Entries	52
Mean	51.54
Std Dev	2.892

B0L002S, U12-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.93
Std Dev	3.303

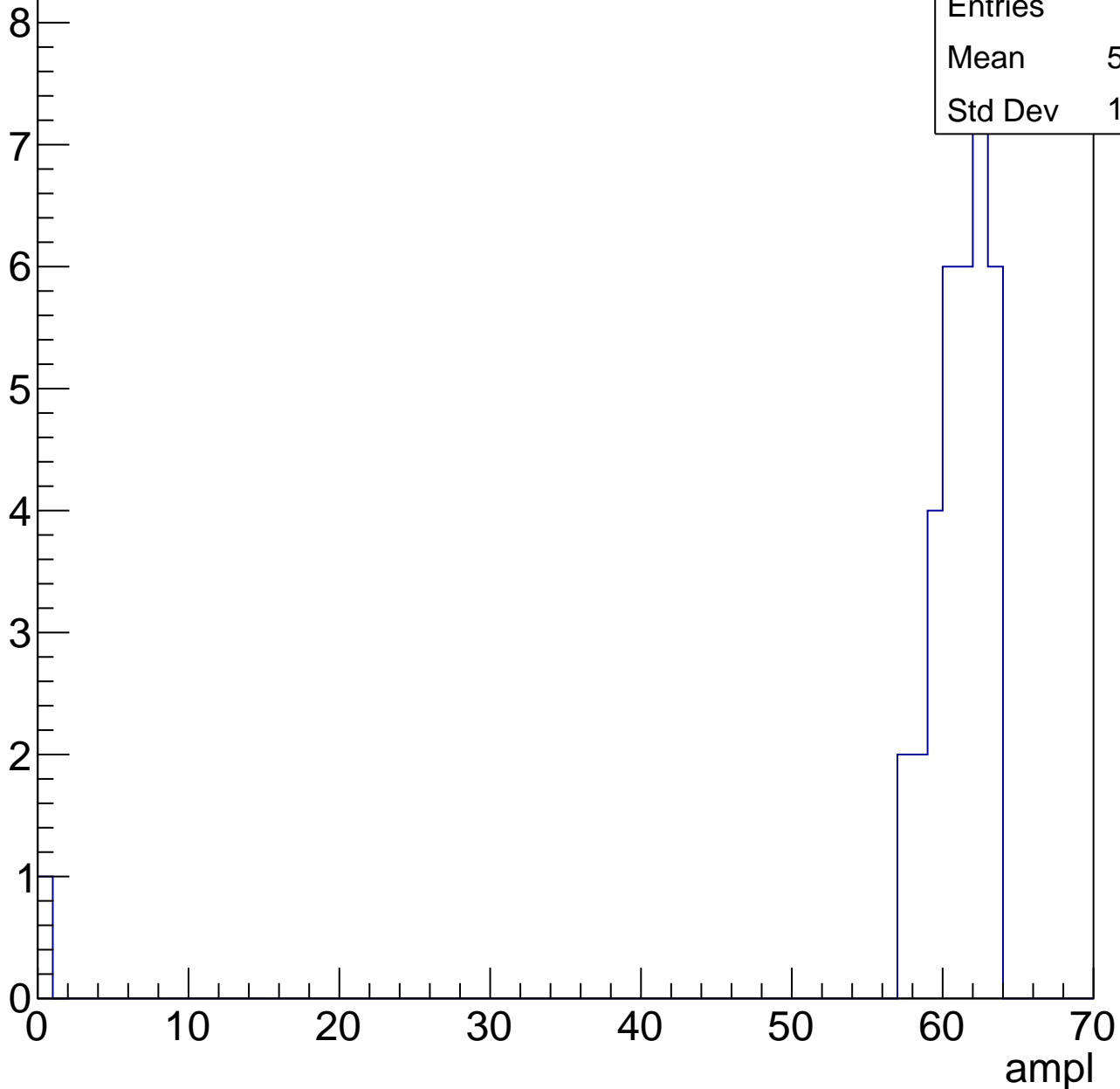


B0L002S, U12-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.03
Std Dev	10.27



B0L002S, U12-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch11, adc0

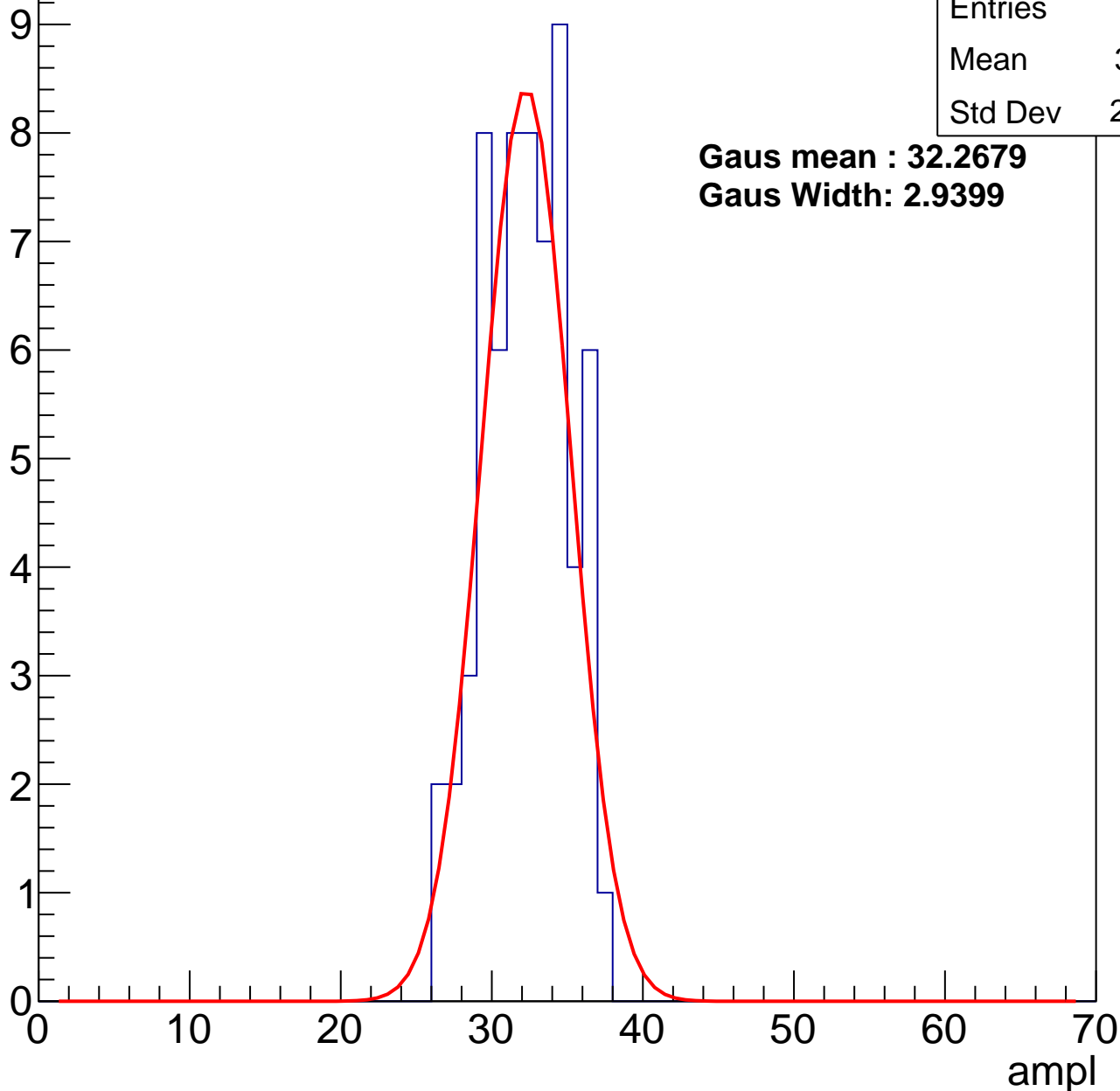
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.81
Std Dev	2.715

Gaus mean : 32.2679

Gaus Width: 2.9399

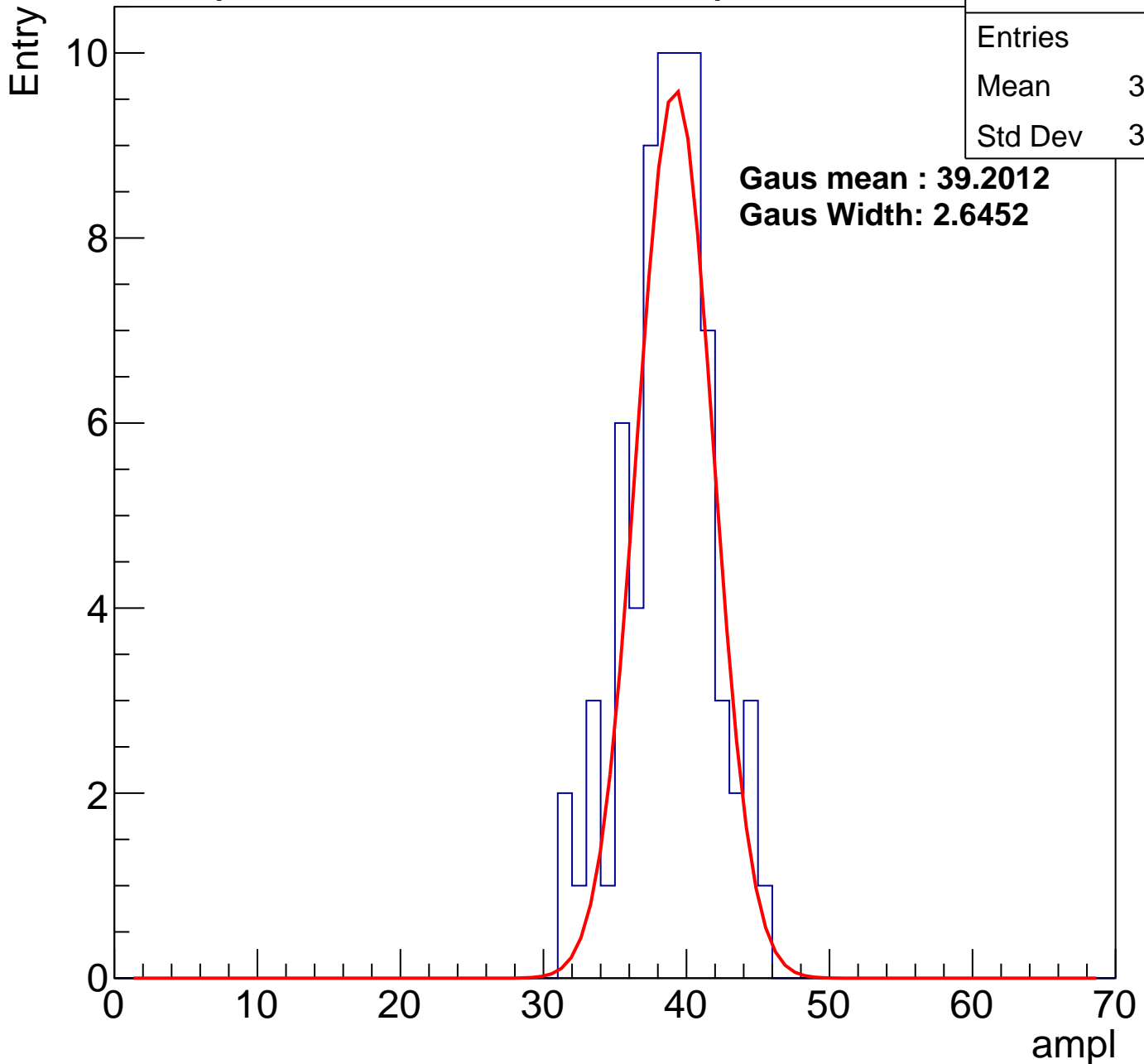


B0L002S, U12-ch11, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	38.33
Std Dev	3.037

Gaus mean : 39.2012
Gaus Width: 2.6452



B0L002S, U12-ch11, adc2

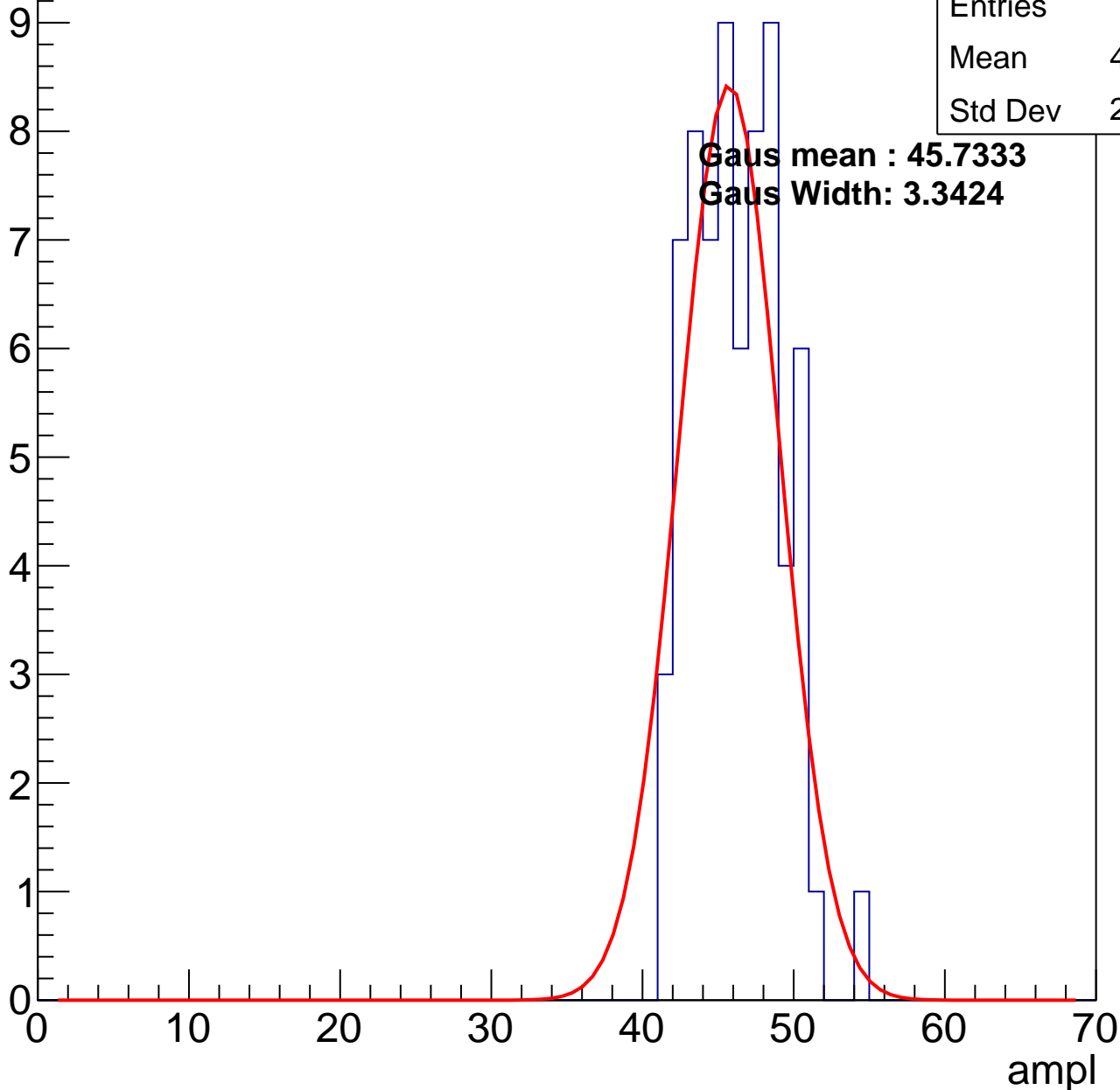
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	45.78
Std Dev	2.843

Gaus mean : 45.7333

Gaus Width: 3.3424



B0L002S, U12-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

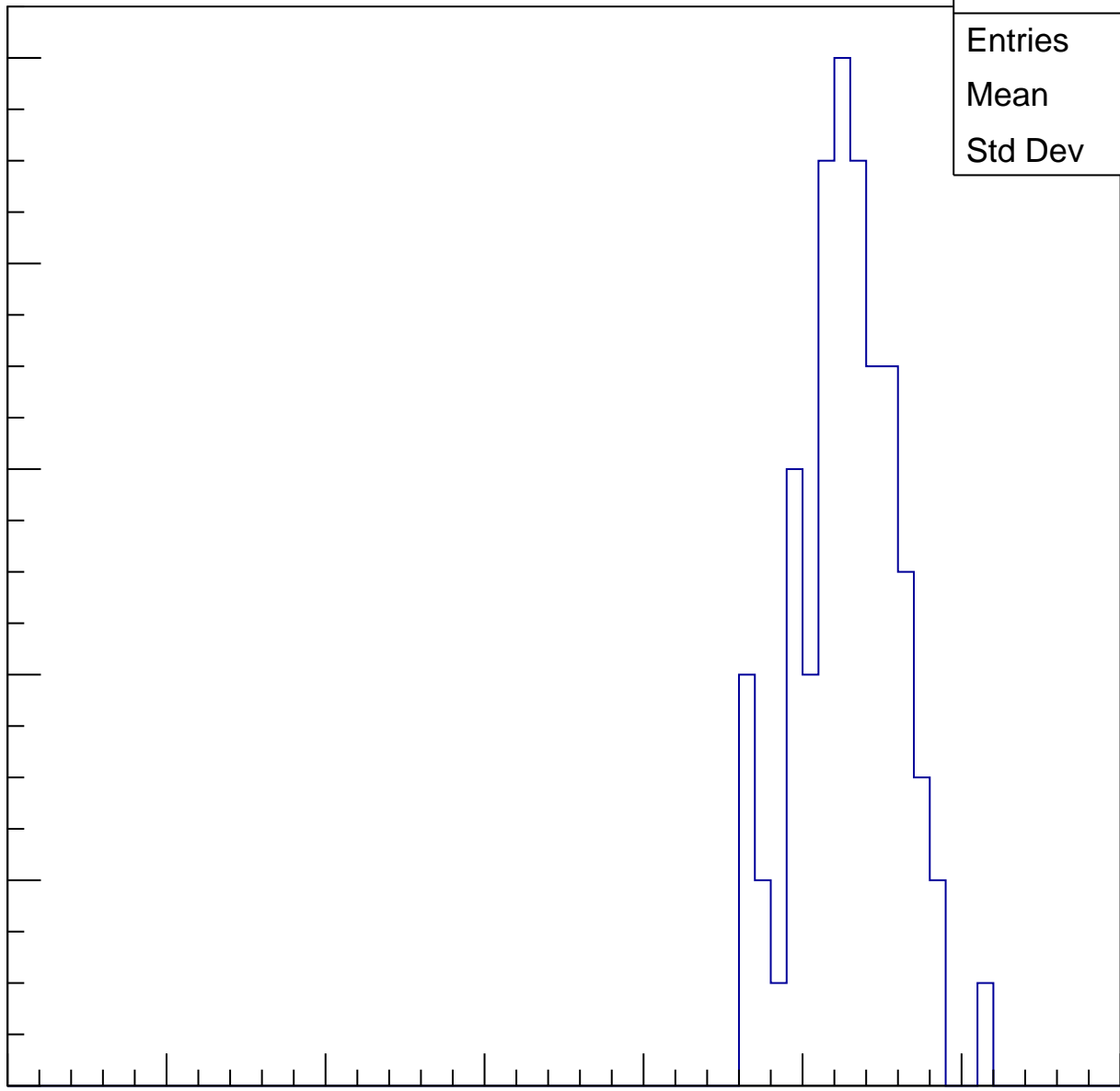
Entries	70
Mean	52.39
Std Dev	3.132

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

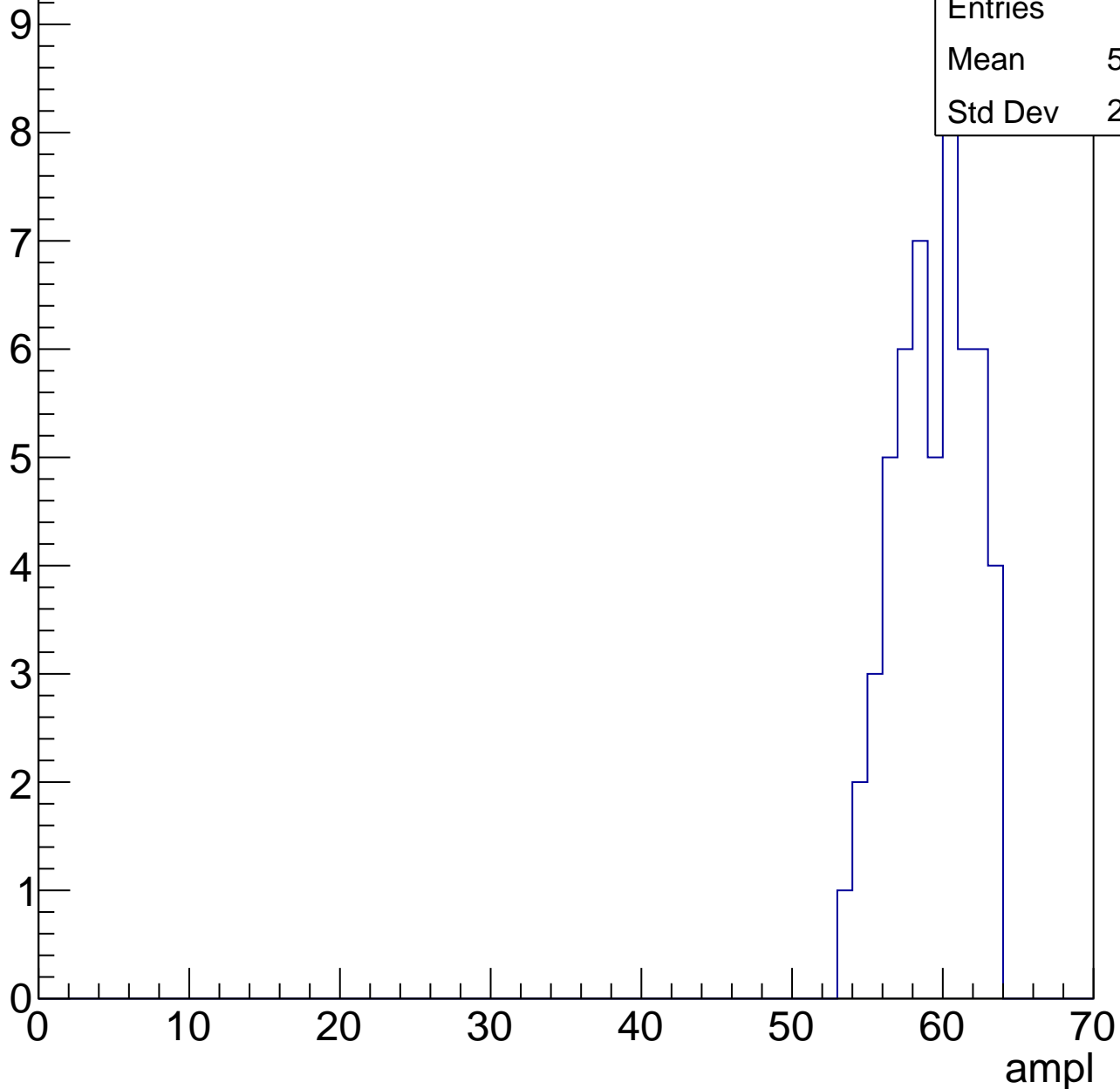
ampl



B0L002S, U12-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



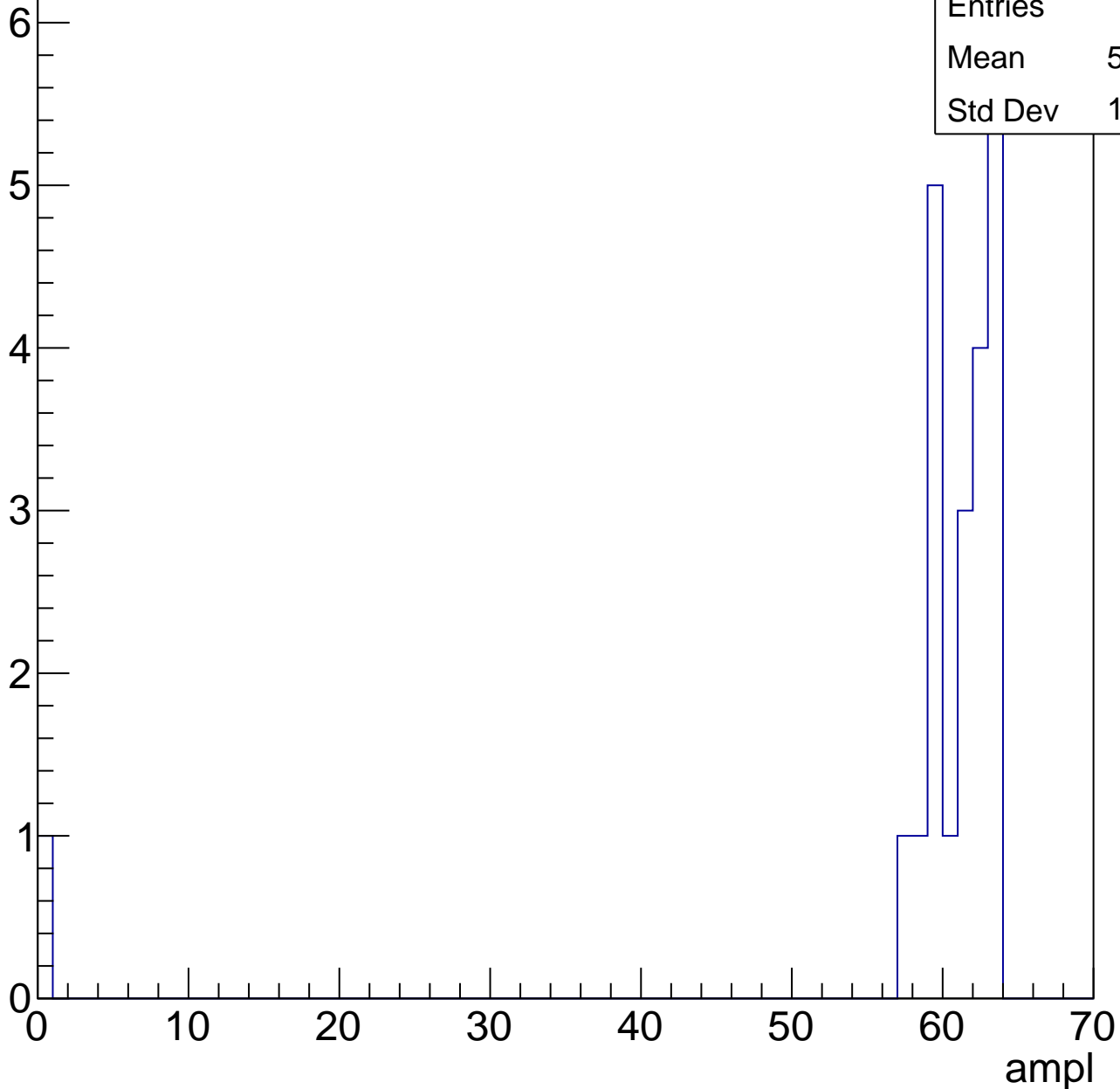
Entries	54
Mean	58.87
Std Dev	2.582

B0L002S, U12-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	58.14
Std Dev	12.82



B0L002S, U12-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch12, adc0

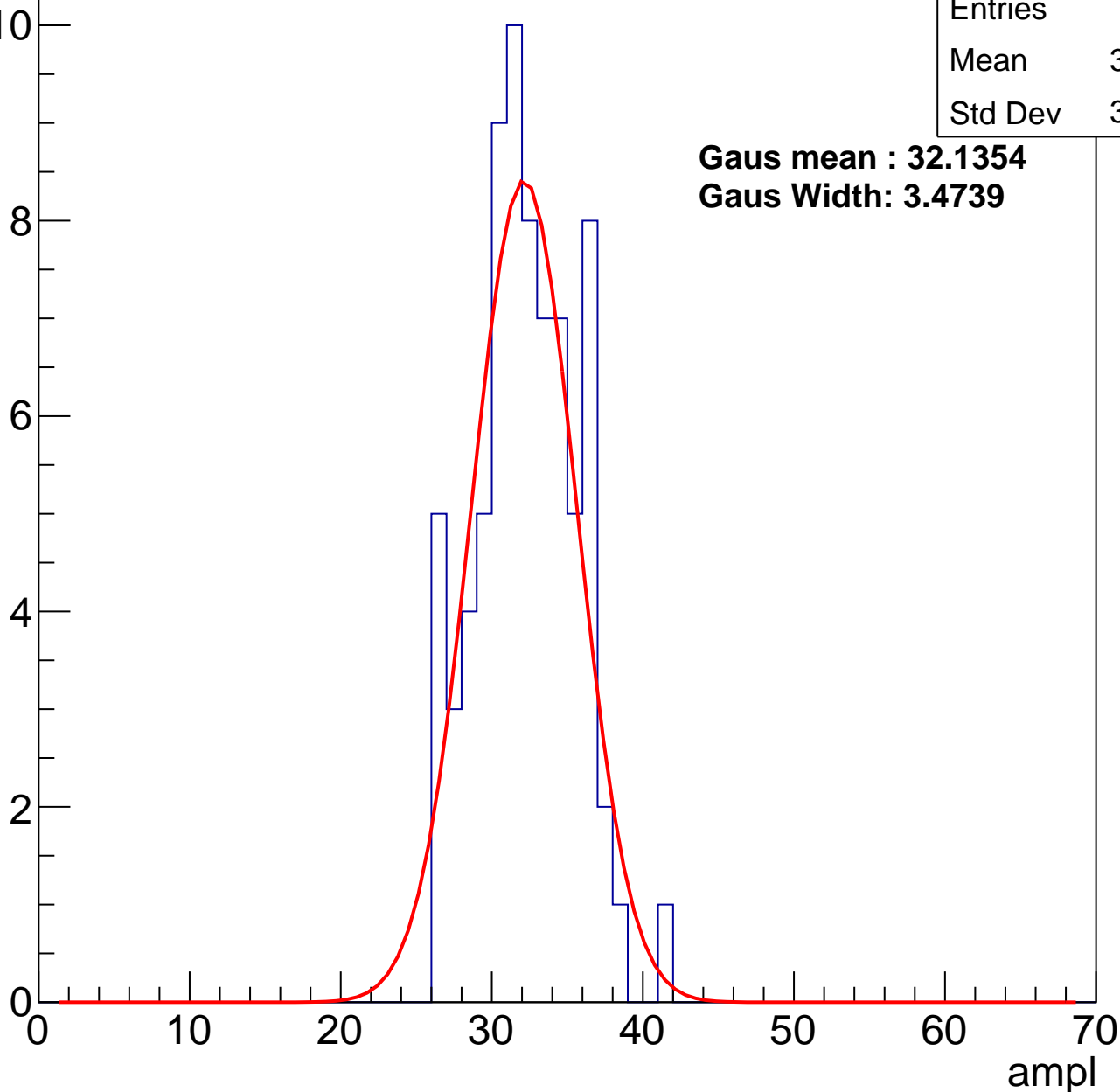
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.85
Std Dev	3.232

Gaus mean : 32.1354

Gaus Width: 3.4739



B0L002S, U12-ch12, adc1

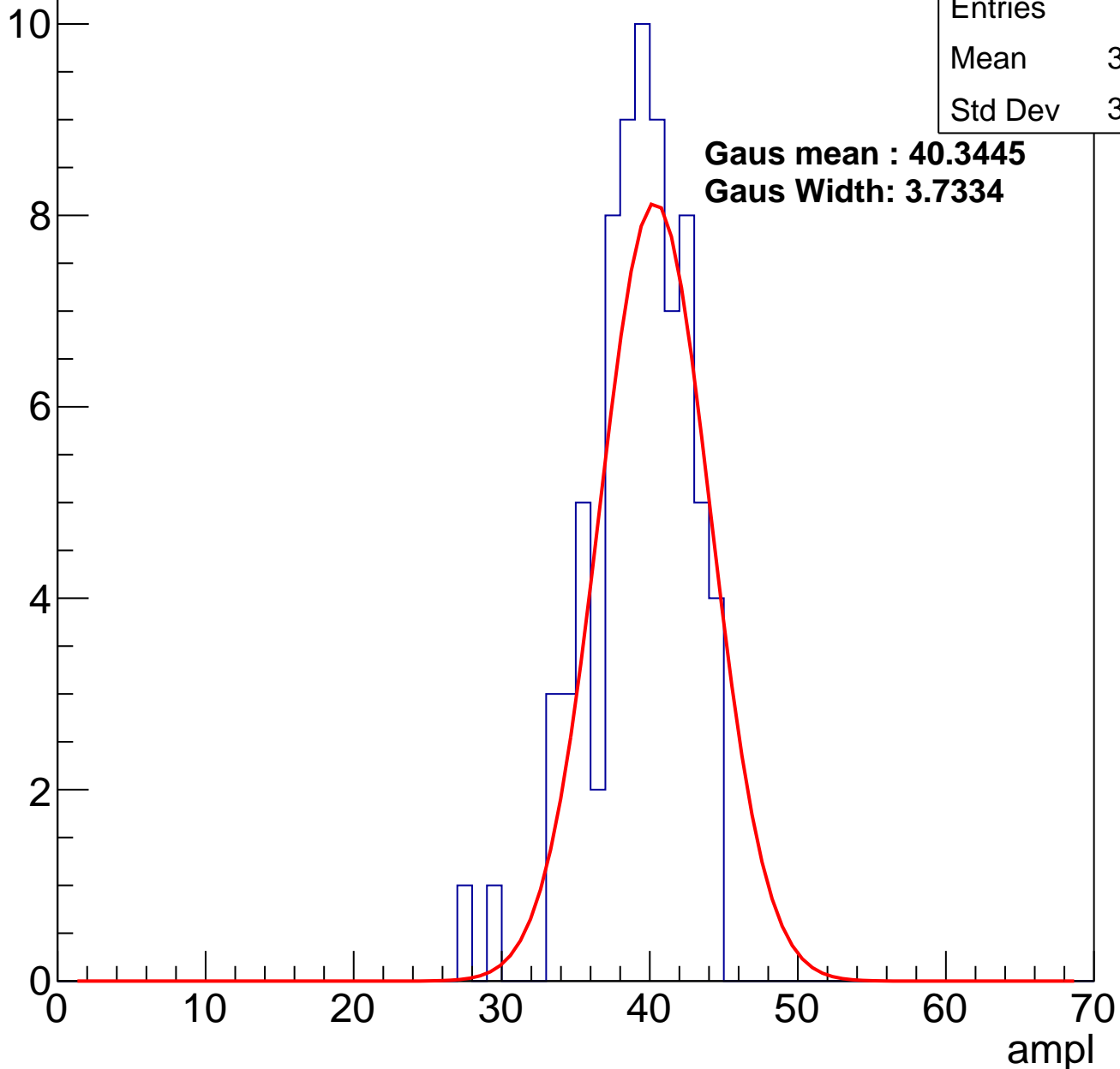
calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	38.75
Std Dev	3.367

Gaus mean : 40.3445

Gaus Width: 3.7334

Entry



B0L002S, U12-ch12, adc2

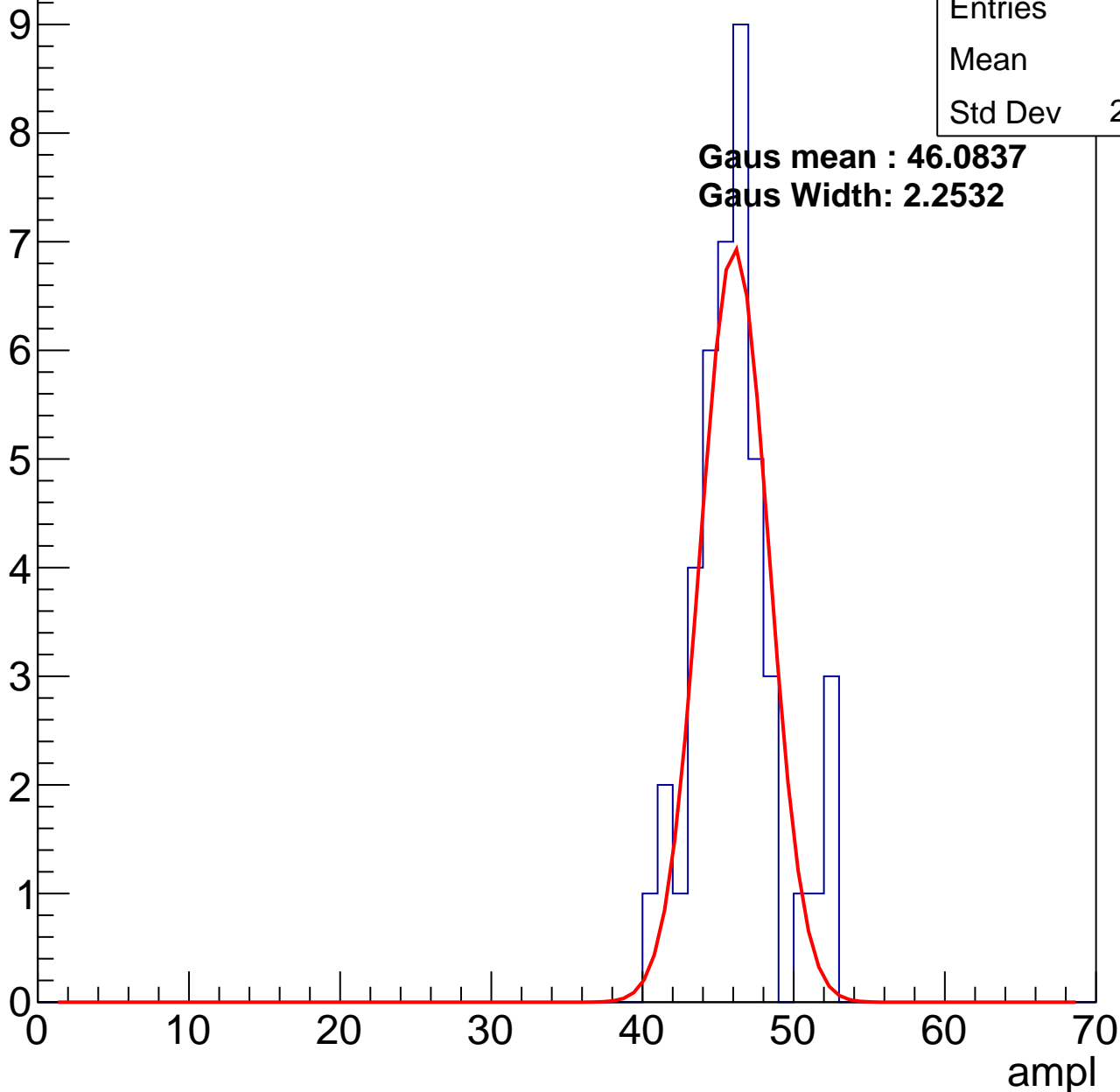
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	45.7
Std Dev	2.775

Gaus mean : 46.0837

Gaus Width: 2.2532



B0L002S, U12-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

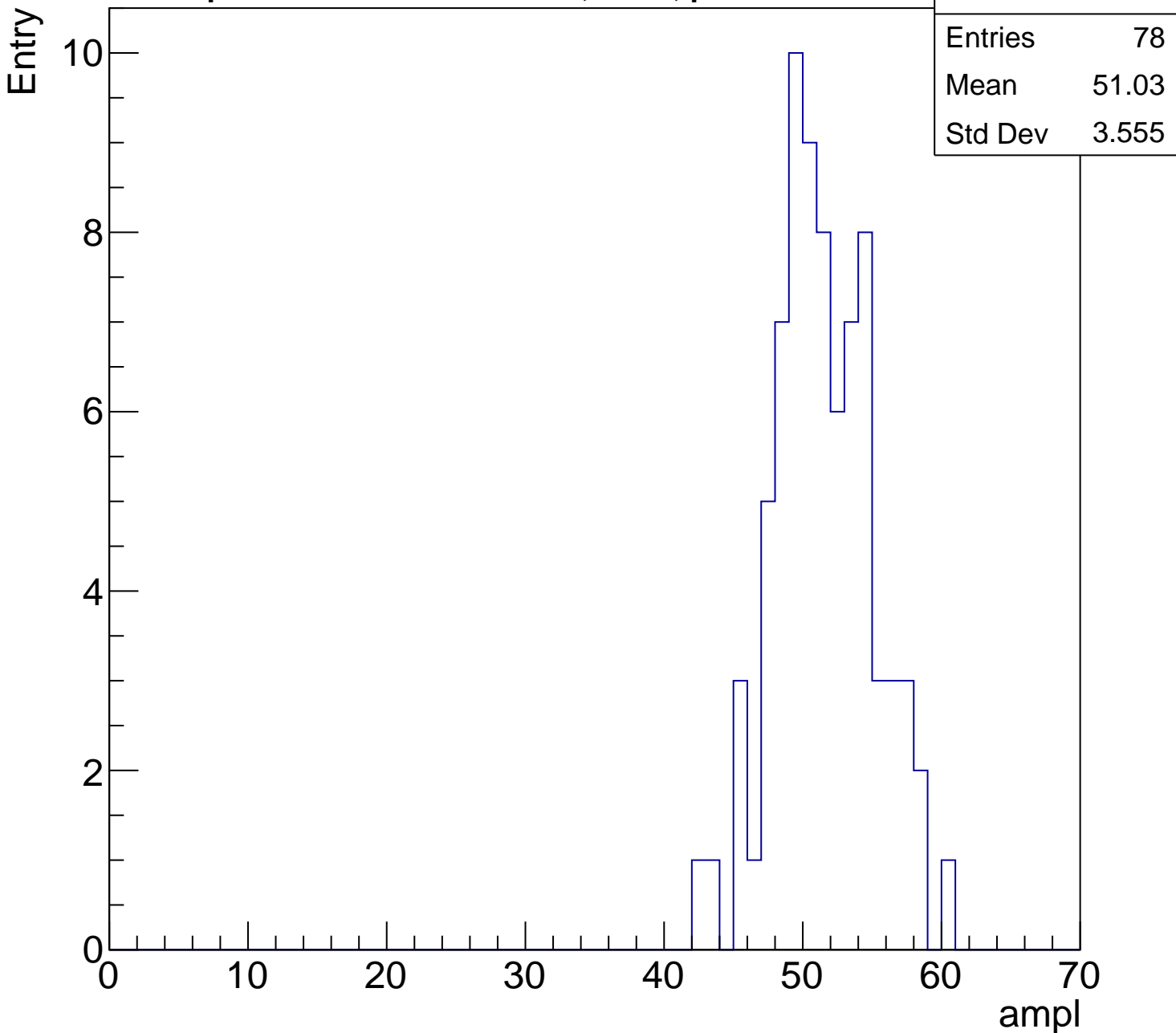
Entries	78
Mean	51.03
Std Dev	3.555

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

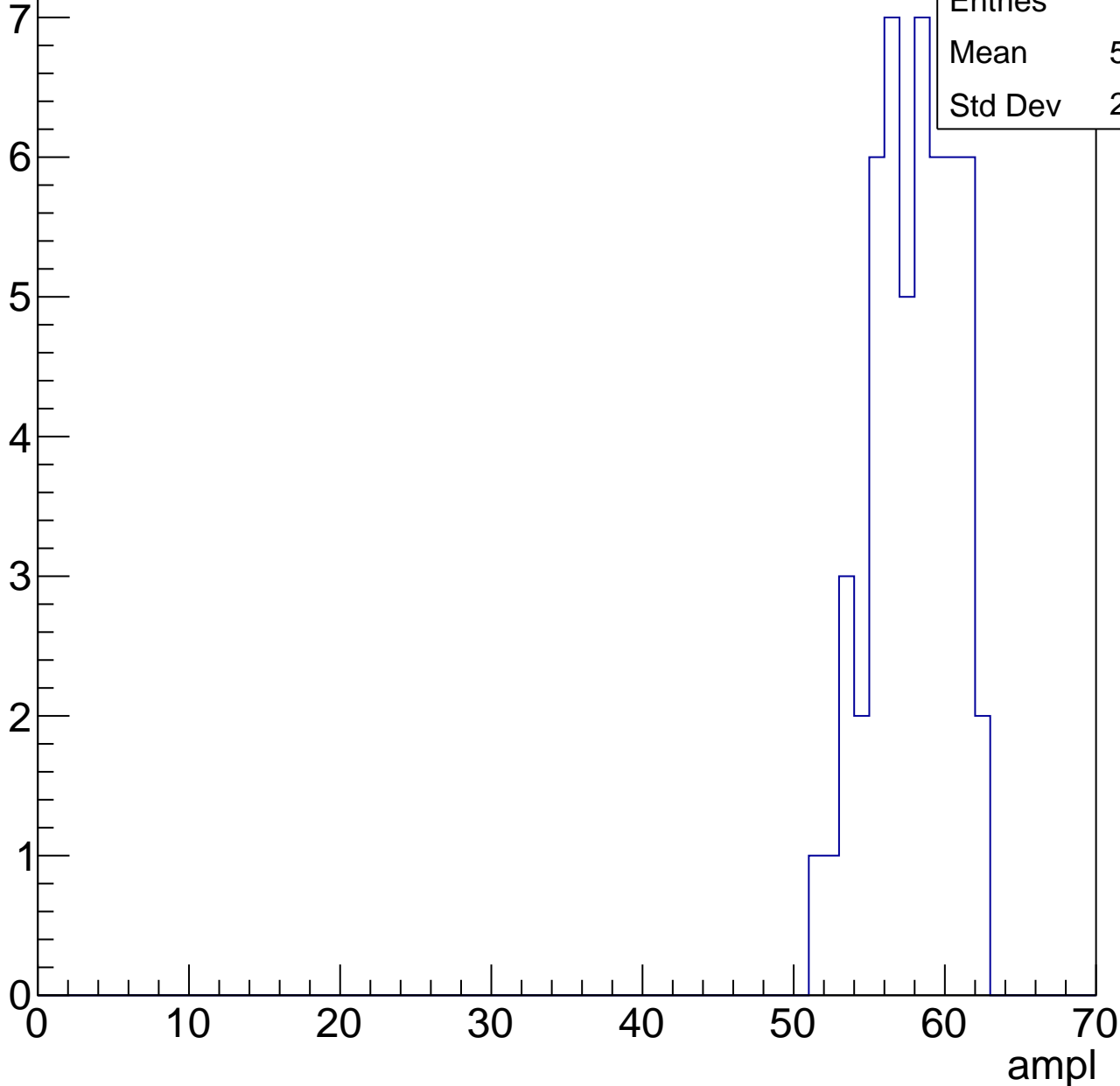


B0L002S, U12-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	57.44
Std Dev	2.699

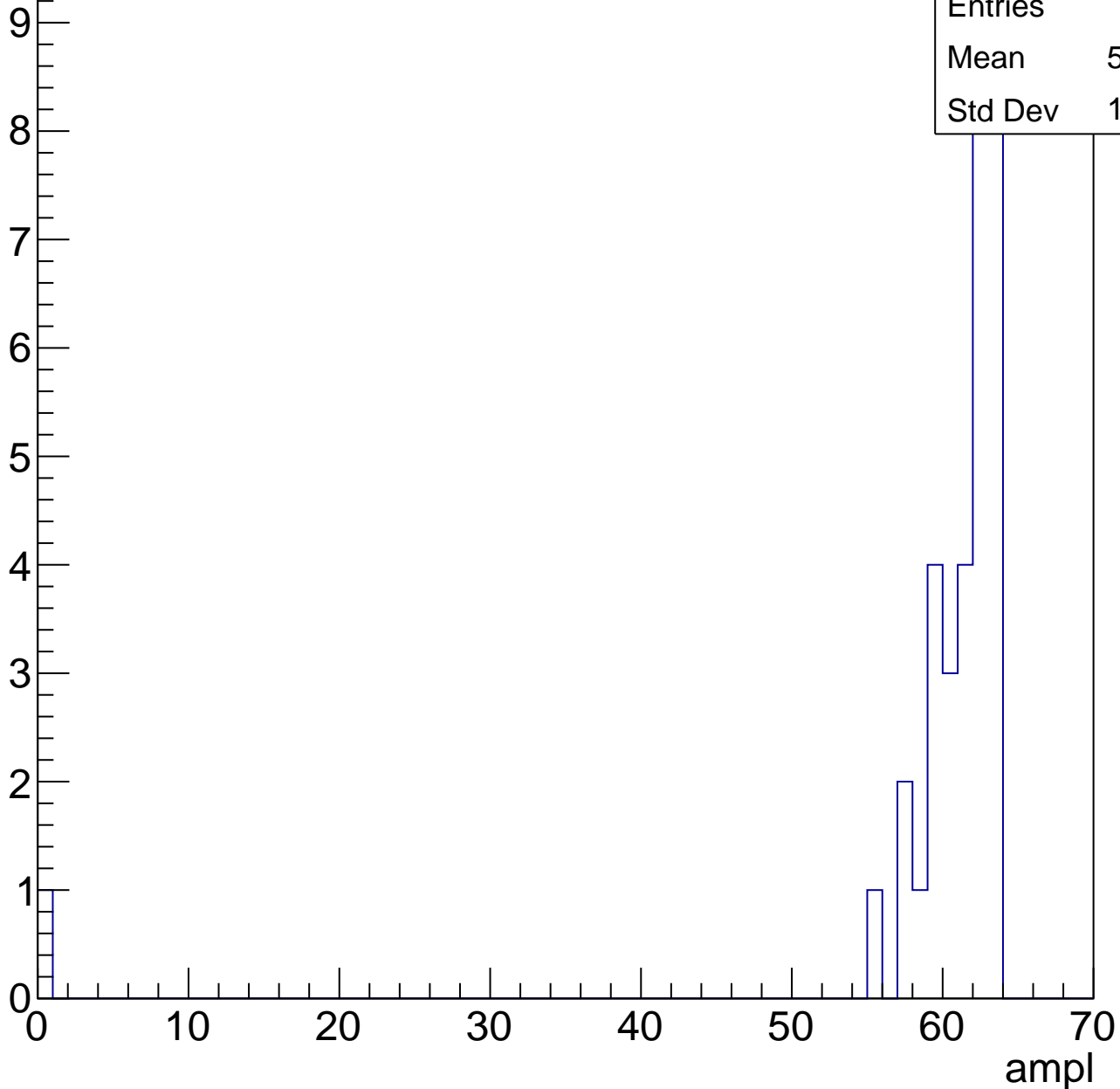


B0L002S, U12-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	59.09
Std Dev	10.65



B0L002S, U12-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U12-ch13, adc0

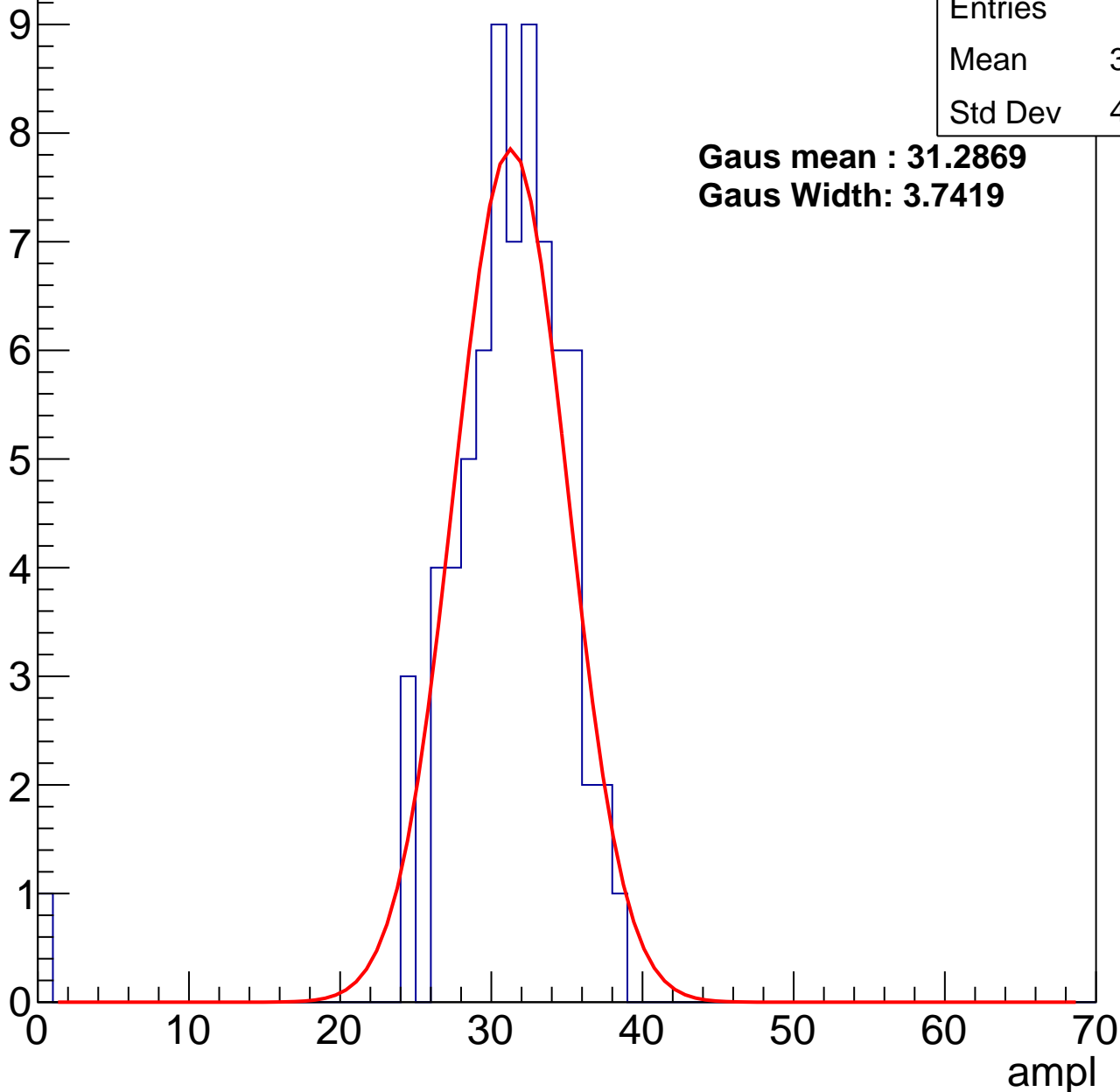
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.58
Std Dev	4.853

Gaus mean : 31.2869

Gaus Width: 3.7419



B0L002S, U12-ch13, adc1

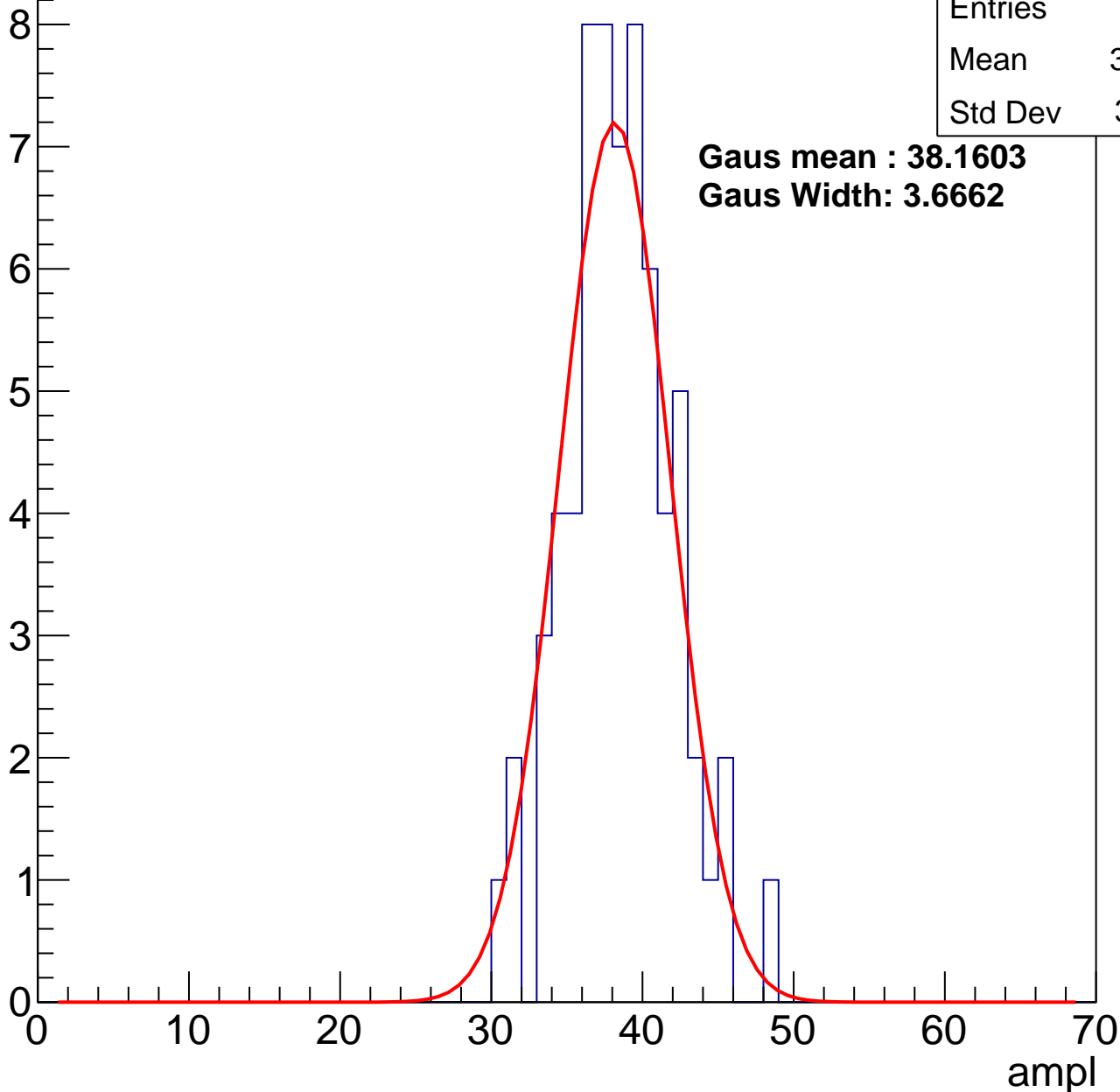
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.05
Std Dev	3.501

Gaus mean : 38.1603

Gaus Width: 3.6662



B0L002S, U12-ch13, adc2

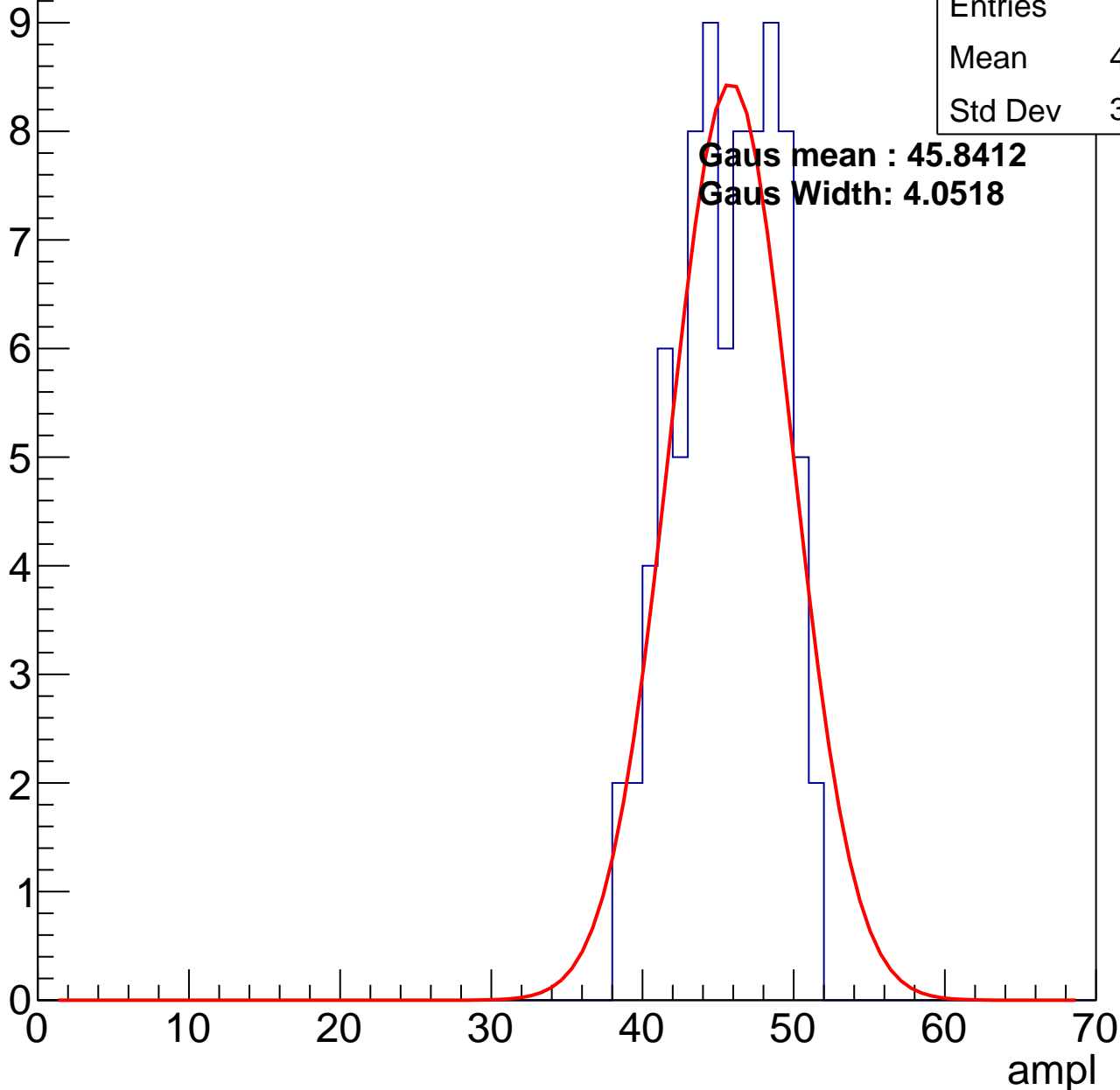
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	45.12
Std Dev	3.307

Gaus mean : 45.8412

Gaus Width: 4.0518

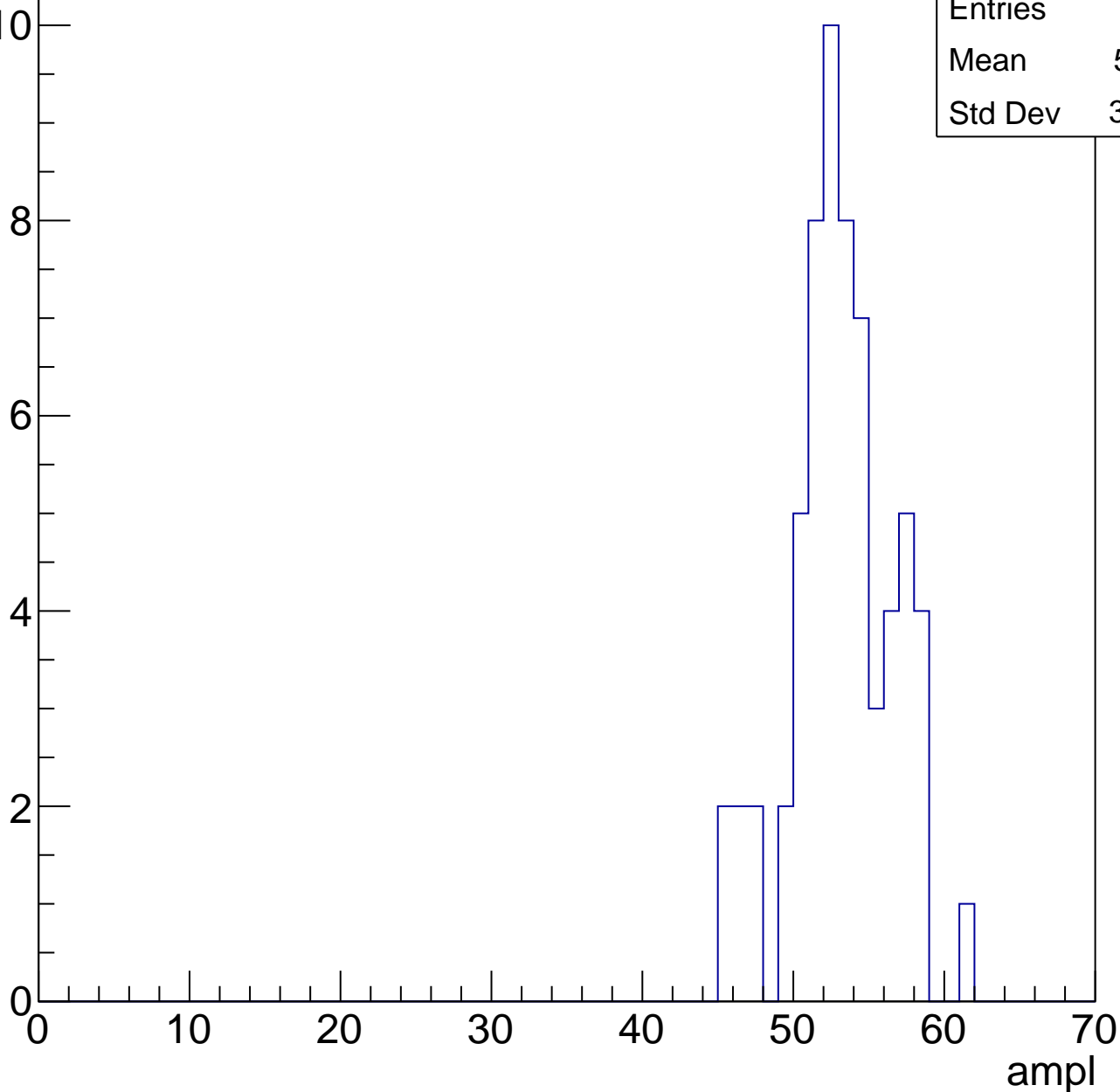


B0L002S, U12-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	52.71
Std Dev	3.359

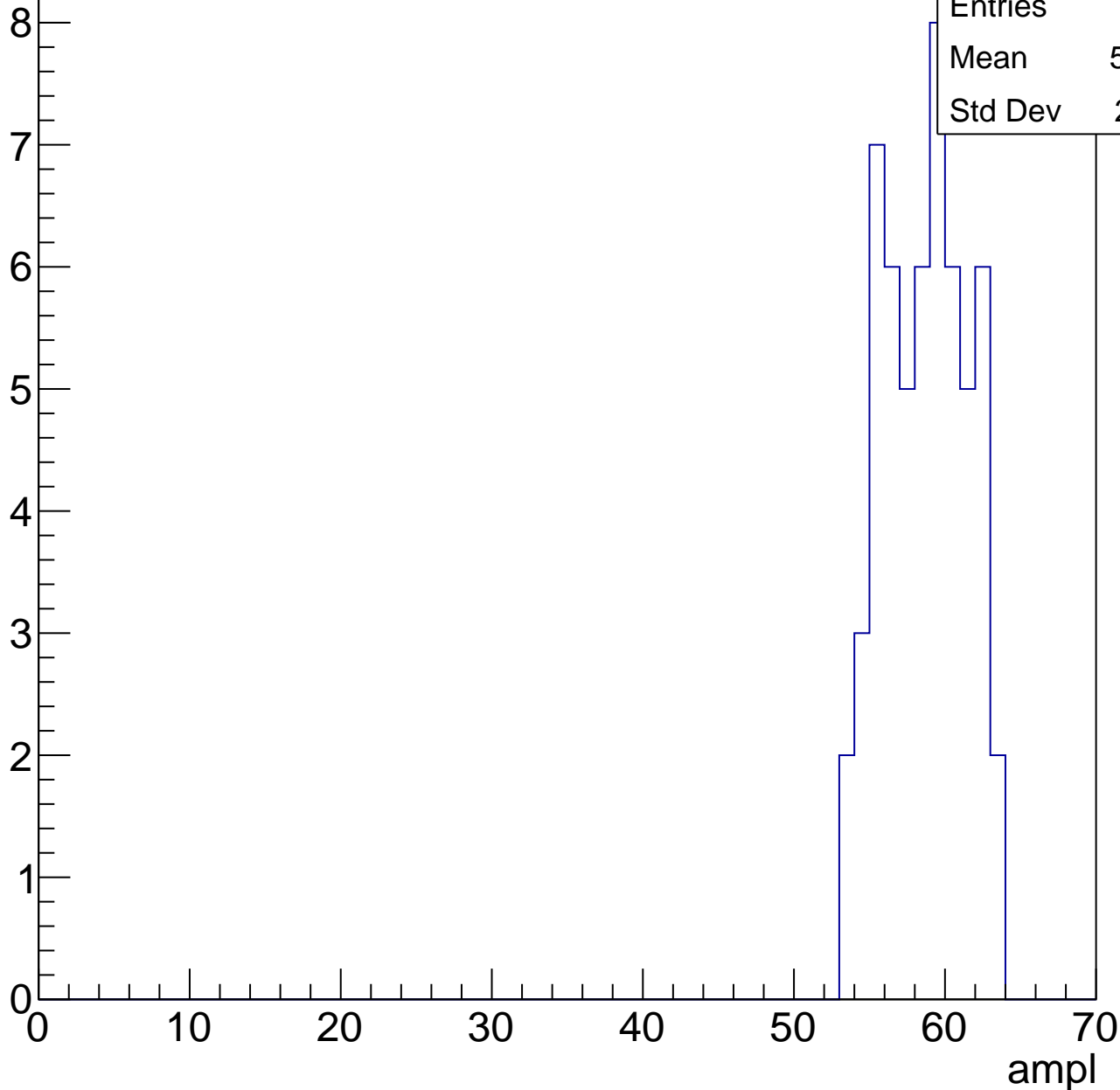


B0L002S, U12-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	58.16
Std Dev	2.711

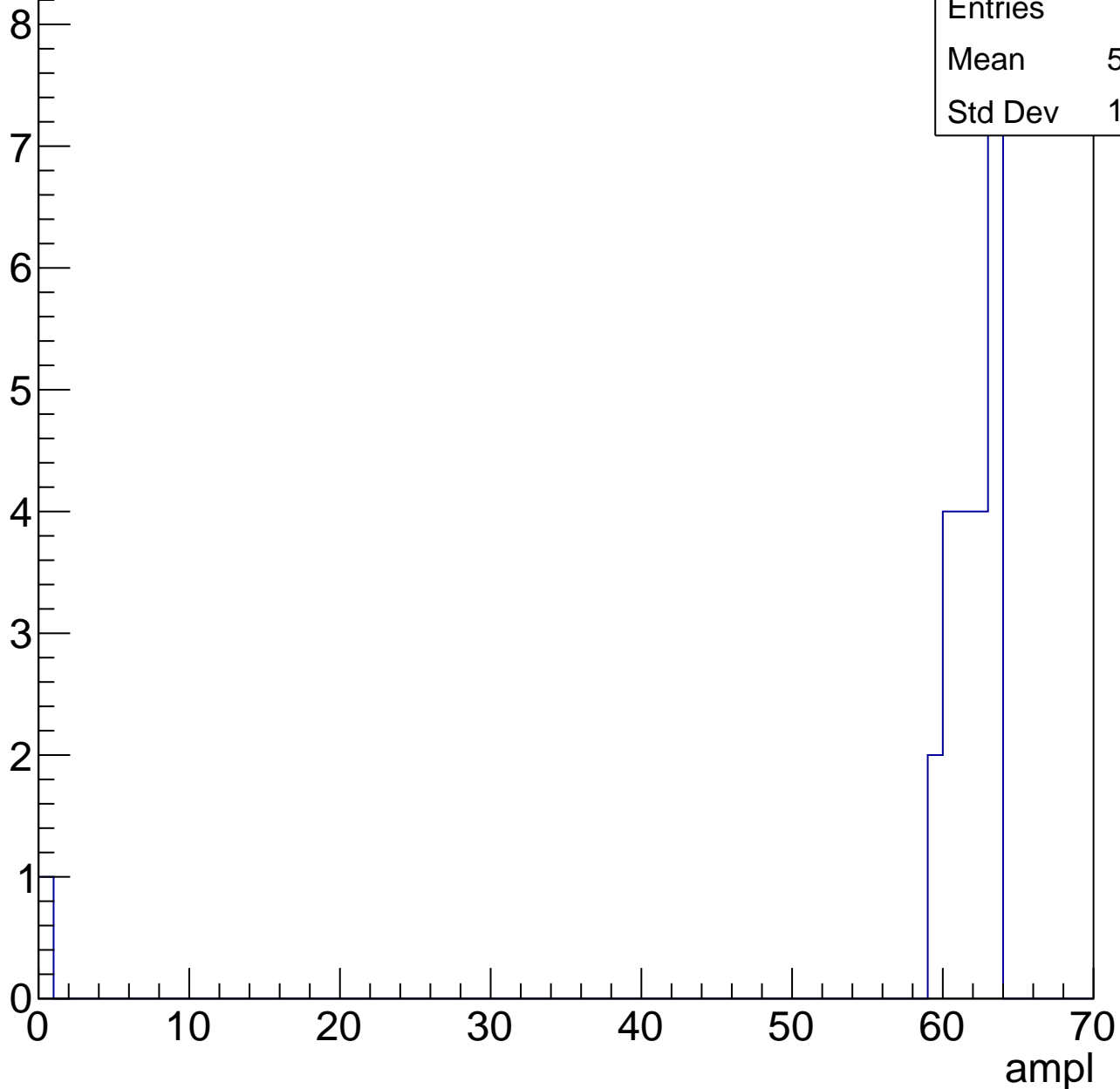


B0L002S, U12-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.87
Std Dev	12.62



B0L002S, U12-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L002S, U12-ch14, adc0

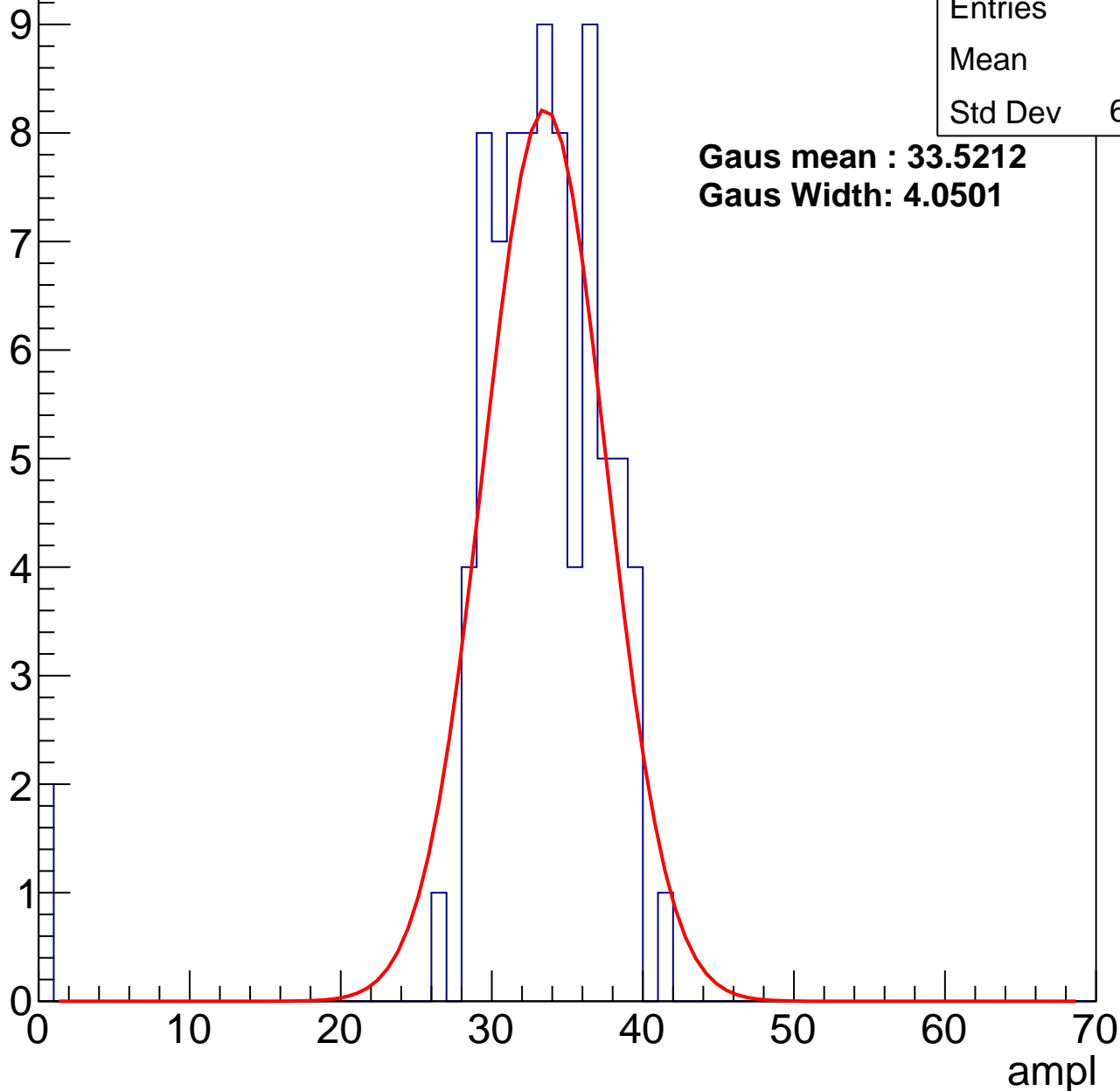
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	32.4
Std Dev	6.058

Gaus mean : 33.5212

Gaus Width: 4.0501

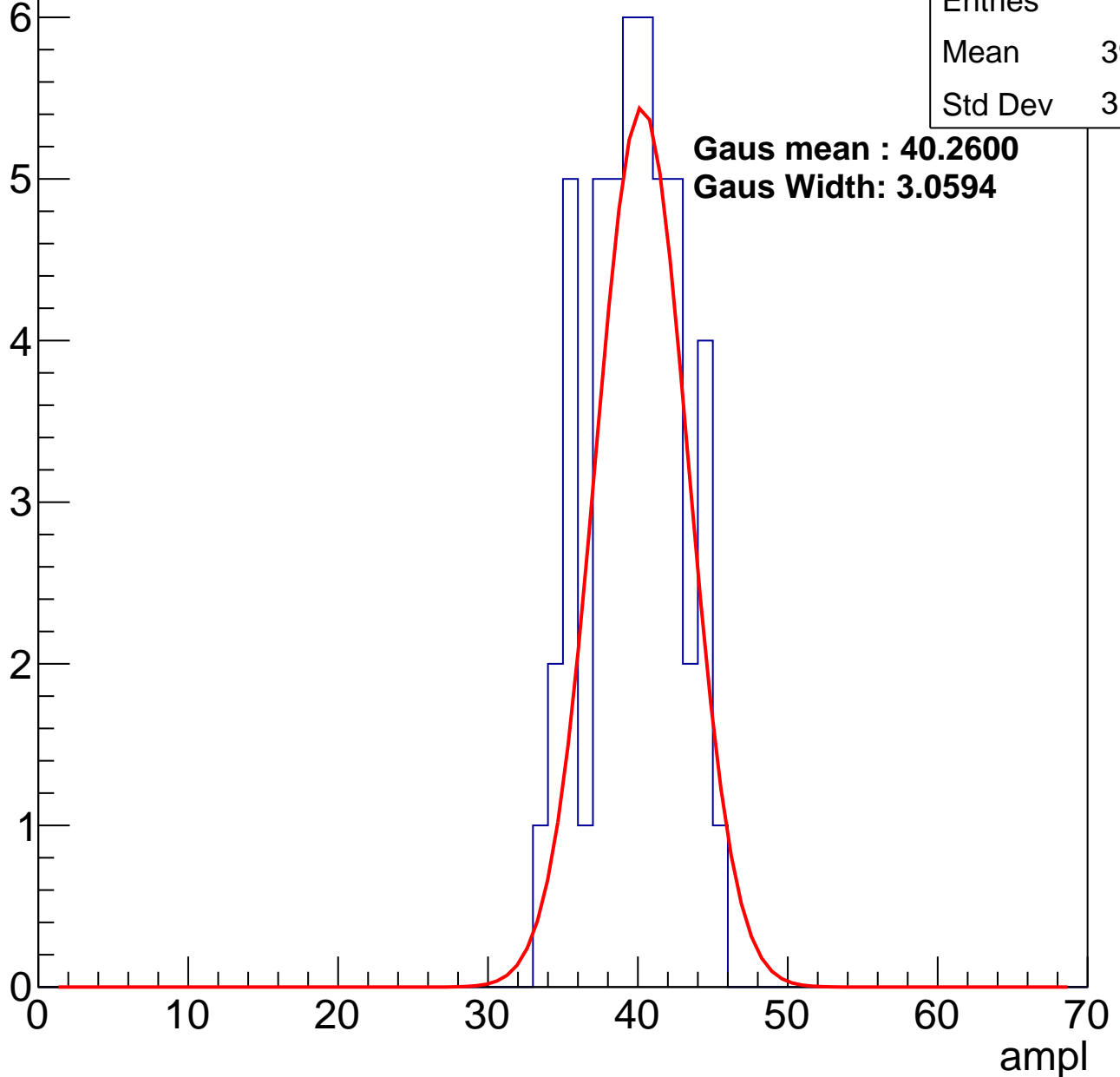


B0L002S, U12-ch14, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	39.23
Std Dev	3.016



B0L002S, U12-ch14, adc2

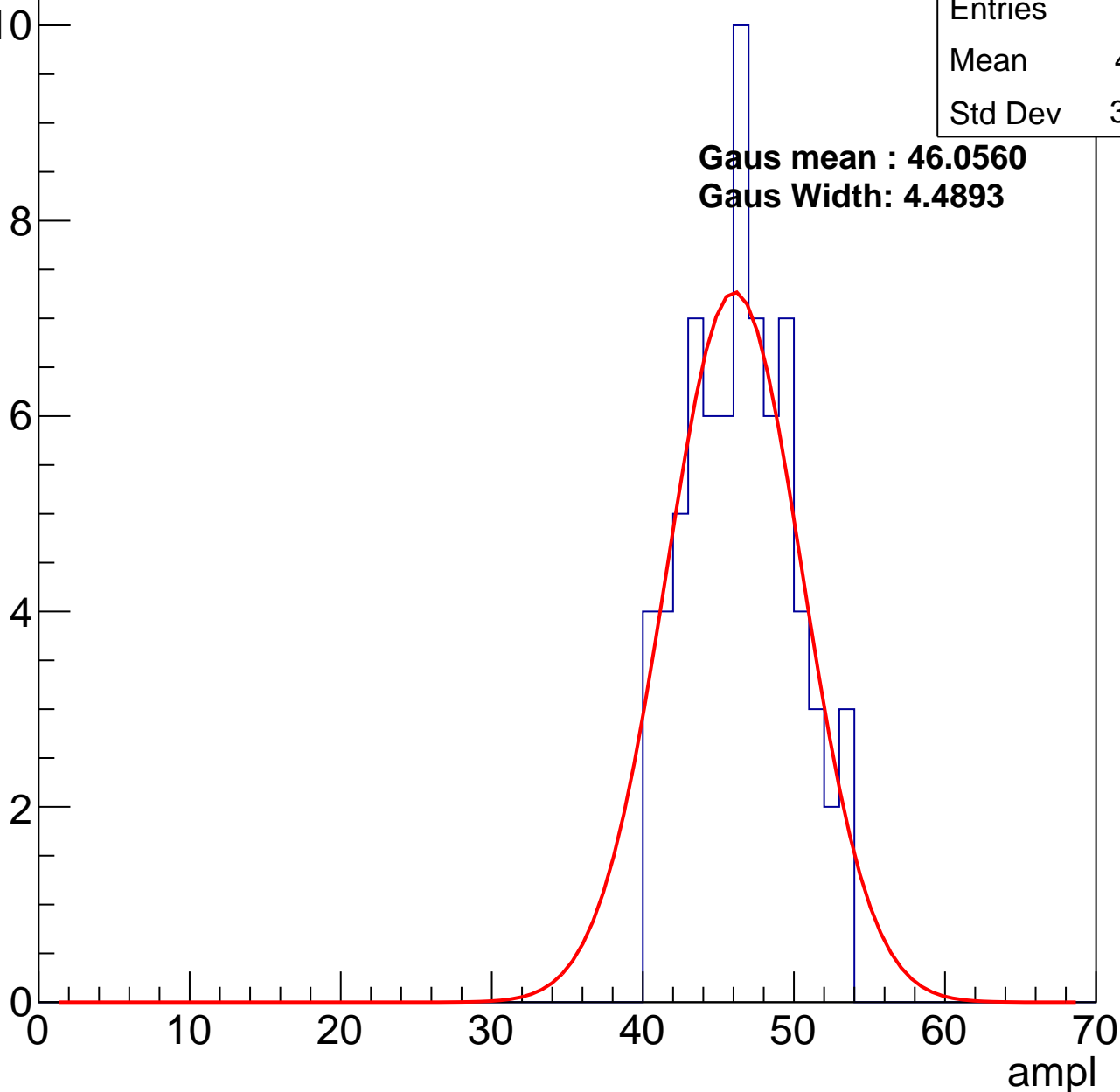
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	46.01
Std Dev	3.427

Gaus mean : 46.0560

Gaus Width: 4.4893

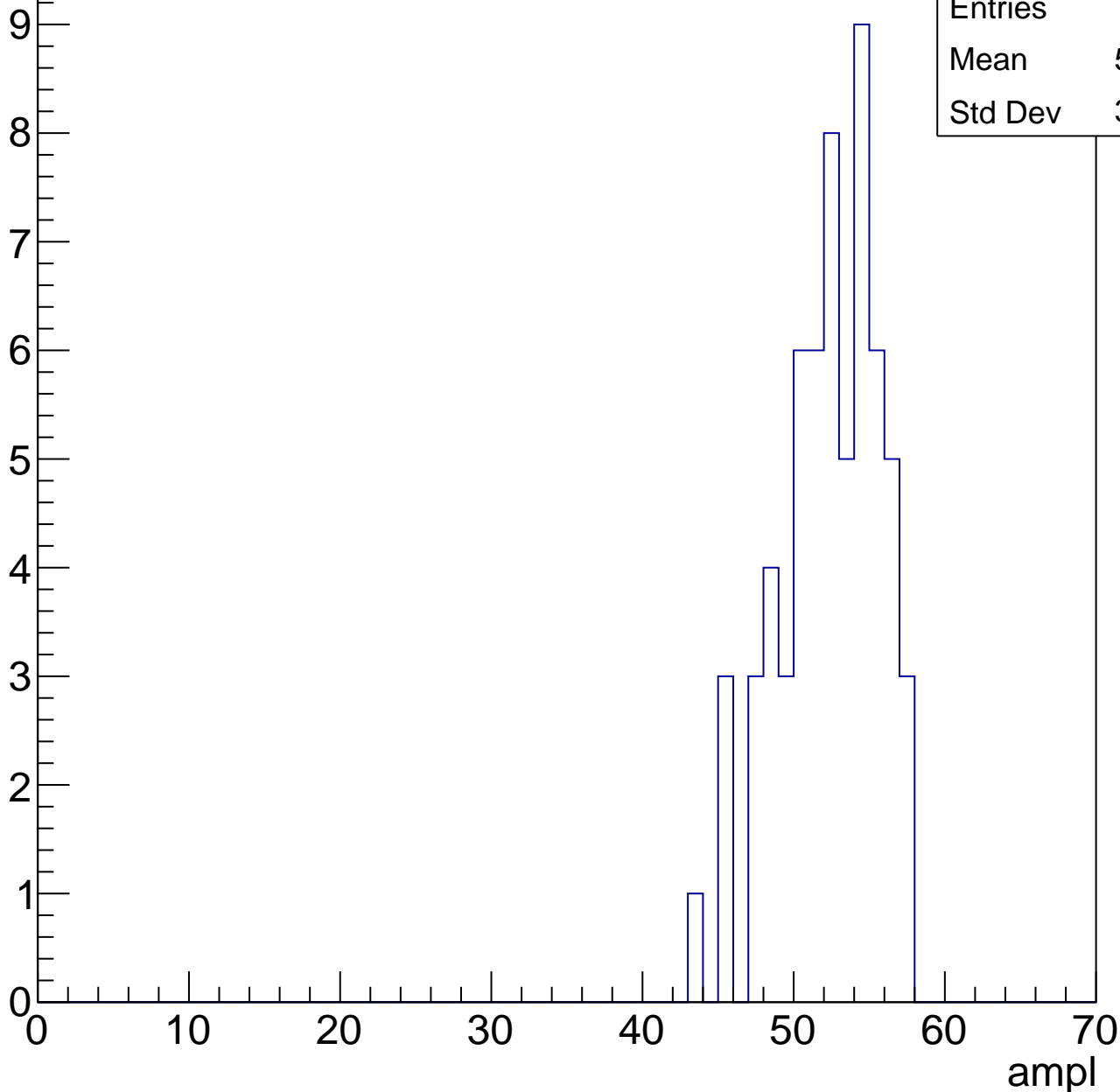


B0L002S, U12-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	51.81
Std Dev	3.291

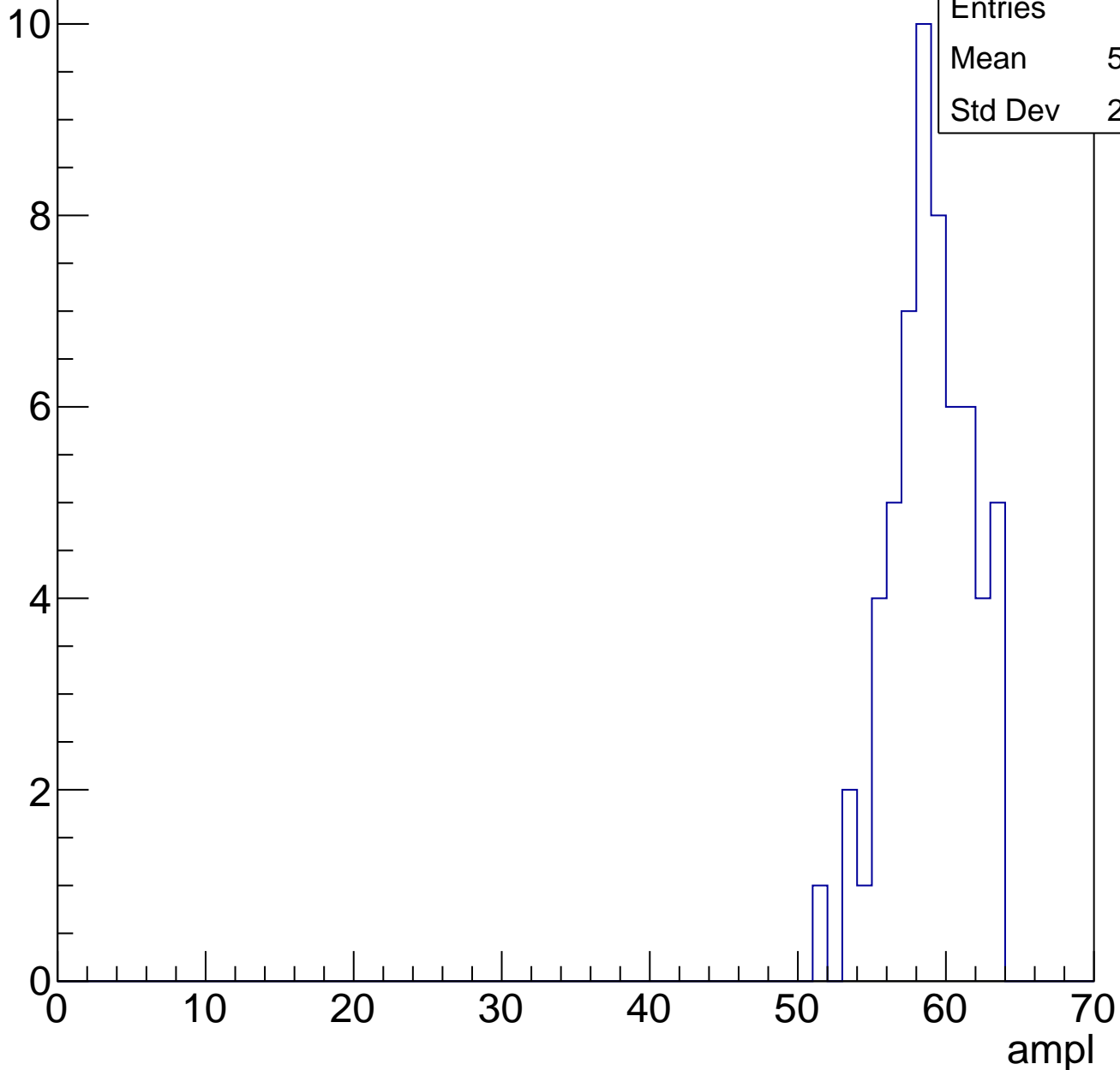


B0L002S, U12-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	59
Mean	58.49
Std Dev	2.727

Entry

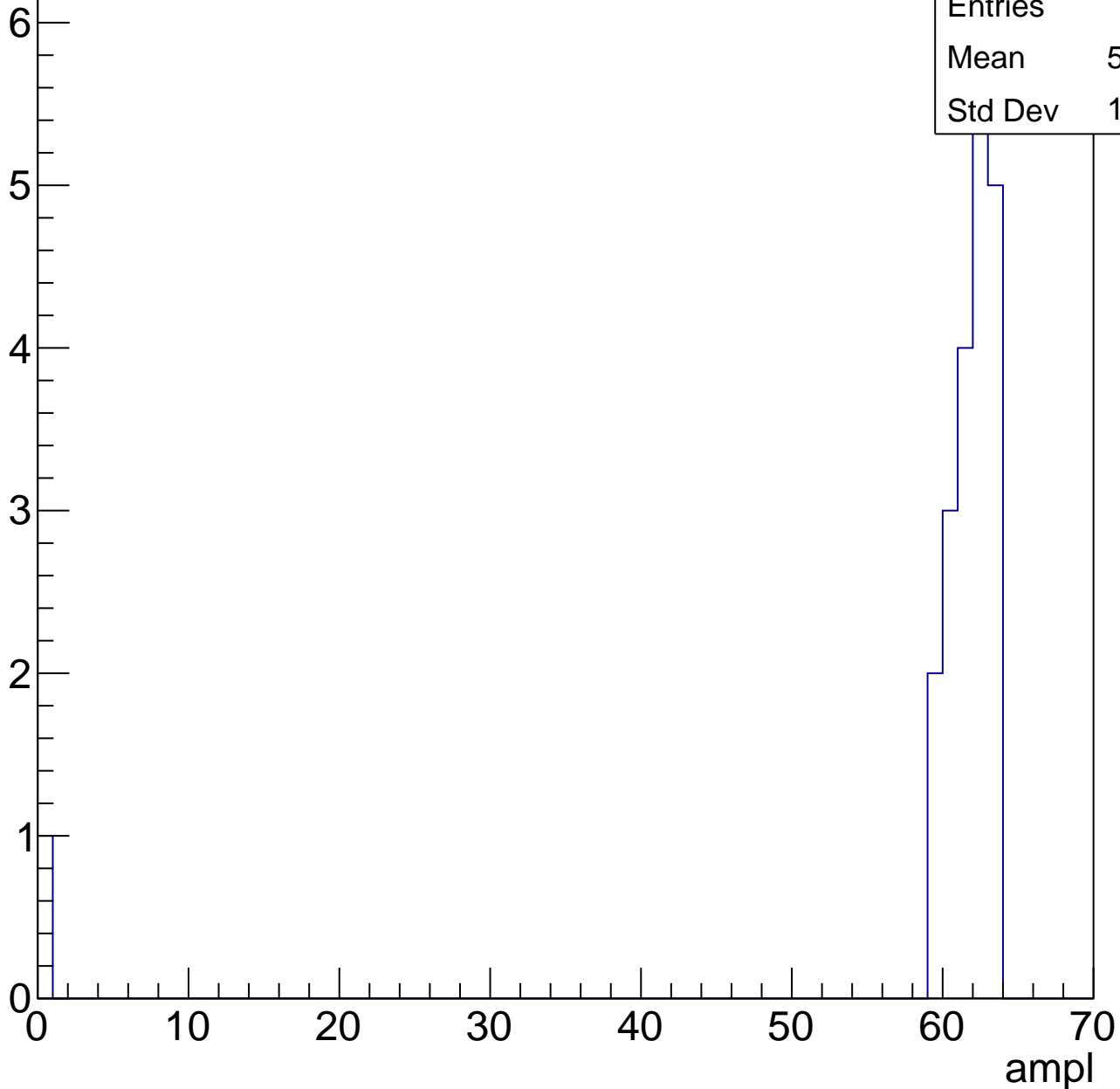


B0L002S, U12-ch14, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

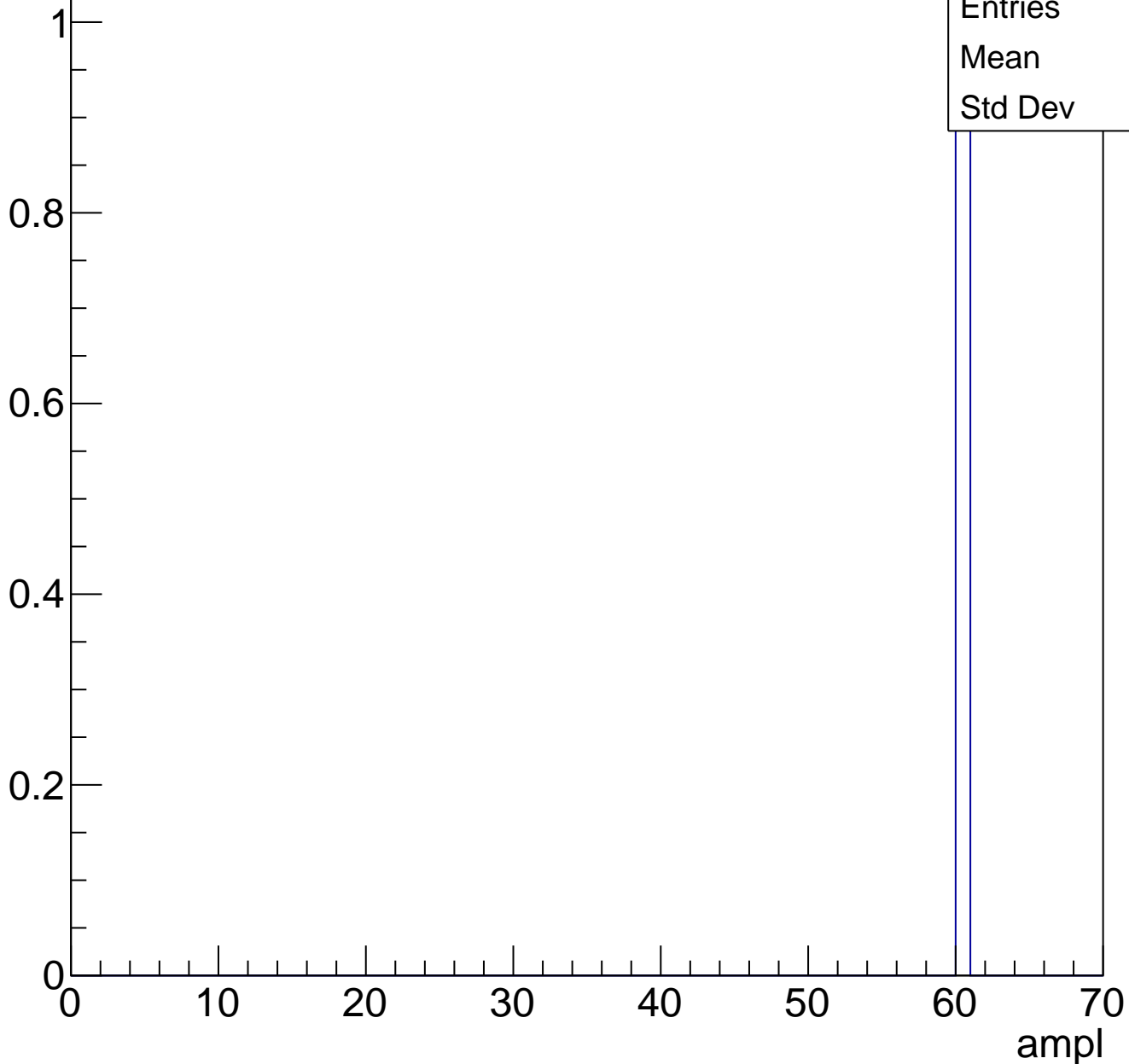
Entries	21
Mean	58.52
Std Dev	13.15



B0L002S, U12-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch15, adc0

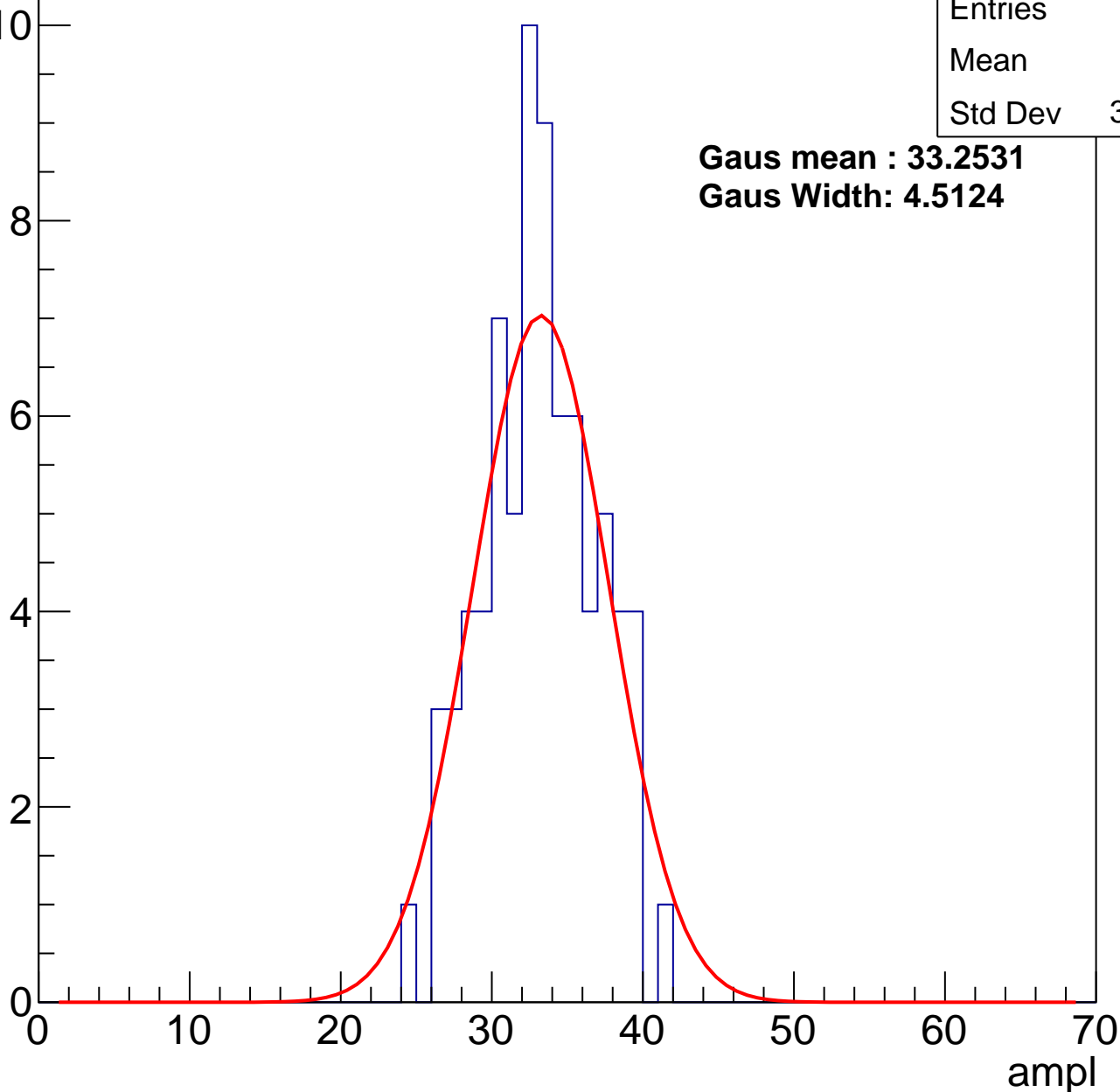
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	32.7
Std Dev	3.699

Gaus mean : 33.2531

Gaus Width: 4.5124



B0L002S, U12-ch15, adc1

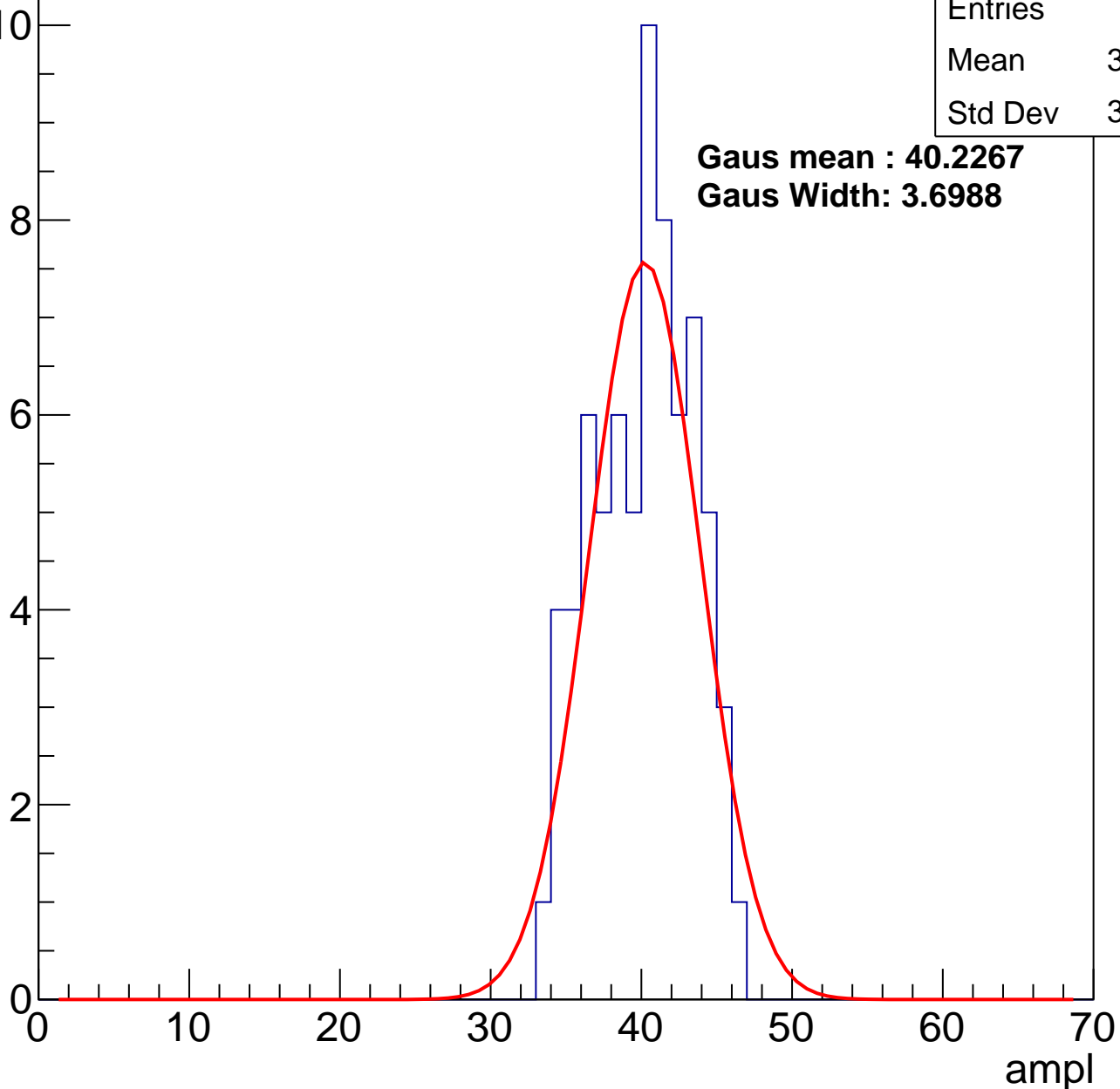
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	39.65
Std Dev	3.229

Gaus mean : 40.2267

Gaus Width: 3.6988



B0L002S, U12-ch15, adc2

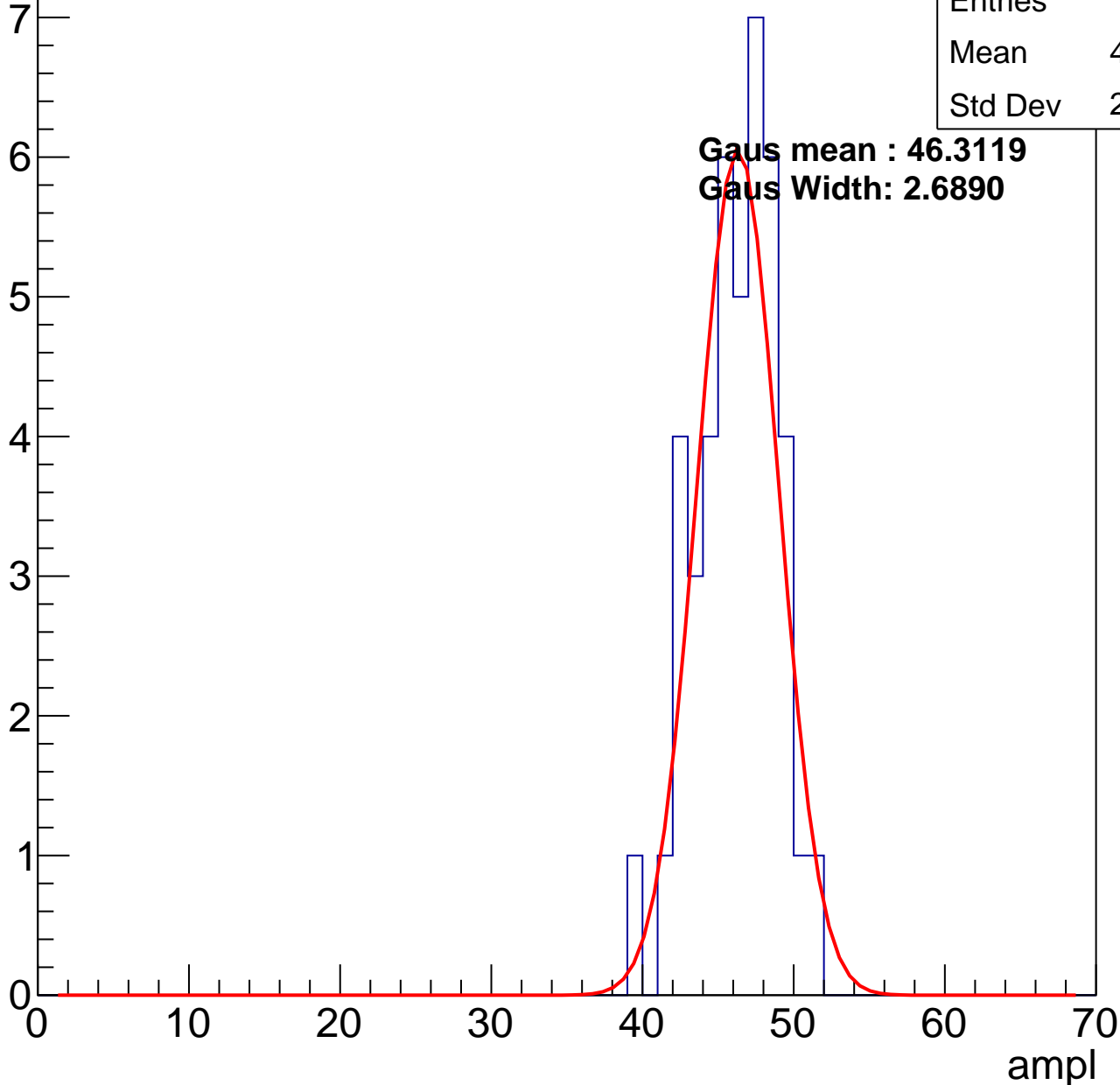
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	45.74
Std Dev	2.607

Gaus mean : 46.3119

Gaus Width: 2.6890



B0L002S, U12-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	52.11
Std Dev	3.535

Entry

10

8

6

4

2

0

0

10

20

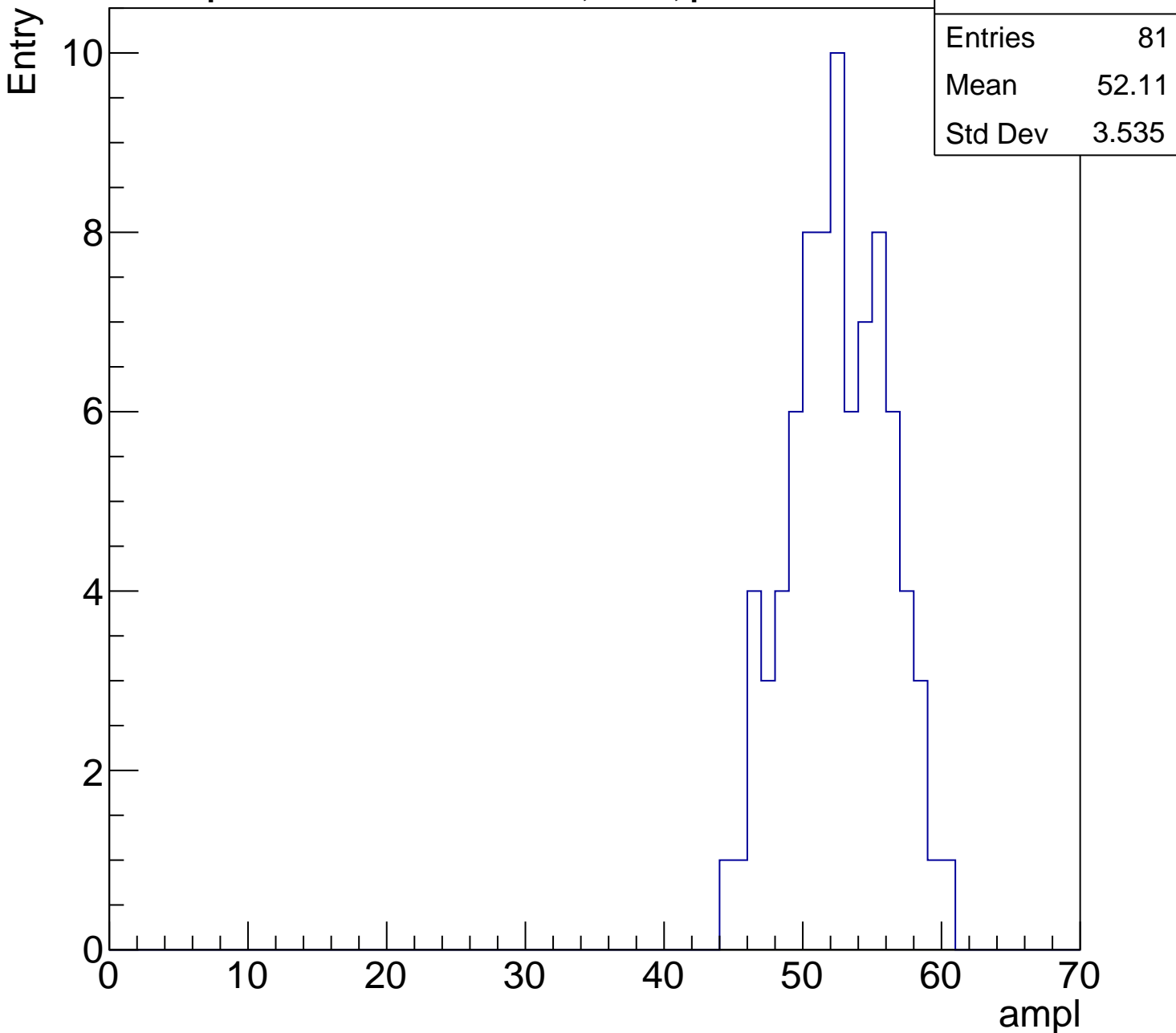
30

40

50

60

ampl

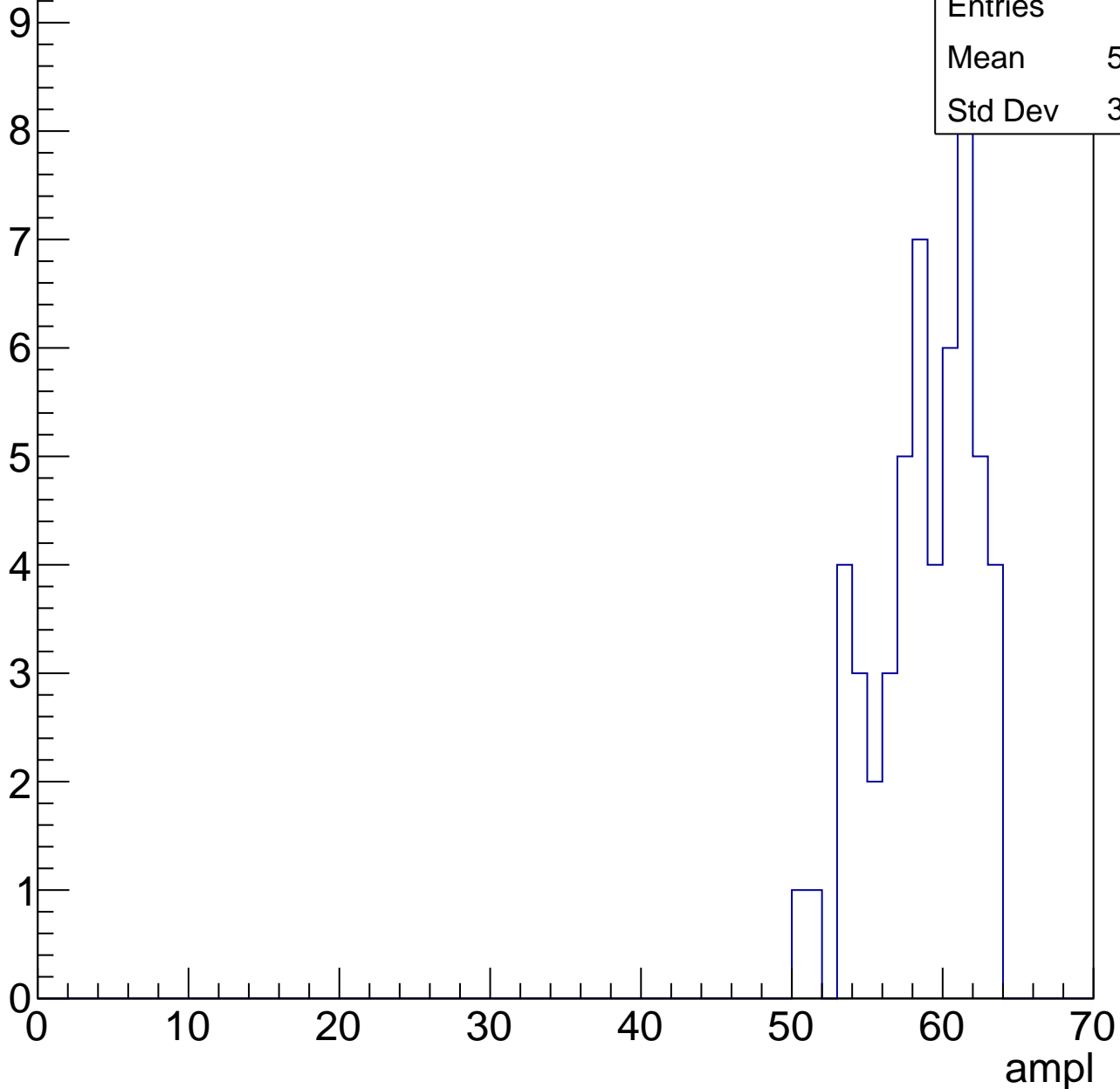


B0L002S, U12-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.35
Std Dev	3.273

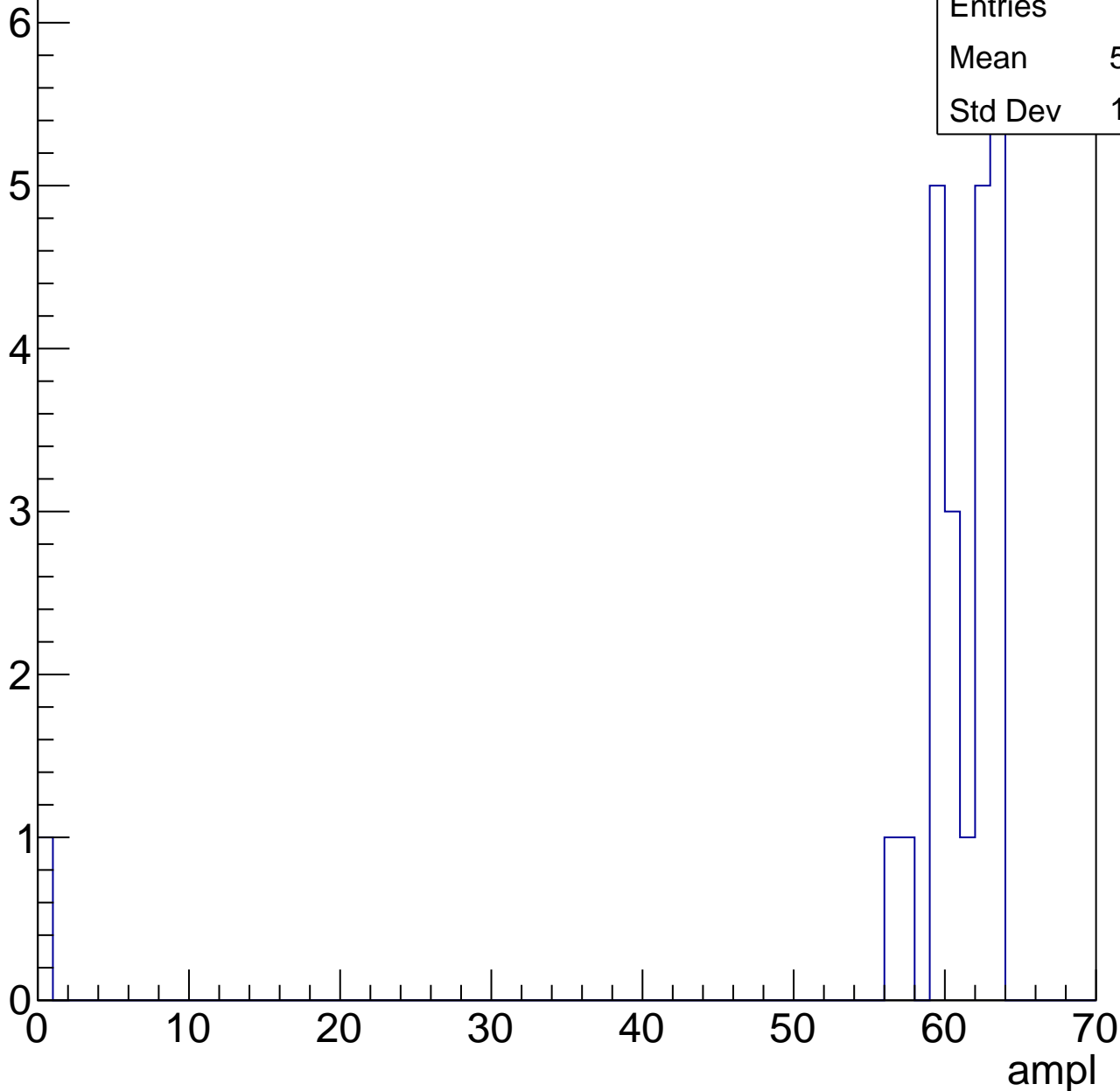


B0L002S, U12-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.13
Std Dev	12.55



B0L002S, U12-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch16, adc0

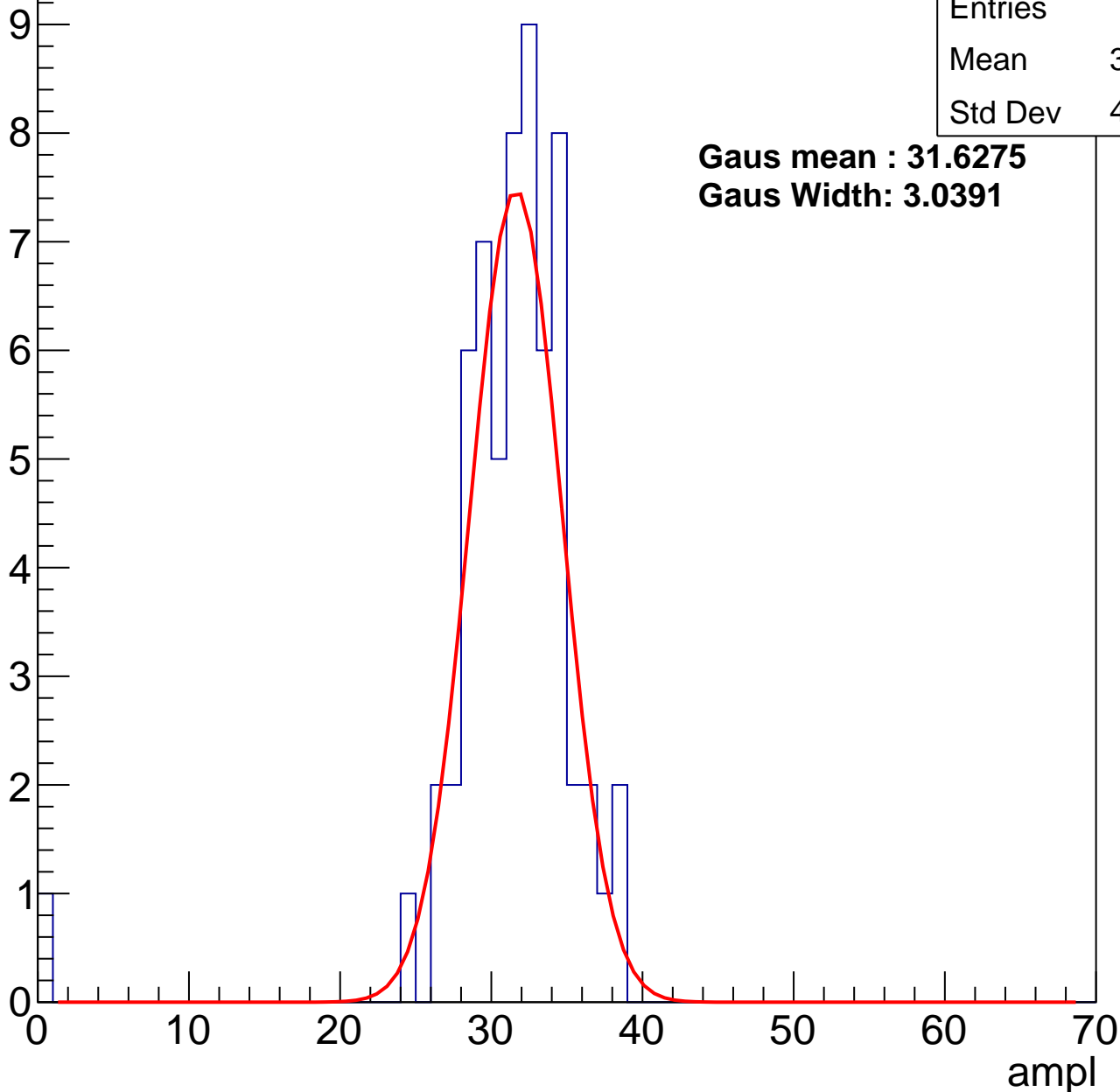
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	30.84
Std Dev	4.916

Gaus mean : 31.6275

Gaus Width: 3.0391



B0L002S, U12-ch16, adc1

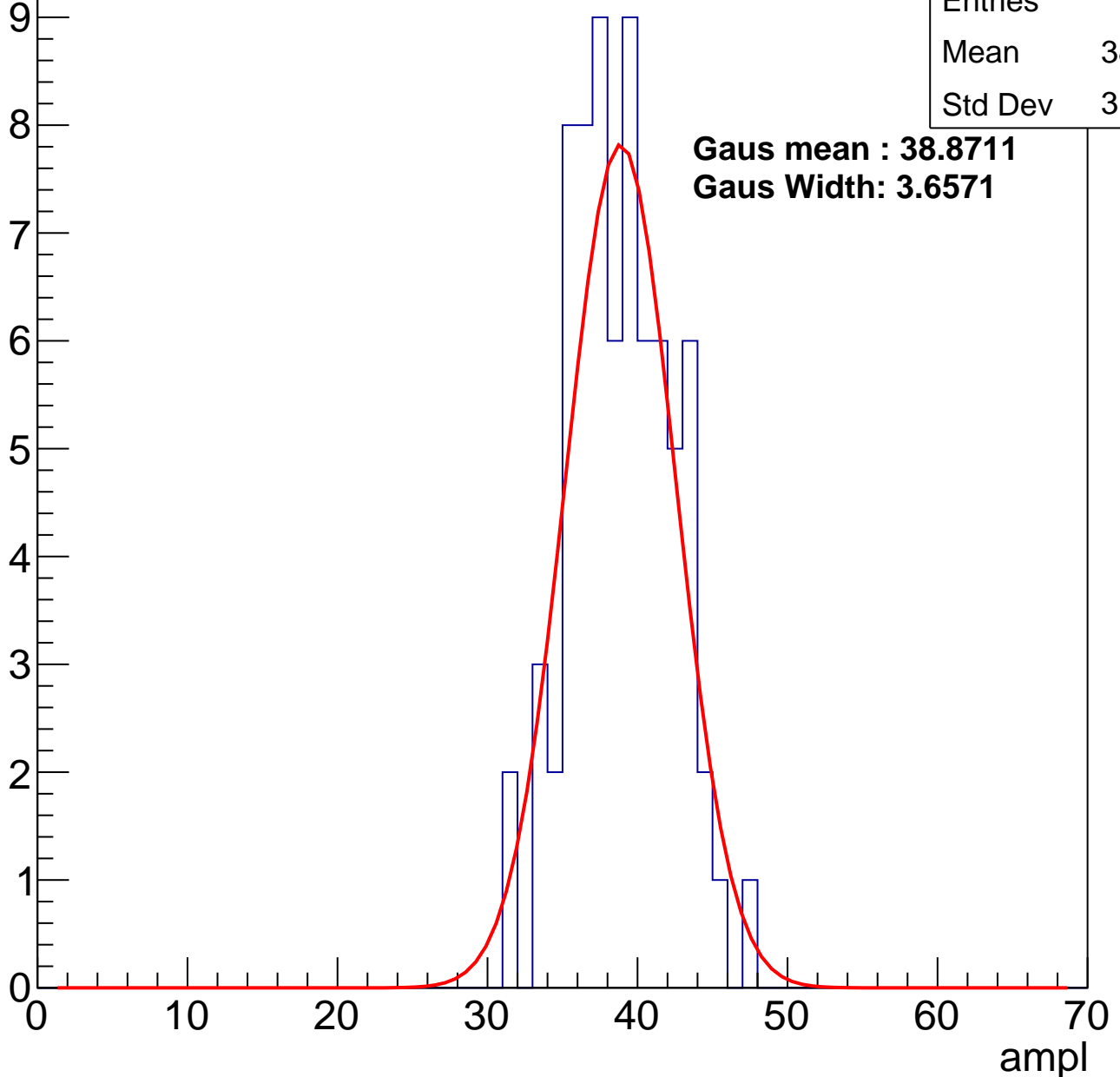
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	38.42
Std Dev	3.337

Gaus mean : 38.8711

Gaus Width: 3.6571



B0L002S, U12-ch16, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	45.03
Std Dev	3.321

Gaus mean : 45.5573

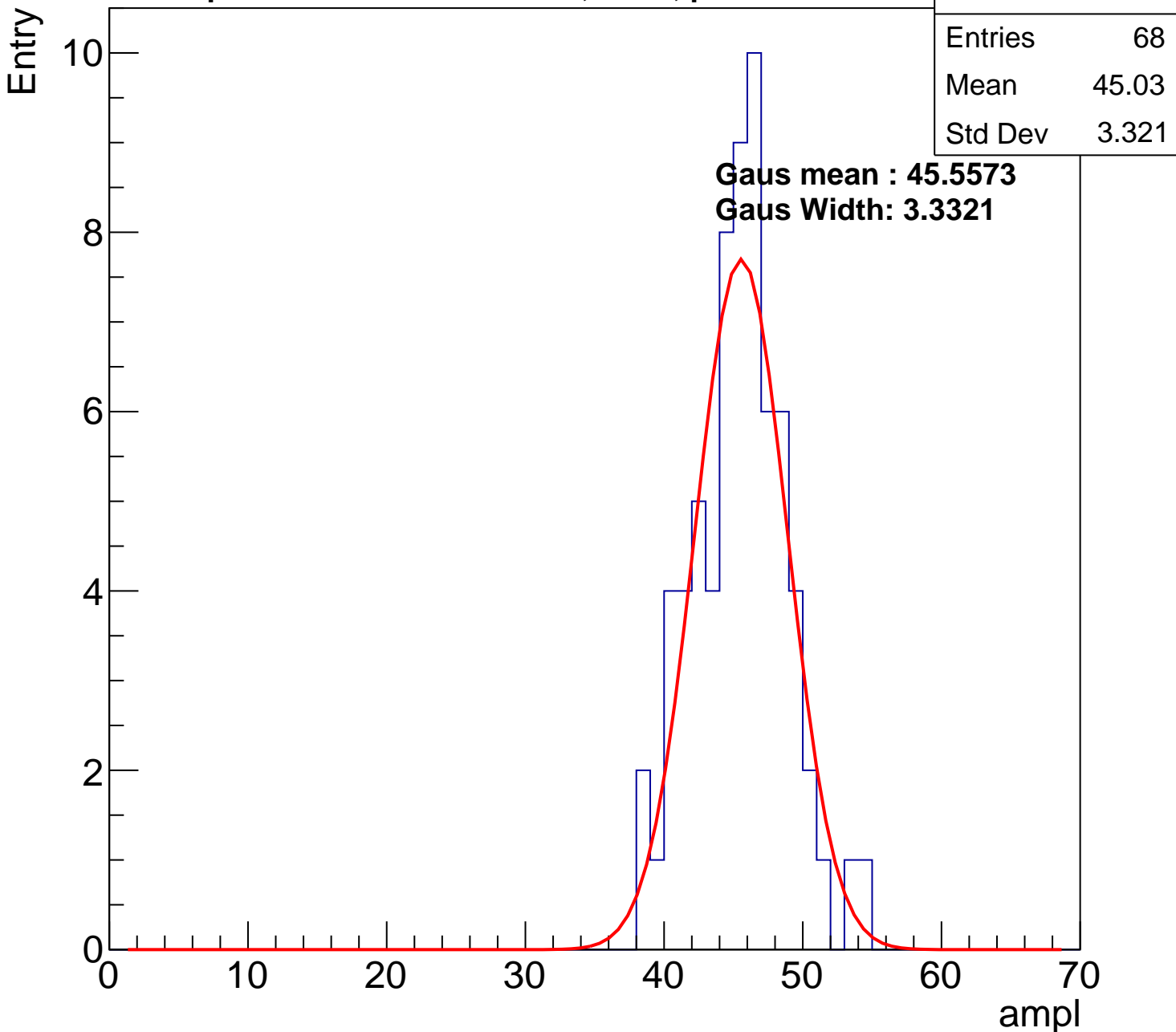
Gaus Width: 3.3321

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

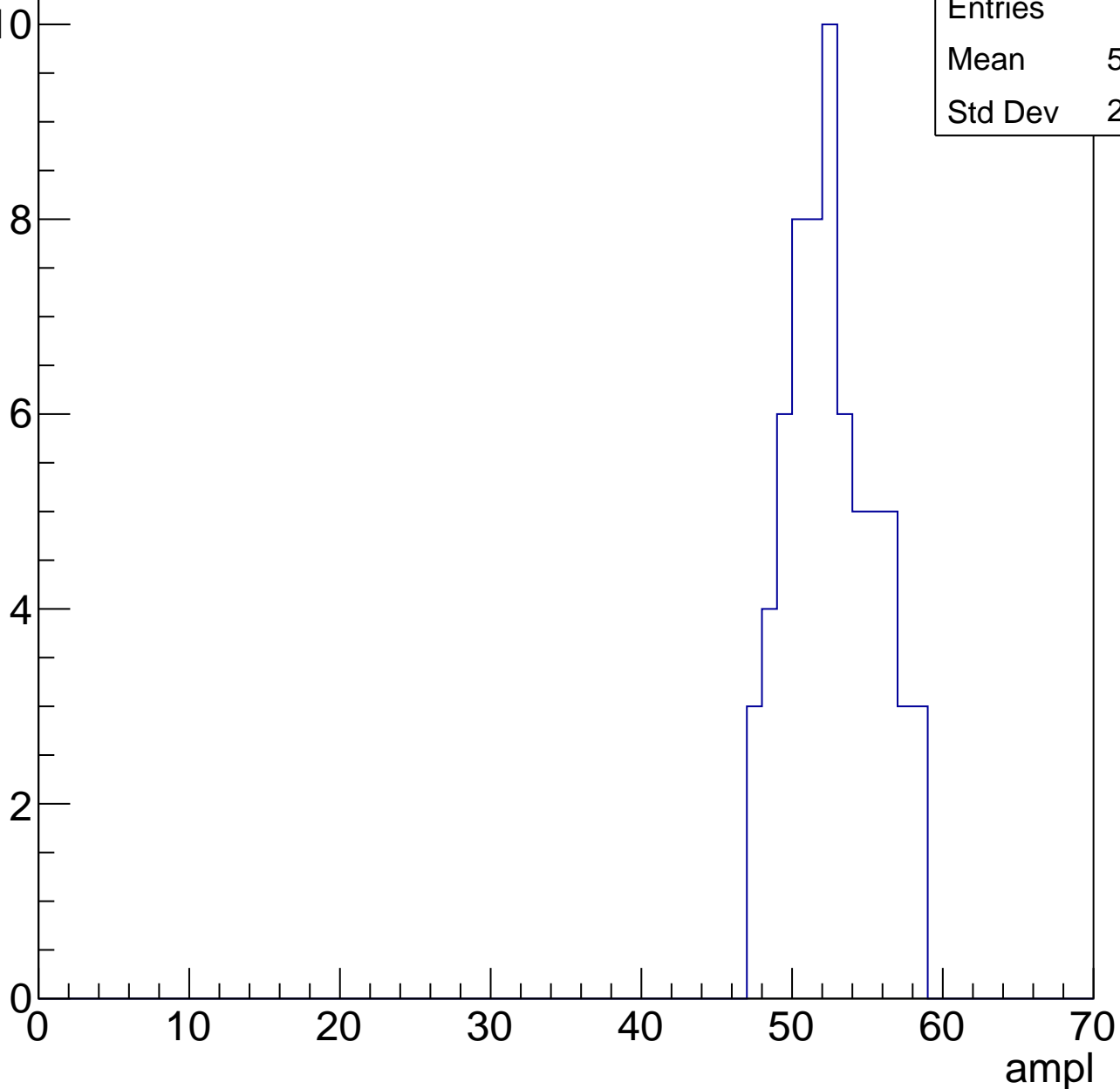


B0L002S, U12-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	52.17
Std Dev	2.926

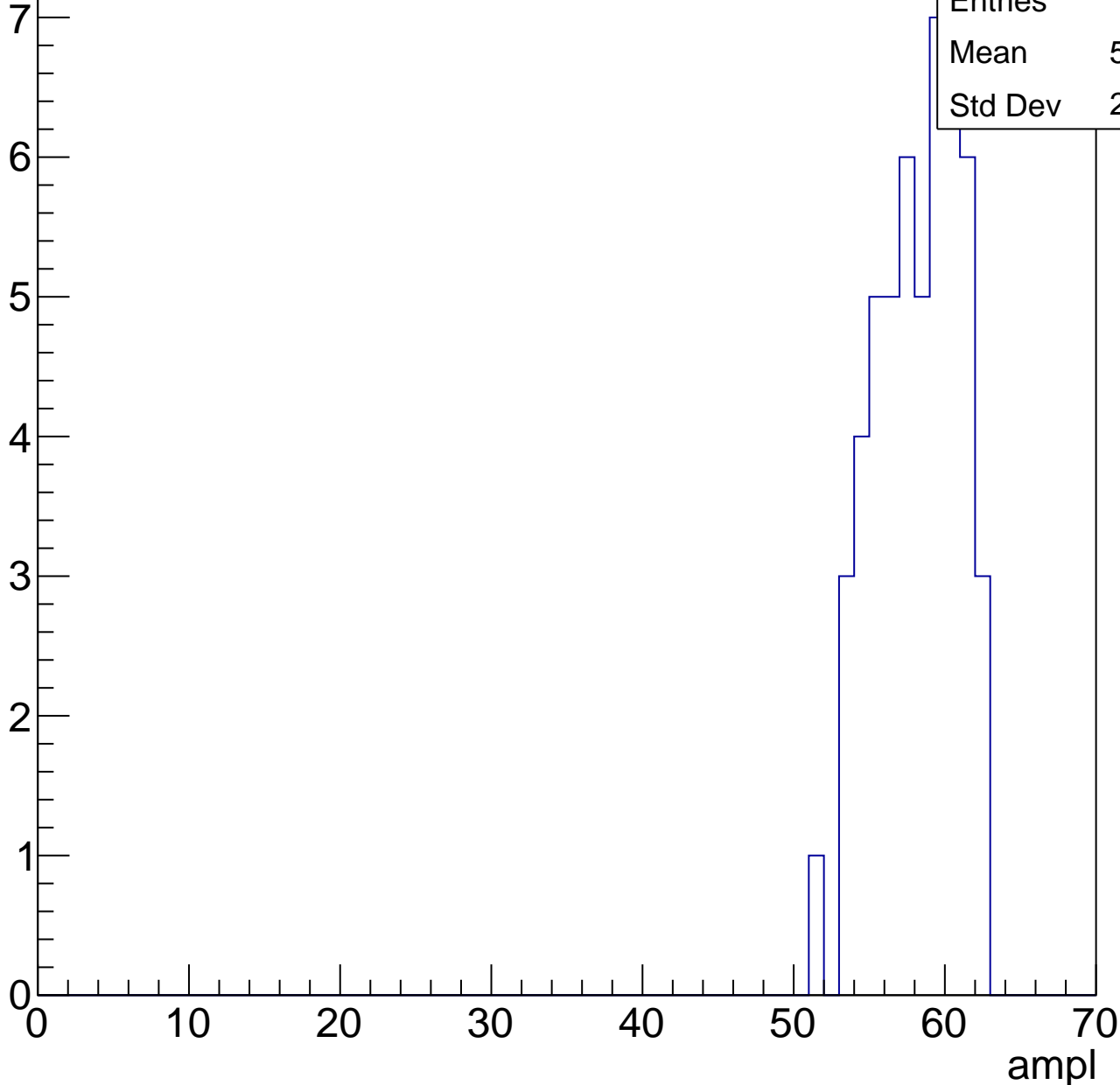


B0L002S, U12-ch16, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	57.65
Std Dev	2.738

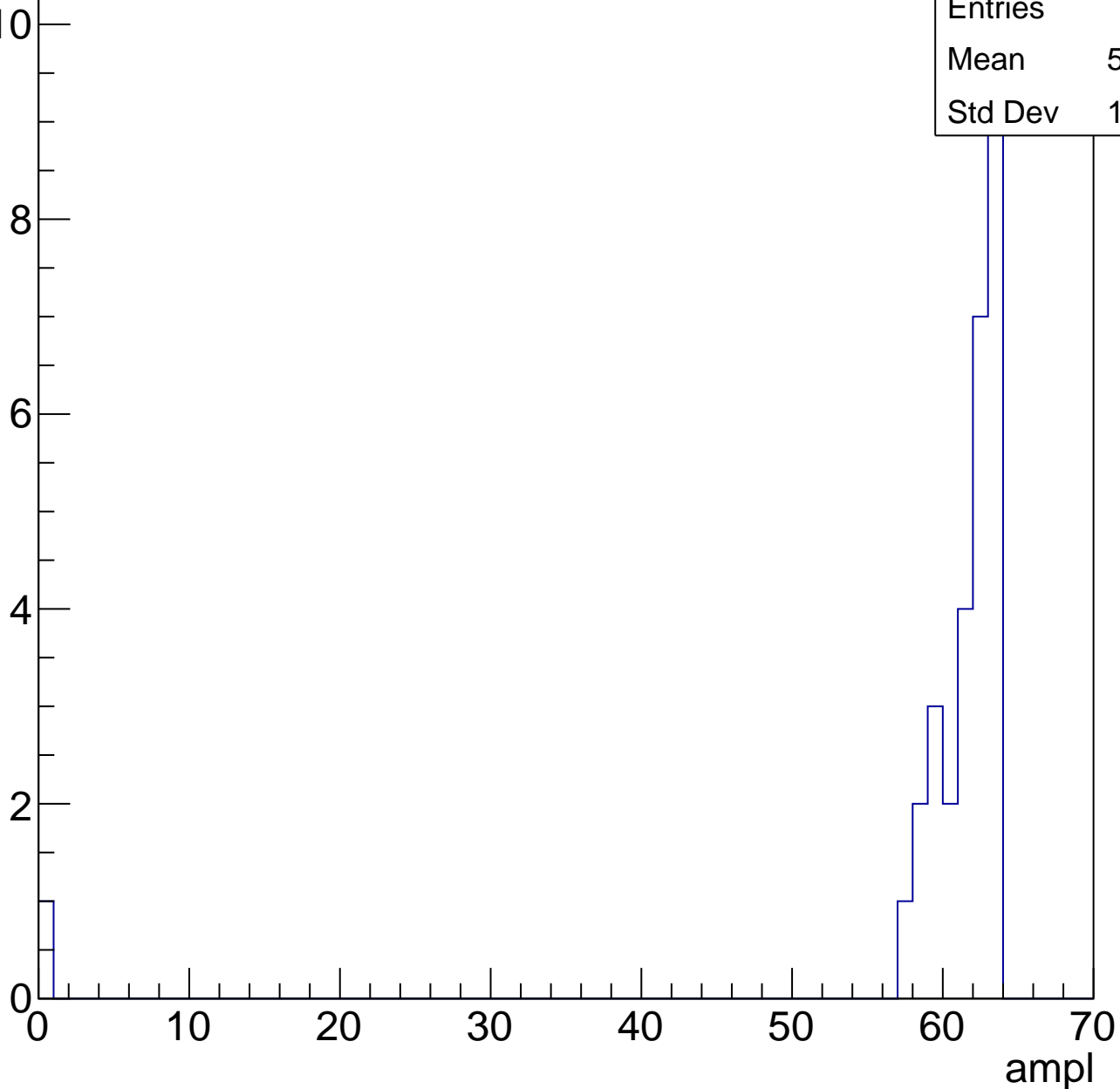


B0L002S, U12-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.27
Std Dev	11.14



B0L002S, U12-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch17, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	31.04
Std Dev	4.922

Gaus mean : 32.0692

Gaus Width: 3.7846

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L002S, U12-ch17, adc1

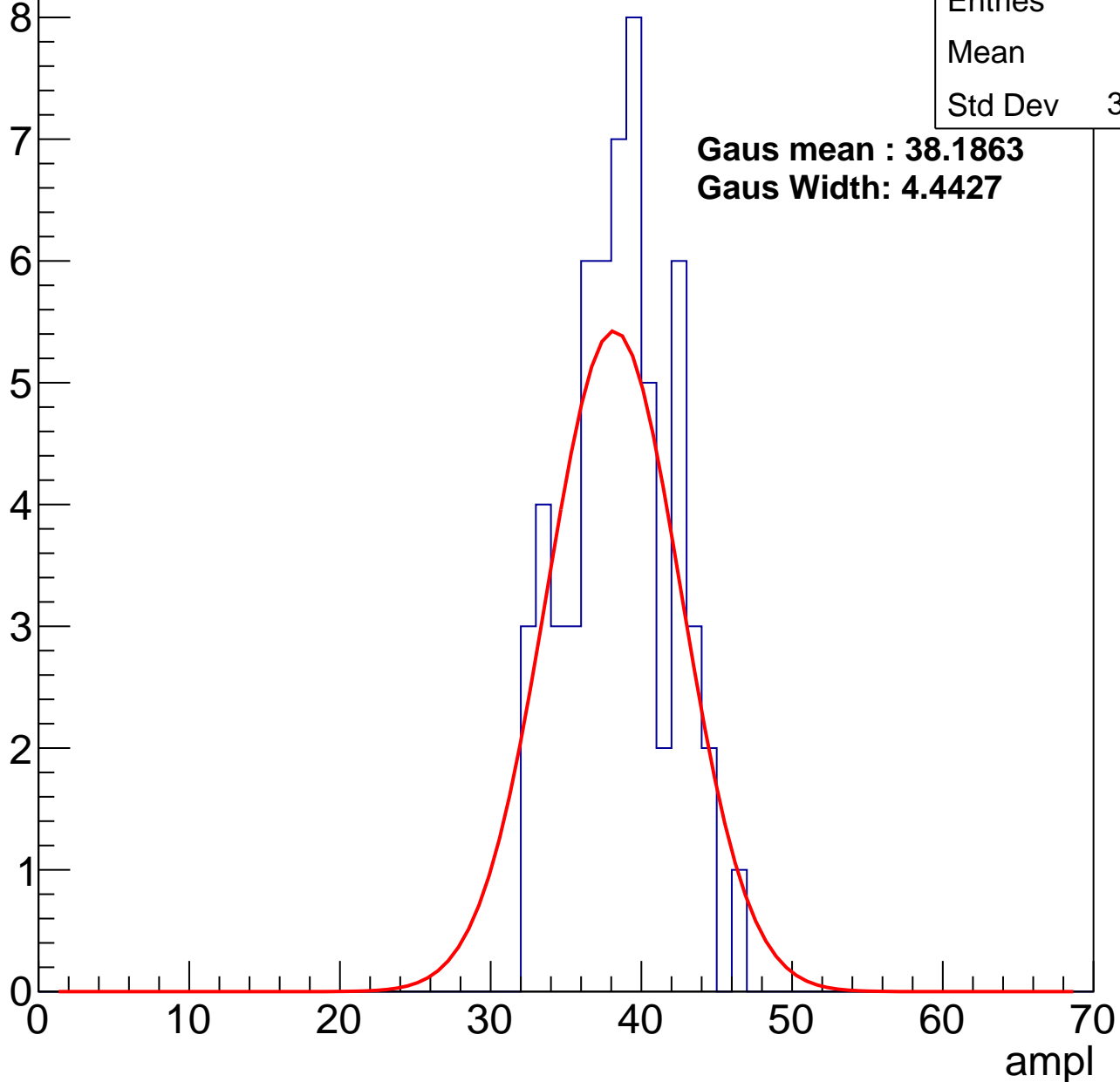
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38.1
Std Dev	3.358

Gaus mean : 38.1863

Gaus Width: 4.4427



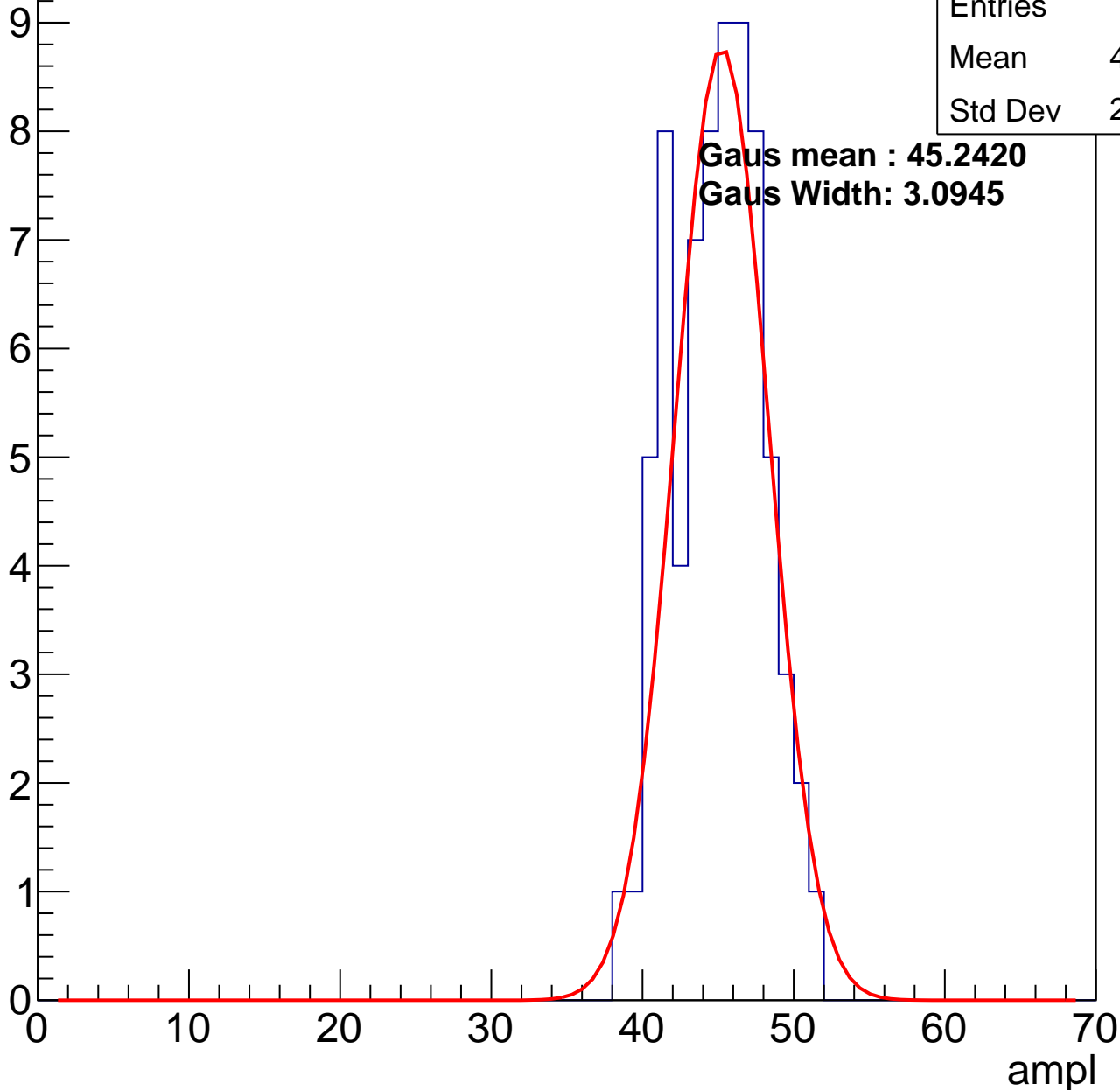
B0L002S, U12-ch17, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.49
Std Dev	2.935

Gaus mean : 45.2420
Gaus Width: 3.0945



B0L002S, U12-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

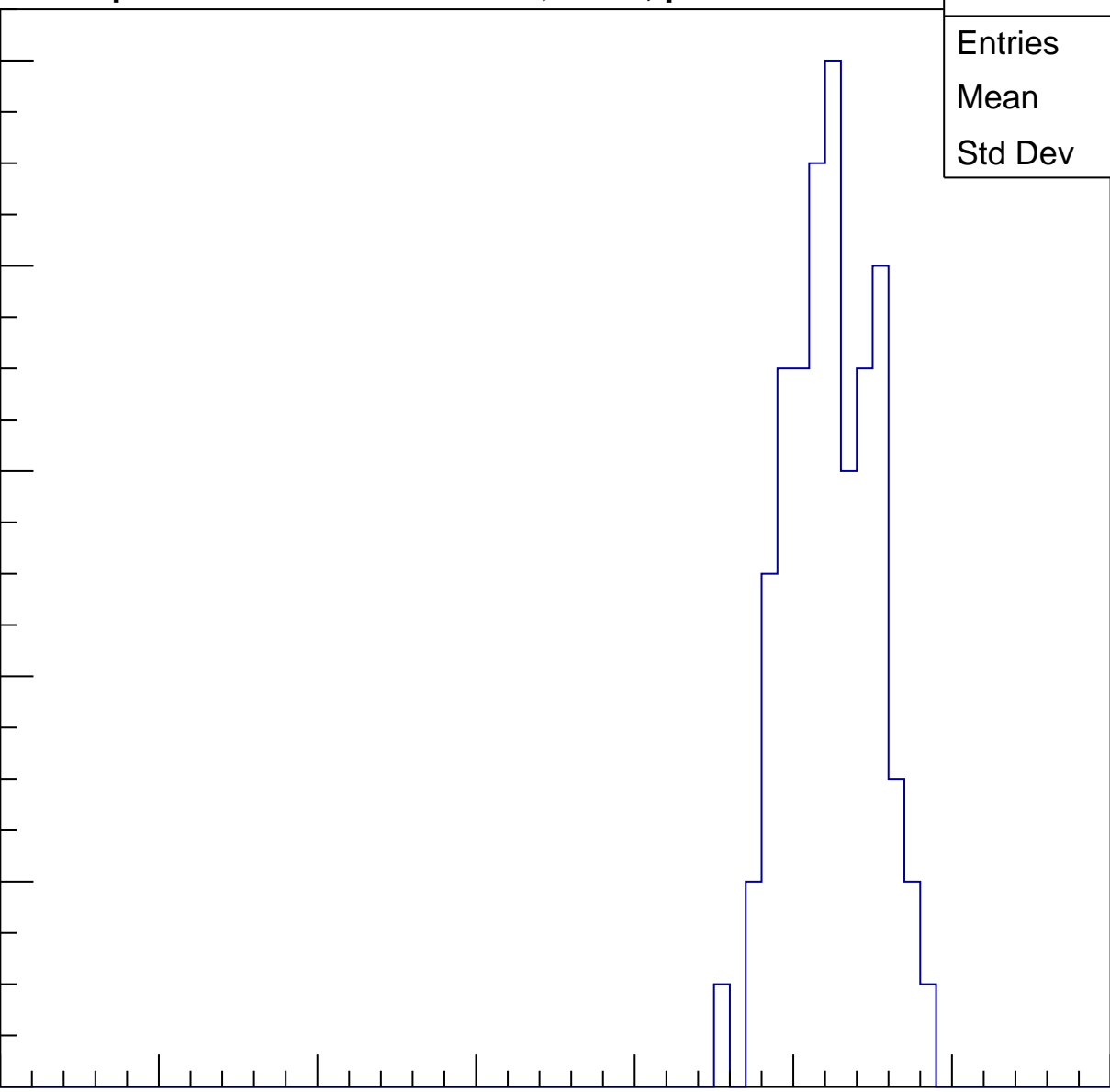
Entries	68
Mean	51.87
Std Dev	2.759

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

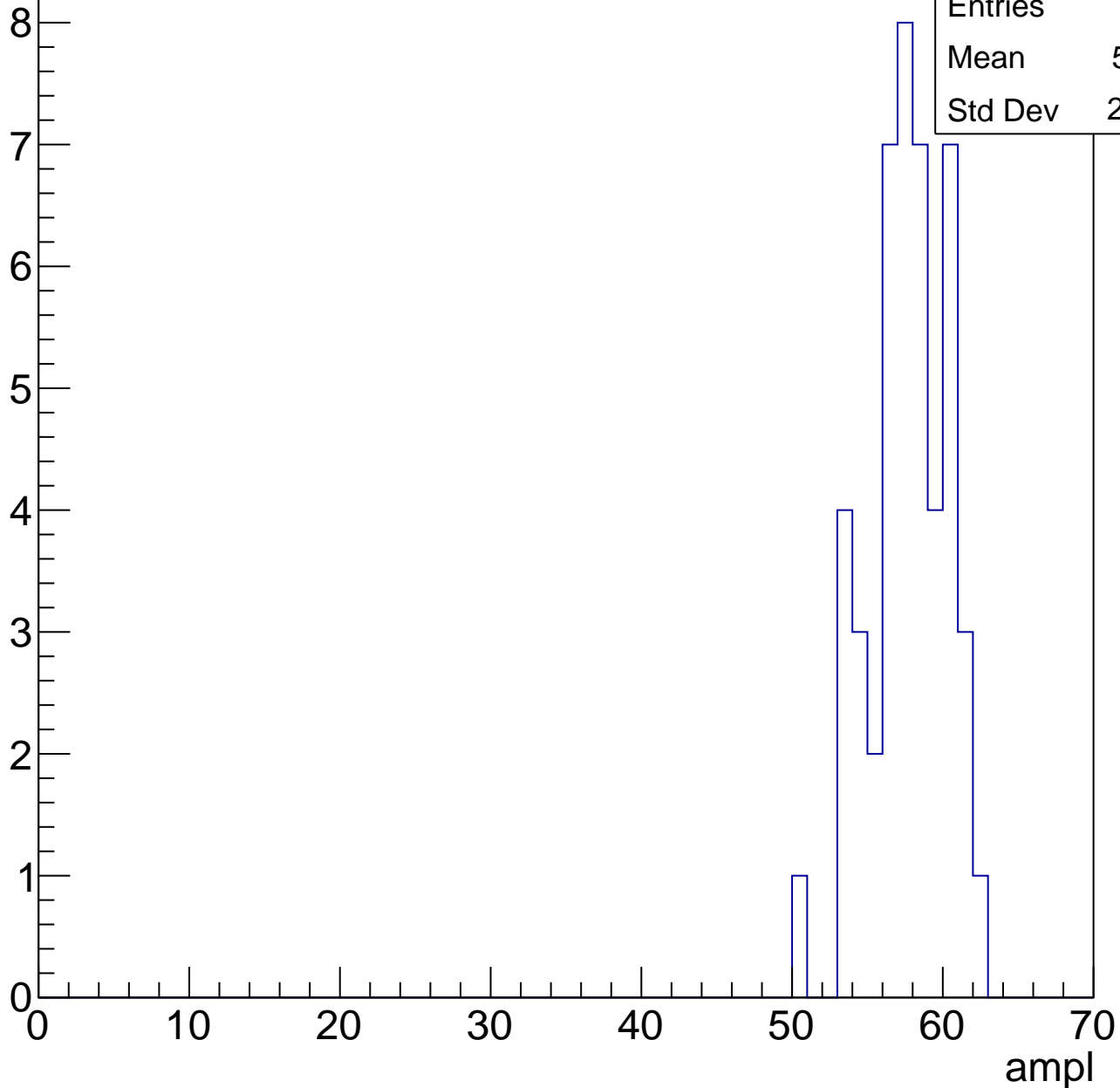


B0L002S, U12-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	57.21
Std Dev	2.576

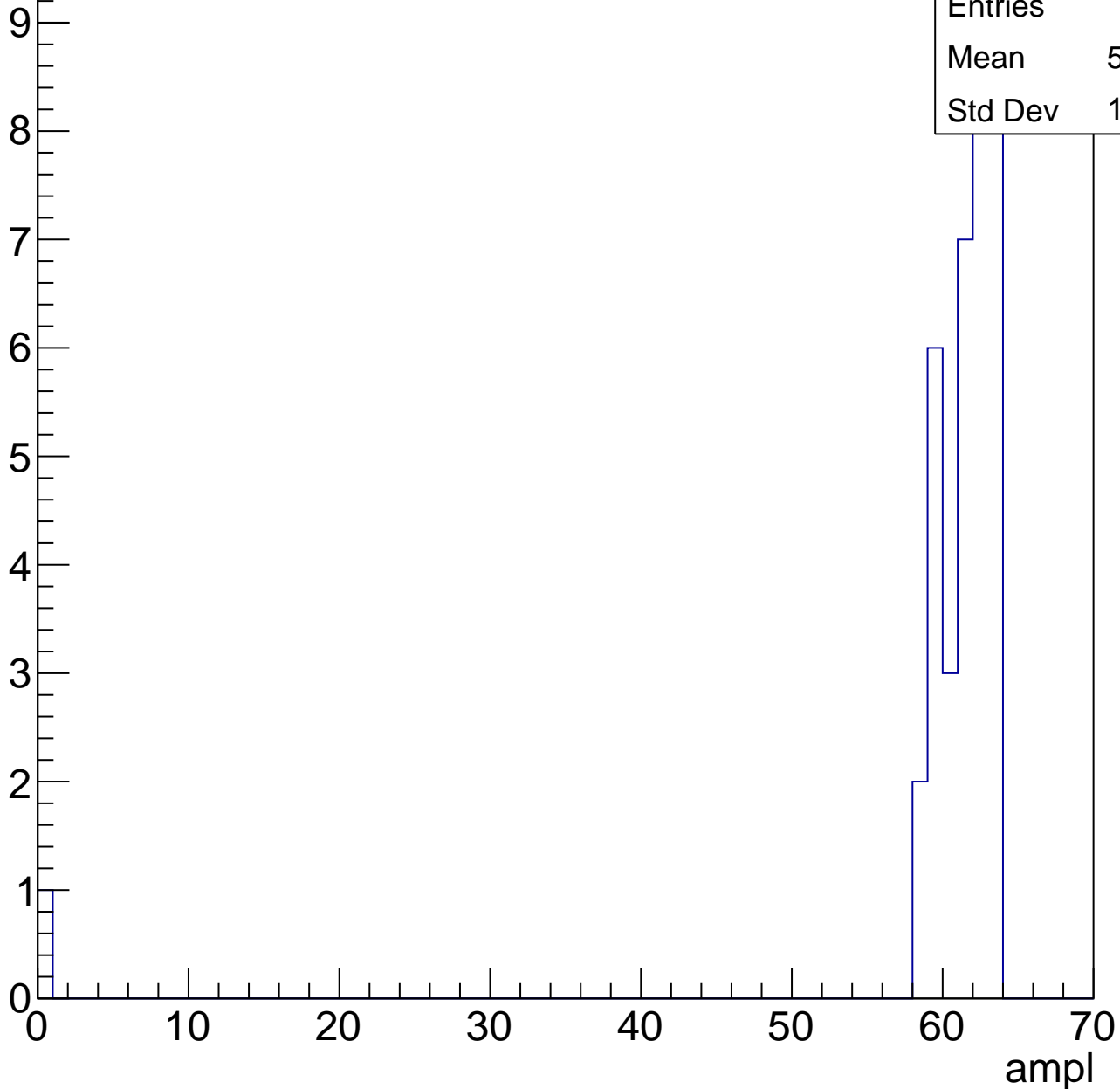


B0L002S, U12-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.44
Std Dev	10.17



B0L002S, U12-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch18, adc0

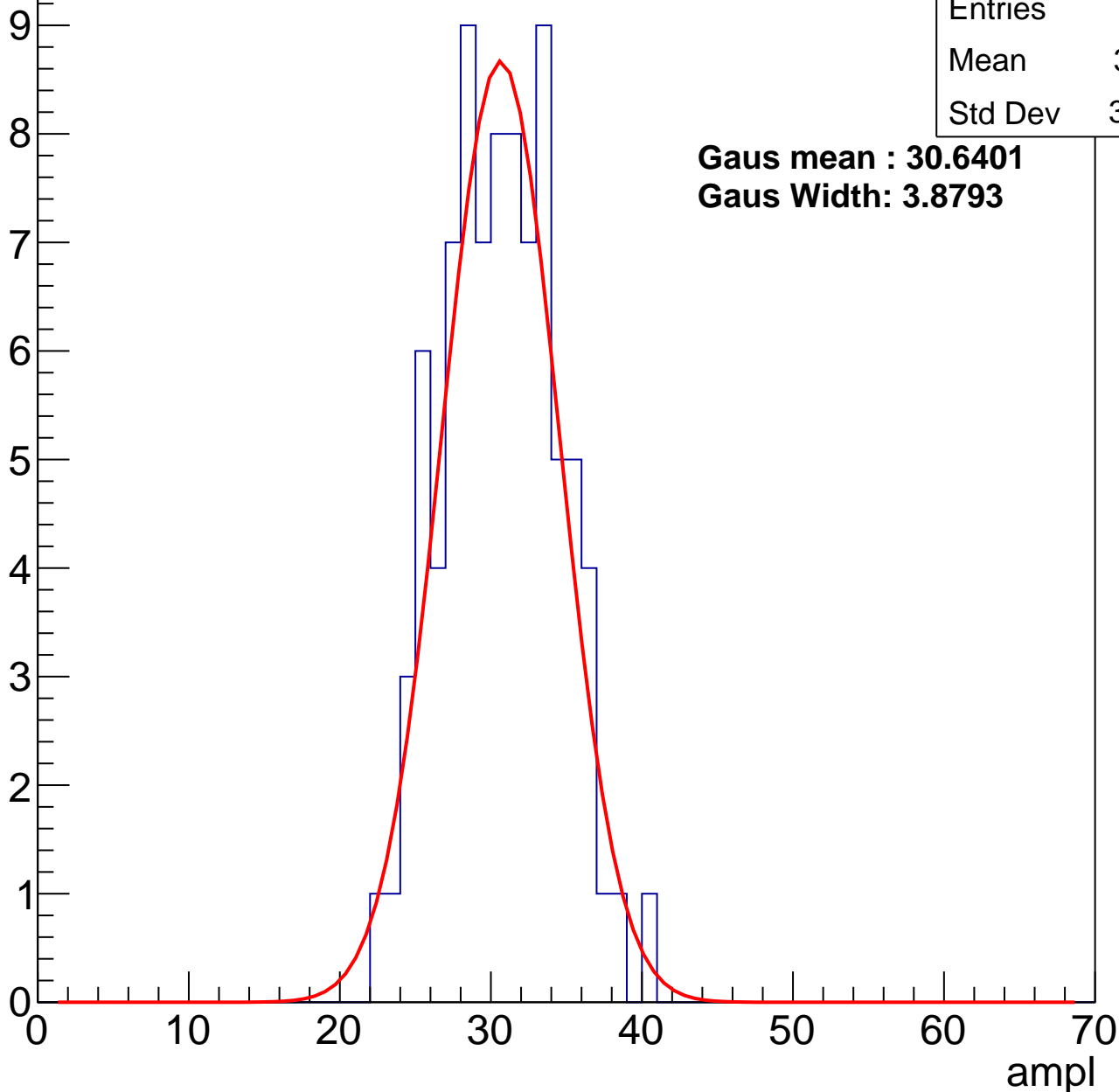
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	30.21
Std Dev	3.739

Gaus mean : 30.6401

Gaus Width: 3.8793



B0L002S, U12-ch18, adc1

calib_packv5_042523_0143.root, FC#8, port C1

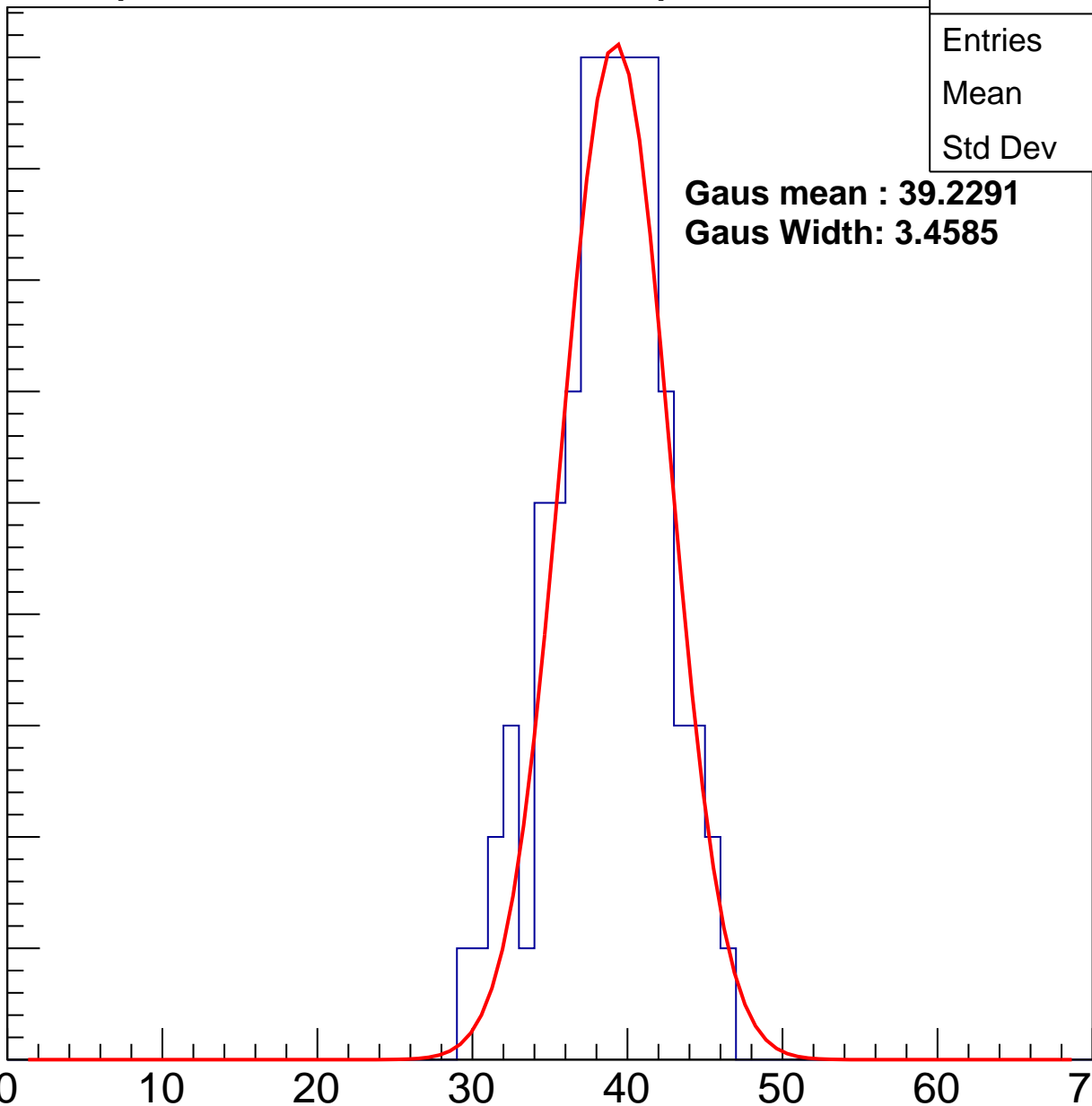
Entry

9
8
7
6
5
4
3
2
1
0

Entries	84
Mean	38.27
Std Dev	3.61

Gaus mean : 39.2291
Gaus Width: 3.4585

0 10 20 30 40 50 60 70
ampl



B0L002S, U12-ch18, adc2

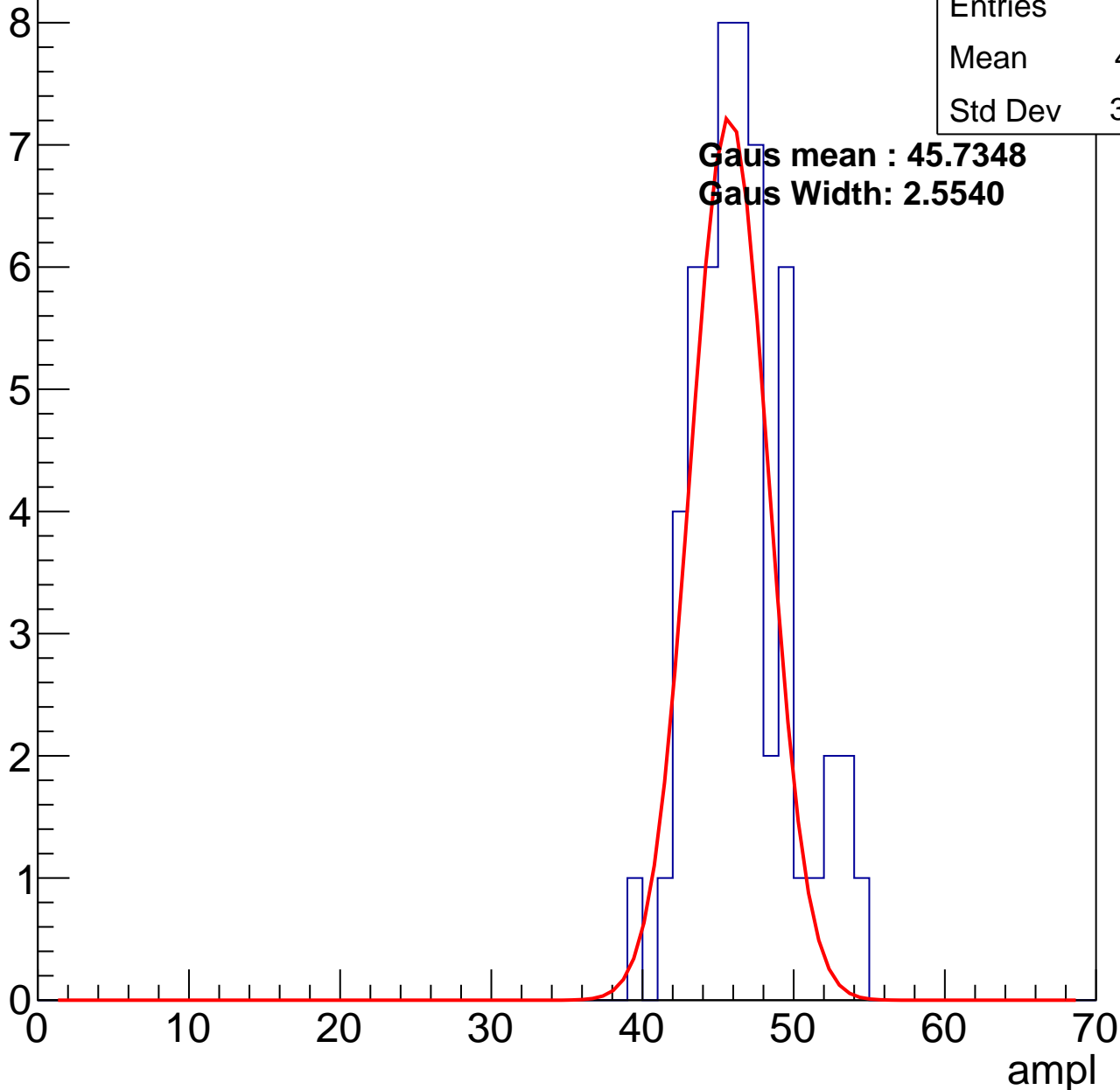
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	46.11
Std Dev	3.183

Gaus mean : 45.7348

Gaus Width: 2.5540

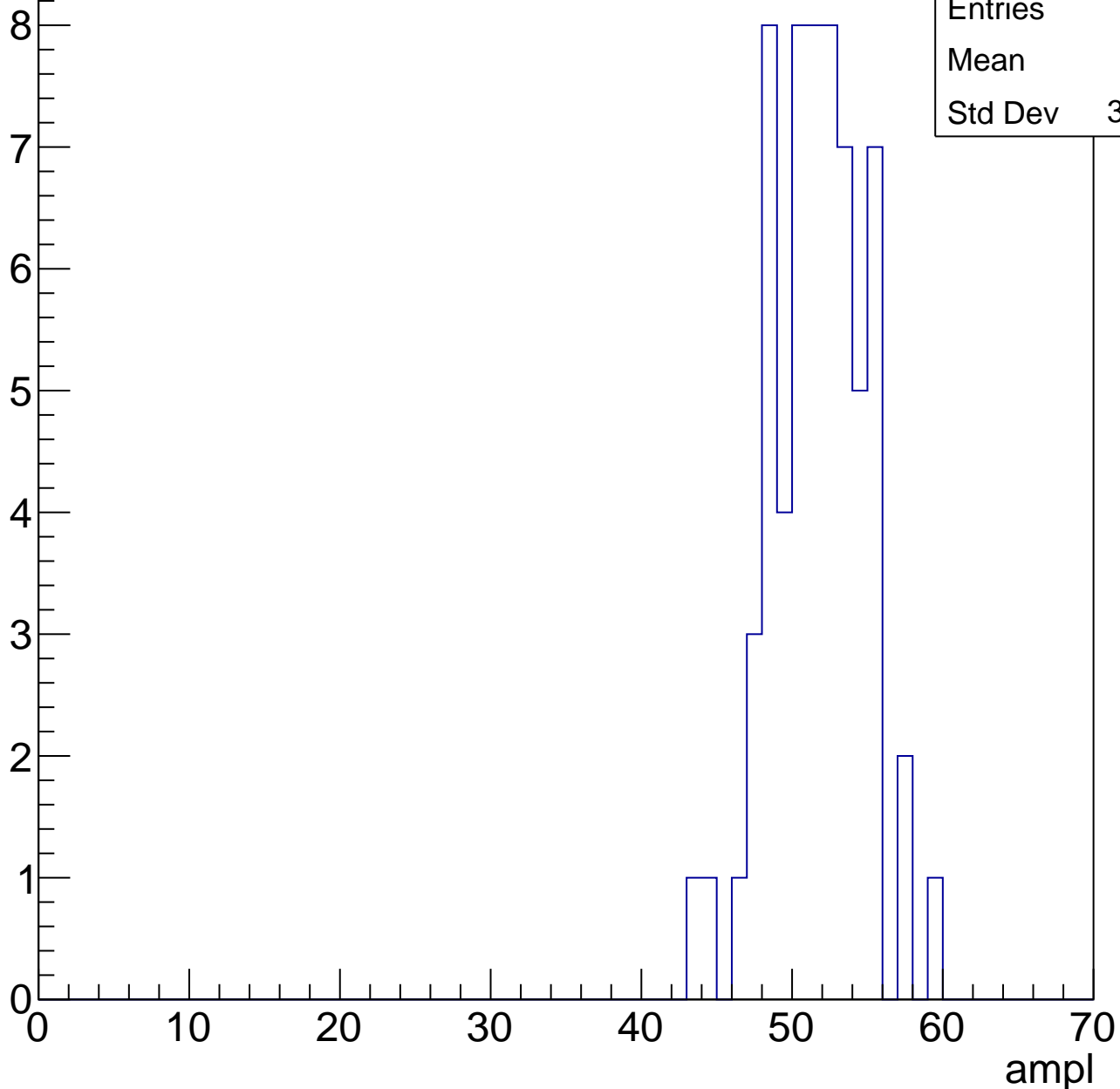


B0L002S, U12-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

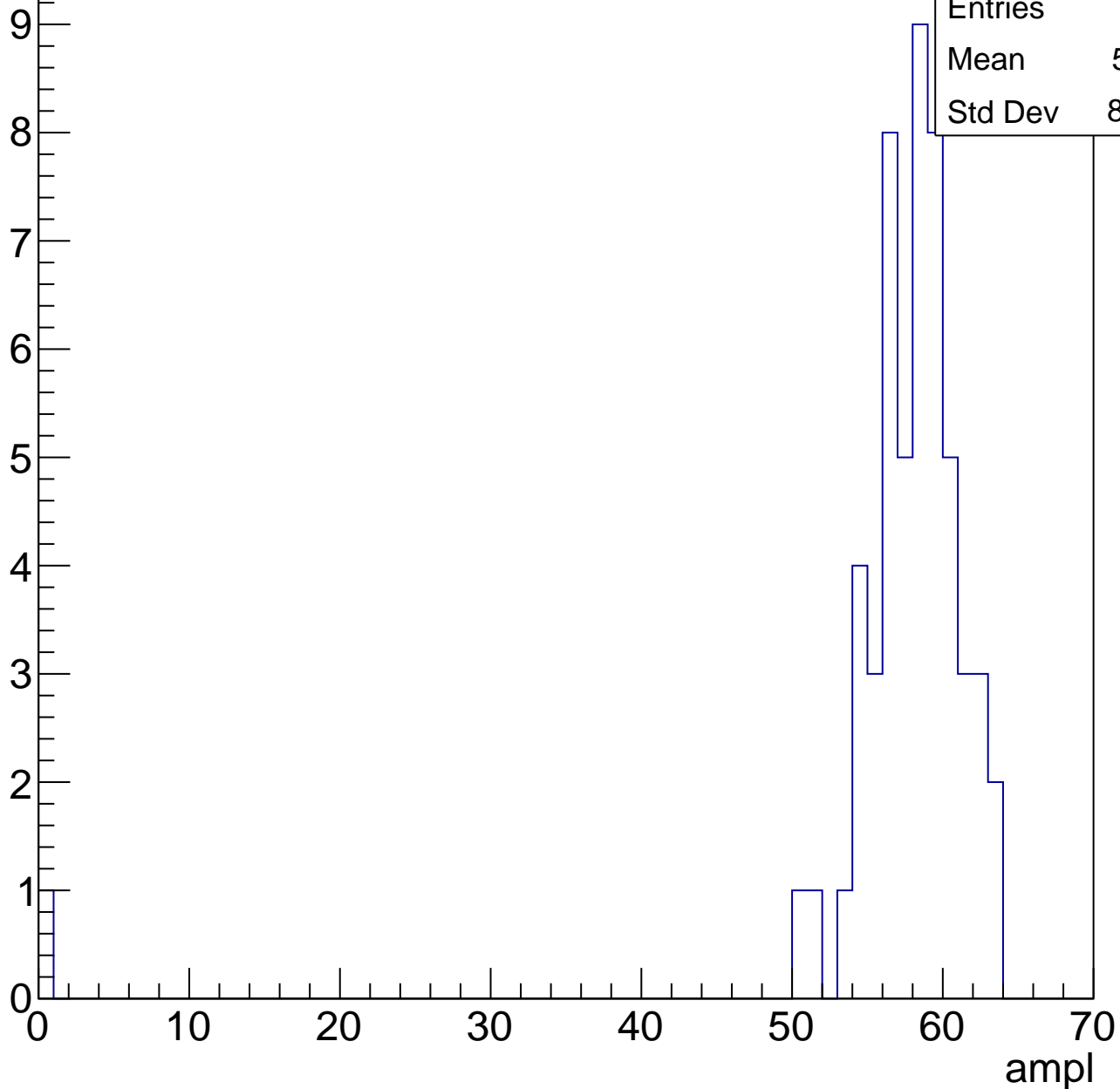
Entries	64
Mean	51.2
Std Dev	3.083



B0L002S, U12-ch18, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

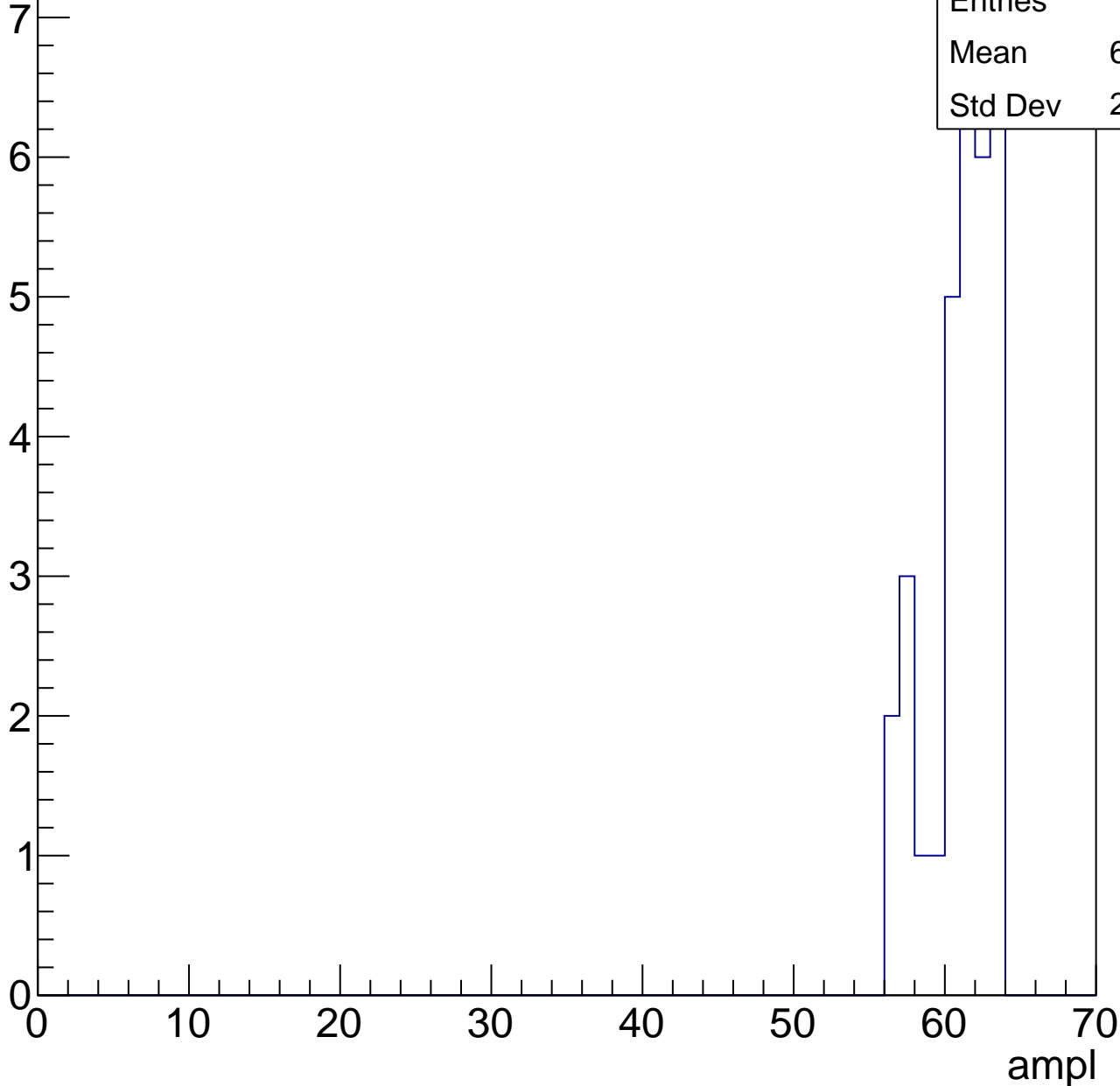


B0L002S, U12-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	60.62
Std Dev	2.132



B0L002S, U12-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch19, adc0

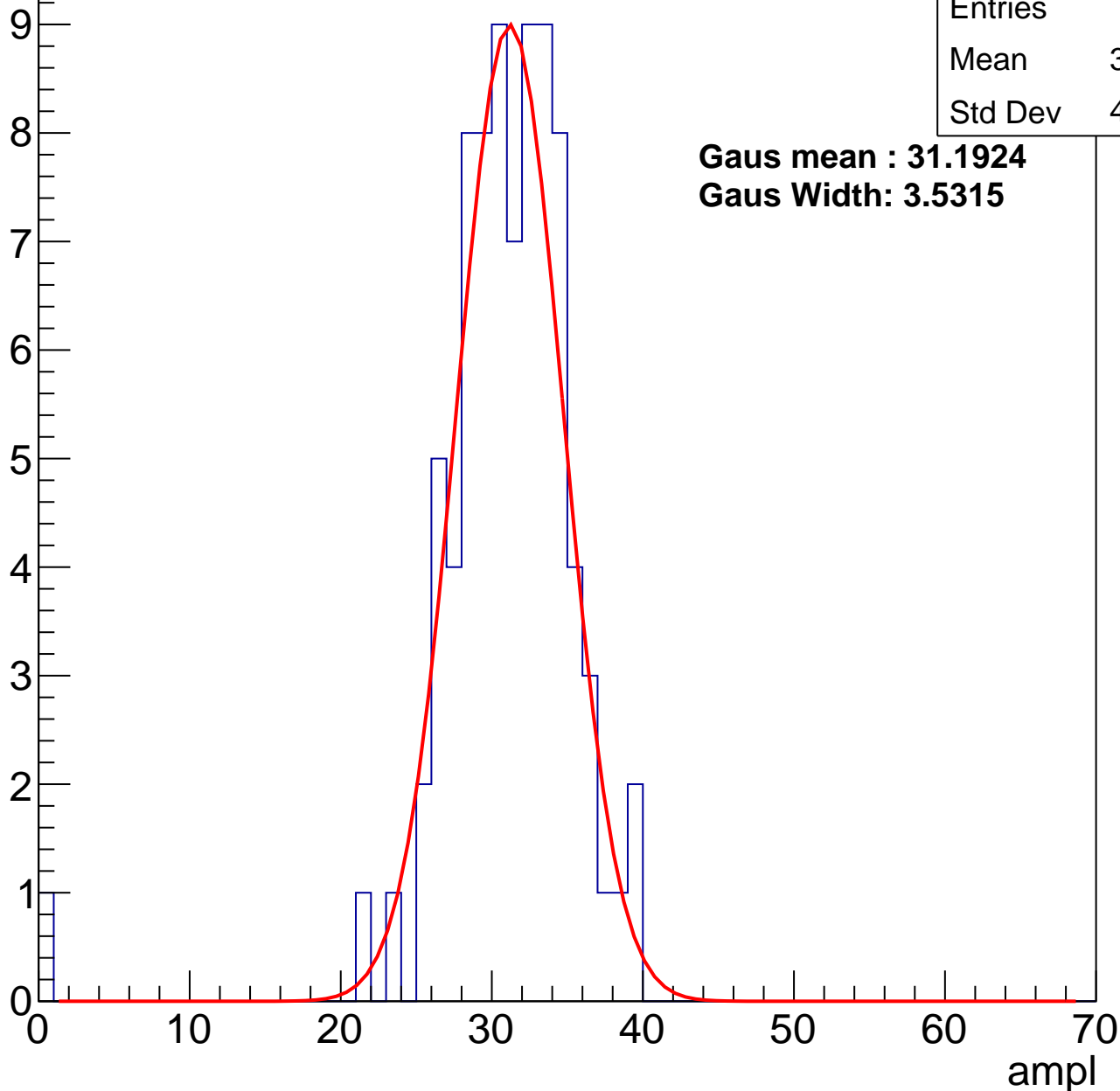
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	30.52
Std Dev	4.845

Gaus mean : 31.1924

Gaus Width: 3.5315



B0L002S, U12-ch19, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	39.39
Std Dev	3.403

Gaus mean : 40.0338

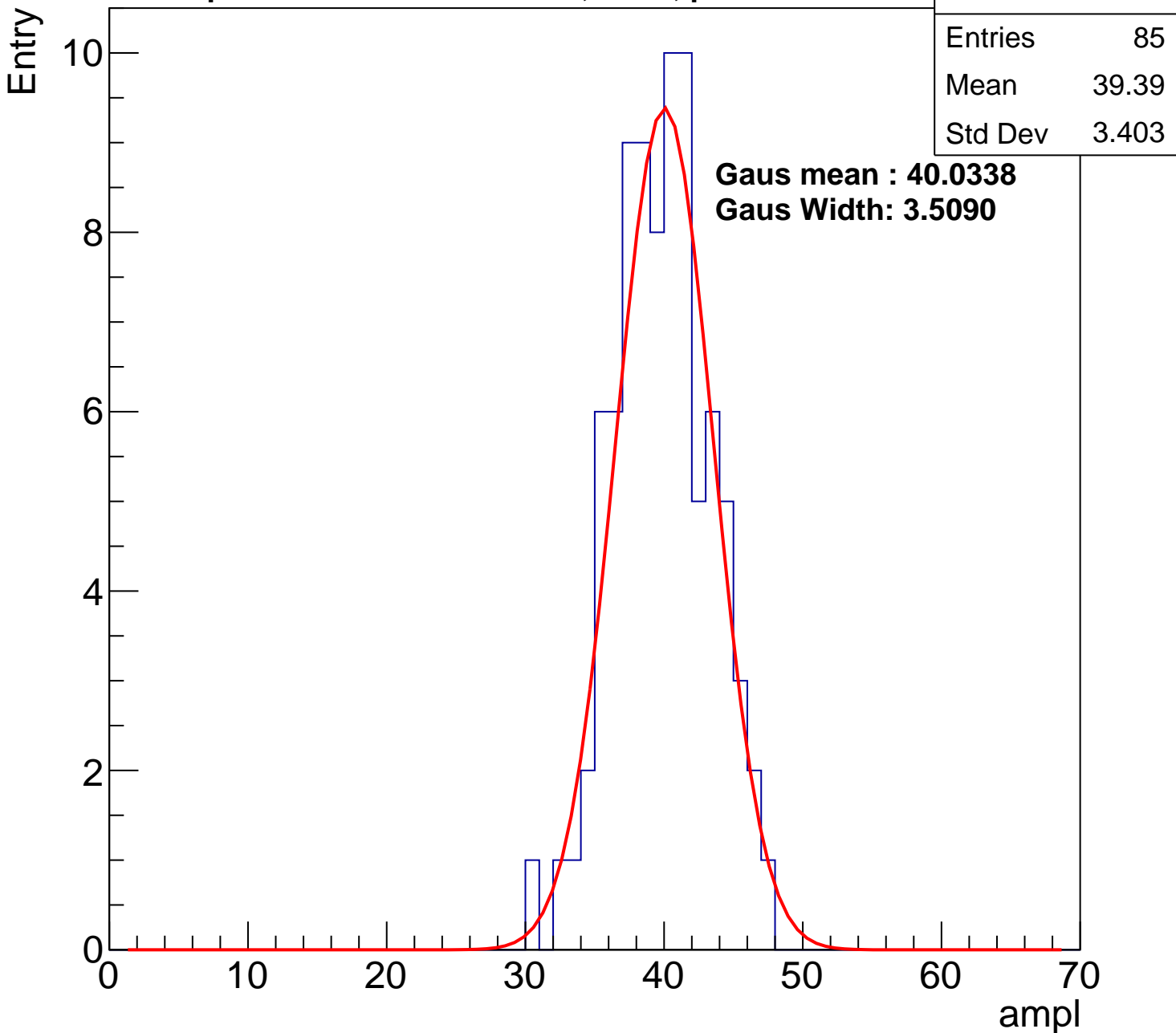
Gaus Width: 3.5090

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch19, adc2

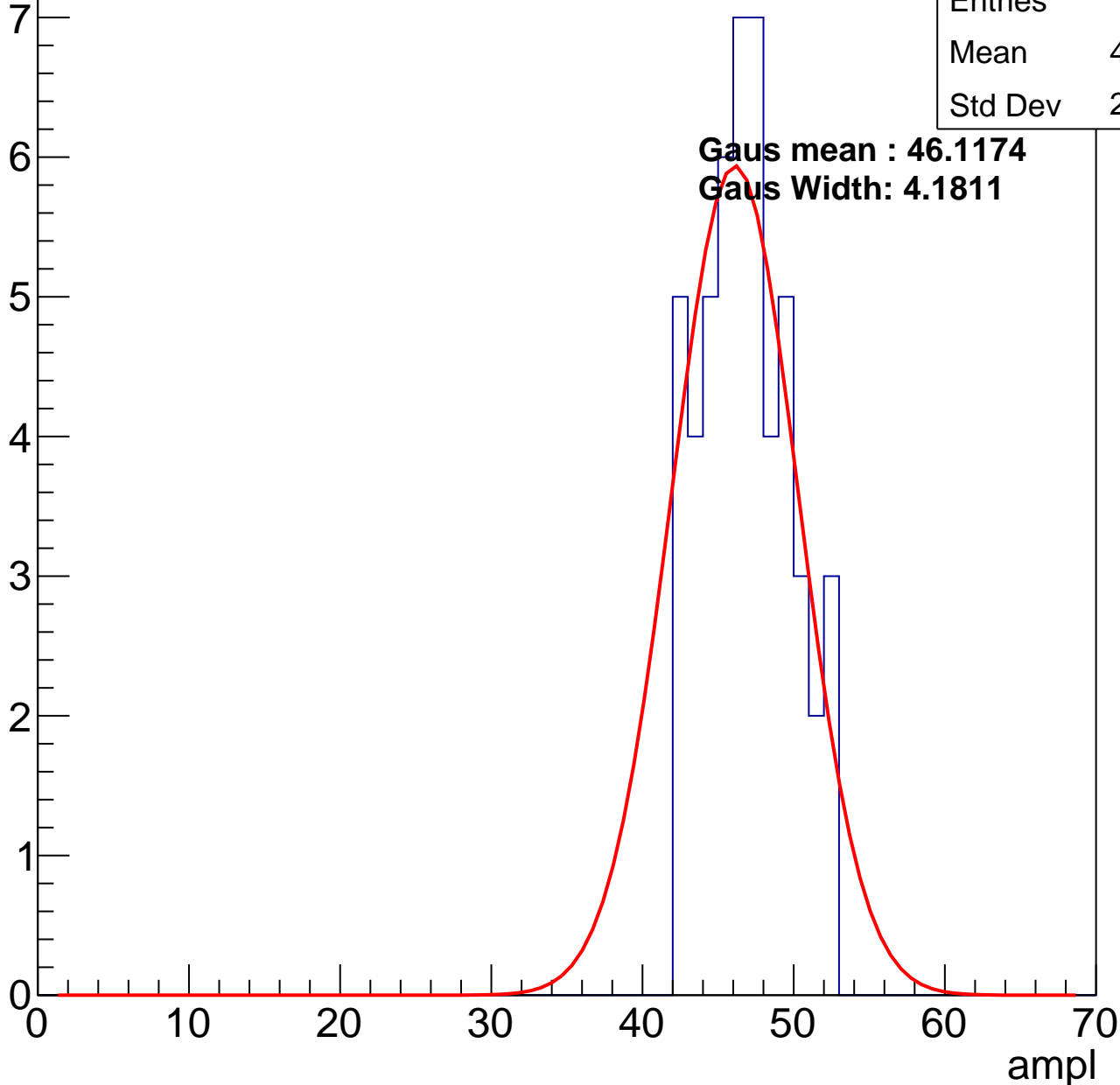
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	46.43
Std Dev	2.823

Gaus mean : 46.1174

Gaus Width: 4.1811



B0L002S, U12-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

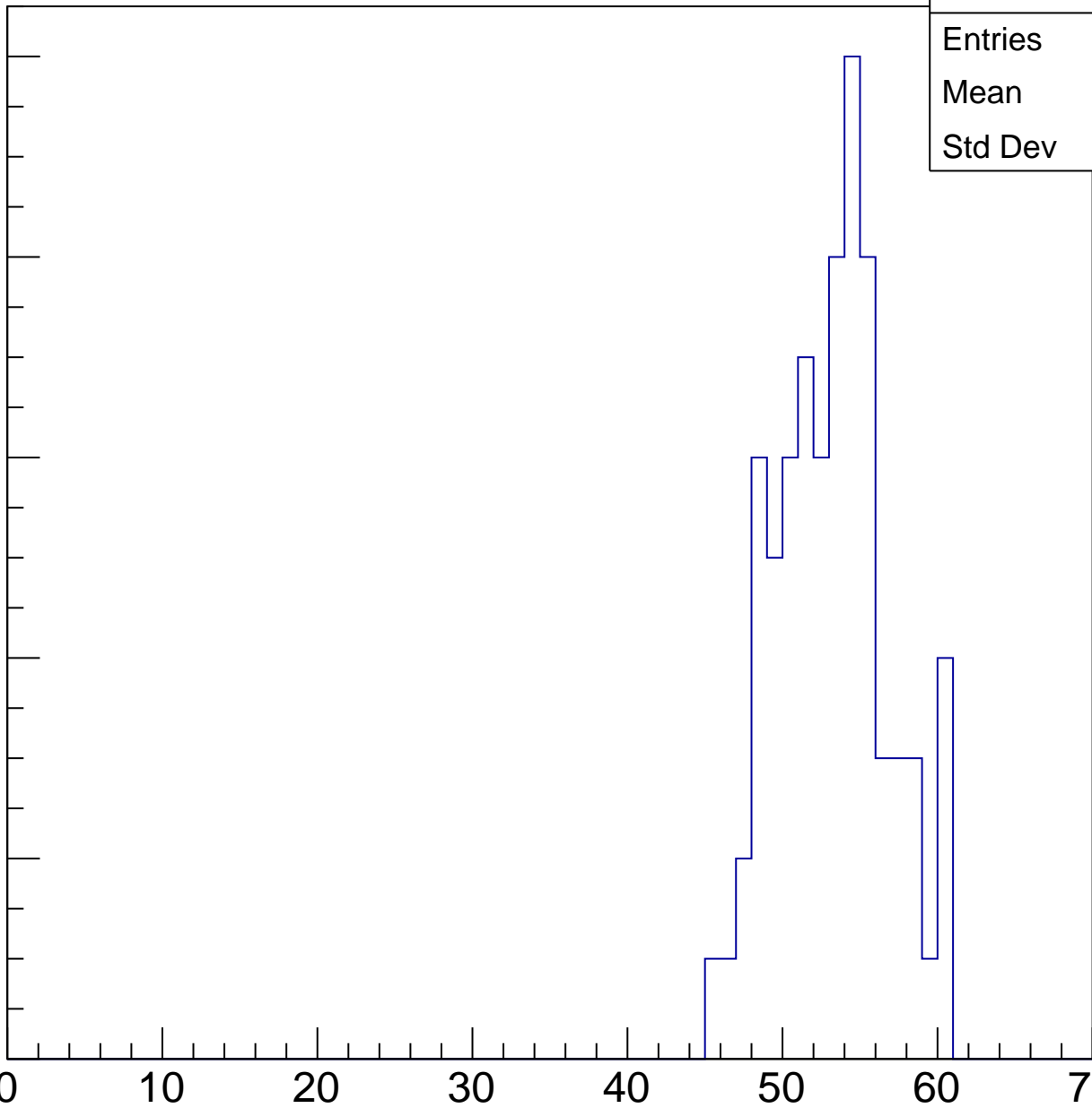
Entries	74
Mean	52.74
Std Dev	3.53

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

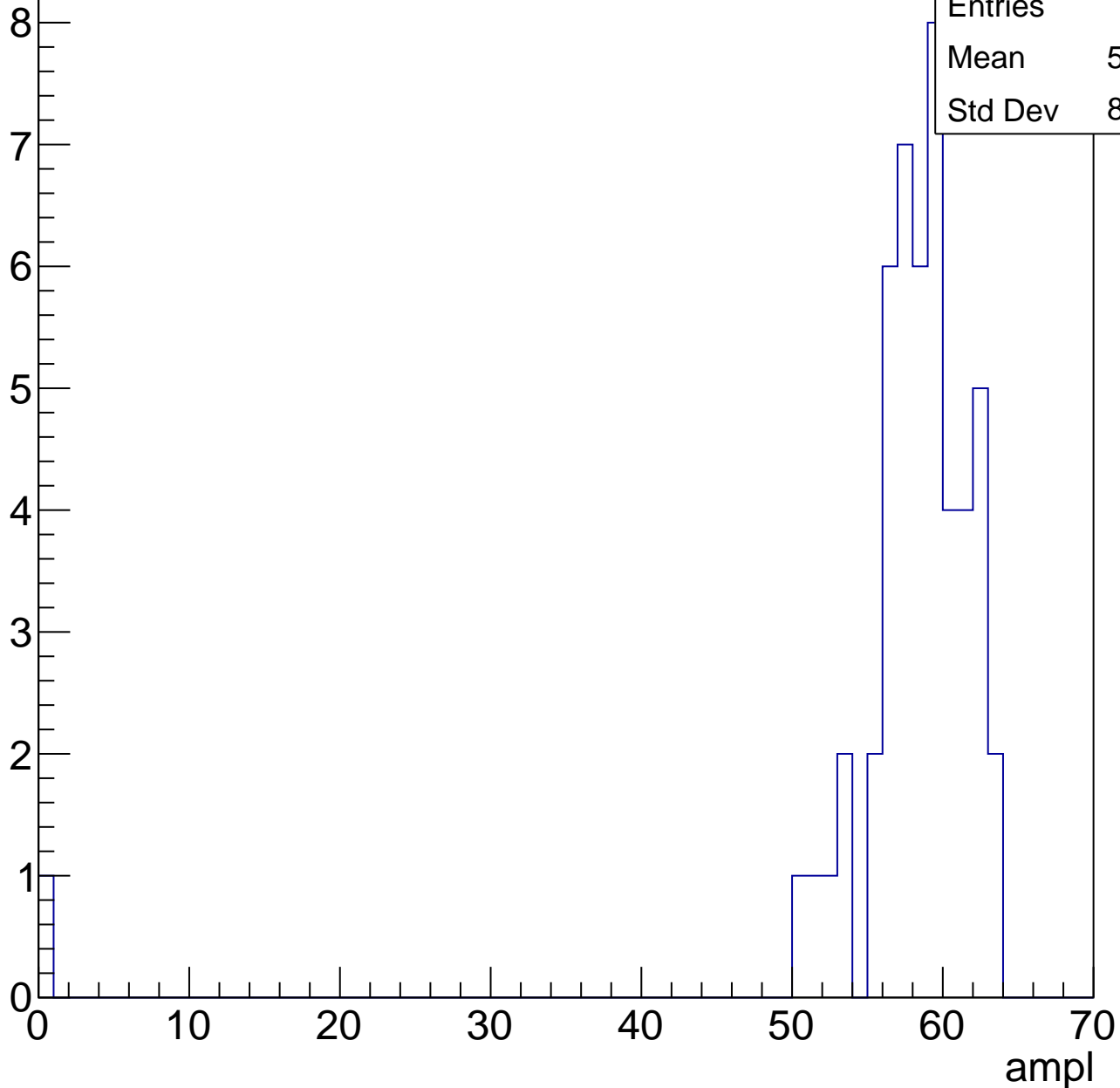


B0L002S, U12-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.88
Std Dev	8.648

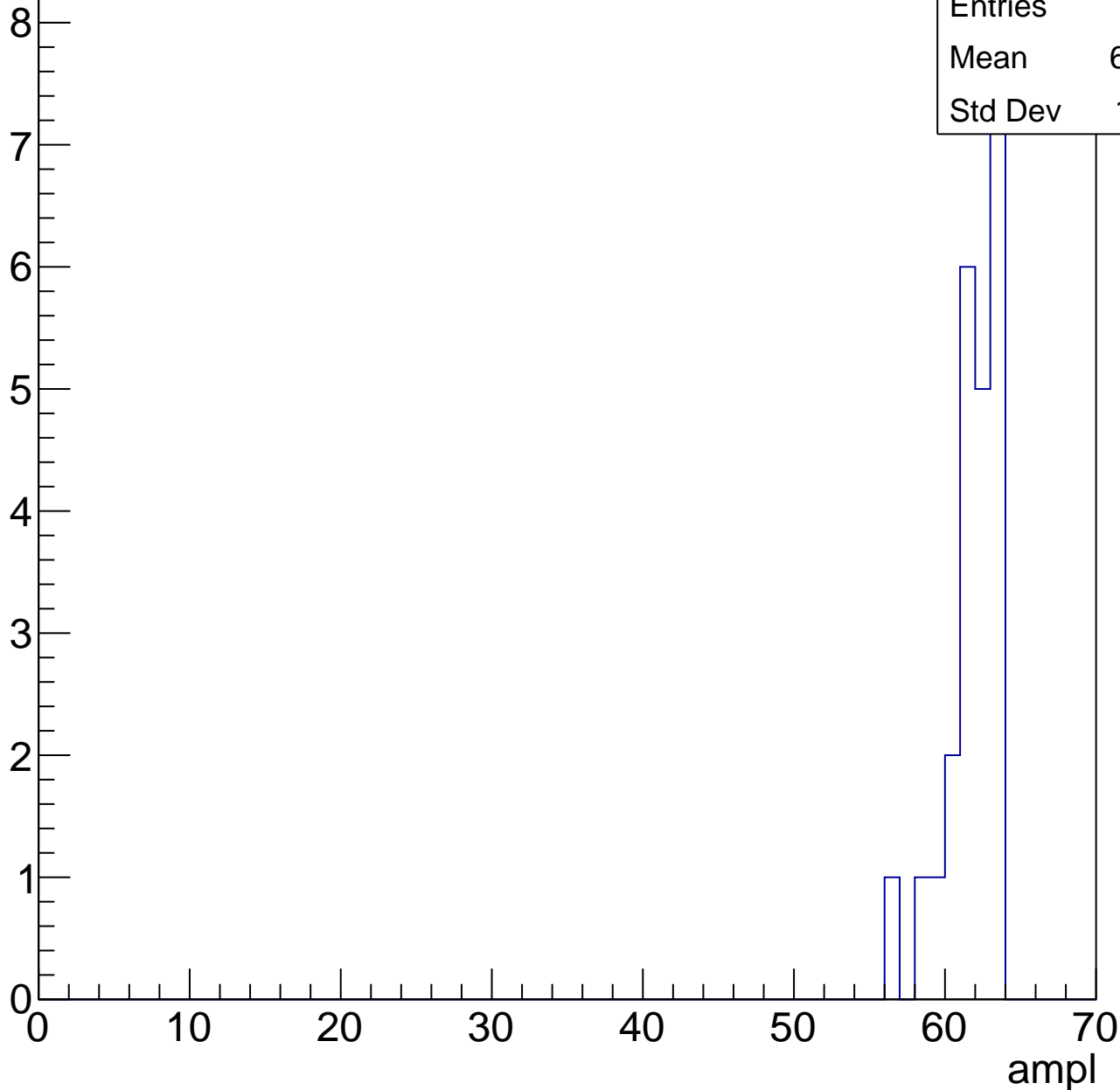


B0L002S, U12-ch19, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	61.38
Std Dev	1.751



B0L002S, U12-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch20, adc0

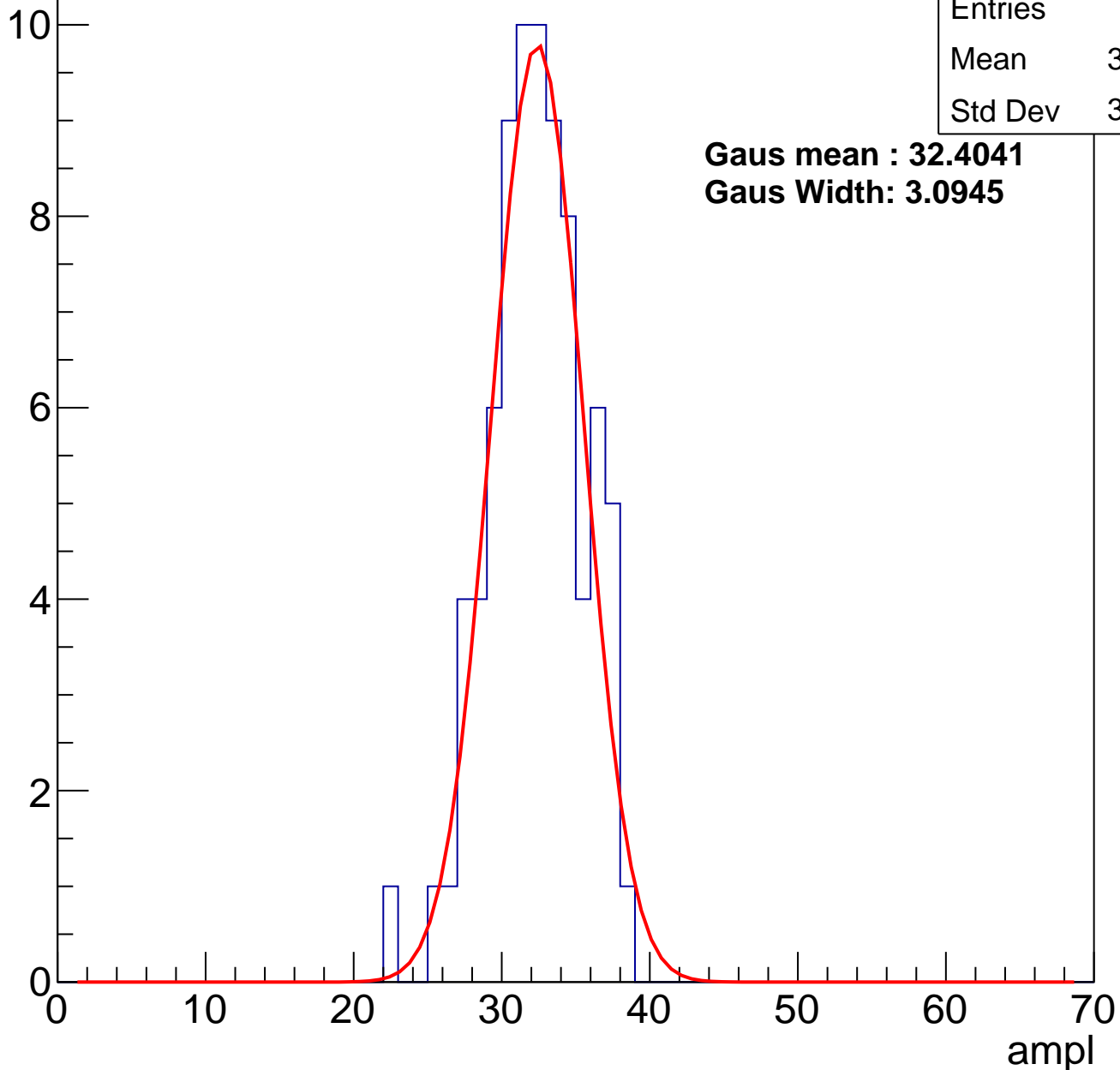
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	31.84
Std Dev	3.144

Gaus mean : 32.4041

Gaus Width: 3.0945

Entry



B0L002S, U12-ch20, adc1

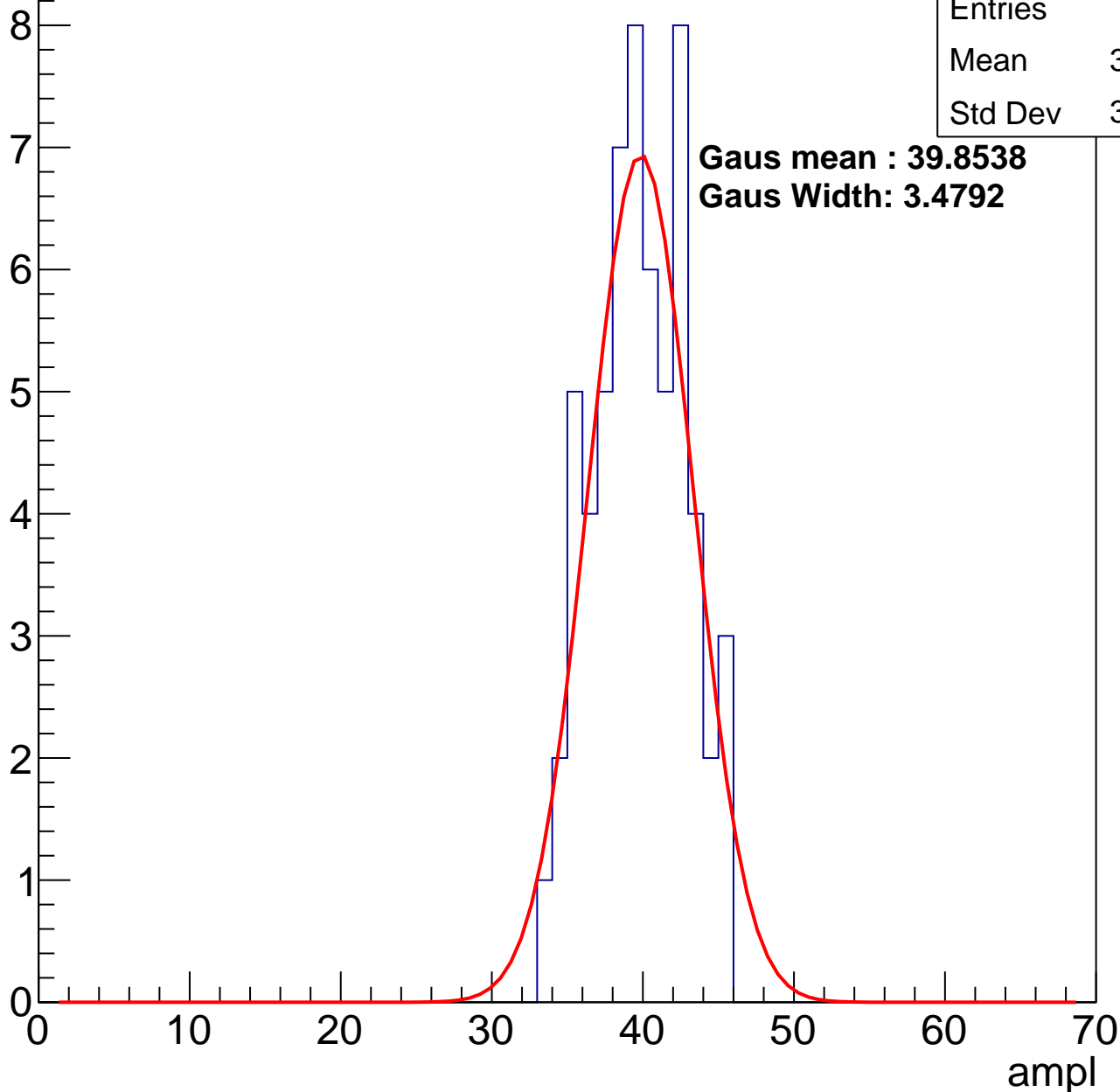
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	39.32
Std Dev	3.008

Gaus mean : 39.8538

Gaus Width: 3.4792



B0L002S, U12-ch20, adc2

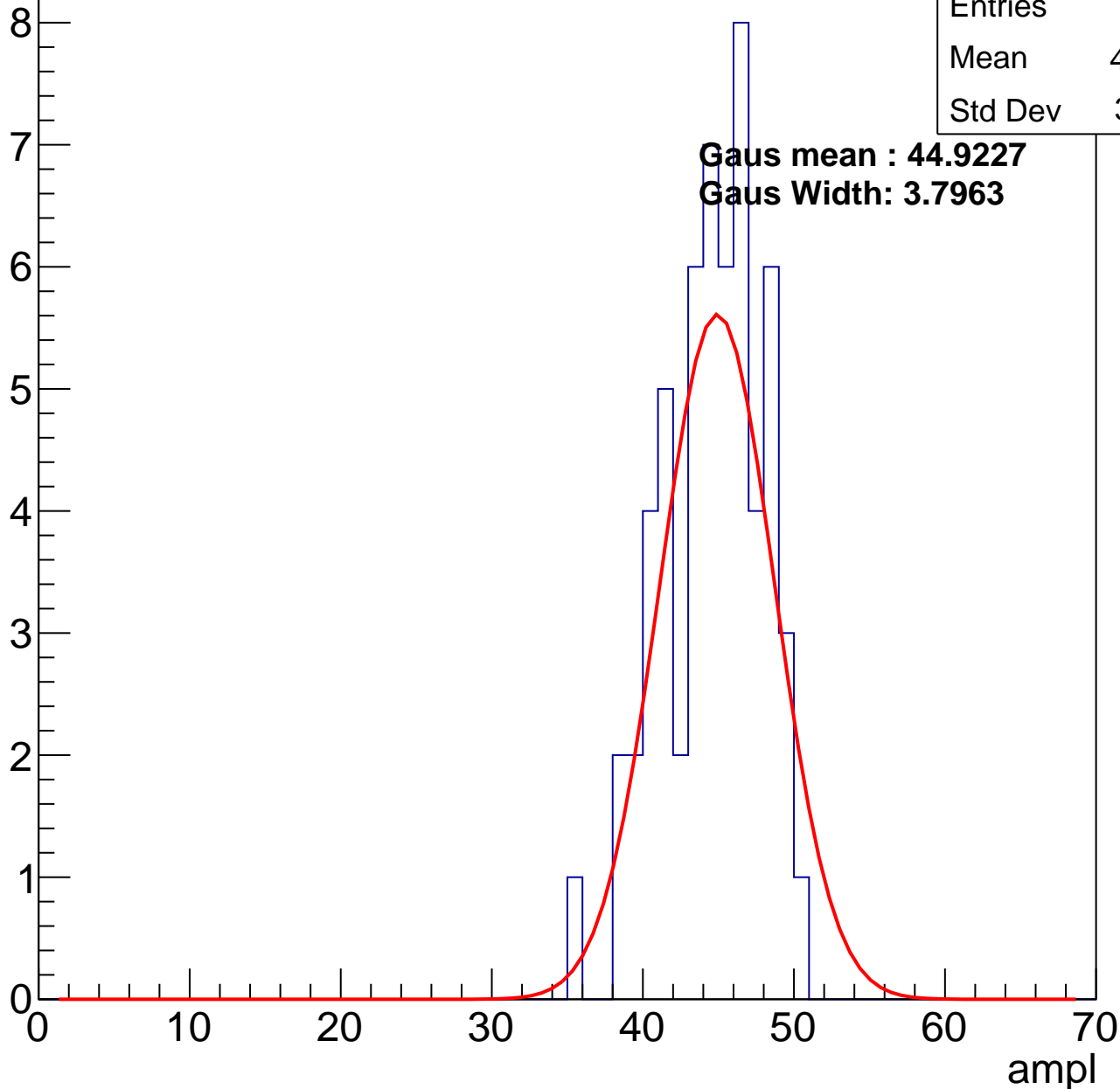
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.12
Std Dev	3.261

Gaus mean : 44.9227

Gaus Width: 3.7963

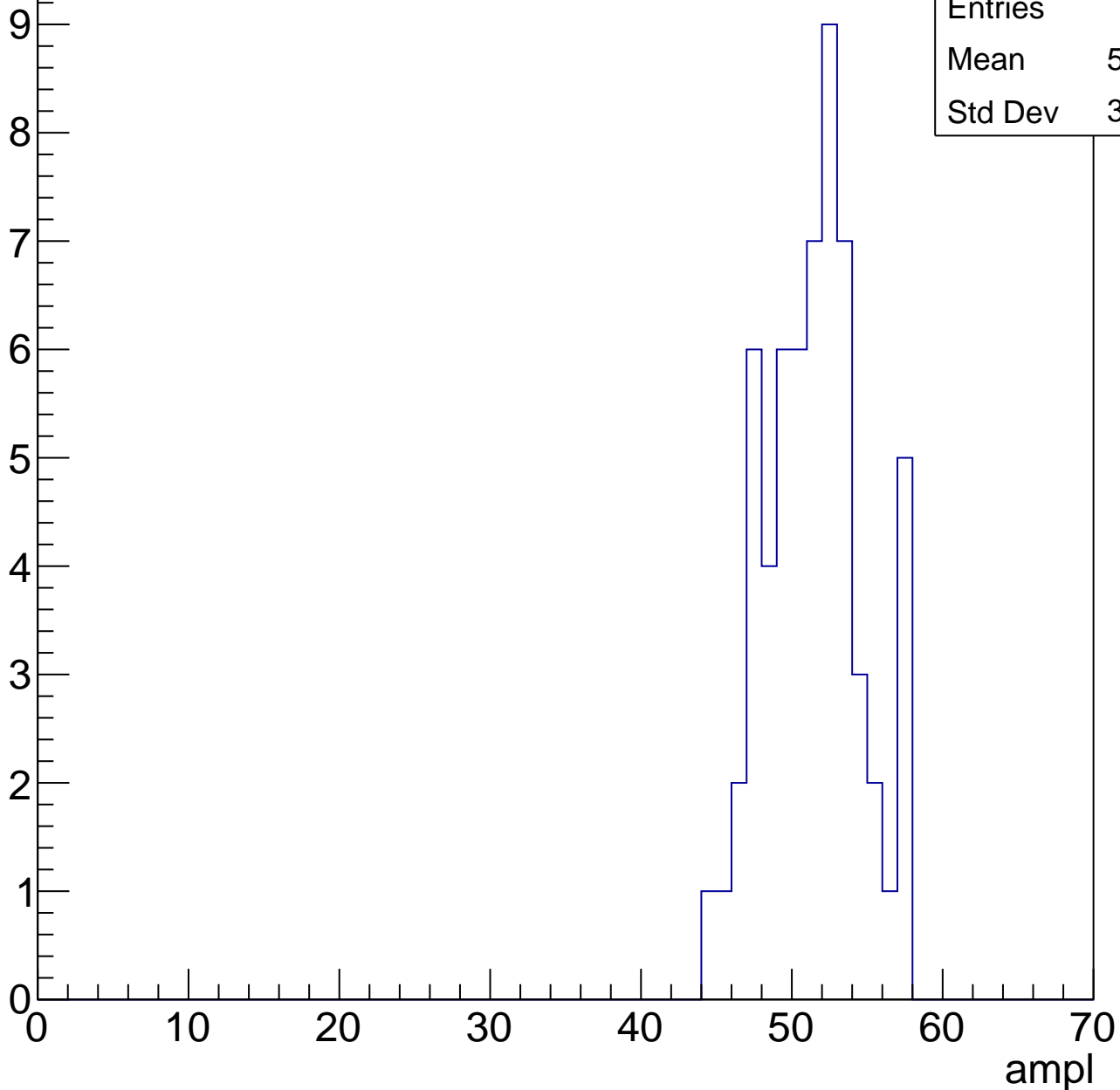


B0L002S, U12-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.97
Std Dev	3.157

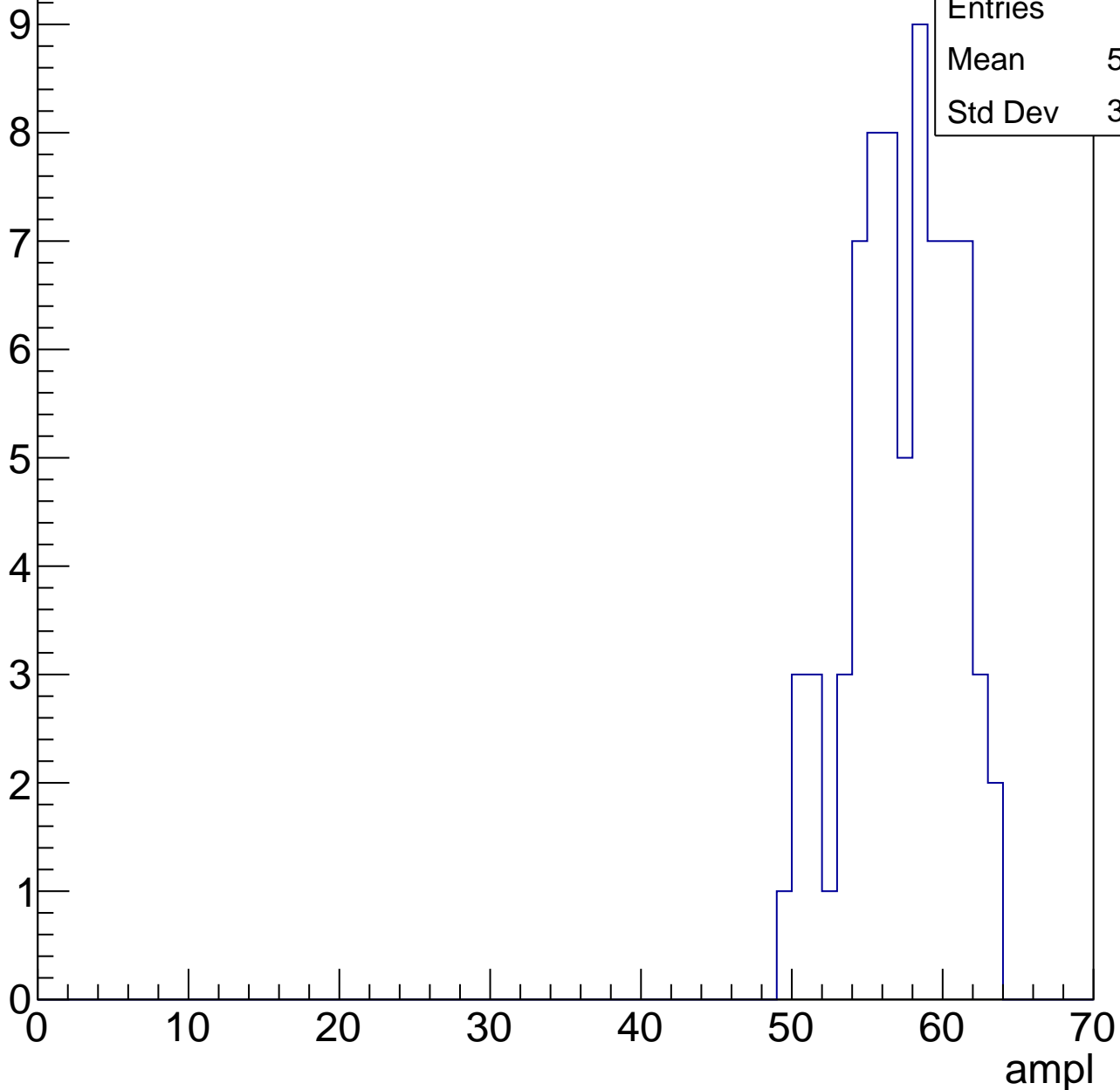


B0L002S, U12-ch20, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	56.86
Std Dev	3.394

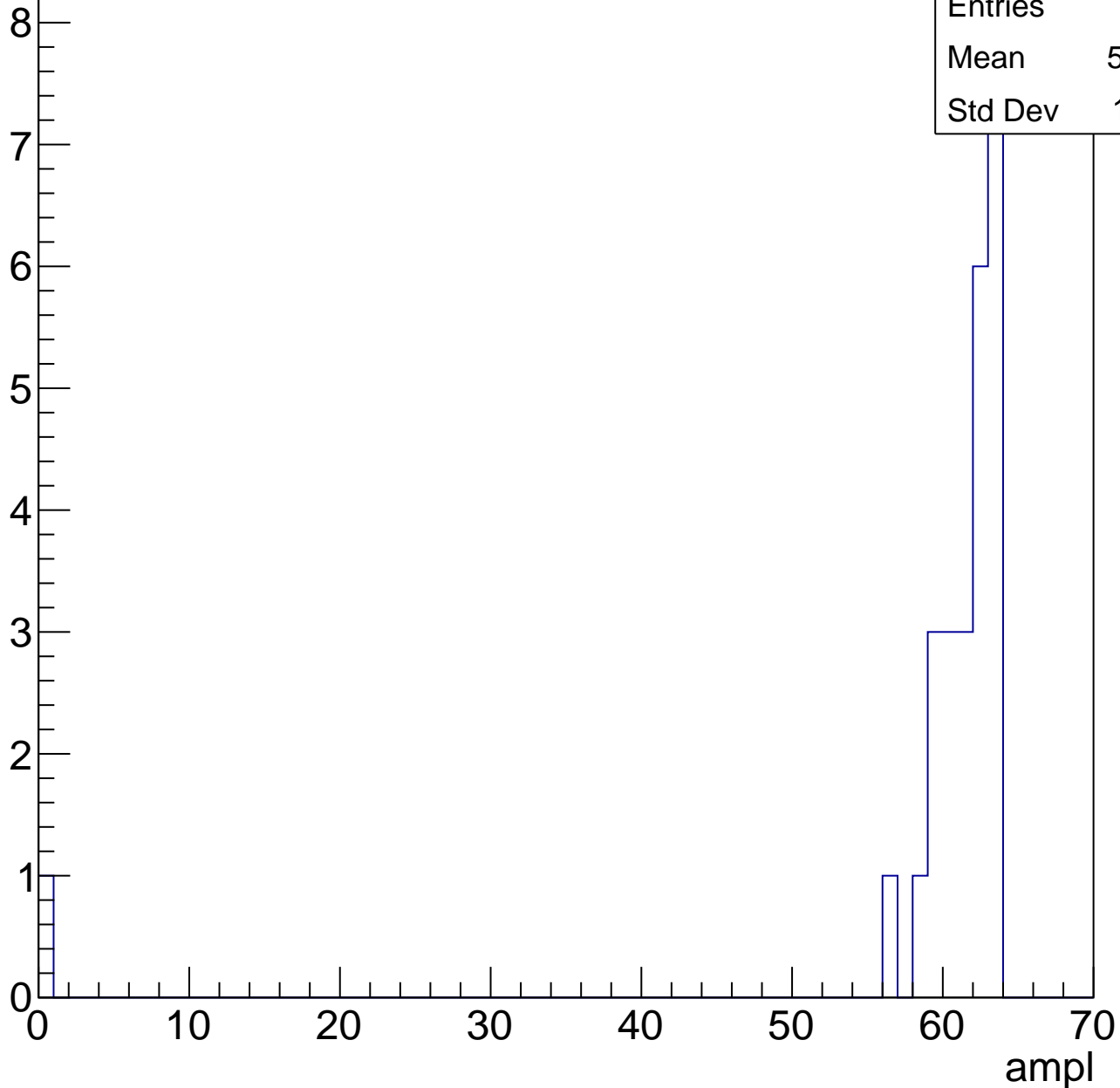


B0L002S, U12-ch20, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	58.85
Std Dev	11.91



B0L002S, U12-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch21, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	91
Mean	30.23
Std Dev	3.855

Gaus mean : 30.8853

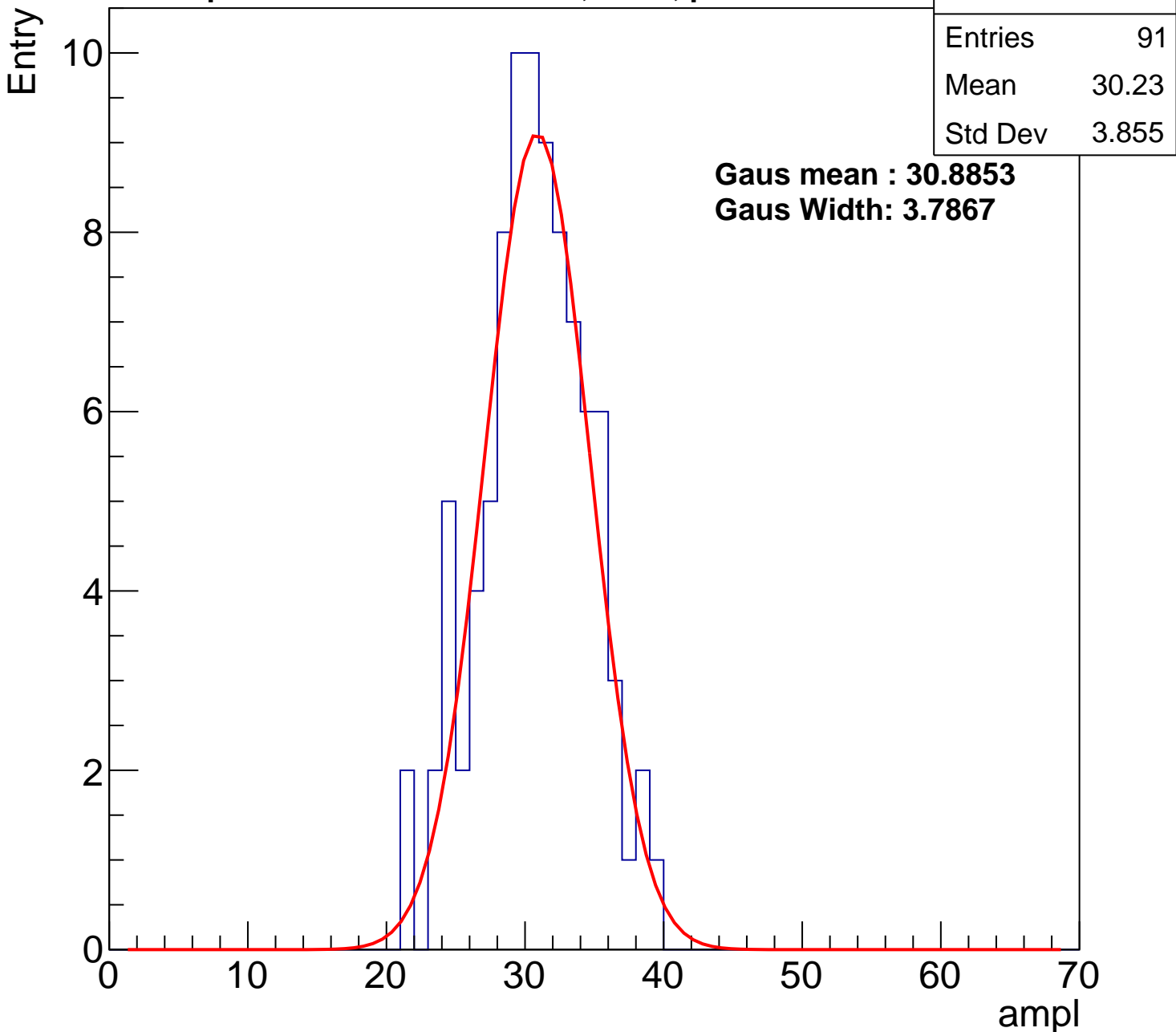
Gaus Width: 3.7867

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch21, adc1

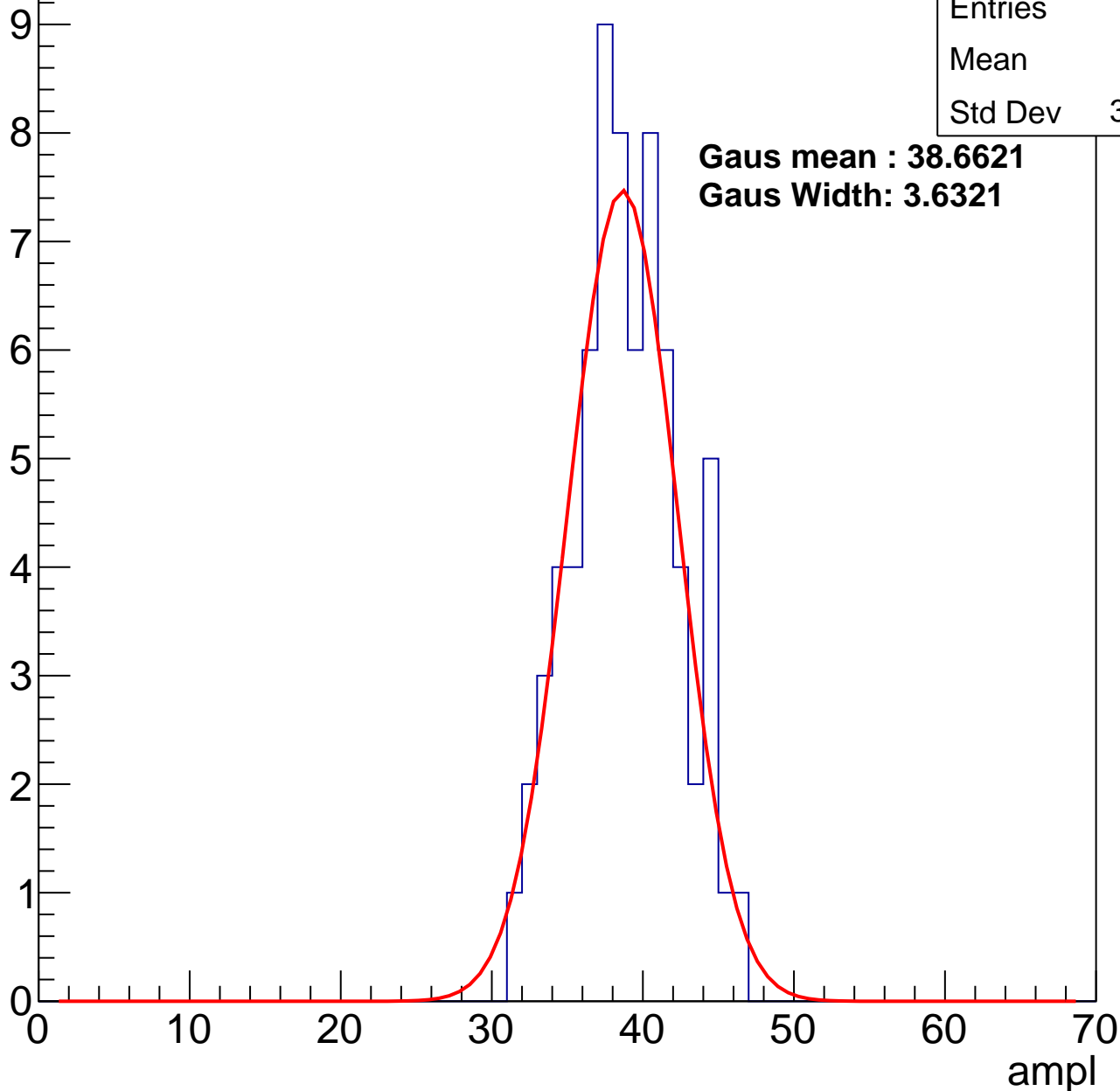
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	38.4
Std Dev	3.416

Gaus mean : 38.6621

Gaus Width: 3.6321



B0L002S, U12-ch21, adc2

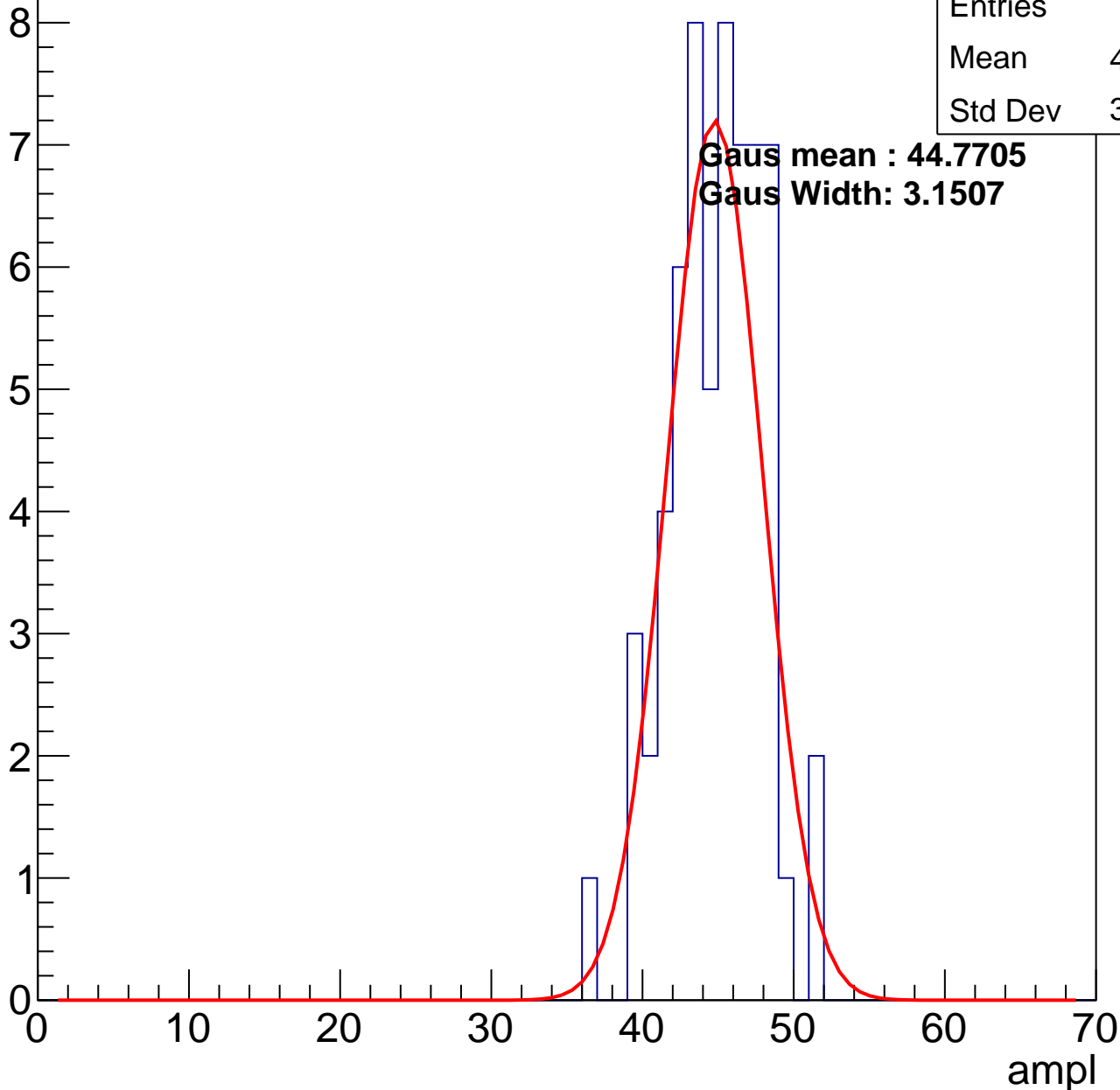
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.44
Std Dev	3.033

Gaus mean : 44.7705

Gaus Width: 3.1507

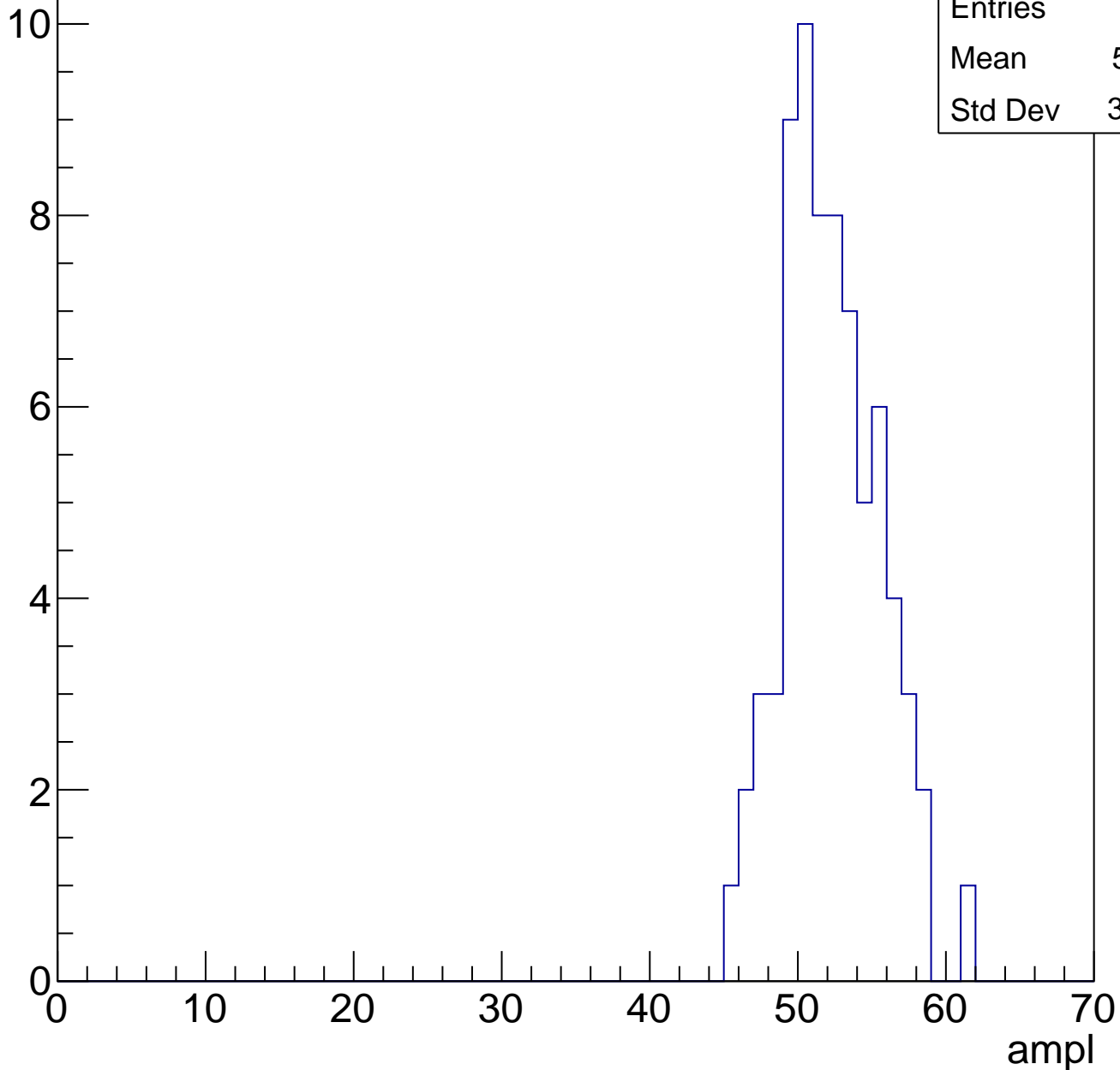


B0L002S, U12-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	51.81
Std Dev	3.213

Entry

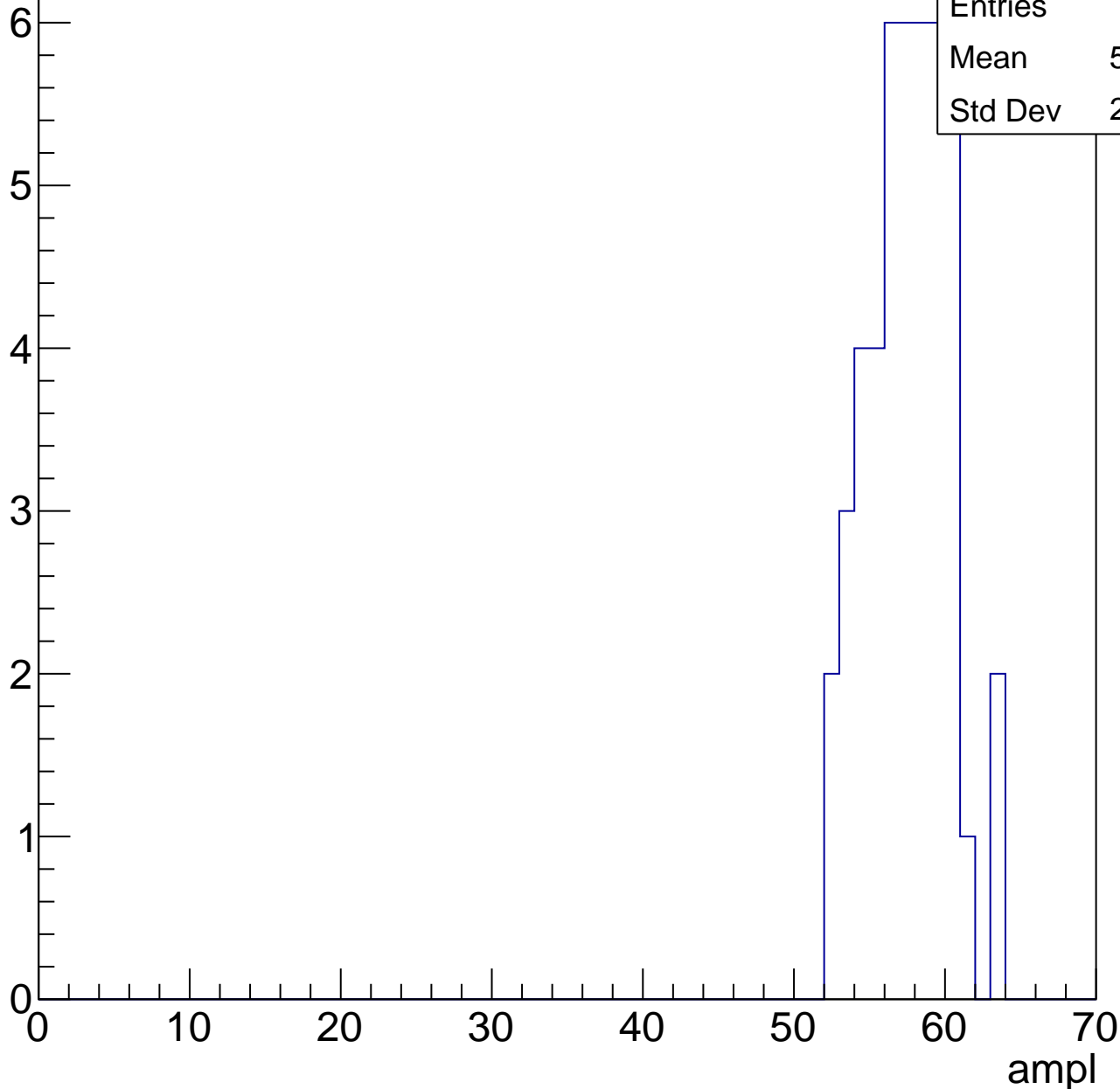


B0L002S, U12-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	57.09
Std Dev	2.669

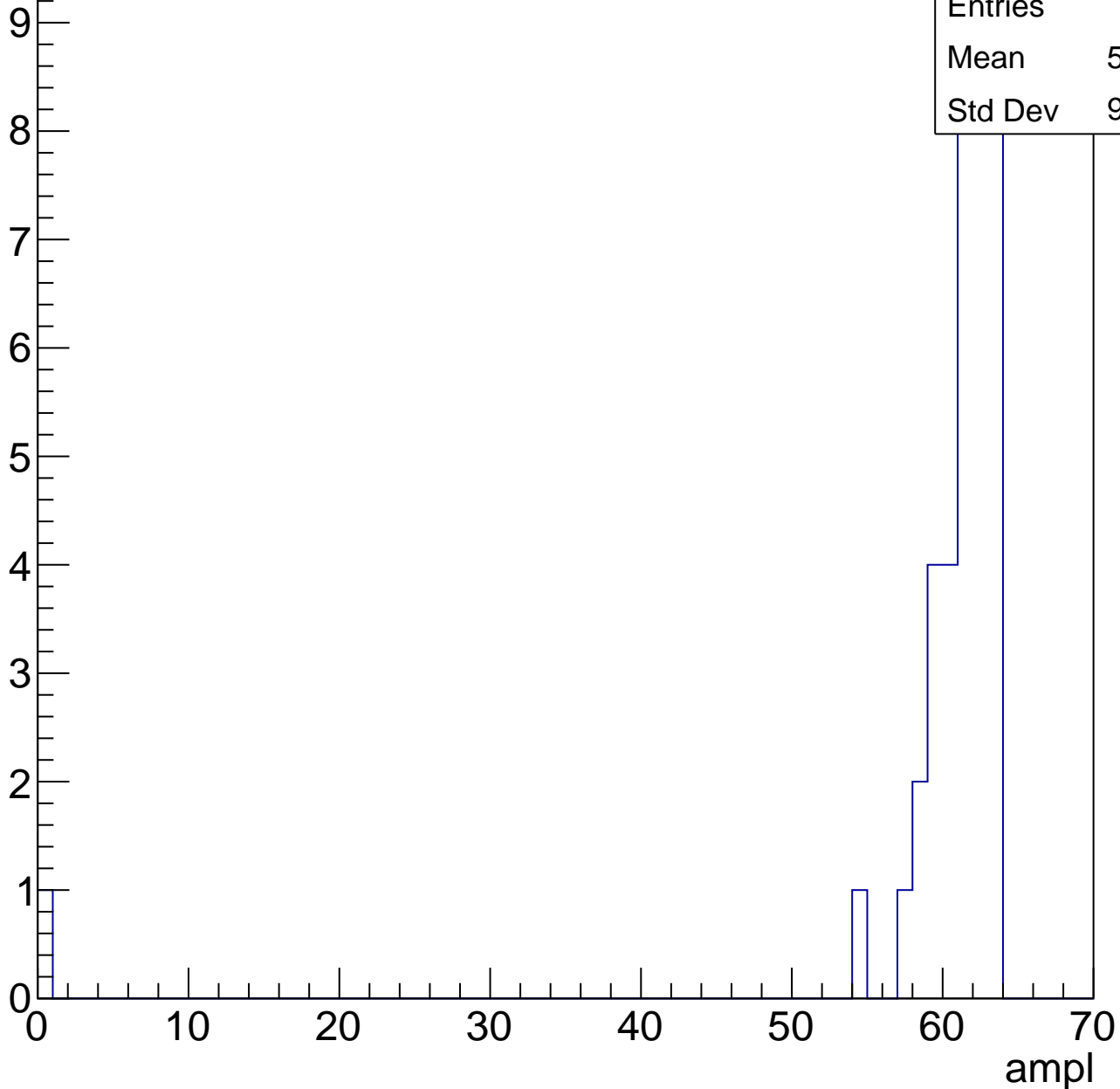


B0L002S, U12-ch21, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.29
Std Dev	9.939



B0L002S, U12-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	62
Std Dev	0

B0L002S, U12-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U12-ch22, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	30.25
Std Dev	3.244

Gaus mean : 30.7014

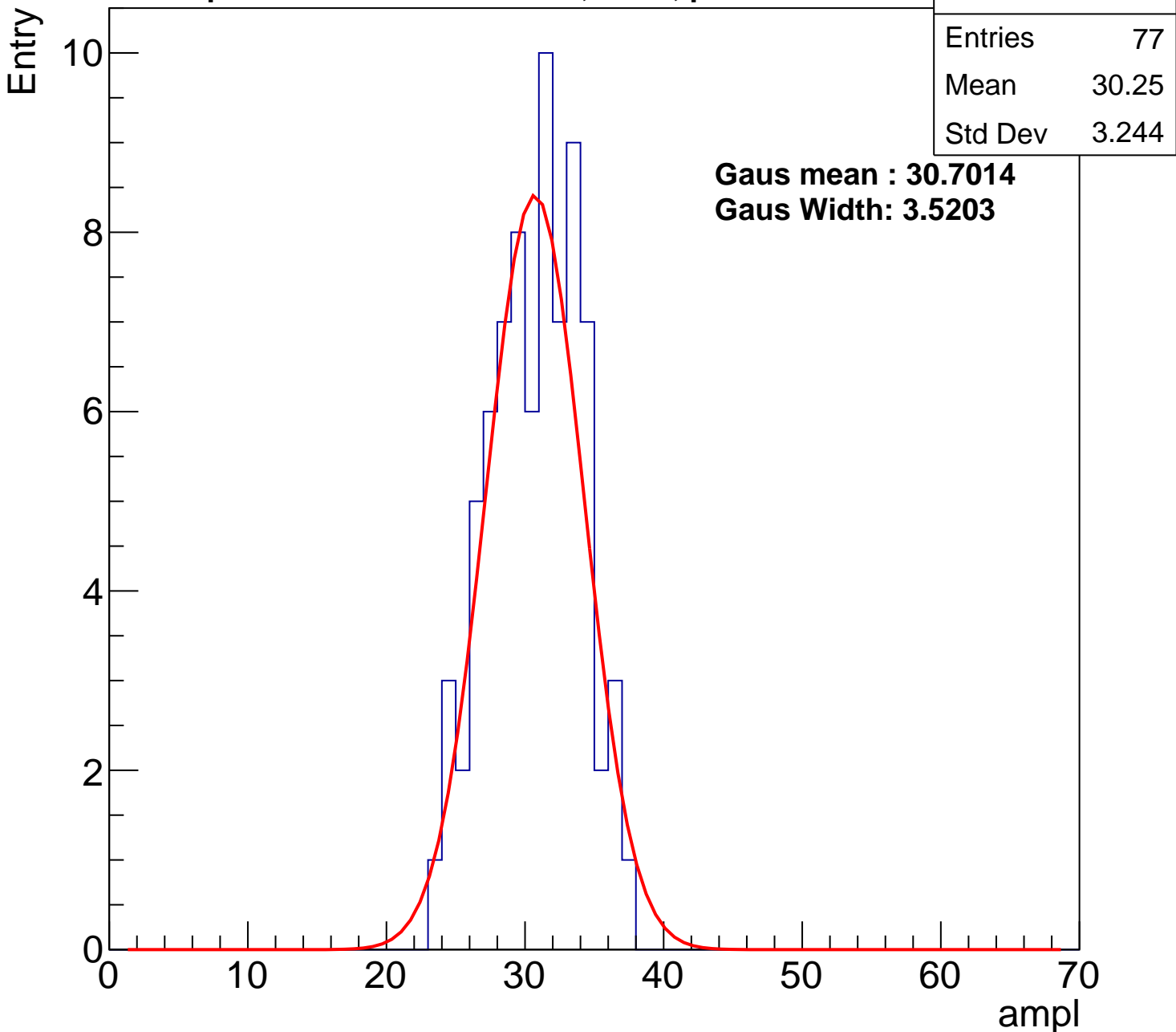
Gaus Width: 3.5203

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch22, adc1

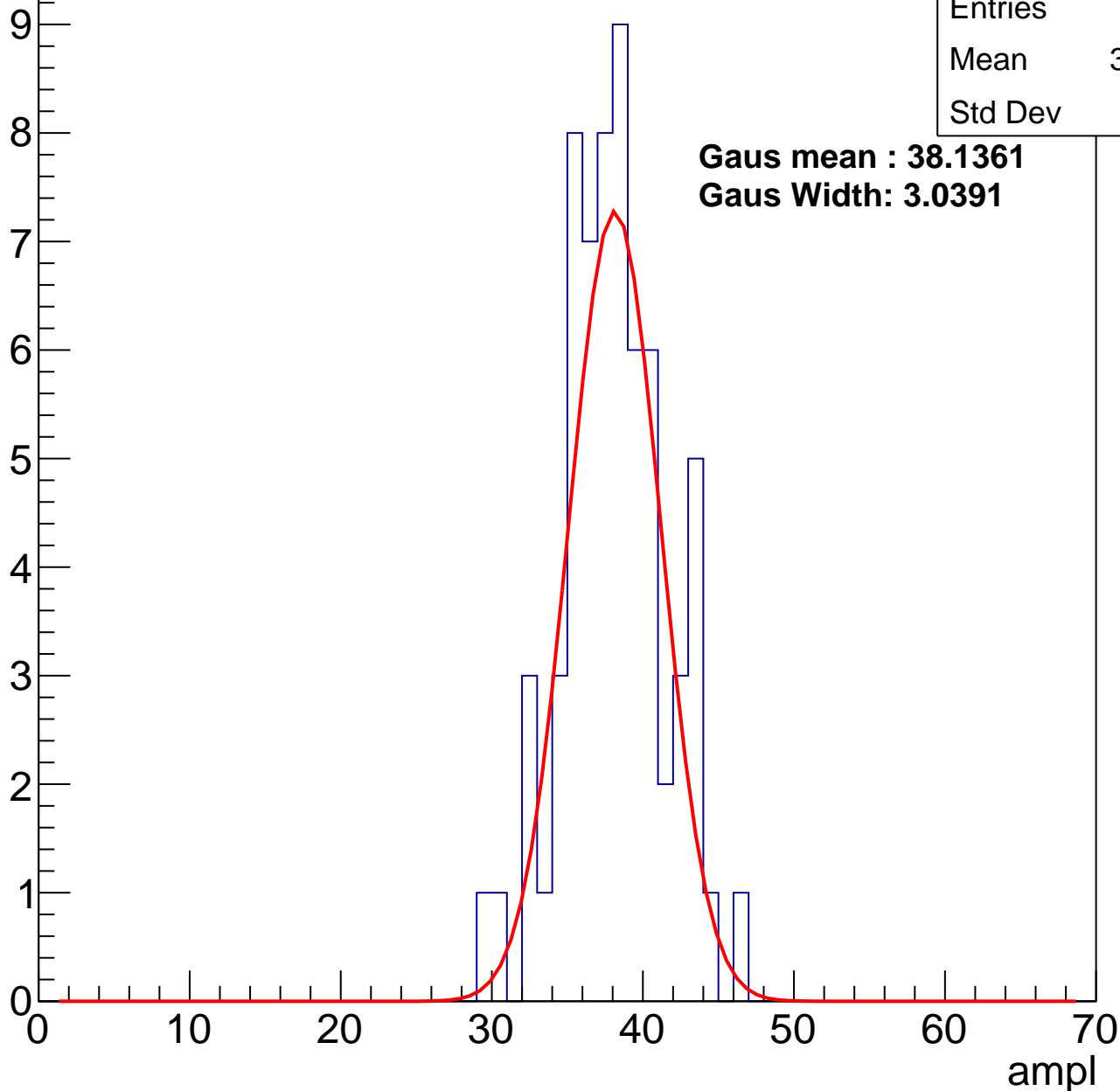
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.65
Std Dev	3.39

Gaus mean : 38.1361

Gaus Width: 3.0391



B0L002S, U12-ch22, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	44.43
Std Dev	3.365

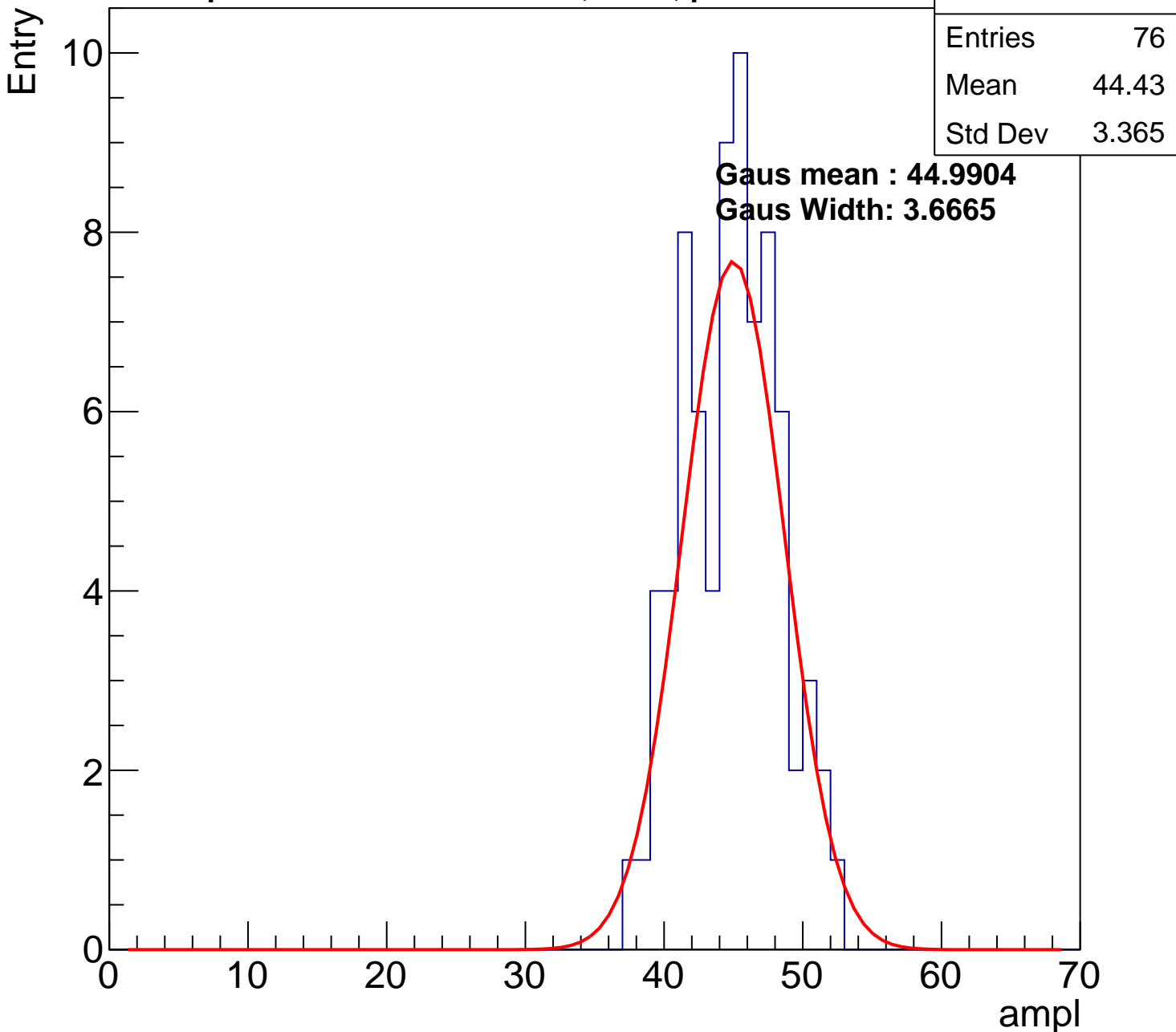
Gaus mean : 44.9904

Gaus Width: 3.6665

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

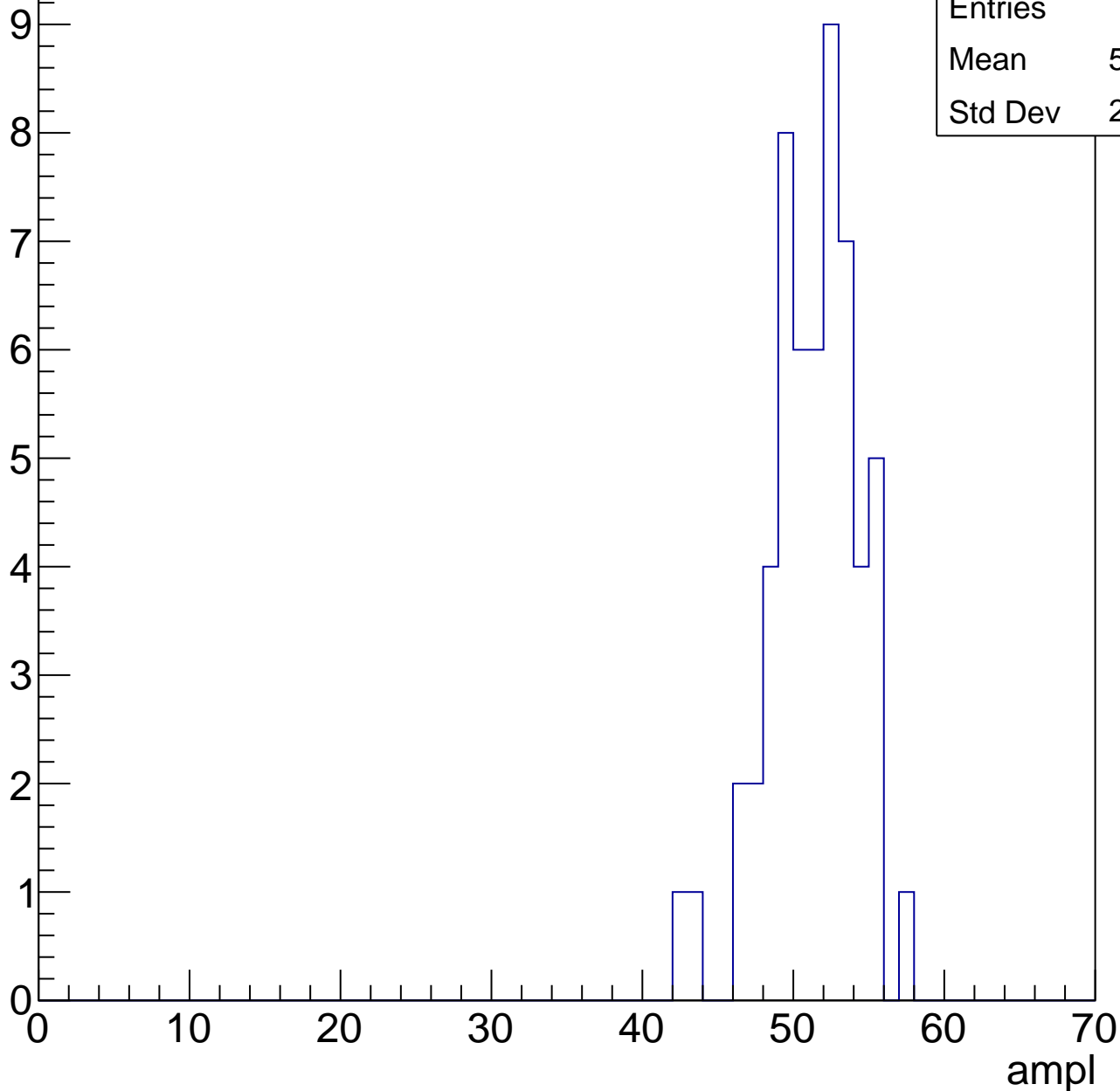


B0L002S, U12-ch22, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	50.86
Std Dev	2.955



B0L002S, U12-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	58
Mean	56.91
Std Dev	2.8

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

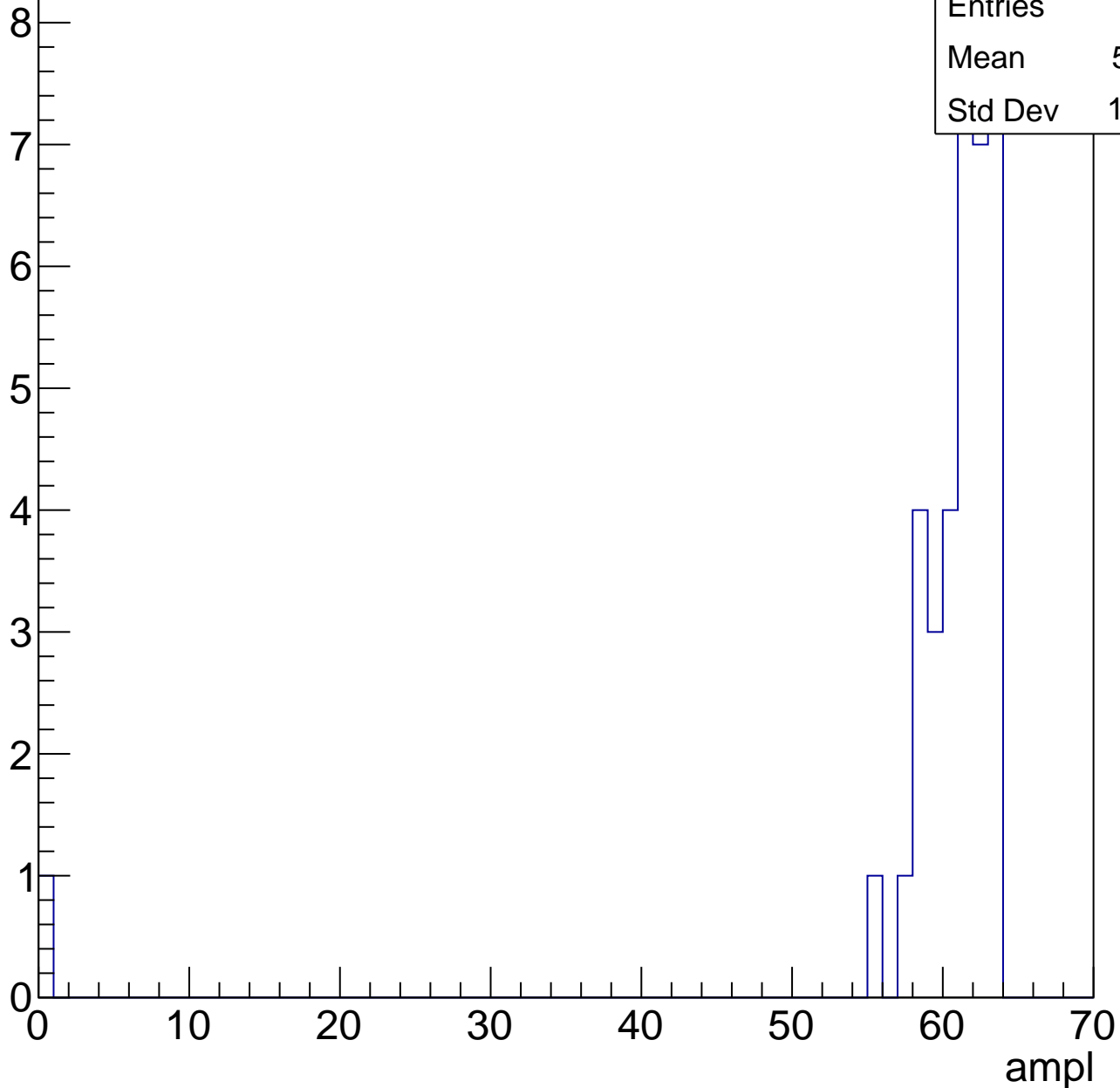
70

B0L002S, U12-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.11
Std Dev	10.04



B0L002S, U12-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch23, adc0

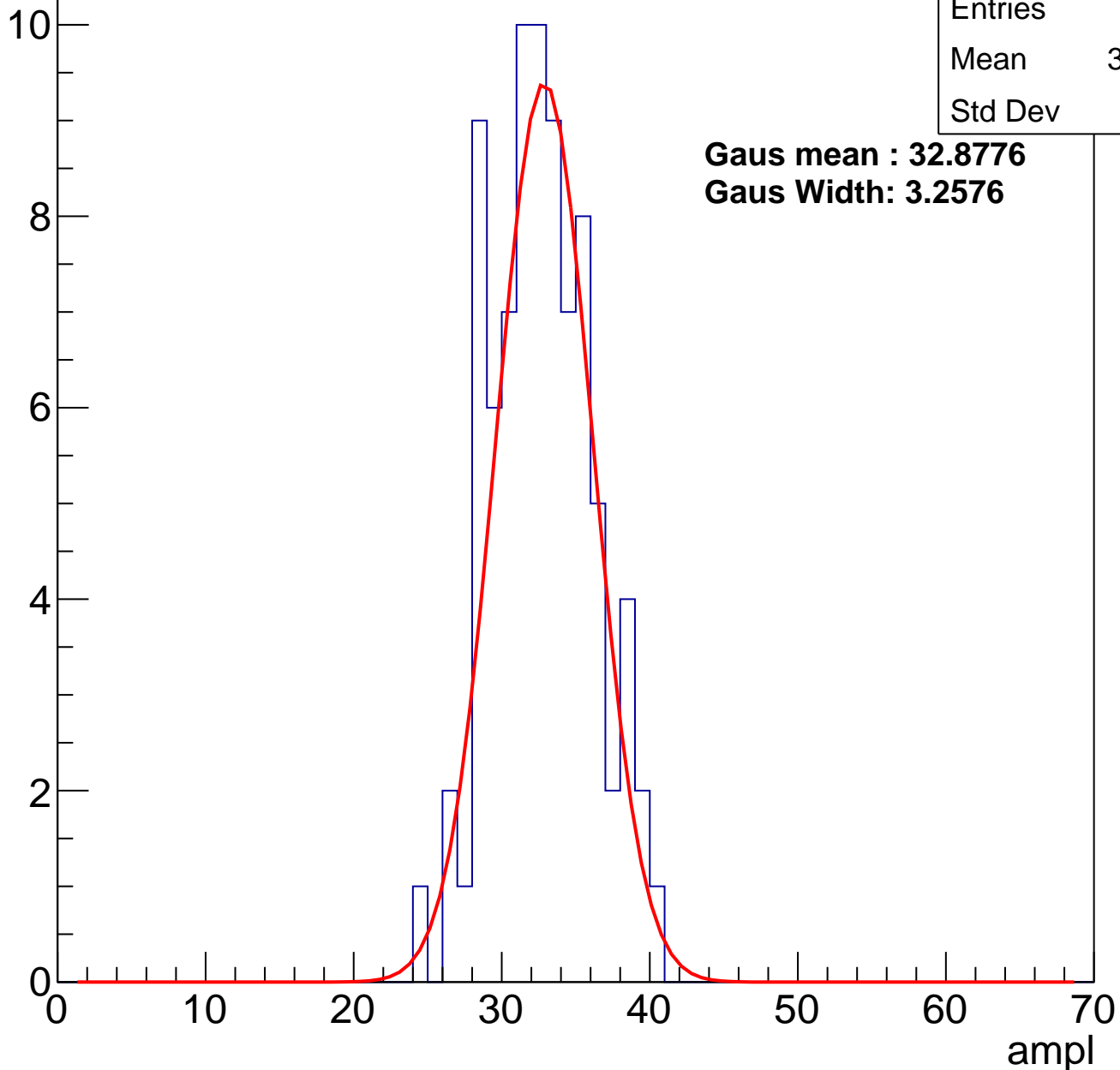
calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	32.24
Std Dev	3.34

Gaus mean : 32.8776

Gaus Width: 3.2576

Entry



B0L002S, U12-ch23, adc1

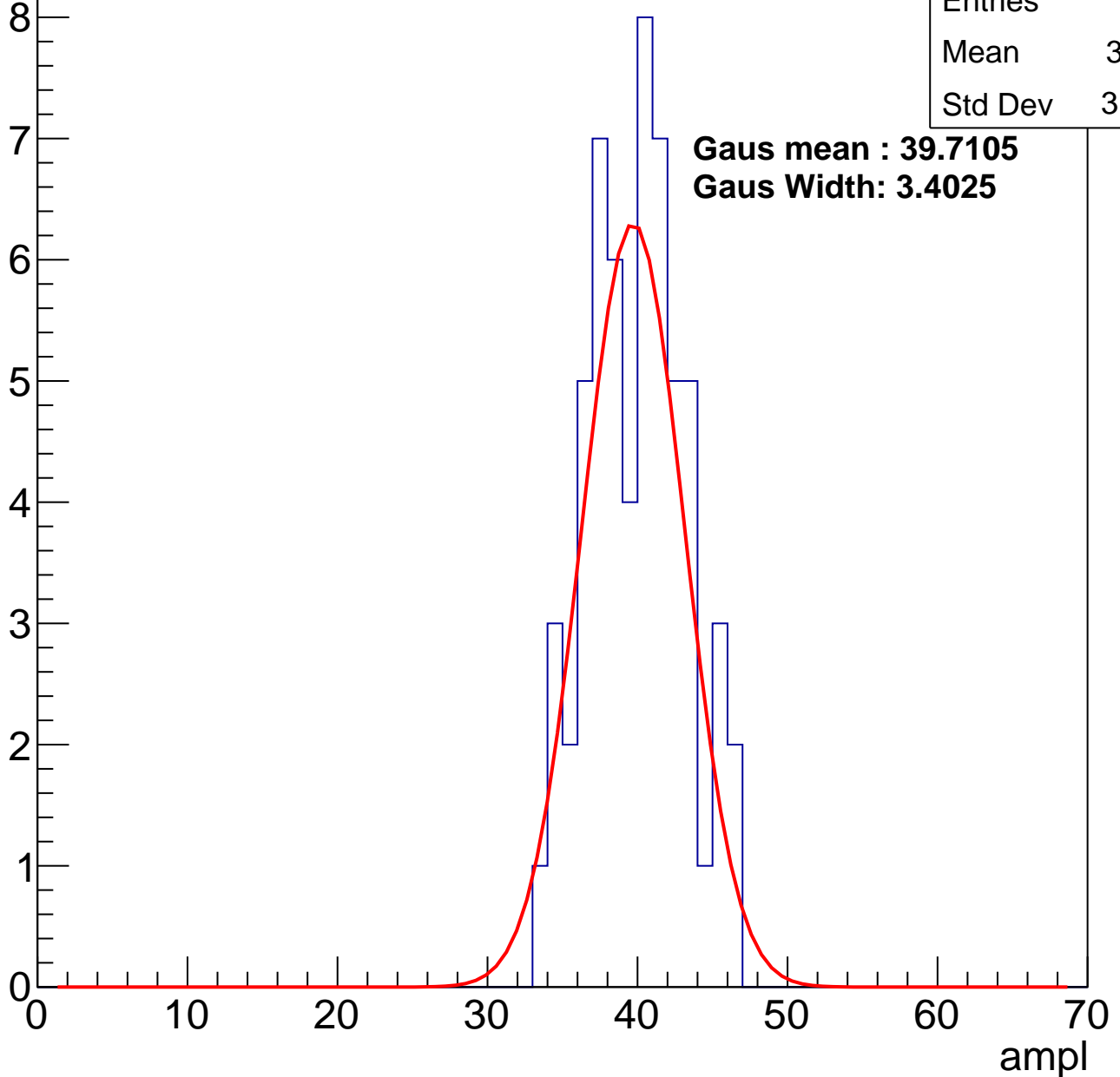
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	39.51
Std Dev	3.186

Gaus mean : 39.7105

Gaus Width: 3.4025



B0L002S, U12-ch23, adc2

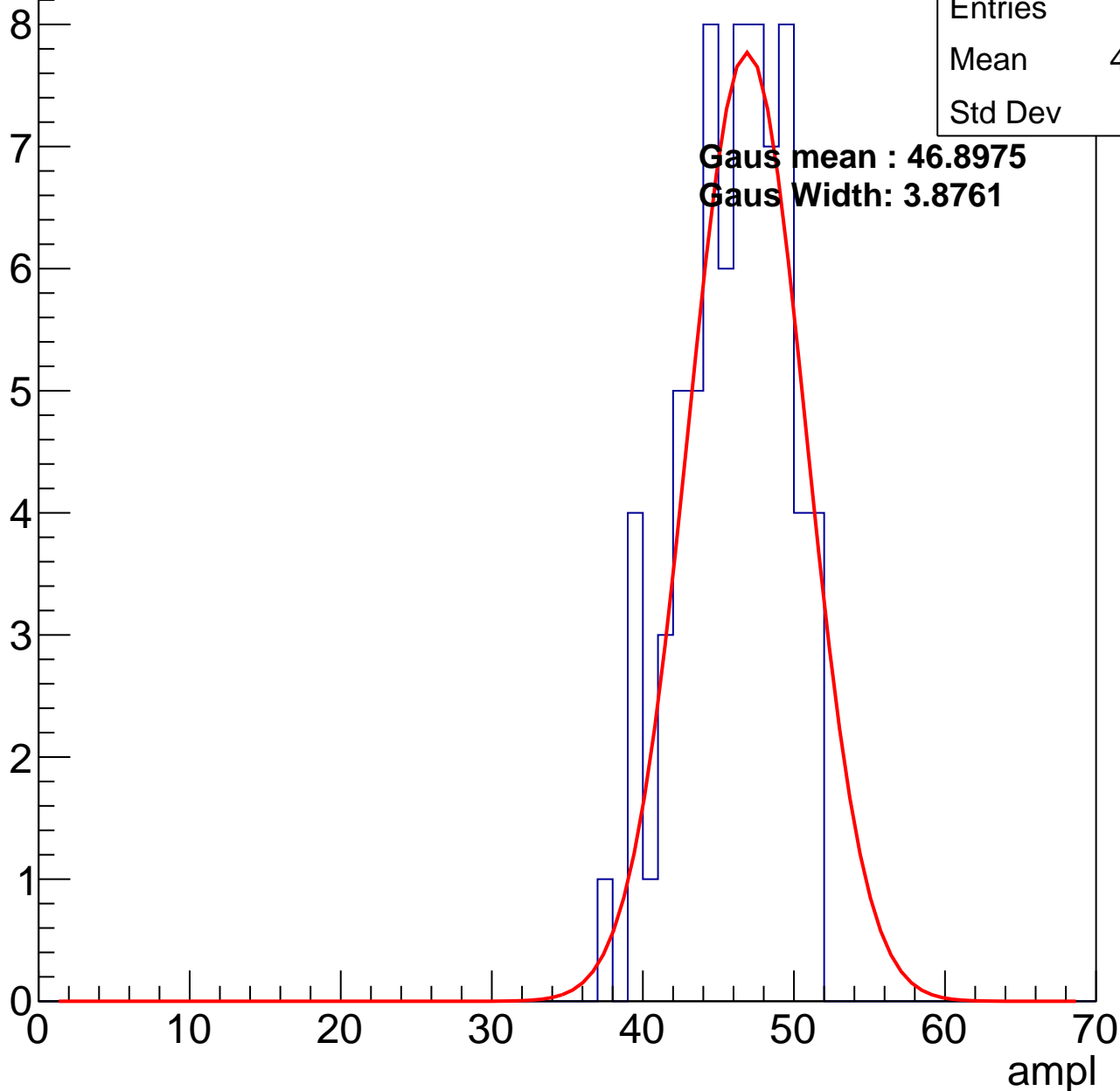
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45.54
Std Dev	3.35

Gaus mean : 46.8975

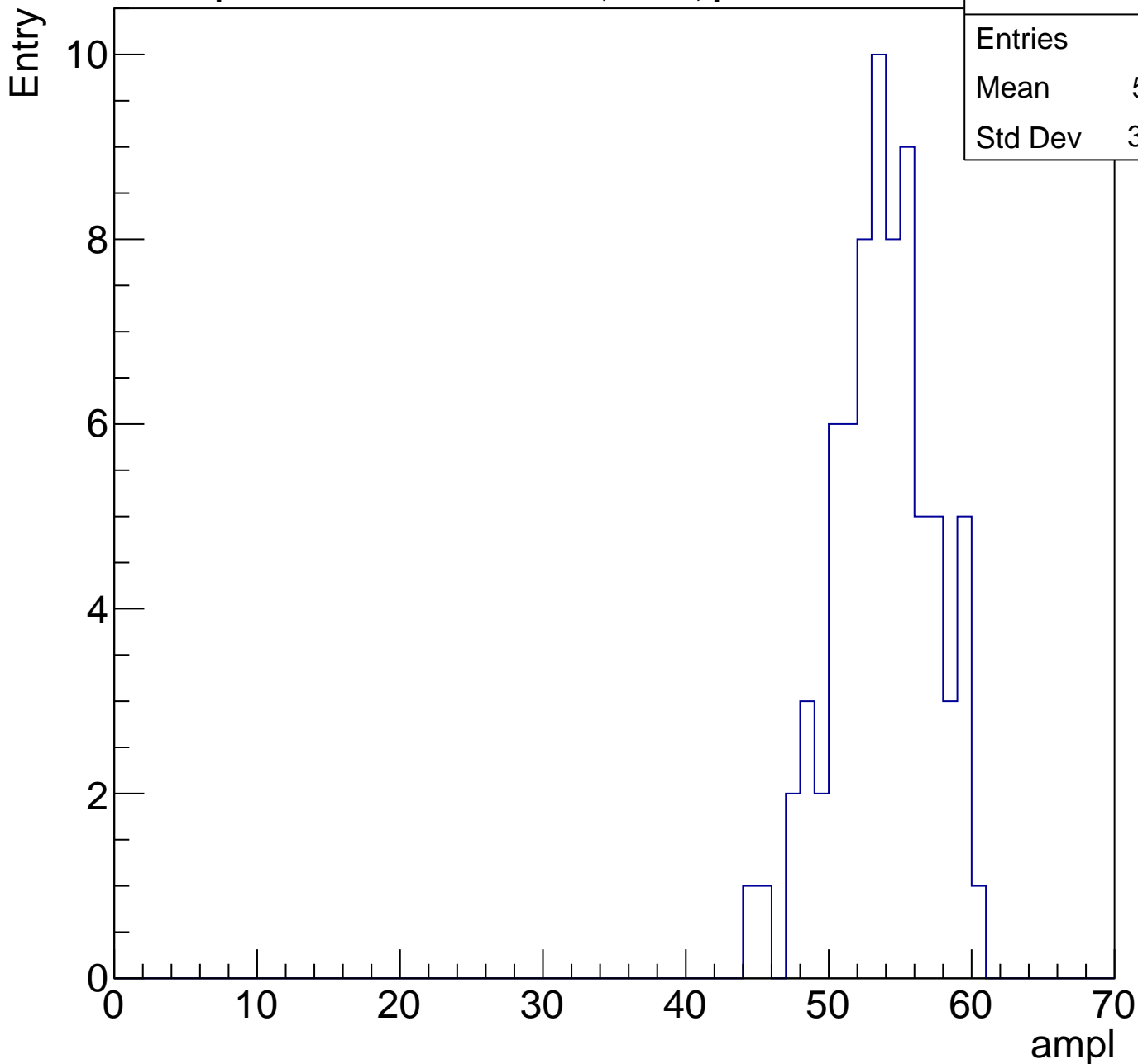
Gaus Width: 3.8761



B0L002S, U12-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	53.31
Std Dev	3.417

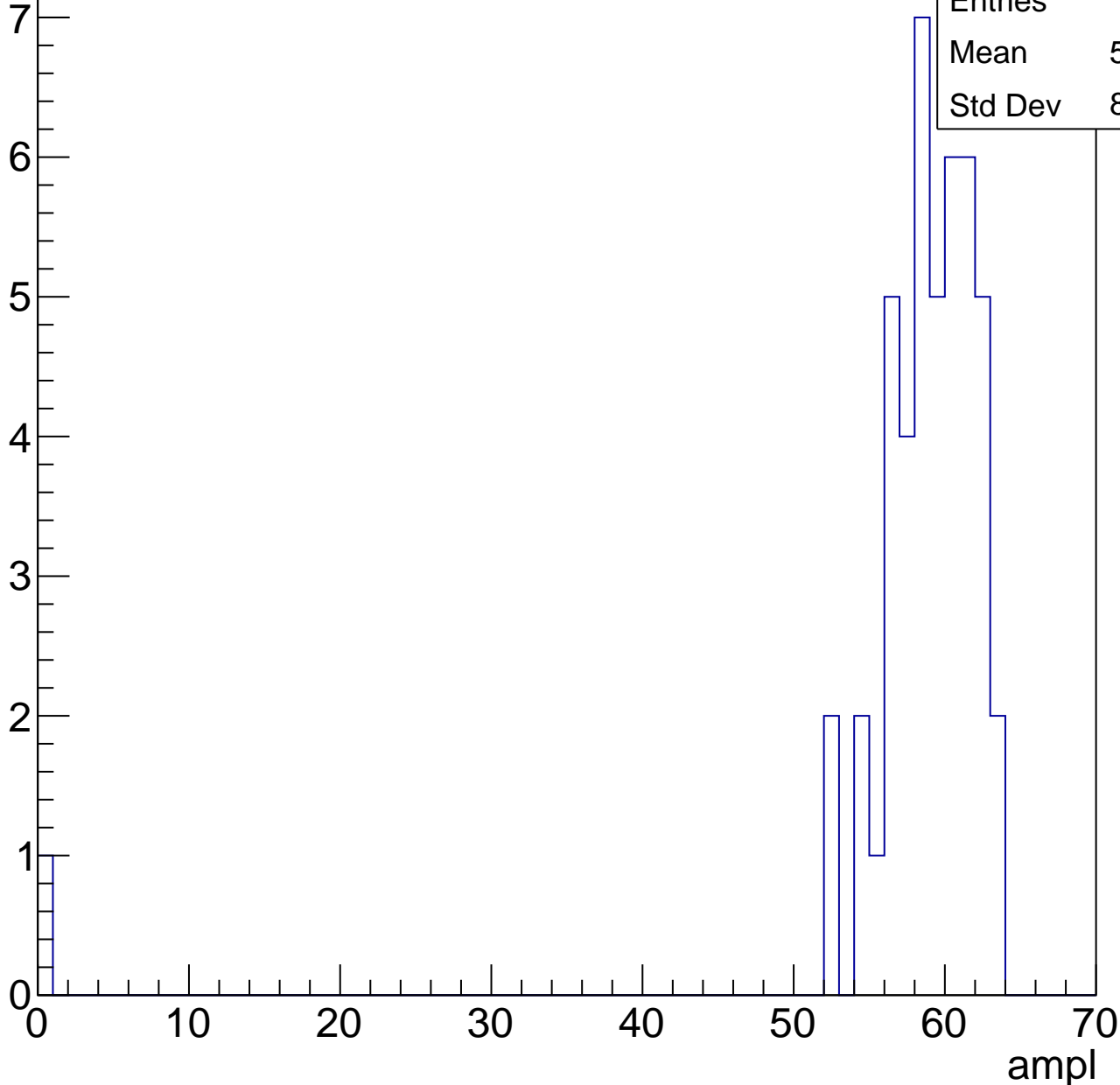


B0L002S, U12-ch23, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	57.35
Std Dev	8.962

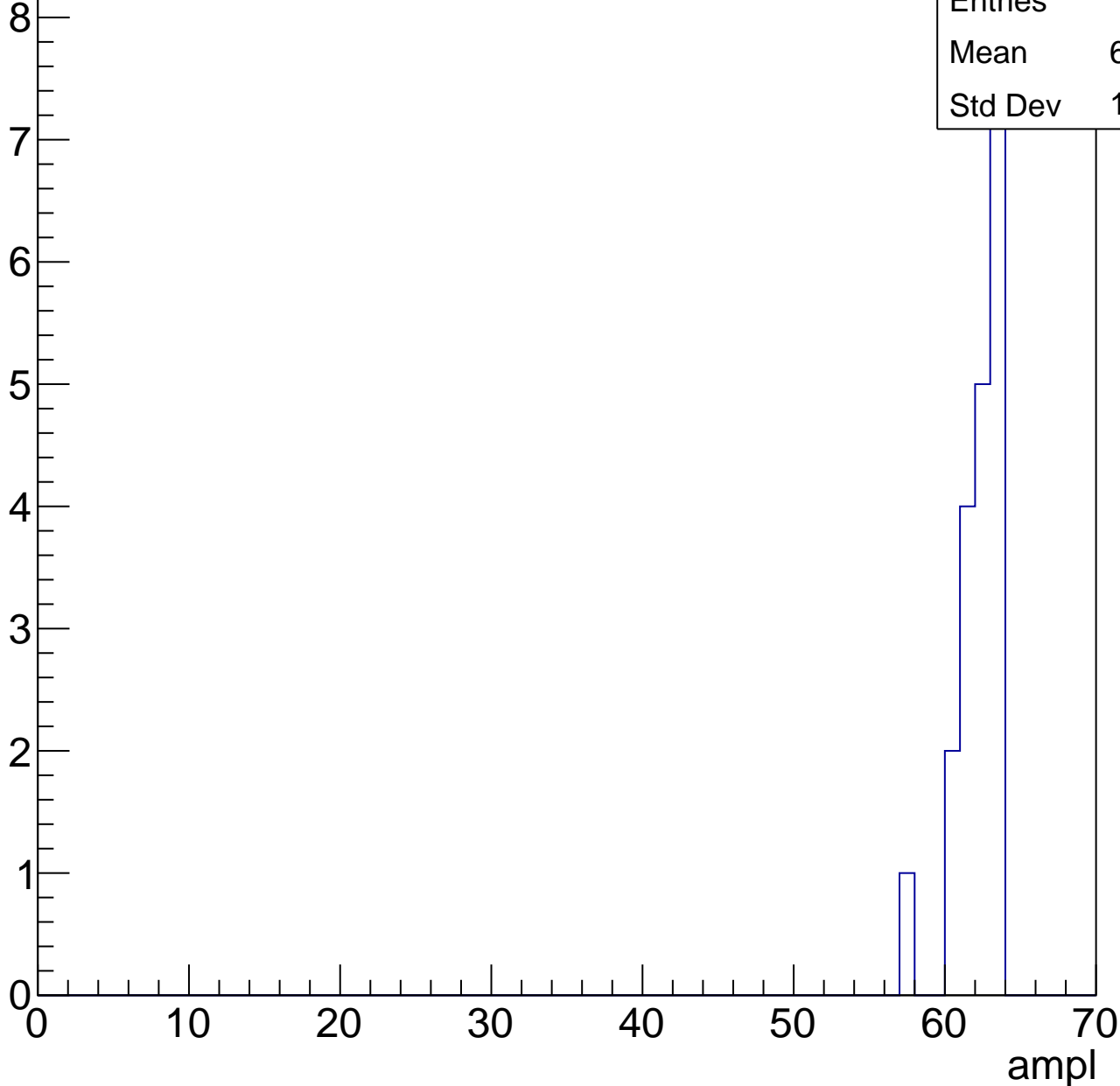


B0L002S, U12-ch23, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	61.75
Std Dev	1.479



B0L002S, U12-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

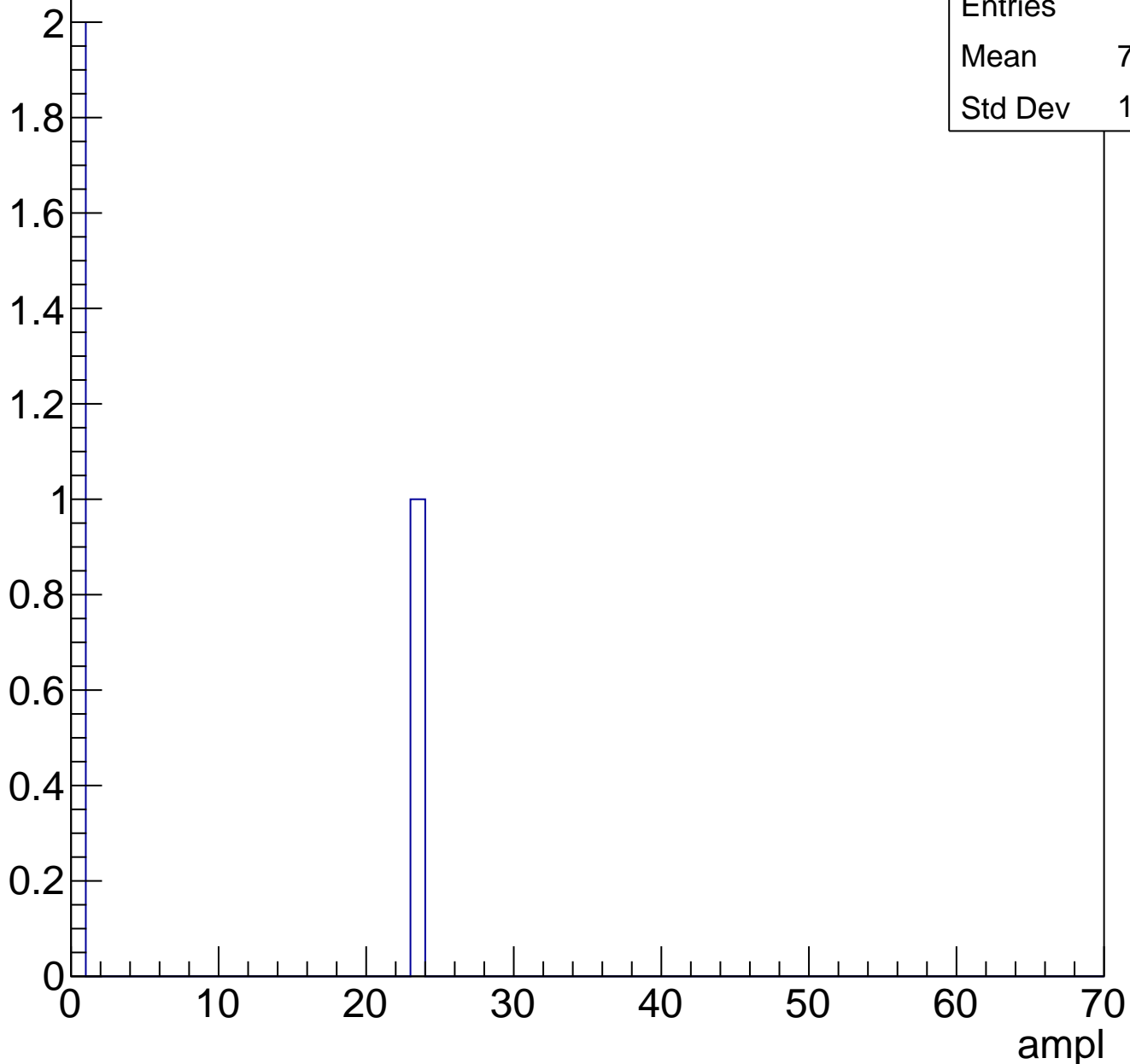


Entries	1
Mean	60
Std Dev	0

B0L002S, U12-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L002S, U12-ch24, adc0

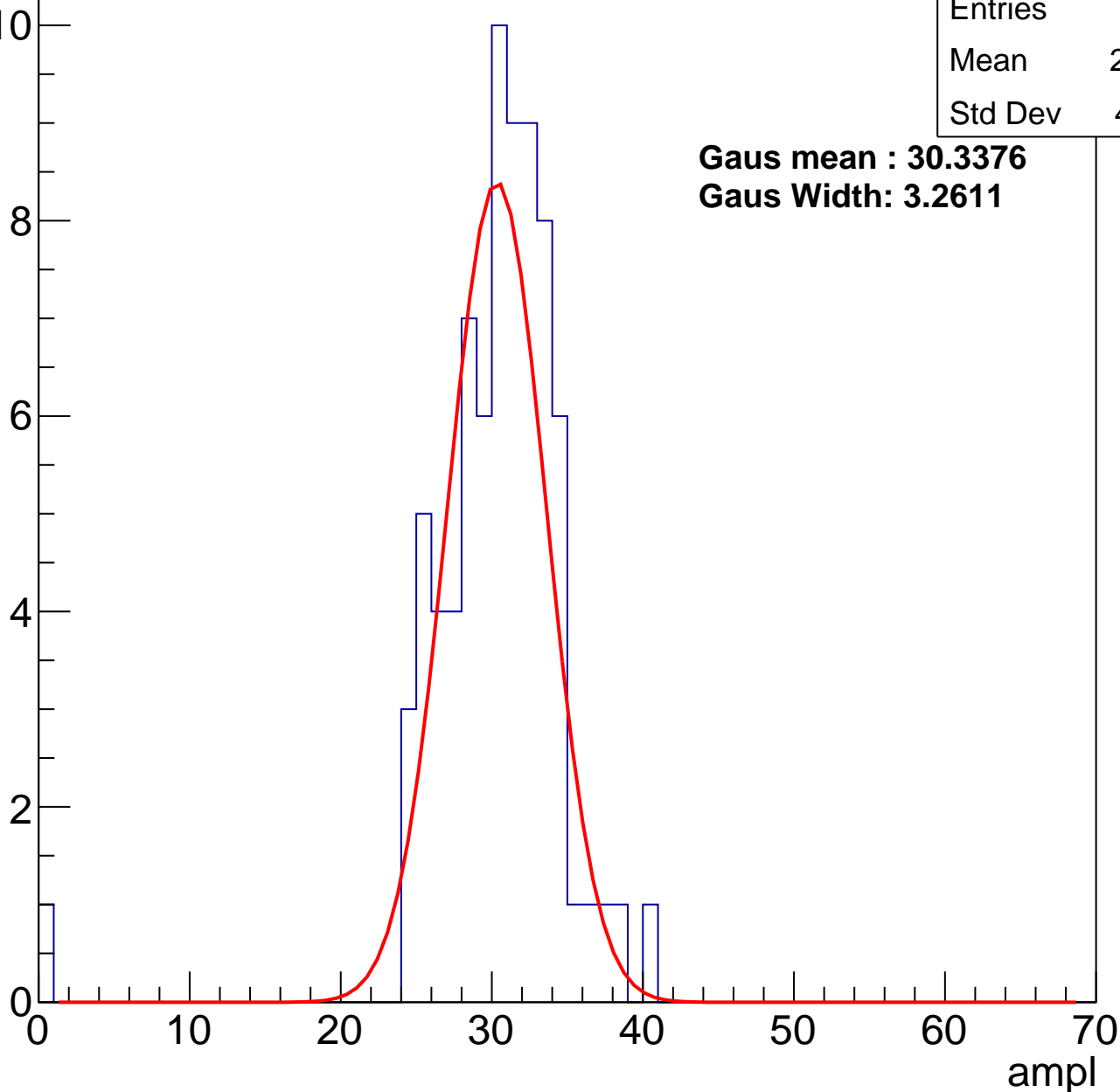
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	29.87
Std Dev	4.771

Gaus mean : 30.3376

Gaus Width: 3.2611



B0L002S, U12-ch24, adc1

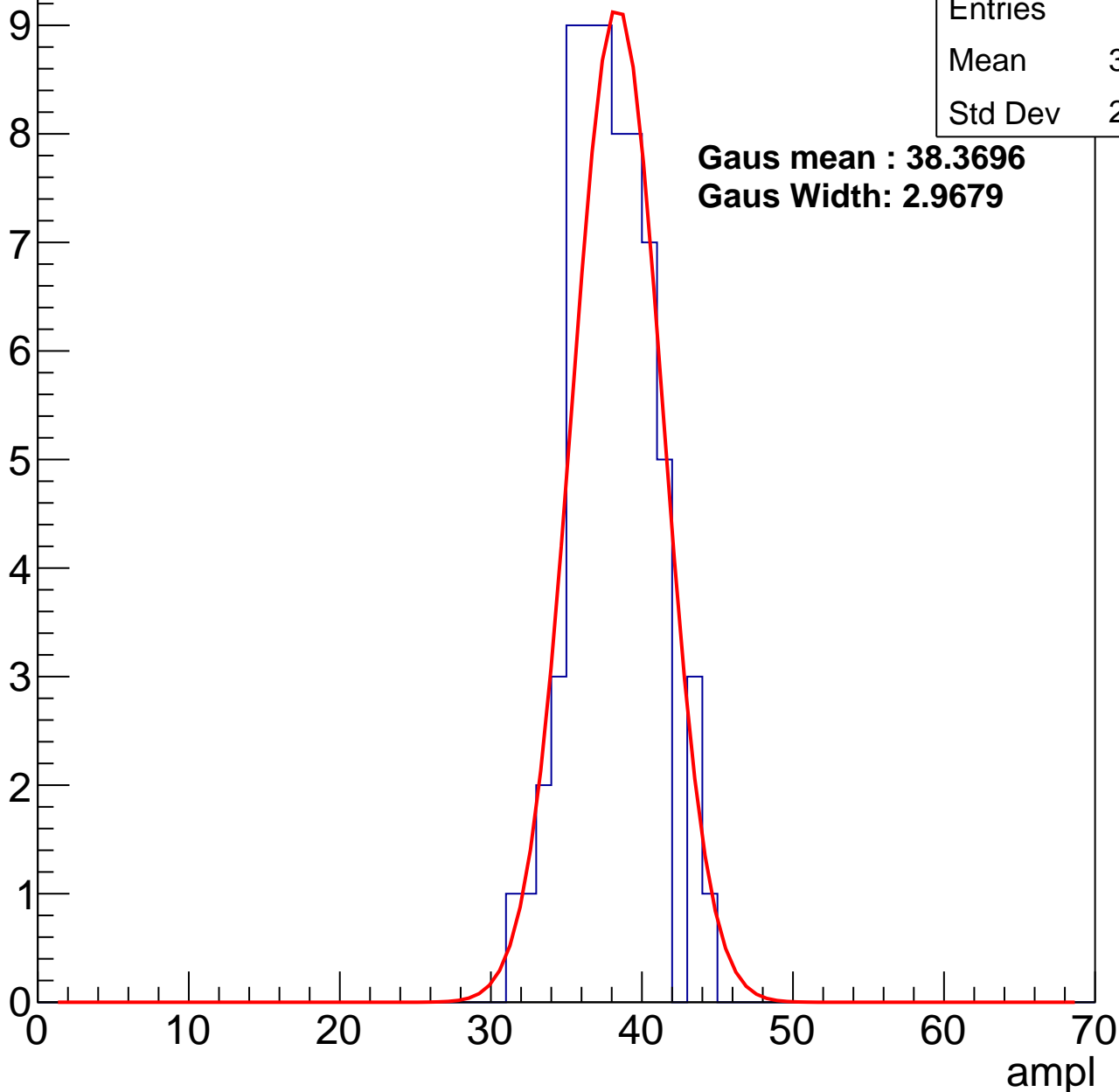
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.53
Std Dev	2.715

Gaus mean : 38.3696

Gaus Width: 2.9679



B0L002S, U12-ch24, adc2

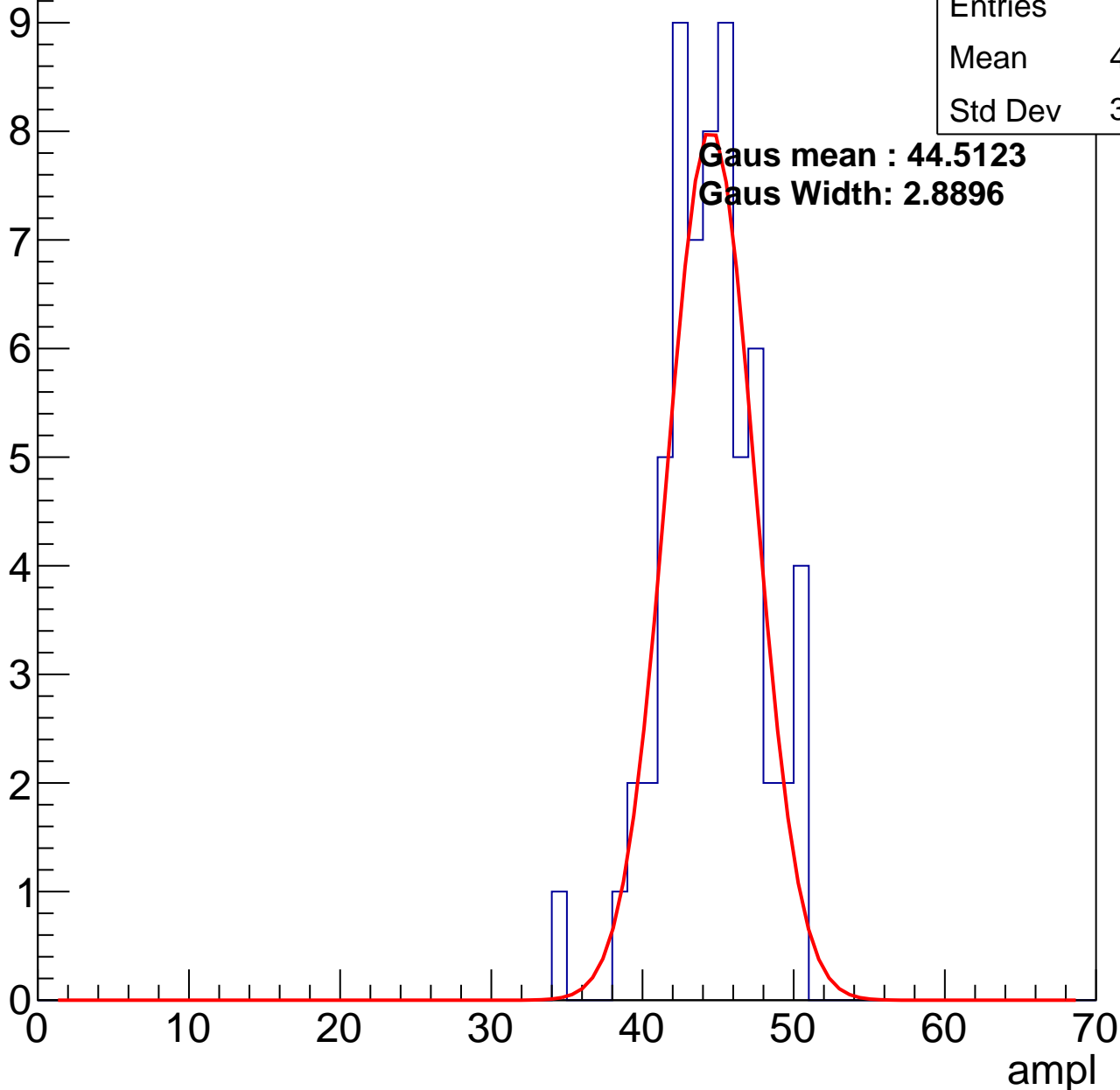
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.08
Std Dev	3.123

Gaus mean : 44.5123

Gaus Width: 2.8896

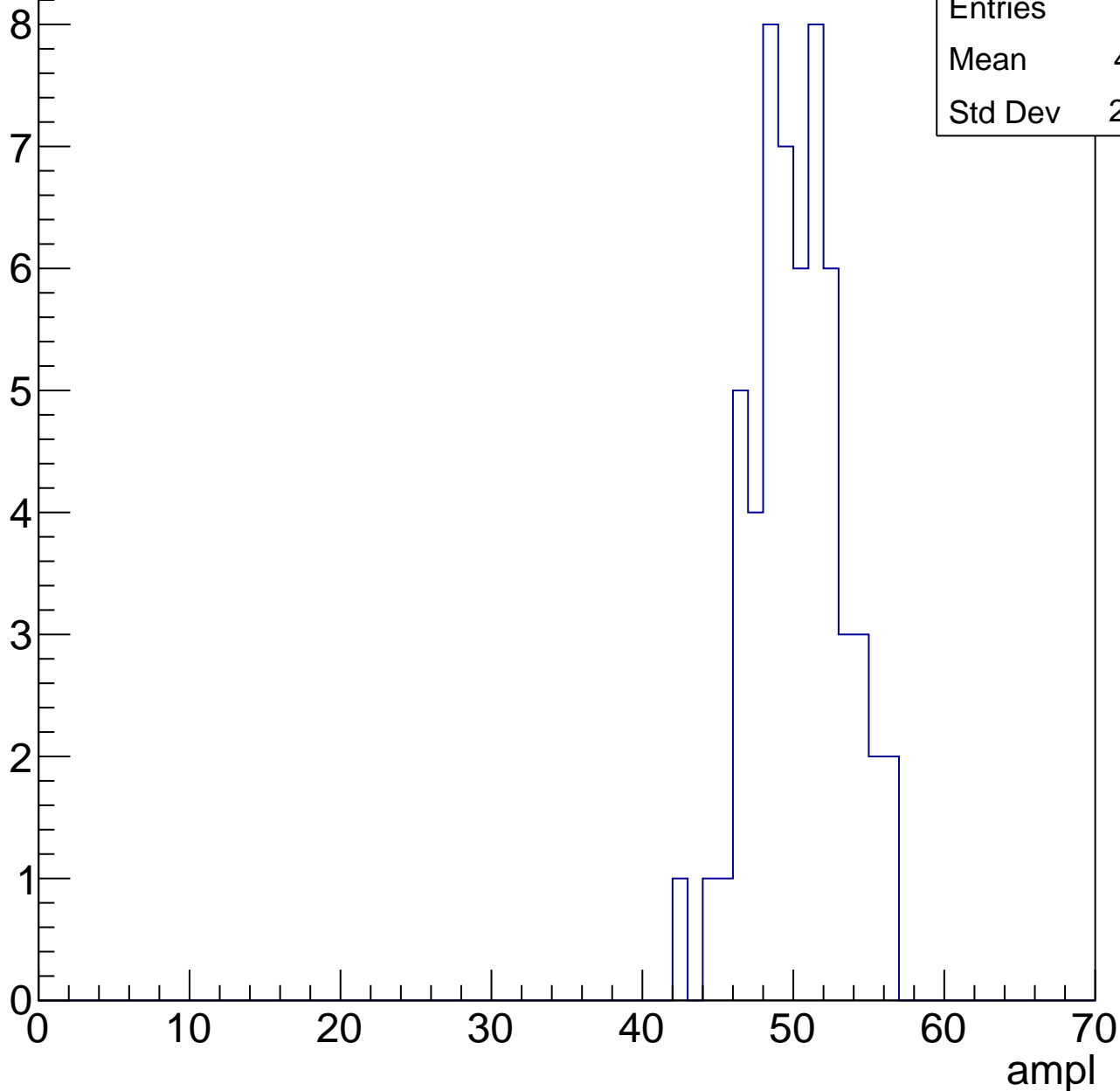


B0L002S, U12-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49.81
Std Dev	2.982

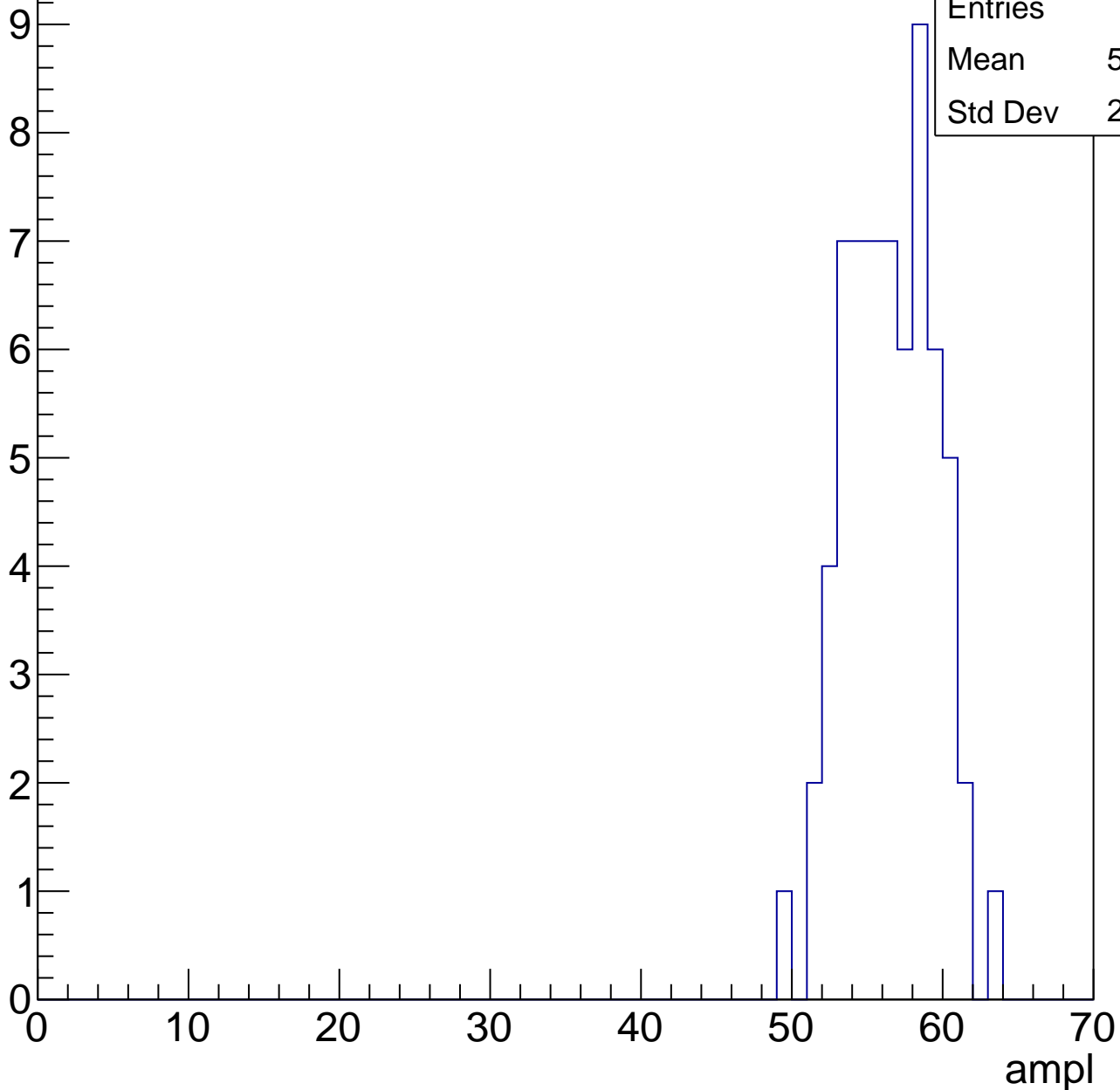


B0L002S, U12-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	56.06
Std Dev	2.893

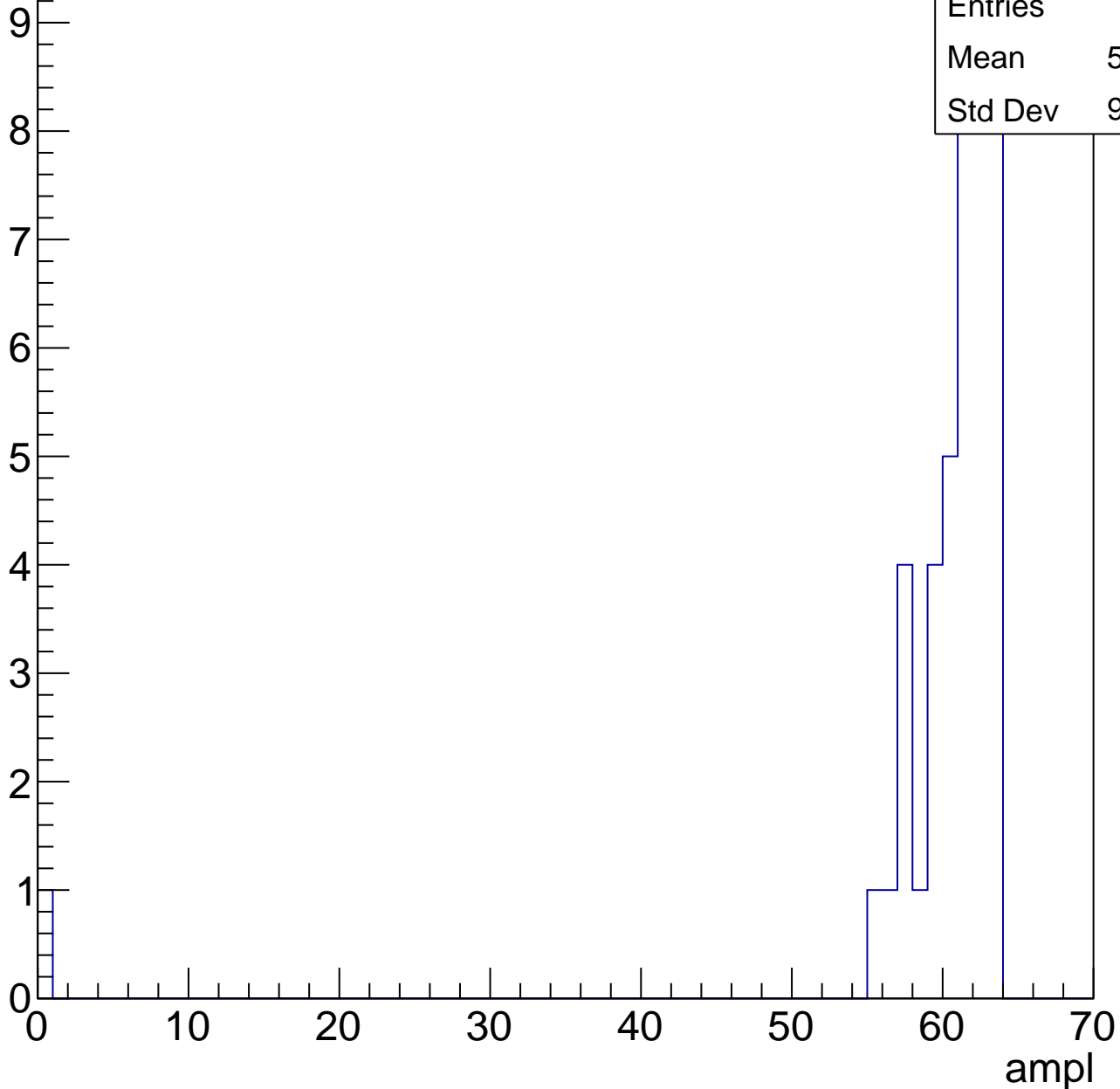


B0L002S, U12-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	59.12
Std Dev	9.472



B0L002S, U12-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U12-ch25, adc0

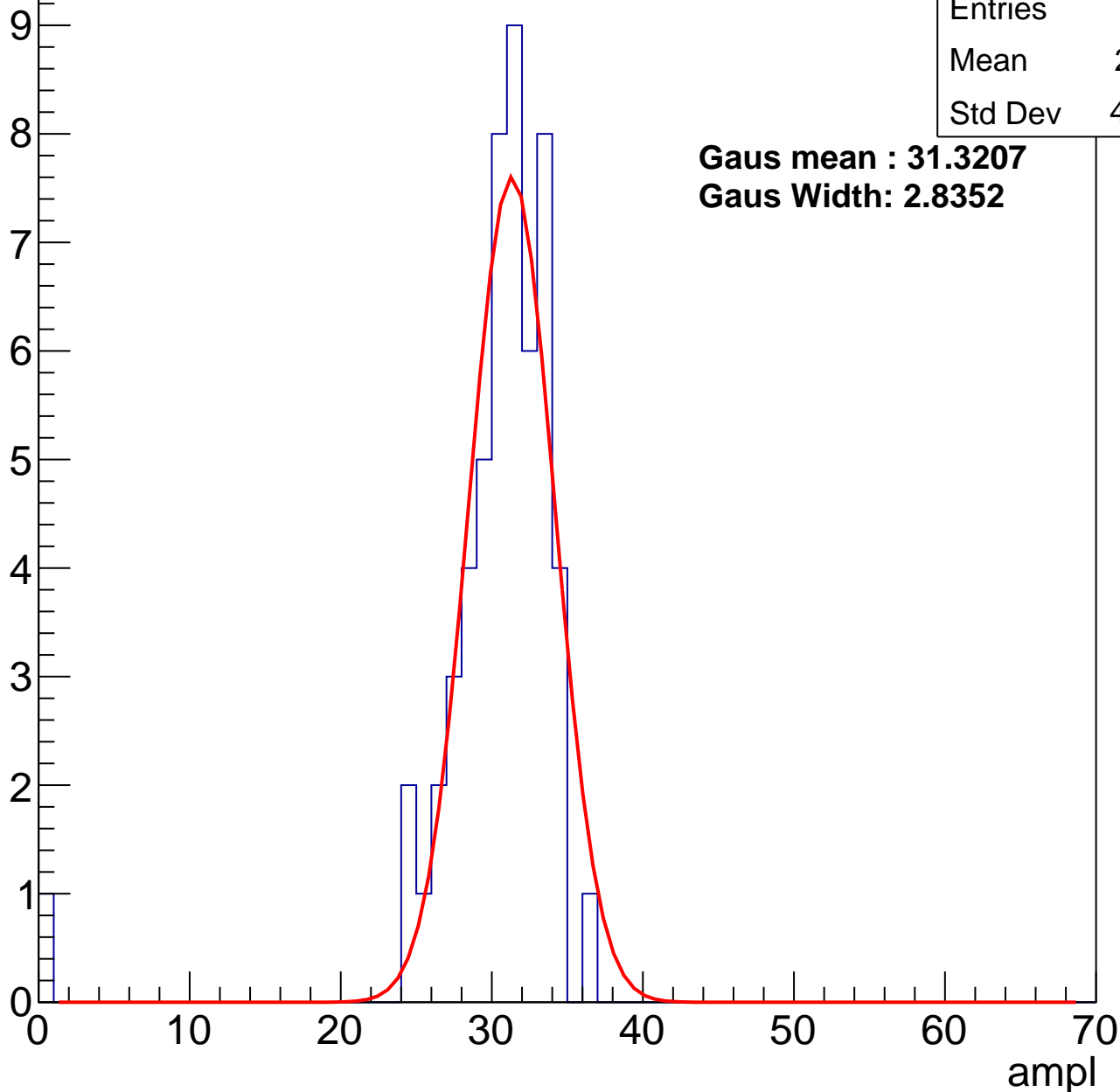
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	29.81
Std Dev	4.869

Gaus mean : 31.3207

Gaus Width: 2.8352

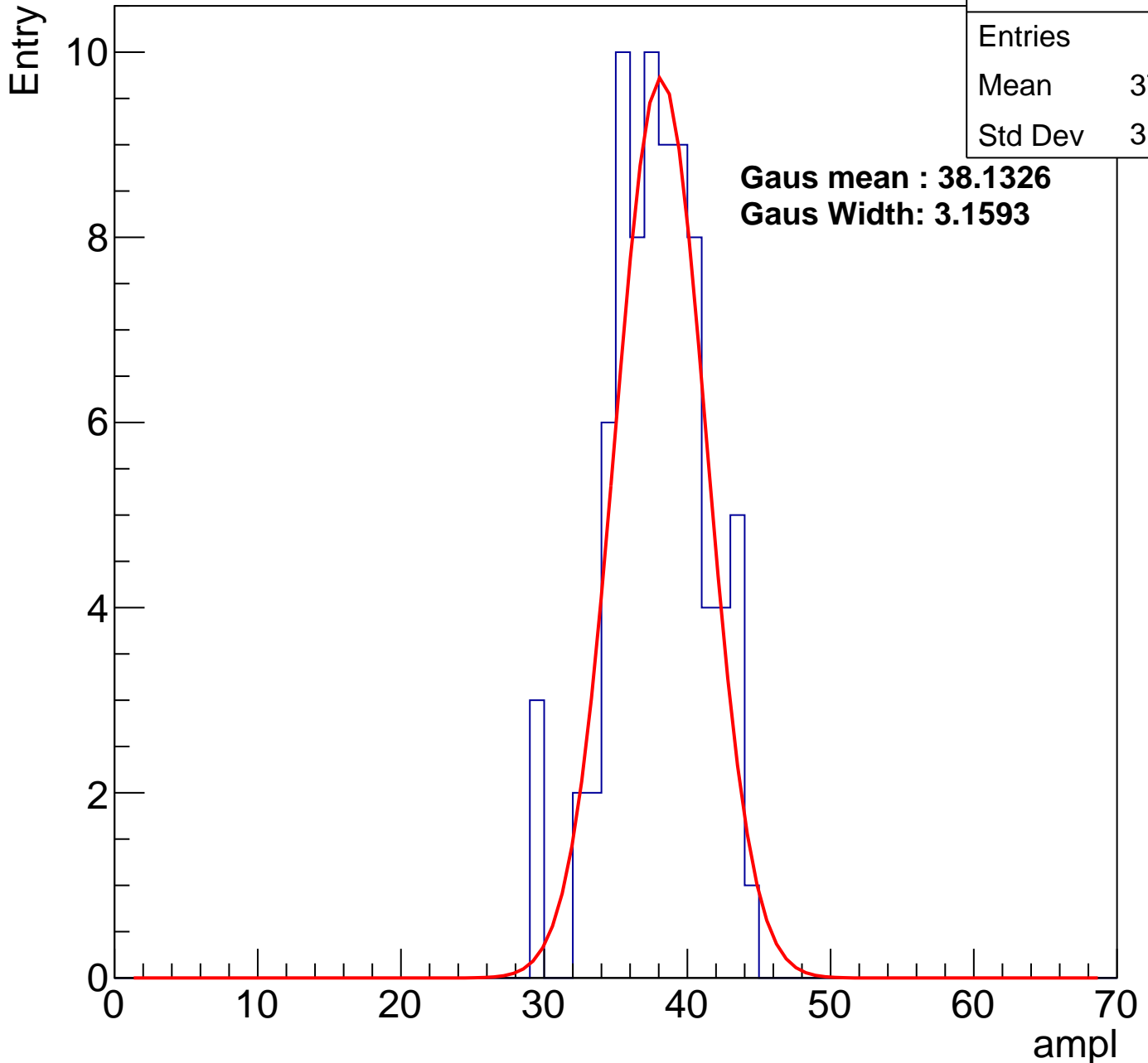


B0L002S, U12-ch25, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	37.44
Std Dev	3.277

Gaus mean : 38.1326
Gaus Width: 3.1593



B0L002S, U12-ch25, adc2

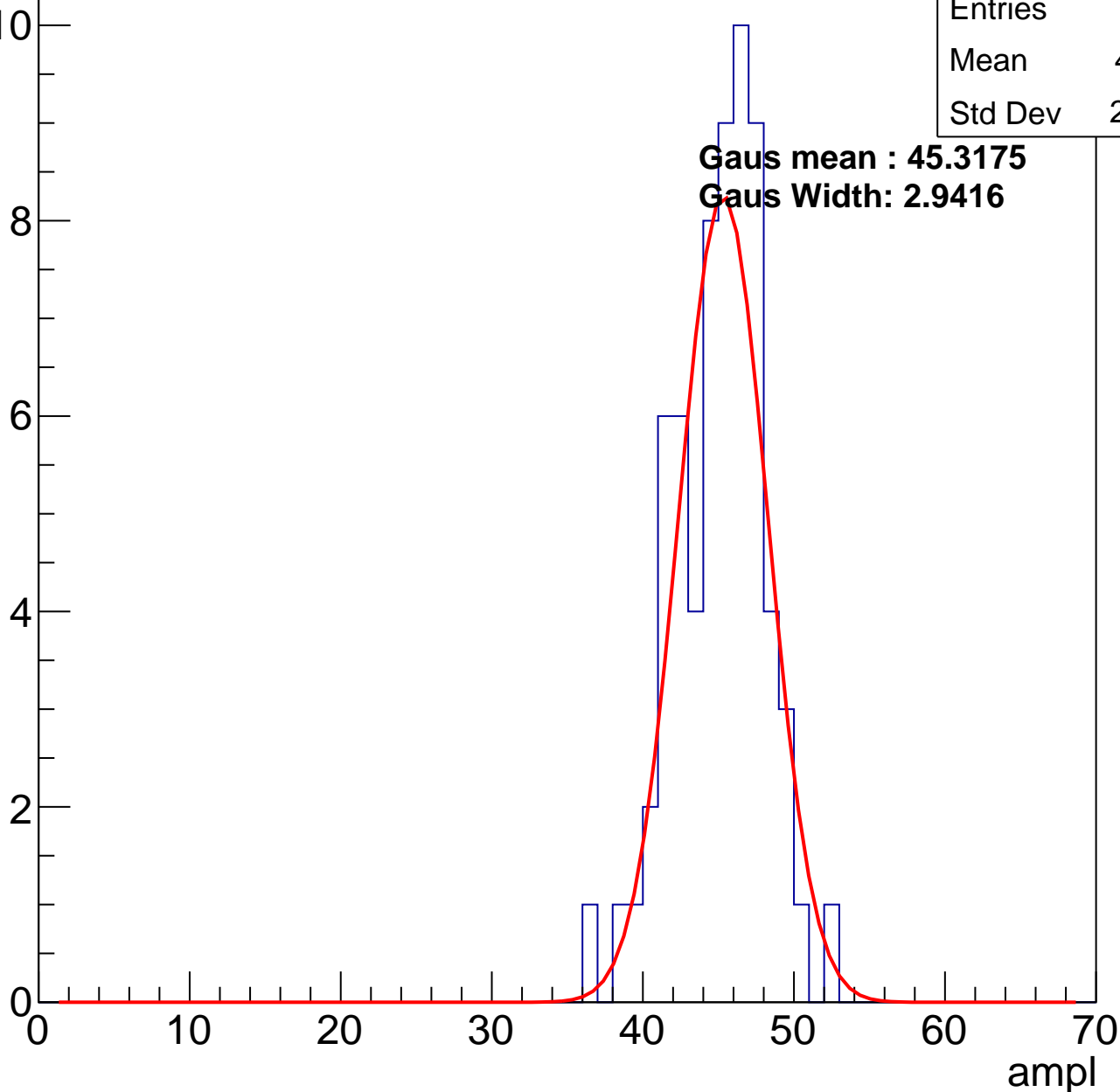
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.61
Std Dev	2.974

Gaus mean : 45.3175

Gaus Width: 2.9416

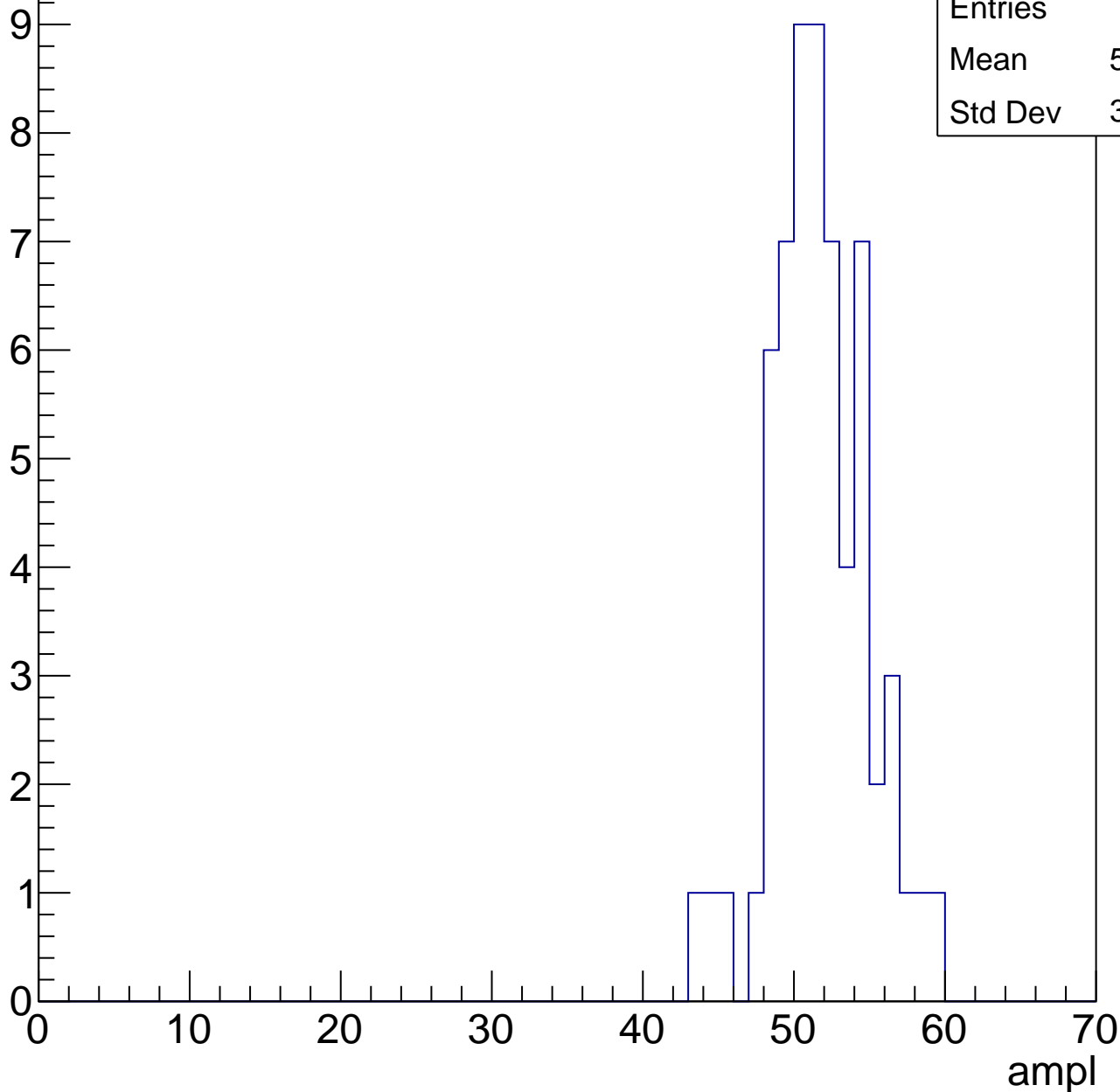


B0L002S, U12-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	51.23
Std Dev	3.123

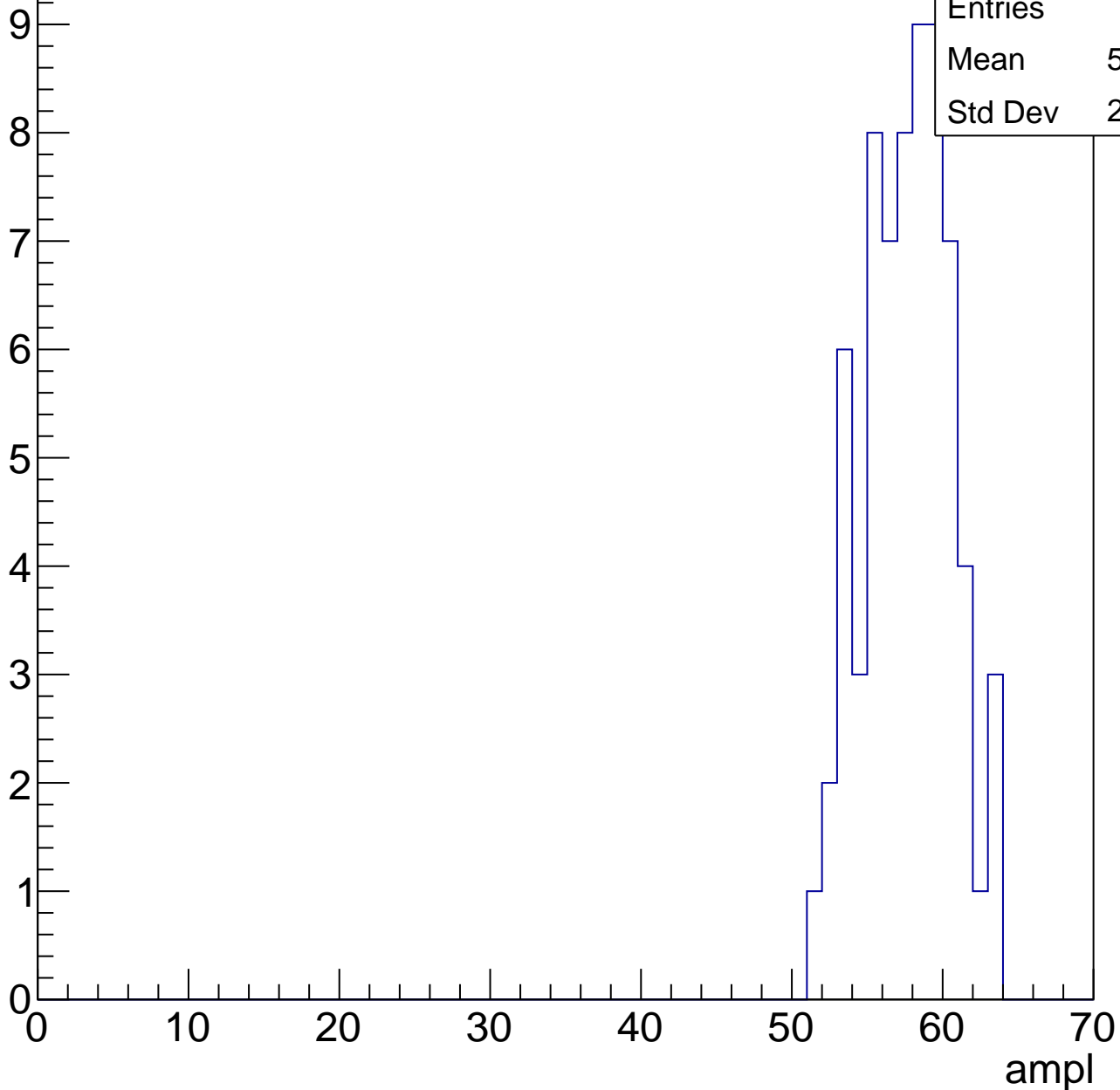


B0L002S, U12-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	57.22
Std Dev	2.843

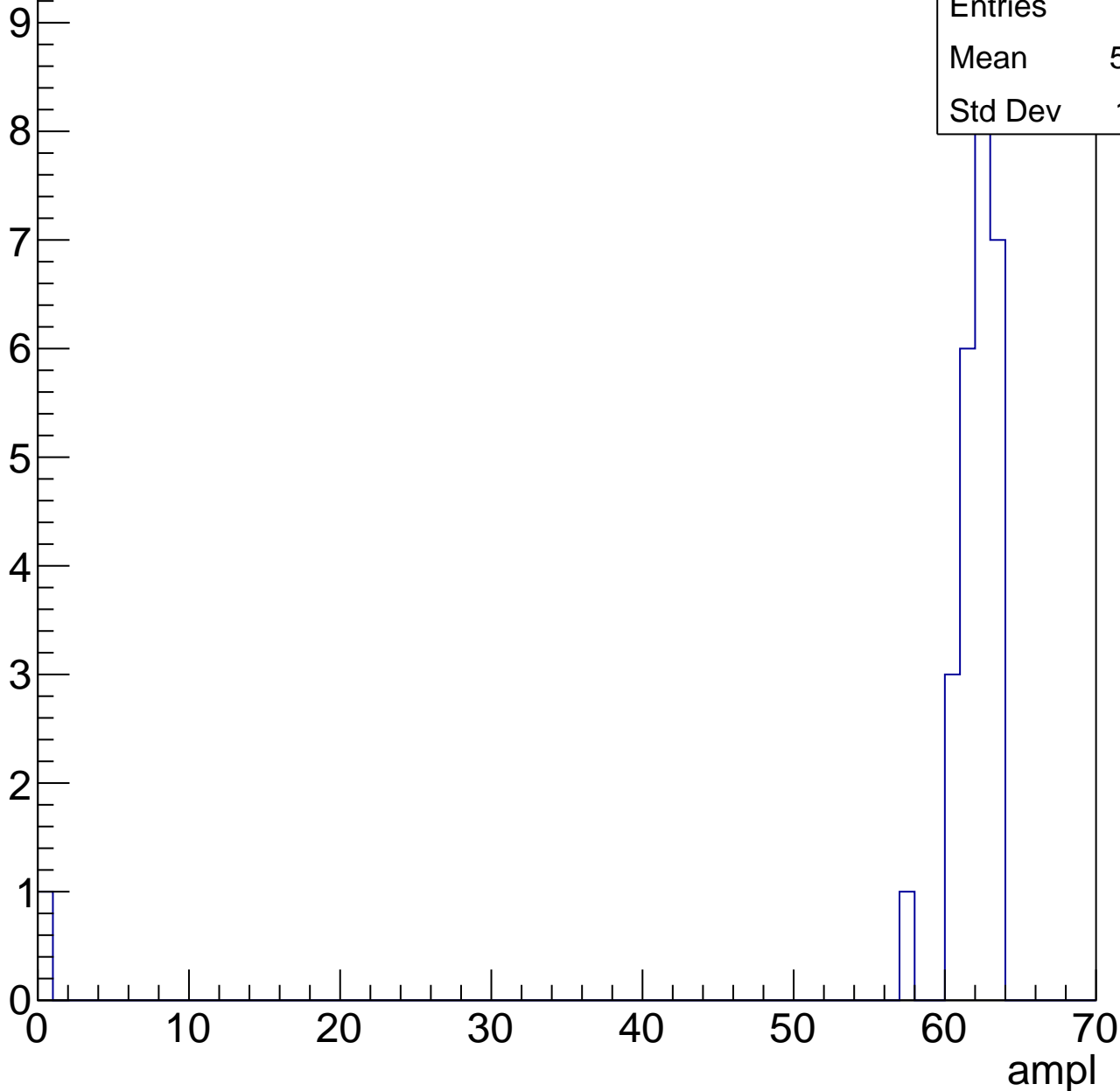


B0L002S, U12-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	59.33
Std Dev	11.71



B0L002S, U12-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch25, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U12-ch26, adc0

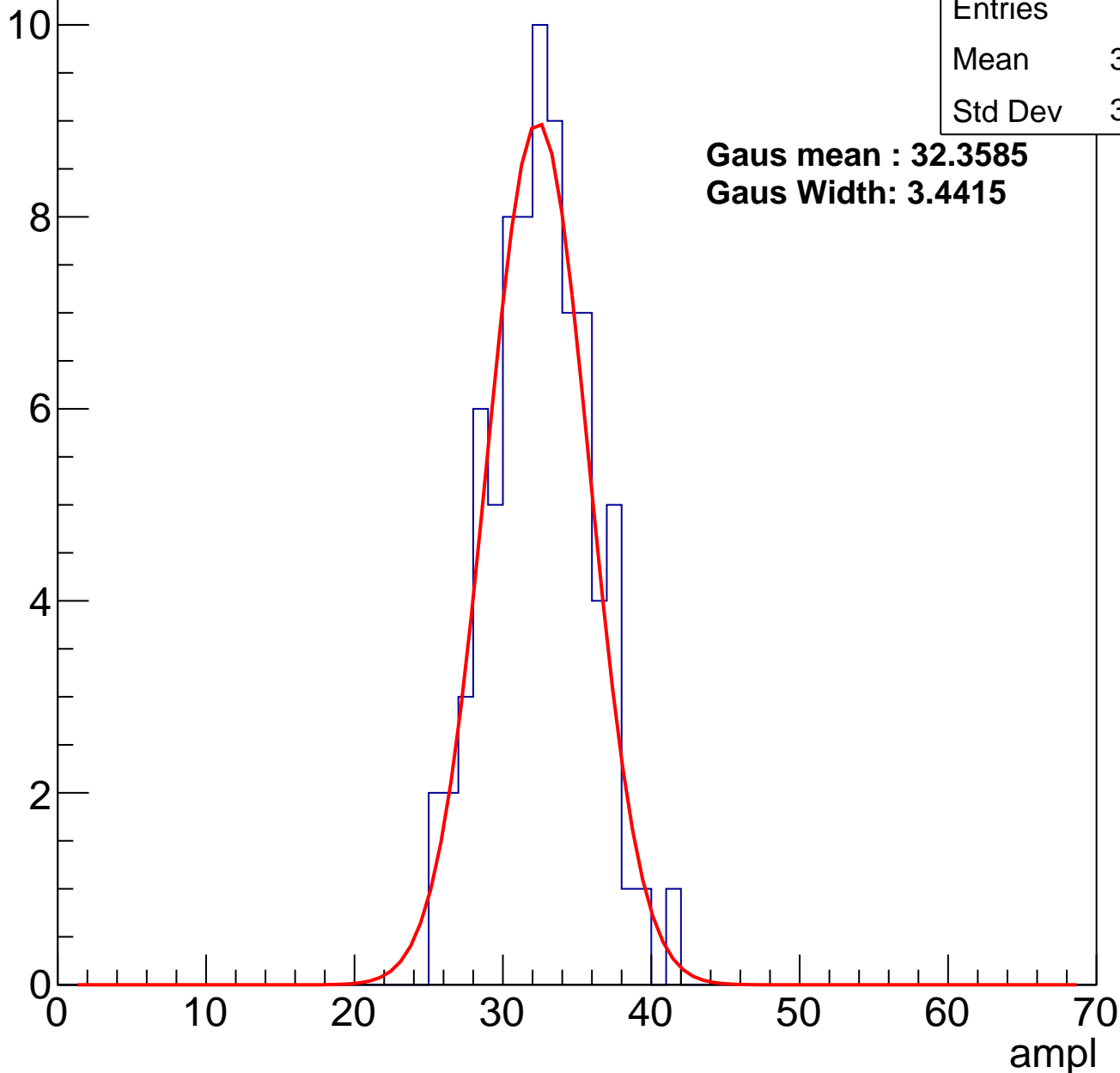
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	32.04
Std Dev	3.339

Gaus mean : 32.3585

Gaus Width: 3.4415

Entry

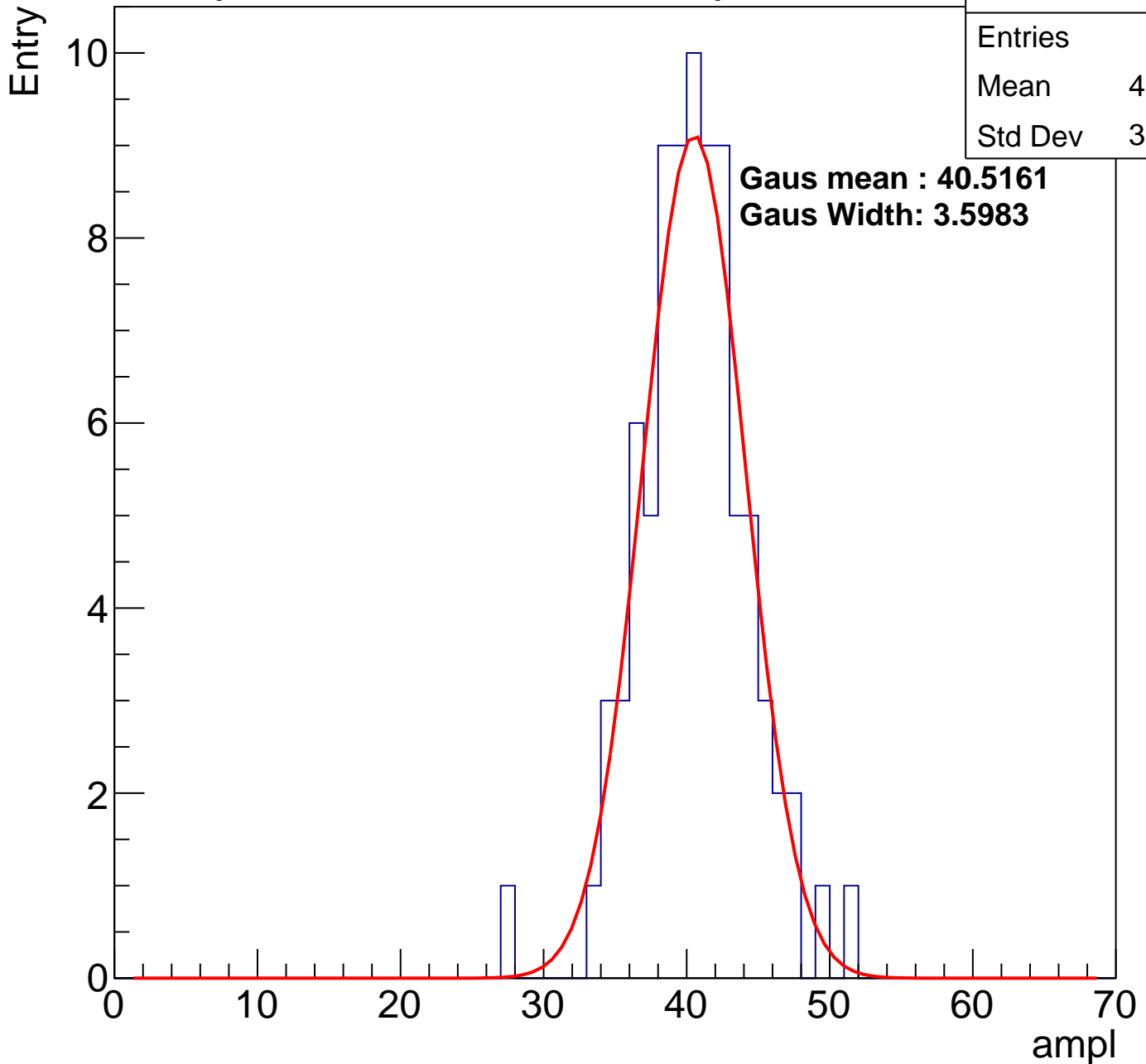


B0L002S, U12-ch26, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	40.05
Std Dev	3.786

Gaus mean : 40.5161
Gaus Width: 3.5983



B0L002S, U12-ch26, adc2

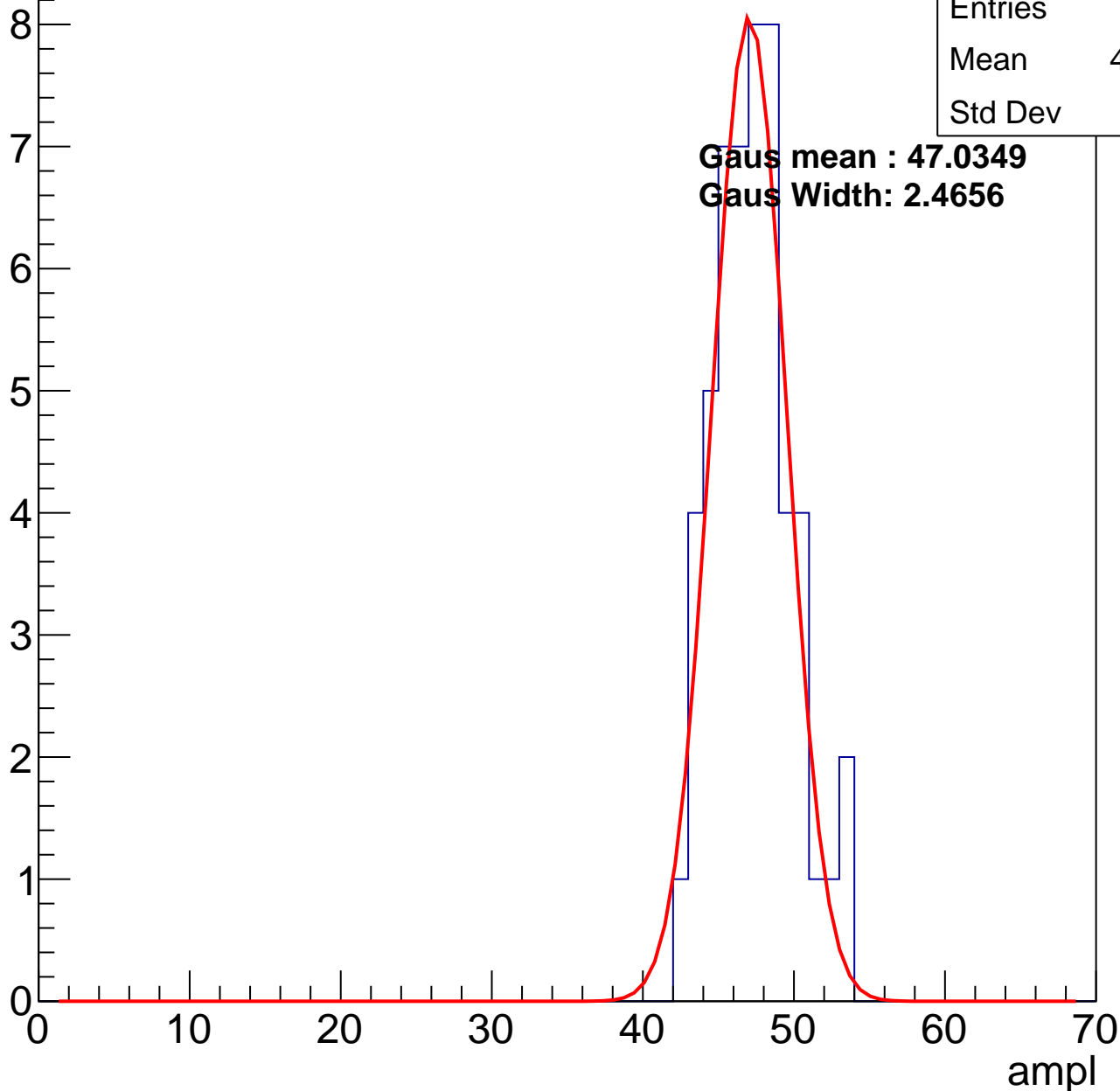
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	46.85
Std Dev	2.56

Gaus mean : 47.0349

Gaus Width: 2.4656

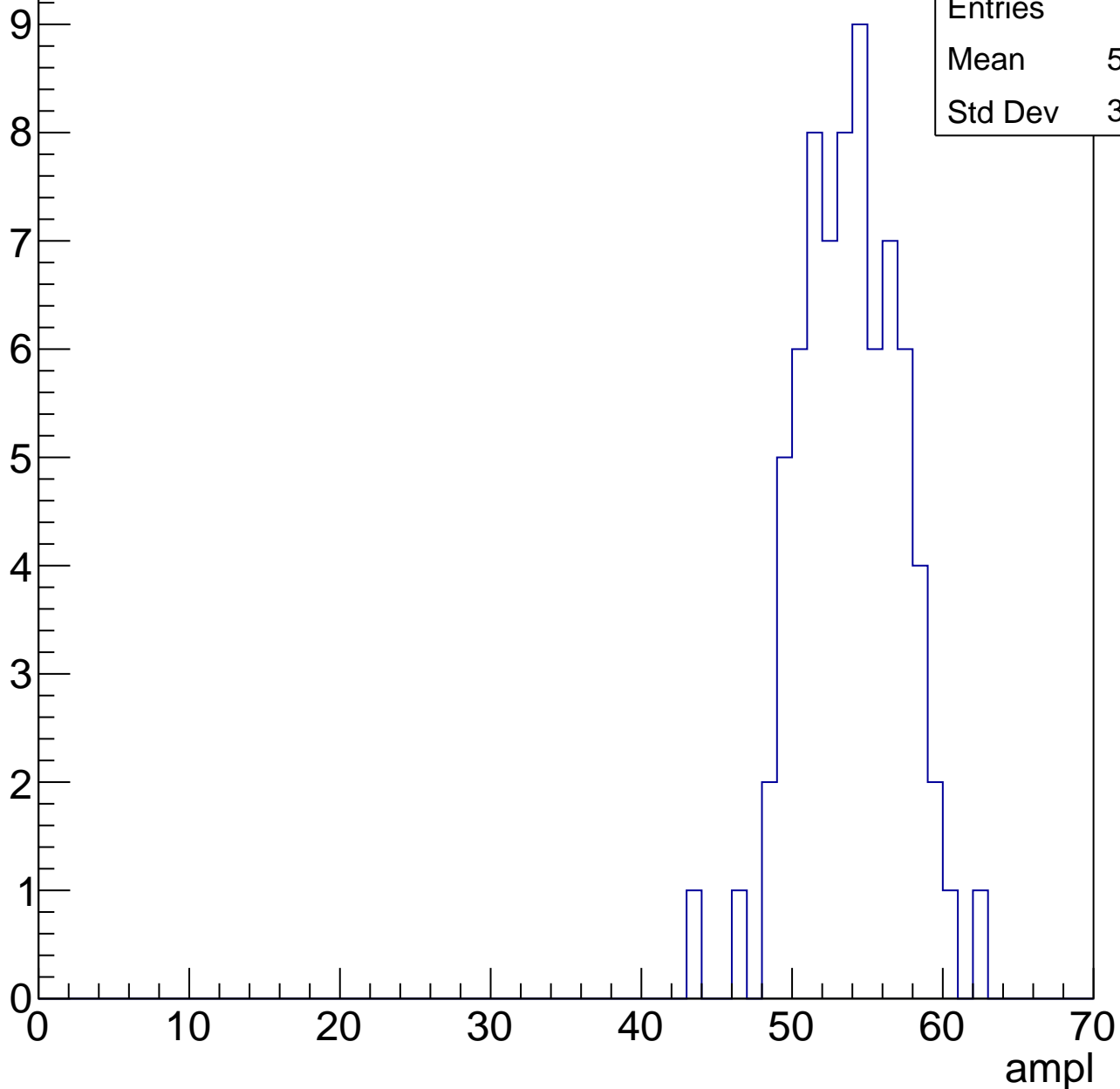


B0L002S, U12-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

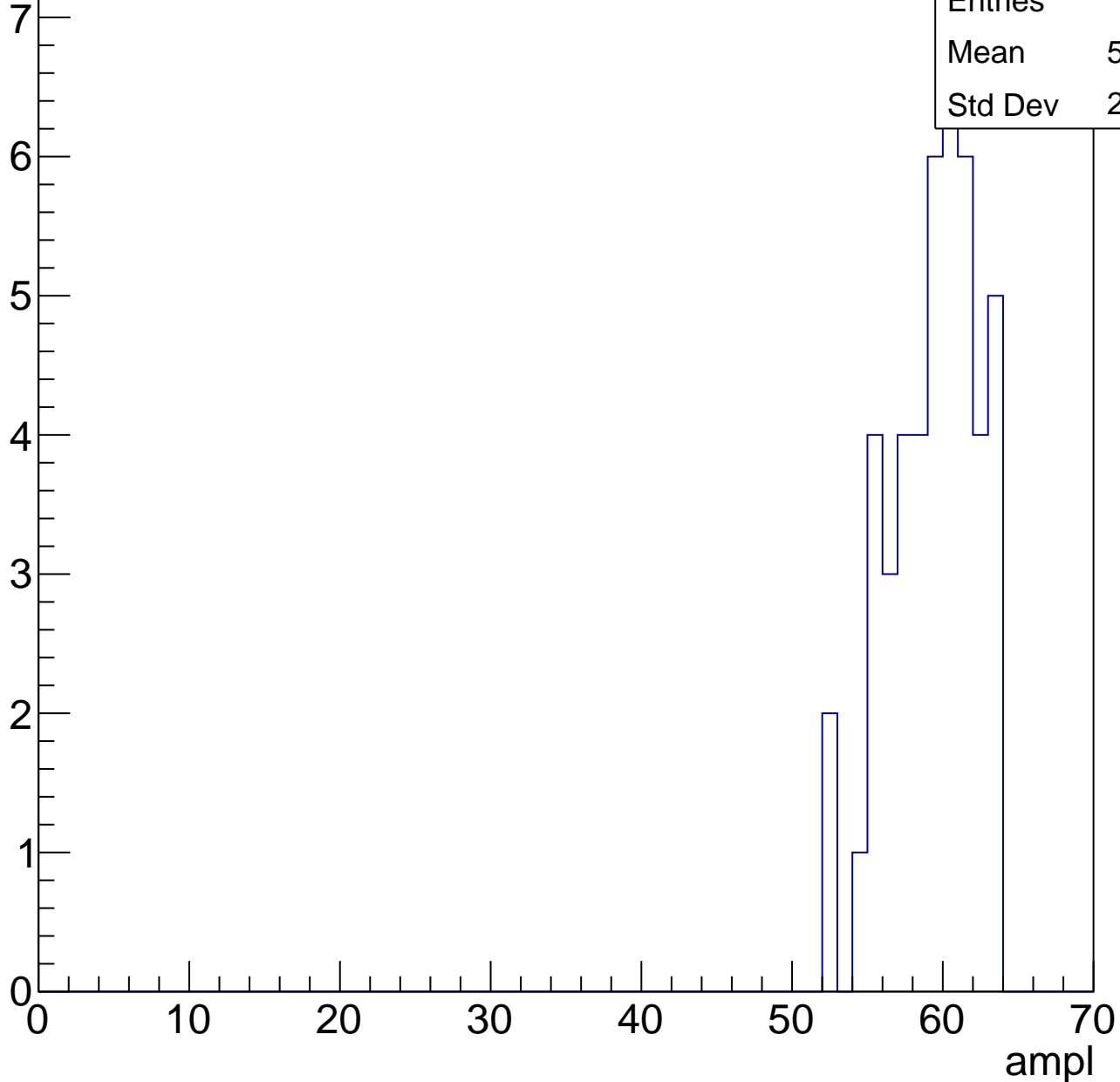
Entries	74
Mean	53.35
Std Dev	3.403



B0L002S, U12-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

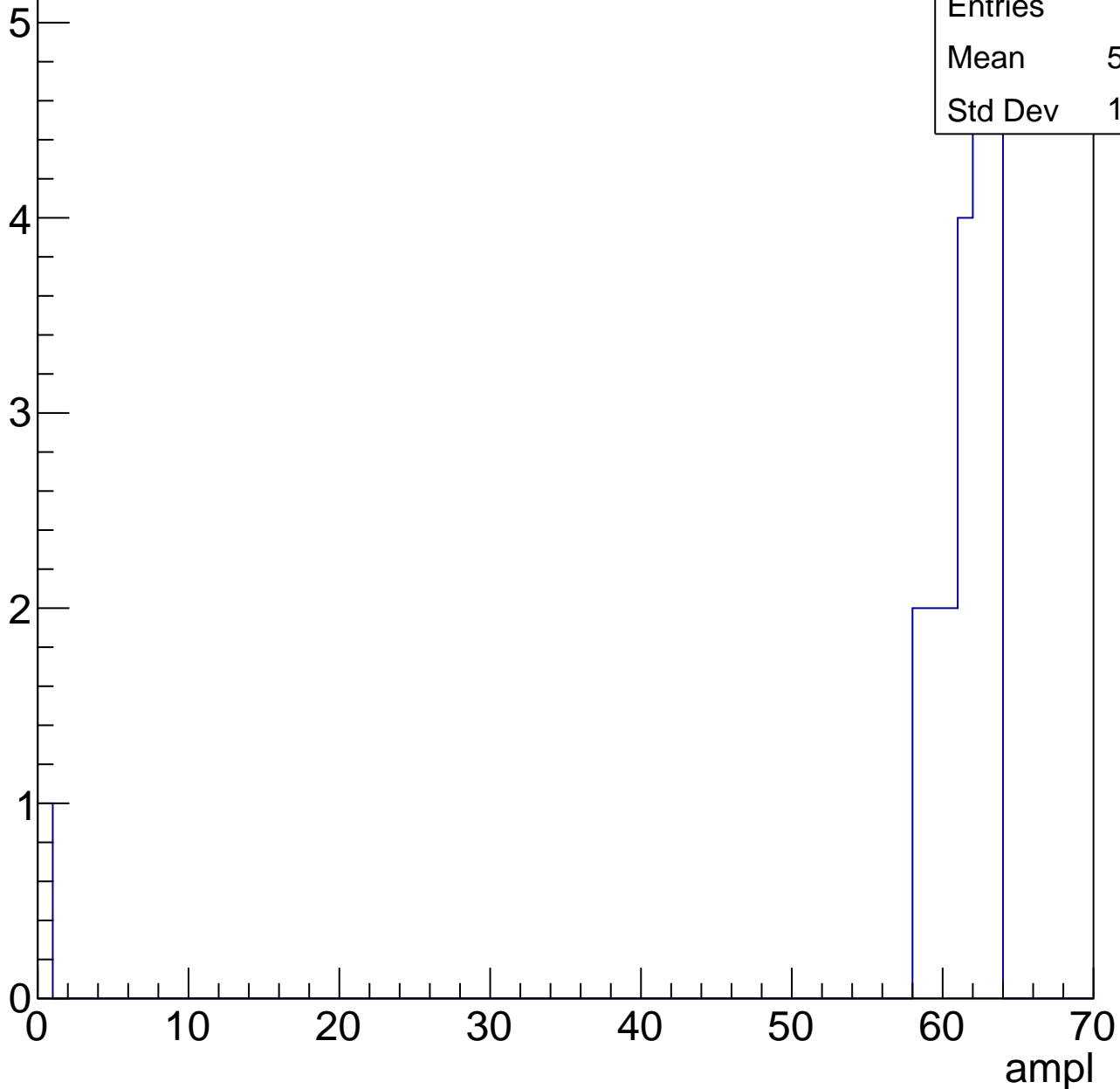


B0L002S, U12-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.24
Std Dev	13.12



B0L002S, U12-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B0L002S, U12-ch27, adc0

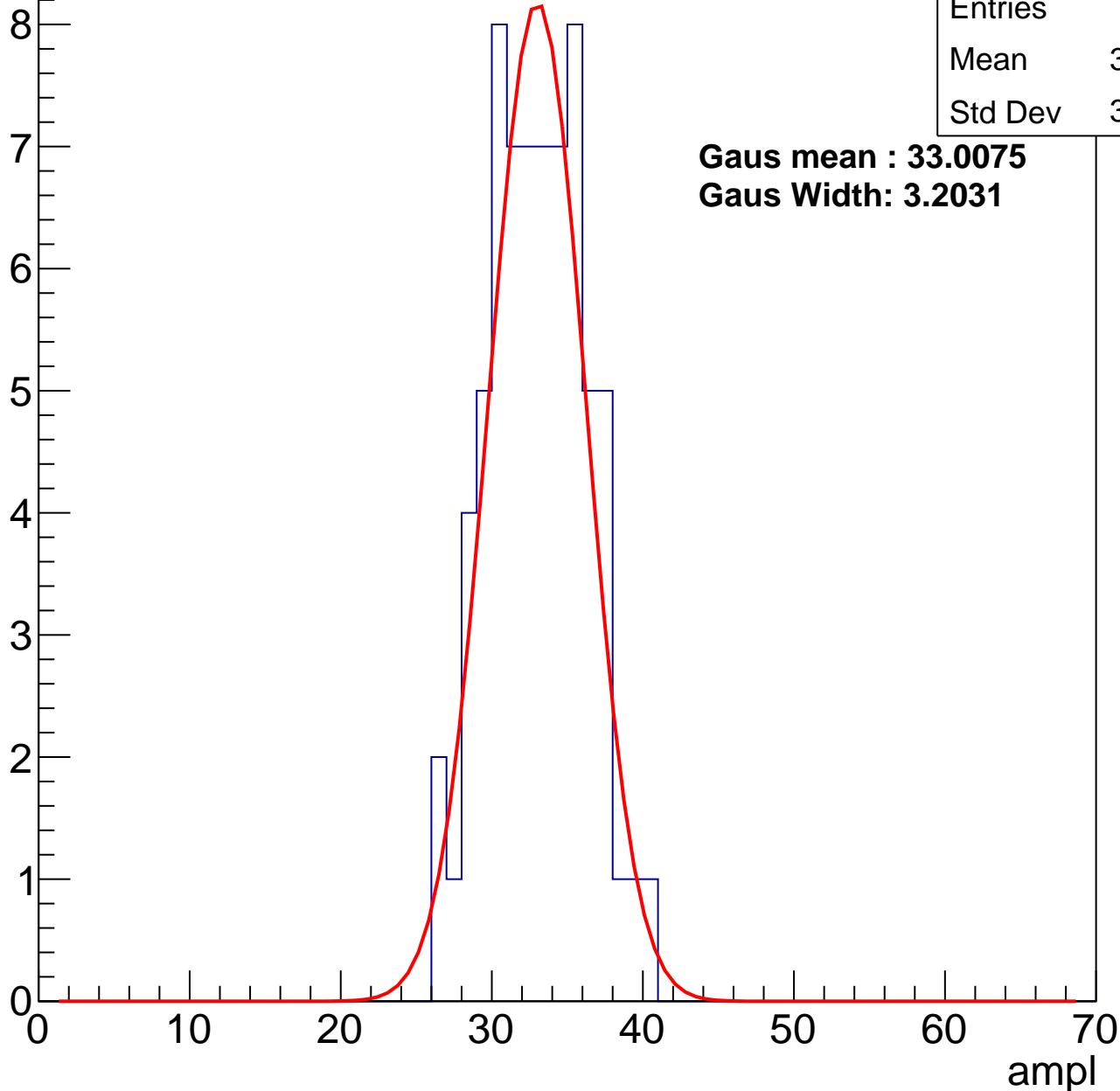
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	32.58
Std Dev	3.146

Gaus mean : 33.0075

Gaus Width: 3.2031



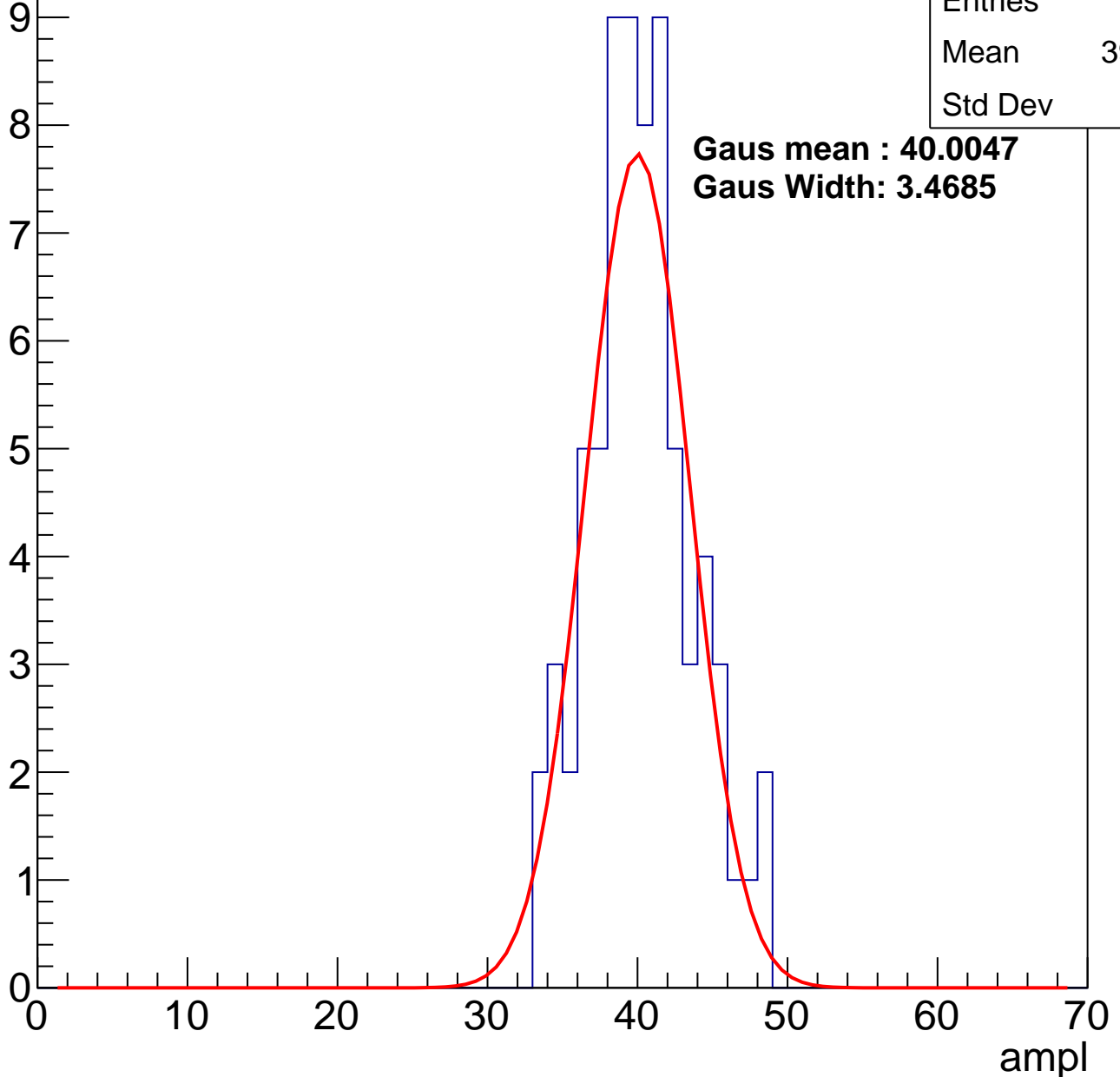
B0L002S, U12-ch27, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

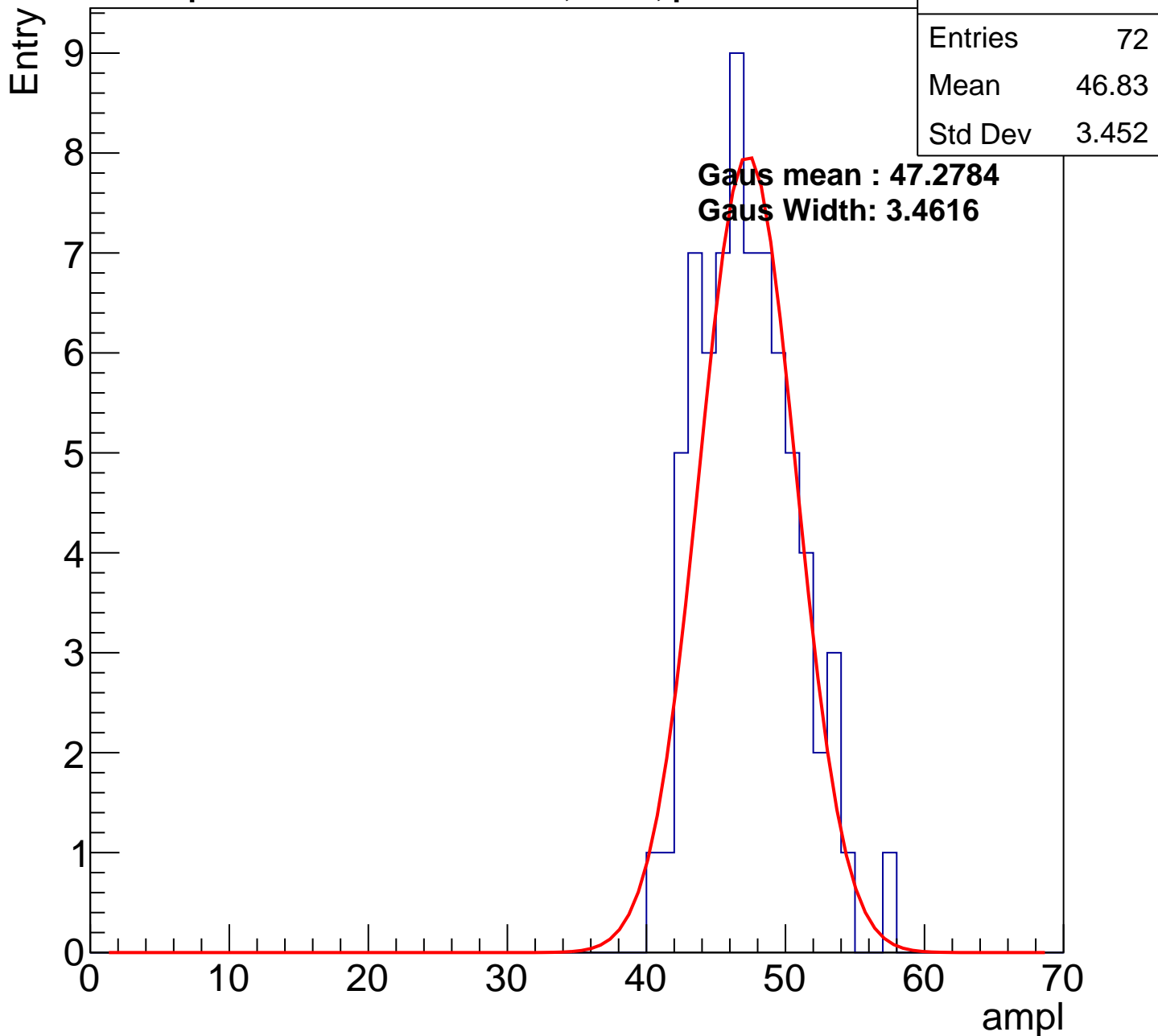
Entries	71
Mean	39.77
Std Dev	3.42

Gaus mean : 40.0047
Gaus Width: 3.4685



B0L002S, U12-ch27, adc2

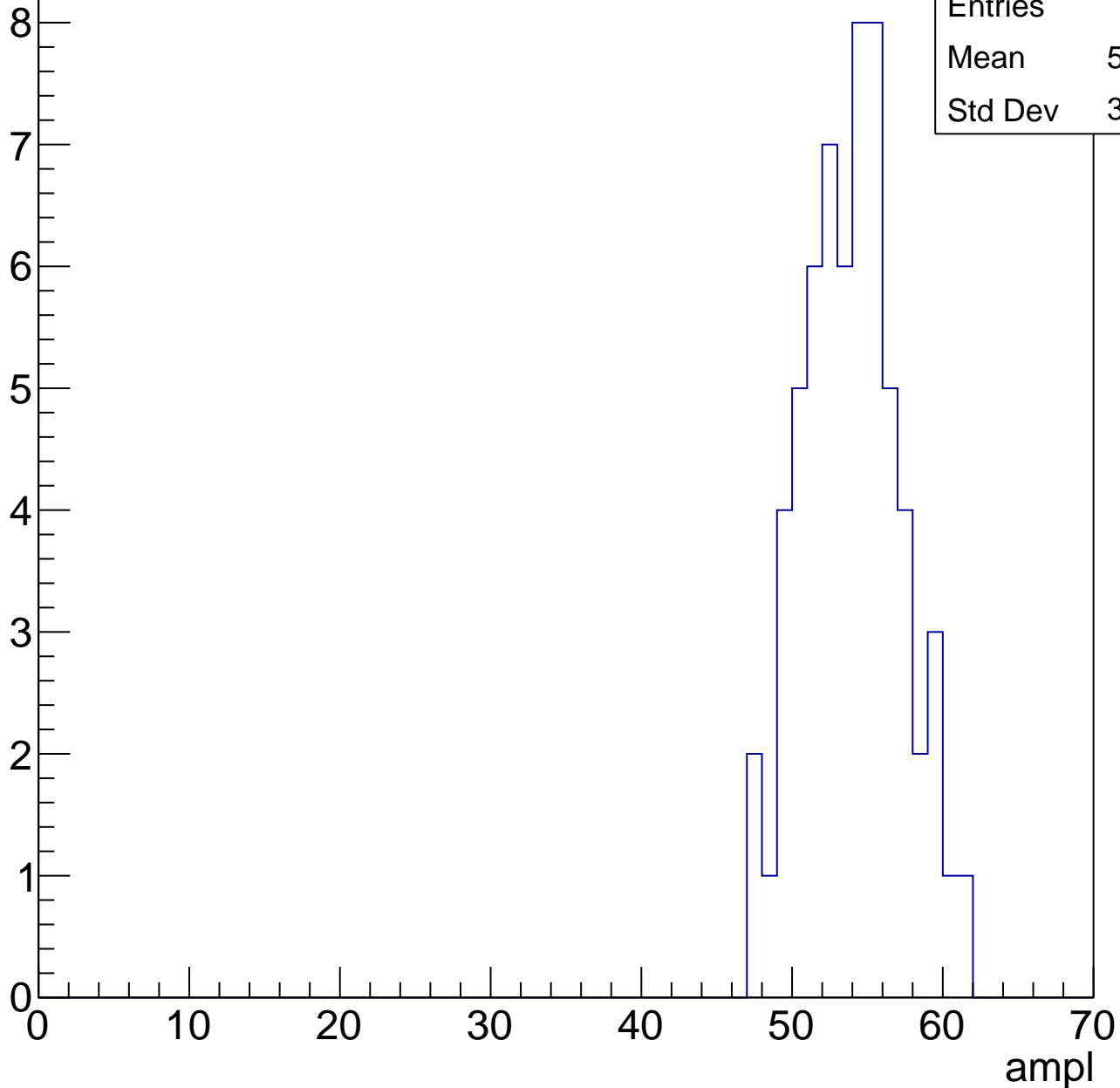
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U12-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

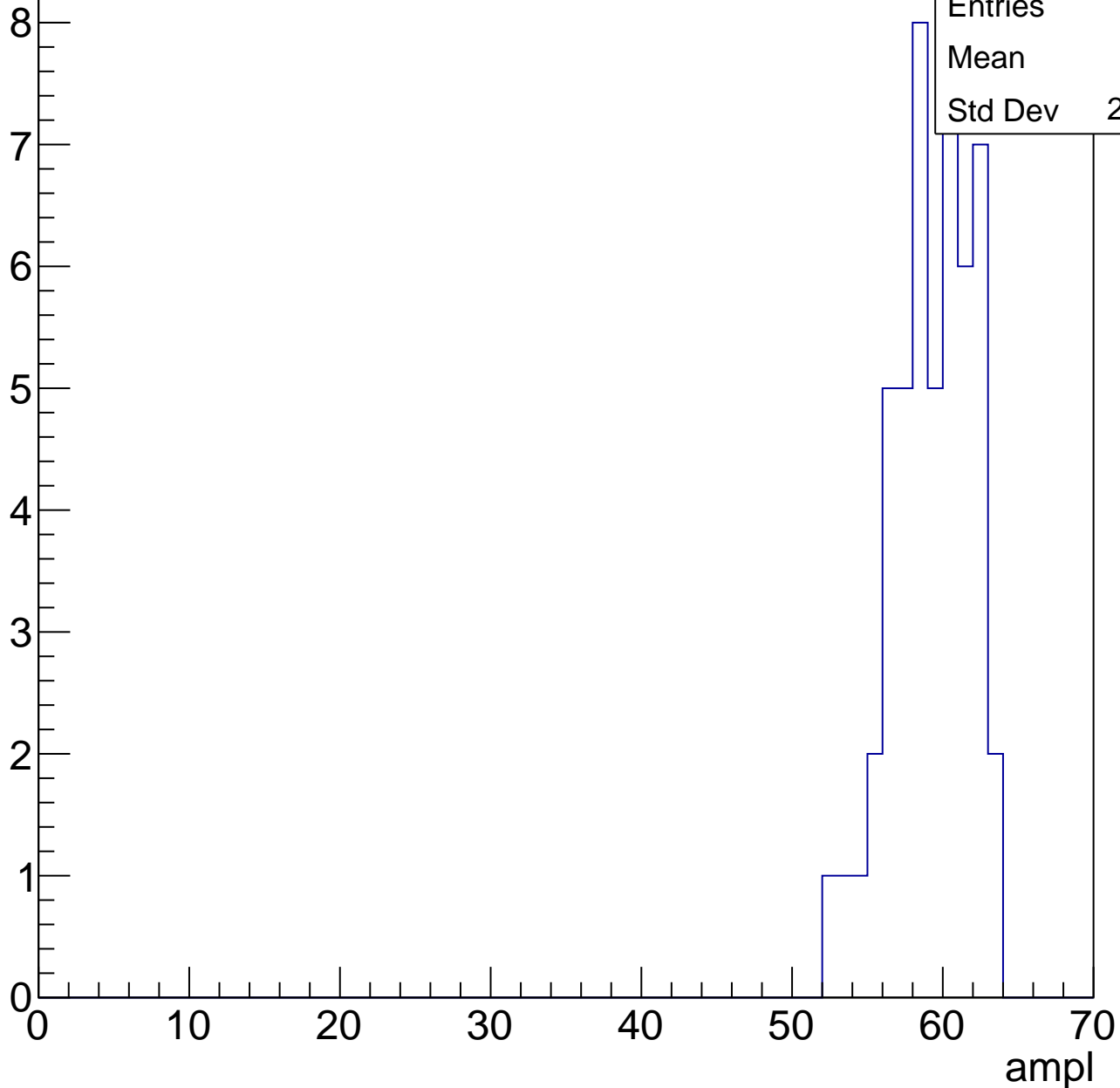
Entry



B0L002S, U12-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

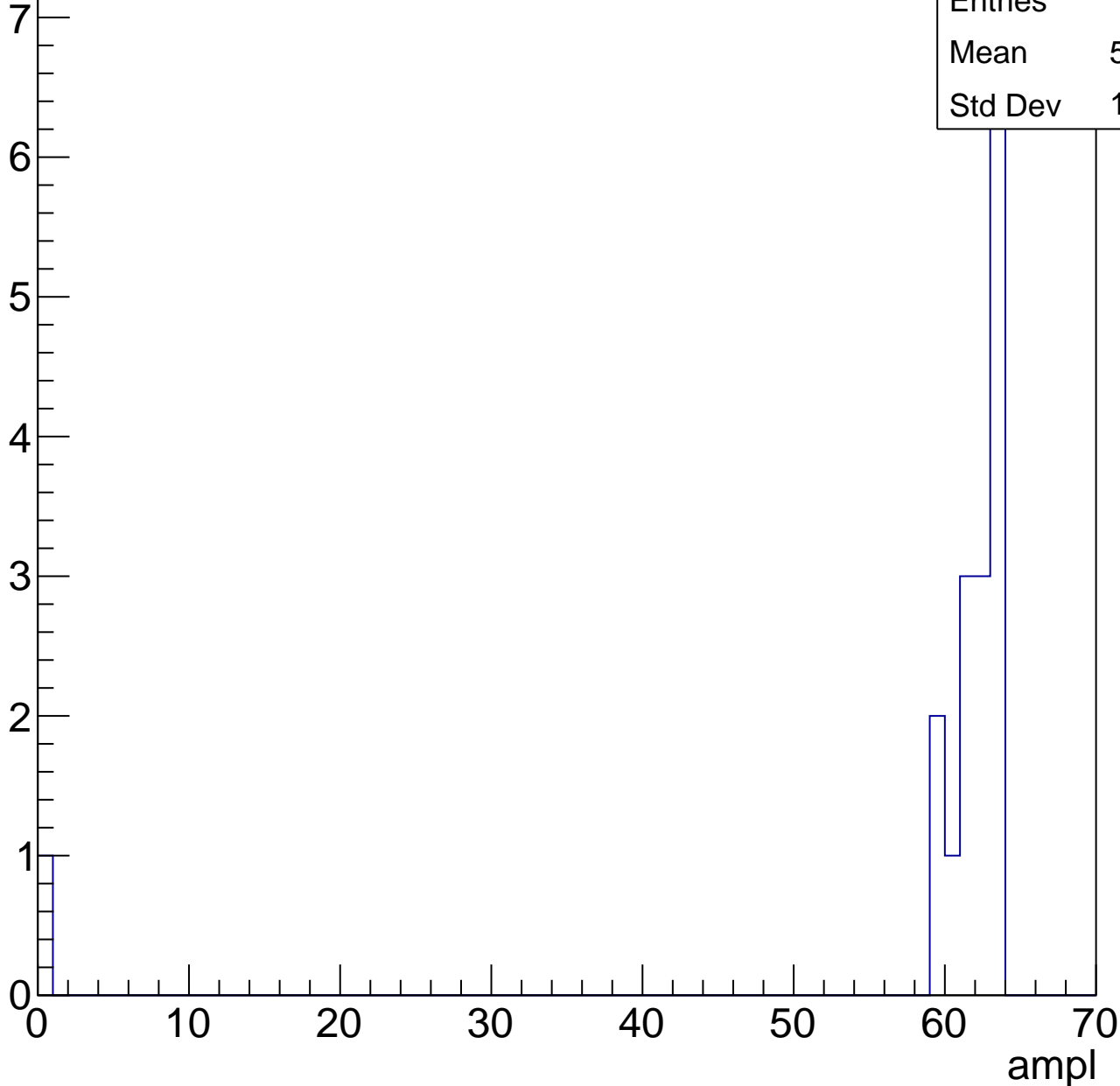


B0L002S, U12-ch27, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	58.12
Std Dev	14.59



B0L002S, U12-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch28, adc0

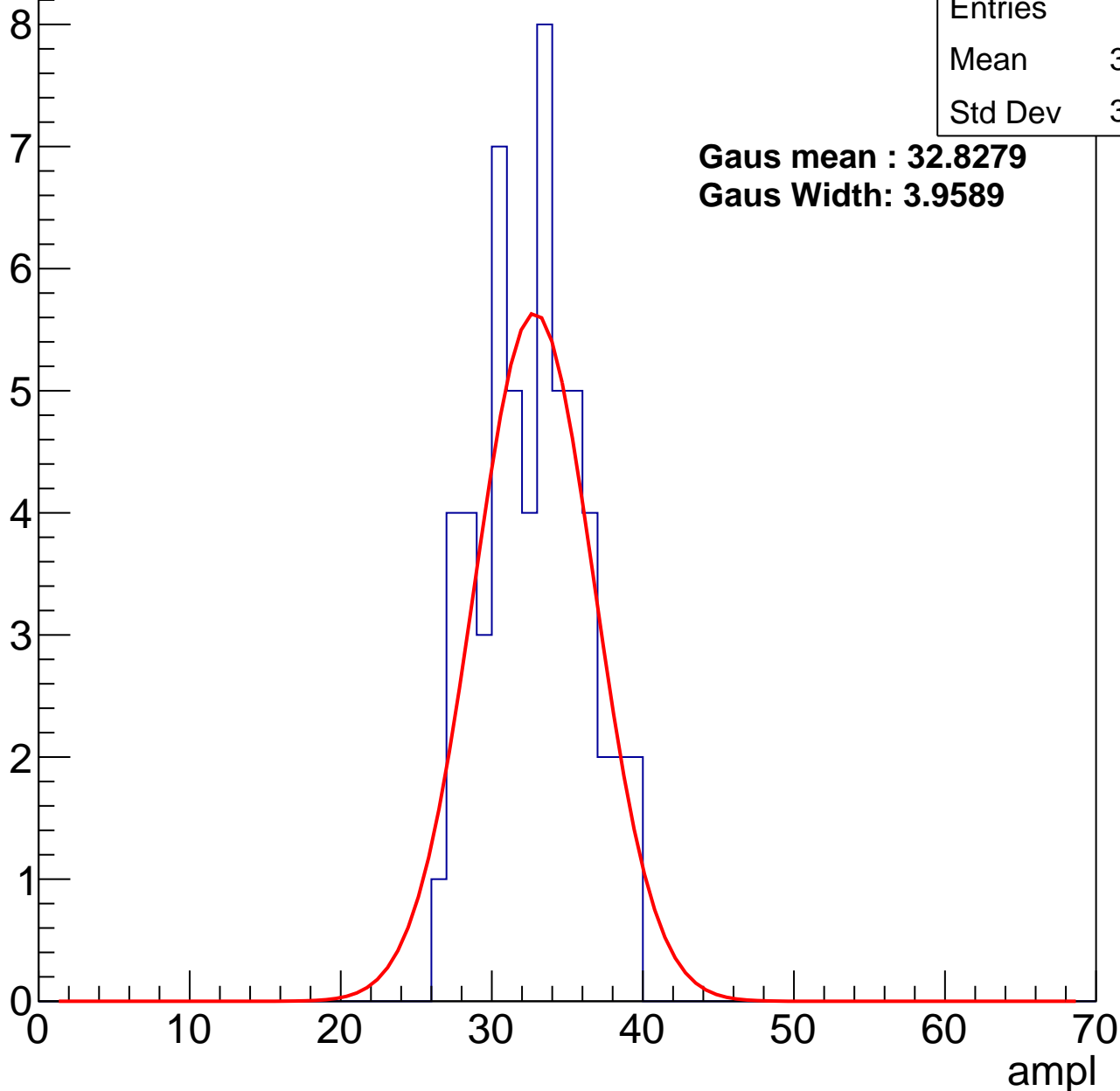
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	32.27
Std Dev	3.308

Gaus mean : 32.8279

Gaus Width: 3.9589



B0L002S, U12-ch28, adc1

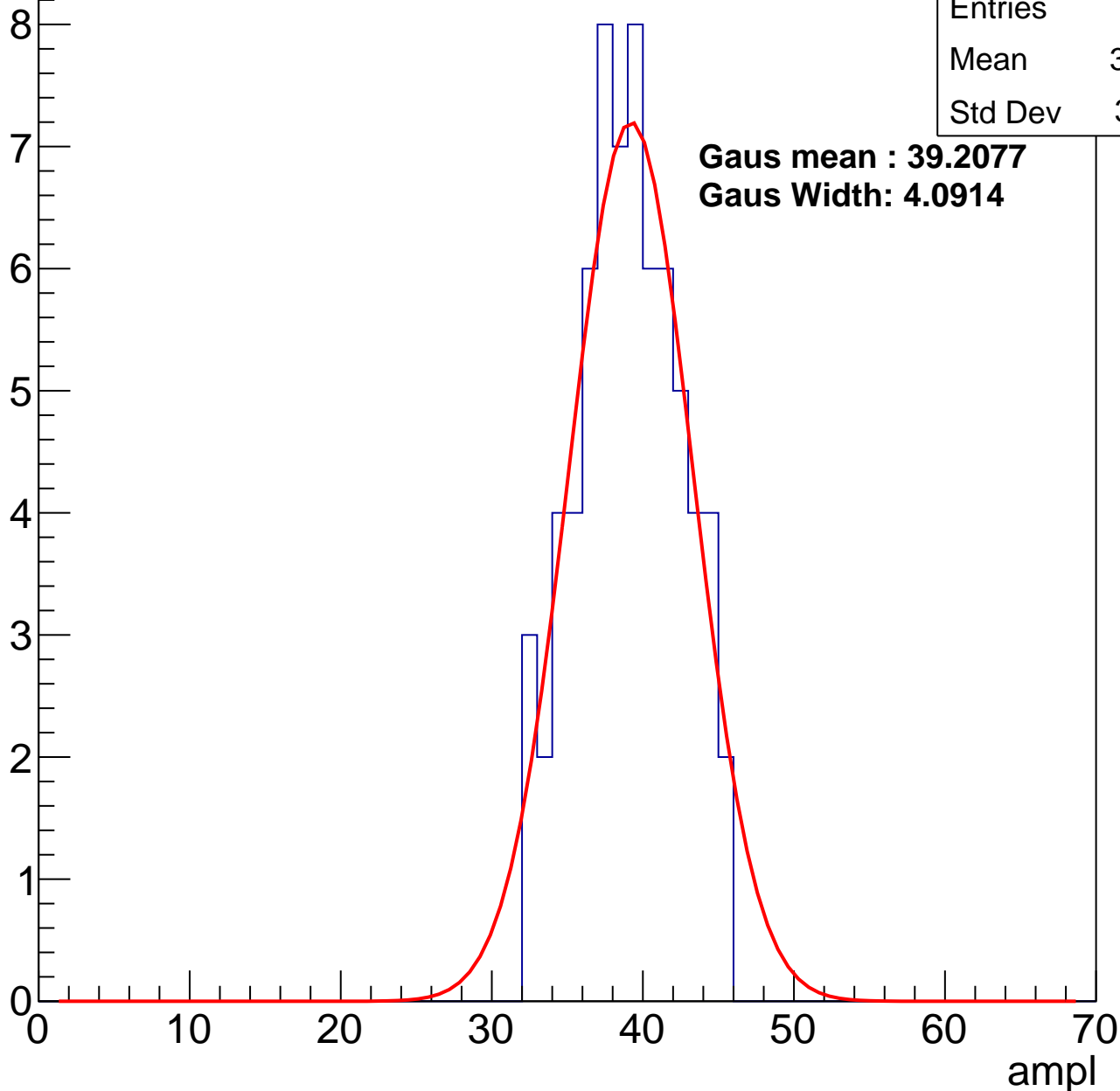
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	38.58
Std Dev	3.351

Gaus mean : 39.2077

Gaus Width: 4.0914



B0L002S, U12-ch28, adc2

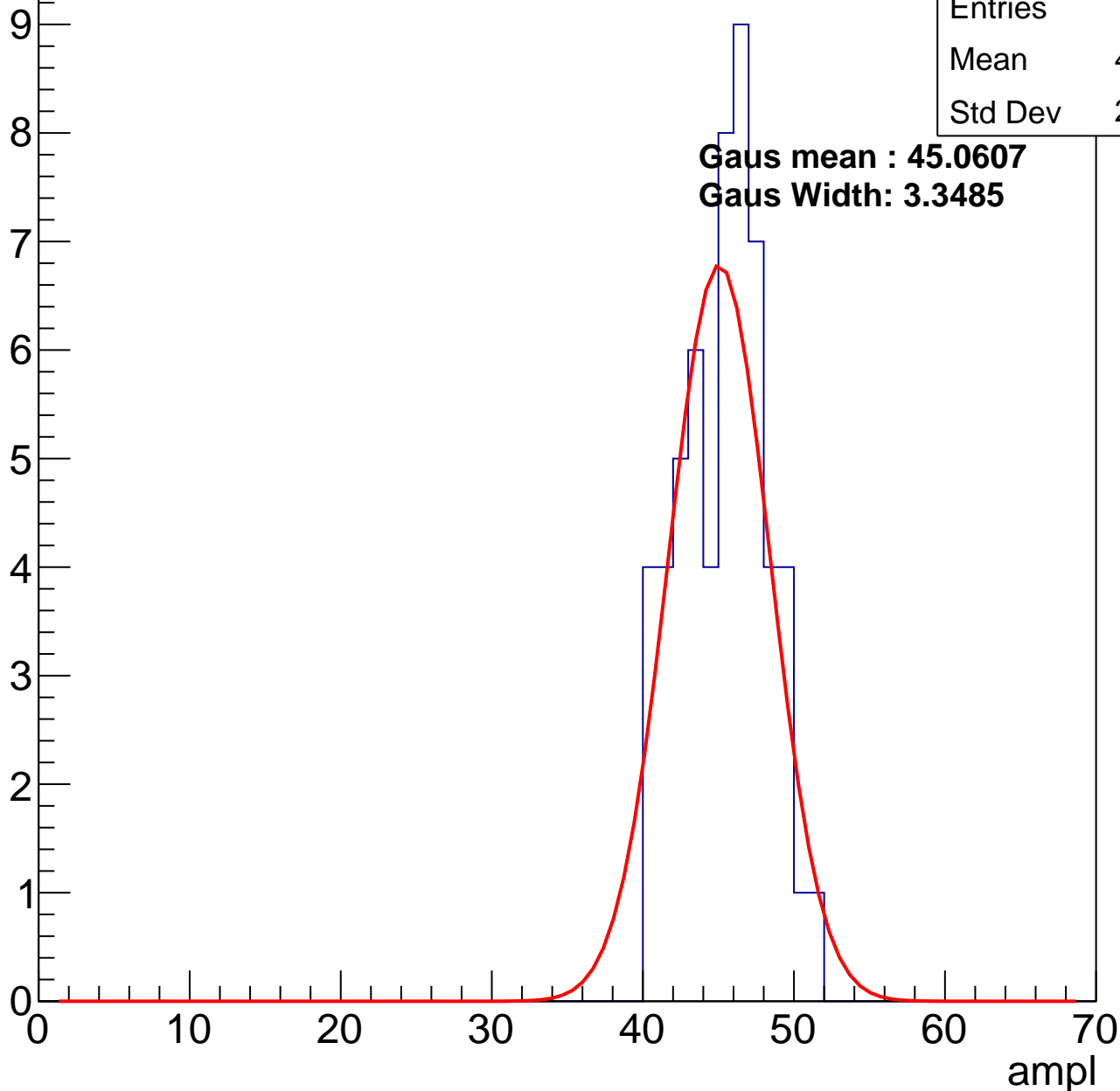
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.91
Std Dev	2.761

Gaus mean : 45.0607

Gaus Width: 3.3485



B0L002S, U12-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

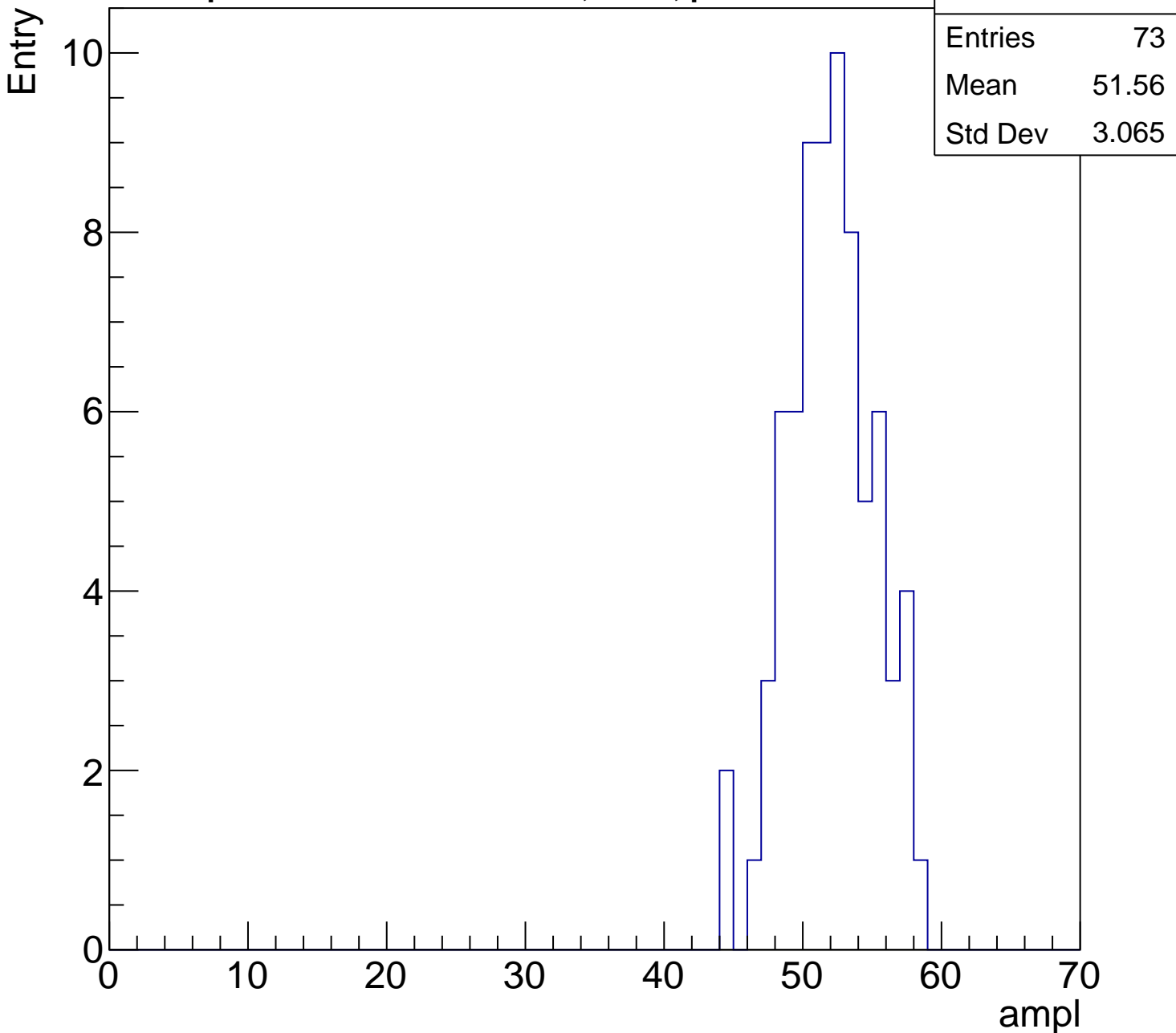
Entries	73
Mean	51.56
Std Dev	3.065

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

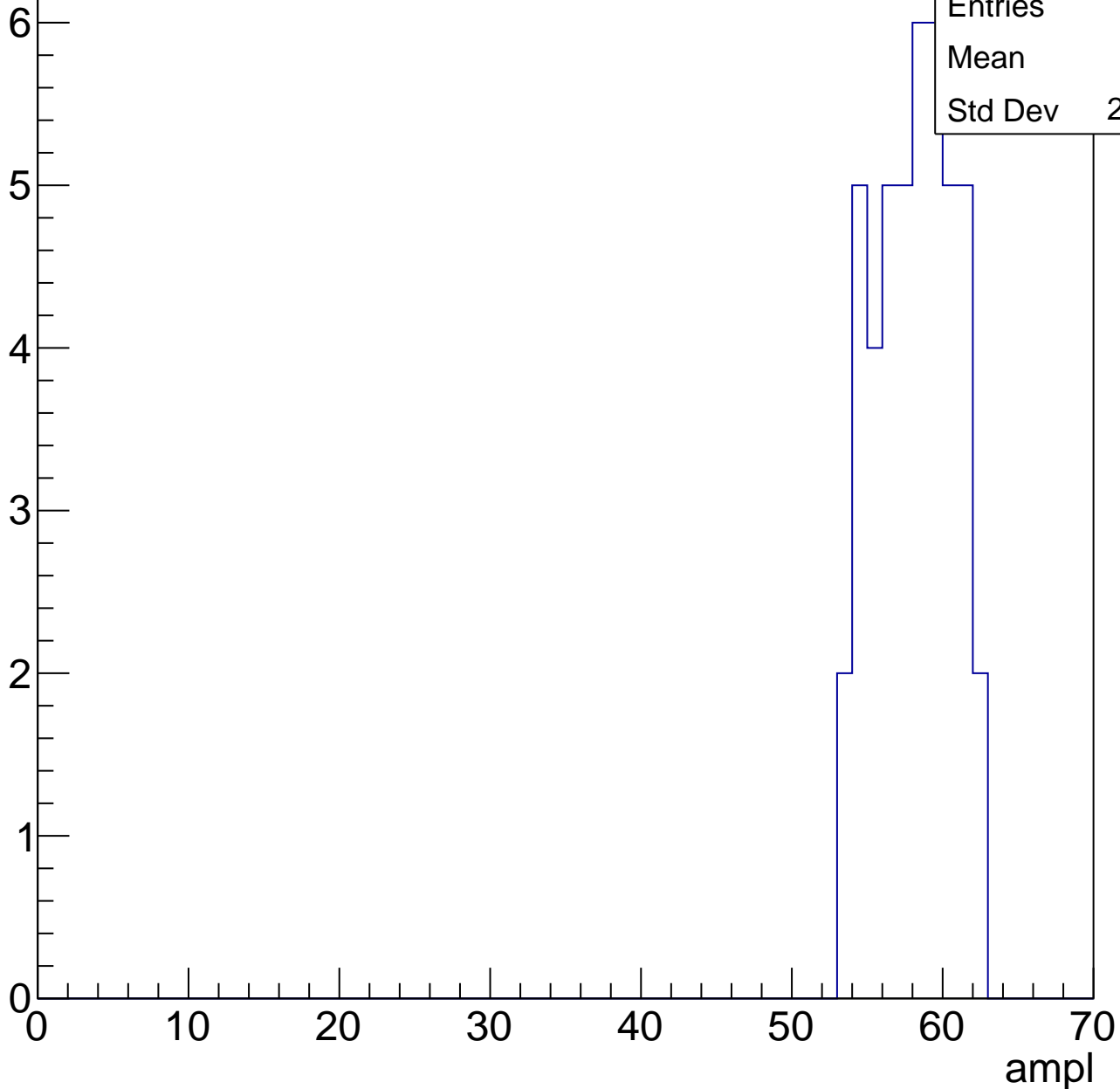


B0L002S, U12-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	57.6
Std Dev	2.525

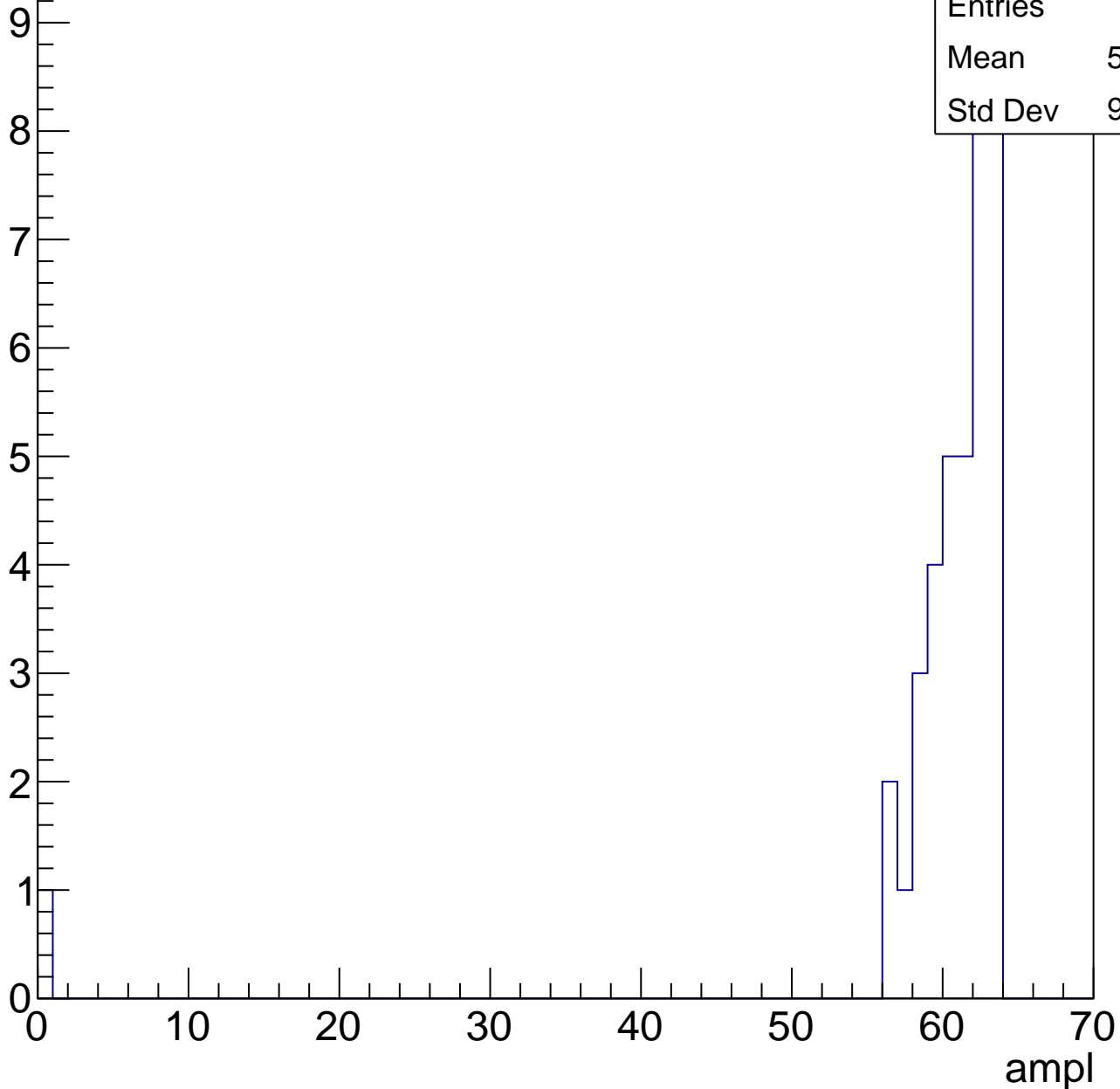


B0L002S, U12-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.13
Std Dev	9.929



B0L002S, U12-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U12-ch29, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	29.79
Std Dev	3.013

Gaus mean : 30.4745

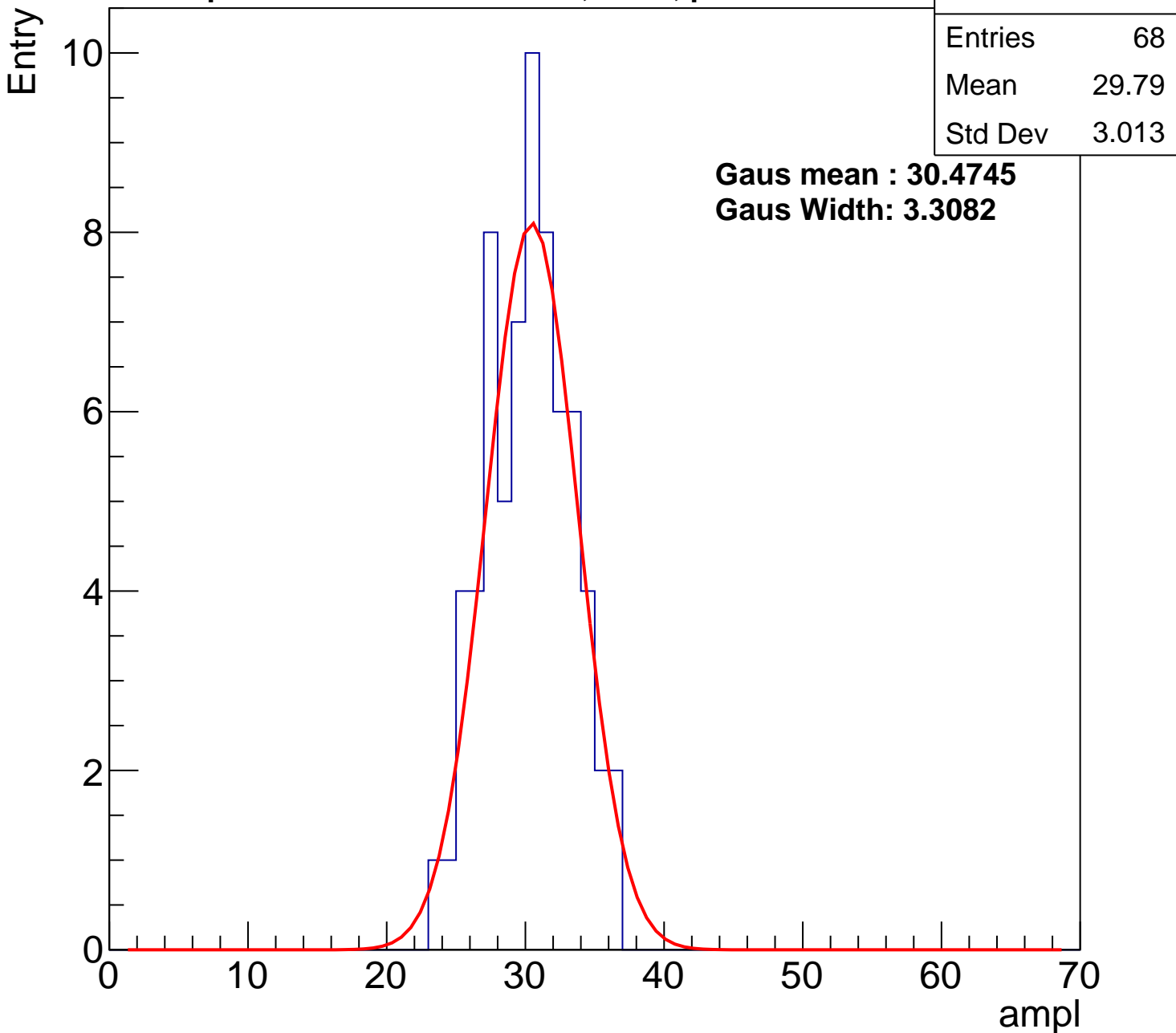
Gaus Width: 3.3082

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch29, adc1

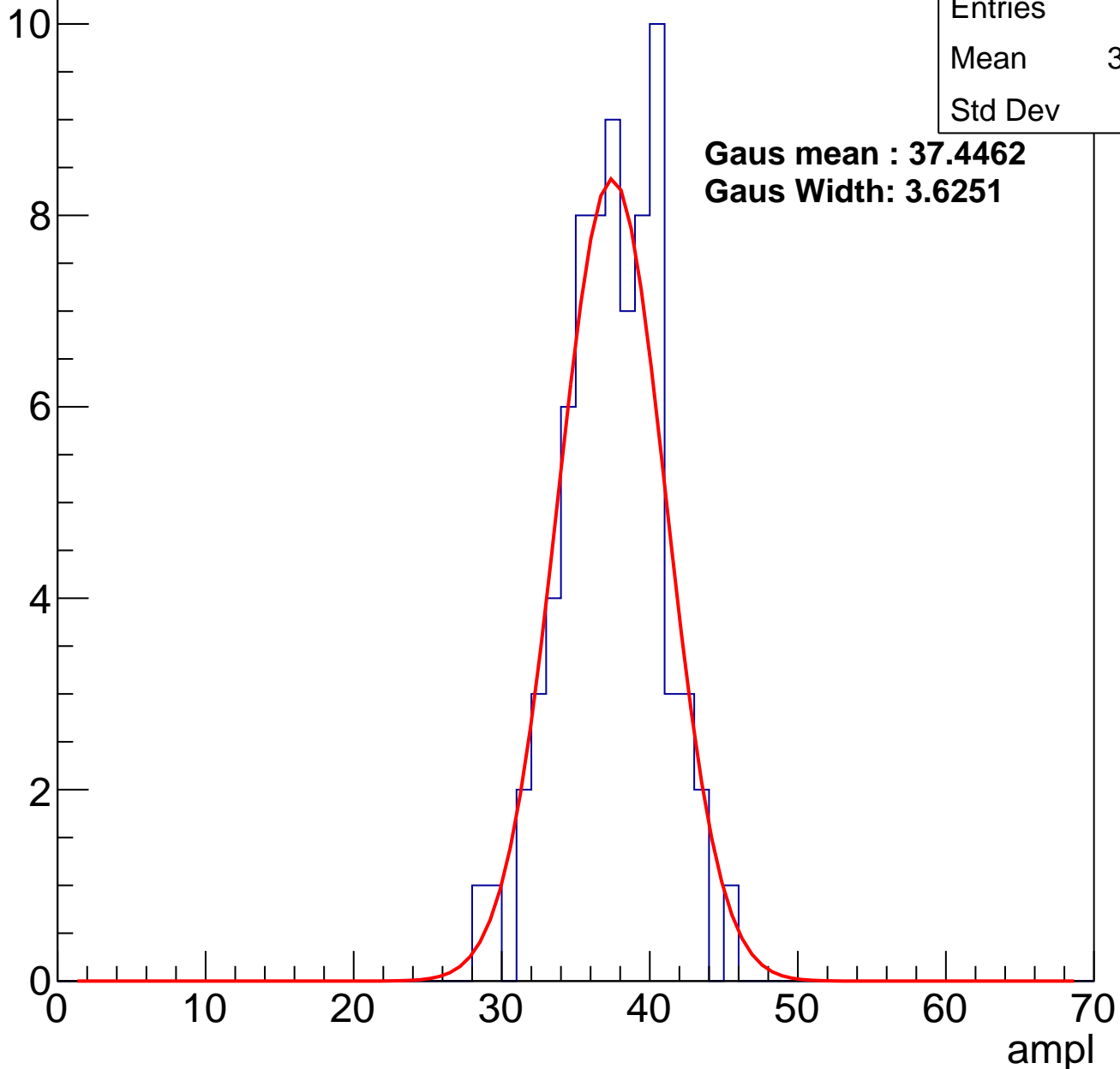
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	36.97
Std Dev	3.32

Gaus mean : 37.4462

Gaus Width: 3.6251

Entry



B0L002S, U12-ch29, adc2

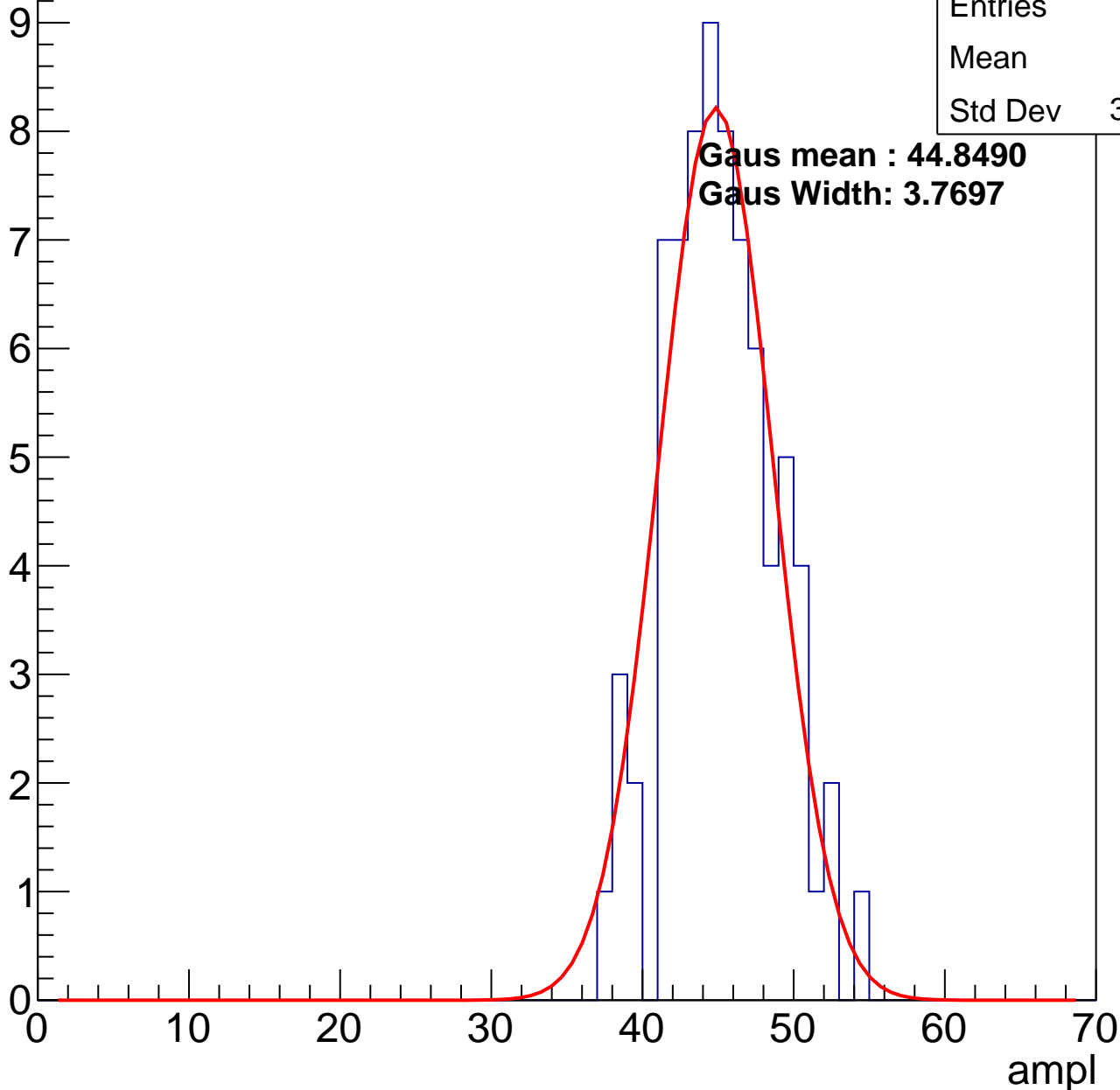
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.8
Std Dev	3.574

Gaus mean : 44.8490

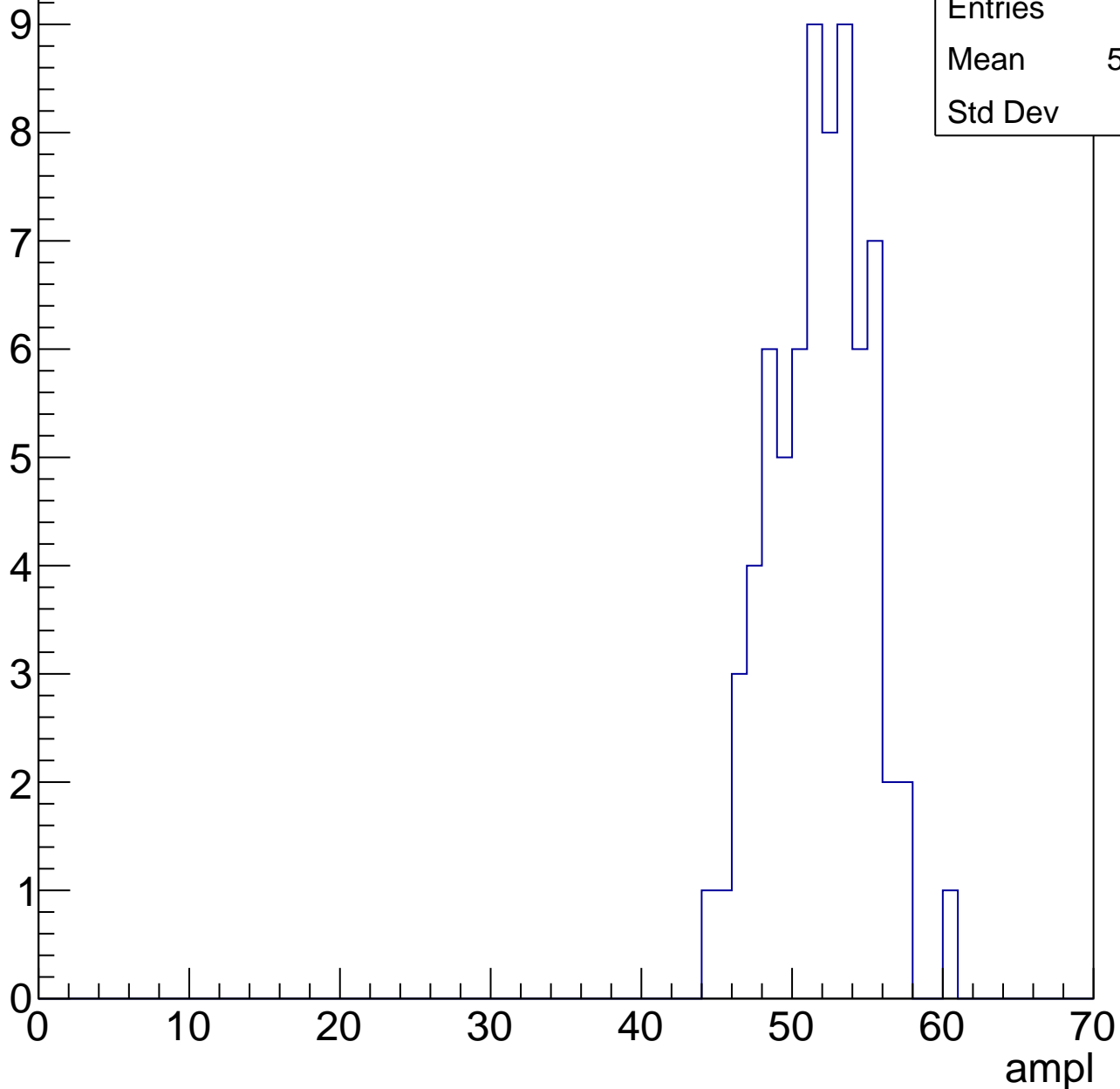
Gaus Width: 3.7697



B0L002S, U12-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

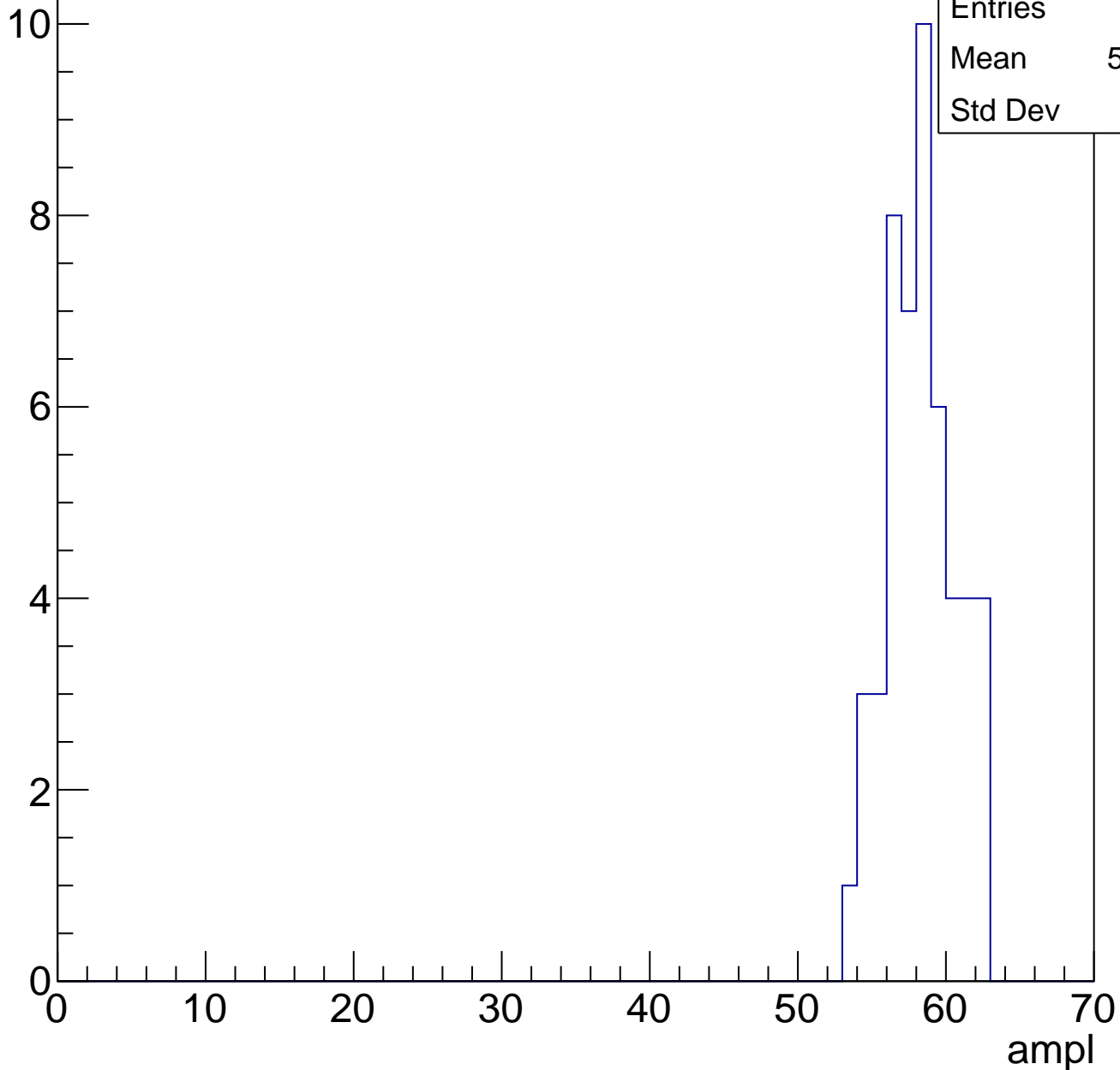


B0L002S, U12-ch29, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	50
Mean	57.86
Std Dev	2.28

Entry

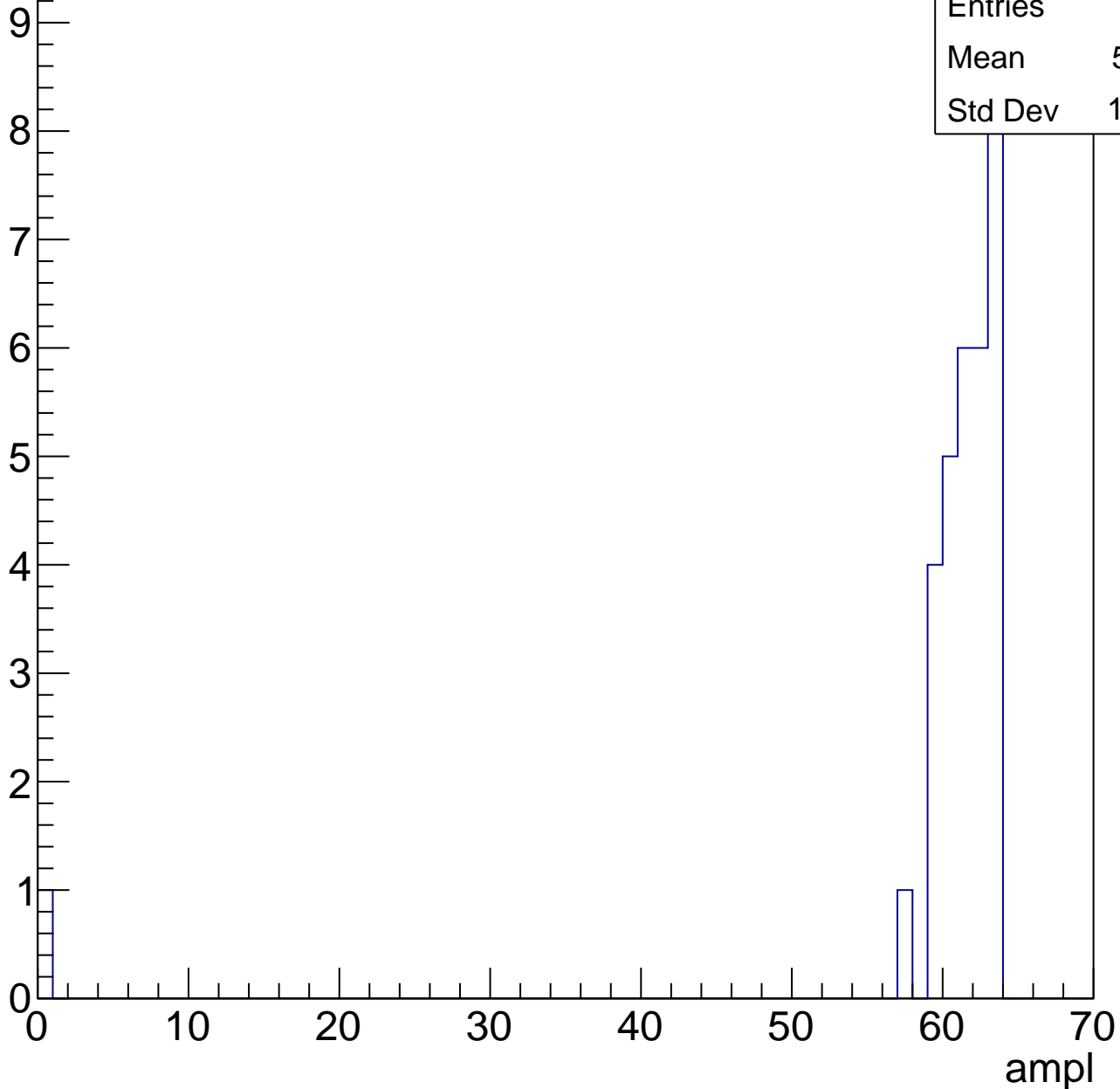


B0L002S, U12-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59.31
Std Dev	10.77



B0L002S, U12-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch30, adc0

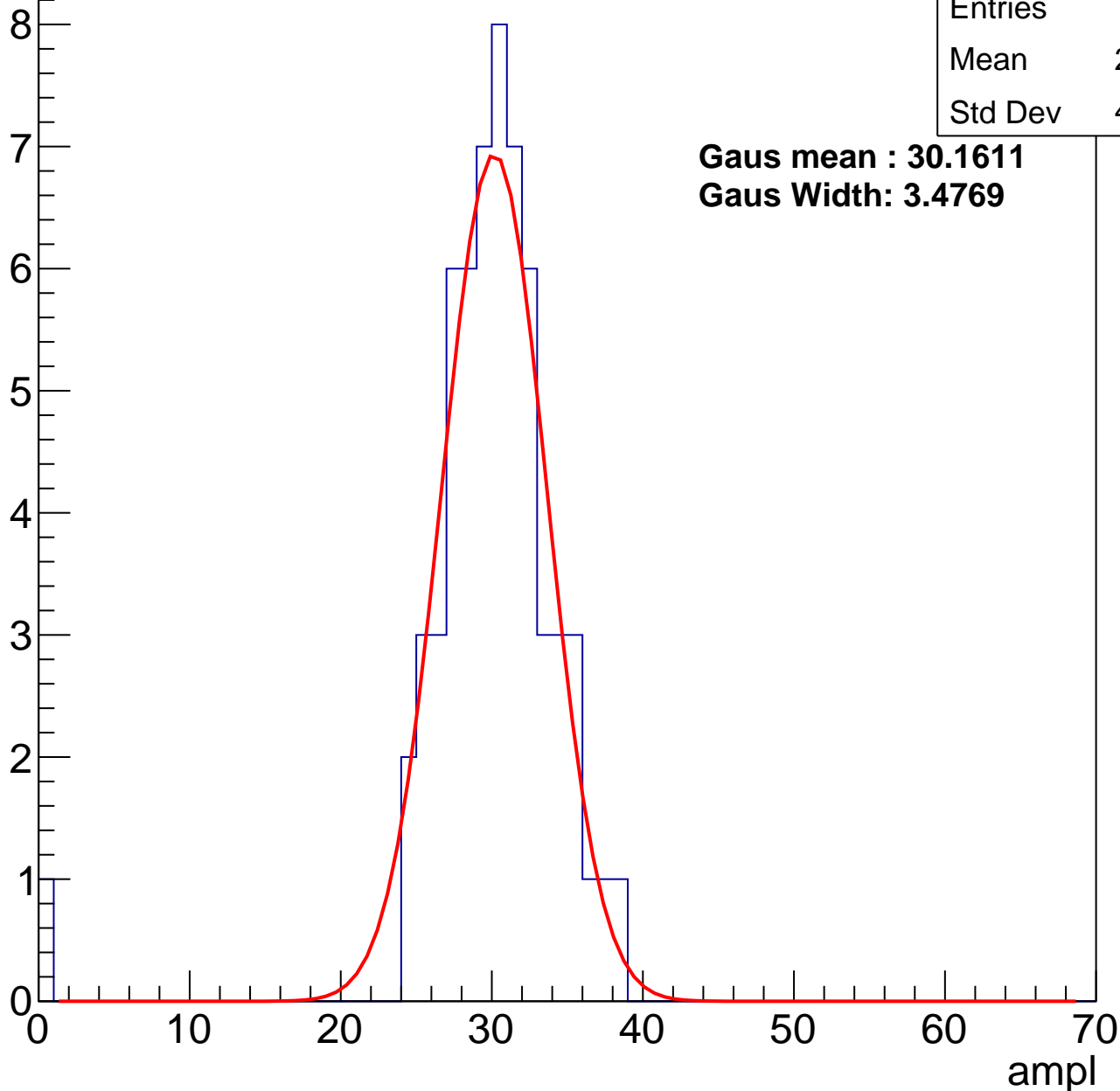
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.51
Std Dev	4.951

Gaus mean : 30.1611

Gaus Width: 3.4769



B0L002S, U12-ch30, adc1

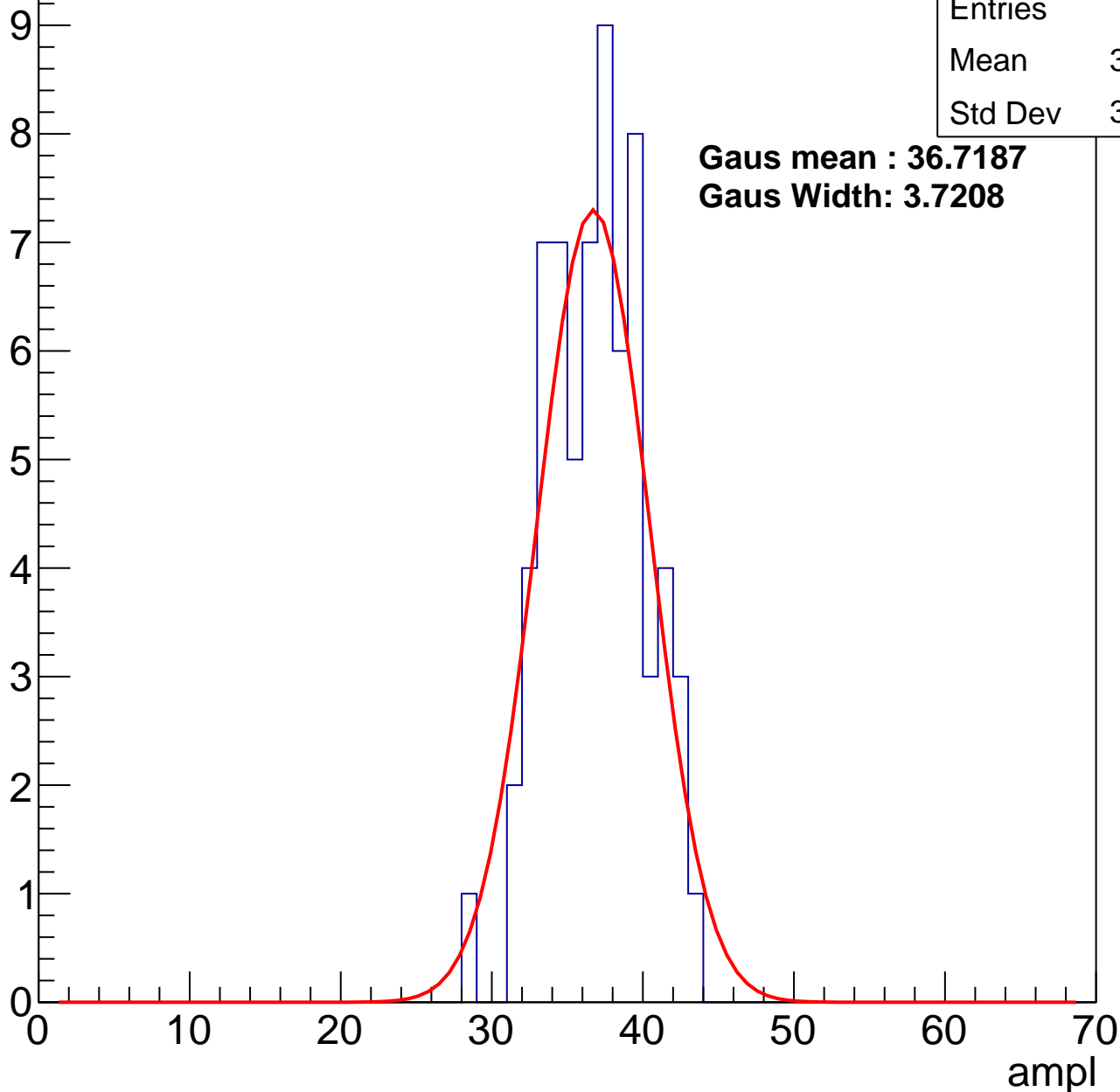
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.42
Std Dev	3.168

Gaus mean : 36.7187

Gaus Width: 3.7208



B0L002S, U12-ch30, adc2

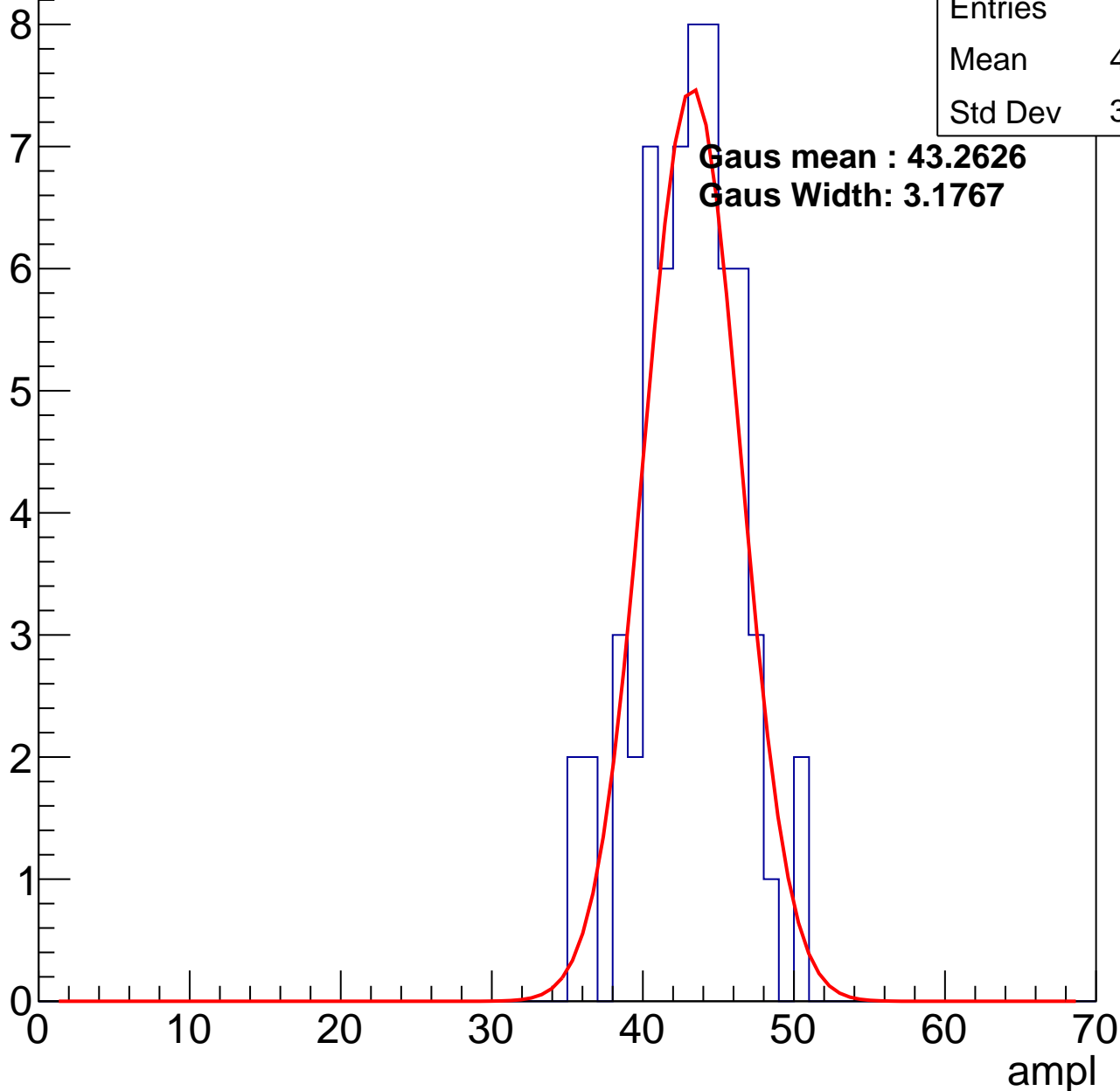
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.62
Std Dev	3.273

Gaus mean : 43.2626

Gaus Width: 3.1767

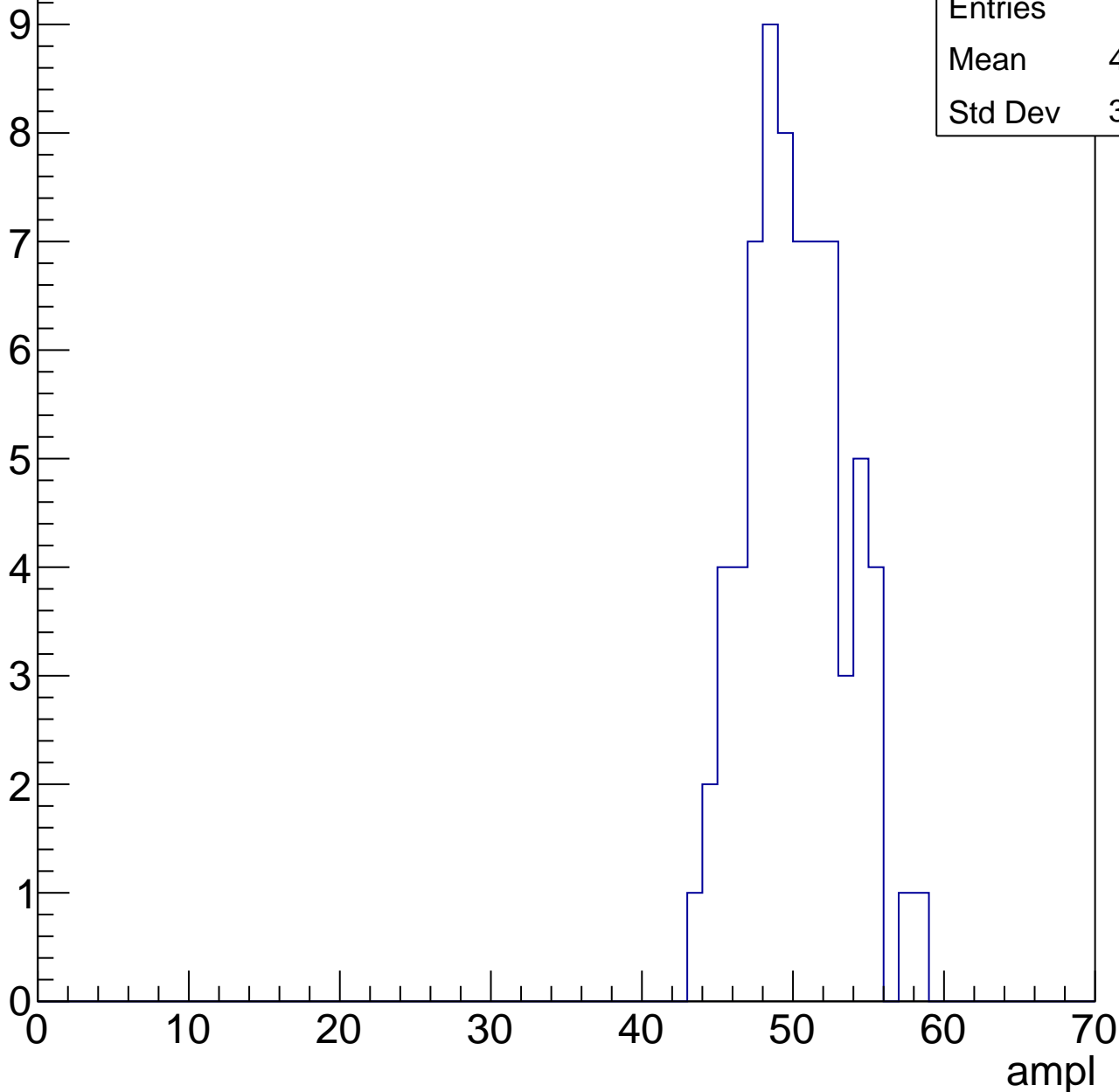


B0L002S, U12-ch30, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

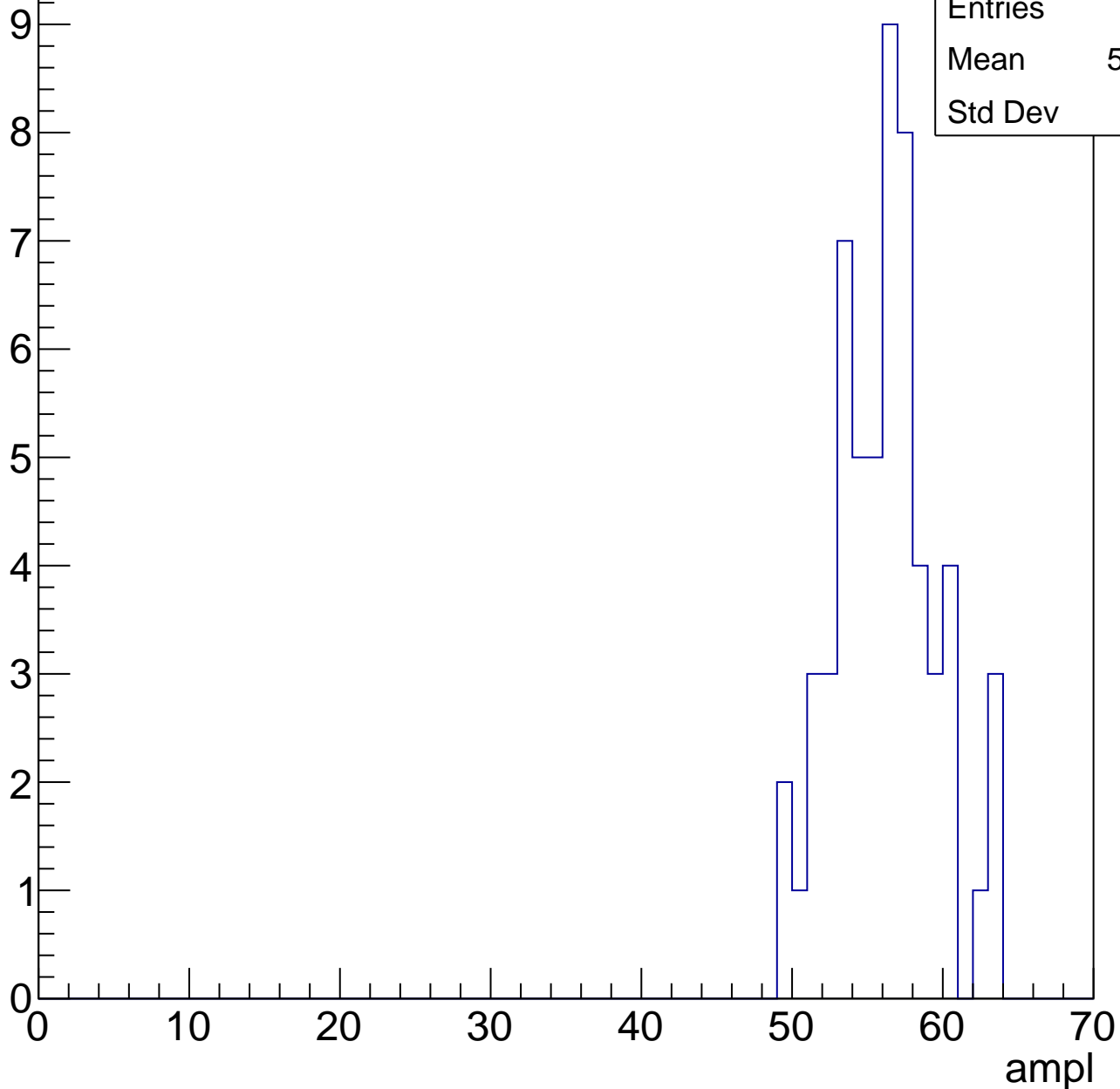
Entries	70
Mean	49.76
Std Dev	3.258



B0L002S, U12-ch30, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



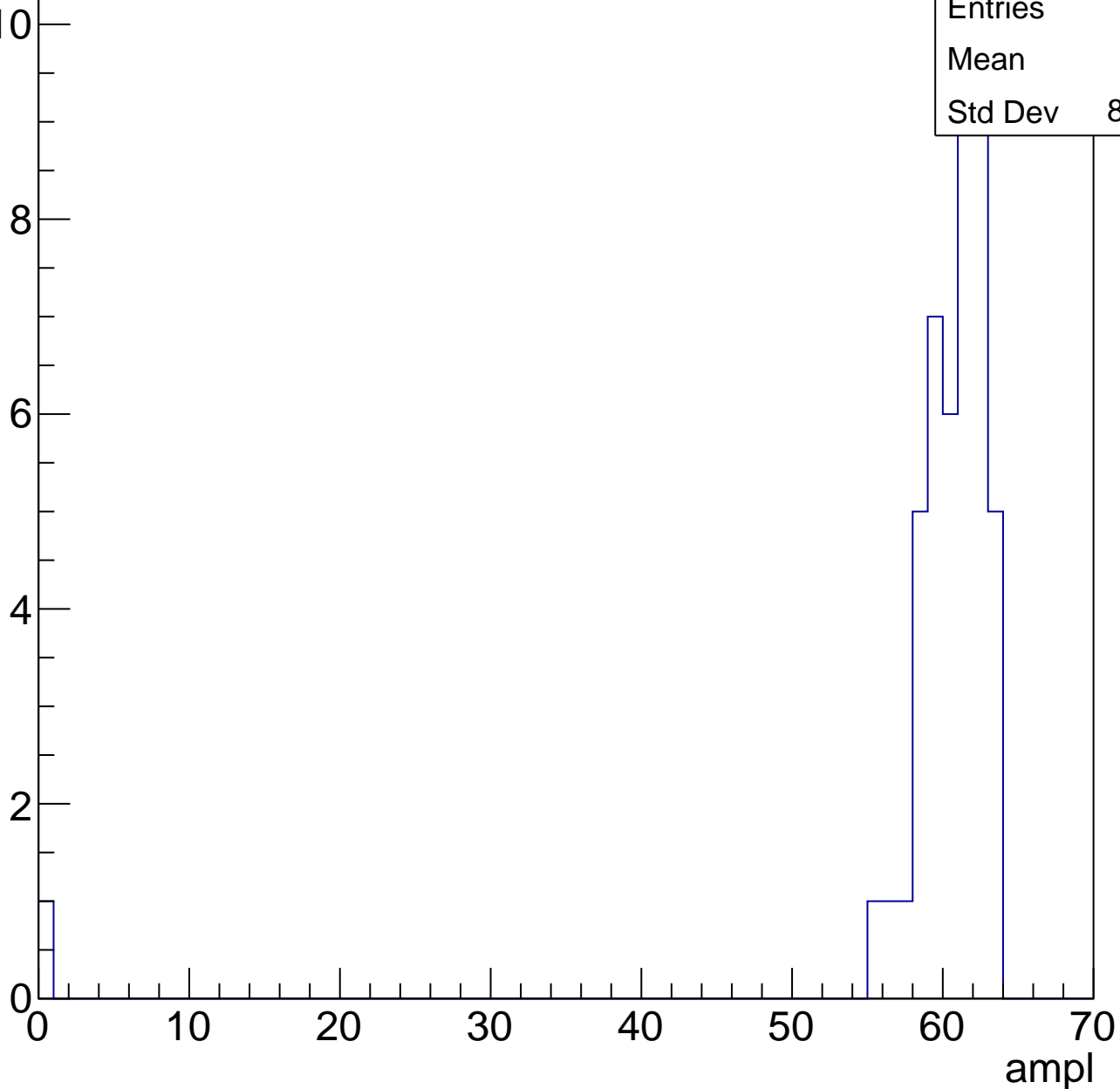
Entries	58
Mean	55.74
Std Dev	3.32

B0L002S, U12-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	59
Std Dev	8.995



B0L002S, U12-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U12-ch31, adc0

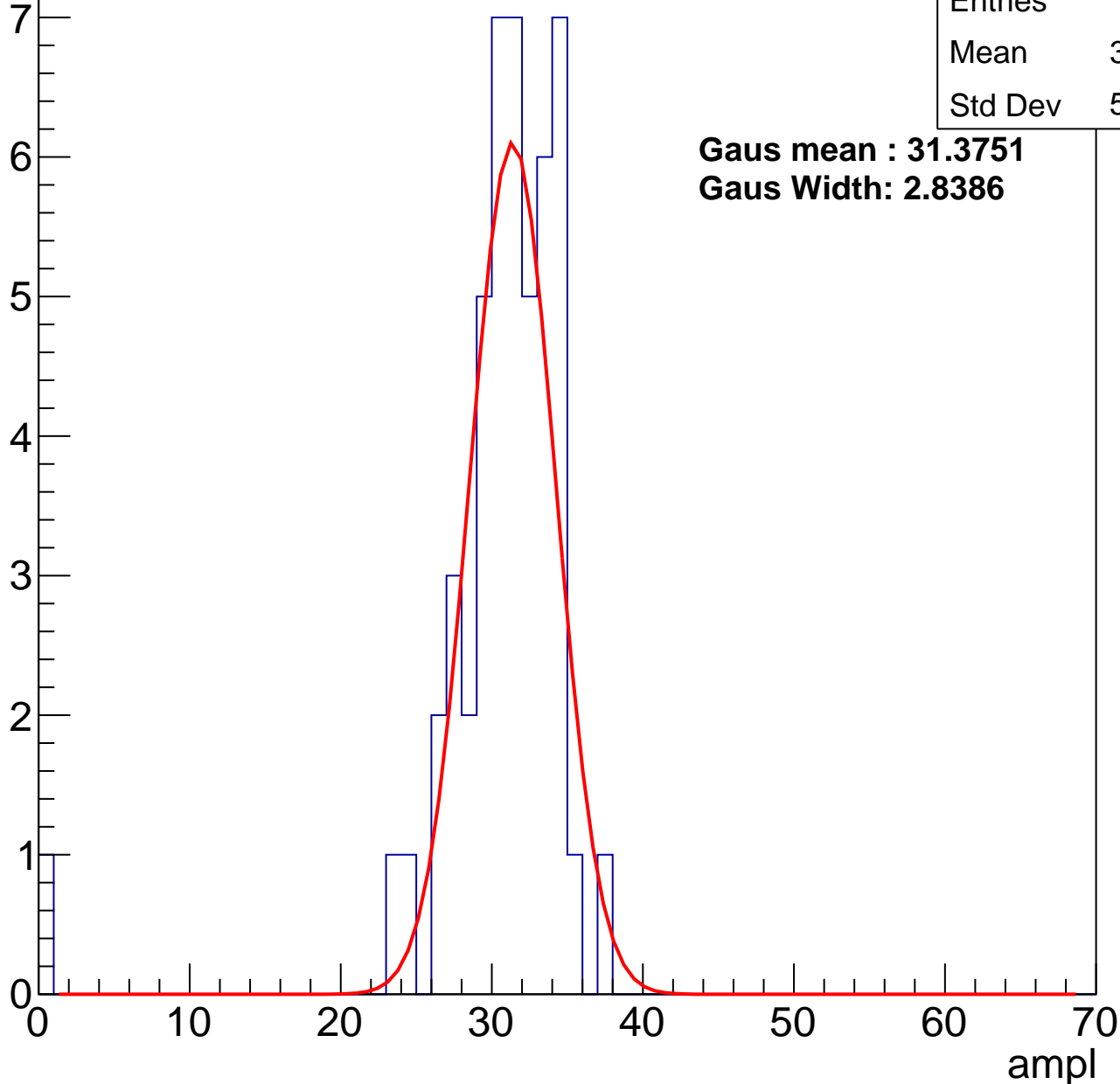
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	30.12
Std Dev	5.197

Gaus mean : 31.3751

Gaus Width: 2.8386



B0L002S, U12-ch31, adc1

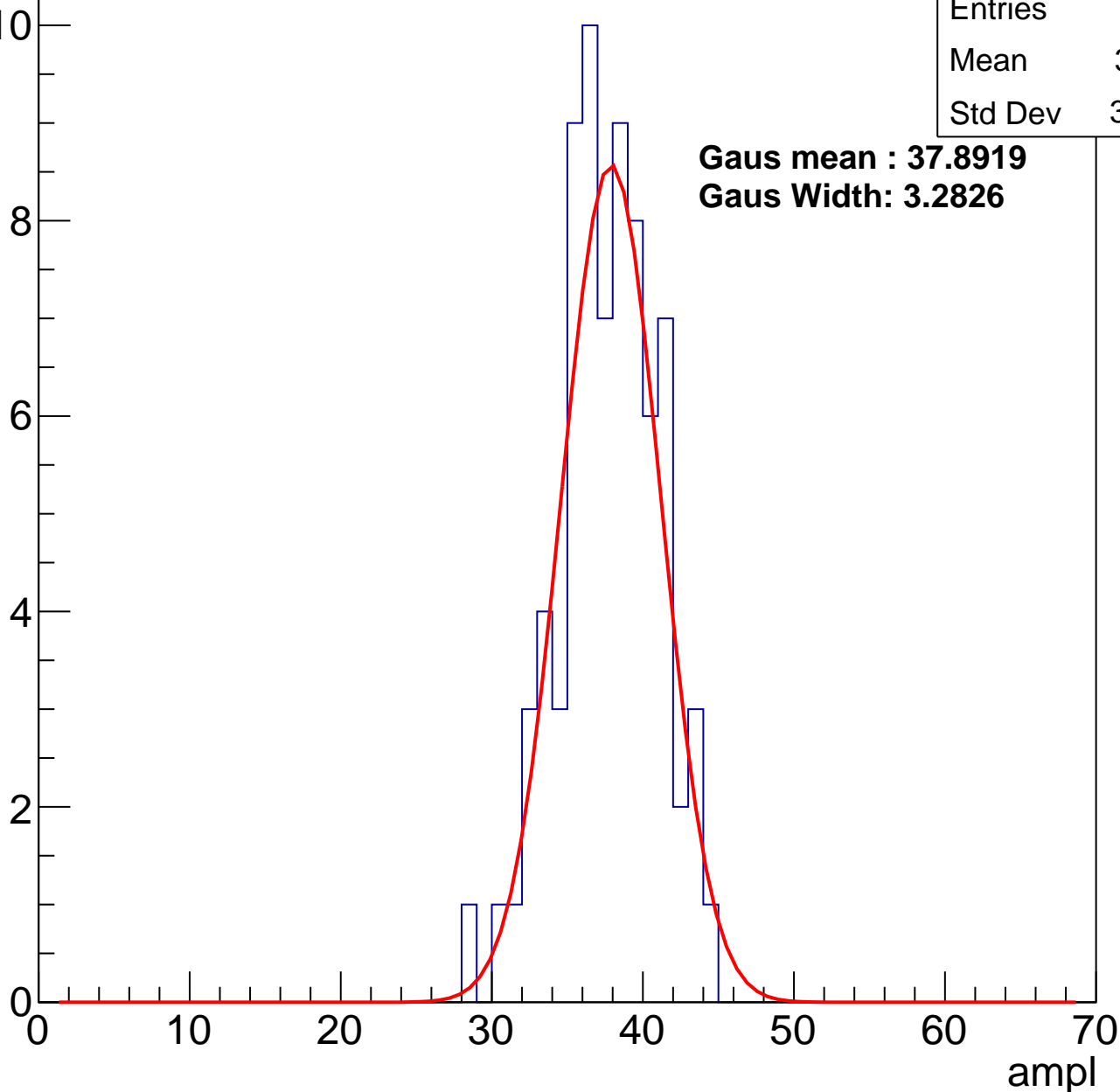
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.21
Std Dev	3.243

Gaus mean : 37.8919

Gaus Width: 3.2826



B0L002S, U12-ch31, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	44.82
Std Dev	3.459

Gaus mean : 45.6234

Gaus Width: 3.7839

10

8

6

4

2

0

0

10

20

30

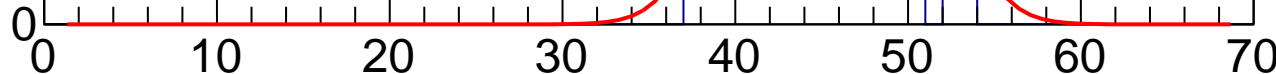
40

50

60

70

ampl

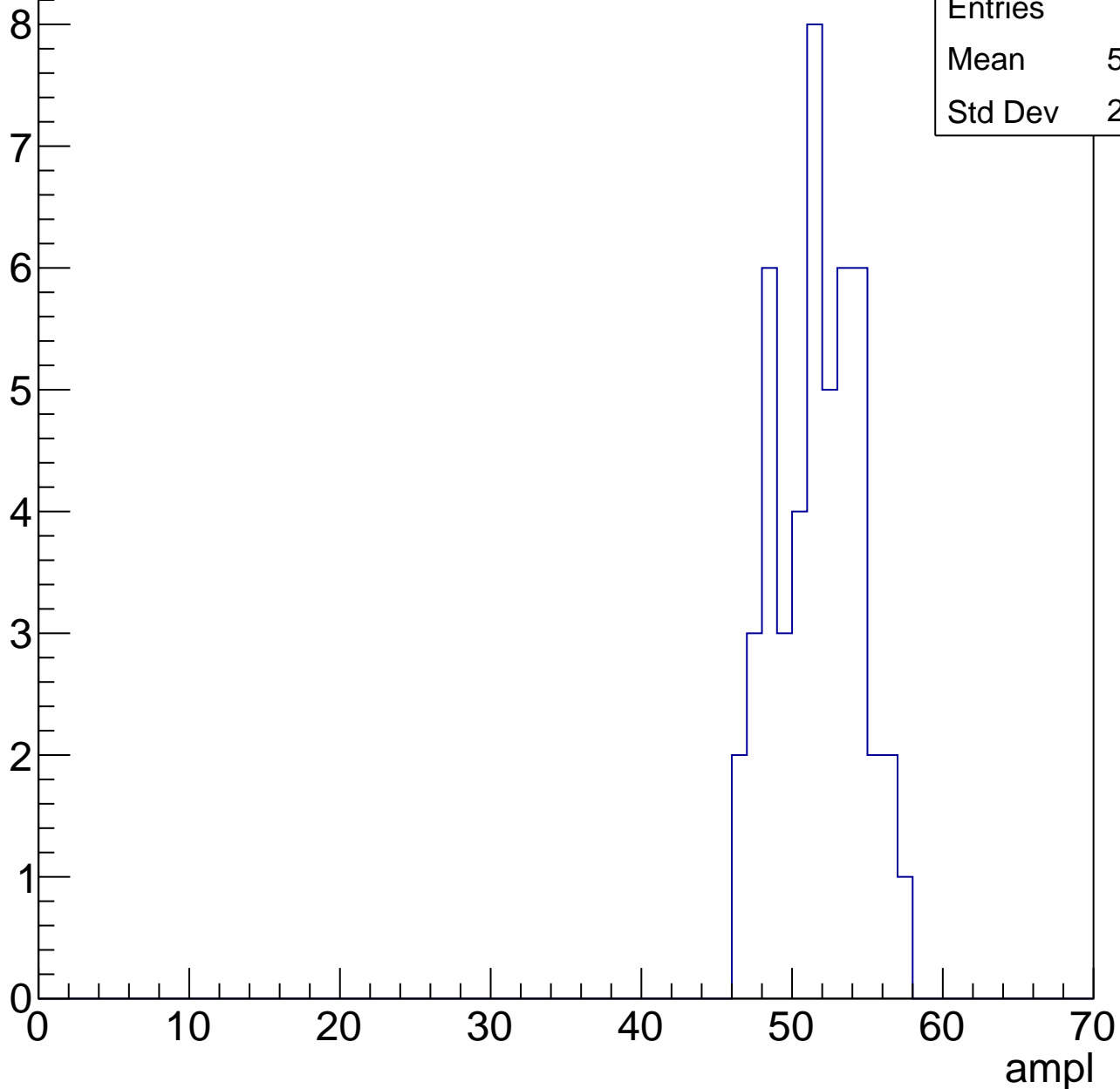


B0L002S, U12-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	51.19
Std Dev	2.766



B0L002S, U12-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

Entries 80

Mean 57.33

Std Dev 3.24

8

6

4

2

0

0

10

20

30

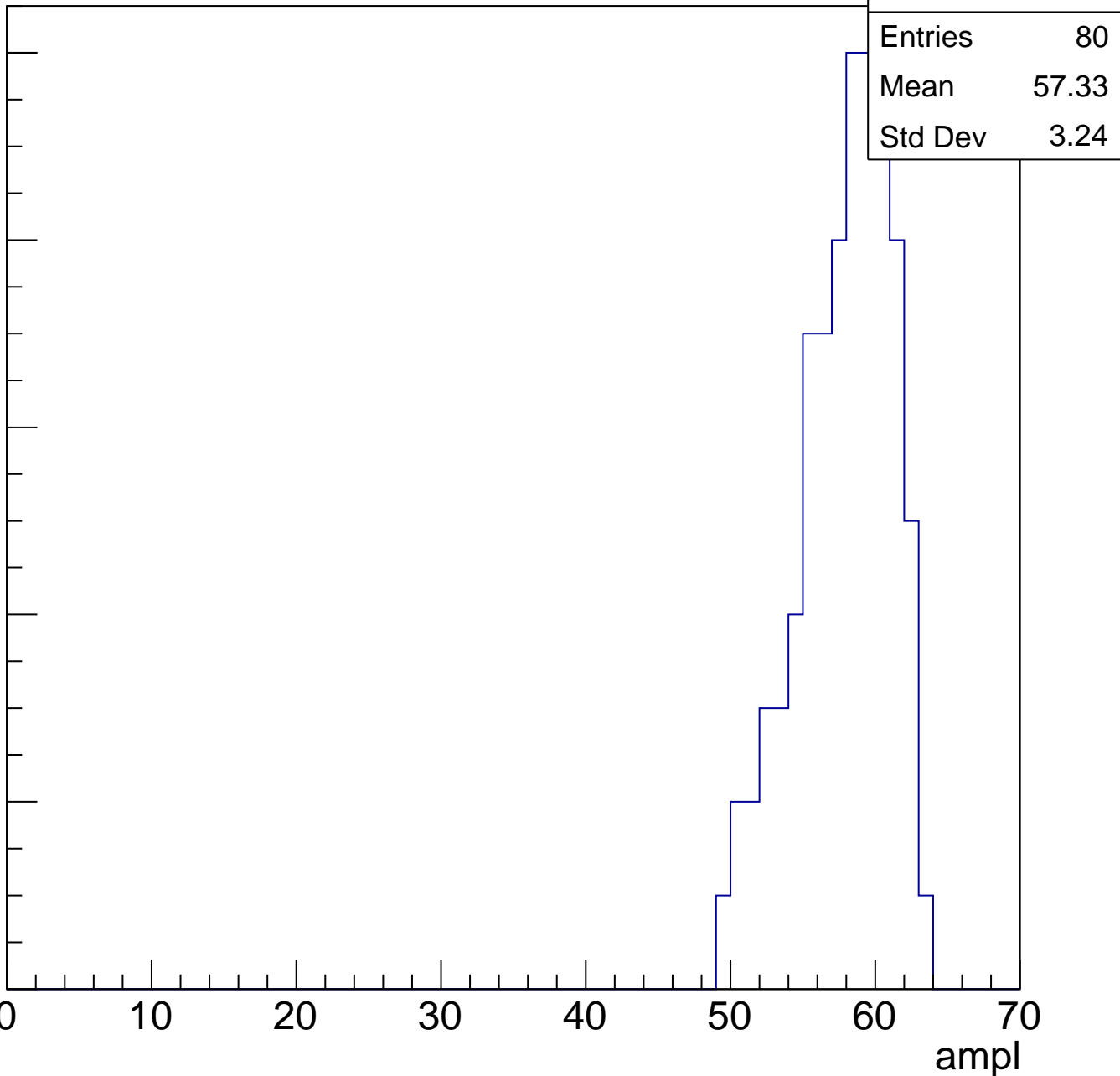
40

50

60

70

ampl

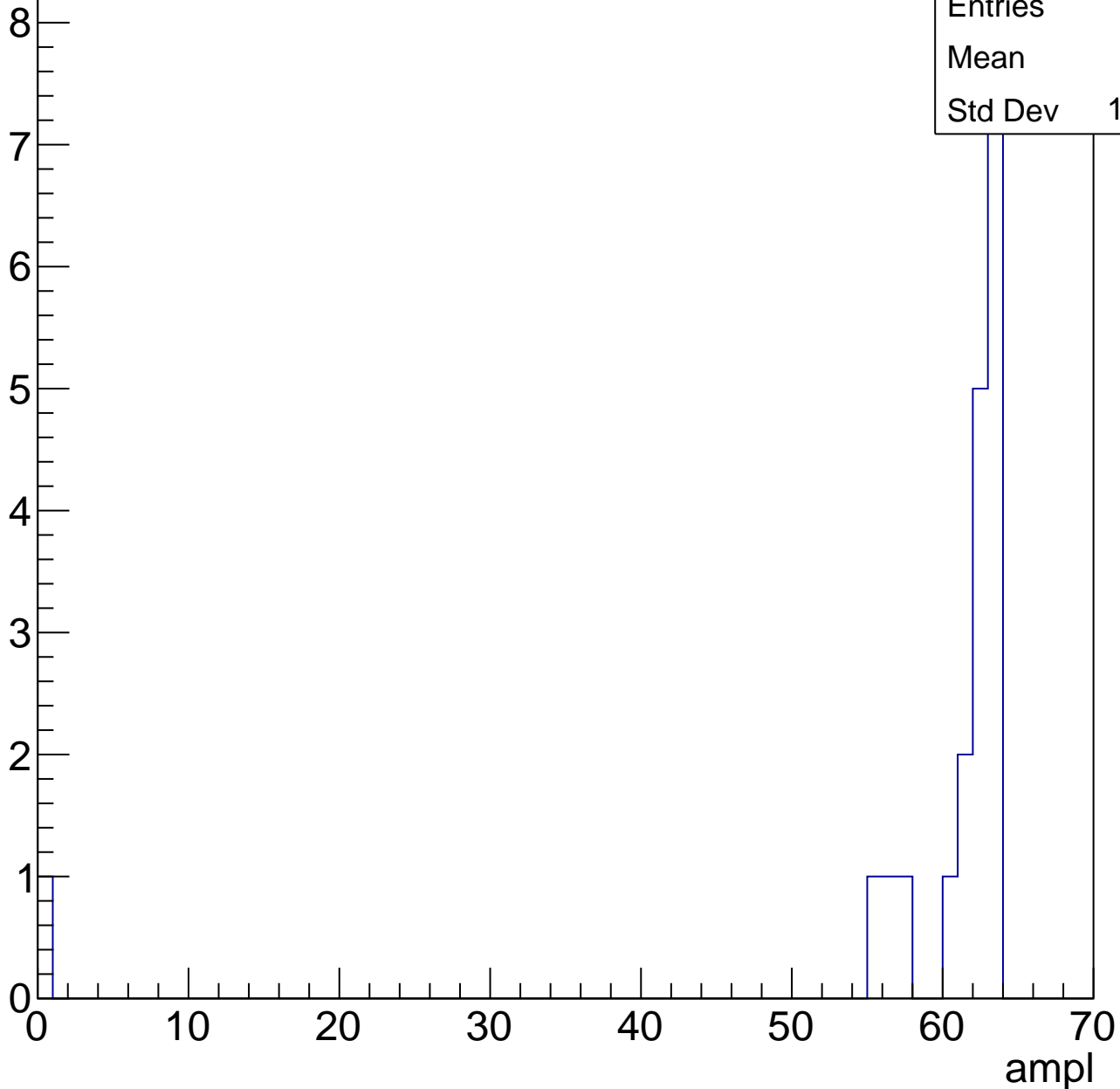


B0L002S, U12-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	20
Mean	58.2
Std Dev	13.56



B0L002S, U12-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch32, adc0

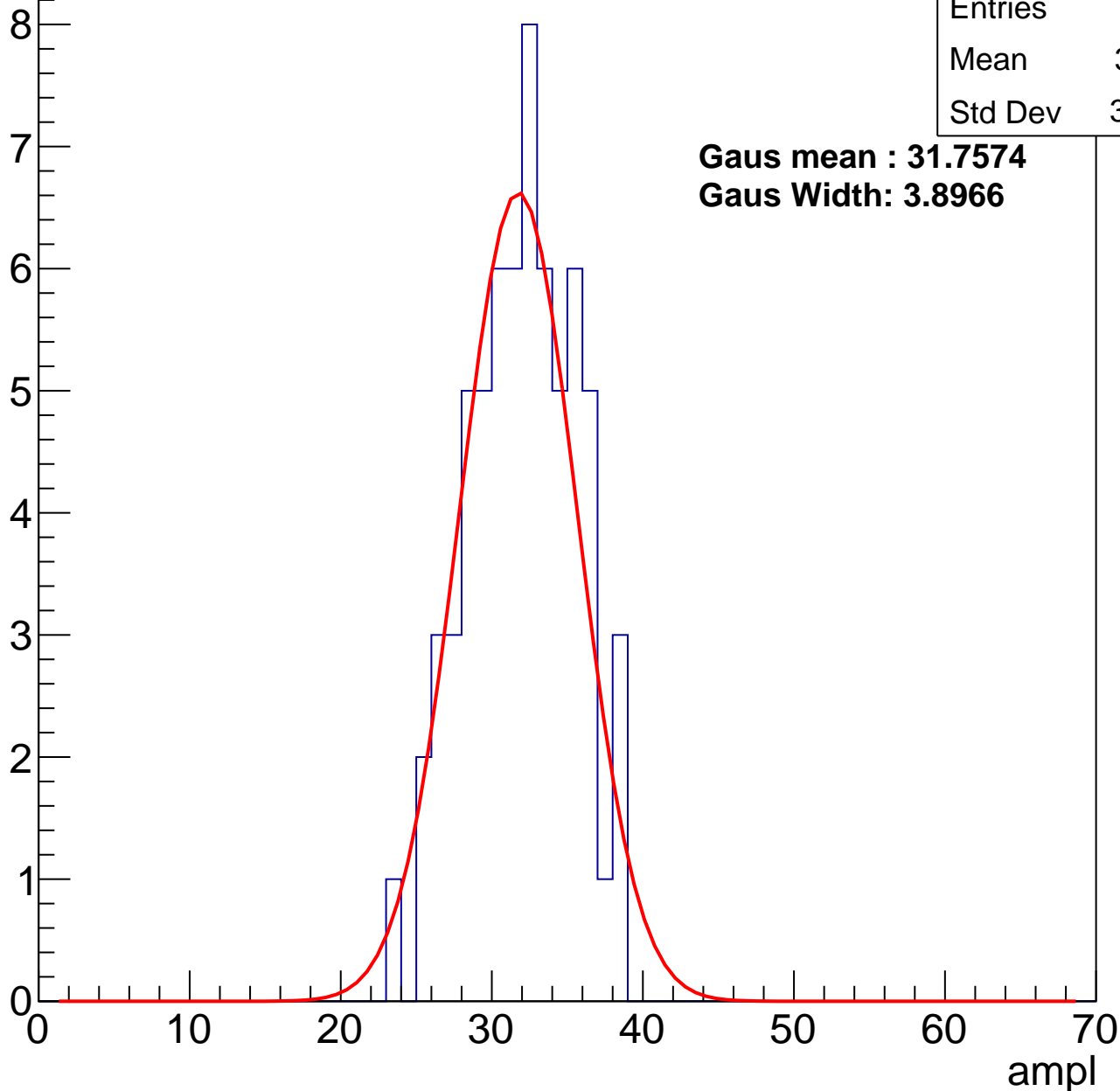
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	31.51
Std Dev	3.496

Gaus mean : 31.7574

Gaus Width: 3.8966



B0L002S, U12-ch32, adc1

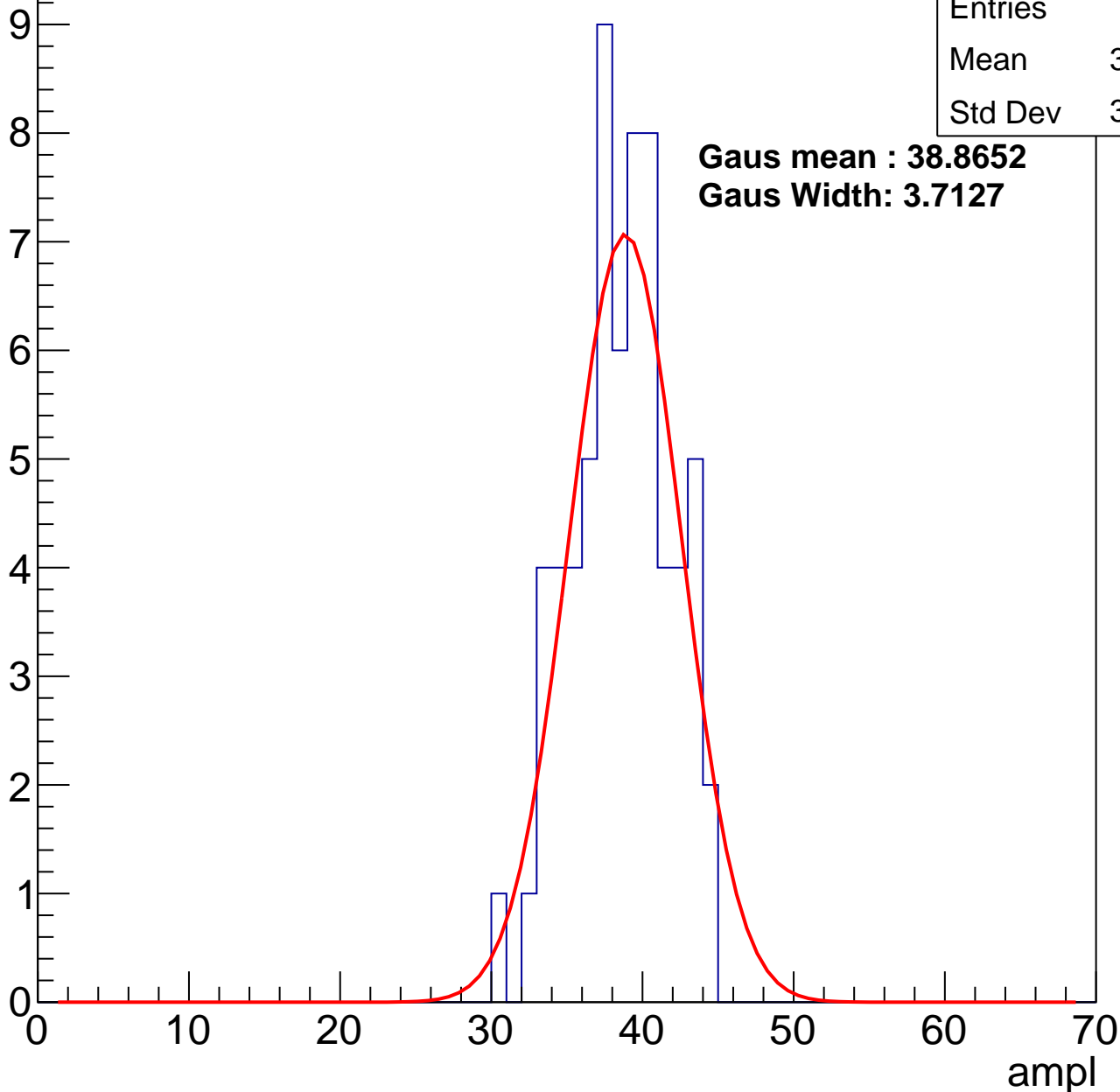
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	38.12
Std Dev	3.199

Gaus mean : 38.8652

Gaus Width: 3.7127



B0L002S, U12-ch32, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	45.14
Std Dev	3.19

Gaus mean : 45.7127

Gaus Width: 3.4813

10

8

6

4

2

0

0

10

20

30

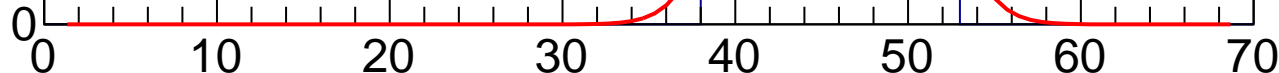
40

50

60

70

ampl

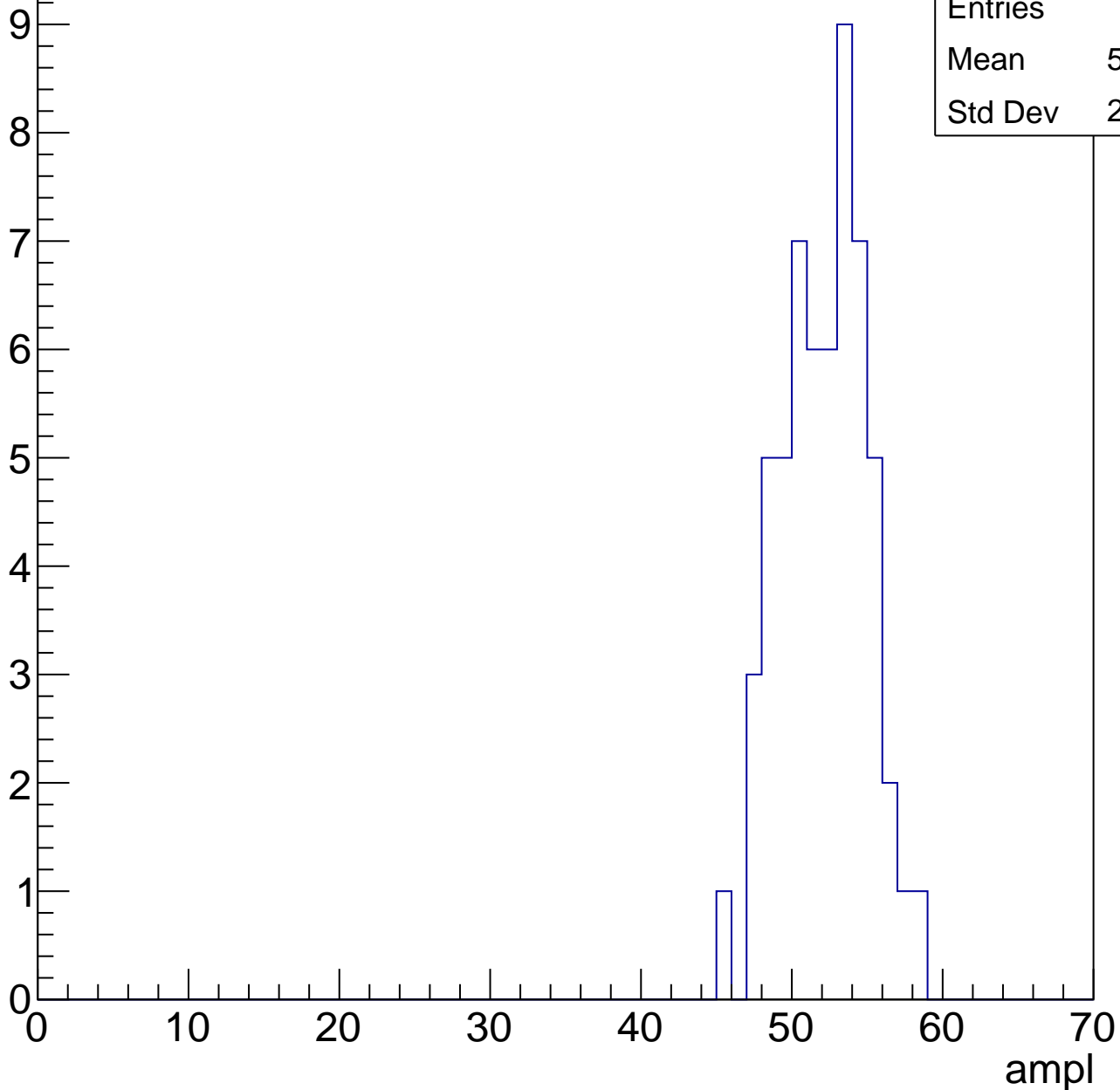


B0L002S, U12-ch32, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	51.66
Std Dev	2.789

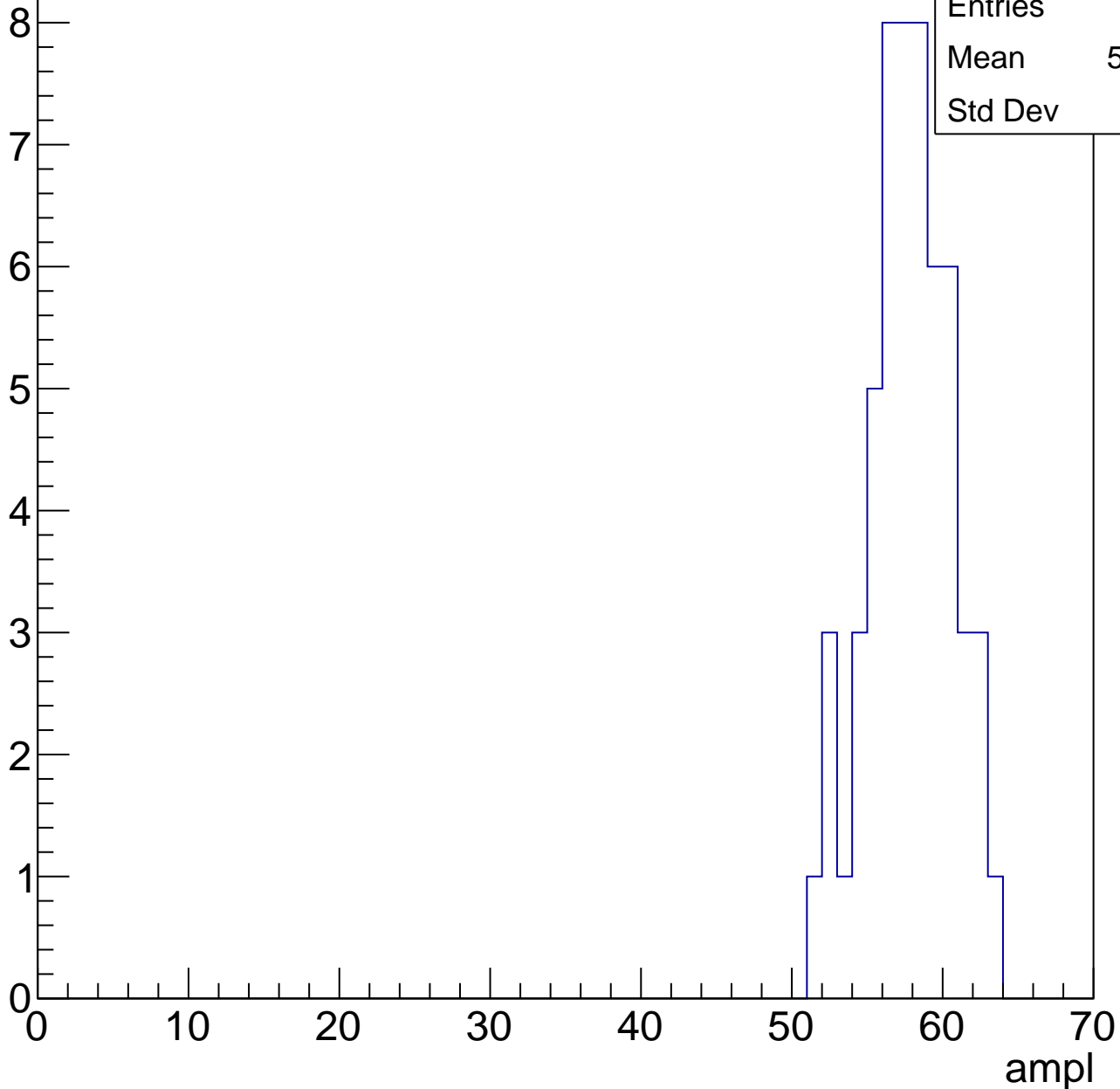


B0L002S, U12-ch32, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

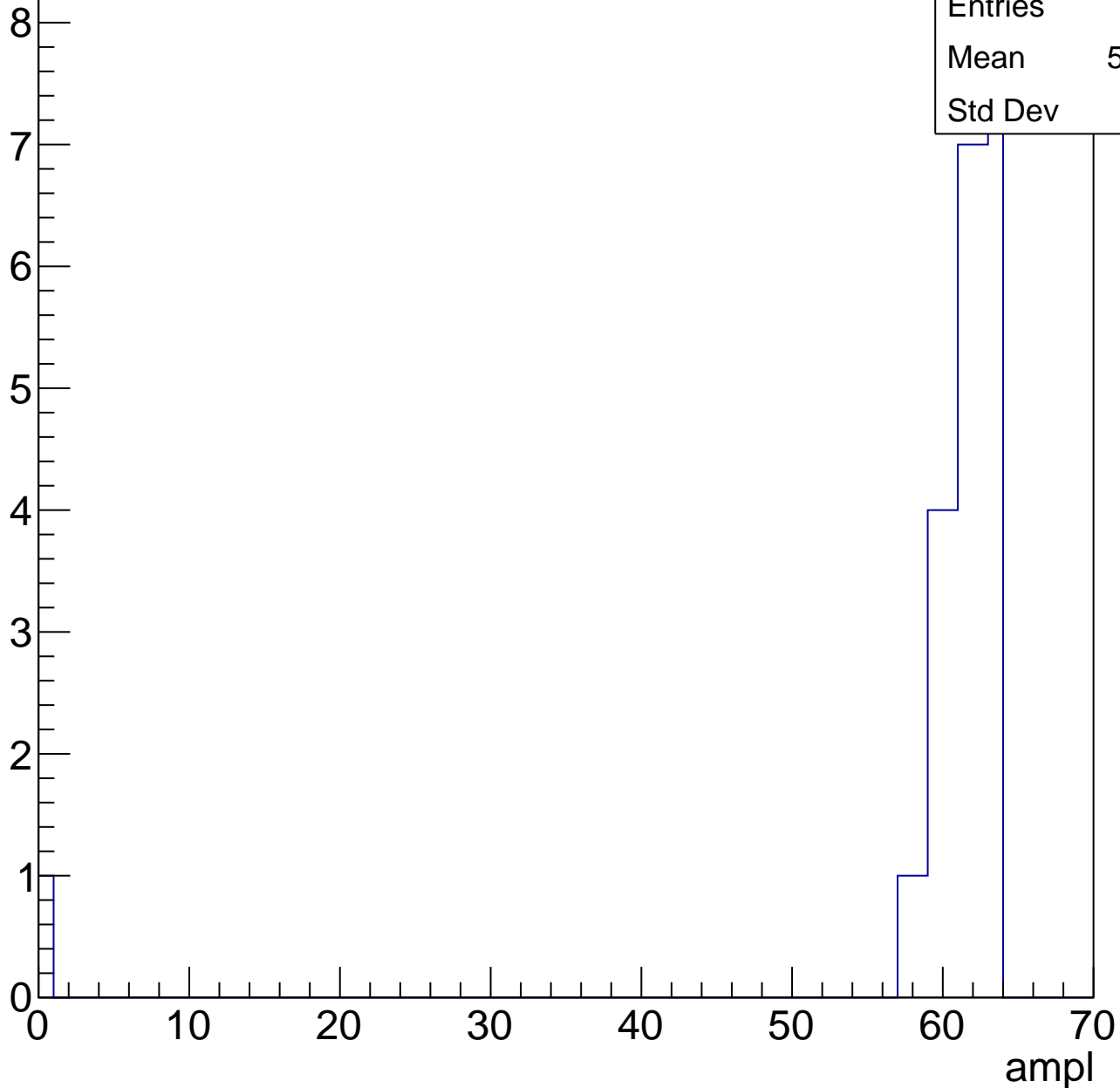
Entries	56
Mean	57.34
Std Dev	2.74



B0L002S, U12-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L002S, U12-ch33, adc0

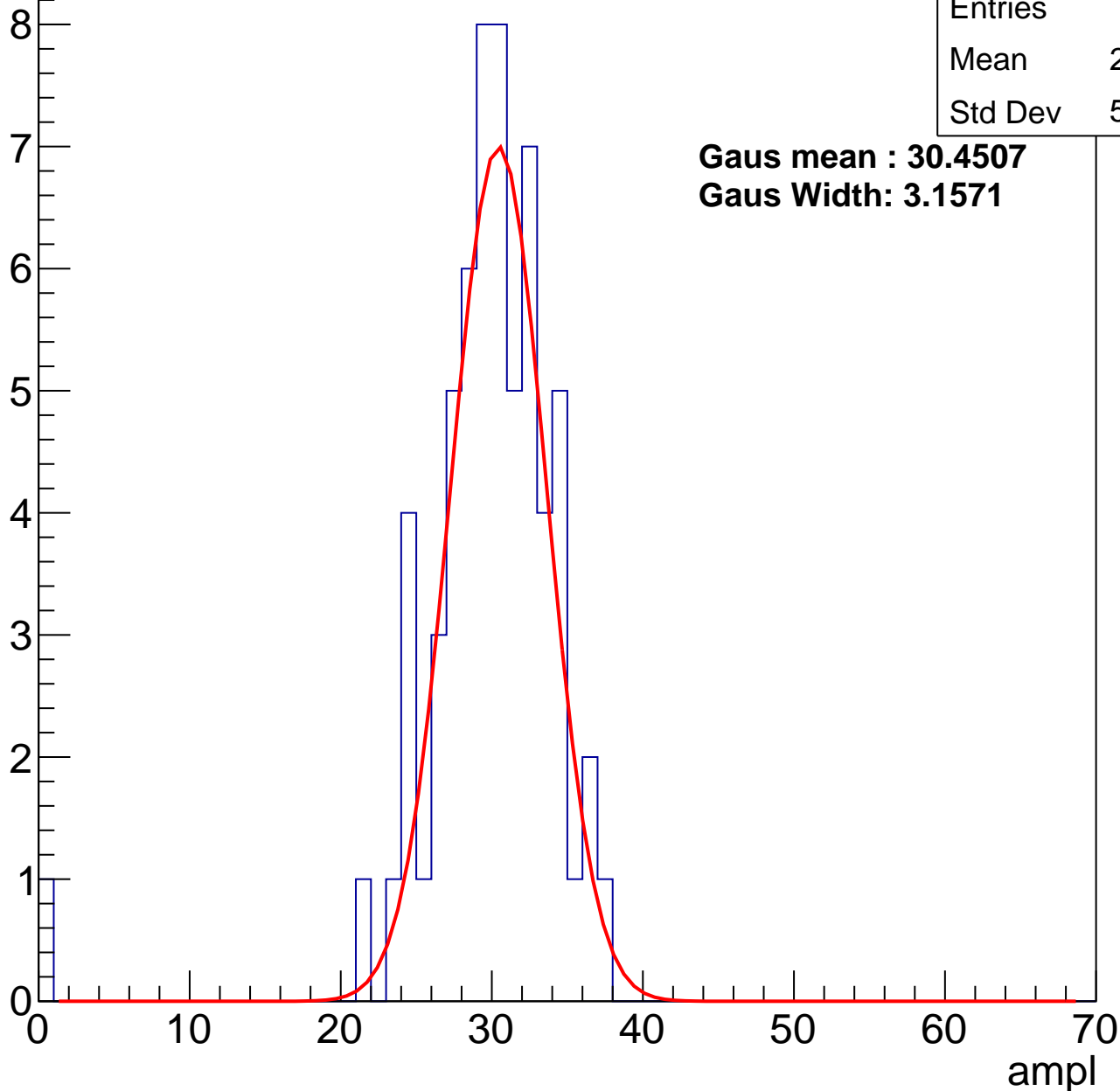
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.25
Std Dev	5.014

Gaus mean : 30.4507

Gaus Width: 3.1571

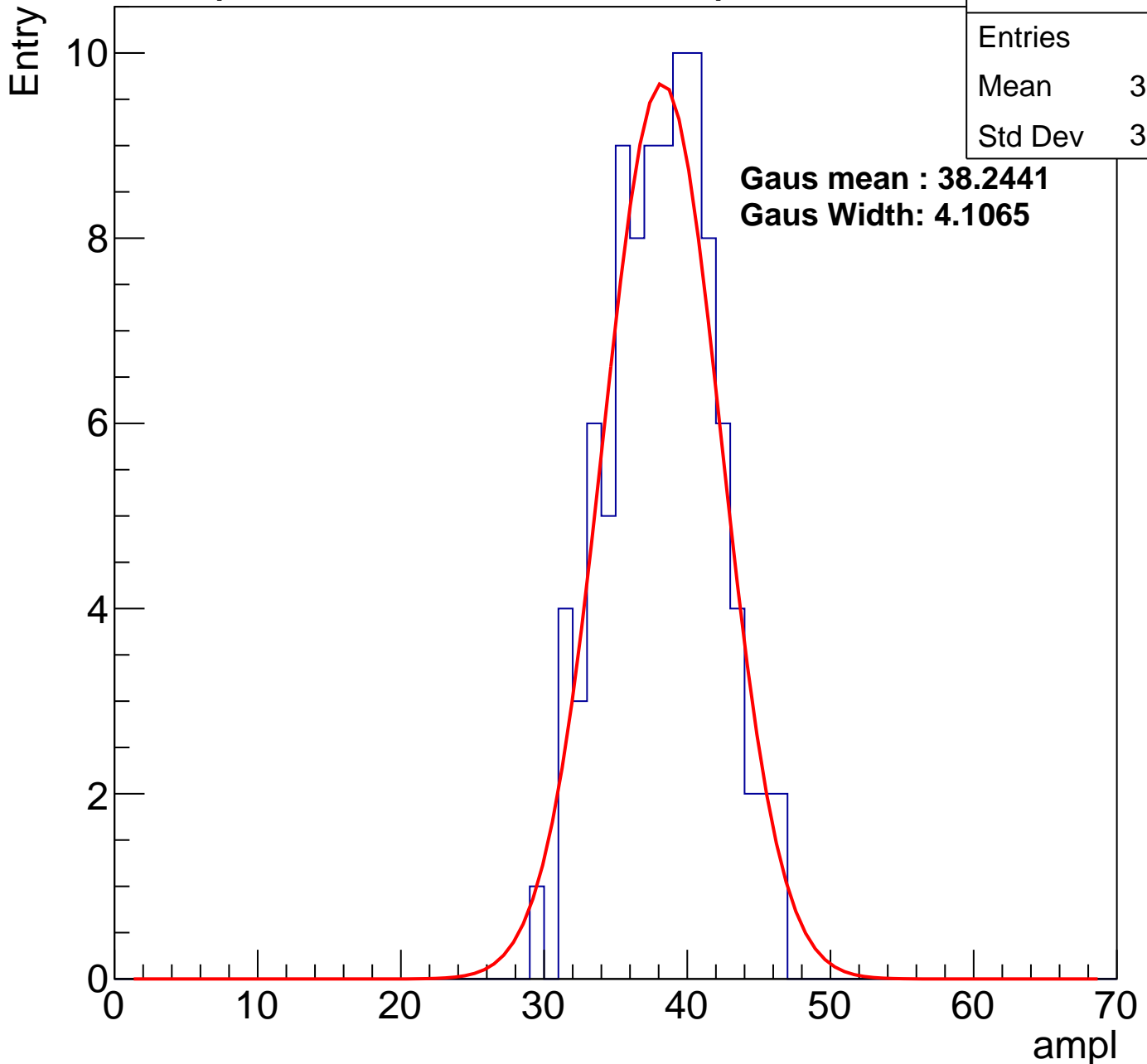


B0L002S, U12-ch33, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	98
Mean	37.83
Std Dev	3.712

Gaus mean : 38.2441
Gaus Width: 4.1065



B0L002S, U12-ch33, adc2

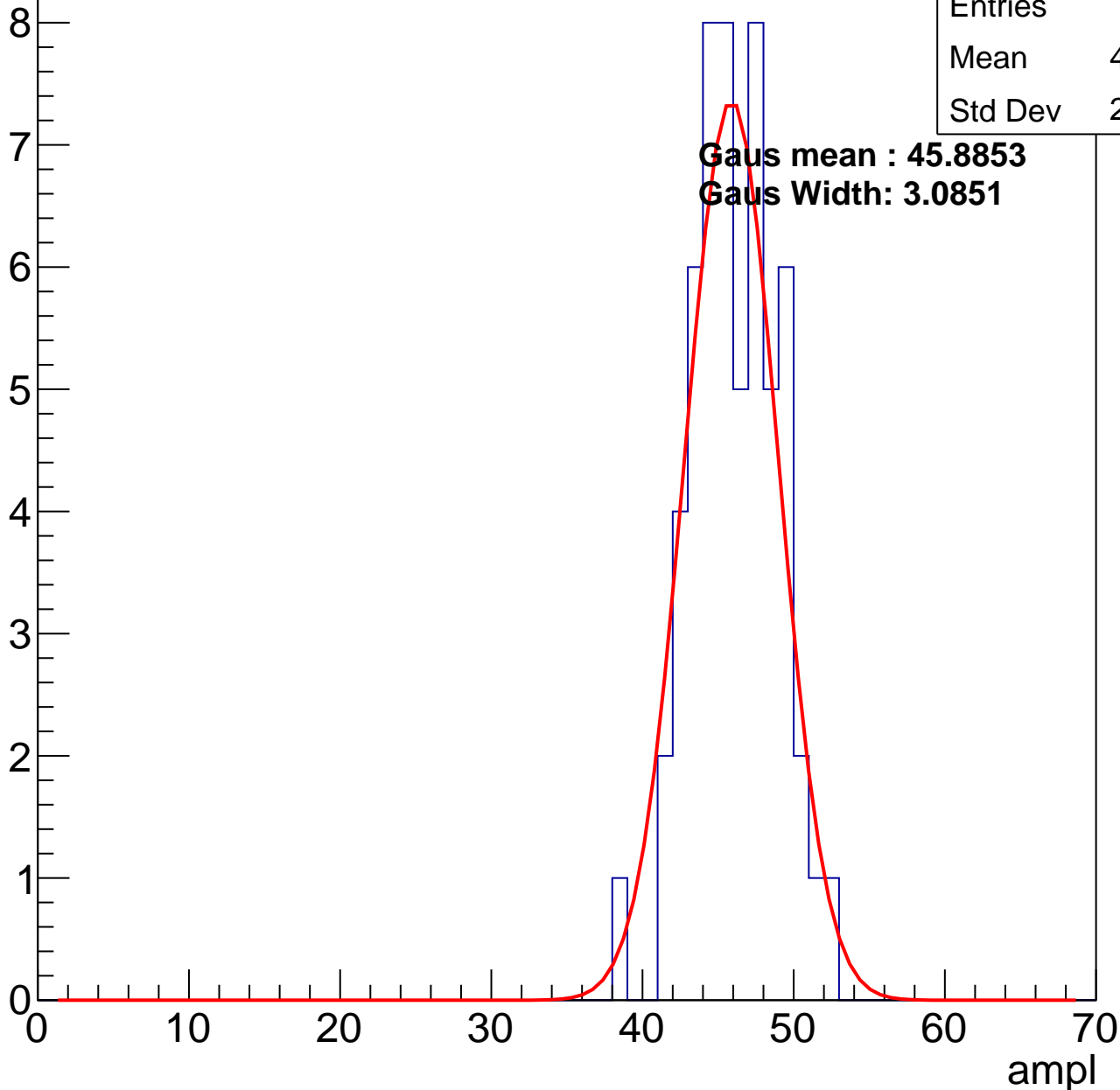
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	45.63
Std Dev	2.776

Gaus mean : 45.8853

Gaus Width: 3.0851

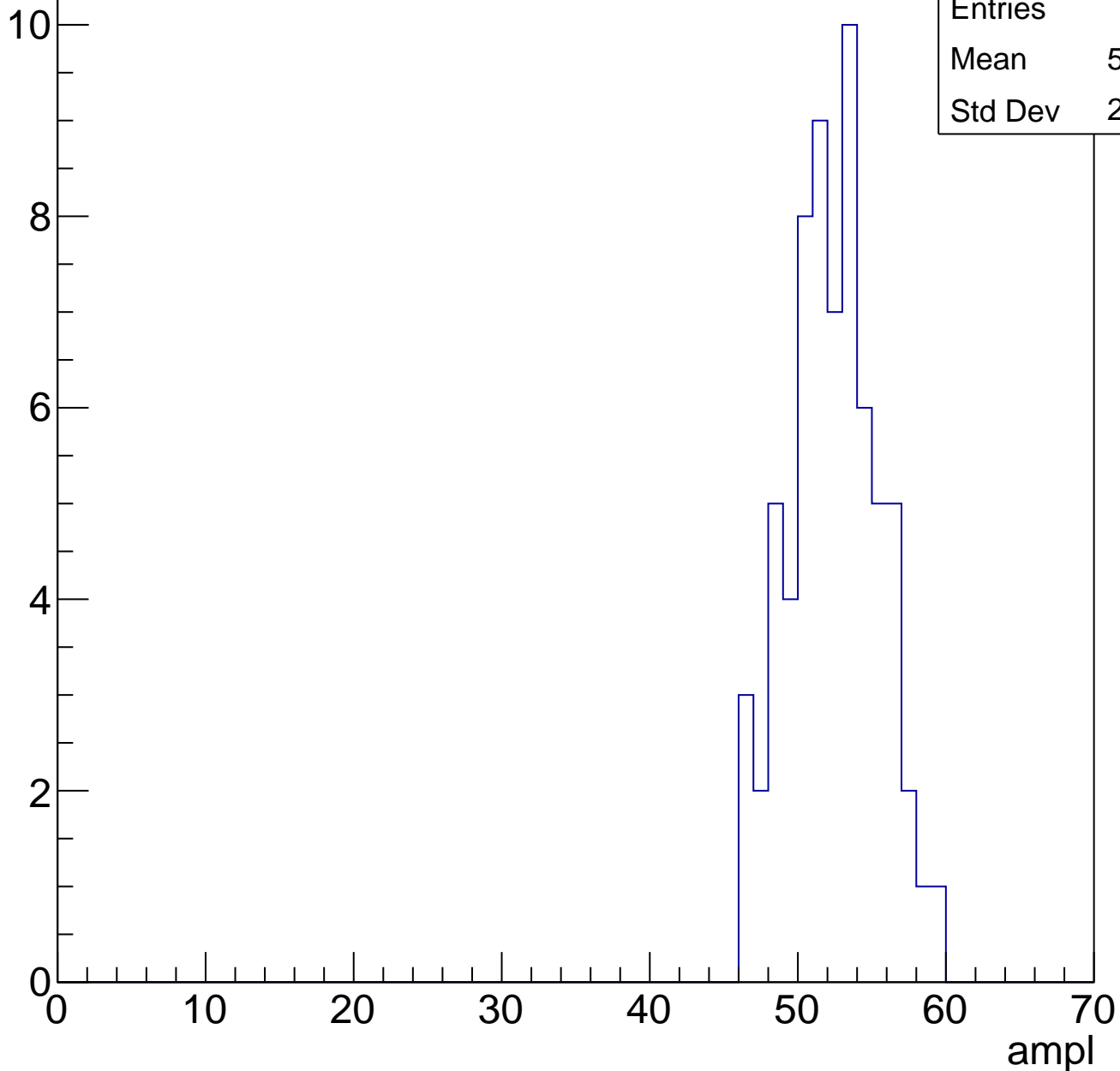


B0L002S, U12-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	51.93
Std Dev	2.992

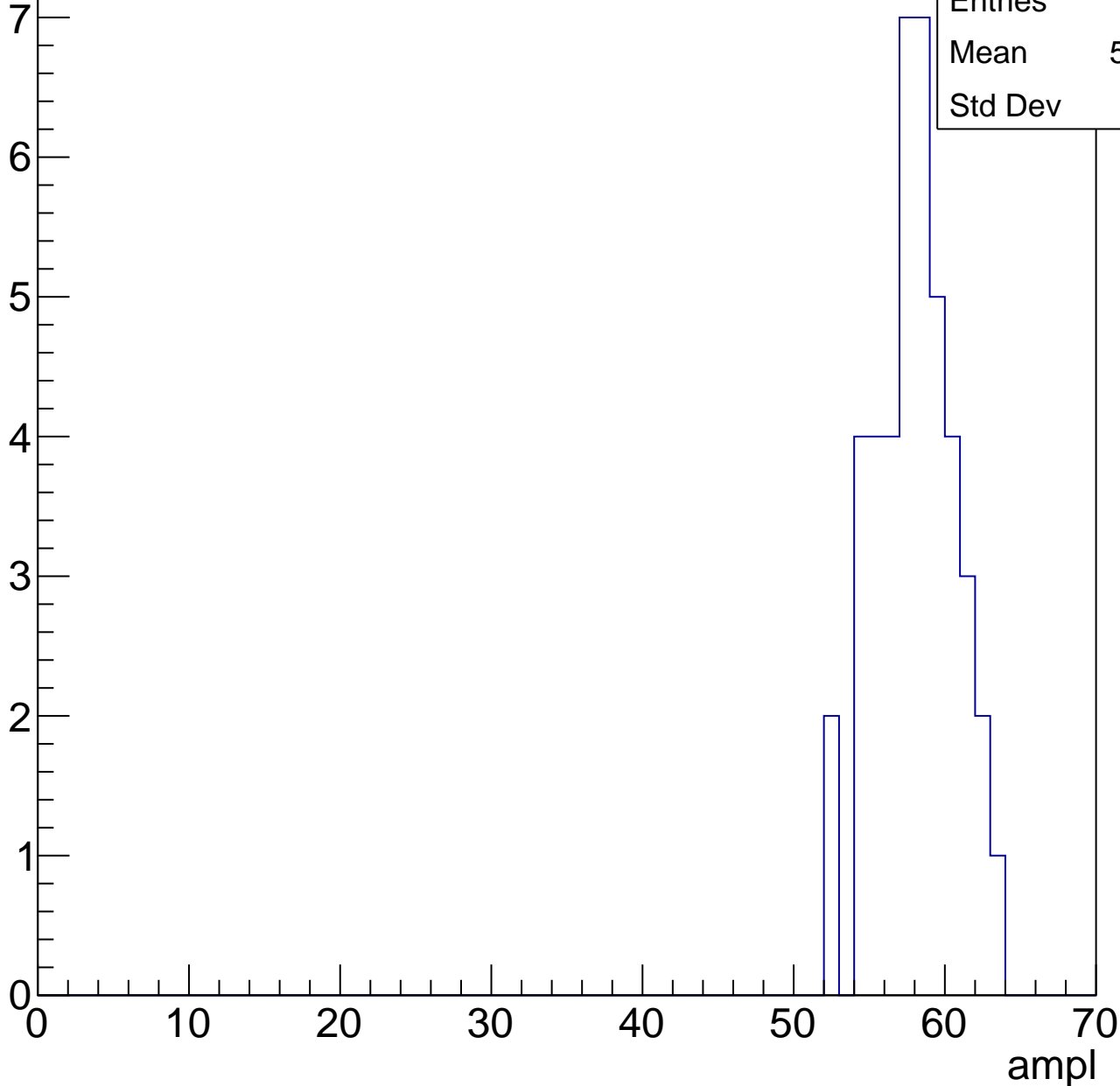


B0L002S, U12-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	57.53
Std Dev	2.6

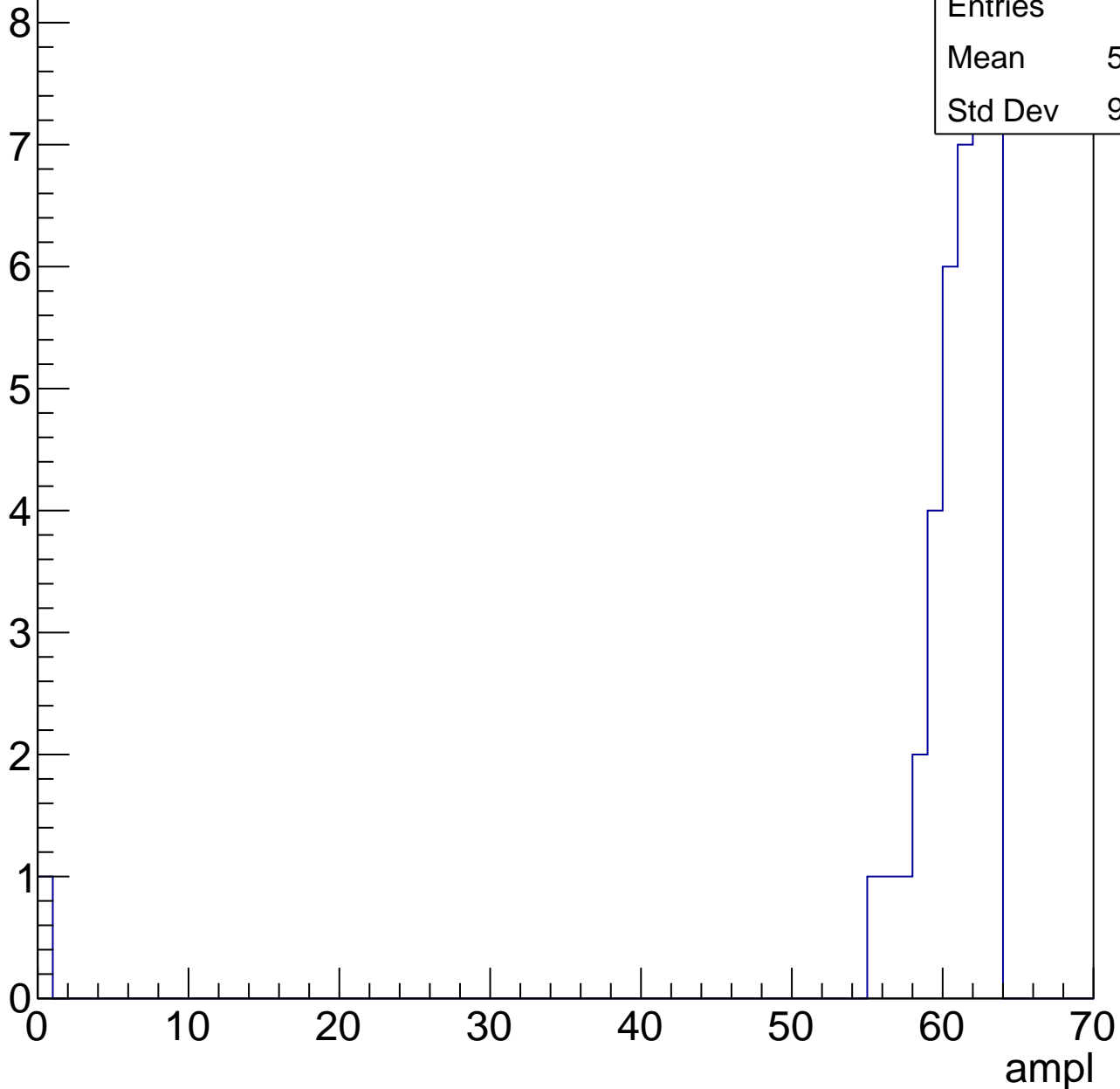


B0L002S, U12-ch33, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.15
Std Dev	9.799



B0L002S, U12-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch34, adc0

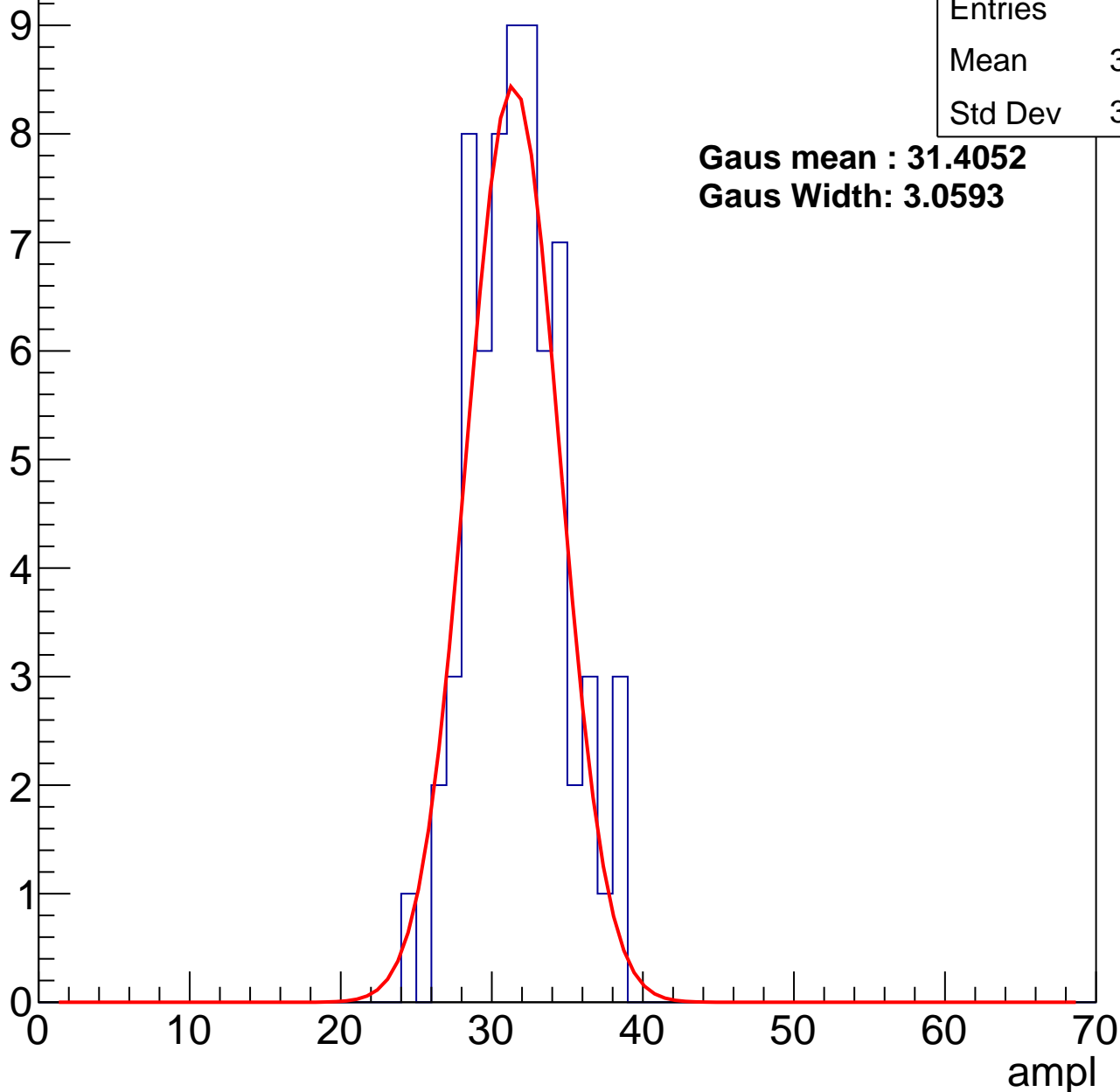
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.28
Std Dev	3.048

Gaus mean : 31.4052

Gaus Width: 3.0593



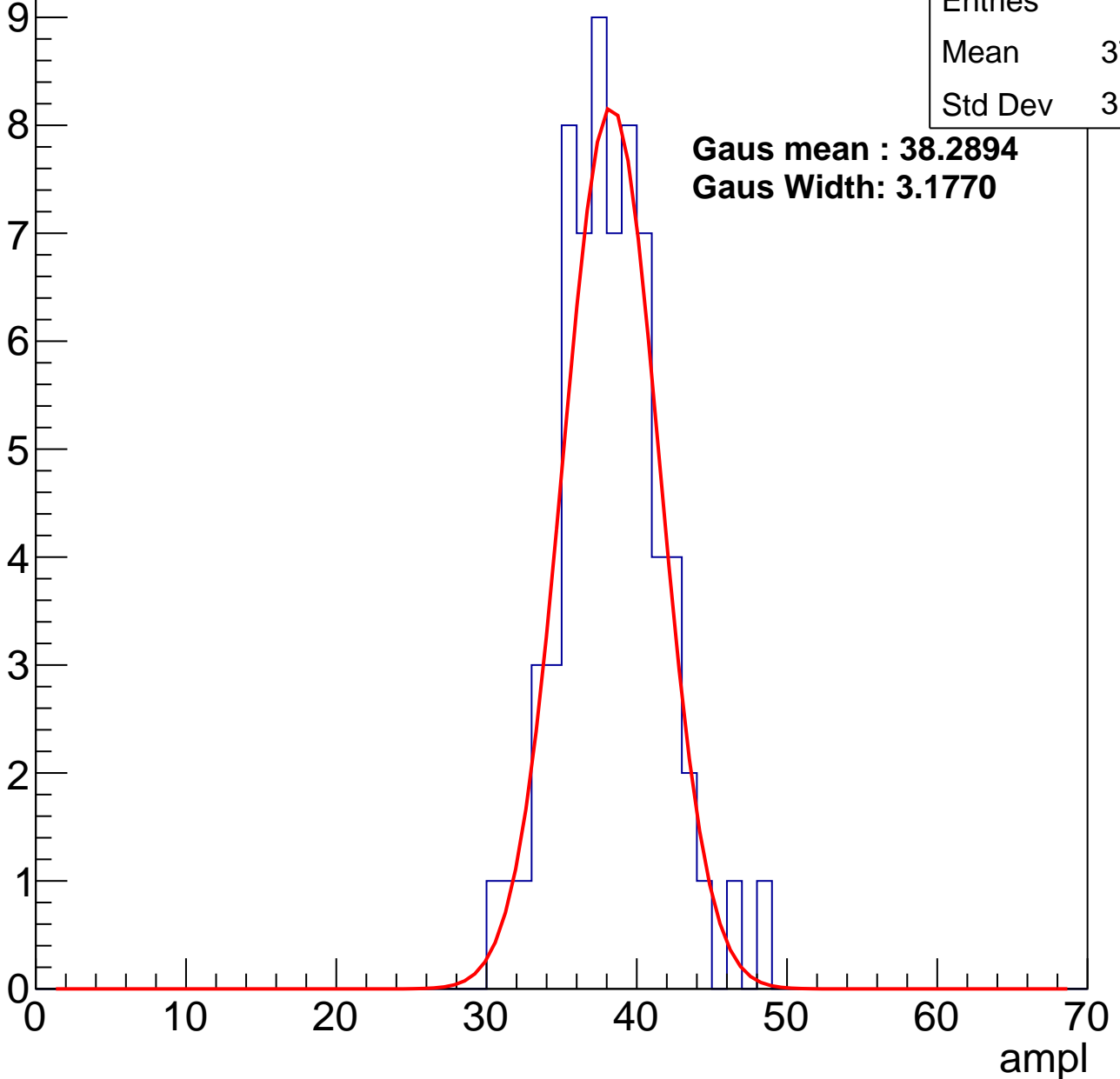
B0L002S, U12-ch34, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.84
Std Dev	3.355

Gaus mean : 38.2894
Gaus Width: 3.1770



B0L002S, U12-ch34, adc2

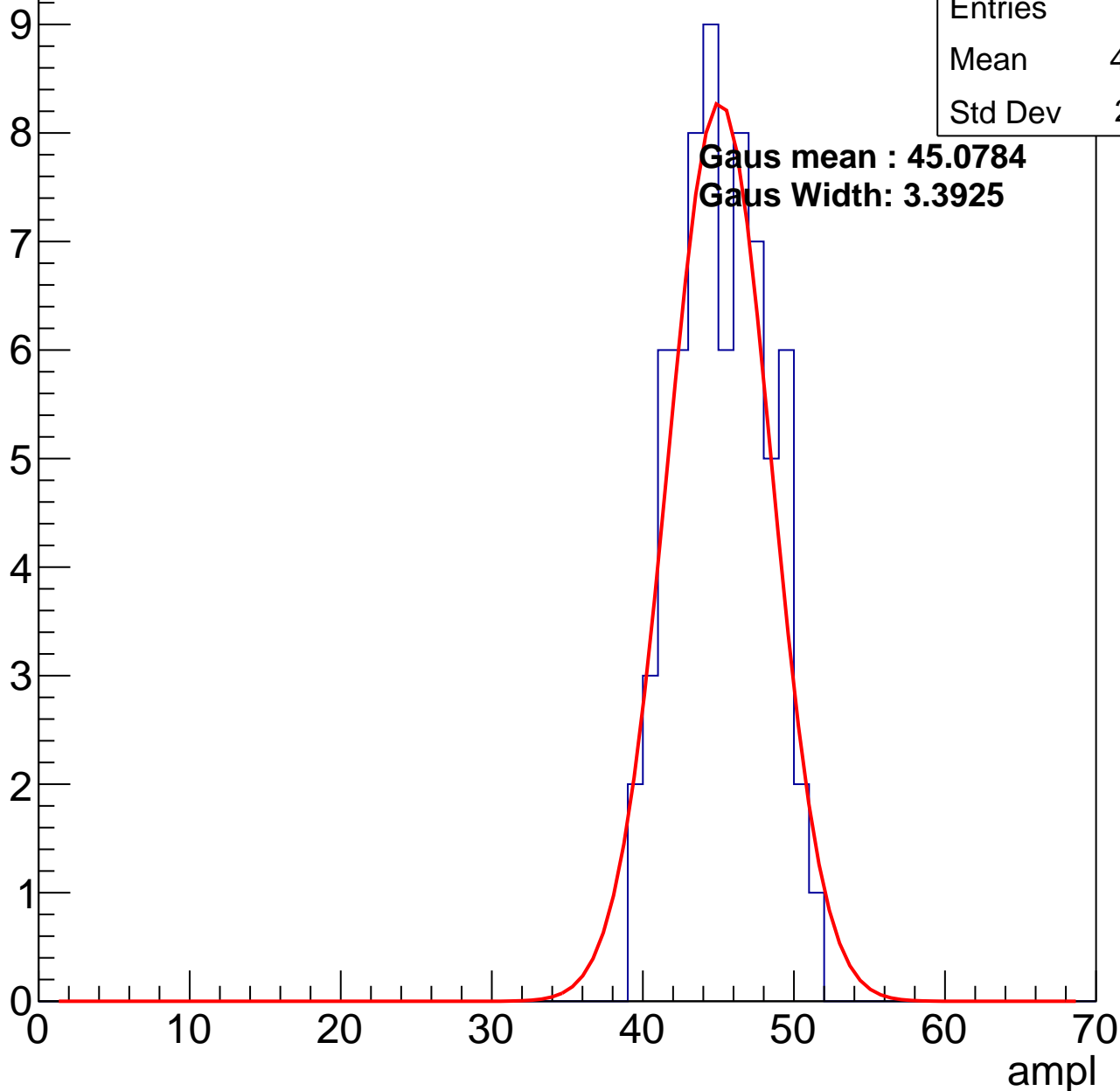
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.75
Std Dev	2.941

Gaus mean : 45.0784

Gaus Width: 3.3925

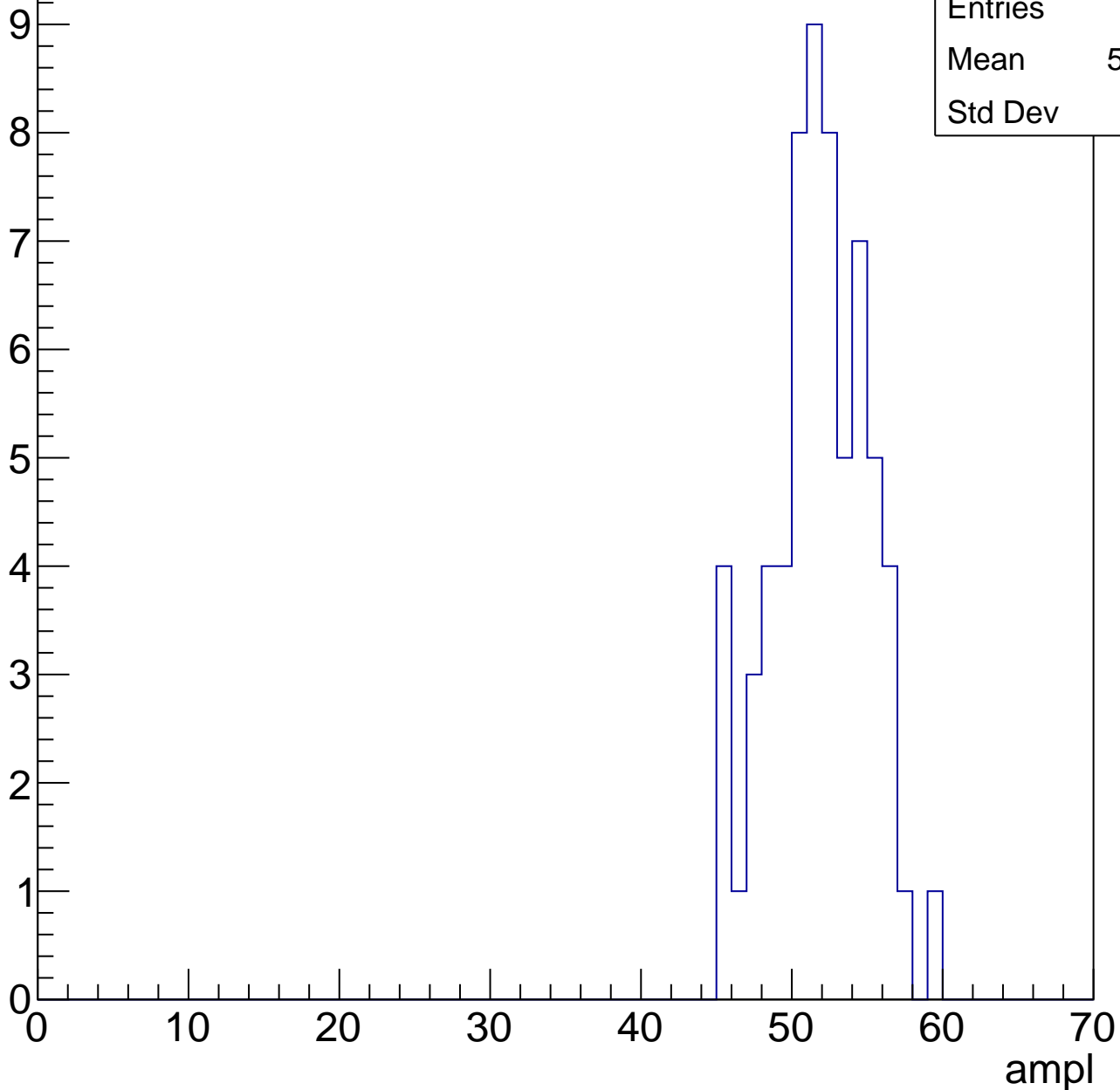


B0L002S, U12-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

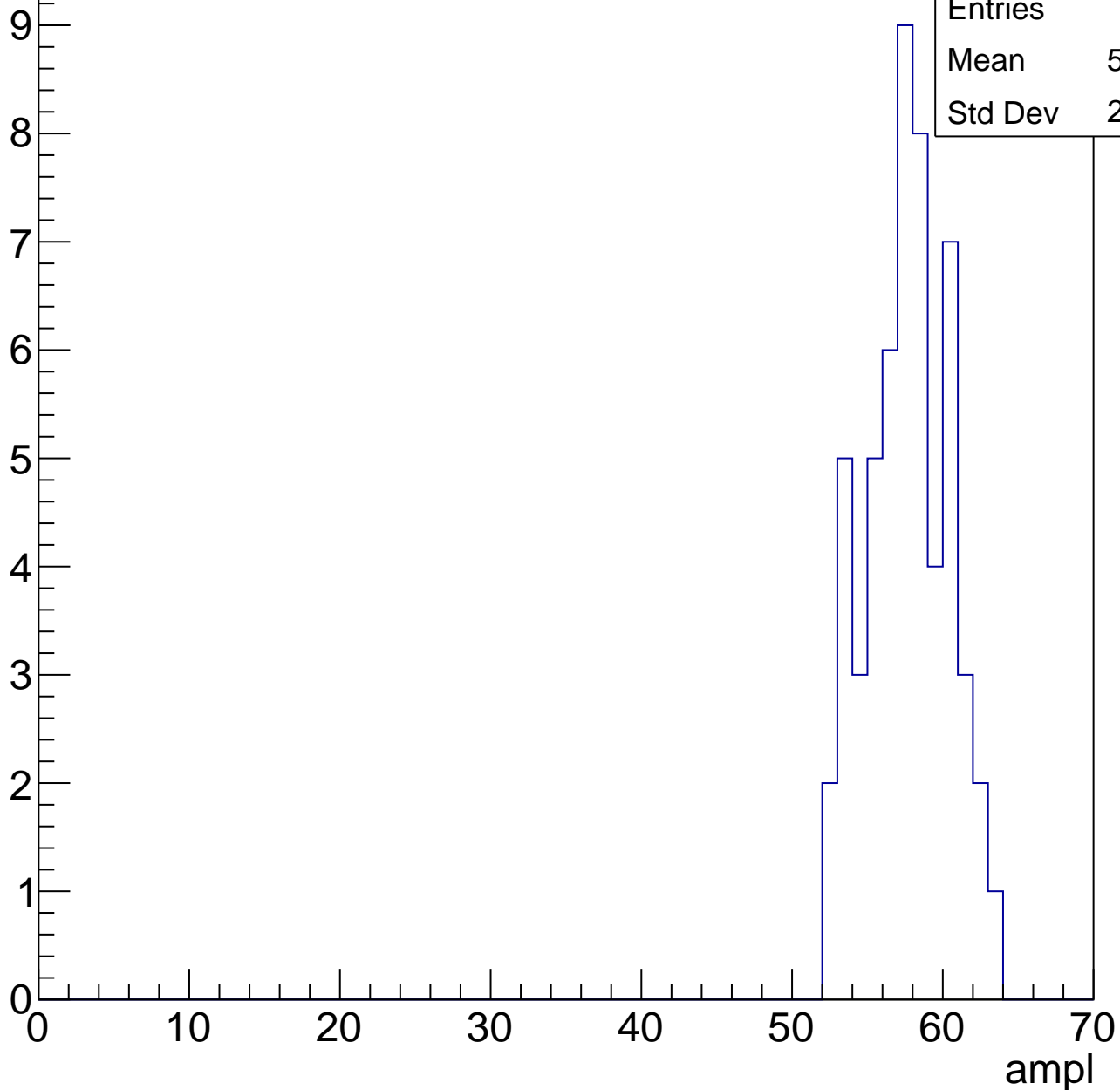
Entries	64
Mean	51.38
Std Dev	3.16



B0L002S, U12-ch34, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



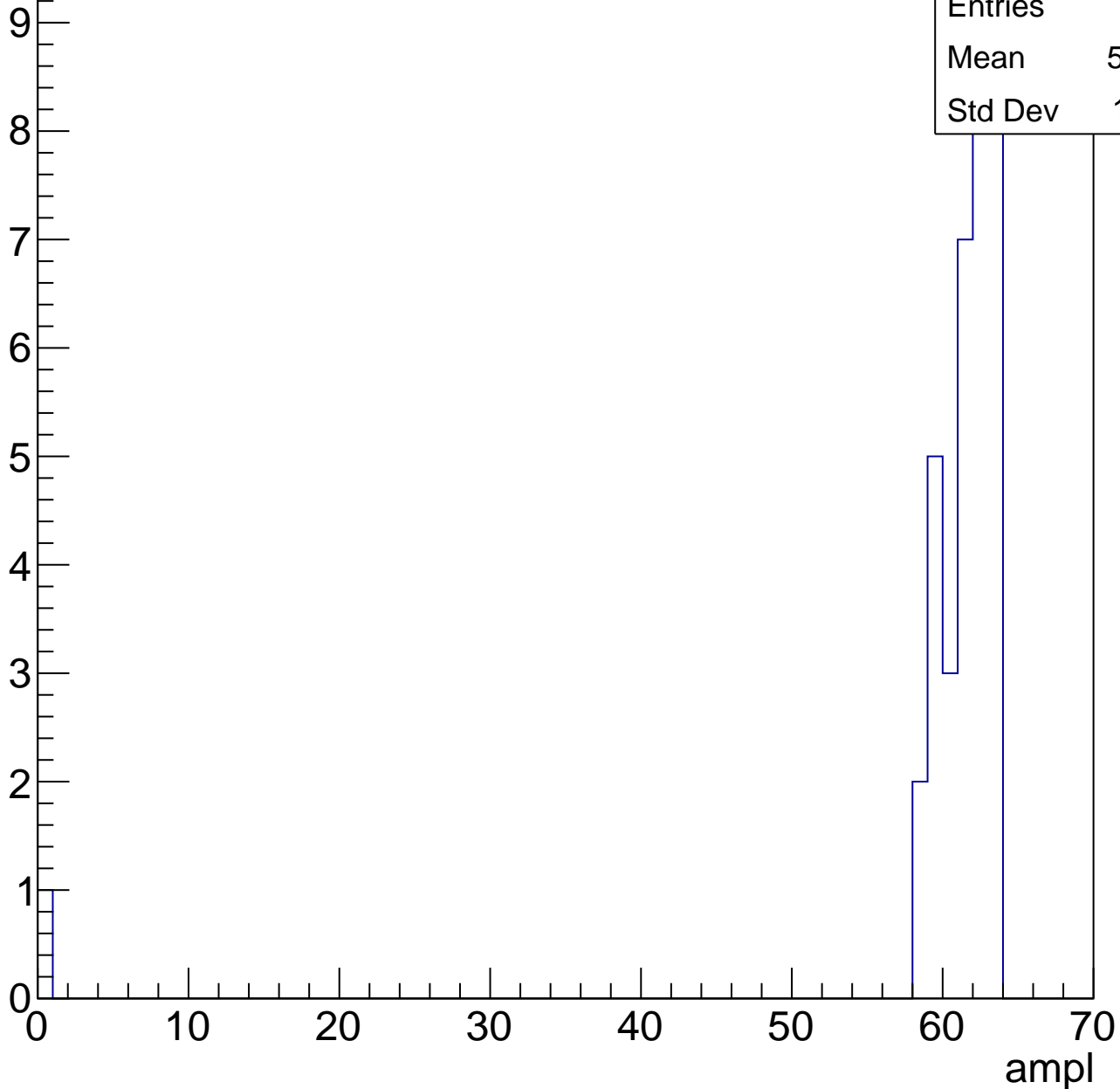
Entries	55
Mean	57.18
Std Dev	2.704

B0L002S, U12-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.46
Std Dev	10.31



B0L002S, U12-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch35, adc0

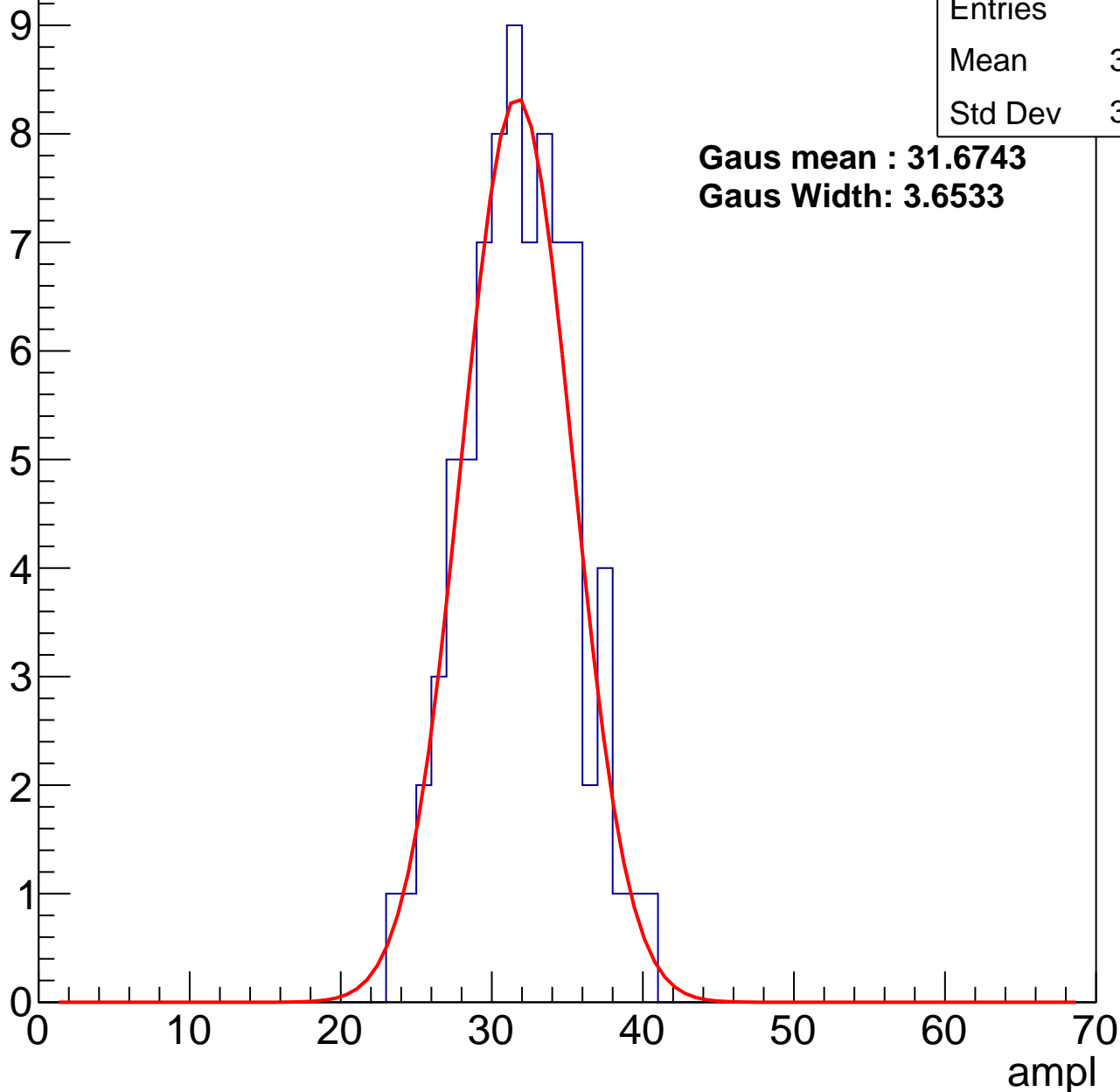
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	31.39
Std Dev	3.577

Gaus mean : 31.6743

Gaus Width: 3.6533



B0L002S, U12-ch35, adc1

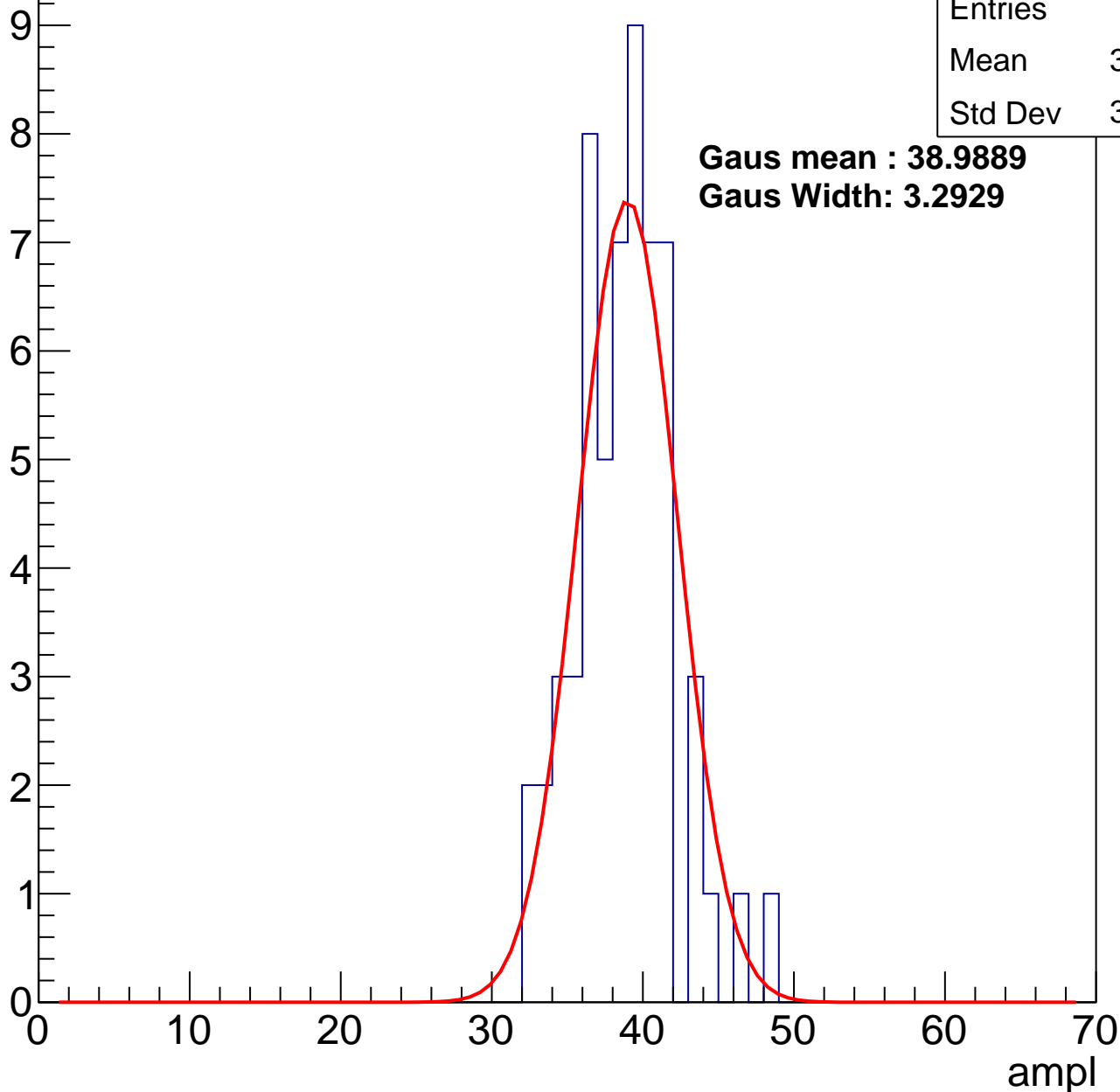
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	38.32
Std Dev	3.197

Gaus mean : 38.9889

Gaus Width: 3.2929



B0L002S, U12-ch35, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	44.61
Std Dev	3.049

Gaus mean : 45.1911

Gaus Width: 3.3861

Entry

10

8

6

4

2

0

0

10

20

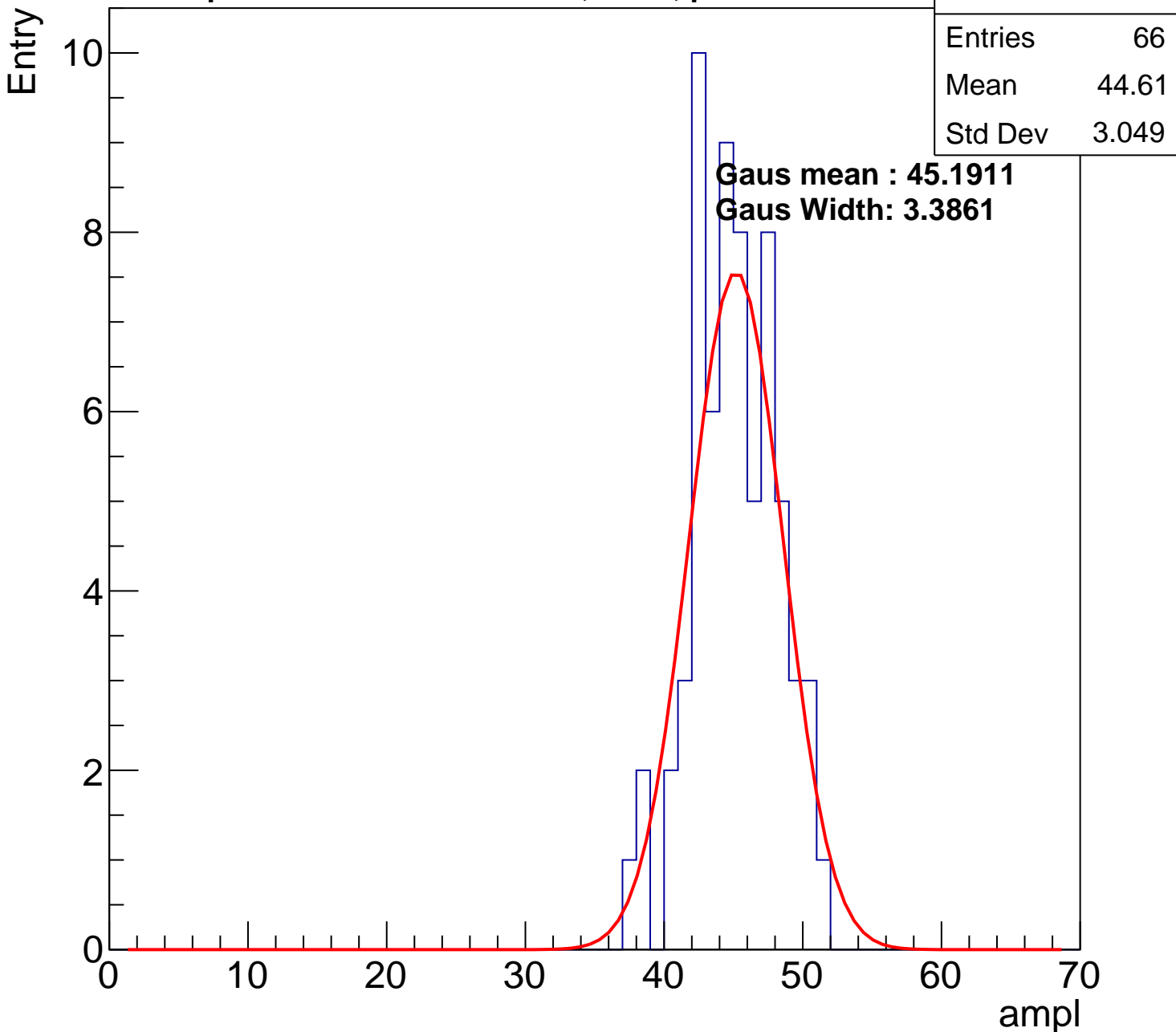
30

40

50

60

ampl



B0L002S, U12-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	51.27
Std Dev	3.211

Entry

10

8

6

4

2

0

0

10

20

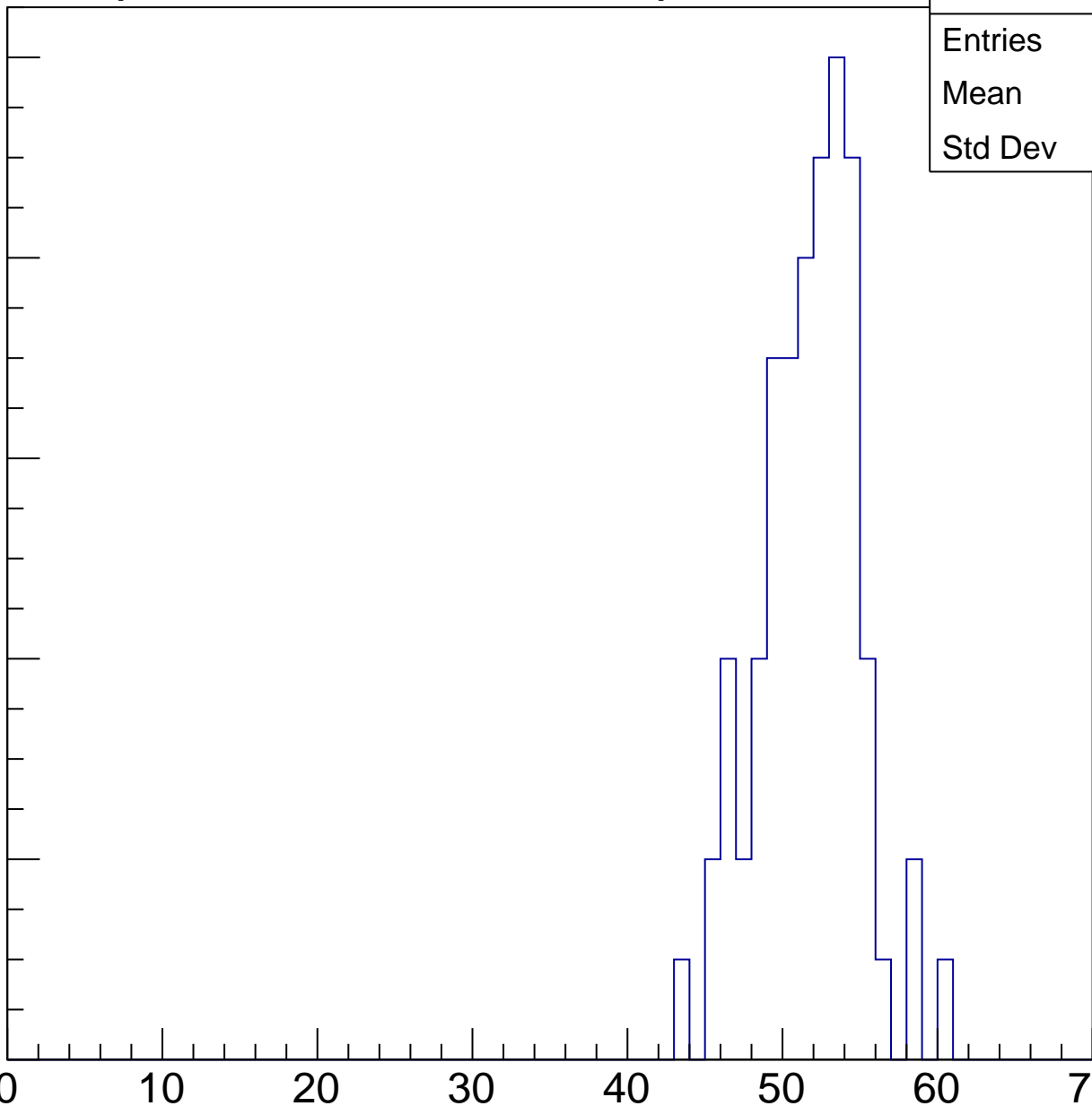
30

40

50

60

ampl

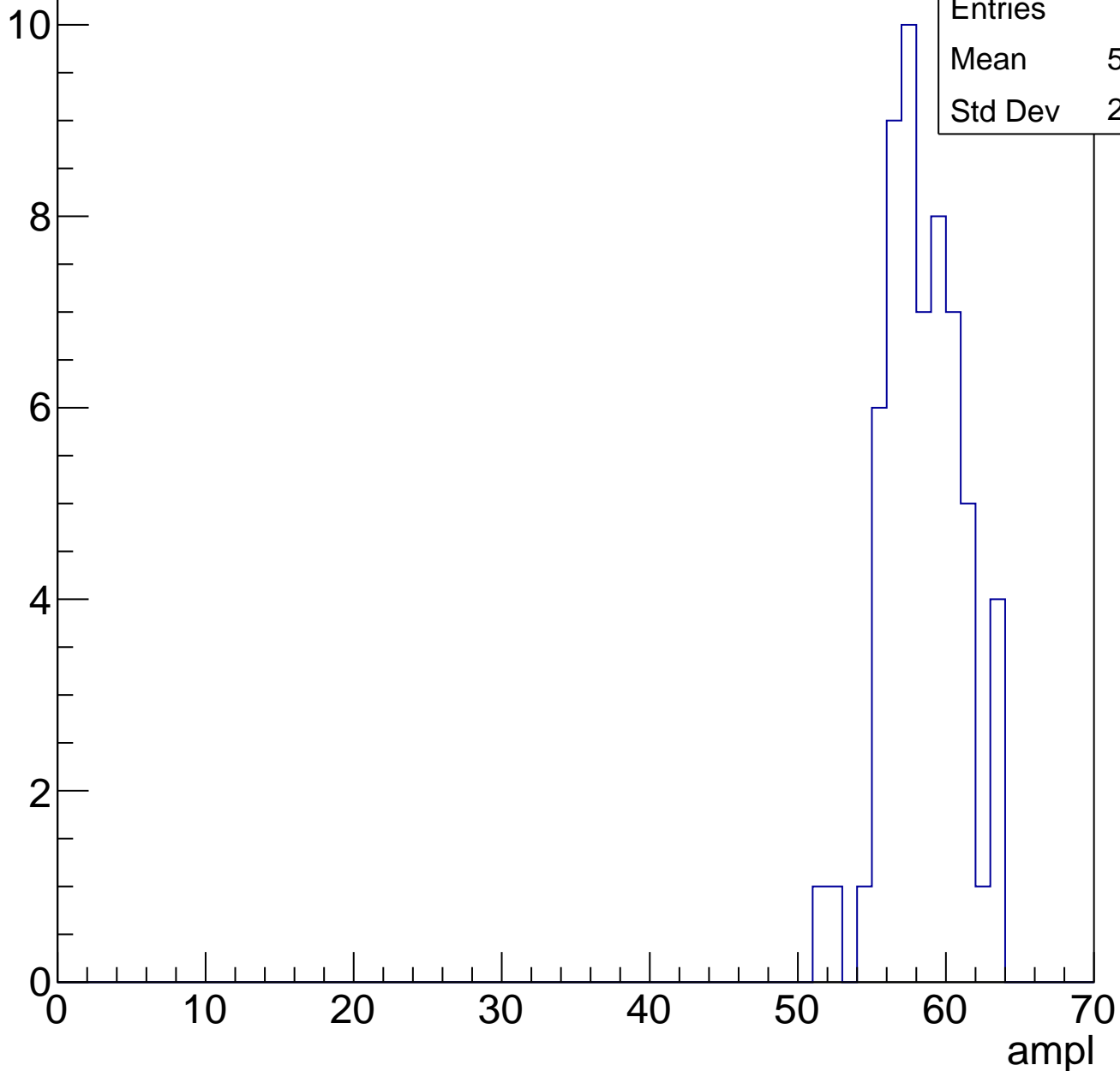


B0L002S, U12-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	57.97
Std Dev	2.575

Entry

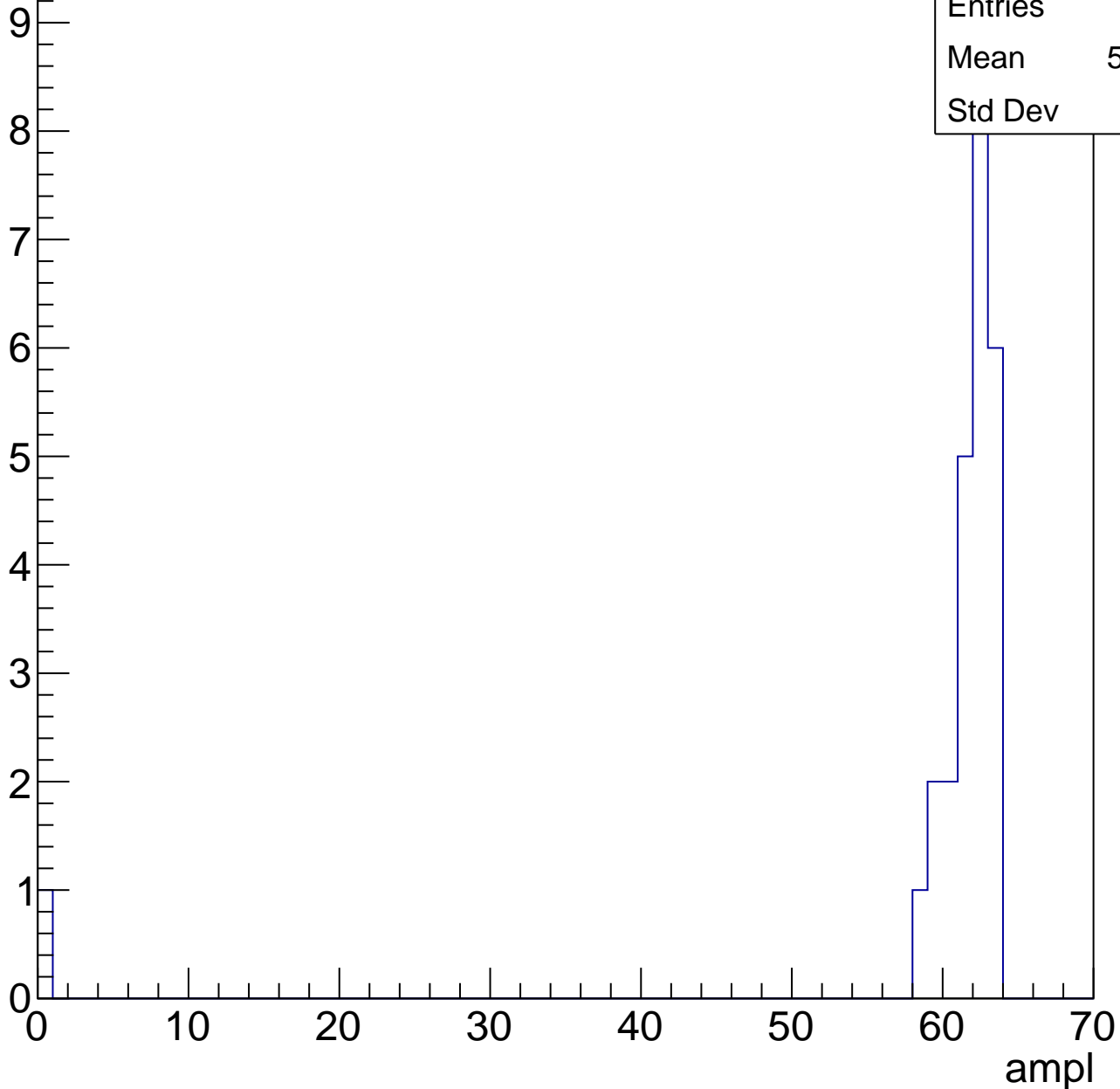


B0L002S, U12-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	59.12
Std Dev	11.9



B0L002S, U12-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U12-ch36, adc0

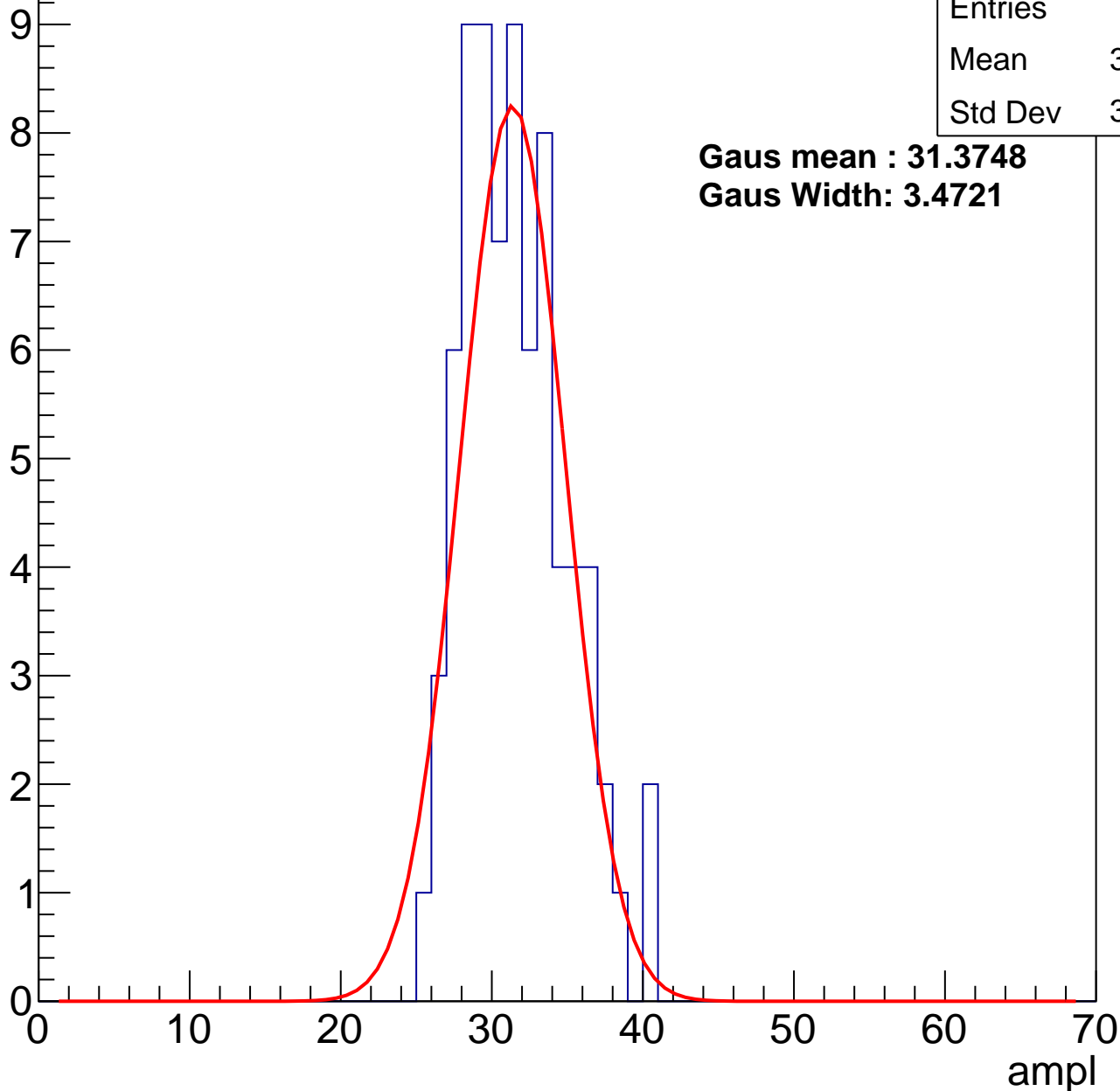
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.13
Std Dev	3.368

Gaus mean : 31.3748

Gaus Width: 3.4721



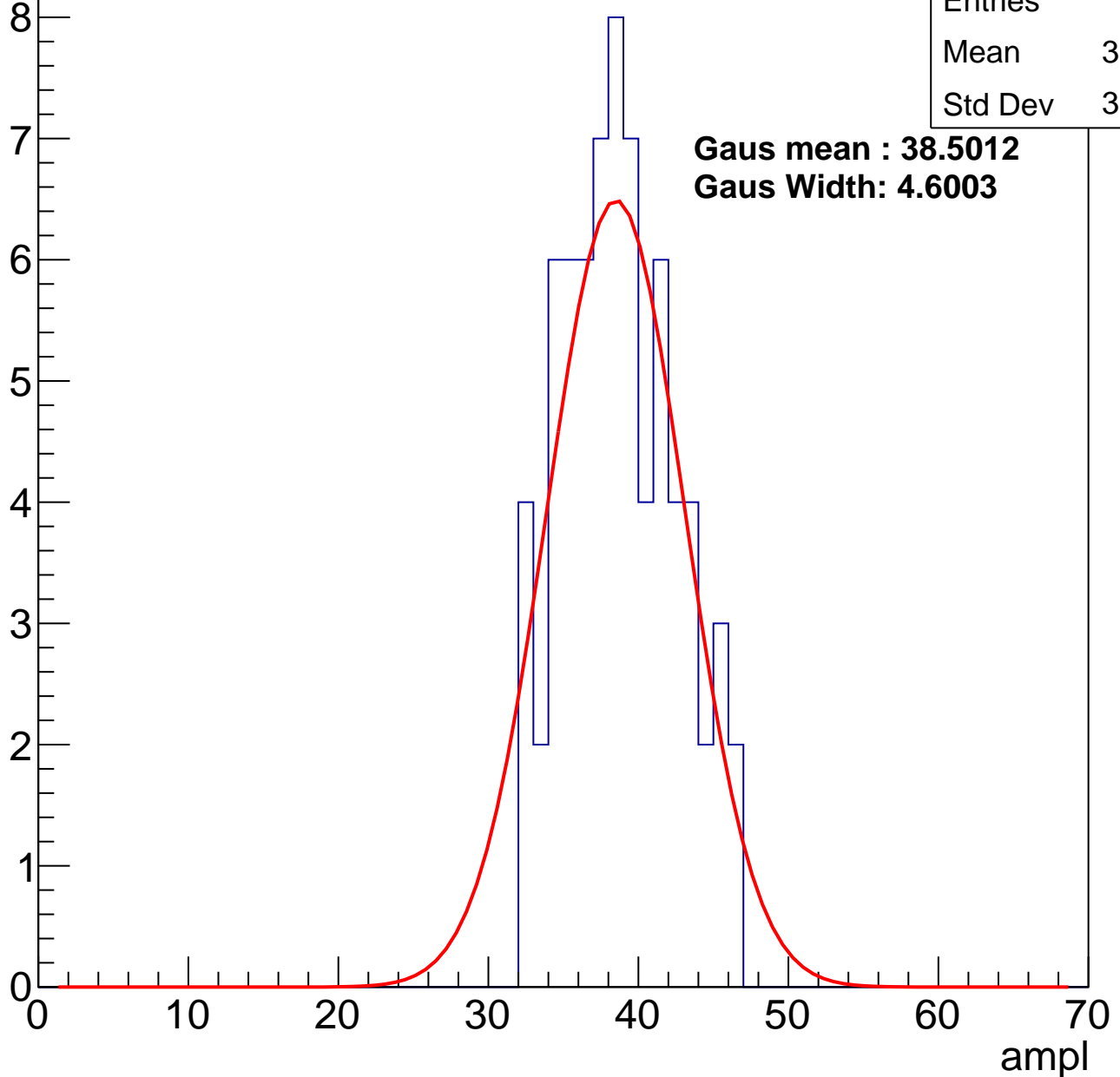
B0L002S, U12-ch36, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38.32
Std Dev	3.669

Gaus mean : 38.5012
Gaus Width: 4.6003



B0L002S, U12-ch36, adc2

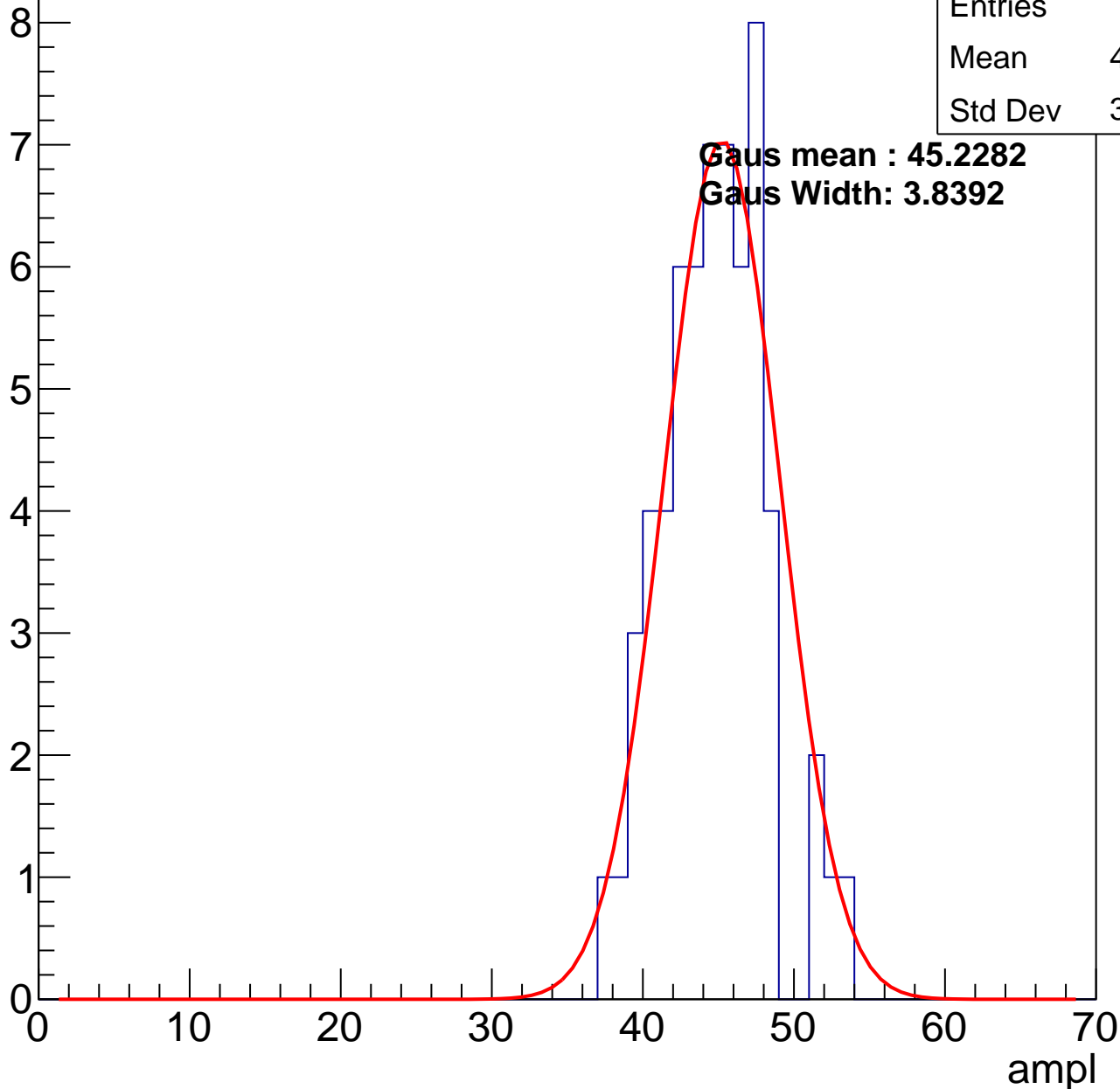
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.26
Std Dev	3.377

Gaus mean : 45.2282

Gaus Width: 3.8392

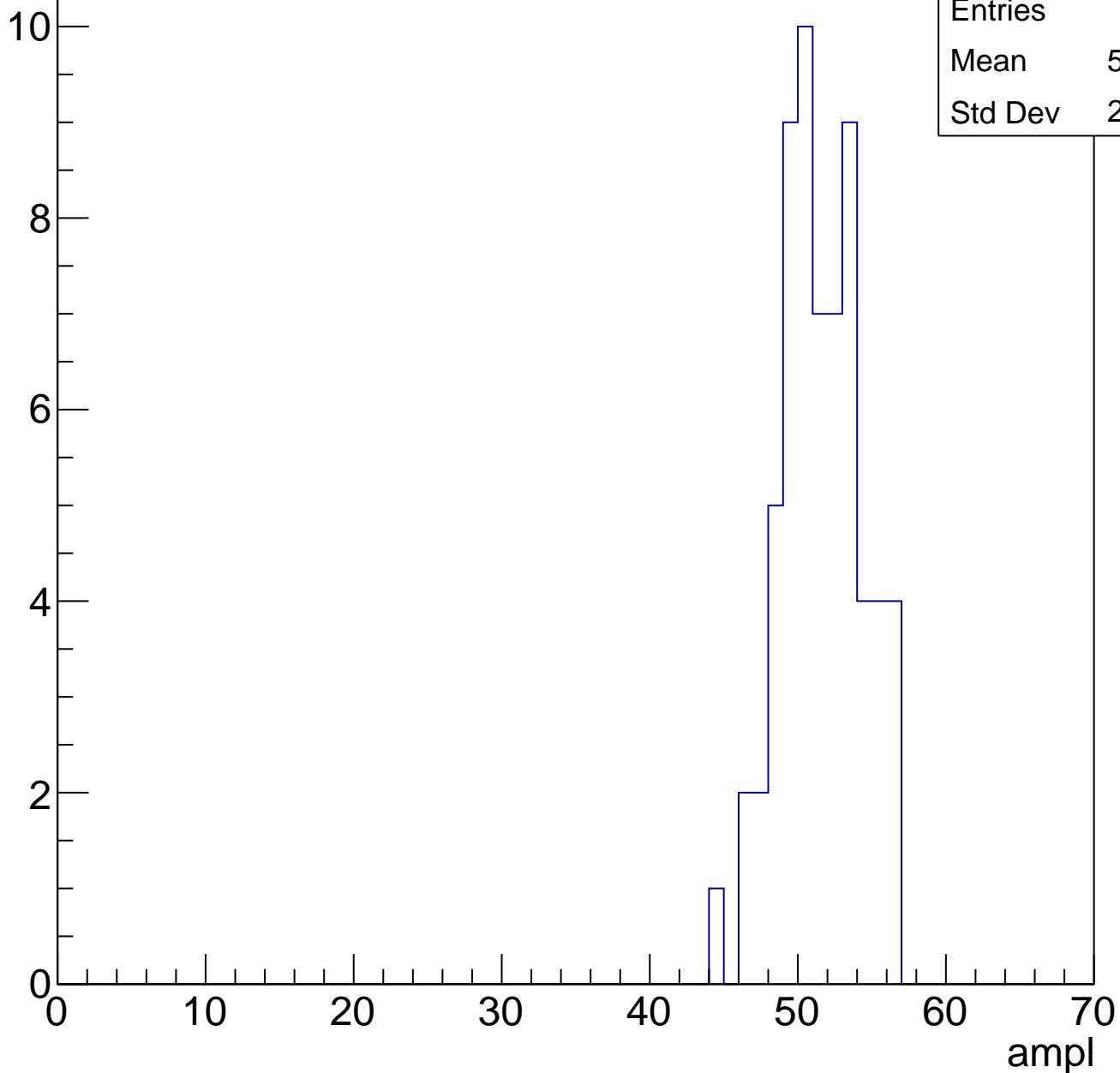


B0L002S, U12-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

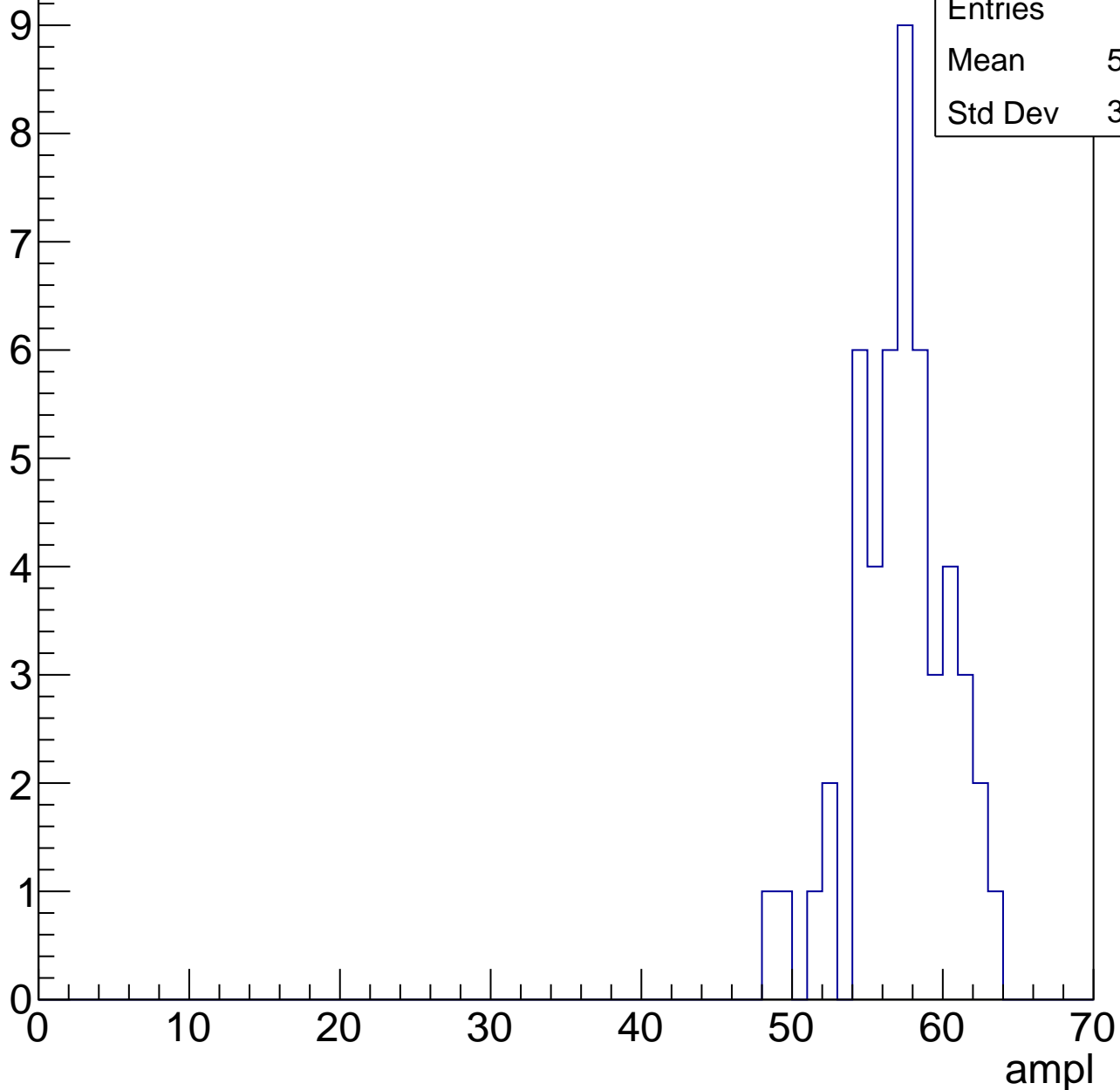
Entries	64
Mean	51.08
Std Dev	2.694



B0L002S, U12-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

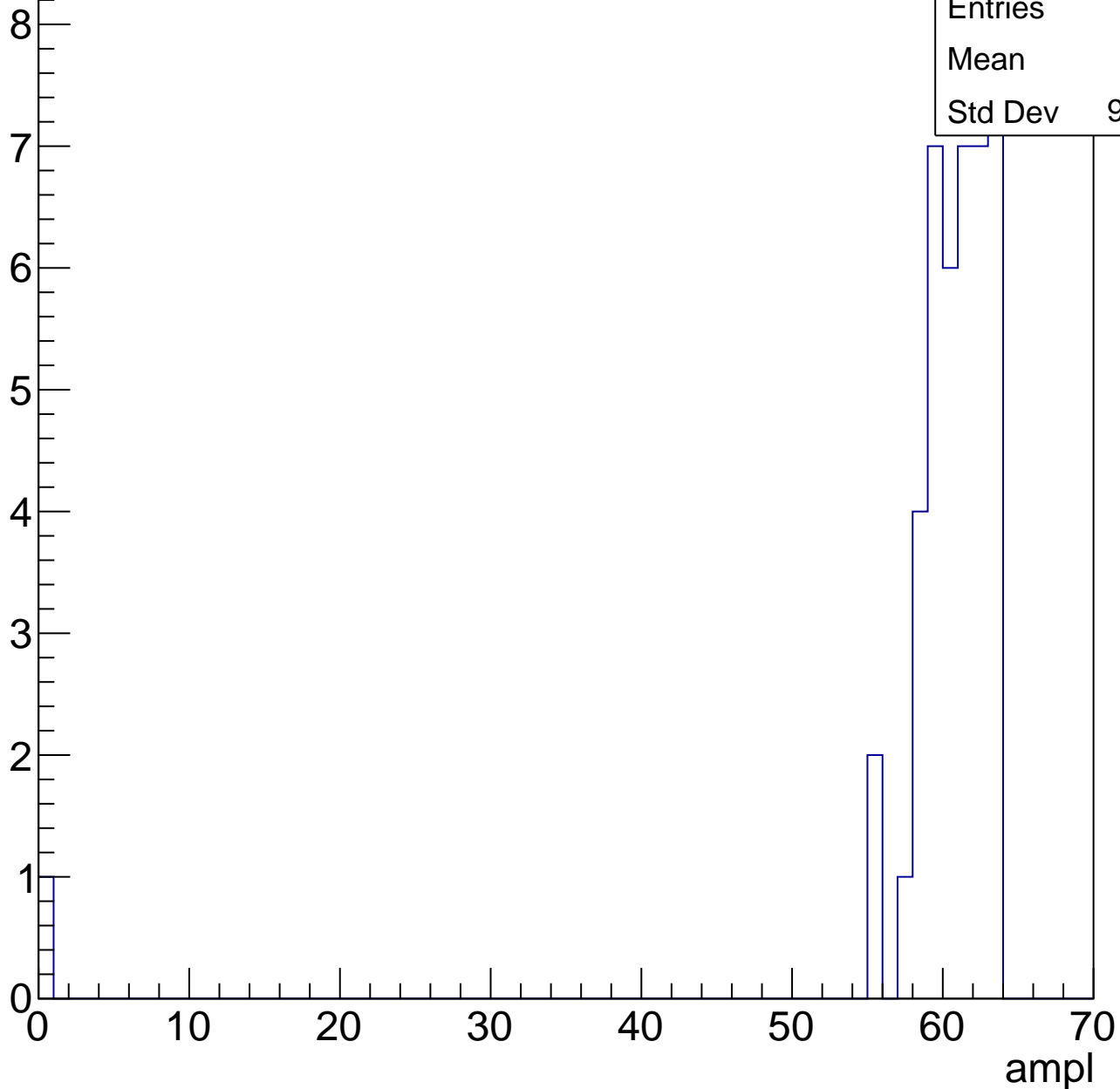


Entries	49
Mean	56.73
Std Dev	3.167

B0L002S, U12-ch36, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch37, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	31.71
Std Dev	3.359

Gaus mean : 32.0837

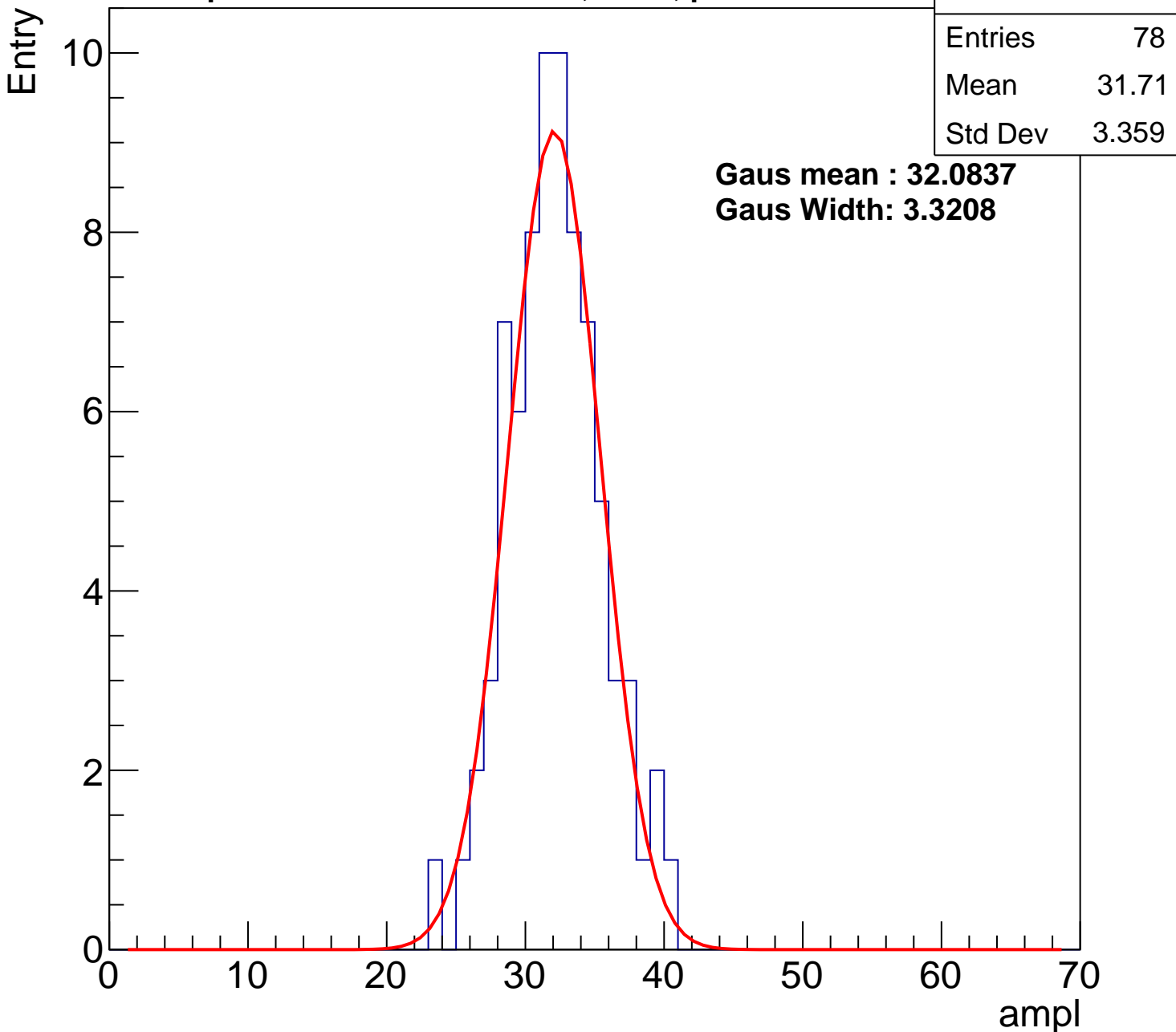
Gaus Width: 3.3208

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch37, adc1

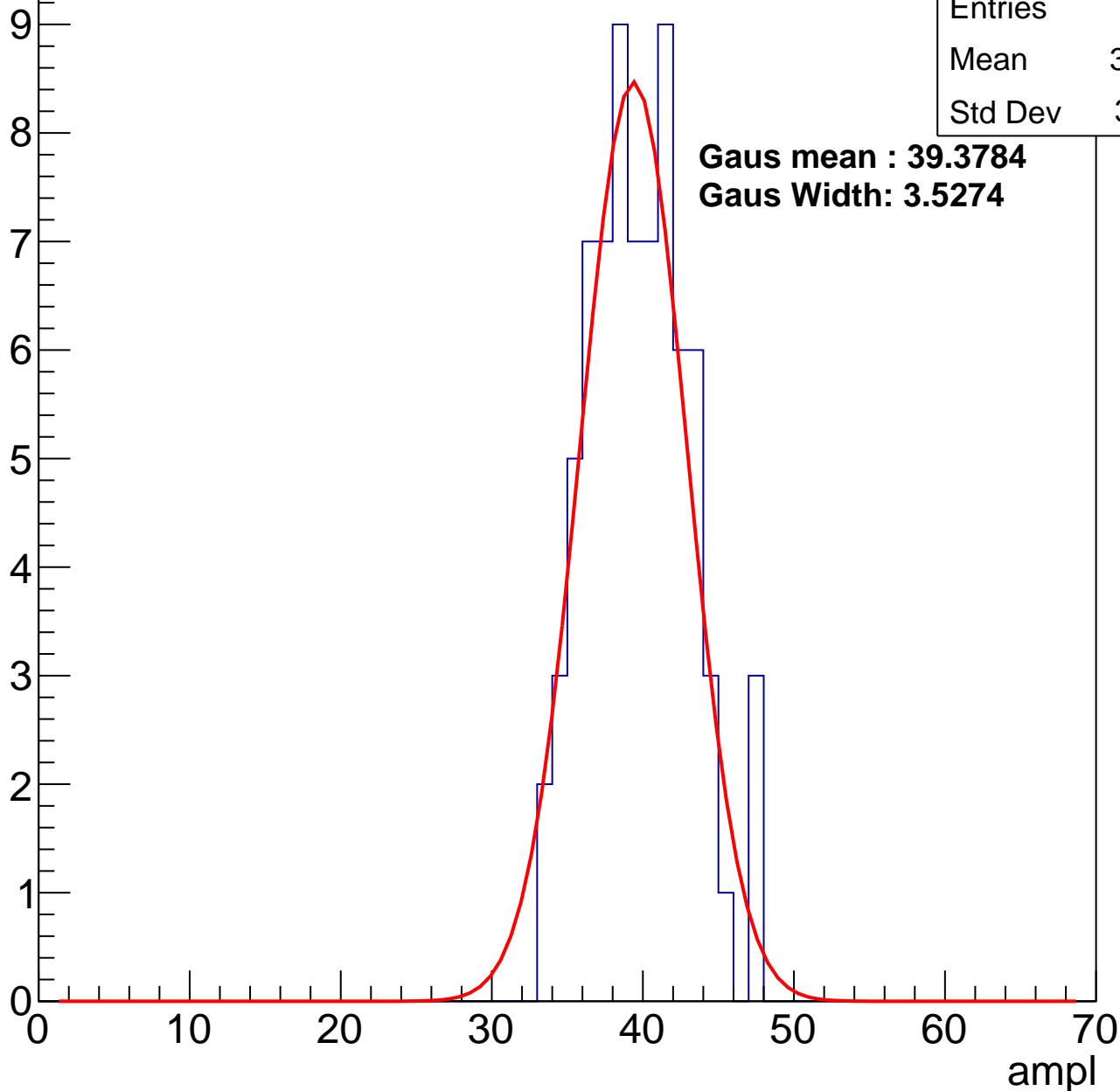
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	39.28
Std Dev	3.301

Gaus mean : 39.3784

Gaus Width: 3.5274



B0L002S, U12-ch37, adc2

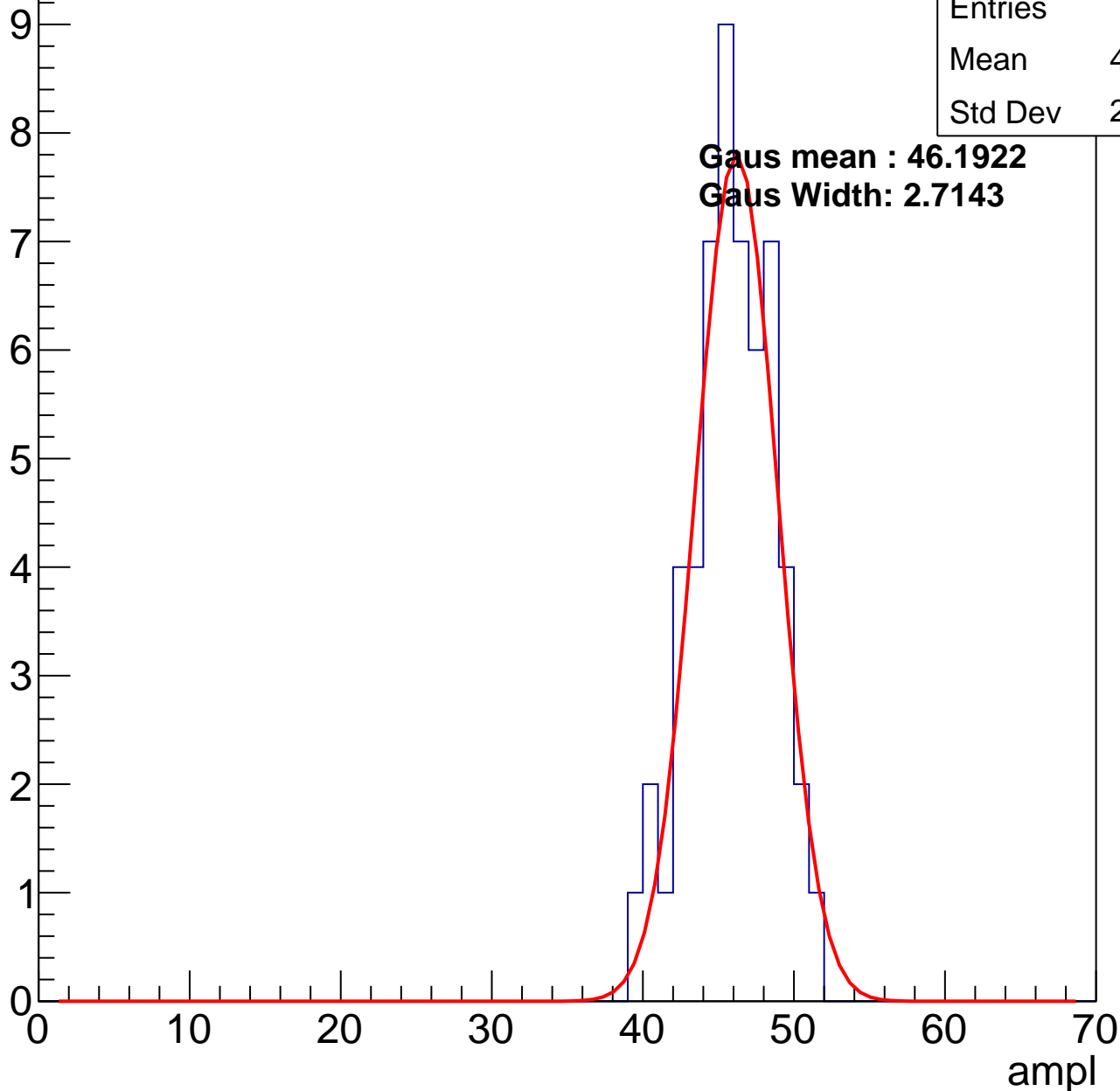
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	45.45
Std Dev	2.675

Gaus mean : 46.1922

Gaus Width: 2.7143



B0L002S, U12-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

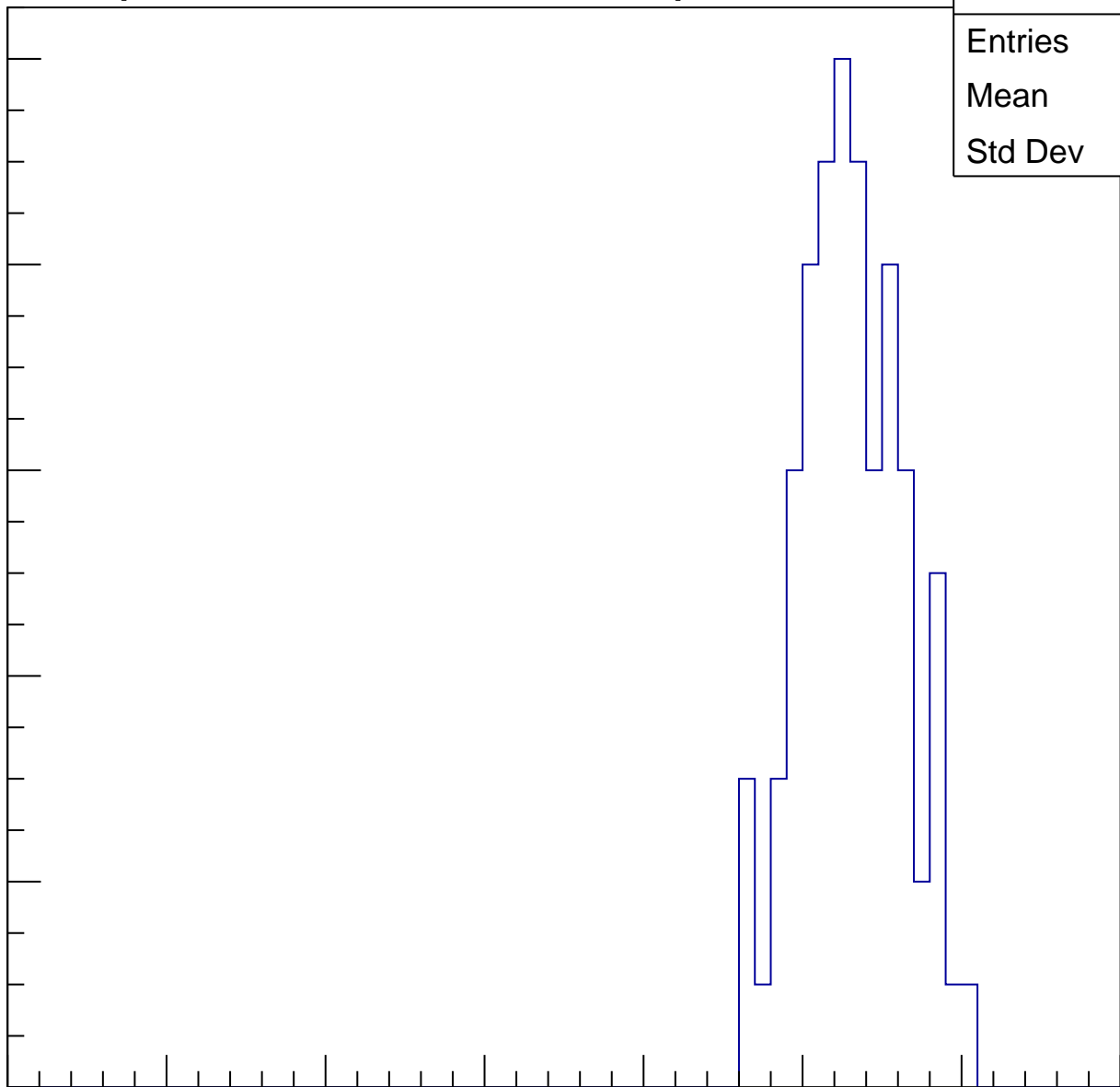
Entries	78
Mean	52.59
Std Dev	3.188

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

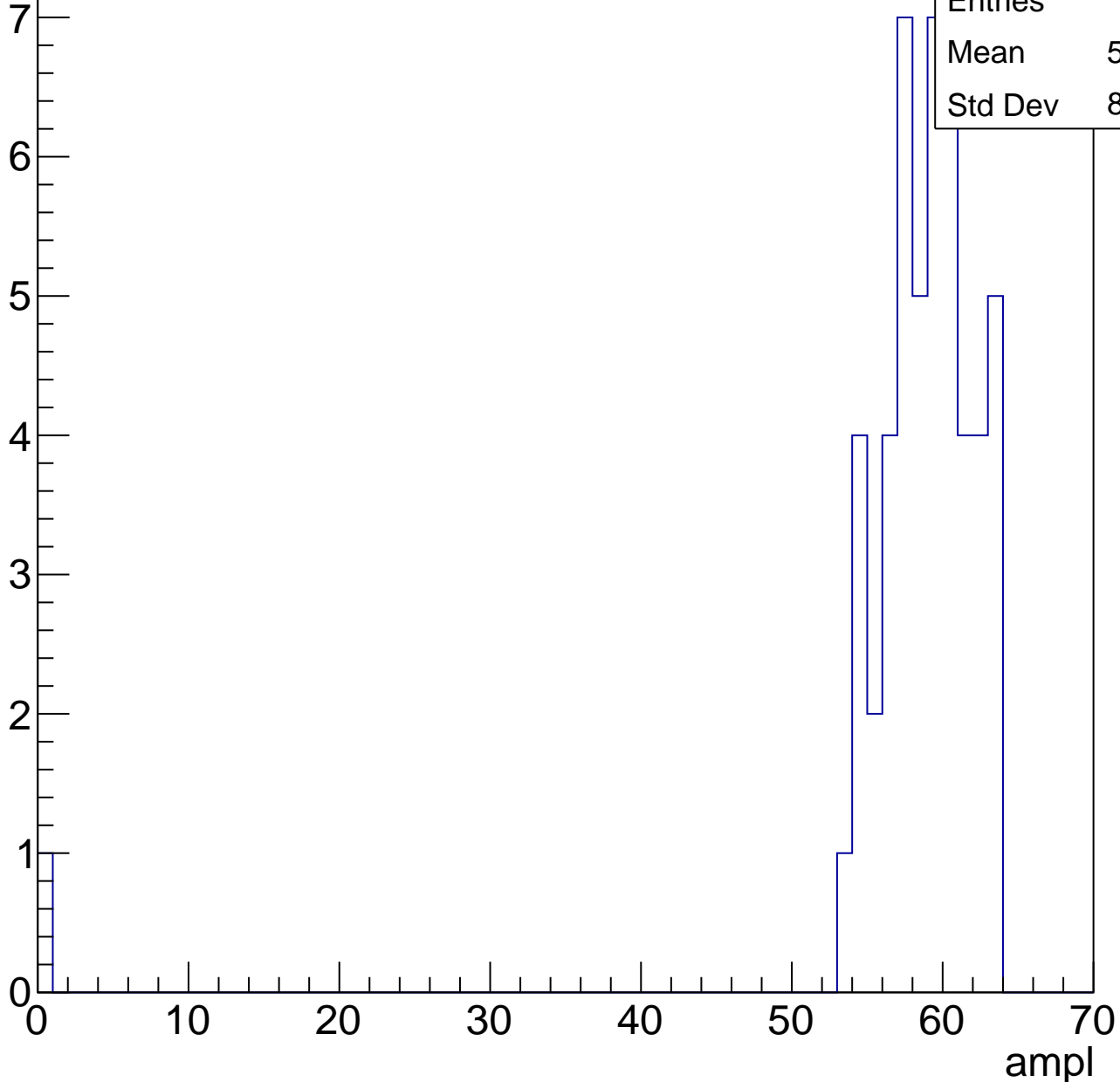


B0L002S, U12-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57.49
Std Dev	8.564

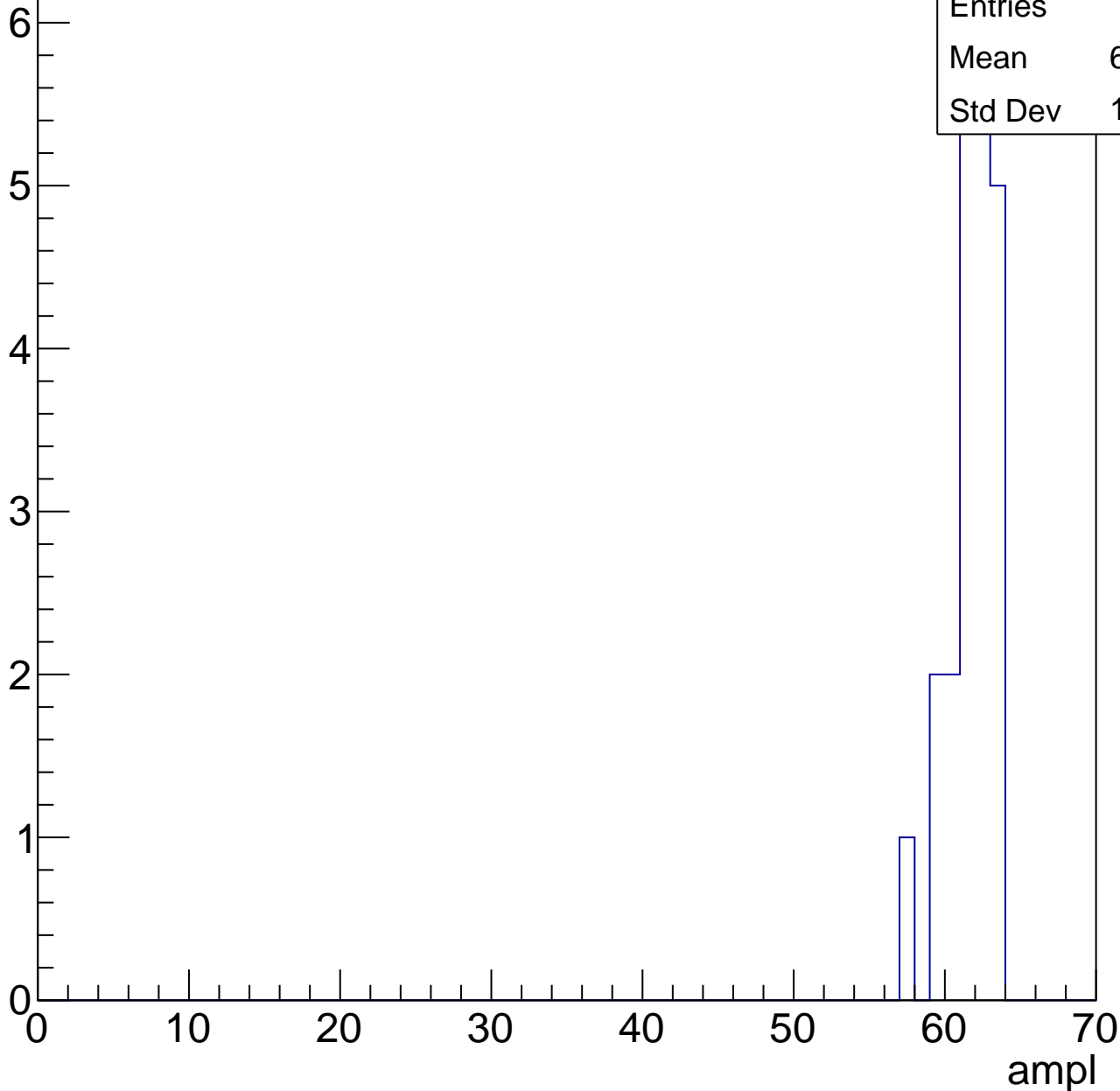


B0L002S, U12-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	22
Mean	61.27
Std Dev	1.513



B0L002S, U12-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch38, adc0

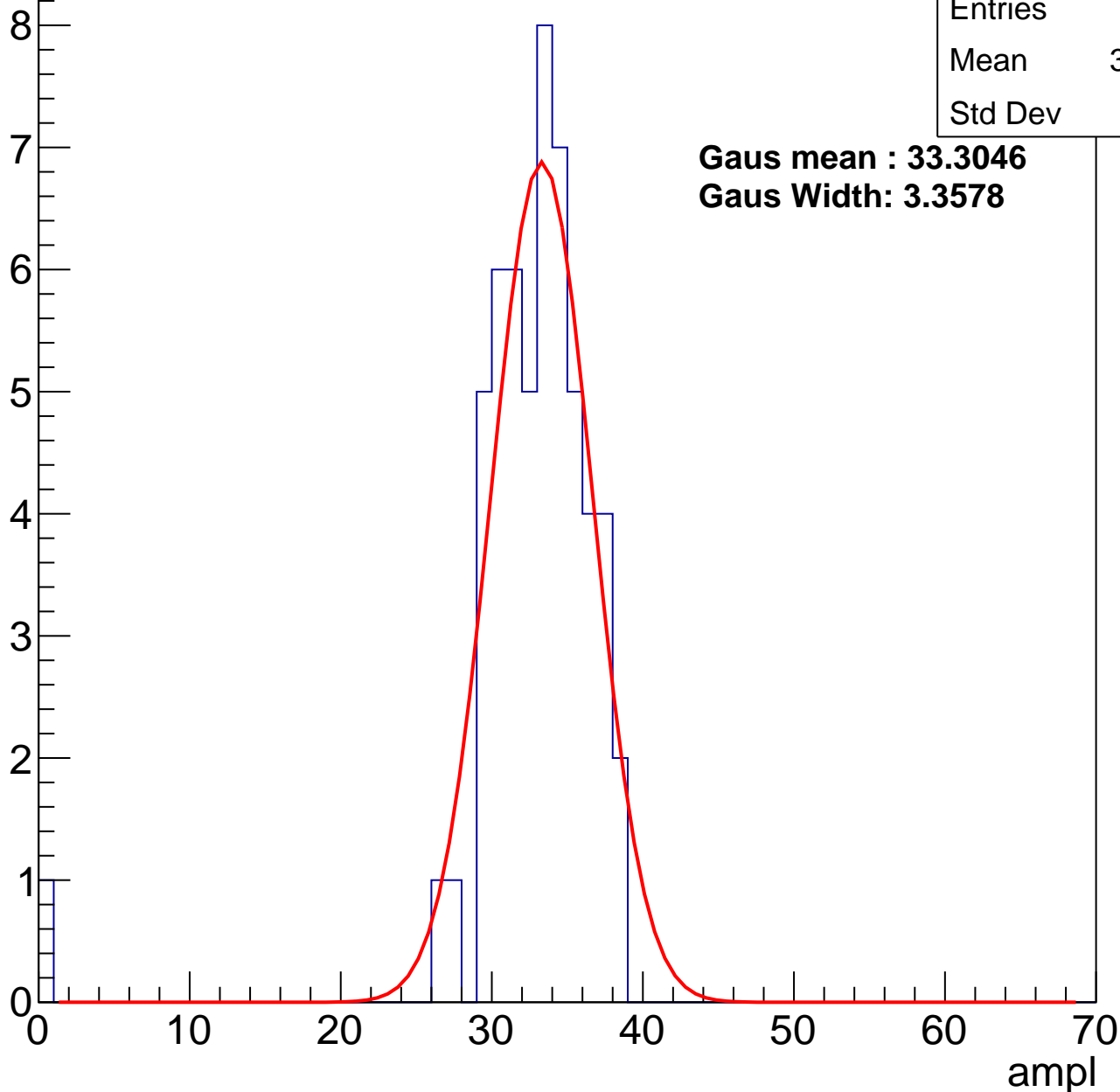
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	32.16
Std Dev	5.18

Gaus mean : 33.3046

Gaus Width: 3.3578



B0L002S, U12-ch38, adc1

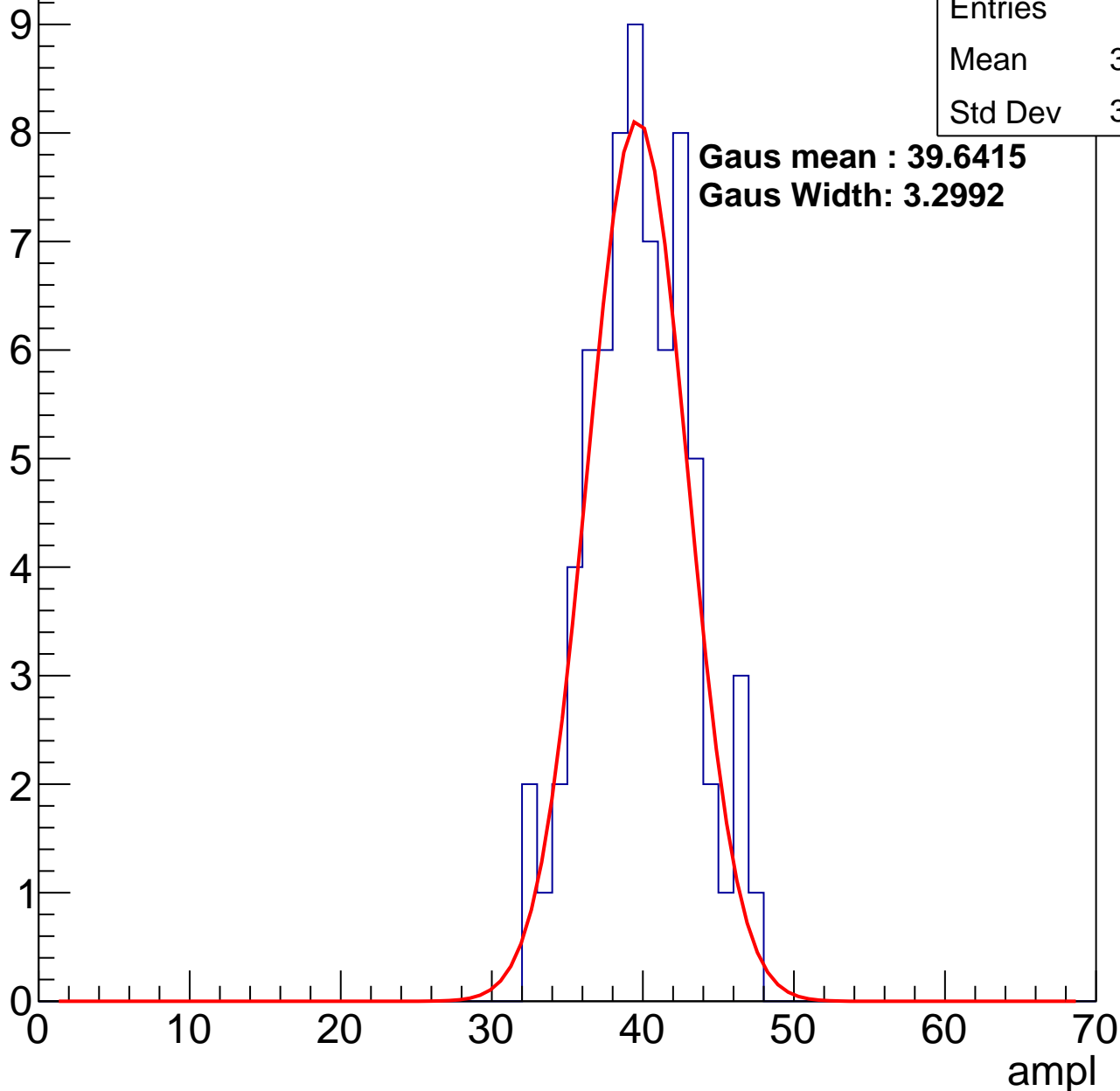
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	39.34
Std Dev	3.369

Gaus mean : 39.6415

Gaus Width: 3.2992



B0L002S, U12-ch38, adc2

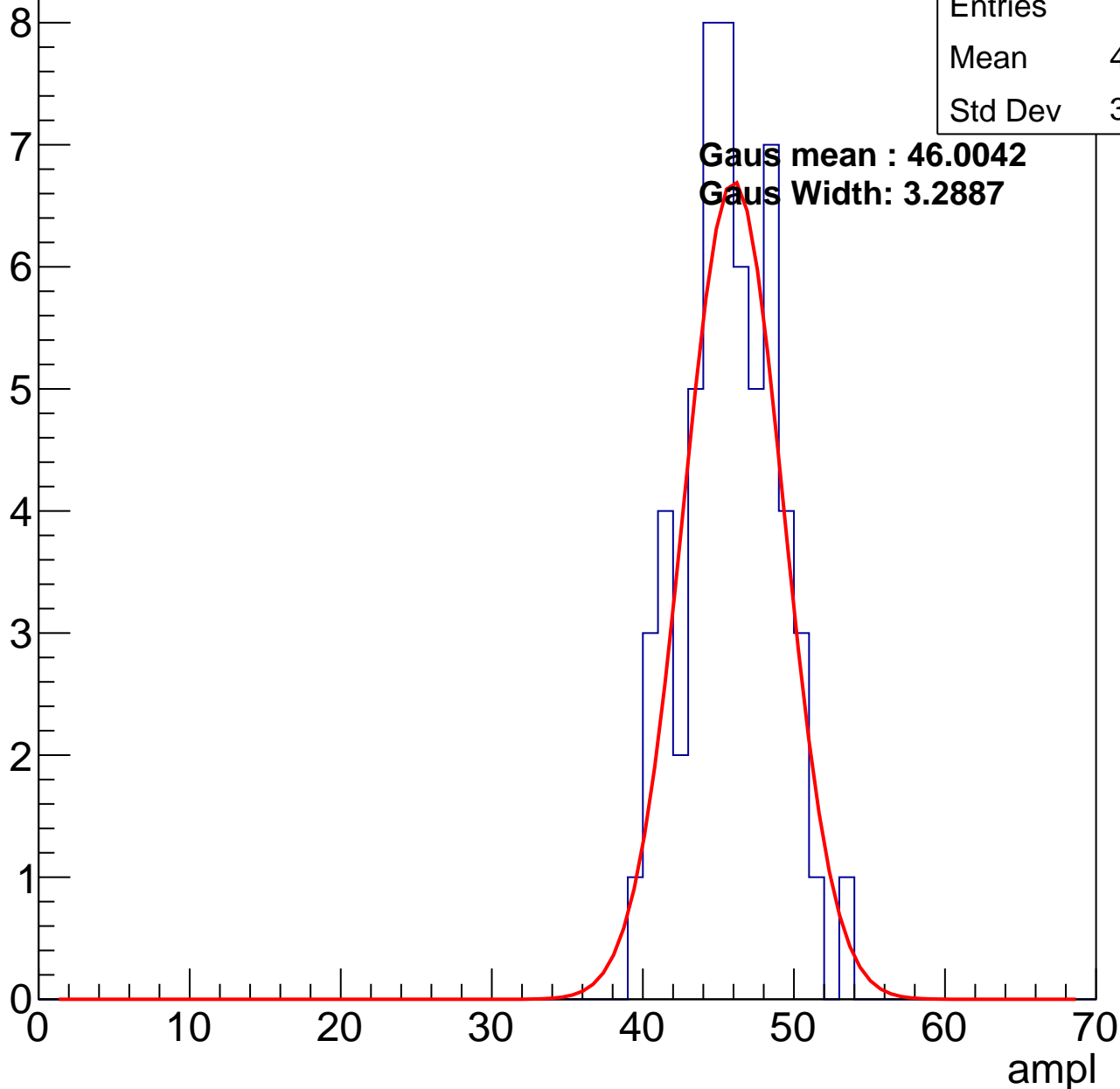
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	45.36
Std Dev	3.055

Gaus mean : 46.0042

Gaus Width: 3.2887



B0L002S, U12-ch38, adc3

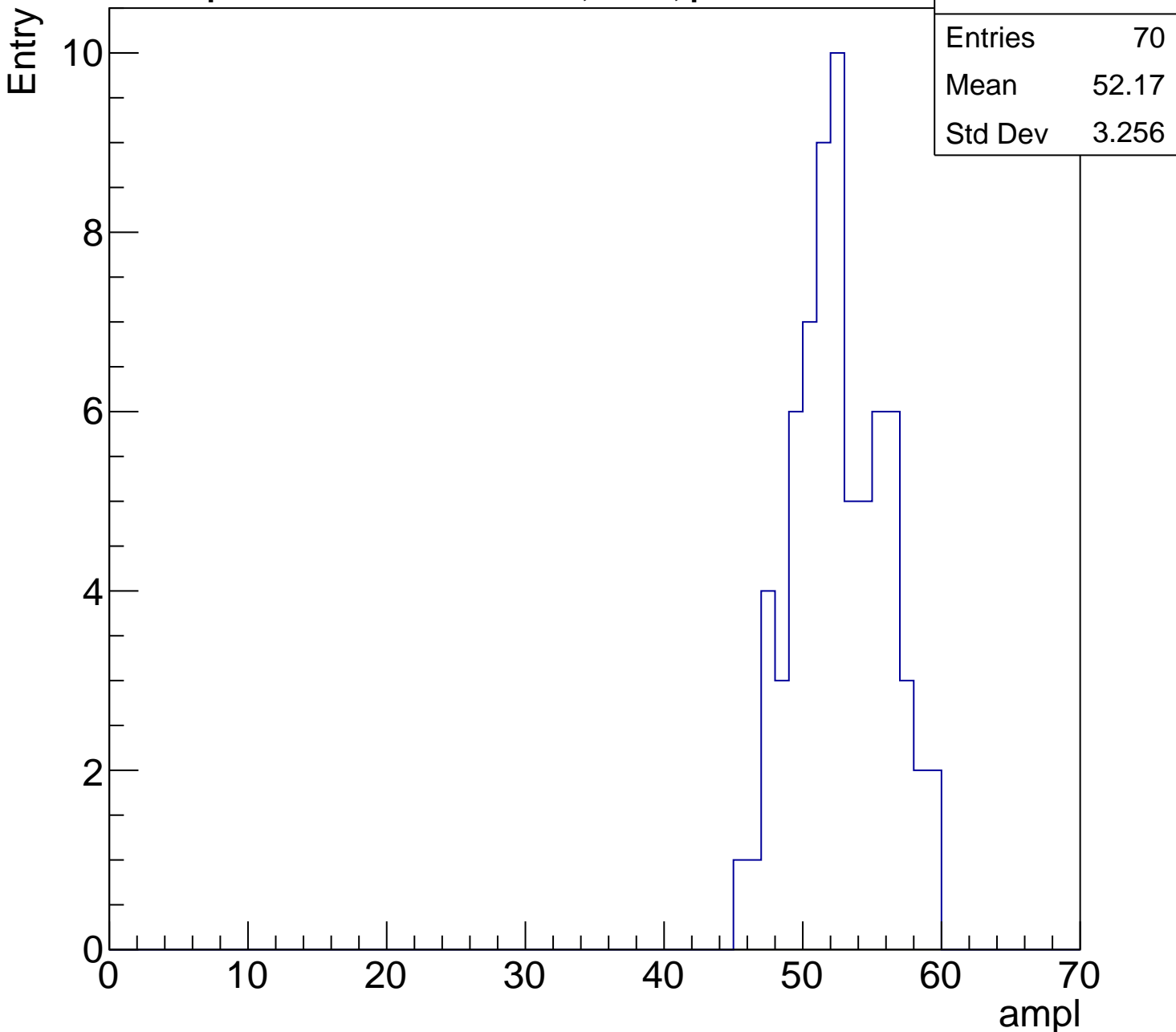
calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	52.17
Std Dev	3.256

Entry

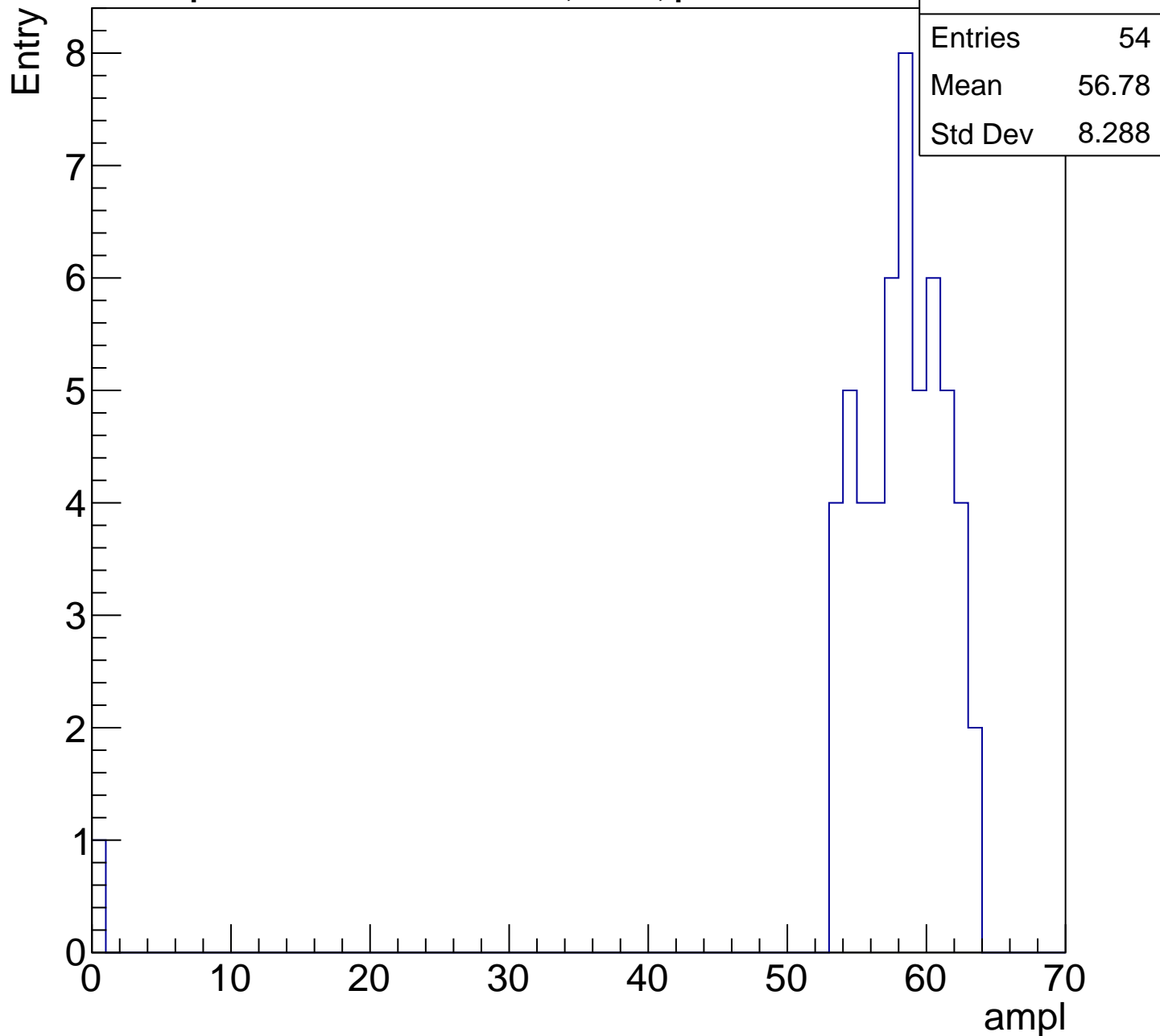
10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl



B0L002S, U12-ch38, adc4

calib_packv5_042523_0143.root, FC#8, port C1

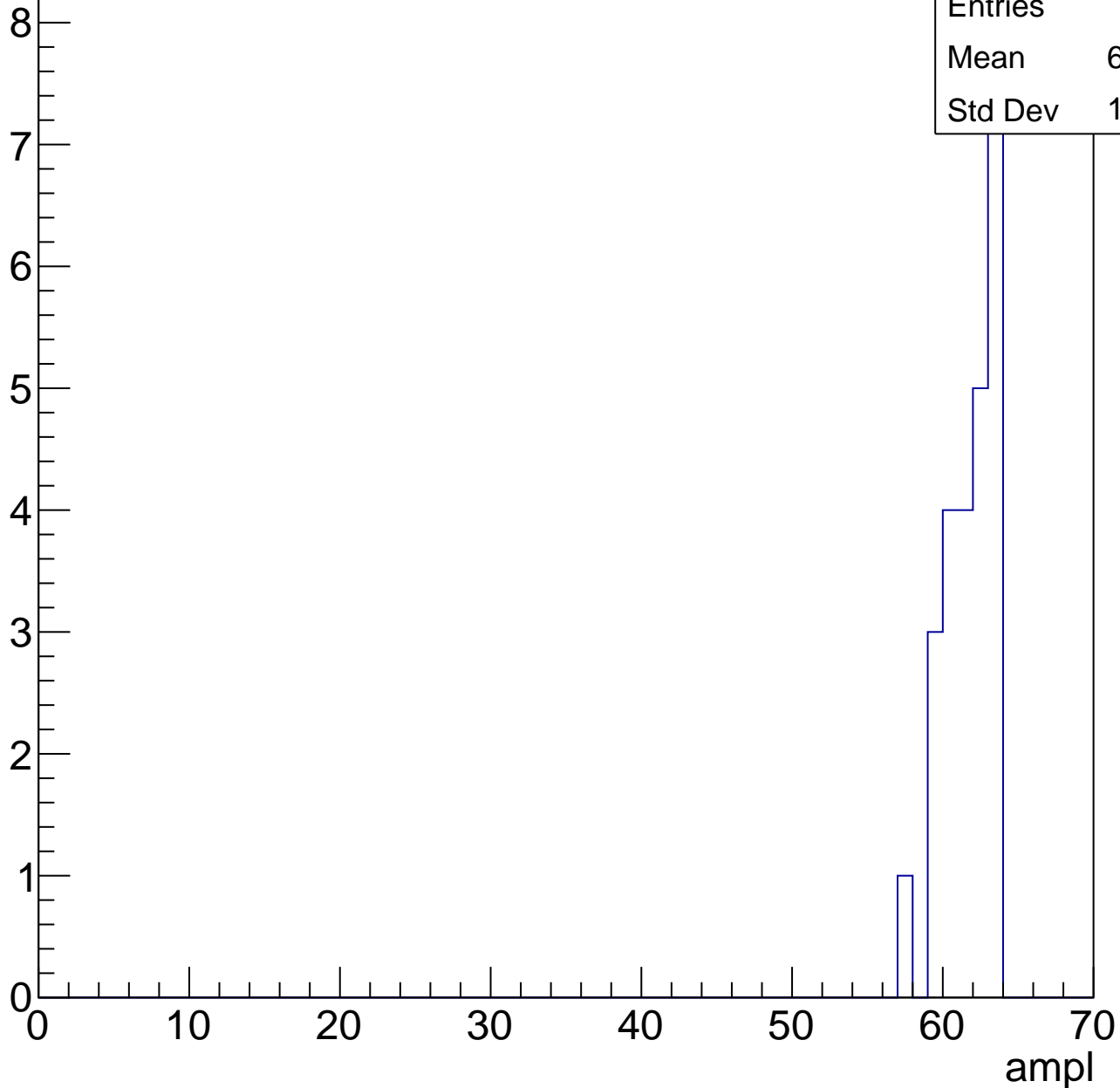


B0L002S, U12-ch38, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	61.28
Std Dev	1.638



B0L002S, U12-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch39, adc0

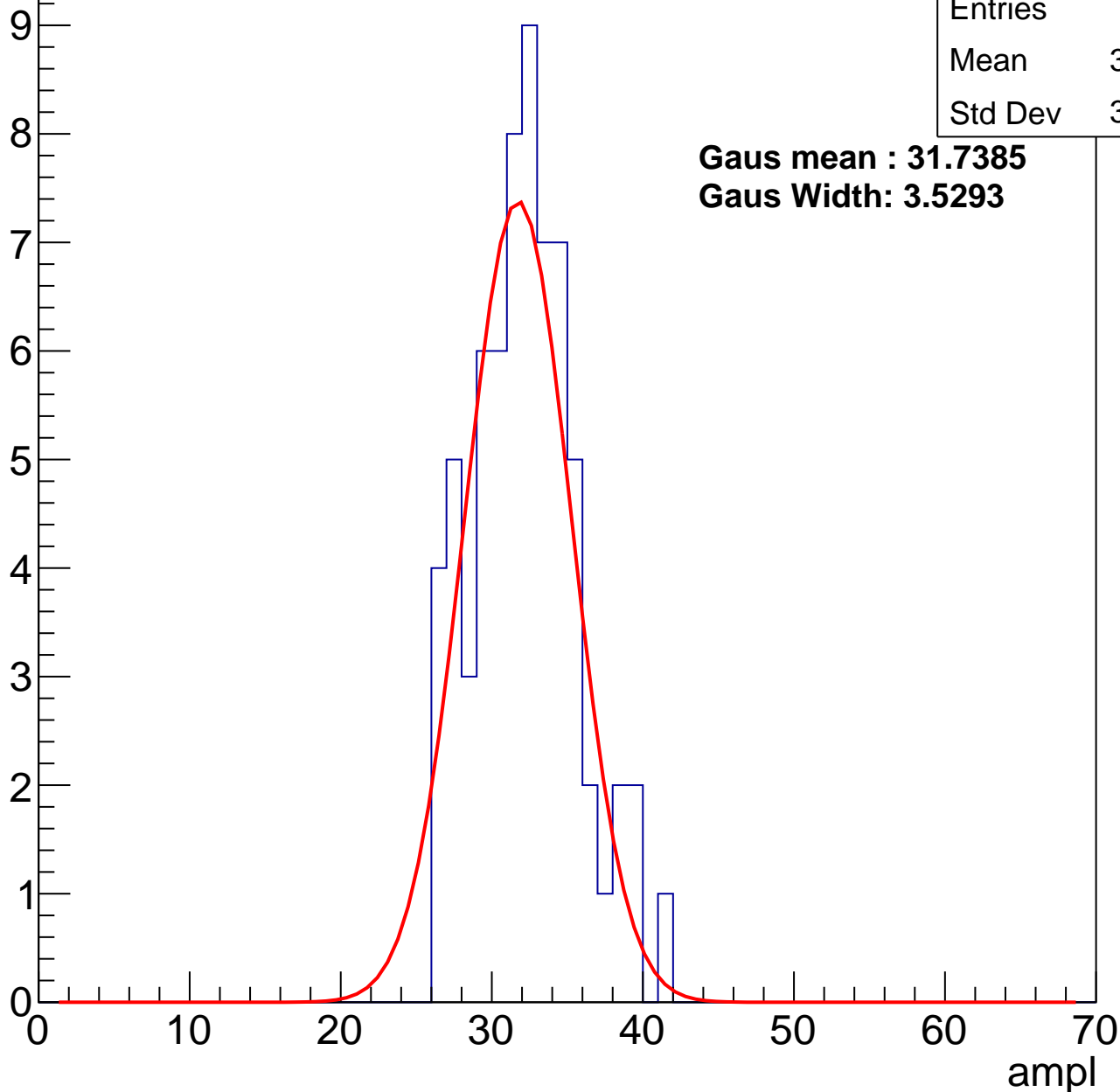
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.78
Std Dev	3.403

Gaus mean : 31.7385

Gaus Width: 3.5293



B0L002S, U12-ch39, adc1

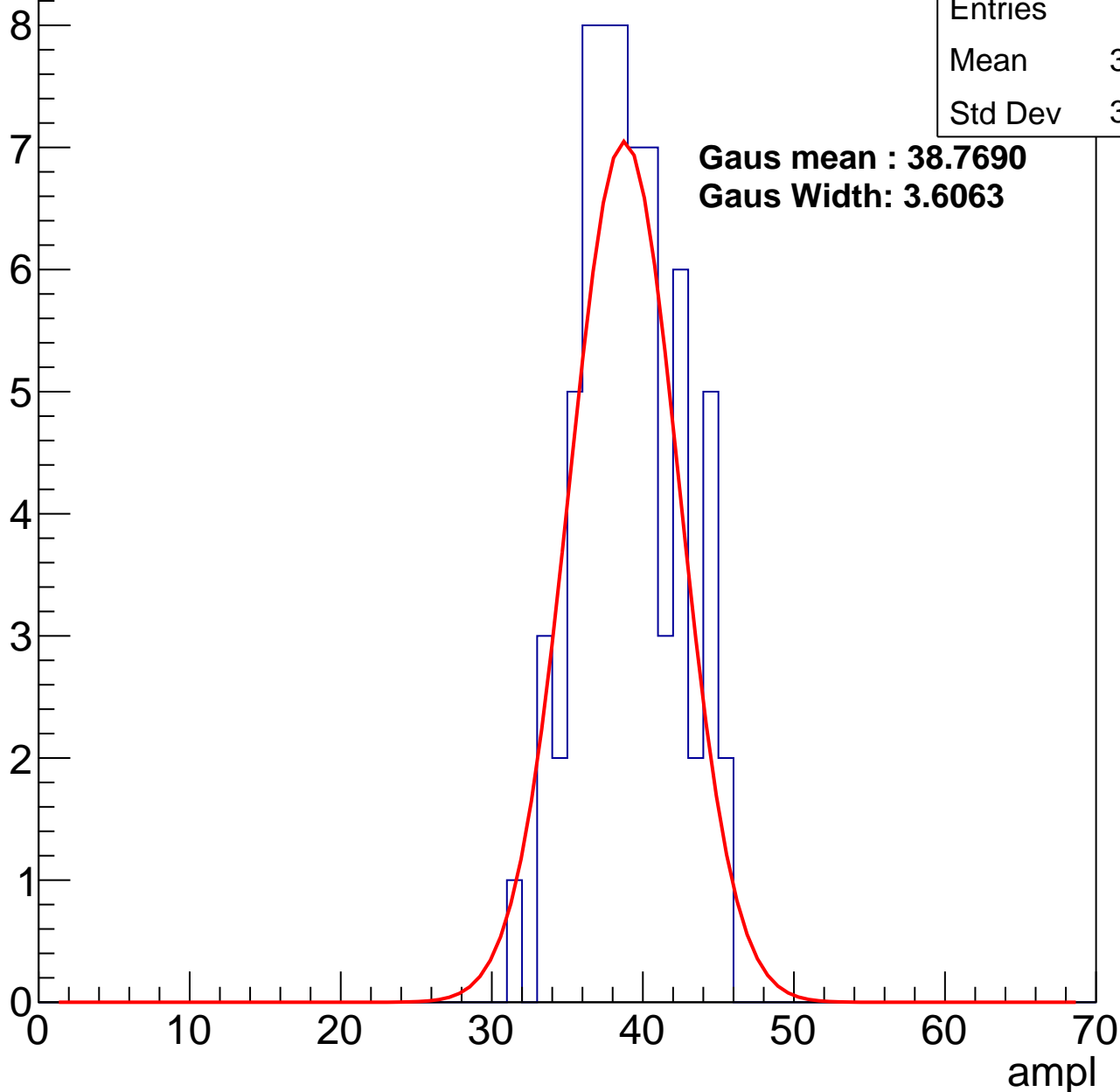
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	38.58
Std Dev	3.242

Gaus mean : 38.7690

Gaus Width: 3.6063



B0L002S, U12-ch39, adc2

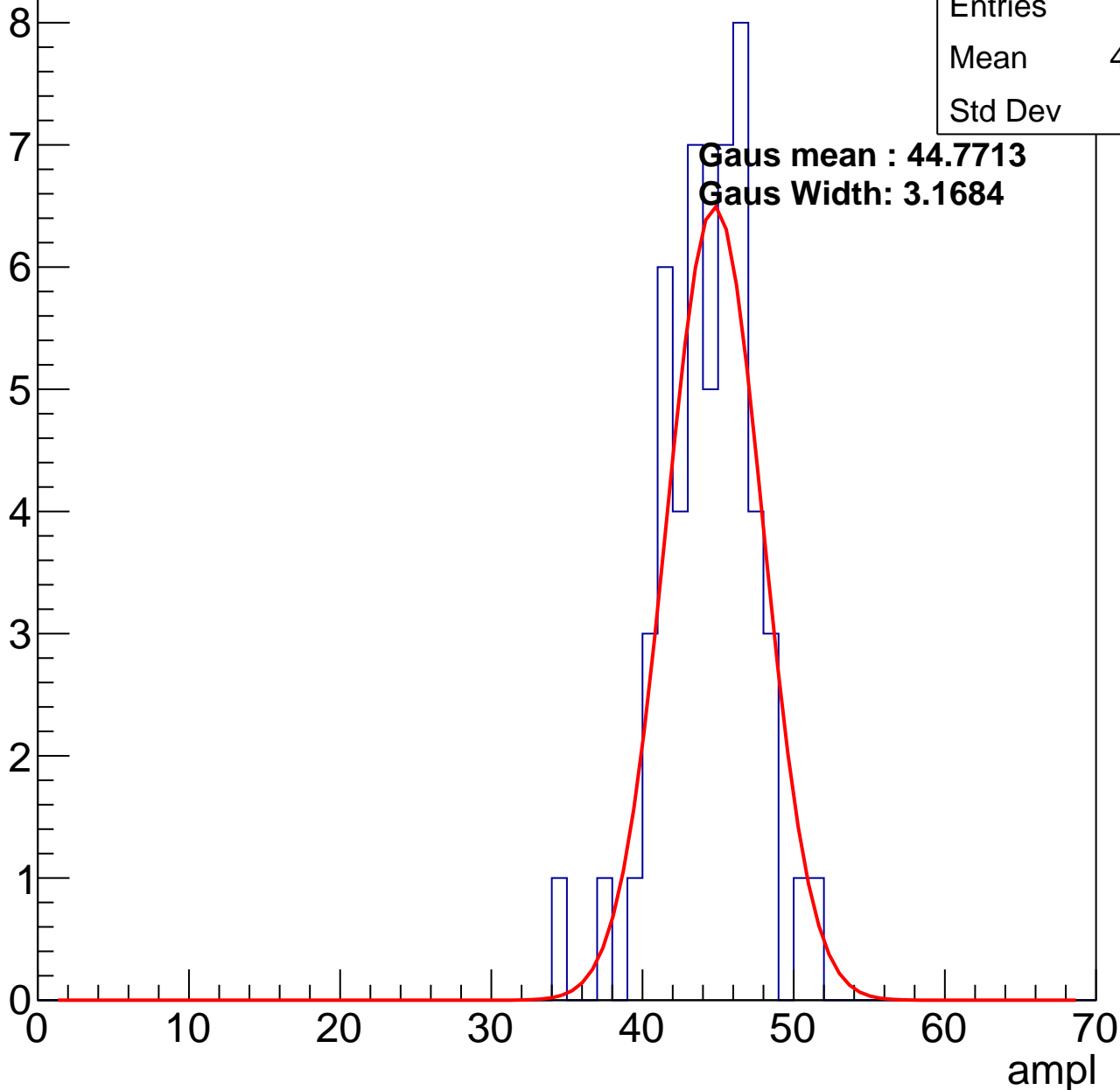
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	43.87
Std Dev	3.12

Gaus mean : 44.7713

Gaus Width: 3.1684

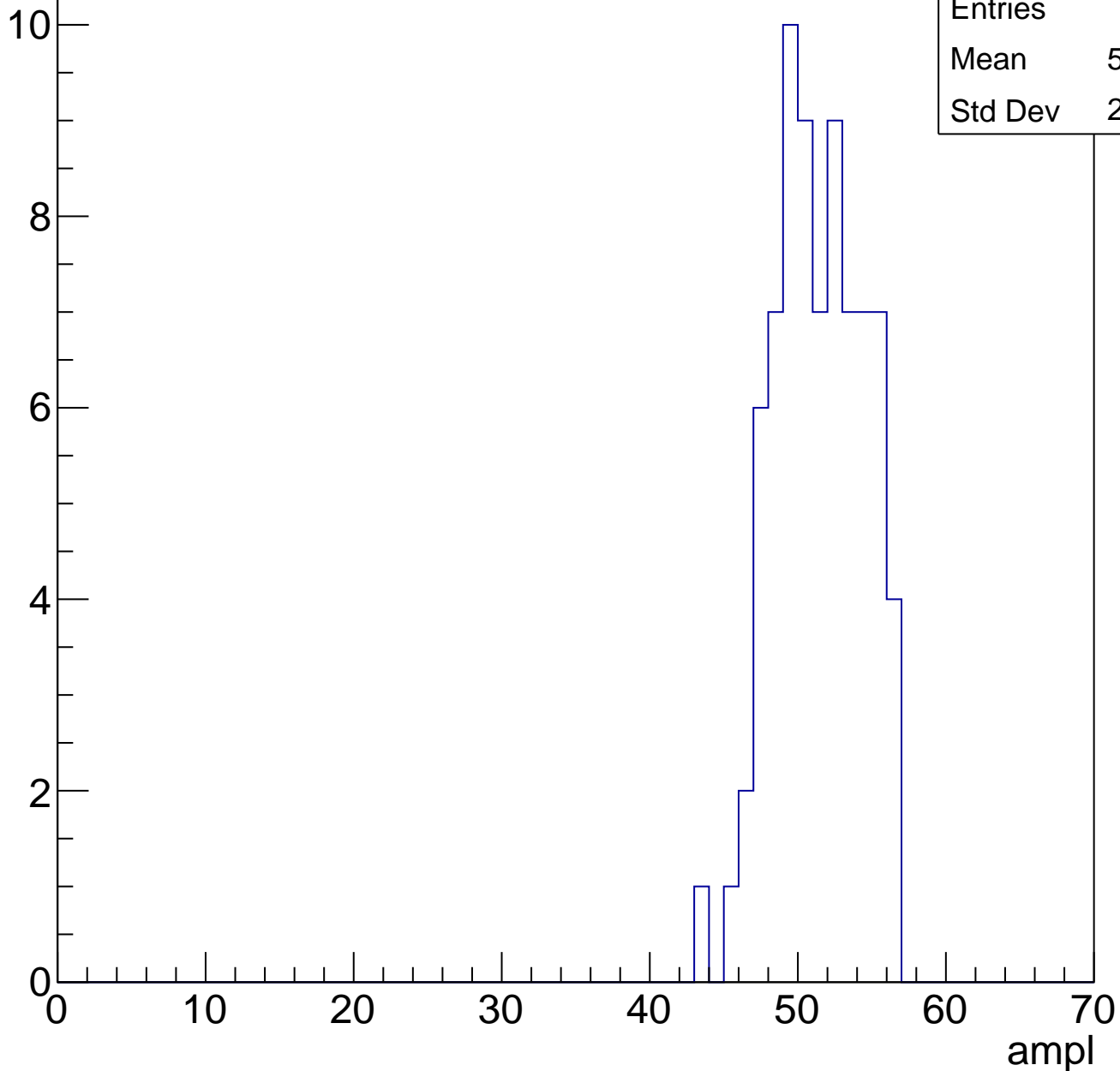


B0L002S, U12-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	50.92
Std Dev	2.949

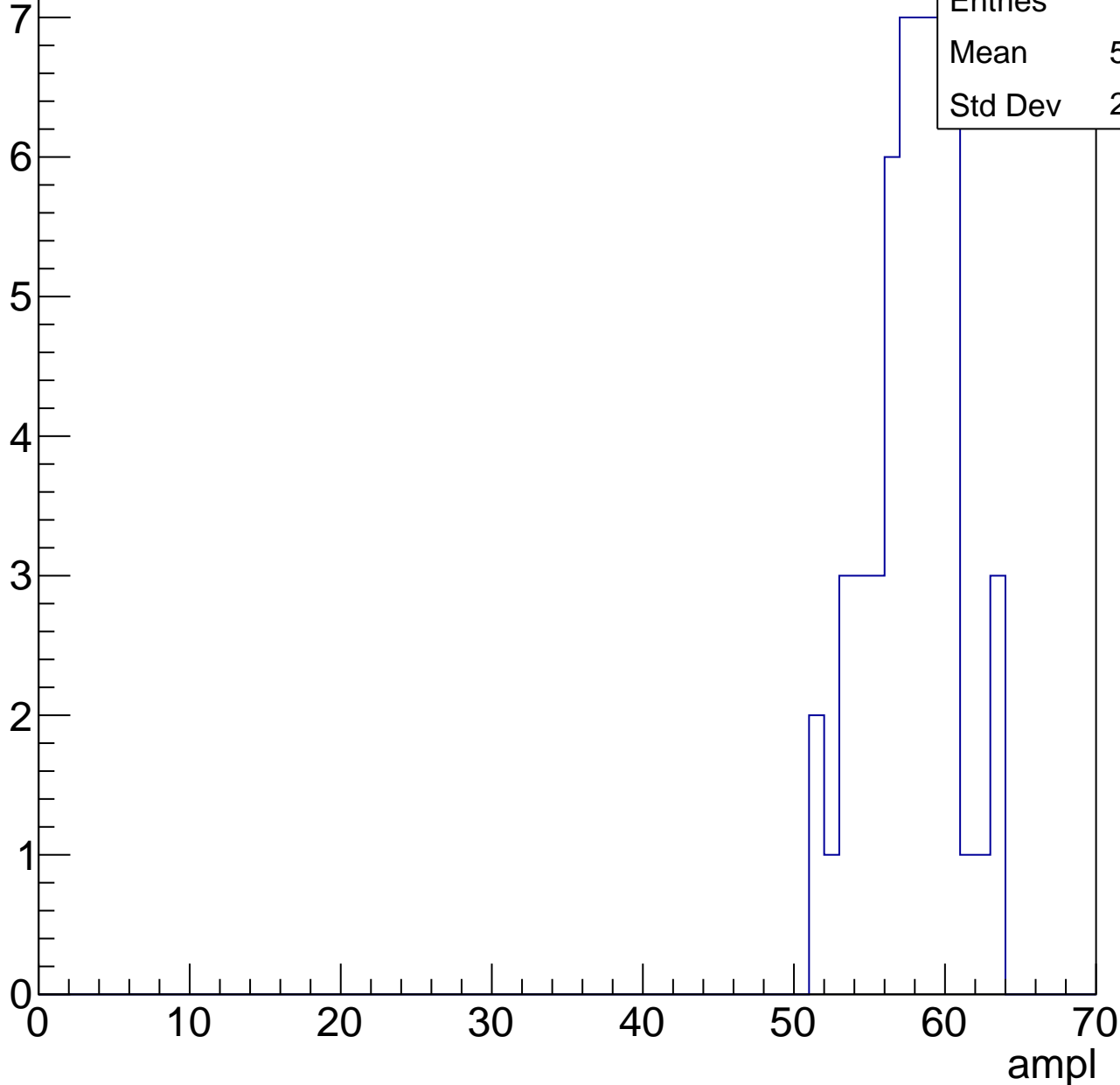
Entry



B0L002S, U12-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

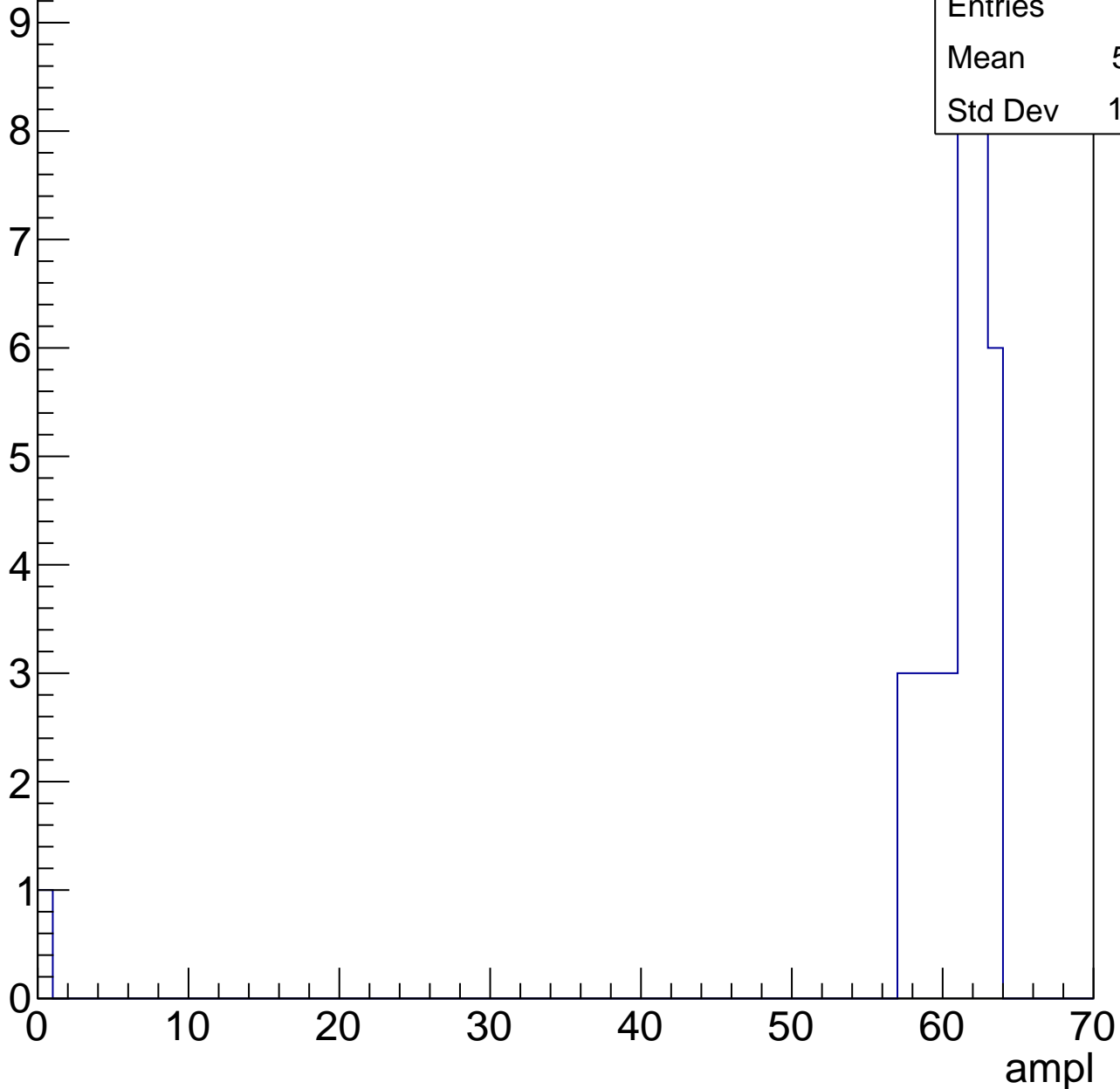


B0L002S, U12-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.11
Std Dev	10.02



B0L002S, U12-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B0L002S, U12-ch40, adc0

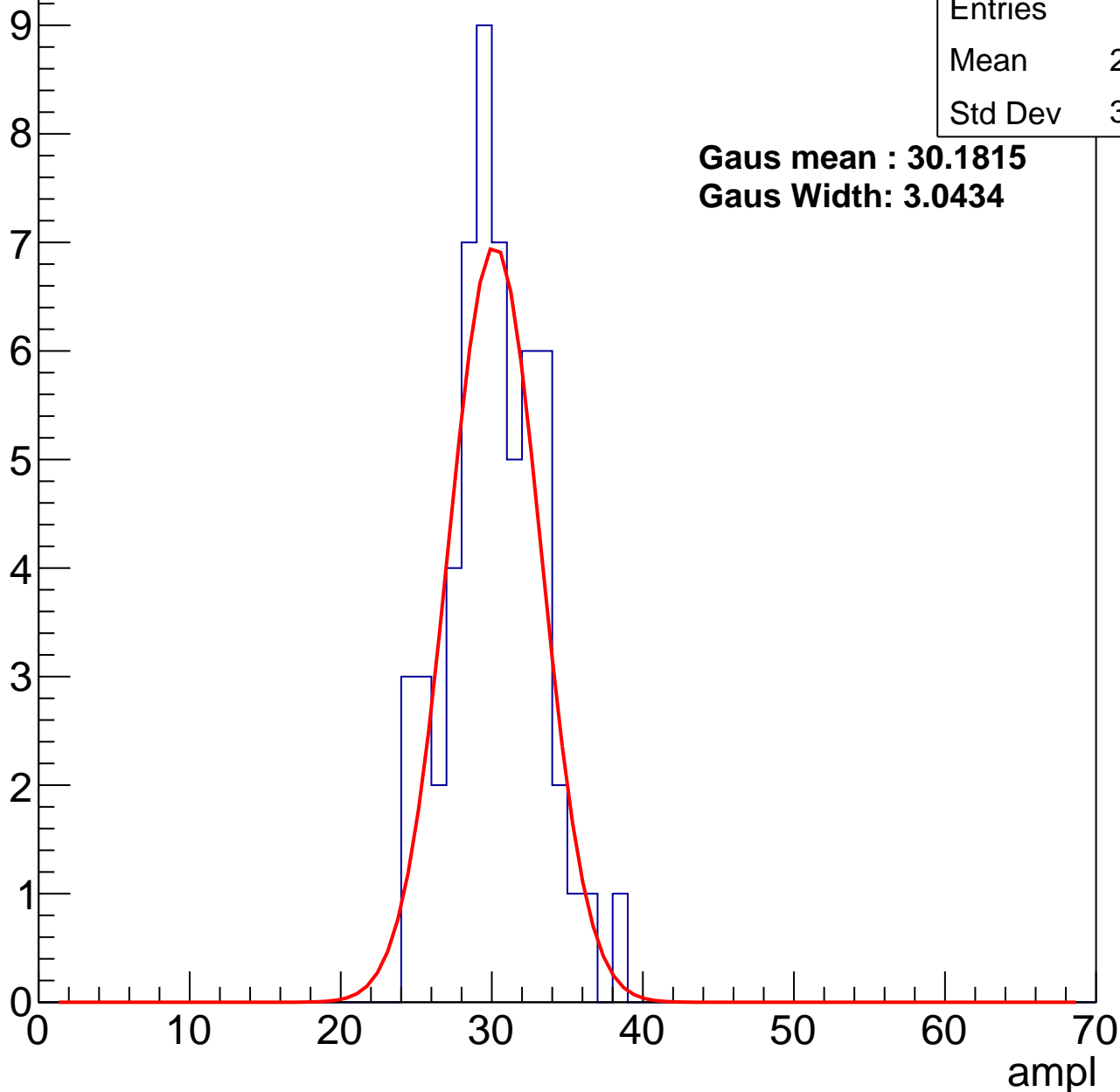
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.75
Std Dev	3.034

Gaus mean : 30.1815

Gaus Width: 3.0434



B0L002S, U12-ch40, adc1

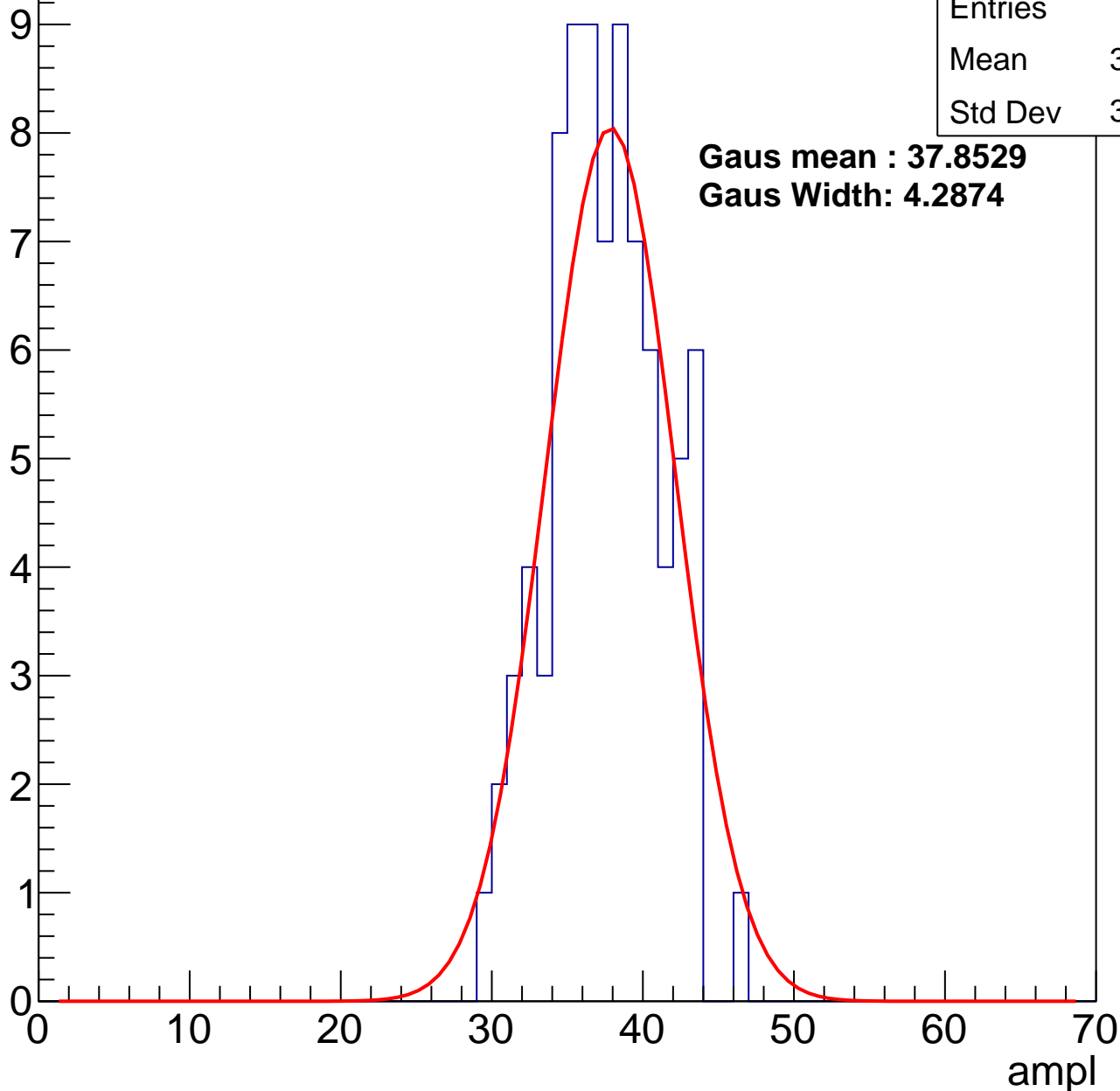
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	37.05
Std Dev	3.638

Gaus mean : 37.8529

Gaus Width: 4.2874



B0L002S, U12-ch40, adc2

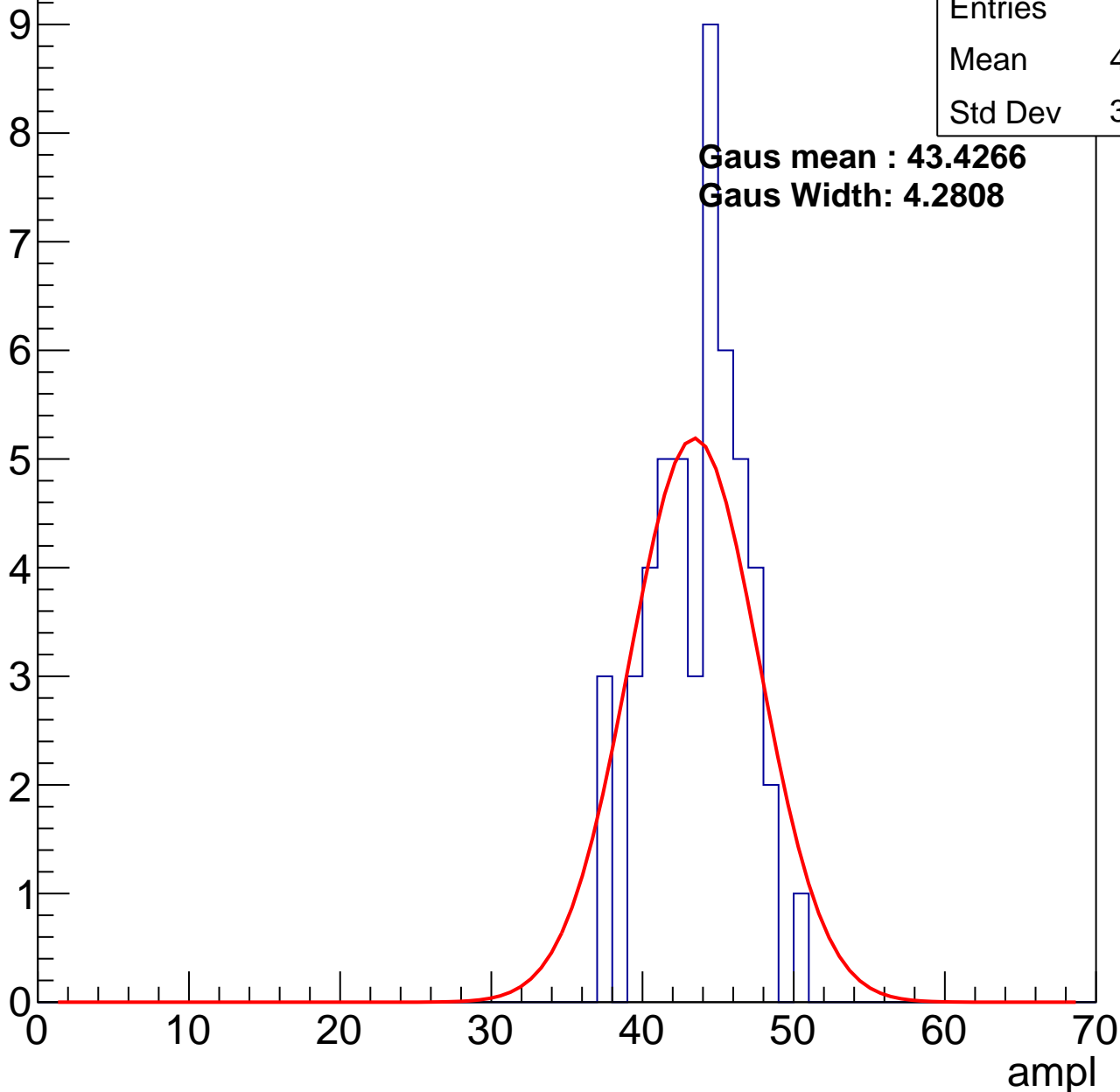
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	43.24
Std Dev	3.017

Gaus mean : 43.4266

Gaus Width: 4.2808



B0L002S, U12-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	50.05
Std Dev	3.369

Entry

10

8

6

4

2

0

0

10

20

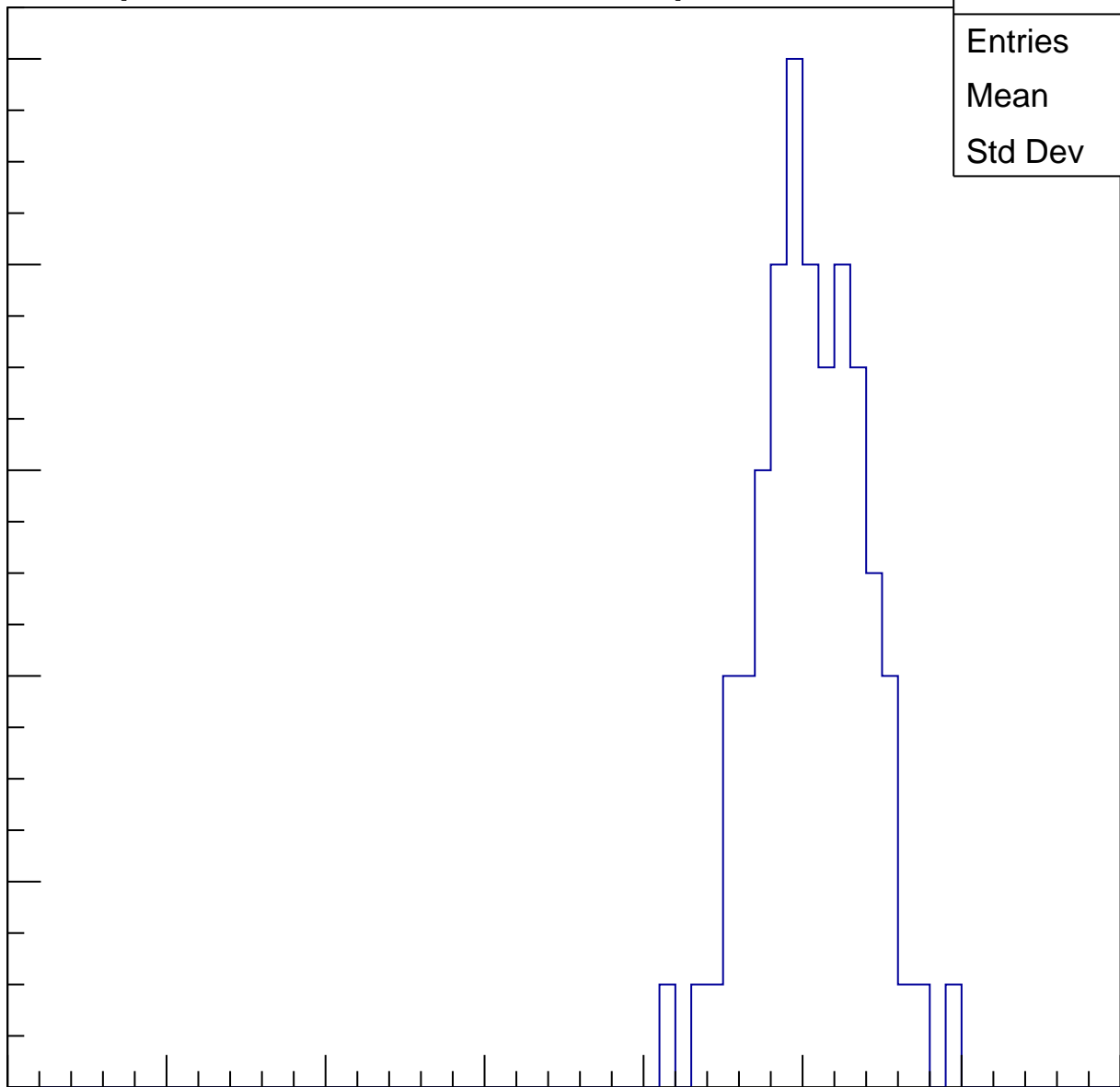
30

40

50

60

ampl

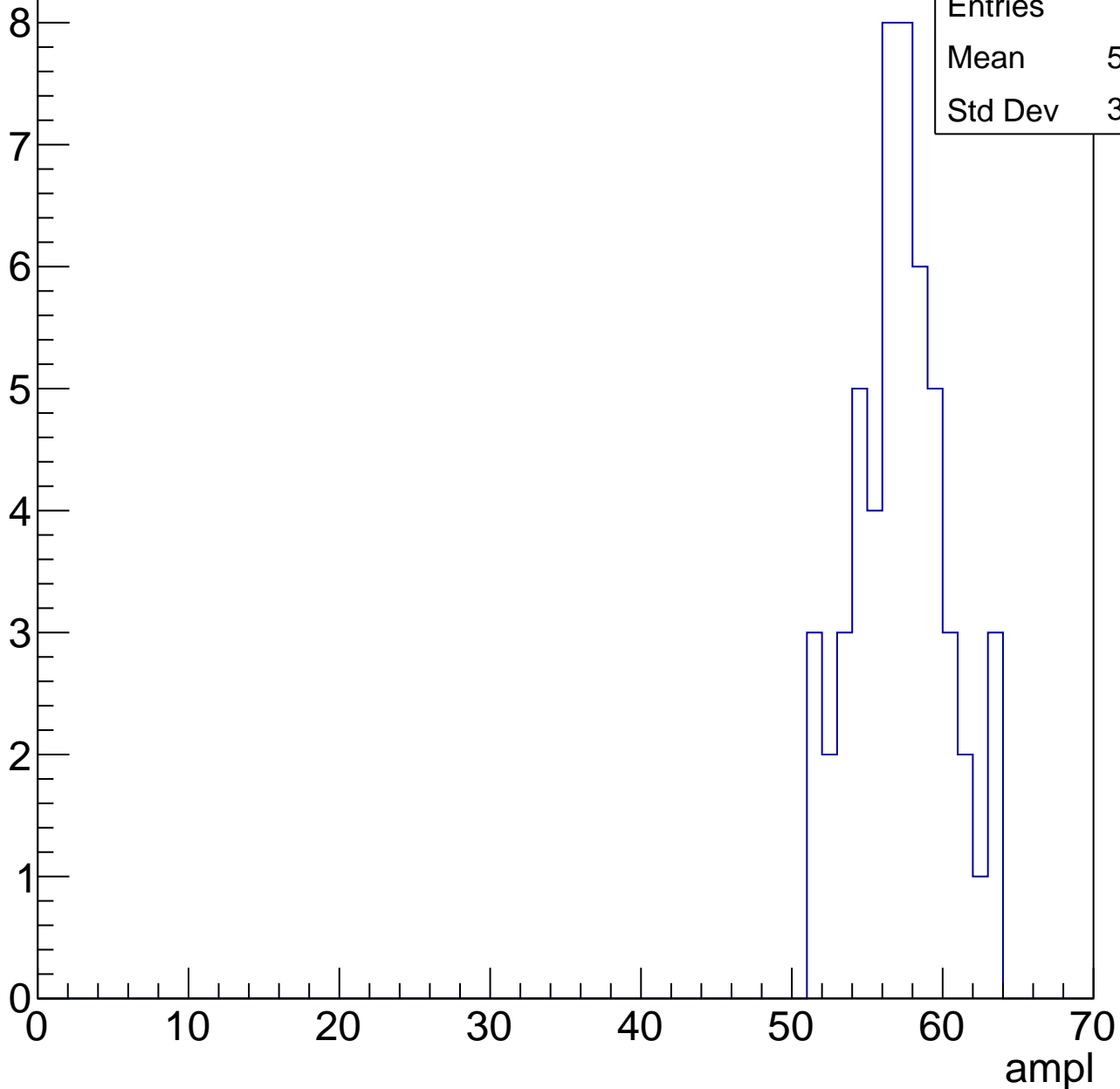


B0L002S, U12-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.72
Std Dev	3.037

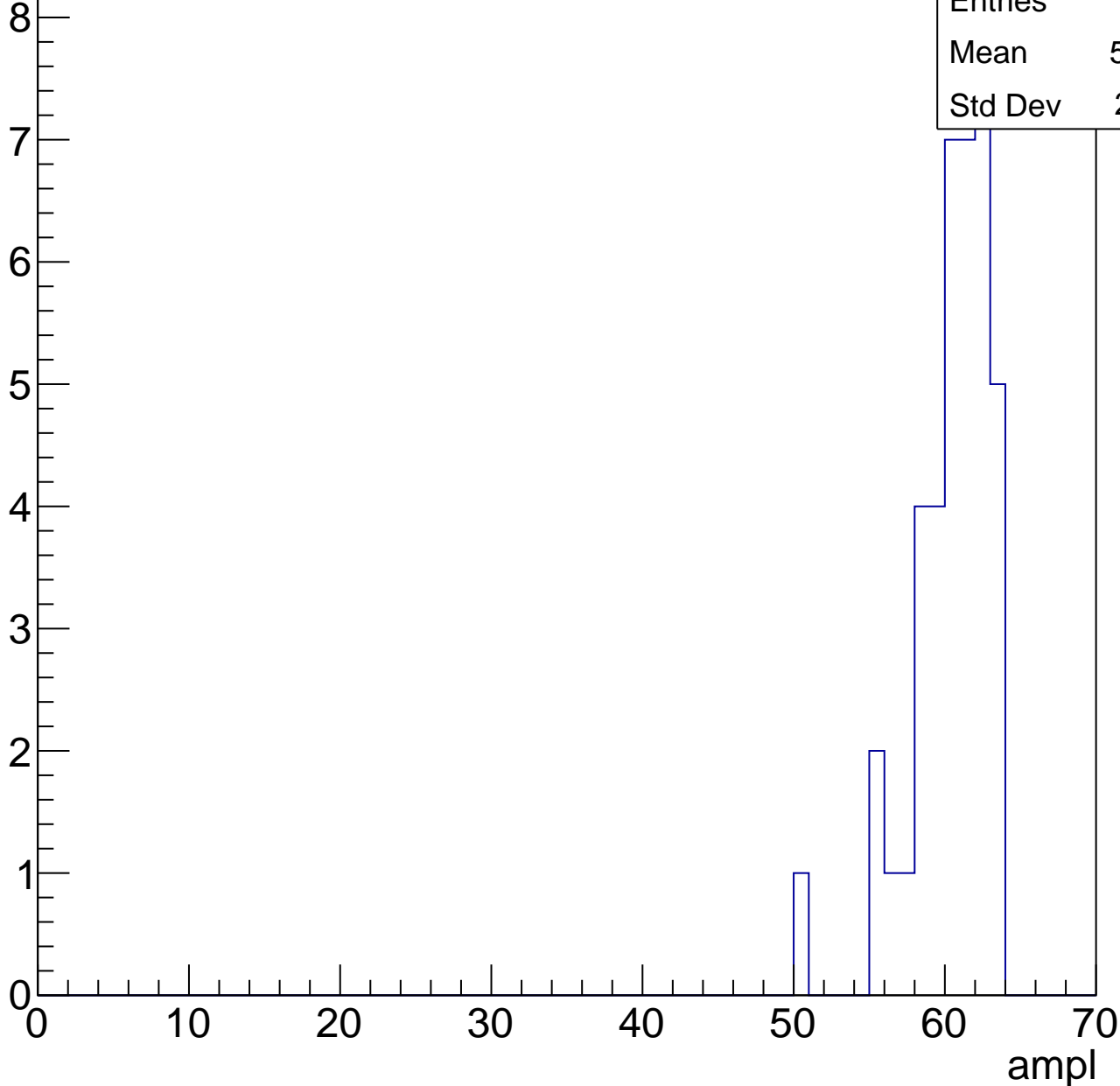


B0L002S, U12-ch40, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.98
Std Dev	2.641



B0L002S, U12-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	49.8
Std Dev	24.91

B0L002S, U12-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch41, adc0

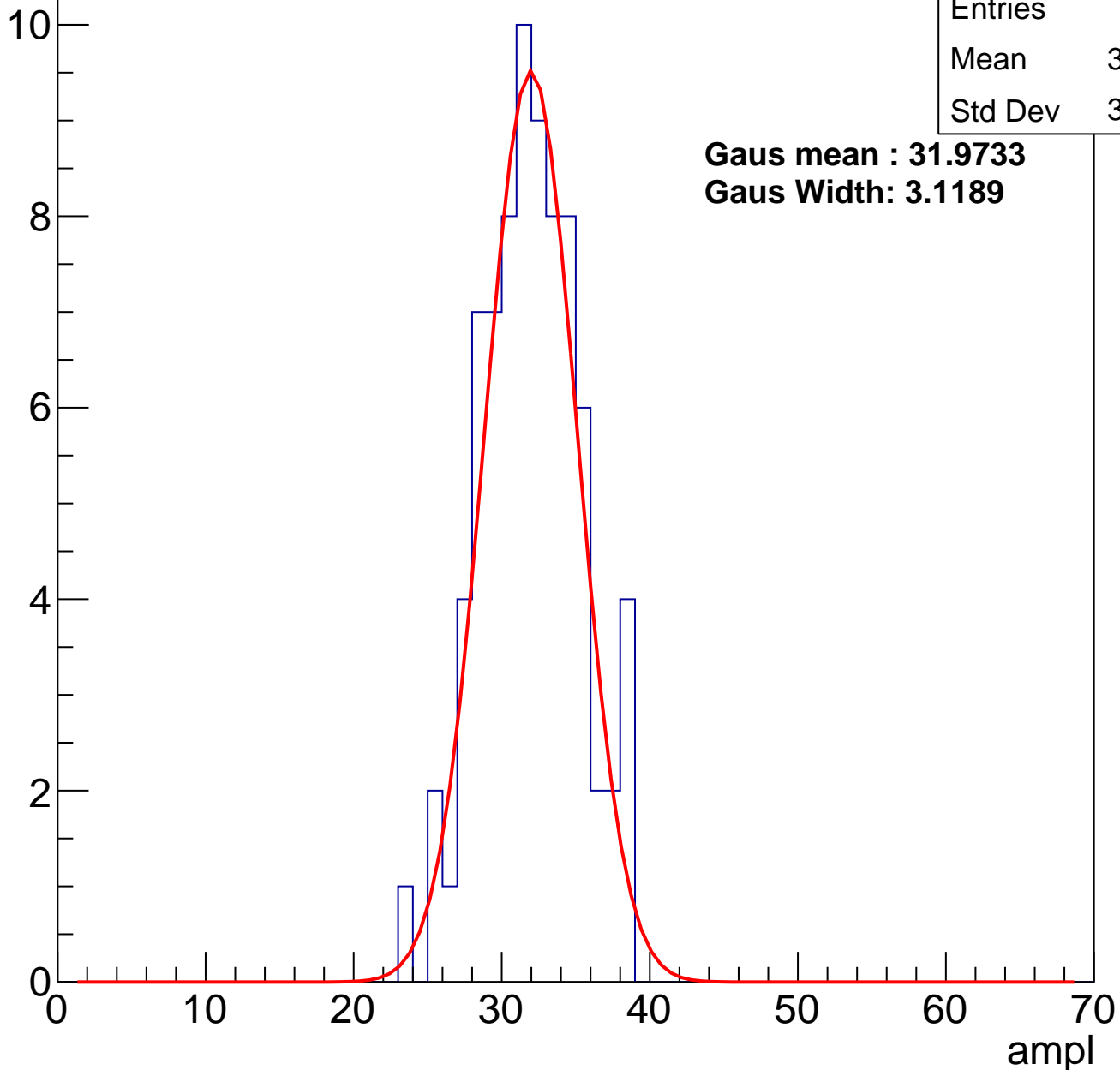
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	31.49
Std Dev	3.245

Gaus mean : 31.9733

Gaus Width: 3.1189

Entry



B0L002S, U12-ch41, adc1

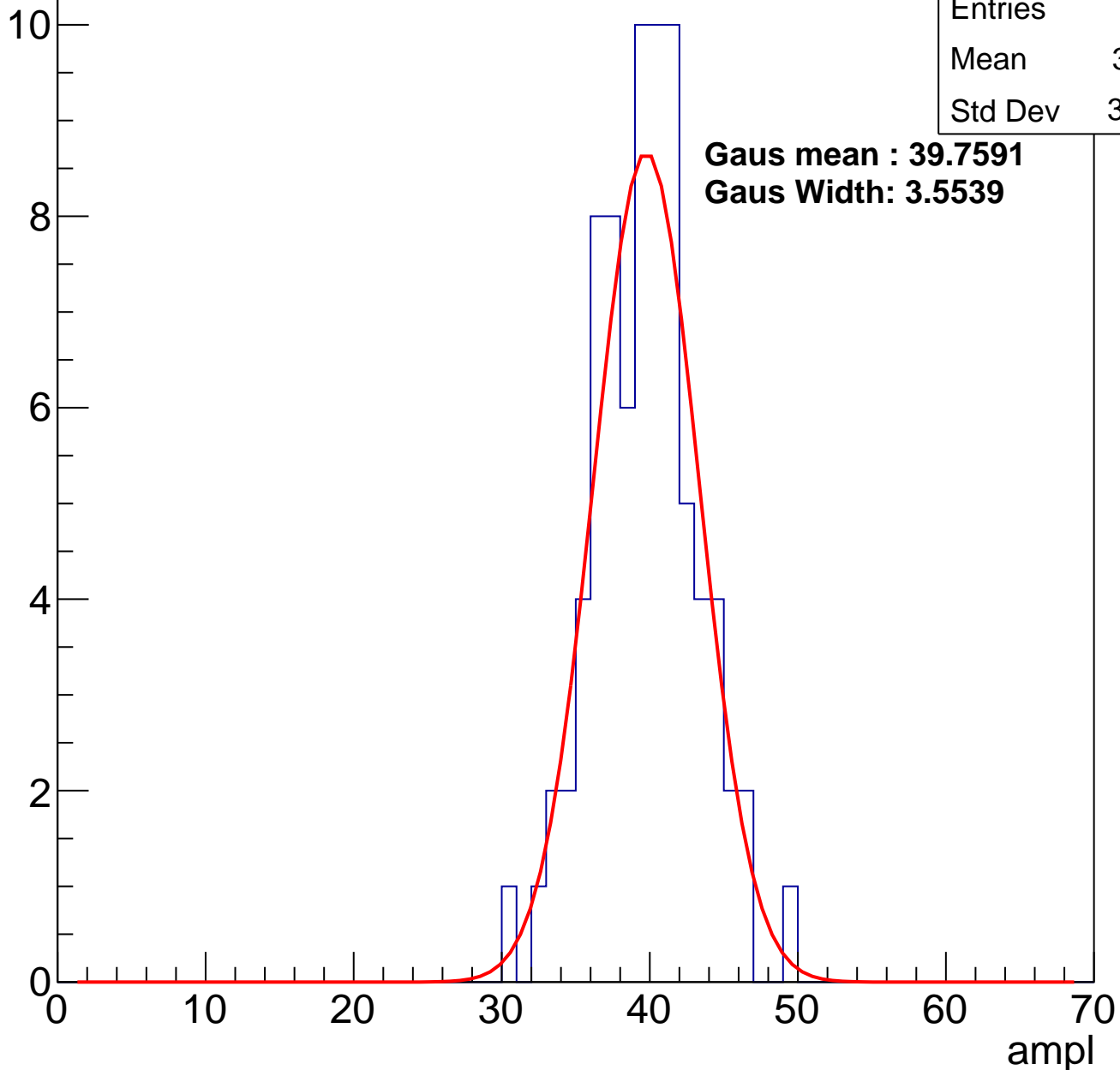
calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	39.21
Std Dev	3.449

Gaus mean : 39.7591

Gaus Width: 3.5539

Entry



B0L002S, U12-ch41, adc2

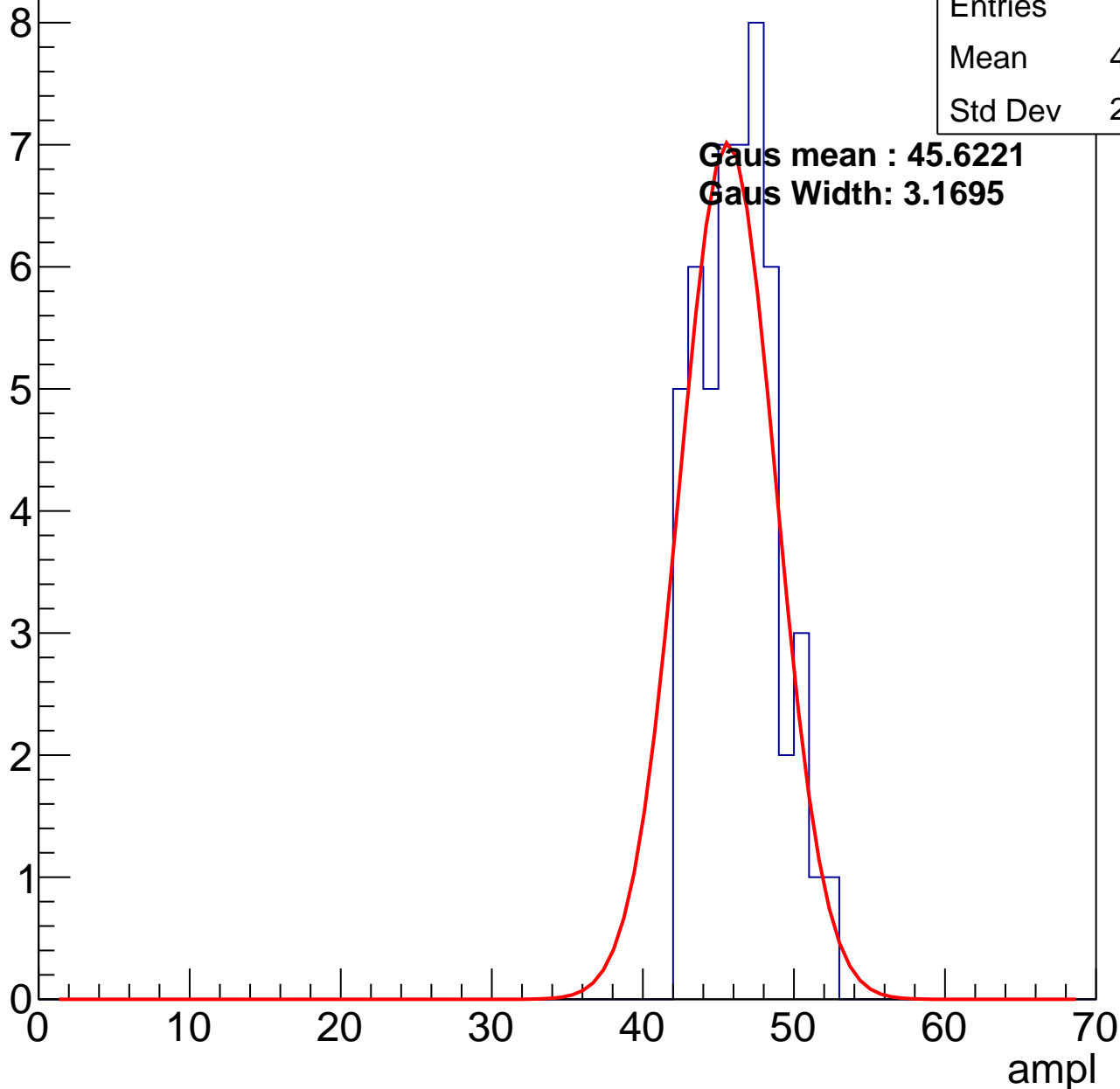
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.88
Std Dev	2.502

Gaus mean : 45.6221

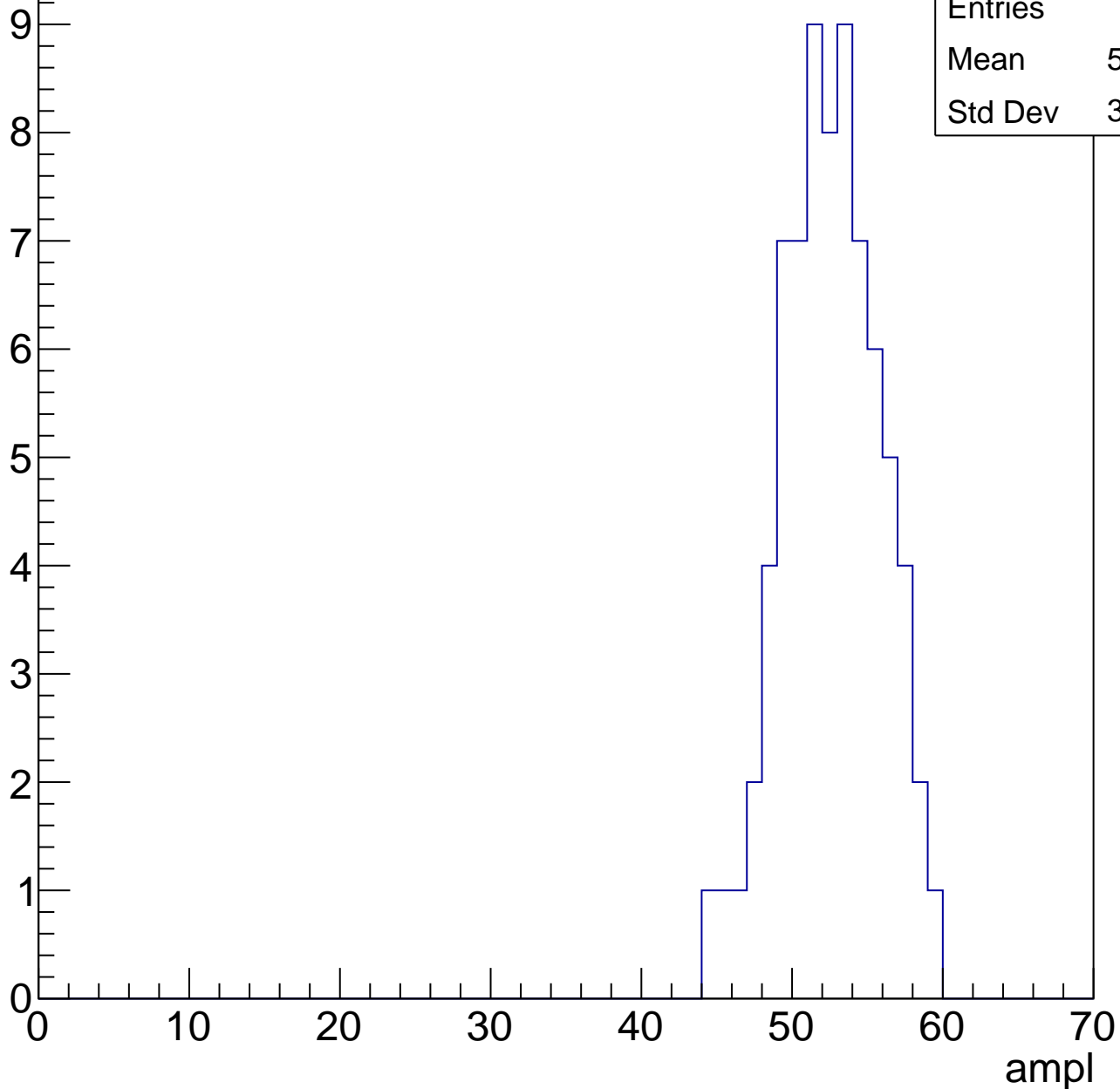
Gaus Width: 3.1695



B0L002S, U12-ch41, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch41, adc4

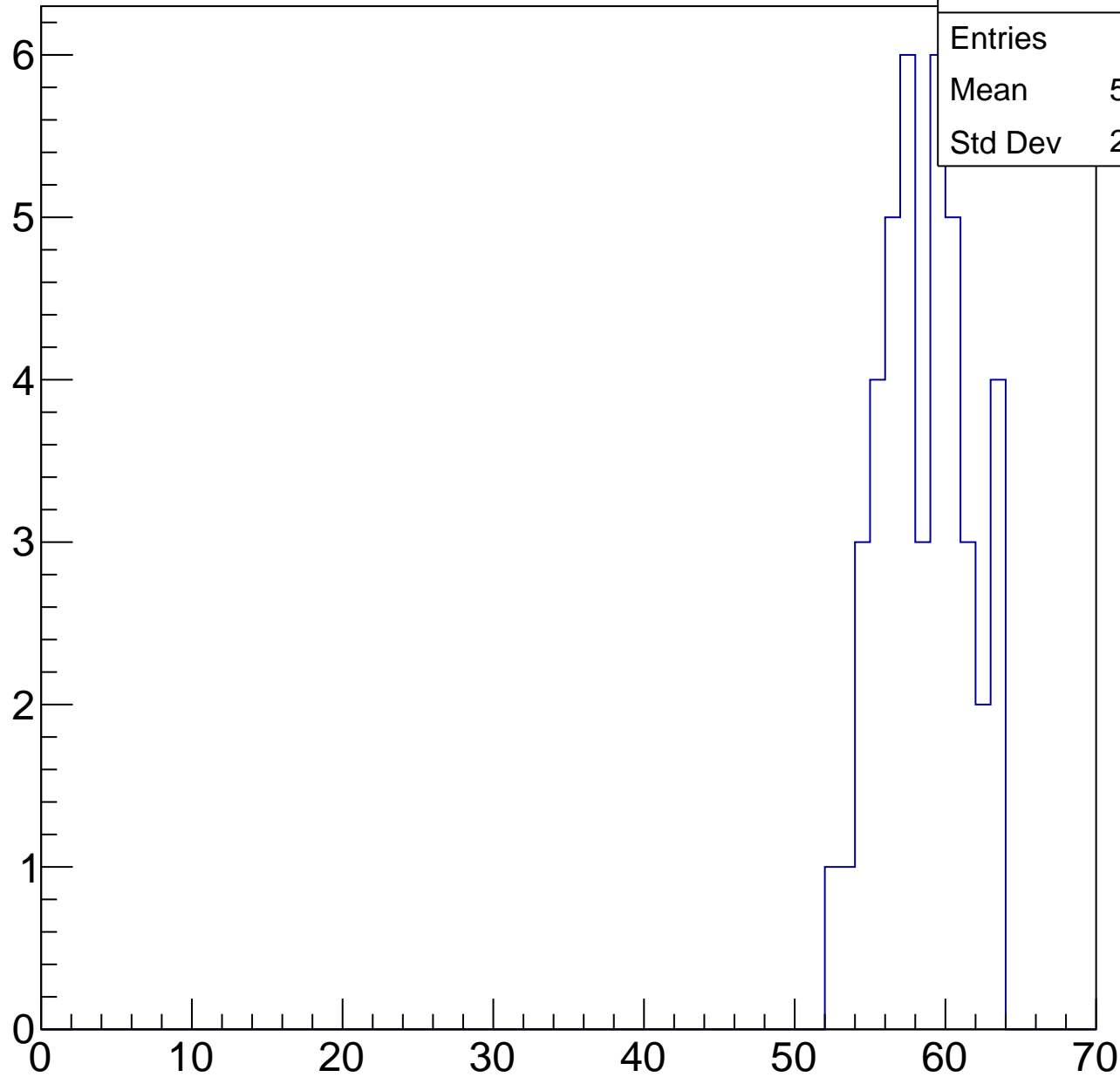
calib_packv5_042523_0143.root, FC#8, port C1

Entry

6
5
4
3
2
1
0

Entries	43
Mean	58.05
Std Dev	2.877

ampl

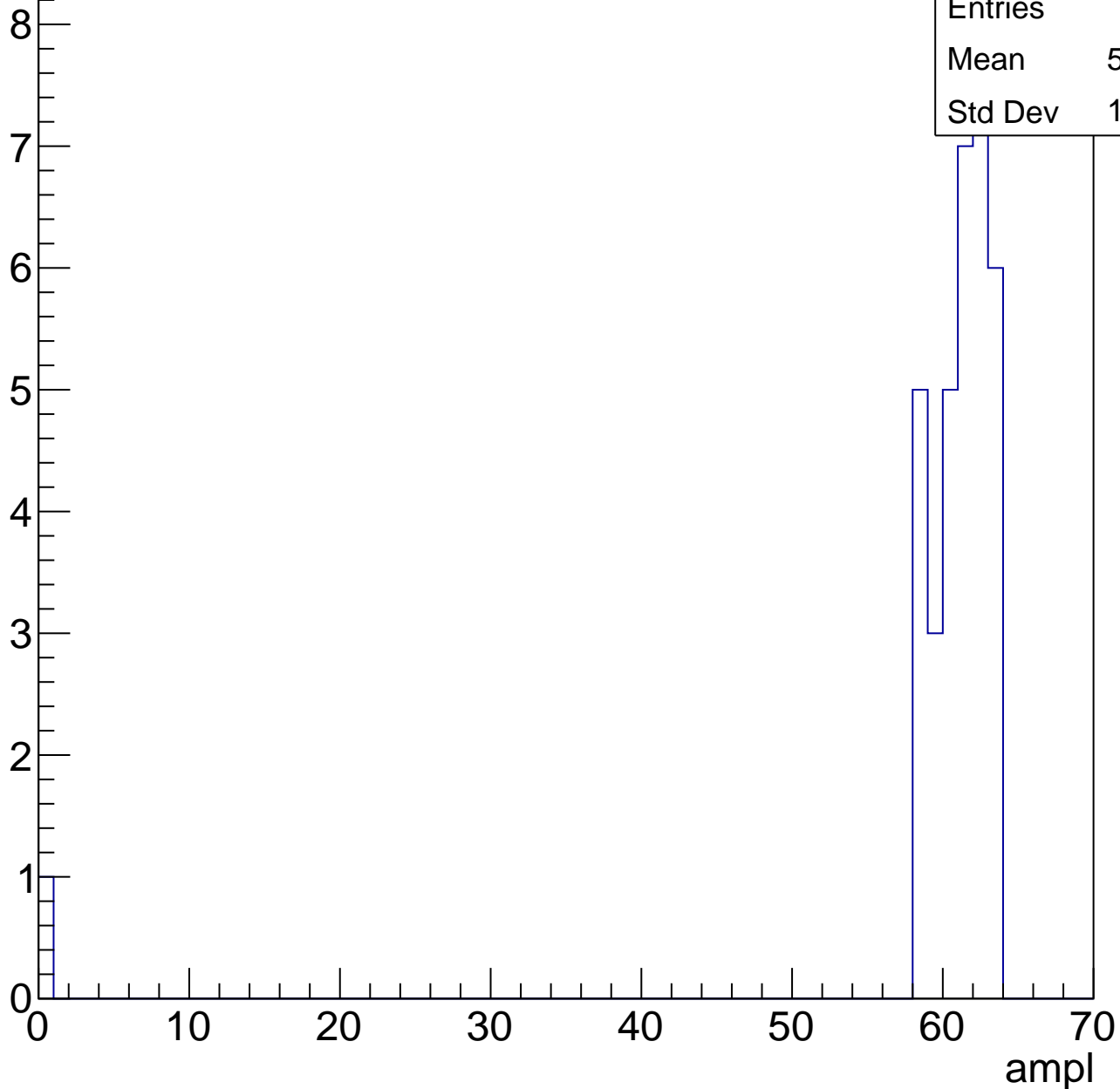


B0L002S, U12-ch41, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.09
Std Dev	10.26



B0L002S, U12-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch42, adc0

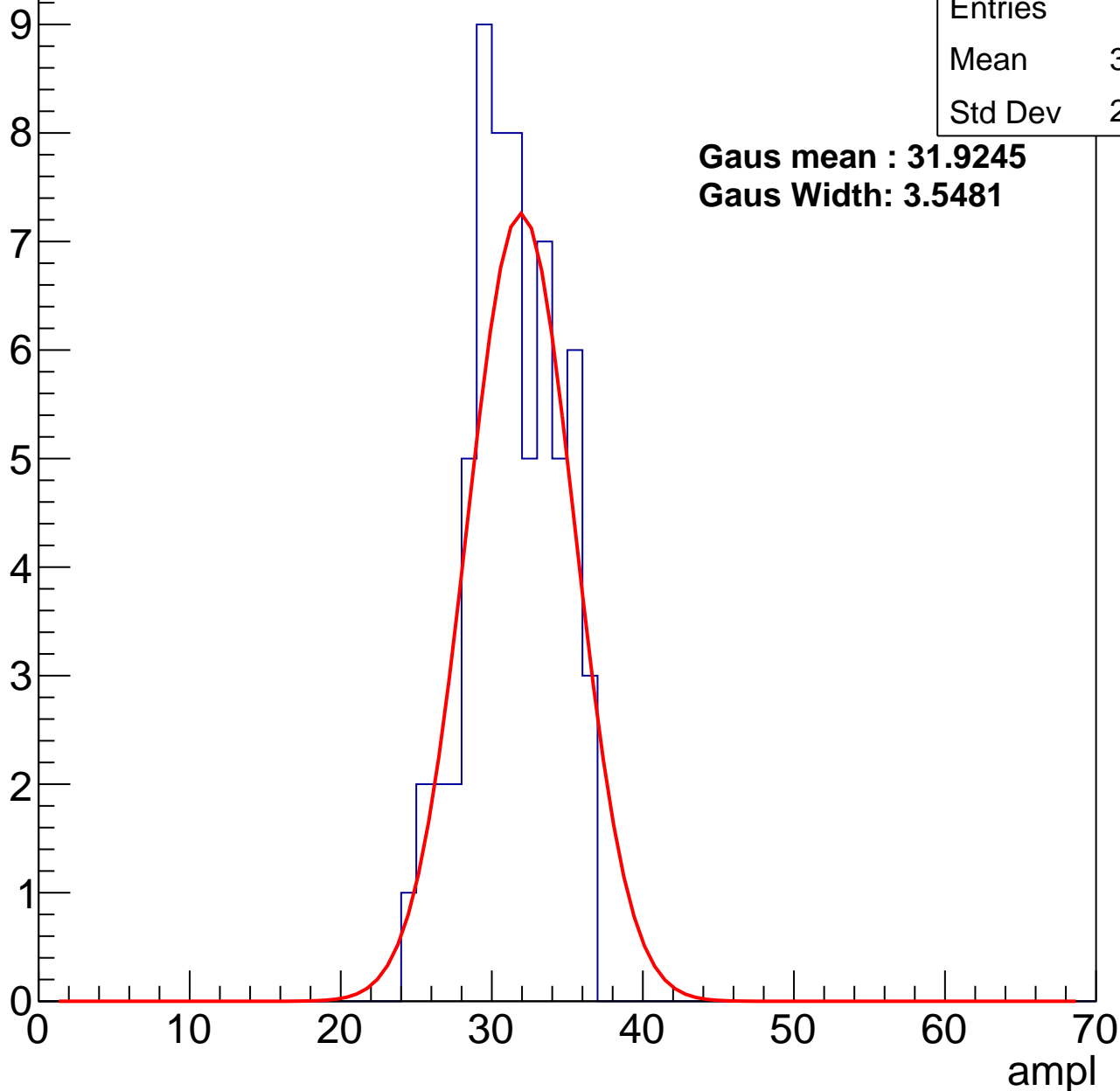
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.92
Std Dev	2.929

Gaus mean : 31.9245

Gaus Width: 3.5481



B0L002S, U12-ch42, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	37.16
Std Dev	3.116

Gaus mean : 37.5439

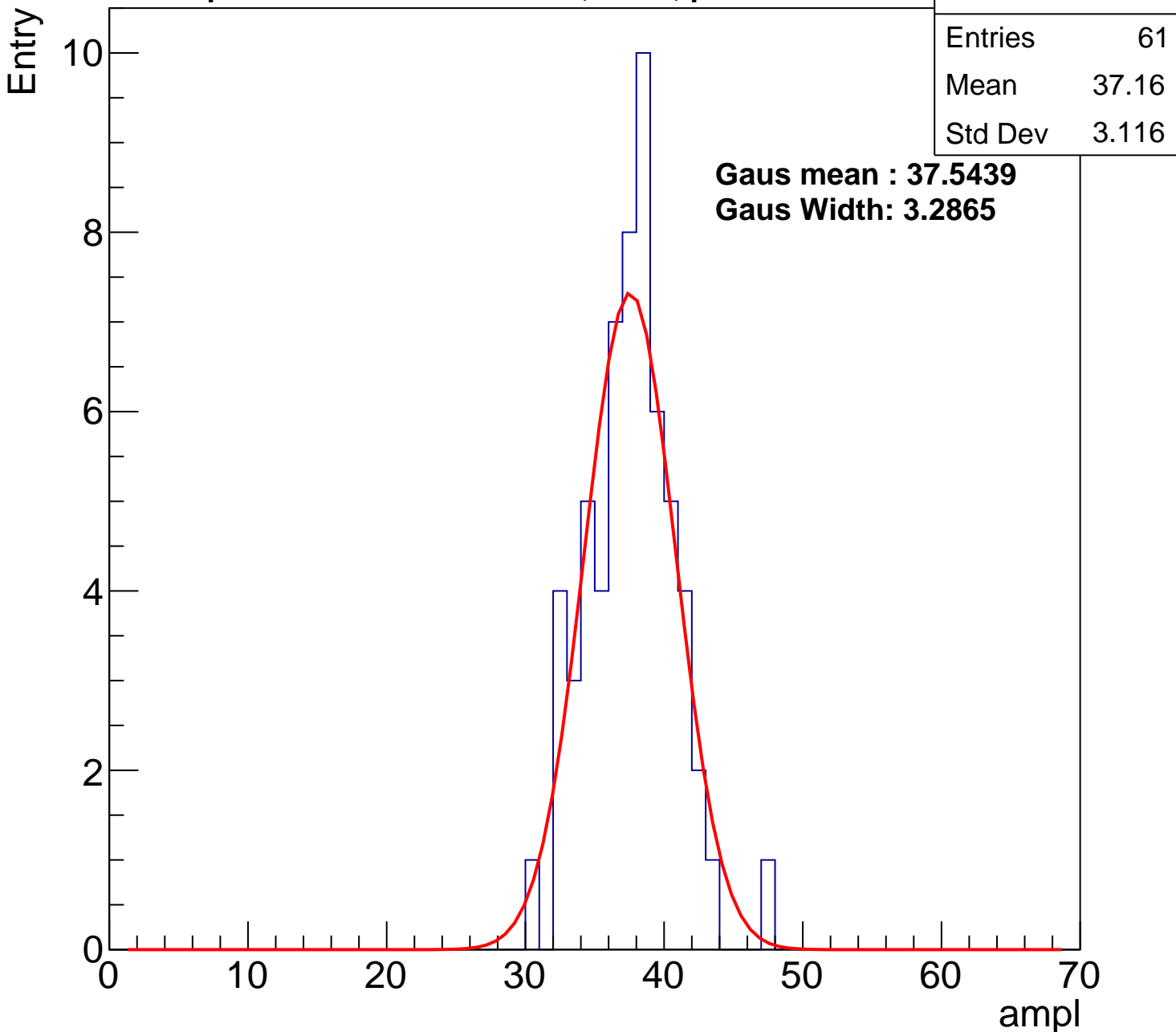
Gaus Width: 3.2865

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch42, adc2

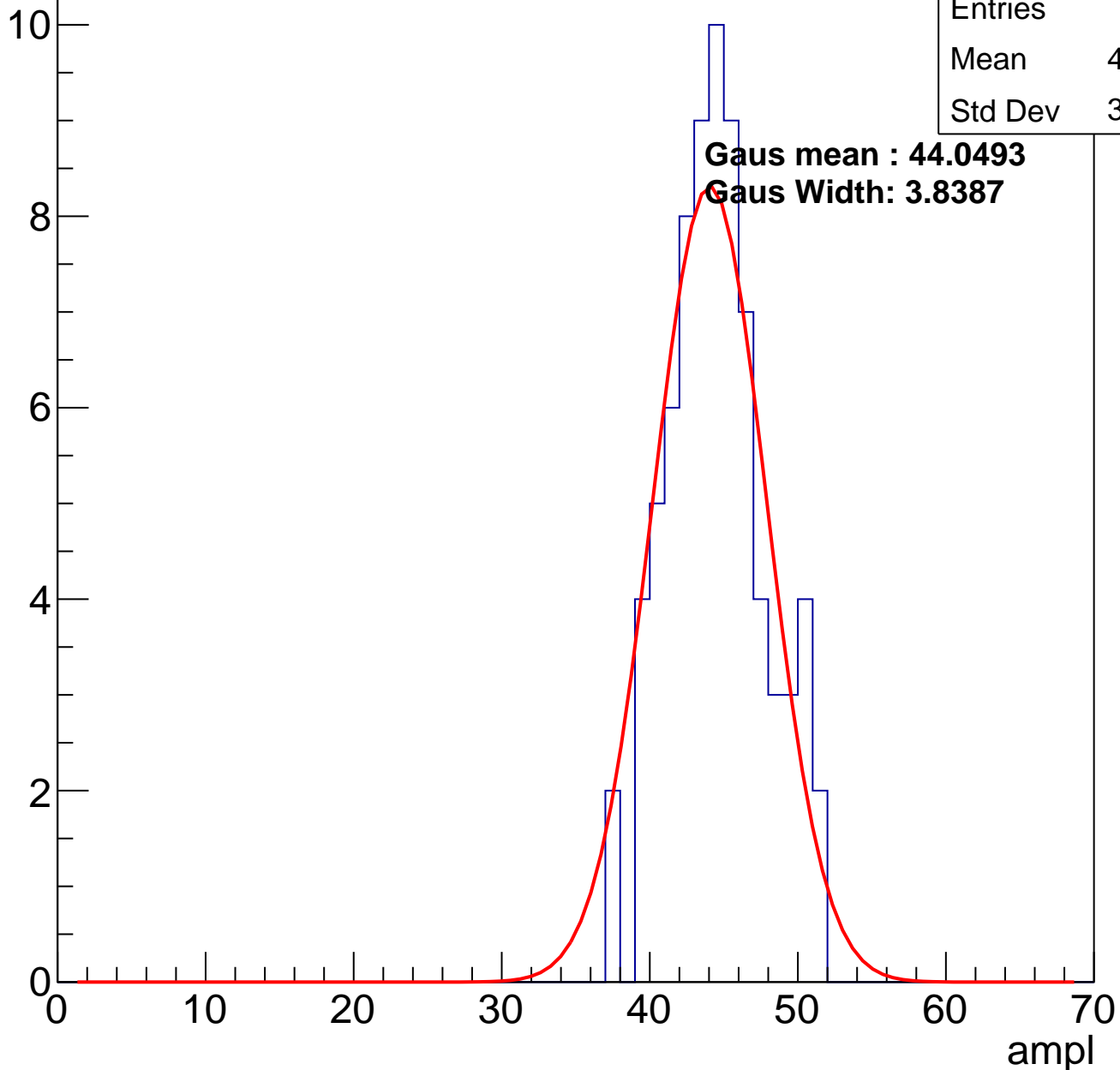
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	44.04
Std Dev	3.266

Gaus mean : 44.0493

Gaus Width: 3.8387

Entry

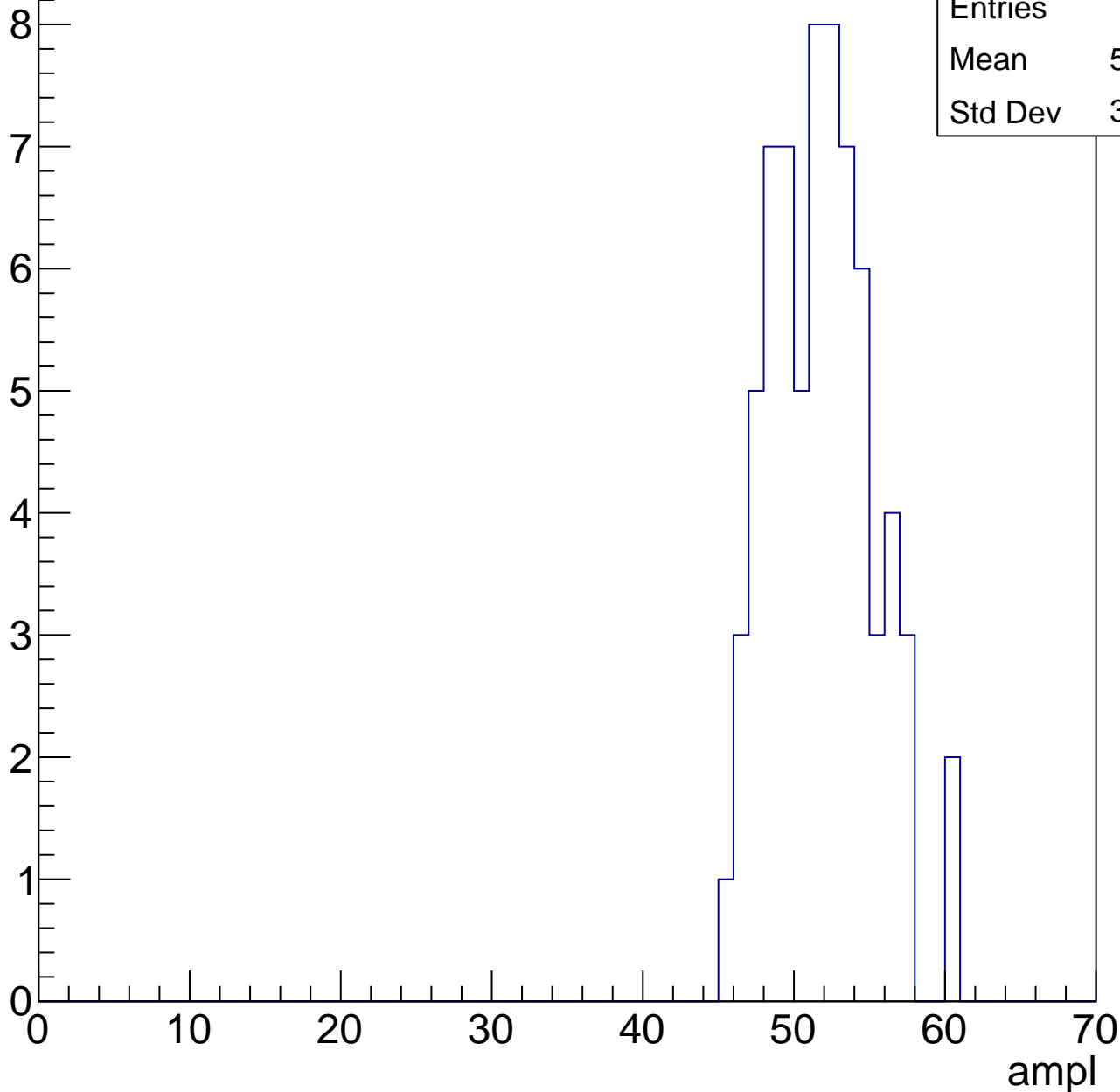


B0L002S, U12-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

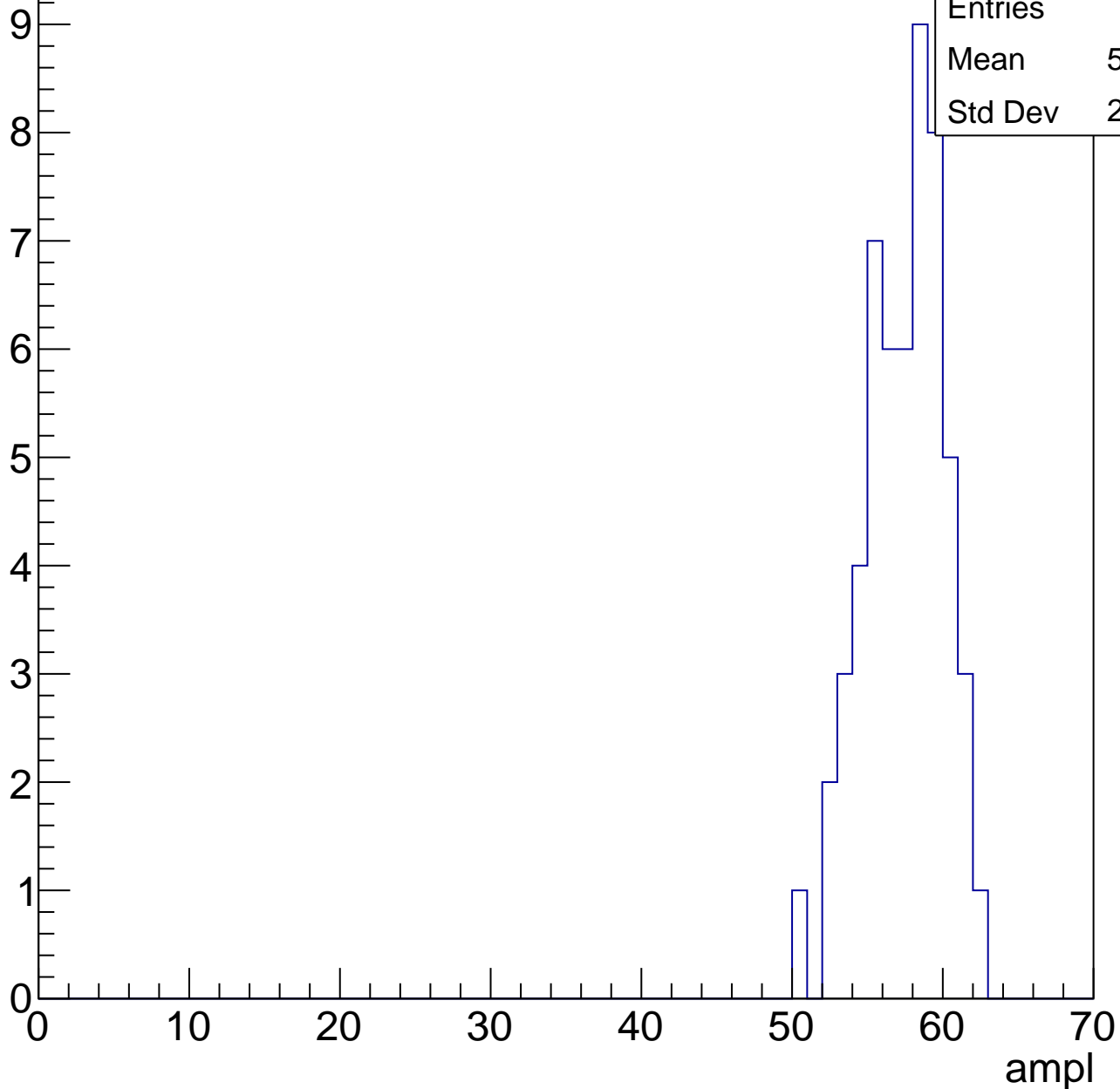
Entries	69
Mean	51.39
Std Dev	3.372



B0L002S, U12-ch42, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

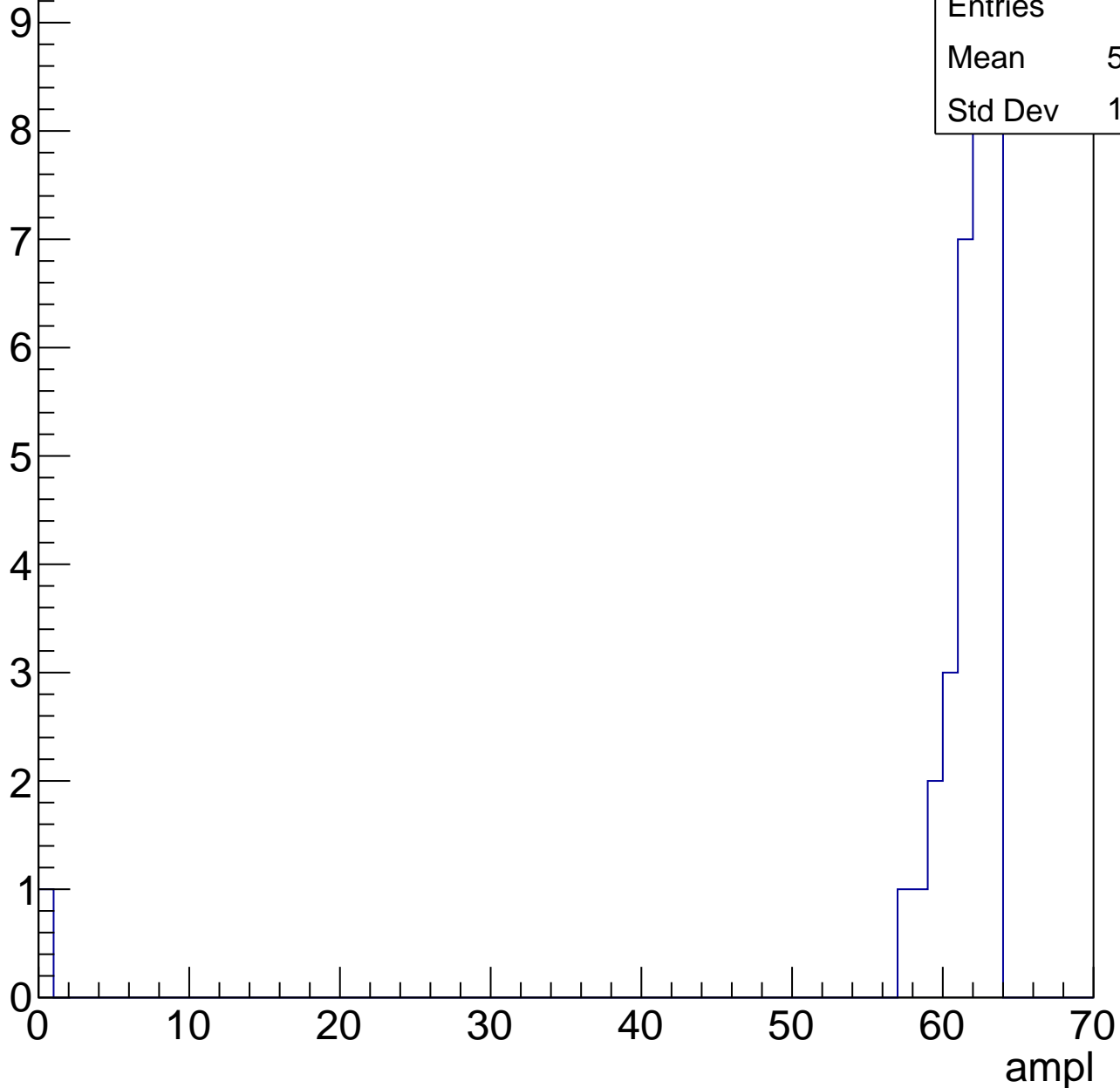


Entries	55
Mean	56.93
Std Dev	2.614

B0L002S, U12-ch42, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	32
Mean	59.44
Std Dev	10.78

B0L002S, U12-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch43, adc0

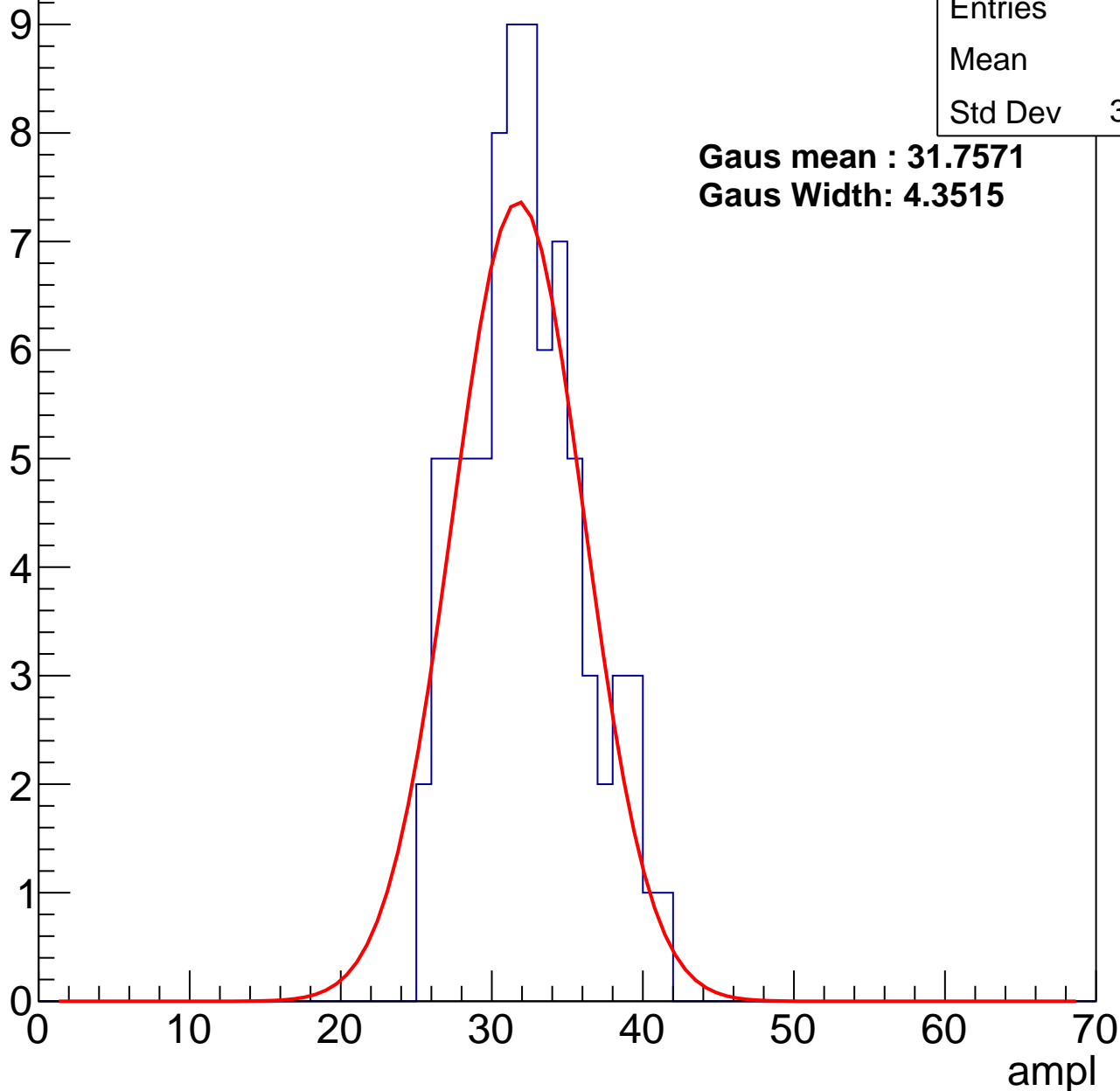
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	31.8
Std Dev	3.803

Gaus mean : 31.7571

Gaus Width: 4.3515



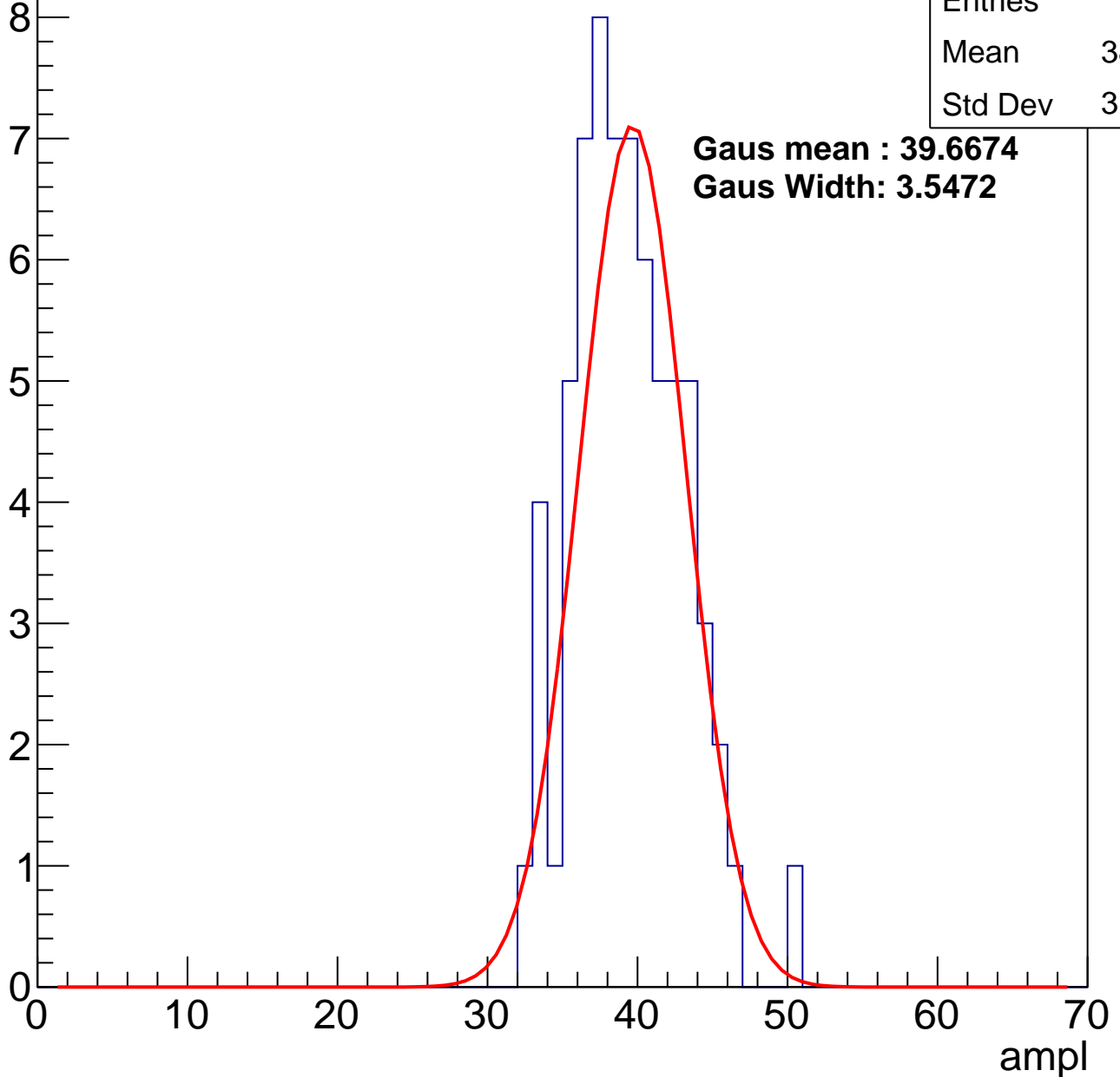
B0L002S, U12-ch43, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38.94
Std Dev	3.572

Gaus mean : 39.6674
Gaus Width: 3.5472



B0L002S, U12-ch43, adc2

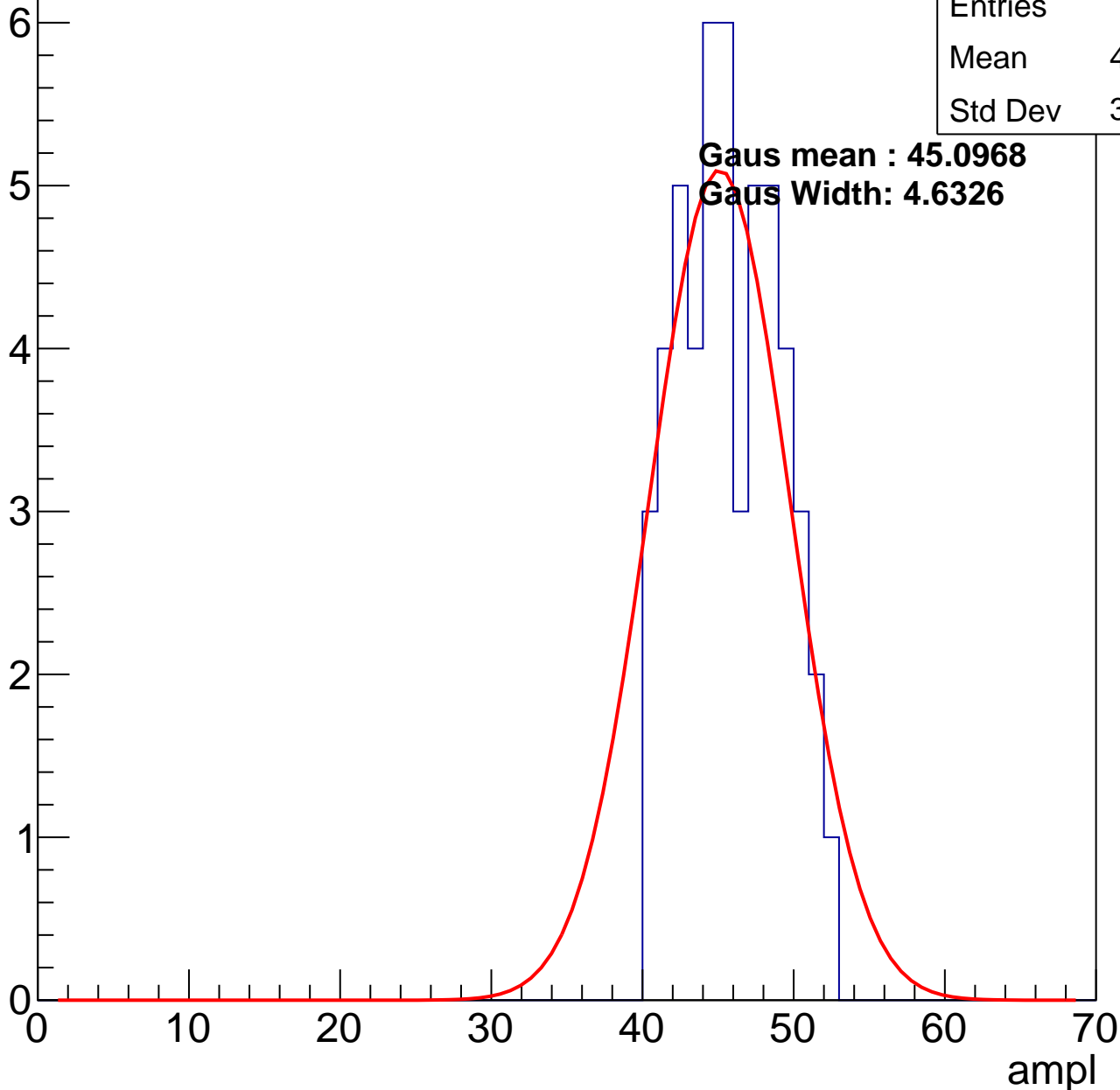
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.35
Std Dev	3.217

Gaus mean : 45.0968

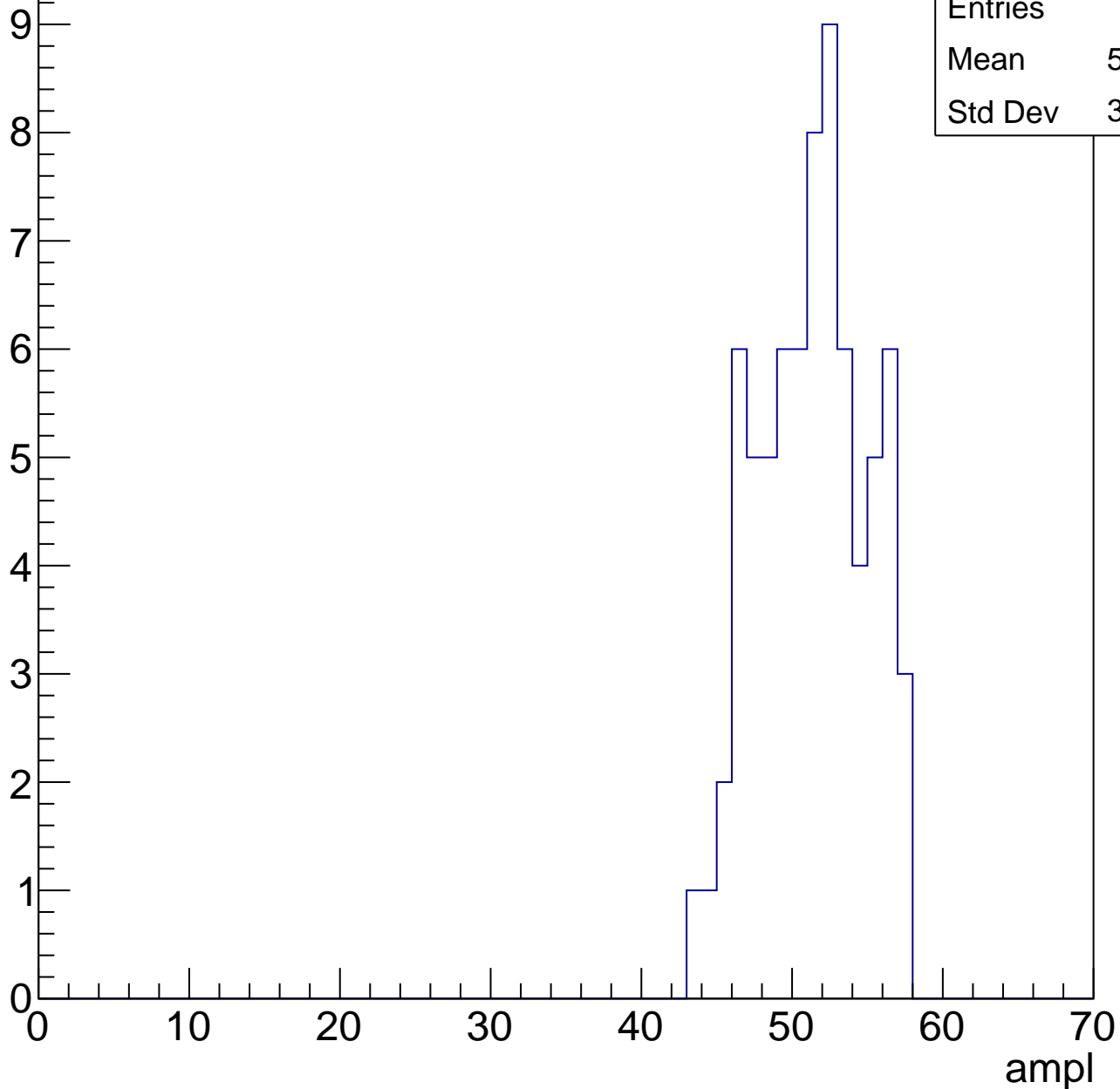
Gaus Width: 4.6326



B0L002S, U12-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

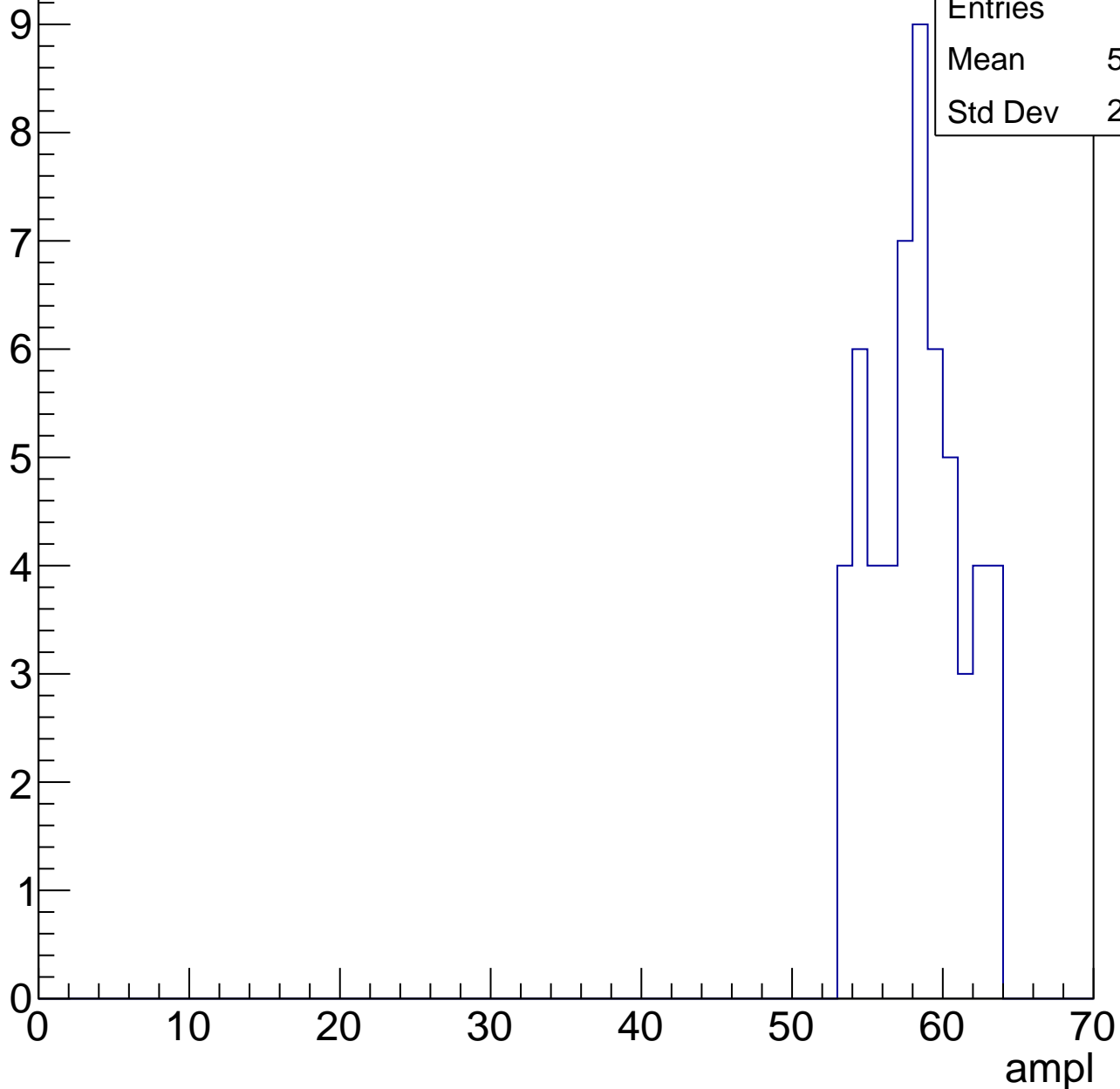


Entries	73
Mean	50.88
Std Dev	3.503

B0L002S, U12-ch43, adc4

calib_packv5_042523_0143.root, FC#8, port C1

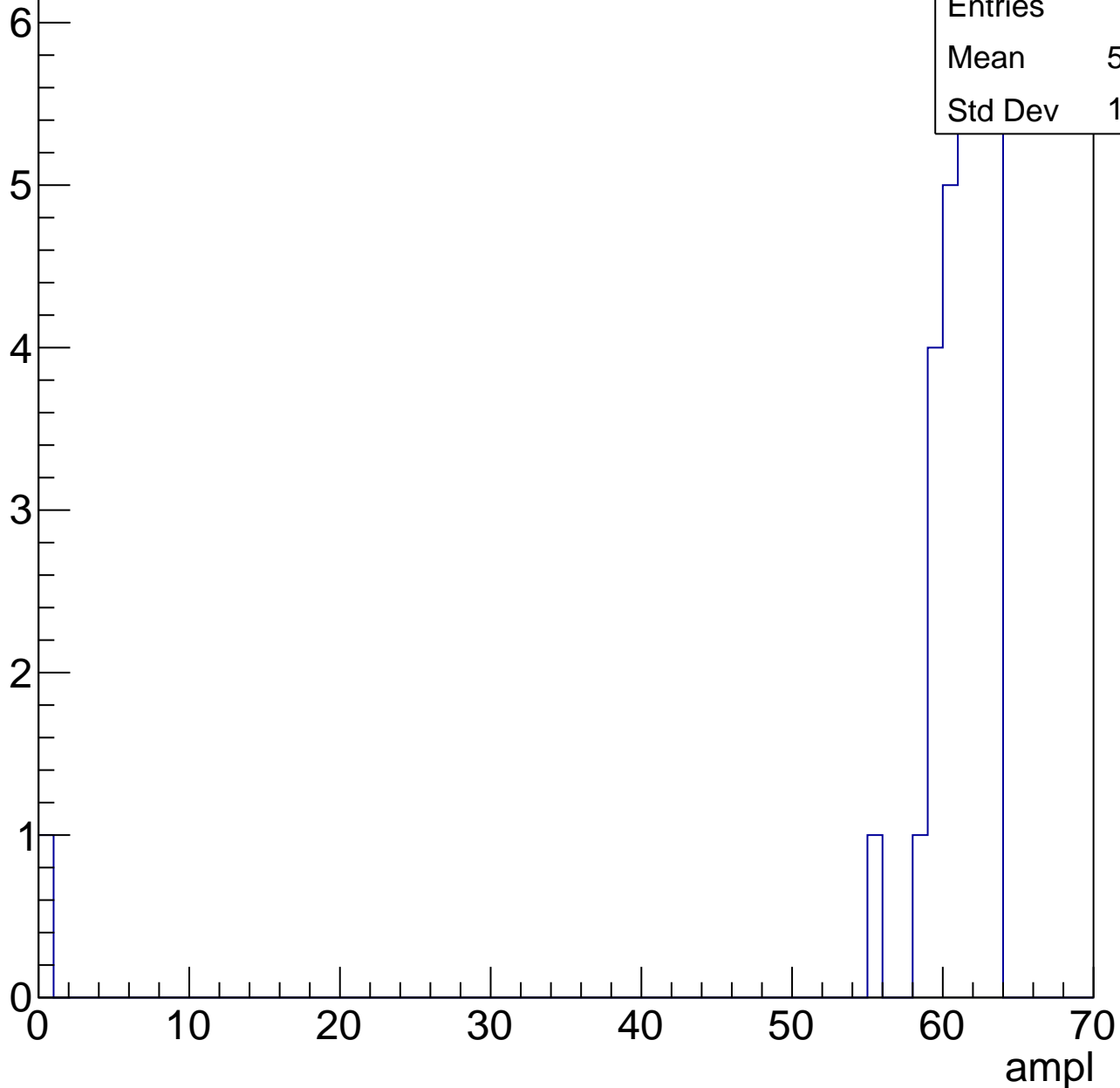
Entry



B0L002S, U12-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch44, adc0

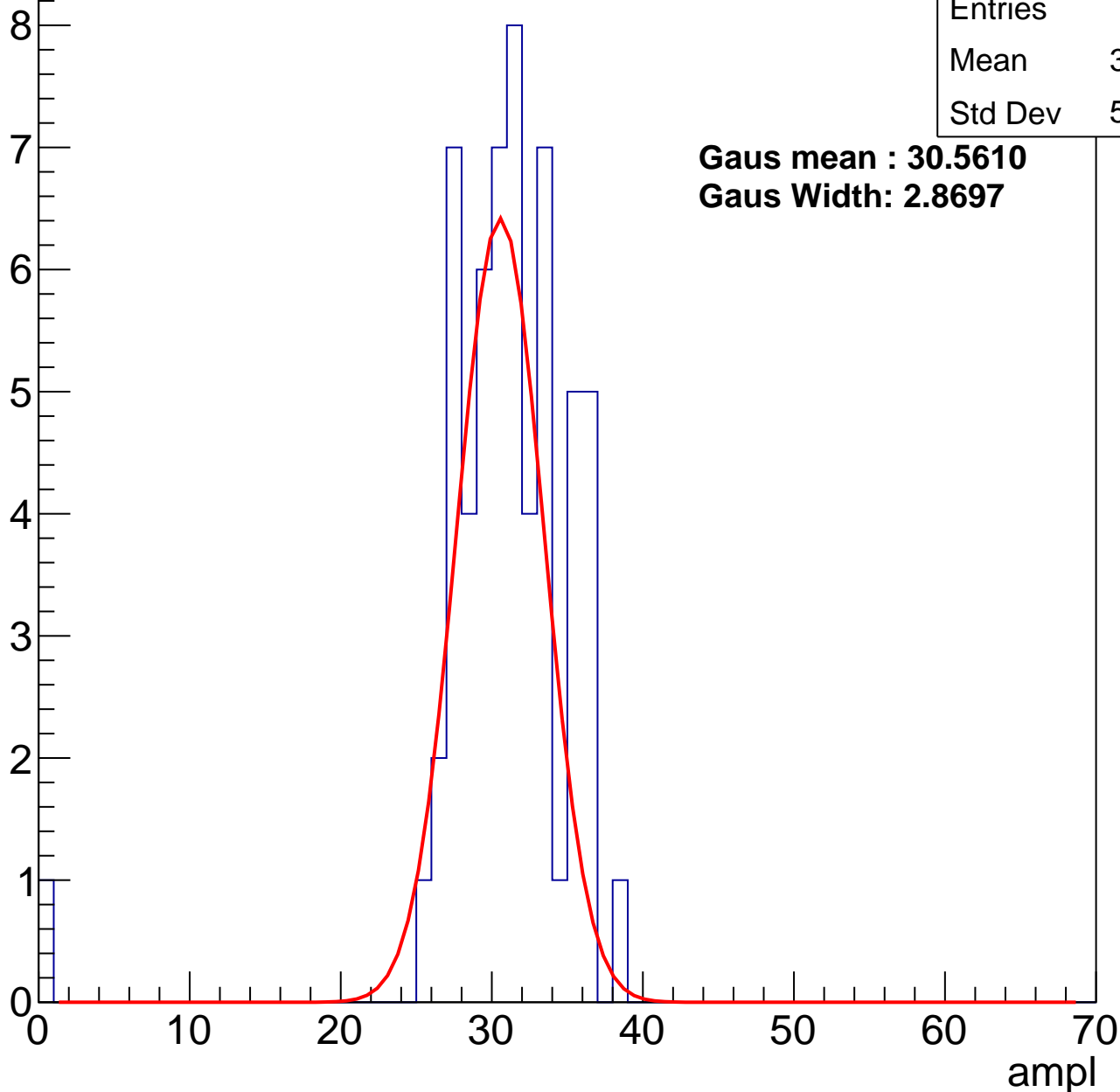
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.44
Std Dev	5.046

Gaus mean : 30.5610

Gaus Width: 2.8697



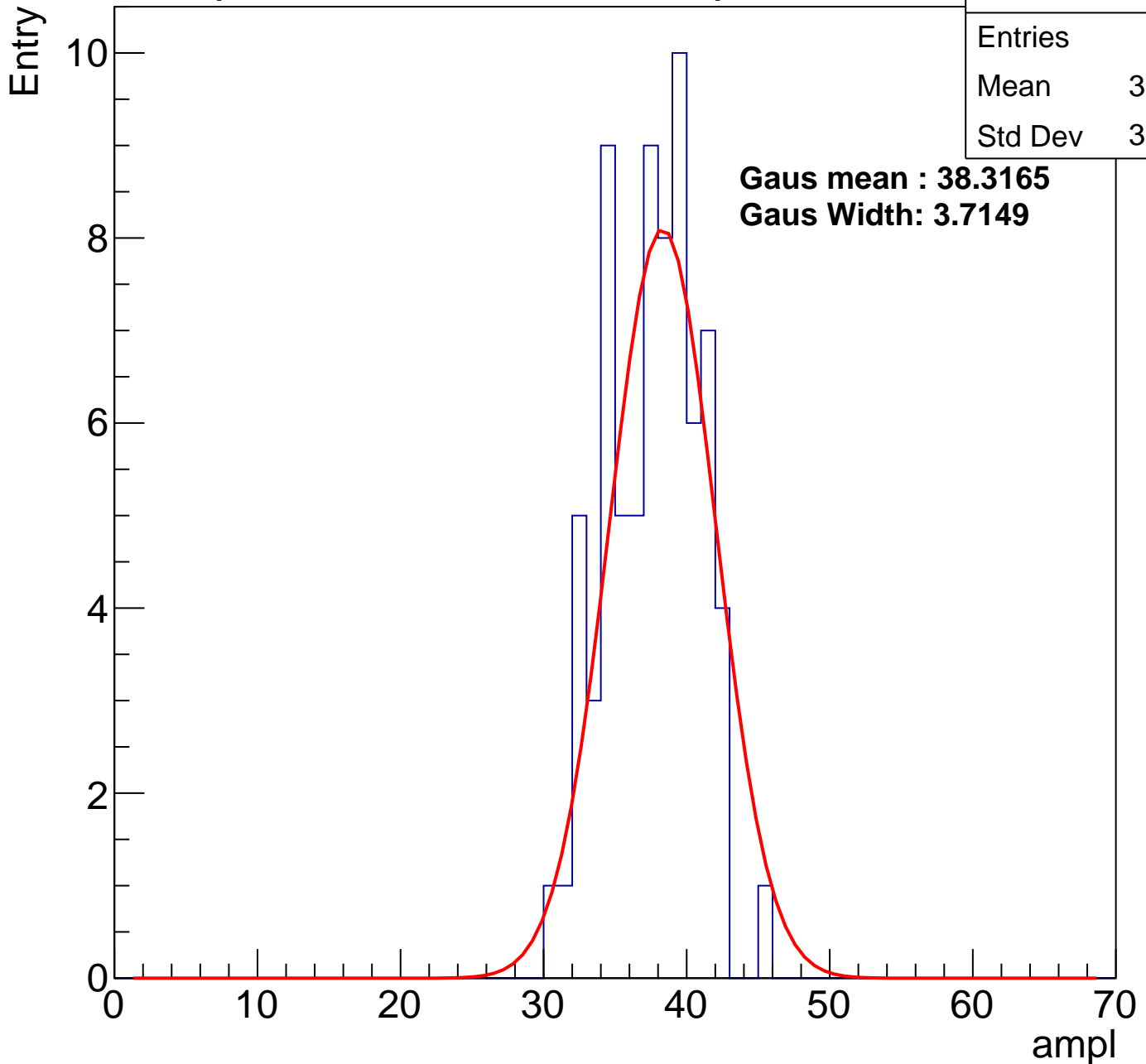
B0L002S, U12-ch44, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	37.14
Std Dev	3.164

Gaus mean : 38.3165

Gaus Width: 3.7149



B0L002S, U12-ch44, adc2

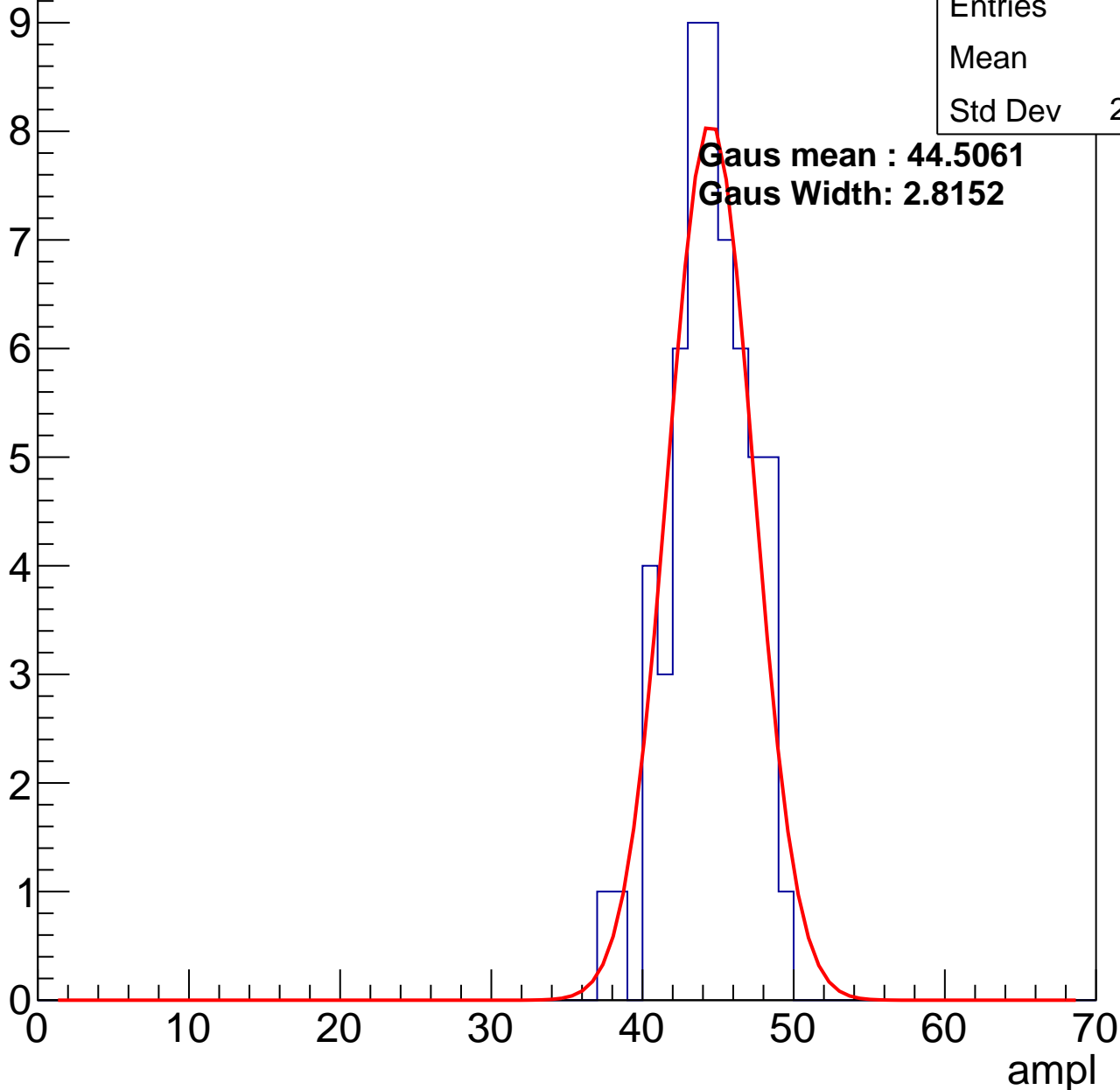
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44
Std Dev	2.616

Gaus mean : 44.5061

Gaus Width: 2.8152

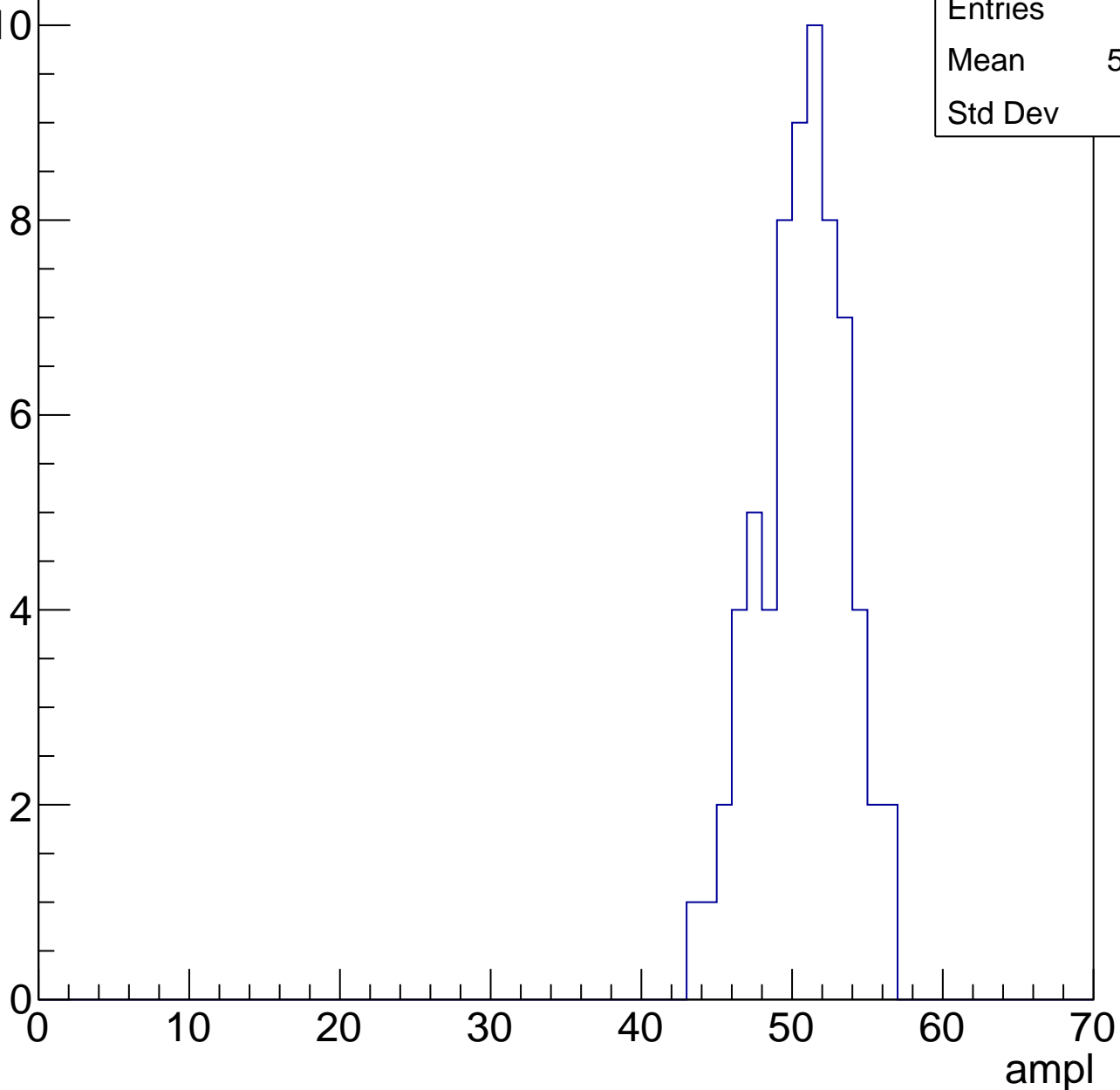


B0L002S, U12-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.22
Std Dev	2.88

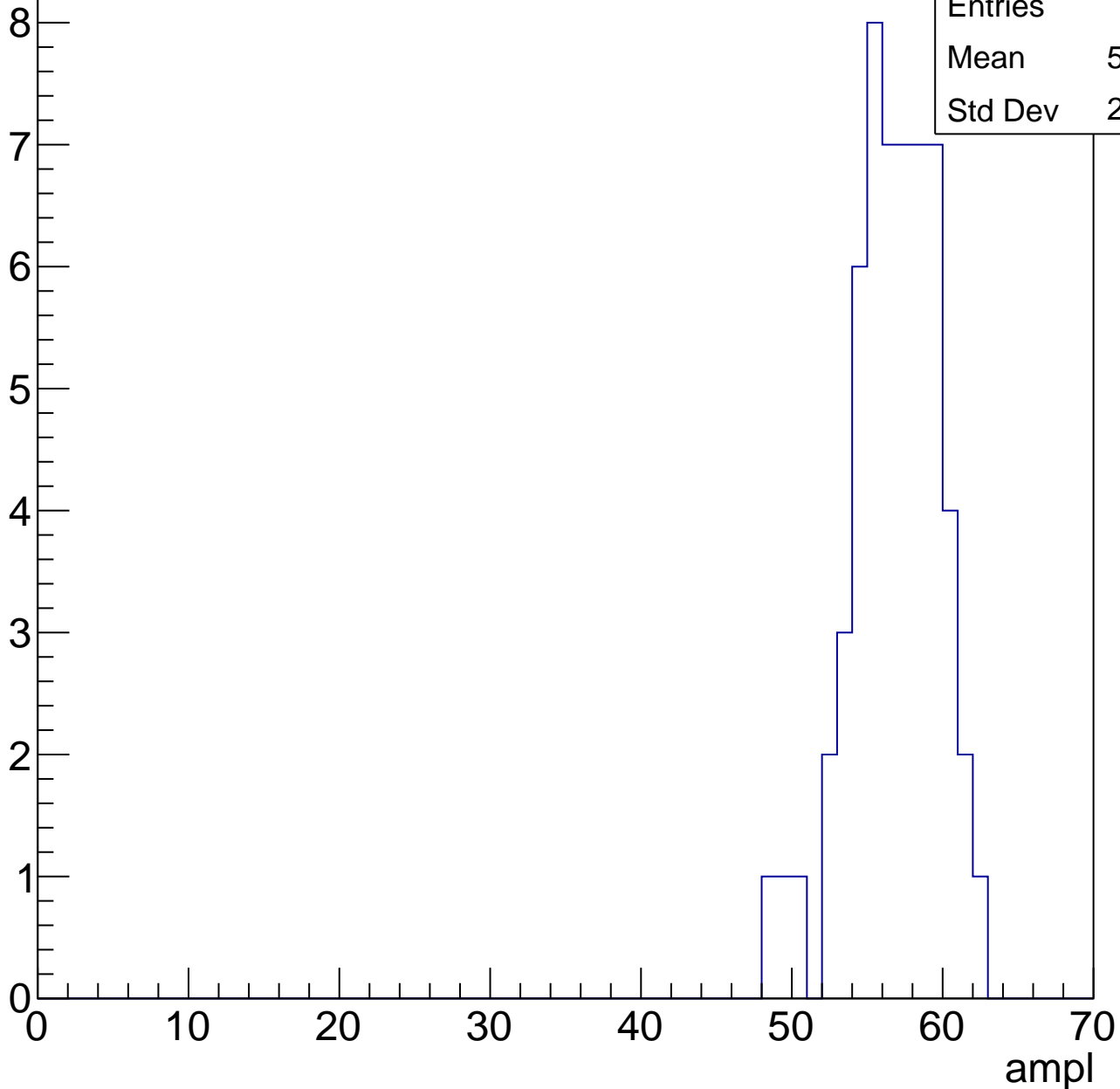


B0L002S, U12-ch44, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

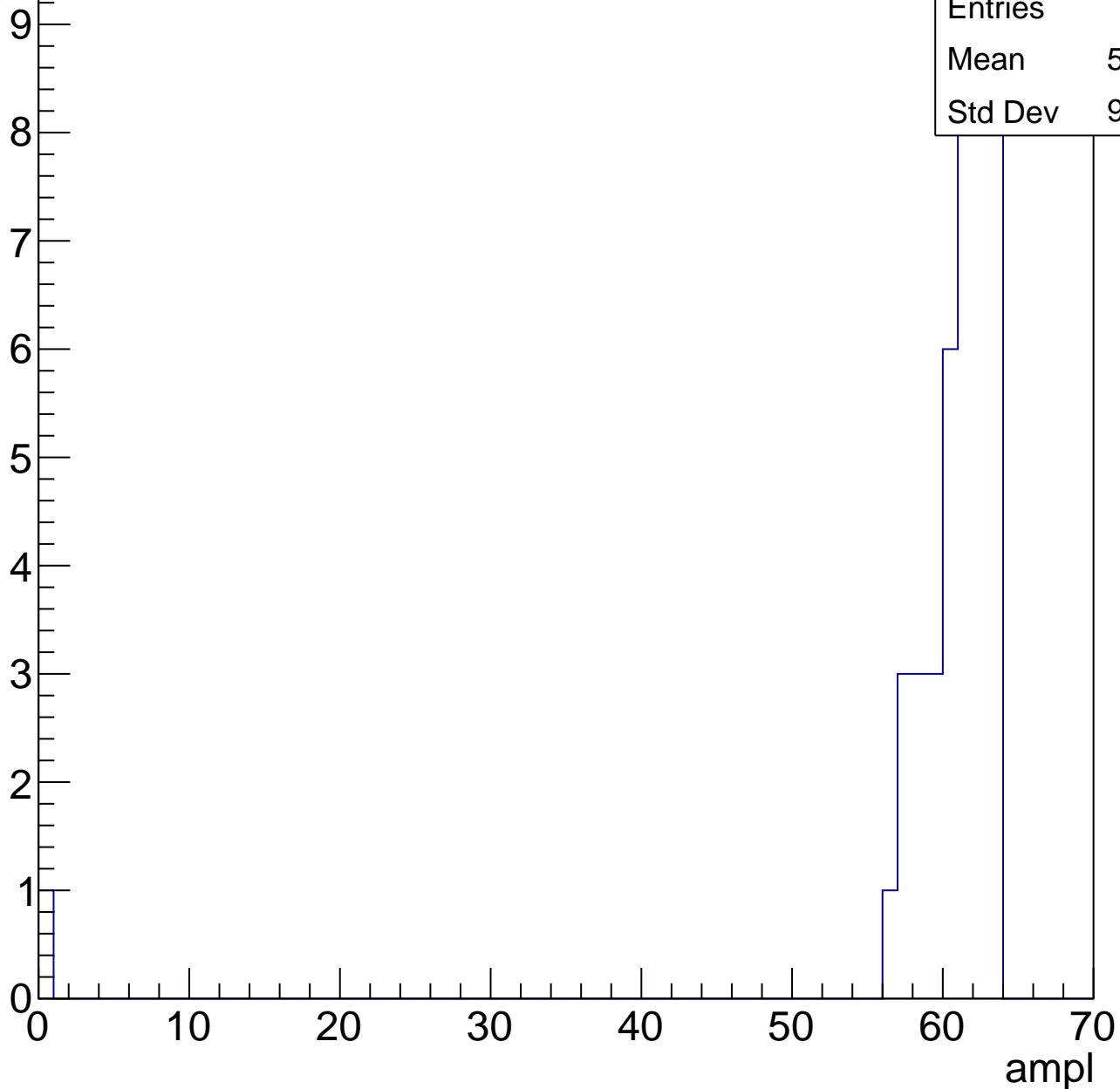
Entries	57
Mean	56.28
Std Dev	2.912



B0L002S, U12-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch45, adc0

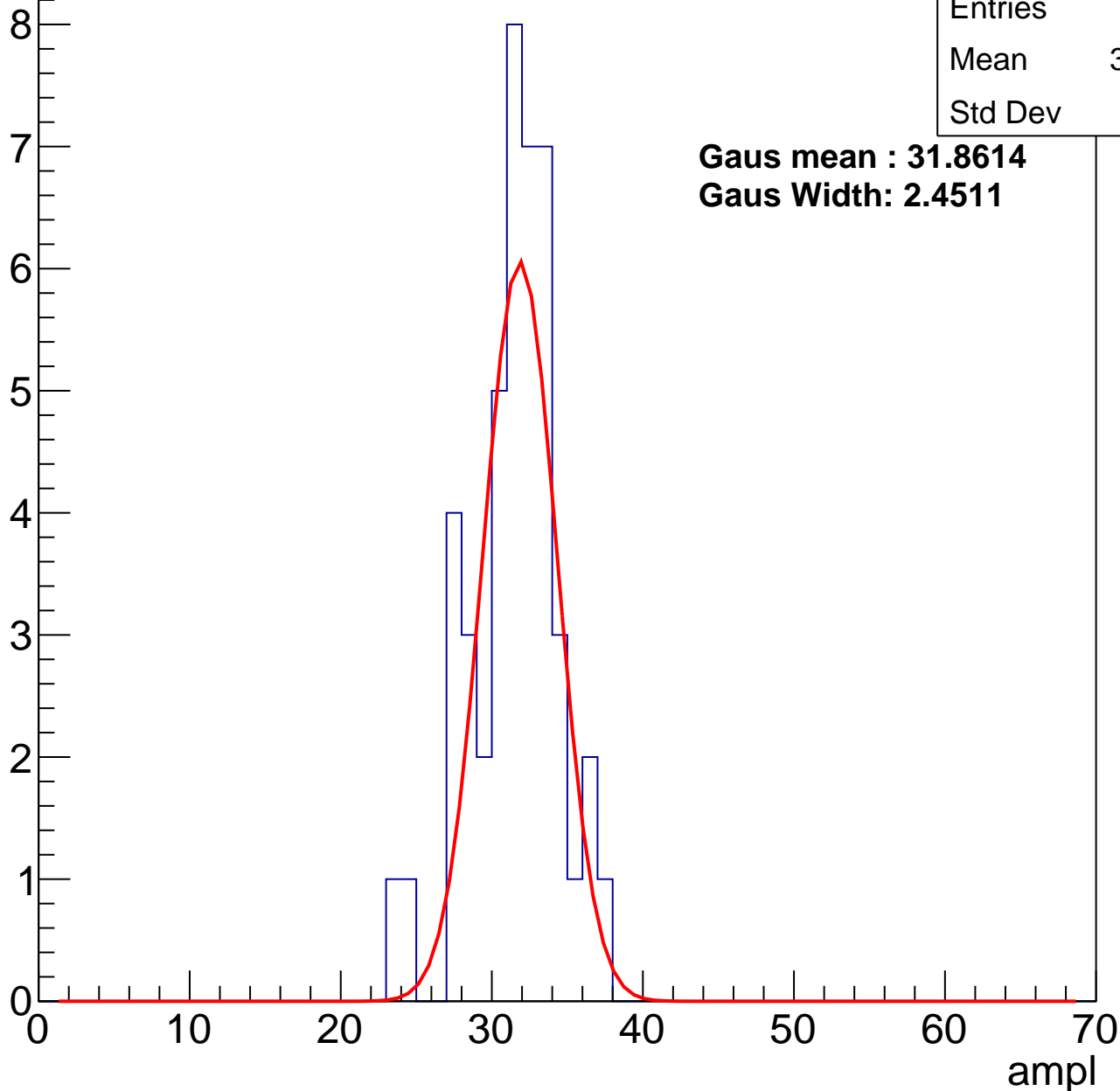
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	31.02
Std Dev	2.91

Gaus mean : 31.8614

Gaus Width: 2.4511



B0L002S, U12-ch45, adc1

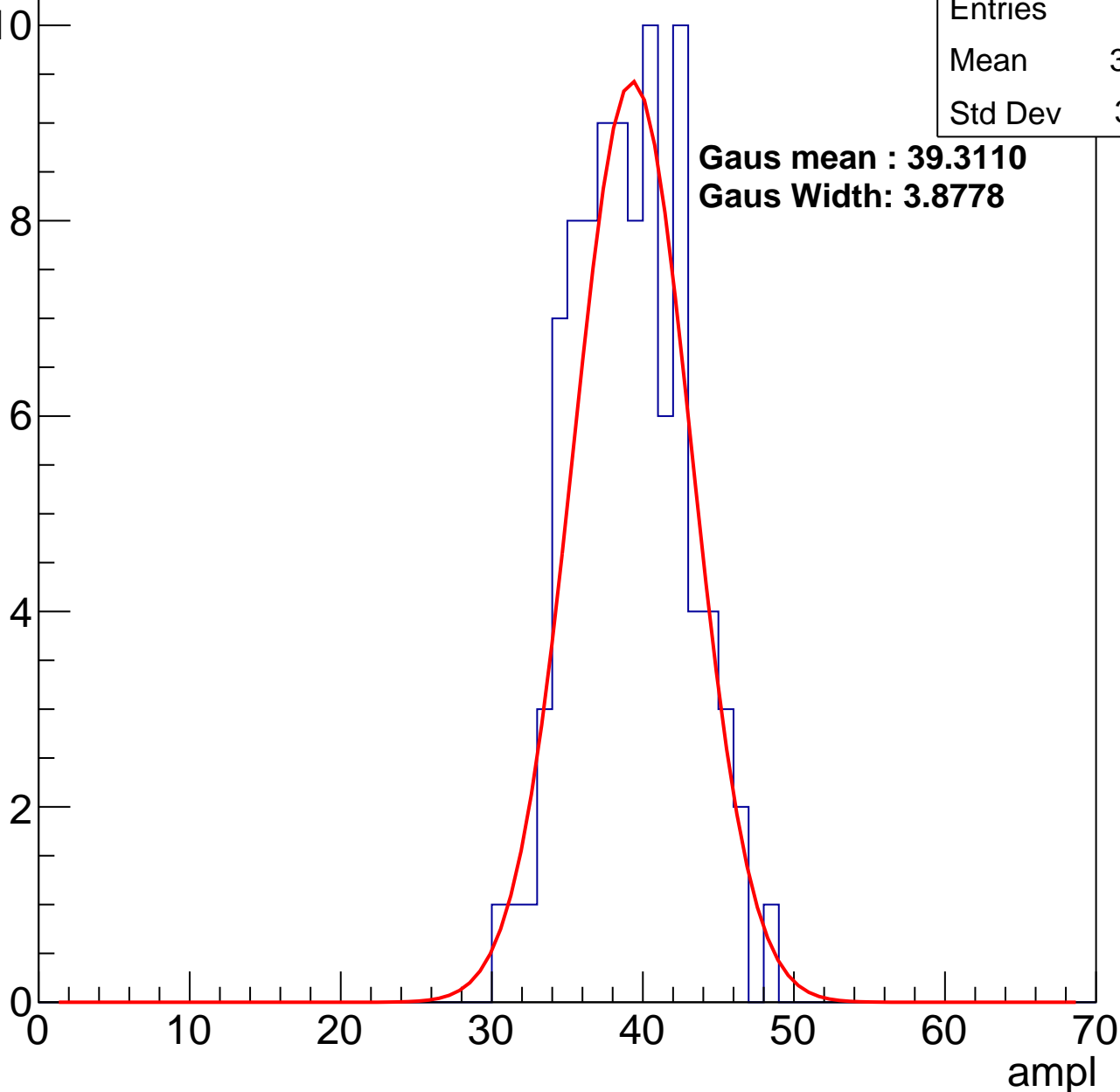
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	95
Mean	38.67
Std Dev	3.681

Gaus mean : 39.3110

Gaus Width: 3.8778



B0L002S, U12-ch45, adc2

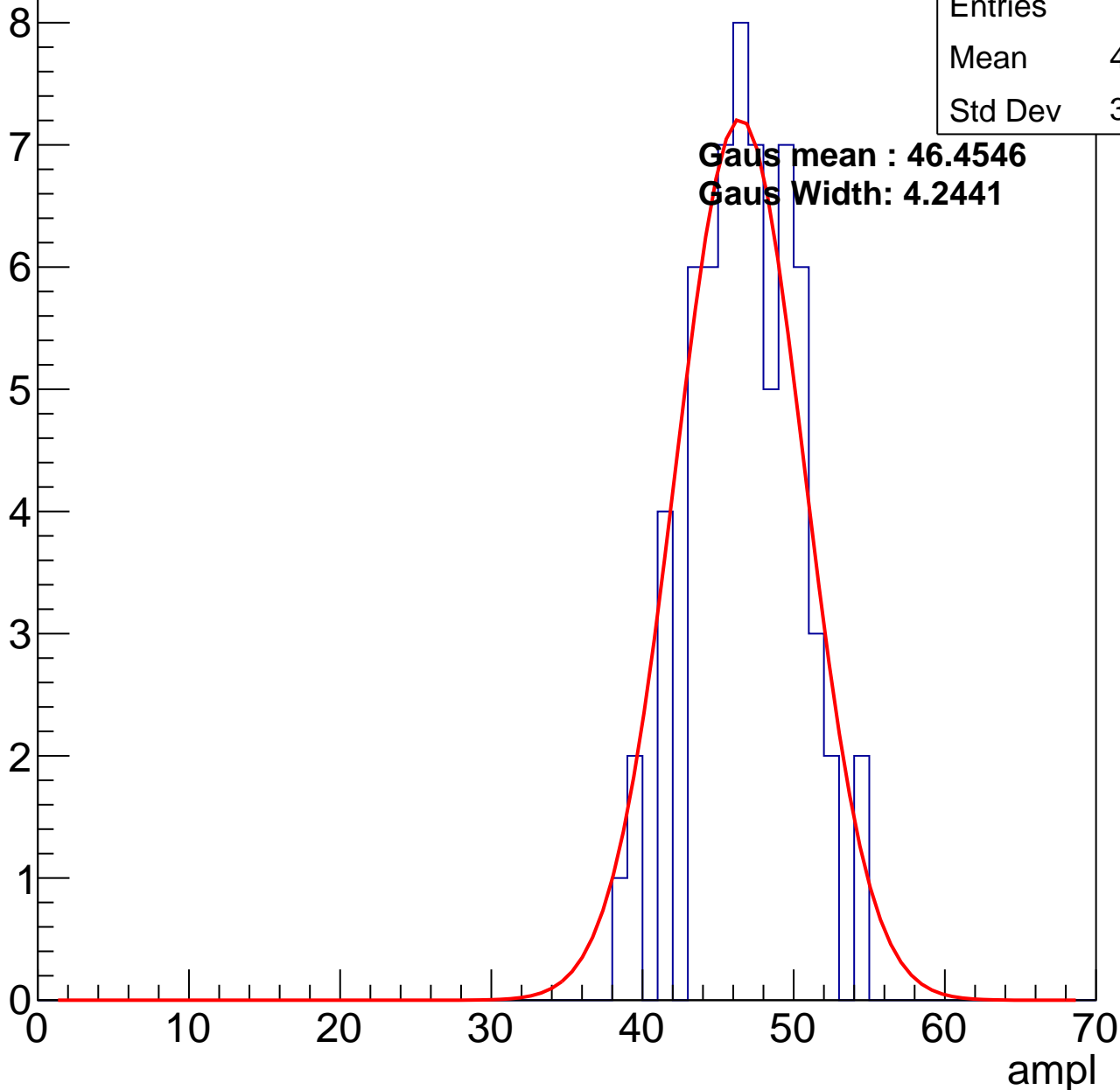
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	46.39
Std Dev	3.477

Gaus mean : 46.4546

Gaus Width: 4.2441

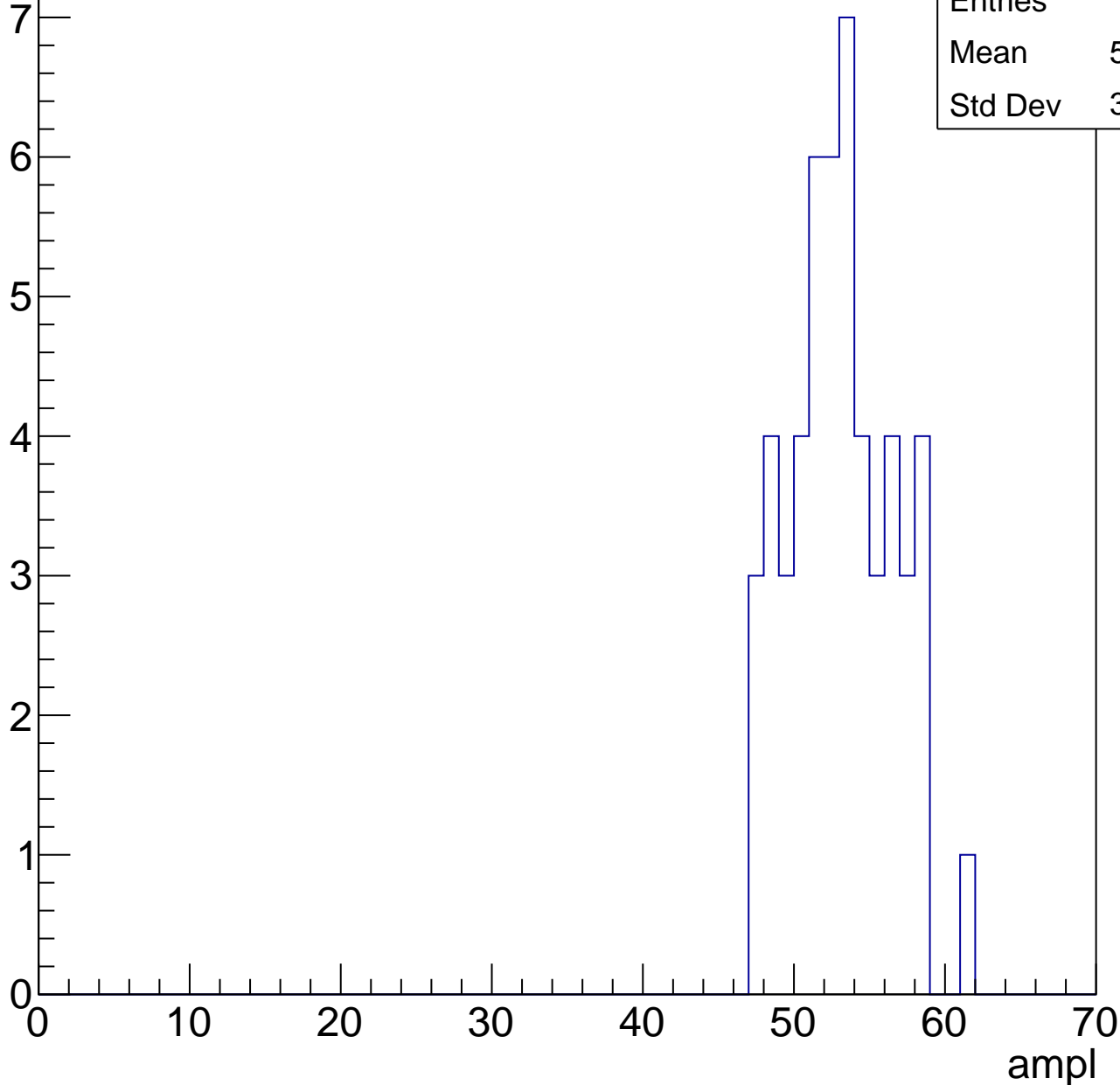


B0L002S, U12-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

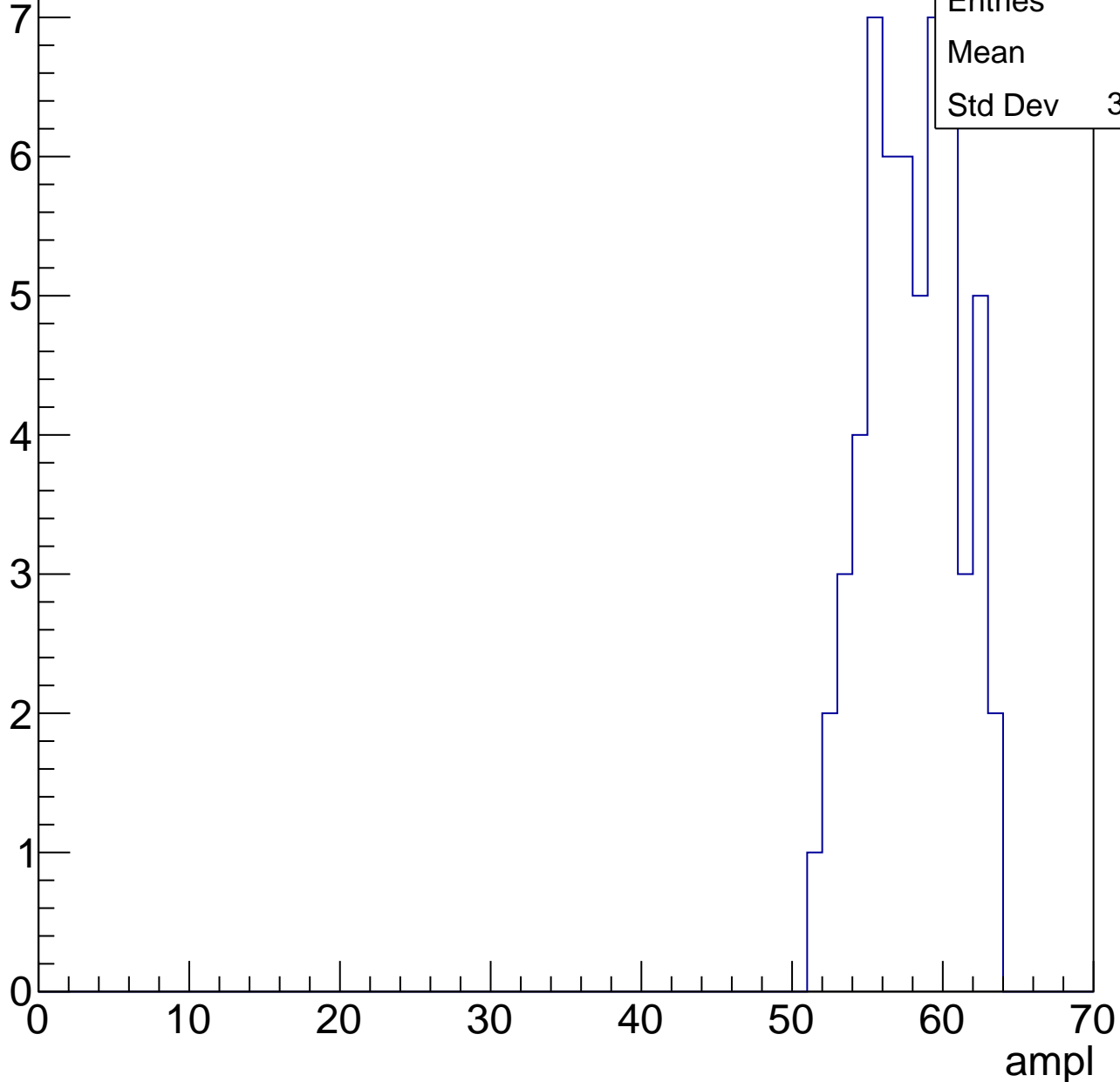
Entries	52
Mean	52.65
Std Dev	3.339



B0L002S, U12-ch45, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

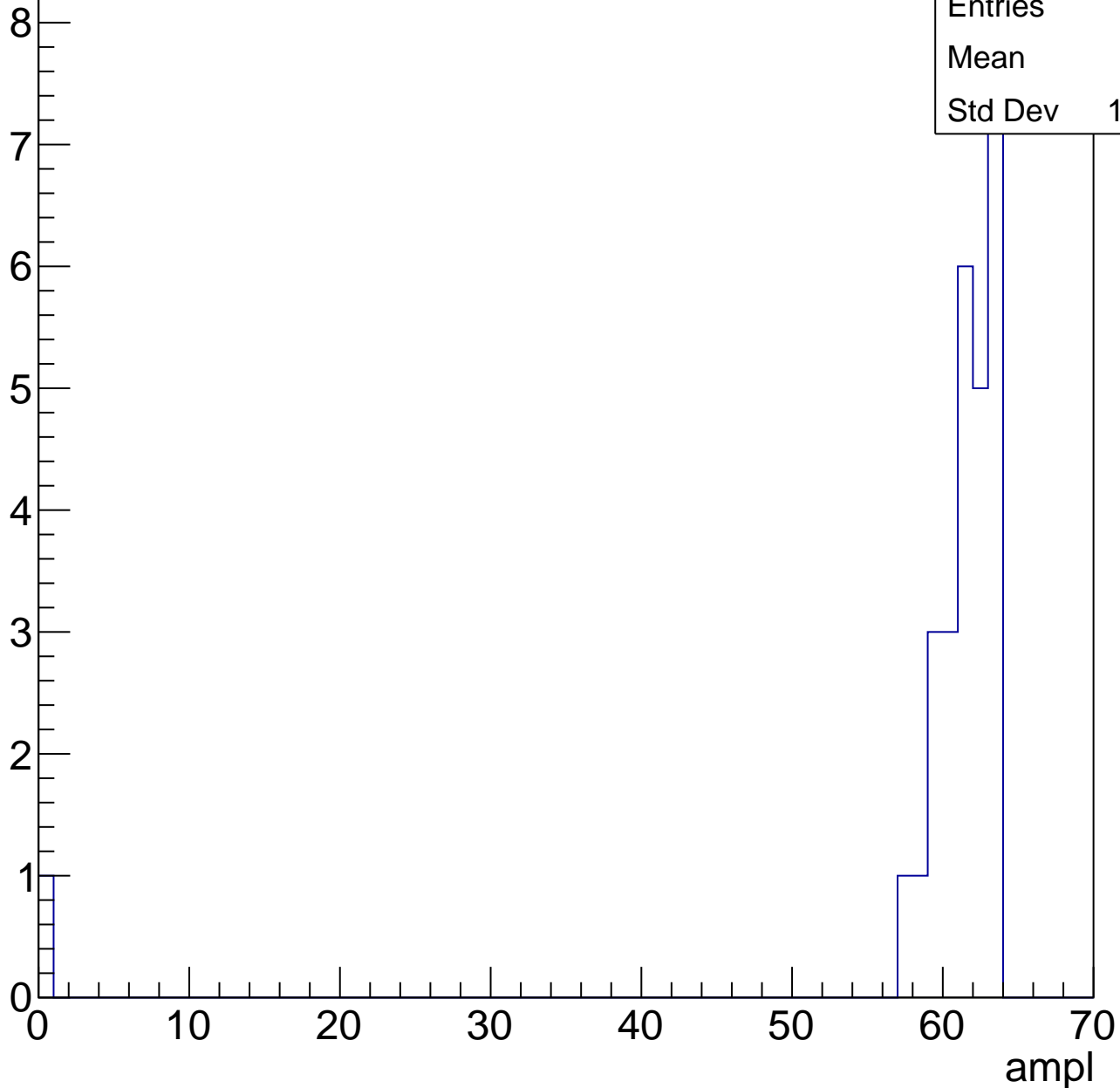


B0L002S, U12-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	59
Std Dev	11.47



B0L002S, U12-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch46, adc0

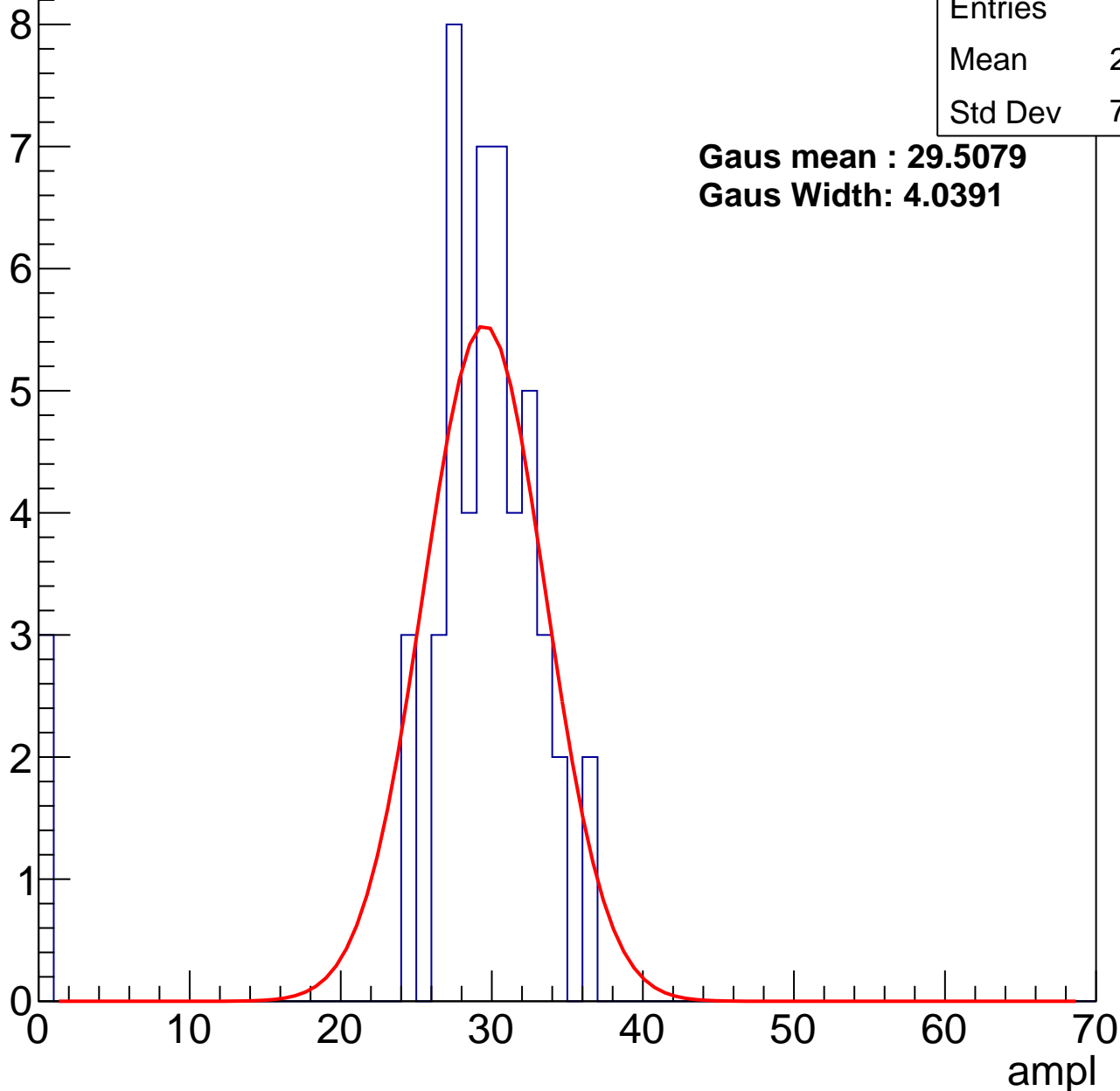
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	27.73
Std Dev	7.465

Gaus mean : 29.5079

Gaus Width: 4.0391



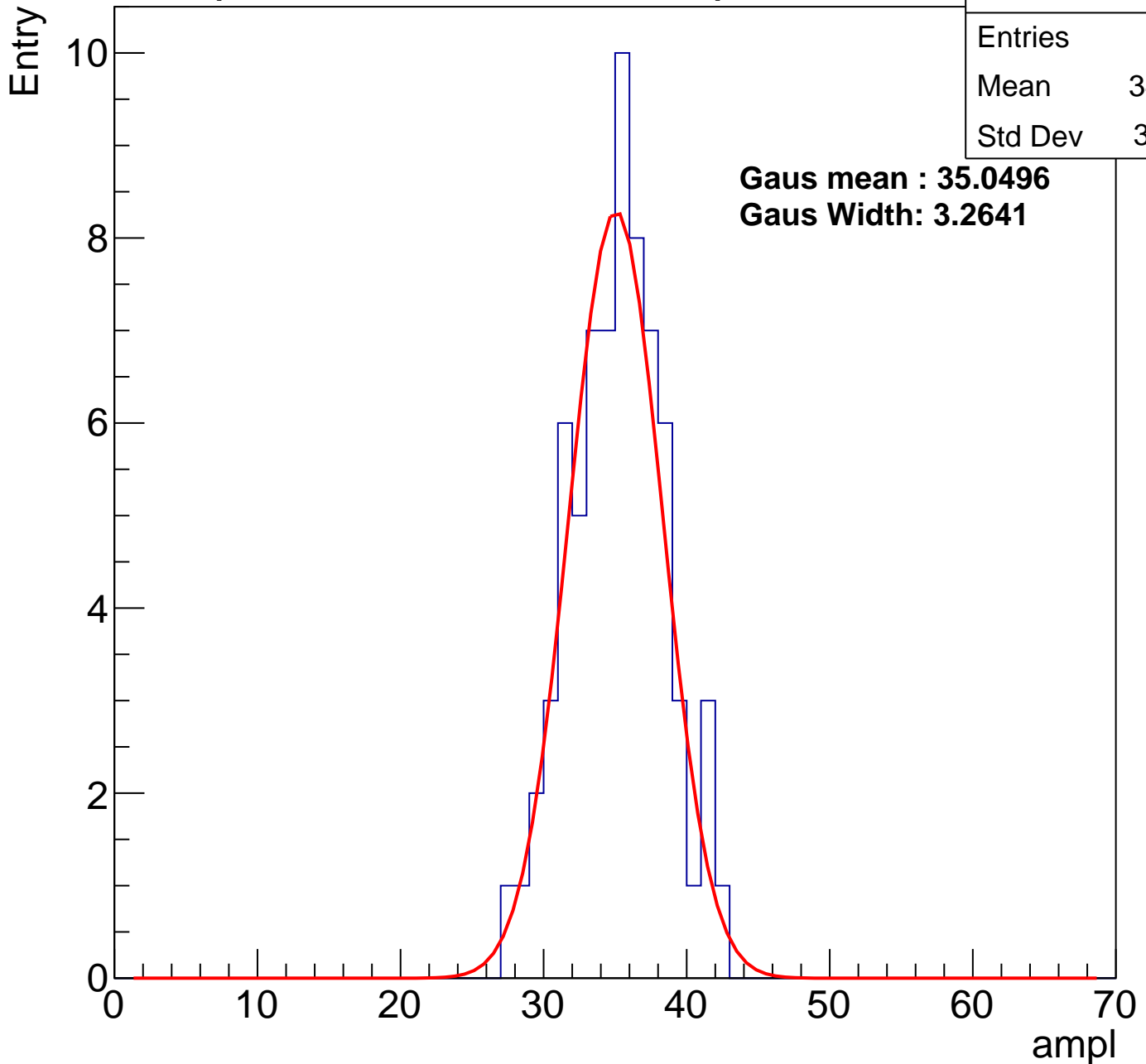
B0L002S, U12-ch46, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	34.72
Std Dev	3.251

Gaus mean : 35.0496

Gaus Width: 3.2641



B0L002S, U12-ch46, adc2

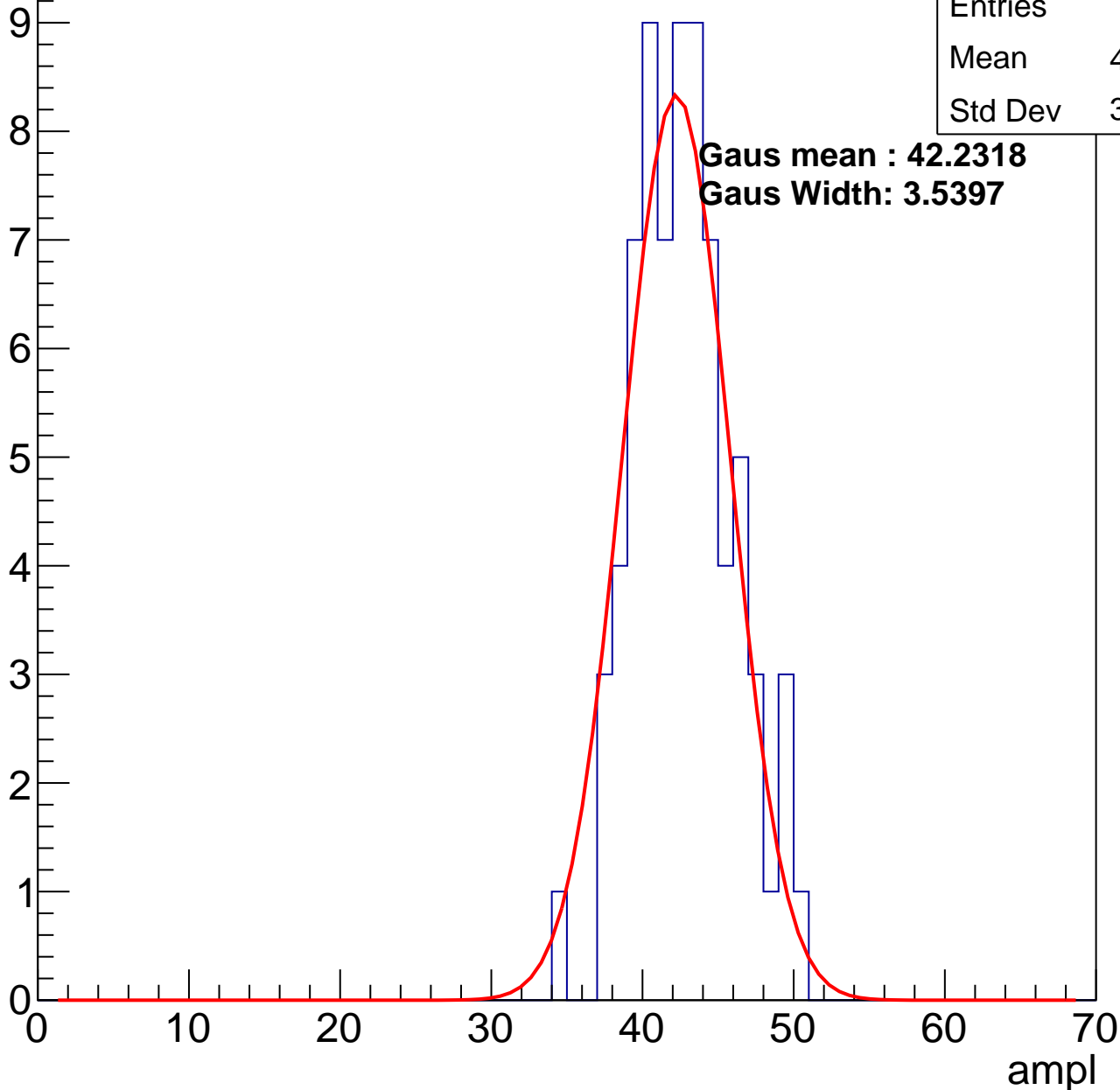
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.27
Std Dev	3.266

Gaus mean : 42.2318

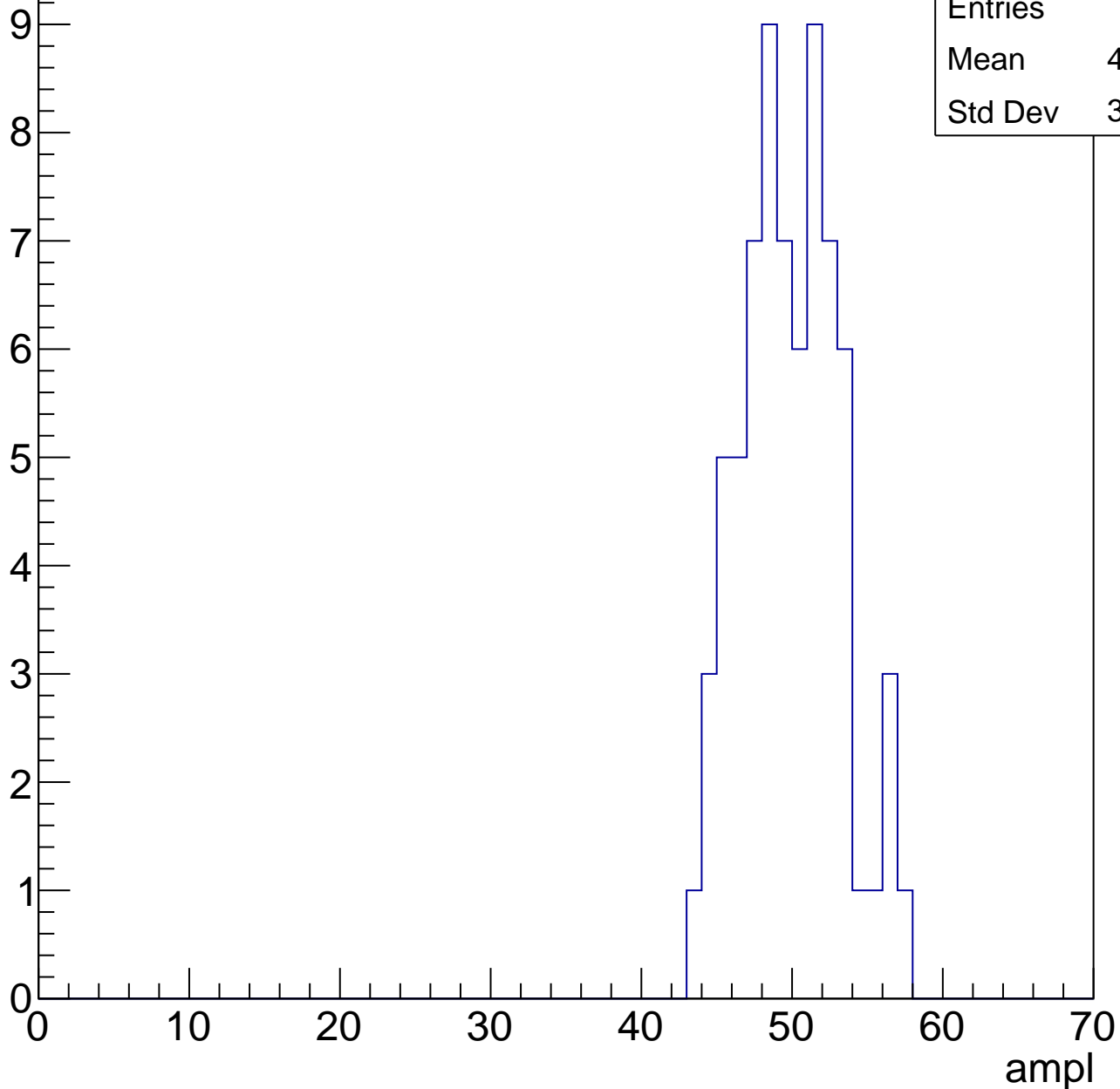
Gaus Width: 3.5397



B0L002S, U12-ch46, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

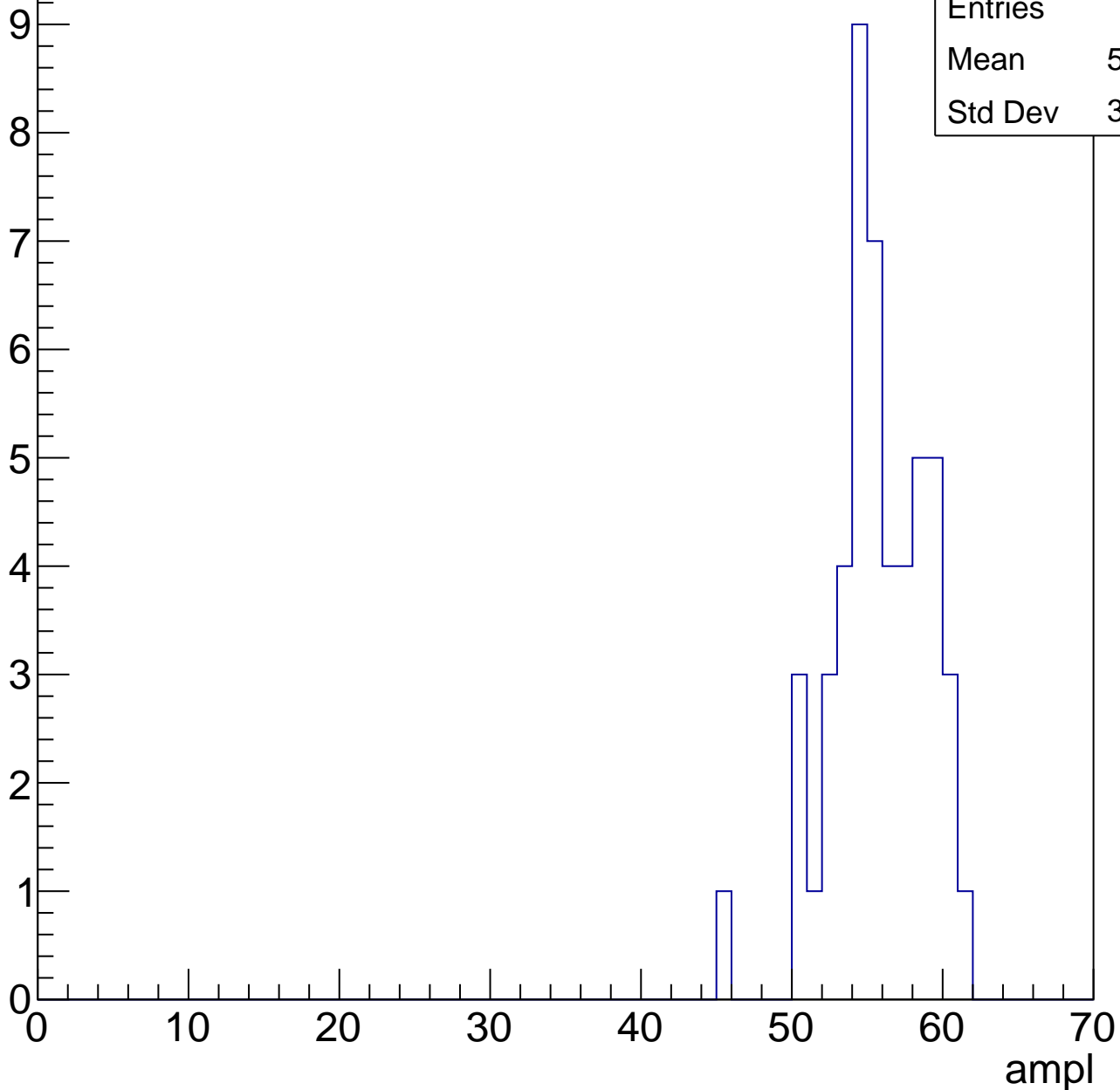


B0L002S, U12-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	55.26
Std Dev	3.148

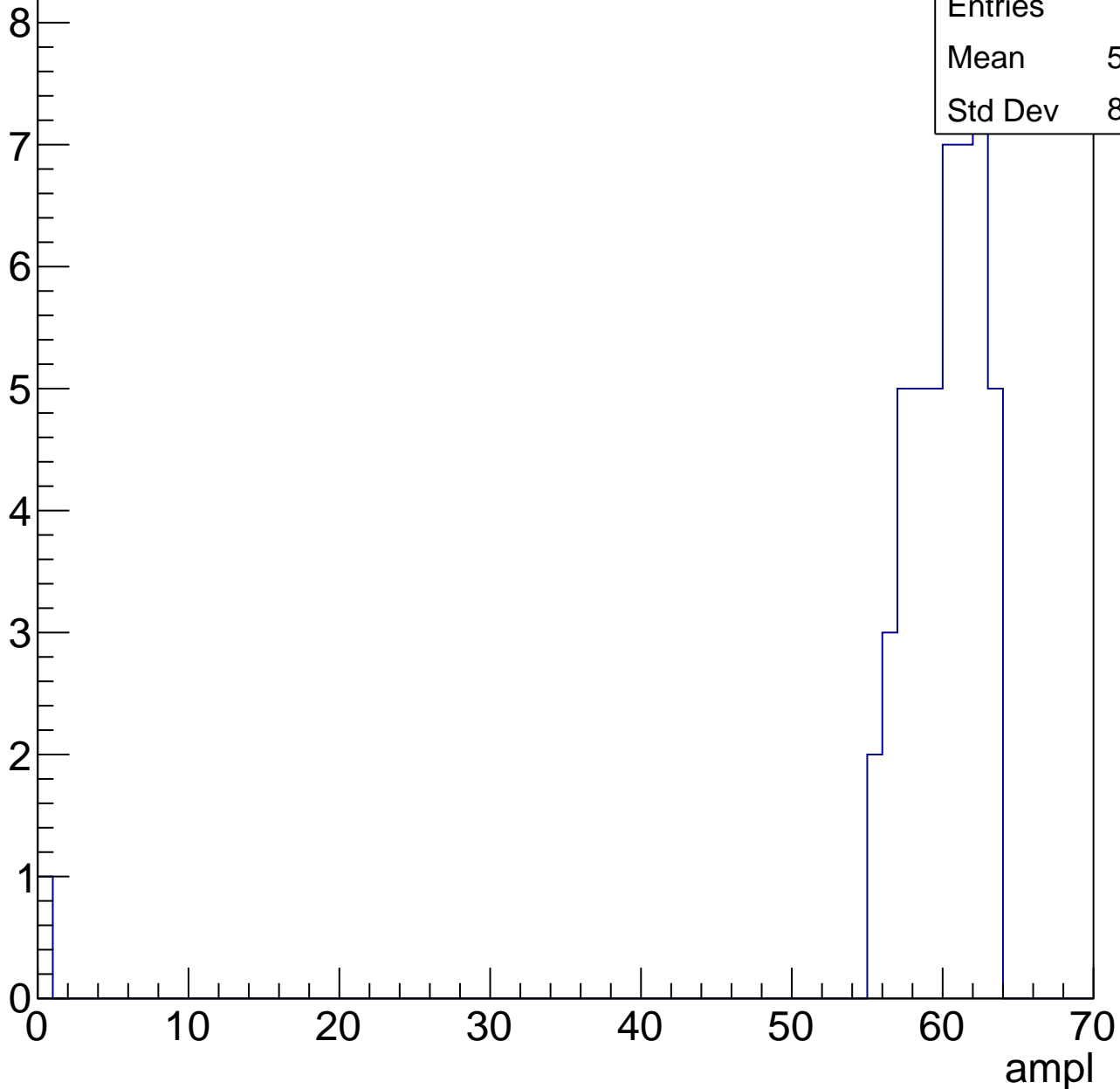


B0L002S, U12-ch46, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	58.46
Std Dev	8.825

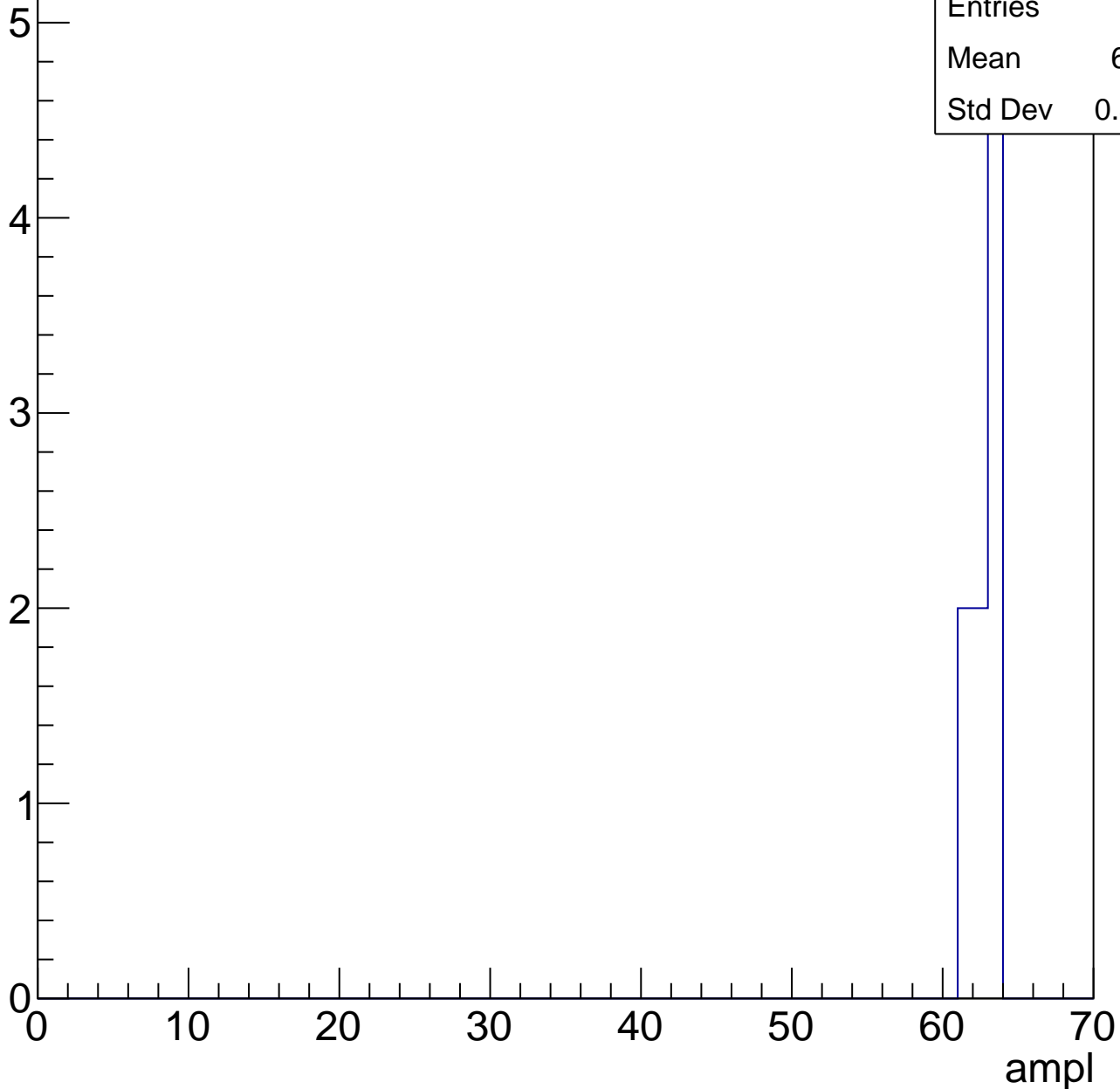


B0L002S, U12-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	62.33
Std Dev	0.8165



B0L002S, U12-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch47, adc0

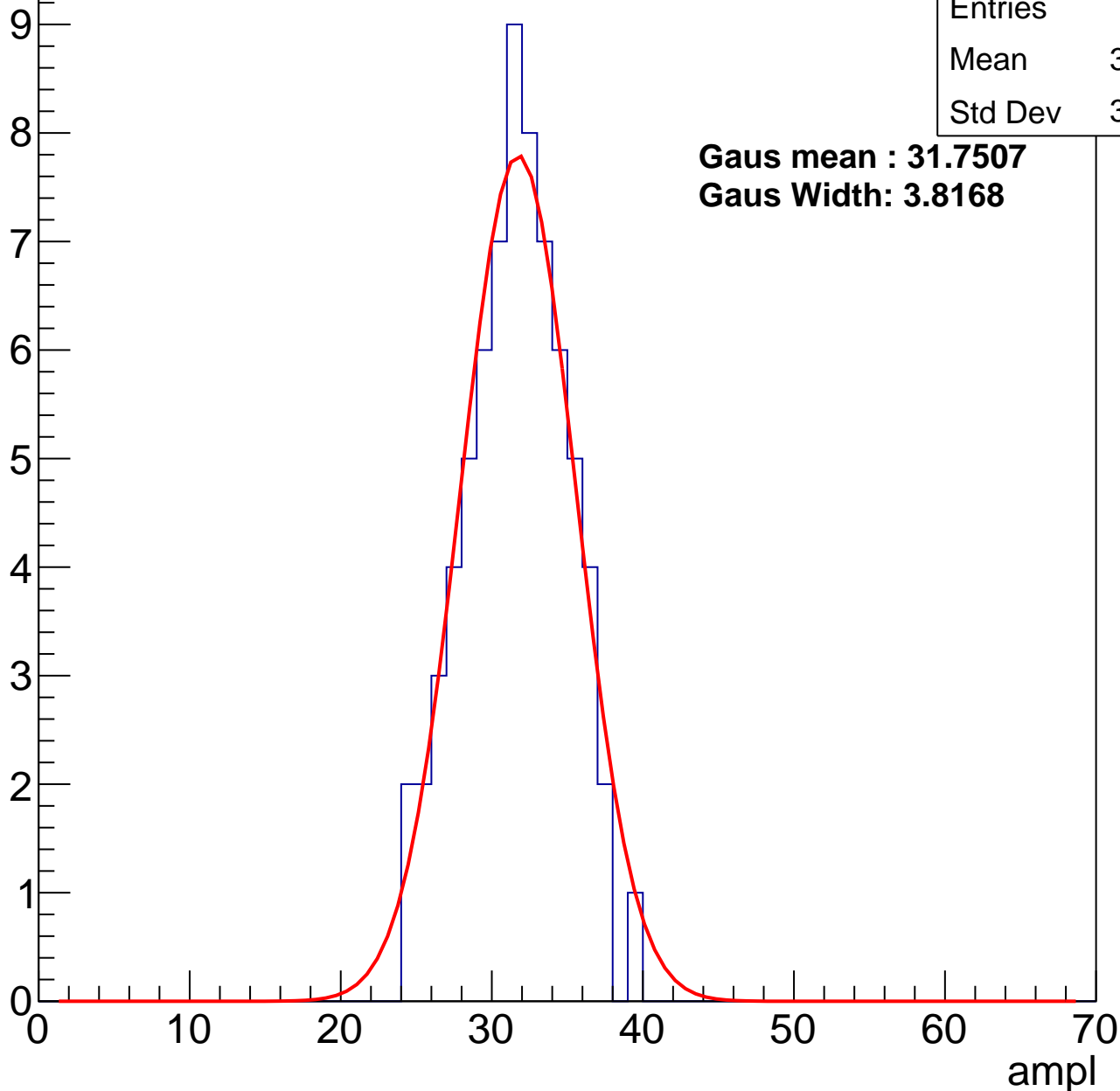
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.13
Std Dev	3.335

Gaus mean : 31.7507

Gaus Width: 3.8168



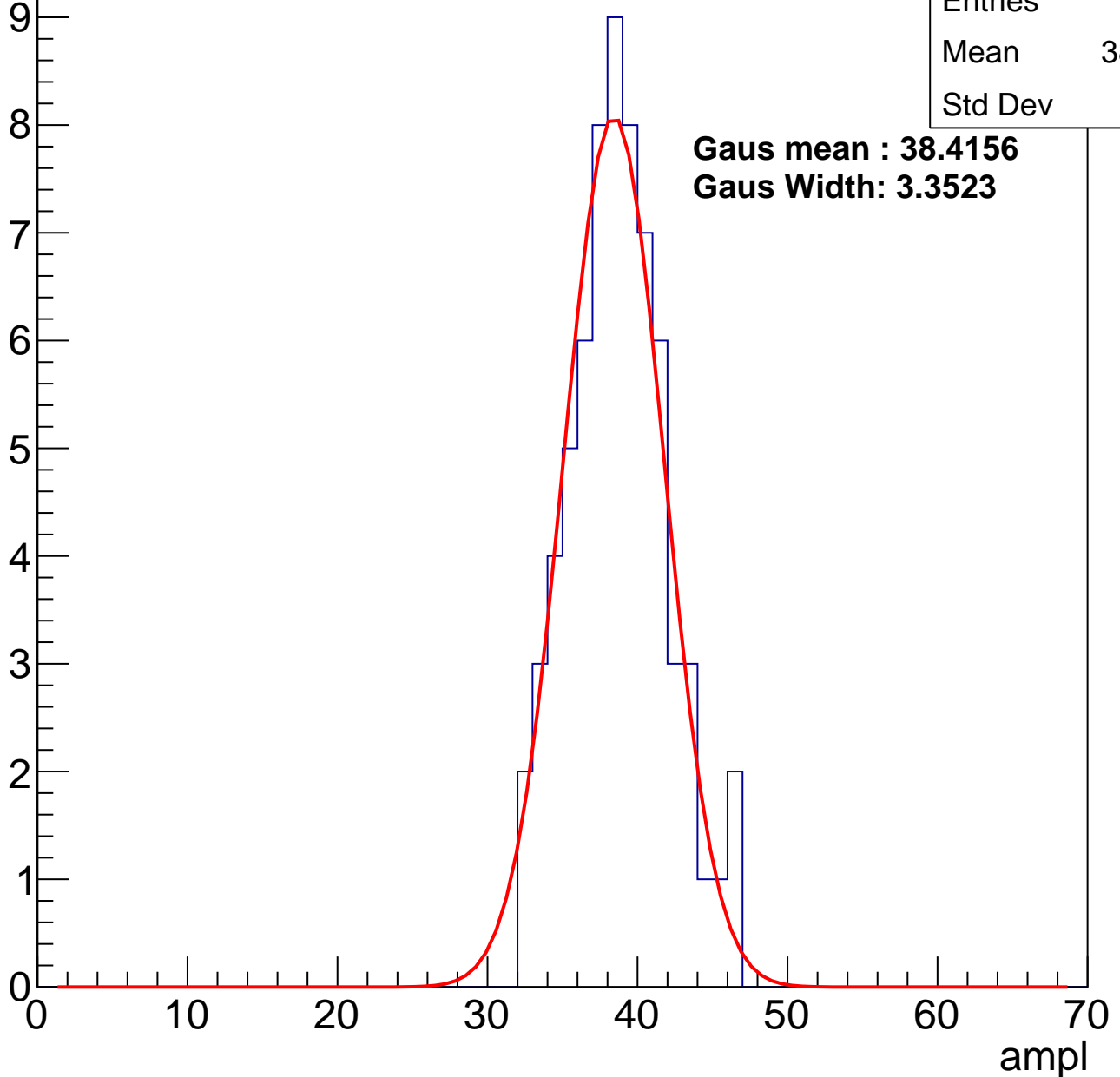
B0L002S, U12-ch47, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38.26
Std Dev	3.23

Gaus mean : 38.4156
Gaus Width: 3.3523



B0L002S, U12-ch47, adc2

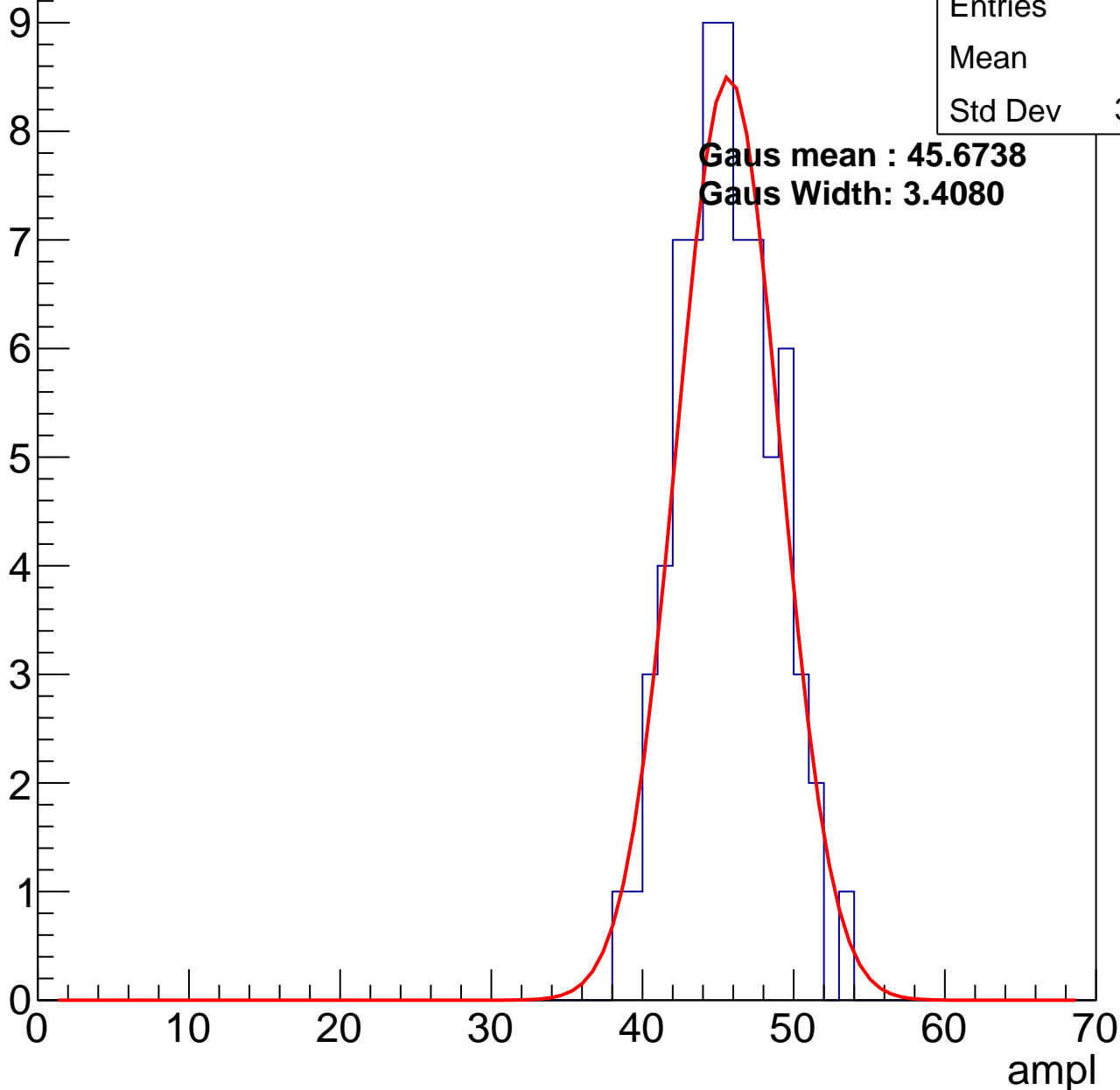
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45.1
Std Dev	3.141

Gaus mean : 45.6738

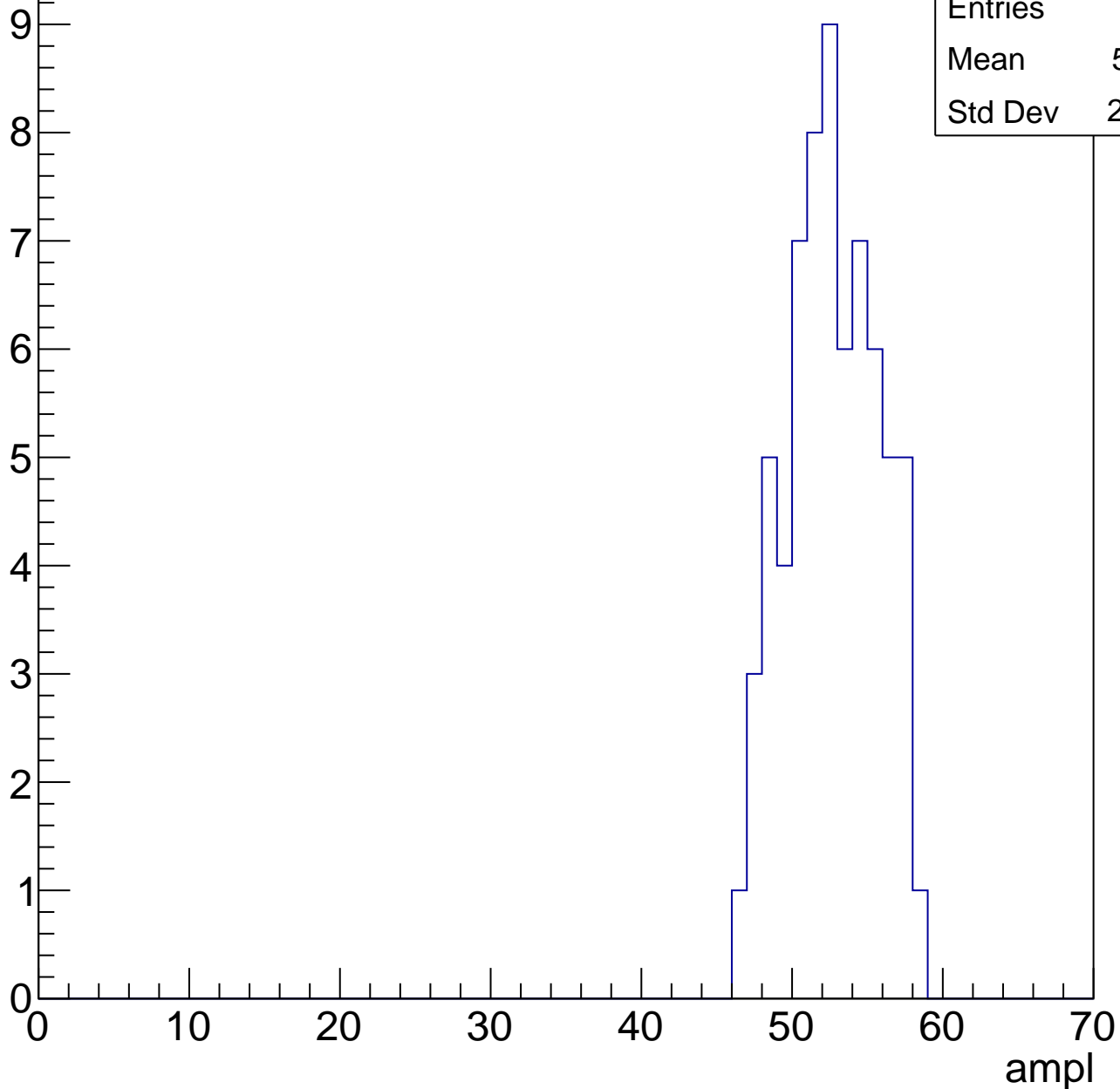
Gaus Width: 3.4080



B0L002S, U12-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

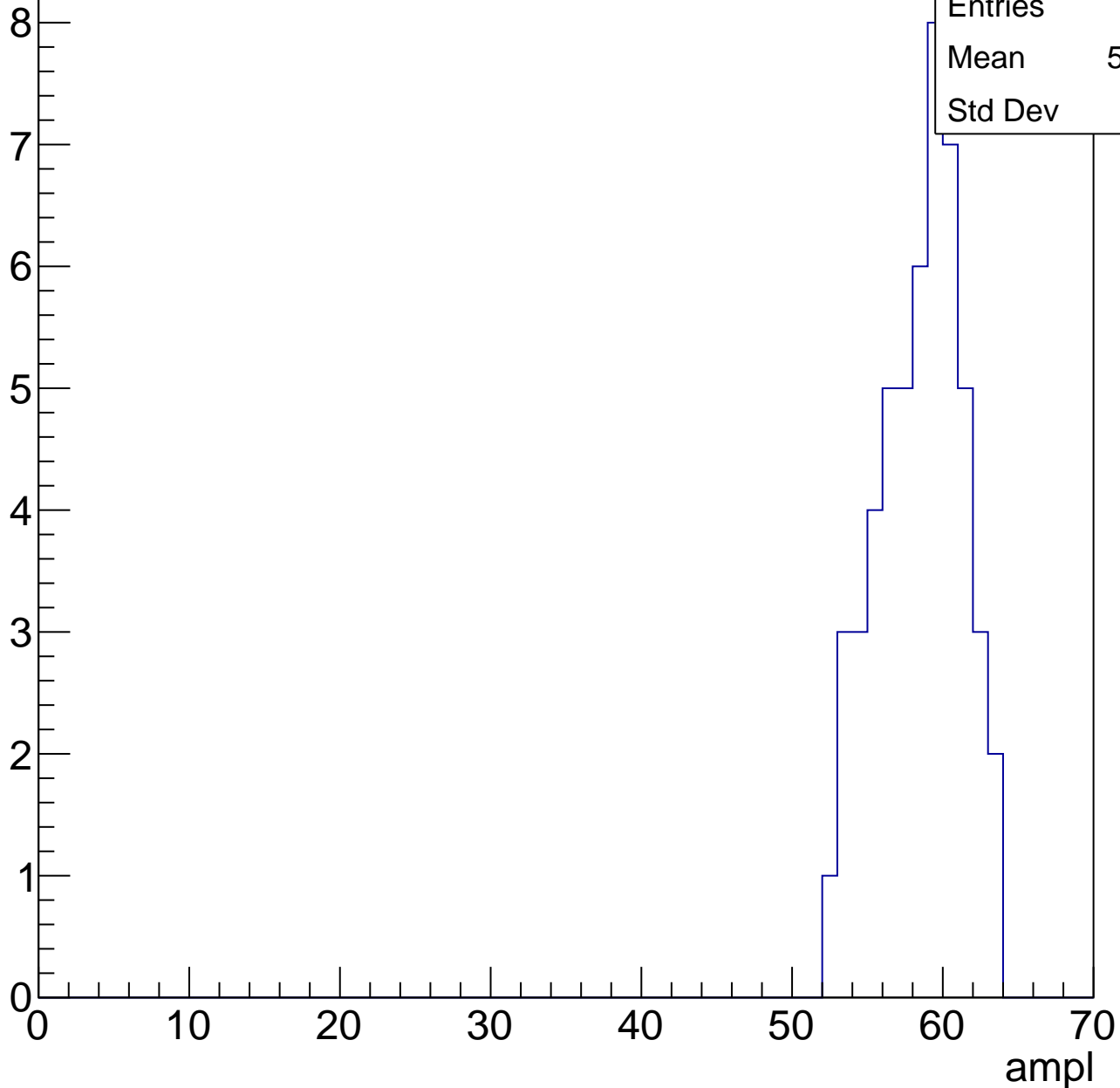
Entry



B0L002S, U12-ch47, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

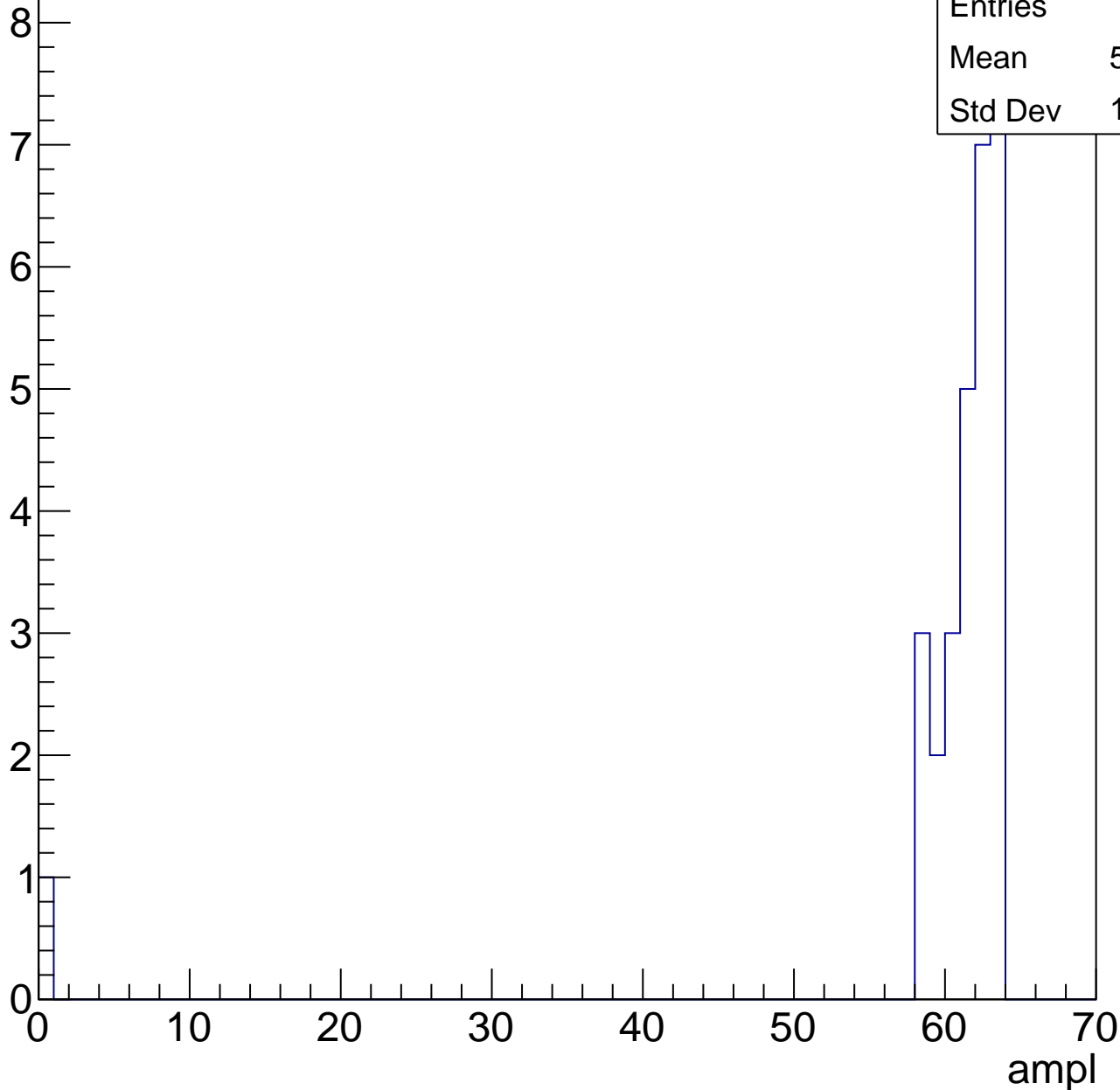


B0L002S, U12-ch47, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59.14
Std Dev	11.29



B0L002S, U12-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch48, adc0

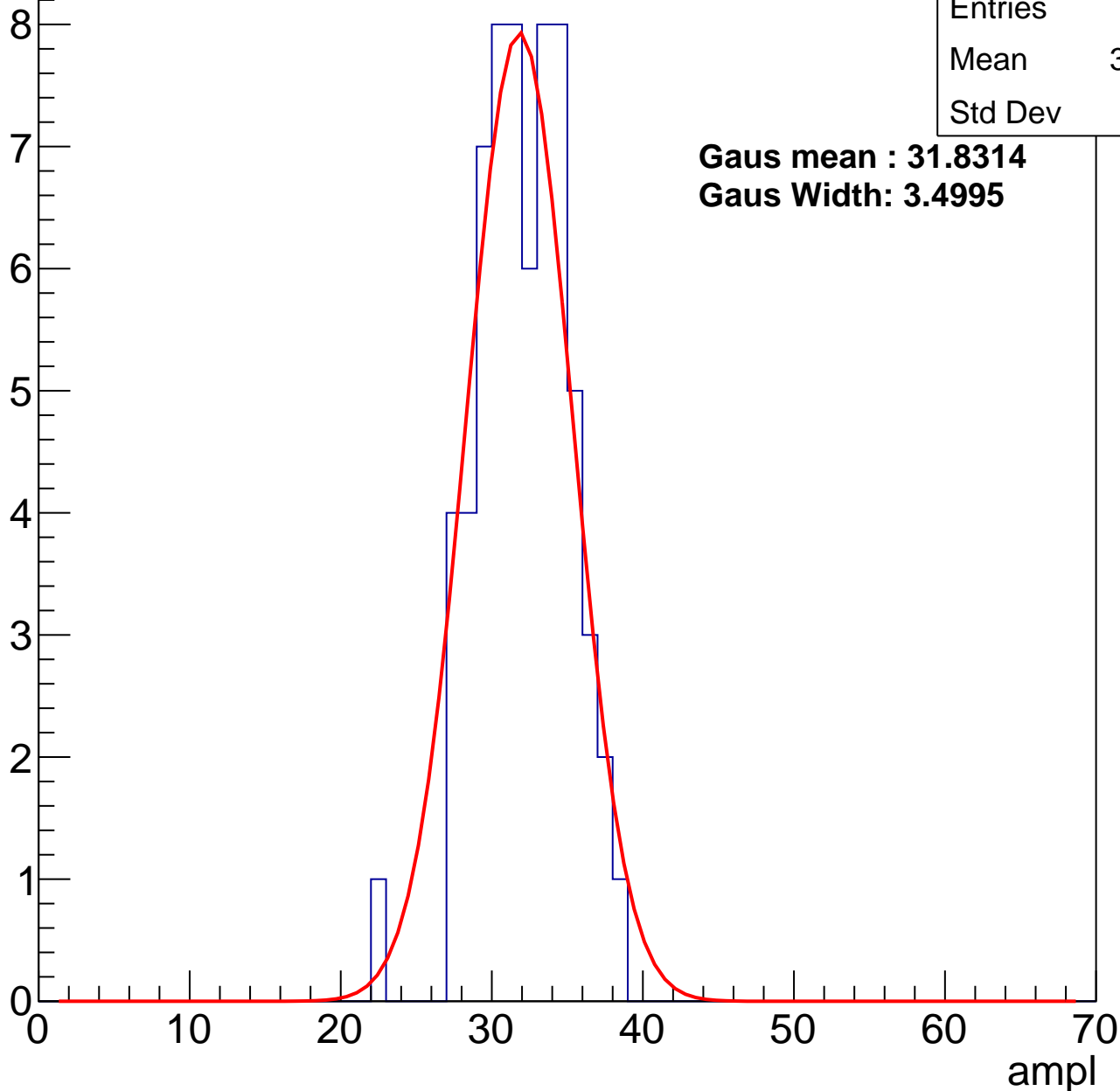
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	31.63
Std Dev	2.98

Gaus mean : 31.8314

Gaus Width: 3.4995



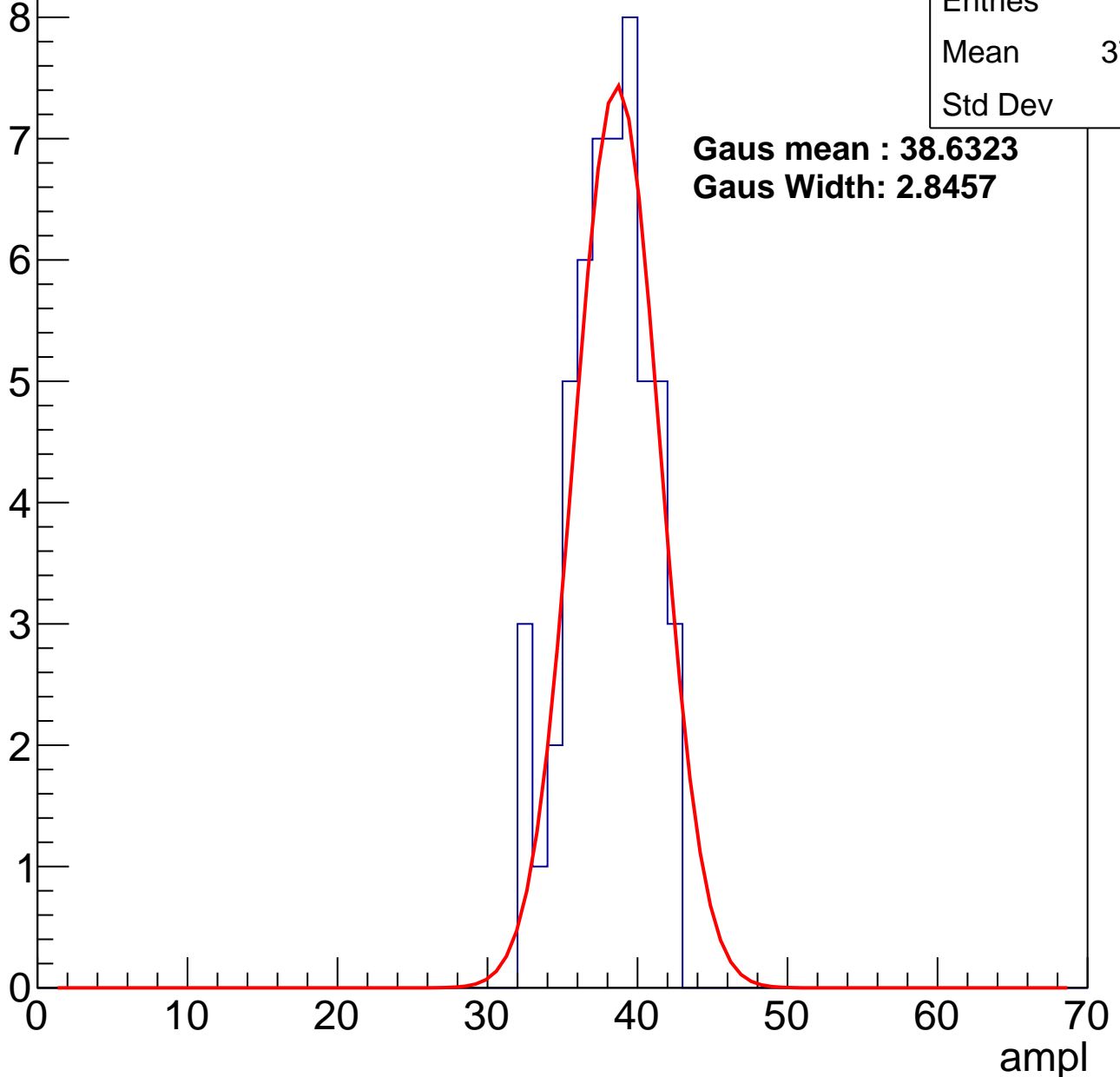
B0L002S, U12-ch48, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	37.62
Std Dev	2.61

Gaus mean : 38.6323
Gaus Width: 2.8457



B0L002S, U12-ch48, adc2

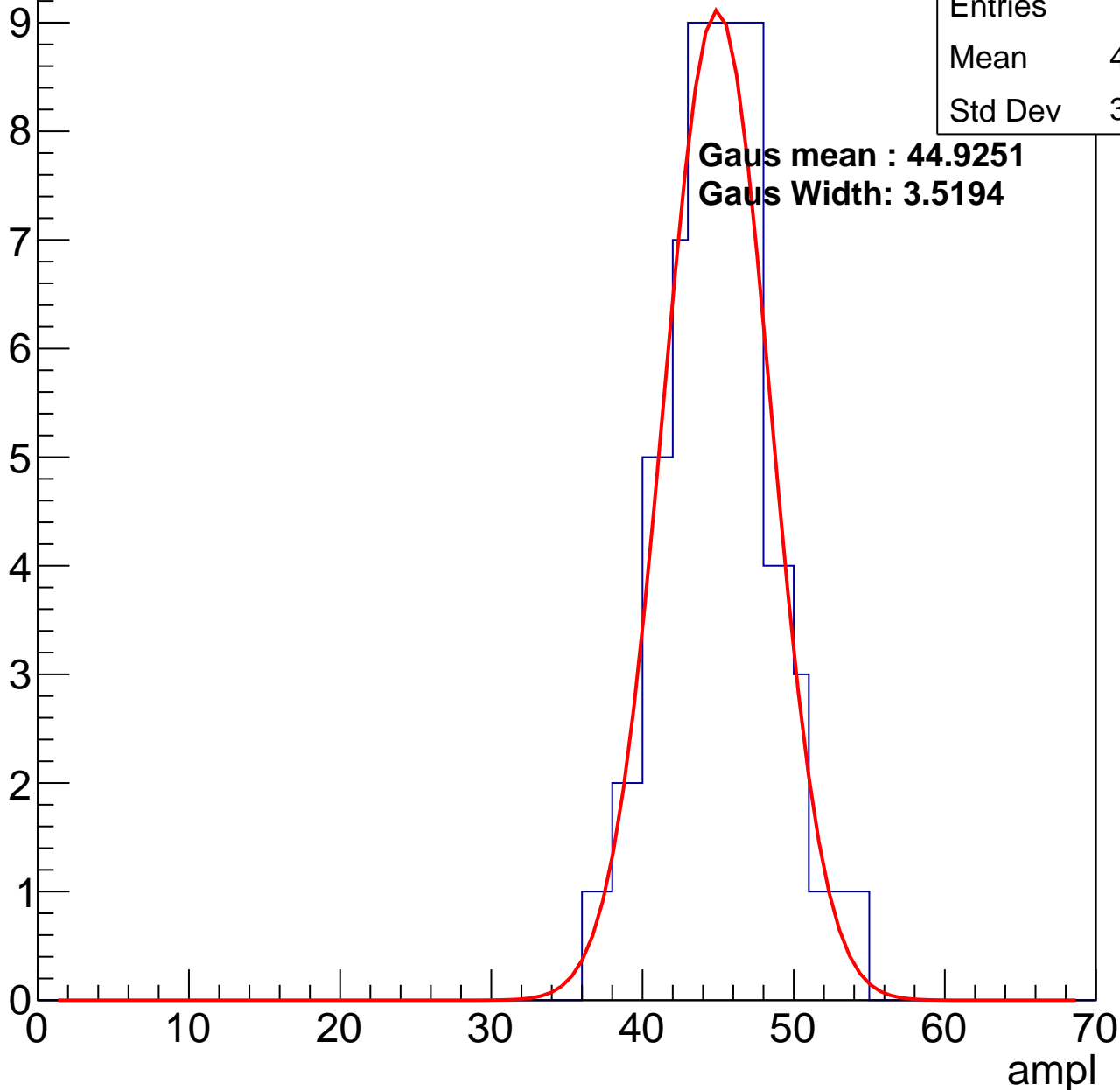
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	44.57
Std Dev	3.578

Gaus mean : 44.9251

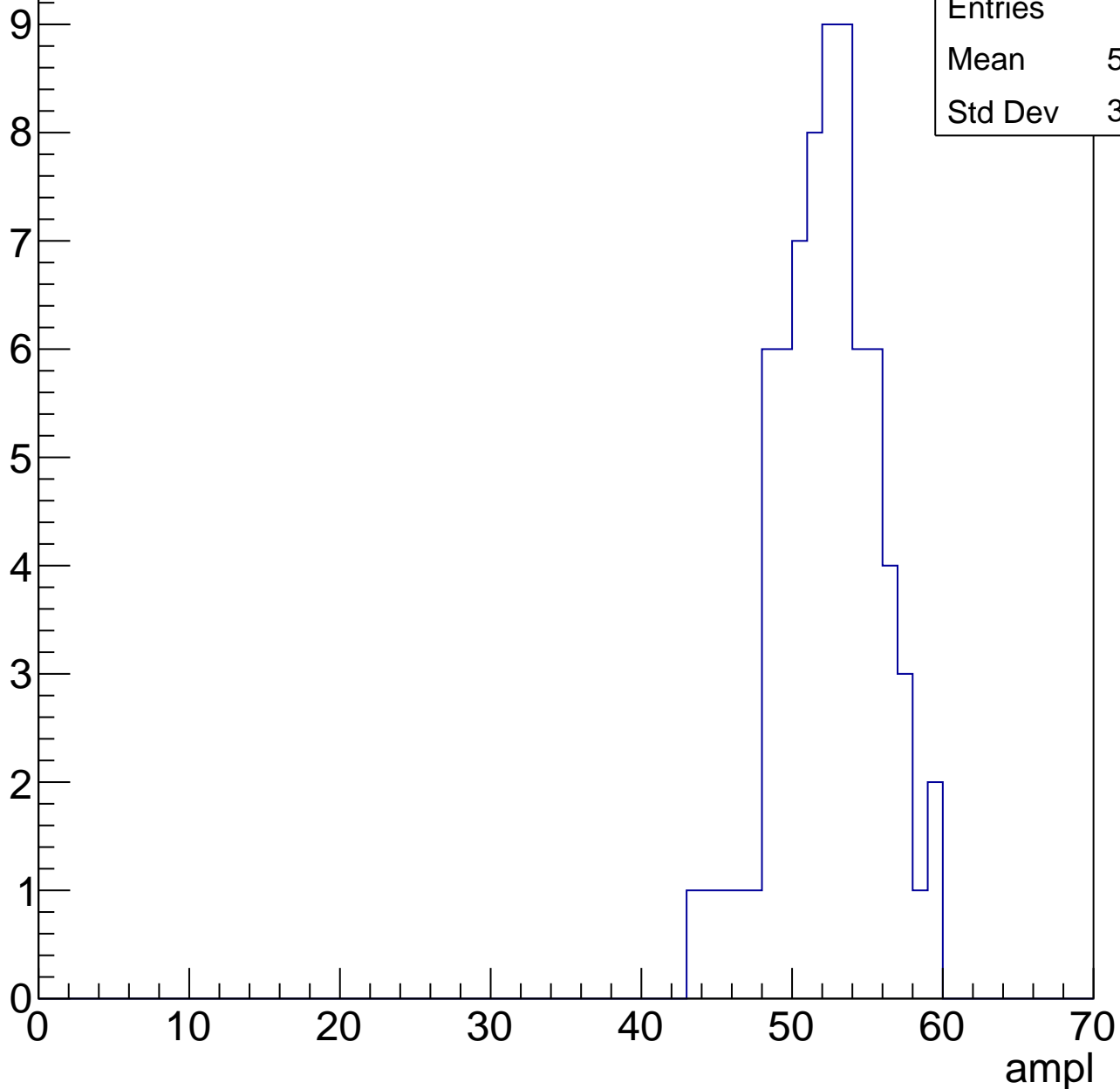
Gaus Width: 3.5194



B0L002S, U12-ch48, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



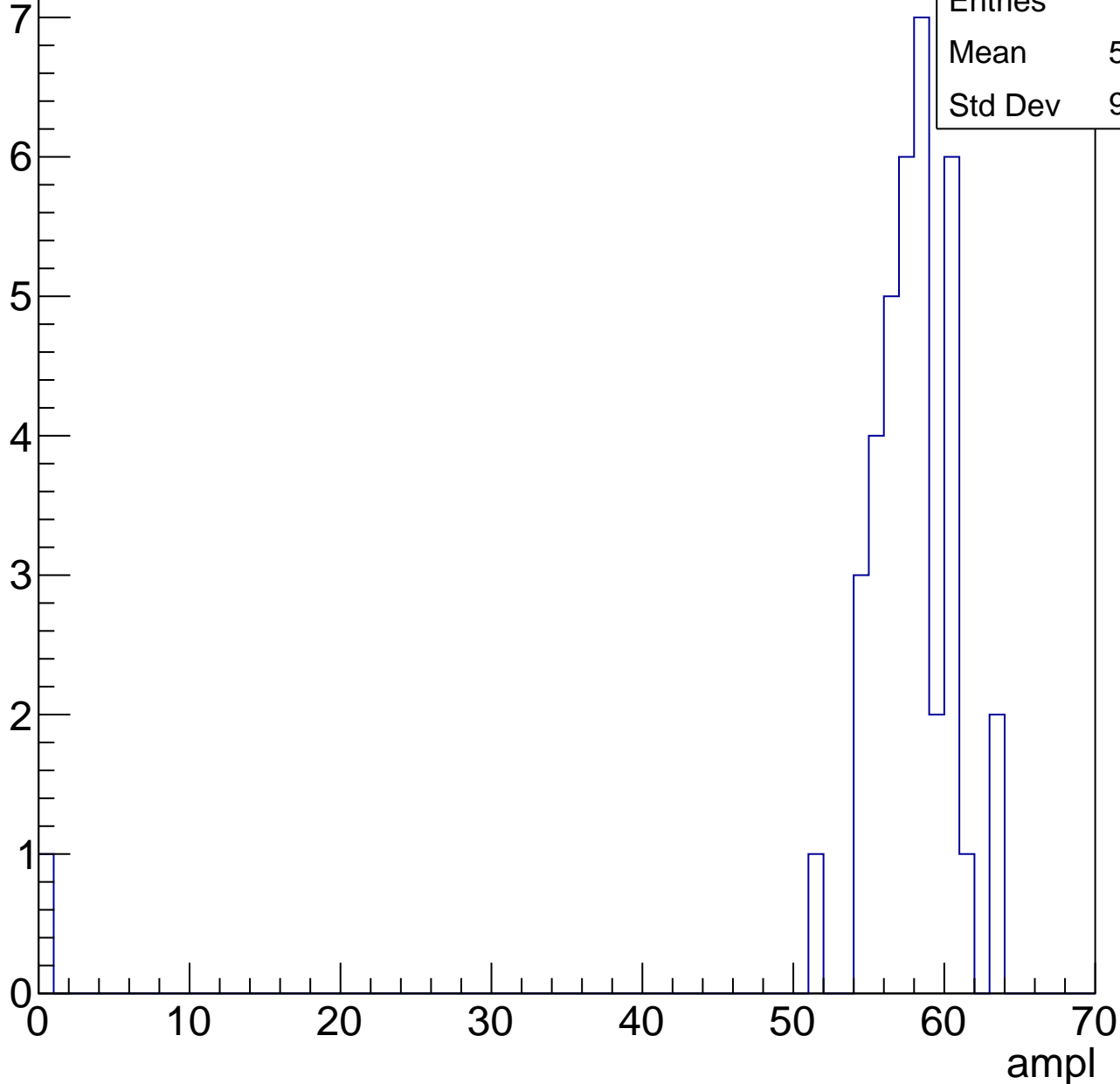
Entries	72
Mean	51.88
Std Dev	3.333

B0L002S, U12-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	55.95
Std Dev	9.525

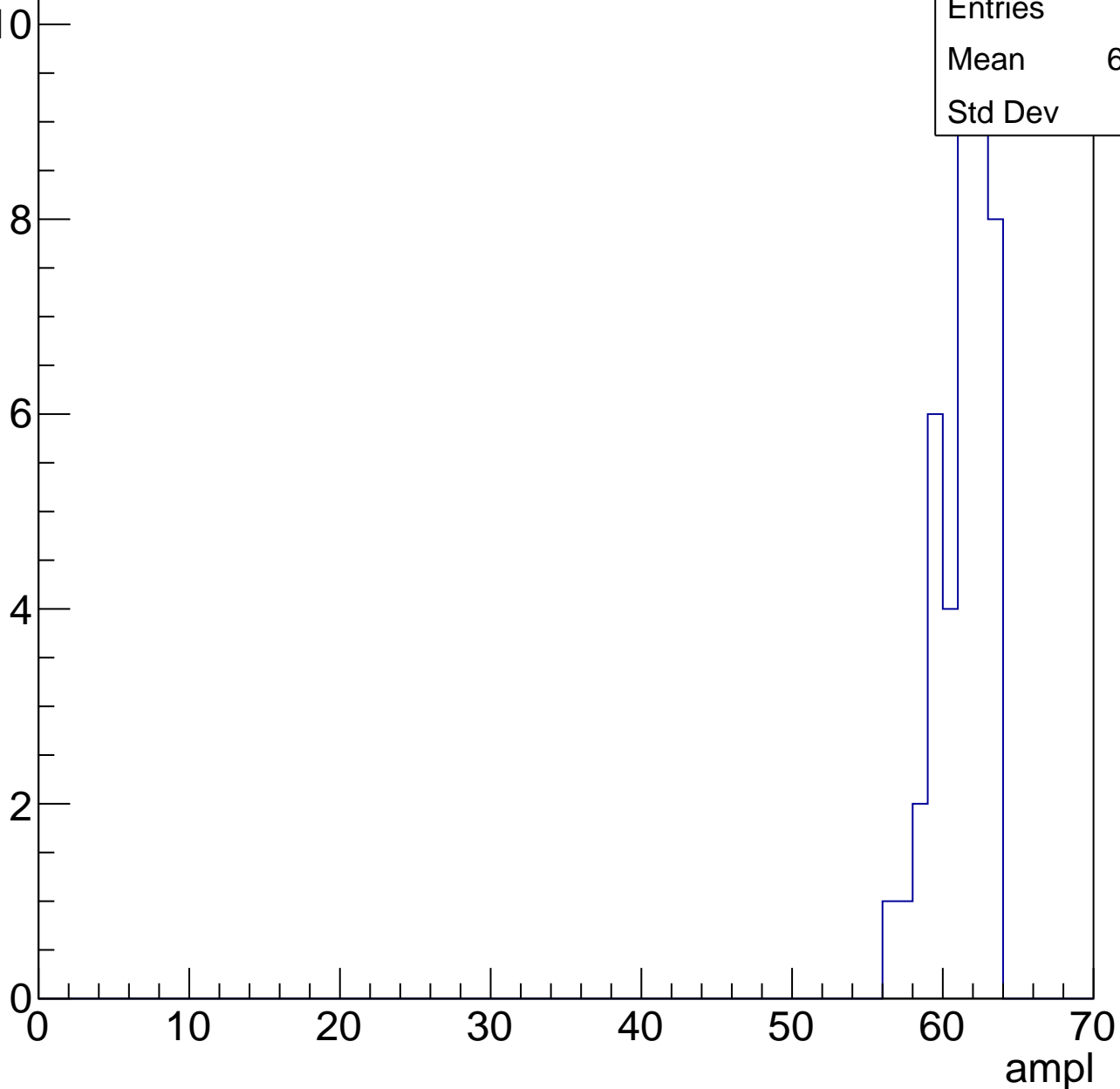


B0L002S, U12-ch48, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	60.88
Std Dev	1.77



B0L002S, U12-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch49, adc0

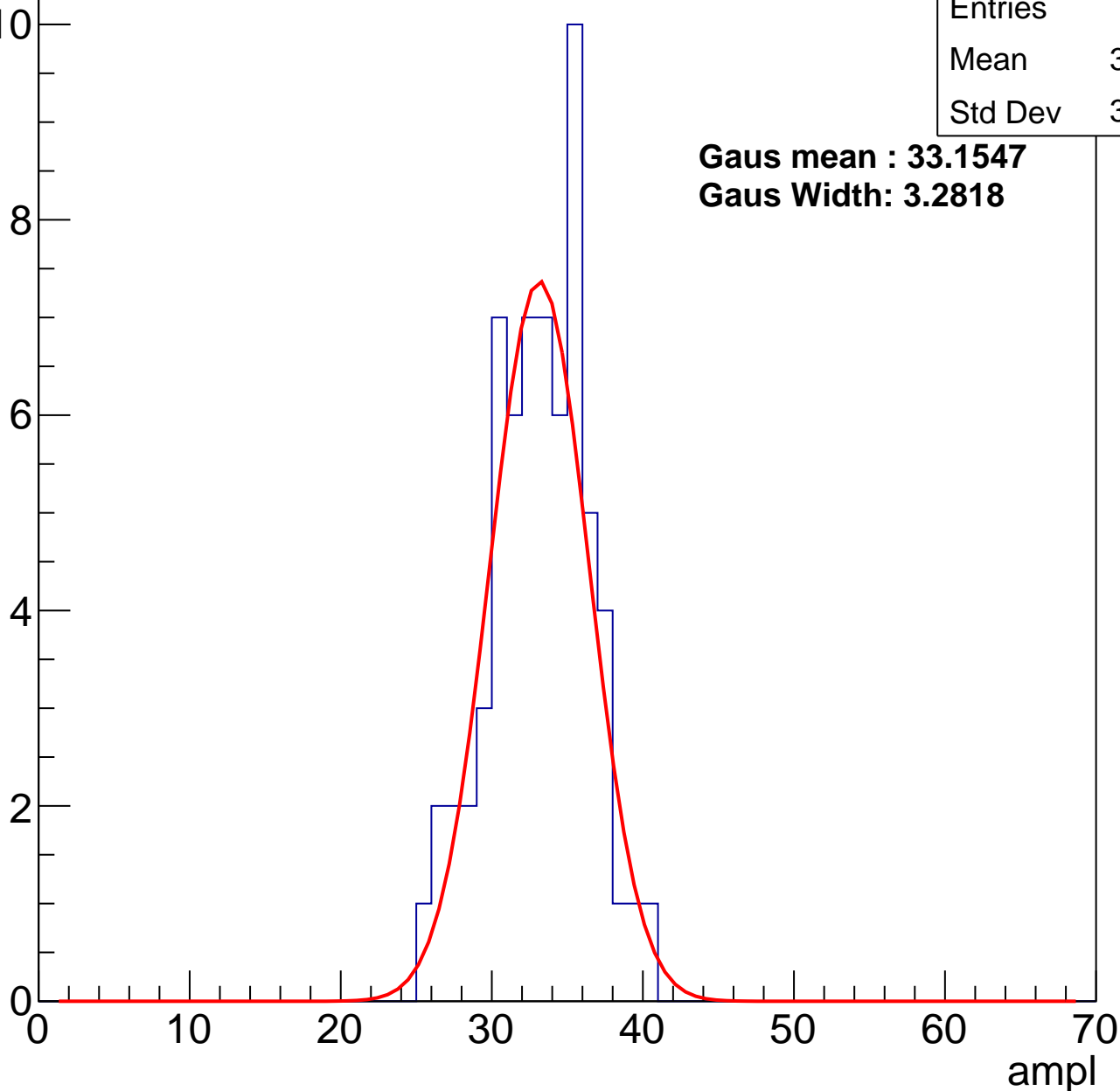
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	32.68
Std Dev	3.244

Gaus mean : 33.1547

Gaus Width: 3.2818



B0L002S, U12-ch49, adc1

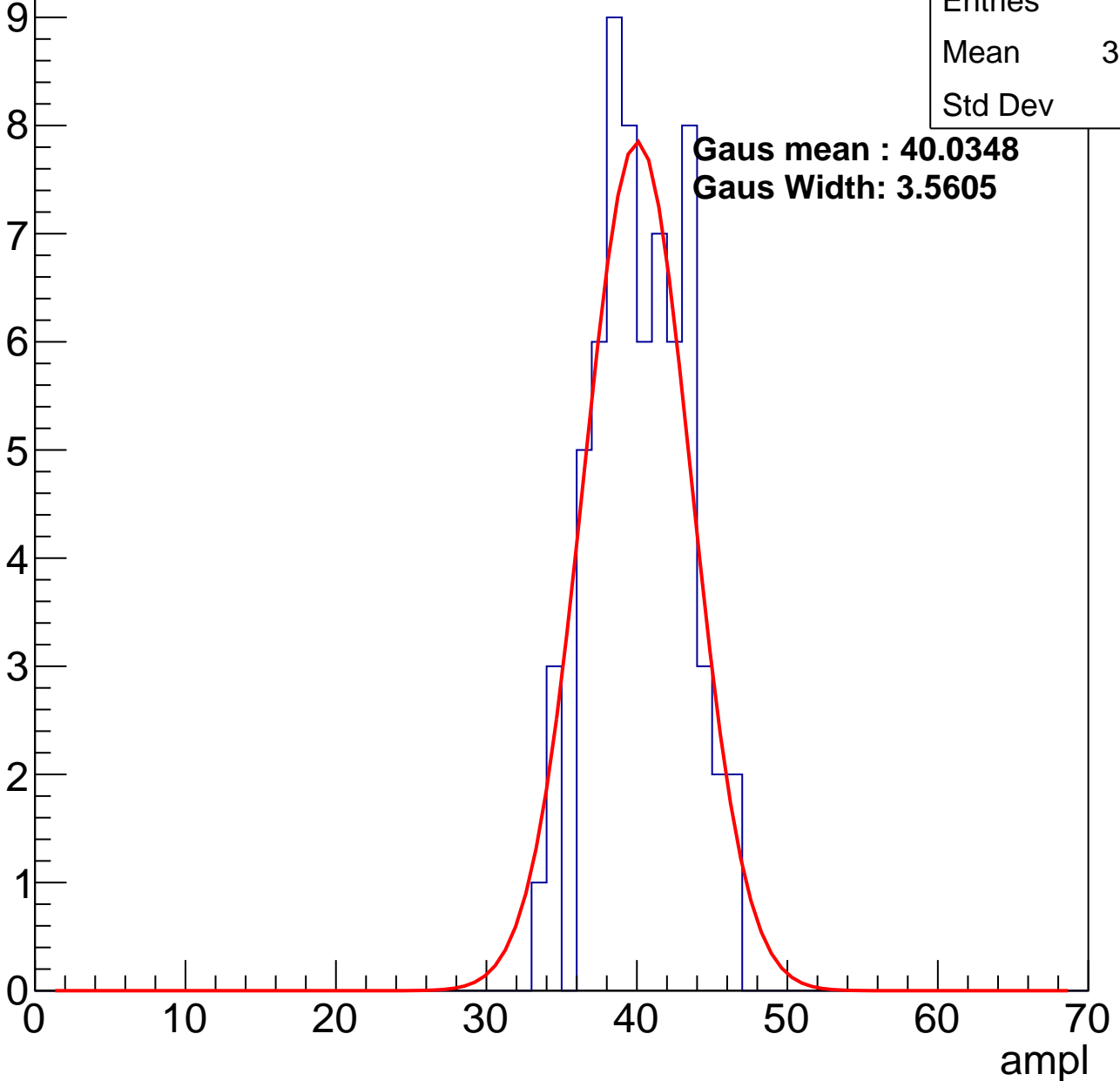
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	39.82
Std Dev	3.03

Gaus mean : 40.0348

Gaus Width: 3.5605



B0L002S, U12-ch49, adc2

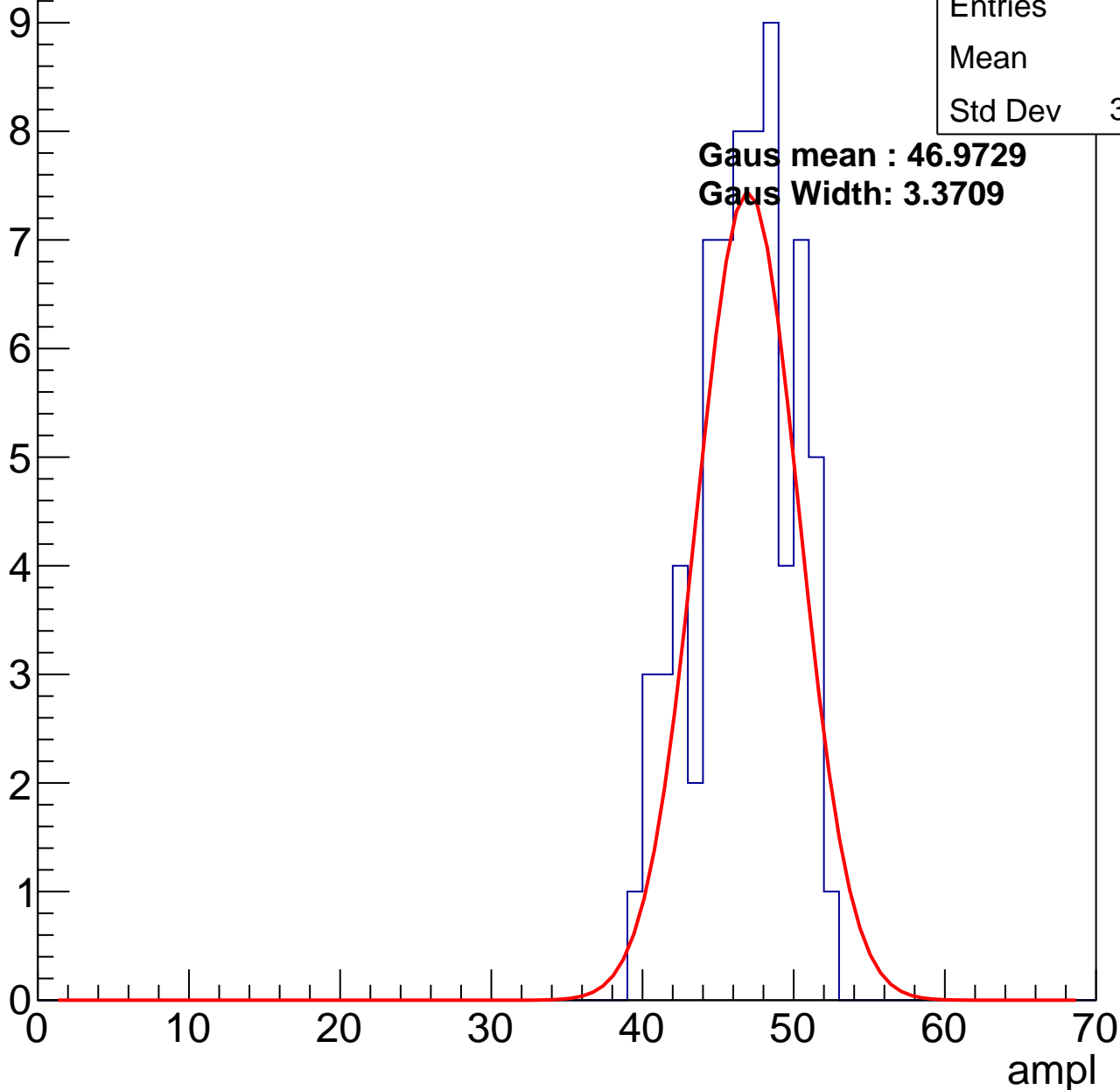
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	46.2
Std Dev	3.183

Gaus mean : 46.9729

Gaus Width: 3.3709



B0L002S, U12-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	53.55
Std Dev	3.189

Entry

10

8

6

4

2

0

0

10

20

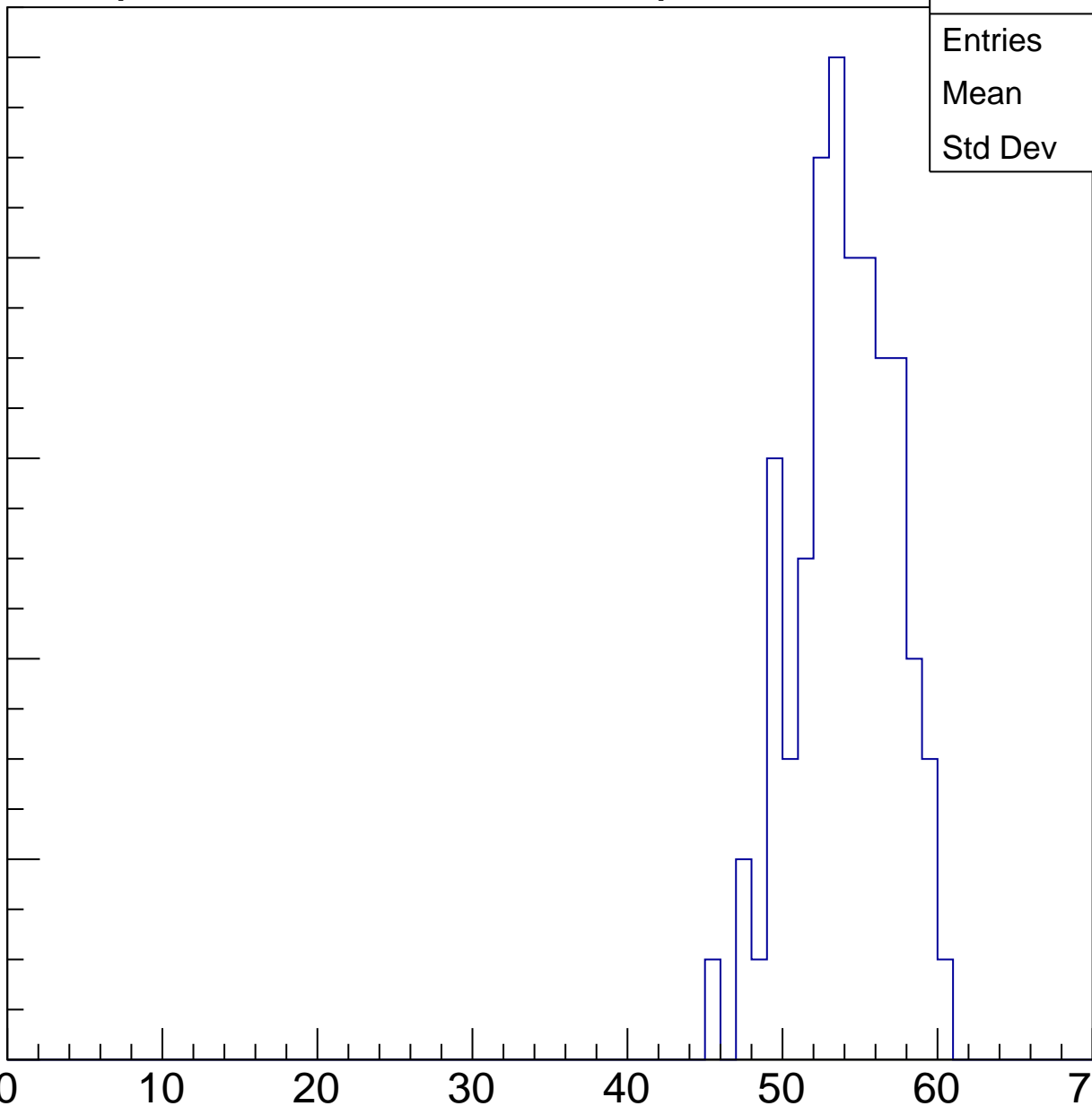
30

40

50

60

ampl



B0L002S, U12-ch49, adc4

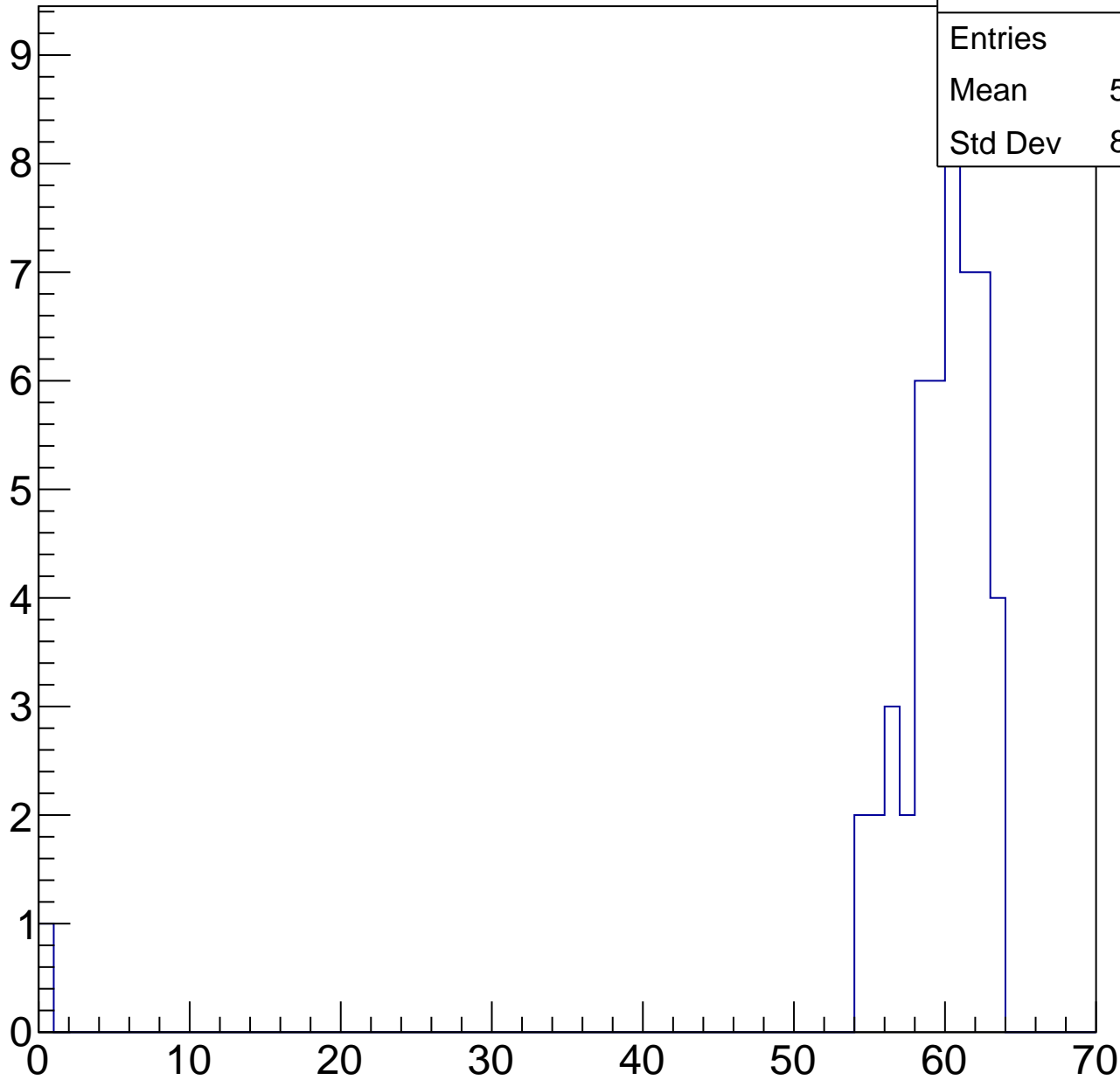
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.27
Std Dev	8.738

ampl

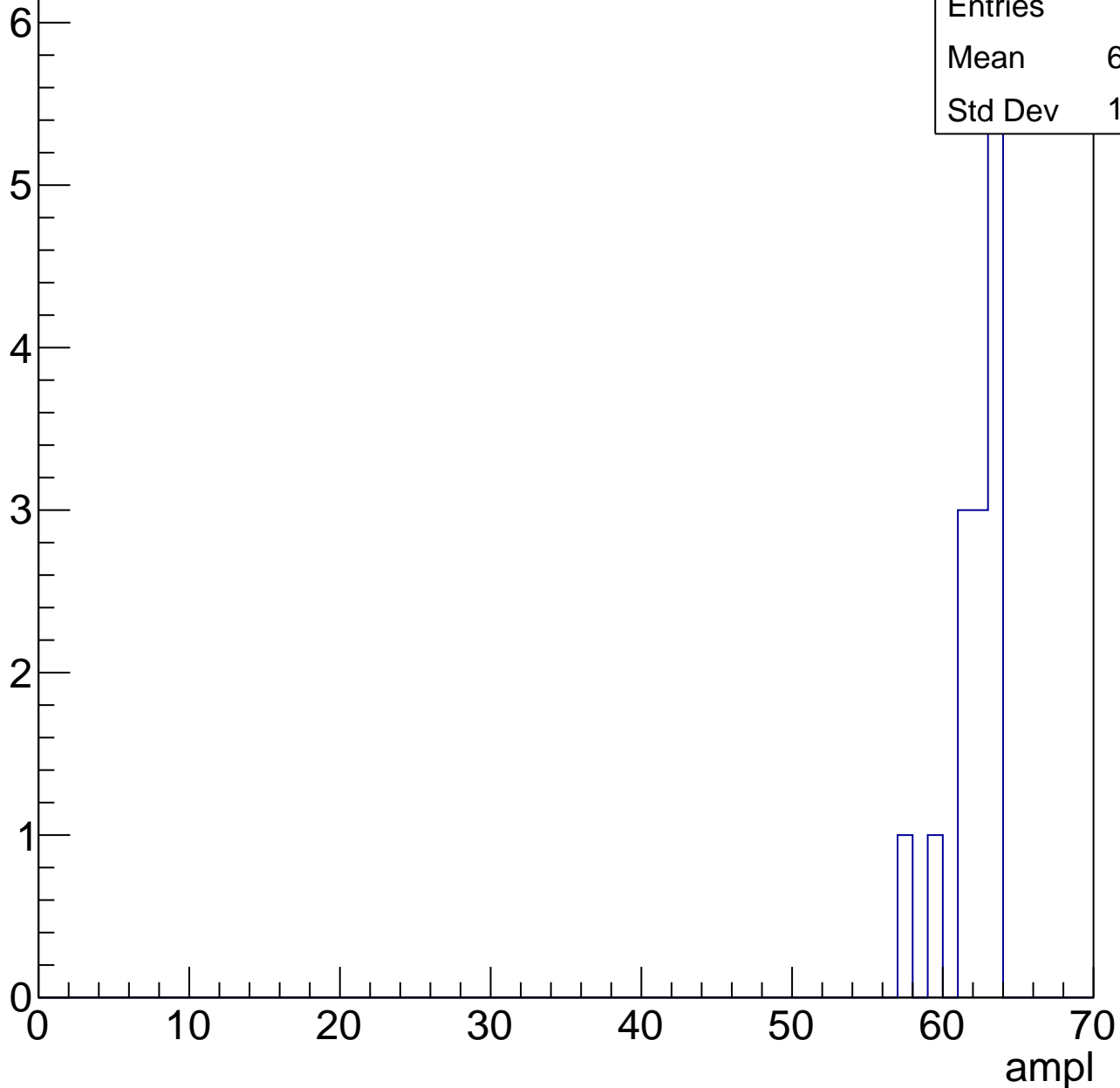


B0L002S, U12-ch49, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	14
Mean	61.64
Std Dev	1.716



B0L002S, U12-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U12-ch50, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	30.72
Std Dev	3.282

Gaus mean : 30.6759

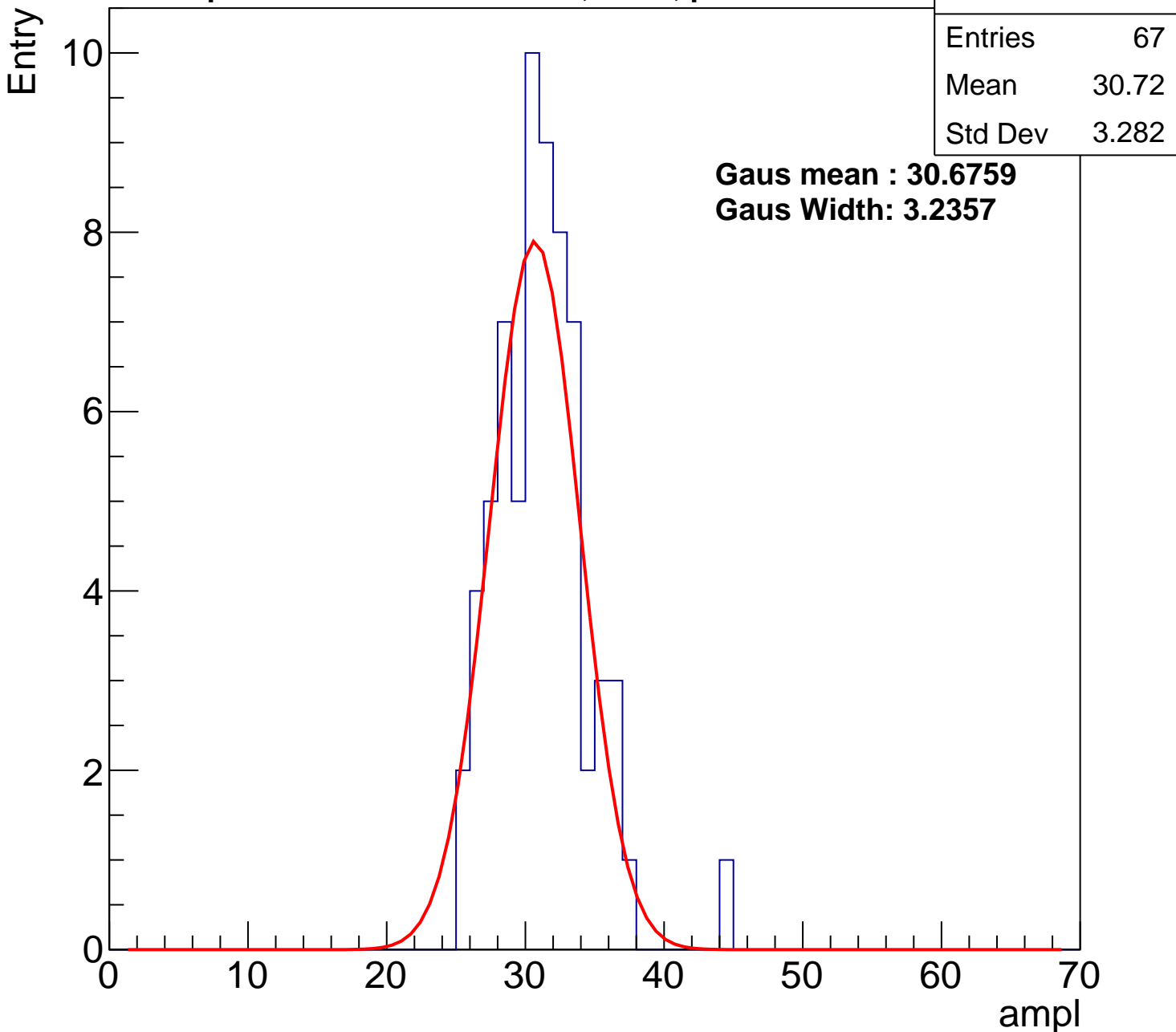
Gaus Width: 3.2357

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



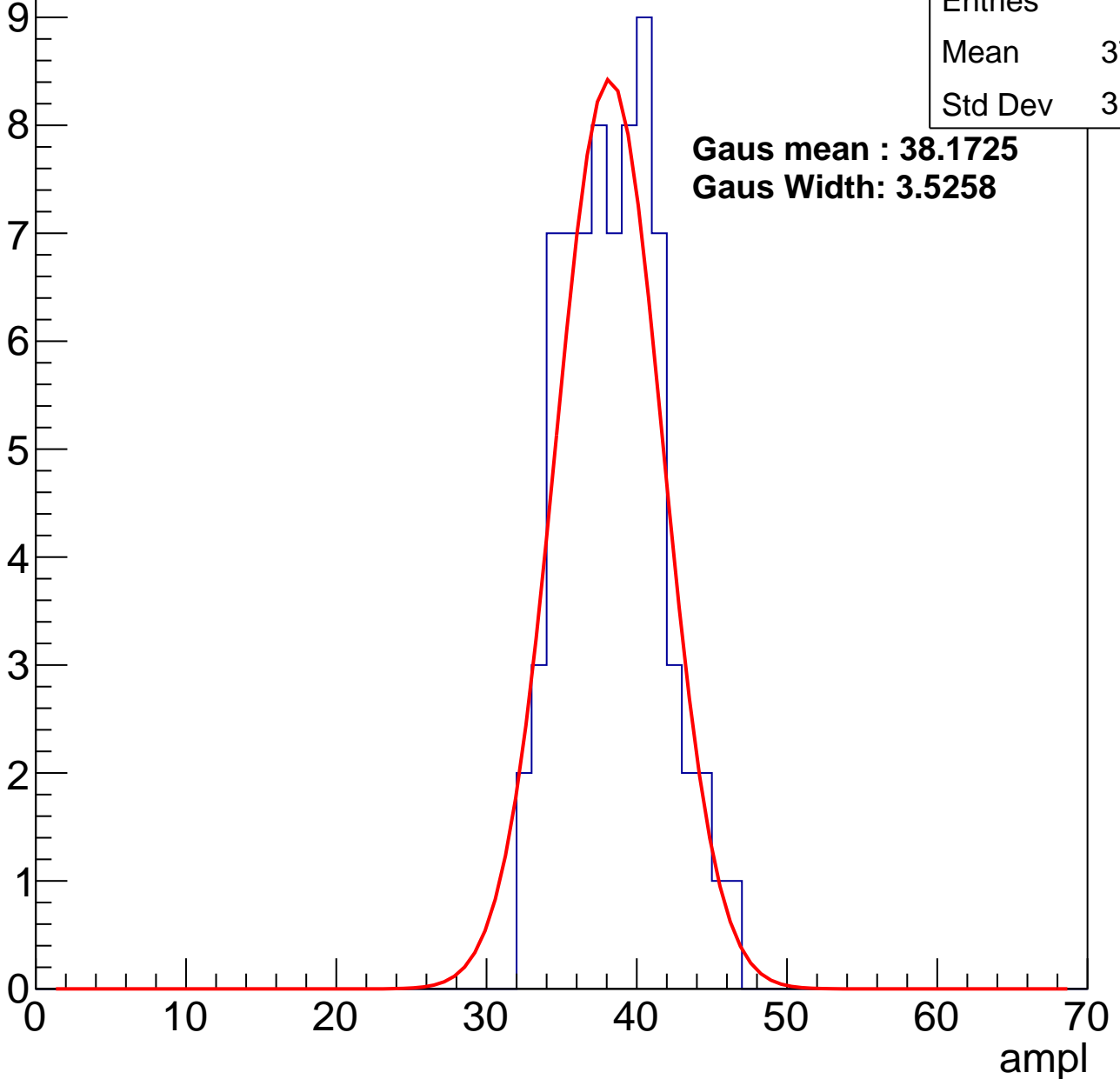
B0L002S, U12-ch50, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.97
Std Dev	3.179

Gaus mean : 38.1725
Gaus Width: 3.5258



B0L002S, U12-ch50, adc2

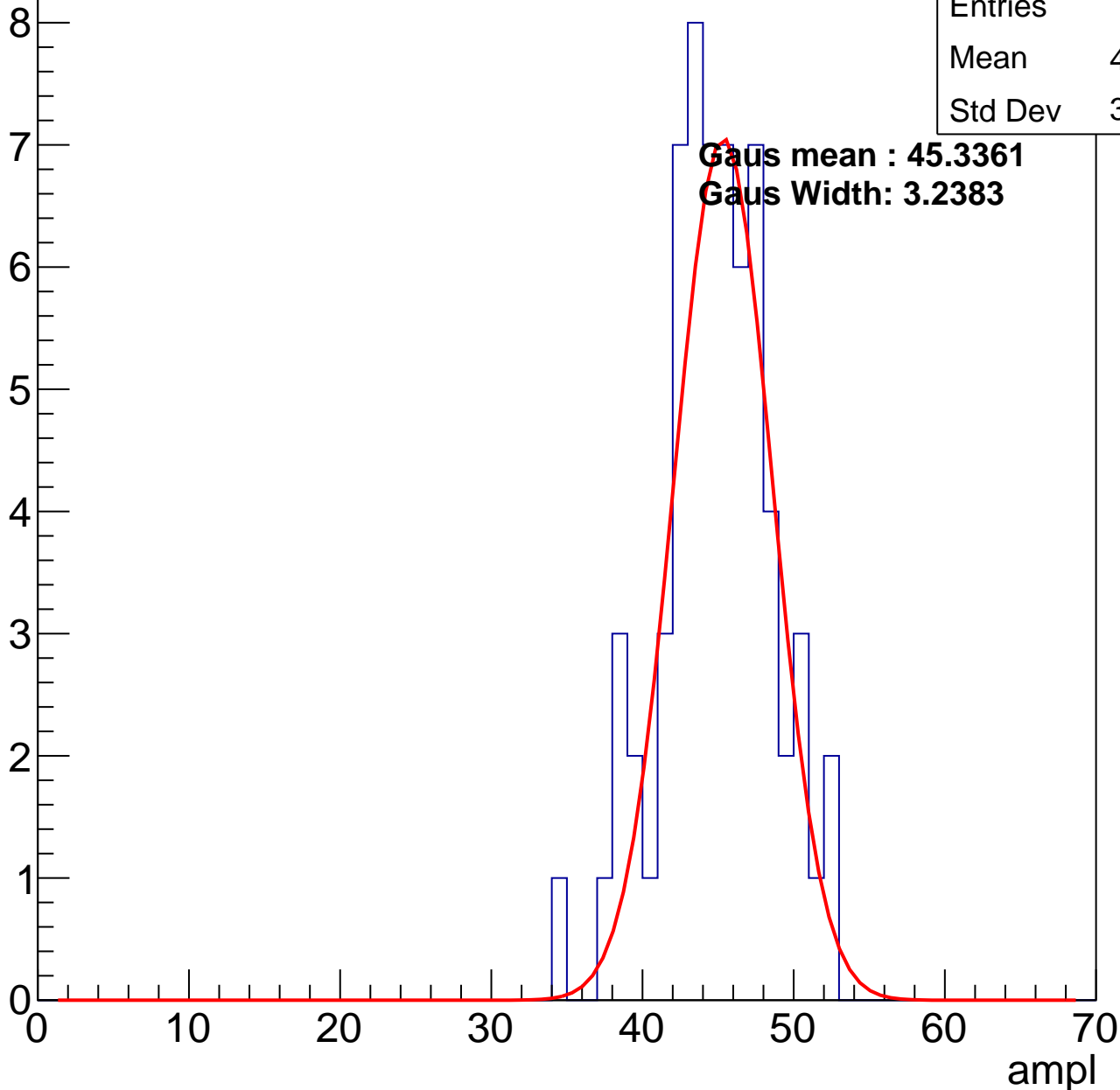
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.42
Std Dev	3.654

Gaus mean : 45.3361

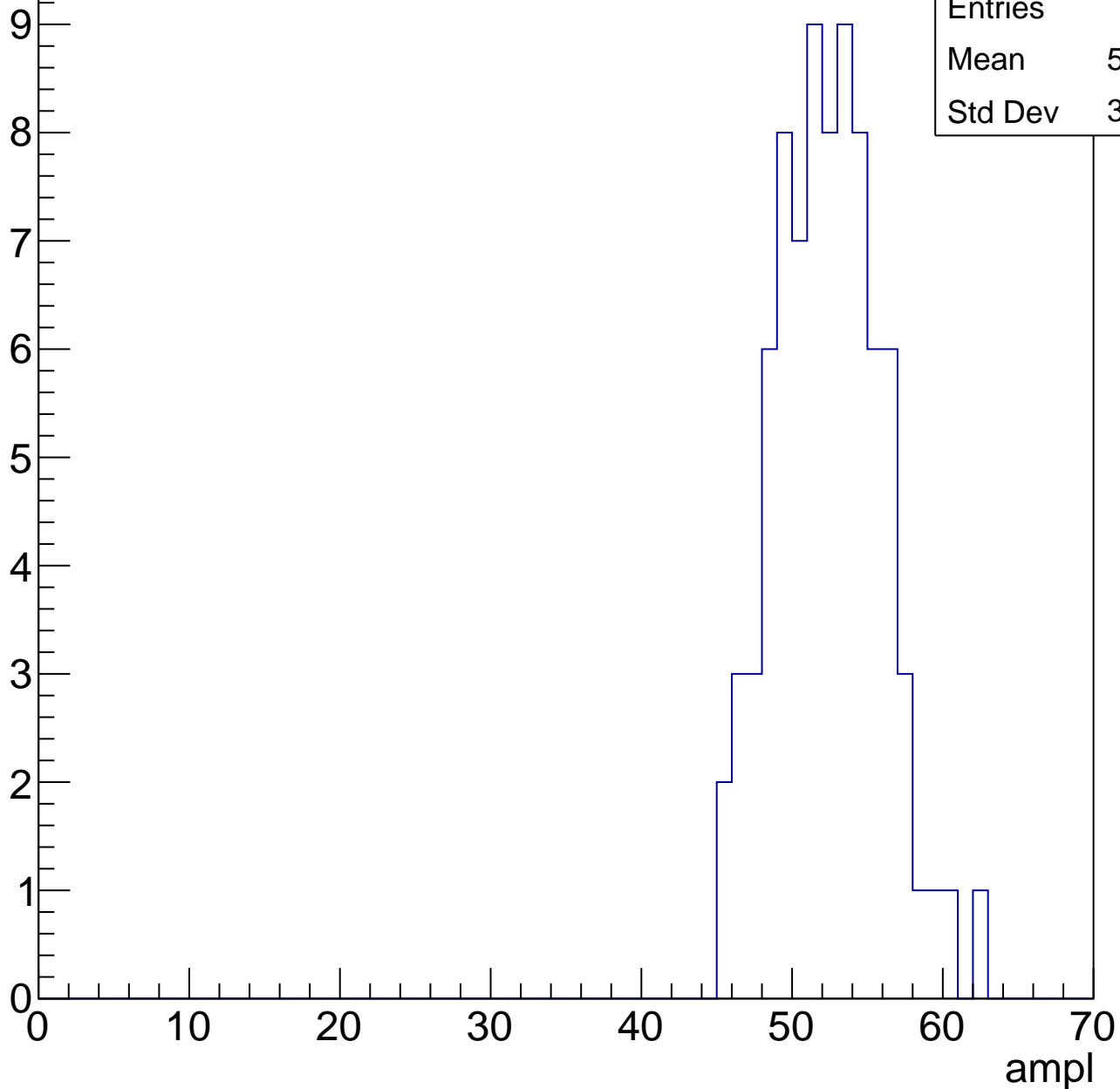
Gaus Width: 3.2383



B0L002S, U12-ch50, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



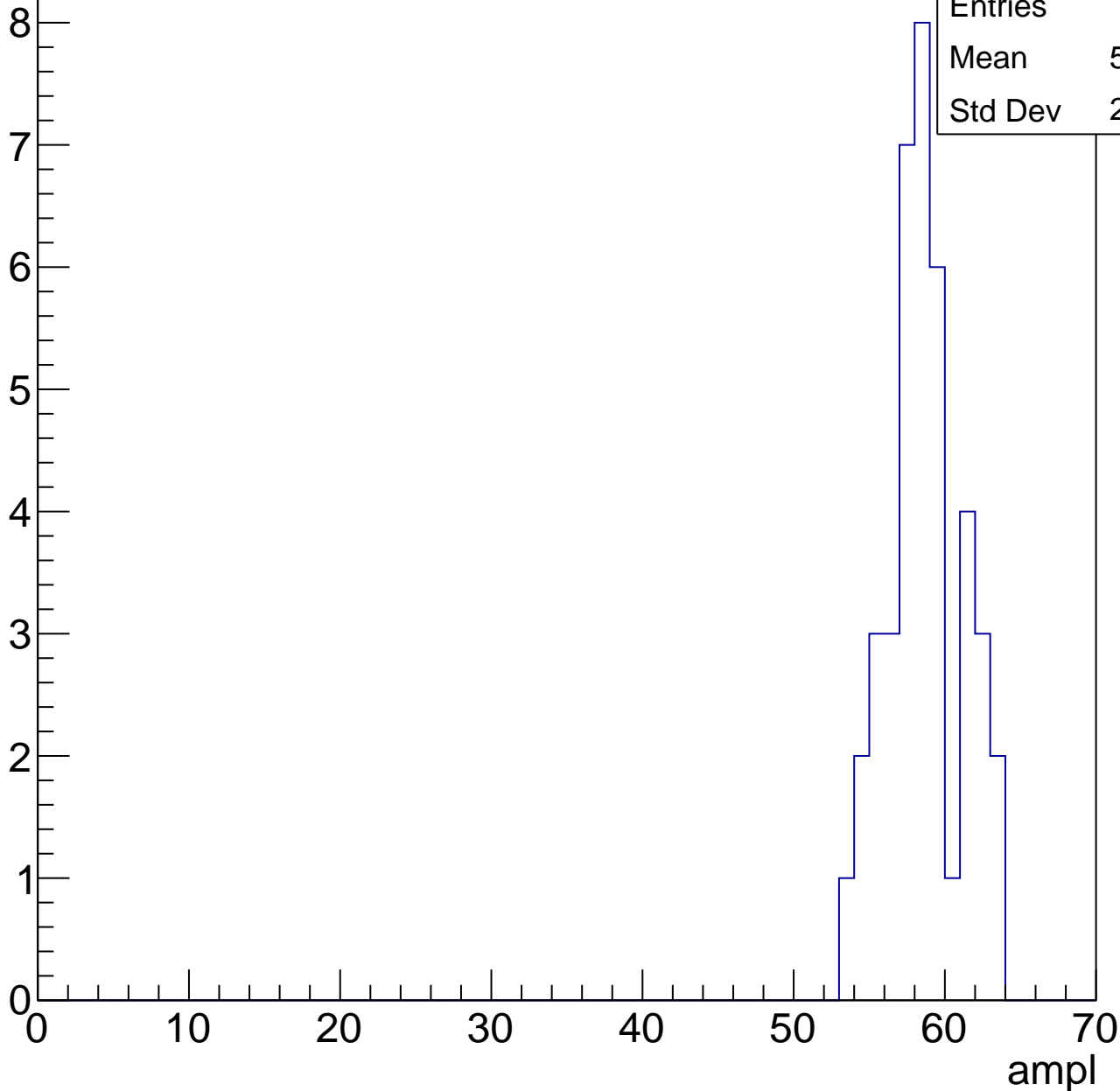
Entries	82
Mean	51.94
Std Dev	3.493

B0L002S, U12-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

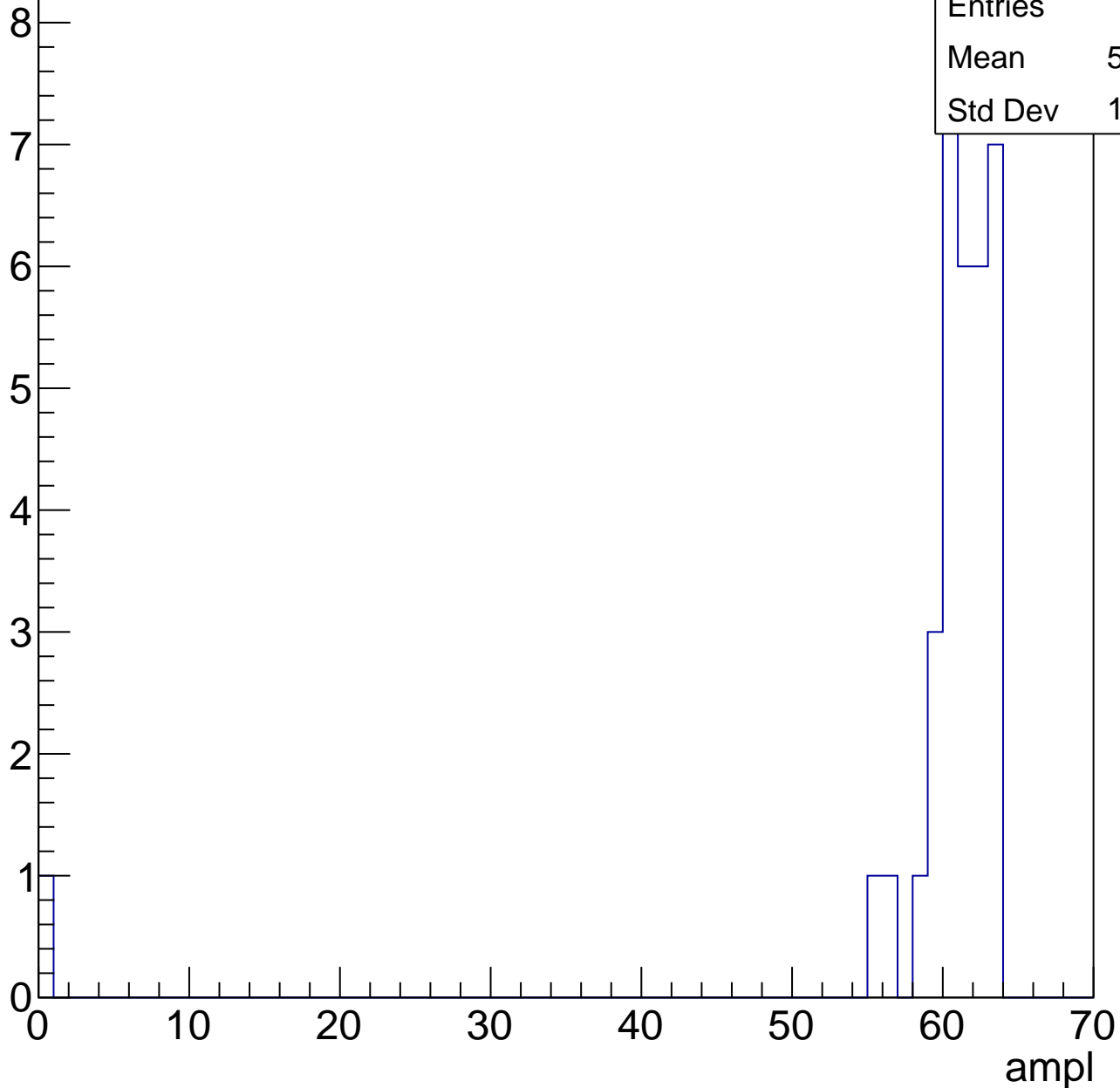
Entries	40
Mean	58.17
Std Dev	2.479



B0L002S, U12-ch50, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch51, adc0

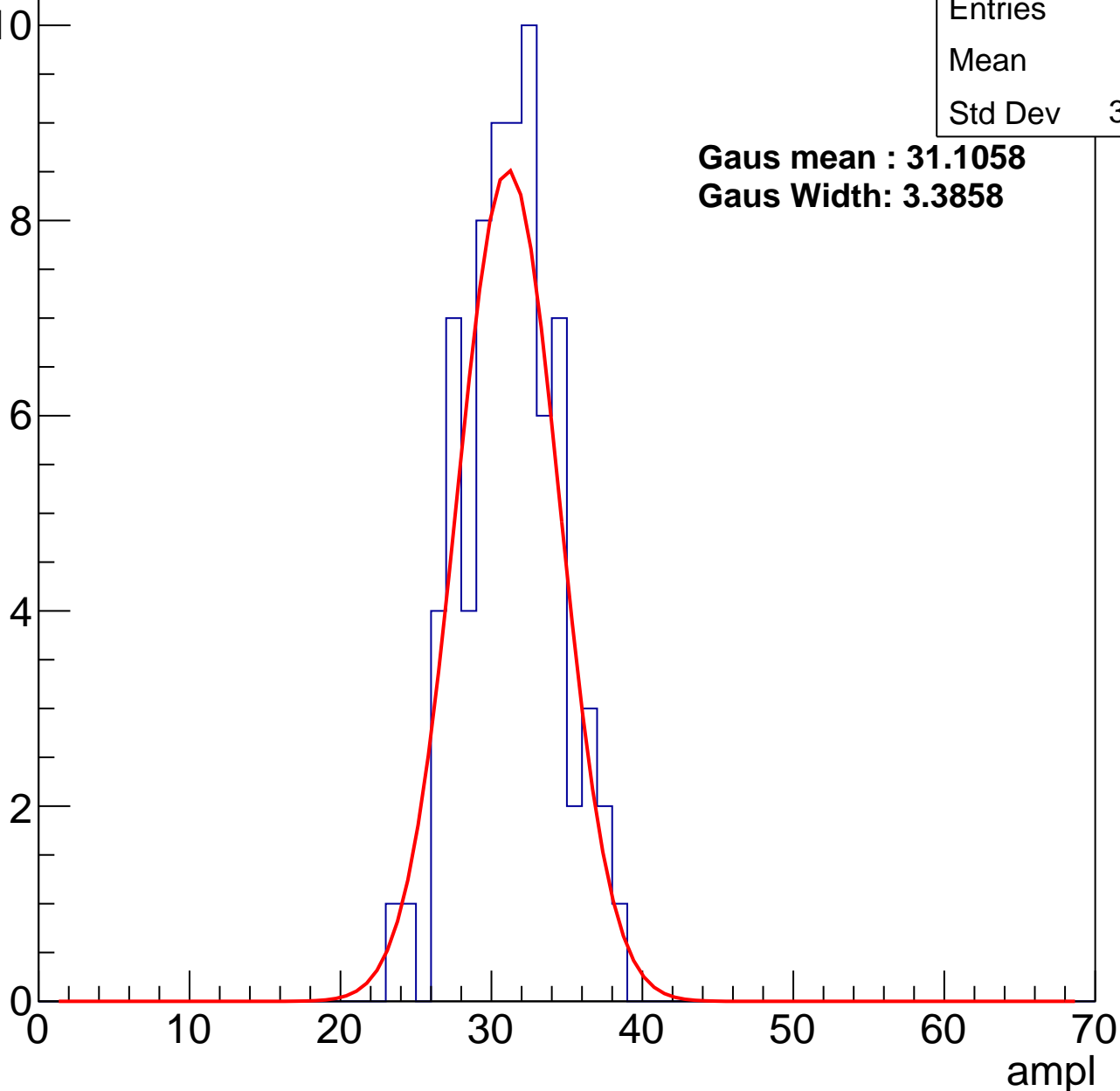
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	30.8
Std Dev	3.128

Gaus mean : 31.1058

Gaus Width: 3.3858



B0L002S, U12-ch51, adc1

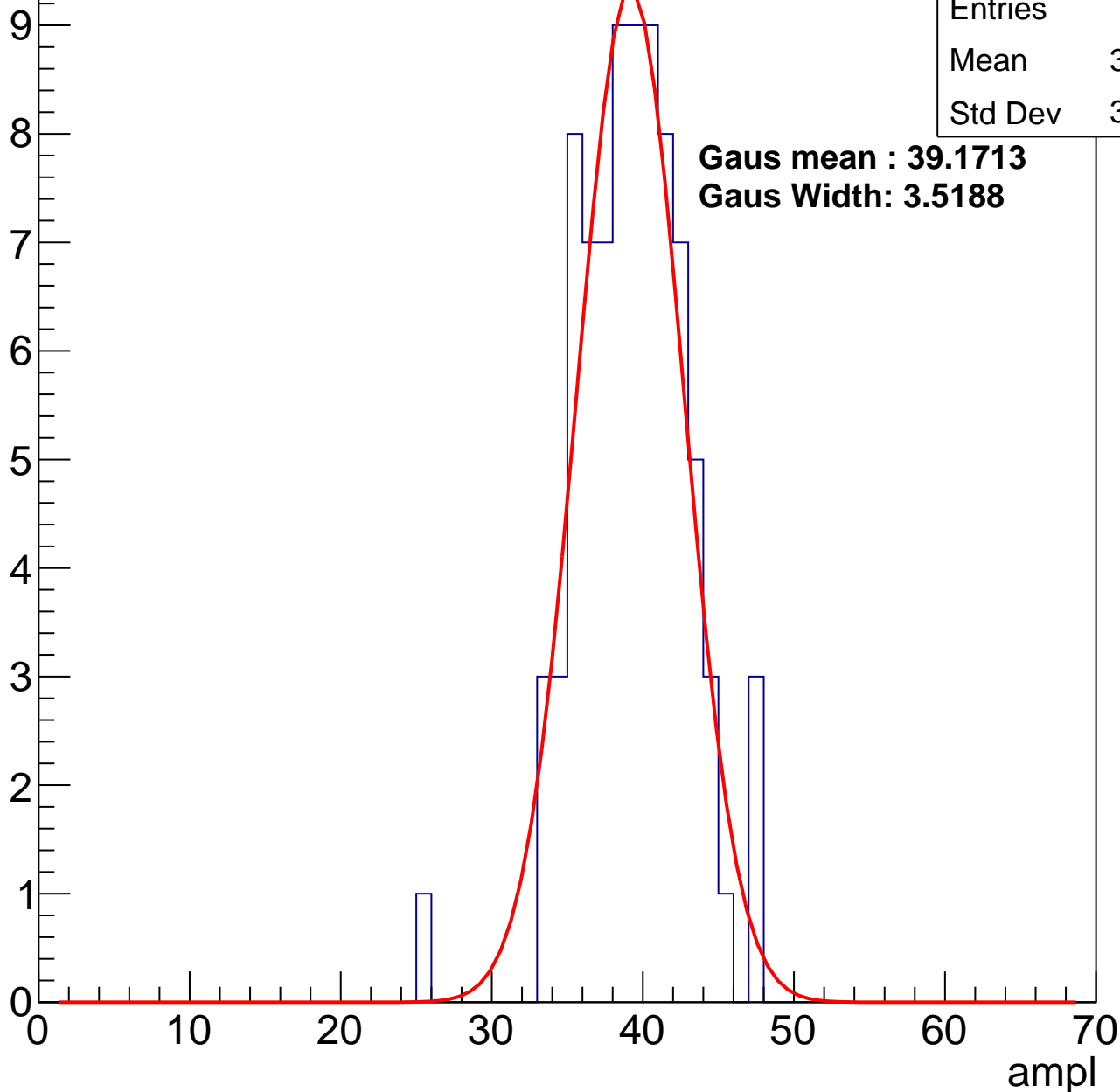
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	38.86
Std Dev	3.628

Gaus mean : 39.1713

Gaus Width: 3.5188



B0L002S, U12-ch51, adc2

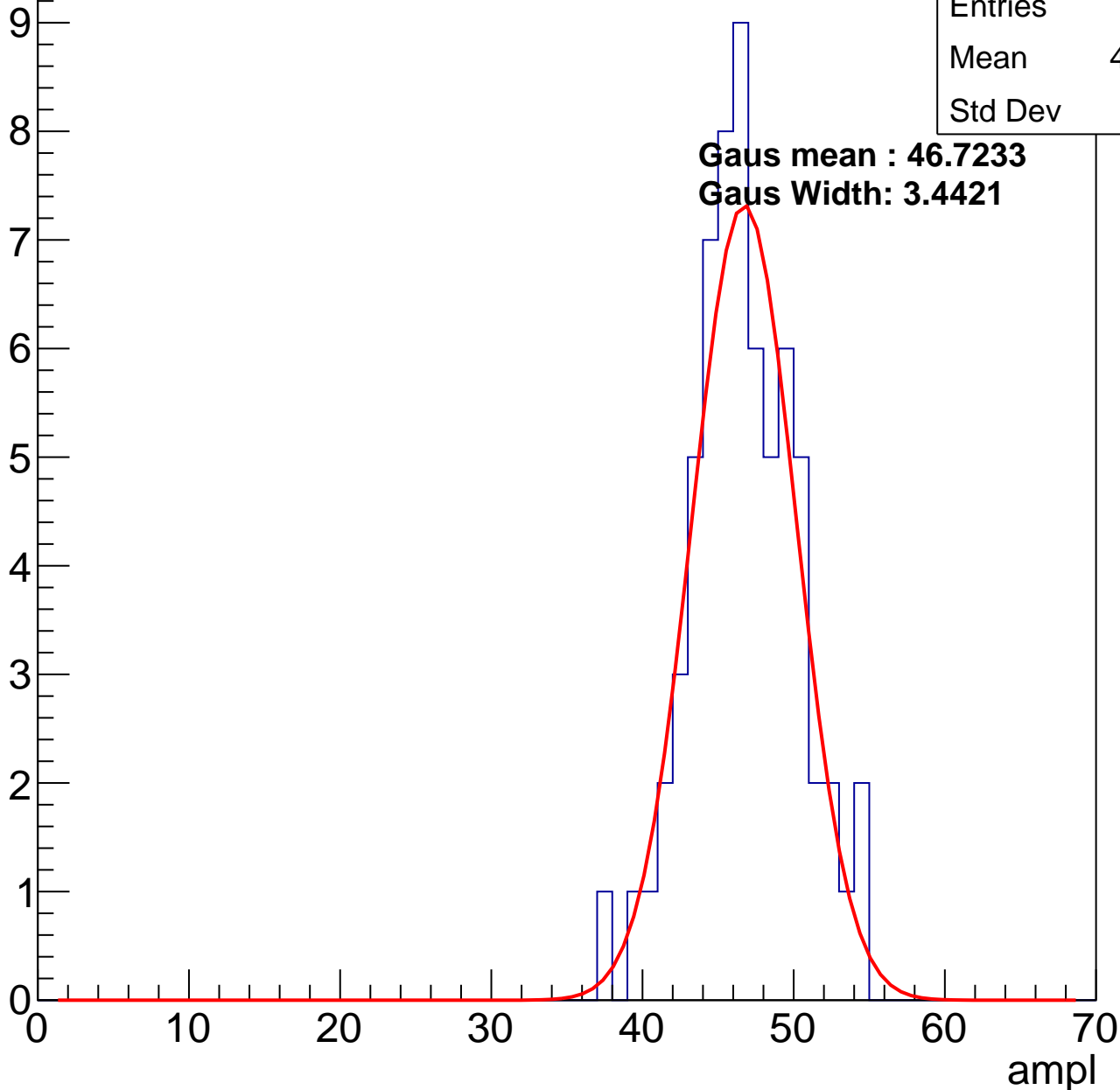
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	46.27
Std Dev	3.48

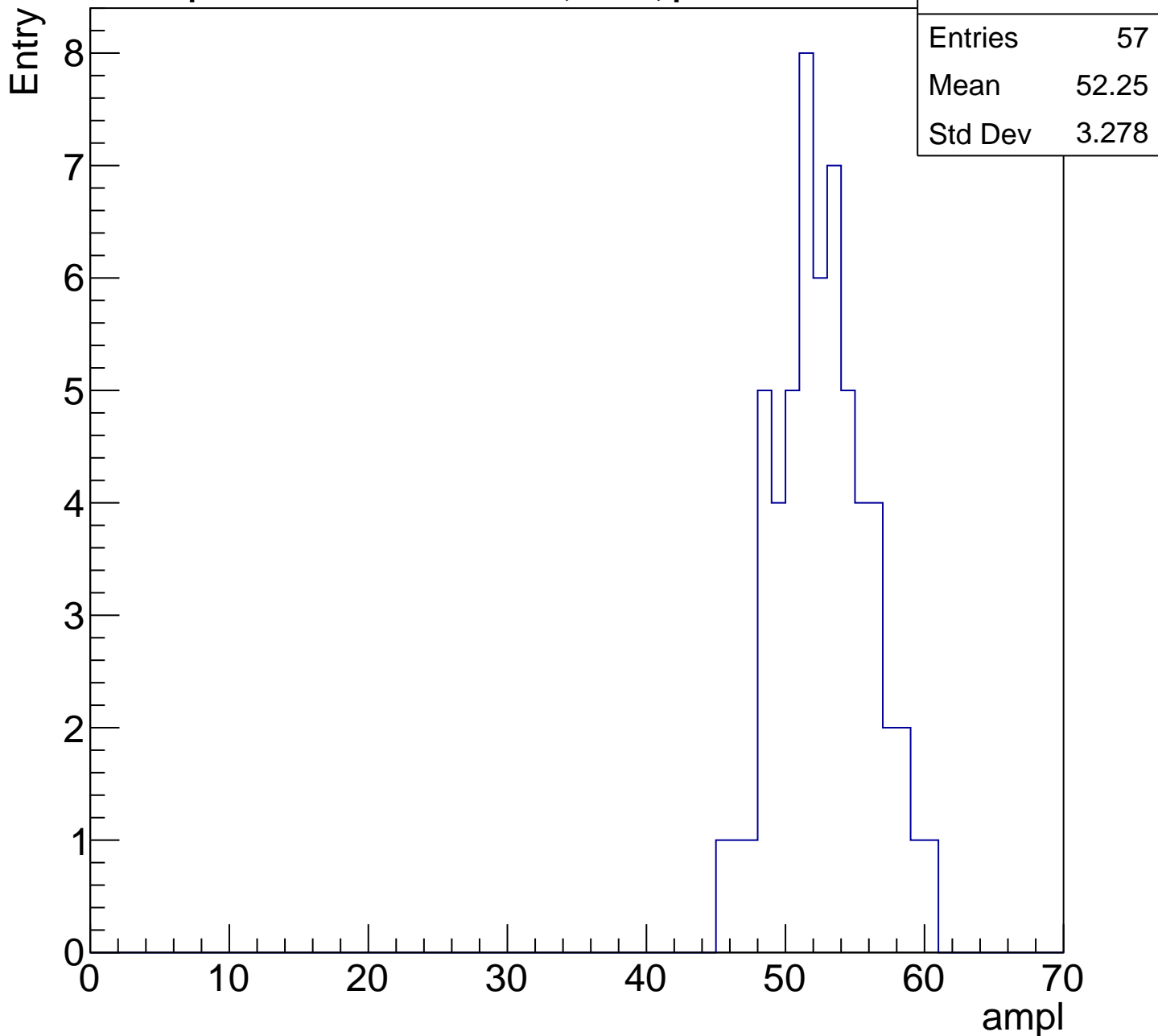
Gaus mean : 46.7233

Gaus Width: 3.4421



B0L002S, U12-ch51, adc3

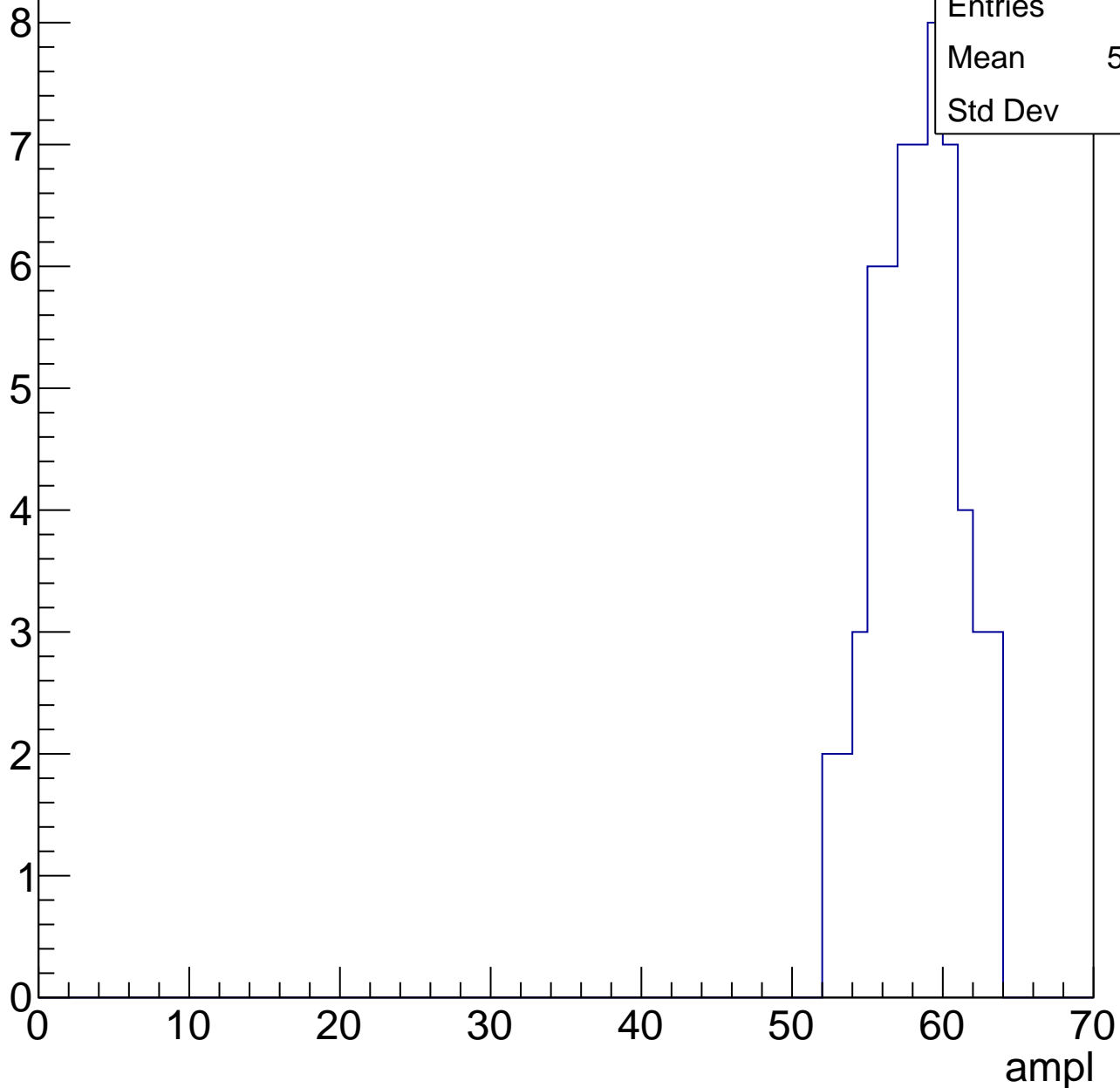
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U12-ch51, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



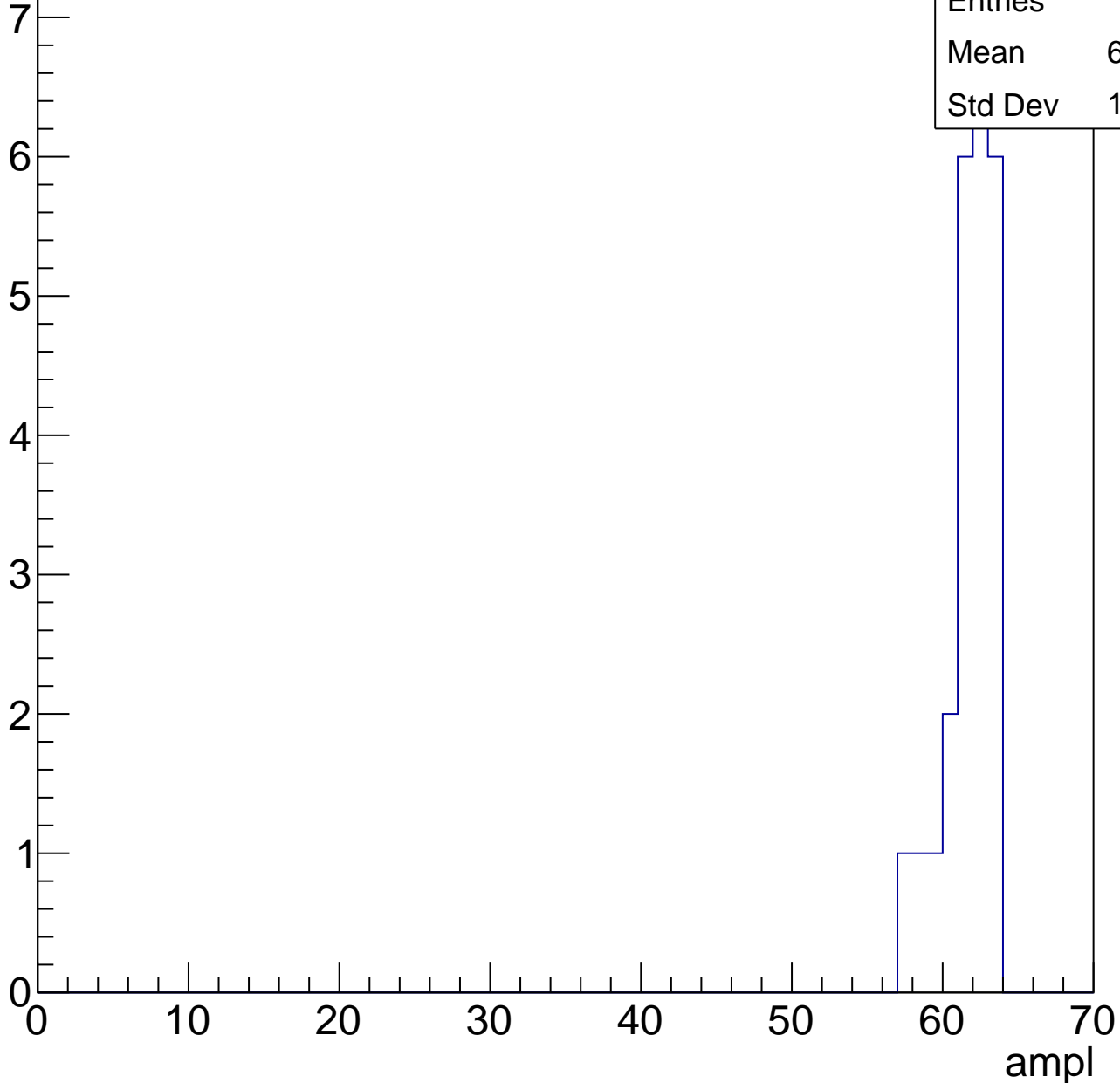
Entries	58
Mean	57.83
Std Dev	2.78

B0L002S, U12-ch51, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	61.33
Std Dev	1.572



B0L002S, U12-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch52, adc0

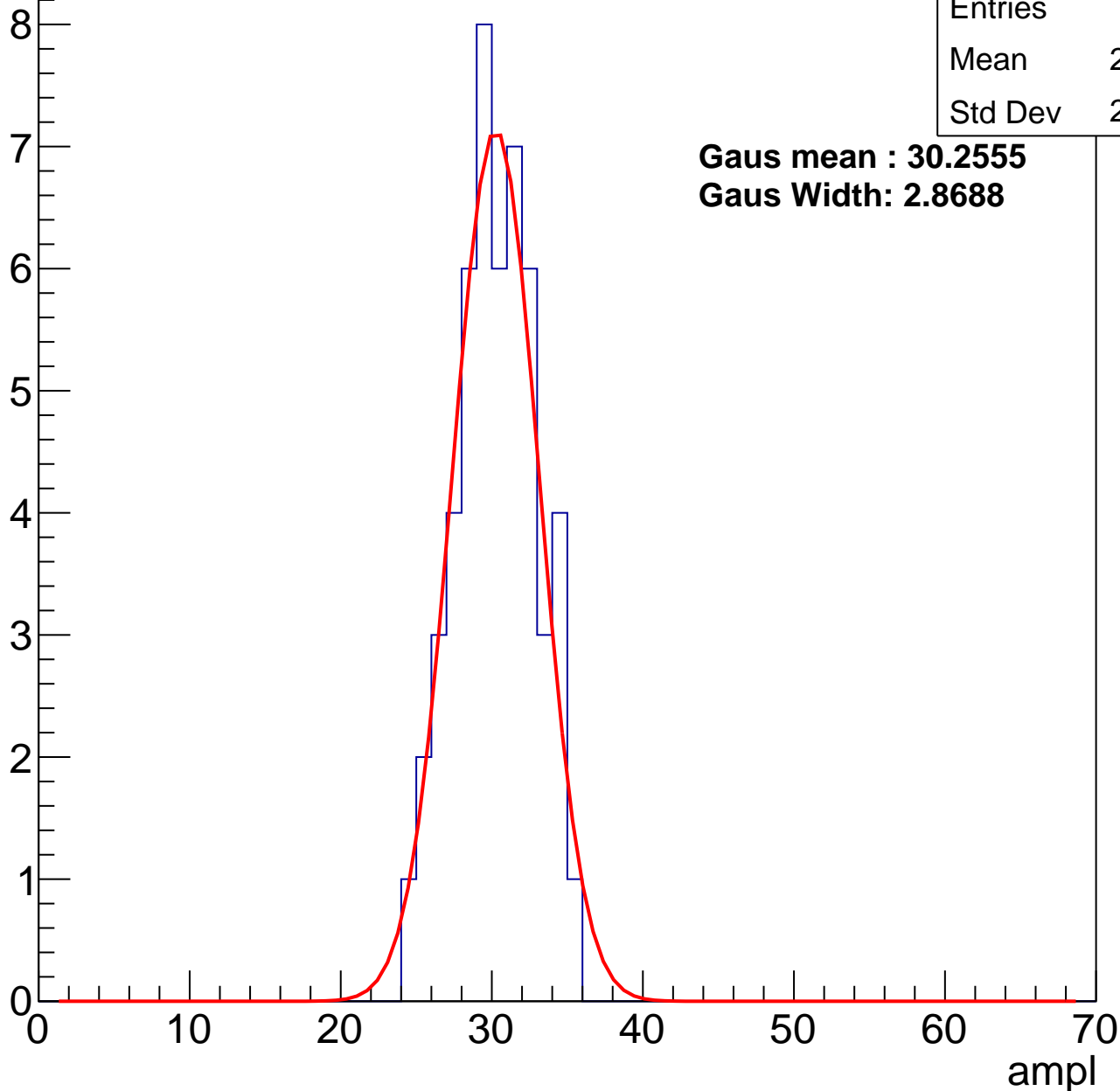
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	29.78
Std Dev	2.607

Gaus mean : 30.2555

Gaus Width: 2.8688



B0L002S, U12-ch52, adc1

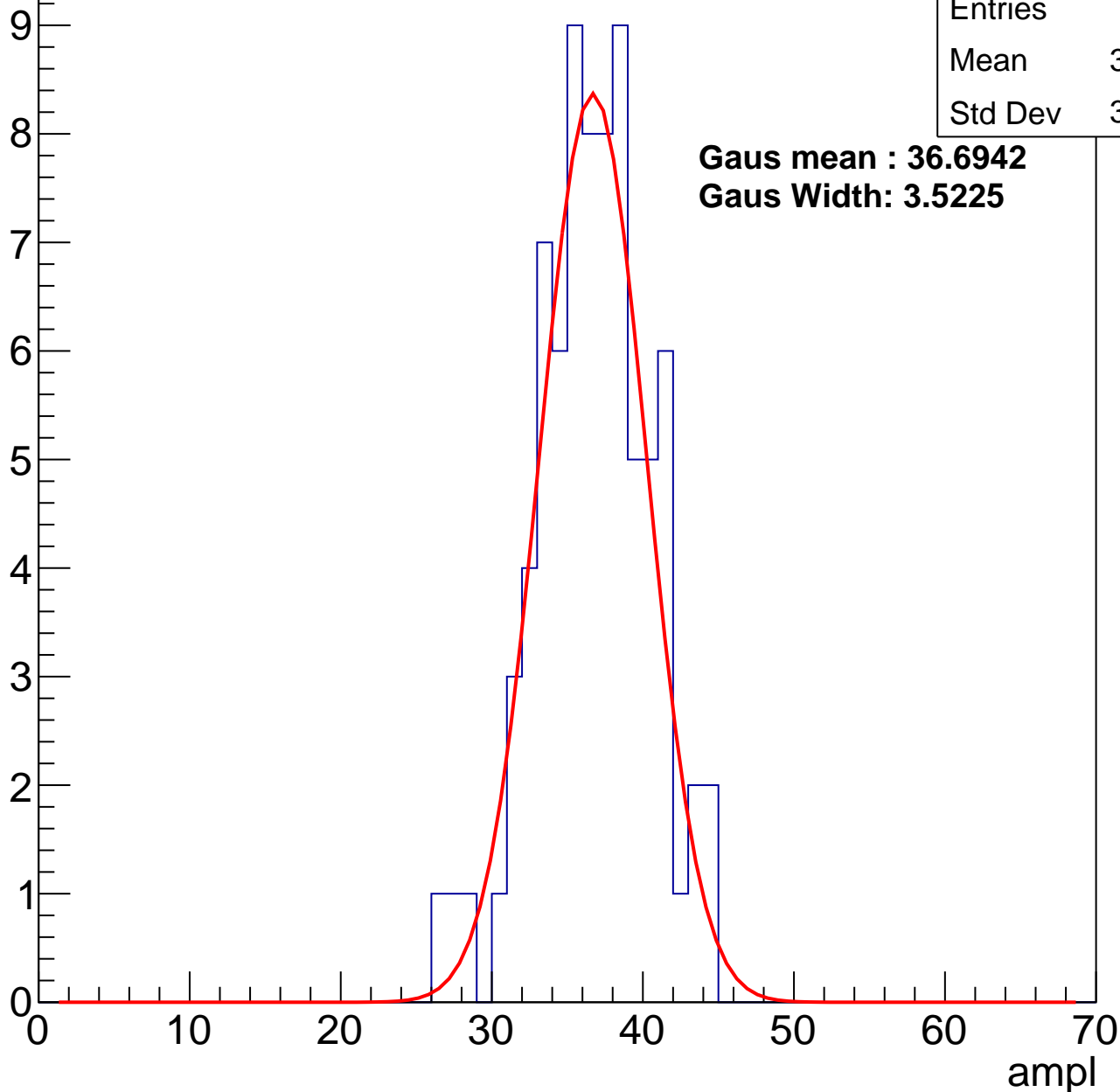
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	36.27
Std Dev	3.717

Gaus mean : 36.6942

Gaus Width: 3.5225



B0L002S, U12-ch52, adc2

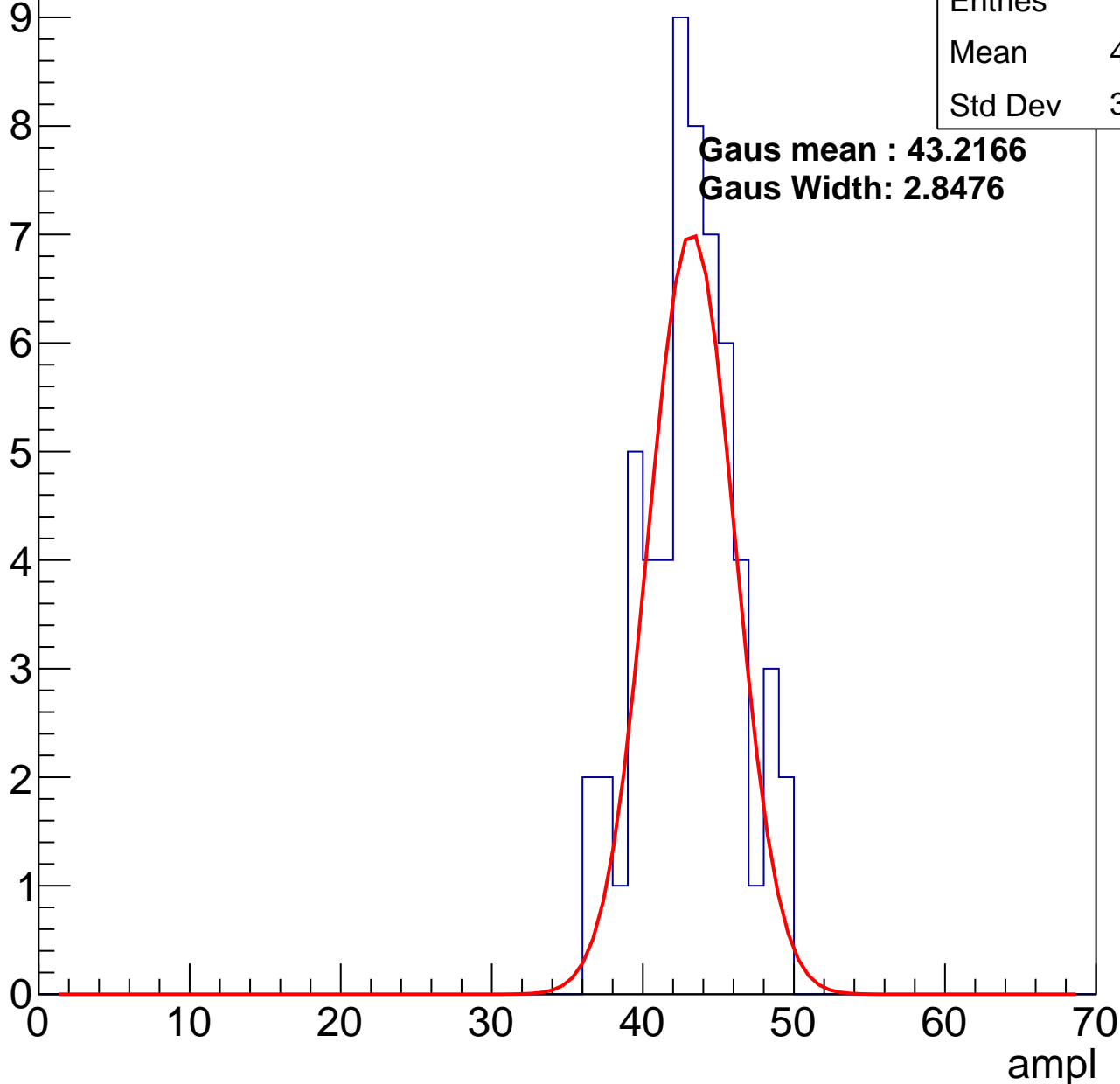
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.69
Std Dev	3.108

Gaus mean : 43.2166

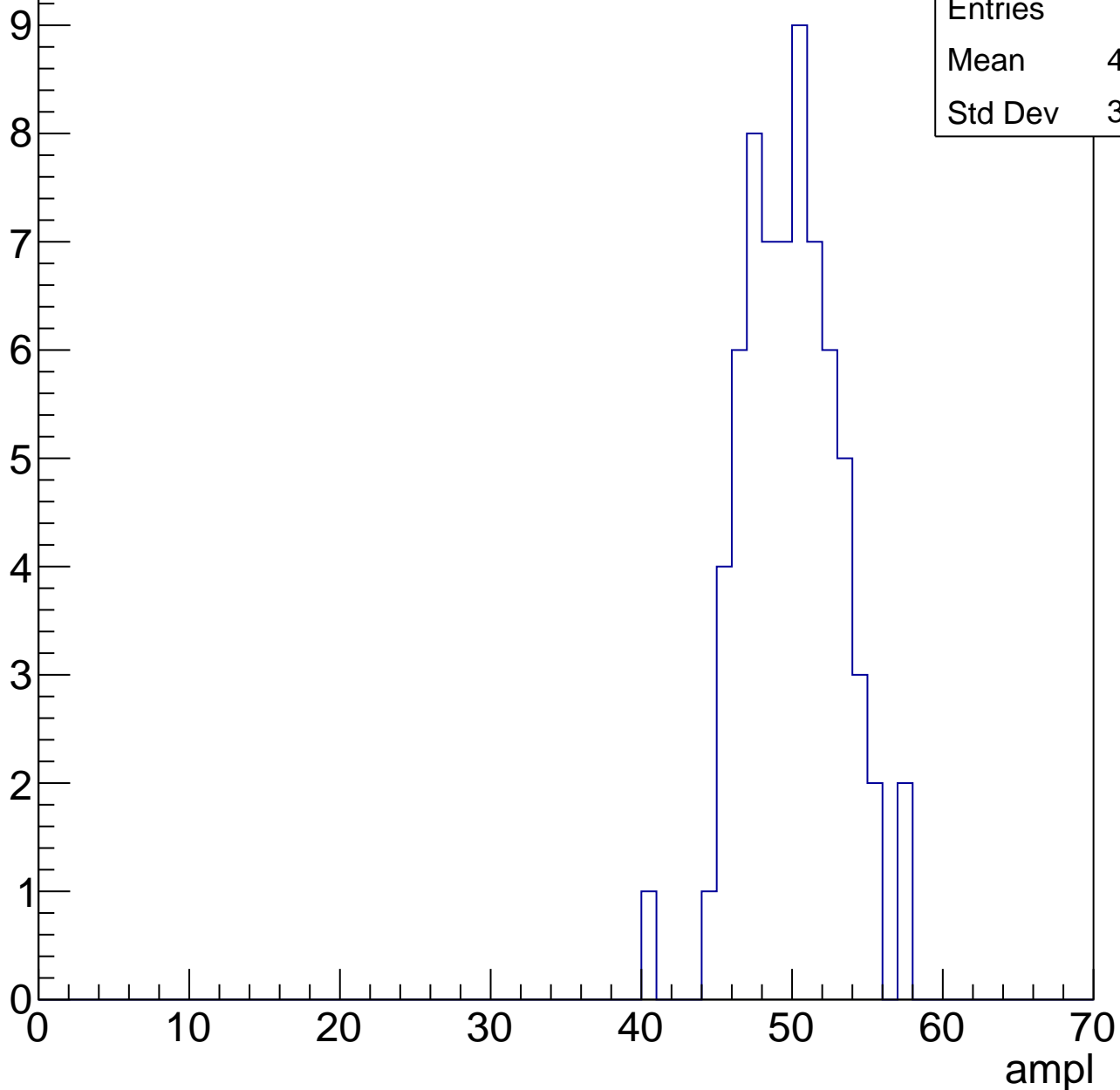
Gaus Width: 2.8476



B0L002S, U12-ch52, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

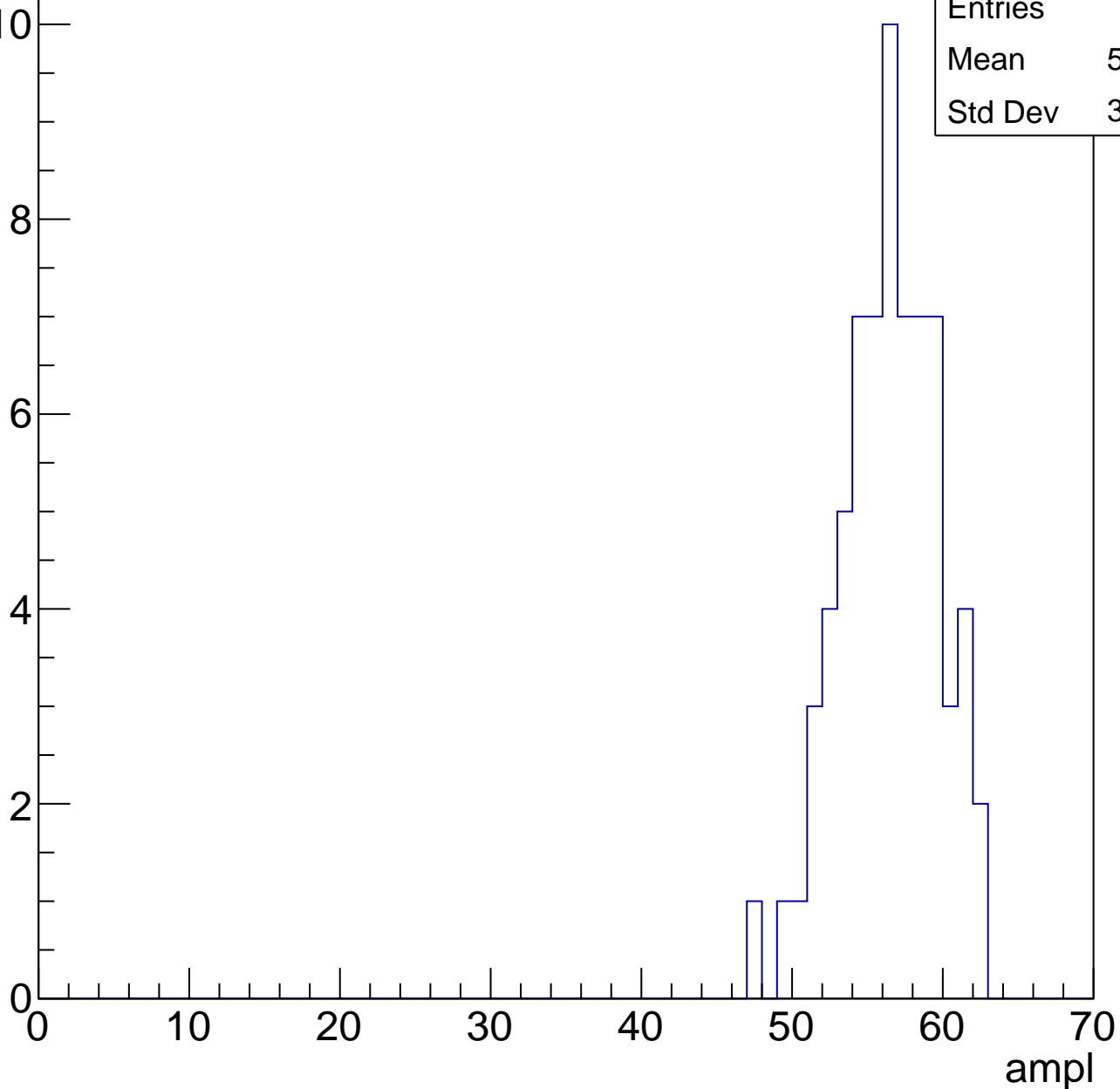


B0L002S, U12-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	55.96
Std Dev	3.192



B0L002S, U12-ch52, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	33
Mean	58.76
Std Dev	10.57

ampl

10

20

30

40

50

60

70

B0L002S, U12-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch52, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch53, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	30.54
Std Dev	3.503

Gaus mean : 30.7570

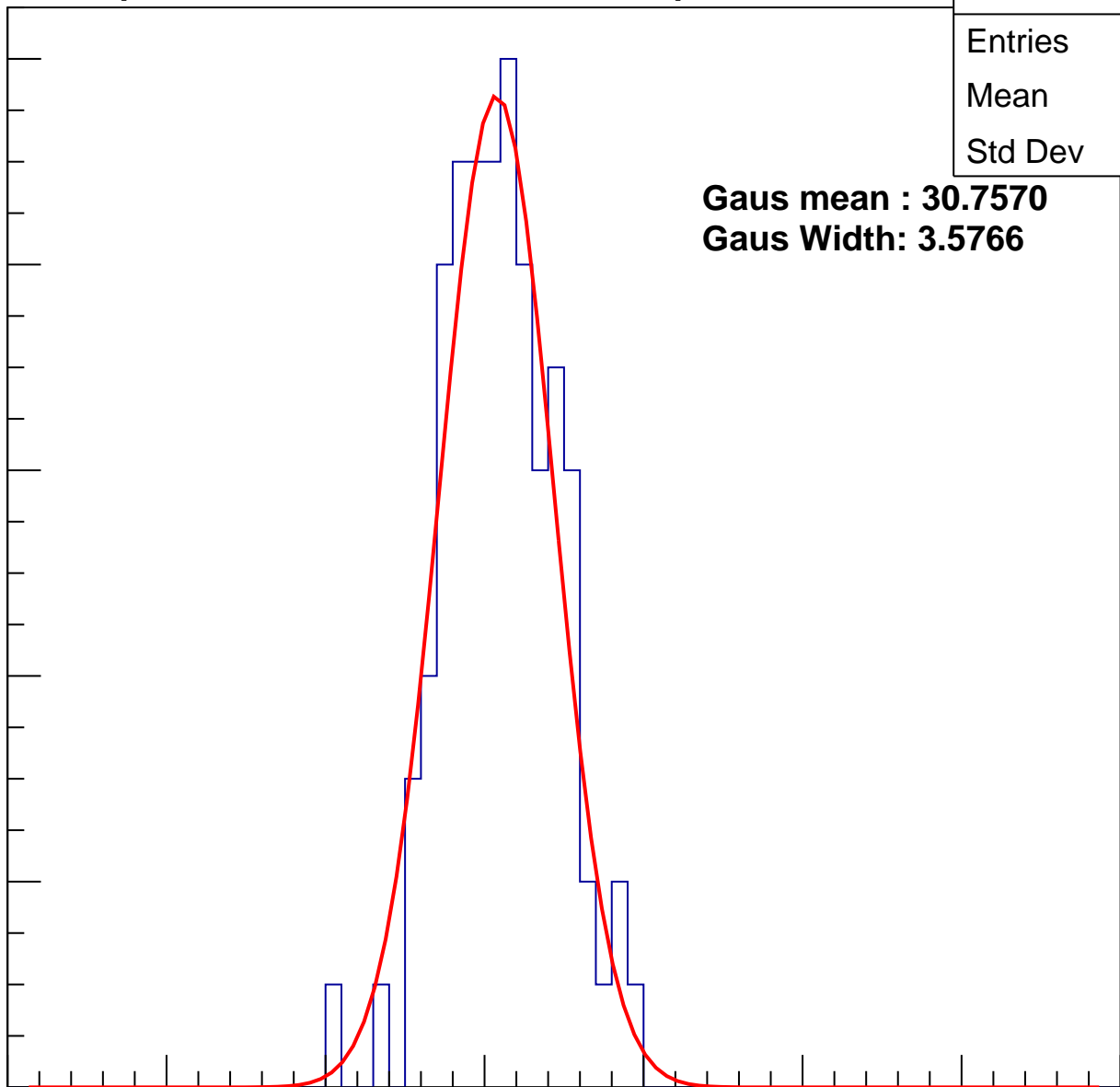
Gaus Width: 3.5766

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch53, adc1

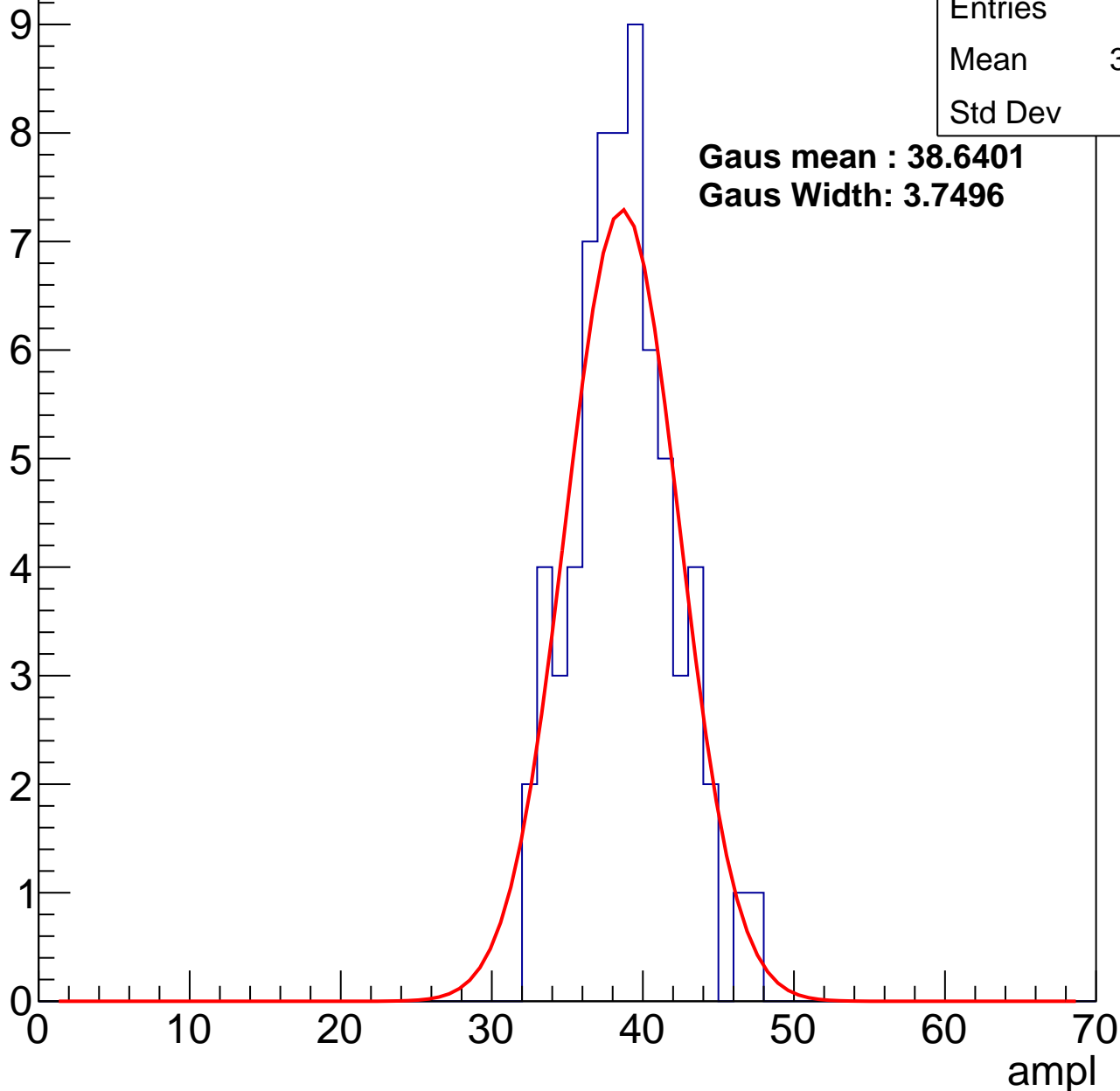
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	38.28
Std Dev	3.3

Gaus mean : 38.6401

Gaus Width: 3.7496



B0L002S, U12-ch53, adc2

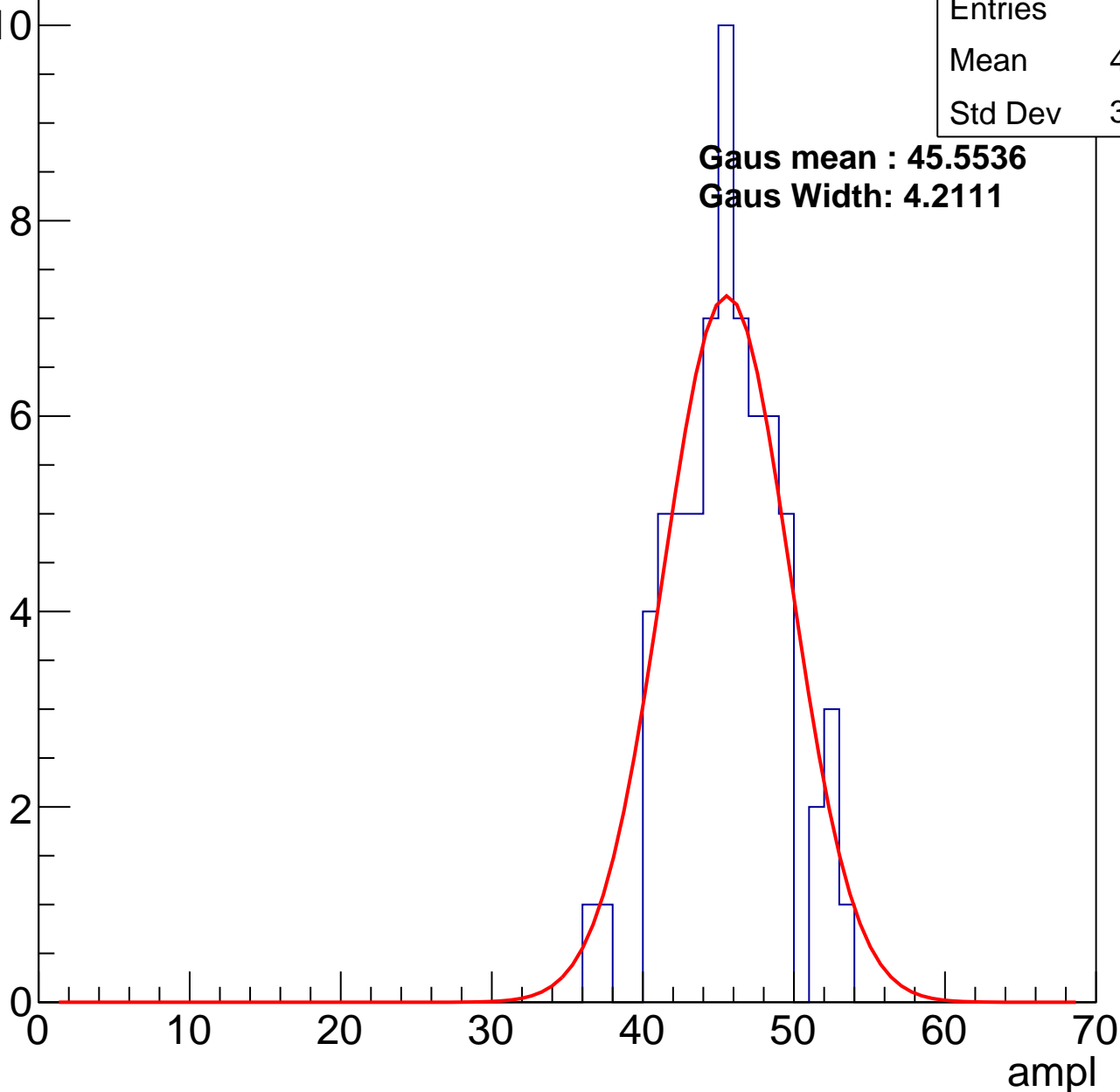
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.13
Std Dev	3.523

Gaus mean : 45.5536

Gaus Width: 4.2111

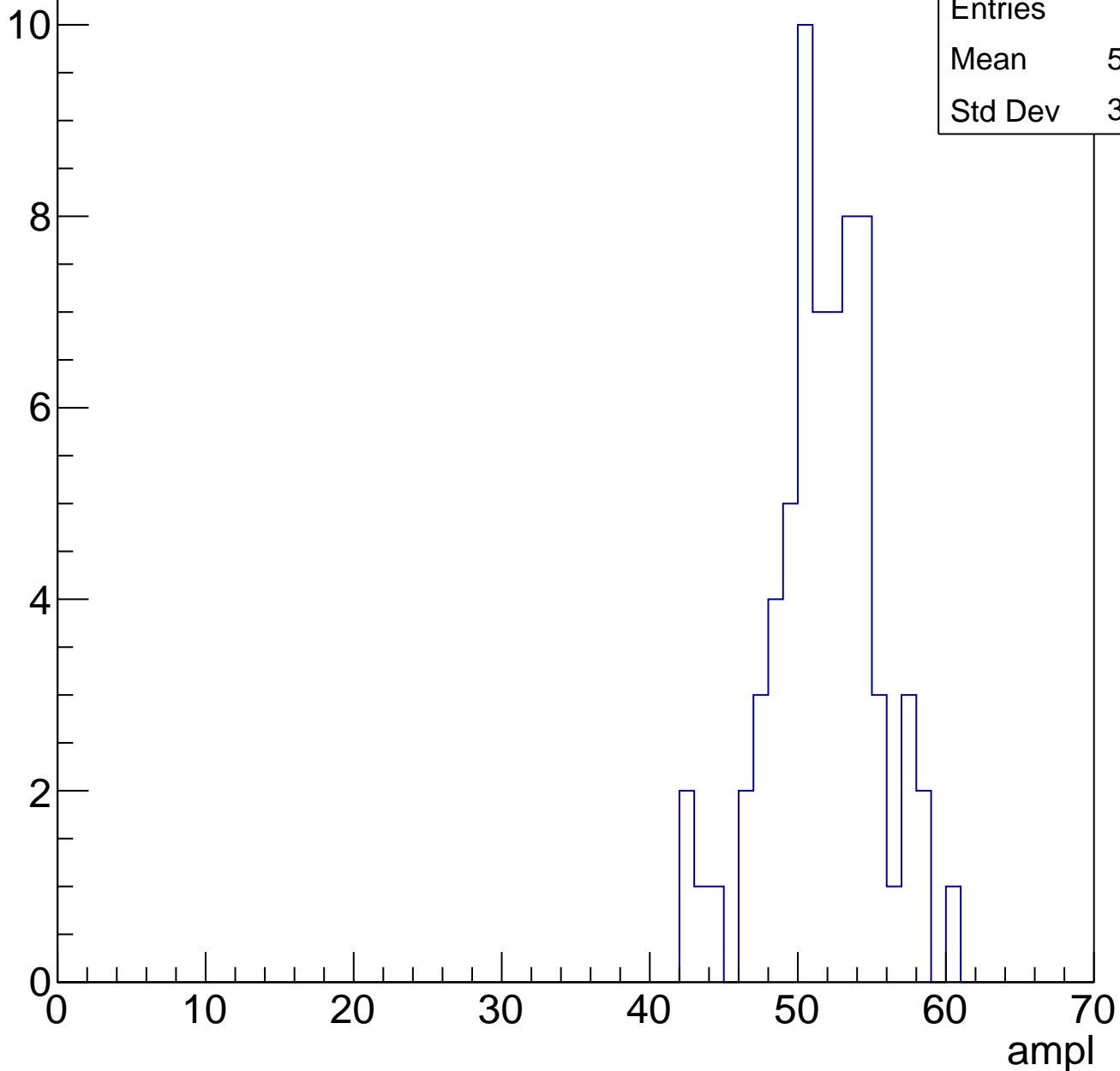


B0L002S, U12-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	51.26
Std Dev	3.649

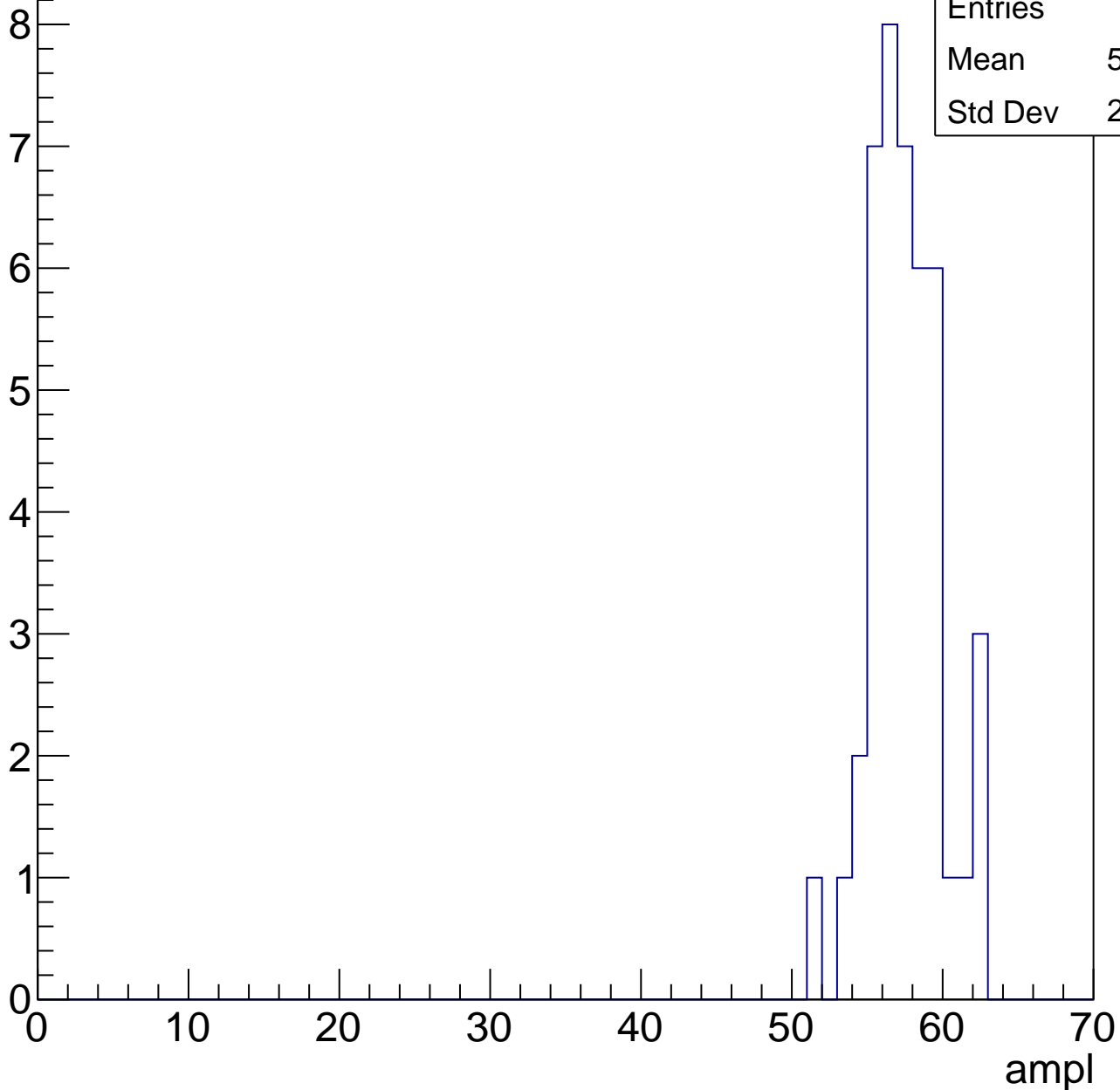


B0L002S, U12-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	57.05
Std Dev	2.342

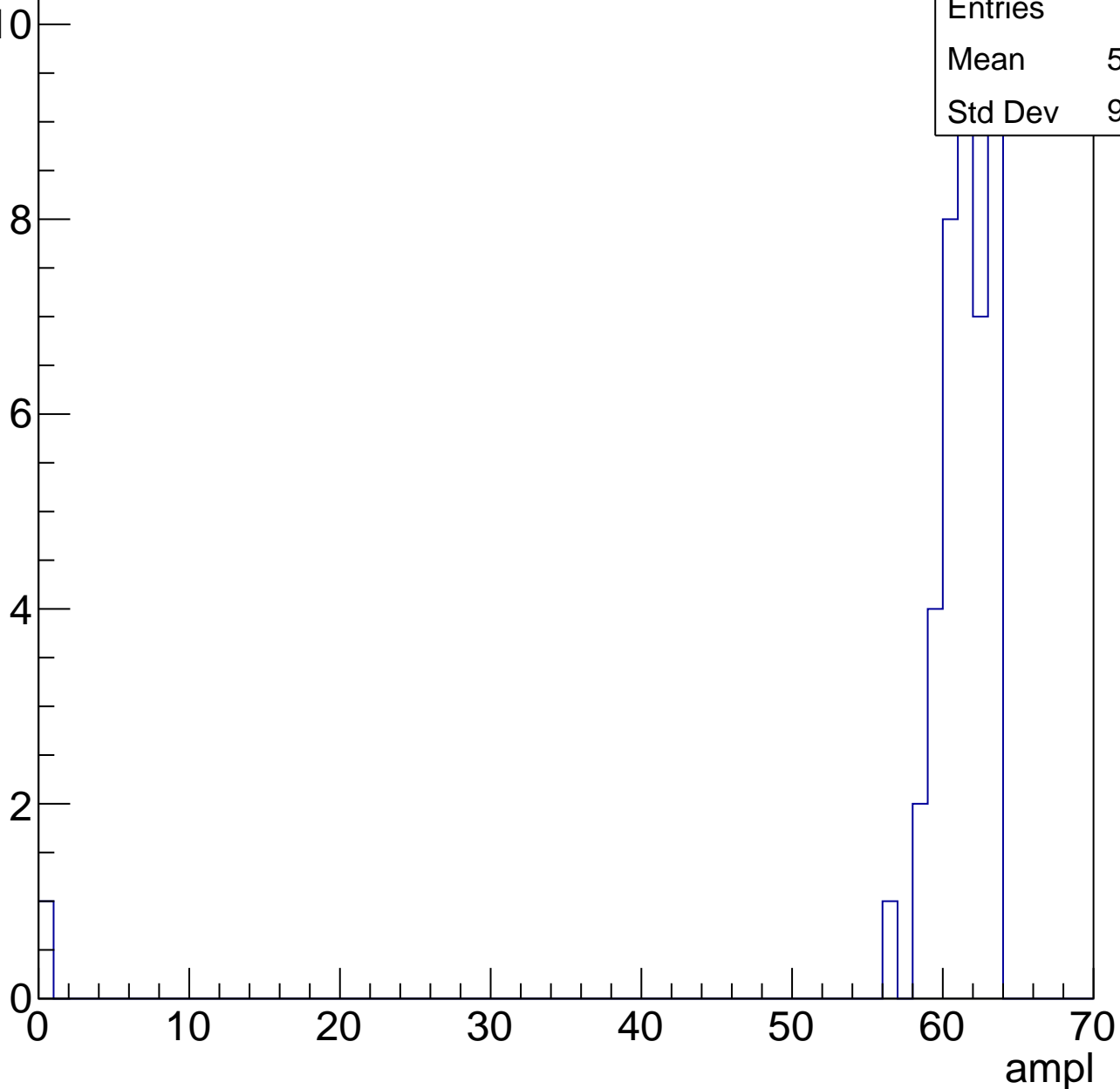


B0L002S, U12-ch53, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	59.55
Std Dev	9.445



B0L002S, U12-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch54, adc0

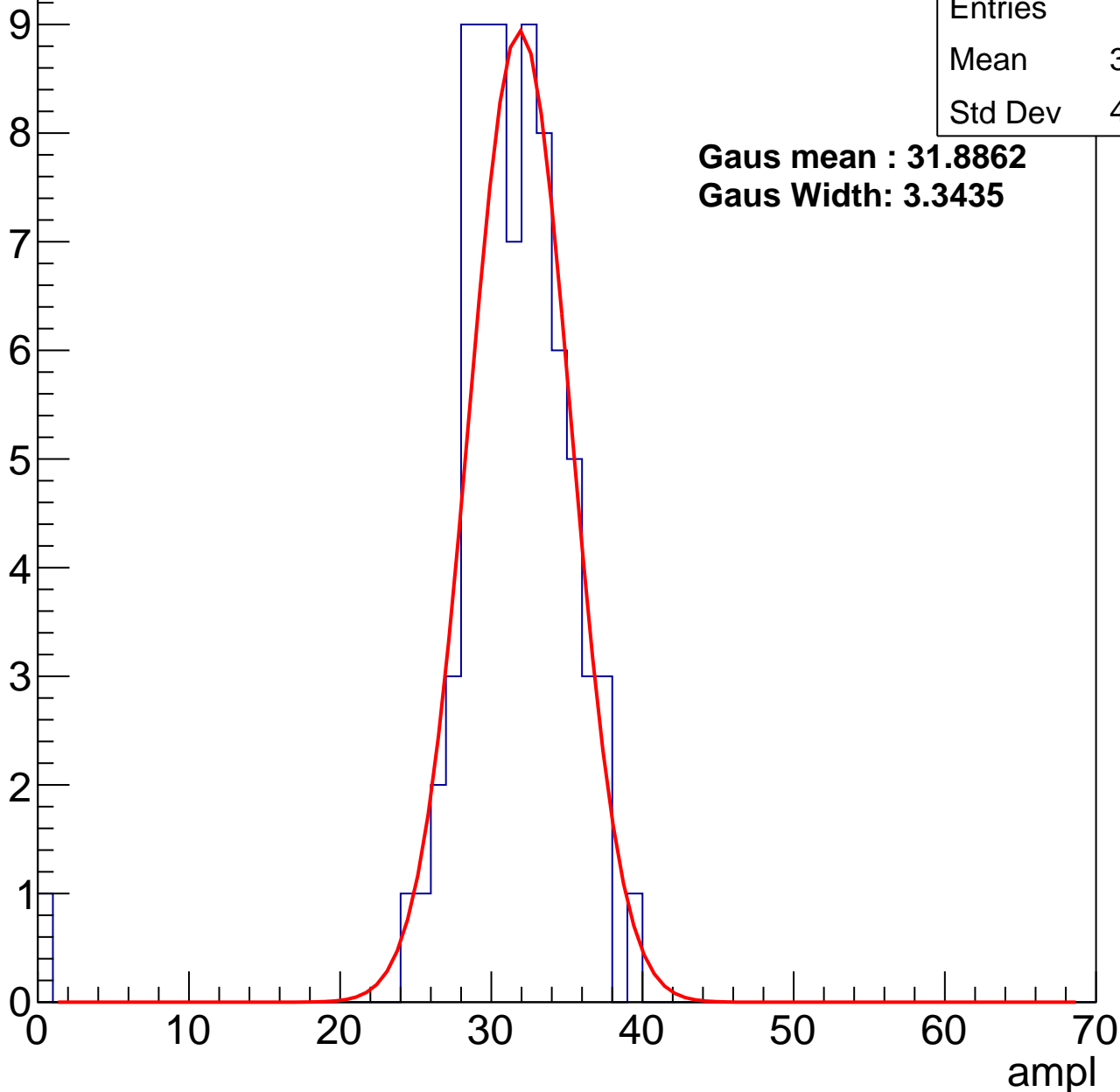
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.79
Std Dev	4.683

Gaus mean : 31.8862

Gaus Width: 3.3435



B0L002S, U12-ch54, adc1

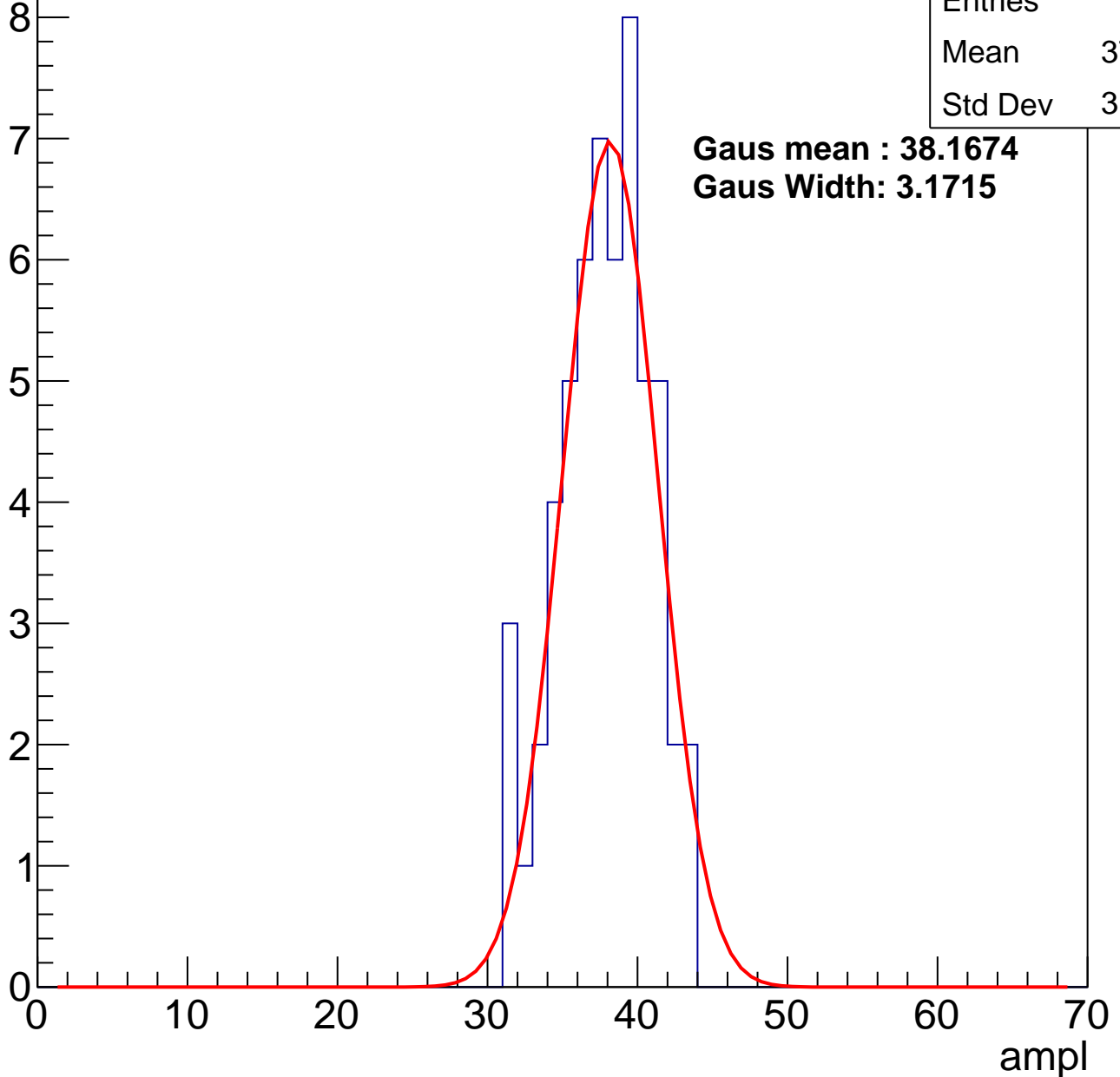
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	37.36
Std Dev	3.003

Gaus mean : 38.1674

Gaus Width: 3.1715



B0L002S, U12-ch54, adc2

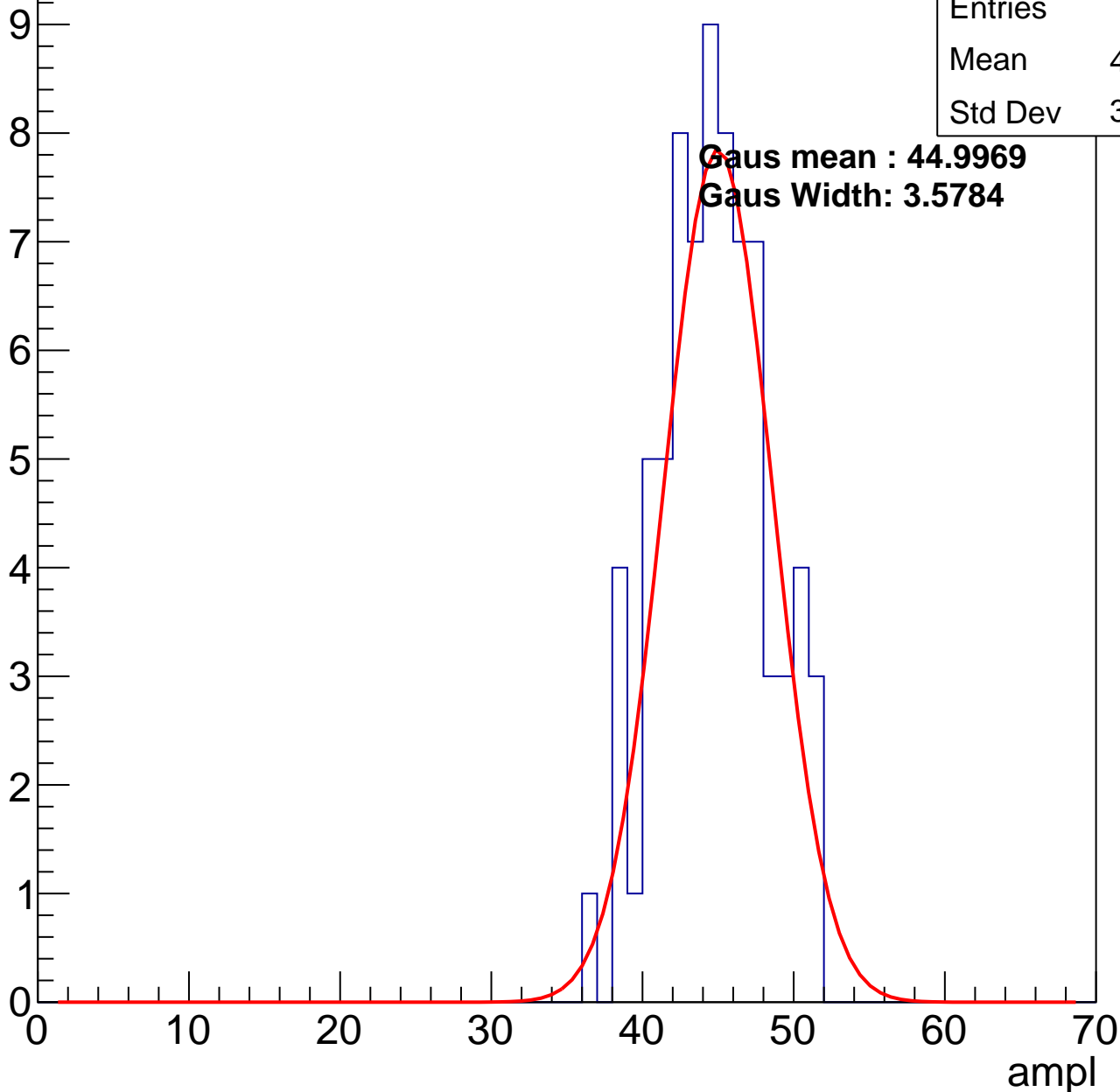
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.27
Std Dev	3.473

Gaus mean : 44.9969

Gaus Width: 3.5784

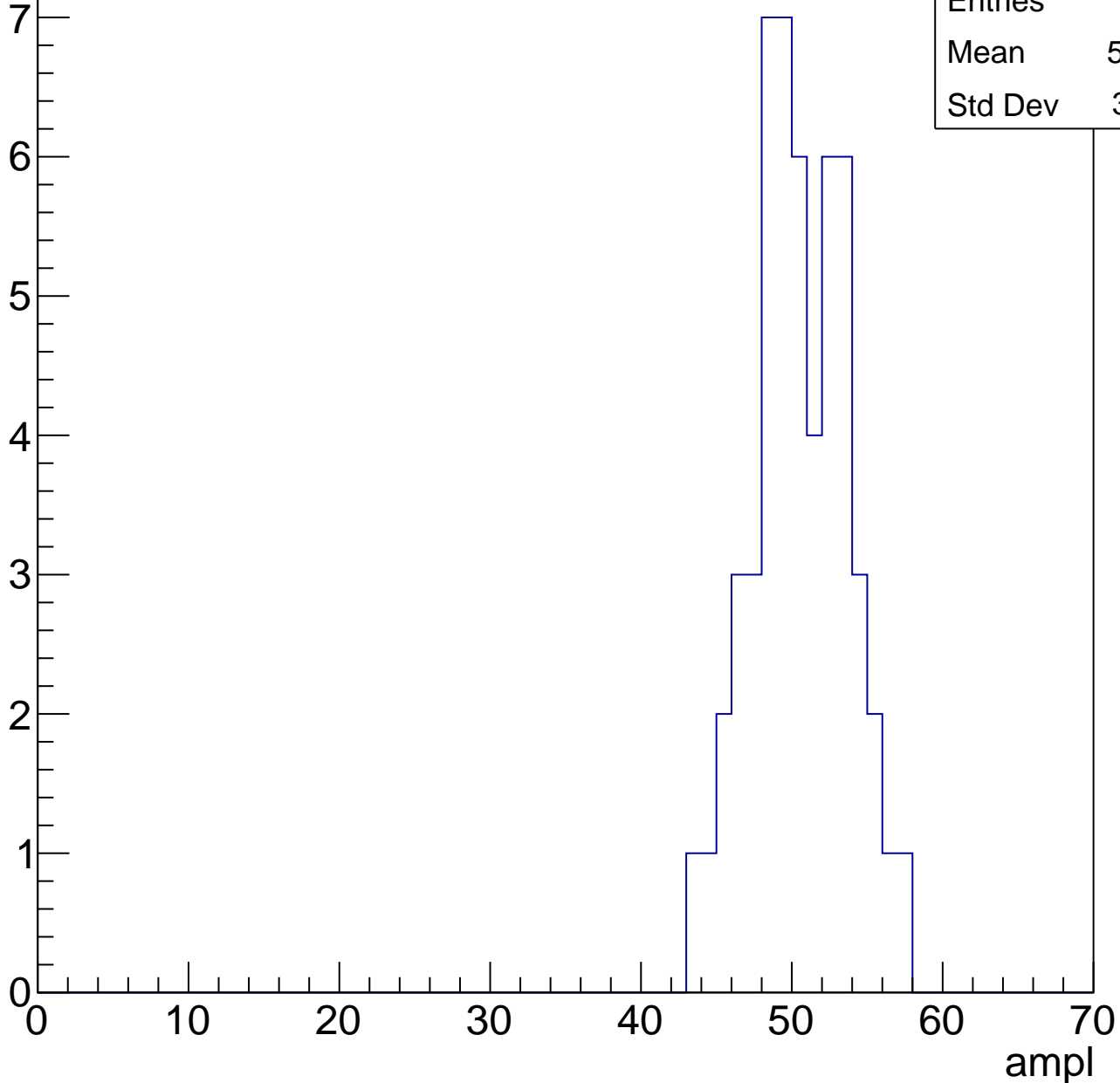


B0L002S, U12-ch54, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	50.08
Std Dev	3.101

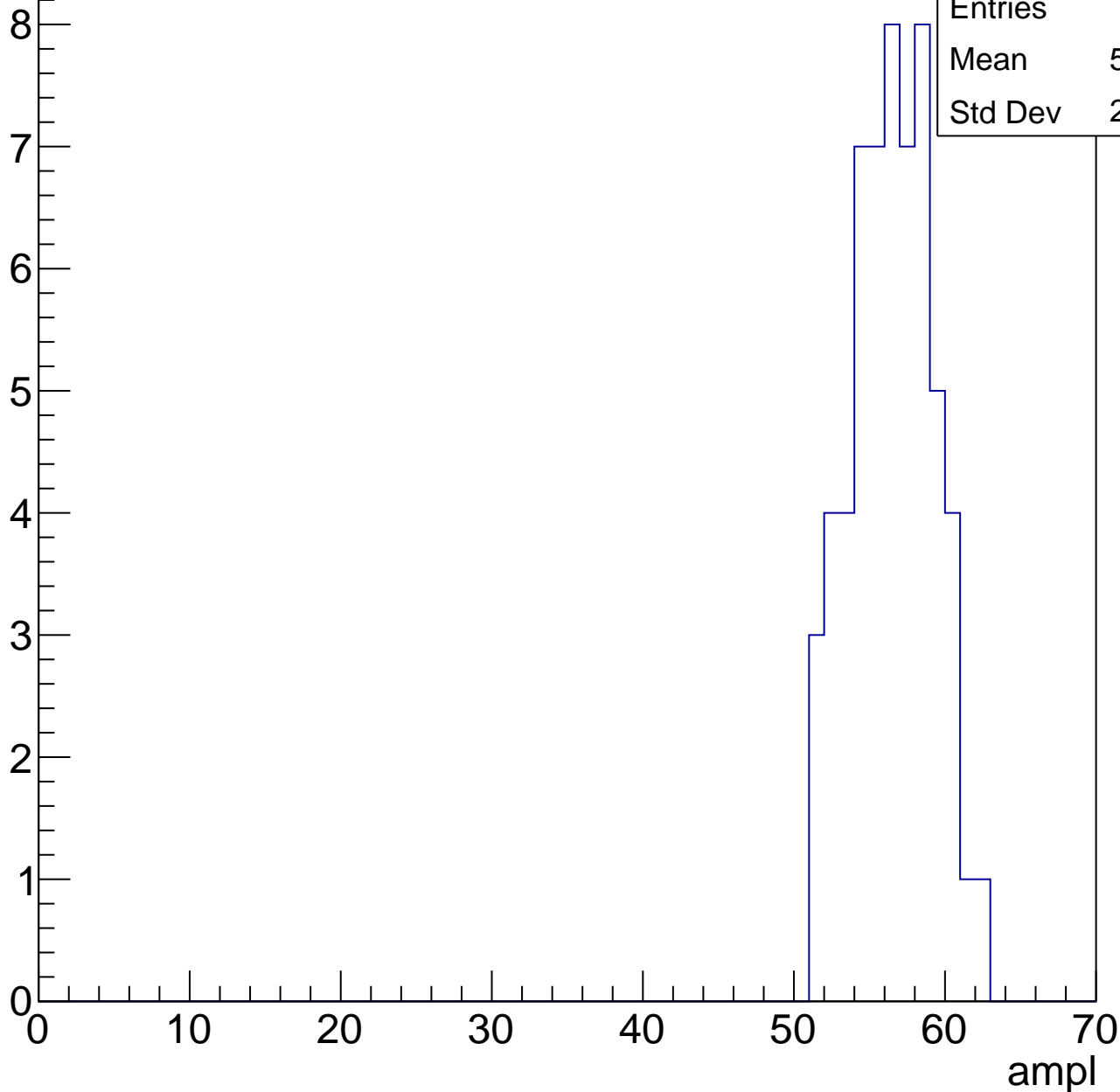


B0L002S, U12-ch54, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.02
Std Dev	2.665

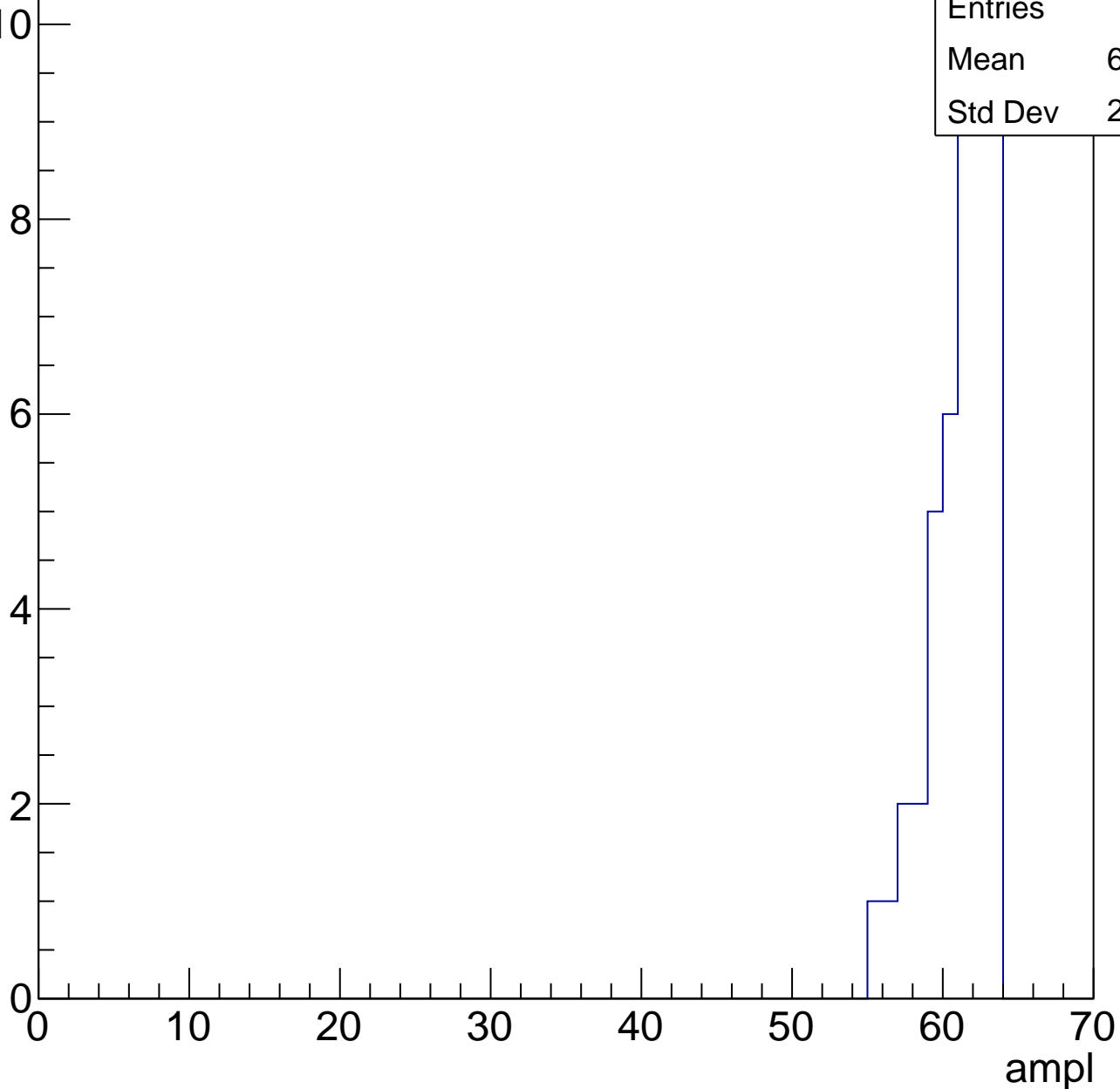


B0L002S, U12-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	60.73
Std Dev	2.015



B0L002S, U12-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch55, adc0

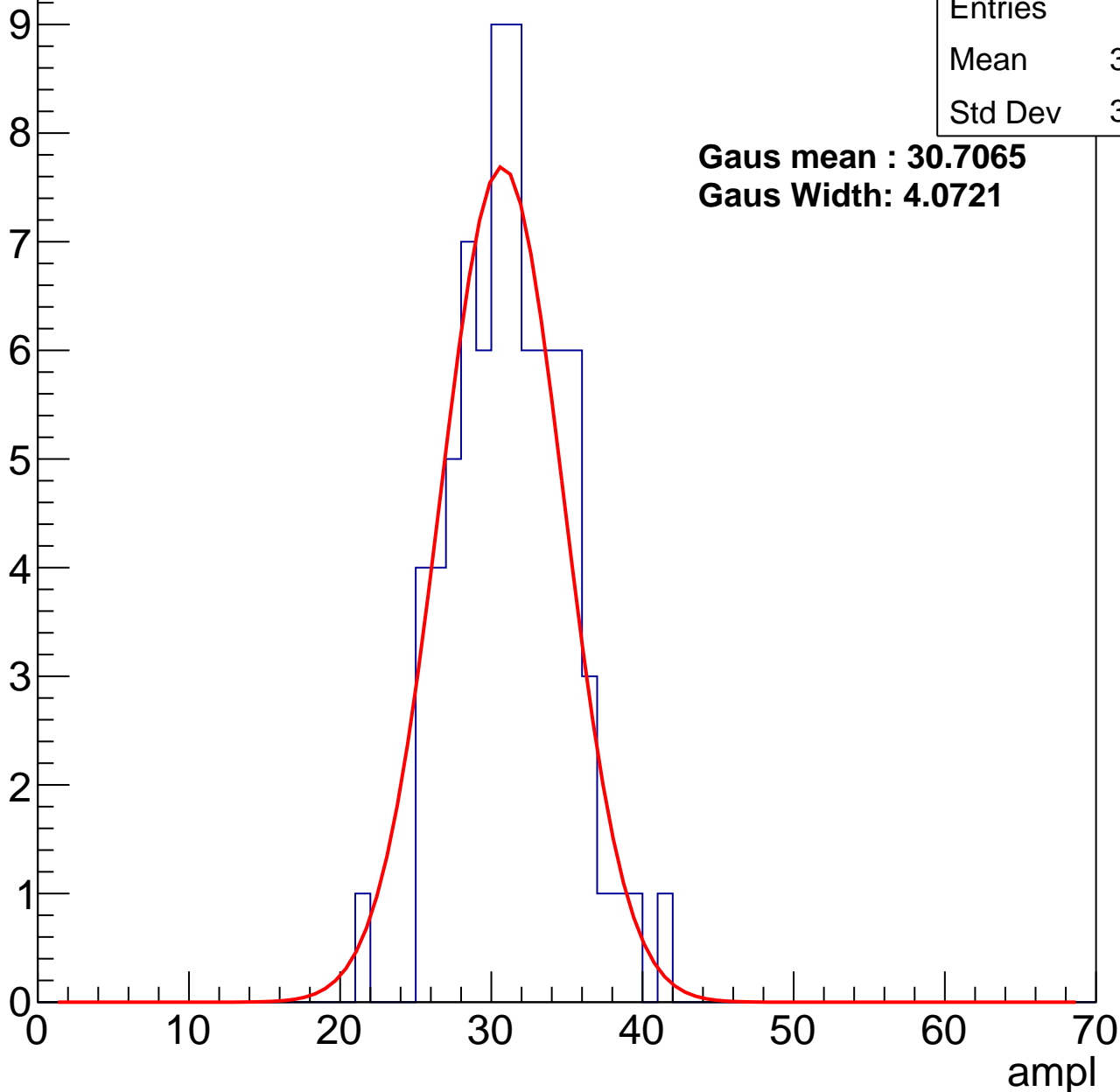
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	30.87
Std Dev	3.668

Gaus mean : 30.7065

Gaus Width: 4.0721



B0L002S, U12-ch55, adc1

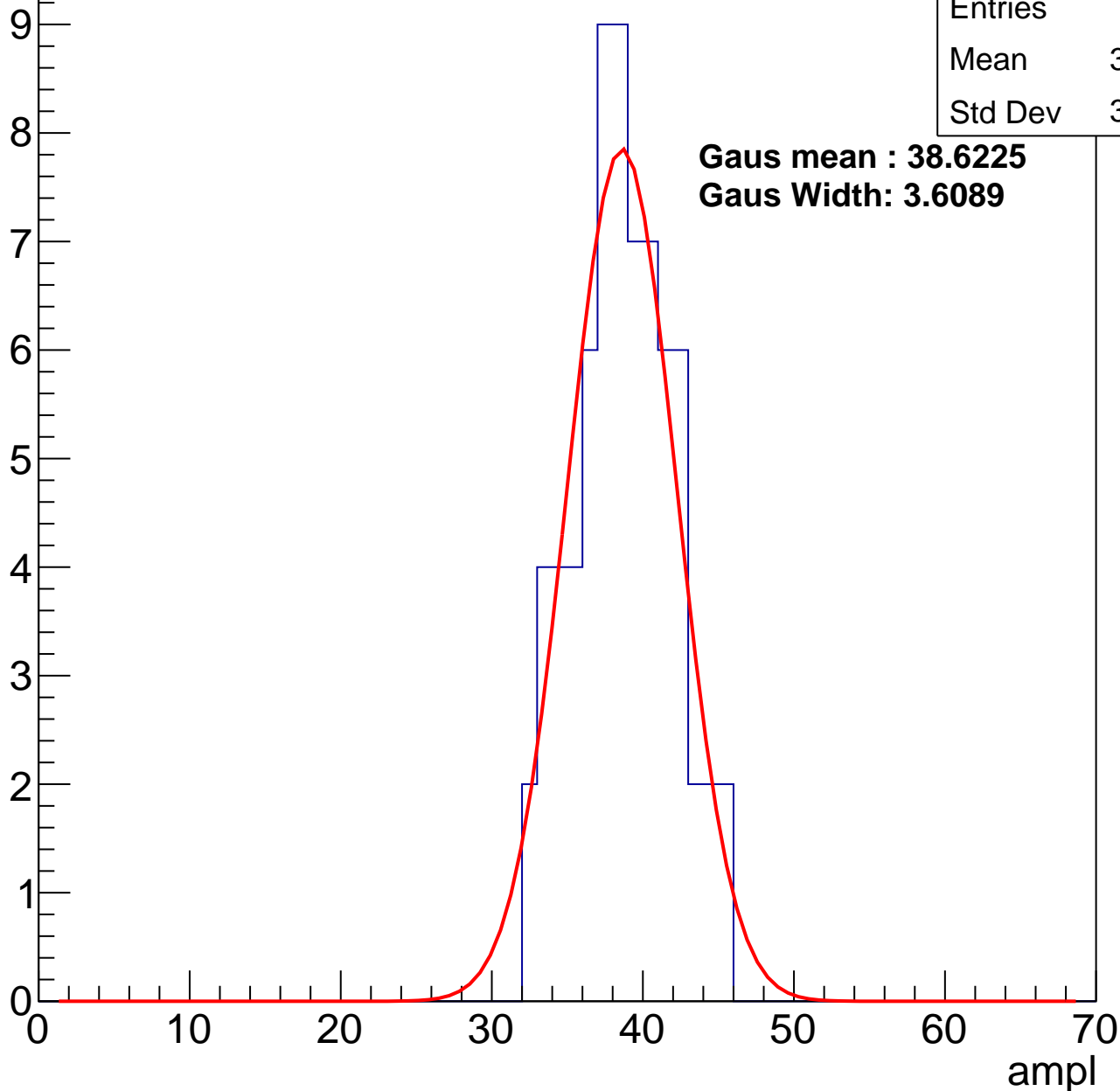
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	38.26
Std Dev	3.174

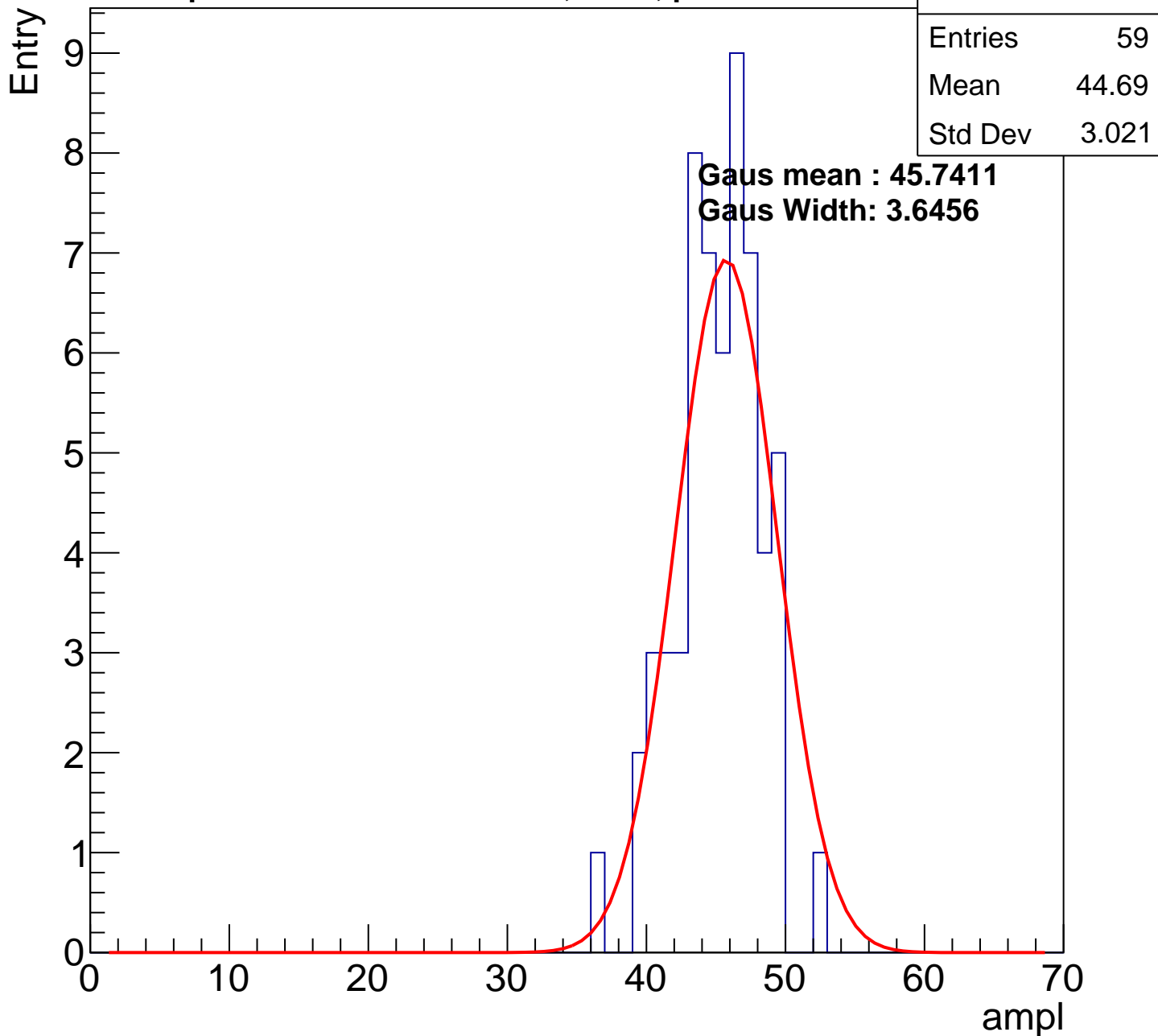
Gaus mean : 38.6225

Gaus Width: 3.6089



B0L002S, U12-ch55, adc2

calib_packv5_042523_0143.root, FC#8, port C1

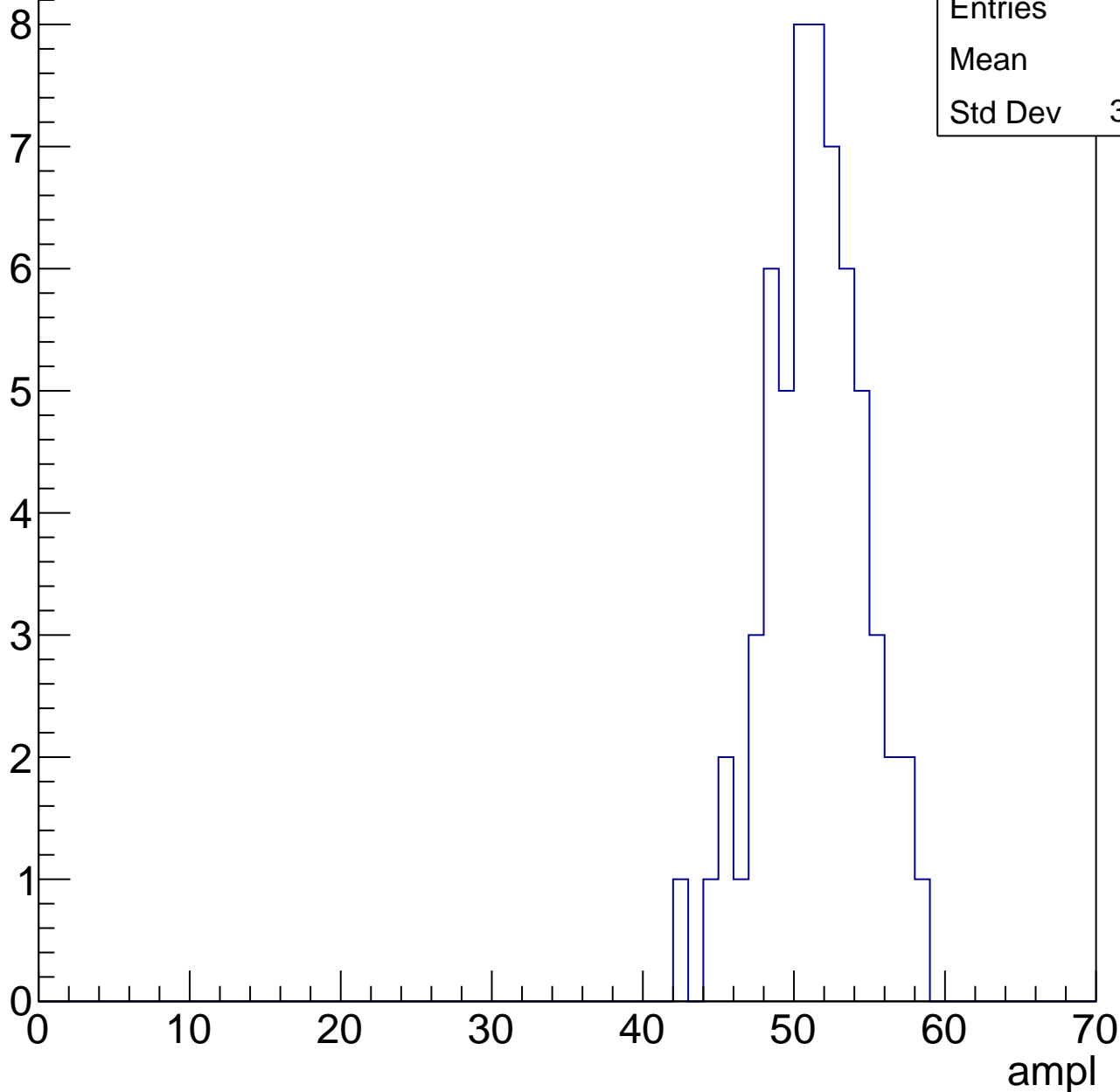


B0L002S, U12-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

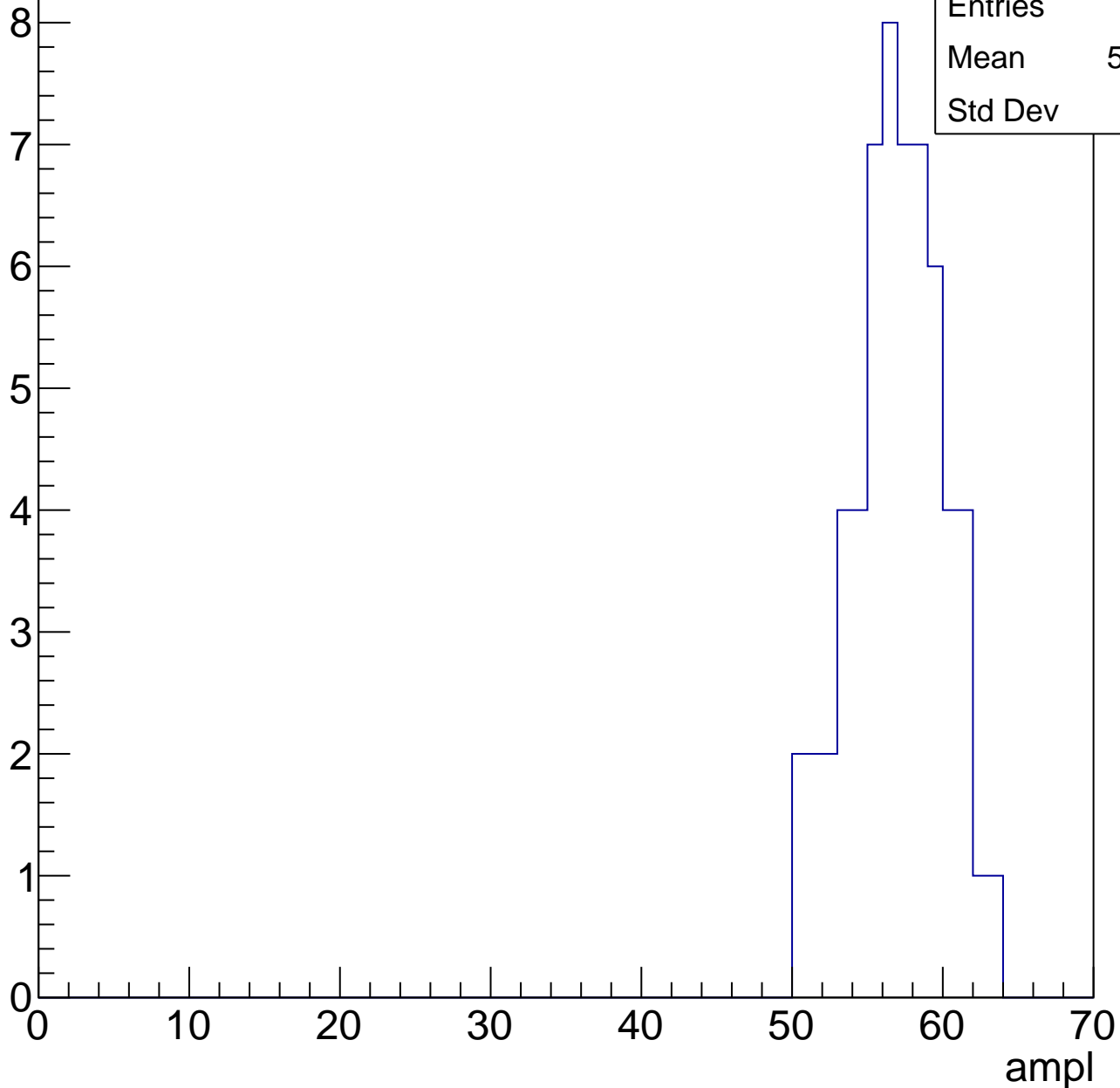
Entries	61
Mean	50.9
Std Dev	3.268



B0L002S, U12-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



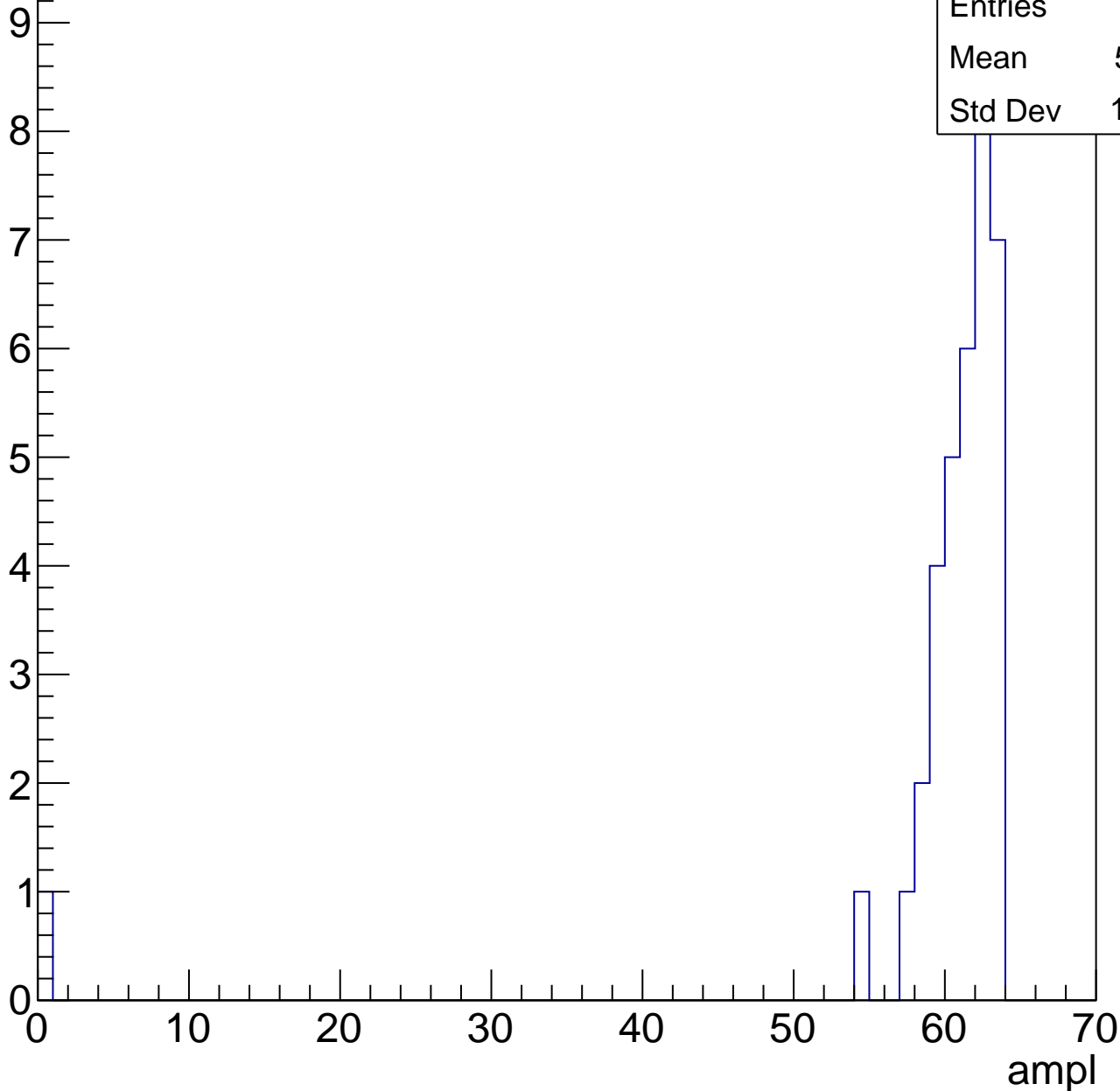
Entries	59
Mean	56.53
Std Dev	3.01

B0L002S, U12-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.11
Std Dev	10.18



B0L002S, U12-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch56, adc0

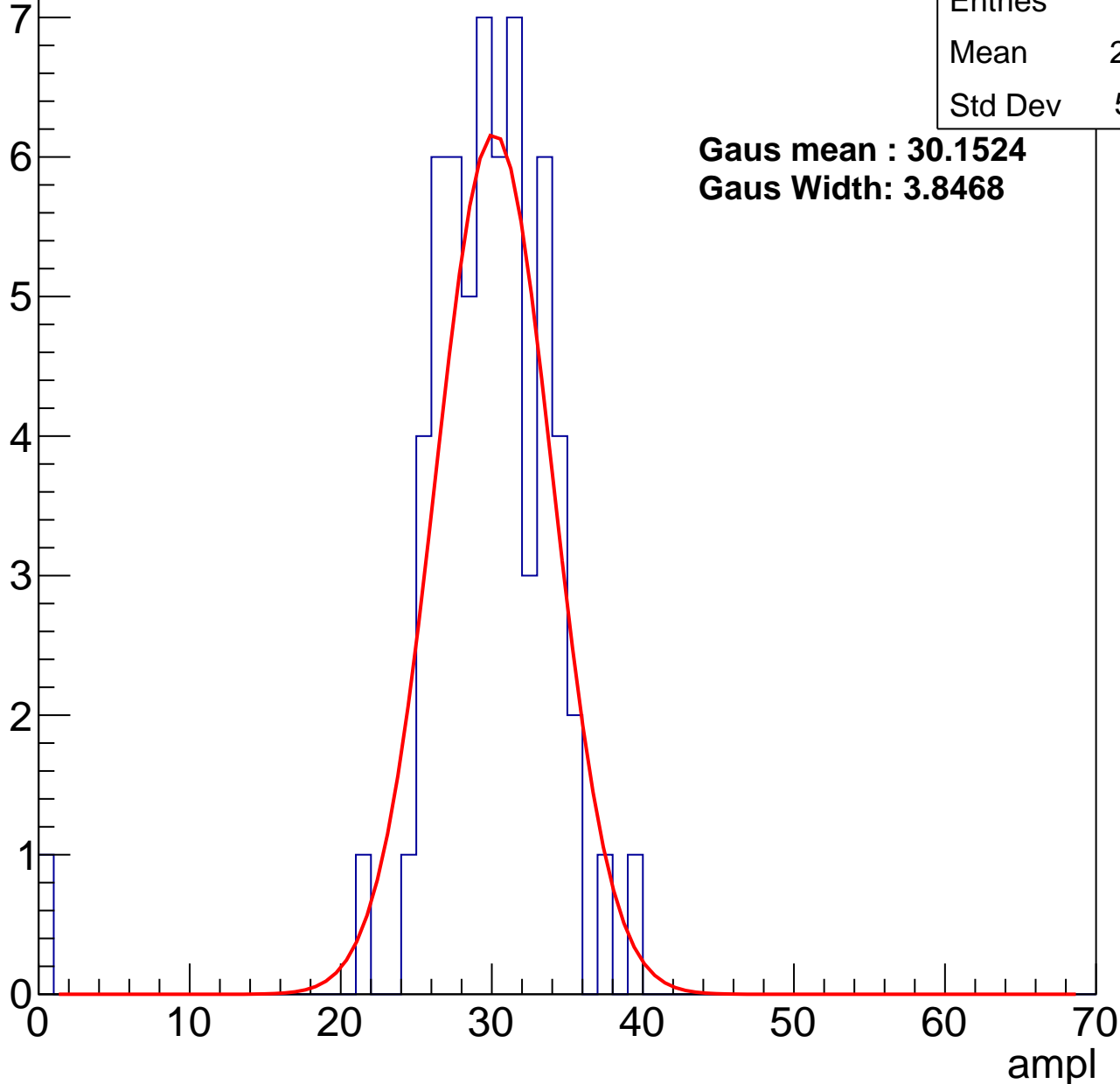
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.16
Std Dev	5.071

Gaus mean : 30.1524

Gaus Width: 3.8468



B0L002S, U12-ch56, adc1

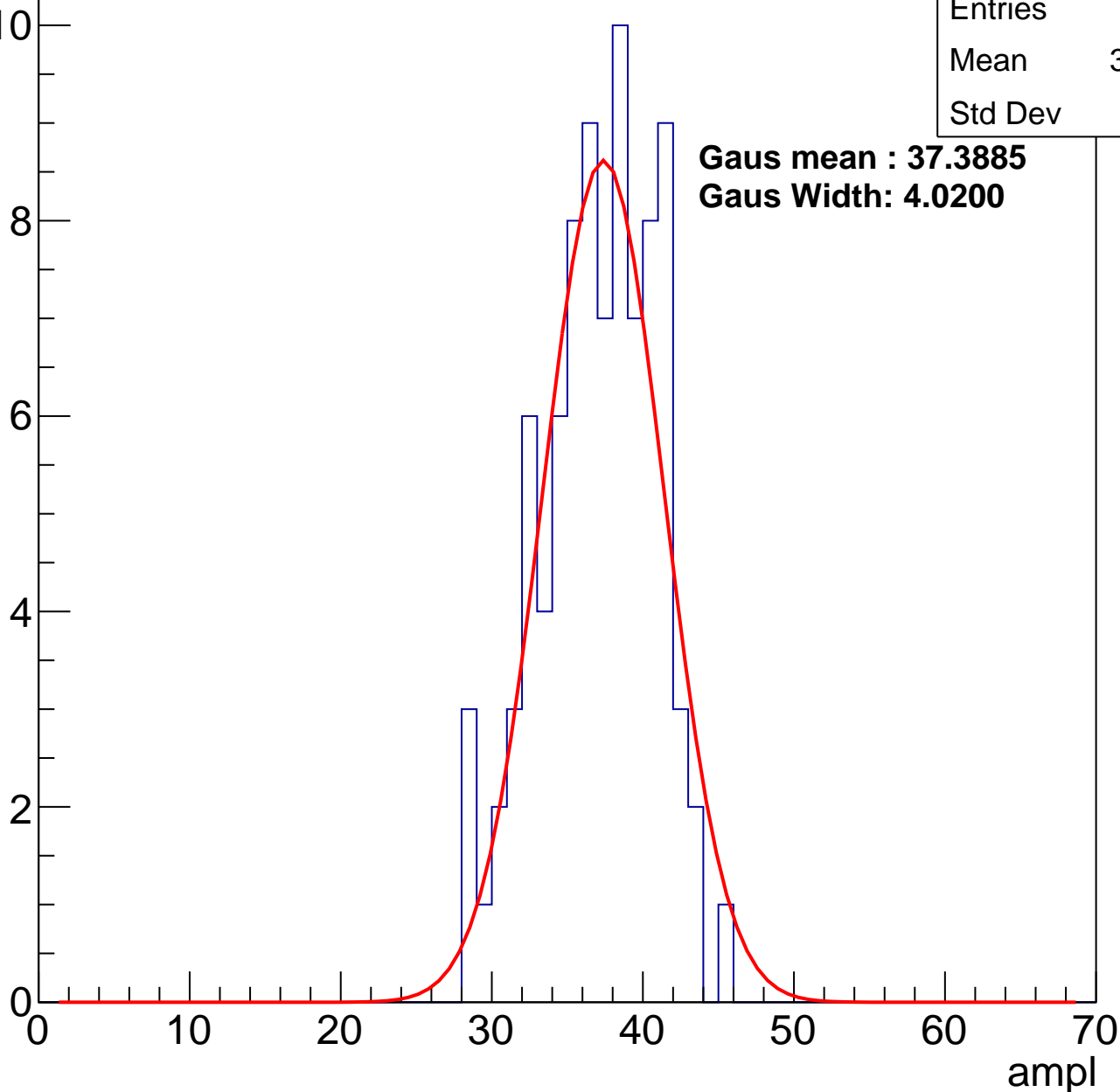
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	89
Mean	36.58
Std Dev	3.78

Gaus mean : 37.3885

Gaus Width: 4.0200



B0L002S, U12-ch56, adc2

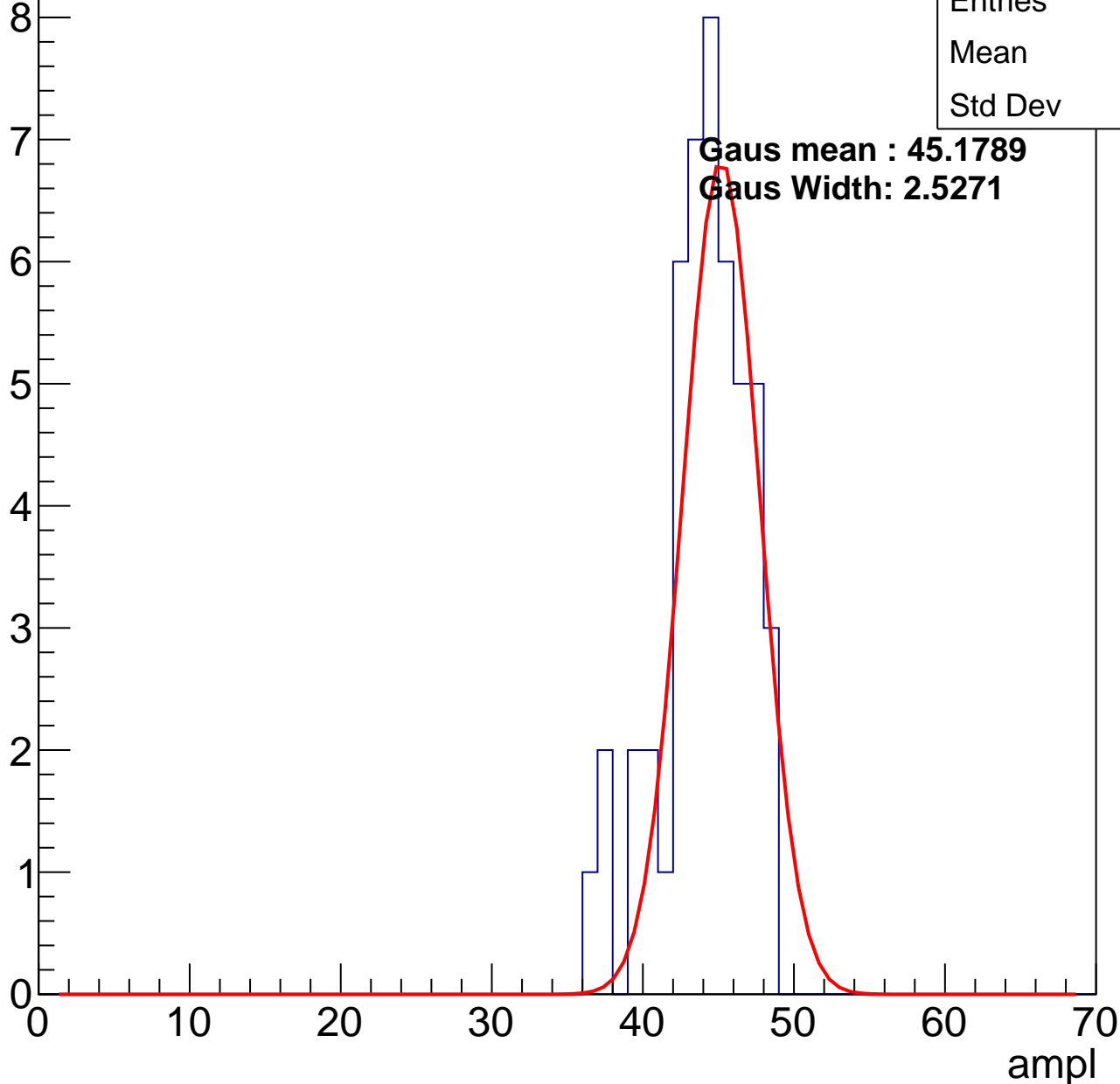
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	43.6
Std Dev	2.87

Gaus mean : 45.1789

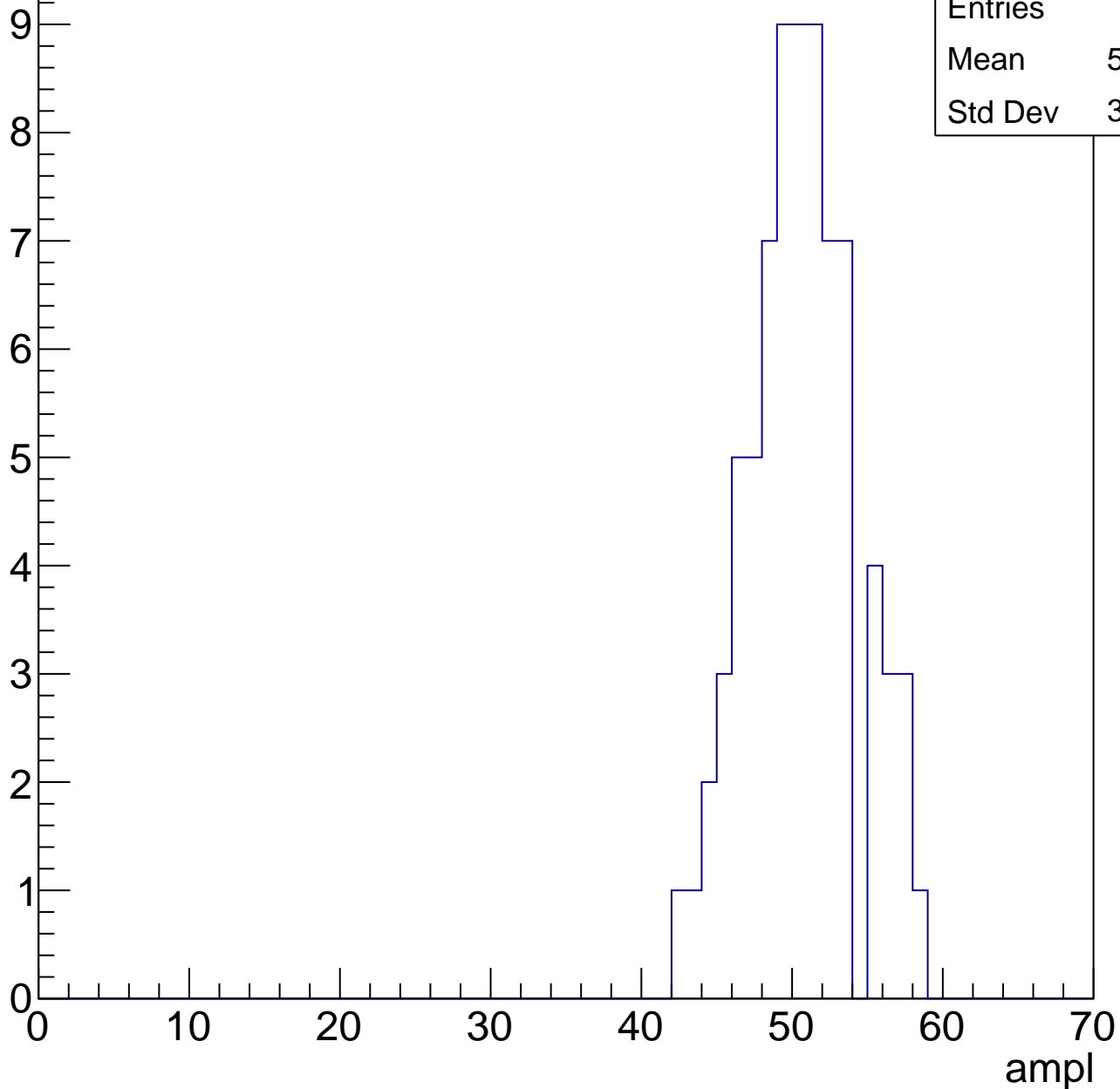
Gaus Width: 2.5271



B0L002S, U12-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	76
Mean	50.14
Std Dev	3.516

B0L002S, U12-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

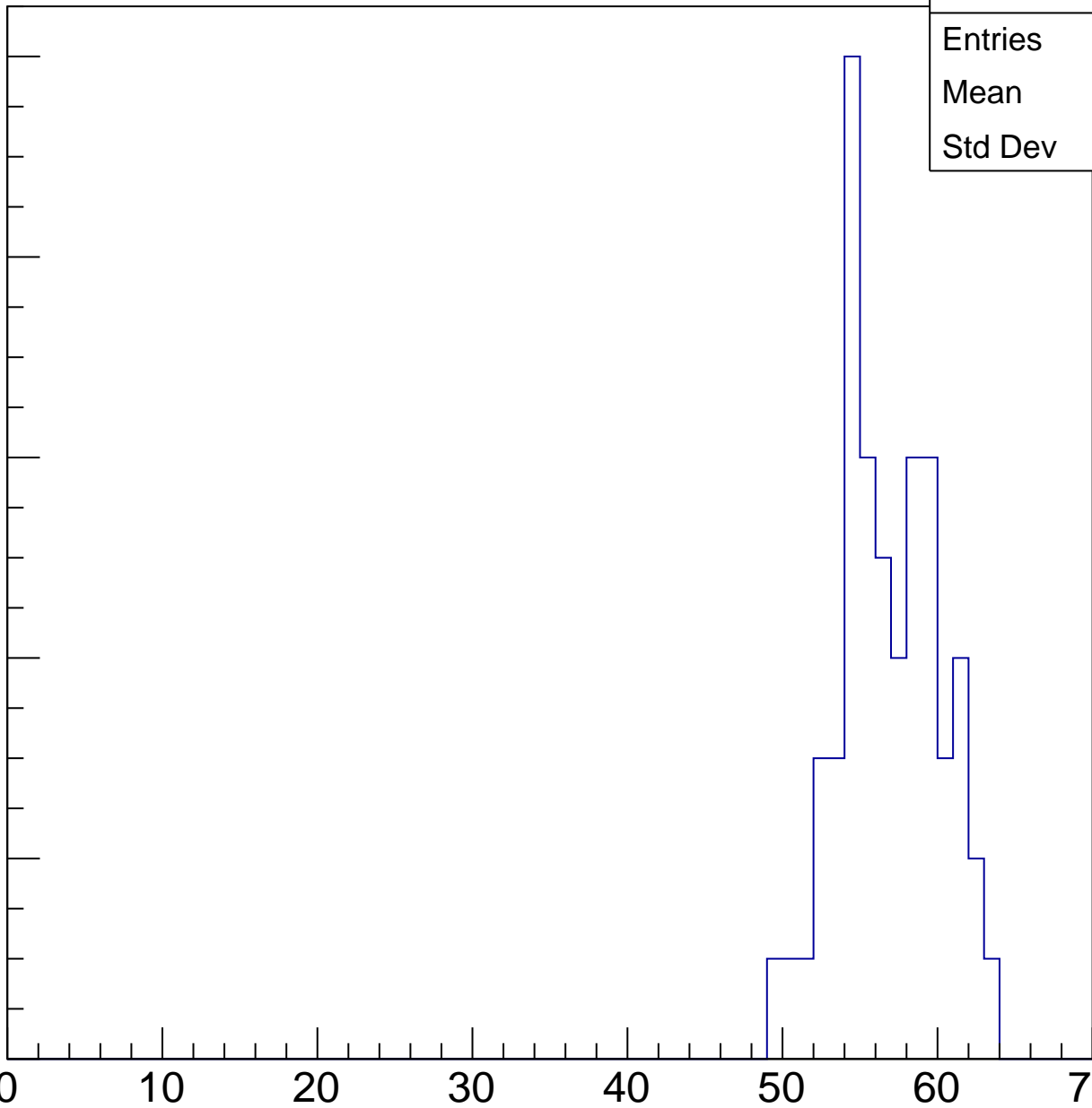
Entries	56
Mean	56.36
Std Dev	3.204

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

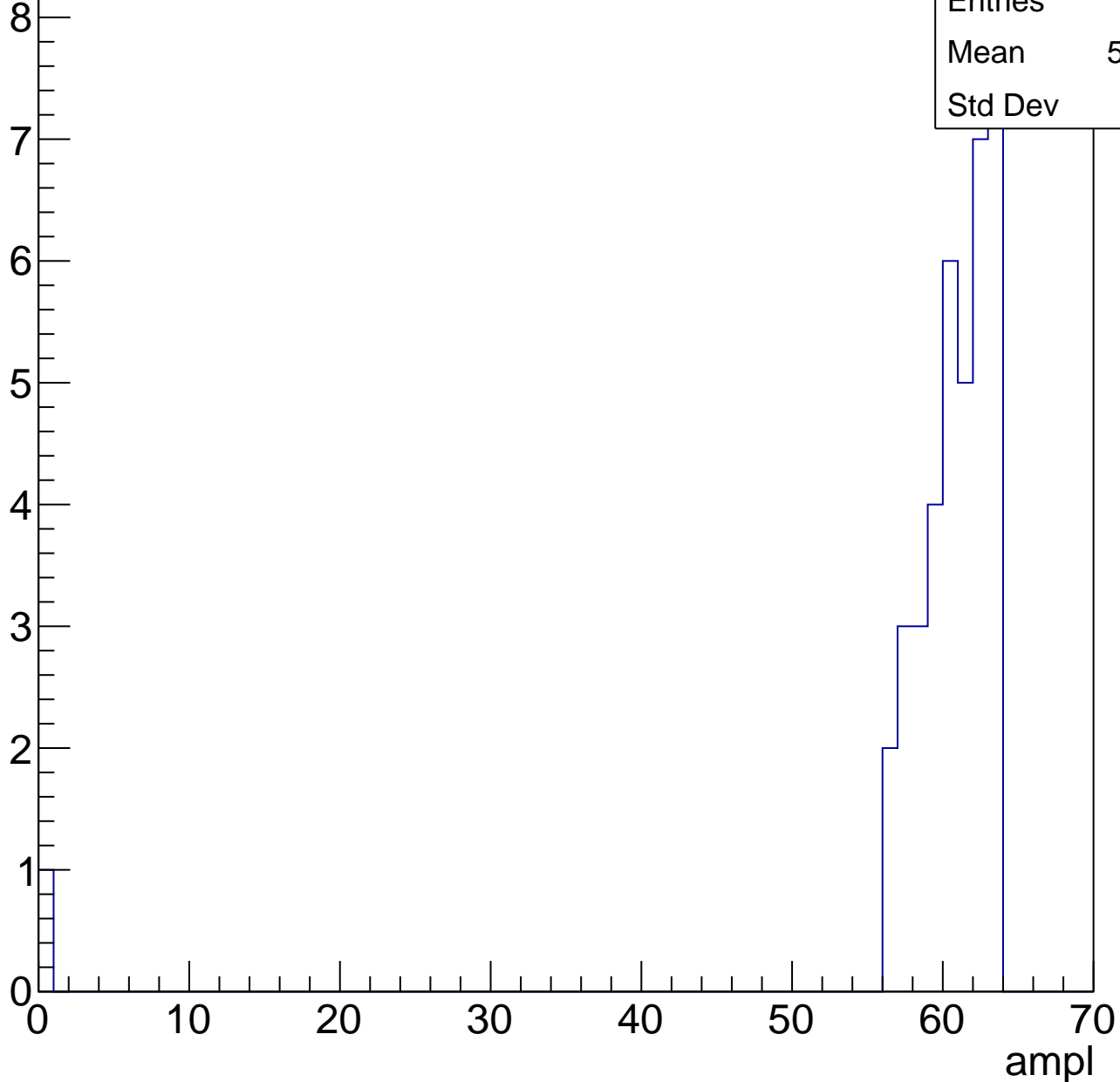
ampl



B0L002S, U12-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch57, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	28.57
Std Dev	5.874

Gaus mean : 29.4553

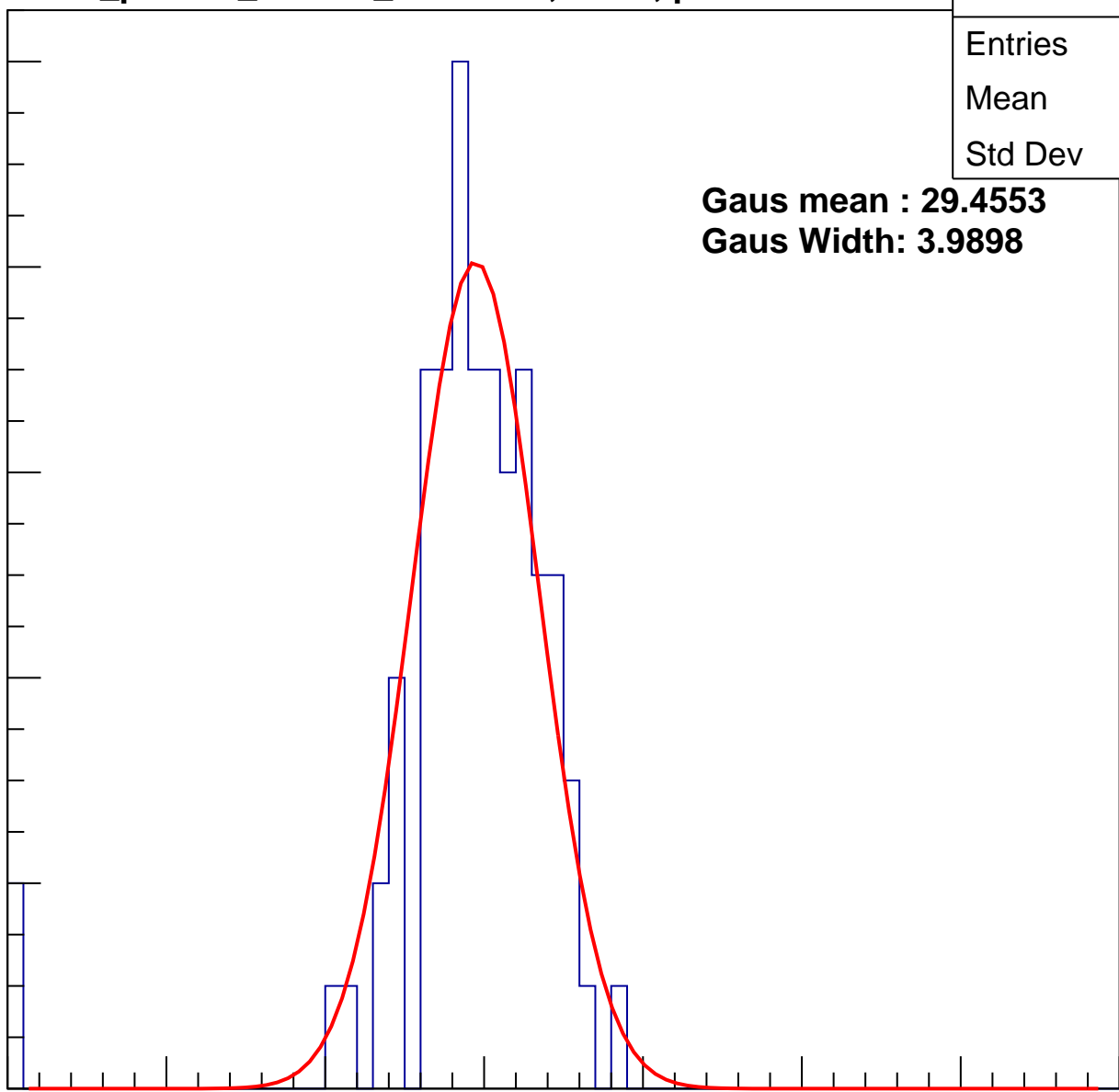
Gaus Width: 3.9898

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

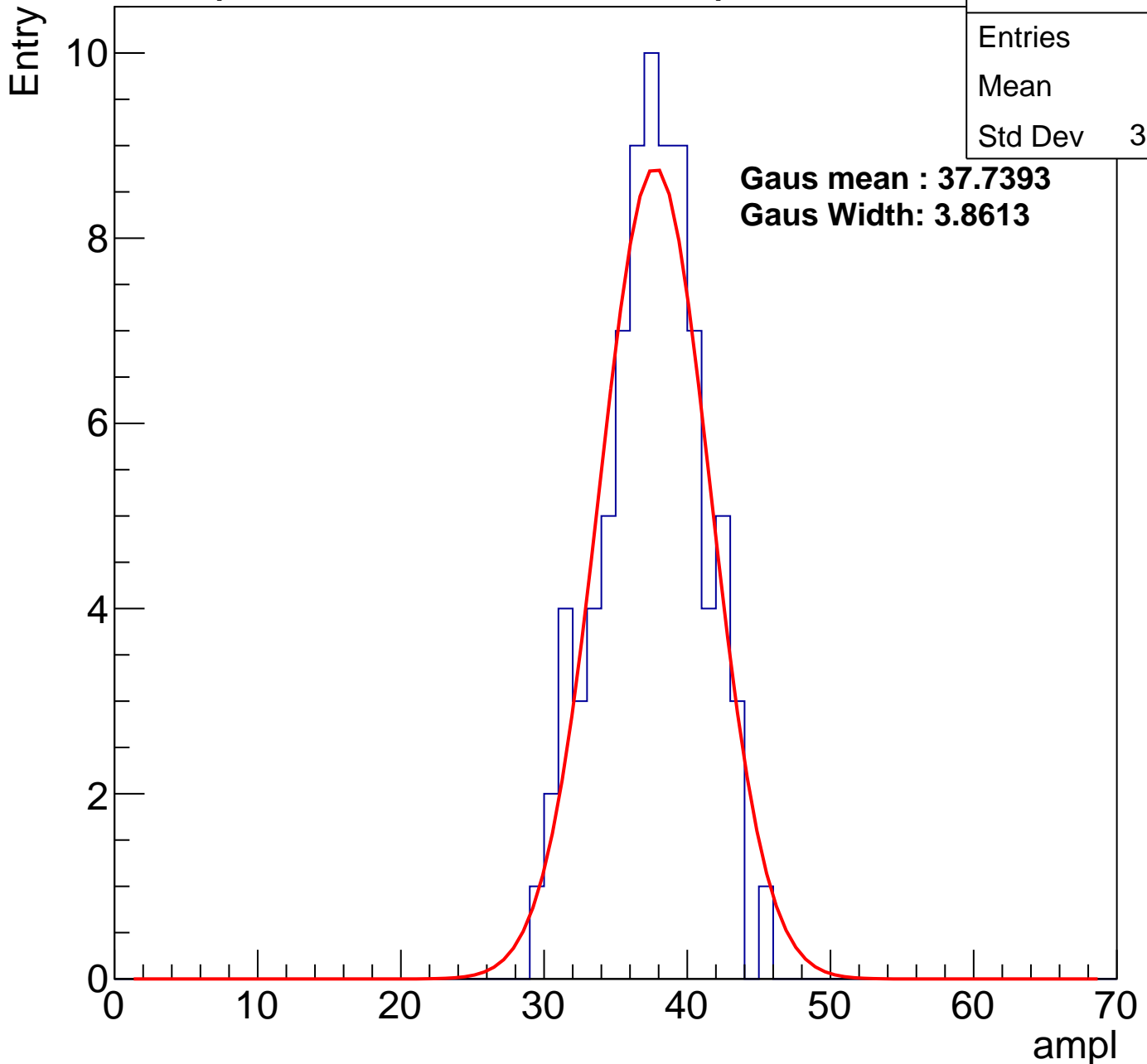


B0L002S, U12-ch57, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	37
Std Dev	3.464

Gaus mean : 37.7393
Gaus Width: 3.8613



B0L002S, U12-ch57, adc2

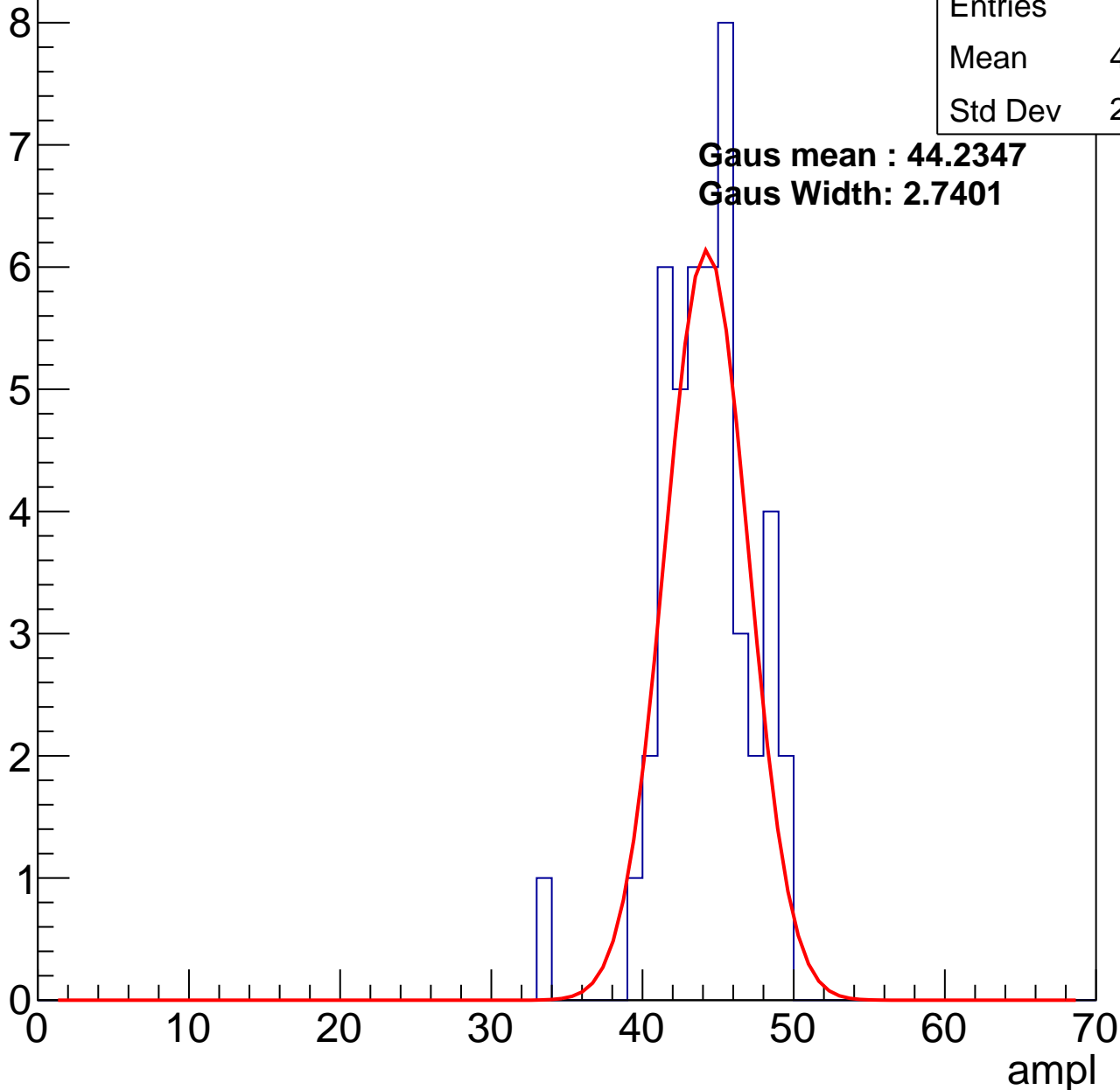
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	43.74
Std Dev	2.974

Gaus mean : 44.2347

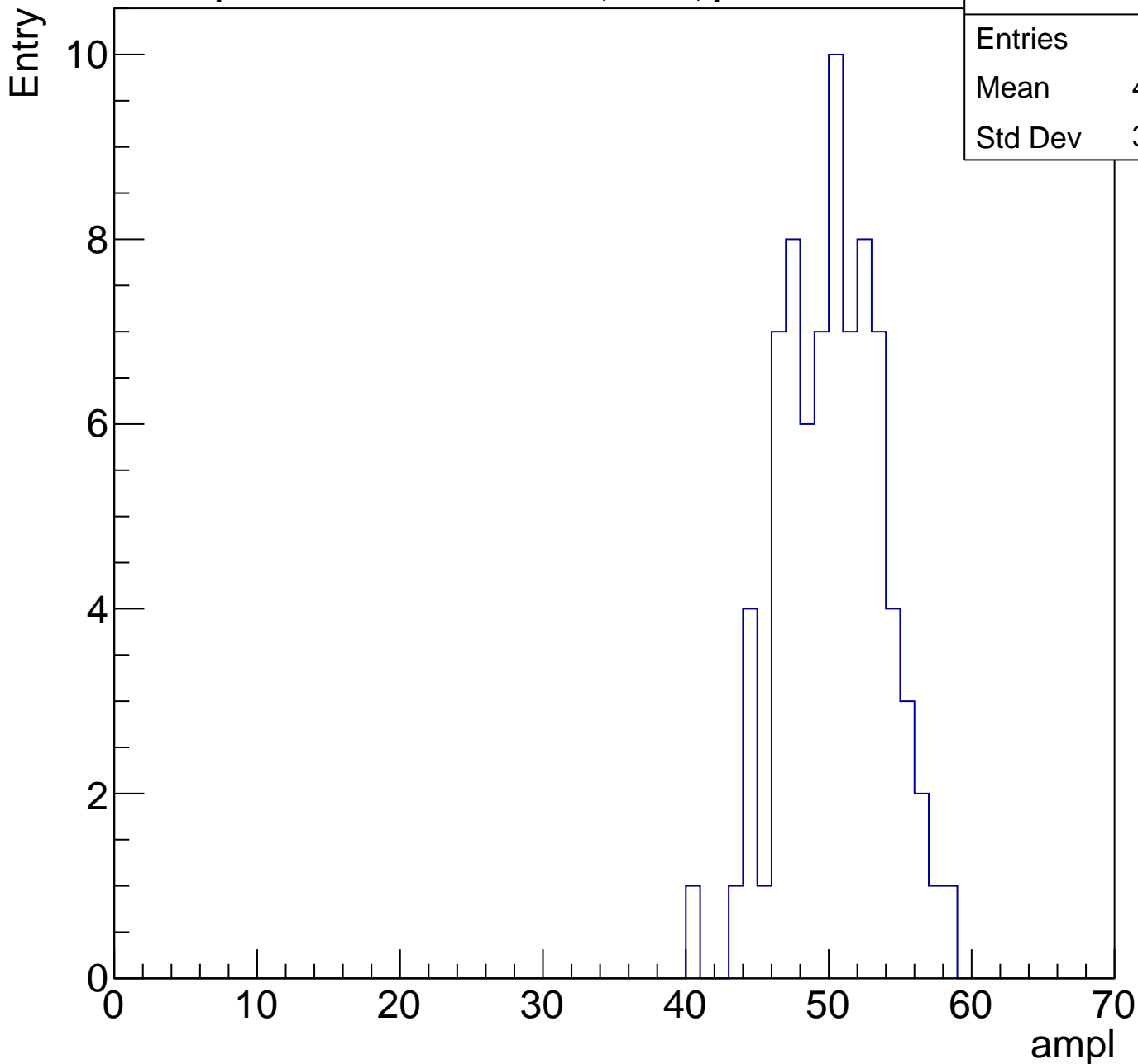
Gaus Width: 2.7401



B0L002S, U12-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	49.81
Std Dev	3.501

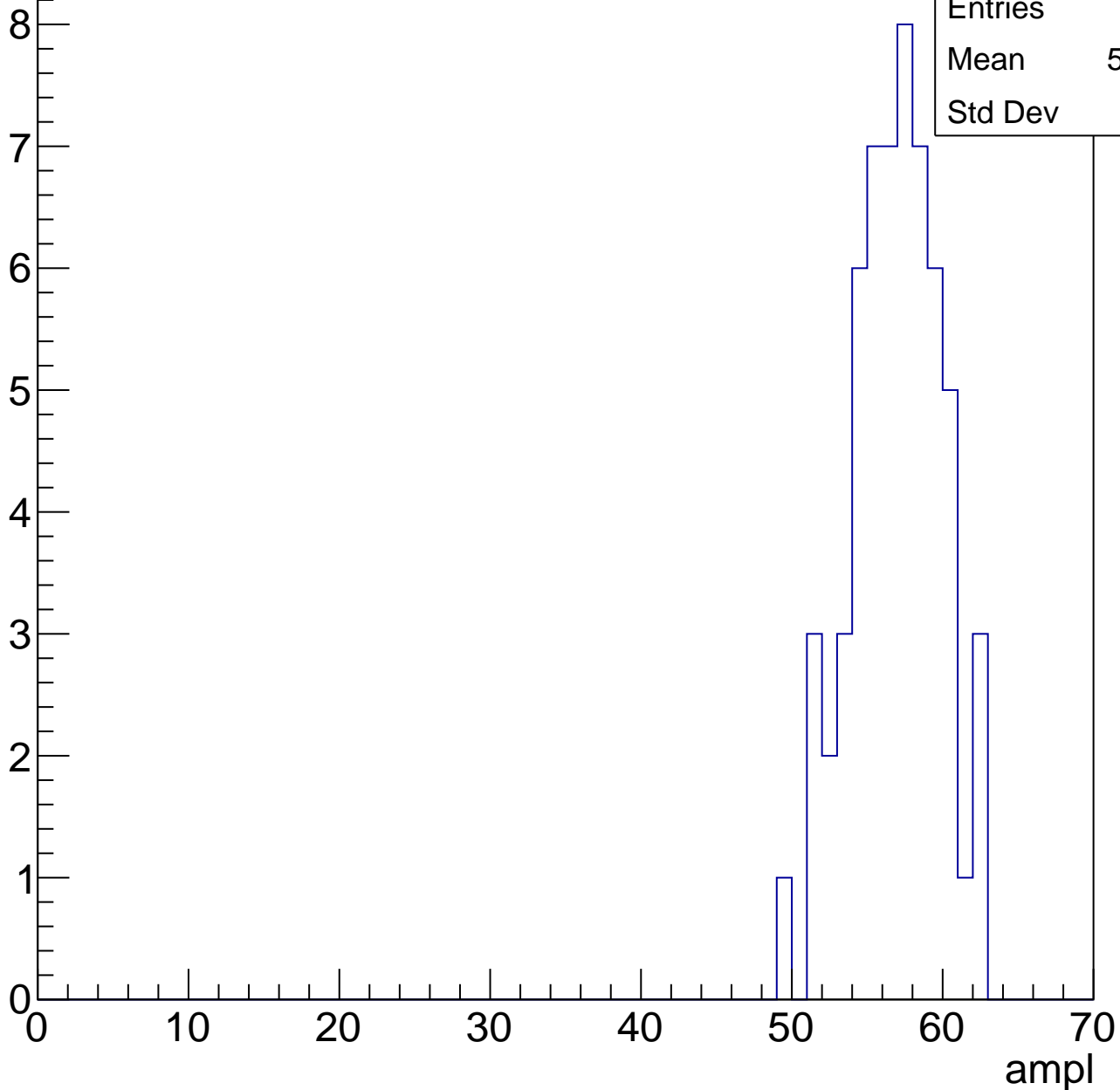


B0L002S, U12-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.42
Std Dev	2.93

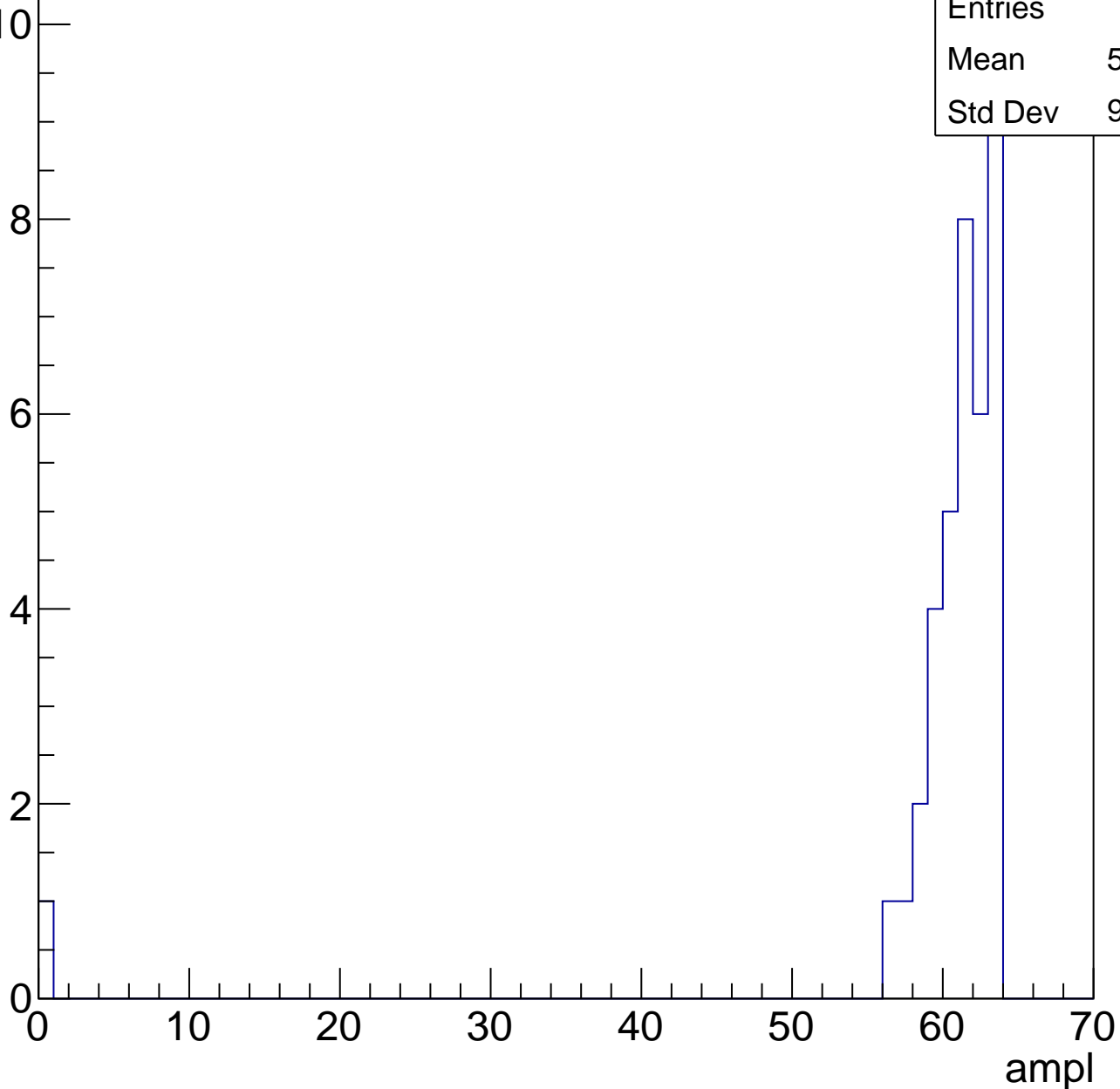


B0L002S, U12-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.34
Std Dev	9.924



B0L002S, U12-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch58, adc0

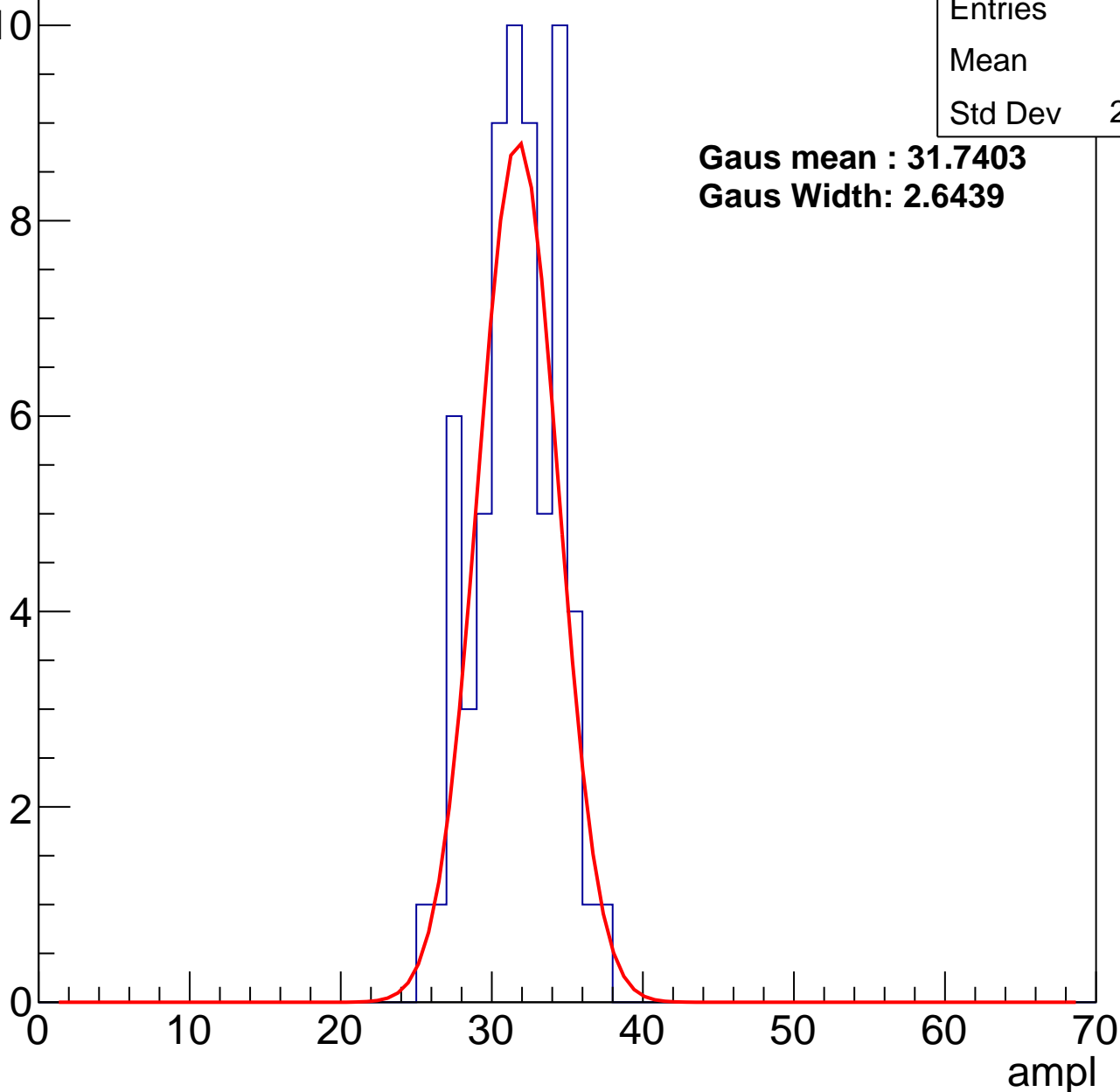
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	31.2
Std Dev	2.644

Gaus mean : 31.7403

Gaus Width: 2.6439



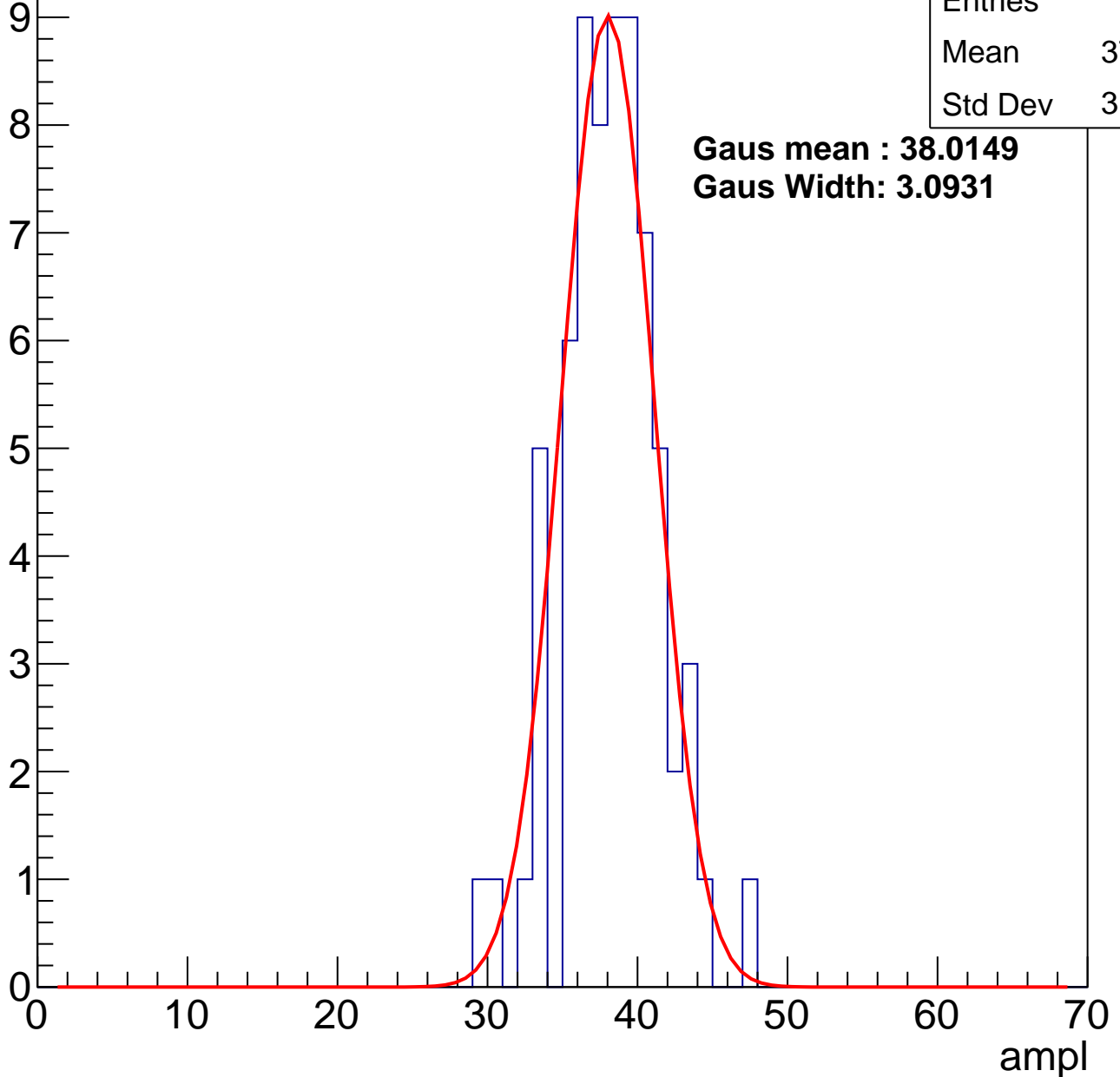
B0L002S, U12-ch58, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.76
Std Dev	3.223

Gaus mean : 38.0149
Gaus Width: 3.0931



B0L002S, U12-ch58, adc2

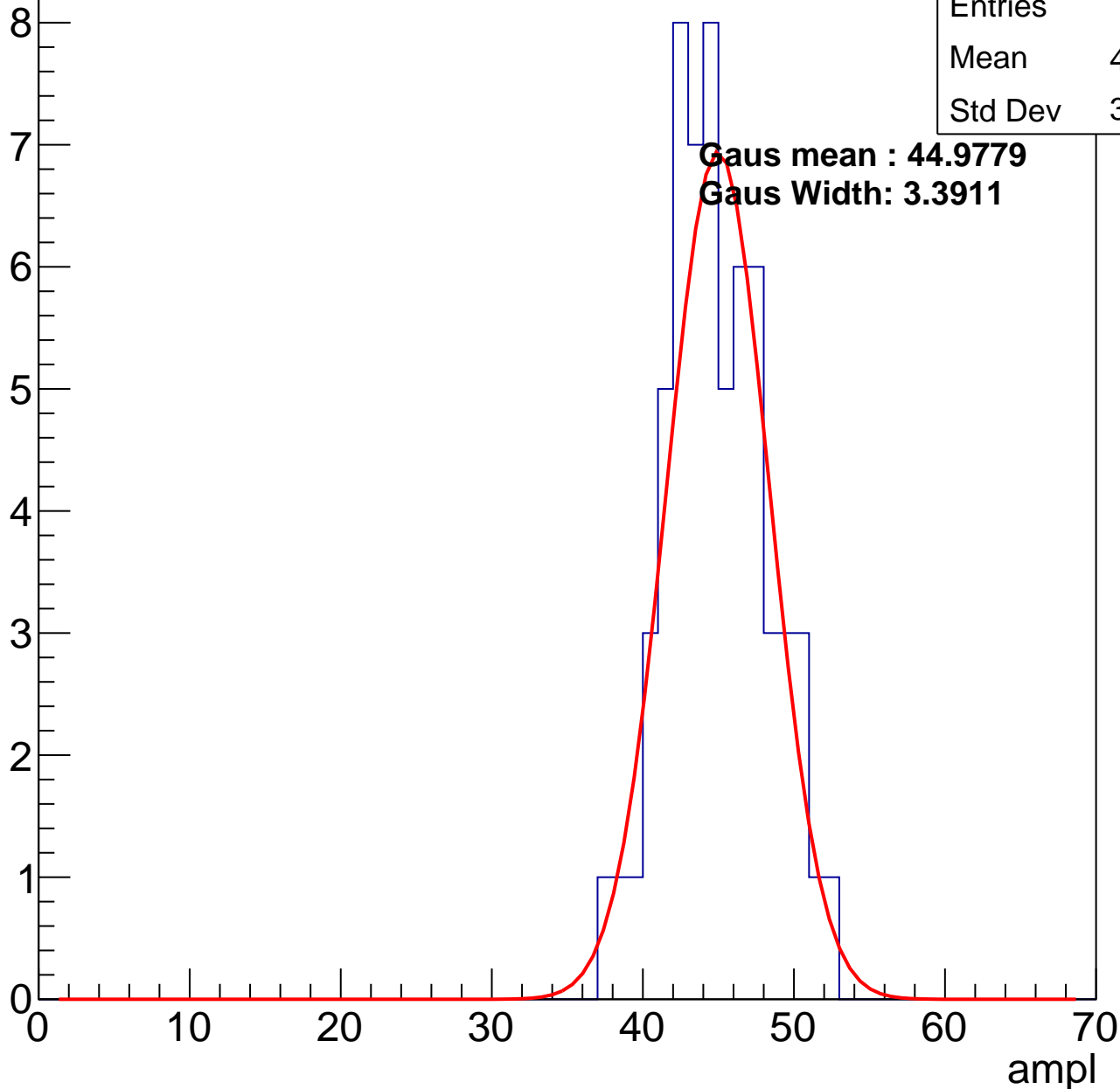
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.44
Std Dev	3.256

Gaus mean : 44.9779

Gaus Width: 3.3911

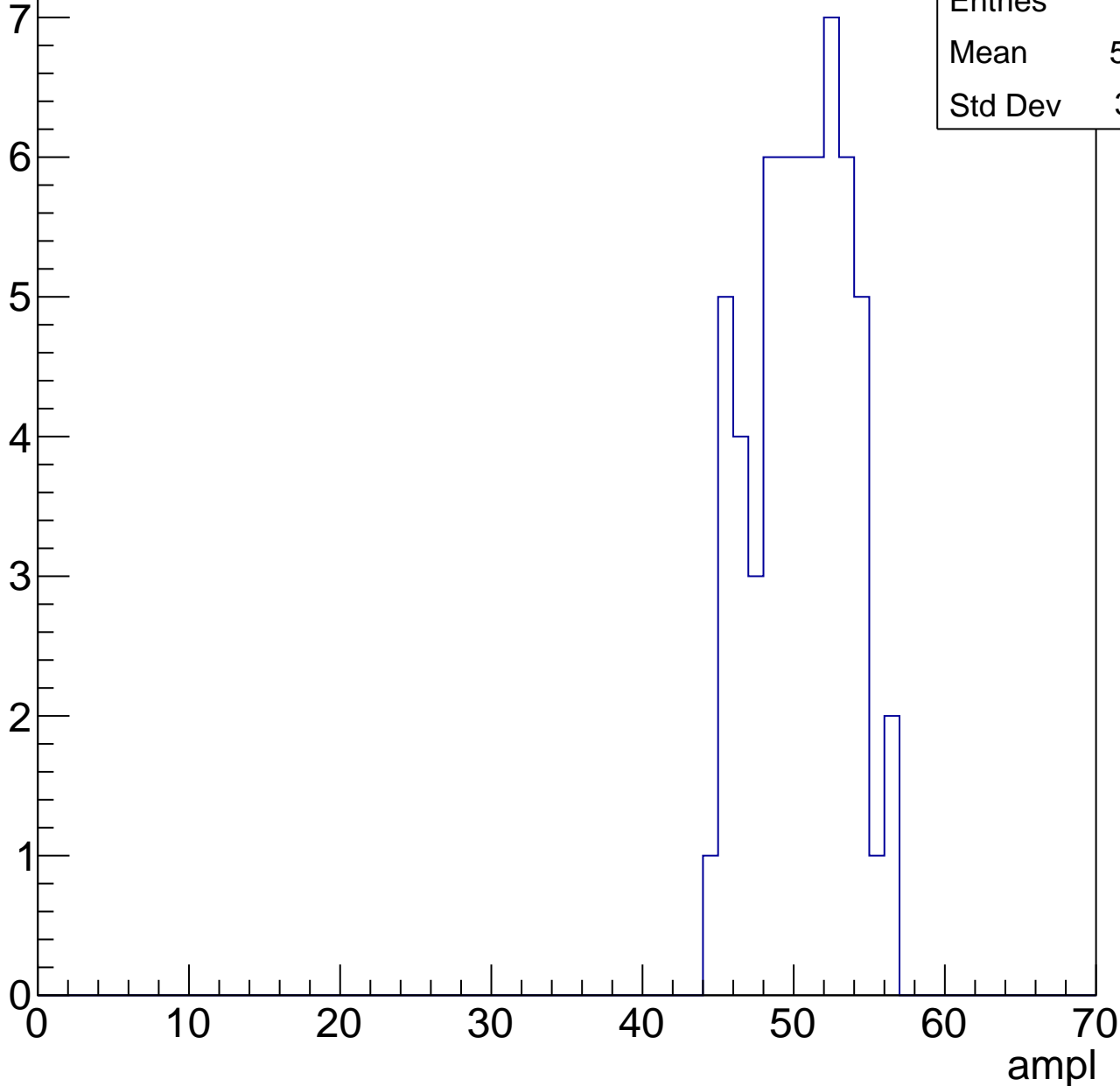


B0L002S, U12-ch58, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	50.02
Std Dev	3.071



B0L002S, U12-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	56.35
Std Dev	3.106

Entry

10

8

6

4

2

0

0

10

20

30

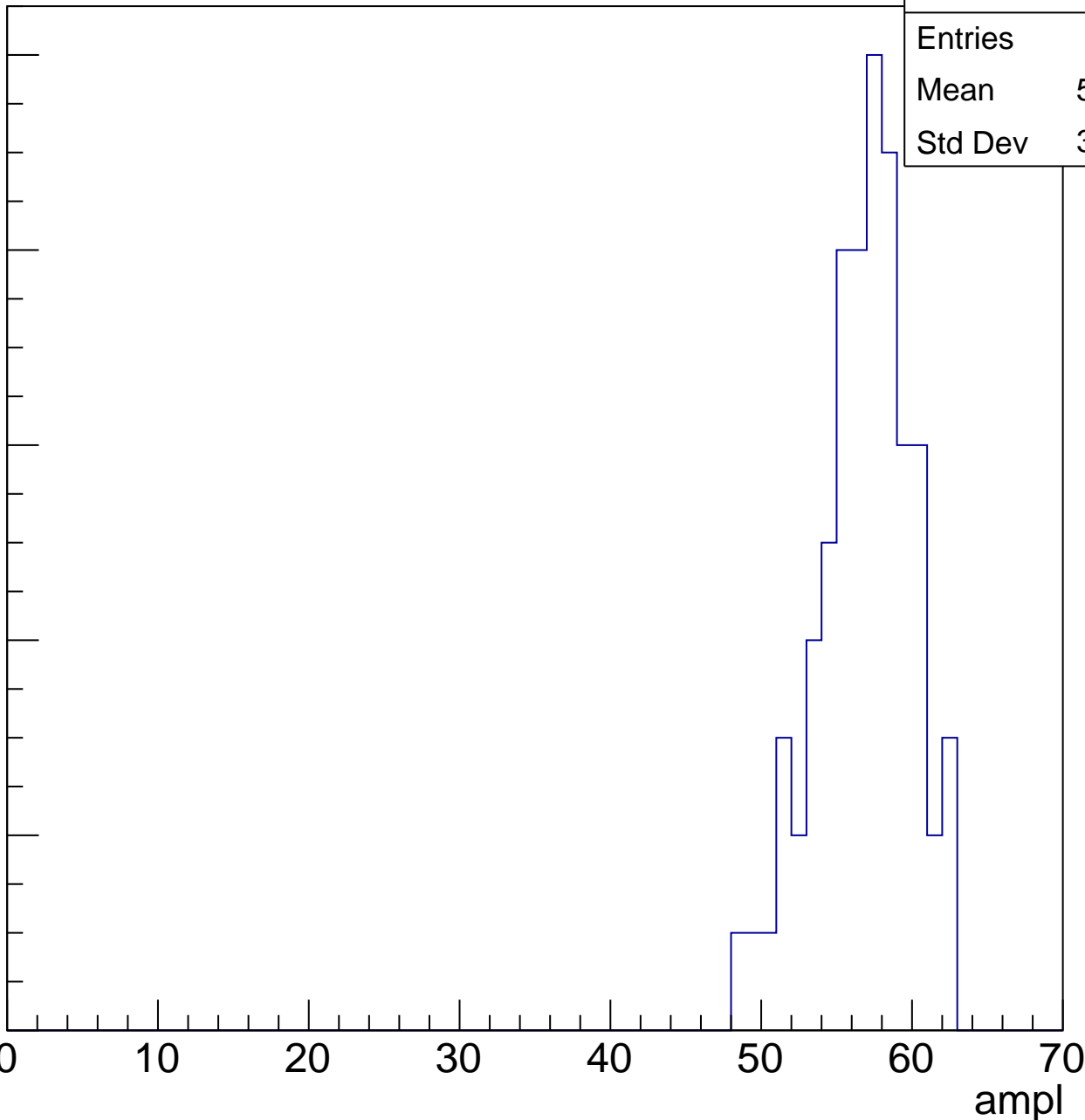
40

50

60

70

ampl

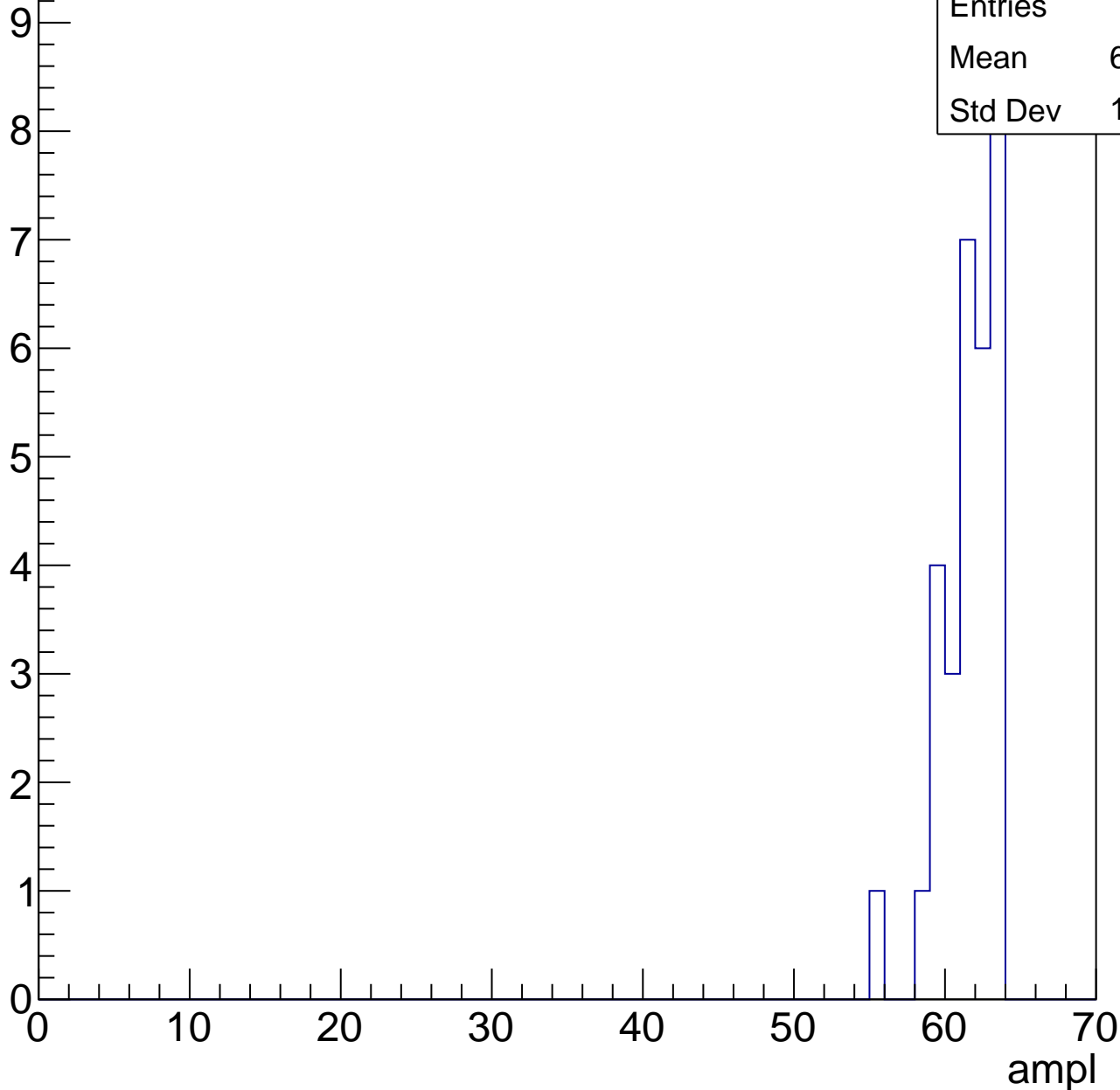


B0L002S, U12-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

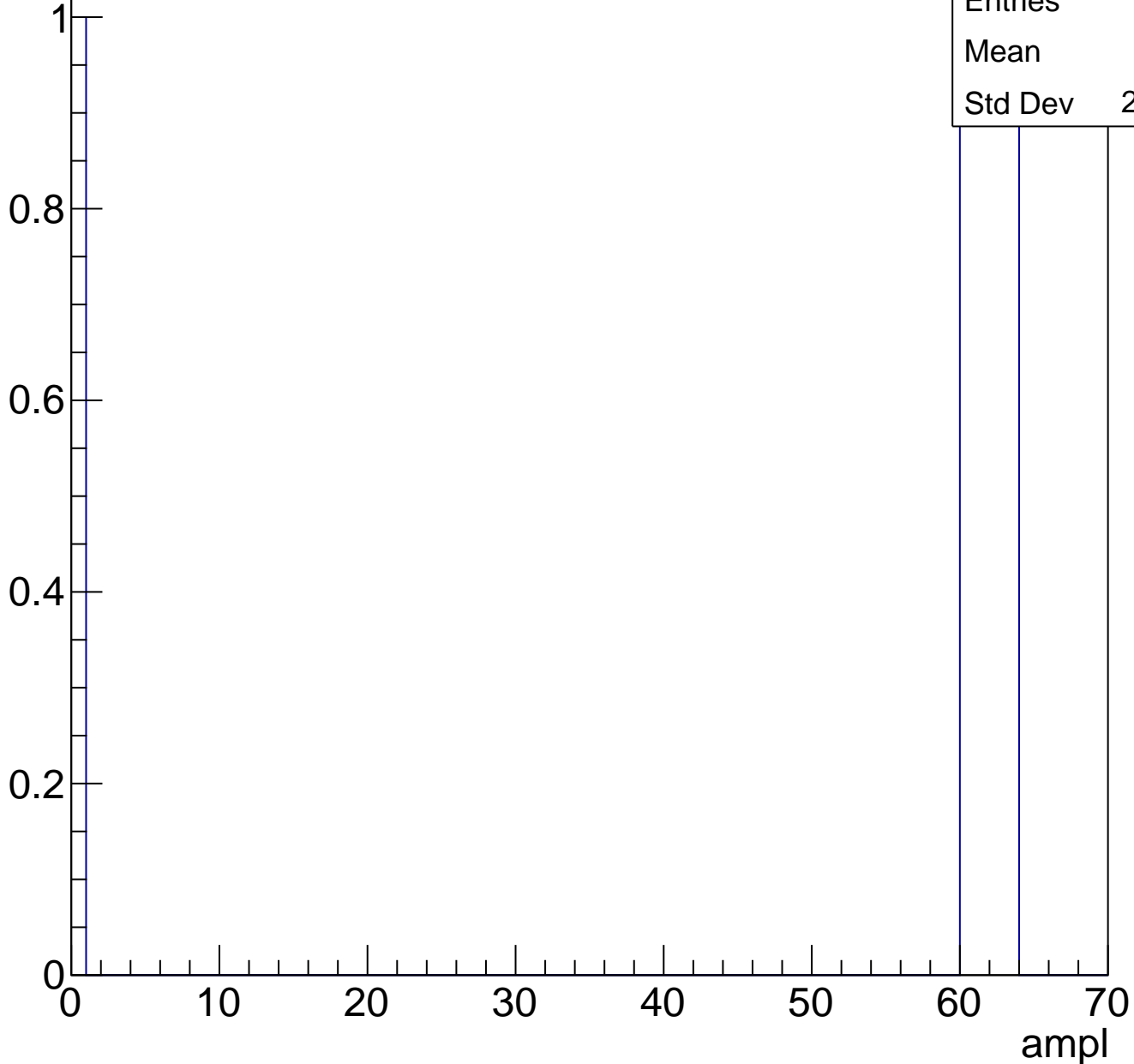
Entries	31
Mean	61.13
Std Dev	1.845



B0L002S, U12-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch59, adc0

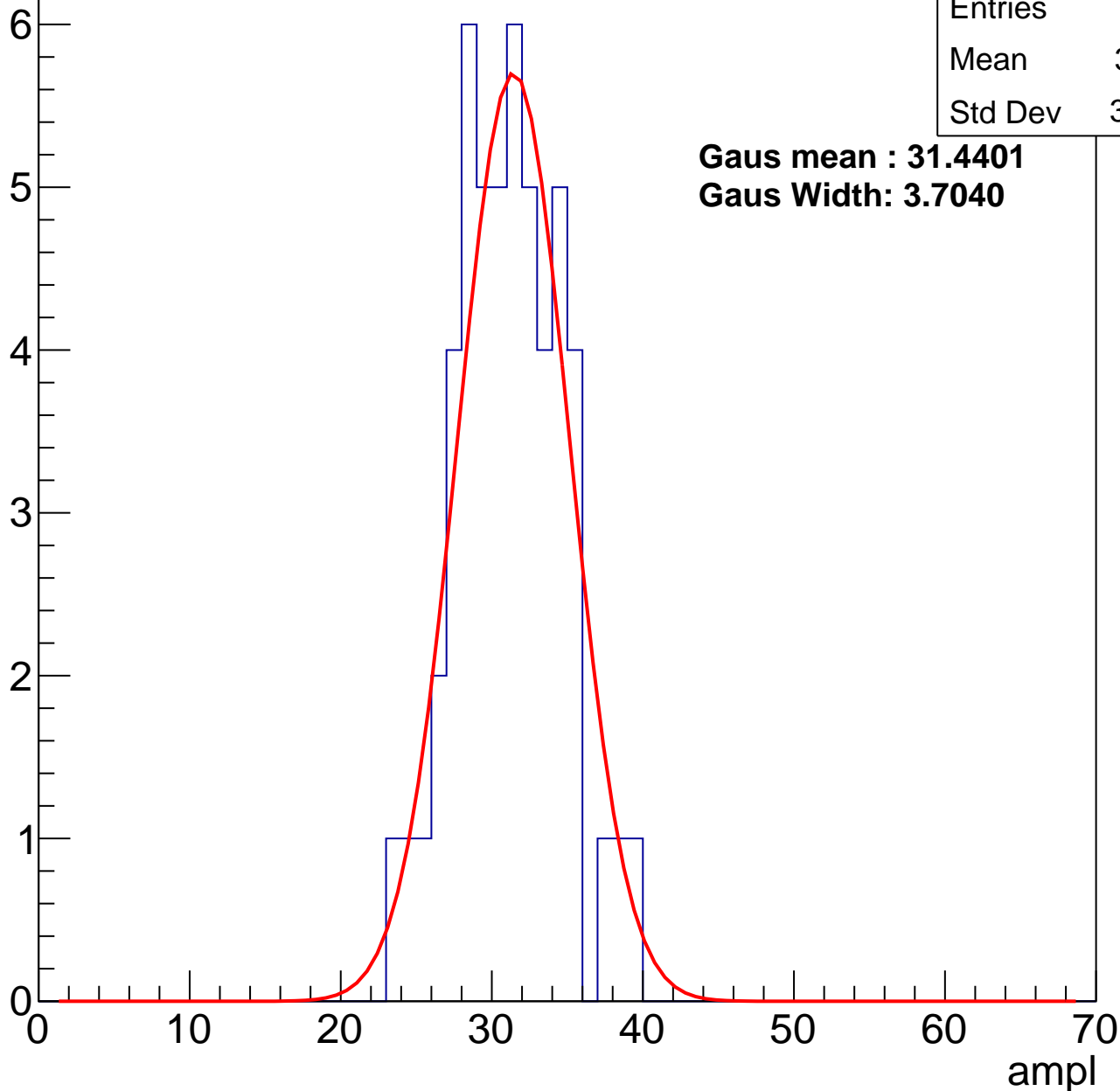
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	30.71
Std Dev	3.444

Gaus mean : 31.4401

Gaus Width: 3.7040



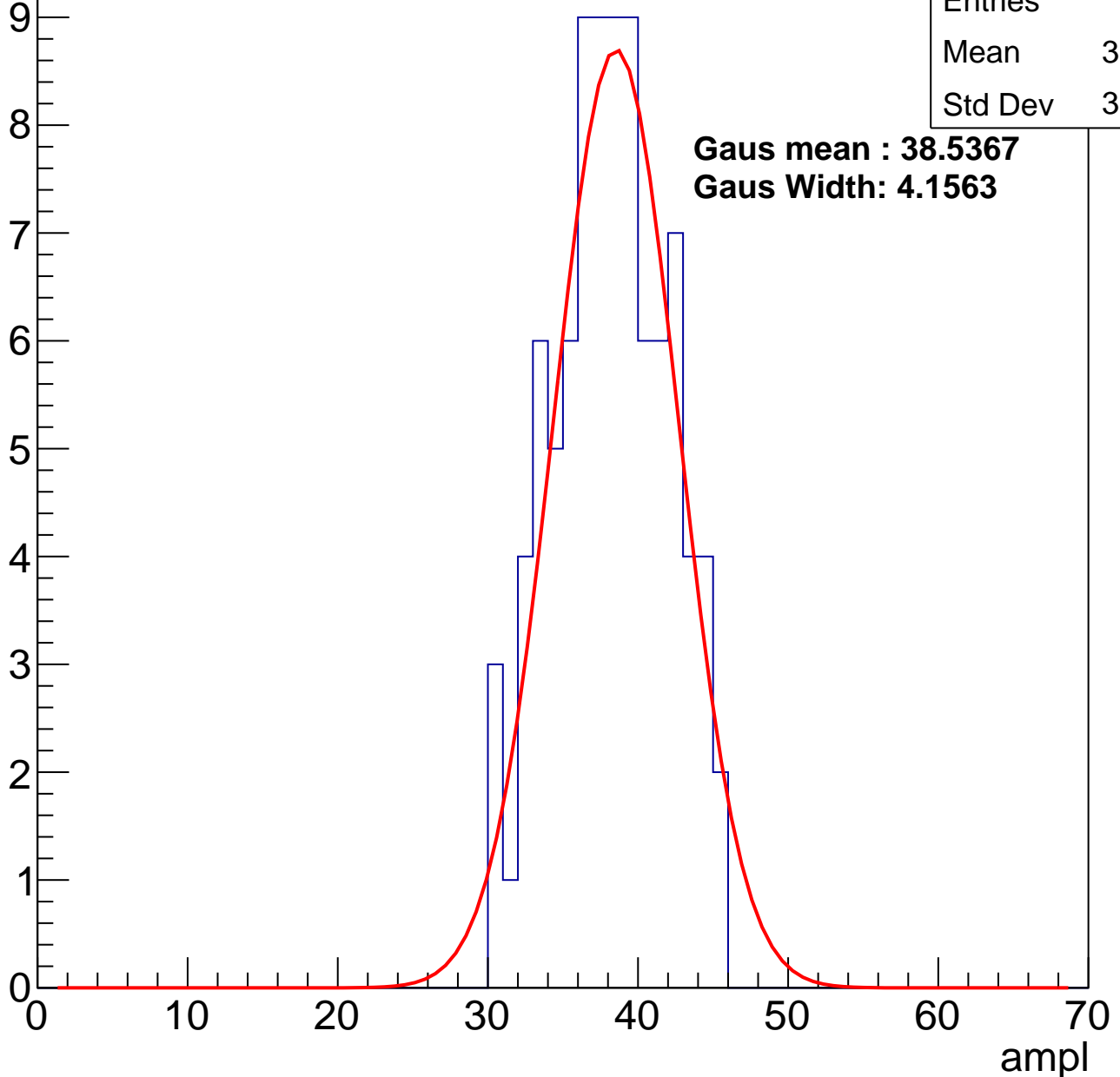
B0L002S, U12-ch59, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	90
Mean	37.72
Std Dev	3.724

Gaus mean : 38.5367
Gaus Width: 4.1563



B0L002S, U12-ch59, adc2

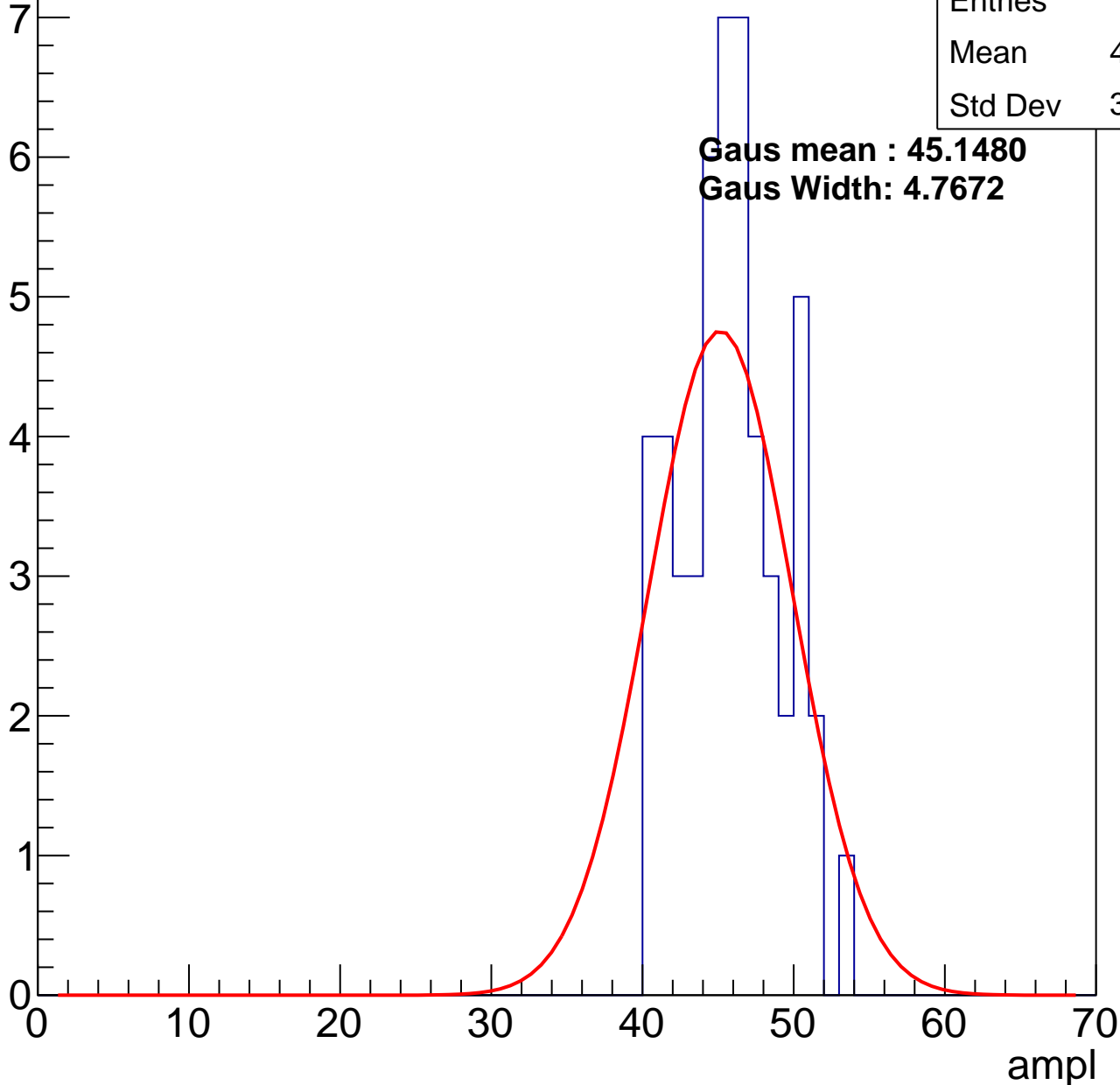
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.39
Std Dev	3.266

Gaus mean : 45.1480

Gaus Width: 4.7672

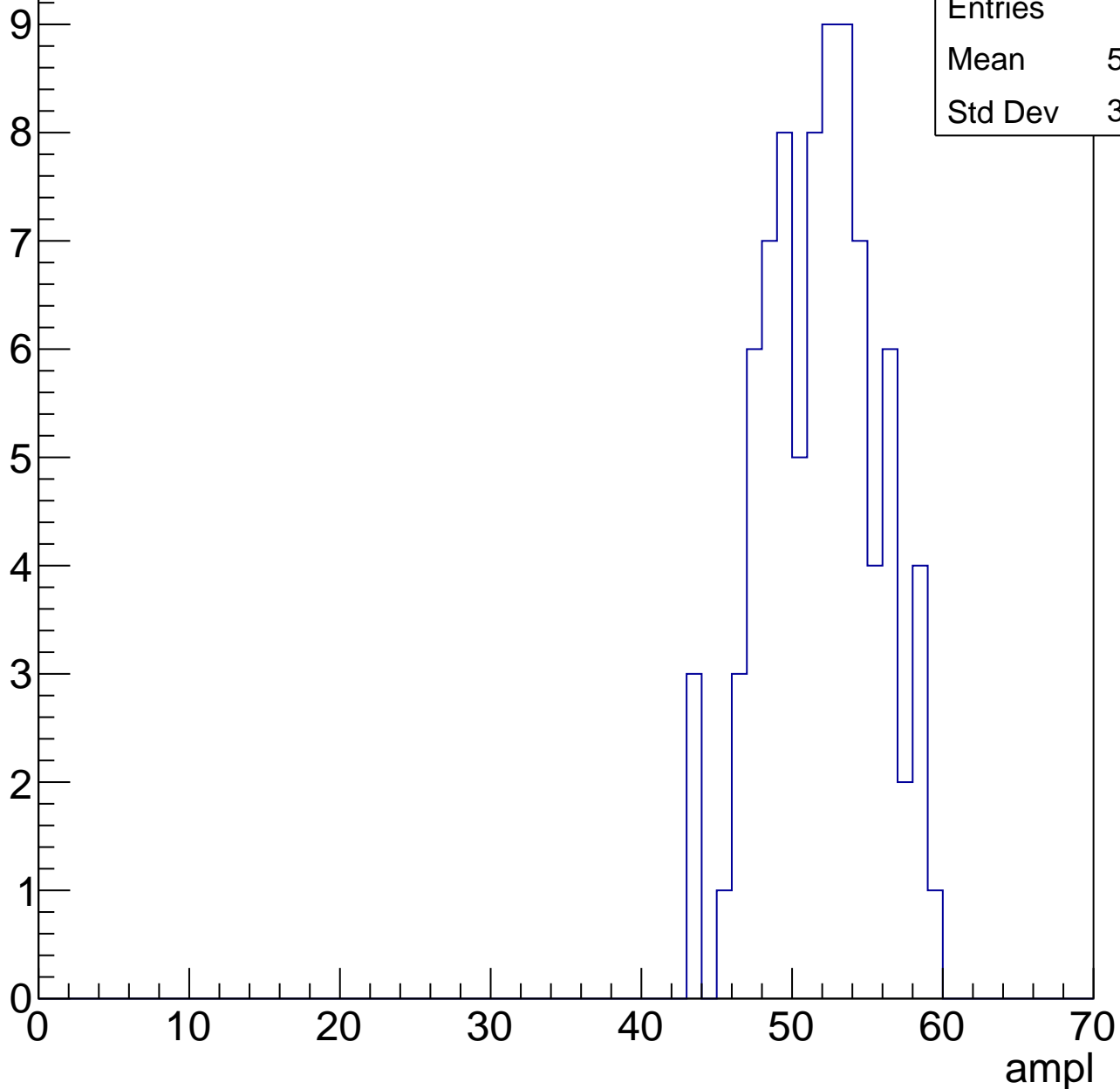


B0L002S, U12-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	51.37
Std Dev	3.708

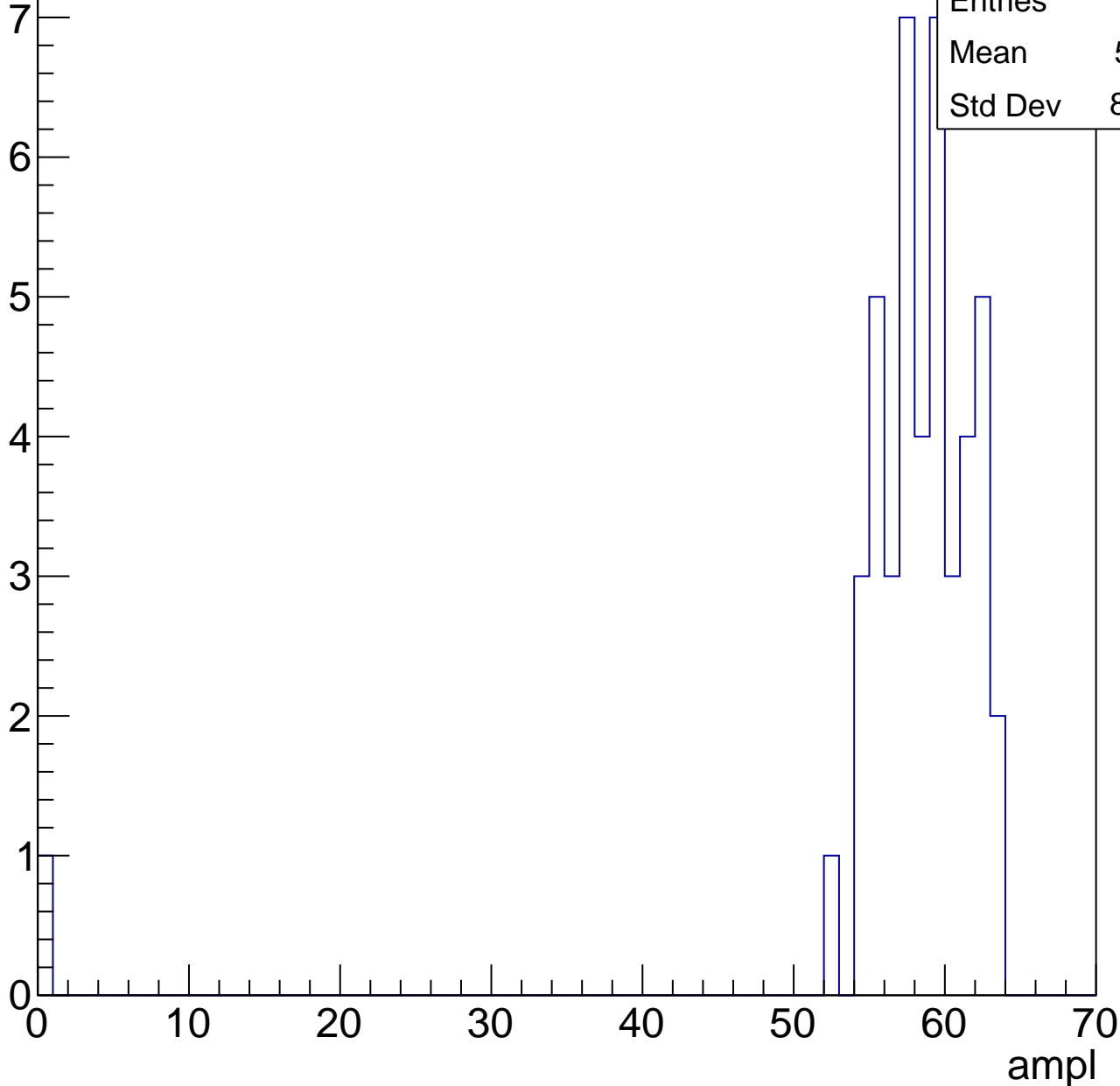


B0L002S, U12-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

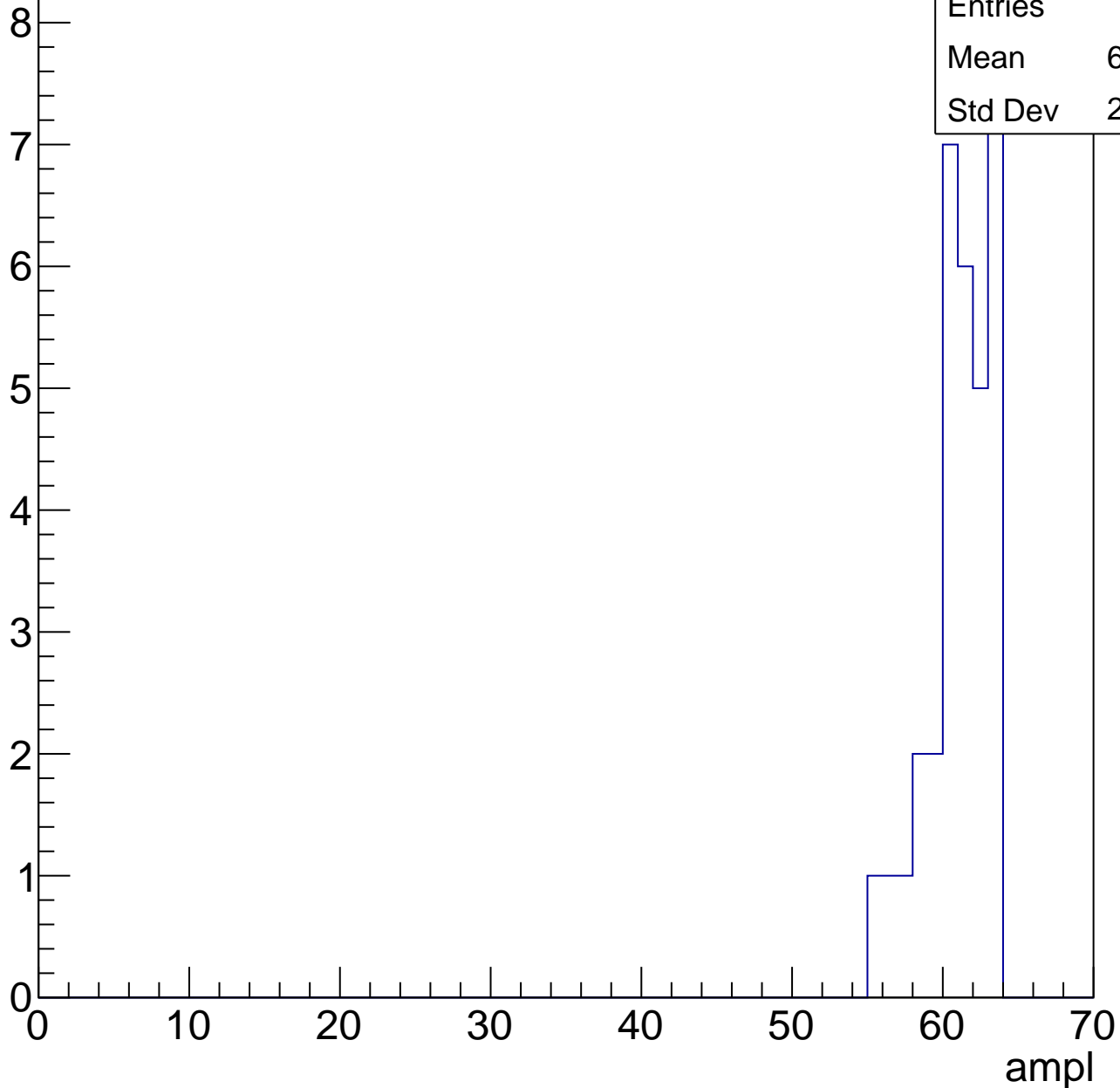
Entries	45
Mean	56.91
Std Dev	8.998



B0L002S, U12-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch60, adc0

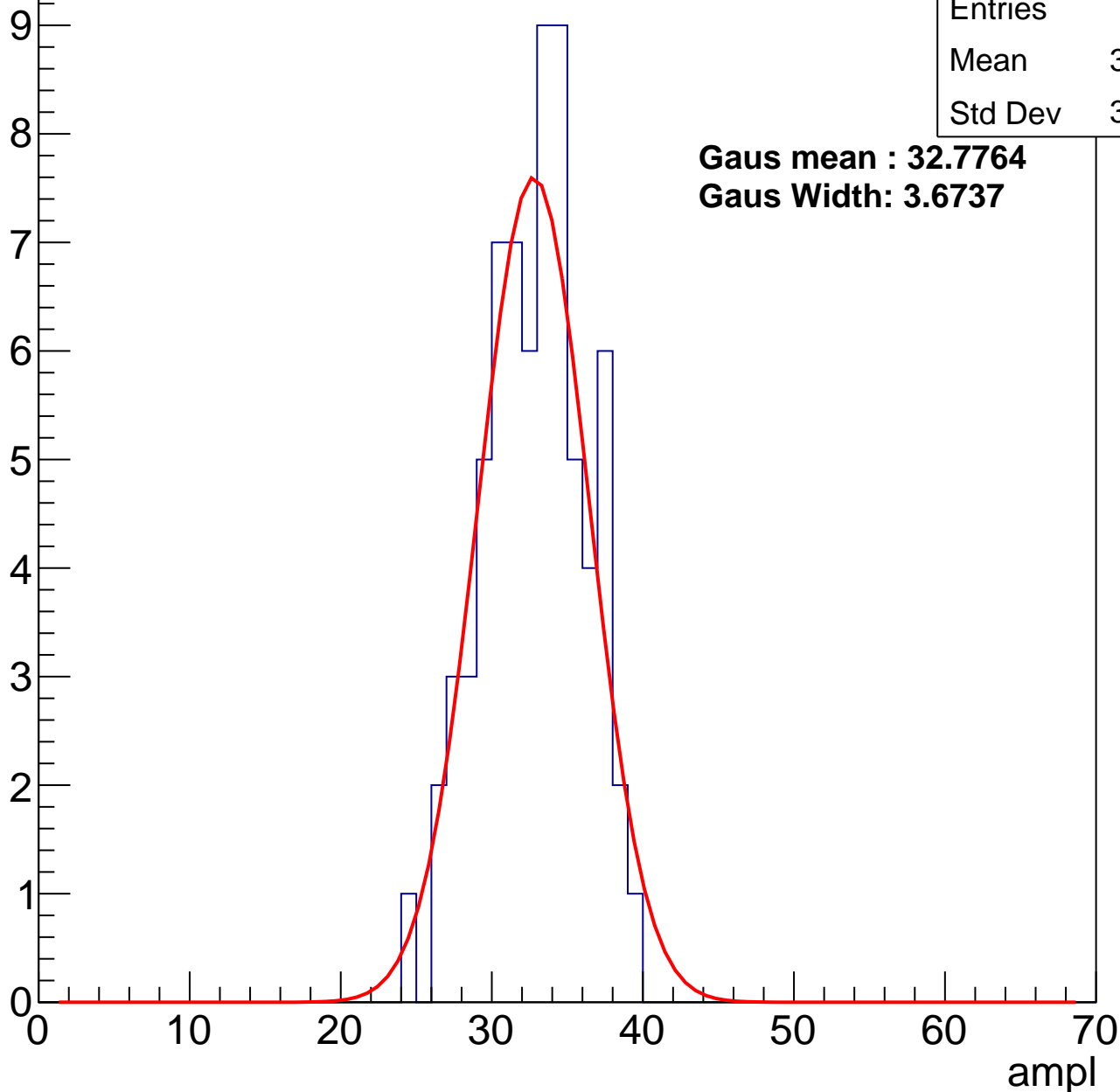
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	32.34
Std Dev	3.286

Gaus mean : 32.7764

Gaus Width: 3.6737



B0L002S, U12-ch60, adc1

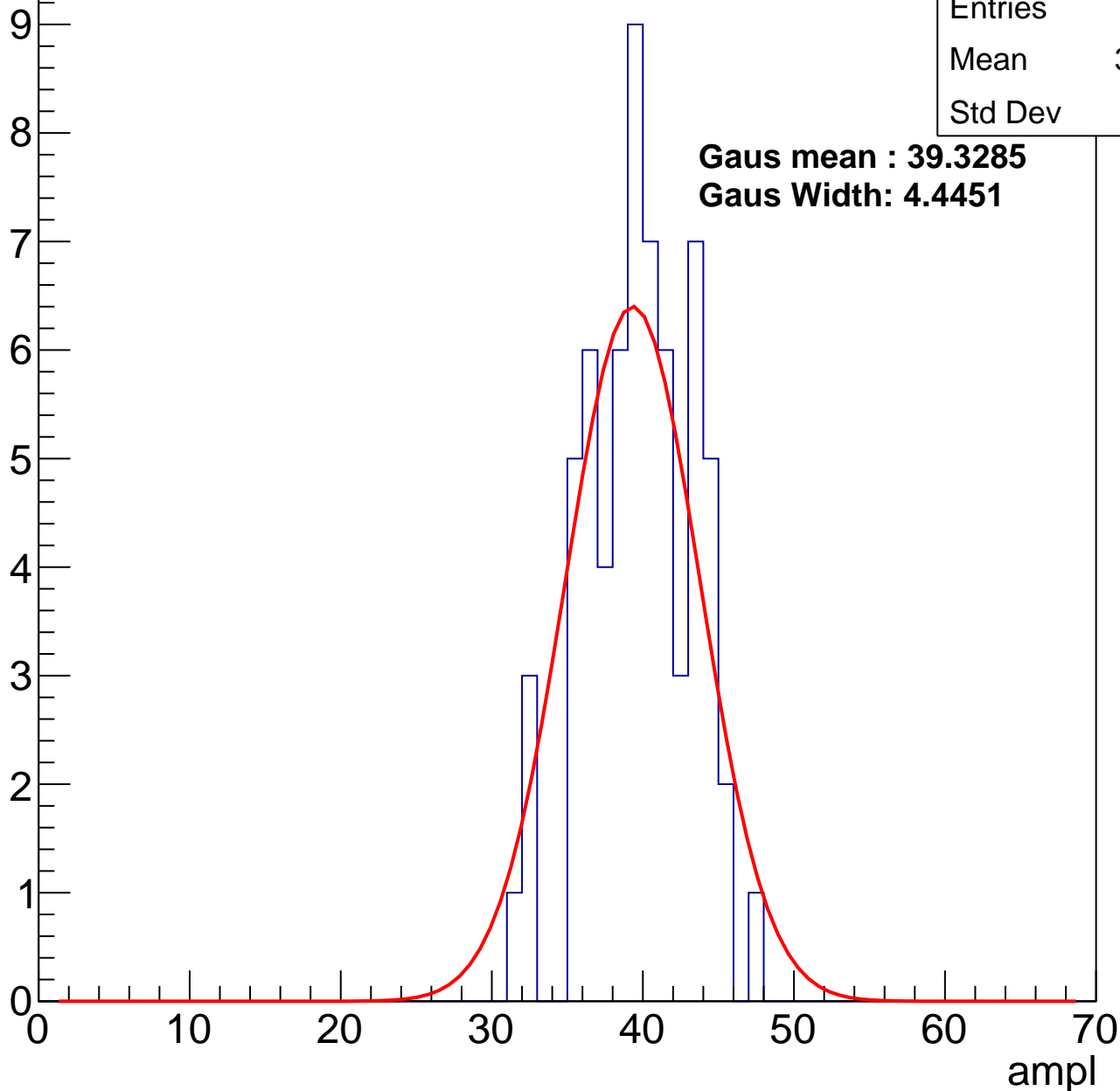
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	39.31
Std Dev	3.49

Gaus mean : 39.3285

Gaus Width: 4.4451



B0L002S, U12-ch60, adc2

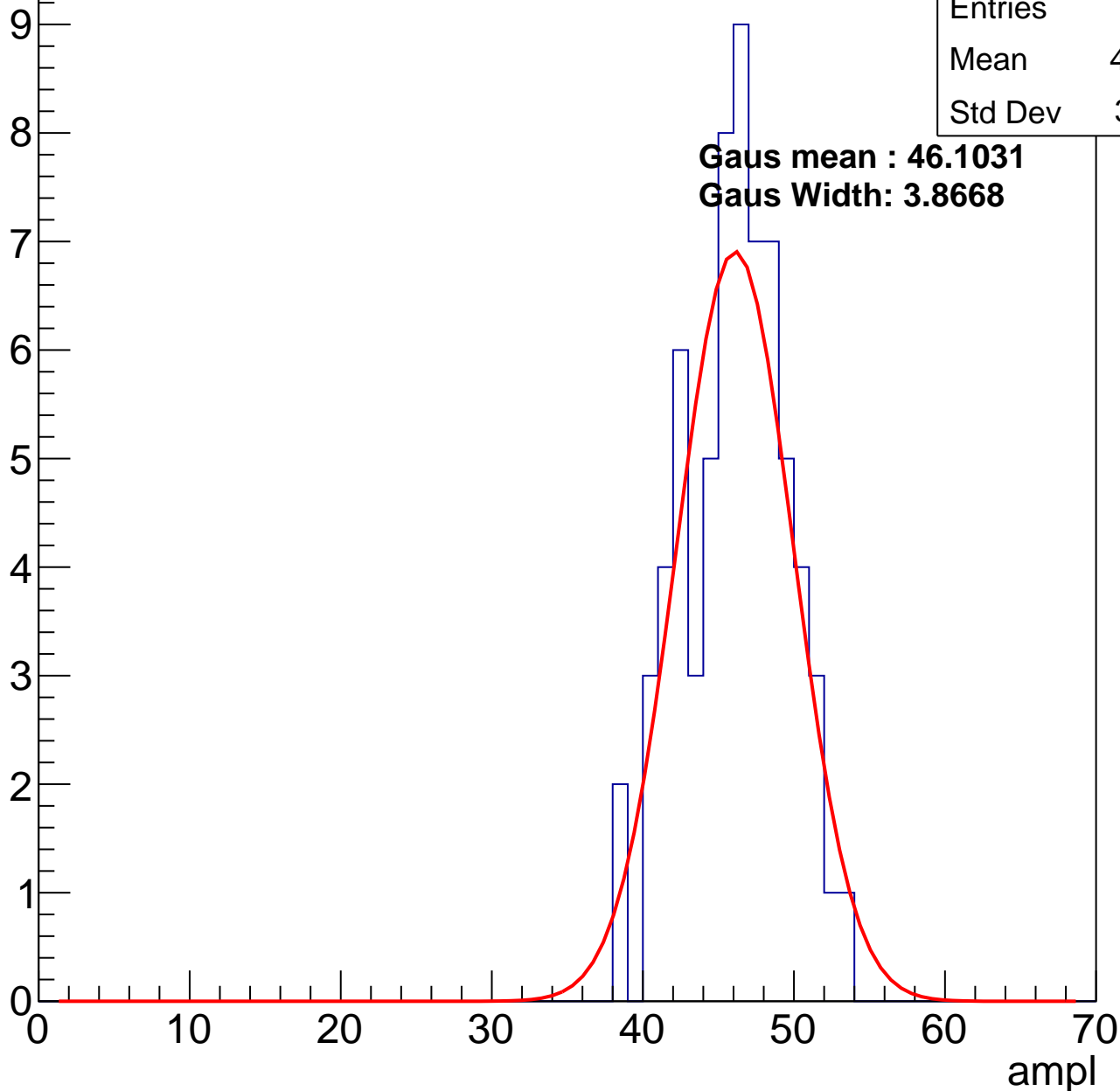
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45.63
Std Dev	3.391

Gaus mean : 46.1031

Gaus Width: 3.8668

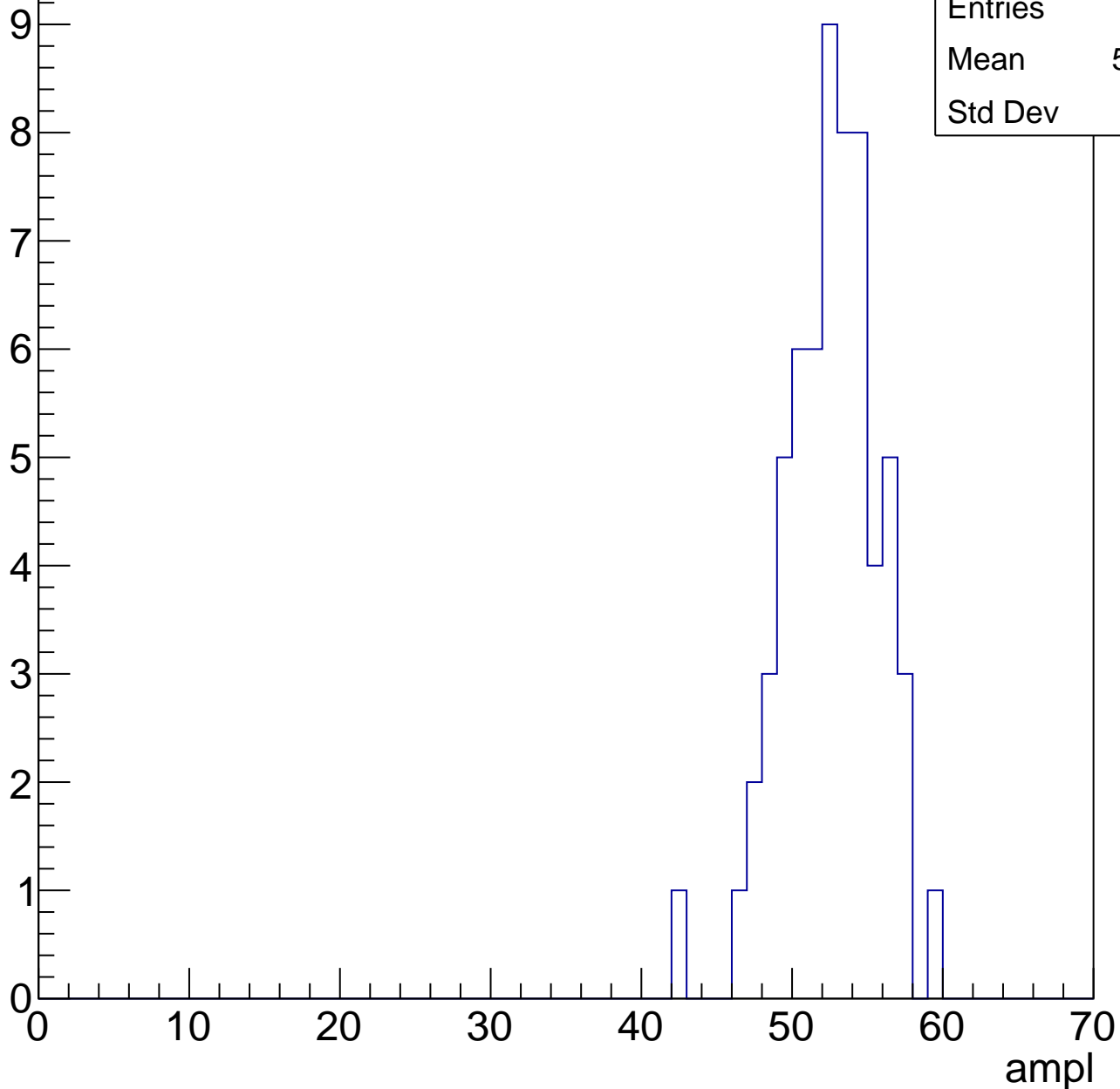


B0L002S, U12-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	52.11
Std Dev	3.08

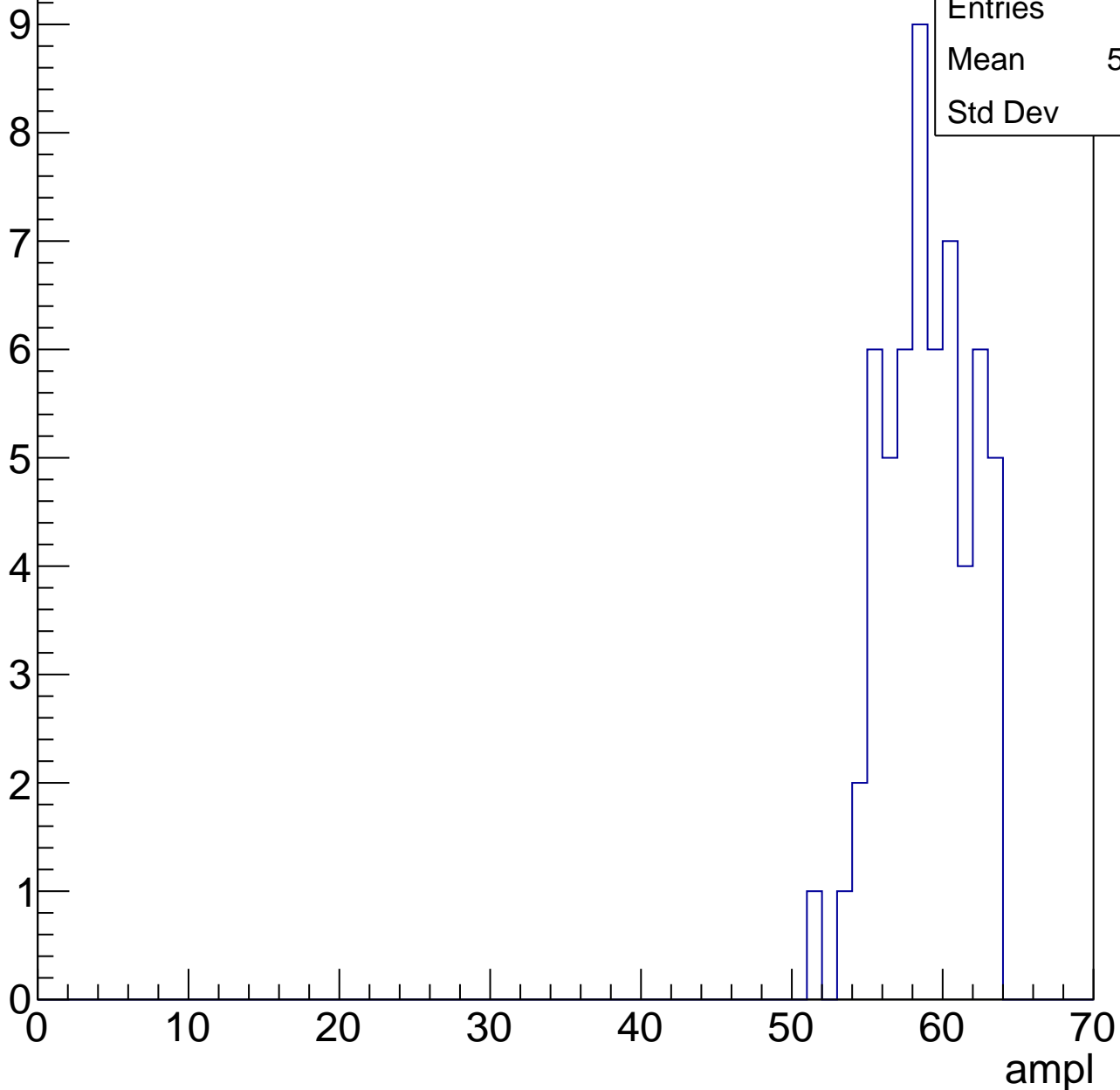


B0L002S, U12-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	58.47
Std Dev	2.83

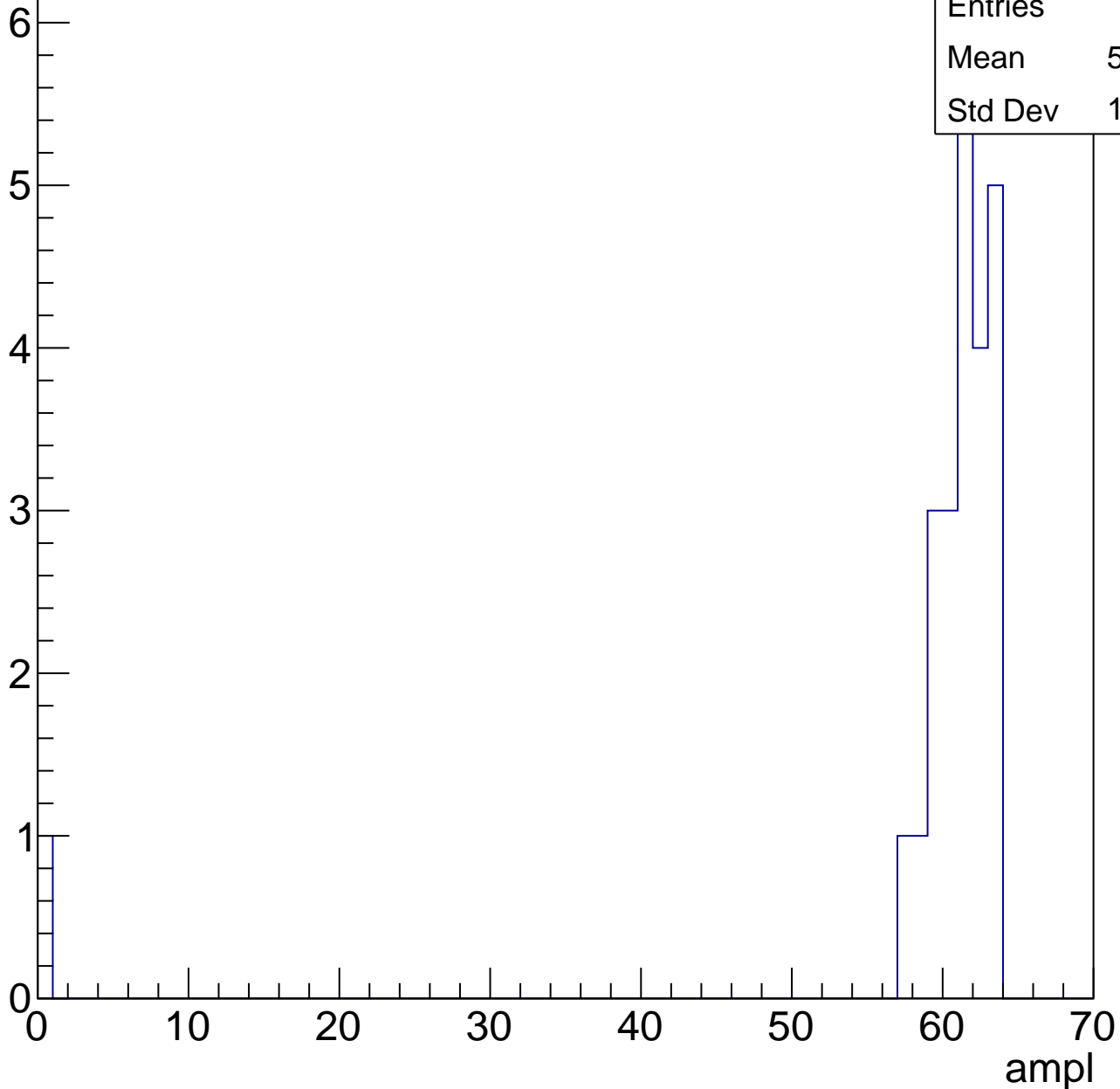


B0L002S, U12-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.38
Std Dev	12.28



B0L002S, U12-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch61, adc0

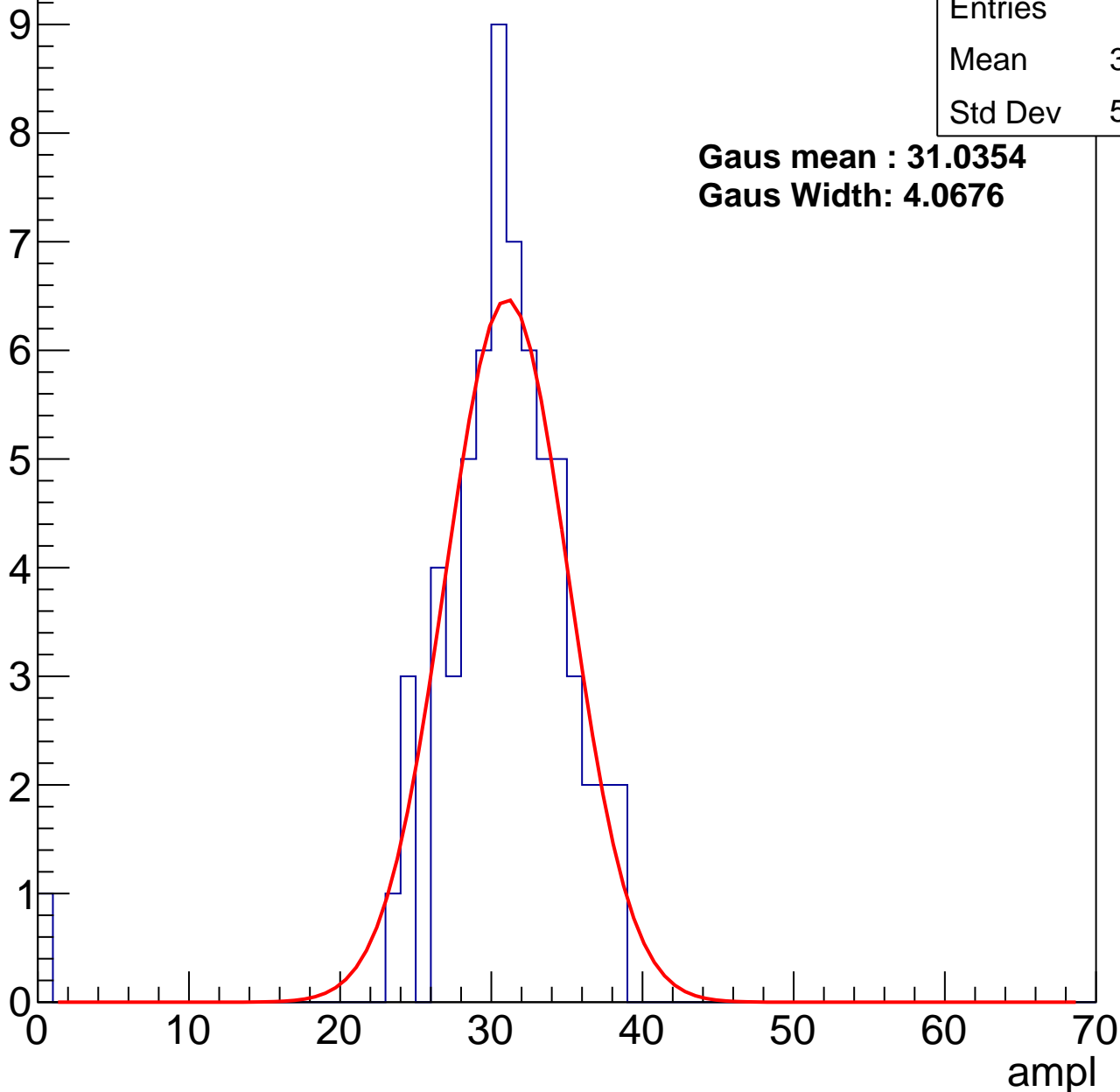
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.23
Std Dev	5.153

Gaus mean : 31.0354

Gaus Width: 4.0676



B0L002S, U12-ch61, adc1

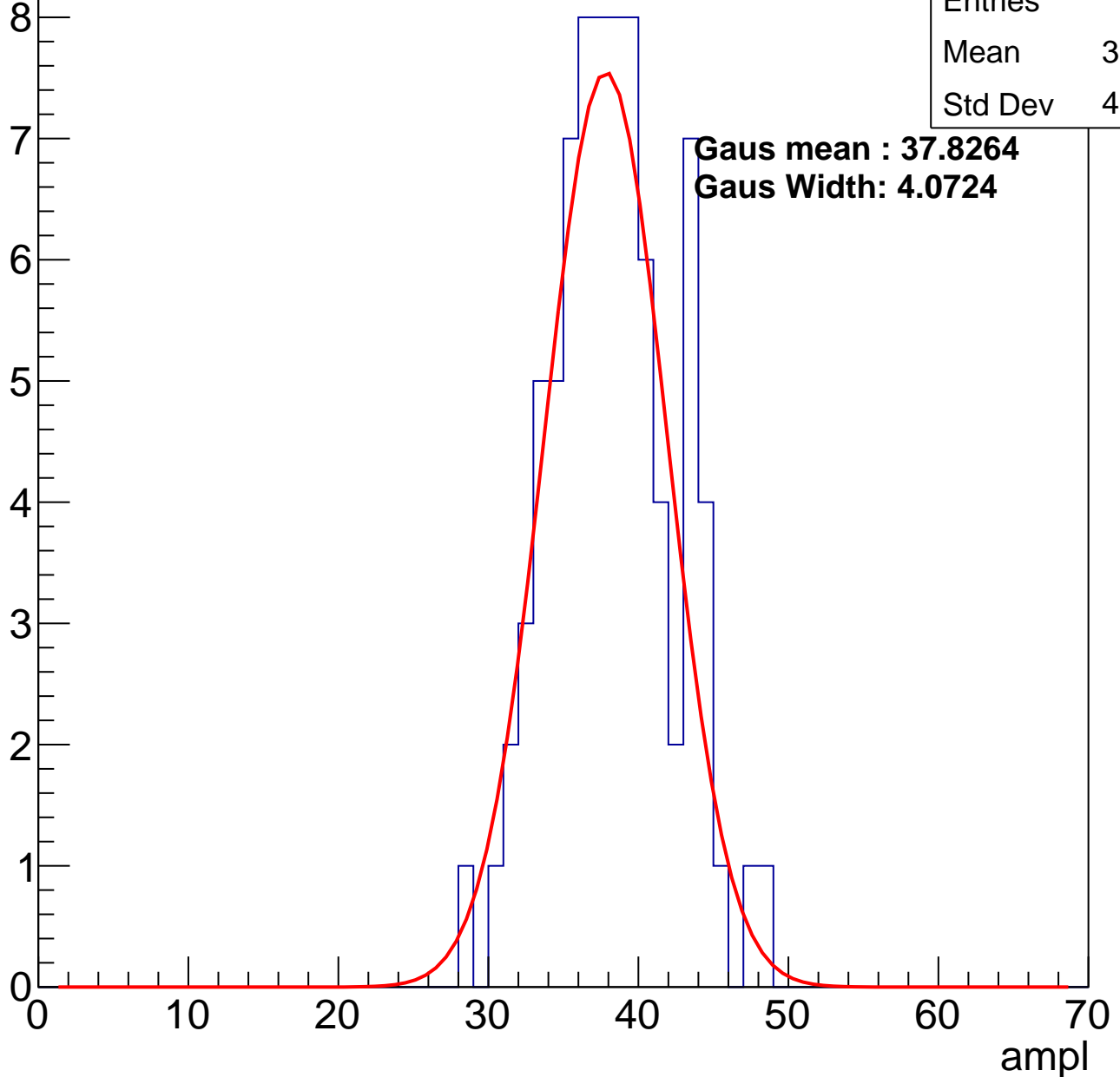
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	37.82
Std Dev	4.019

Gaus mean : 37.8264

Gaus Width: 4.0724



B0L002S, U12-ch61, adc2

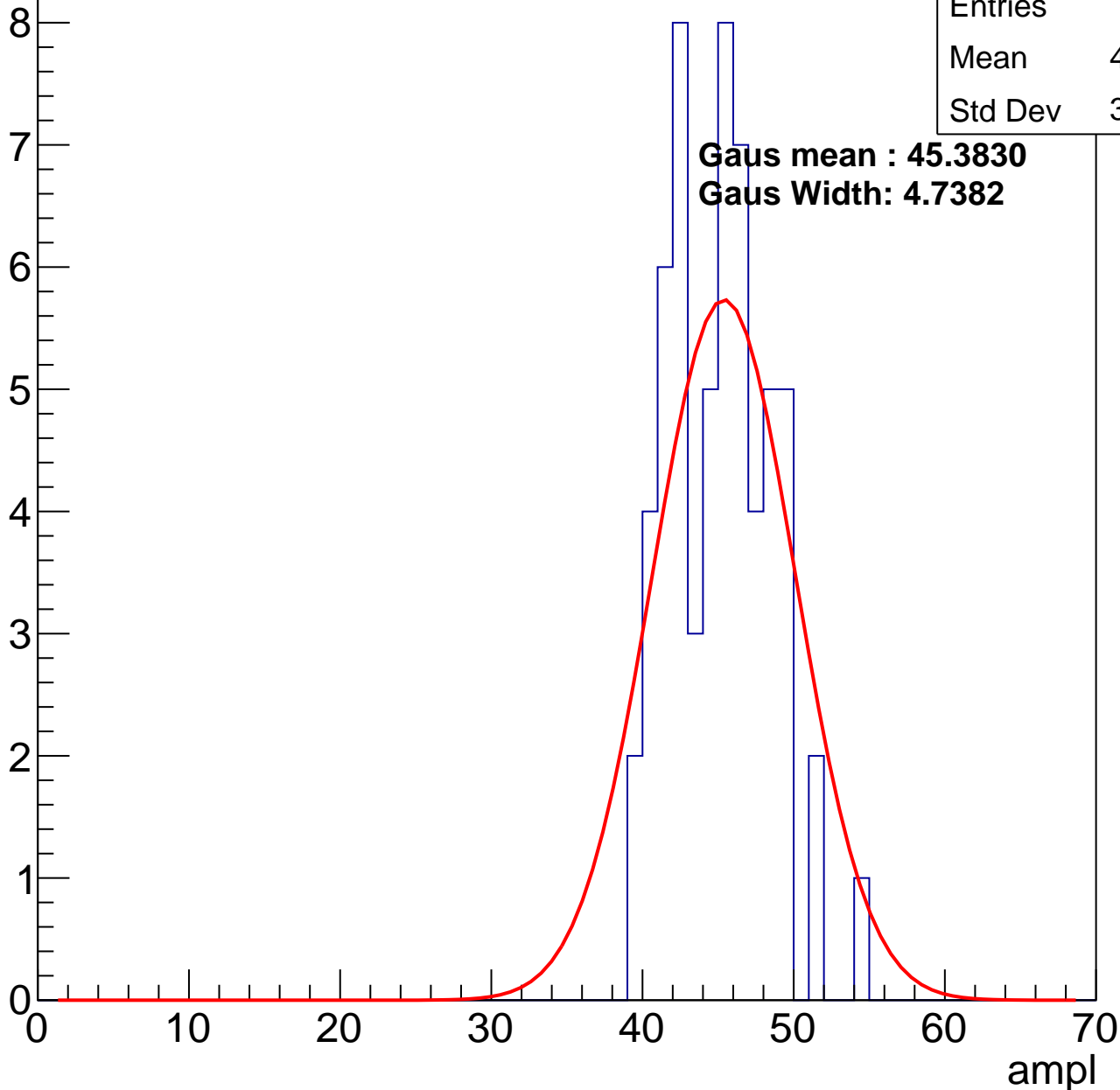
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.67
Std Dev	3.295

Gaus mean : 45.3830

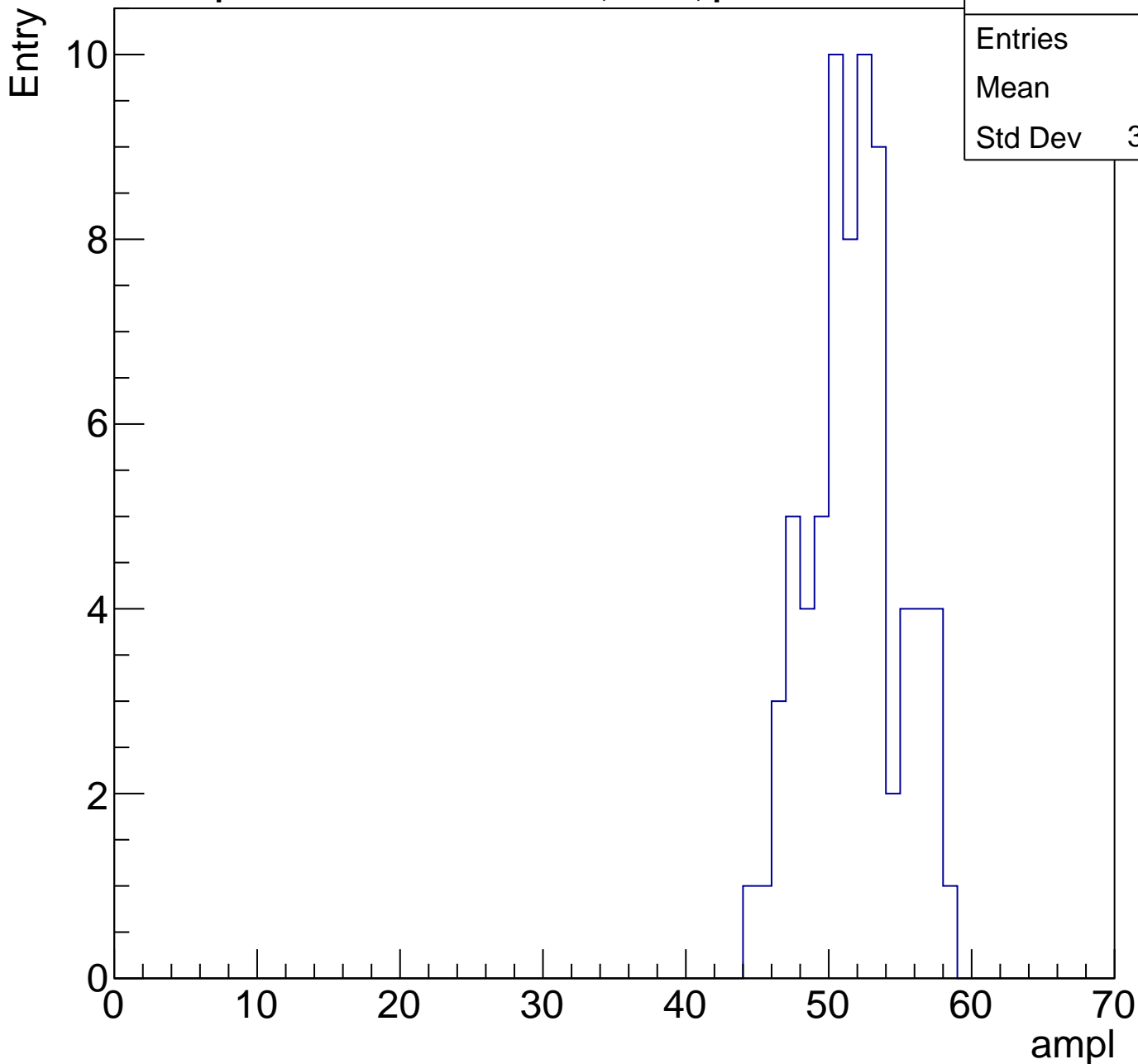
Gaus Width: 4.7382



B0L002S, U12-ch61, adc3

calib_packv5_042523_0143.root, FC#8, port C1

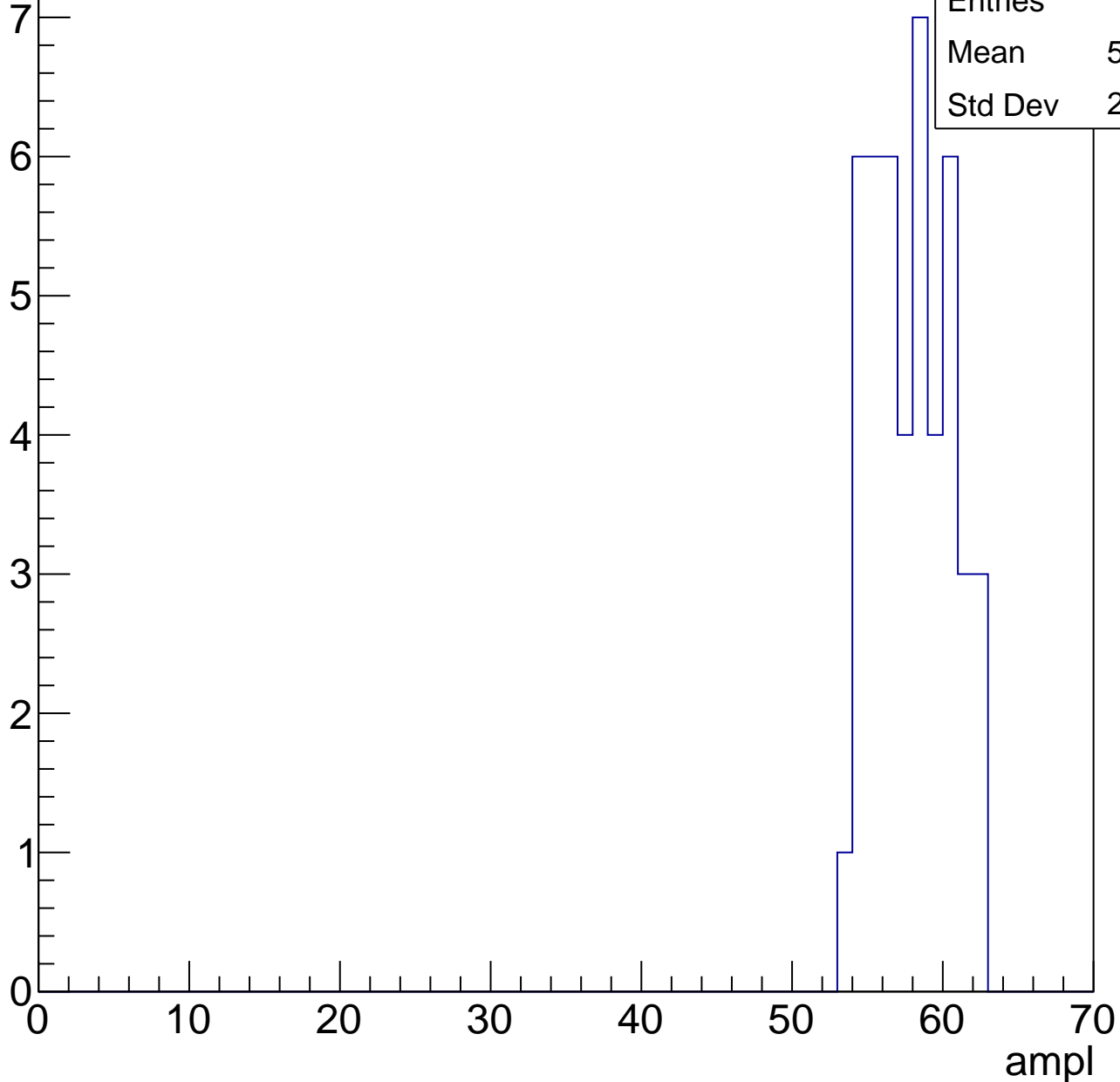
Entries	71
Mean	51.3
Std Dev	3.186



B0L002S, U12-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

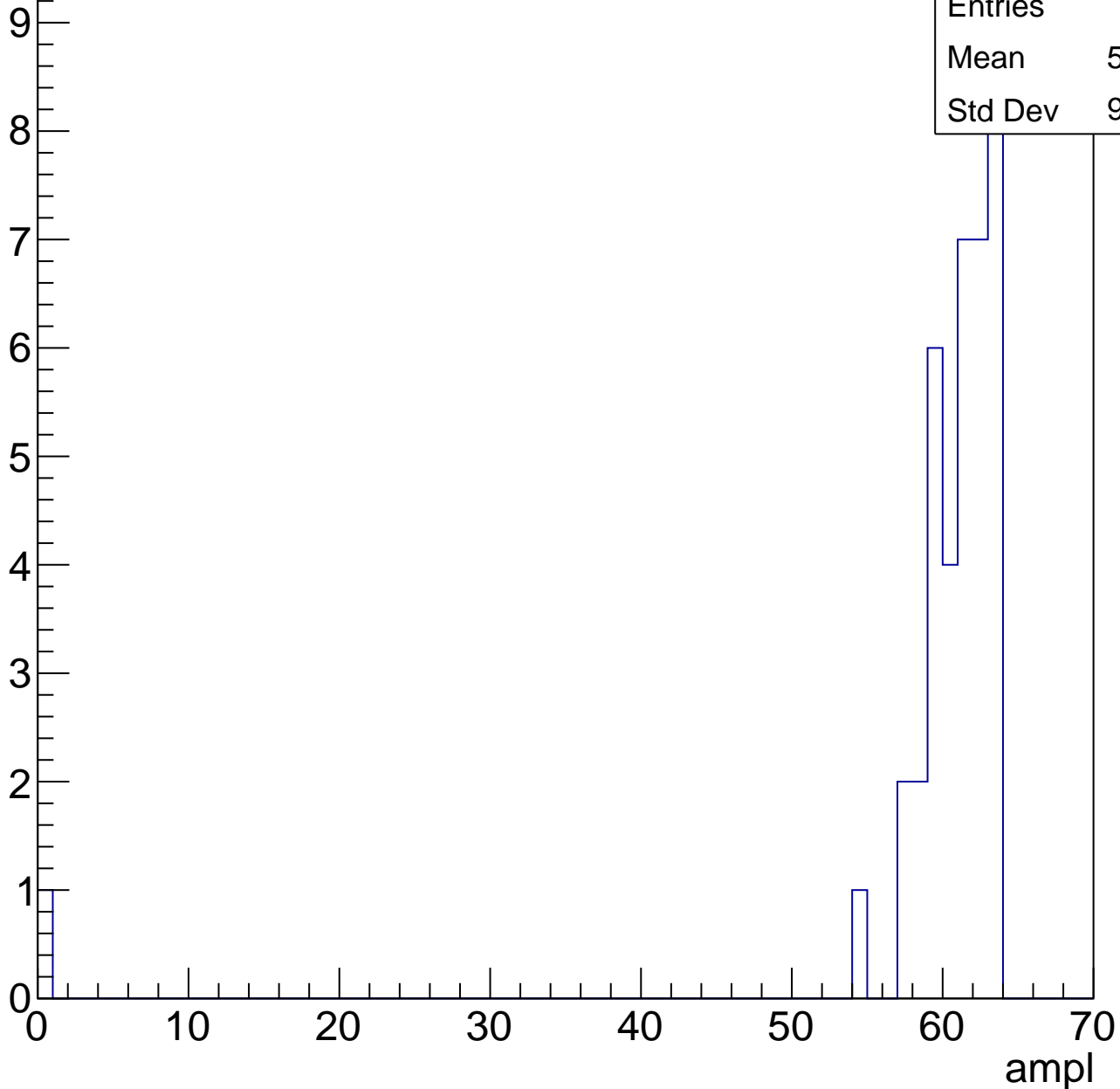


B0L002S, U12-ch61, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.13
Std Dev	9.812



B0L002S, U12-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch62, adc0

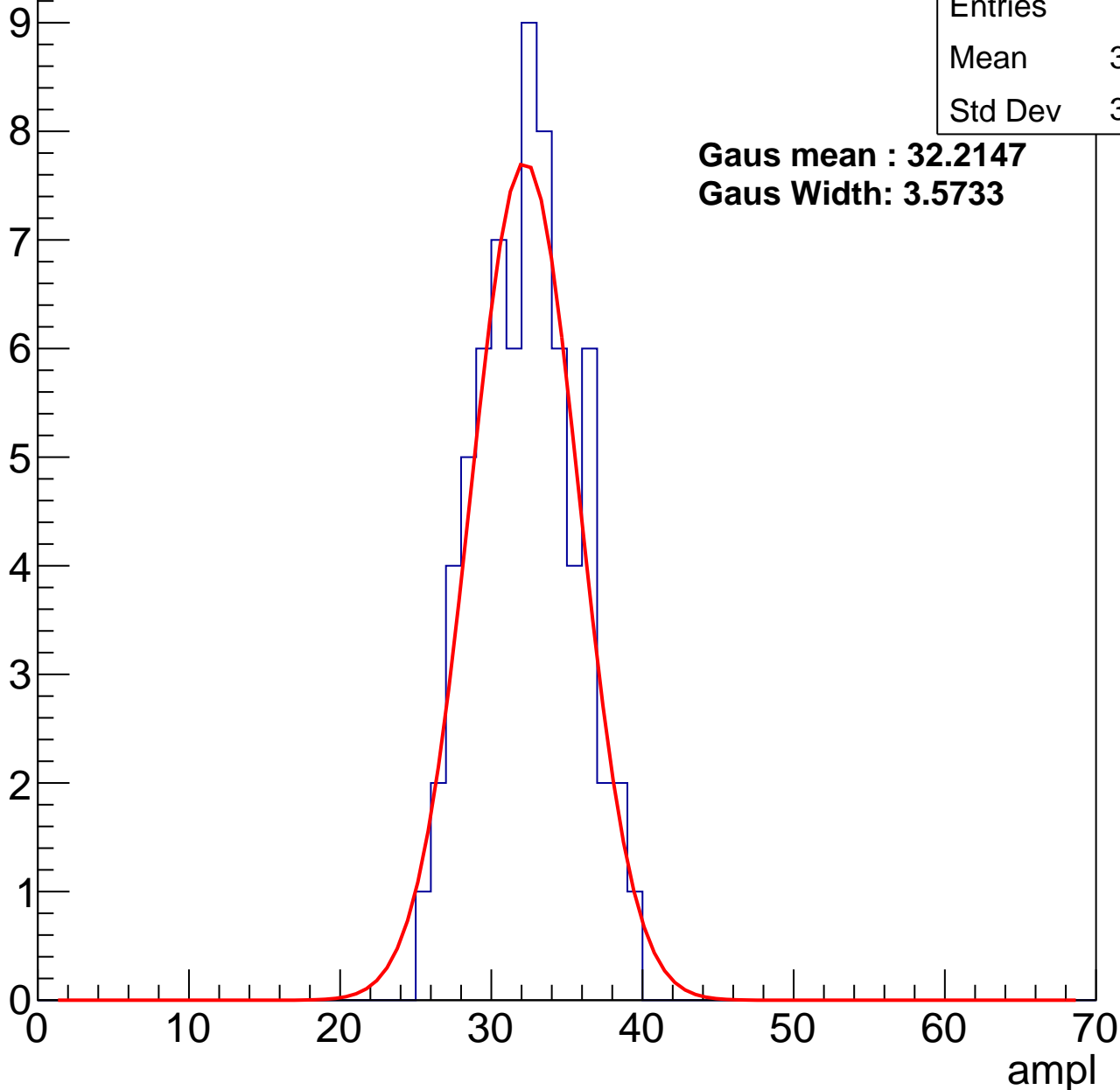
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	31.83
Std Dev	3.235

Gaus mean : 32.2147

Gaus Width: 3.5733



B0L002S, U12-ch62, adc1

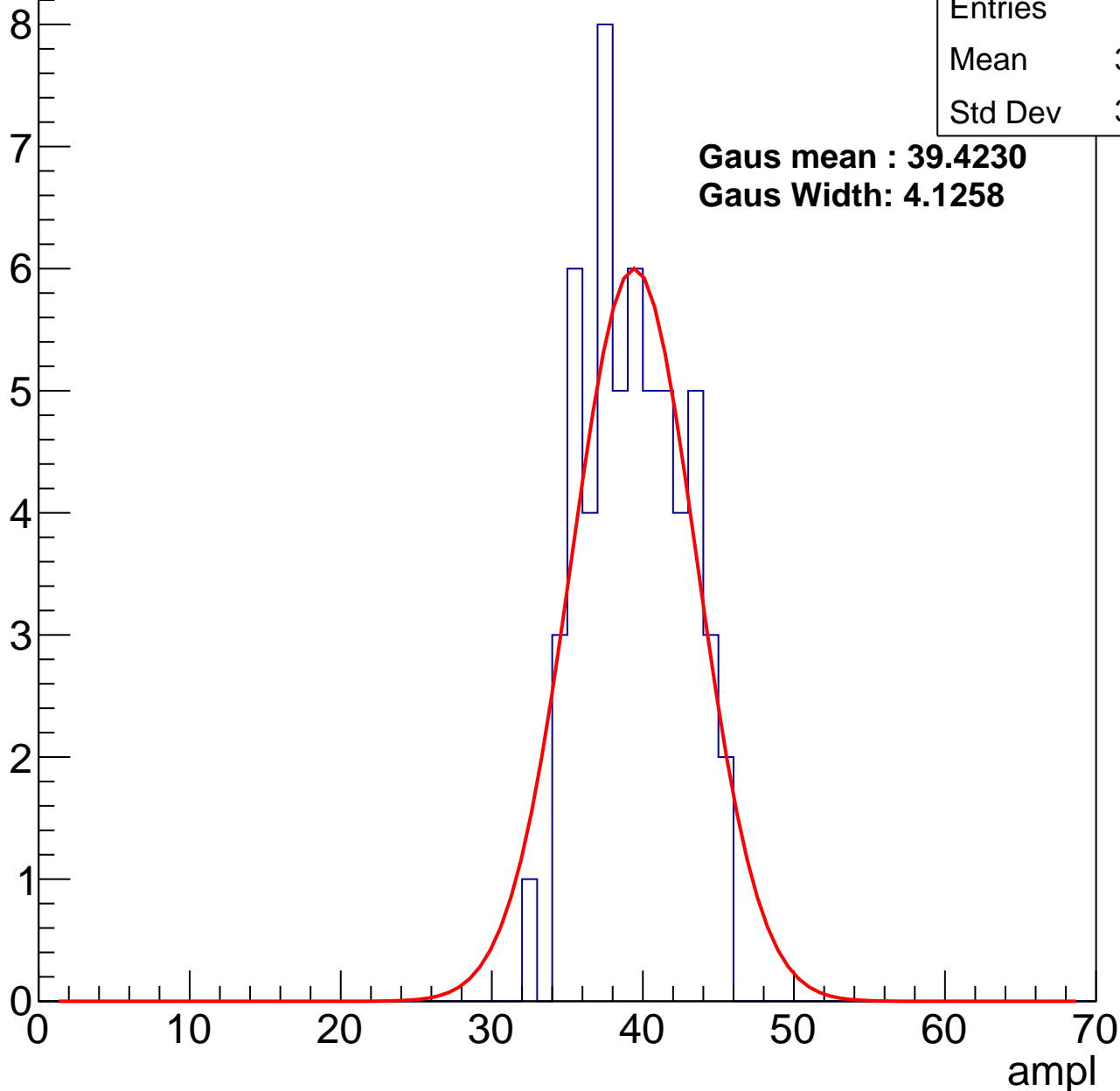
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	38.91
Std Dev	3.191

Gaus mean : 39.4230

Gaus Width: 4.1258



B0L002S, U12-ch62, adc2

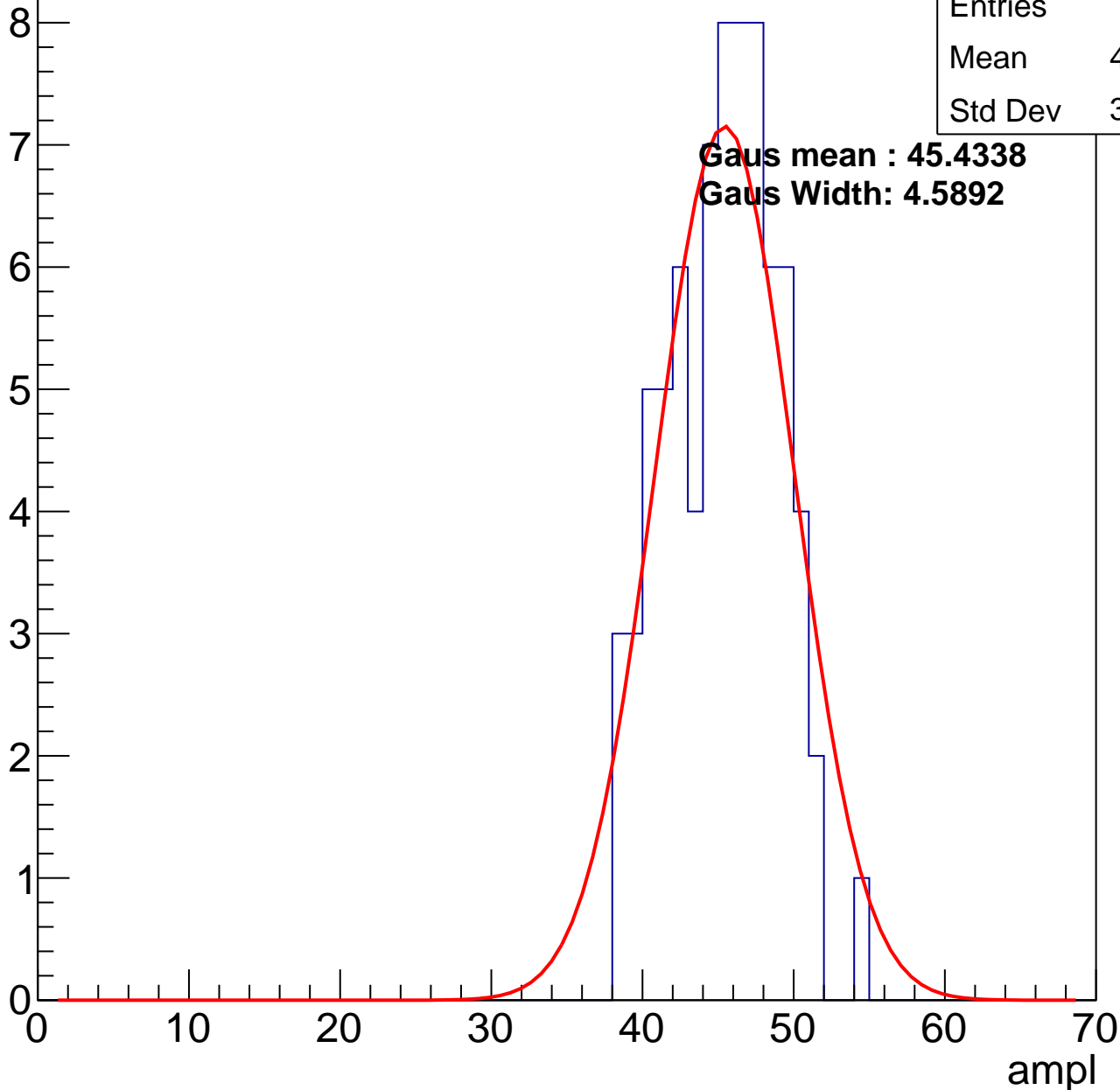
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	44.87
Std Dev	3.589

Gaus mean : 45.4338

Gaus Width: 4.5892

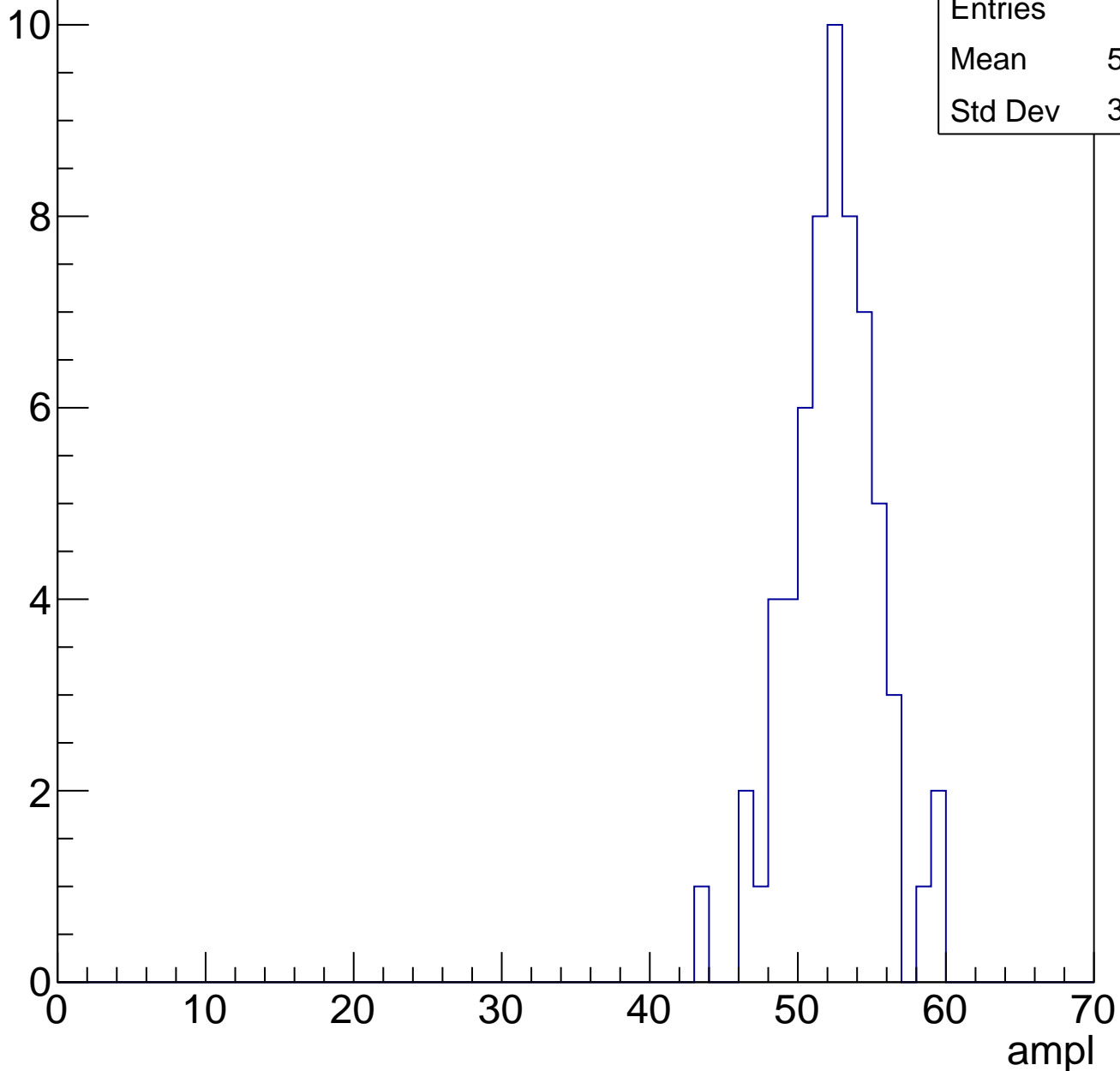


B0L002S, U12-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

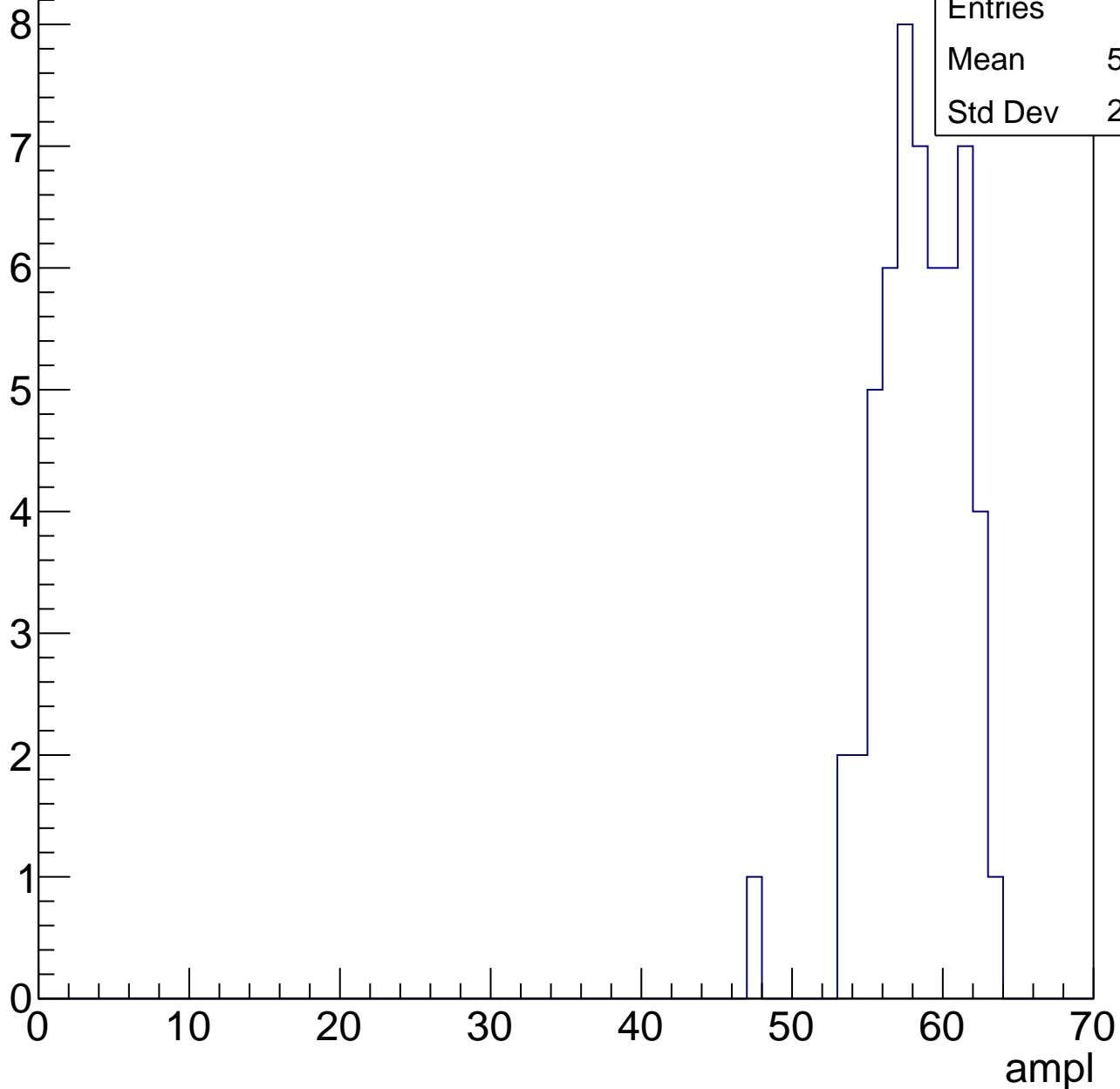
Entries	62
Mean	51.92
Std Dev	3.039



B0L002S, U12-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

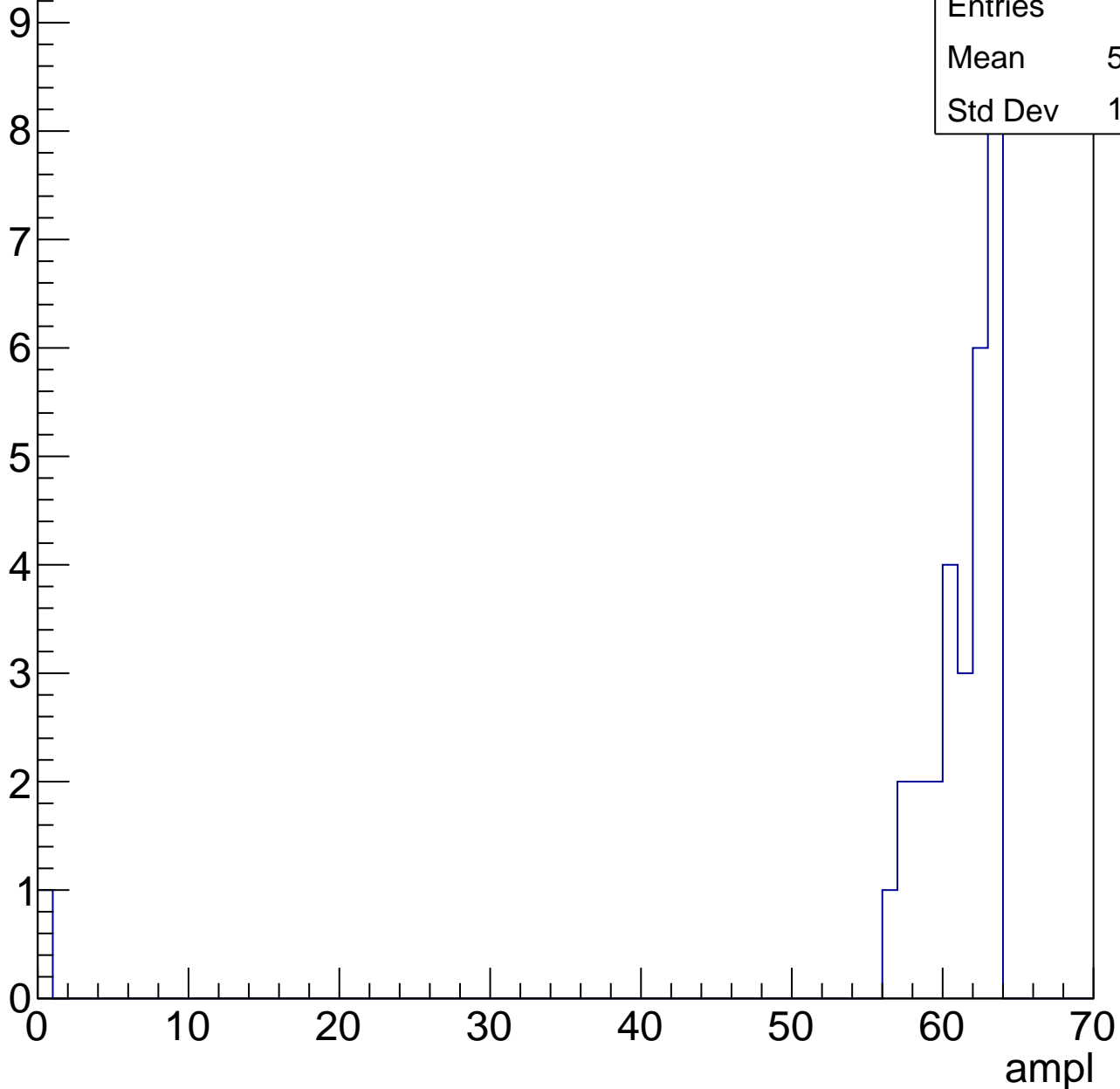


B0L002S, U12-ch62, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.87
Std Dev	11.13



B0L002S, U12-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch62, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch63, adc0

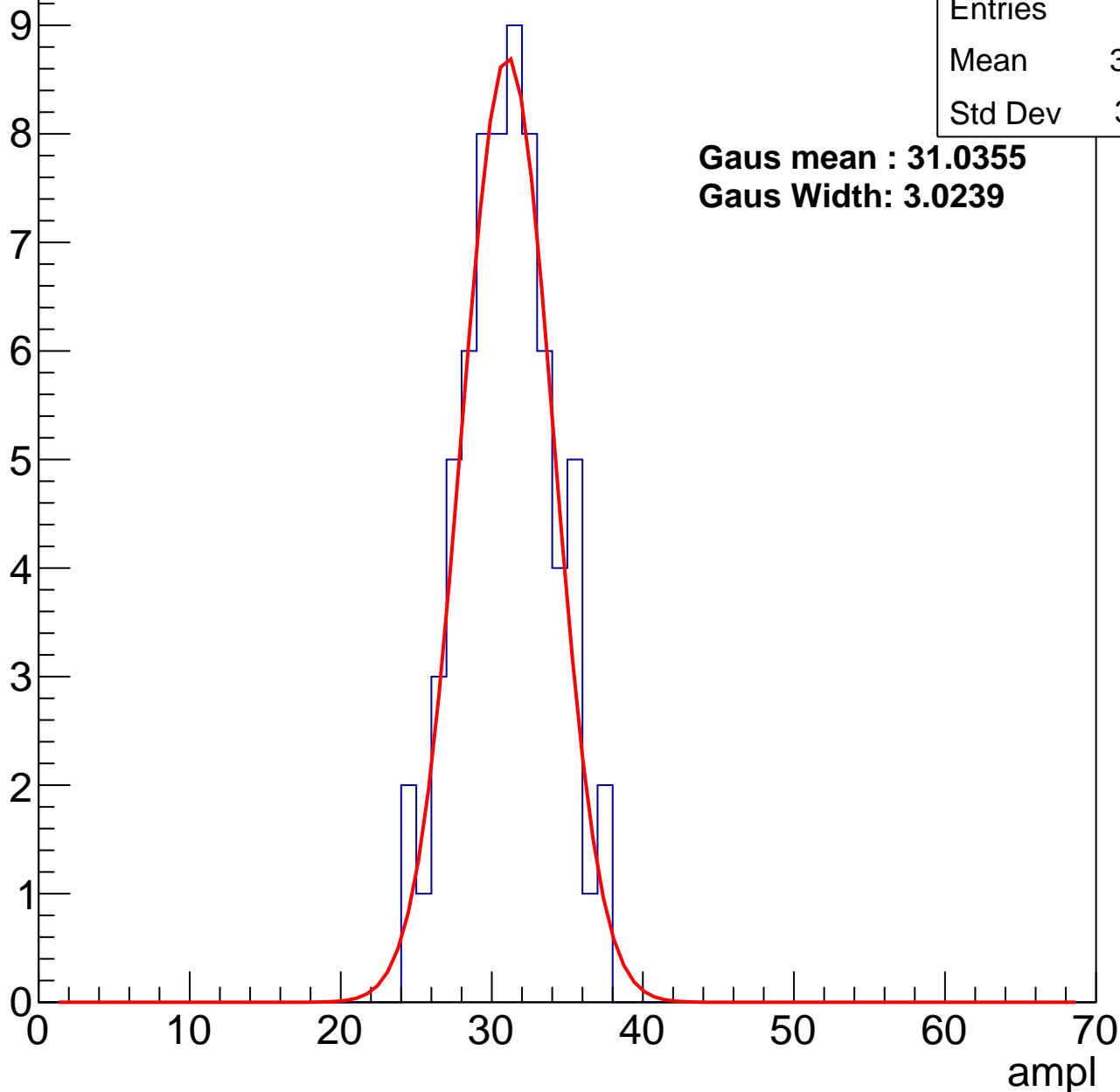
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.59
Std Dev	3.011

Gaus mean : 31.0355

Gaus Width: 3.0239



B0L002S, U12-ch63, adc1

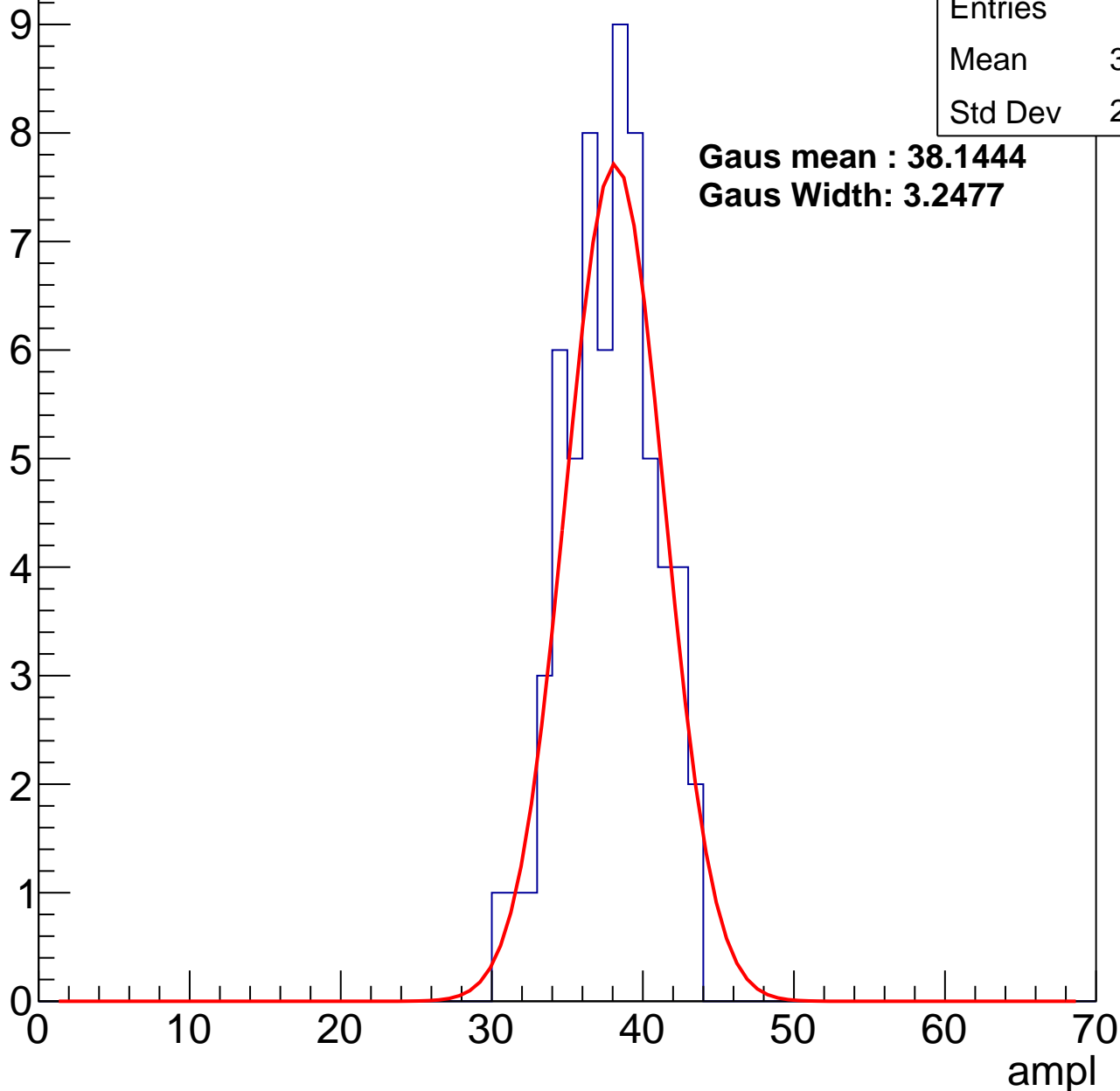
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.35
Std Dev	2.966

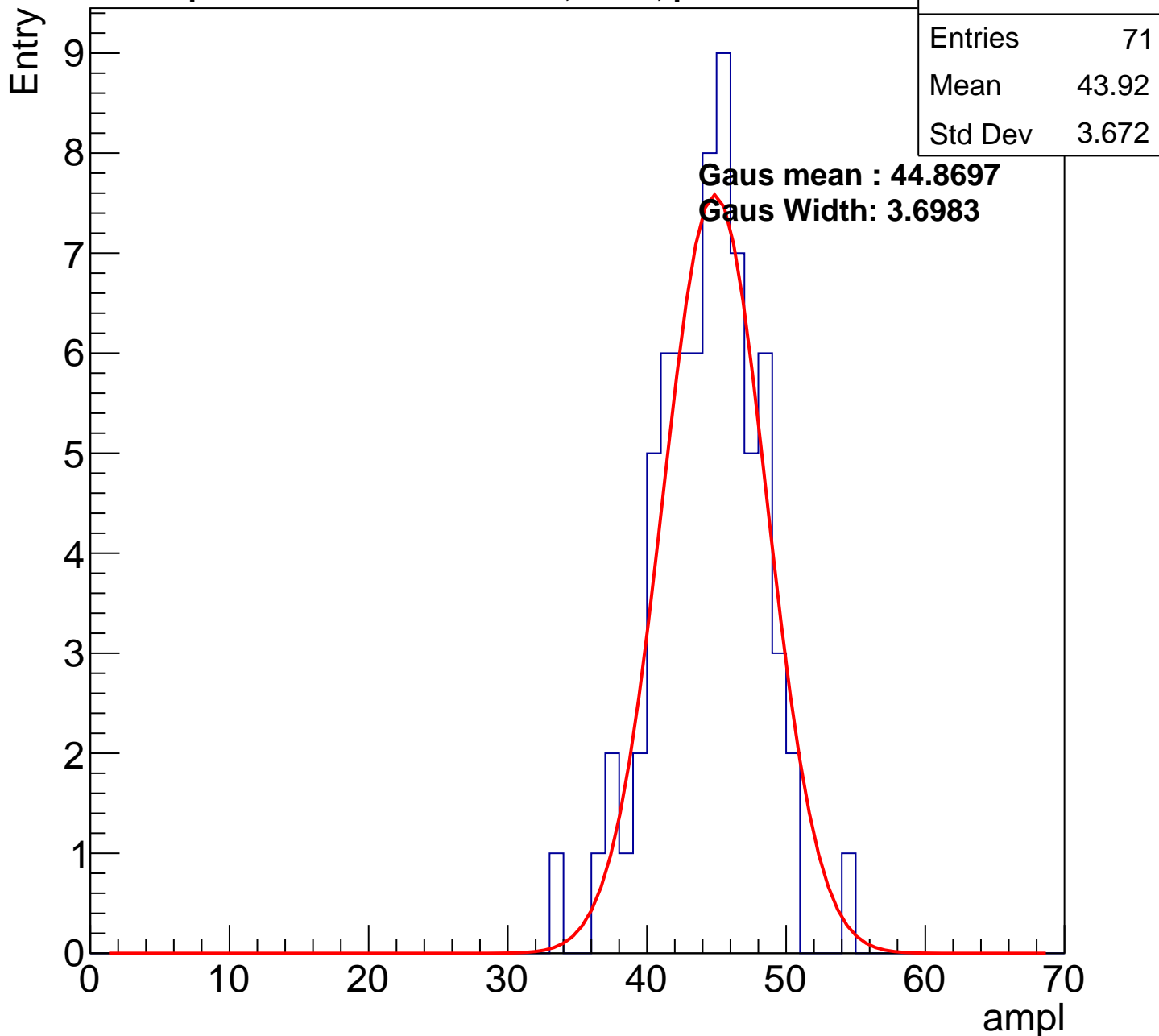
Gaus mean : 38.1444

Gaus Width: 3.2477



B0L002S, U12-ch63, adc2

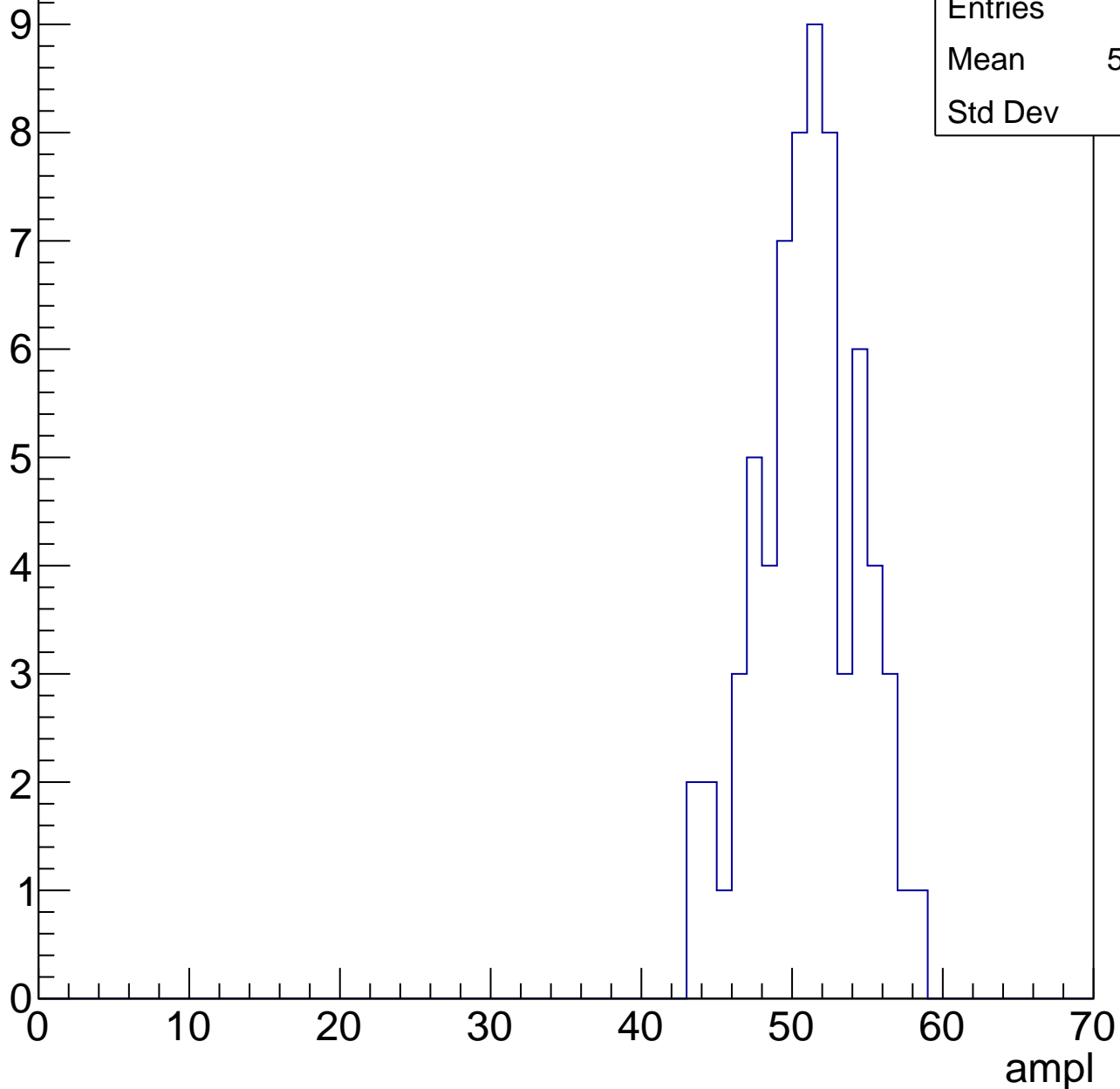
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U12-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

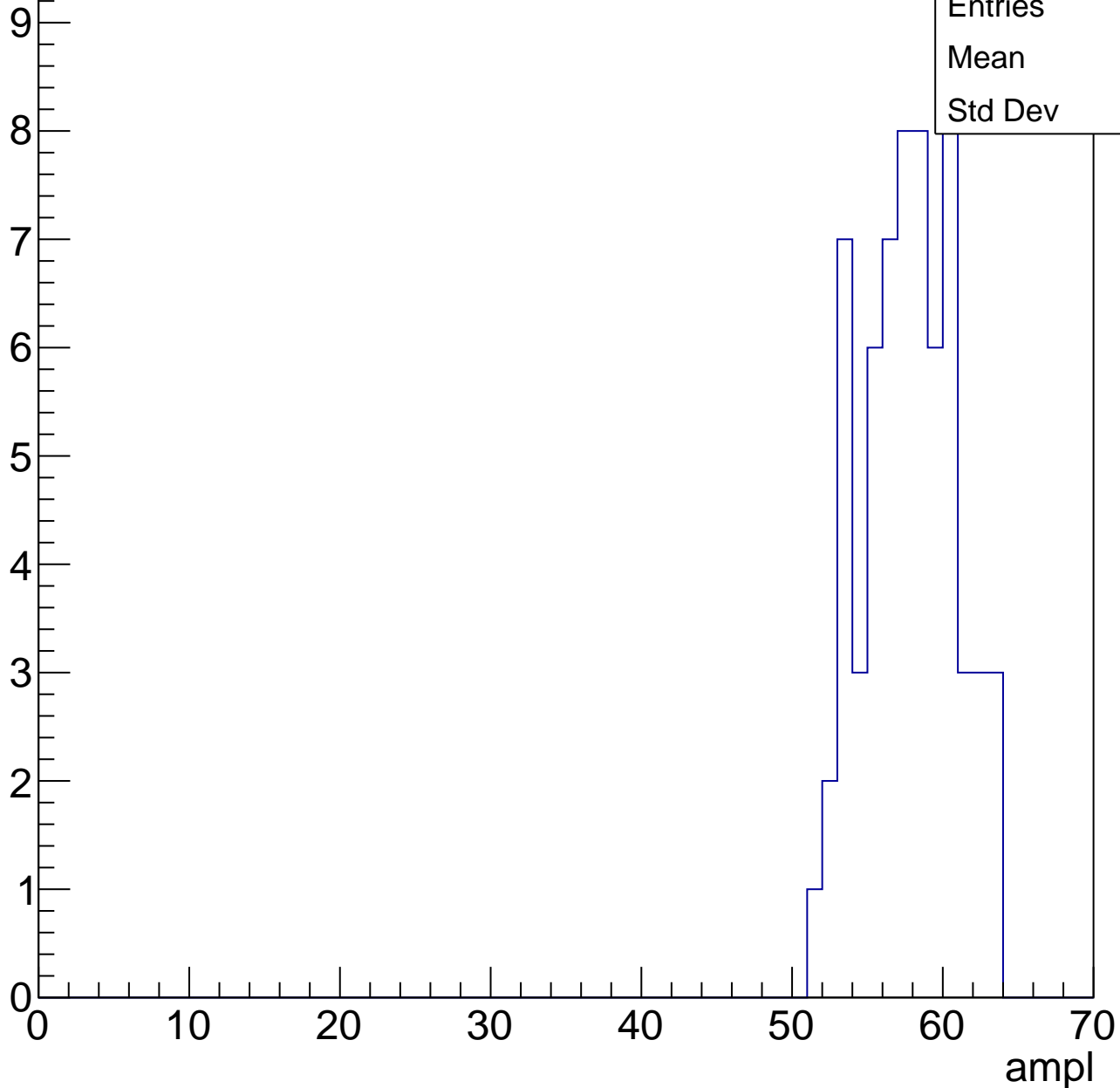


Entries	67
Mean	50.57
Std Dev	3.4

B0L002S, U12-ch63, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

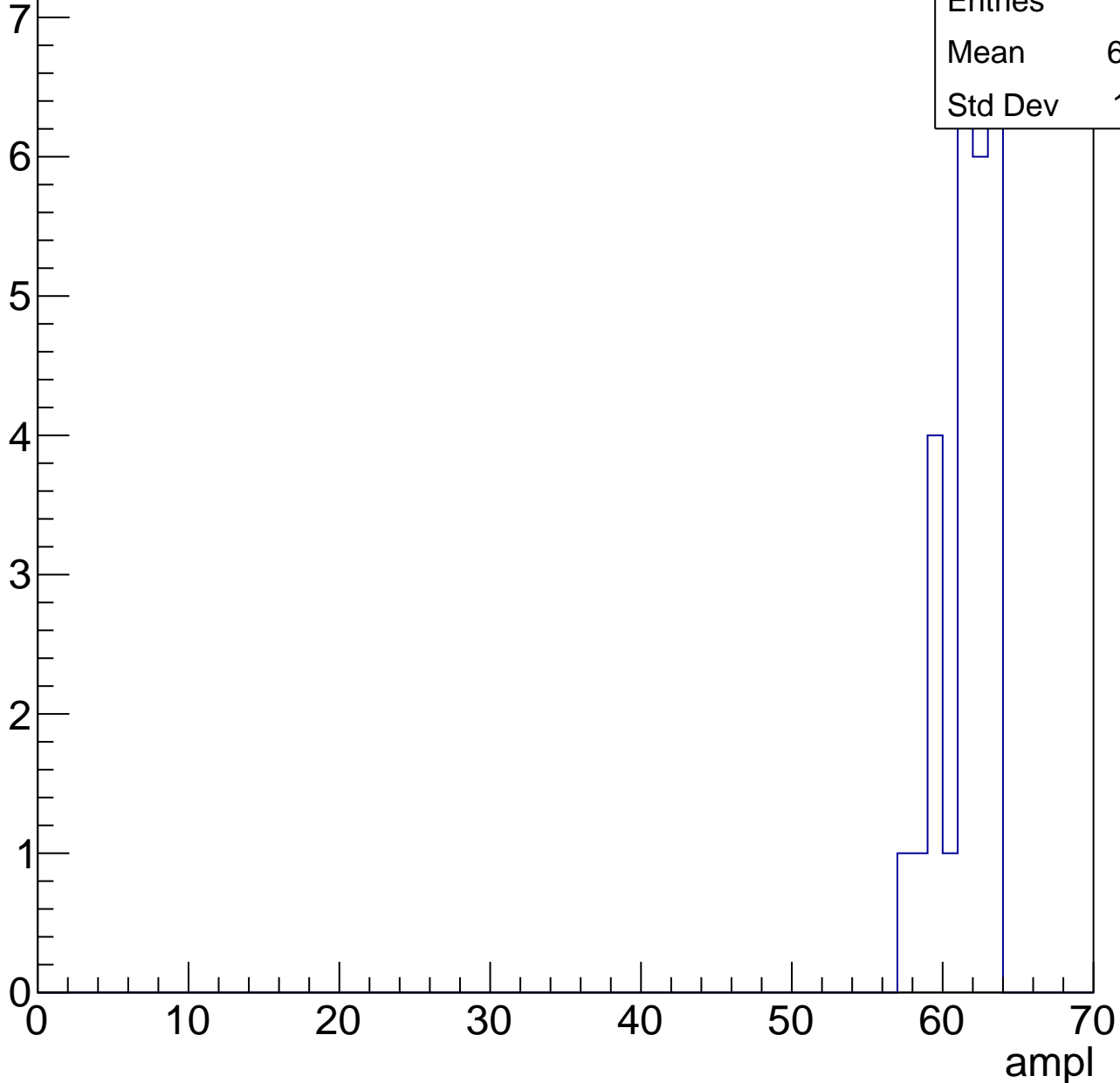


B0L002S, U12-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	61.15
Std Dev	1.671



B0L002S, U12-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch64, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	57
Mean	33.07
Std Dev	3.008

Gaus mean : 33.3570

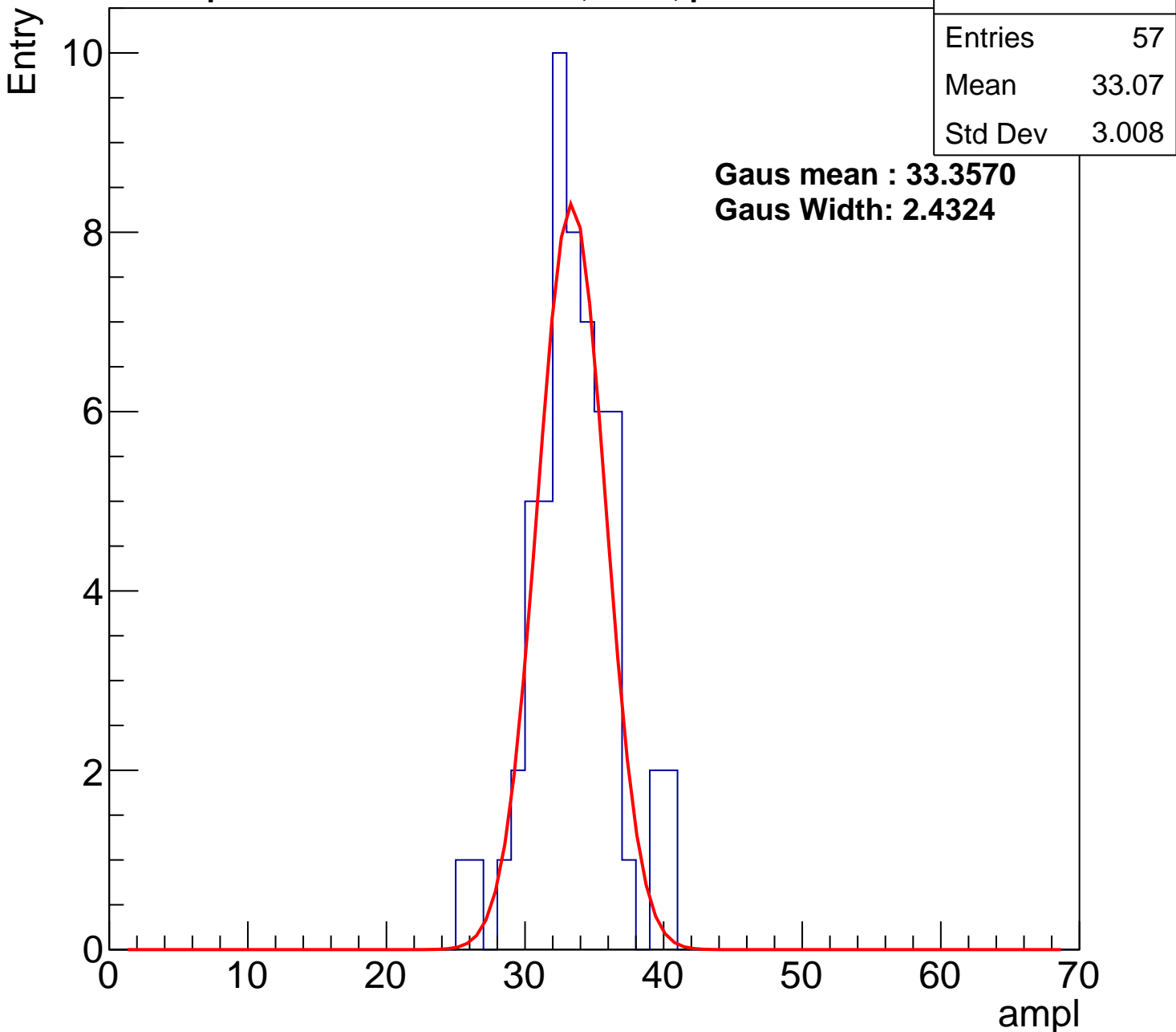
Gaus Width: 2.4324

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch64, adc1

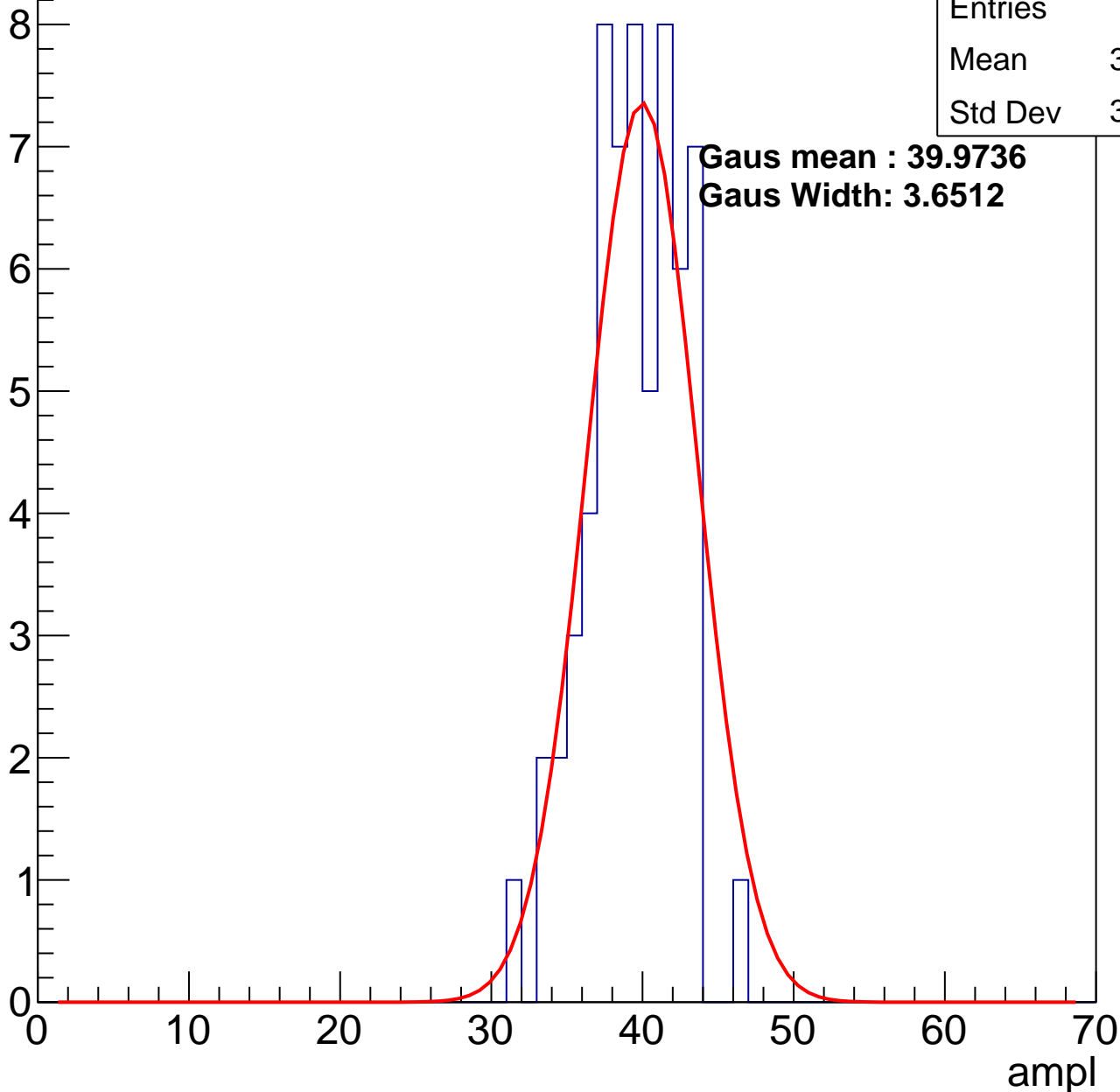
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.95
Std Dev	3.008

Gaus mean : 39.9736

Gaus Width: 3.6512



B0L002S, U12-ch64, adc2

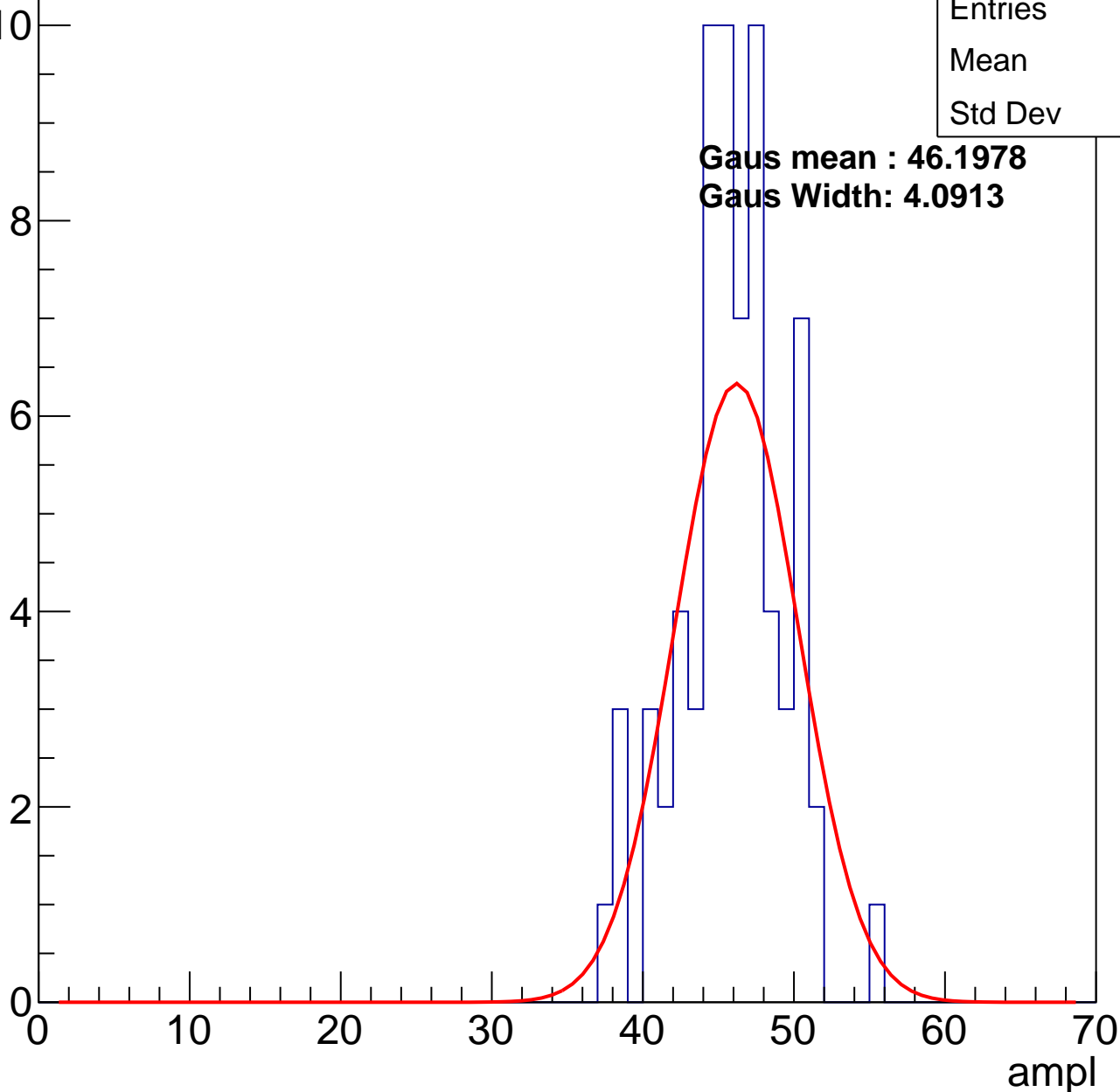
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	45.4
Std Dev	3.47

Gaus mean : 46.1978

Gaus Width: 4.0913

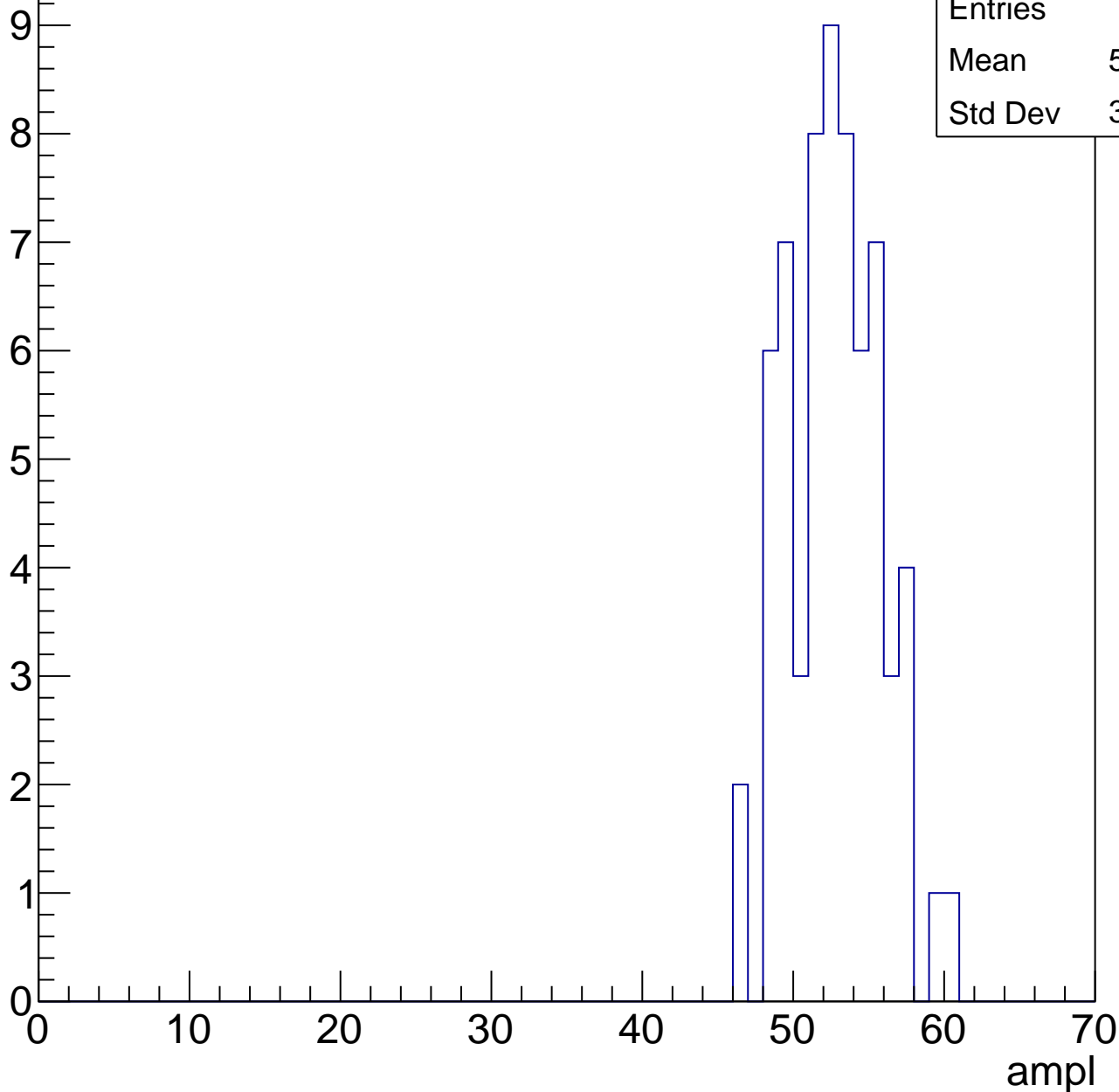


B0L002S, U12-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	52.26
Std Dev	3.045

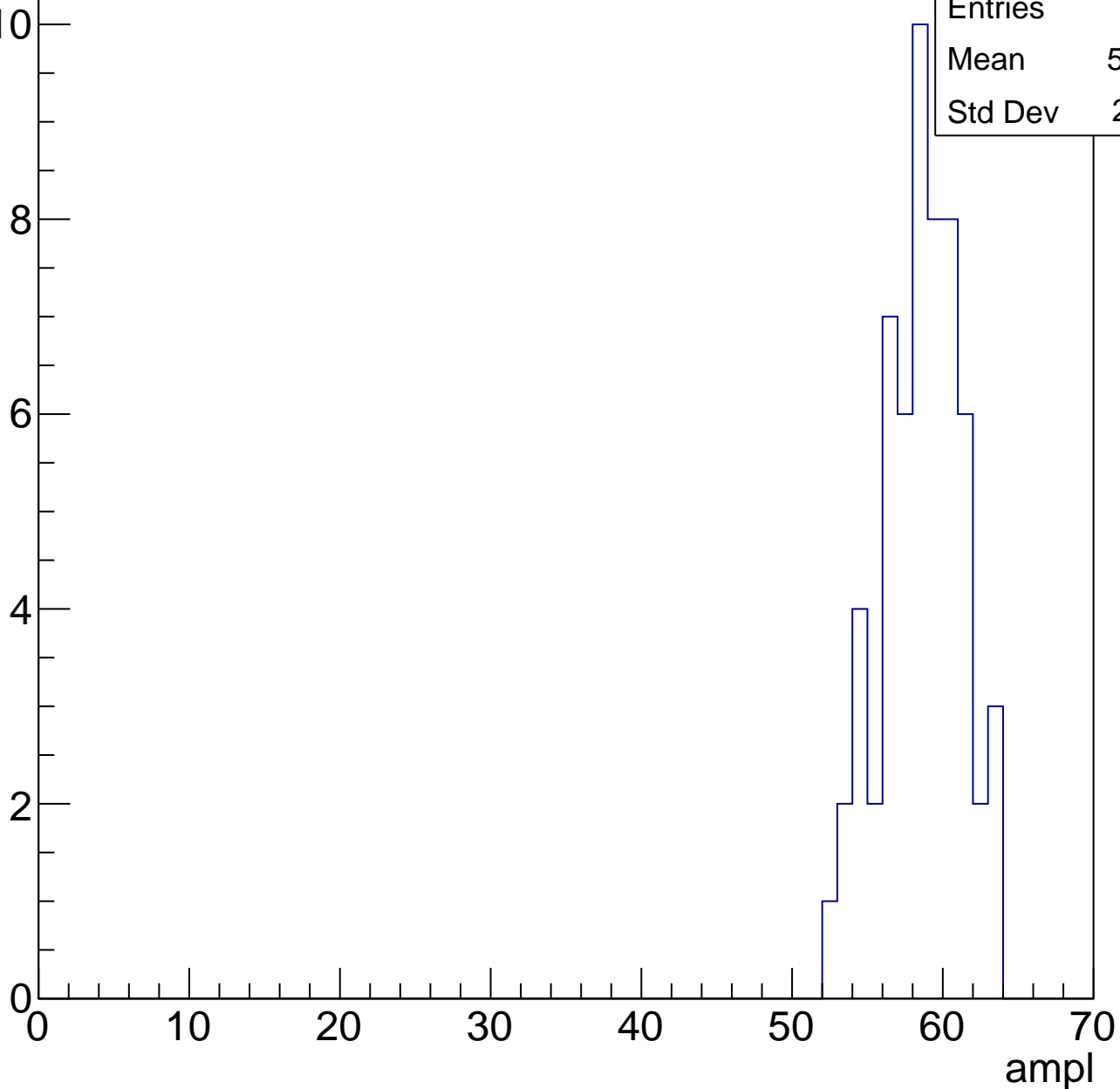


B0L002S, U12-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	58.12
Std Dev	2.611

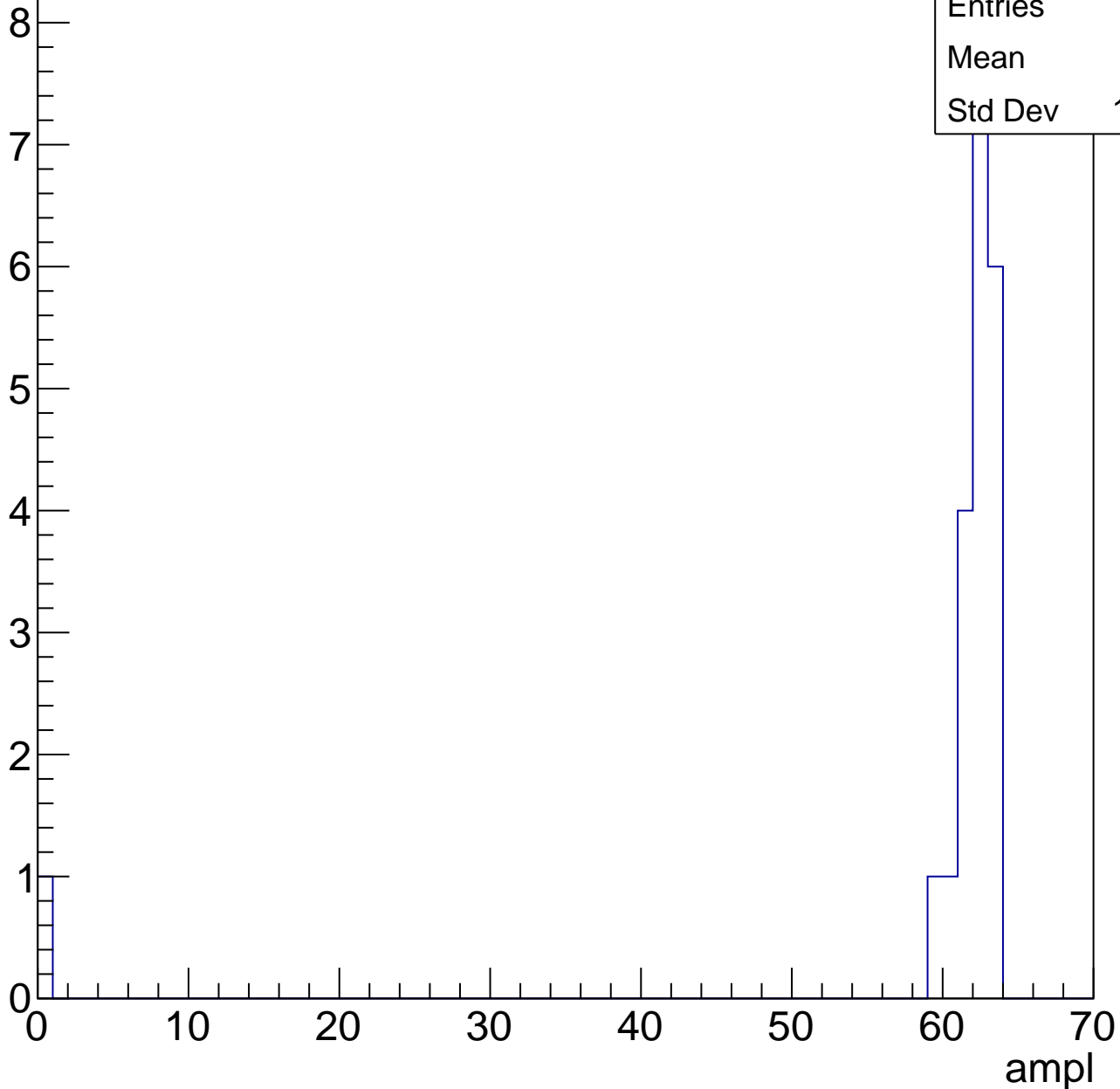


B0L002S, U12-ch64, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.9
Std Dev	13.21



B0L002S, U12-ch64, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L002S, U12-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch65, adc0

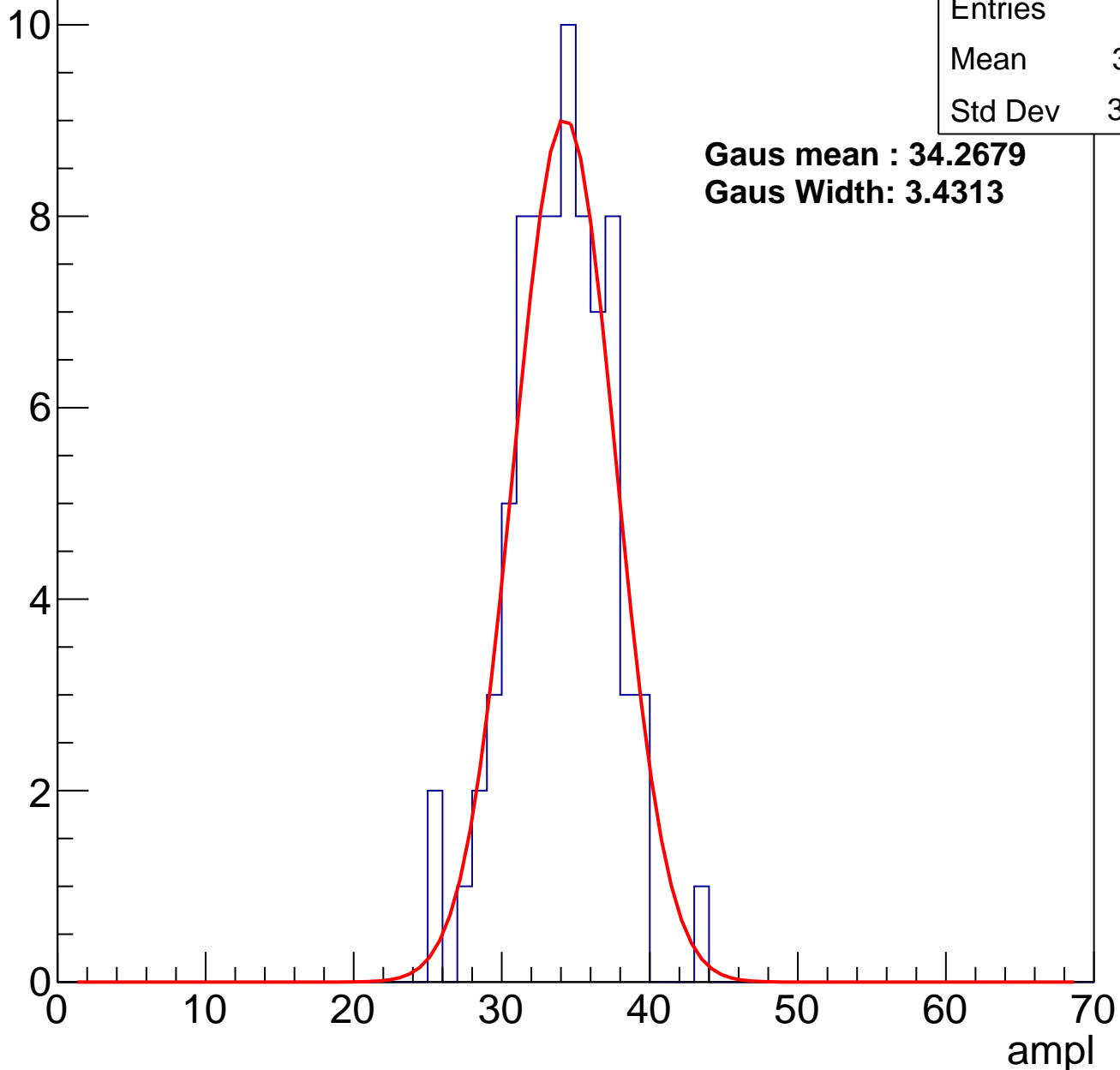
calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	33.51
Std Dev	3.309

Gaus mean : 34.2679

Gaus Width: 3.4313

Entry



B0L002S, U12-ch65, adc1

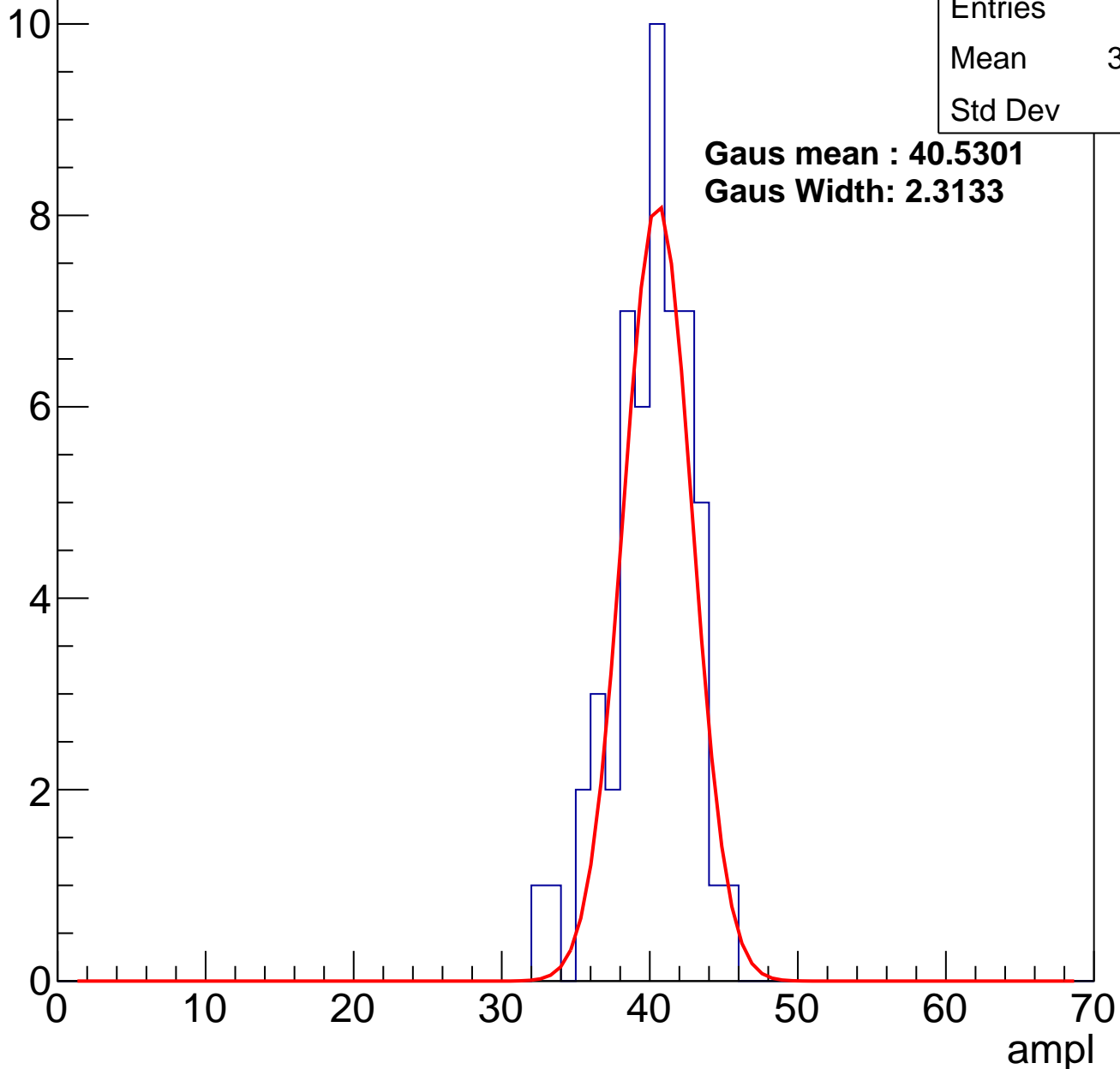
calib_packv5_042523_0143.root, FC#8, port C1

Entries	53
Mean	39.66
Std Dev	2.67

Gaus mean : 40.5301

Gaus Width: 2.3133

Entry



B0L002S, U12-ch65, adc2

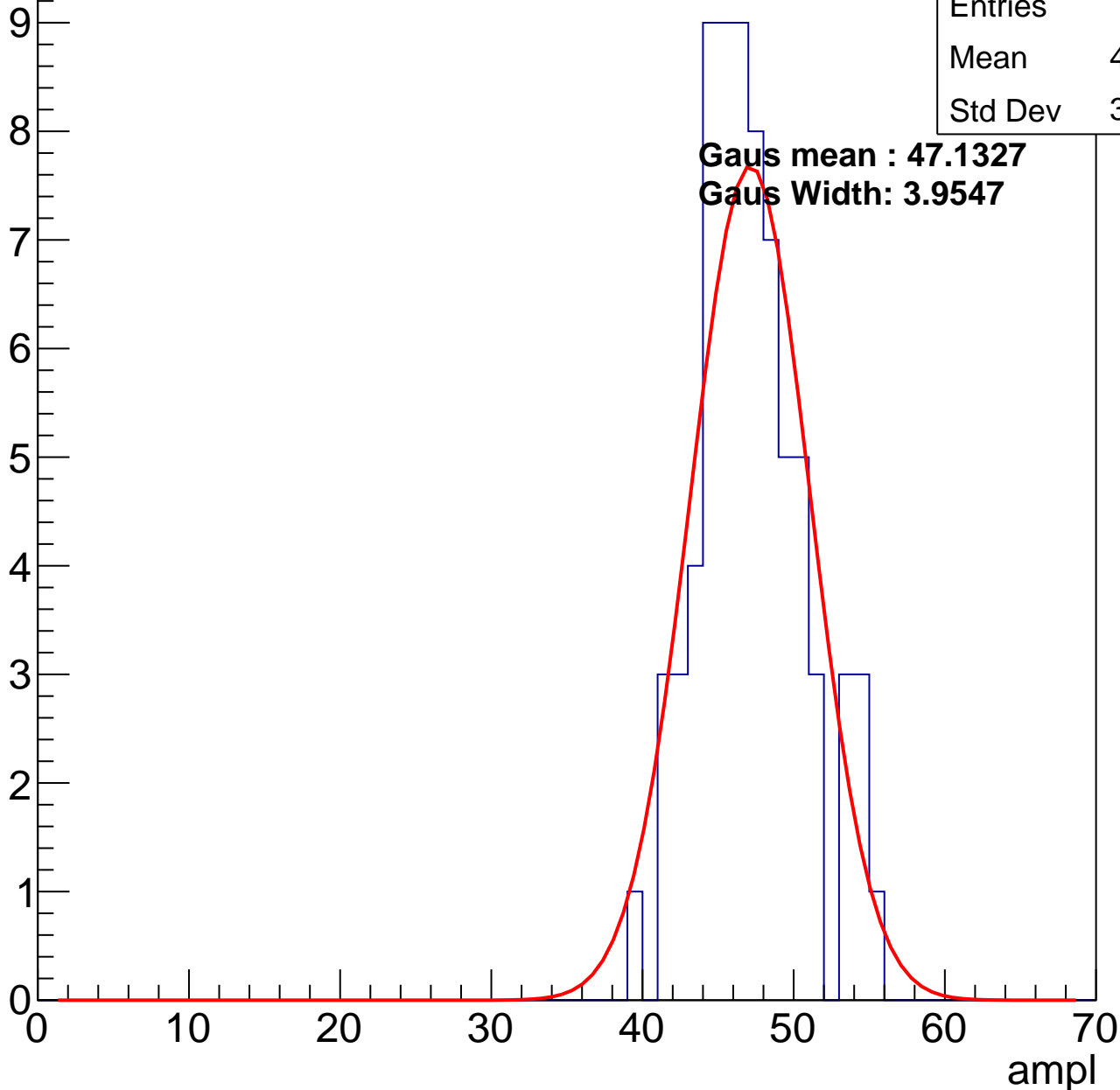
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	46.73
Std Dev	3.453

Gaus mean : 47.1327

Gaus Width: 3.9547

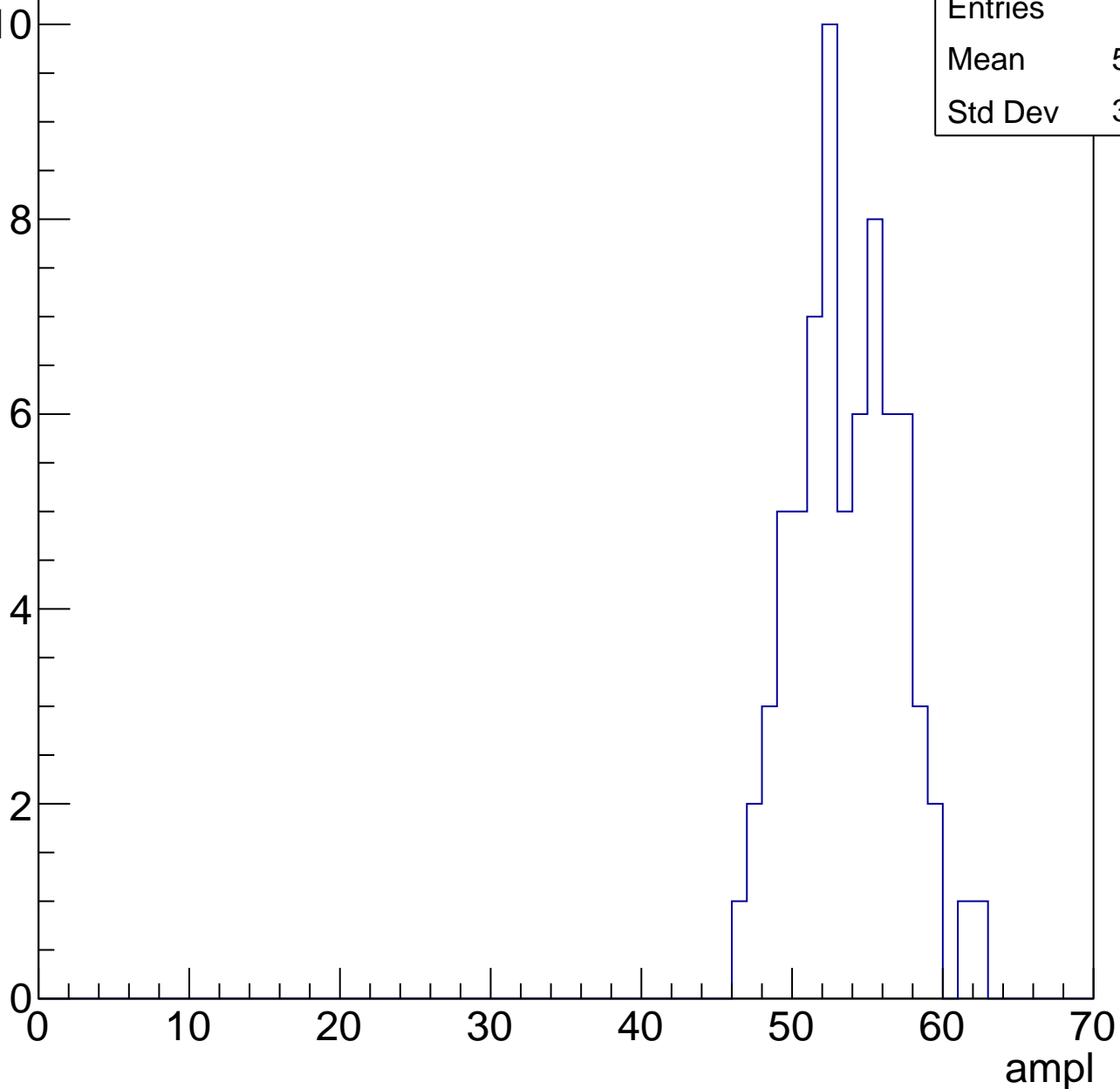


B0L002S, U12-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	53.21
Std Dev	3.431



B0L002S, U12-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 59.06

Std Dev 2.577

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

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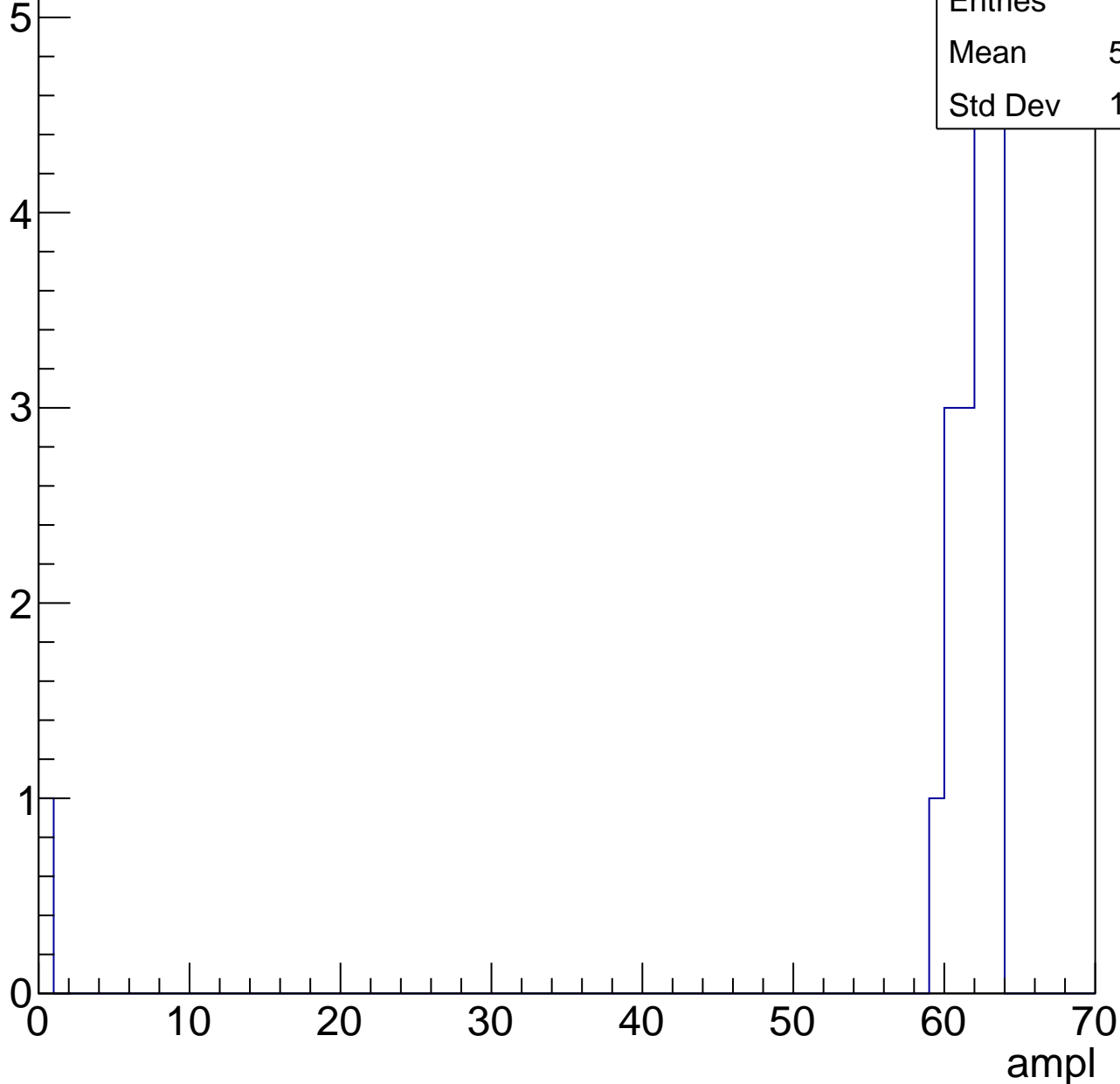
322

B0L002S, U12-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	18
Mean	58.17
Std Dev	14.16



B0L002S, U12-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch66, adc0

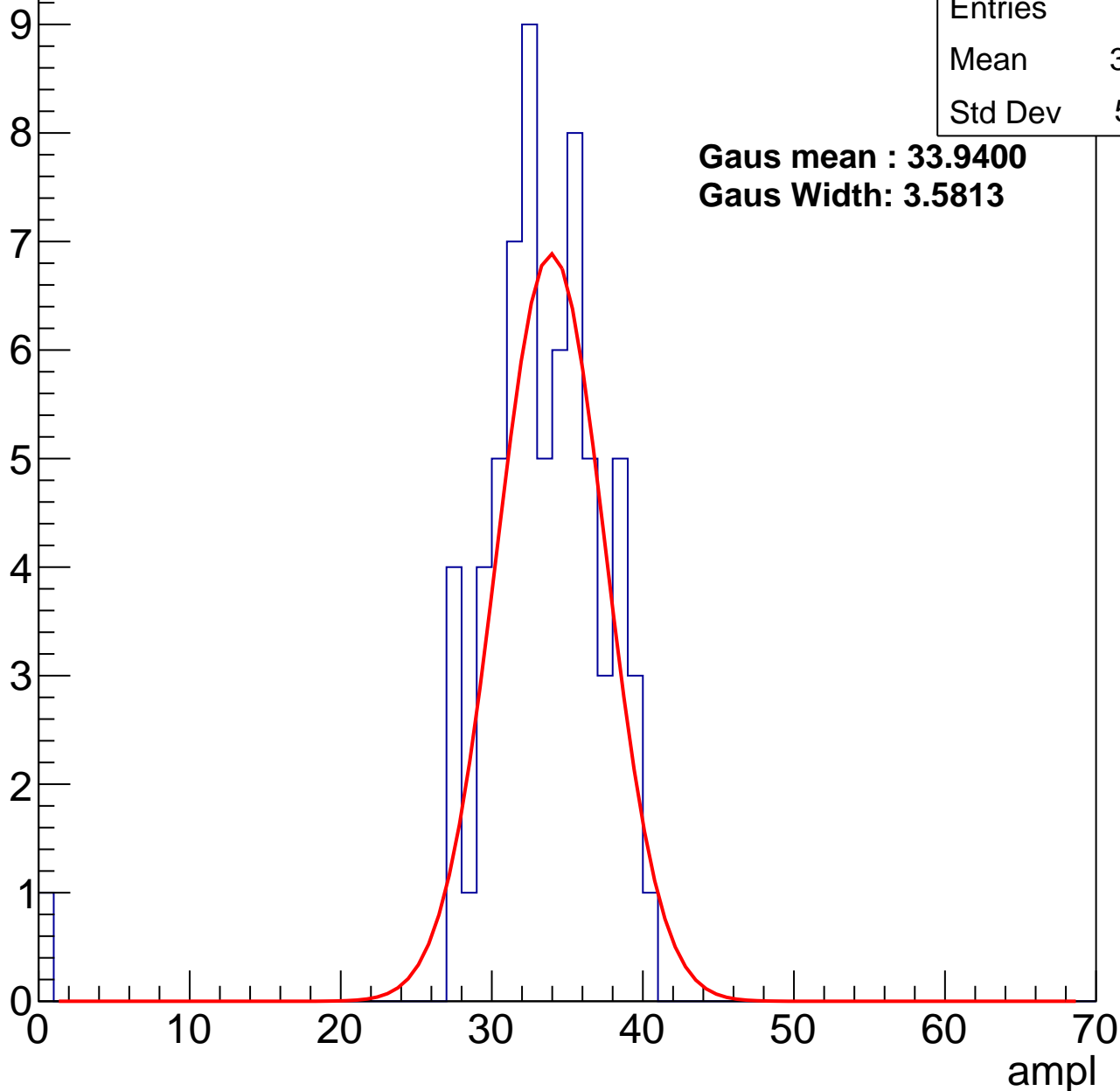
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	32.75
Std Dev	5.201

Gaus mean : 33.9400

Gaus Width: 3.5813



B0L002S, U12-ch66, adc1

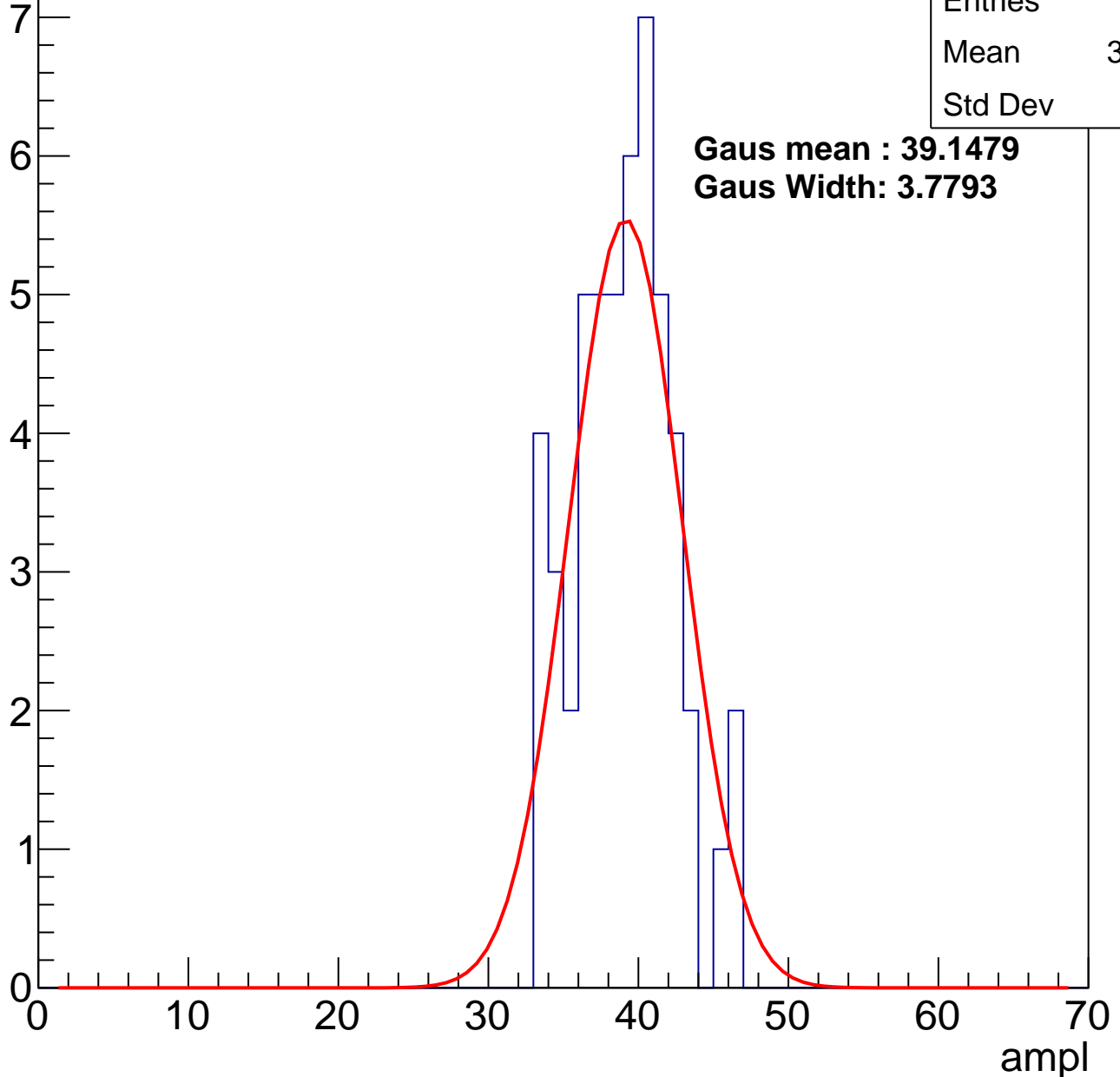
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	38.61
Std Dev	3.26

Gaus mean : 39.1479

Gaus Width: 3.7793



B0L002S, U12-ch66, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	45.68
Std Dev	3.595

Gaus mean : 46.2371

Gaus Width: 3.7363

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

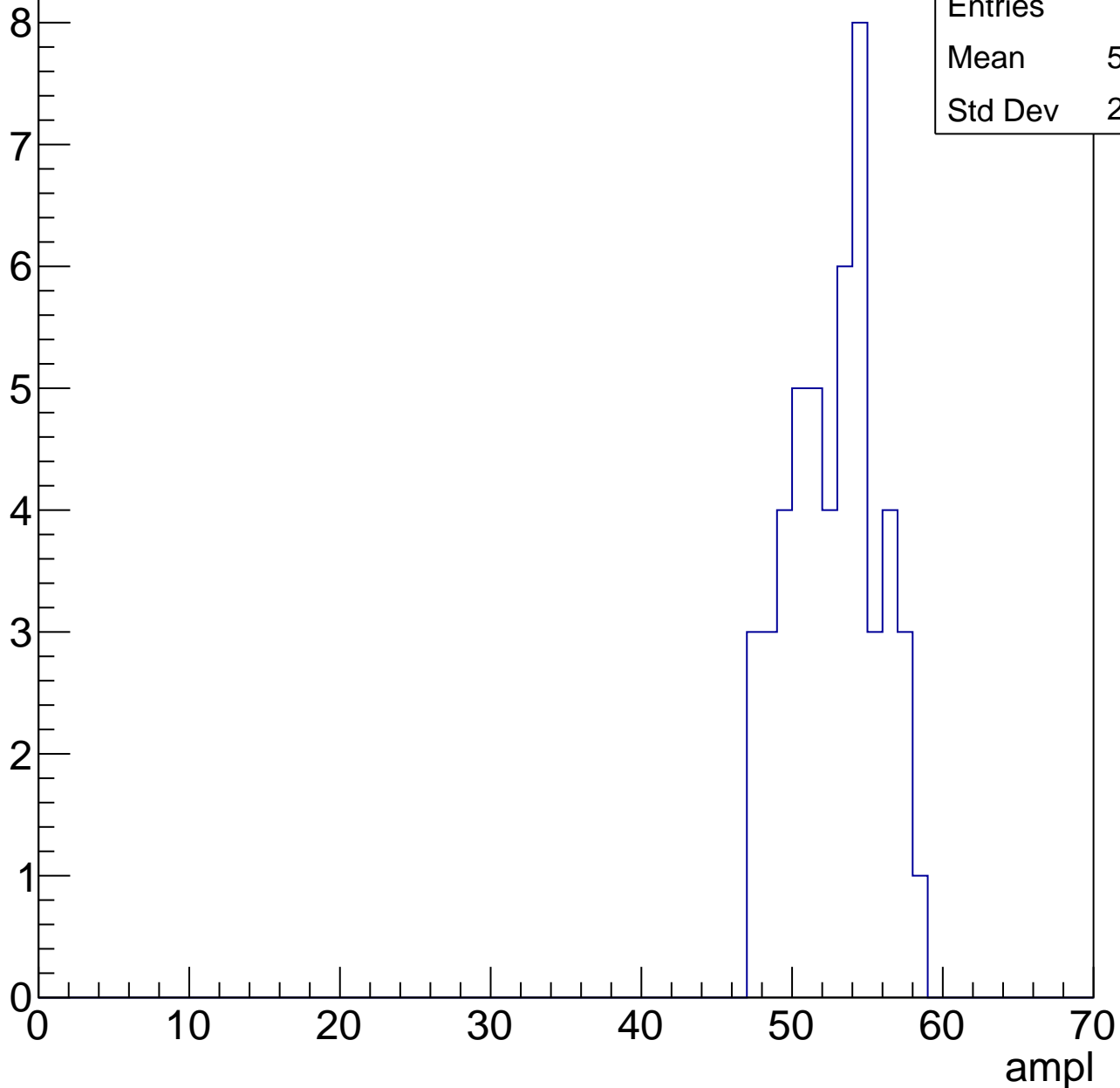
70

B0L002S, U12-ch66, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	52.29
Std Dev	2.928

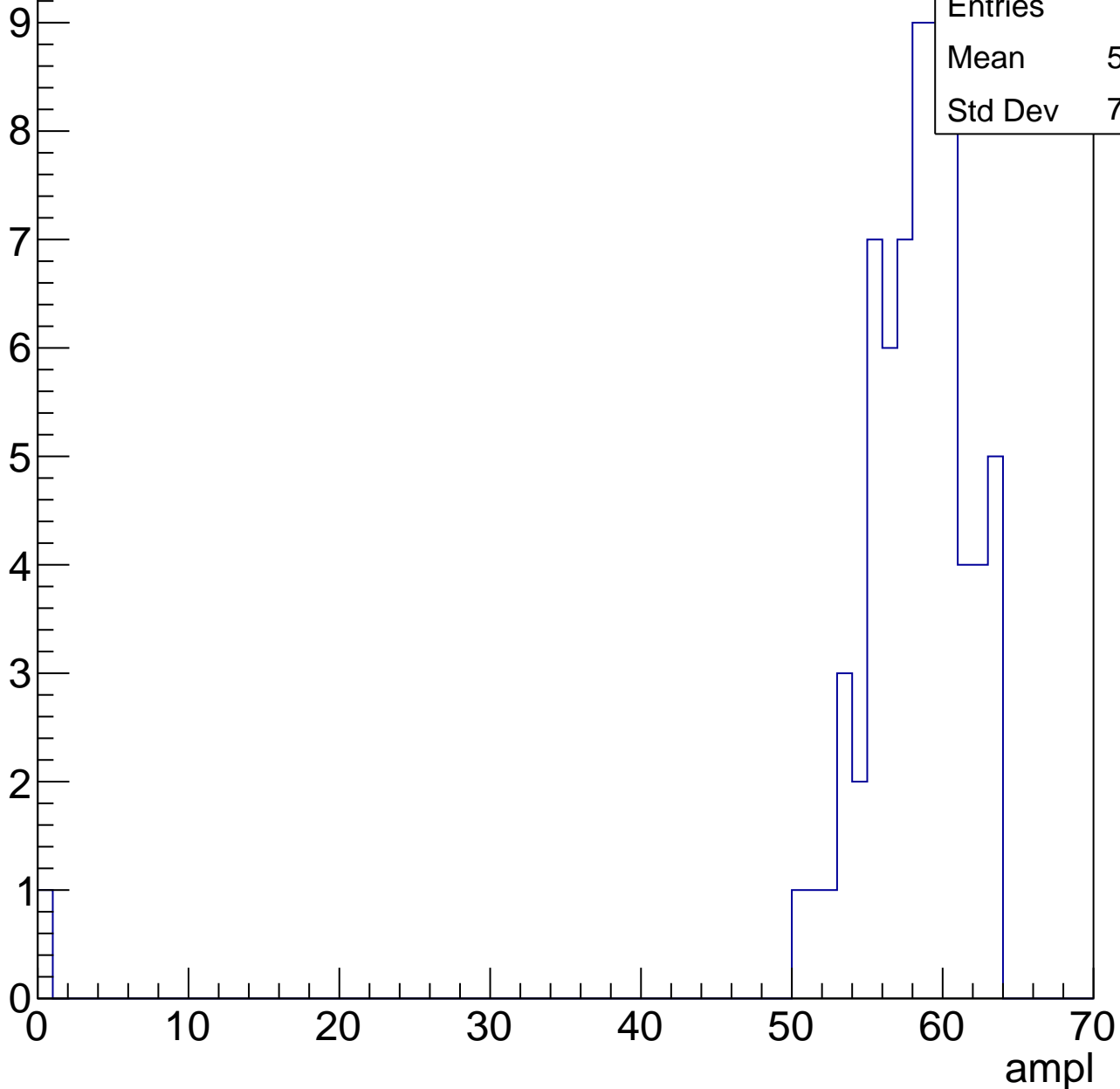


B0L002S, U12-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

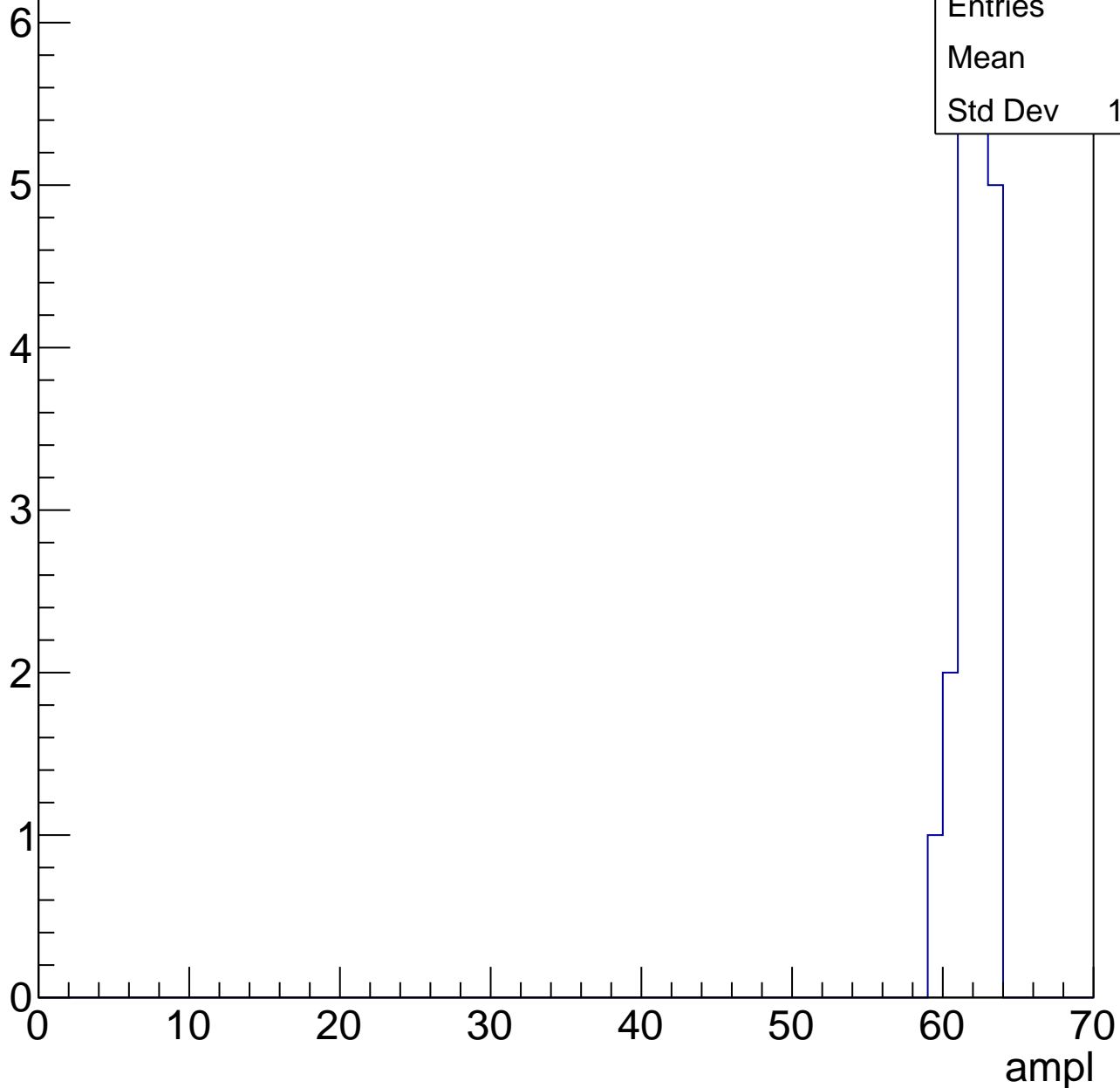
Entries	68
Mean	57.06
Std Dev	7.592



B0L002S, U12-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	20
Mean	61.6
Std Dev	1.114

B0L002S, U12-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U12-ch67, adc0

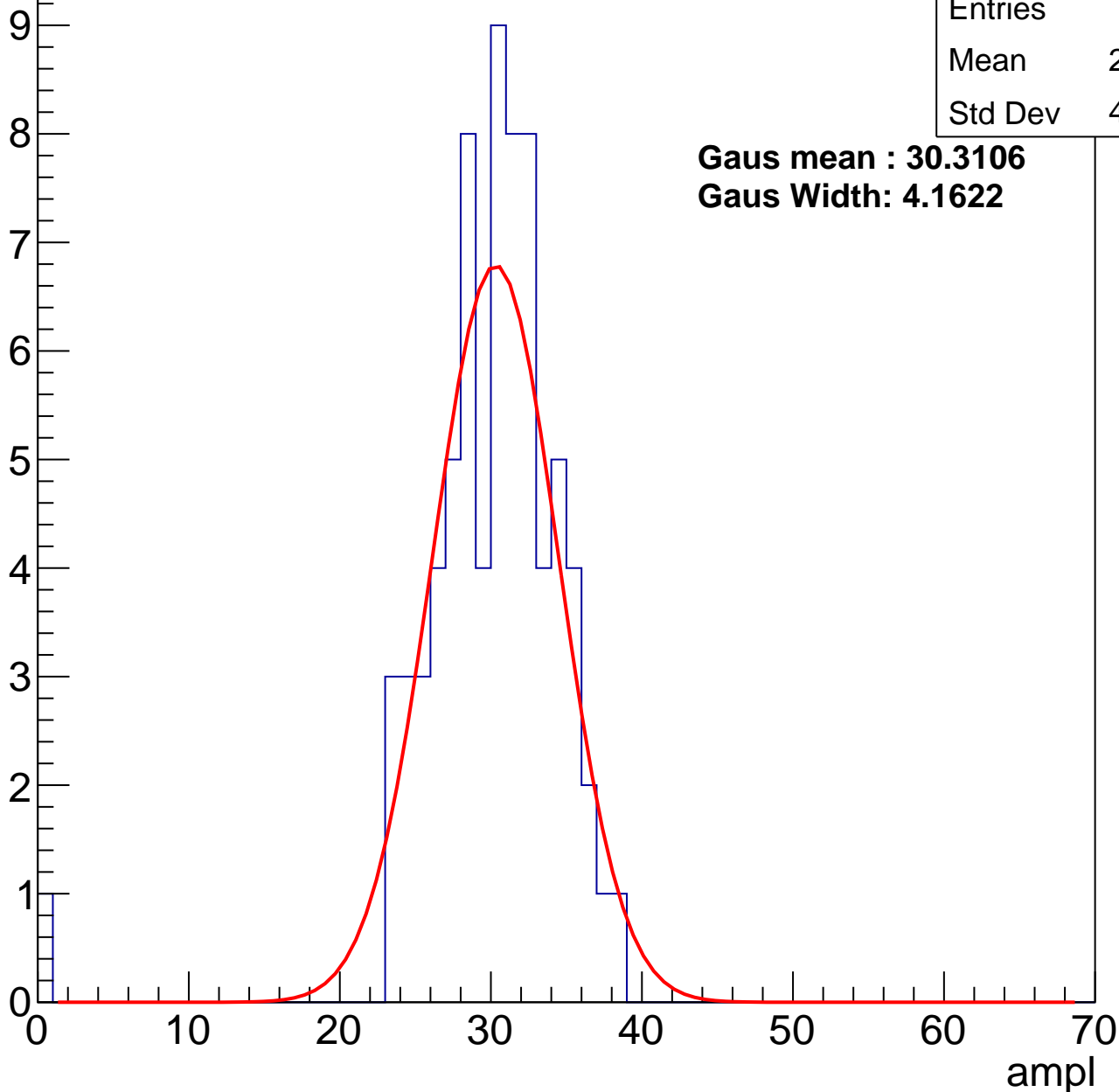
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.56
Std Dev	4.968

Gaus mean : 30.3106

Gaus Width: 4.1622



B0L002S, U12-ch67, adc1

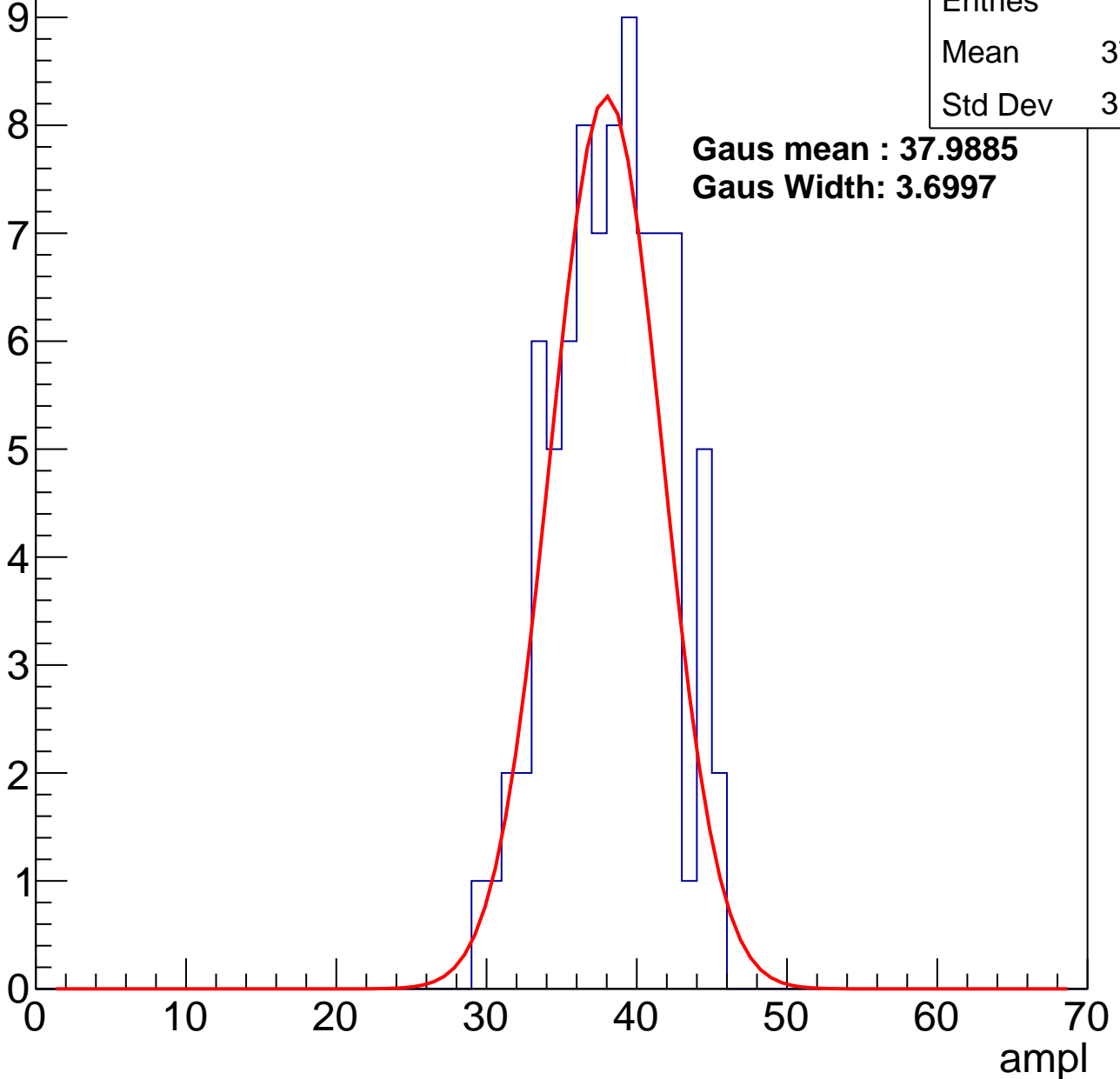
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	37.85
Std Dev	3.702

Gaus mean : 37.9885

Gaus Width: 3.6997



B0L002S, U12-ch67, adc2

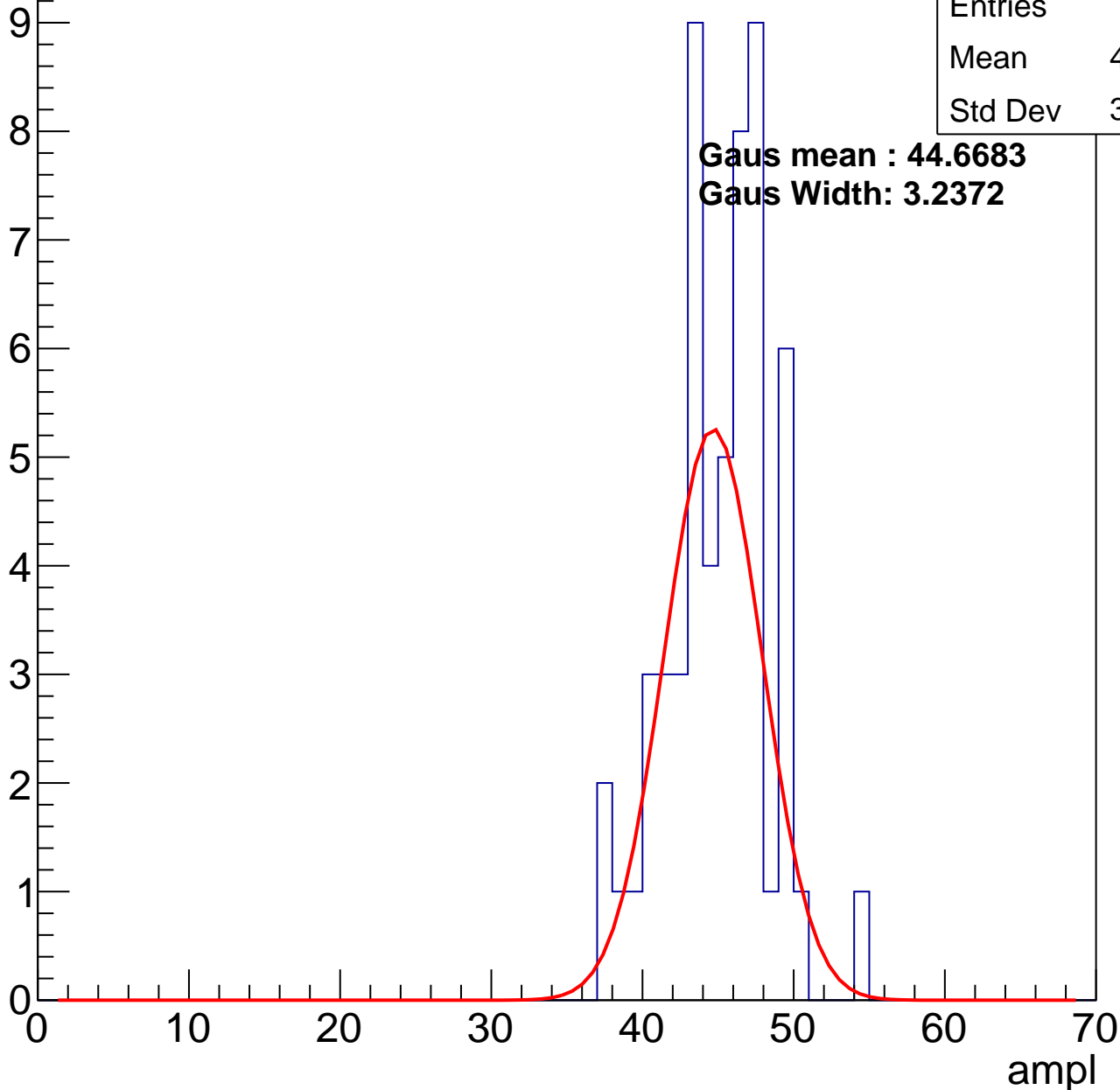
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.65
Std Dev	3.379

Gaus mean : 44.6683

Gaus Width: 3.2372



B0L002S, U12-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	51.5
Std Dev	3.358

Entry

10

8

6

4

2

0

0

10

20

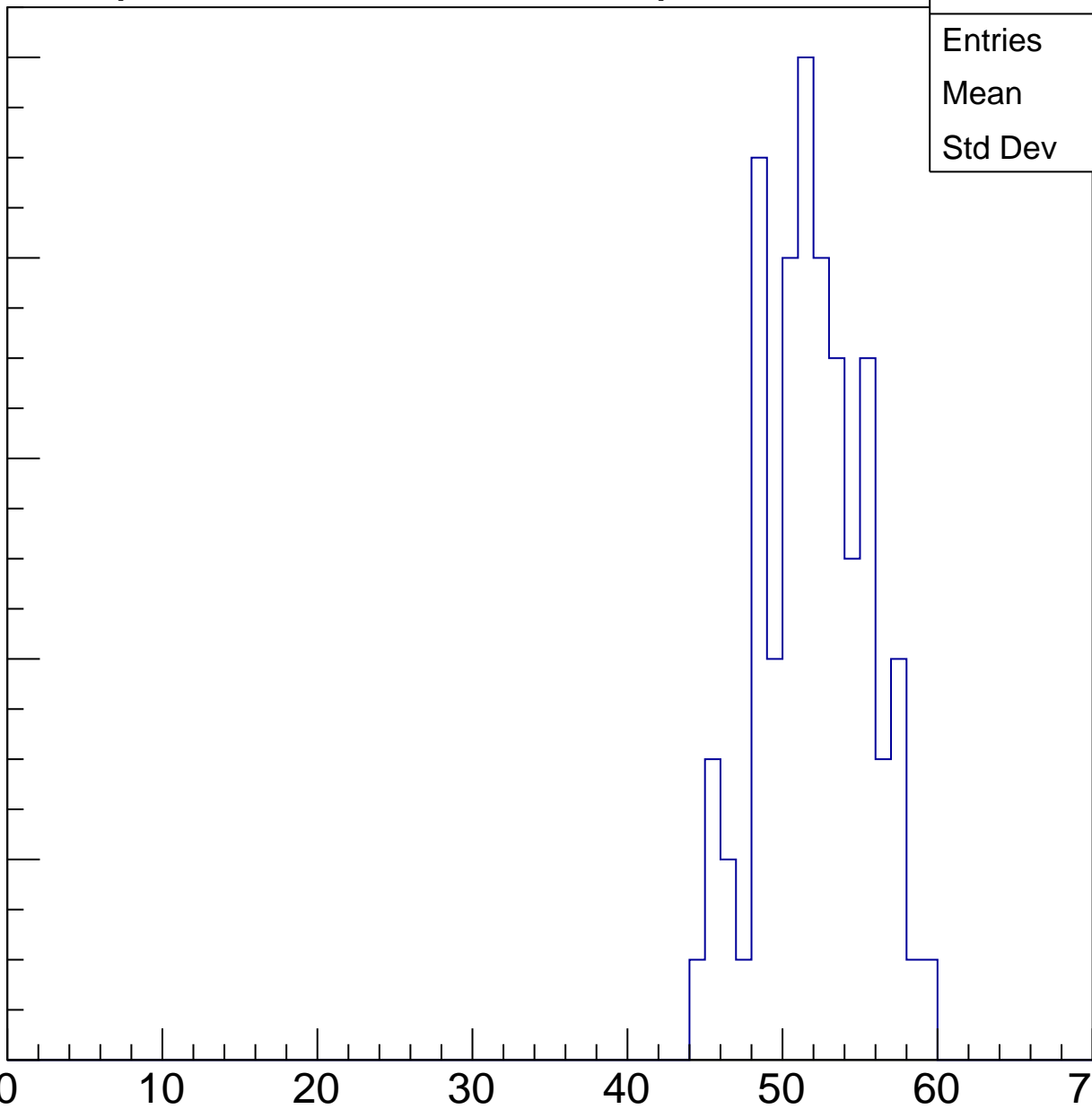
30

40

50

60

ampl

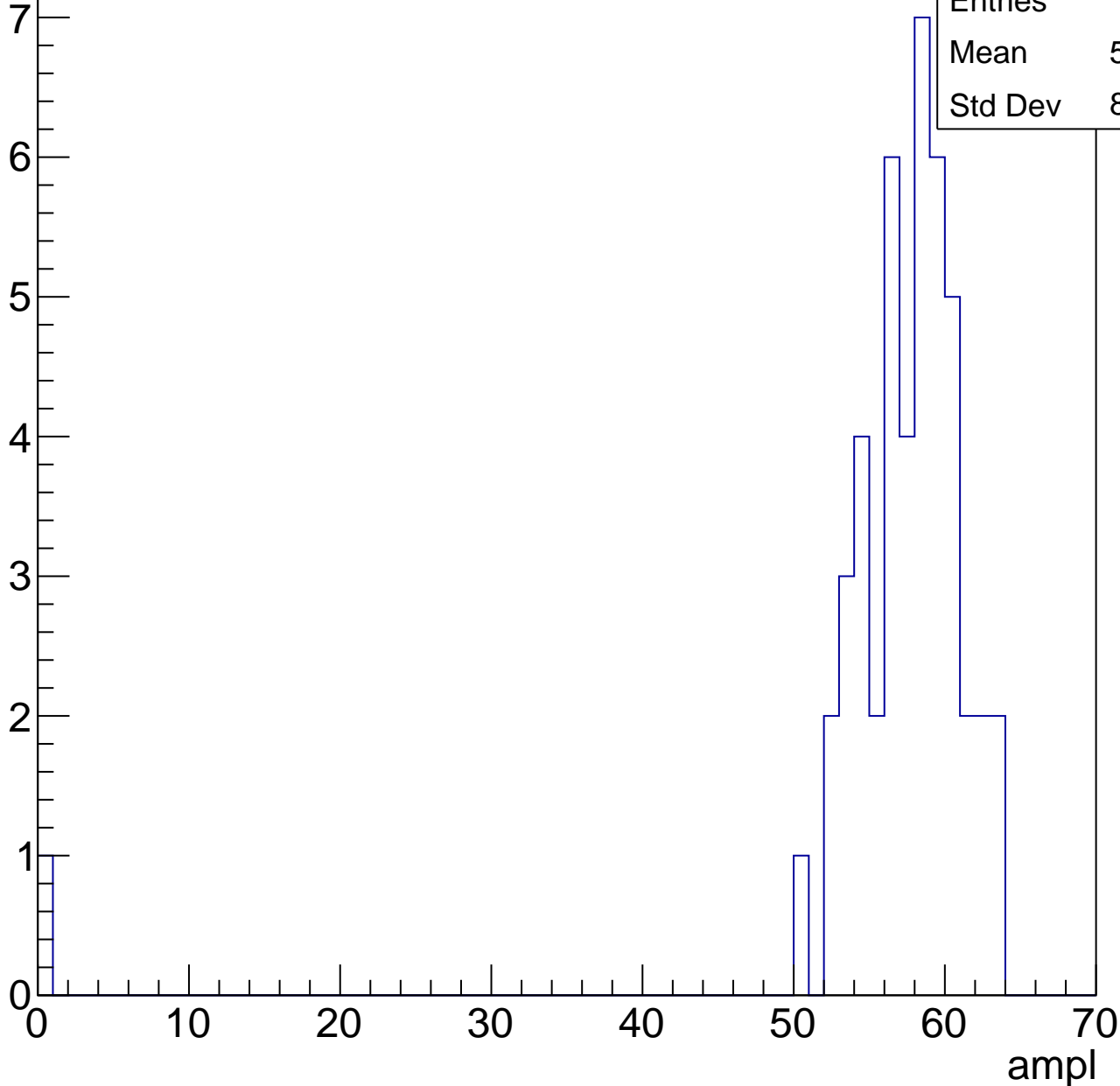


B0L002S, U12-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	56.06
Std Dev	8.794

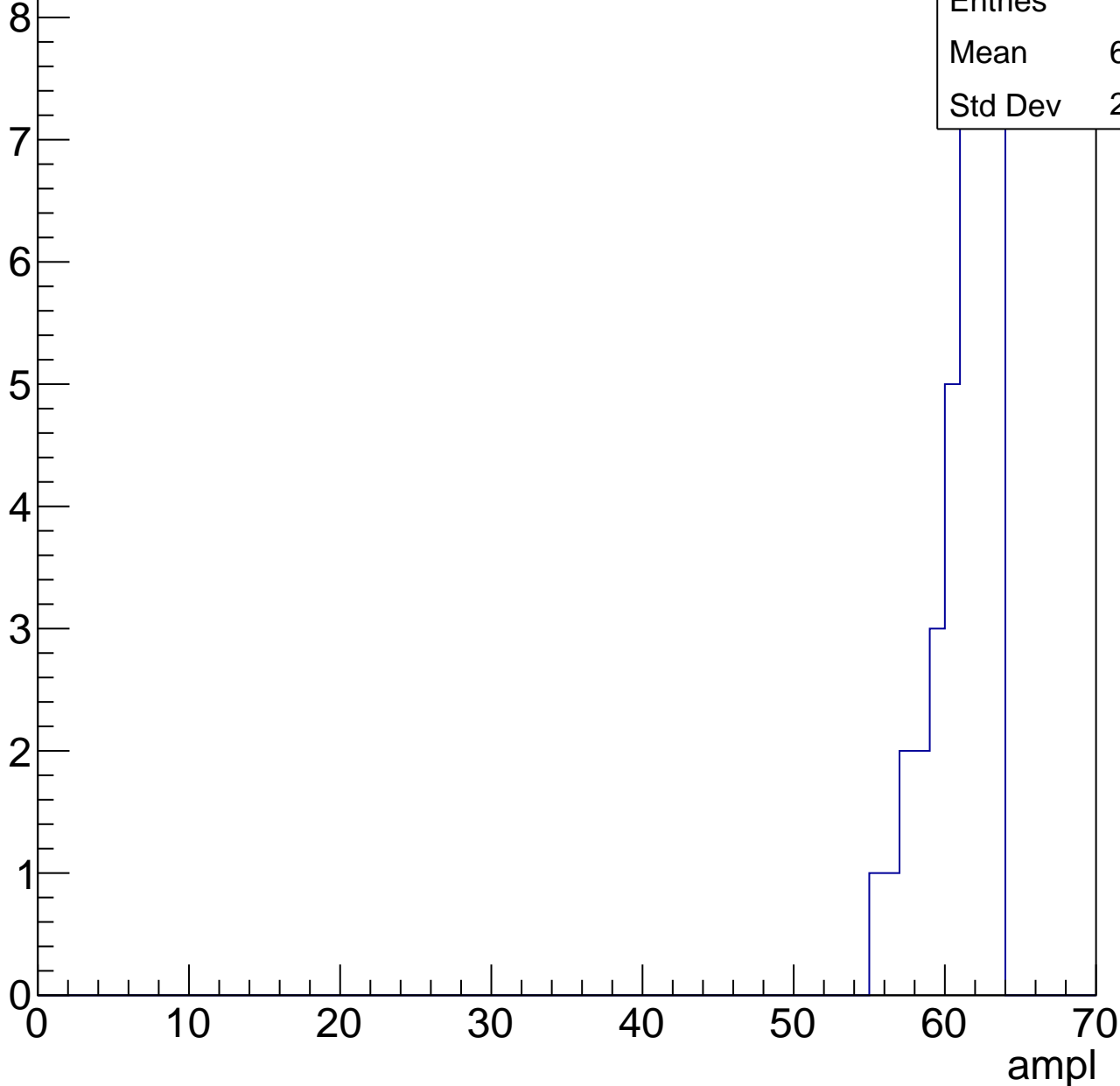


B0L002S, U12-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	60.68
Std Dev	2.079



B0L002S, U12-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B0L002S, U12-ch68, adc0

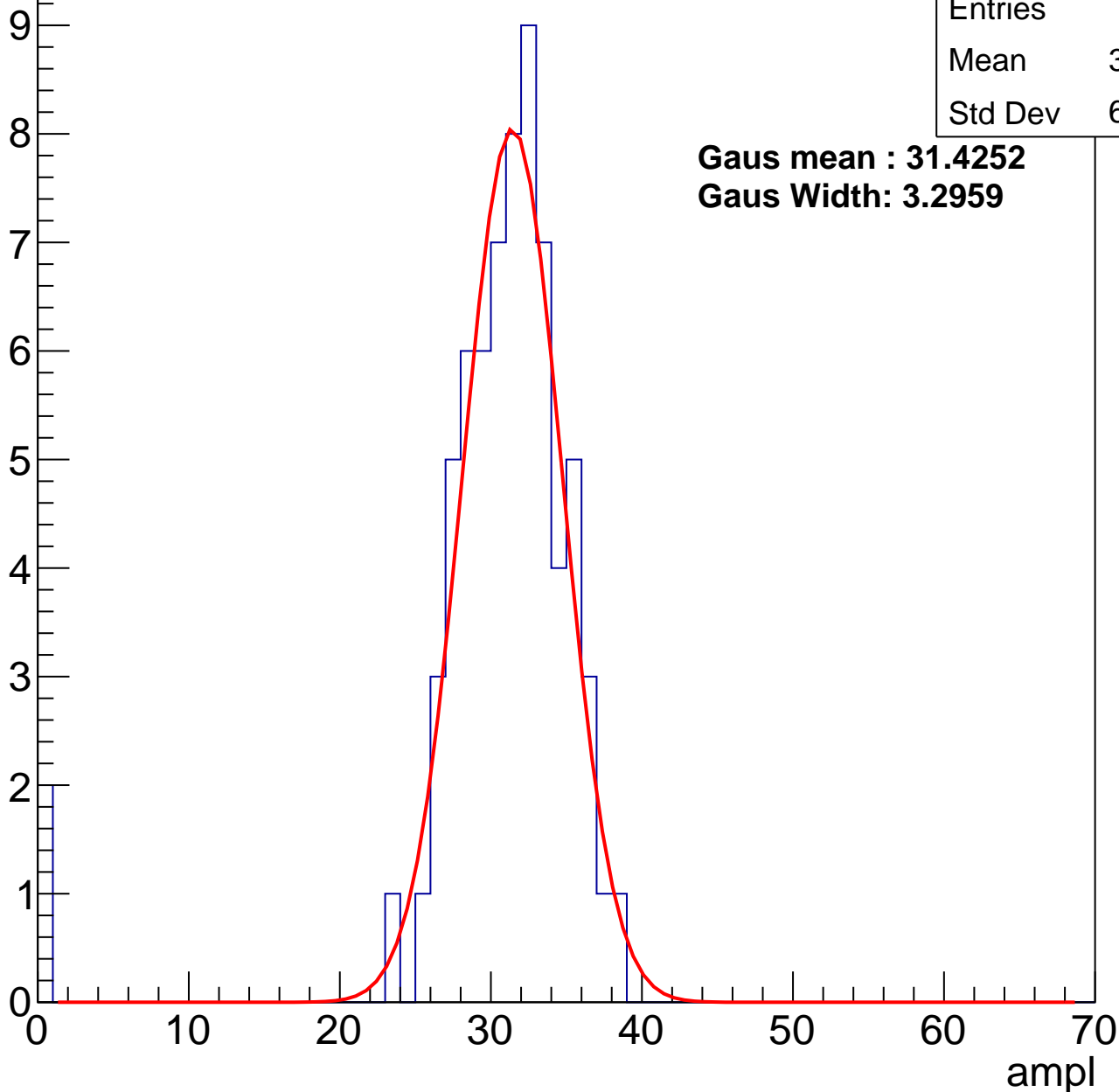
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.06
Std Dev	6.036

Gaus mean : 31.4252

Gaus Width: 3.2959



B0L002S, U12-ch68, adc1

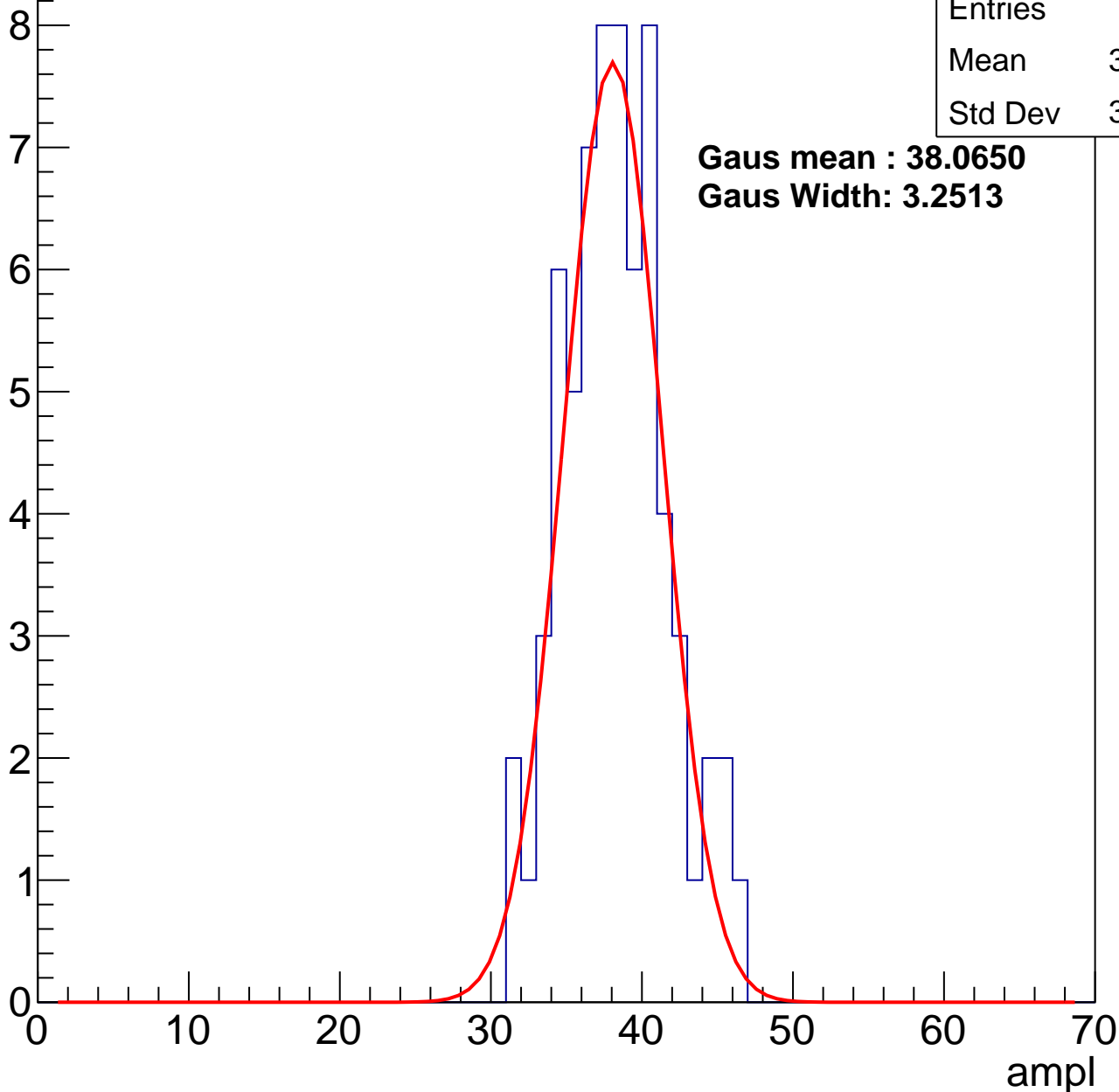
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.84
Std Dev	3.379

Gaus mean : 38.0650

Gaus Width: 3.2513



B0L002S, U12-ch68, adc2

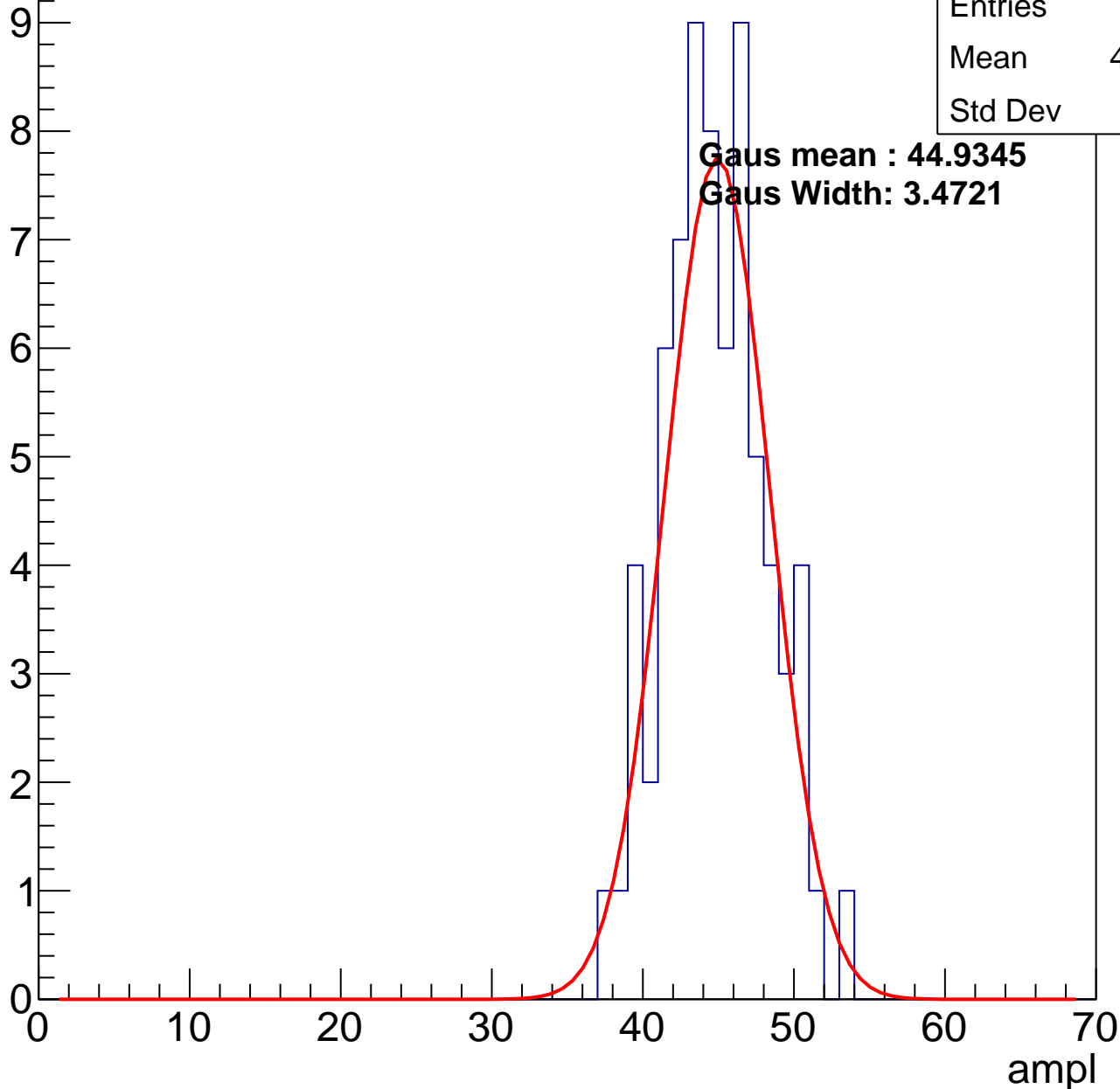
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.39
Std Dev	3.35

Gaus mean : 44.9345

Gaus Width: 3.4721

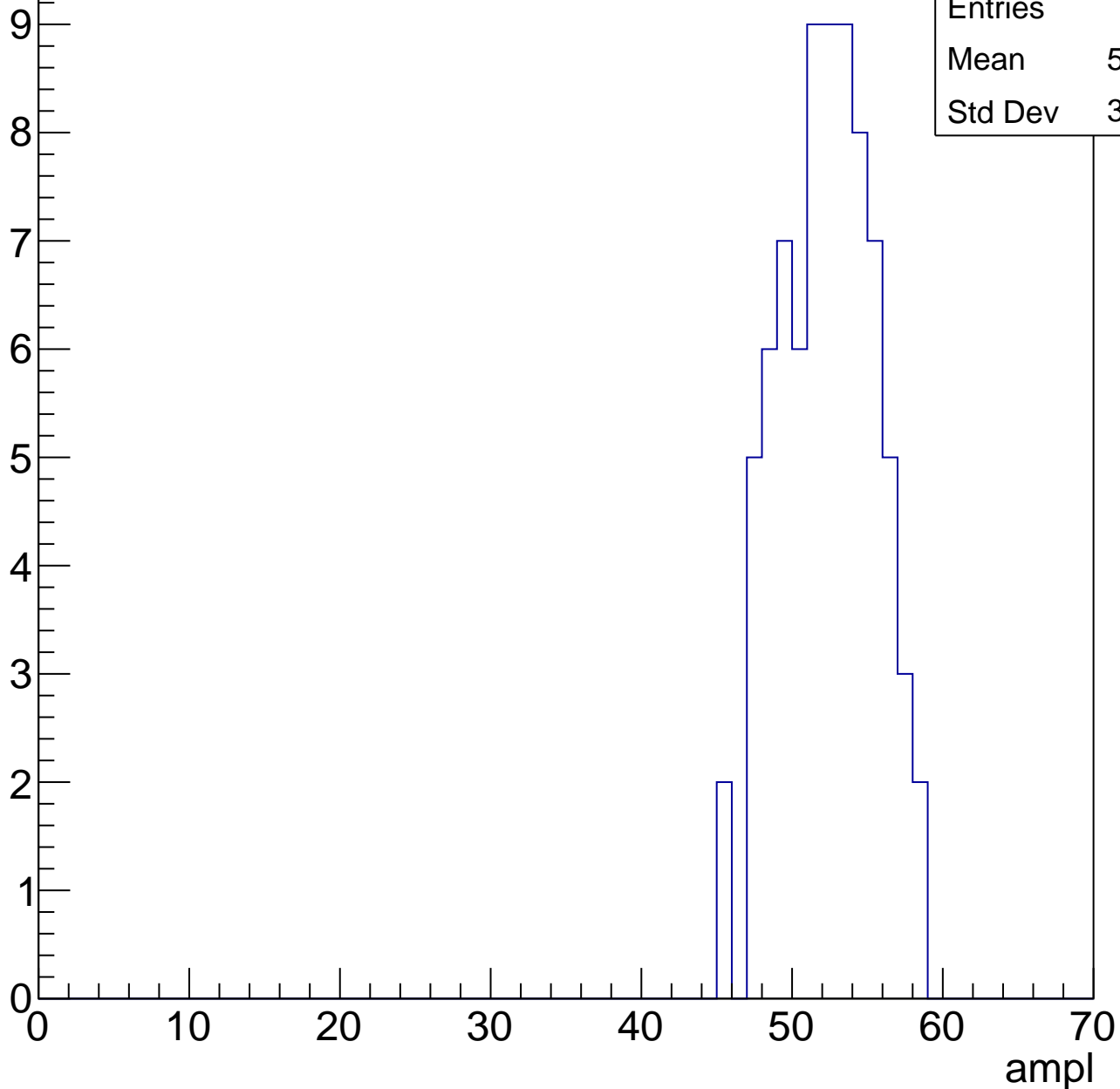


B0L002S, U12-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	51.85
Std Dev	3.089

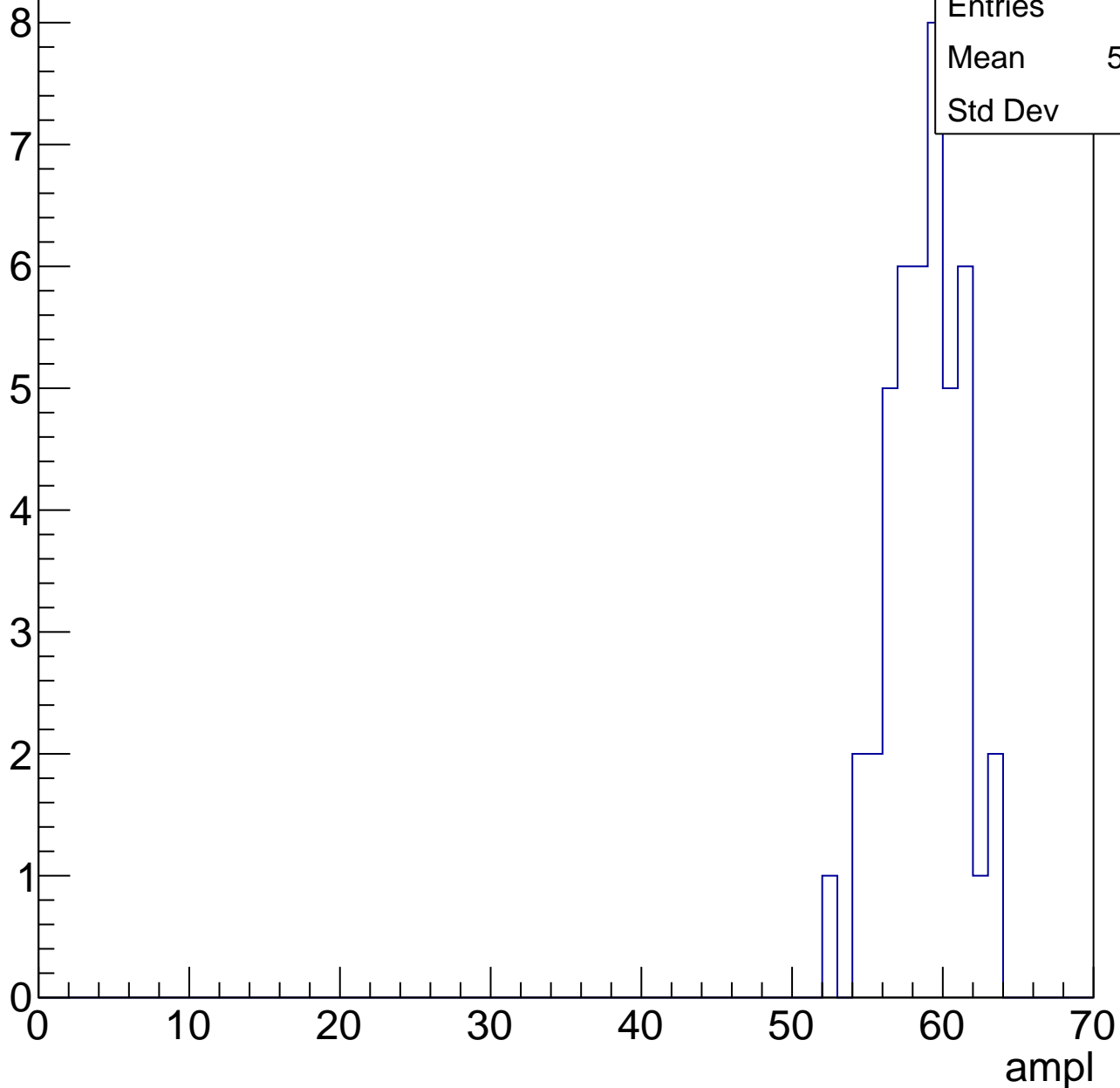


B0L002S, U12-ch68, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	58.32
Std Dev	2.41

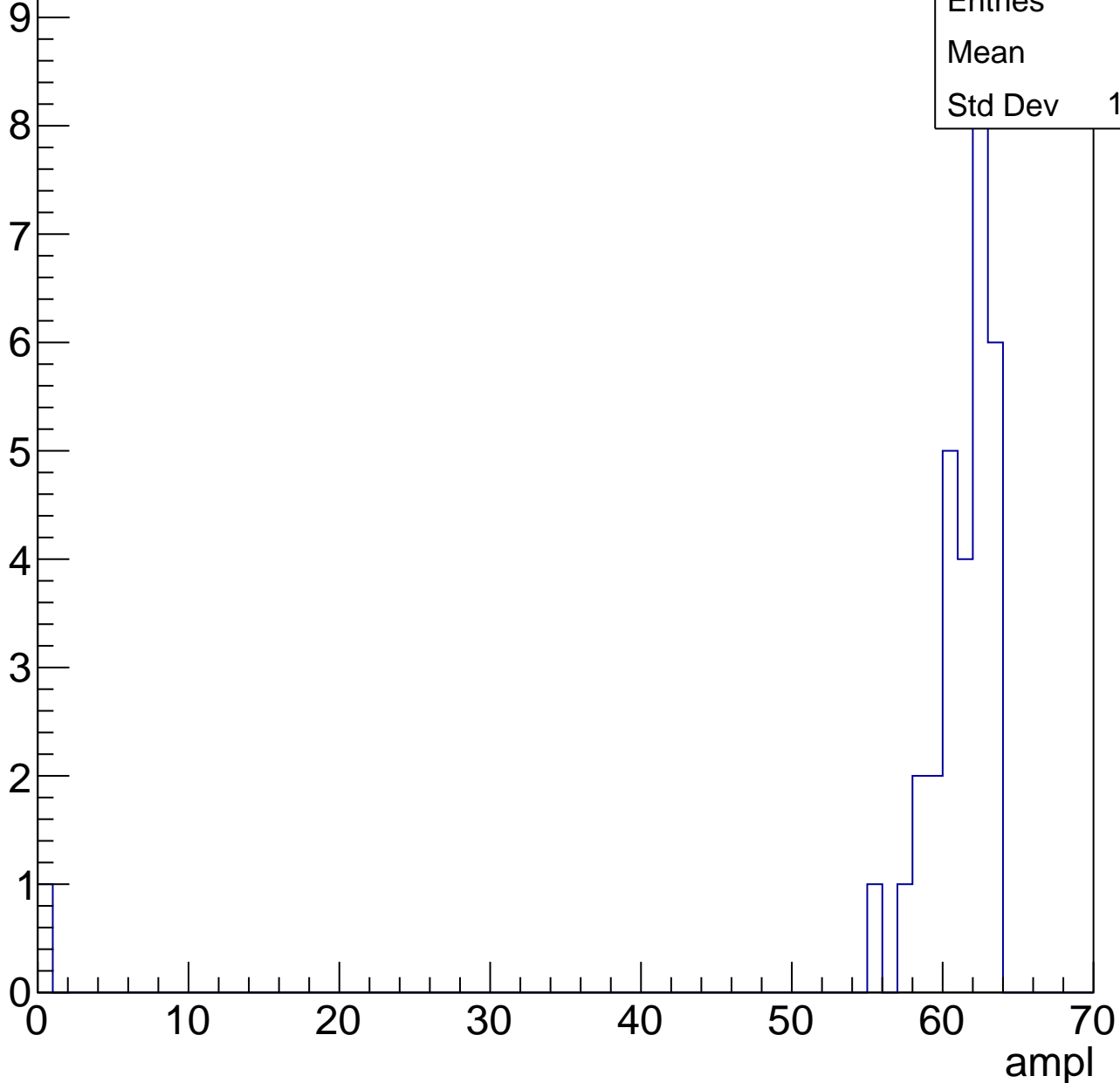


B0L002S, U12-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.9
Std Dev	10.93



B0L002S, U12-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch69, adc0

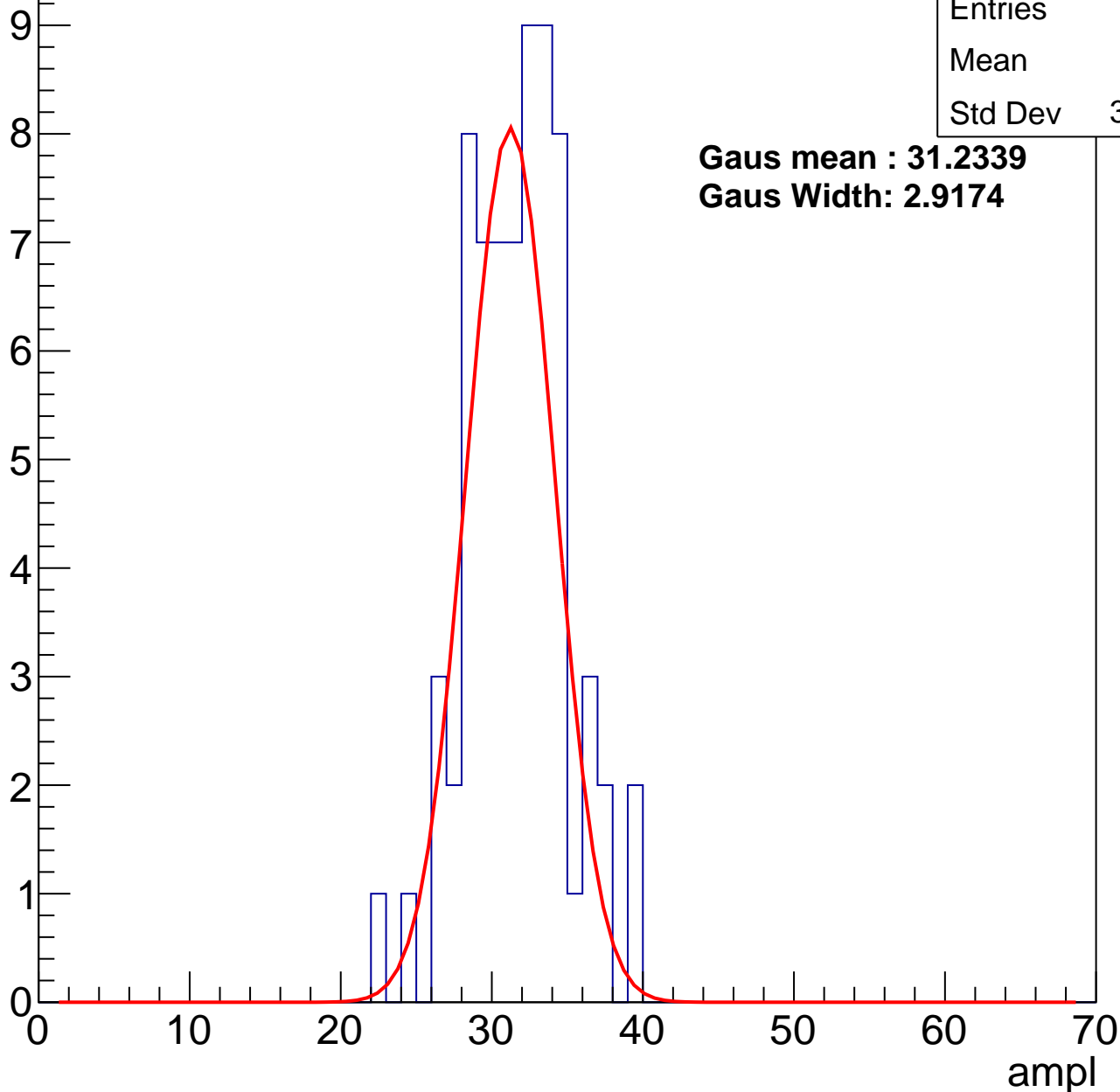
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	31.2
Std Dev	3.272

Gaus mean : 31.2339

Gaus Width: 2.9174



B0L002S, U12-ch69, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	38.32
Std Dev	3.491

Gaus mean : 38.6420

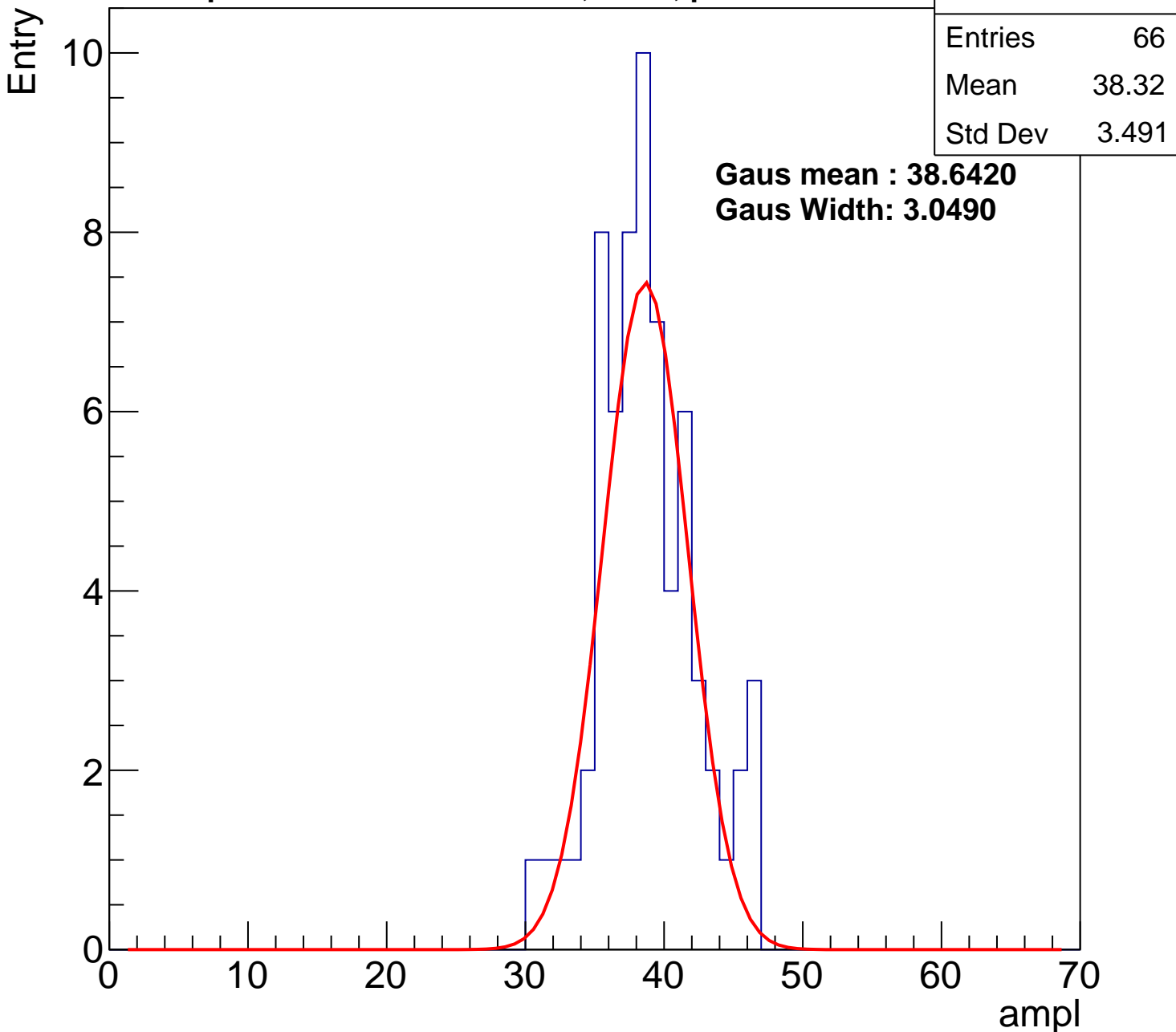
Gaus Width: 3.0490

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch69, adc2

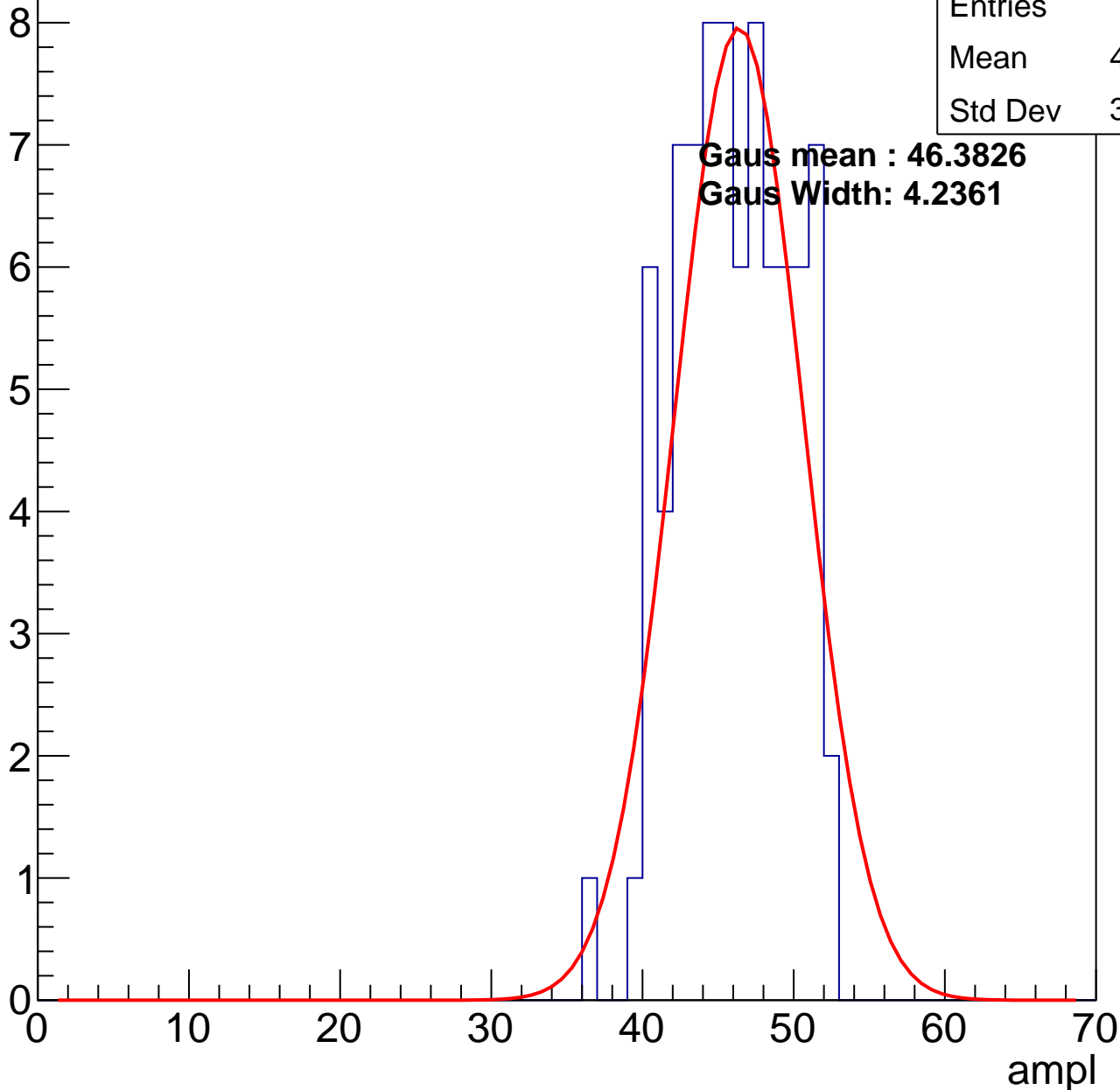
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	45.55
Std Dev	3.628

Gaus mean : 46.3826

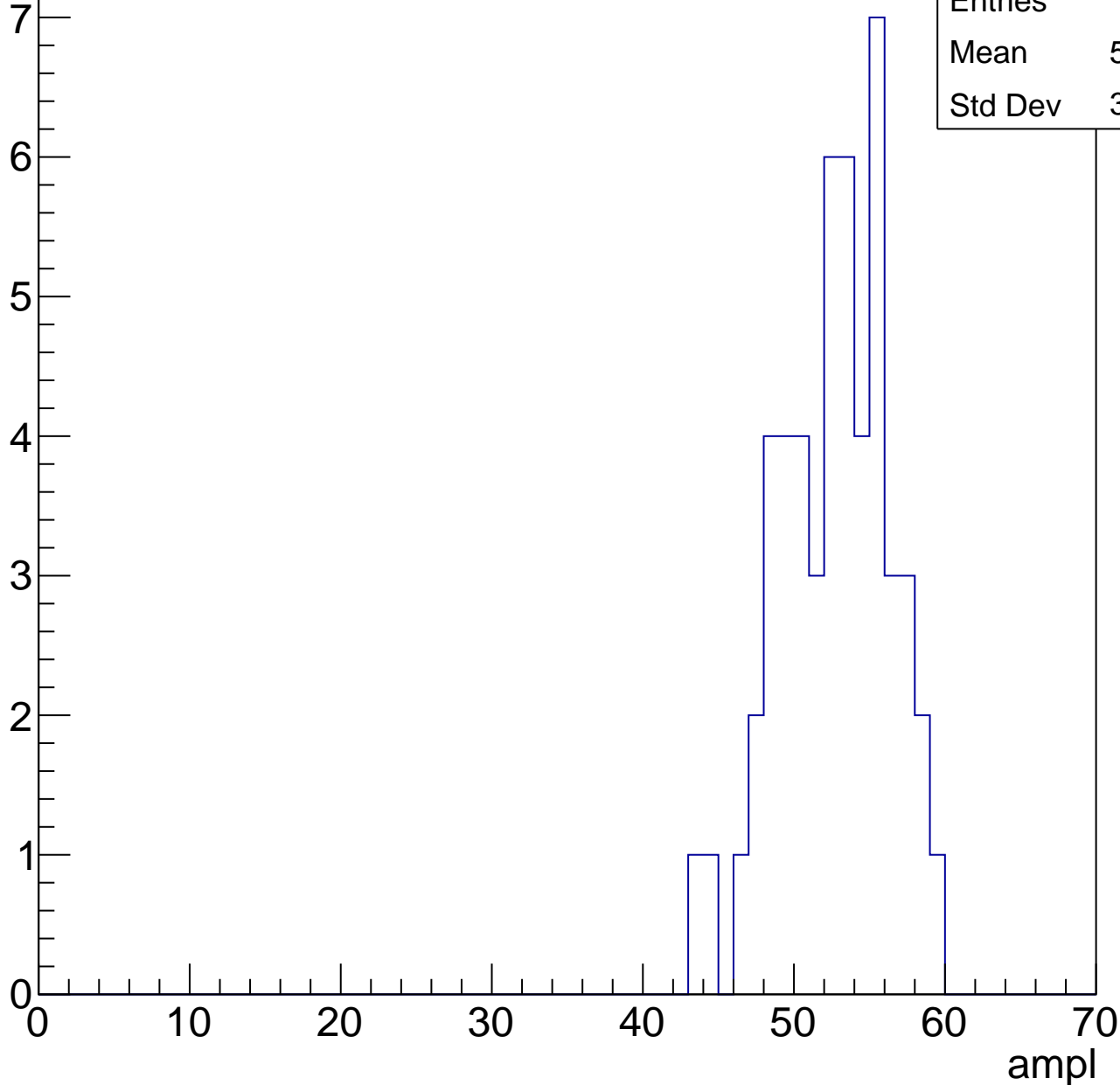
Gaus Width: 4.2361



B0L002S, U12-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

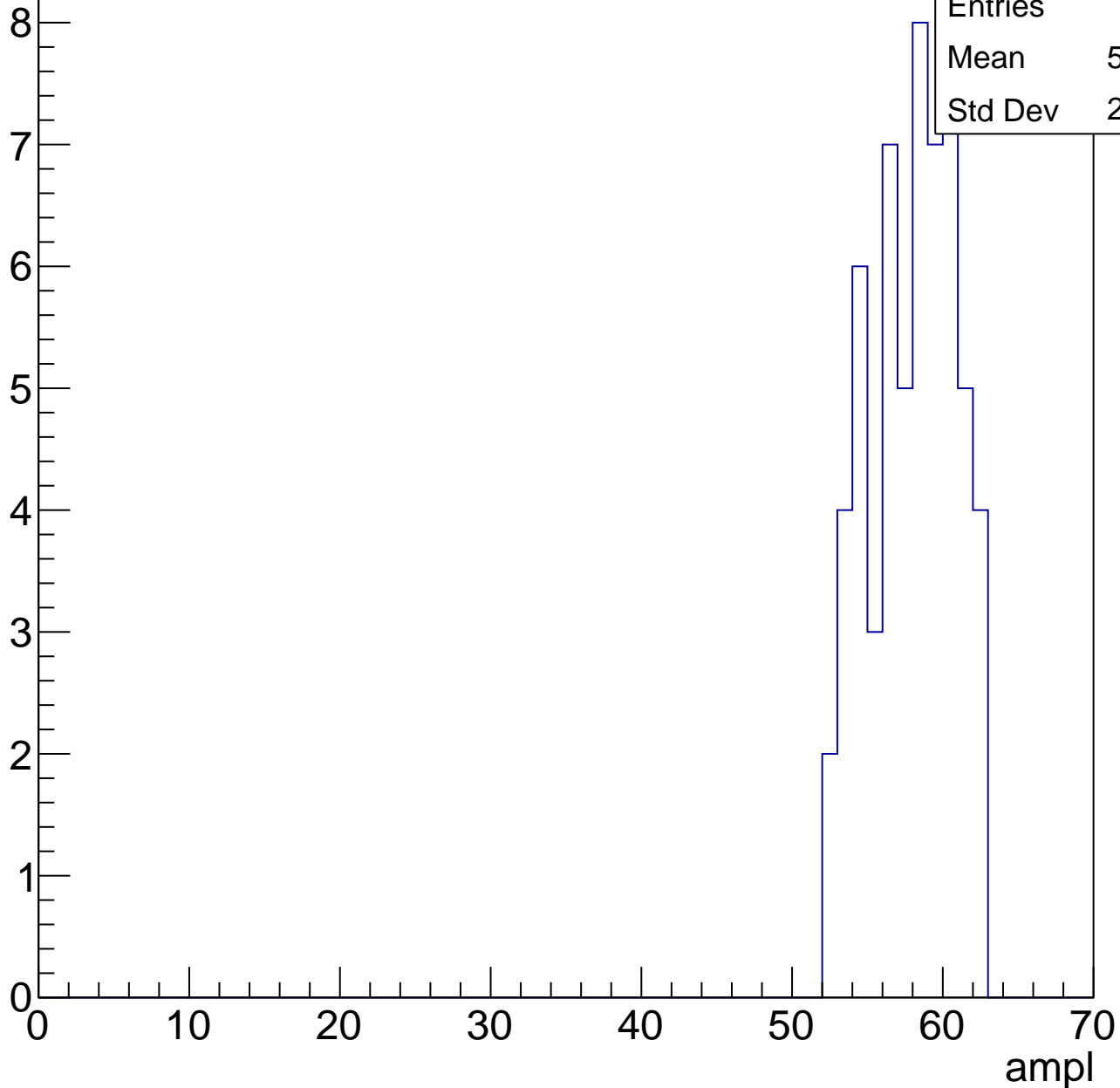
Entry



B0L002S, U12-ch69, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



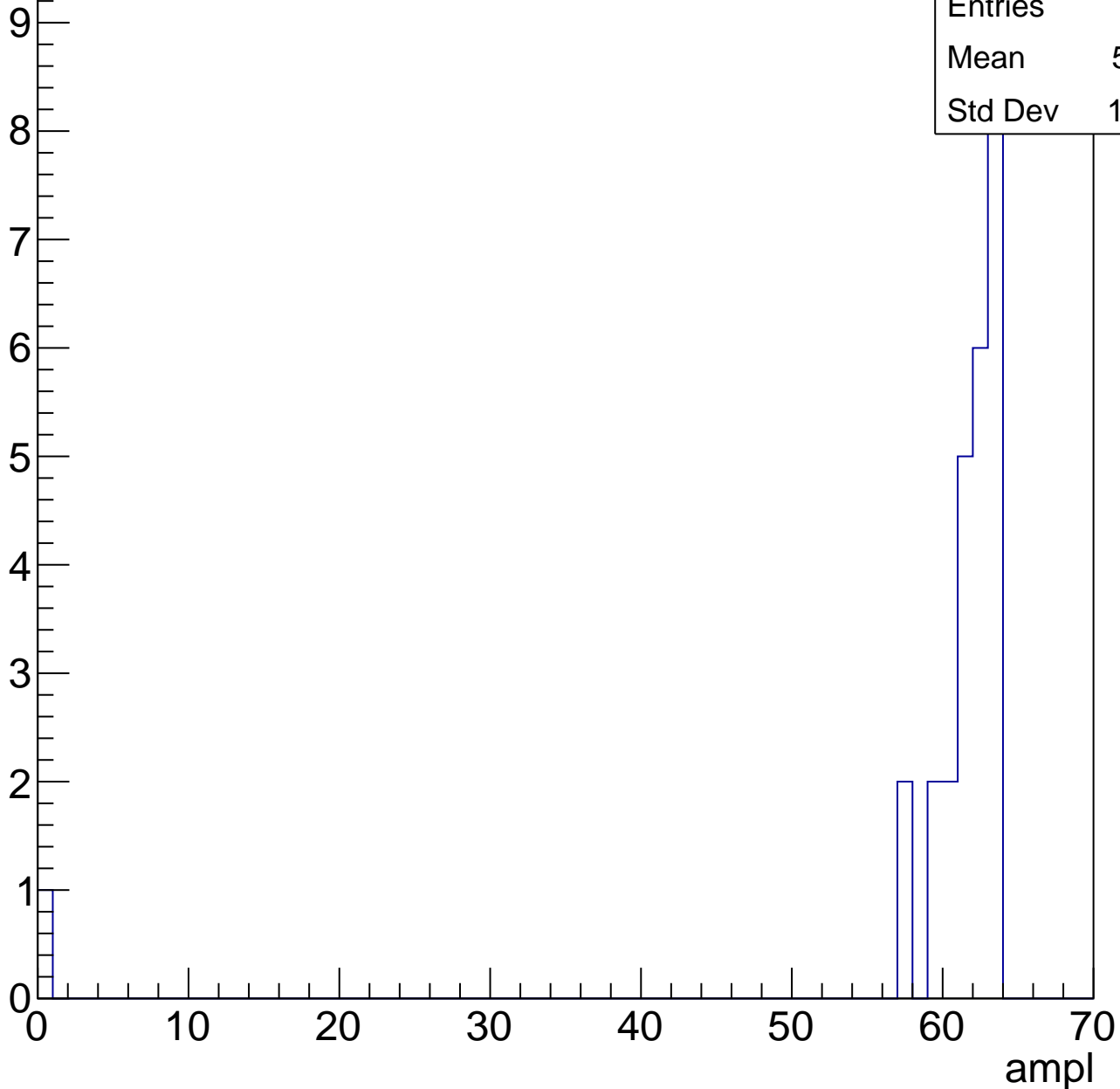
Entries	59
Mean	57.49
Std Dev	2.795

B0L002S, U12-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	59.11
Std Dev	11.72



B0L002S, U12-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch70, adc0

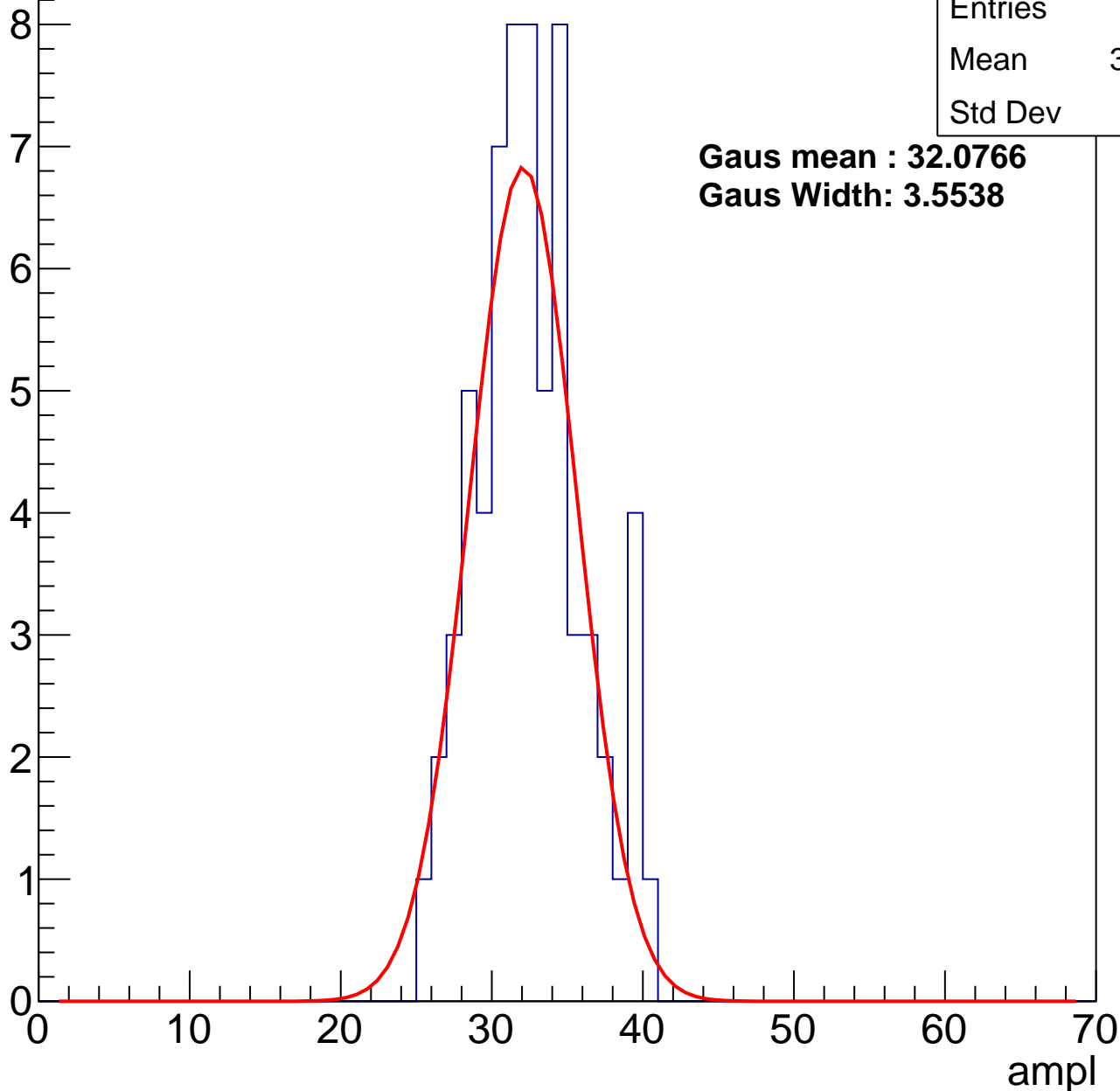
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	32.09
Std Dev	3.52

Gaus mean : 32.0766

Gaus Width: 3.5538



B0L002S, U12-ch70, adc1

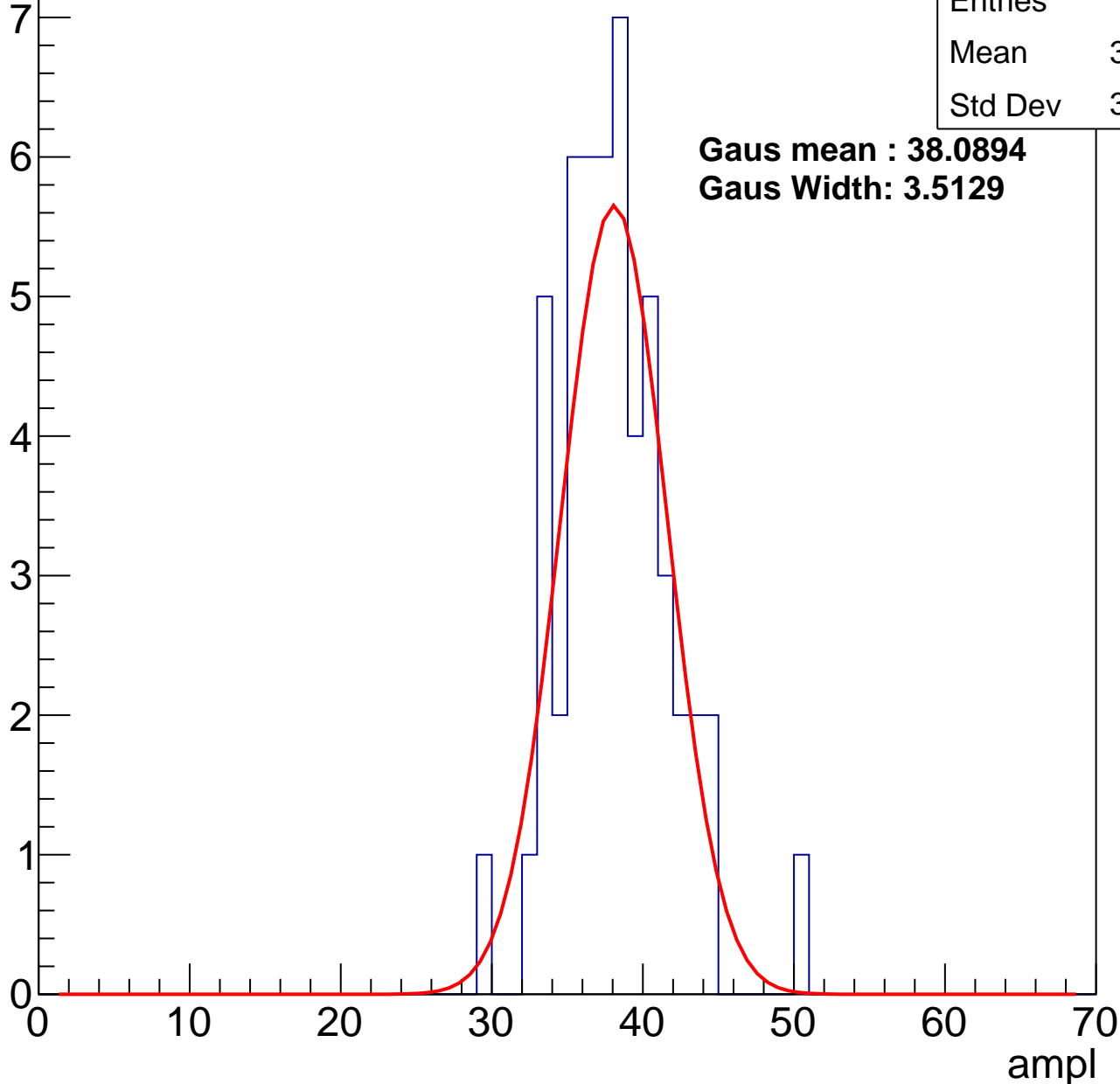
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	37.64
Std Dev	3.635

Gaus mean : 38.0894

Gaus Width: 3.5129



B0L002S, U12-ch70, adc2

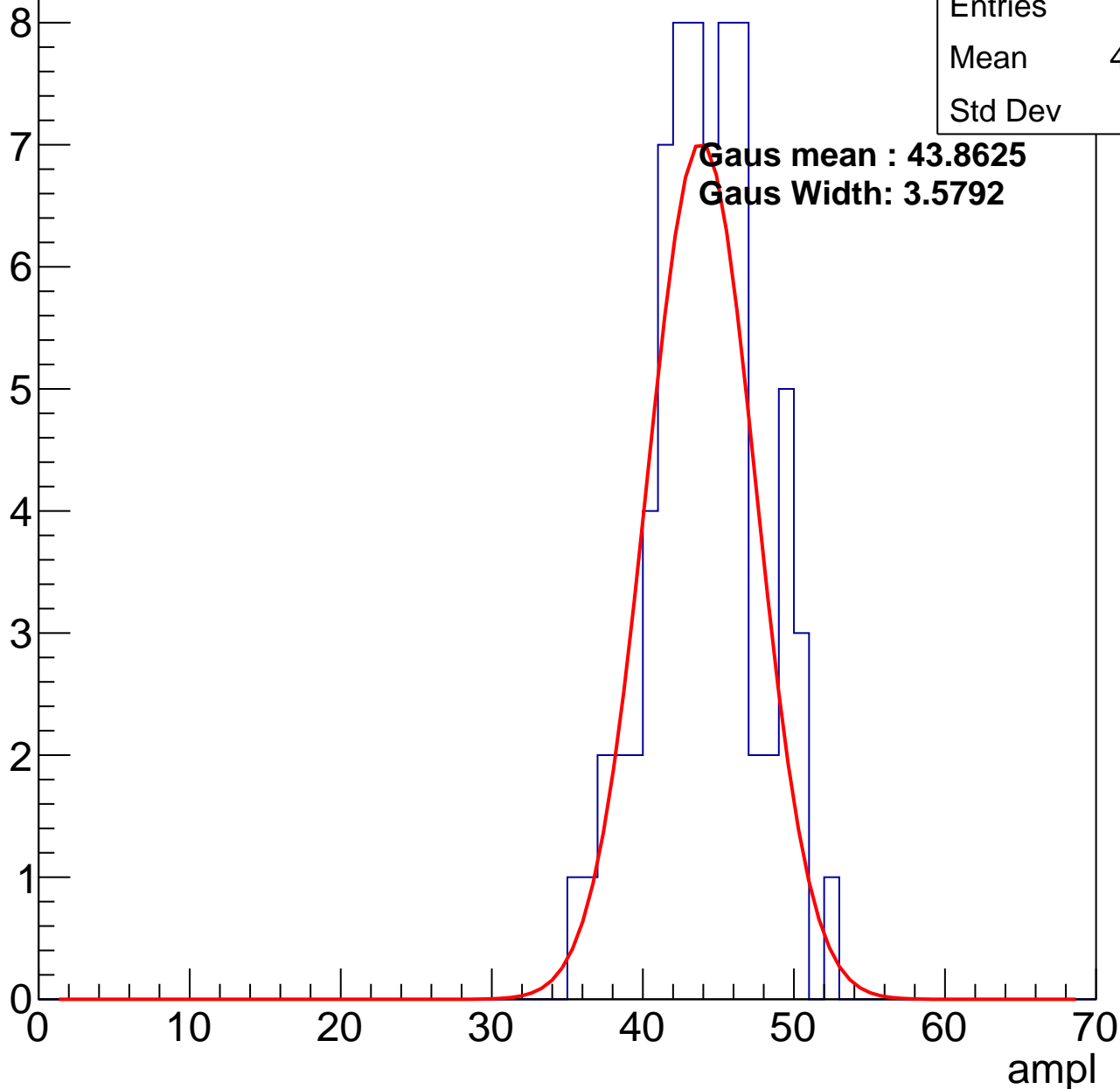
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.65
Std Dev	3.58

Gaus mean : 43.8625

Gaus Width: 3.5792

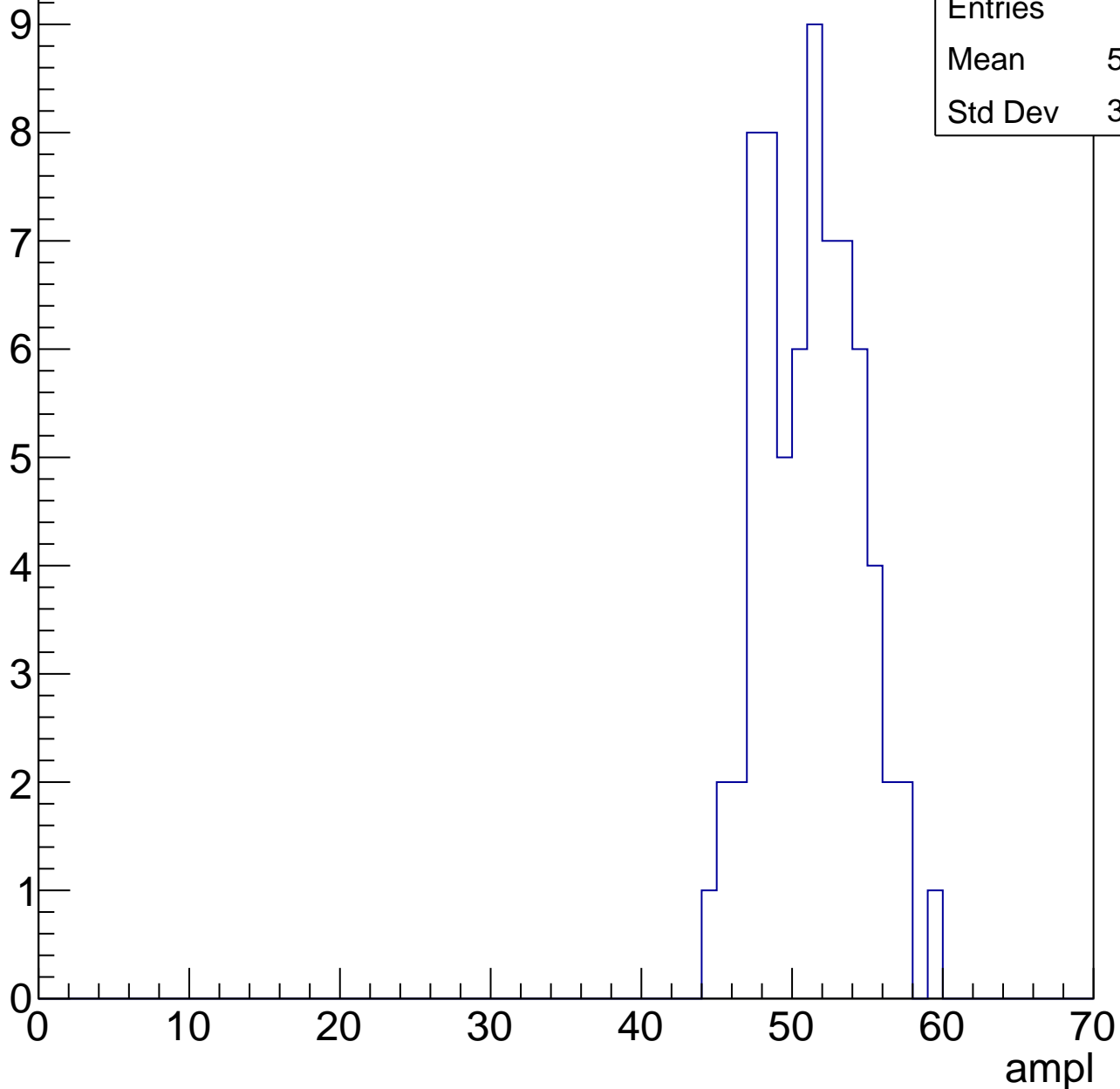


B0L002S, U12-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	50.77
Std Dev	3.235

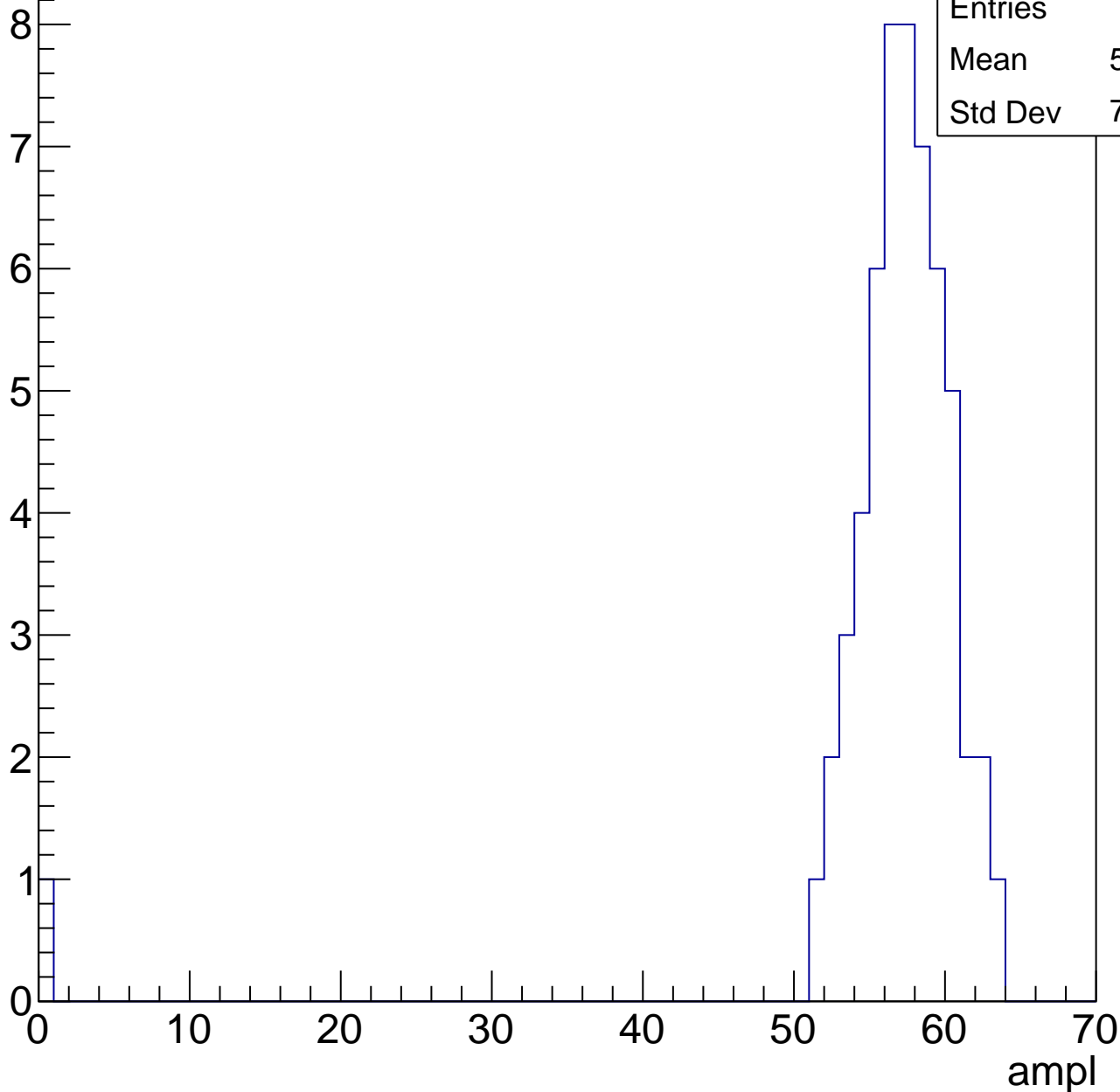


B0L002S, U12-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

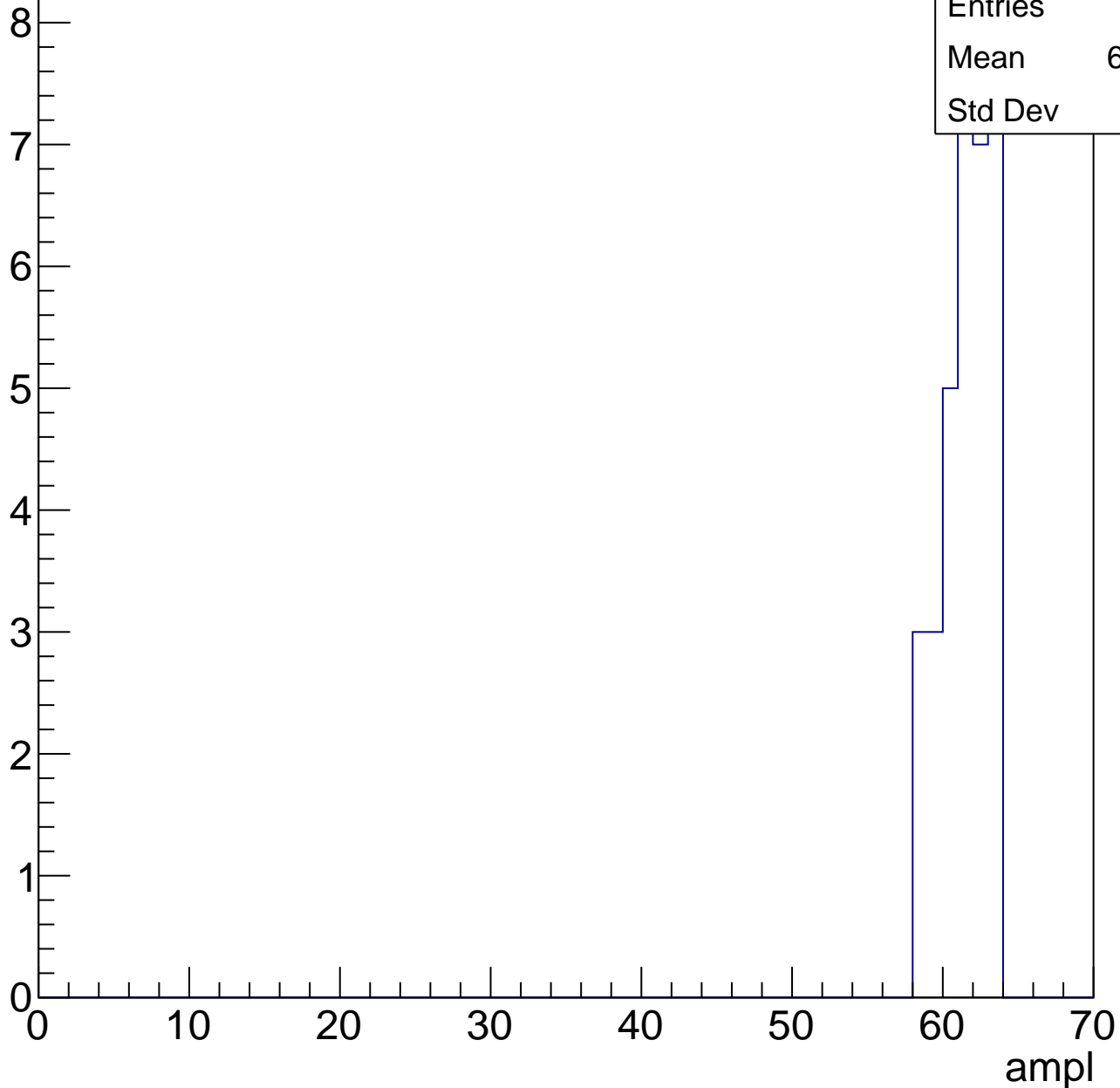
Entries	56
Mean	55.95
Std Dev	7.999



B0L002S, U12-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U12-ch71, adc0

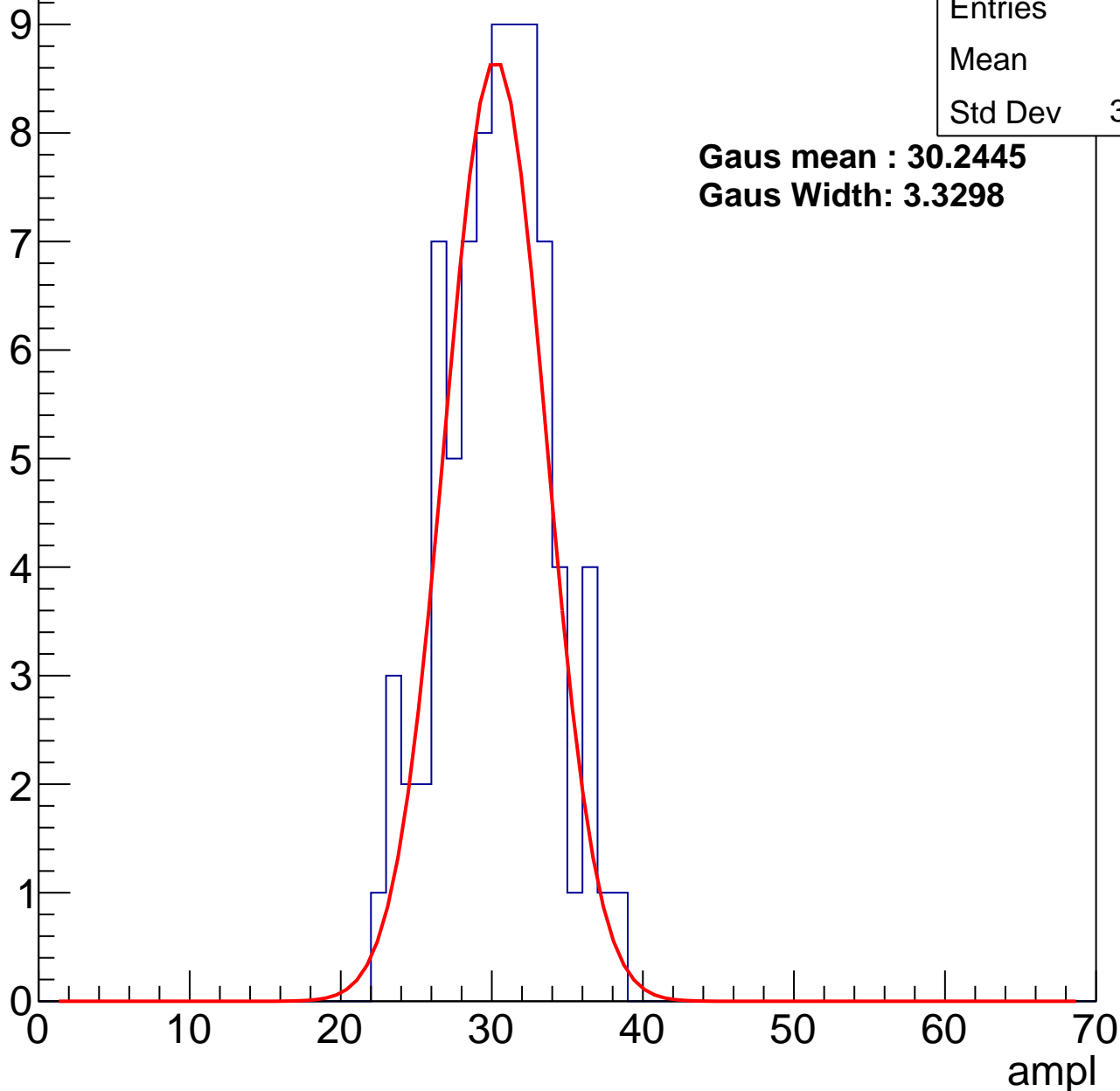
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	29.9
Std Dev	3.499

Gaus mean : 30.2445

Gaus Width: 3.3298



B0L002S, U12-ch71, adc1

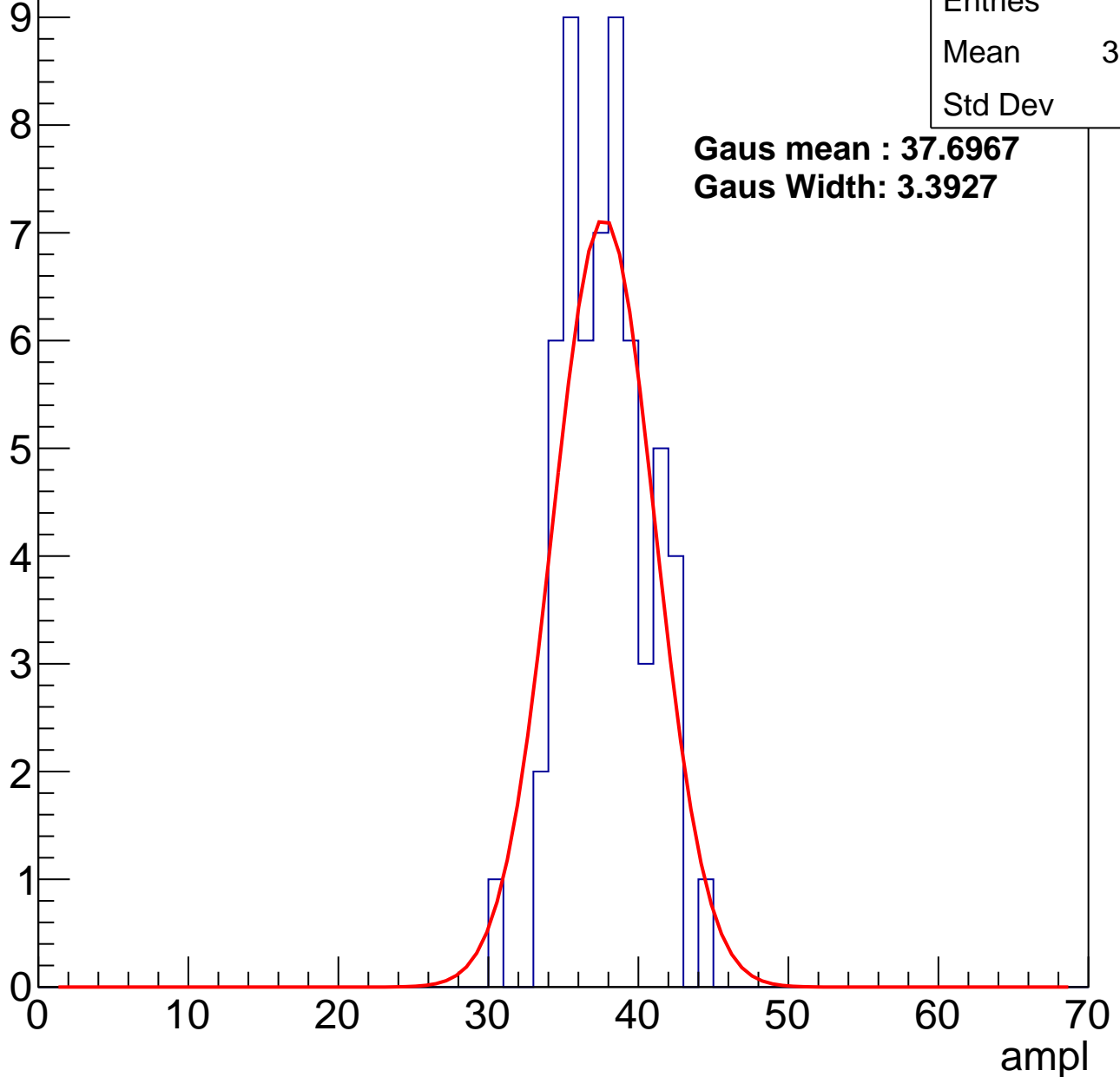
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	37.34
Std Dev	2.79

Gaus mean : 37.6967

Gaus Width: 3.3927



B0L002S, U12-ch71, adc2

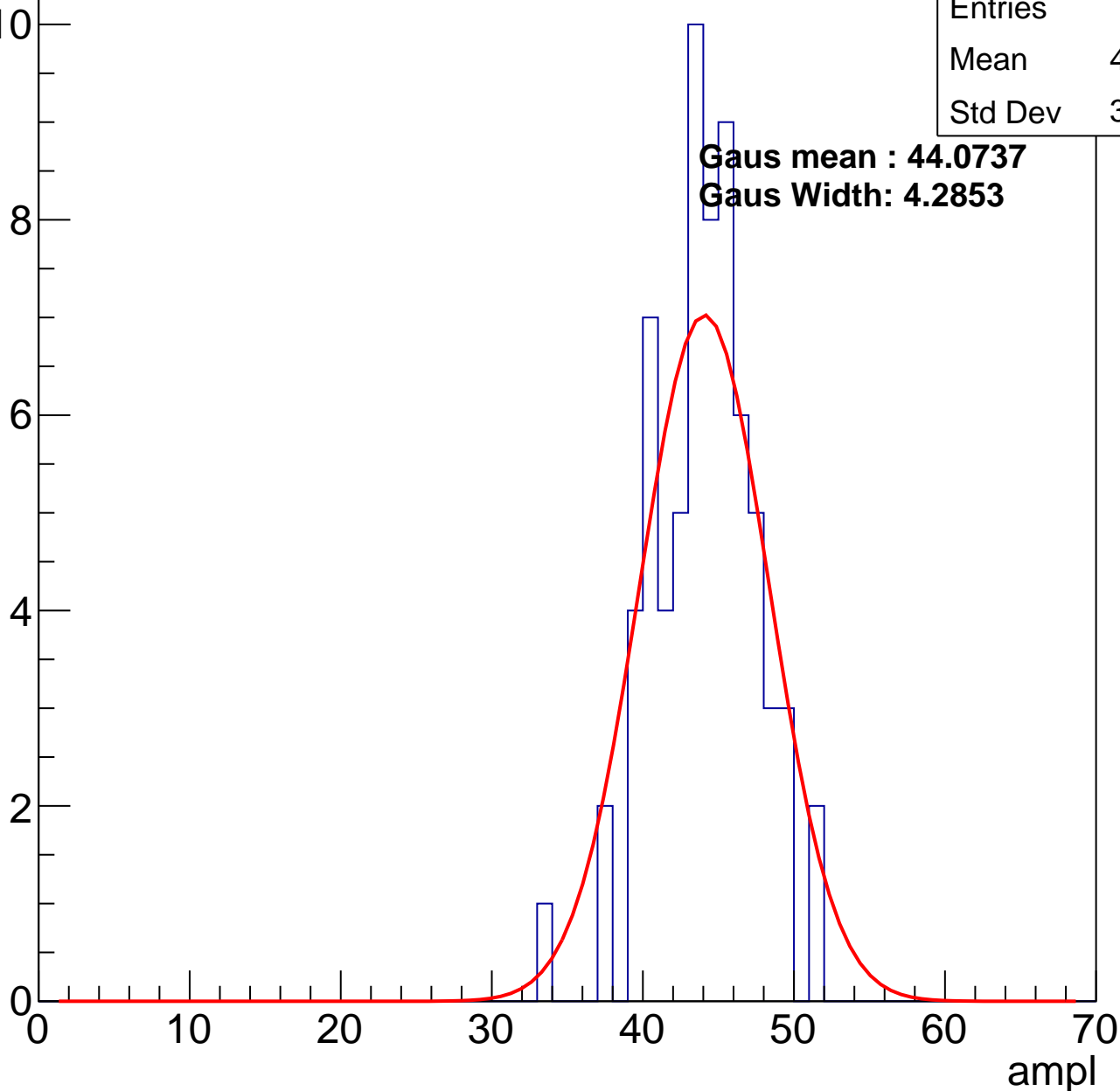
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.59
Std Dev	3.372

Gaus mean : 44.0737

Gaus Width: 4.2853



B0L002S, U12-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

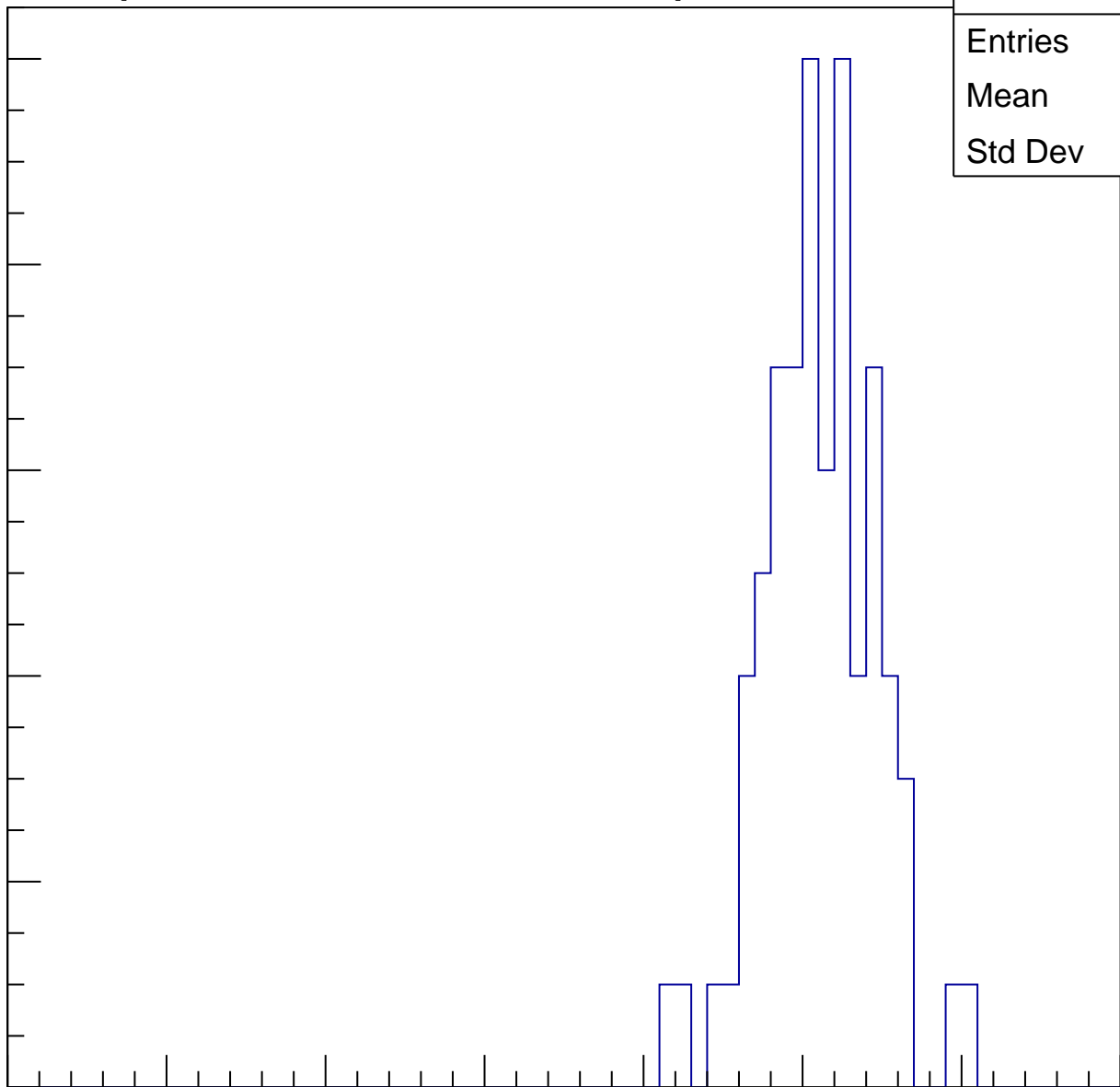
Entries	73
Mean	50.59
Std Dev	3.526

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

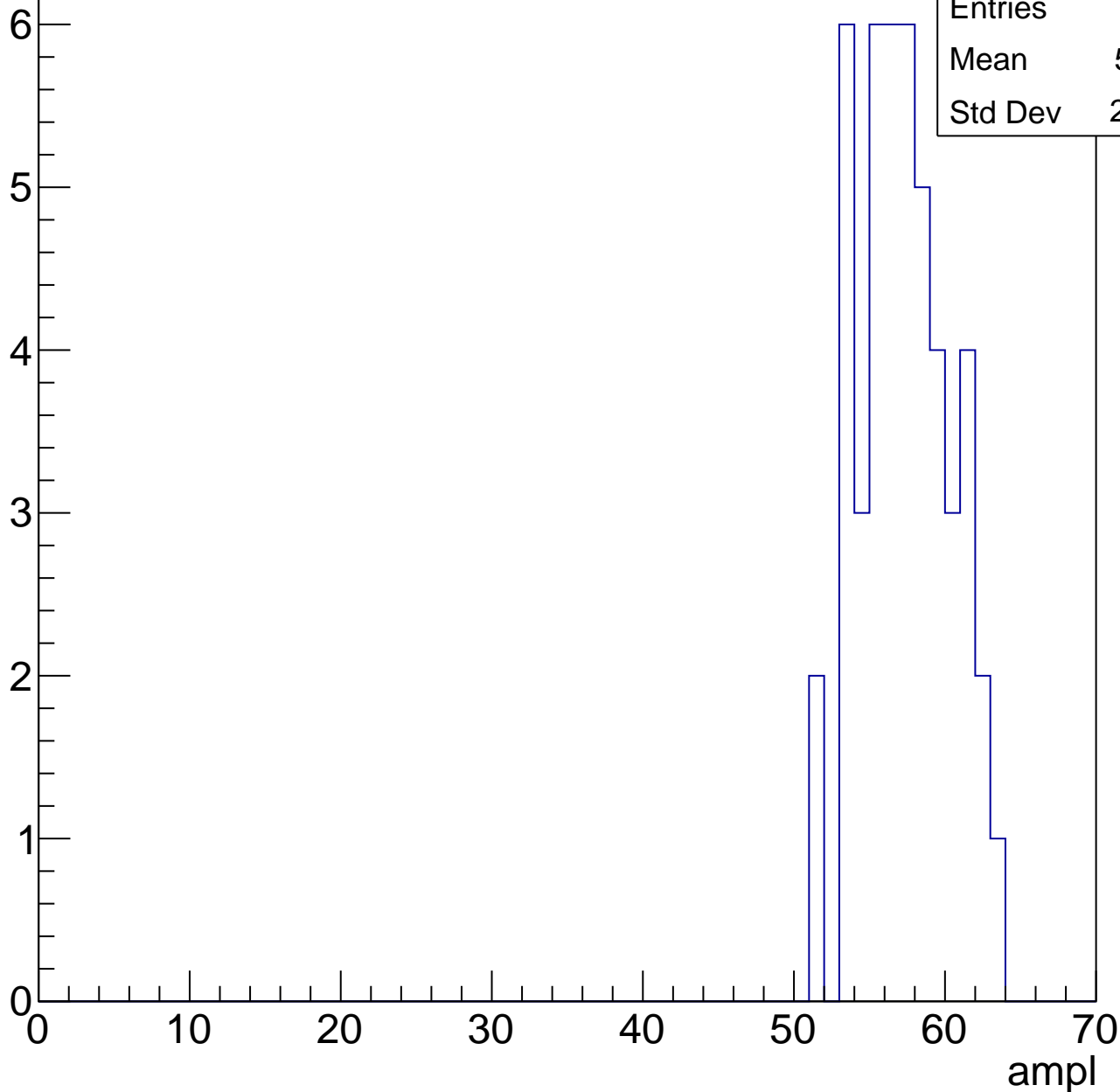


B0L002S, U12-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	56.81
Std Dev	2.963

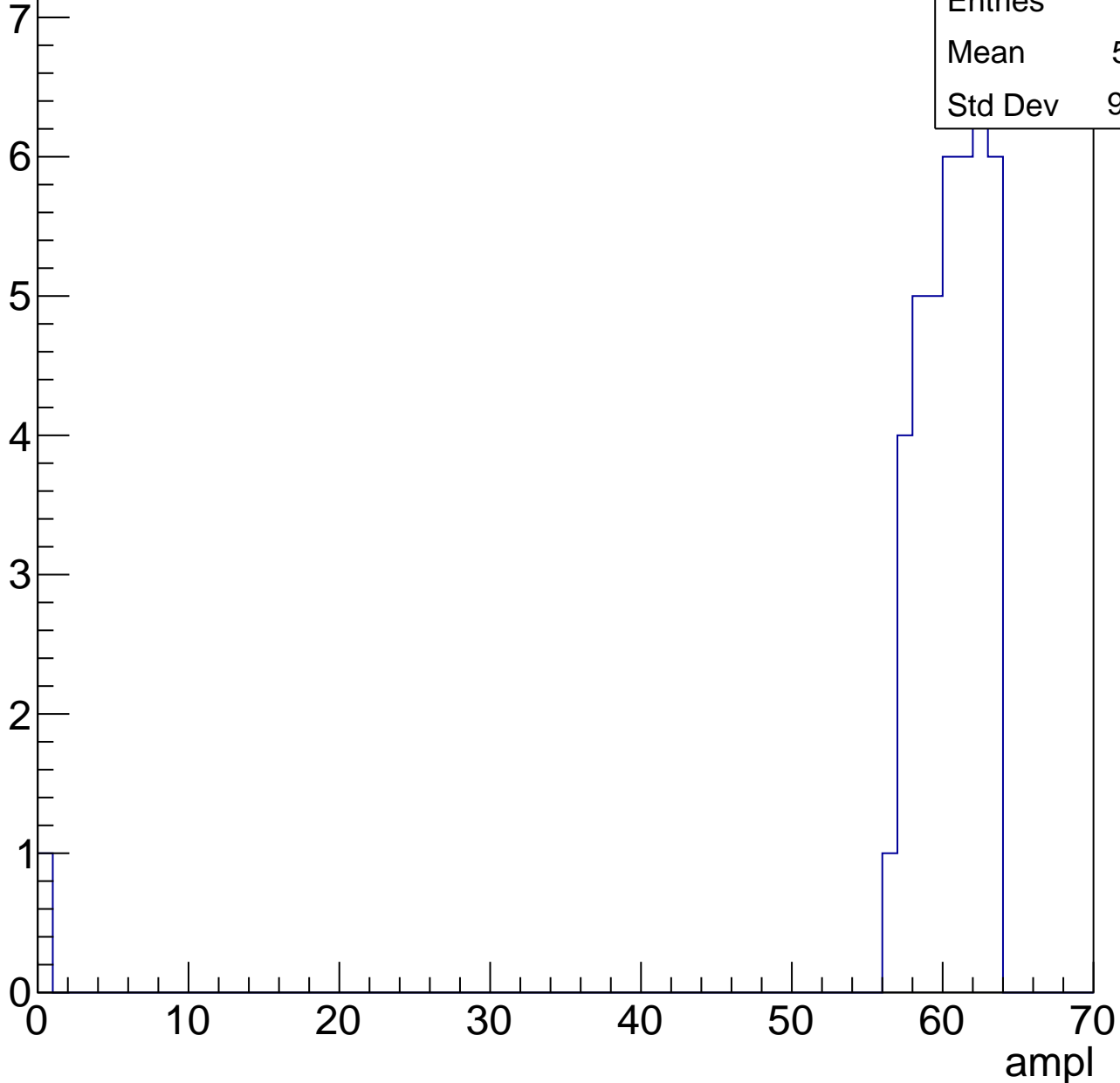


B0L002S, U12-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.71
Std Dev	9.495



B0L002S, U12-ch71, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U12-ch72, adc0

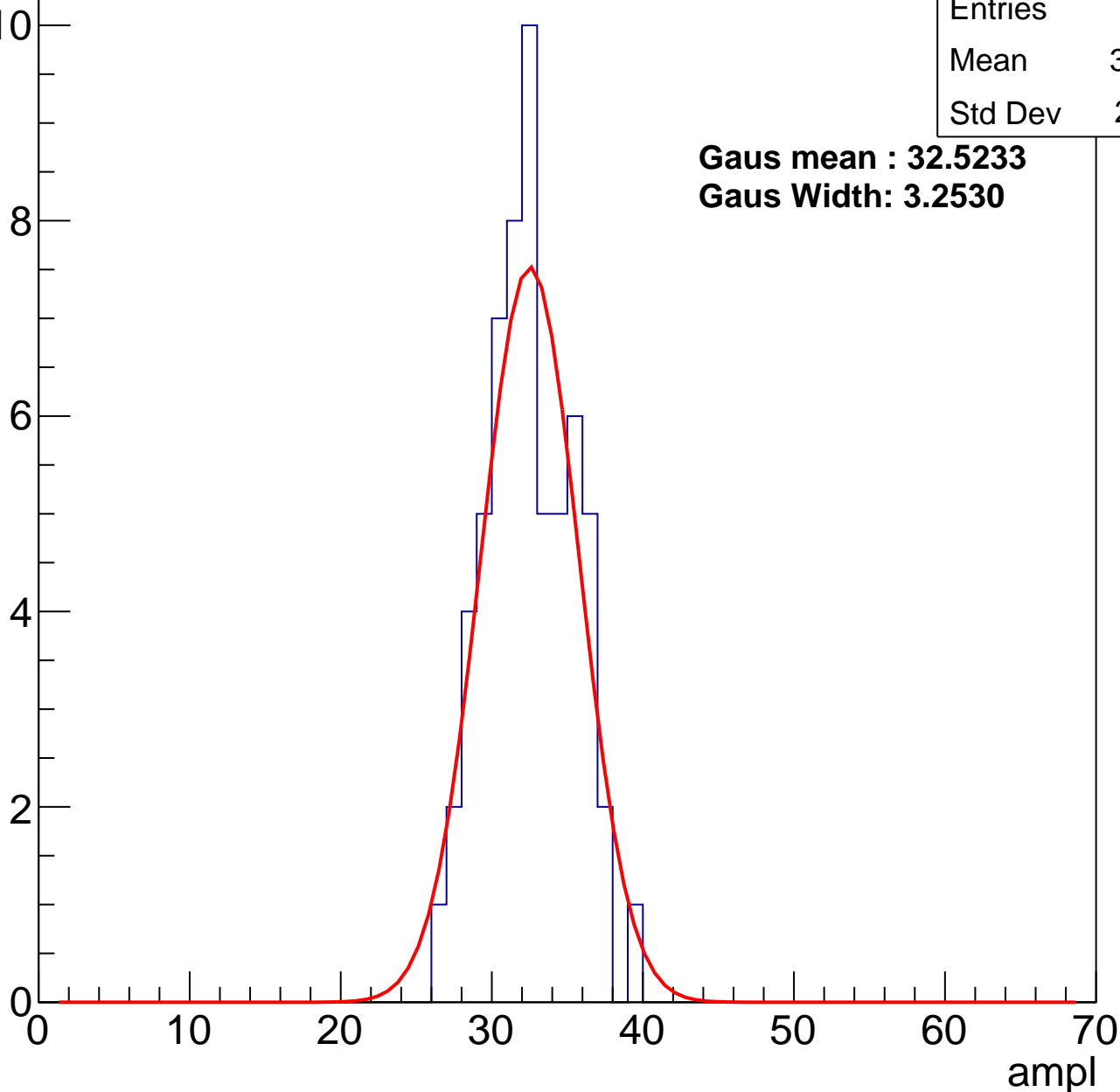
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	32.02
Std Dev	2.831

Gaus mean : 32.5233

Gaus Width: 3.2530



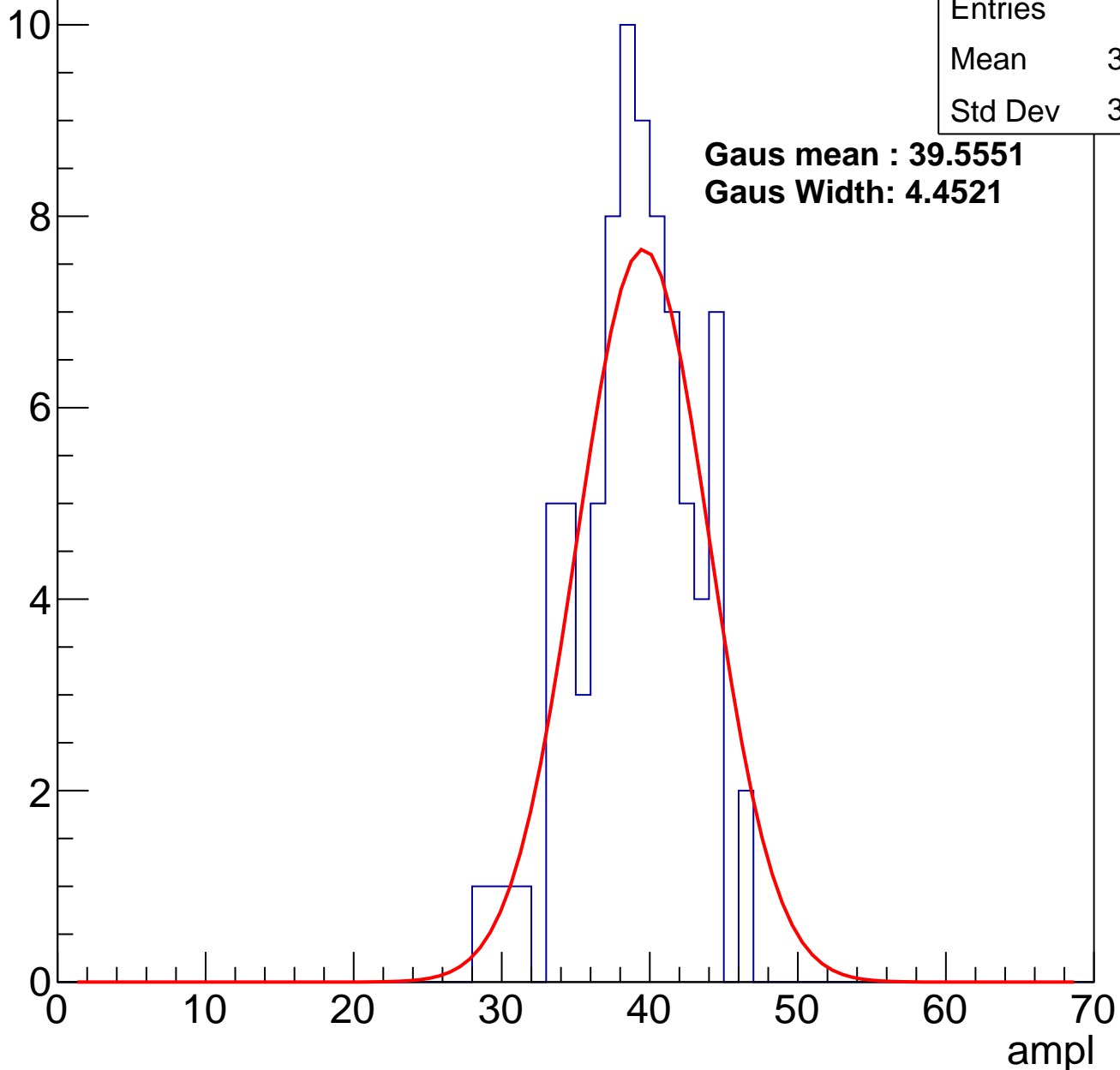
B0L002S, U12-ch72, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	38.46
Std Dev	3.826

Gaus mean : 39.5551
Gaus Width: 4.4521

Entry



B0L002S, U12-ch72, adc2

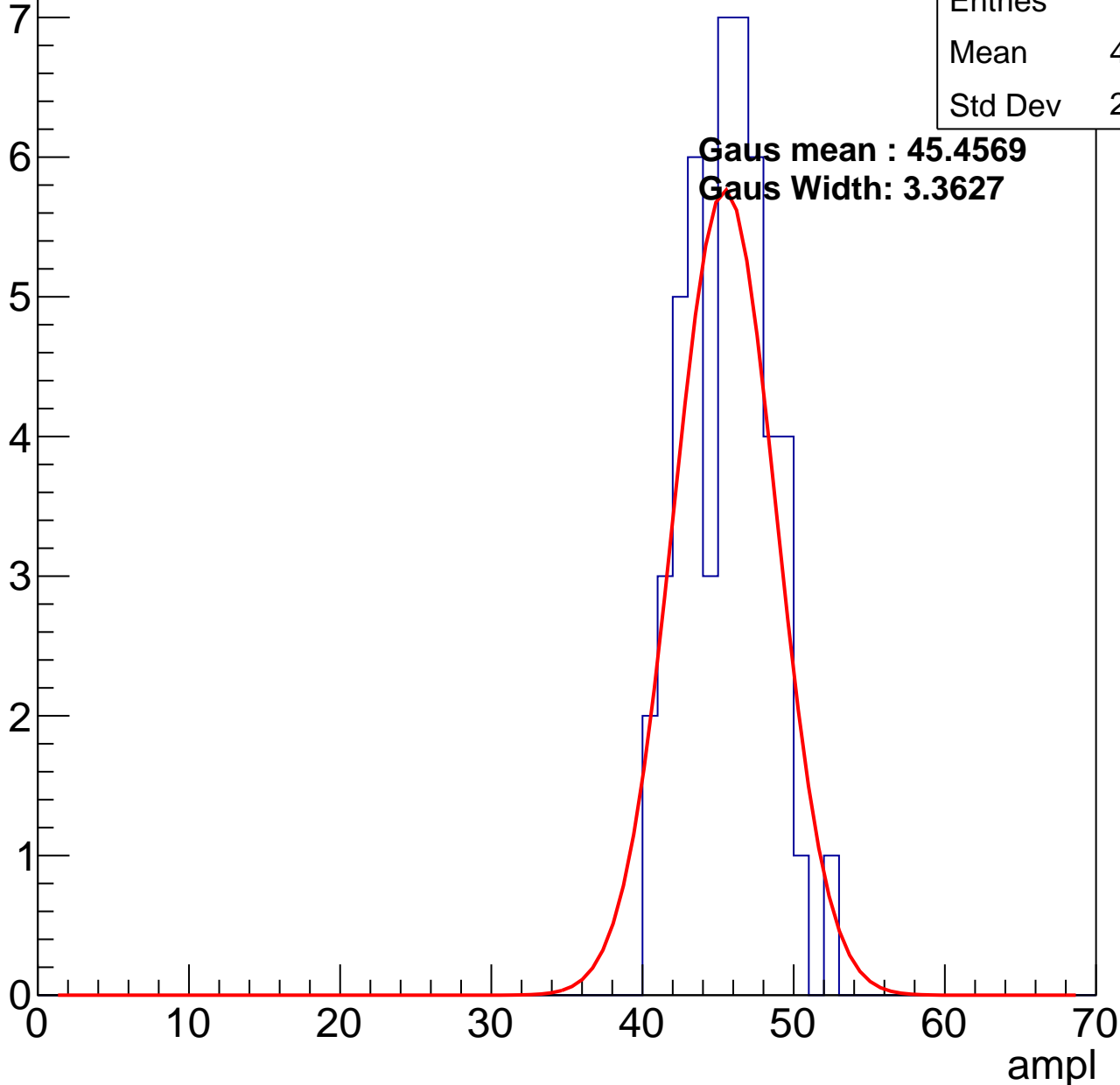
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	45.14
Std Dev	2.763

Gaus mean : 45.4569

Gaus Width: 3.3627

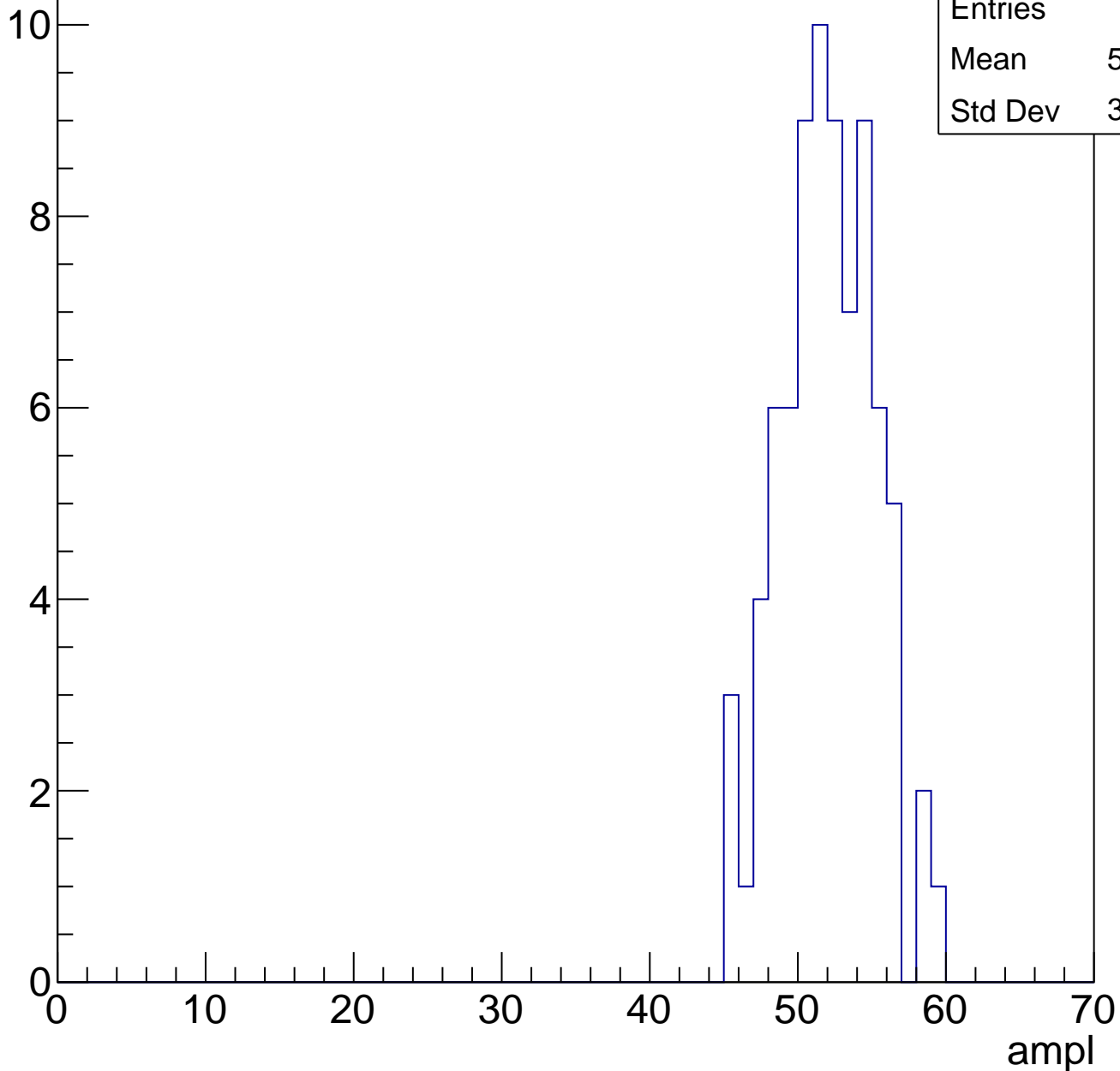


B0L002S, U12-ch72, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	51.55
Std Dev	3.124

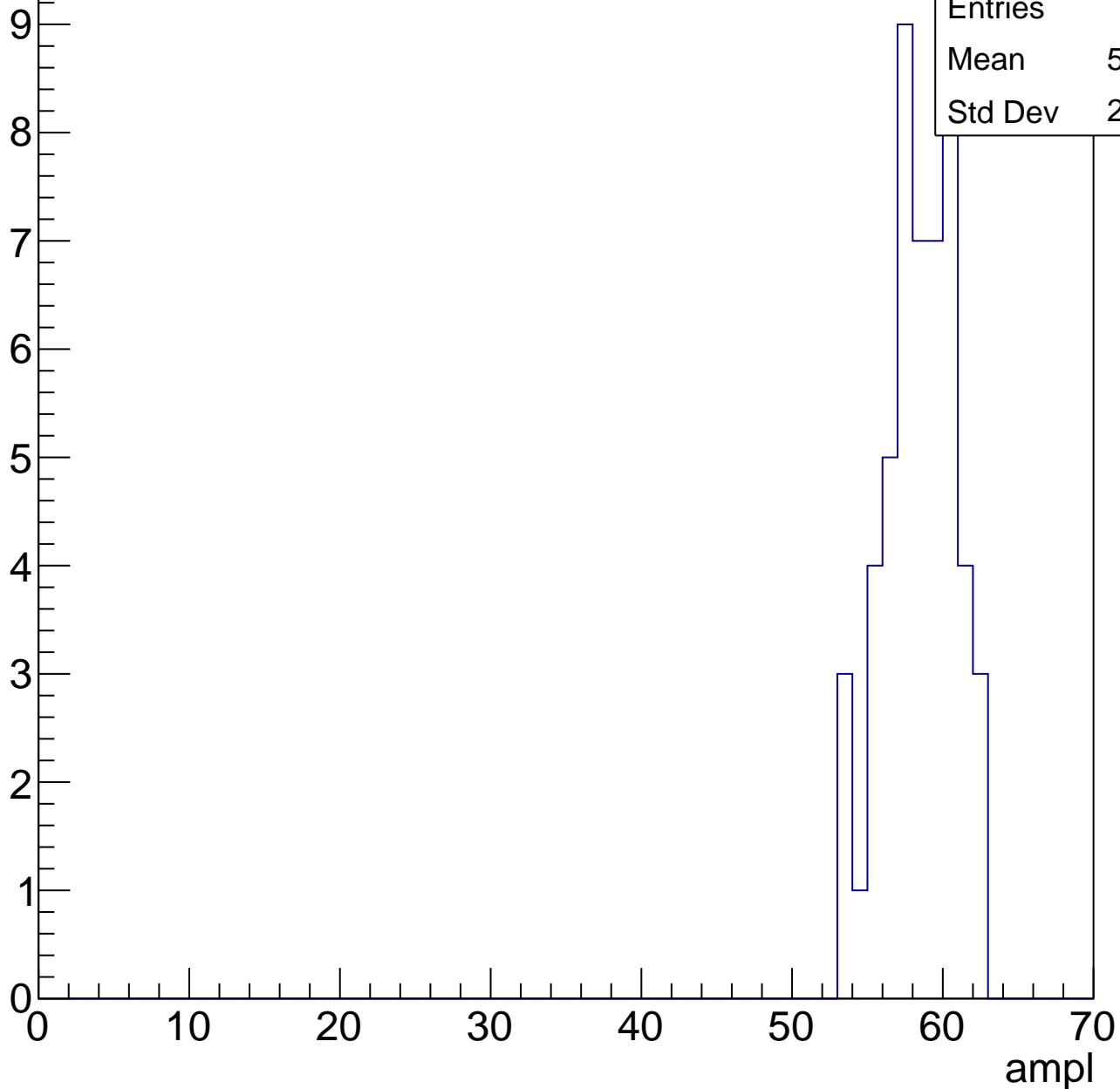
Entry



B0L002S, U12-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



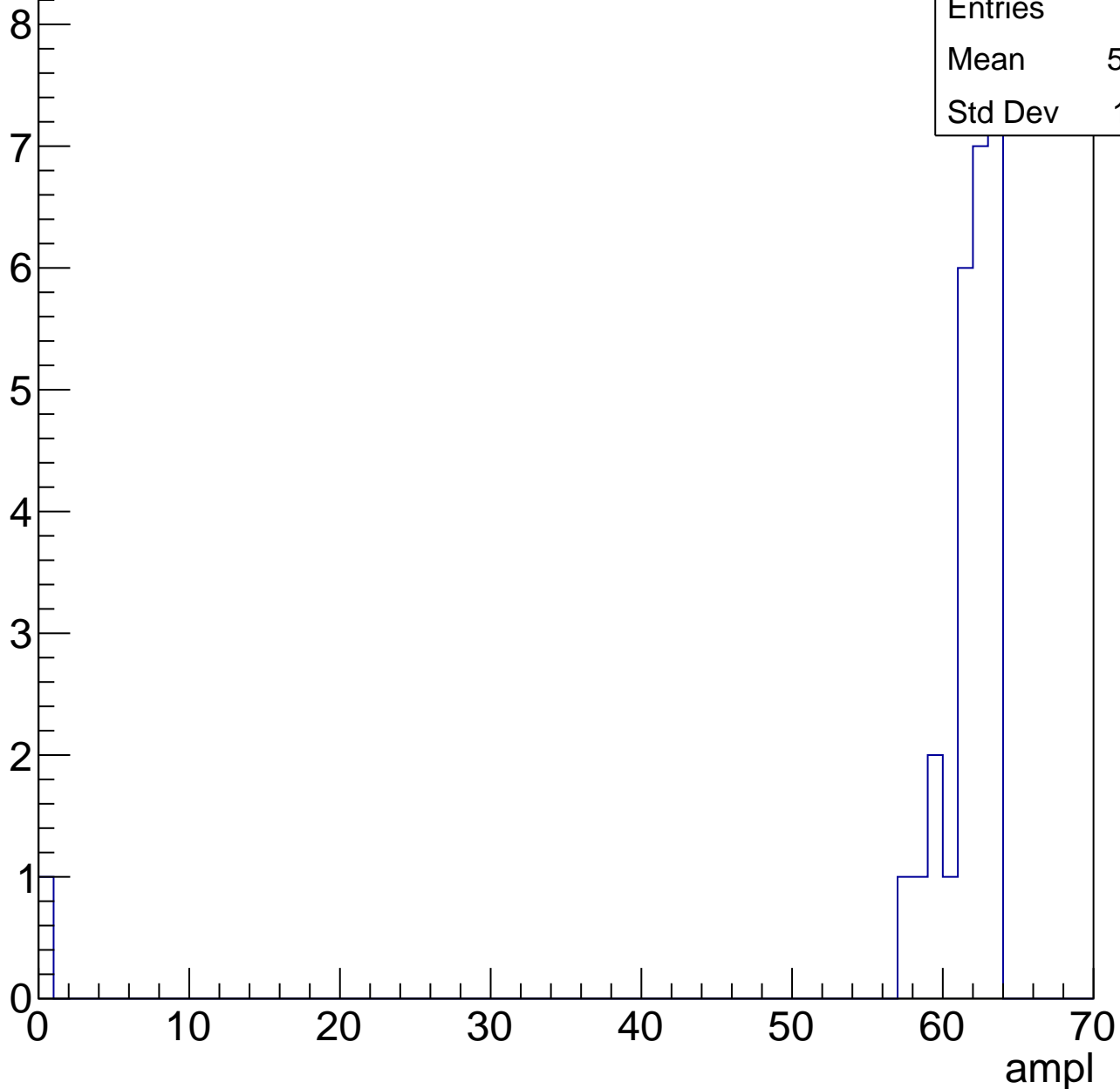
Entries	51
Mean	57.94
Std Dev	2.338

B0L002S, U12-ch72, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	59.15
Std Dev	11.71



B0L002S, U12-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch73, adc0

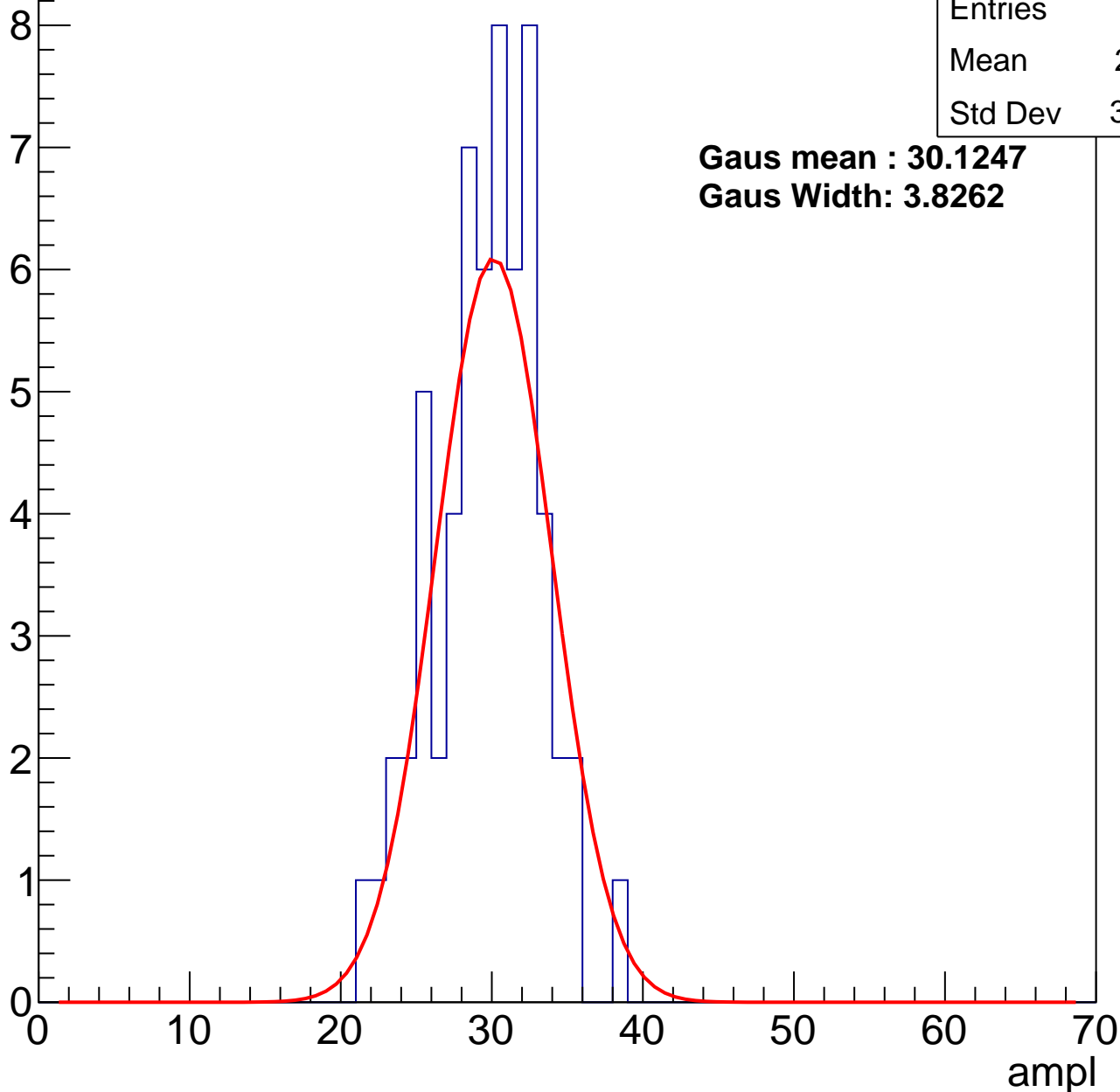
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.21
Std Dev	3.446

Gaus mean : 30.1247

Gaus Width: 3.8262

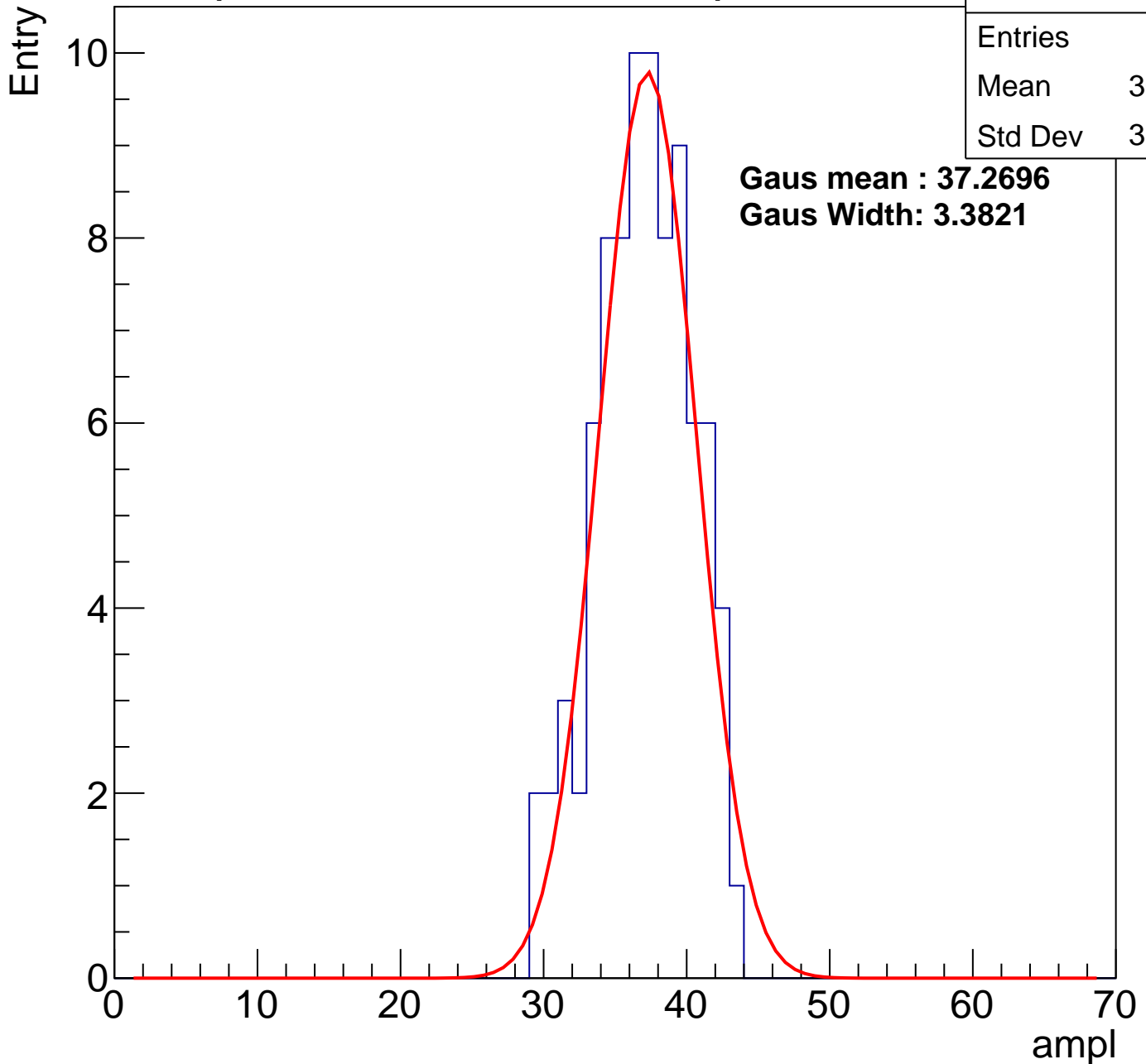


B0L002S, U12-ch73, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	36.55
Std Dev	3.267

Gaus mean : 37.2696
Gaus Width: 3.3821



B0L002S, U12-ch73, adc2

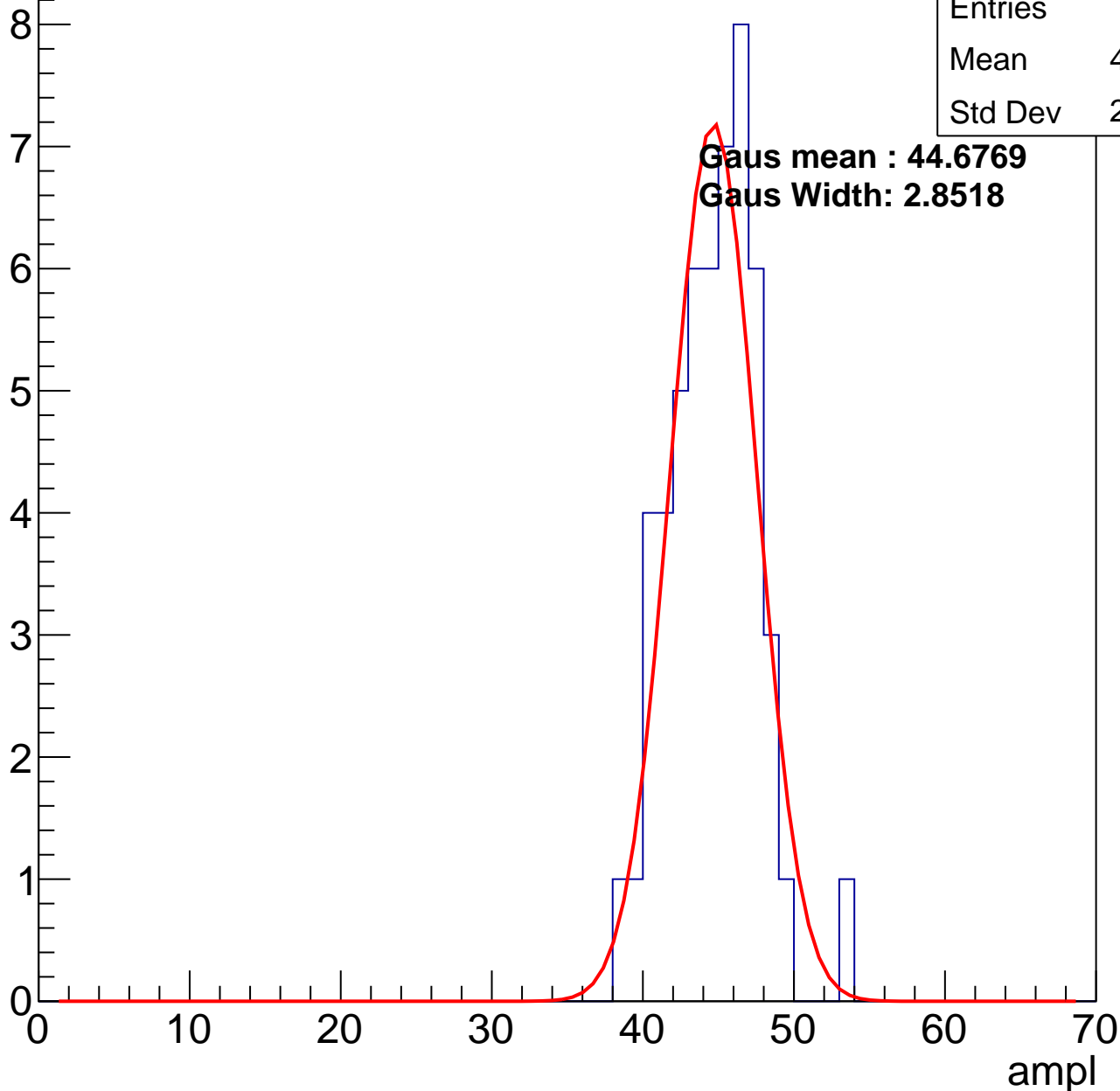
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.23
Std Dev	2.853

Gaus mean : 44.6769

Gaus Width: 2.8518



B0L002S, U12-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	92
Mean	50.78
Std Dev	3.887

Entry

10

8

6

4

2

0

0

10

20

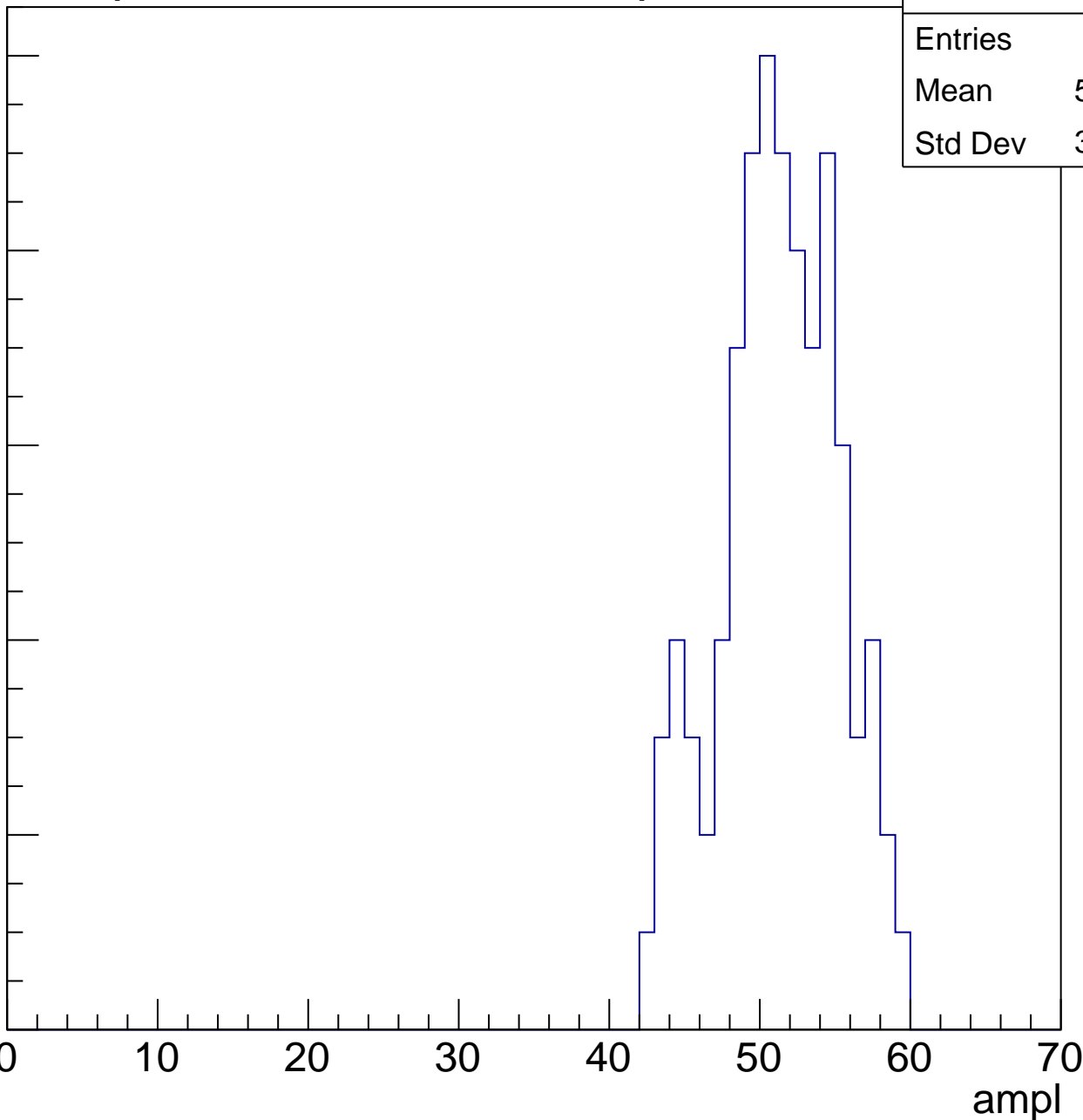
30

40

50

60

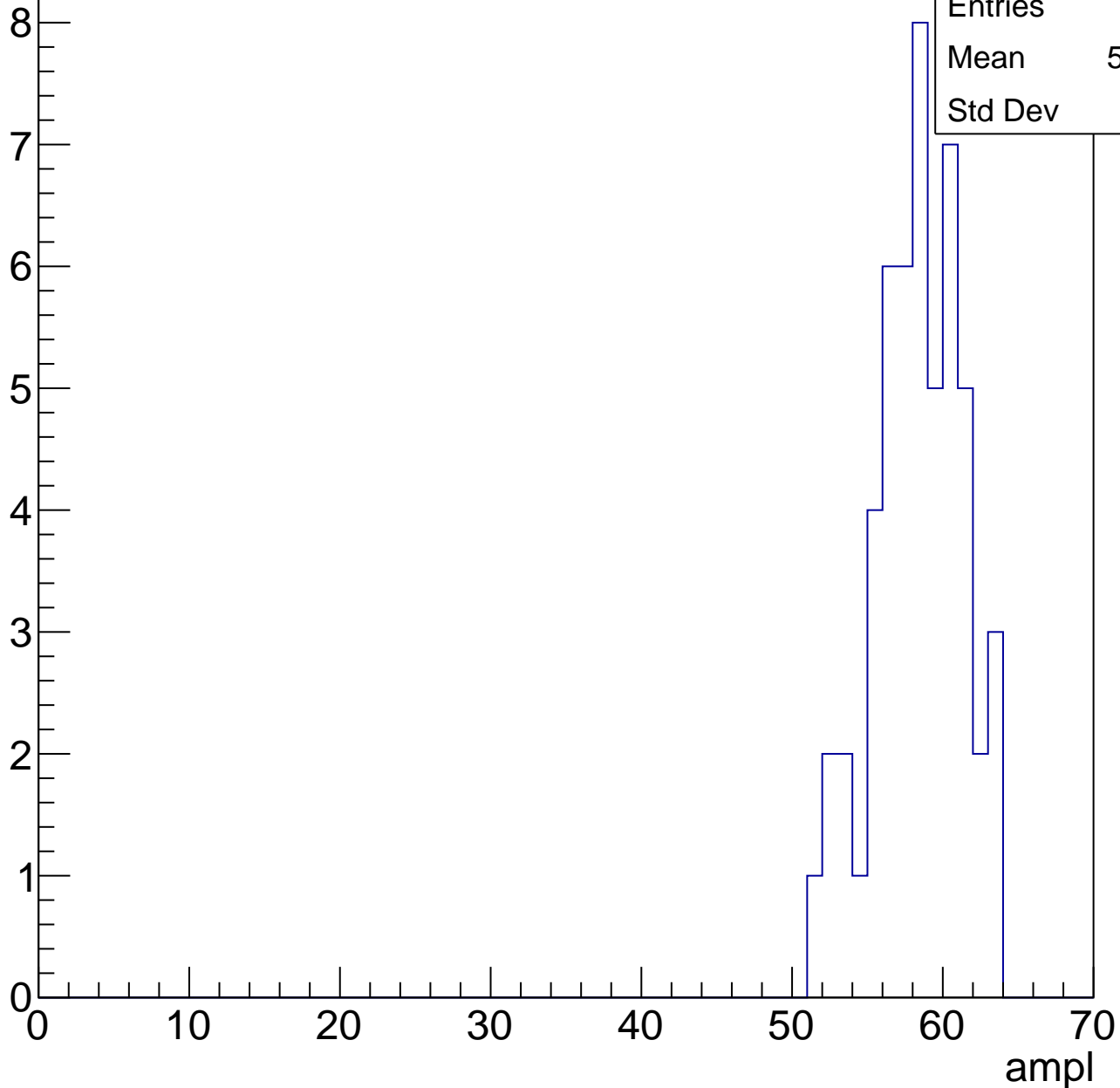
ampl



B0L002S, U12-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

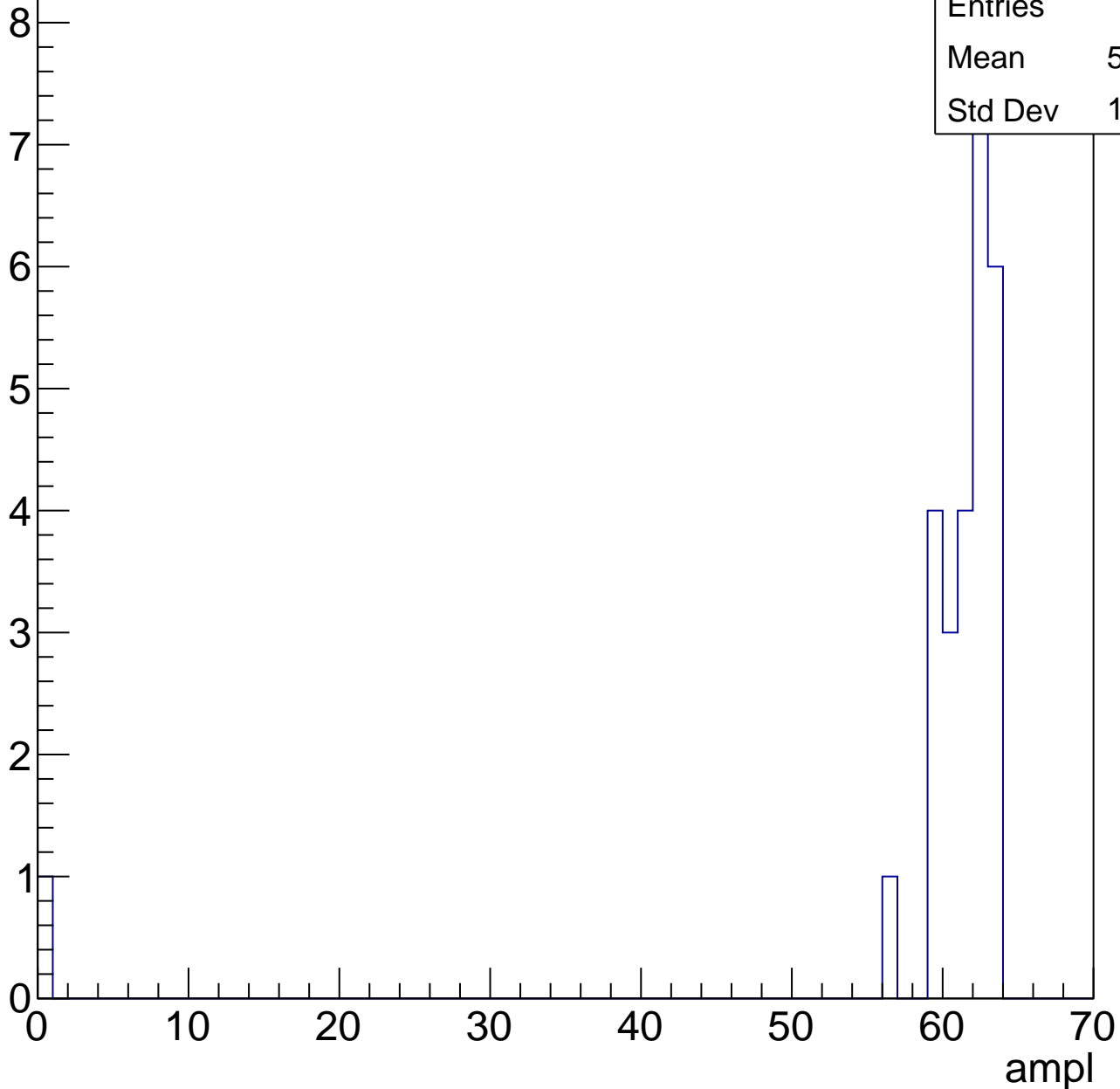


B0L002S, U12-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.89
Std Dev	11.67



B0L002S, U12-ch73, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch74, adc0

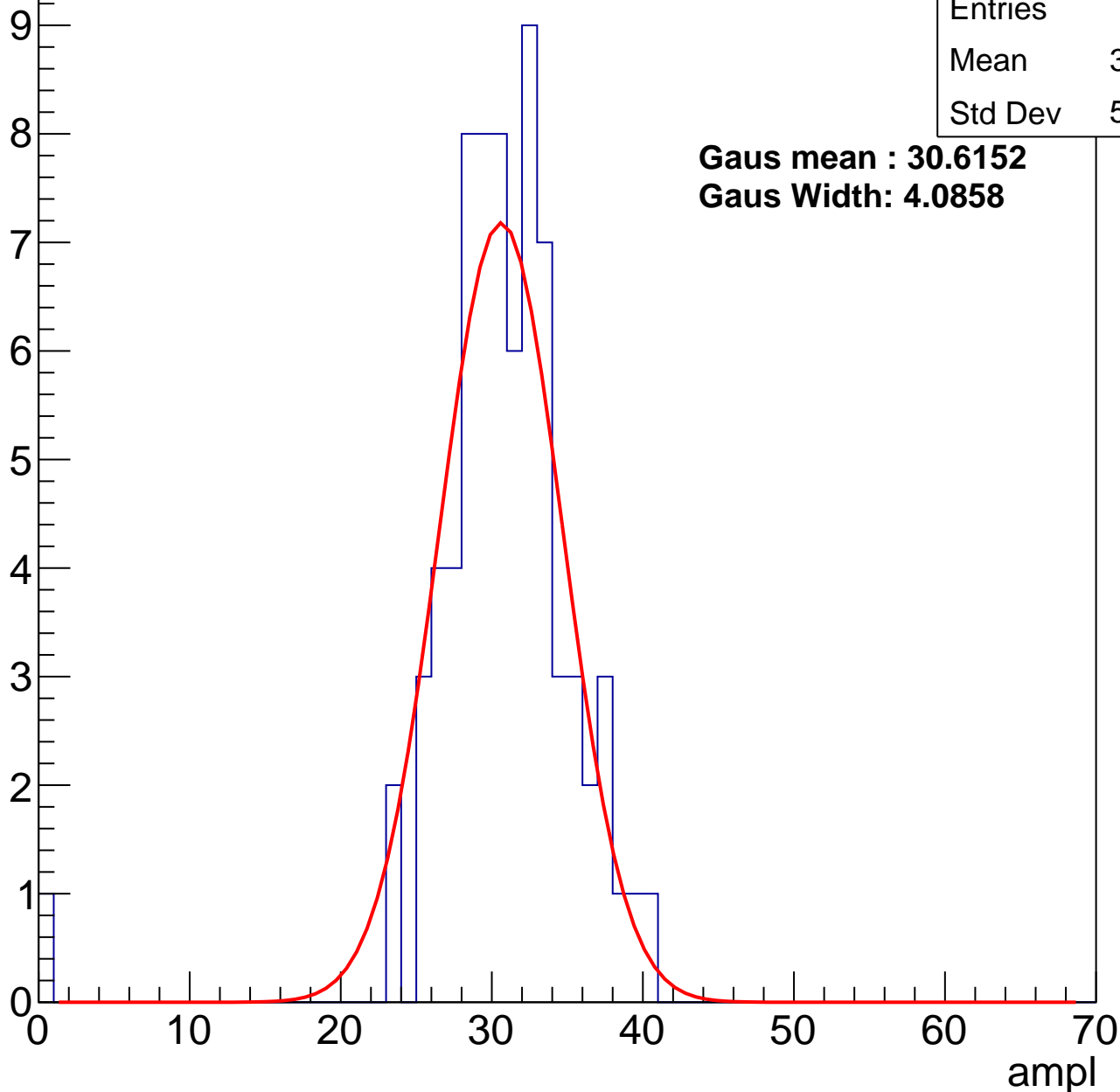
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	30.28
Std Dev	5.066

Gaus mean : 30.6152

Gaus Width: 4.0858



B0L002S, U12-ch74, adc1

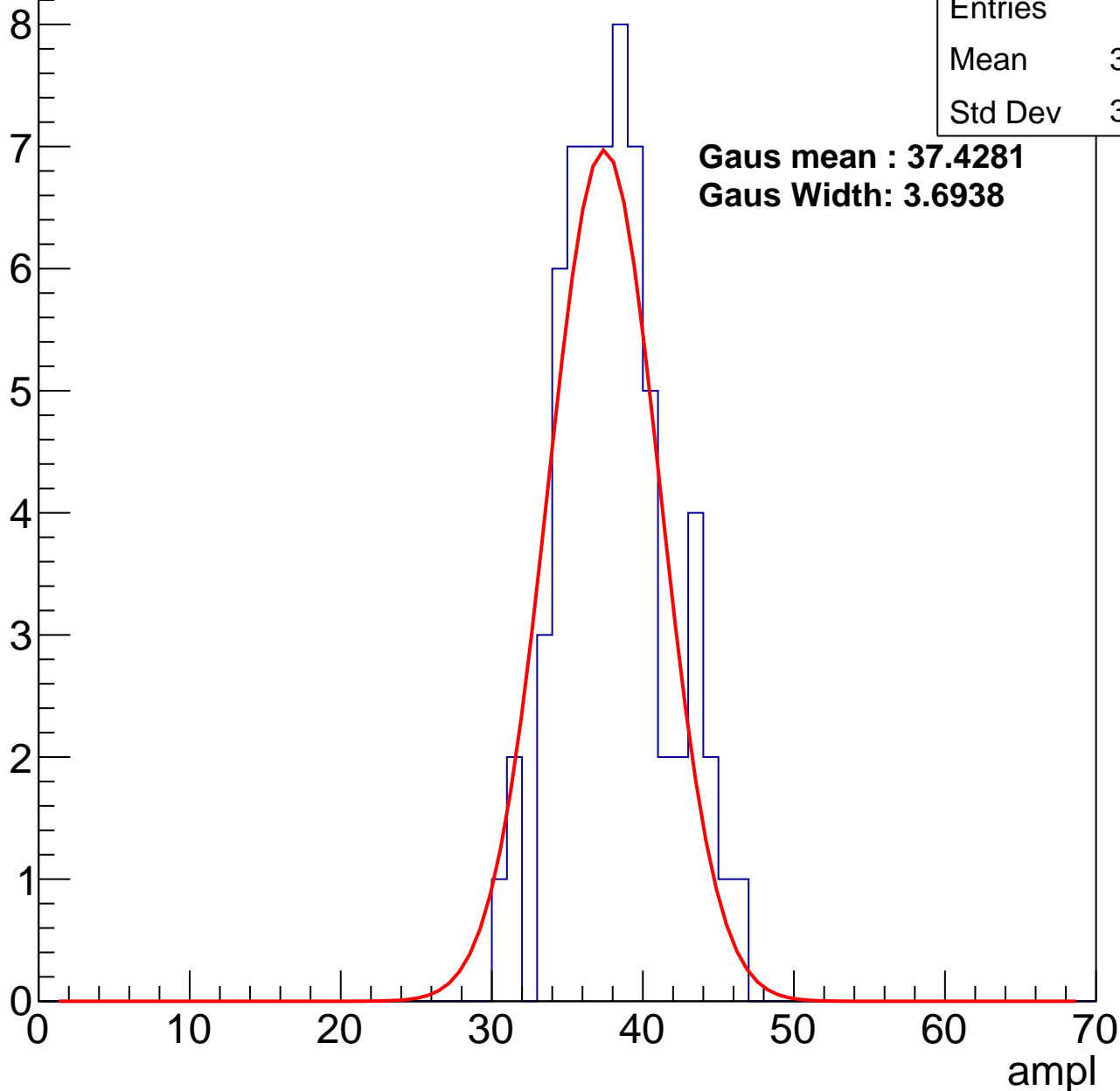
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.62
Std Dev	3.472

Gaus mean : 37.4281

Gaus Width: 3.6938



B0L002S, U12-ch74, adc2

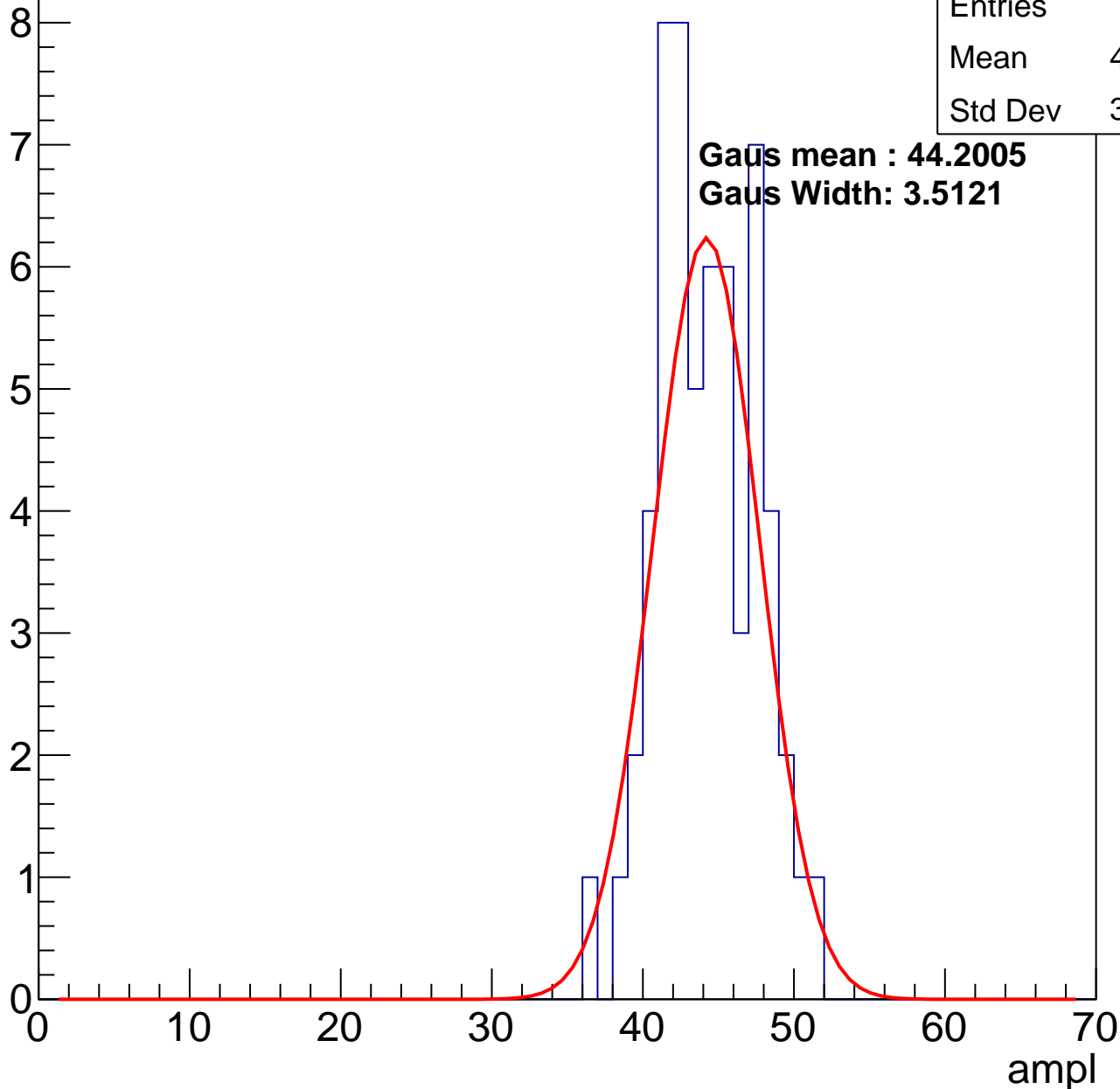
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43.78
Std Dev	3.189

Gaus mean : 44.2005

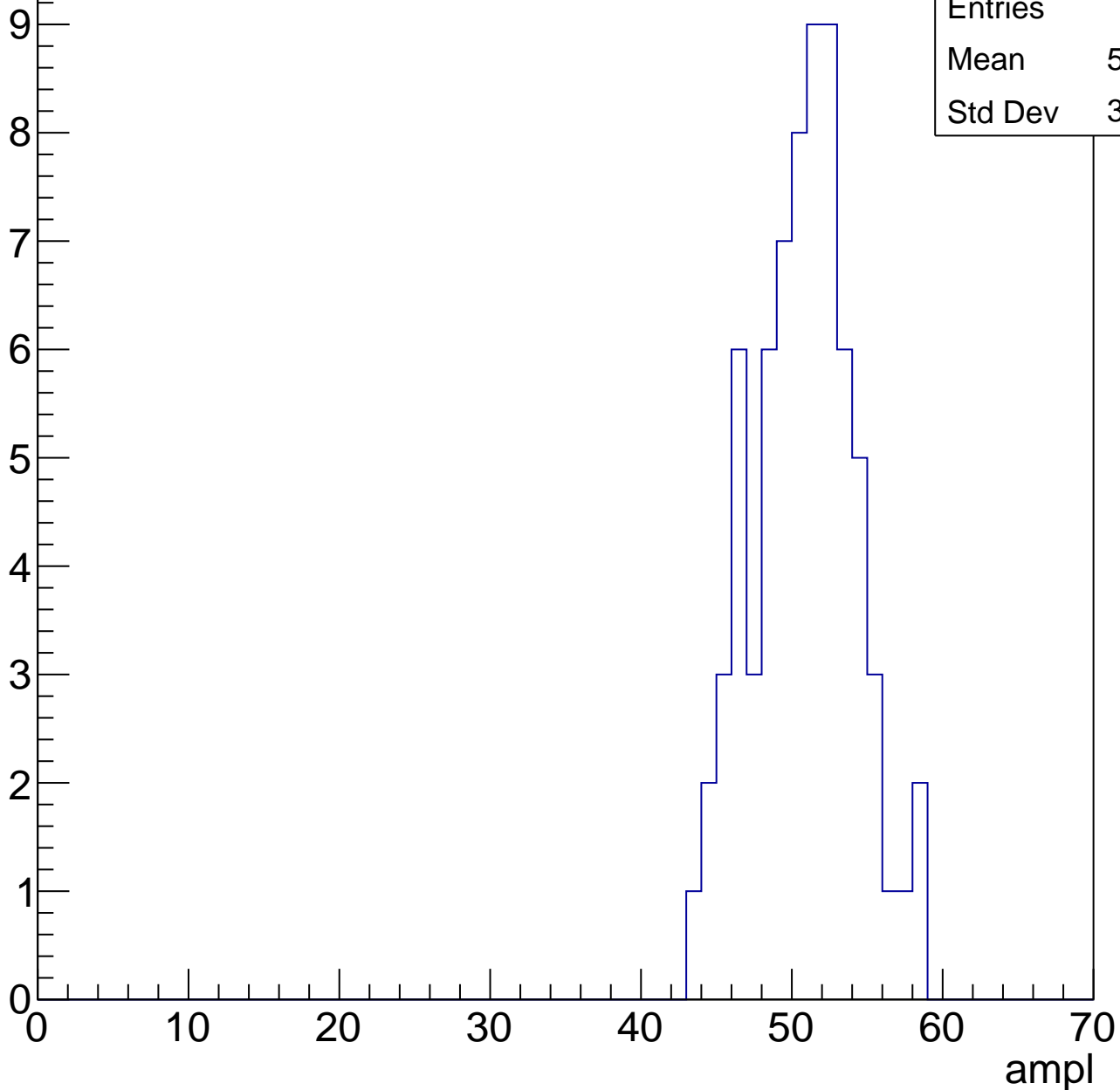
Gaus Width: 3.5121



B0L002S, U12-ch74, adc3

calib_packv5_042523_0143.root, FC#8, port C1

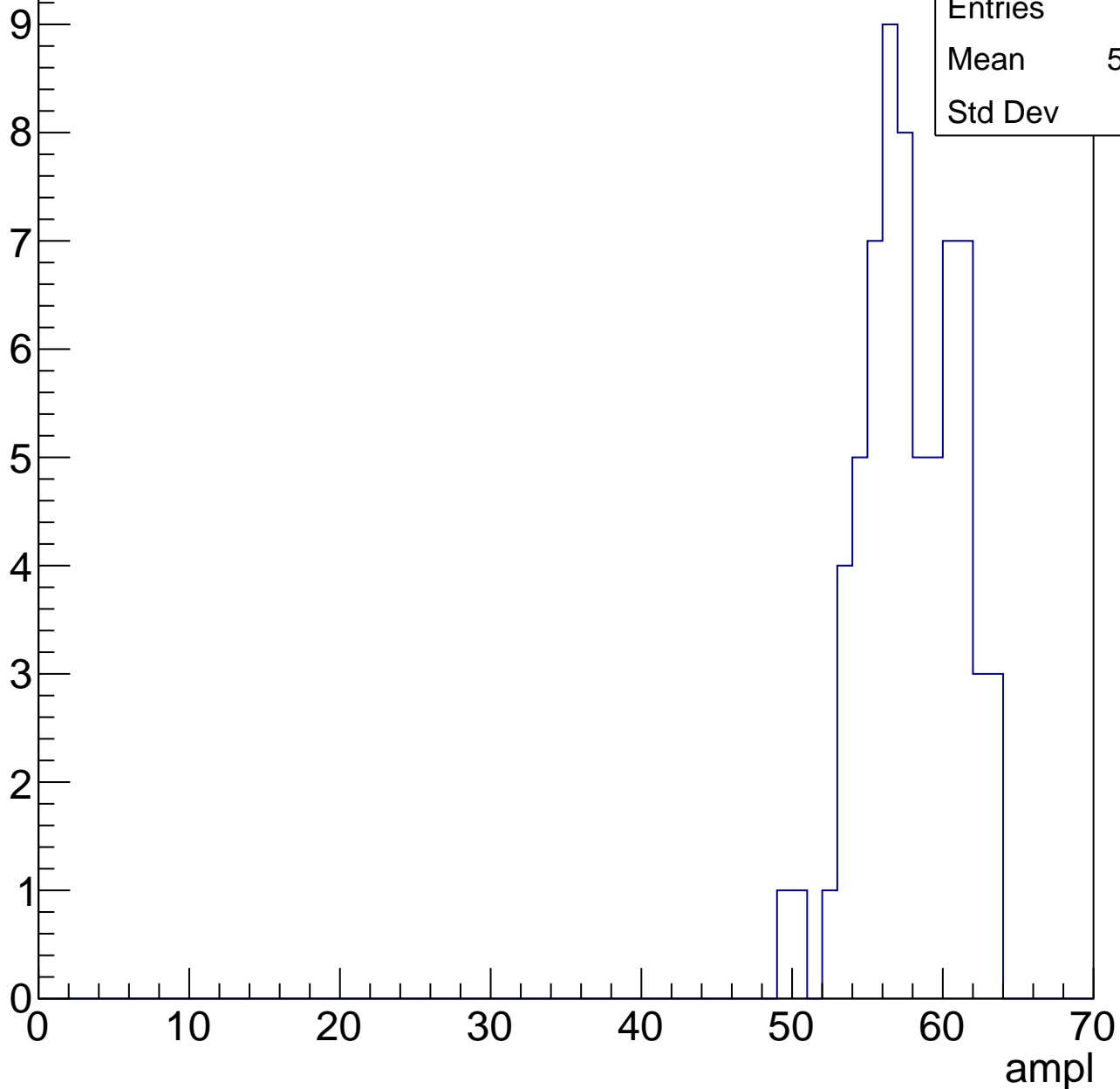
Entry



B0L002S, U12-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



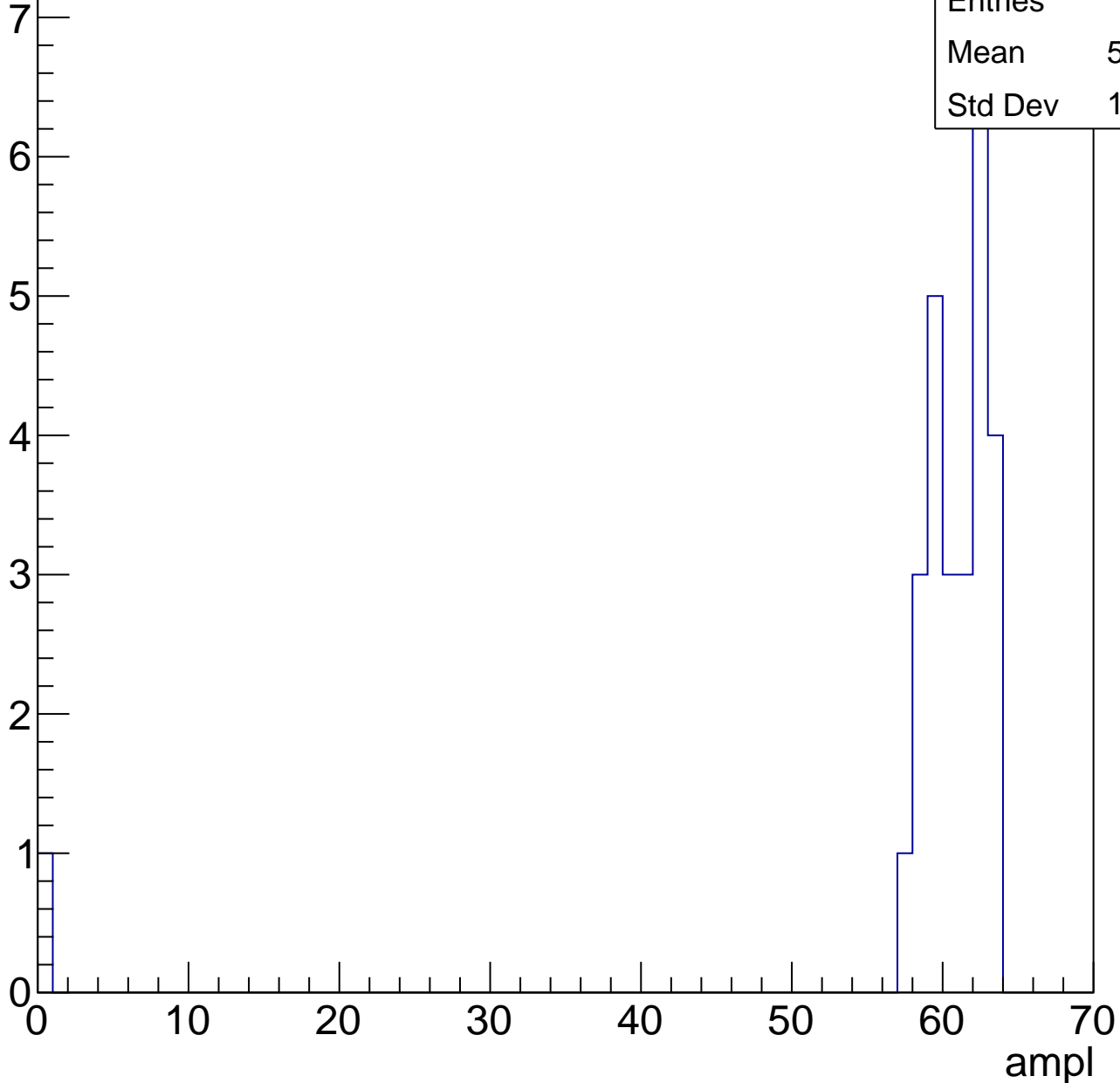
Entries	66
Mean	57.35
Std Dev	3.16

B0L002S, U12-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.33
Std Dev	11.58



B0L002S, U12-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch75, adc0

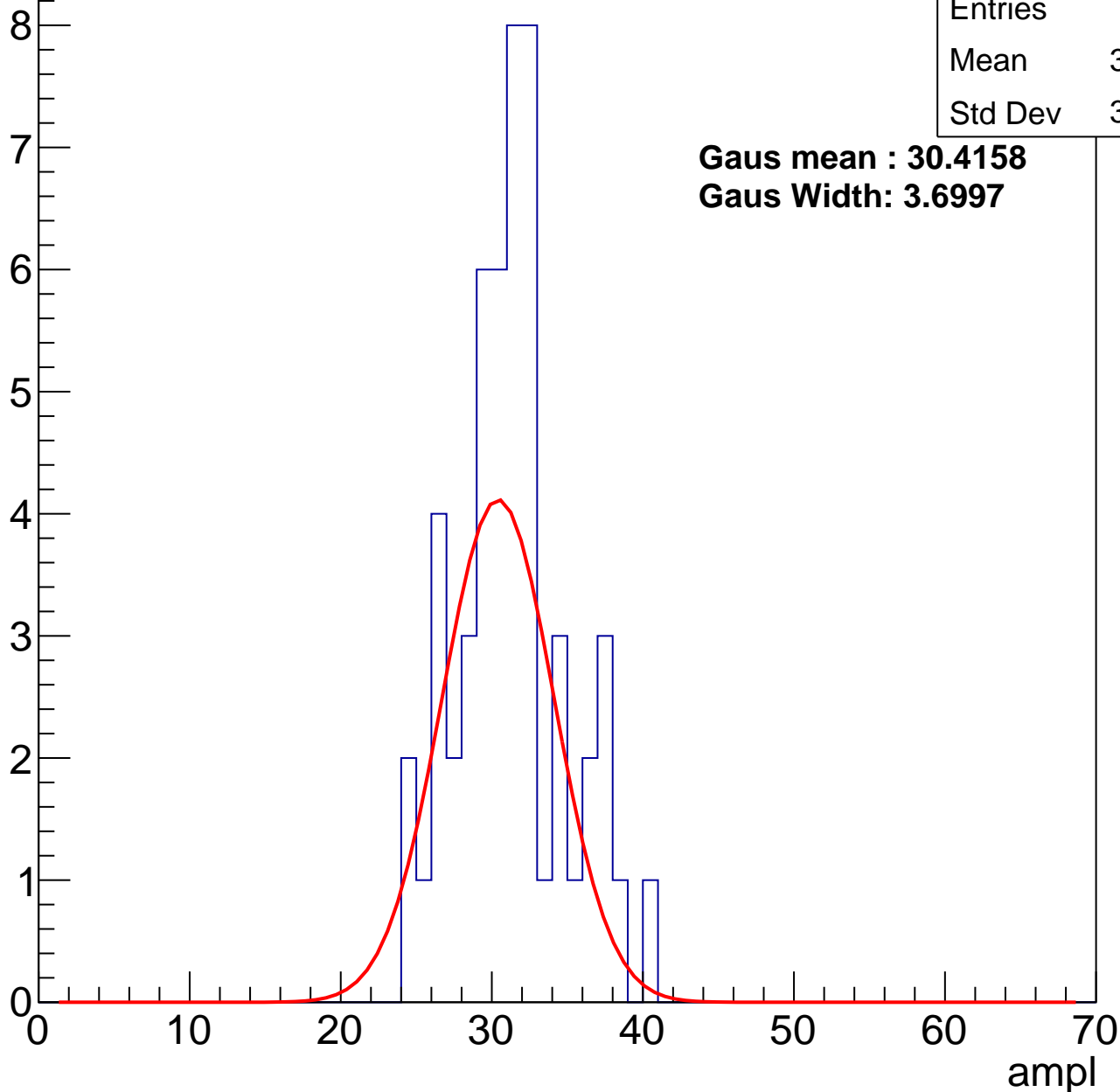
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	30.85
Std Dev	3.575

Gaus mean : 30.4158

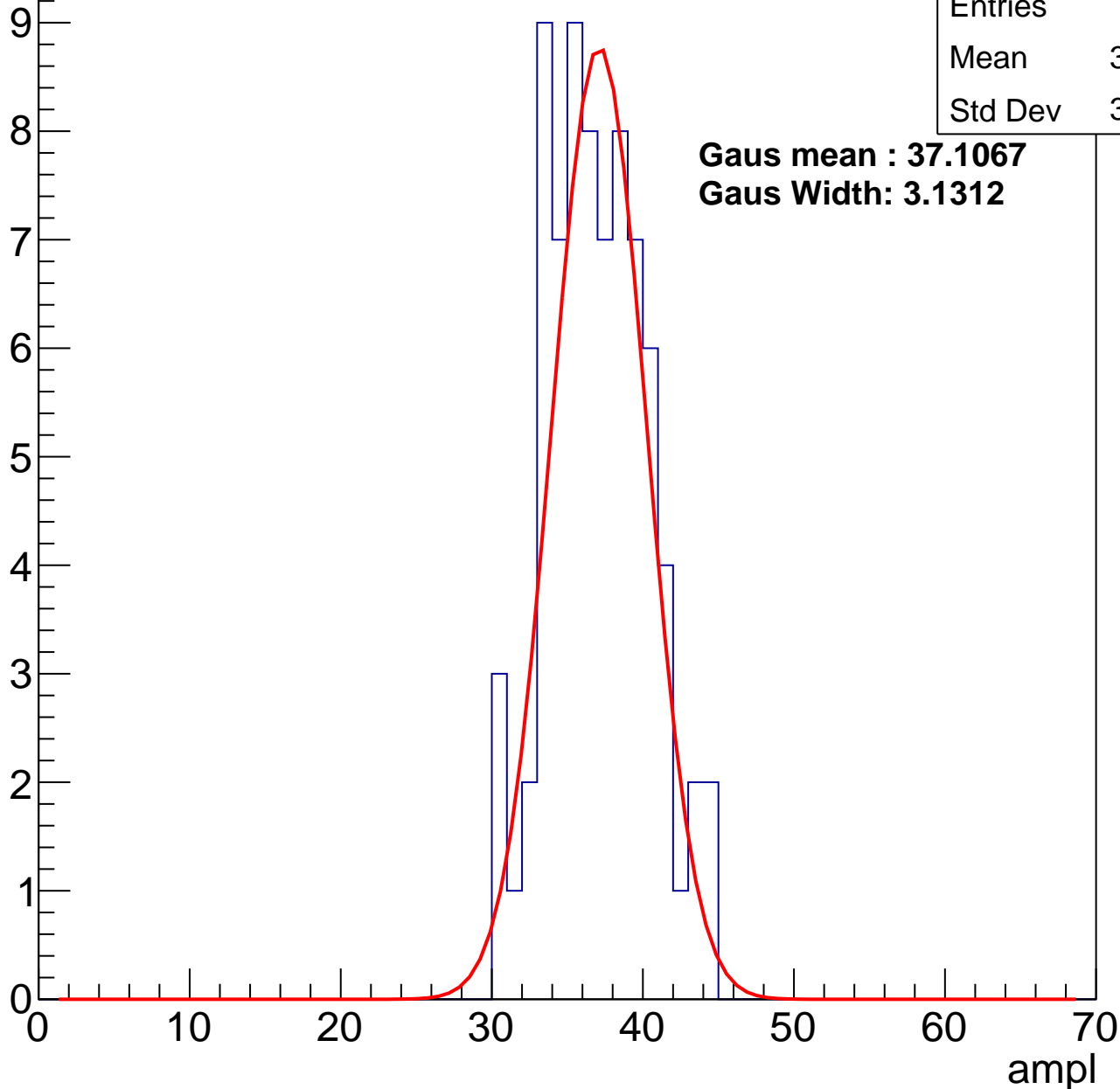
Gaus Width: 3.6997



B0L002S, U12-ch75, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch75, adc2

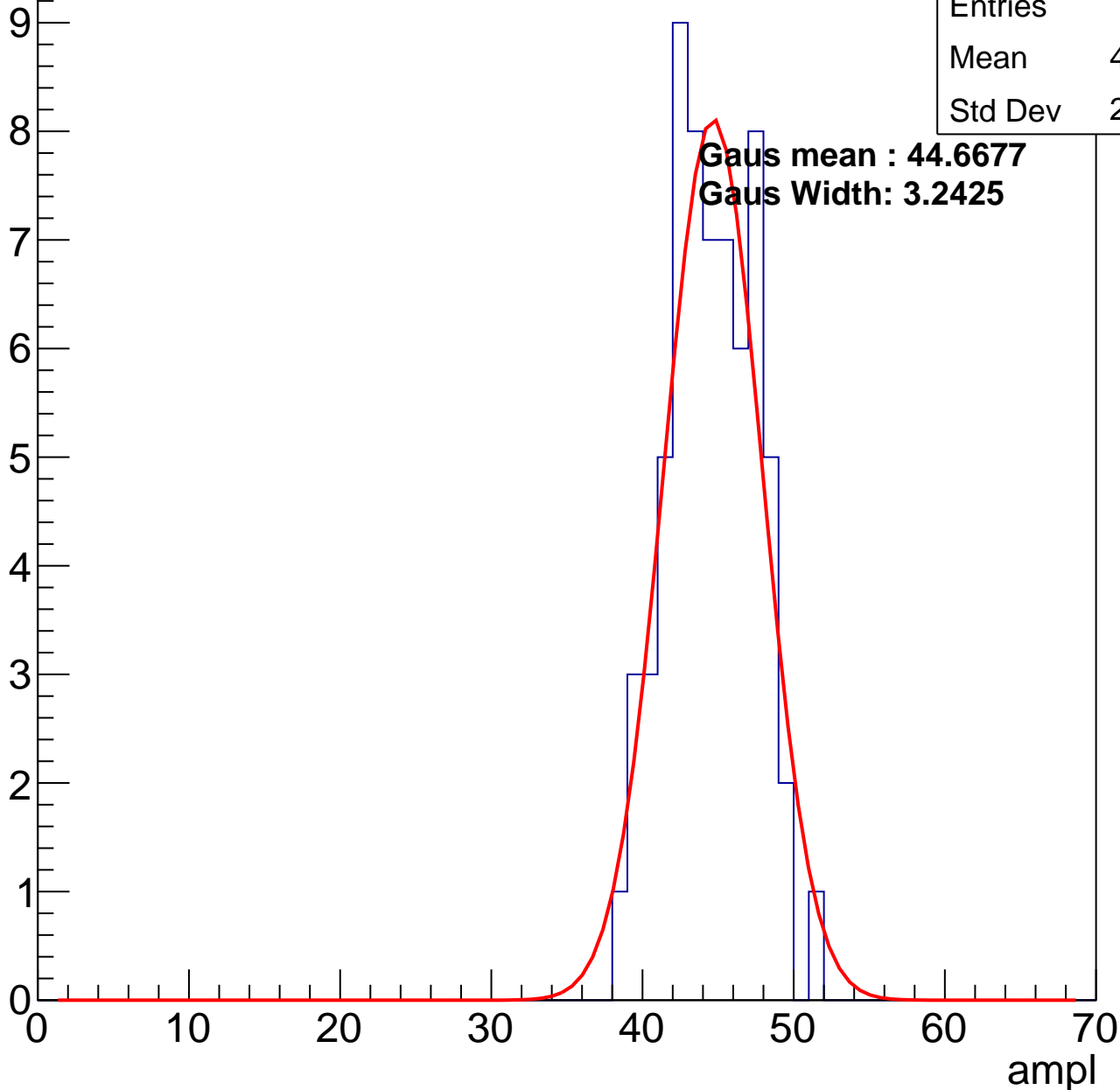
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.09
Std Dev	2.854

Gaus mean : 44.6677

Gaus Width: 3.2425

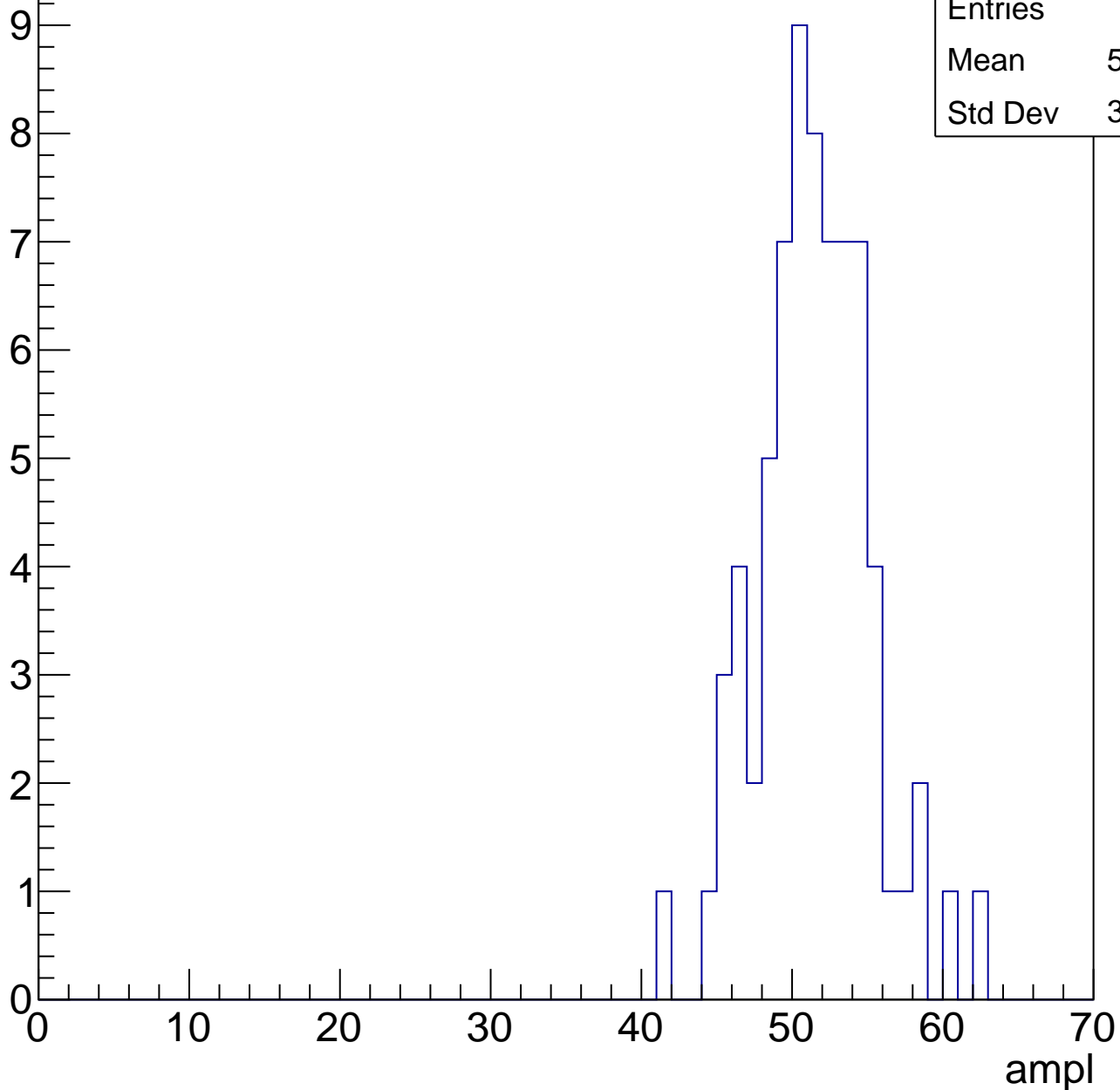


B0L002S, U12-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	51.03
Std Dev	3.742

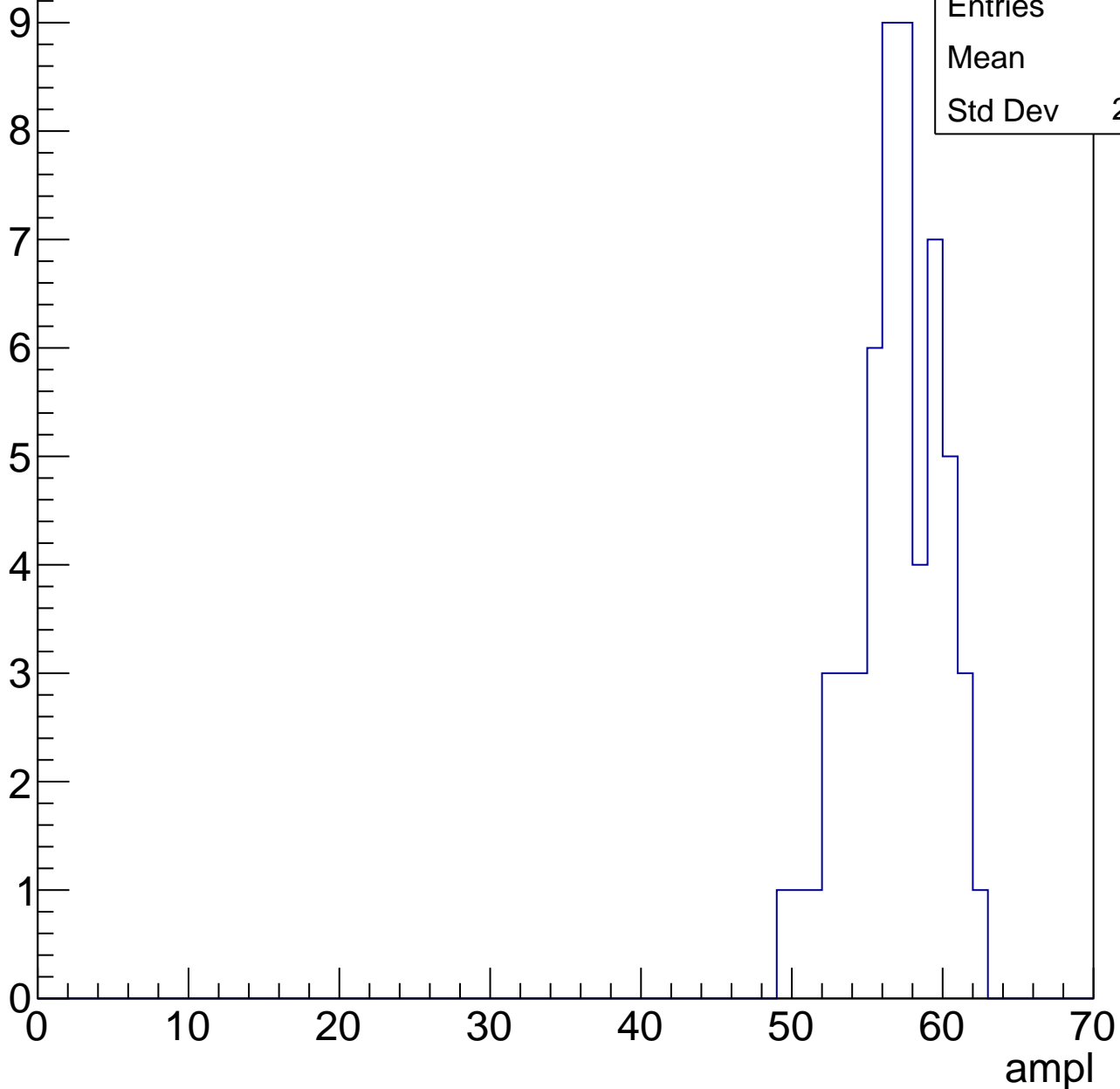


B0L002S, U12-ch75, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.5
Std Dev	2.891

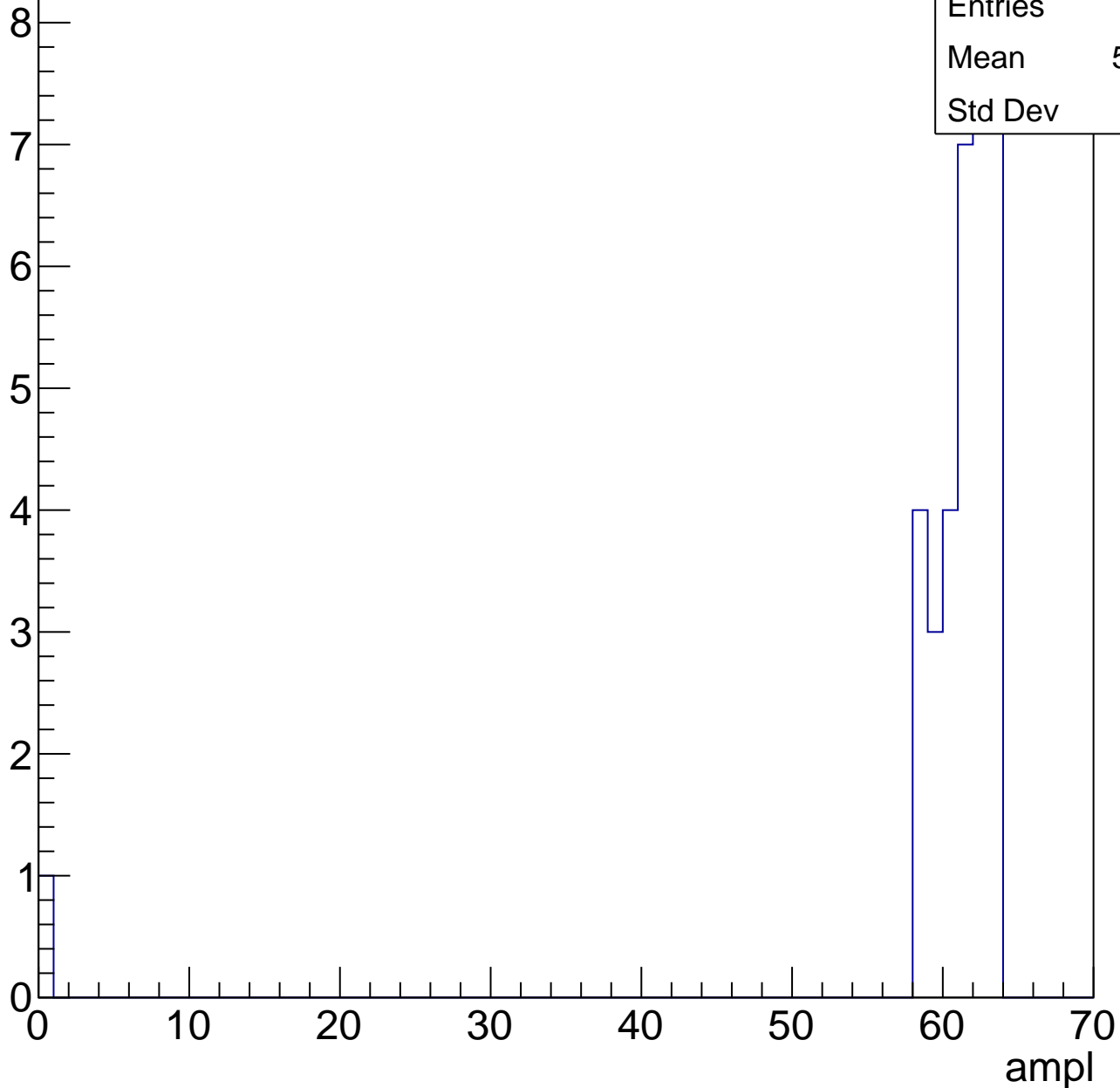


B0L002S, U12-ch75, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.31
Std Dev	10.3



B0L002S, U12-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

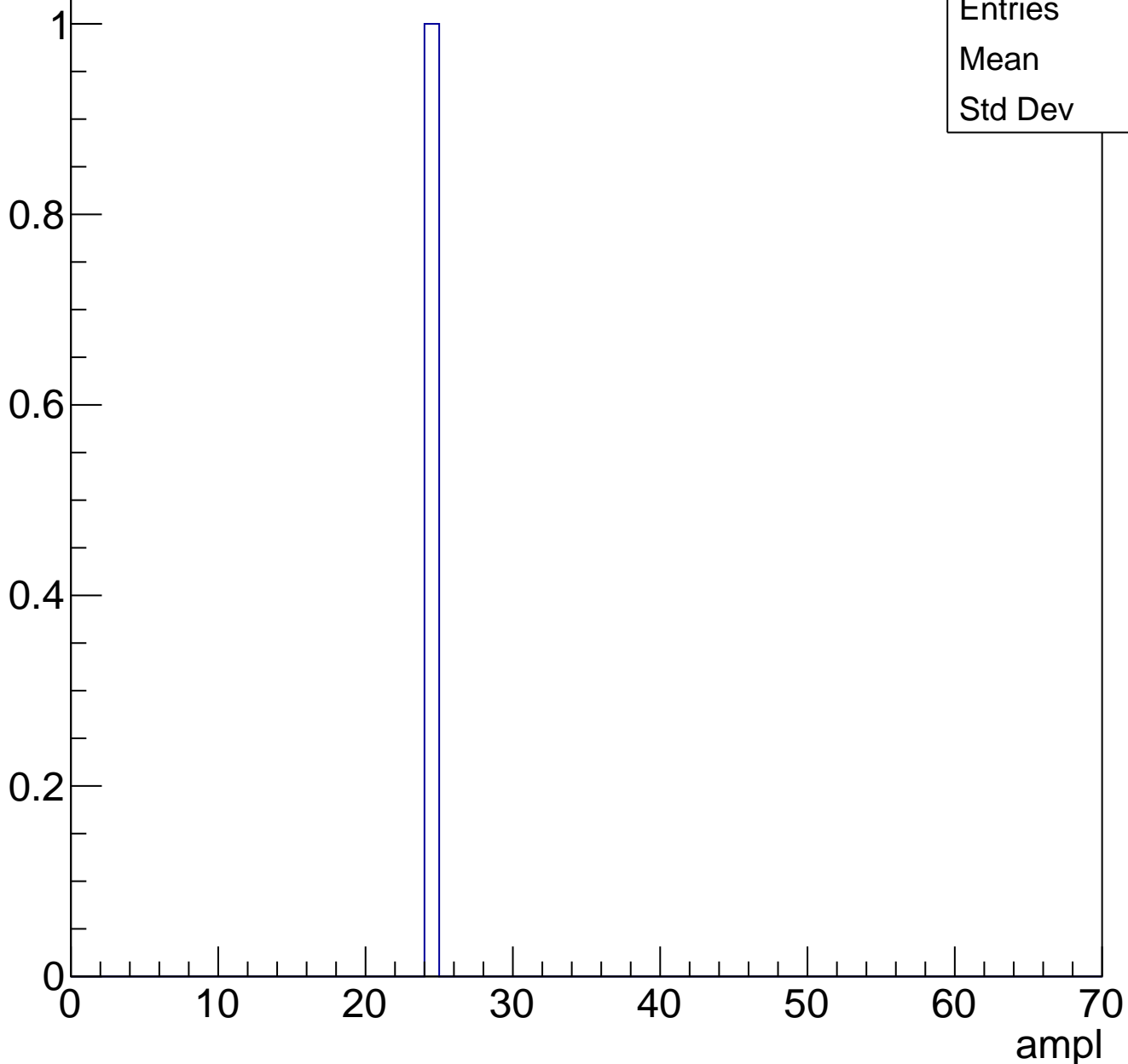
Entry



B0L002S, U12-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U12-ch76, adc0

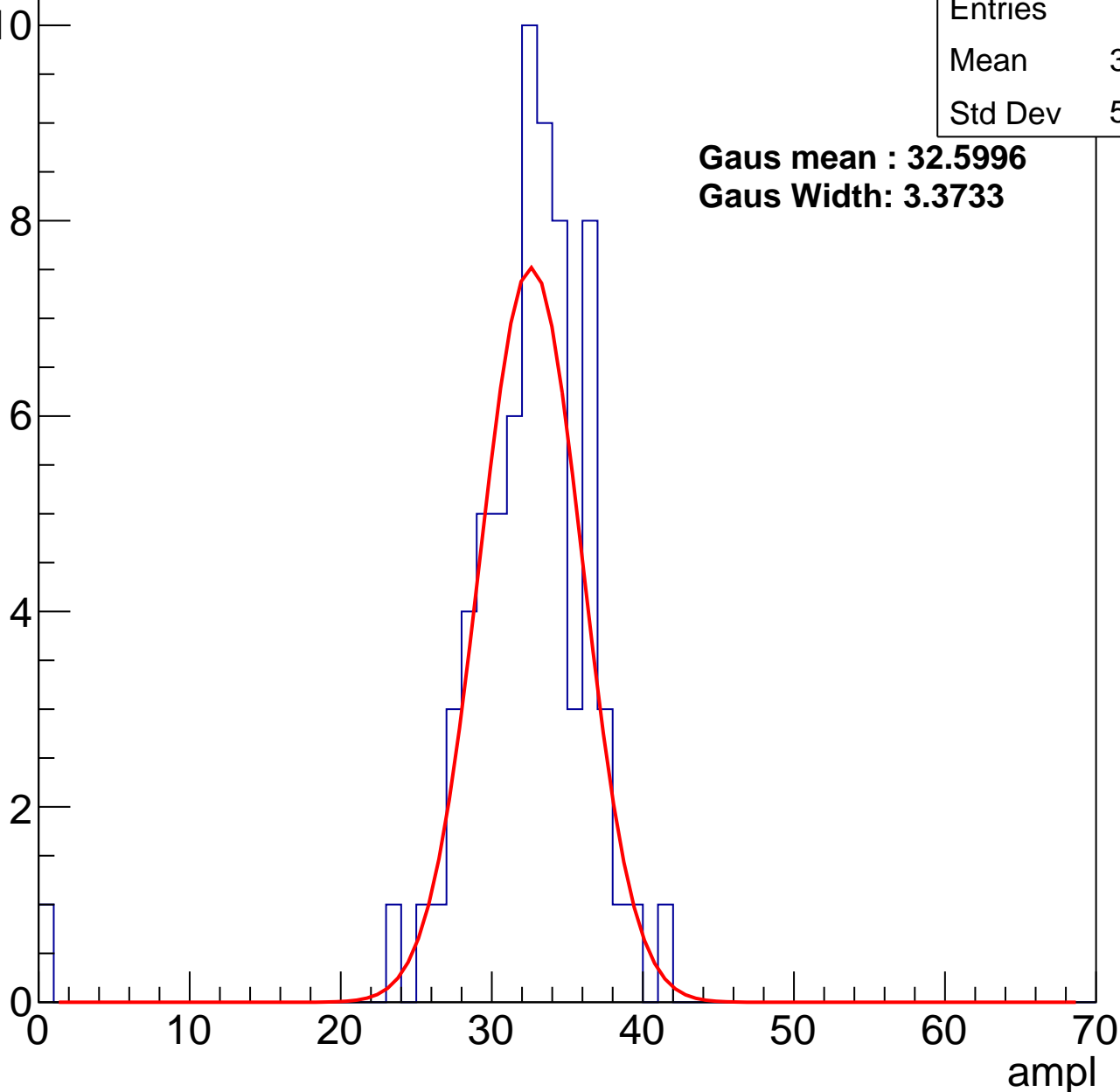
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.82
Std Dev	5.083

Gaus mean : 32.5996

Gaus Width: 3.3733



B0L002S, U12-ch76, adc1

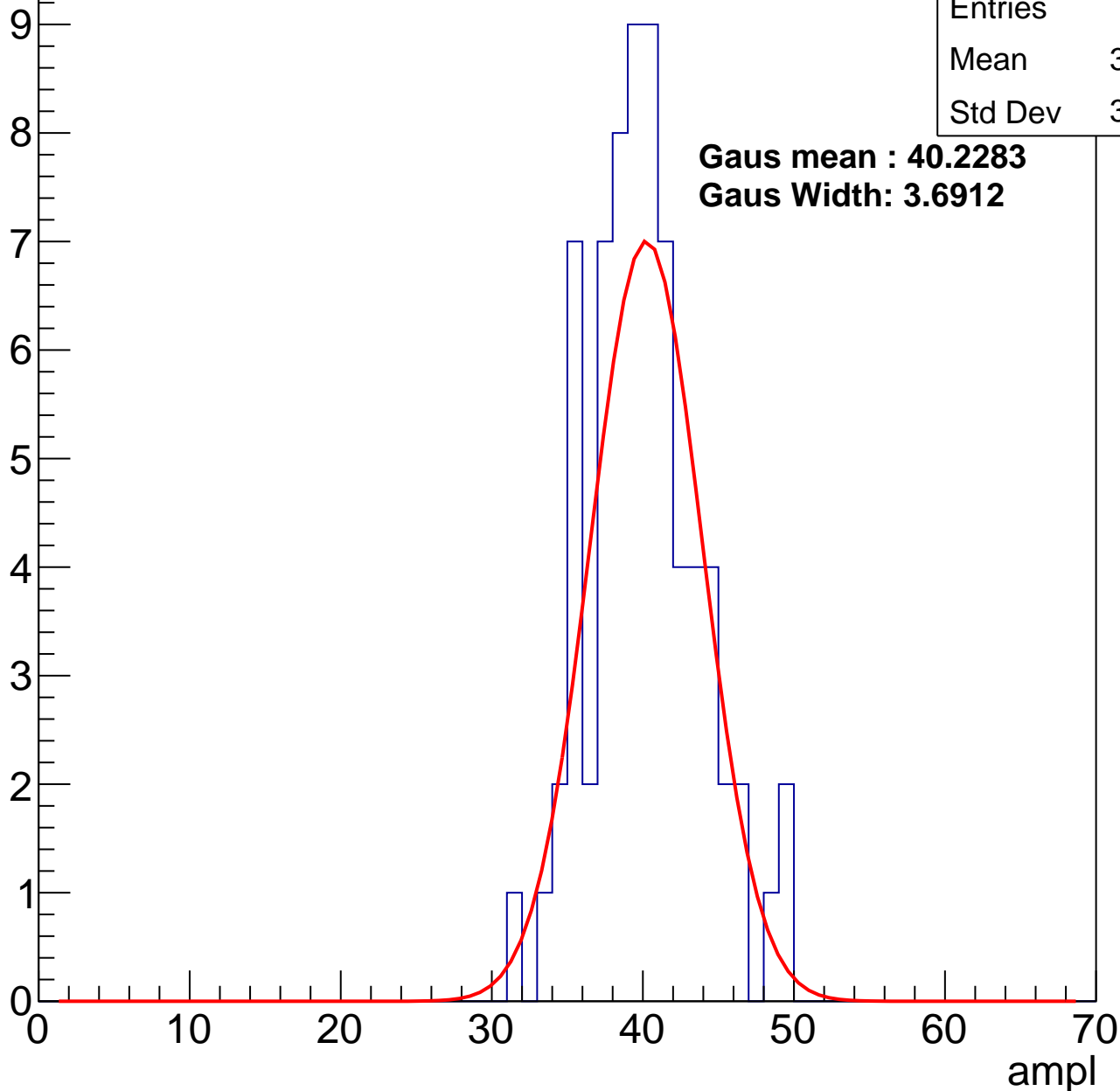
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	39.64
Std Dev	3.679

Gaus mean : 40.2283

Gaus Width: 3.6912



B0L002S, U12-ch76, adc2

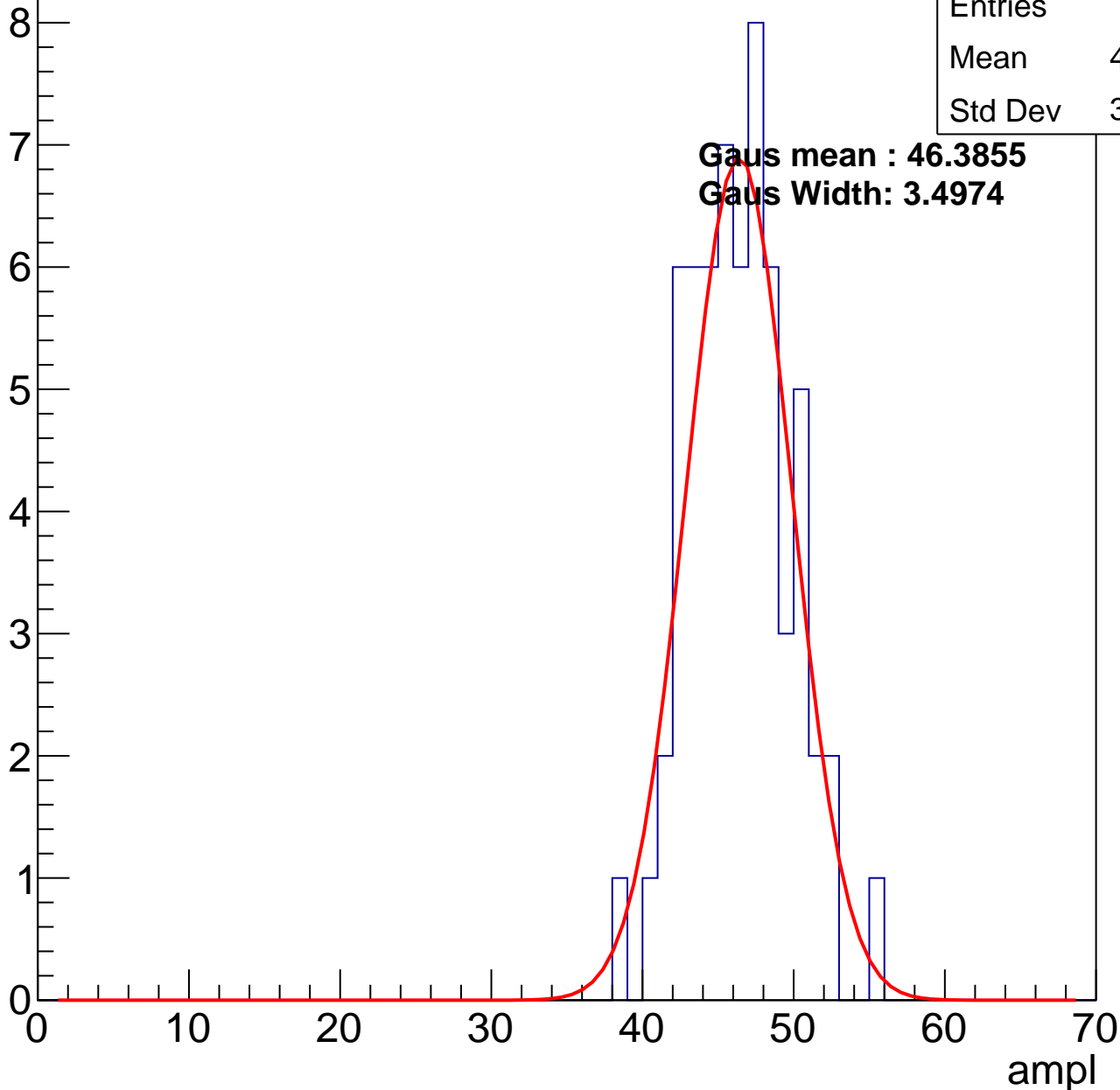
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.92
Std Dev	3.294

Gaus mean : 46.3855

Gaus Width: 3.4974

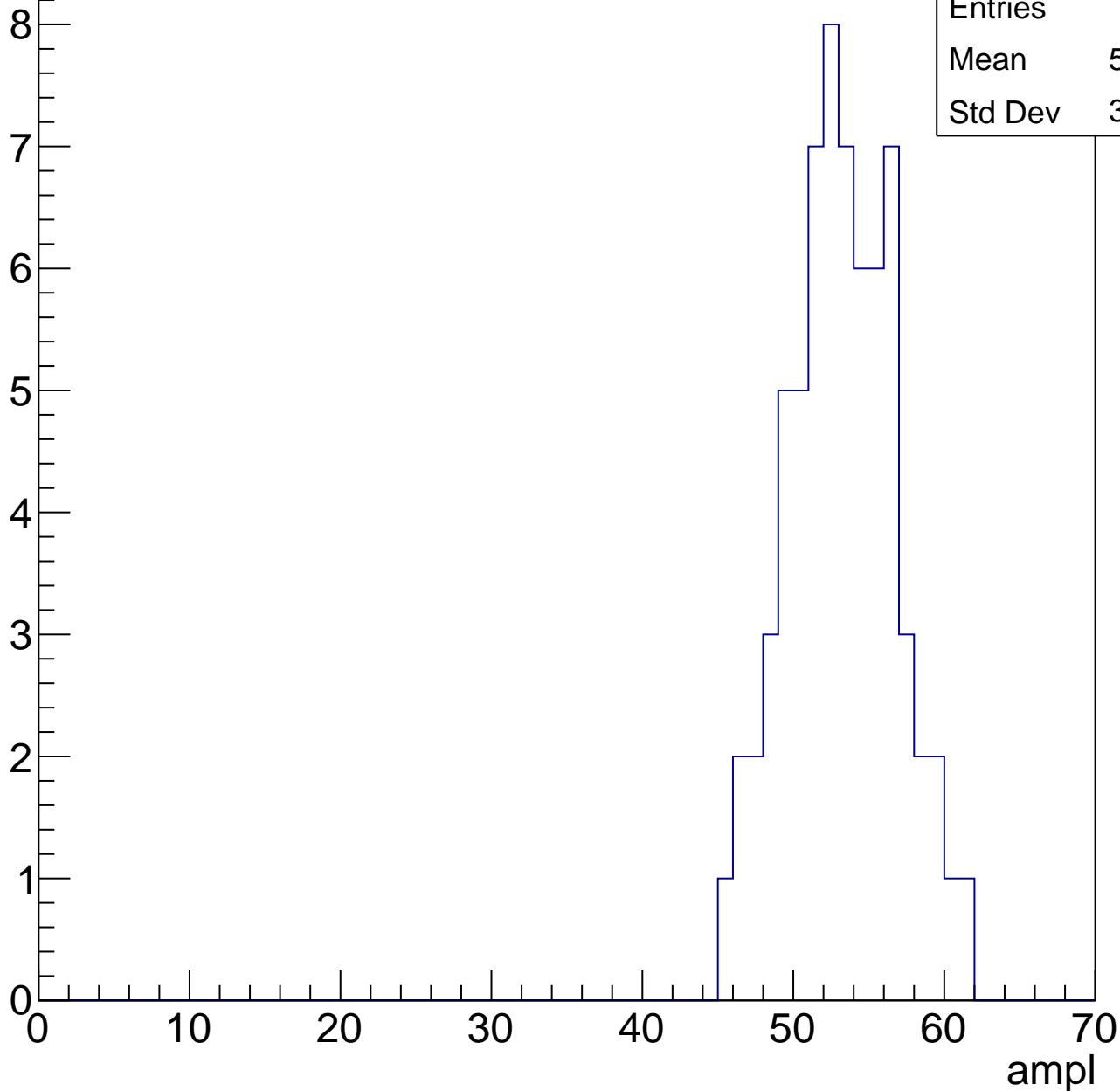


B0L002S, U12-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	52.74
Std Dev	3.517

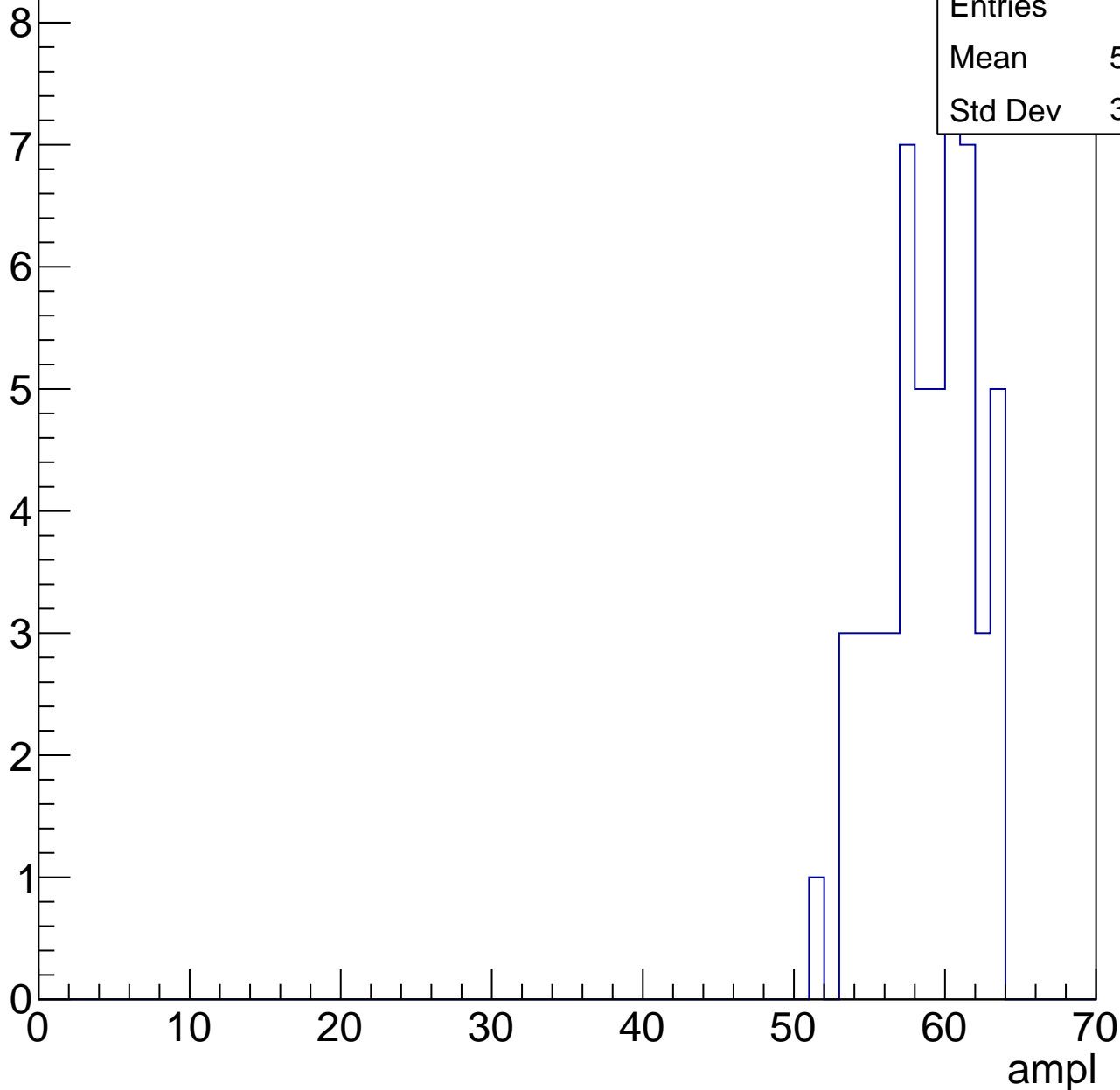


B0L002S, U12-ch76, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	58.43
Std Dev	3.013

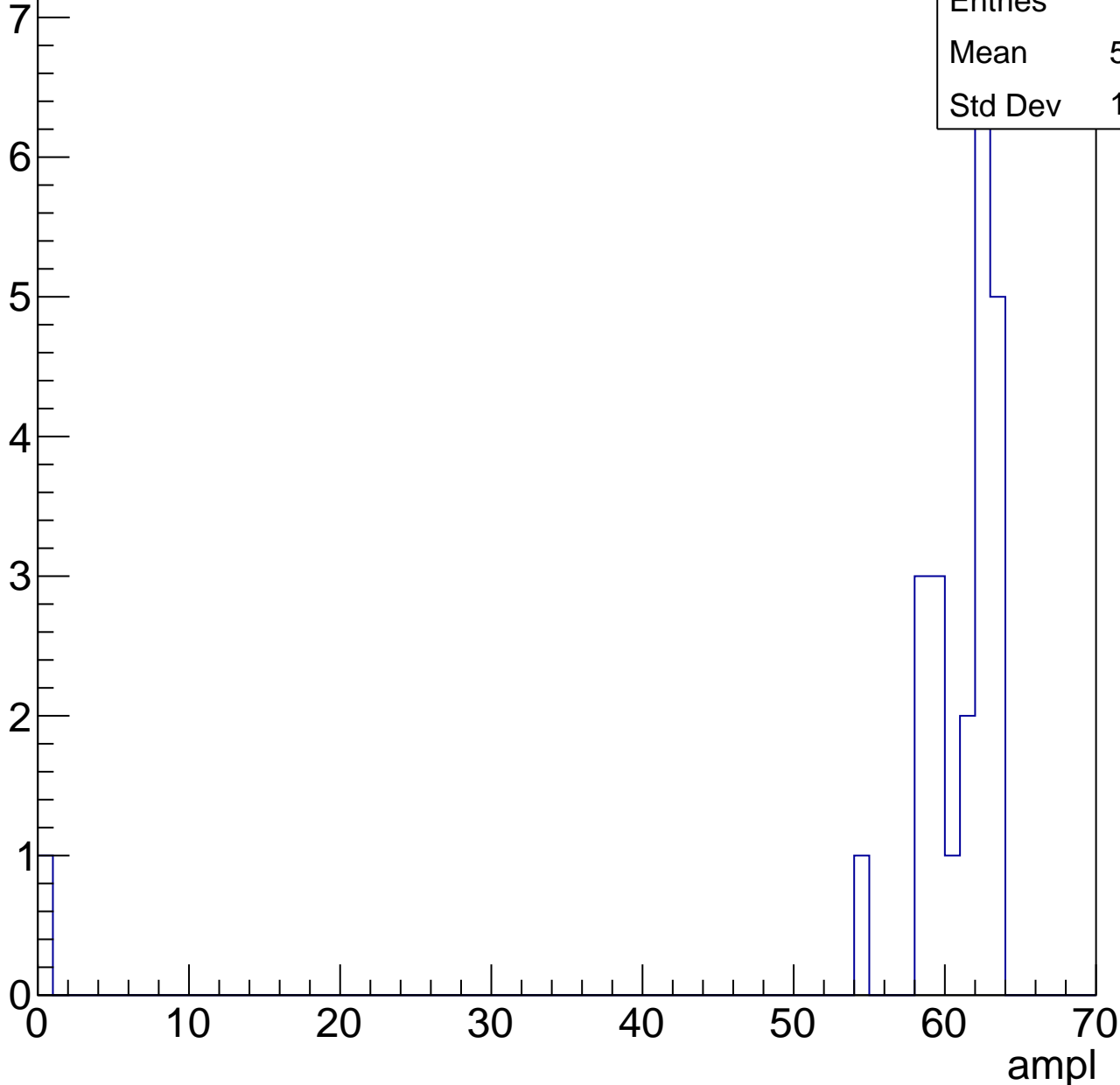


B0L002S, U12-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.09
Std Dev	12.58



B0L002S, U12-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch77, adc0

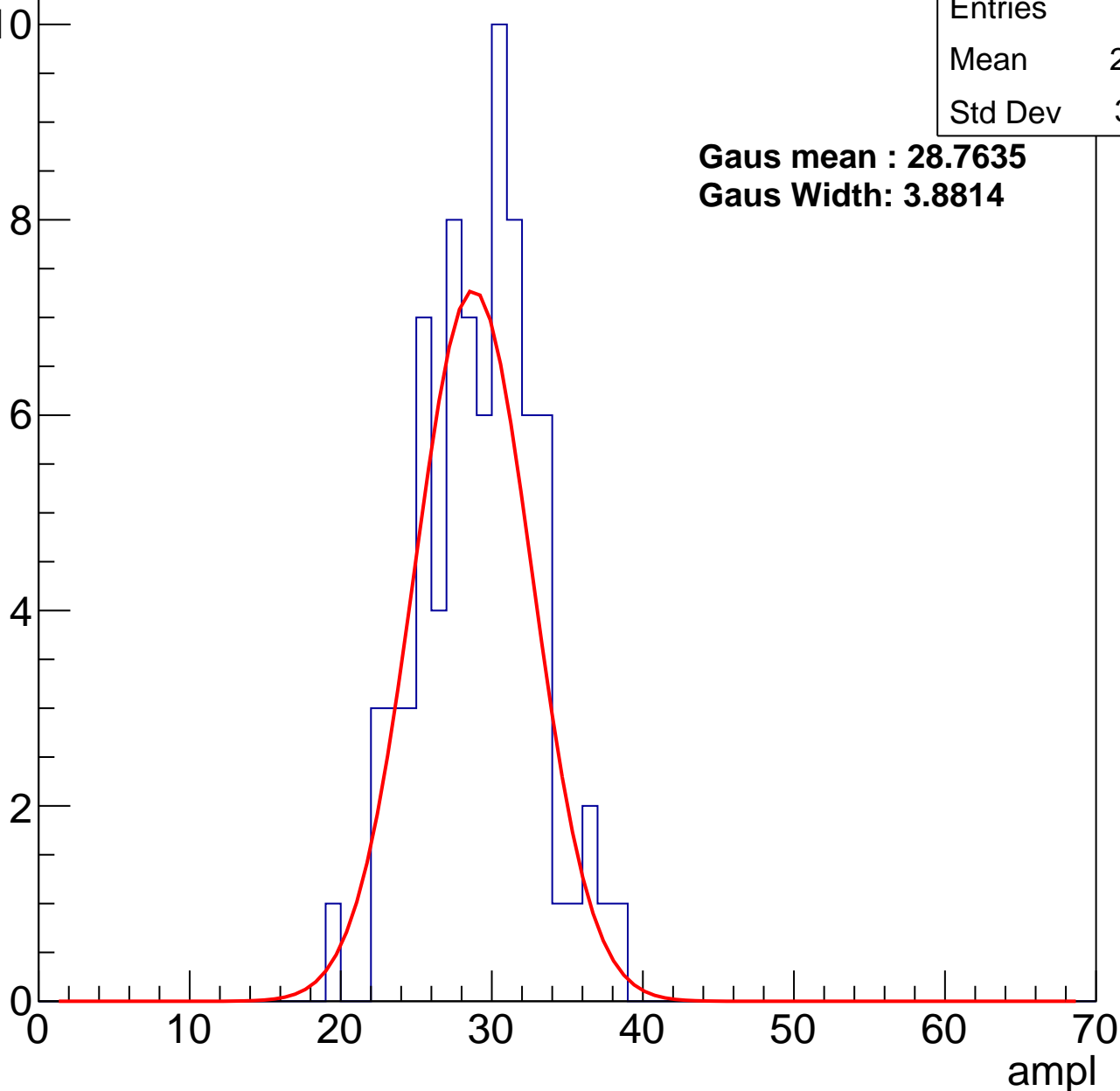
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	28.78
Std Dev	3.771

Gaus mean : 28.7635

Gaus Width: 3.8814



B0L002S, U12-ch77, adc1

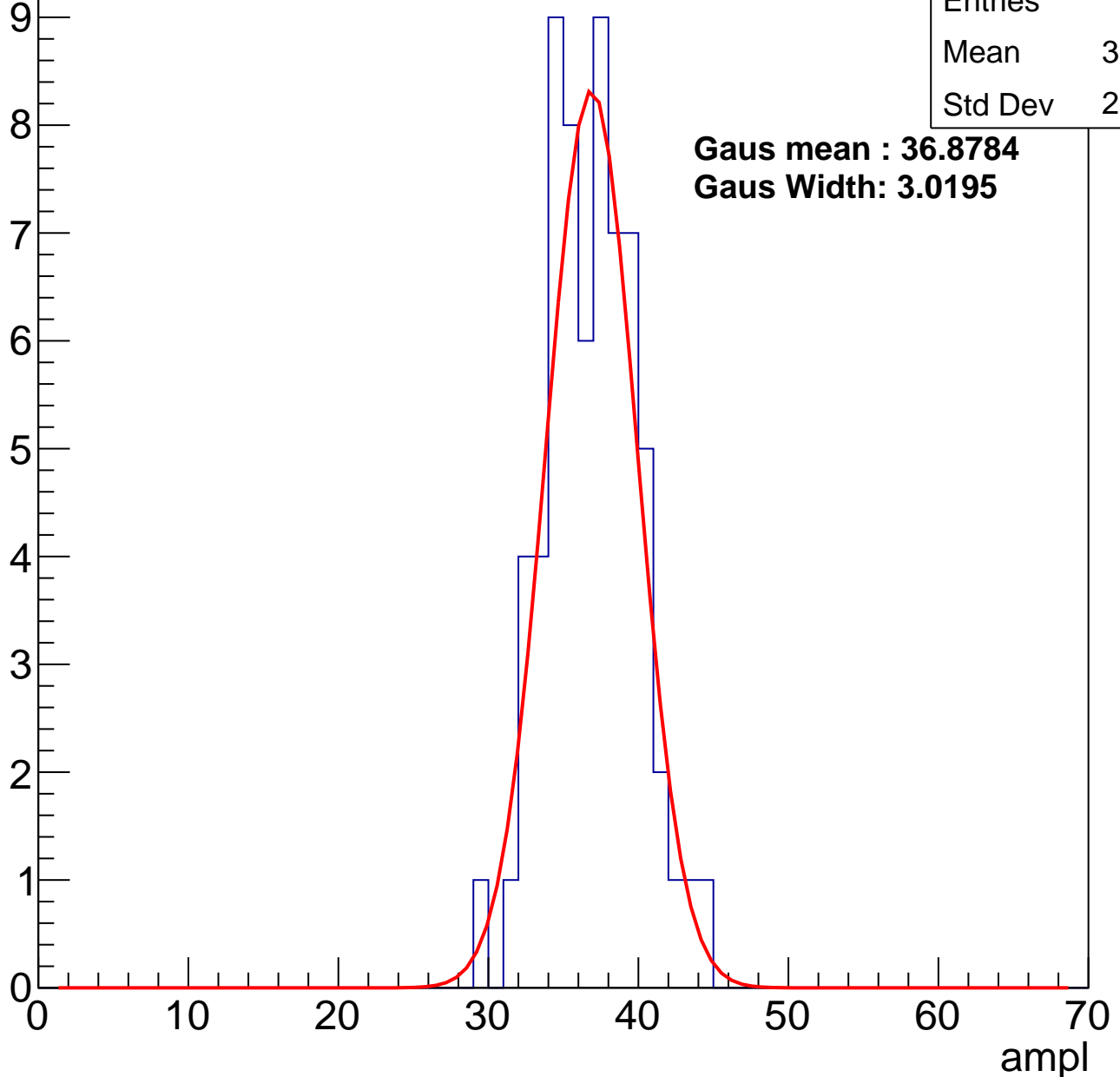
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.44
Std Dev	2.975

Gaus mean : 36.8784

Gaus Width: 3.0195



B0L002S, U12-ch77, adc2

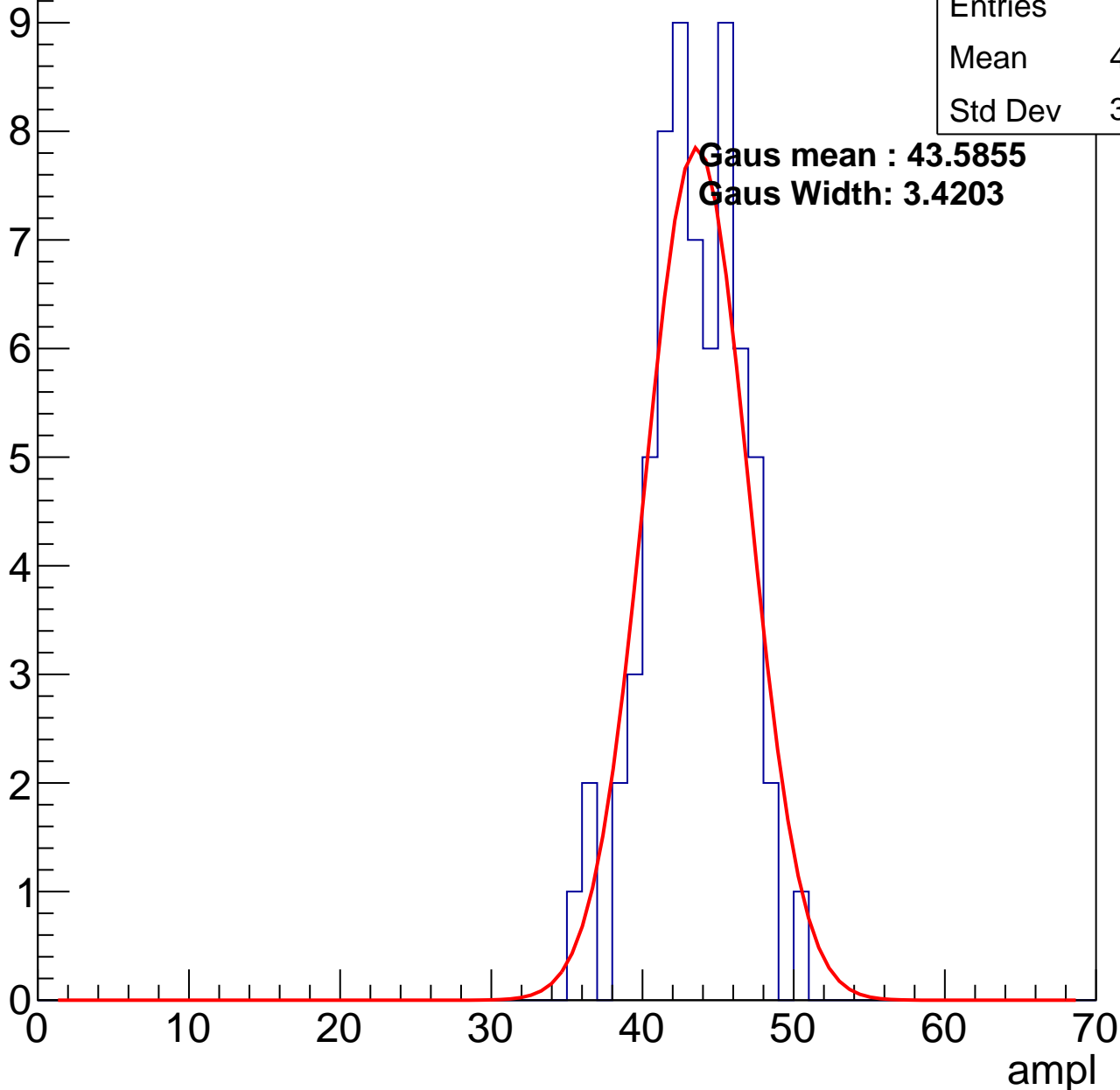
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.92
Std Dev	3.066

Gaus mean : 43.5855

Gaus Width: 3.4203

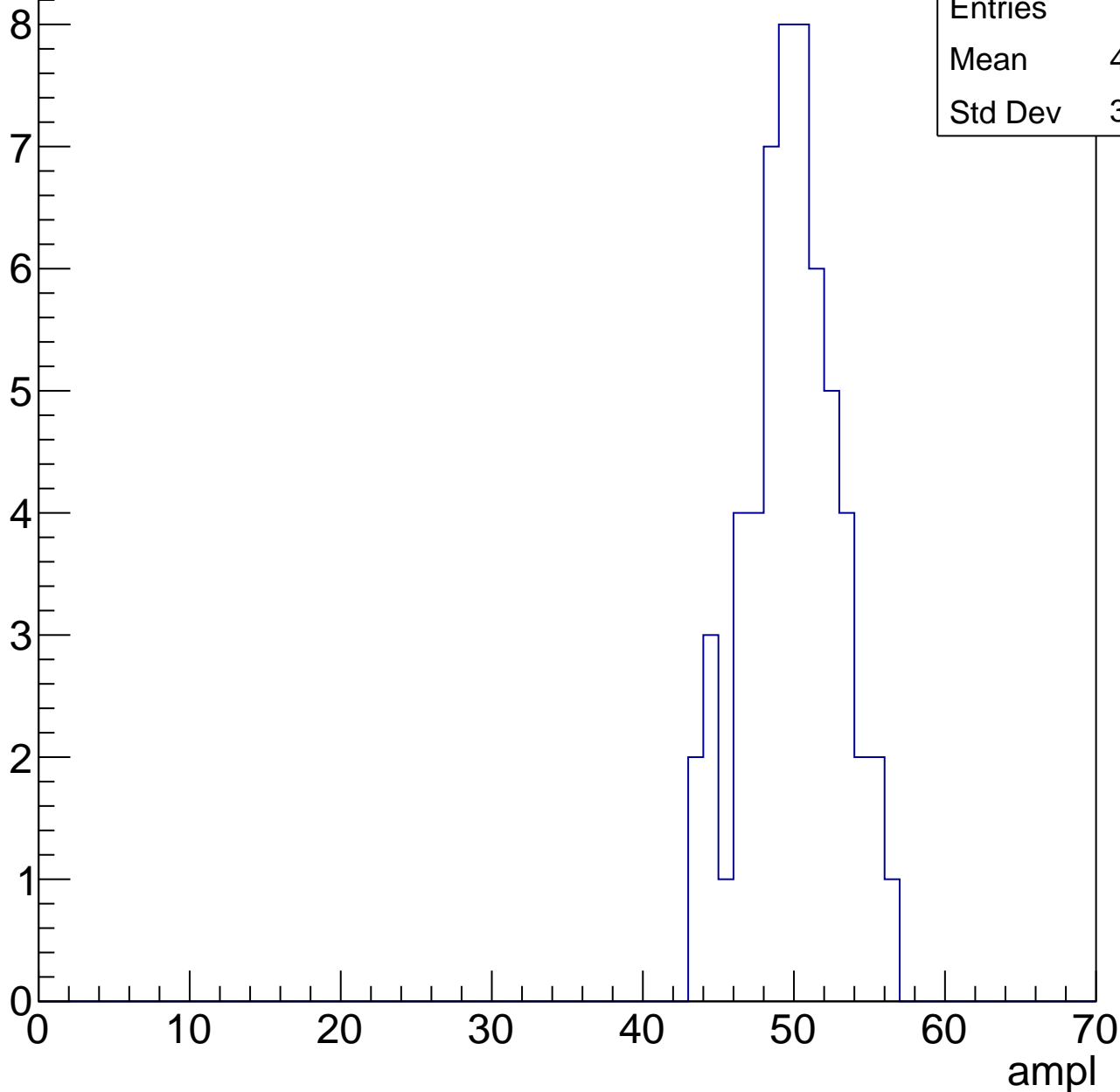


B0L002S, U12-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49.39
Std Dev	3.036



B0L002S, U12-ch77, adc4

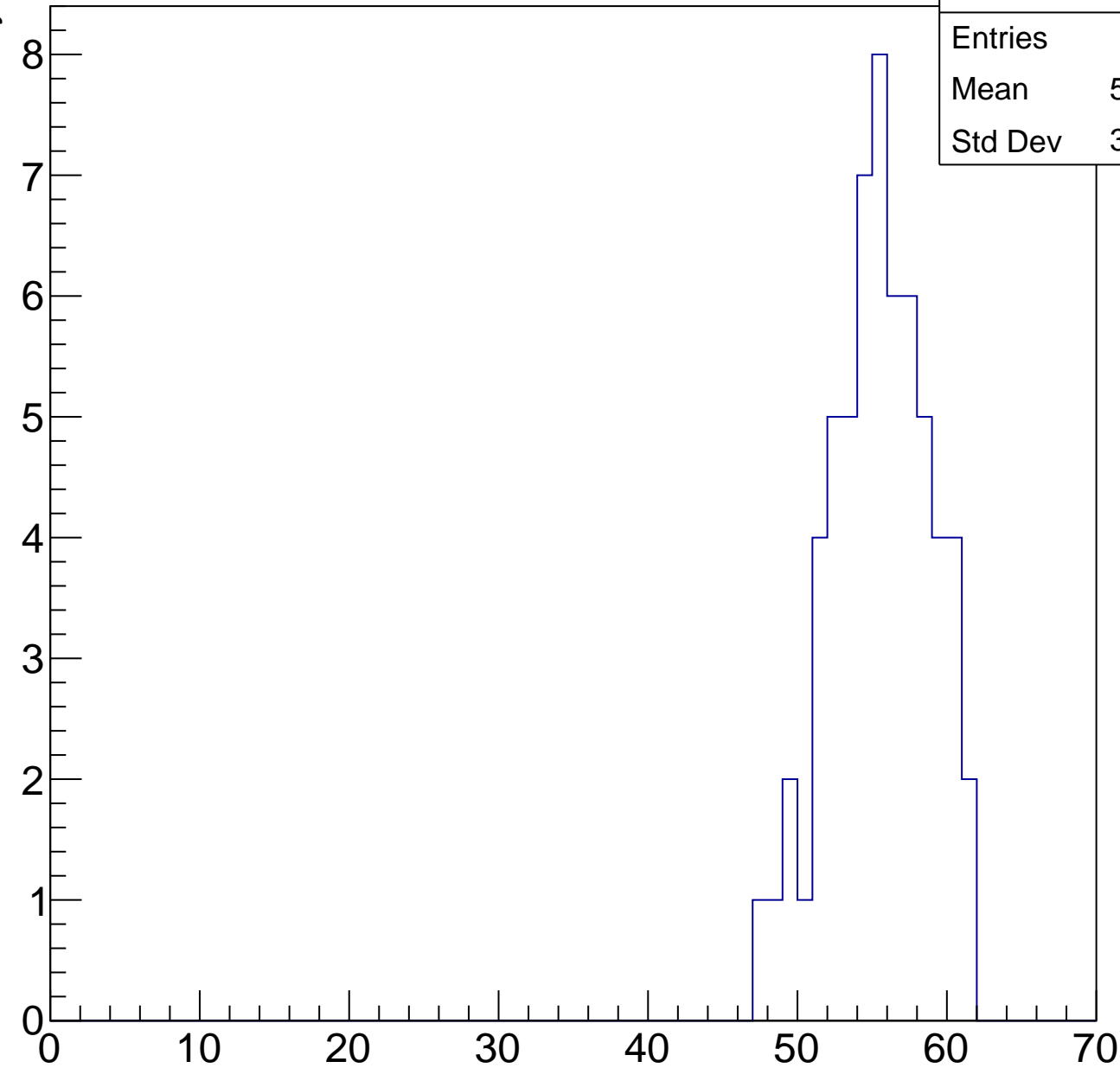
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	55.02
Std Dev	3.282

ampl



B0L002S, U12-ch77, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	52
Mean	58.85
Std Dev	8.599

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B0L002S, U12-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch77, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch78, adc0

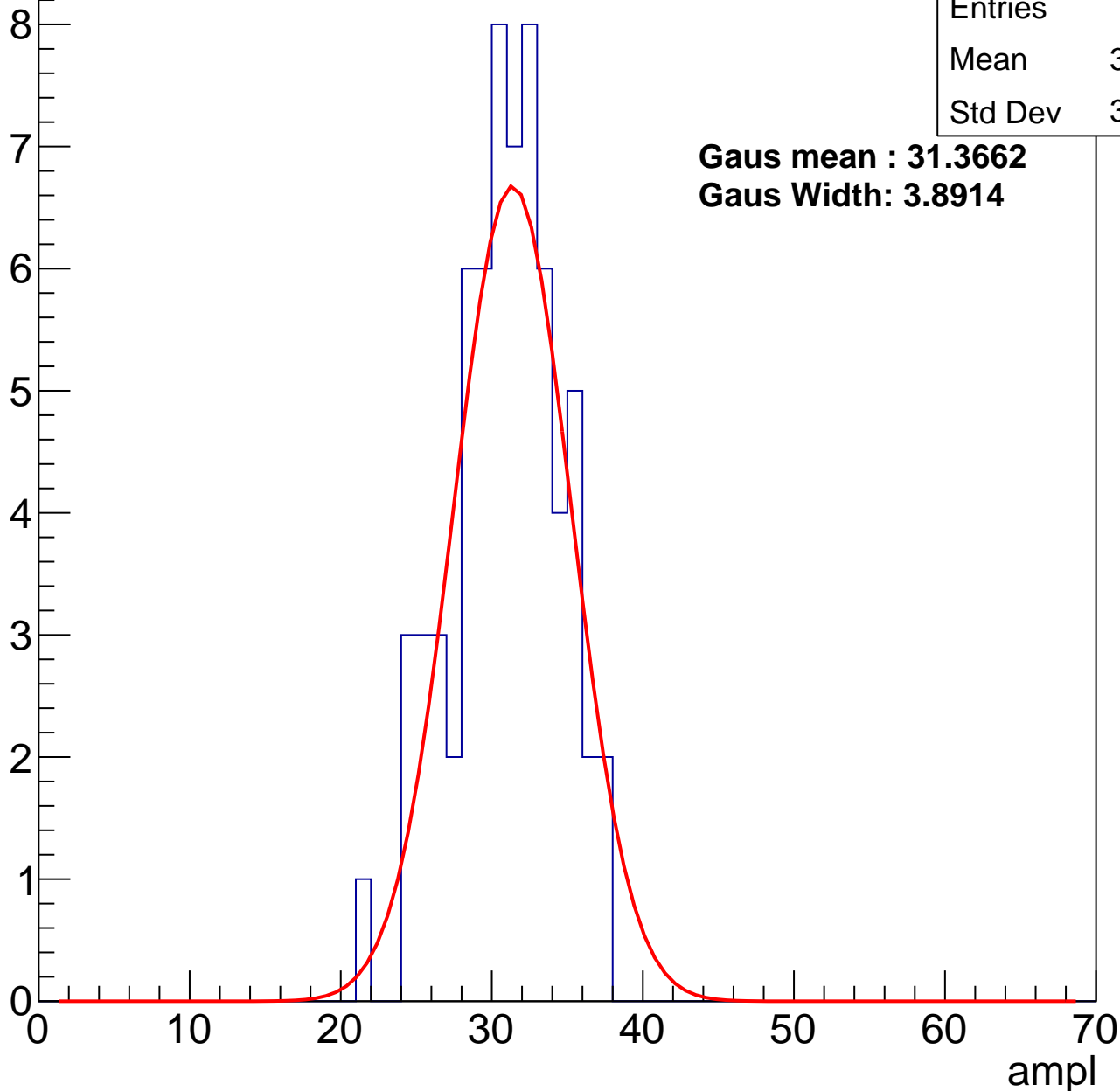
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.45
Std Dev	3.478

Gaus mean : 31.3662

Gaus Width: 3.8914



B0L002S, U12-ch78, adc1

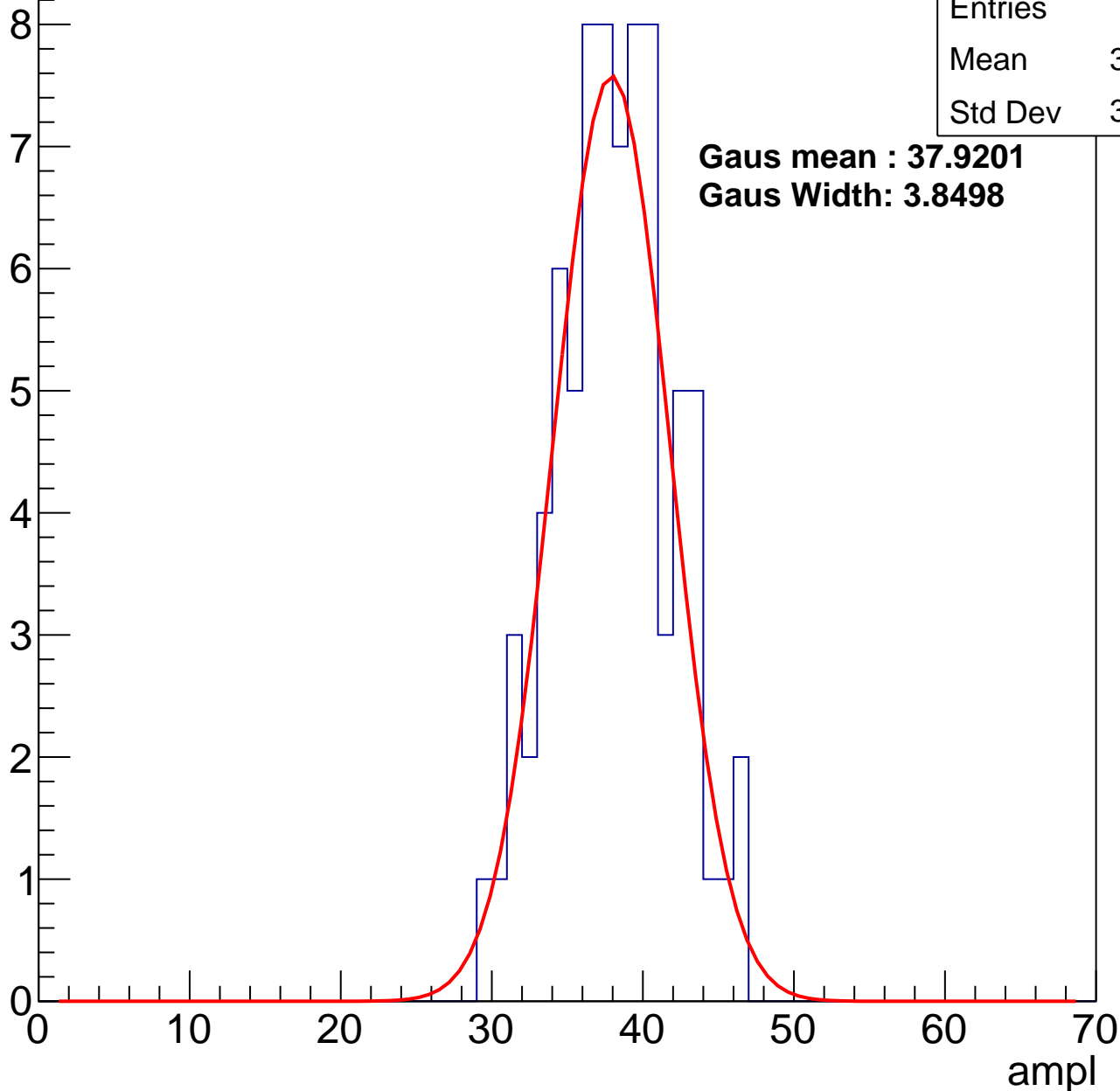
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	37.67
Std Dev	3.792

Gaus mean : 37.9201

Gaus Width: 3.8498



B0L002S, U12-ch78, adc2

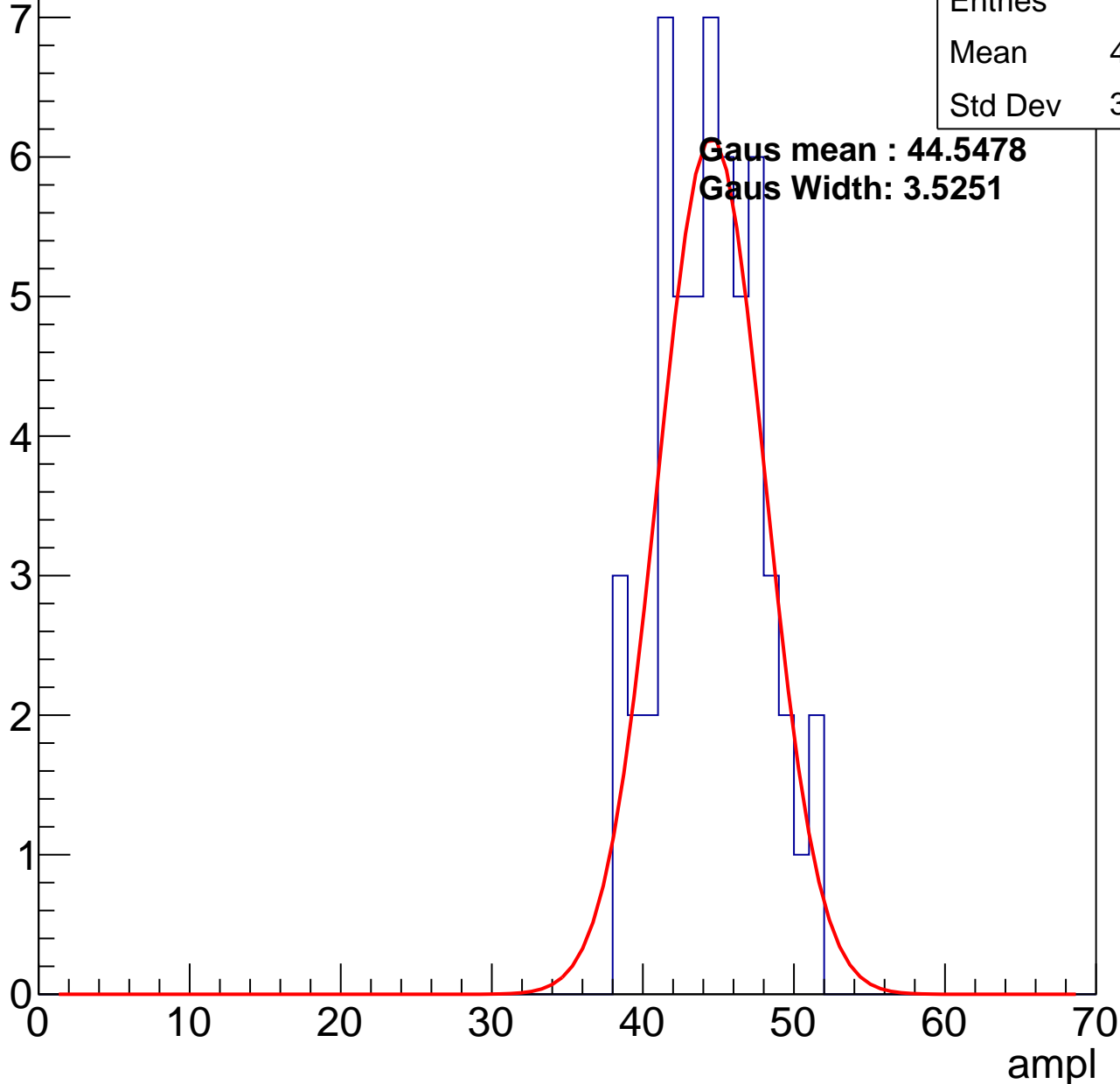
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	44.07
Std Dev	3.245

Gaus mean : 44.5478

Gaus Width: 3.5251

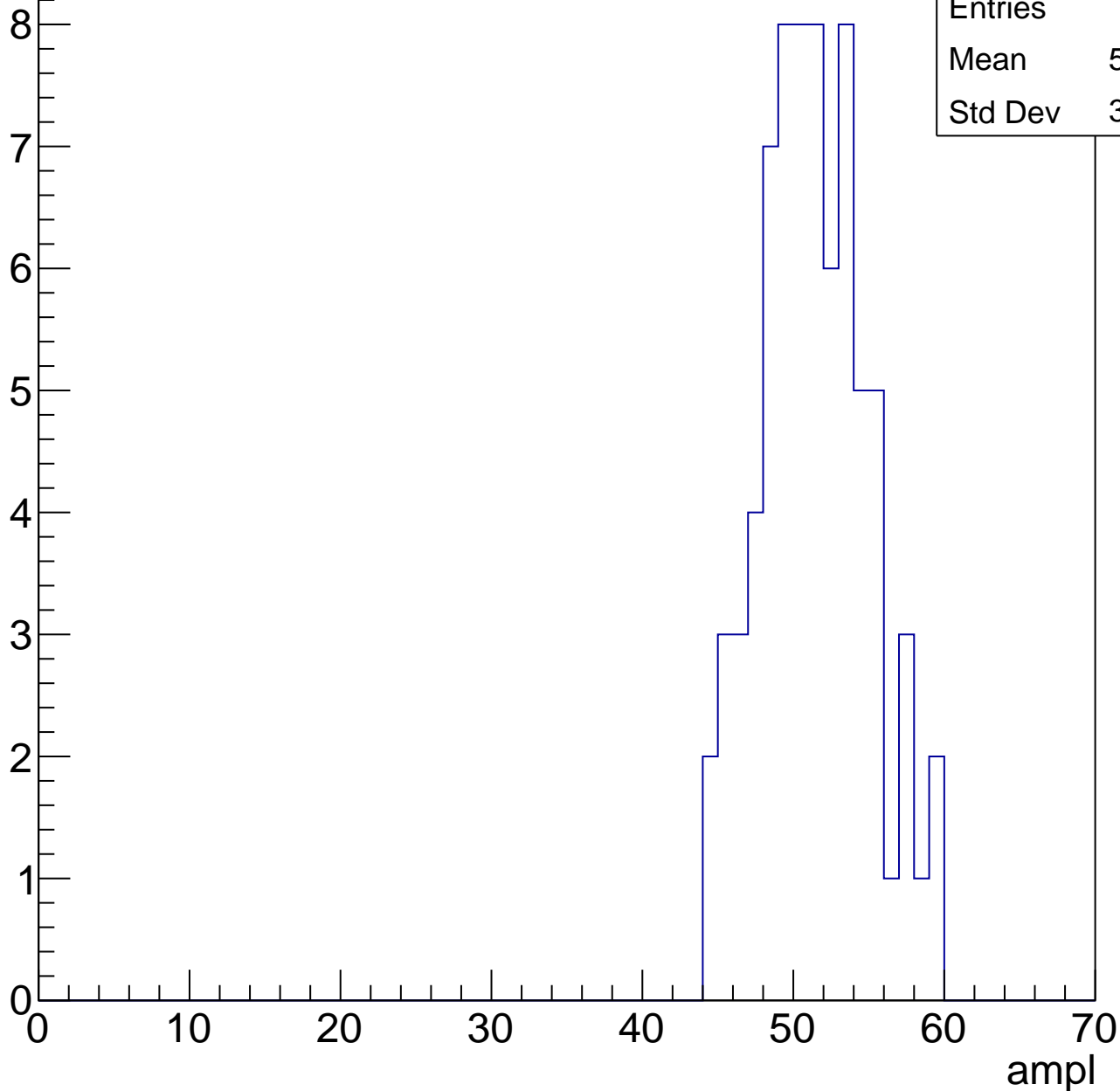


B0L002S, U12-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	50.93
Std Dev	3.527

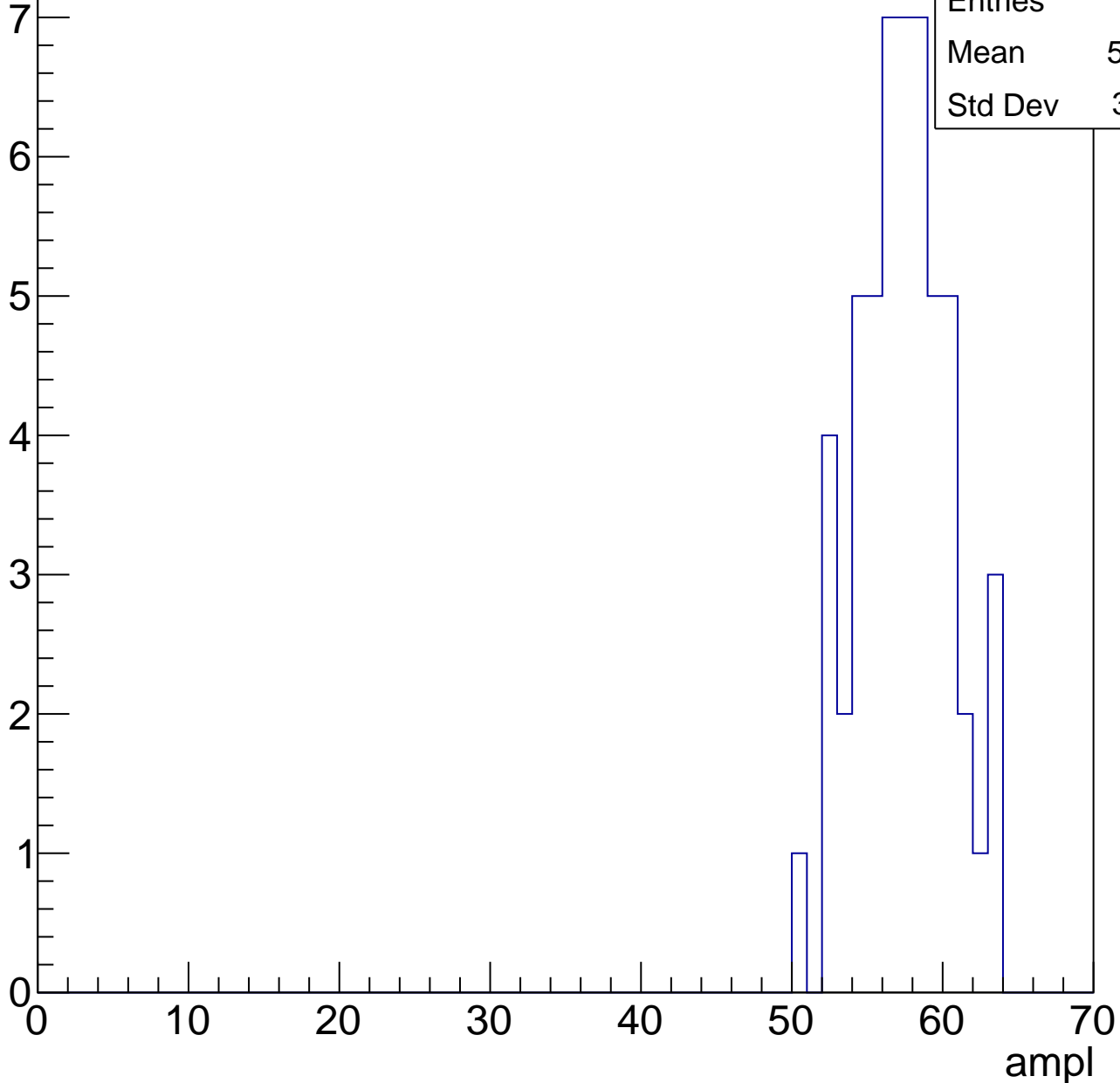


B0L002S, U12-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	56.93
Std Dev	3.011

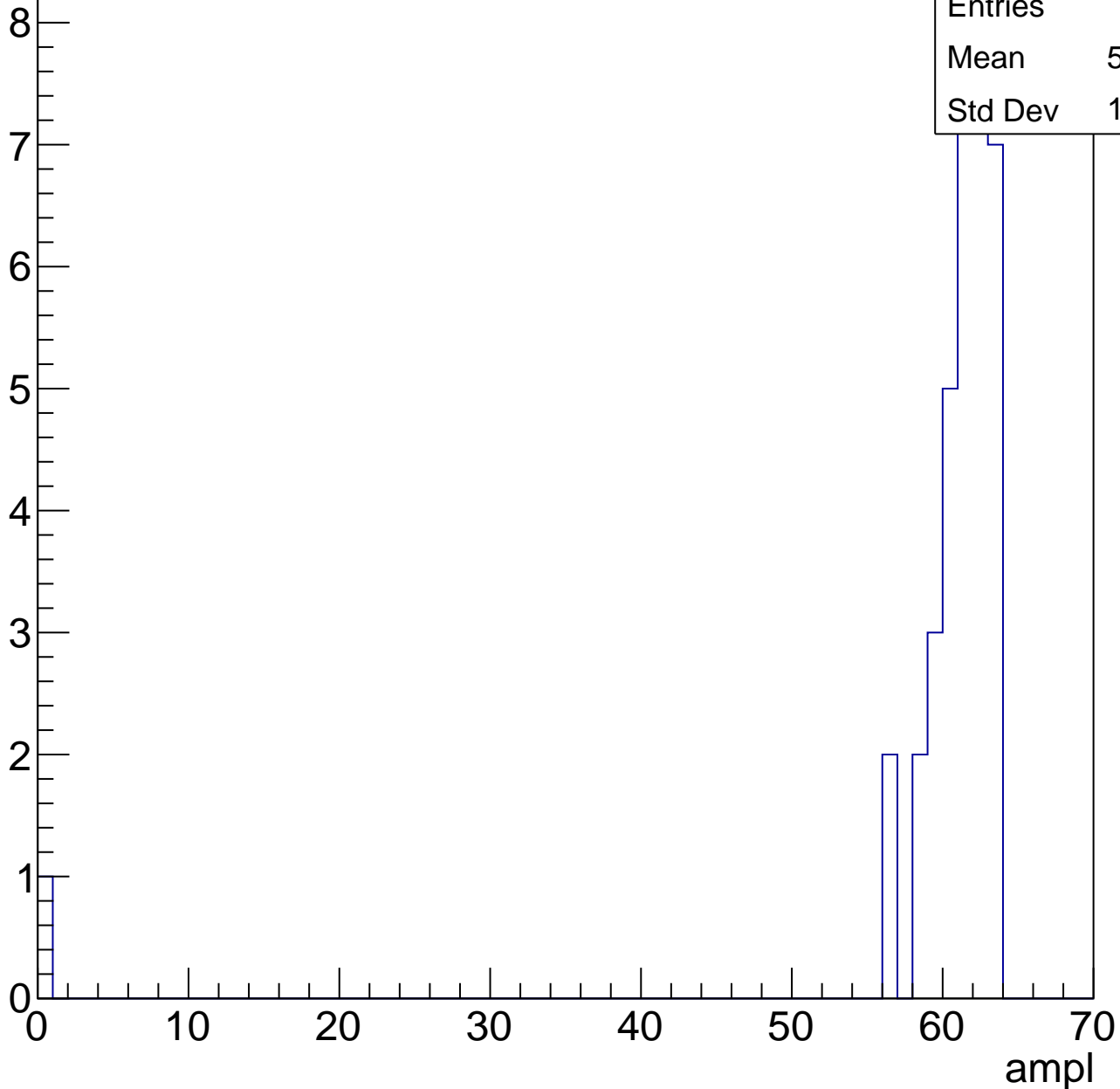


B0L002S, U12-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.17
Std Dev	10.17



B0L002S, U12-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	62
Std Dev	0

B0L002S, U12-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U12-ch79, adc0

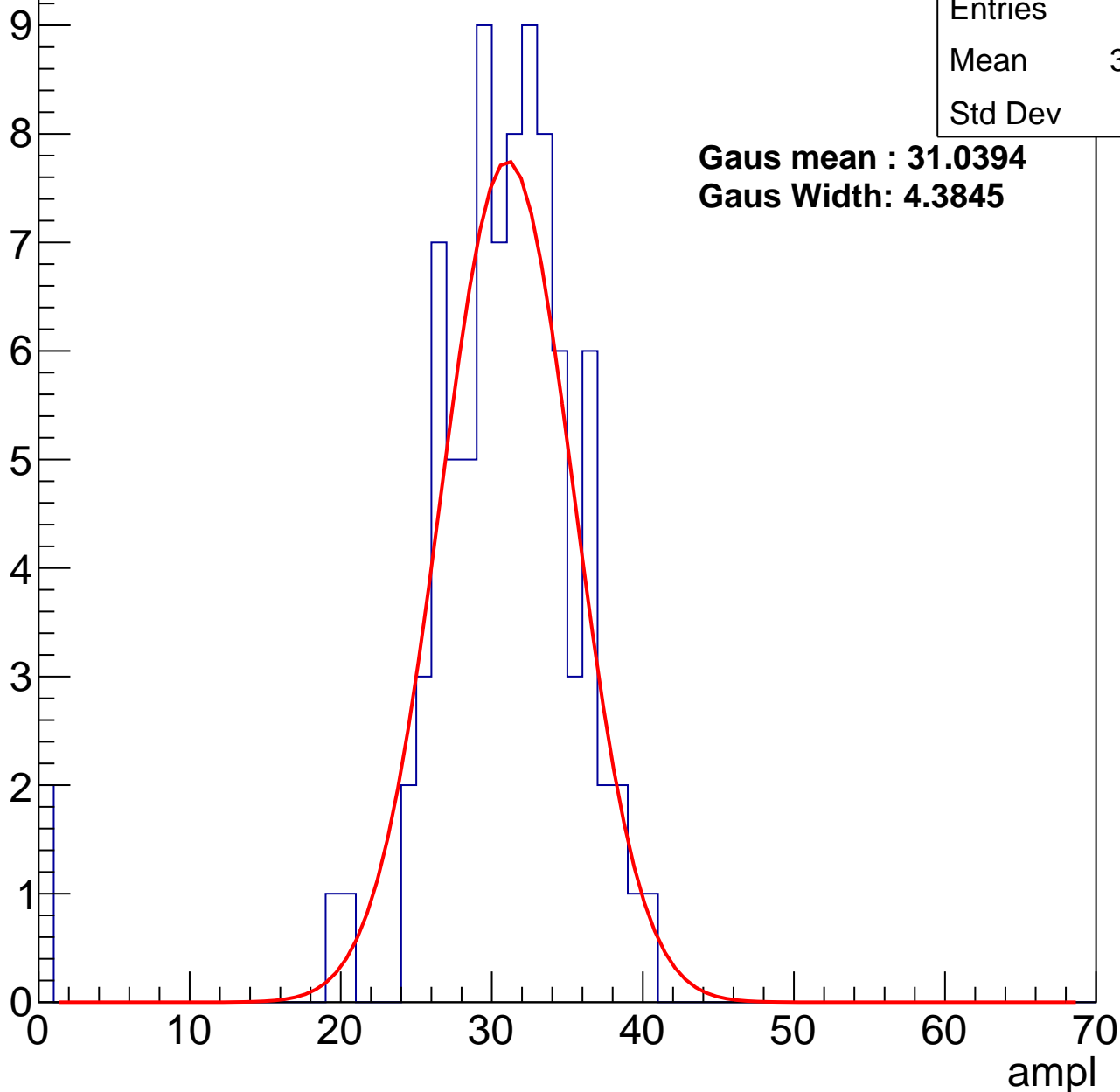
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	88
Mean	30.05
Std Dev	6.09

Gaus mean : 31.0394

Gaus Width: 4.3845



B0L002S, U12-ch79, adc1

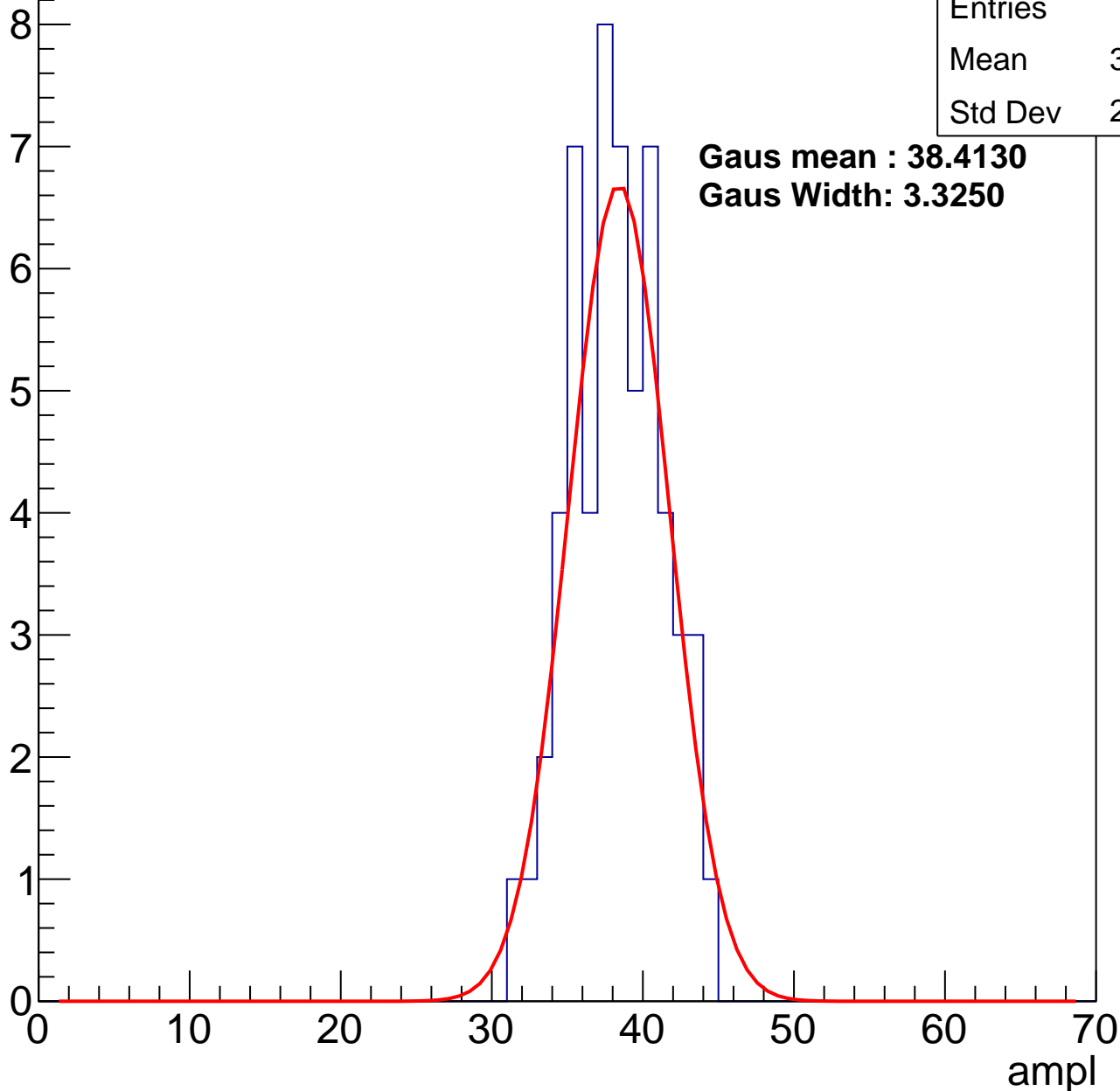
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	37.79
Std Dev	2.996

Gaus mean : 38.4130

Gaus Width: 3.3250



B0L002S, U12-ch79, adc2

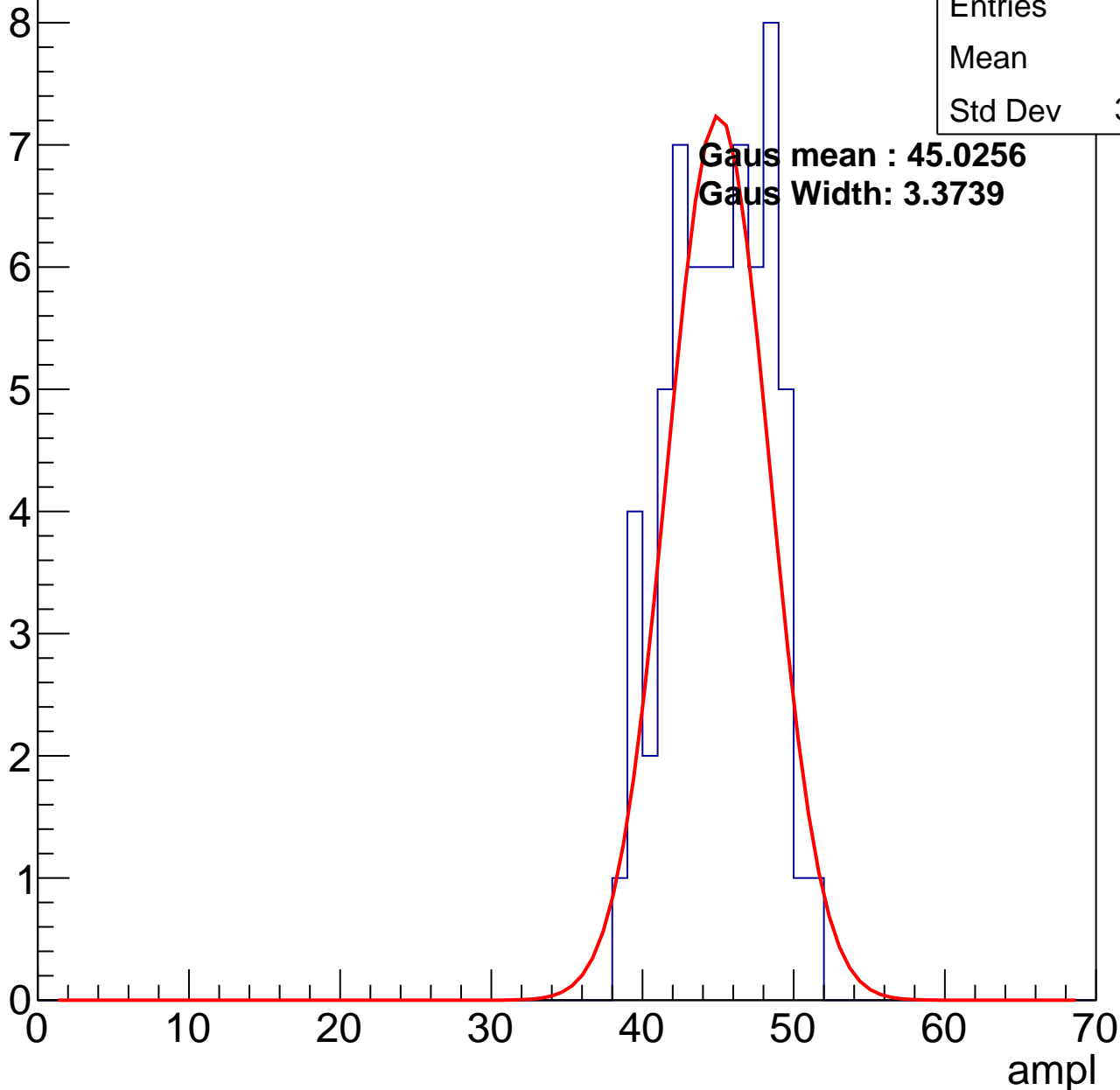
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.6
Std Dev	3.161

Gaus mean : 45.0256

Gaus Width: 3.3739

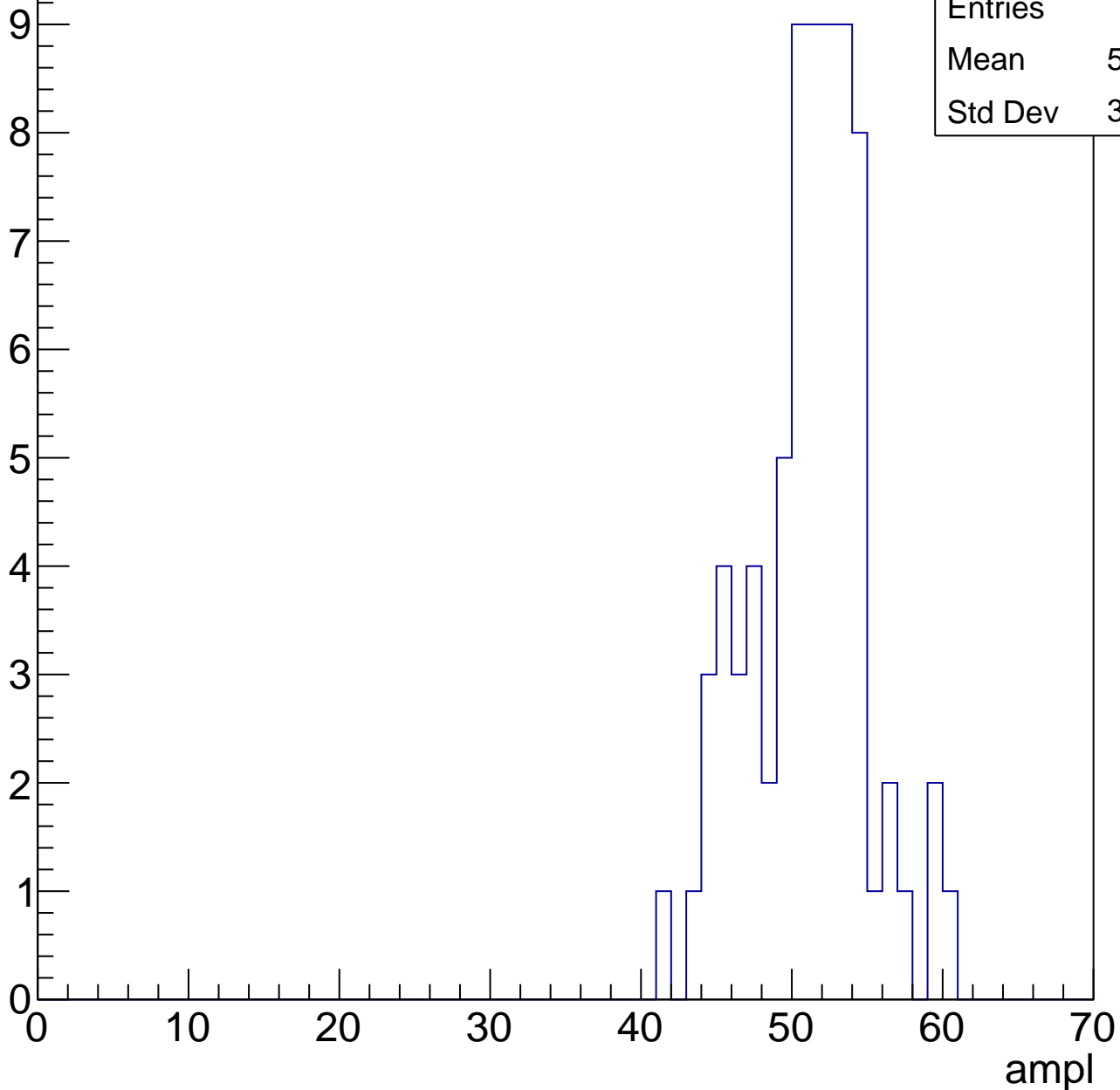


B0L002S, U12-ch79, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	50.69
Std Dev	3.788

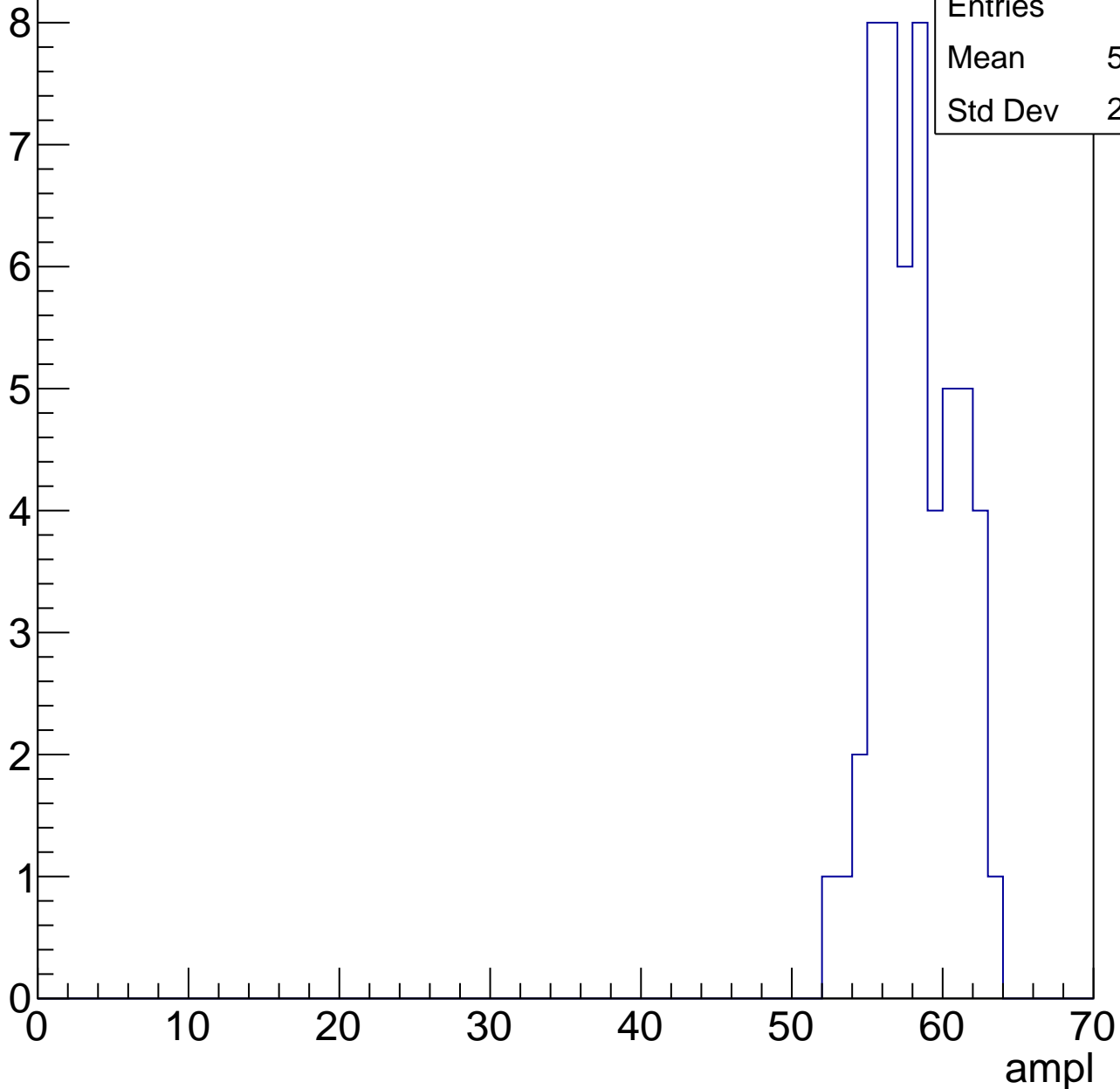


B0L002S, U12-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	57.72
Std Dev	2.594

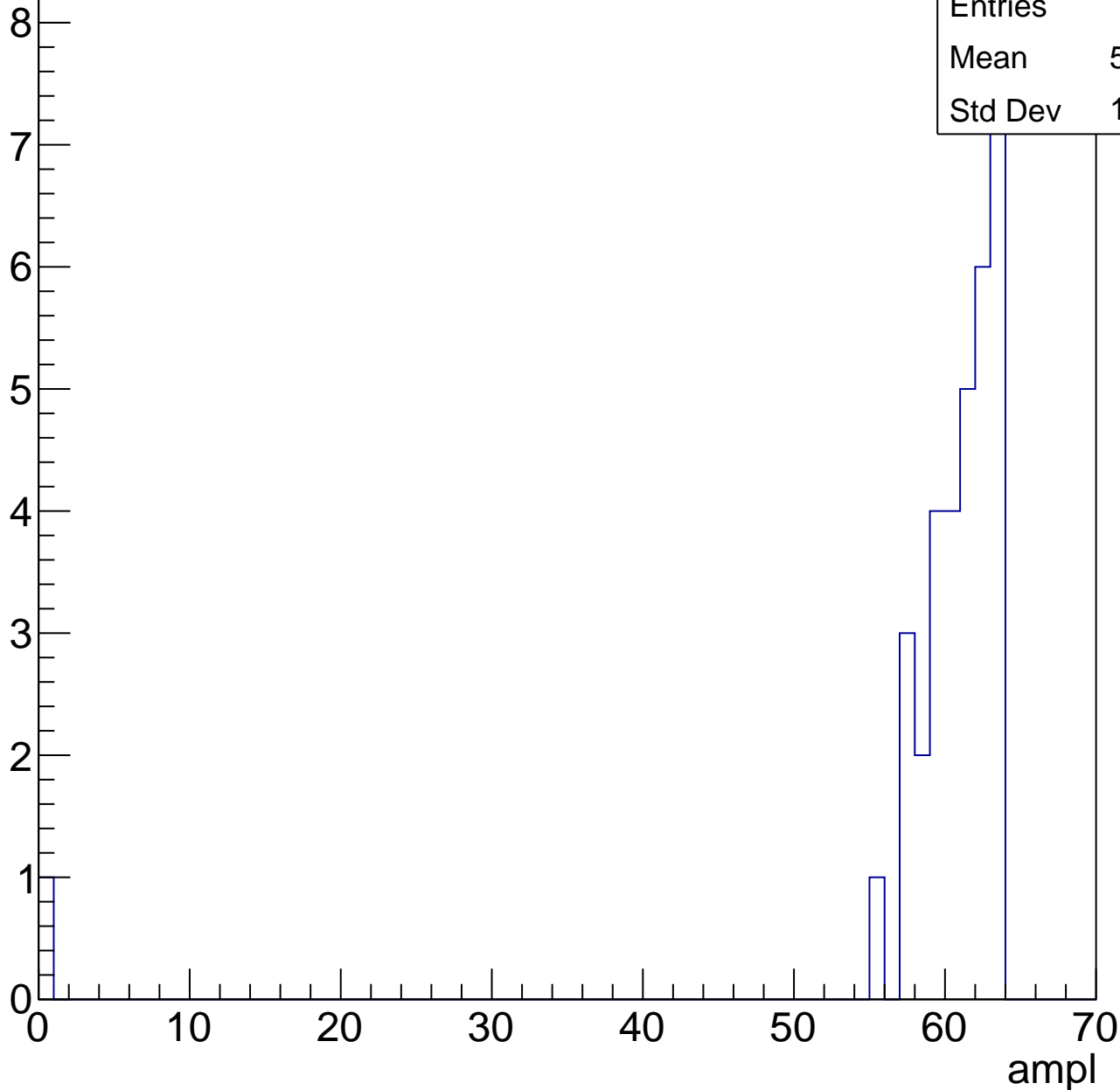


B0L002S, U12-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.79
Std Dev	10.45



B0L002S, U12-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch80, adc0

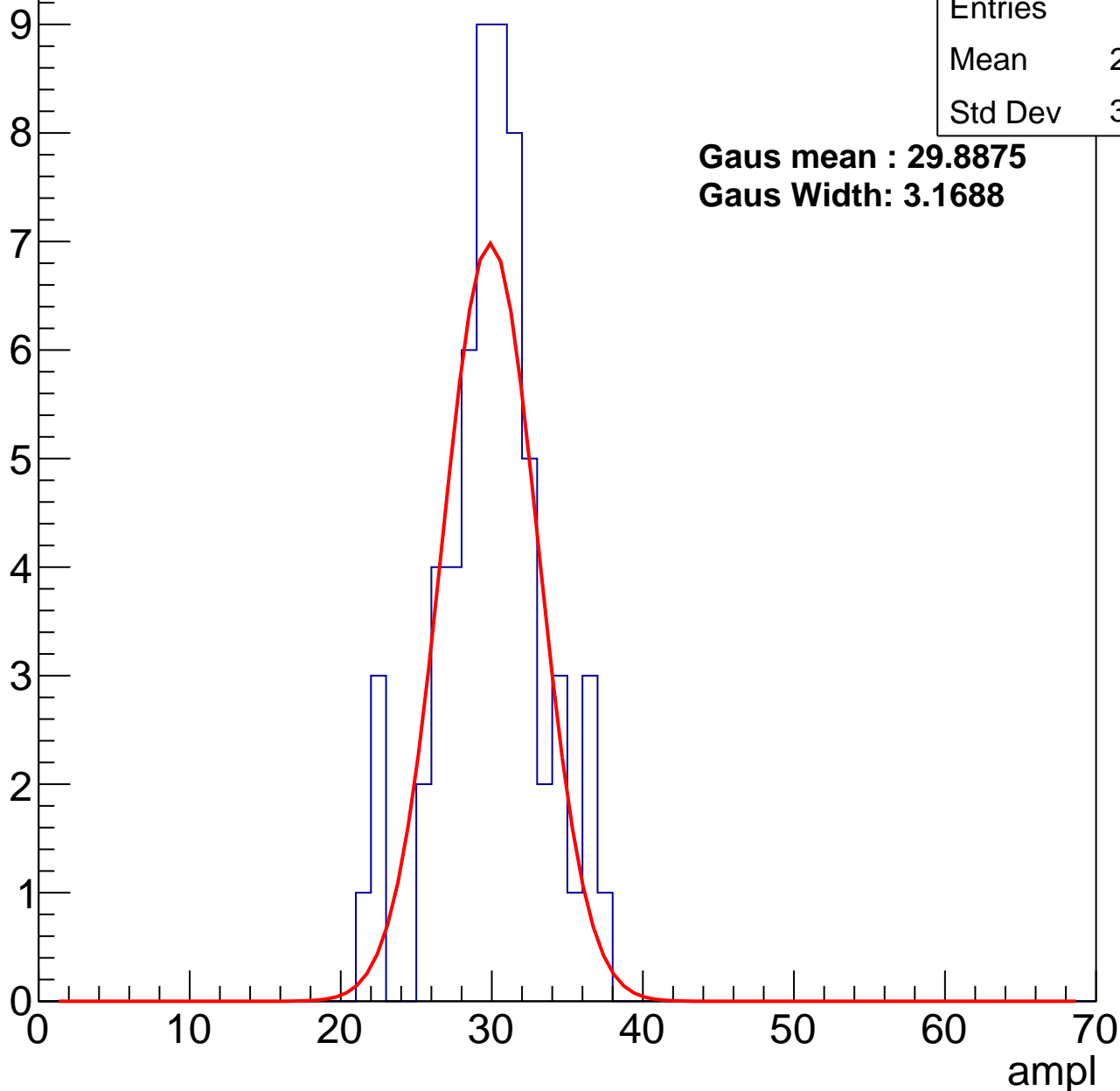
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.57
Std Dev	3.438

Gaus mean : 29.8875

Gaus Width: 3.1688



B0L002S, U12-ch80, adc1

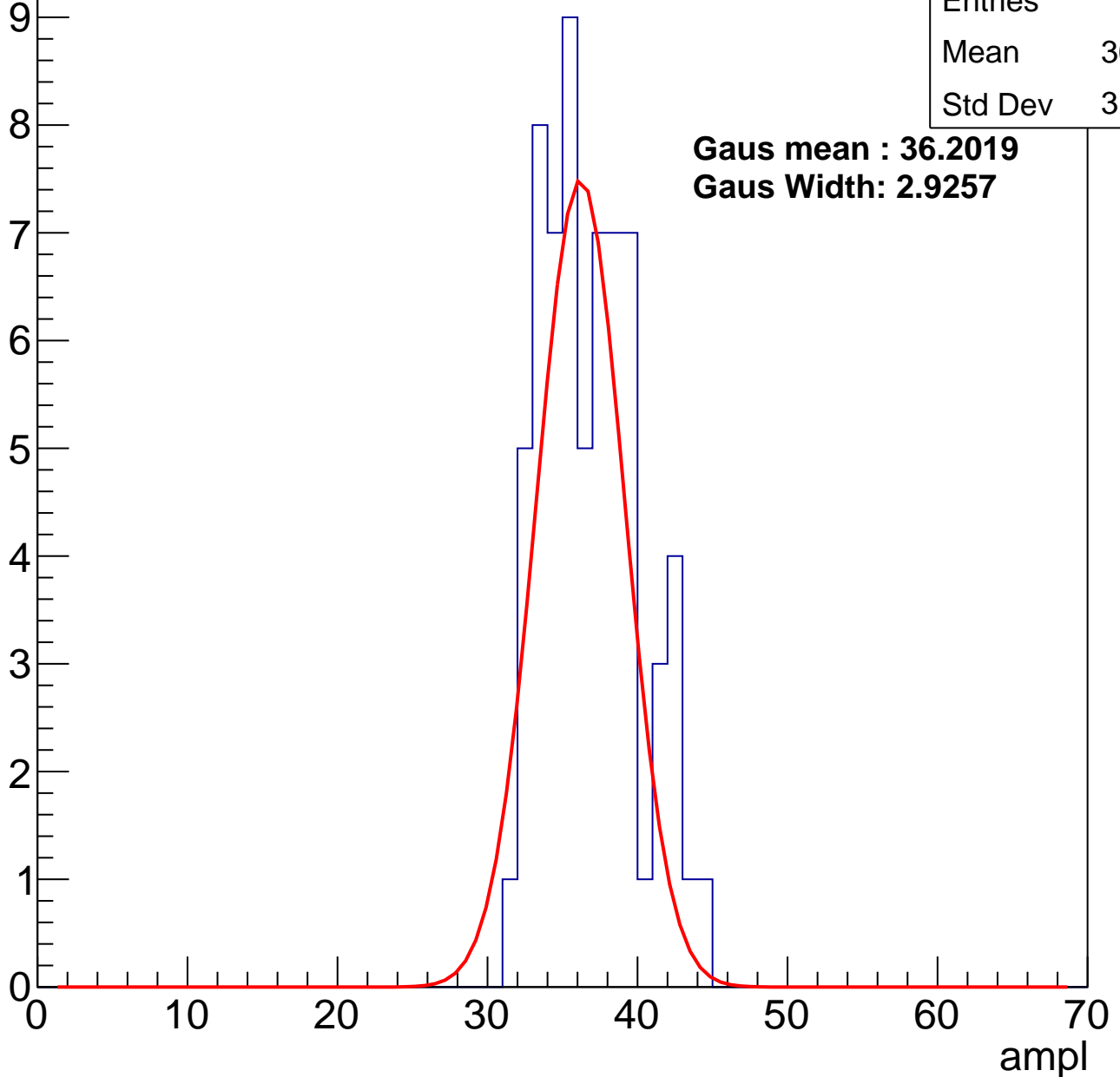
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.42
Std Dev	3.134

Gaus mean : 36.2019

Gaus Width: 2.9257



B0L002S, U12-ch80, adc2

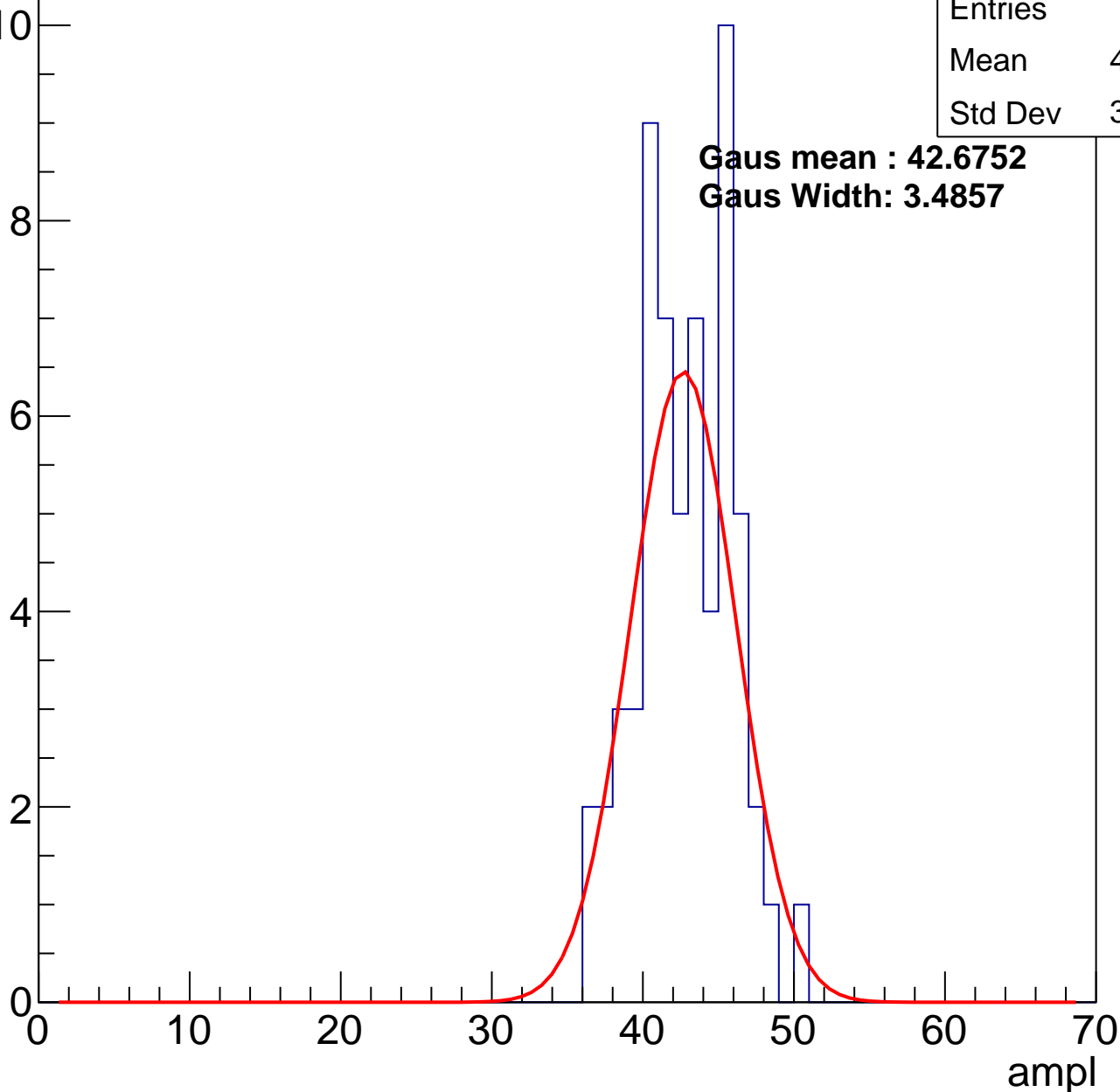
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.34
Std Dev	3.072

Gaus mean : 42.6752

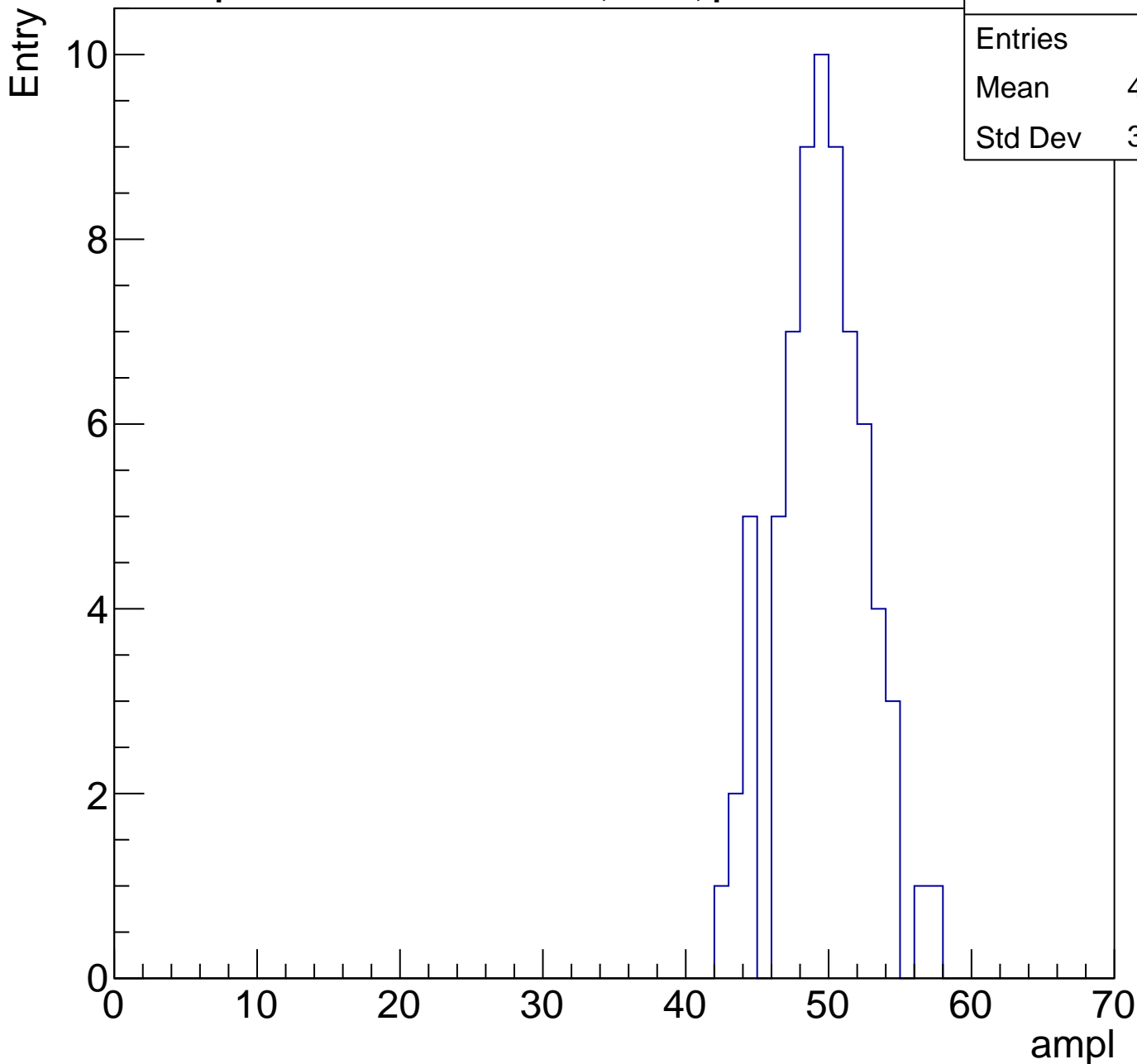
Gaus Width: 3.4857



B0L002S, U12-ch80, adc3

calib_packv5_042523_0143.root, FC#8, port C1

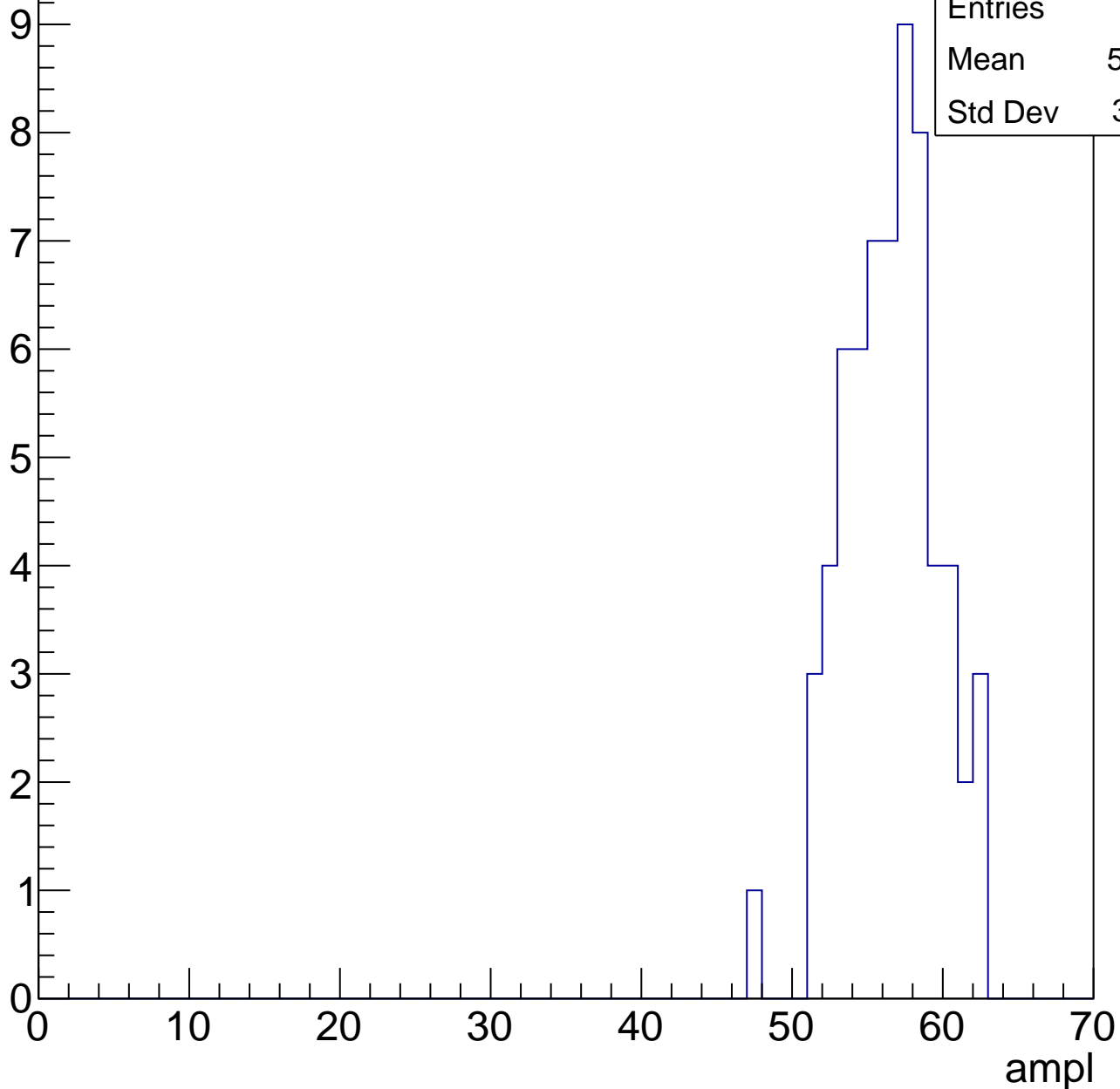
Entries	70
Mean	49.07
Std Dev	3.095



B0L002S, U12-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



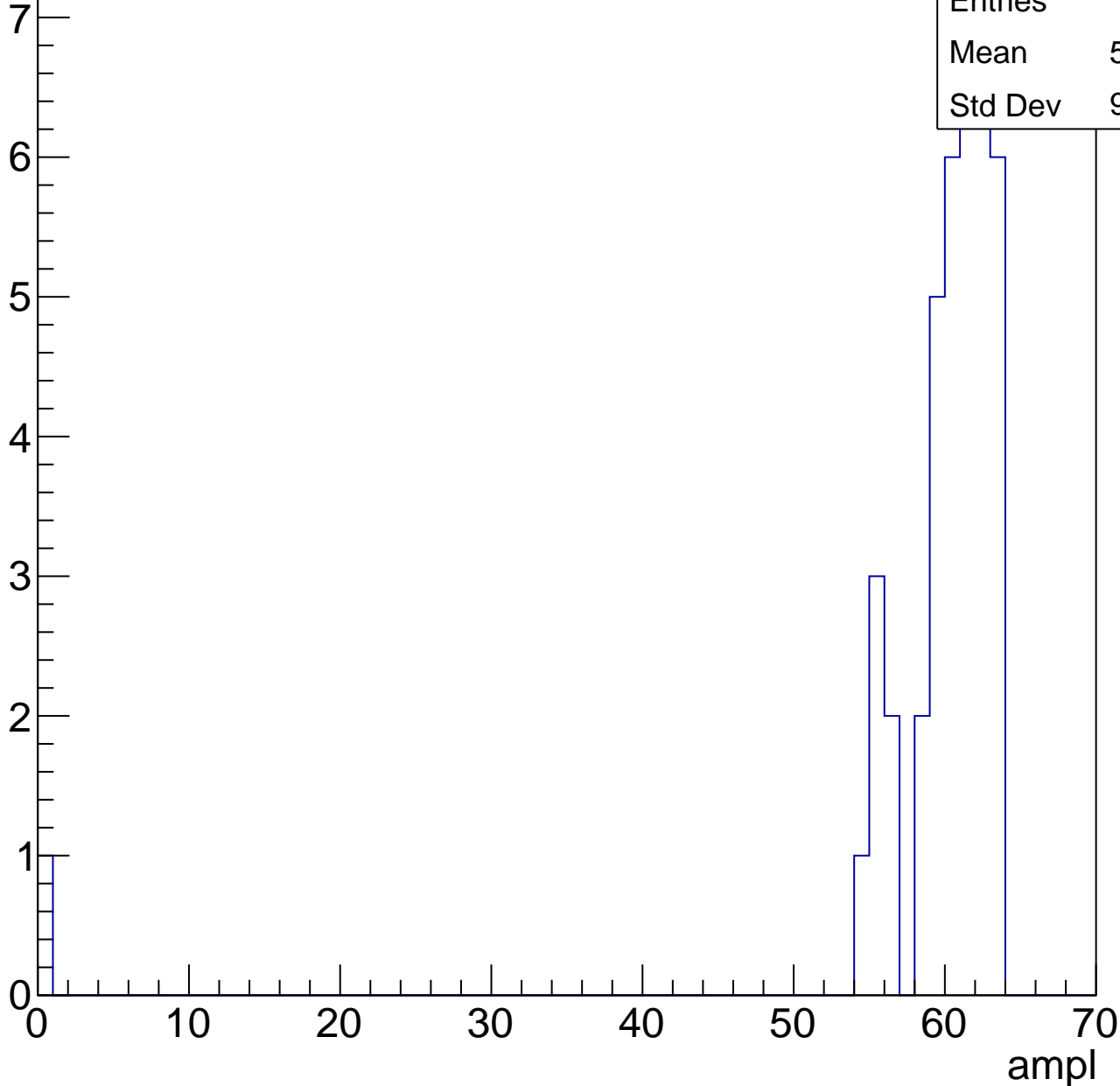
Entries	64
Mean	56.06
Std Dev	3.071

B0L002S, U12-ch80, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

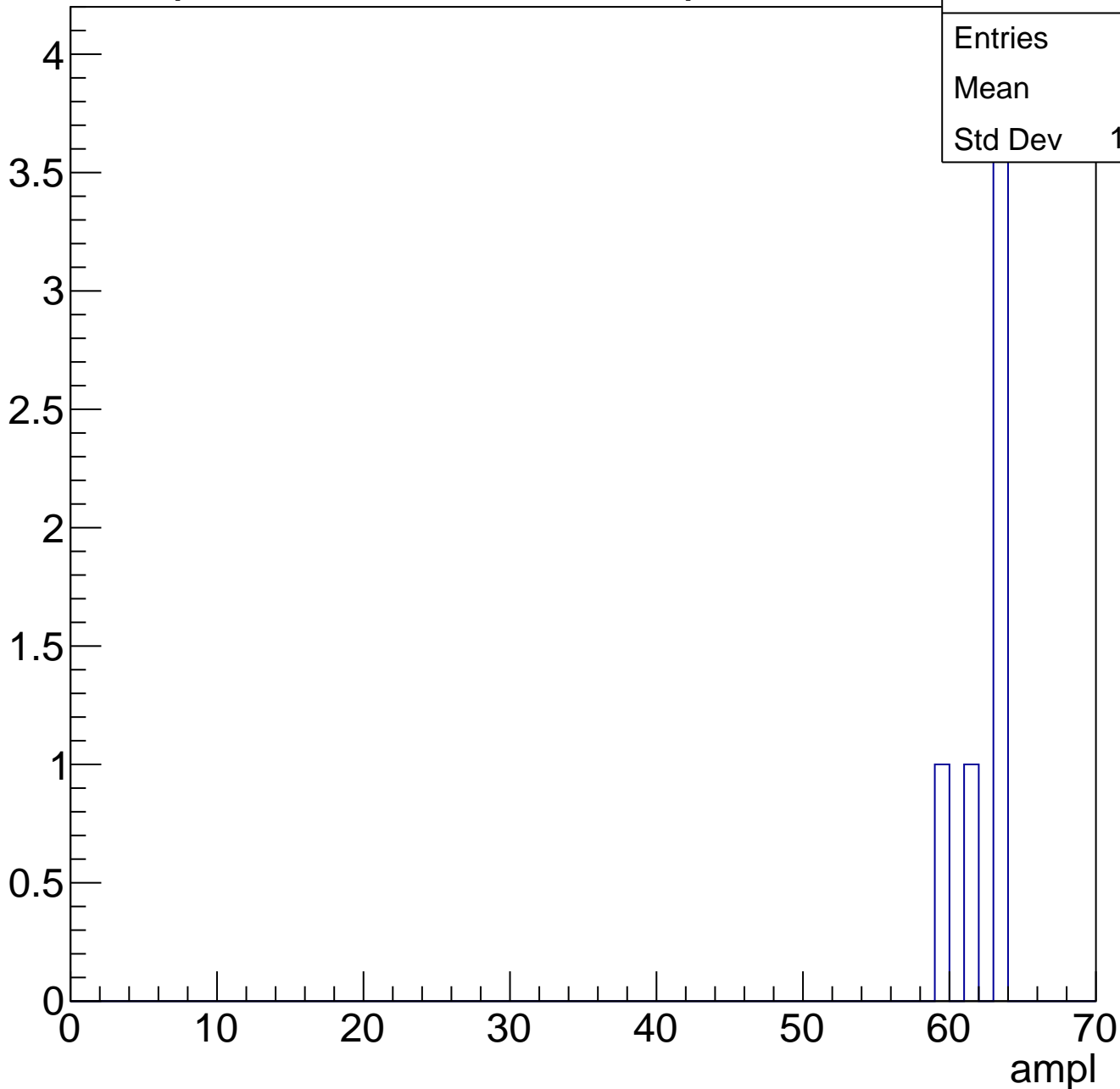
Entries	40
Mean	58.52
Std Dev	9.693



B0L002S, U12-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch81, adc0

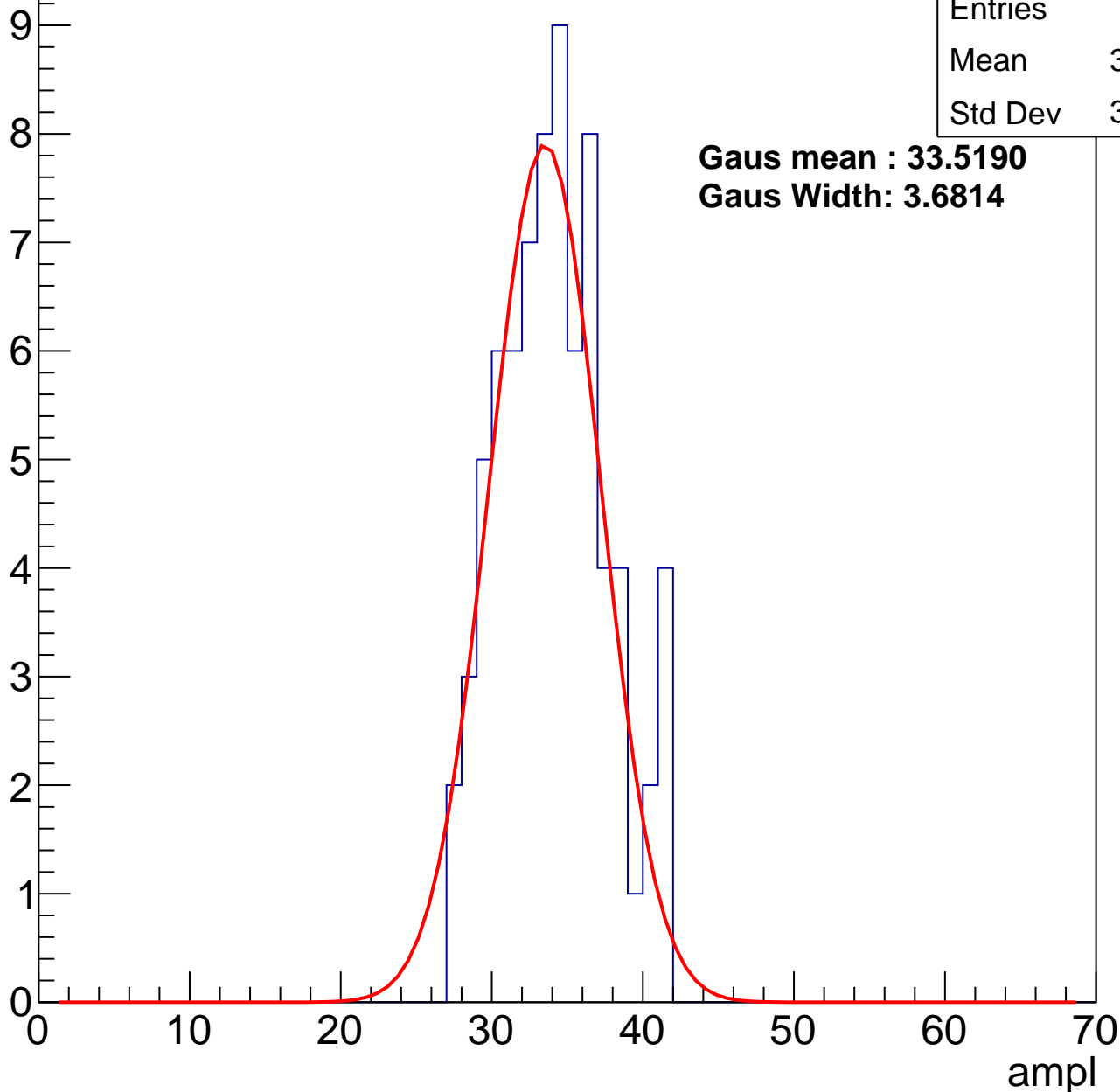
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	33.65
Std Dev	3.538

Gaus mean : 33.5190

Gaus Width: 3.6814



B0L002S, U12-ch81, adc1

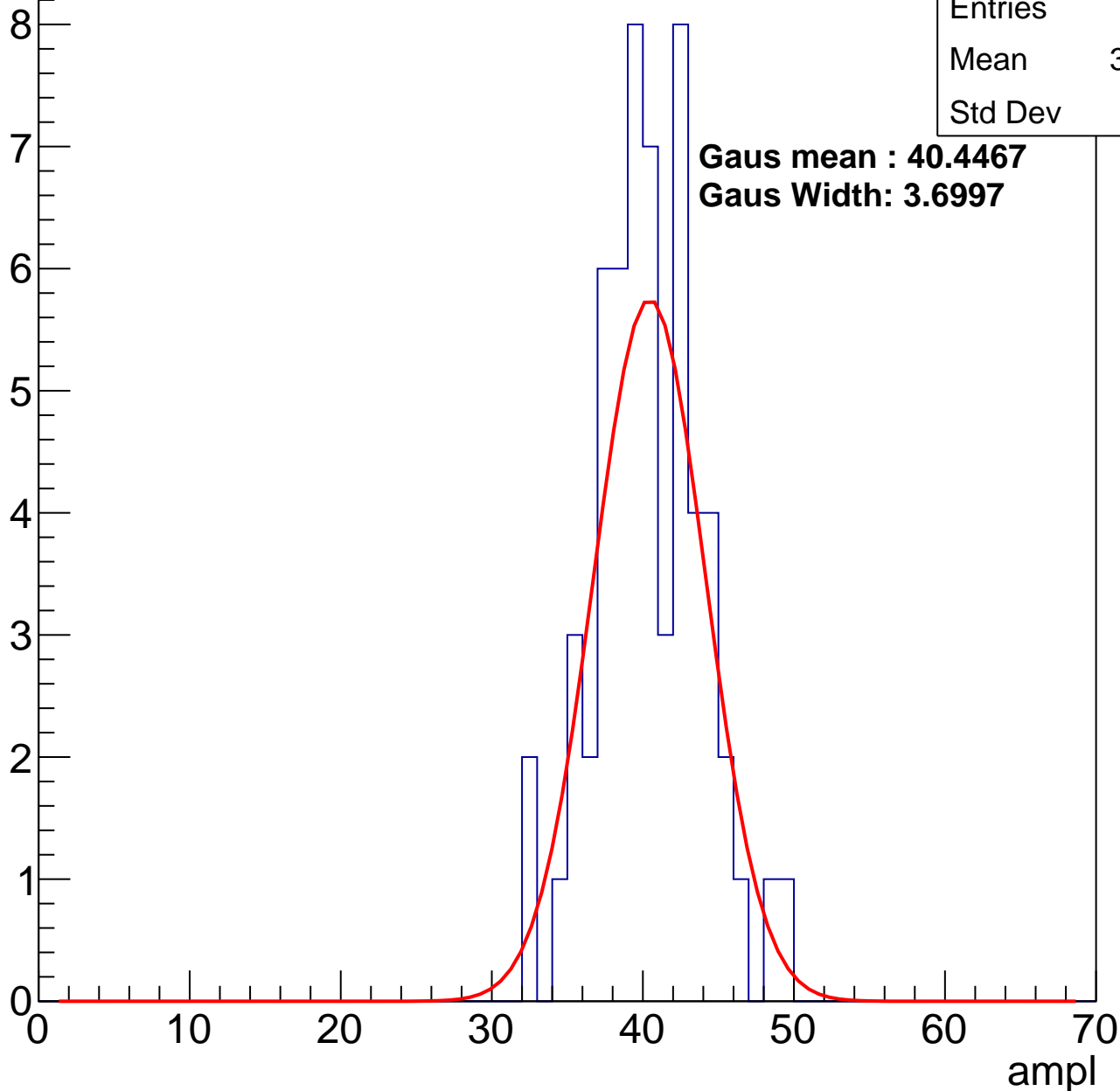
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	39.95
Std Dev	3.5

Gaus mean : 40.4467

Gaus Width: 3.6997



B0L002S, U12-ch81, adc2

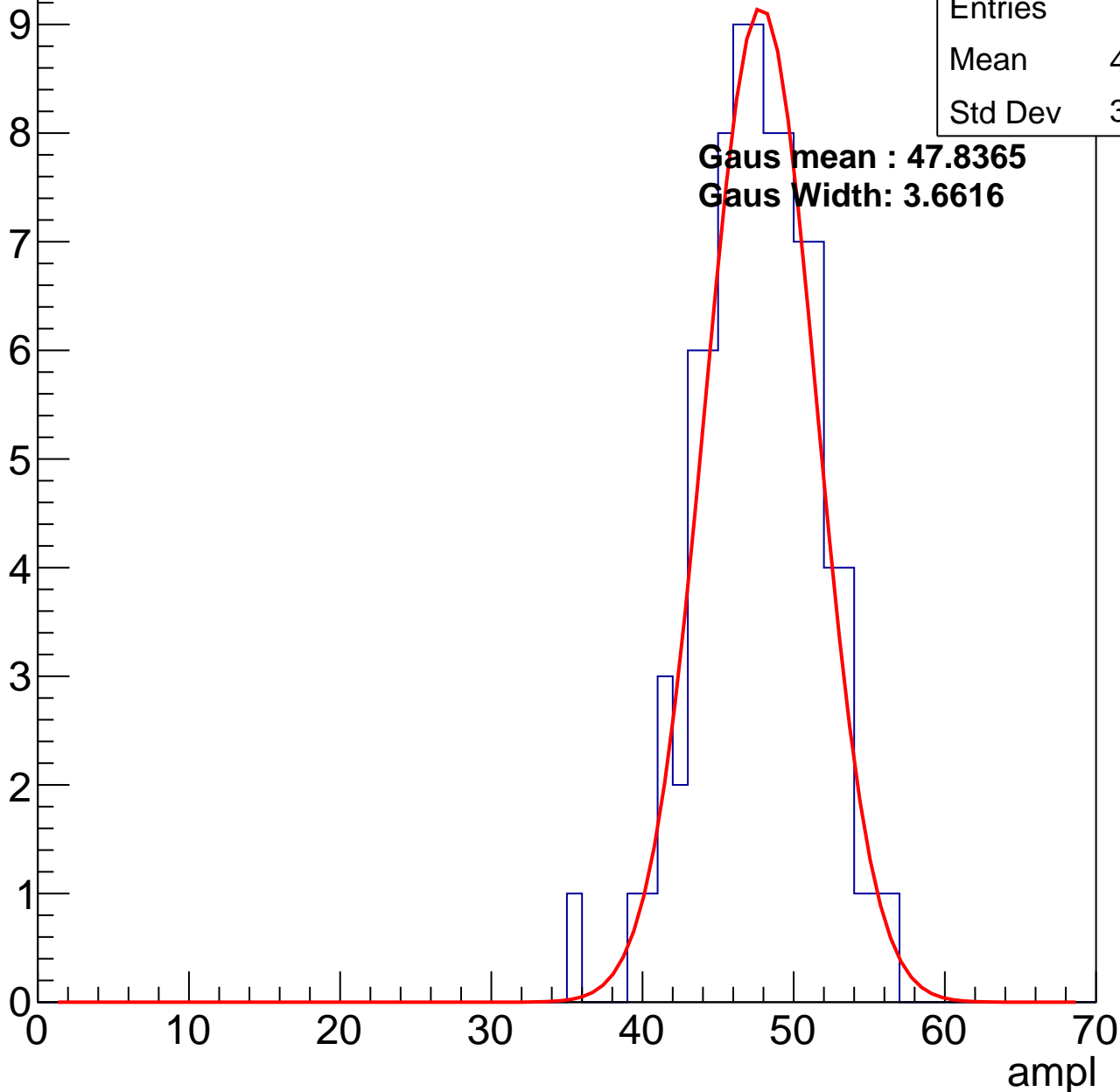
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	47.22
Std Dev	3.798

Gaus mean : 47.8365

Gaus Width: 3.6616

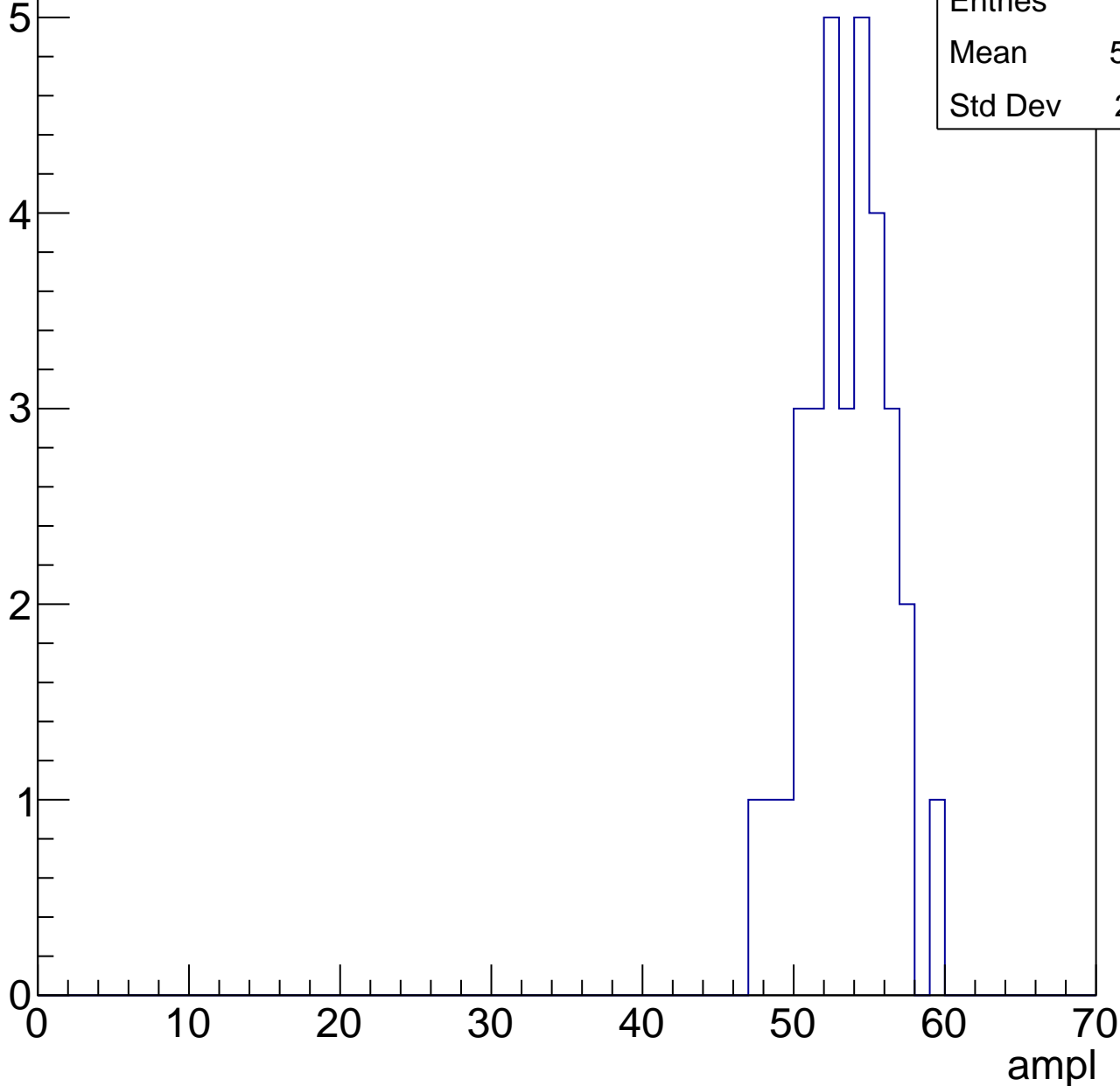


B0L002S, U12-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

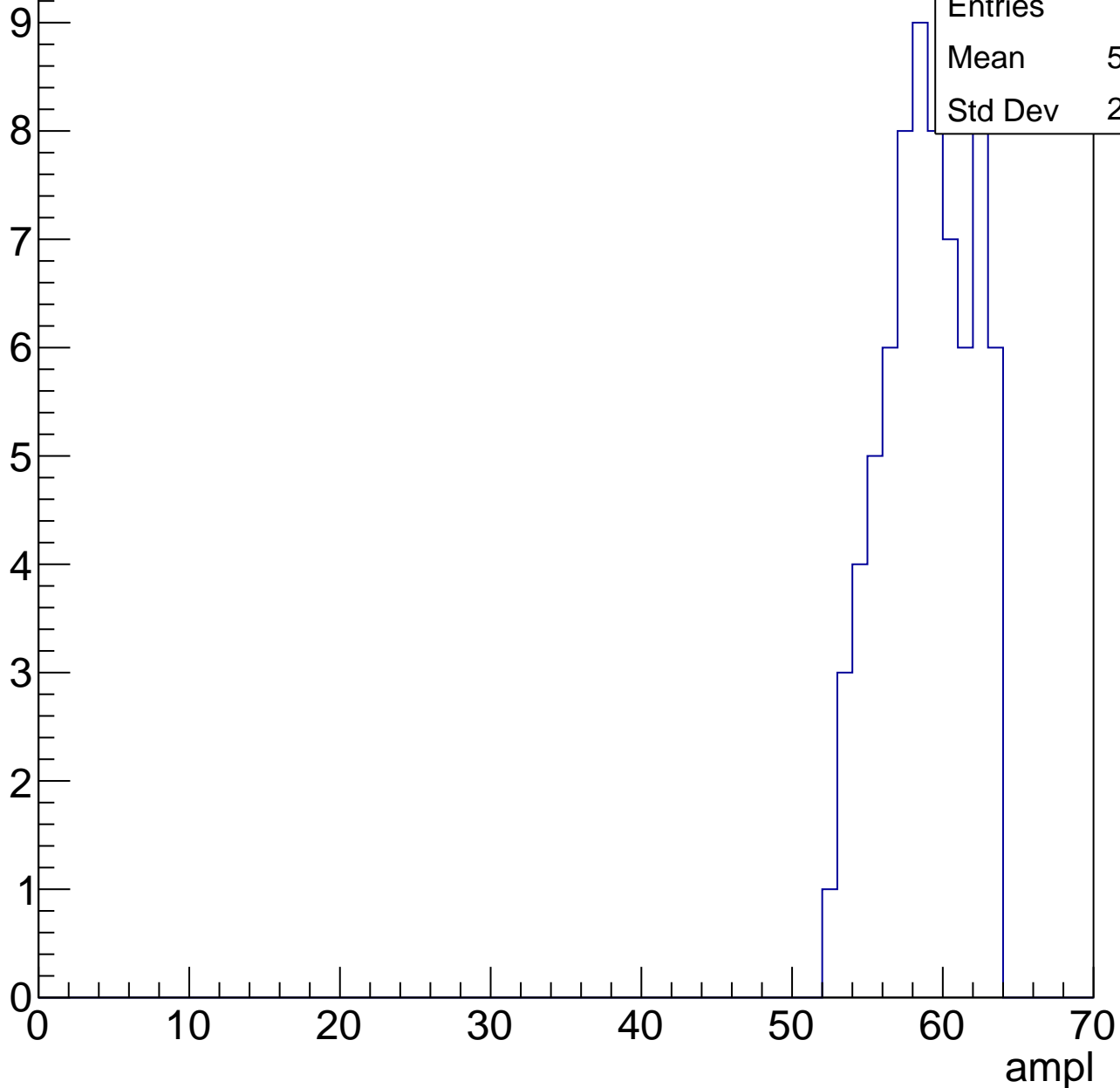
Entries	32
Mean	53.03
Std Dev	2.721



B0L002S, U12-ch81, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

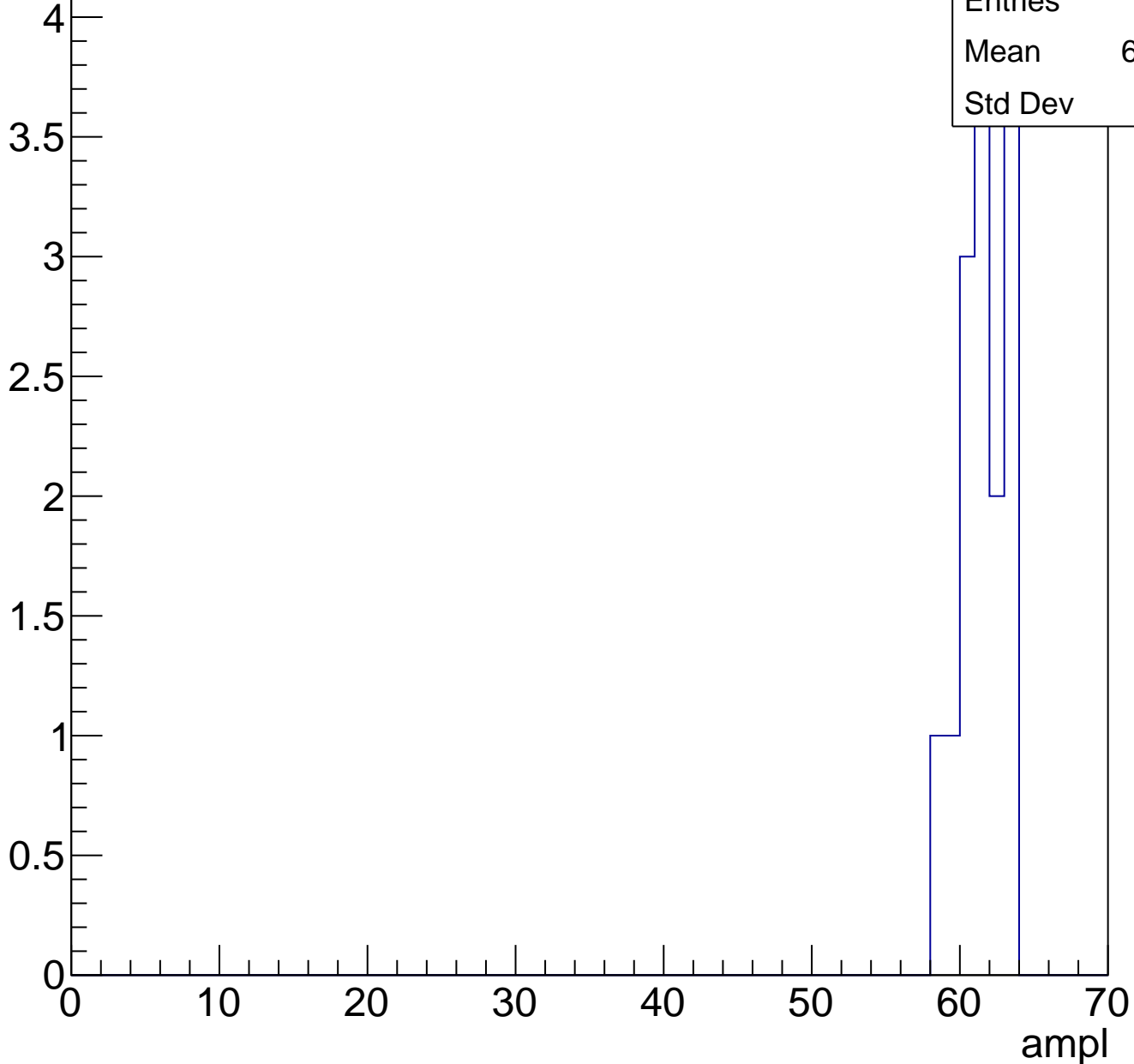


Entries	71
Mean	58.42
Std Dev	2.925

B0L002S, U12-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

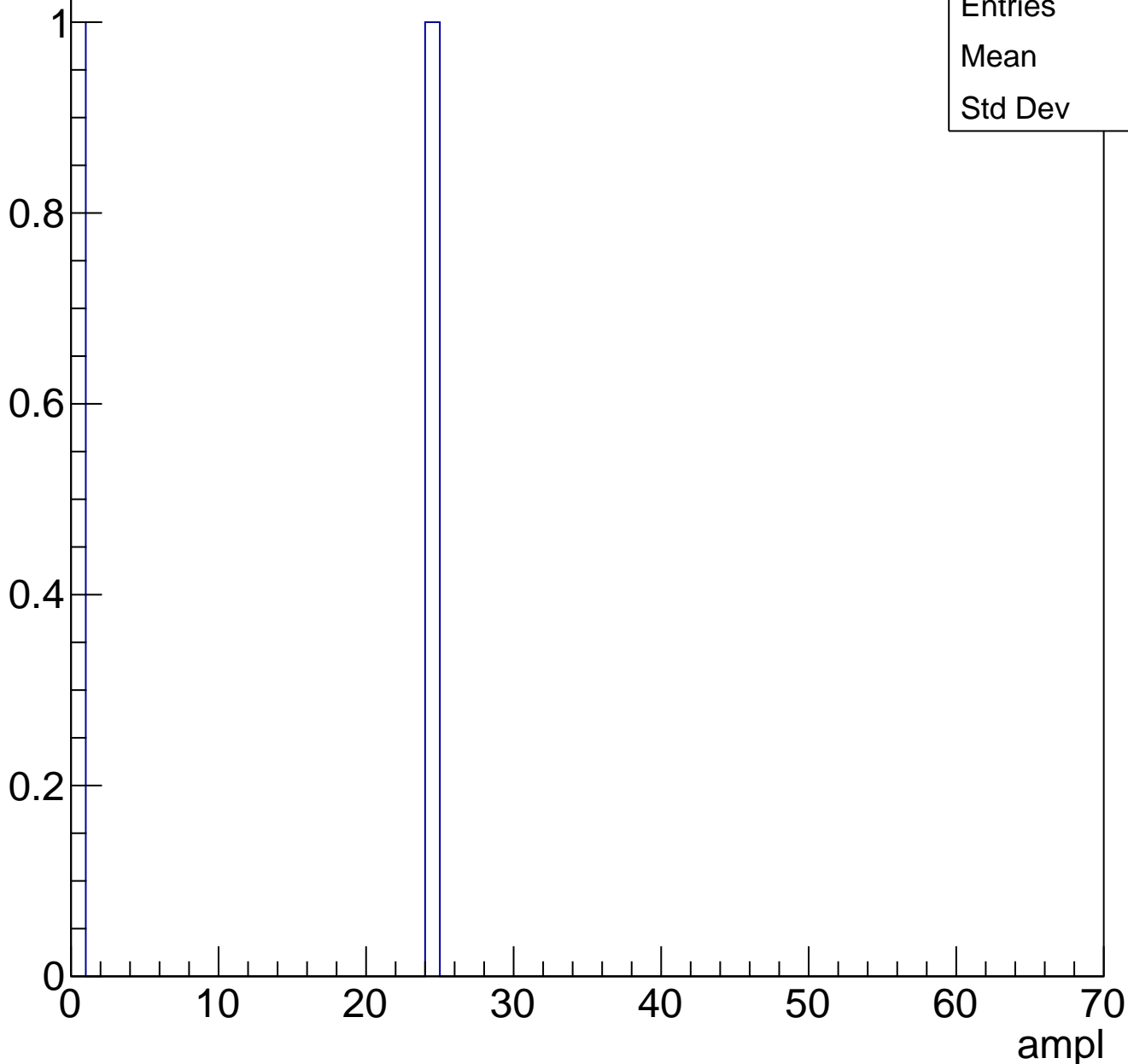
Entry



B0L002S, U12-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch82, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	29.57
Std Dev	4.764

Gaus mean : 30.4574

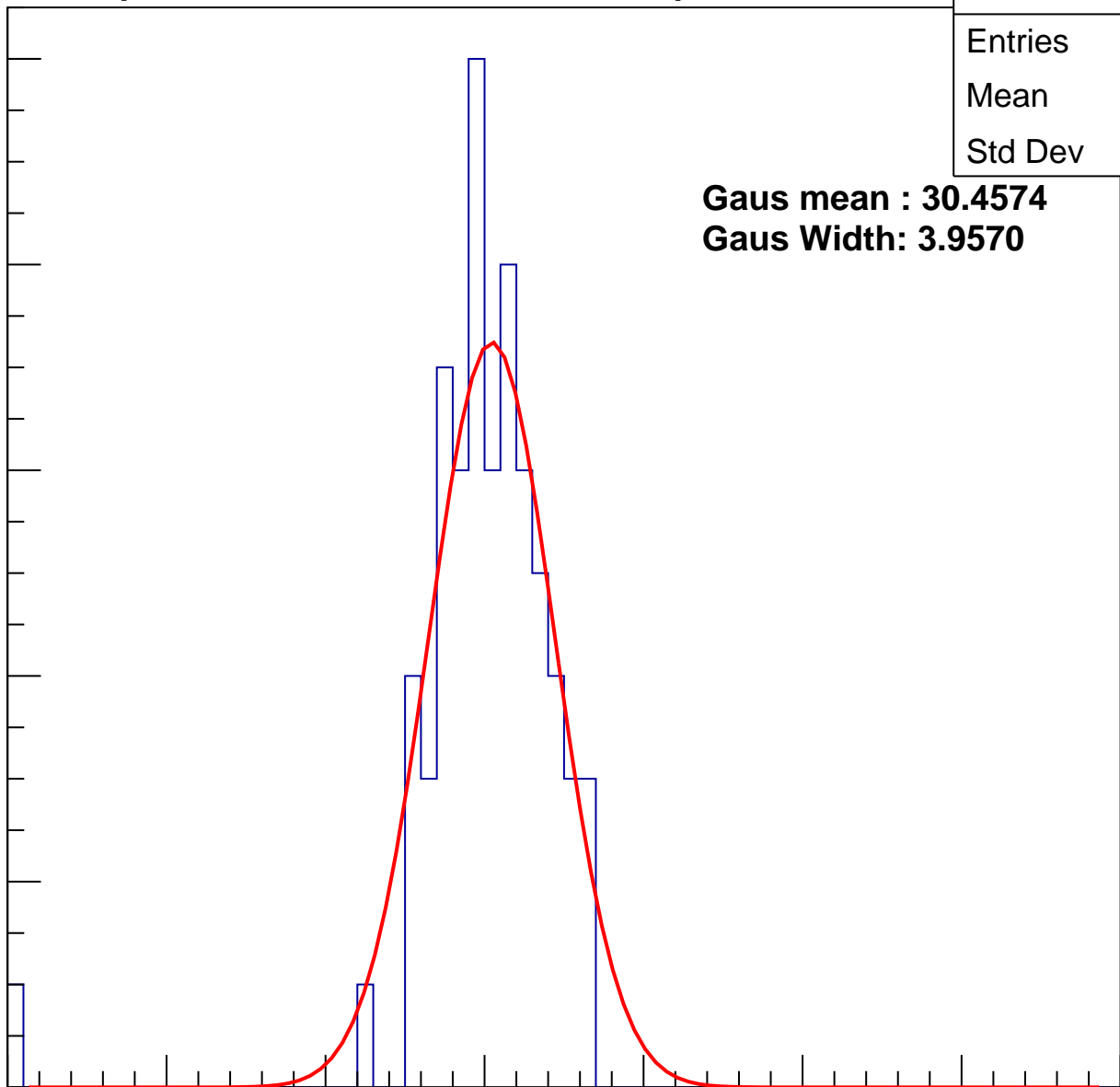
Gaus Width: 3.9570

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch82, adc1

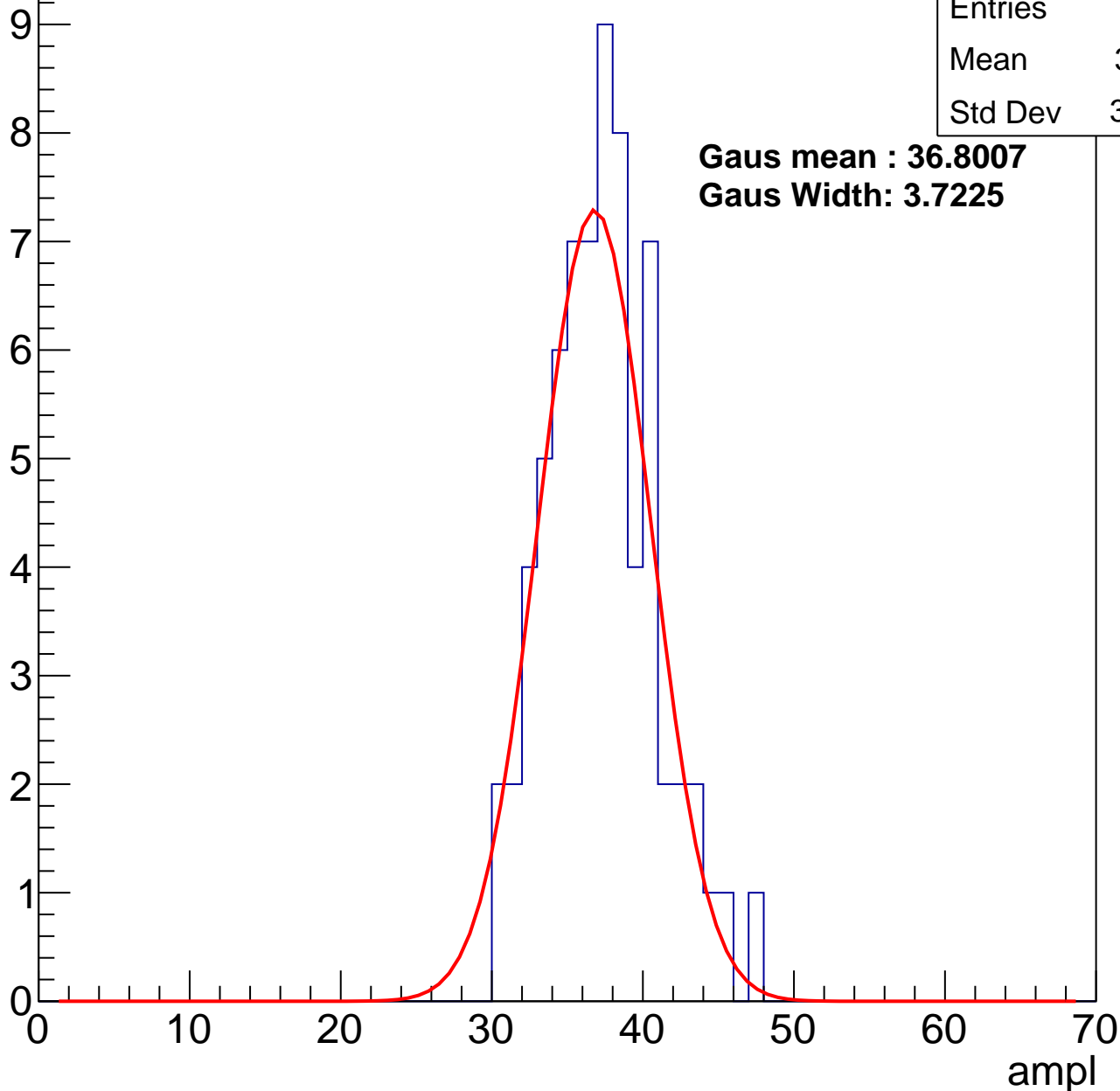
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	36.81
Std Dev	3.567

Gaus mean : 36.8007

Gaus Width: 3.7225



B0L002S, U12-ch82, adc2

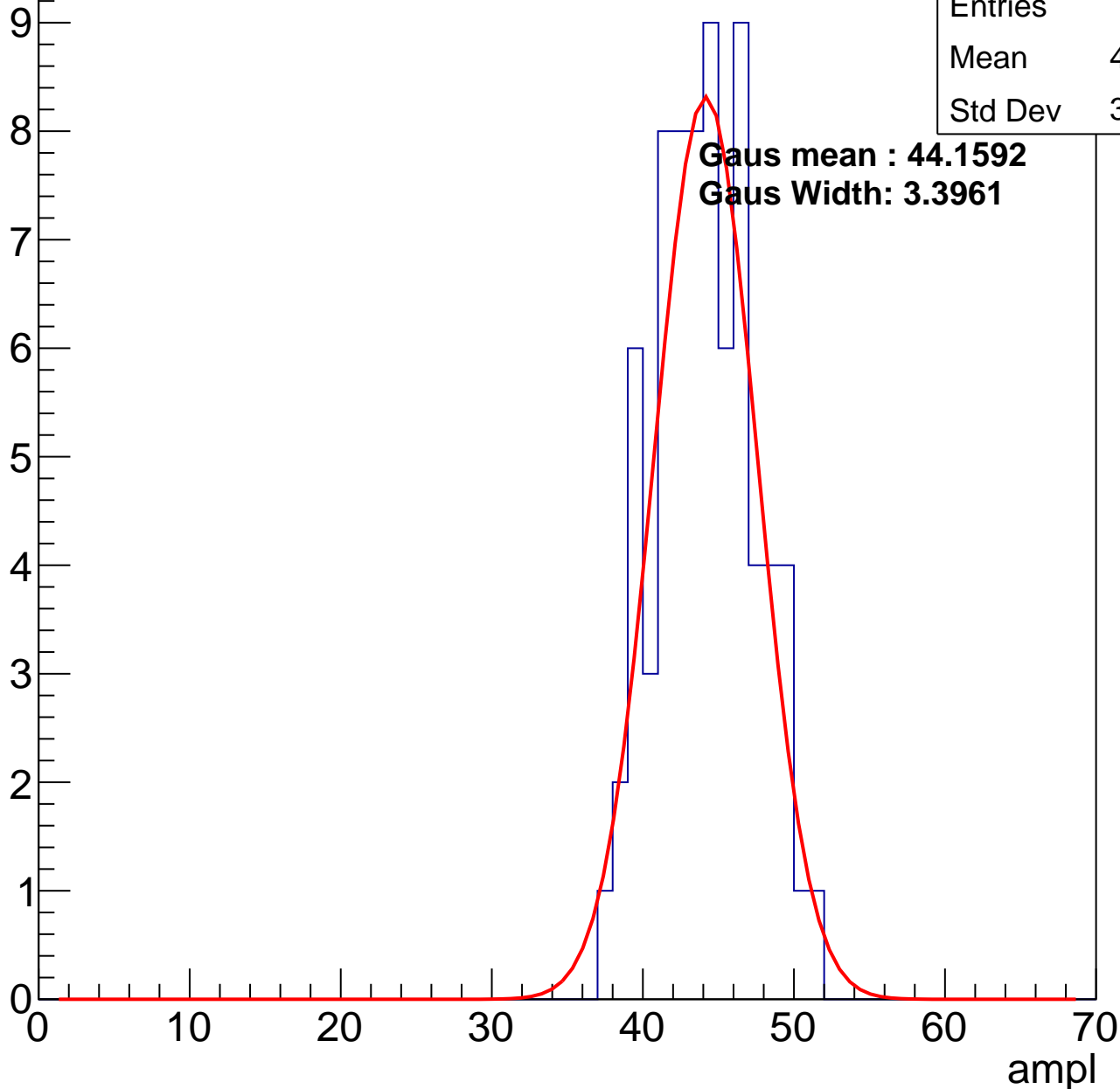
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.68
Std Dev	3.184

Gaus mean : 44.1592

Gaus Width: 3.3961

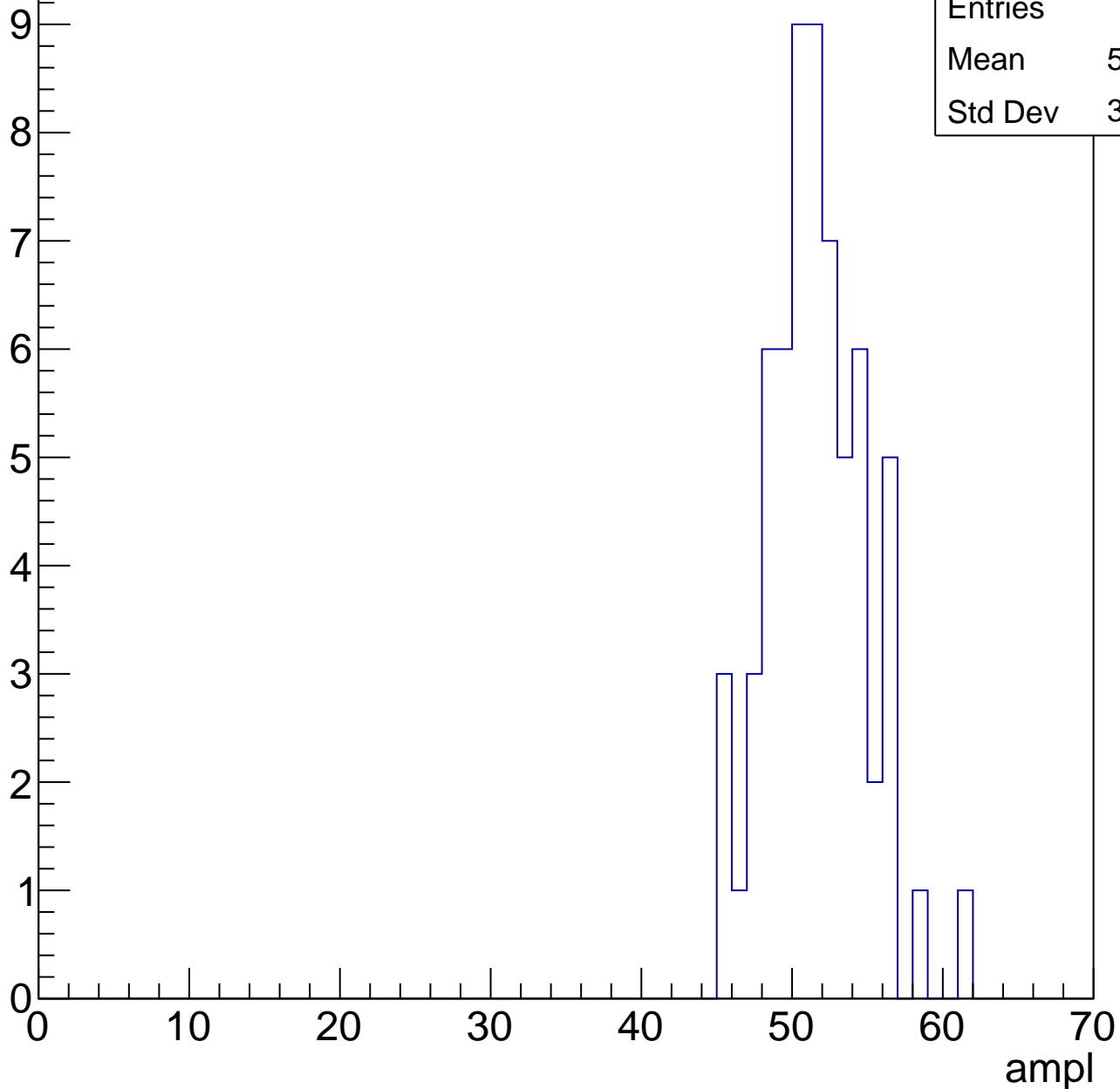


B0L002S, U12-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

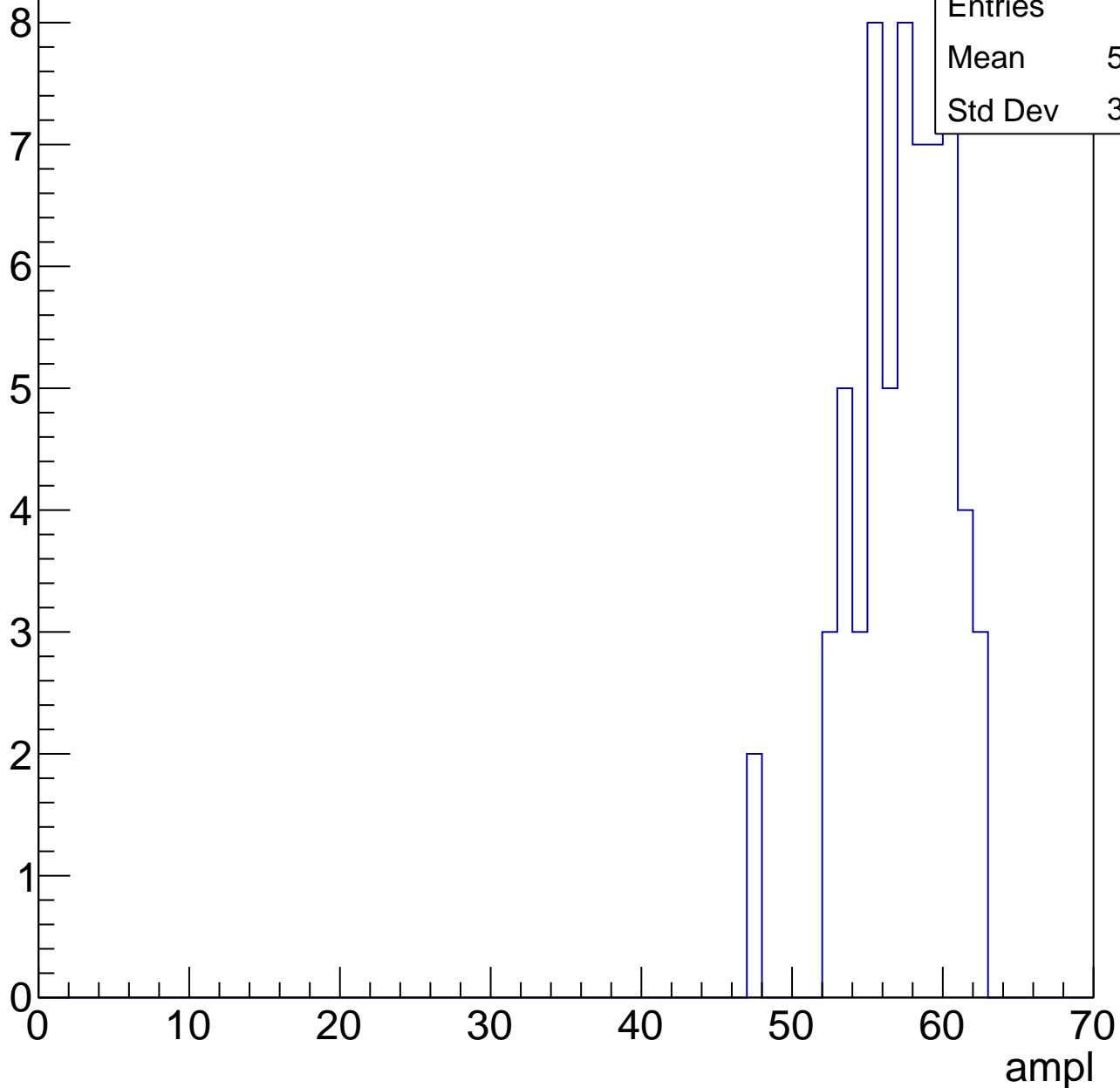
Entries	64
Mean	51.17
Std Dev	3.195



B0L002S, U12-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

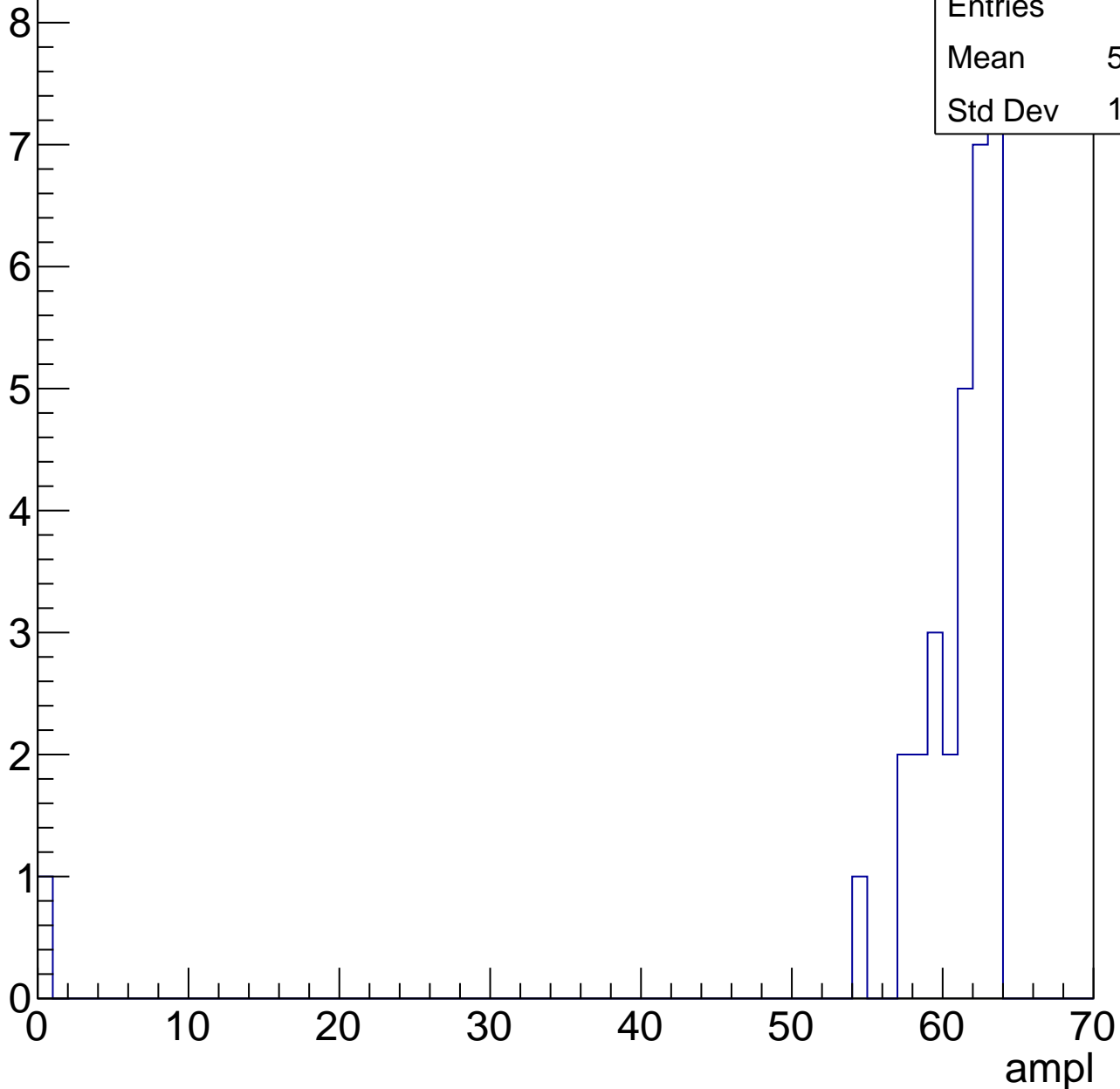


B0L002S, U12-ch82, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.84
Std Dev	10.97



B0L002S, U12-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch83, adc0

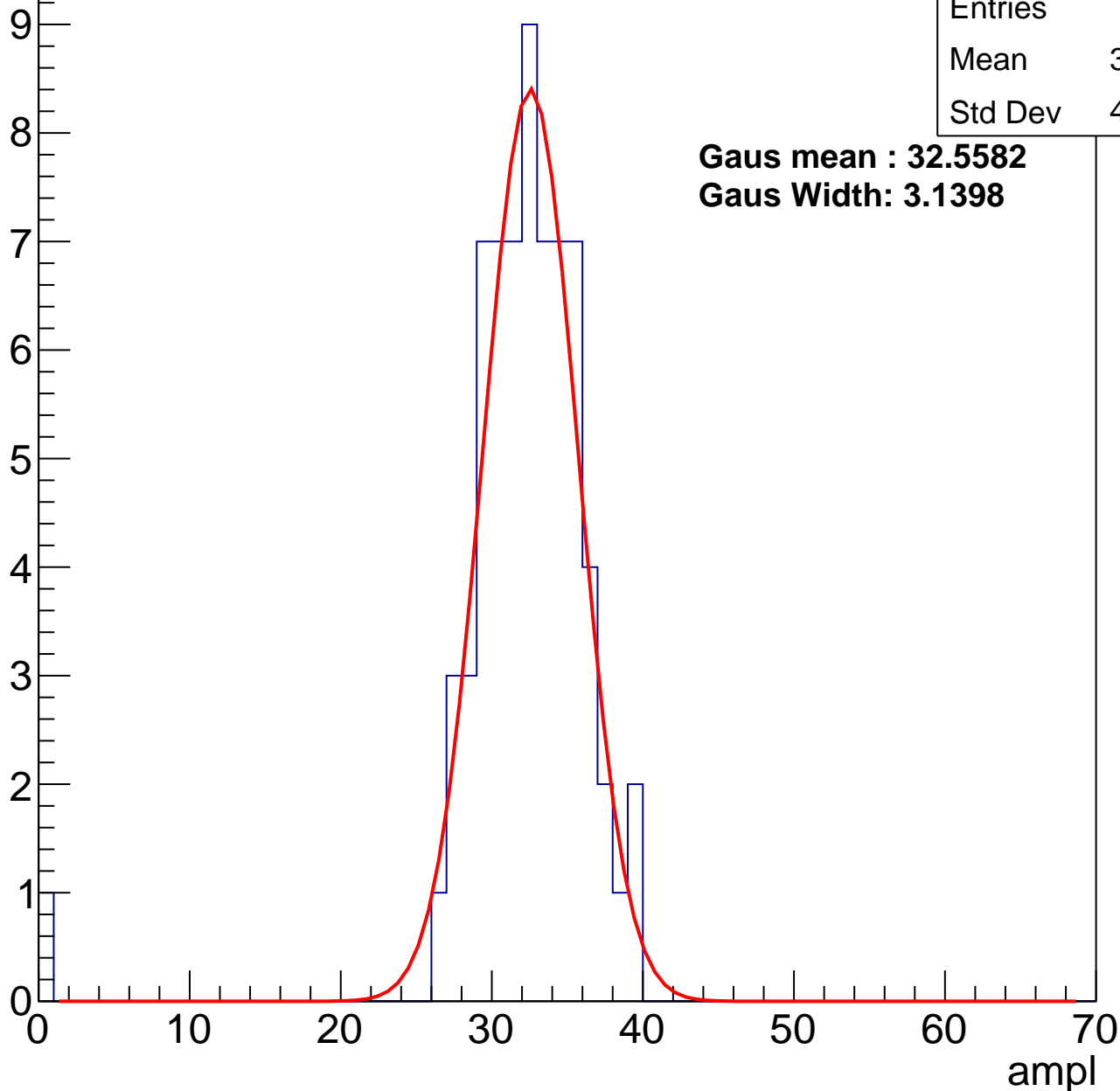
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	31.72
Std Dev	4.883

Gaus mean : 32.5582

Gaus Width: 3.1398



B0L002S, U12-ch83, adc1

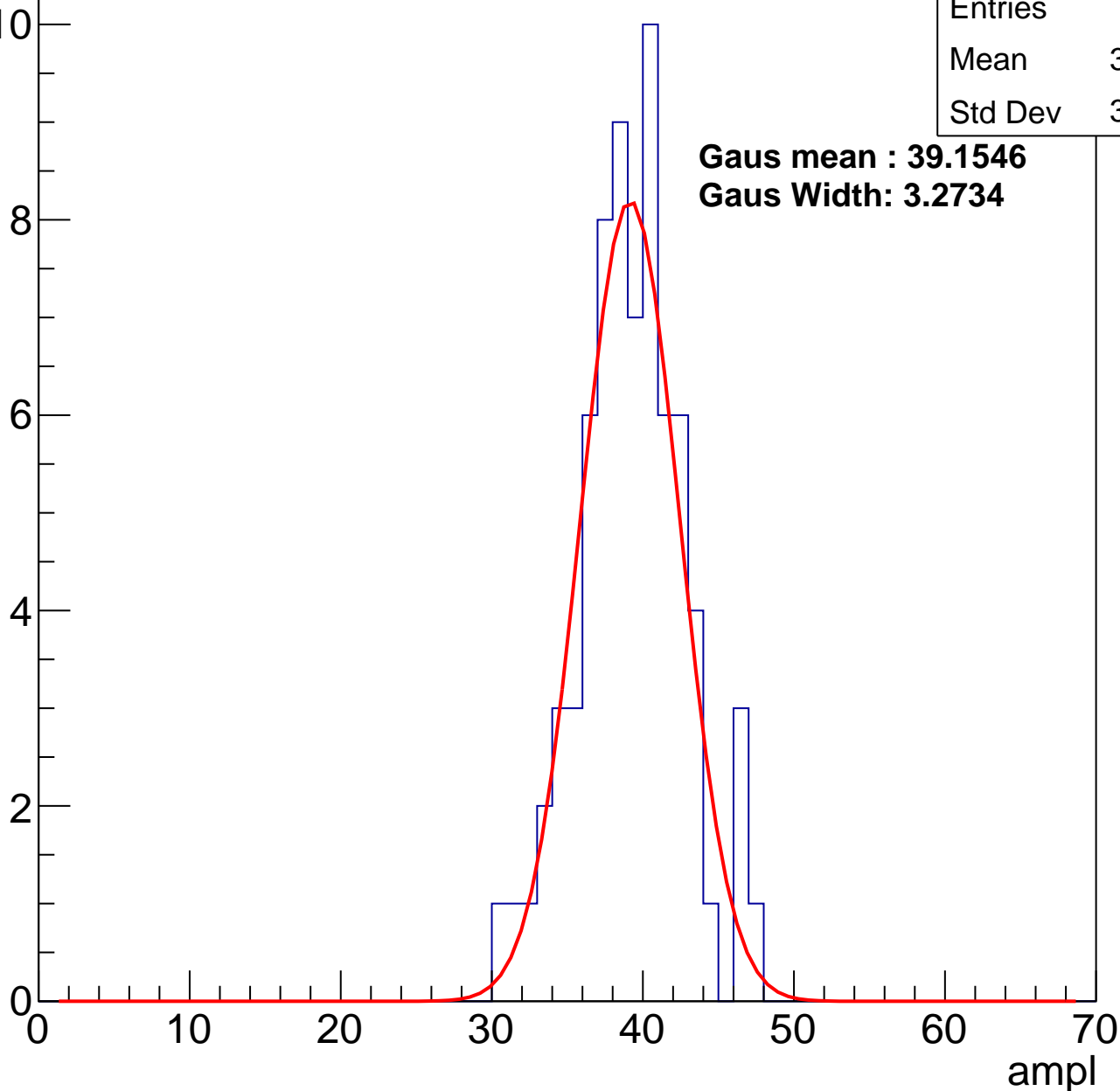
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	38.78
Std Dev	3.473

Gaus mean : 39.1546

Gaus Width: 3.2734



B0L002S, U12-ch83, adc2

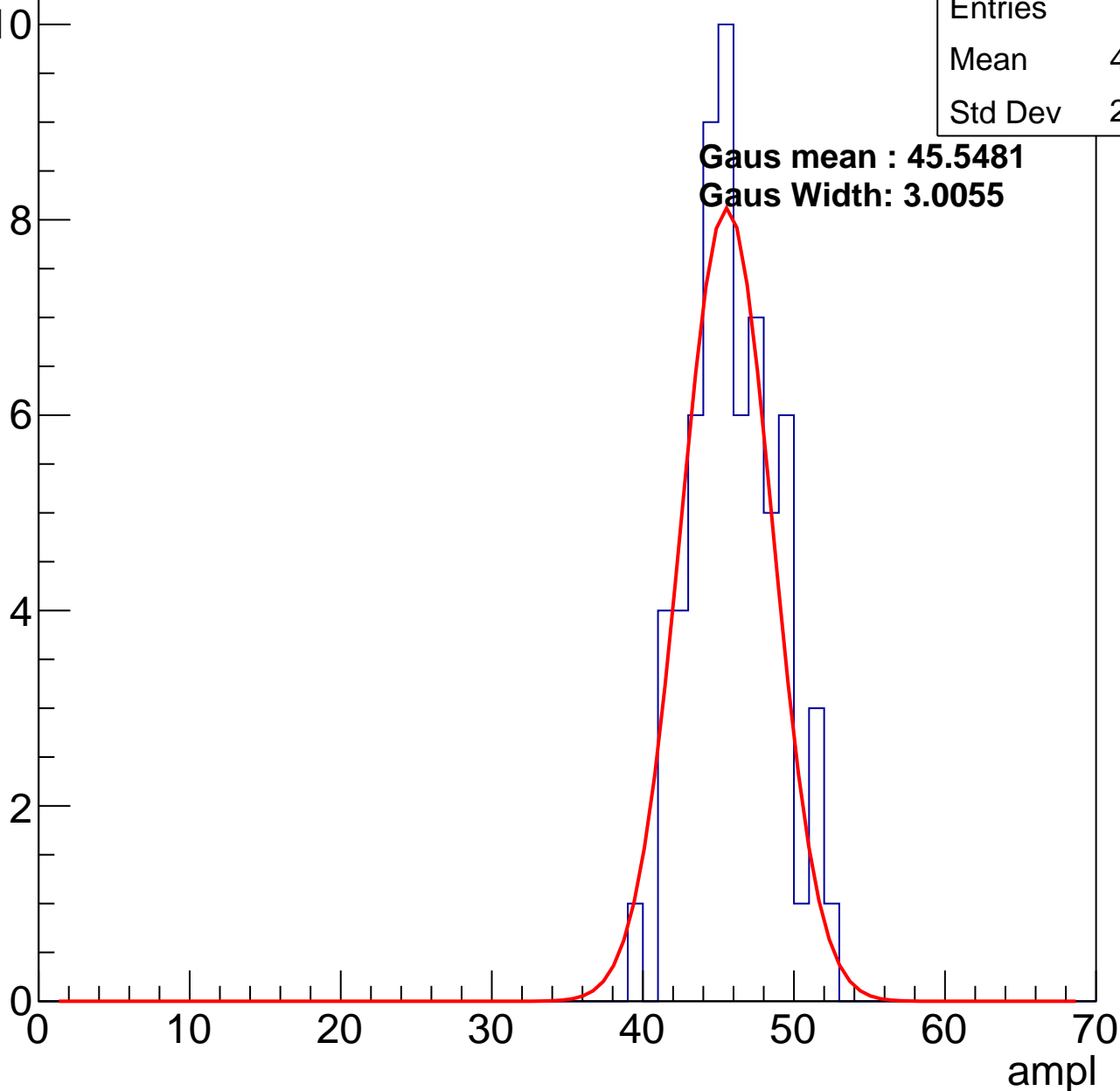
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	45.54
Std Dev	2.839

Gaus mean : 45.5481

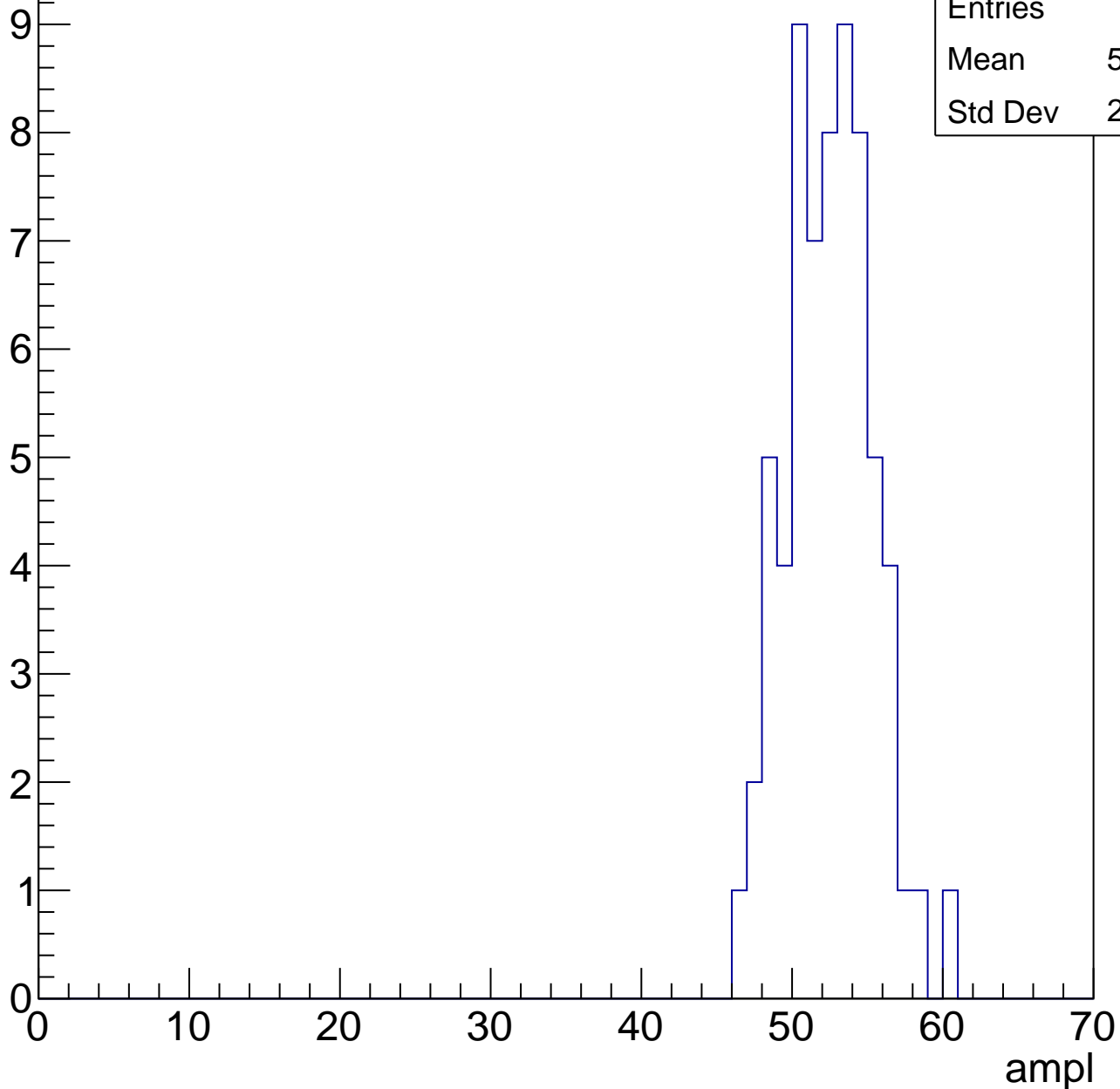
Gaus Width: 3.0055



B0L002S, U12-ch83, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

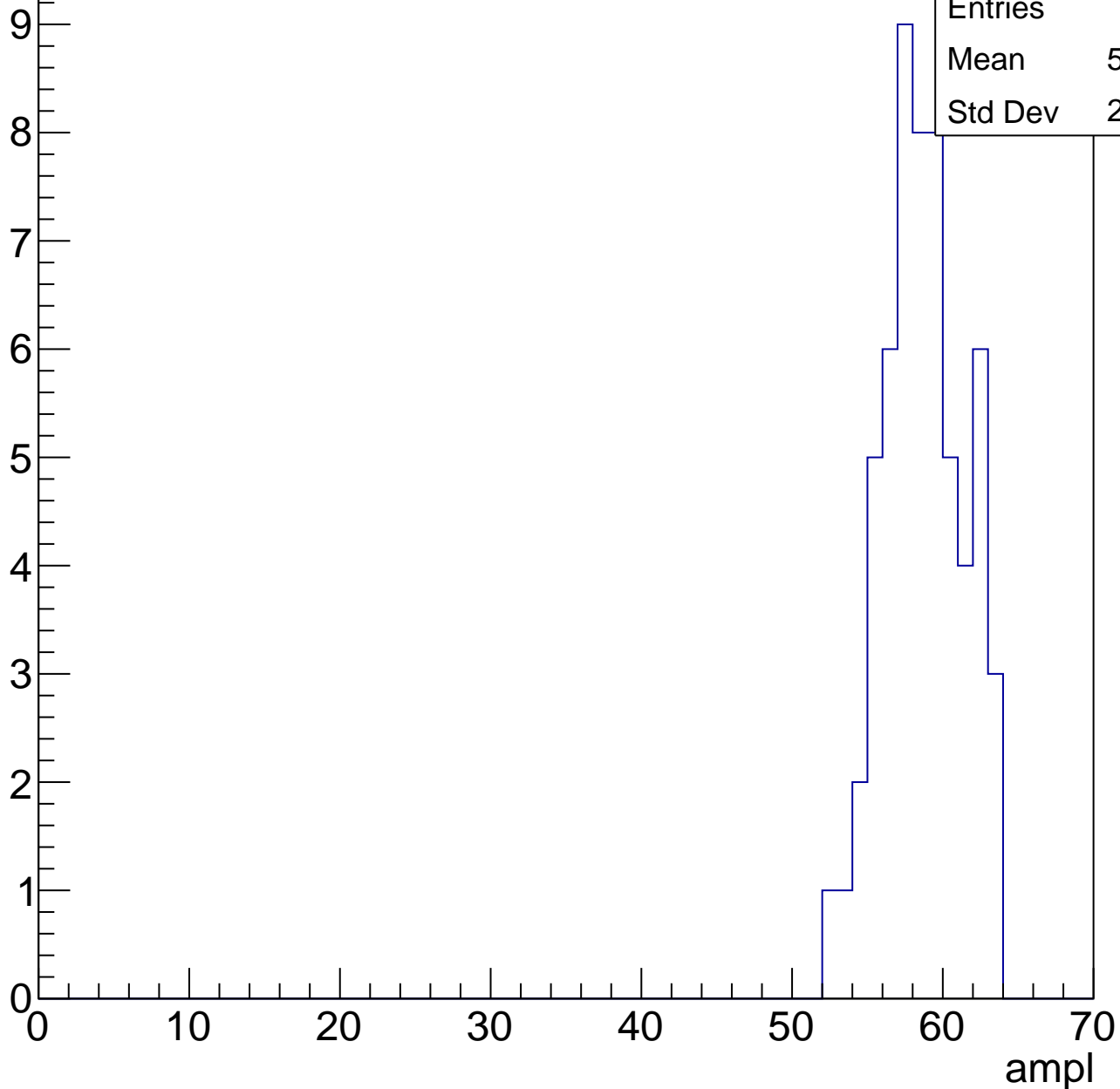


Entries	65
Mean	52.03
Std Dev	2.828

B0L002S, U12-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



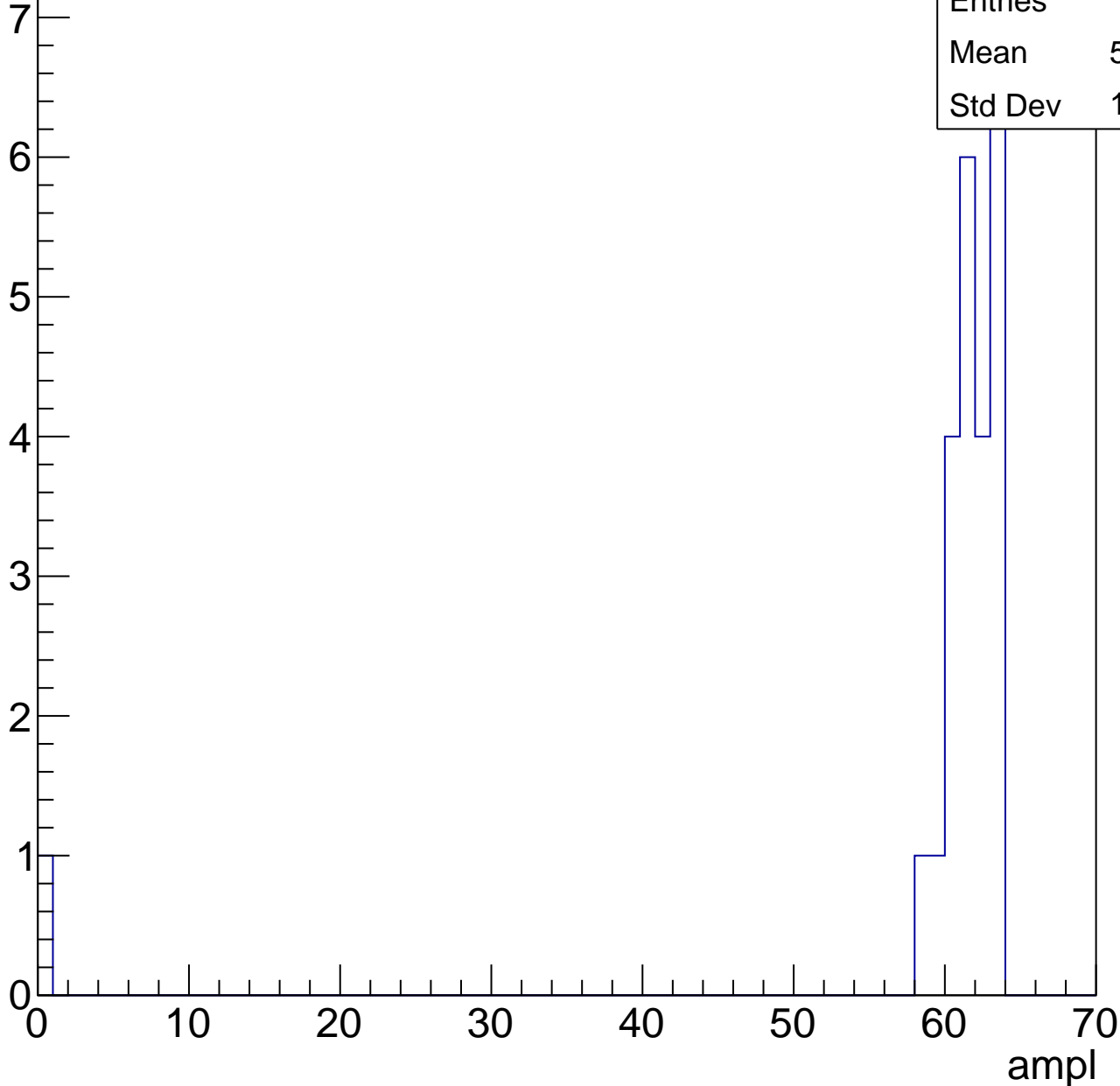
Entries	58
Mean	58.24
Std Dev	2.635

B0L002S, U12-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

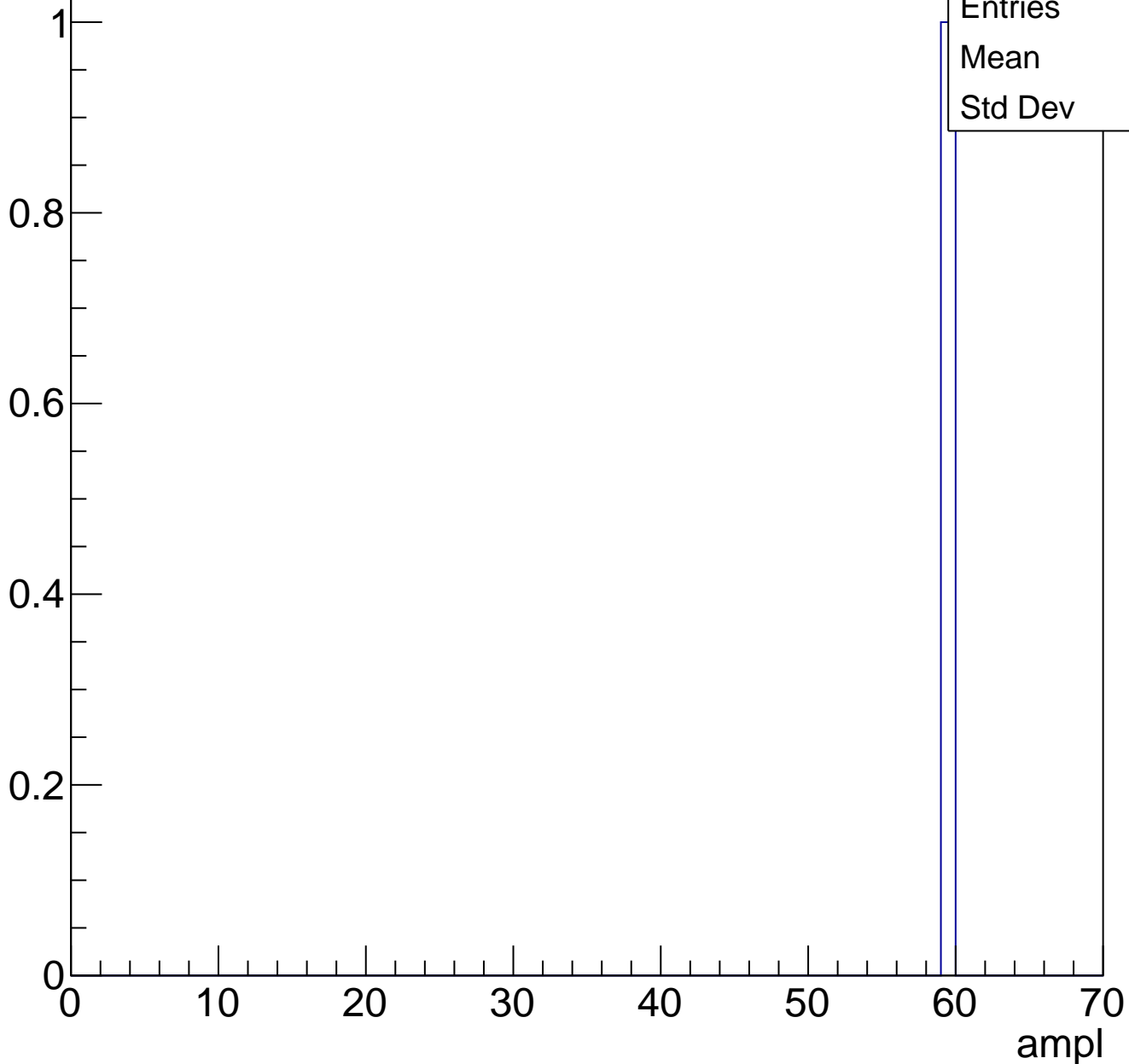
Entries	24
Mean	58.83
Std Dev	12.34



B0L002S, U12-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L002S, U12-ch84, adc0

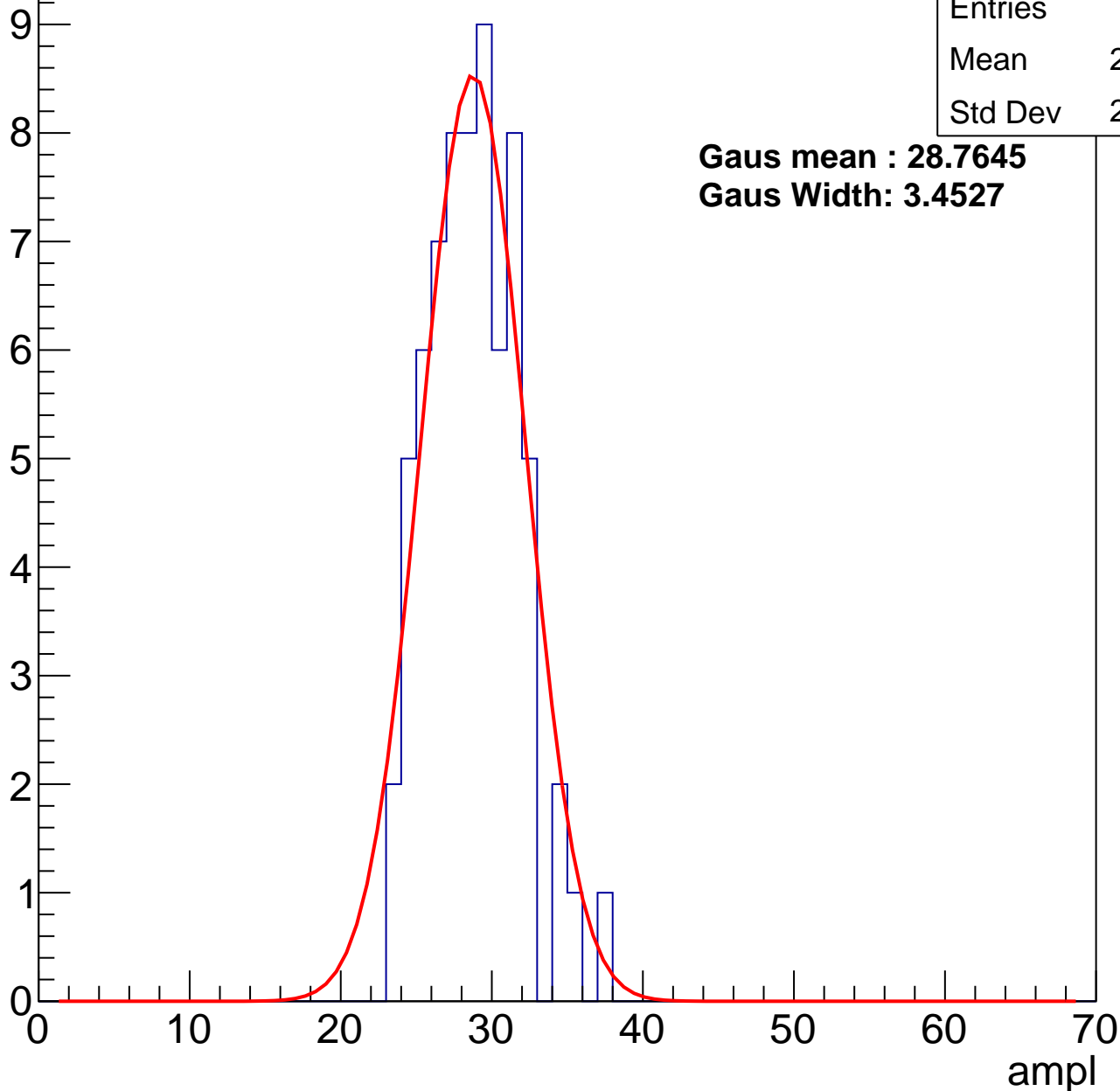
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.34
Std Dev	2.969

Gaus mean : 28.7645

Gaus Width: 3.4527



B0L002S, U12-ch84, adc1

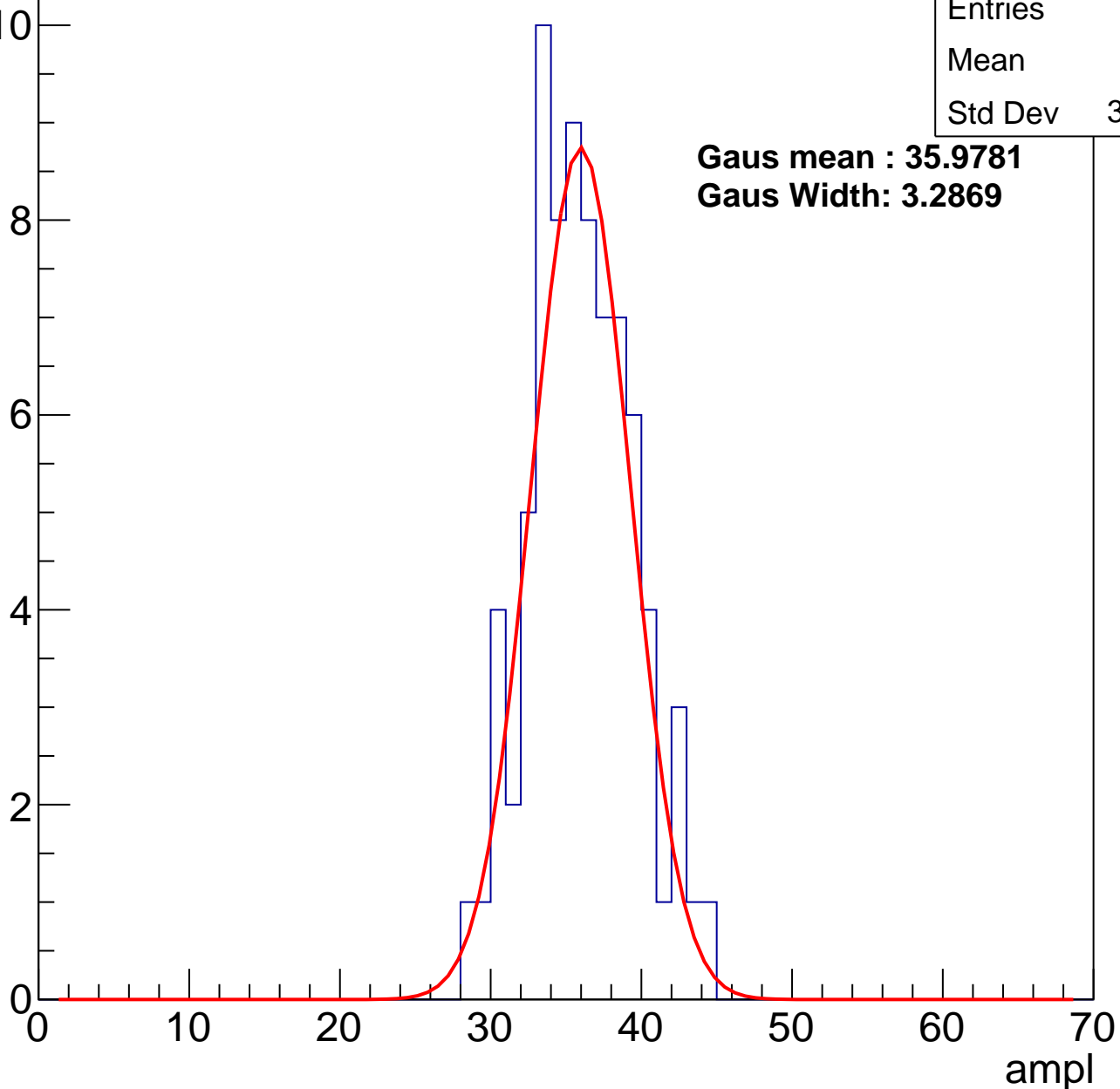
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	35.6
Std Dev	3.406

Gaus mean : 35.9781

Gaus Width: 3.2869



B0L002S, U12-ch84, adc2

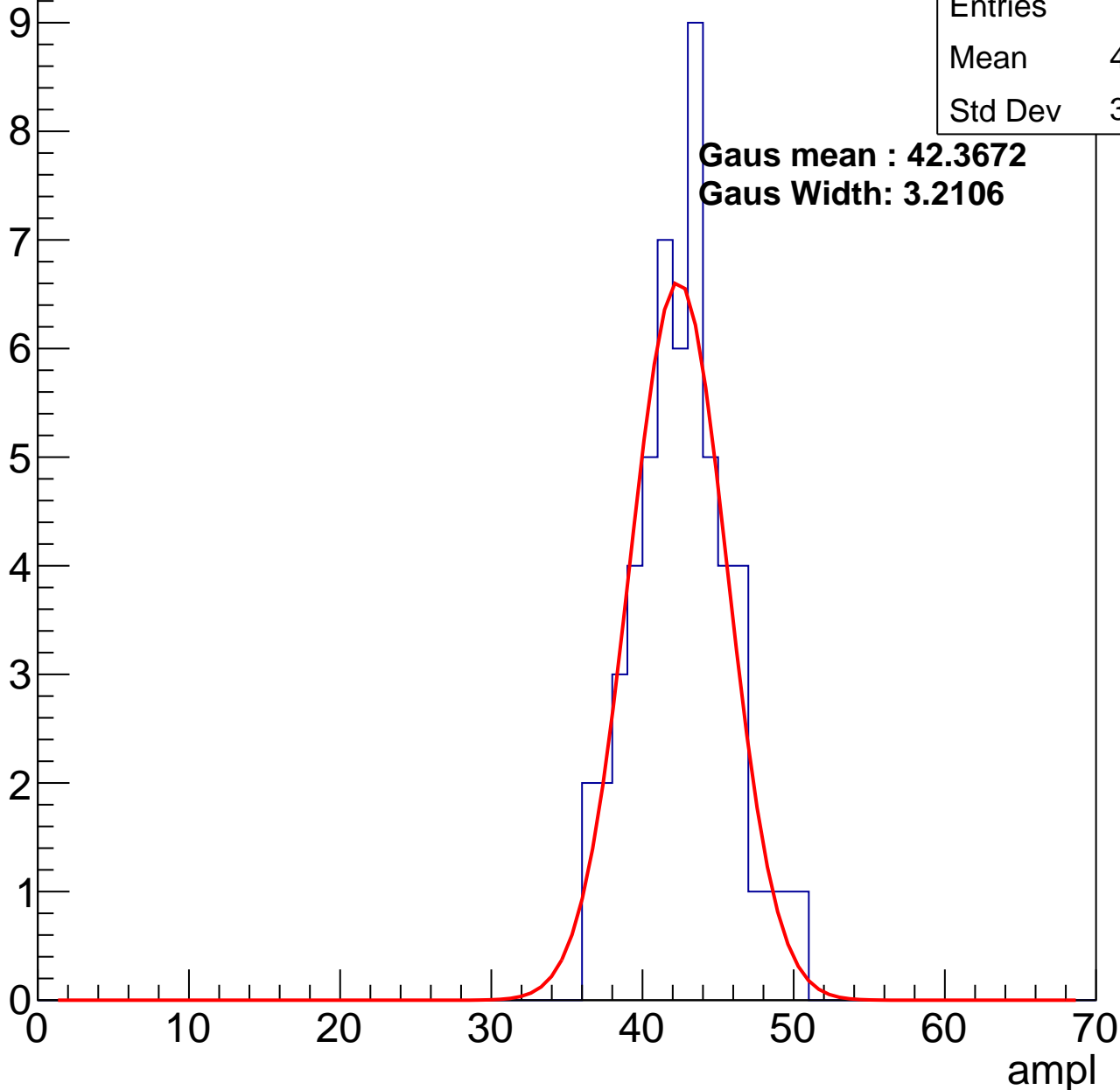
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	42.18
Std Dev	3.116

Gaus mean : 42.3672

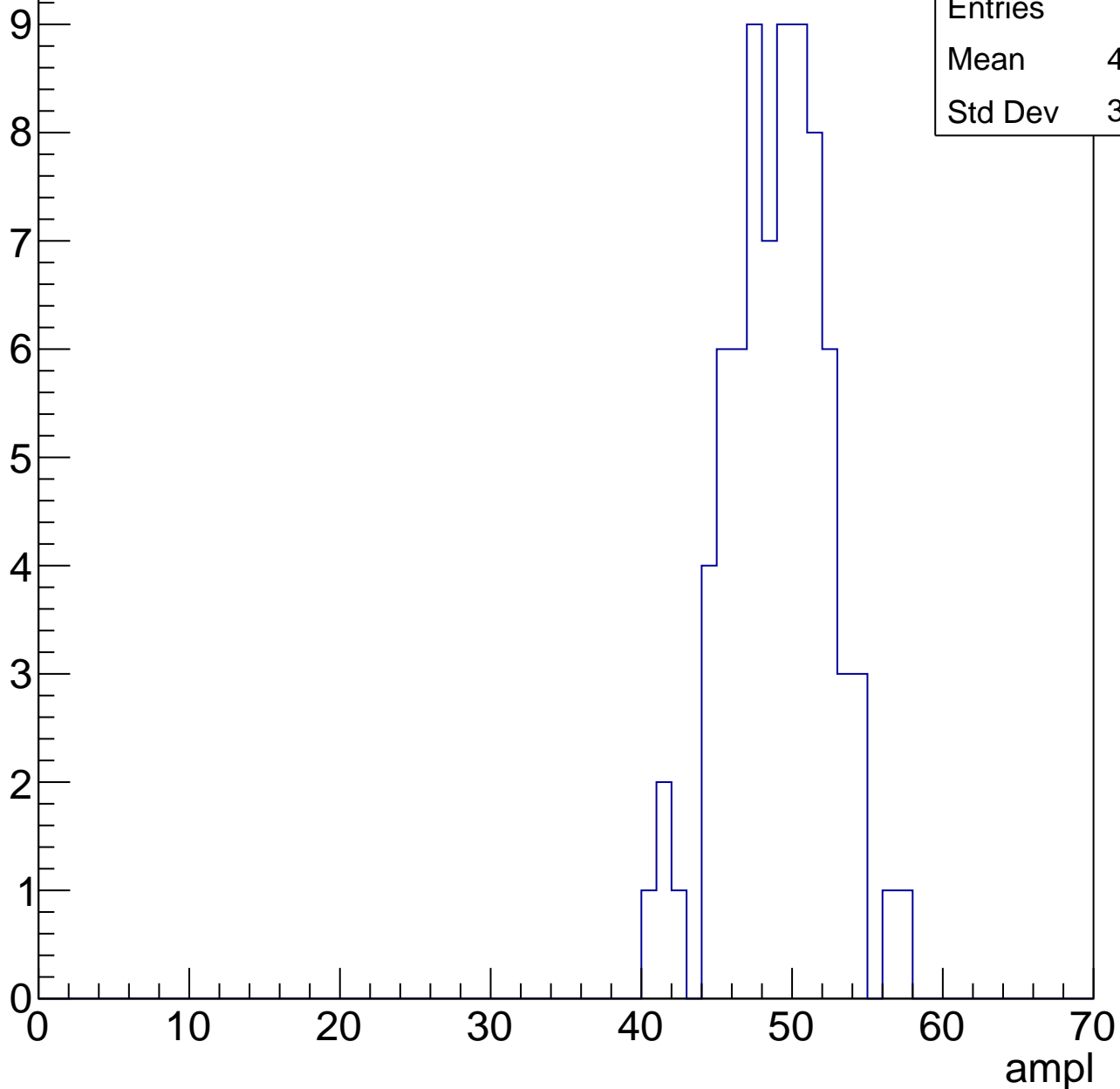
Gaus Width: 3.2106



B0L002S, U12-ch84, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



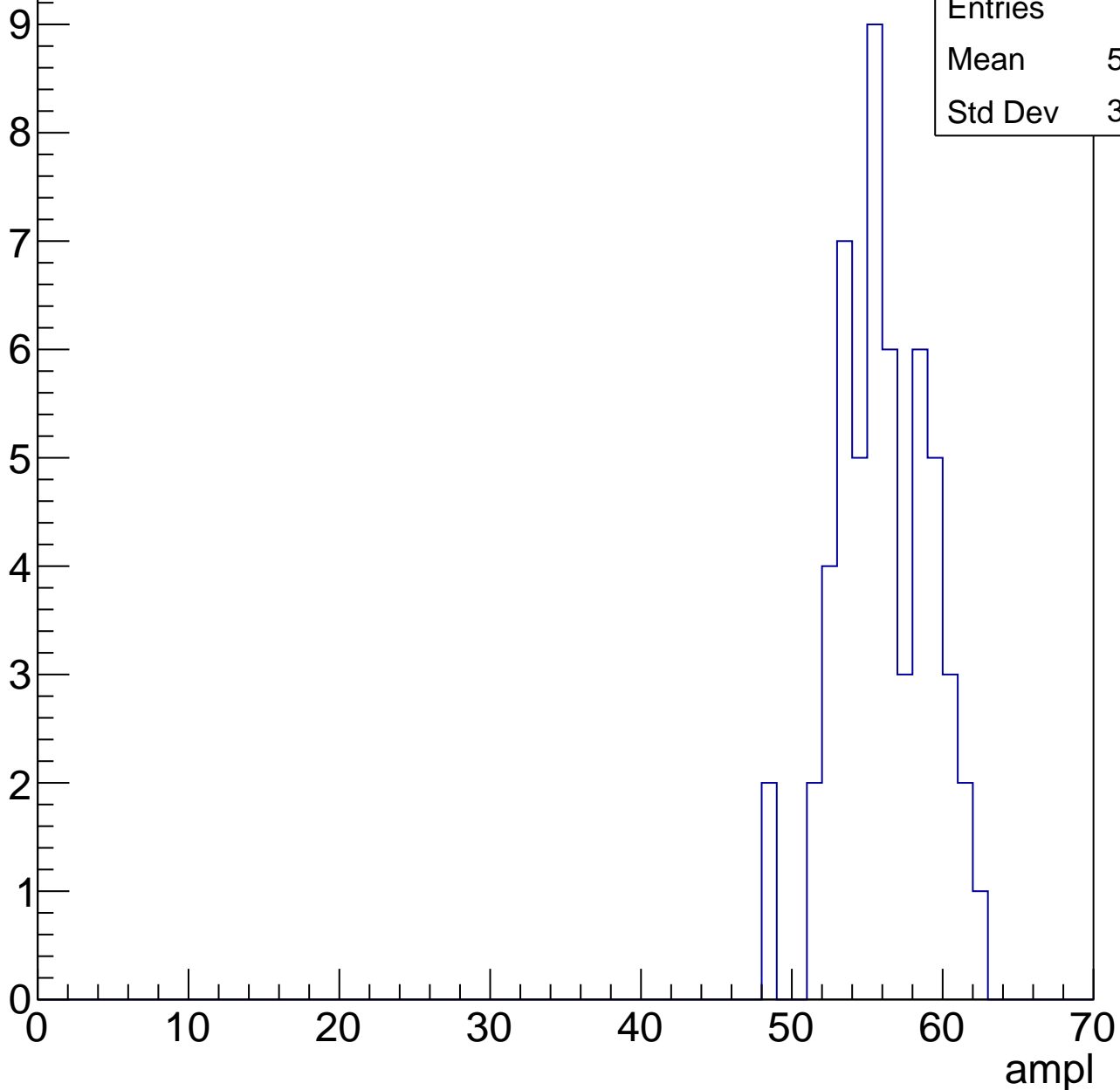
Entries	76
Mean	48.55
Std Dev	3.377

B0L002S, U12-ch84, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	55.56
Std Dev	3.103

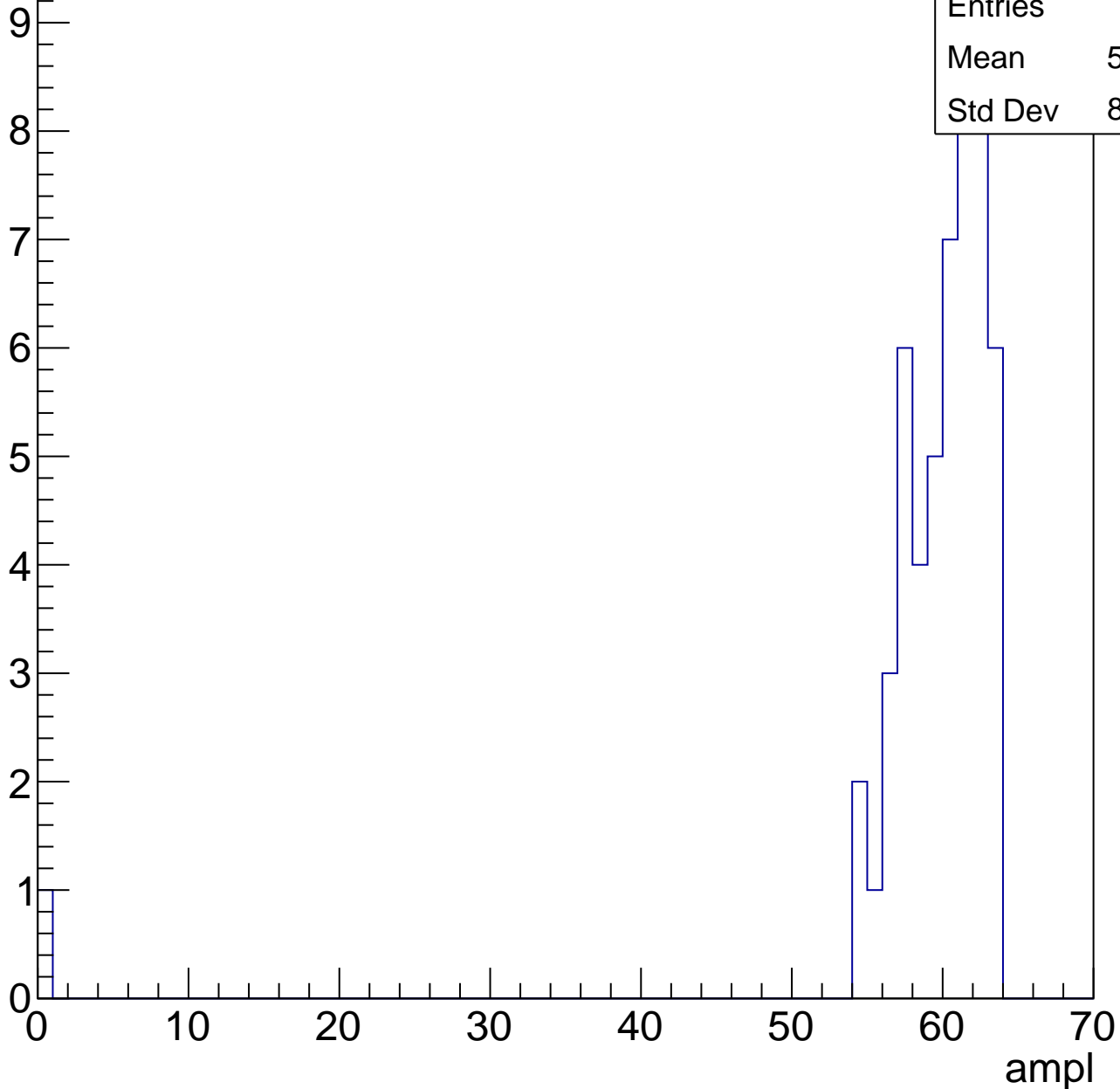


B0L002S, U12-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	58.54
Std Dev	8.556



B0L002S, U12-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U12-ch85, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	32.11
Std Dev	3.454

Gaus mean : 33.2437

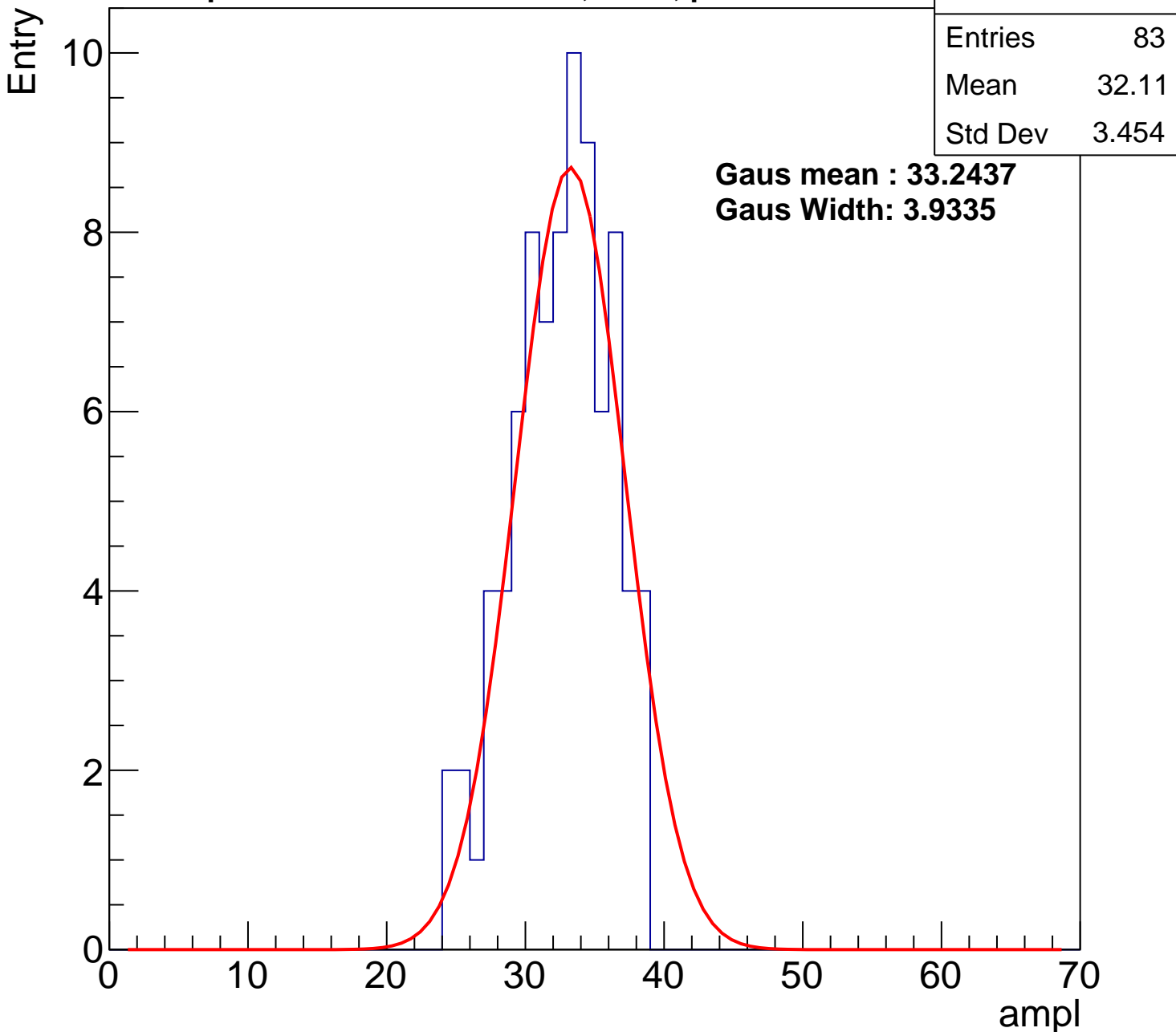
Gaus Width: 3.9335

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

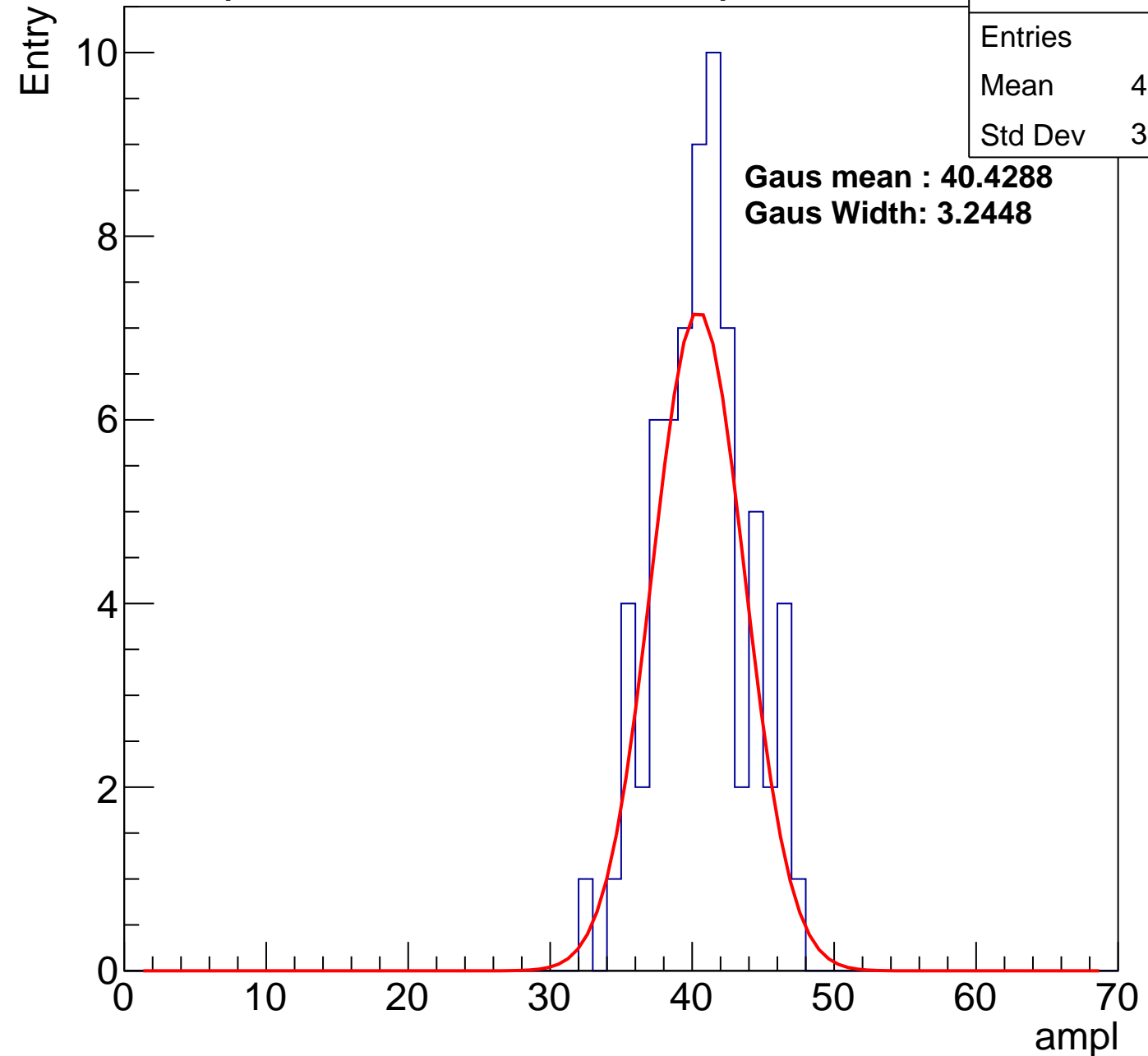


B0L002S, U12-ch85, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	40.18
Std Dev	3.218

Gaus mean : 40.4288
Gaus Width: 3.2448



B0L002S, U12-ch85, adc2

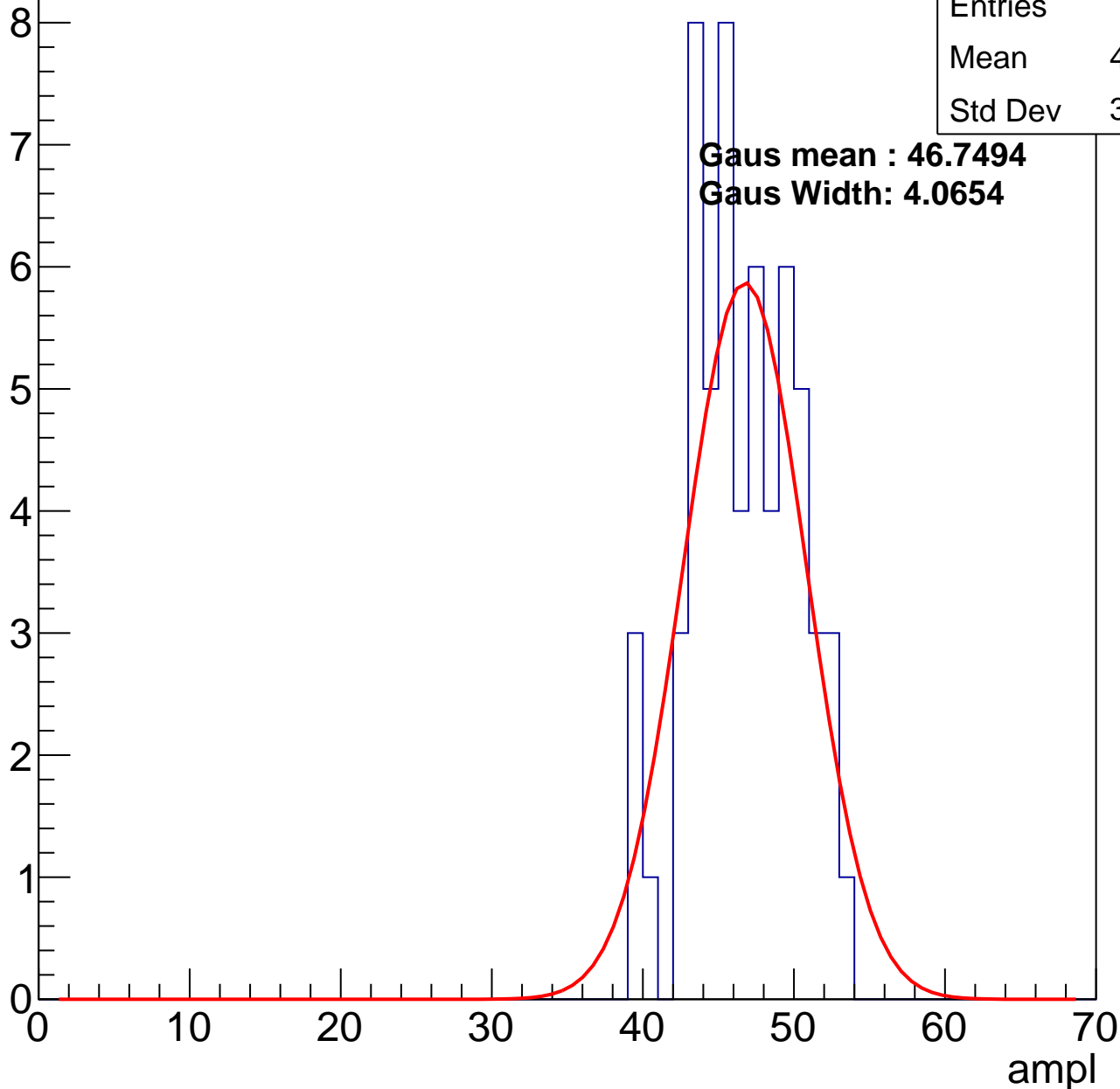
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	46.18
Std Dev	3.457

Gaus mean : 46.7494

Gaus Width: 4.0654

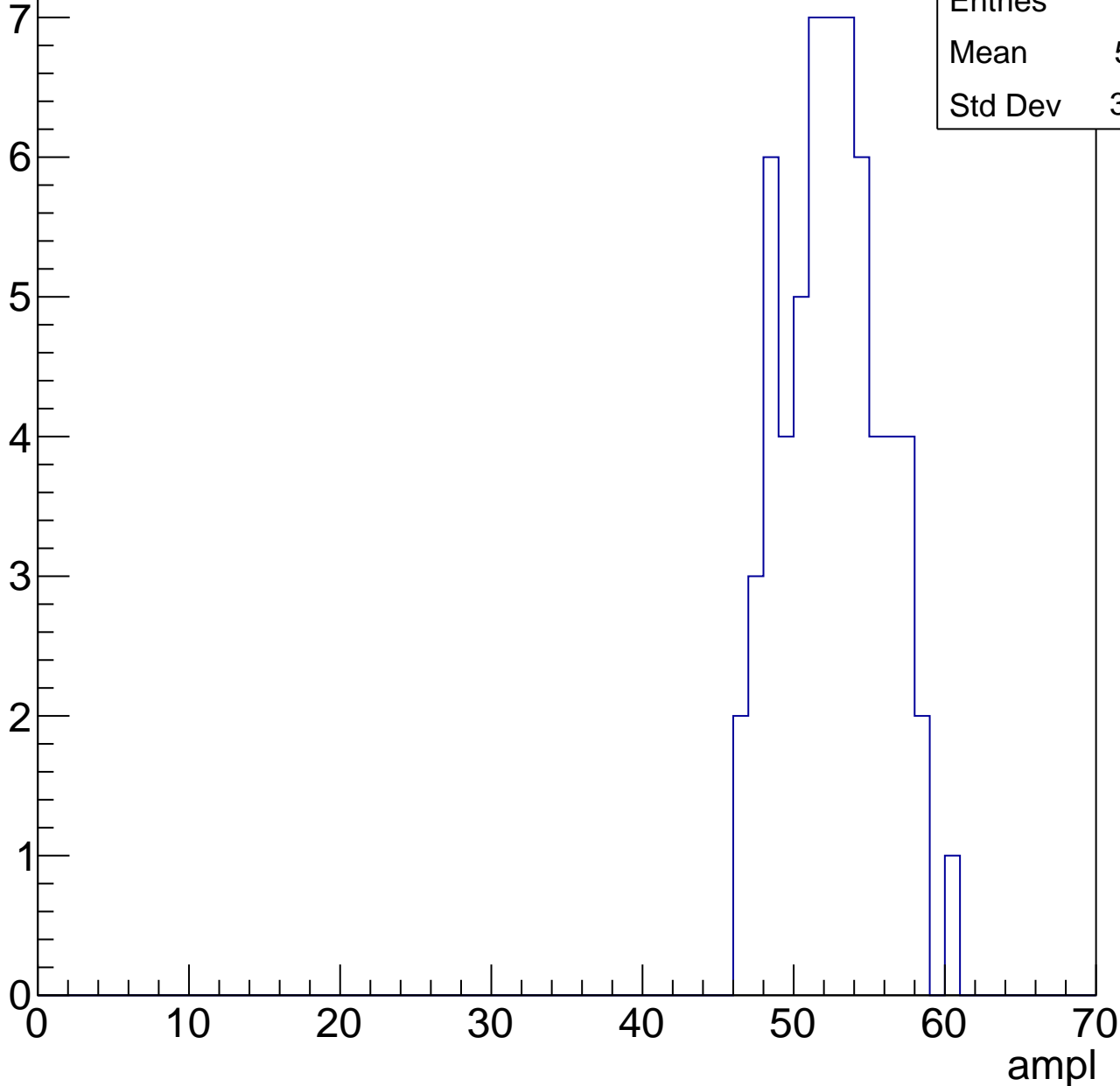


B0L002S, U12-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	52.11
Std Dev	3.293

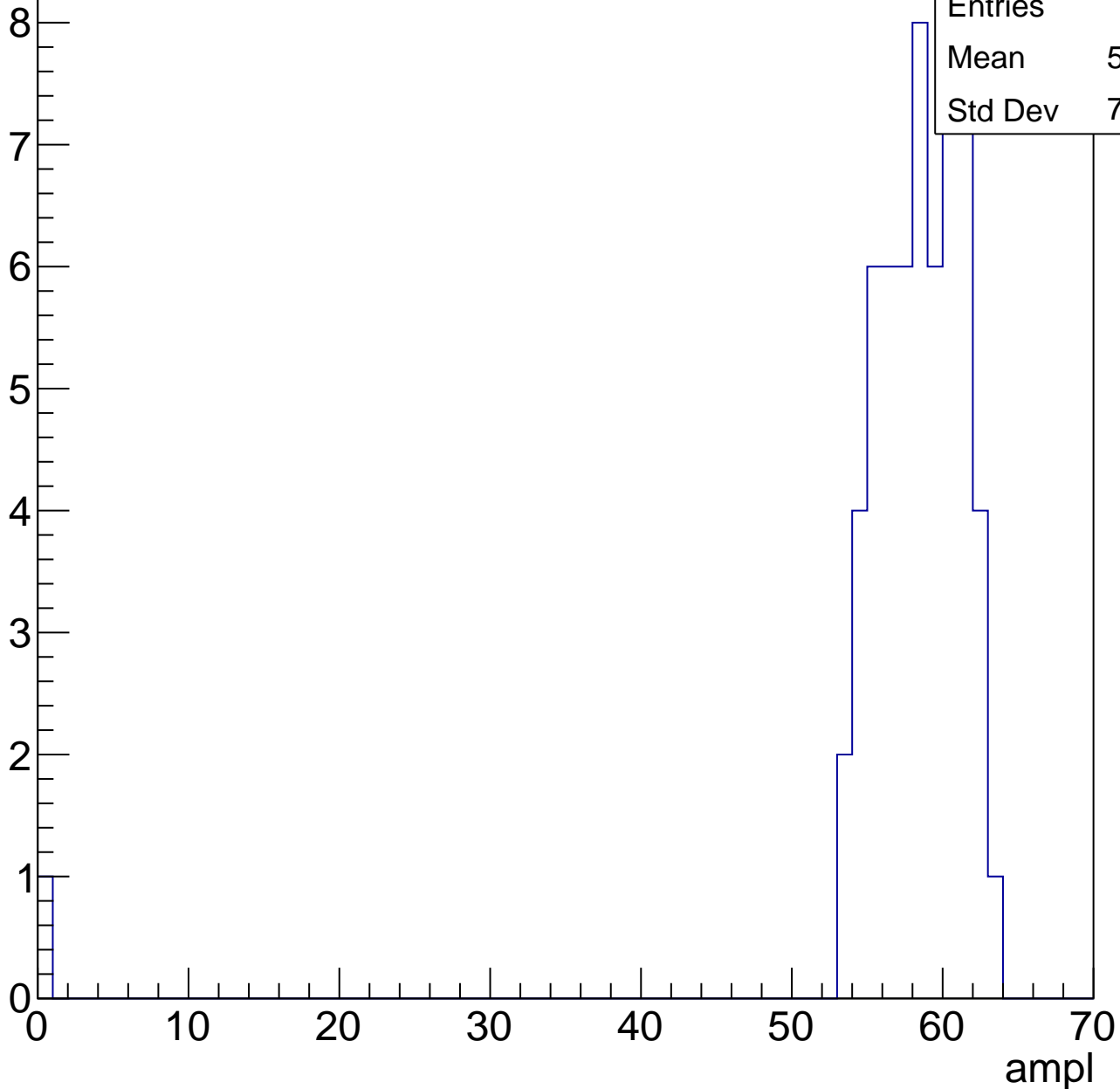


B0L002S, U12-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	57.12
Std Dev	7.868

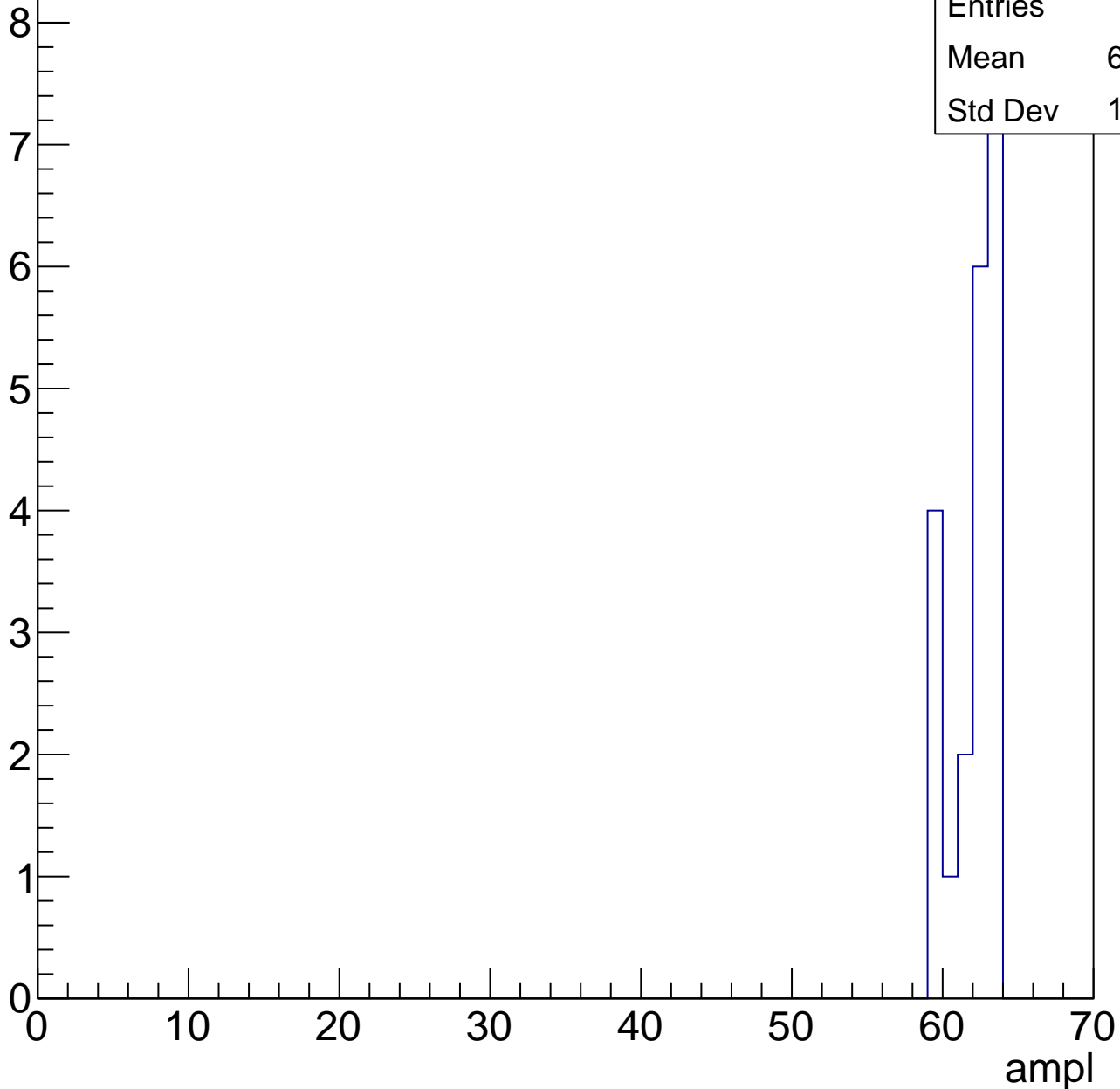


B0L002S, U12-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	61.62
Std Dev	1.495



B0L002S, U12-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch86, adc0

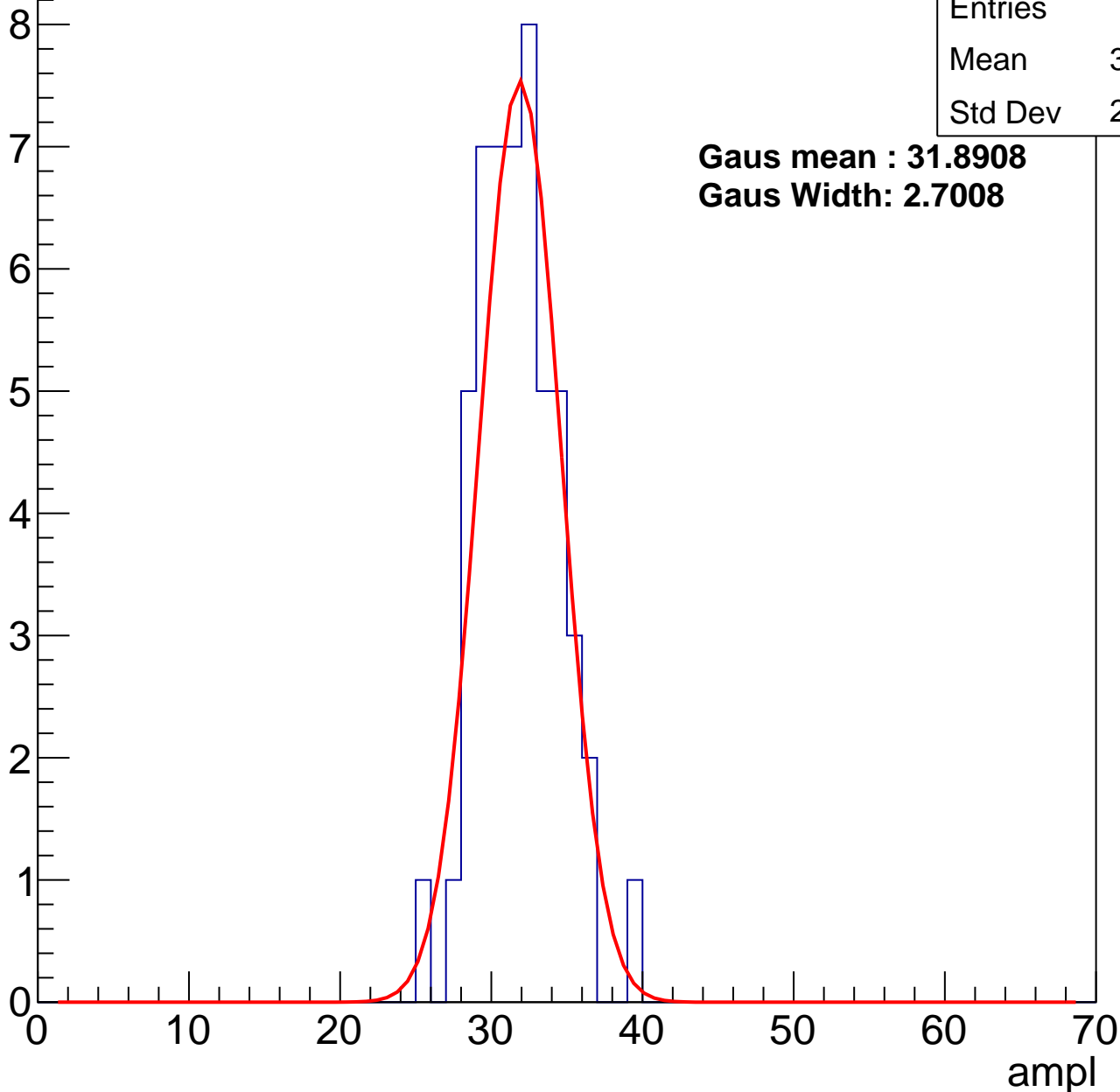
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	31.33
Std Dev	2.636

Gaus mean : 31.8908

Gaus Width: 2.7008



B0L002S, U12-ch86, adc1

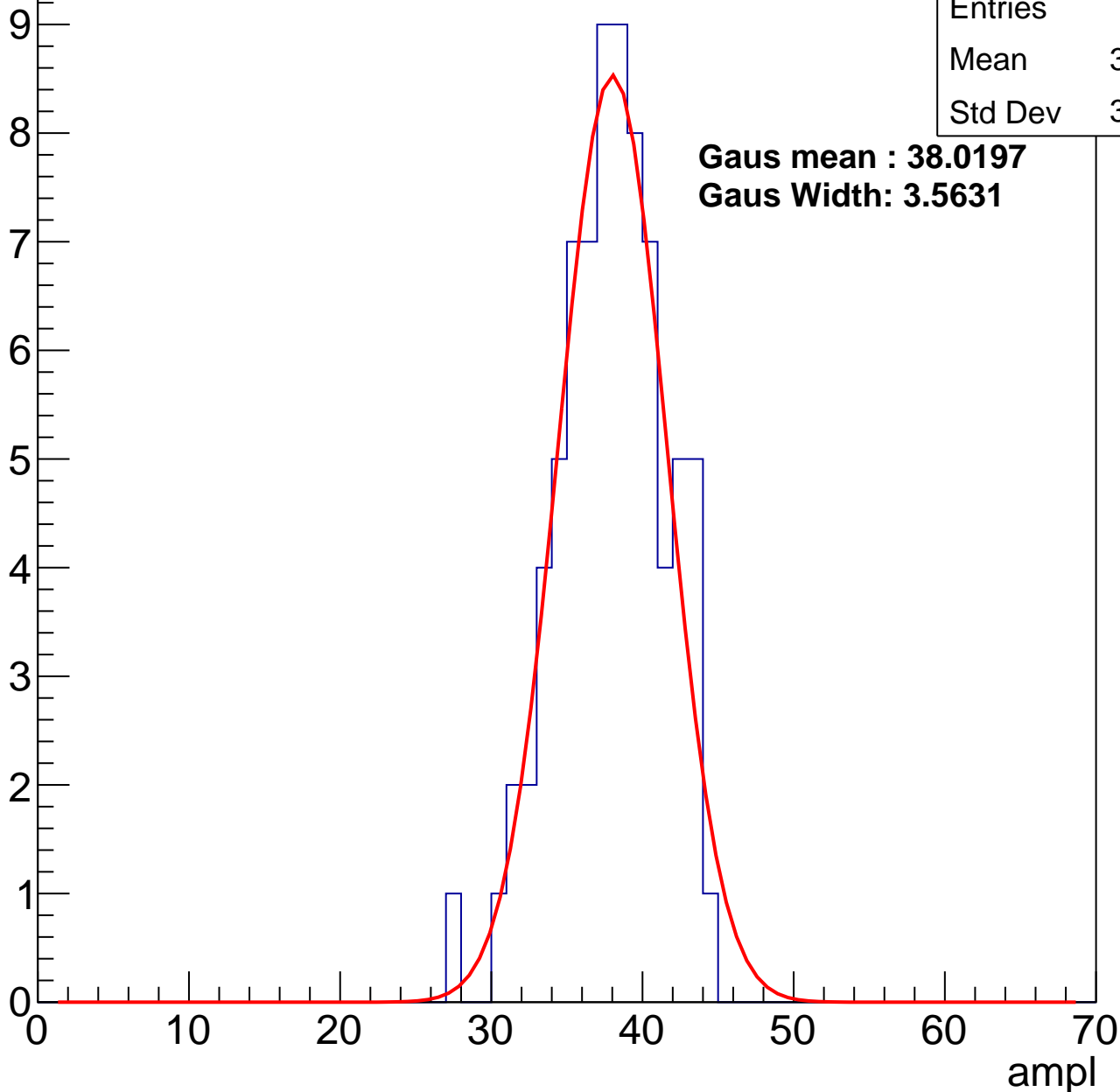
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	37.43
Std Dev	3.462

Gaus mean : 38.0197

Gaus Width: 3.5631



B0L002S, U12-ch86, adc2

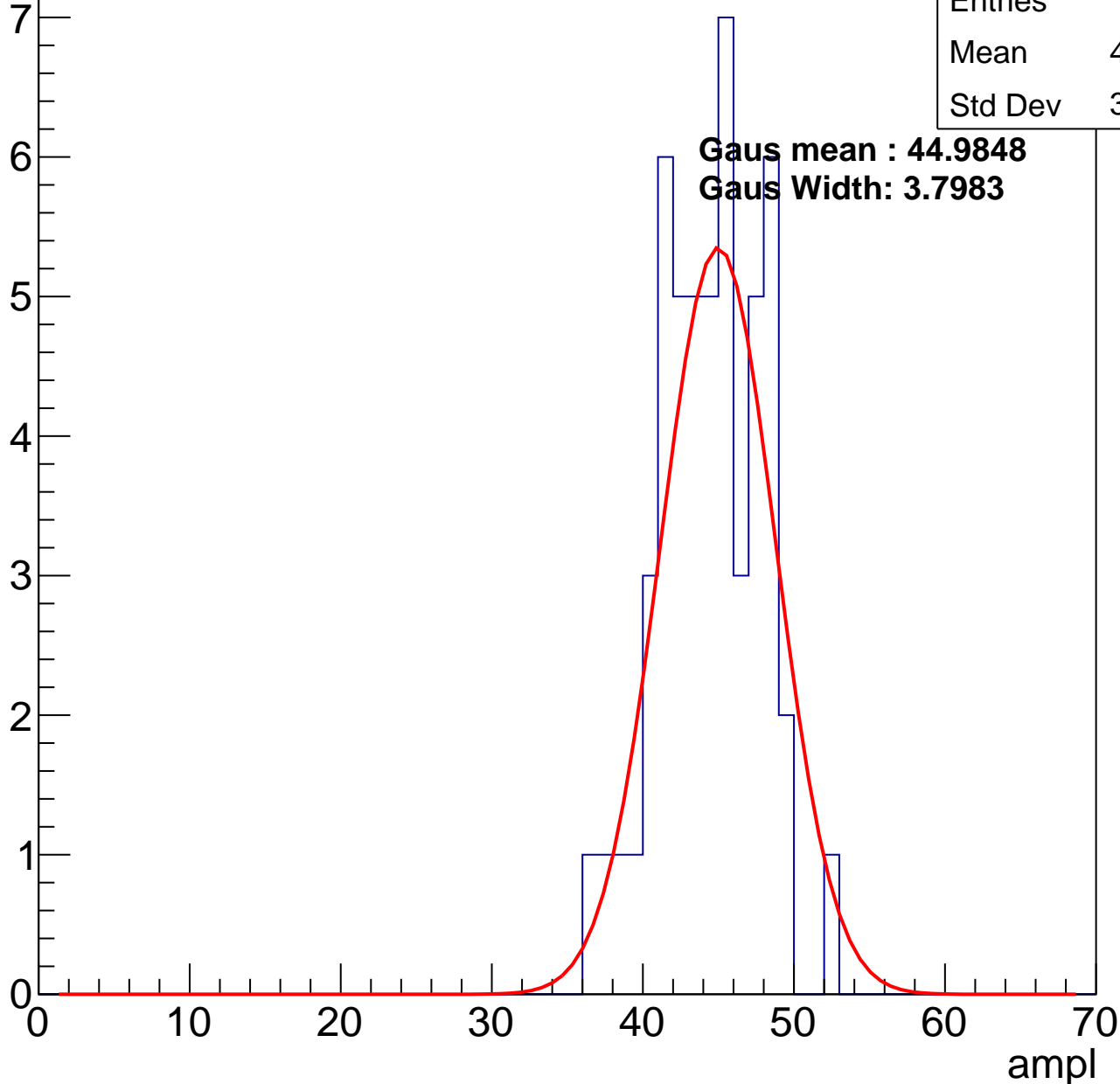
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	43.98
Std Dev	3.325

Gaus mean : 44.9848

Gaus Width: 3.7983



B0L002S, U12-ch86, adc3

calib_packv5_042523_0143.root, FC#8, port C1

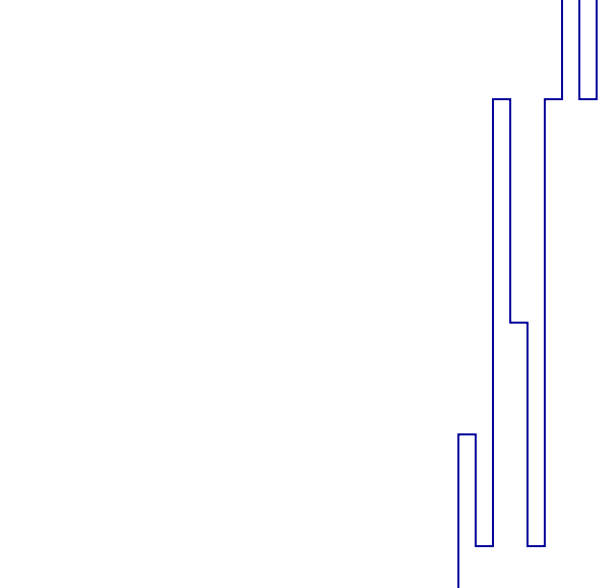
Entries	69
Mean	49.88
Std Dev	3.286

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

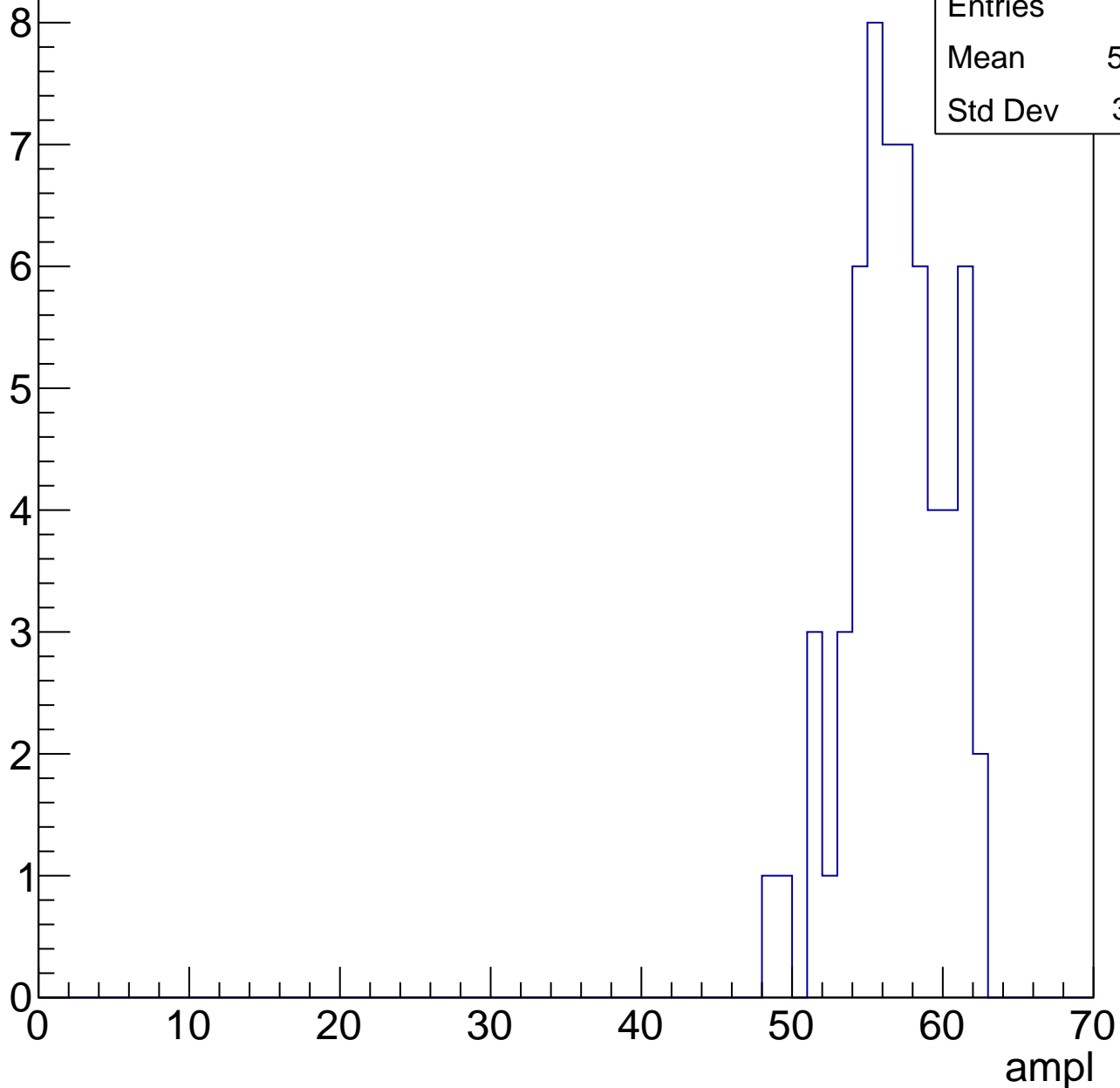


B0L002S, U12-ch86, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.44
Std Dev	3.201

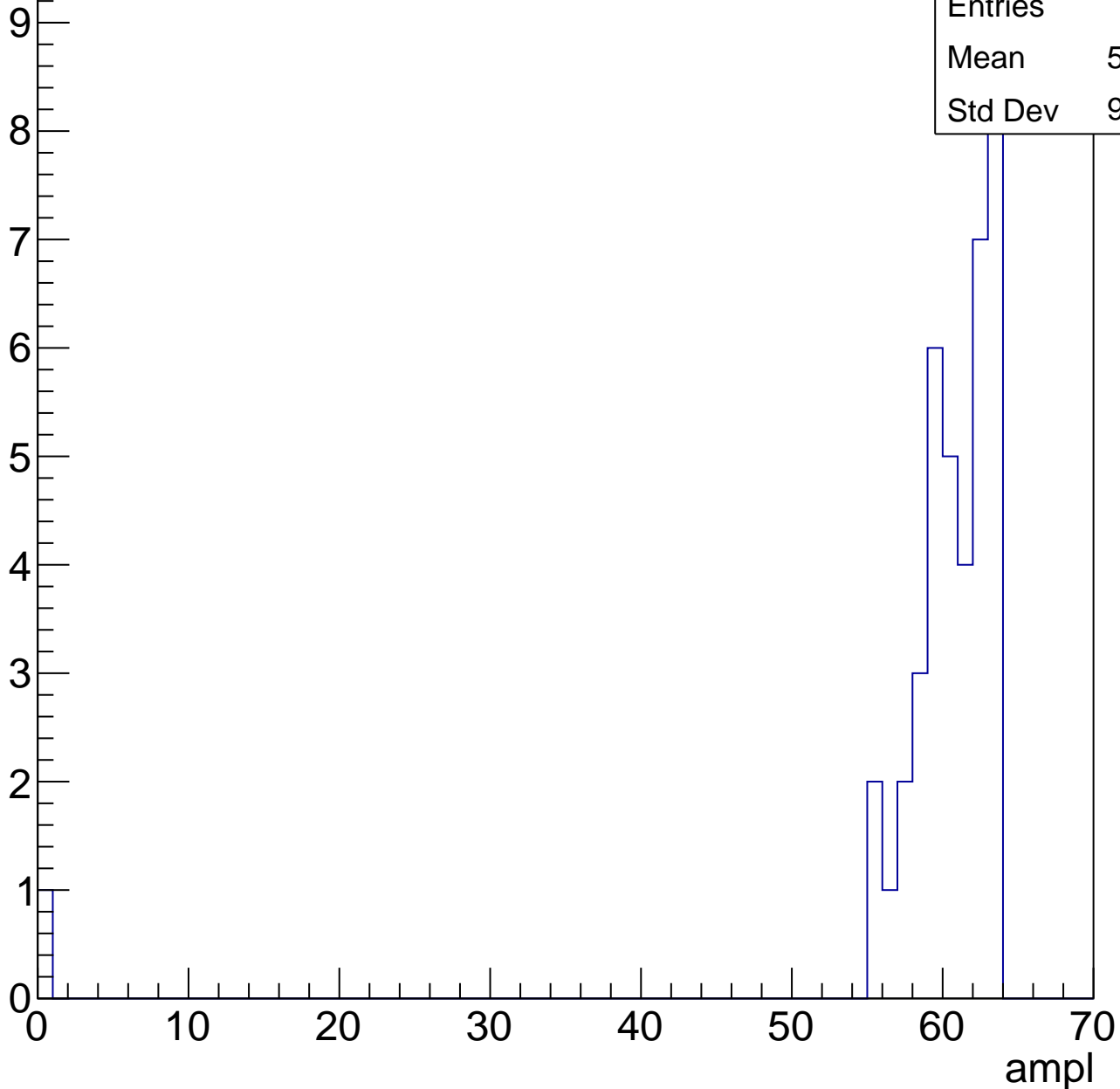


B0L002S, U12-ch86, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	58.83
Std Dev	9.695



B0L002S, U12-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch87, adc0

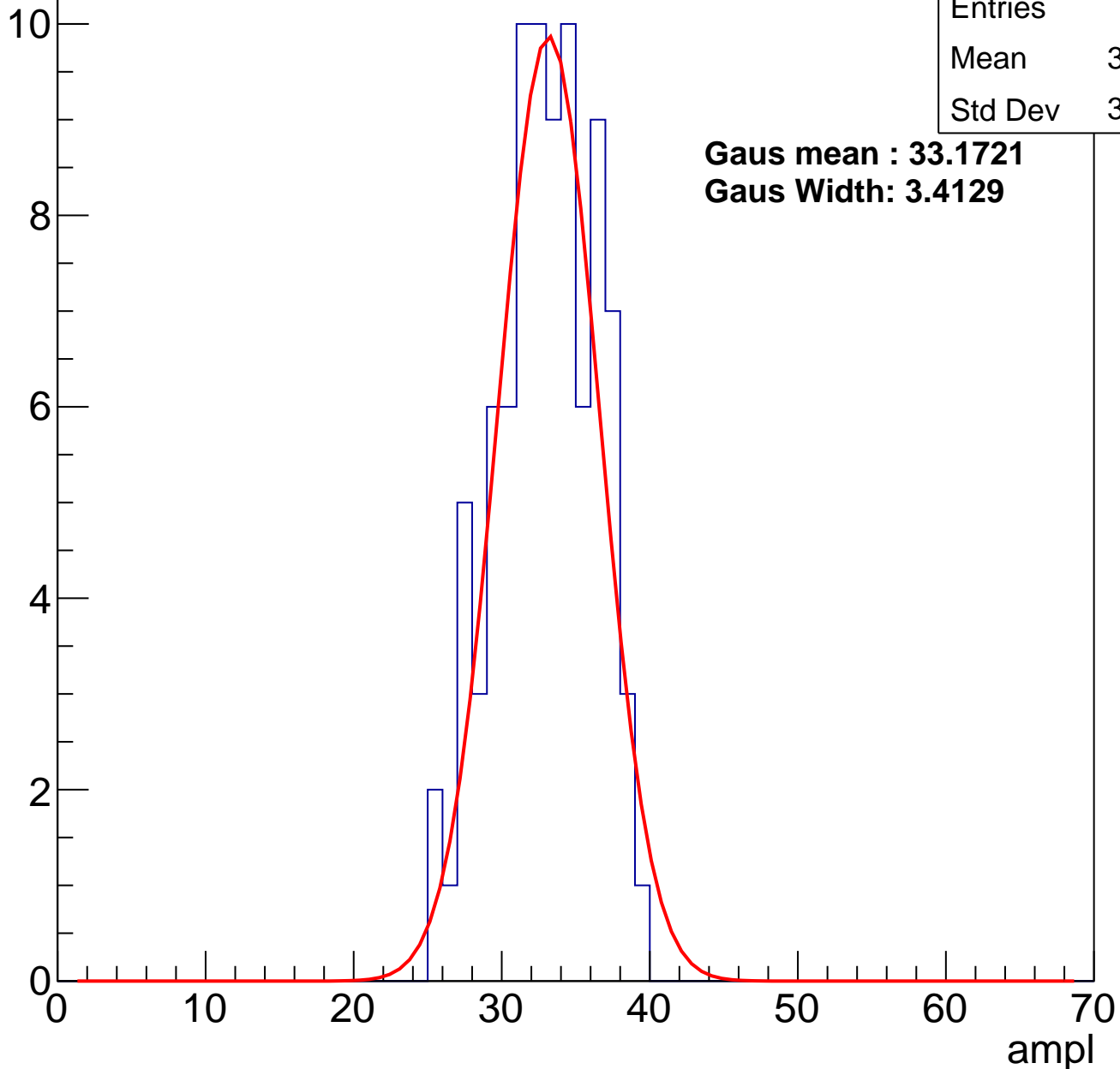
calib_packv5_042523_0143.root, FC#8, port C1

Entries	88
Mean	32.52
Std Dev	3.286

Gaus mean : 33.1721

Gaus Width: 3.4129

Entry



B0L002S, U12-ch87, adc1

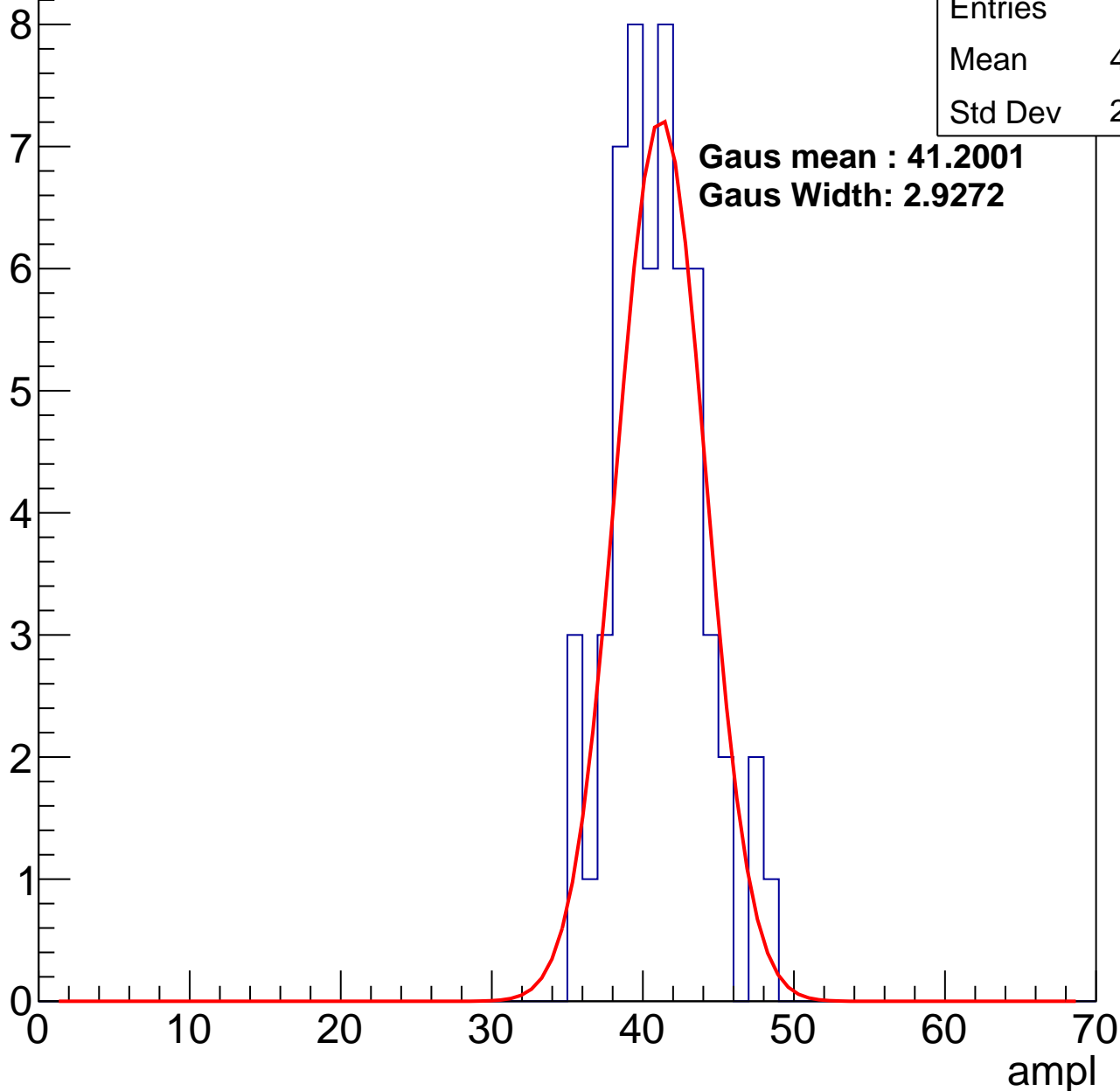
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	40.57
Std Dev	2.933

Gaus mean : 41.2001

Gaus Width: 2.9272

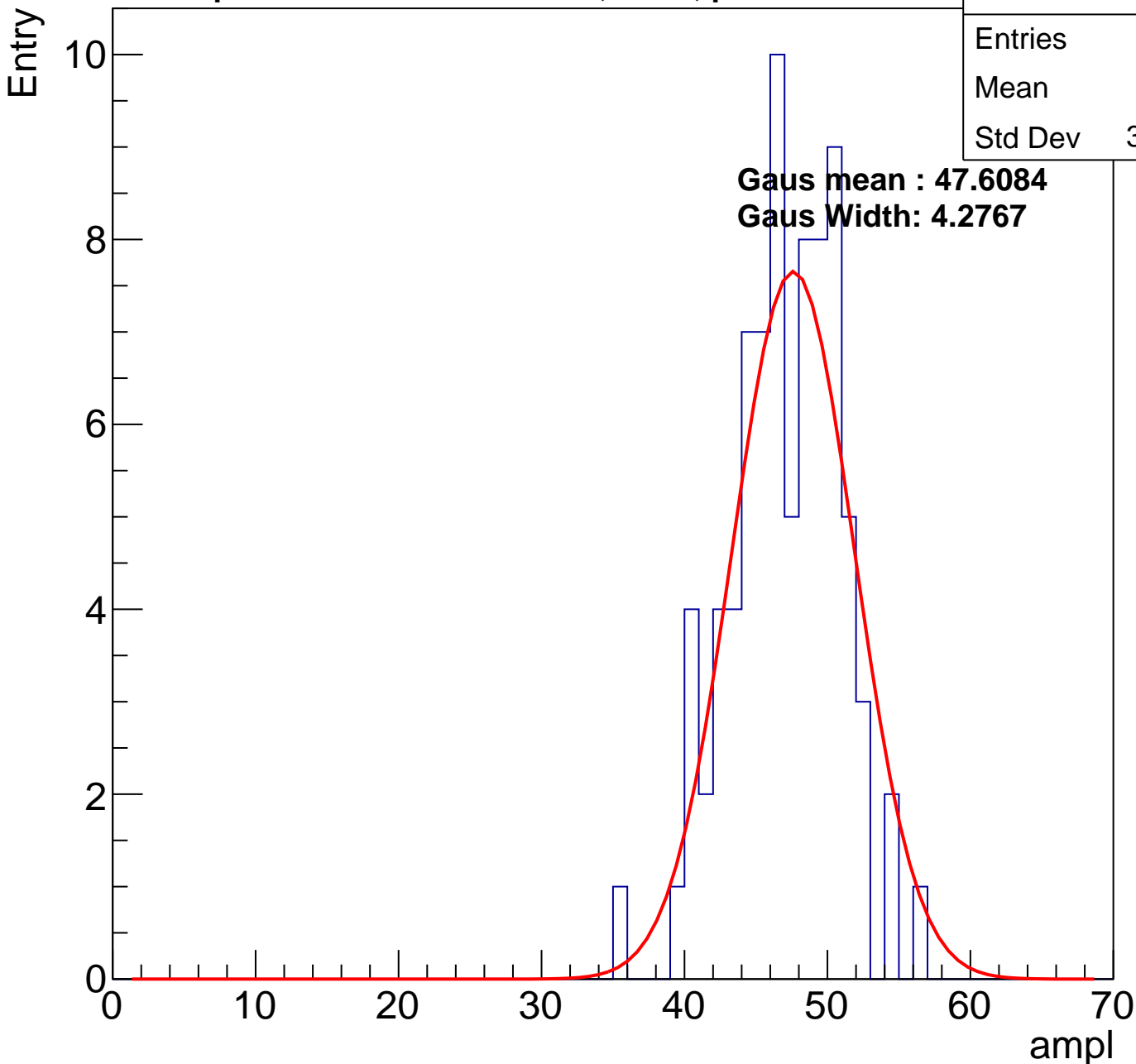


B0L002S, U12-ch87, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	46.6
Std Dev	3.822

Gaus mean : 47.6084
Gaus Width: 4.2767

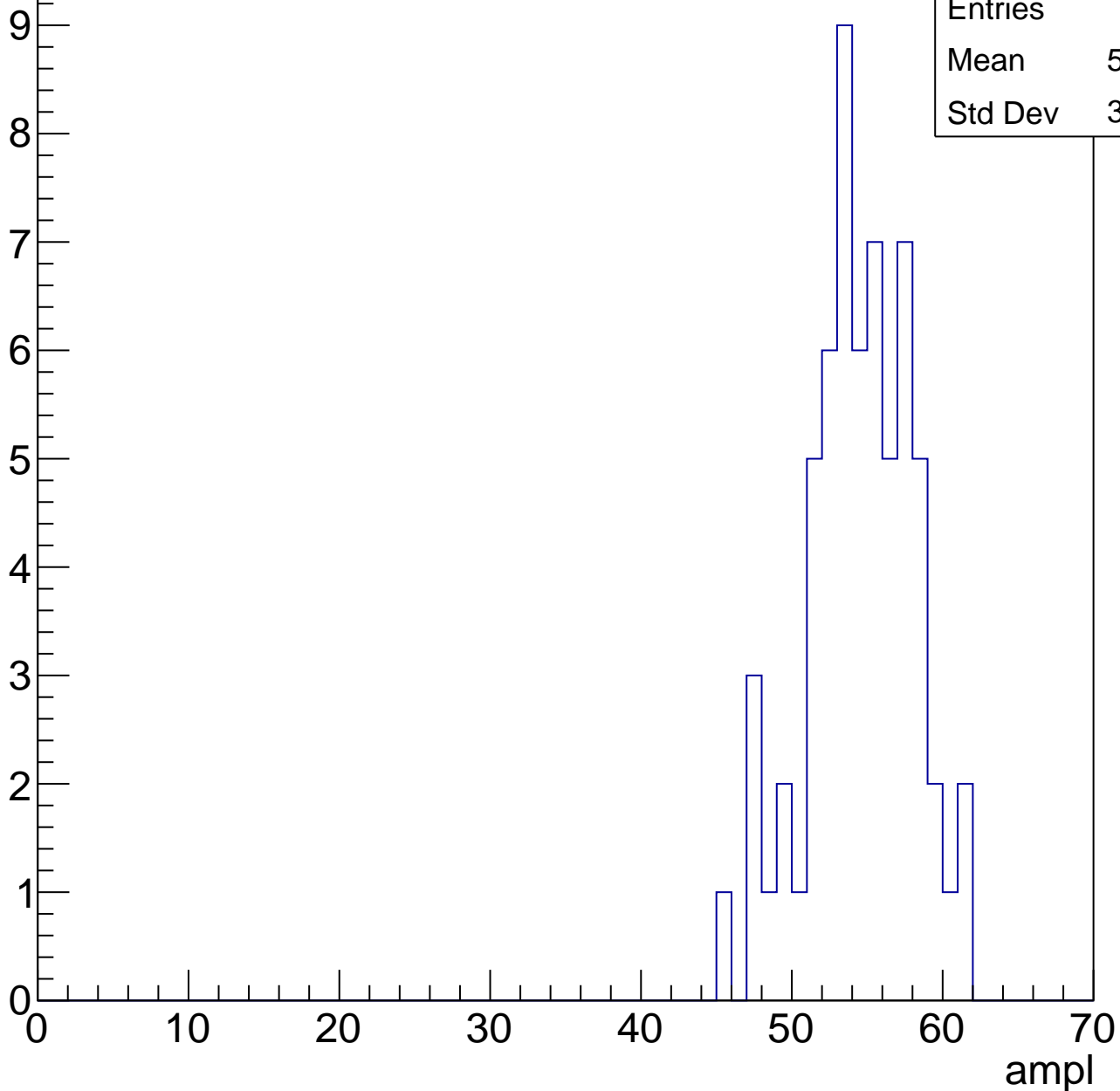


B0L002S, U12-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	54.03
Std Dev	3.478

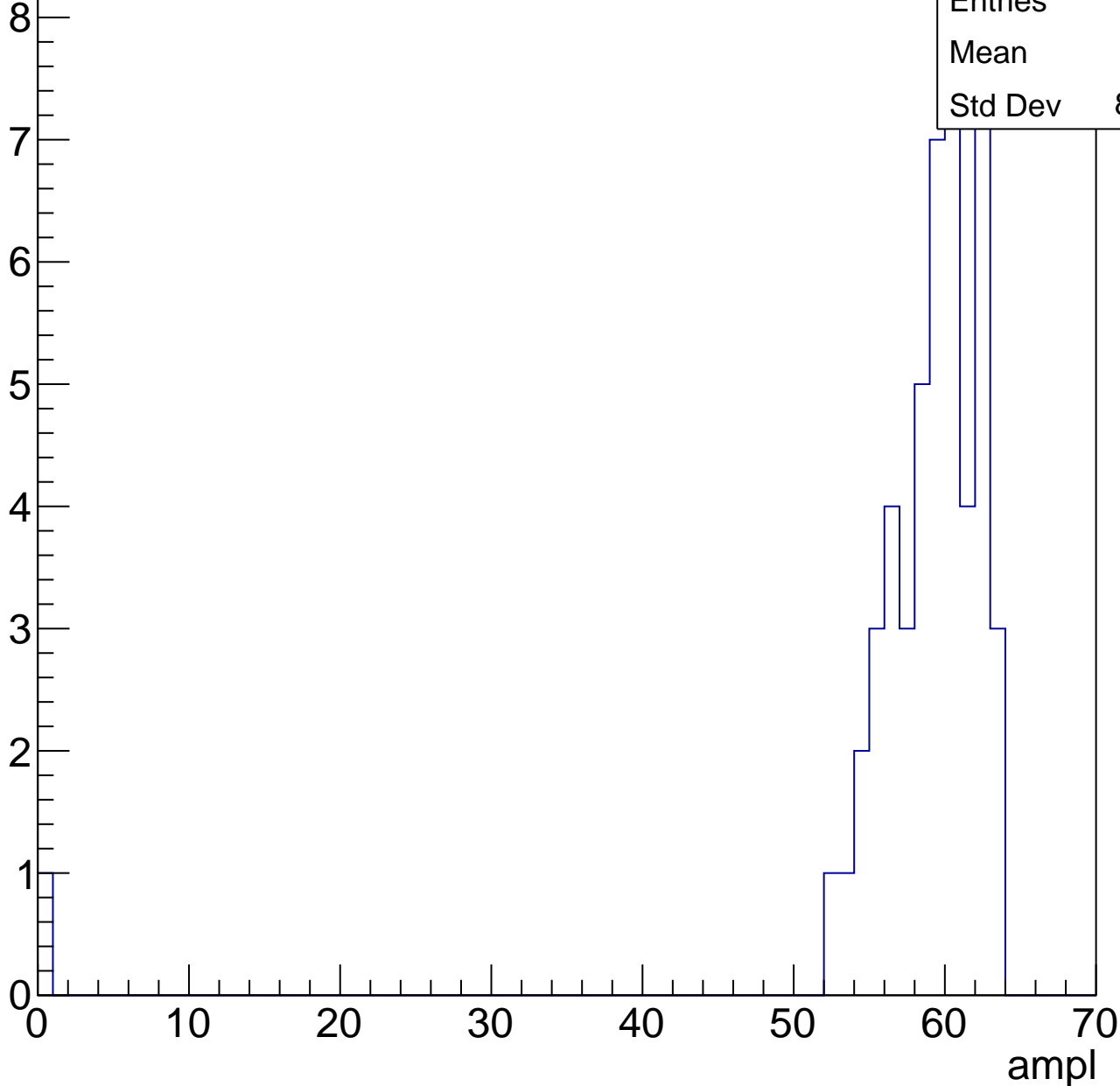


B0L002S, U12-ch87, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

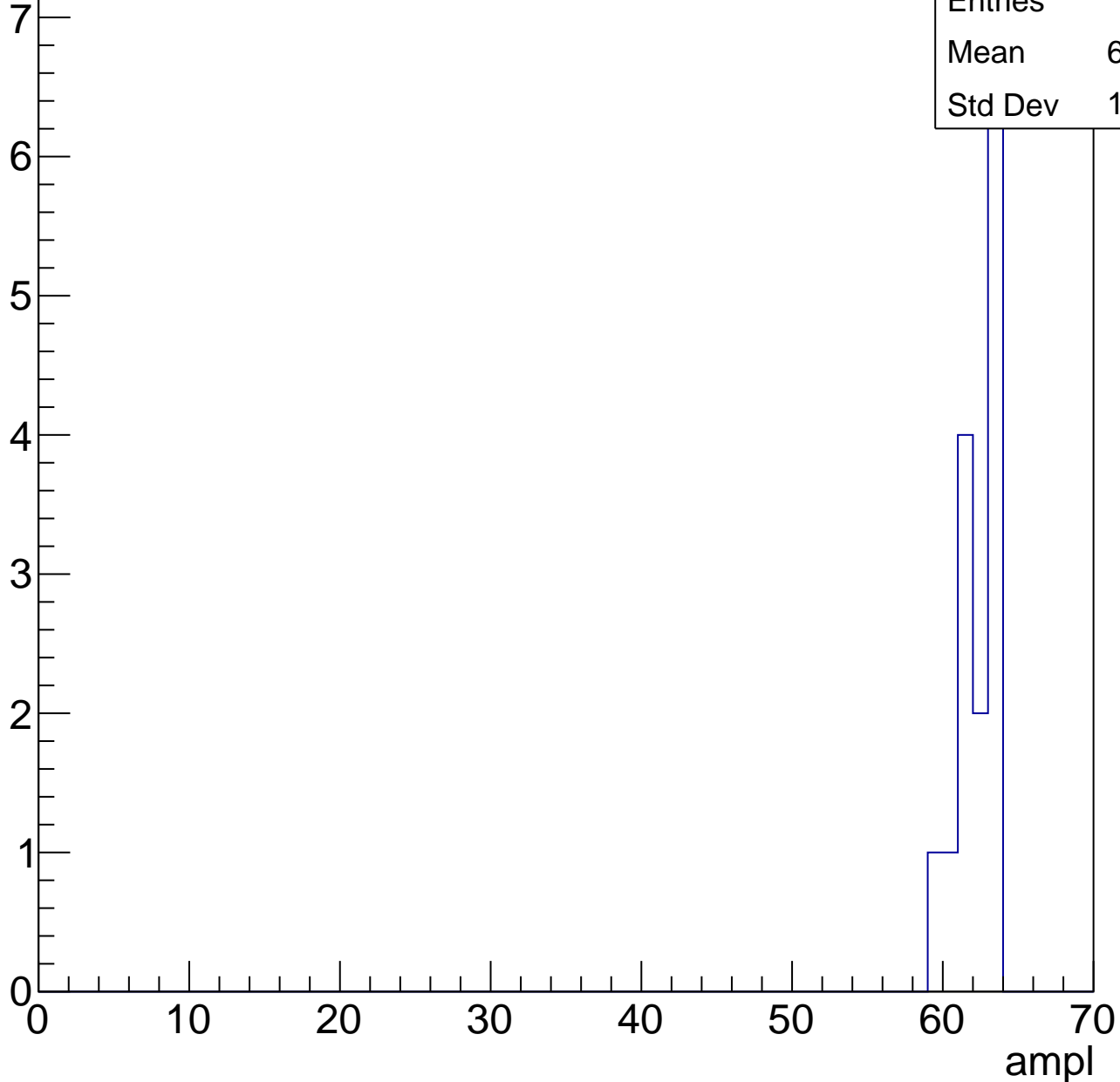
Entries	50
Mean	57.7
Std Dev	8.691



B0L002S, U12-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch88, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	86
Mean	30.33
Std Dev	4.787

Gaus mean : 31.1568

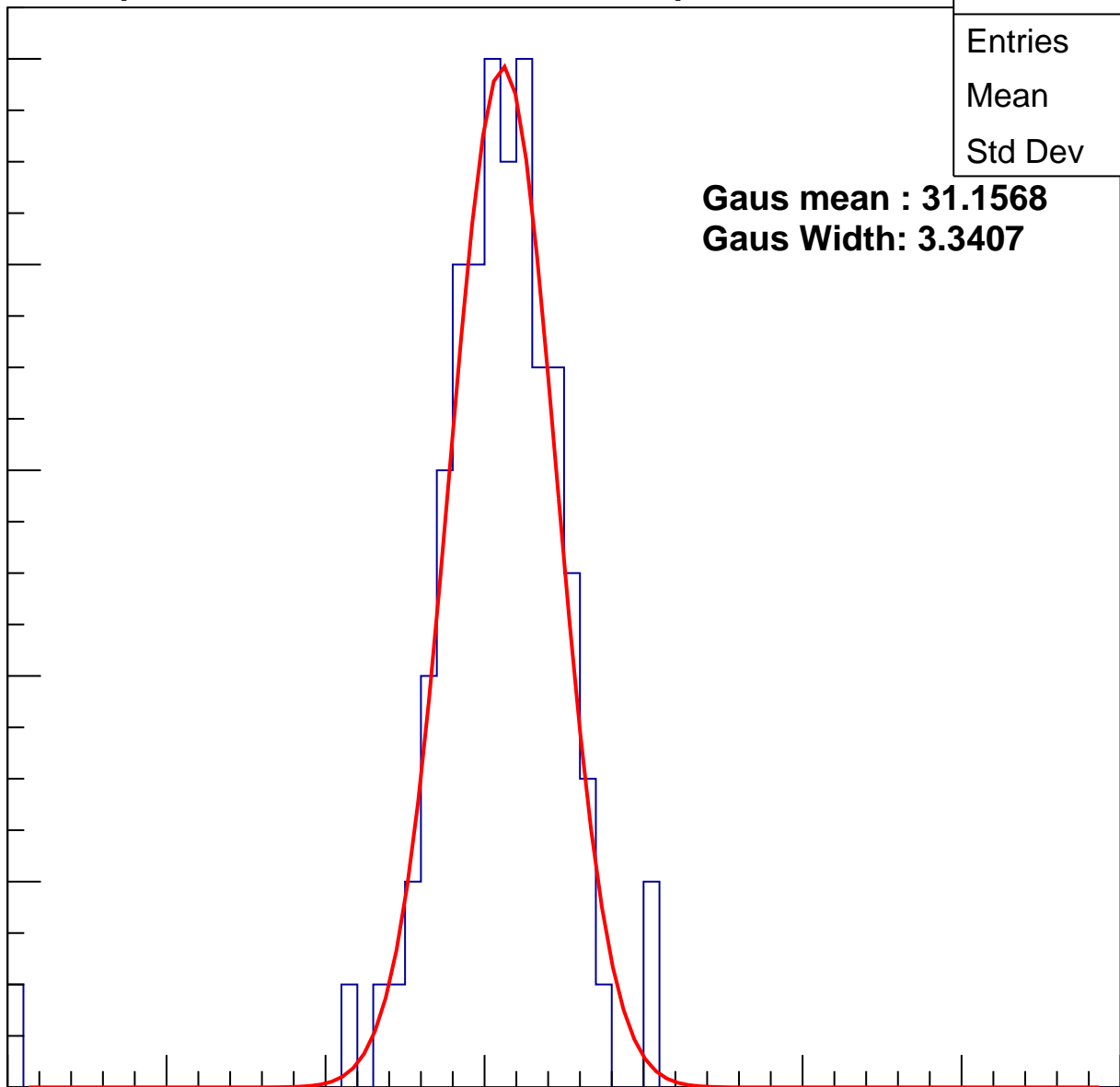
Gaus Width: 3.3407

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch88, adc1

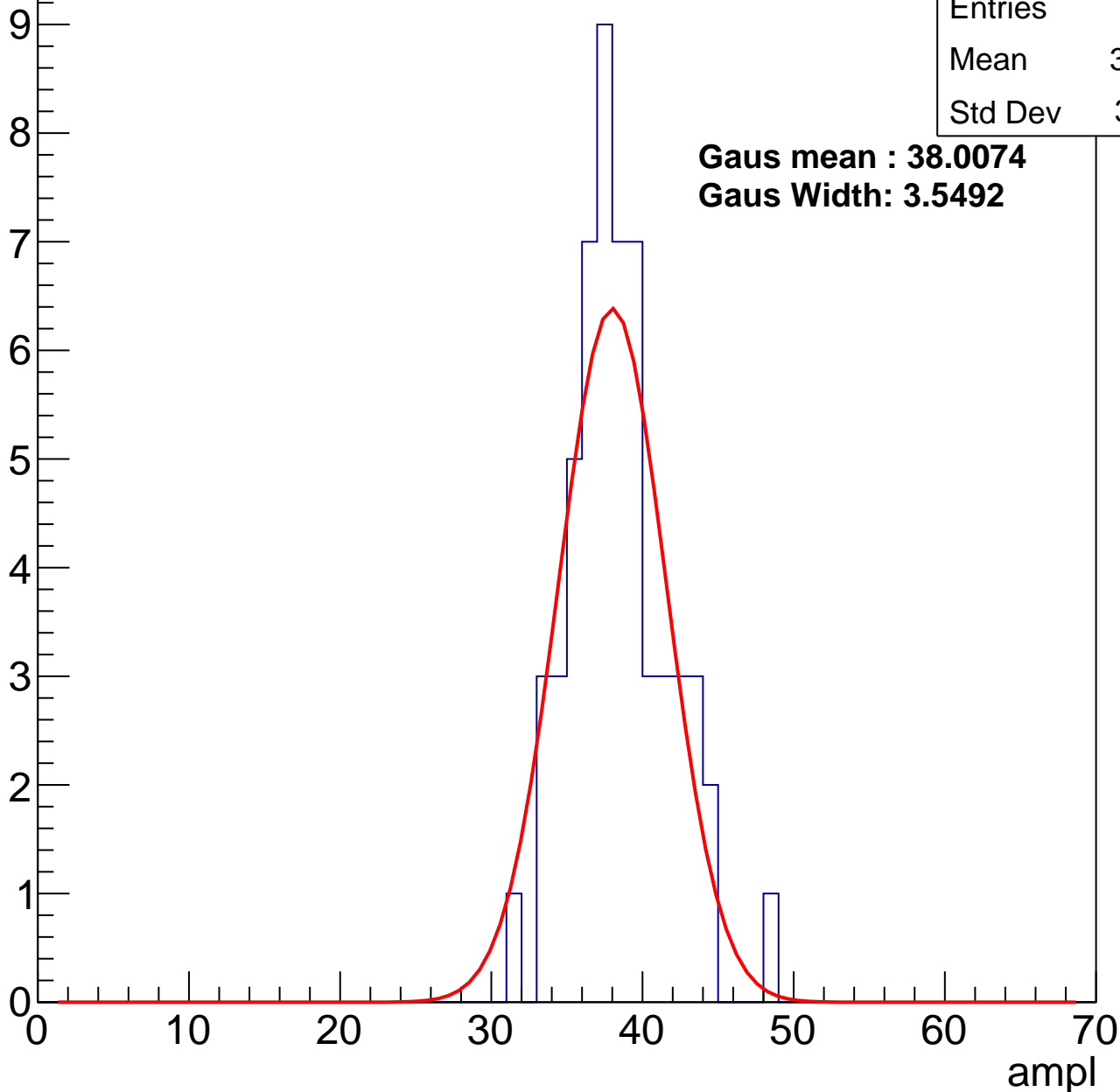
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	37.98
Std Dev	3.231

Gaus mean : 38.0074

Gaus Width: 3.5492

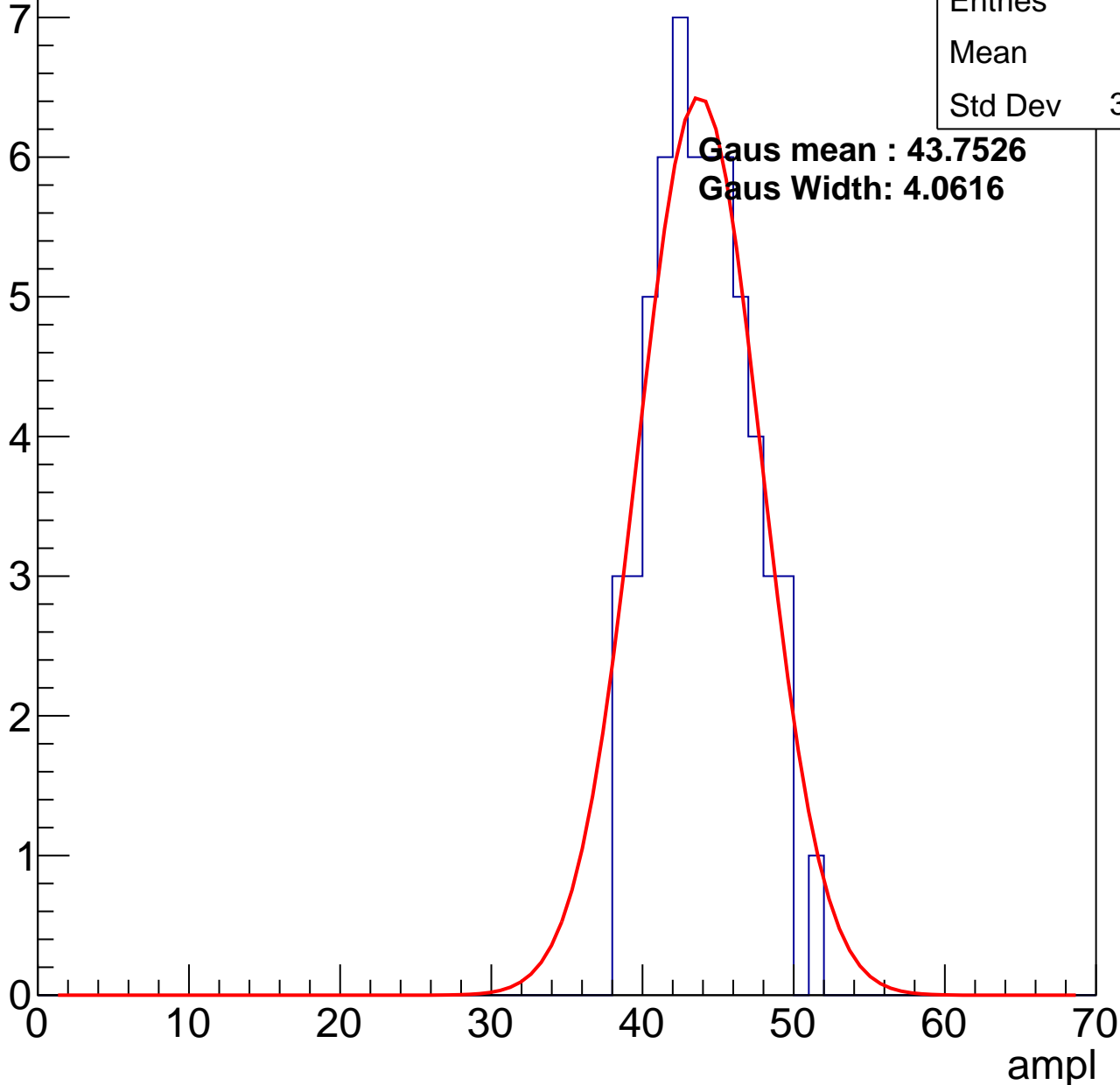


B0L002S, U12-ch88, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.5
Std Dev	3.136



B0L002S, U12-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1

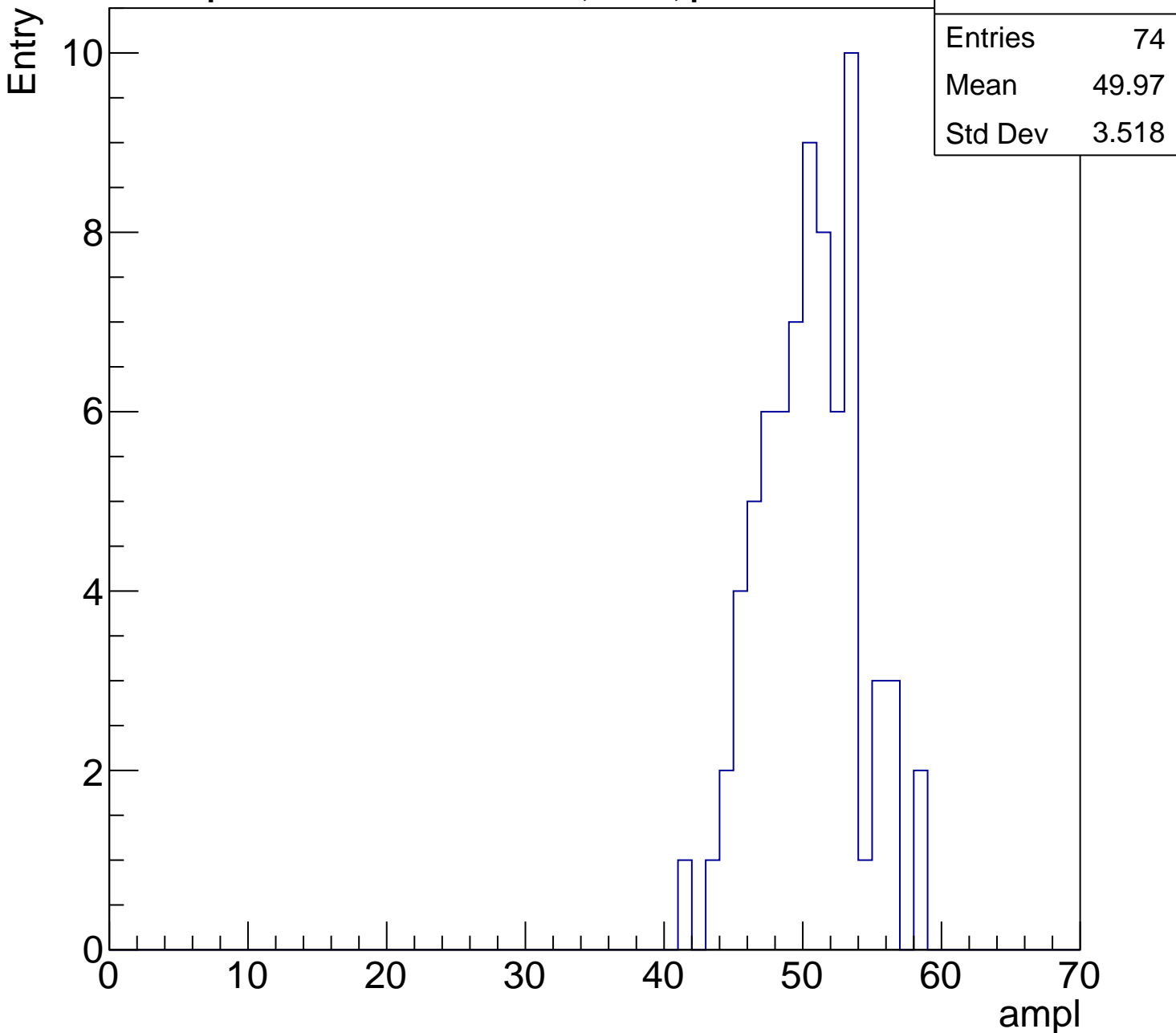
Entries	74
Mean	49.97
Std Dev	3.518

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

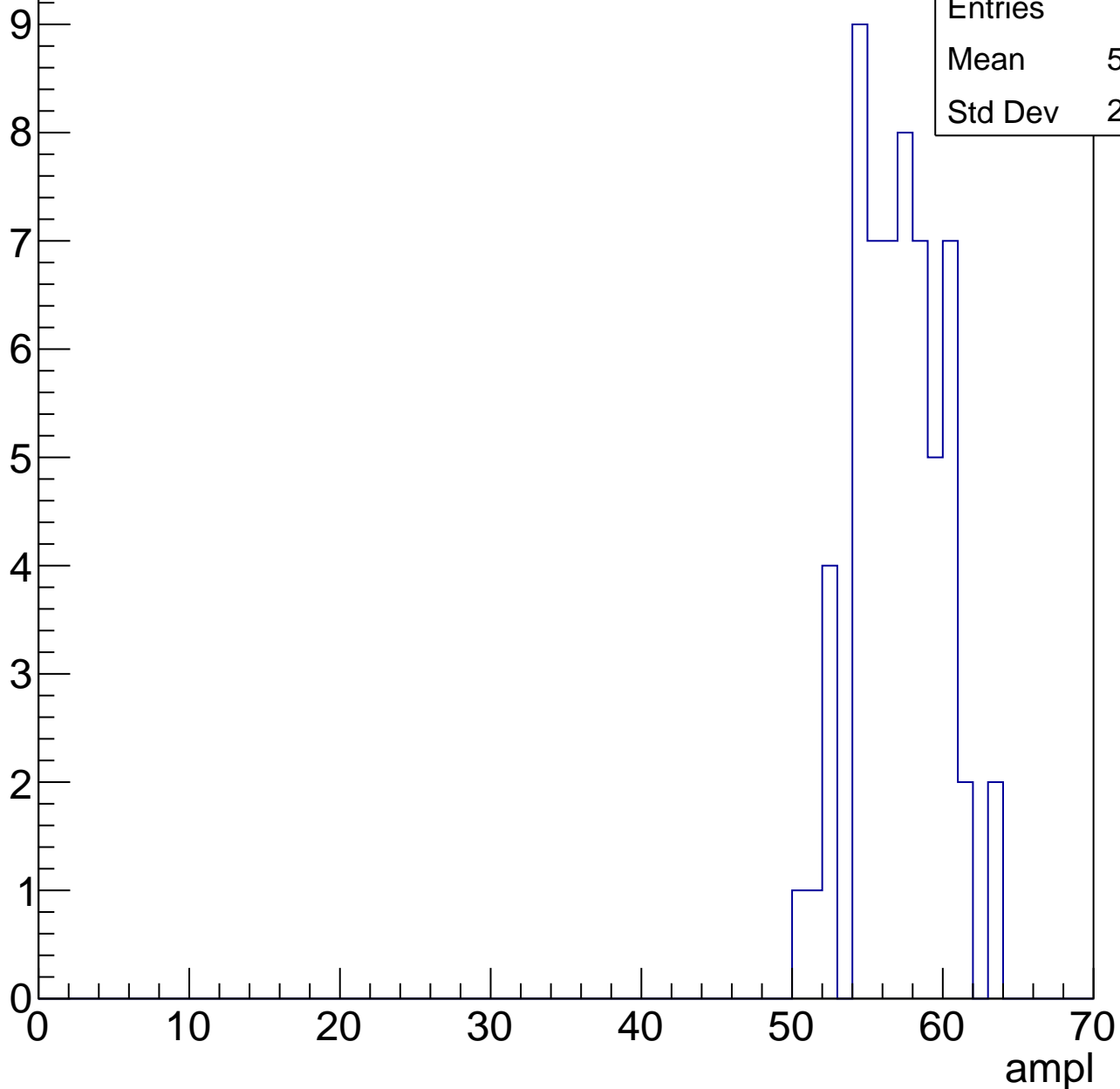


B0L002S, U12-ch88, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.62
Std Dev	2.847

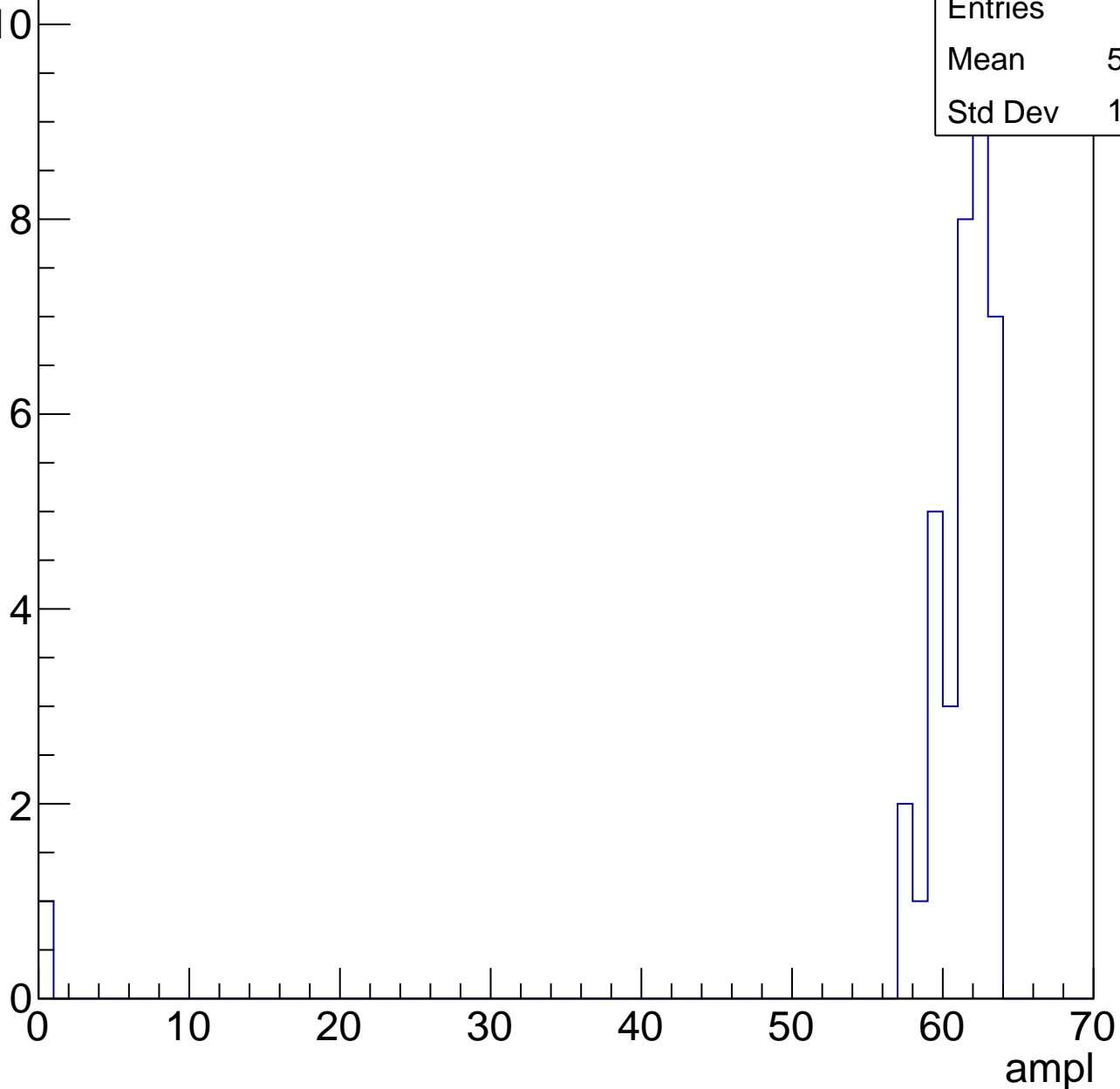


B0L002S, U12-ch88, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.35
Std Dev	10.03



B0L002S, U12-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U12-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

B0L002S, U12-ch89, adc0

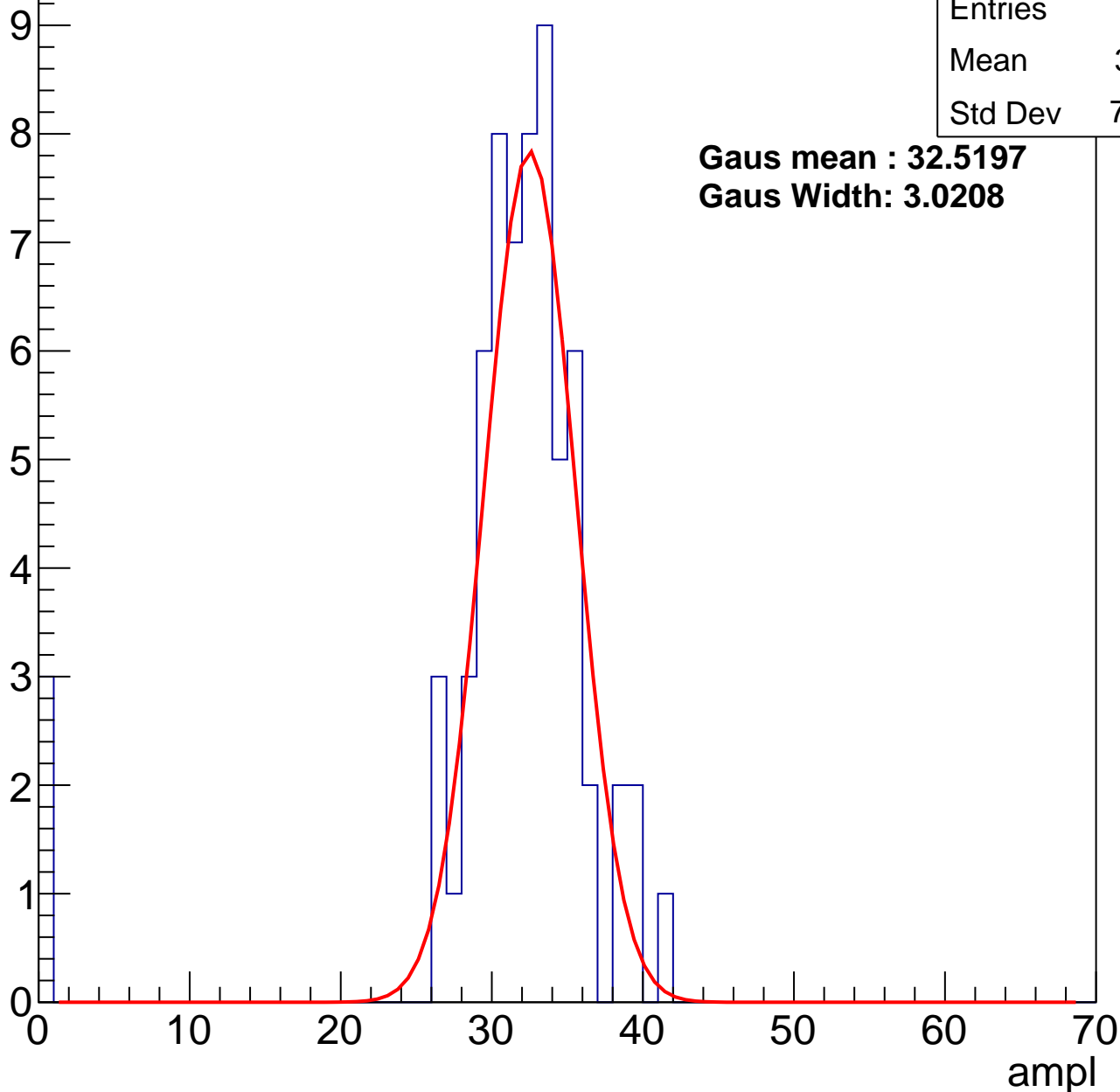
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.61
Std Dev	7.369

Gaus mean : 32.5197

Gaus Width: 3.0208



B0L002S, U12-ch89, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	38.22
Std Dev	2.997

Gaus mean : 38.6307

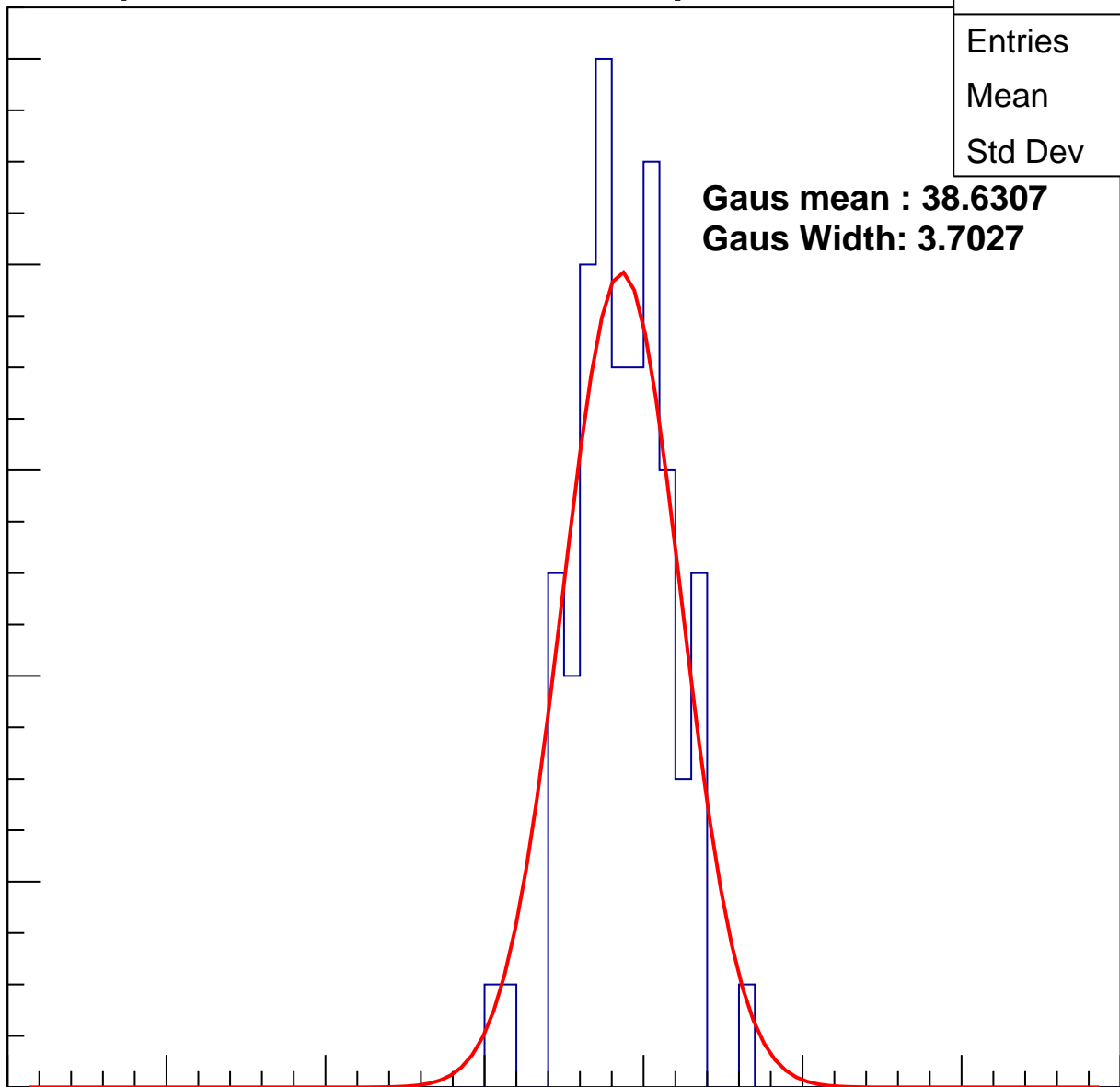
Gaus Width: 3.7027

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch89, adc2

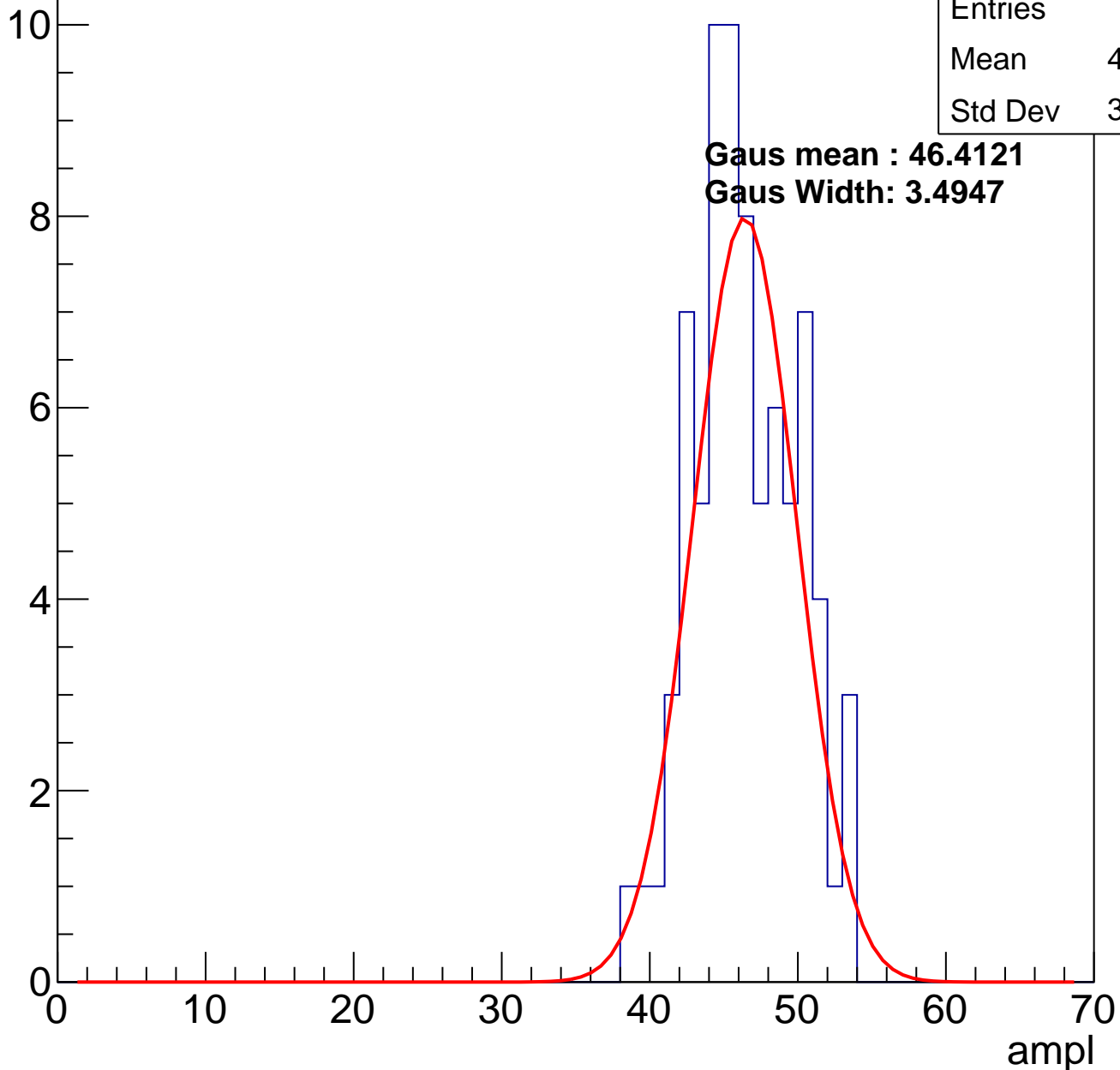
calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	45.97
Std Dev	3.419

Gaus mean : 46.4121

Gaus Width: 3.4947

Entry

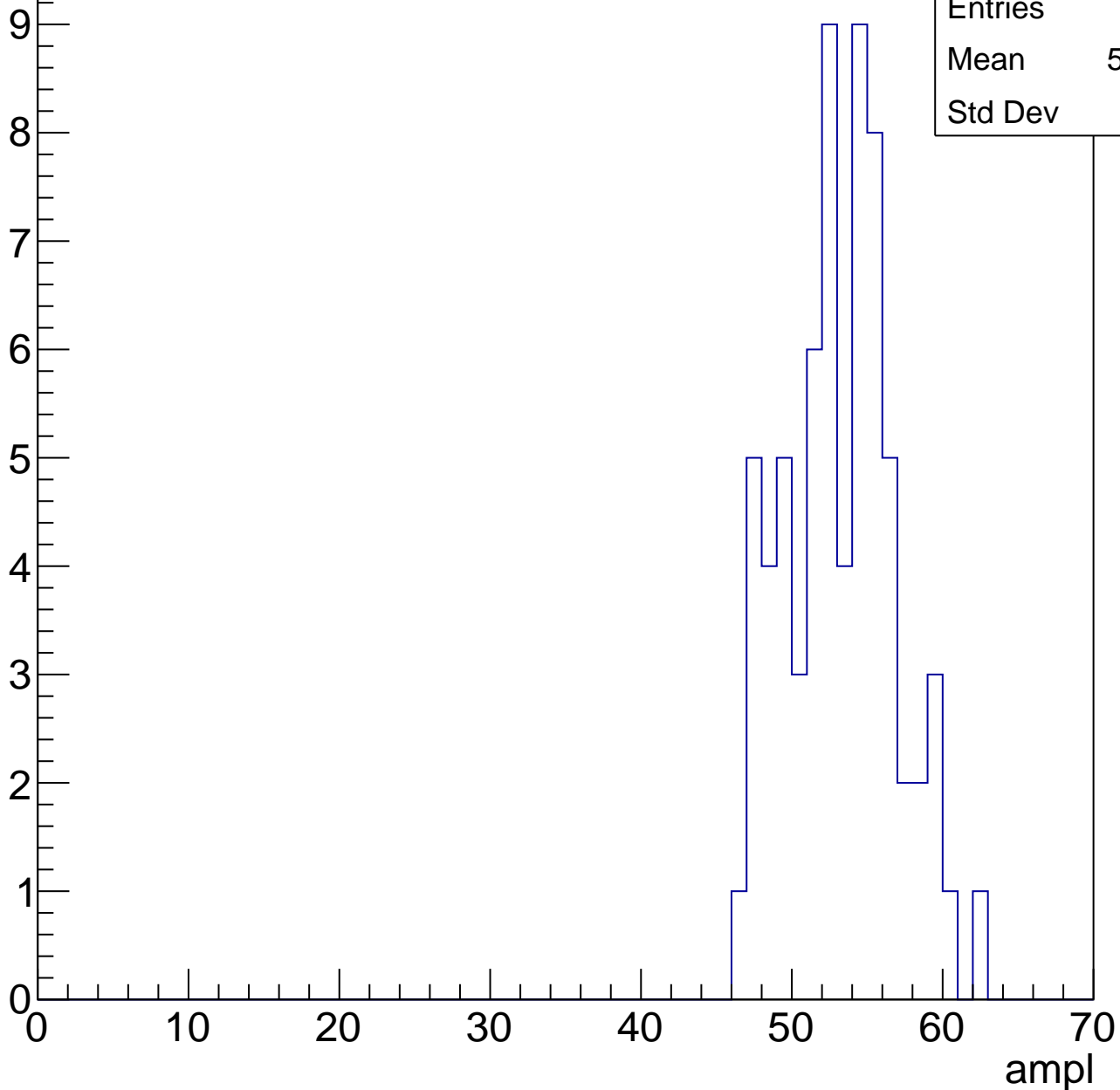


B0L002S, U12-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

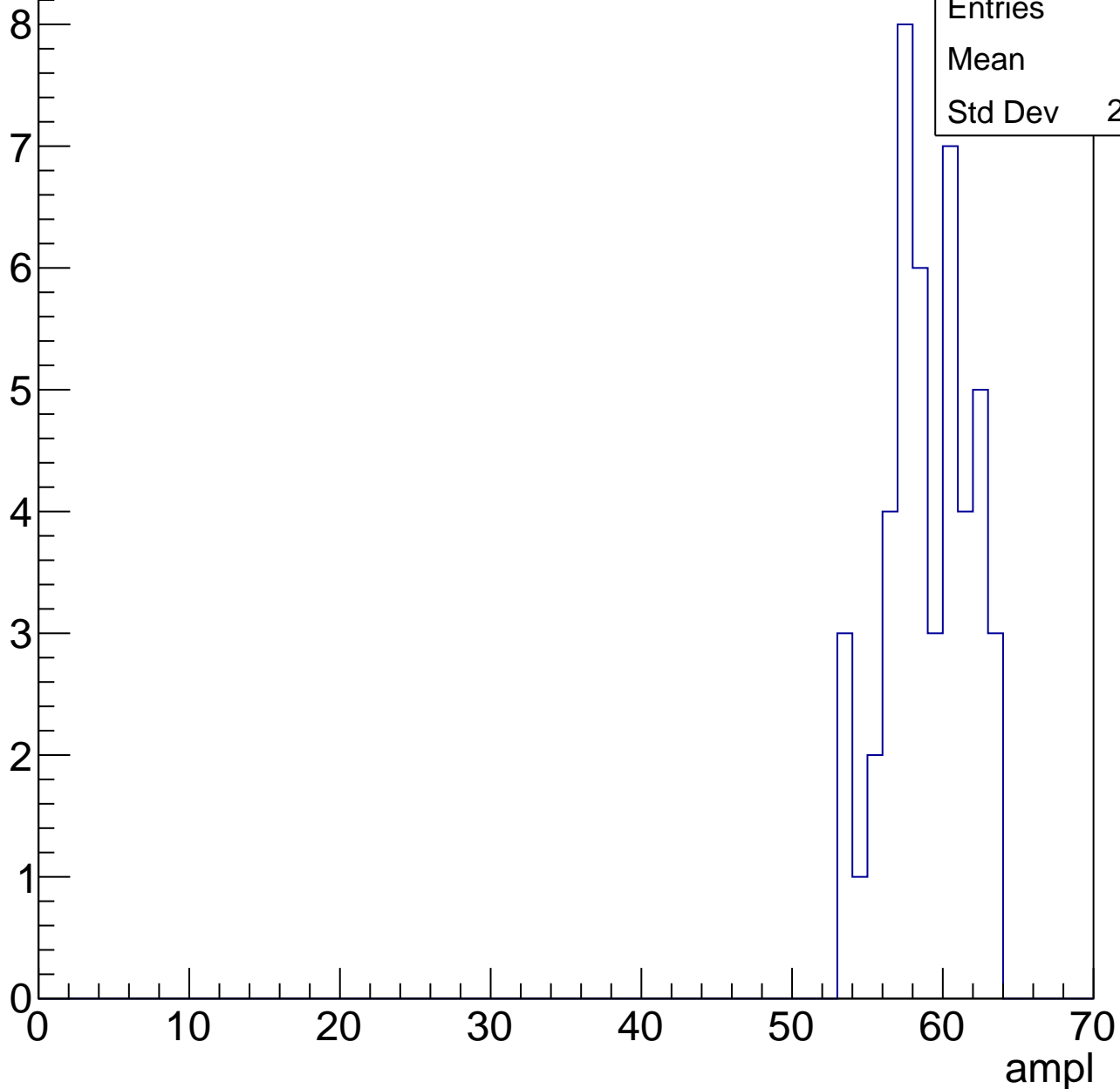
Entries	68
Mean	52.78
Std Dev	3.58



B0L002S, U12-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

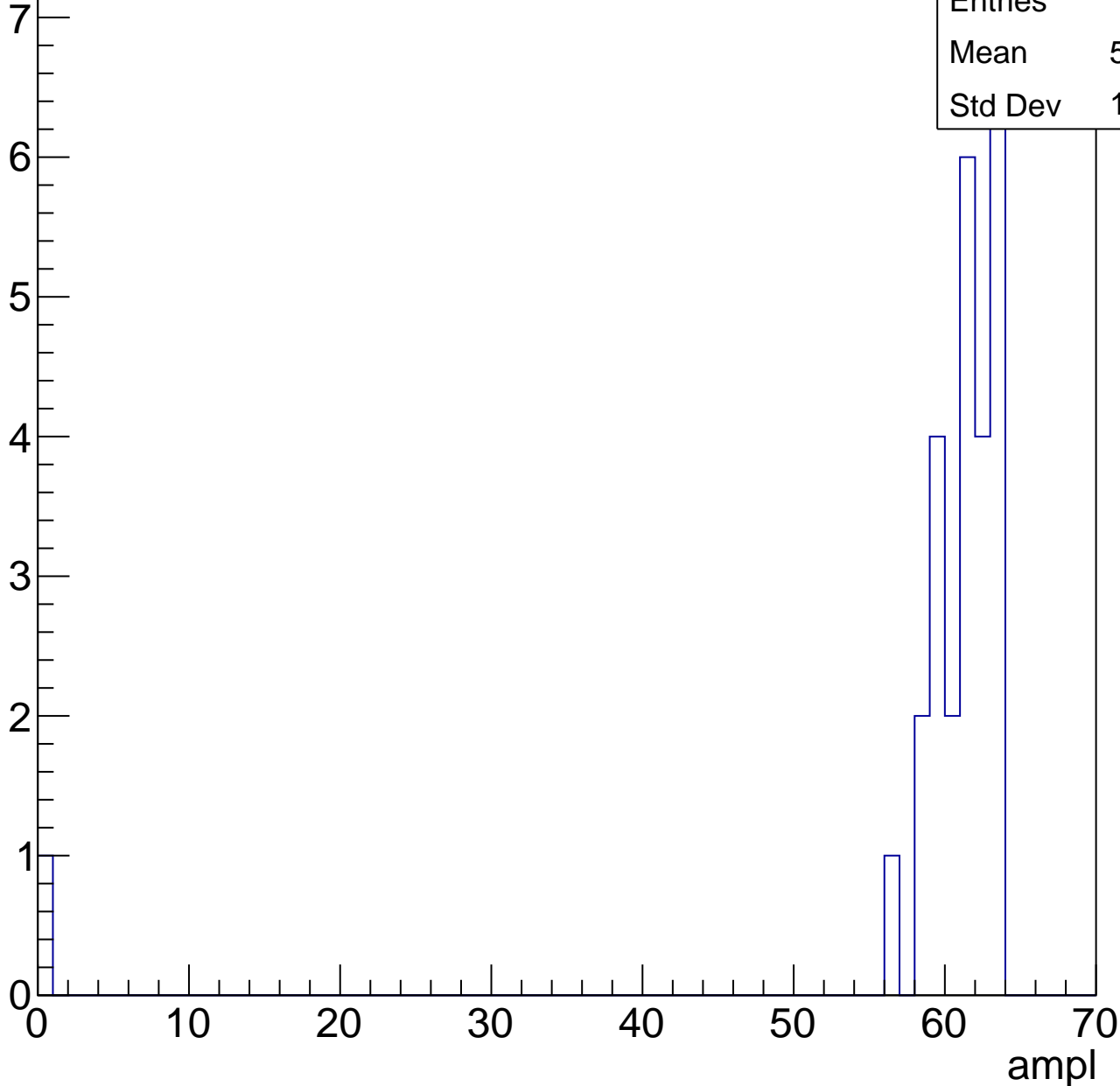


B0L002S, U12-ch89, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.63
Std Dev	11.65



B0L002S, U12-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch90, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	29.54
Std Dev	4.653

Gaus mean : 30.3678

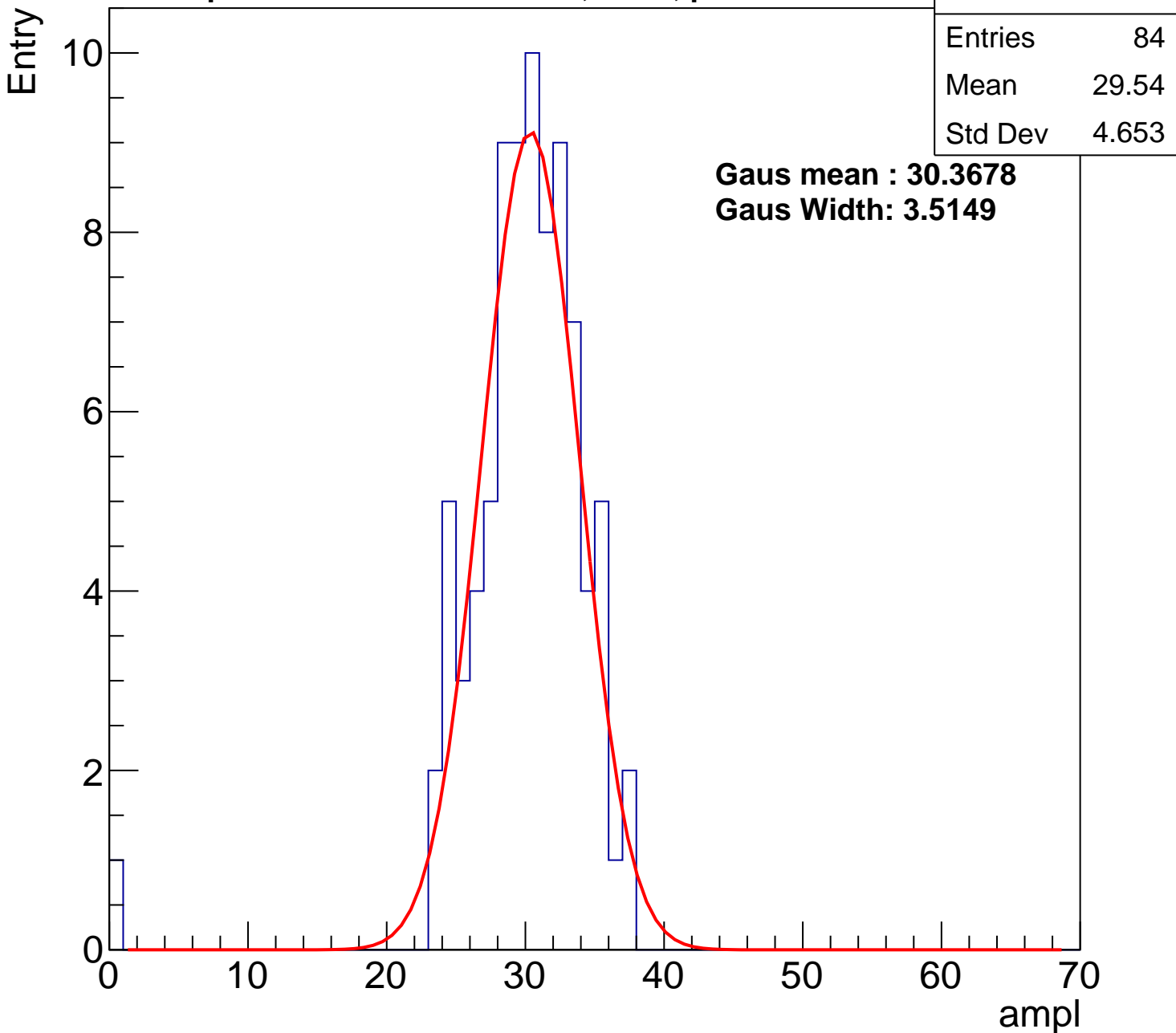
Gaus Width: 3.5149

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch90, adc1

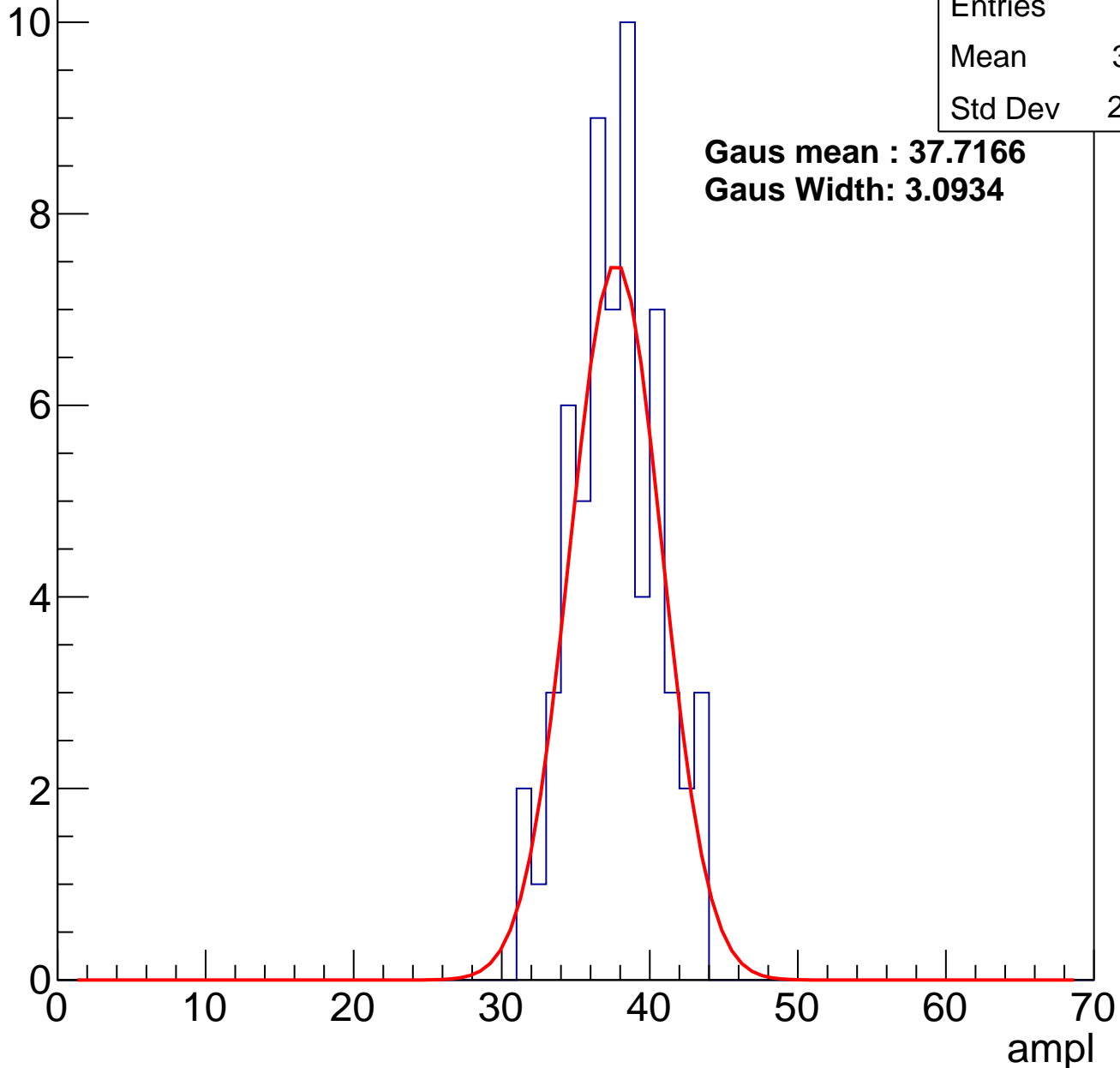
calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	37.21
Std Dev	2.897

Gaus mean : 37.7166

Gaus Width: 3.0934

Entry



B0L002S, U12-ch90, adc2

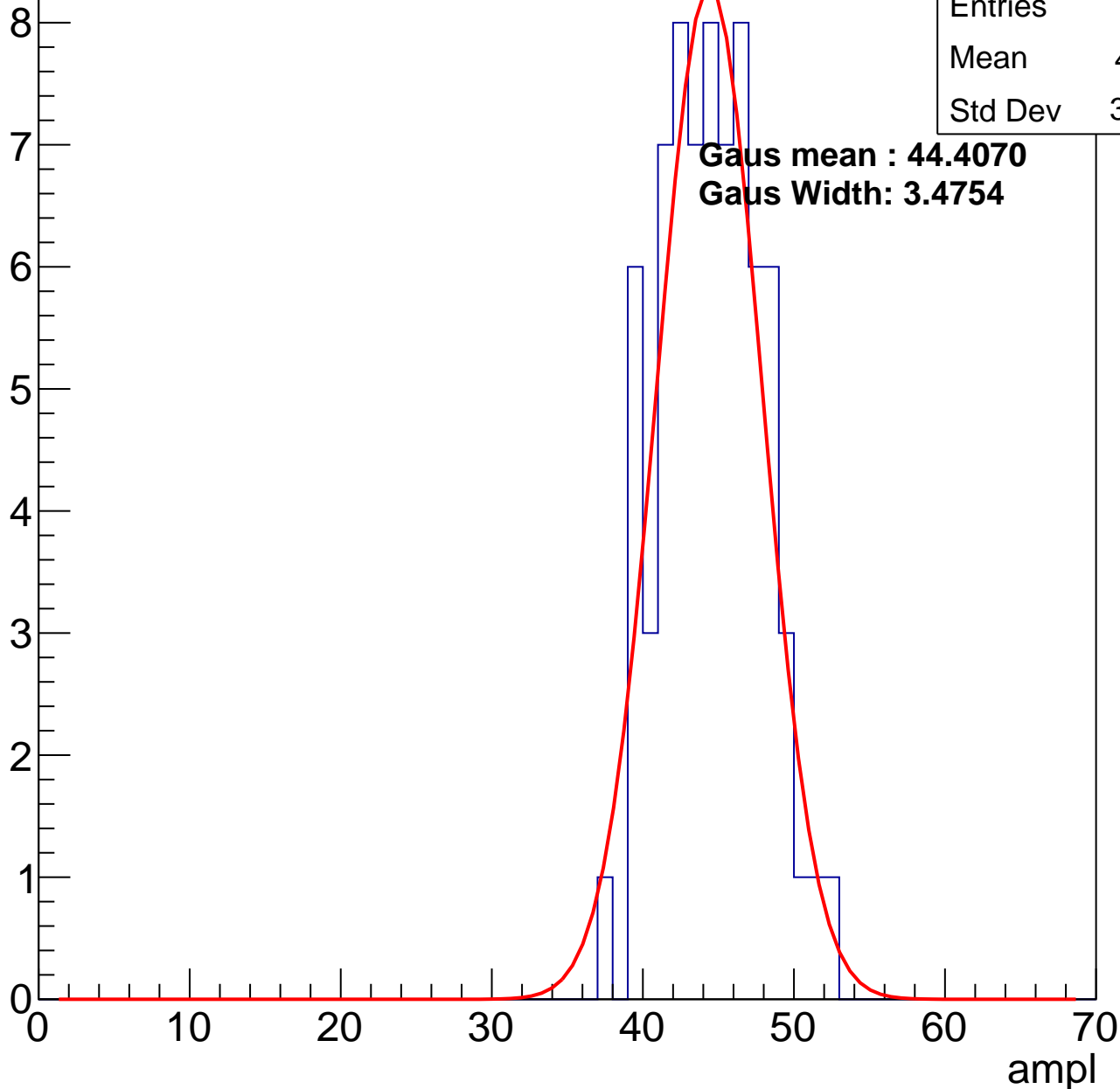
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.11
Std Dev	3.229

Gaus mean : 44.4070

Gaus Width: 3.4754

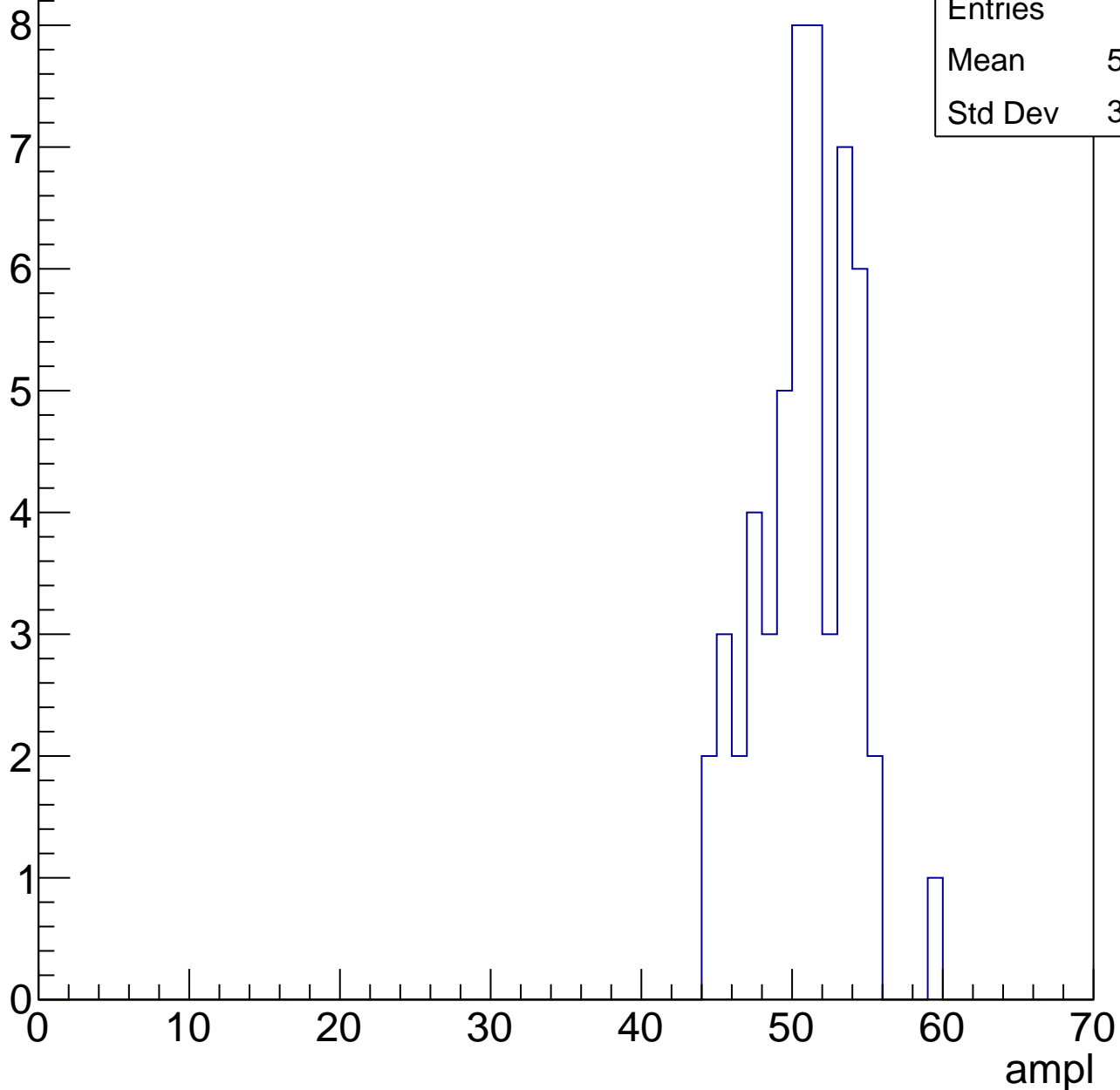


B0L002S, U12-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	50.37
Std Dev	3.146



B0L002S, U12-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1

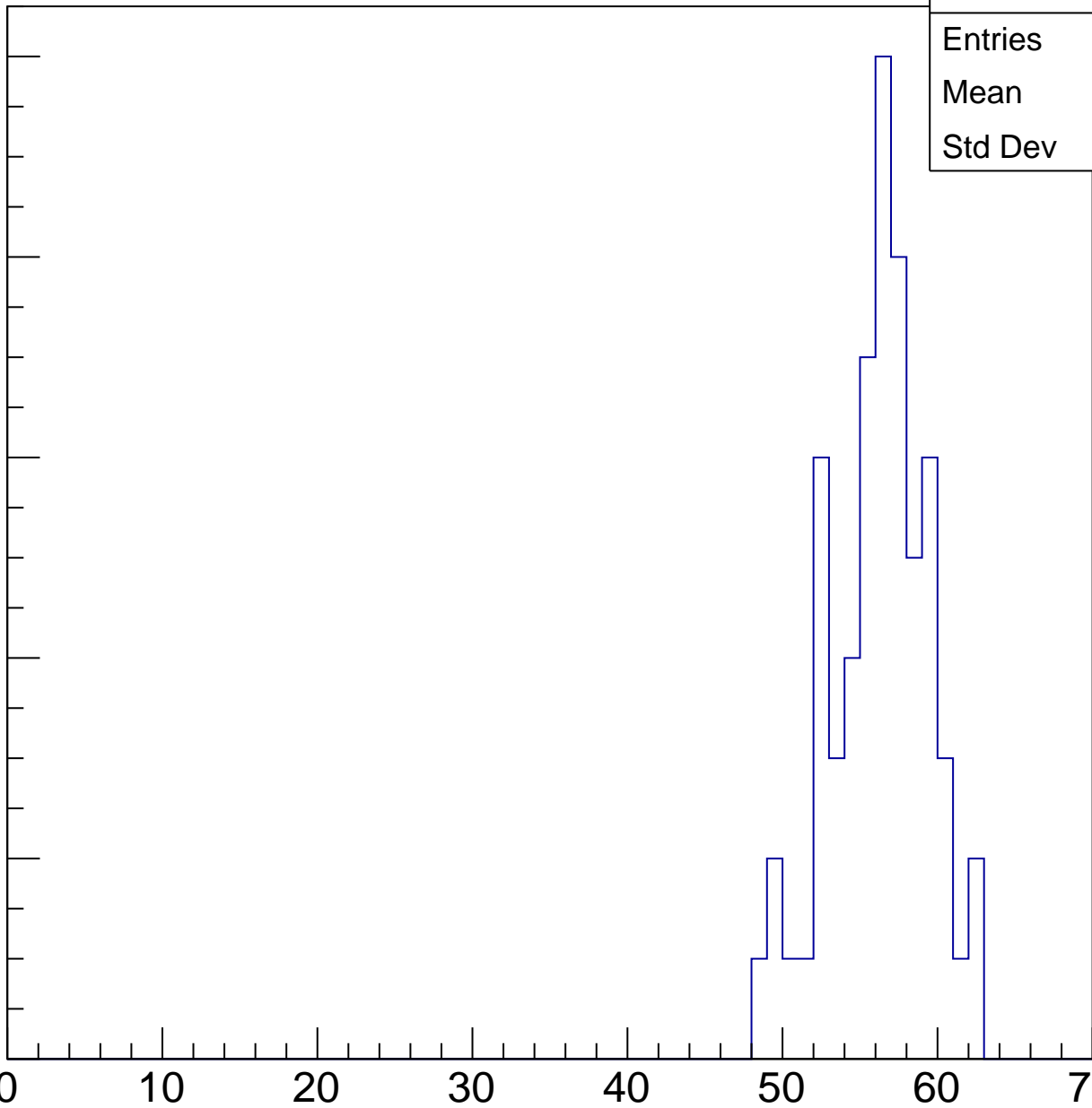
Entries	60
Mean	55.73
Std Dev	3.14

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

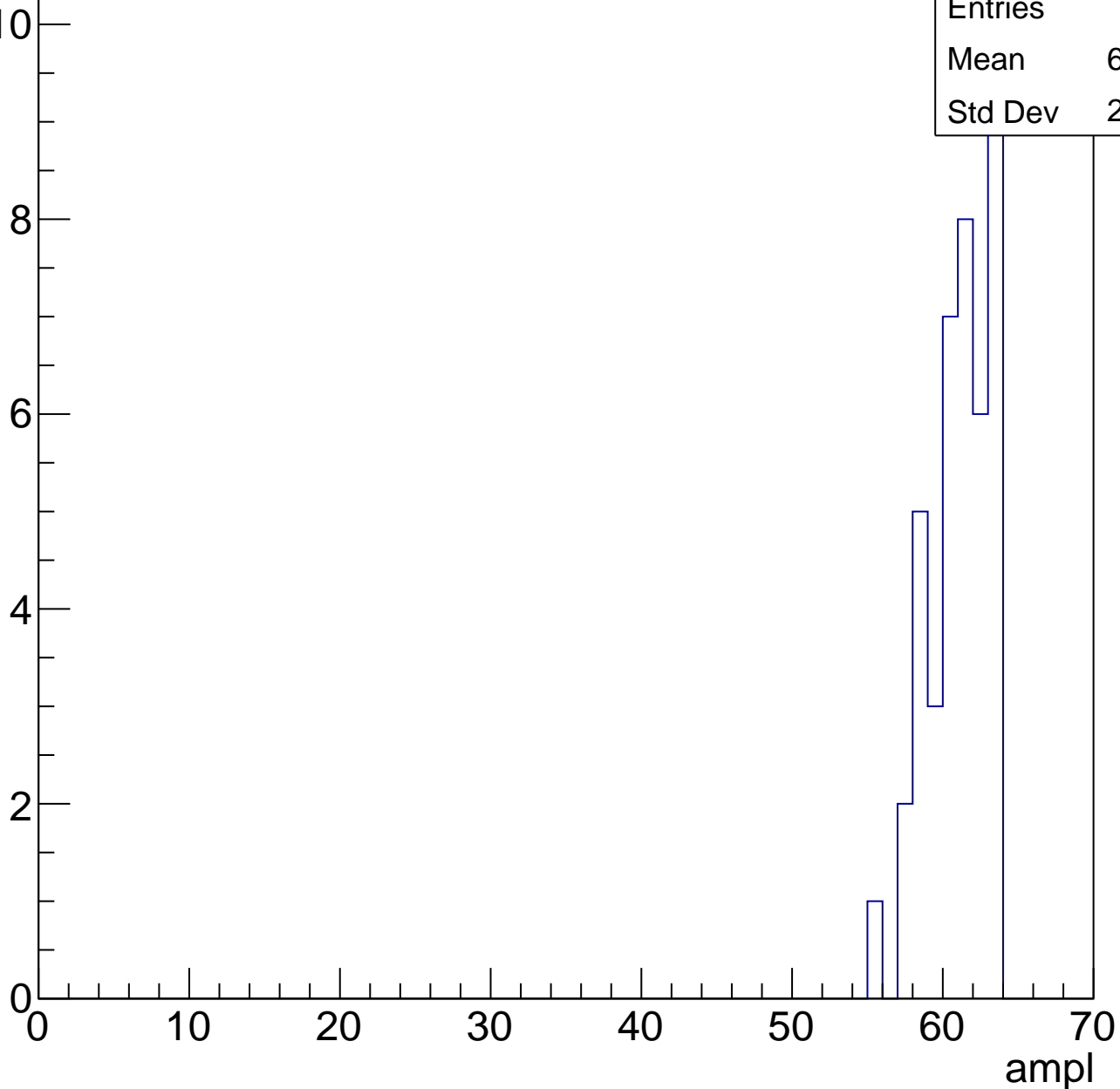


B0L002S, U12-ch90, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

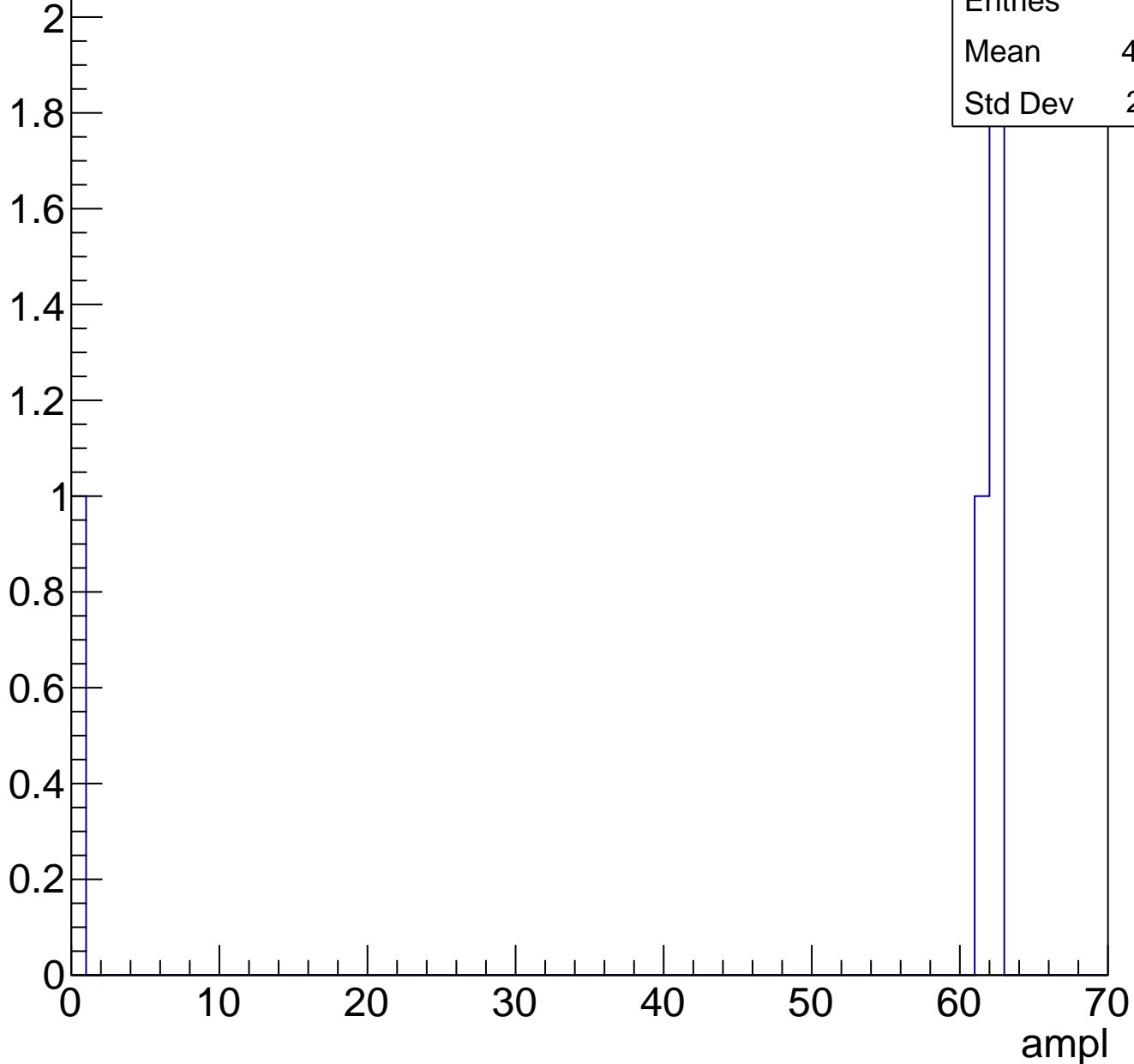
Entries	42
Mean	60.62
Std Dev	2.023



B0L002S, U12-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

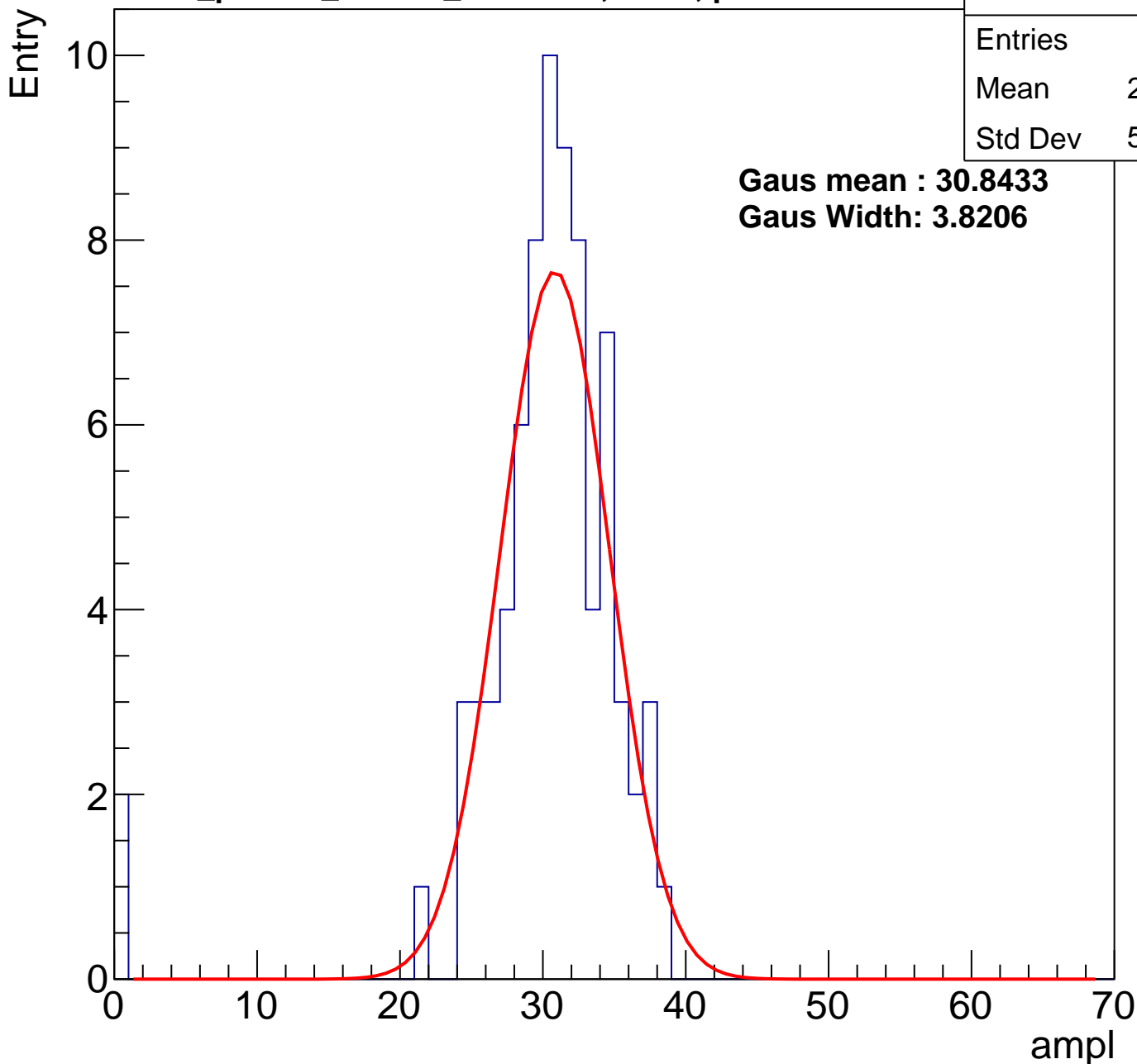
B0L002S, U12-ch91, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	29.68
Std Dev	5.942

Gaus mean : 30.8433

Gaus Width: 3.8206



B0L002S, U12-ch91, adc1

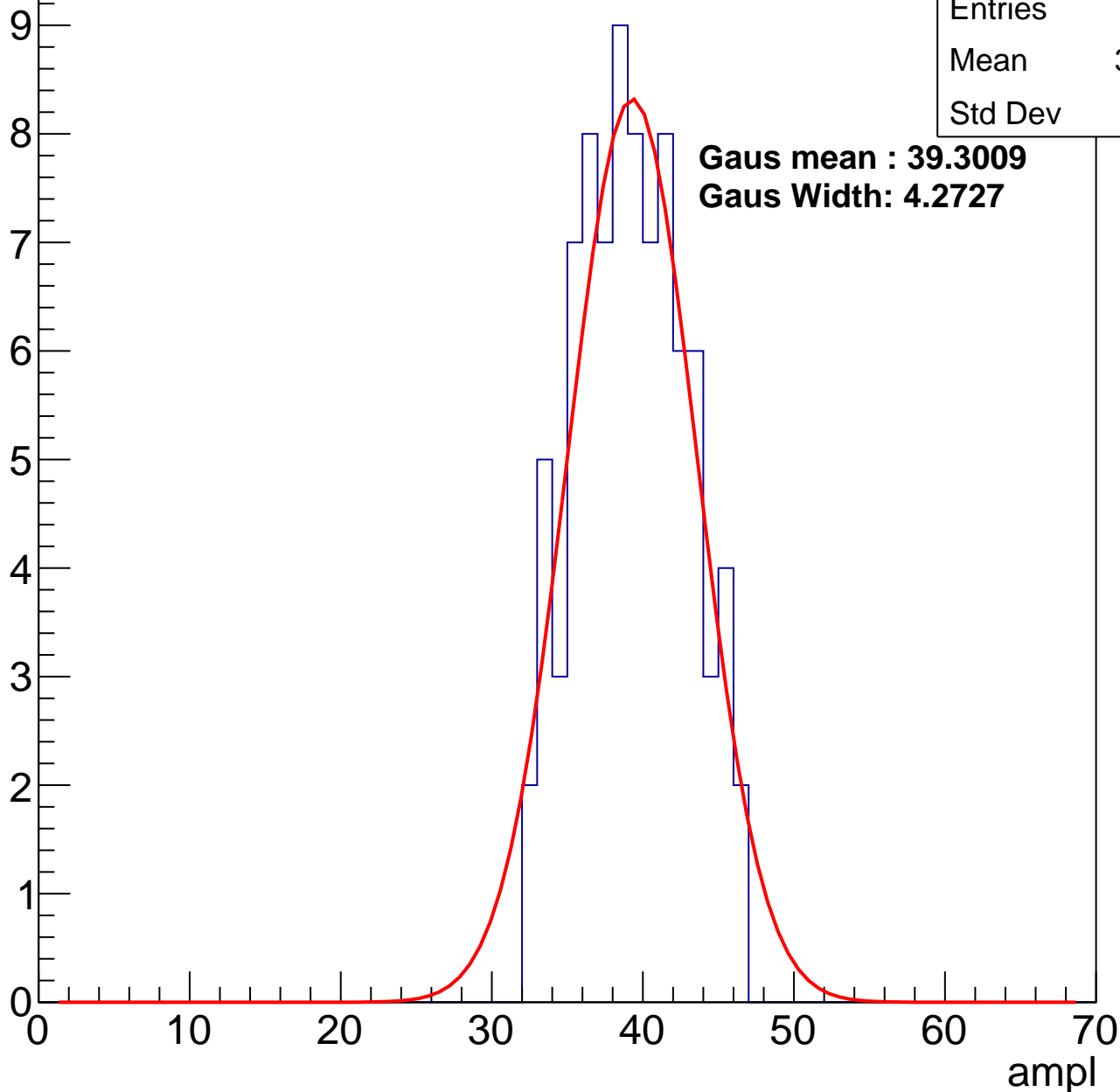
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	38.81
Std Dev	3.56

Gaus mean : 39.3009

Gaus Width: 4.2727



B0L002S, U12-ch91, adc2

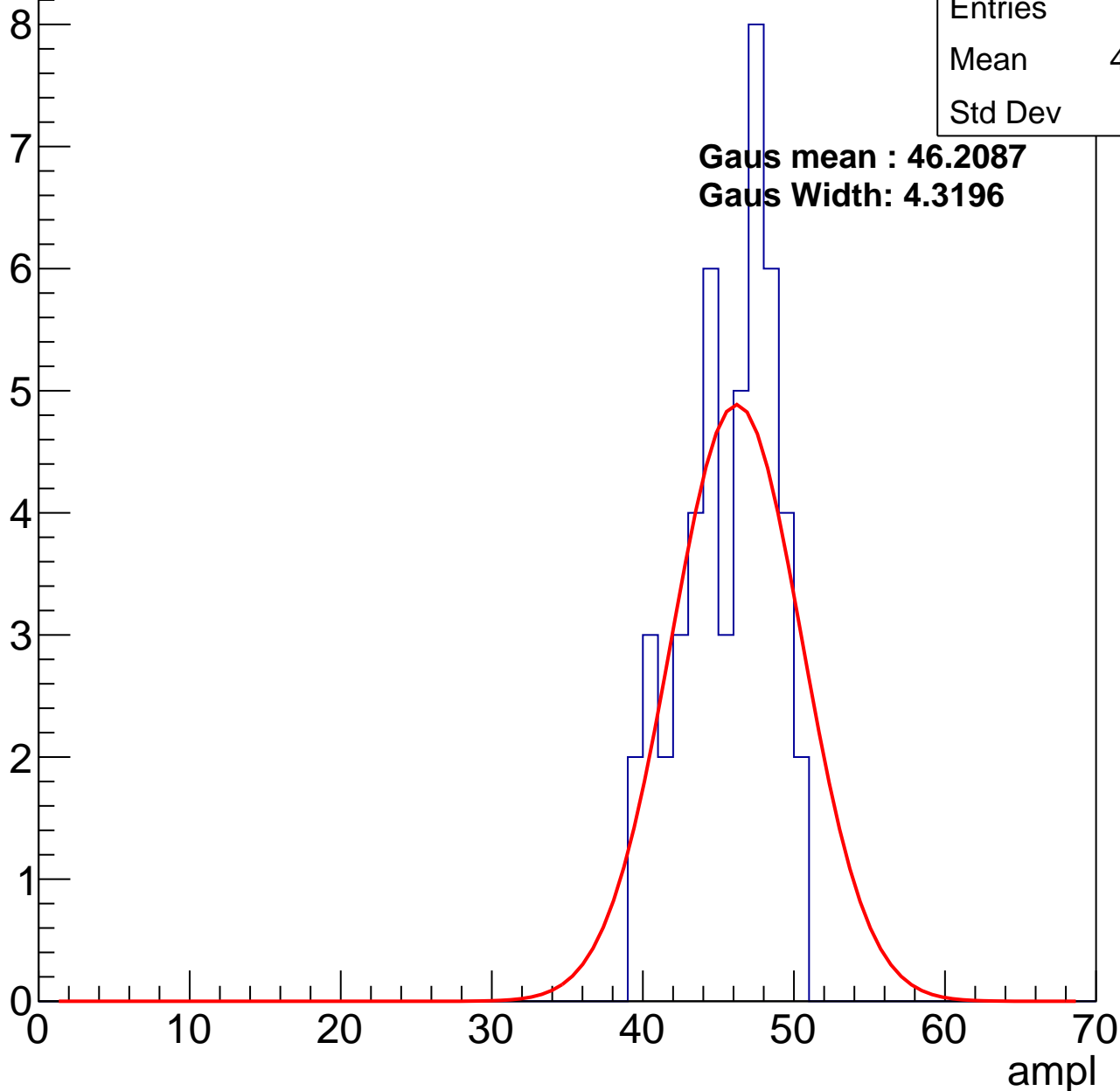
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	45.15
Std Dev	3

Gaus mean : 46.2087

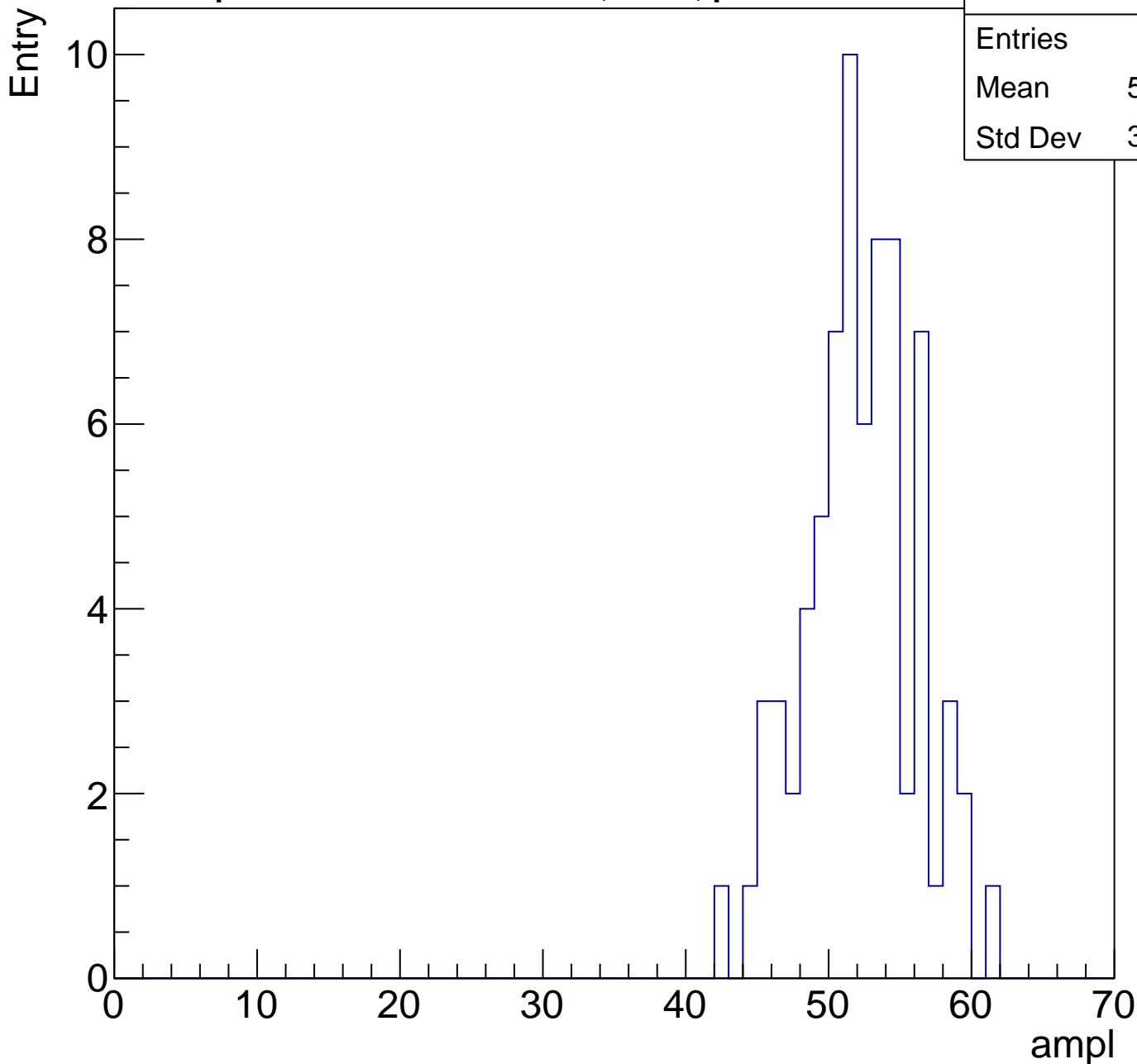
Gaus Width: 4.3196



B0L002S, U12-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

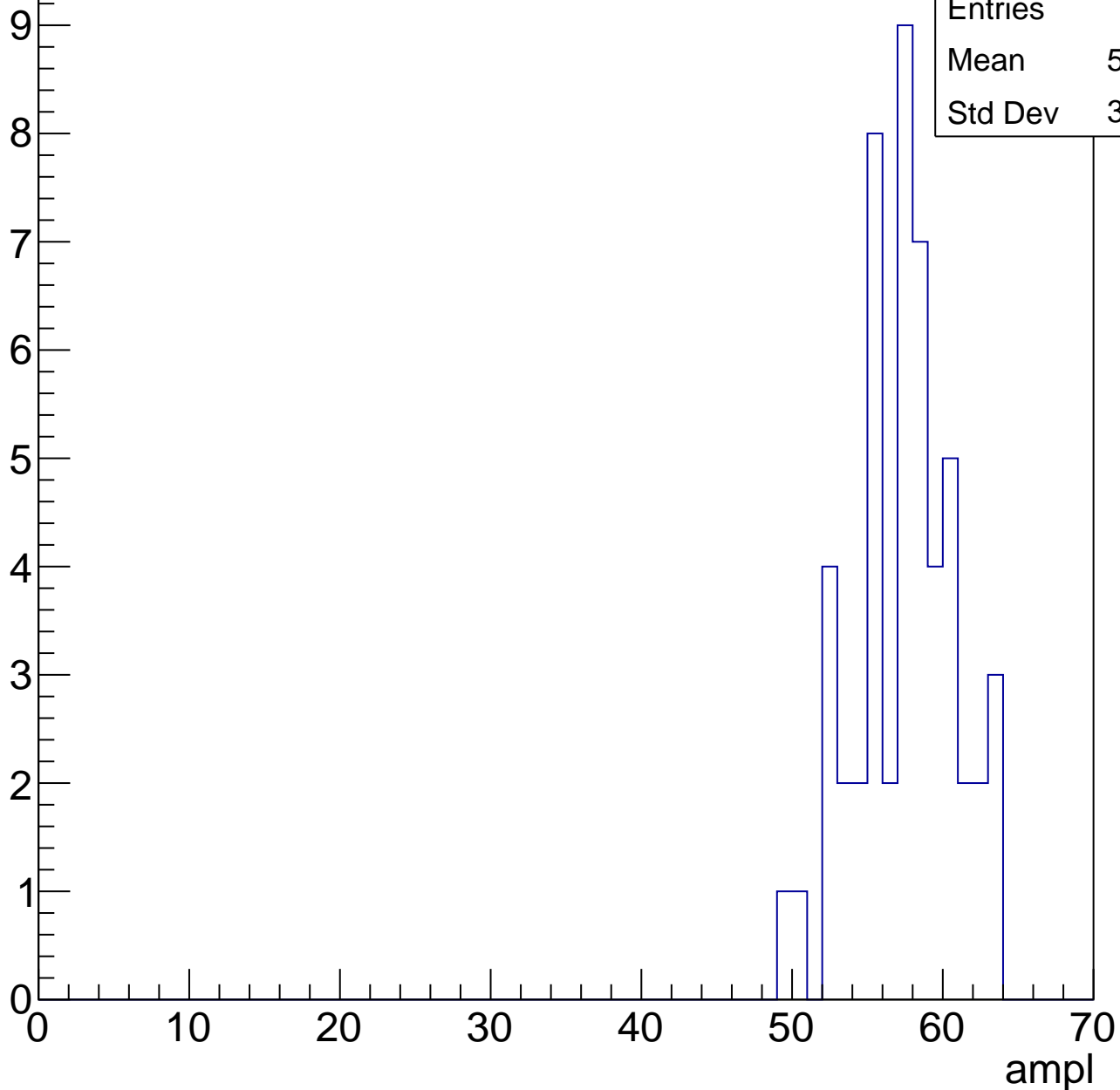
Entries	74
Mean	51.76
Std Dev	3.844



B0L002S, U12-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

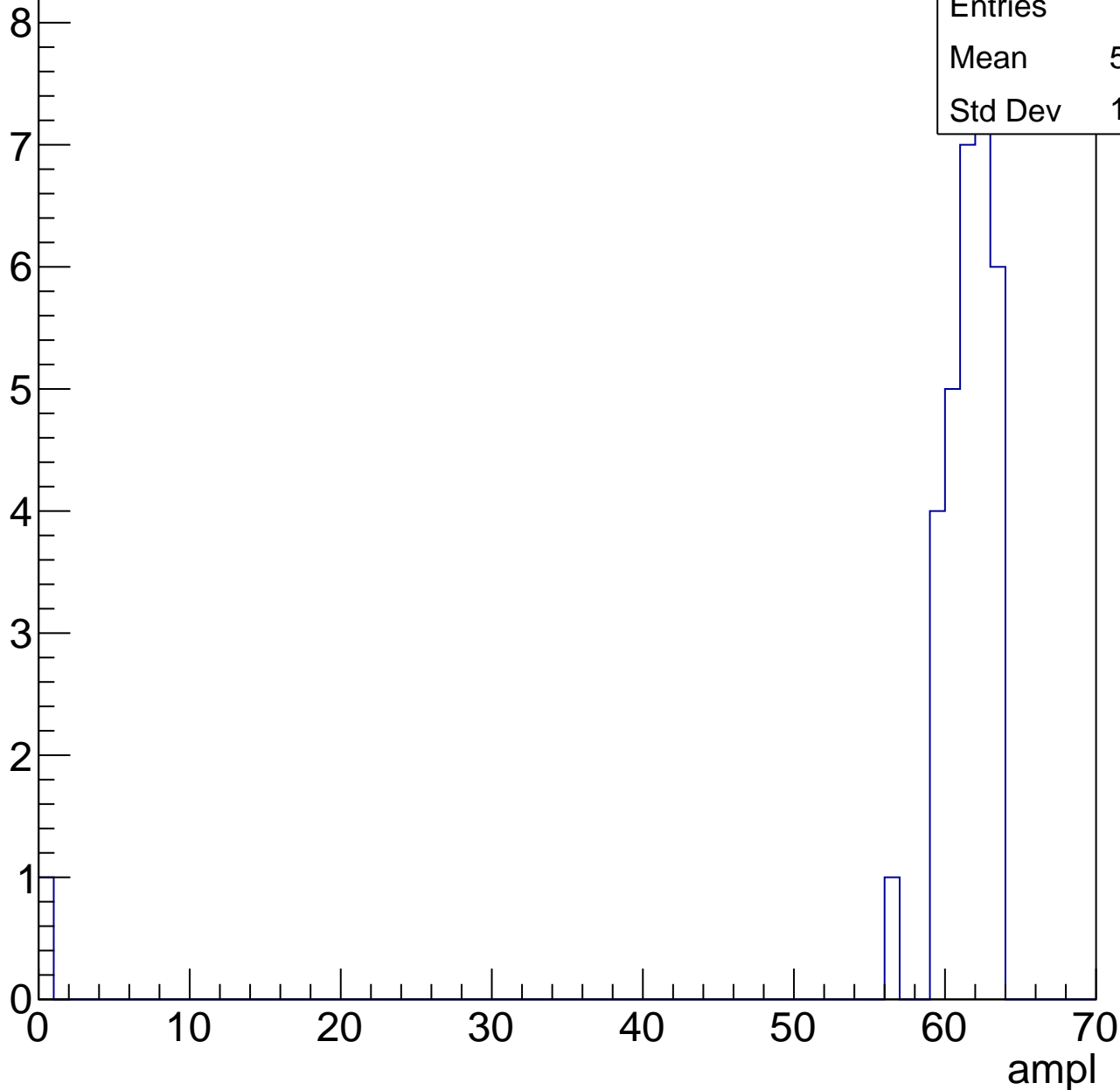


B0L002S, U12-ch91, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	59.16
Std Dev	10.74



B0L002S, U12-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch92, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	31.21
Std Dev	3.136

Gaus mean : 31.5017

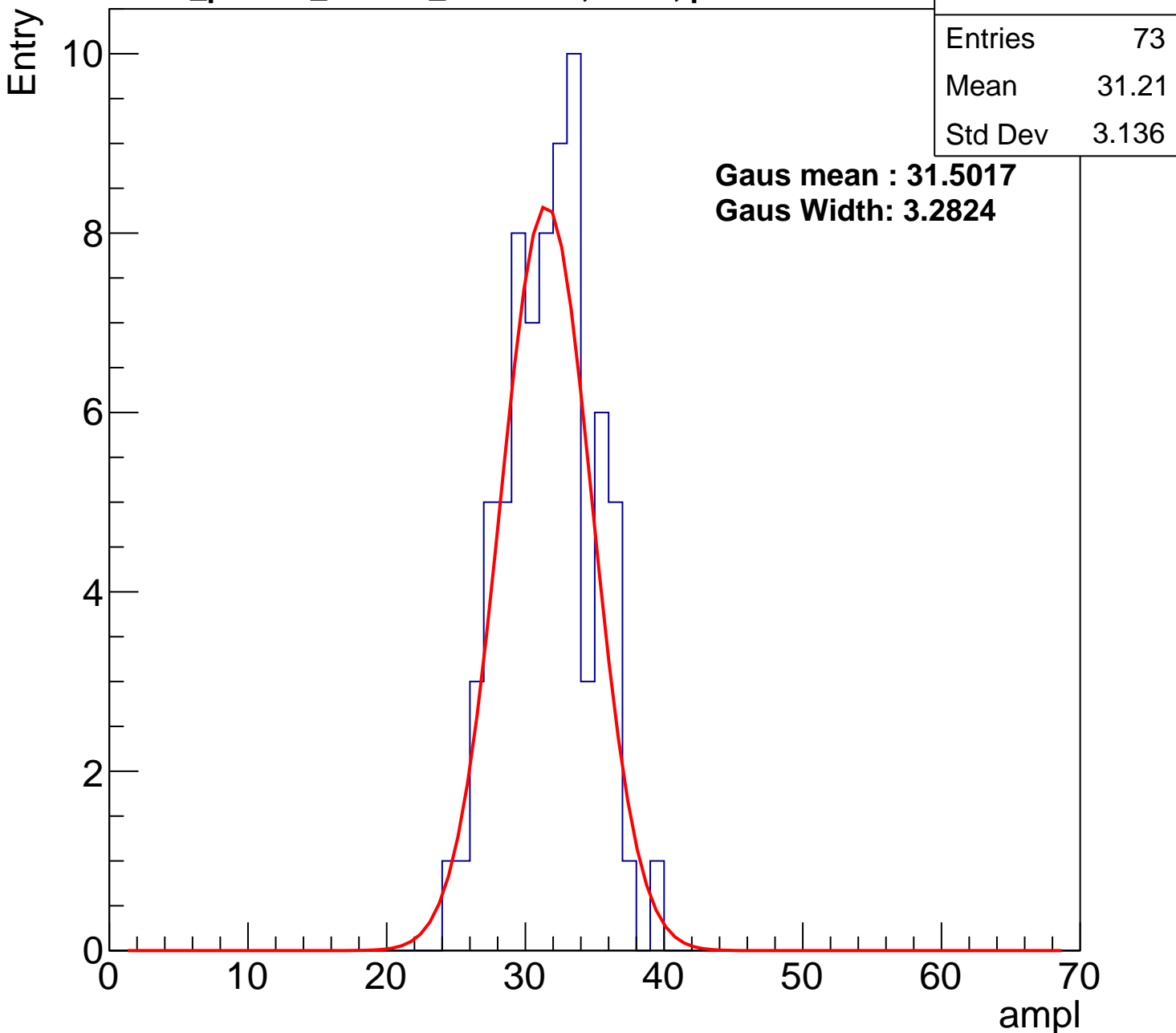
Gaus Width: 3.2824

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch92, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	38.82
Std Dev	3.155

Gaus mean : 39.3348

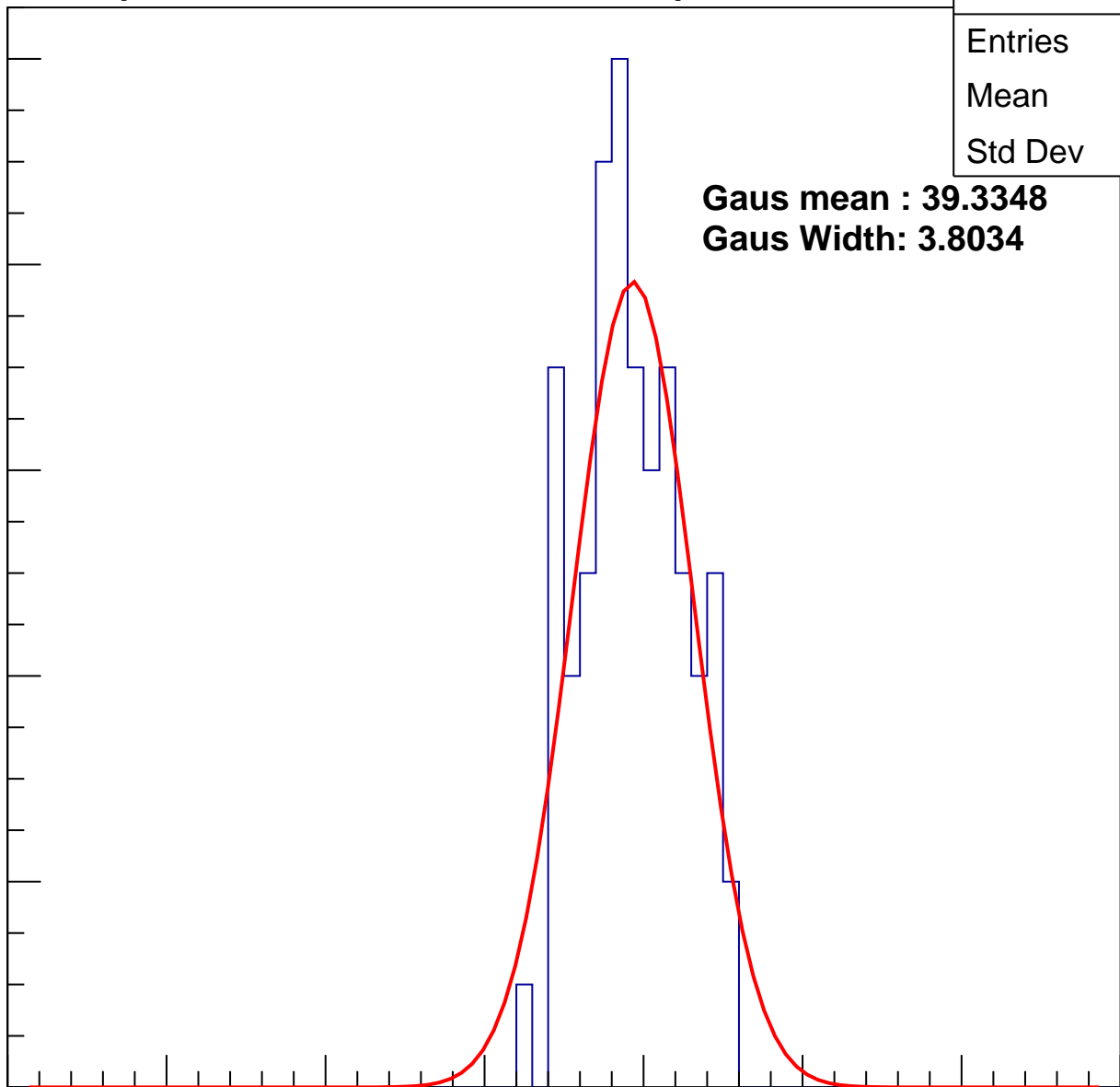
Gaus Width: 3.8034

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch92, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	45.25
Std Dev	3.112

Gaus mean : 45.7114

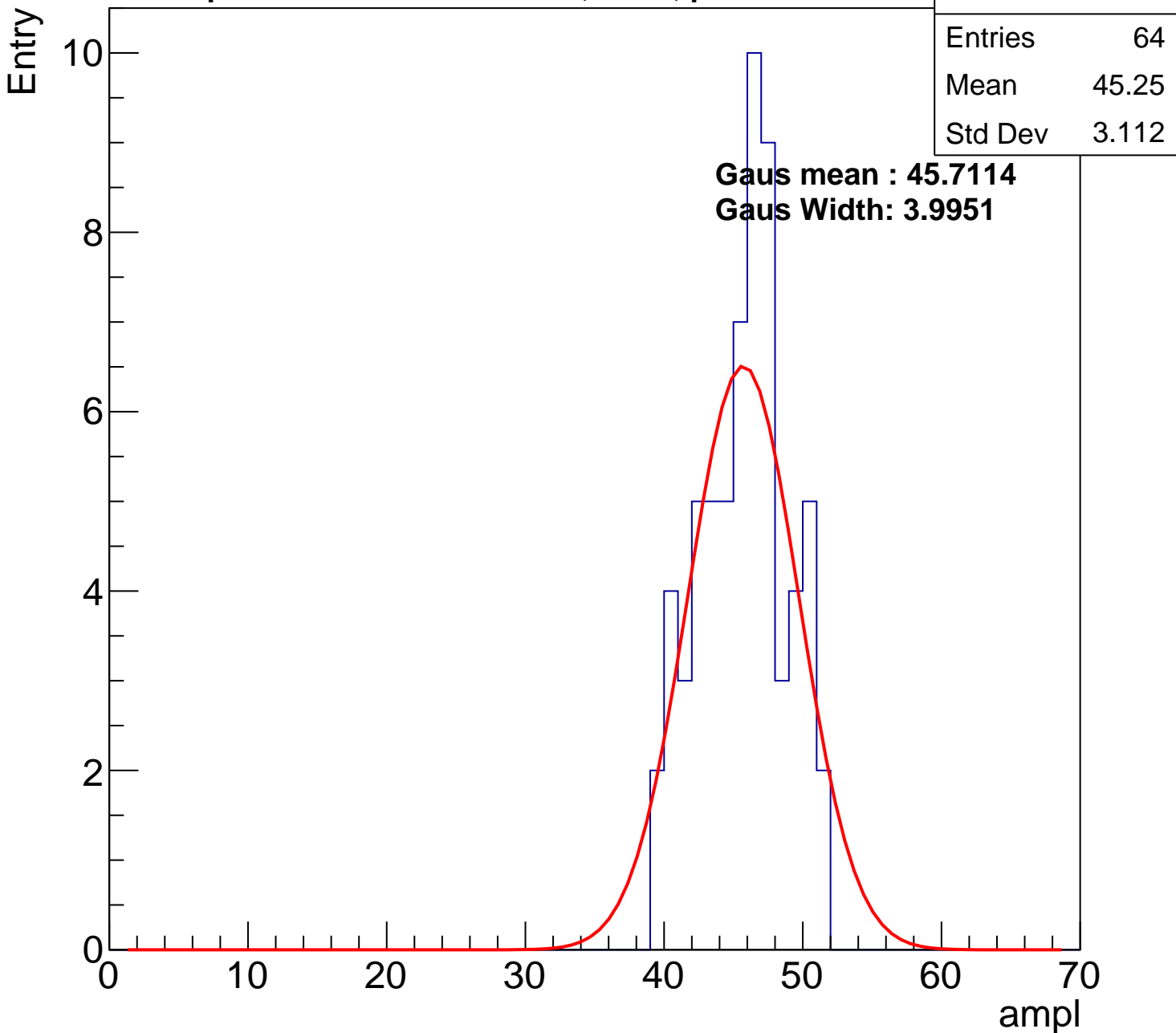
Gaus Width: 3.9951

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

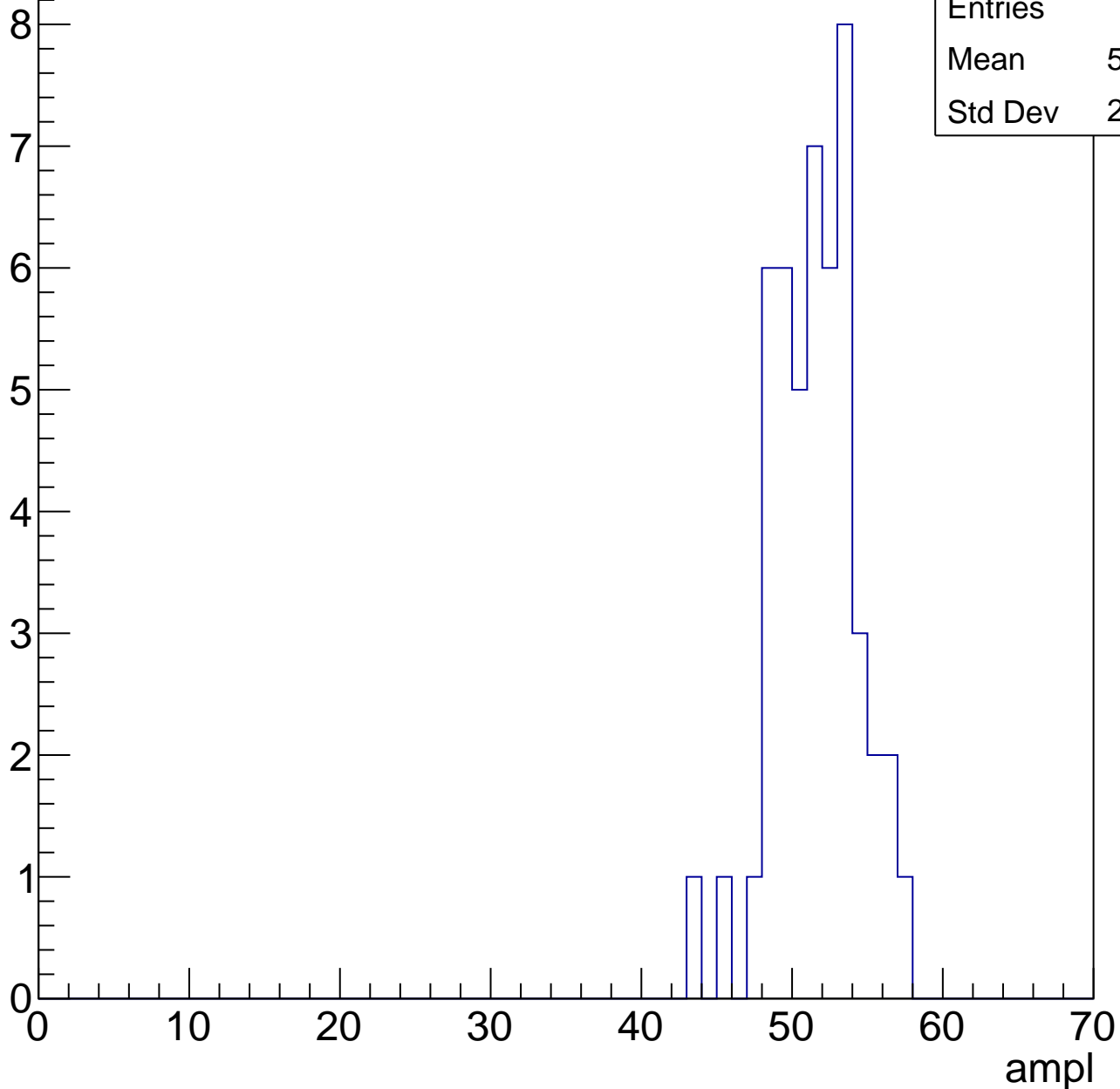


B0L002S, U12-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

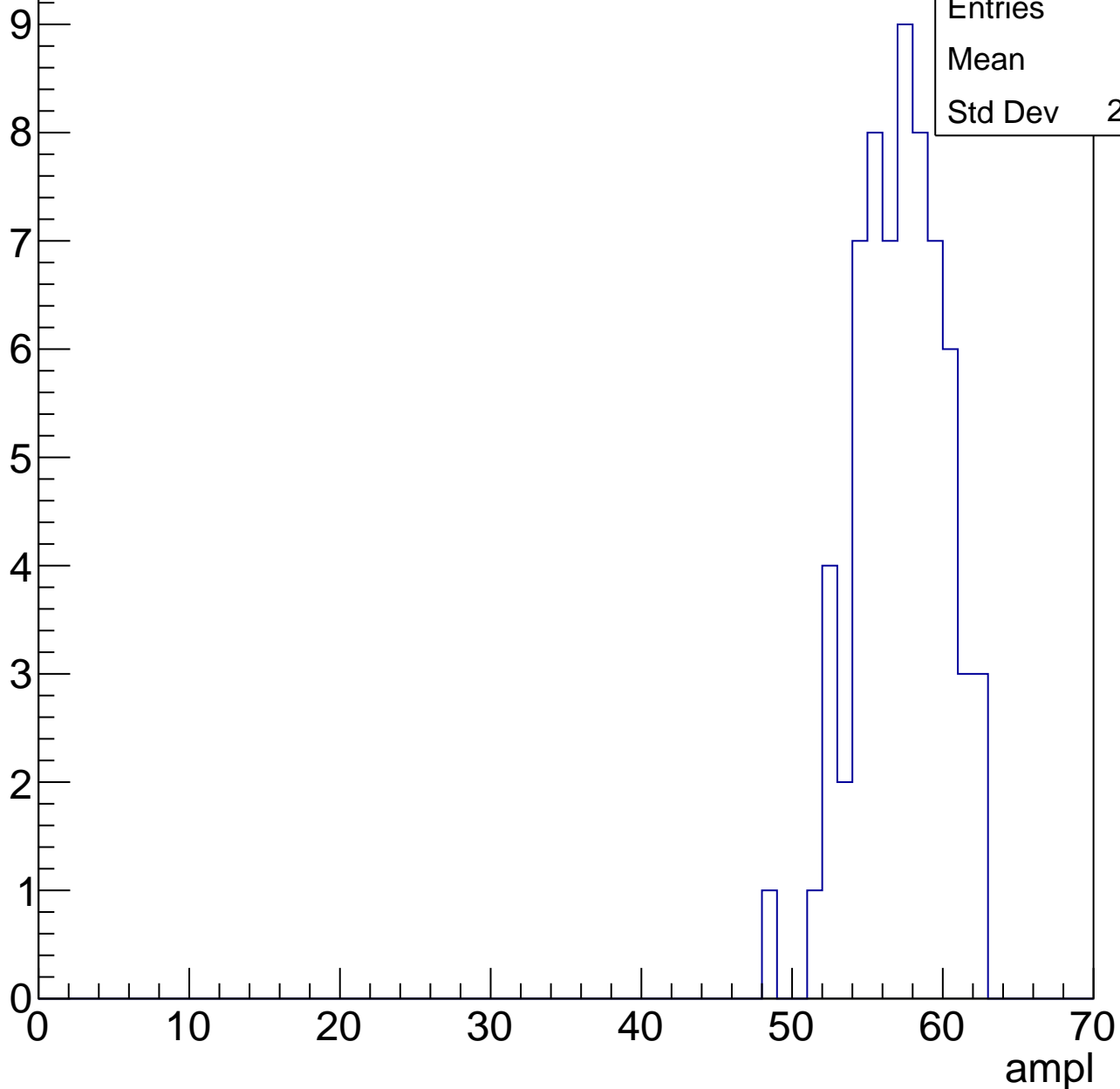
Entries	49
Mean	51.04
Std Dev	2.792



B0L002S, U12-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

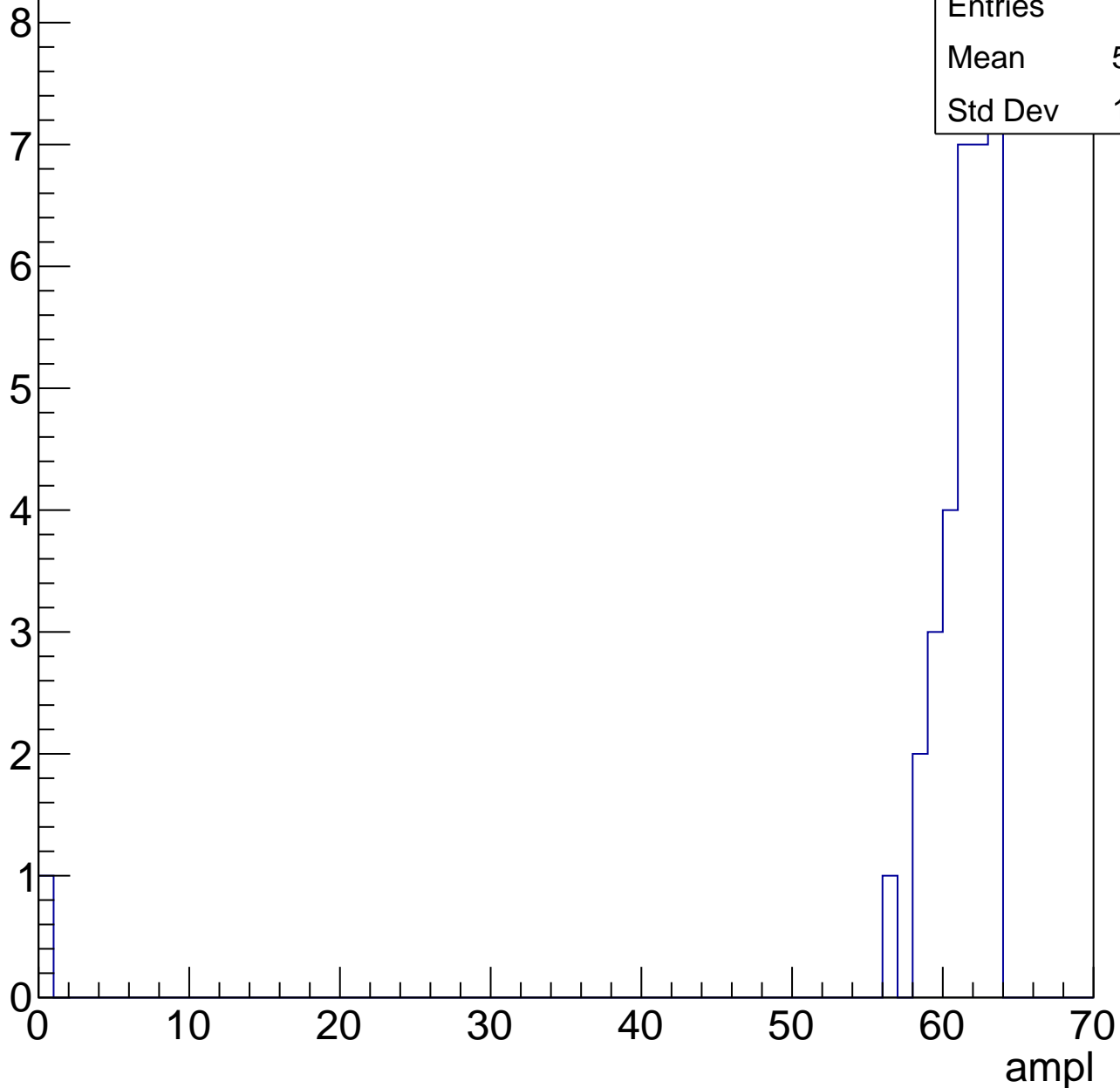


B0L002S, U12-ch92, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	59.21
Std Dev	10.61



B0L002S, U12-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B0L002S, U12-ch93, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	32.02
Std Dev	5.134

Gaus mean : 32.6892

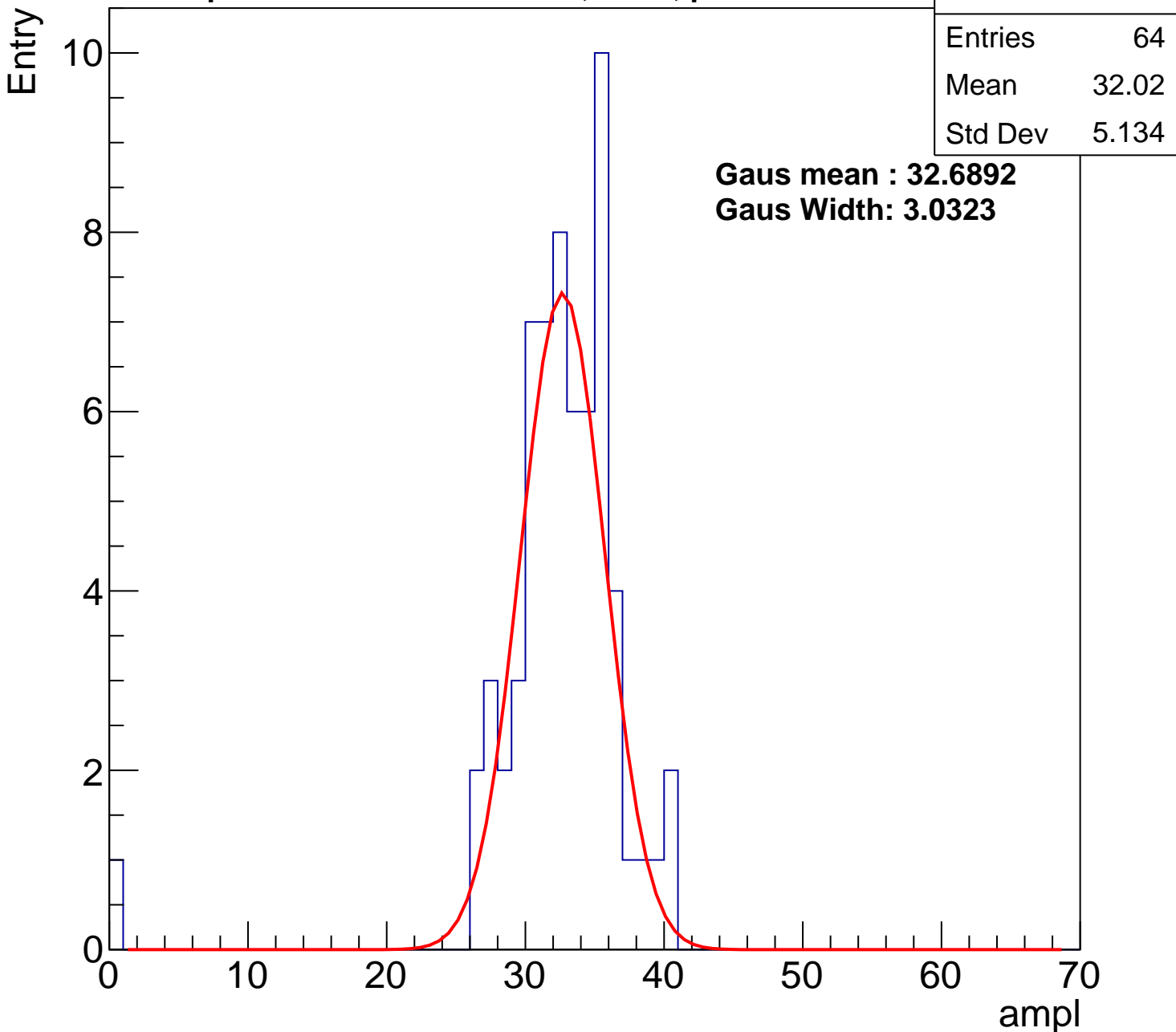
Gaus Width: 3.0323

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



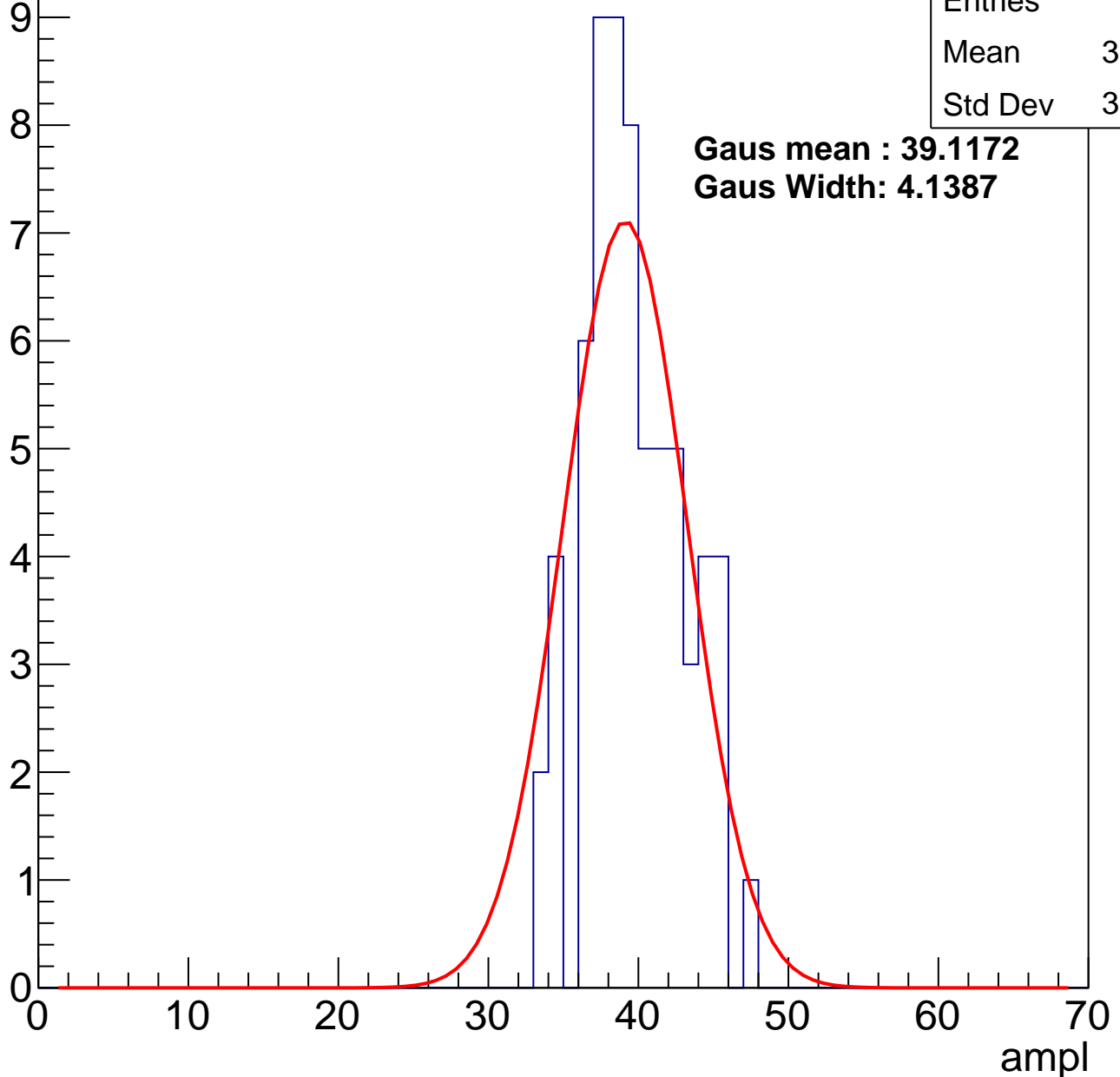
B0L002S, U12-ch93, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	39.26
Std Dev	3.264

Gaus mean : 39.1172
Gaus Width: 4.1387



B0L002S, U12-ch93, adc2

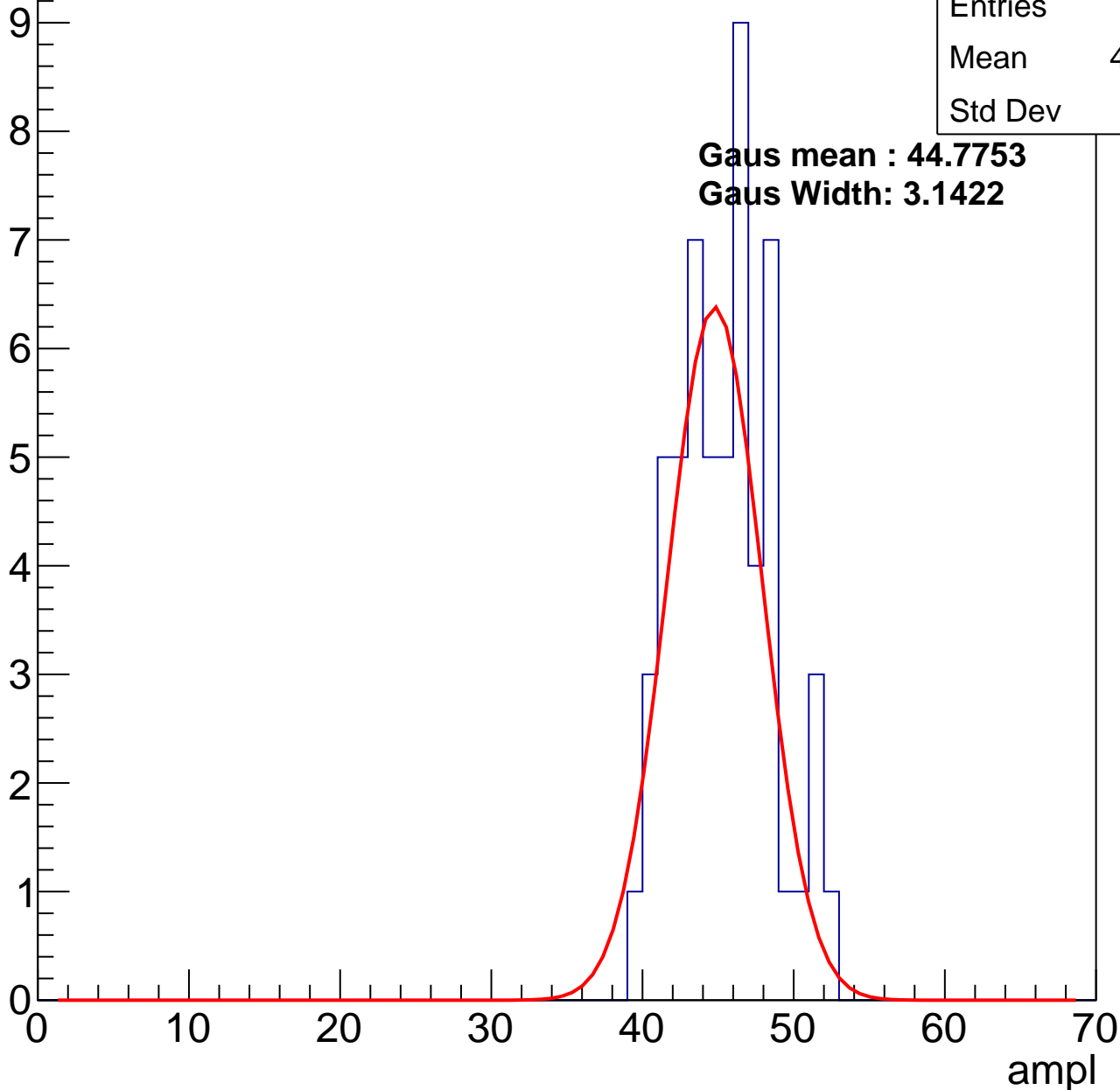
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.95
Std Dev	3.12

Gaus mean : 44.7753

Gaus Width: 3.1422

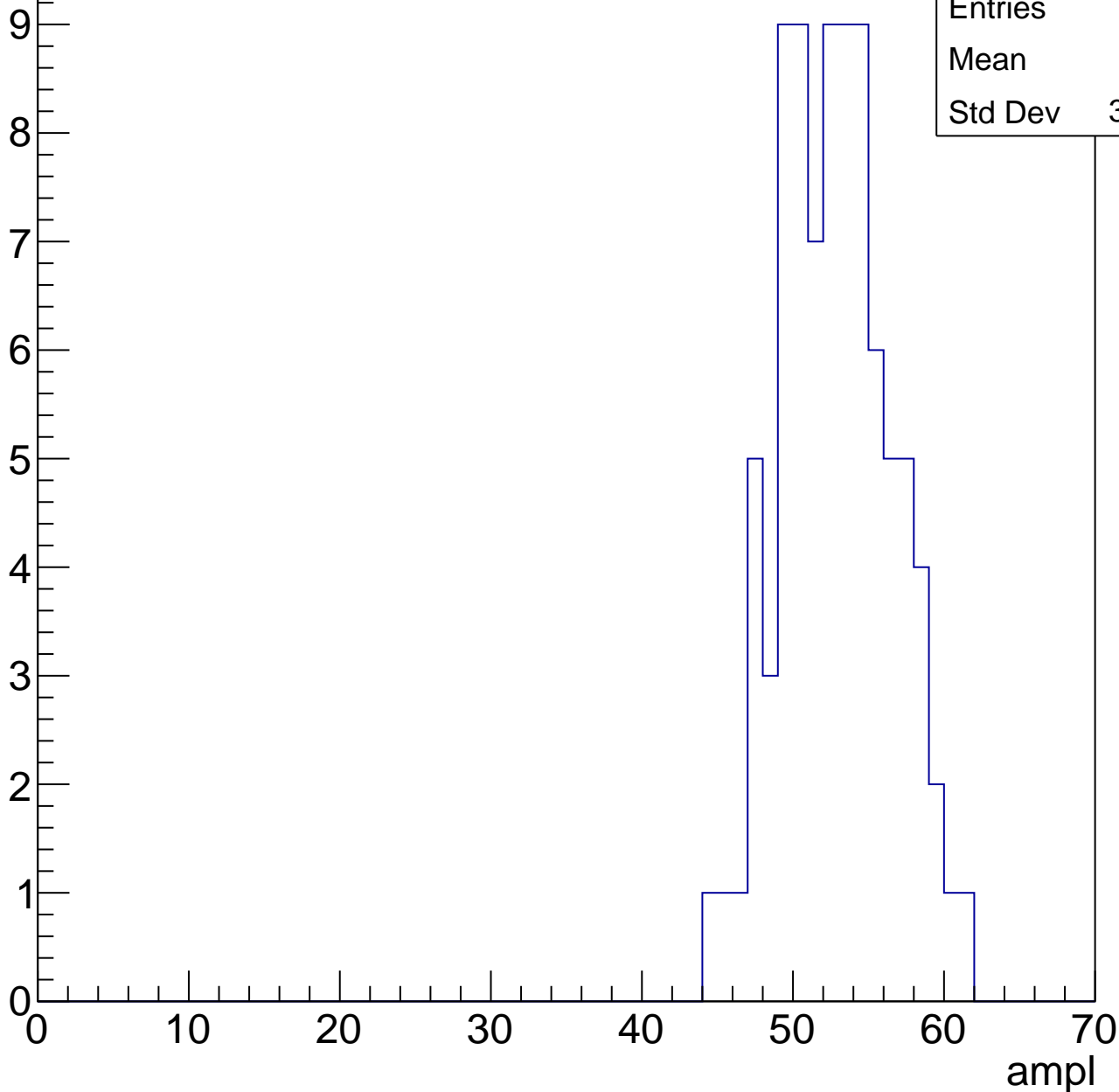


B0L002S, U12-ch93, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	52.4
Std Dev	3.593



B0L002S, U12-ch93, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

5

4

3

2

1

0

Entries	42
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Mean	58.83
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Std Dev	2.751
---------	-------

ampl

0

10

20

30

40

50

60

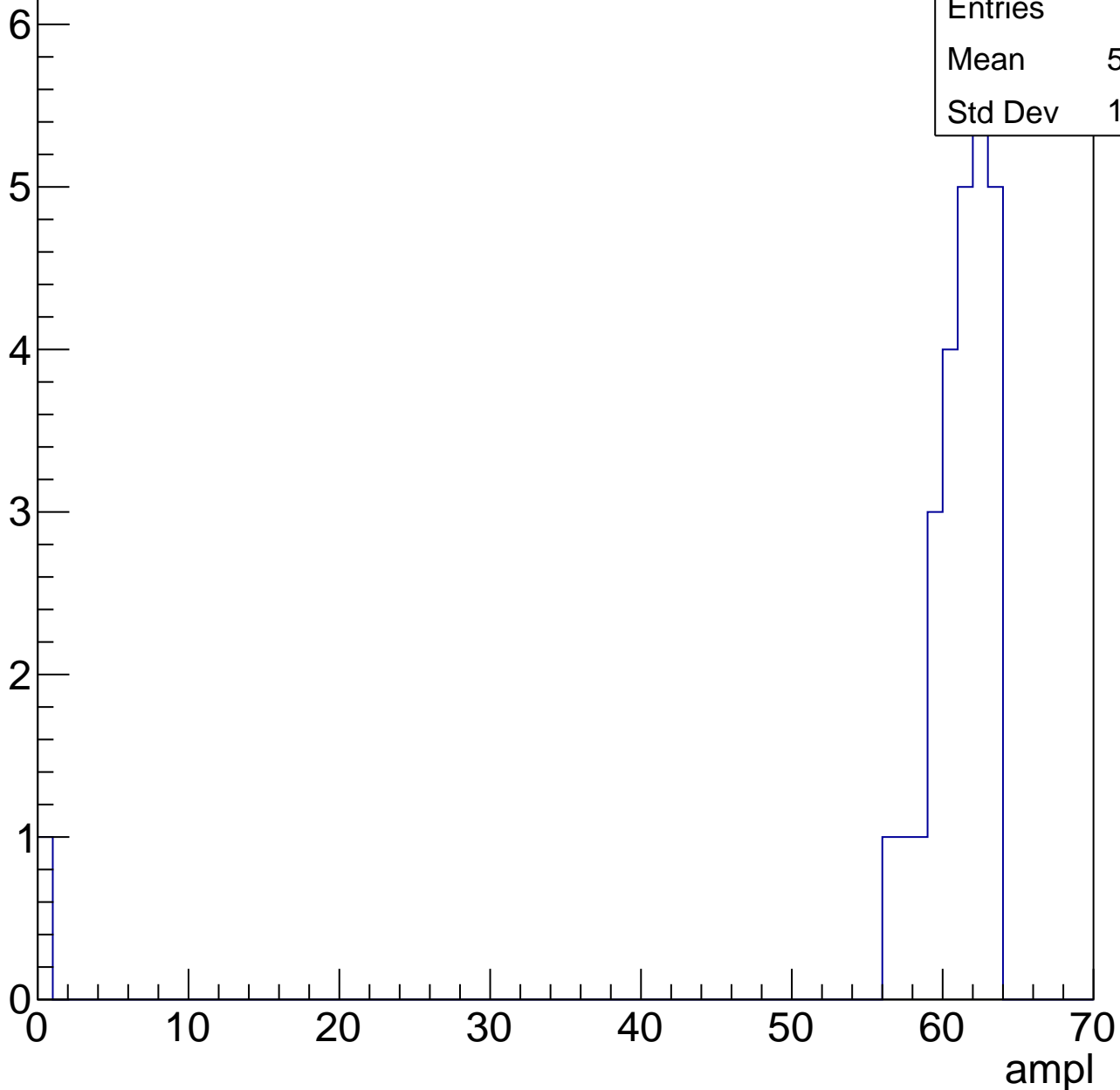
70

B0L002S, U12-ch93, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.52
Std Dev	11.62



B0L002S, U12-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

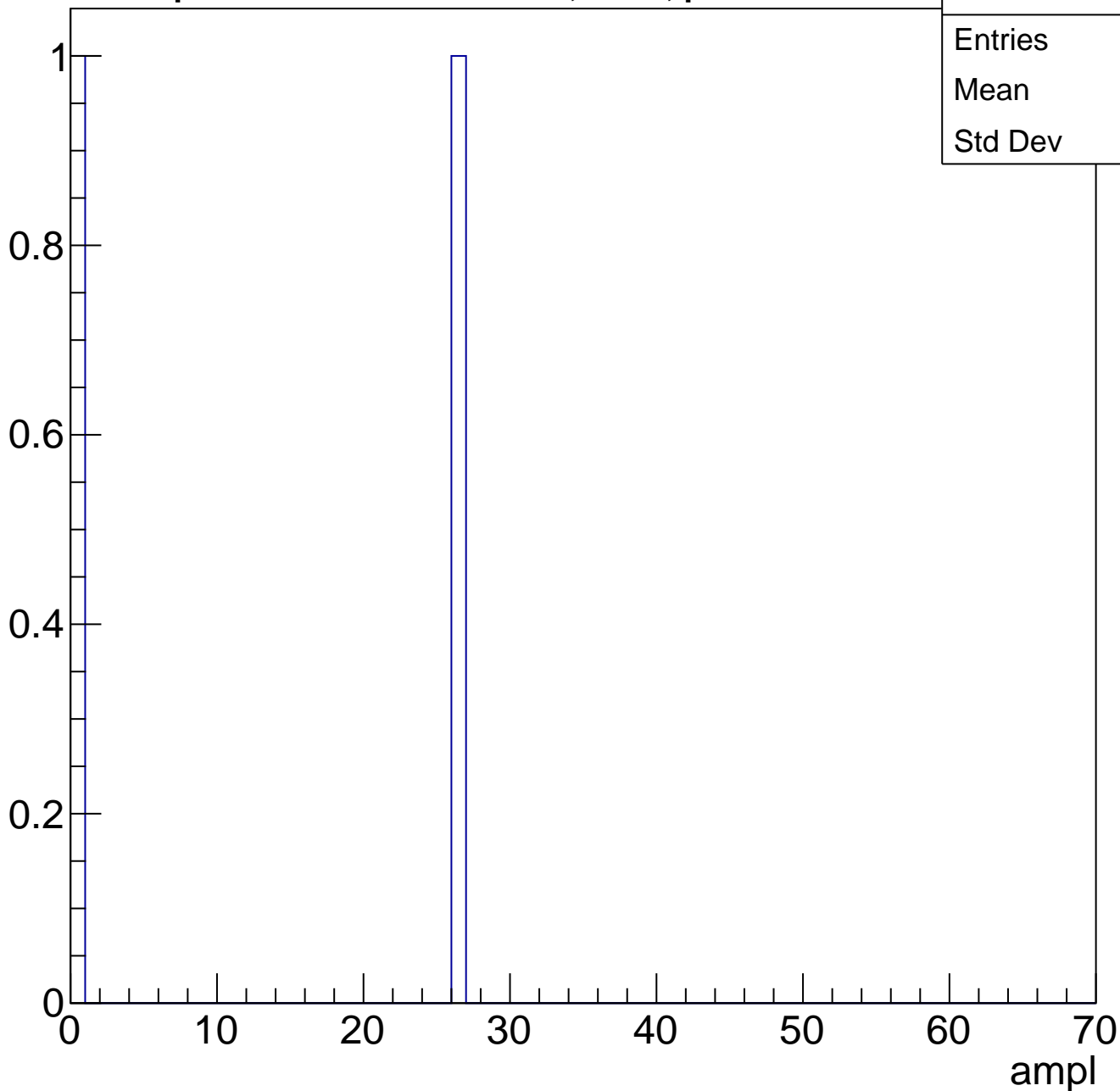


Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	13
Std Dev	13

B0L002S, U12-ch94, adc0

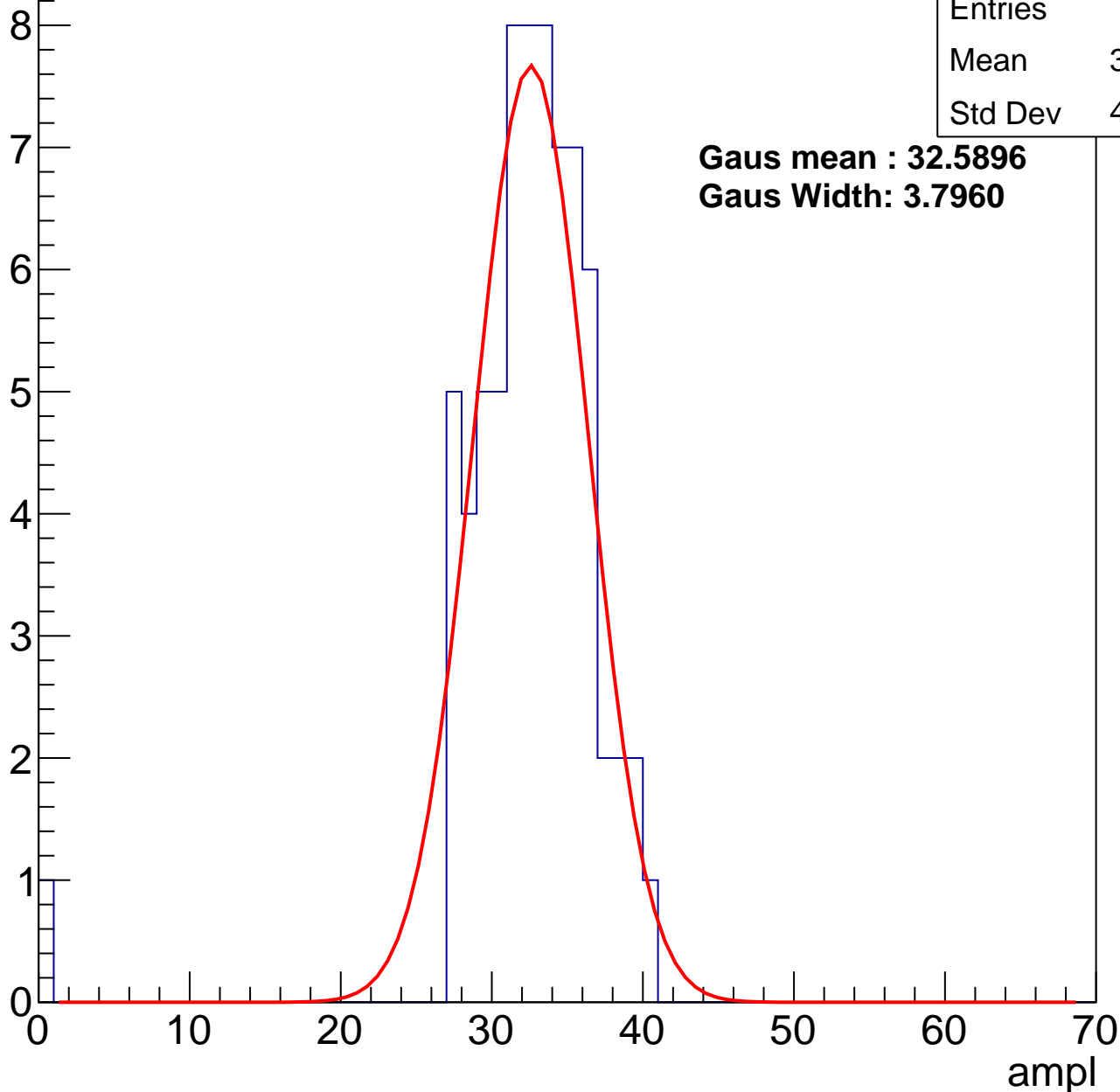
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	32.07
Std Dev	4.985

Gaus mean : 32.5896

Gaus Width: 3.7960



B0L002S, U12-ch94, adc1

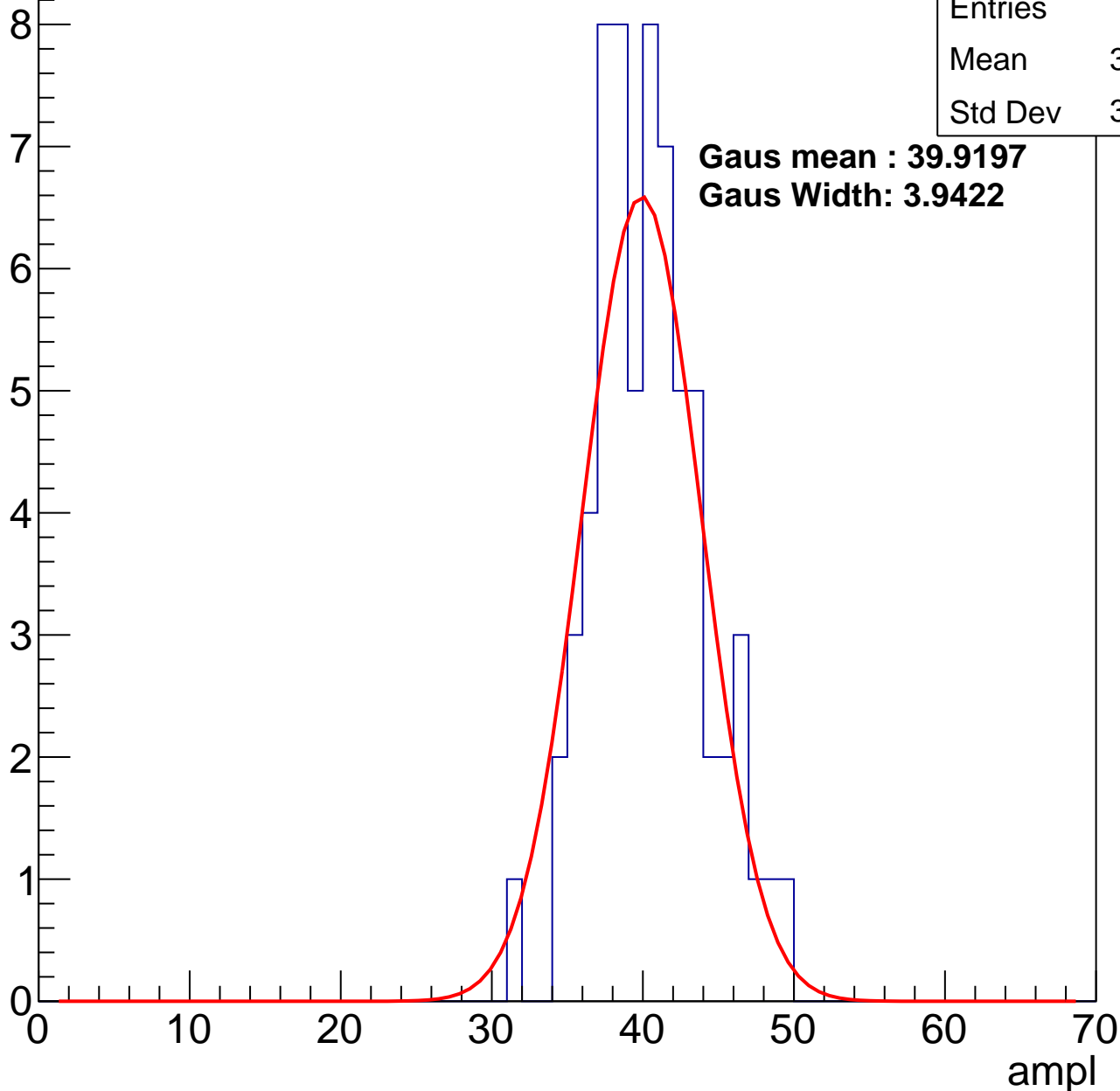
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	39.92
Std Dev	3.603

Gaus mean : 39.9197

Gaus Width: 3.9422



B0L002S, U12-ch94, adc2

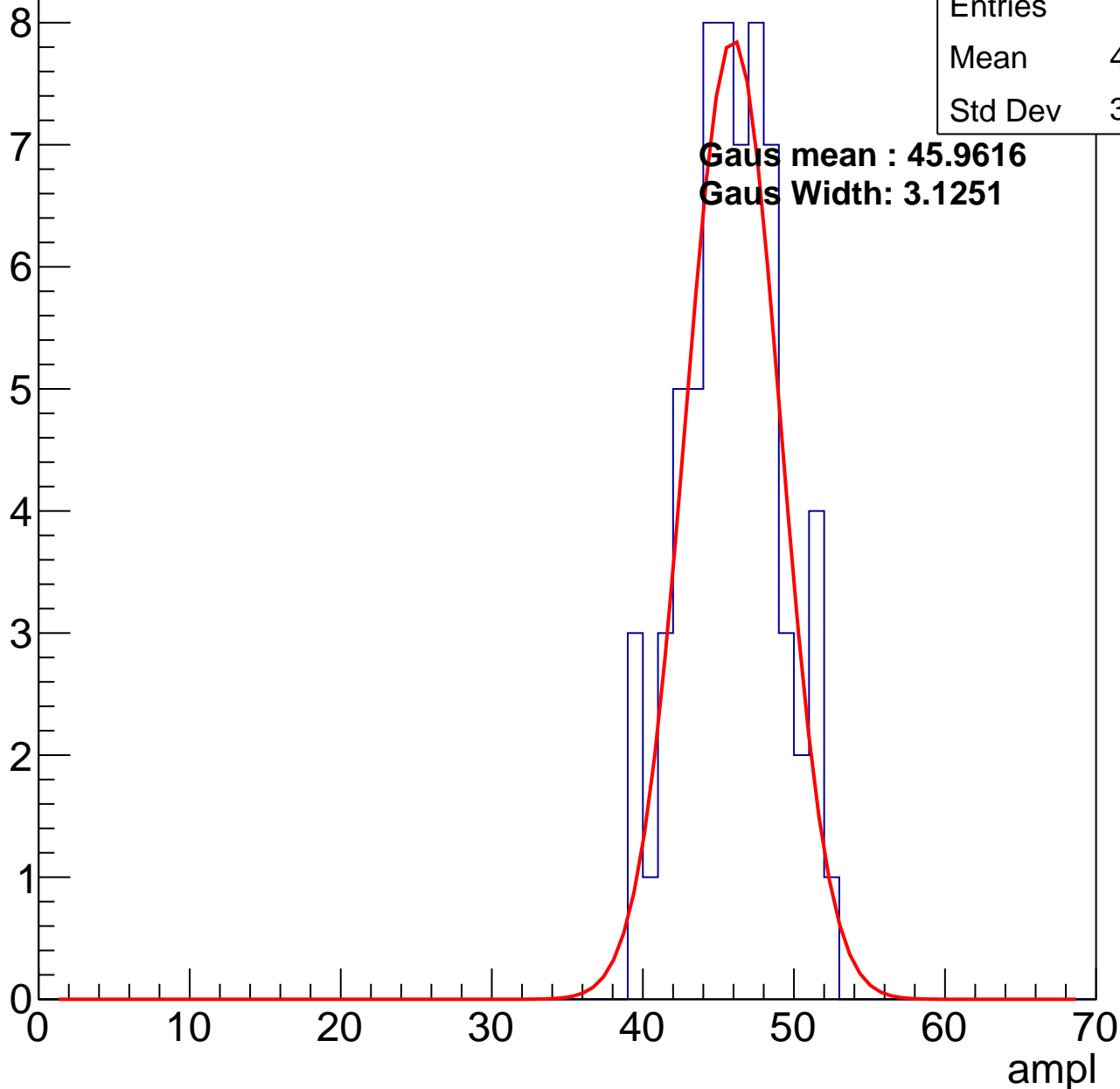
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	45.45
Std Dev	3.123

Gaus mean : 45.9616

Gaus Width: 3.1251

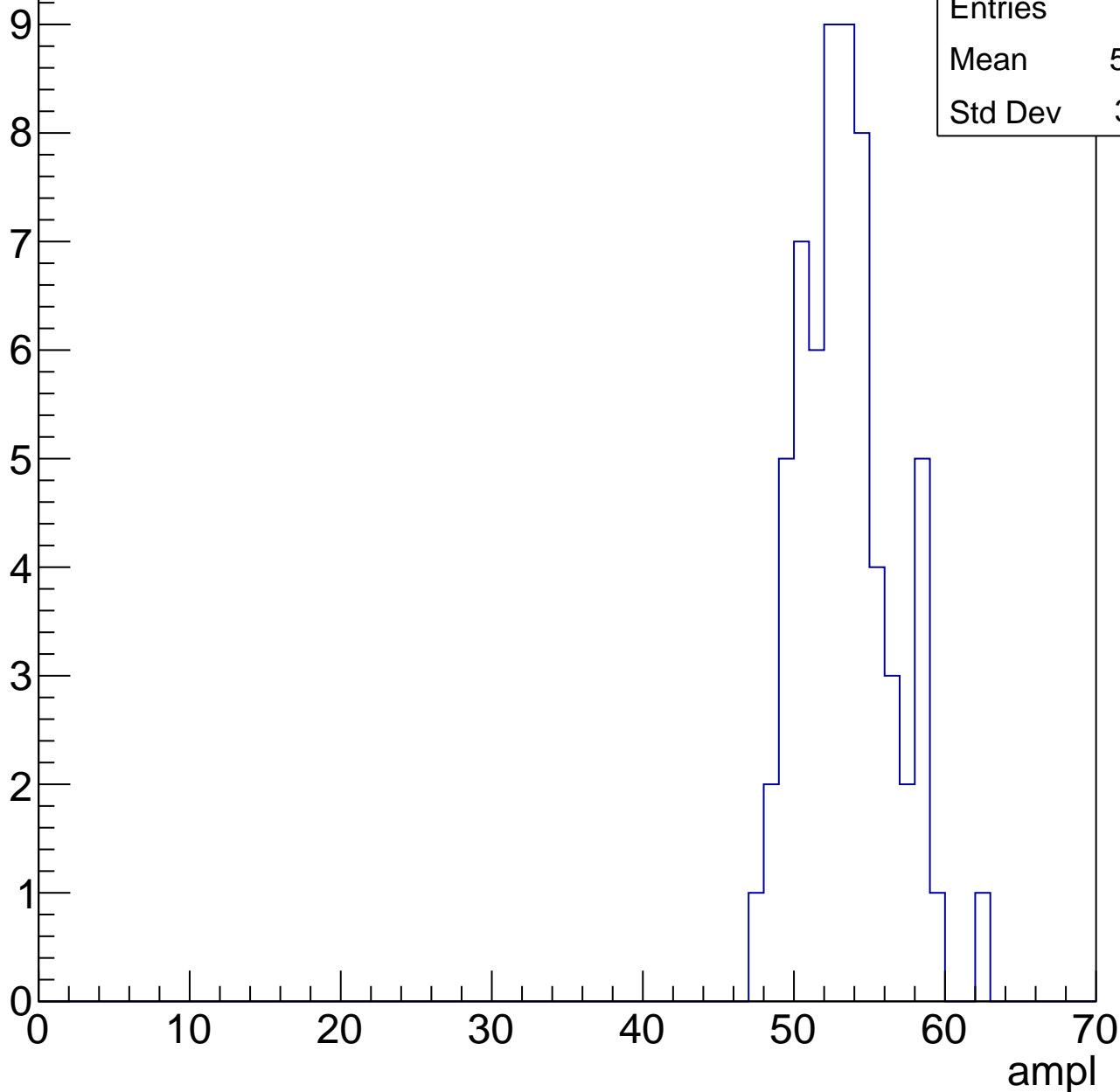


B0L002S, U12-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

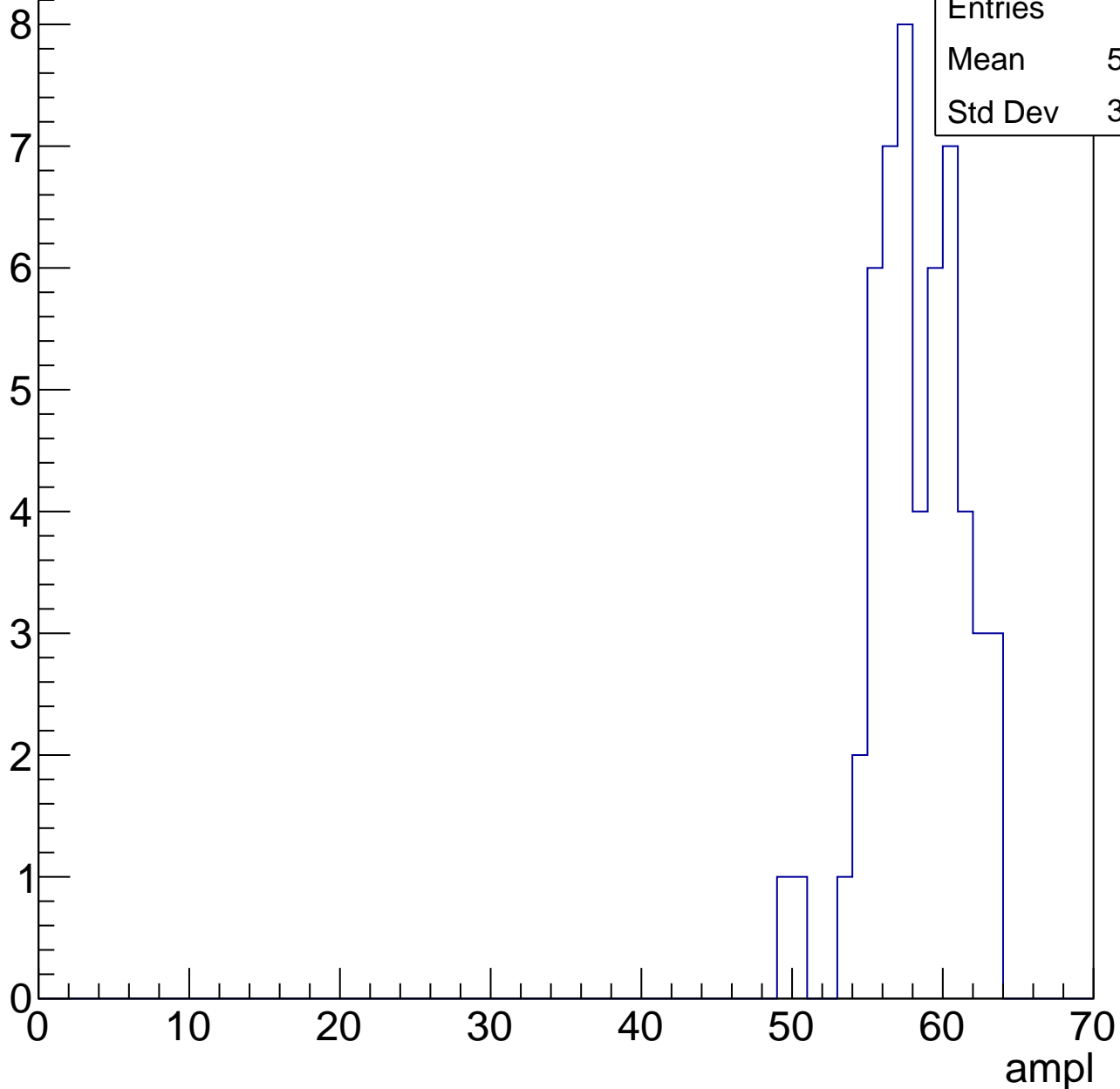
Entries	63
Mean	52.92
Std Dev	3.051



B0L002S, U12-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

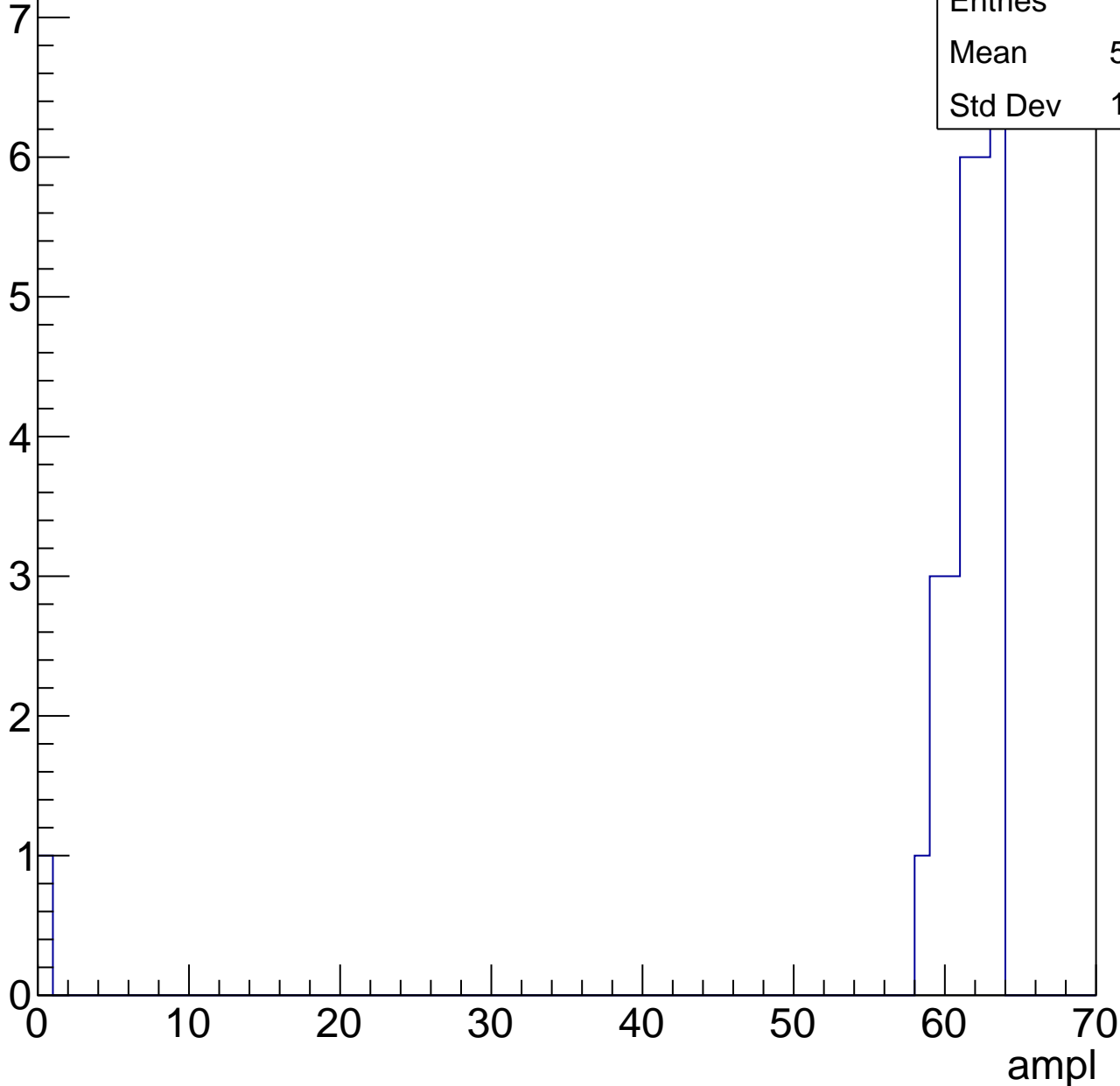


B0L002S, U12-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	59.04
Std Dev	11.67



B0L002S, U12-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch95, adc0

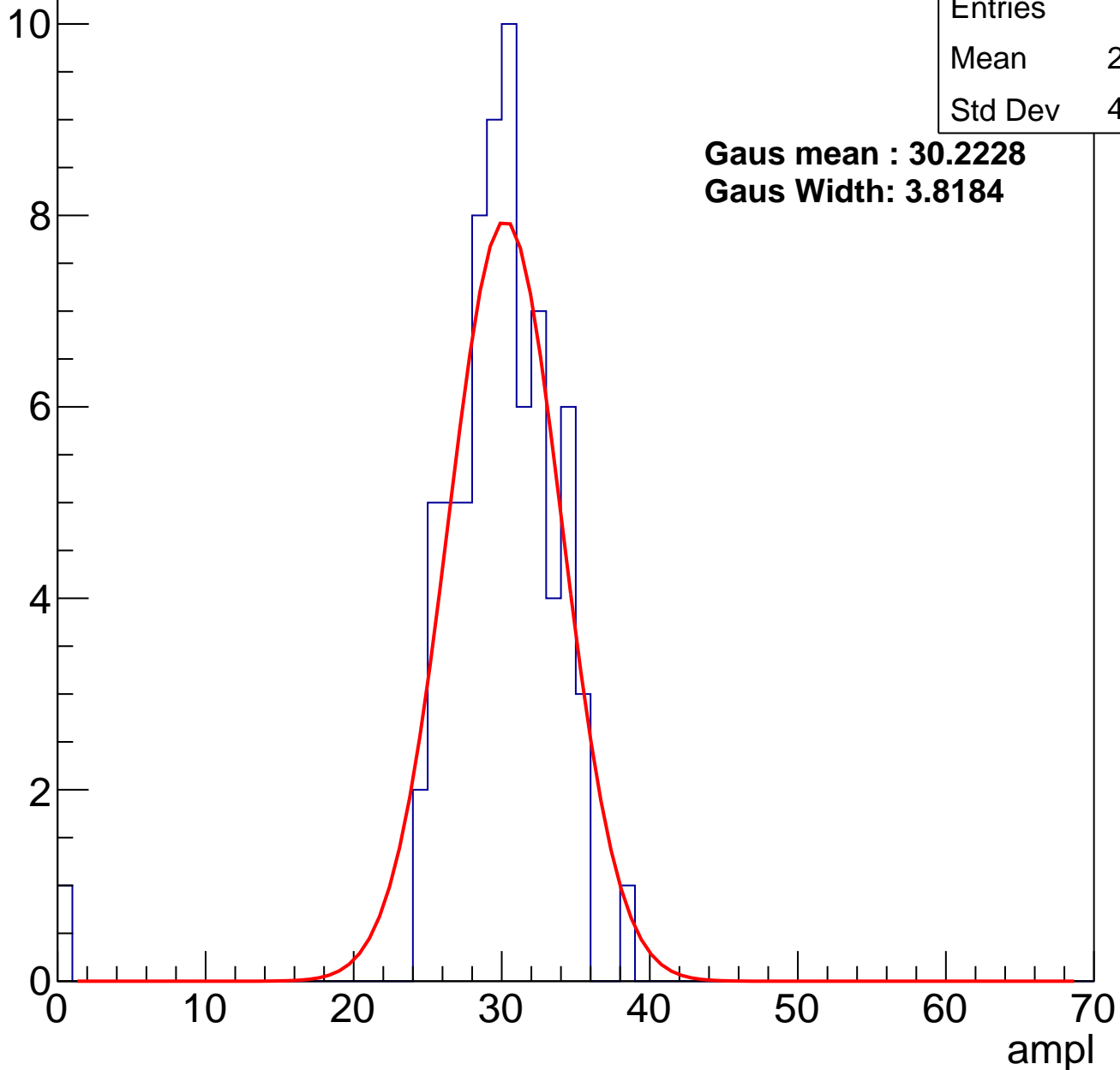
calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	29.33
Std Dev	4.619

Gaus mean : 30.2228

Gaus Width: 3.8184

Entry



B0L002S, U12-ch95, adc1

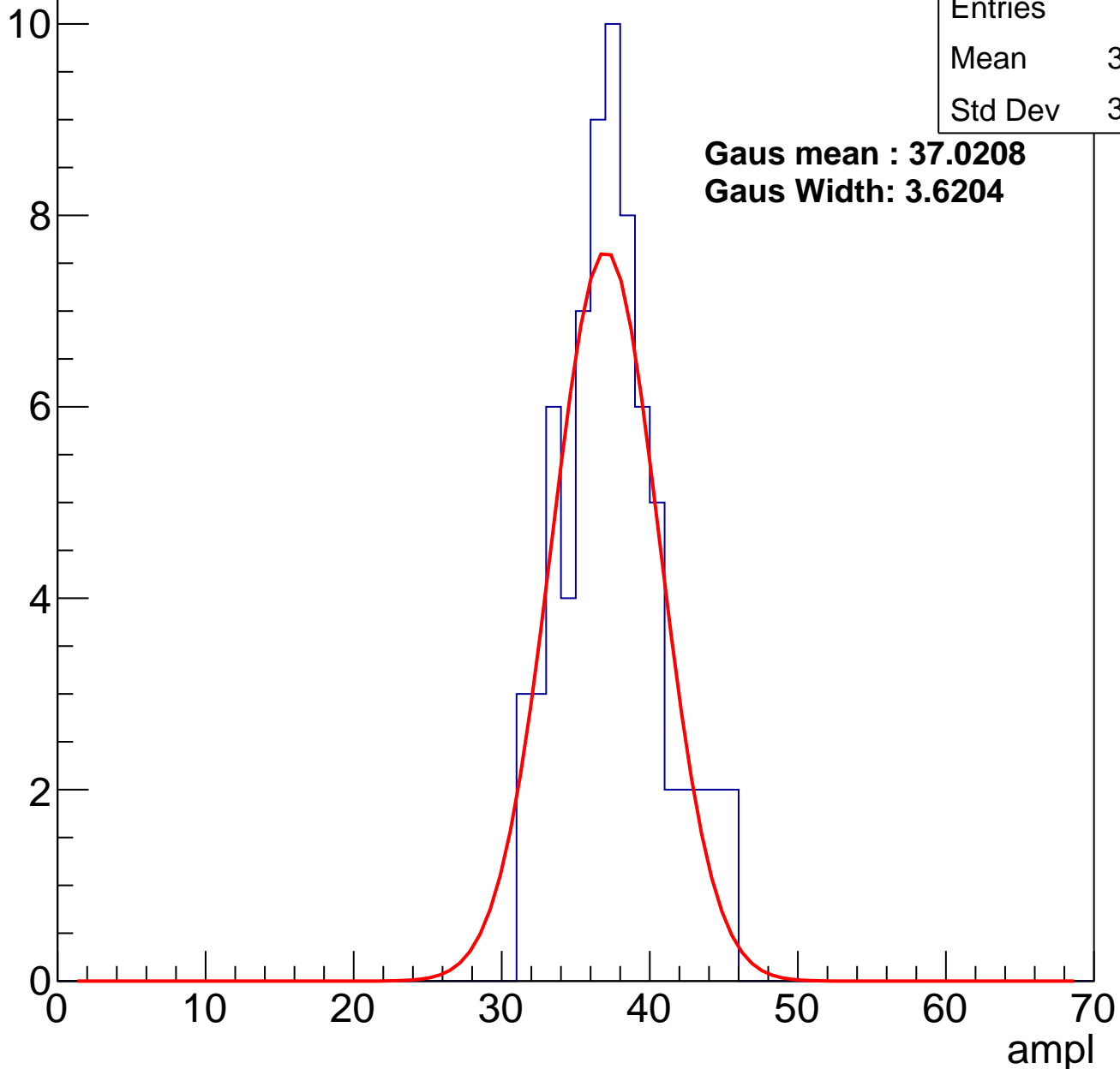
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	37.04
Std Dev	3.375

Gaus mean : 37.0208

Gaus Width: 3.6204

Entry



B0L002S, U12-ch95, adc2

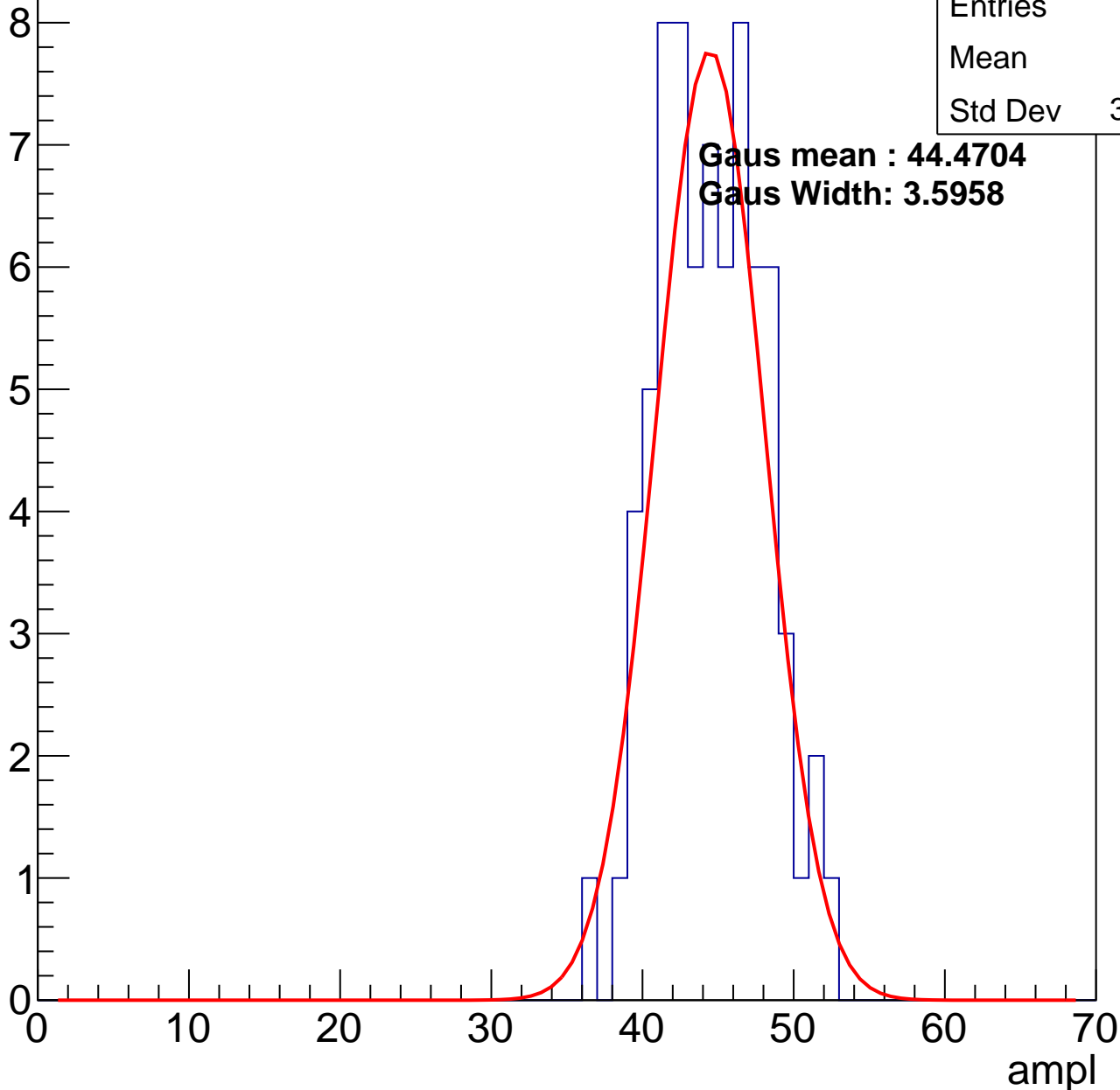
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.1
Std Dev	3.413

Gaus mean : 44.4704

Gaus Width: 3.5958

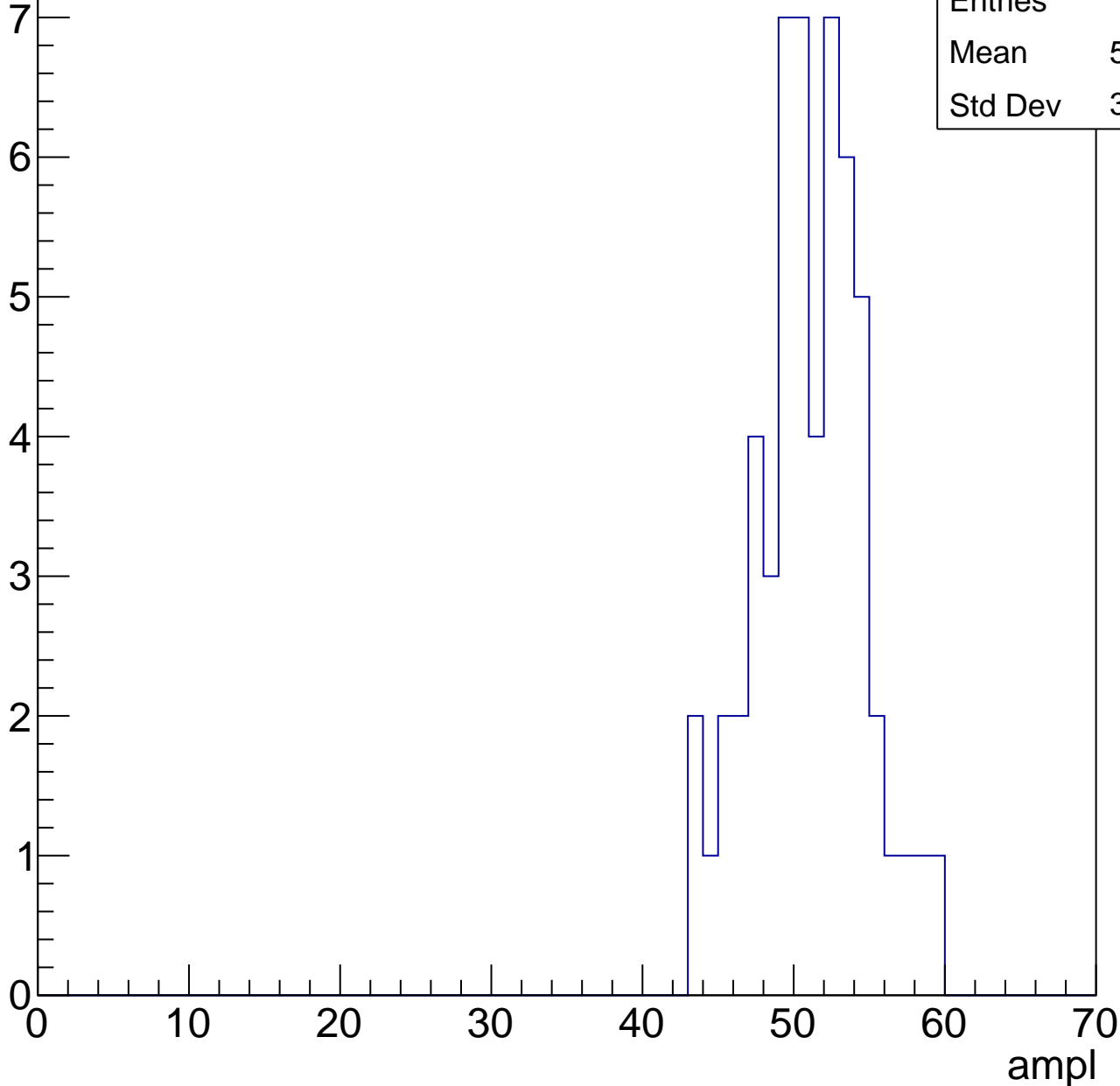


B0L002S, U12-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

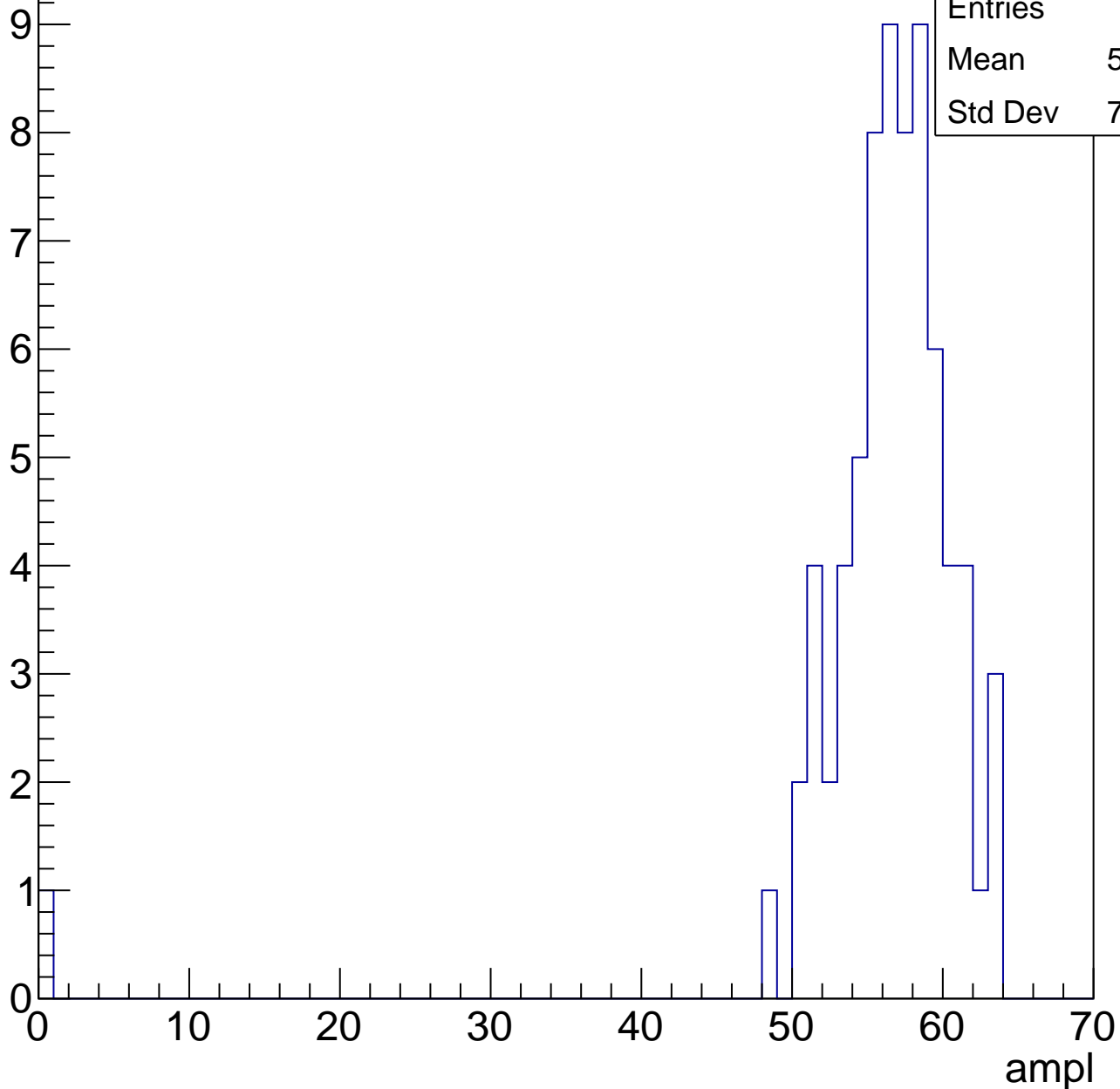
Entries	56
Mean	50.59
Std Dev	3.529



B0L002S, U12-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



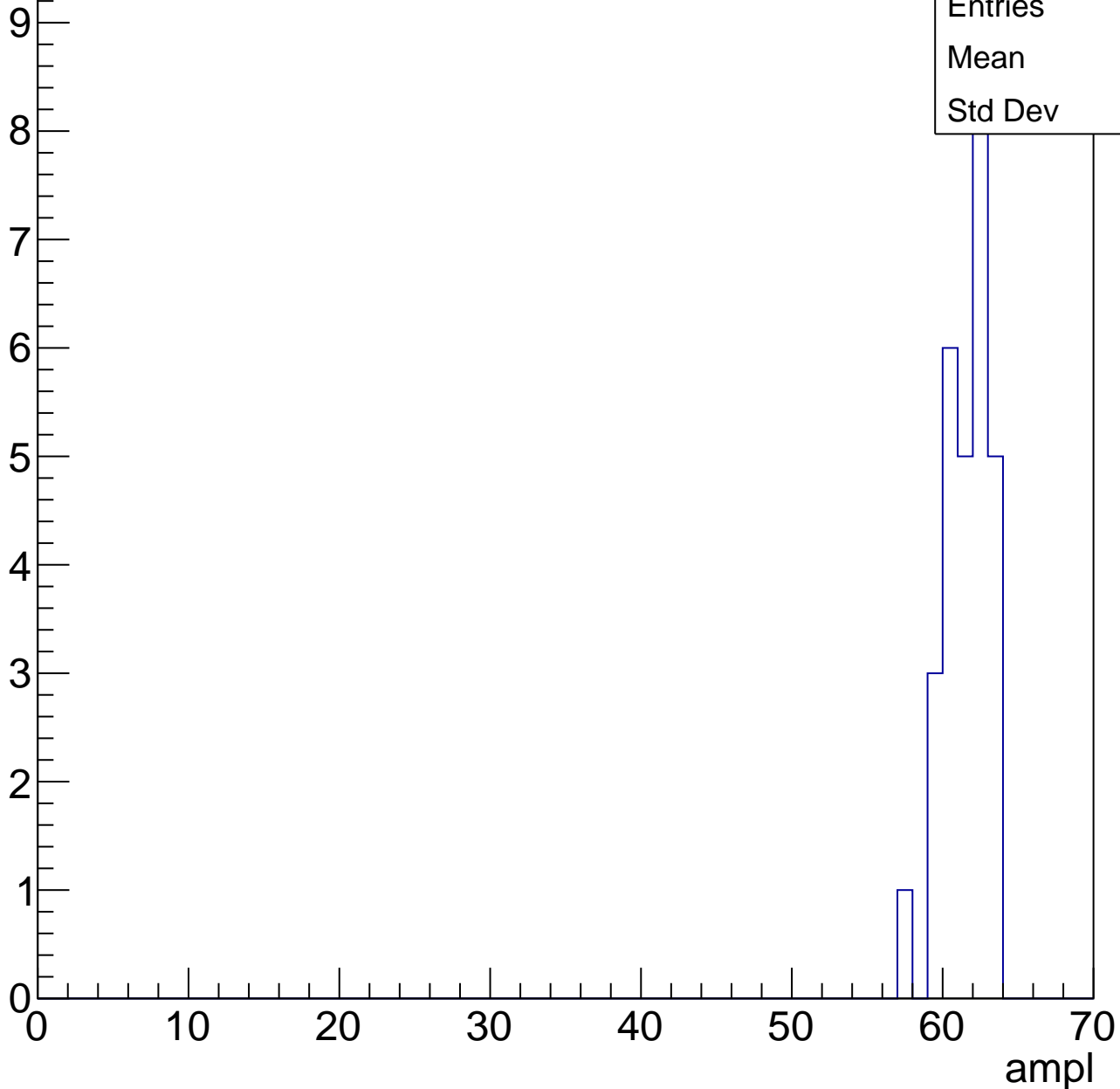
Entries	71
Mean	55.62
Std Dev	7.414

B0L002S, U12-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	61.1
Std Dev	1.47



B0L002S, U12-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

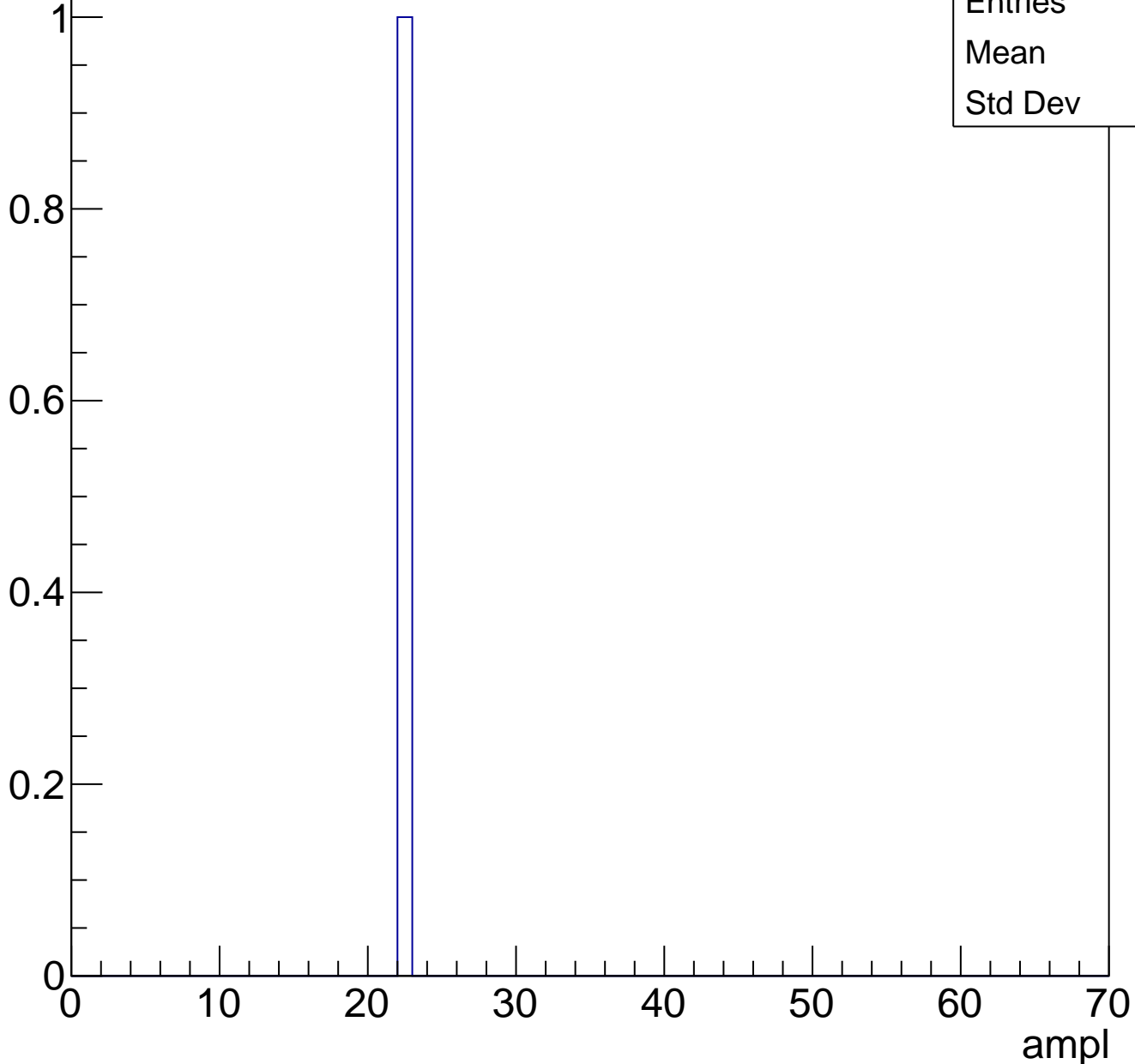
ampl



B0L002S, U12-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U12-ch96, adc0

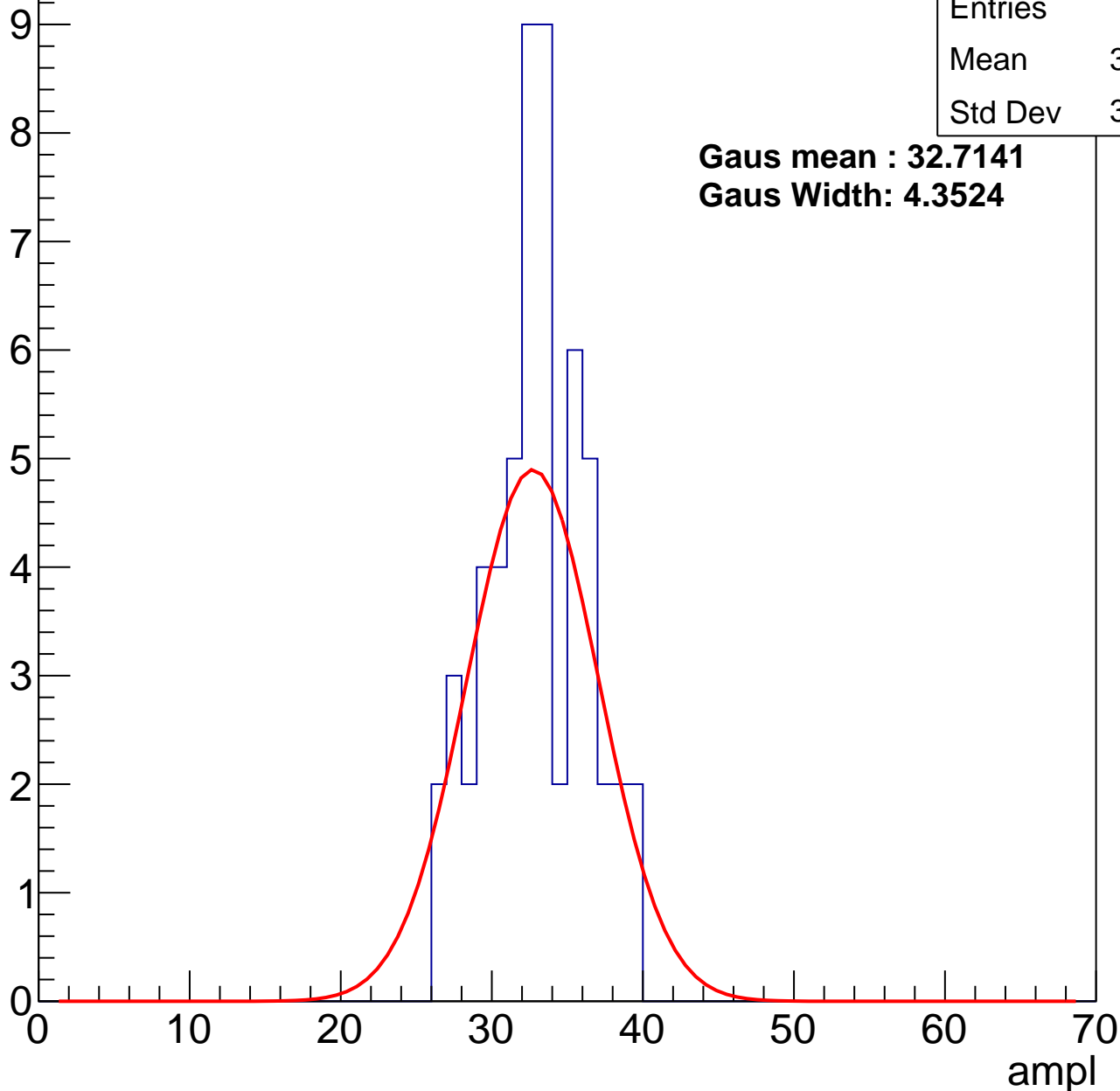
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	32.47
Std Dev	3.229

Gaus mean : 32.7141

Gaus Width: 4.3524



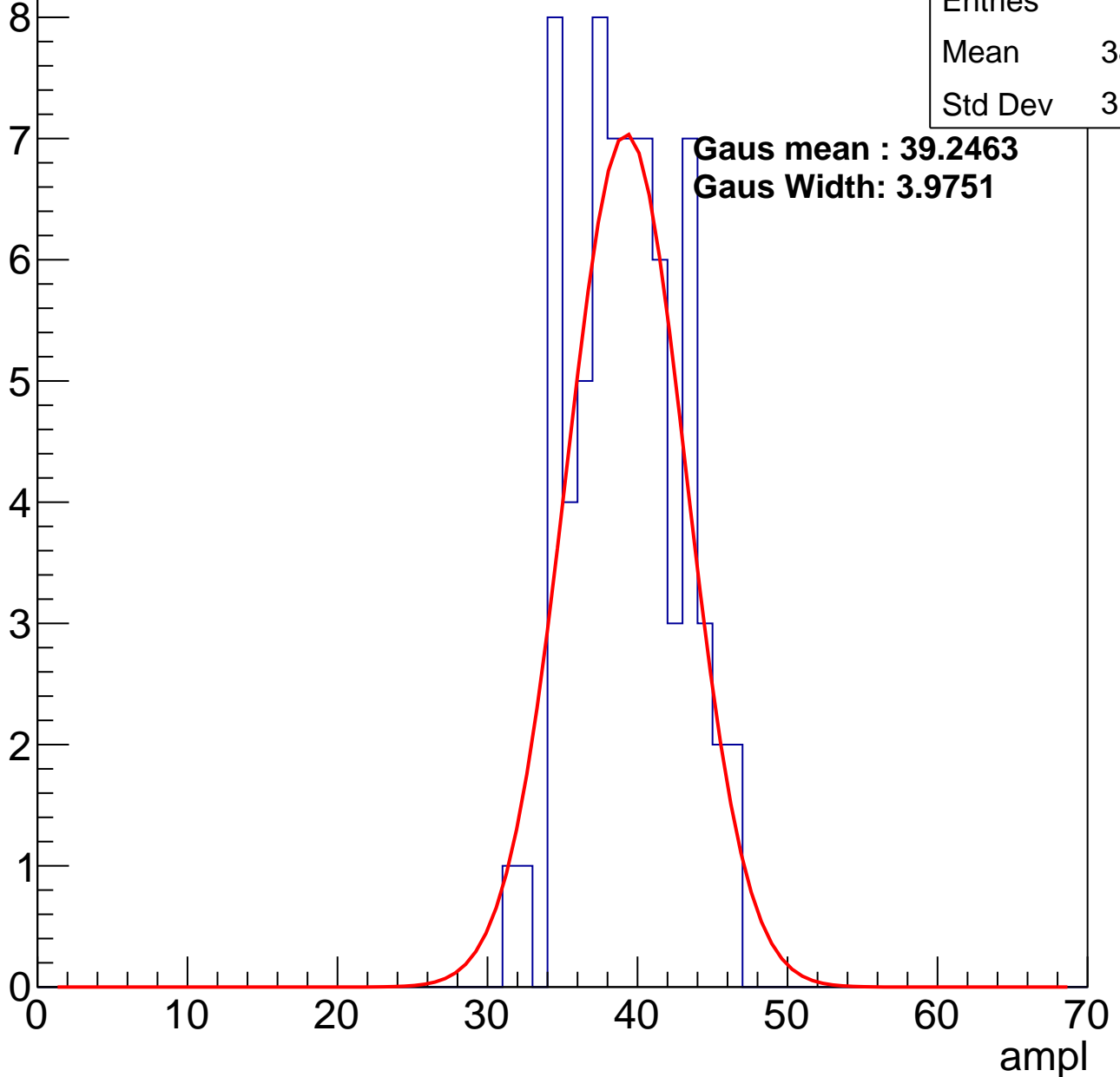
B0L002S, U12-ch96, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

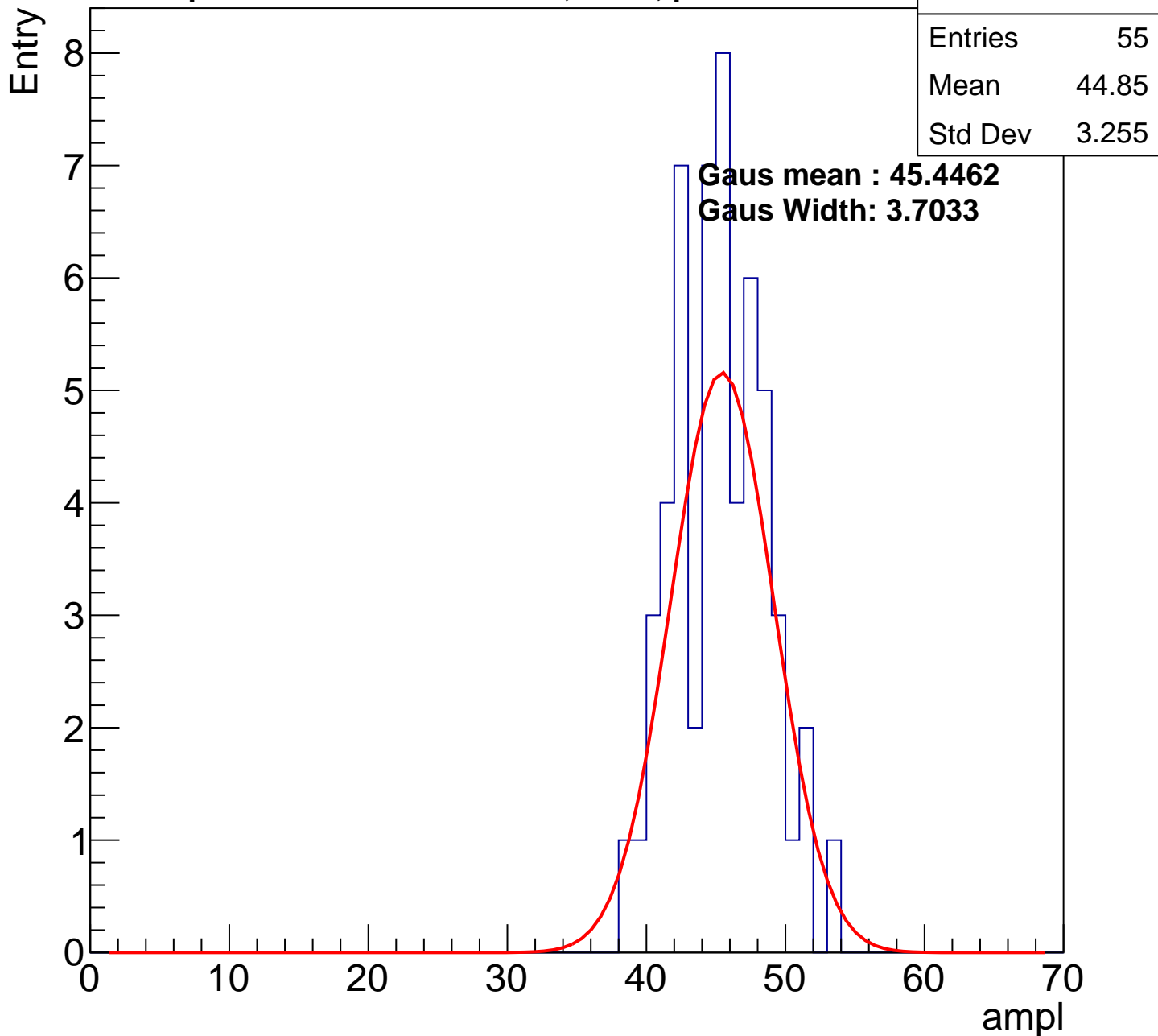
Entries	71
Mean	38.83
Std Dev	3.508

Gaus mean : 39.2463
Gaus Width: 3.9751



B0L002S, U12-ch96, adc2

calib_packv5_042523_0143.root, FC#8, port C1

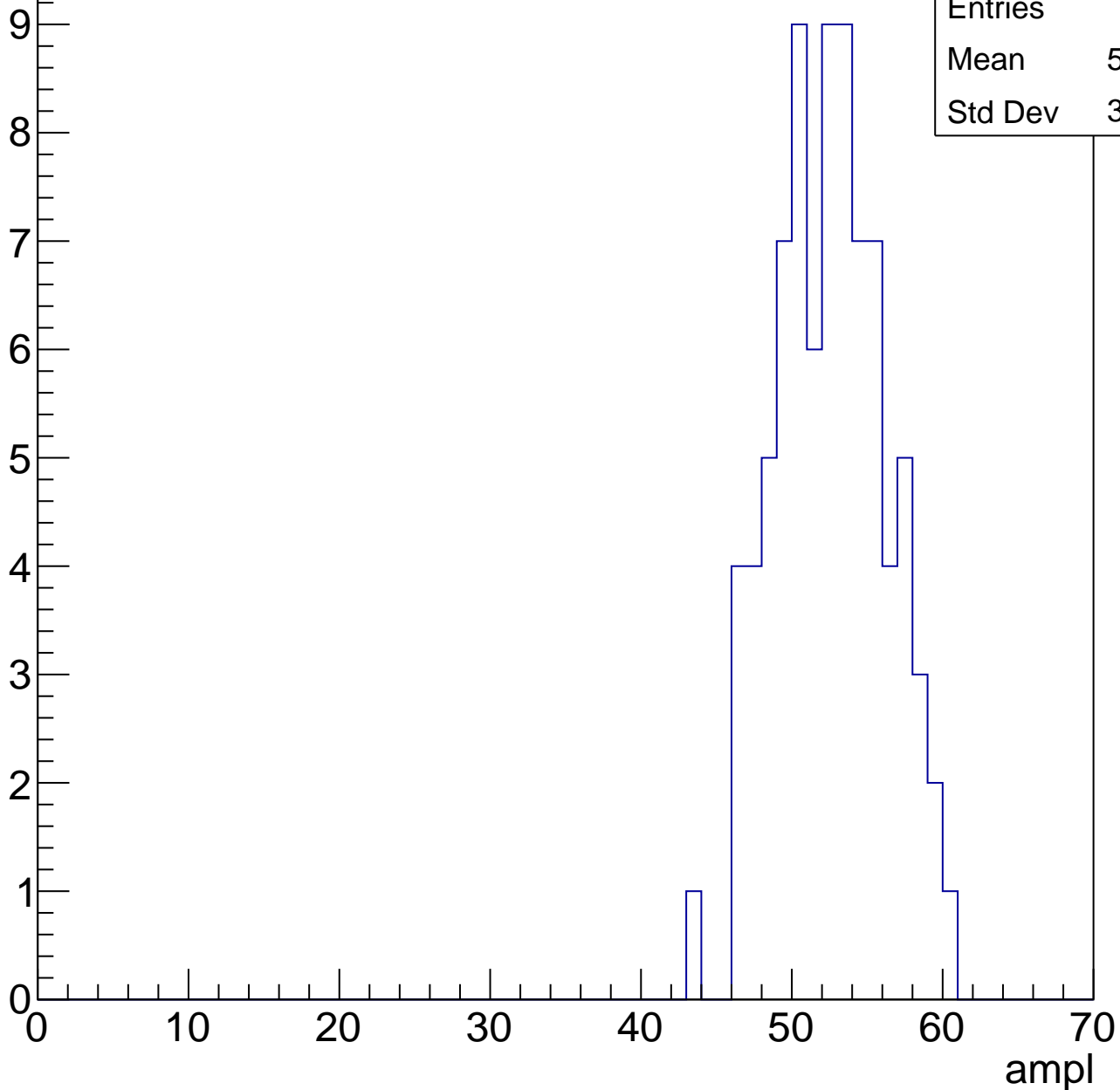


B0L002S, U12-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	52.08
Std Dev	3.588

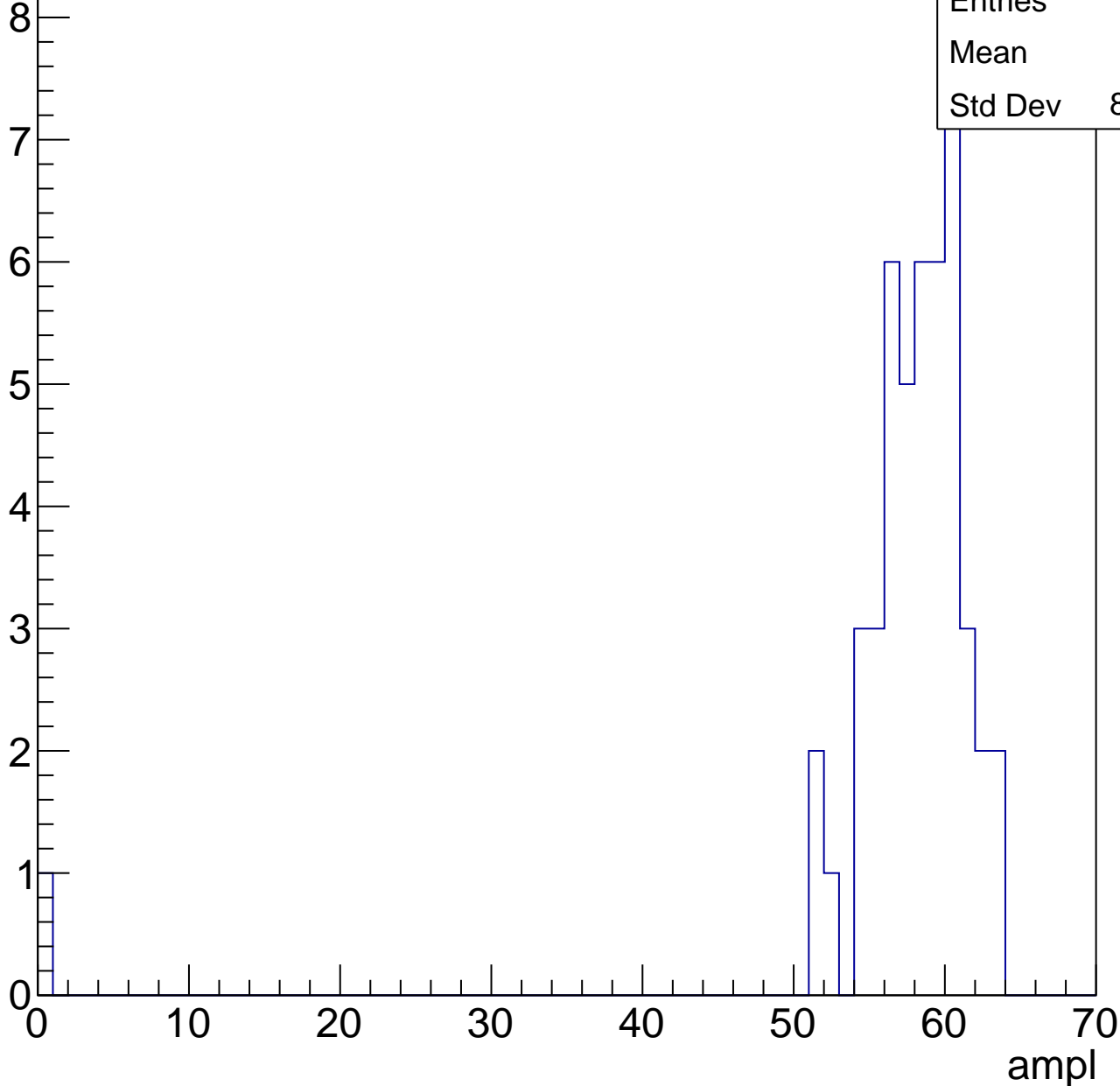


B0L002S, U12-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

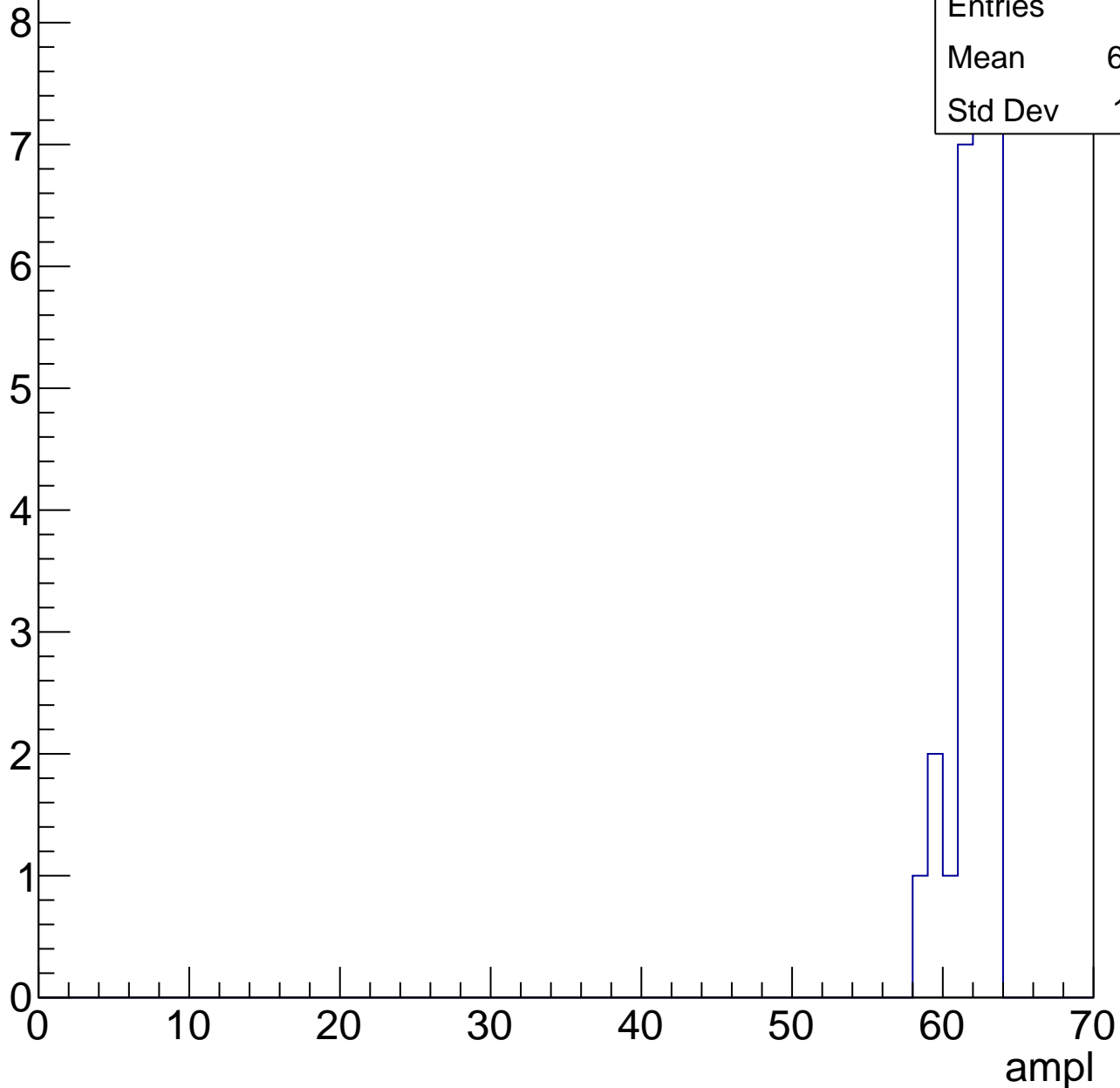
Entries	48
Mean	56.6
Std Dev	8.727



B0L002S, U12-ch96, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch97, adc0

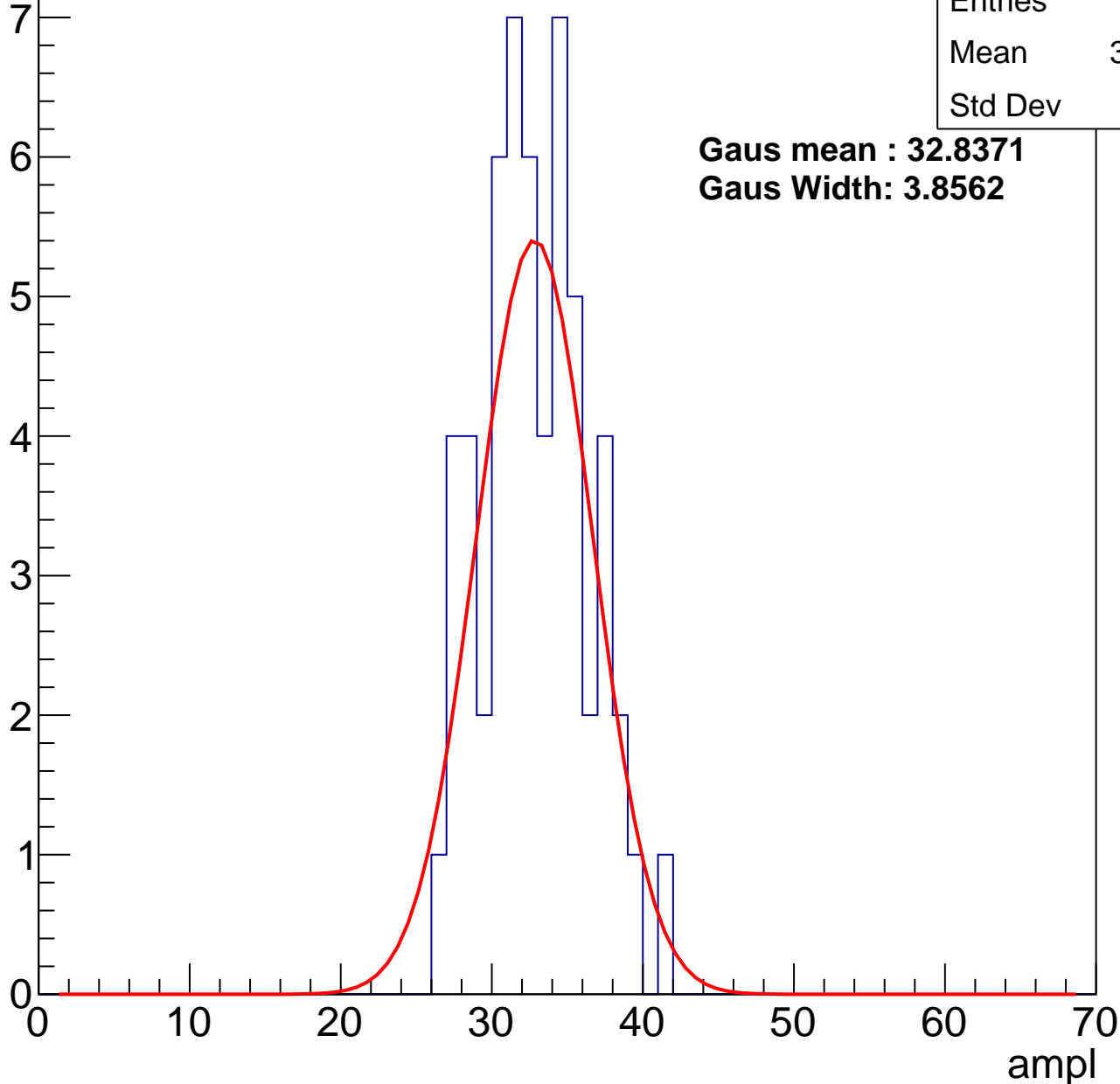
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	32.39
Std Dev	3.41

Gaus mean : 32.8371

Gaus Width: 3.8562



B0L002S, U12-ch97, adc1

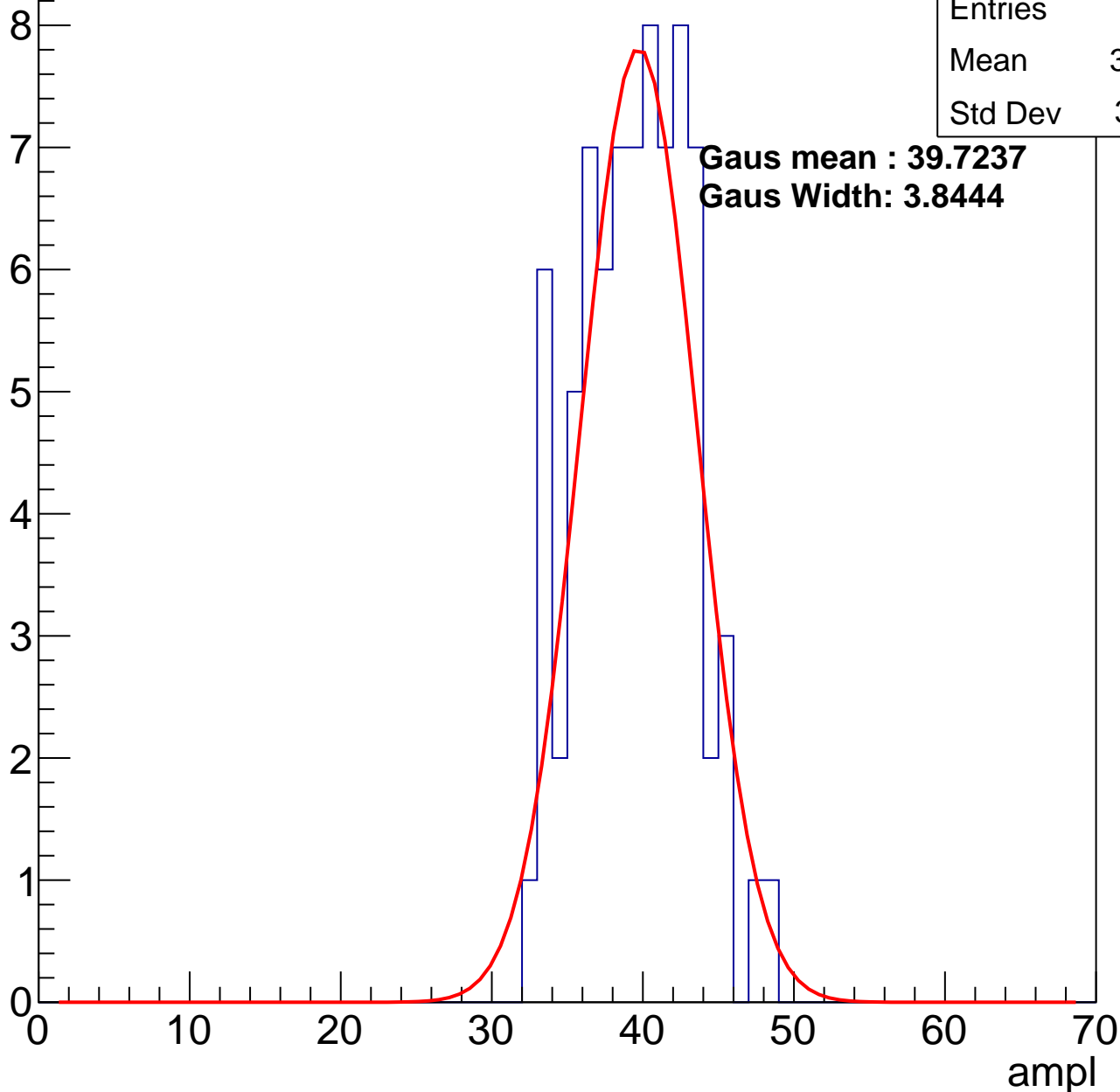
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	39.08
Std Dev	3.601

Gaus mean : 39.7237

Gaus Width: 3.8444



B0L002S, U12-ch97, adc2

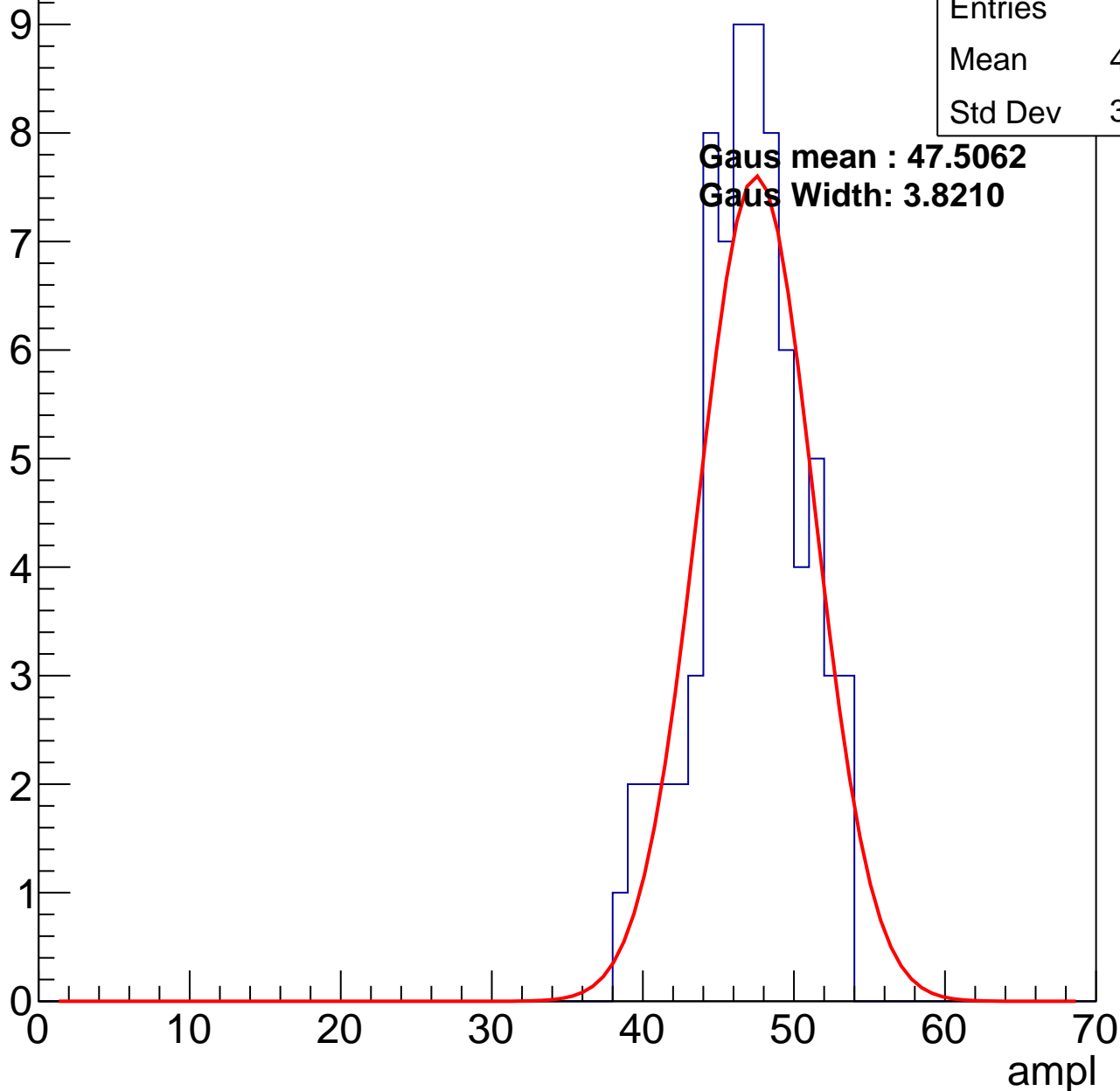
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	46.53
Std Dev	3.488

Gaus mean : 47.5062

Gaus Width: 3.8210

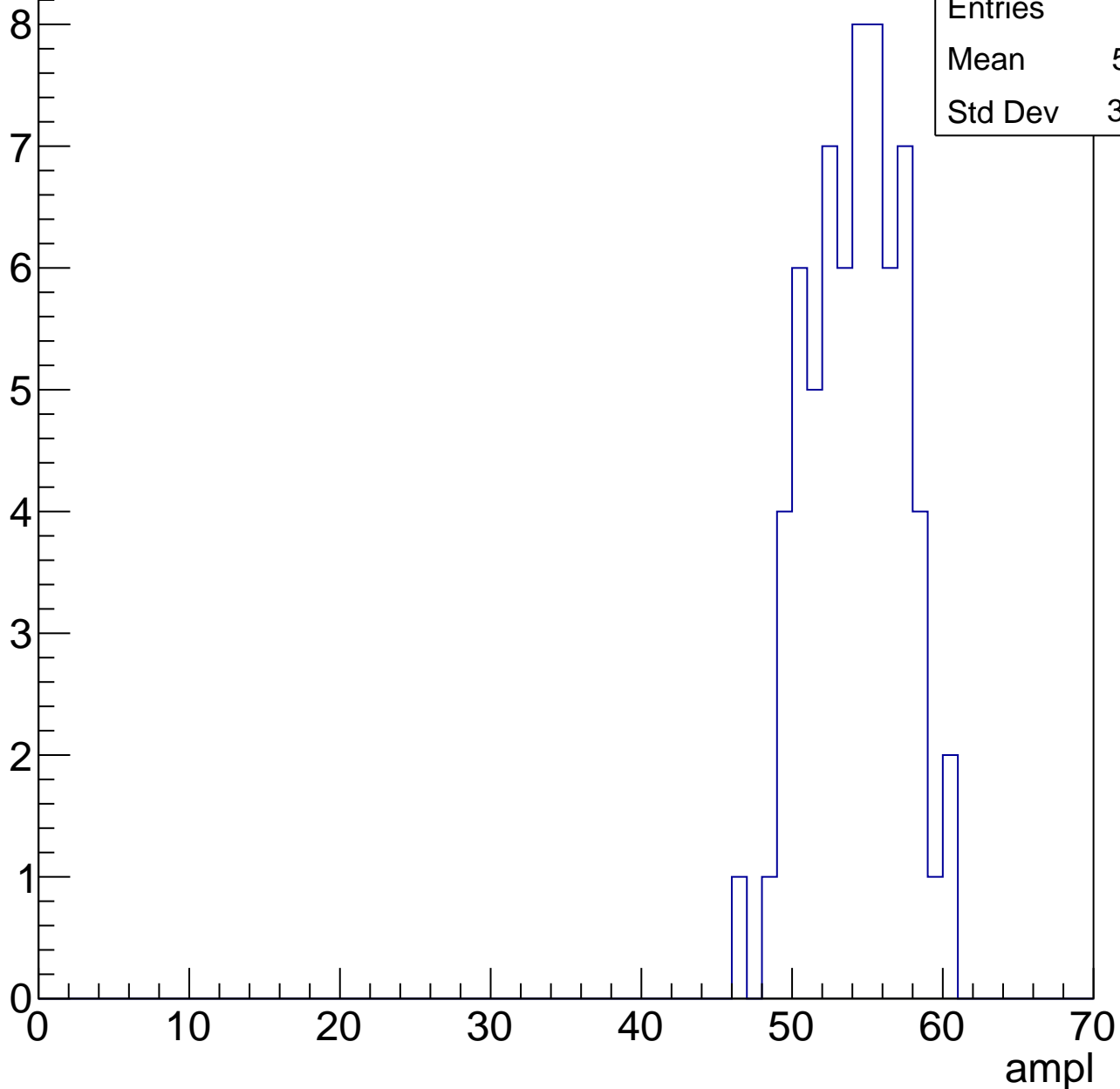


B0L002S, U12-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

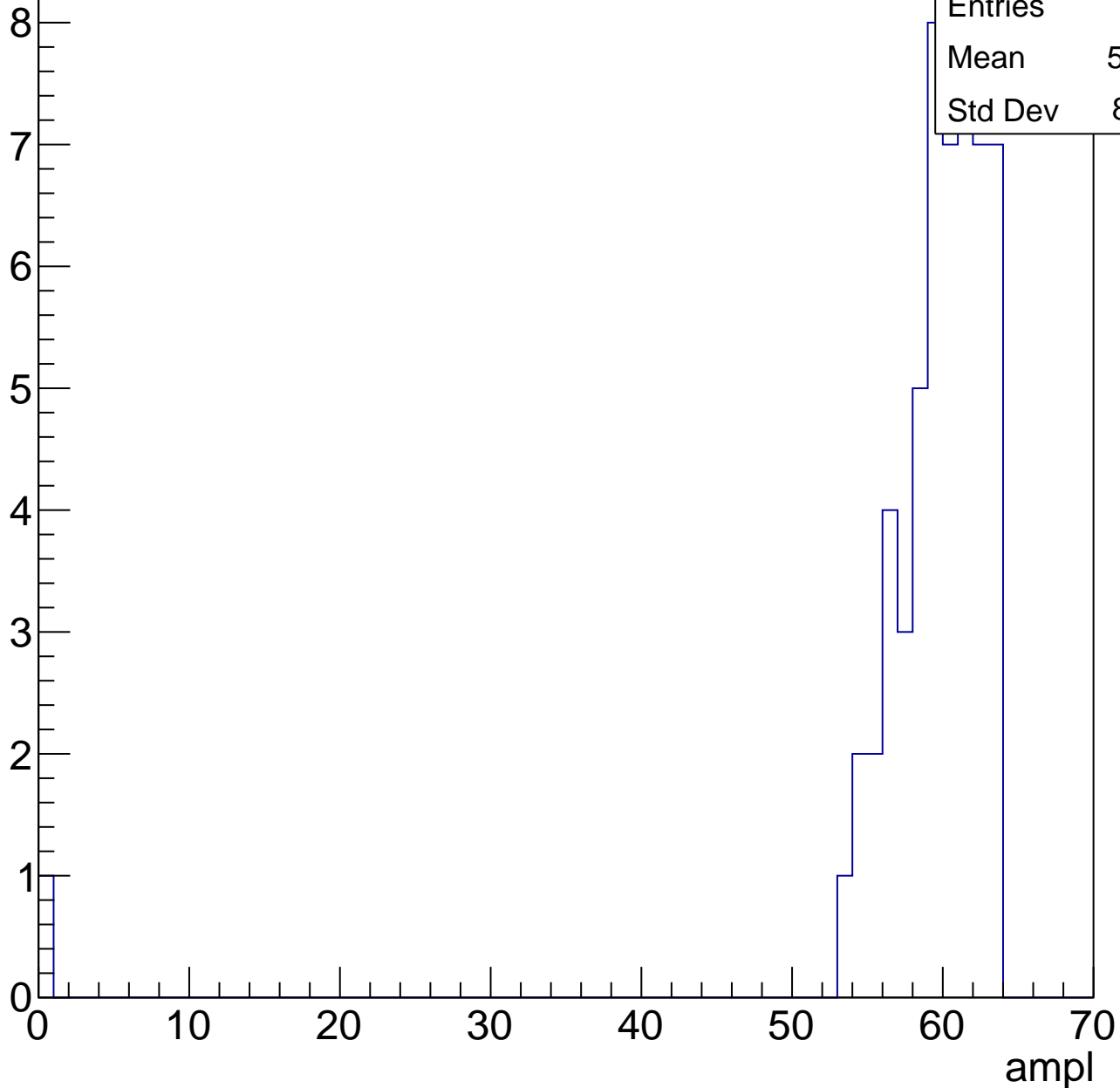
Entries	66
Mean	53.71
Std Dev	3.079



B0L002S, U12-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

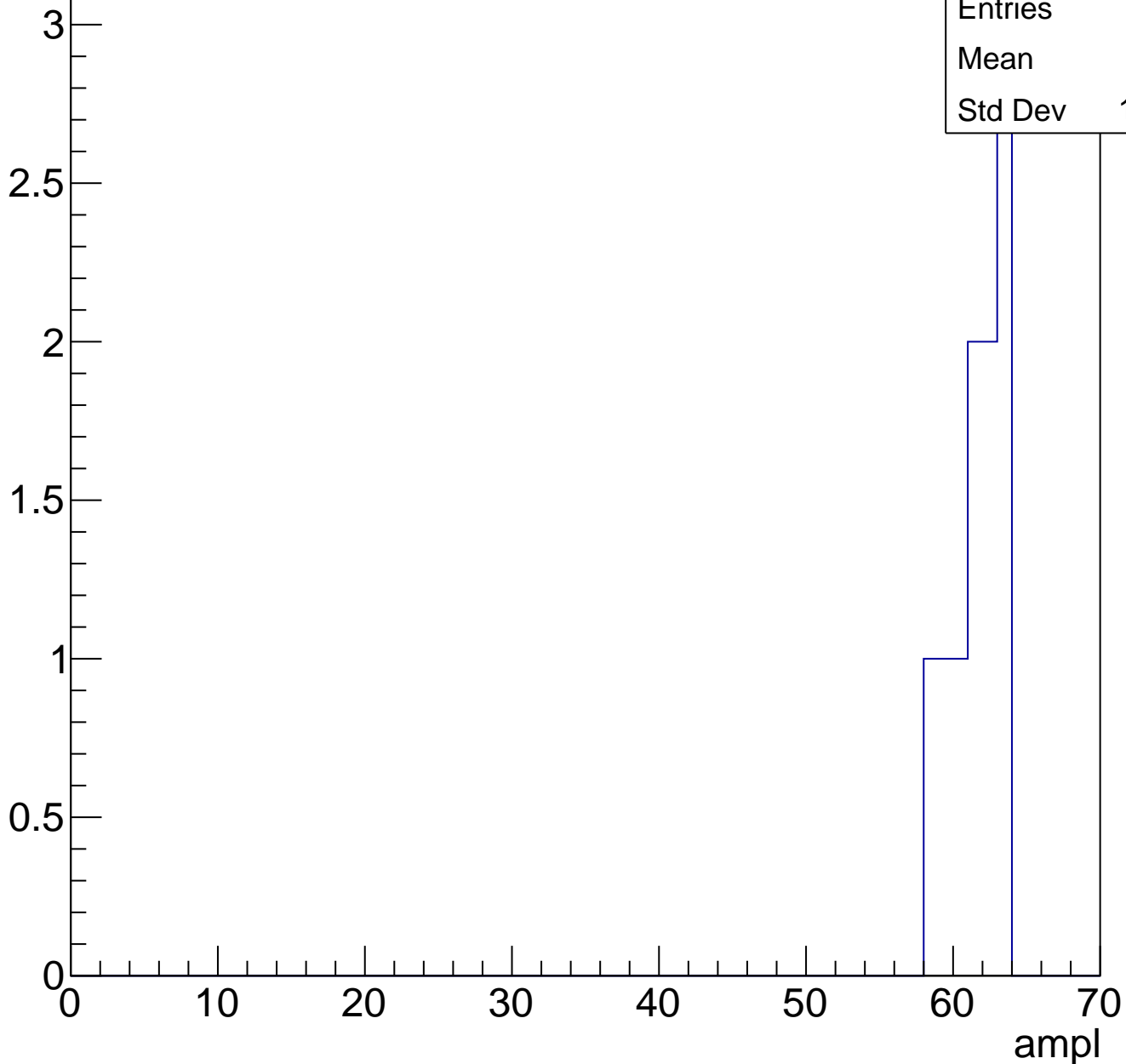


Entries	55
Mean	58.38
Std Dev	8.361

B0L002S, U12-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U12-ch98, adc0

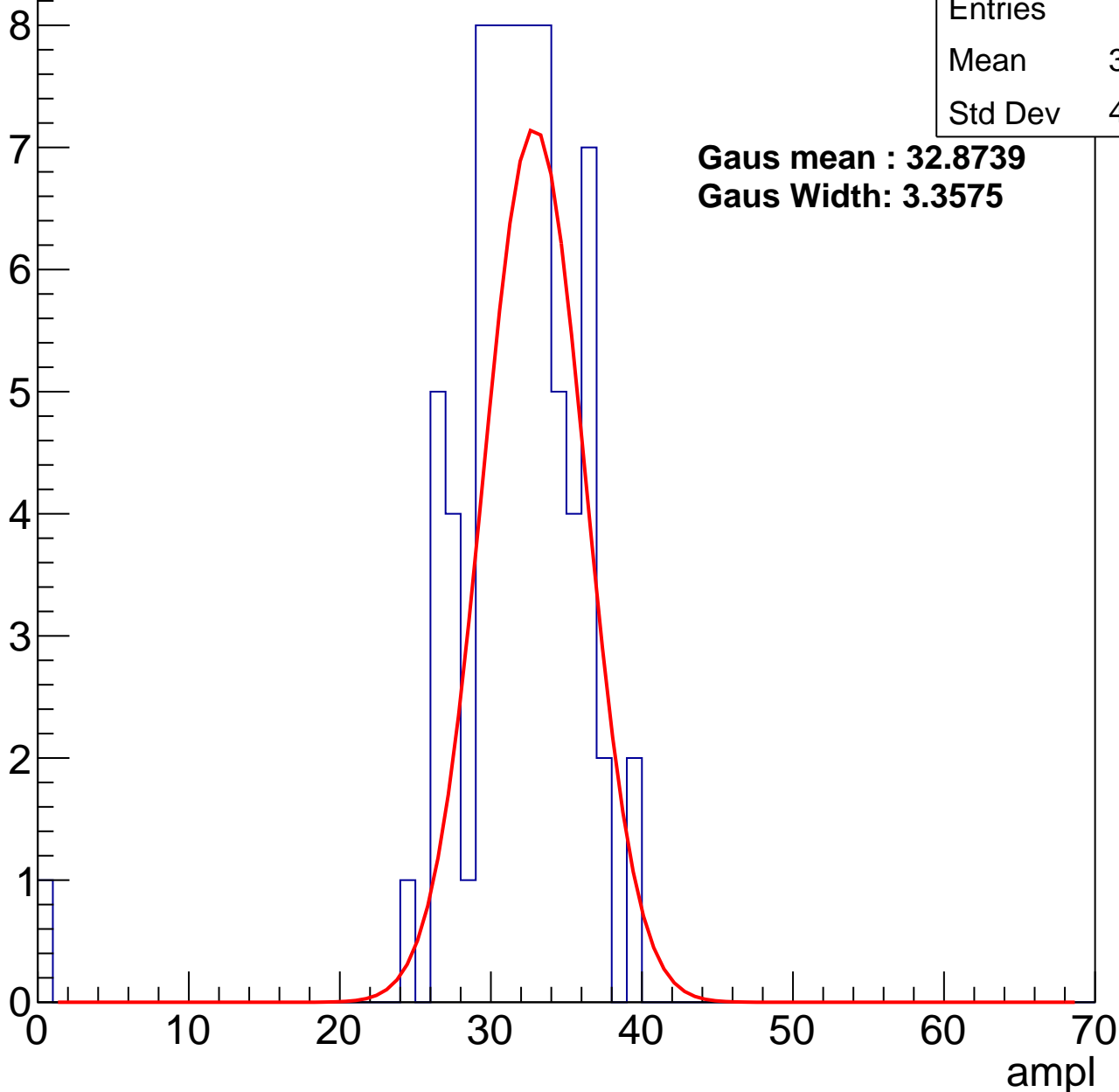
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	31.17
Std Dev	4.958

Gaus mean : 32.8739

Gaus Width: 3.3575



B0L002S, U12-ch98, adc1

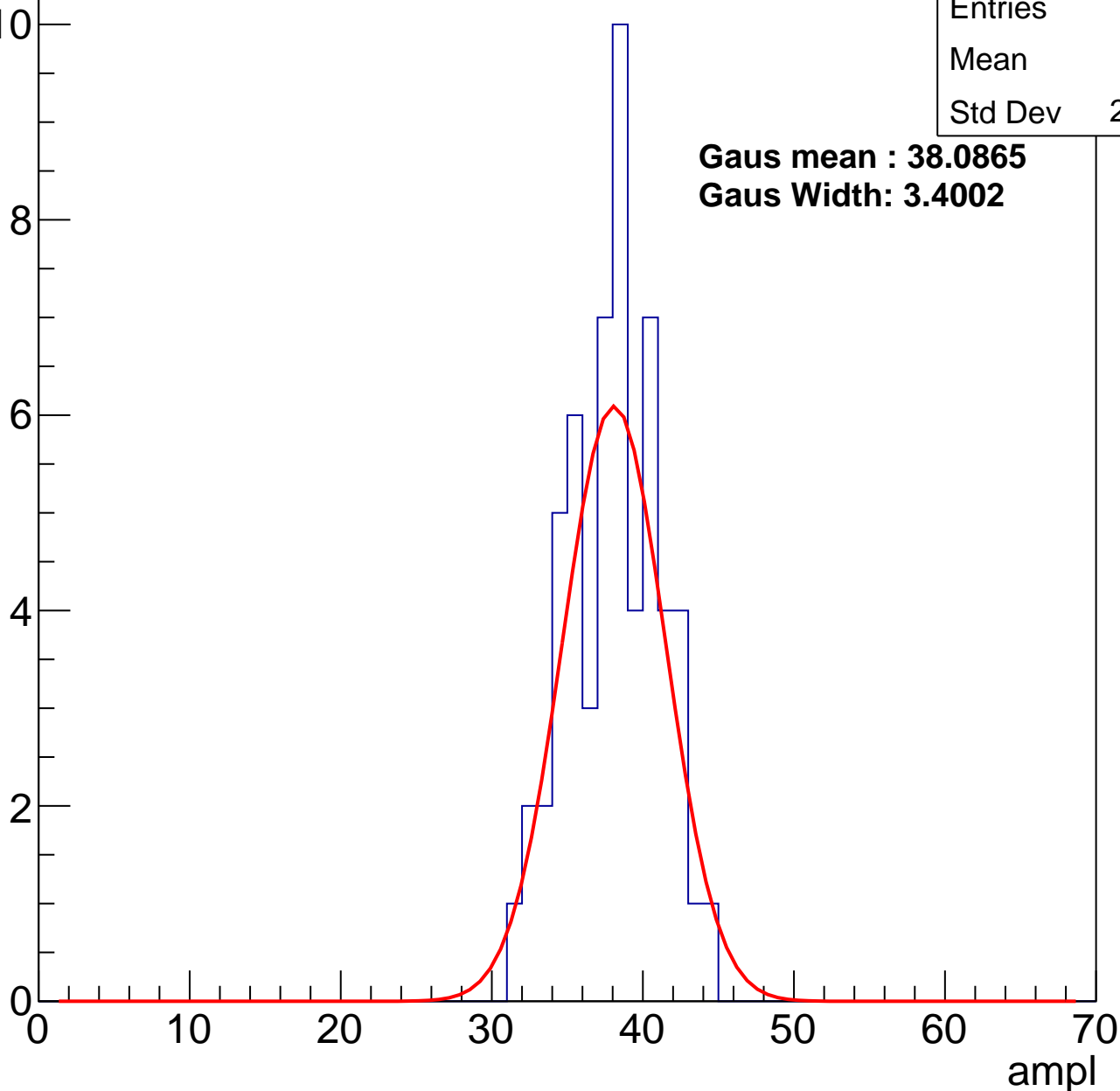
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	37.6
Std Dev	2.985

Gaus mean : 38.0865

Gaus Width: 3.4002



B0L002S, U12-ch98, adc2

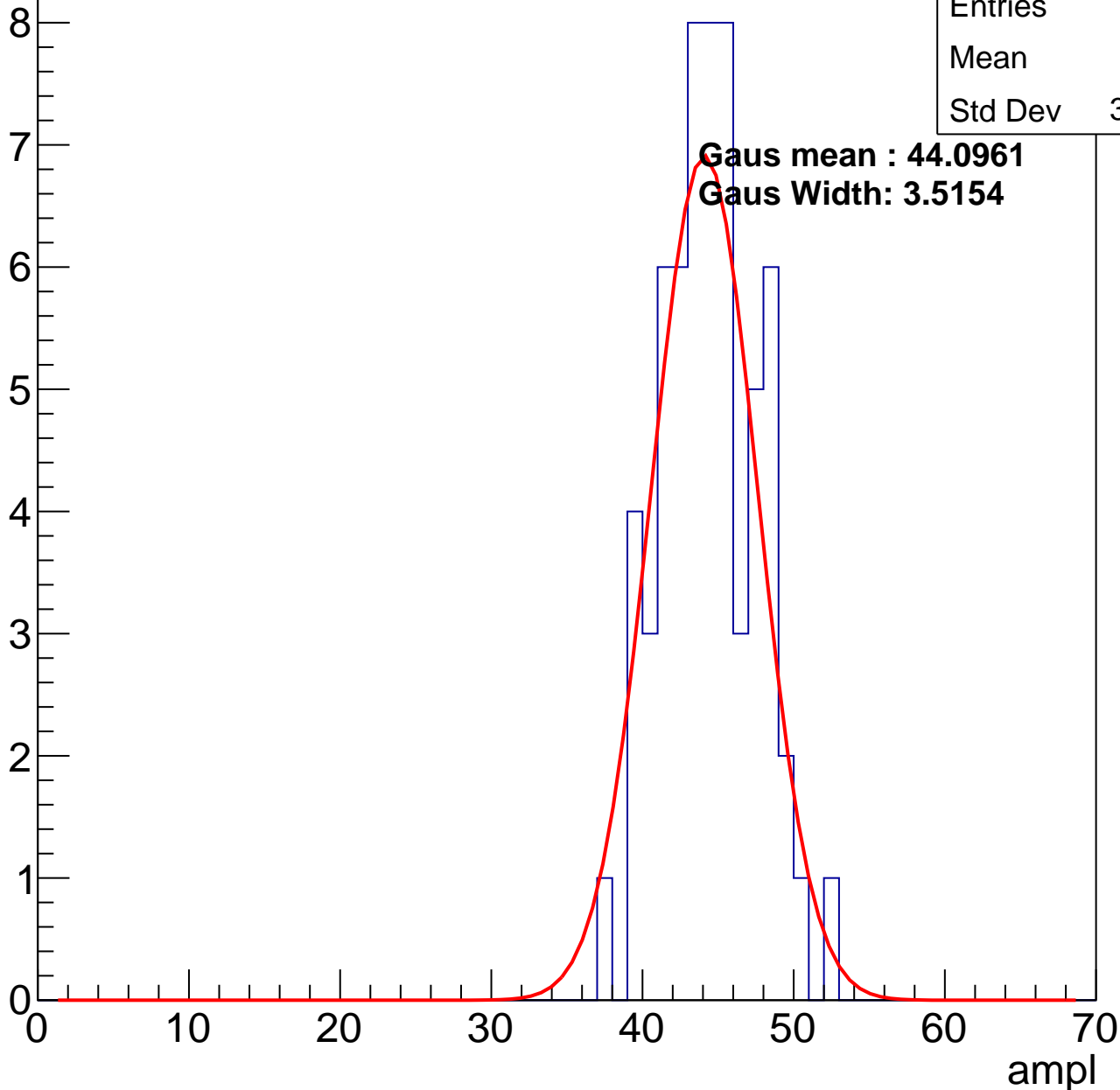
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44
Std Dev	3.095

Gaus mean : 44.0961

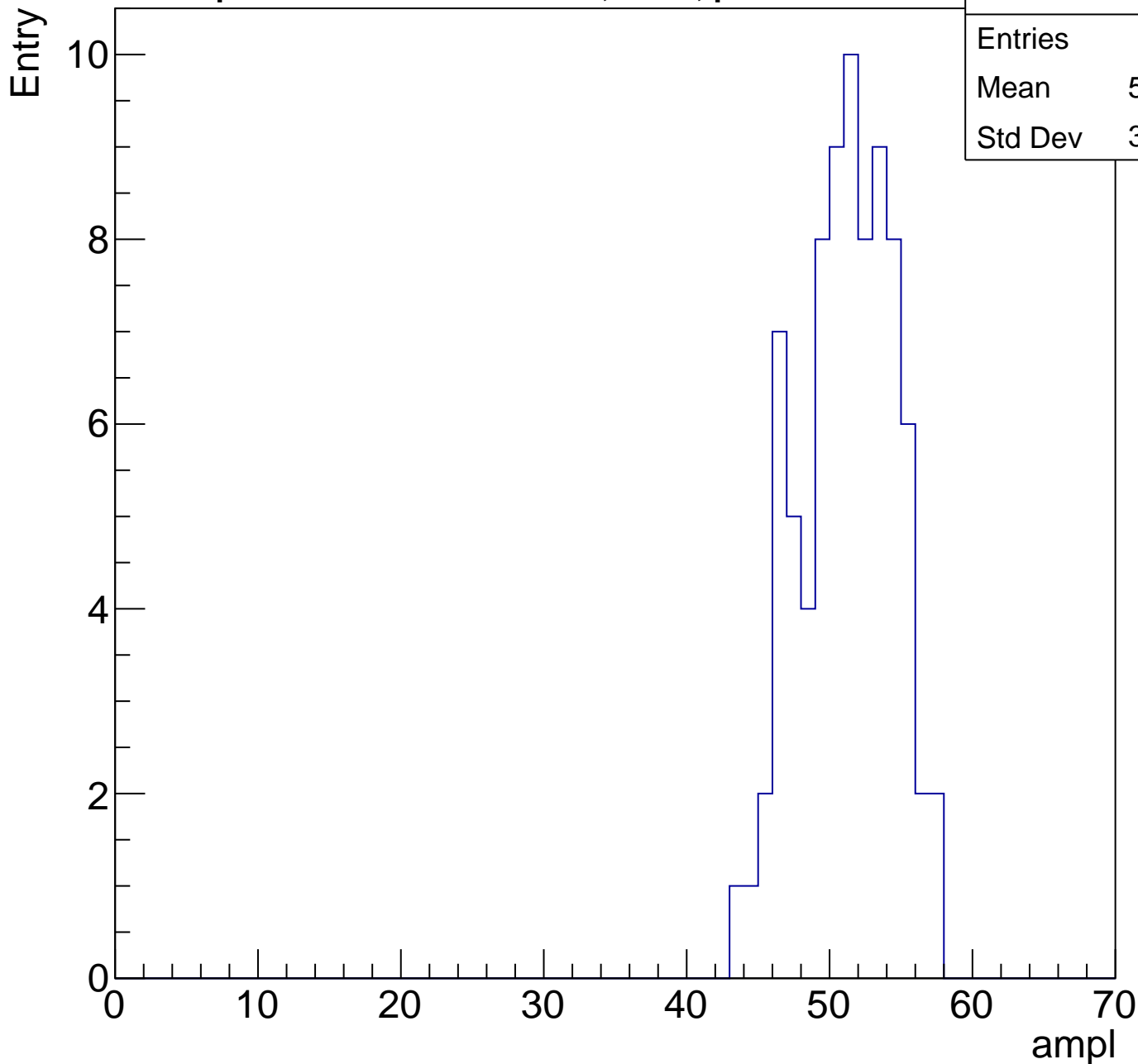
Gaus Width: 3.5154



B0L002S, U12-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	50.72
Std Dev	3.209



B0L002S, U12-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	50
Mean	57.96
Std Dev	2.441

Entry

10

8

6

4

2

0

0

10

20

30

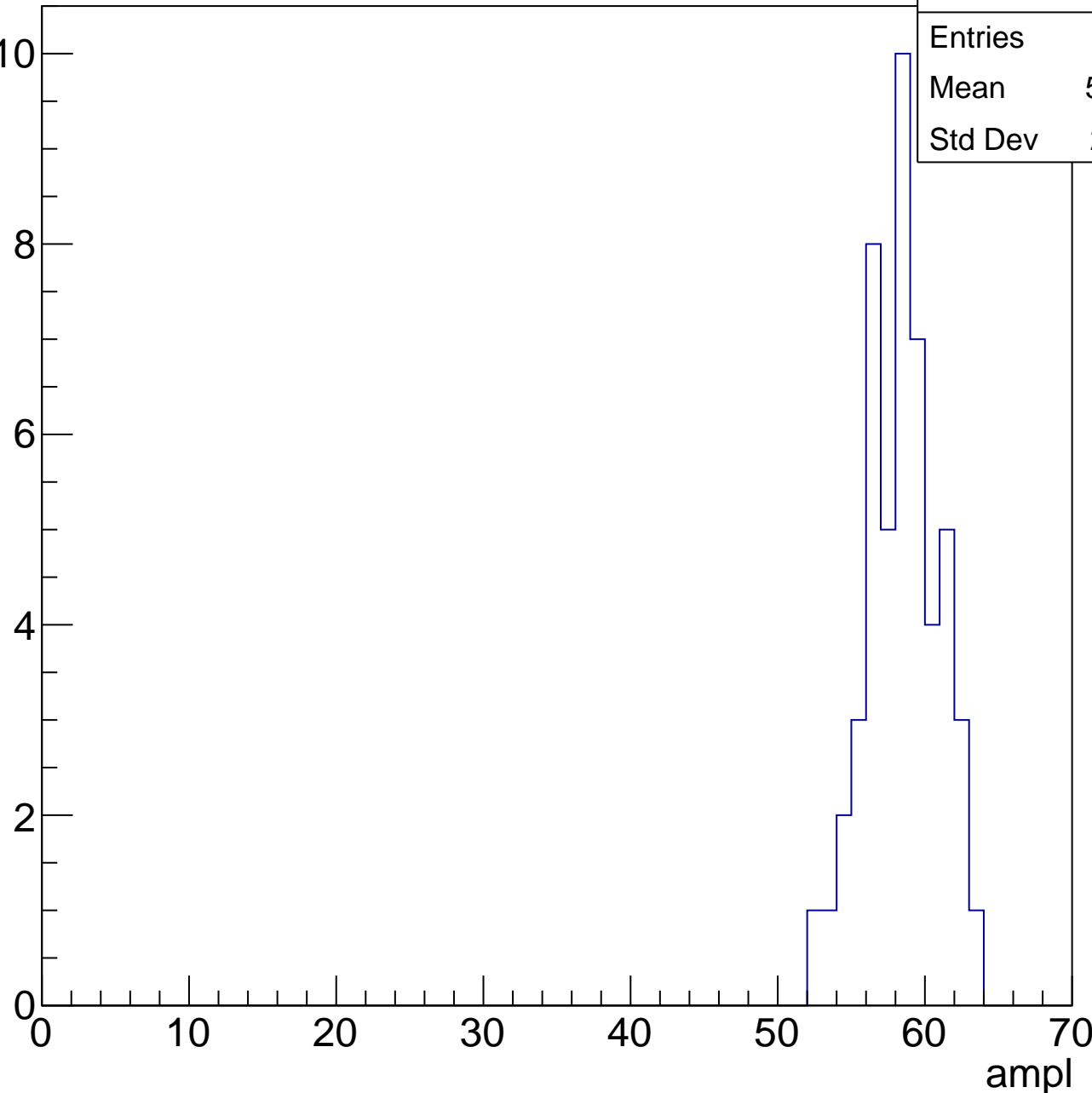
40

50

60

70

ampl

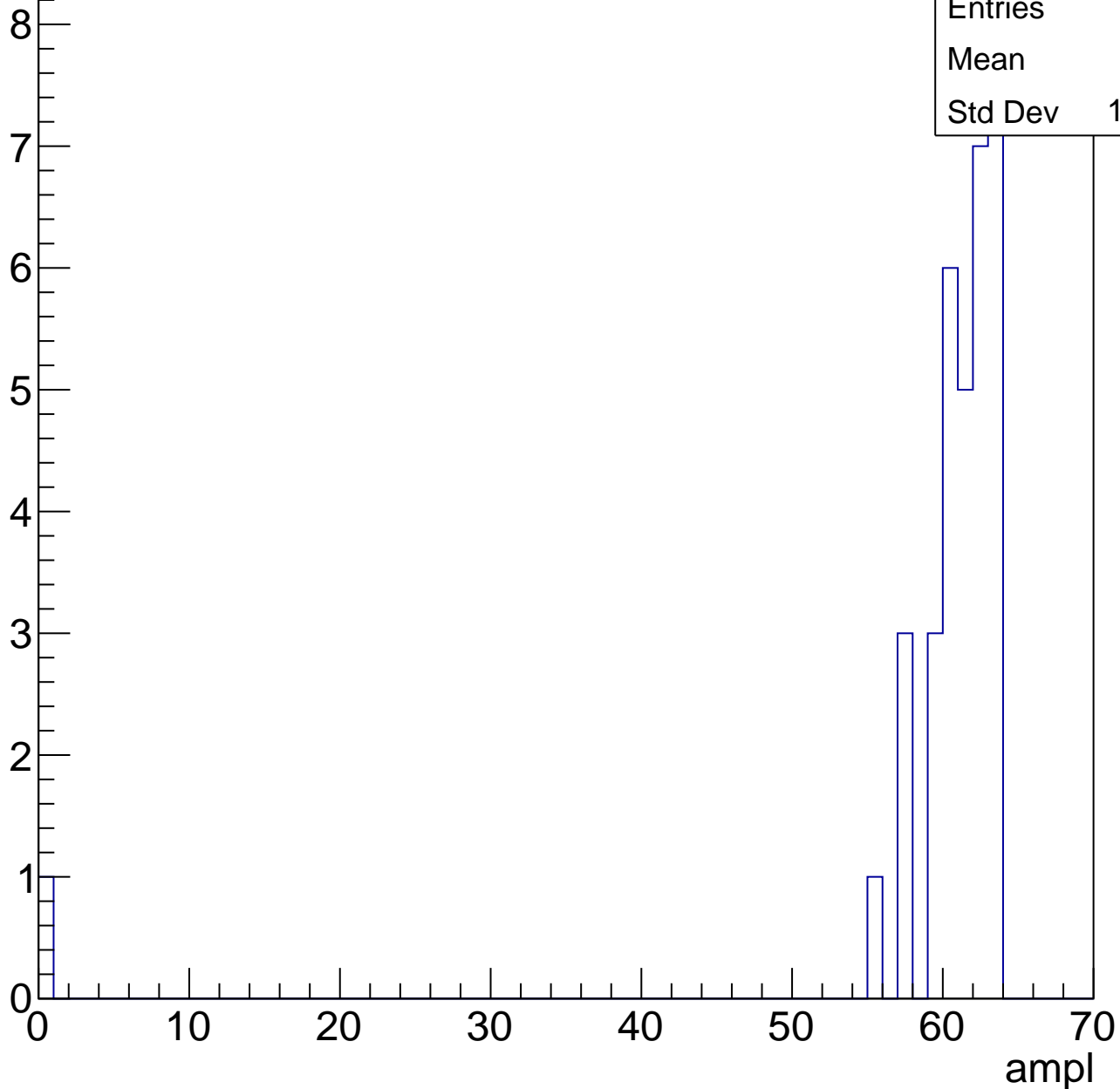


B0L002S, U12-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	59
Std Dev	10.47



B0L002S, U12-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch99, adc0

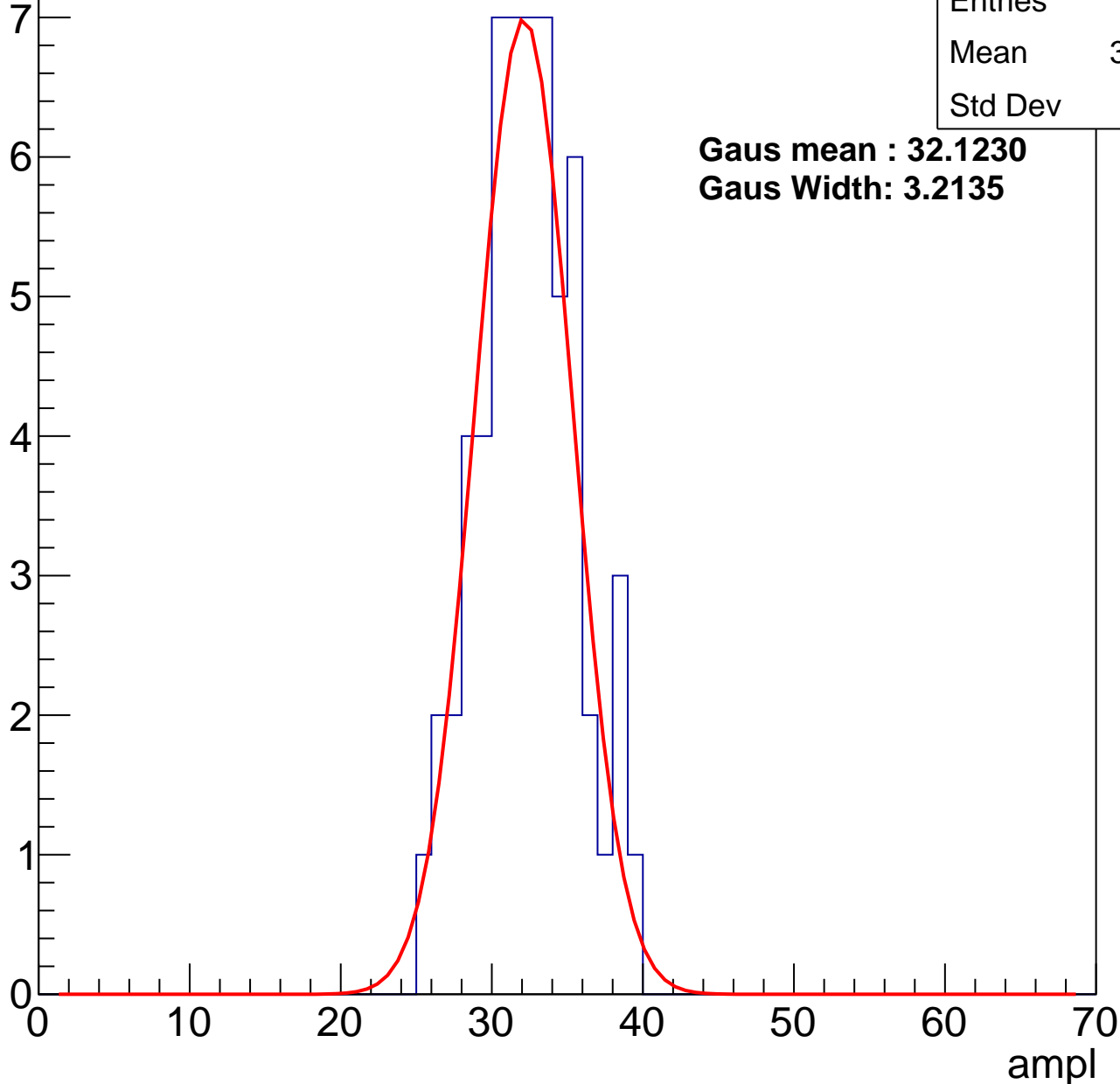
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	31.92
Std Dev	3.19

Gaus mean : 32.1230

Gaus Width: 3.2135



B0L002S, U12-ch99, adc1

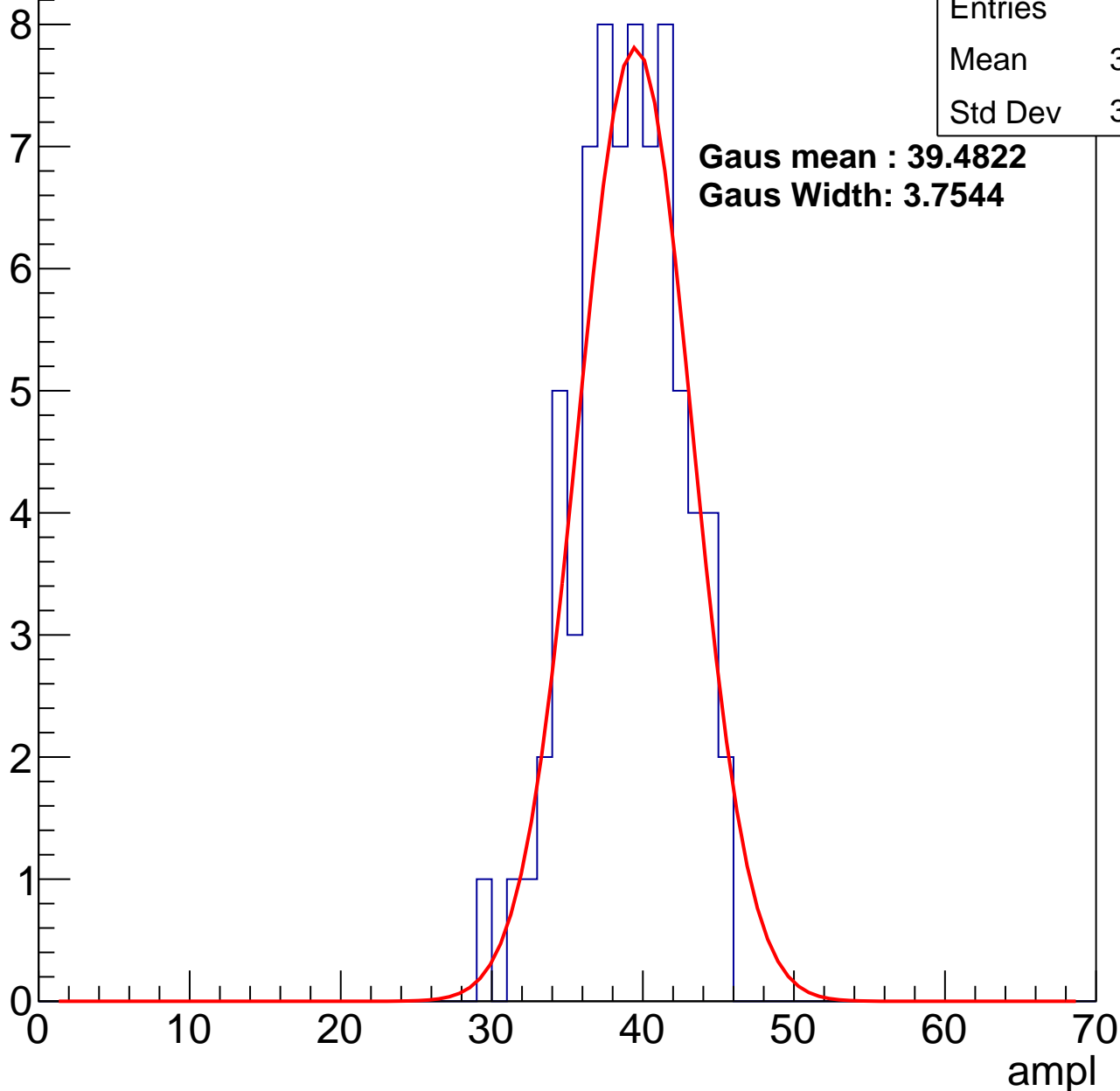
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	38.56
Std Dev	3.444

Gaus mean : 39.4822

Gaus Width: 3.7544

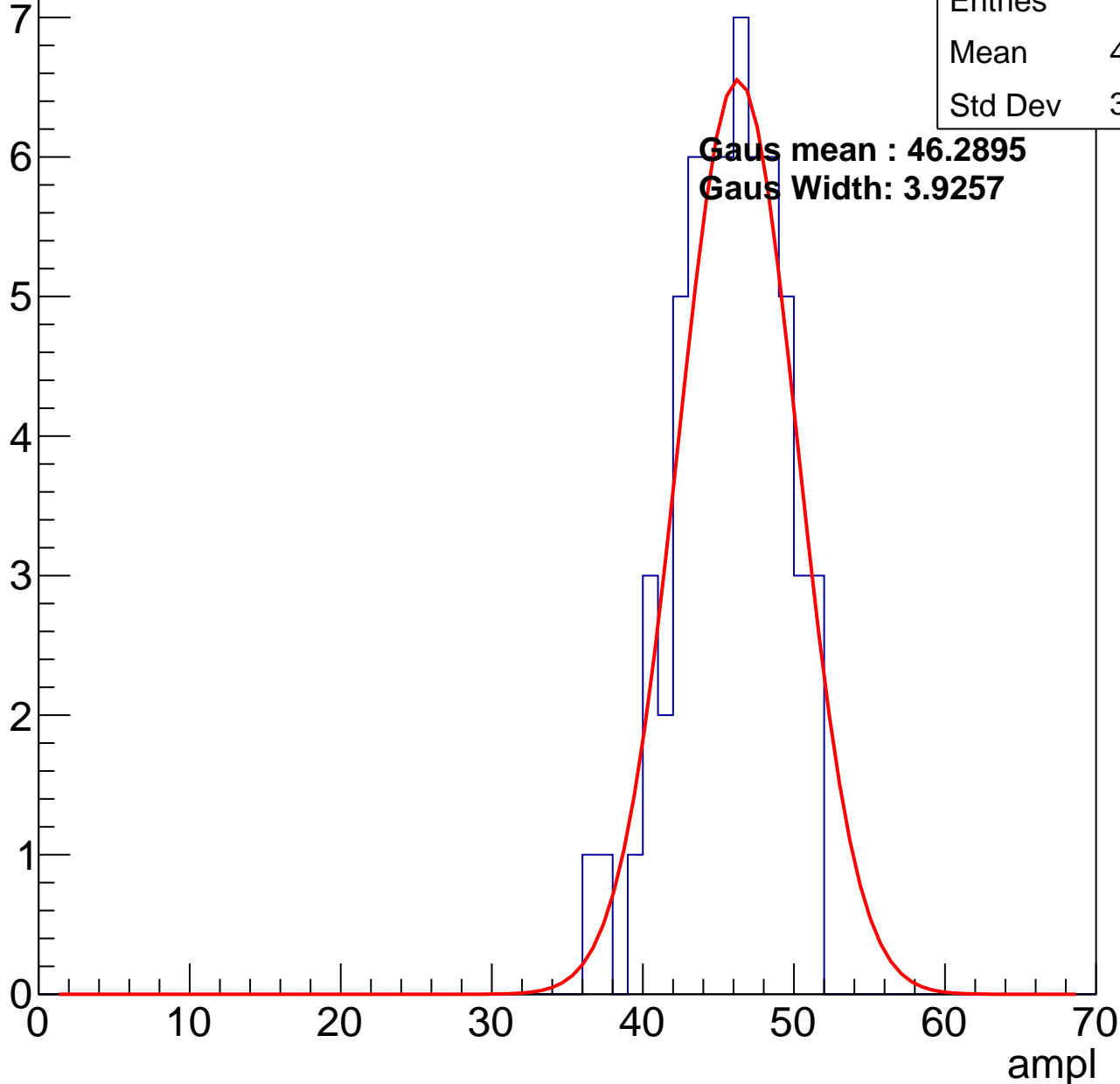


B0L002S, U12-ch99, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

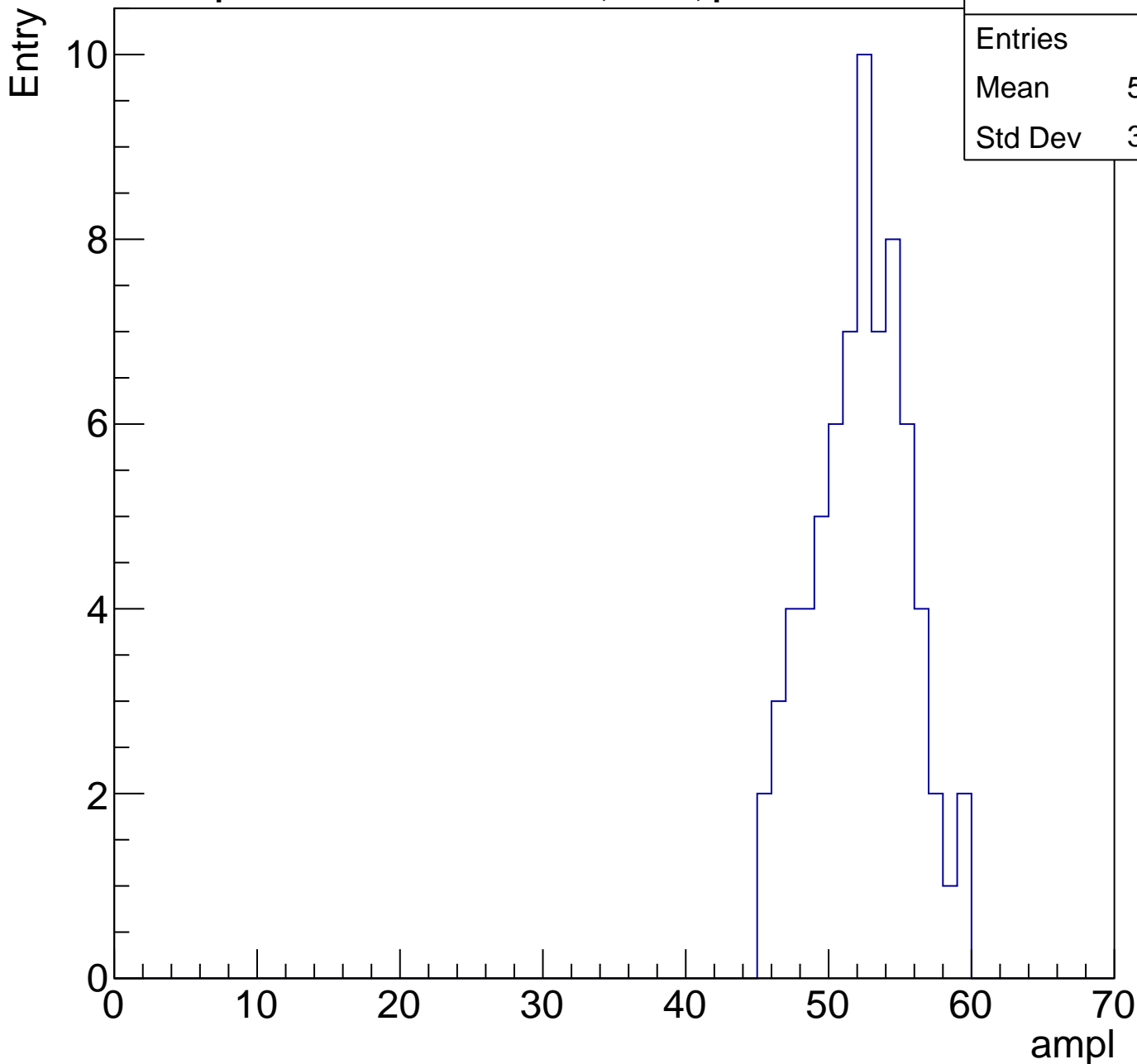
Entries	61
Mean	45.18
Std Dev	3.409



B0L002S, U12-ch99, adc3

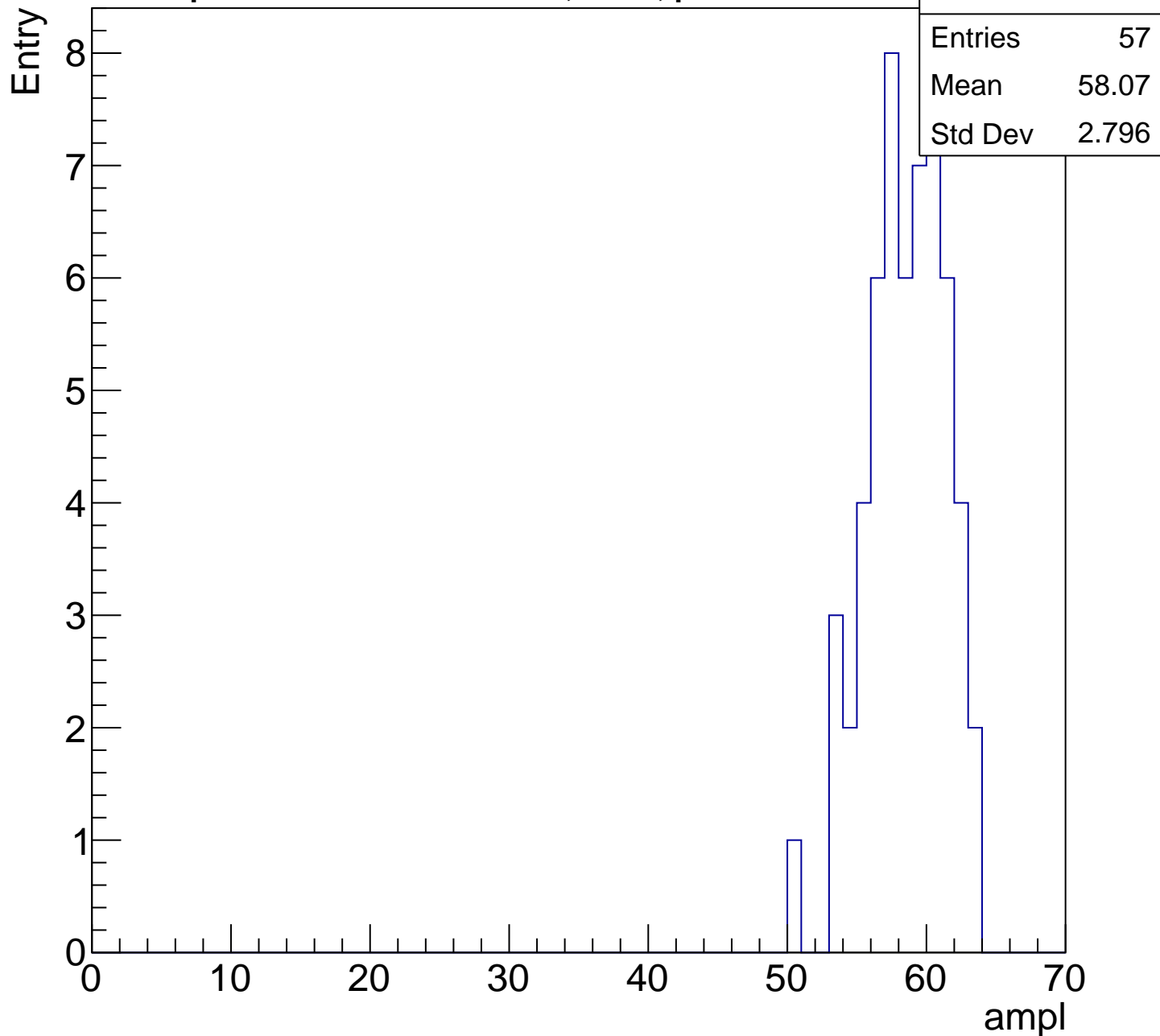
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	51.79
Std Dev	3.323



B0L002S, U12-ch99, adc4

calib_packv5_042523_0143.root, FC#8, port C1

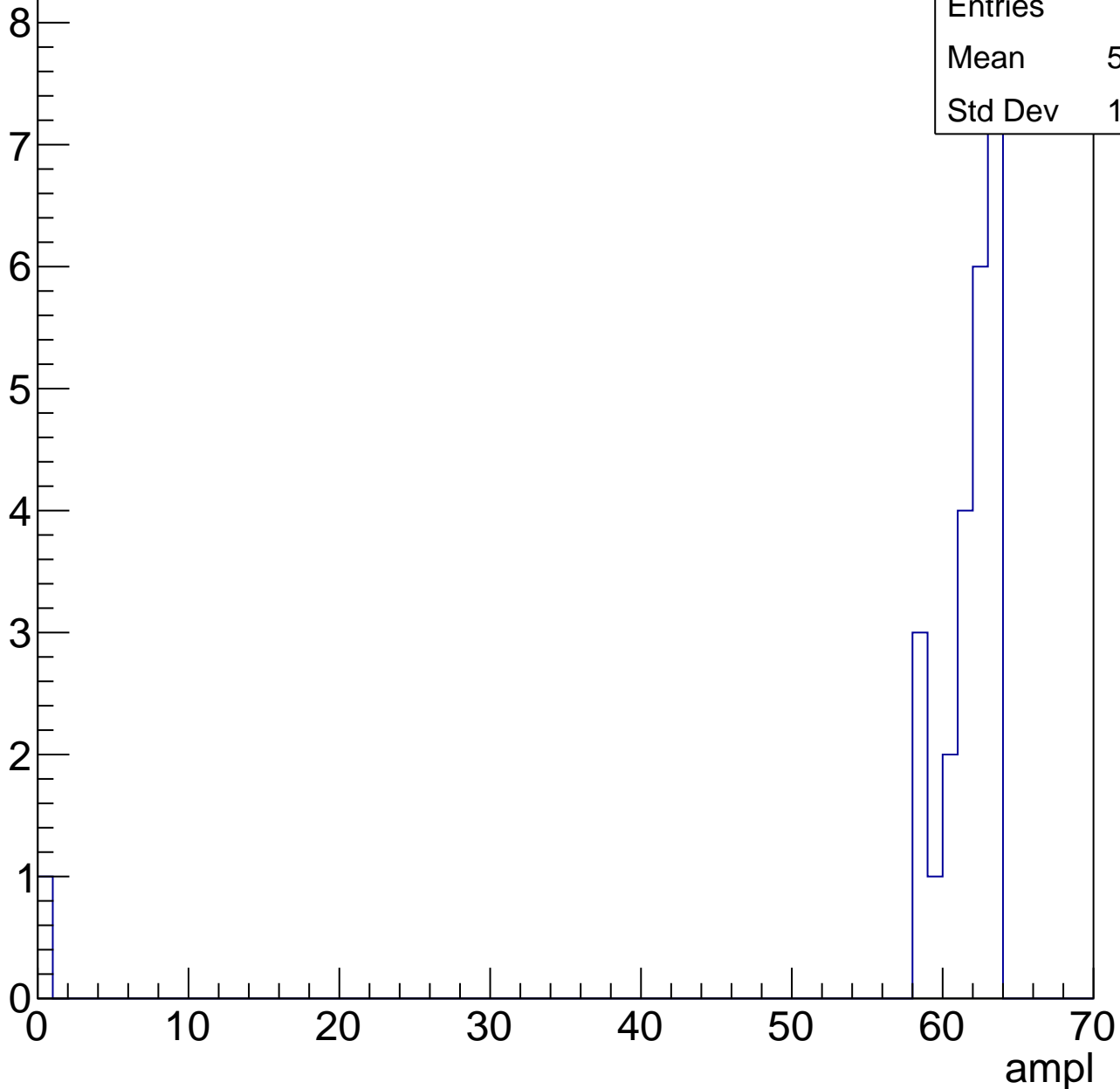


B0L002S, U12-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.92
Std Dev	12.14



B0L002S, U12-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

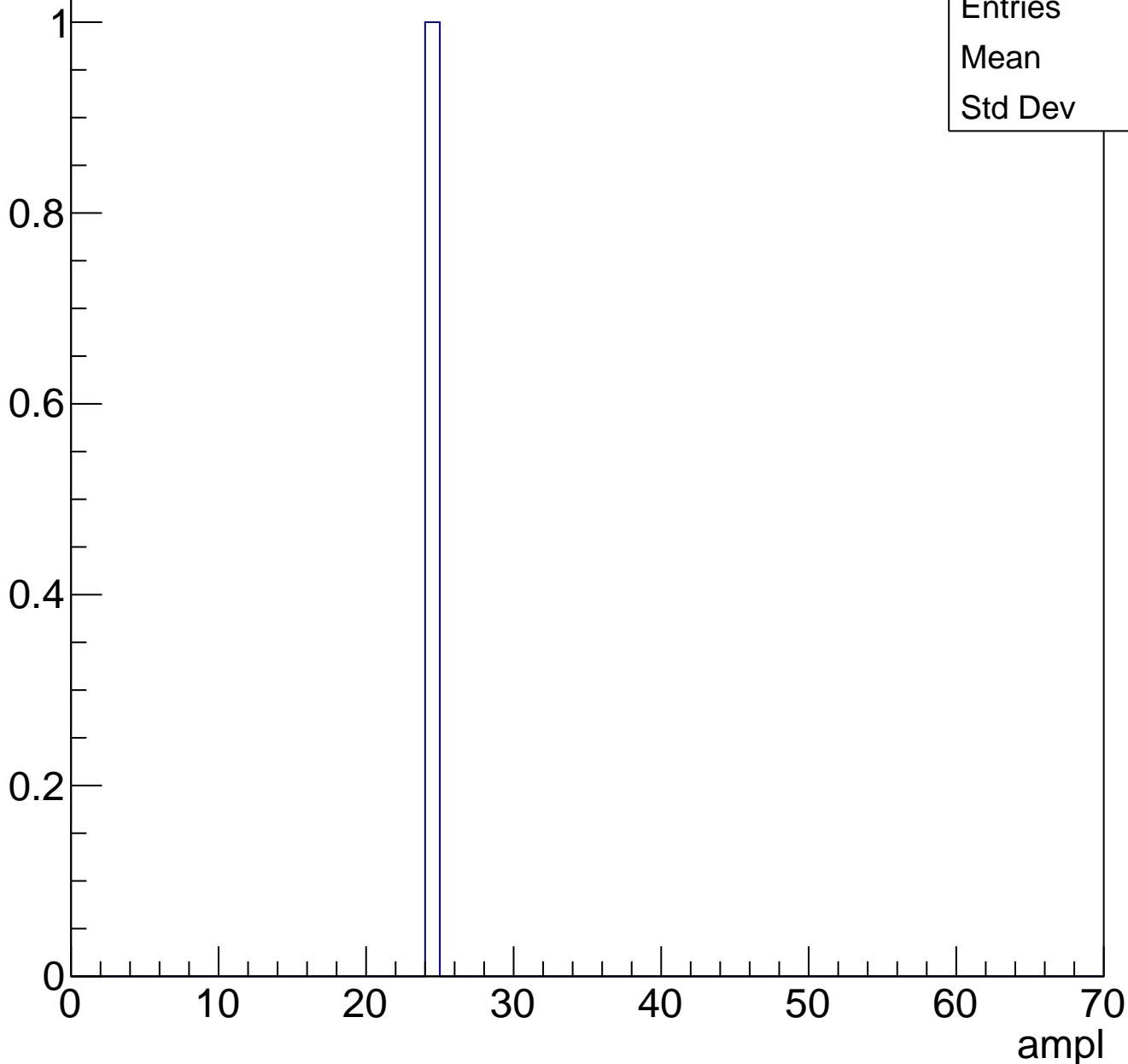


Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch100, adc0

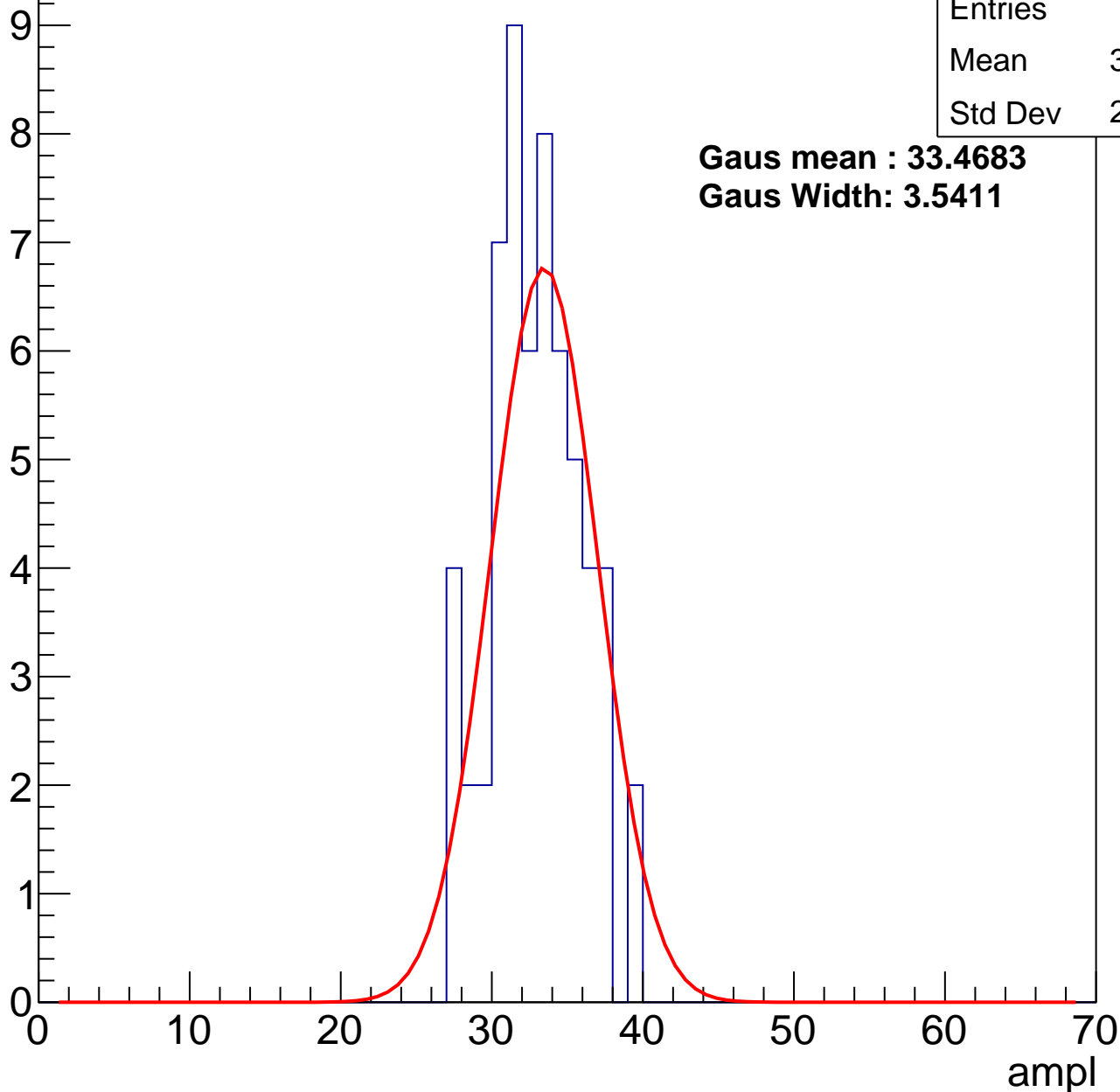
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	32.47
Std Dev	2.948

Gaus mean : 33.4683

Gaus Width: 3.5411



B0L002S, U12-ch100, adc1

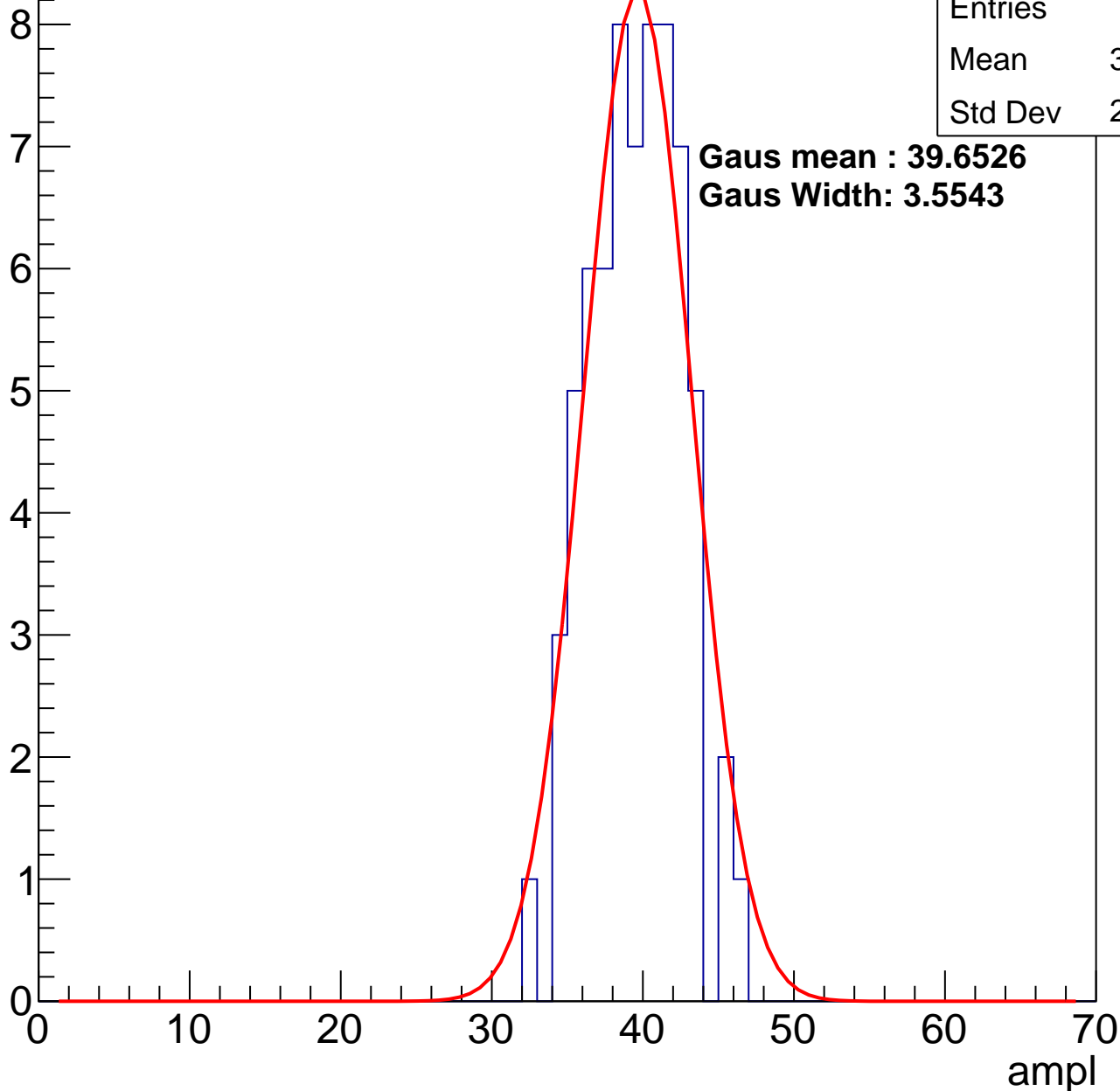
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	39.06
Std Dev	2.977

Gaus mean : 39.6526

Gaus Width: 3.5543



B0L002S, U12-ch100, adc2

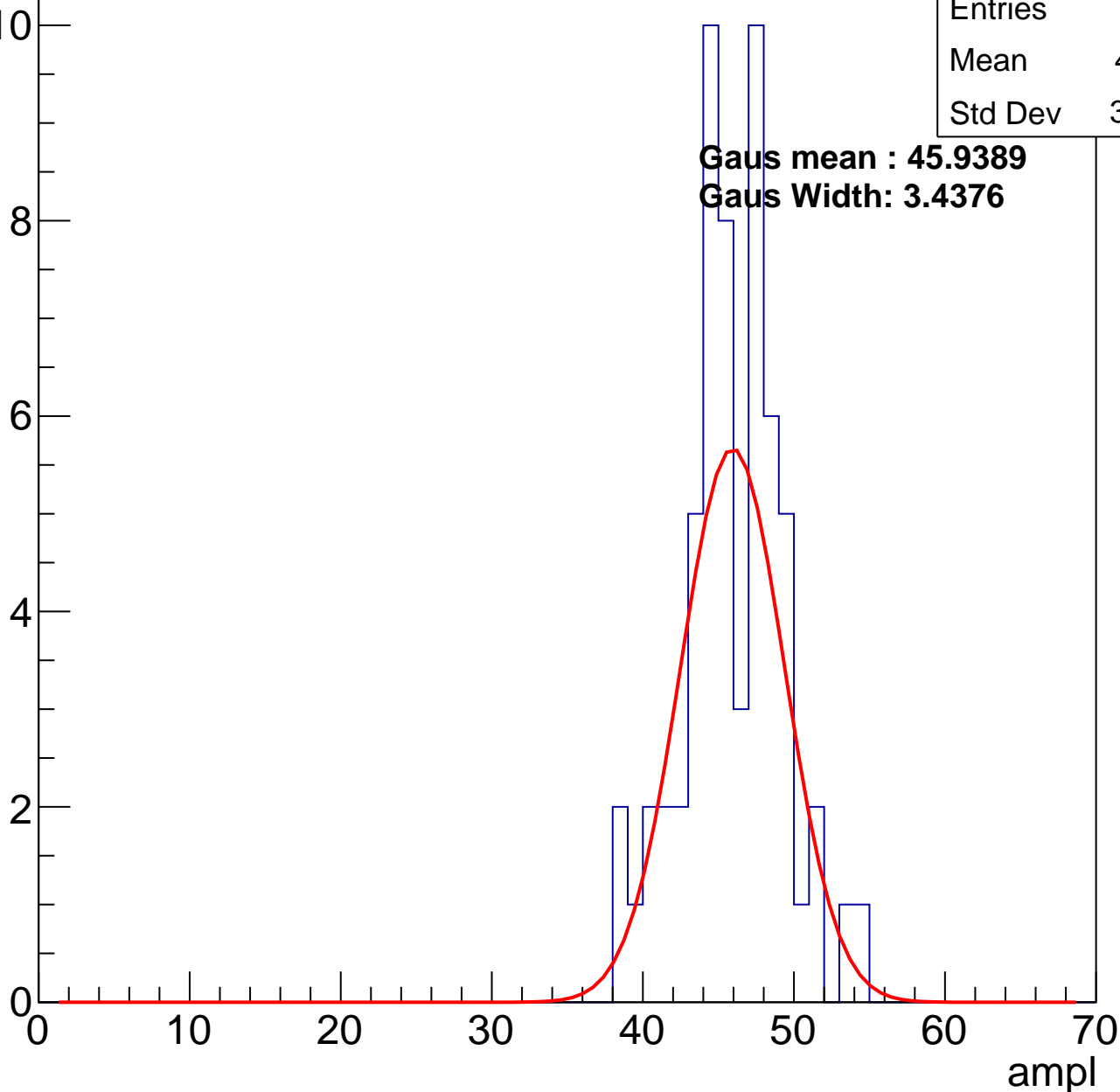
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	45.51
Std Dev	3.307

Gaus mean : 45.9389

Gaus Width: 3.4376

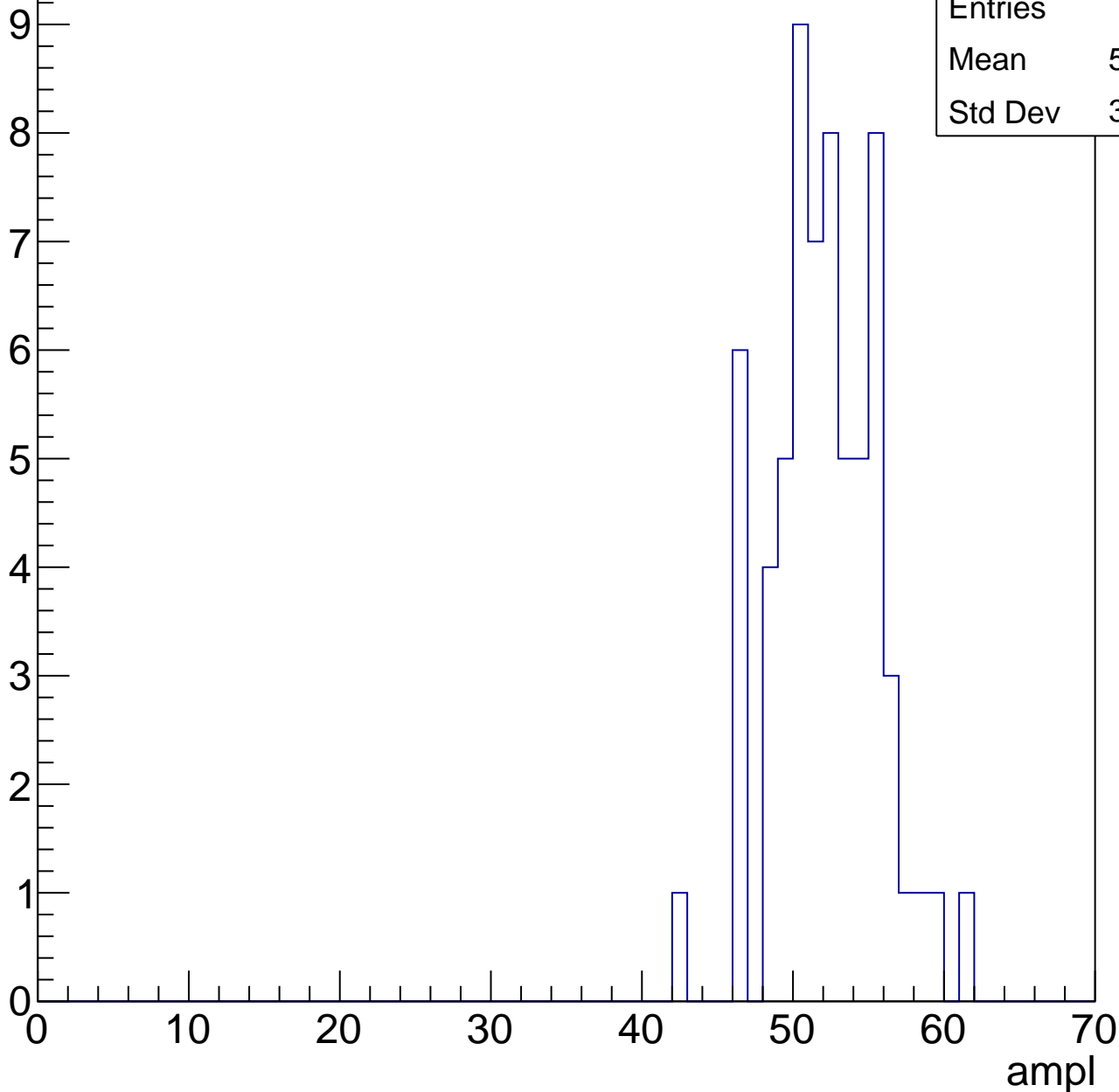


B0L002S, U12-ch100, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	51.63
Std Dev	3.498

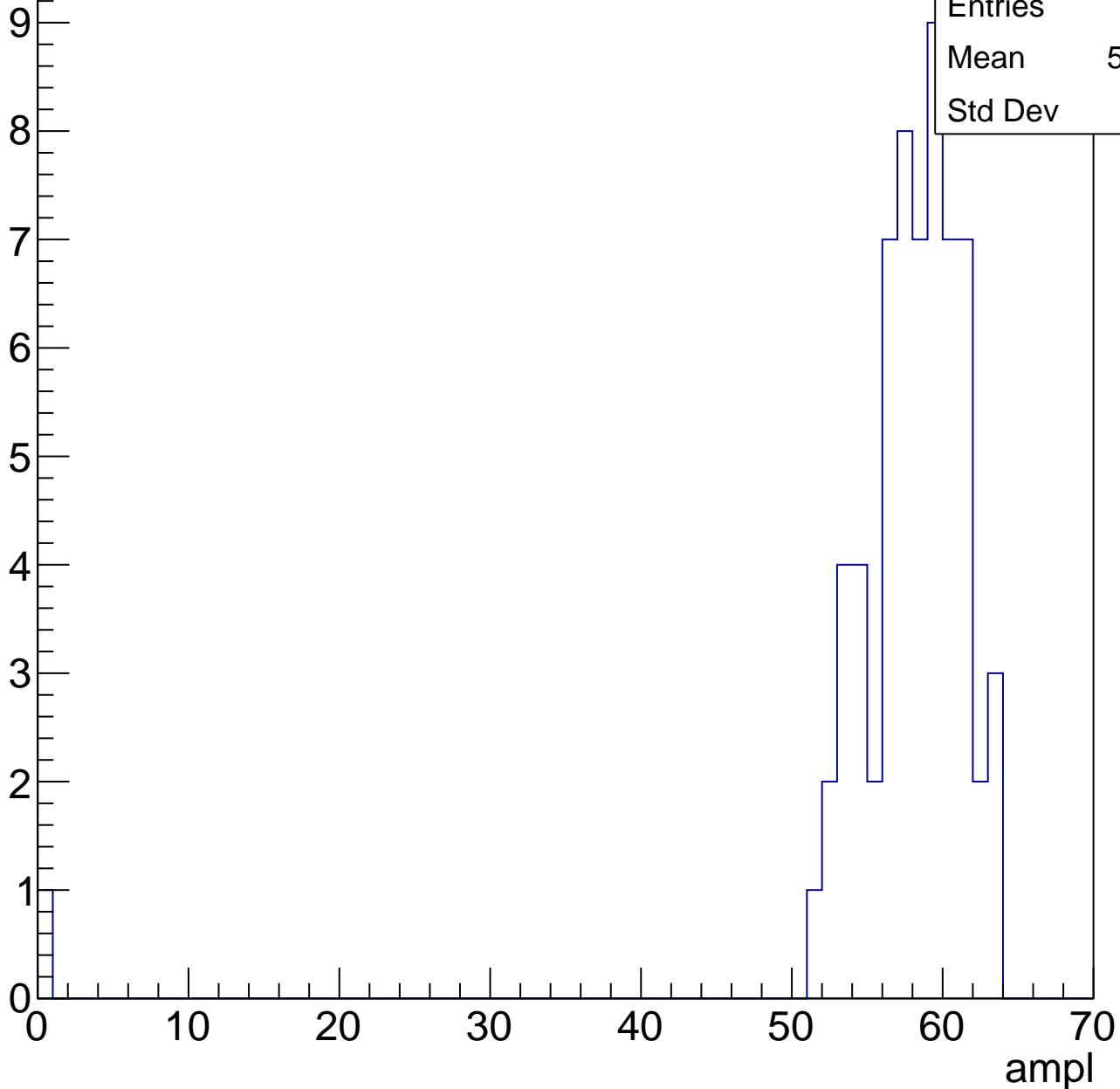


B0L002S, U12-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	56.84
Std Dev	7.73

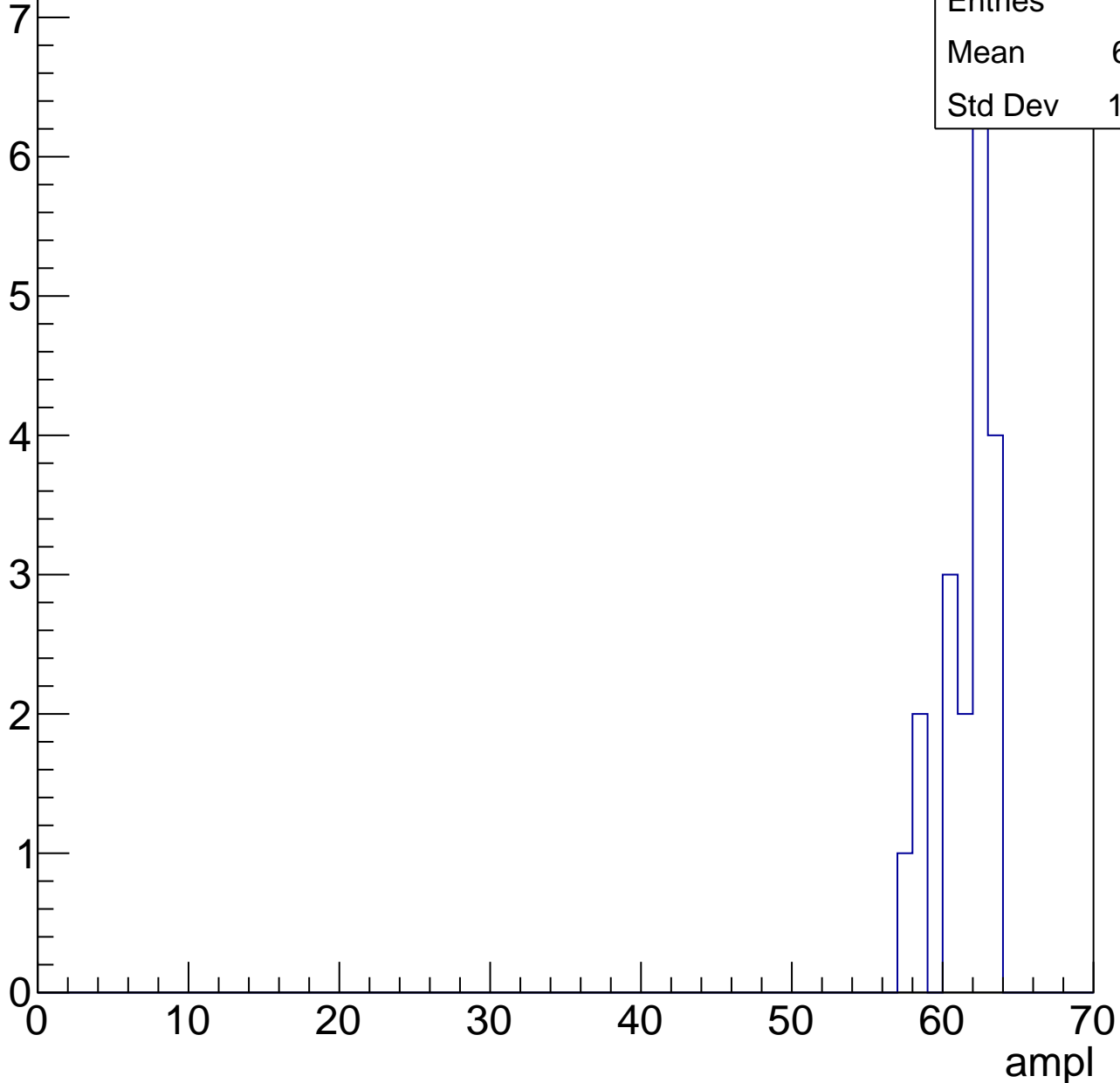


B0L002S, U12-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	61.11
Std Dev	1.774



B0L002S, U12-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch101, adc0

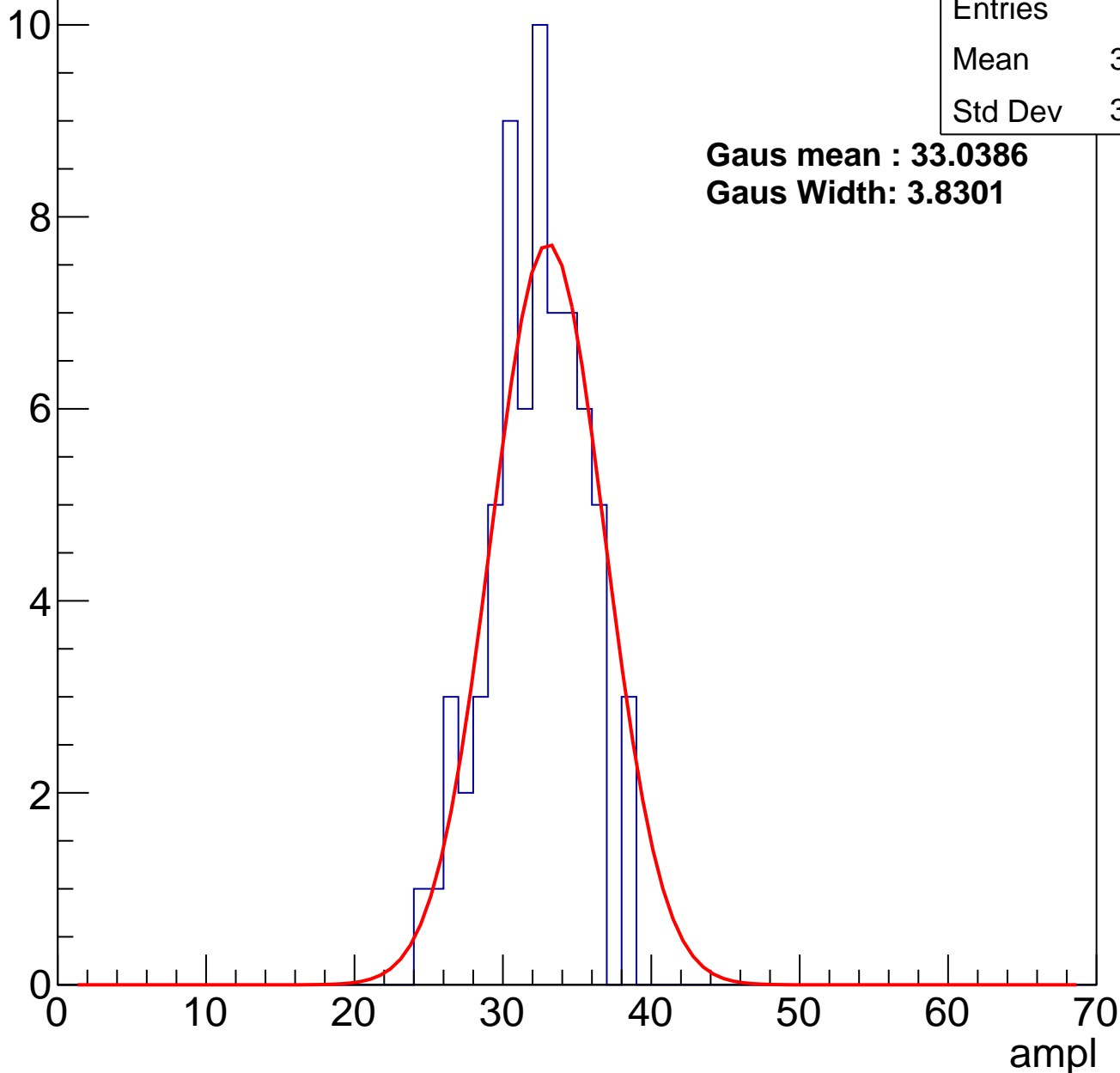
calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	31.75
Std Dev	3.159

Gaus mean : 33.0386

Gaus Width: 3.8301

Entry



B0L002S, U12-ch101, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	53
Mean	37.75
Std Dev	3.325

Gaus mean : 38.2060

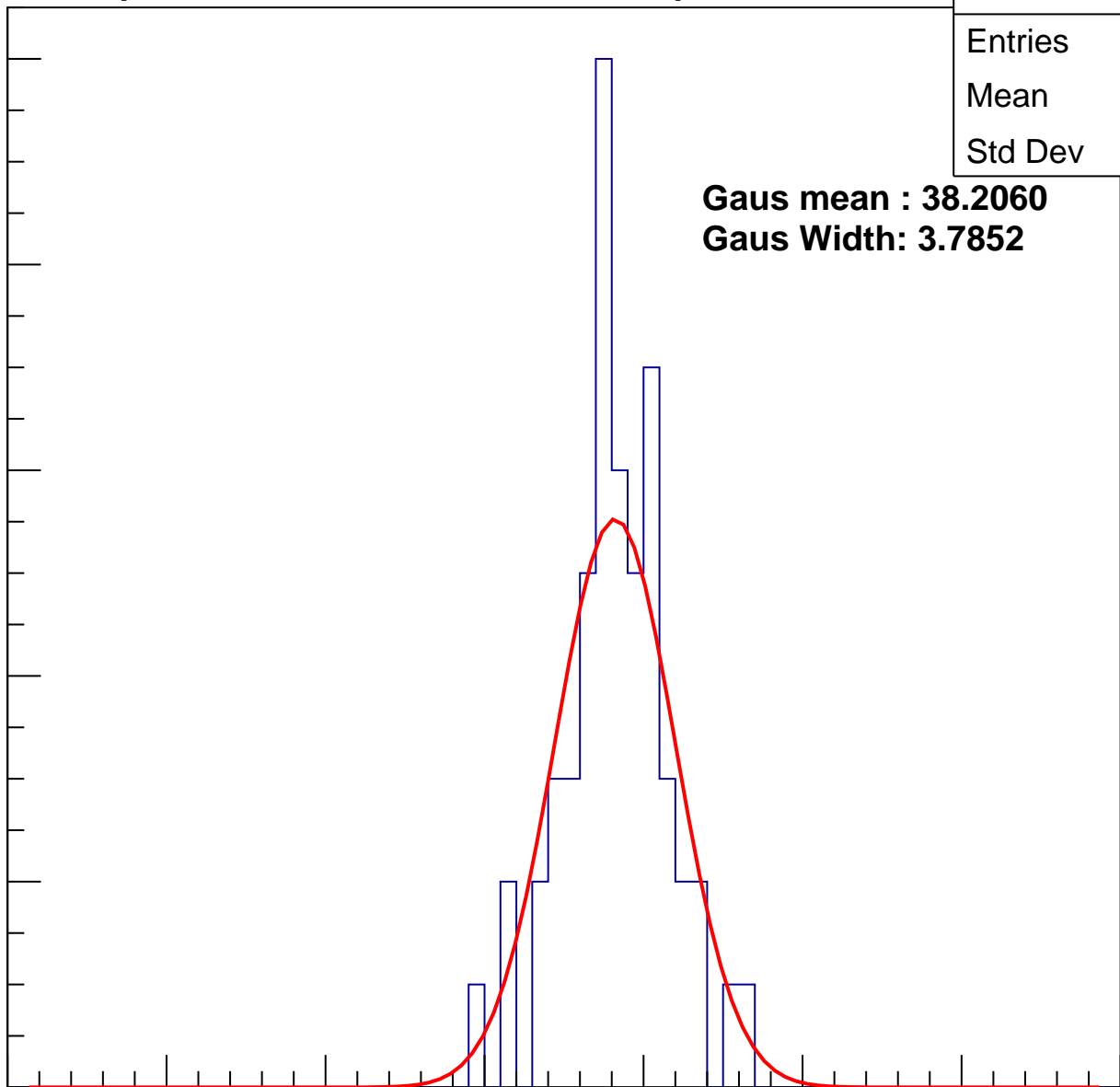
Gaus Width: 3.7852

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch101, adc2

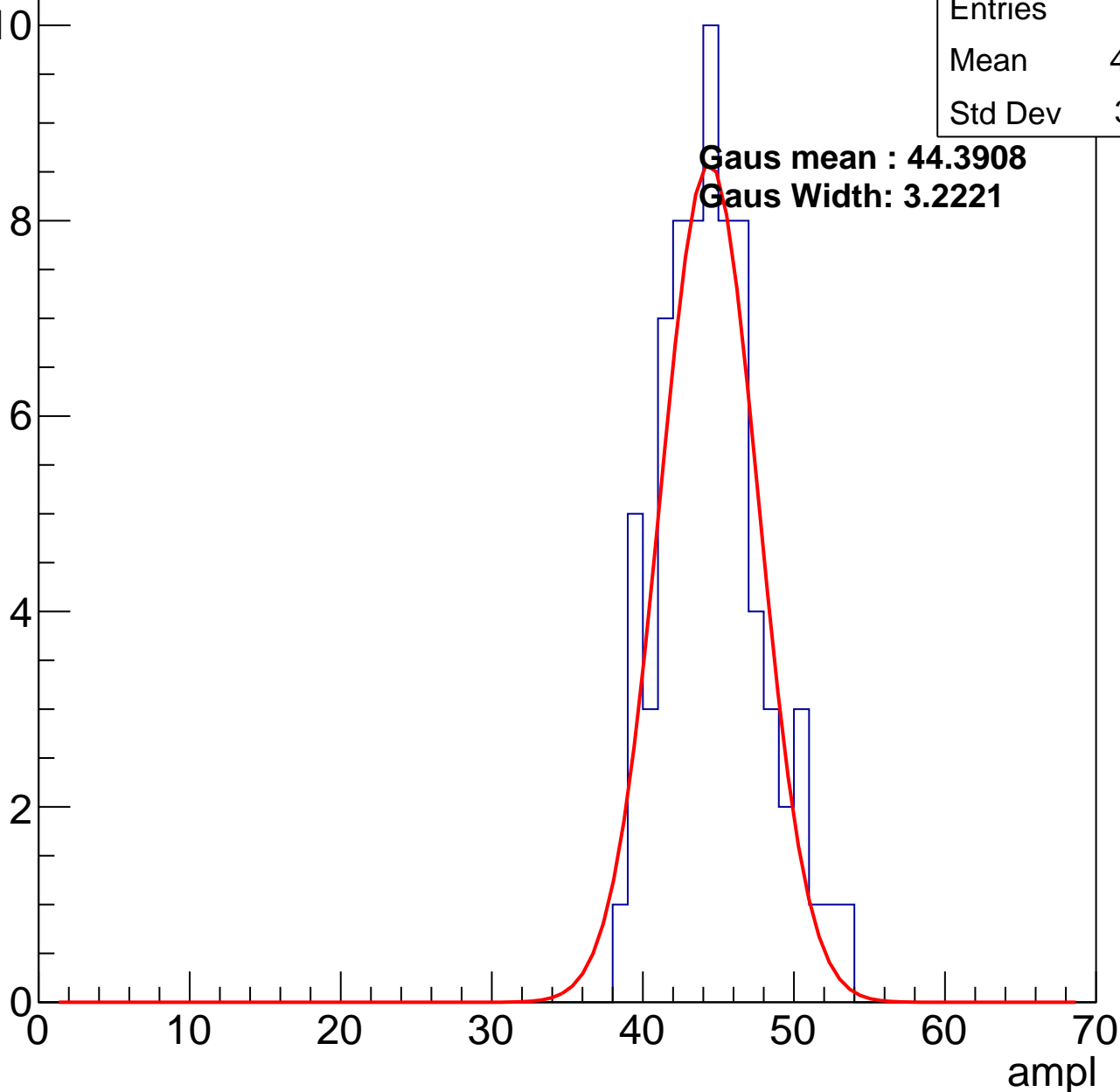
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.16
Std Dev	3.281

Gaus mean : 44.3908

Gaus Width: 3.2221

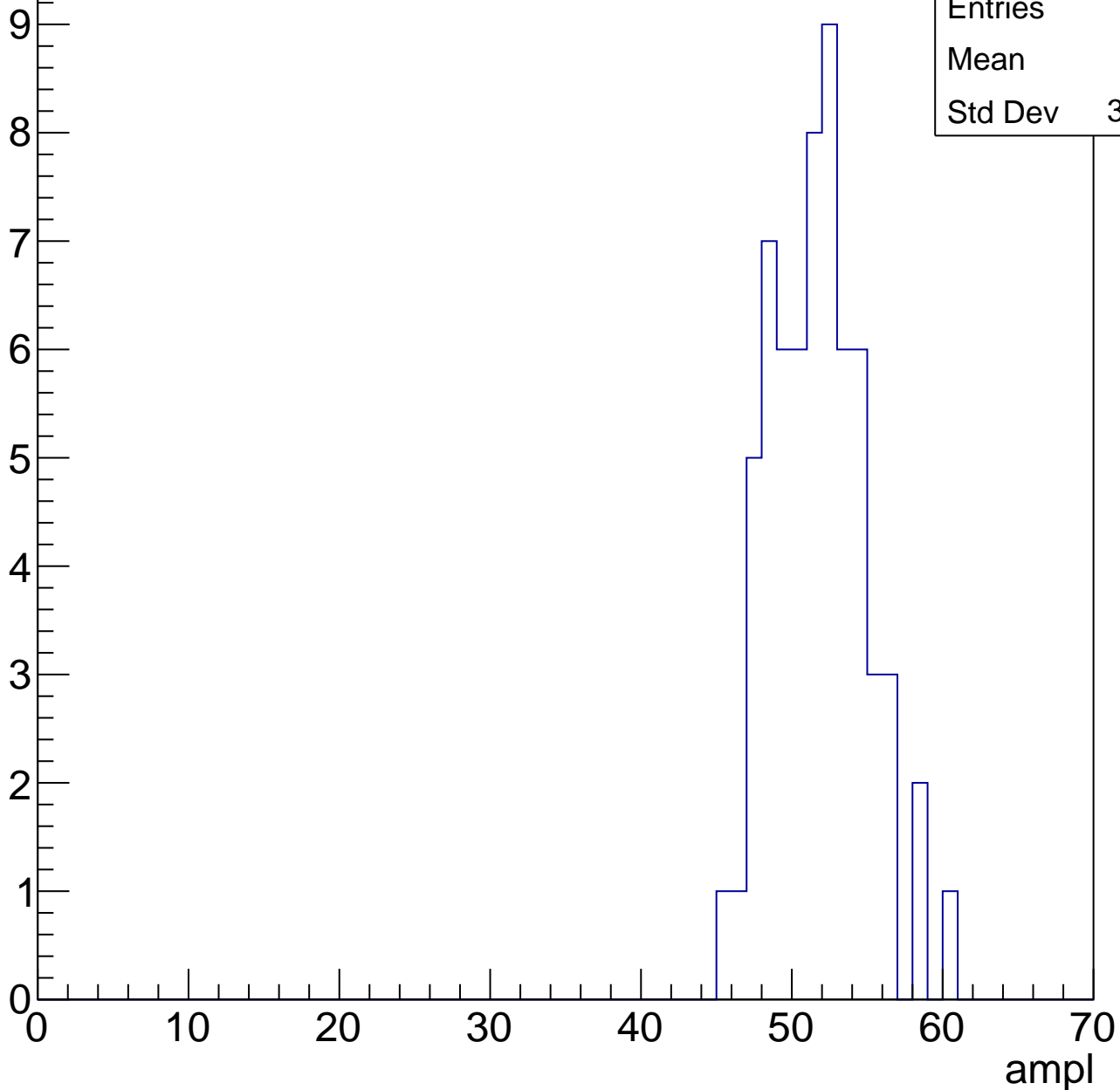


B0L002S, U12-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

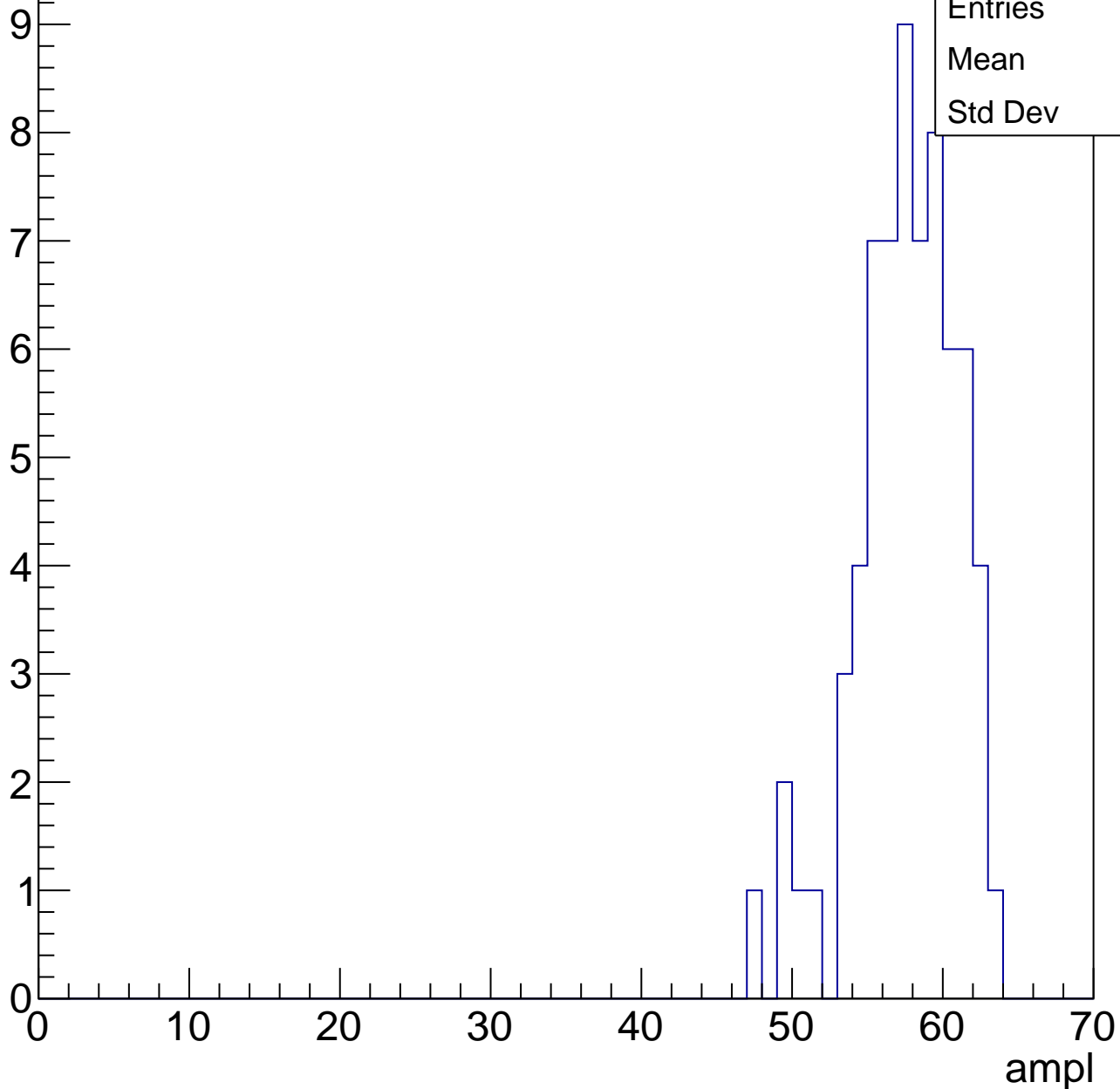
Entries	64
Mean	51.3
Std Dev	3.106



B0L002S, U12-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

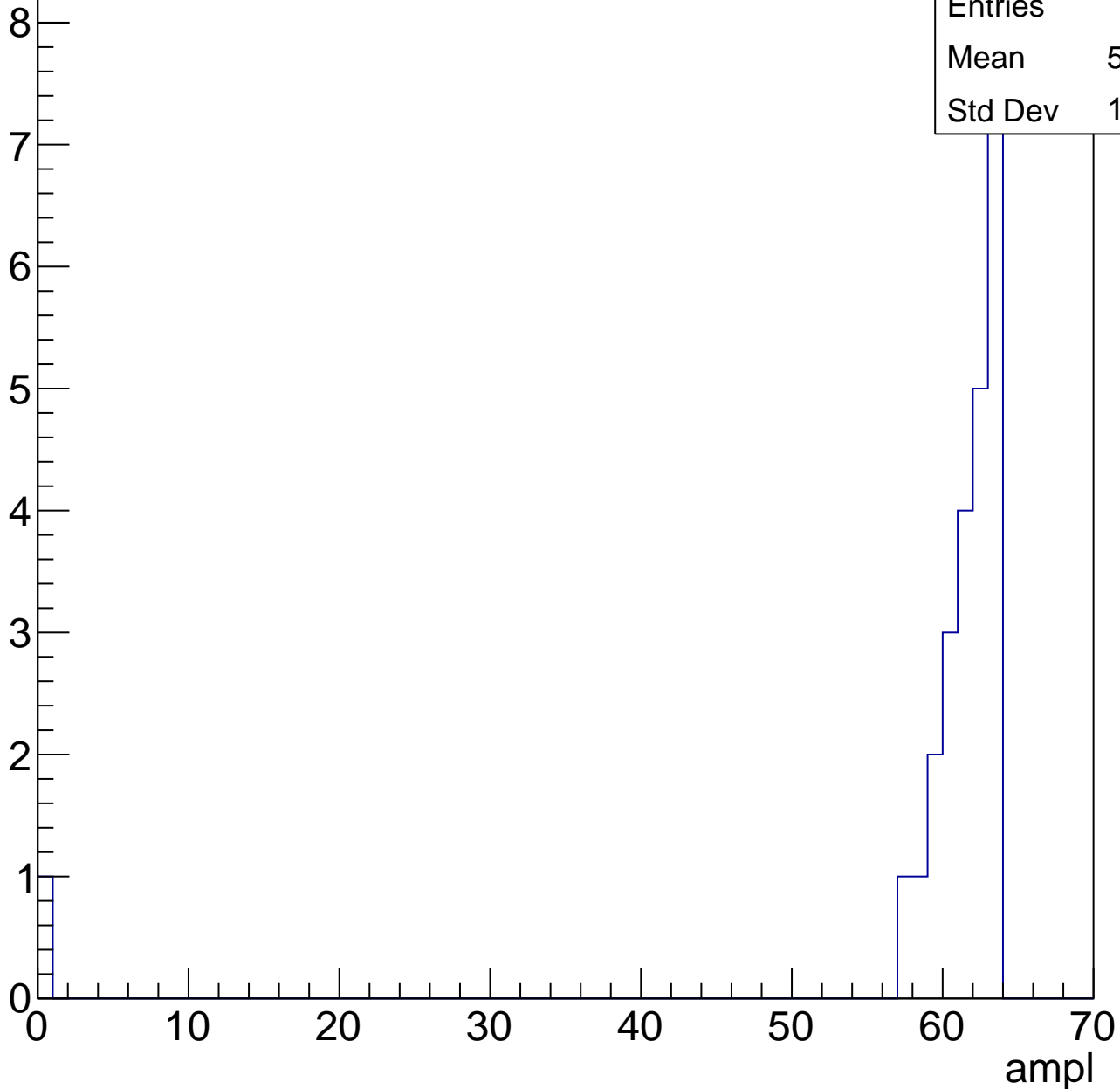


B0L002S, U12-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.84
Std Dev	12.13



B0L002S, U12-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B0L002S, U12-ch102, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	32.15
Std Dev	5.316

Gaus mean : 33.3152

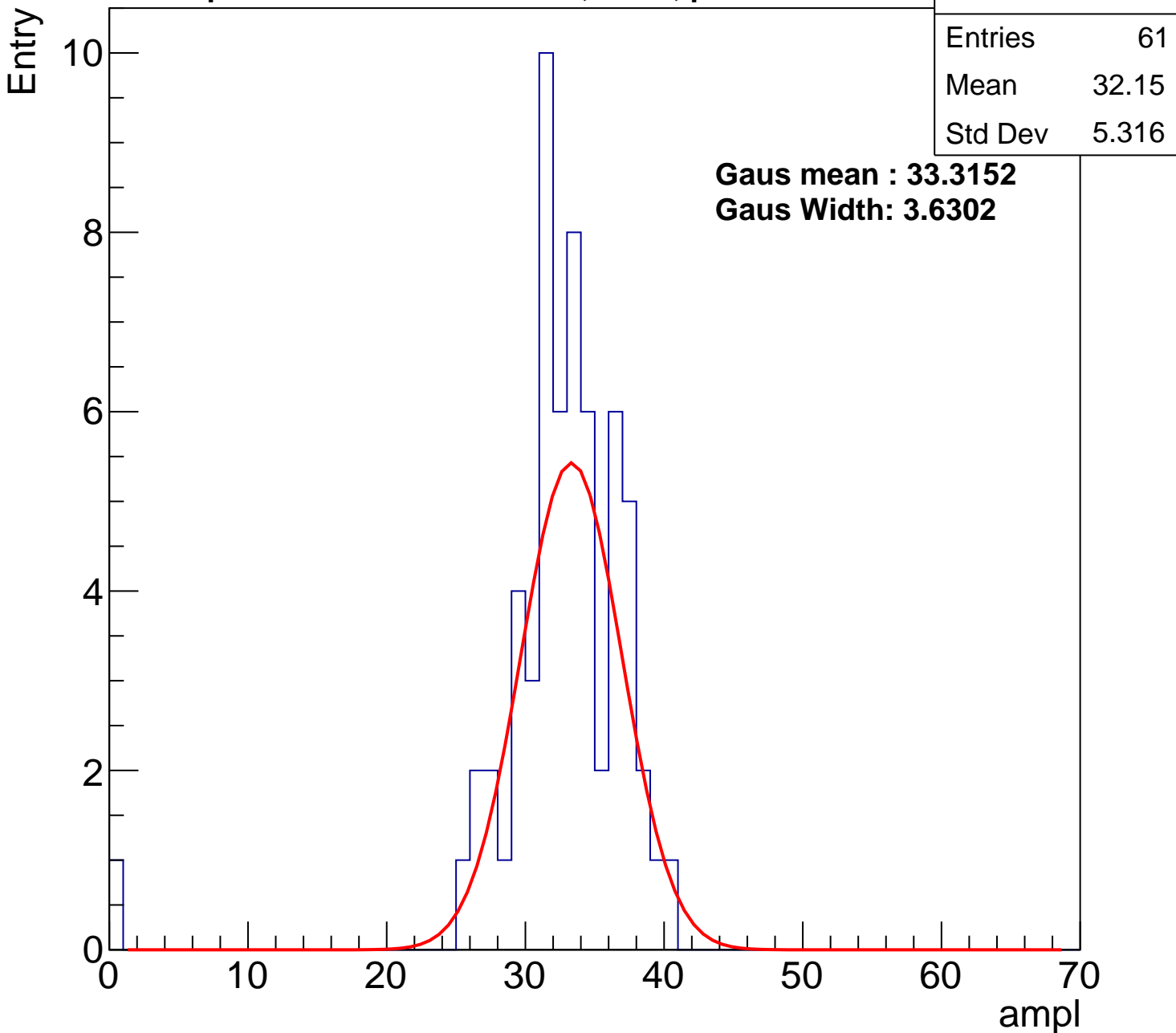
Gaus Width: 3.6302

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch102, adc1

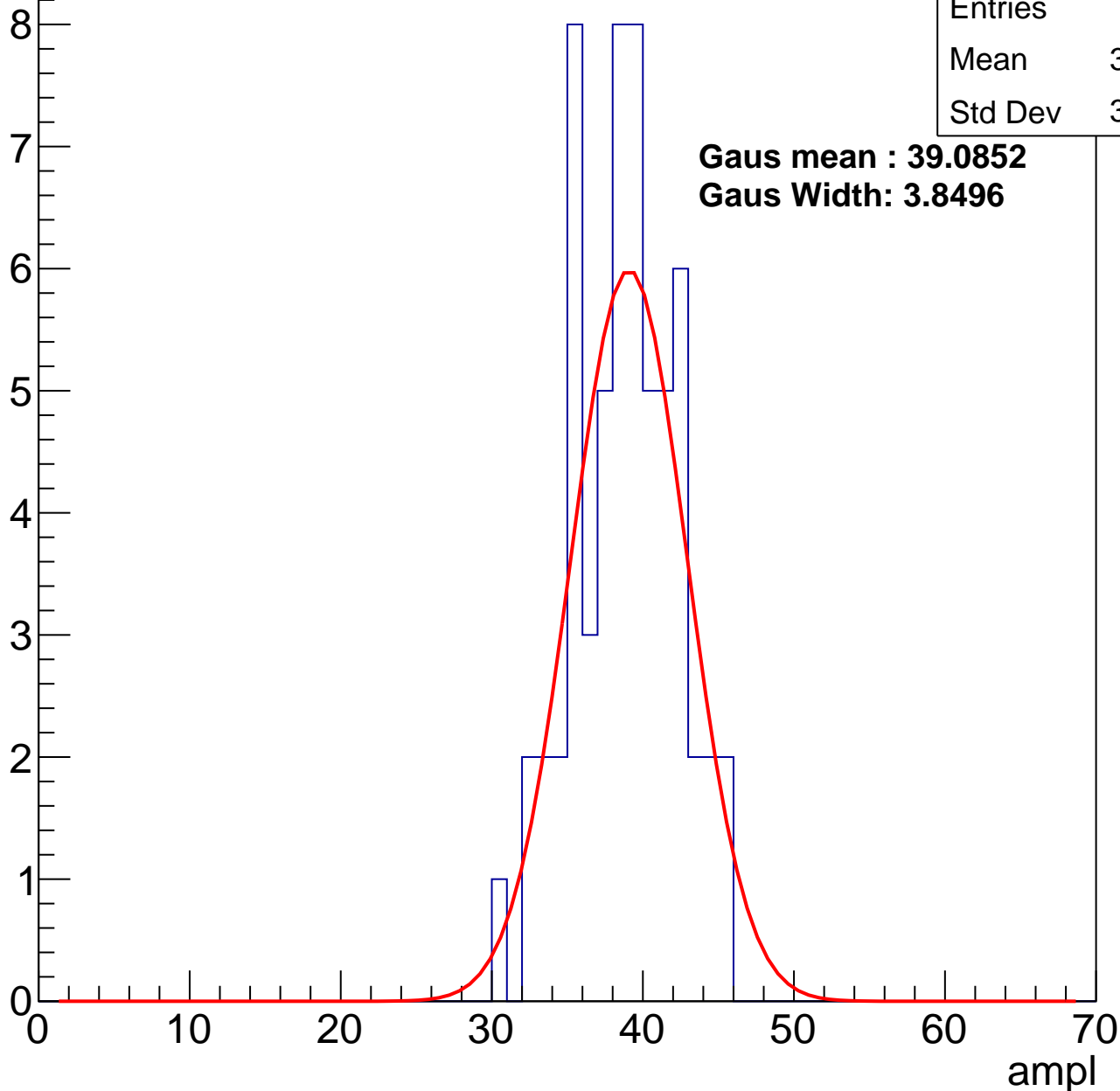
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.33
Std Dev	3.362

Gaus mean : 39.0852

Gaus Width: 3.8496



B0L002S, U12-ch102, adc2

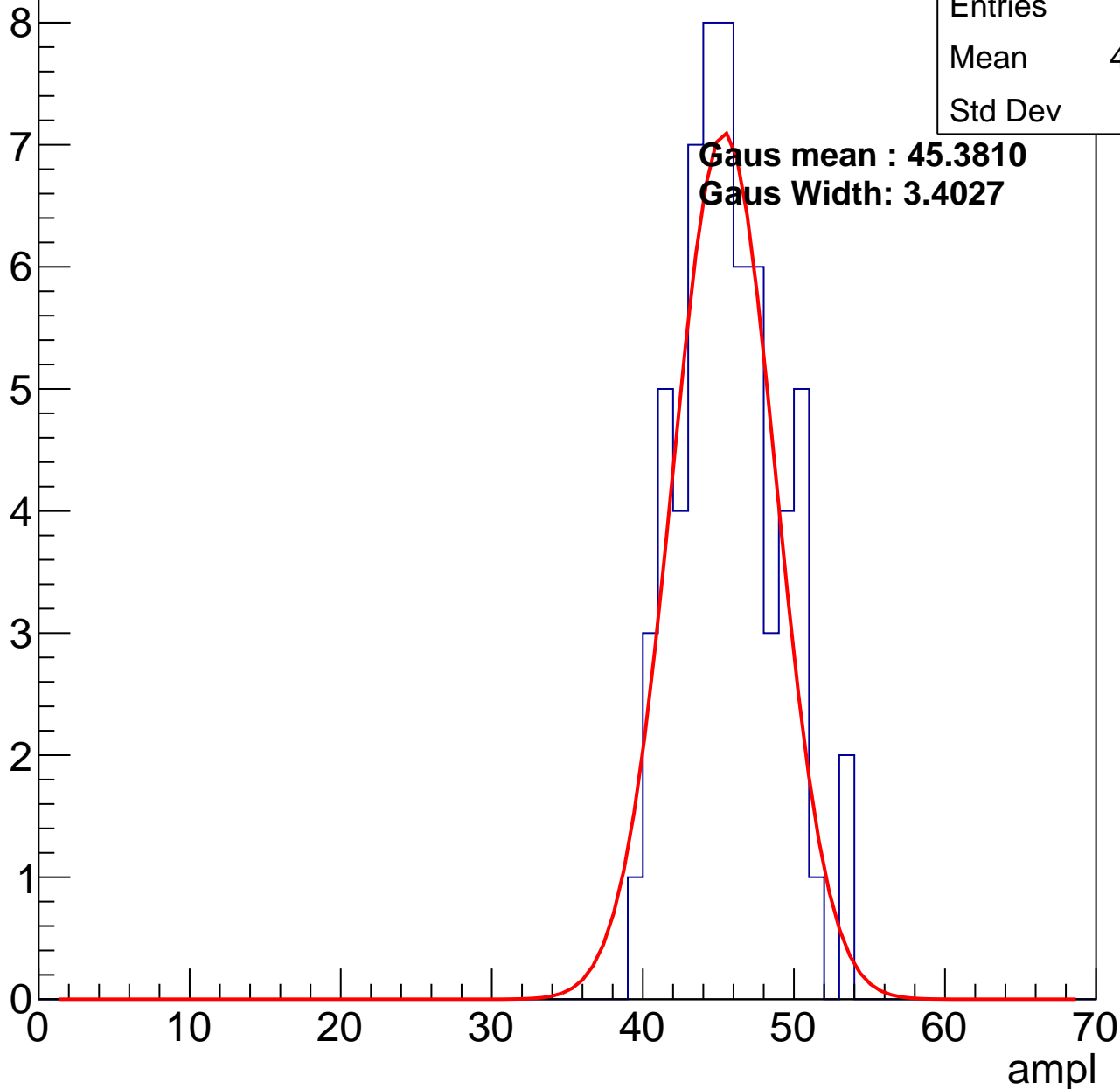
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	45.24
Std Dev	3.26

Gaus mean : 45.3810

Gaus Width: 3.4027

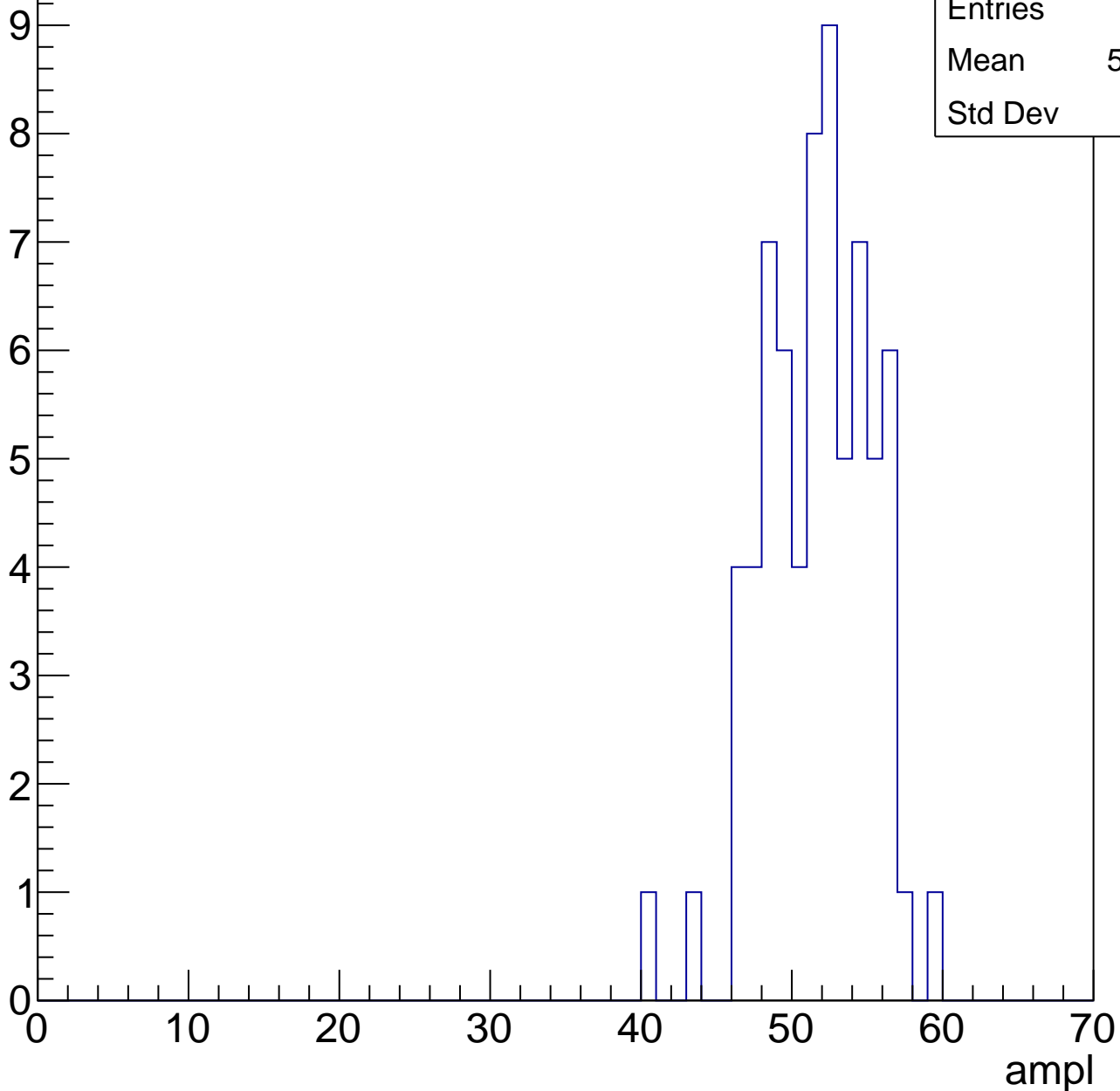


B0L002S, U12-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	51.17
Std Dev	3.53

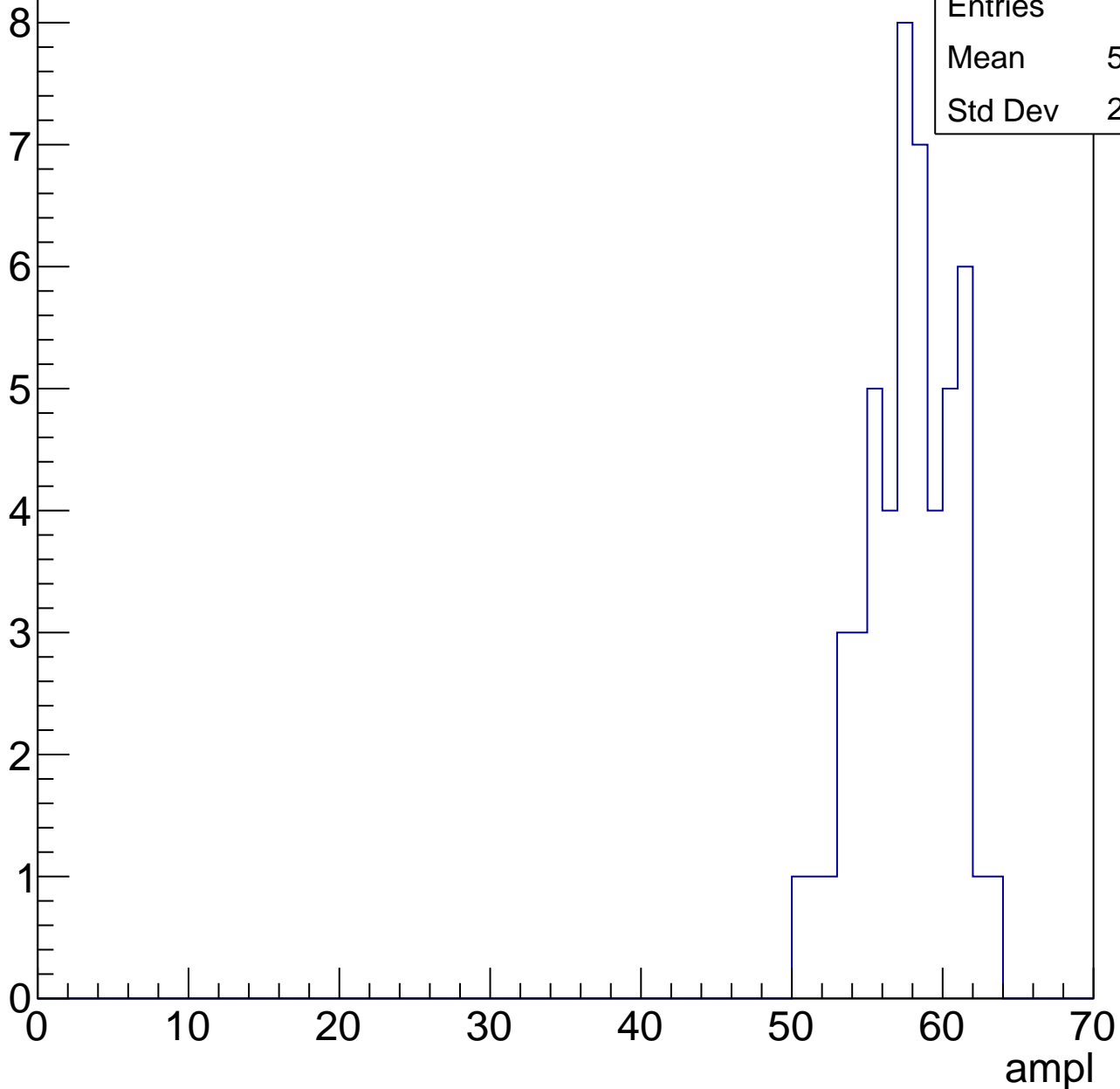


B0L002S, U12-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	57.24
Std Dev	2.936

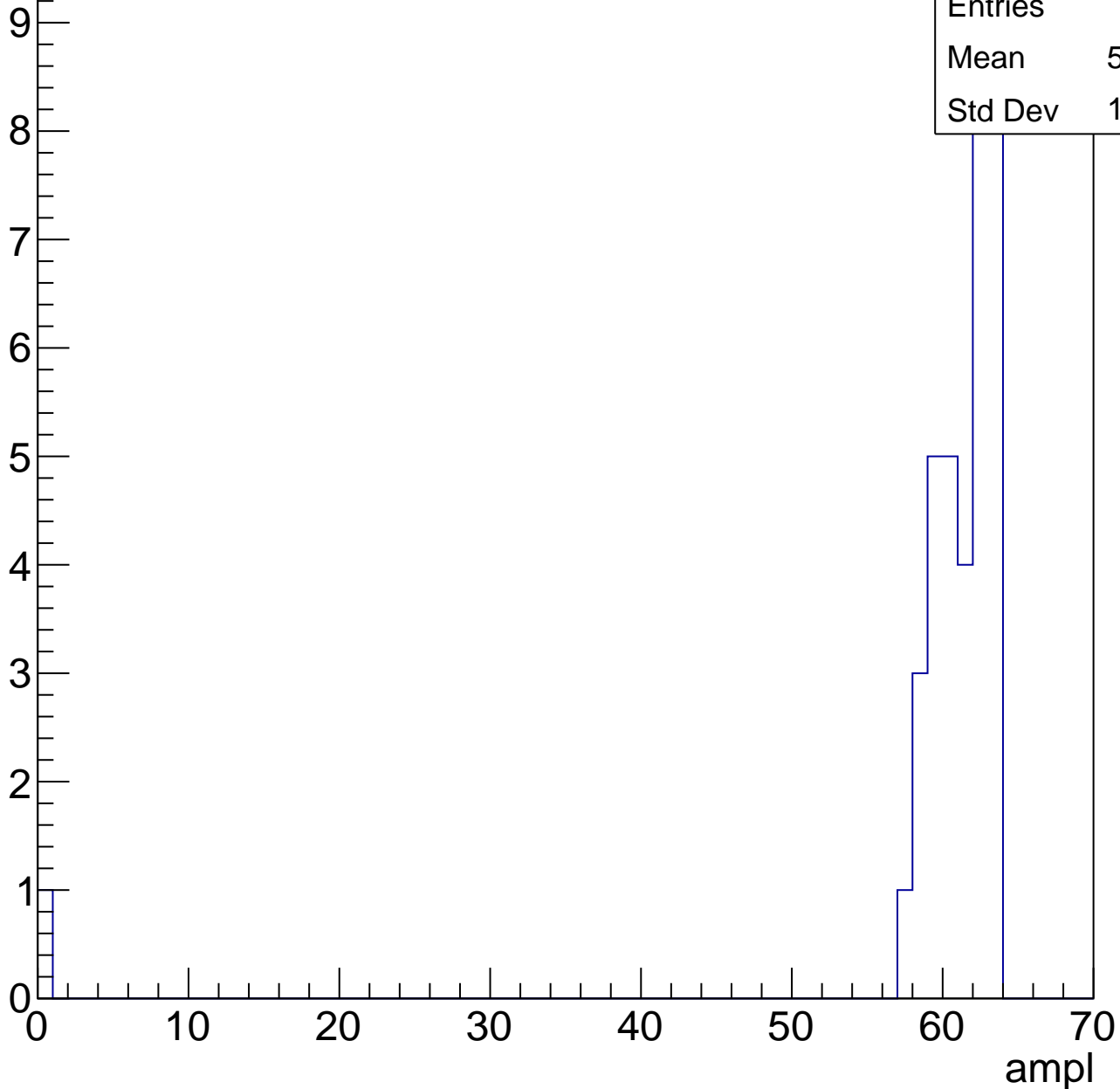


B0L002S, U12-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.32
Std Dev	10.04



B0L002S, U12-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

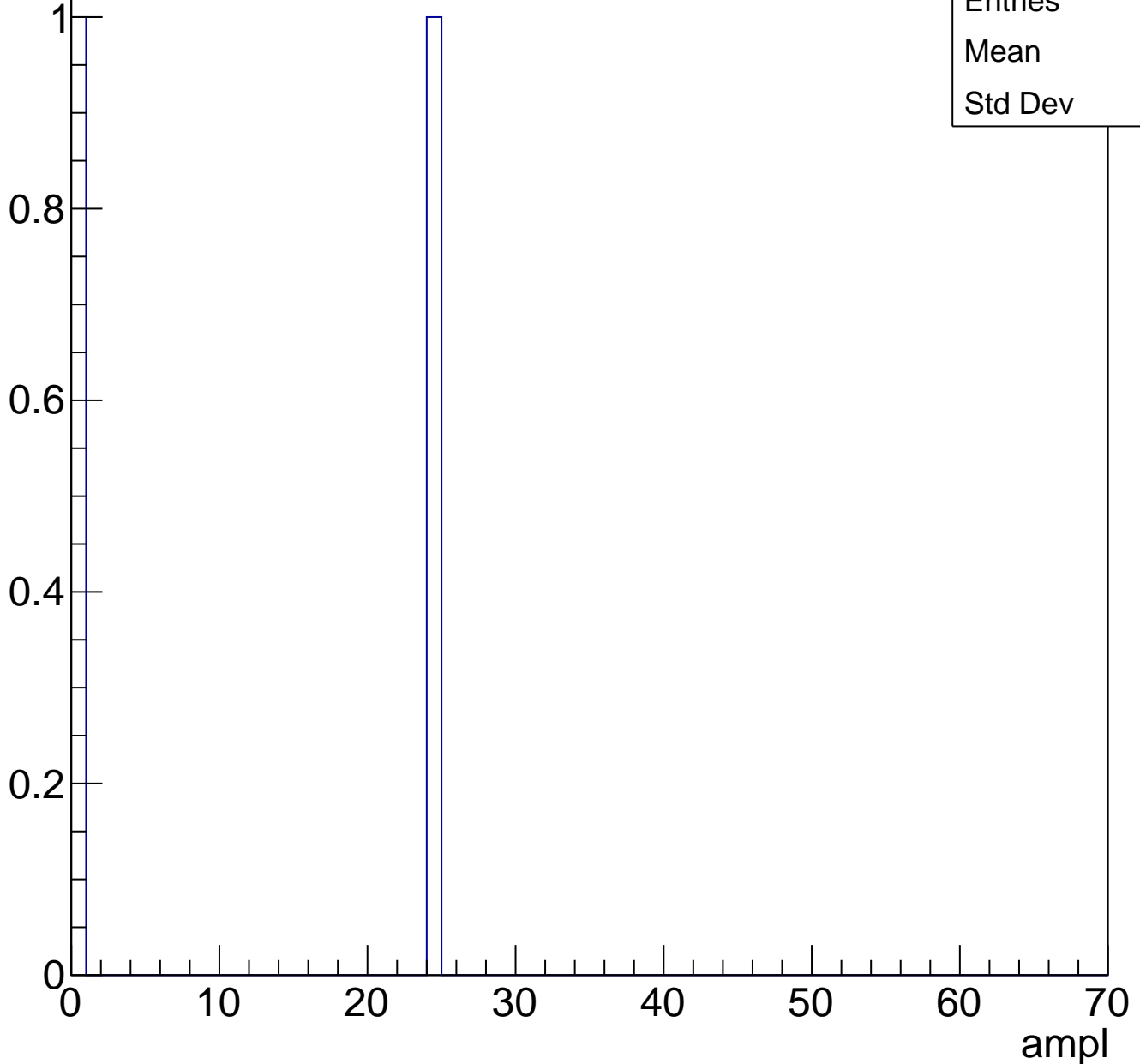


Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch103, adc0

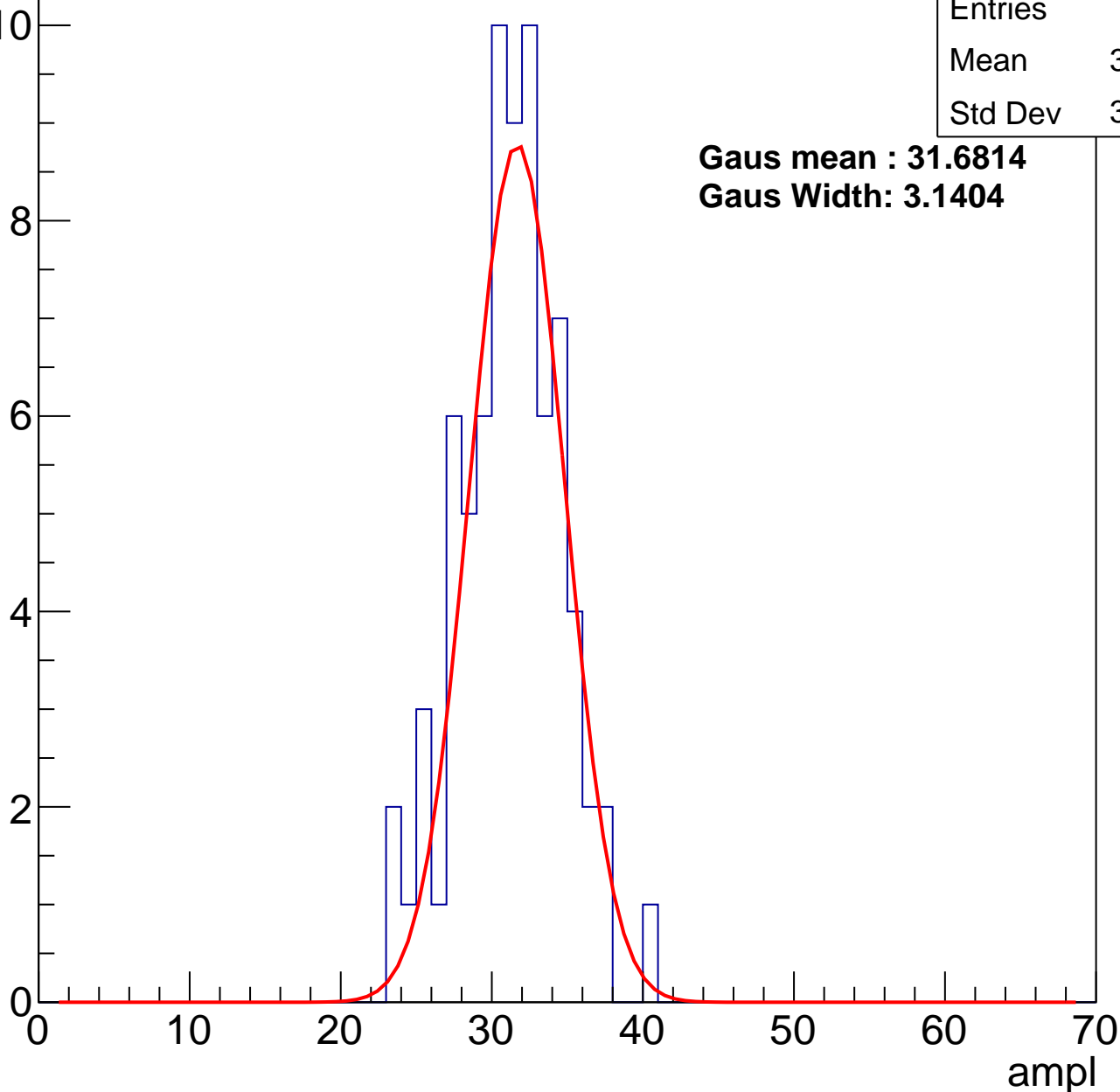
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	30.77
Std Dev	3.357

Gaus mean : 31.6814

Gaus Width: 3.1404



B0L002S, U12-ch103, adc1

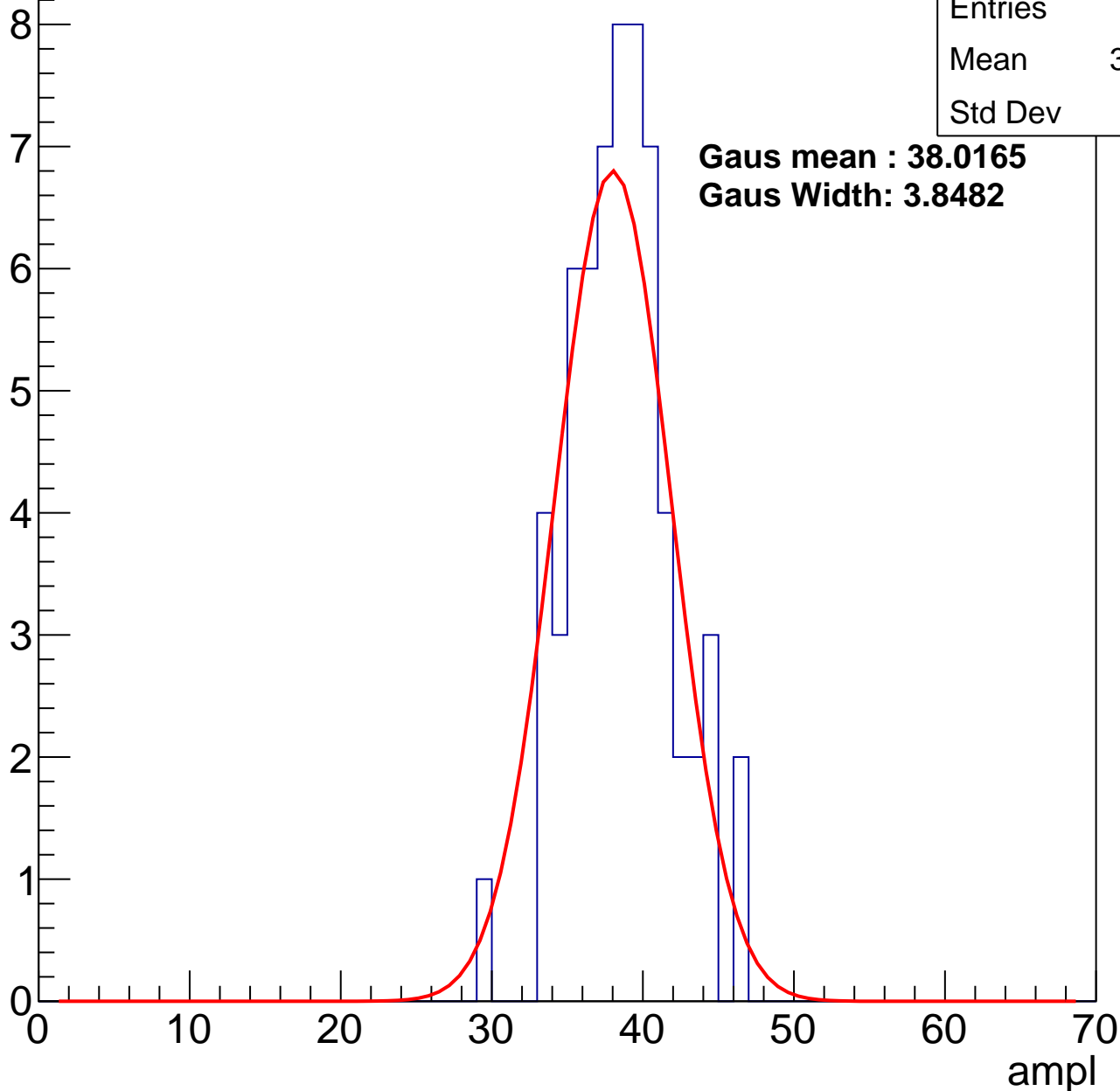
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.13
Std Dev	3.34

Gaus mean : 38.0165

Gaus Width: 3.8482



B0L002S, U12-ch103, adc2

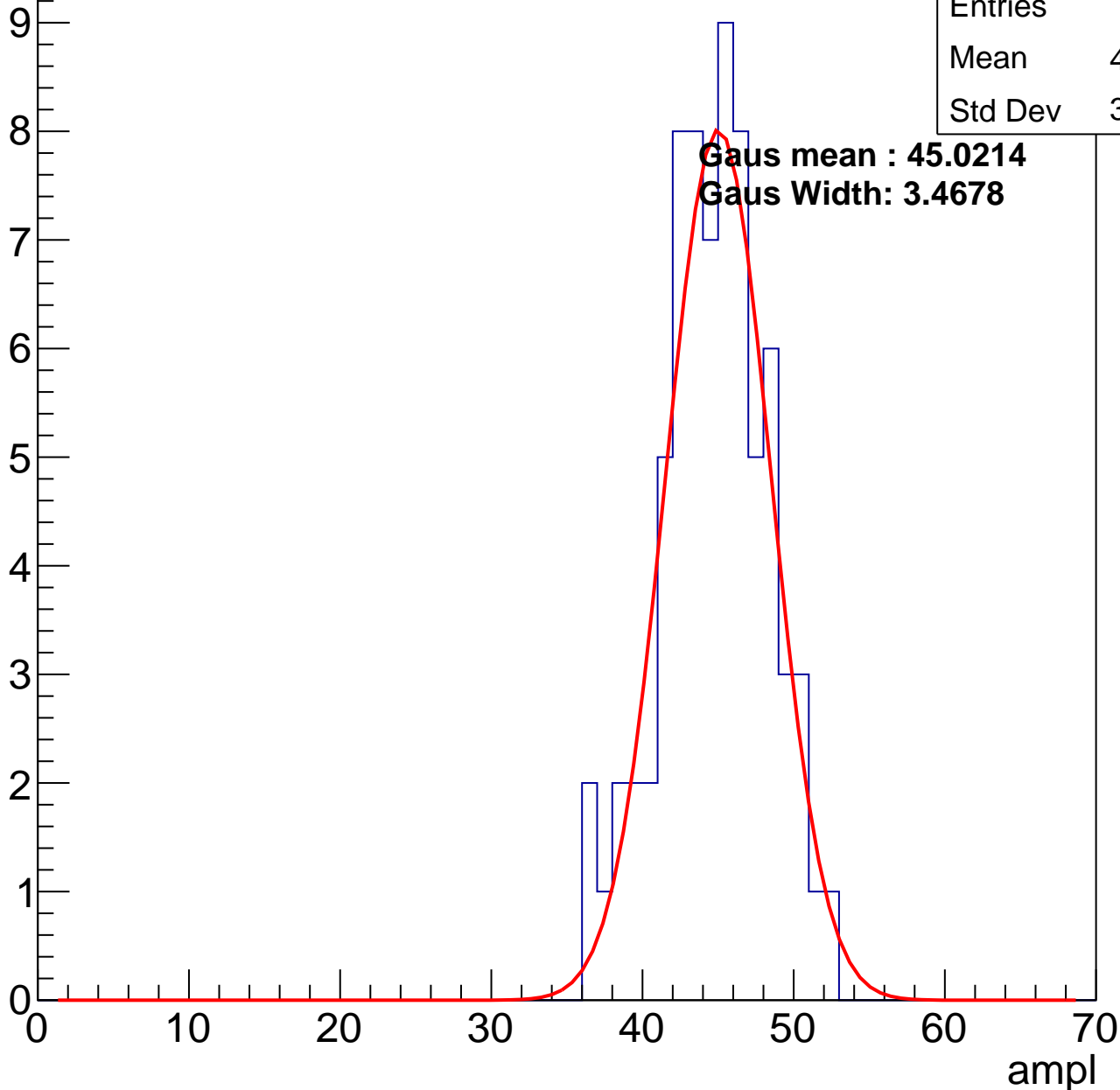
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.27
Std Dev	3.489

Gaus mean : 45.0214

Gaus Width: 3.4678

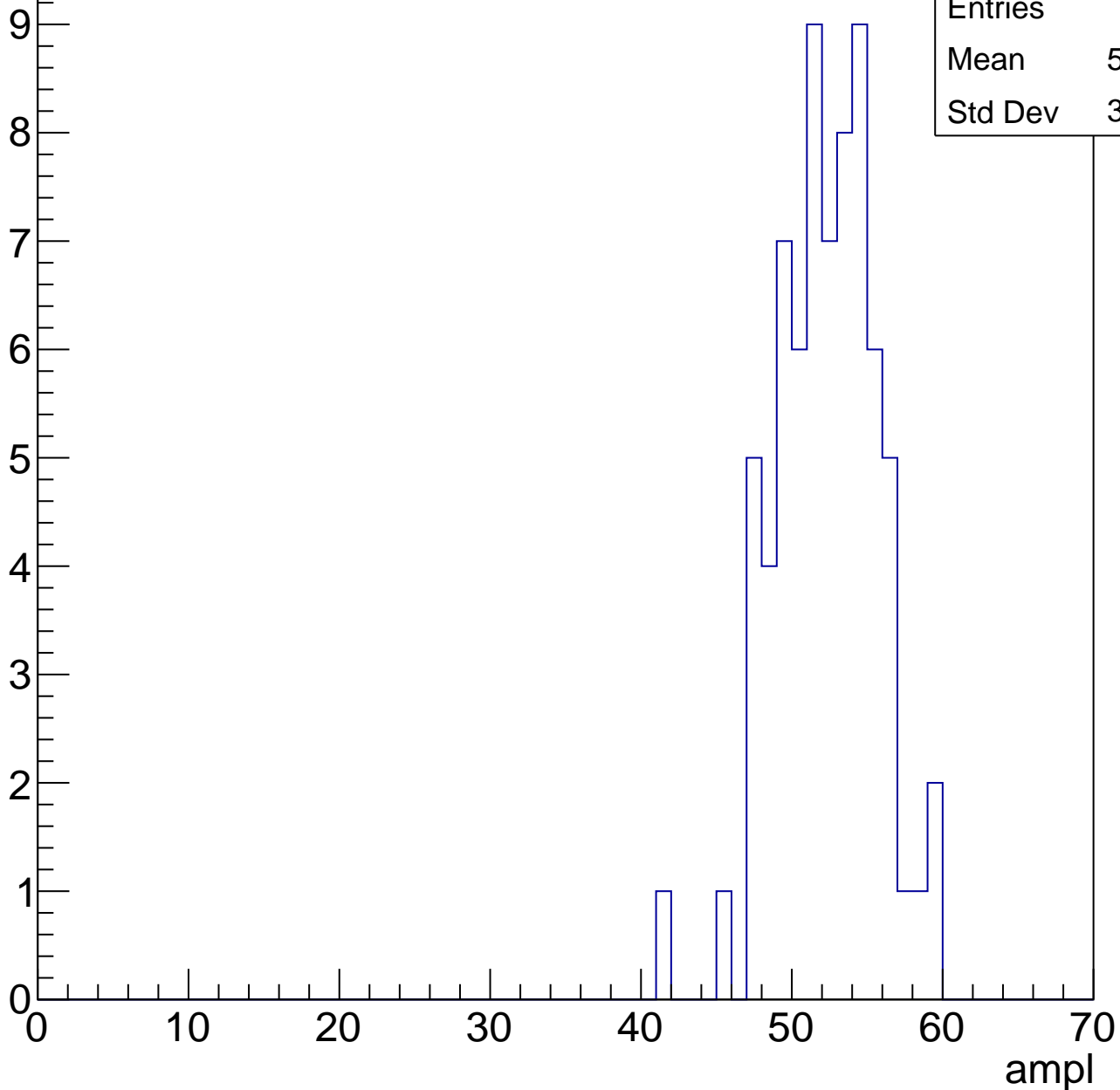


B0L002S, U12-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	51.83
Std Dev	3.312

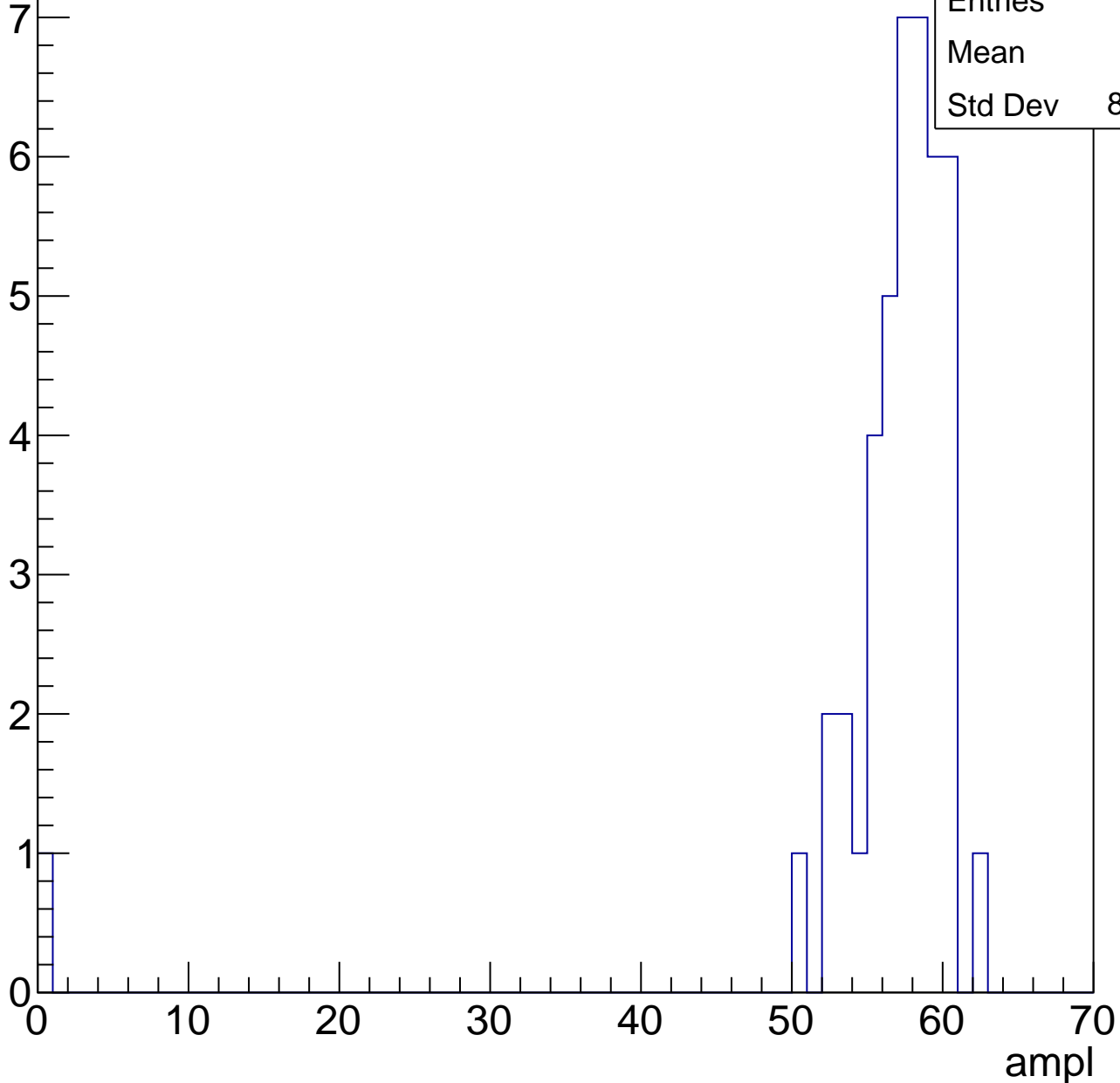


B0L002S, U12-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	55.7
Std Dev	8.953

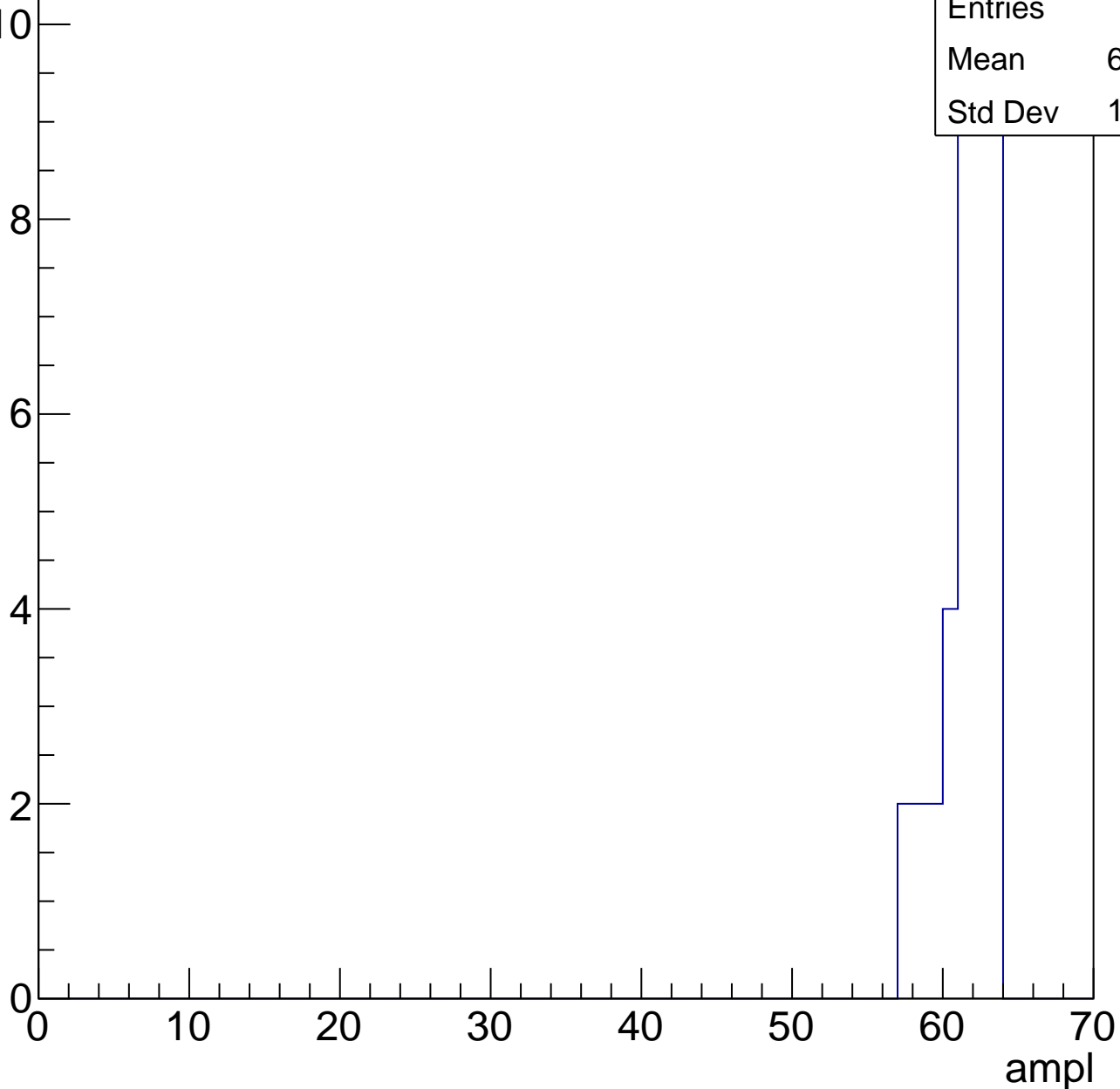


B0L002S, U12-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	61.18
Std Dev	1.677



B0L002S, U12-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U12-ch104, adc0

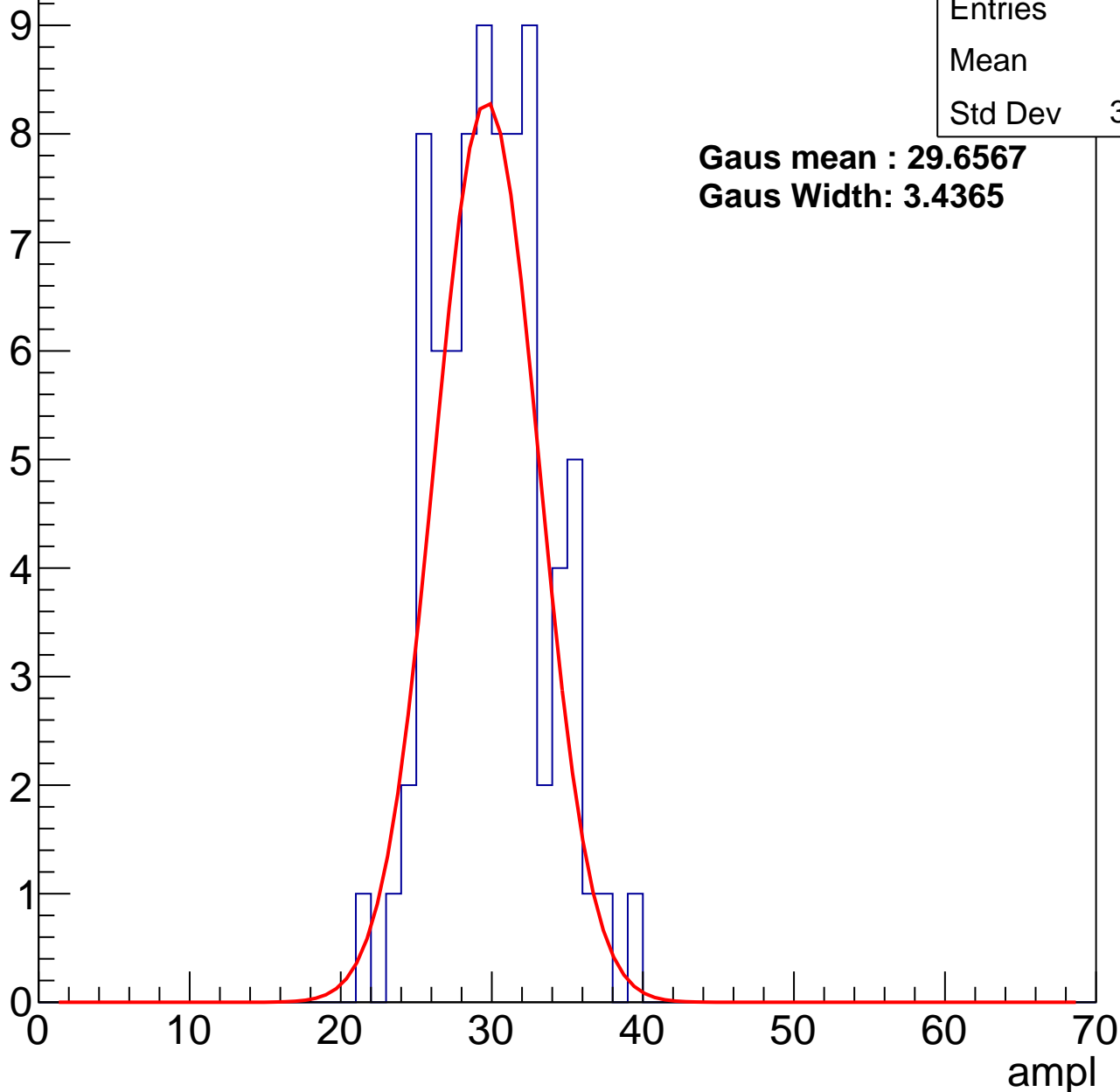
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	29.5
Std Dev	3.518

Gaus mean : 29.6567

Gaus Width: 3.4365



B0L002S, U12-ch104, adc1

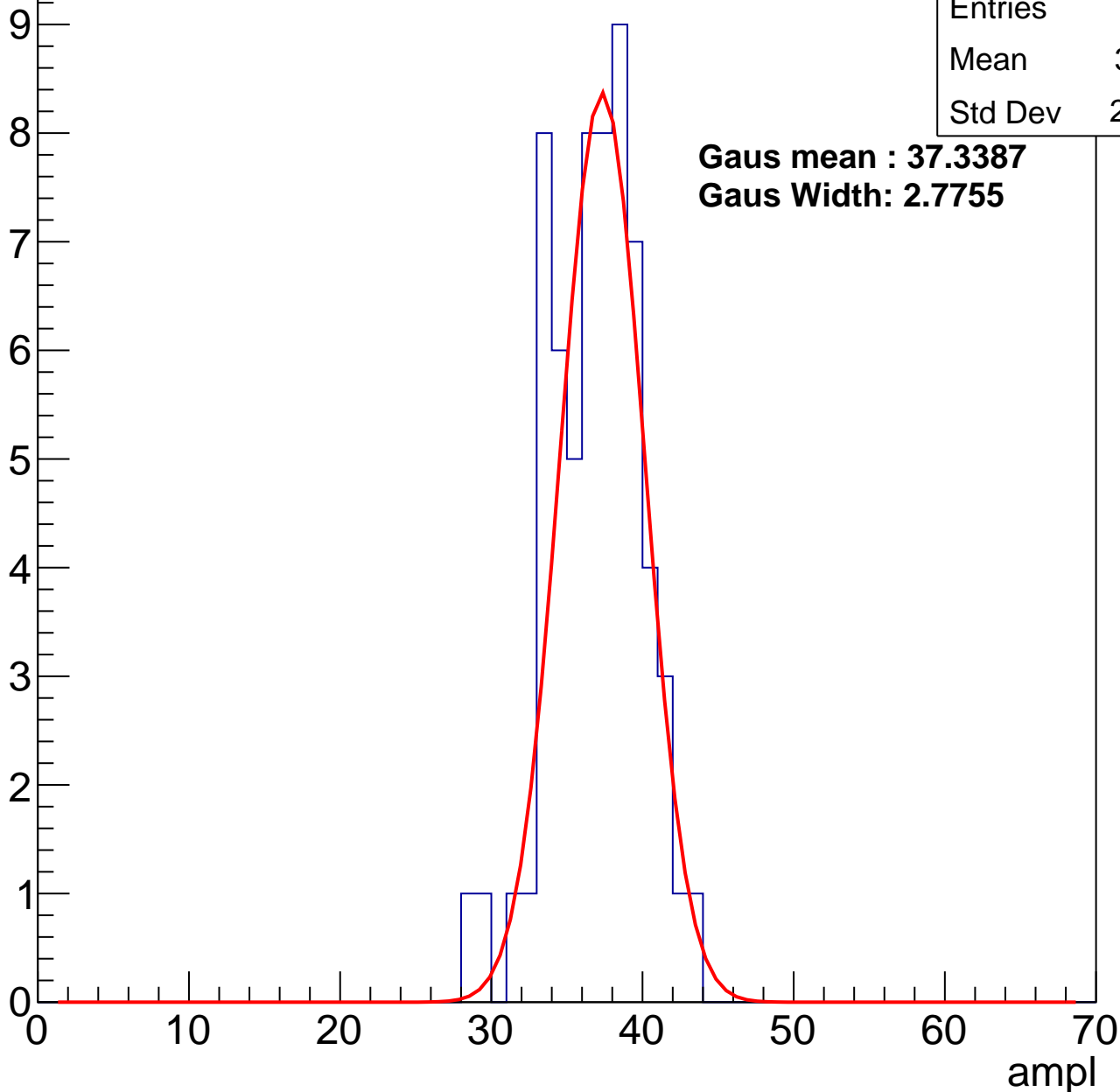
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.41
Std Dev	2.999

Gaus mean : 37.3387

Gaus Width: 2.7755



B0L002S, U12-ch104, adc2

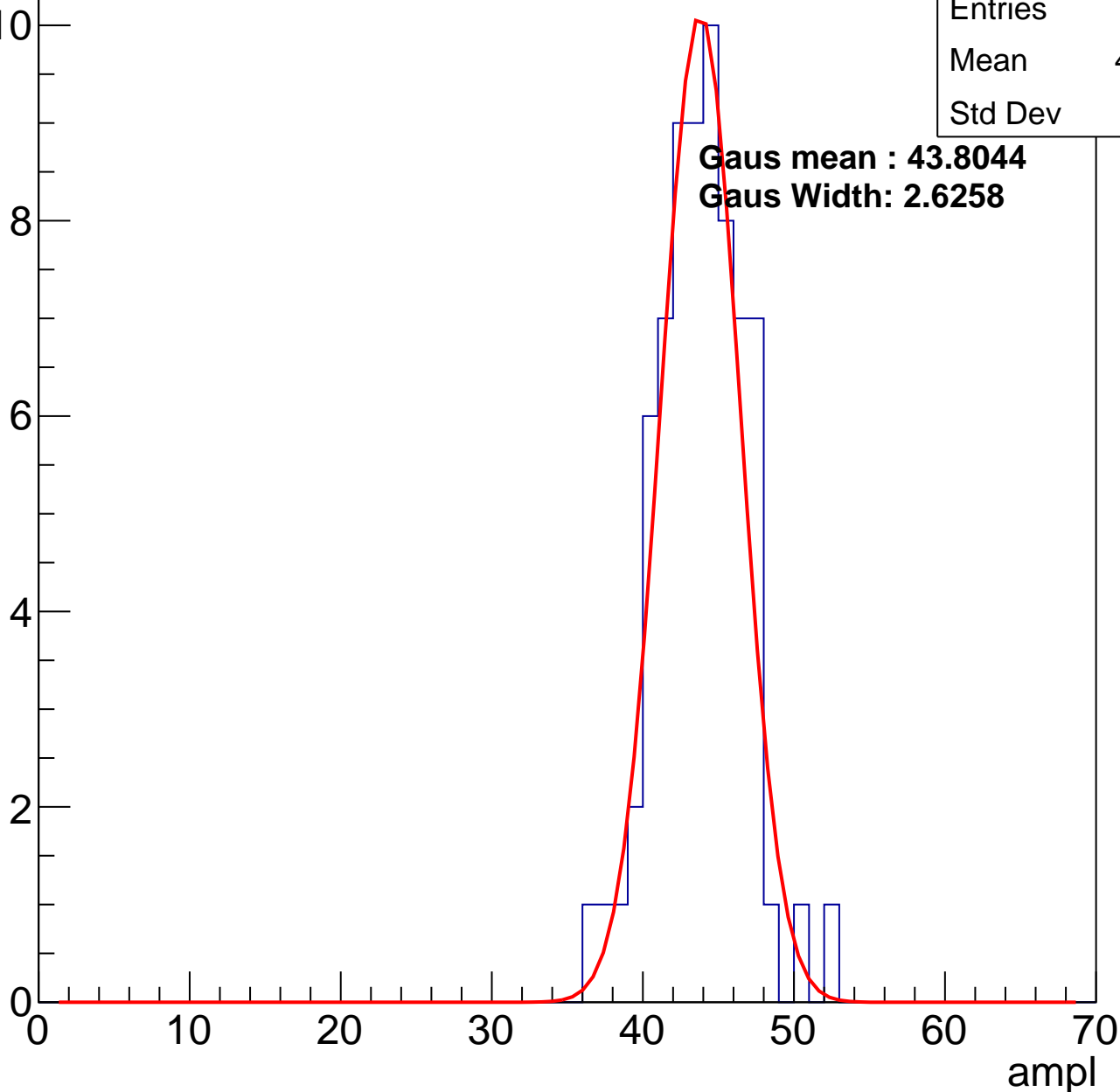
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.41
Std Dev	2.89

Gaus mean : 43.8044

Gaus Width: 2.6258



B0L002S, U12-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	50.16
Std Dev	2.606

Entry

10

8

6

4

2

0

0

10

20

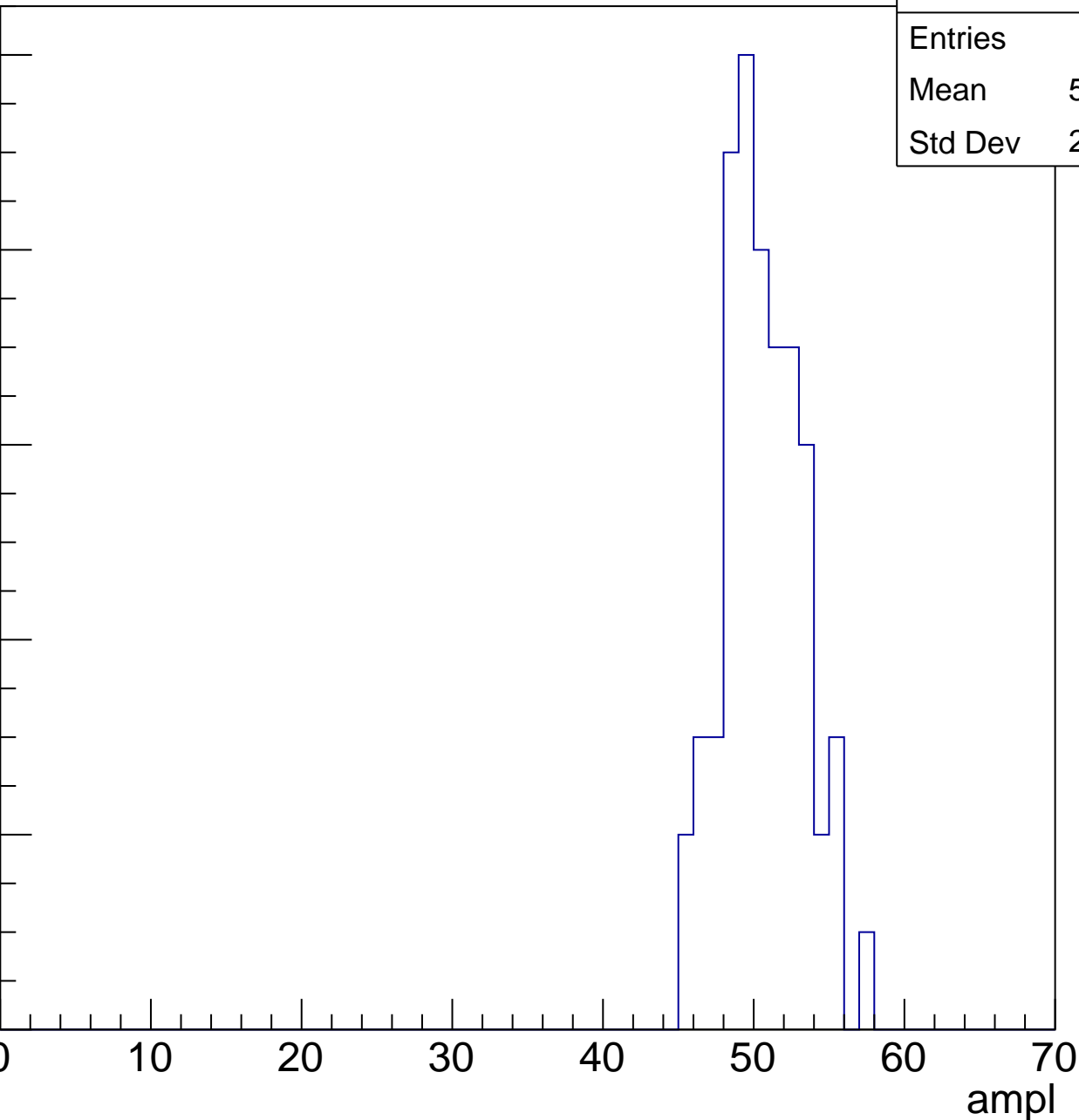
30

40

50

60

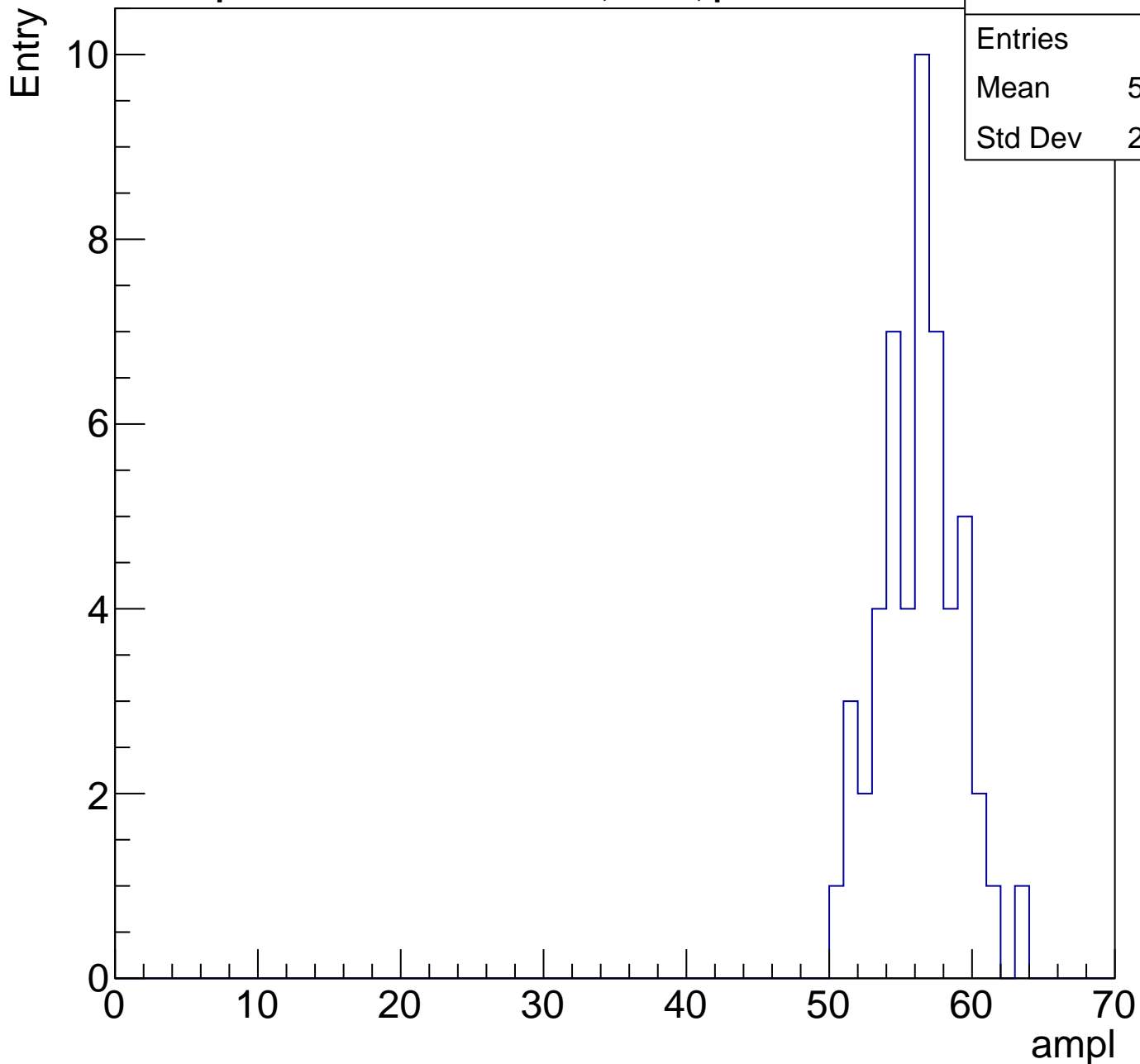
ampl



B0L002S, U12-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	51
Mean	55.82
Std Dev	2.742

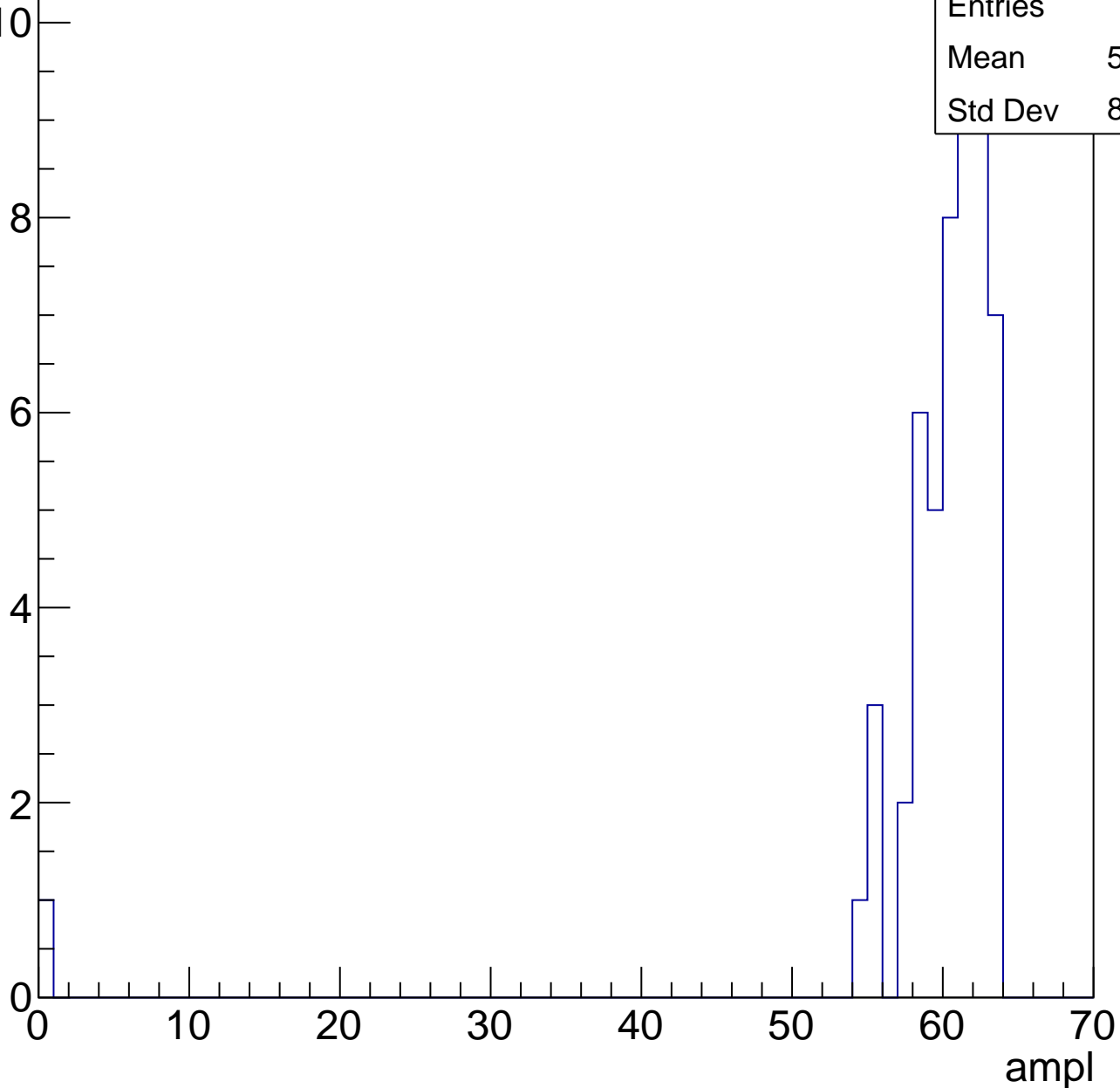


B0L002S, U12-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	58.96
Std Dev	8.564



B0L002S, U12-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch105, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	31.24
Std Dev	5.069

Gaus mean : 32.3247

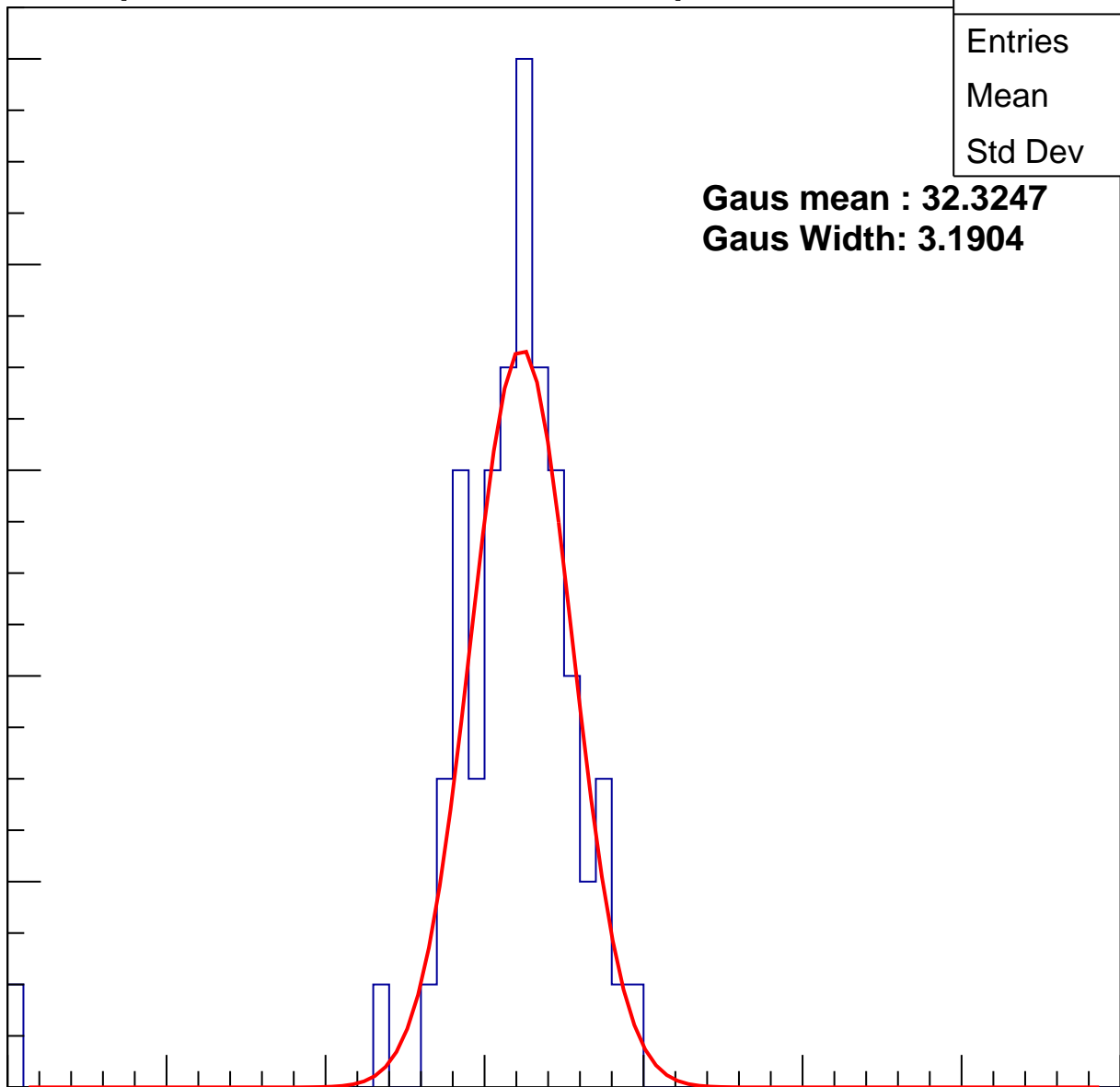
Gaus Width: 3.1904

Entry

10
8
6
4
2
0

ampl

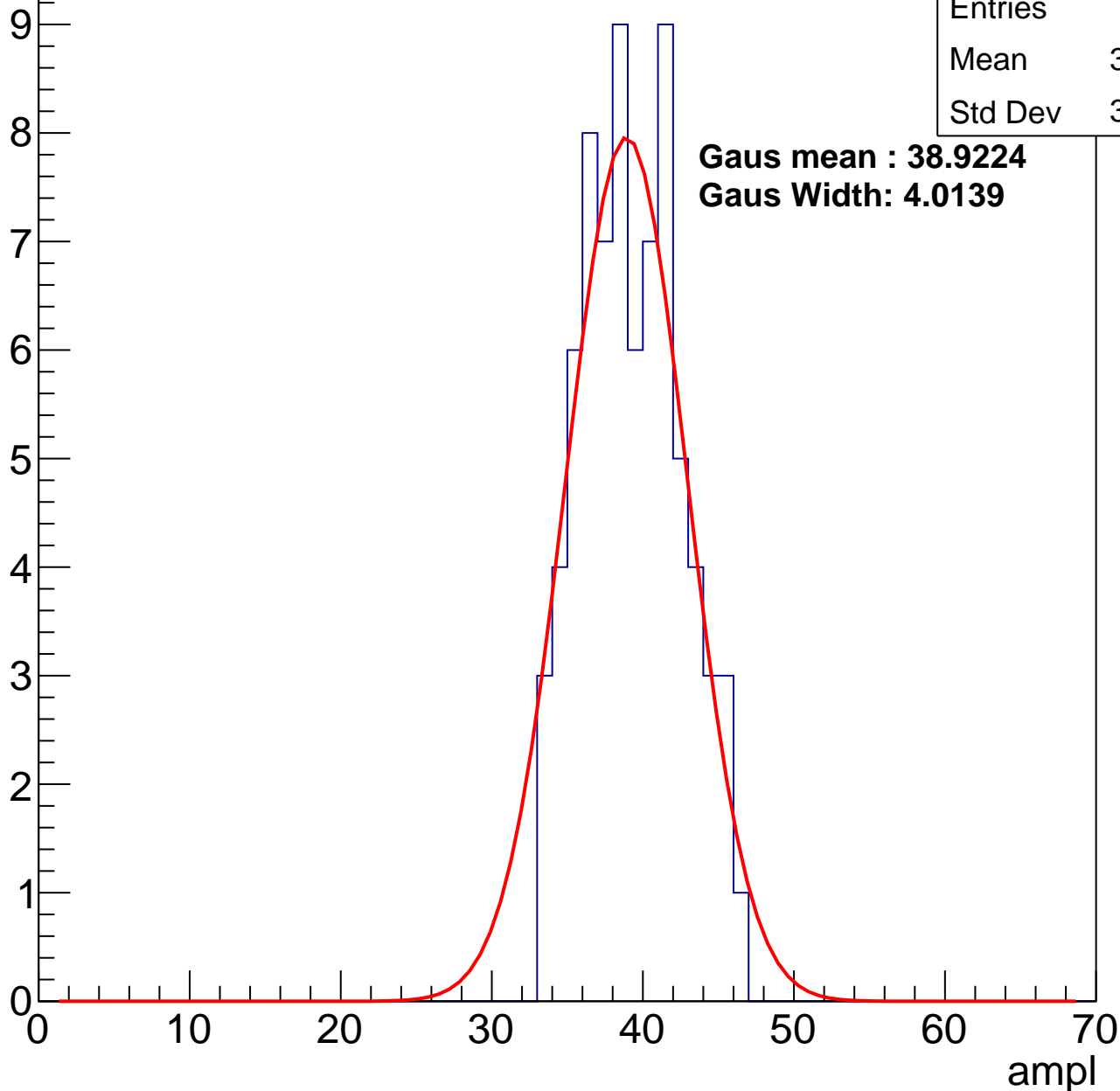
0 10 20 30 40 50 60 70



B0L002S, U12-ch105, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch105, adc2

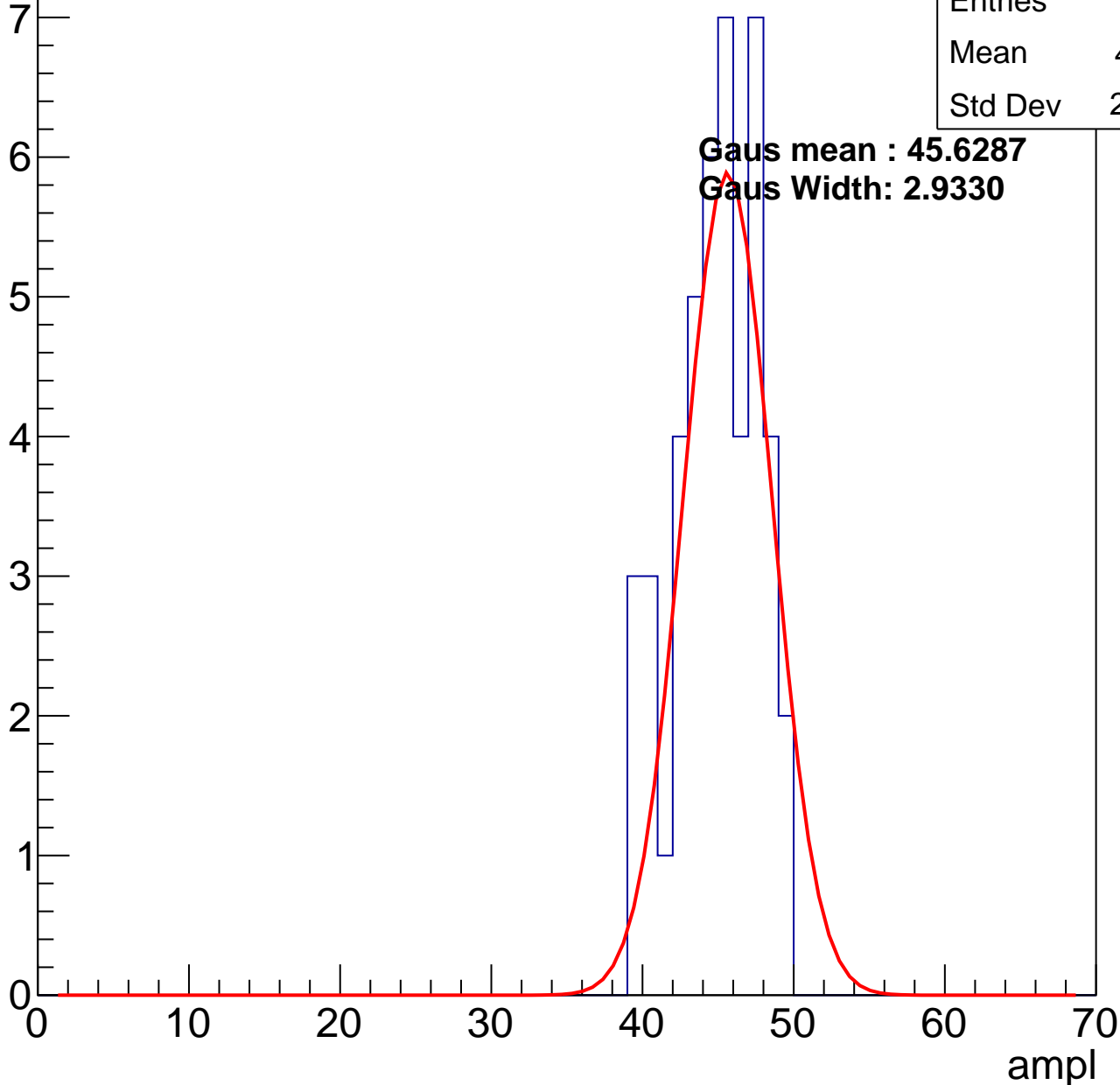
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	44.41
Std Dev	2.739

Gaus mean : 45.6287

Gaus Width: 2.9330



B0L002S, U12-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

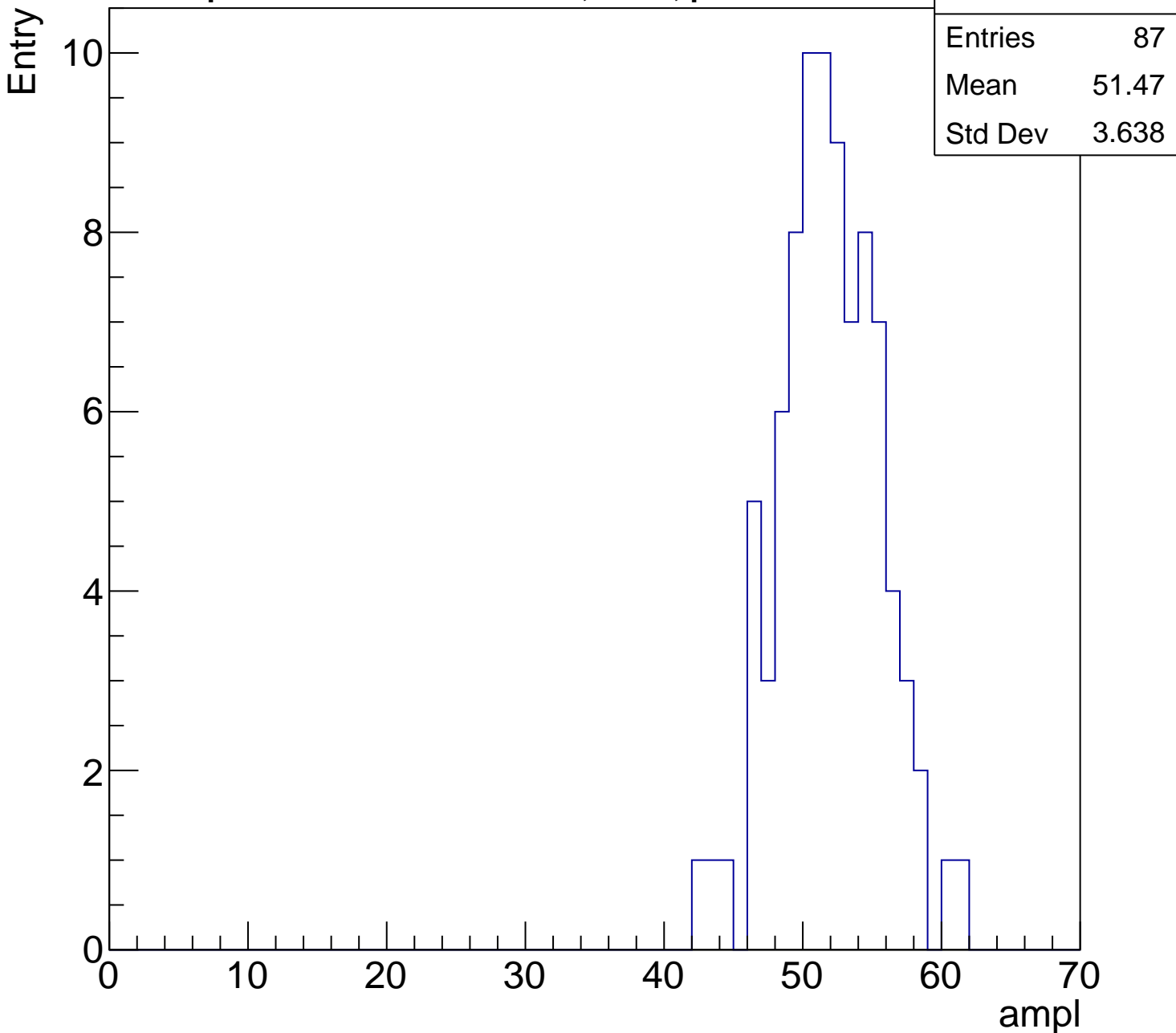
Entries	87
Mean	51.47
Std Dev	3.638

Entry

10
8
6
4
2
0

ampl

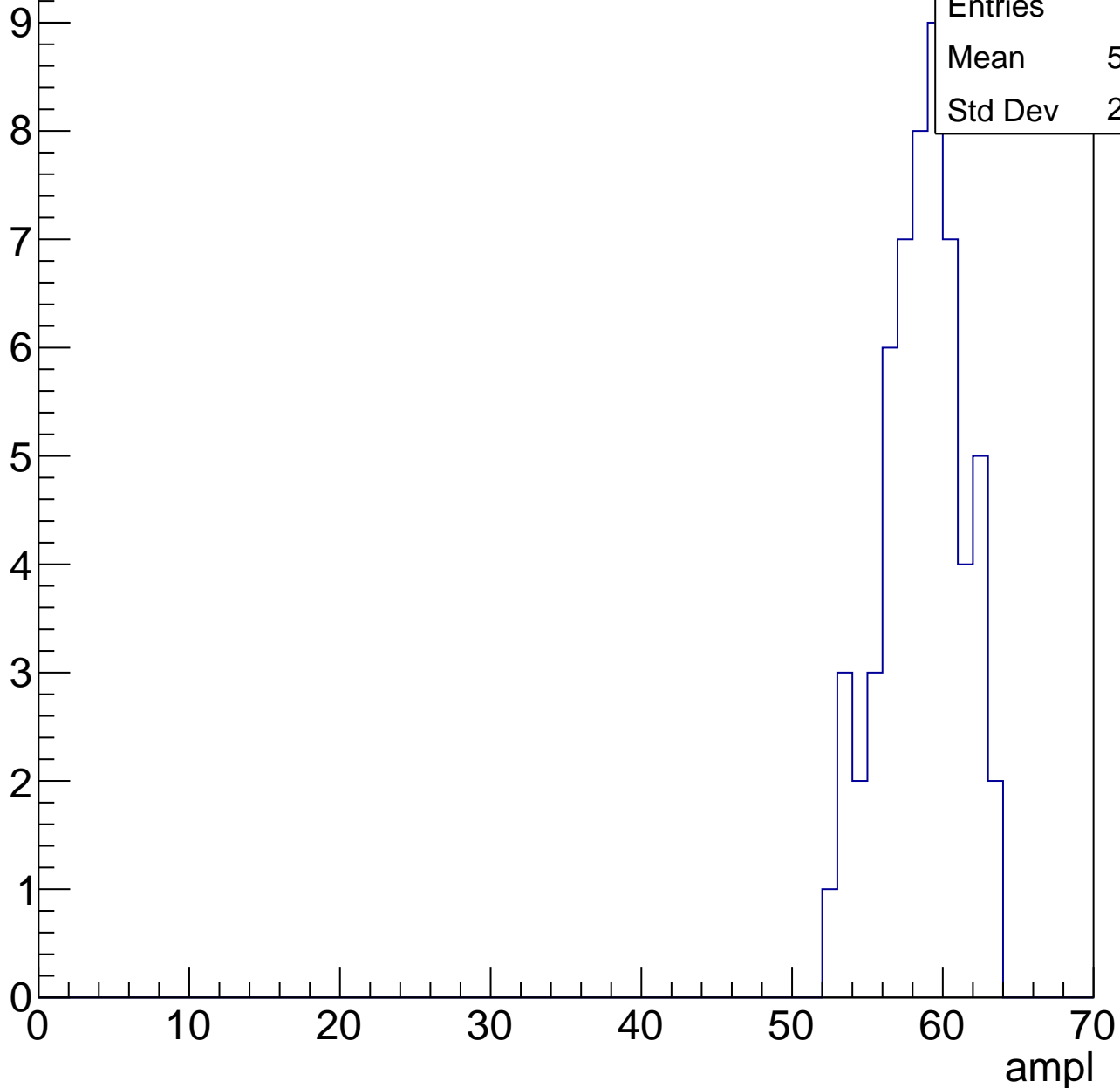
0 10 20 30 40 50 60 70



B0L002S, U12-ch105, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

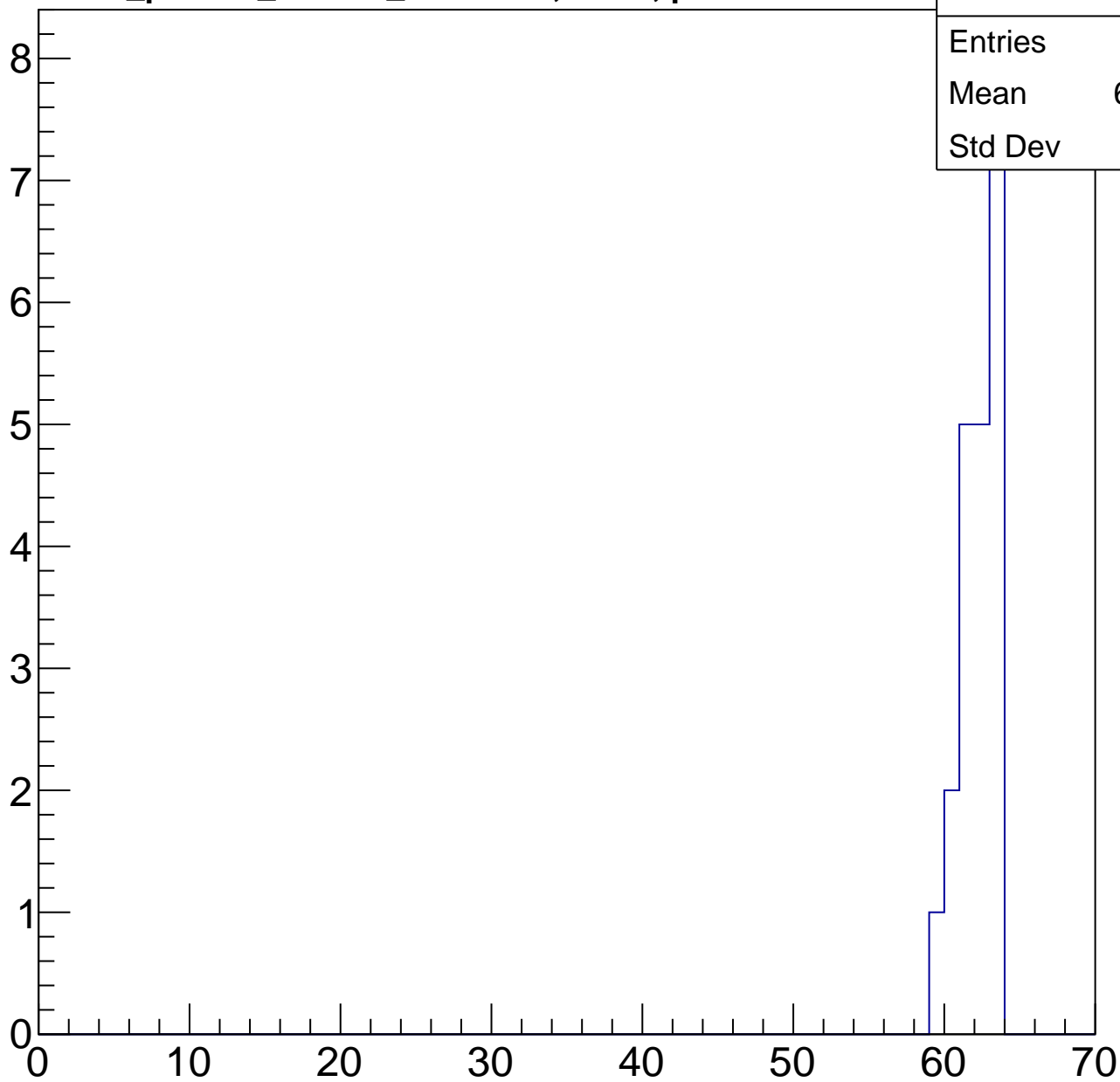
Entry

8
7
6
5
4
3
2
1
0

Entries	21
Mean	61.81
Std Dev	1.18

ampl

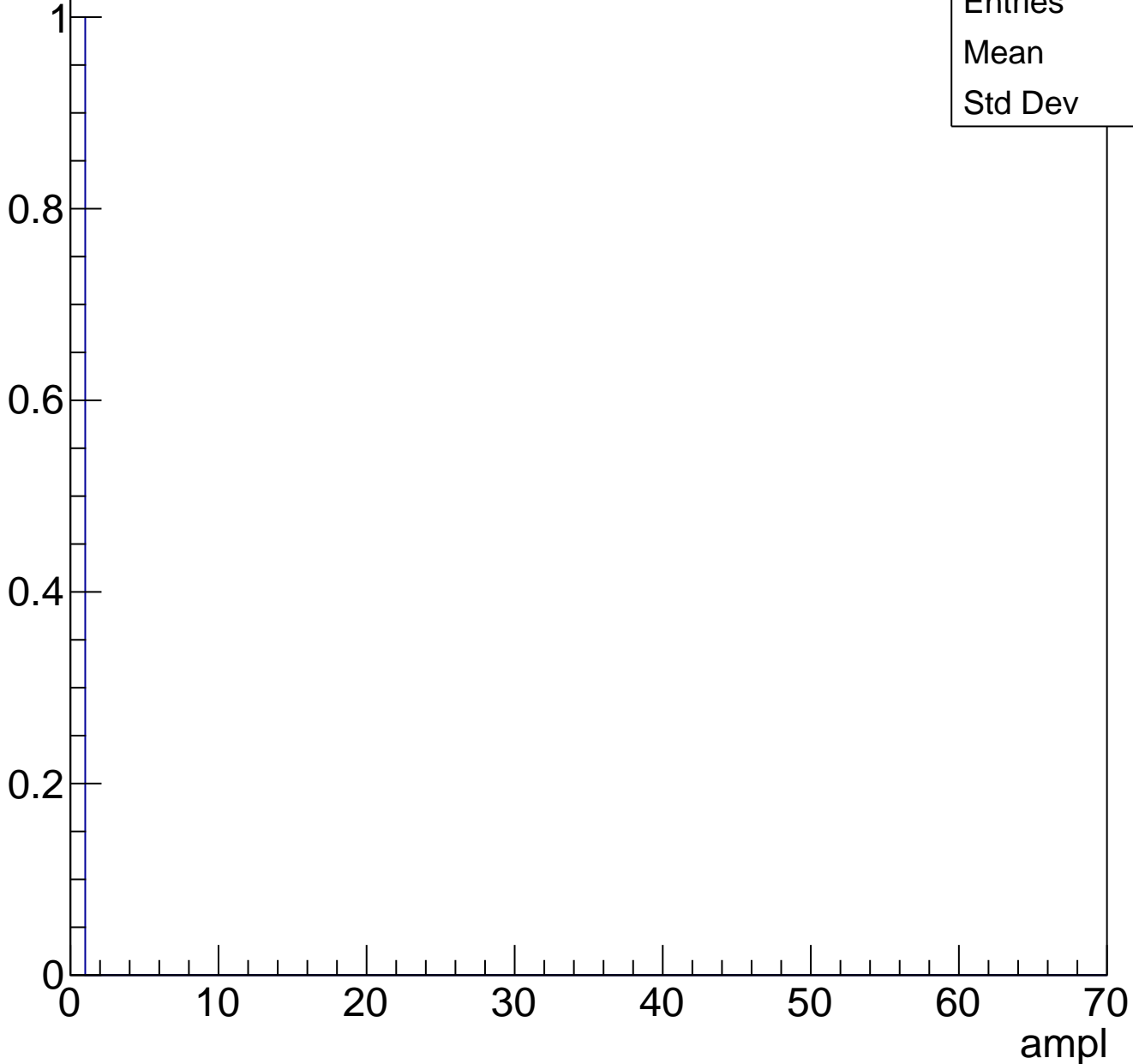
0 10 20 30 40 50 60 70



B0L002S, U12-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch106, adc0

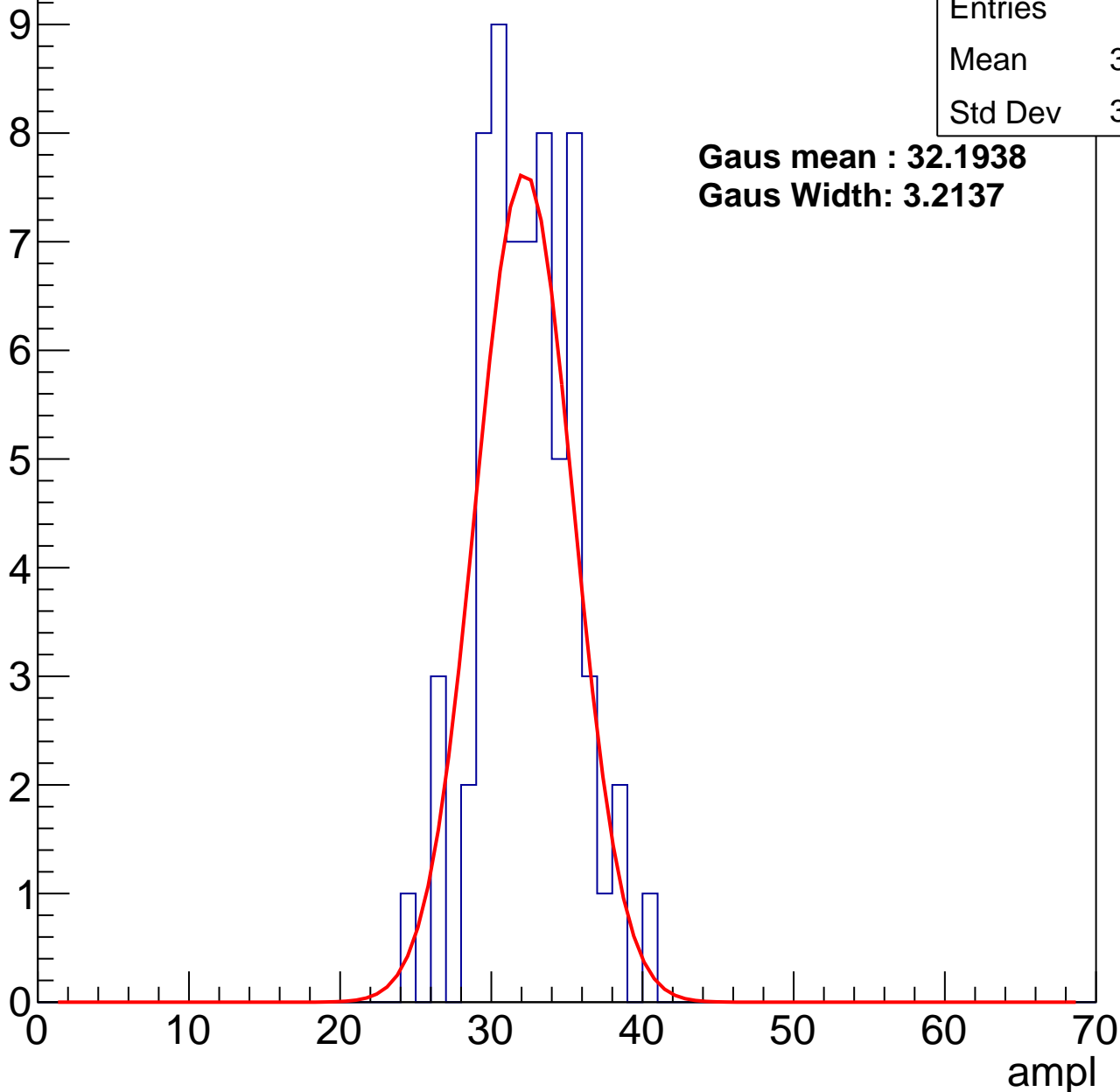
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	31.94
Std Dev	3.108

Gaus mean : 32.1938

Gaus Width: 3.2137



B0L002S, U12-ch106, adc1

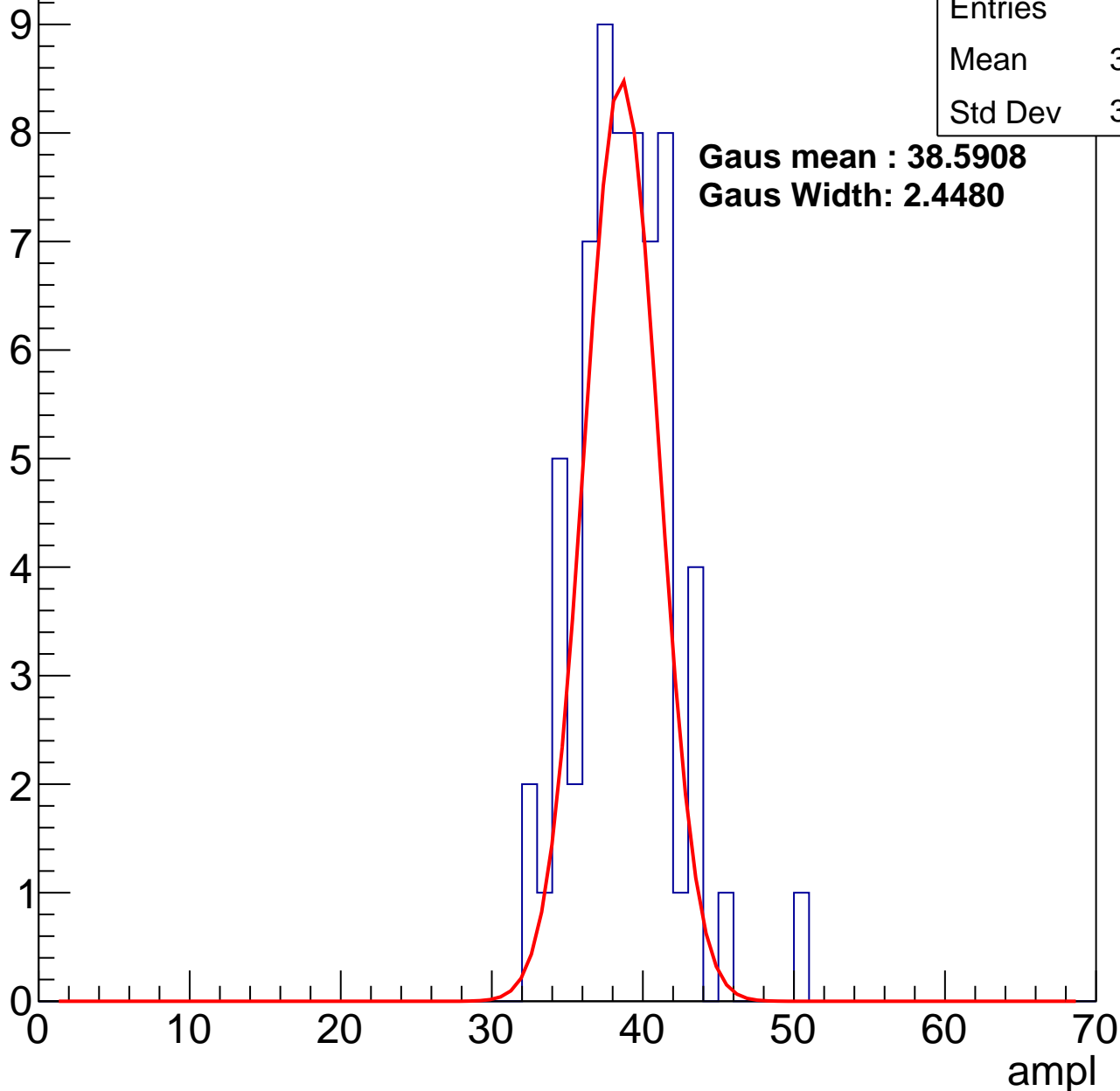
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.36
Std Dev	3.164

Gaus mean : 38.5908

Gaus Width: 2.4480



B0L002S, U12-ch106, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	45.98
Std Dev	3.634

Gaus mean : 46.3434

Gaus Width: 3.7665

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

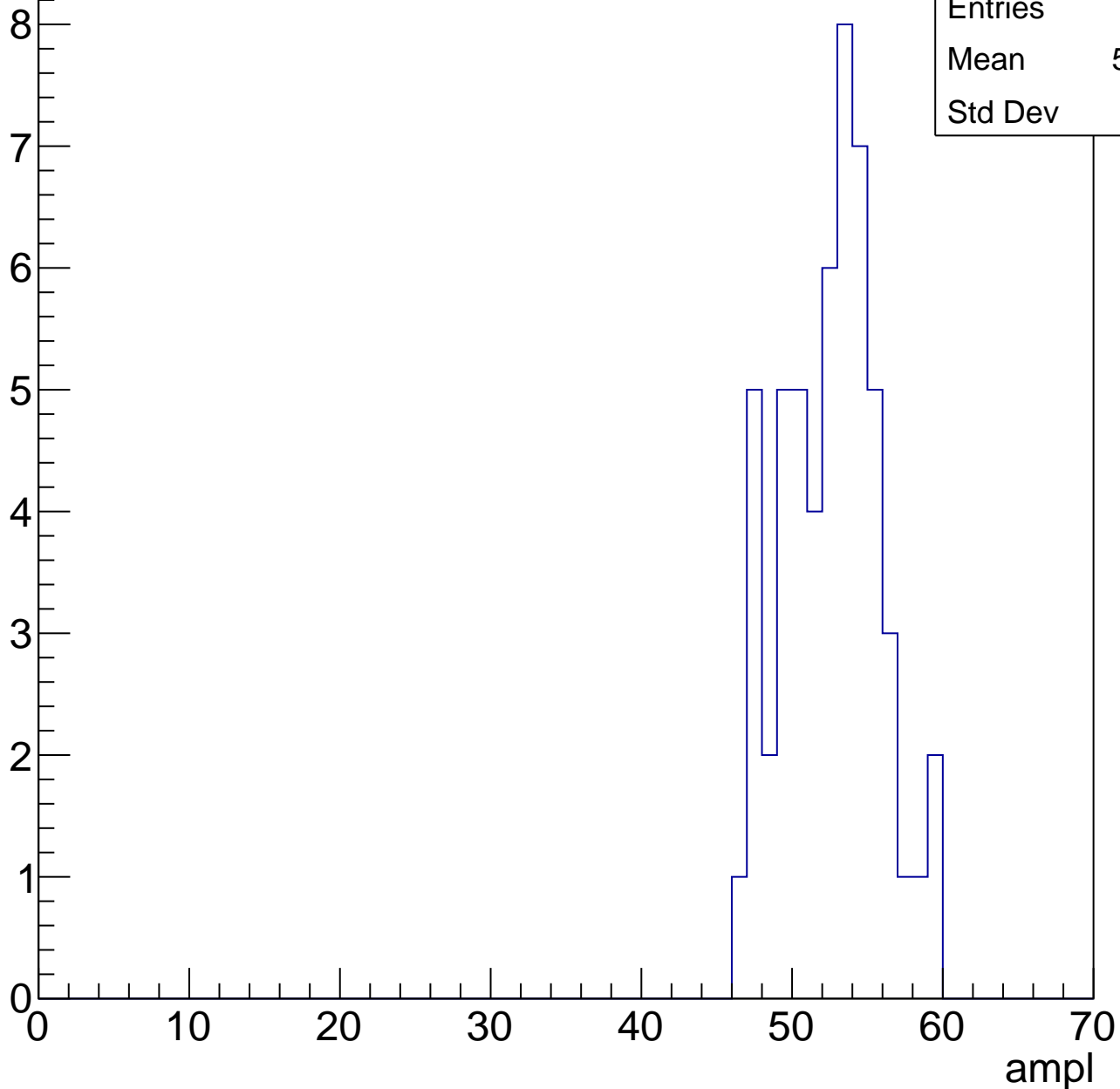
70

B0L002S, U12-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

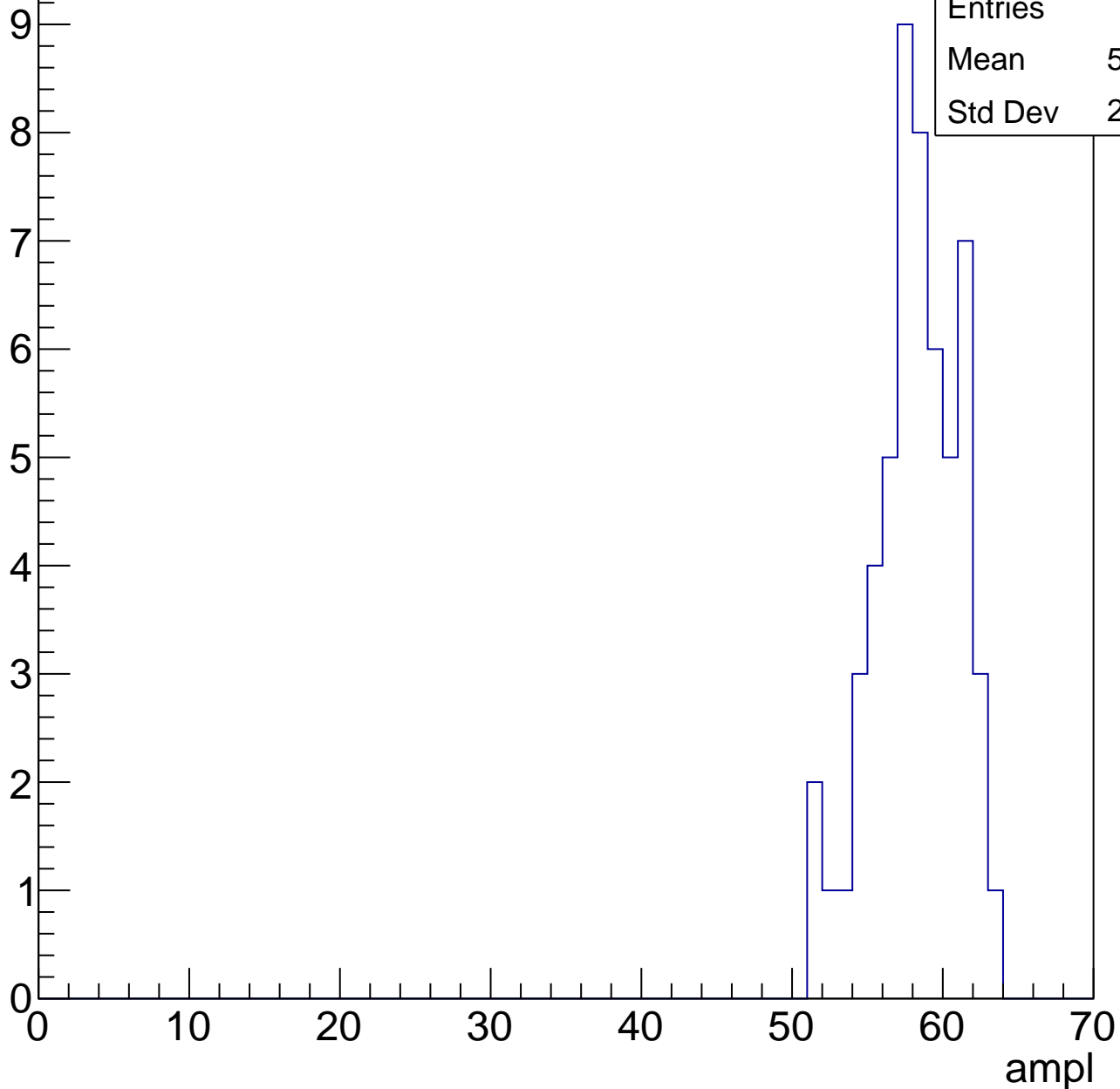
Entries	55
Mean	52.11
Std Dev	3.16



B0L002S, U12-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



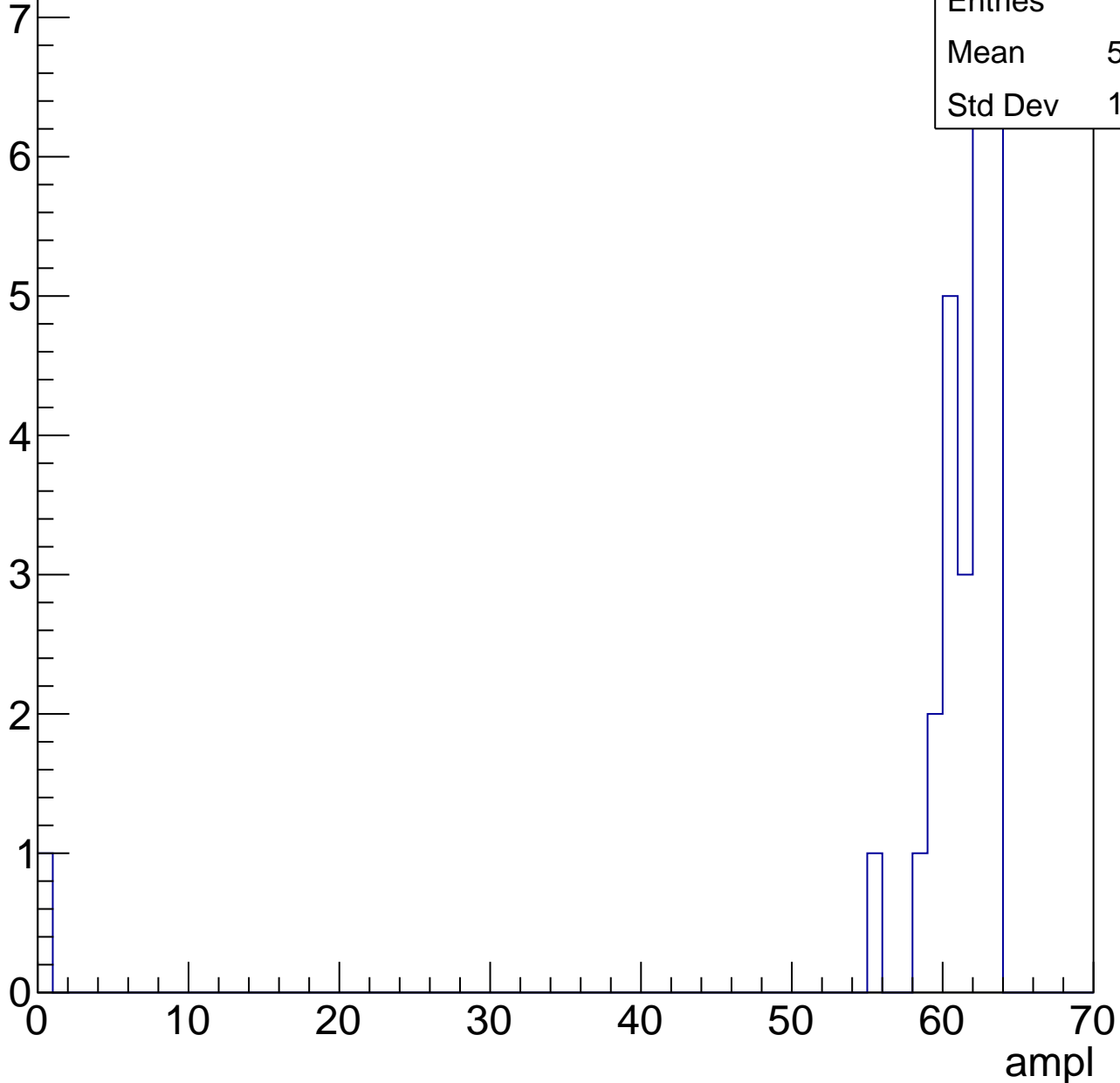
Entries	55
Mean	57.75
Std Dev	2.798

B0L002S, U12-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.85
Std Dev	11.69



B0L002S, U12-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch107, adc0

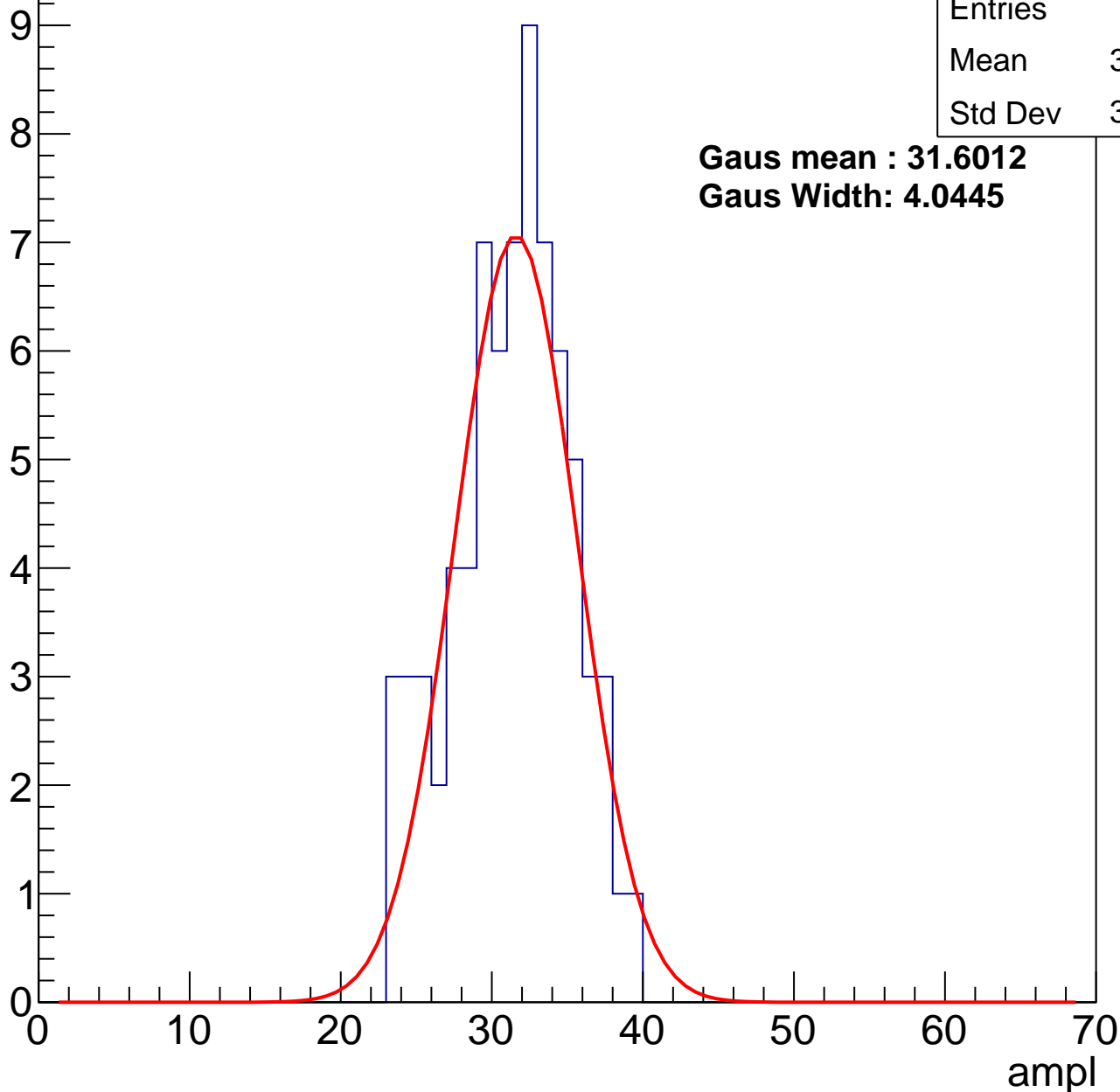
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	30.84
Std Dev	3.849

Gaus mean : 31.6012

Gaus Width: 4.0445



B0L002S, U12-ch107, adc1

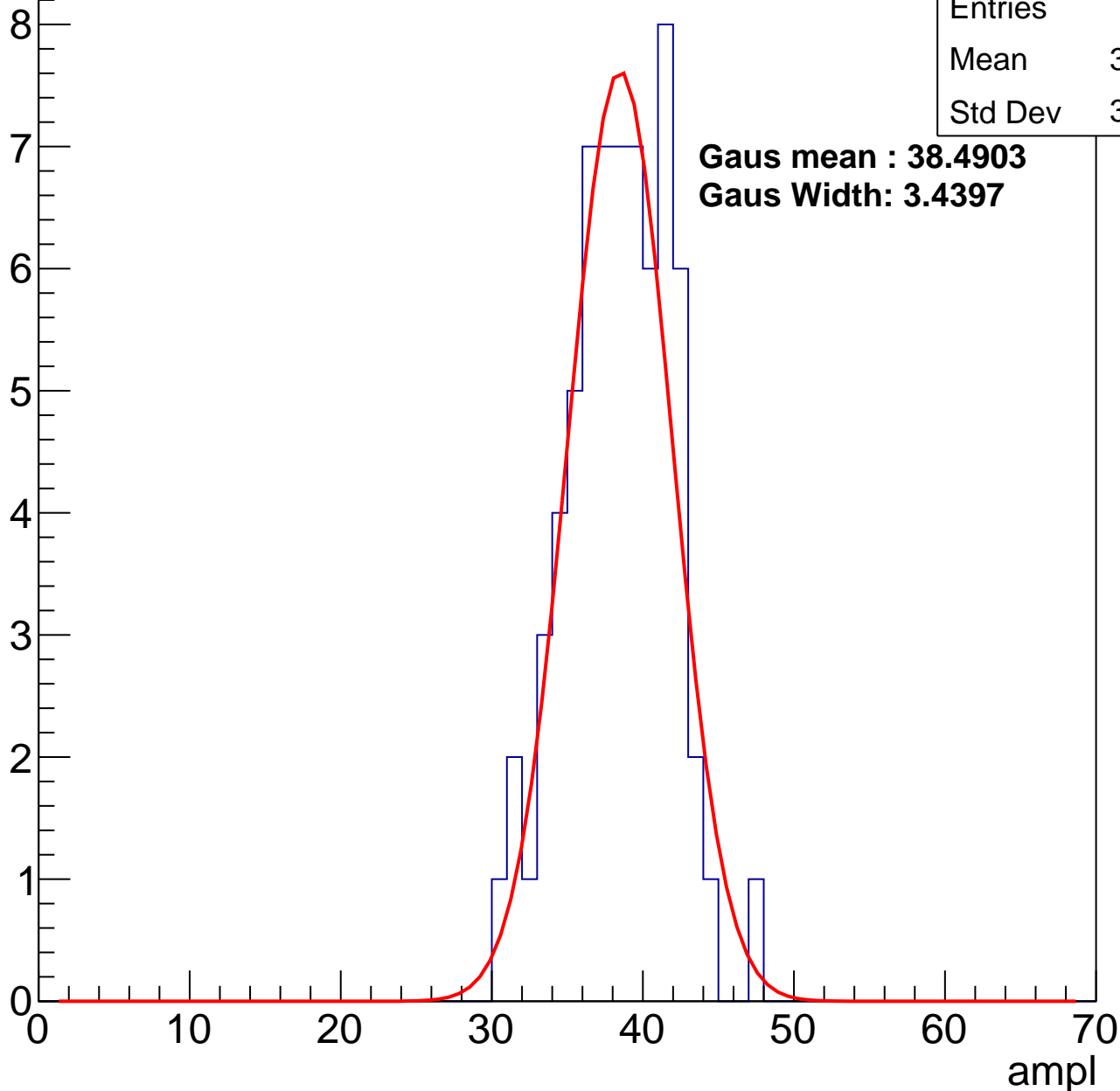
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.96
Std Dev	3.393

Gaus mean : 38.4903

Gaus Width: 3.4397



B0L002S, U12-ch107, adc2

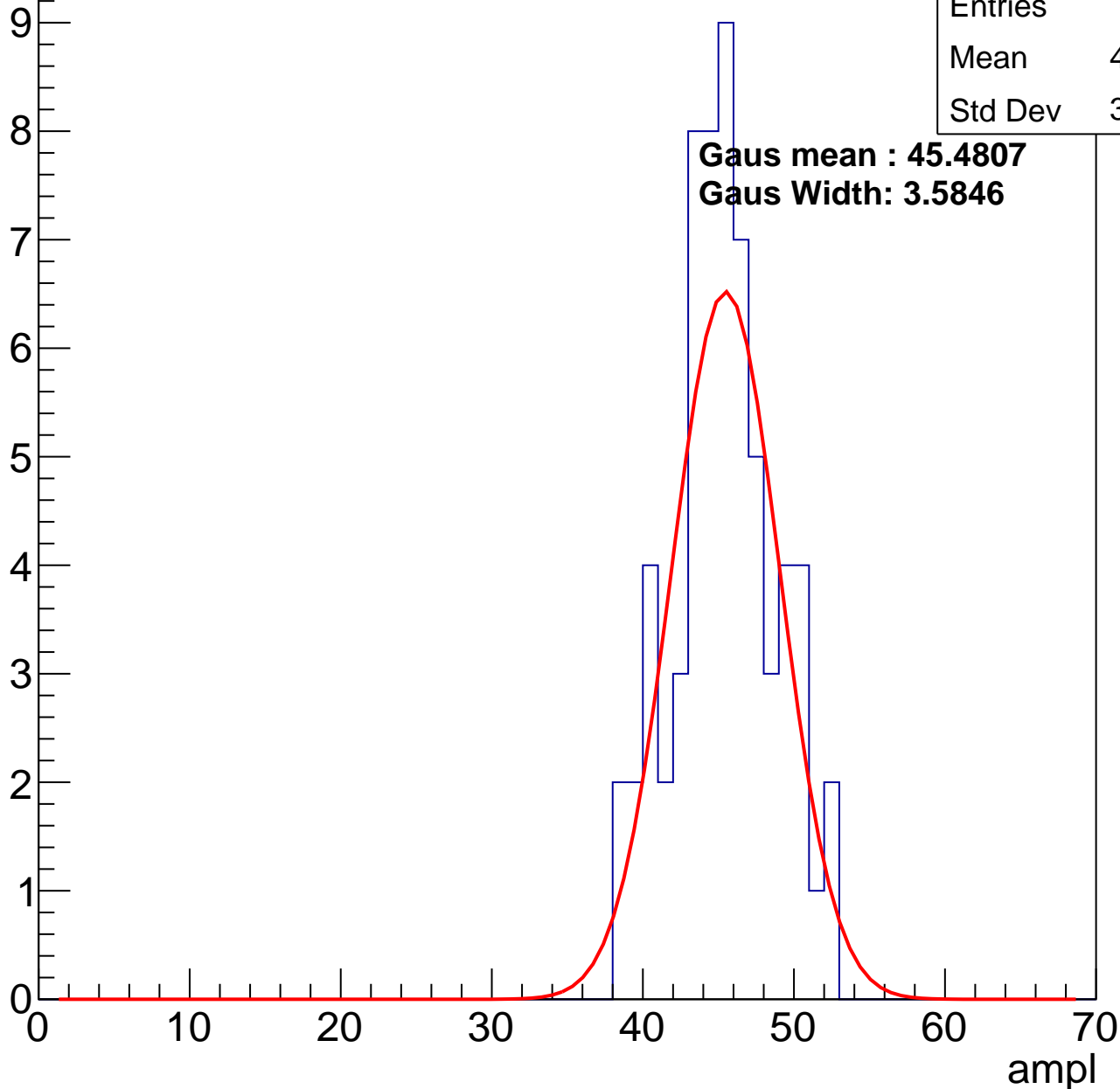
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	44.92
Std Dev	3.356

Gaus mean : 45.4807

Gaus Width: 3.5846

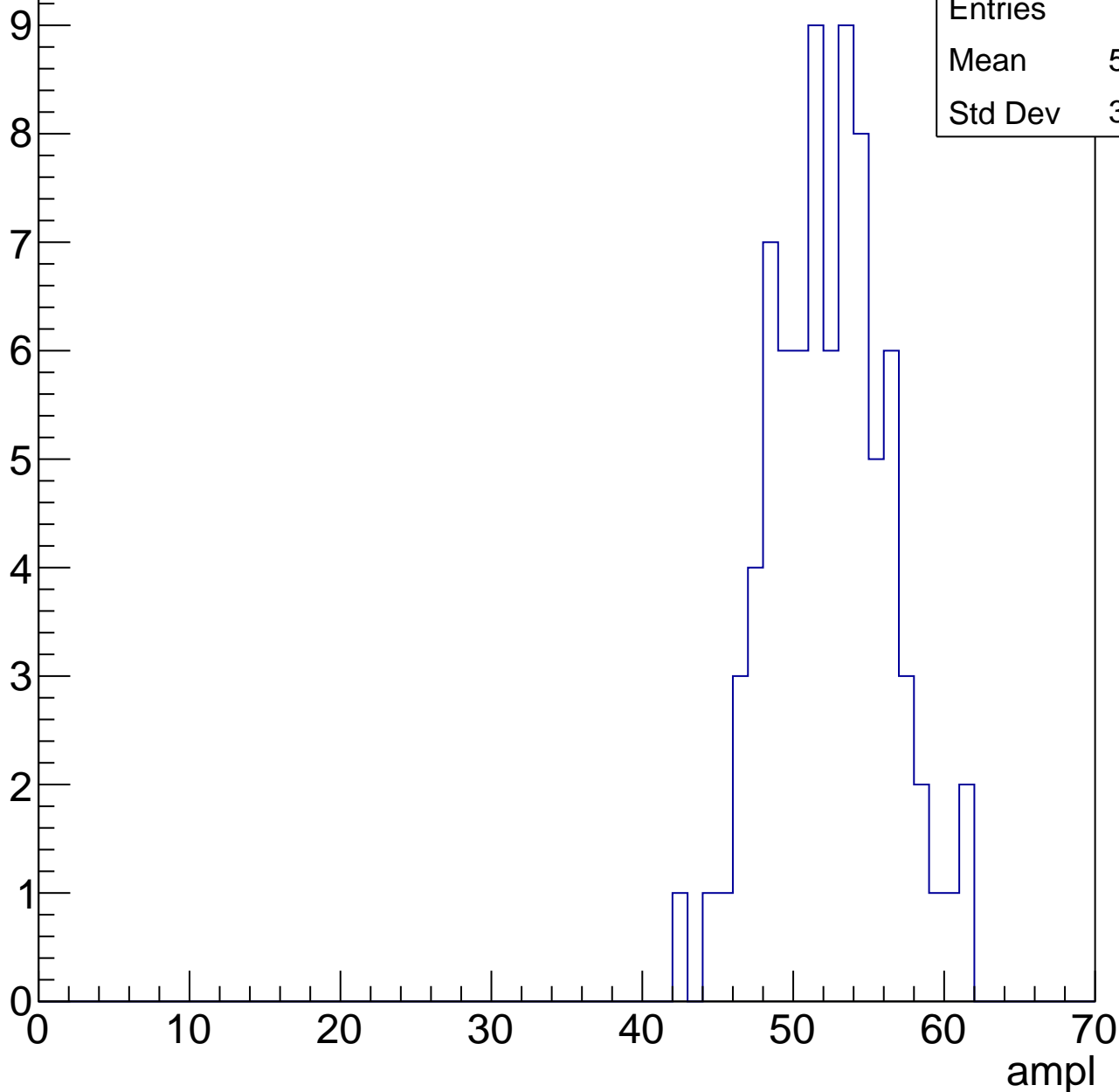


B0L002S, U12-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

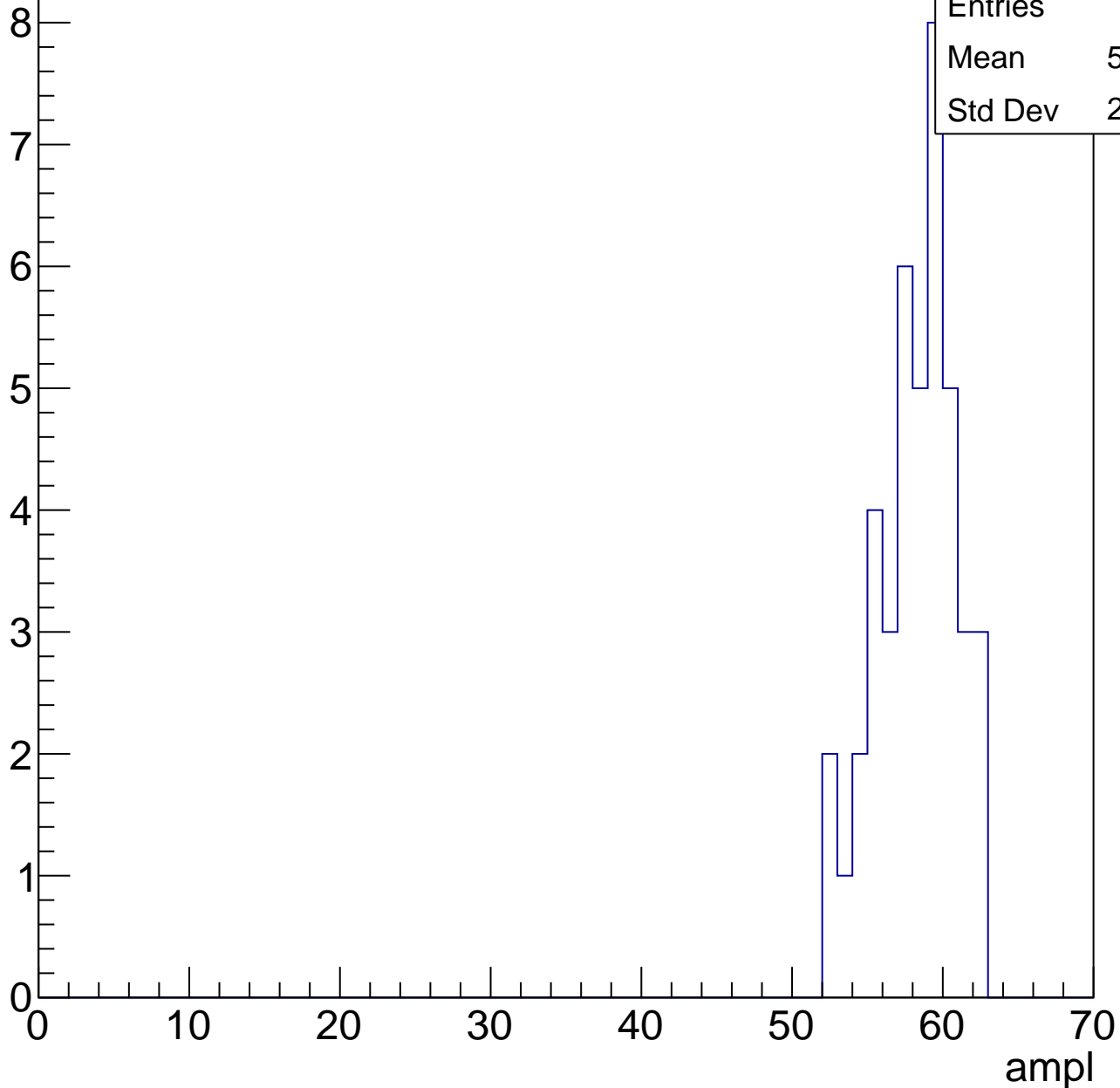
Entries	81
Mean	51.93
Std Dev	3.887



B0L002S, U12-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

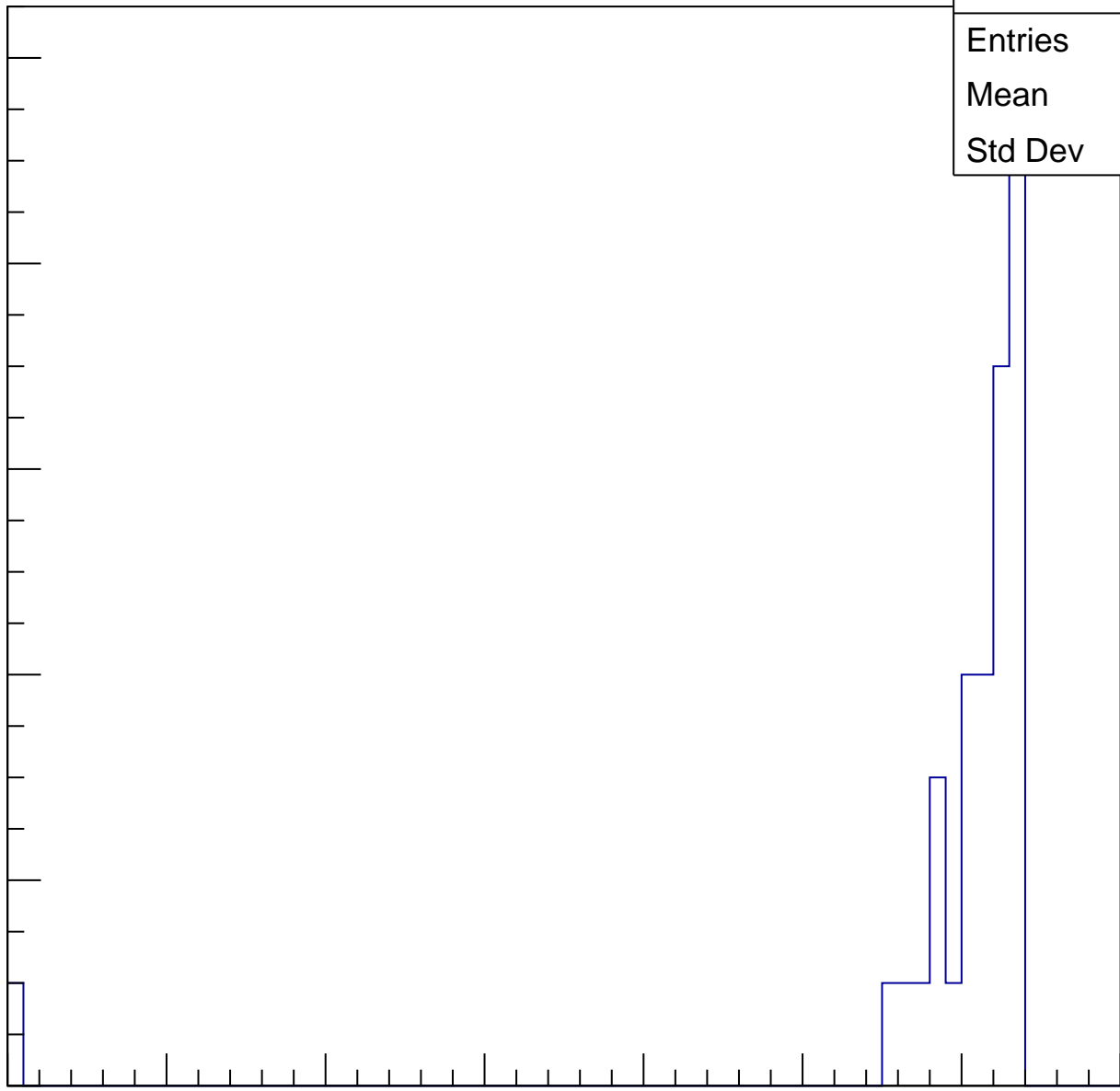
Entries	33
Mean	59.06
Std Dev	10.67

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

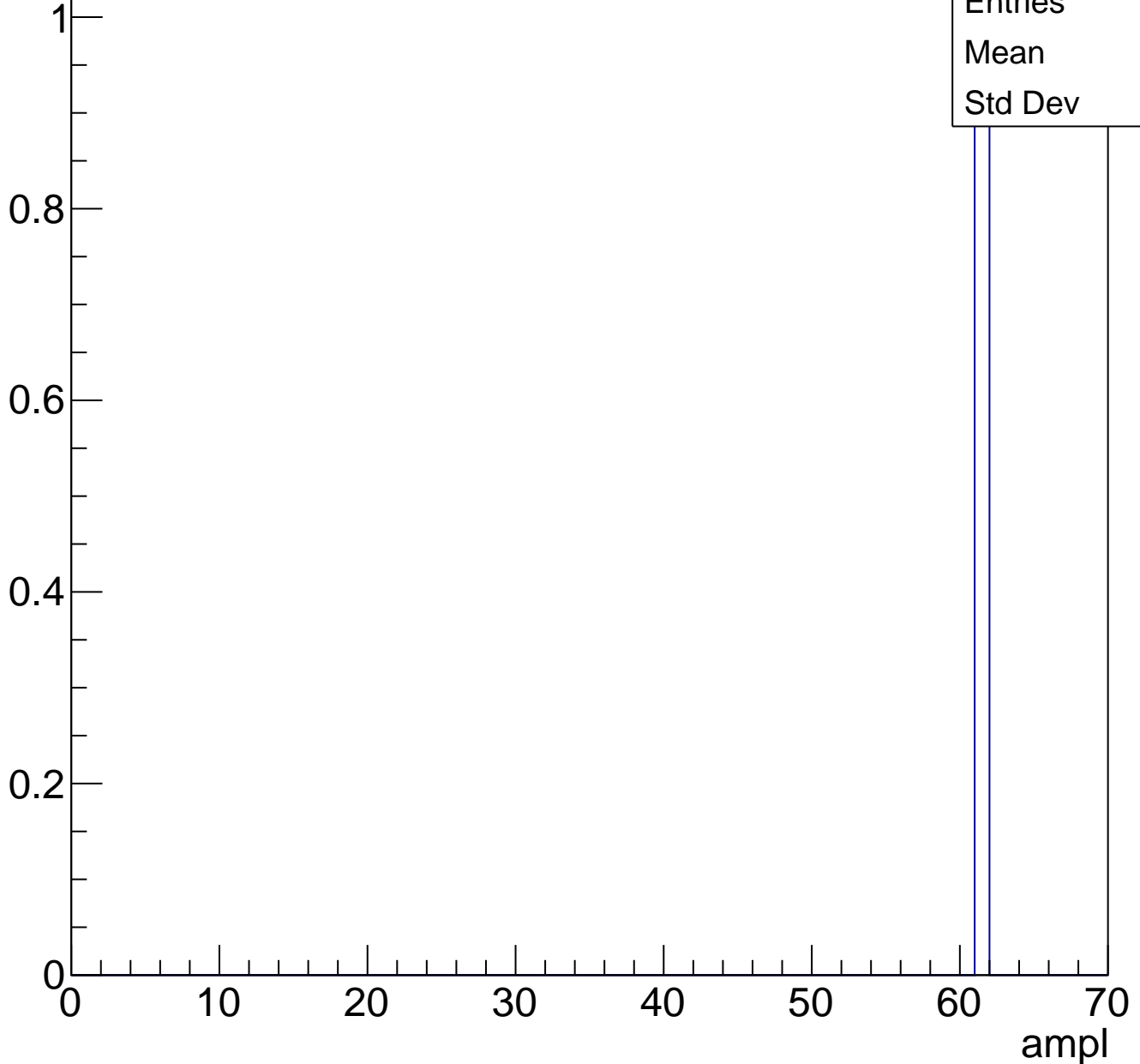
ampl



B0L002S, U12-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

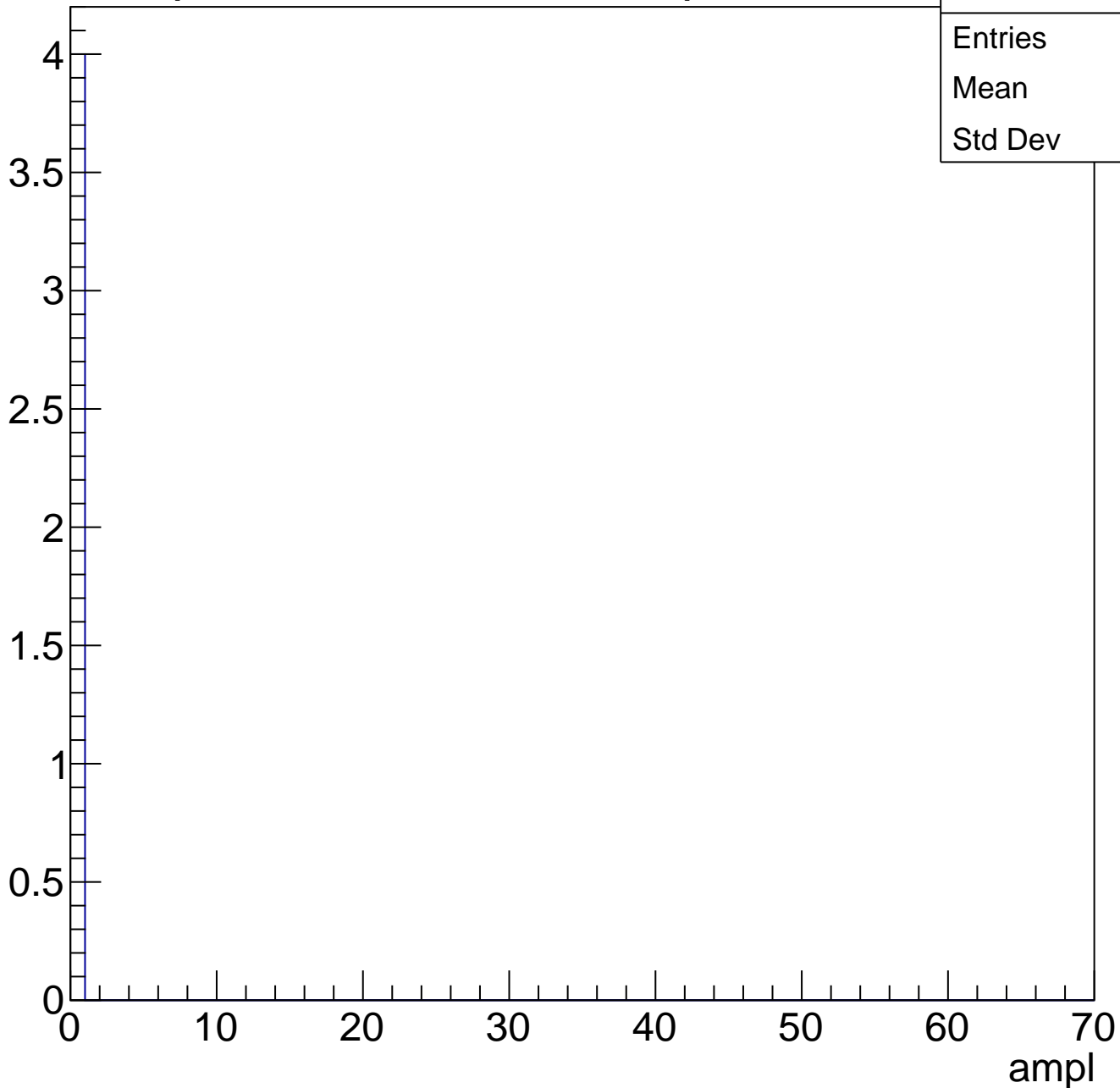
Entry



B0L002S, U12-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	0
Std Dev	0

B0L002S, U12-ch108, adc0

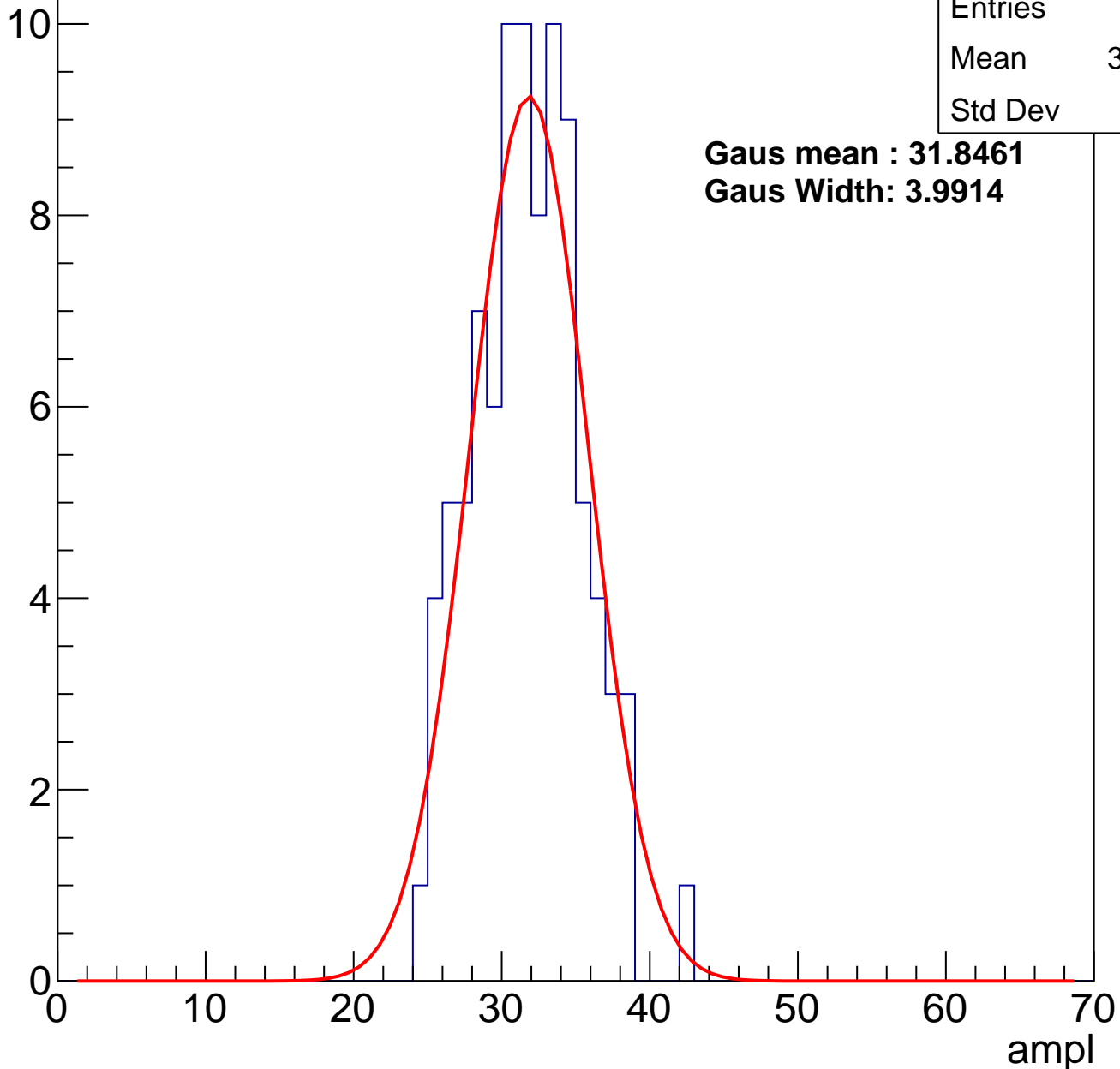
calib_packv5_042523_0143.root, FC#8, port C1

Entries	91
Mean	31.29
Std Dev	3.59

Gaus mean : 31.8461

Gaus Width: 3.9914

Entry



B0L002S, U12-ch108, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	38.97
Std Dev	2.851

Gaus mean : 39.5686

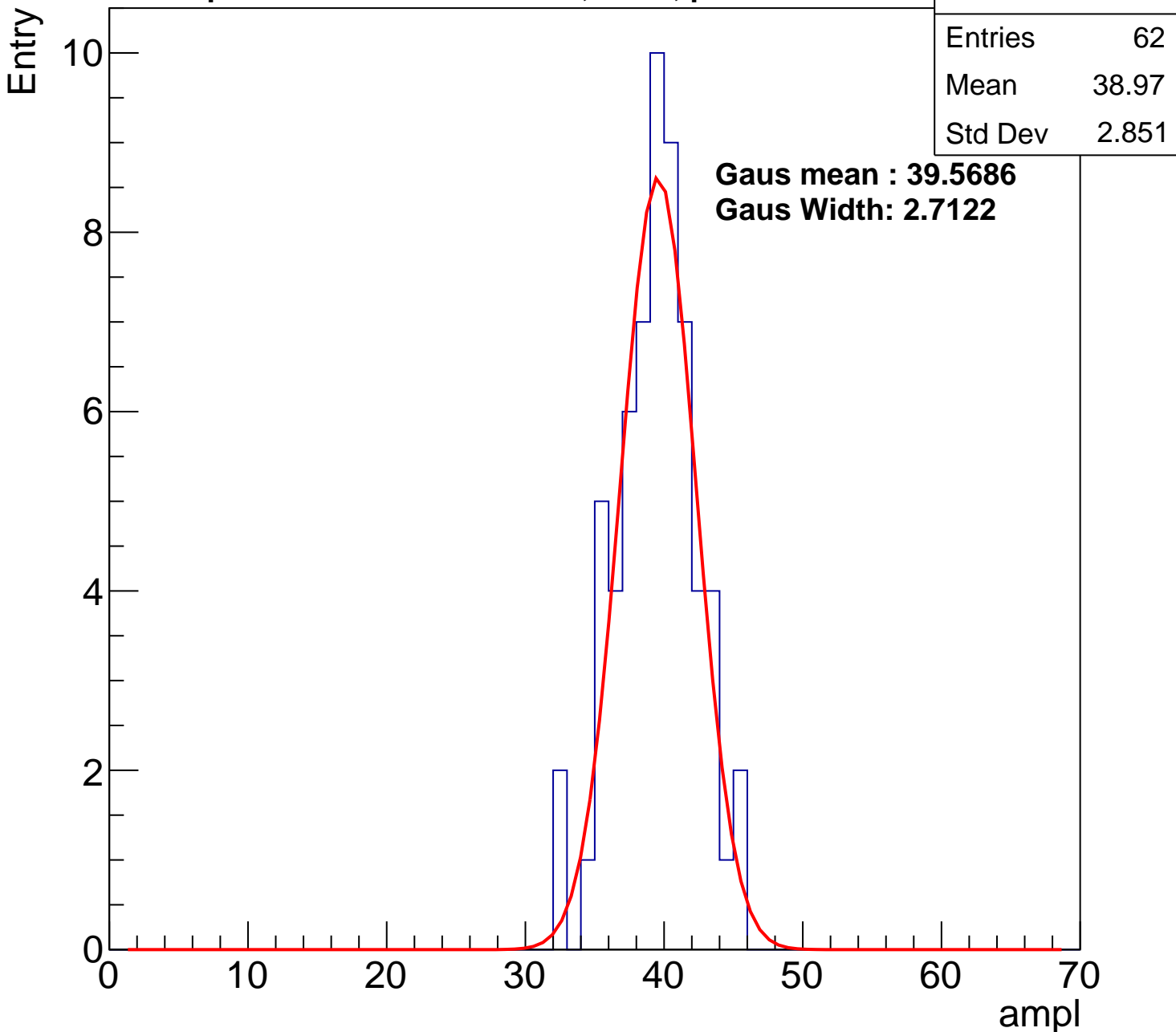
Gaus Width: 2.7122

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch108, adc2

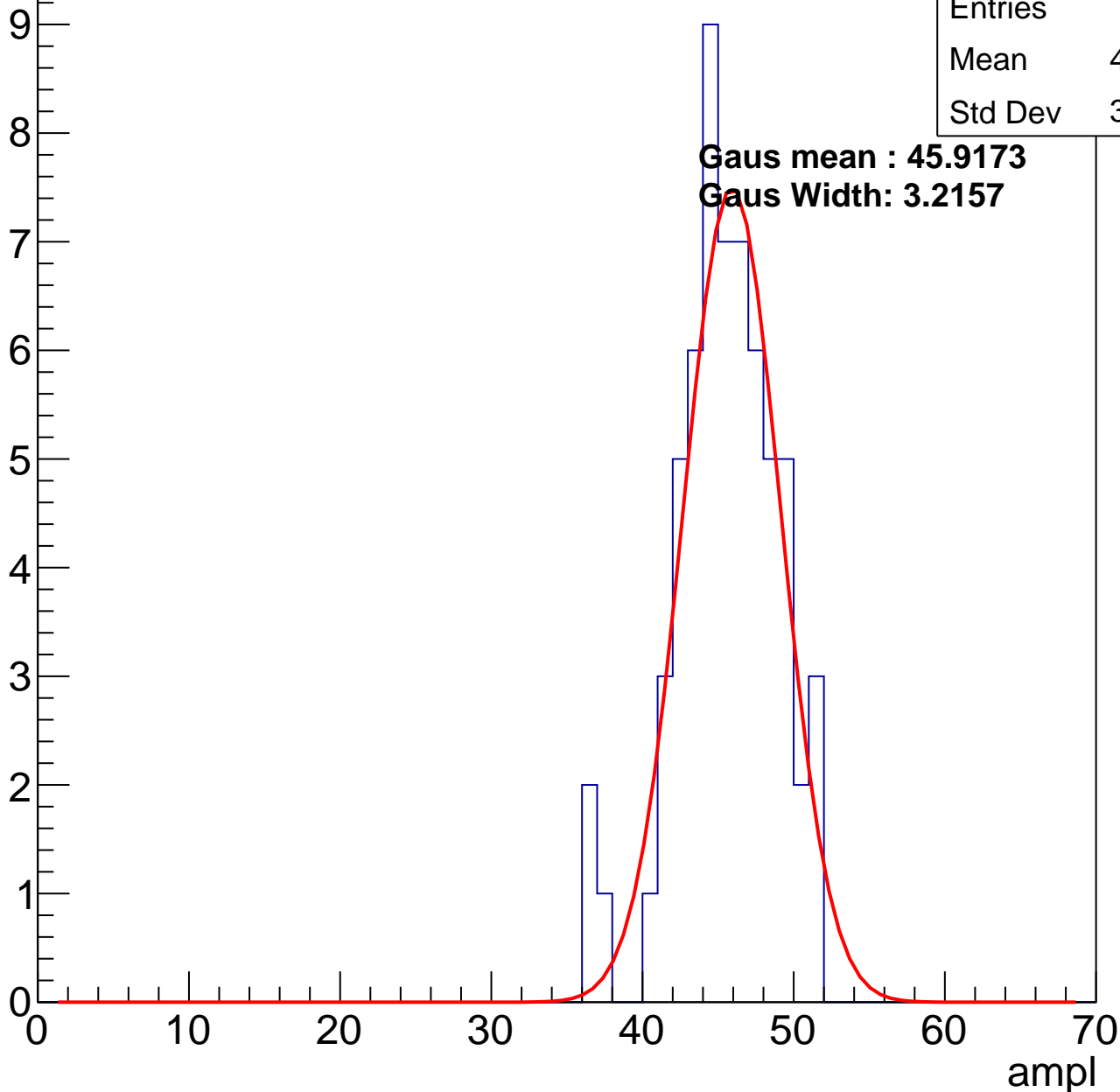
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.05
Std Dev	3.338

Gaus mean : 45.9173

Gaus Width: 3.2157

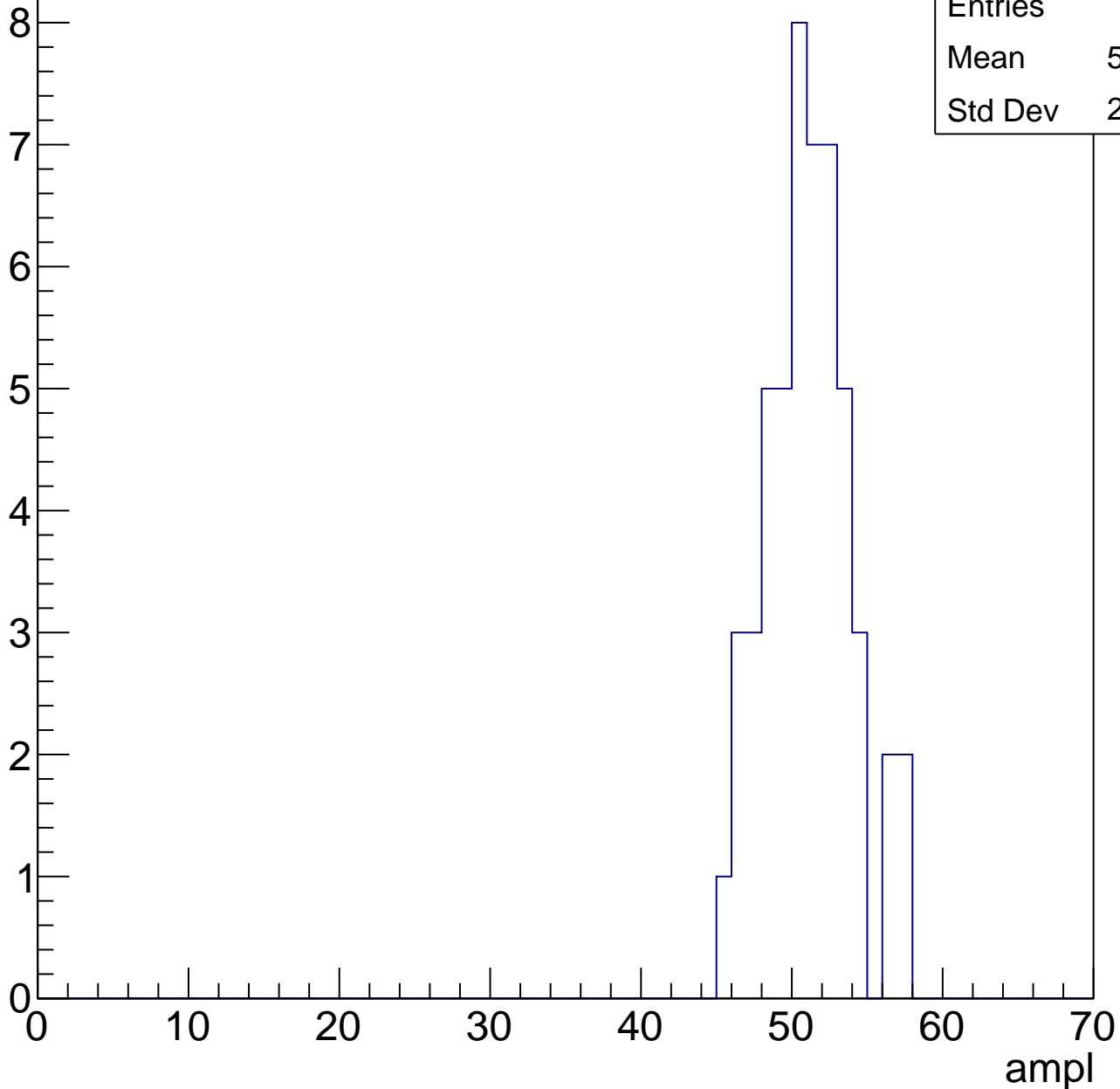


B0L002S, U12-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

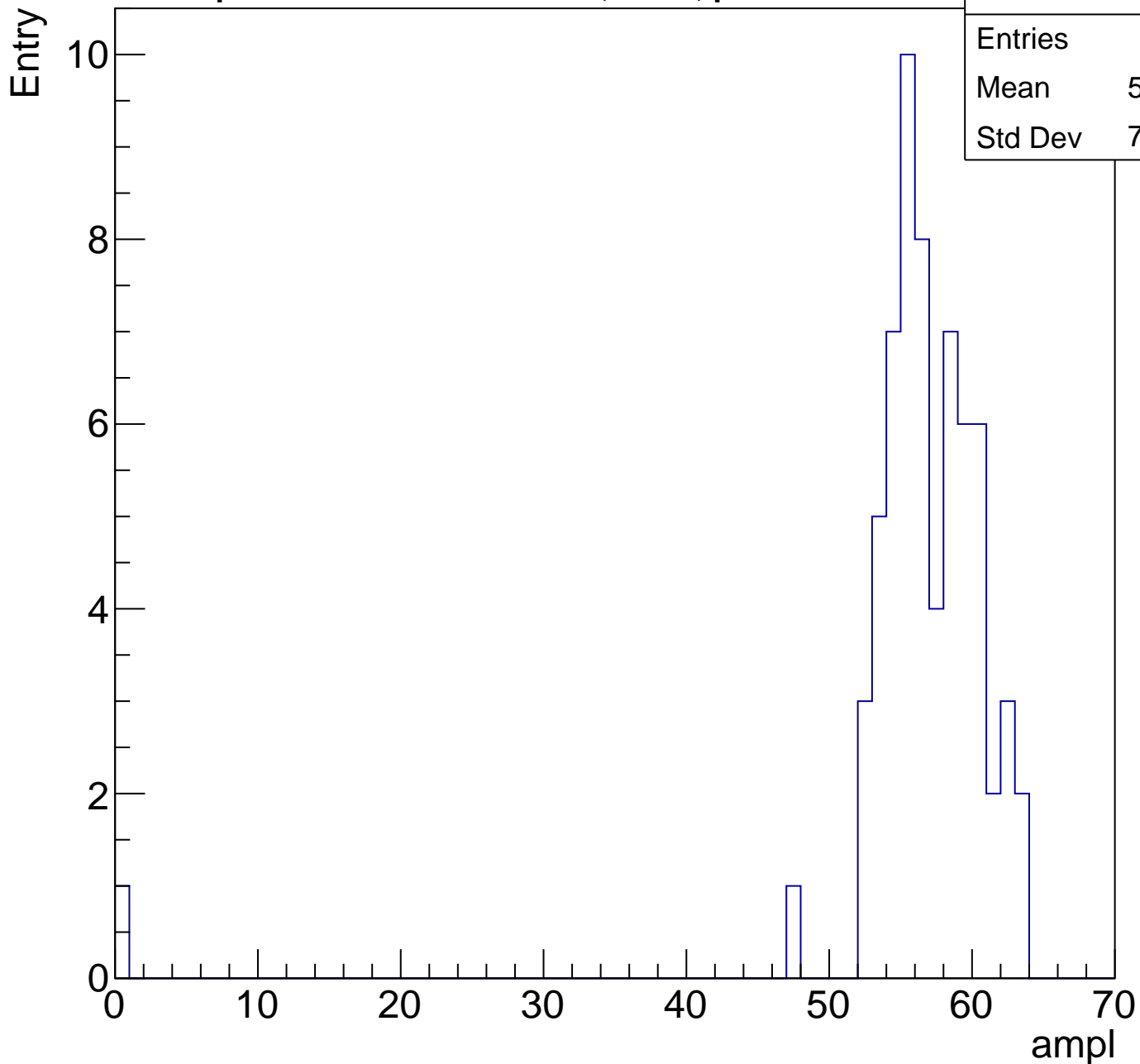
Entries	51
Mean	50.65
Std Dev	2.806



B0L002S, U12-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

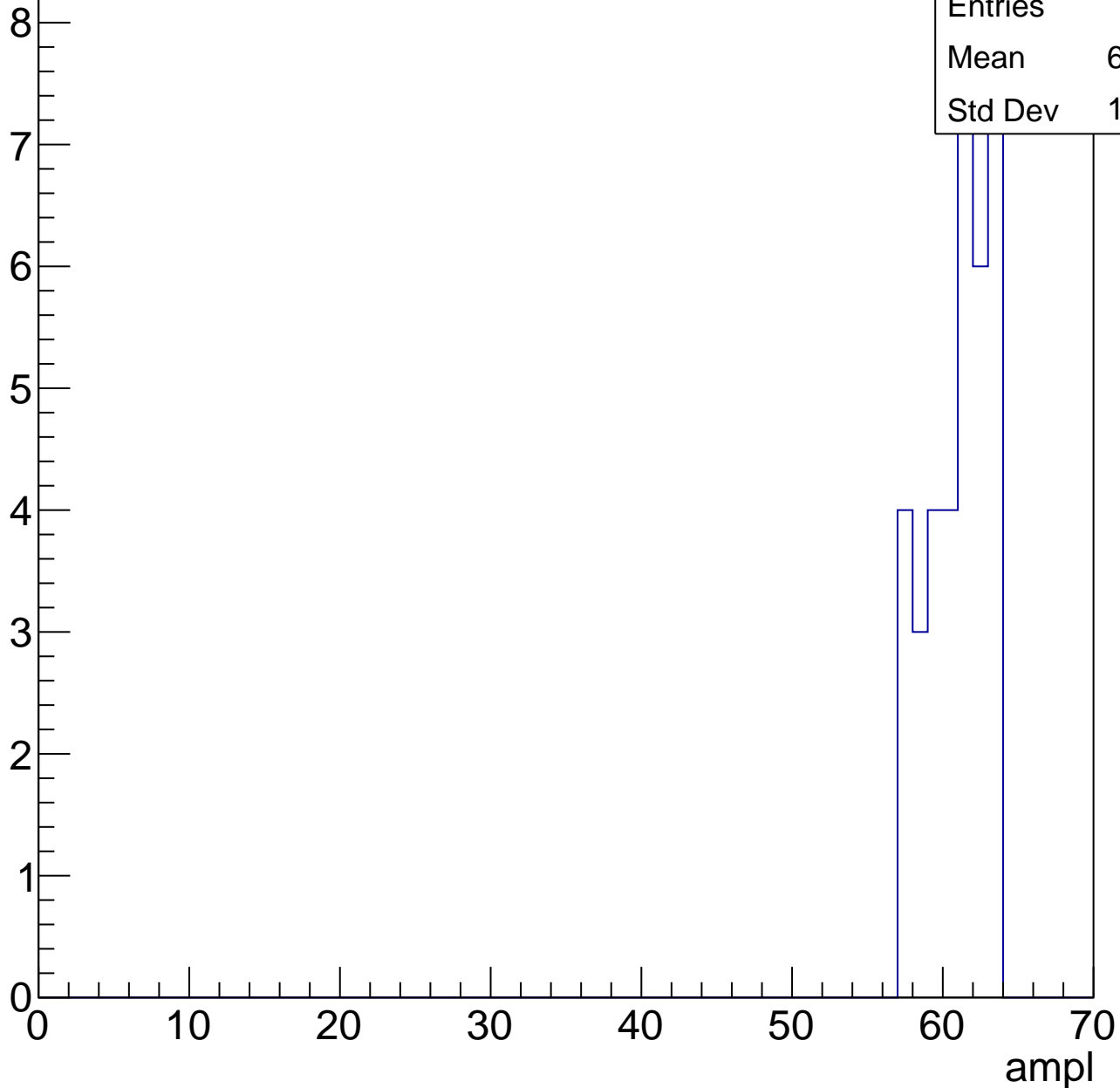
Entries	65
Mean	55.78
Std Dev	7.629



B0L002S, U12-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	37
Mean	60.59
Std Dev	1.965

B0L002S, U12-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch109, adc0

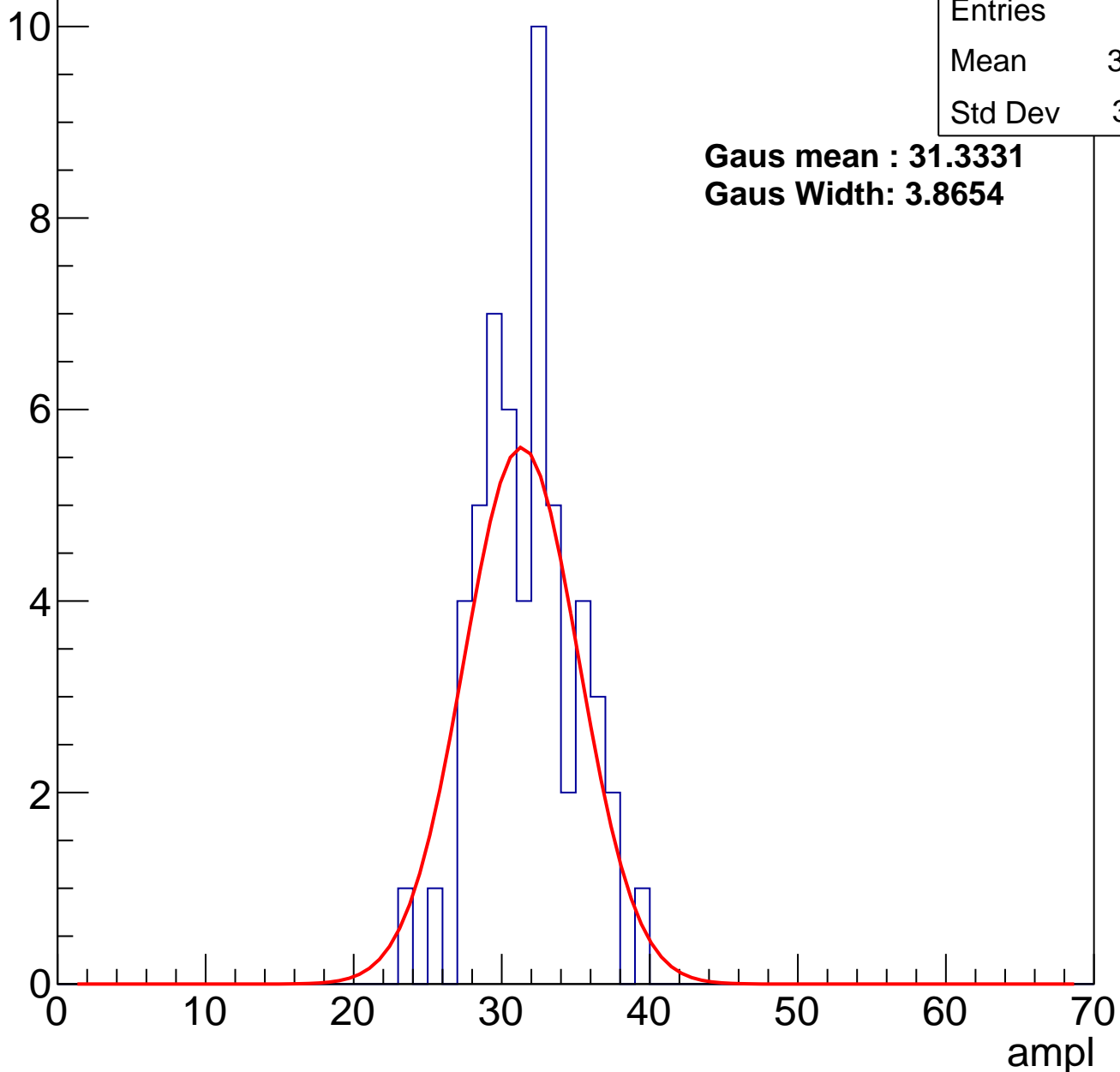
calib_packv5_042523_0143.root, FC#8, port C1

Entries	55
Mean	31.22
Std Dev	3.201

Gaus mean : 31.3331

Gaus Width: 3.8654

Entry



B0L002S, U12-ch109, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	38.04
Std Dev	3.458

Gaus mean : 37.9323

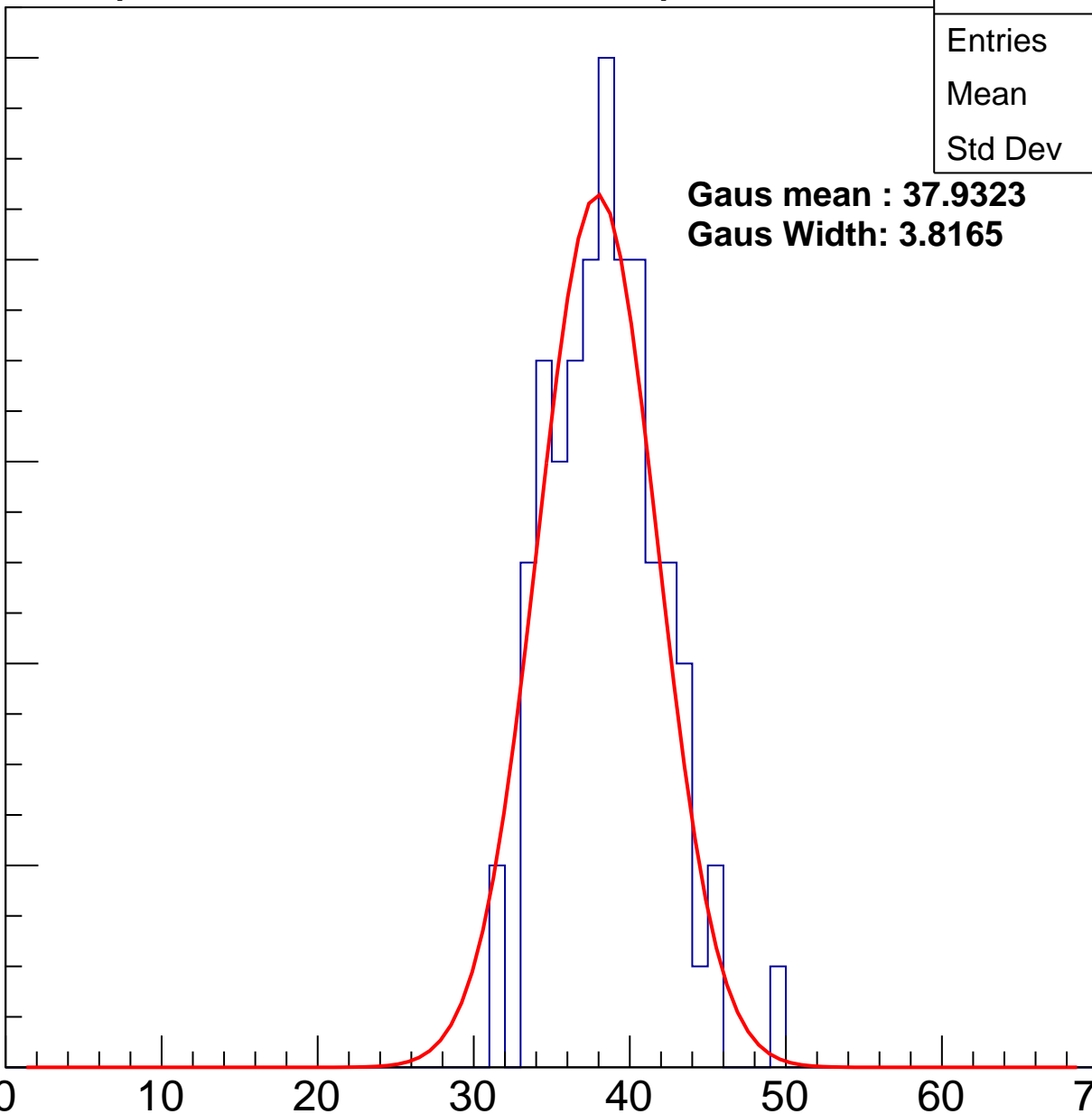
Gaus Width: 3.8165

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch109, adc2

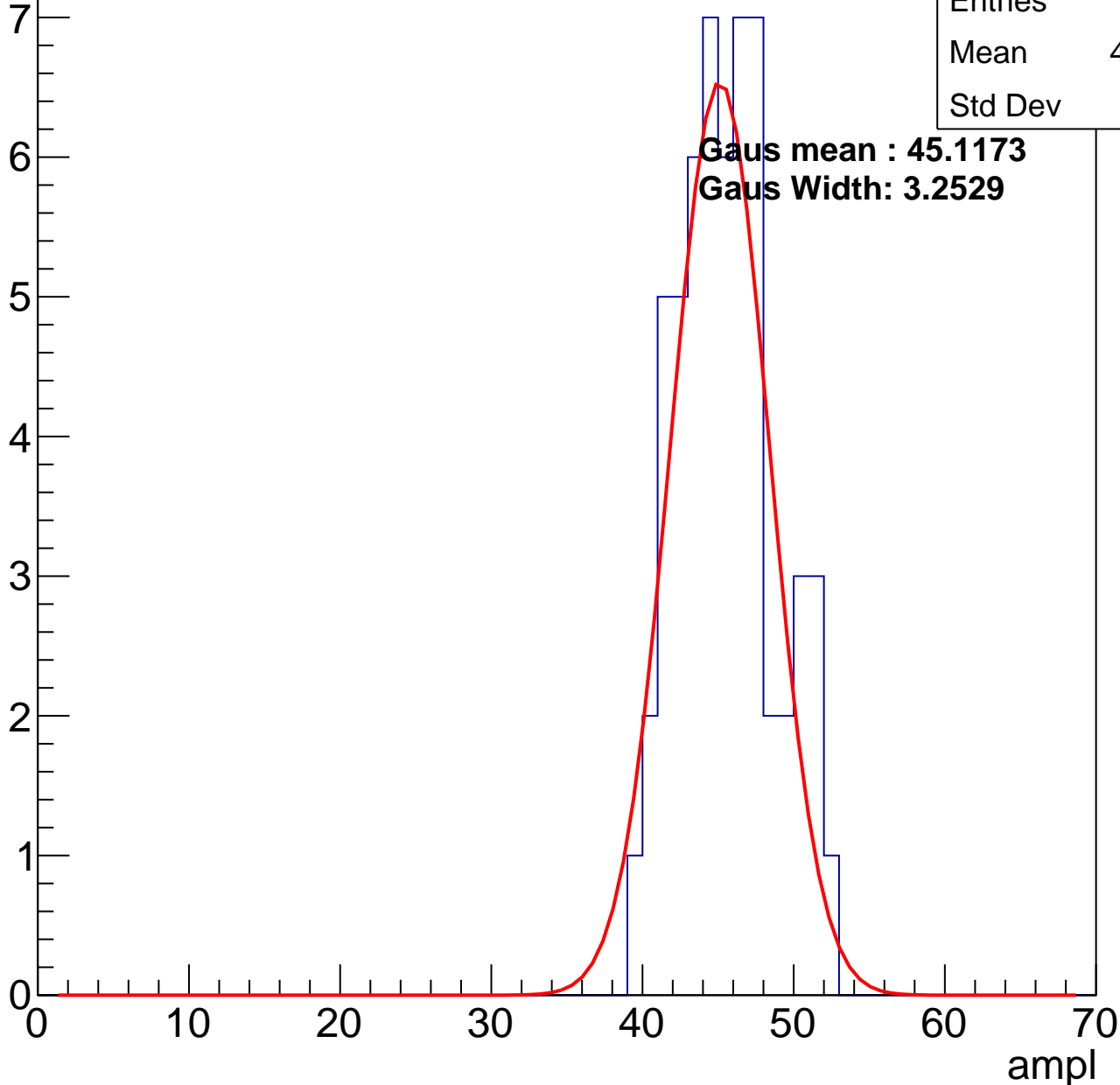
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	45.09
Std Dev	3.13

Gaus mean : 45.1173

Gaus Width: 3.2529

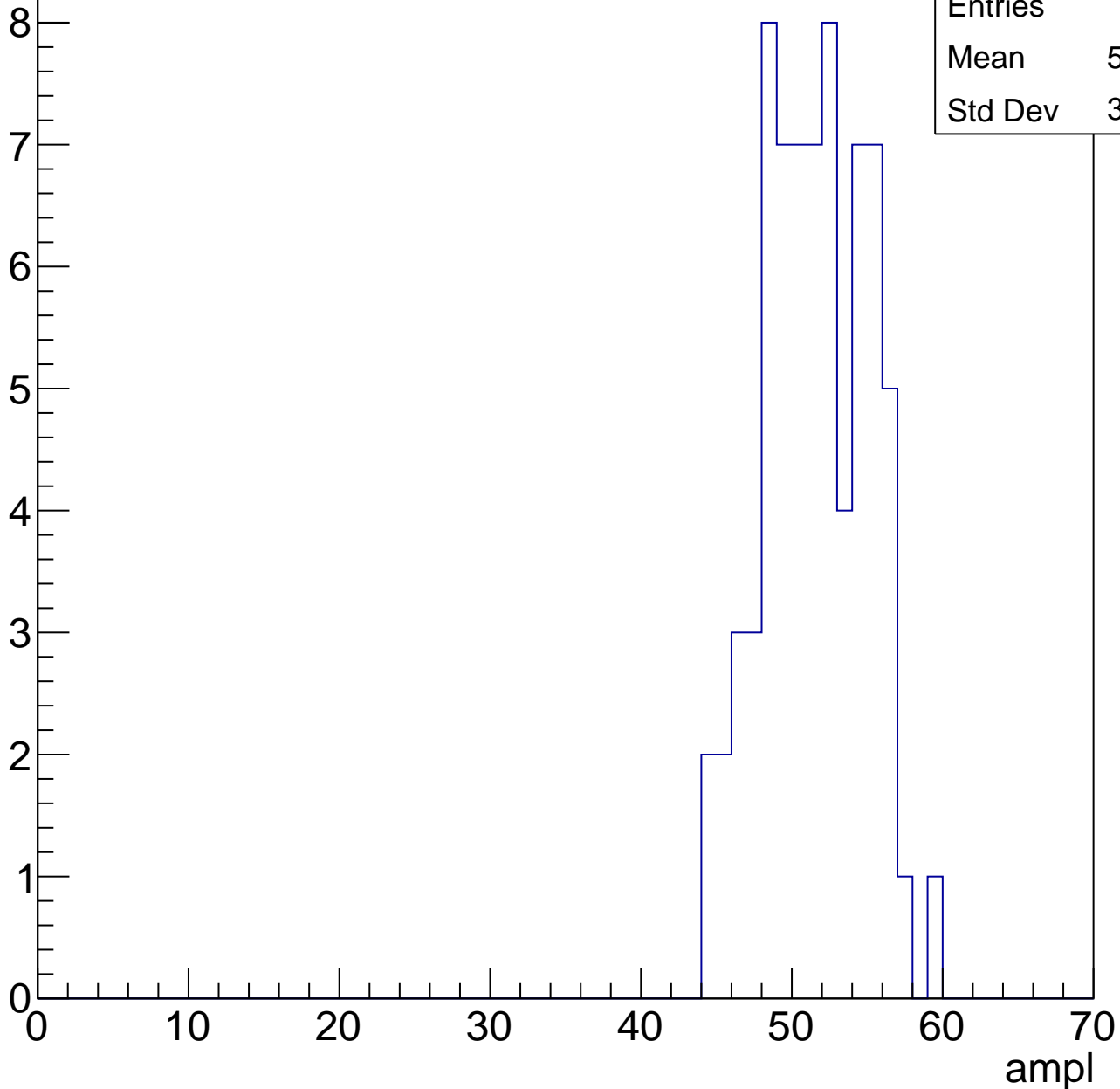


B0L002S, U12-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	51.08
Std Dev	3.382

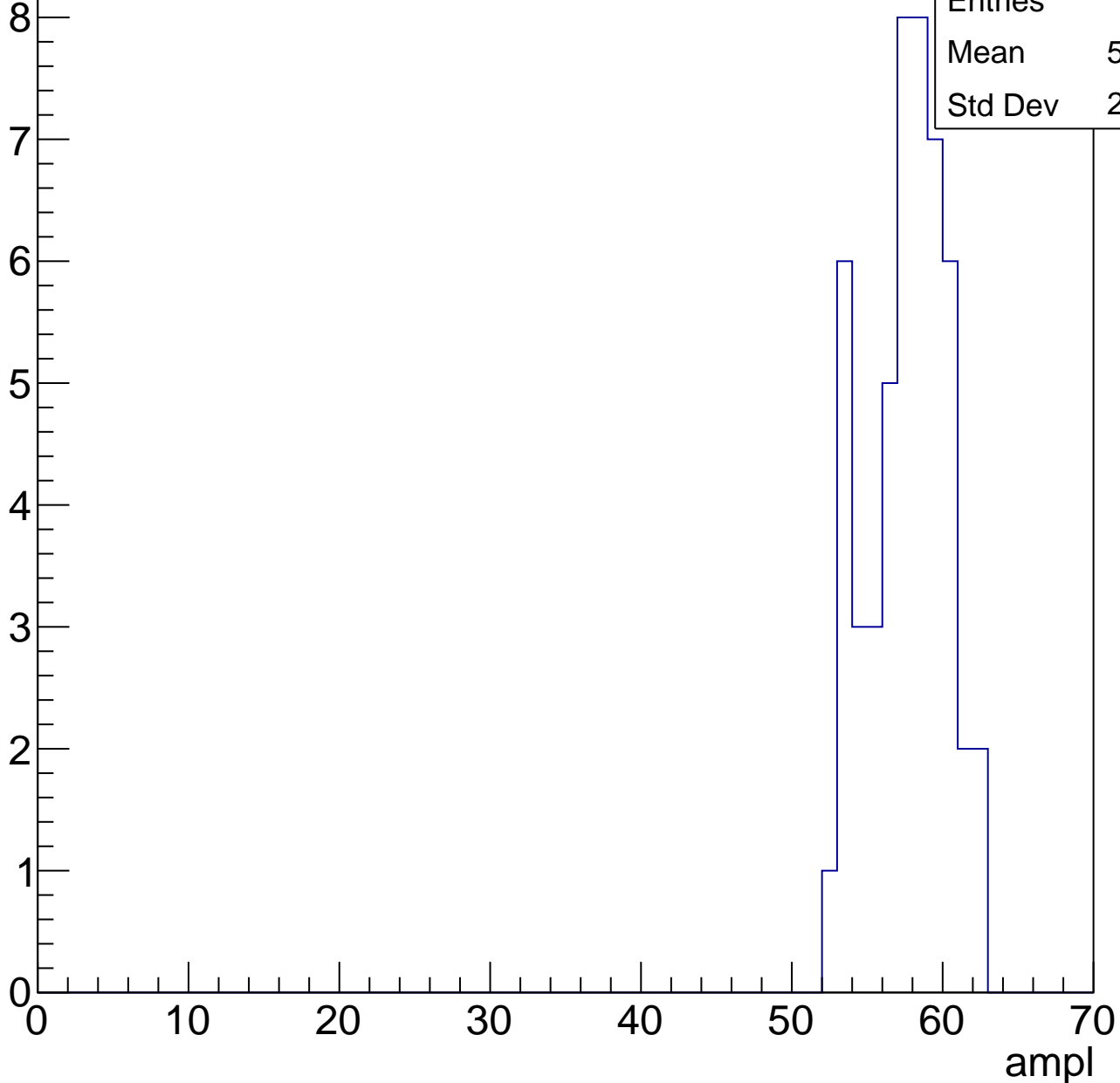


B0L002S, U12-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57.18
Std Dev	2.565

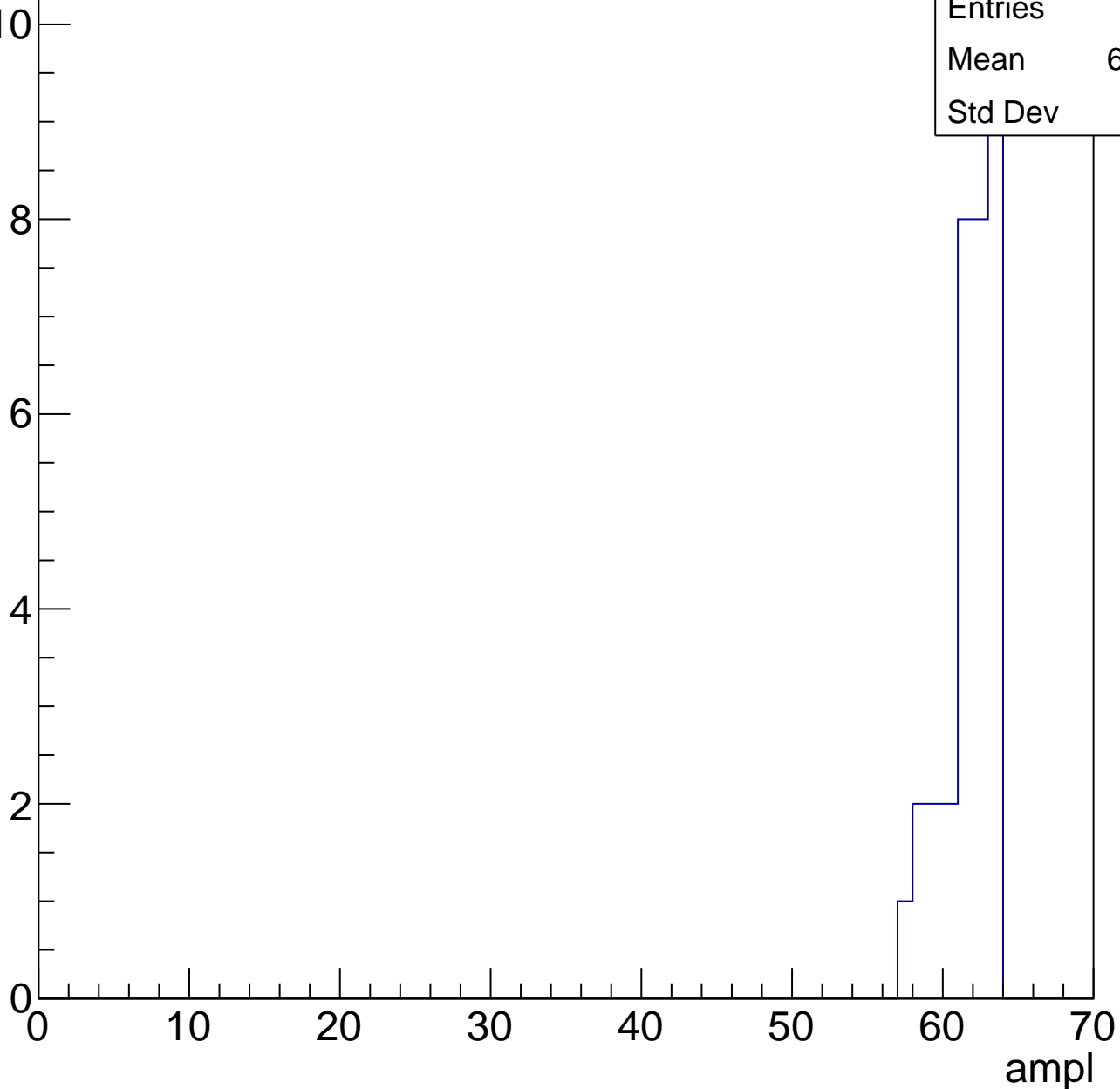


B0L002S, U12-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	61.36
Std Dev	1.63



B0L002S, U12-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

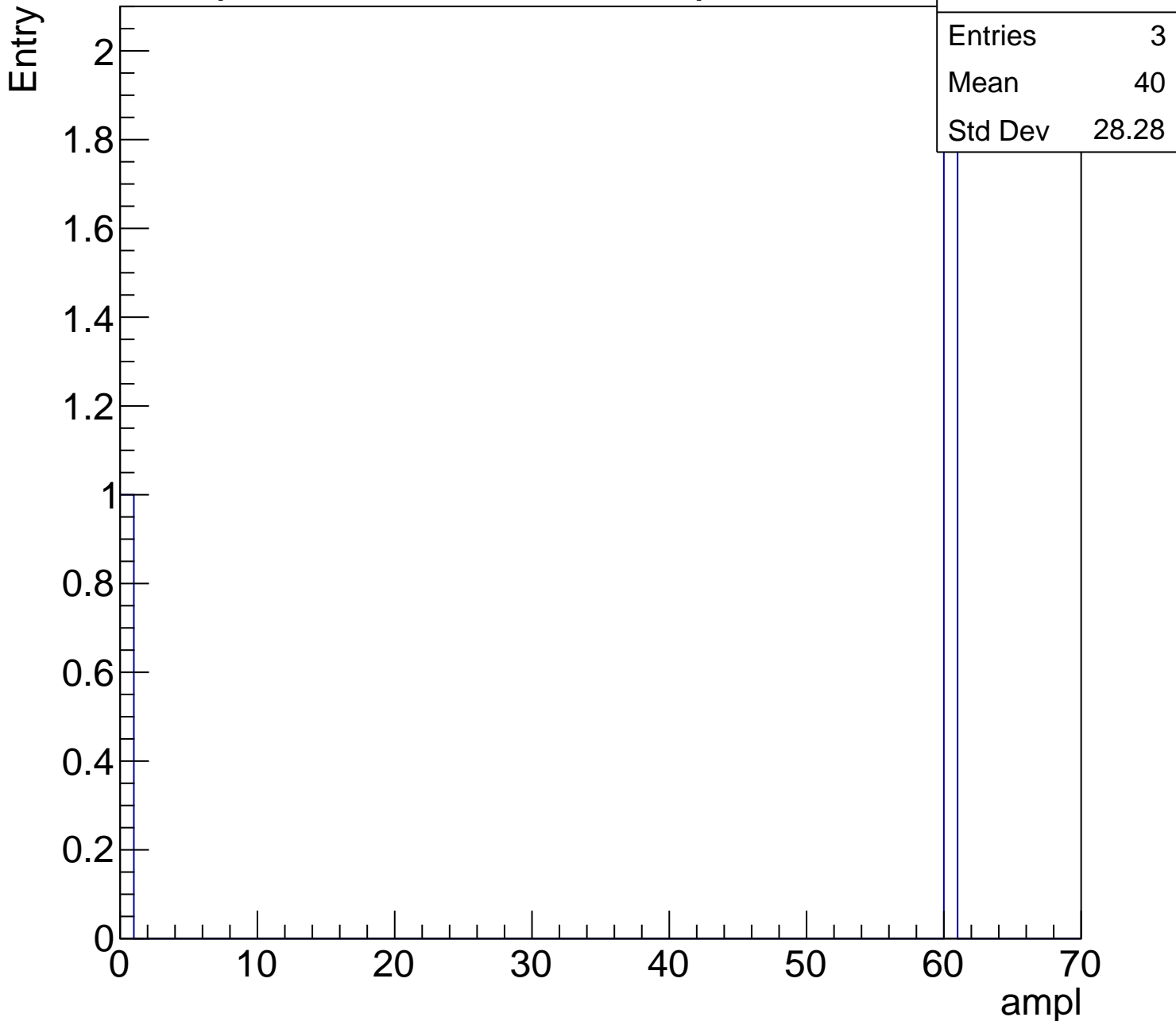
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	40
Std Dev	28.28

0 10 20 30 40 50 60 70

ampl



B0L002S, U12-ch109, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B0L002S, U12-ch110, adc0

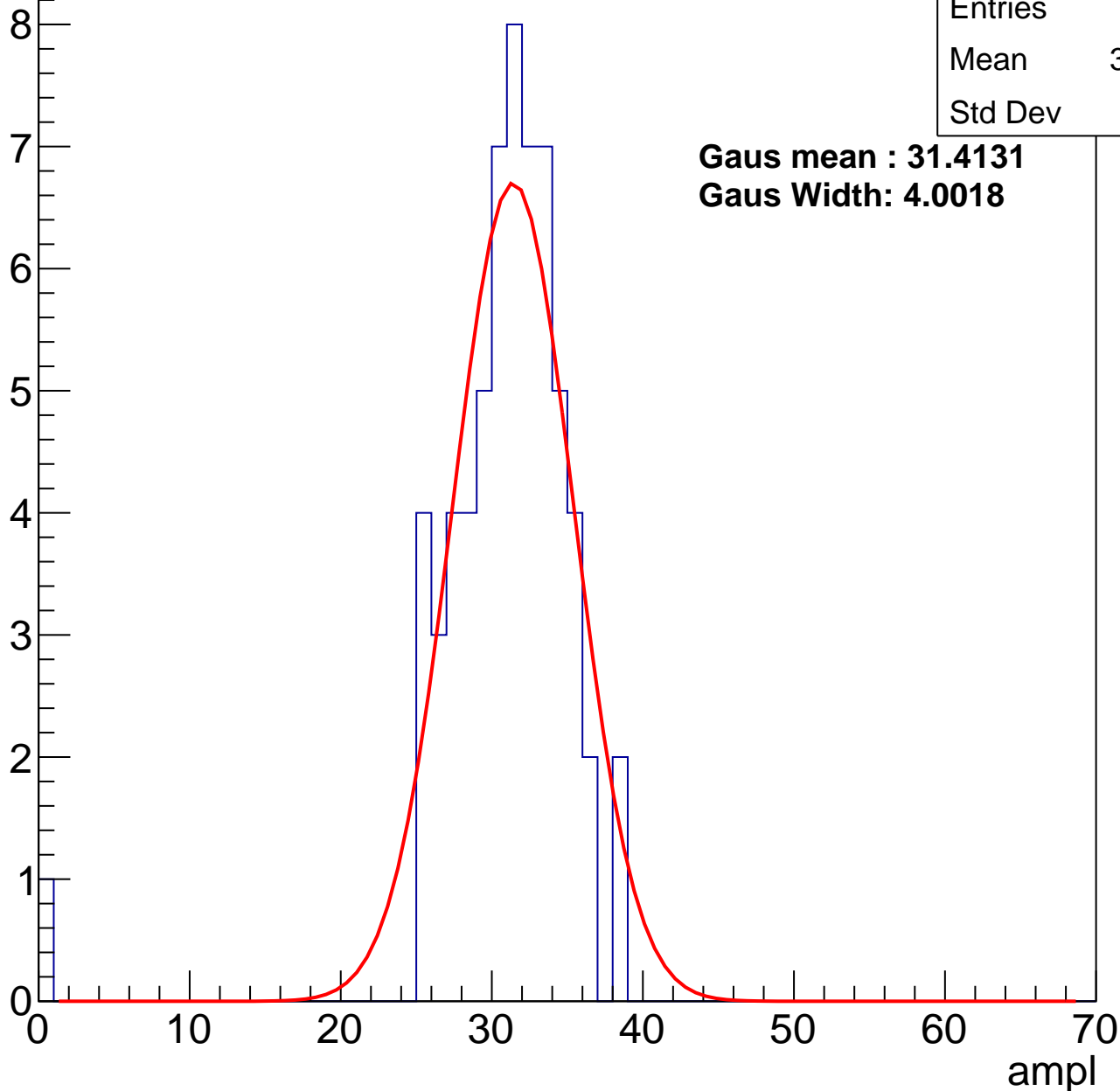
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.38
Std Dev	5

Gaus mean : 31.4131

Gaus Width: 4.0018



B0L002S, U12-ch110, adc1

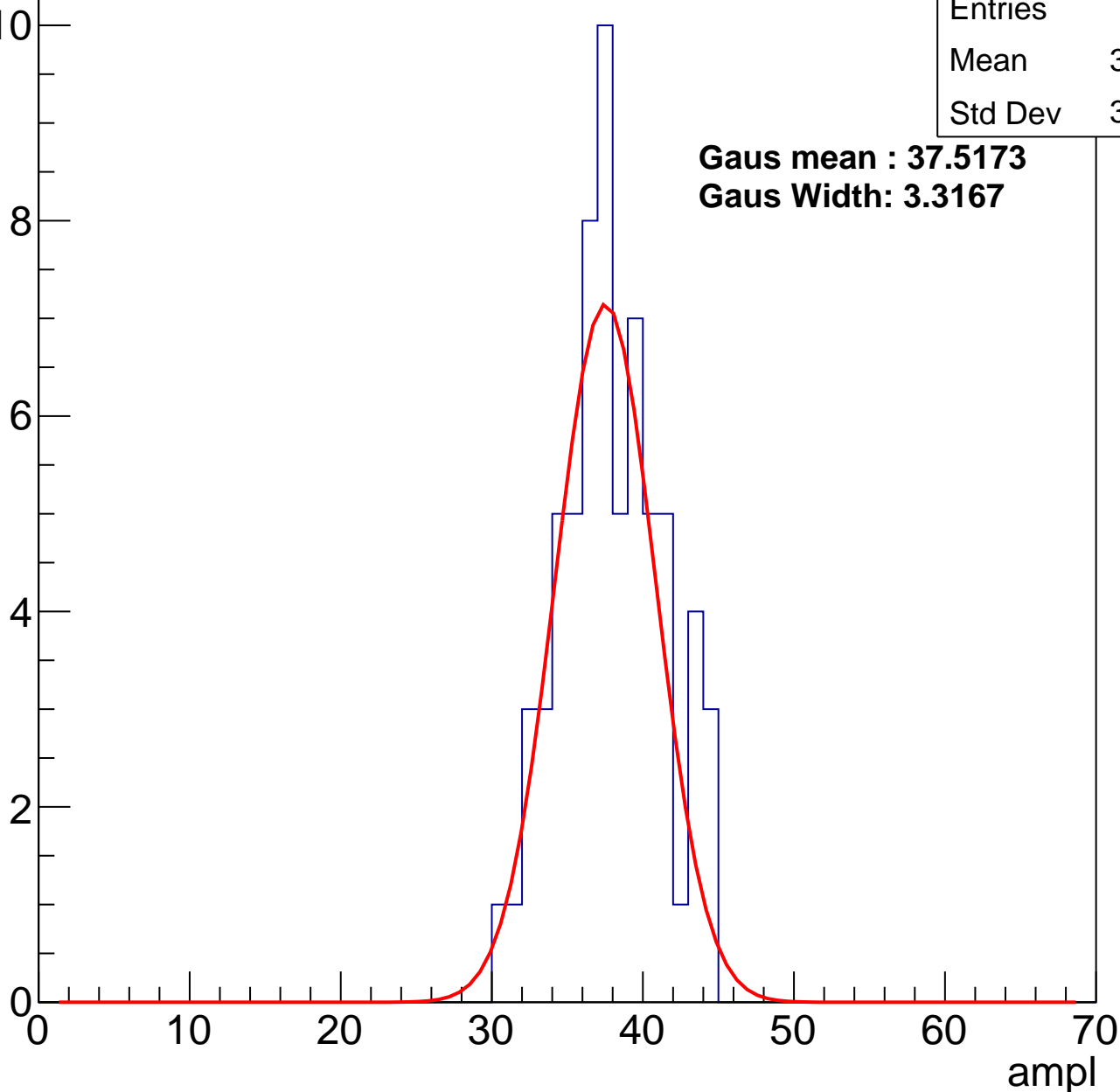
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.47
Std Dev	3.349

Gaus mean : 37.5173

Gaus Width: 3.3167



B0L002S, U12-ch110, adc2

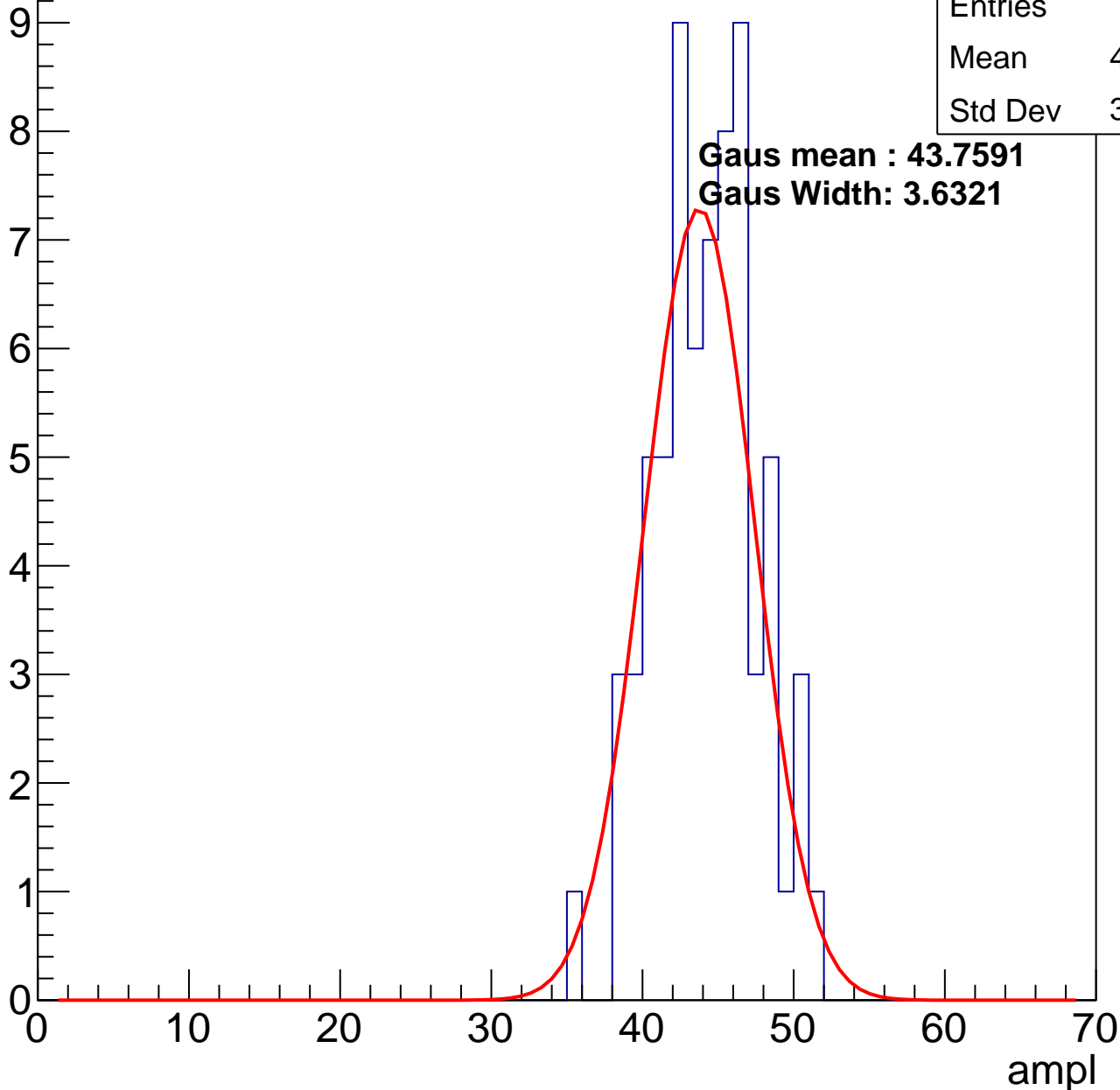
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.77
Std Dev	3.319

Gaus mean : 43.7591

Gaus Width: 3.6321



B0L002S, U12-ch110, adc3

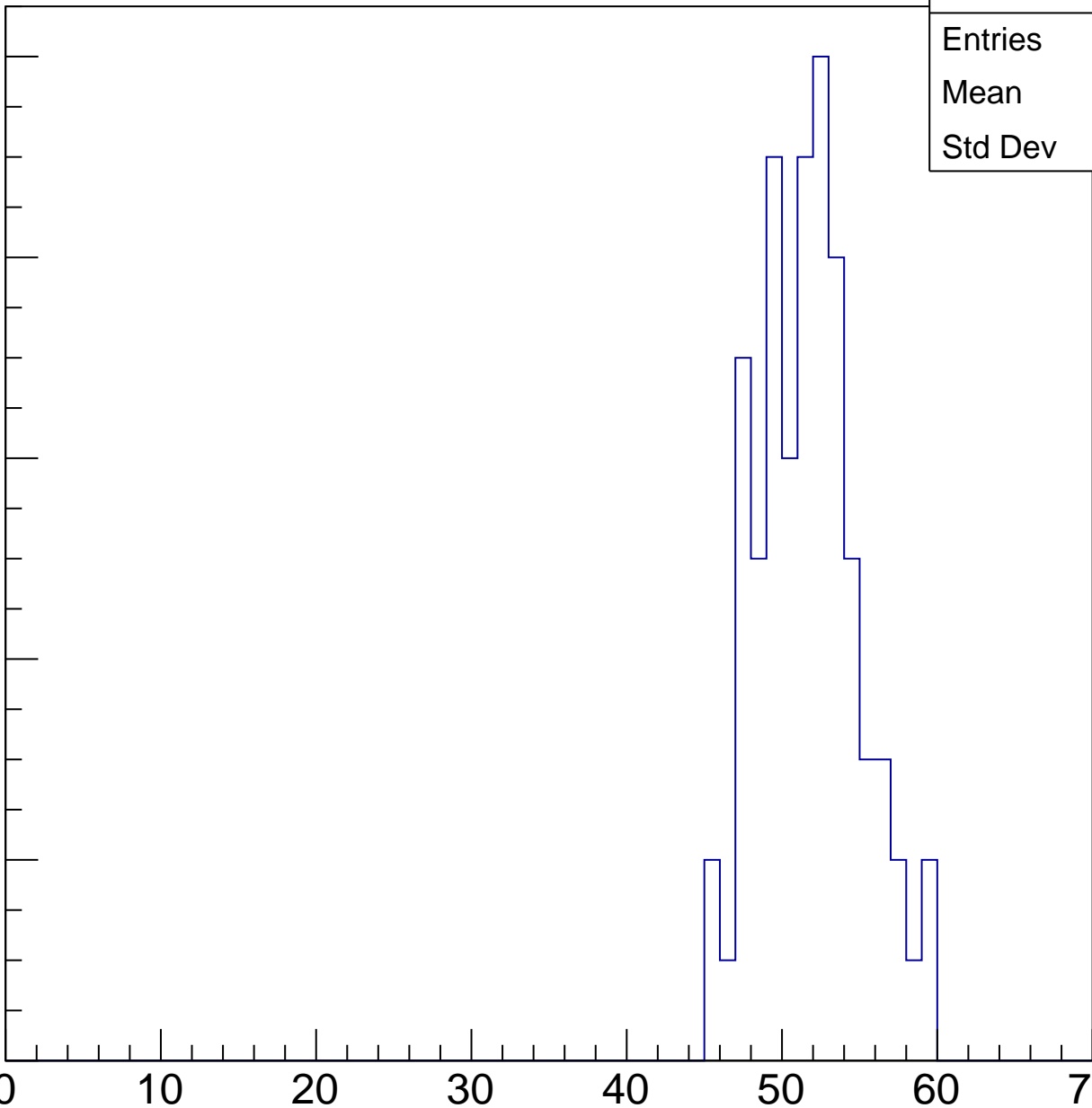
calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	51.26
Std Dev	3.205

Entry

10
8
6
4
2
0

ampl

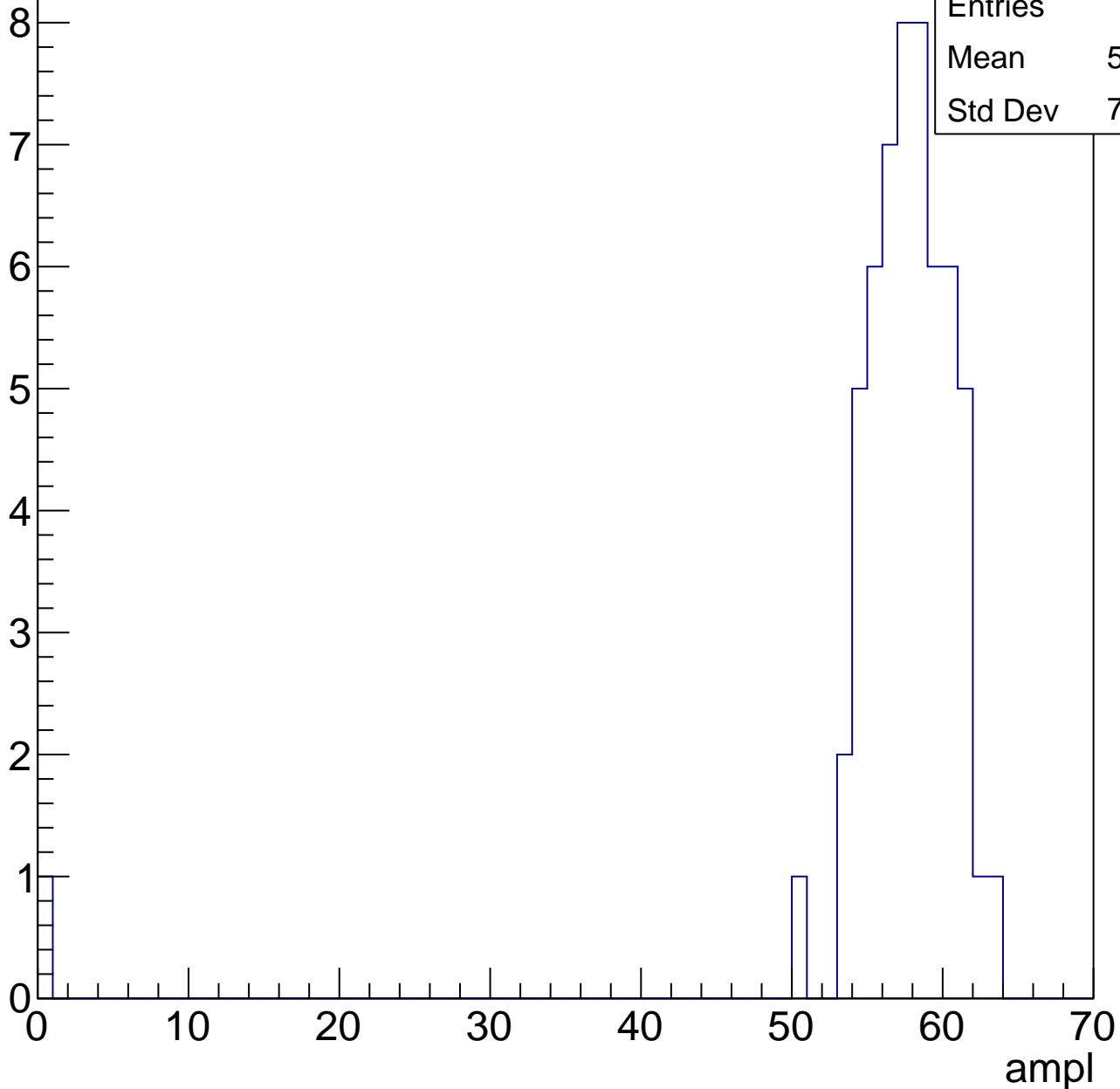


B0L002S, U12-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.35
Std Dev	7.957

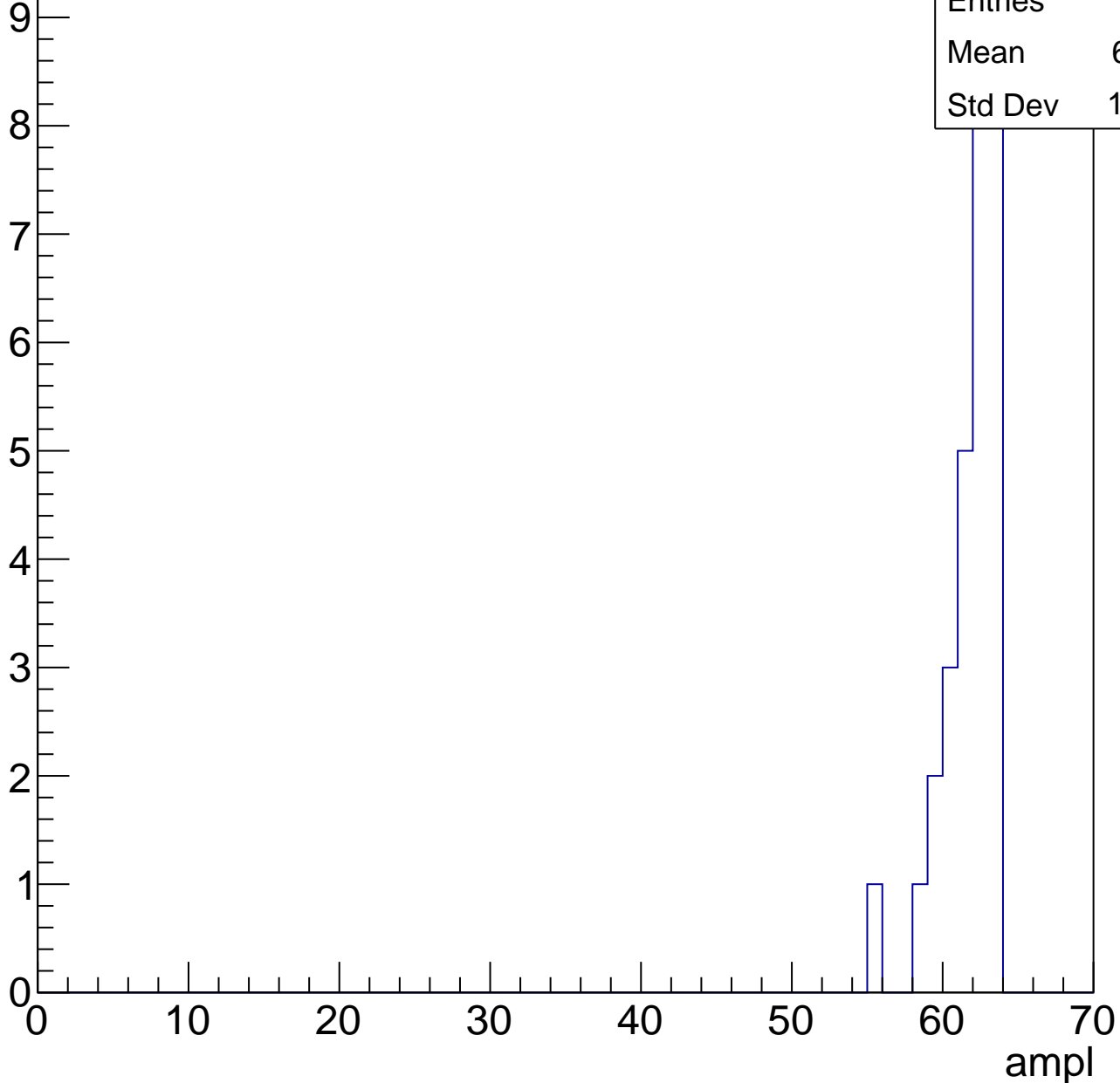


B0L002S, U12-ch110, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

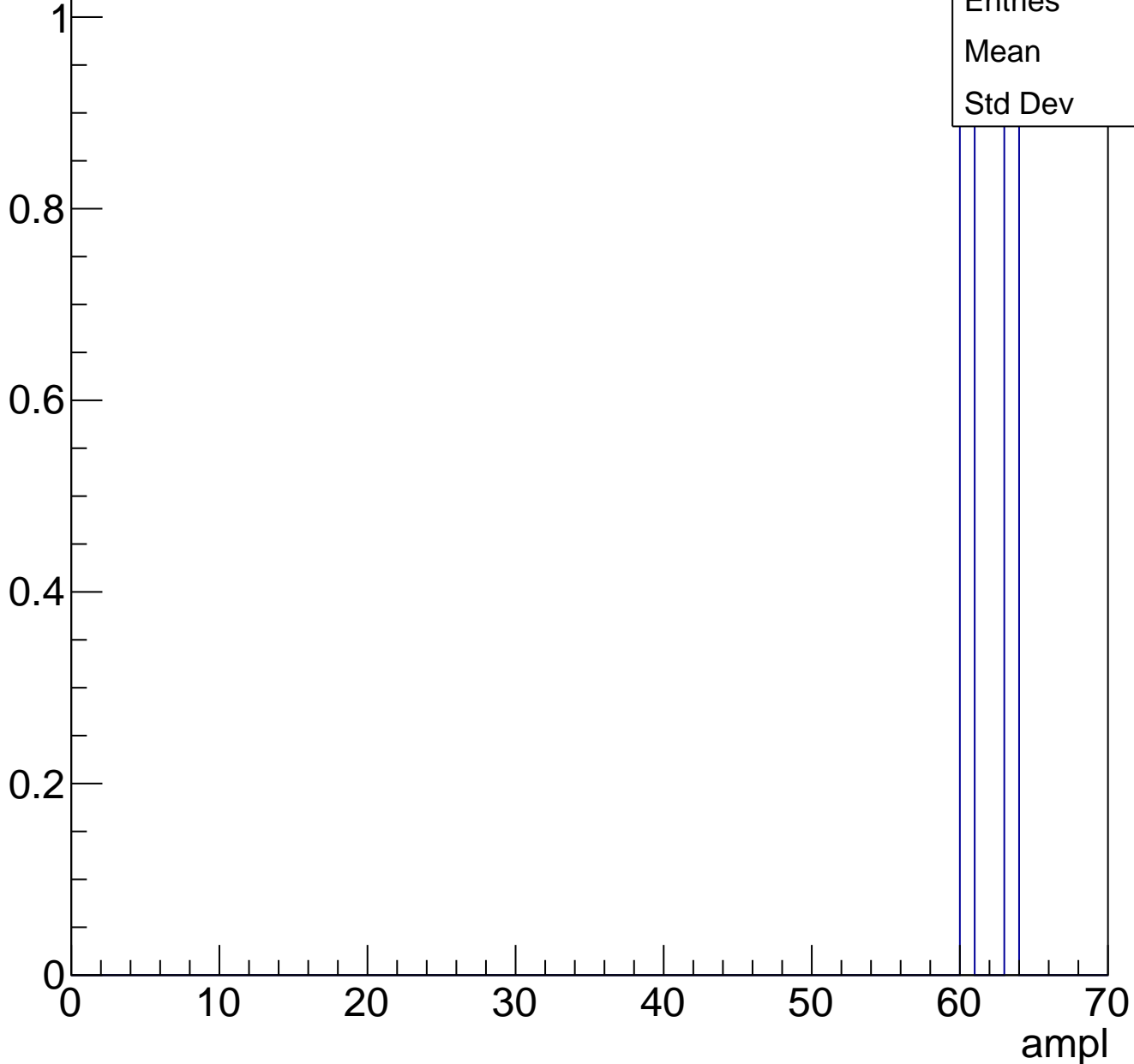
Entries	29
Mean	61.31
Std Dev	1.802



B0L002S, U12-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch110, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch111, adc0

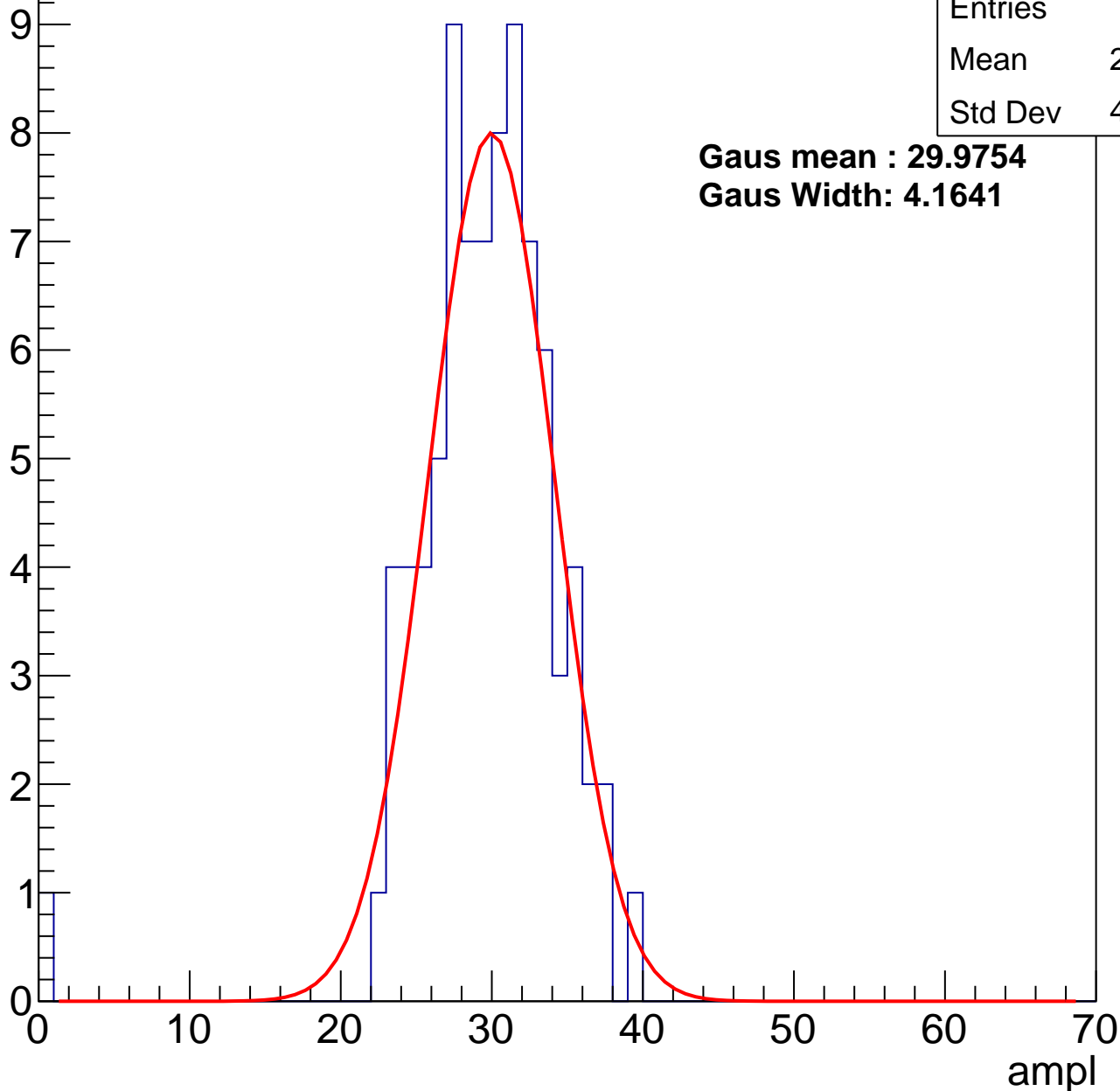
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	29.17
Std Dev	4.918

Gaus mean : 29.9754

Gaus Width: 4.1641



B0L002S, U12-ch111, adc1

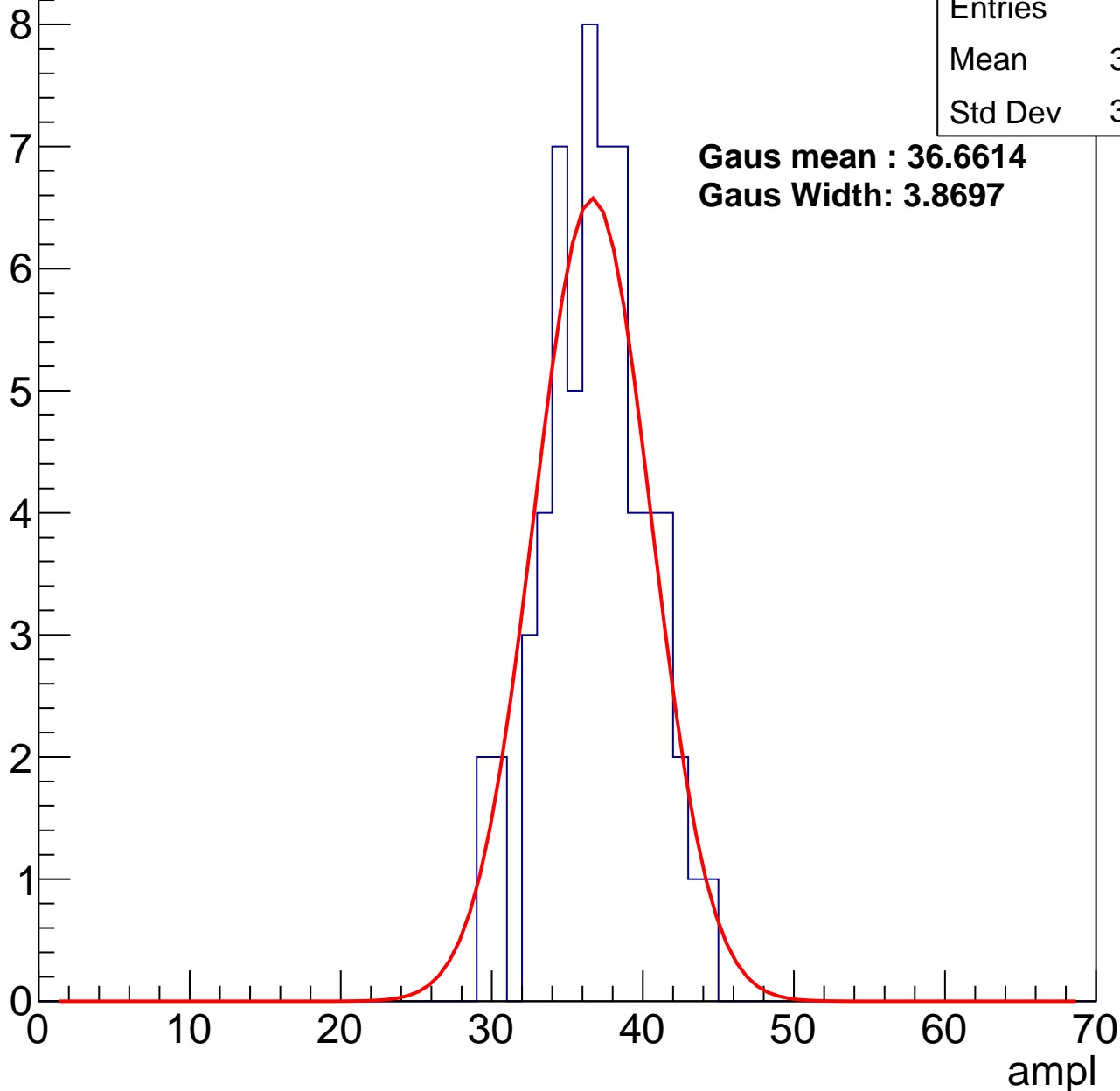
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.44
Std Dev	3.376

Gaus mean : 36.6614

Gaus Width: 3.8697



B0L002S, U12-ch111, adc2

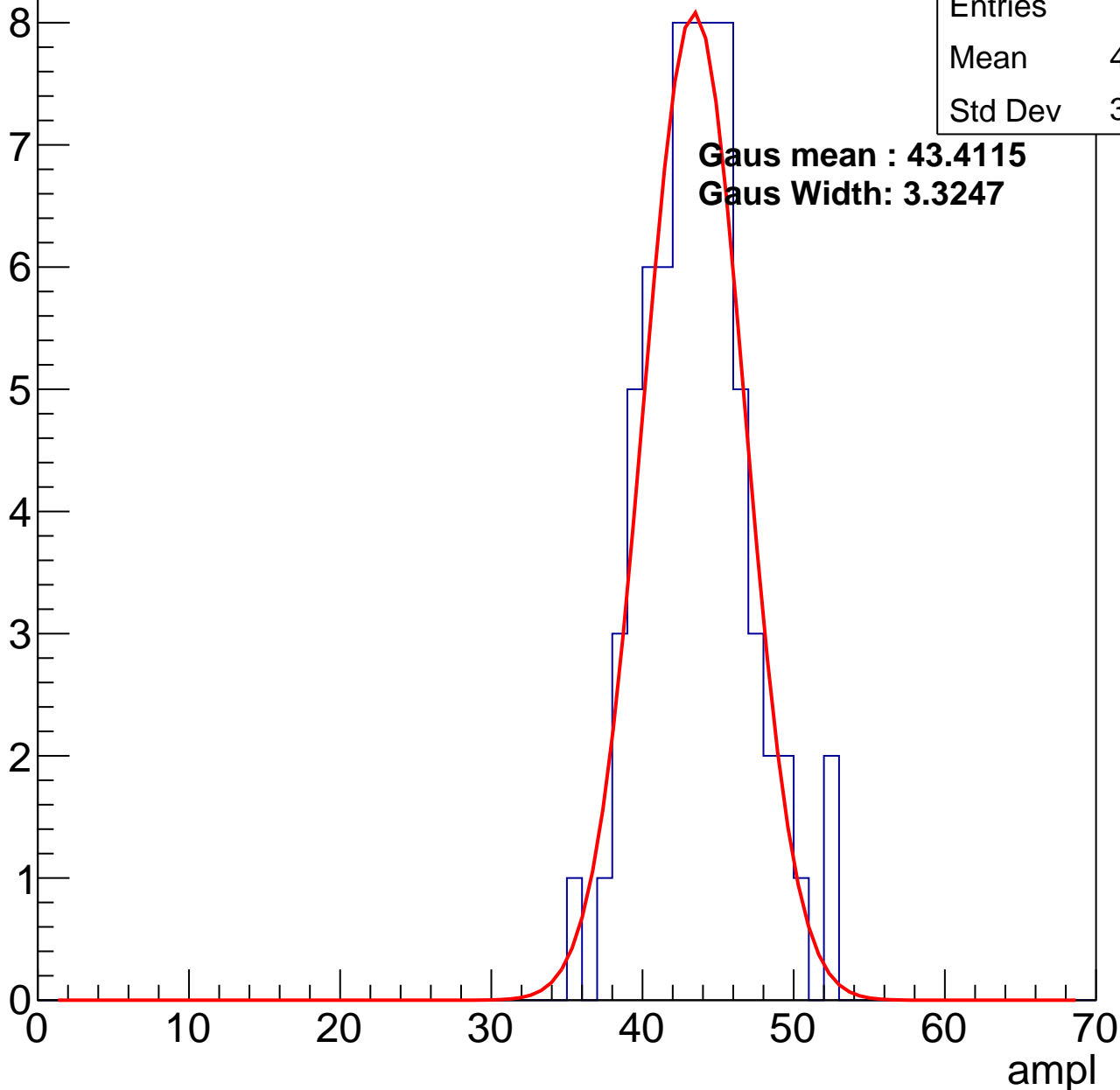
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.16
Std Dev	3.416

Gaus mean : 43.4115

Gaus Width: 3.3247

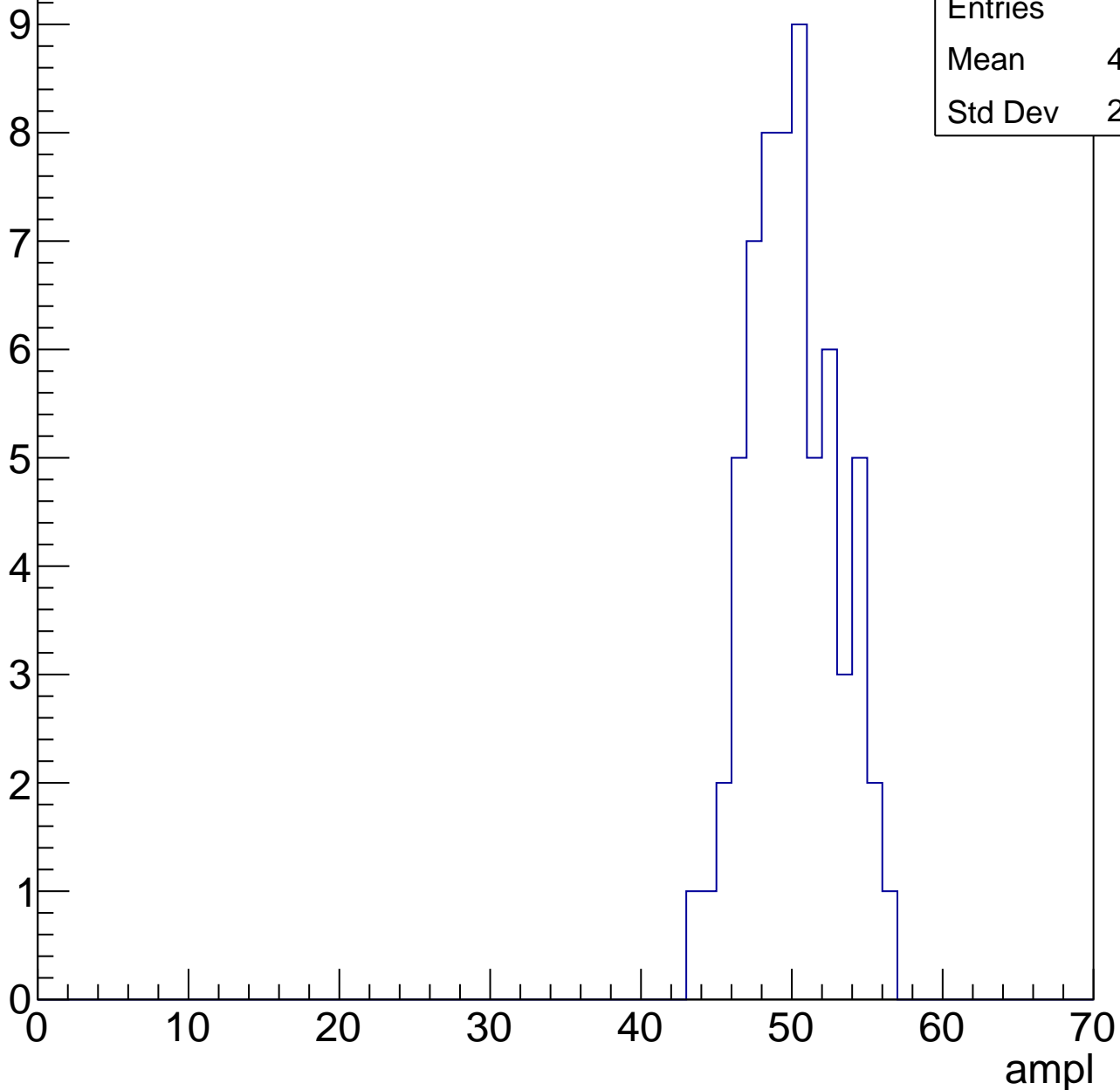


B0L002S, U12-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	49.59
Std Dev	2.898



B0L002S, U12-ch111, adc4

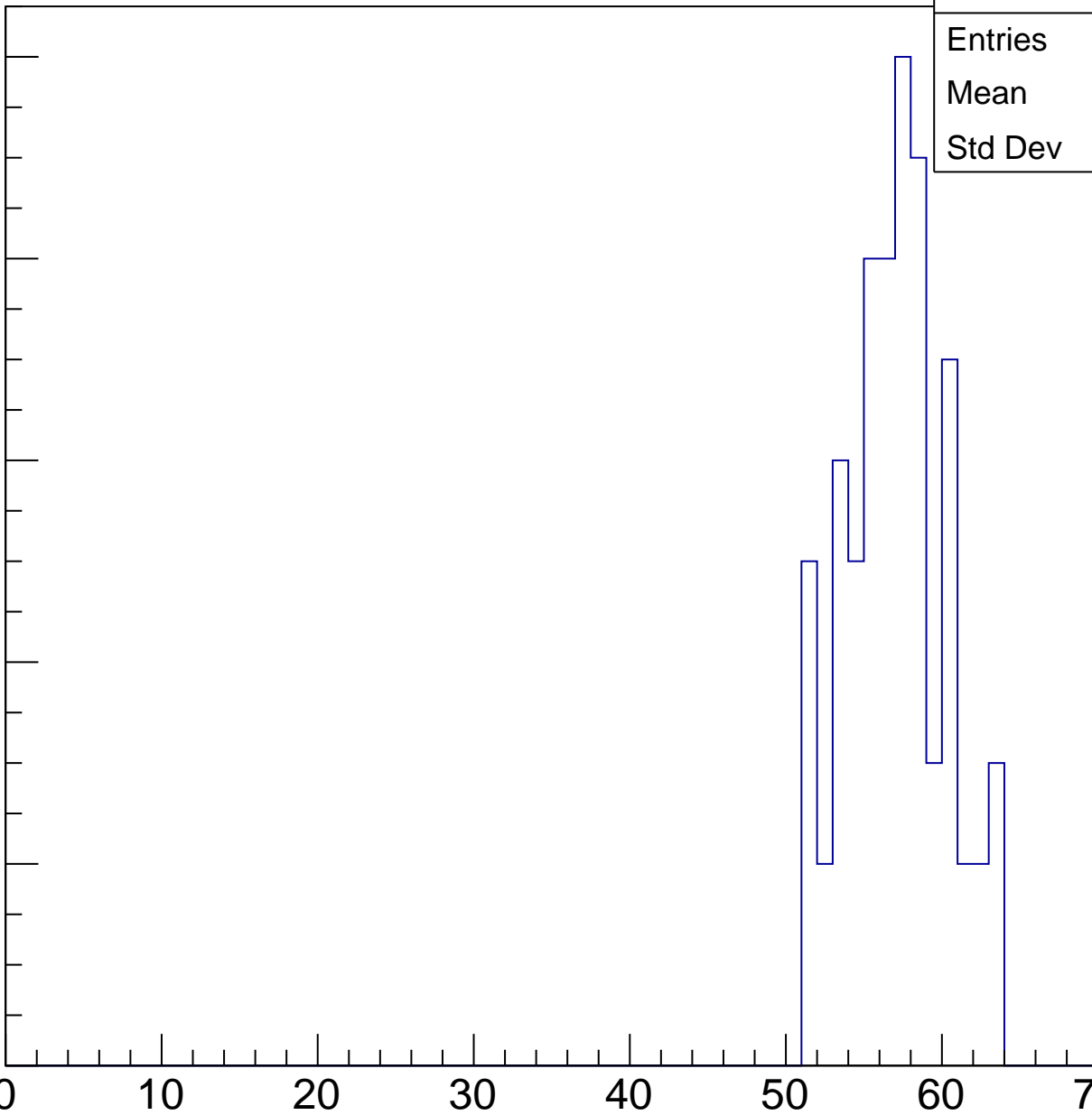
calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	56.56
Std Dev	3.097

Entry

10
8
6
4
2
0

ampl

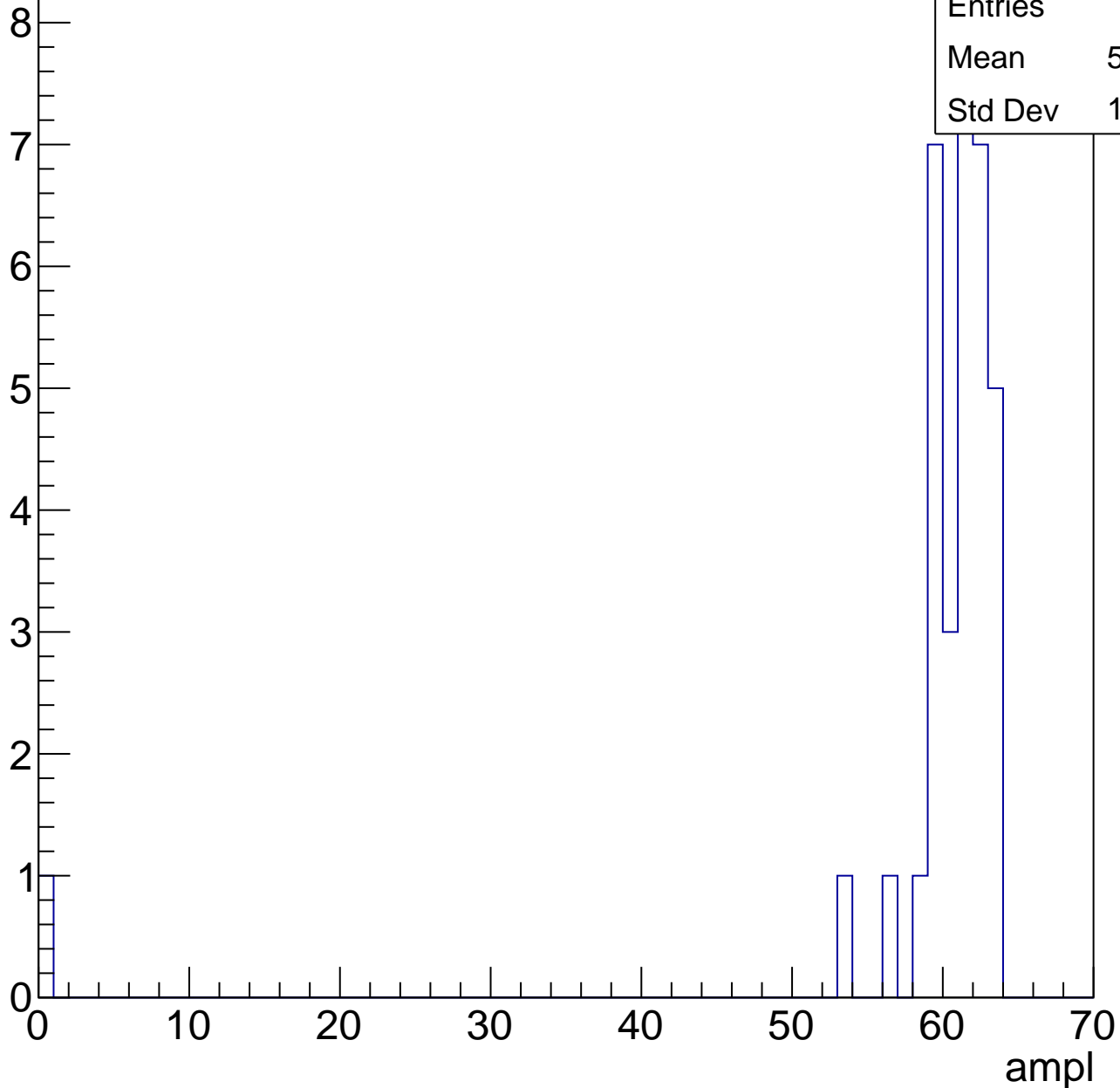


B0L002S, U12-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.74
Std Dev	10.44



B0L002S, U12-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch112, adc0

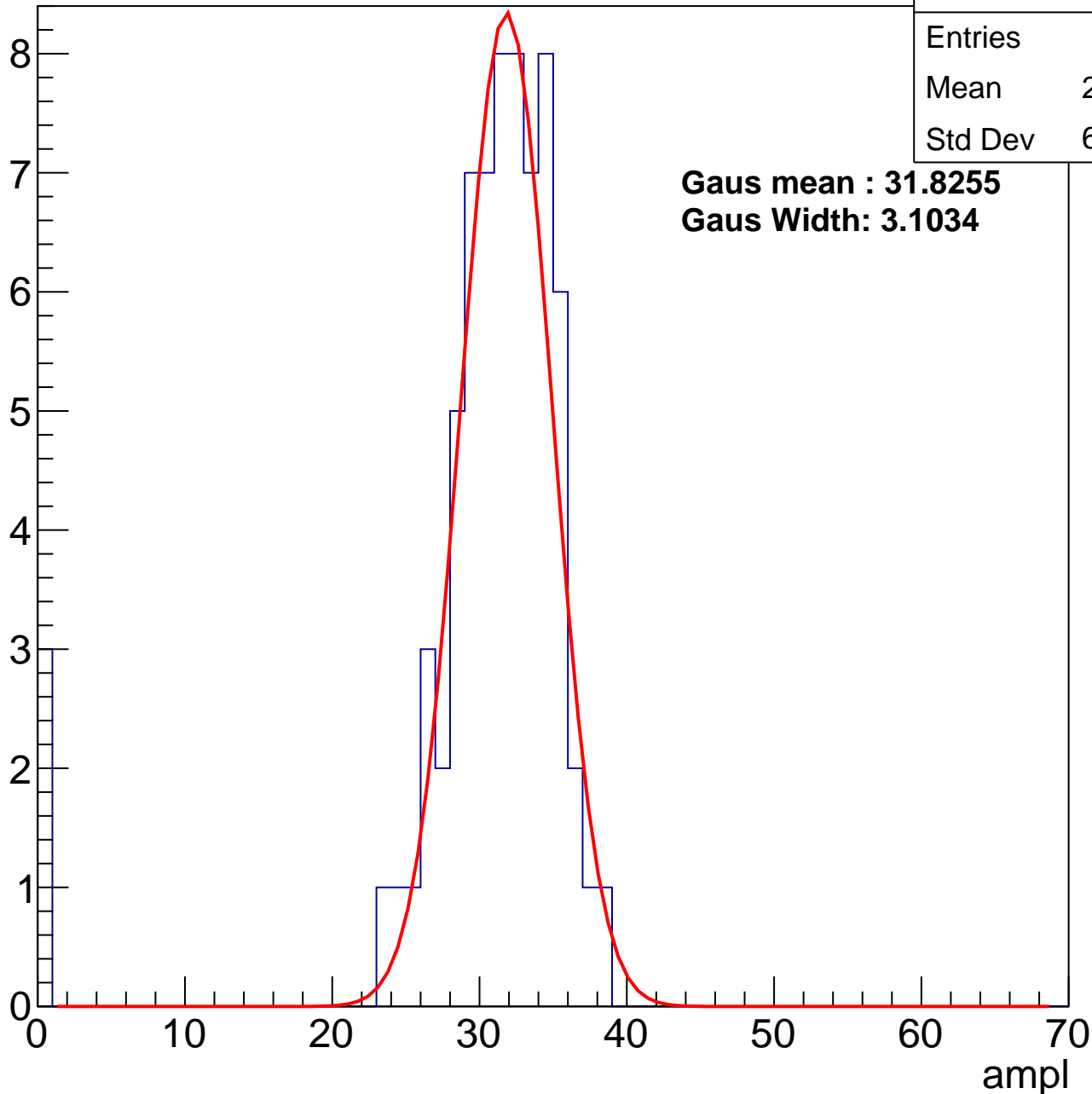
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	29.87
Std Dev	6.989

Gaus mean : 31.8255

Gaus Width: 3.1034



B0L002S, U12-ch112, adc1

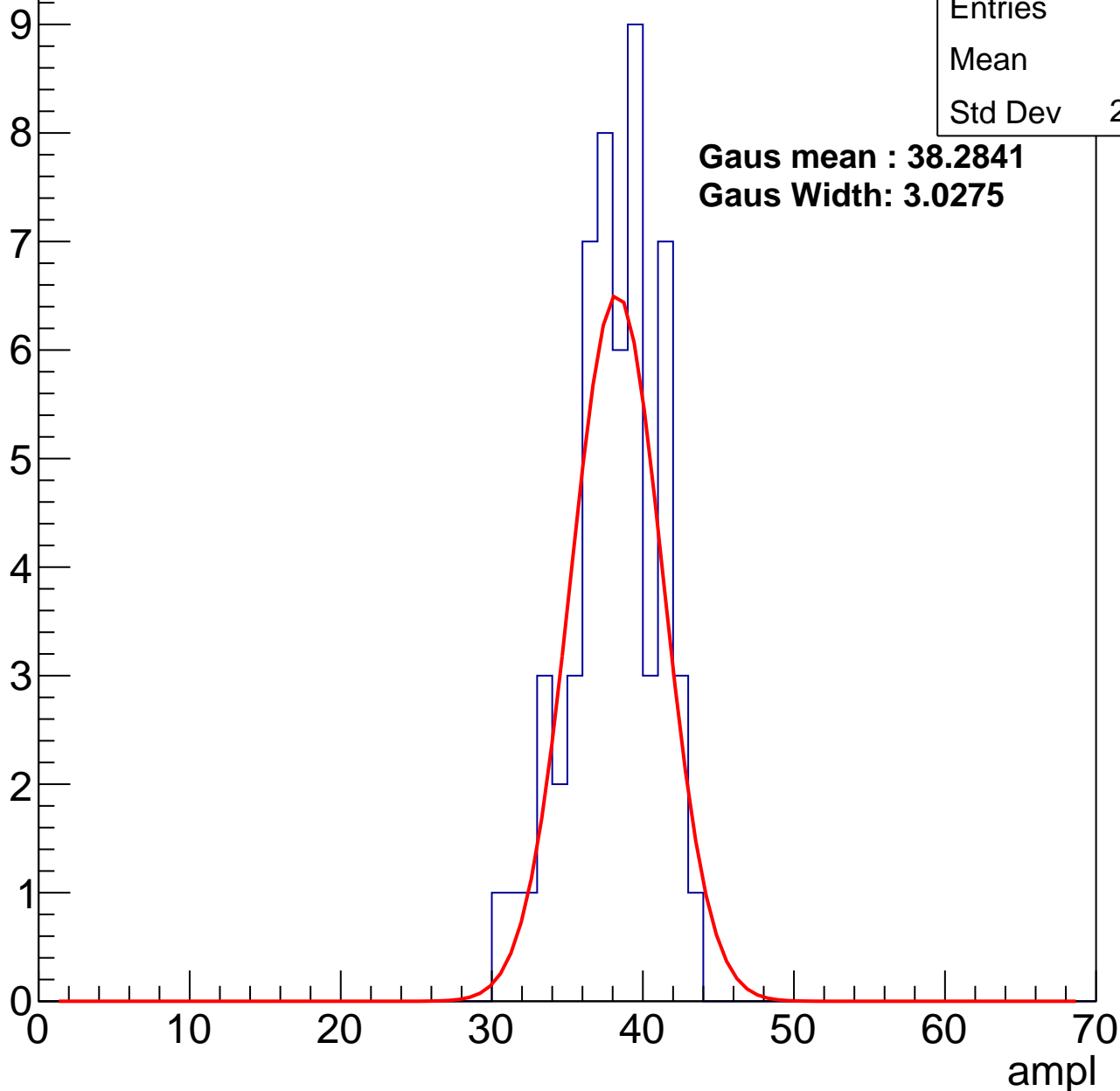
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	37.6
Std Dev	2.915

Gaus mean : 38.2841

Gaus Width: 3.0275



B0L002S, U12-ch112, adc2

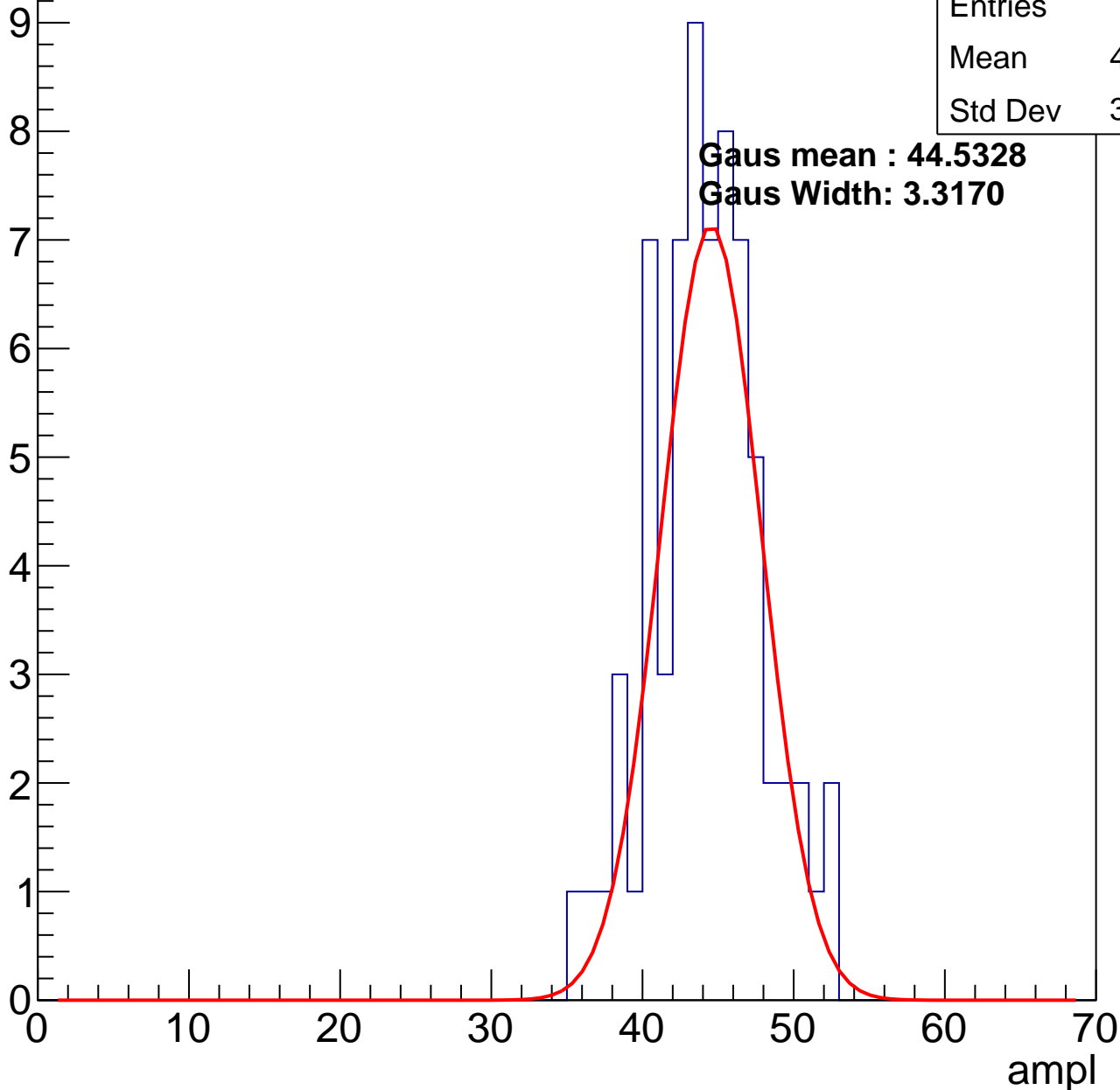
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.75
Std Dev	3.633

Gaus mean : 44.5328

Gaus Width: 3.3170

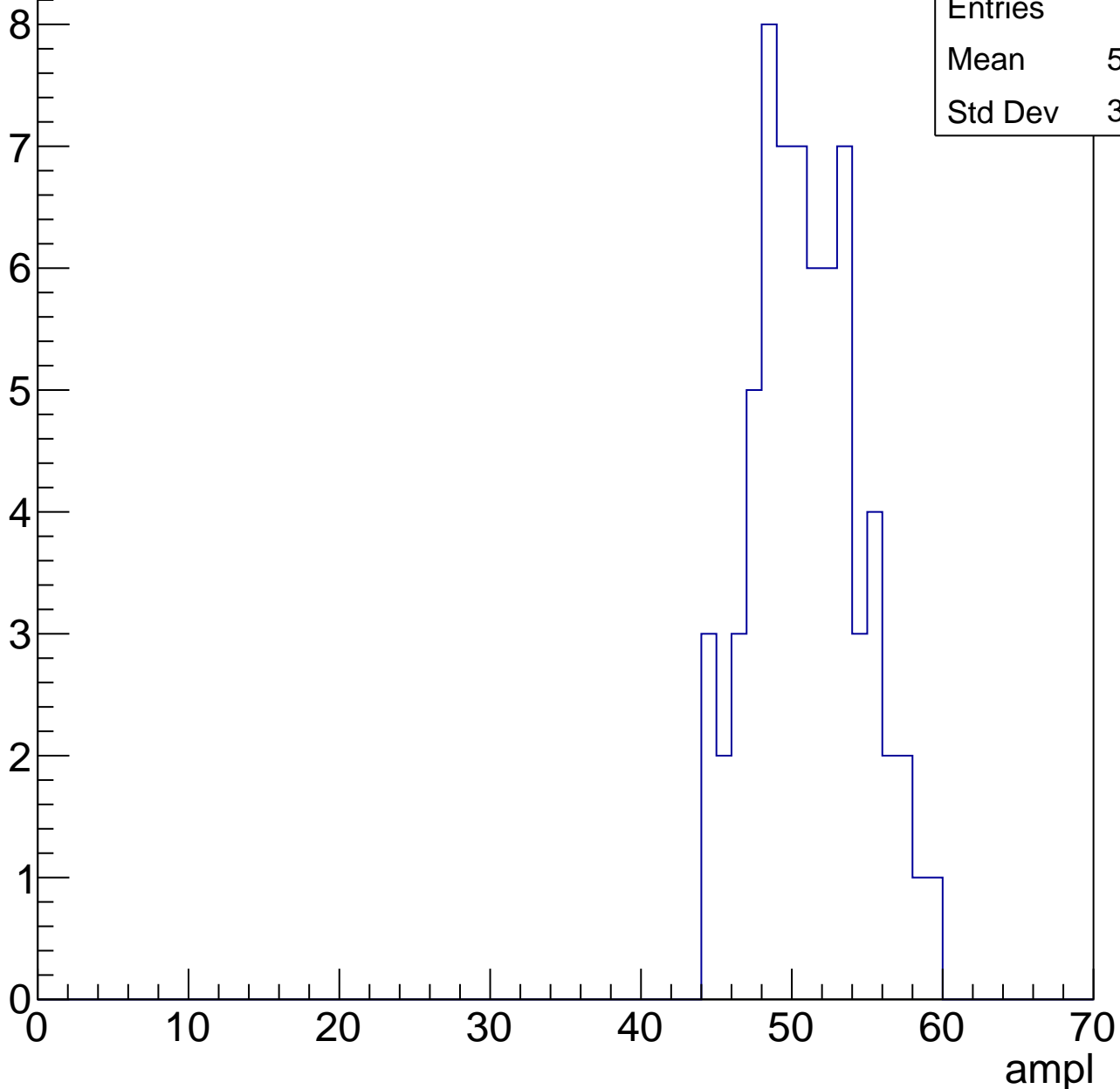


B0L002S, U12-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.54
Std Dev	3.513

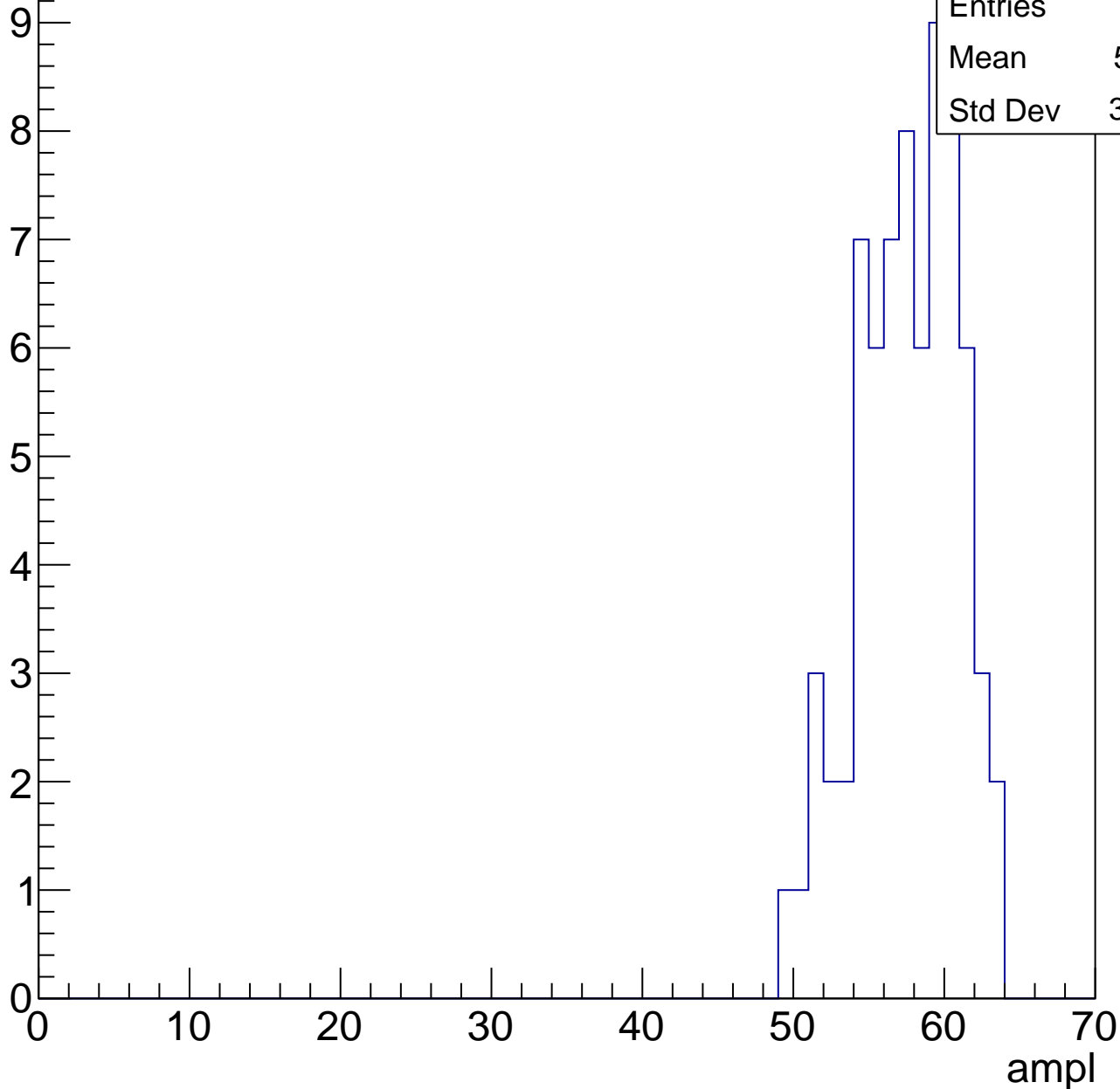


B0L002S, U12-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	57.11
Std Dev	3.257

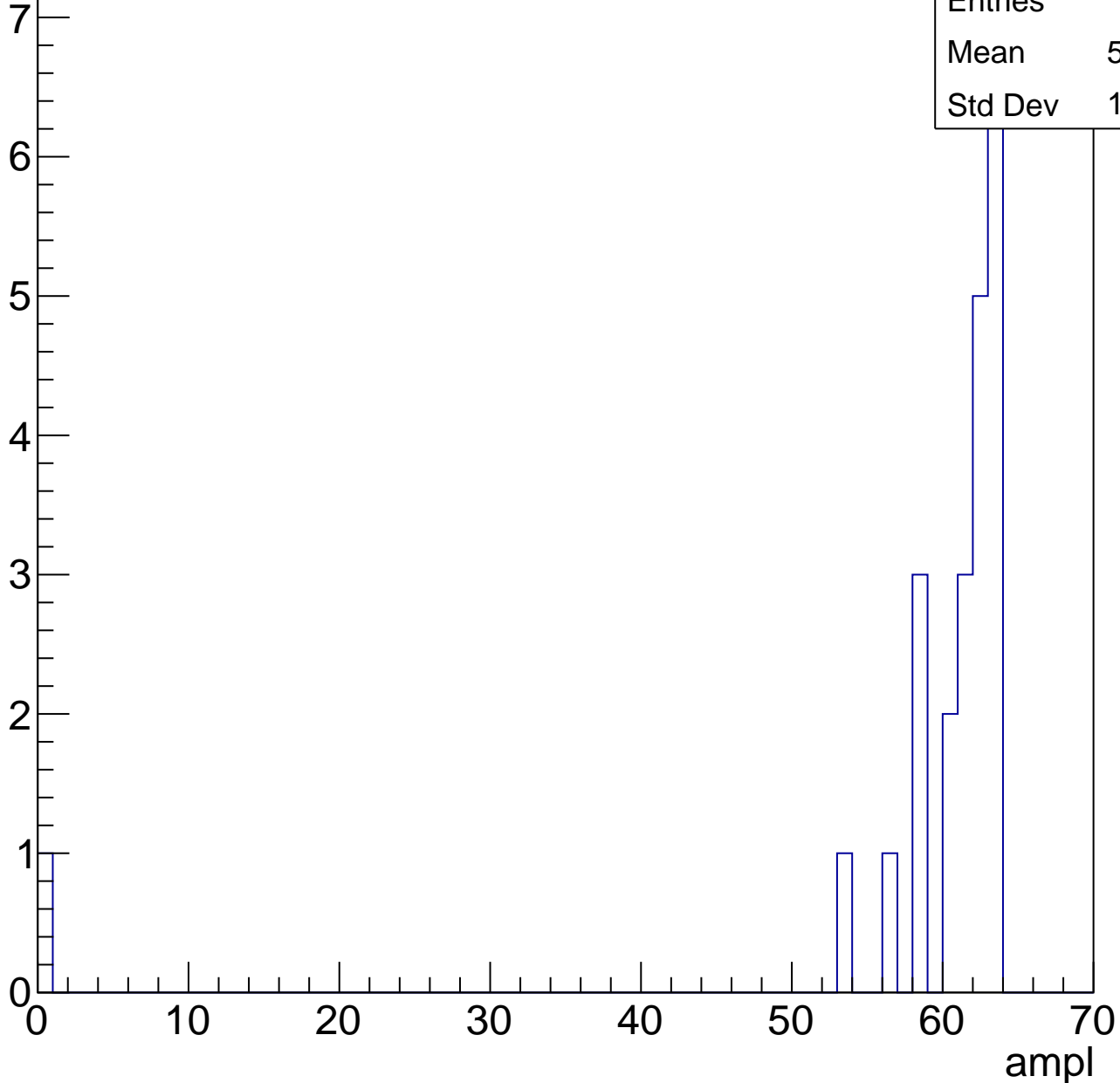


B0L002S, U12-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.13
Std Dev	12.65



B0L002S, U12-ch112, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	62
Std Dev	0.7071

B0L002S, U12-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch113, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	30.37
Std Dev	3.916

Gaus mean : 30.5610

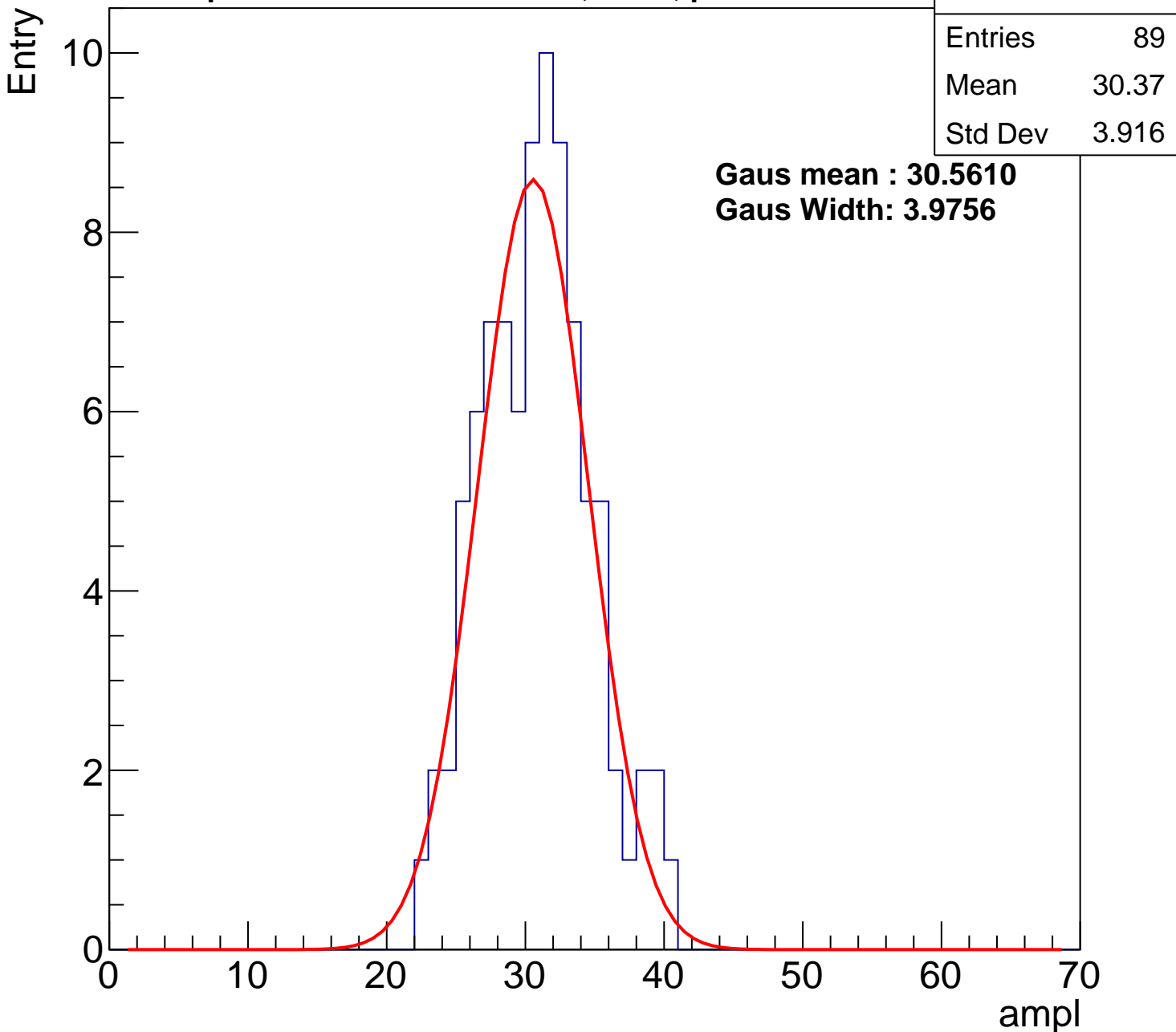
Gaus Width: 3.9756

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch113, adc1

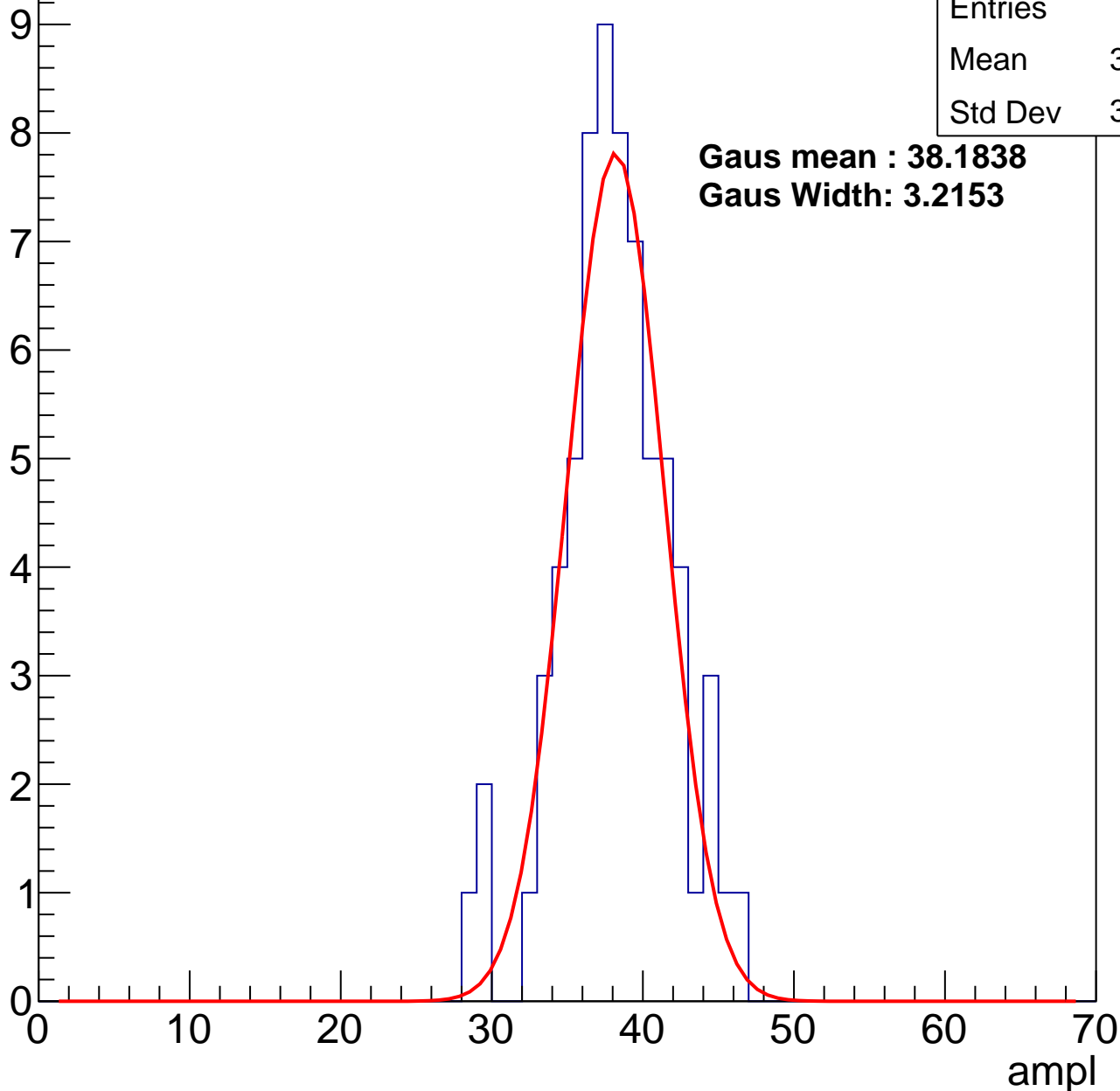
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.72
Std Dev	3.637

Gaus mean : 38.1838

Gaus Width: 3.2153



B0L002S, U12-ch113, adc2

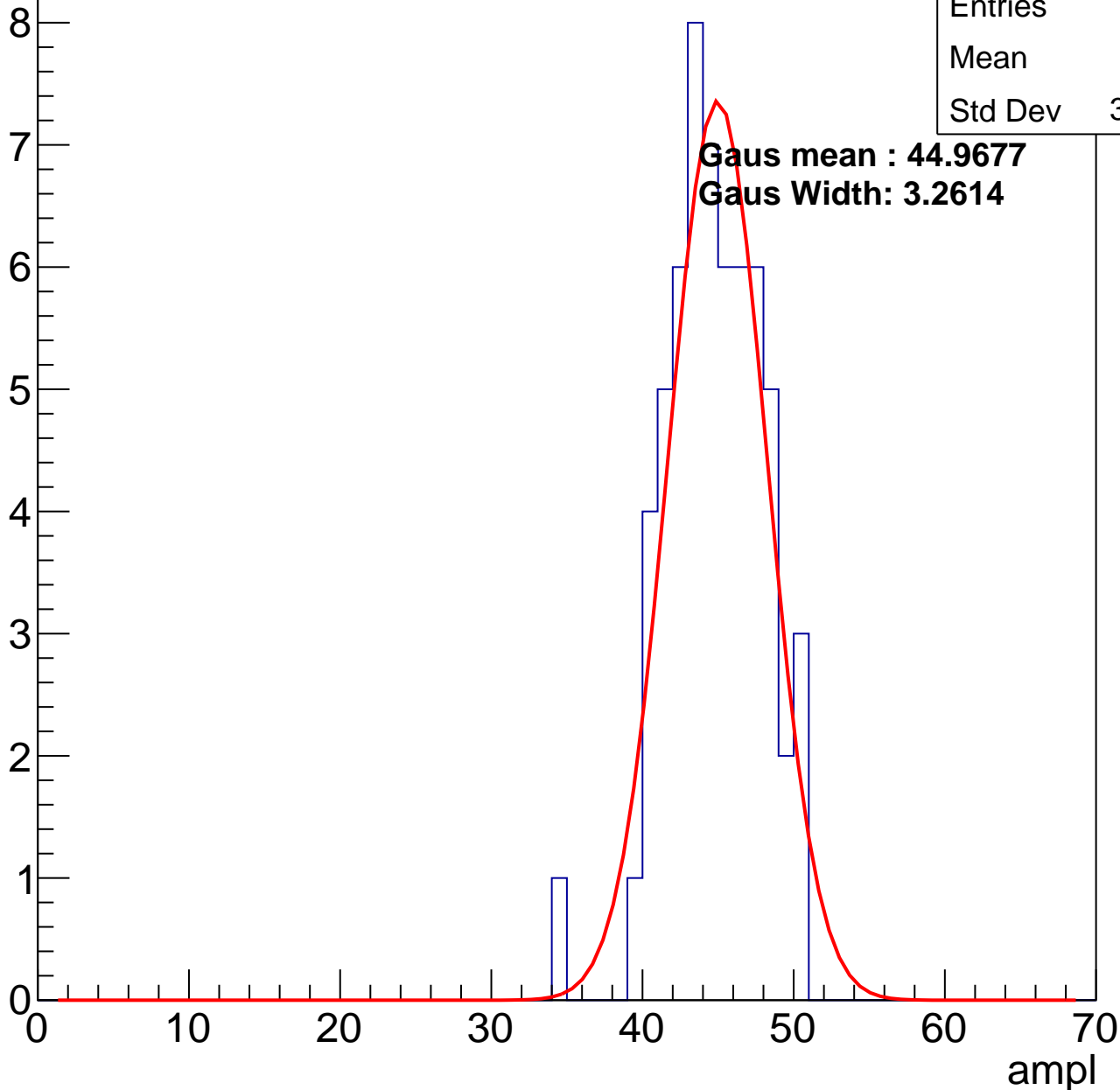
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	44.3
Std Dev	3.116

Gaus mean : 44.9677

Gaus Width: 3.2614



B0L002S, U12-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	50.54
Std Dev	2.993

Entry

10

8

6

4

2

0

0

10

20

30

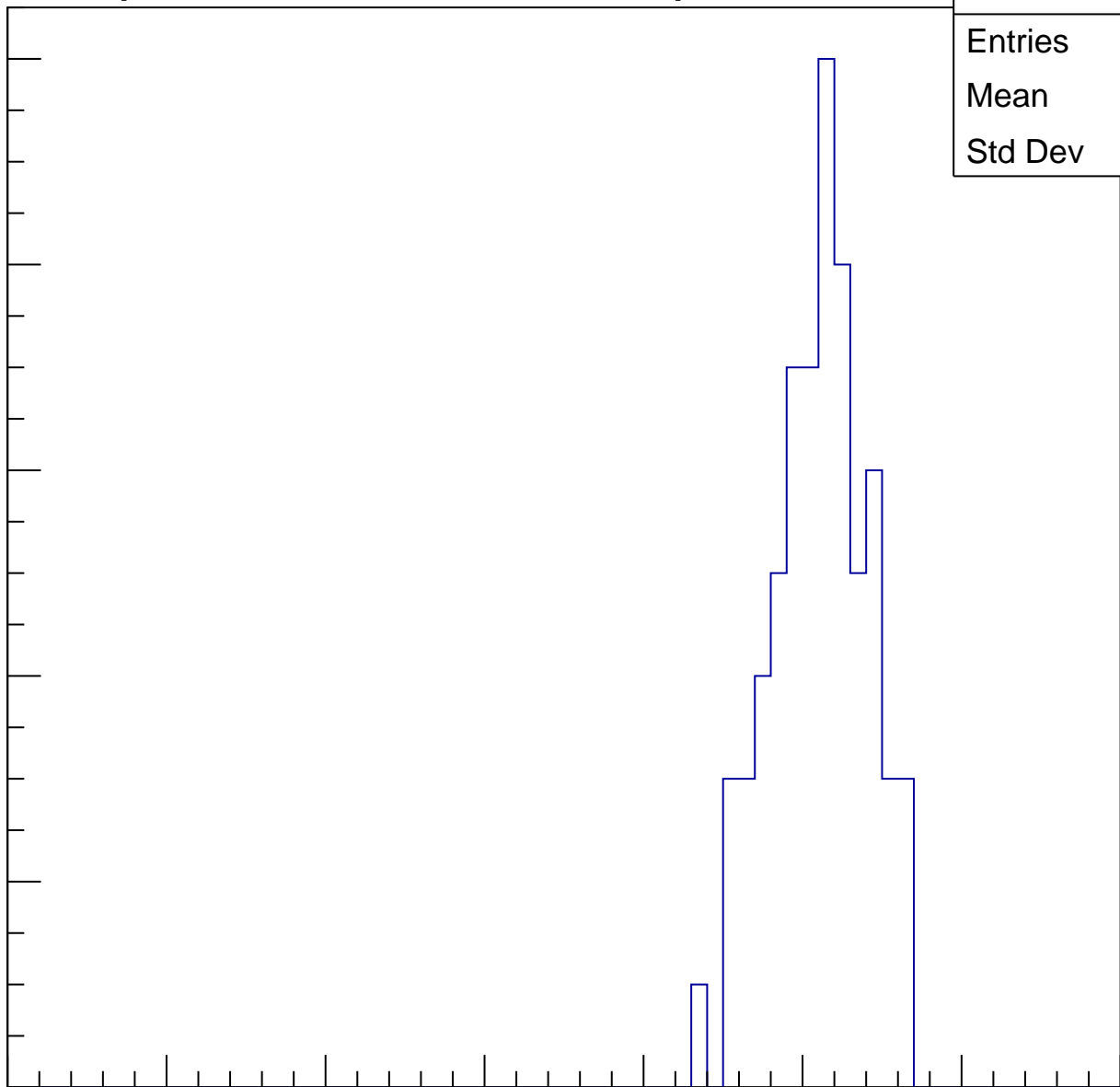
40

50

60

70

ampl

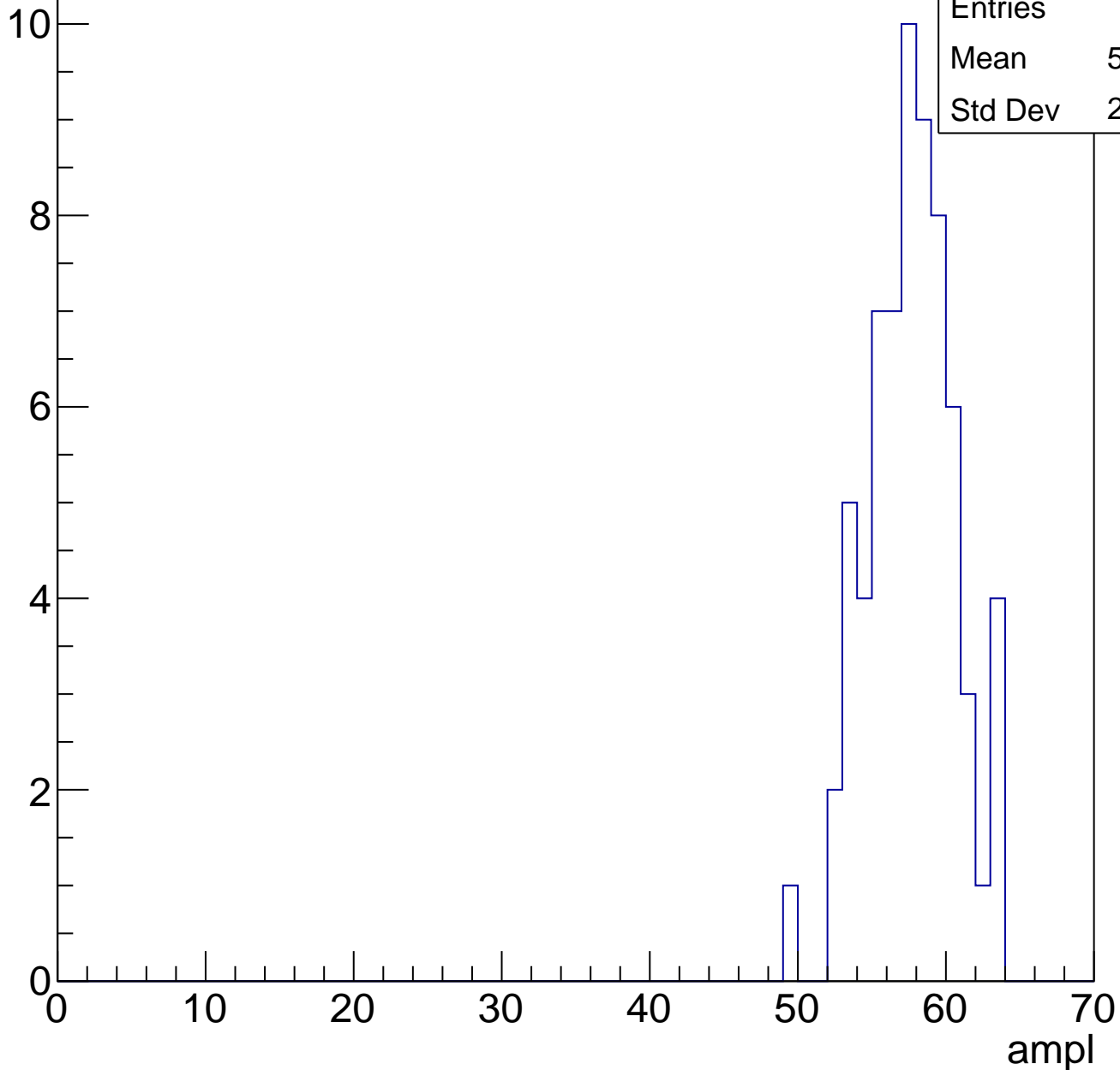


B0L002S, U12-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	57.19
Std Dev	2.928

Entry

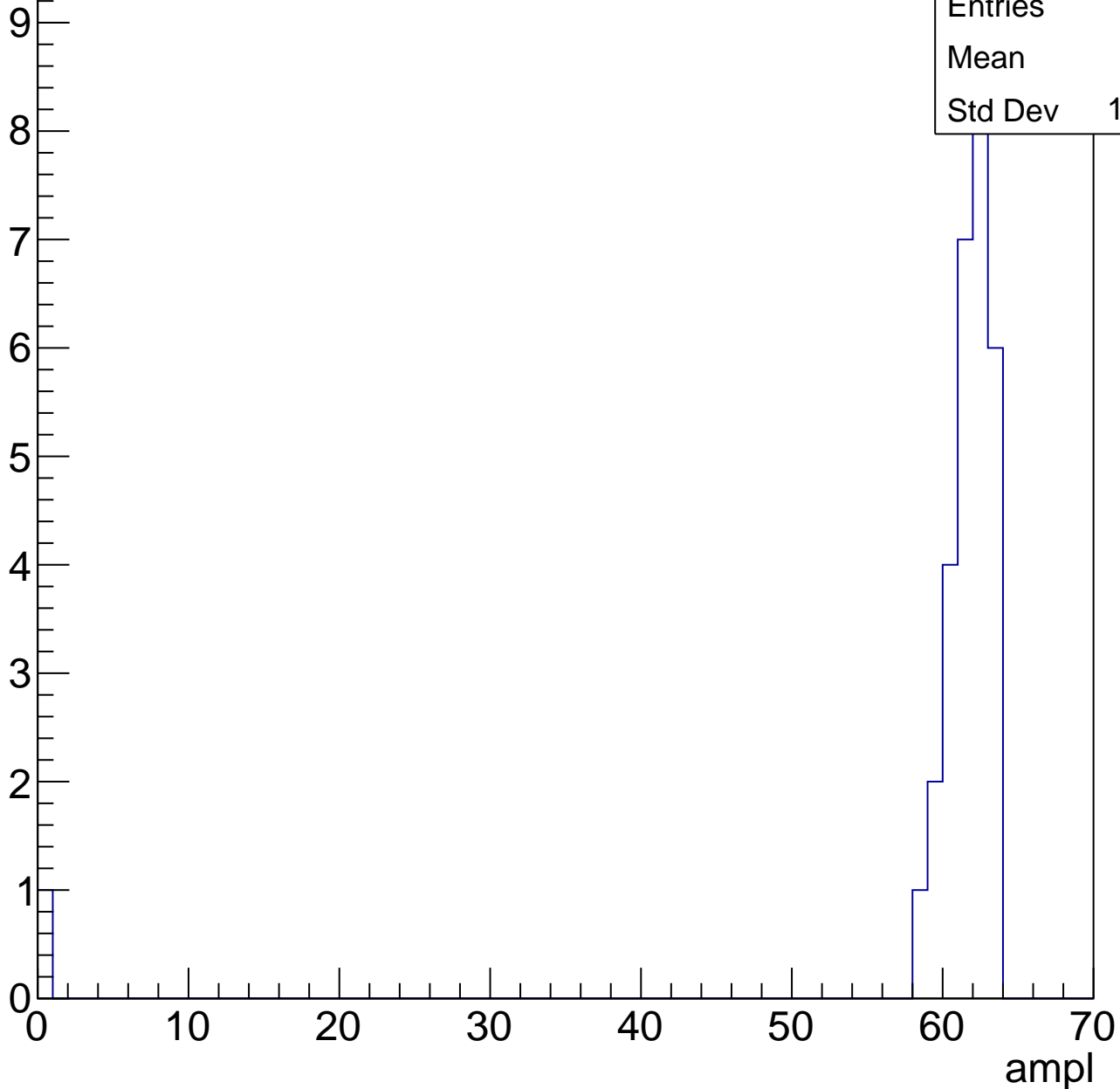


B0L002S, U12-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.3
Std Dev	11.09



B0L002S, U12-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L002S, U12-ch114, adc0

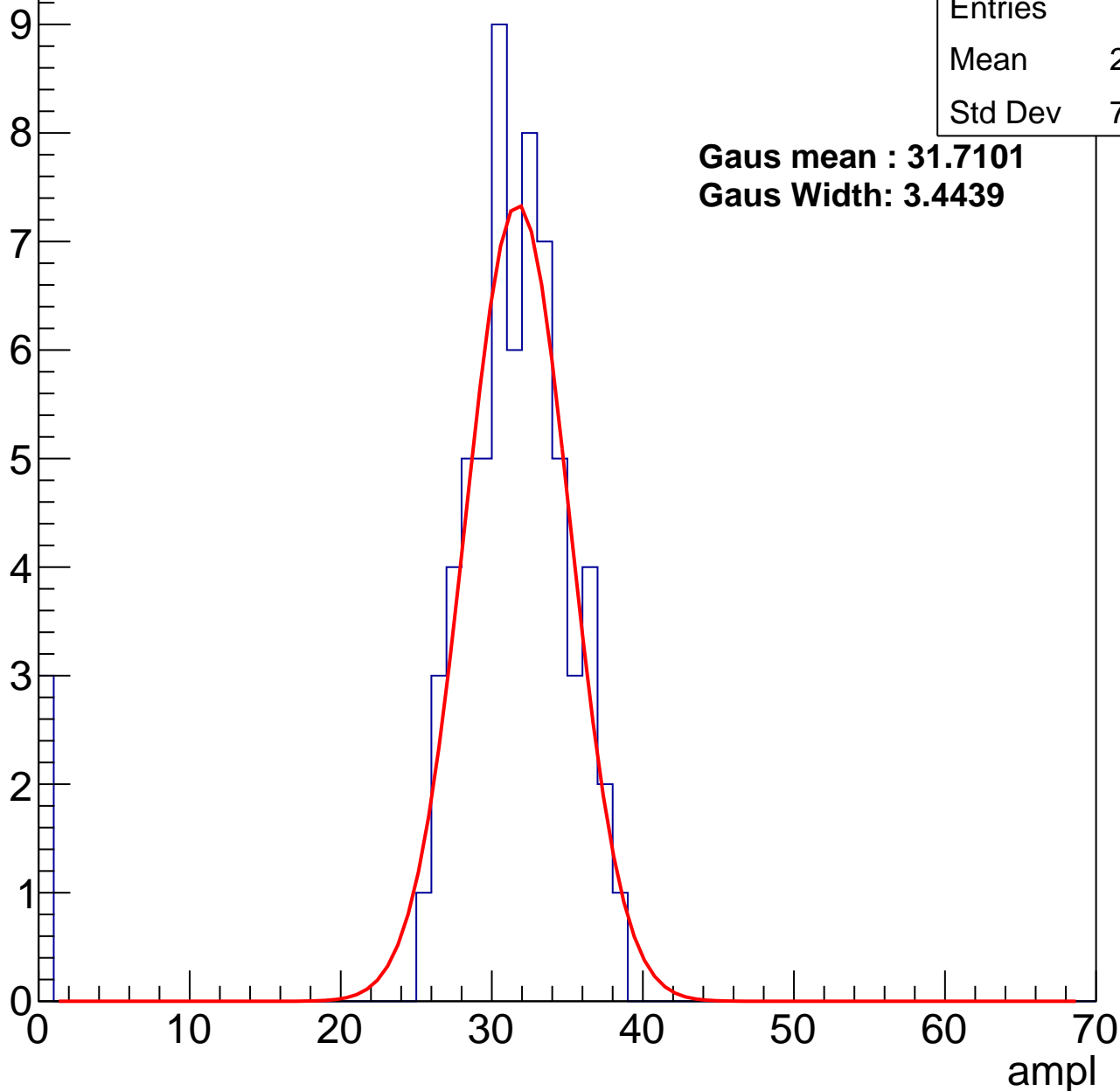
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.85
Std Dev	7.172

Gaus mean : 31.7101

Gaus Width: 3.4439



B0L002S, U12-ch114, adc1

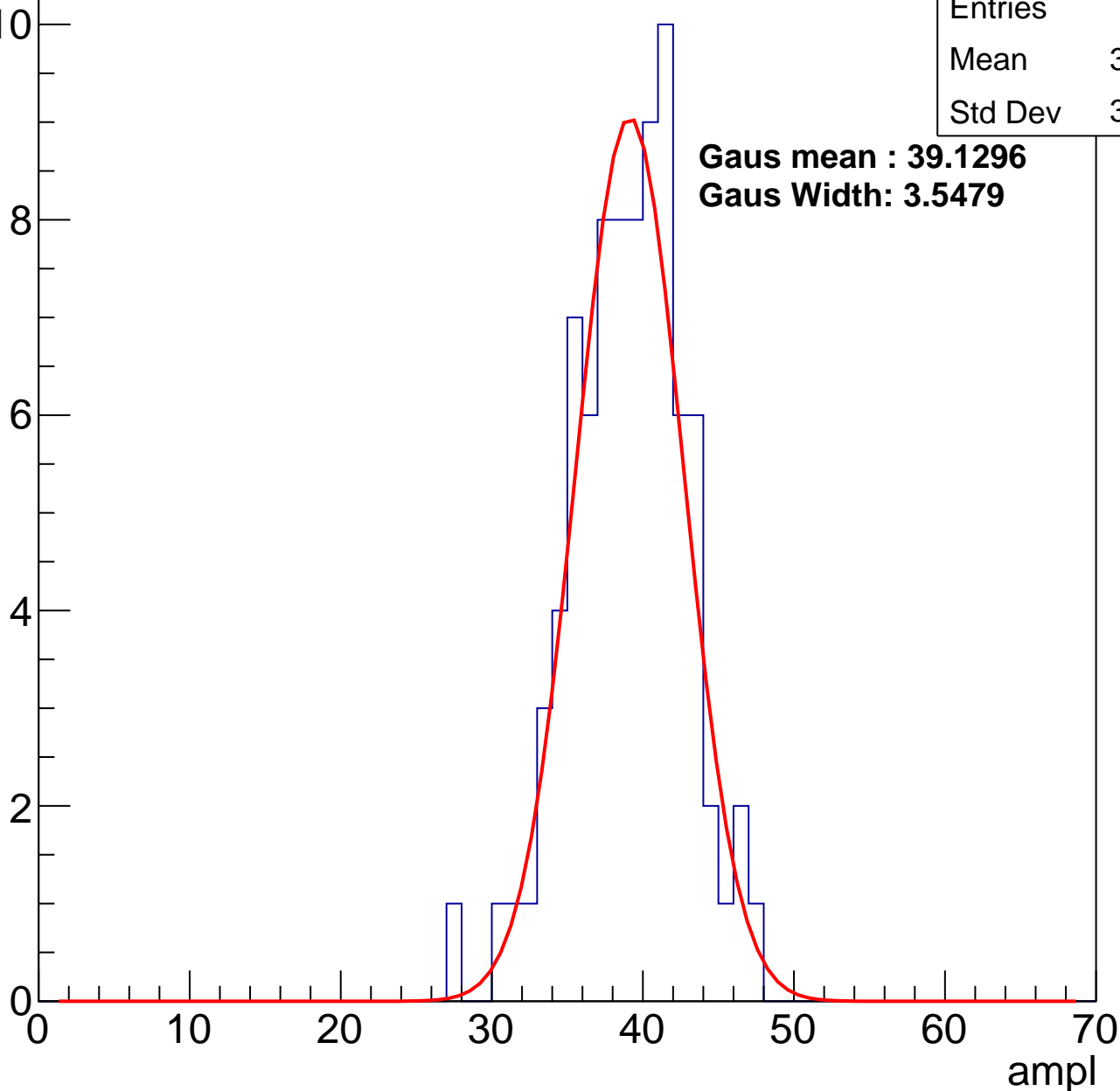
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	38.59
Std Dev	3.733

Gaus mean : 39.1296

Gaus Width: 3.5479



B0L002S, U12-ch114, adc2

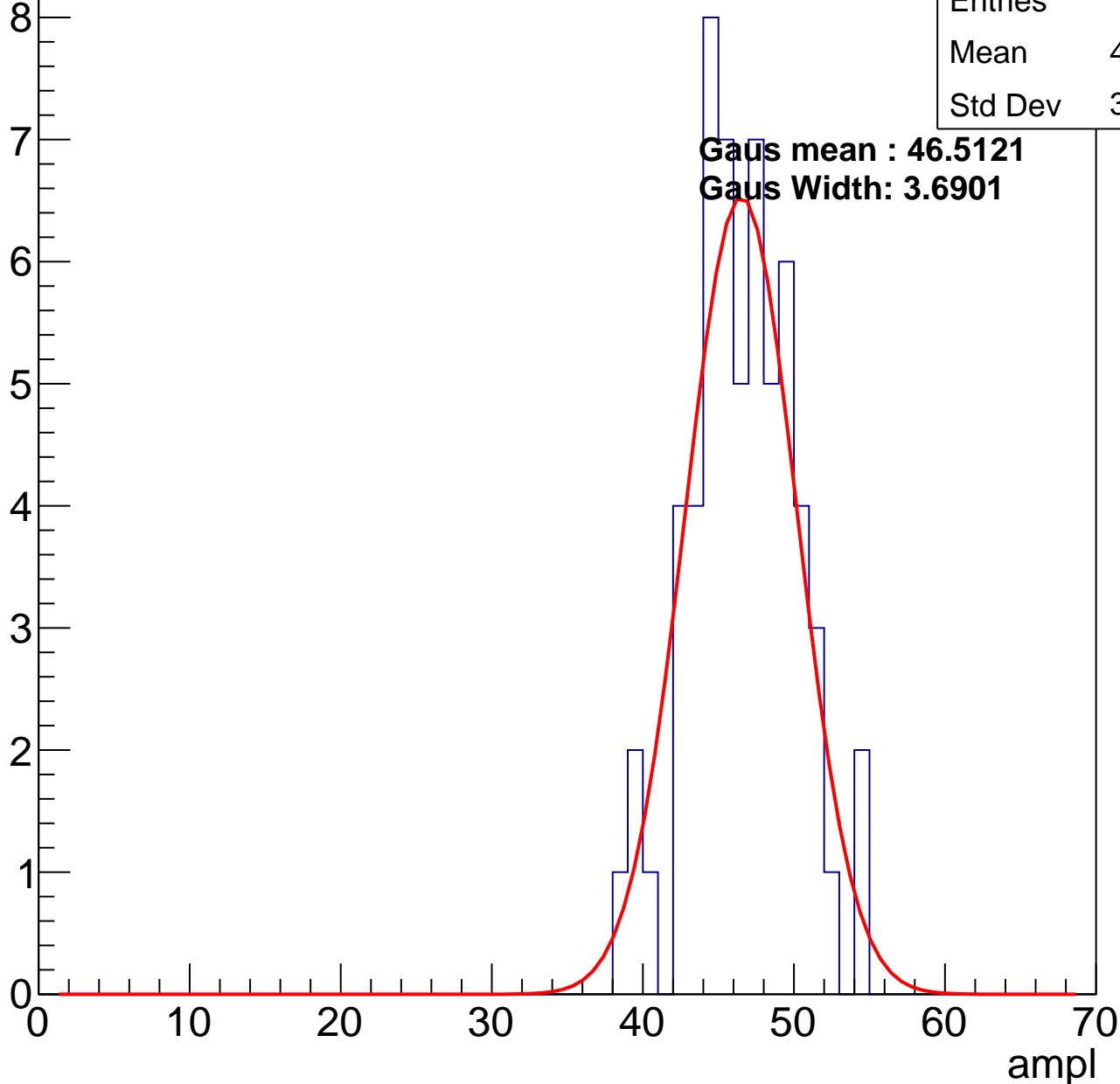
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	46.15
Std Dev	3.463

Gaus mean : 46.5121

Gaus Width: 3.6901

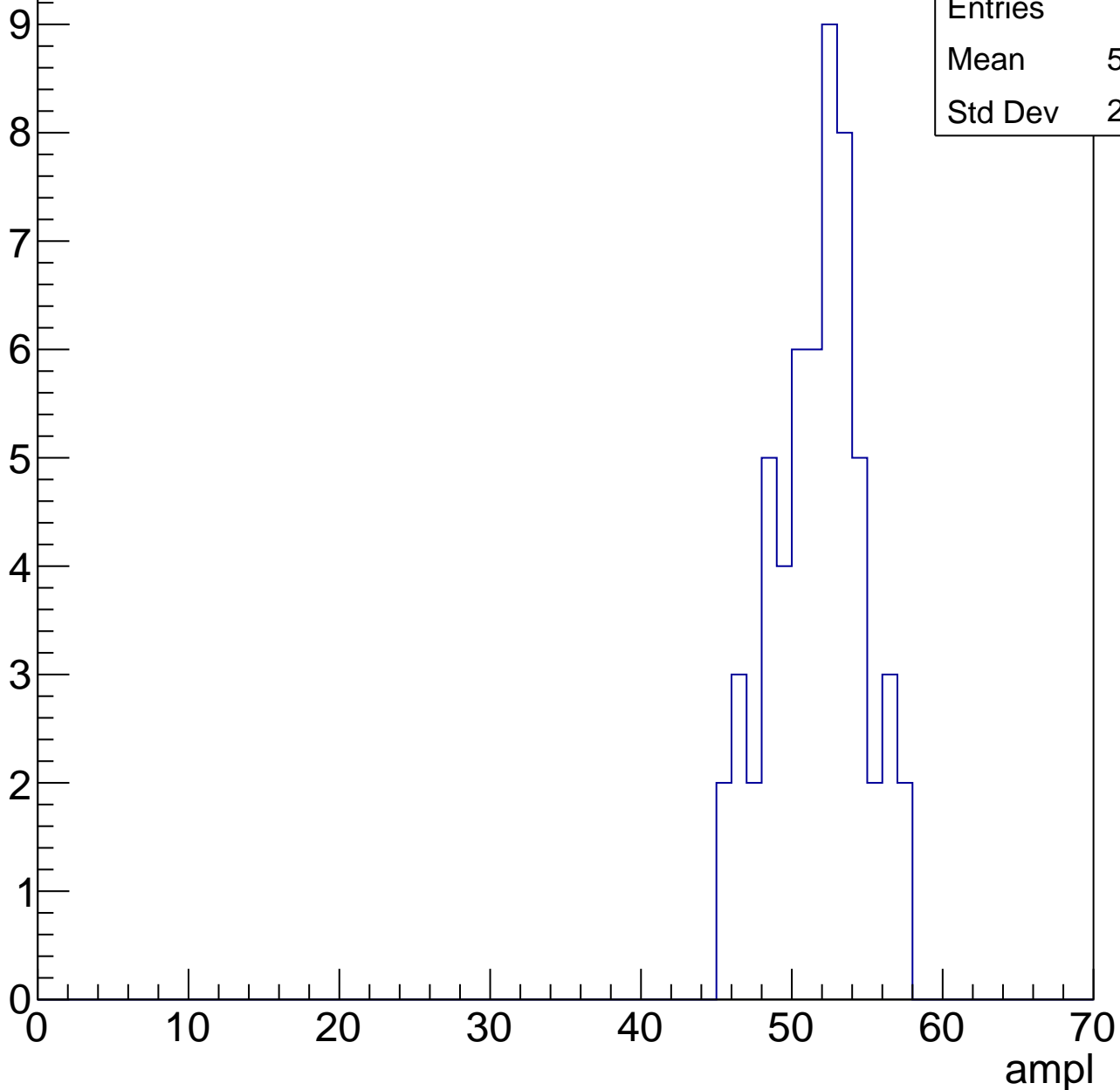


B0L002S, U12-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	51.19
Std Dev	2.988

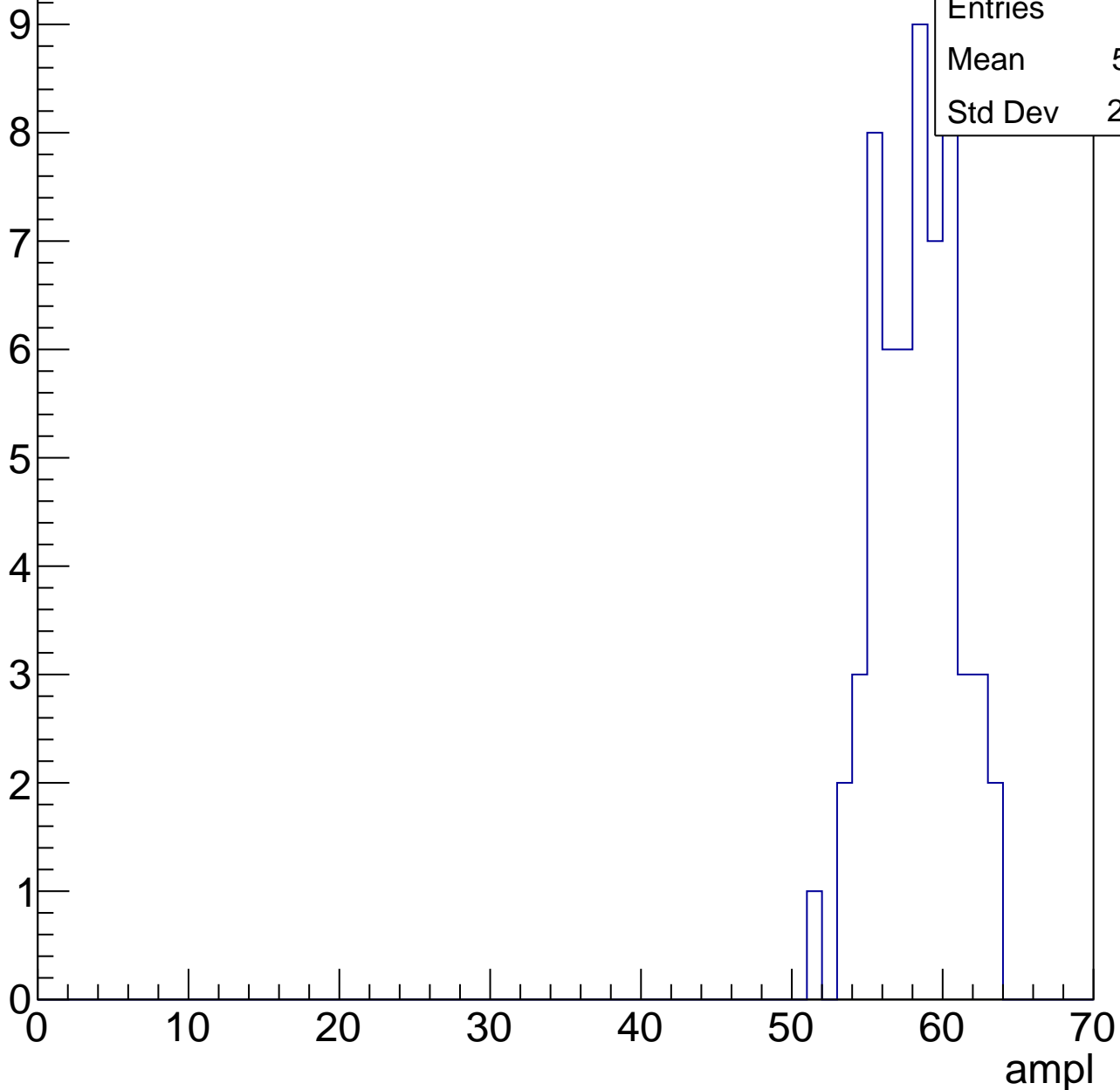


B0L002S, U12-ch114, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	57.71
Std Dev	2.652

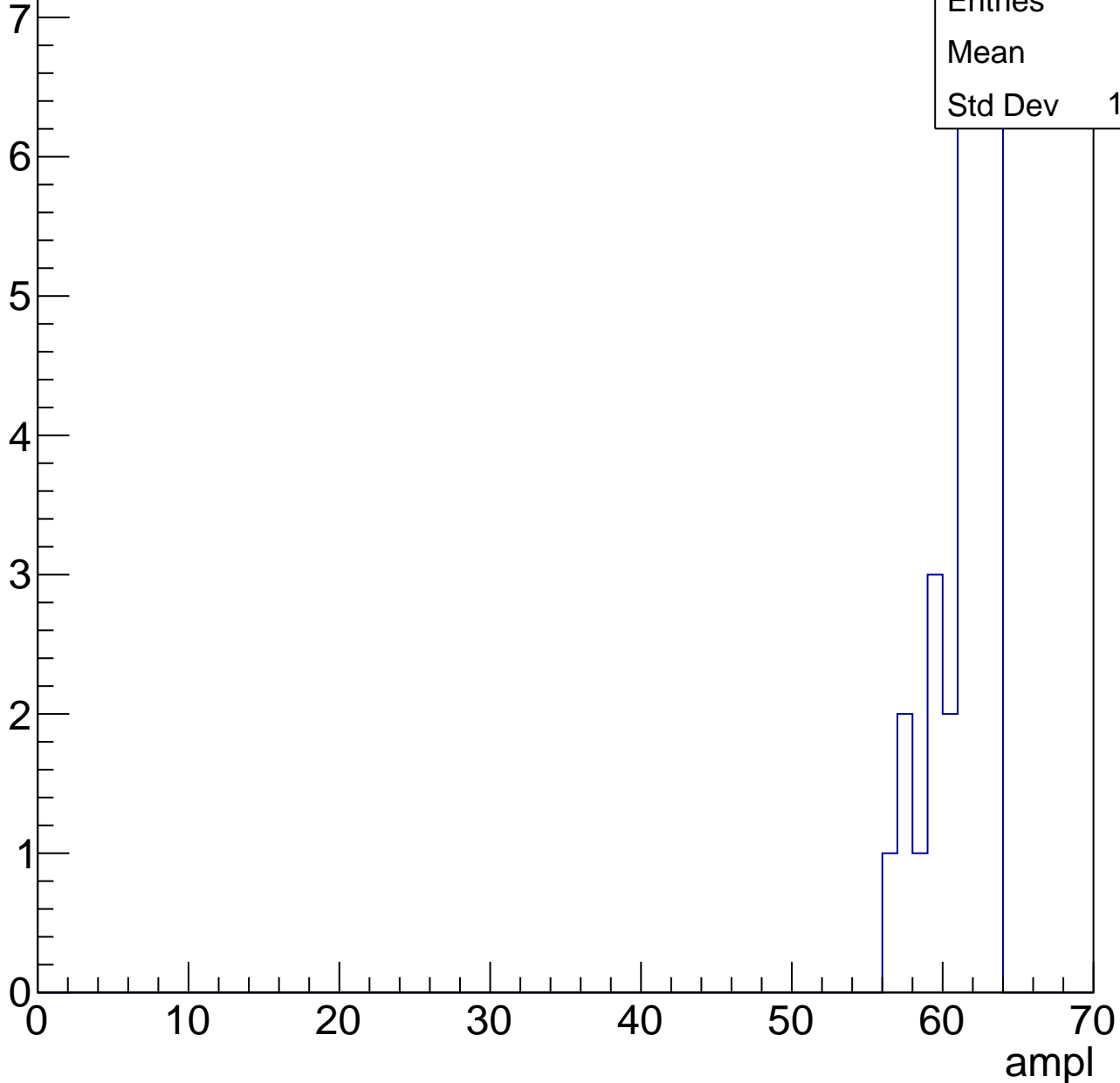


B0L002S, U12-ch114, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

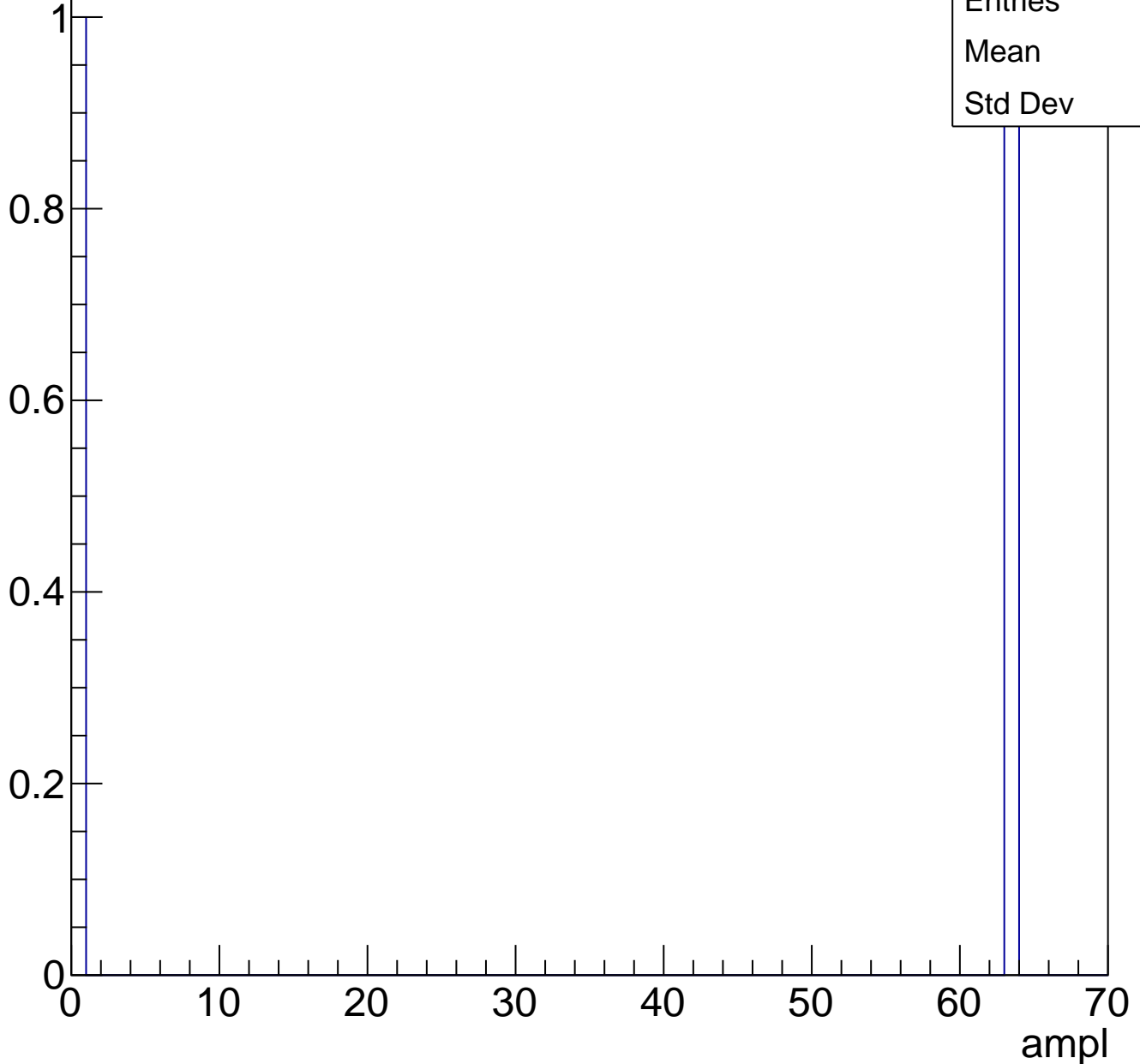
Entries	30
Mean	60.9
Std Dev	1.955



B0L002S, U12-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch115, adc0

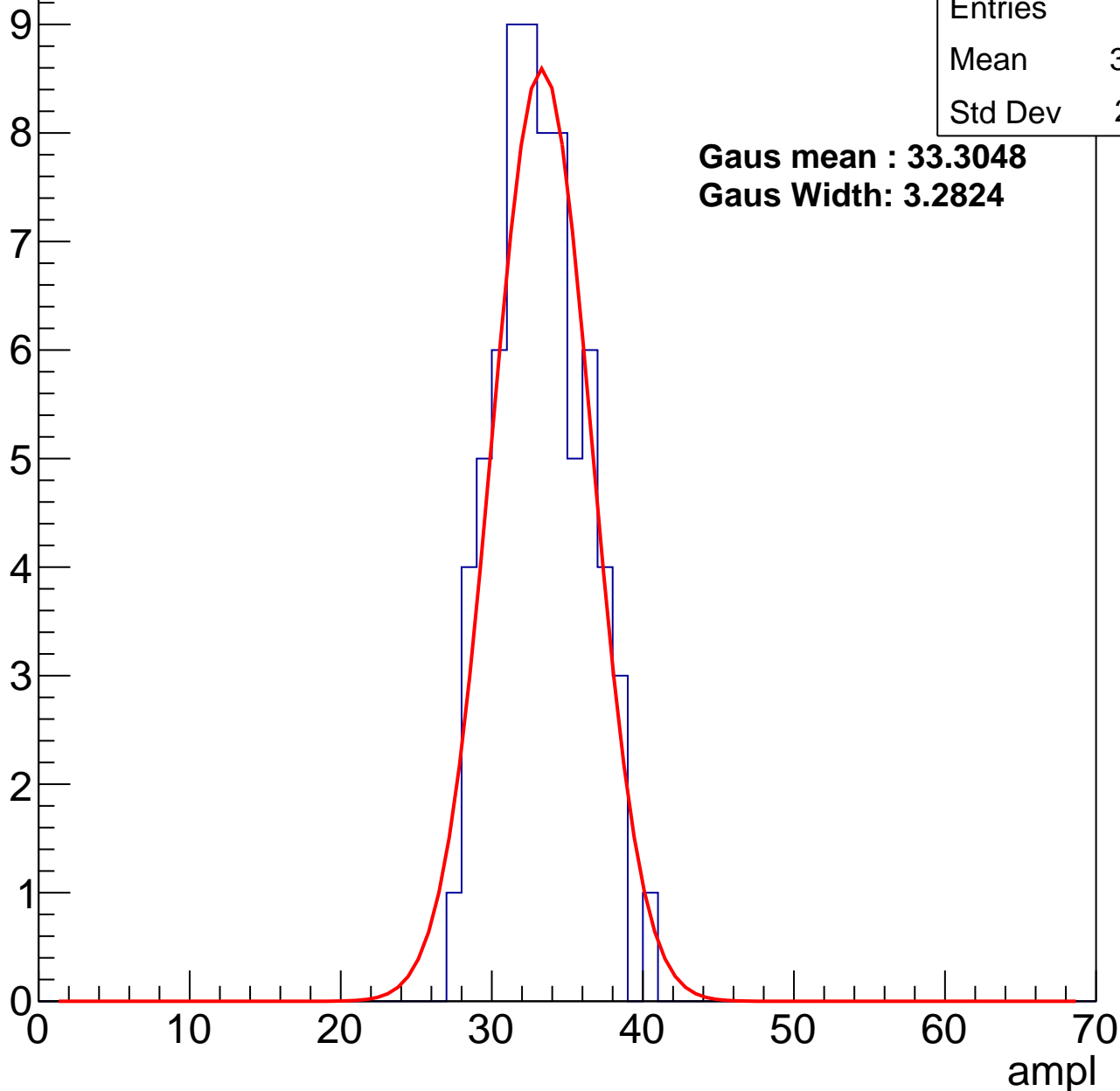
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	32.75
Std Dev	2.901

Gaus mean : 33.3048

Gaus Width: 3.2824



B0L002S, U12-ch115, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	39.89
Std Dev	3.42

Gaus mean : 40.5077

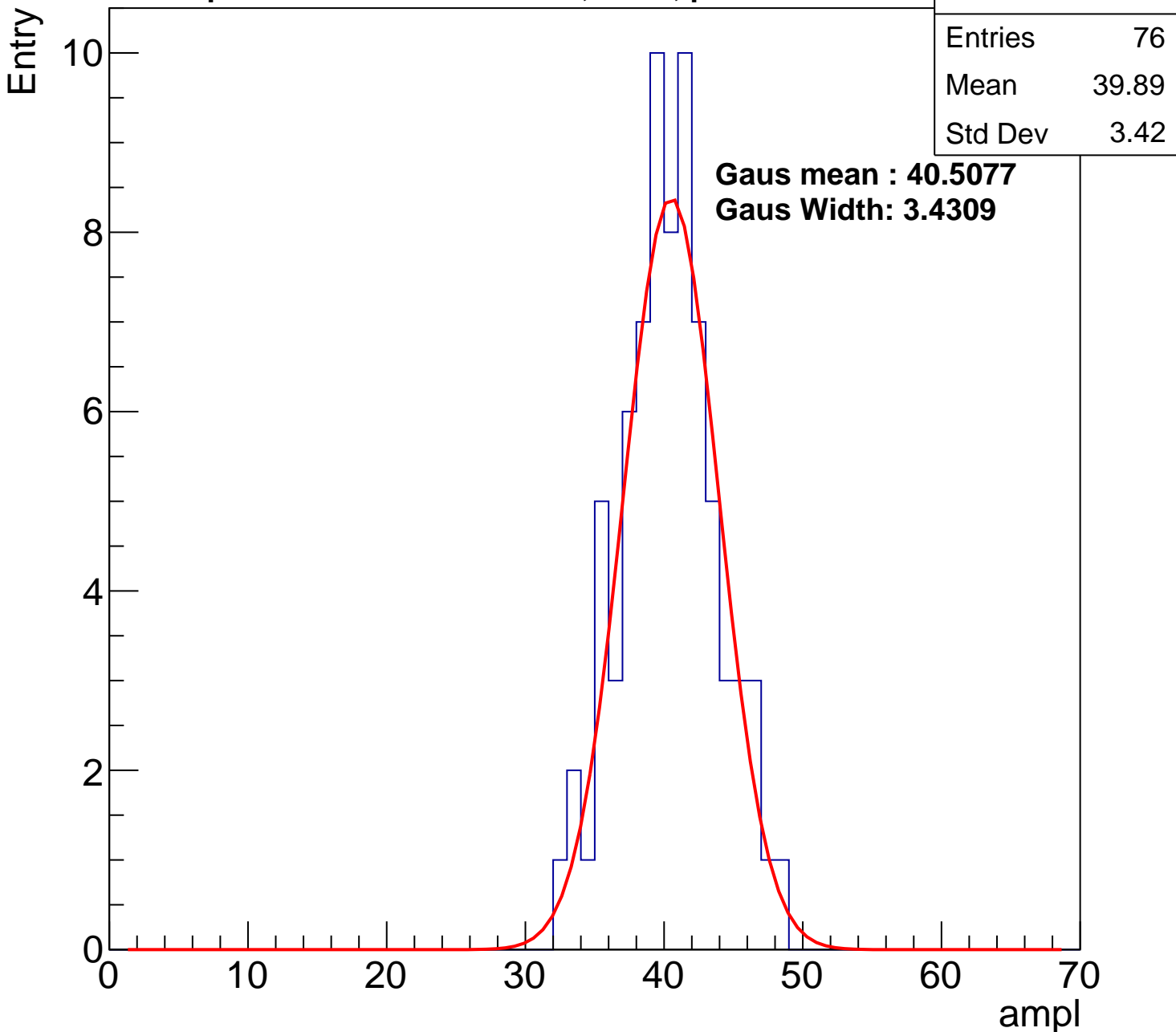
Gaus Width: 3.4309

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch115, adc2

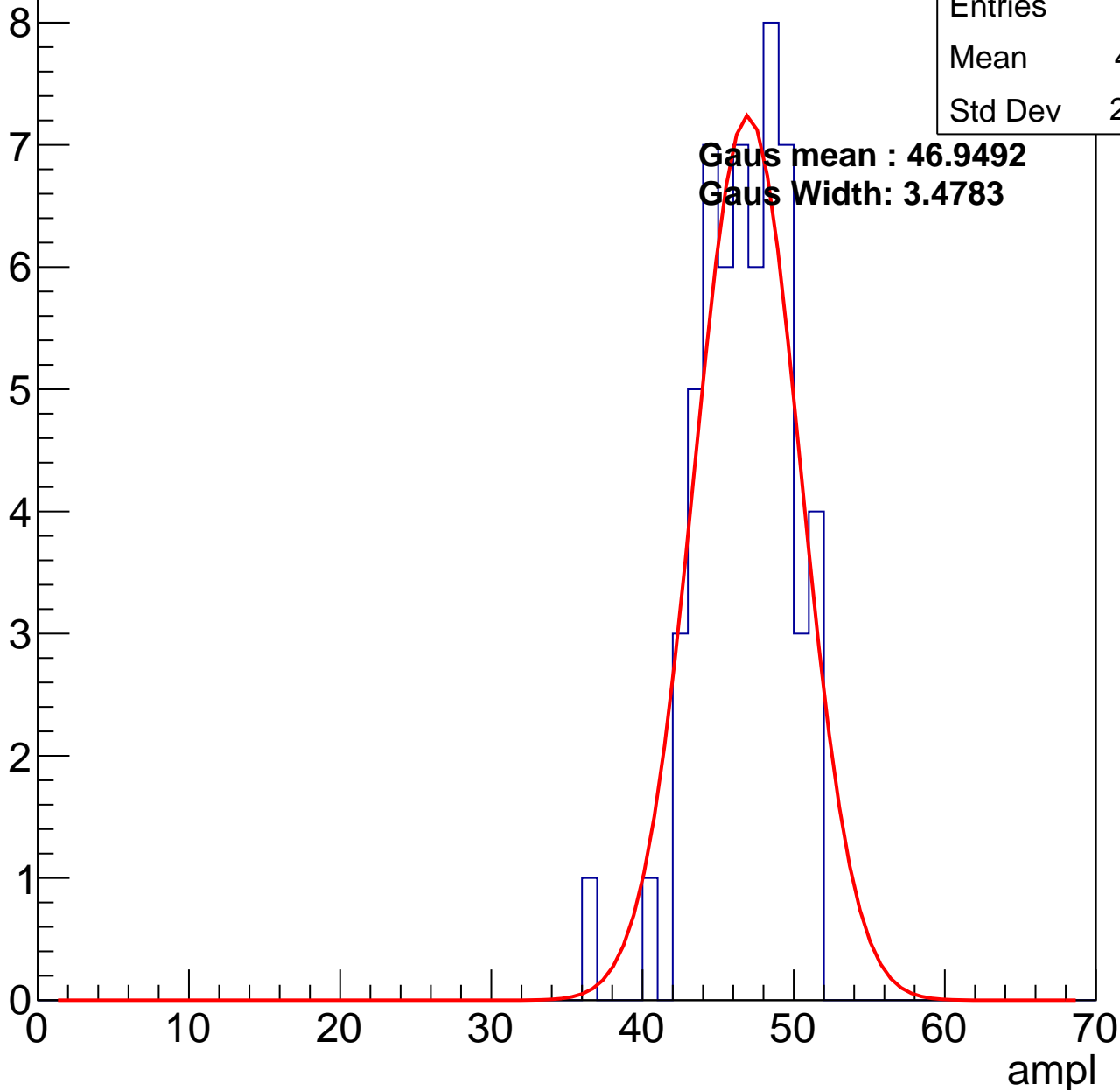
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	46.21
Std Dev	2.964

Gaus mean : 46.9492

Gaus Width: 3.4783

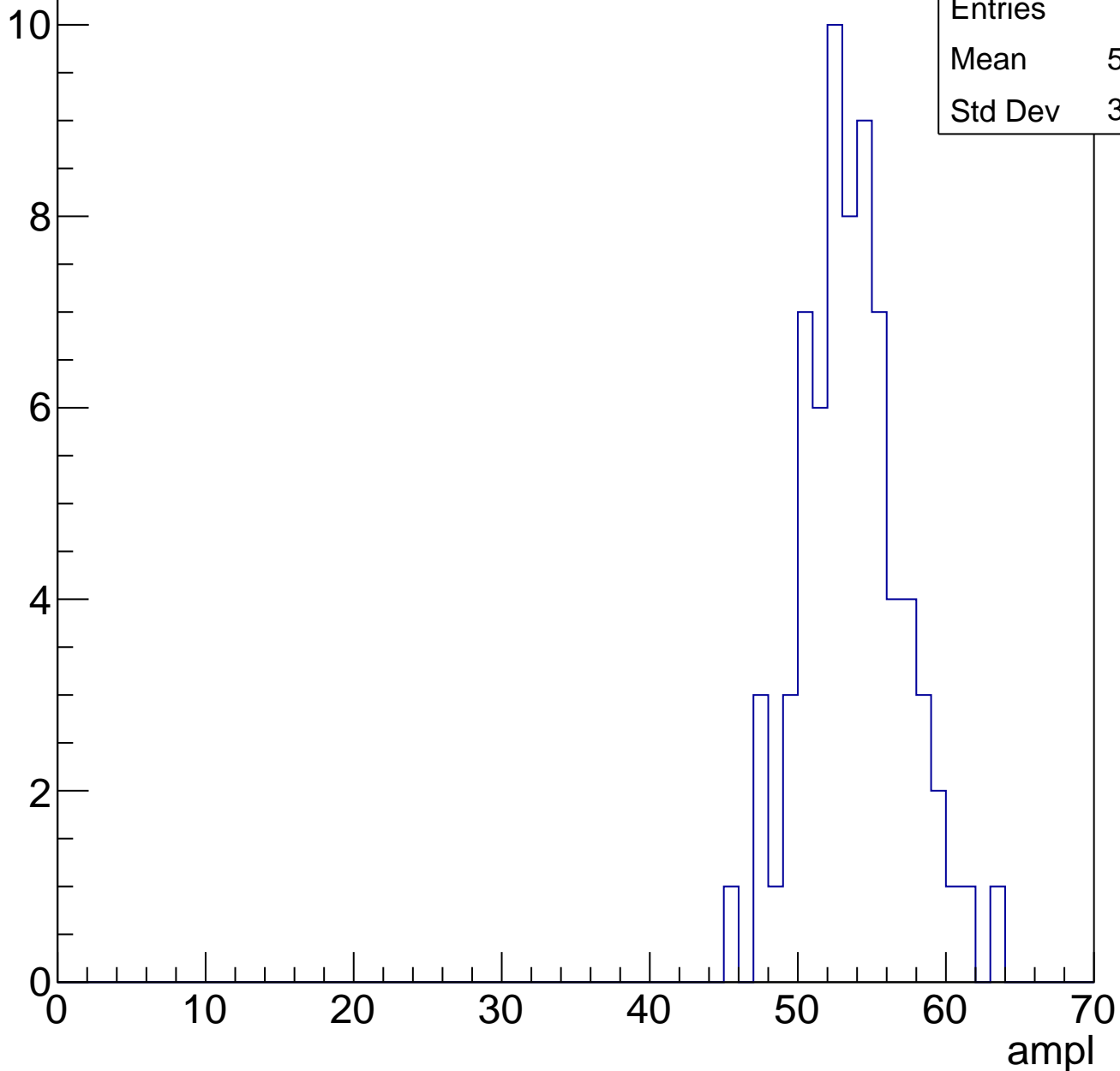


B0L002S, U12-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

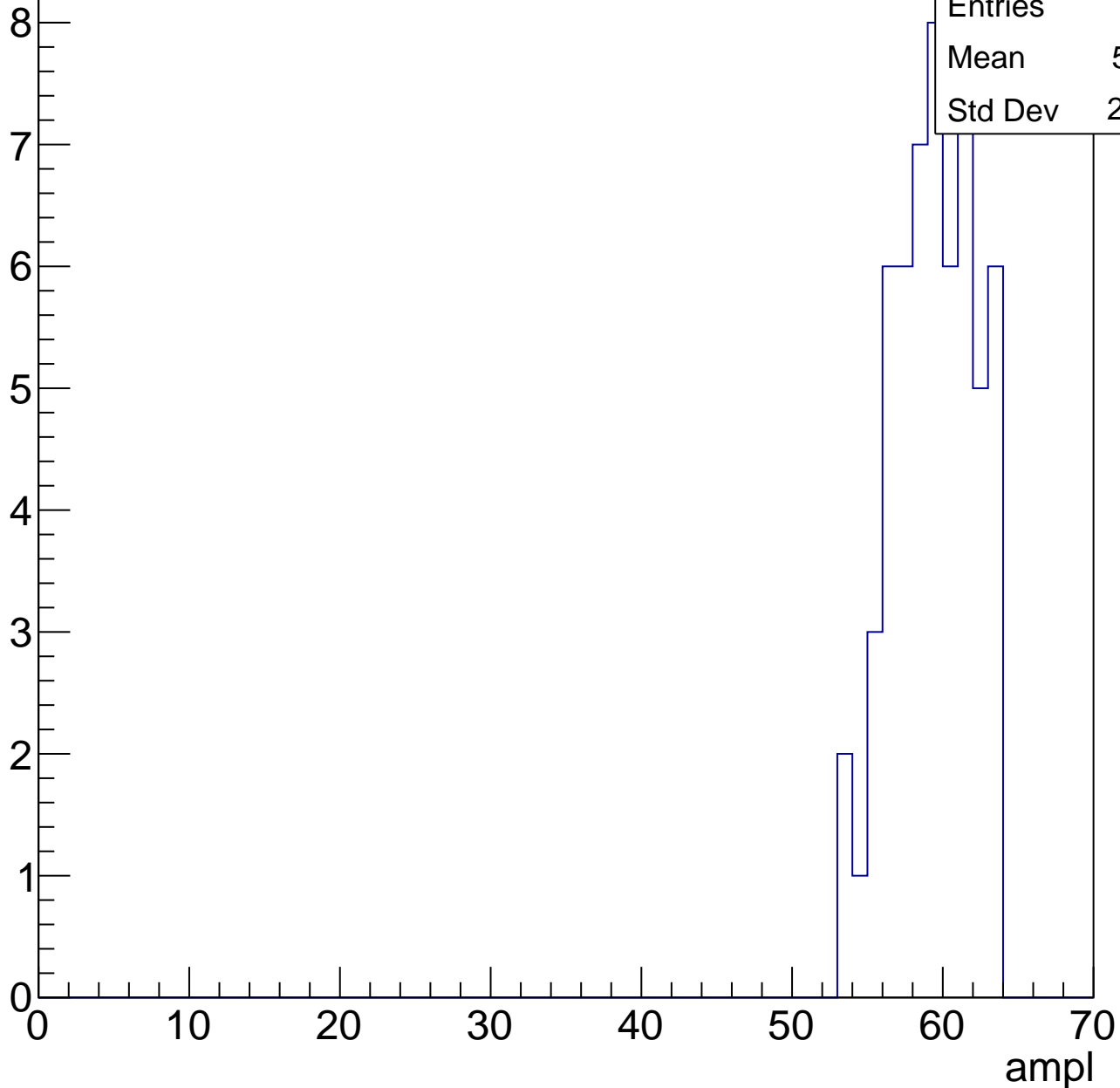
Entries	71
Mean	53.24
Std Dev	3.429



B0L002S, U12-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



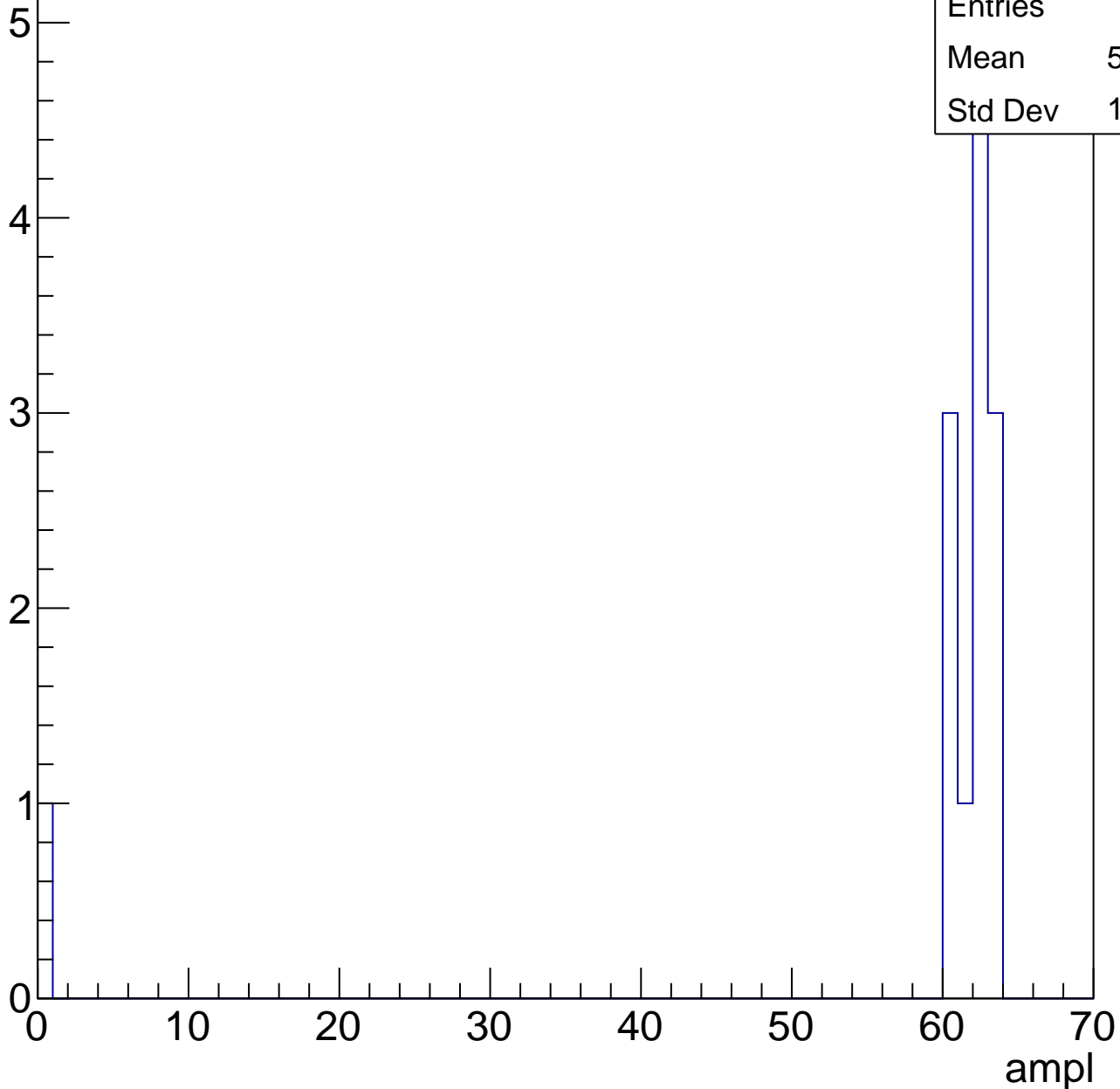
Entries	58
Mean	58.91
Std Dev	2.654

B0L002S, U12-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	13
Mean	56.92
Std Dev	16.47



B0L002S, U12-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch116, adc0

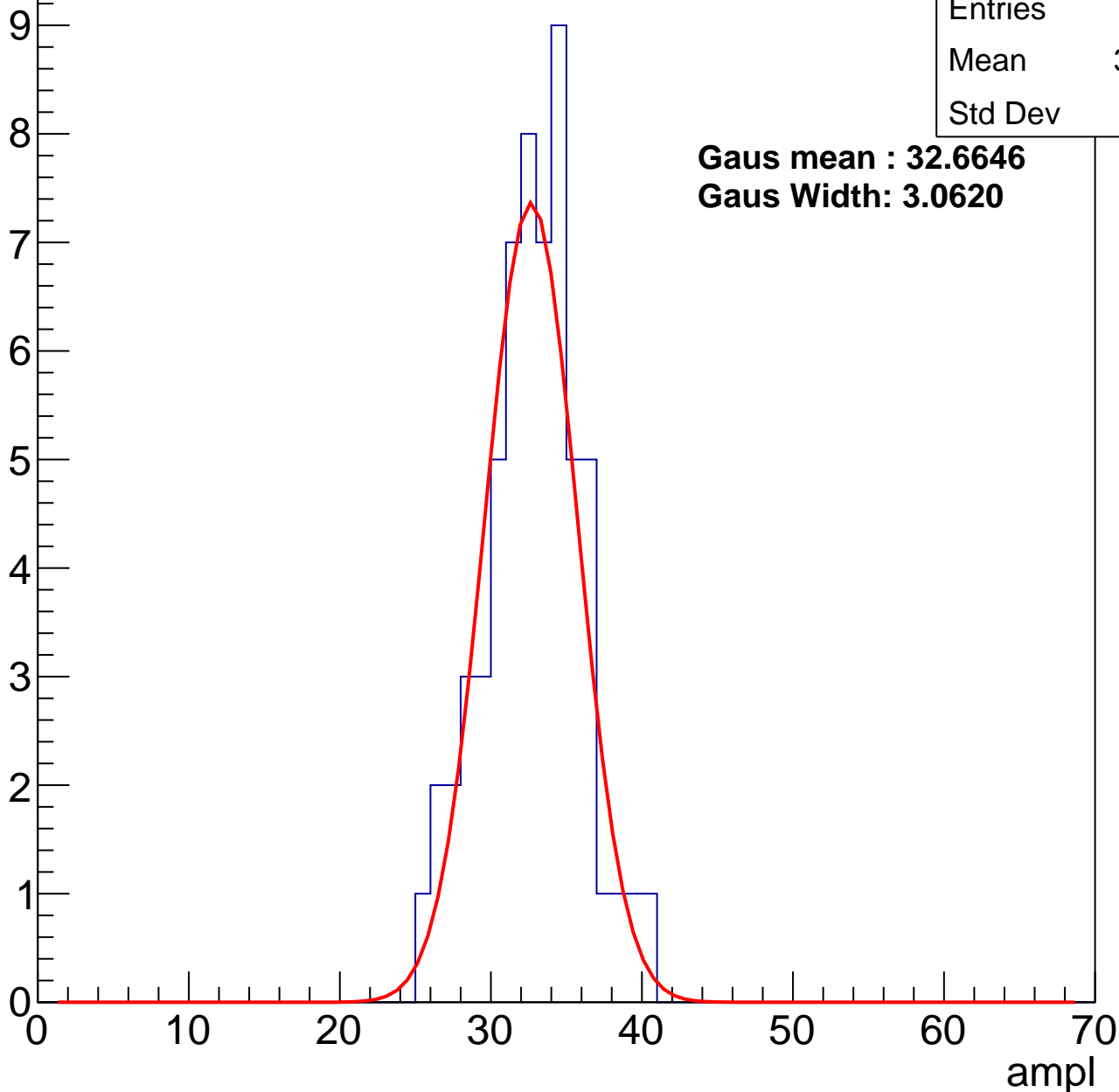
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	32.31
Std Dev	3.16

Gaus mean : 32.6646

Gaus Width: 3.0620



B0L002S, U12-ch116, adc1

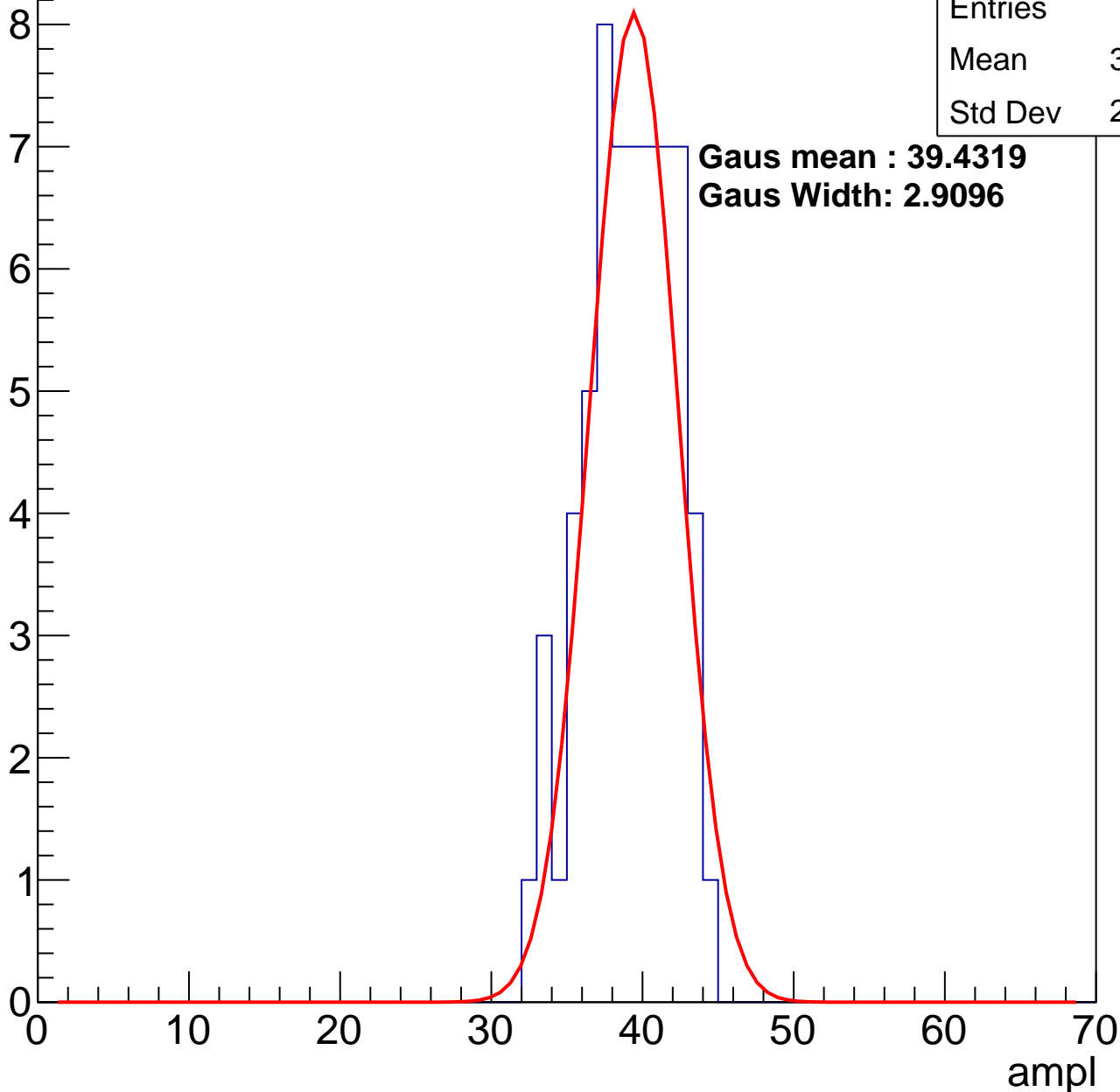
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	38.66
Std Dev	2.868

Gaus mean : 39.4319

Gaus Width: 2.9096



B0L002S, U12-ch116, adc2

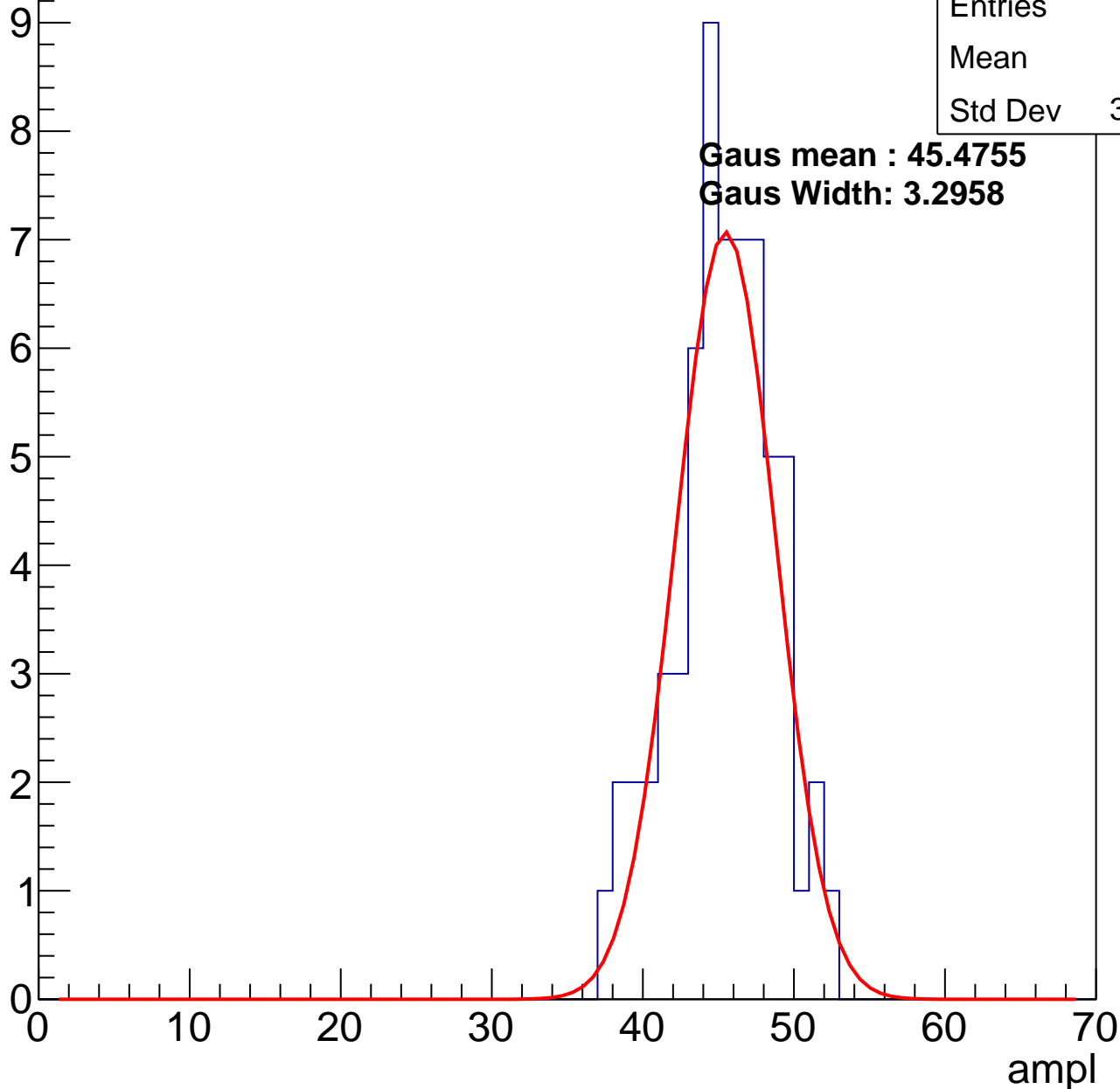
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.9
Std Dev	3.327

Gaus mean : 45.4755

Gaus Width: 3.2958

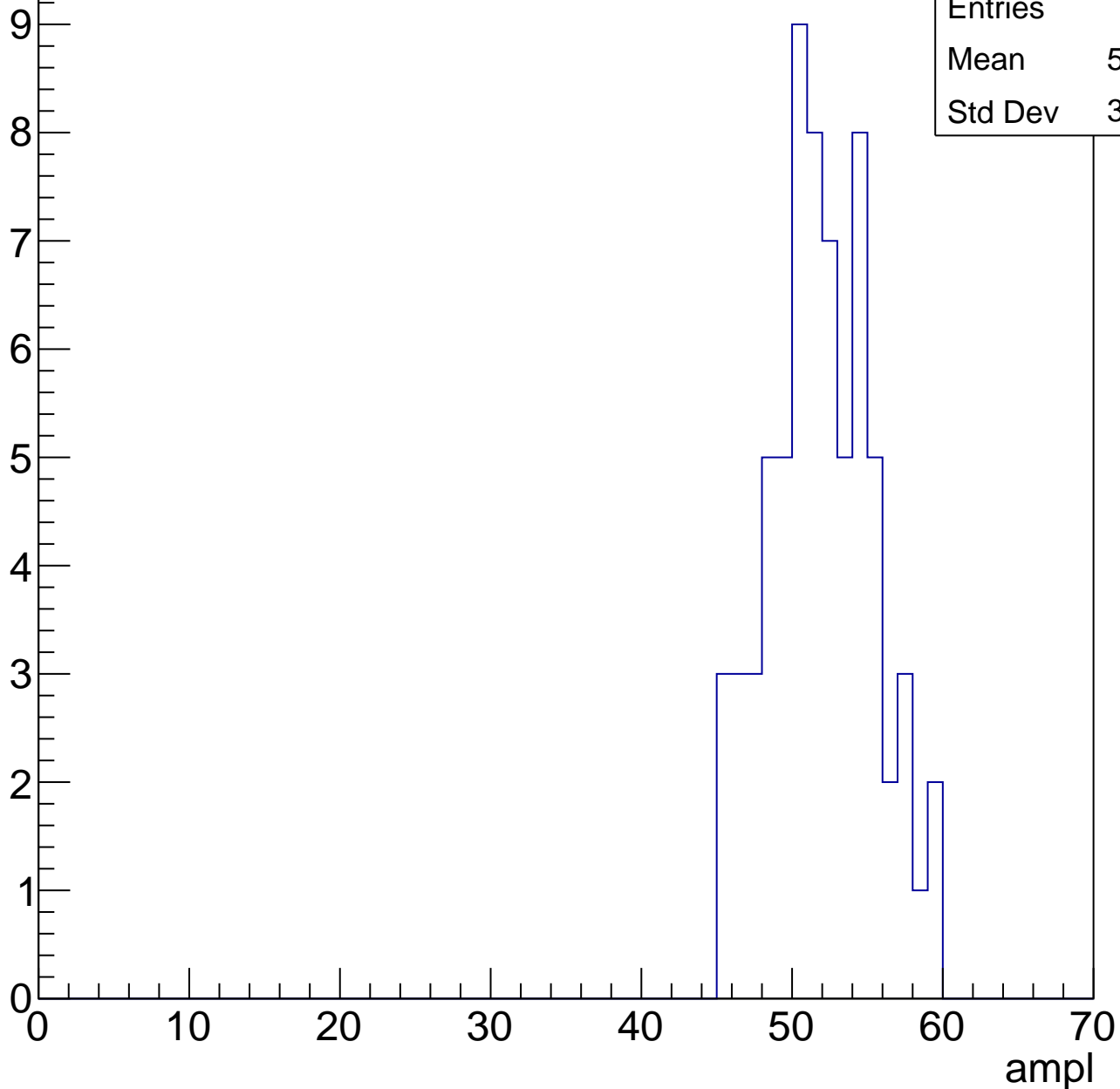


B0L002S, U12-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	51.48
Std Dev	3.412

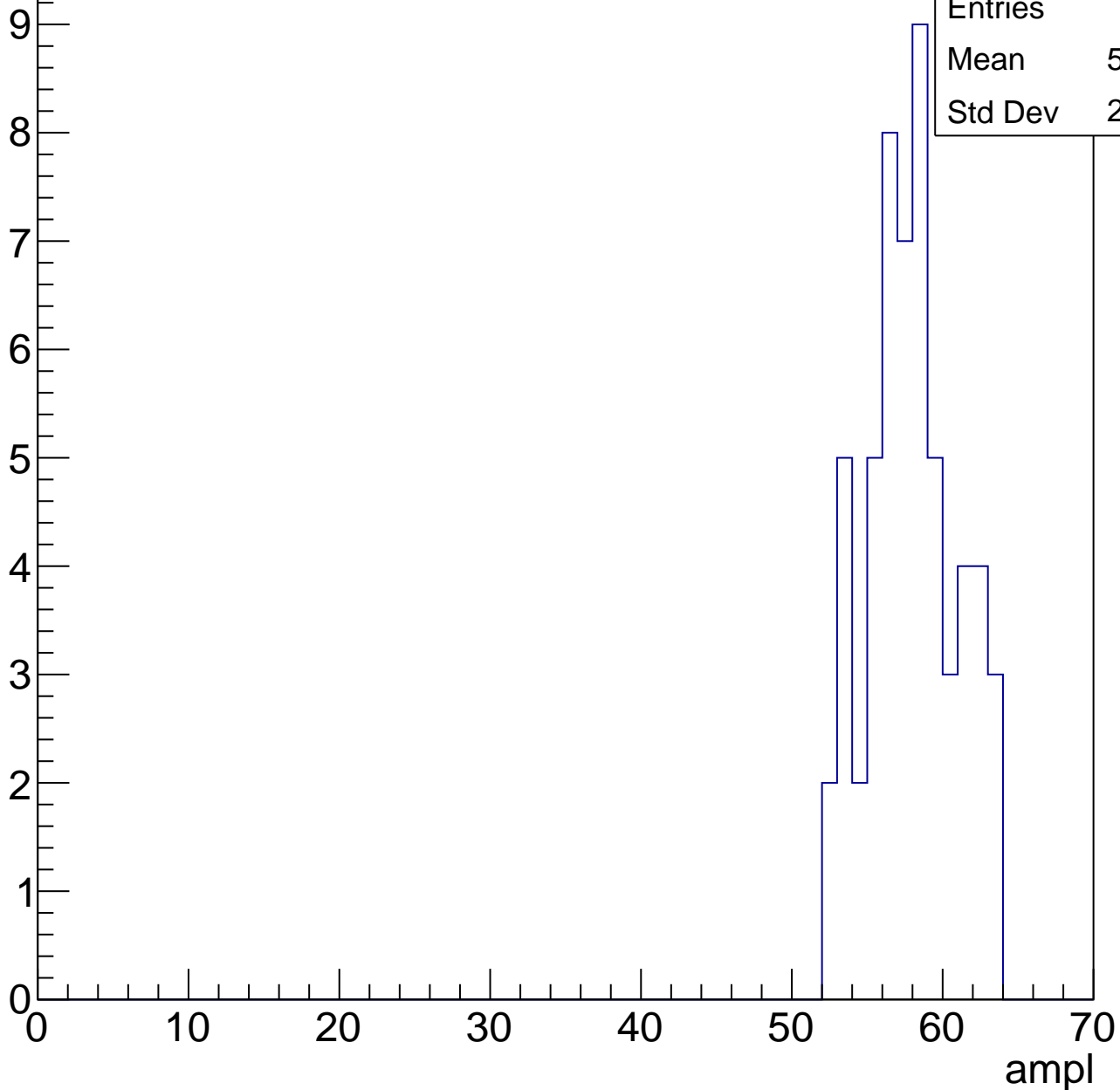


B0L002S, U12-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

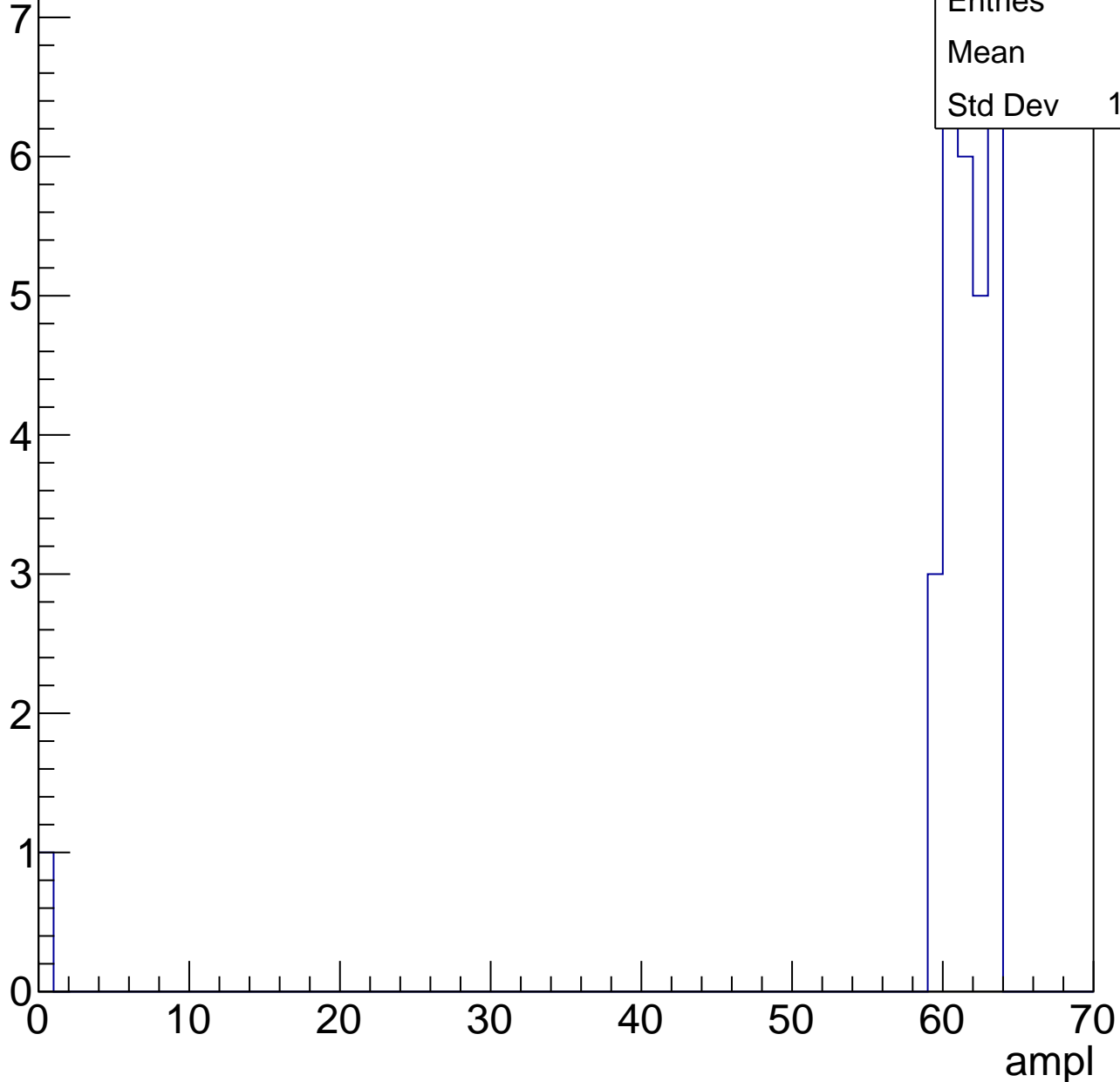
Entries	57
Mean	57.49
Std Dev	2.933



B0L002S, U12-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch116, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch117, adc0

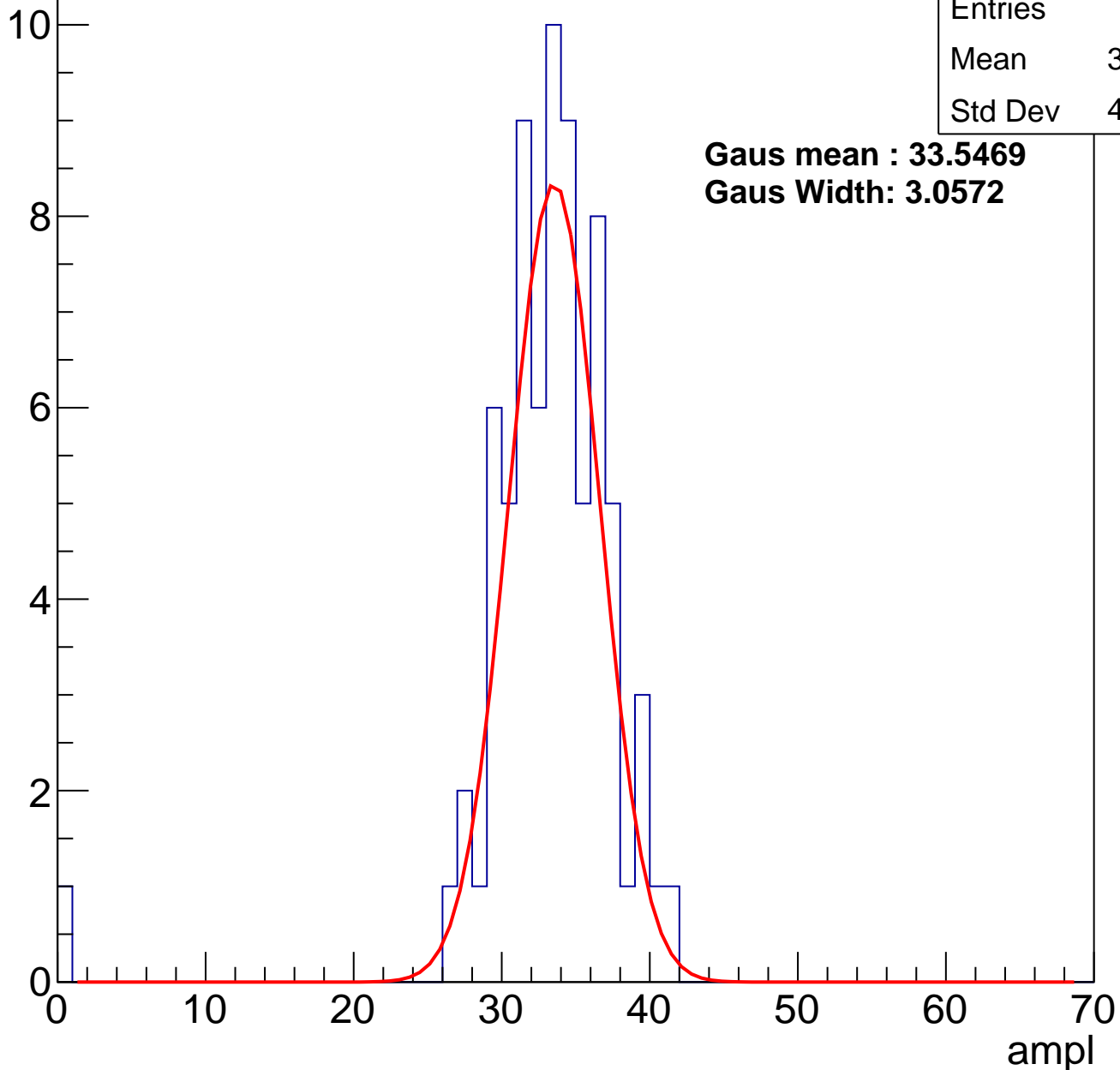
calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	32.74
Std Dev	4.987

Gaus mean : 33.5469

Gaus Width: 3.0572

Entry



B0L002S, U12-ch117, adc1

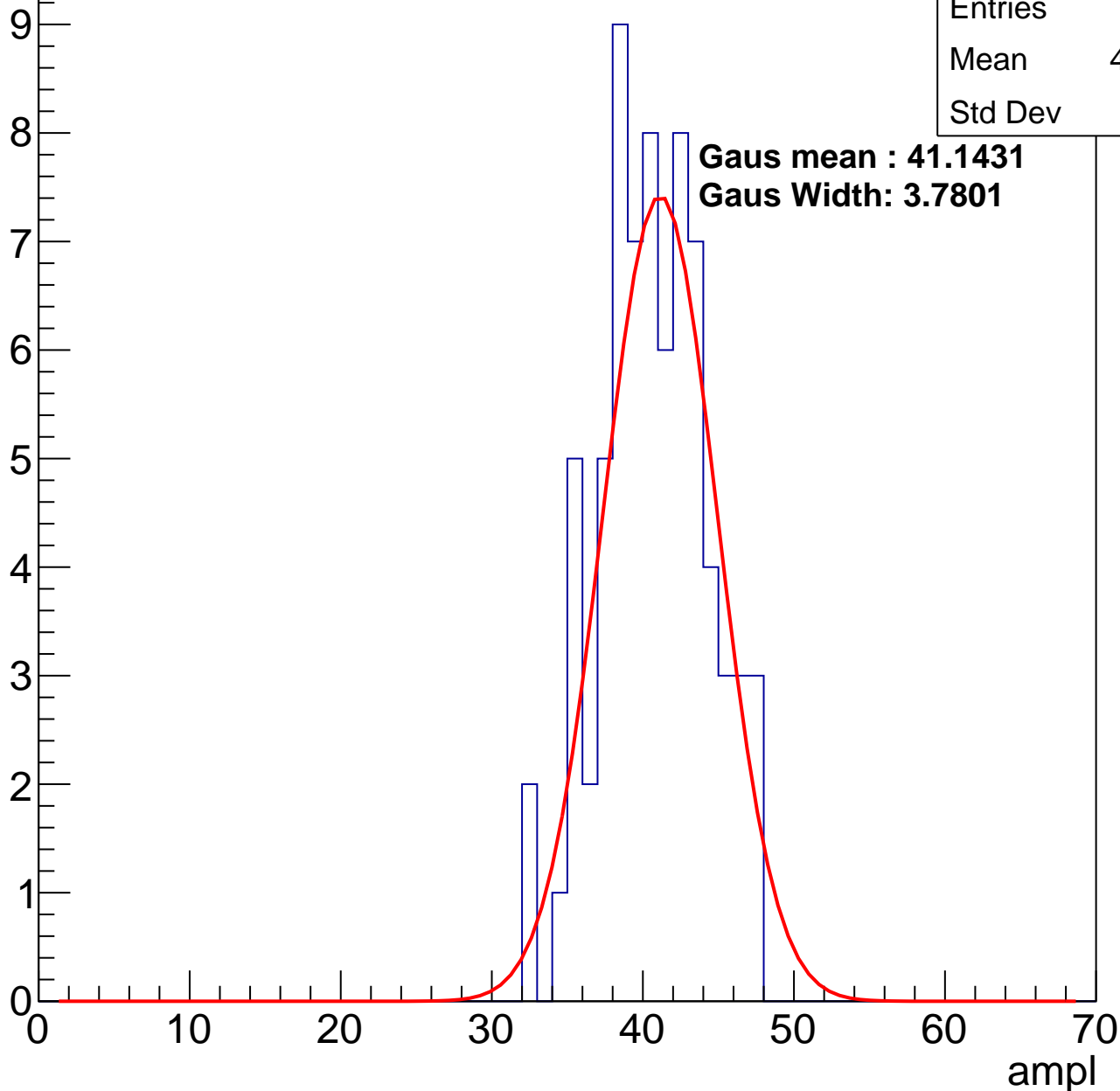
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	40.25
Std Dev	3.51

Gaus mean : 41.1431

Gaus Width: 3.7801



B0L002S, U12-ch117, adc2

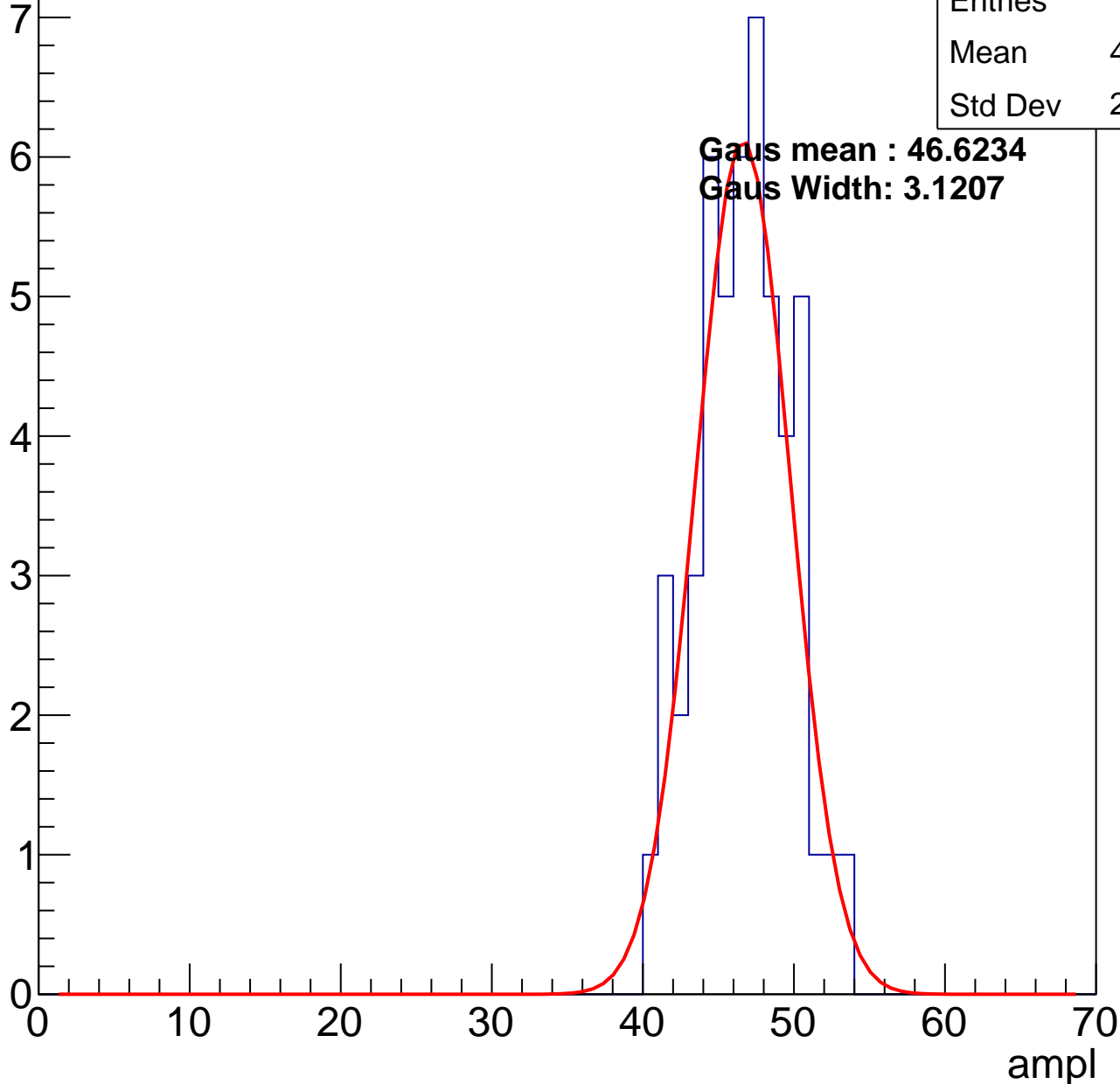
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	46.24
Std Dev	2.997

Gaus mean : 46.6234

Gaus Width: 3.1207

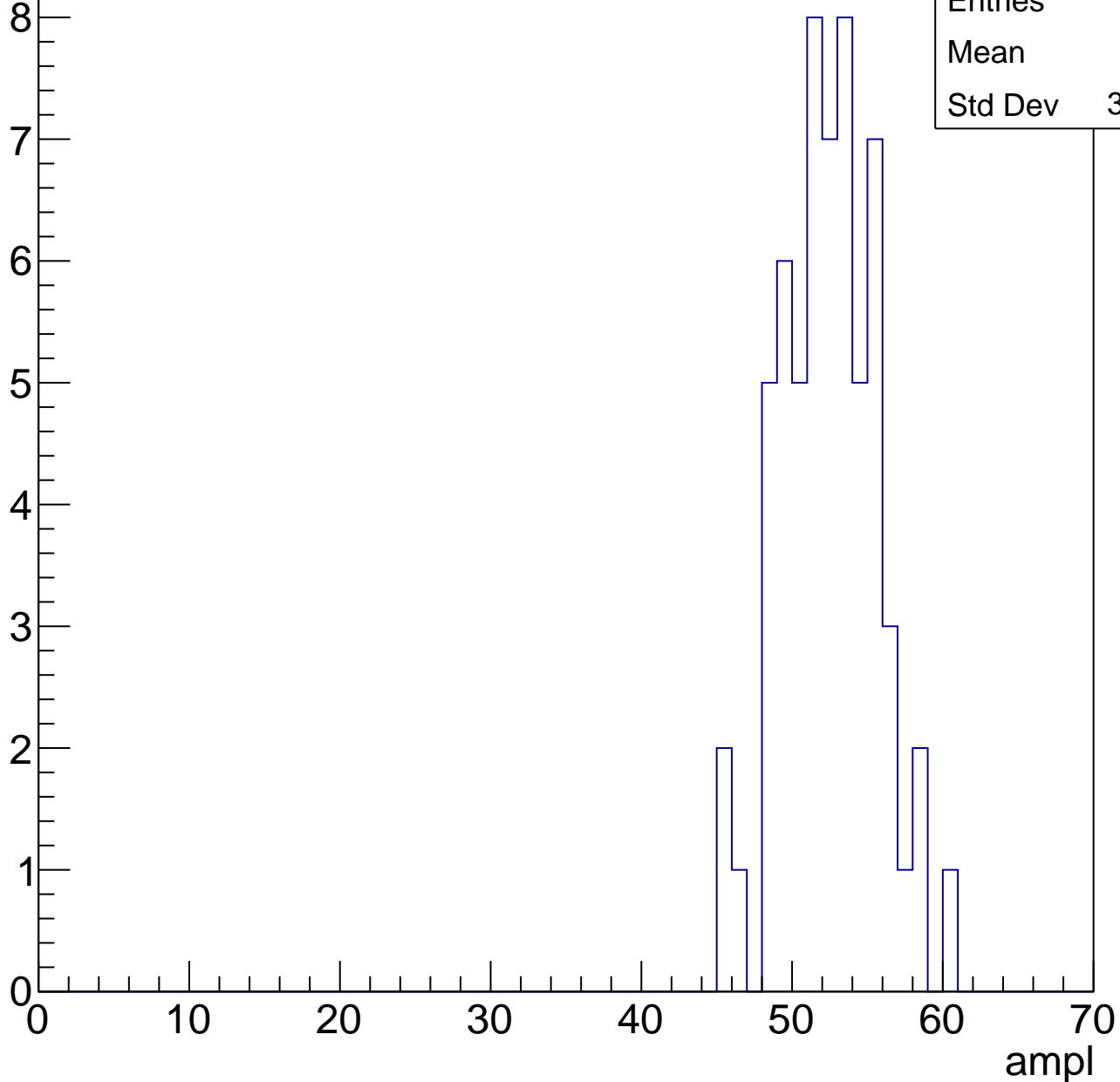


B0L002S, U12-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

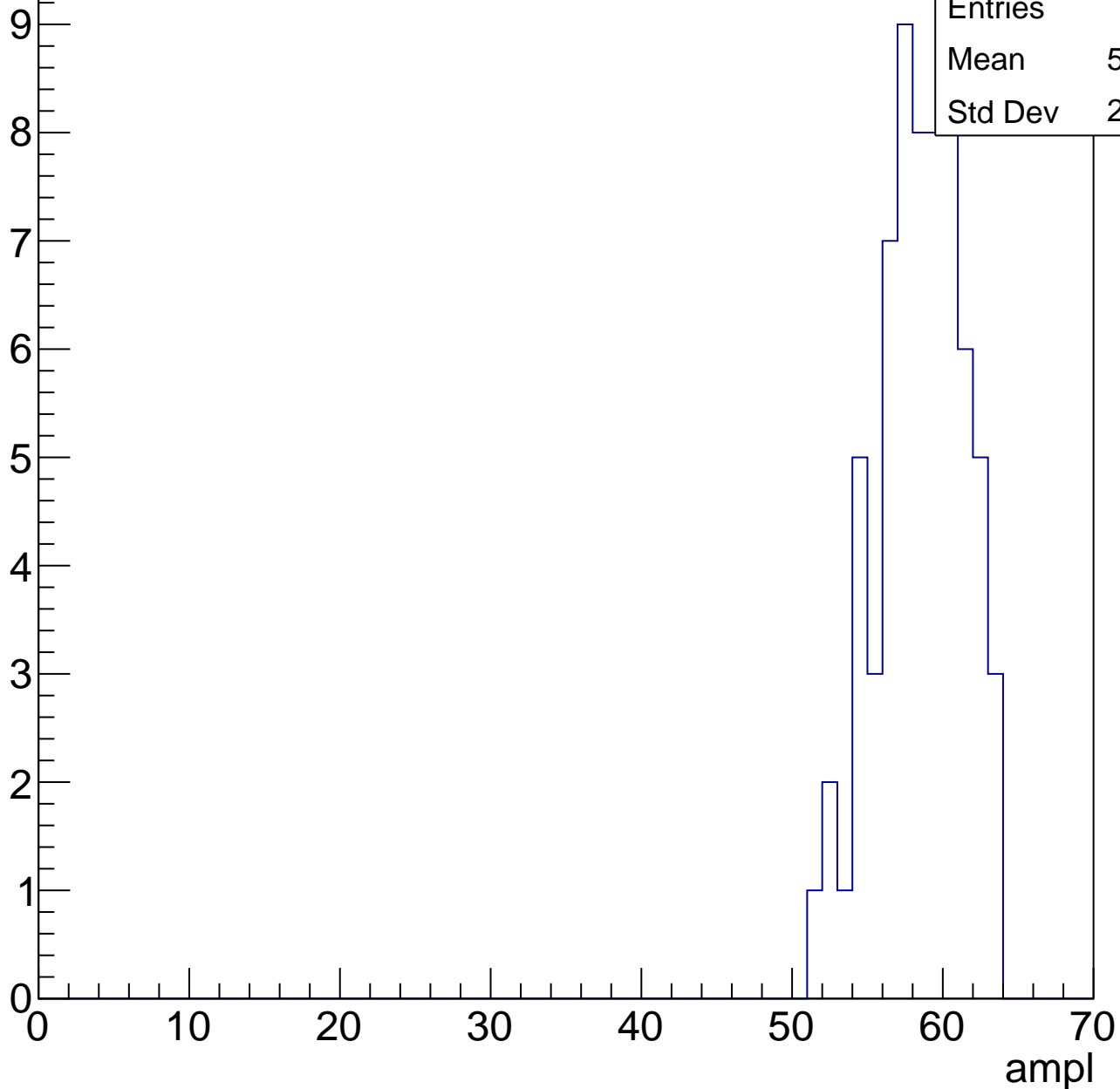
Entries	61
Mean	52
Std Dev	3.126



B0L002S, U12-ch117, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

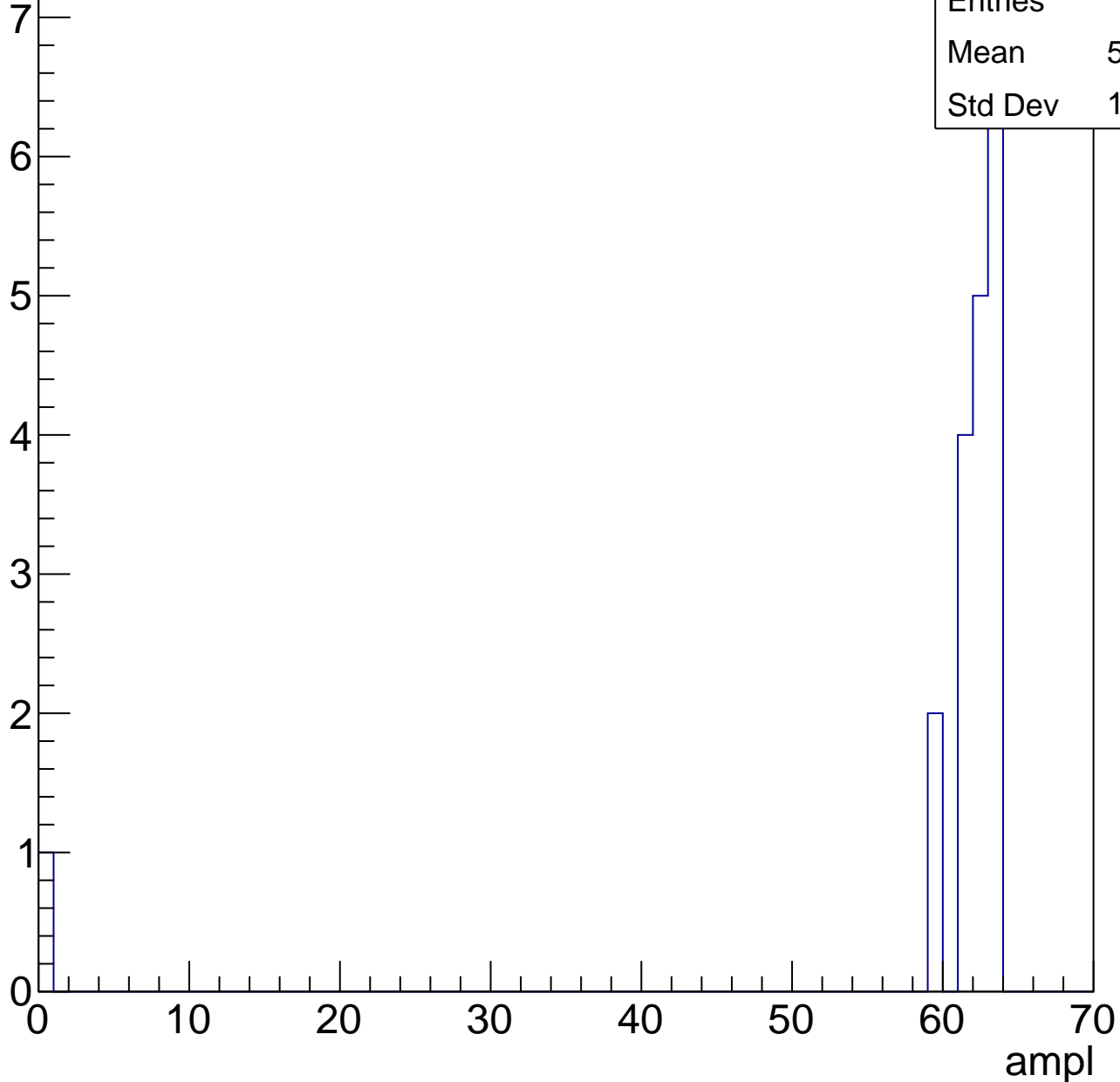


B0L002S, U12-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	58.58
Std Dev	13.86



B0L002S, U12-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch118, adc0

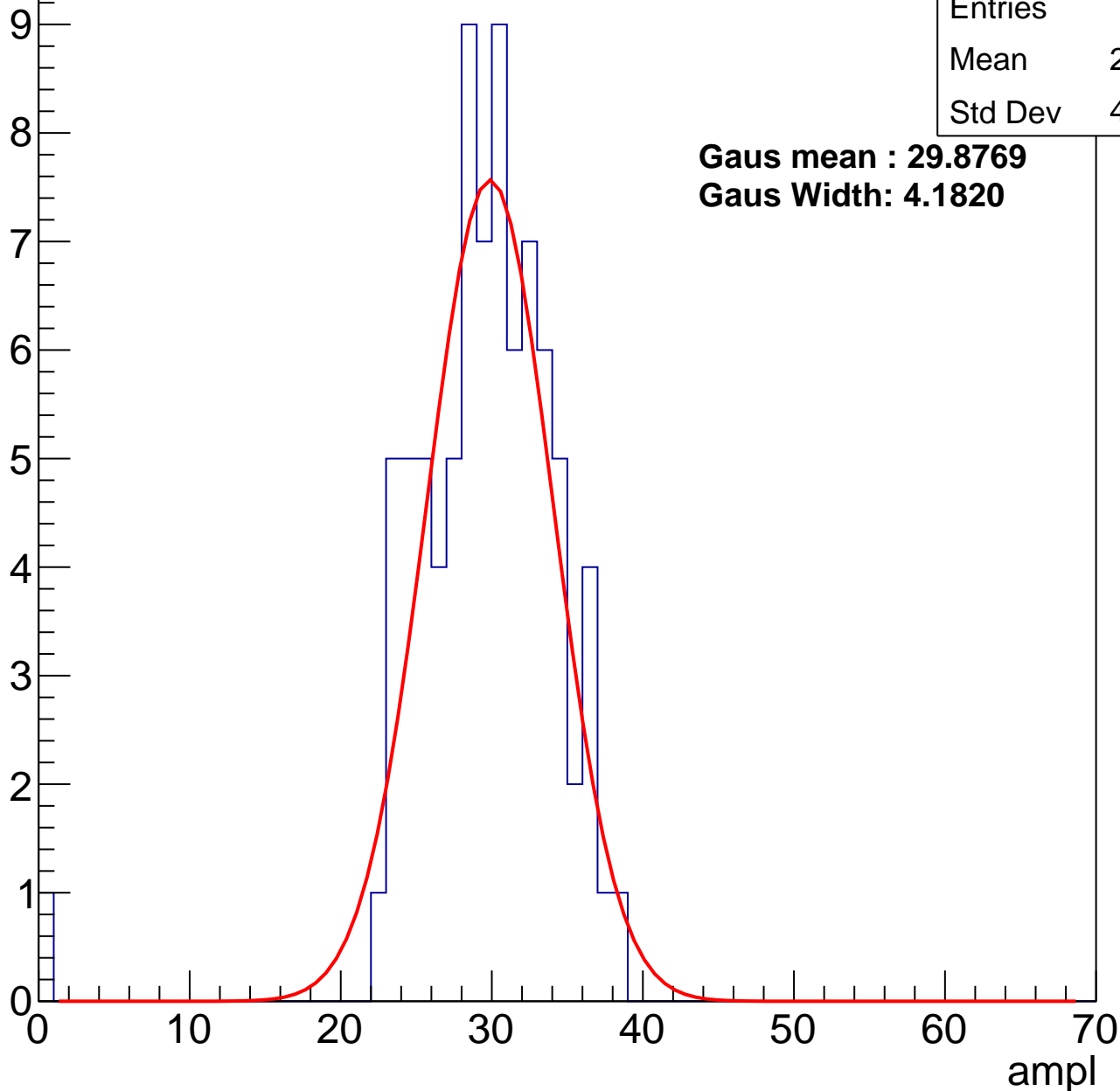
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	29.07
Std Dev	4.993

Gaus mean : 29.8769

Gaus Width: 4.1820



B0L002S, U12-ch118, adc1

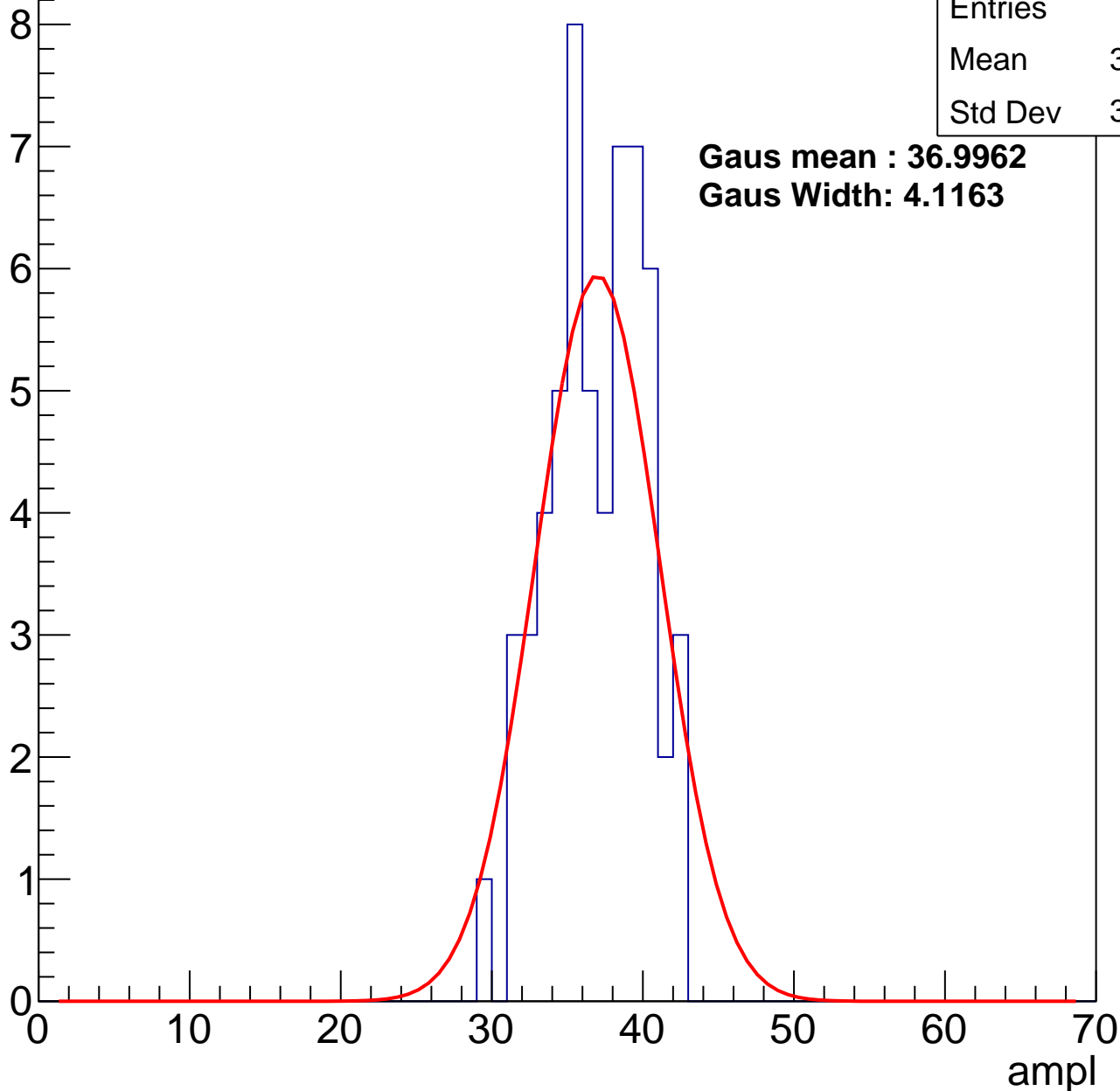
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	36.47
Std Dev	3.142

Gaus mean : 36.9962

Gaus Width: 4.1163



B0L002S, U12-ch118, adc2

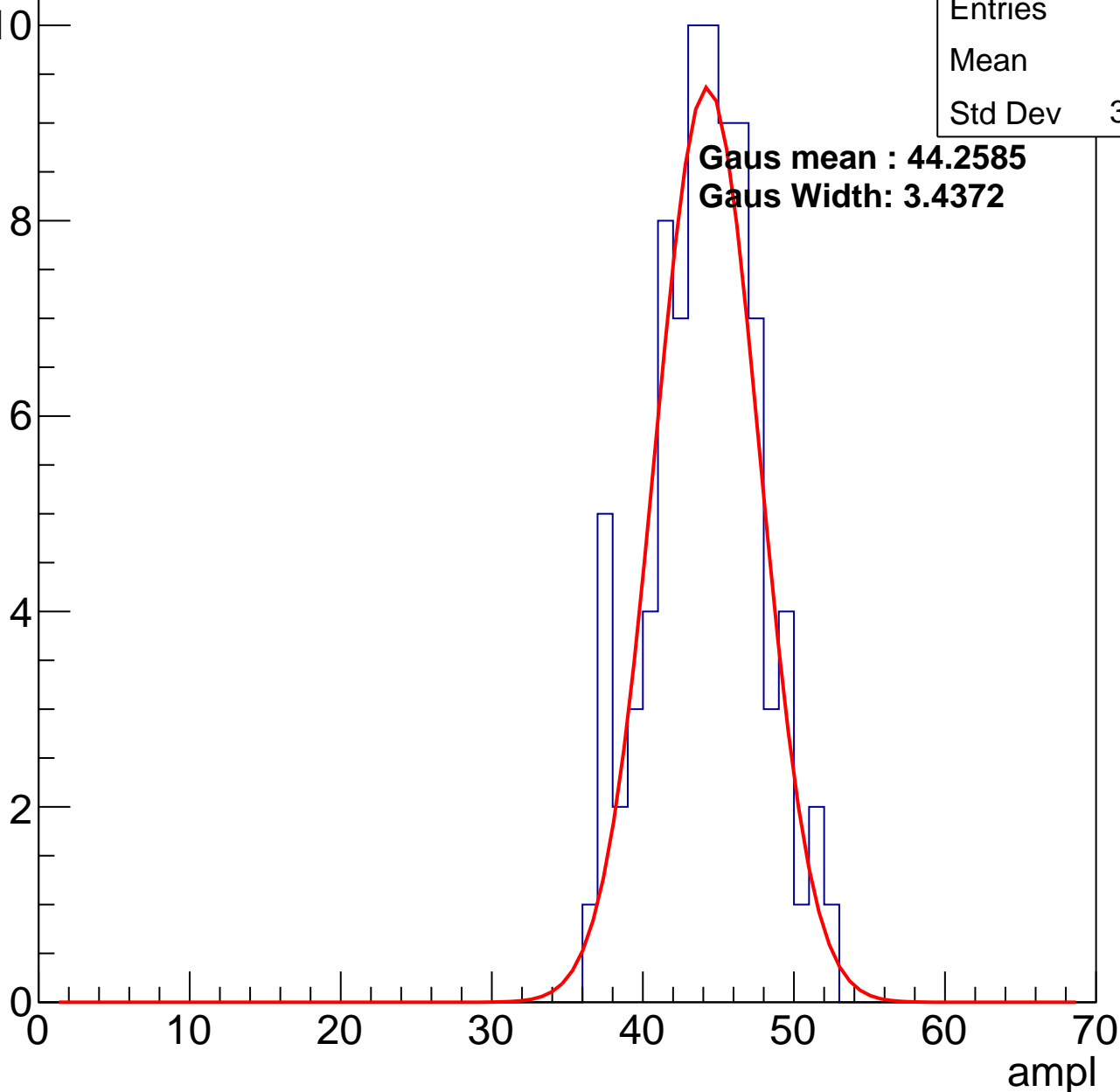
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	43.7
Std Dev	3.537

Gaus mean : 44.2585

Gaus Width: 3.4372

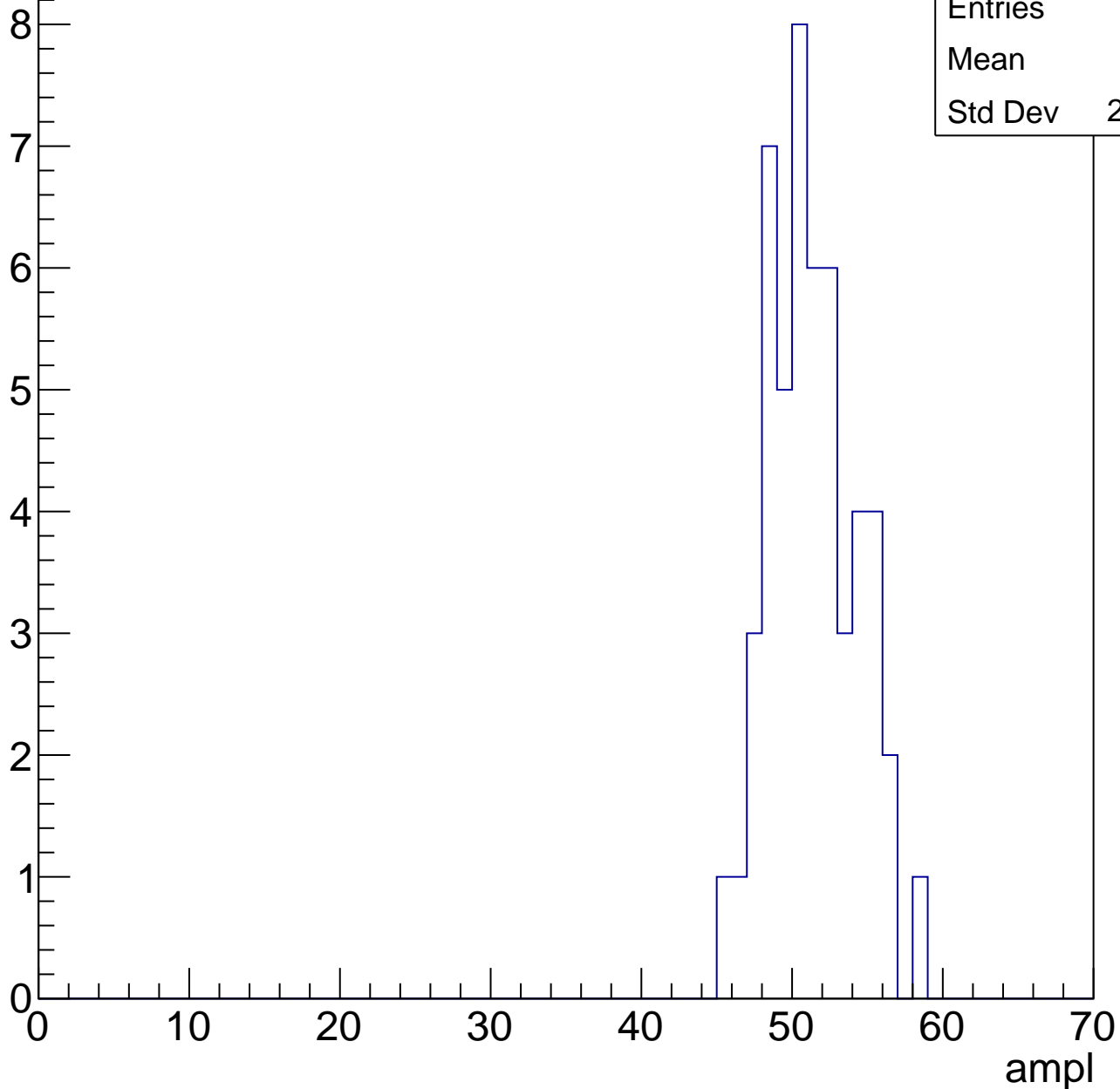


B0L002S, U12-ch118, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	50.9
Std Dev	2.858



B0L002S, U12-ch118, adc4

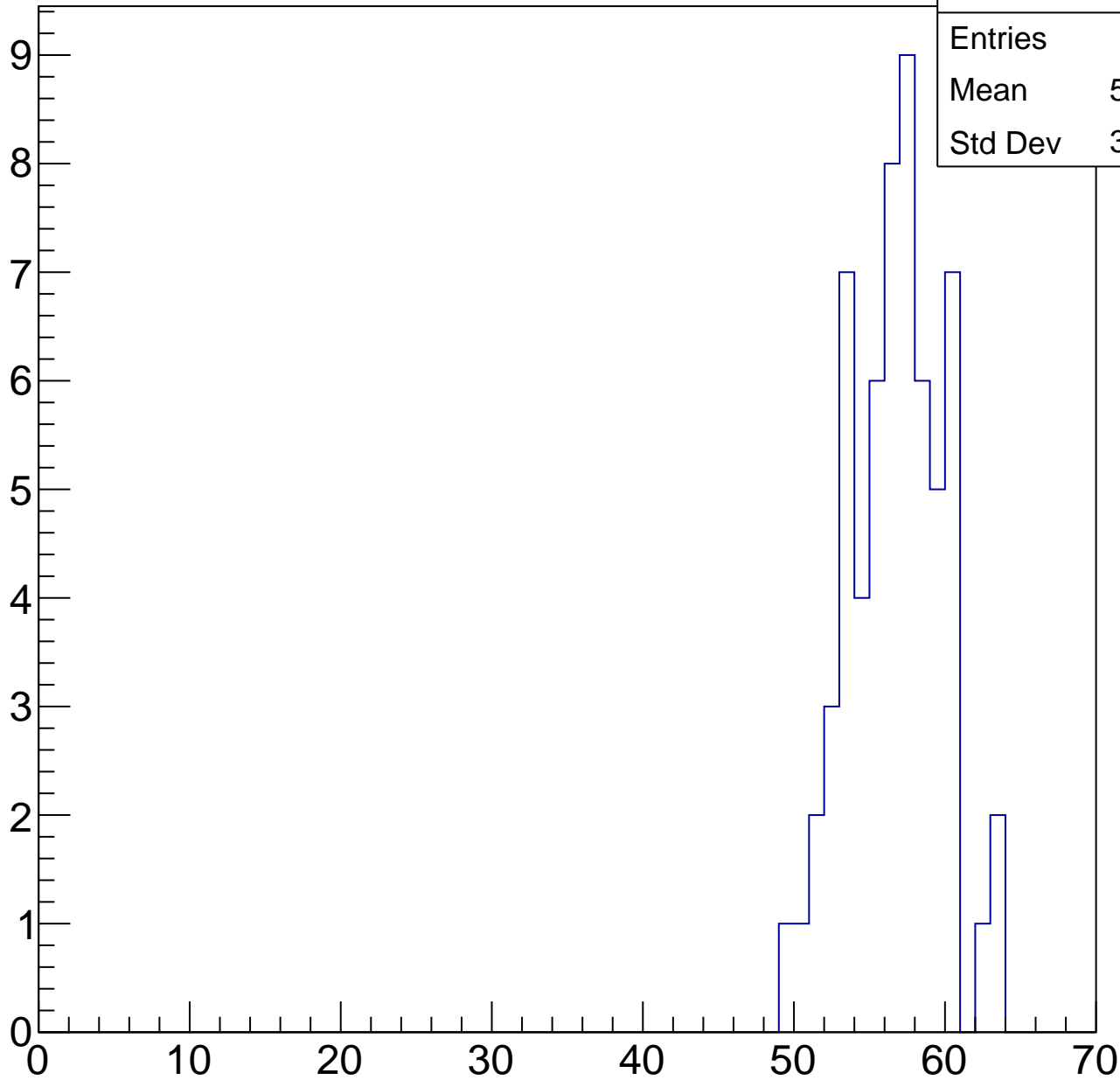
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	56.23
Std Dev	3.082

ampl

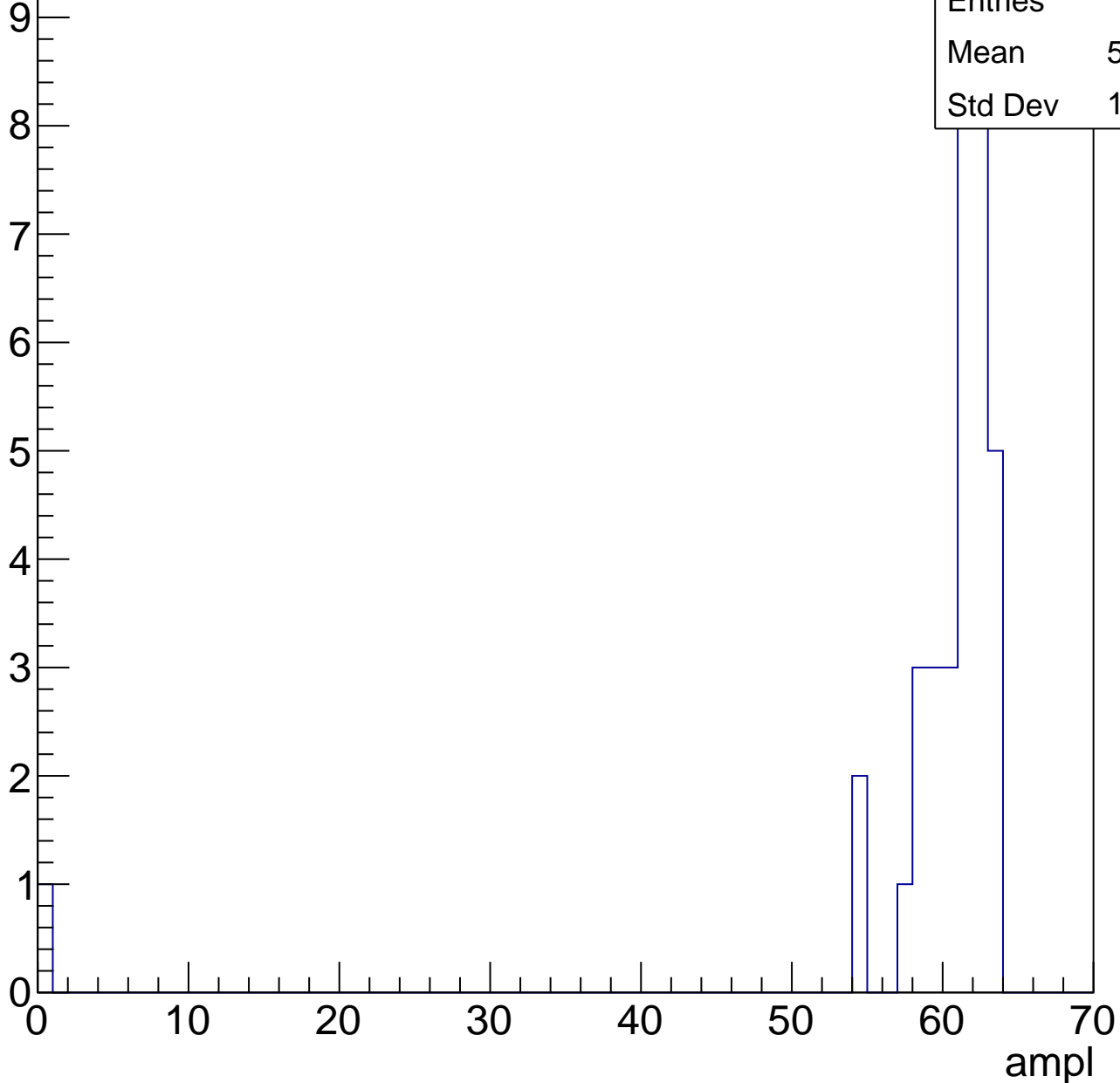


B0L002S, U12-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

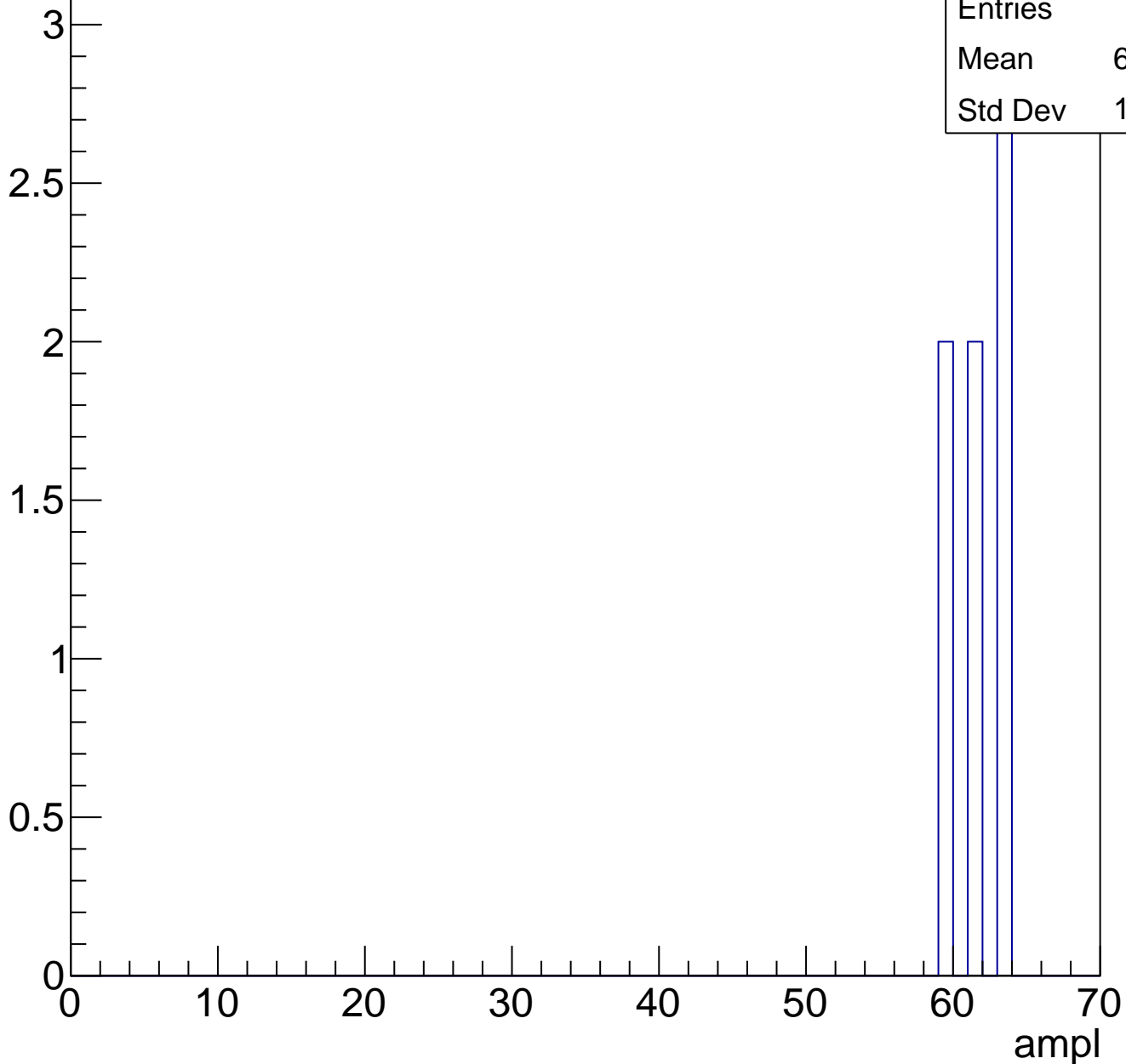
Entries	35
Mean	58.77
Std Dev	10.33



B0L002S, U12-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U12-ch119, adc0

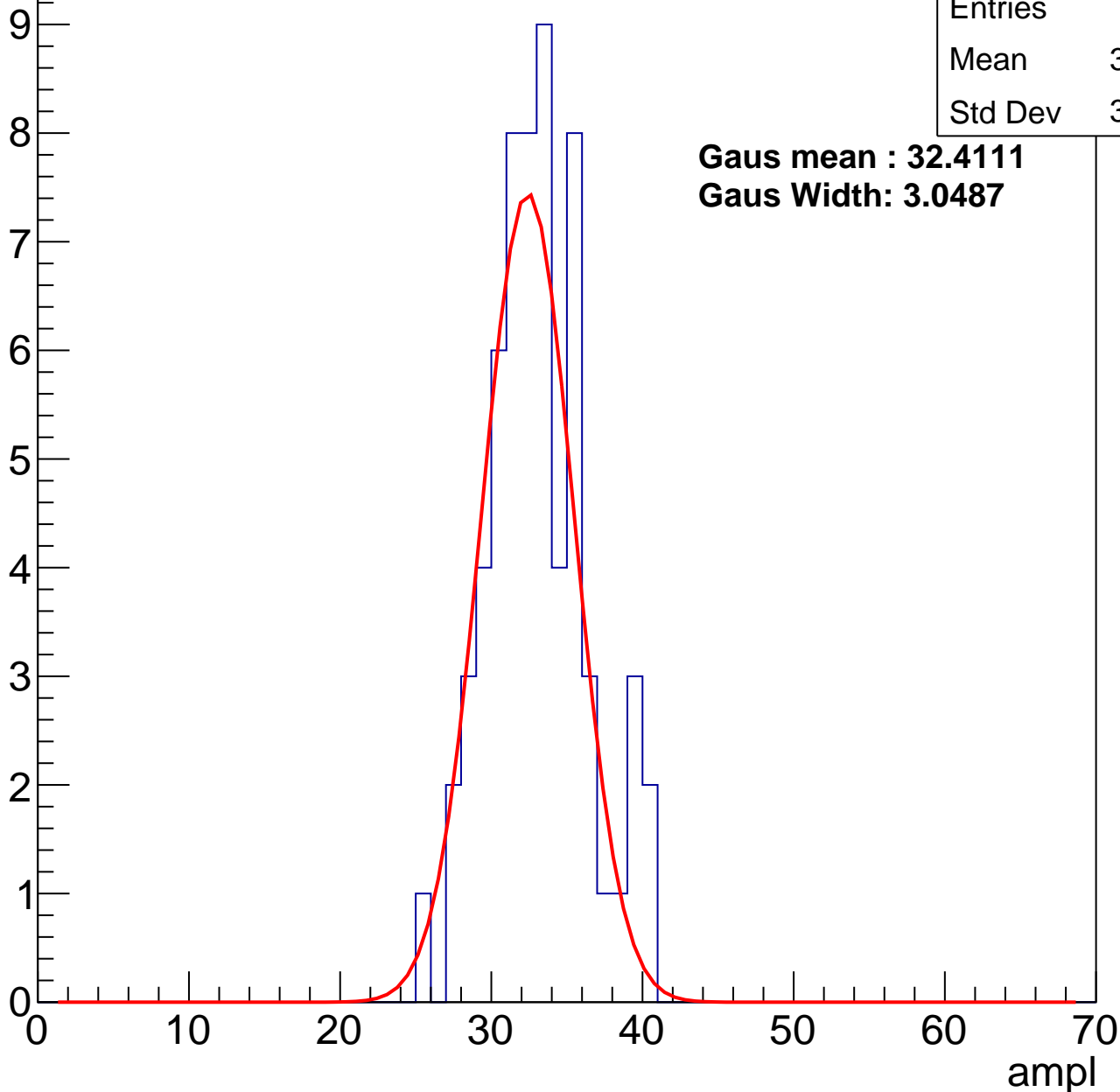
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	32.63
Std Dev	3.263

Gaus mean : 32.4111

Gaus Width: 3.0487



B0L002S, U12-ch119, adc1

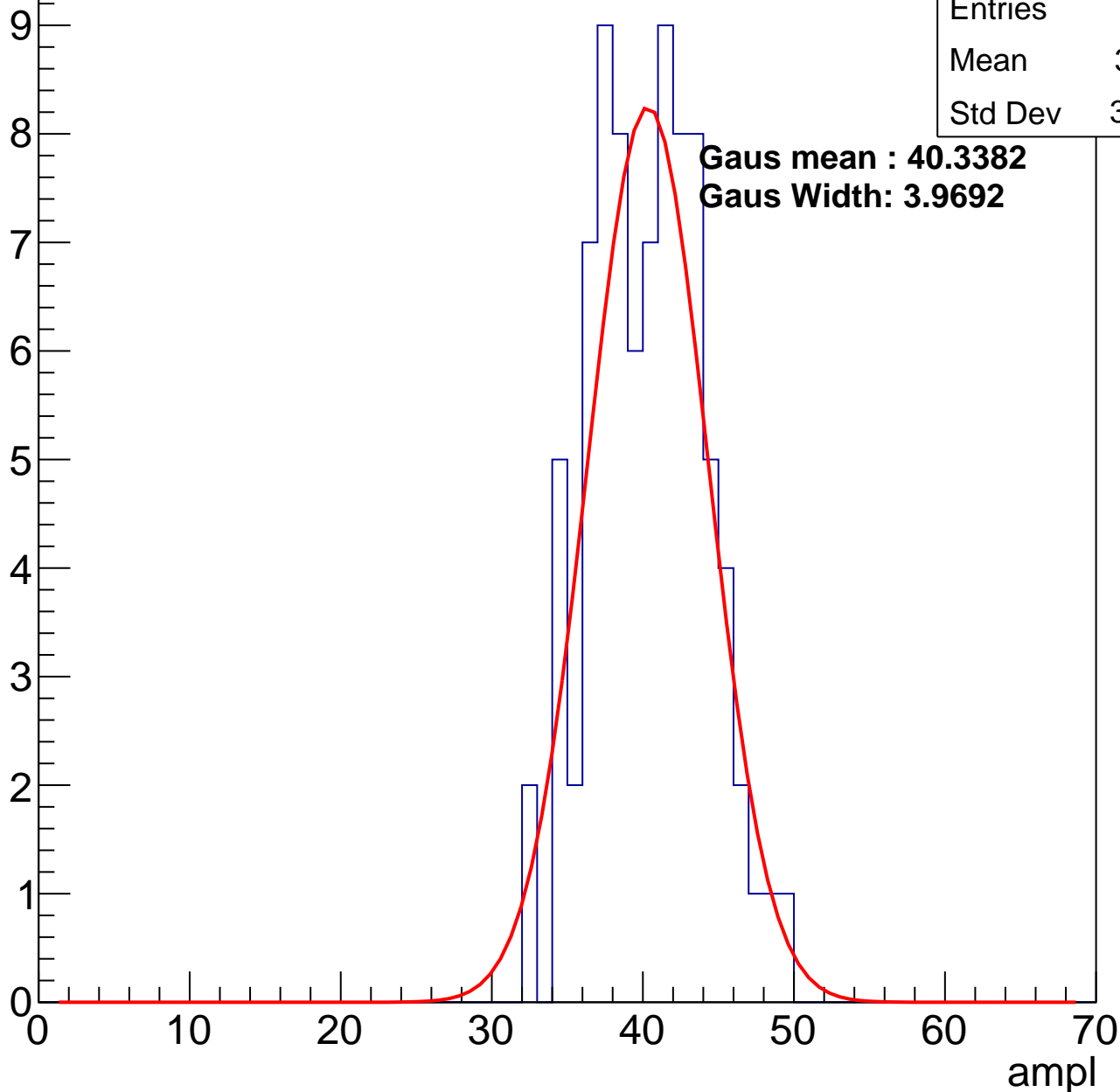
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	39.91
Std Dev	3.674

Gaus mean : 40.3382

Gaus Width: 3.9692



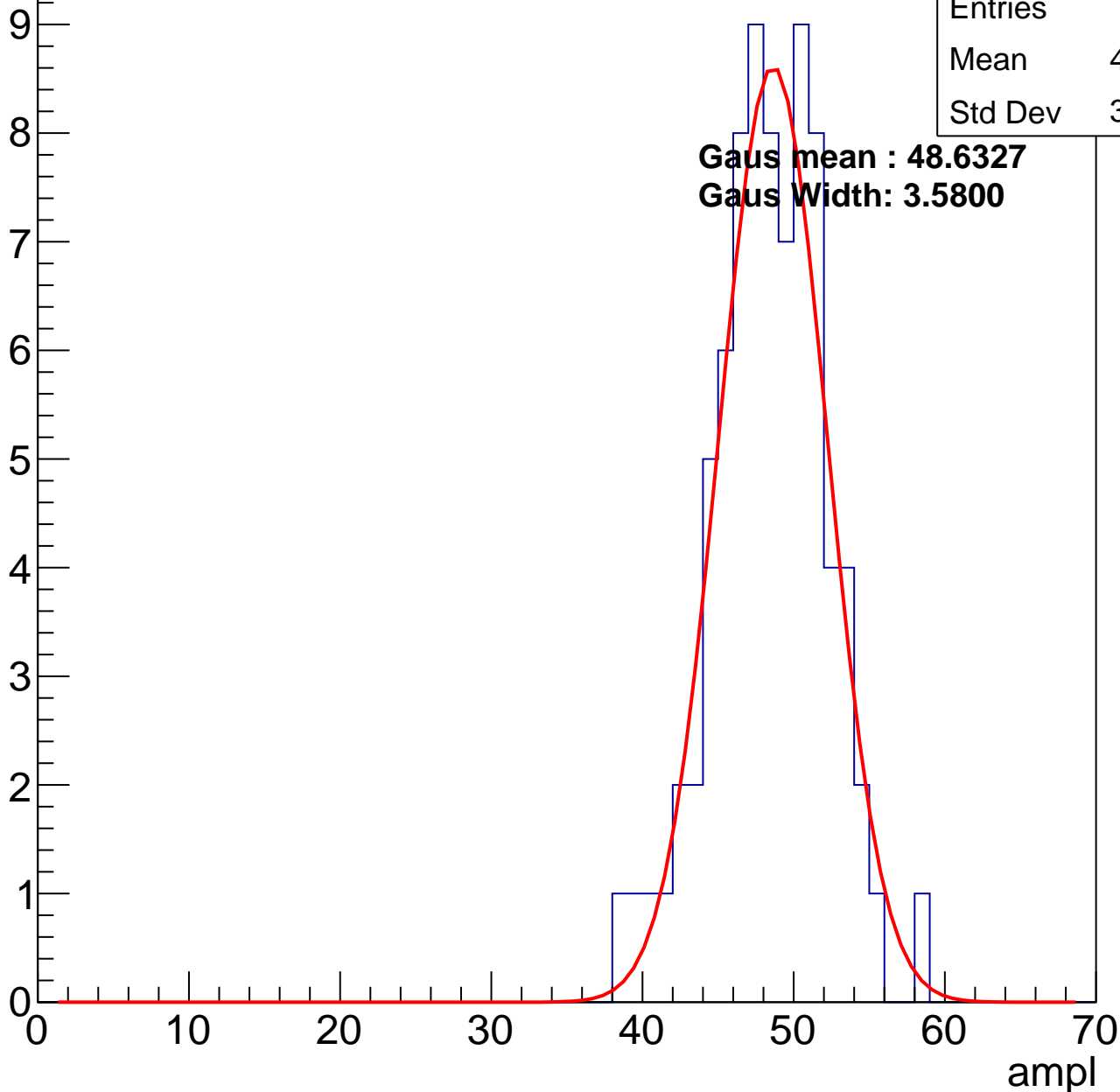
B0L002S, U12-ch119, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	47.94
Std Dev	3.692

Gaus mean : 48.6327
Gaus Width: 3.5800

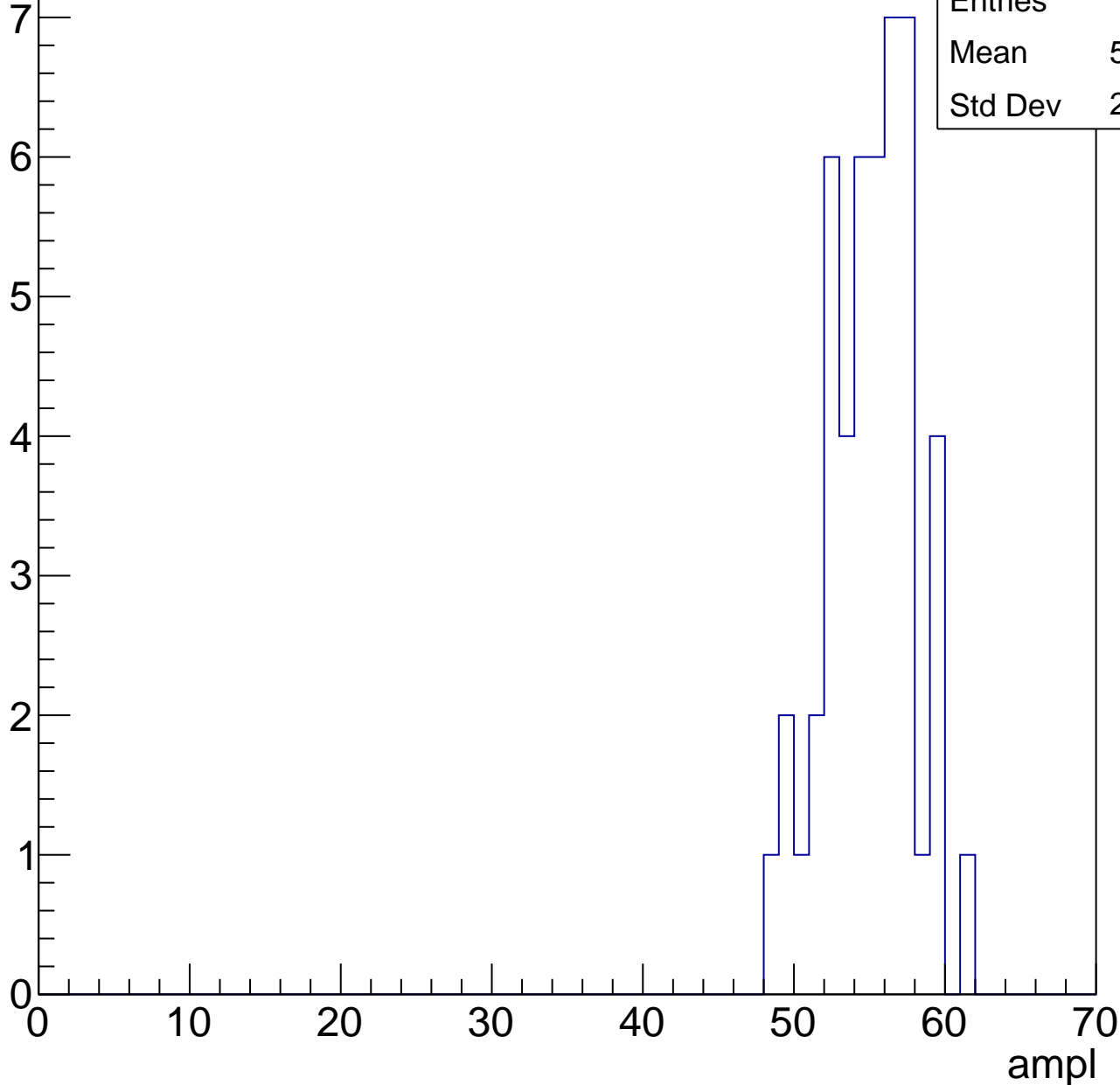


B0L002S, U12-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	54.62
Std Dev	2.855



B0L002S, U12-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	56
Mean	59.23
Std Dev	2.706

Entry

10

8

6

4

2

0

0

10

20

30

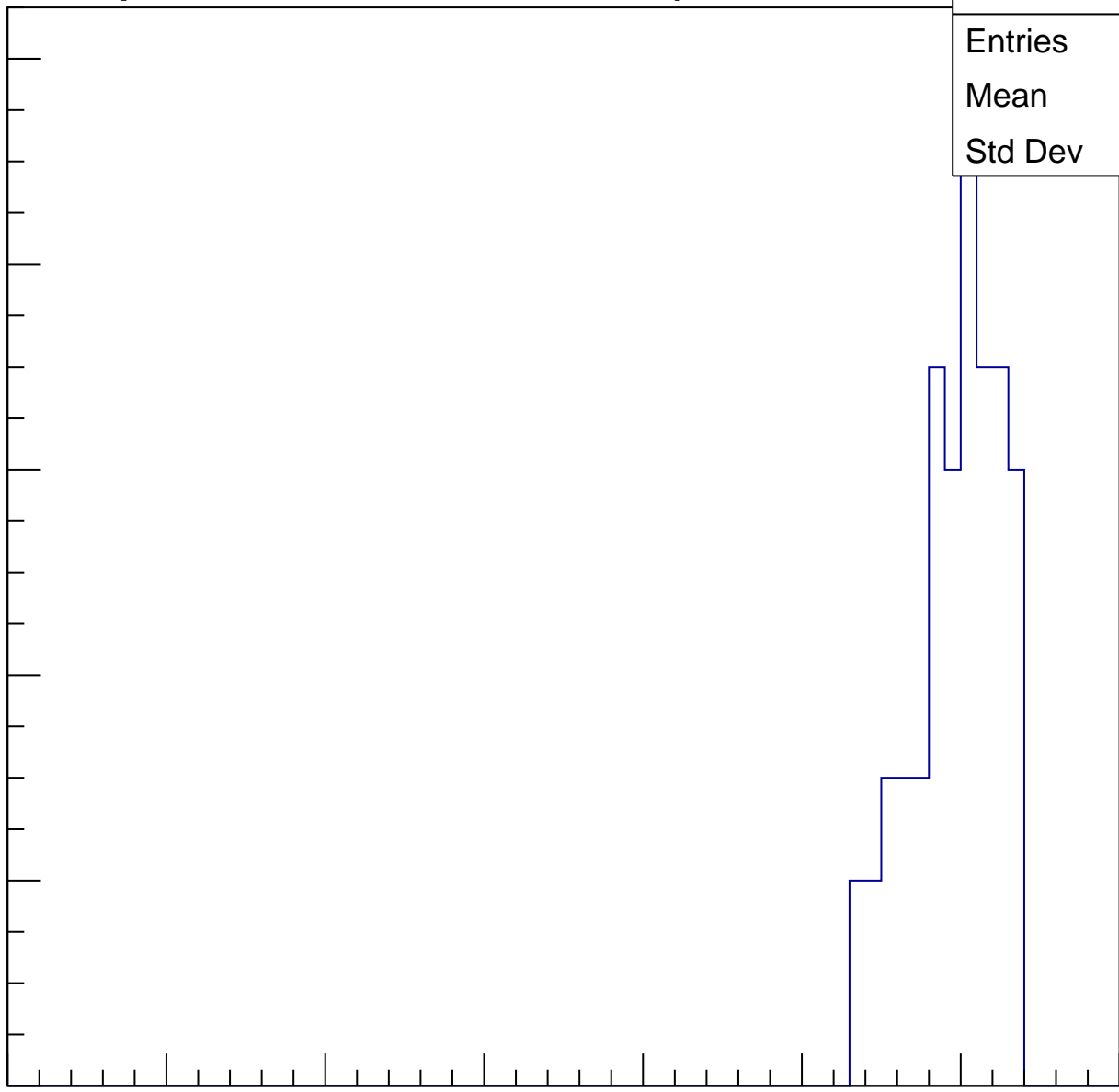
40

50

60

70

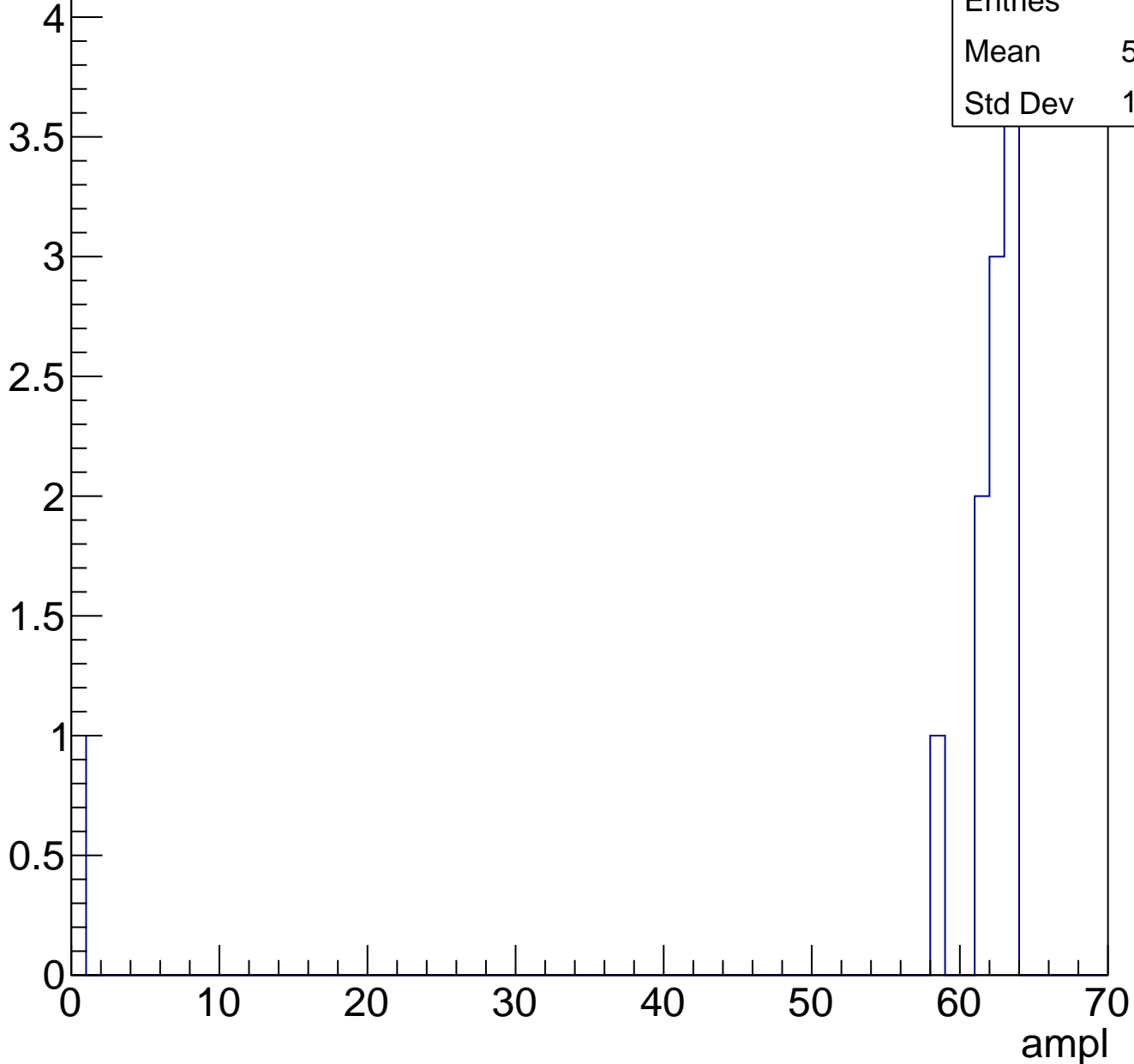
ampl



B0L002S, U12-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

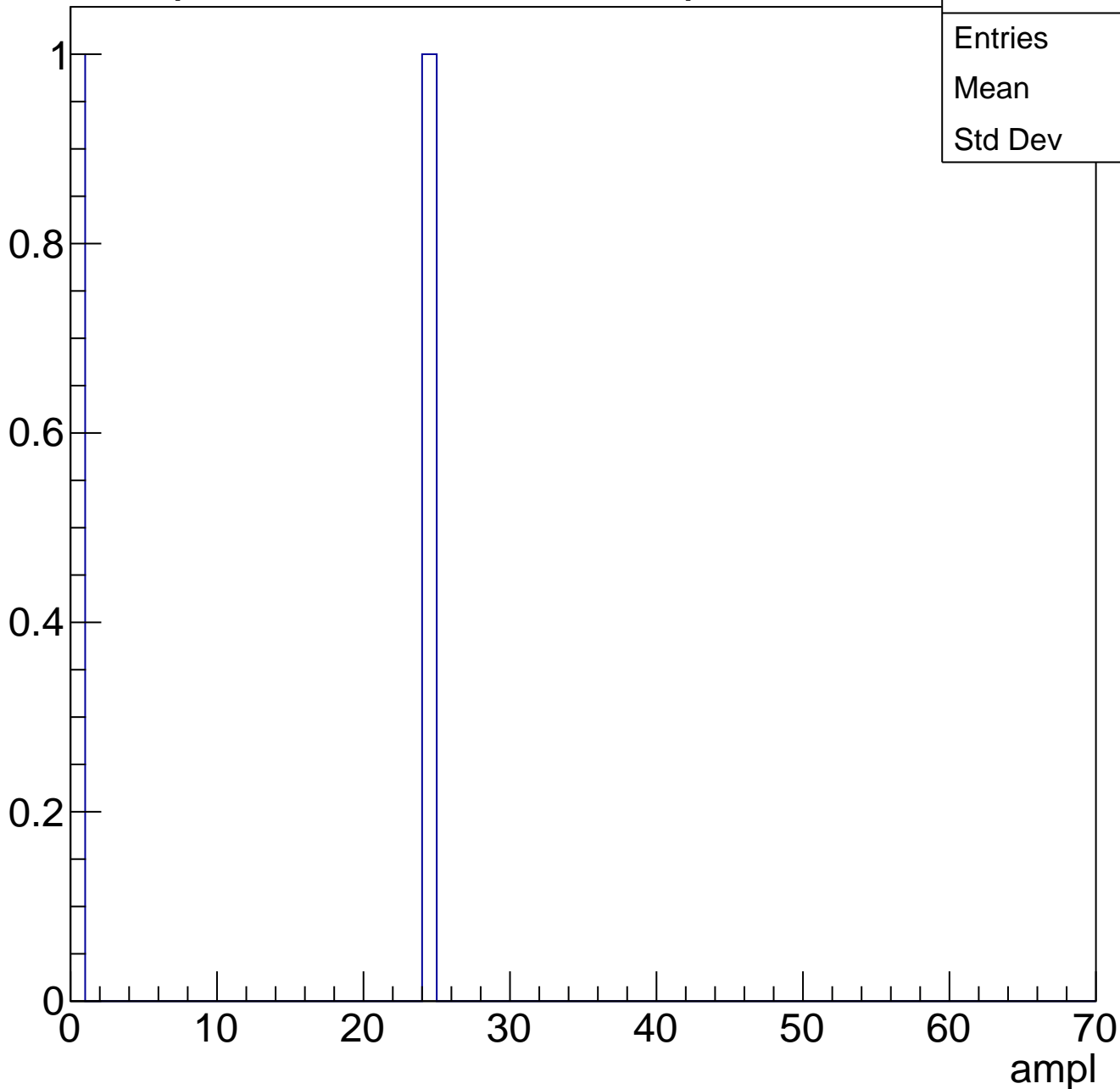


Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch120, adc0

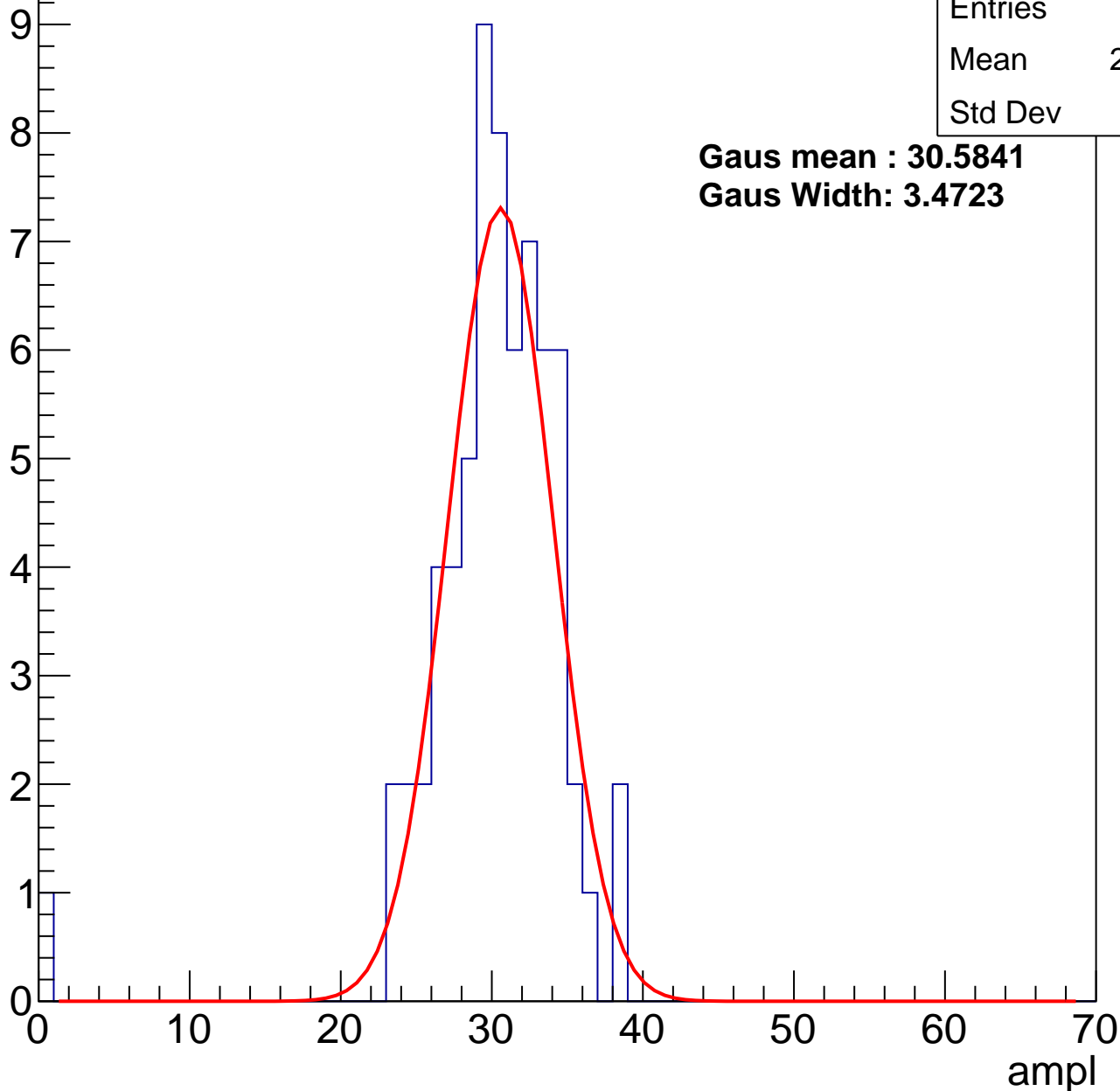
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.72
Std Dev	4.95

Gaus mean : 30.5841

Gaus Width: 3.4723



B0L002S, U12-ch120, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	36.55
Std Dev	3.396

Gaus mean : 36.7159

Gaus Width: 3.0639

Entry

10

8

6

4

2

0

0

10

20

30

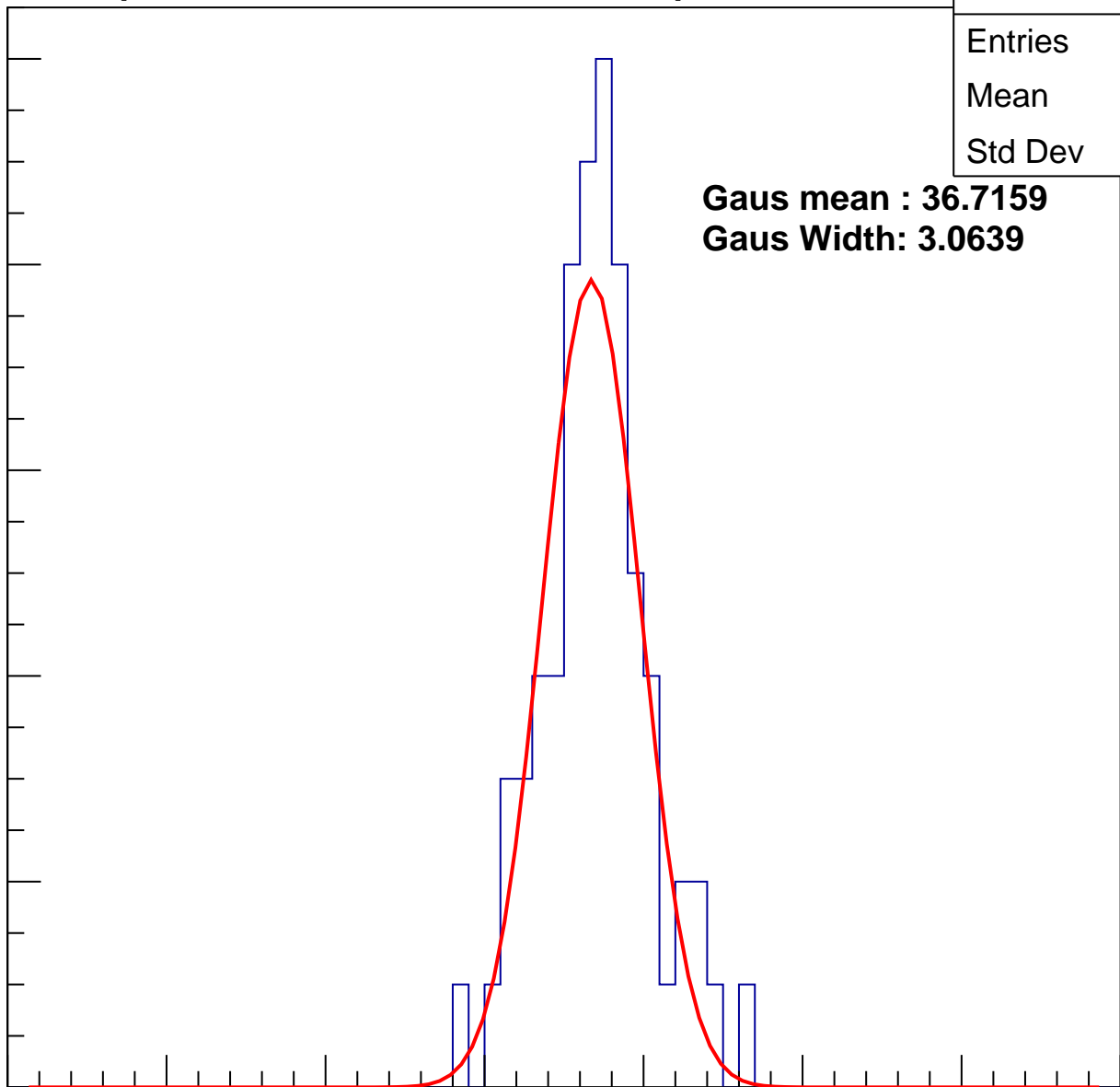
40

50

60

70

ampl



B0L002S, U12-ch120, adc2

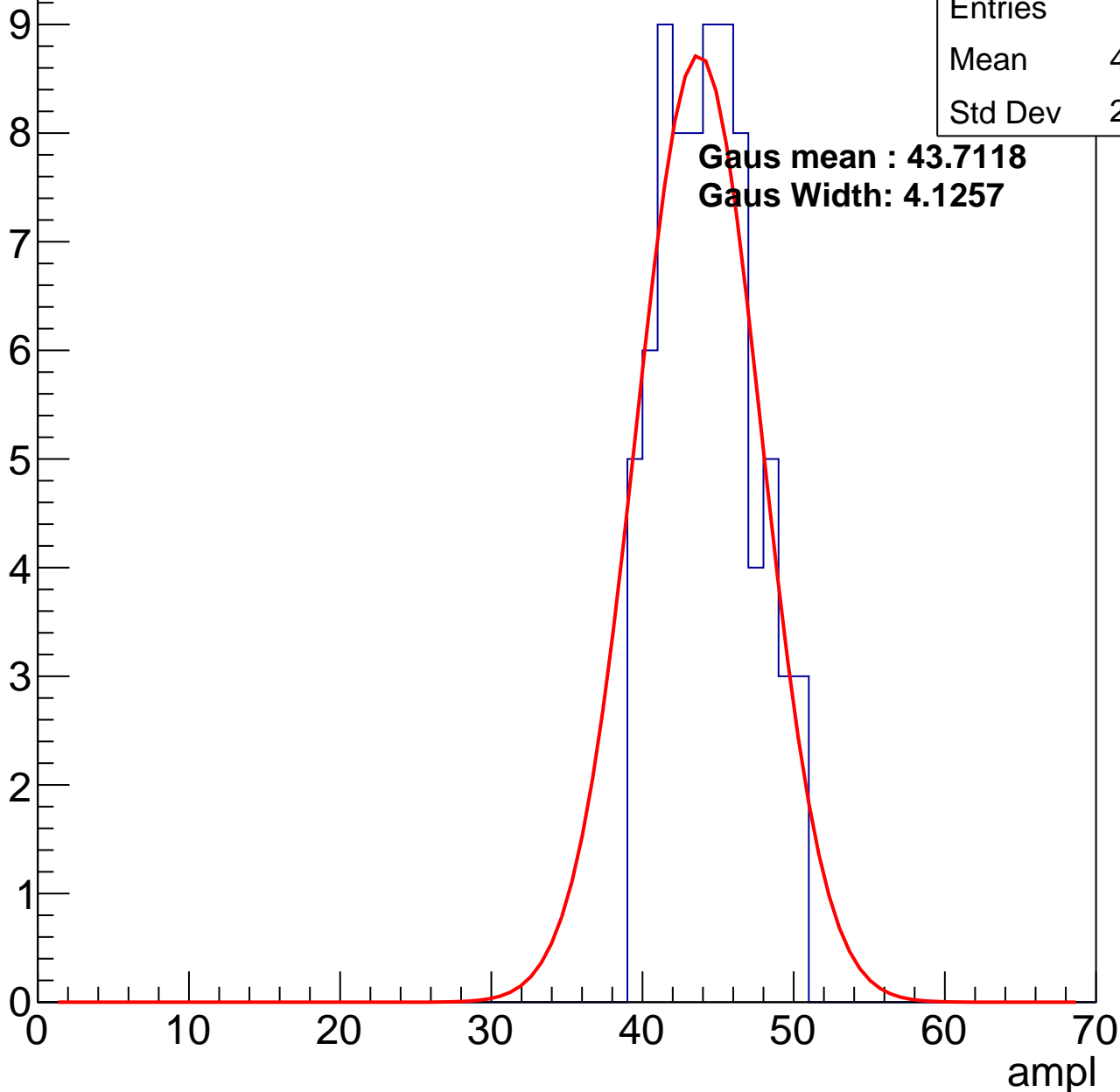
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	43.87
Std Dev	2.973

Gaus mean : 43.7118

Gaus Width: 4.1257

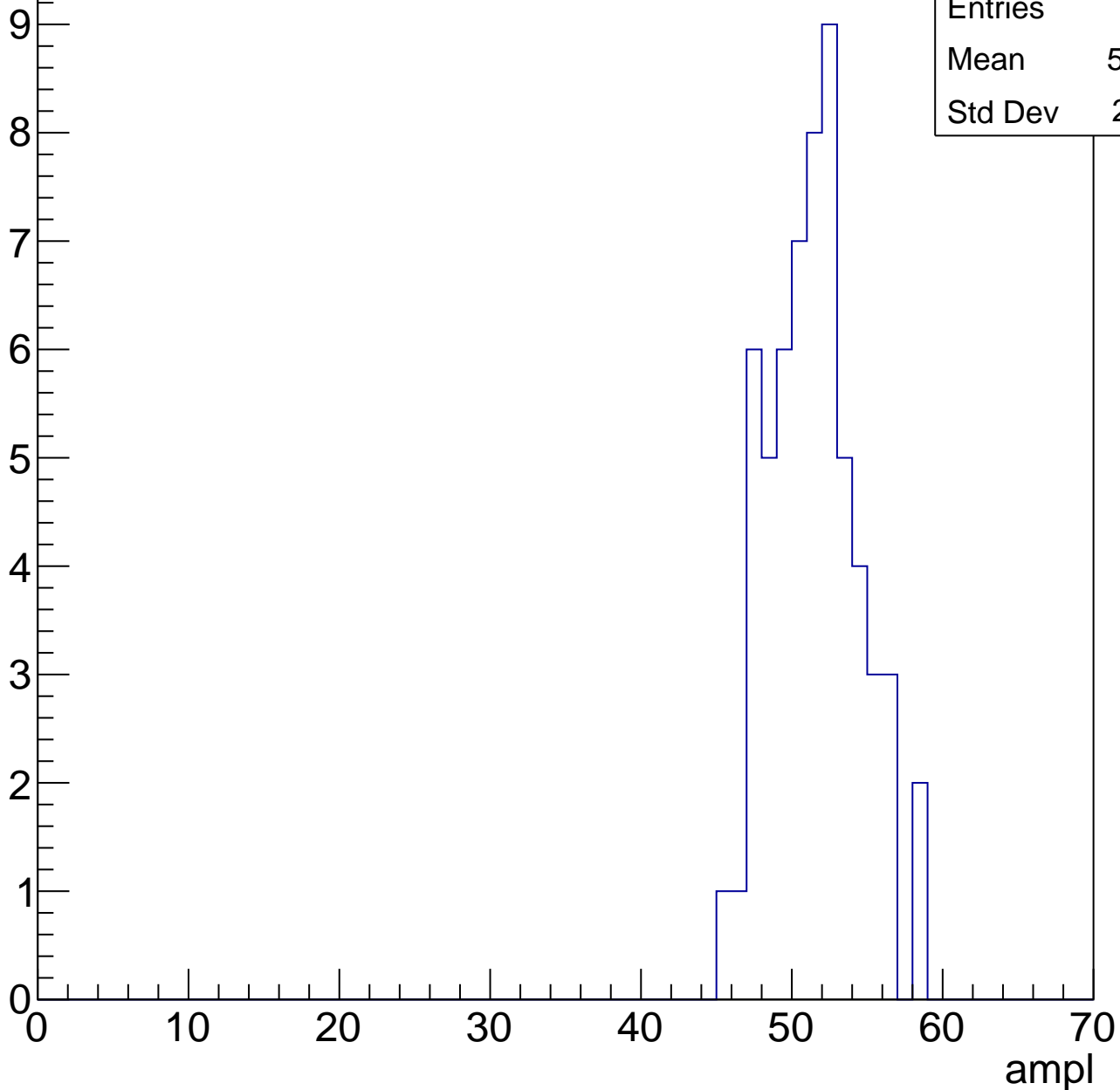


B0L002S, U12-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

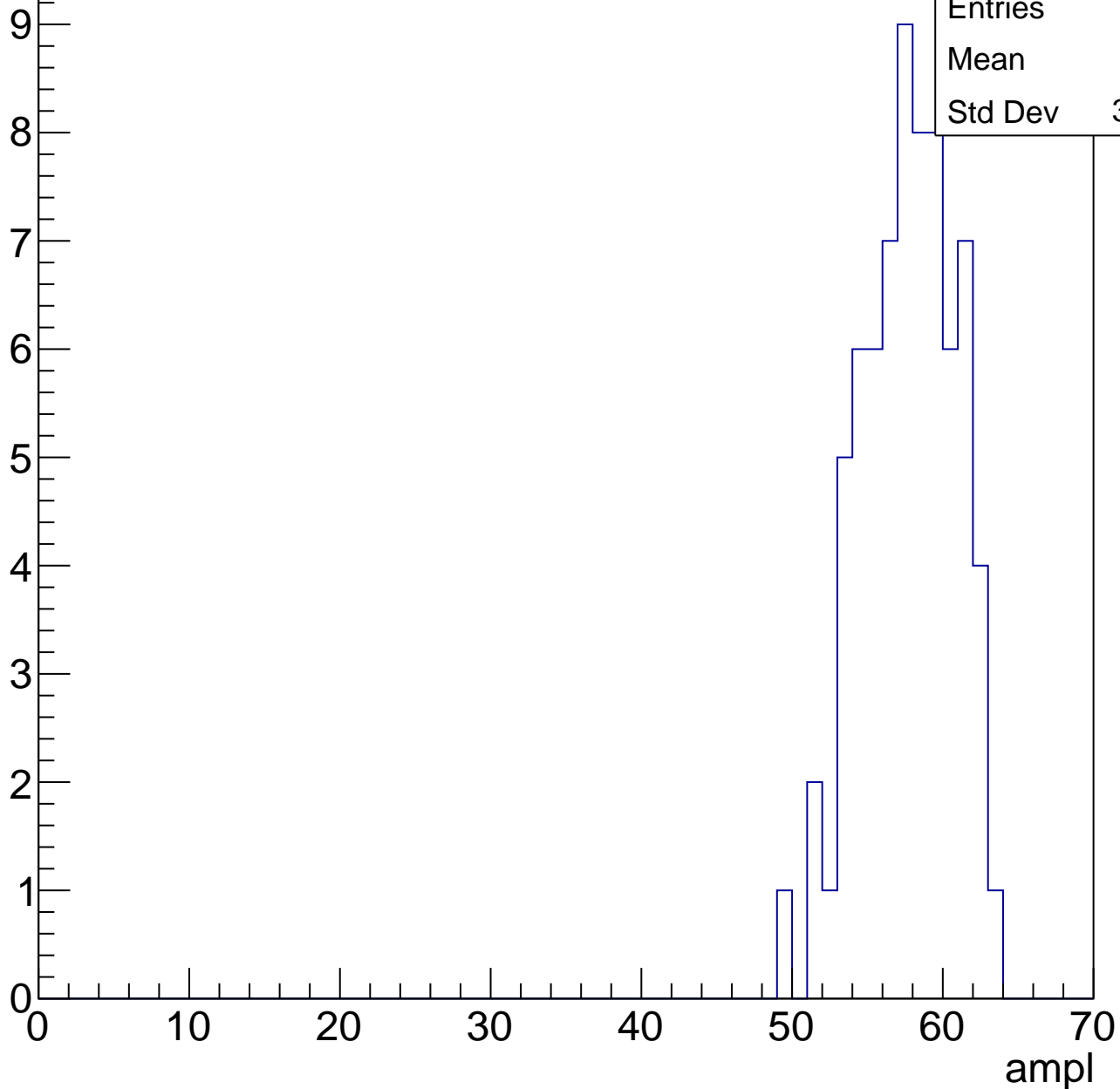
Entries	60
Mean	51.05
Std Dev	2.941



B0L002S, U12-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



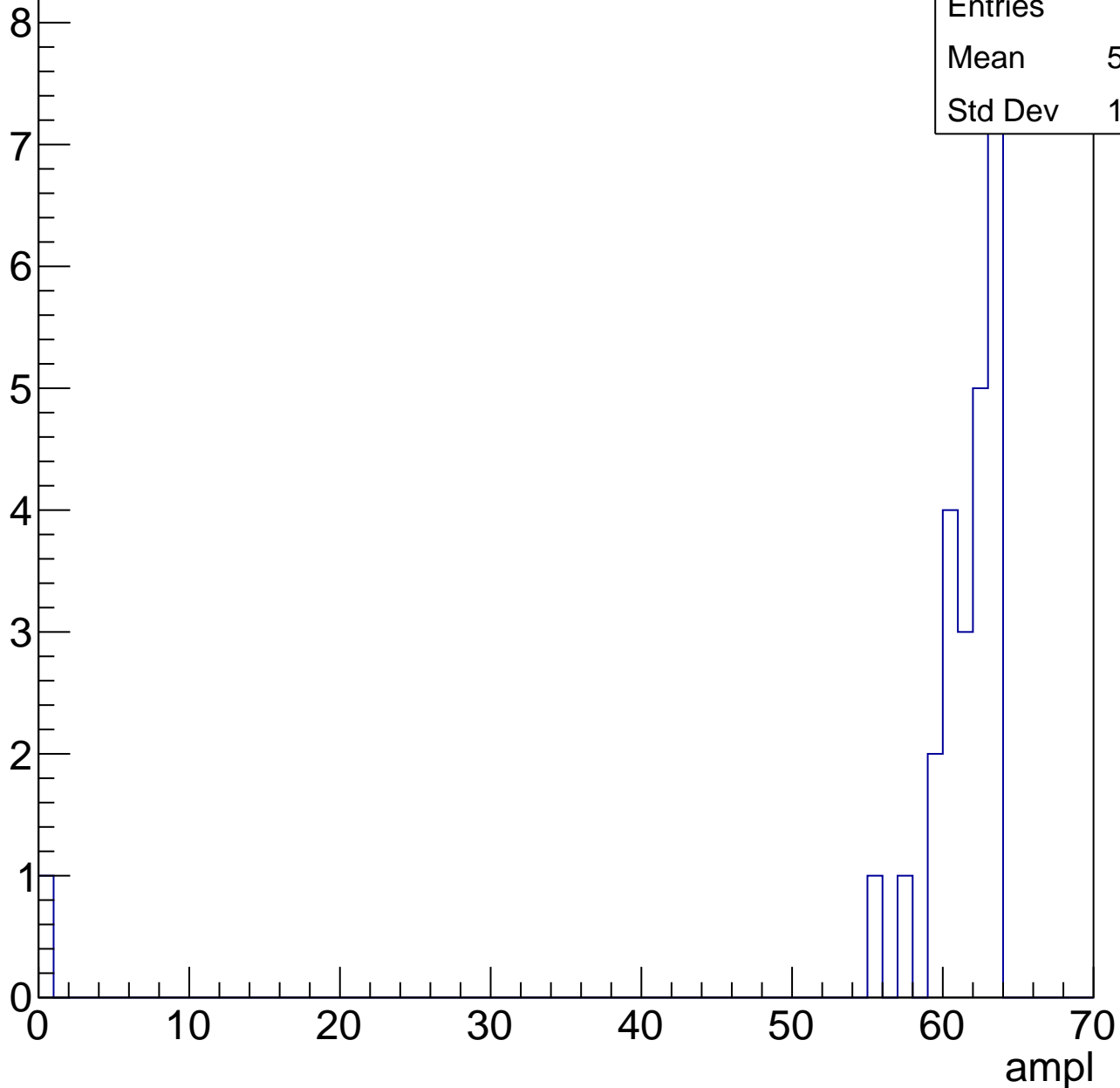
Entries	71
Mean	57.2
Std Dev	3.061

B0L002S, U12-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.68
Std Dev	12.14



B0L002S, U12-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L002S, U12-ch121, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	30.82
Std Dev	5.89

Gaus mean : 31.9657

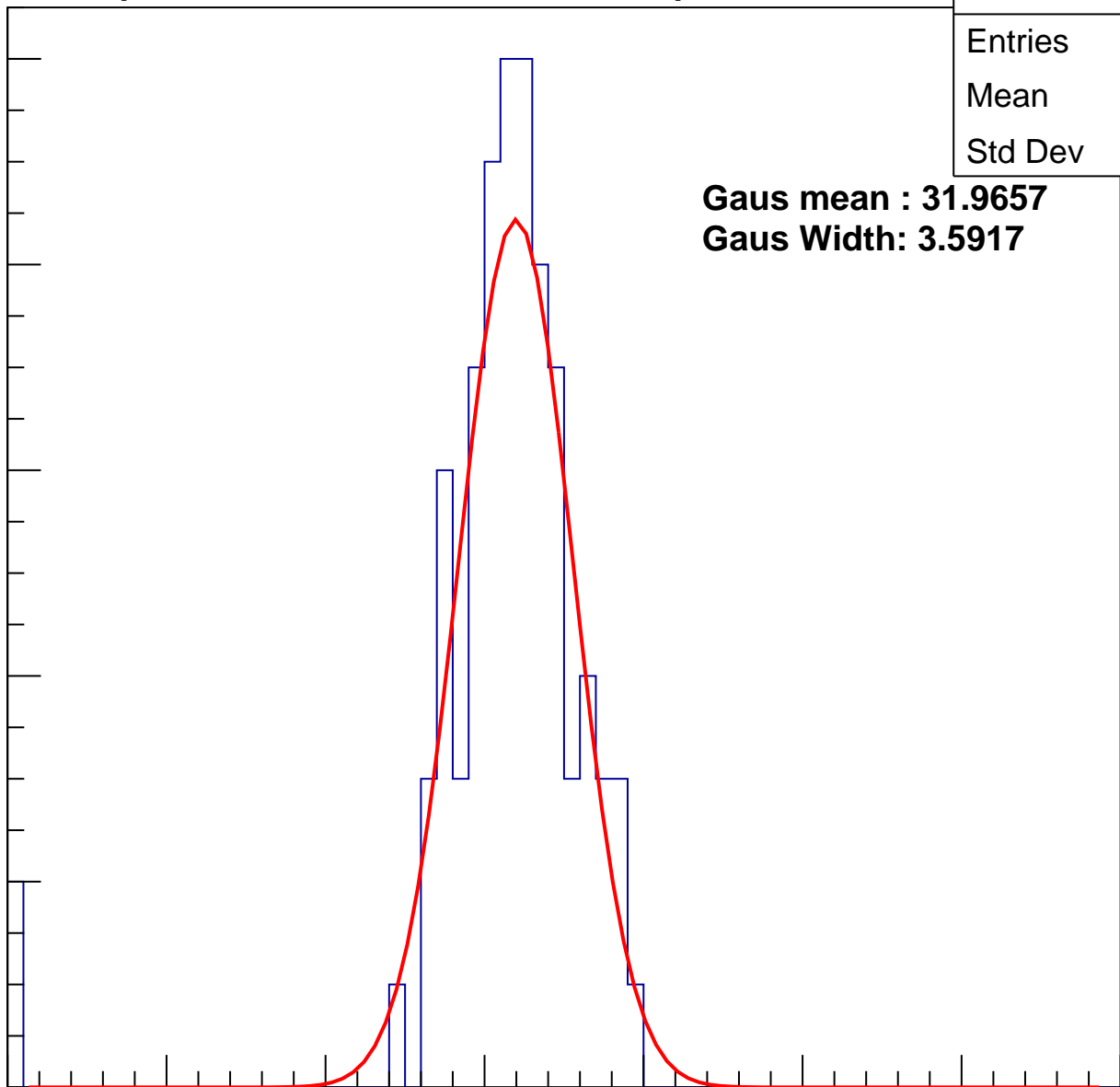
Gaus Width: 3.5917

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch121, adc1

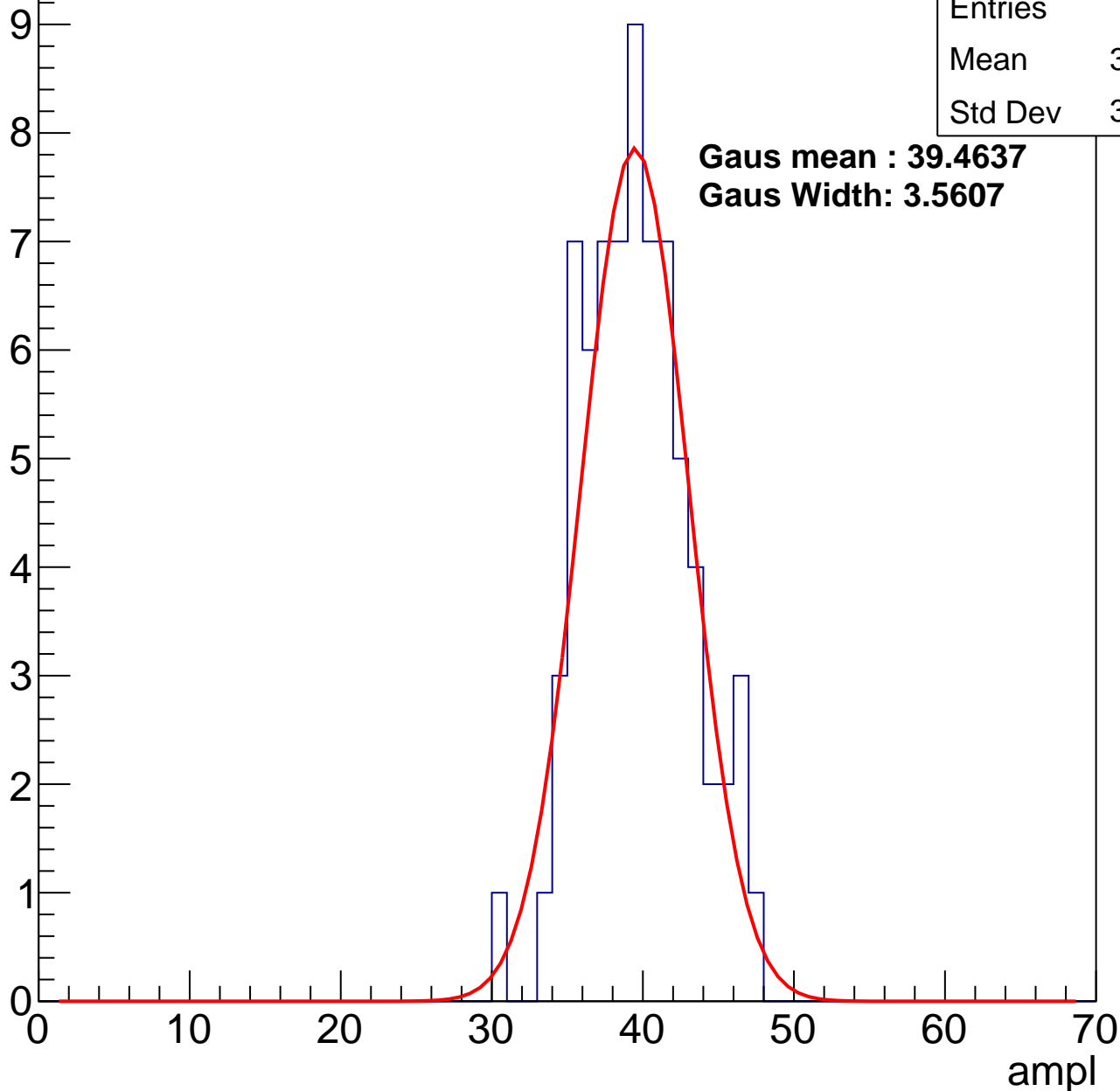
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	39.08
Std Dev	3.475

Gaus mean : 39.4637

Gaus Width: 3.5607



B0L002S, U12-ch121, adc2

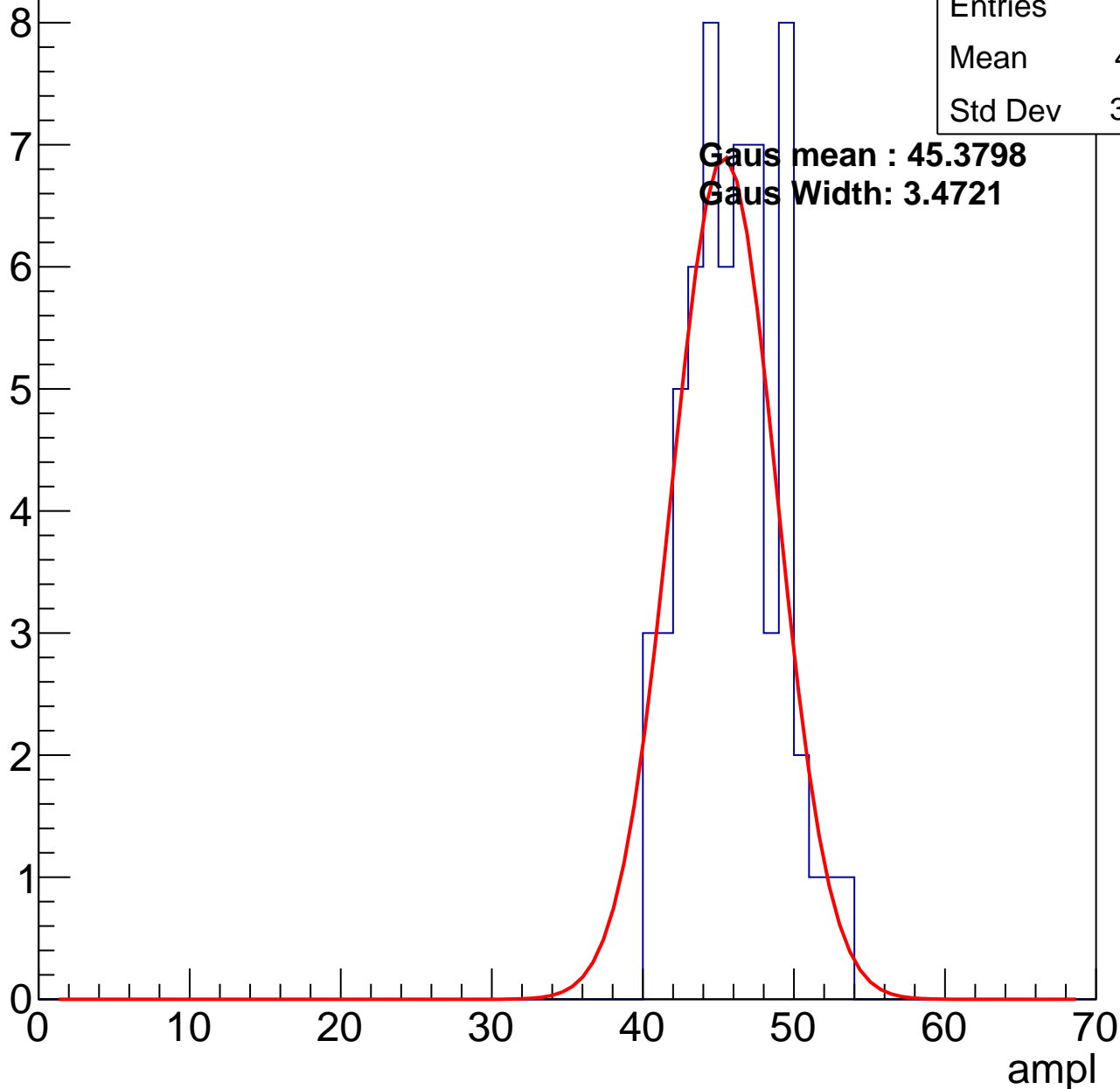
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	45.51
Std Dev	3.066

Gaus mean : 45.3798

Gaus Width: 3.4721

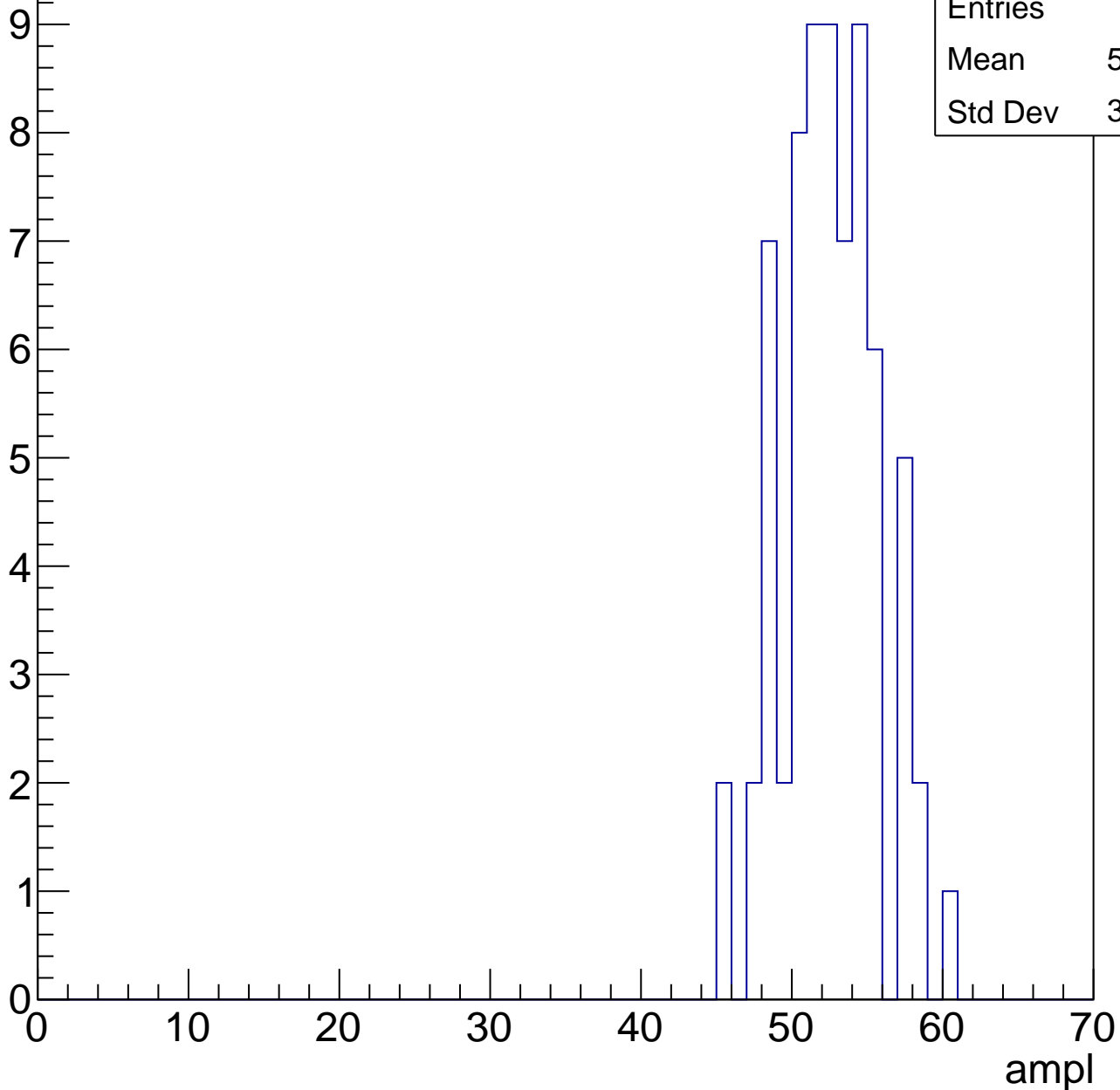


B0L002S, U12-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	52.07
Std Dev	3.132



B0L002S, U12-ch121, adc4

calib_packv5_042523_0143.root, FC#8, port C1

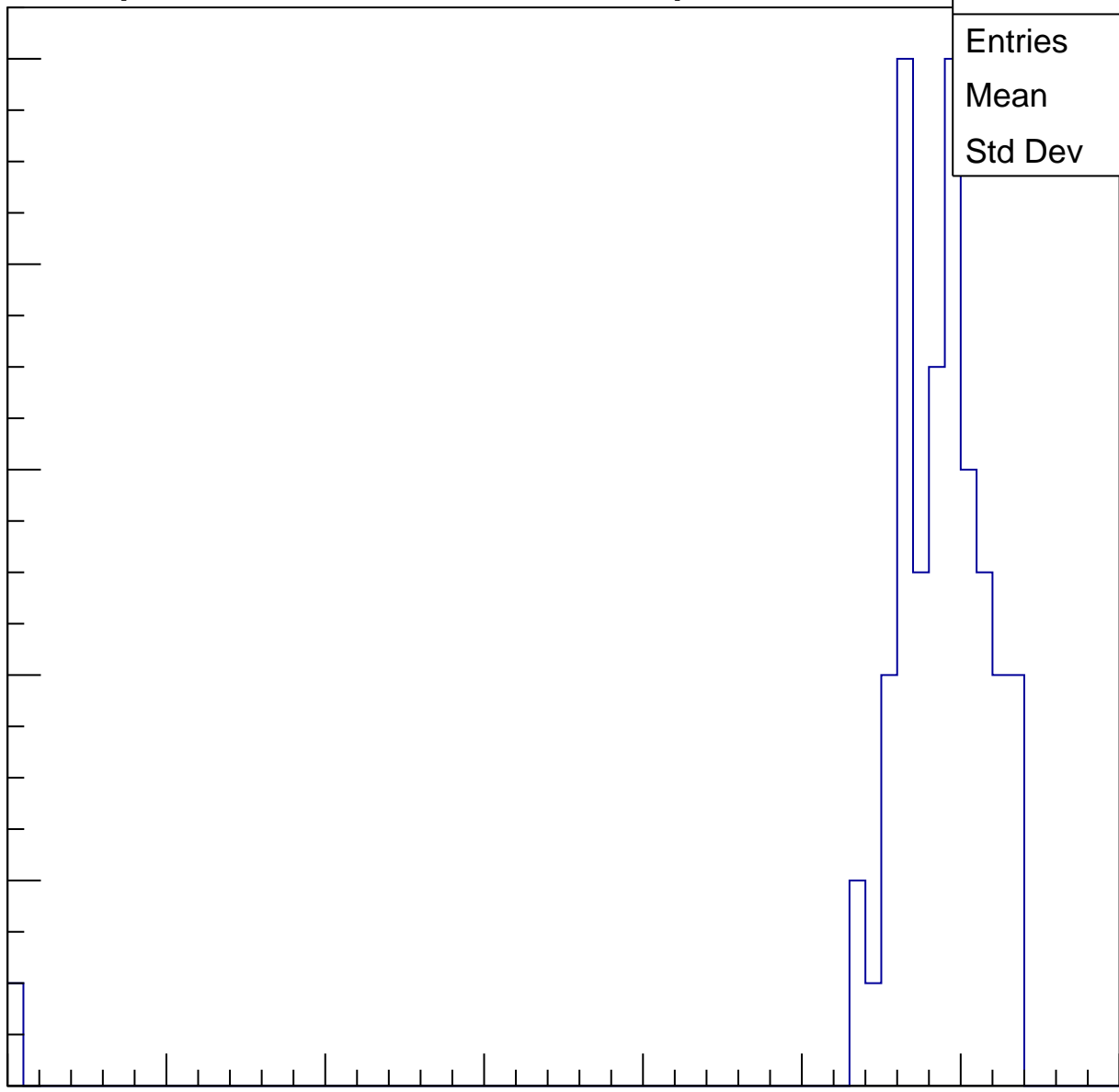
Entries	59
Mean	57.39
Std Dev	7.953

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

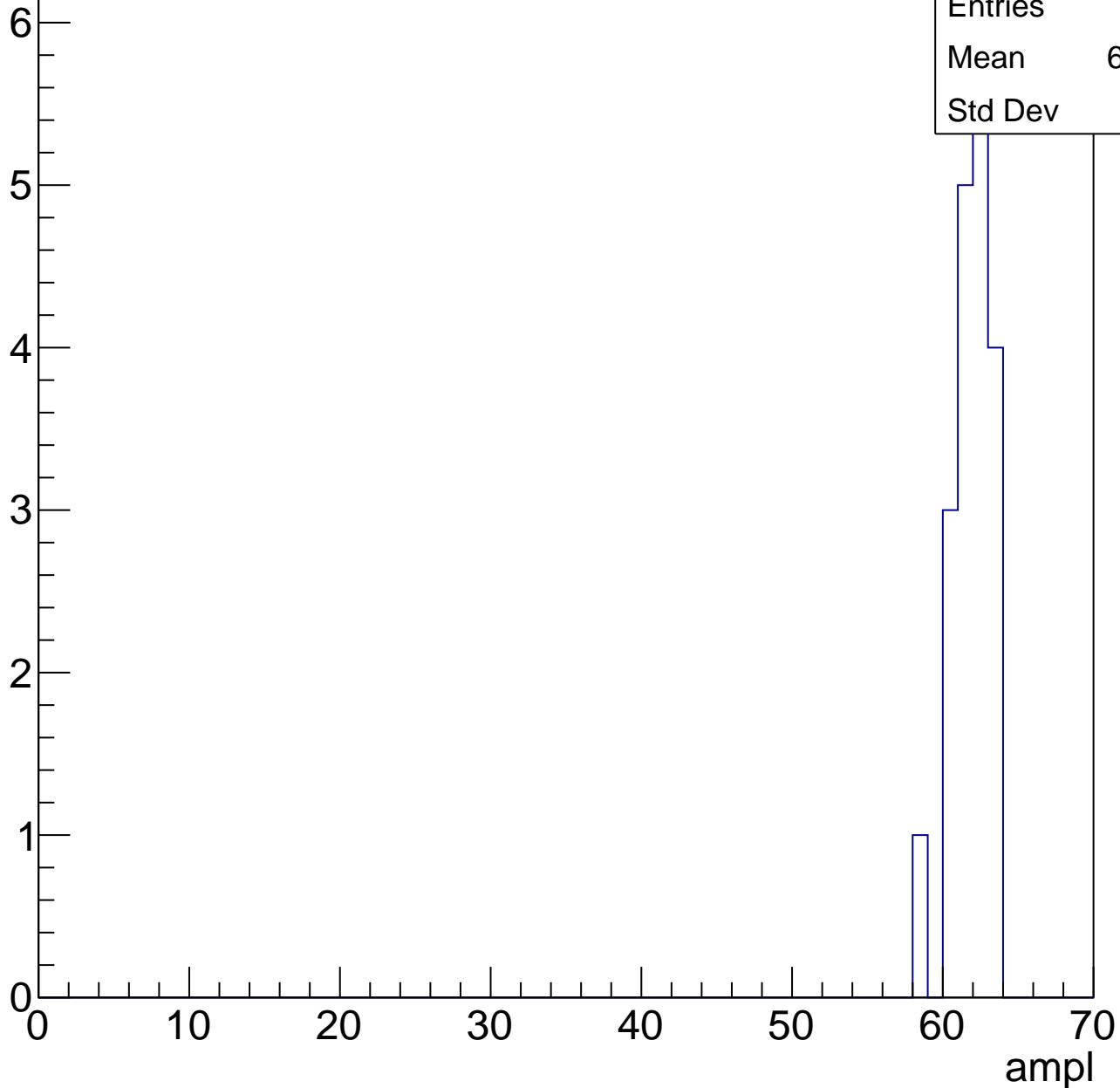


B0L002S, U12-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	61.42
Std Dev	1.27



B0L002S, U12-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch122, adc0

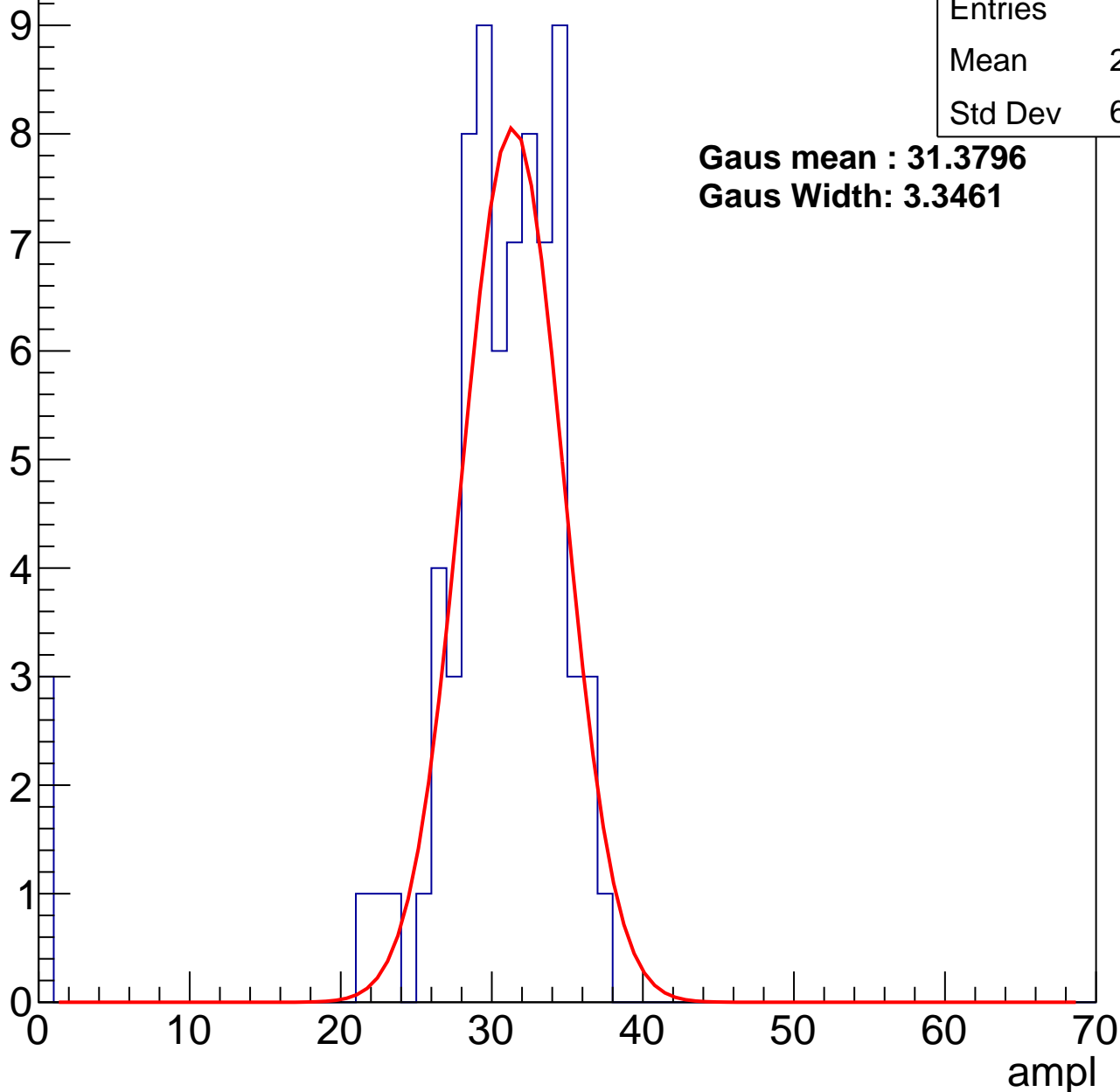
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	29.35
Std Dev	6.828

Gaus mean : 31.3796

Gaus Width: 3.3461



B0L002S, U12-ch122, adc1

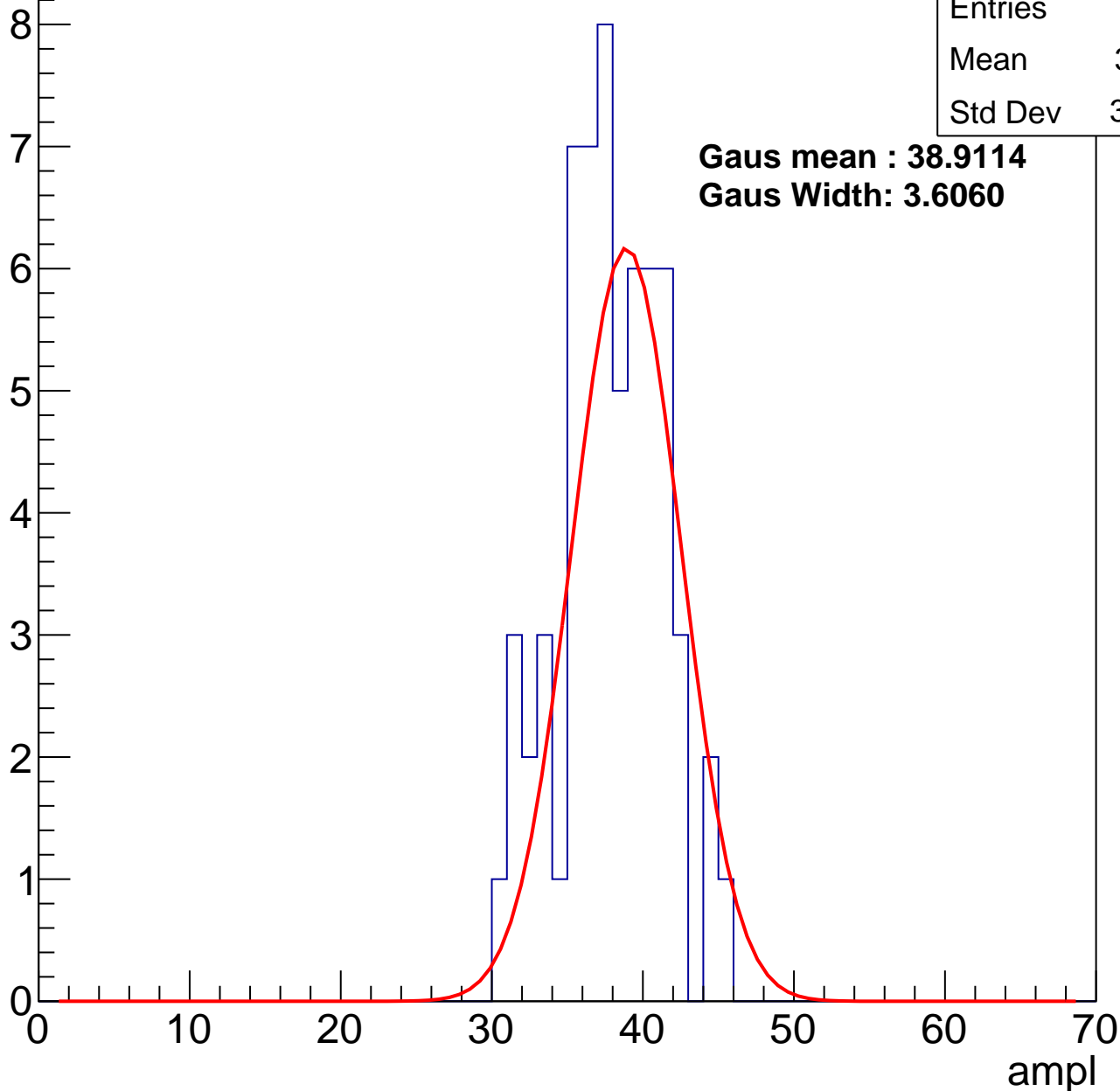
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.41
Std Dev	3.399

Gaus mean : 38.9114

Gaus Width: 3.6060



B0L002S, U12-ch122, adc2

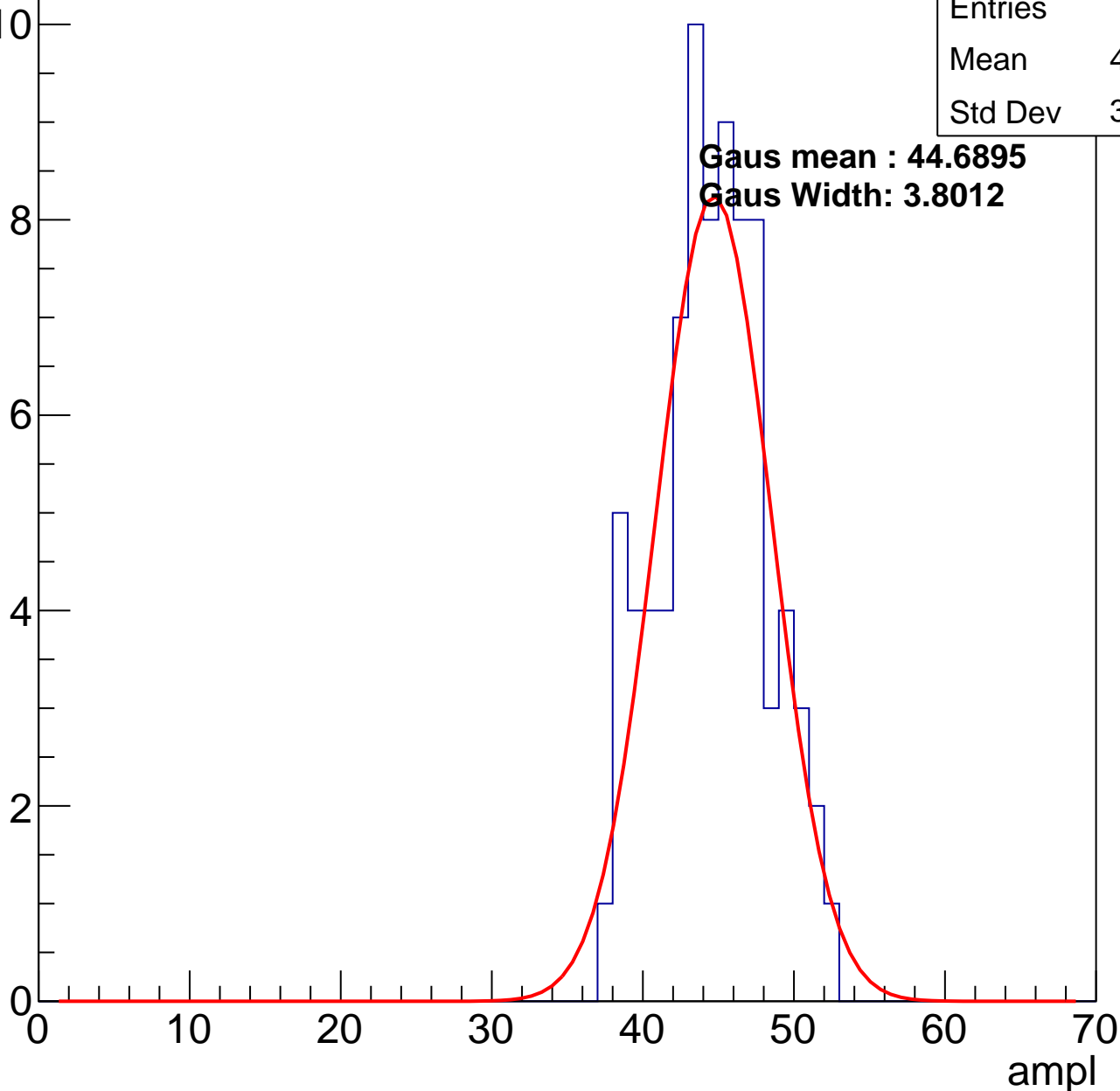
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	44.15
Std Dev	3.507

Gaus mean : 44.6895

Gaus Width: 3.8012

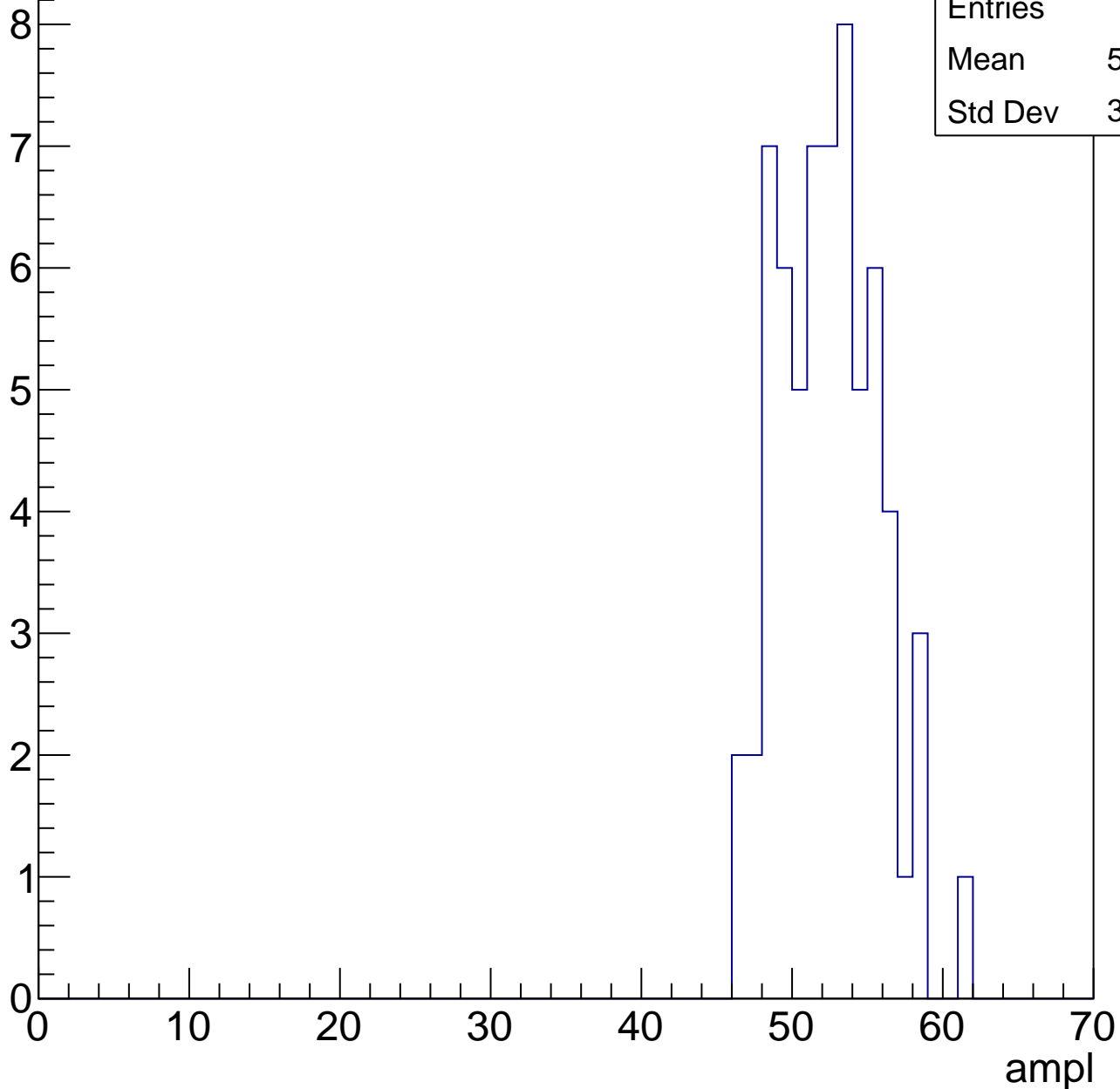


B0L002S, U12-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

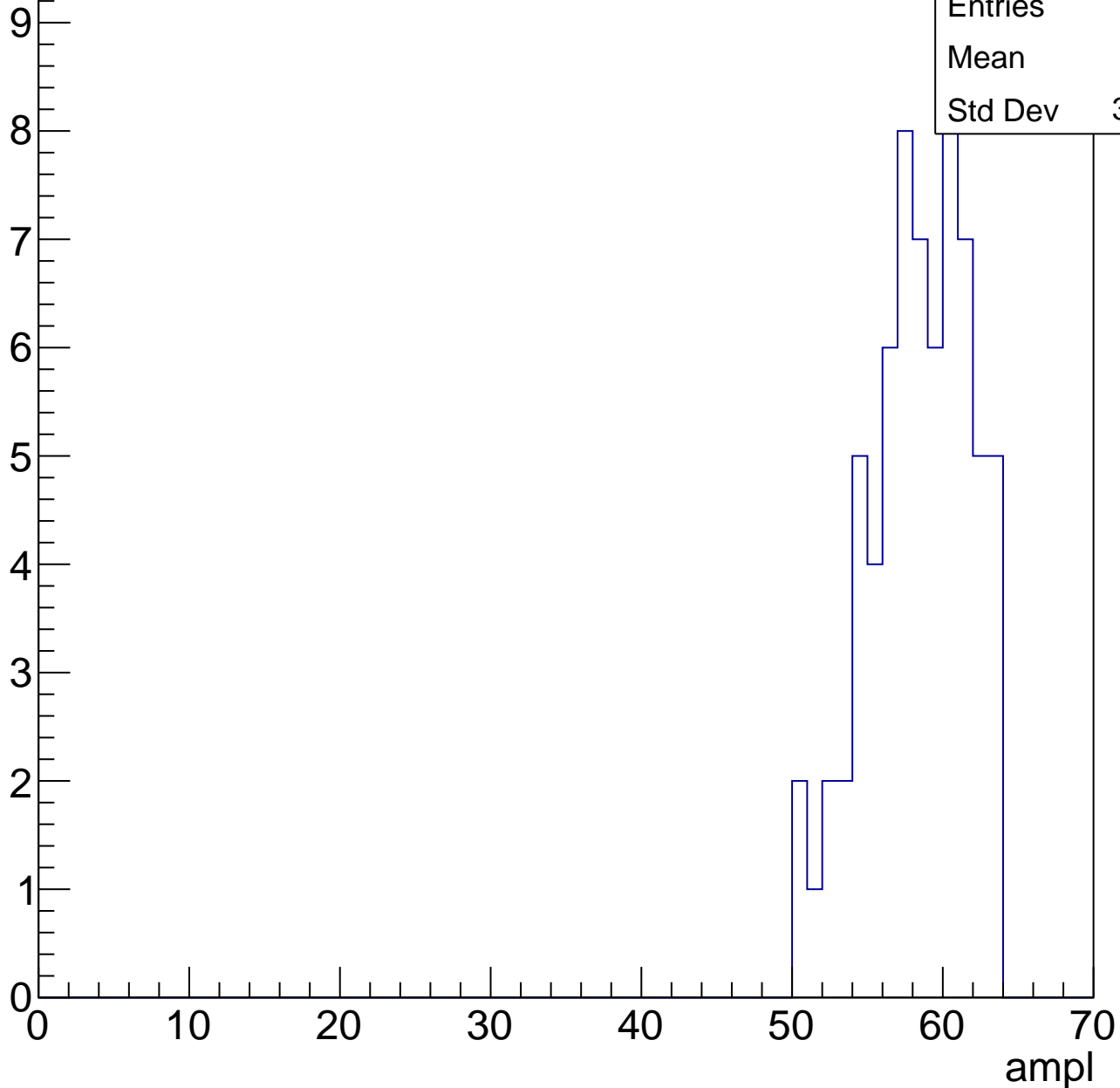
Entries	64
Mean	51.98
Std Dev	3.248



B0L002S, U12-ch122, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

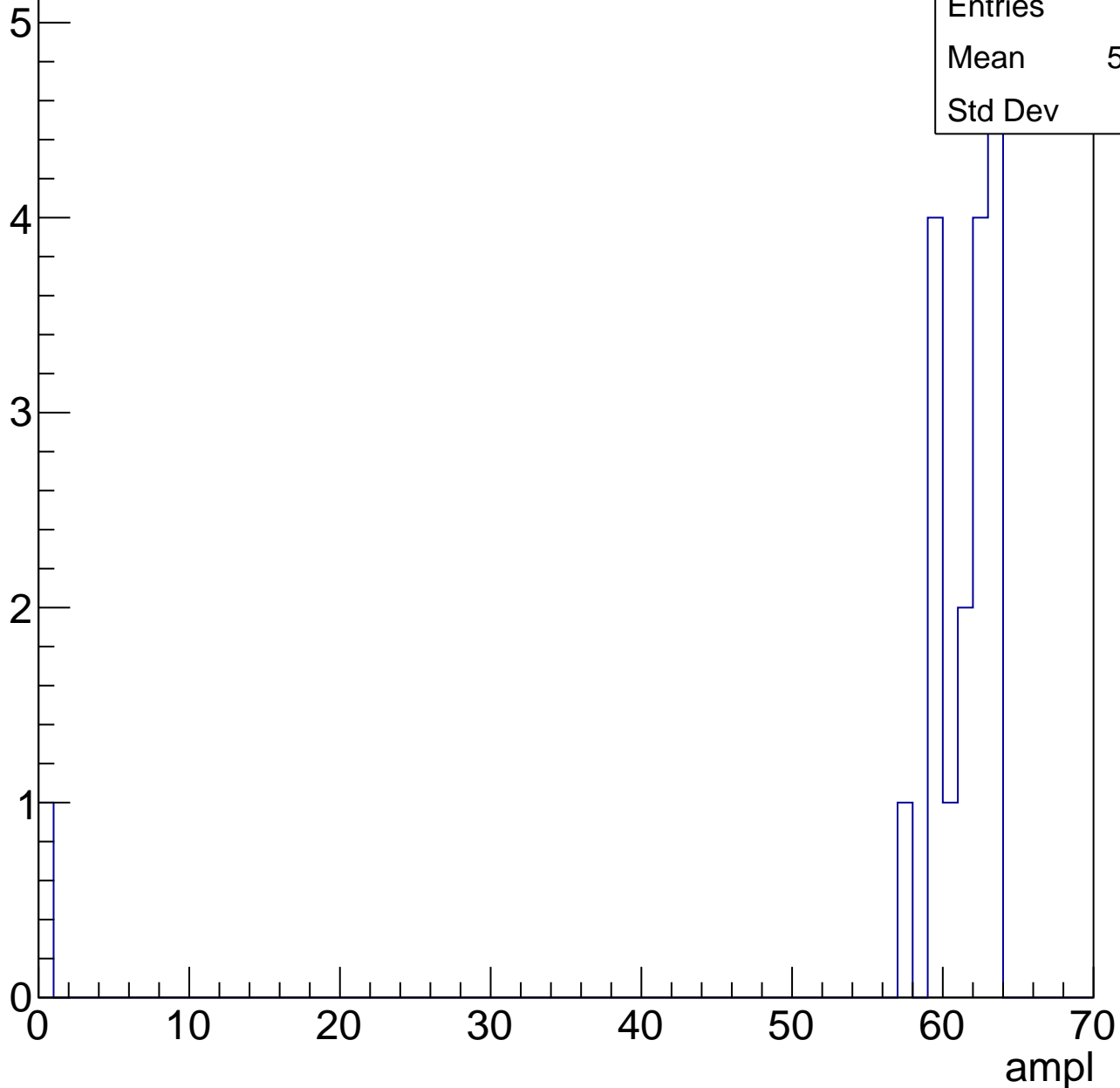


B0L002S, U12-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	18
Mean	57.67
Std Dev	14.1



B0L002S, U12-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch123, adc0

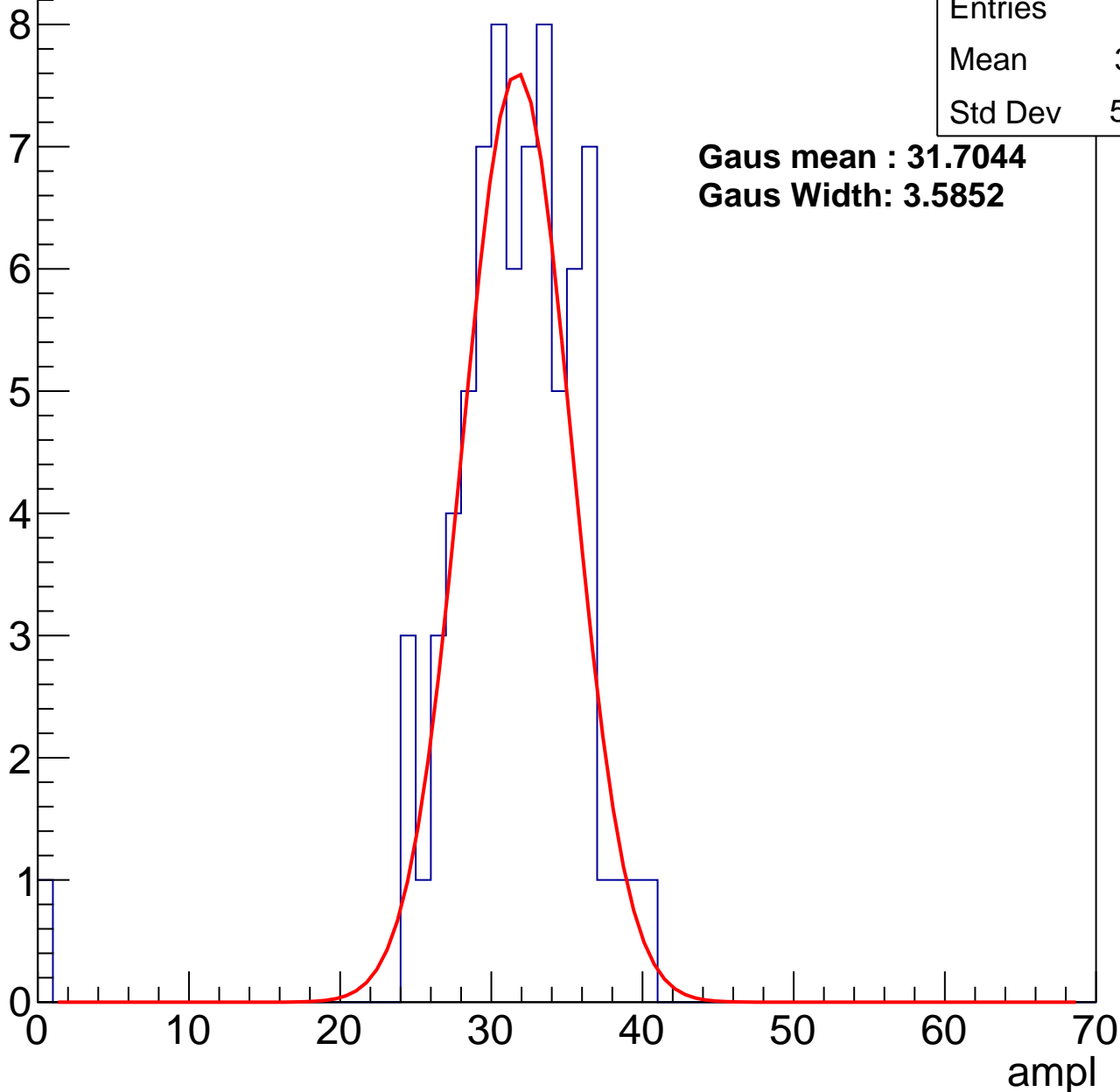
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	31.01
Std Dev	5.098

Gaus mean : 31.7044

Gaus Width: 3.5852



B0L002S, U12-ch123, adc1

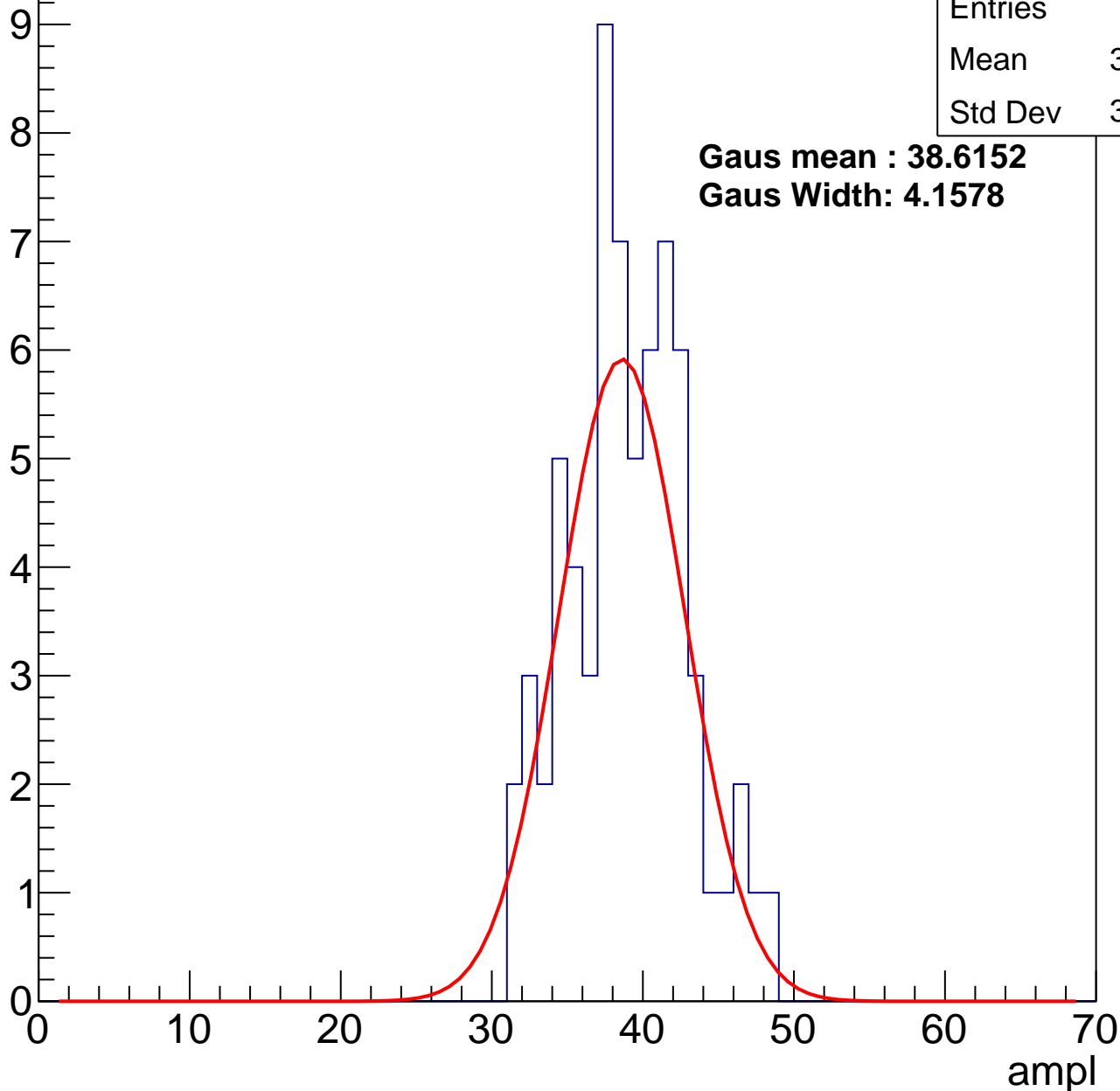
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38.53
Std Dev	3.894

Gaus mean : 38.6152

Gaus Width: 4.1578



B0L002S, U12-ch123, adc2

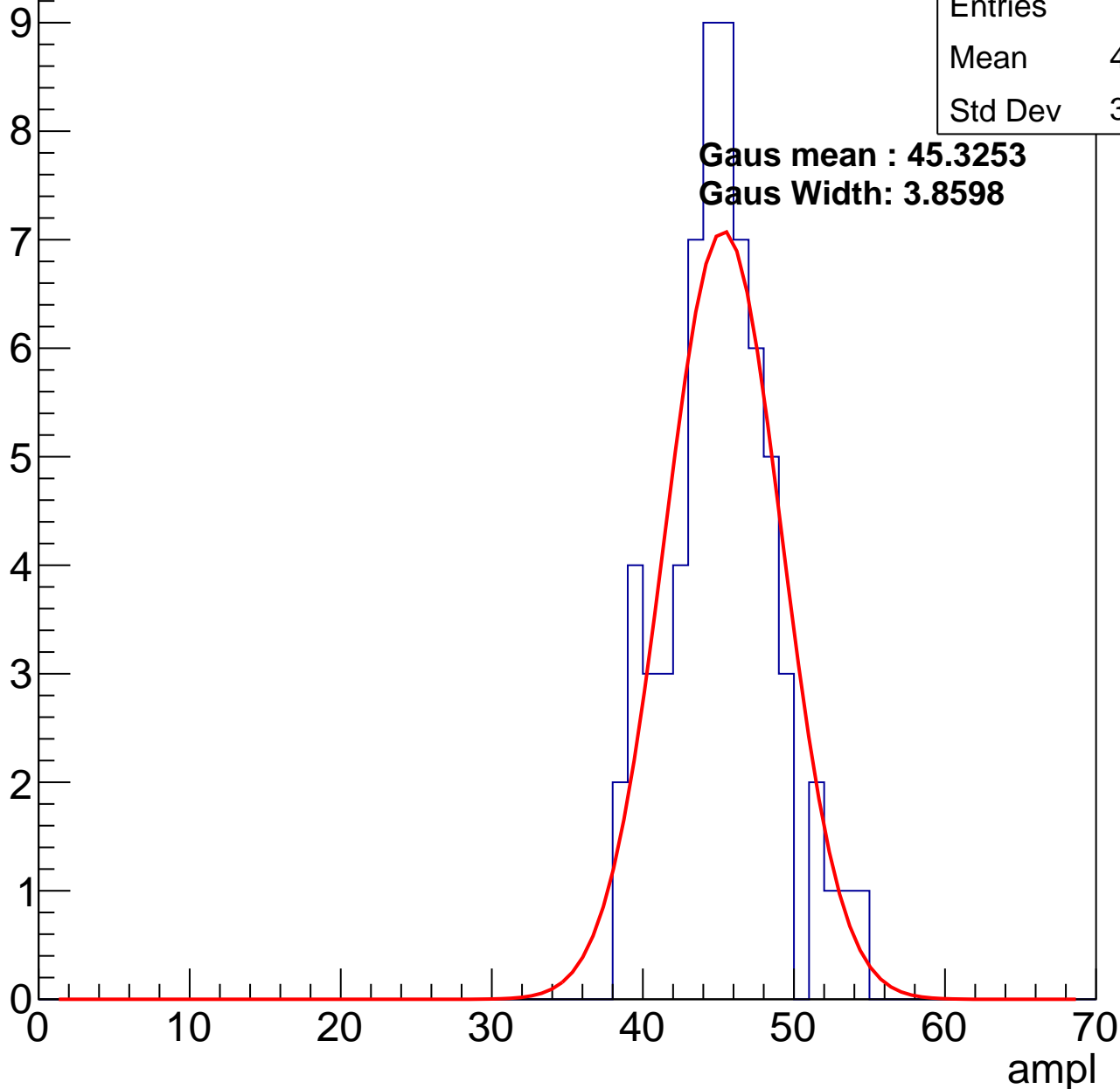
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.73
Std Dev	3.505

Gaus mean : 45.3253

Gaus Width: 3.8598

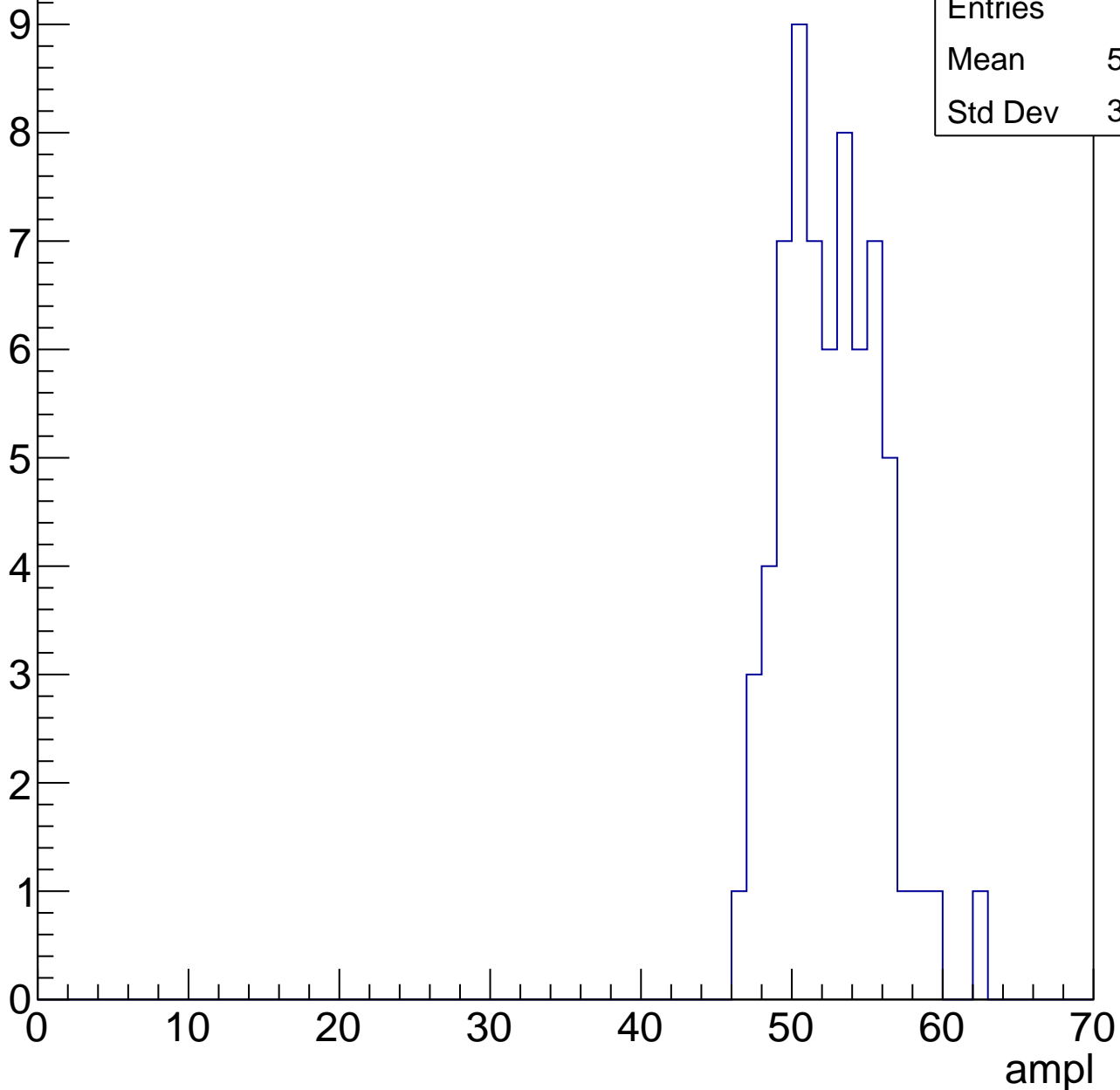


B0L002S, U12-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	52.09
Std Dev	3.152

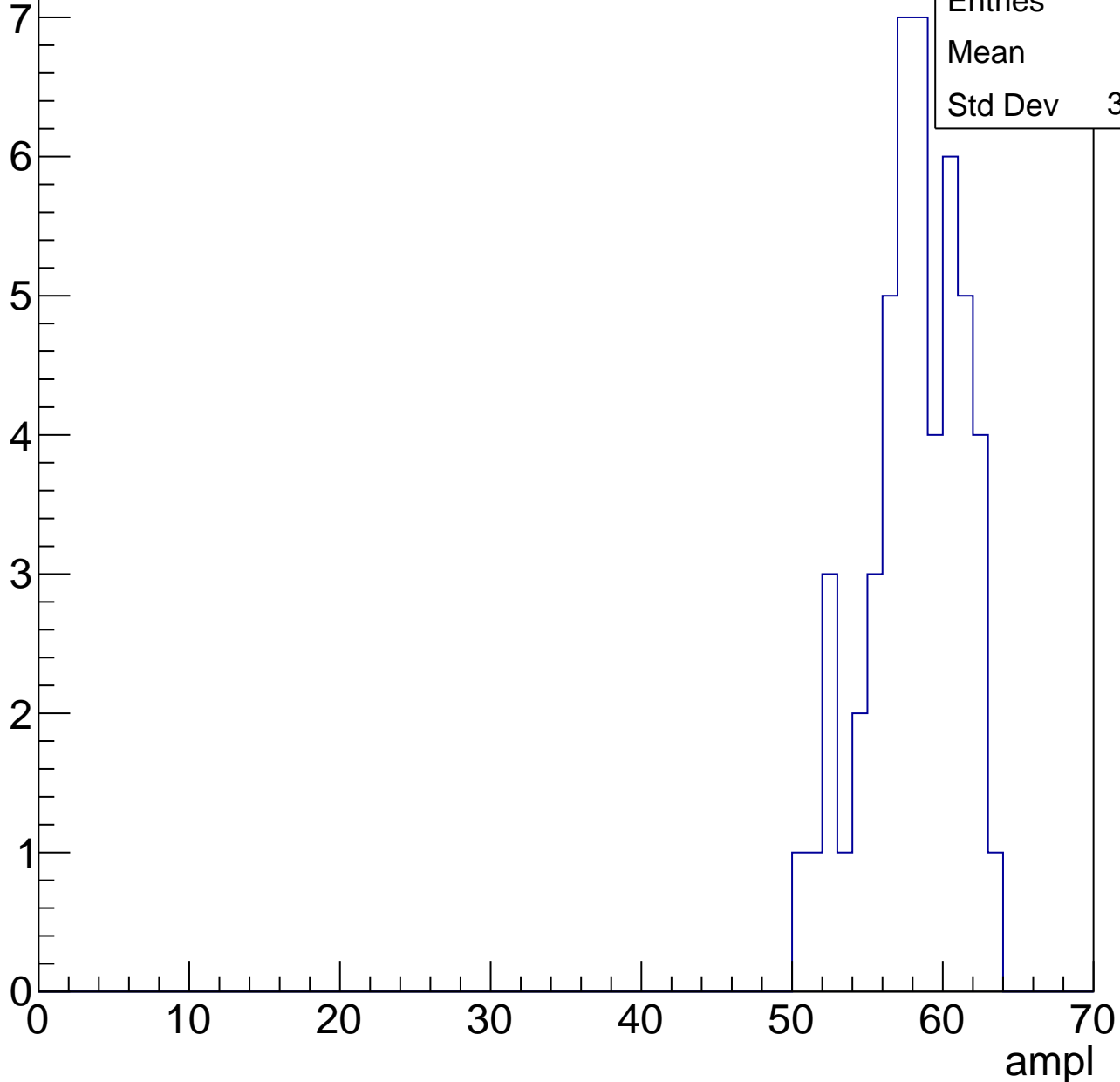


B0L002S, U12-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	57.6
Std Dev	3.118

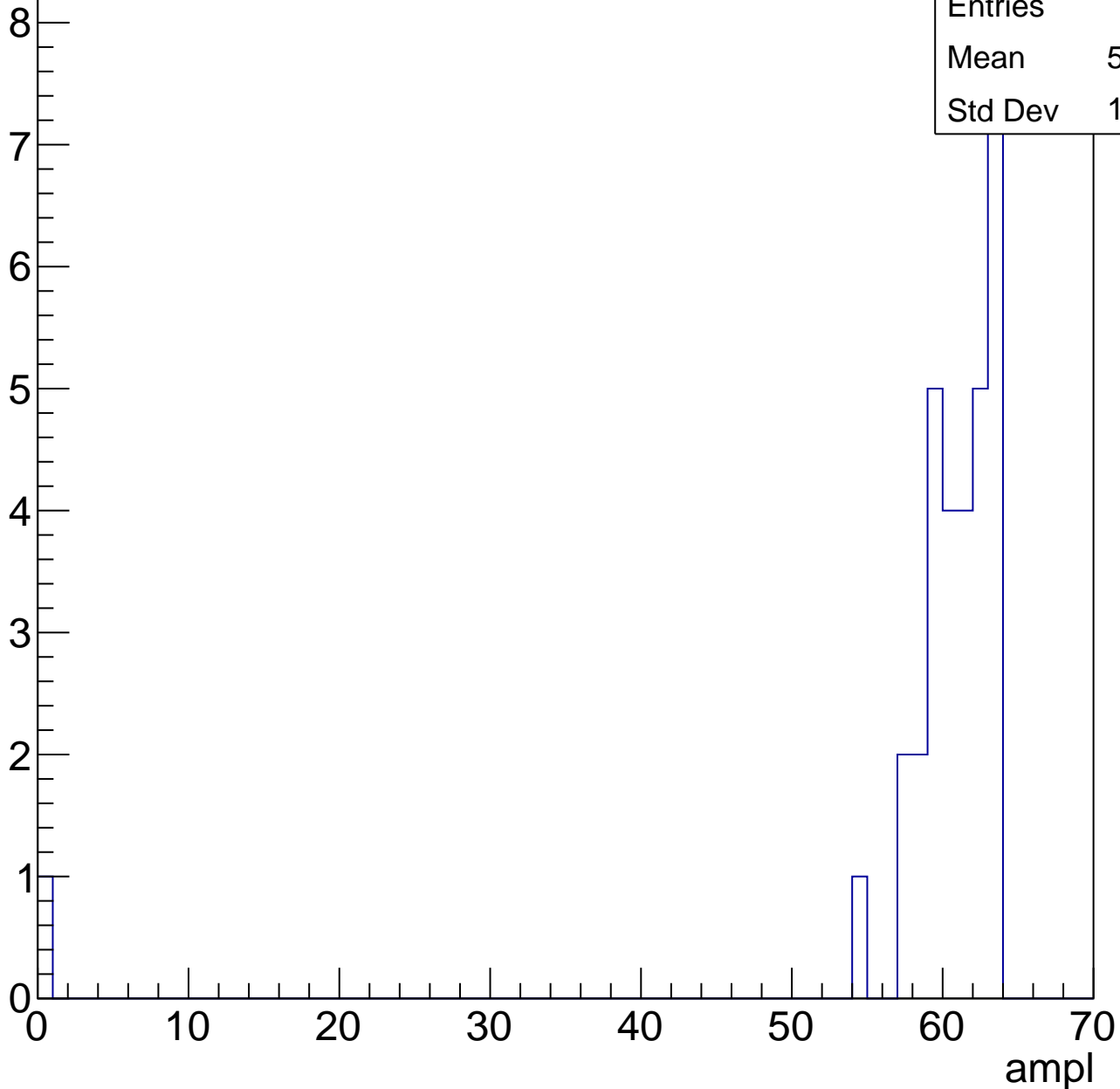


B0L002S, U12-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

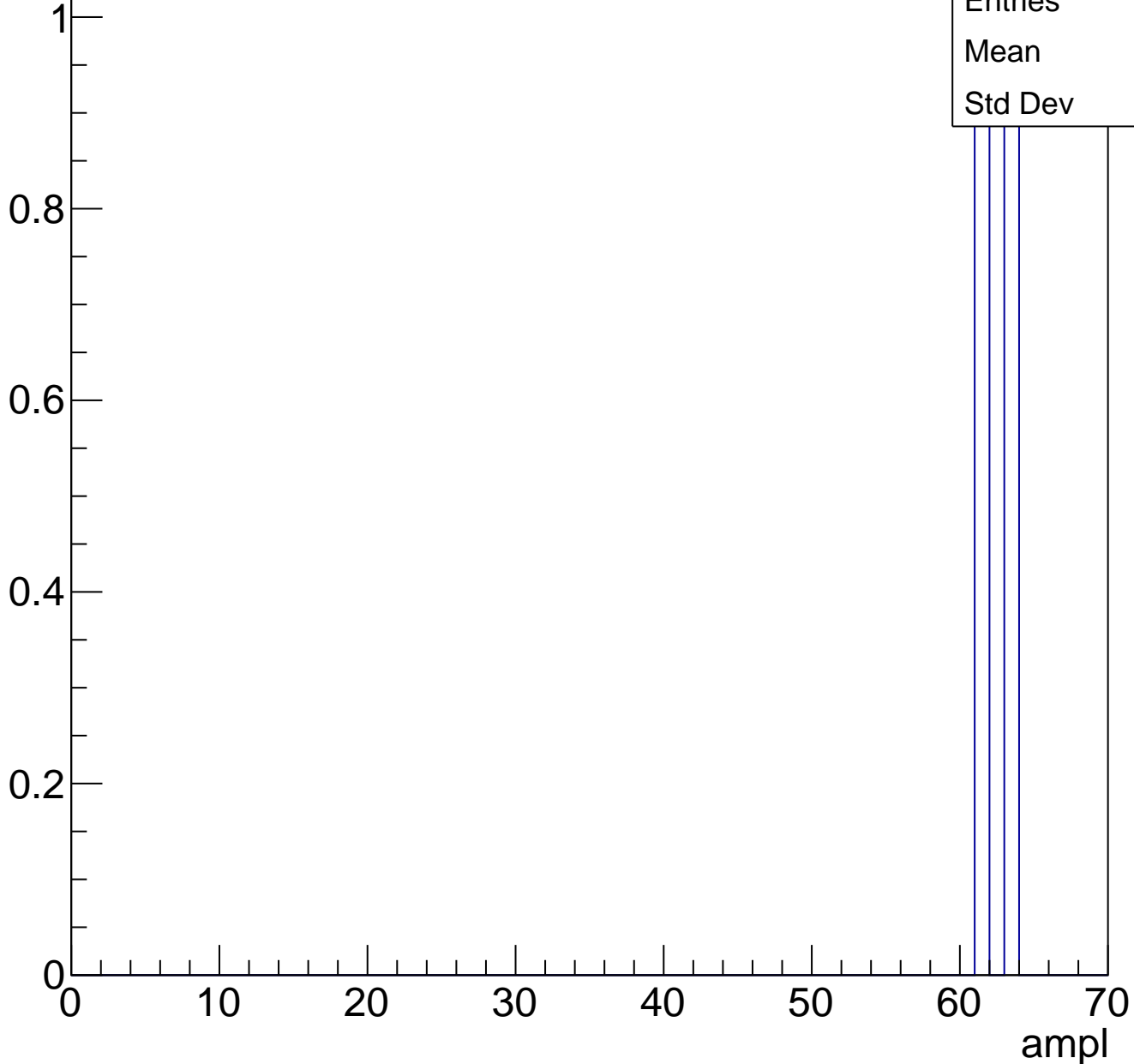
Entries	32
Mean	58.66
Std Dev	10.76



B0L002S, U12-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch124, adc0

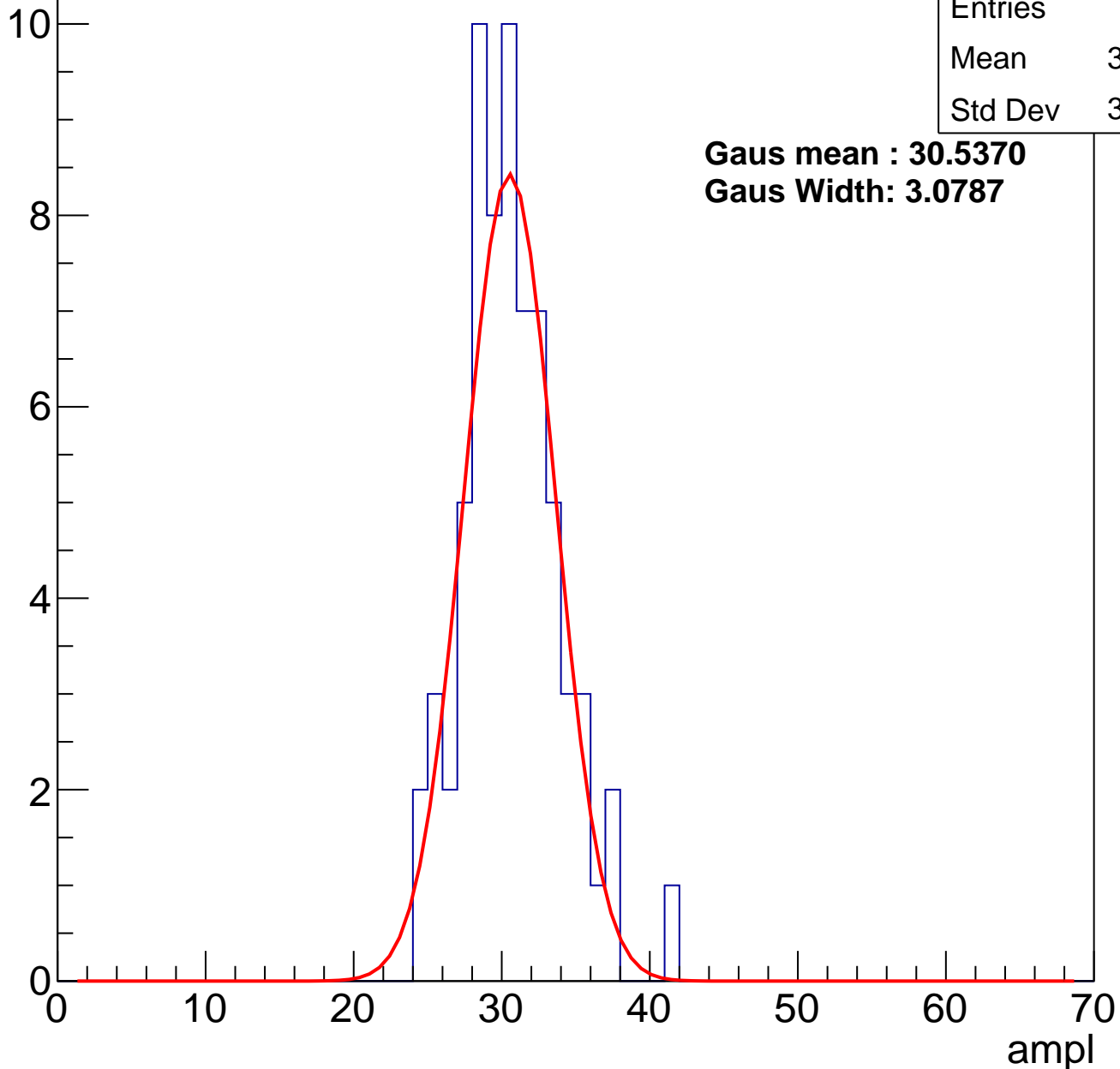
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	30.23
Std Dev	3.244

Gaus mean : 30.5370

Gaus Width: 3.0787

Entry



B0L002S, U12-ch124, adc1

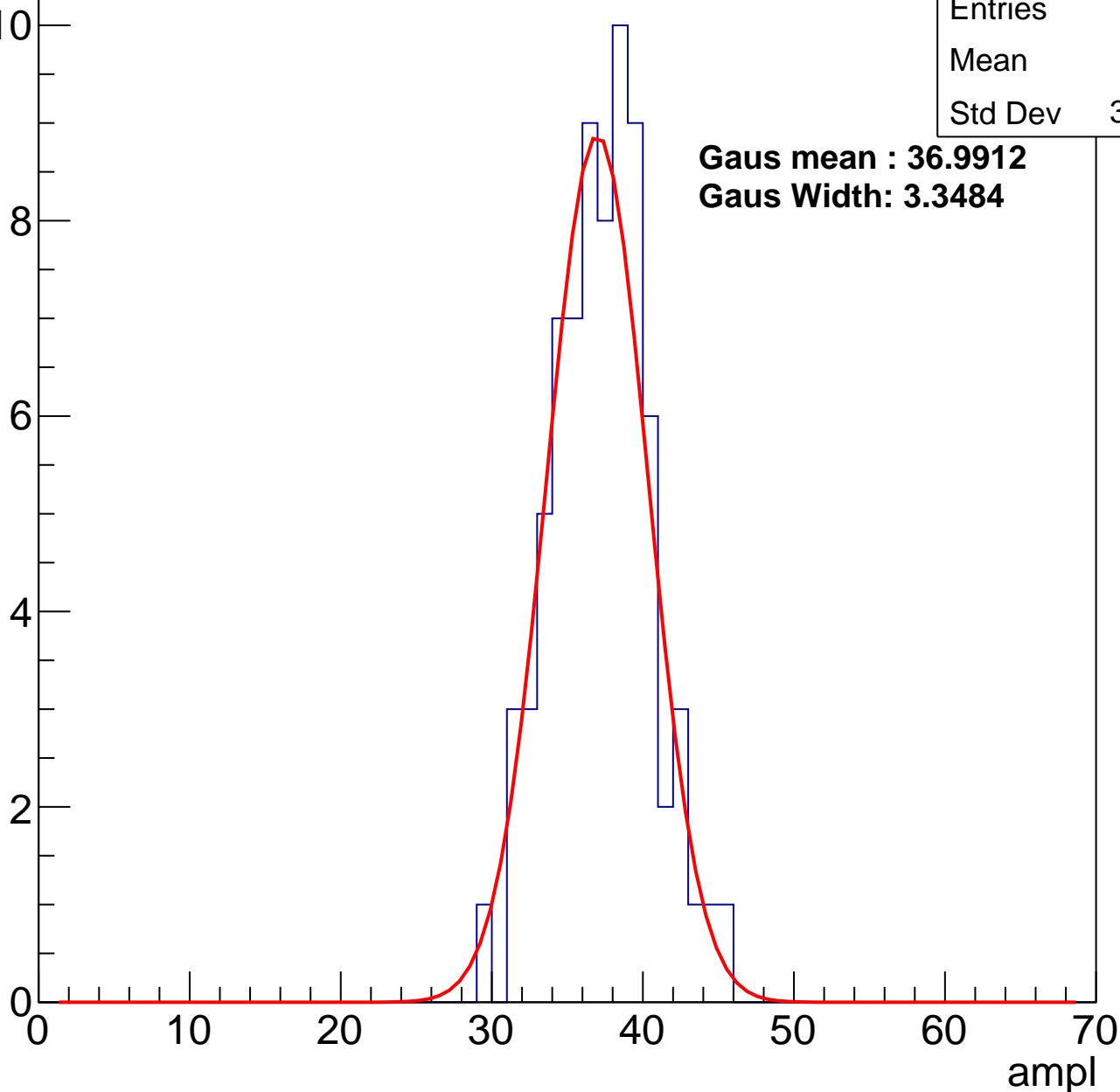
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.8
Std Dev	3.204

Gaus mean : 36.9912

Gaus Width: 3.3484



B0L002S, U12-ch124, adc2

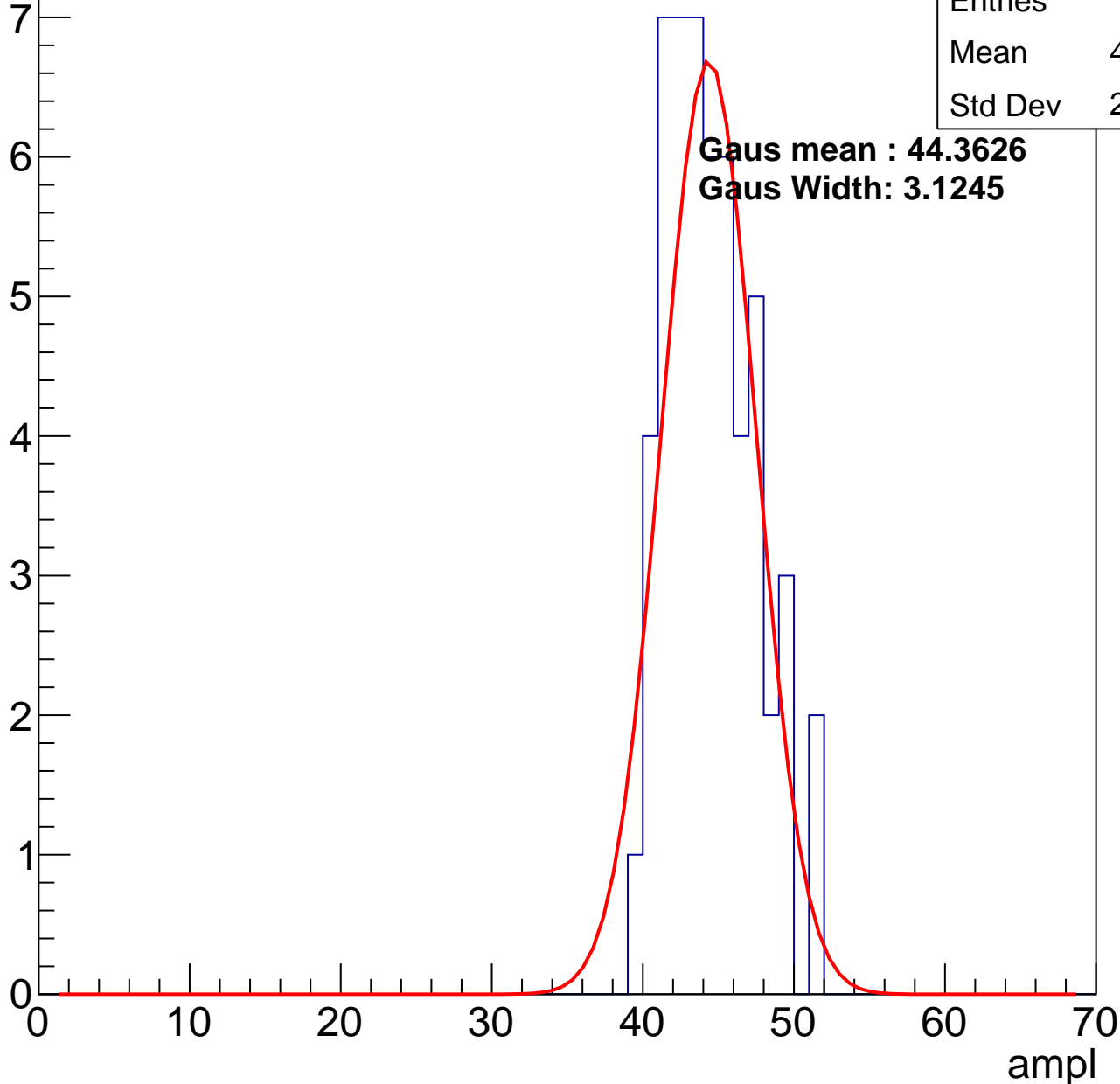
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	44.06
Std Dev	2.915

Gaus mean : 44.3626

Gaus Width: 3.1245

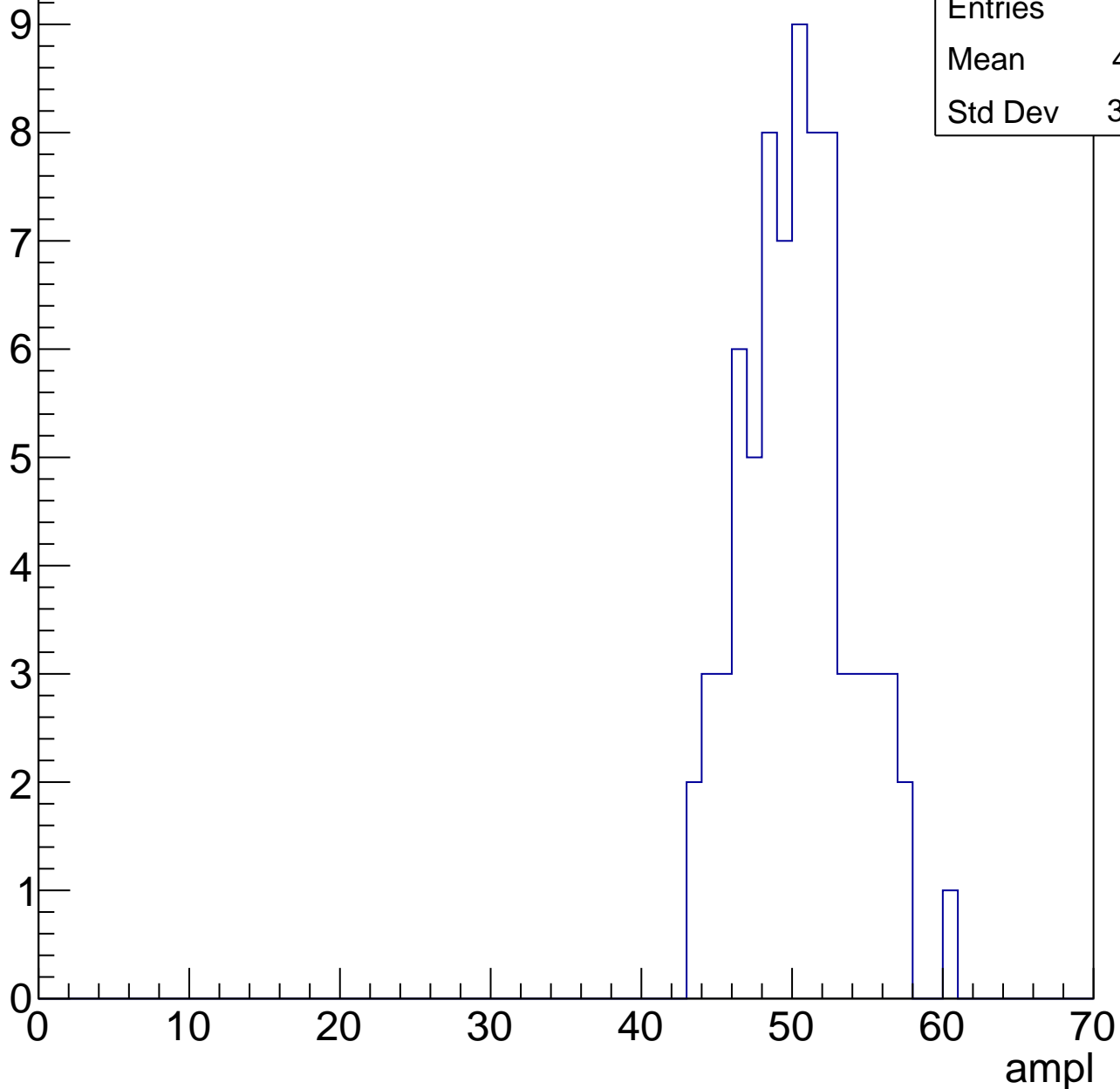


B0L002S, U12-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	49.91
Std Dev	3.595

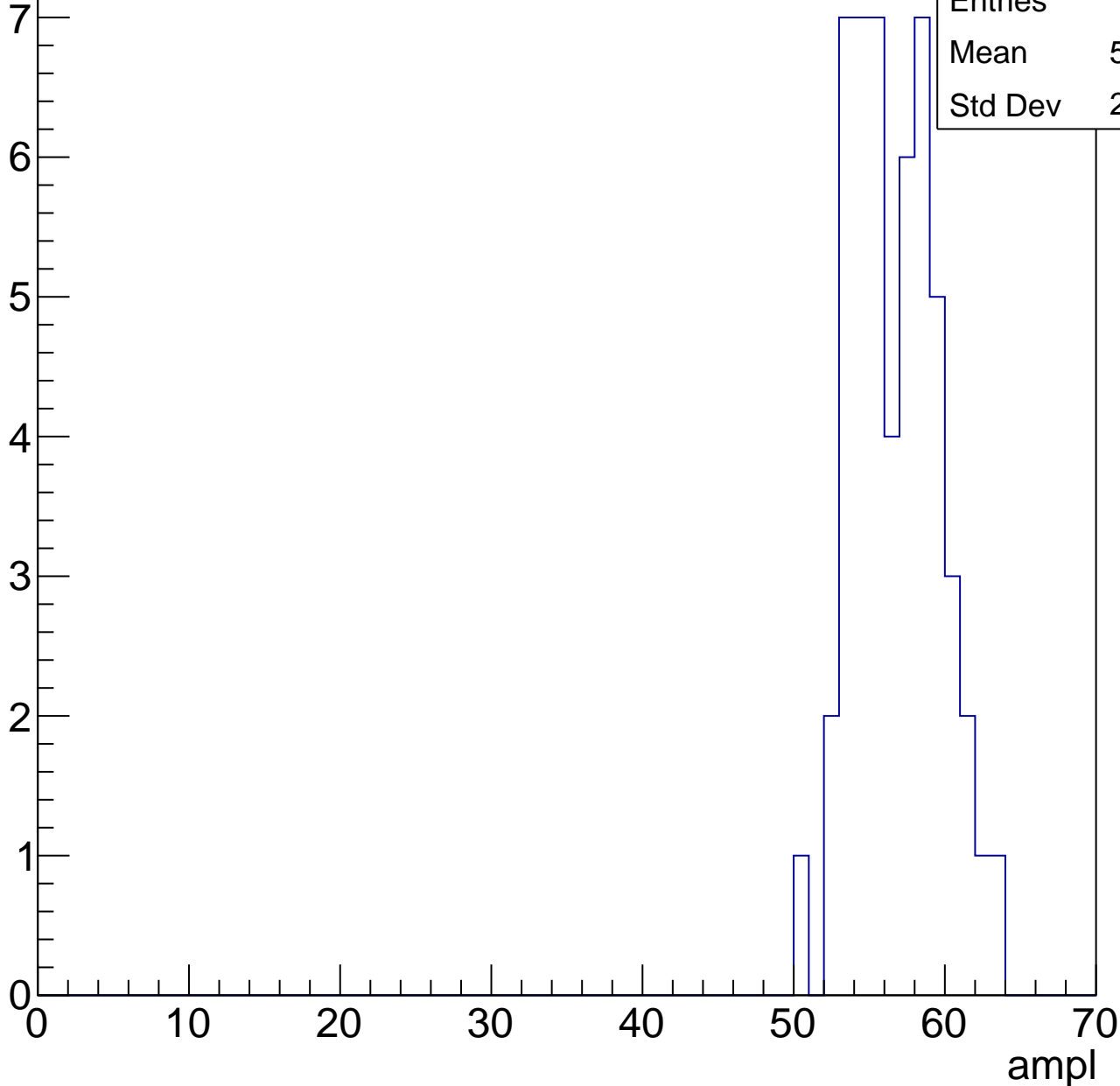


B0L002S, U12-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.26
Std Dev	2.829

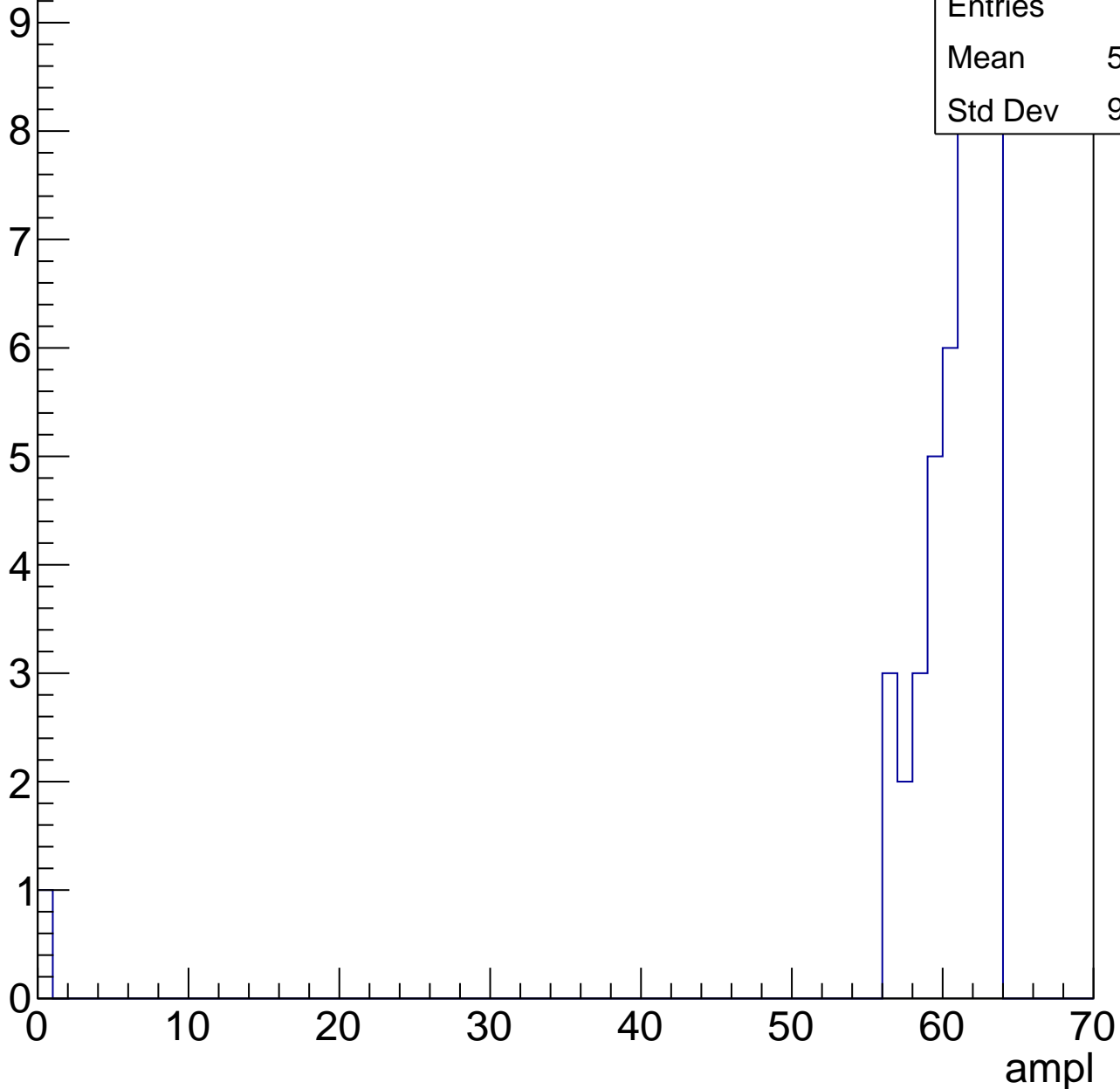


B0L002S, U12-ch124, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	59.22
Std Dev	9.065



B0L002S, U12-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U12-ch125, adc0

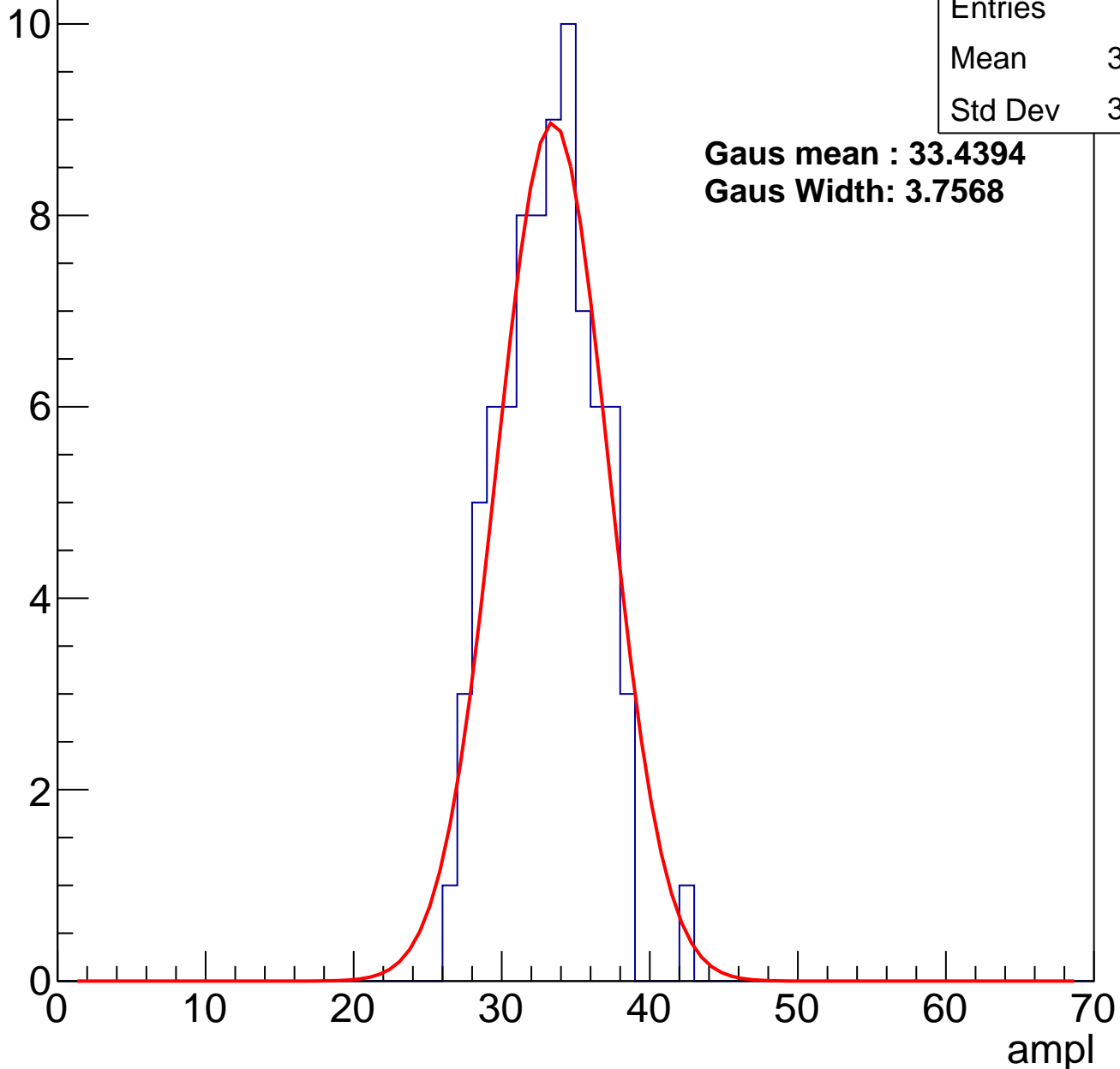
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	32.67
Std Dev	3.197

Gaus mean : 33.4394

Gaus Width: 3.7568

Entry



B0L002S, U12-ch125, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	40.18
Std Dev	3.29

Gaus mean : 40.1892

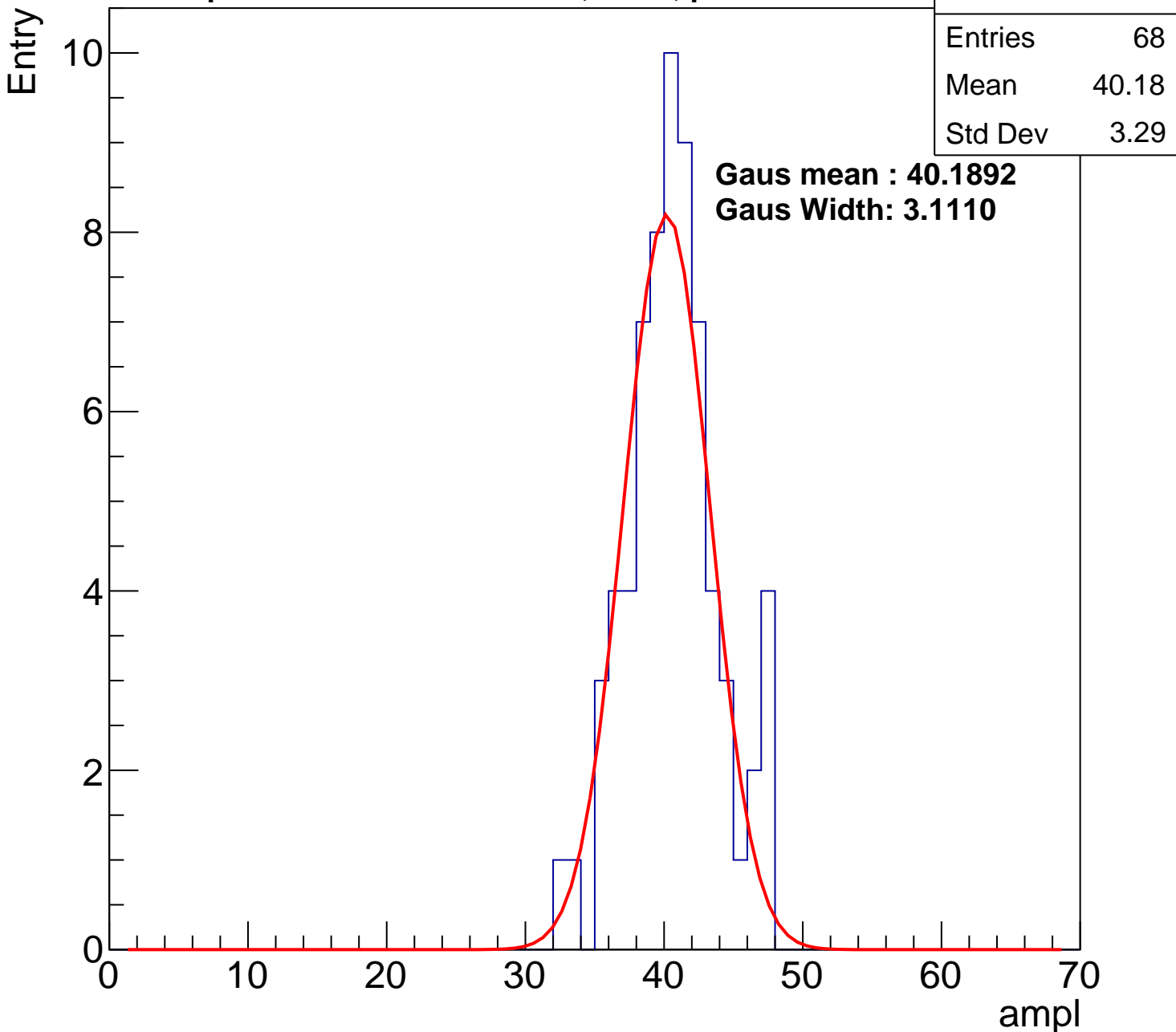
Gaus Width: 3.1110

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch125, adc2

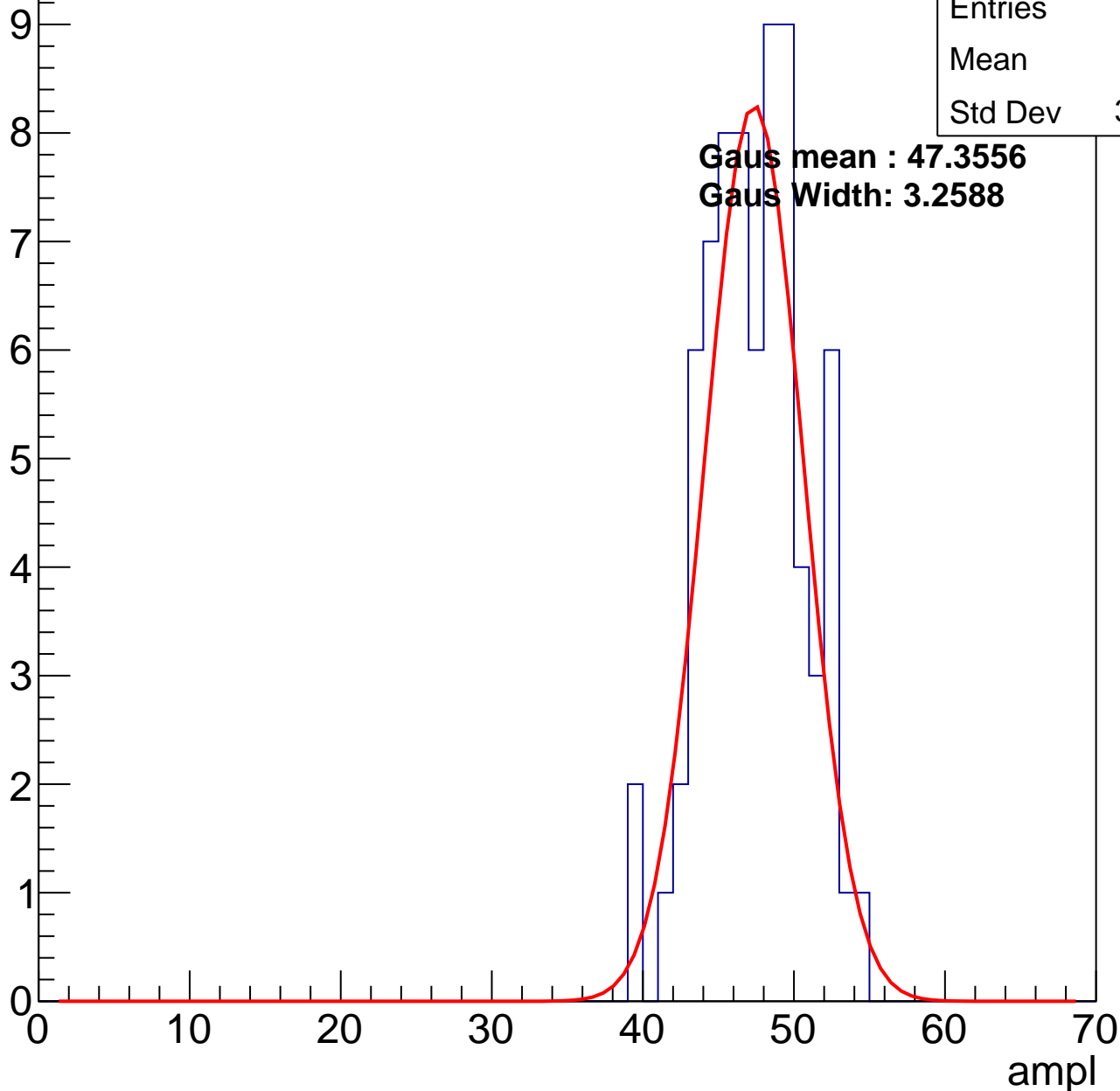
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	46.9
Std Dev	3.261

Gaus mean : 47.3556

Gaus Width: 3.2588

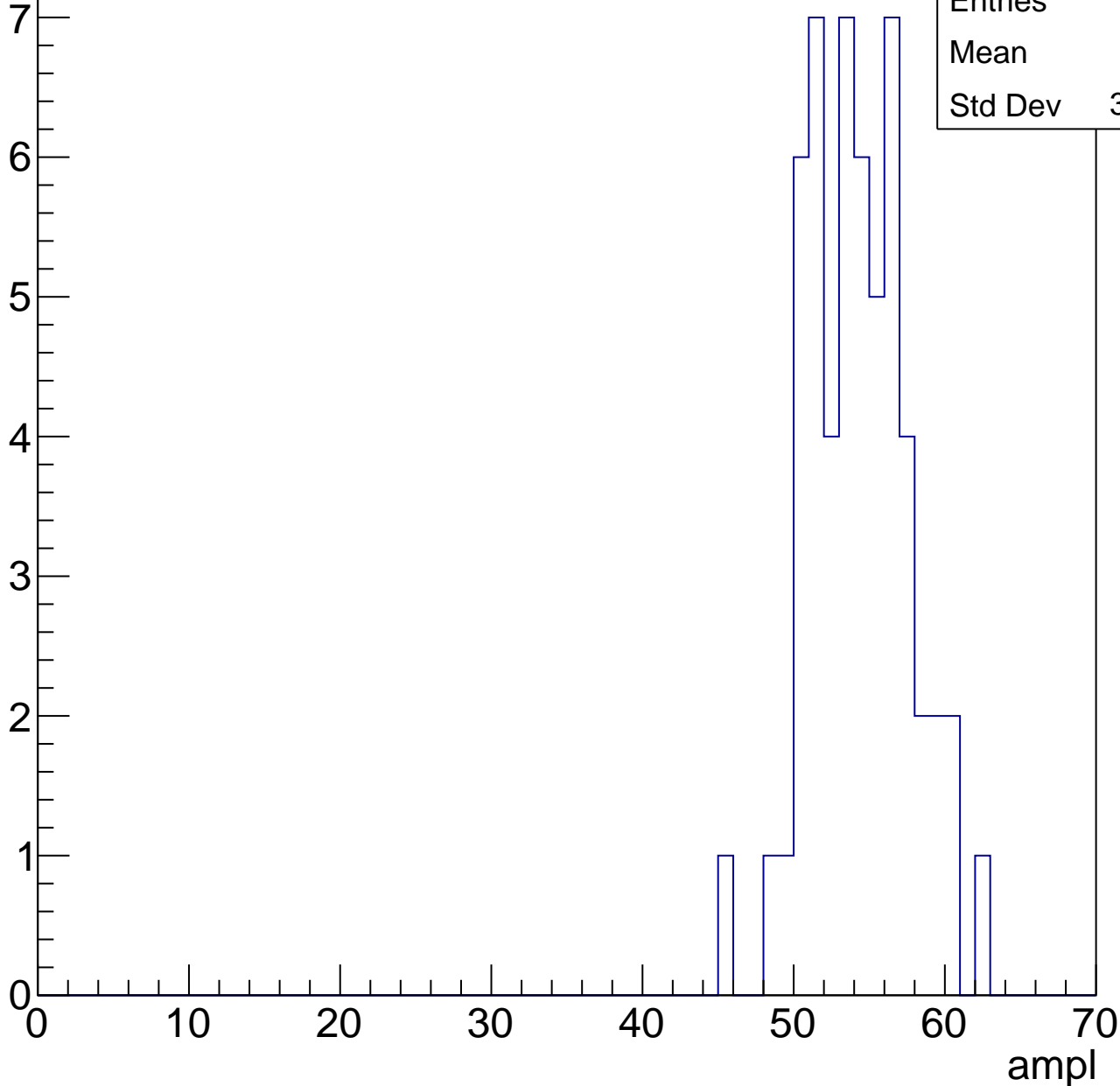


B0L002S, U12-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	53.8
Std Dev	3.297

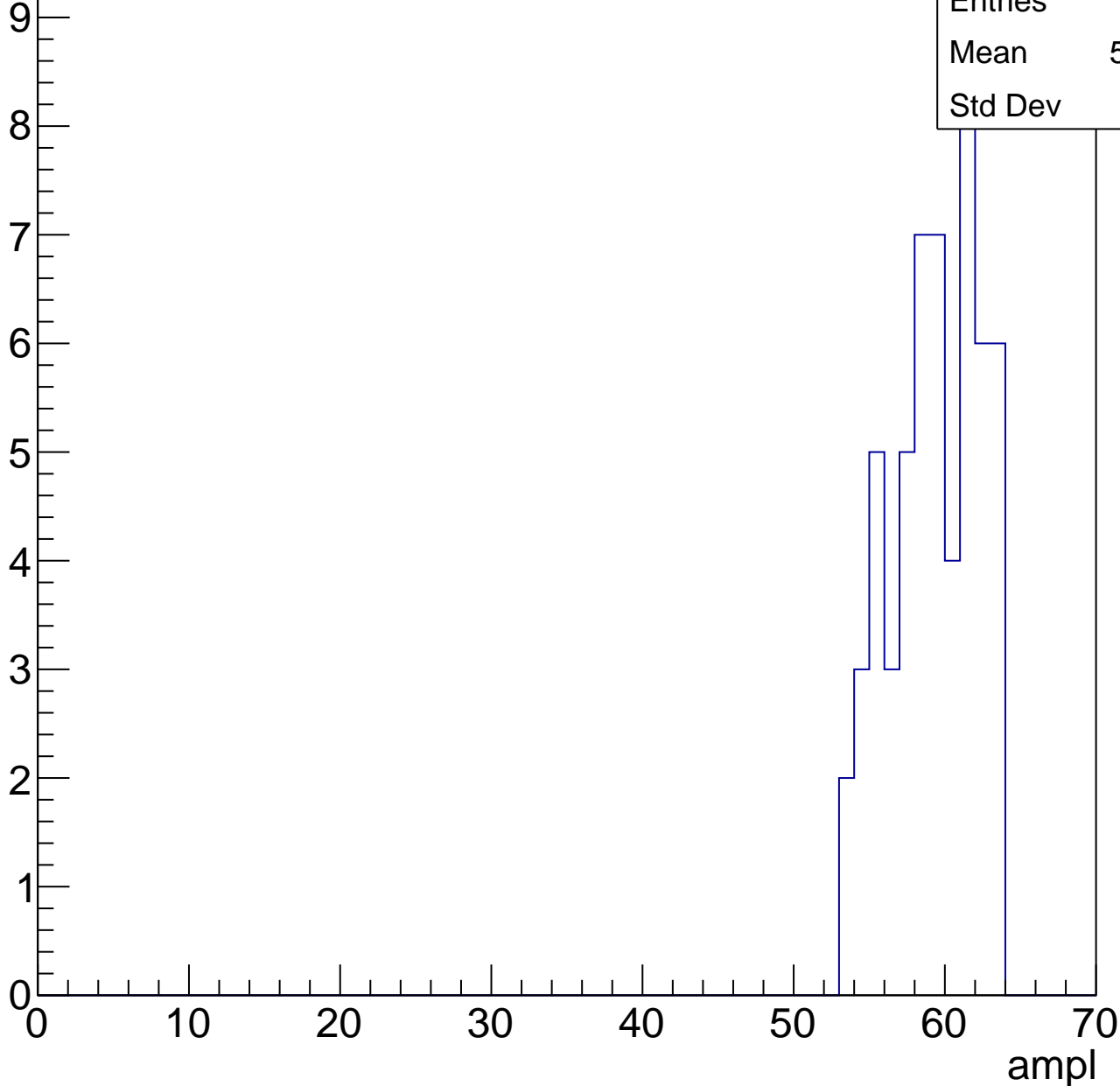


B0L002S, U12-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

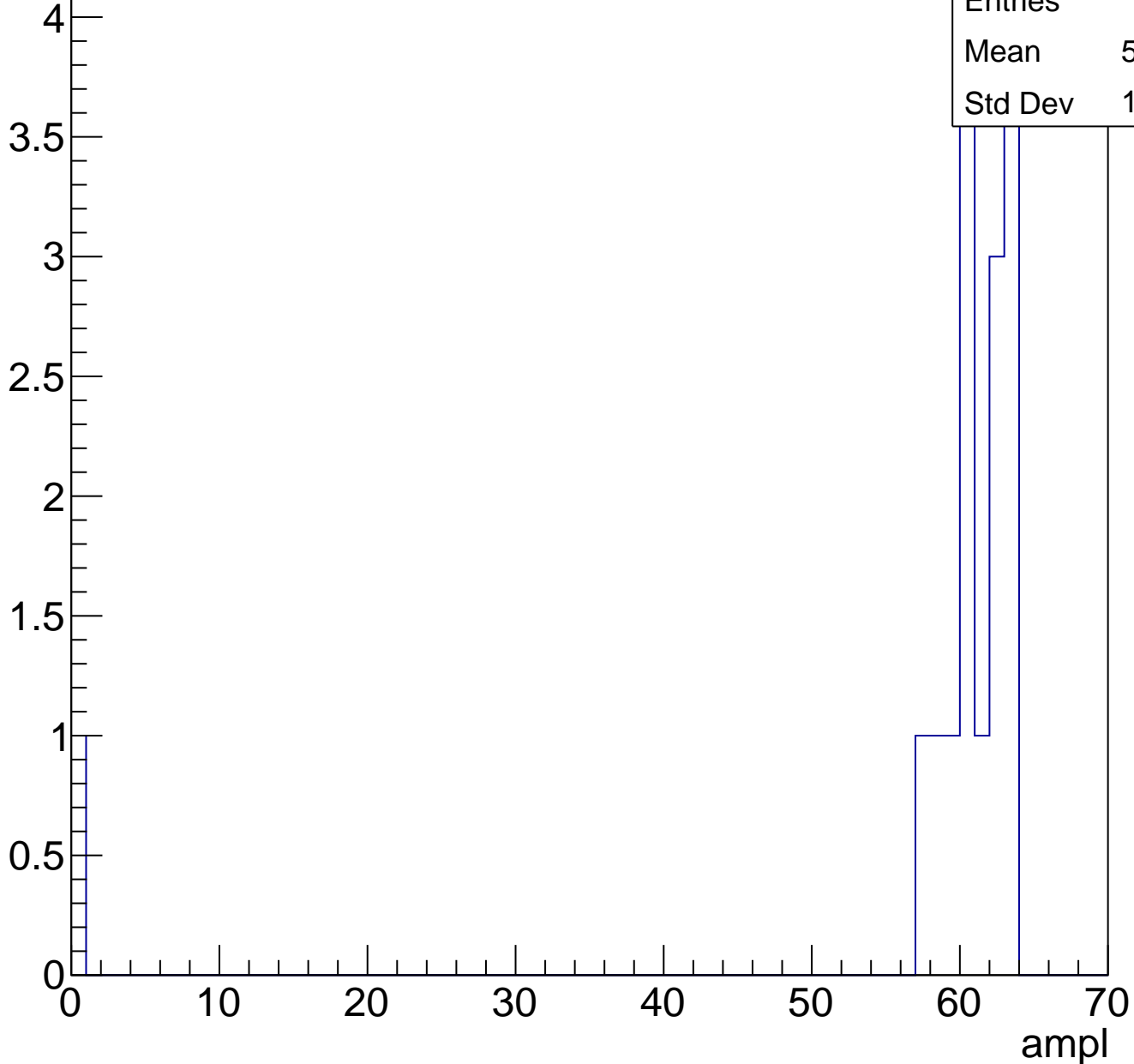
Entries	57
Mean	58.84
Std Dev	2.87



B0L002S, U12-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

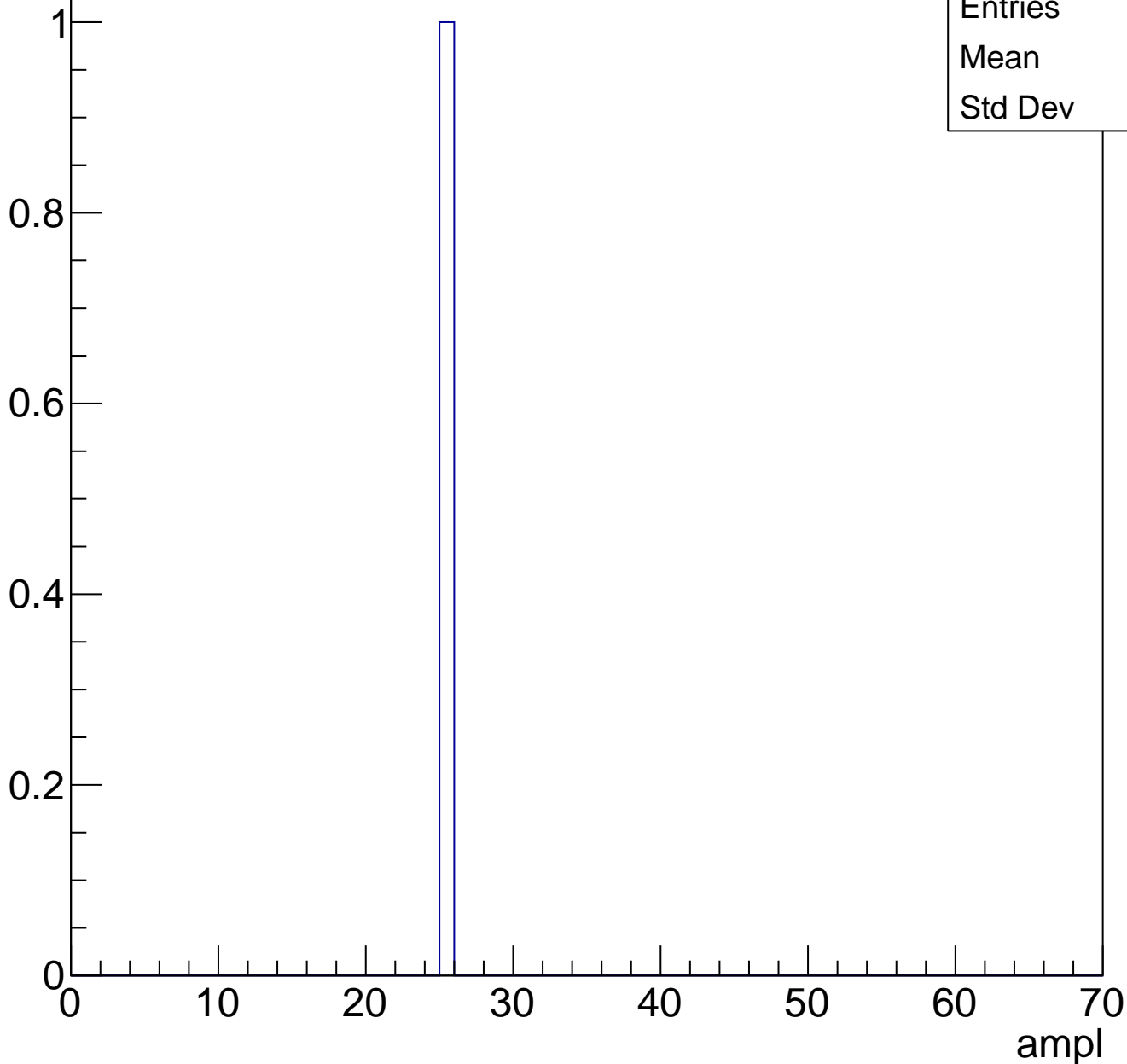


Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch126, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	32.35
Std Dev	3.373

Gaus mean : 32.9717

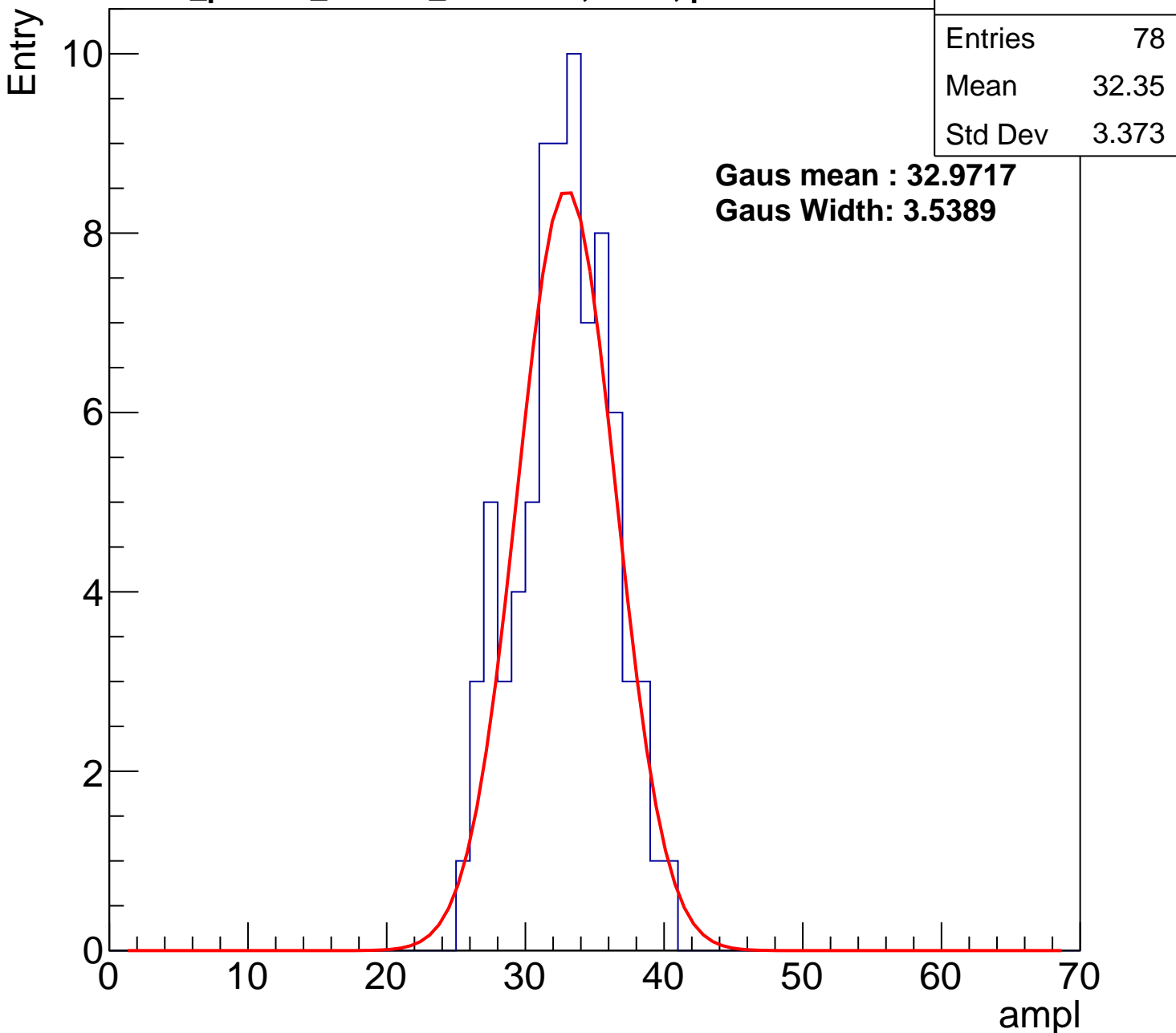
Gaus Width: 3.5389

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch126, adc1

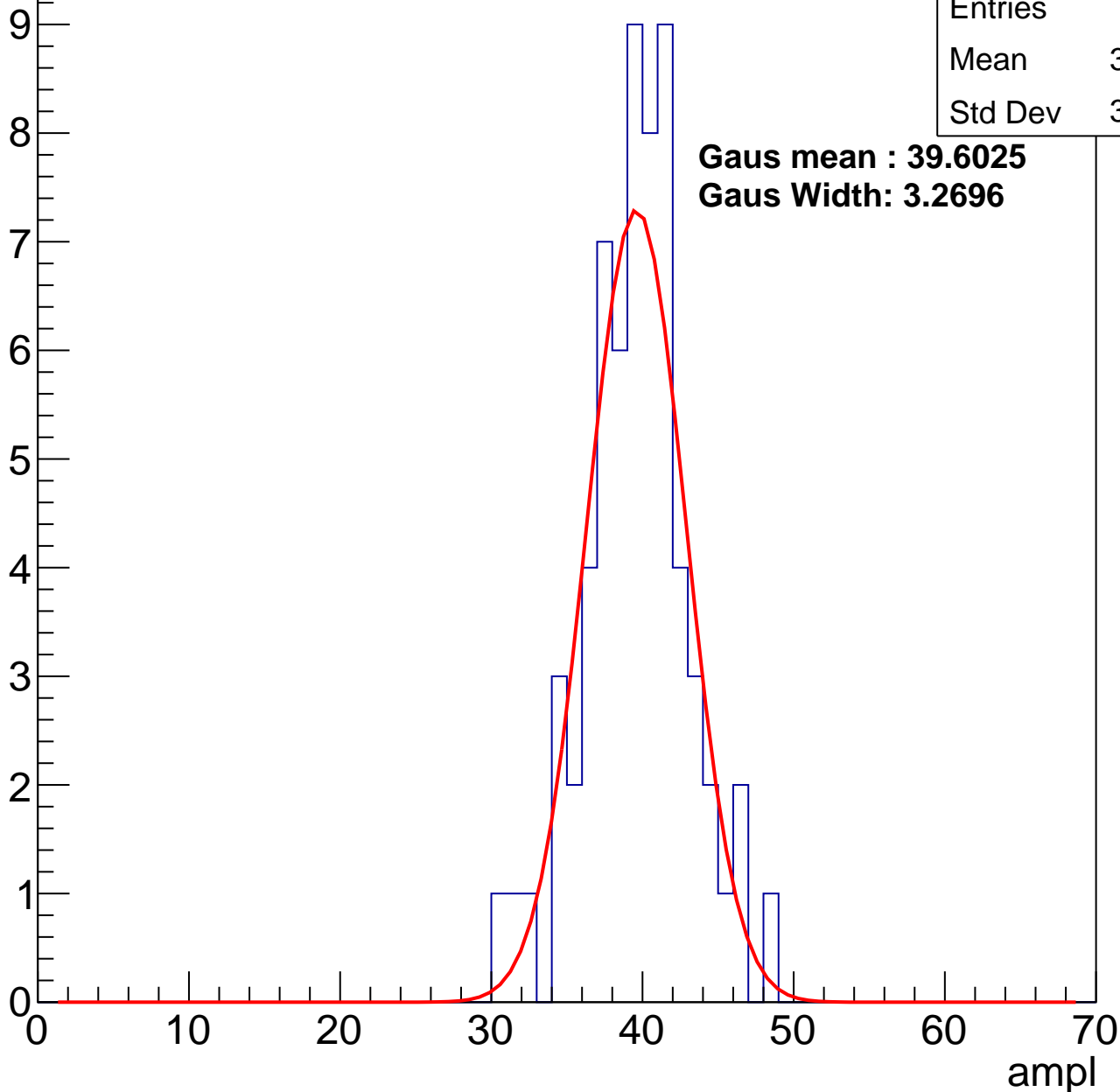
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	39.16
Std Dev	3.452

Gaus mean : 39.6025

Gaus Width: 3.2696



B0L002S, U12-ch126, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	46.16
Std Dev	3.352

Gaus mean : 46.6382

Gaus Width: 3.3280

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

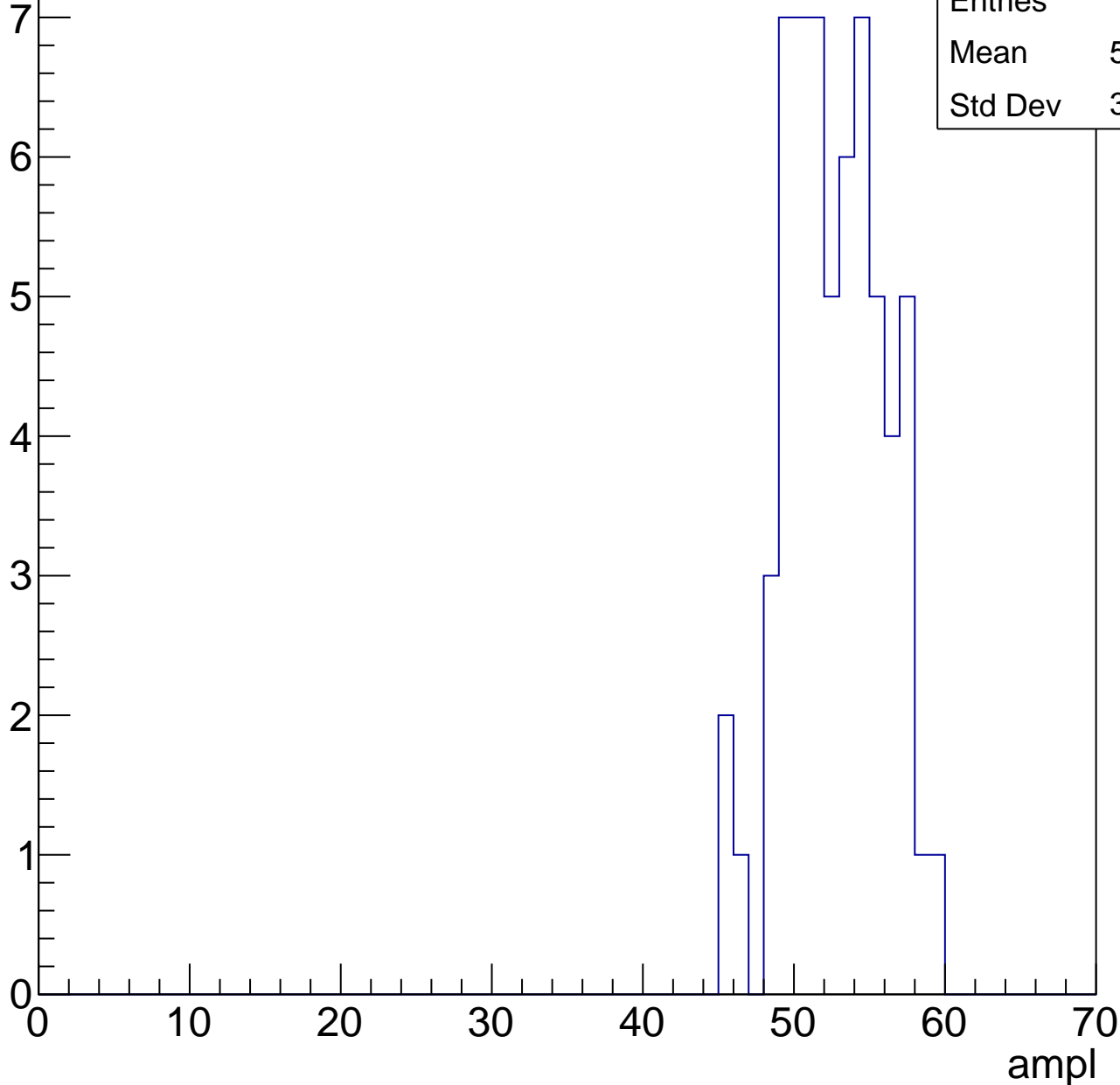
70

B0L002S, U12-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

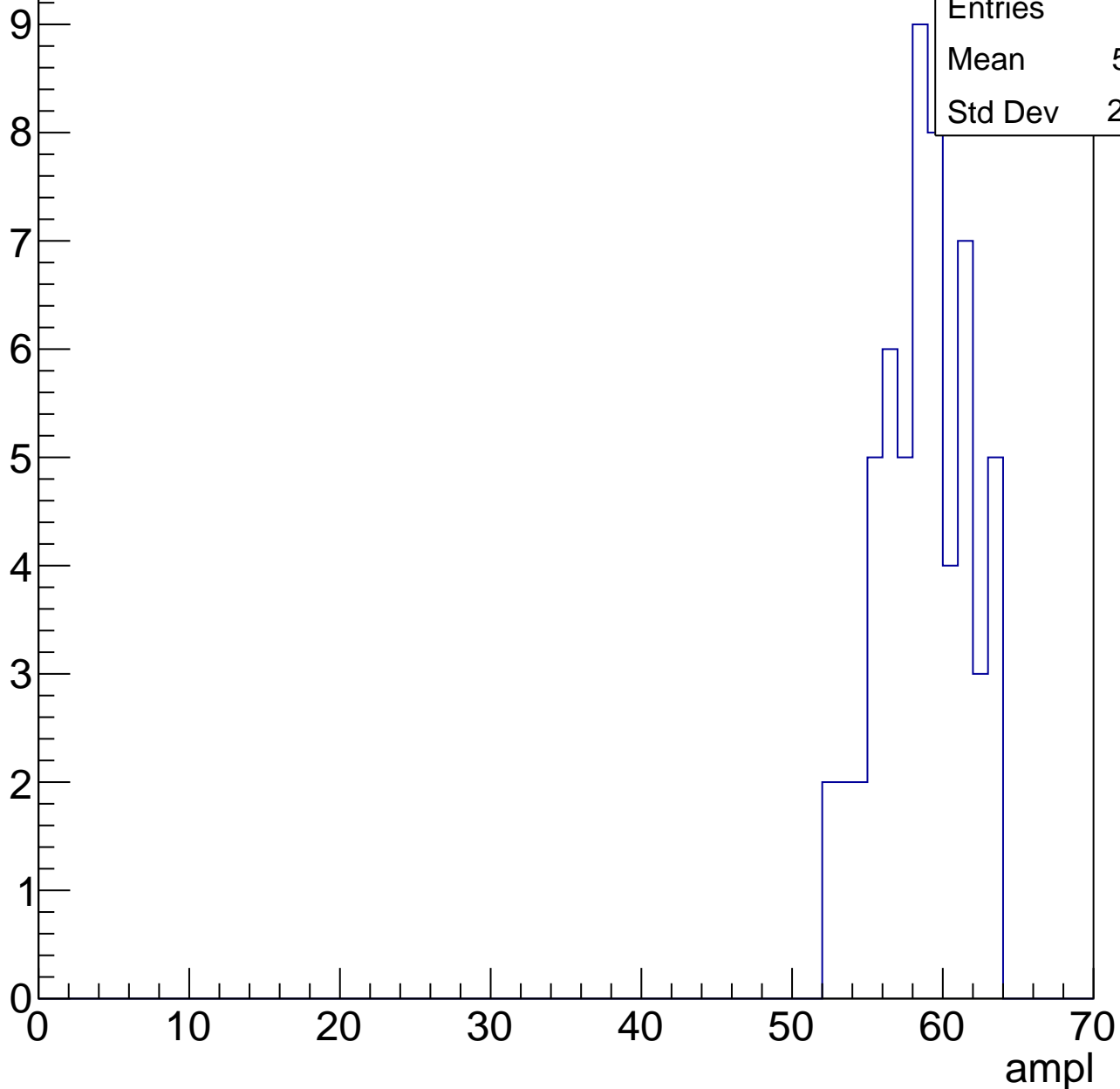
Entries	61
Mean	52.25
Std Dev	3.212



B0L002S, U12-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



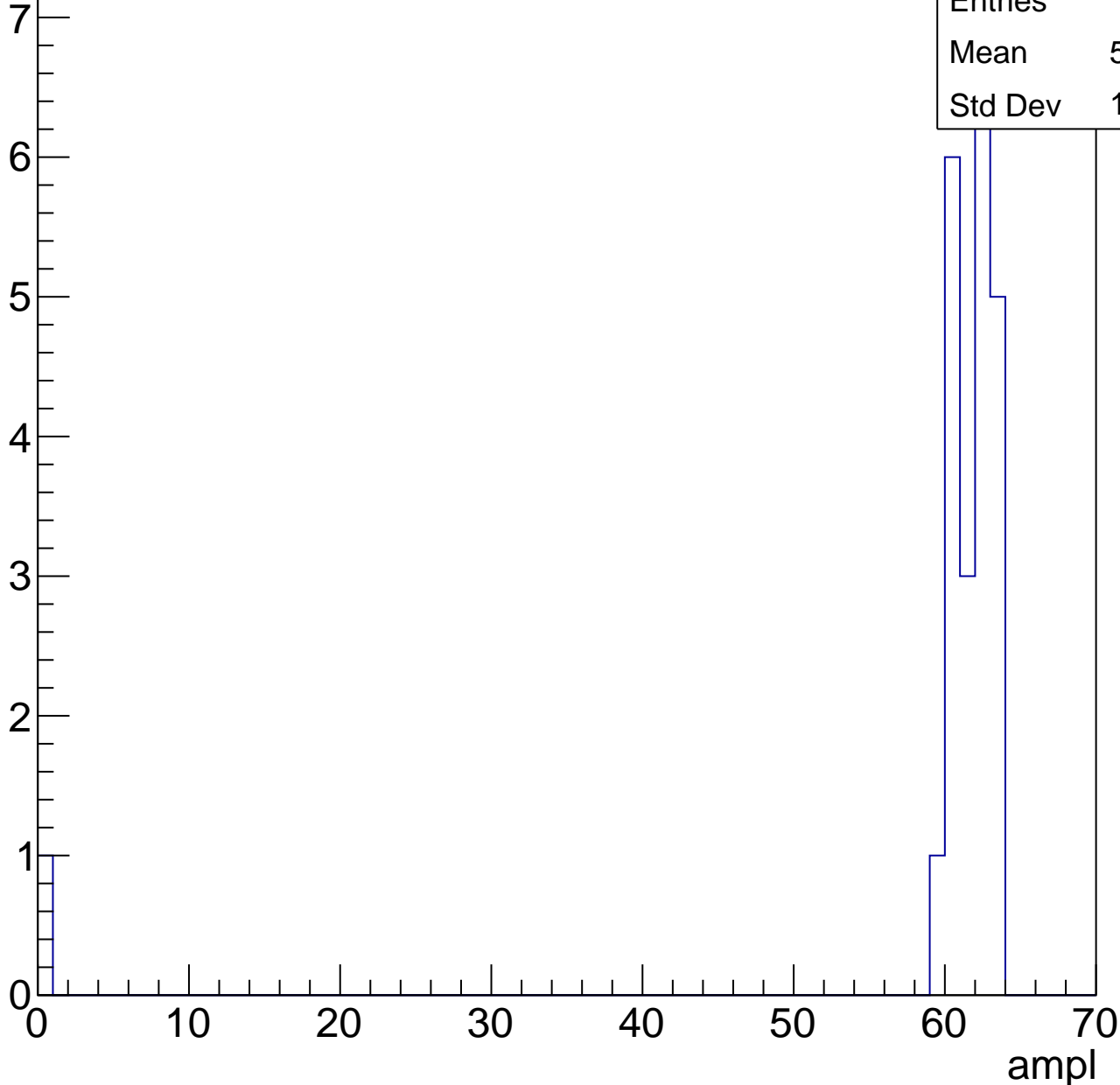
Entries	58
Mean	58.21
Std Dev	2.893

B0L002S, U12-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.74
Std Dev	12.58



B0L002S, U12-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch127, adc0

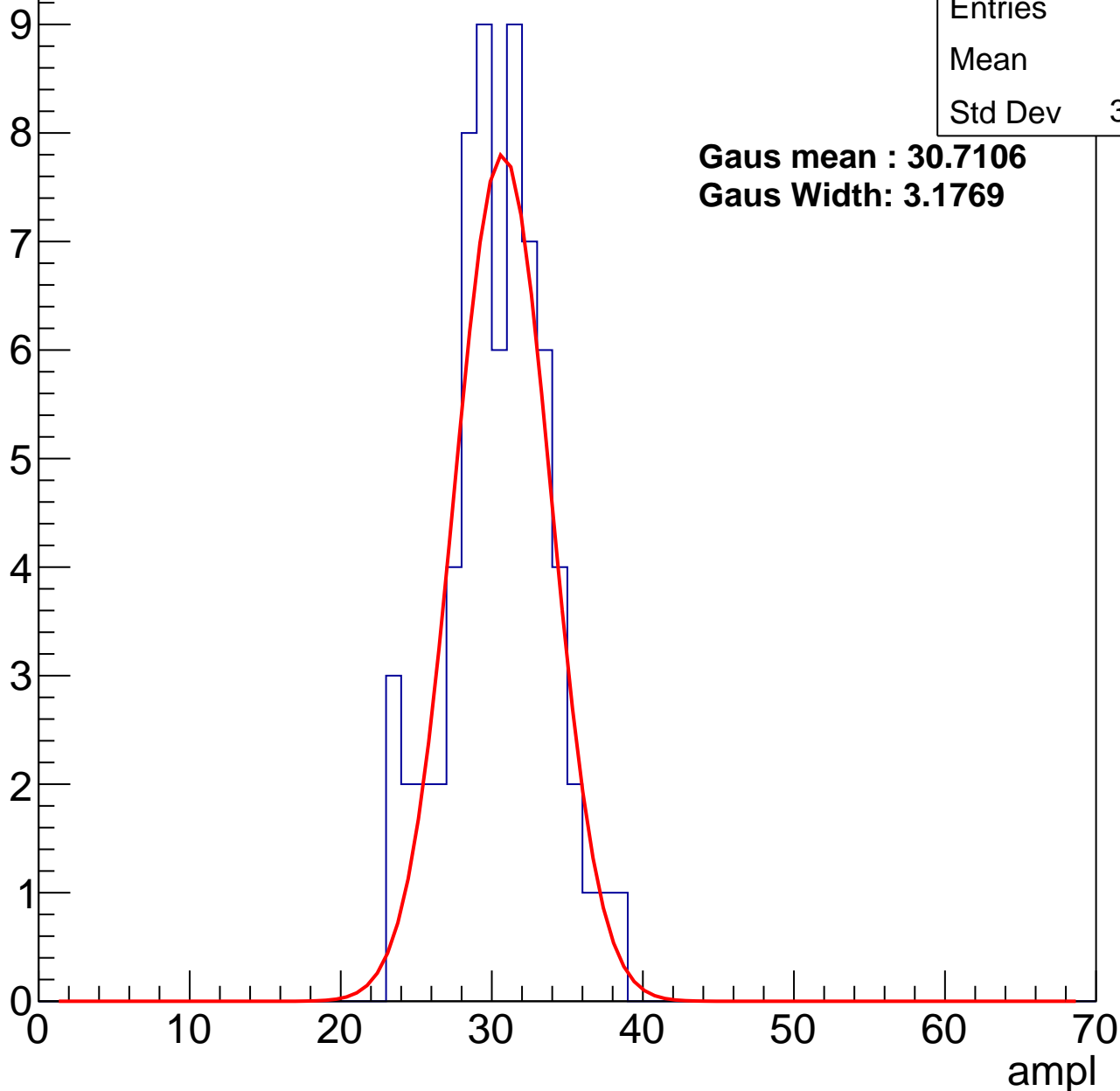
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30
Std Dev	3.305

Gaus mean : 30.7106

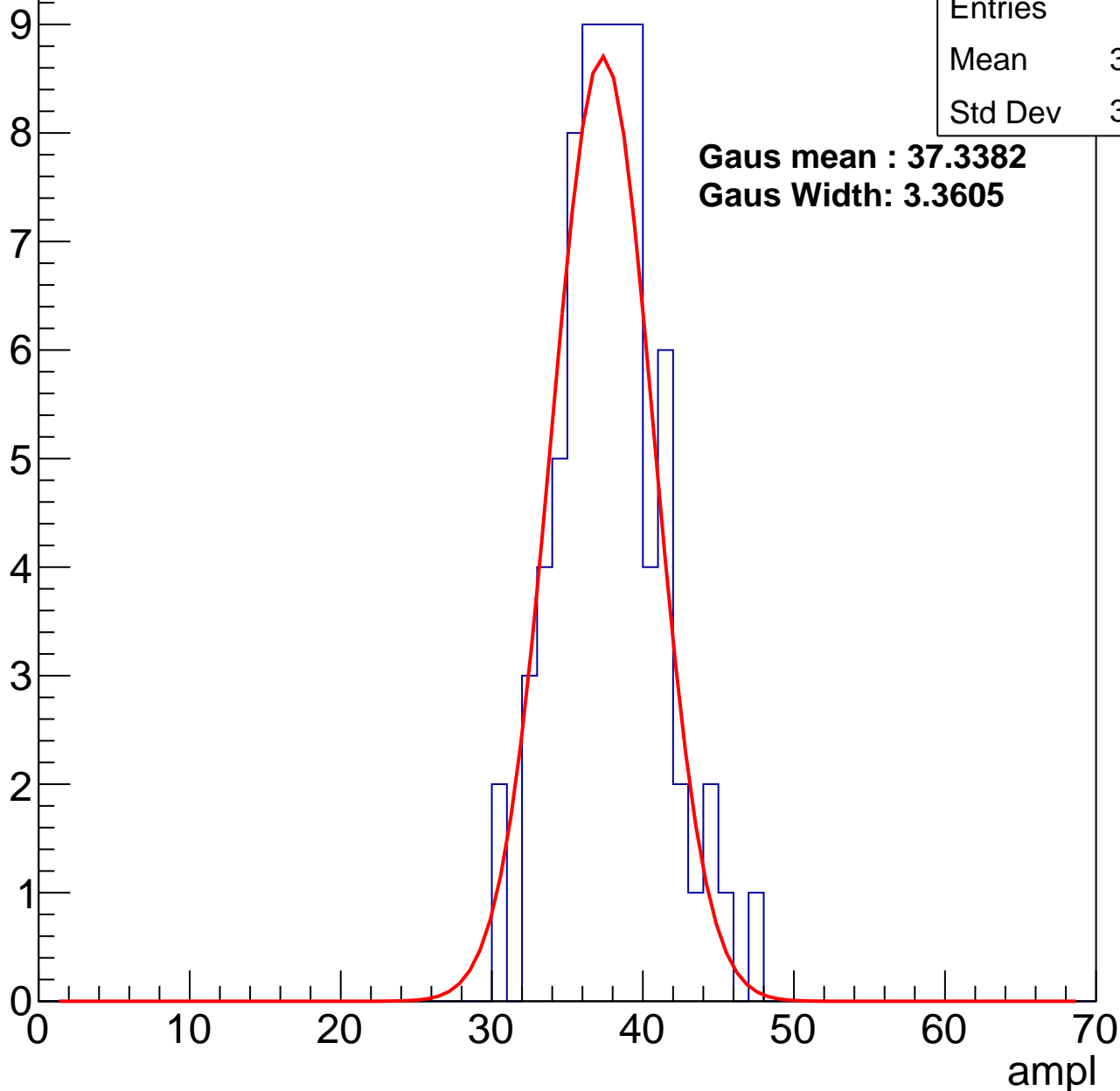
Gaus Width: 3.1769



B0L002S, U12-ch127, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch127, adc2

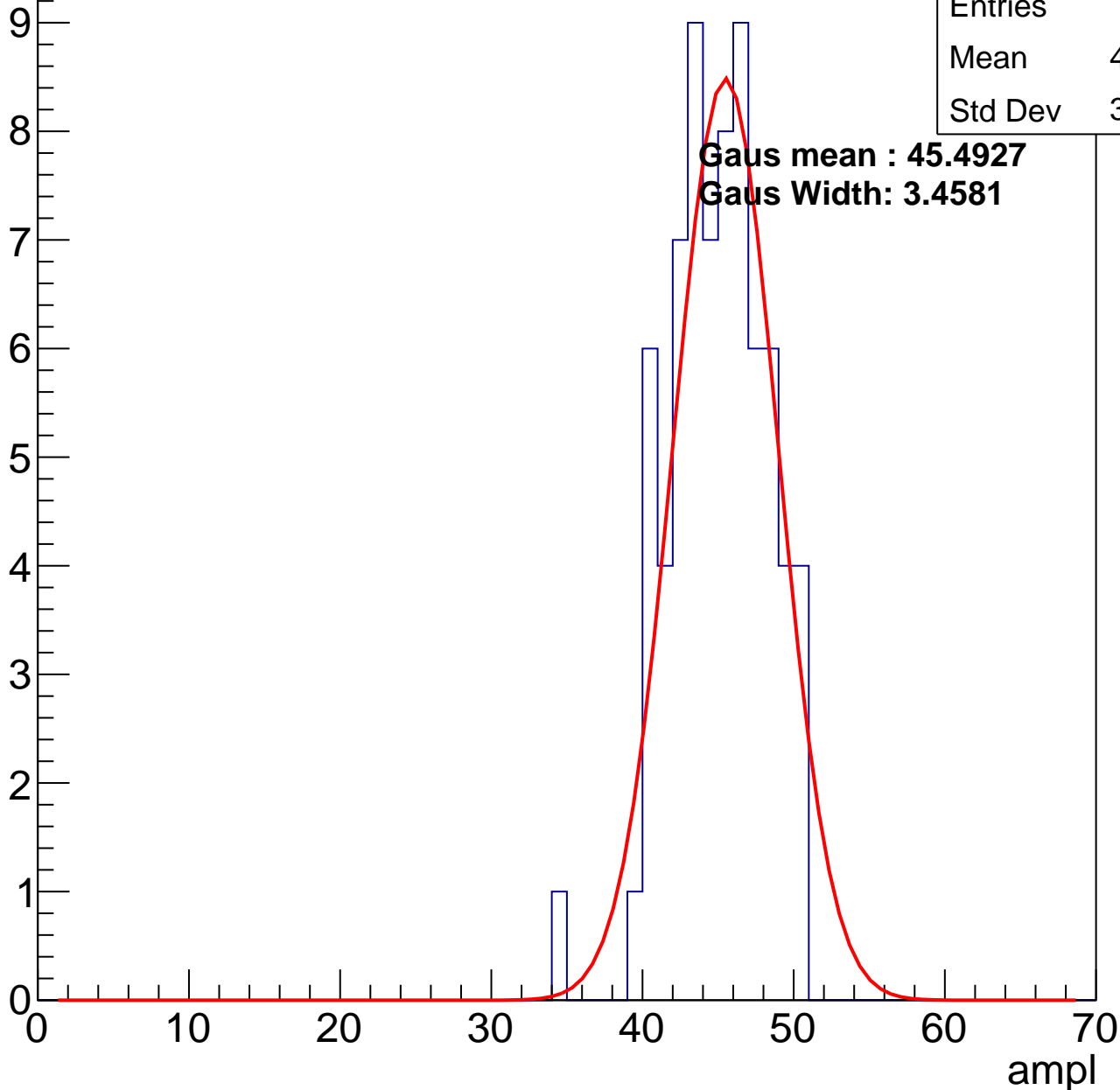
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	44.53
Std Dev	3.145

Gaus mean : 45.4927

Gaus Width: 3.4581

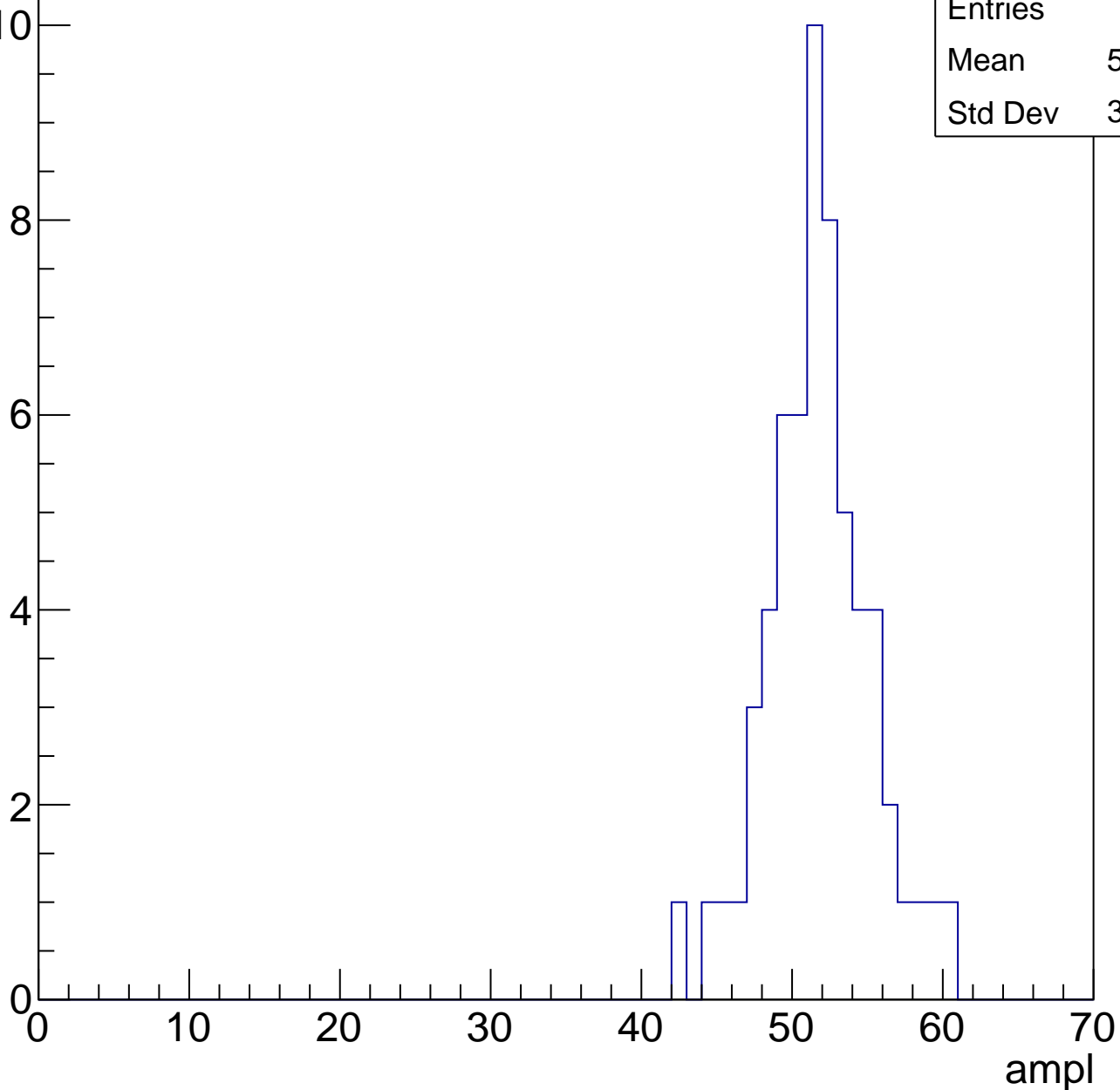


B0L002S, U12-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	51.28
Std Dev	3.436

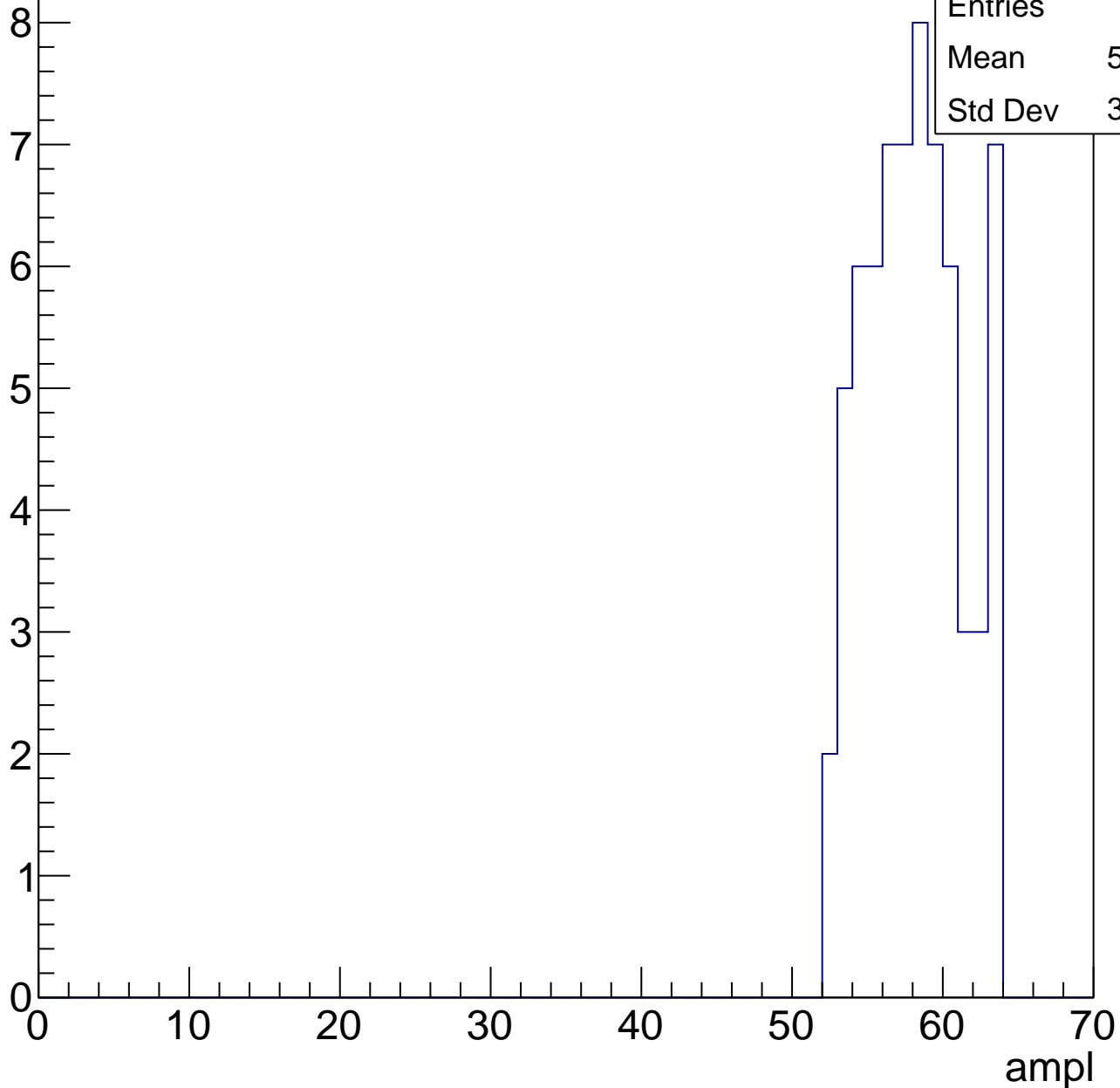


B0L002S, U12-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	57.63
Std Dev	3.124

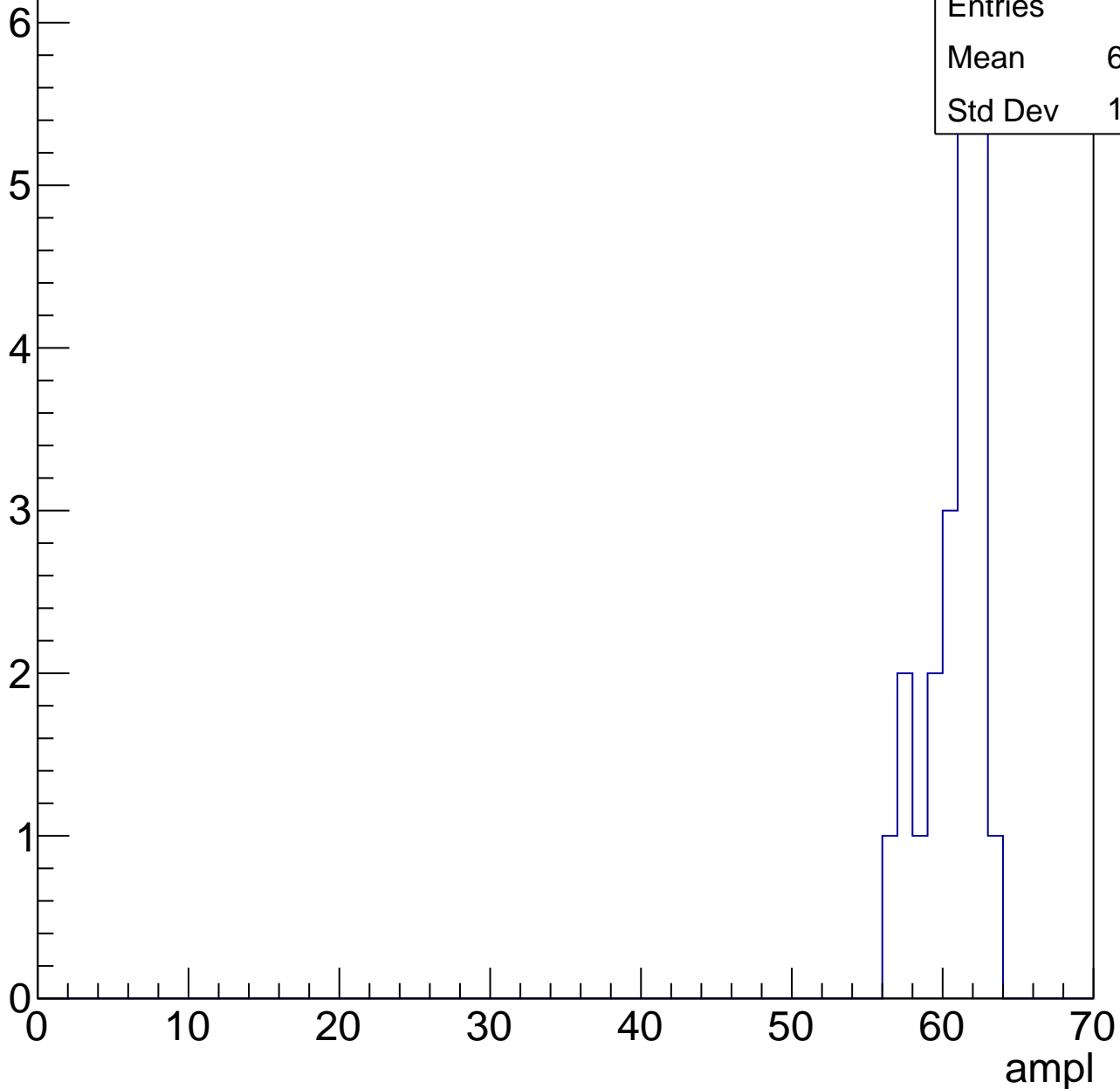


B0L002S, U12-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

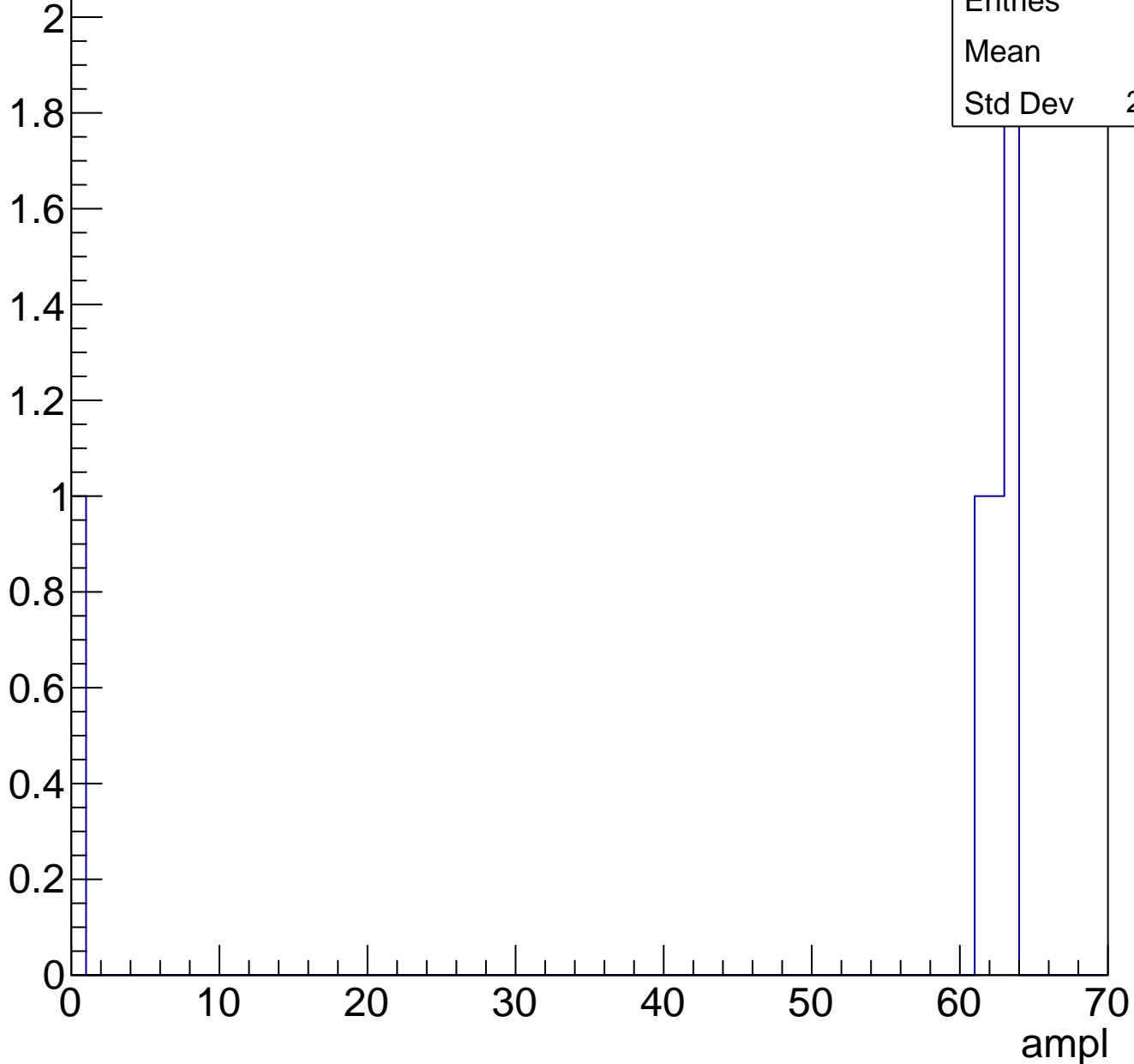
Entries	22
Mean	60.32
Std Dev	1.868



B0L002S, U12-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

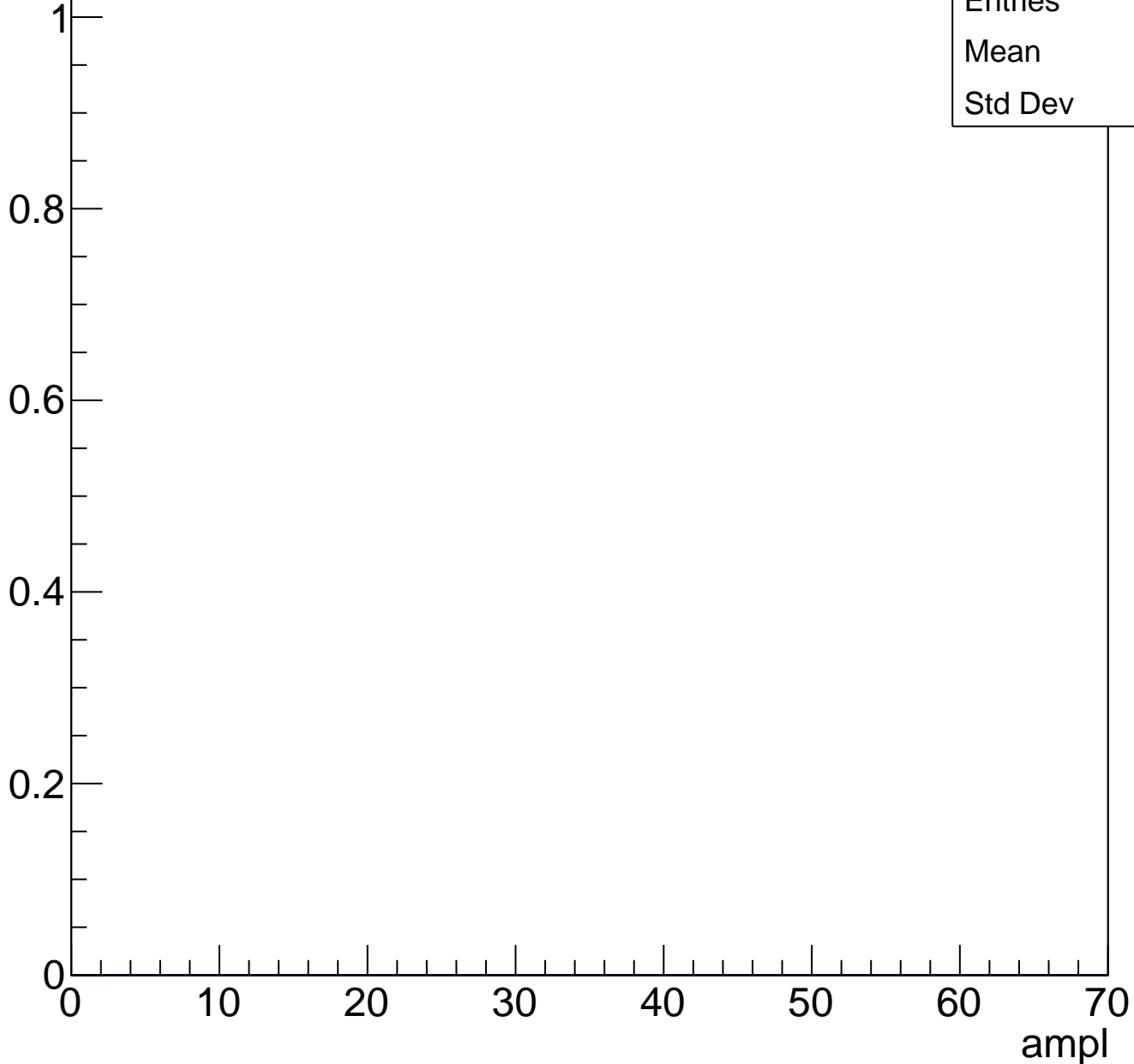


Entries	0
Mean	0
Std Dev	0

B0L002S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0