

B0L101S, U19-ch0, adc0

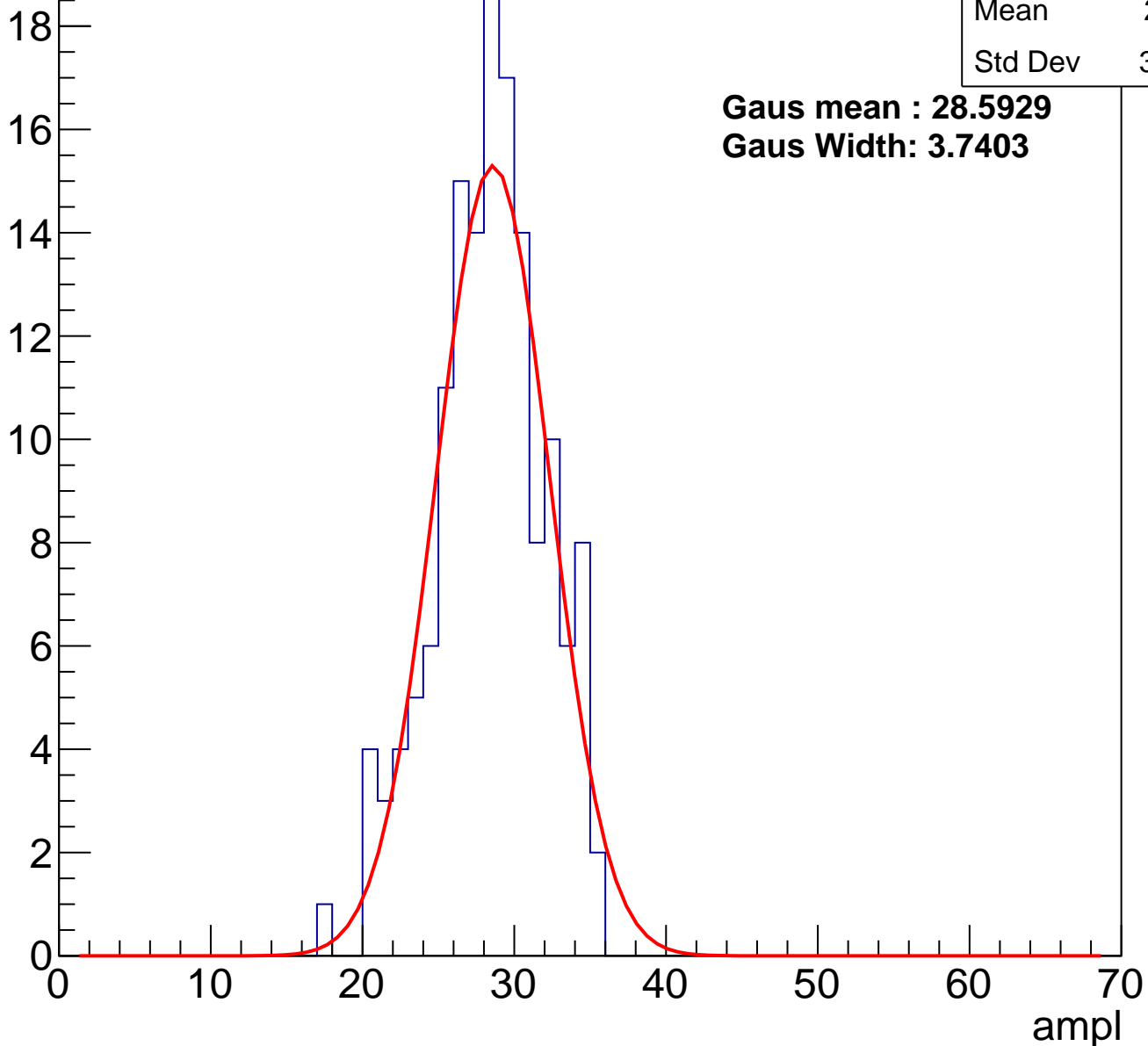
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	27.91
Std Dev	3.597

Gaus mean : 28.5929

Gaus Width: 3.7403

Entry



B0L101S, U19-ch0, adc1

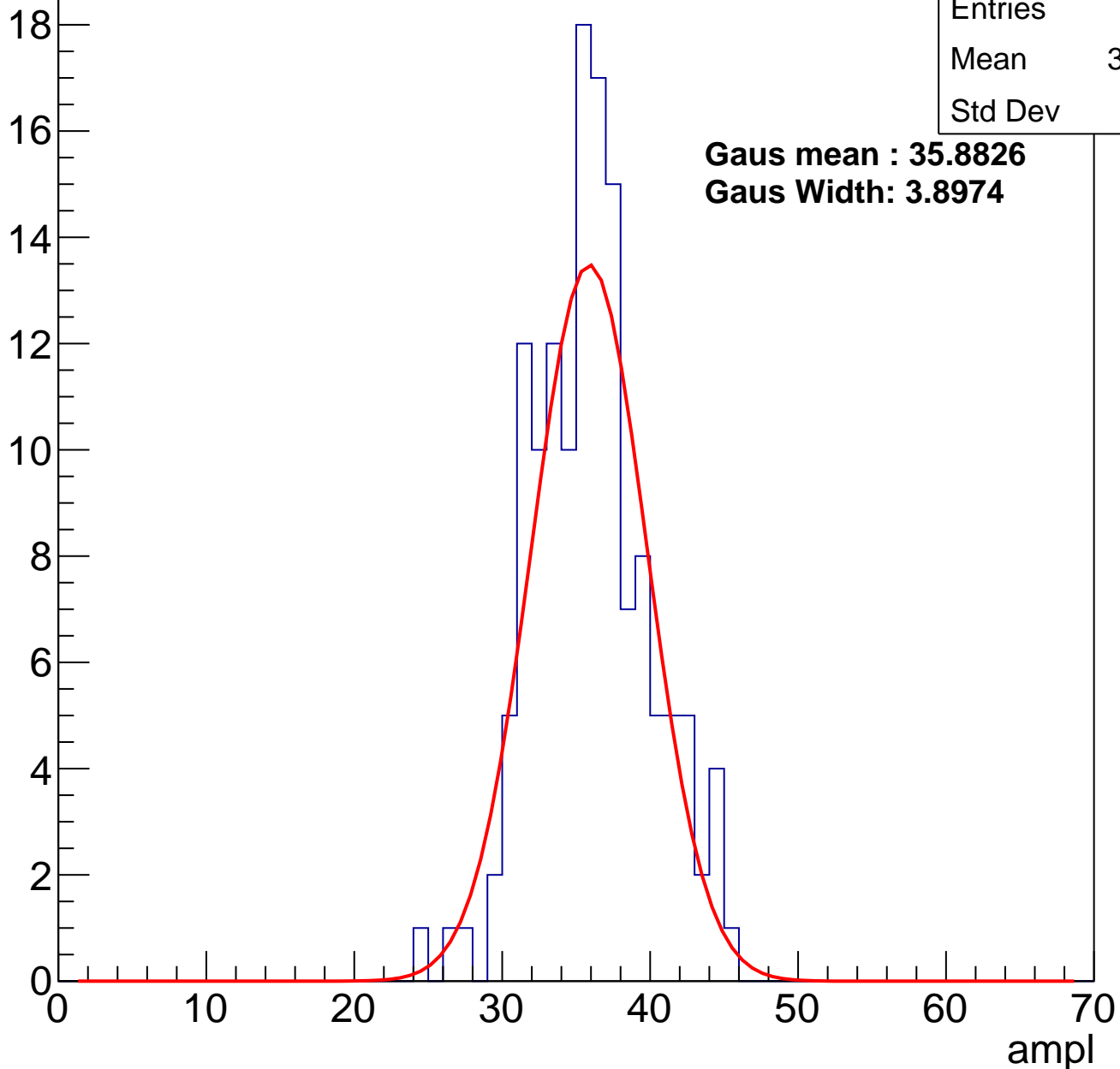
calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	35.53
Std Dev	3.89

Gaus mean : 35.8826

Gaus Width: 3.8974

Entry



B0L101S, U19-ch0, adc2

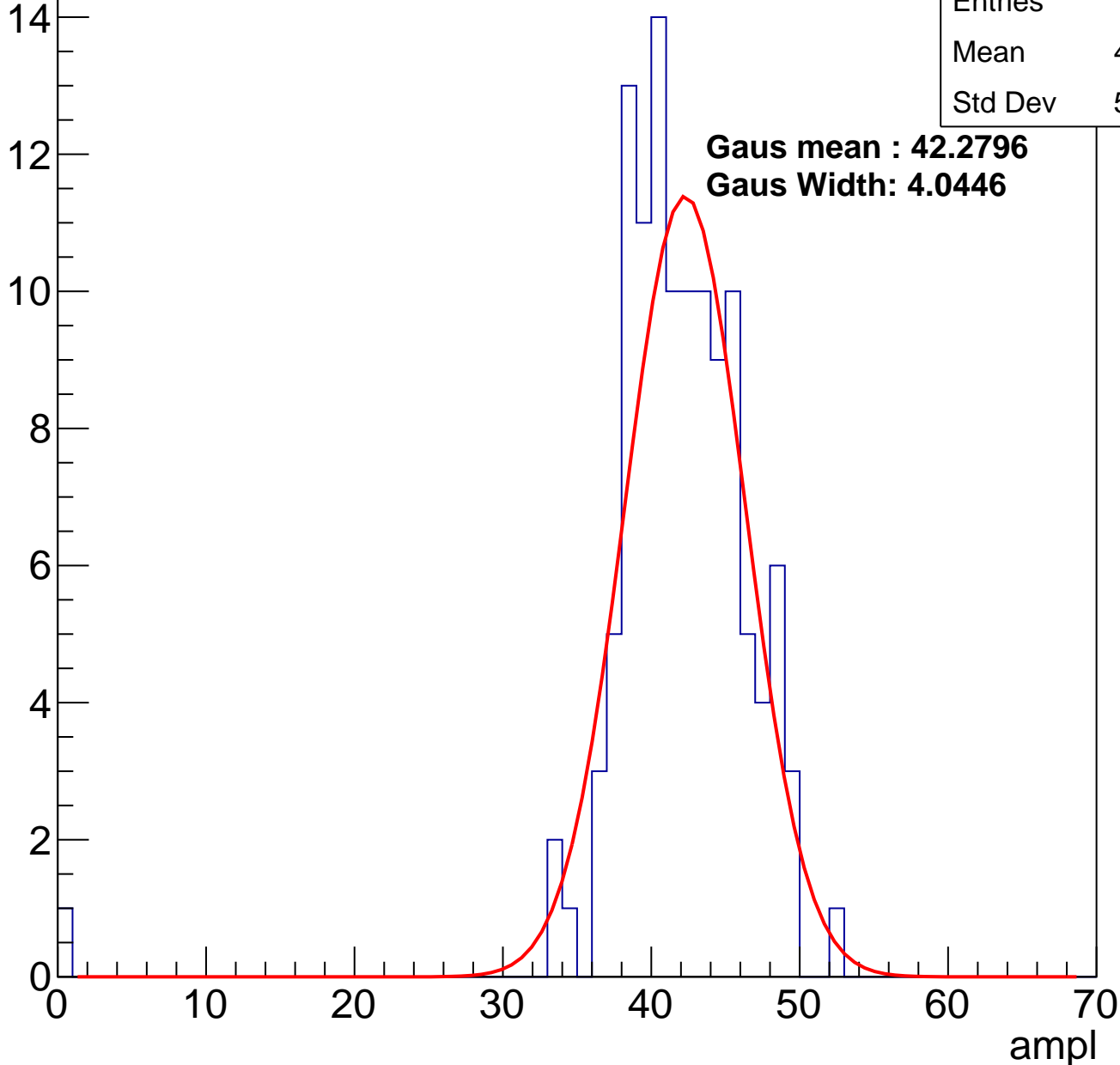
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	41.42
Std Dev	5.316

Gaus mean : 42.2796

Gaus Width: 4.0446

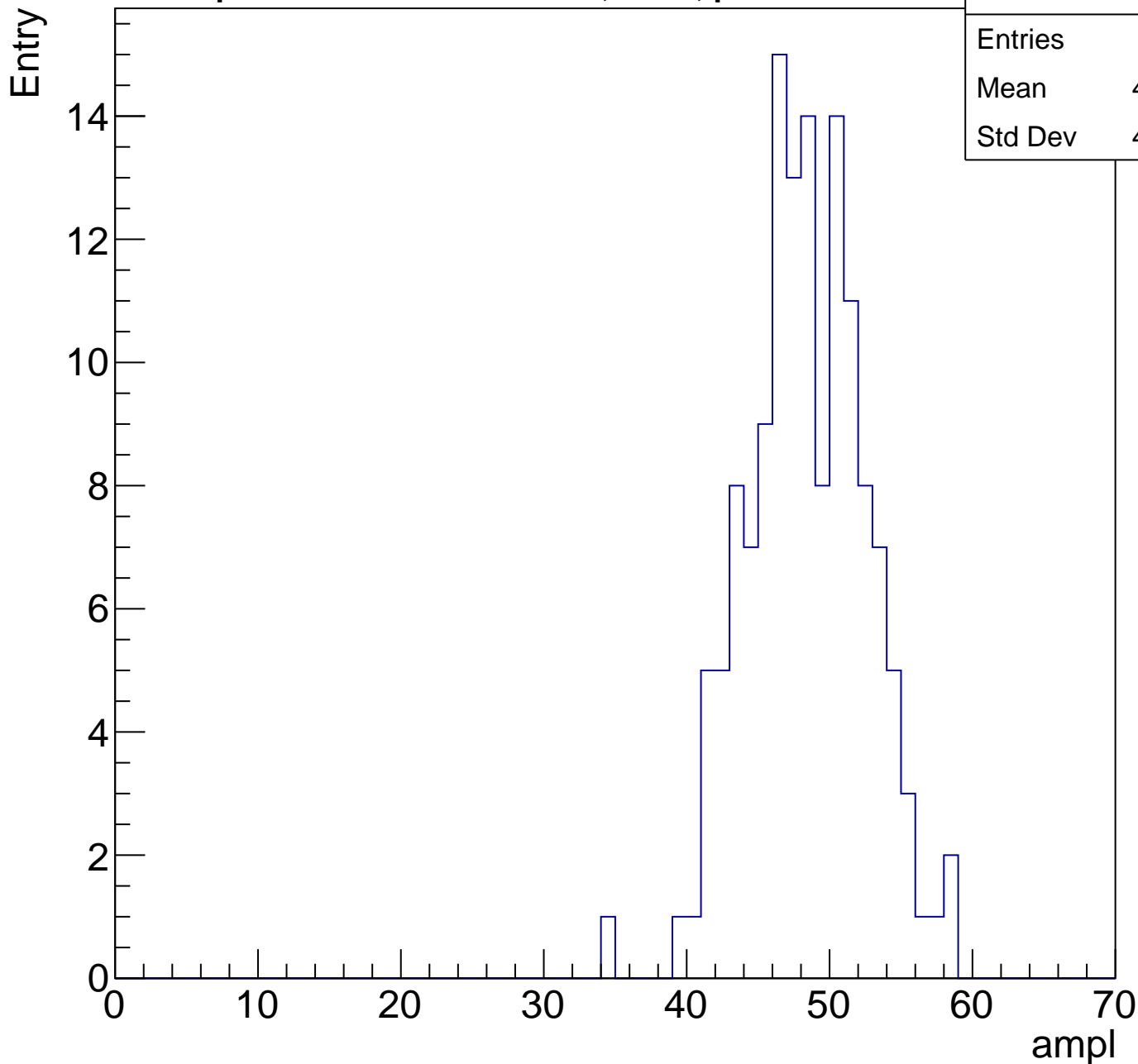
Entry



B0L101S, U19-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	47.93
Std Dev	4.129



B0L101S, U19-ch0, adc4

calib_packv5_042523_0143.root, FC#1, port C1

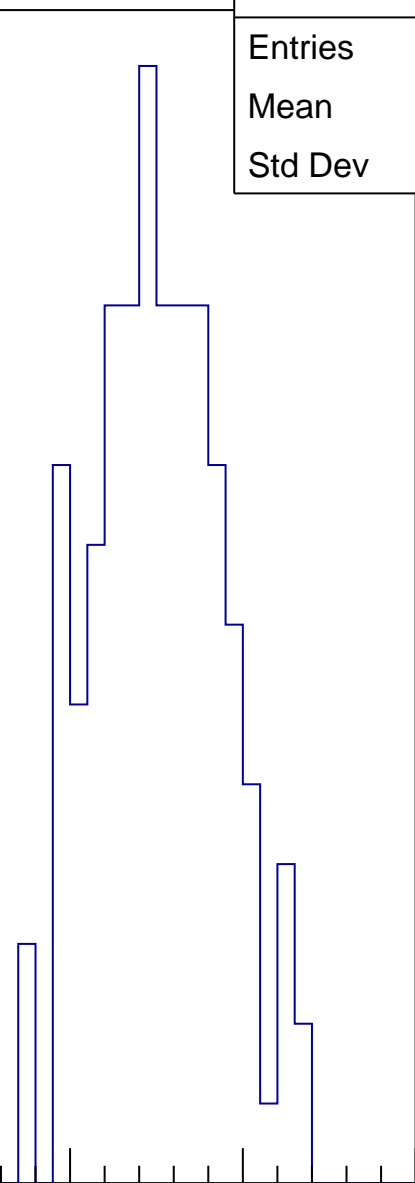
Entries	123
Mean	54.63
Std Dev	3.681

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch0, adc5

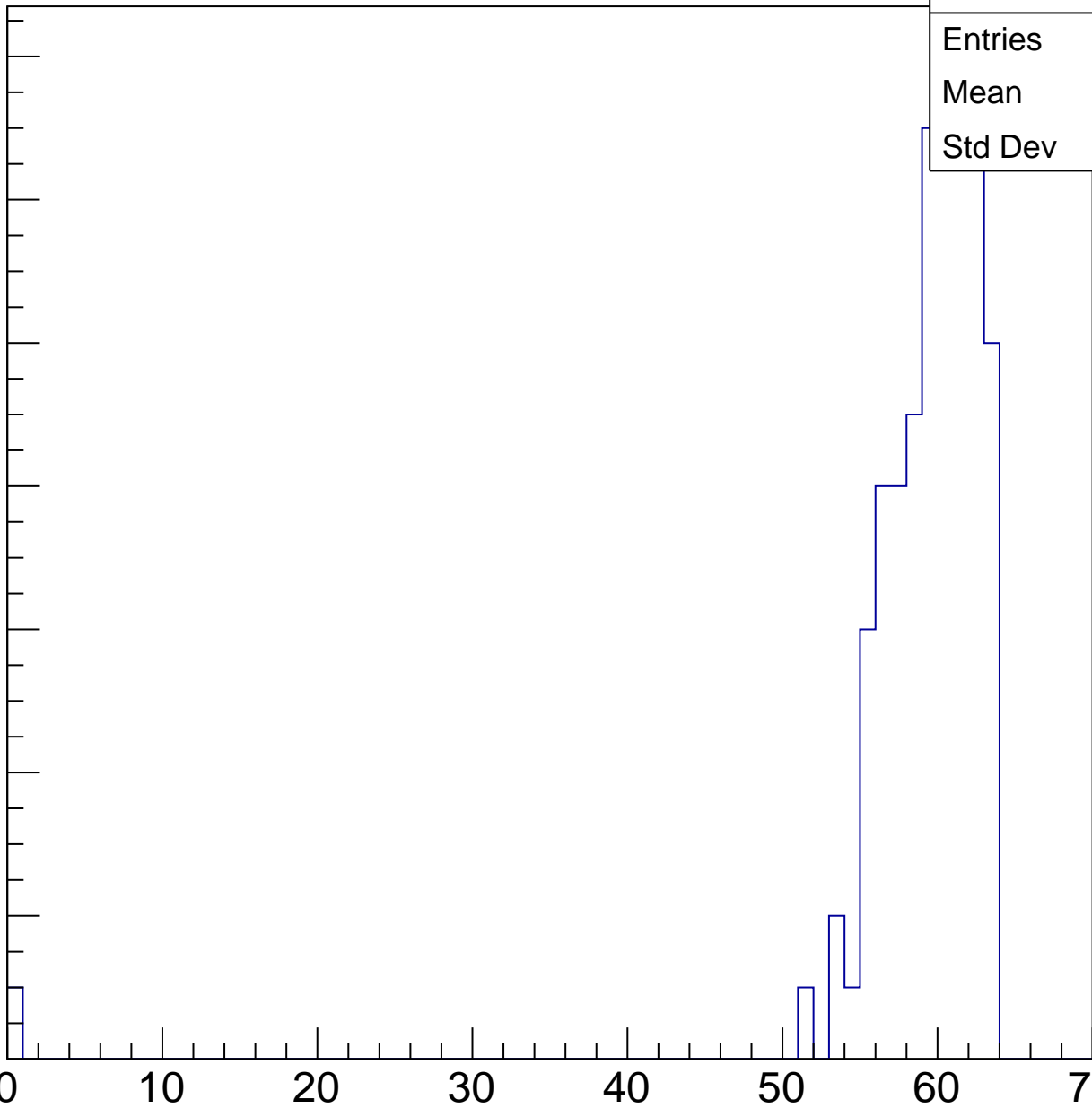
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	58.63
Std Dev	6.5

ampl

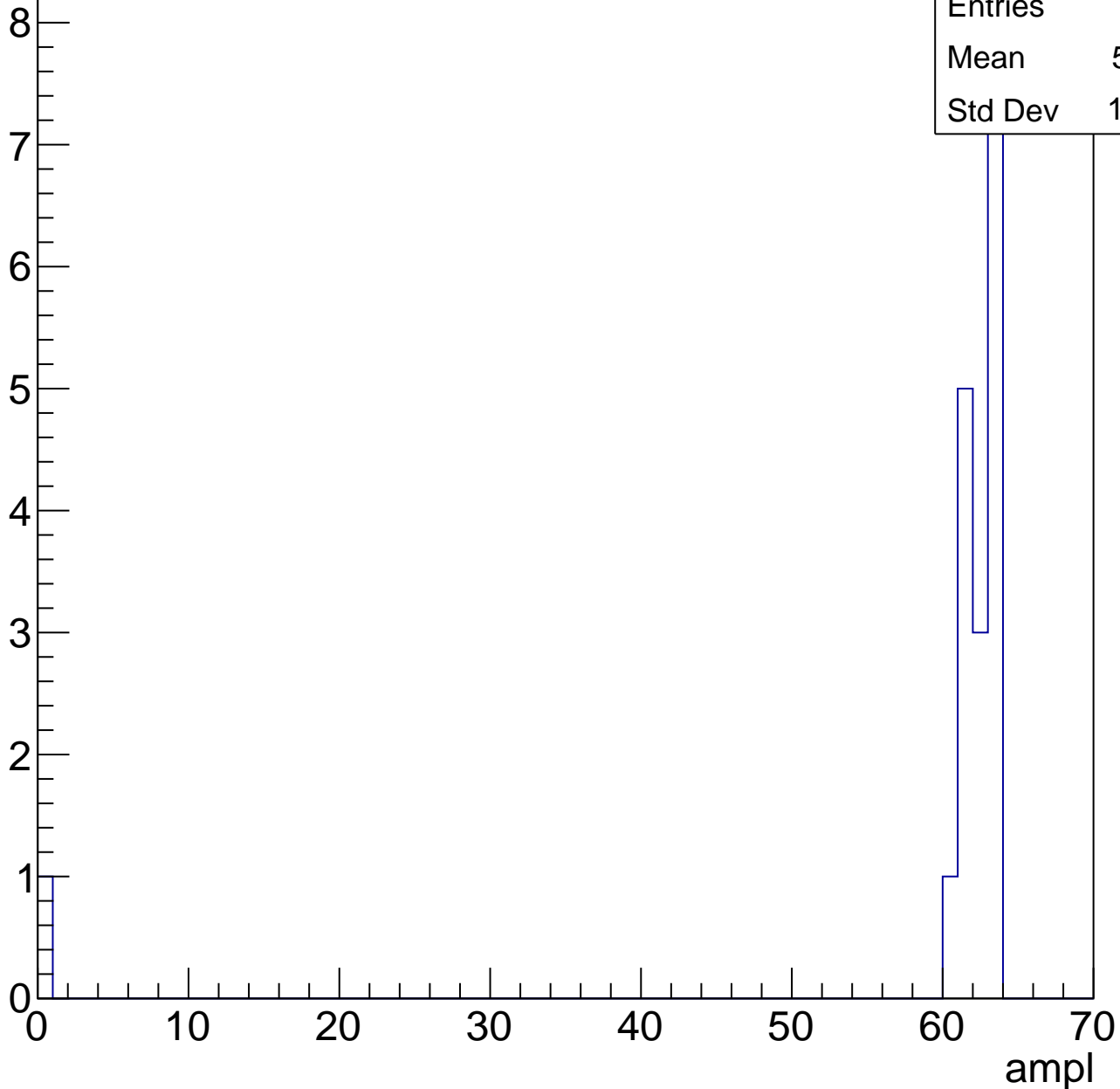


B0L101S, U19-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

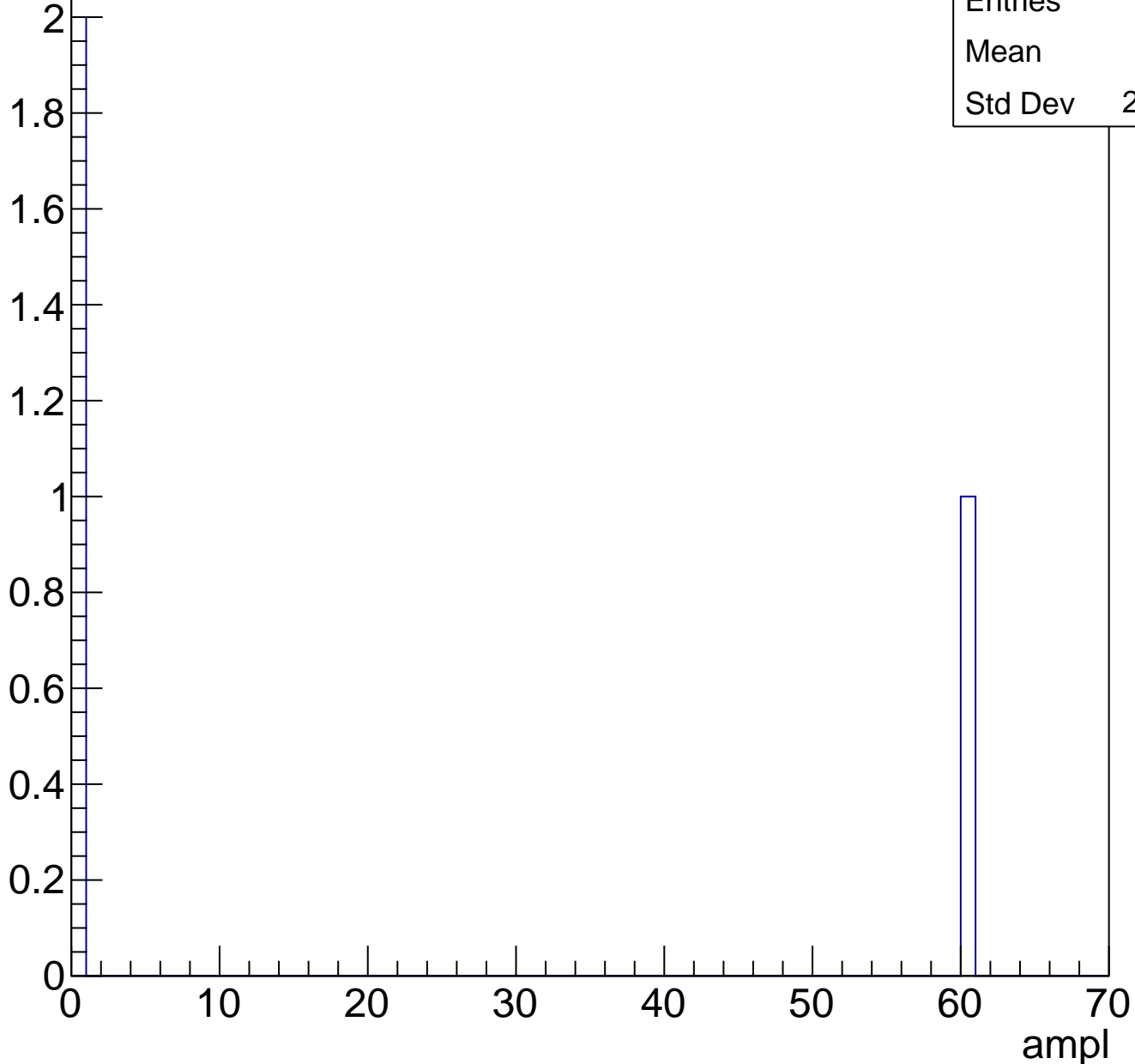
Entries	18
Mean	58.61
Std Dev	14.25



B0L101S, U19-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

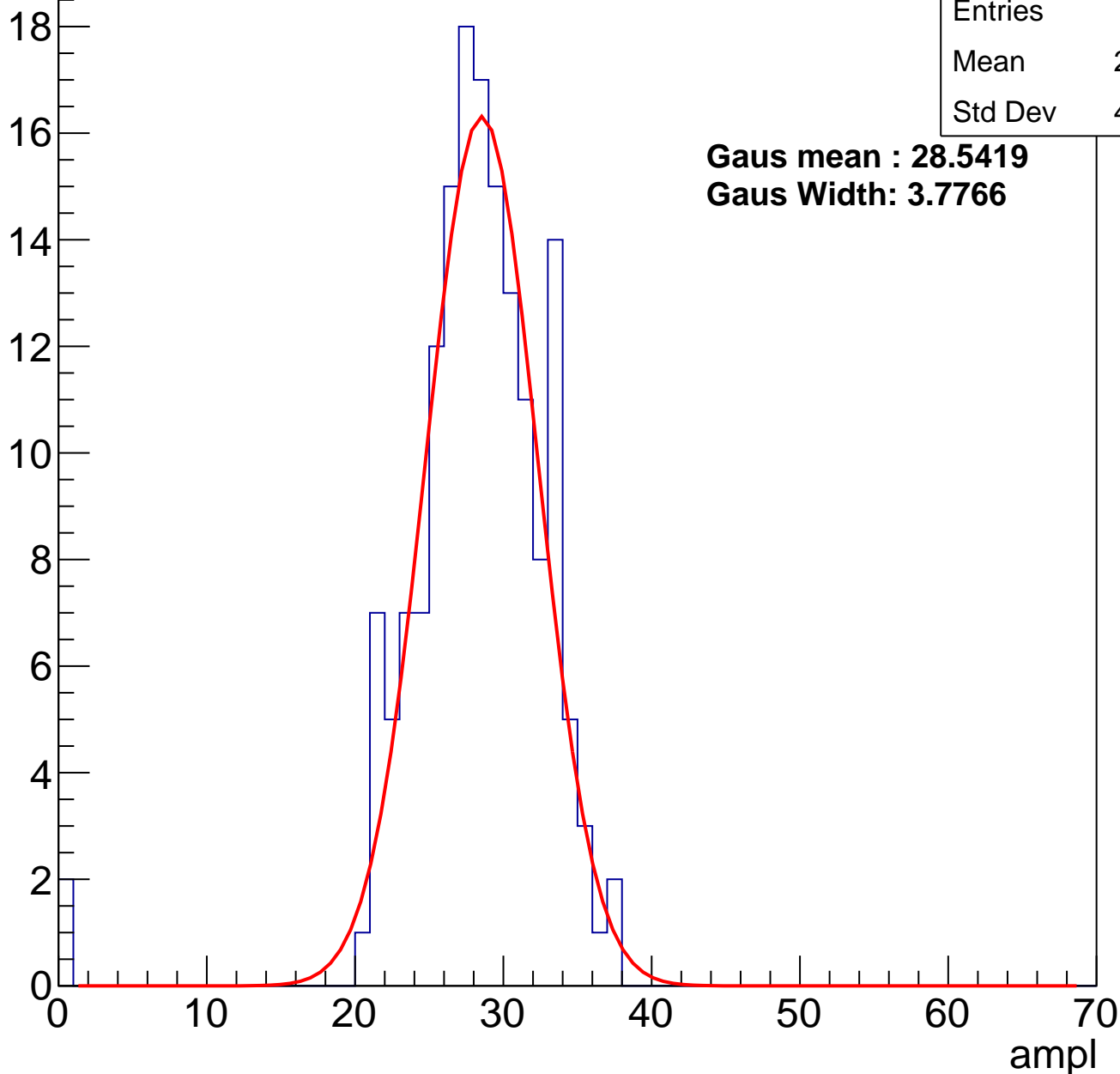


Entries	3
Mean	20
Std Dev	28.28

B0L101S, U19-ch1, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch1, adc1

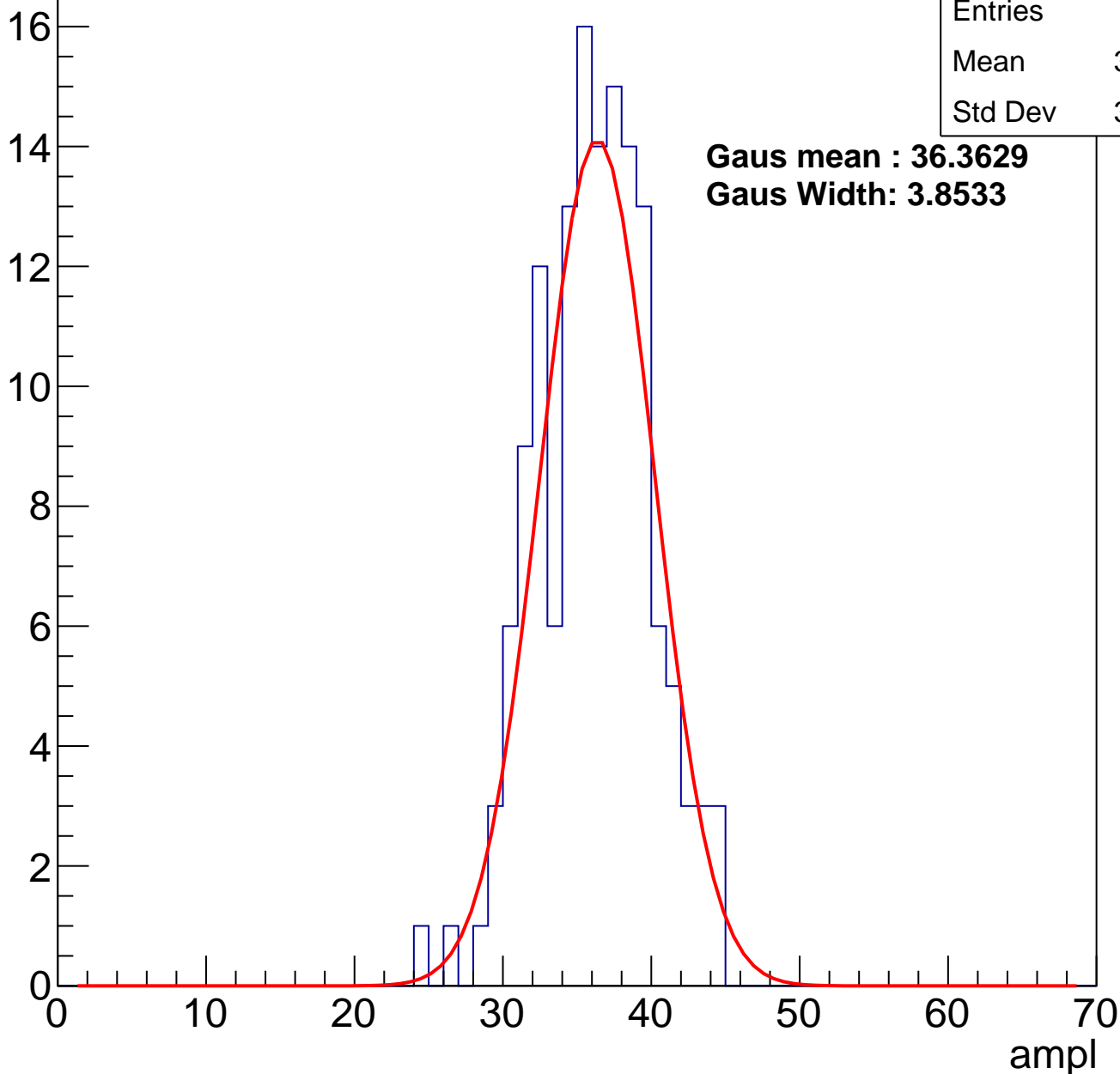
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	35.68
Std Dev	3.774

Gaus mean : 36.3629

Gaus Width: 3.8533

Entry



B0L101S, U19-ch1, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

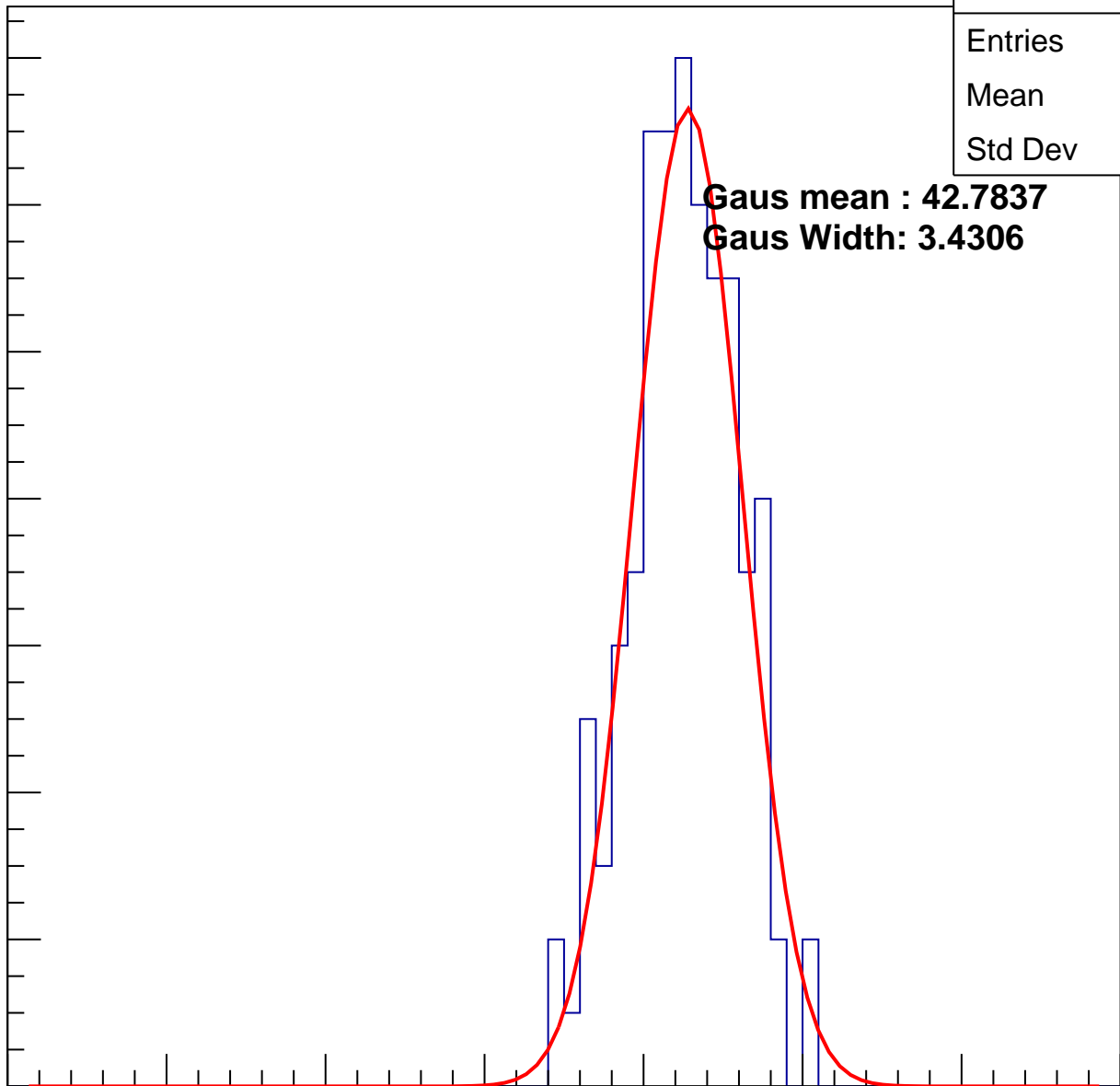
14
12
10
8
6
4
2
0

Entries	117
Mean	42.09
Std Dev	3.361

Gaus mean : 42.7837
Gaus Width: 3.4306

ampl

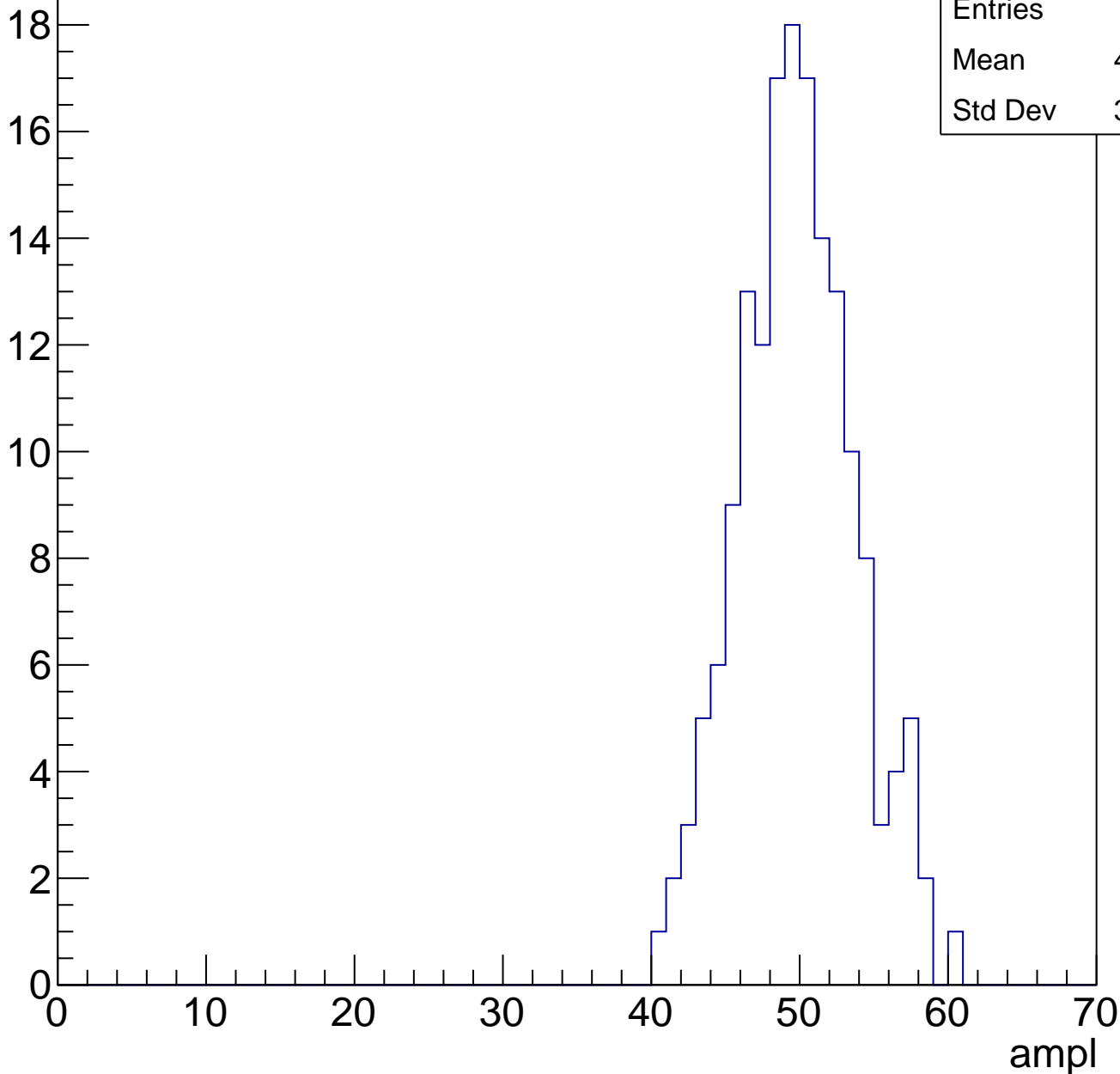
0 10 20 30 40 50 60 70



B0L101S, U19-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch1, adc4

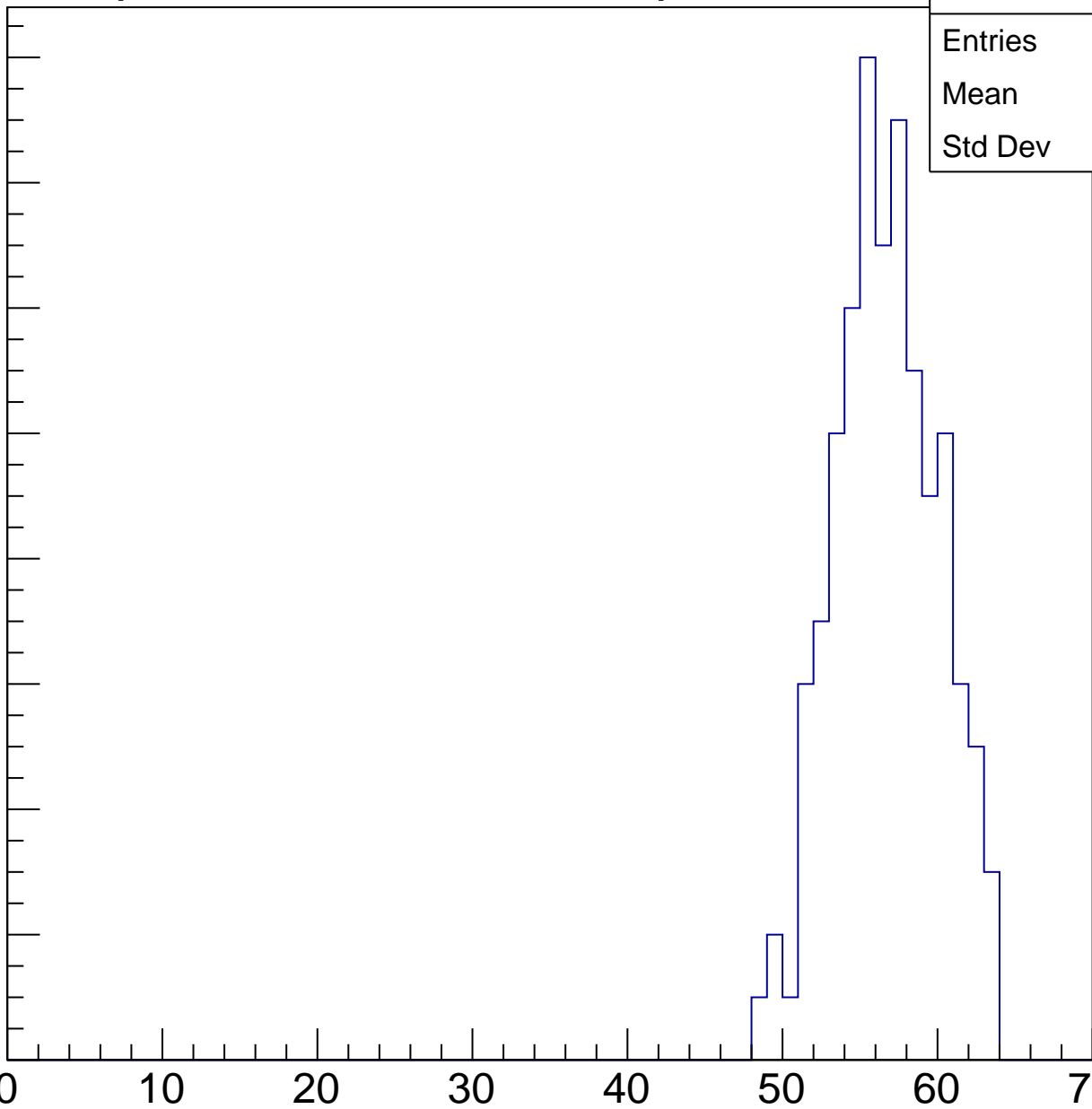
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	56.23
Std Dev	3.302

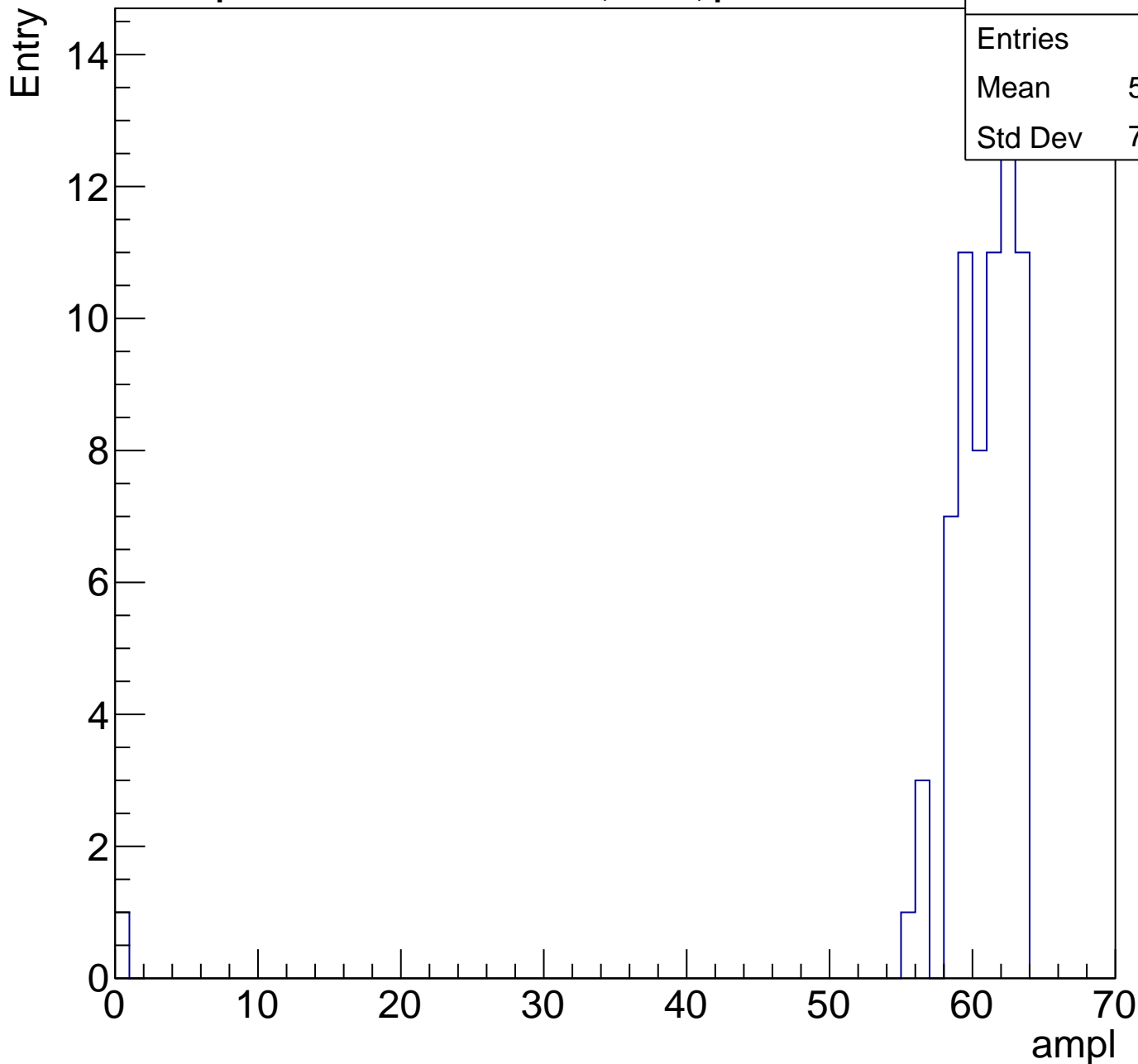
ampl



B0L101S, U19-ch1, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	67
Mean	59.55
Std Dev	7.595

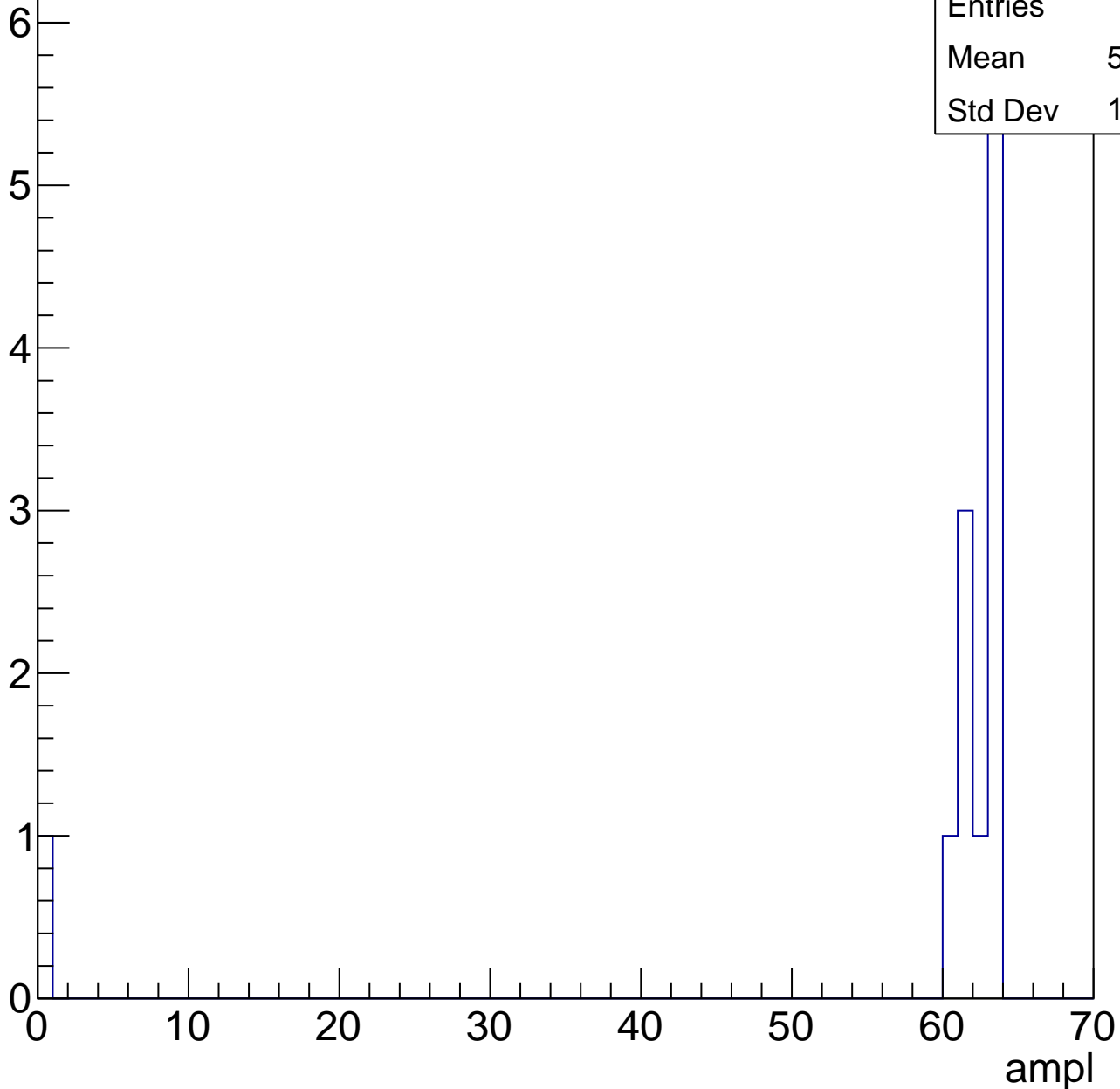


B0L101S, U19-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.92
Std Dev	17.19



B0L101S, U19-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

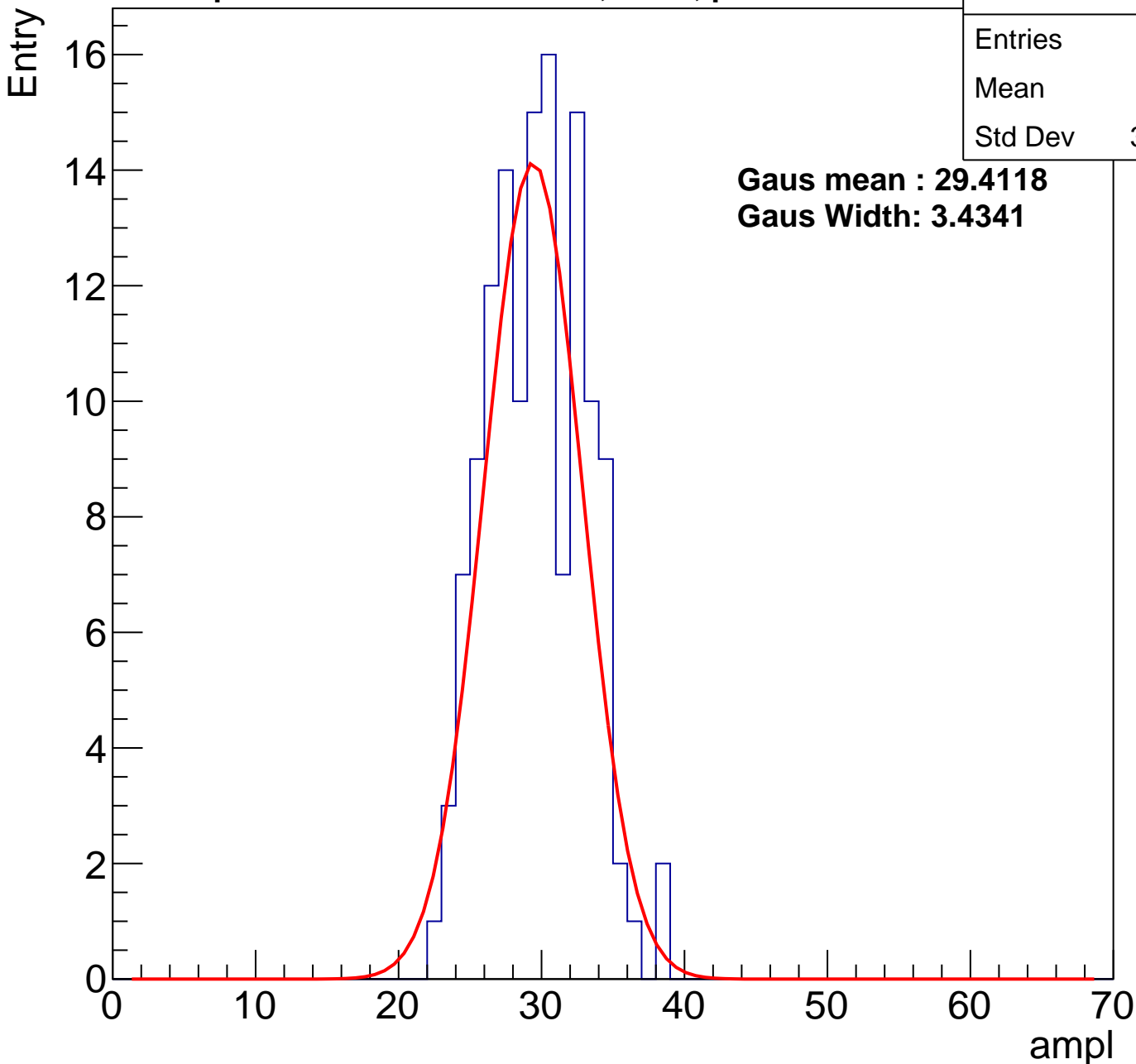
B0L101S, U19-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	29.2
Std Dev	3.349

Gaus mean : 29.4118

Gaus Width: 3.4341



B0L101S, U19-ch2, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	35.34
Std Dev	4.881

Gaus mean : 36.2213

Gaus Width: 3.4521

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

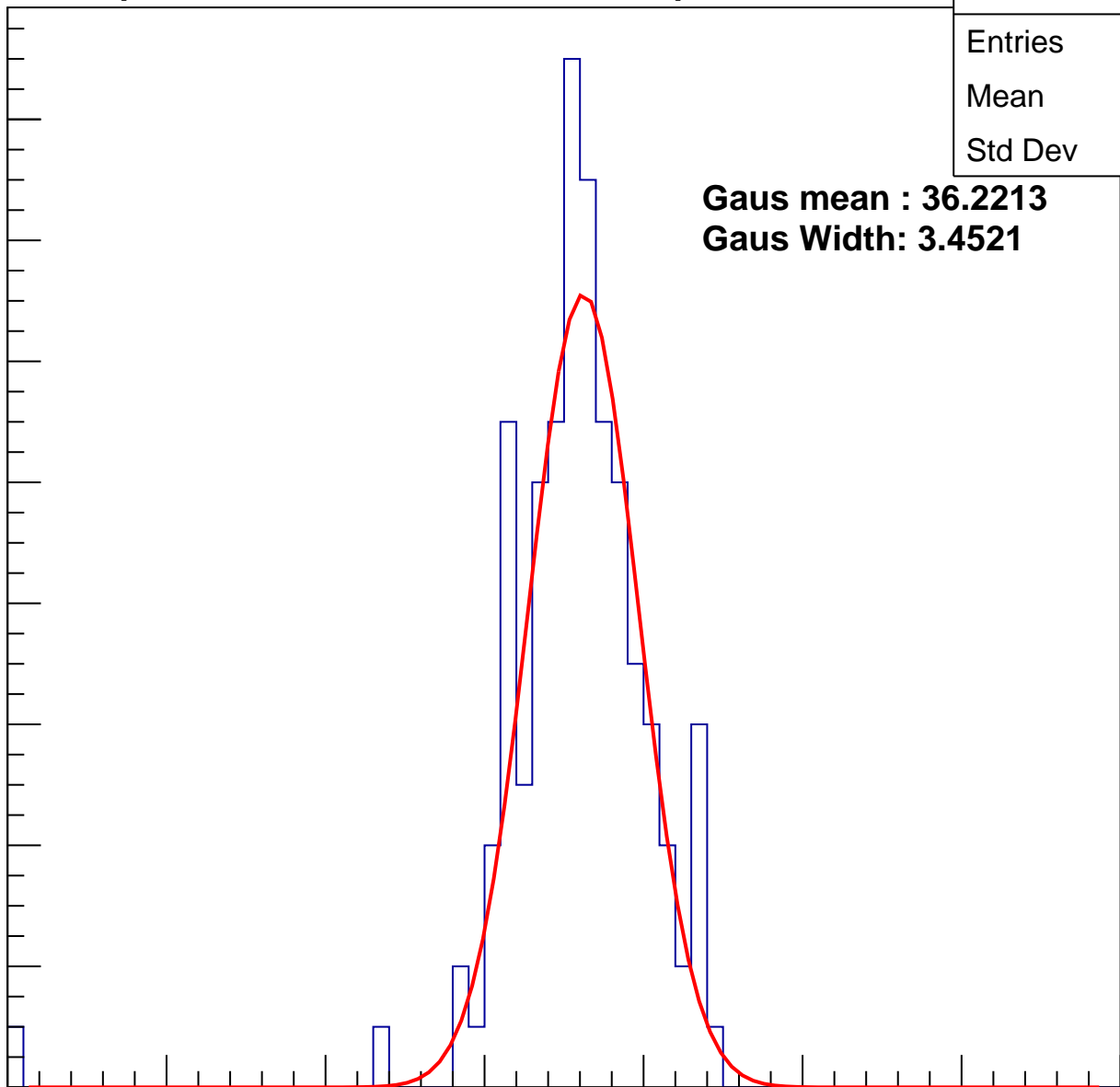
40

50

60

70

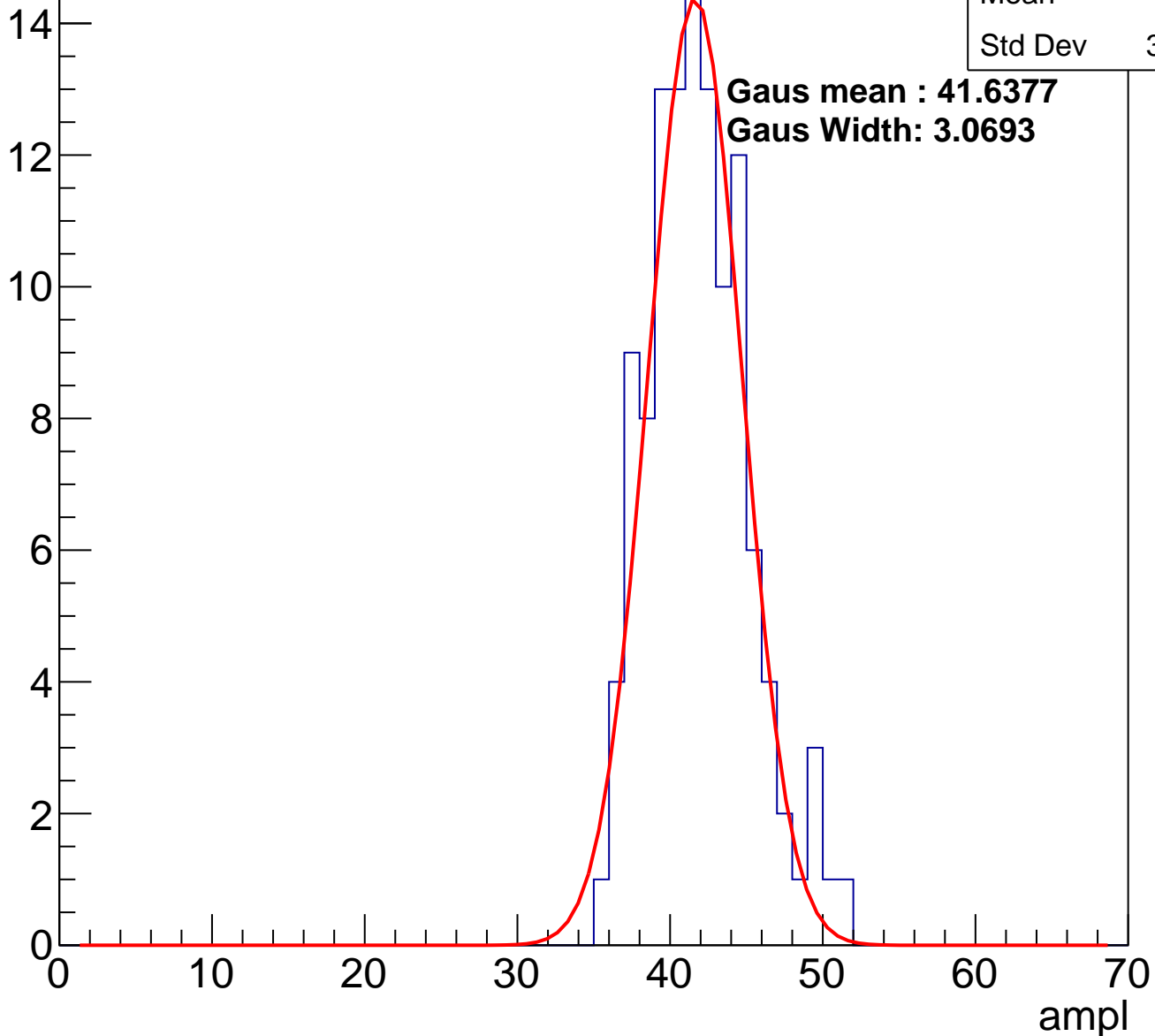
ampl



B0L101S, U19-ch2, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	116
Mean	41.43
Std Dev	3.278

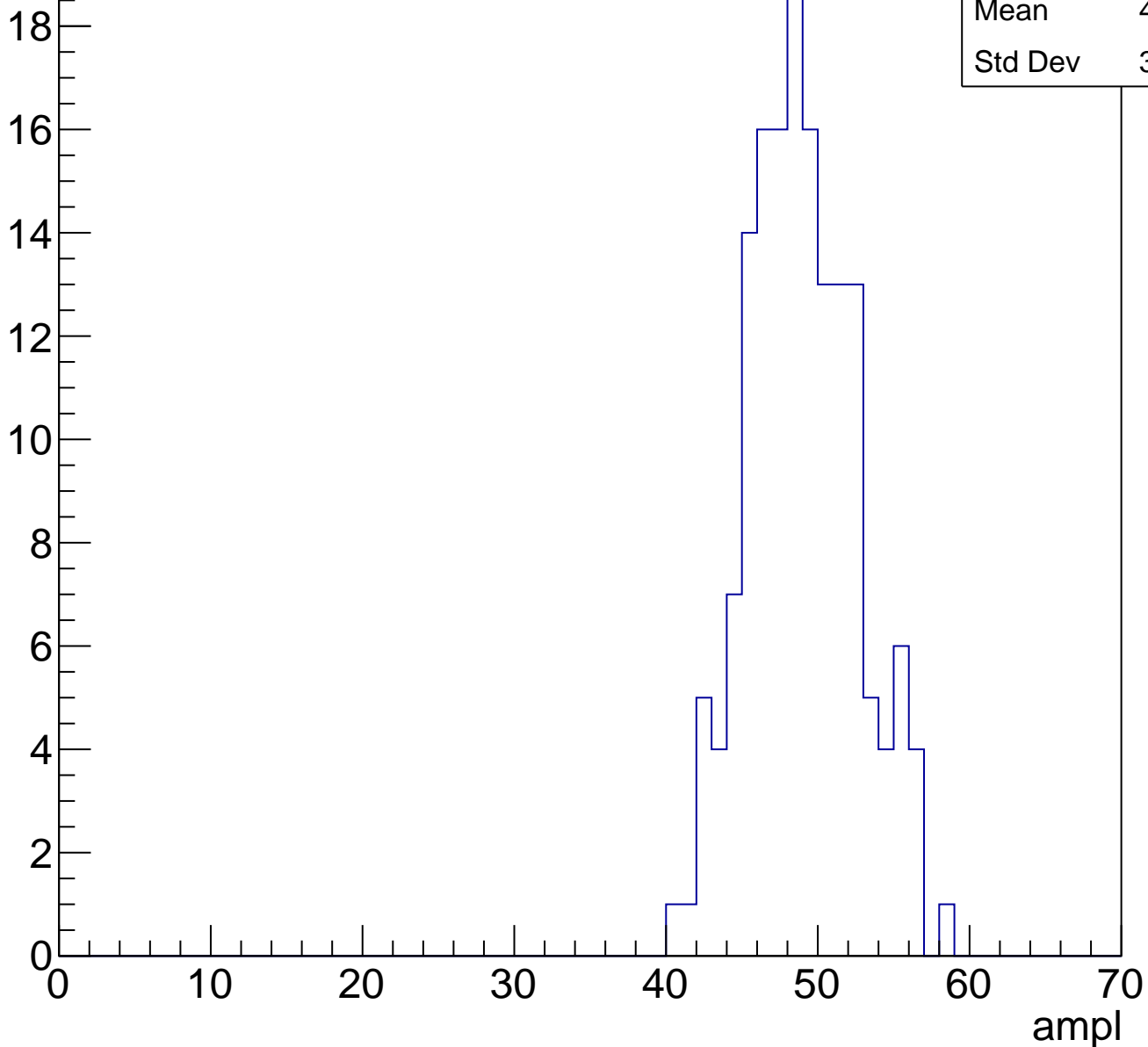
Gaus mean : 41.6377

Gaus Width: 3.0693

B0L101S, U19-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	158
Mean	48.53
Std Dev	3.539

B0L101S, U19-ch2, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.07
Std Dev	3.132

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

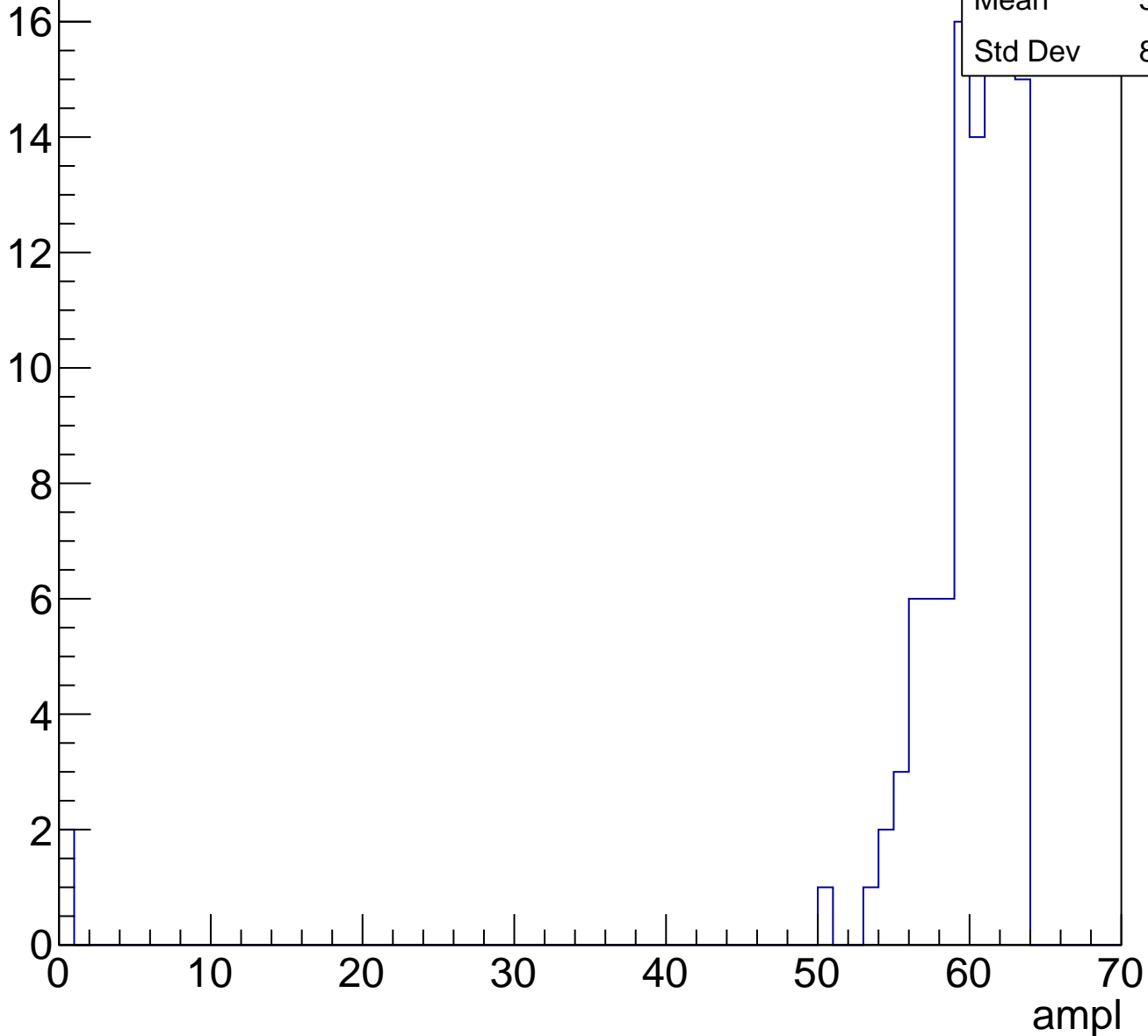
ampl

ampl

B0L101S, U19-ch2, adc5

calib_packv5_042523_0143.root, FC#1, port C1

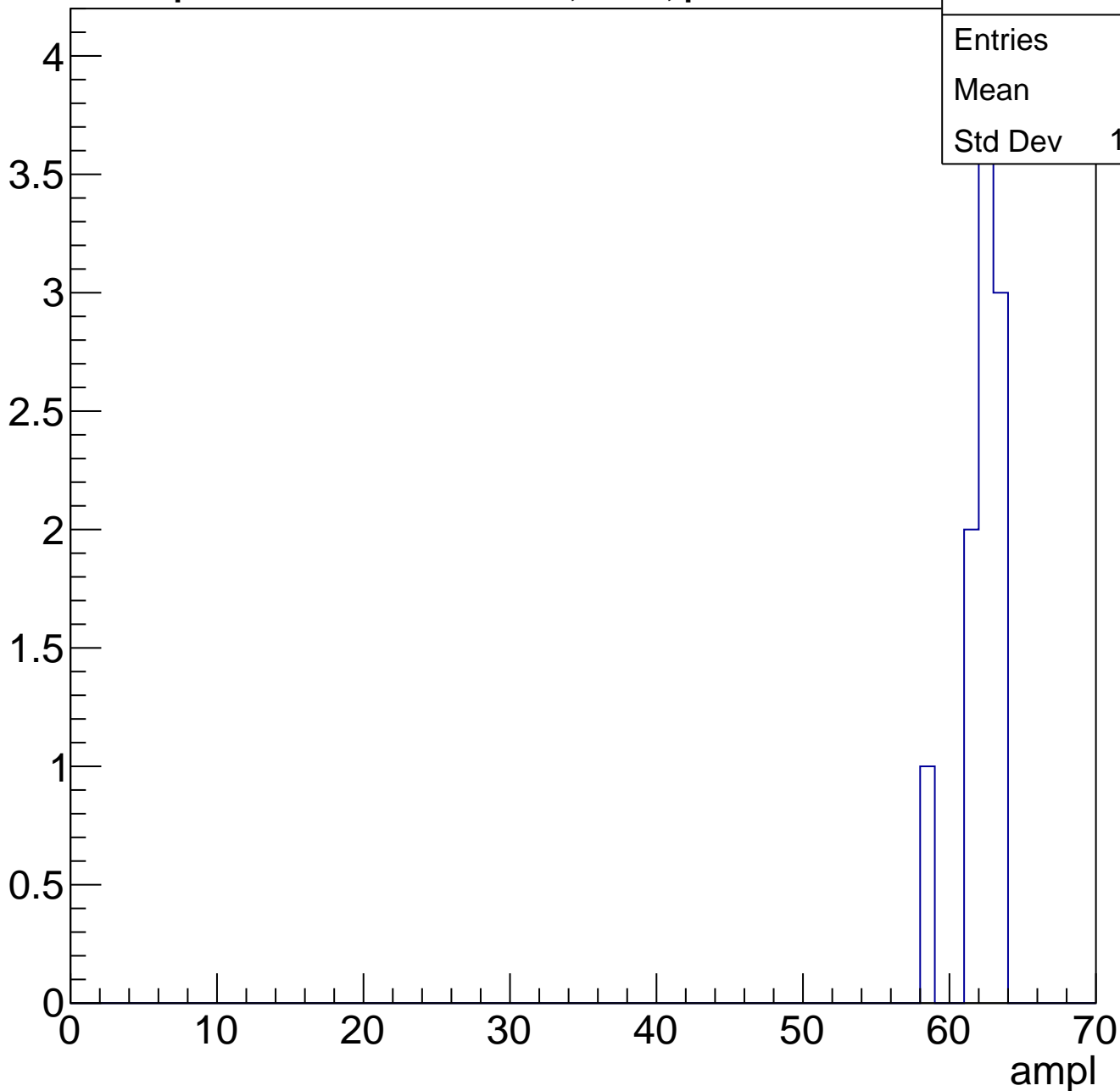
Entry



B0L101S, U19-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch3, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

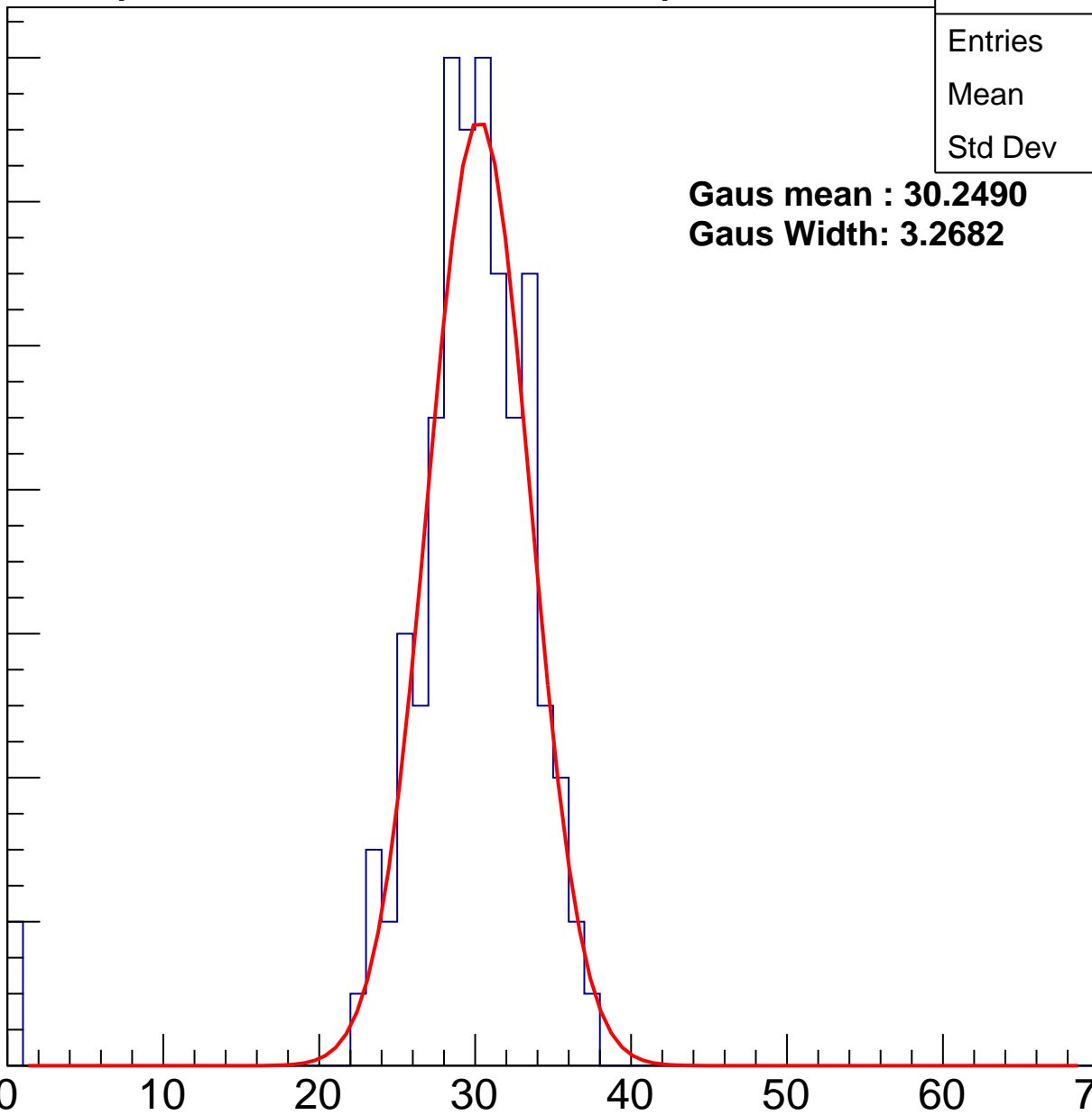
14
12
10
8
6
4
2
0

Entries	112
Mean	29.12
Std Dev	5.022

Gaus mean : 30.2490

Gaus Width: 3.2682

ampl



B0L101S, U19-ch3, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	36.48
Std Dev	3.378

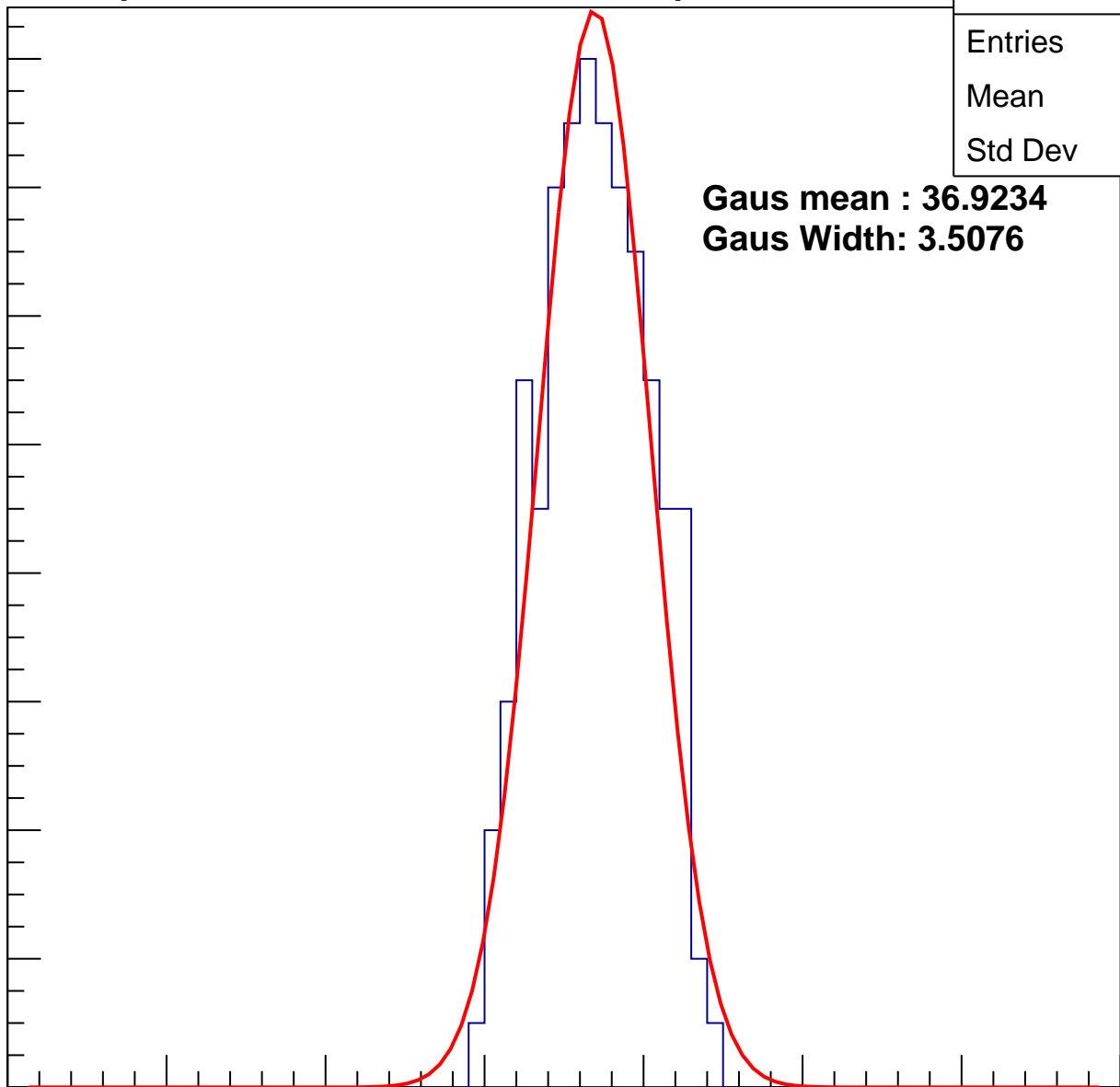
Gaus mean : 36.9234
Gaus Width: 3.5076

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch3, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

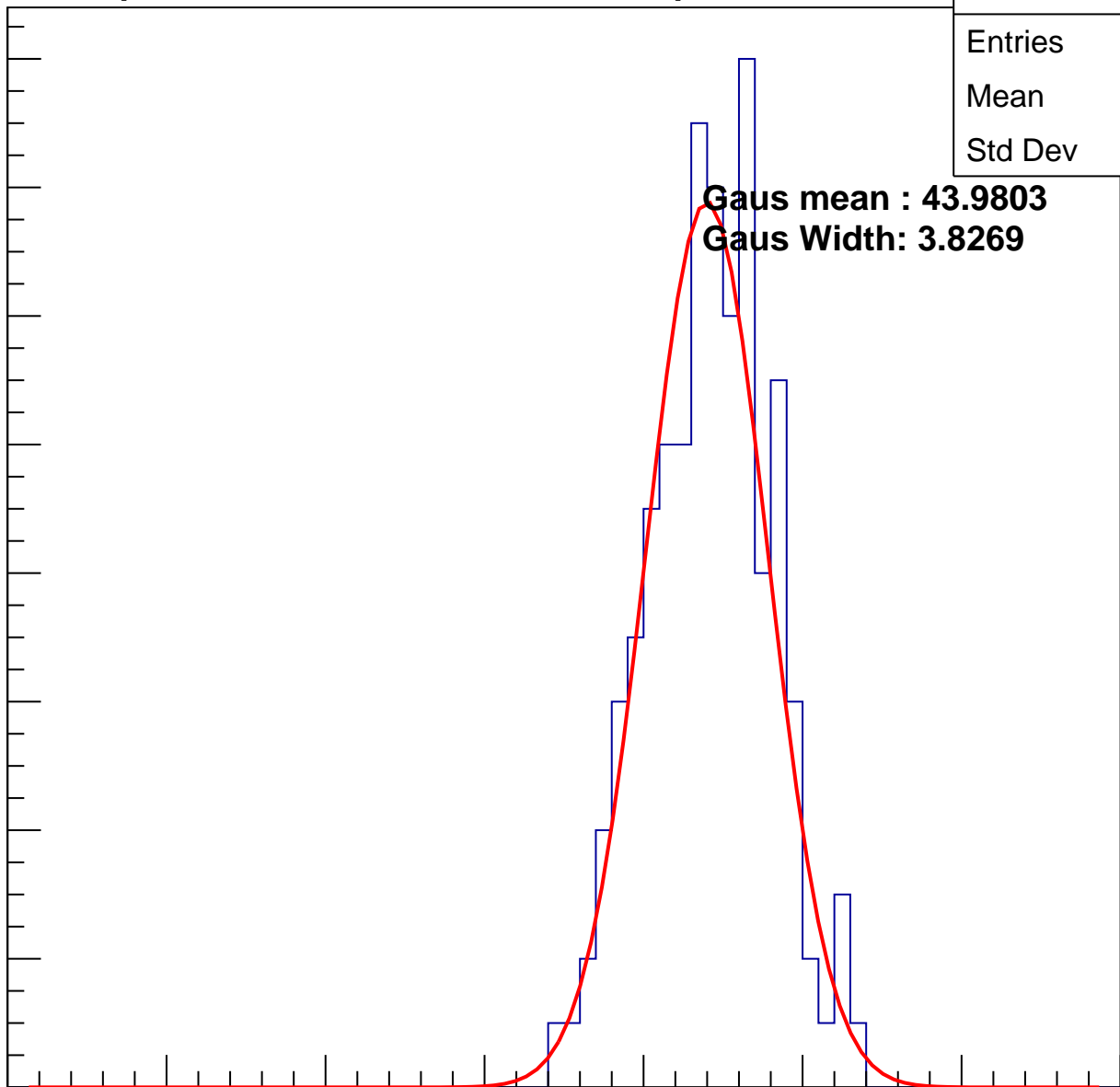
Entries	139
Mean	43.71
Std Dev	3.832

Gaus mean : 43.9803

Gaus Width: 3.8269

0 10 20 30 40 50 60 70

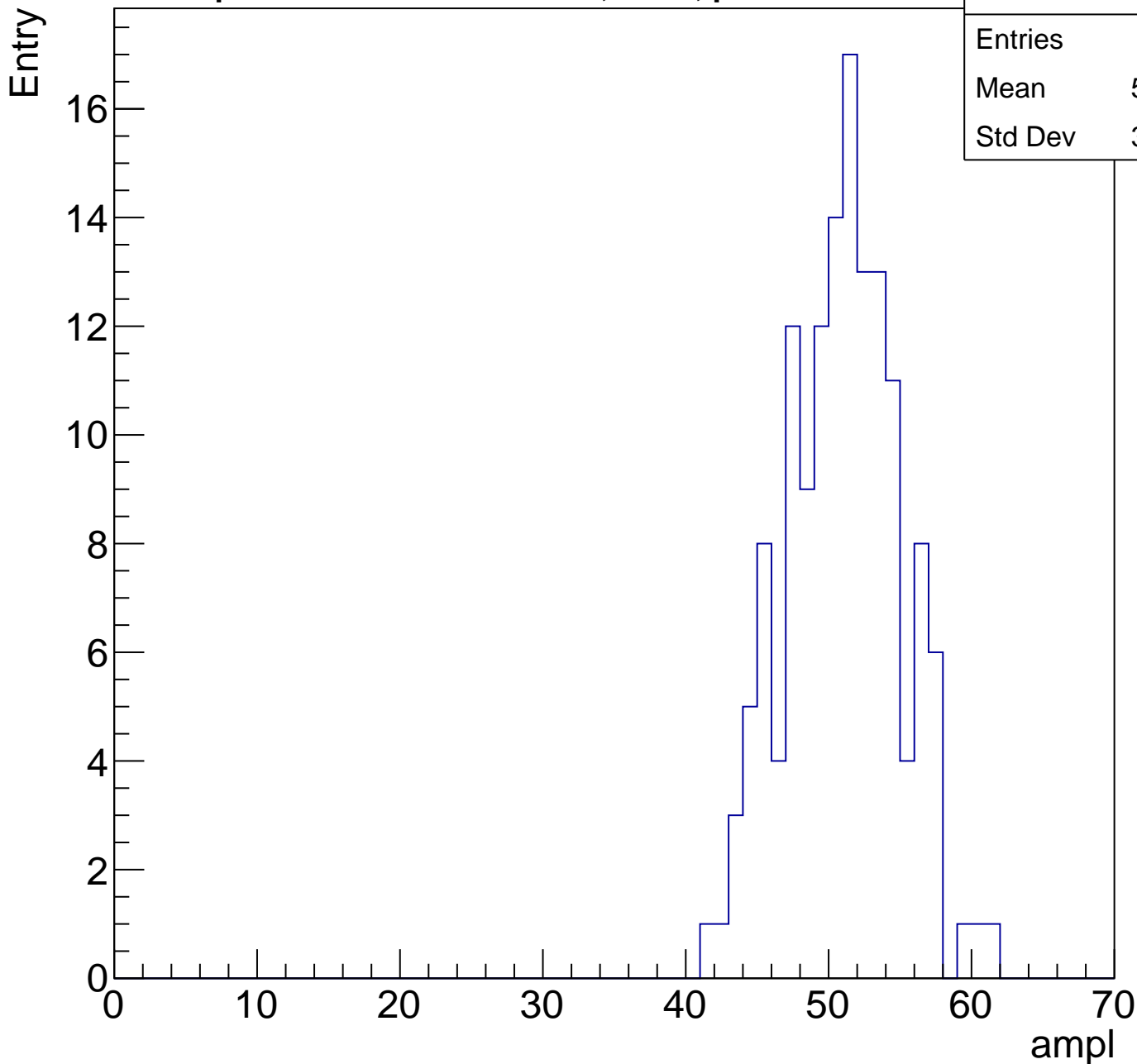
ampl



B0L101S, U19-ch3, adc3

calib_packv5_042523_0143.root, FC#1, port C1

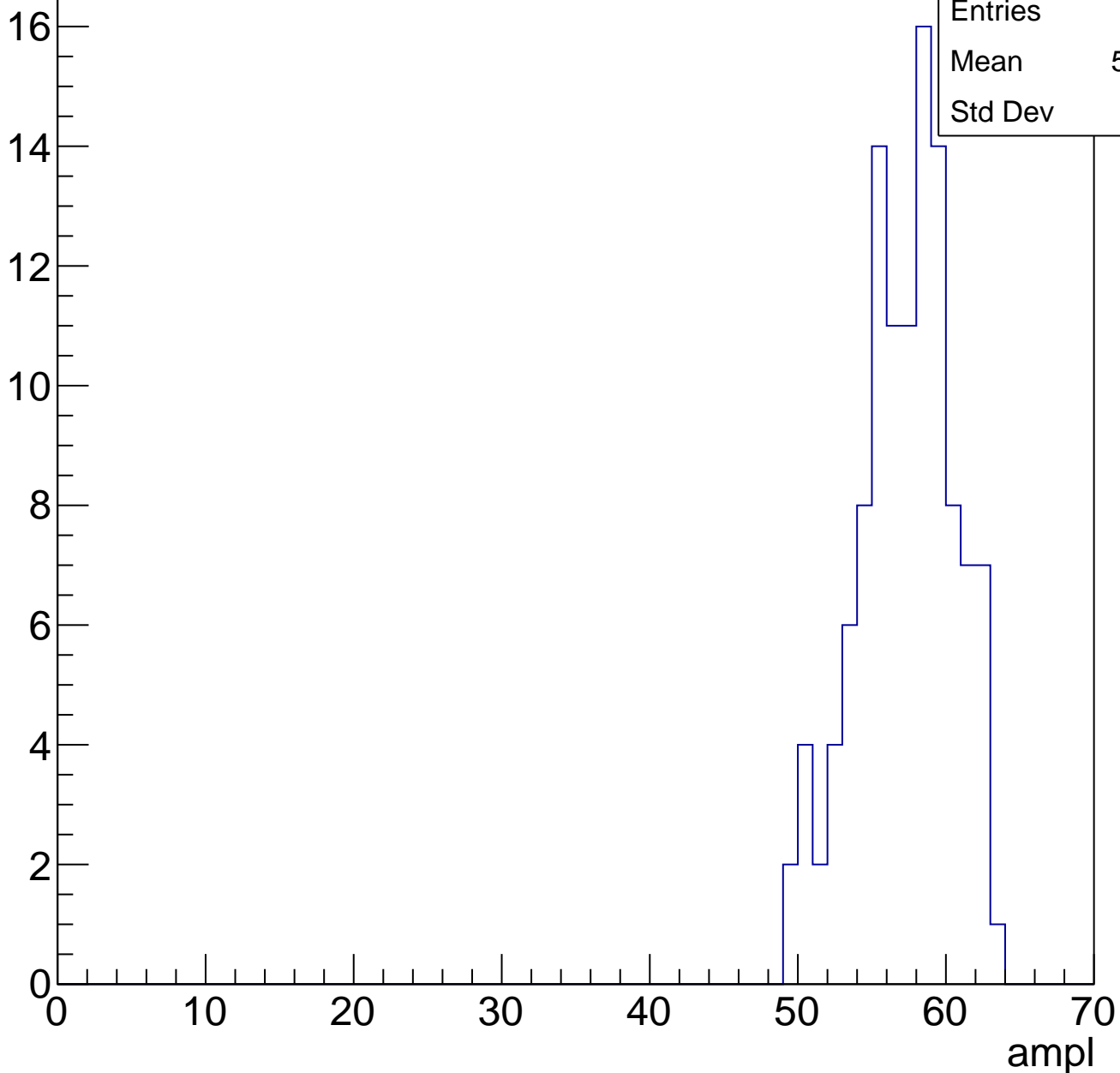
Entries	144
Mean	50.53
Std Dev	3.907



B0L101S, U19-ch3, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch3, adc5

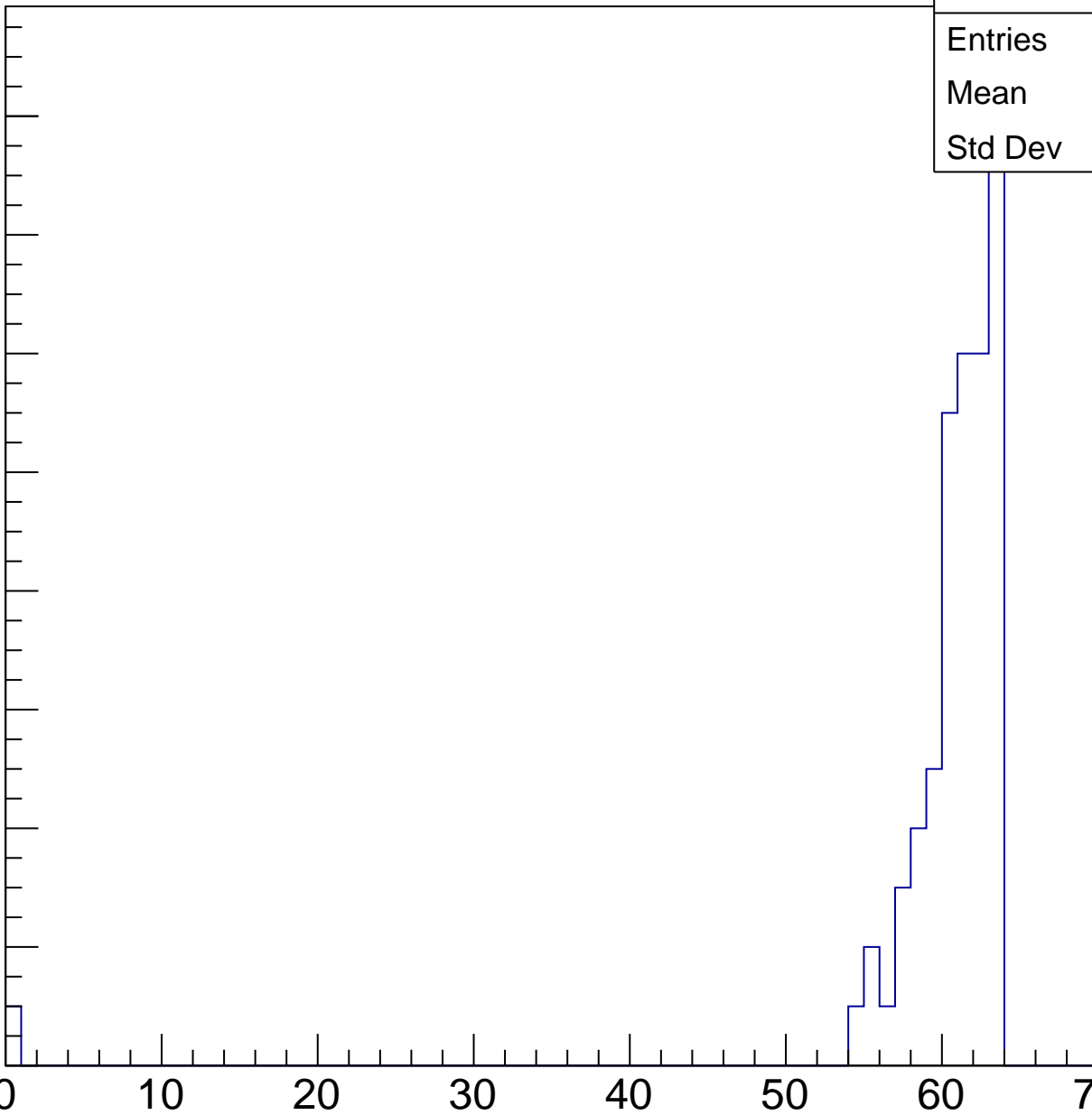
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	59.78
Std Dev	7.575

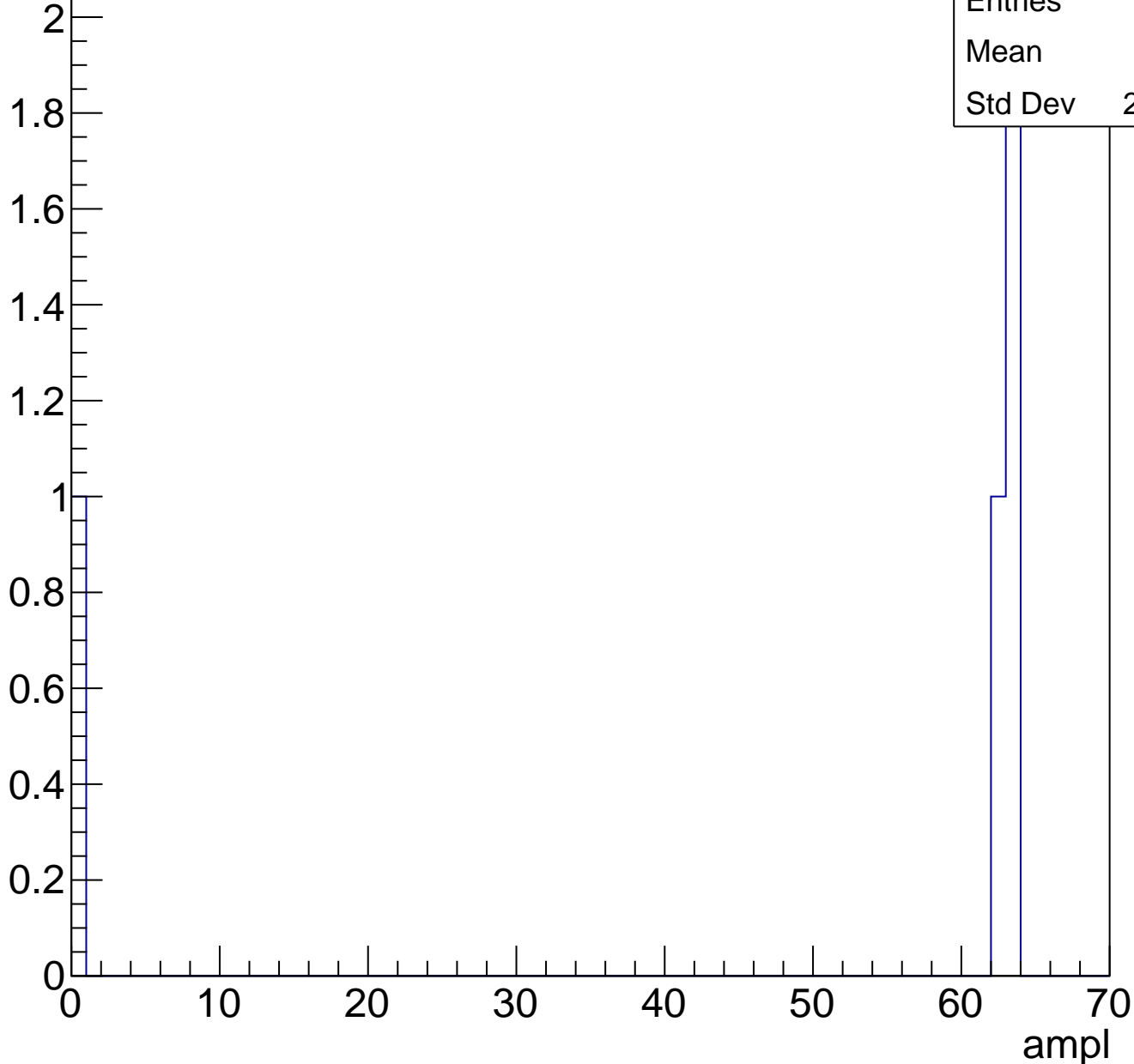
ampl



B0L101S, U19-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch4, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	28.47
Std Dev	6.352

Gaus mean : 29.9453

Gaus Width: 4.0683

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

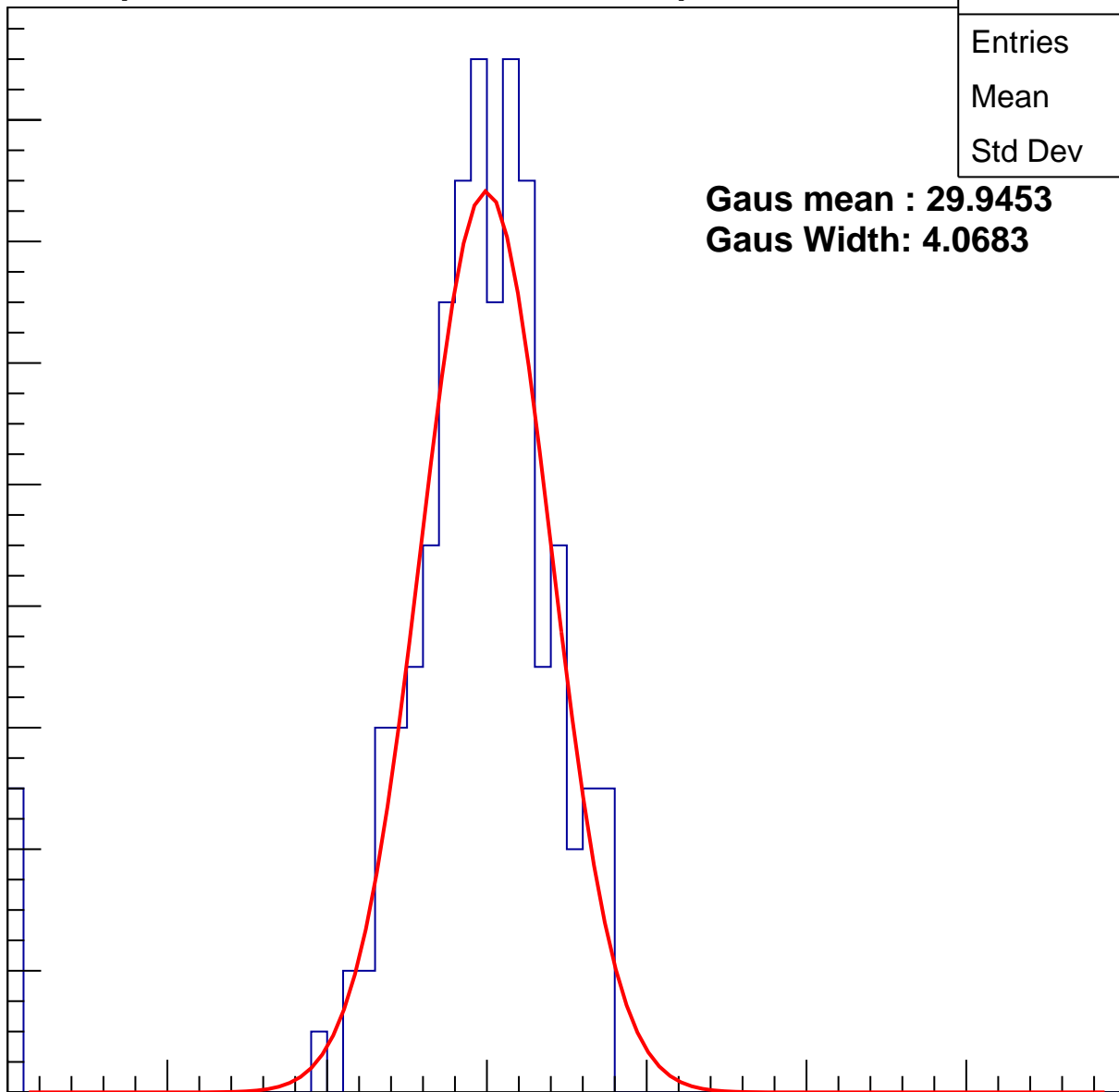
40

50

60

70

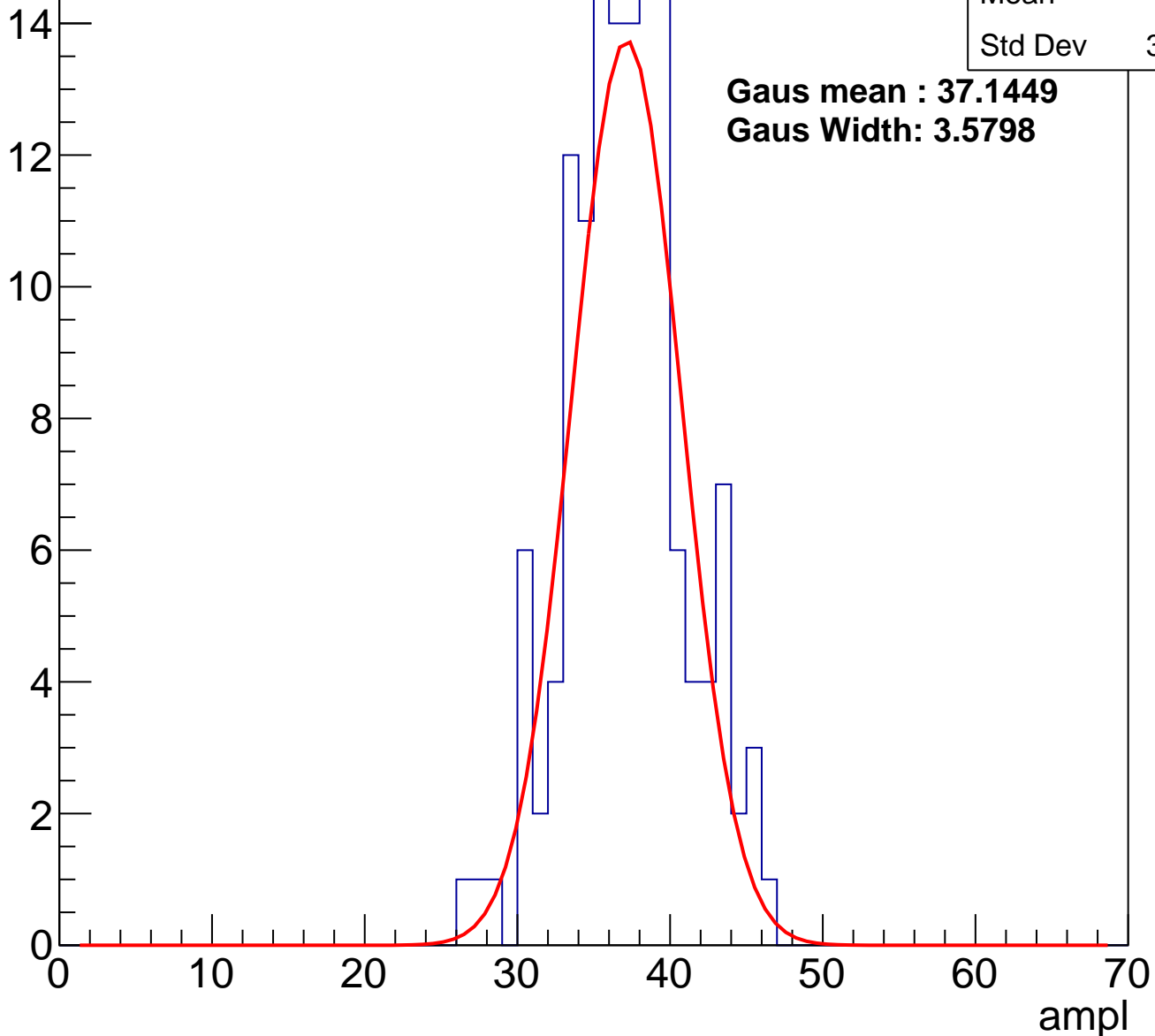
ampl



B0L101S, U19-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	138
Mean	36.7
Std Dev	3.842

B0L101S, U19-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

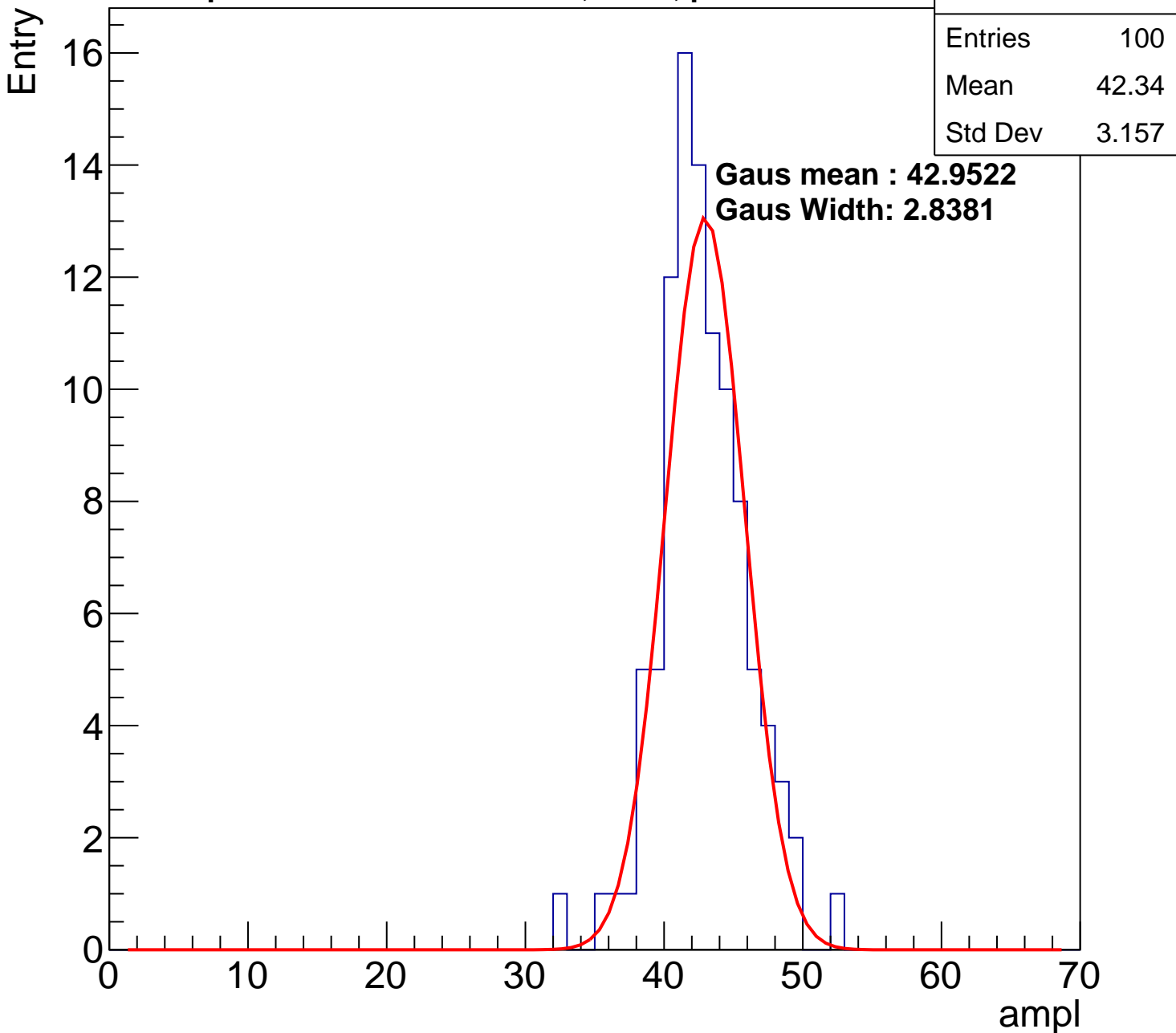
Entries	100
Mean	42.34
Std Dev	3.157

Gaus mean : 42.9522

Gaus Width: 2.8381

0 10 20 30 40 50 60 70

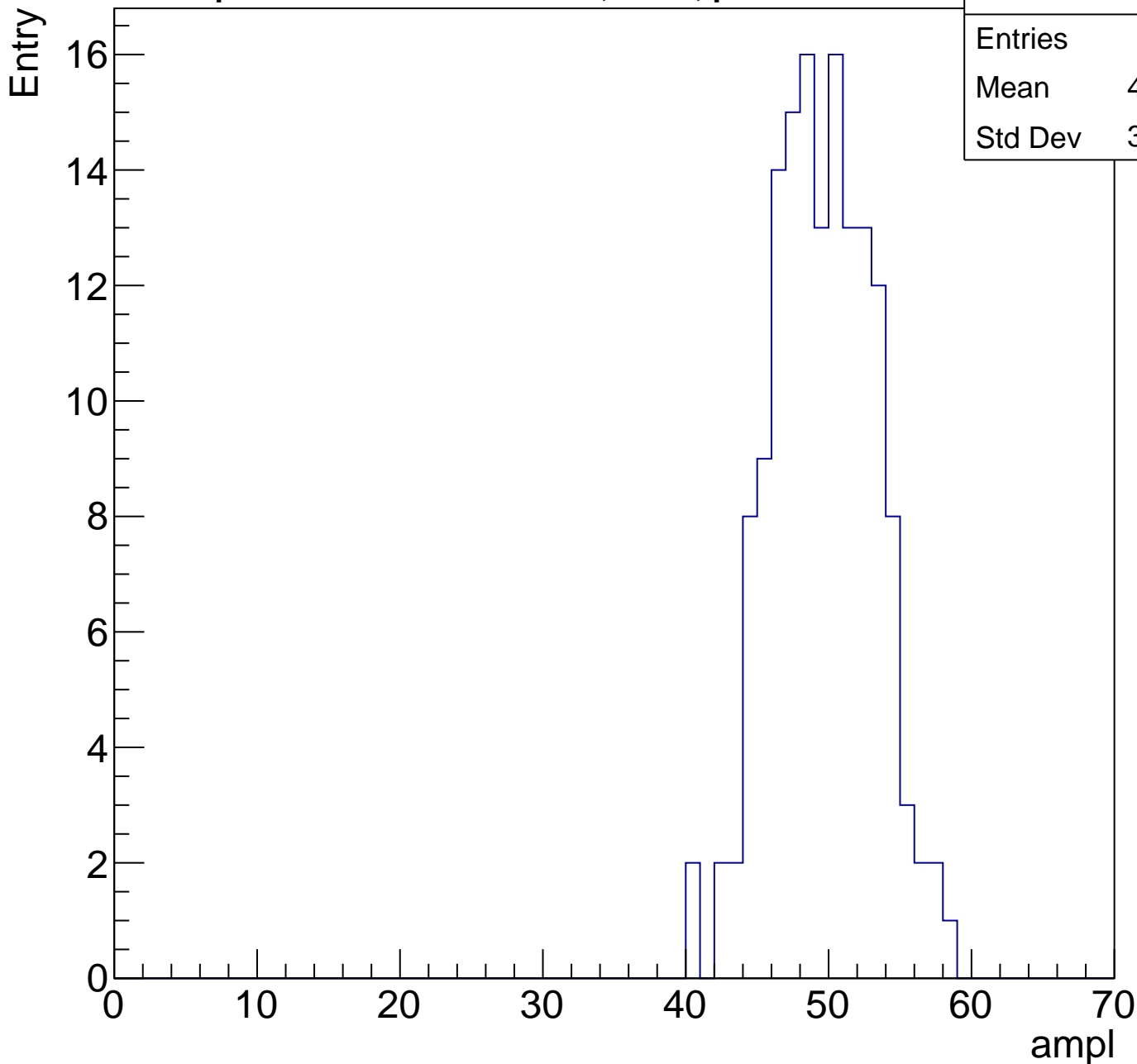
ampl



B0L101S, U19-ch4, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	49.12
Std Dev	3.517



B0L101S, U19-ch4, adc4

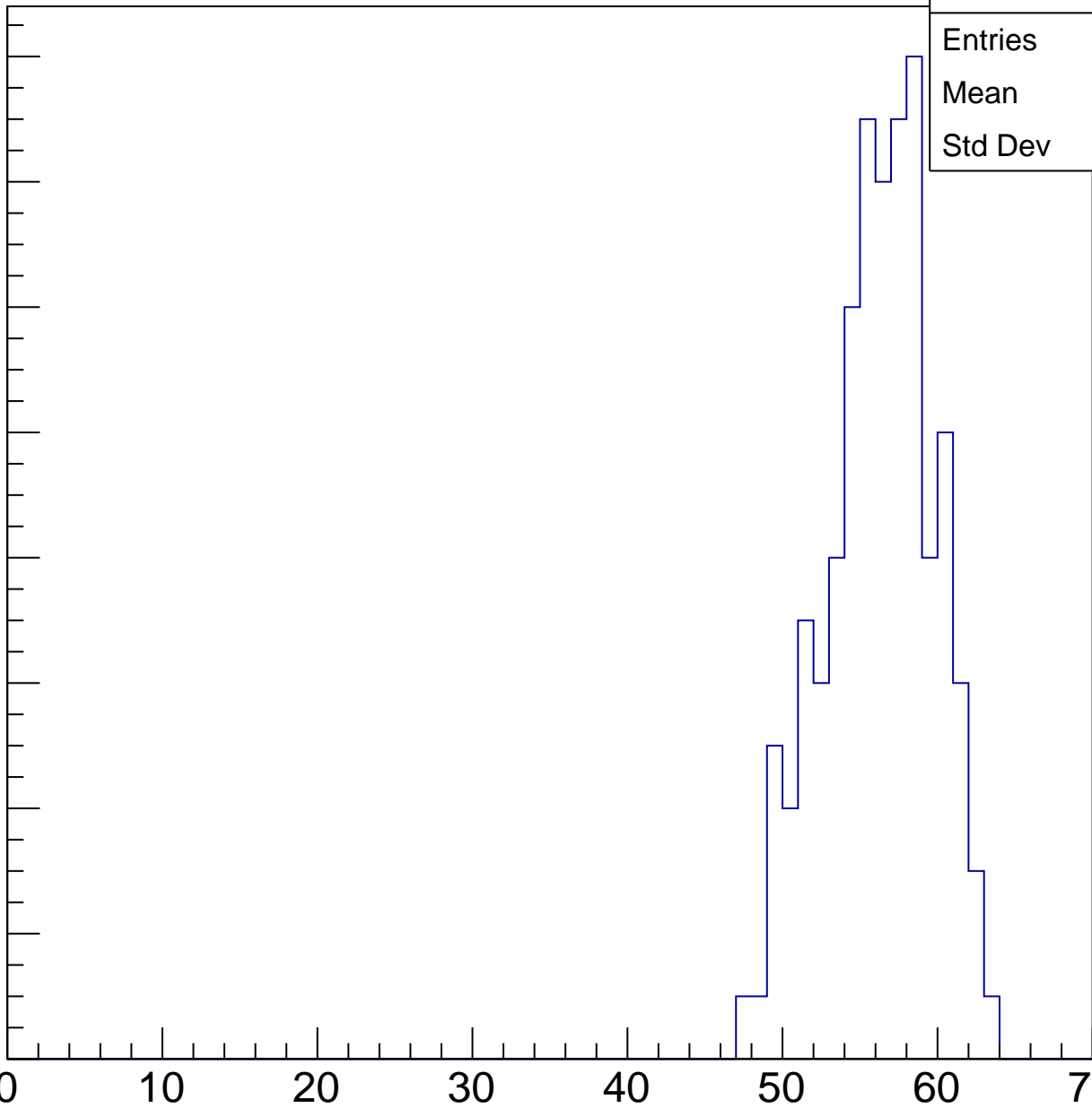
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	55.76
Std Dev	3.427

ampl



B0L101S, U19-ch4, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

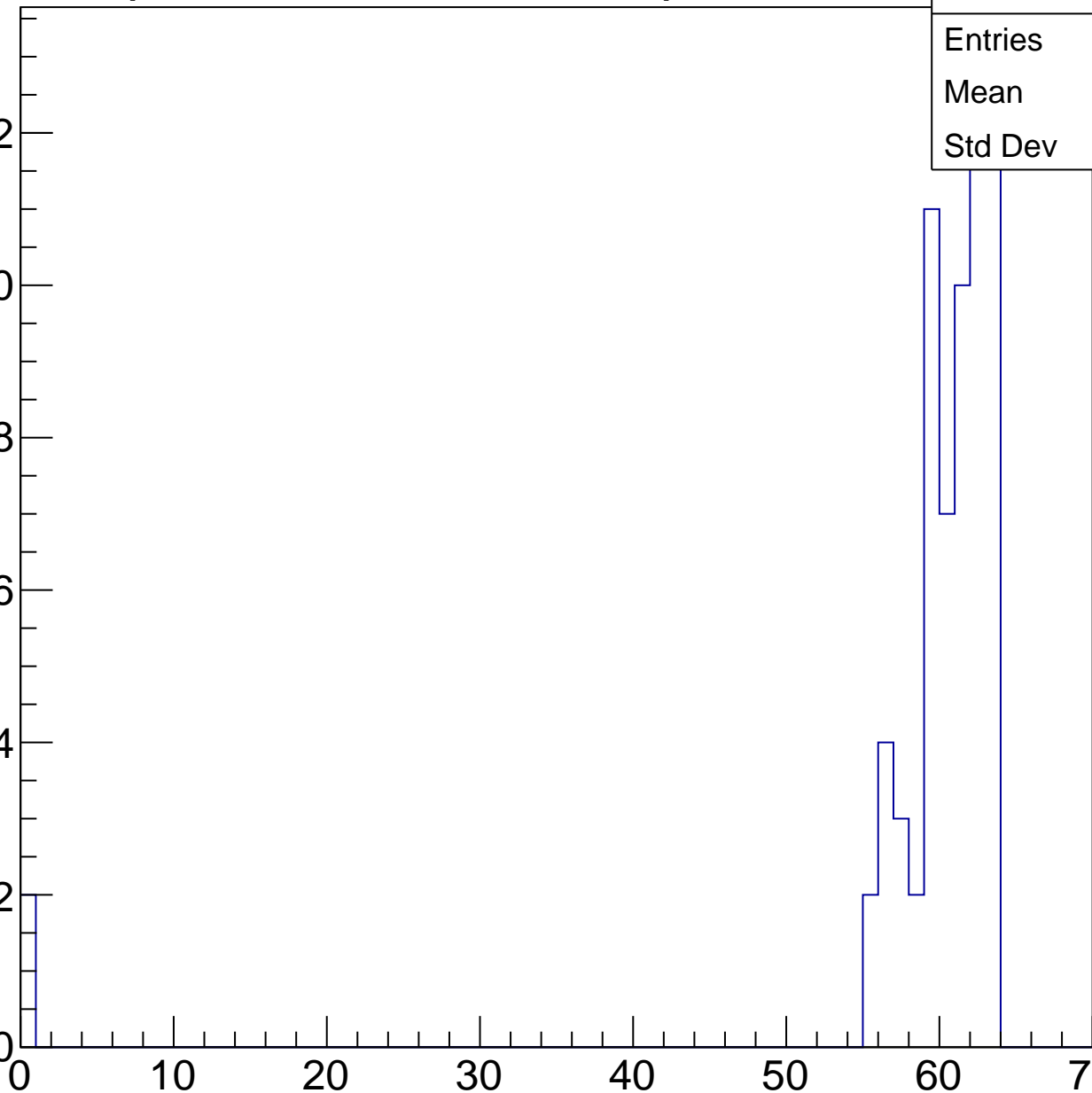
4

2

0

Entries	66
Mean	58.52
Std Dev	10.58

ampl

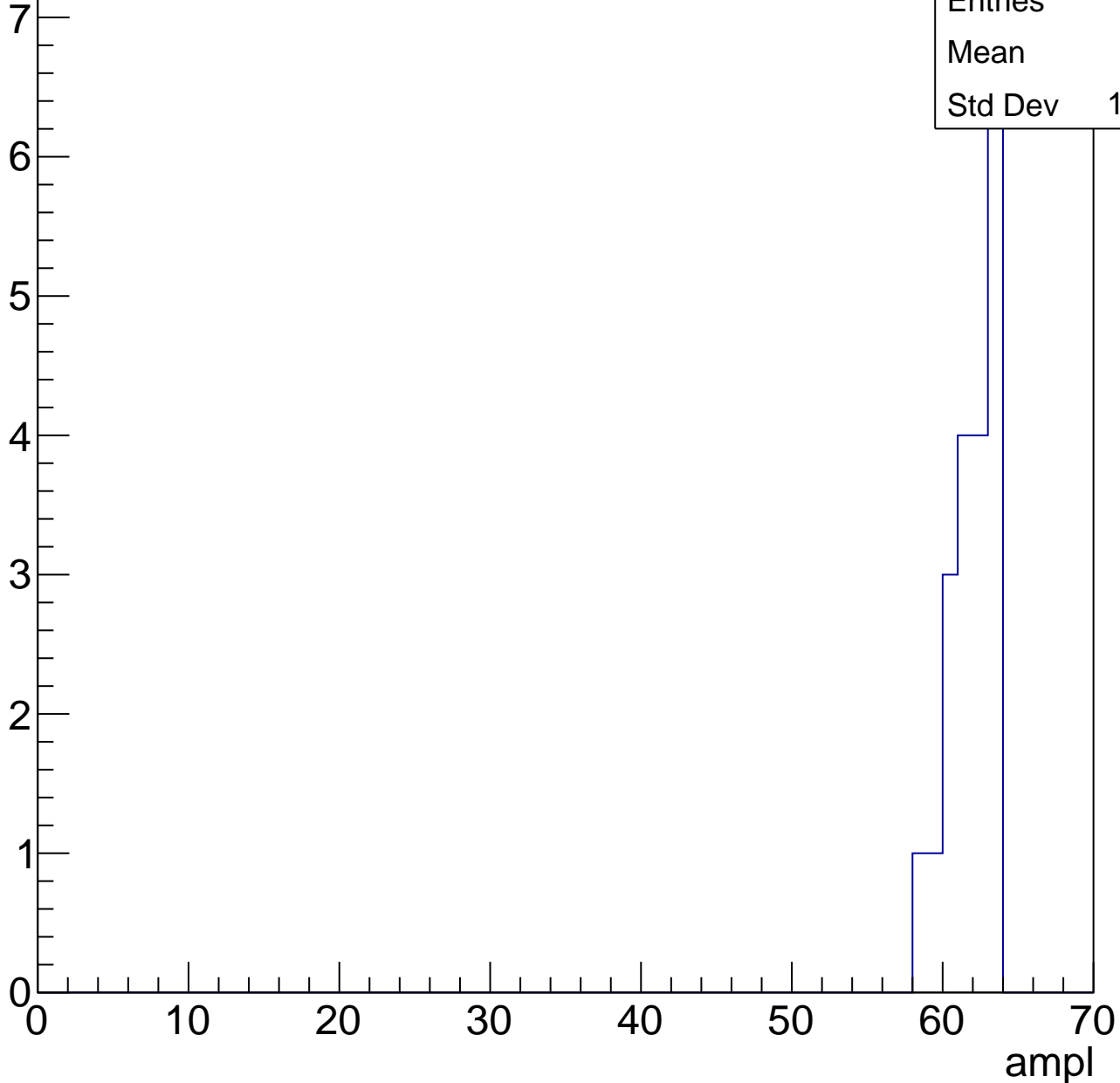


B0L101S, U19-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	61.5
Std Dev	1.466



B0L101S, U19-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch5, adc0

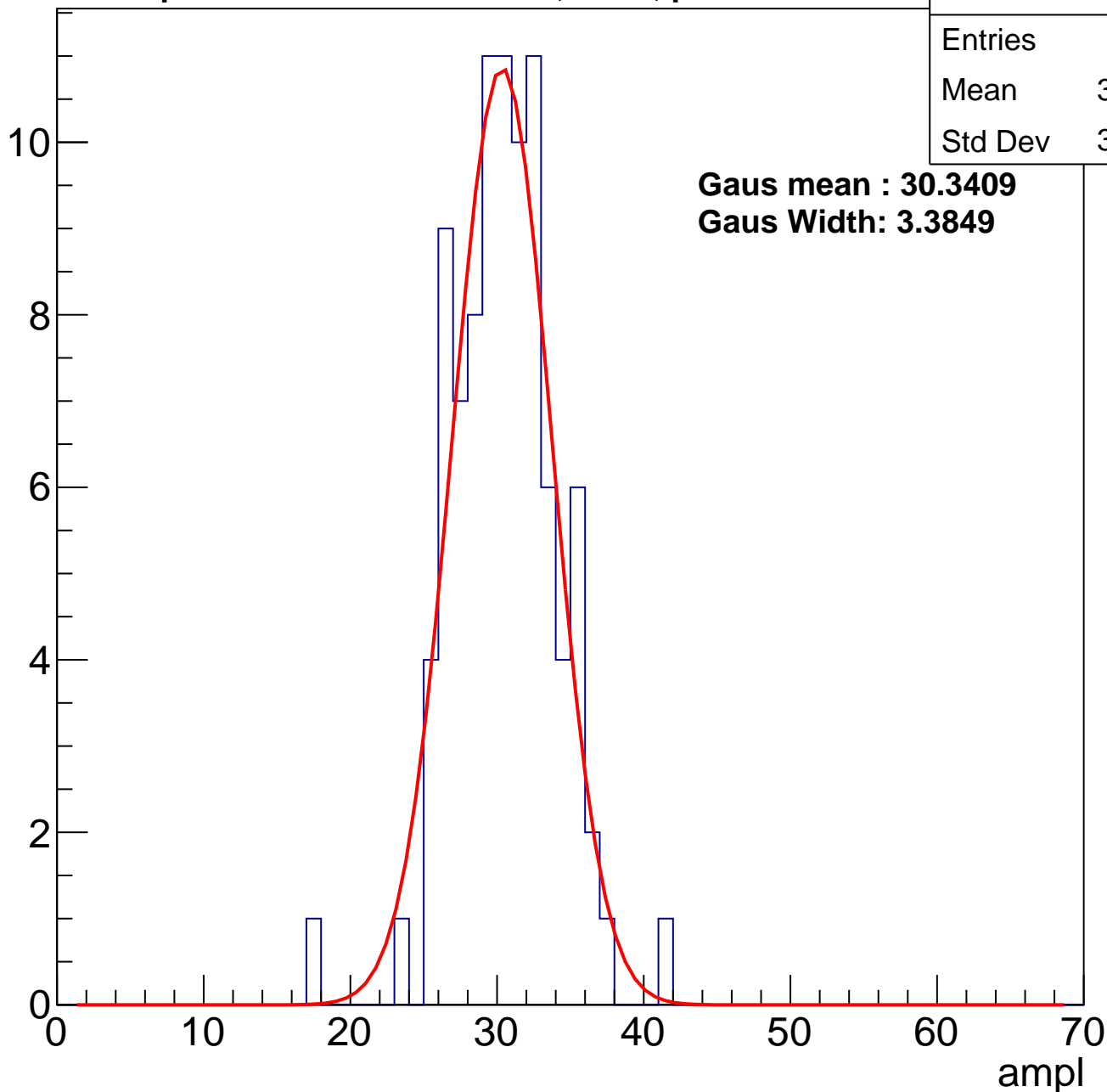
calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	30.02
Std Dev	3.489

Gaus mean : 30.3409

Gaus Width: 3.3849

Entry



B0L101S, U19-ch5, adc1

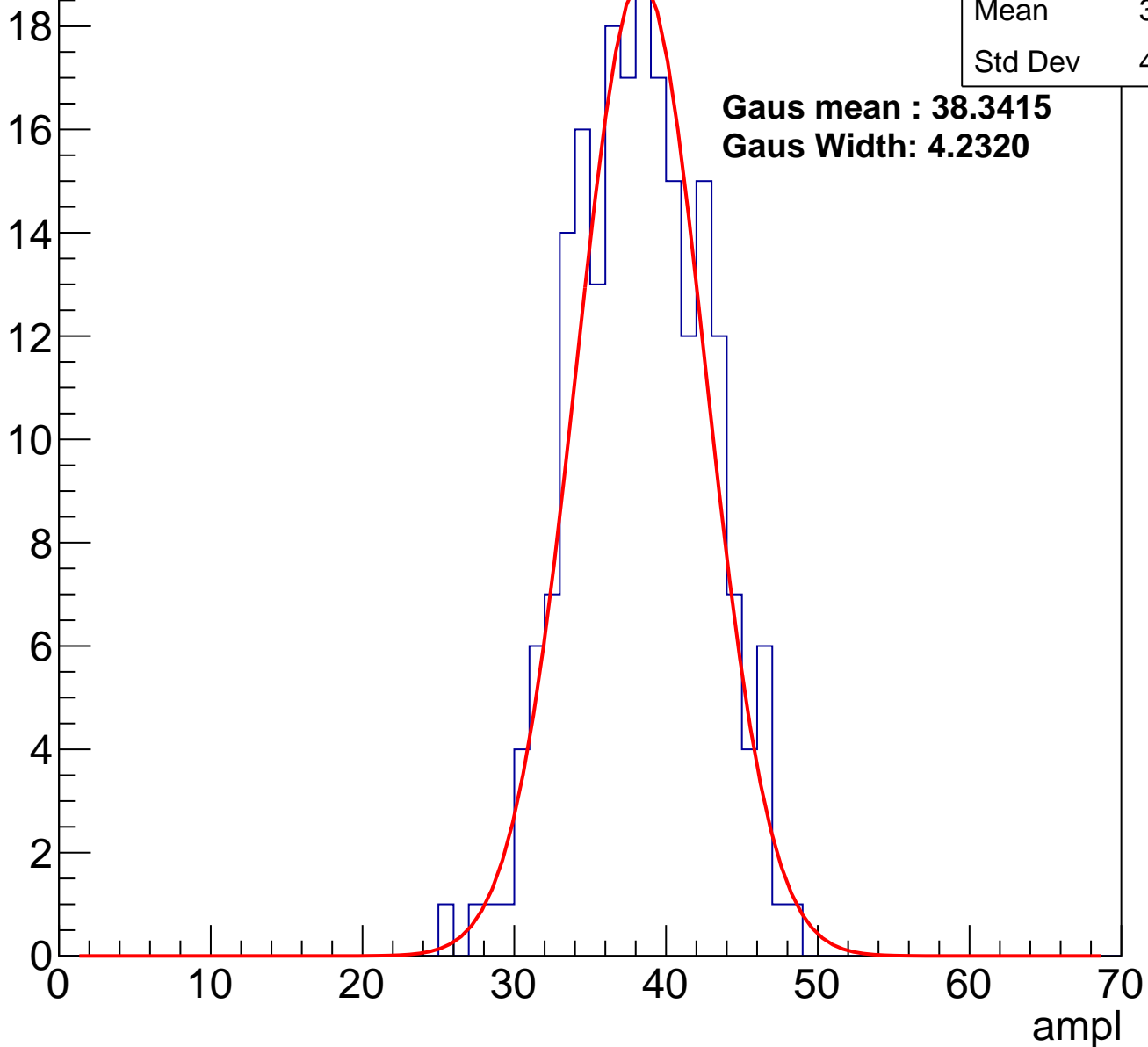
calib_packv5_042523_0143.root, FC#1, port C1

Entries	208
Mean	37.78
Std Dev	4.282

Gaus mean : 38.3415

Gaus Width: 4.2320

Entry



B0L101S, U19-ch5, adc2

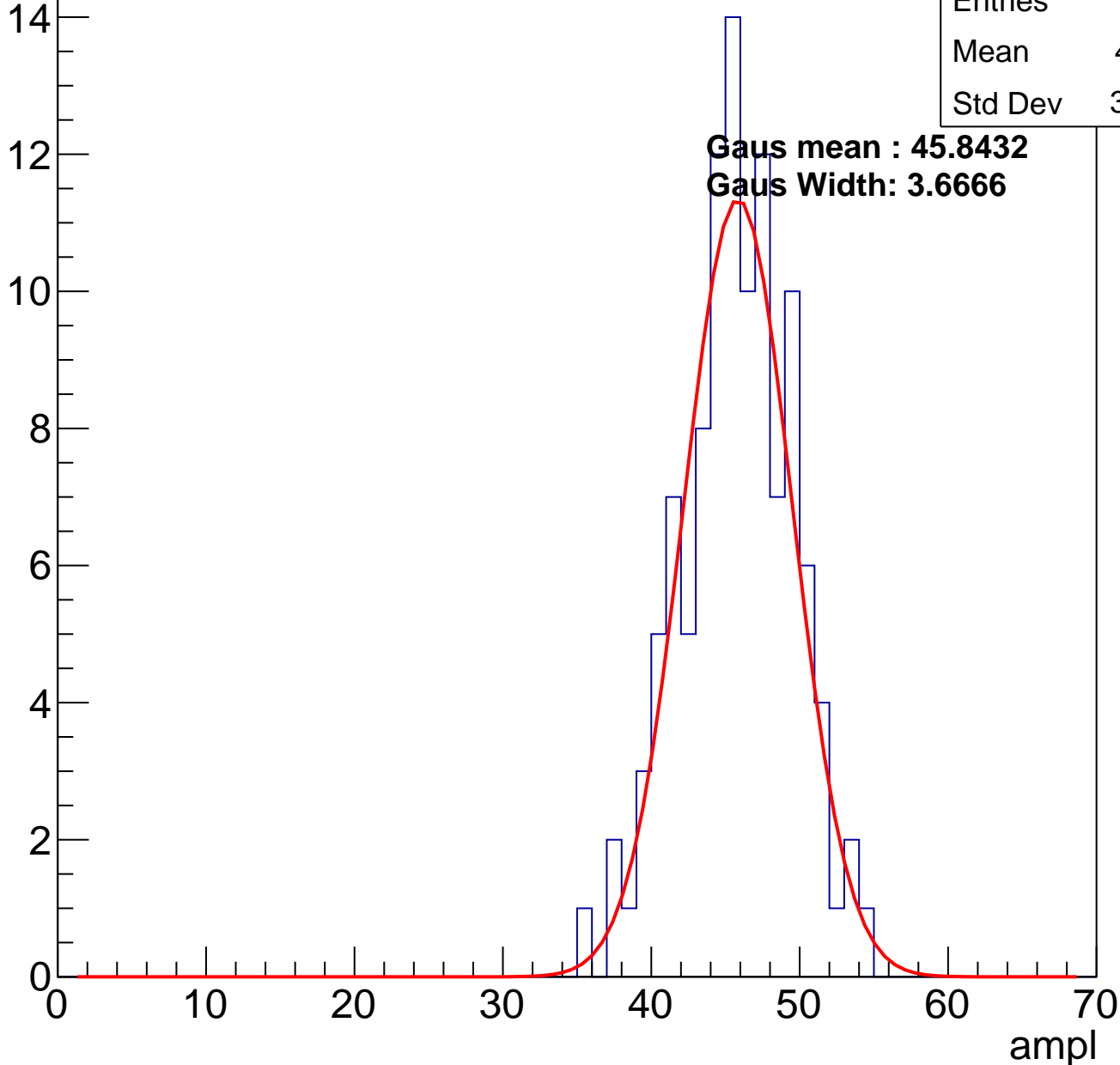
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	45.31
Std Dev	3.717

Gaus mean : 45.8432

Gaus Width: 3.6666

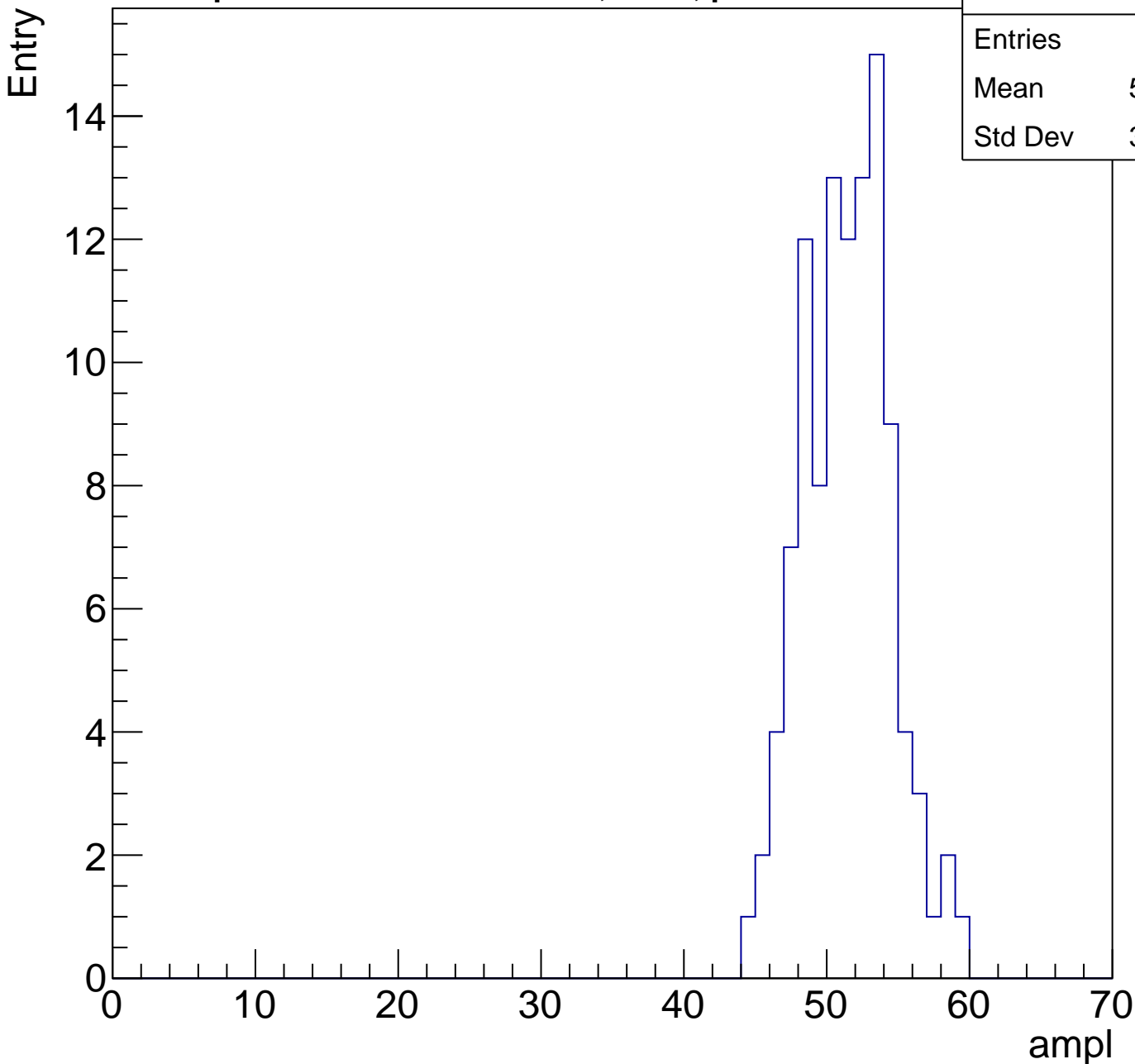
Entry



B0L101S, U19-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

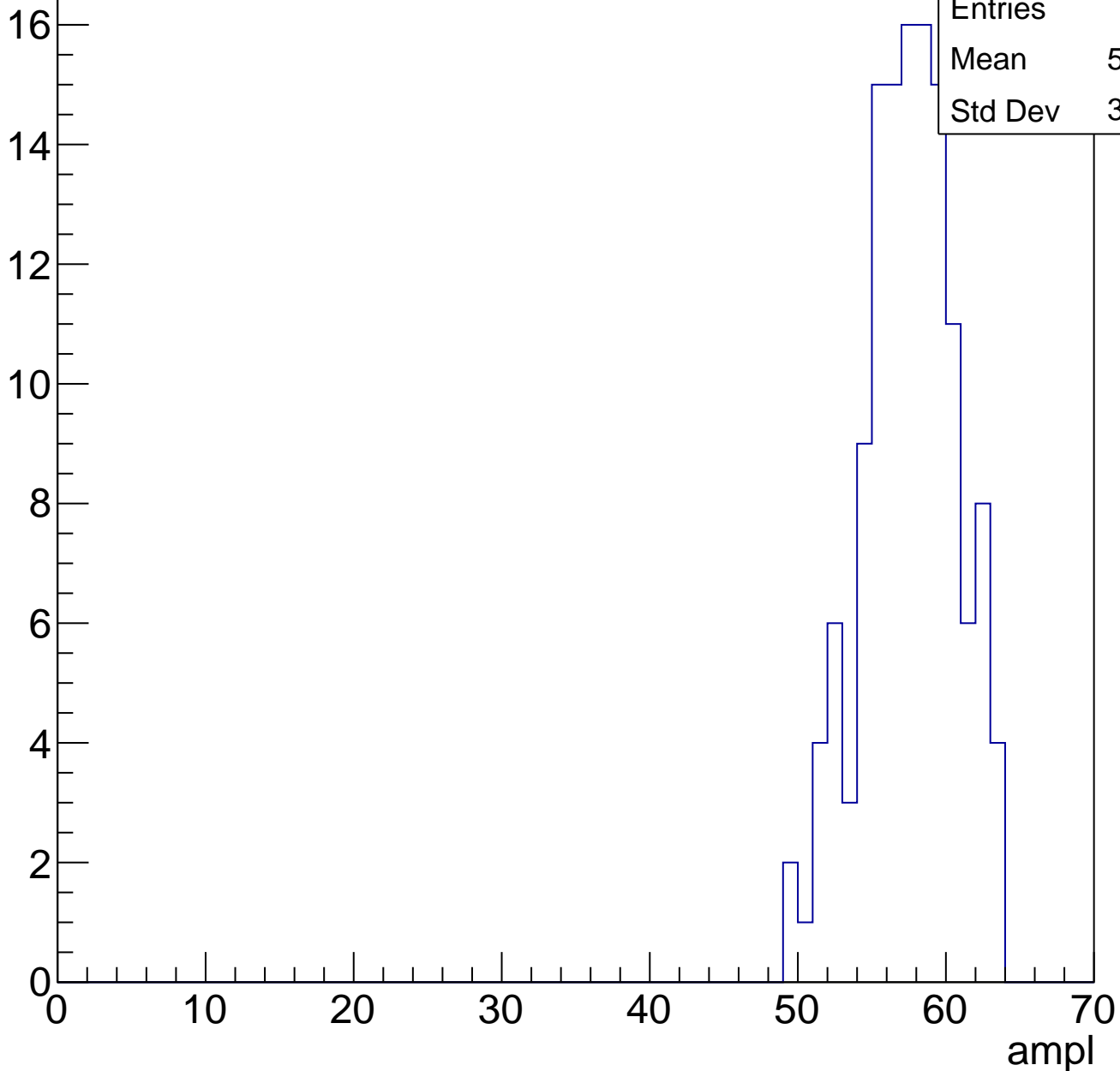
Entries	107
Mean	50.97
Std Dev	3.022



B0L101S, U19-ch5, adc4

calib_packv5_042523_0143.root, FC#1, port C1

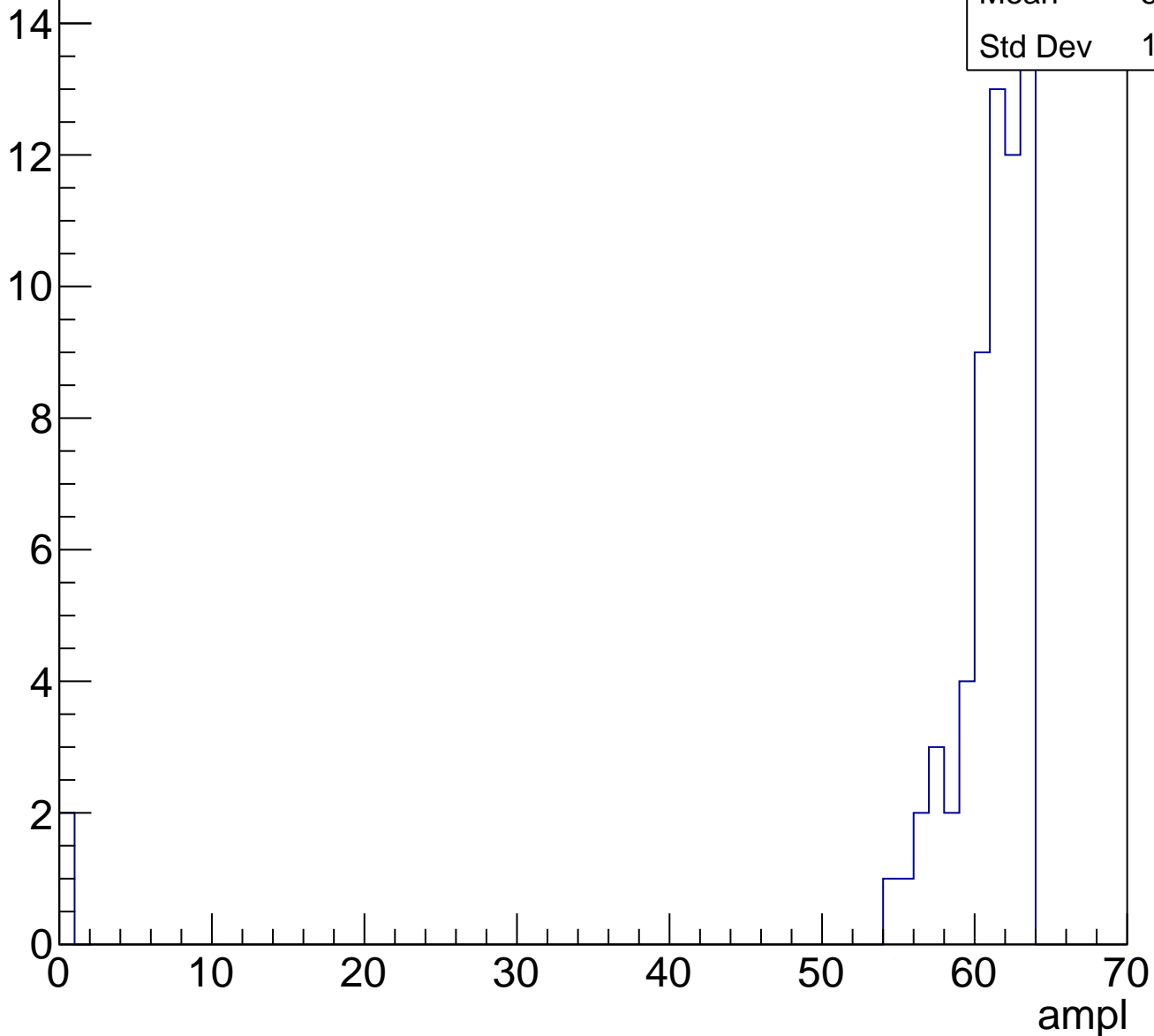
Entry



B0L101S, U19-ch5, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L101S, U19-ch6, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	28.54
Std Dev	4.065

Gaus mean : 29.5418

Gaus Width: 3.1637

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

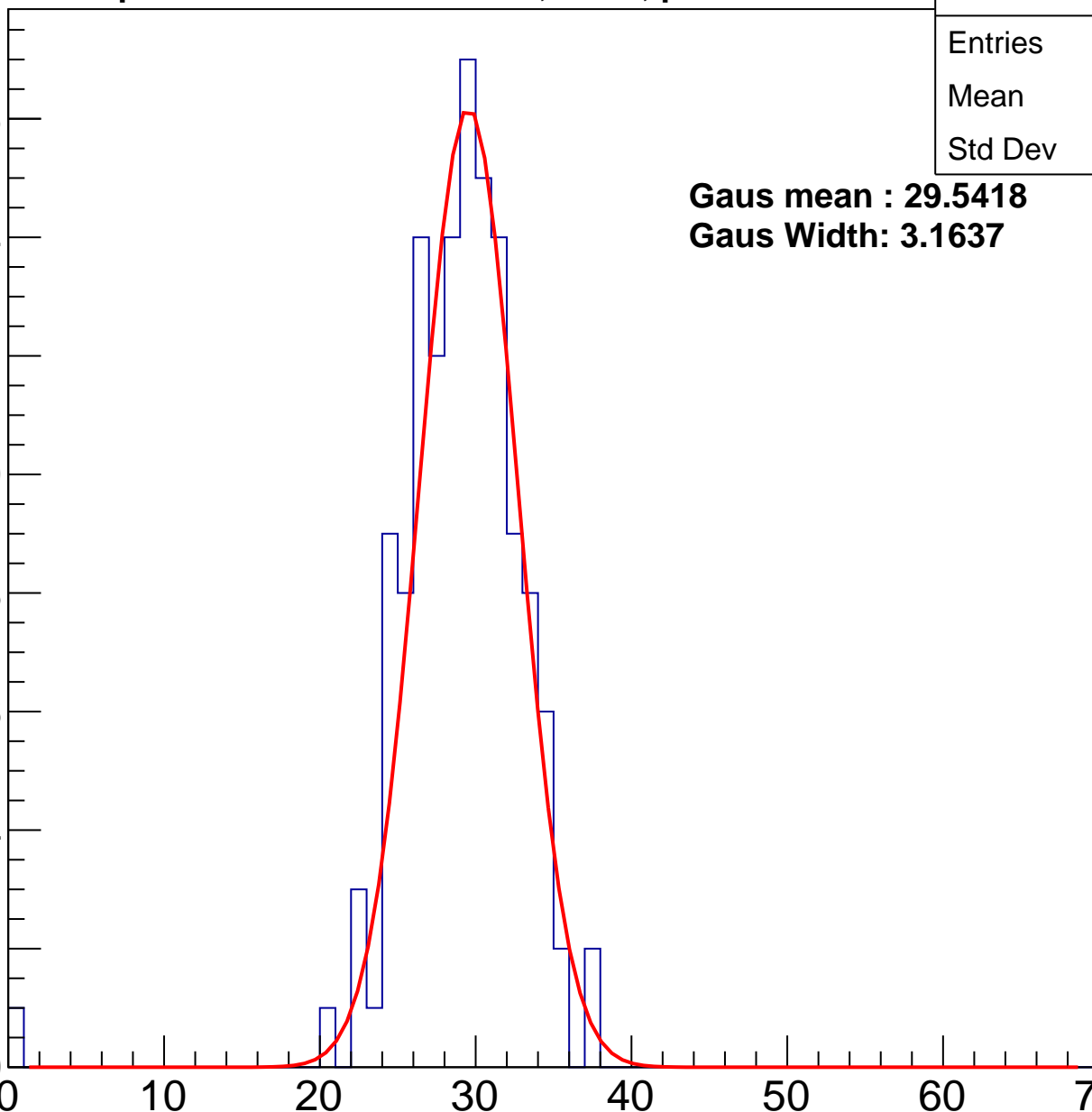
40

50

60

70

ampl



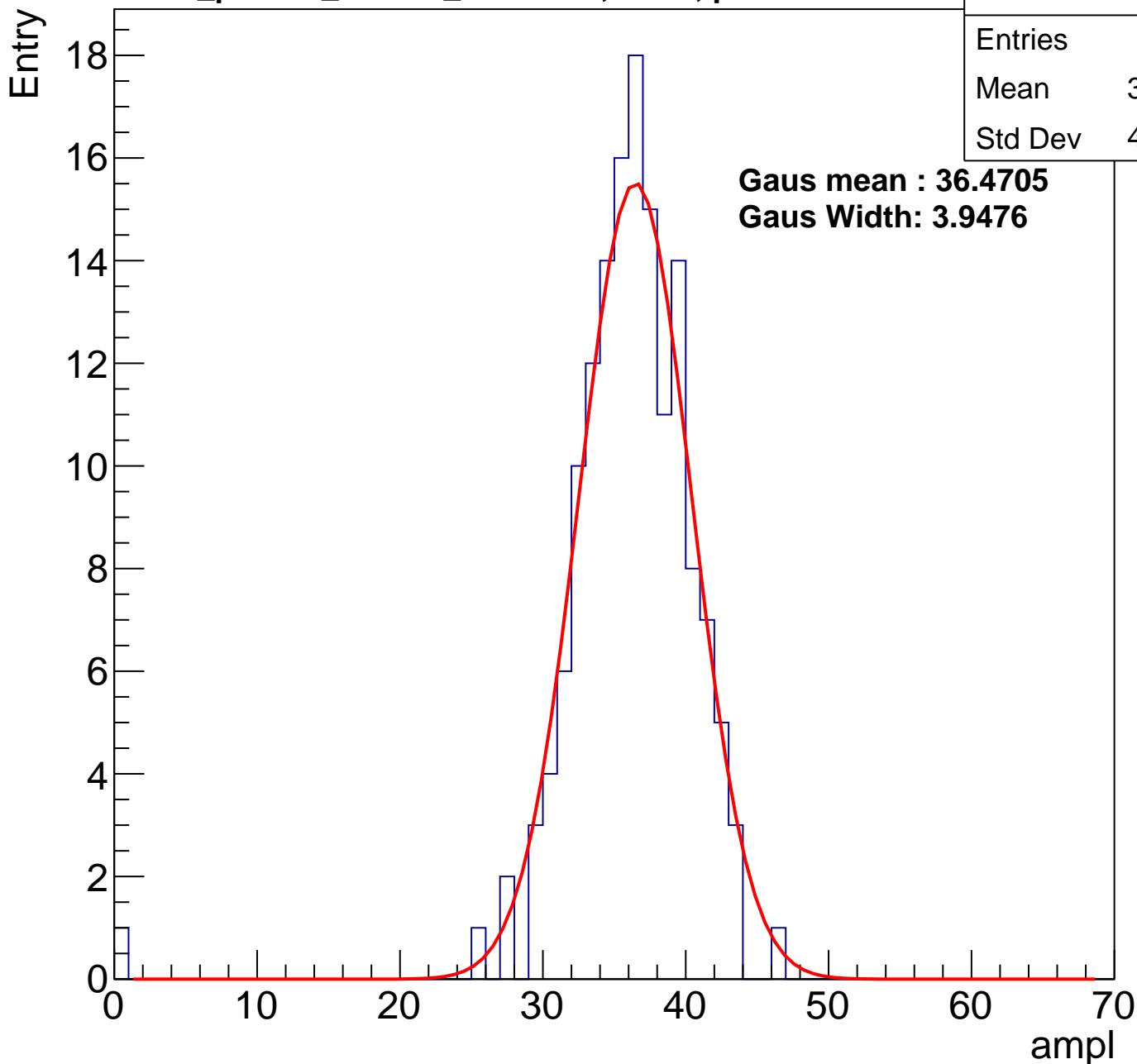
B0L101S, U19-ch6, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	35.65
Std Dev	4.644

Gaus mean : 36.4705

Gaus Width: 3.9476



B0L101S, U19-ch6, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

153

Mean

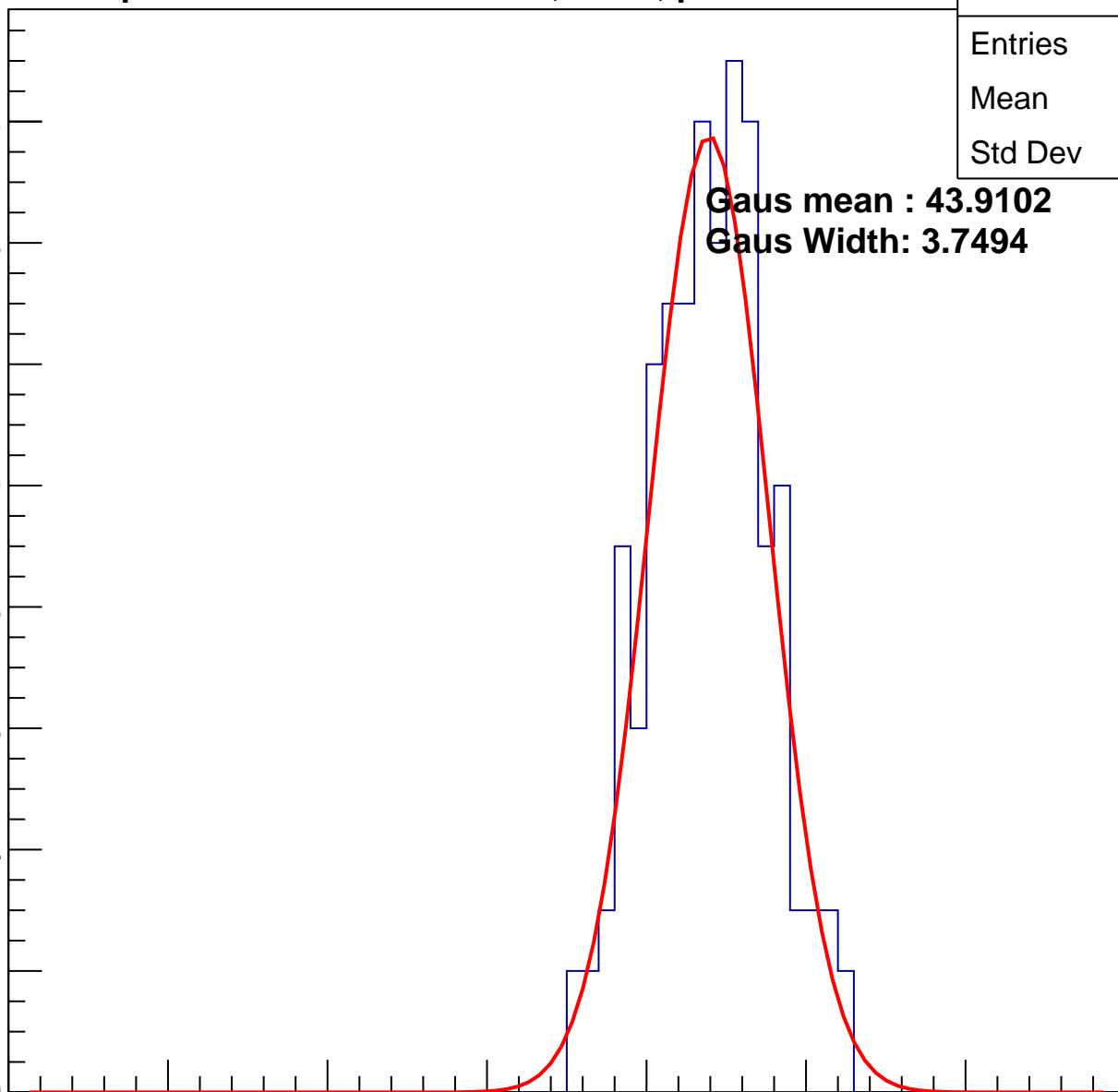
43.46

Std Dev

3.637

Gaus mean : 43.9102

Gaus Width: 3.7494



B0L101S, U19-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

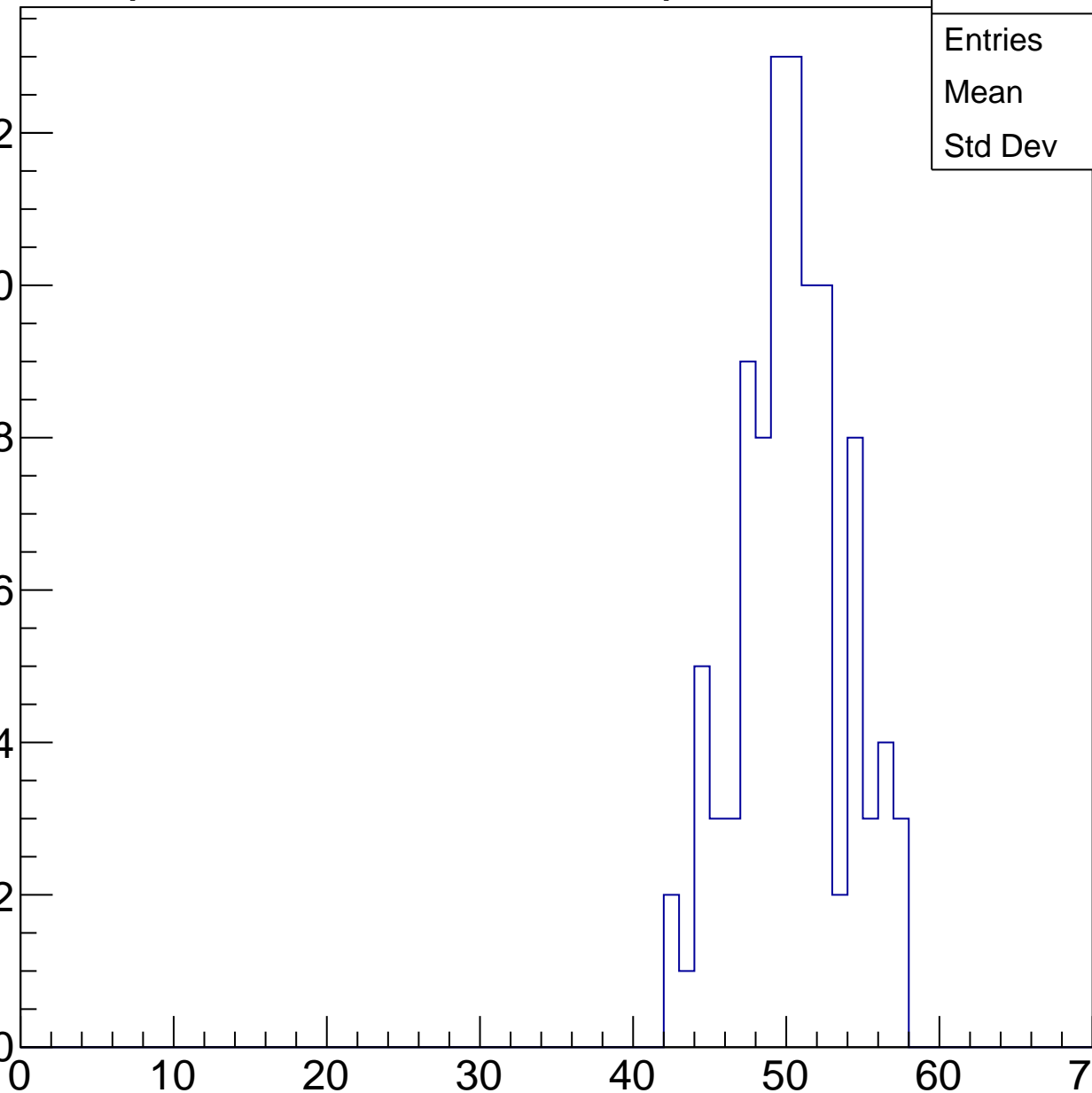
4

2

0

Entries	97
Mean	49.92
Std Dev	3.469

ampl



B0L101S, U19-ch6, adc4

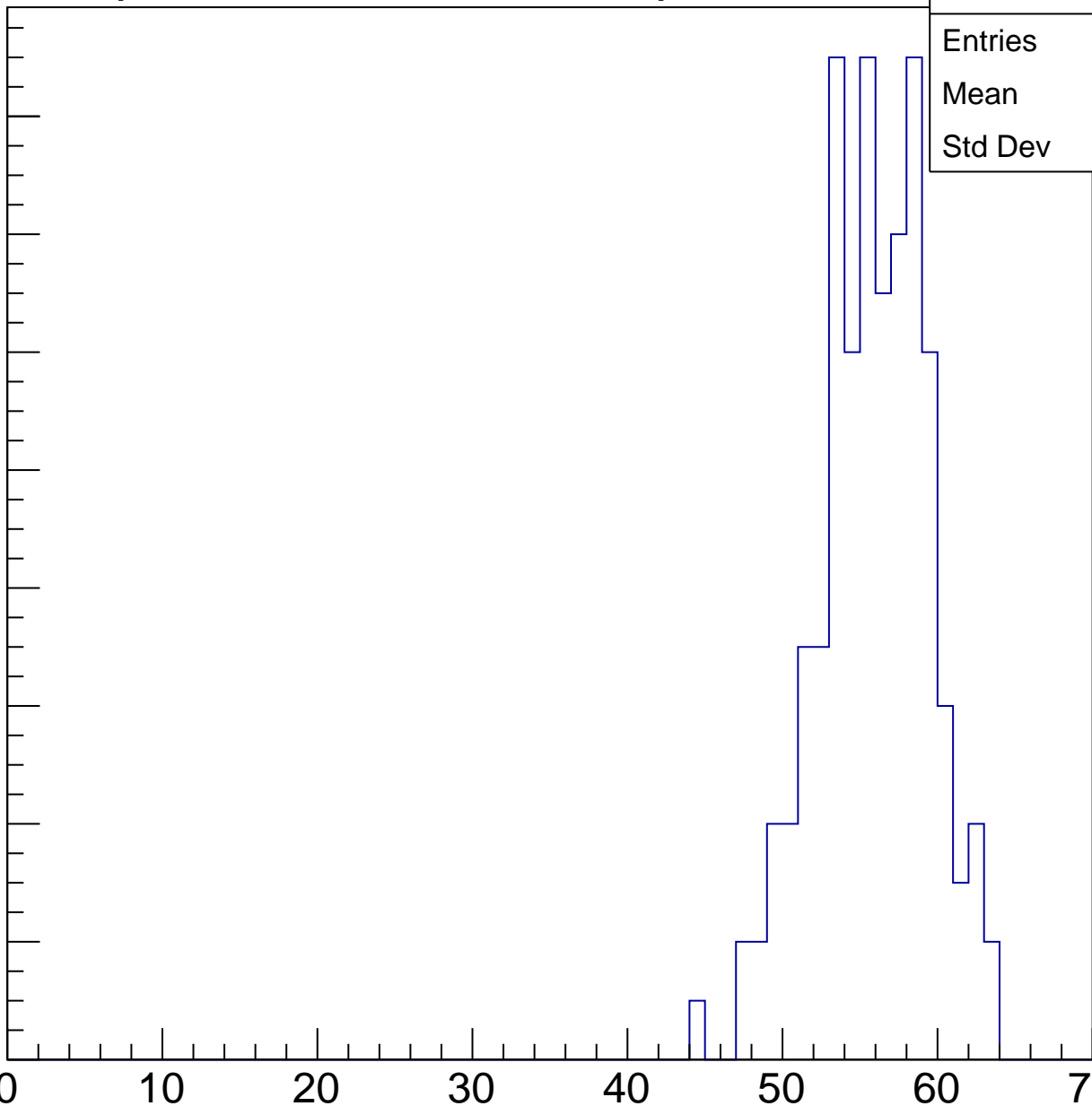
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	144
Mean	55.36
Std Dev	3.558

ampl



B0L101S, U19-ch6, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries	75
Mean	58.87
Std Dev	10.01

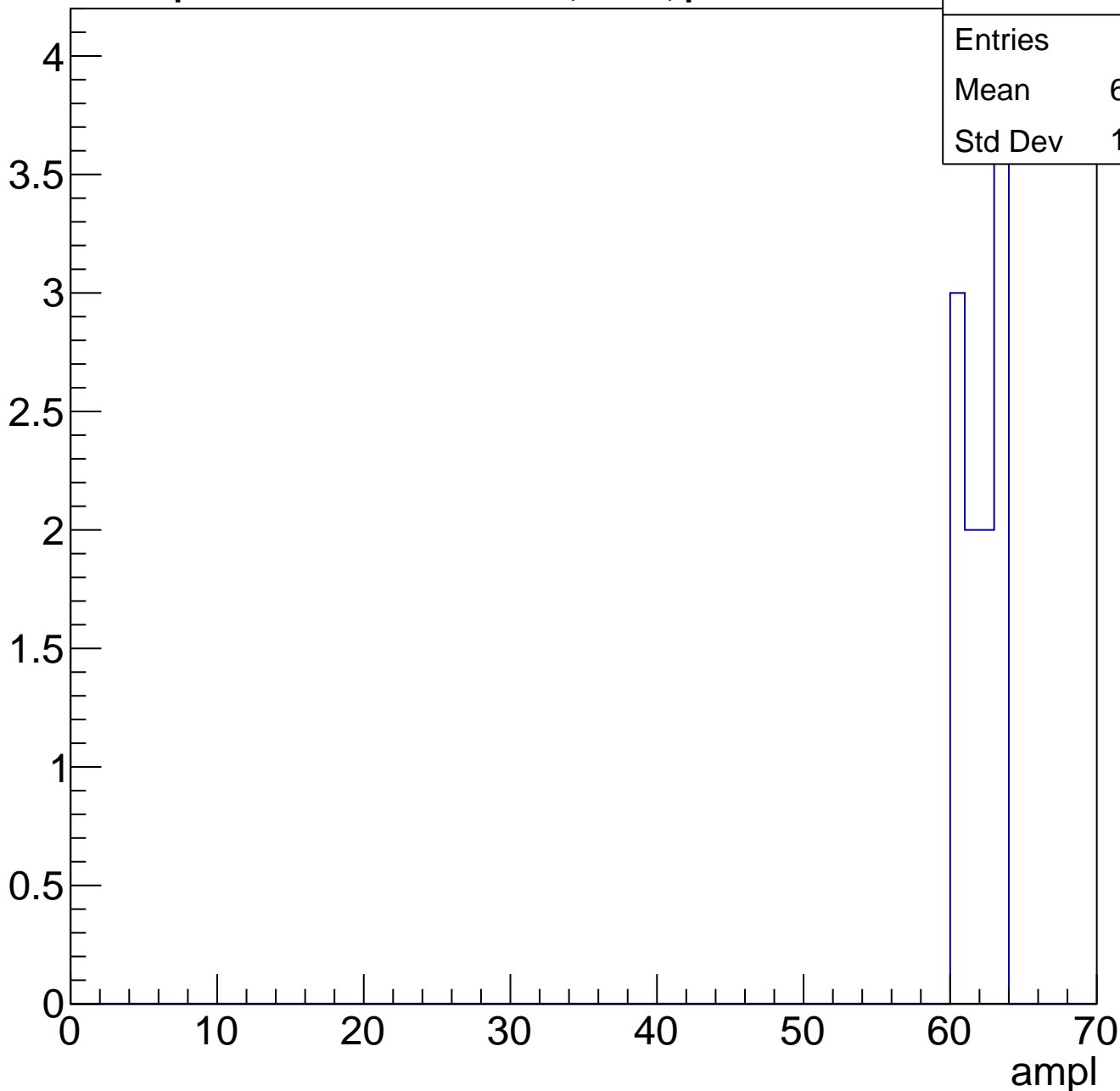
ampl

0 10 20 30 40 50 60 70

B0L101S, U19-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

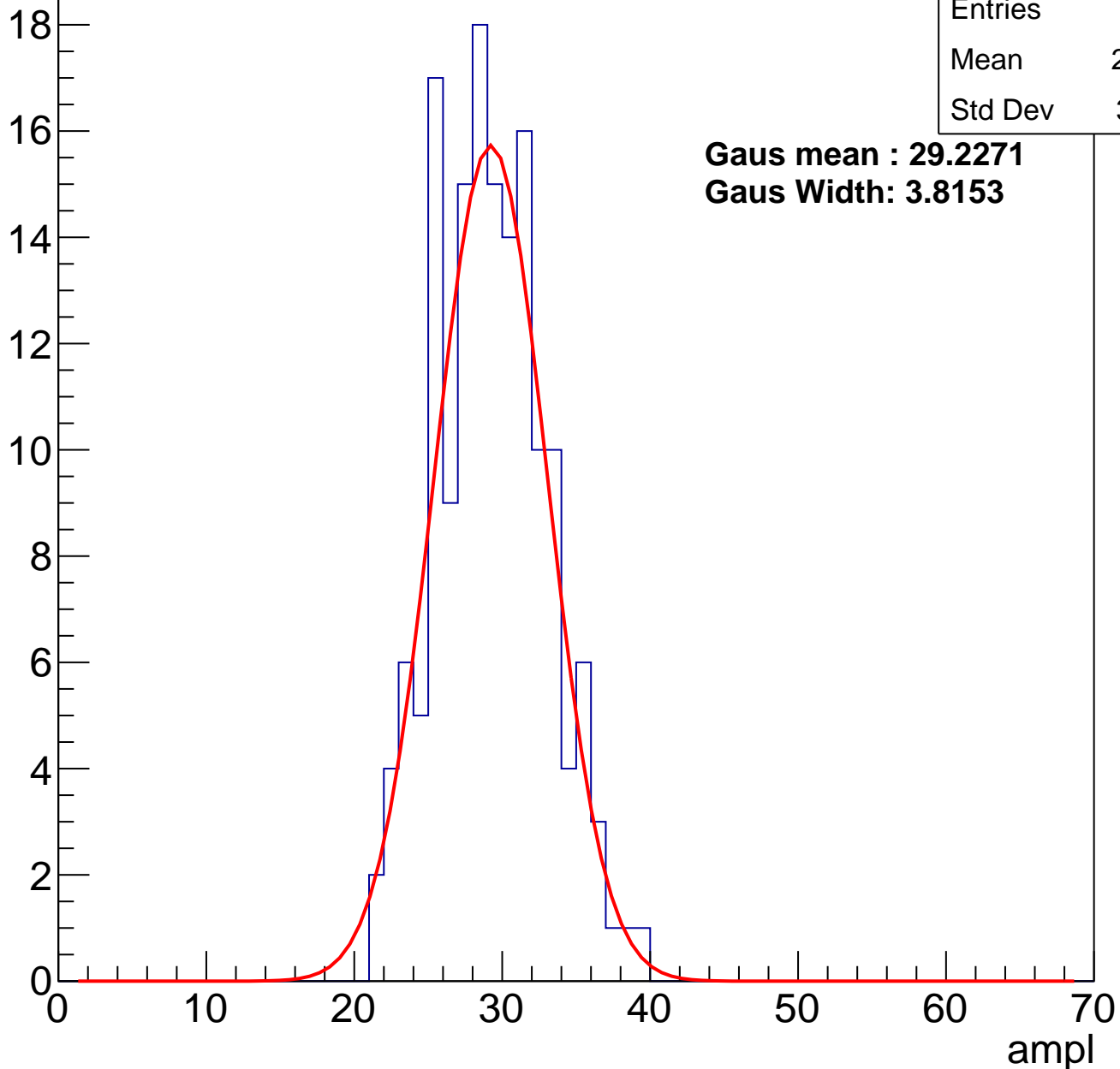
Entries	1
Mean	20
Std Dev	0

ampl

B0L101S, U19-ch7, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch7, adc1

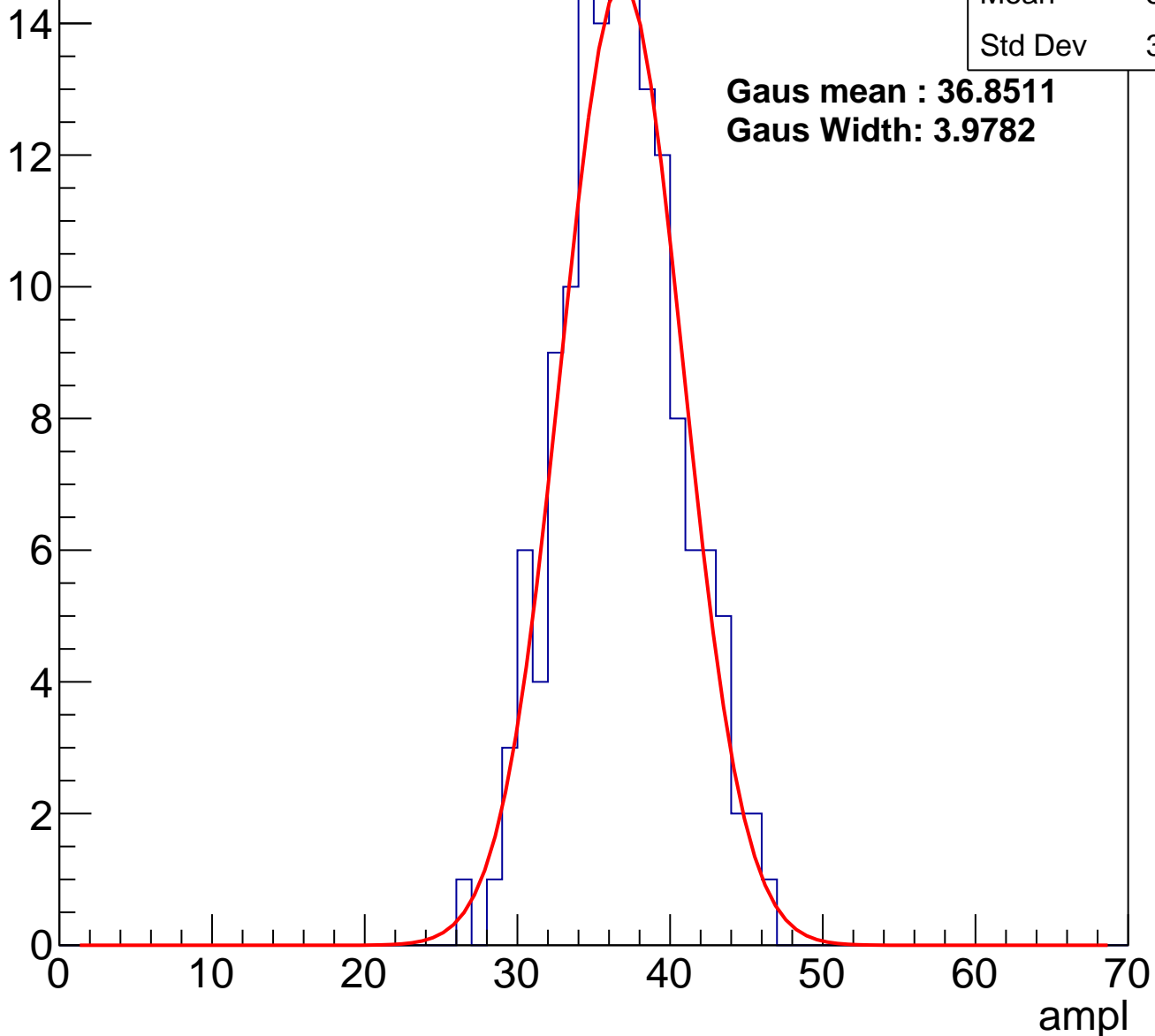
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	36.33
Std Dev	3.875

Gaus mean : 36.8511

Gaus Width: 3.9782

Entry



B0L101S, U19-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

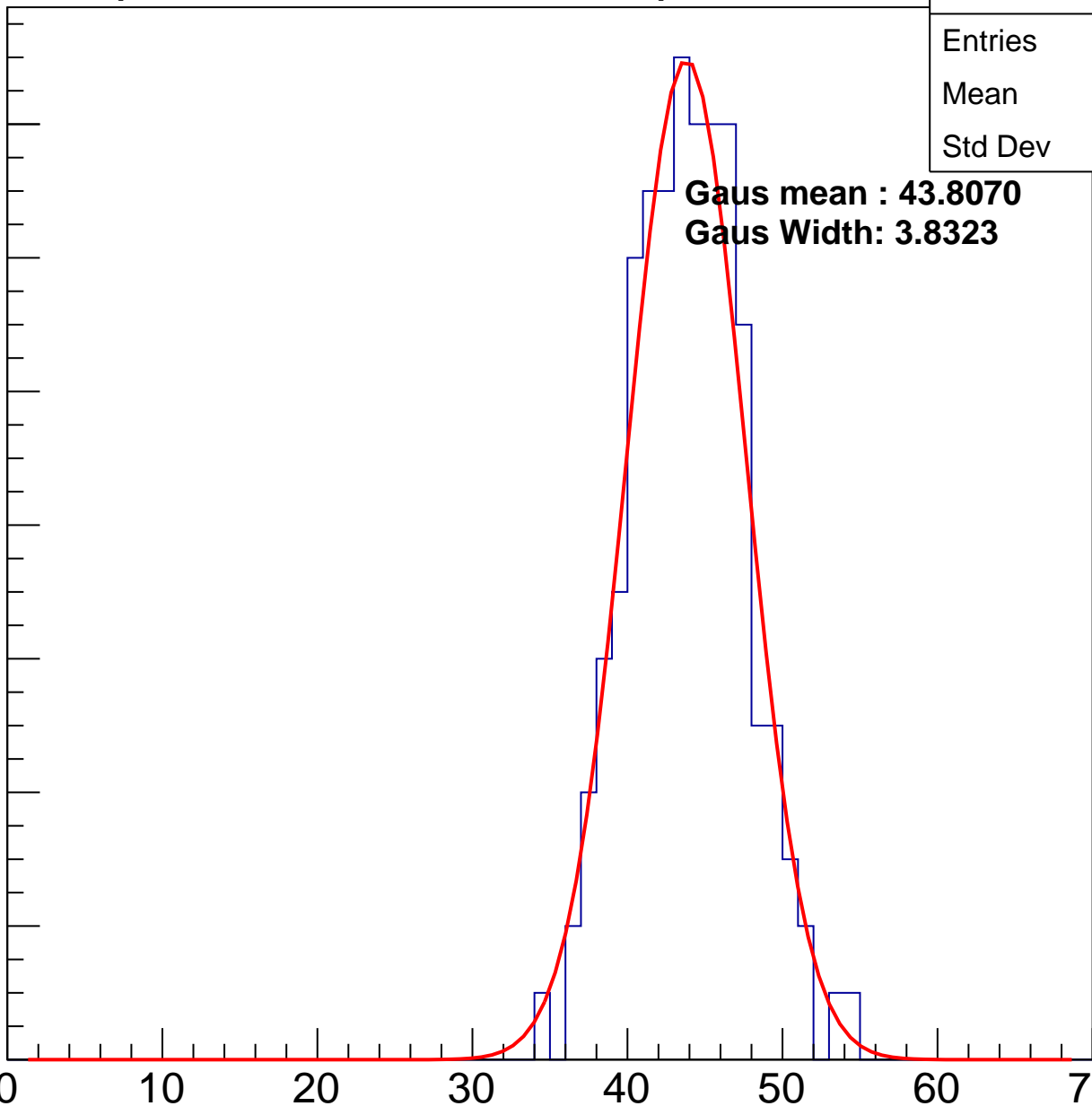
14
12
10
8
6
4
2
0

Entries	143
Mean	43.43
Std Dev	3.65

Gaus mean : 43.8070

Gaus Width: 3.8323

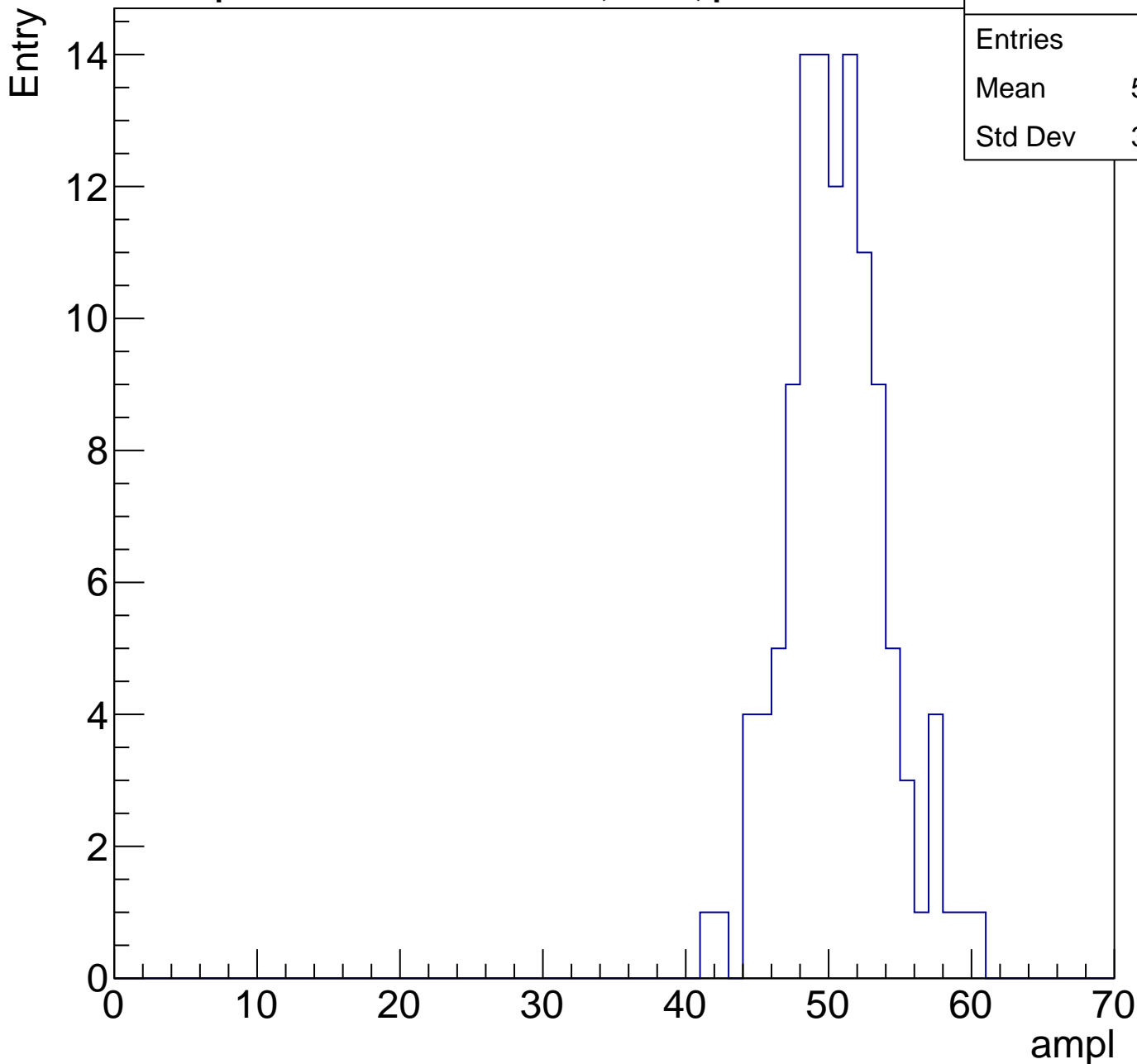
0 10 20 30 40 50 60 70
ampl



B0L101S, U19-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

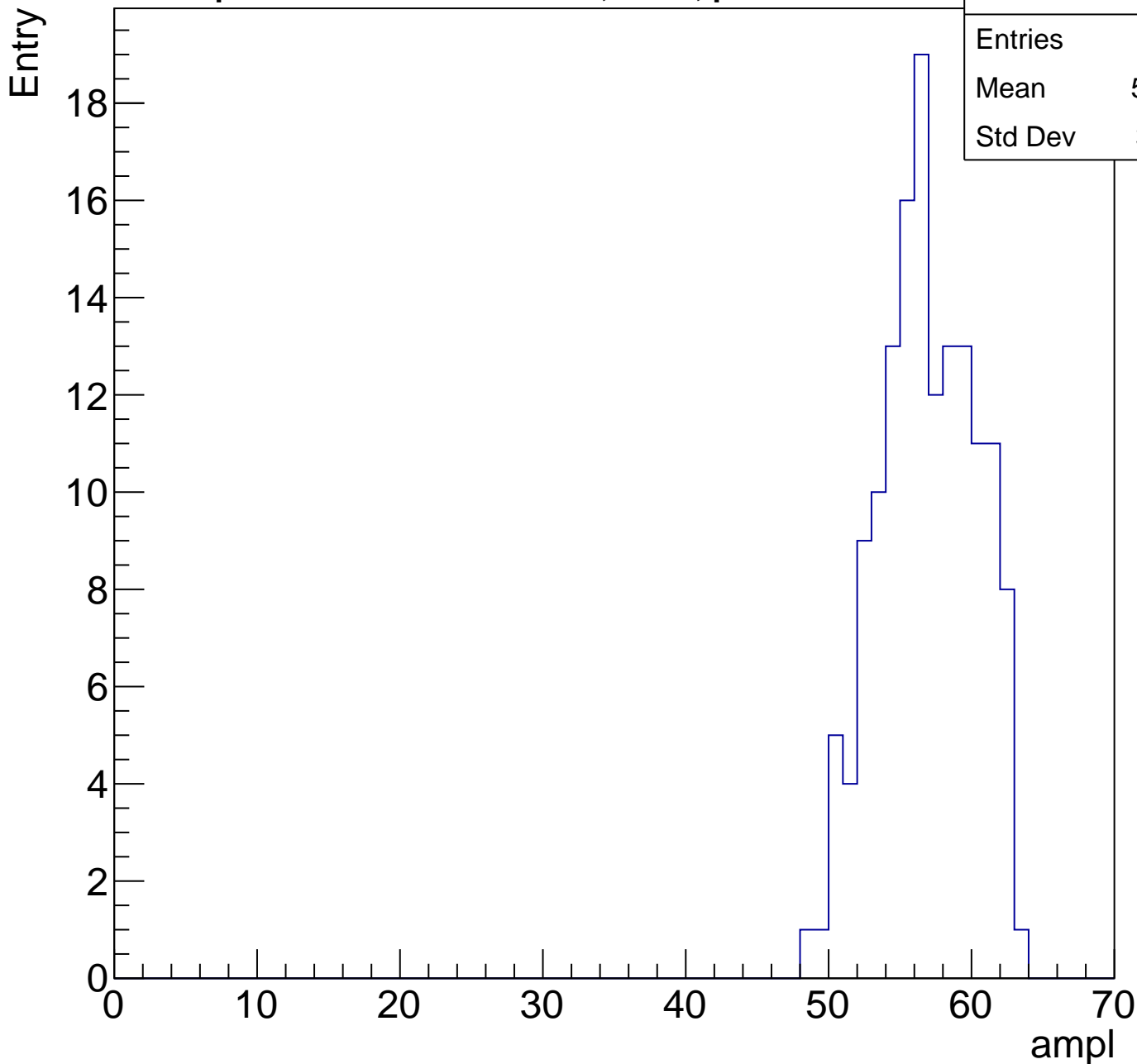
Entries	114
Mean	50.08
Std Dev	3.492



B0L101S, U19-ch7, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	56.39
Std Dev	3.341



B0L101S, U19-ch7, adc5

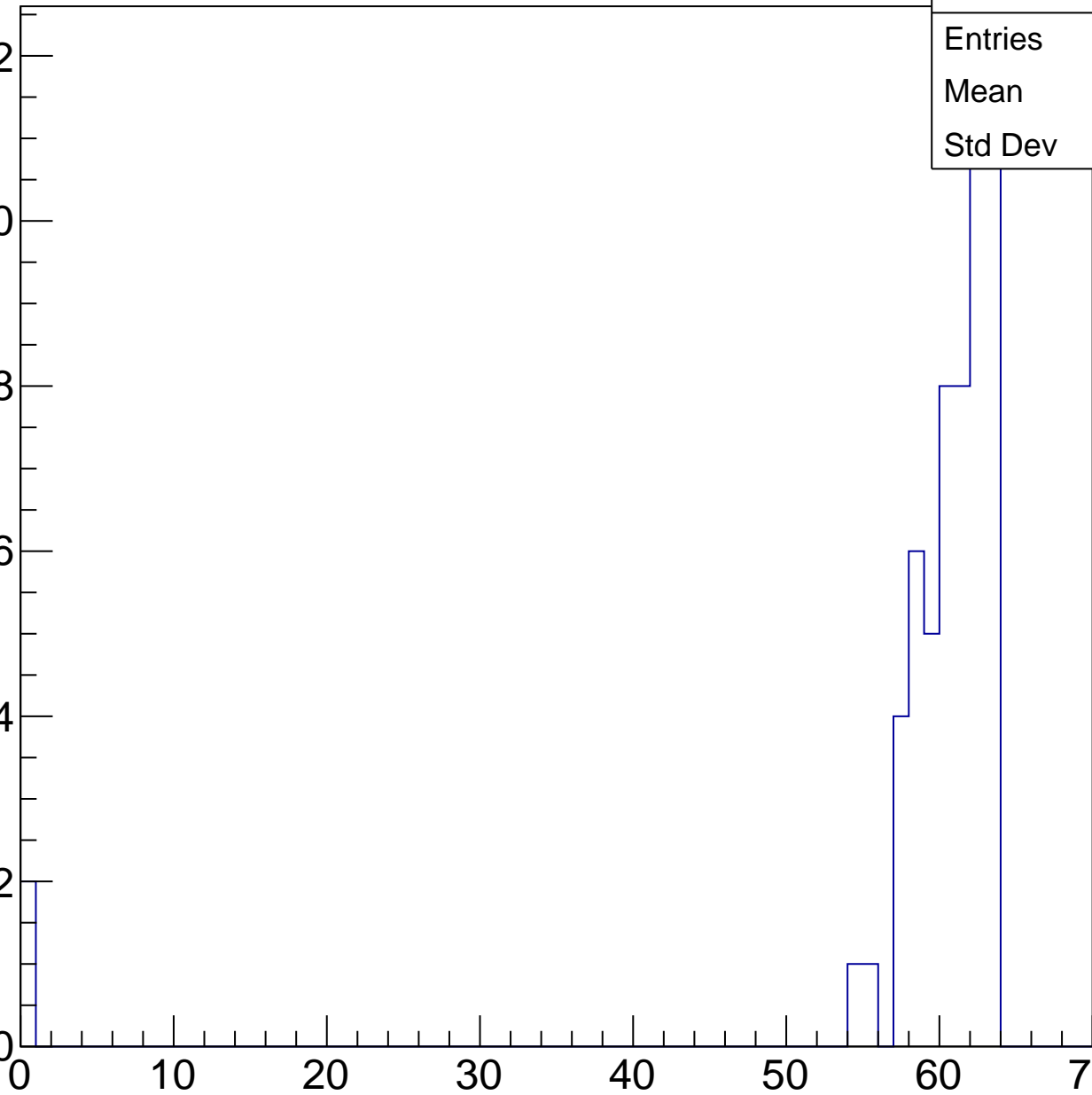
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	58
Mean	58.38
Std Dev	11.24

ampl

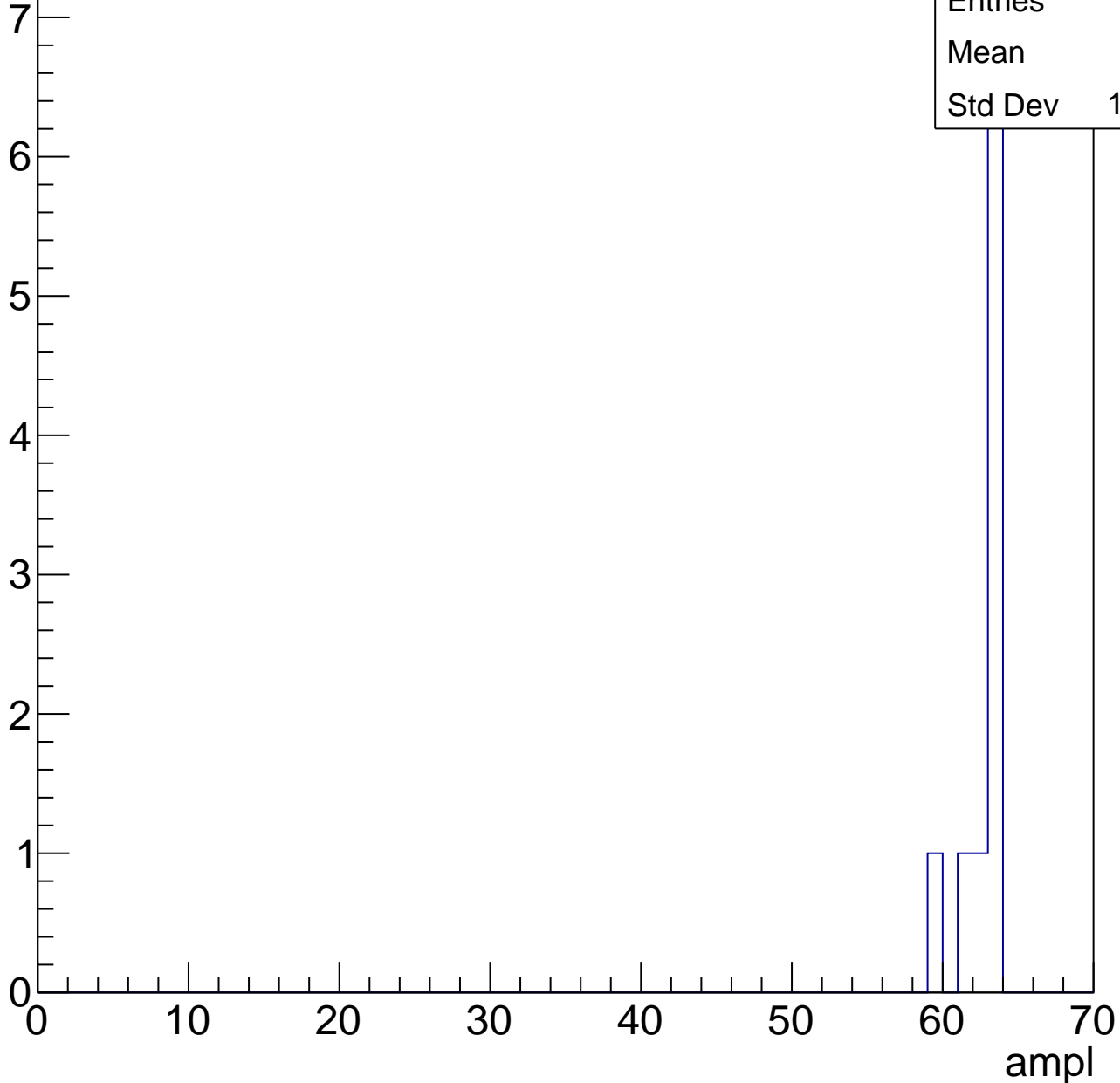


B0L101S, U19-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.3
Std Dev	1.269



B0L101S, U19-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

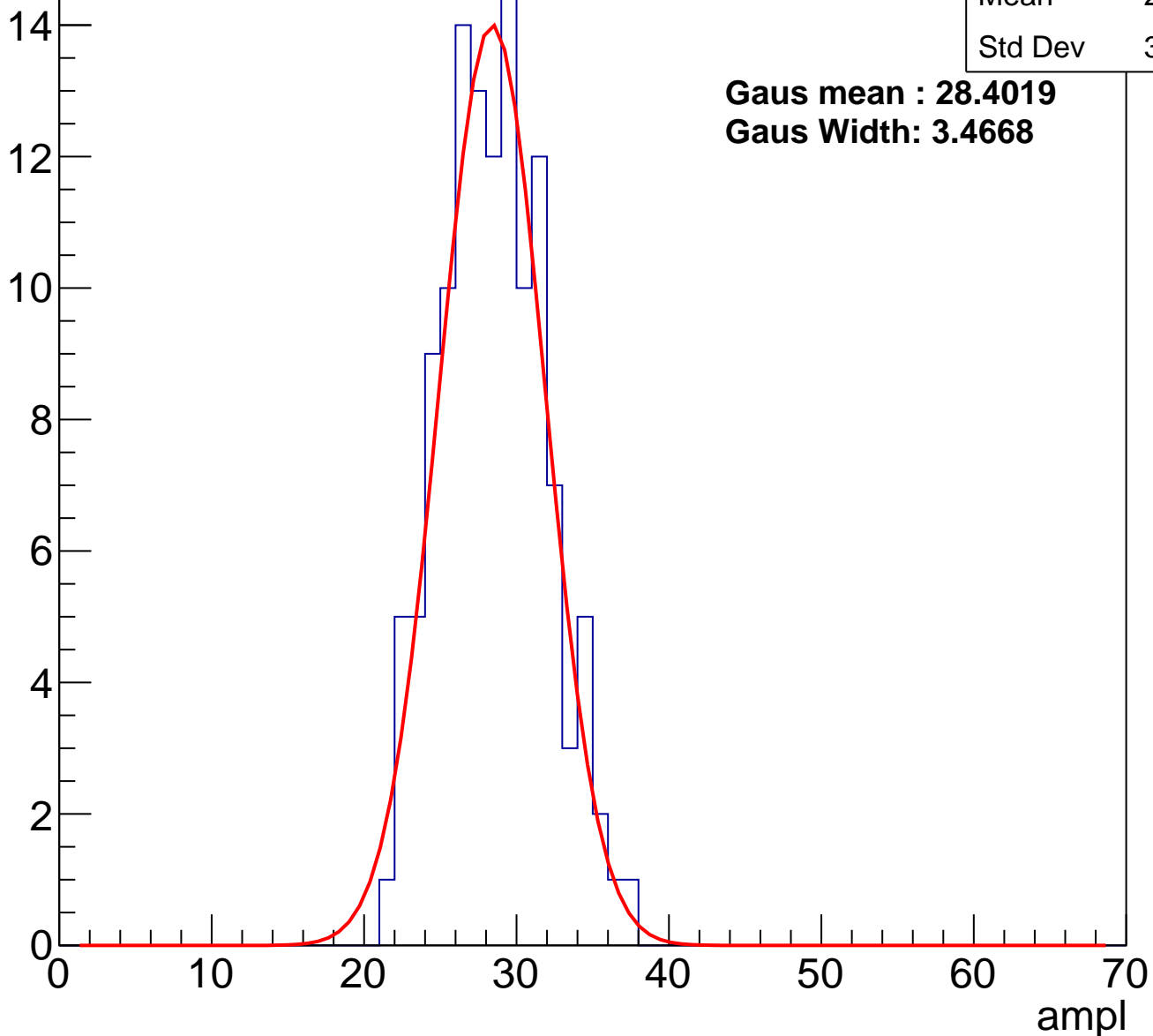


Entries	2
Mean	0
Std Dev	0

B0L101S, U19-ch8, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	125
Mean	28.05
Std Dev	3.382

Gaus mean : 28.4019

Gaus Width: 3.4668

B0L101S, U19-ch8, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

142

Mean

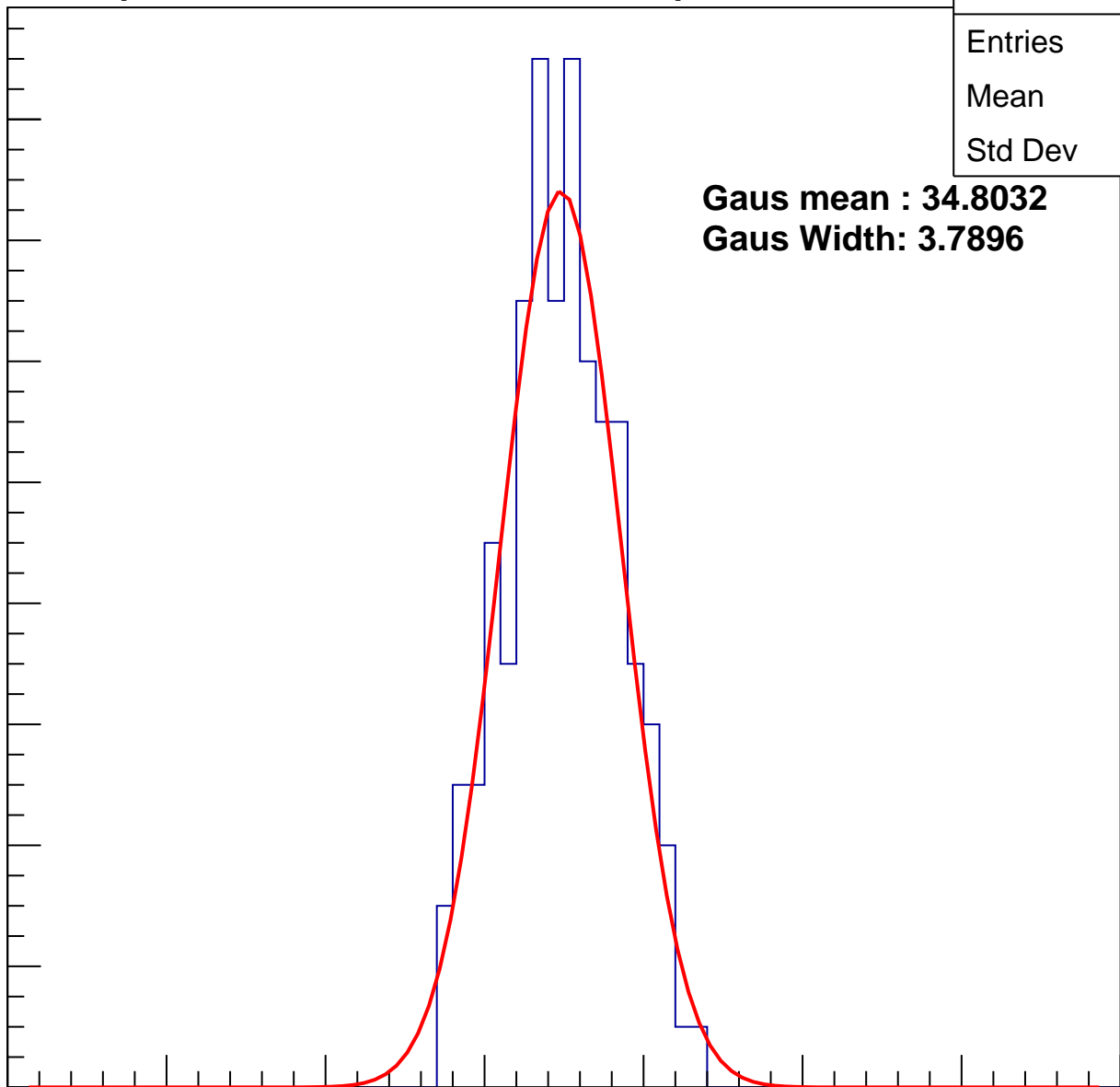
34.41

Std Dev

3.537

Gaus mean : 34.8032

Gaus Width: 3.7896



B0L101S, U19-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

117

Mean

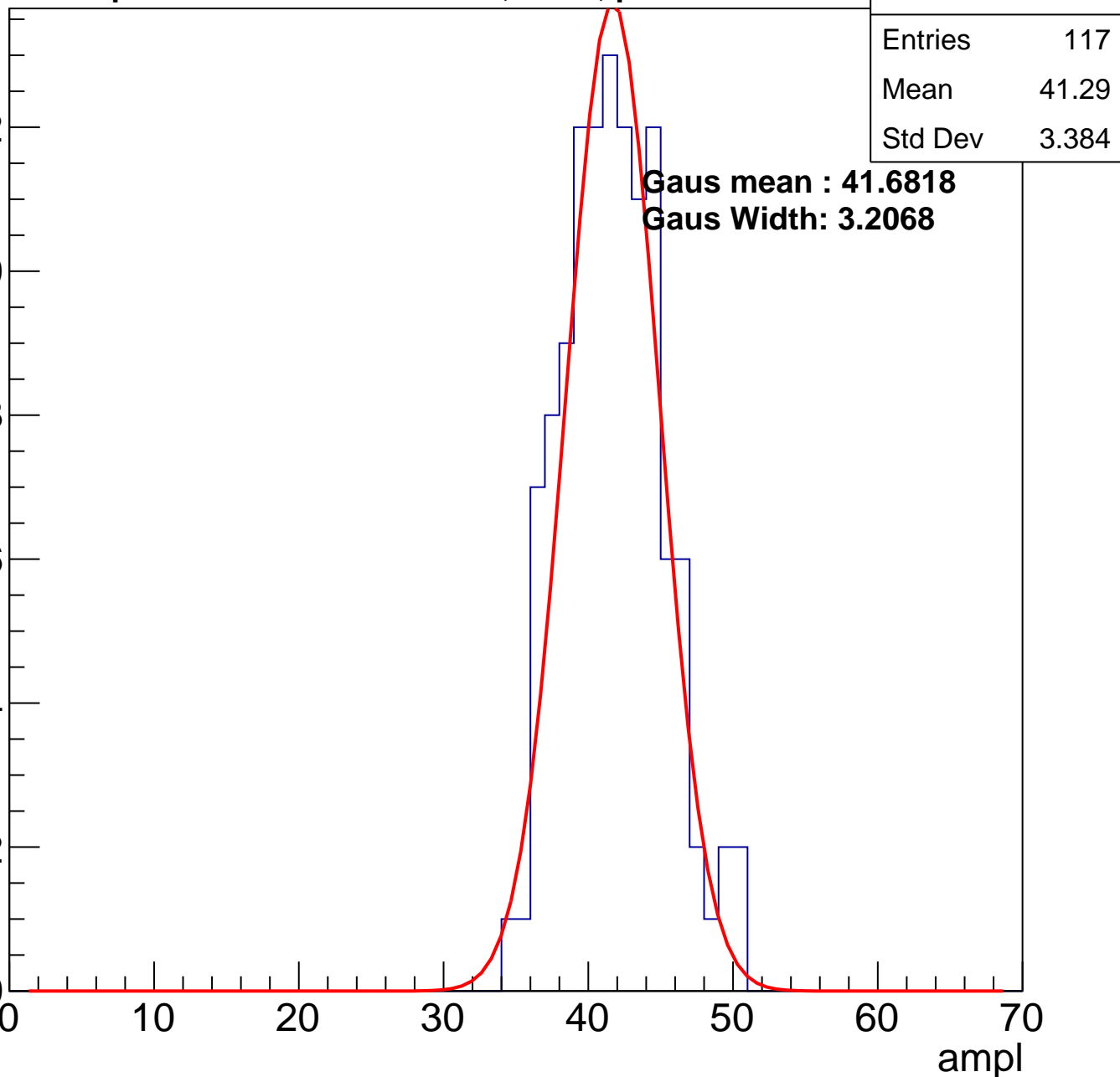
41.29

Std Dev

3.384

Gaus mean : 41.6818

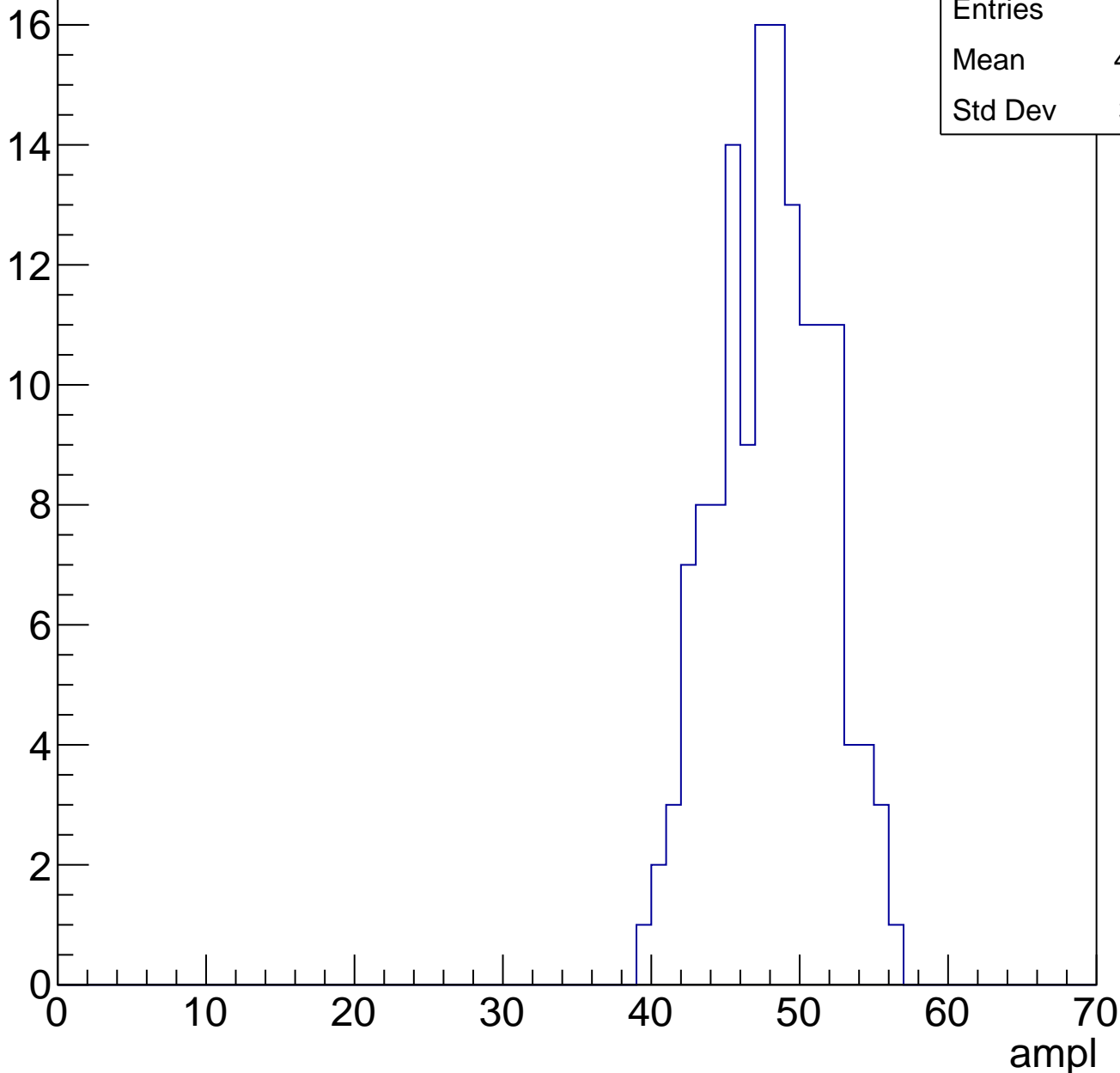
Gaus Width: 3.2068



B0L101S, U19-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

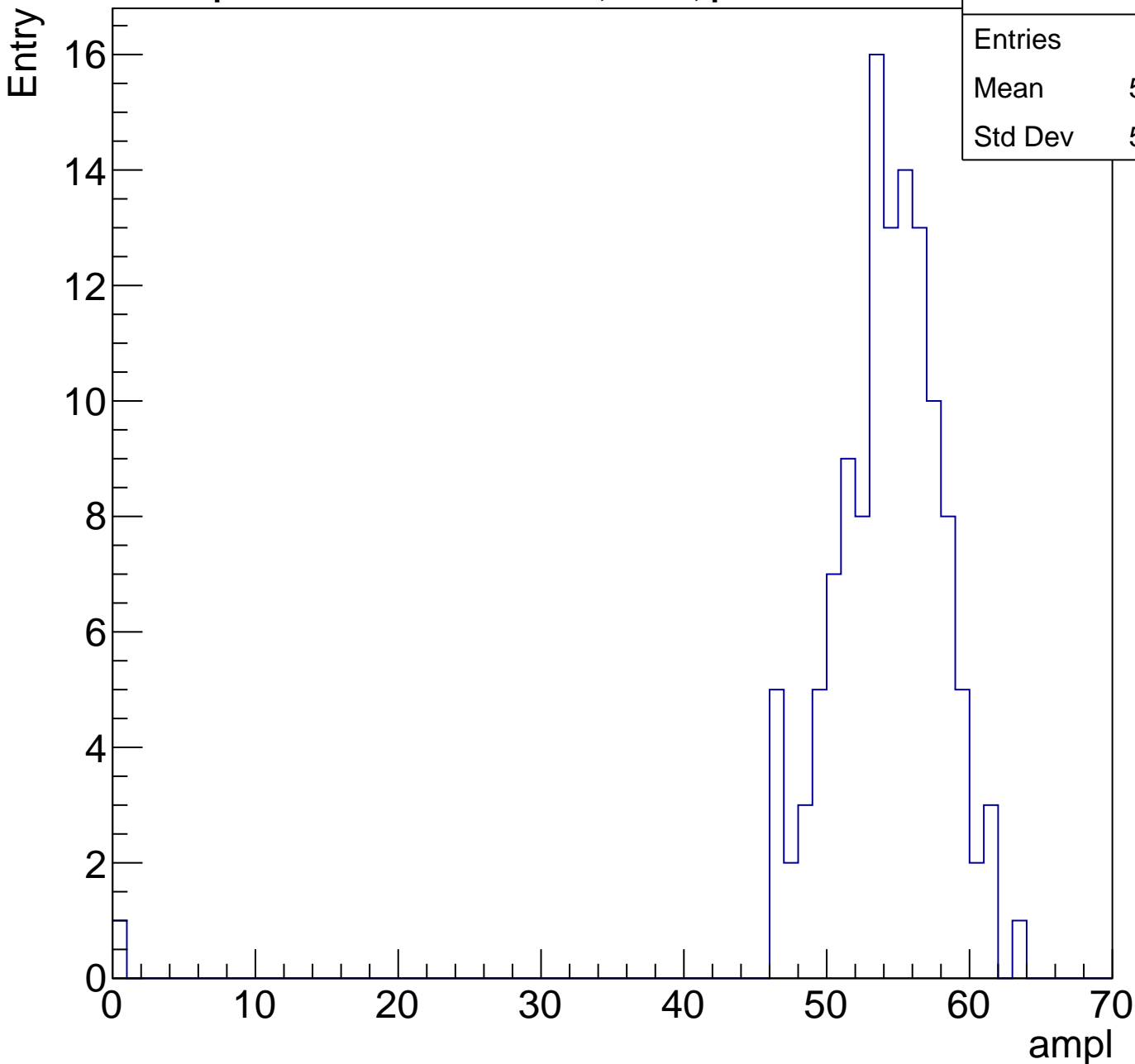


Entries	142
Mean	47.64
Std Dev	3.641

B0L101S, U19-ch8, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	53.45
Std Dev	5.986



B0L101S, U19-ch8, adc5

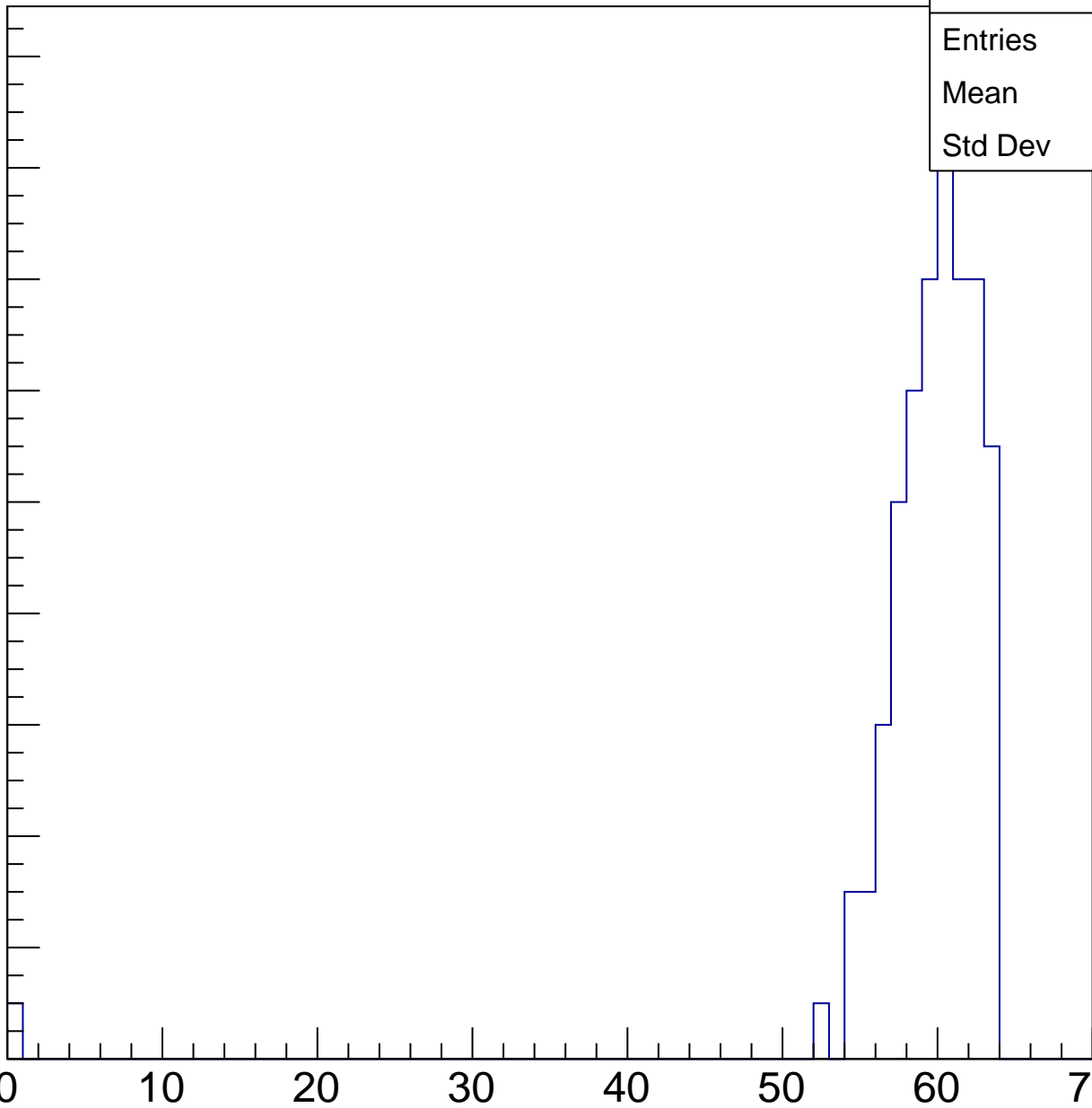
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.9
Std Dev	6.216

ampl

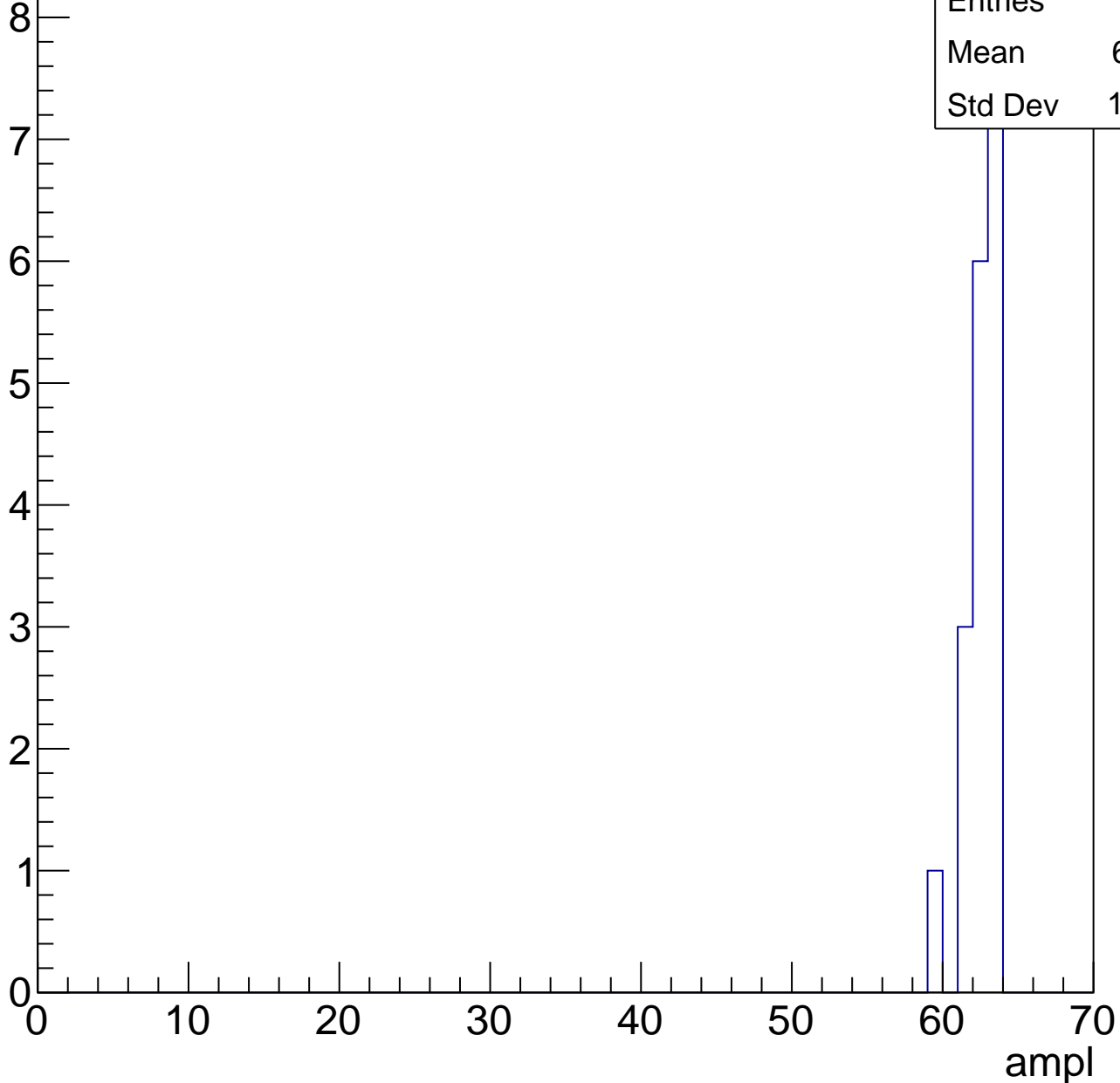


B0L101S, U19-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

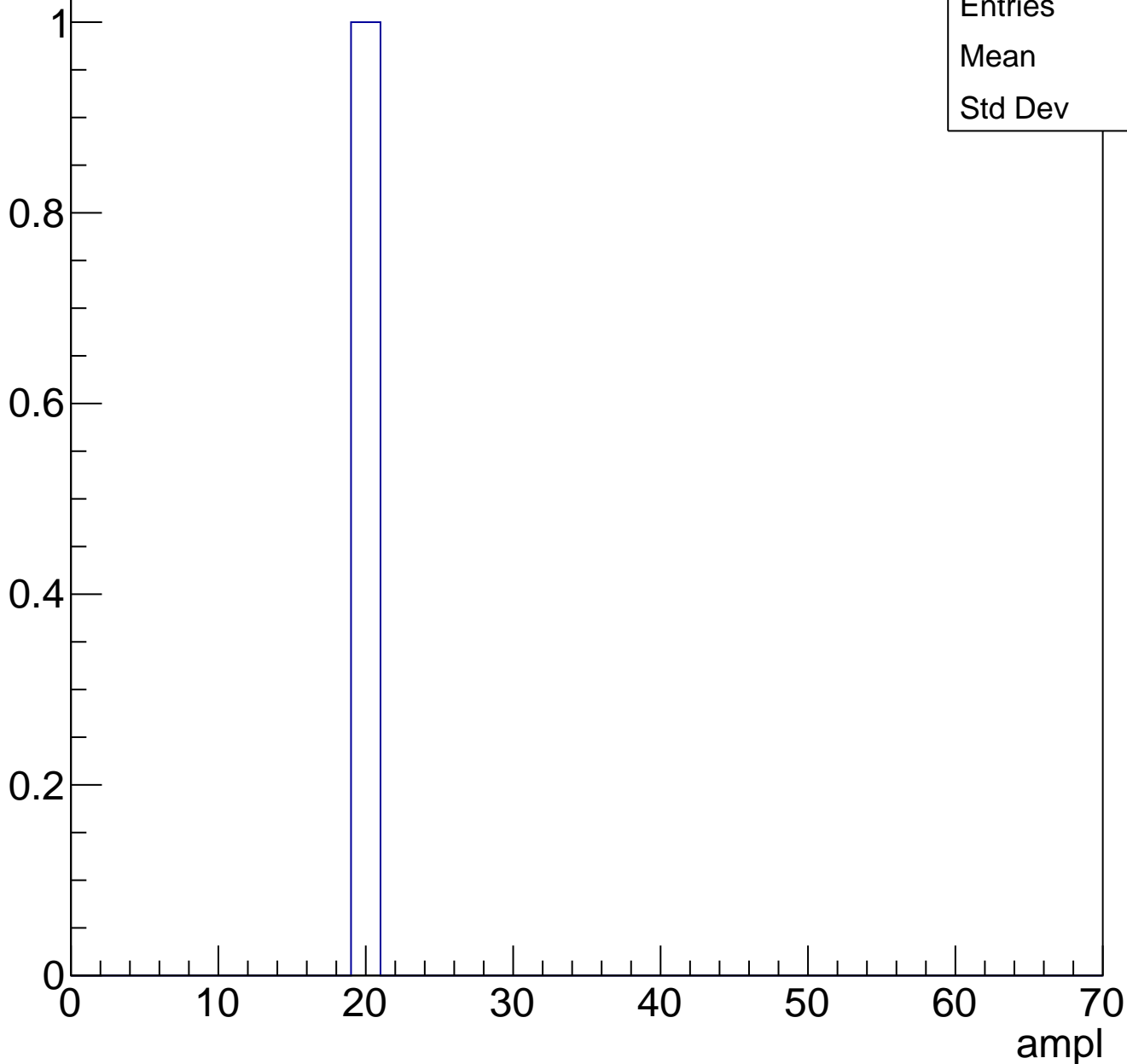
Entries	18
Mean	62.11
Std Dev	1.048



B0L101S, U19-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch9, adc0

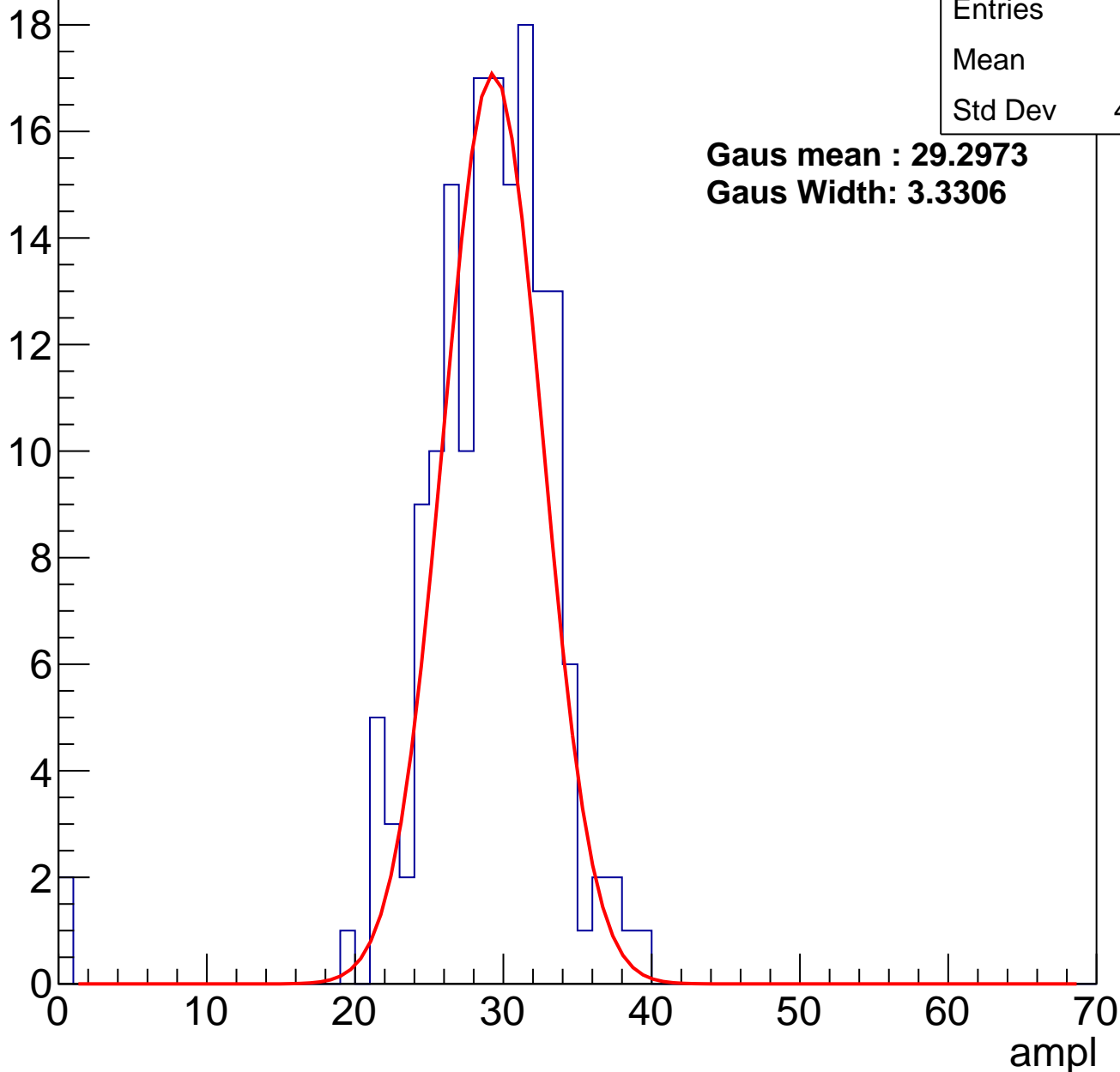
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	28.5
Std Dev	4.868

Gaus mean : 29.2973

Gaus Width: 3.3306

Entry



B0L101S, U19-ch9, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.02
Std Dev	3.309

Gaus mean : 37.0023

Gaus Width: 2.8673

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

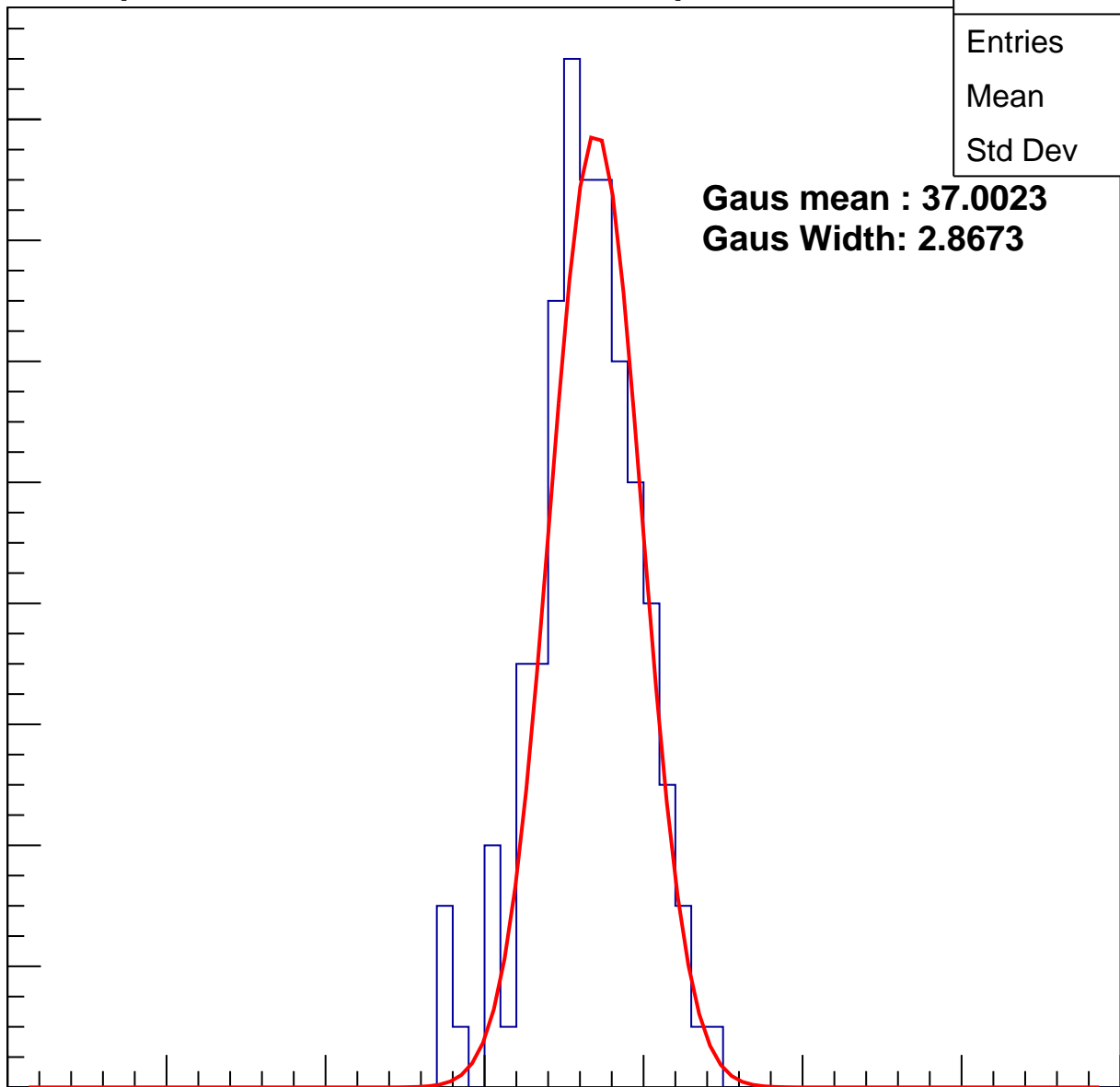
40

50

60

70

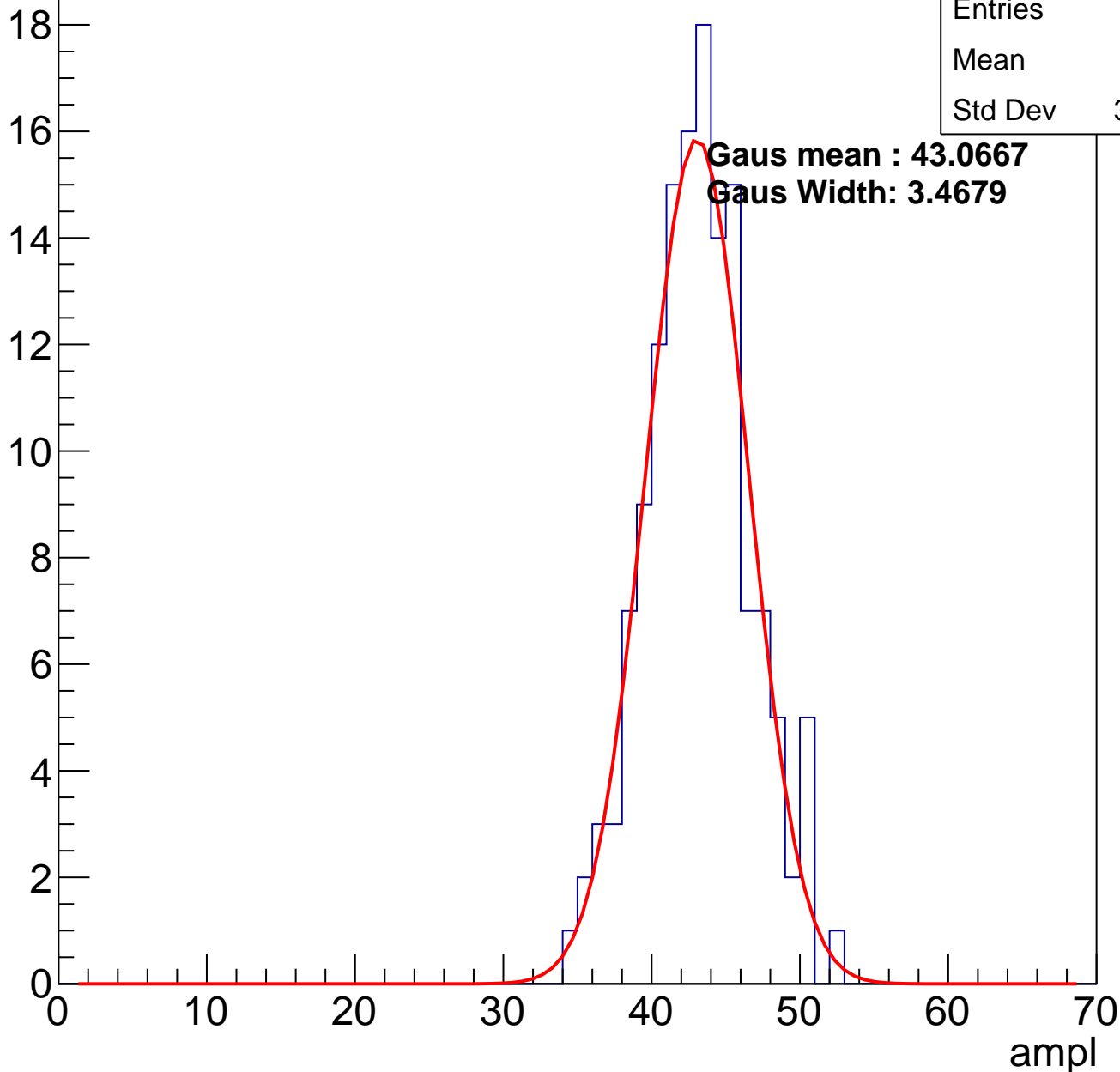
ampl



B0L101S, U19-ch9, adc2

calib_packv5_042523_0143.root, FC#1, port C1

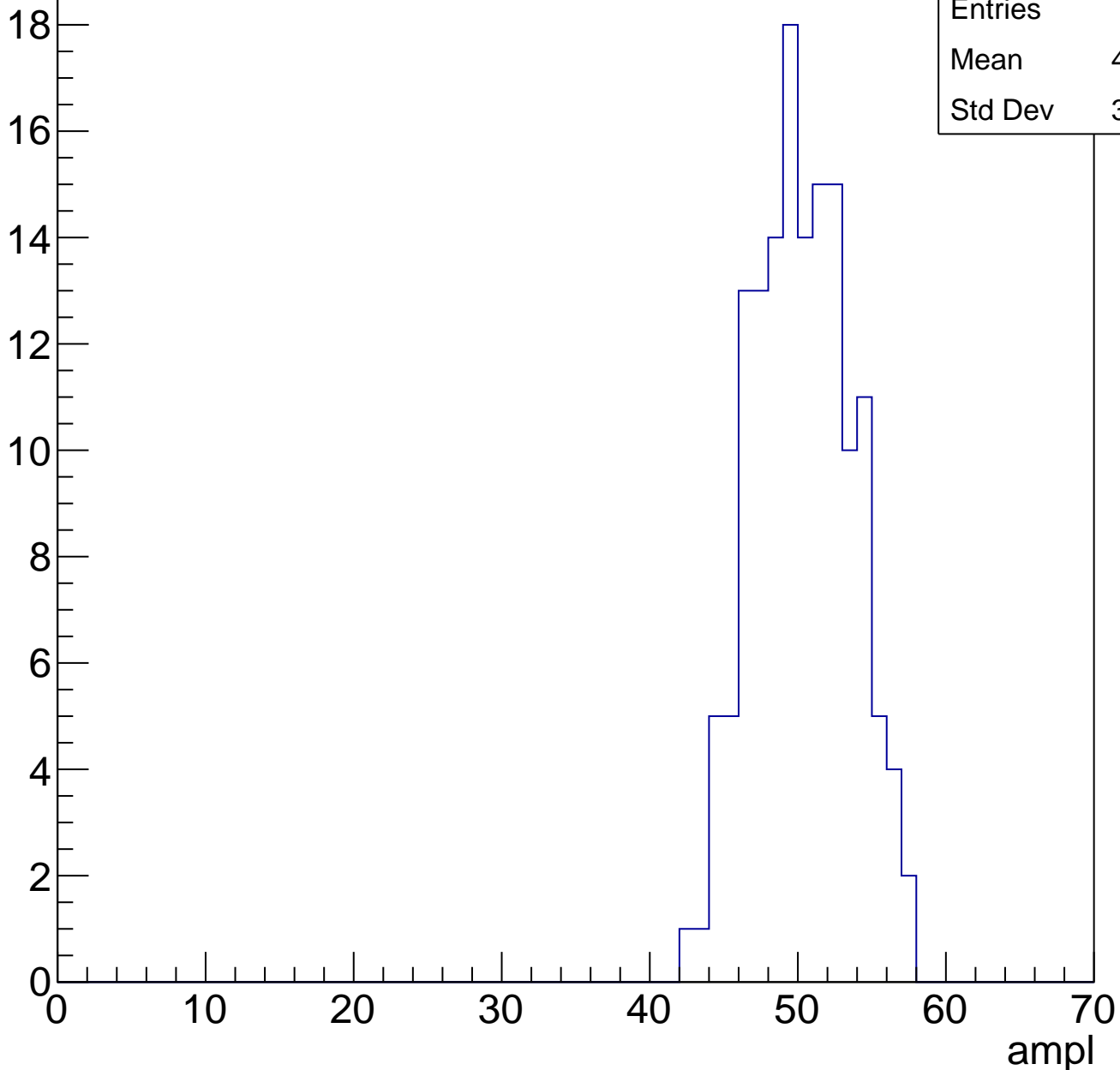
Entry



B0L101S, U19-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

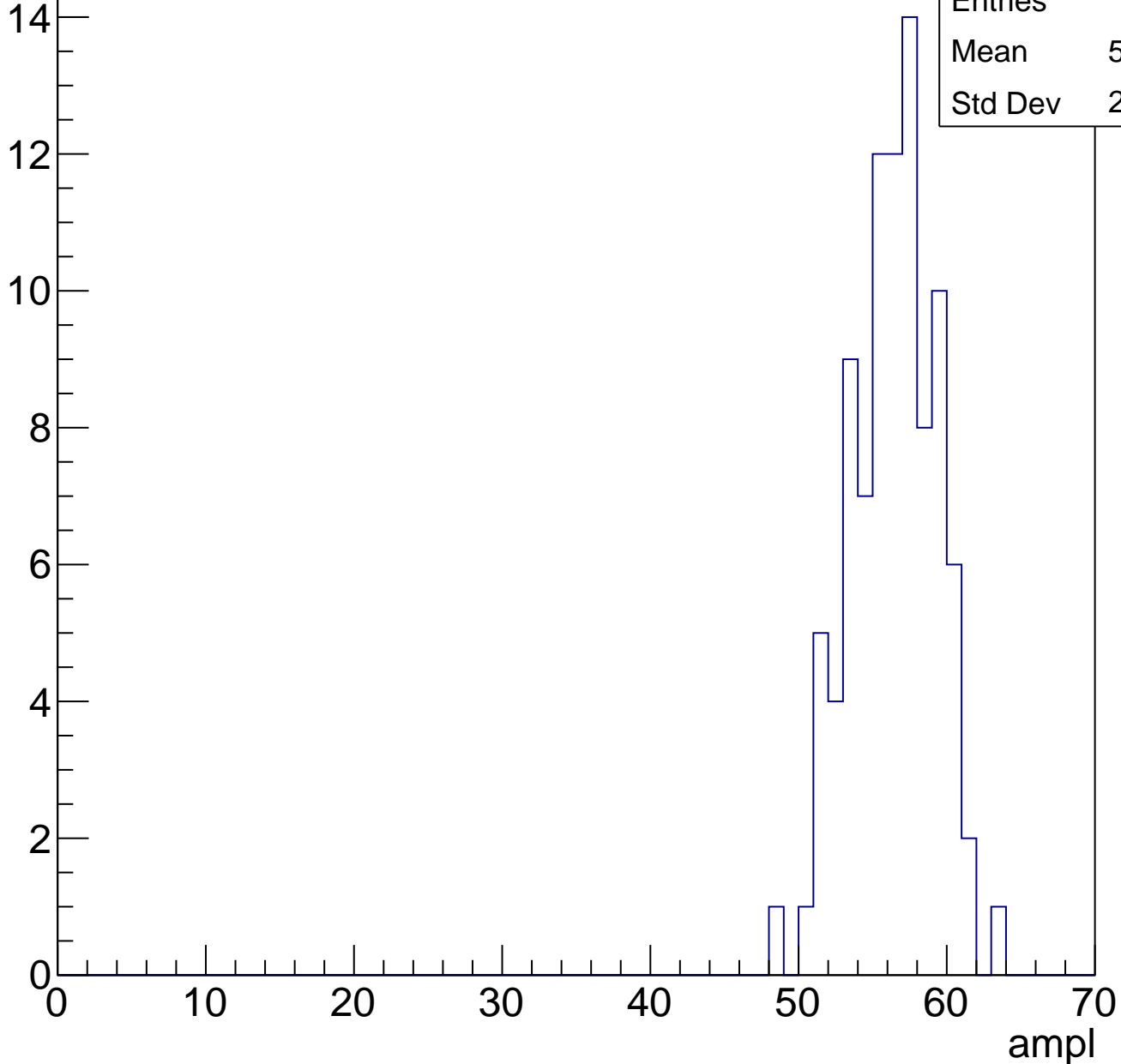
Entry



B0L101S, U19-ch9, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch9, adc5

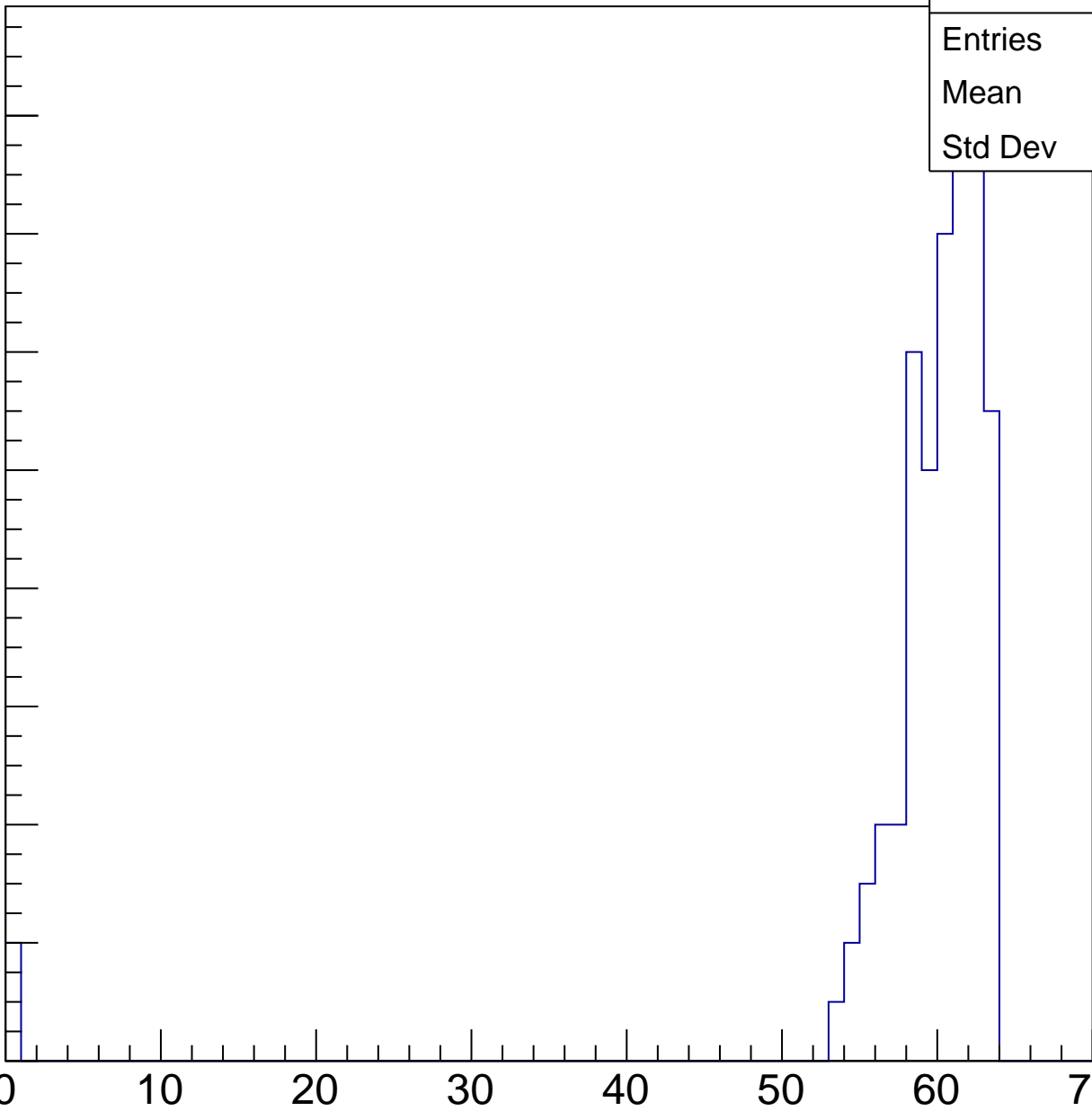
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.64
Std Dev	8.827

ampl

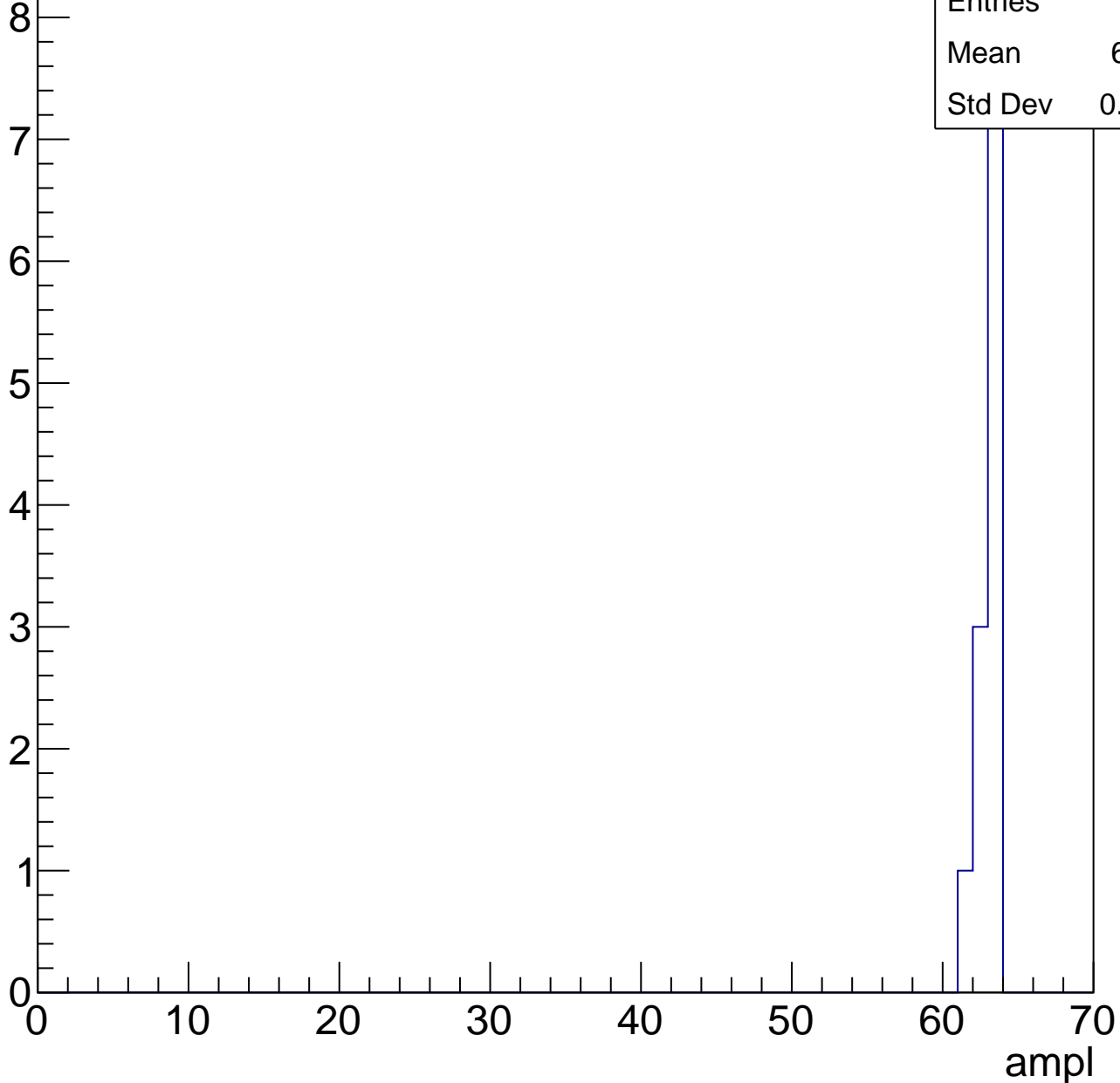


B0L101S, U19-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	62.58
Std Dev	0.6401



B0L101S, U19-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch10, adc0

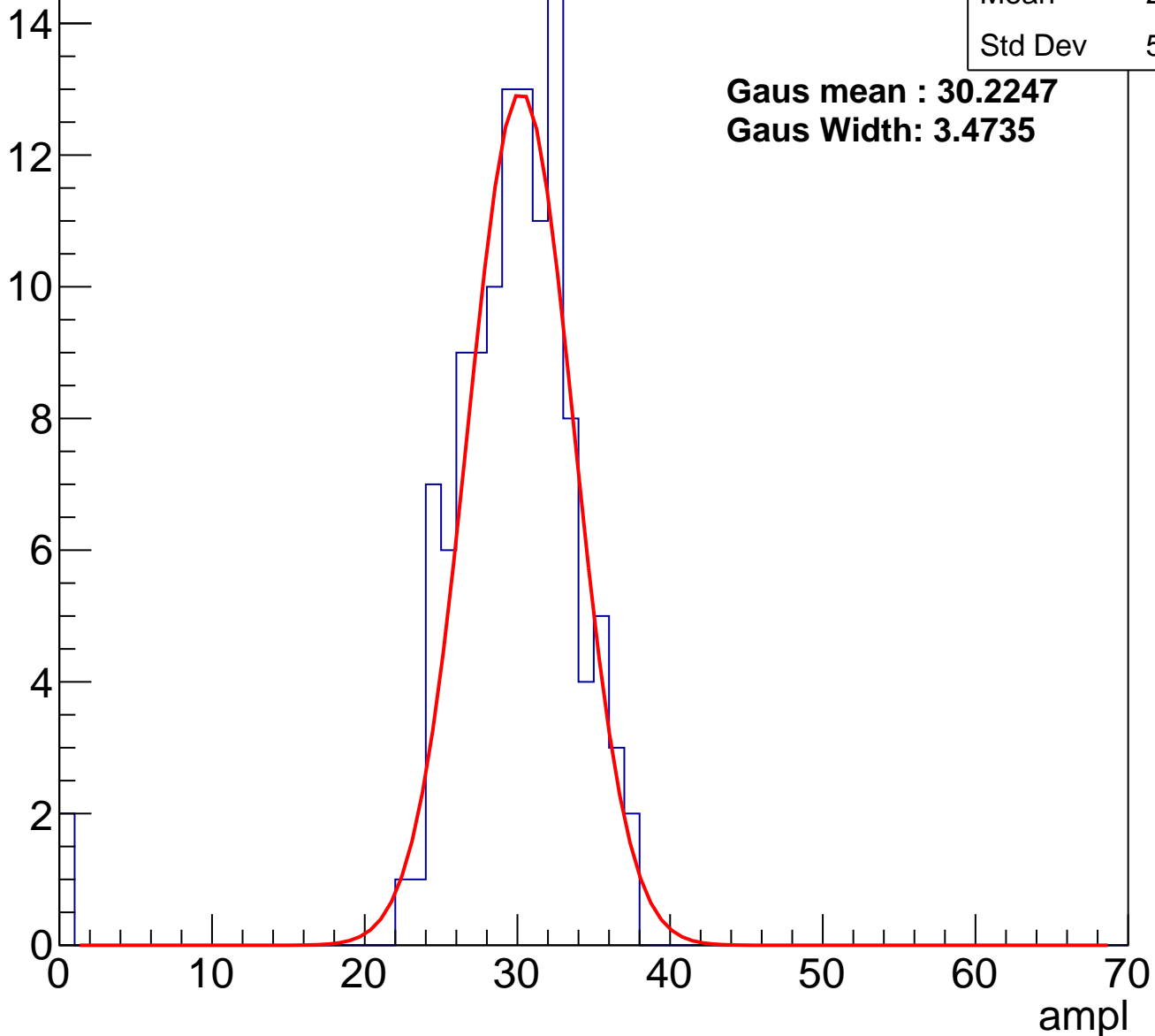
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	29.12
Std Dev	5.053

Gaus mean : 30.2247

Gaus Width: 3.4735

Entry



B0L101S, U19-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

138

Mean

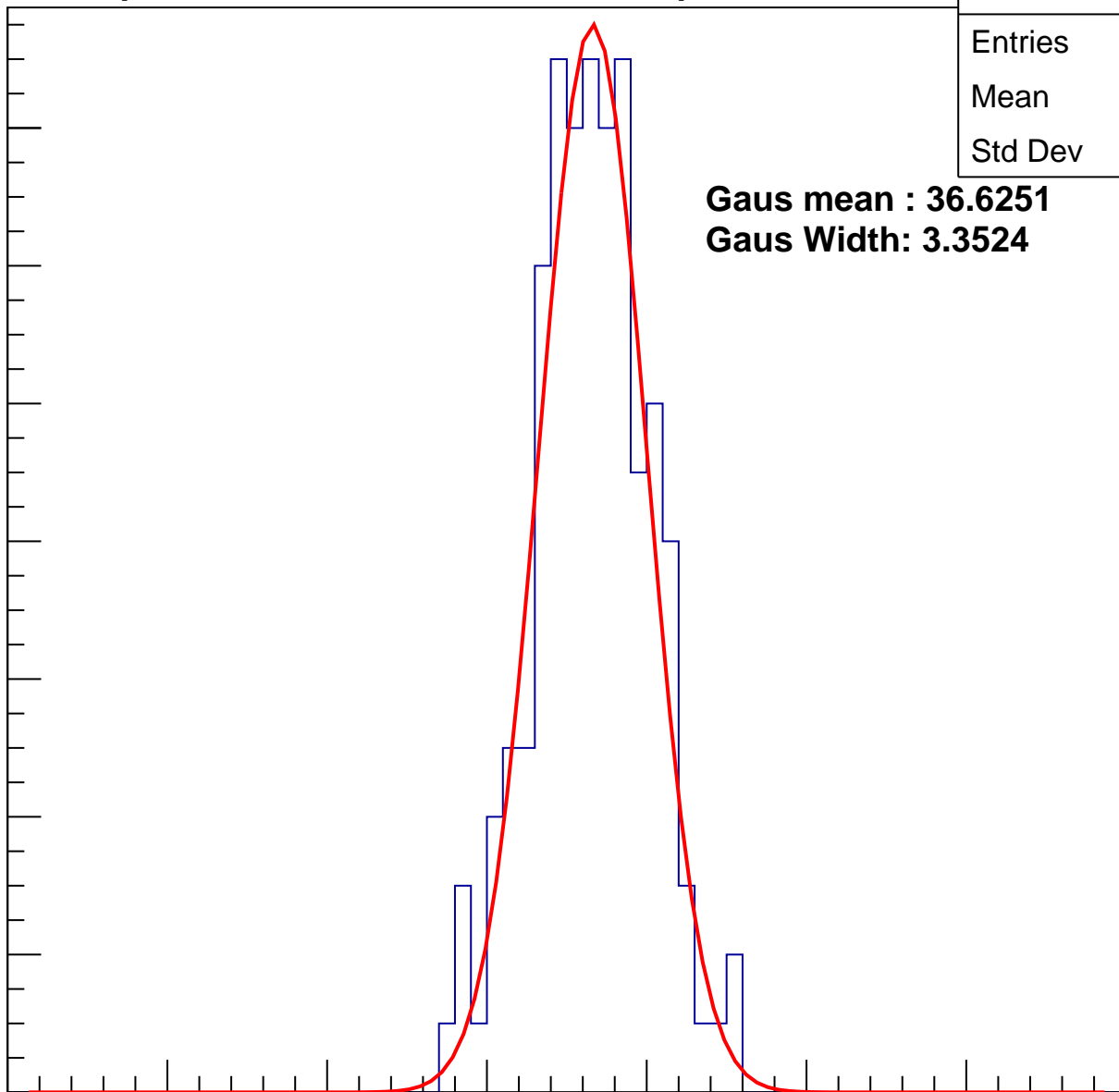
36.09

Std Dev

3.561

Gaus mean : 36.6251

Gaus Width: 3.3524



B0L101S, U19-ch10, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	42.97
Std Dev	3.453

Gaus mean : 43.3733

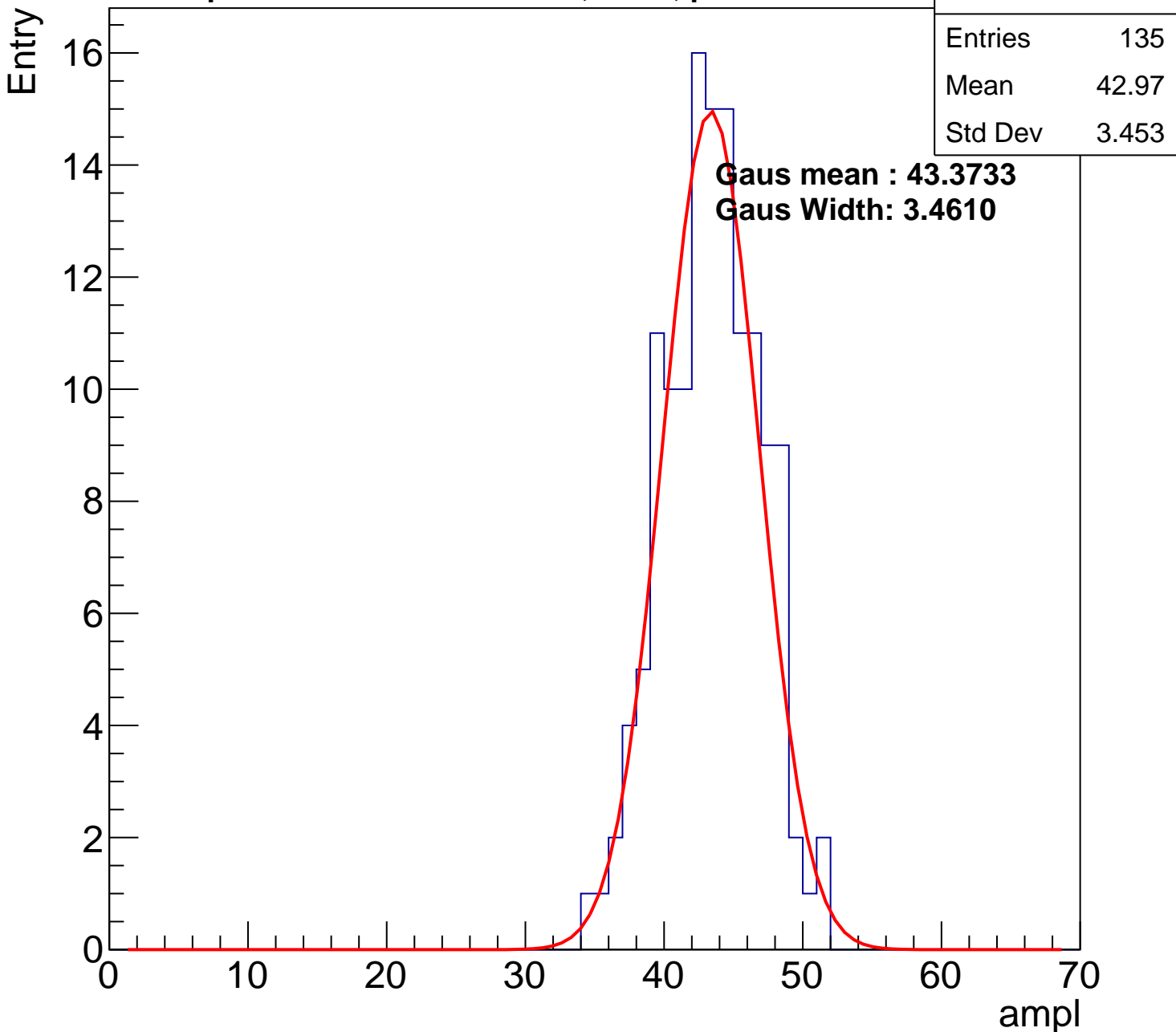
Gaus Width: 3.4610

Entry

16
14
12
10
8
6
4
2
0

ampl

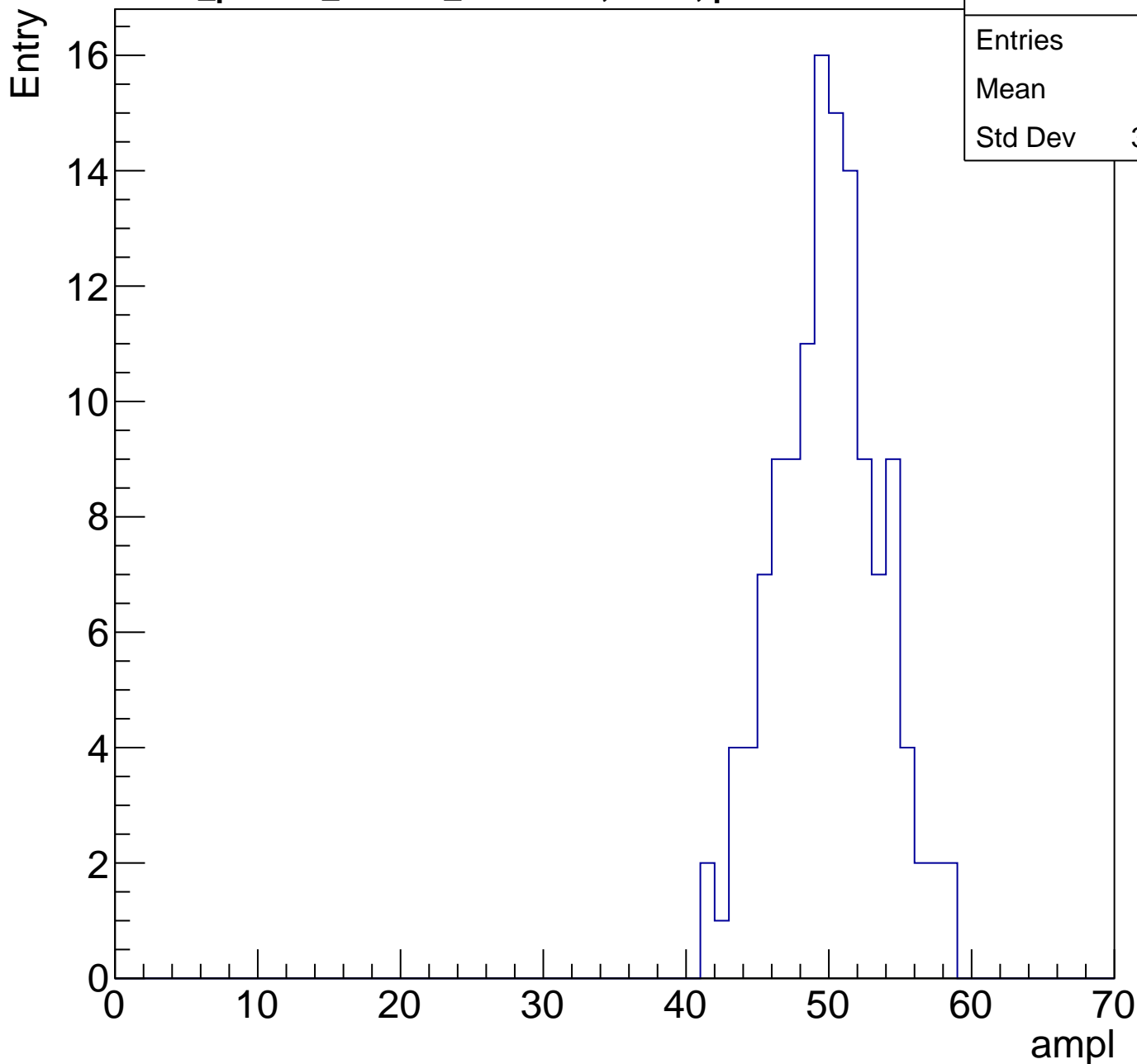
0 10 20 30 40 50 60 70



B0L101S, U19-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

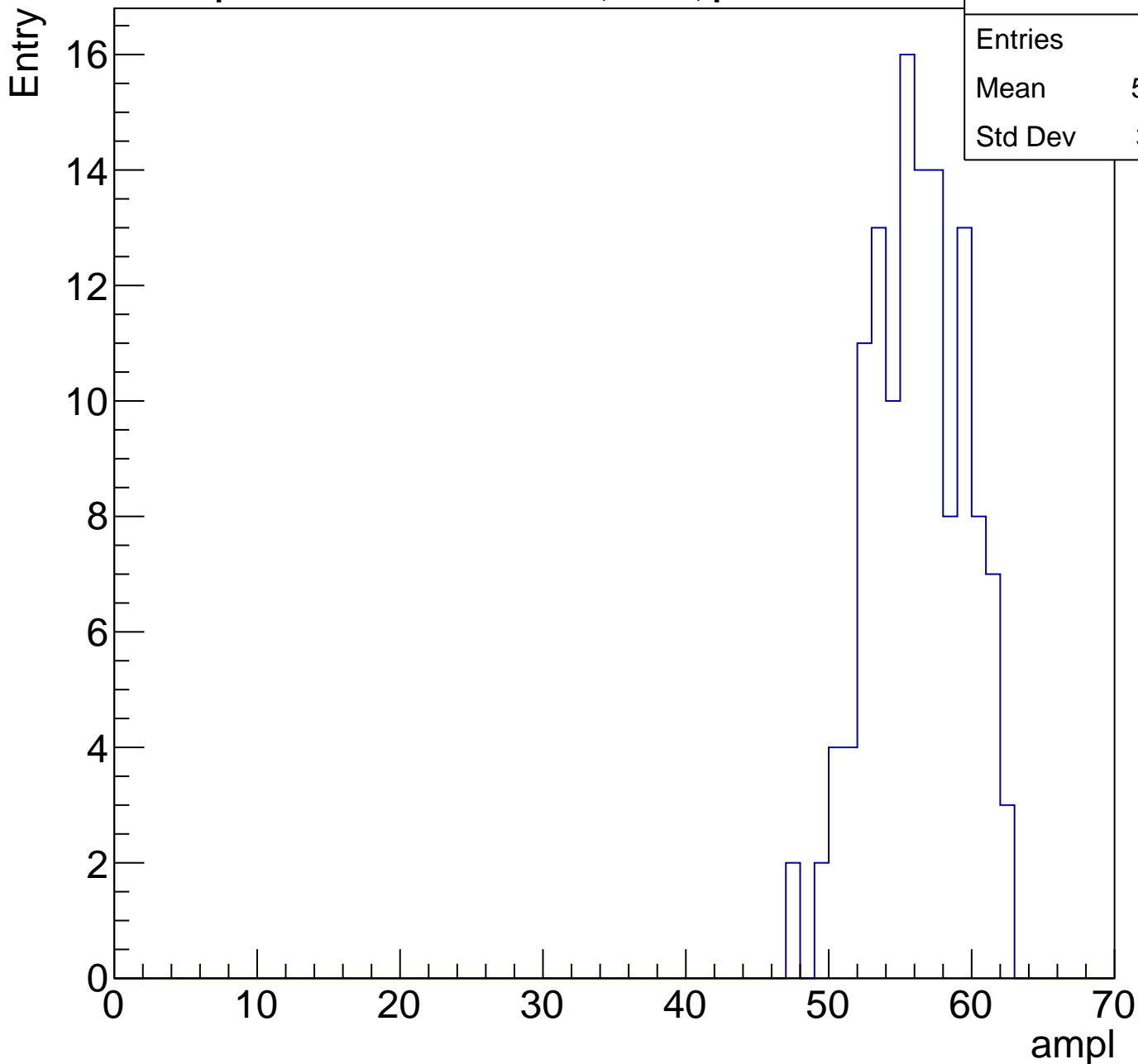
Entries	127
Mean	49.5
Std Dev	3.604



B0L101S, U19-ch10, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	55.68
Std Dev	3.311



B0L101S, U19-ch10, adc5

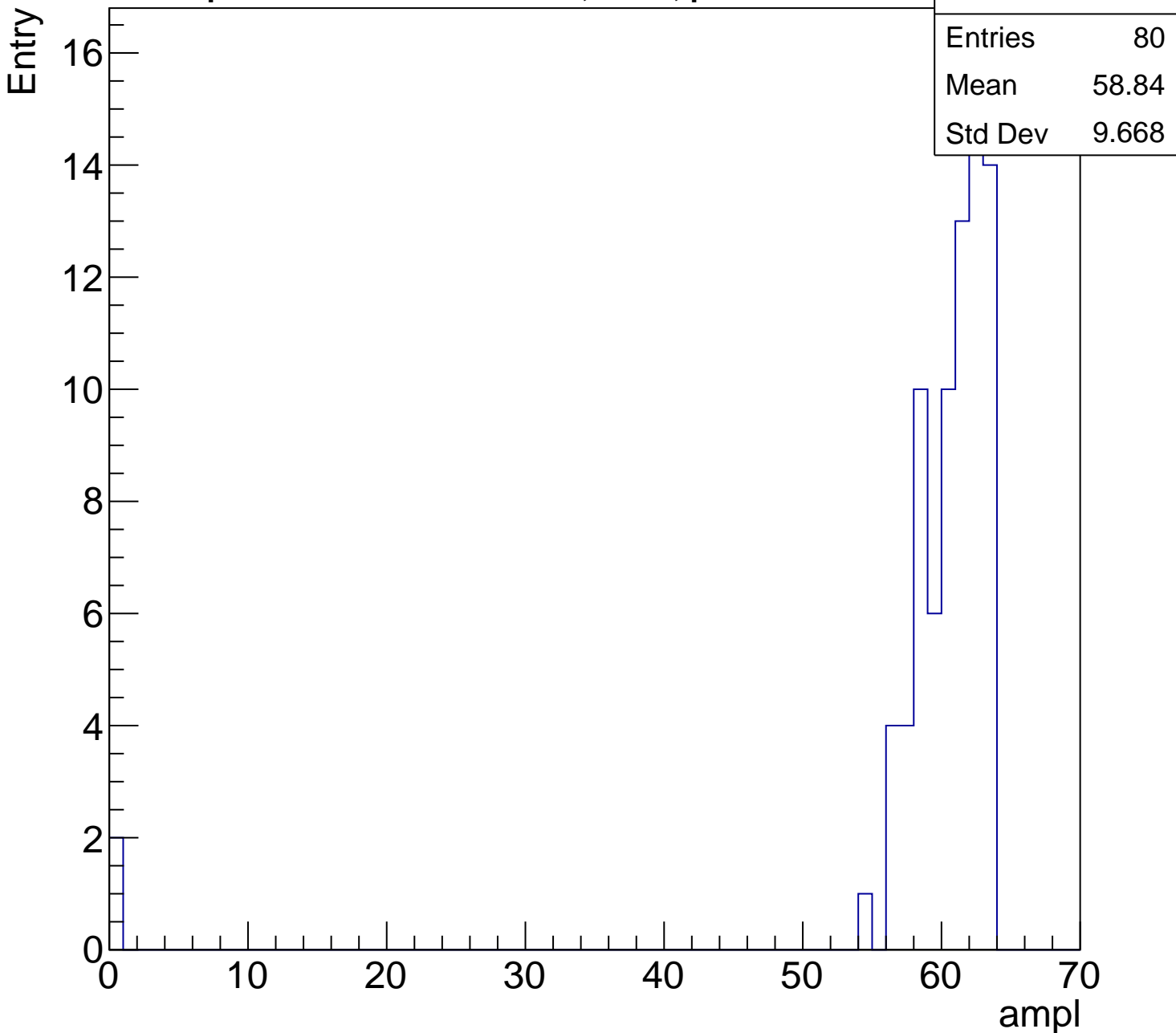
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	58.84
Std Dev	9.668

ampl

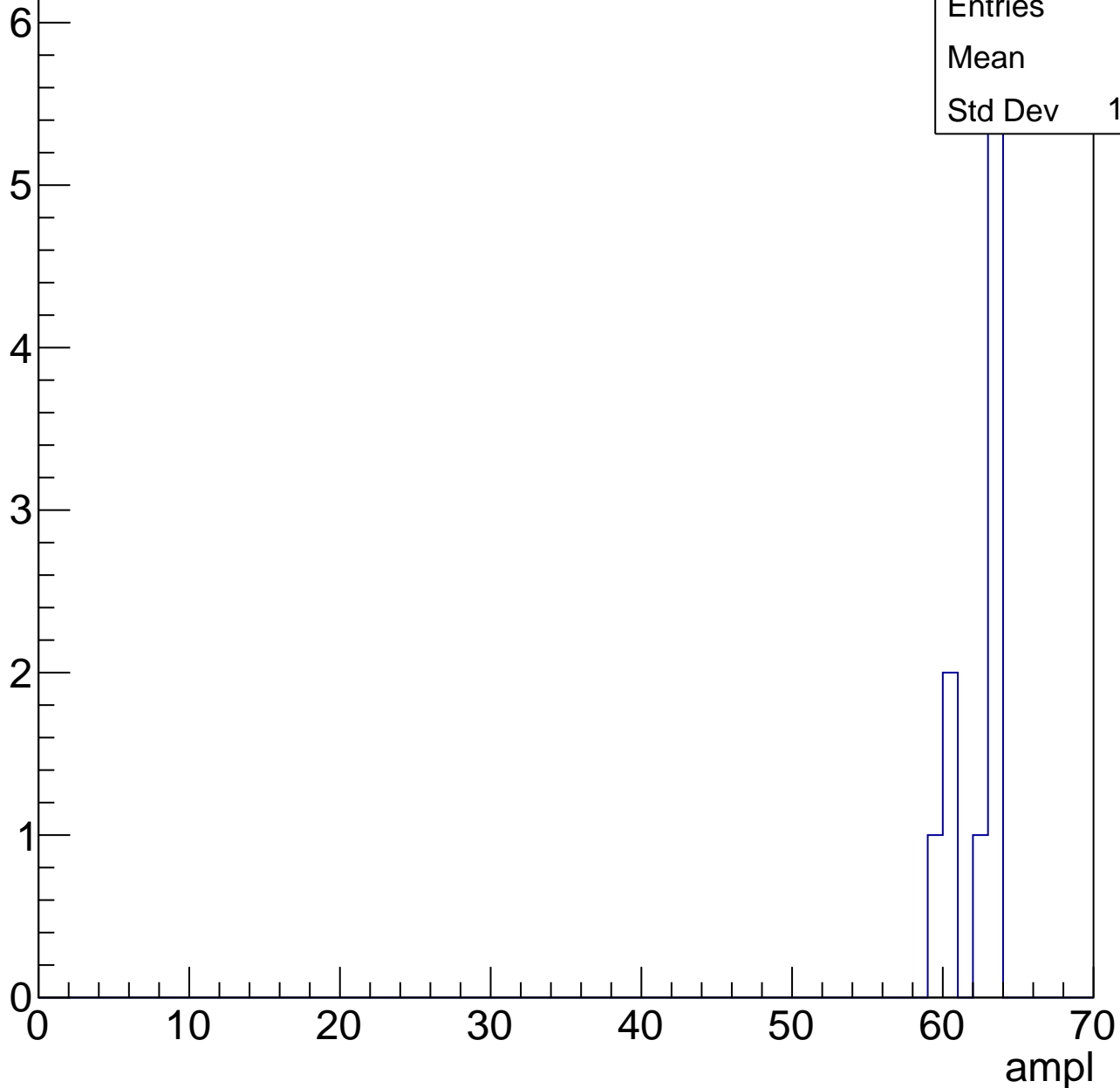


B0L101S, U19-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	61.9
Std Dev	1.513



B0L101S, U19-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L101S, U19-ch11, adc0

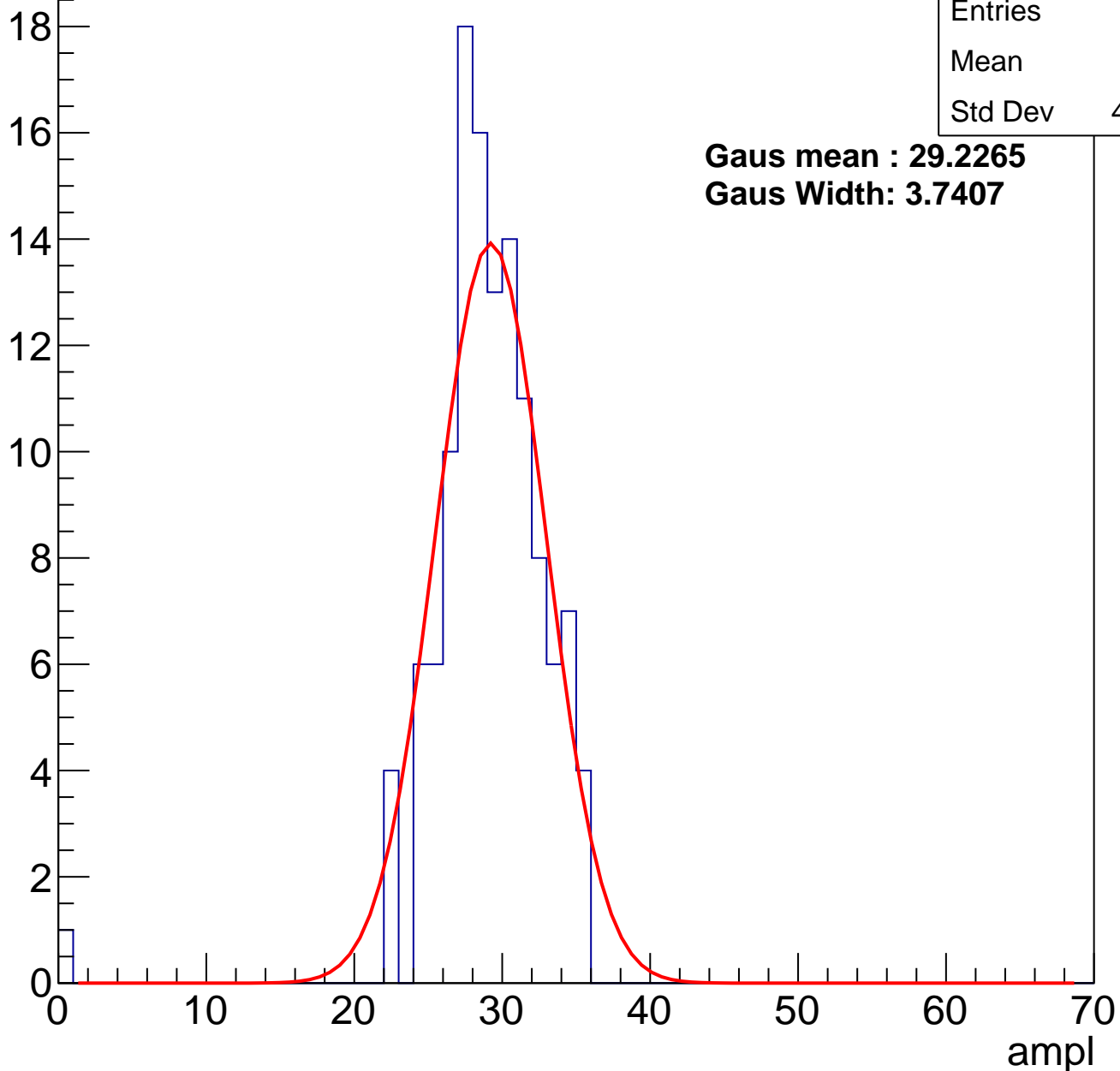
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	28.6
Std Dev	4.002

Gaus mean : 29.2265

Gaus Width: 3.7407

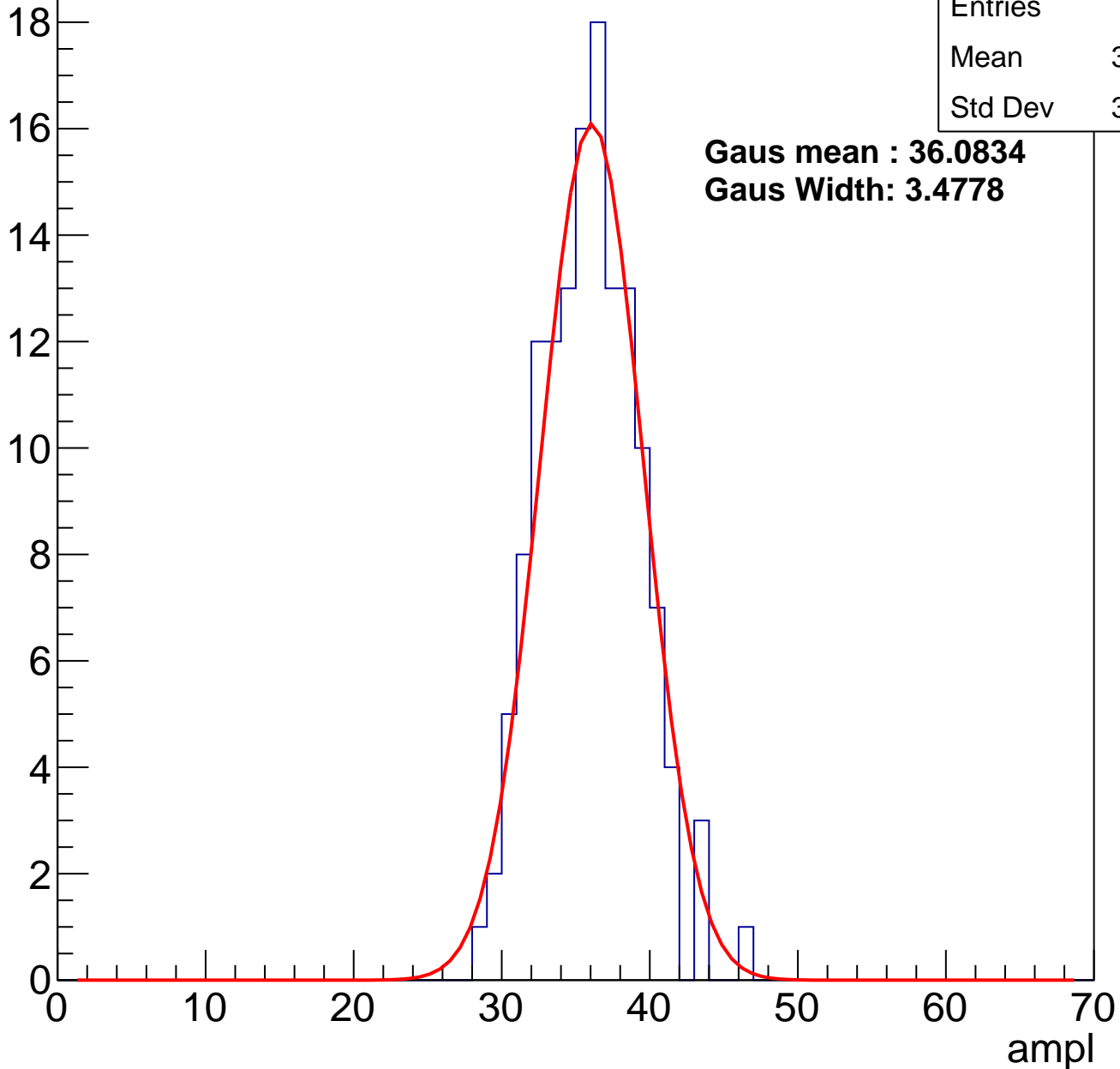
Entry



B0L101S, U19-ch11, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch11, adc2

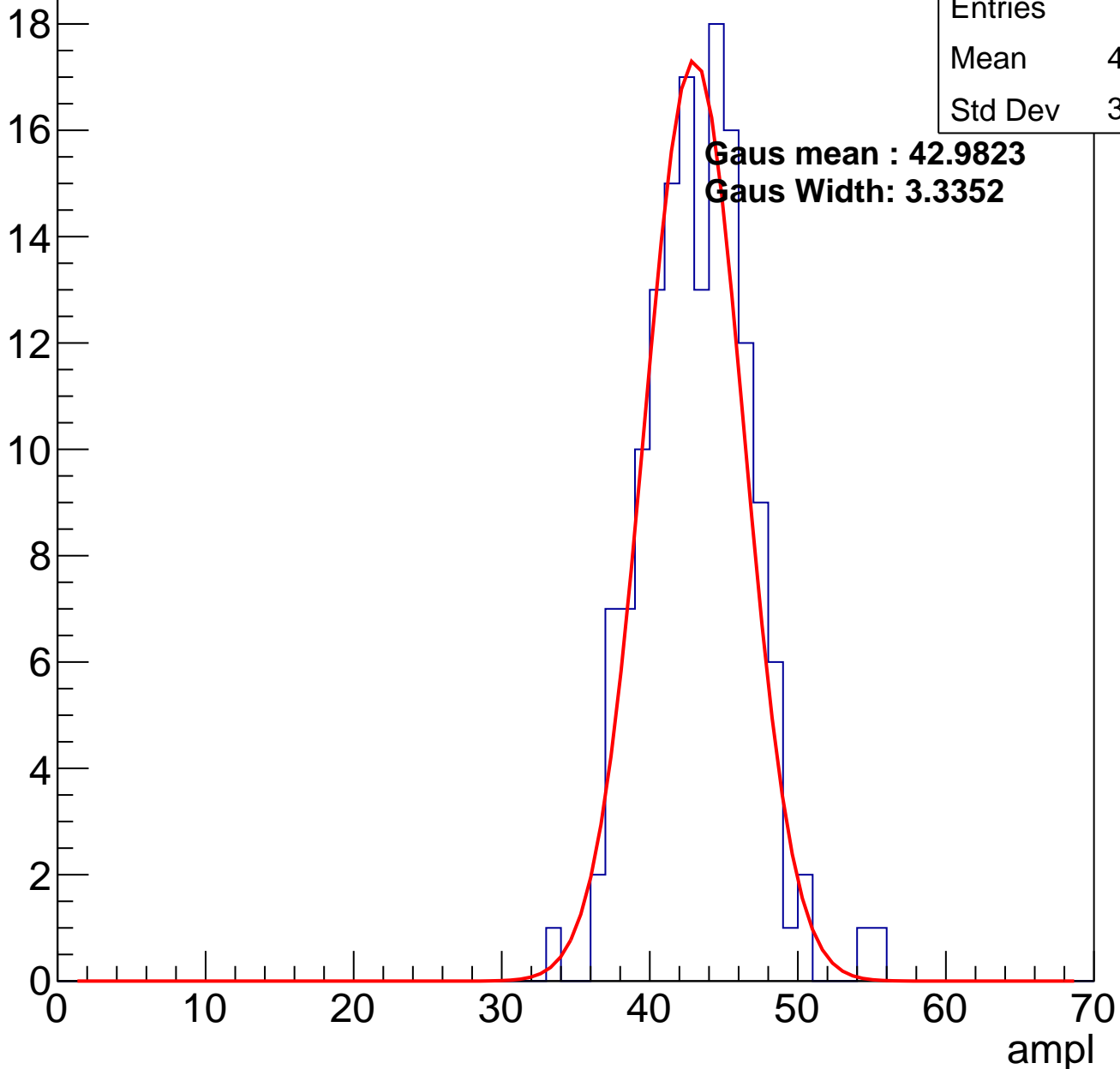
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	42.79
Std Dev	3.505

Gaus mean : 42.9823

Gaus Width: 3.3352

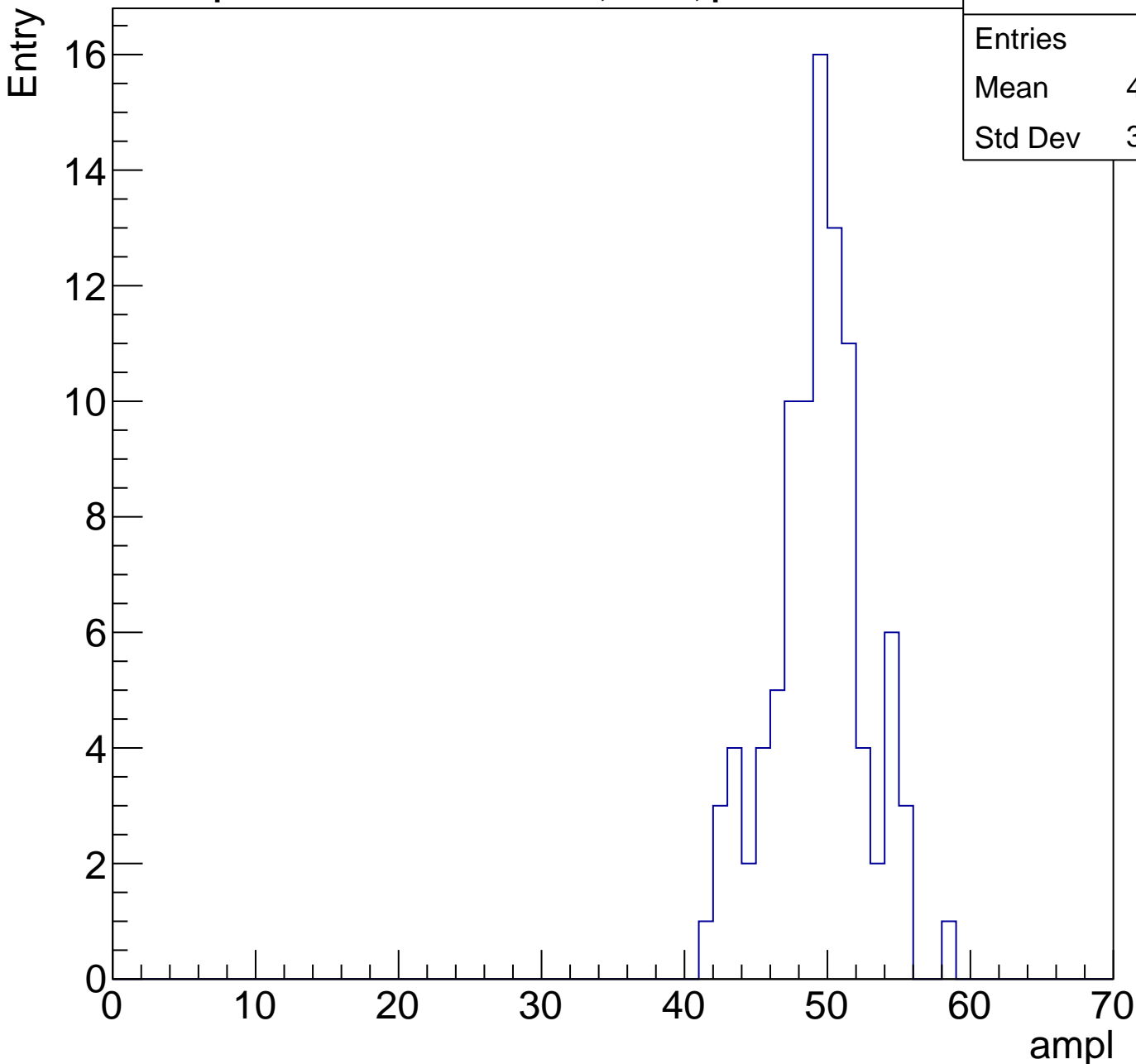
Entry



B0L101S, U19-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

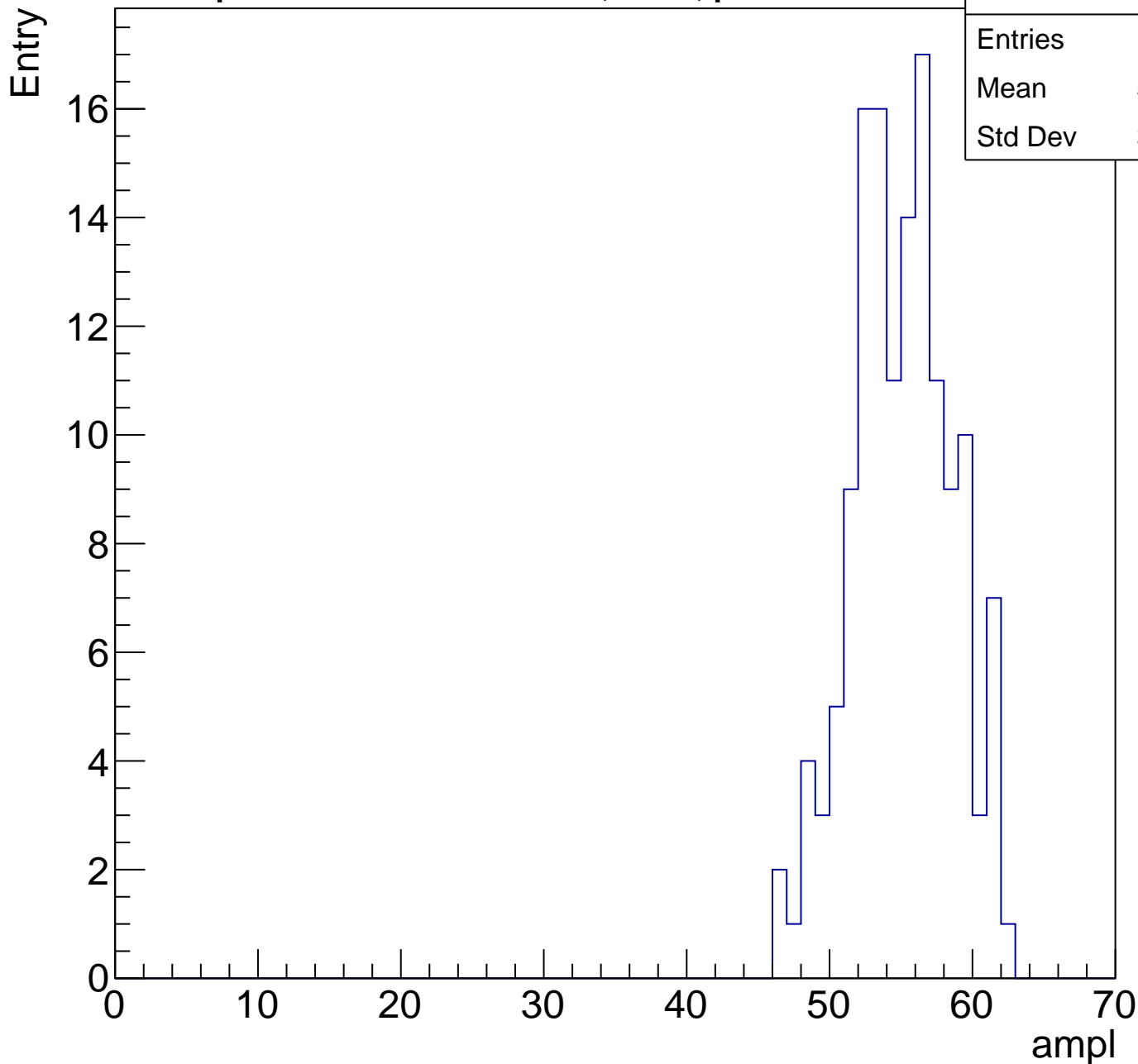
Entries	95
Mean	48.87
Std Dev	3.287



B0L101S, U19-ch11, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	54.61
Std Dev	3.471



B0L101S, U19-ch11, adc5

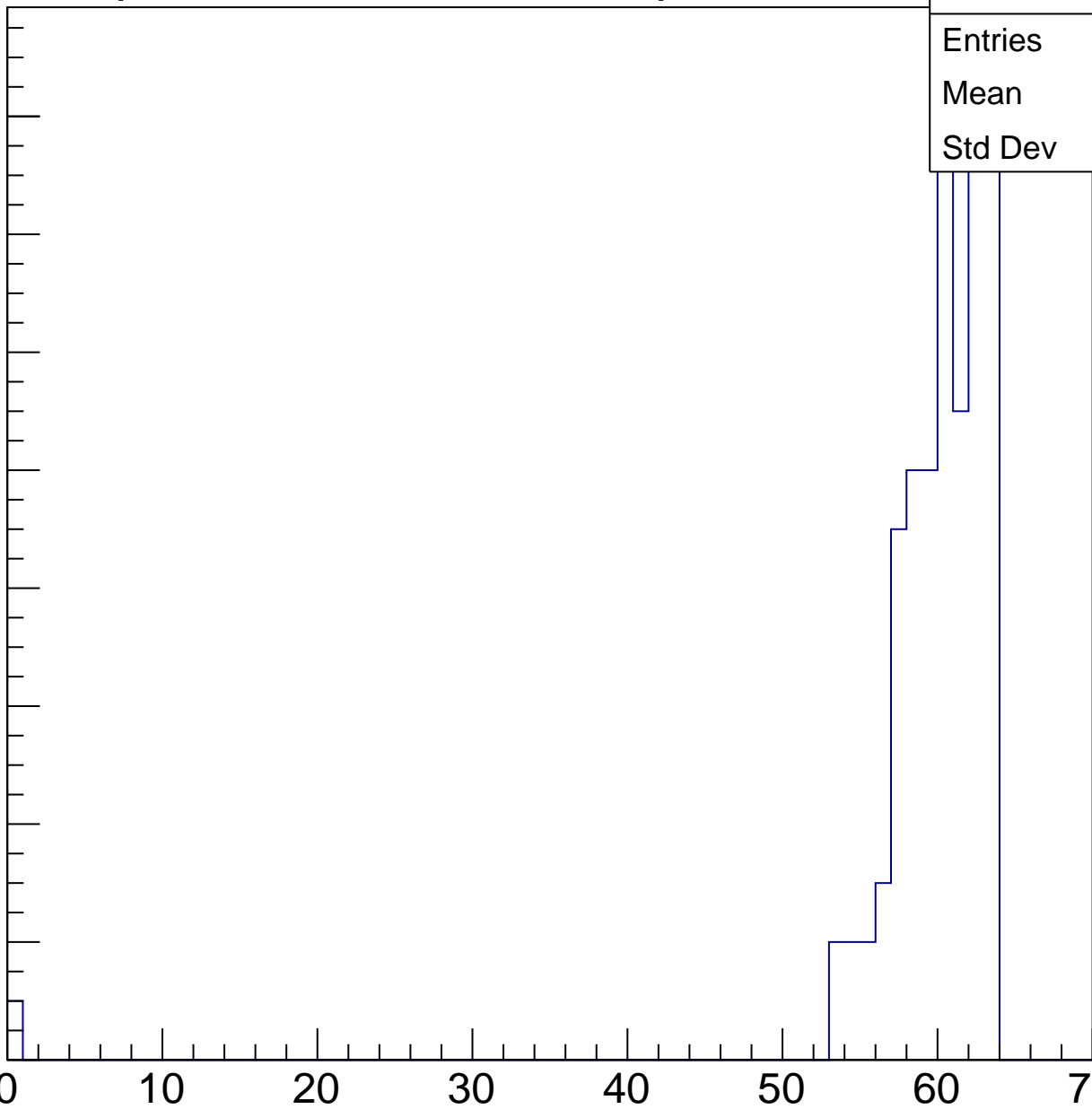
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.27
Std Dev	6.49

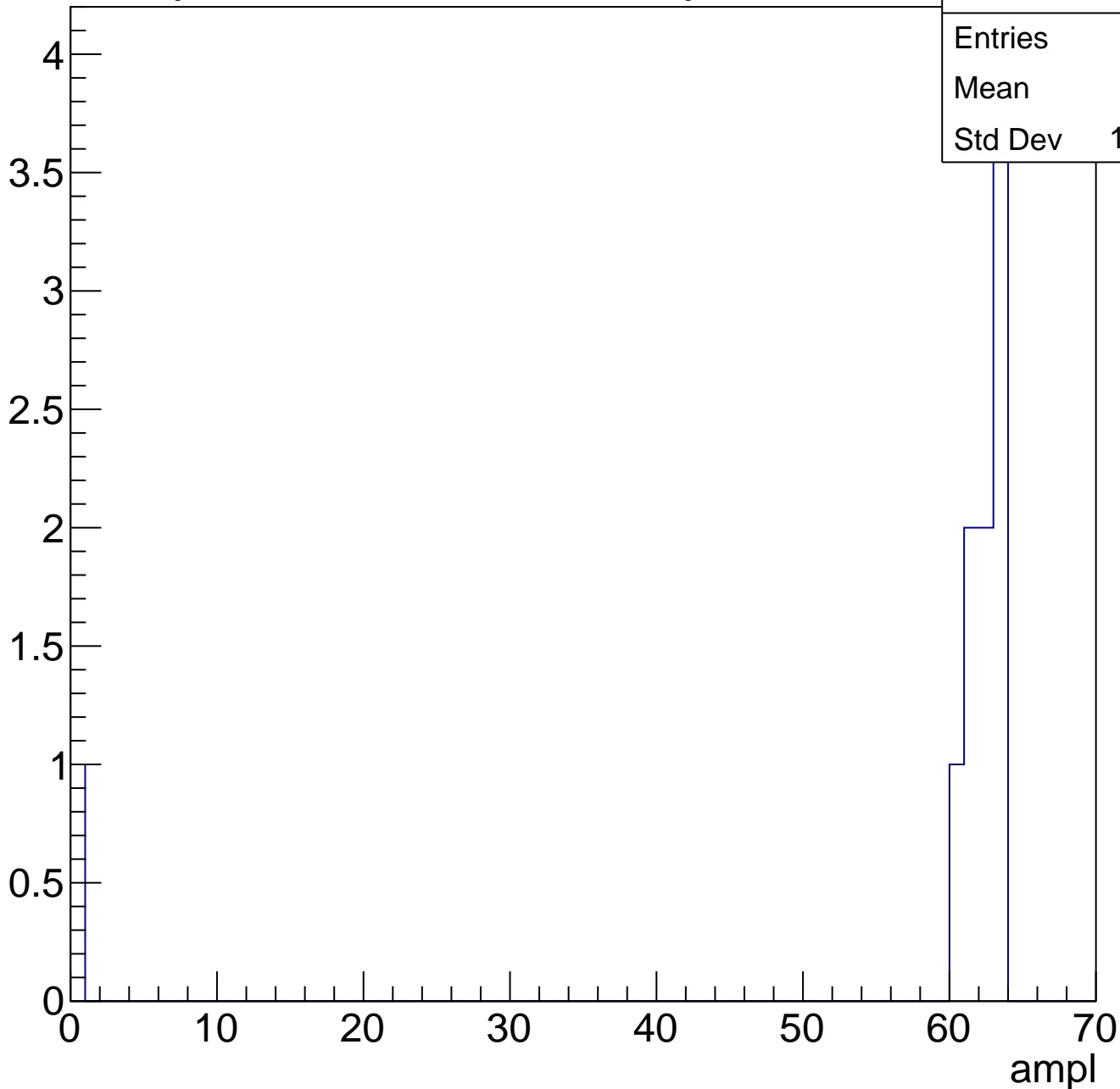
ampl



B0L101S, U19-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch12, adc0

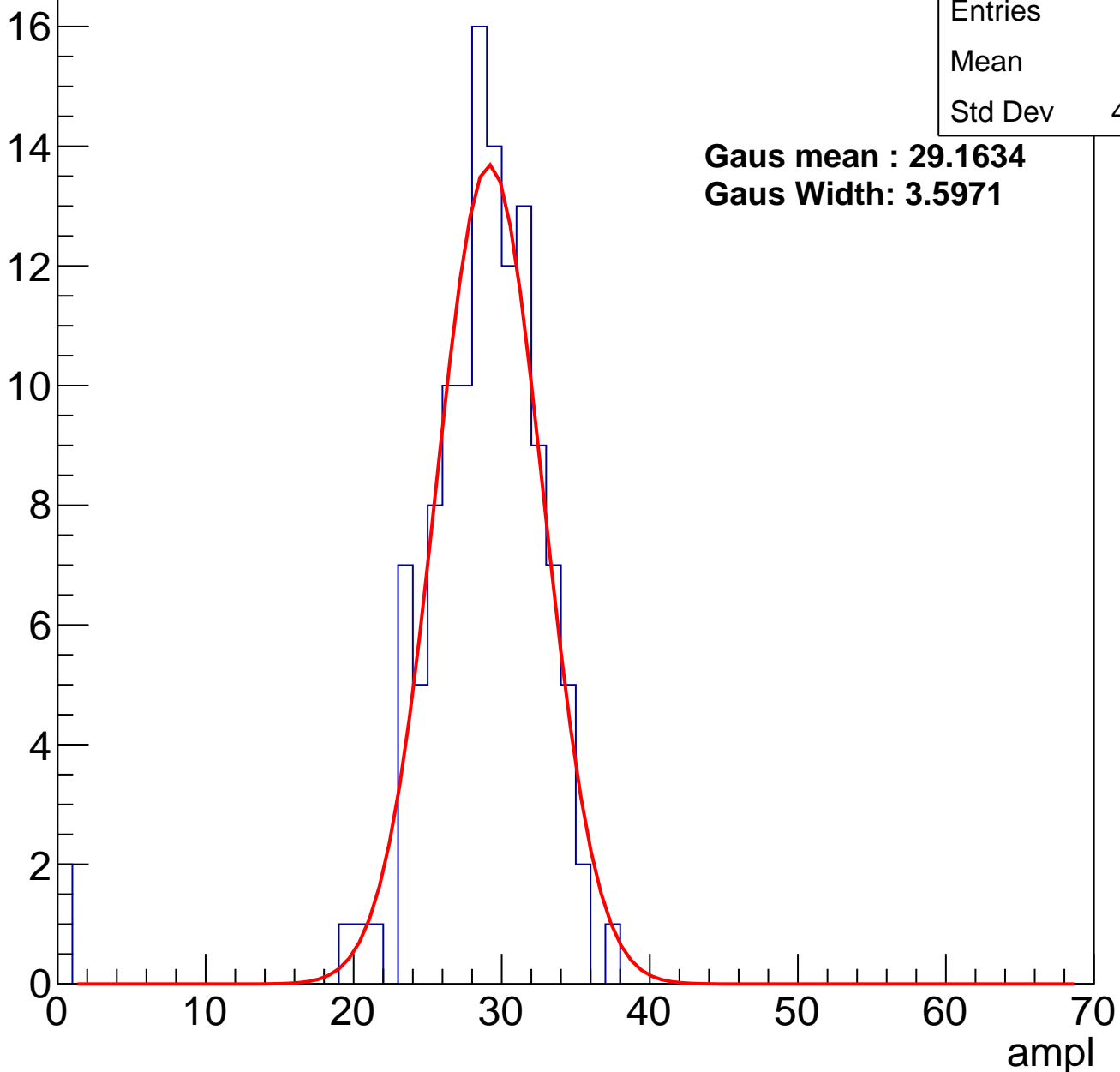
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	28.1
Std Dev	4.914

Gaus mean : 29.1634

Gaus Width: 3.5971

Entry



B0L101S, U19-ch12, adc1

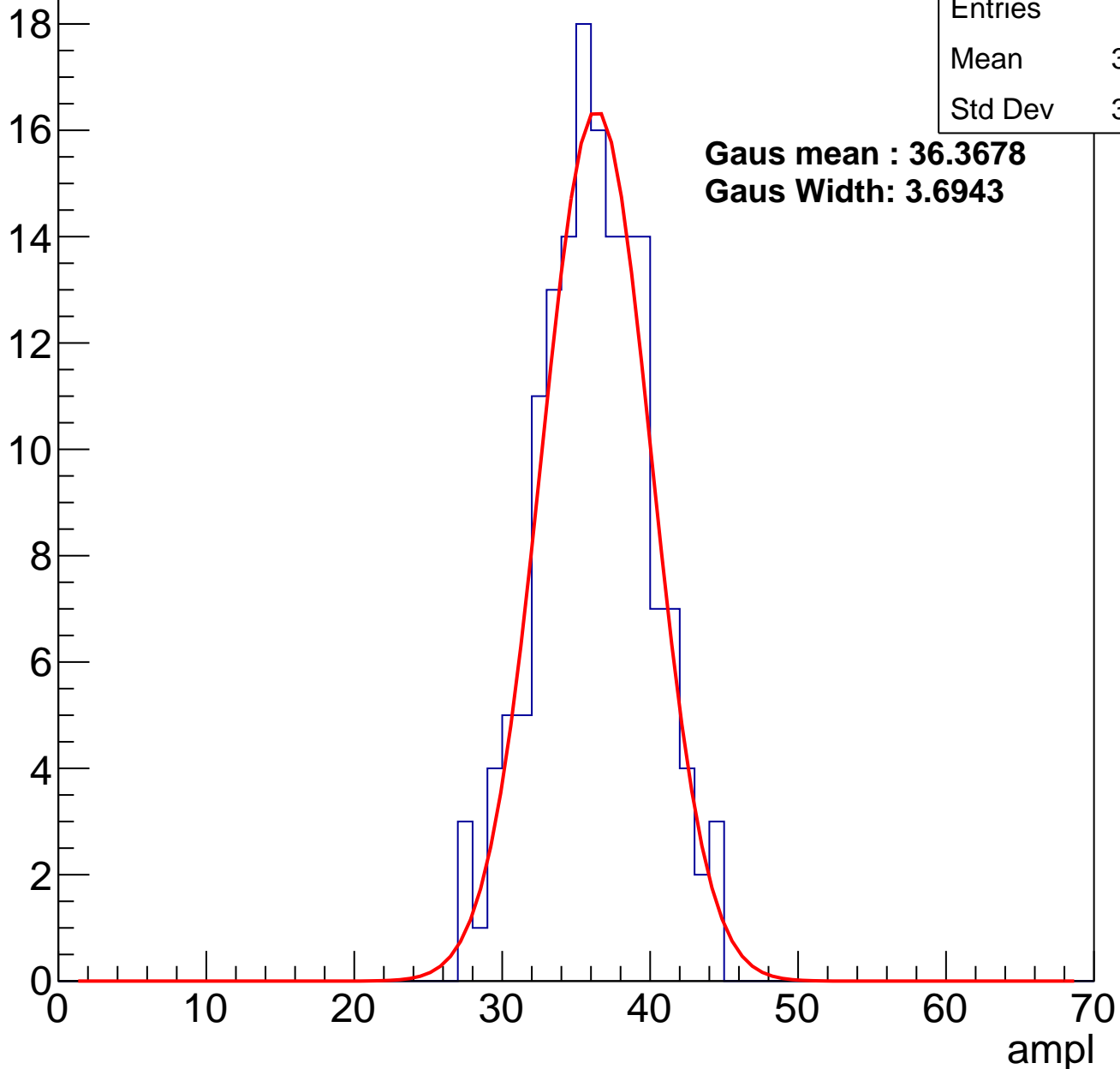
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	35.75
Std Dev	3.666

Gaus mean : 36.3678

Gaus Width: 3.6943

Entry



B0L101S, U19-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

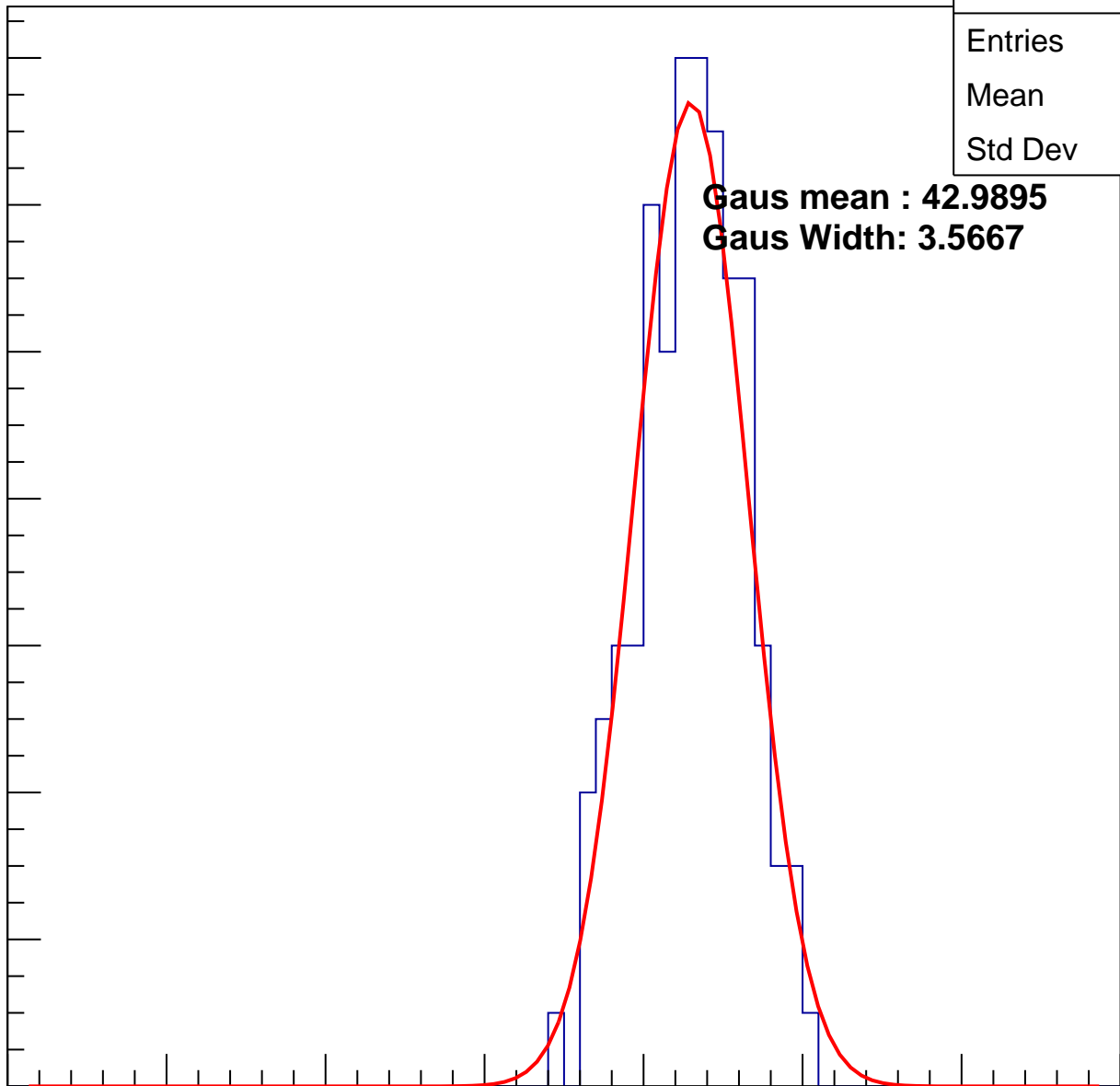
14
12
10
8
6
4
2
0

Entries	120
Mean	42.51
Std Dev	3.317

Gaus mean : 42.9895
Gaus Width: 3.5667

ampl

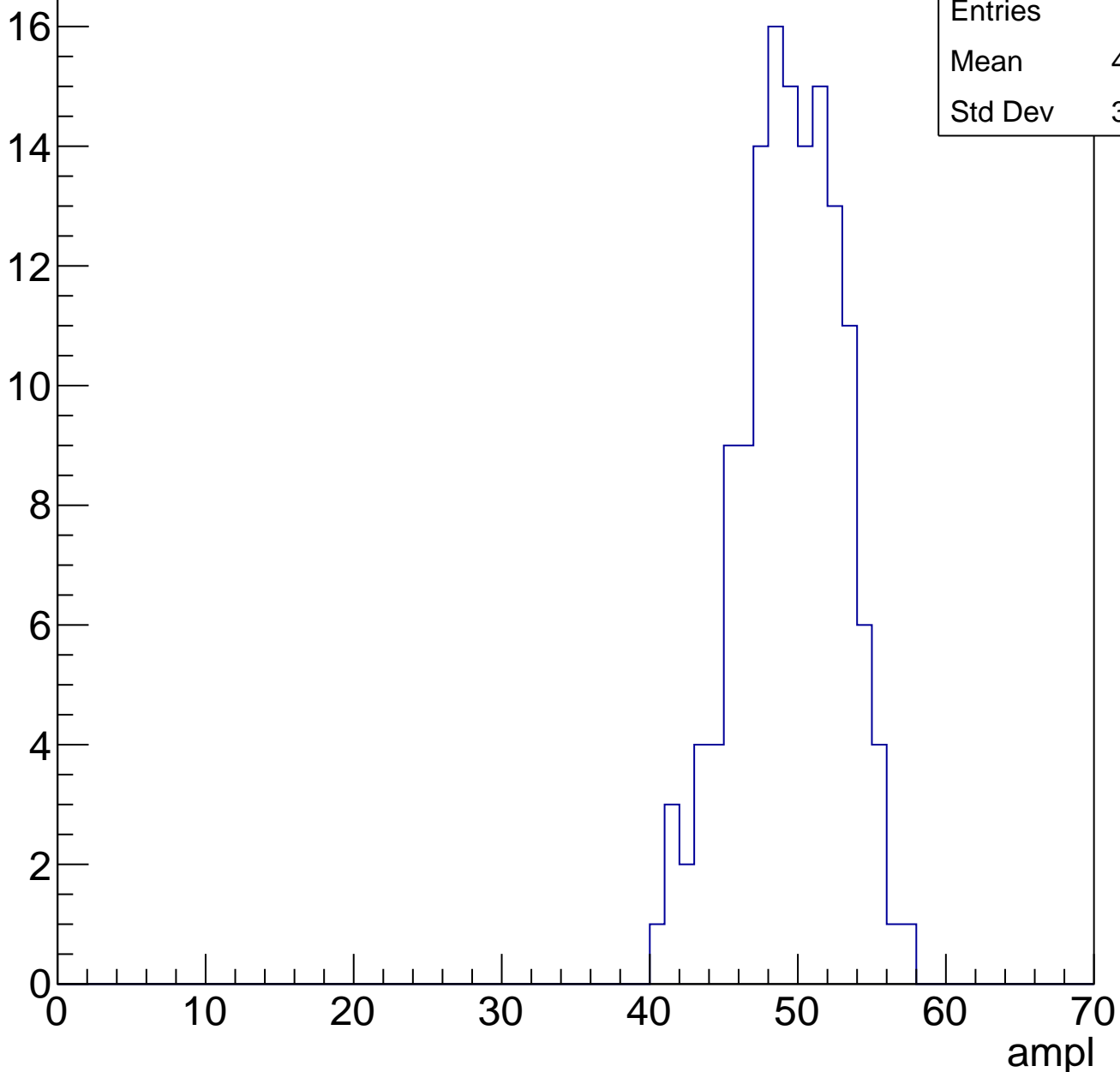
0 10 20 30 40 50 60 70



B0L101S, U19-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch12, adc4

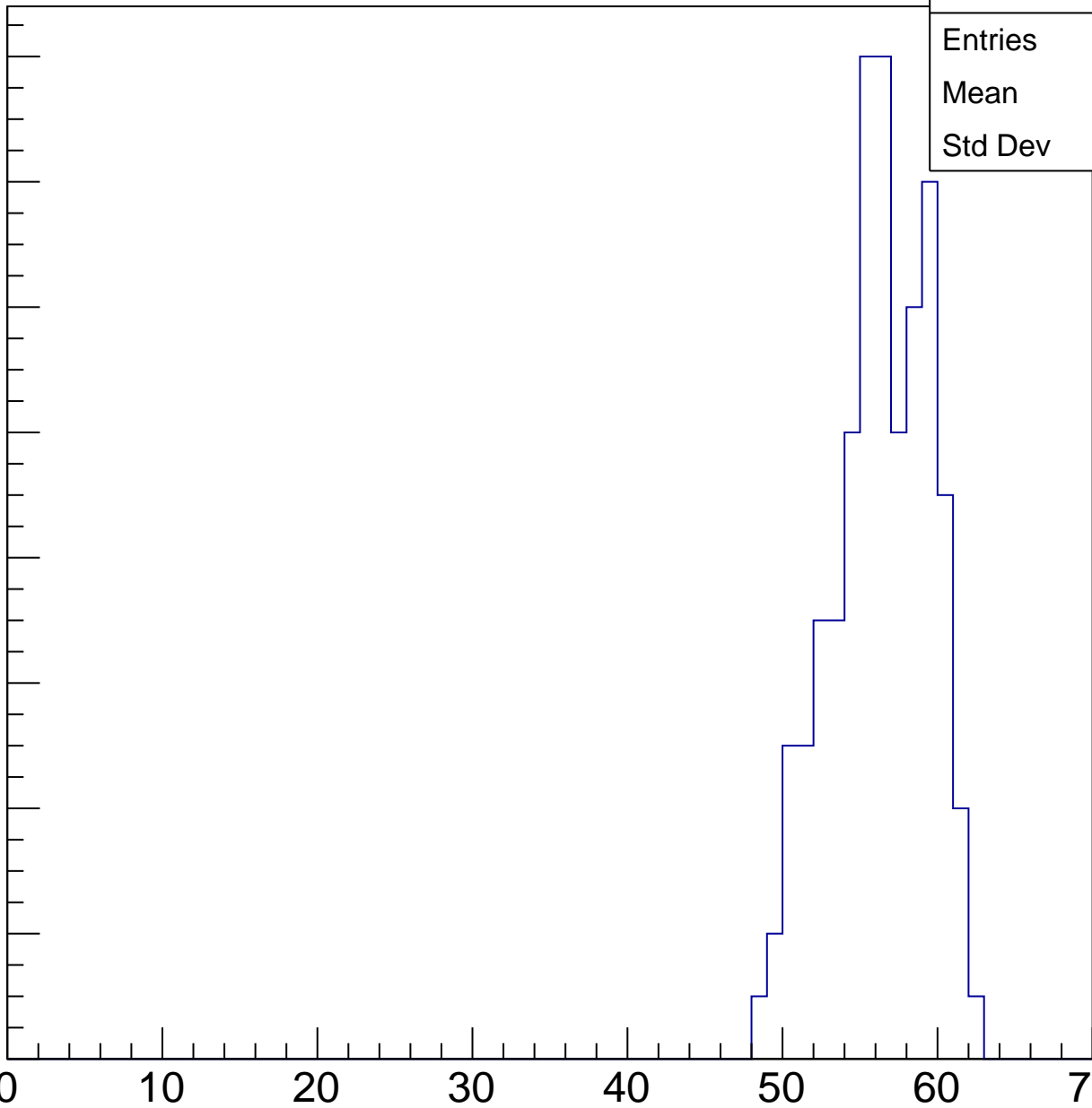
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	55.8
Std Dev	3.118

ampl



B0L101S, U19-ch12, adc5

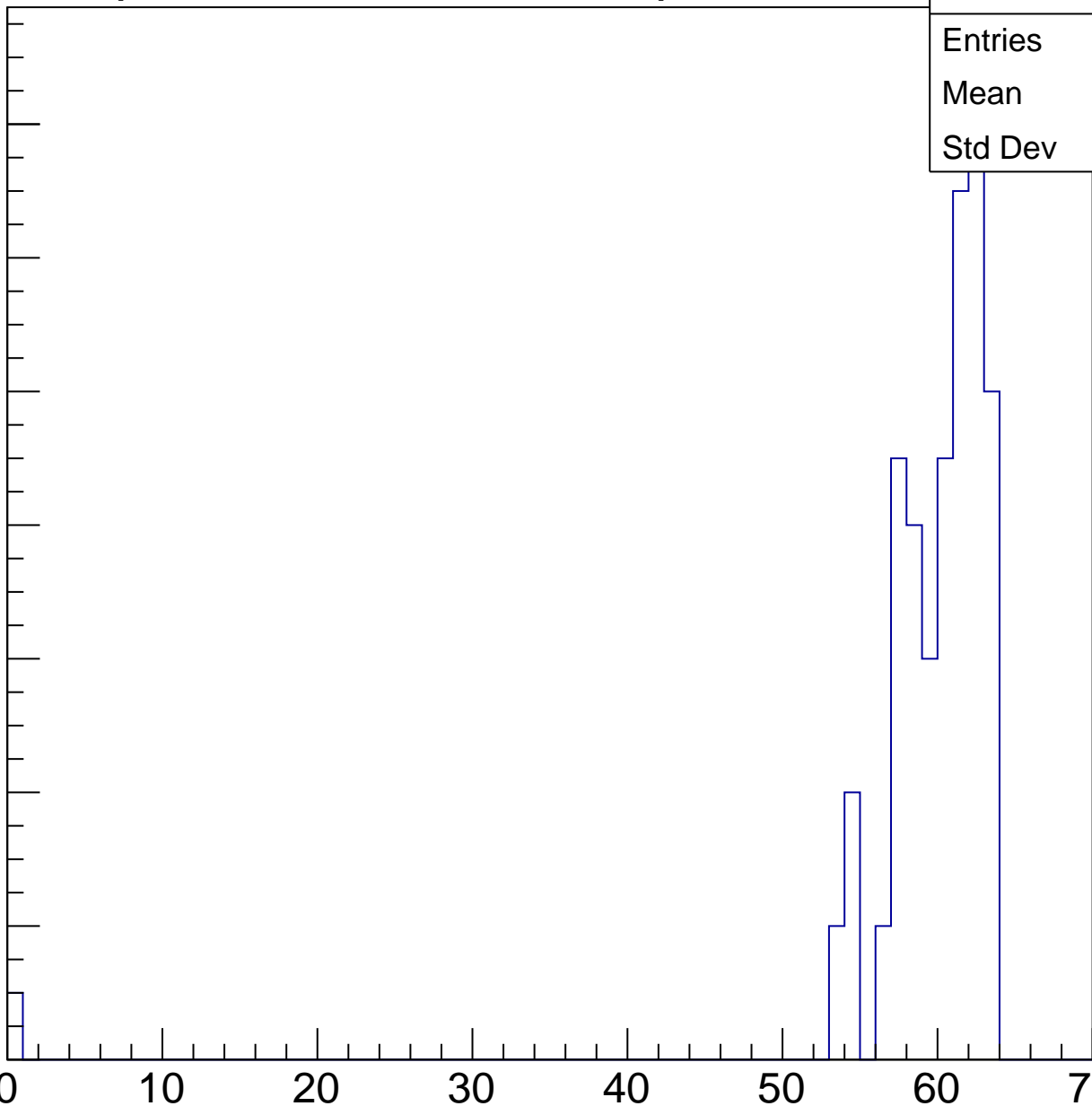
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	58.96
Std Dev	7.178

ampl

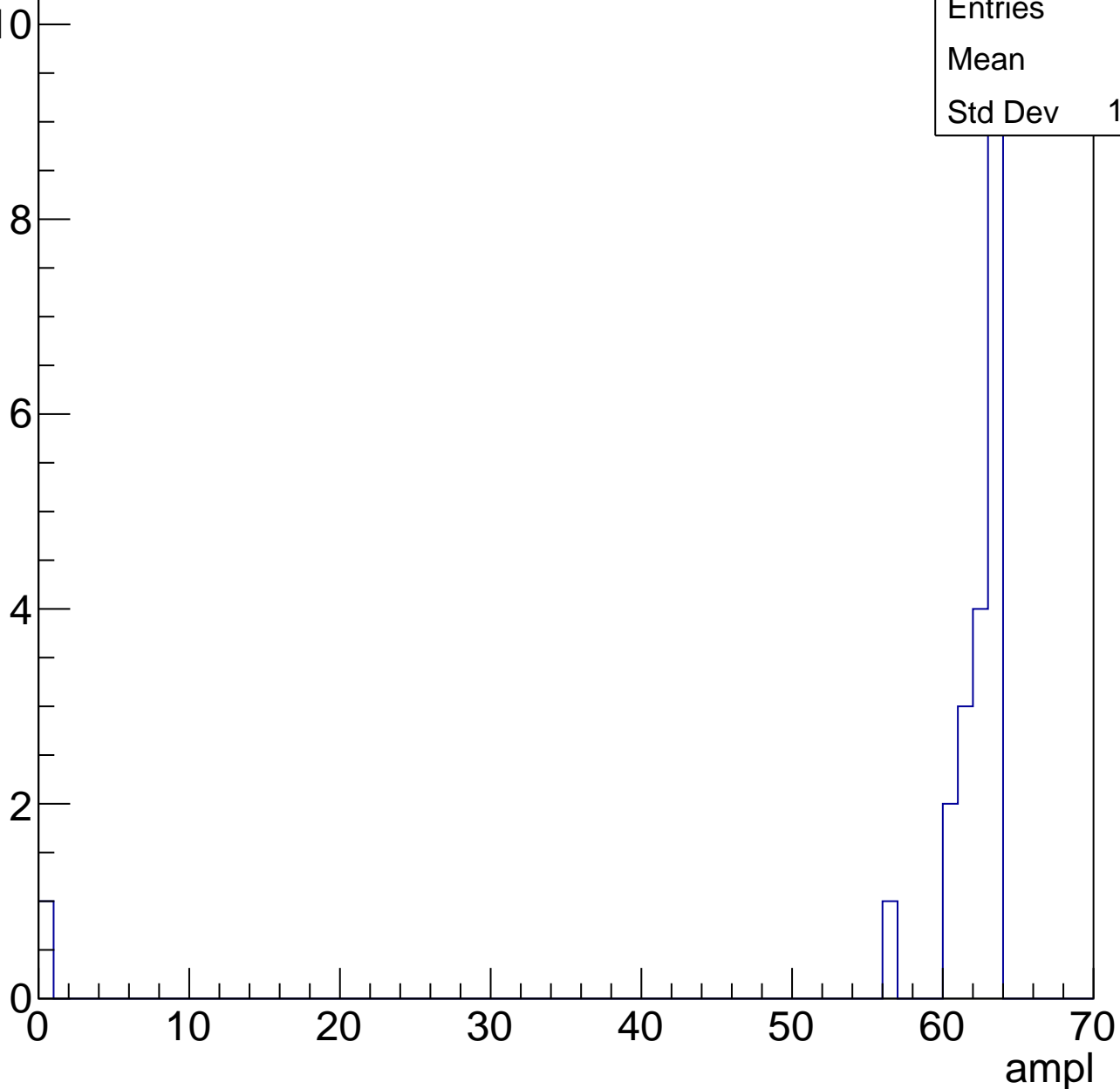


B0L101S, U19-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	58.9
Std Dev	13.27



B0L101S, U19-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B0L101S, U19-ch13, adc0

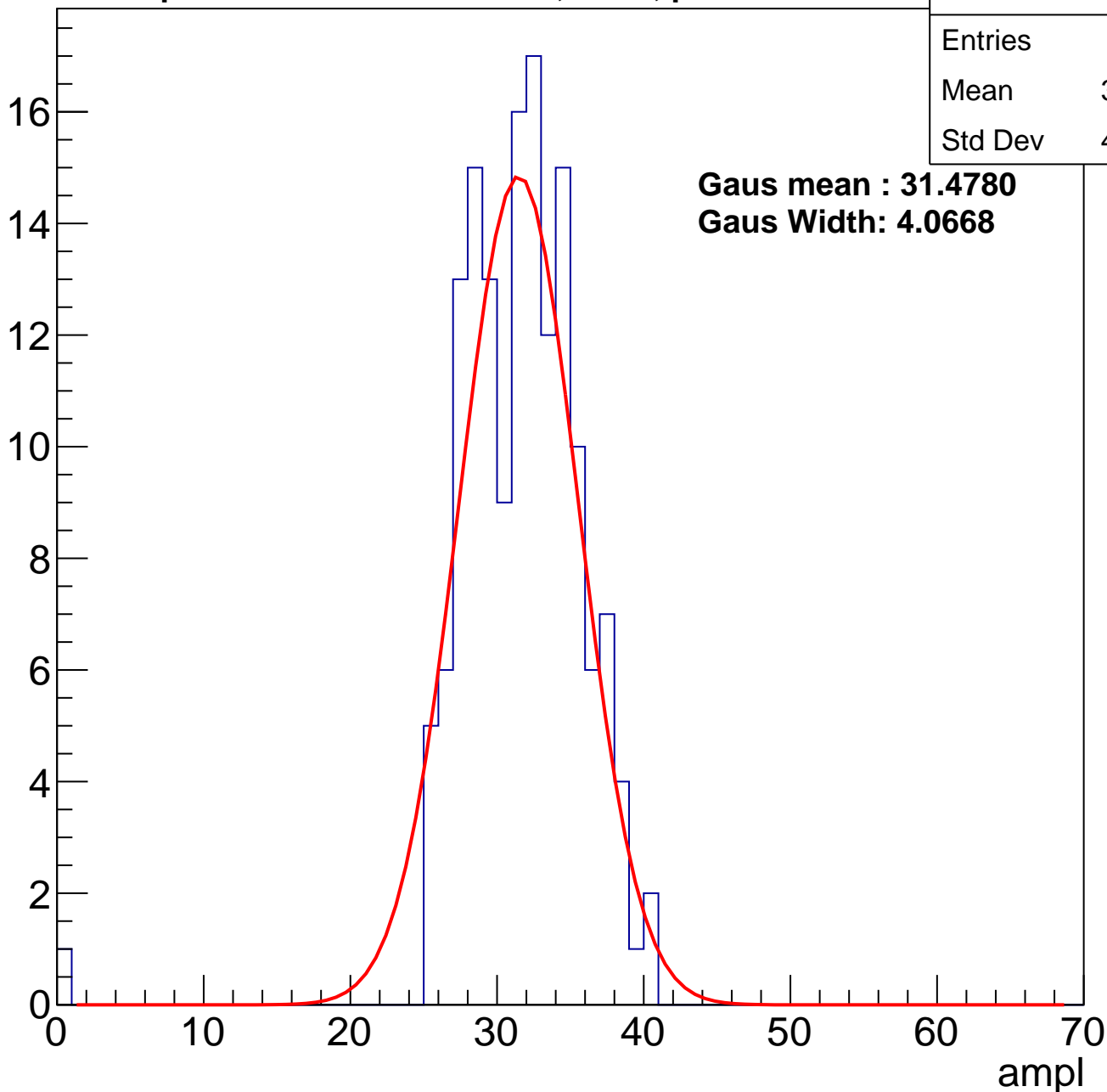
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	31.19
Std Dev	4.348

Gaus mean : 31.4780

Gaus Width: 4.0668

Entry



B0L101S, U19-ch13, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	38.4
Std Dev	3.525

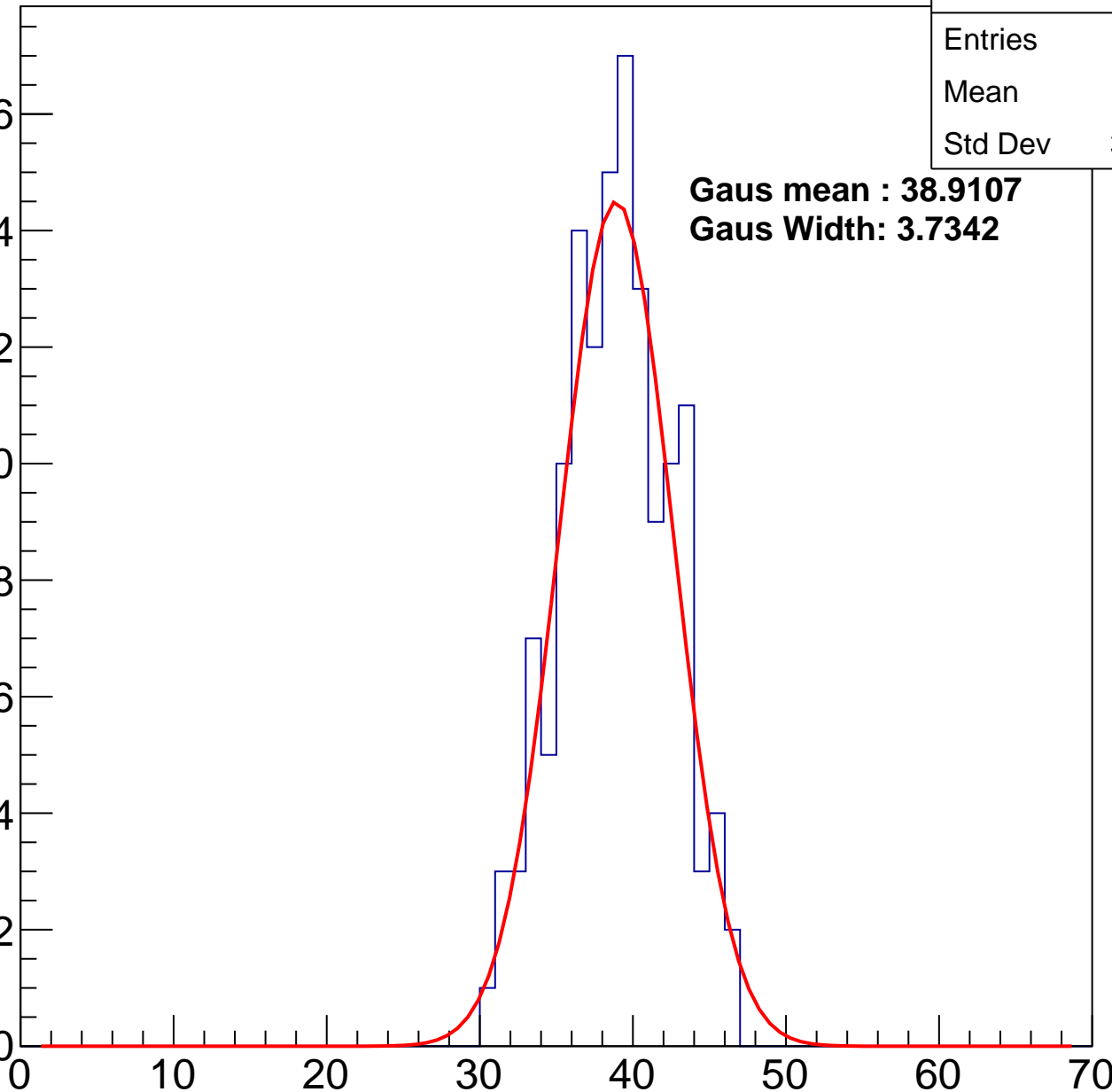
Gaus mean : 38.9107

Gaus Width: 3.7342

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch13, adc2

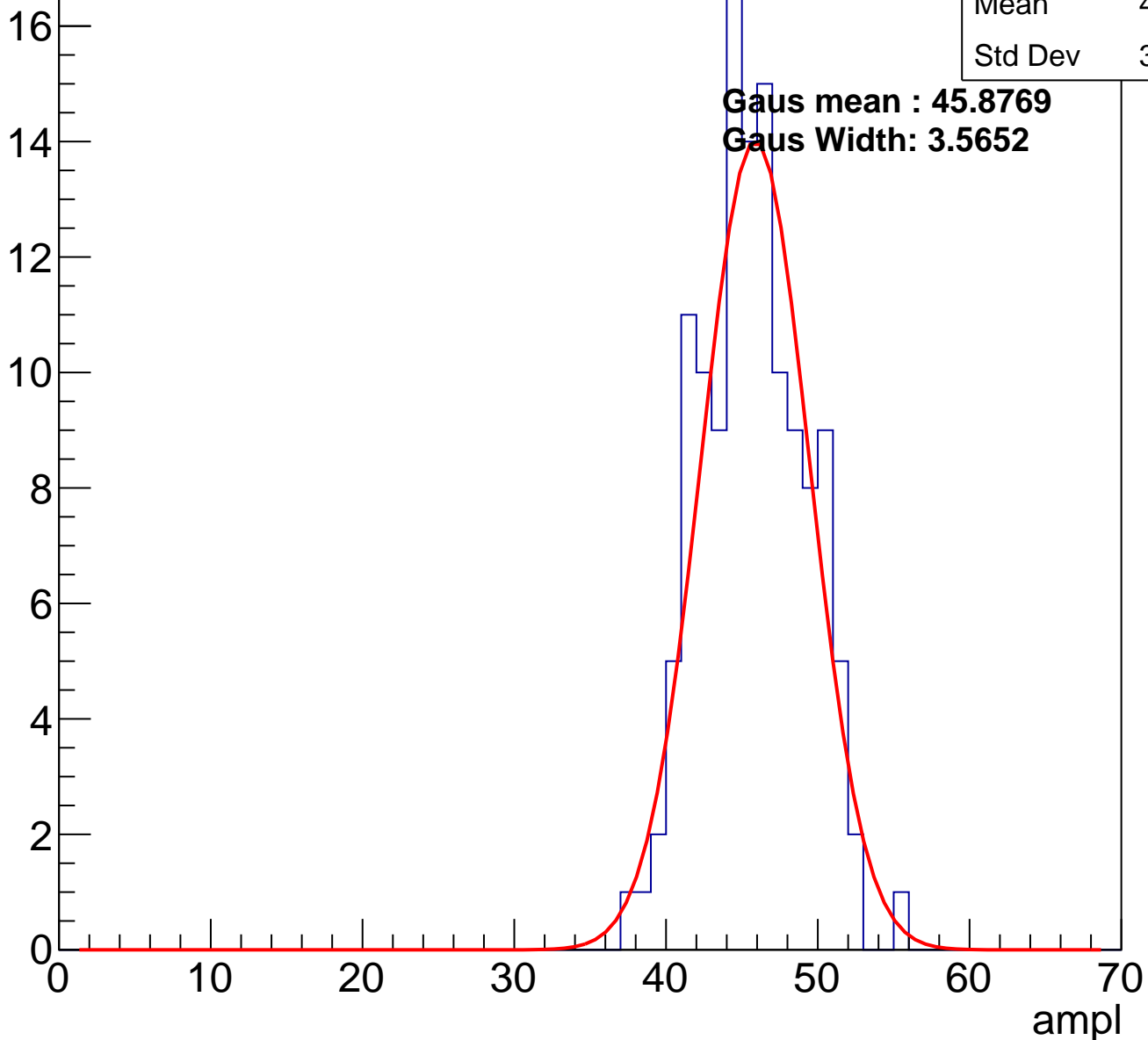
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	129
Mean	45.25
Std Dev	3.396

Gaus mean : 45.8769

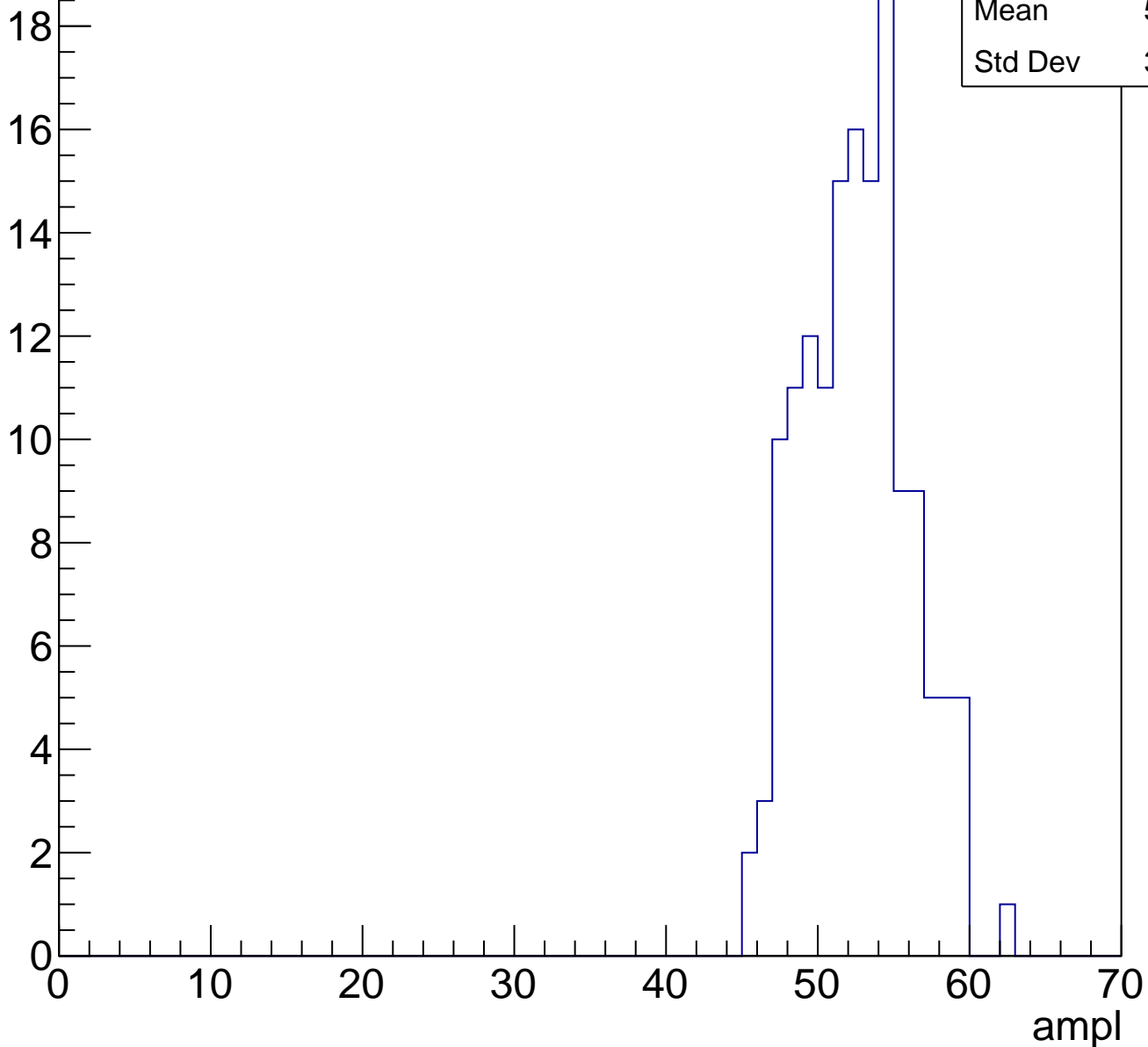
Gaus Width: 3.5652



B0L101S, U19-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch13, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	57.98
Std Dev	6.208

Entry

12

10

8

6

4

2

0

0

10

20

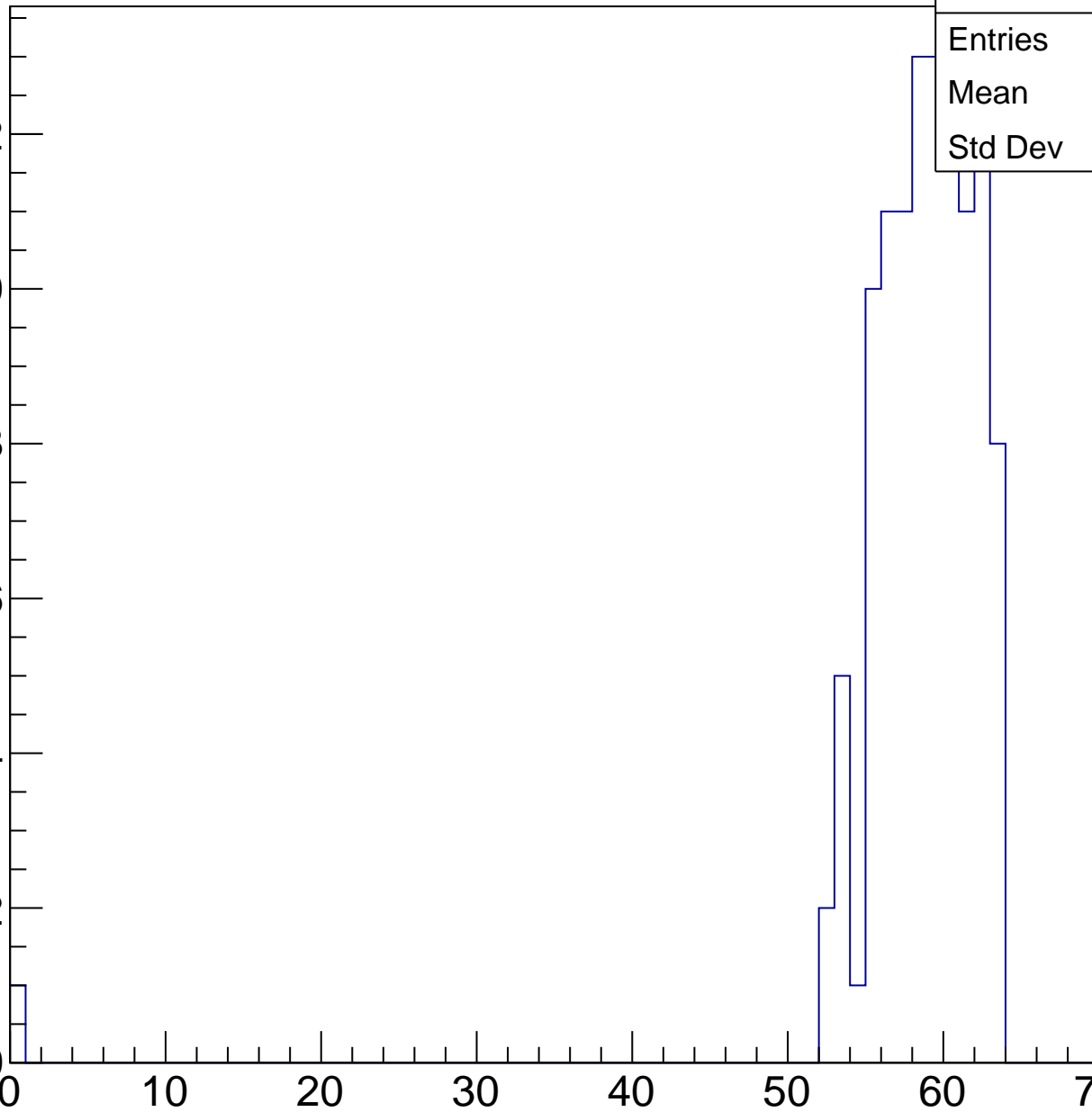
30

40

50

60

ampl



B0L101S, U19-ch13, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

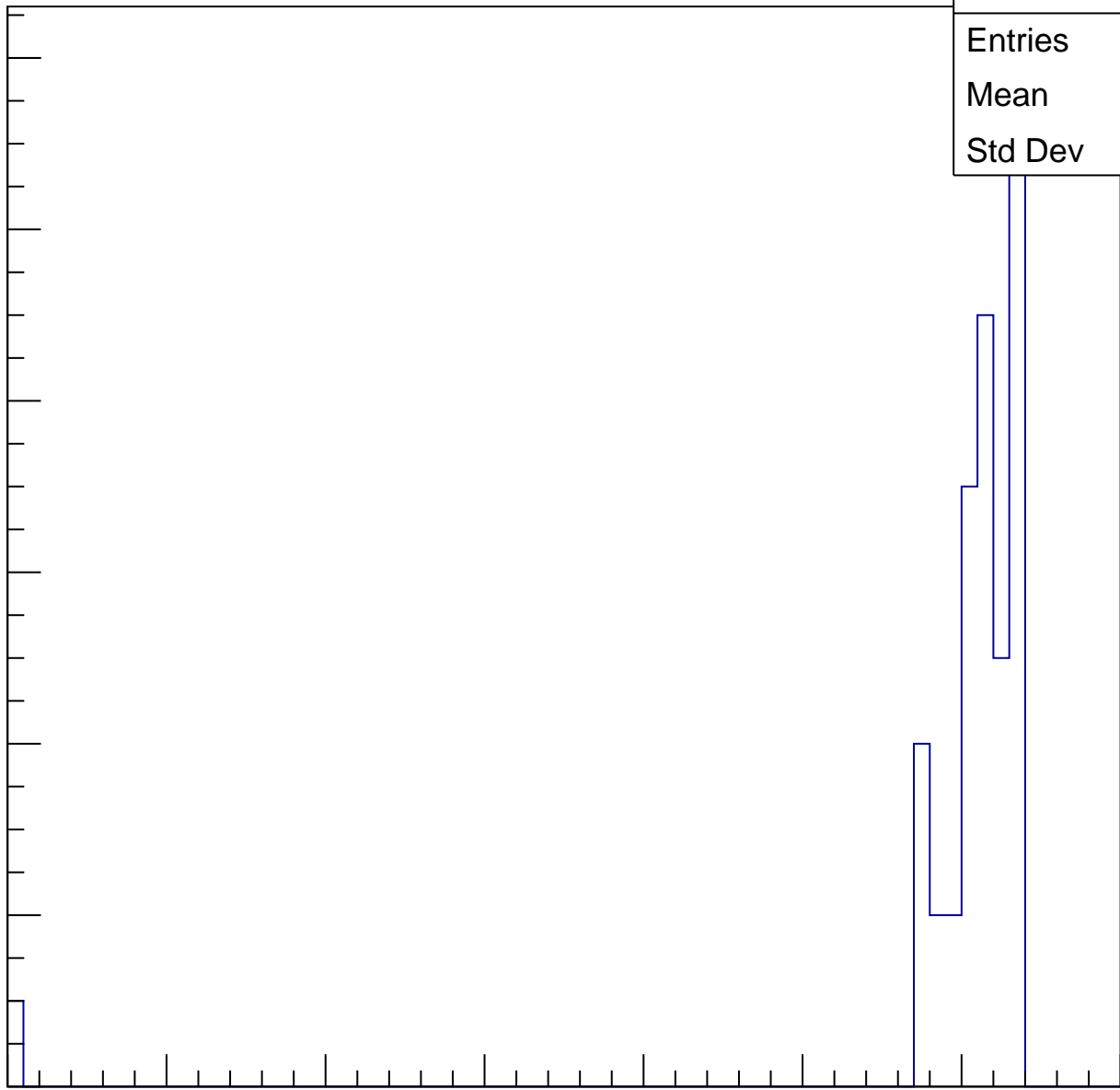
2

0

Entries	42
Mean	59.45
Std Dev	9.475

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

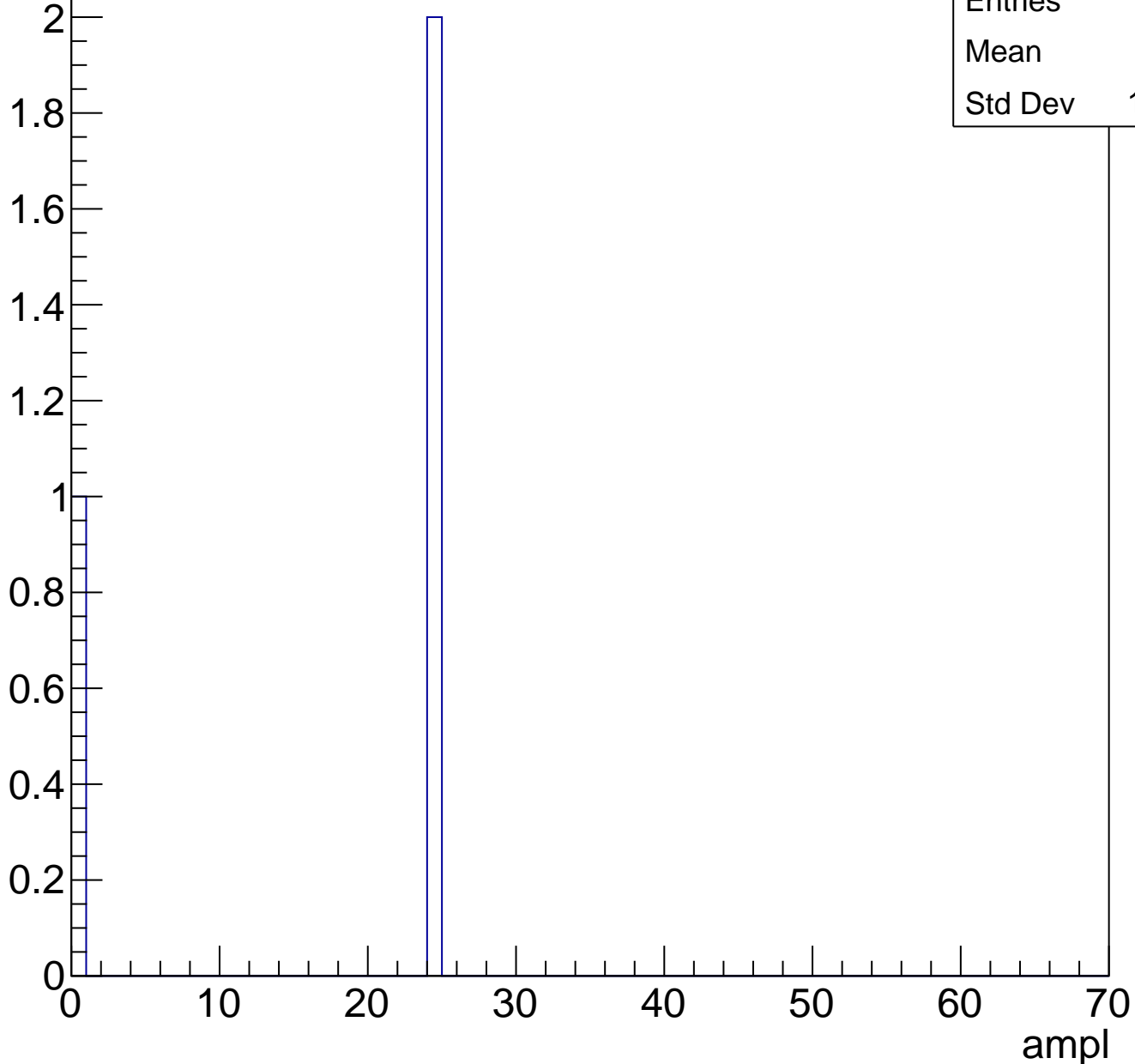


Entries	2
Mean	62
Std Dev	0

B0L101S, U19-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	16
Std Dev	11.31

B0L101S, U19-ch14, adc0

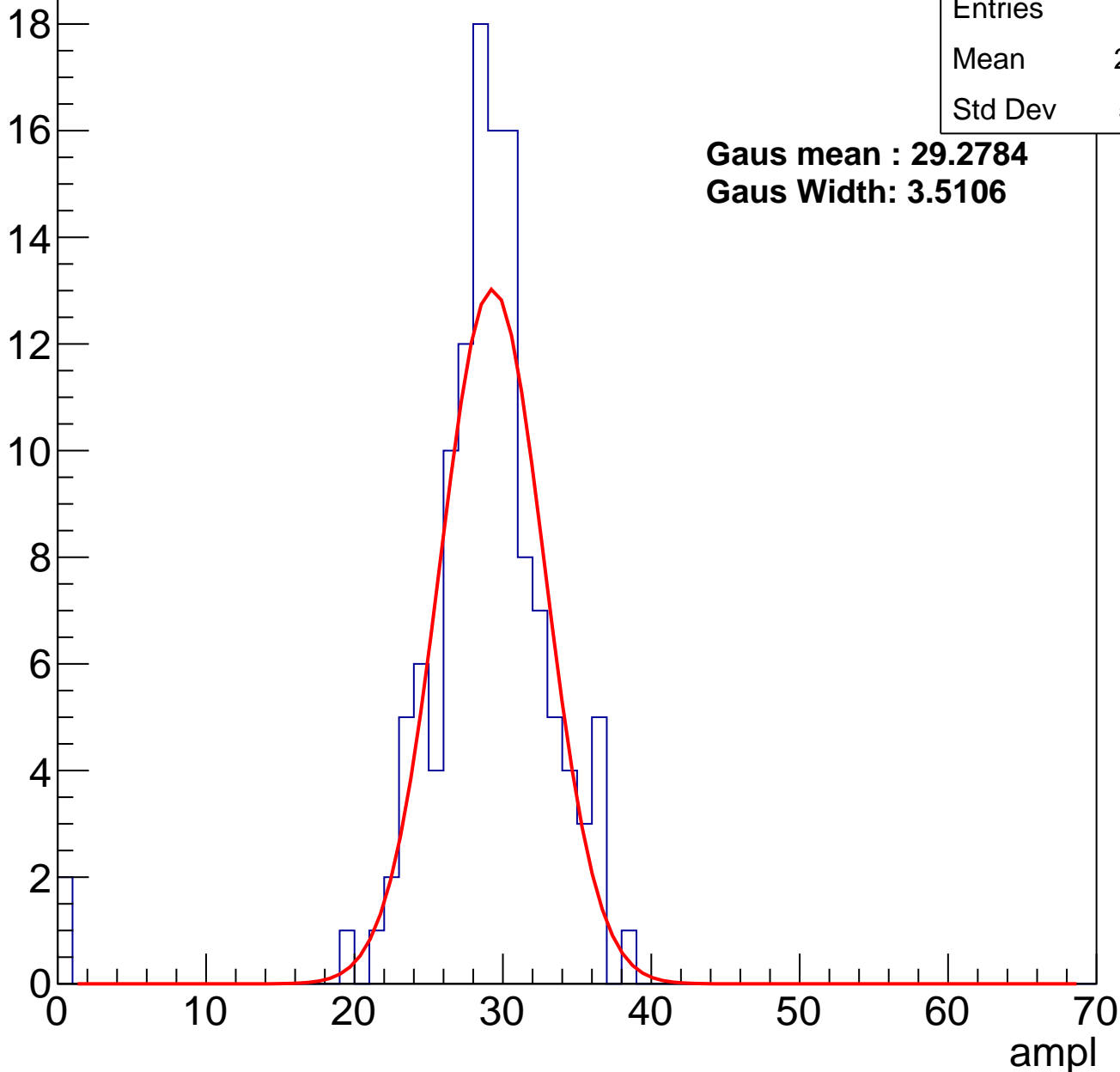
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	28.34
Std Dev	5.011

Gaus mean : 29.2784

Gaus Width: 3.5106

Entry



B0L101S, U19-ch14, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	35.87
Std Dev	3.535

Gaus mean : 36.2160

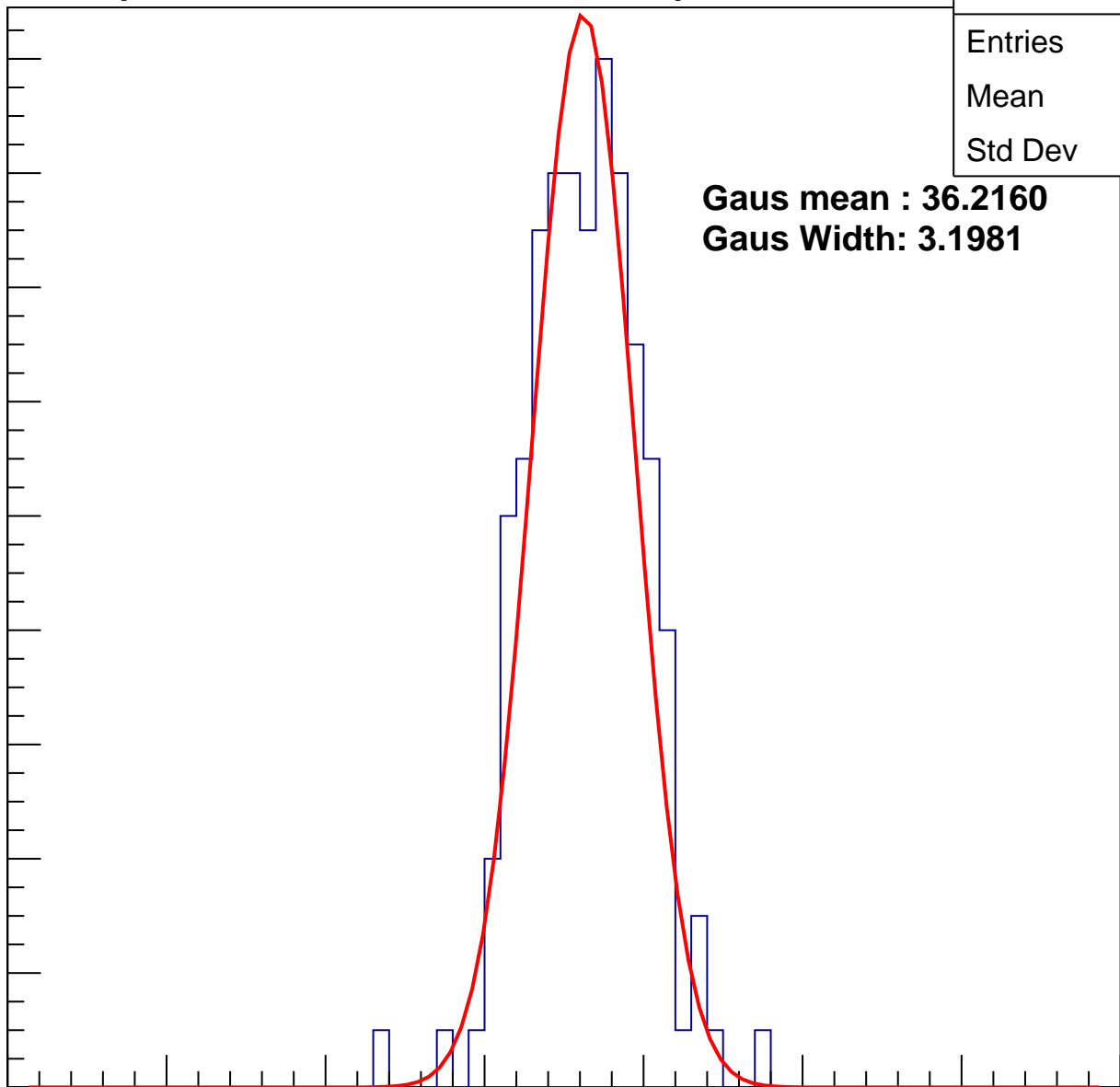
Gaus Width: 3.1981

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch14, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	97
Mean	42.94
Std Dev	3.314

Gaus mean : 43.1078

Gaus Width: 2.8475

0

2

4

6

8

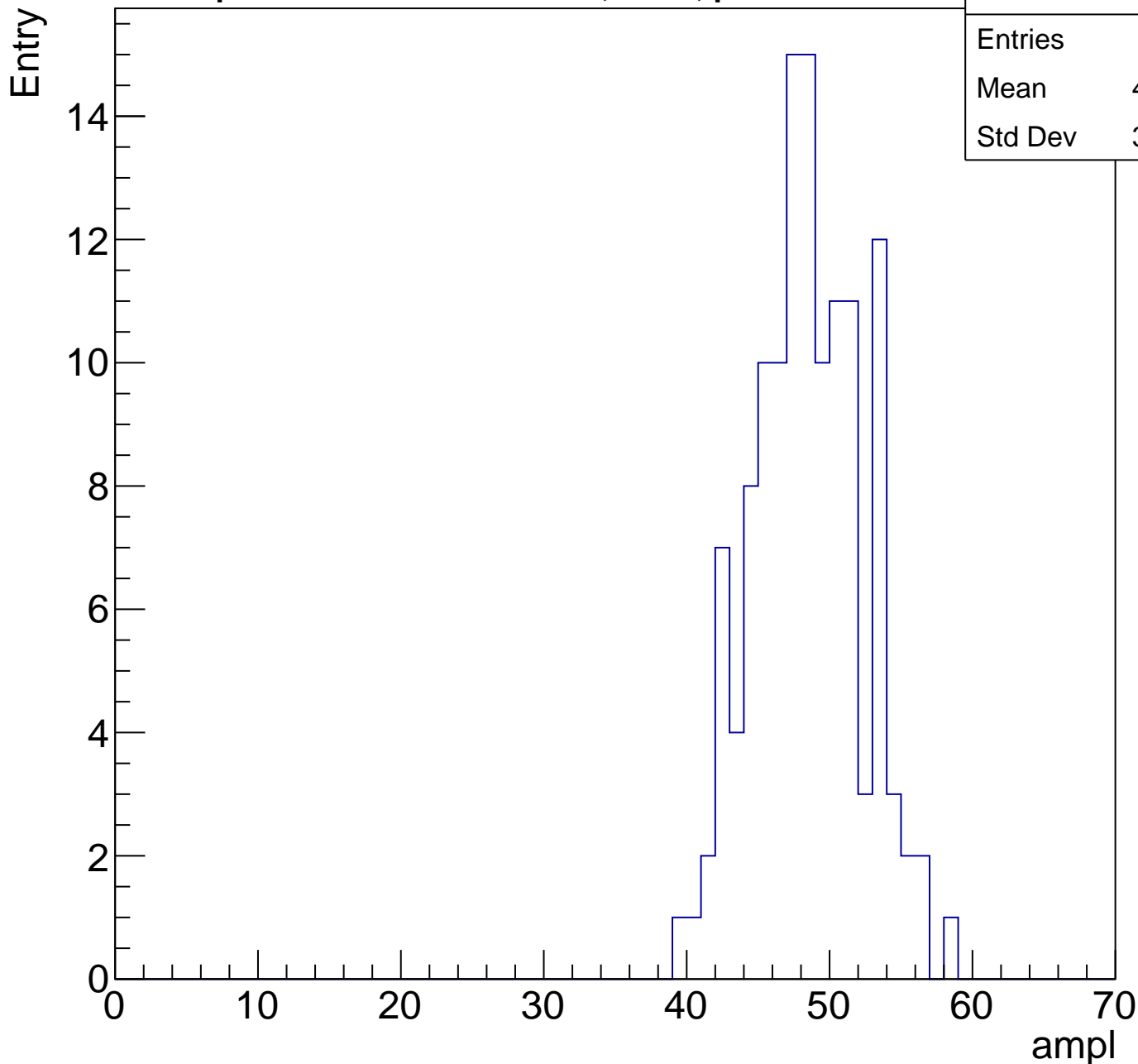
10

12

B0L101S, U19-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	48.04
Std Dev	3.753



B0L101S, U19-ch14, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	54.07
Std Dev	3.535

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

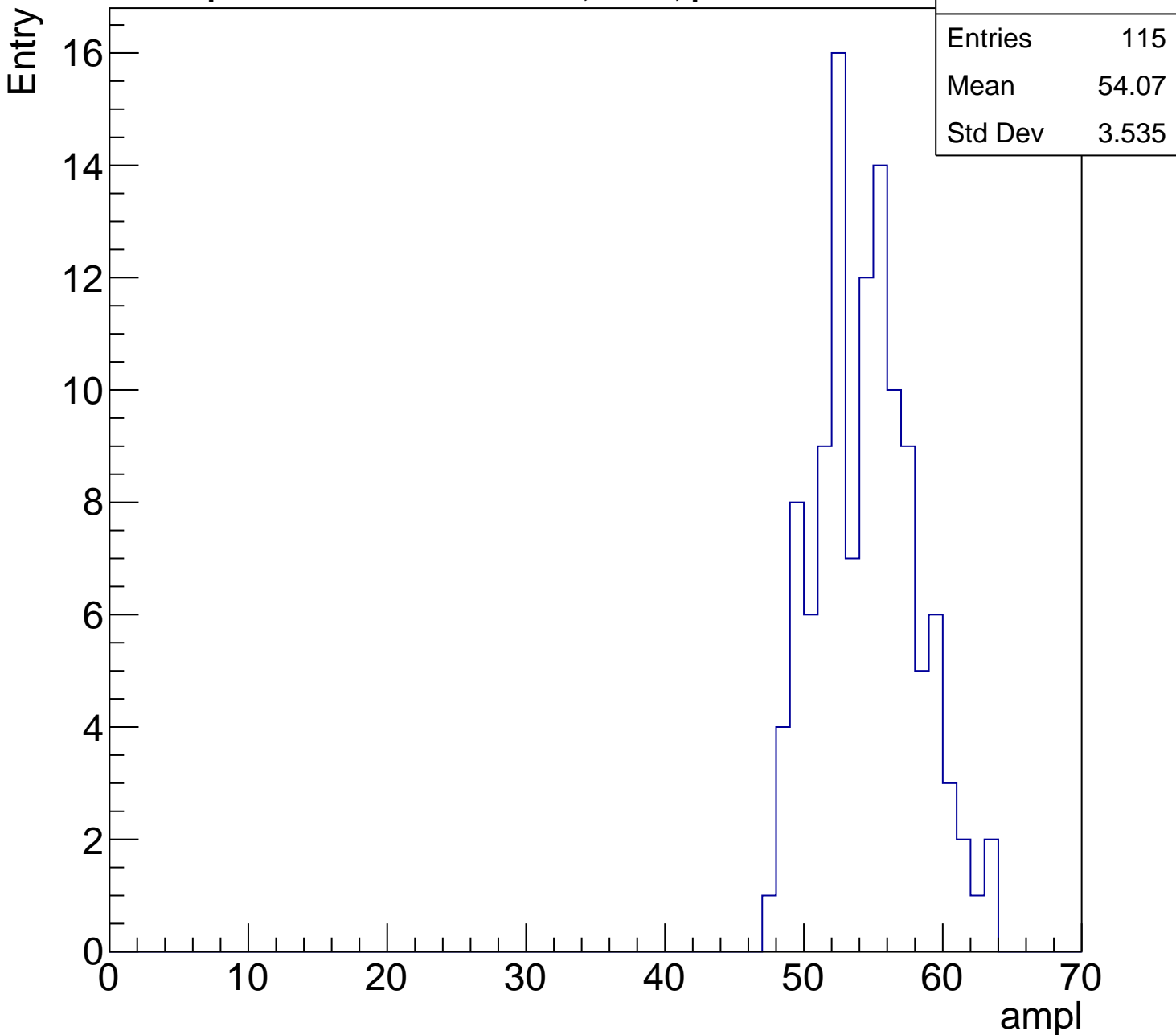
40

50

60

70

ampl



B0L101S, U19-ch14, adc5

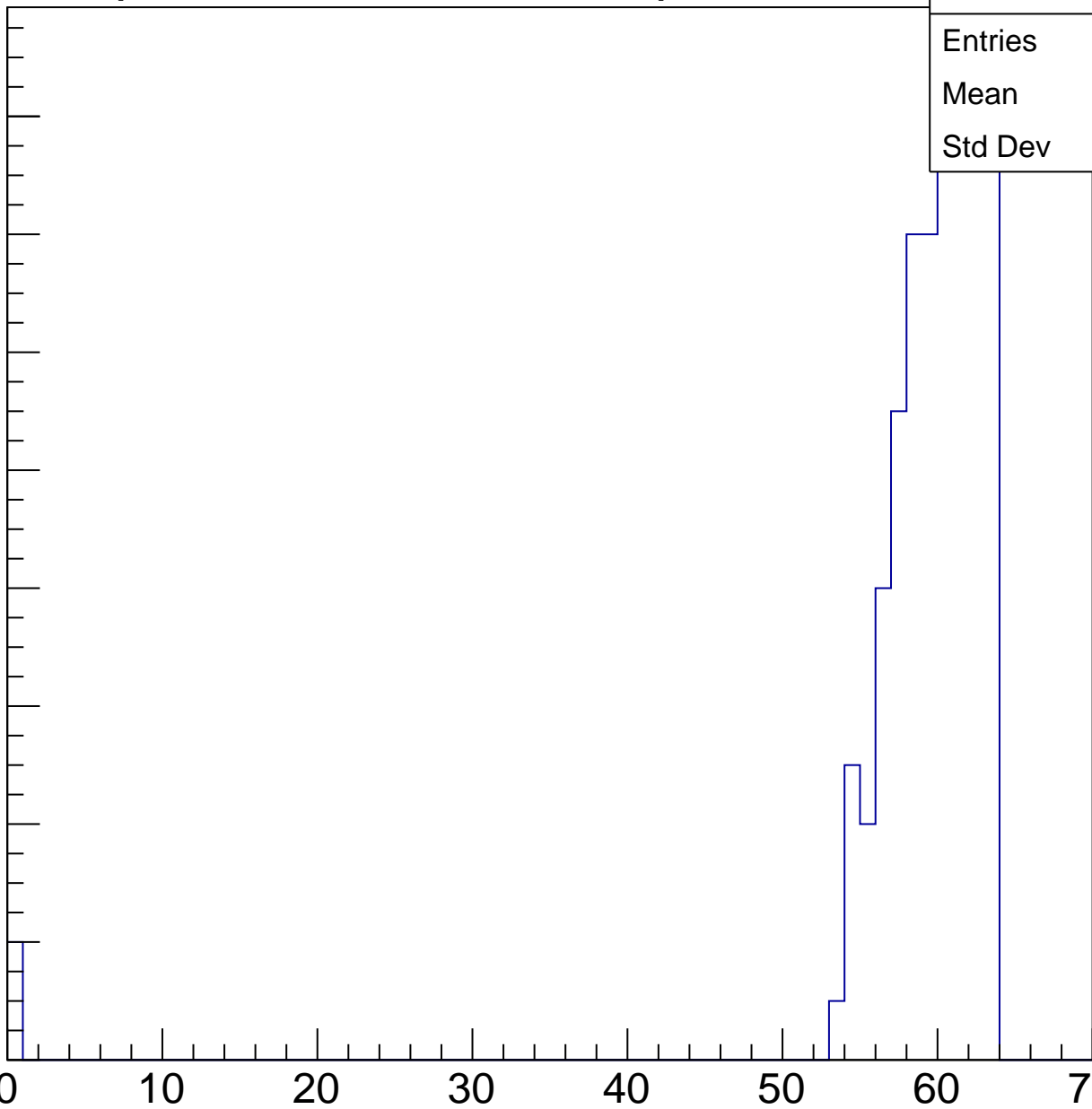
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	58.49
Std Dev	7.915

ampl



B0L101S, U19-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

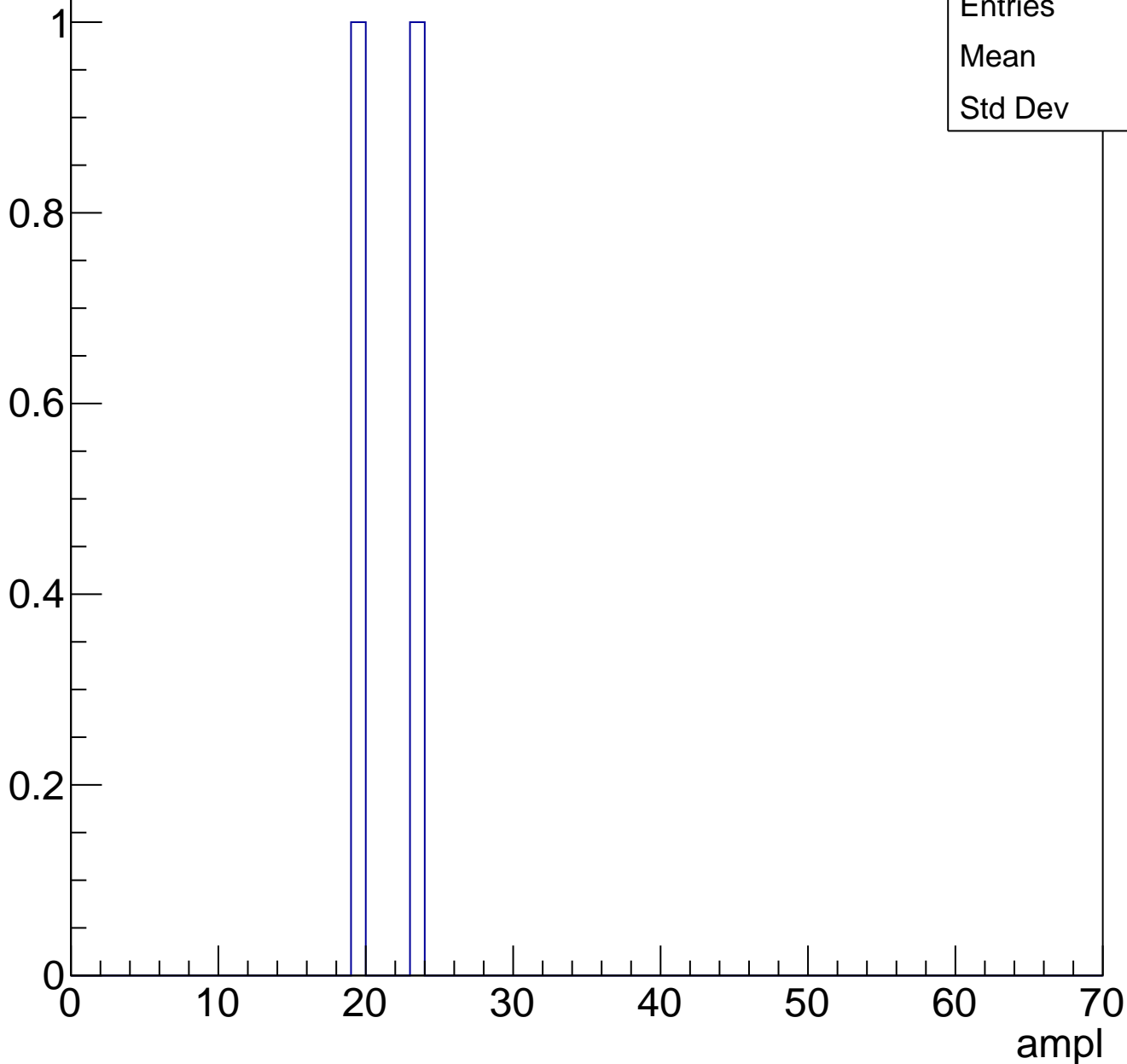
Entry



B0L101S, U19-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch15, adc0

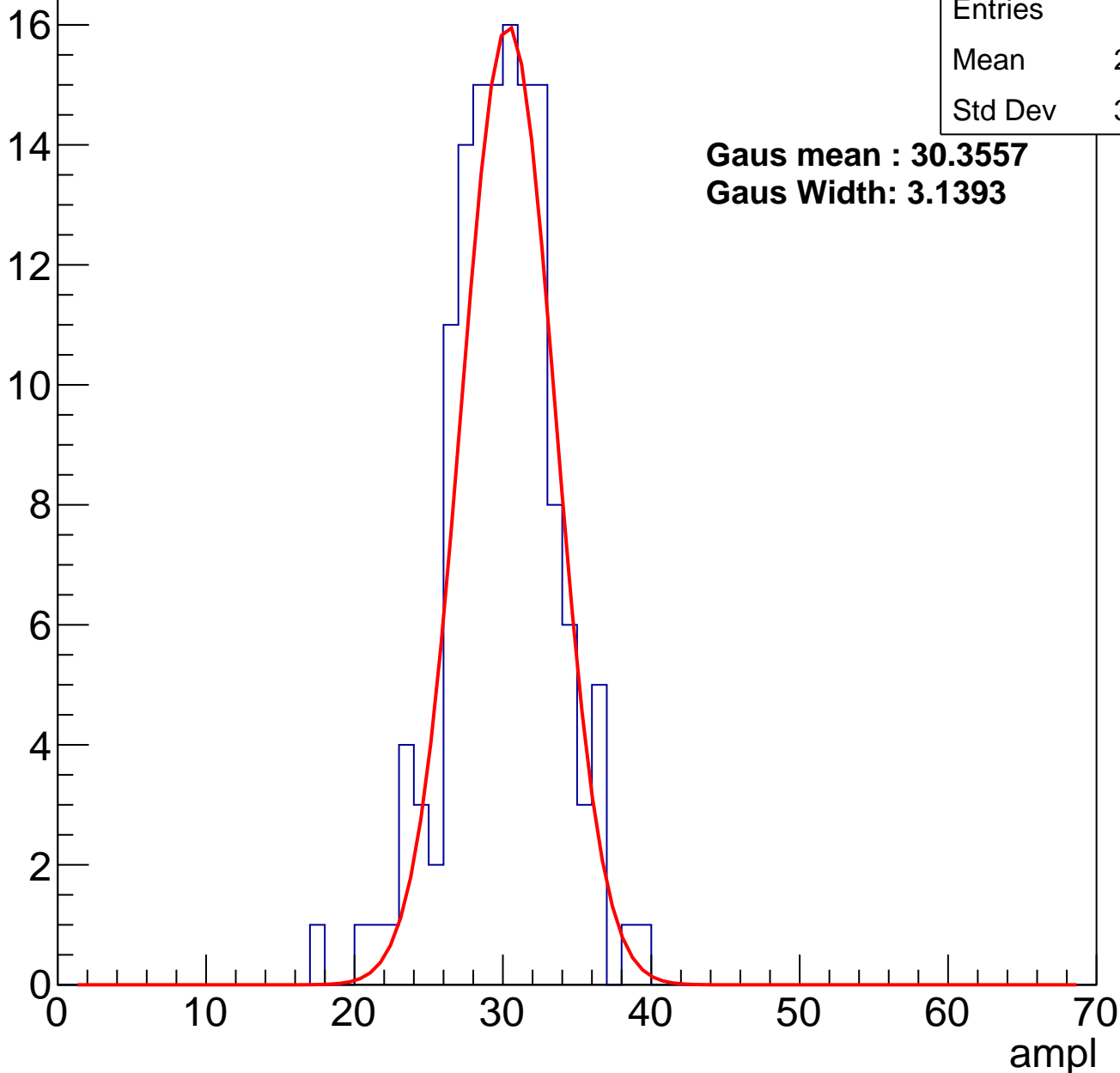
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	29.48
Std Dev	3.562

Gaus mean : 30.3557

Gaus Width: 3.1393

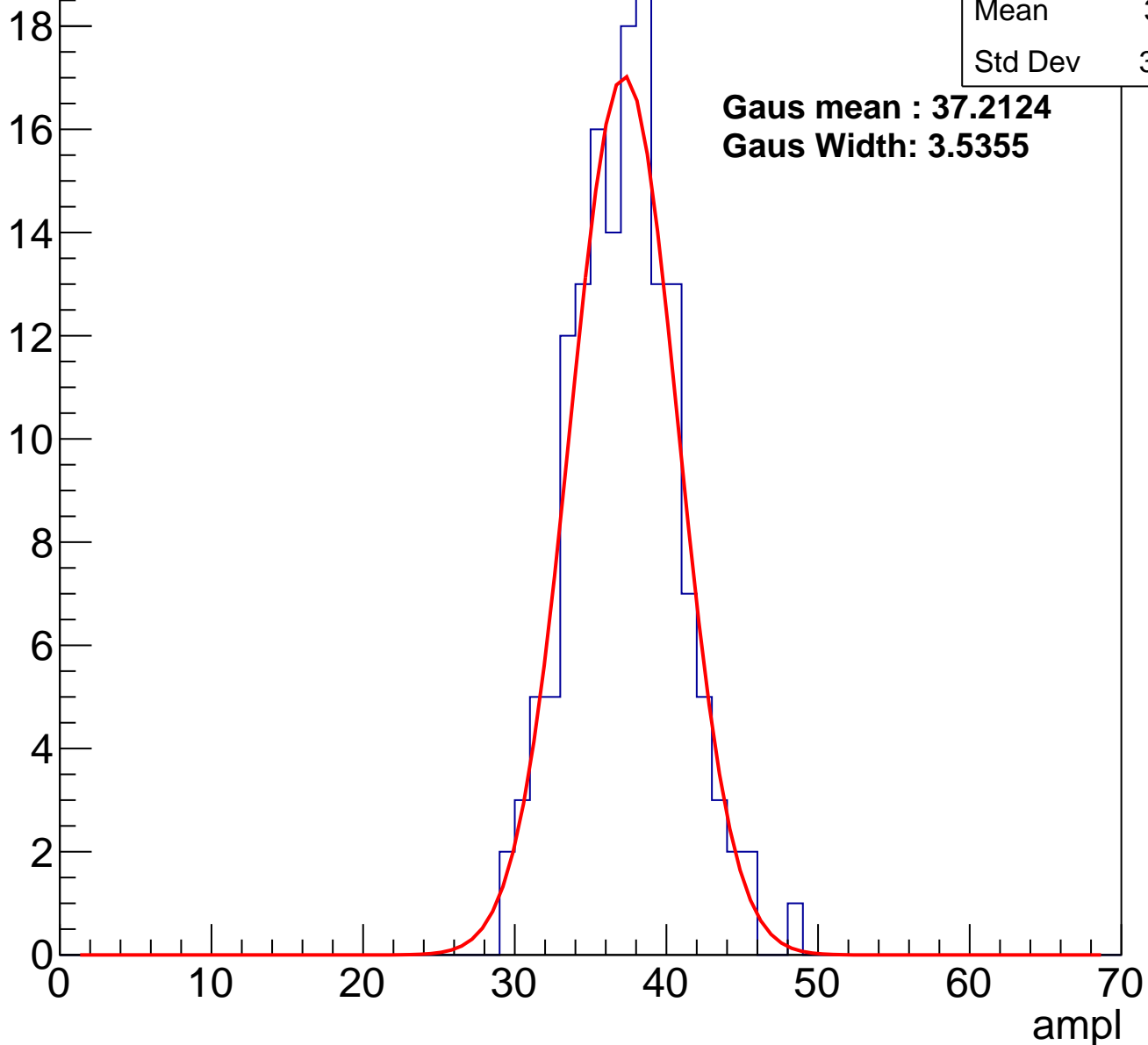
Entry



B0L101S, U19-ch15, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch15, adc2

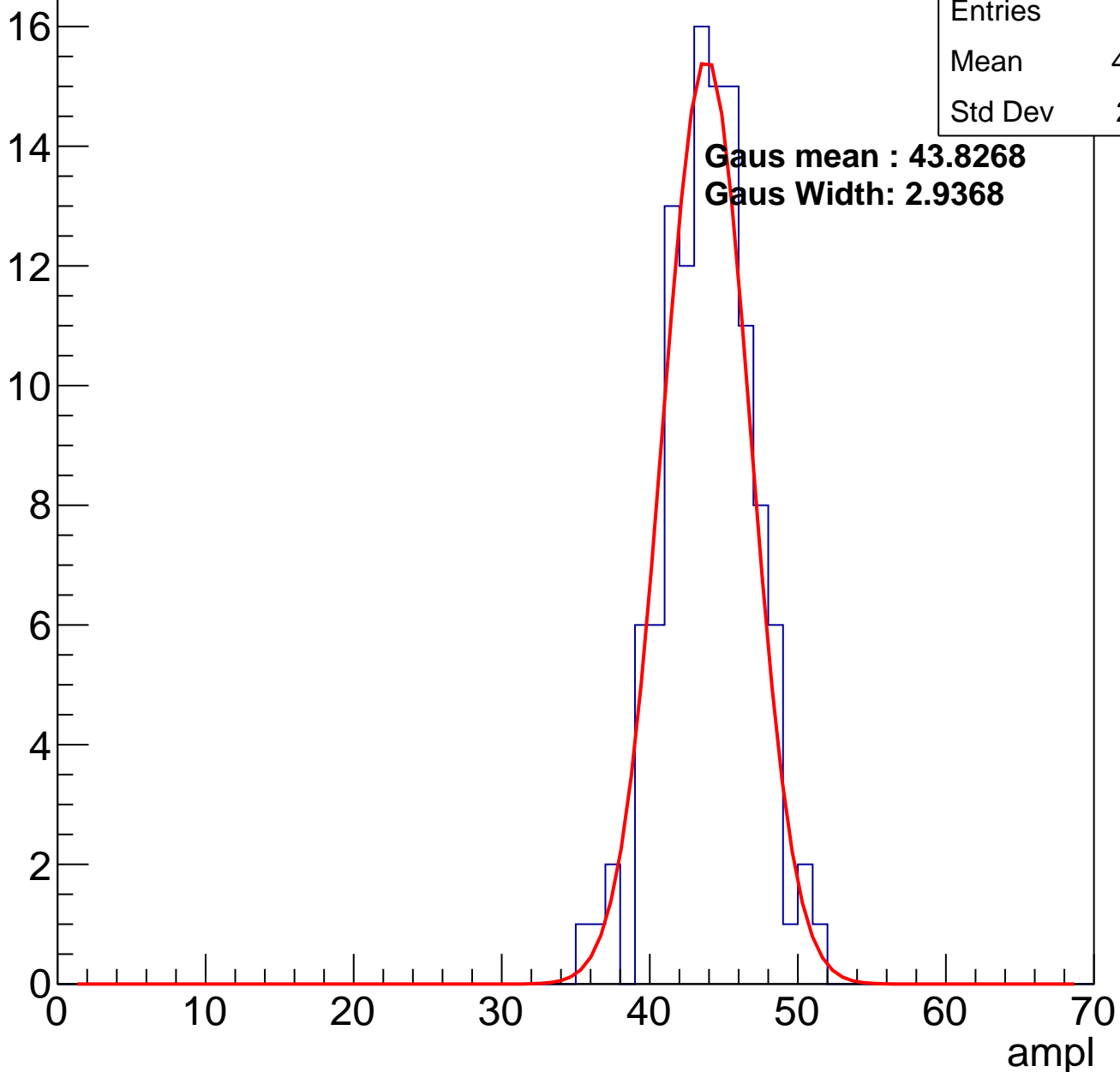
calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	43.53
Std Dev	2.961

Gaus mean : 43.8268

Gaus Width: 2.9368

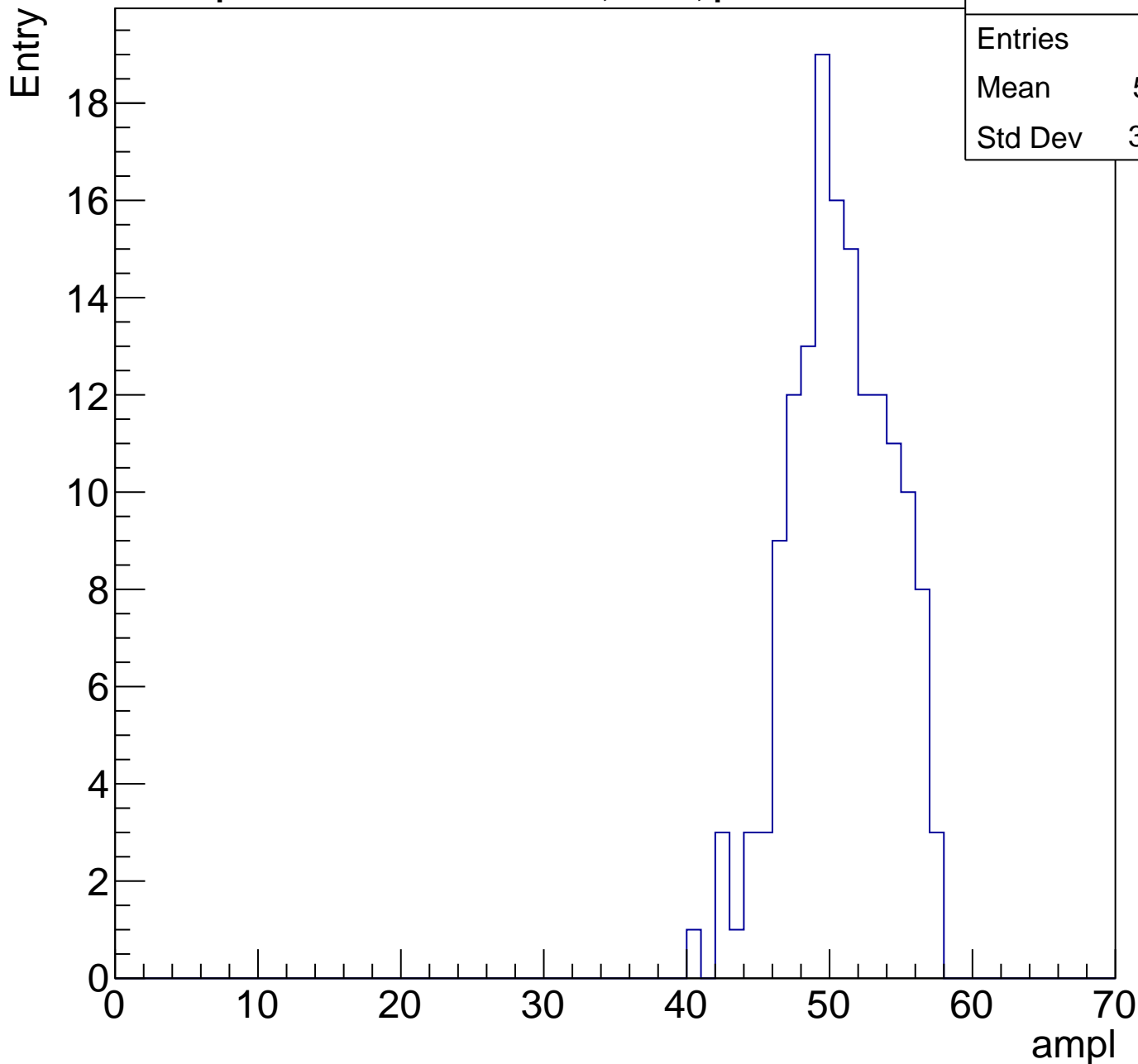
Entry



B0L101S, U19-ch15, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	50.31
Std Dev	3.514

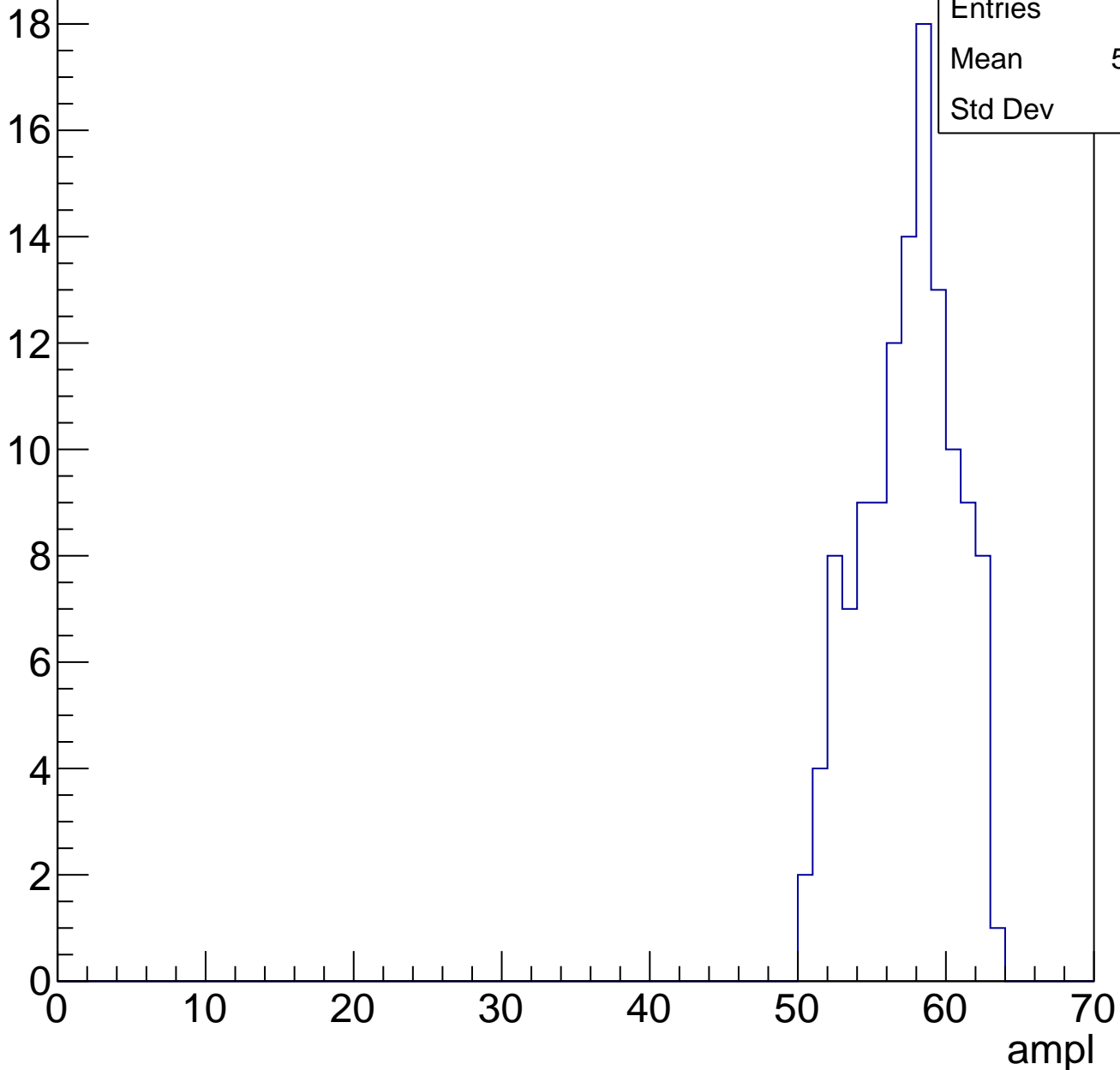


B0L101S, U19-ch15, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	56.94
Std Dev	3.14

Entry



B0L101S, U19-ch15, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

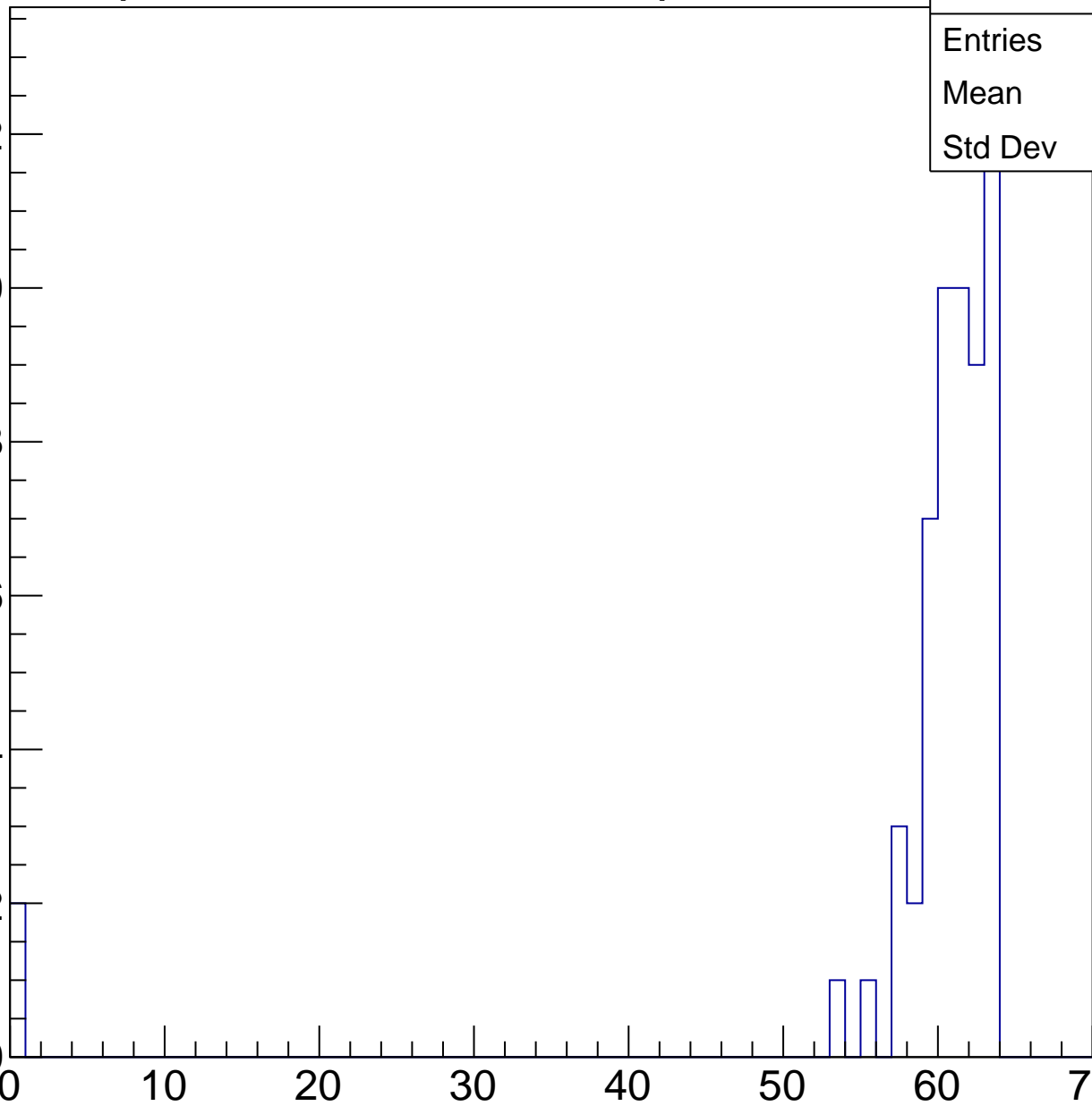
4

2

0

Entries	58
Mean	58.53
Std Dev	11.26

ampl

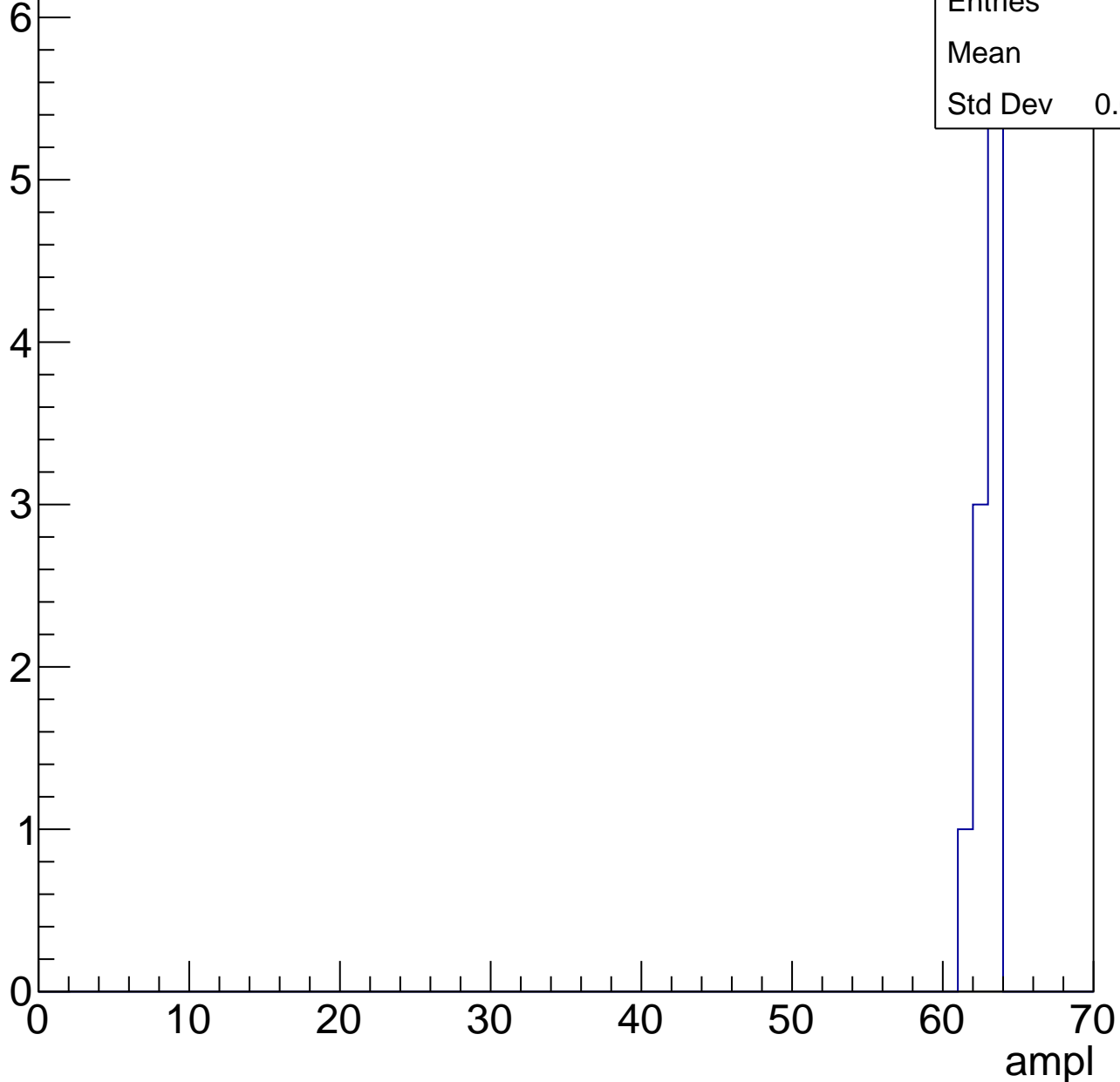


B0L101S, U19-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.5
Std Dev	0.6708



B0L101S, U19-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

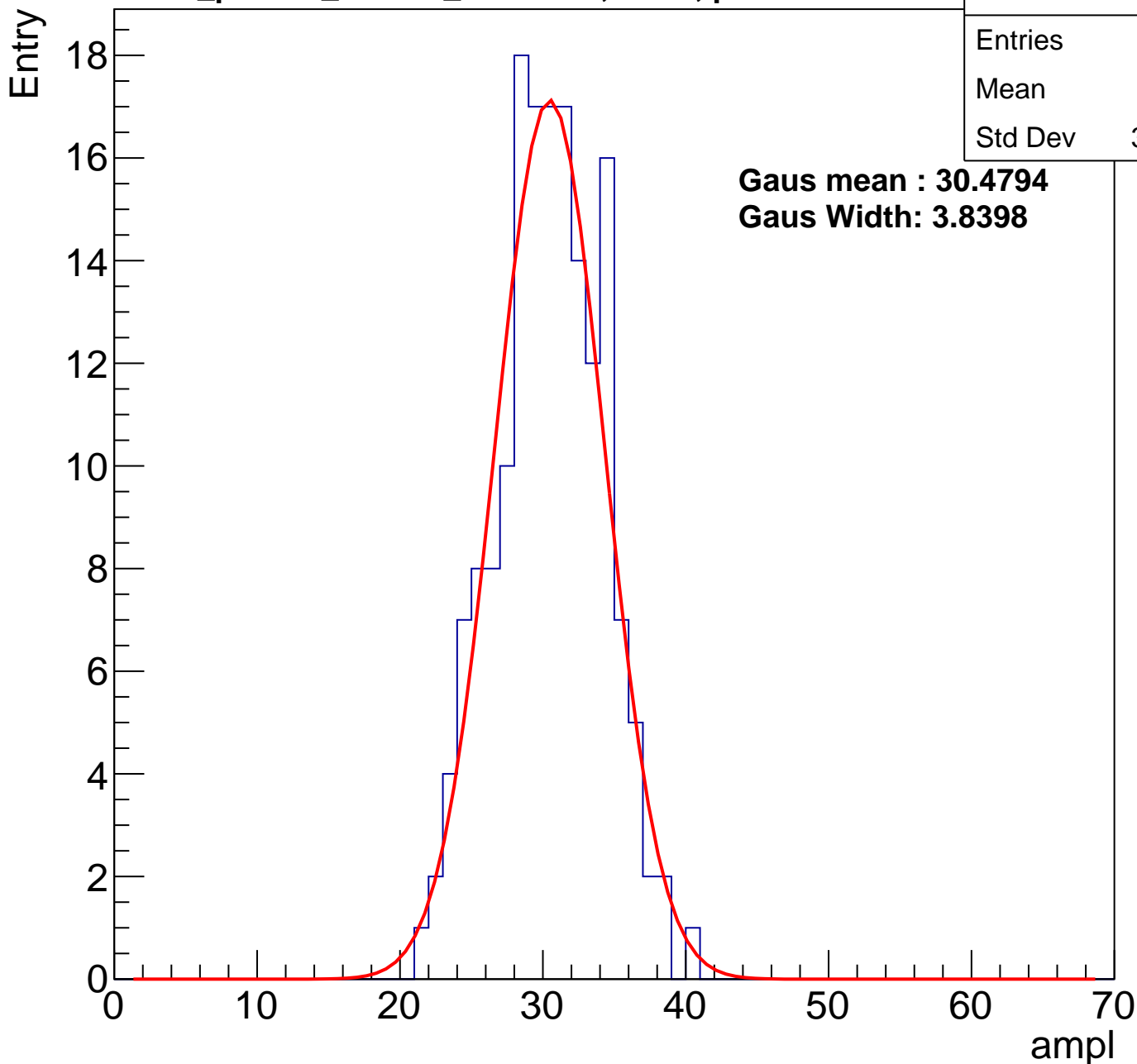
B0L101S, U19-ch16, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	30
Std Dev	3.686

Gaus mean : 30.4794

Gaus Width: 3.8398



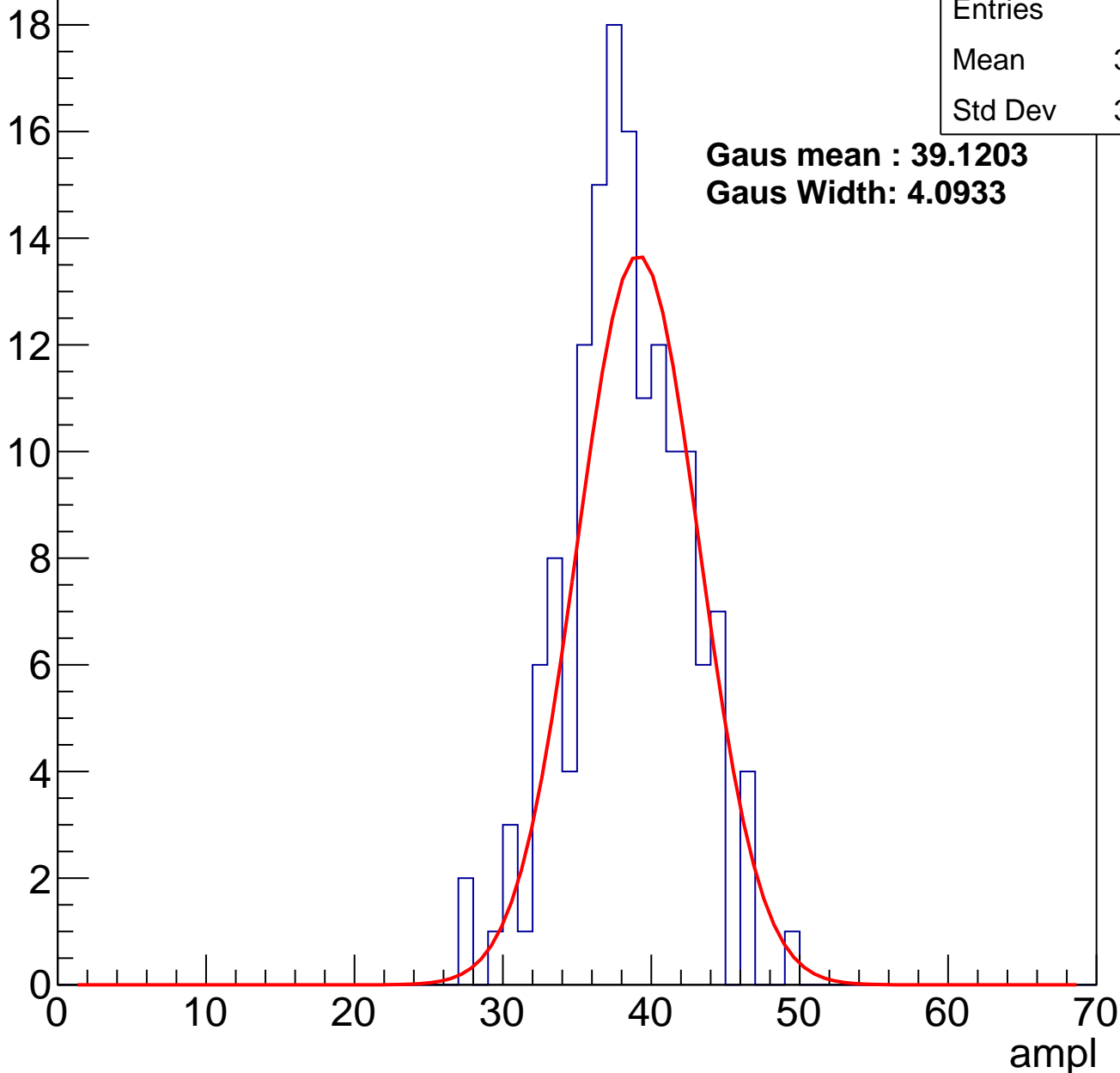
B0L101S, U19-ch16, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	37.88
Std Dev	3.969

Gaus mean : 39.1203
Gaus Width: 4.0933

Entry



B0L101S, U19-ch16, adc2

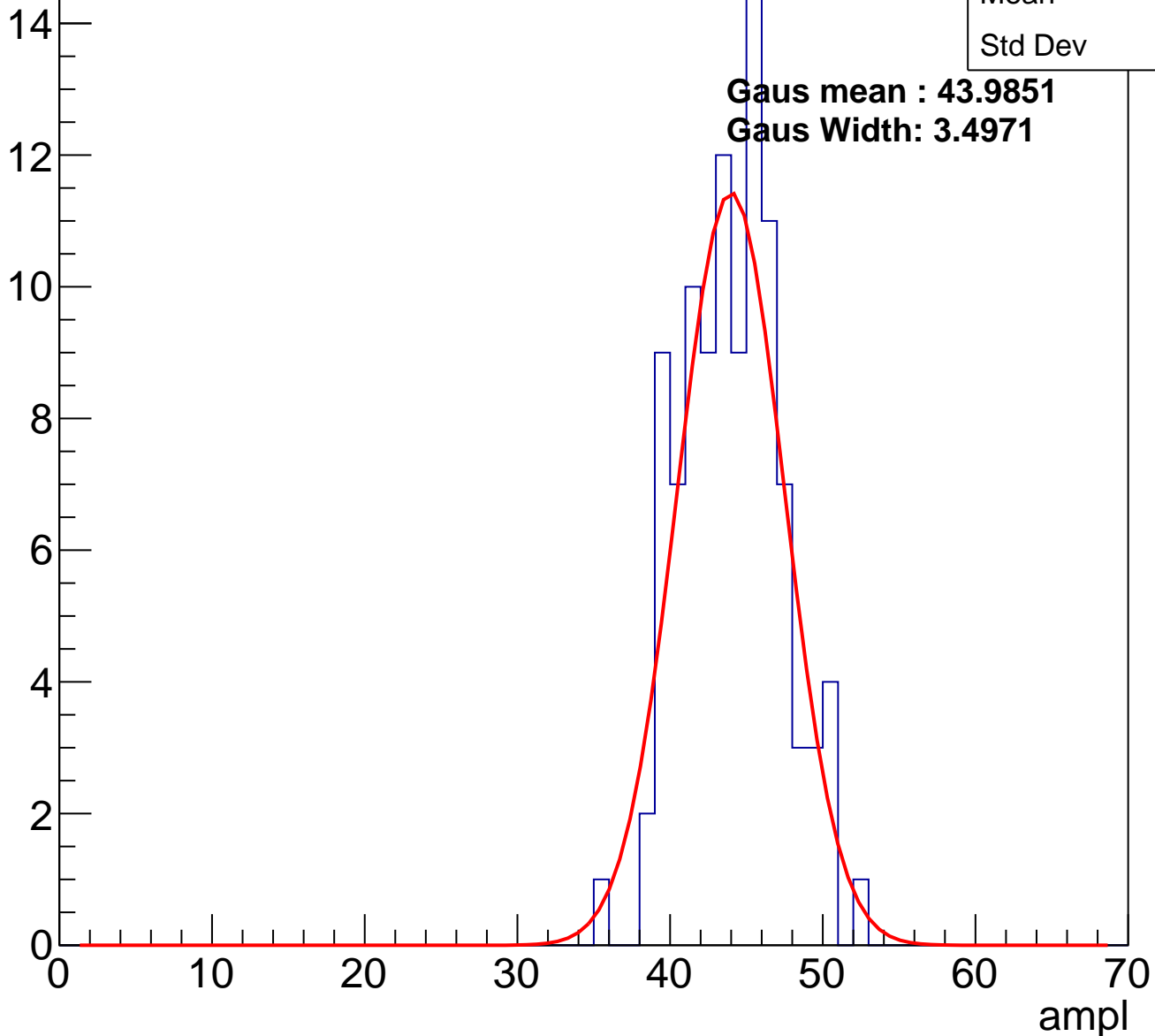
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	43.64
Std Dev	3.22

Gaus mean : 43.9851

Gaus Width: 3.4971

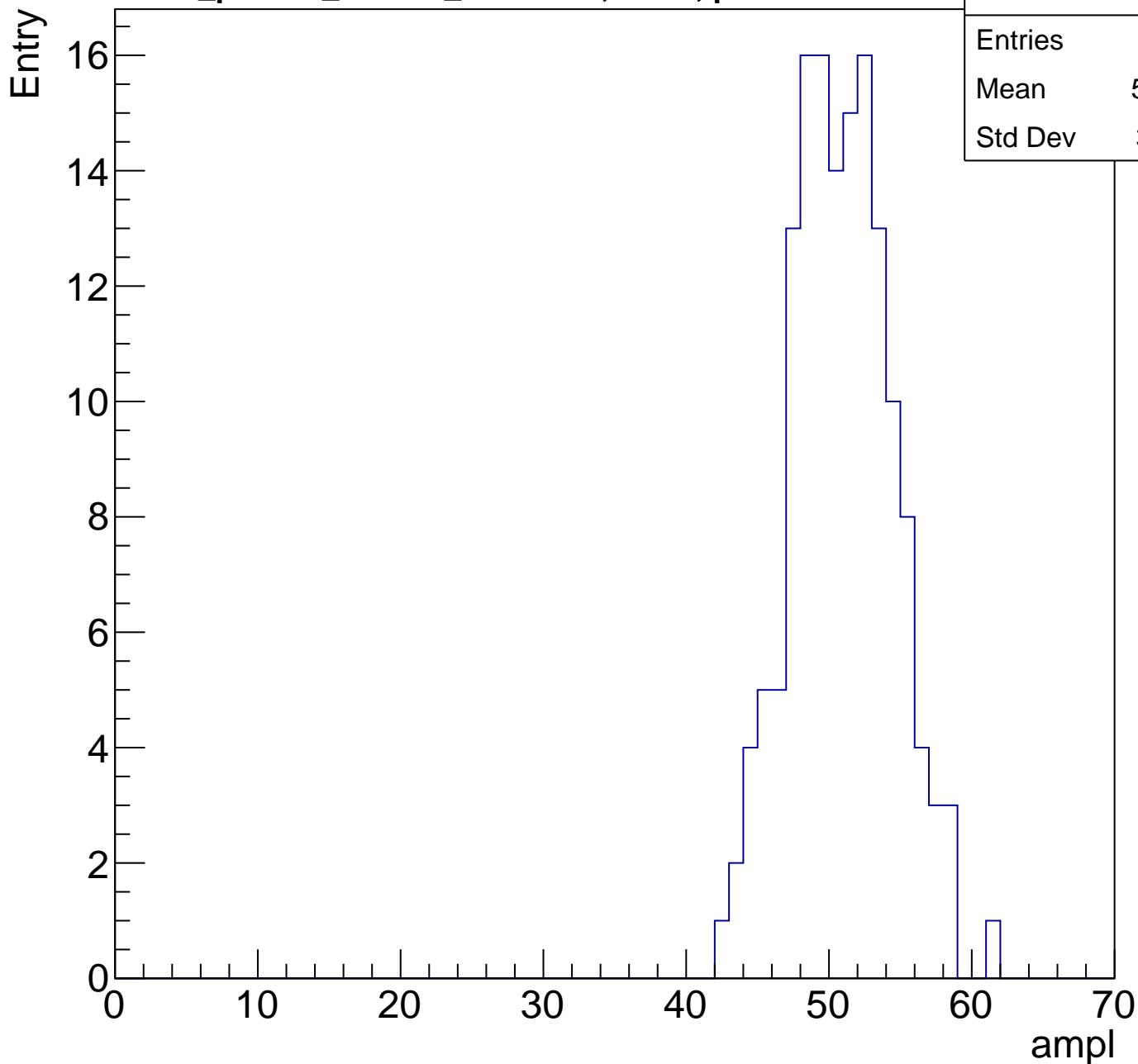
Entry



B0L101S, U19-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	50.46
Std Dev	3.511



B0L101S, U19-ch16, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	56.48
Std Dev	3.156

Entry

12

10

8

6

4

2

0

0

10

20

30

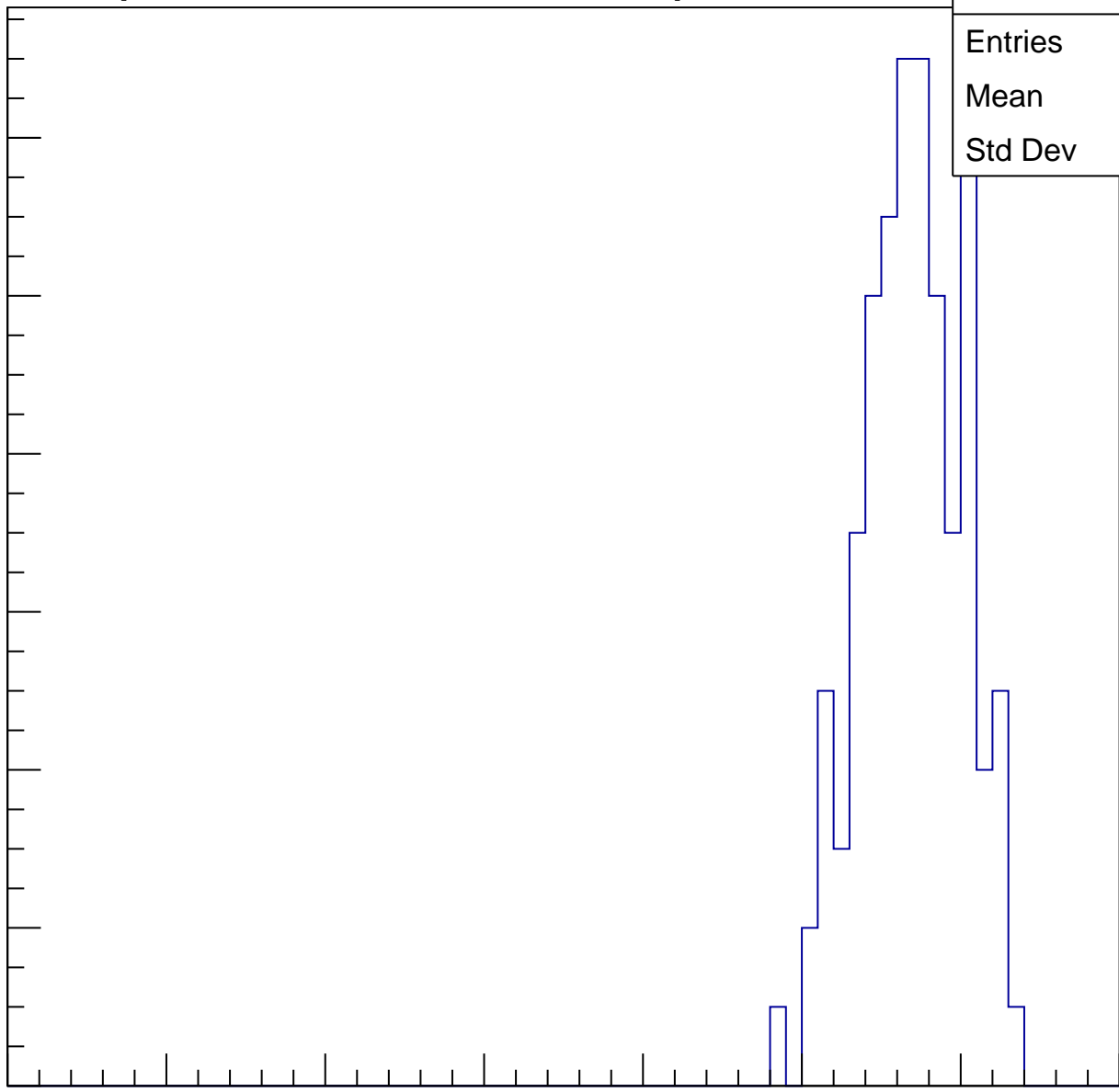
40

50

60

70

ampl



B0L101S, U19-ch16, adc5

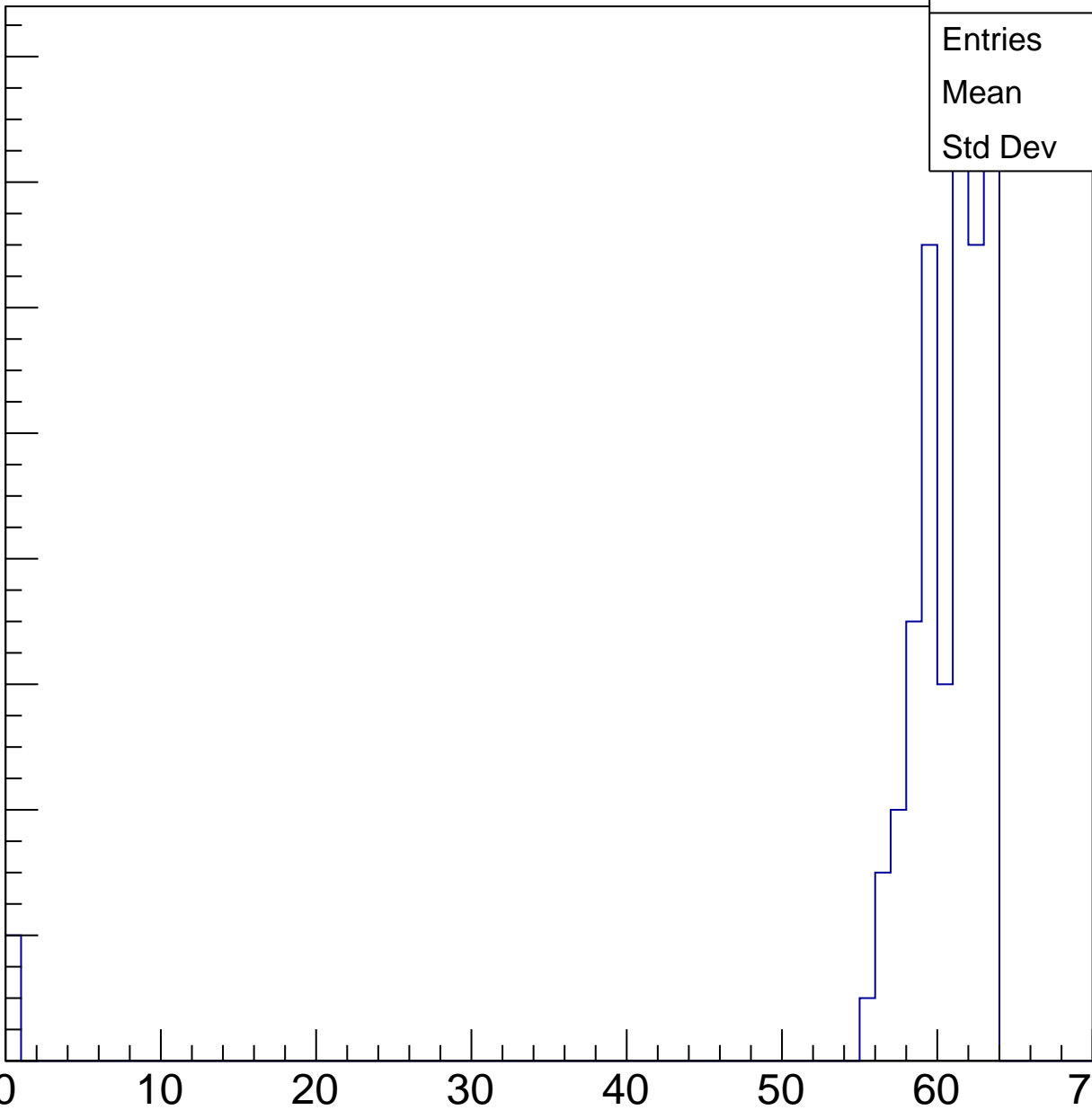
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	58.91
Std Dev	9.661

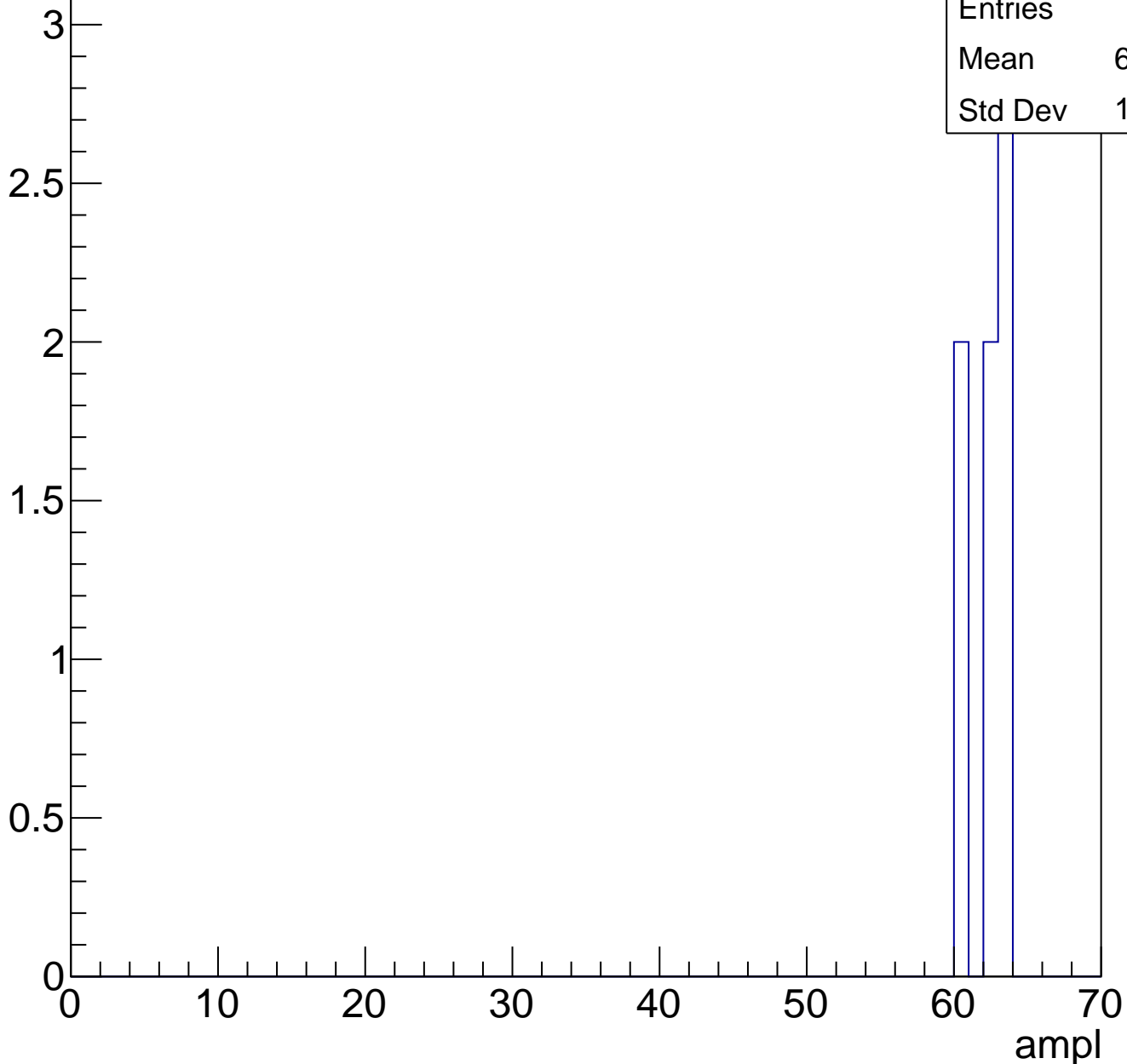
ampl



B0L101S, U19-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch17, adc0

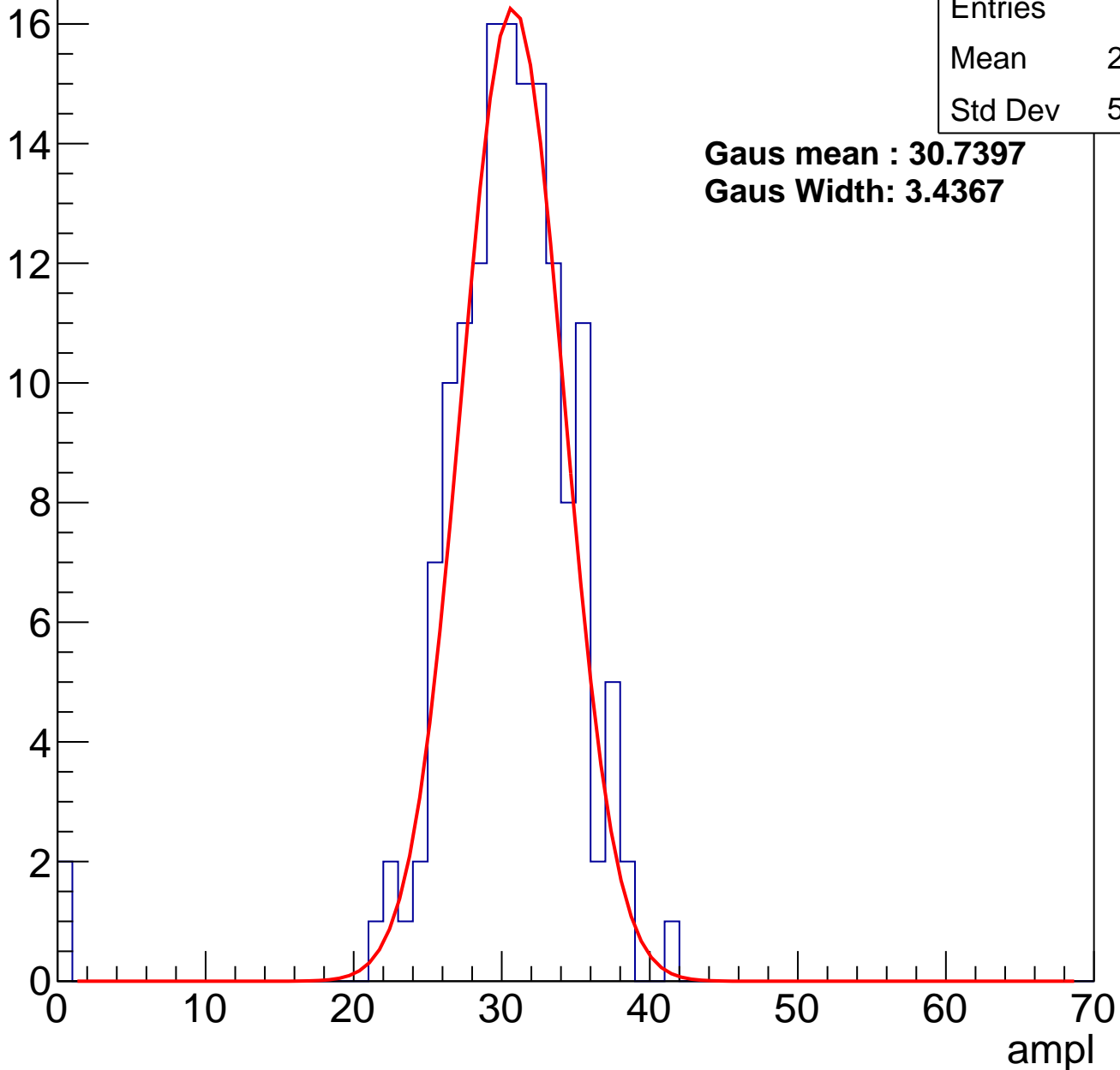
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	29.93
Std Dev	5.003

Gaus mean : 30.7397

Gaus Width: 3.4367

Entry



B0L101S, U19-ch17, adc1

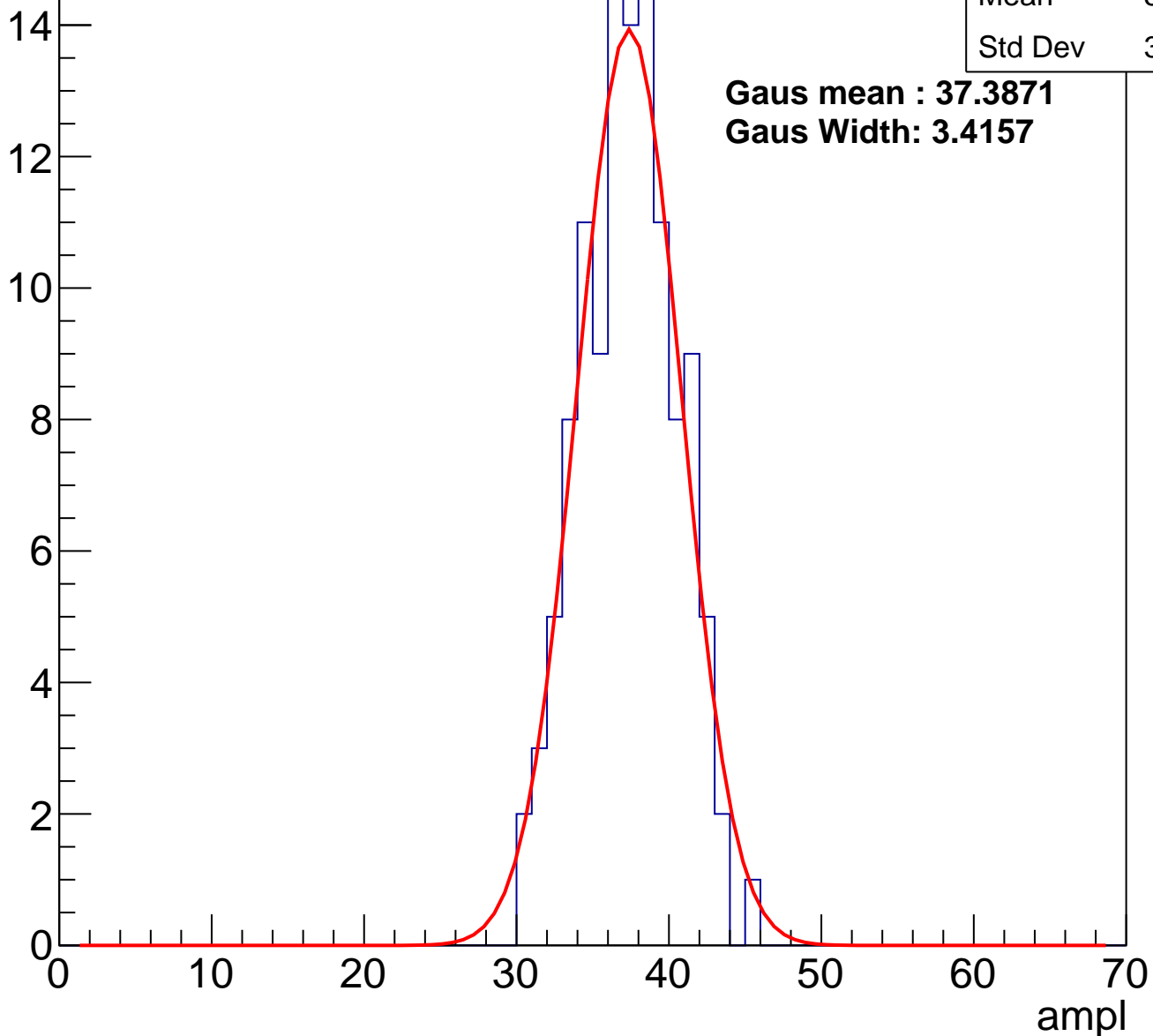
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	36.89
Std Dev	3.113

Gaus mean : 37.3871

Gaus Width: 3.4157

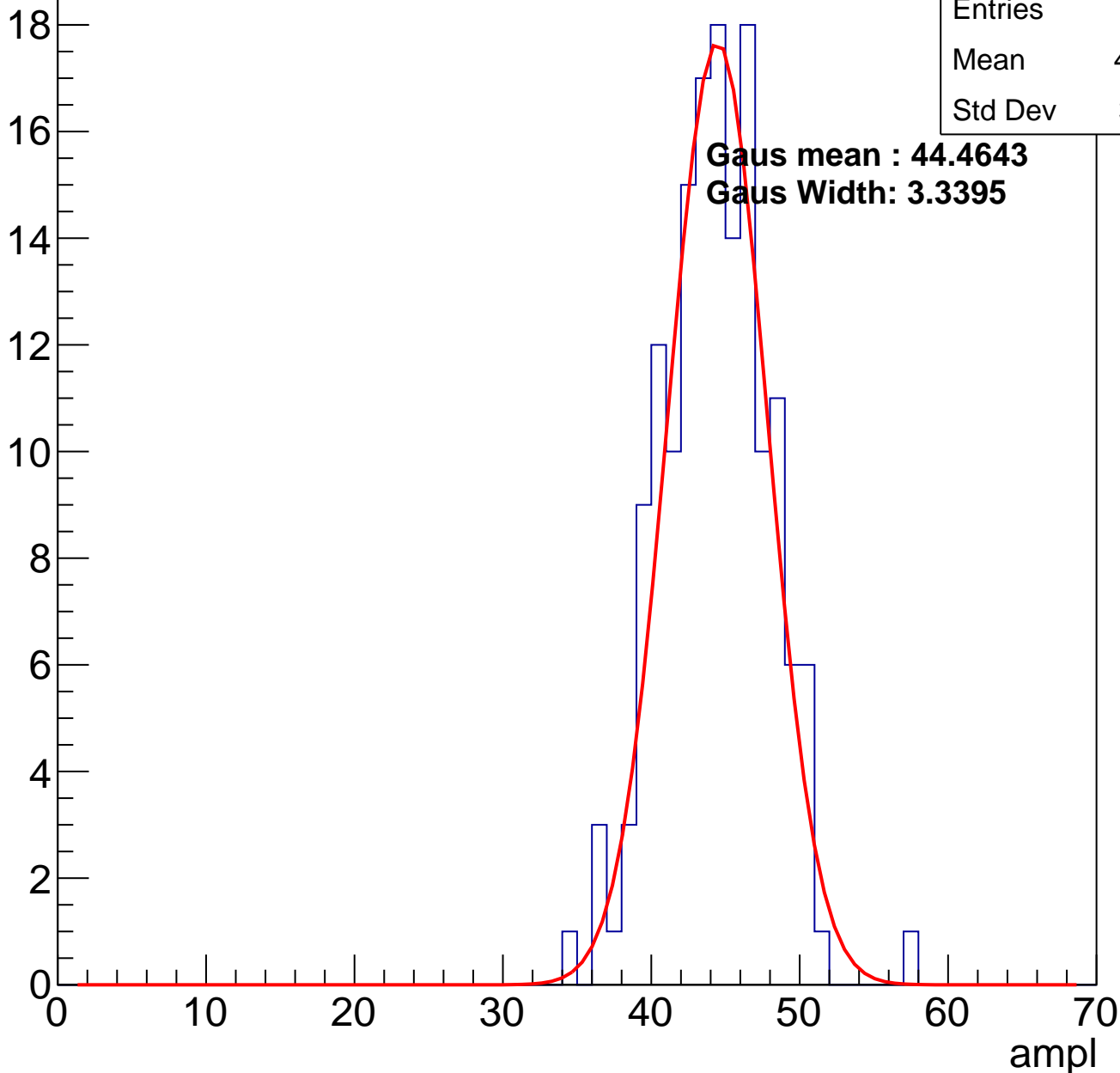
Entry



B0L101S, U19-ch17, adc2

calib_packv5_042523_0143.root, FC#1, port C1

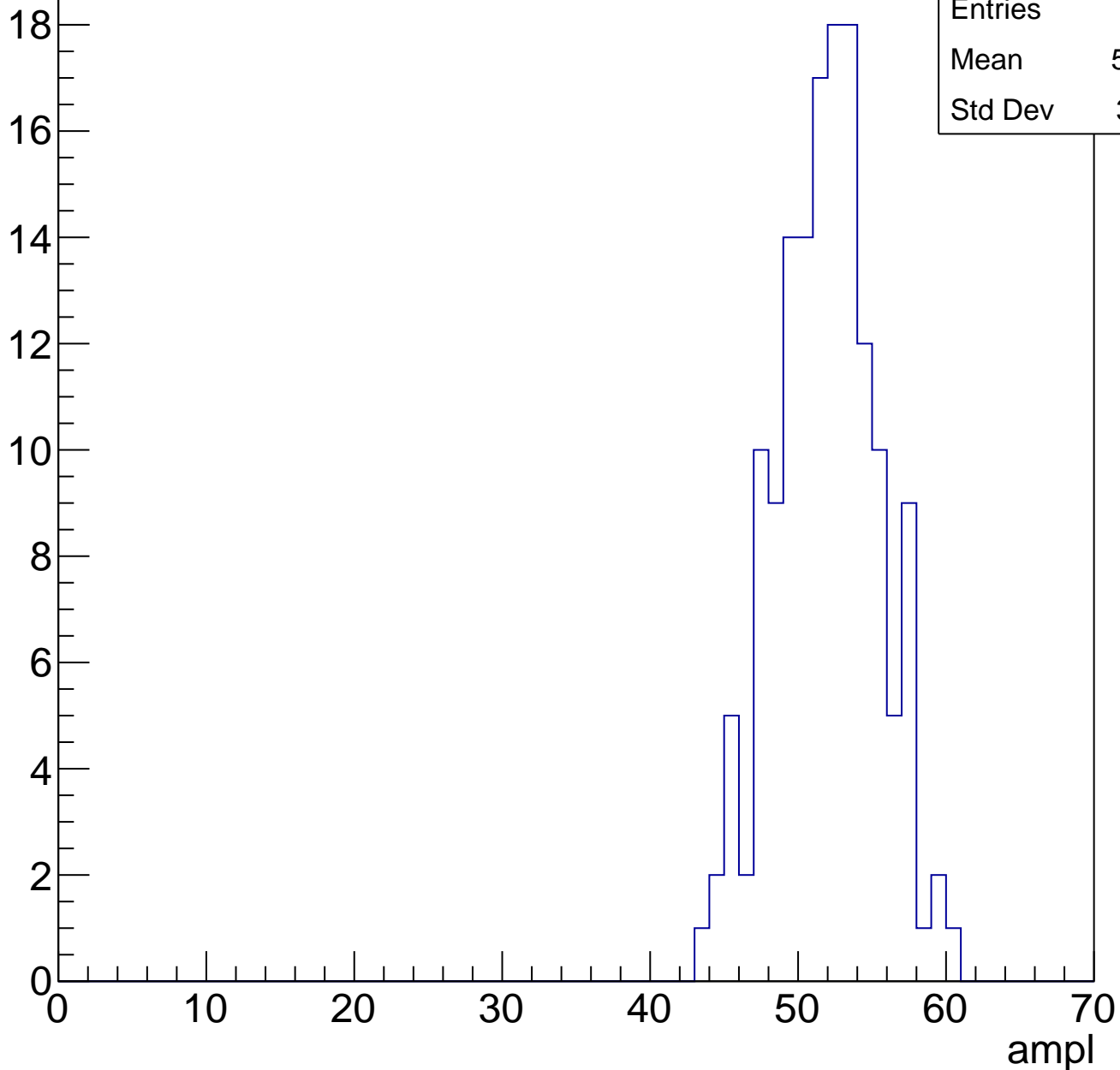
Entry



B0L101S, U19-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch17, adc4

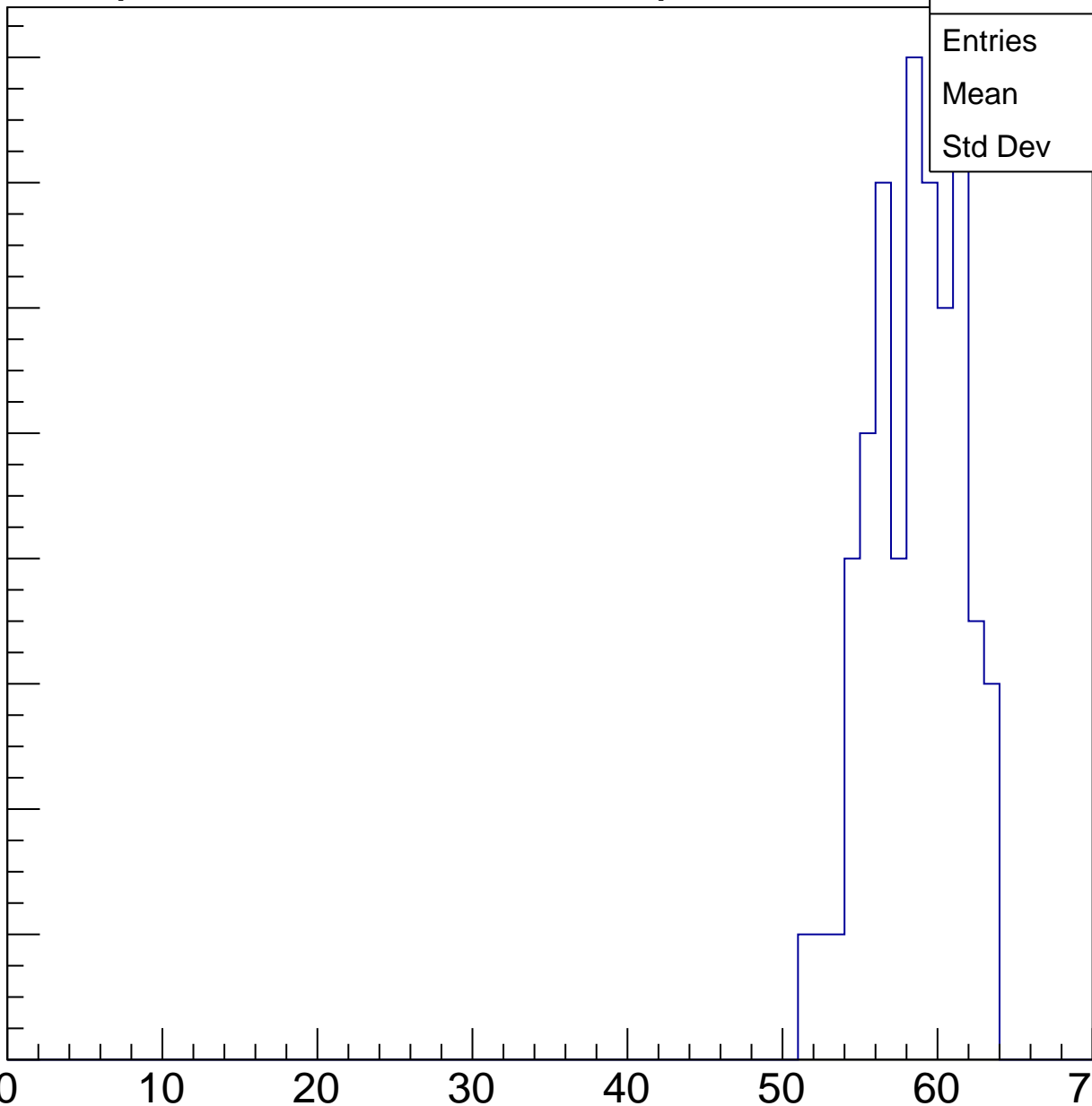
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	58.06
Std Dev	2.878

ampl



B0L101S, U19-ch17, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	49
Mean	59.8
Std Dev	8.832

ampl

0

10

20

30

40

50

60

70

B0L101S, U19-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	30.36
Std Dev	4.17

Gaus mean : 31.0445

Gaus Width: 3.0049

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

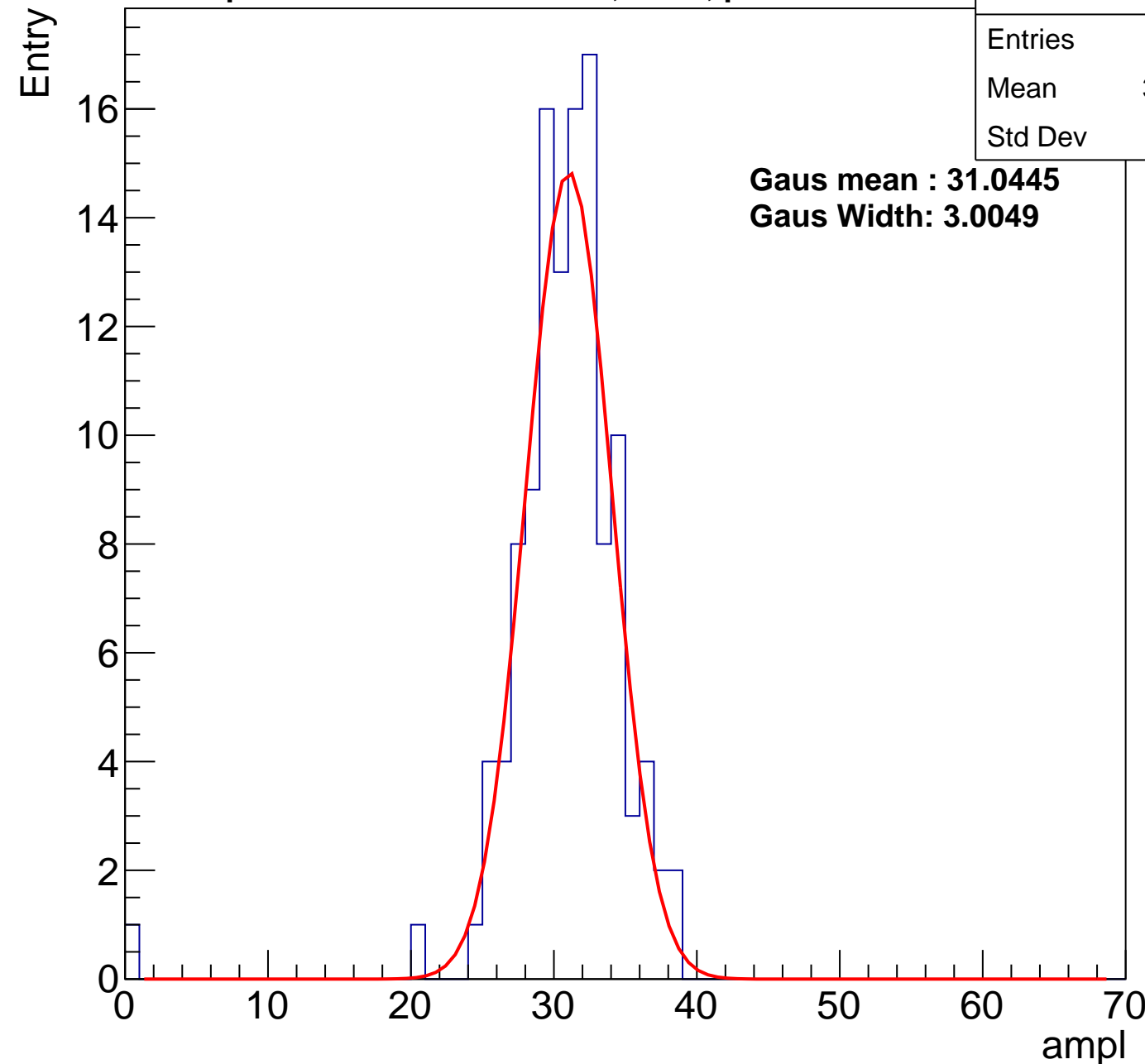
40

50

60

70

ampl

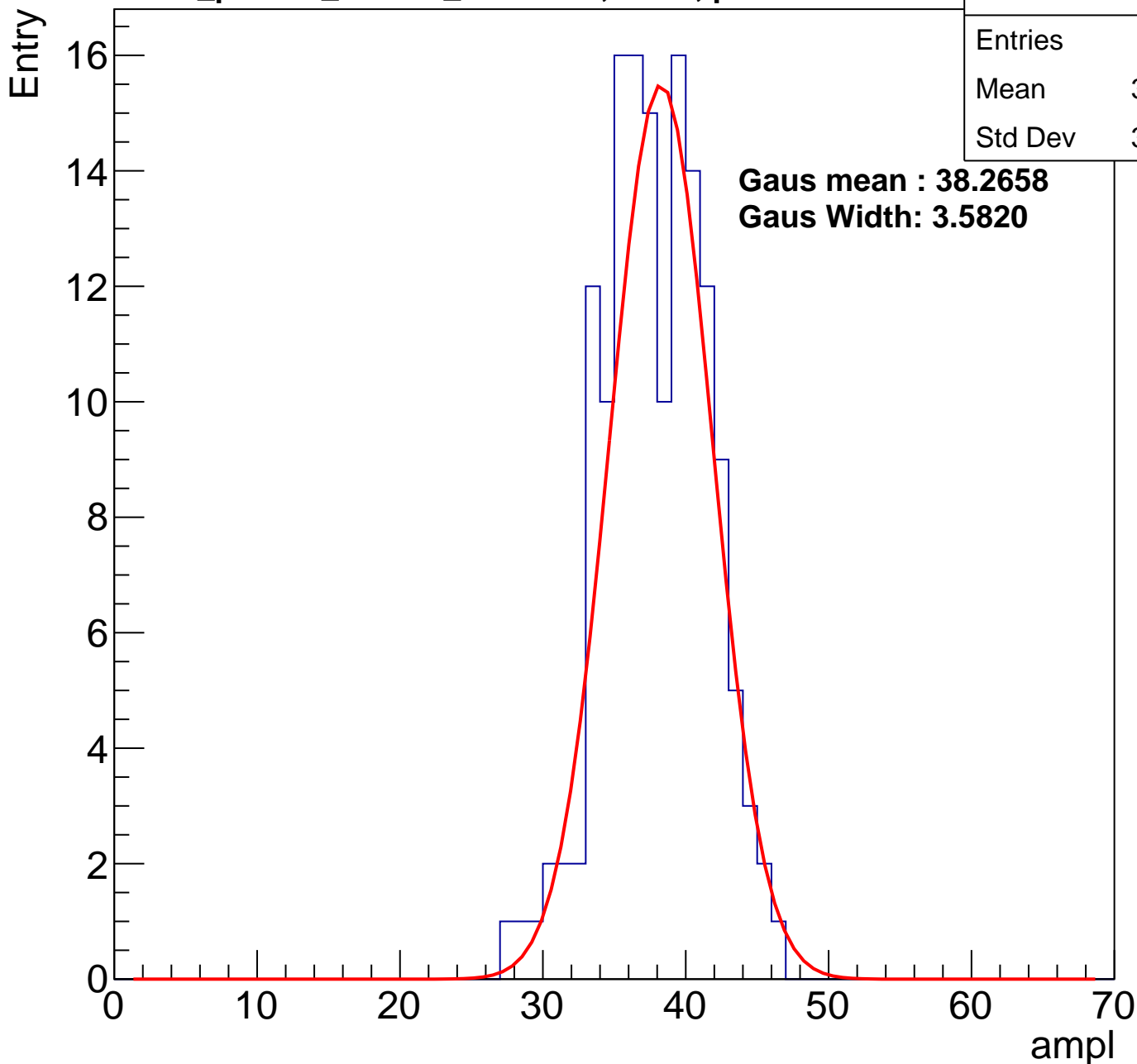


B0L101S, U19-ch18, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	37.43
Std Dev	3.608

Gaus mean : 38.2658
Gaus Width: 3.5820



B0L101S, U19-ch18, adc2

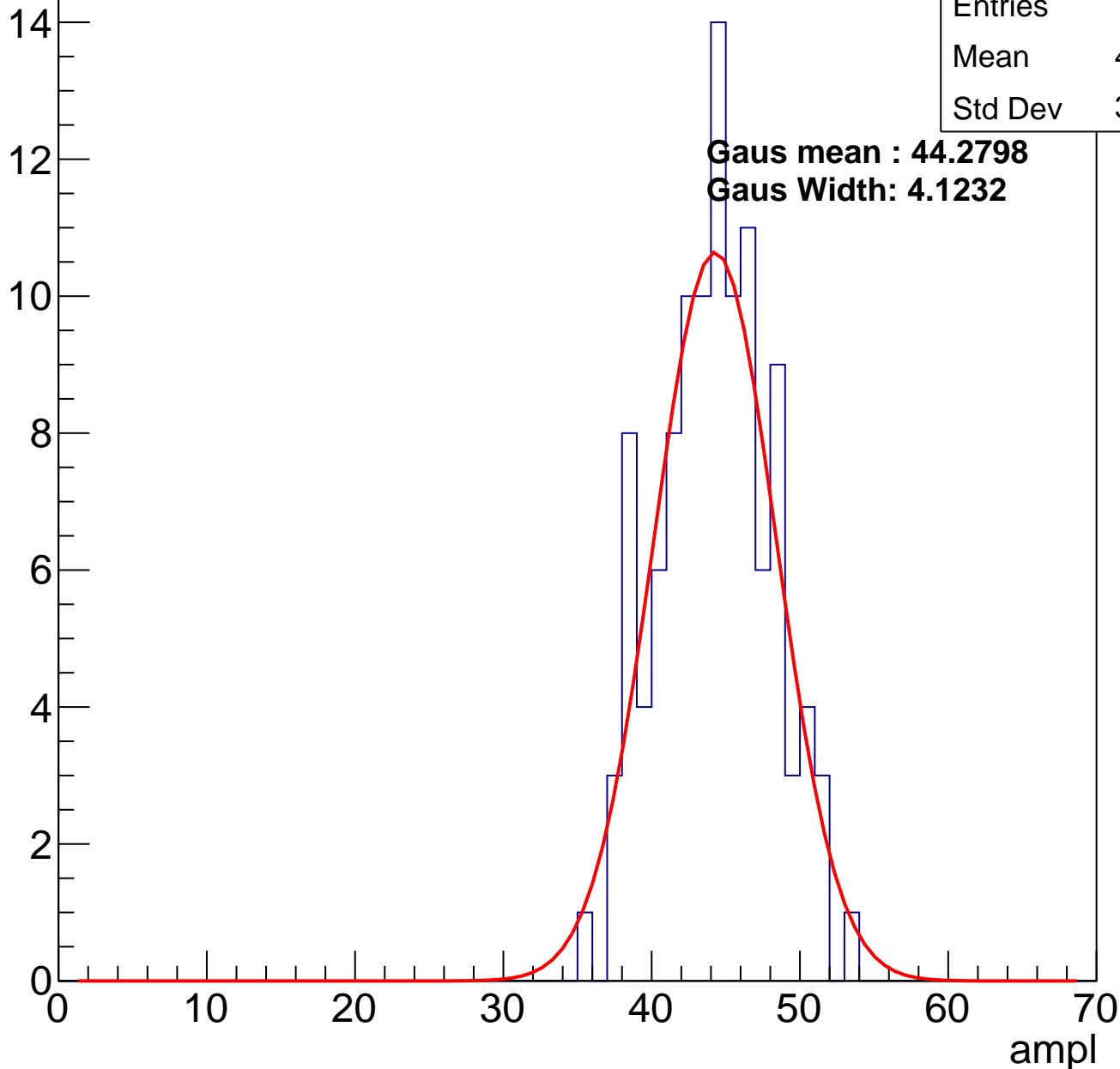
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	43.81
Std Dev	3.711

Gaus mean : 44.2798

Gaus Width: 4.1232

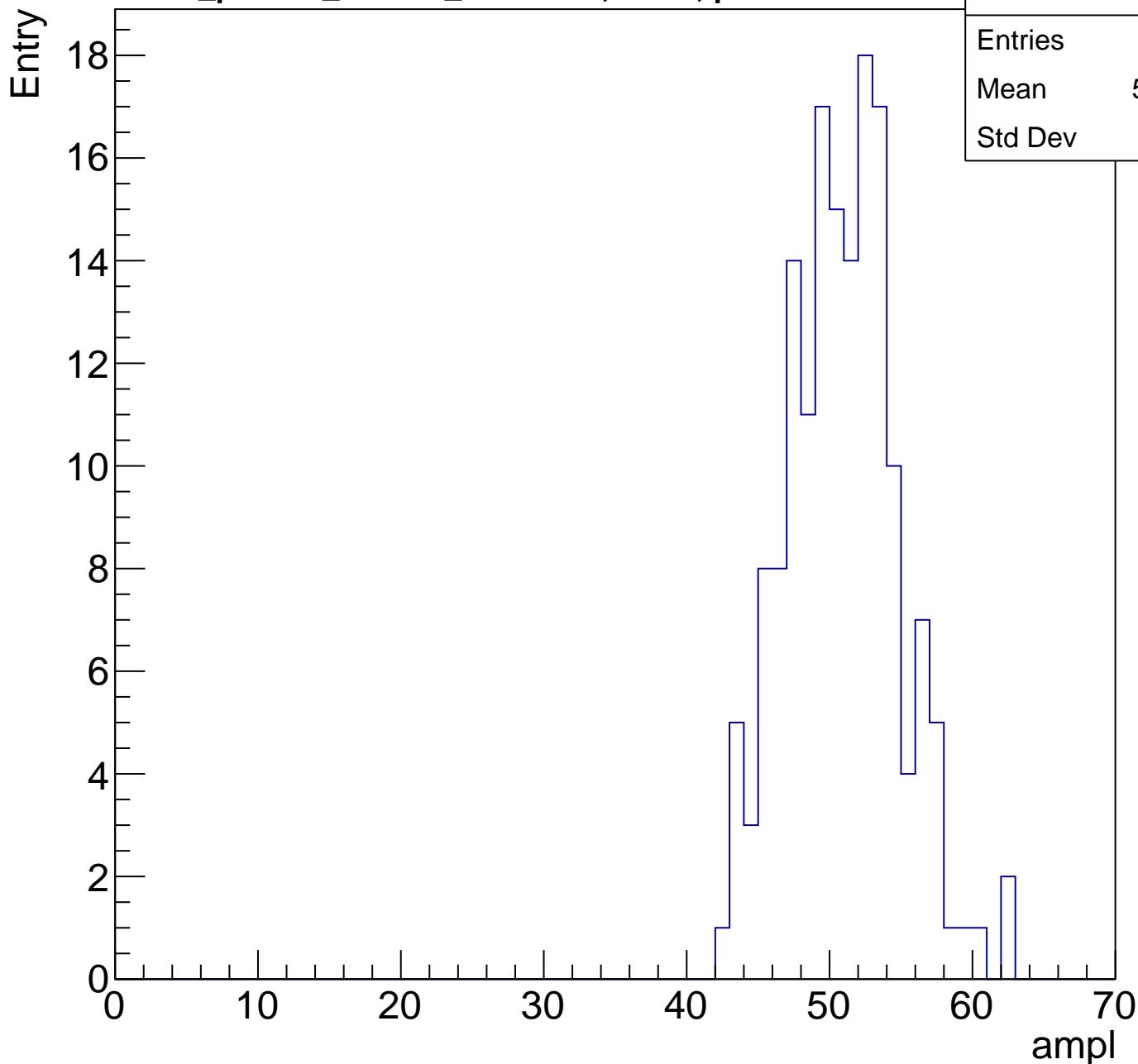
Entry



B0L101S, U19-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

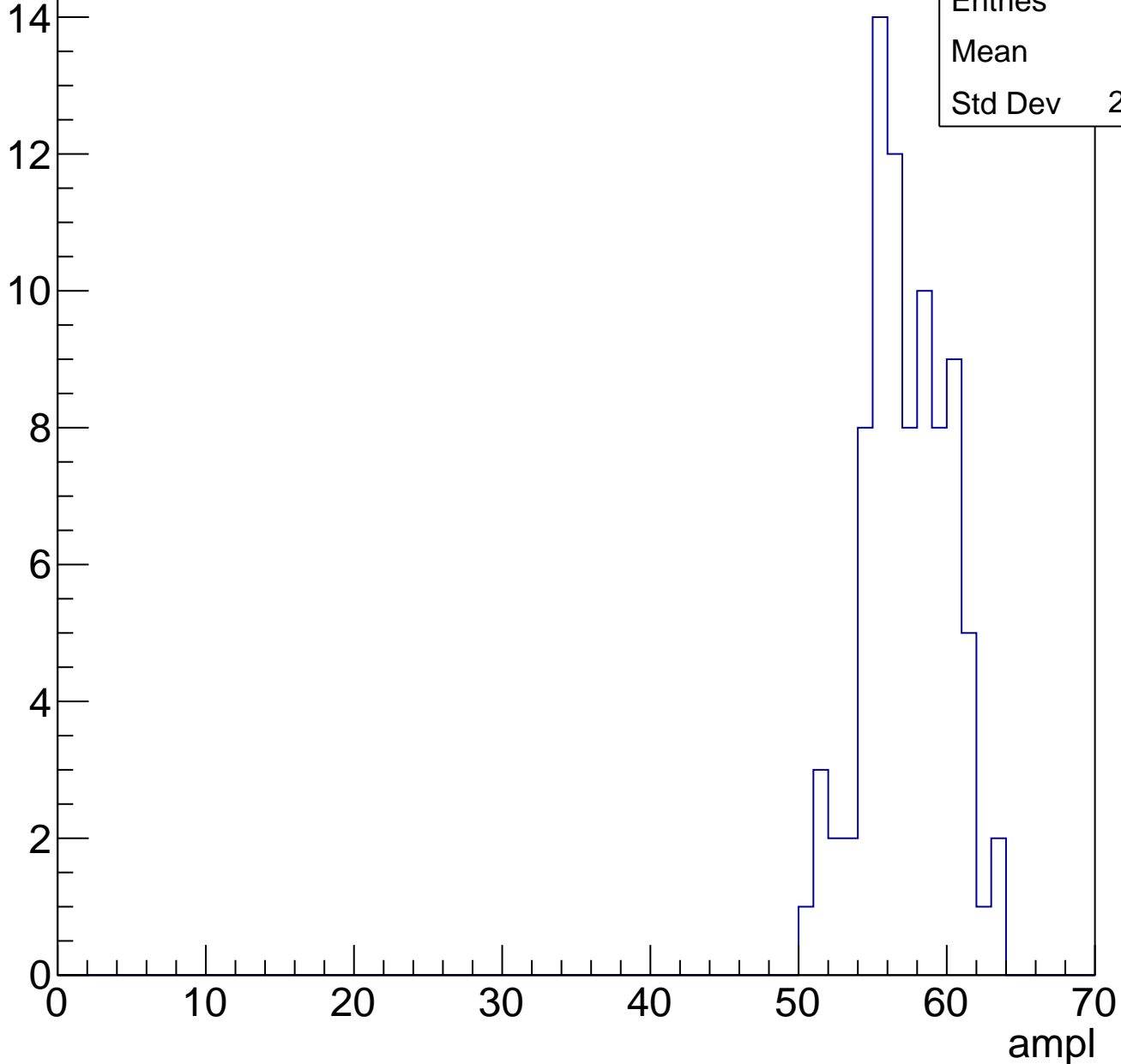
Entries	162
Mean	50.46
Std Dev	3.87



B0L101S, U19-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch18, adc5

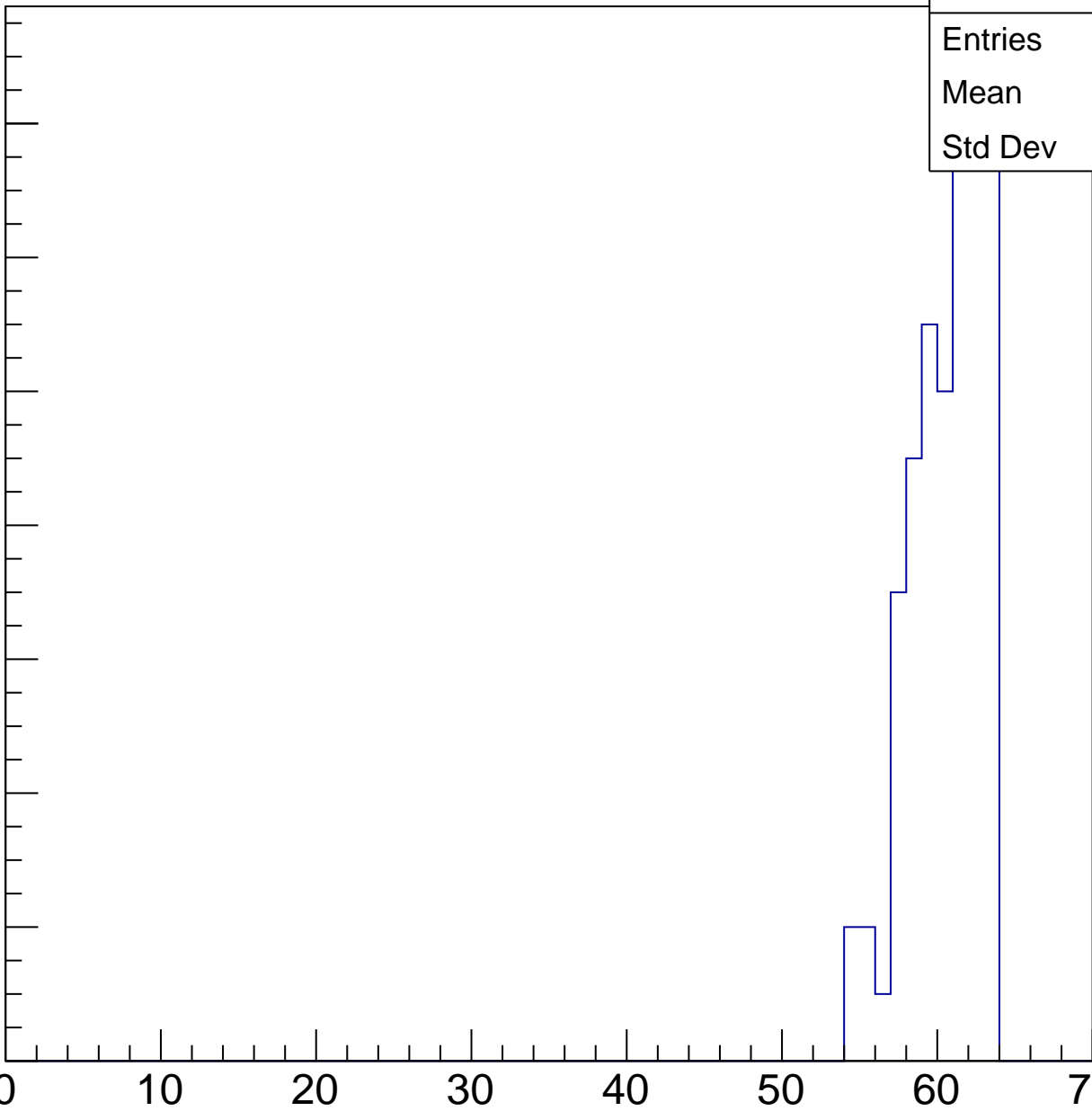
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	60.13
Std Dev	2.305

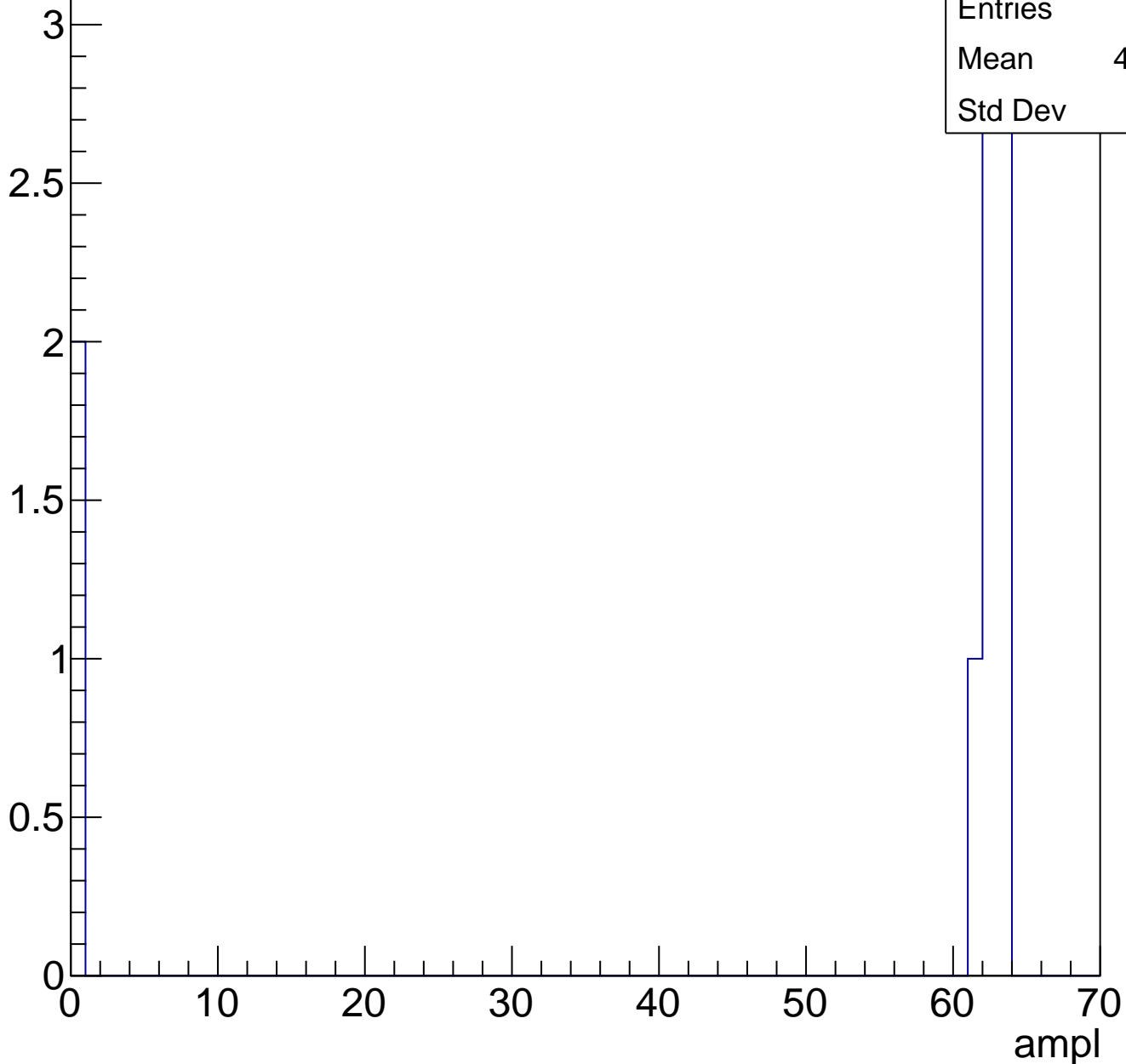
ampl



B0L101S, U19-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L101S, U19-ch19, adc0

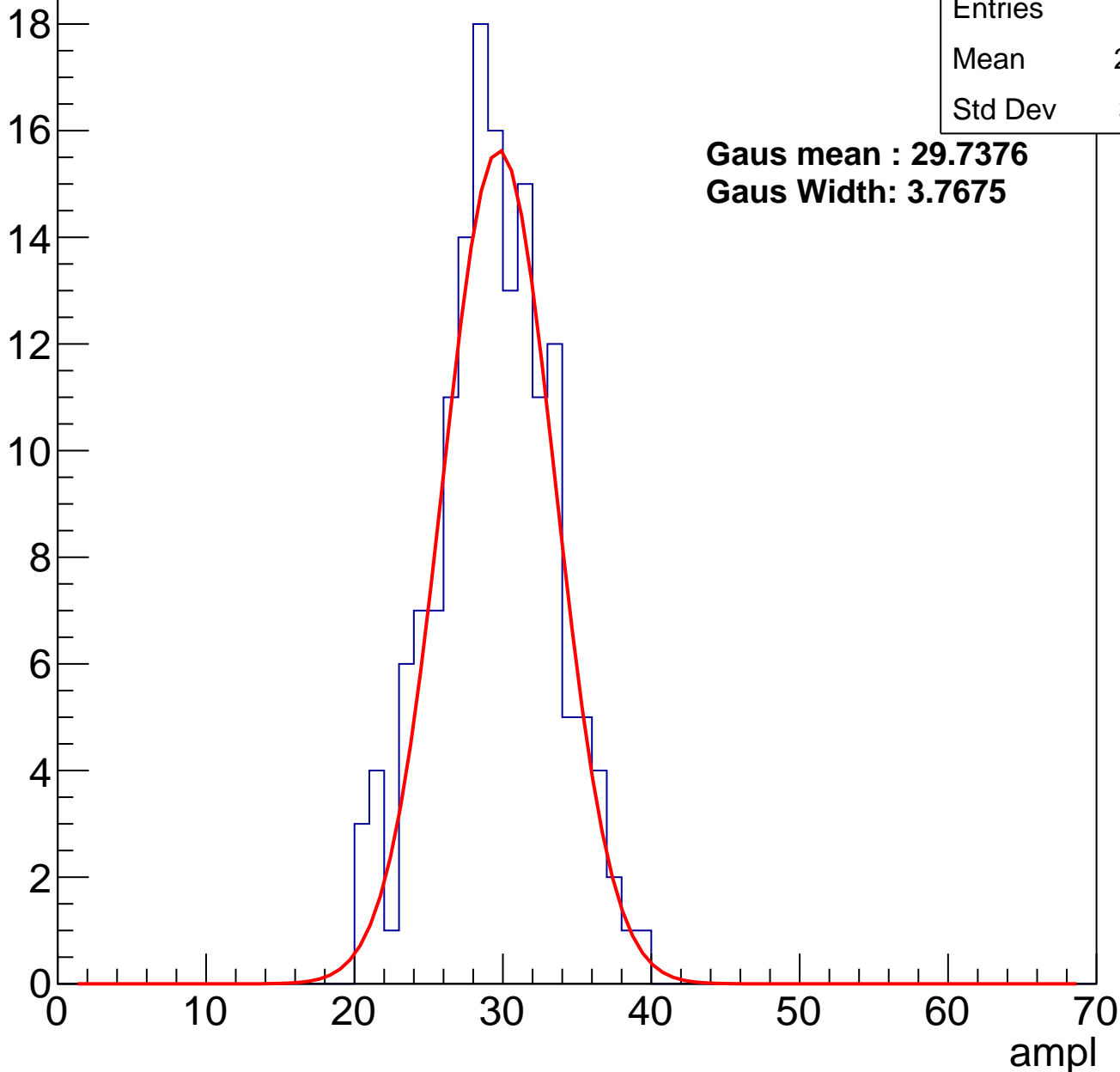
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	28.99
Std Dev	3.921

Gaus mean : 29.7376

Gaus Width: 3.7675

Entry



B0L101S, U19-ch19, adc1

calib_packv5_042523_0143.root, FC#1, port C1

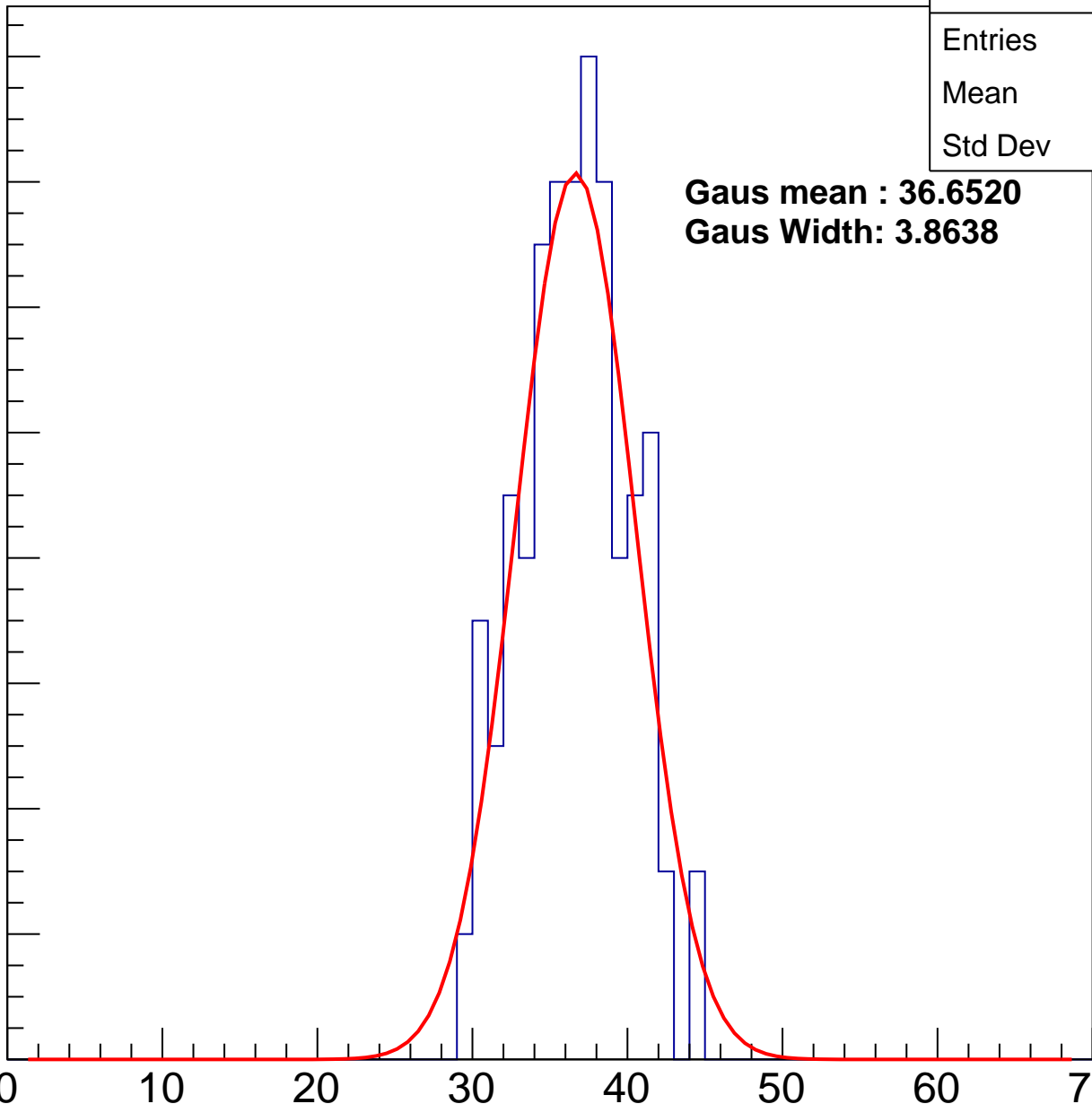
Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	36.11
Std Dev	3.435

Gaus mean : 36.6520
Gaus Width: 3.8638

ampl



B0L101S, U19-ch19, adc2

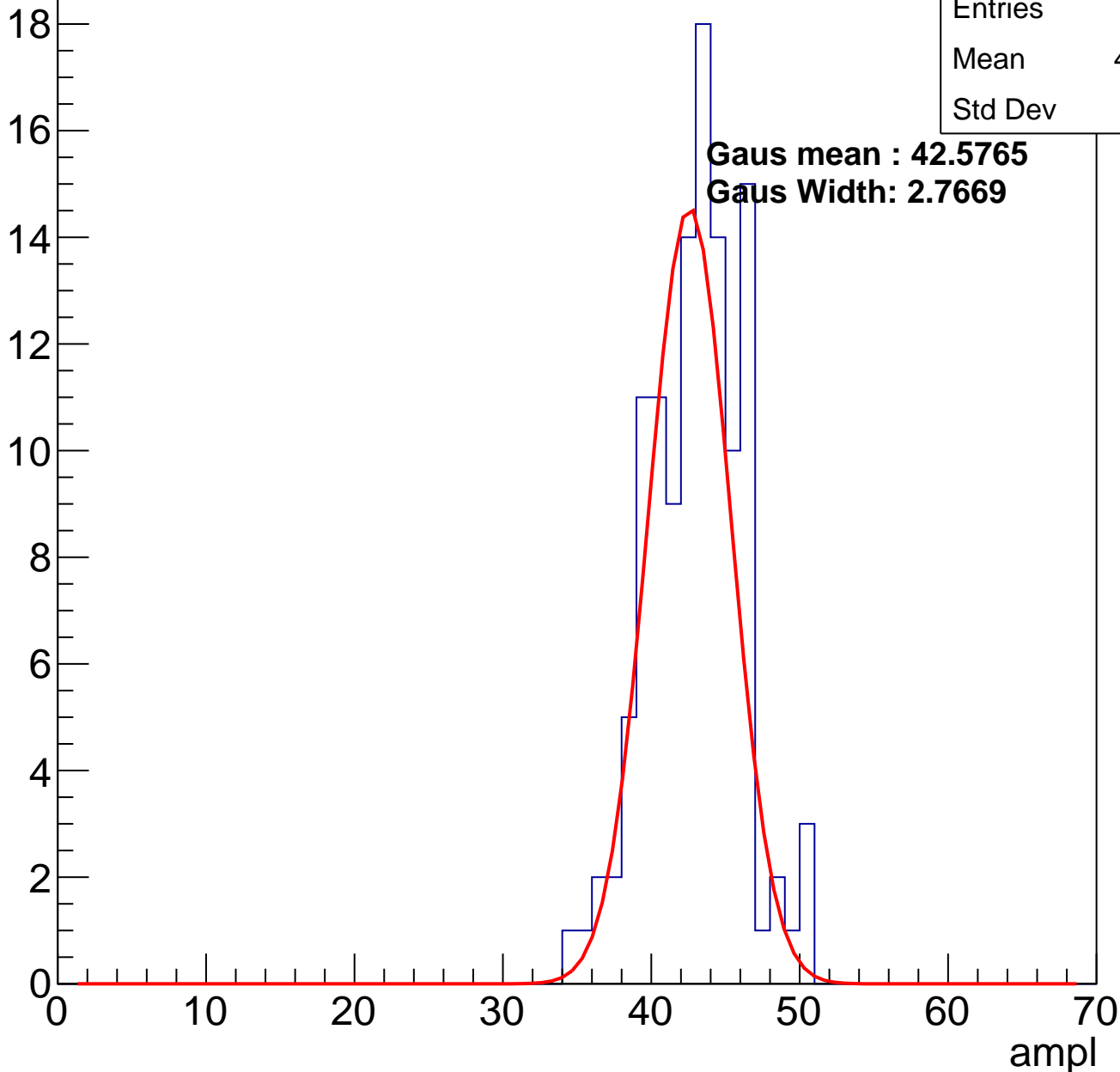
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	42.52
Std Dev	3.13

Gaus mean : 42.5765

Gaus Width: 2.7669

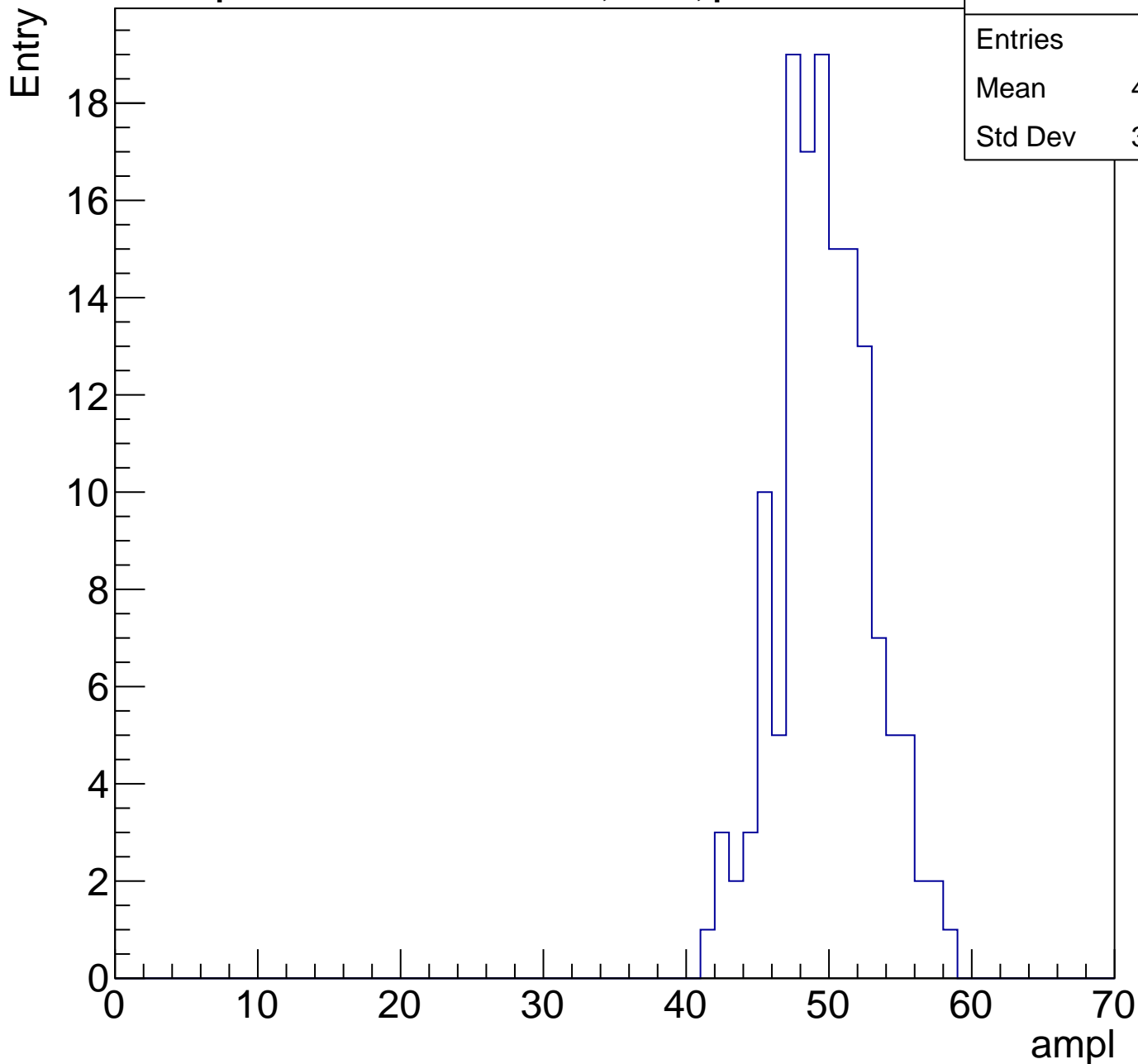
Entry



B0L101S, U19-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	49.28
Std Dev	3.292



B0L101S, U19-ch19, adc4

calib_packv5_042523_0143.root, FC#1, port C1

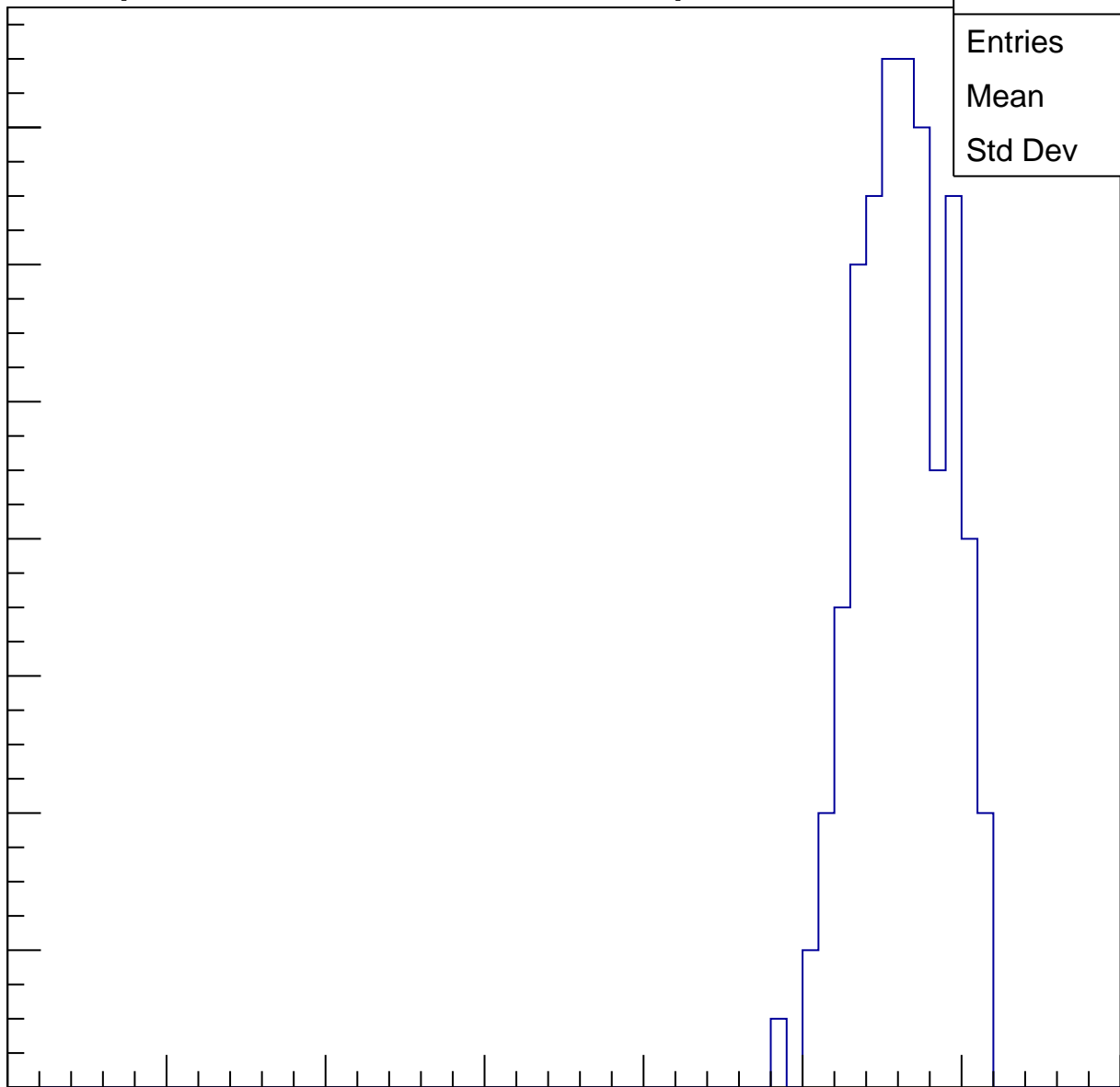
Entries	117
Mean	55.81
Std Dev	2.795

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

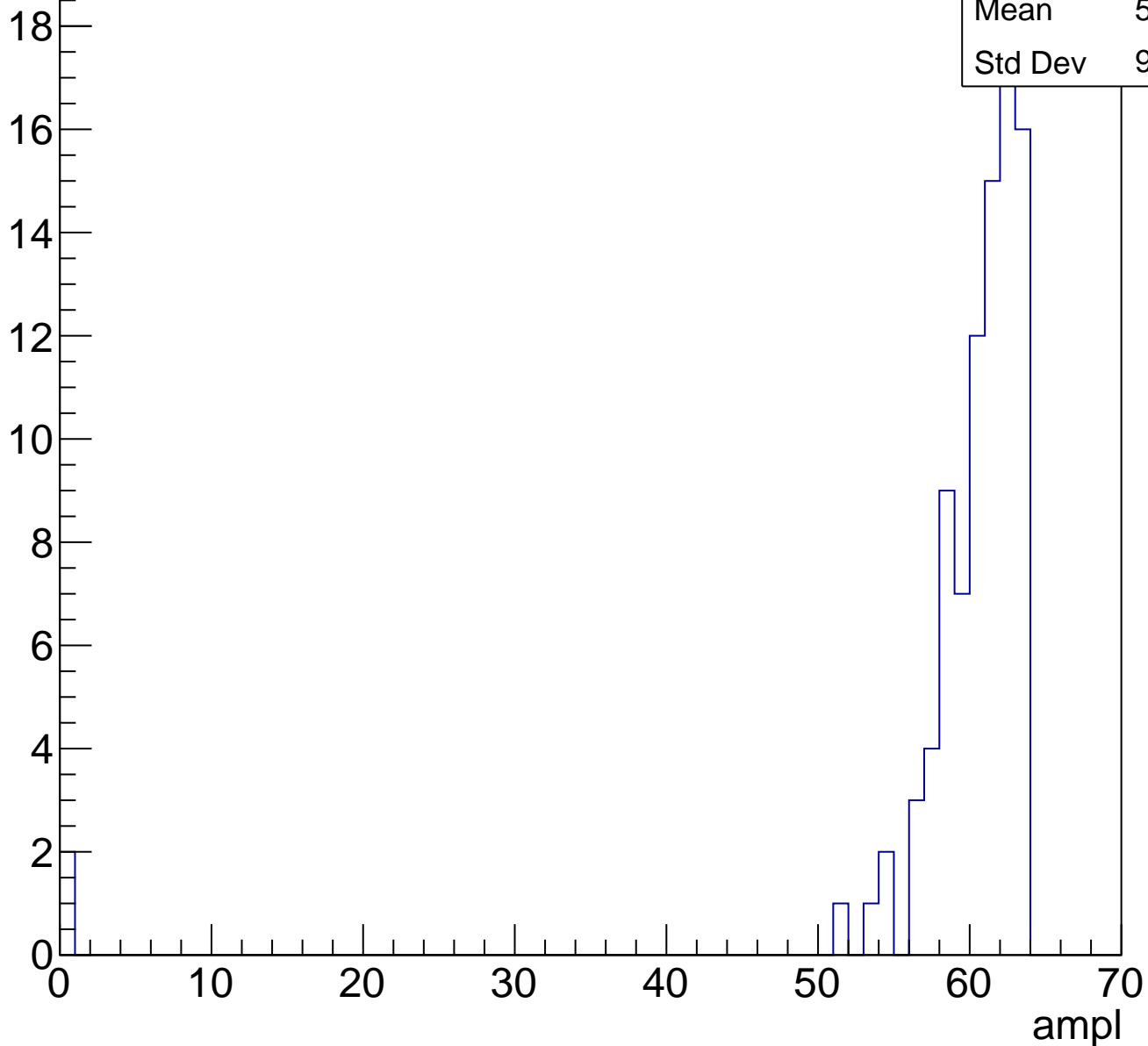


B0L101S, U19-ch19, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	91
Mean	58.95
Std Dev	9.176

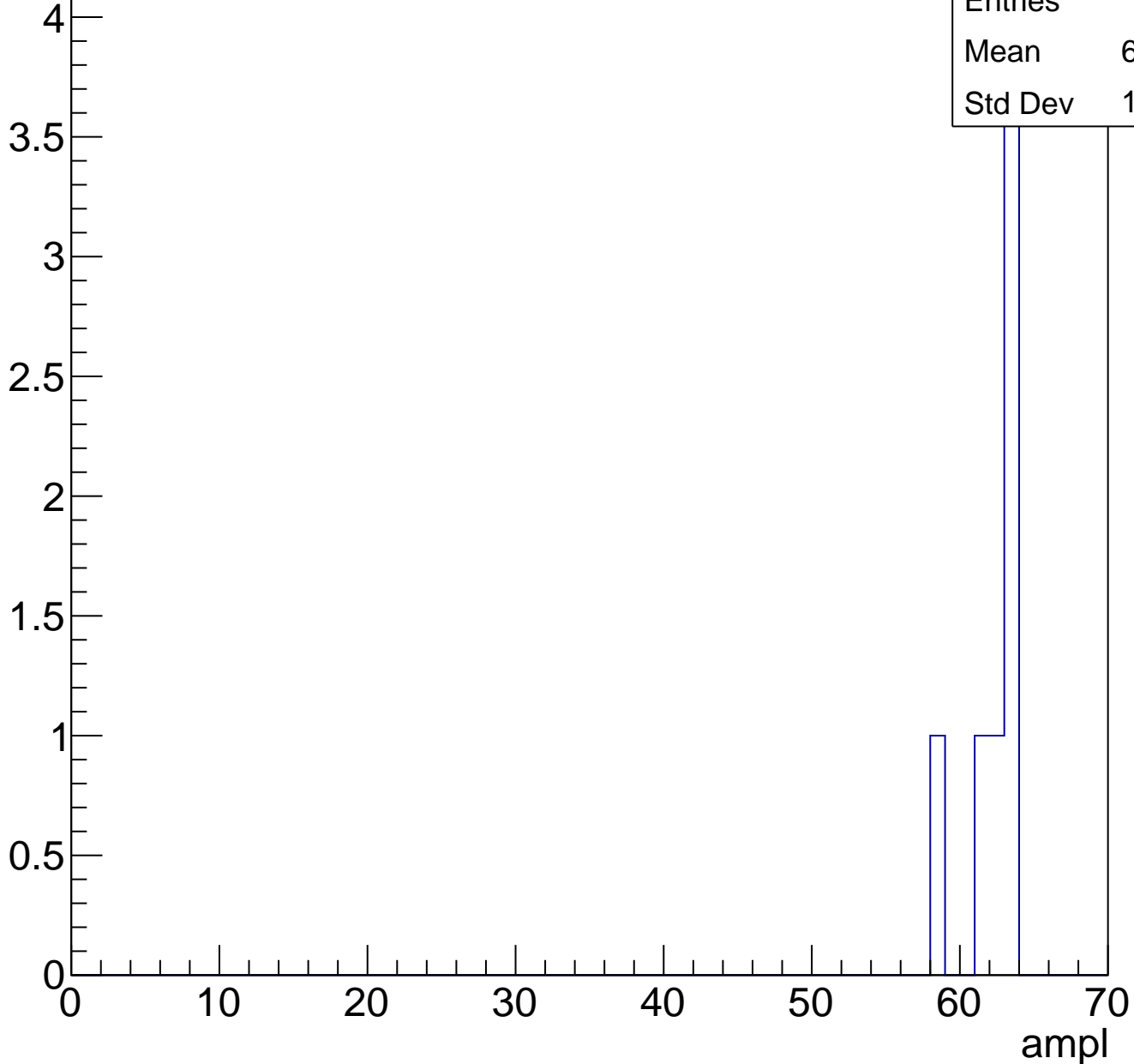
Entry



B0L101S, U19-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch20, adc0

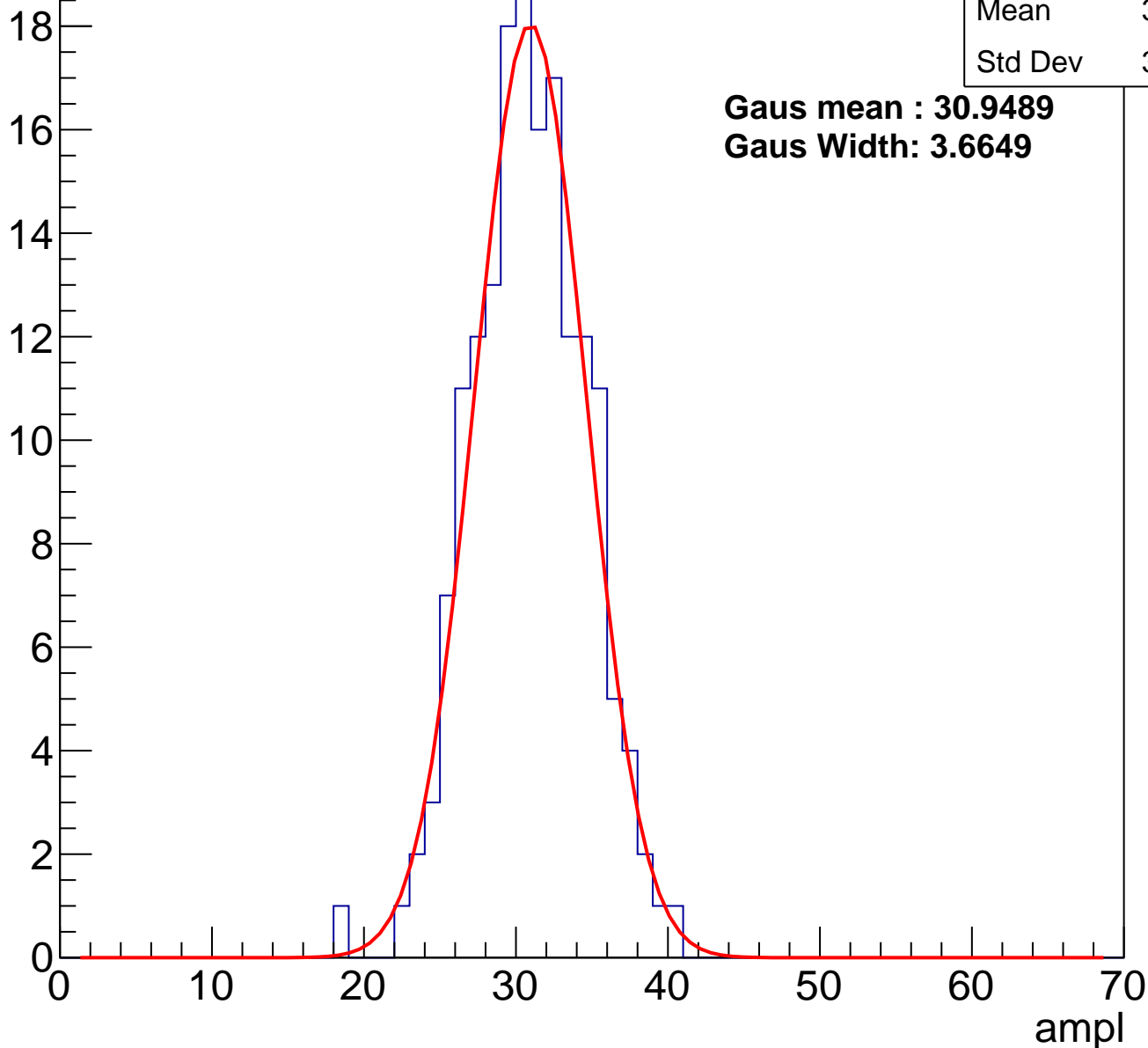
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	30.42
Std Dev	3.665

Gaus mean : 30.9489

Gaus Width: 3.6649

Entry



B0L101S, U19-ch20, adc1

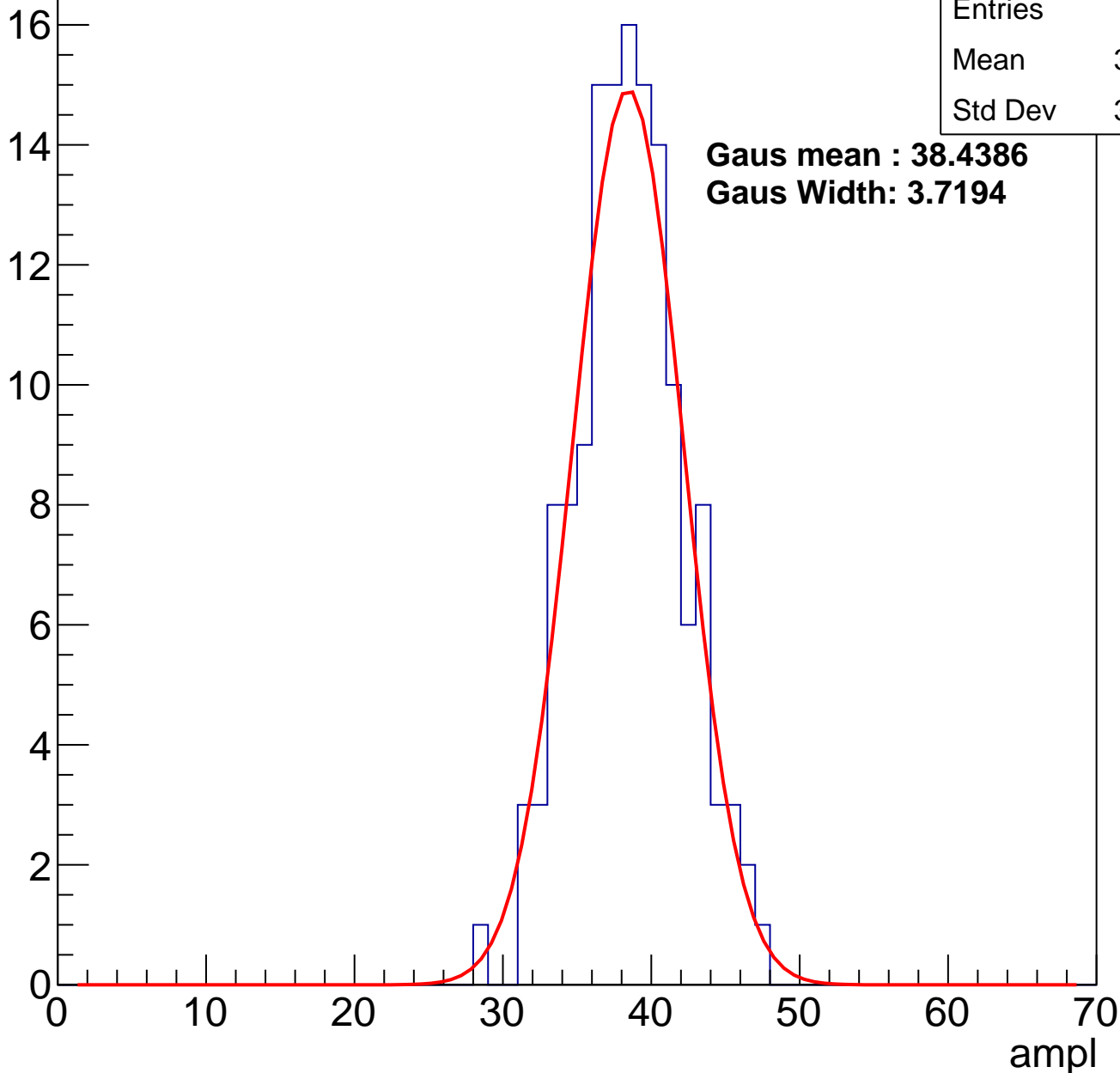
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	38.06
Std Dev	3.545

Gaus mean : 38.4386

Gaus Width: 3.7194

Entry



B0L101S, U19-ch20, adc2

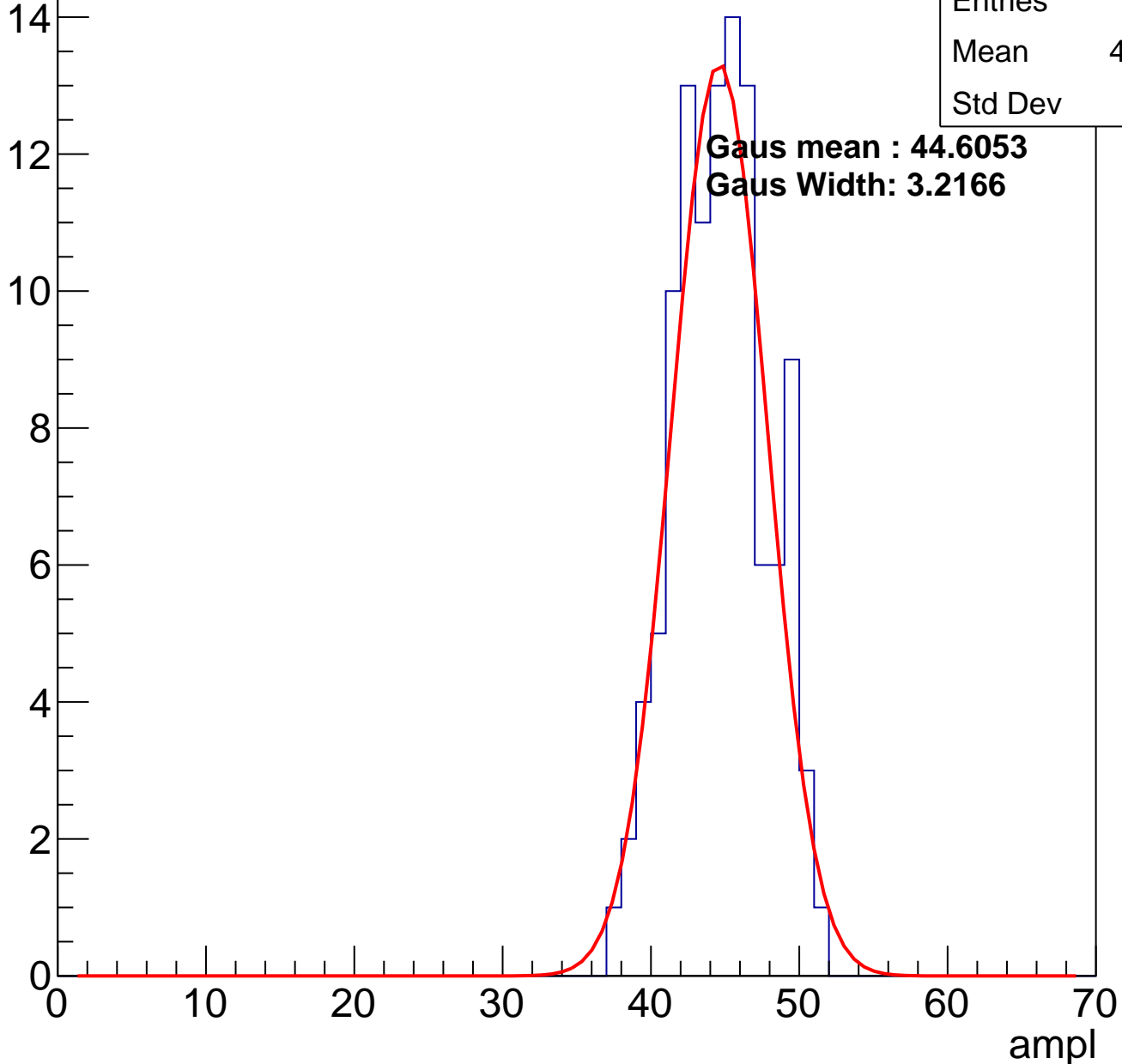
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	44.23
Std Dev	3.07

Gaus mean : 44.6053

Gaus Width: 3.2166

Entry



B0L101S, U19-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

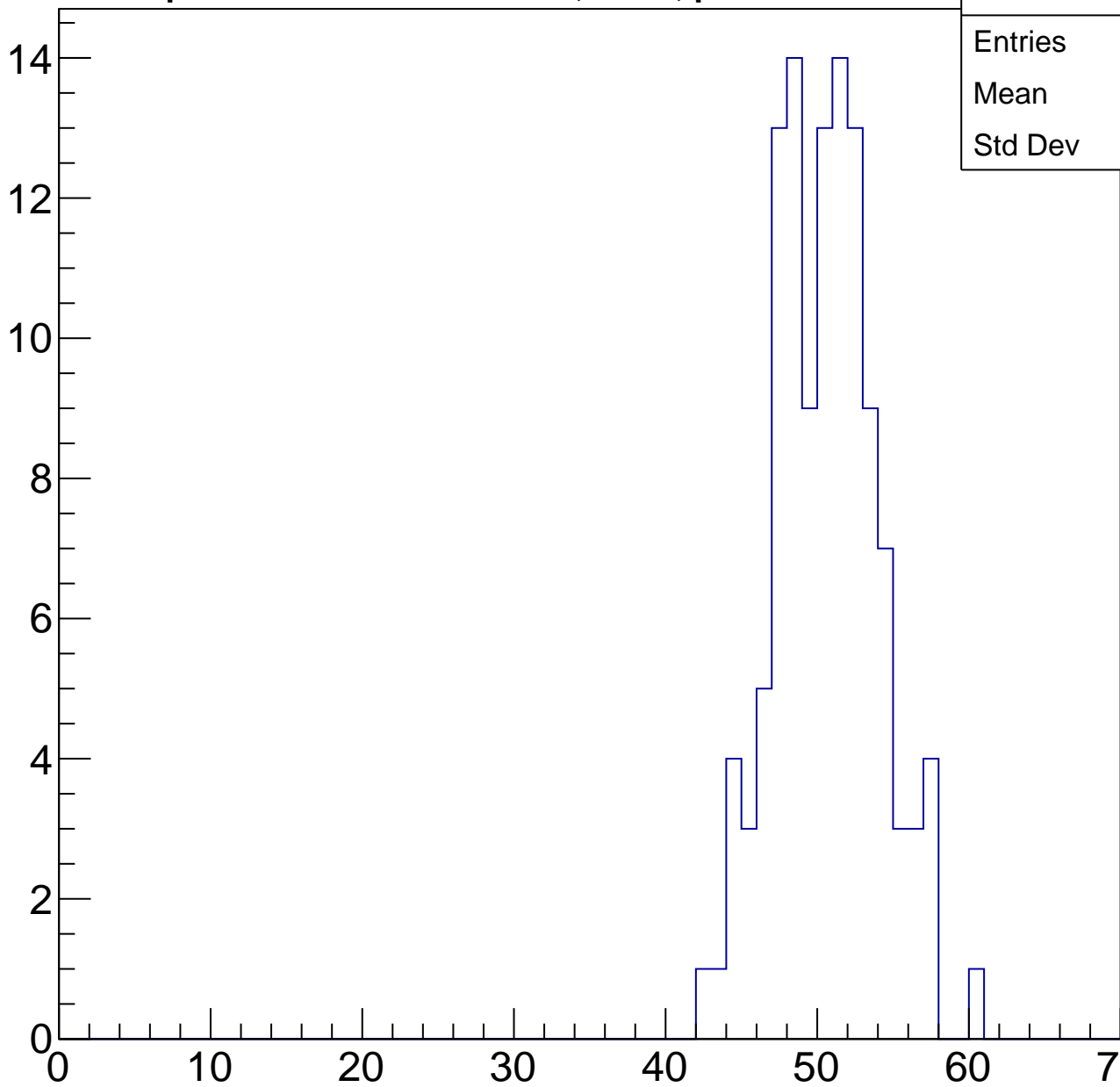
Entries	117
Mean	50.14
Std Dev	3.371

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

147

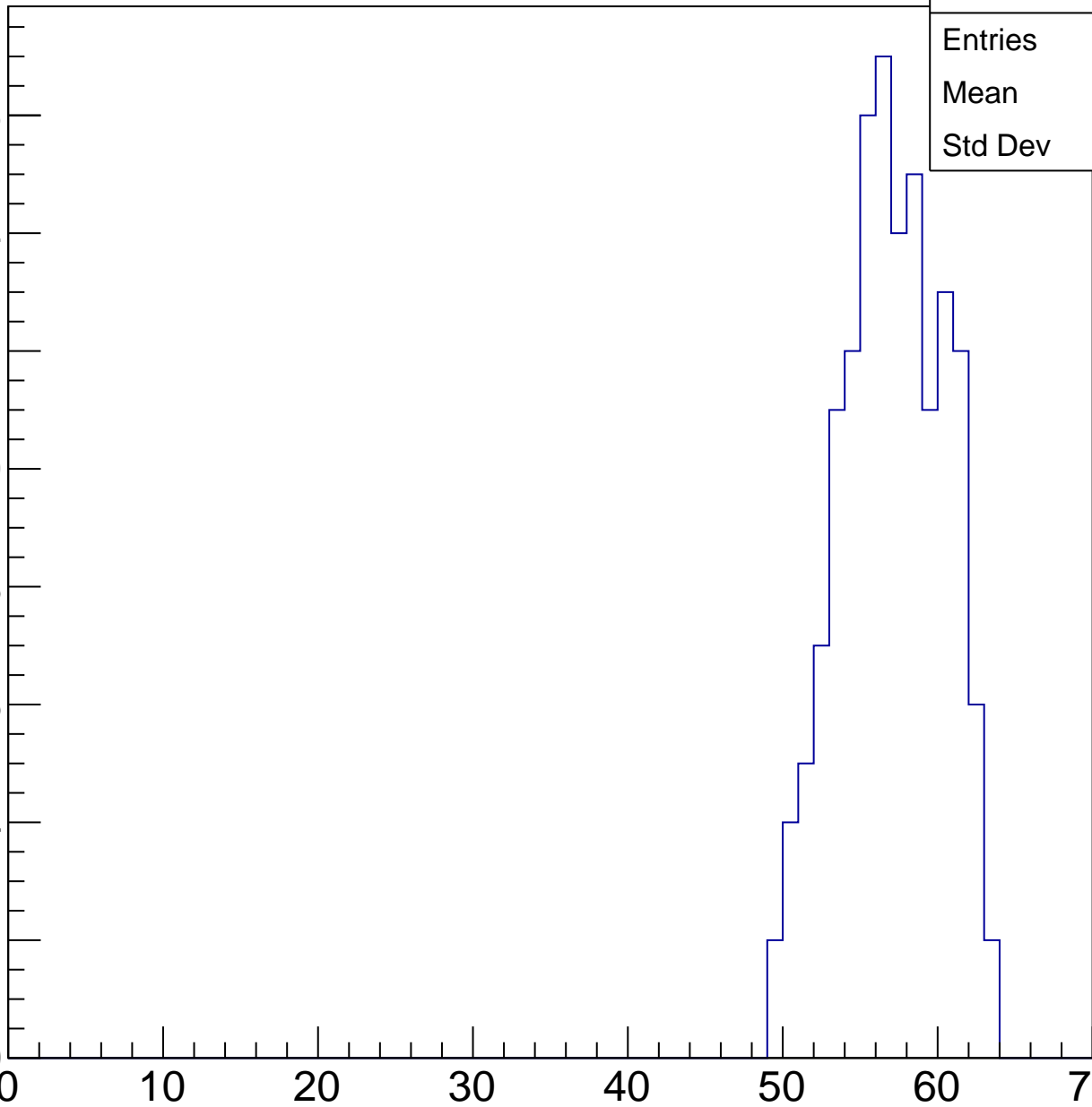
Mean

56.51

Std Dev

3.298

ampl



B0L101S, U19-ch20, adc5

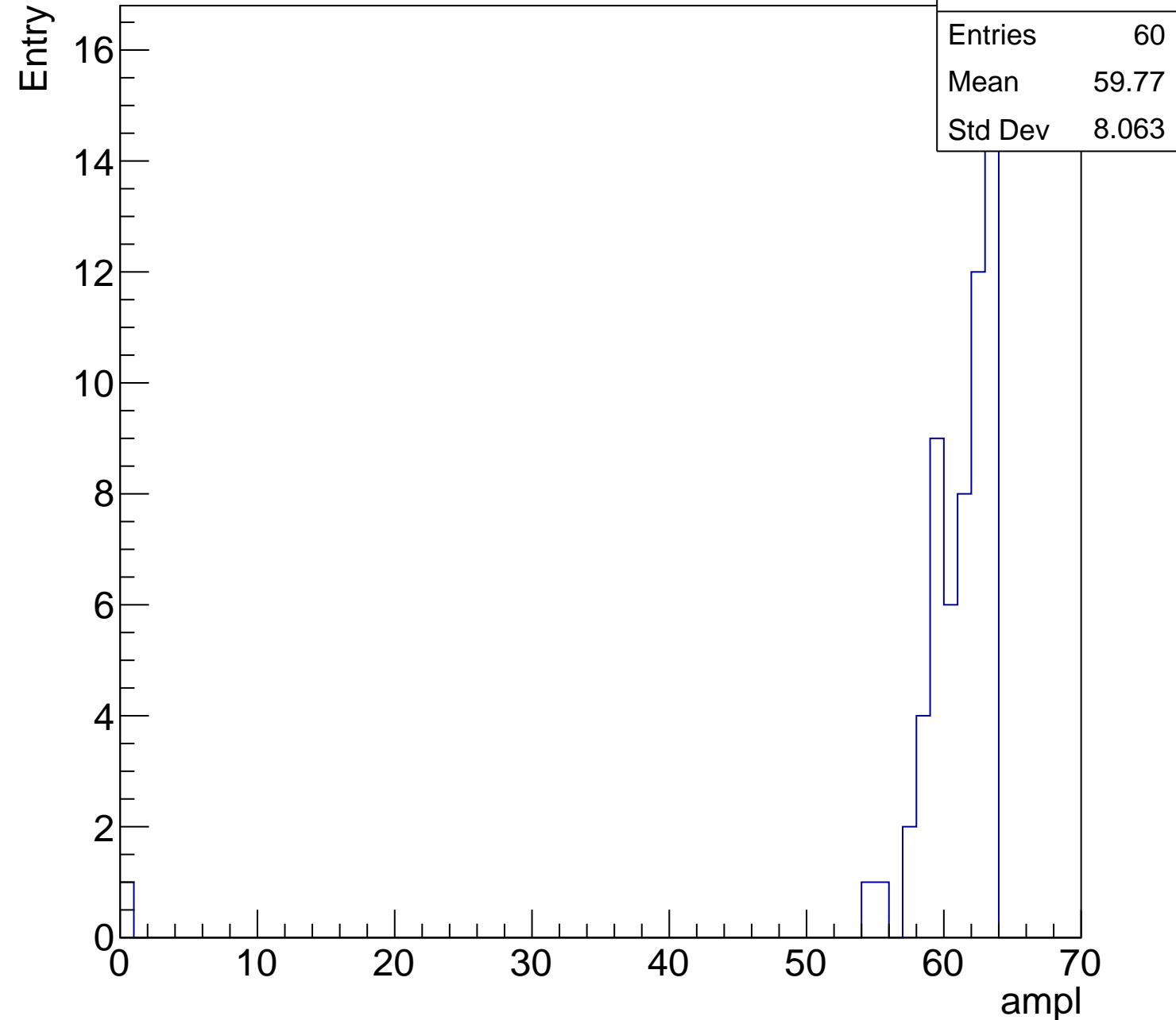
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	60
Mean	59.77
Std Dev	8.063

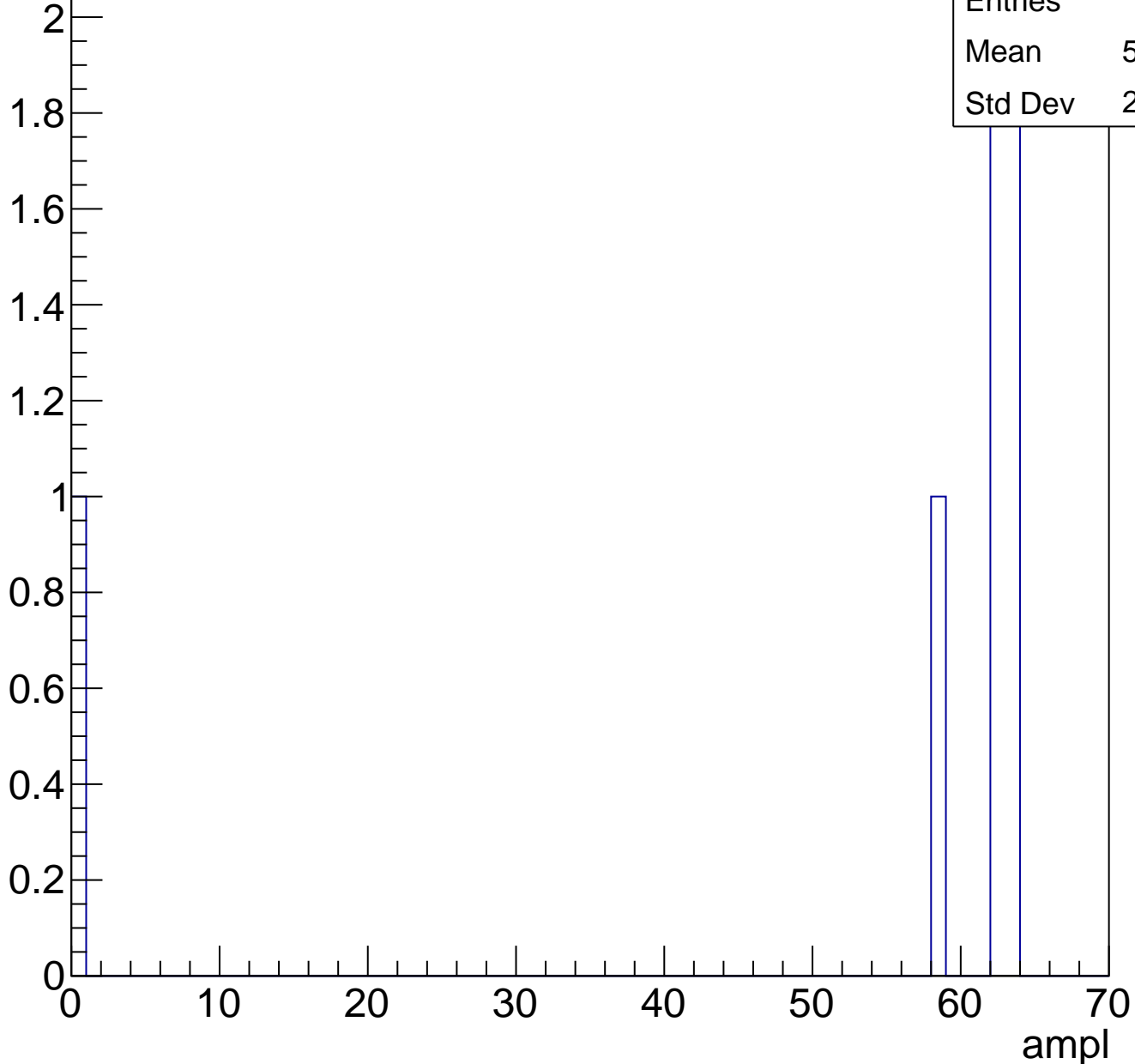
ampl



B0L101S, U19-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	6
Mean	51.33
Std Dev	23.02

B0L101S, U19-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch21, adc0

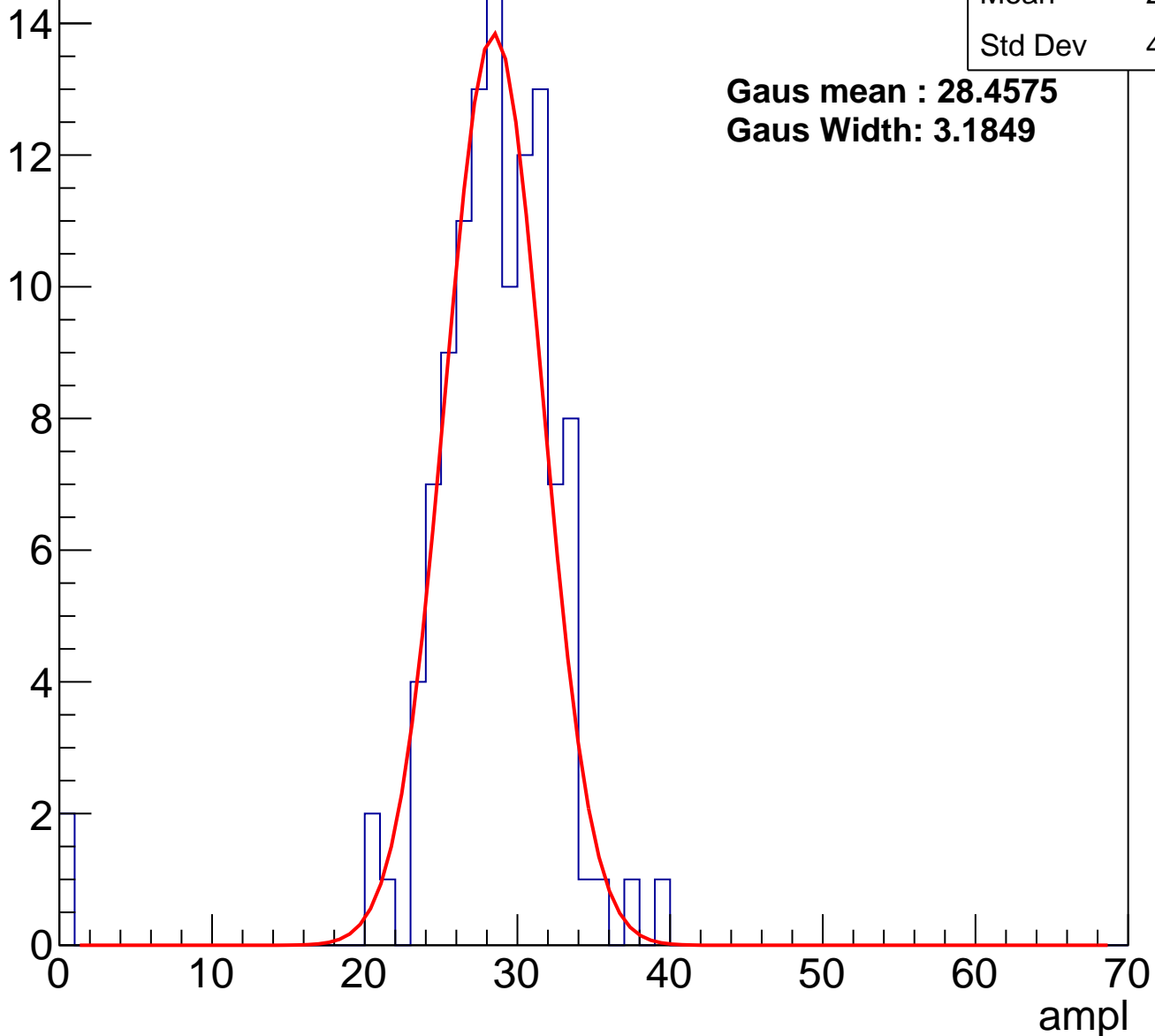
calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	27.87
Std Dev	4.929

Gaus mean : 28.4575

Gaus Width: 3.1849

Entry

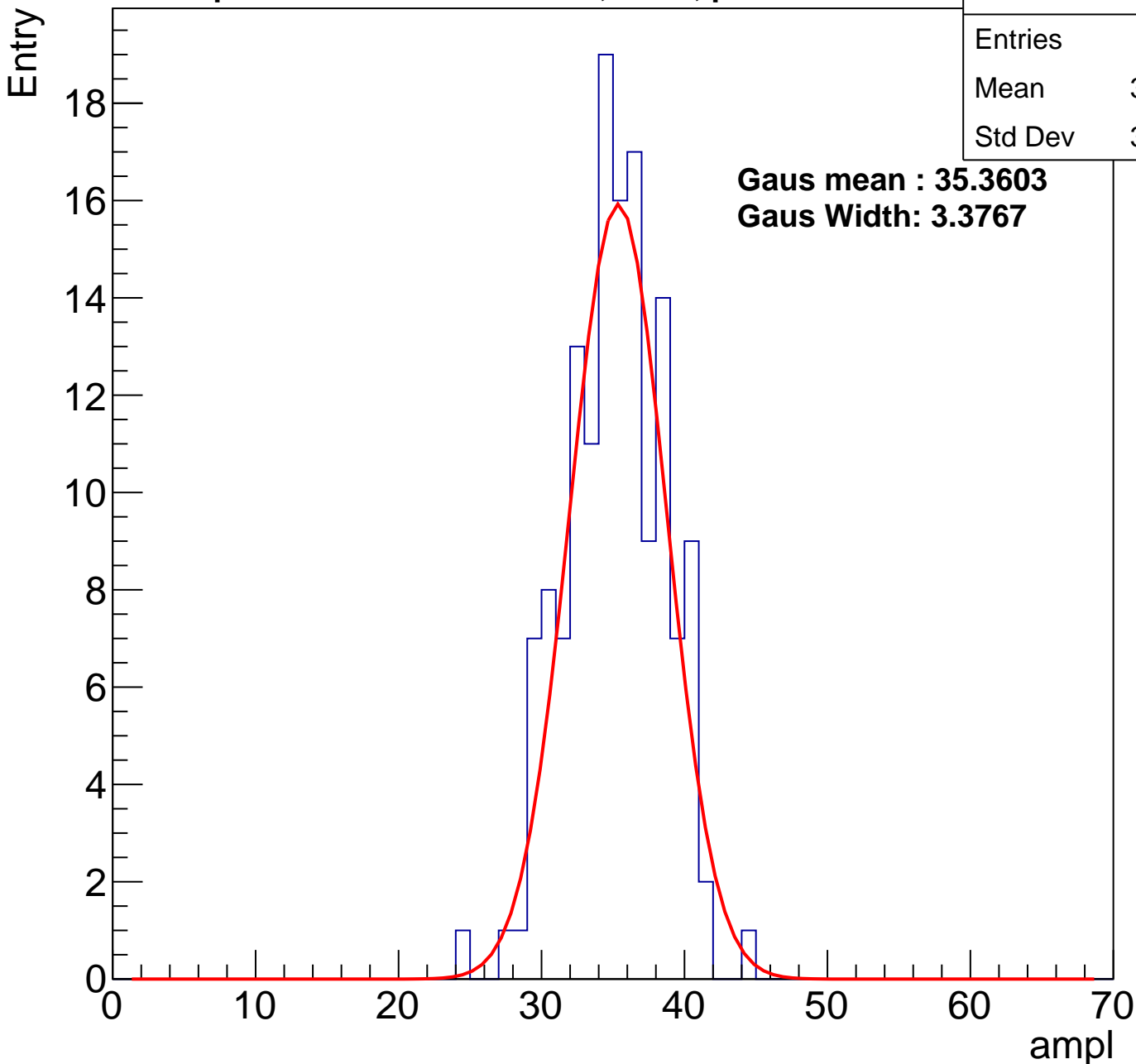


B0L101S, U19-ch21, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	34.69
Std Dev	3.379

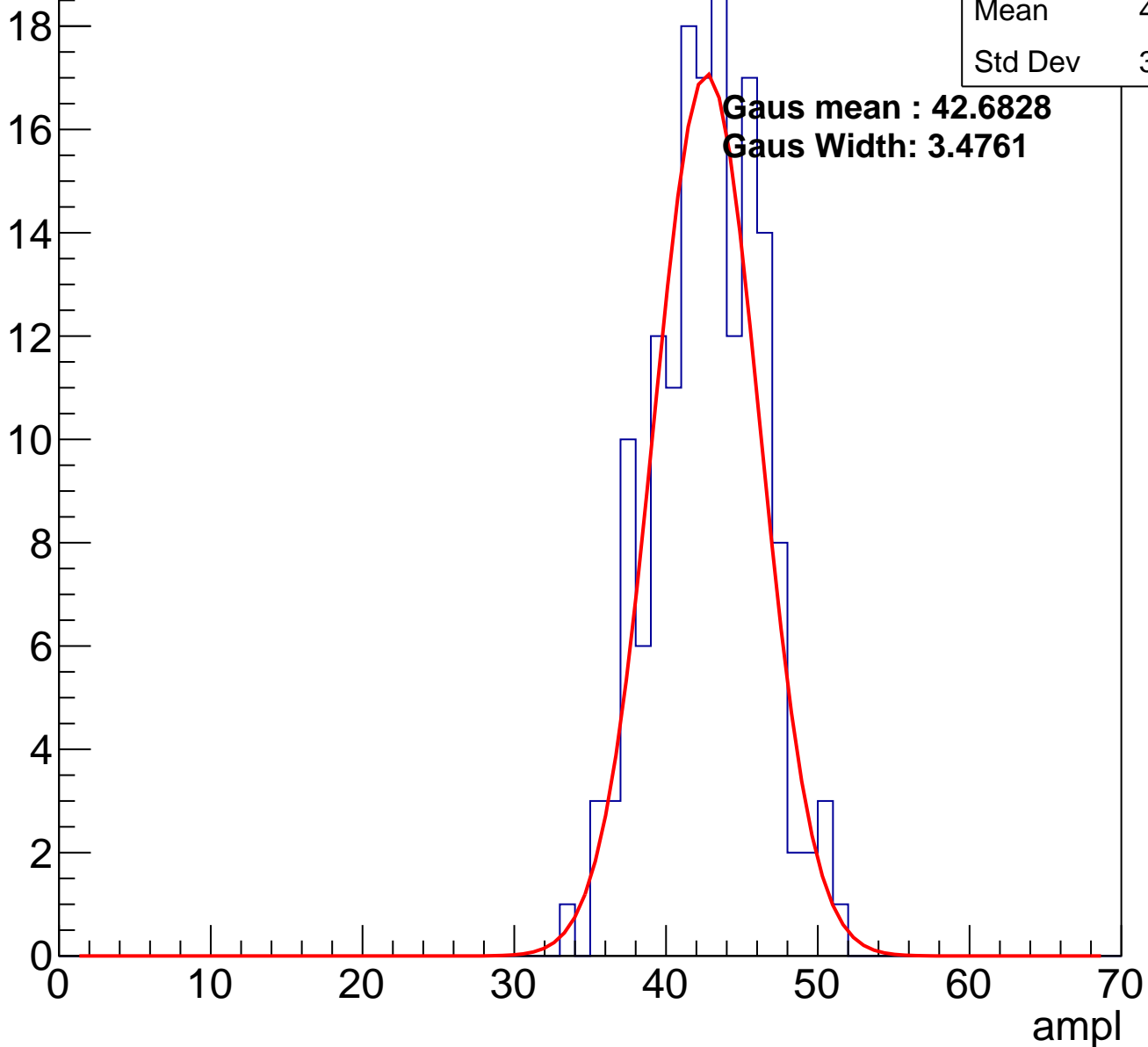
Gaus mean : 35.3603
Gaus Width: 3.3767



B0L101S, U19-ch21, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

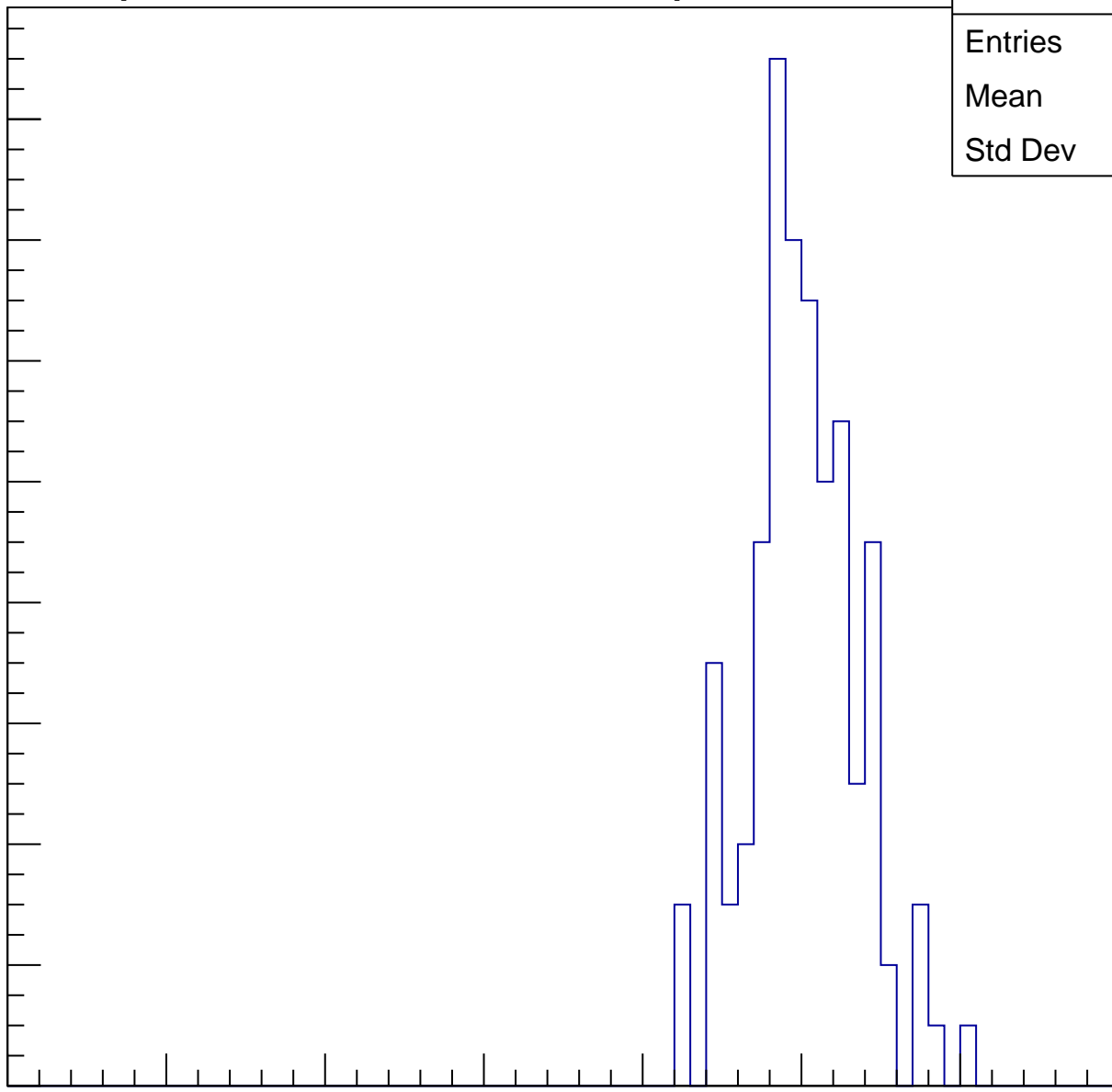
Entries	112
Mean	49.64
Std Dev	3.453

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

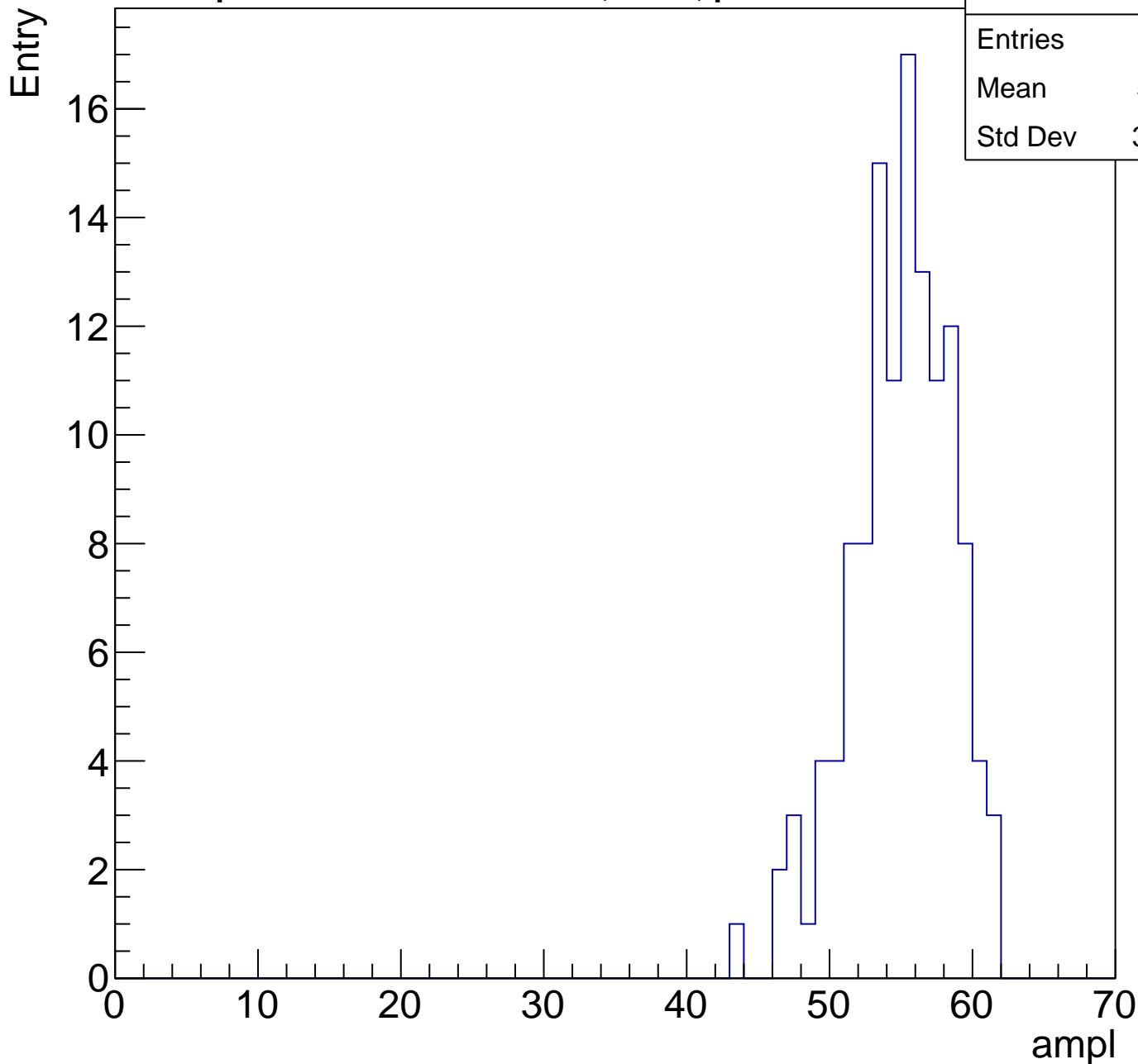
ampl



B0L101S, U19-ch21, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	54.51
Std Dev	3.502



B0L101S, U19-ch21, adc5

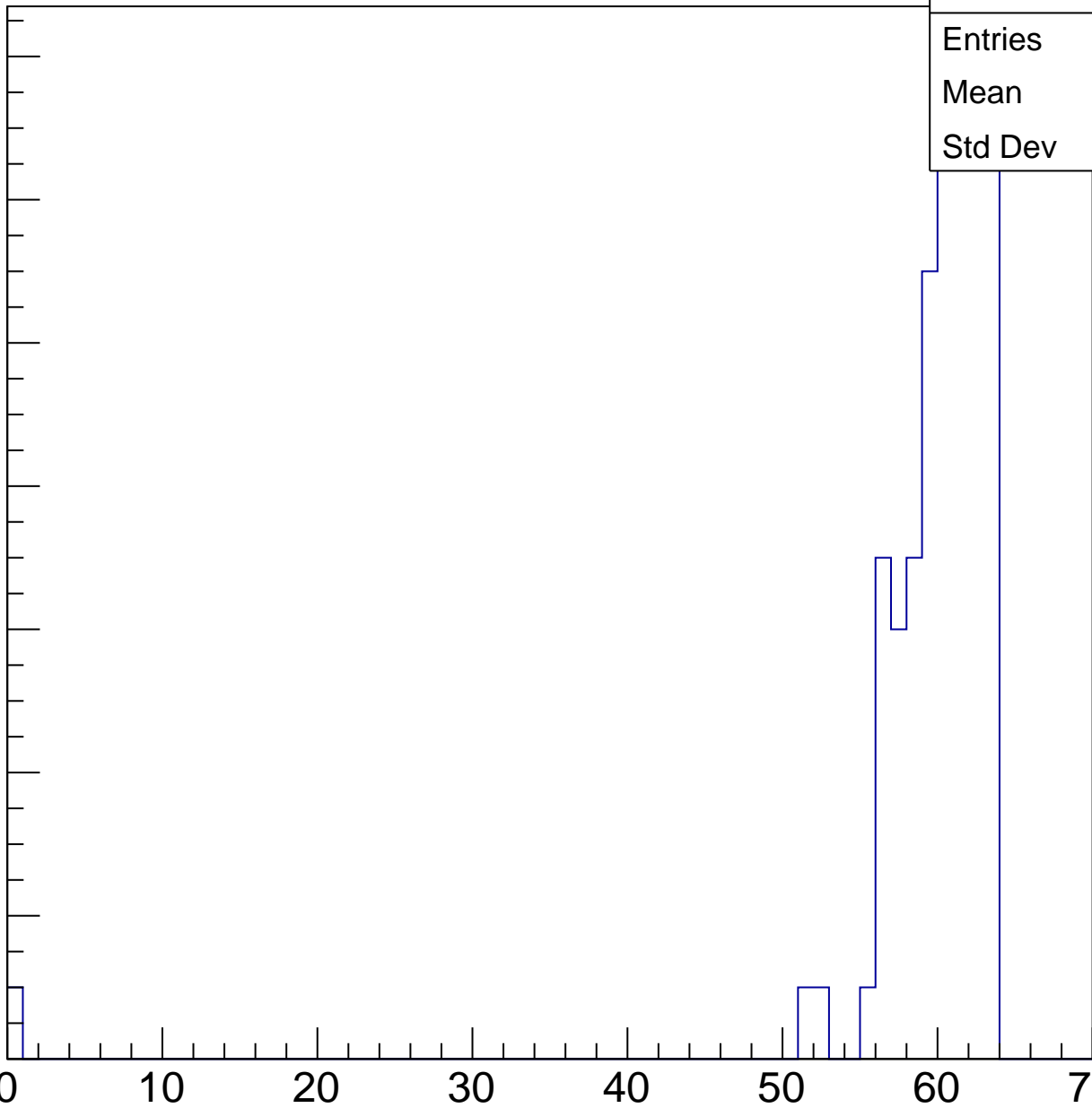
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	59.2
Std Dev	6.753

ampl

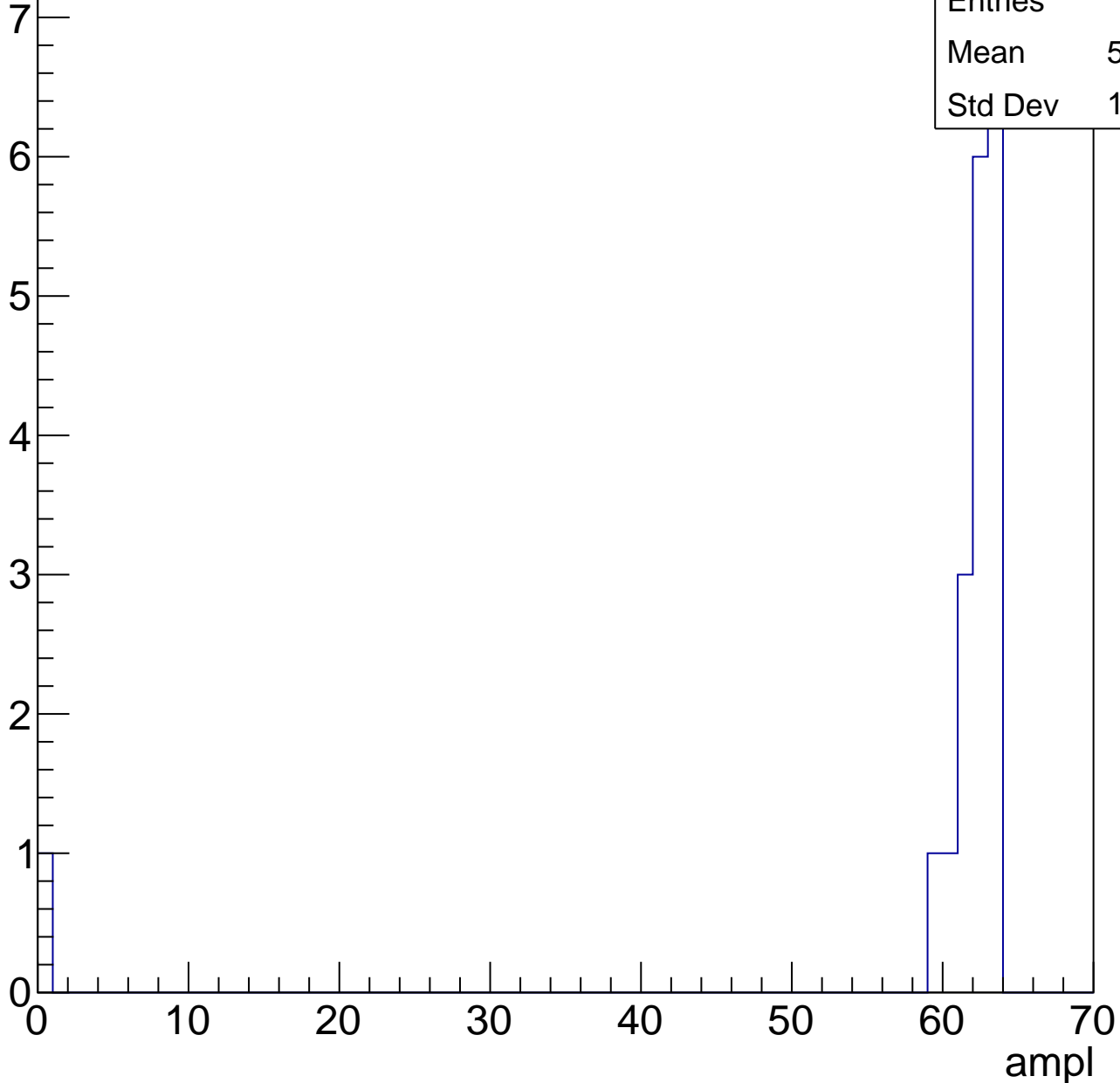


B0L101S, U19-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	58.68
Std Dev	13.88



B0L101S, U19-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch22, adc0

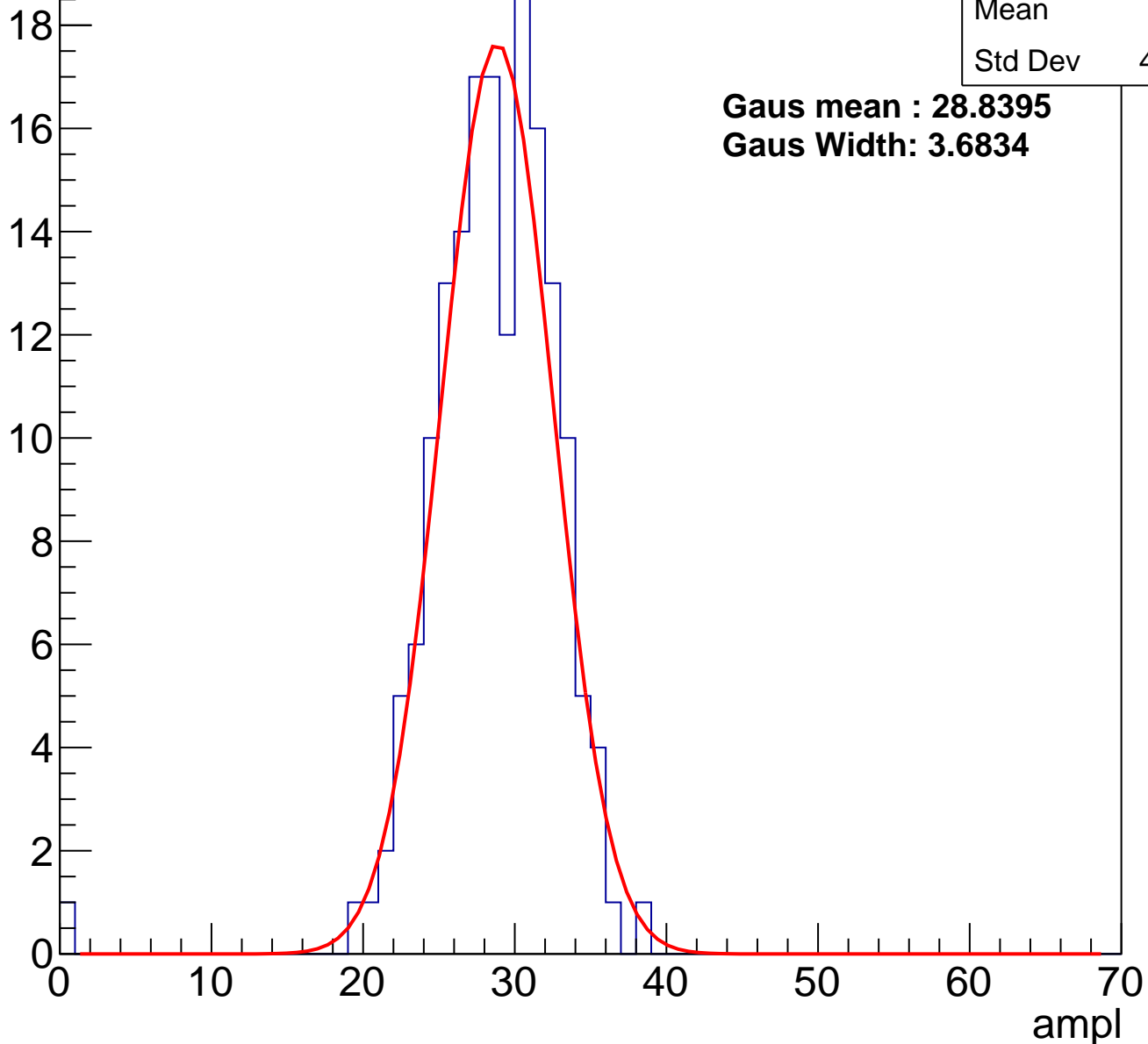
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	28.2
Std Dev	4.177

Gaus mean : 28.8395

Gaus Width: 3.6834

Entry



B0L101S, U19-ch22, adc1

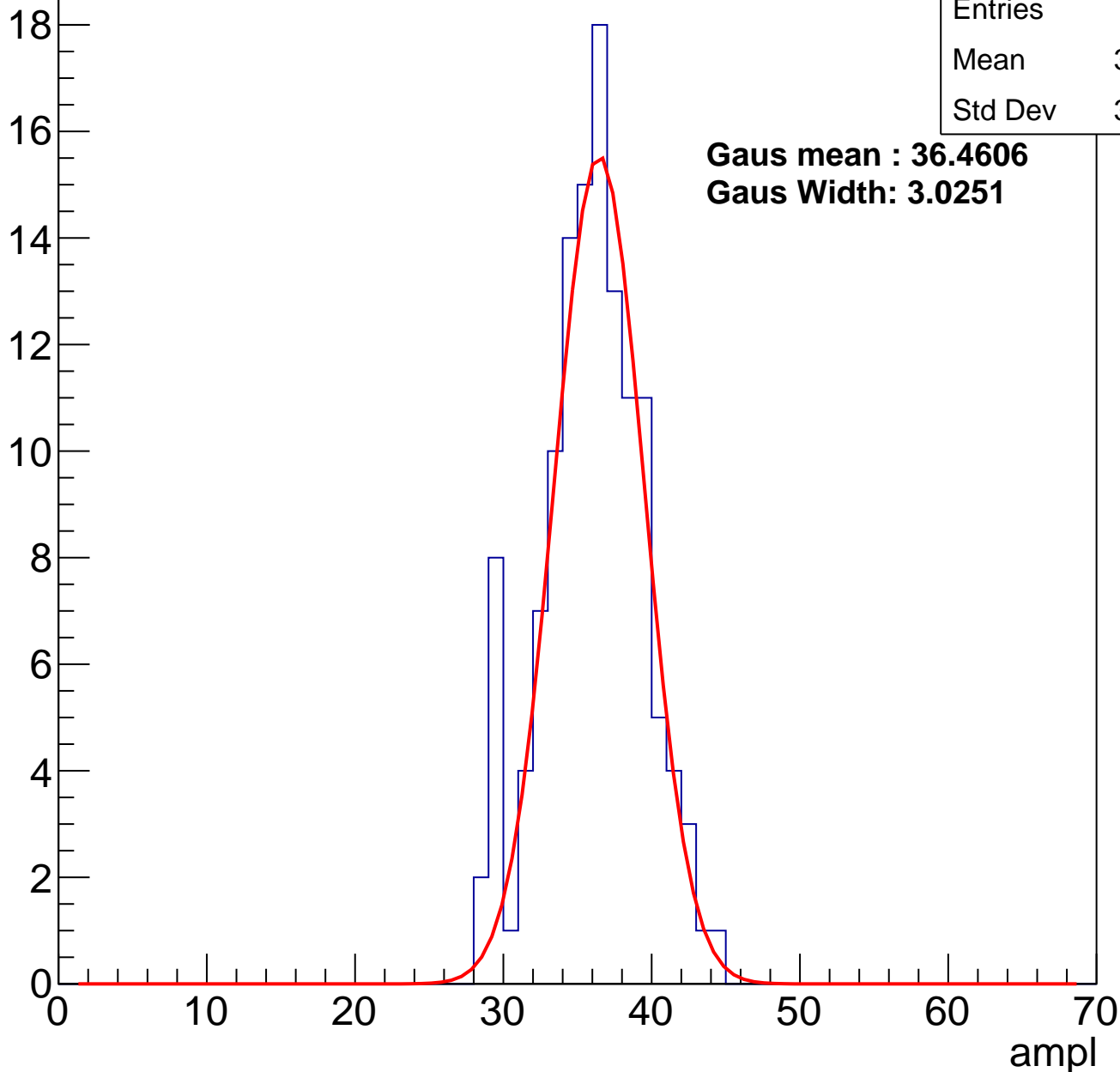
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	35.55
Std Dev	3.377

Gaus mean : 36.4606

Gaus Width: 3.0251

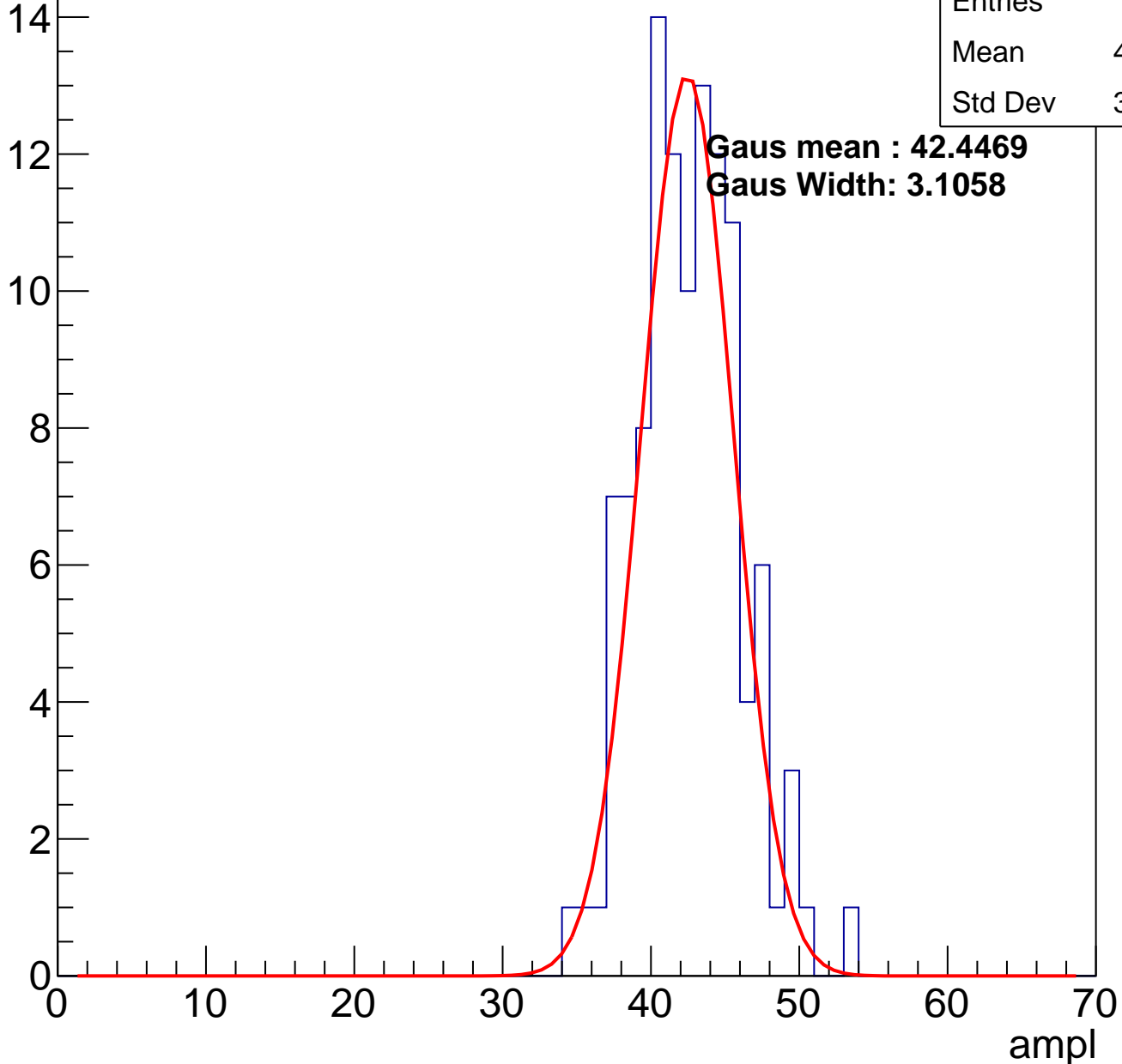
Entry



B0L101S, U19-ch22, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

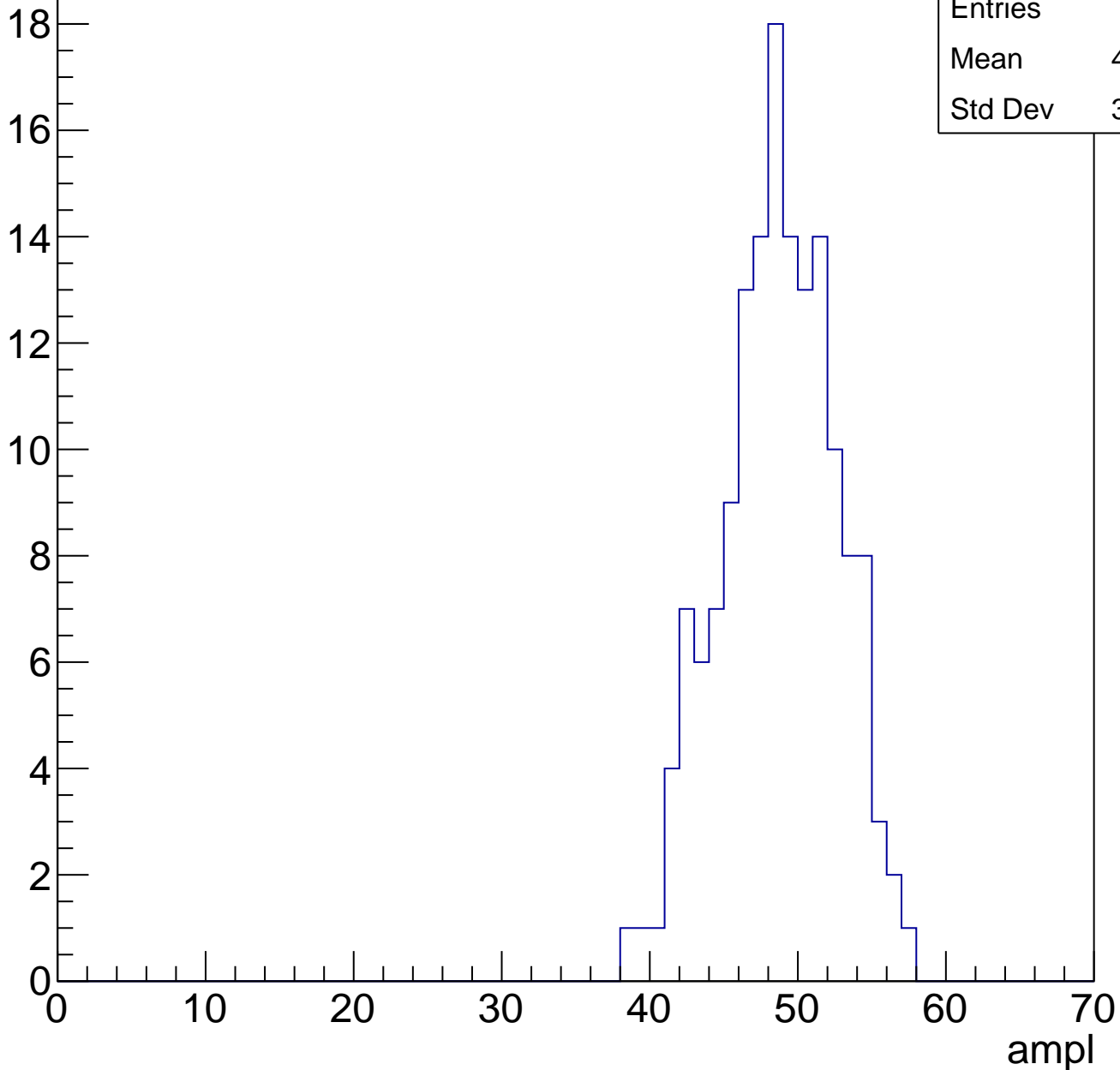


B0L101S, U19-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	48.22
Std Dev	3.857

Entry



B0L101S, U19-ch22, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	54.88
Std Dev	3.482

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

3

1

3

6

10

11

14

12

10

9

4

2

3

B0L101S, U19-ch22, adc5

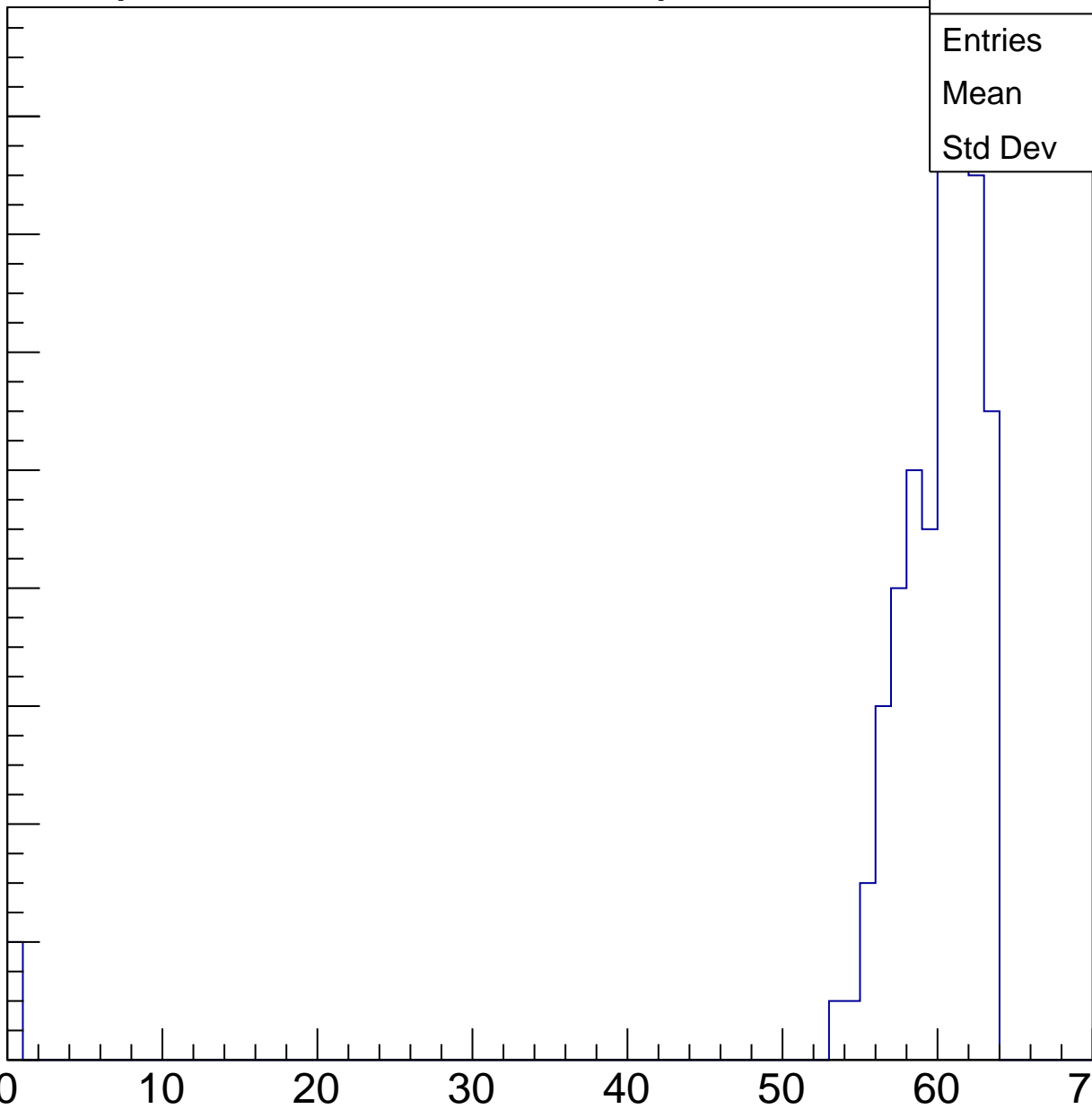
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	58.54
Std Dev	8.727

ampl



B0L101S, U19-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

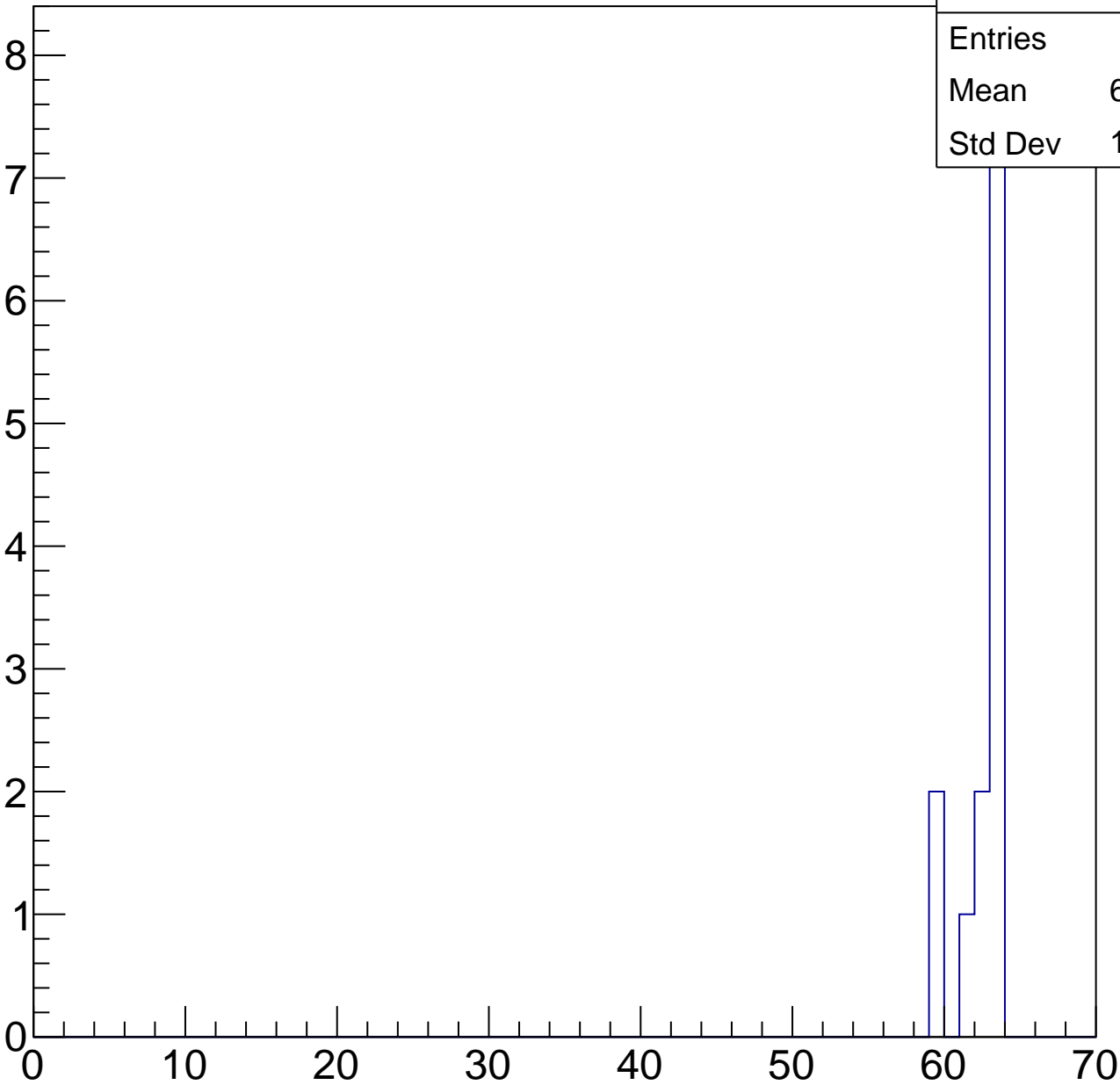
Entry

Entries	13
Mean	62.08
Std Dev	1.439

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch23, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	31.73
Std Dev	3.164

Gaus mean : 32.1481

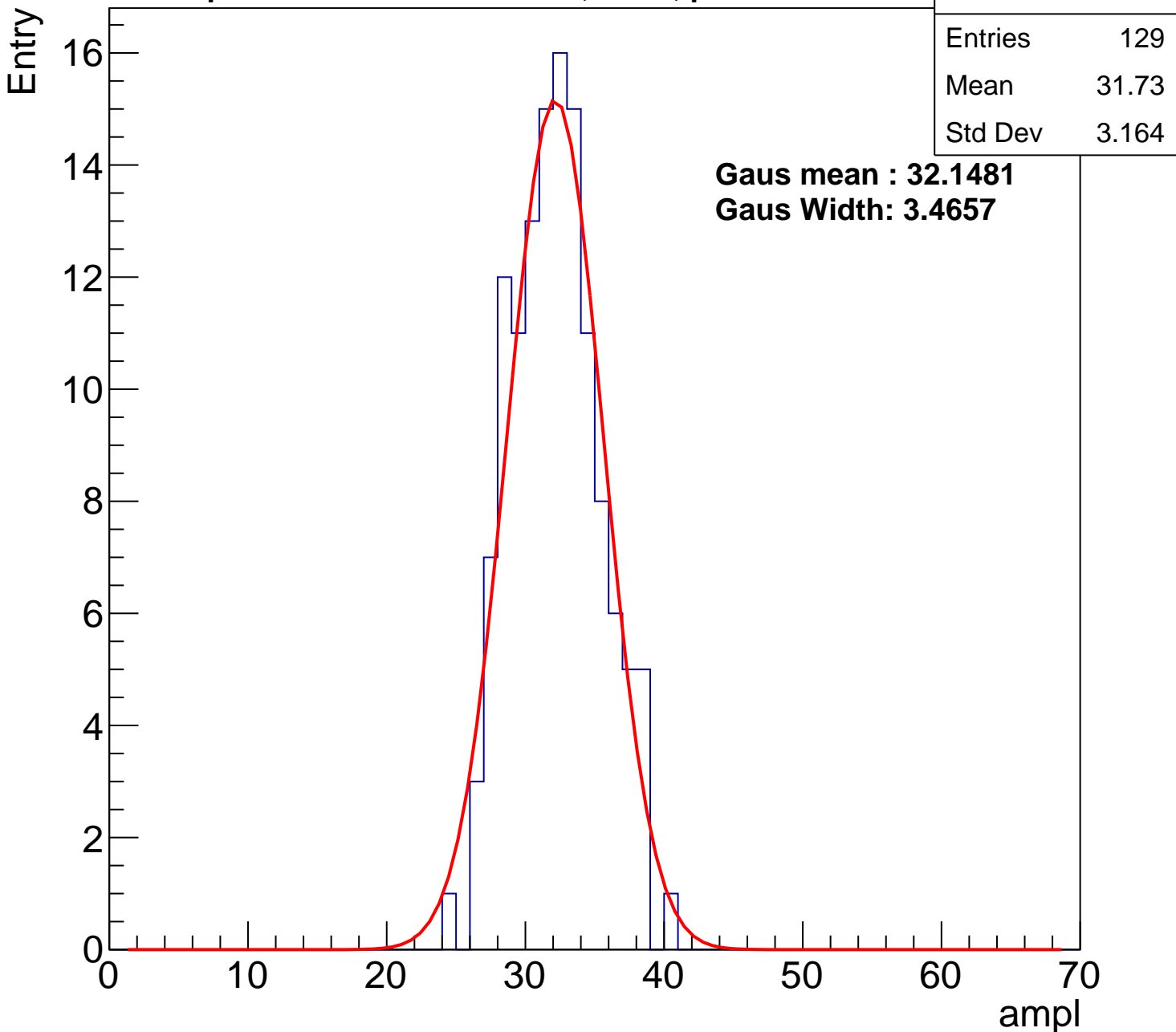
Gaus Width: 3.4657

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch23, adc1

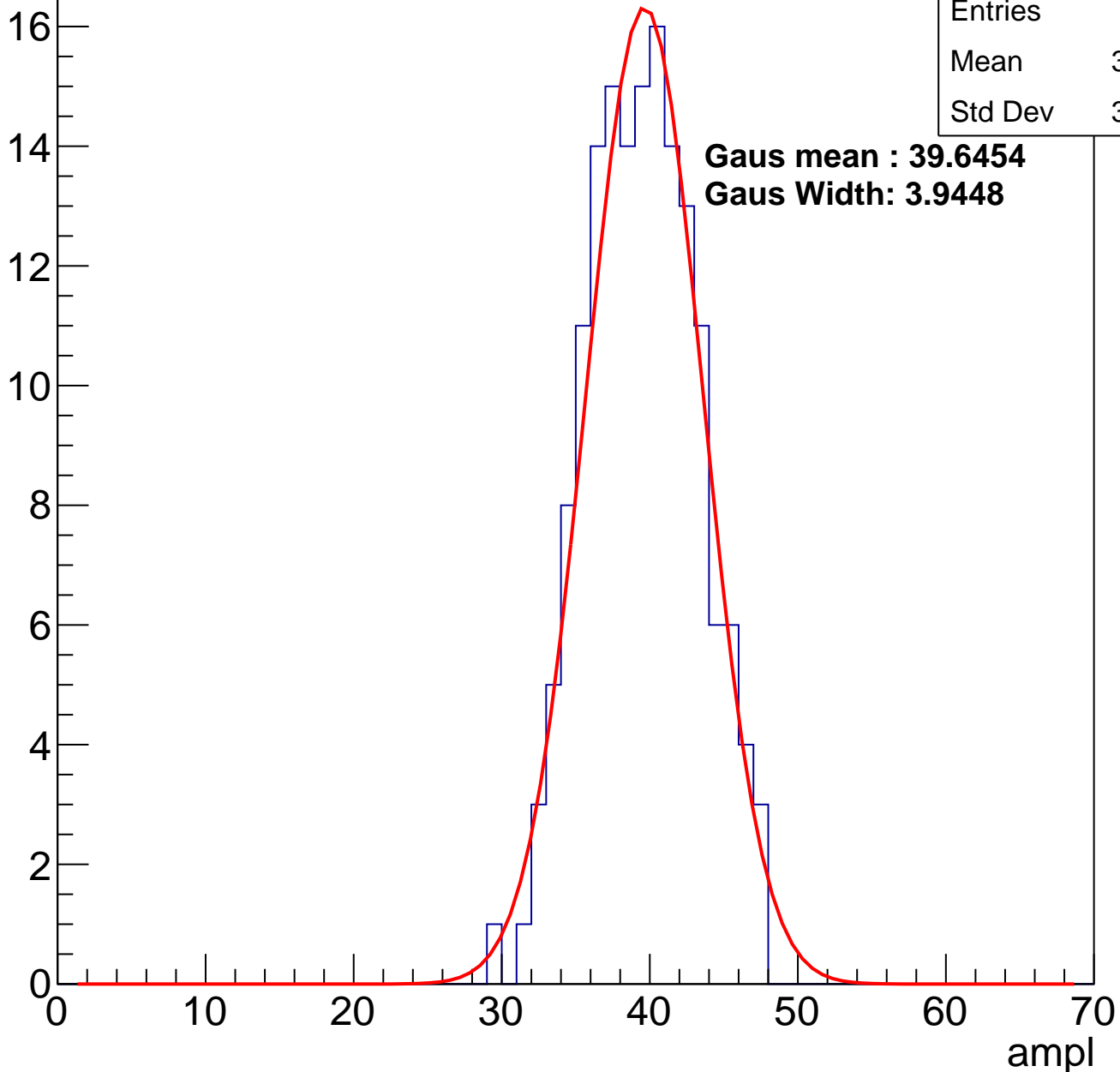
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	39.04
Std Dev	3.696

Gaus mean : 39.6454

Gaus Width: 3.9448

Entry



B0L101S, U19-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	45.2
Std Dev	3.559

Gaus mean : 45.6815

Gaus Width: 3.4617

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U19-ch23, adc3

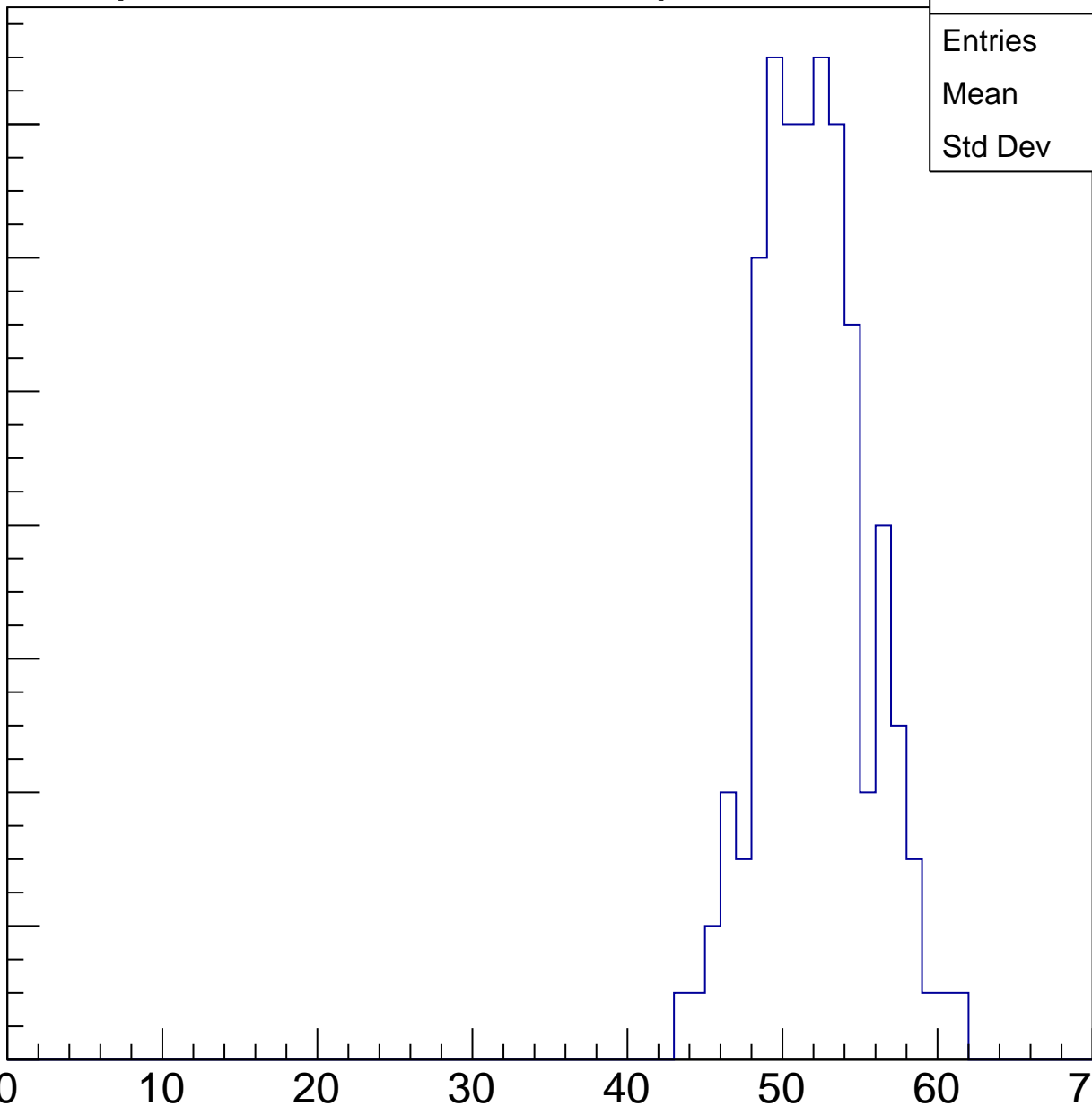
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	129
Mean	51.55
Std Dev	3.389

ampl



B0L101S, U19-ch23, adc4

calib_packv5_042523_0143.root, FC#1, port C1

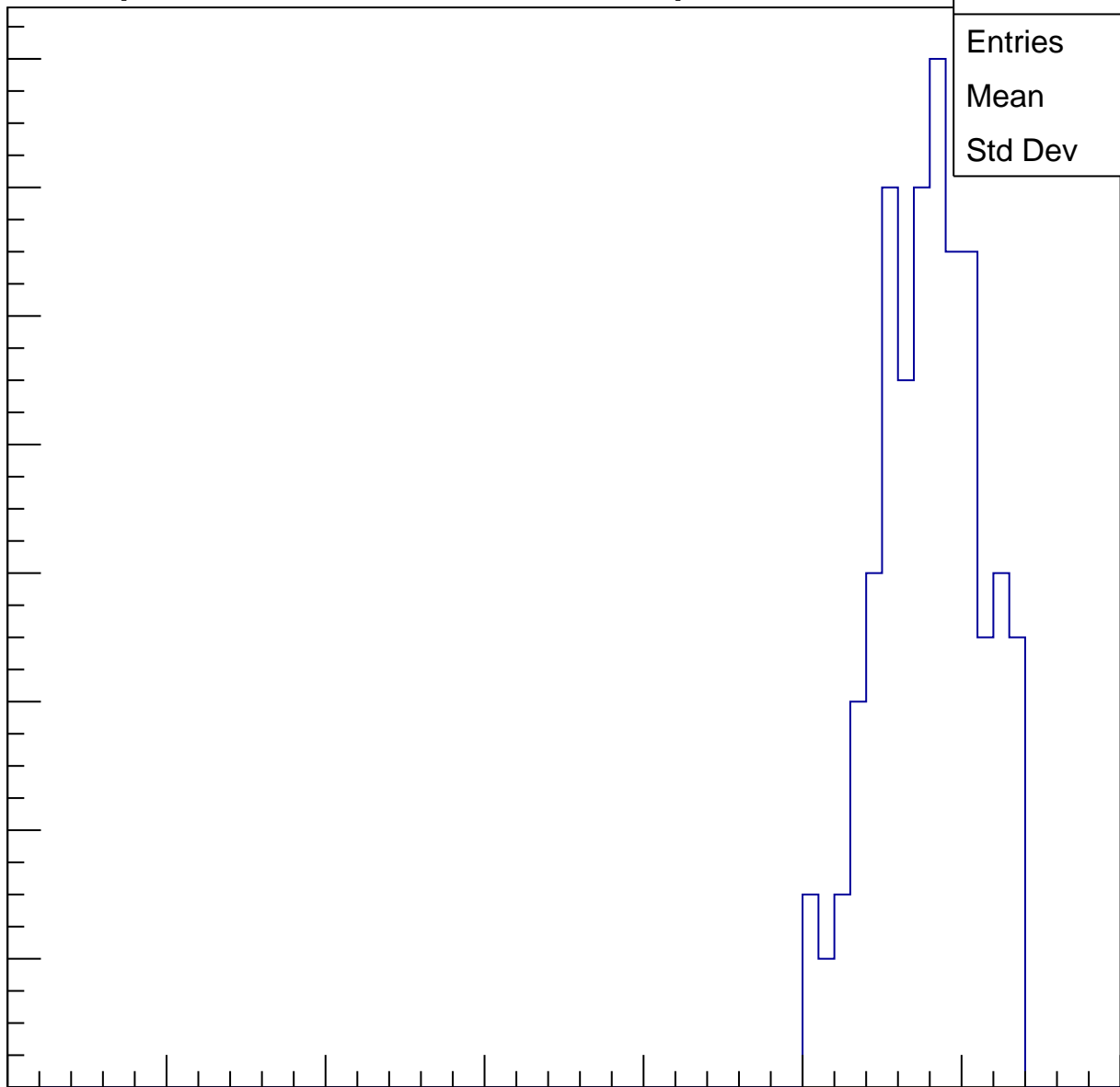
Entries	125
Mean	57.45
Std Dev	3.166

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch23, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	55
Mean	58.8
Std Dev	11.57

0

2

4

6

8

10

12

B0L101S, U19-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch24, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	30.28
Std Dev	3.38

Gaus mean : 30.6405

Gaus Width: 3.4099

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

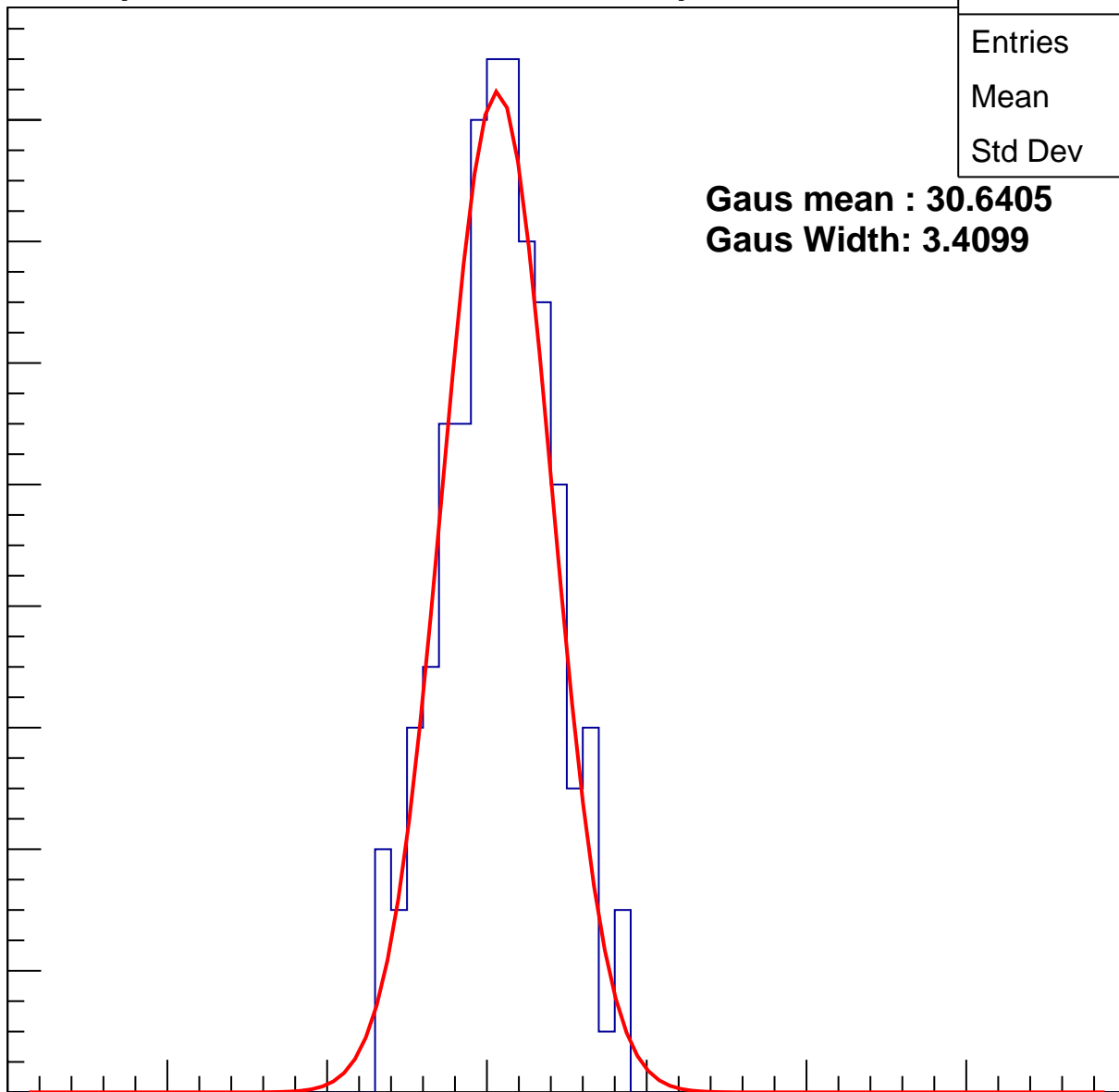
40

50

60

70

ampl



B0L101S, U19-ch24, adc1

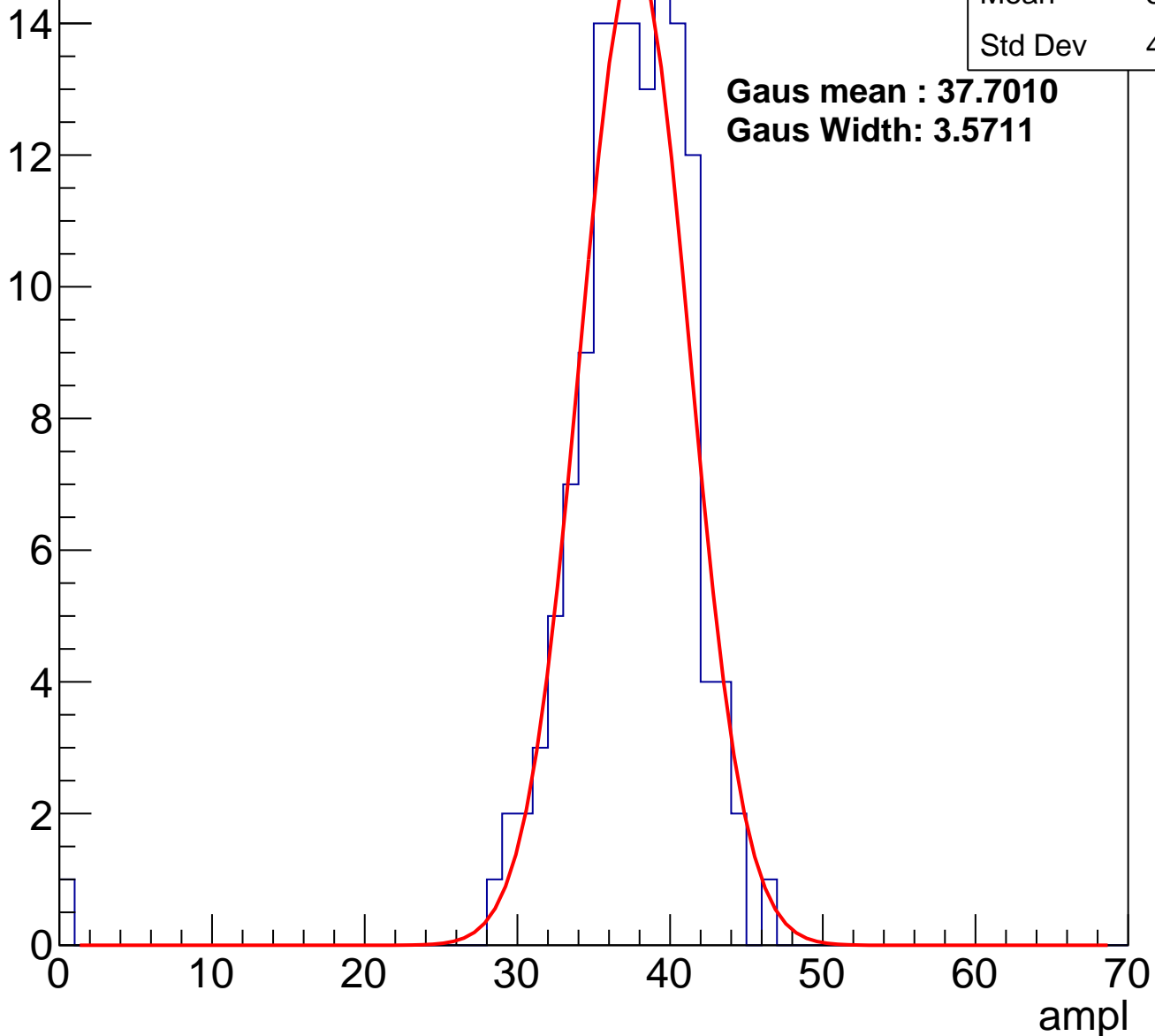
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	36.88
Std Dev	4.667

Gaus mean : 37.7010

Gaus Width: 3.5711

Entry



B0L101S, U19-ch24, adc2

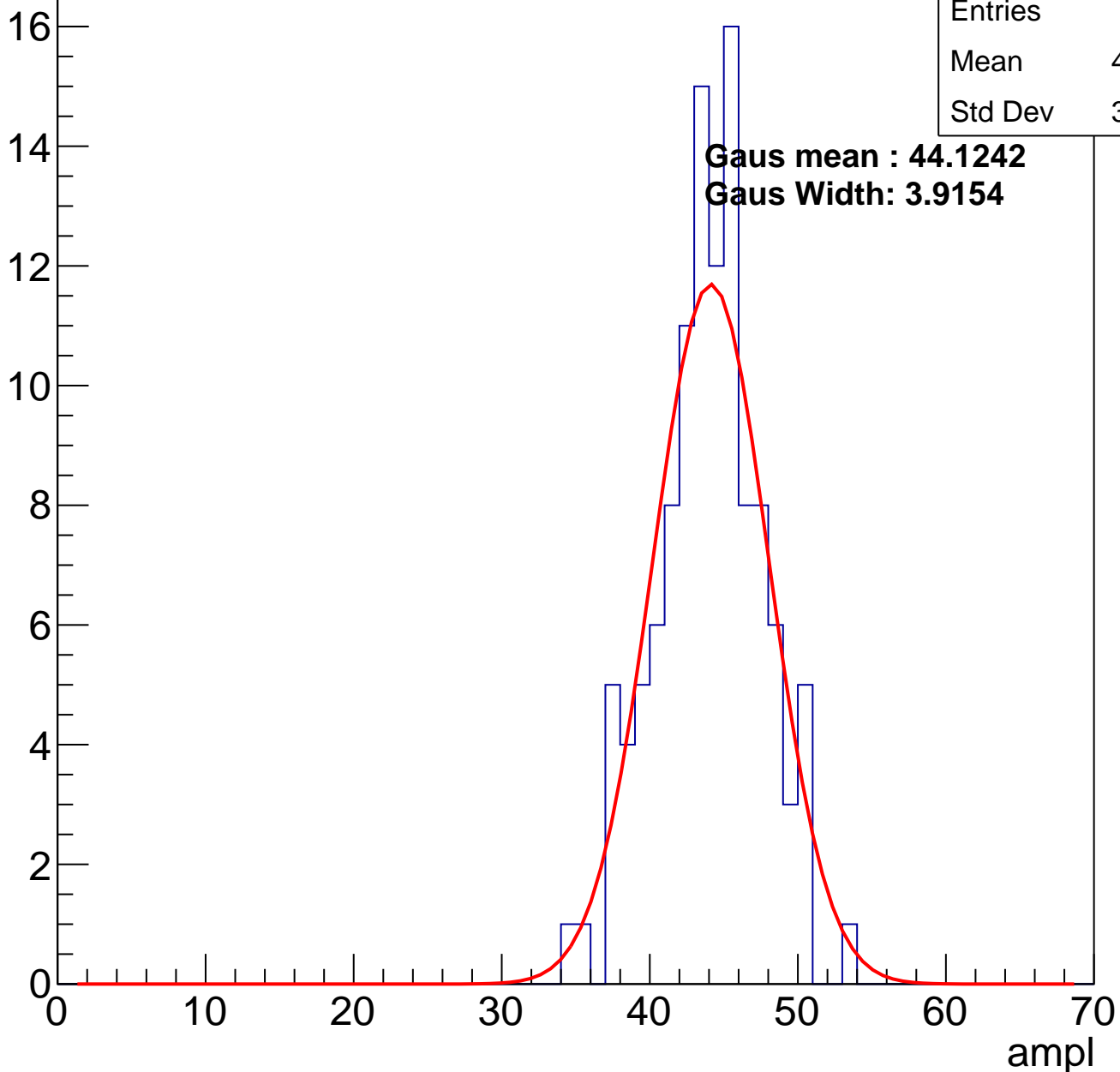
calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	43.53
Std Dev	3.549

Gaus mean : 44.1242

Gaus Width: 3.9154

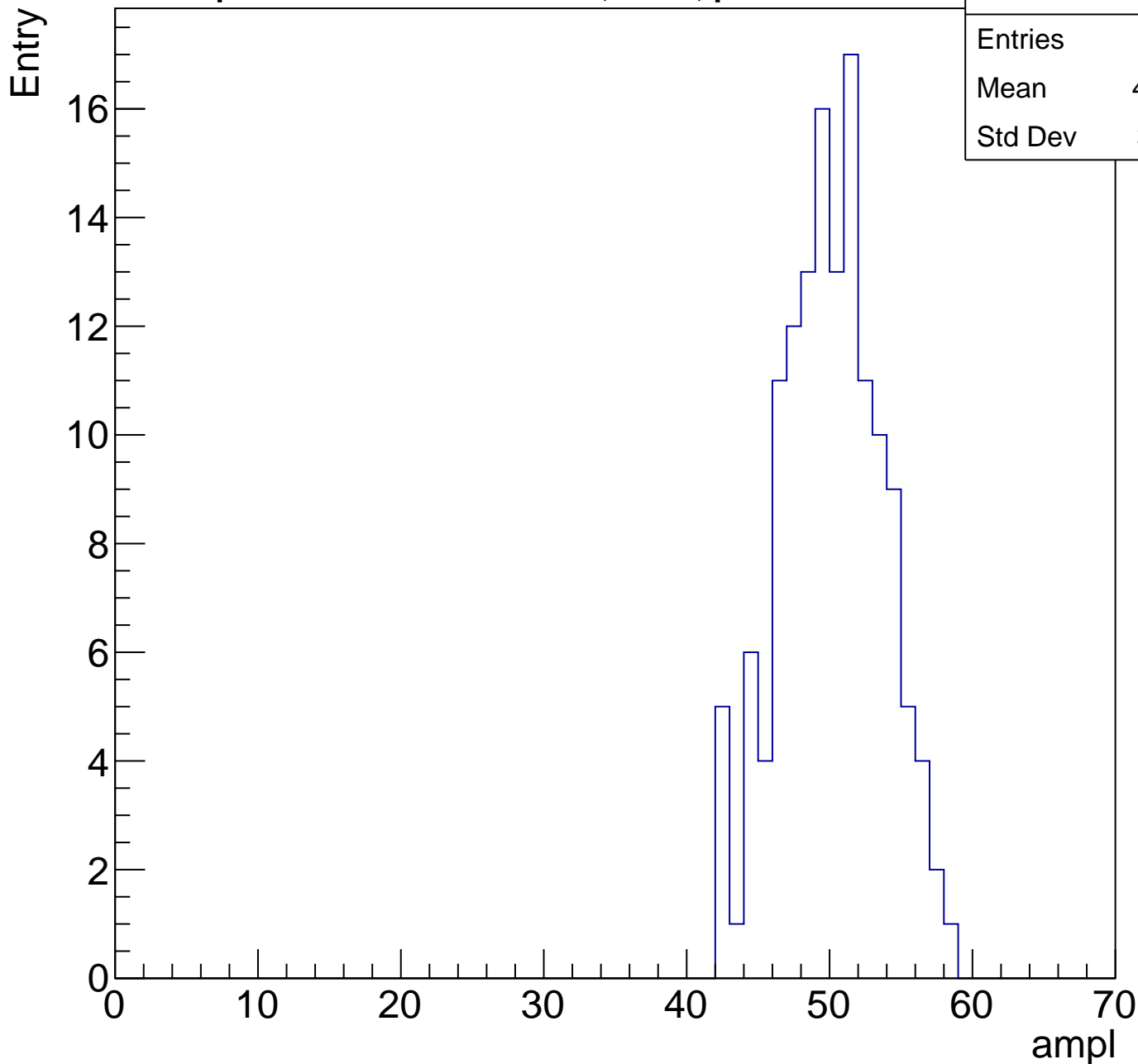
Entry



B0L101S, U19-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	49.65
Std Dev	3.531



B0L101S, U19-ch24, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	55.76
Std Dev	3.045

Entry

12

10

8

6

4

2

0

0

10

20

30

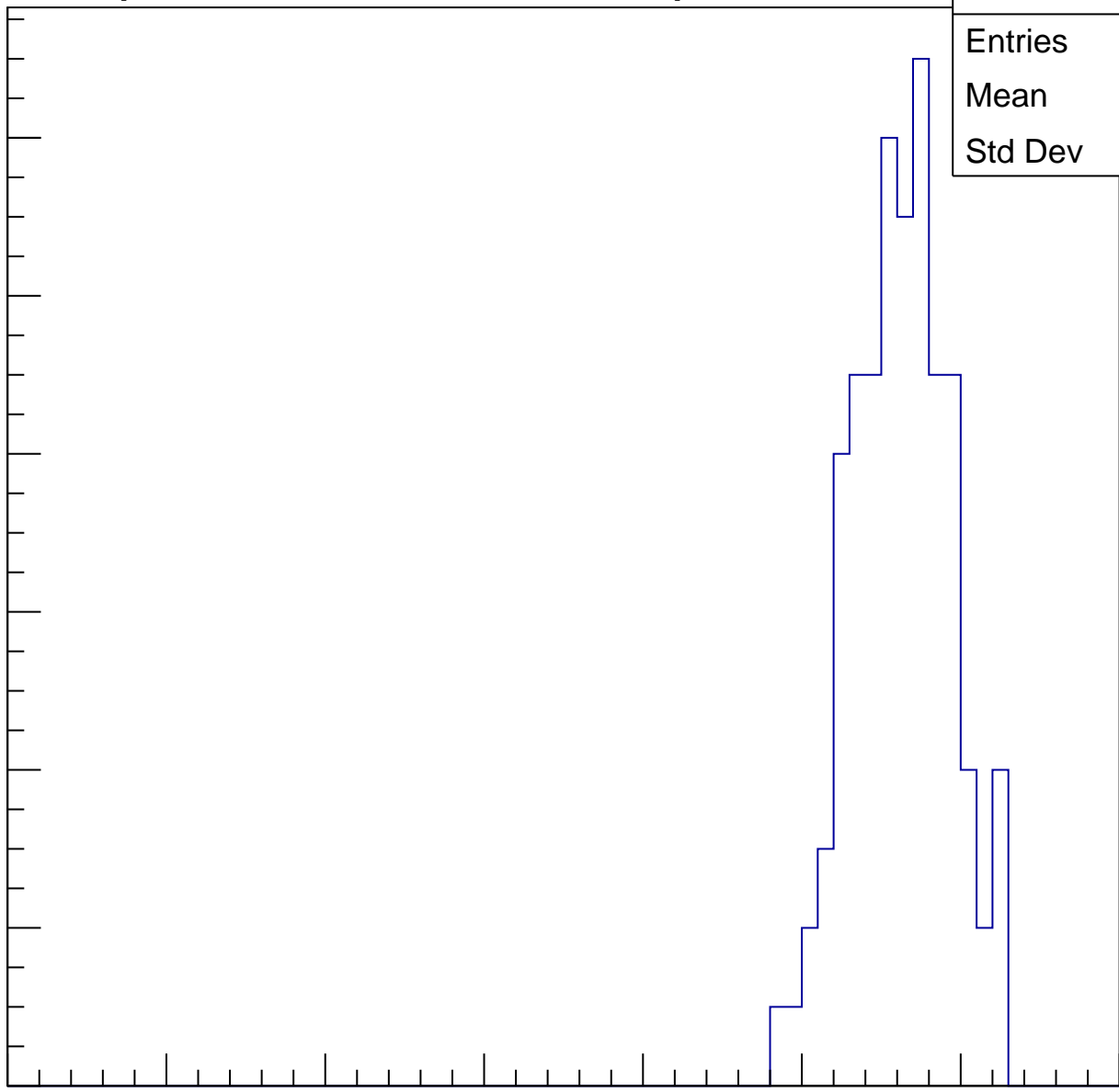
40

50

60

70

ampl



B0L101S, U19-ch24, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	58.75
Std Dev	8.657

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

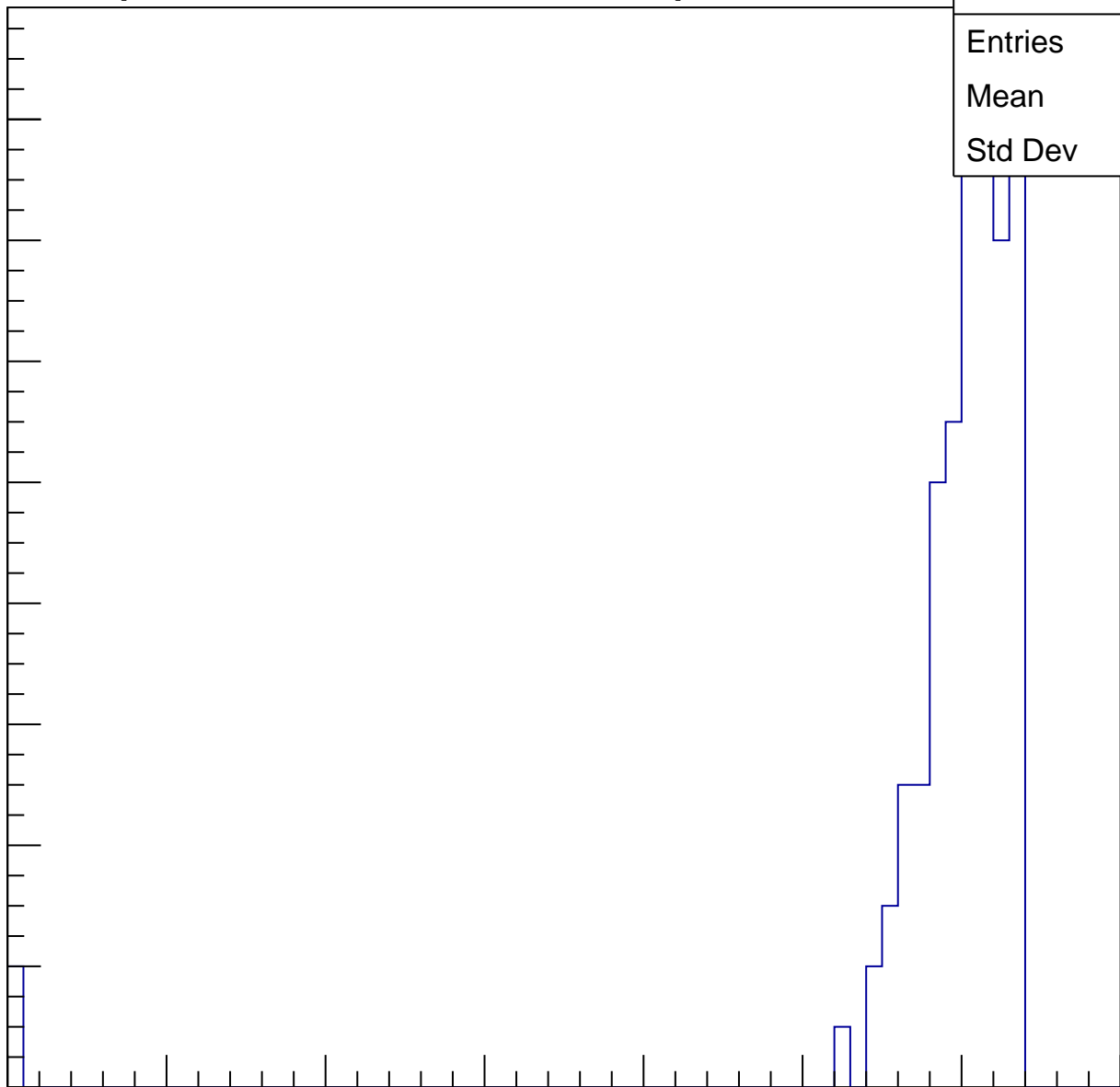
40

50

60

70

ampl



B0L101S, U19-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch25, adc0

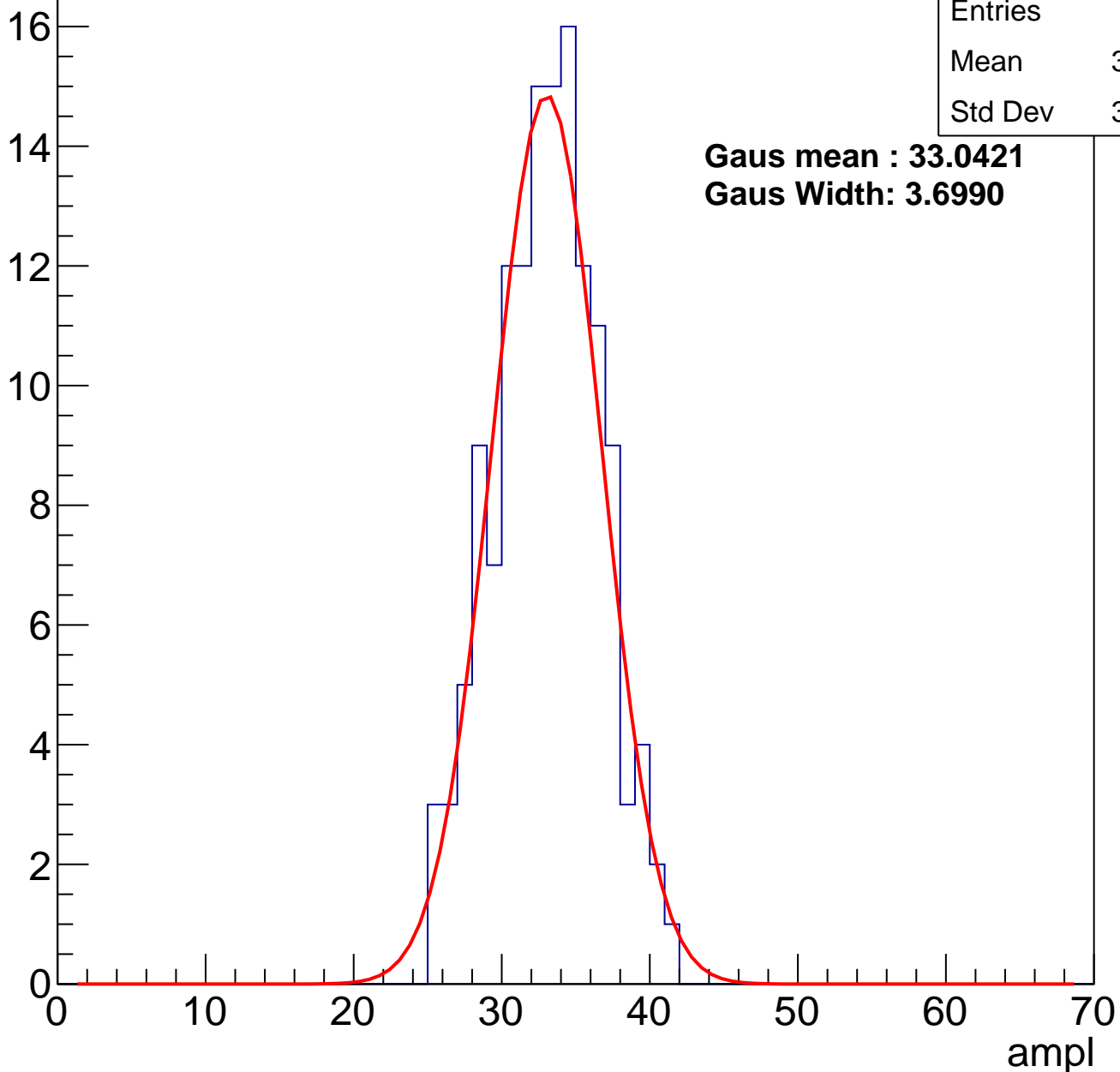
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	32.62
Std Dev	3.492

Gaus mean : 33.0421

Gaus Width: 3.6990

Entry



B0L101S, U19-ch25, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

150

Mean

40.13

Std Dev

3.413

Gaus mean : 40.6560

Gaus Width: 3.4889

0

10

20

30

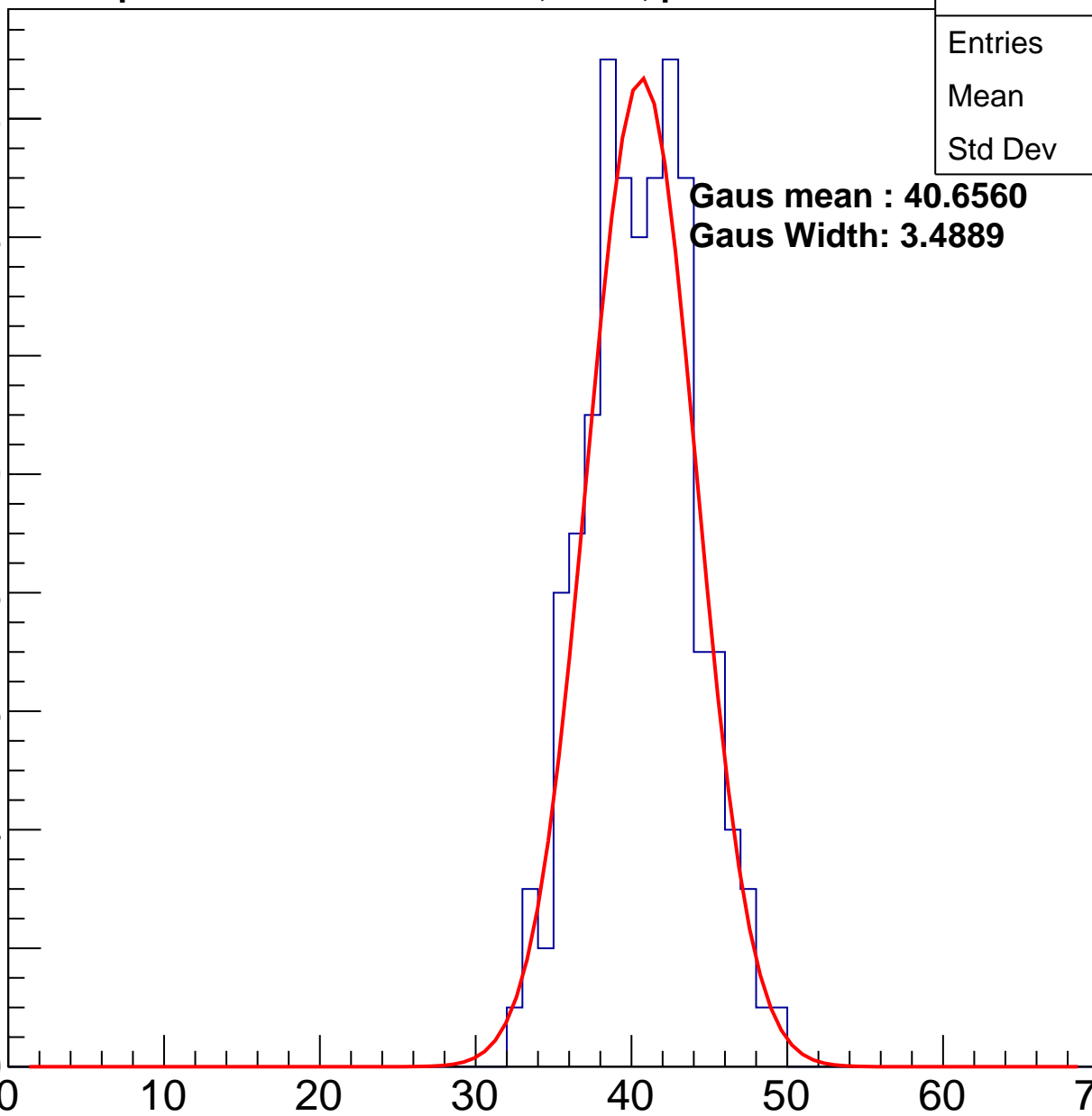
40

50

60

70

ampl



B0L101S, U19-ch25, adc2

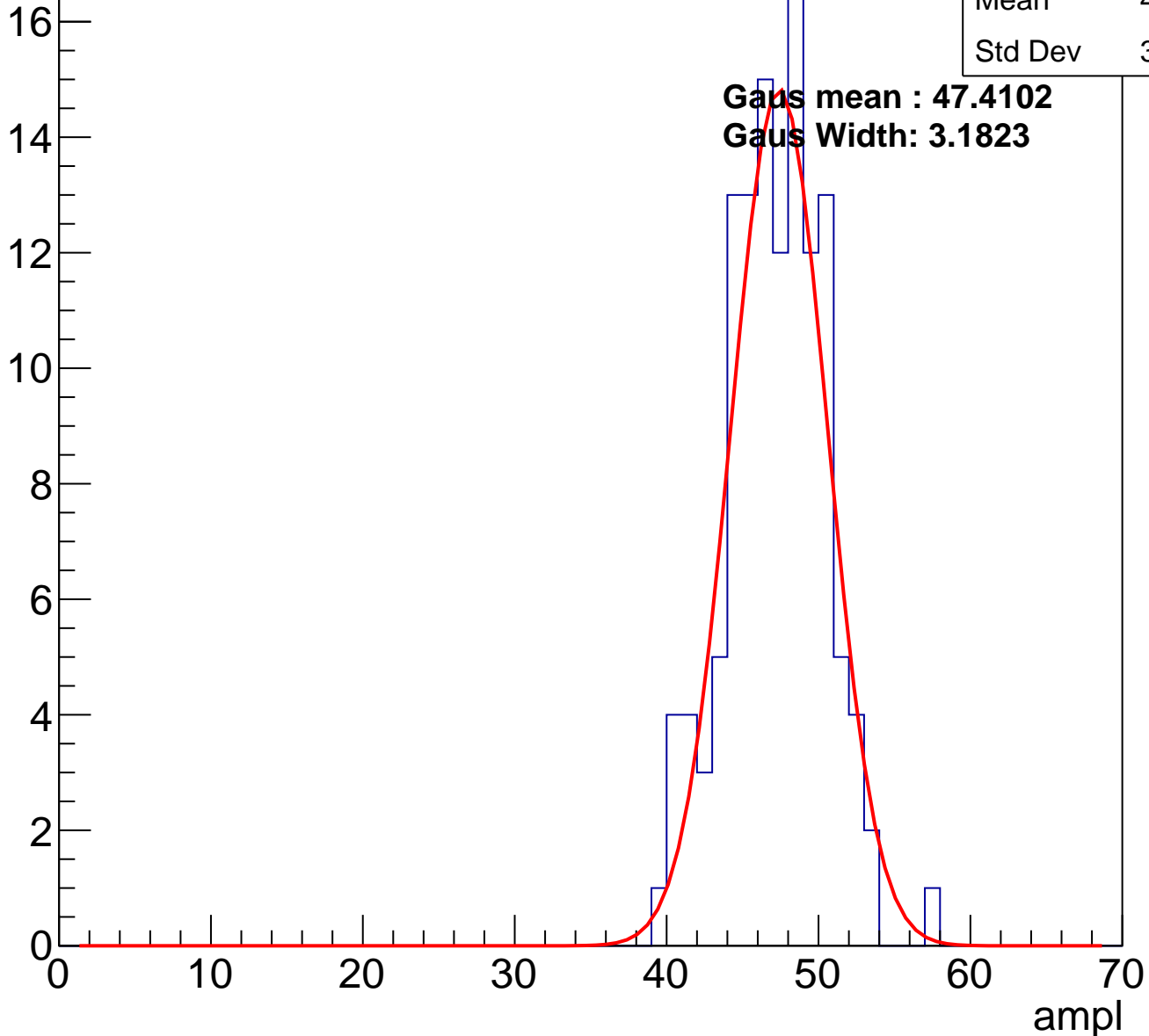
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	46.73
Std Dev	3.198

Gaus mean : 47.4102

Gaus Width: 3.1823

Entry



B0L101S, U19-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

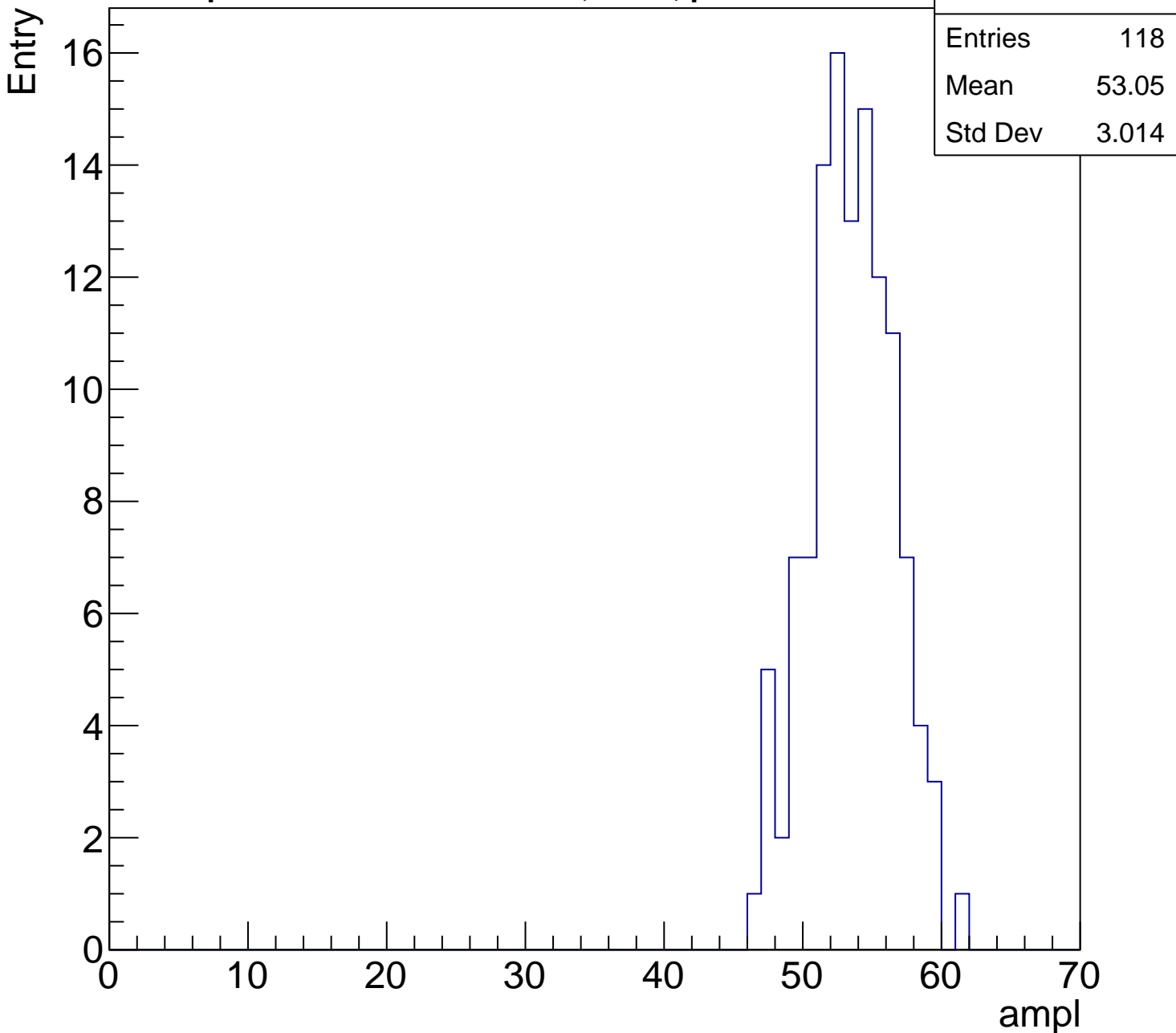
Entries	118
Mean	53.05
Std Dev	3.014

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

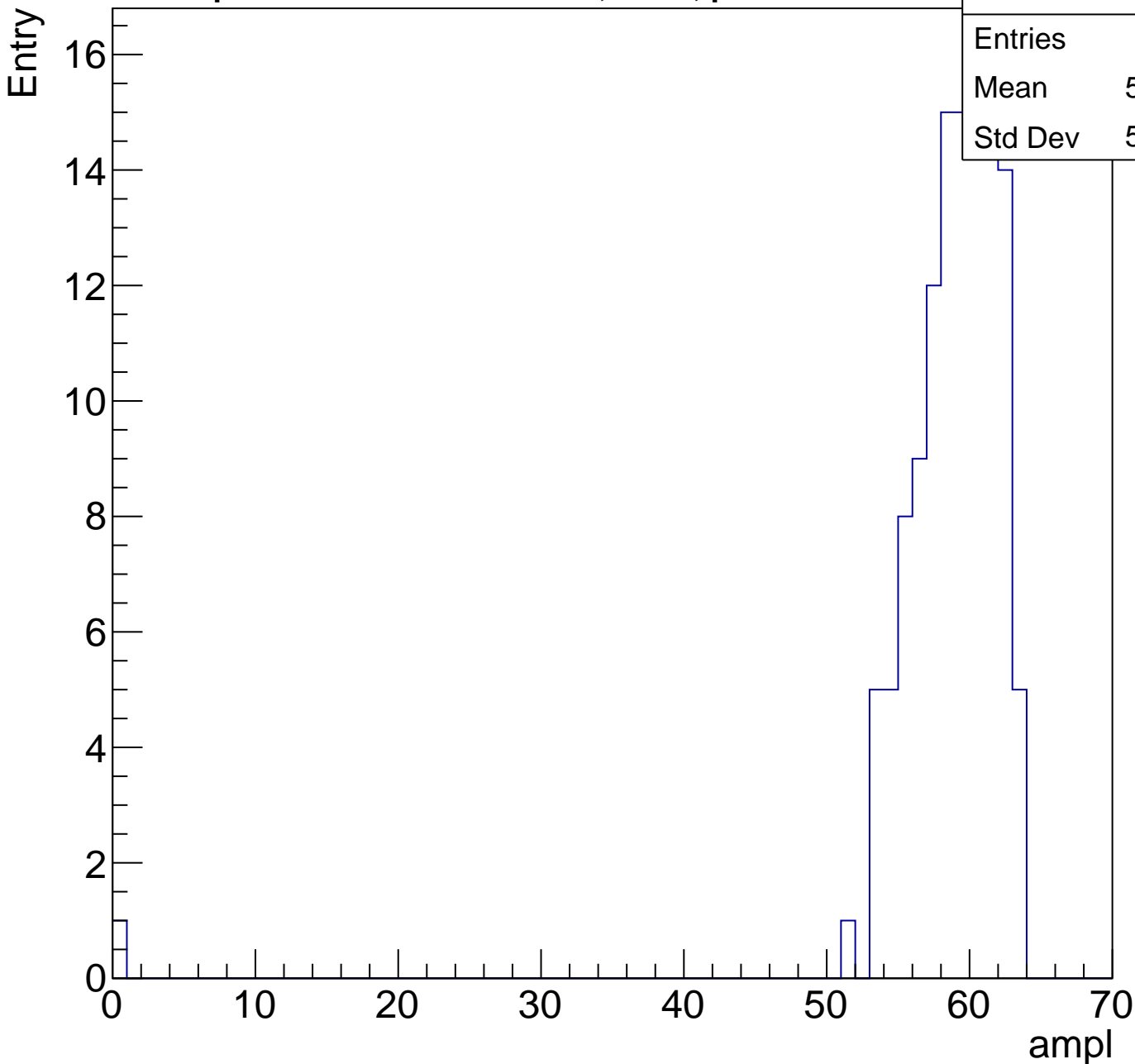
ampl



B0L101S, U19-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	58.07
Std Dev	5.962



B0L101S, U19-ch25, adc5

calib_packv5_042523_0143.root, FC#1, port C1

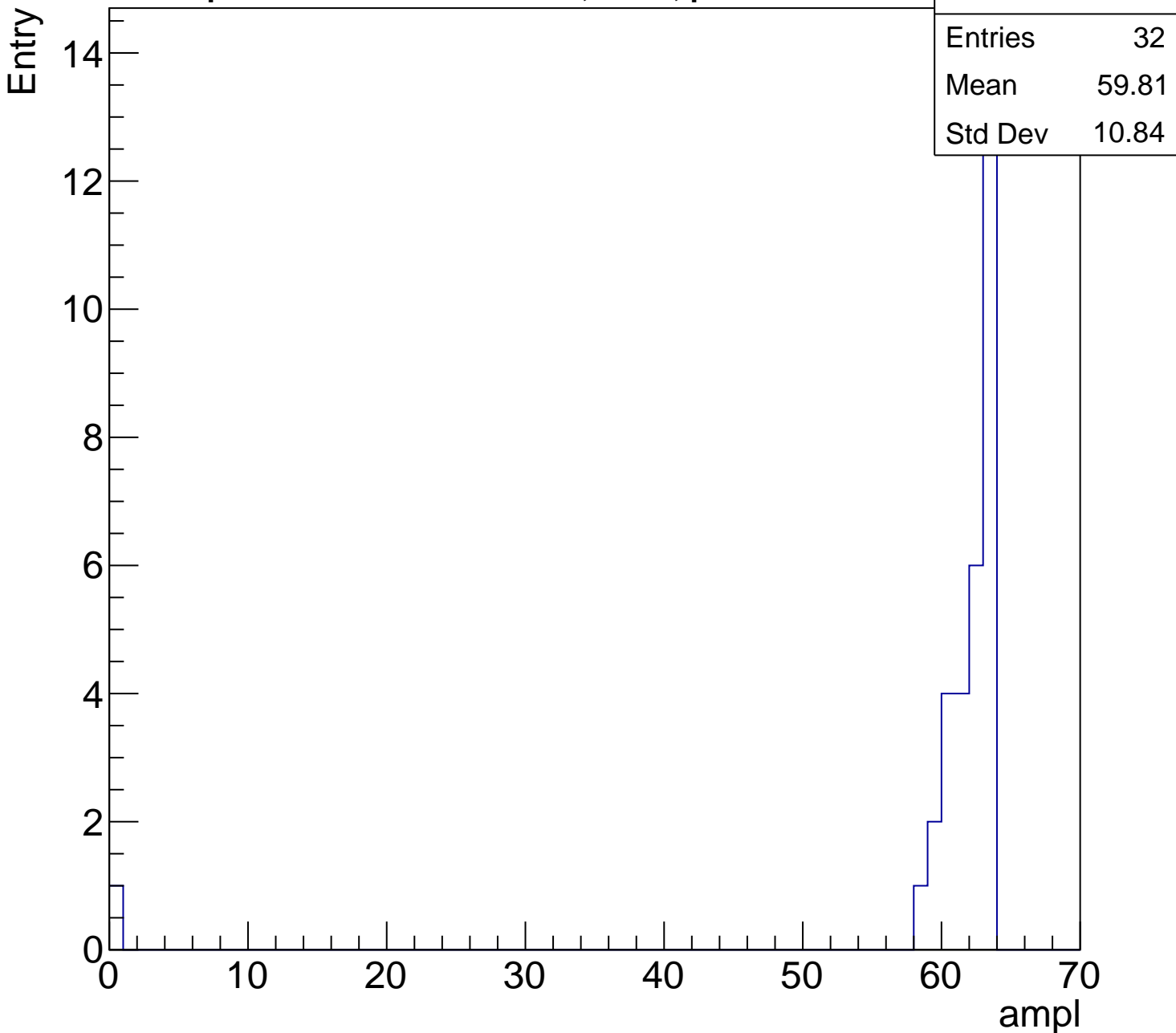
Entry

14
12
10
8
6
4
2
0

Entries	32
Mean	59.81
Std Dev	10.84

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

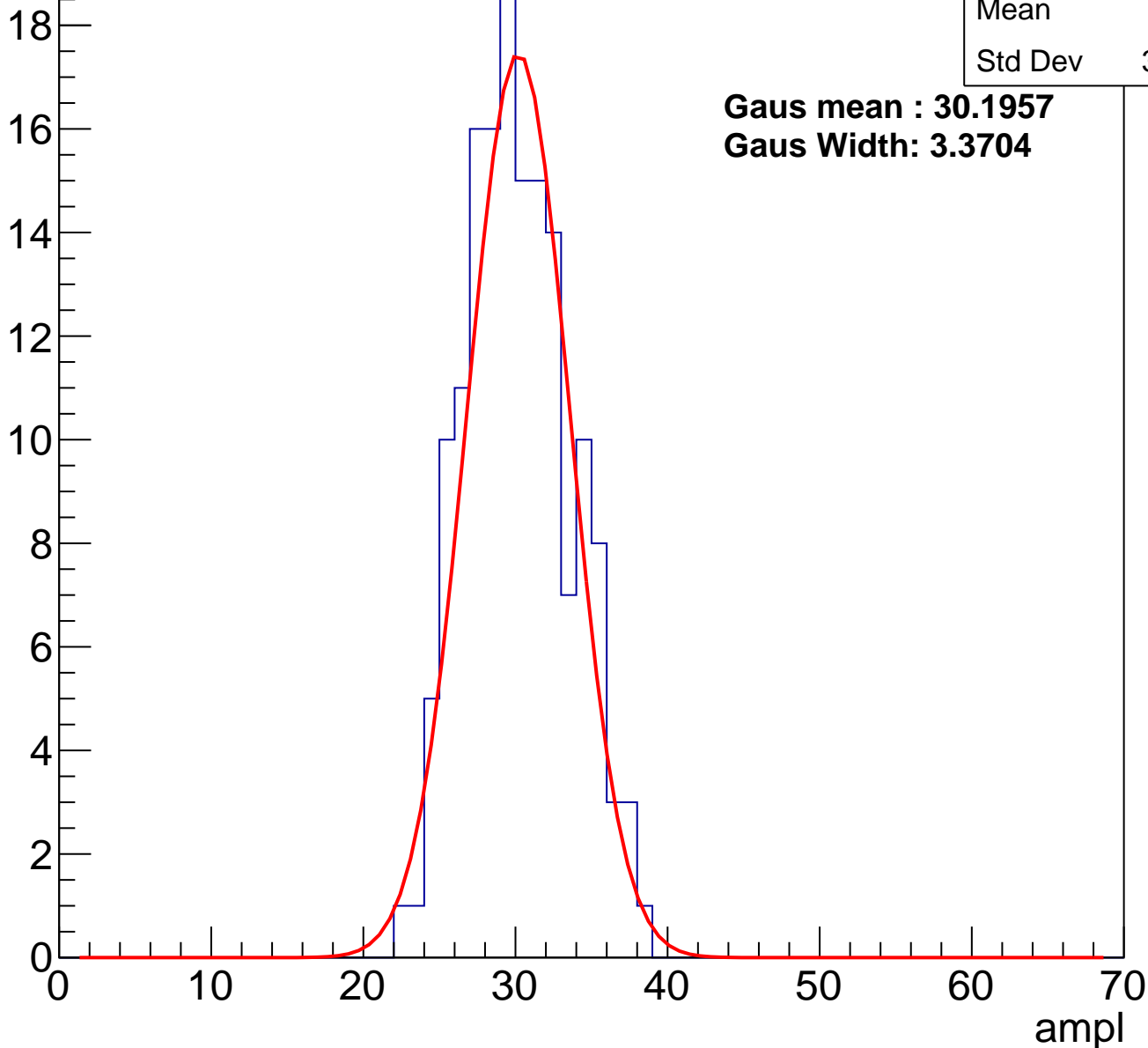
B0L101S, U19-ch26, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	29.7
Std Dev	3.355

Gaus mean : 30.1957
Gaus Width: 3.3704

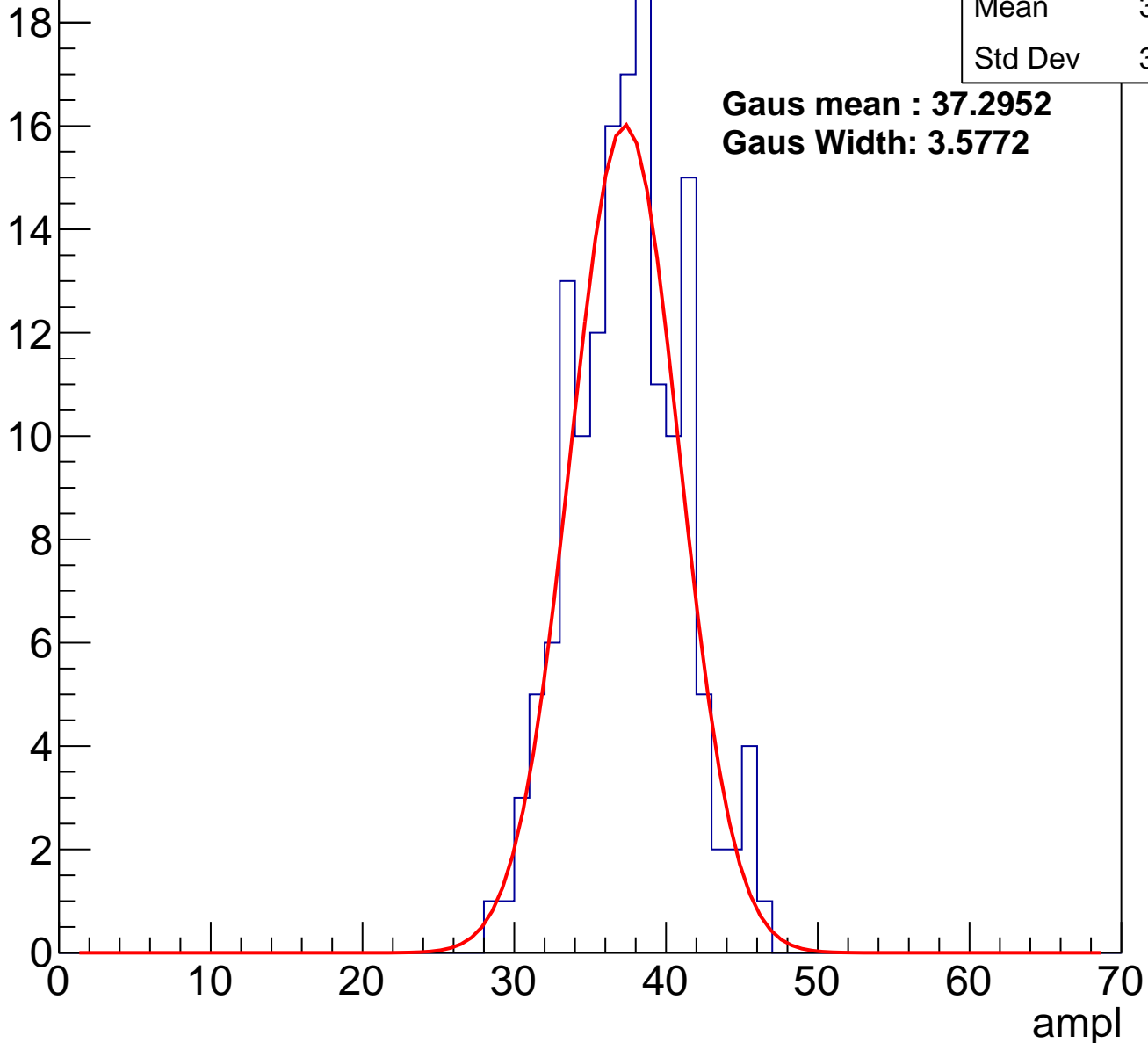
Entry



B0L101S, U19-ch26, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	153
Mean	37.02
Std Dev	3.624

B0L101S, U19-ch26, adc2

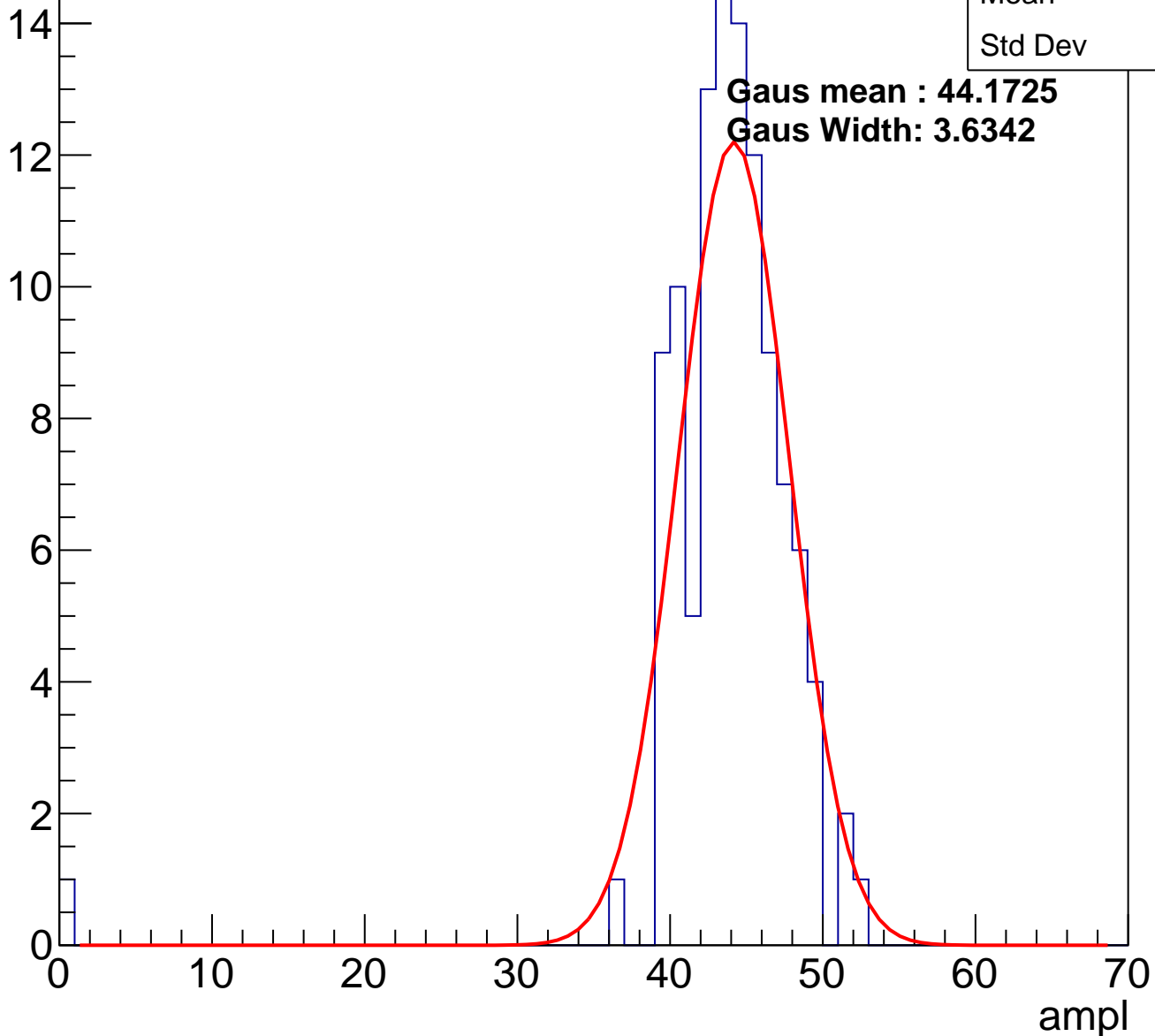
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	43.3
Std Dev	5.17

Gaus mean : 44.1725

Gaus Width: 3.6342

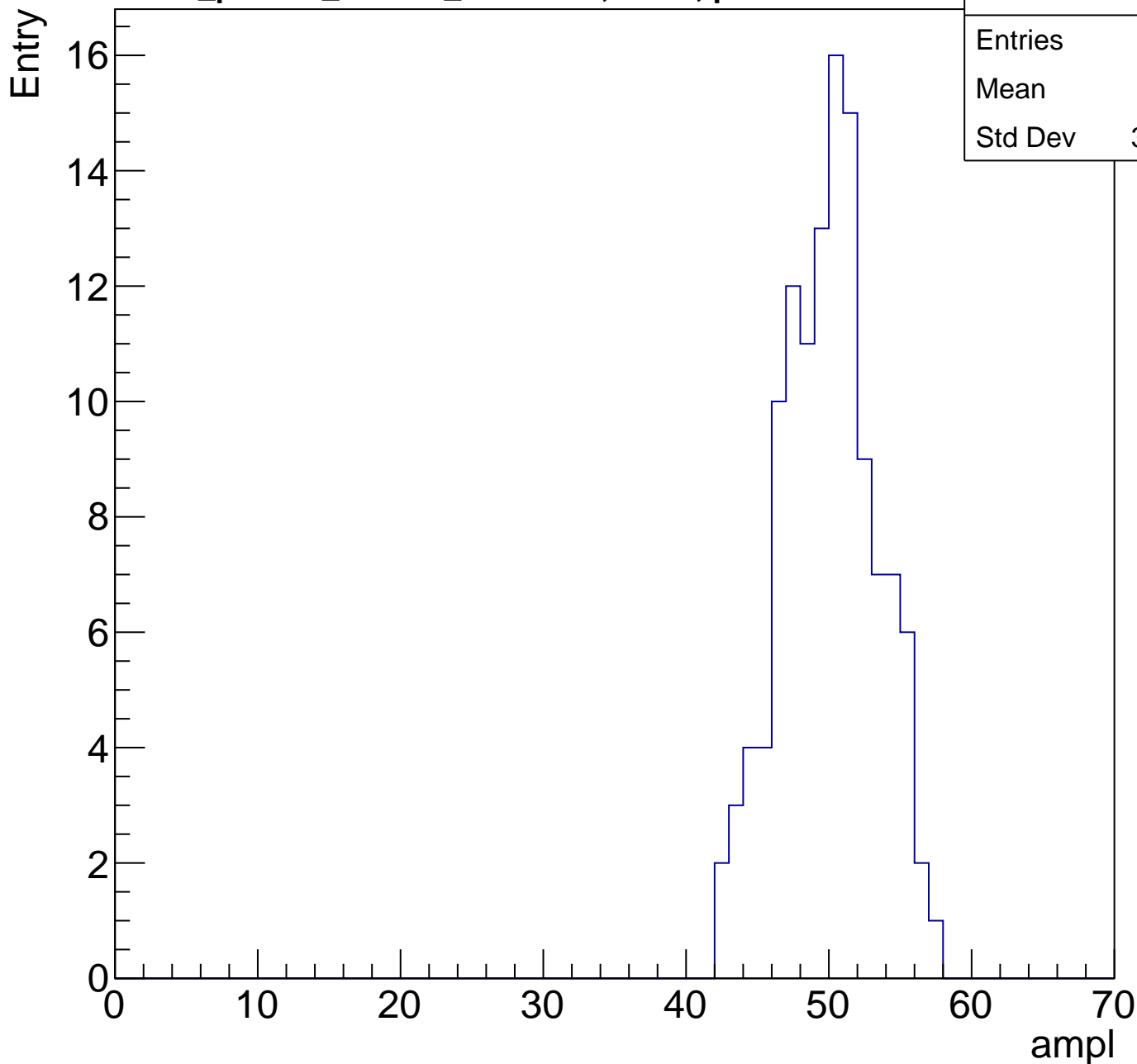
Entry



B0L101S, U19-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	49.5
Std Dev	3.277

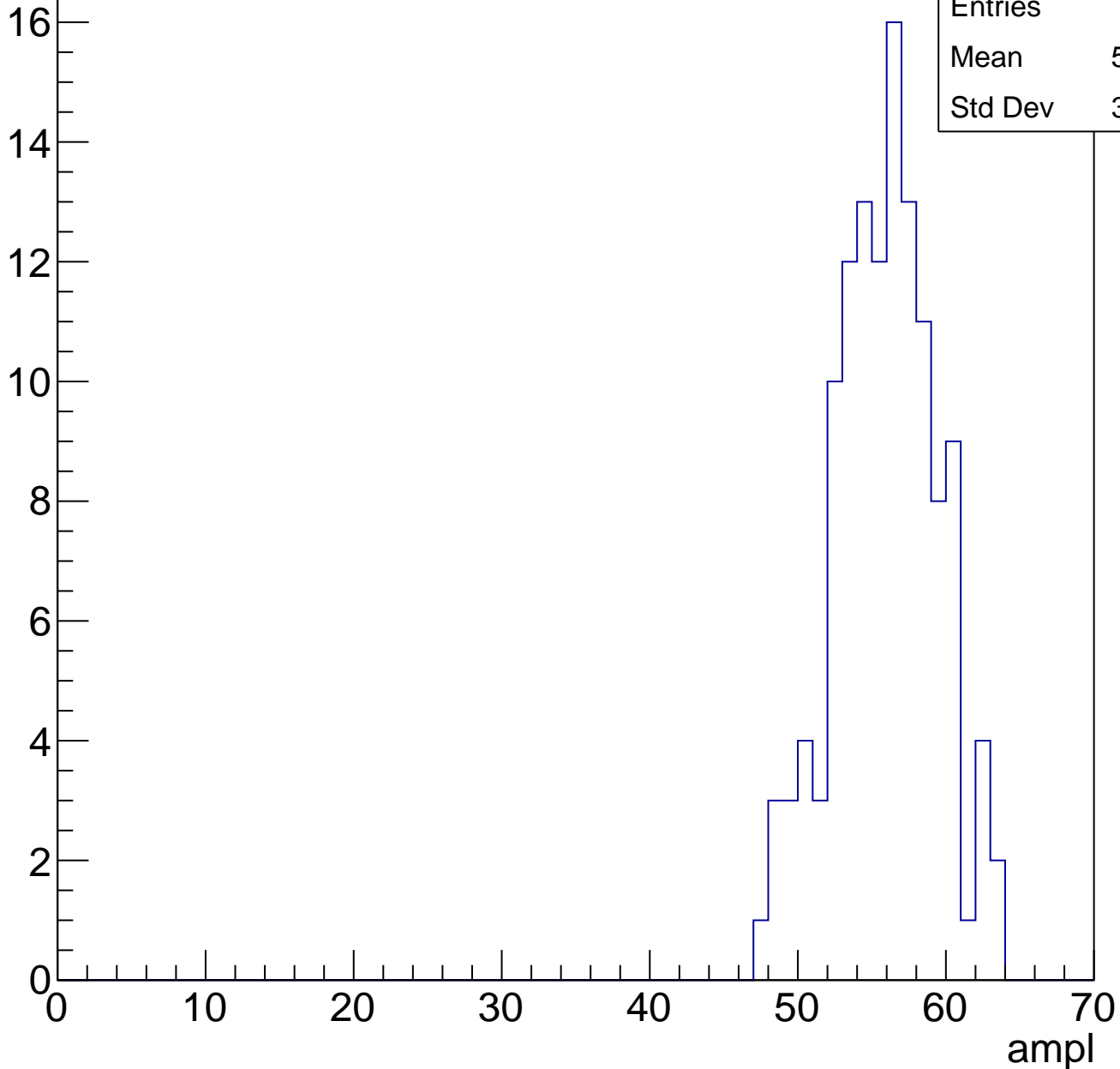


B0L101S, U19-ch26, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.45
Std Dev	3.433

Entry



B0L101S, U19-ch26, adc5

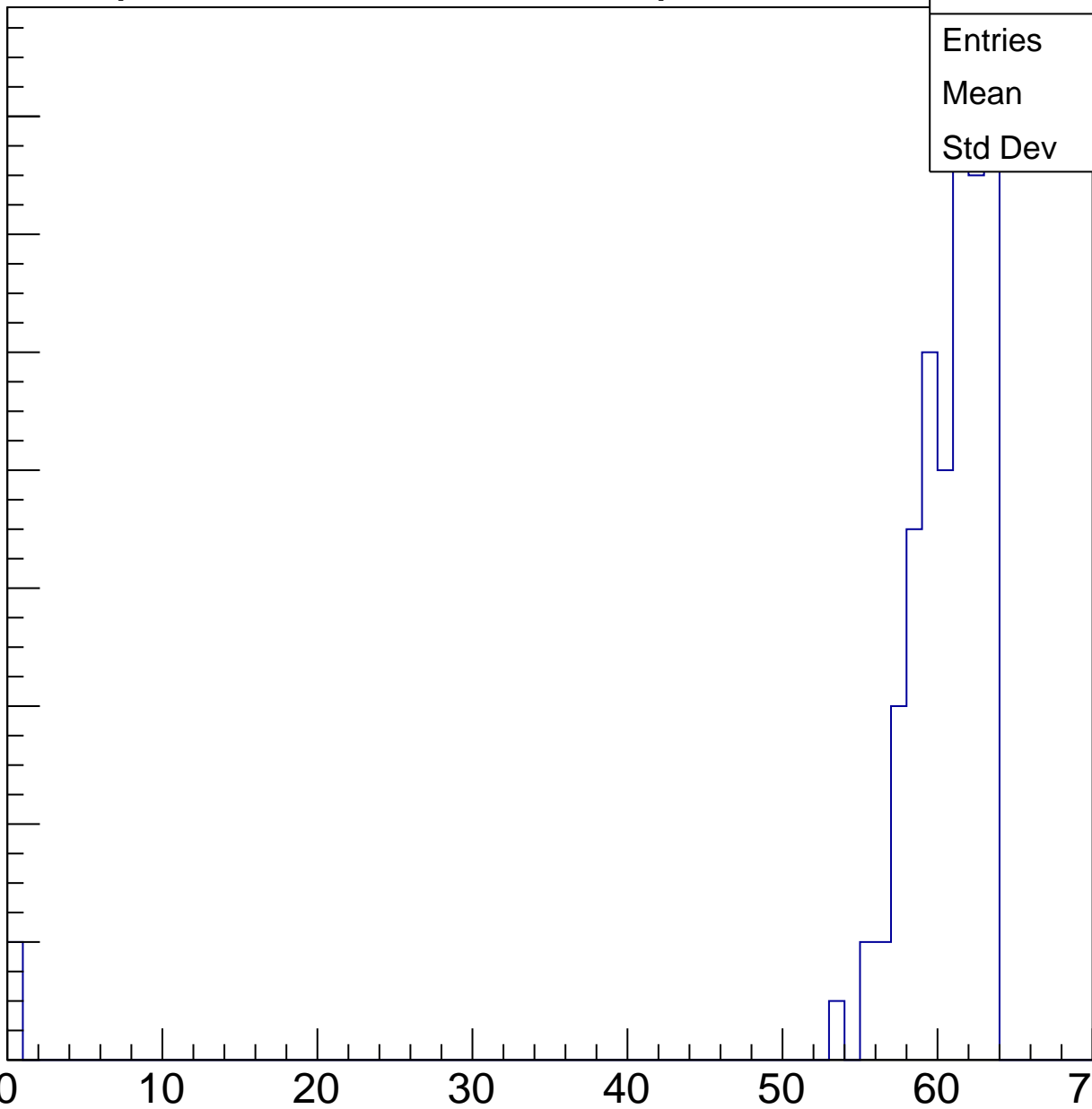
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.93
Std Dev	9.059

ampl



B0L101S, U19-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

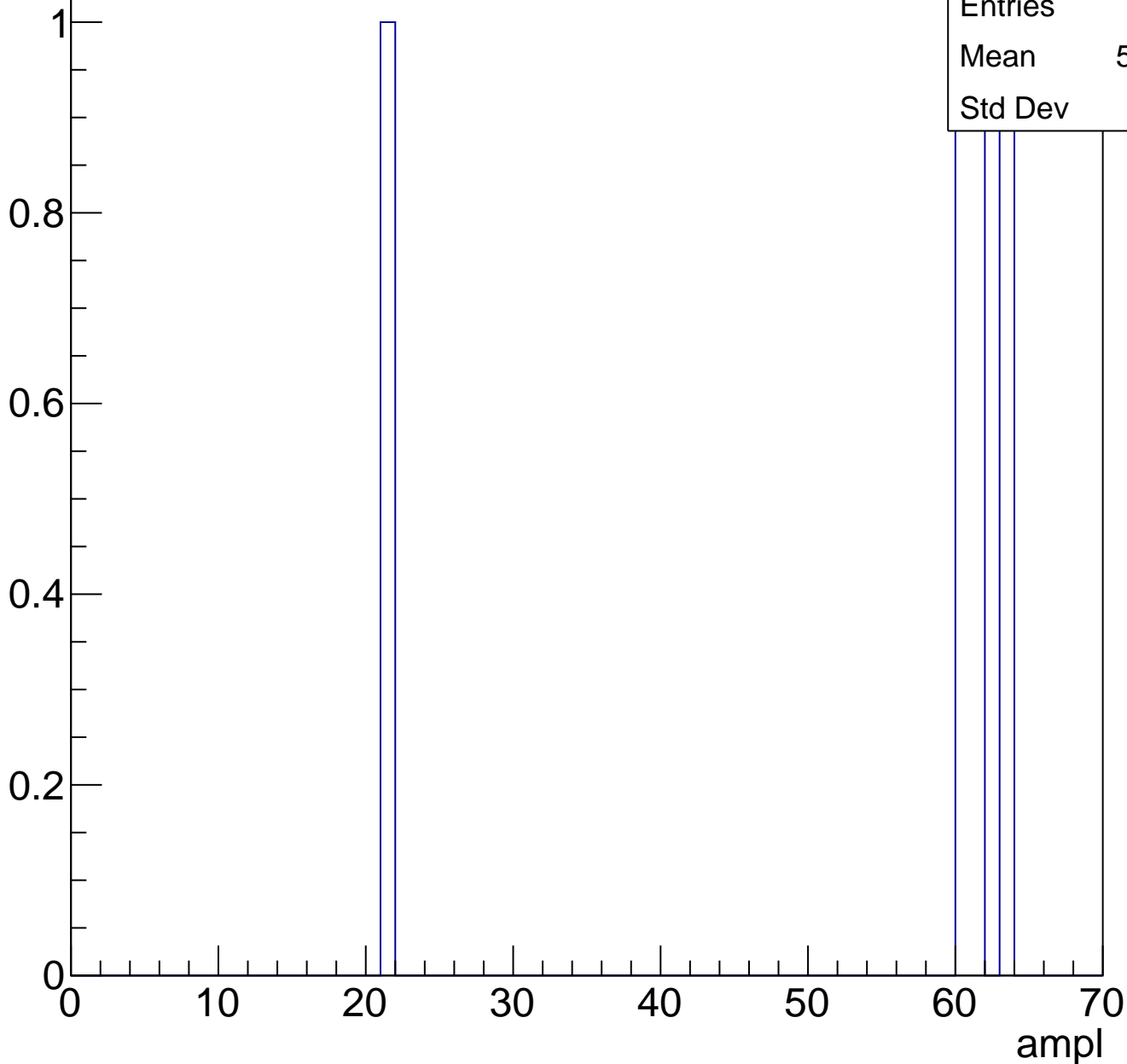
Entry



B0L101S, U19-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch27, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	31.07
Std Dev	4.464

Gaus mean : 31.5091

Gaus Width: 3.3773

Entry

14

12

10

8

6

4

2

0

0

10

20

30

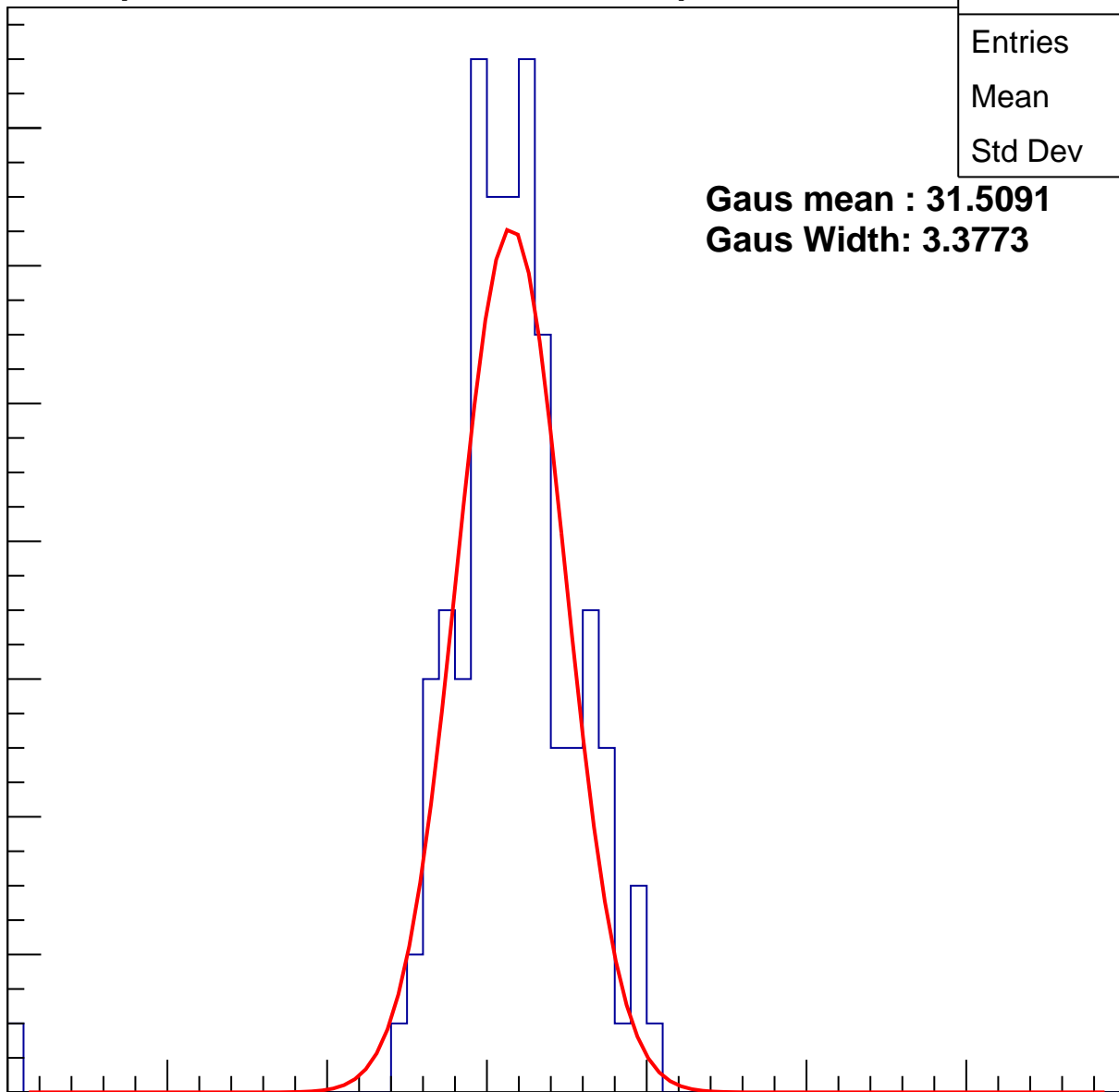
40

50

60

70

ampl



B0L101S, U19-ch27, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	130
Mean	37.3
Std Dev	3.547

Gaus mean : 37.4774

Gaus Width: 3.4950

0

0

10

20

30

40

50

60

70

ampl

B0L101S, U19-ch27, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

126

Mean

43.38

Std Dev

3.352

Gaus mean : 43.9788

Gaus Width: 3.5681

0

10

20

30

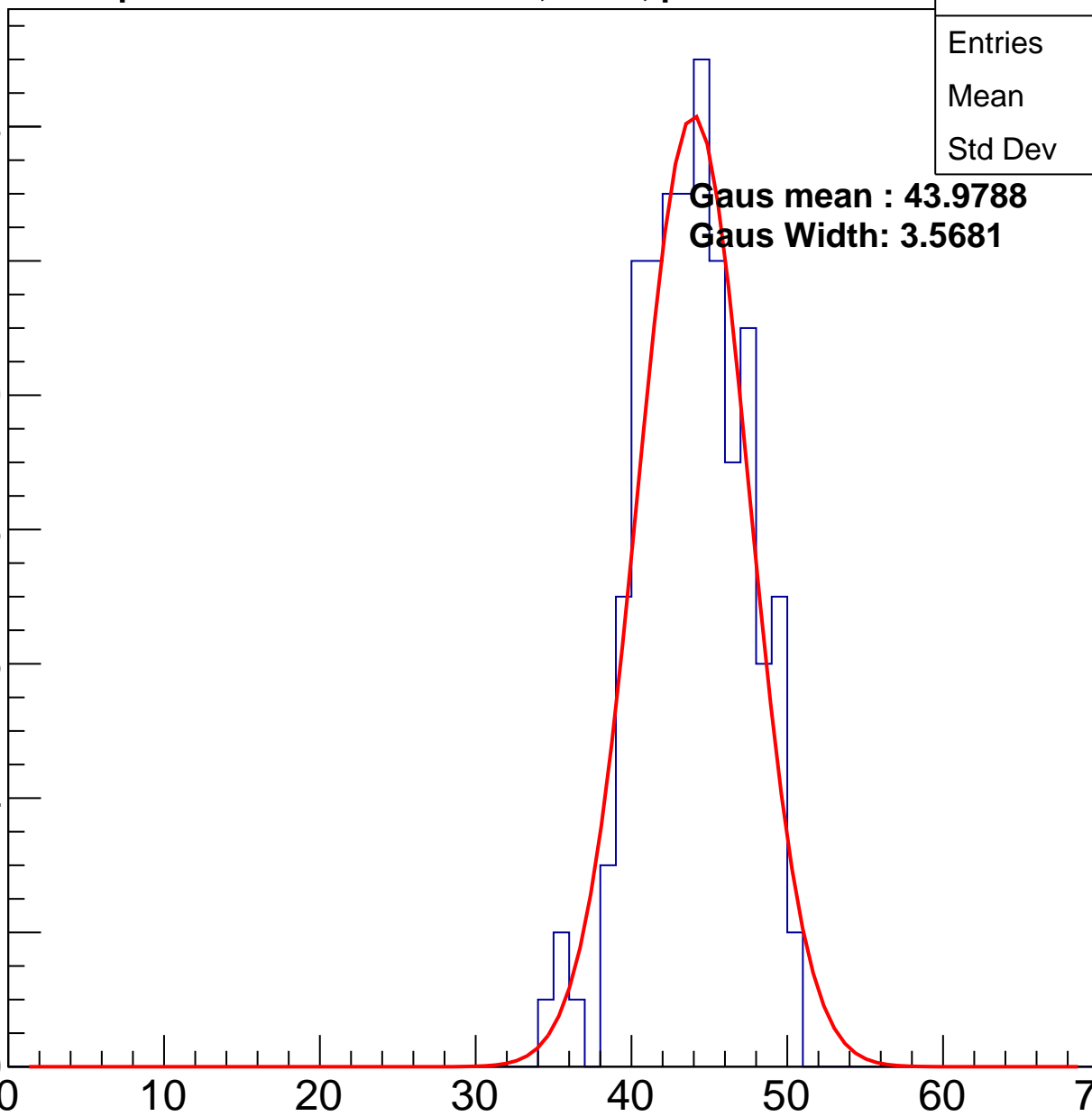
40

50

60

70

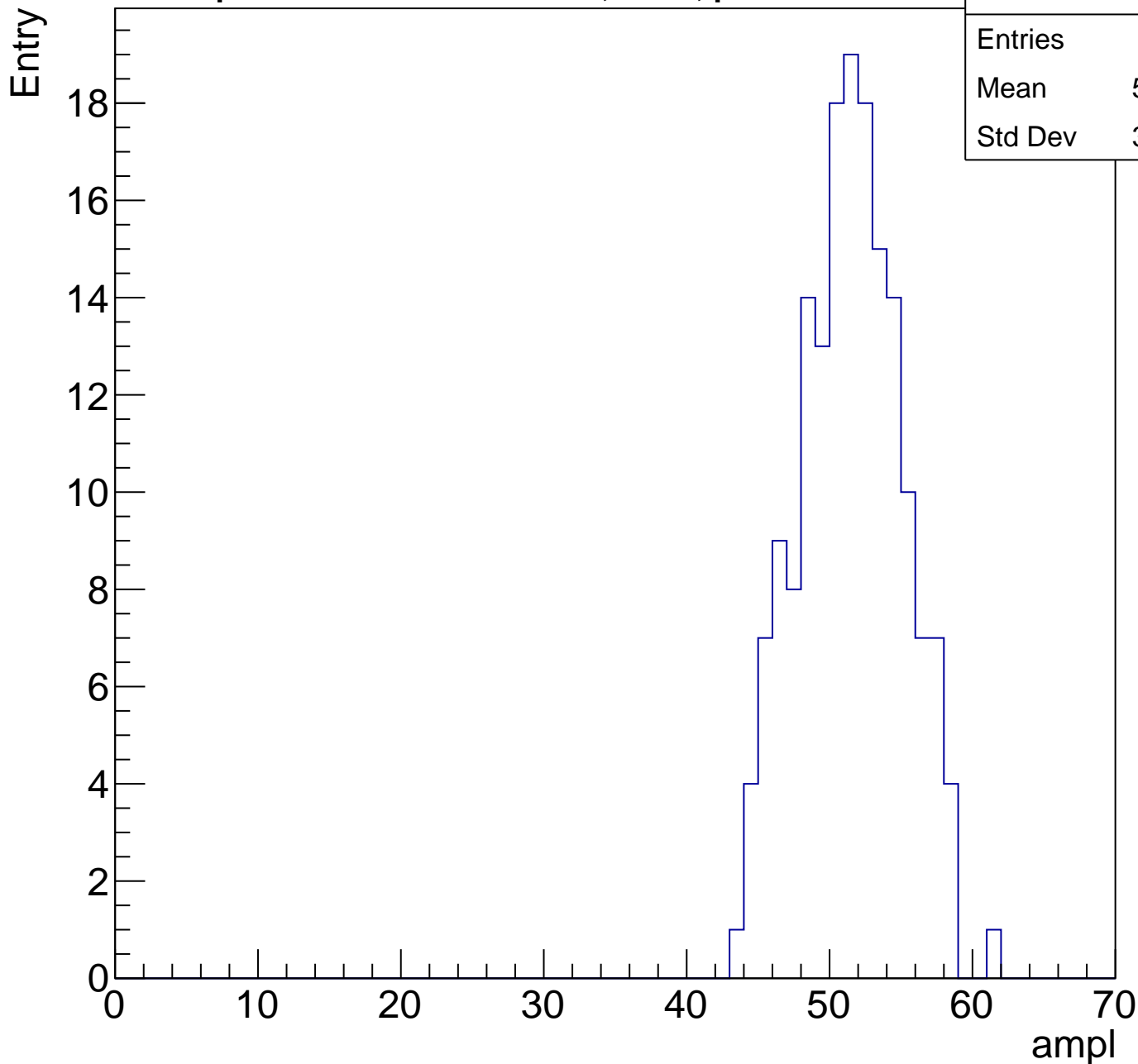
ampl



B0L101S, U19-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

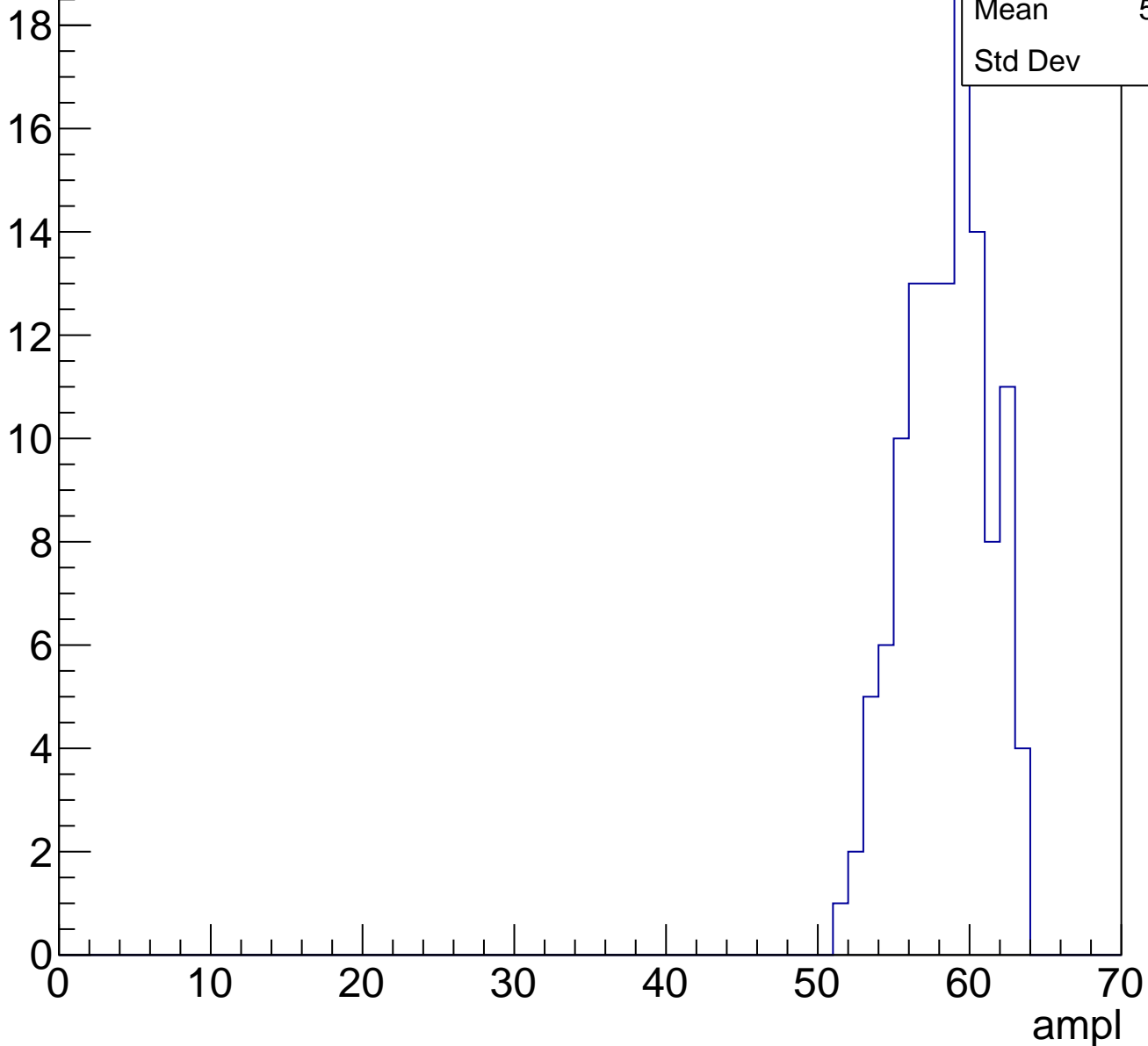
Entries	169
Mean	51.02
Std Dev	3.565



B0L101S, U19-ch27, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch27, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	42
Mean	58.52
Std Dev	13.17

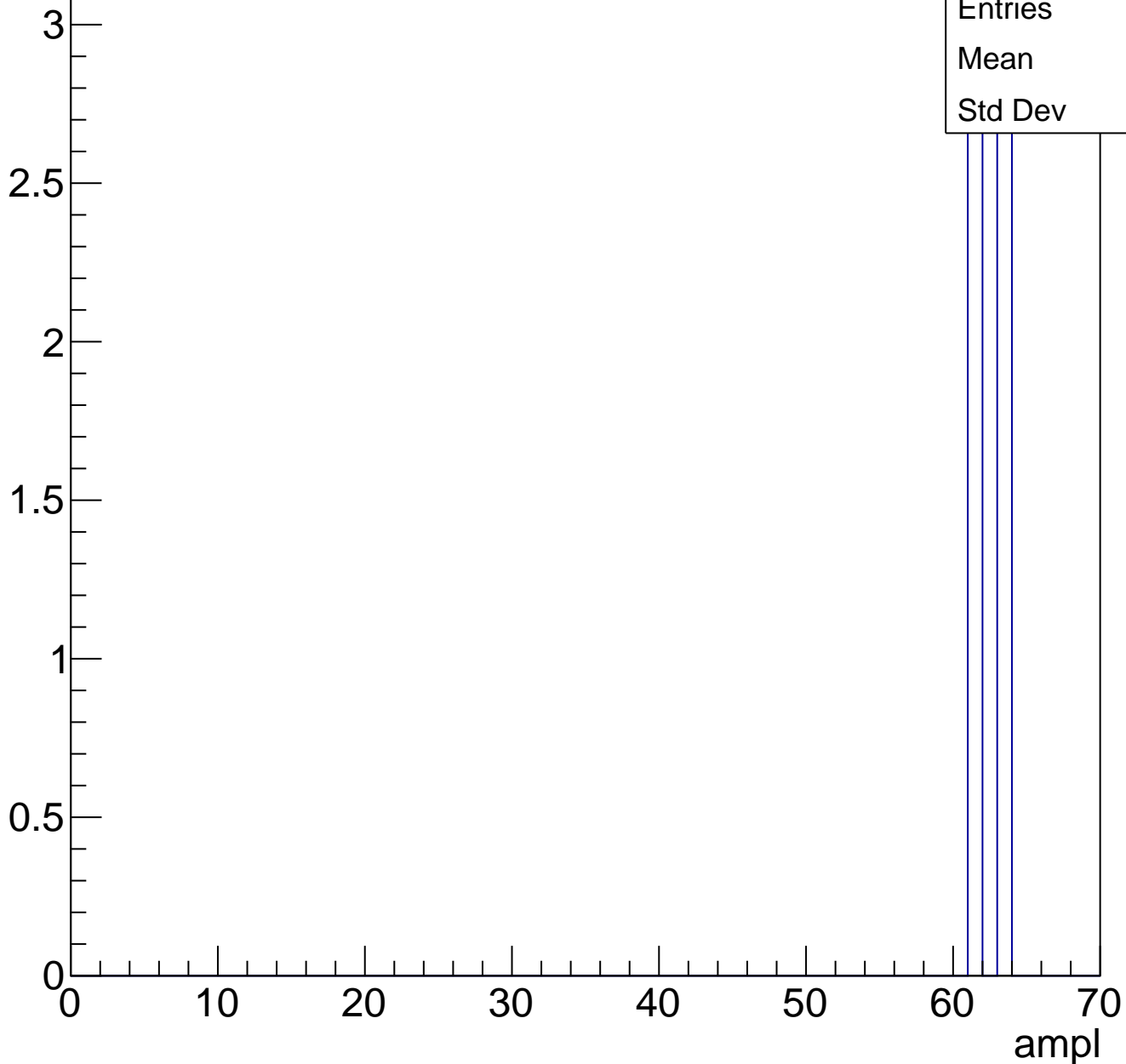
ampl

0 10 20 30 40 50 60 70

B0L101S, U19-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

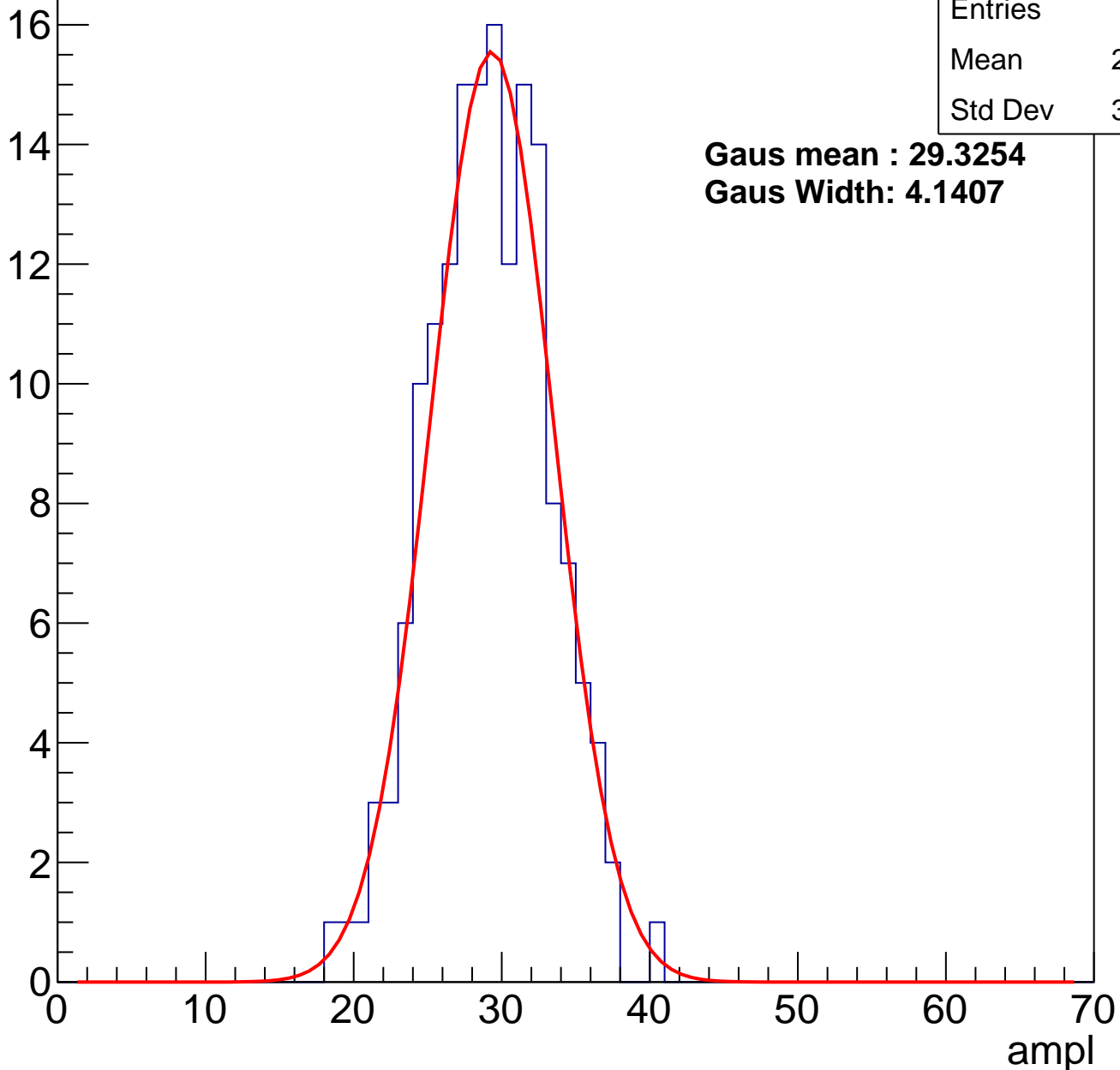


Entries	1
Mean	22
Std Dev	0

B0L101S, U19-ch28, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



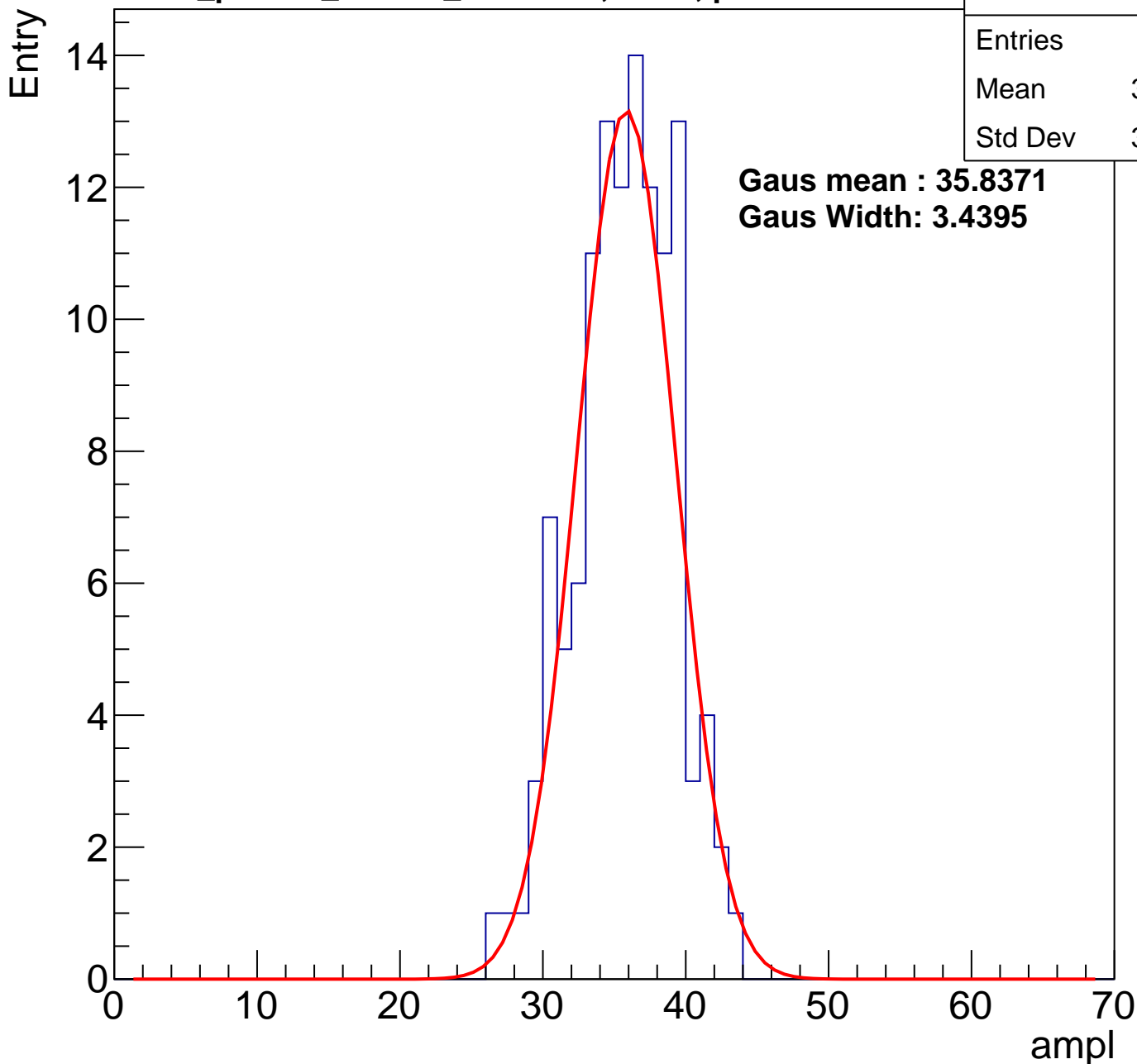
B0L101S, U19-ch28, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	35.28
Std Dev	3.426

Gaus mean : 35.8371

Gaus Width: 3.4395



B0L101S, U19-ch28, adc2

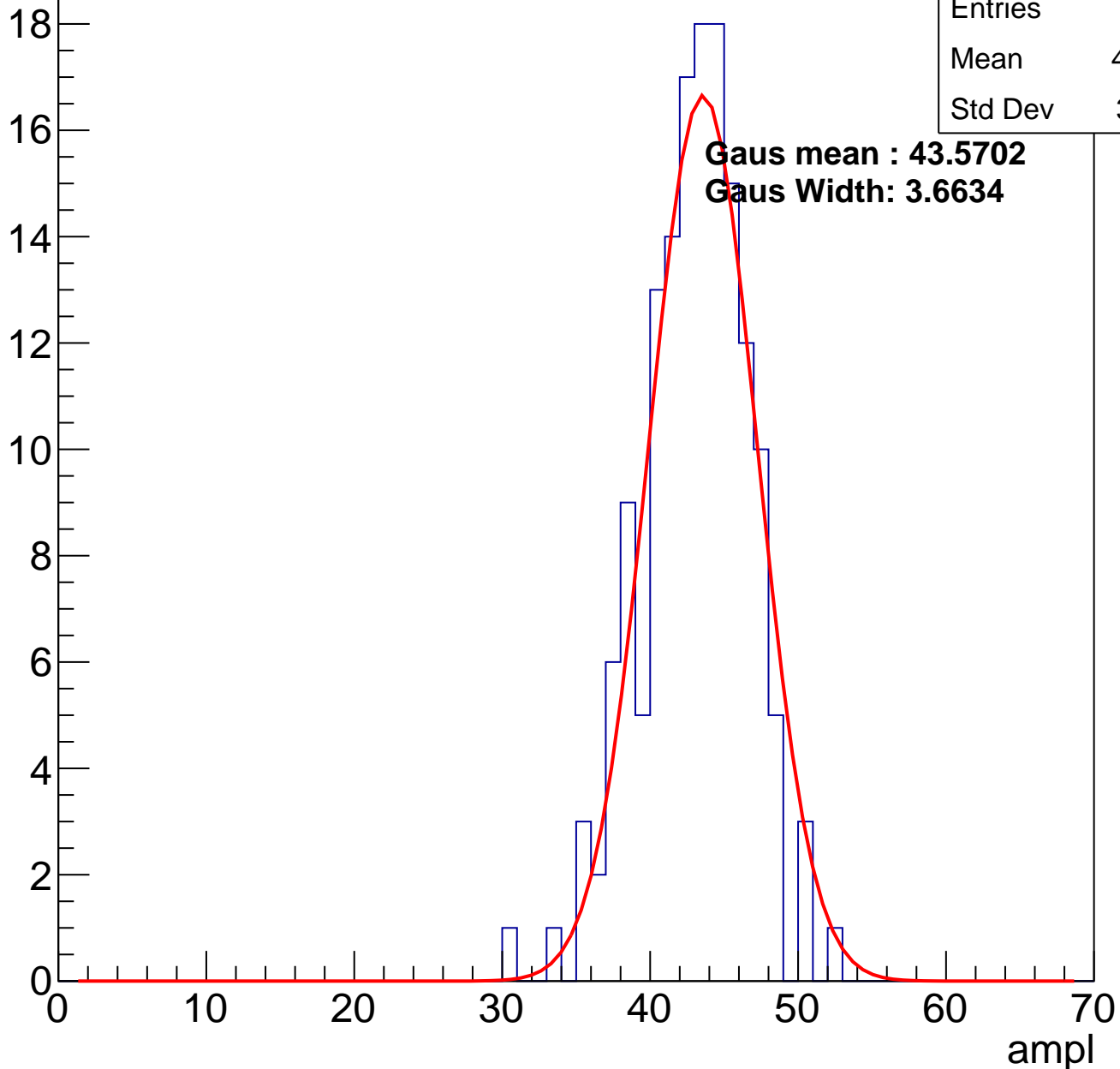
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	42.56
Std Dev	3.561

Gaus mean : 43.5702

Gaus Width: 3.6634

Entry



B0L101S, U19-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

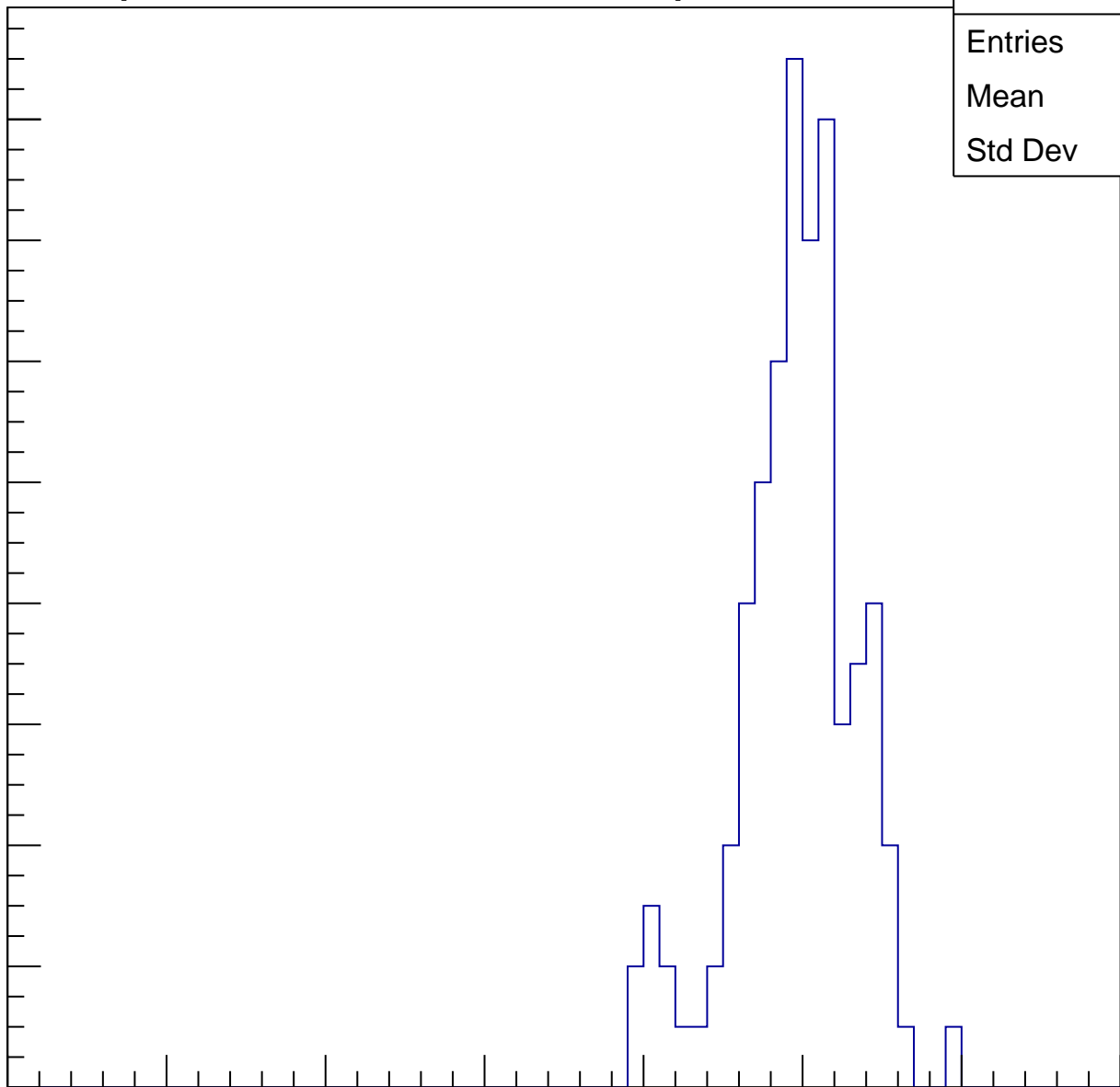
Entries	119
Mean	49.13
Std Dev	3.7

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

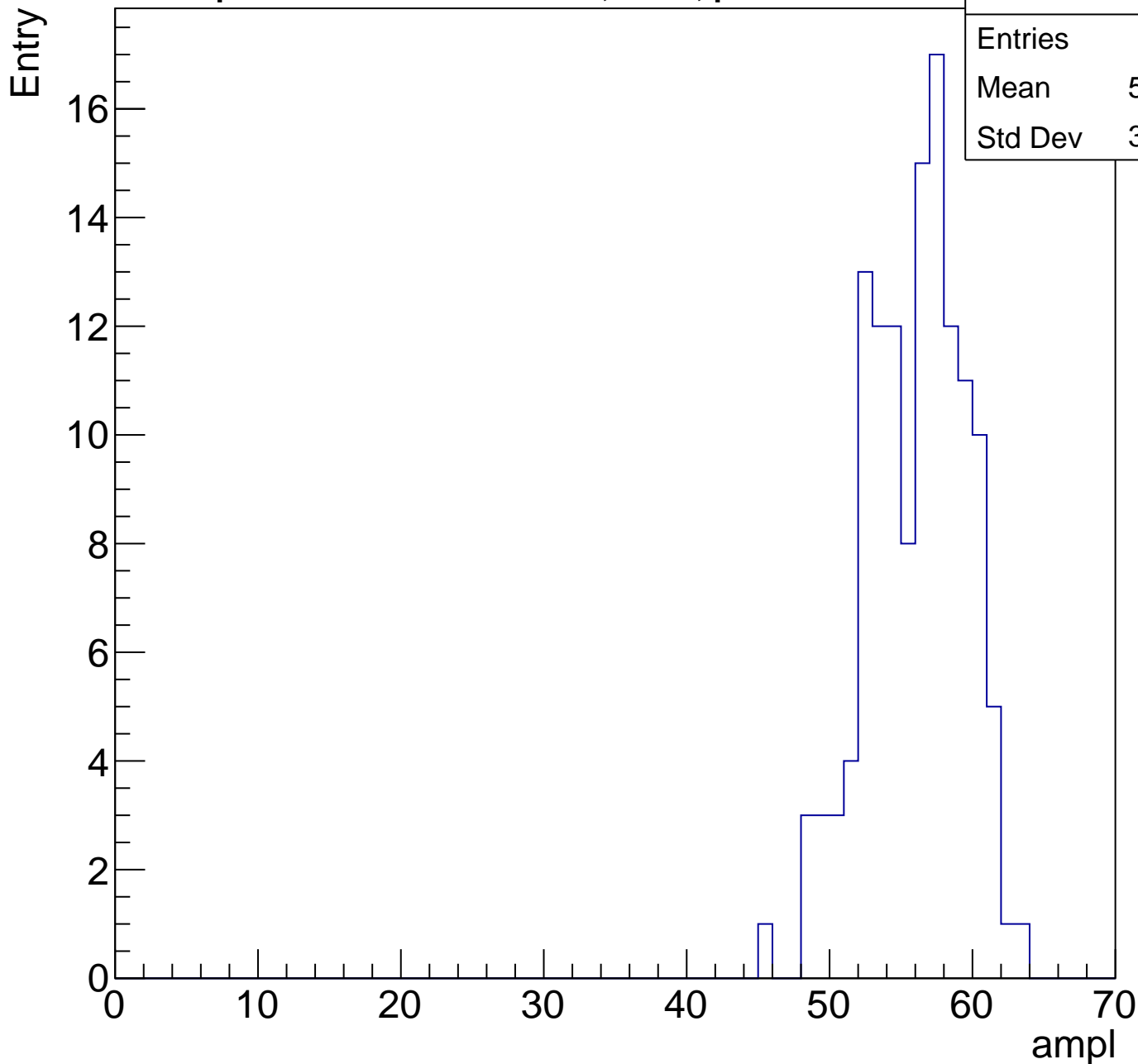
ampl



B0L101S, U19-ch28, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	55.53
Std Dev	3.434



B0L101S, U19-ch28, adc5

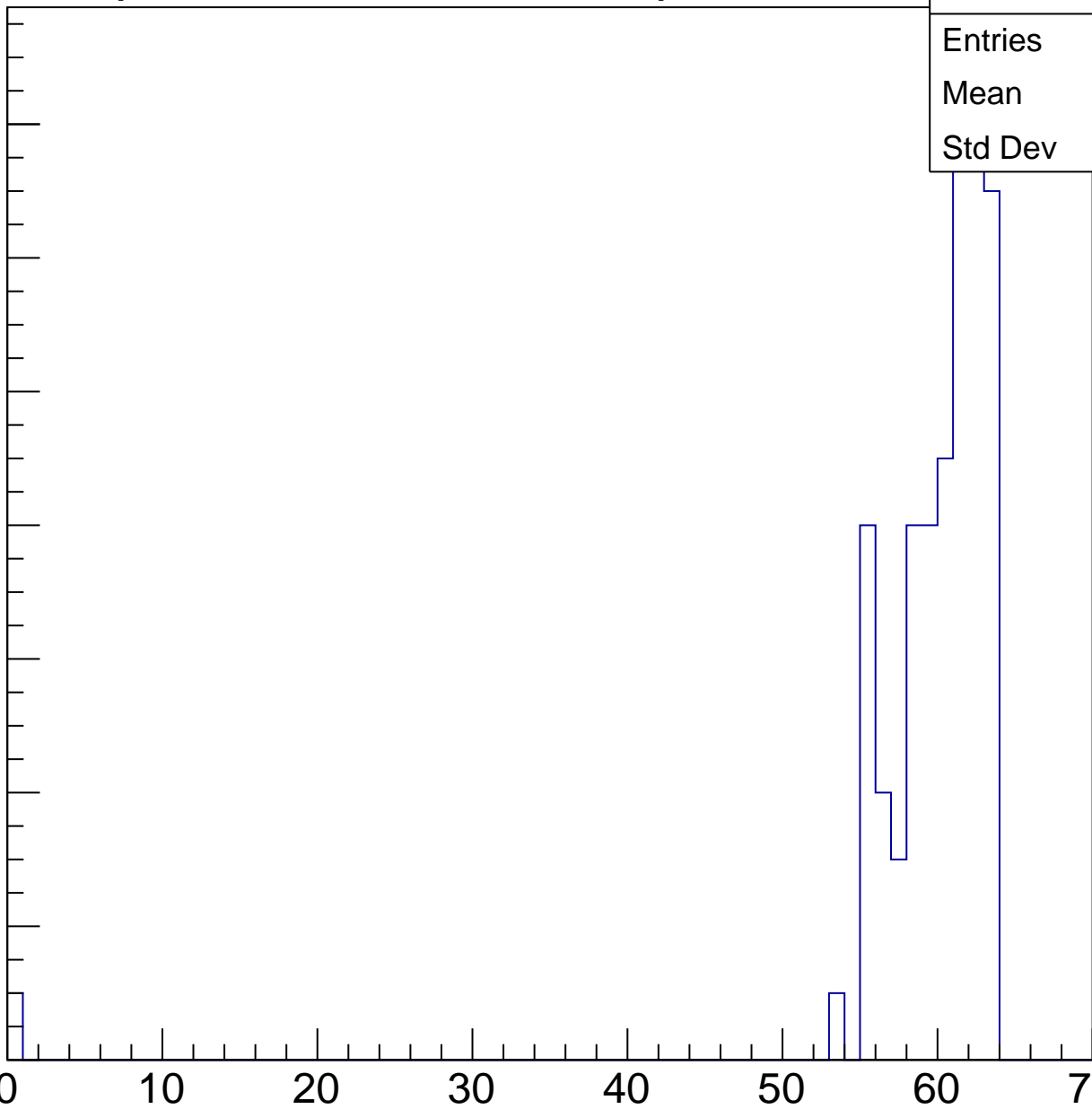
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	59.15
Std Dev	6.954

ampl

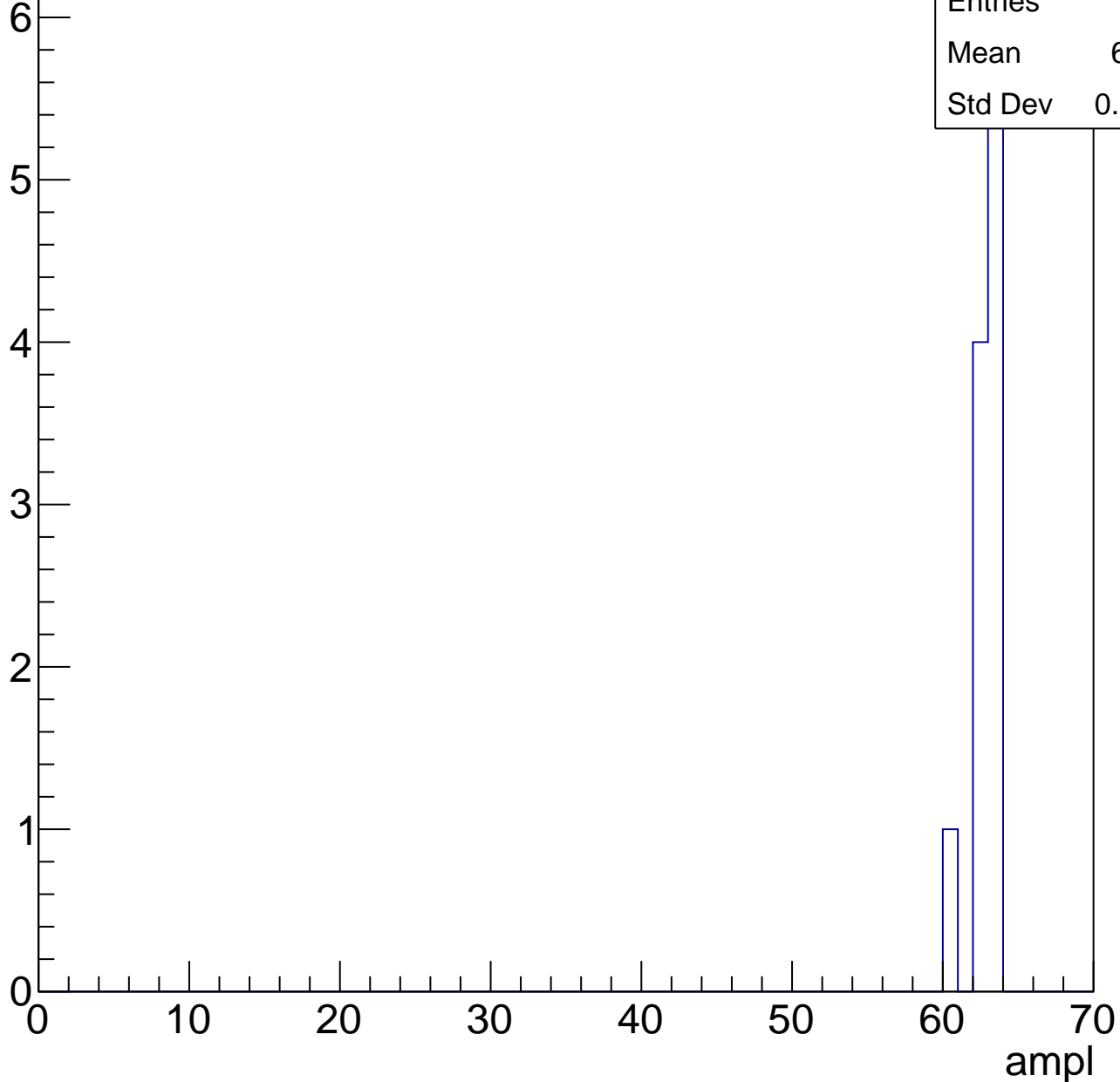


B0L101S, U19-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62.36
Std Dev	0.8814



B0L101S, U19-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch29, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	28.8
Std Dev	4.929

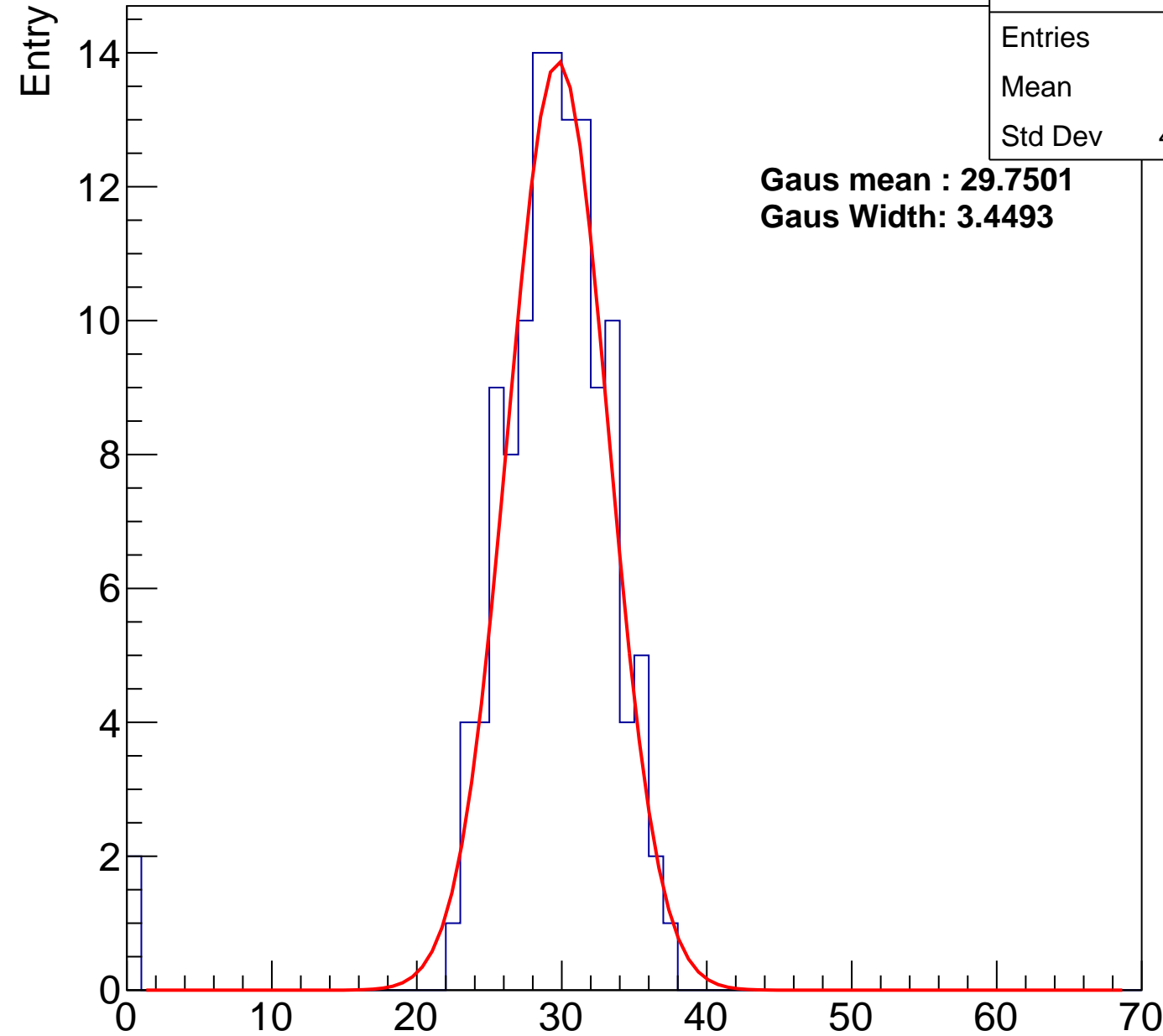
Gaus mean : 29.7501

Gaus Width: 3.4493

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch29, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	35.8
Std Dev	3.77

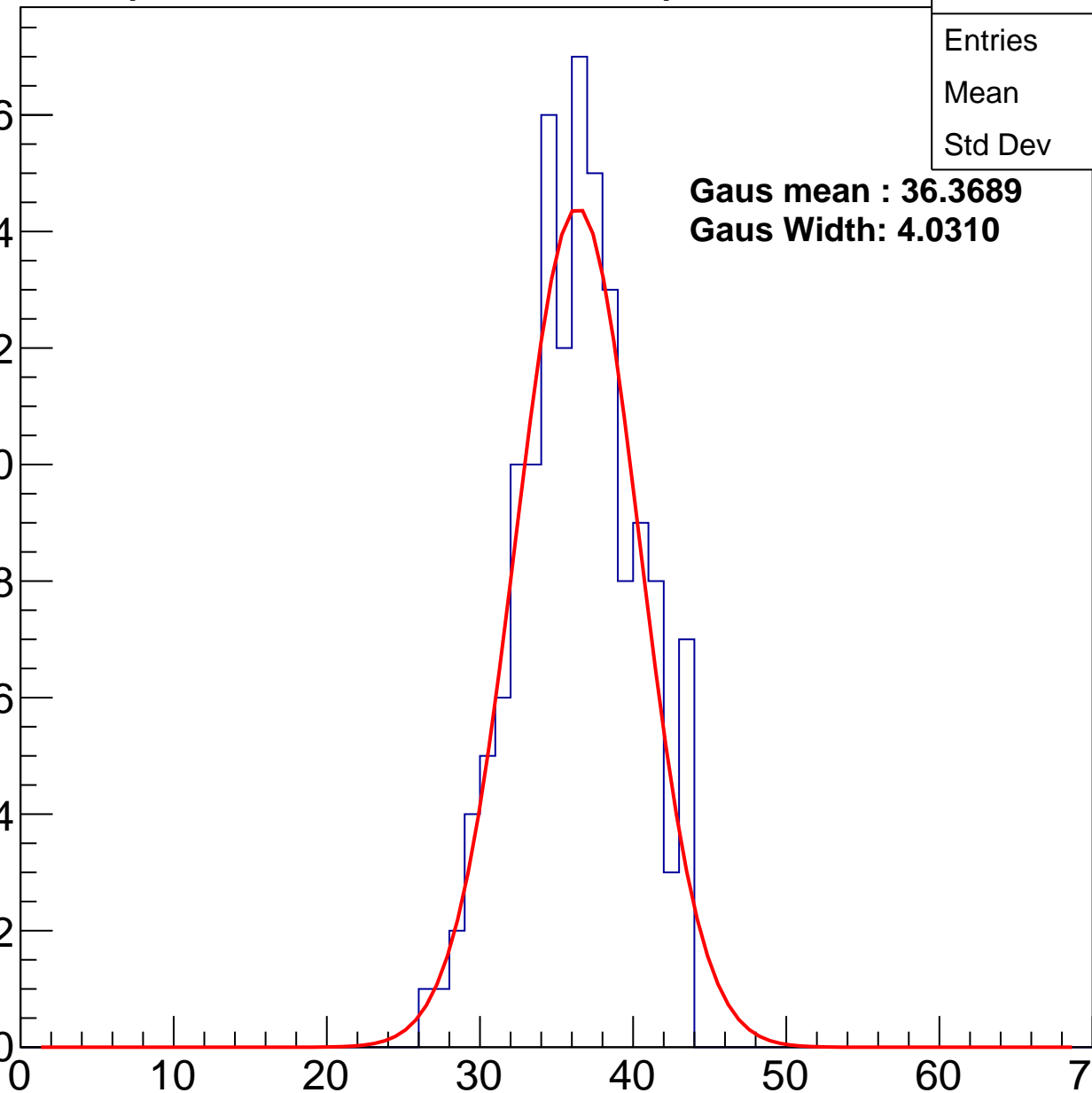
Gaus mean : 36.3689

Gaus Width: 4.0310

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch29, adc2

calib_packv5_042523_0143.root, FC#1, port C1

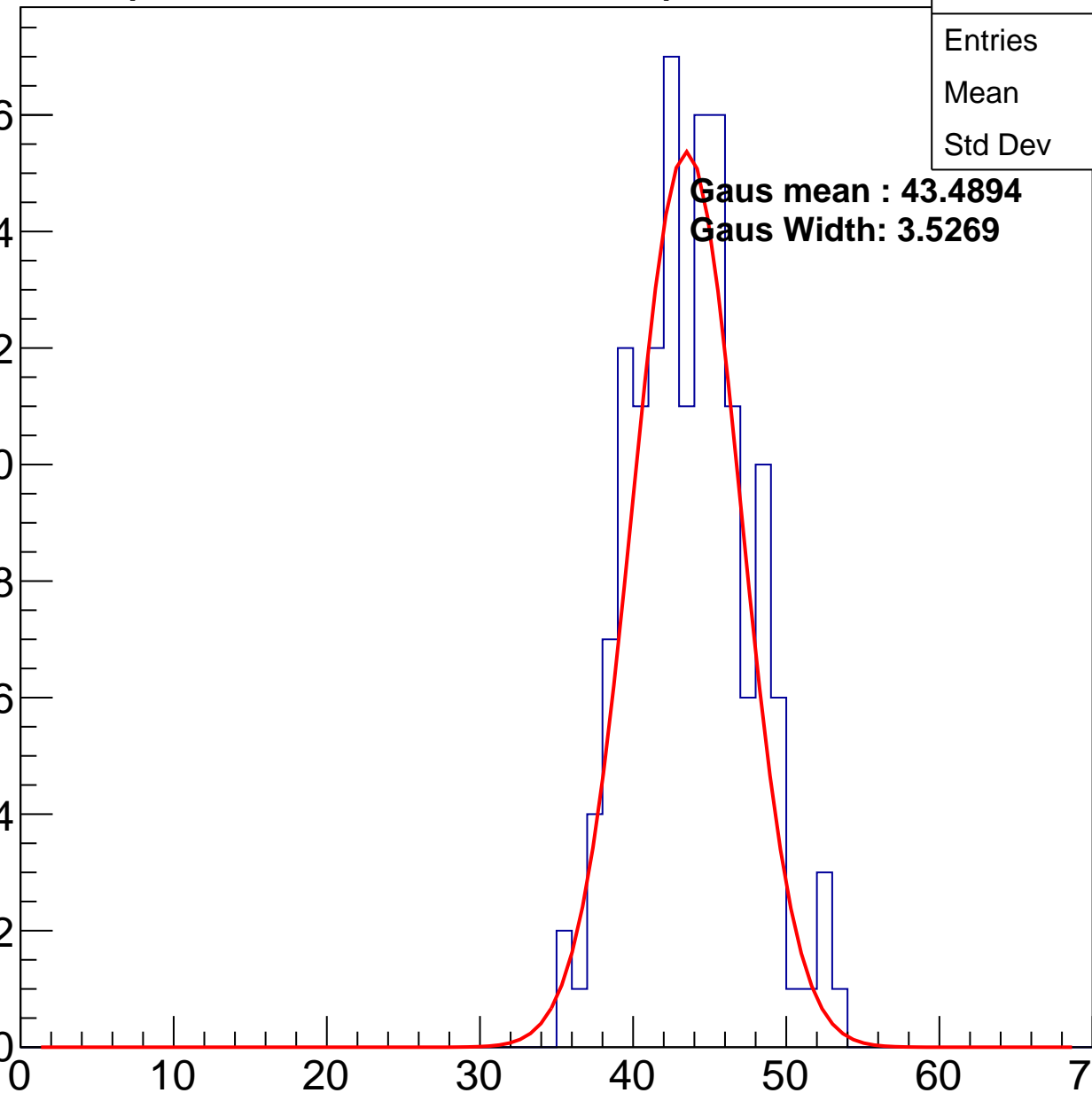
Entries	148
Mean	43.26
Std Dev	3.711

Entry

16
14
12
10
8
6
4
2
0

Gaus mean : 43.4894
Gaus Width: 3.5269

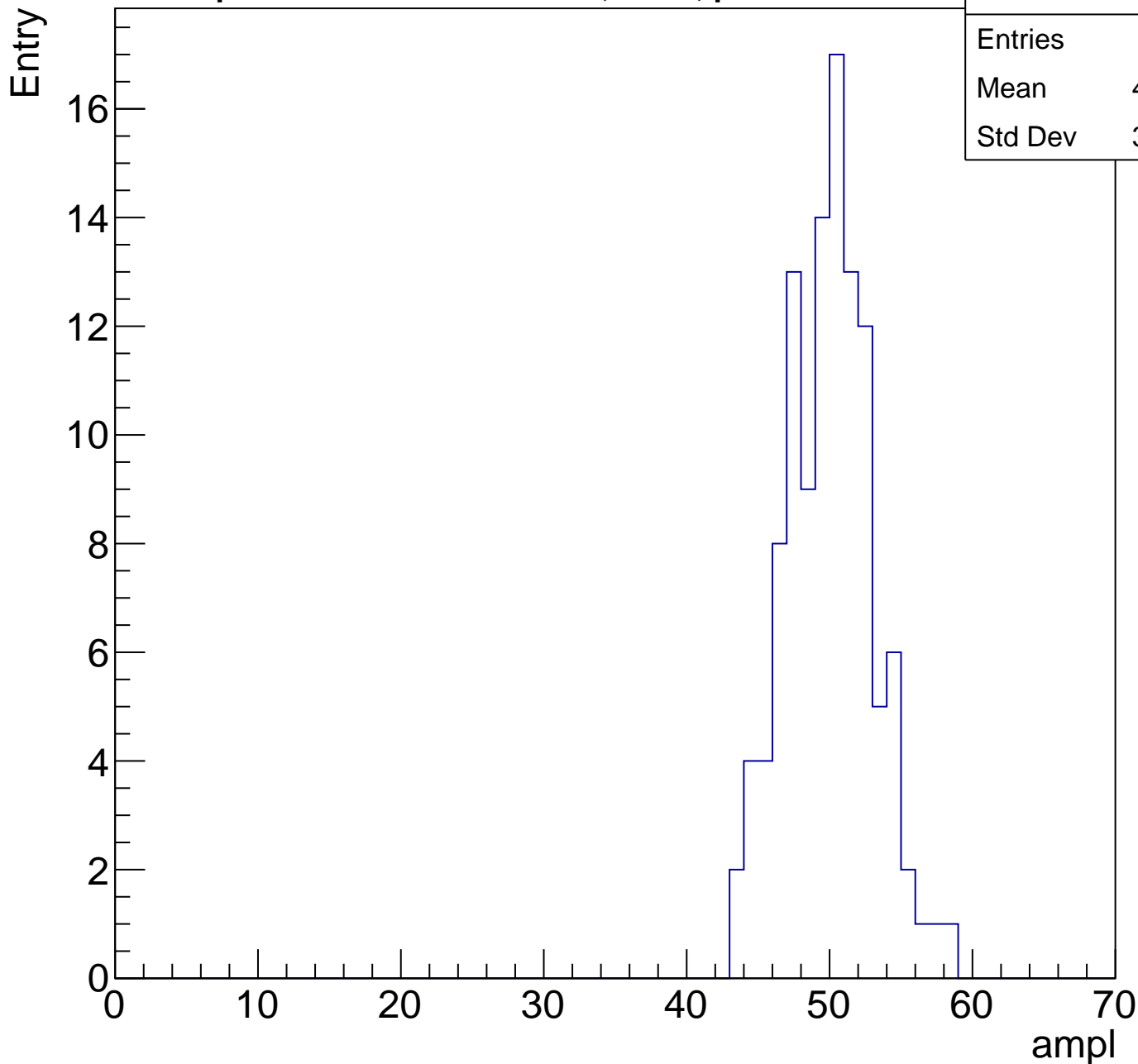
ampl



B0L101S, U19-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	49.52
Std Dev	3.006



B0L101S, U19-ch29, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	55.9
Std Dev	3.325

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

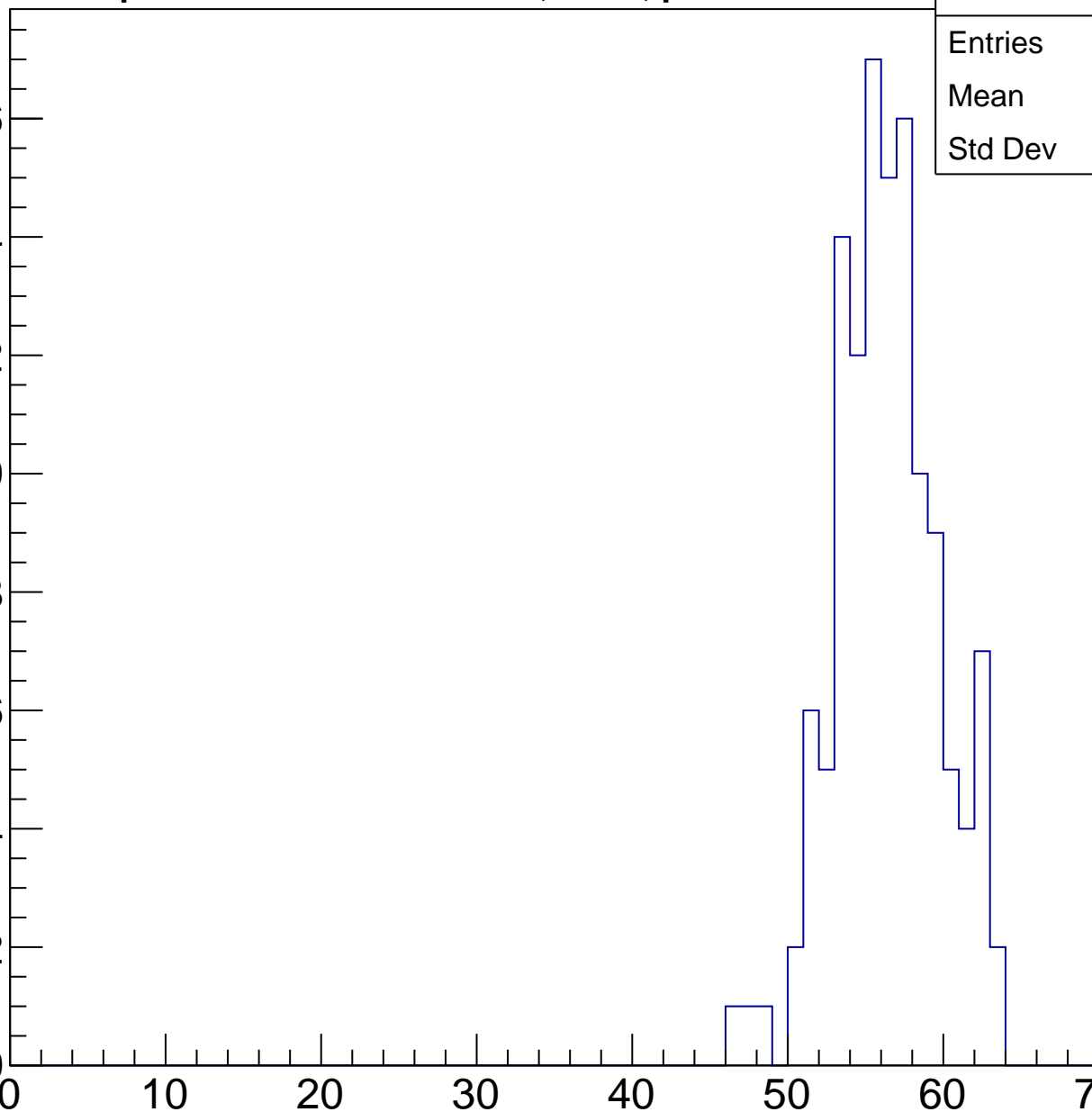
40

50

60

70

ampl



B0L101S, U19-ch29, adc5

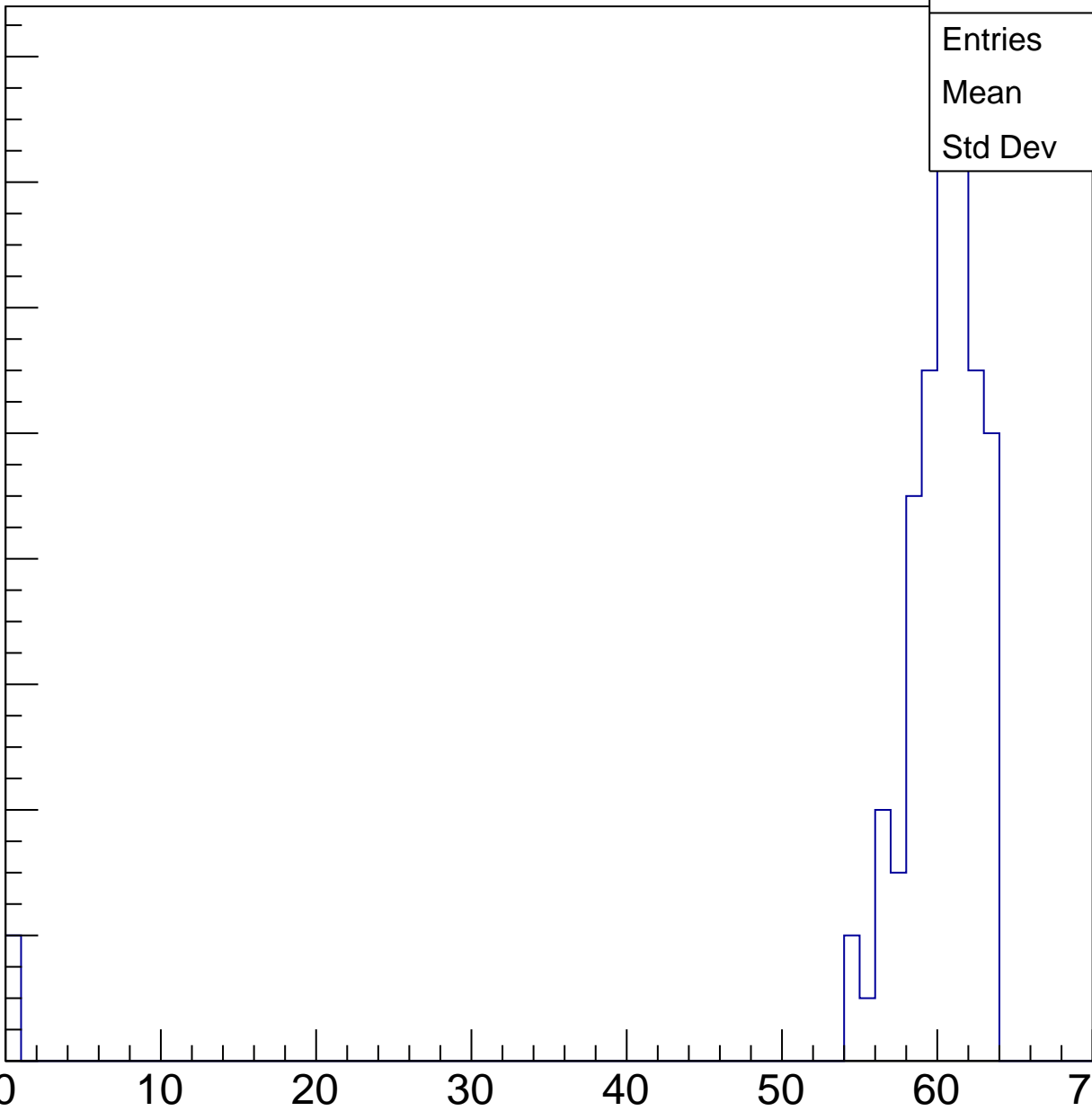
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.54
Std Dev	9.389

ampl



B0L101S, U19-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	10
Mean	62.8
Std Dev	0.4

ampl

0 10 20 30 40 50 60 70

B0L101S, U19-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

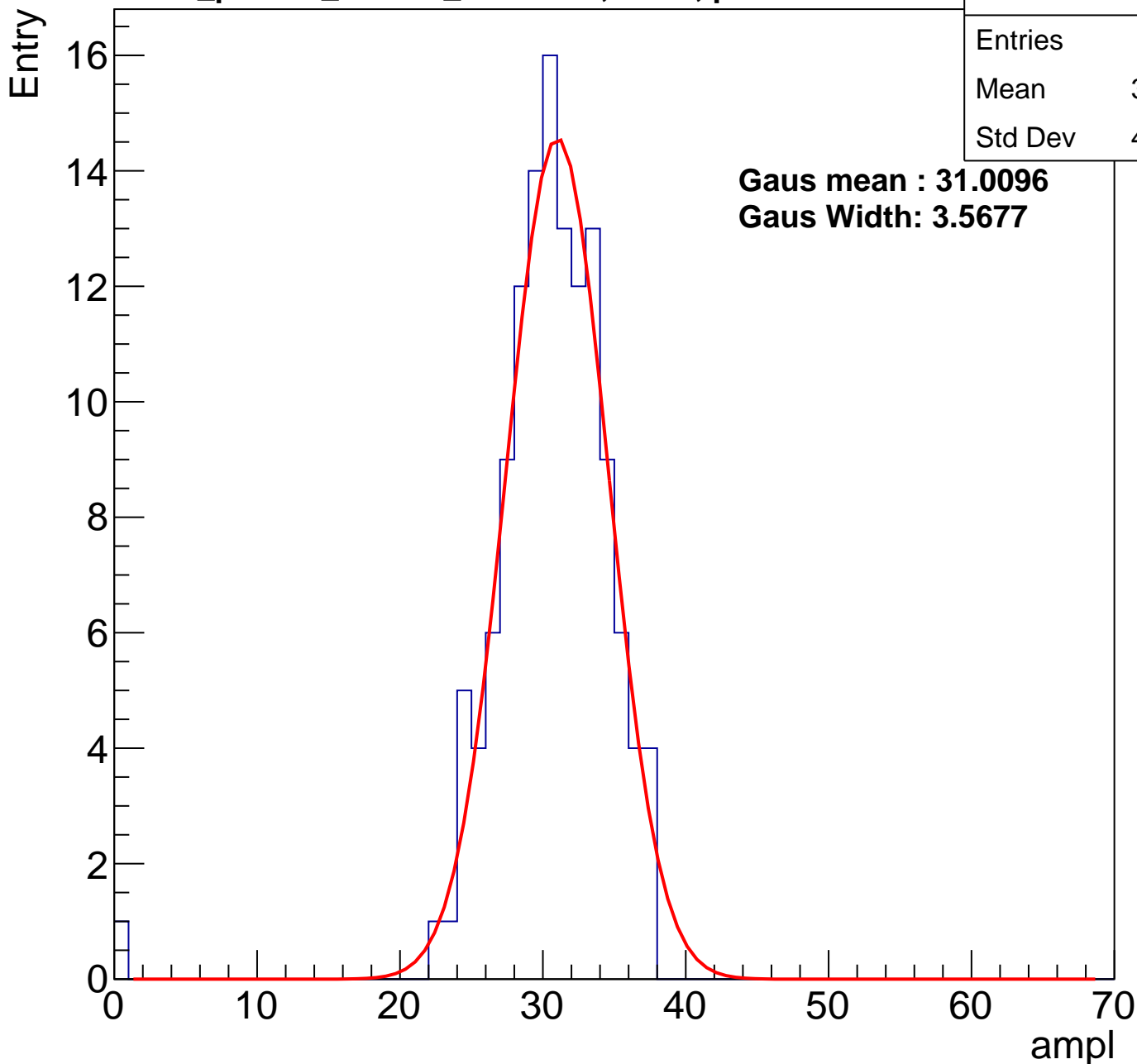
B0L101S, U19-ch30, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	30.08
Std Dev	4.246

Gaus mean : 31.0096

Gaus Width: 3.5677



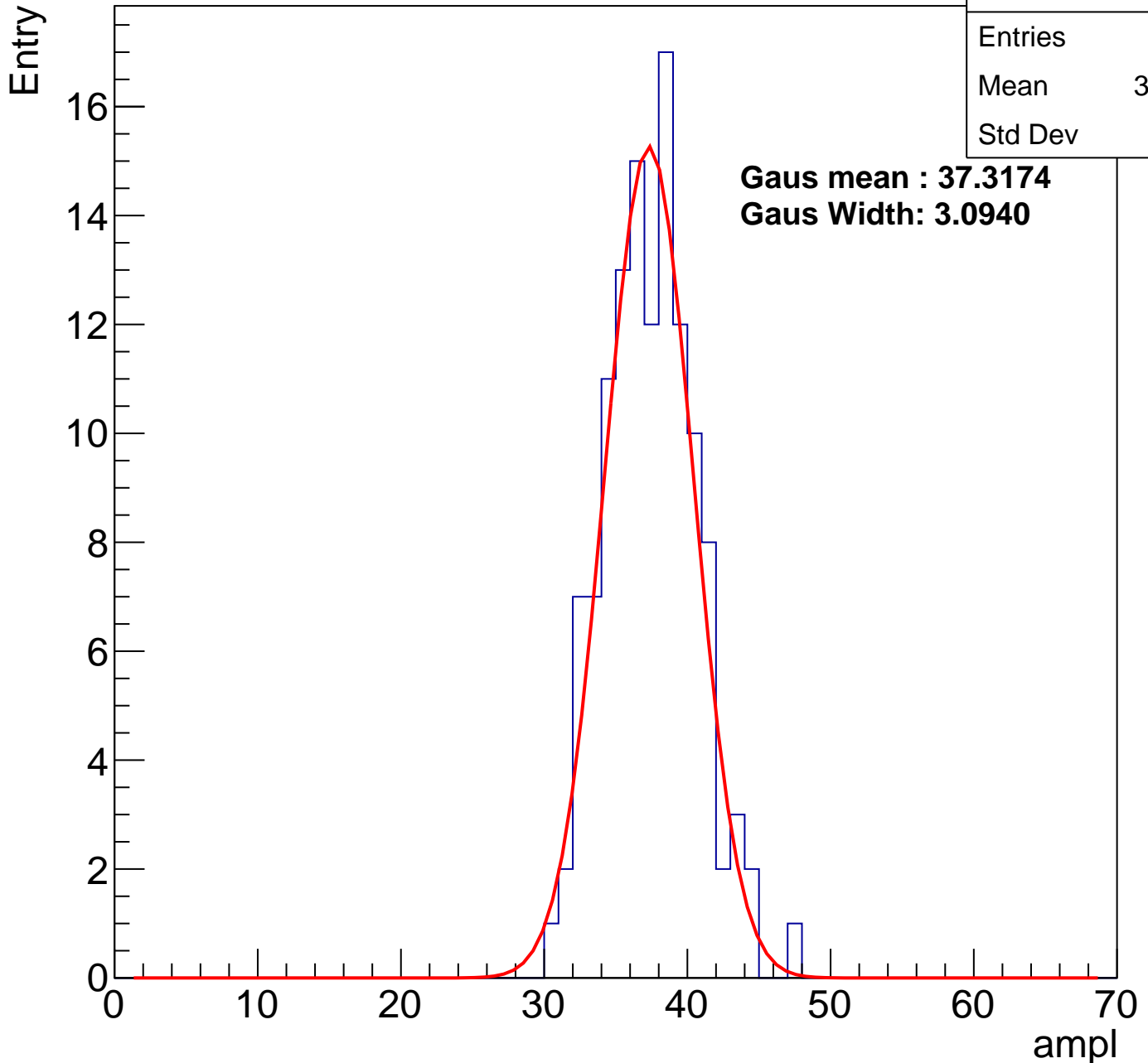
B0L101S, U19-ch30, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	36.99
Std Dev	3.14

Gaus mean : 37.3174

Gaus Width: 3.0940



B0L101S, U19-ch30, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

43.55

Std Dev

3.264

Gaus mean : 44.0041

Gaus Width: 3.3857

0

10

20

30

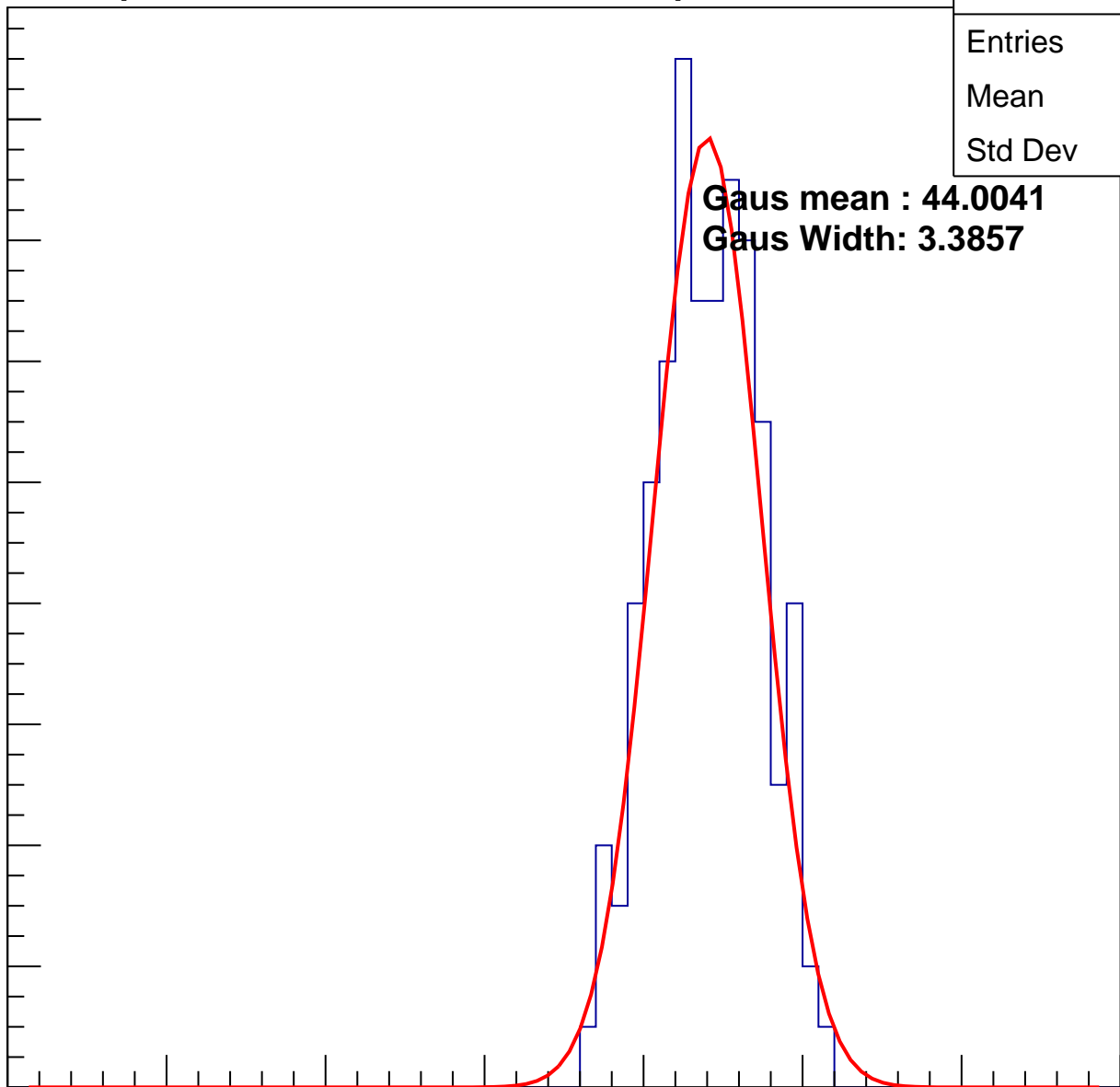
40

50

60

70

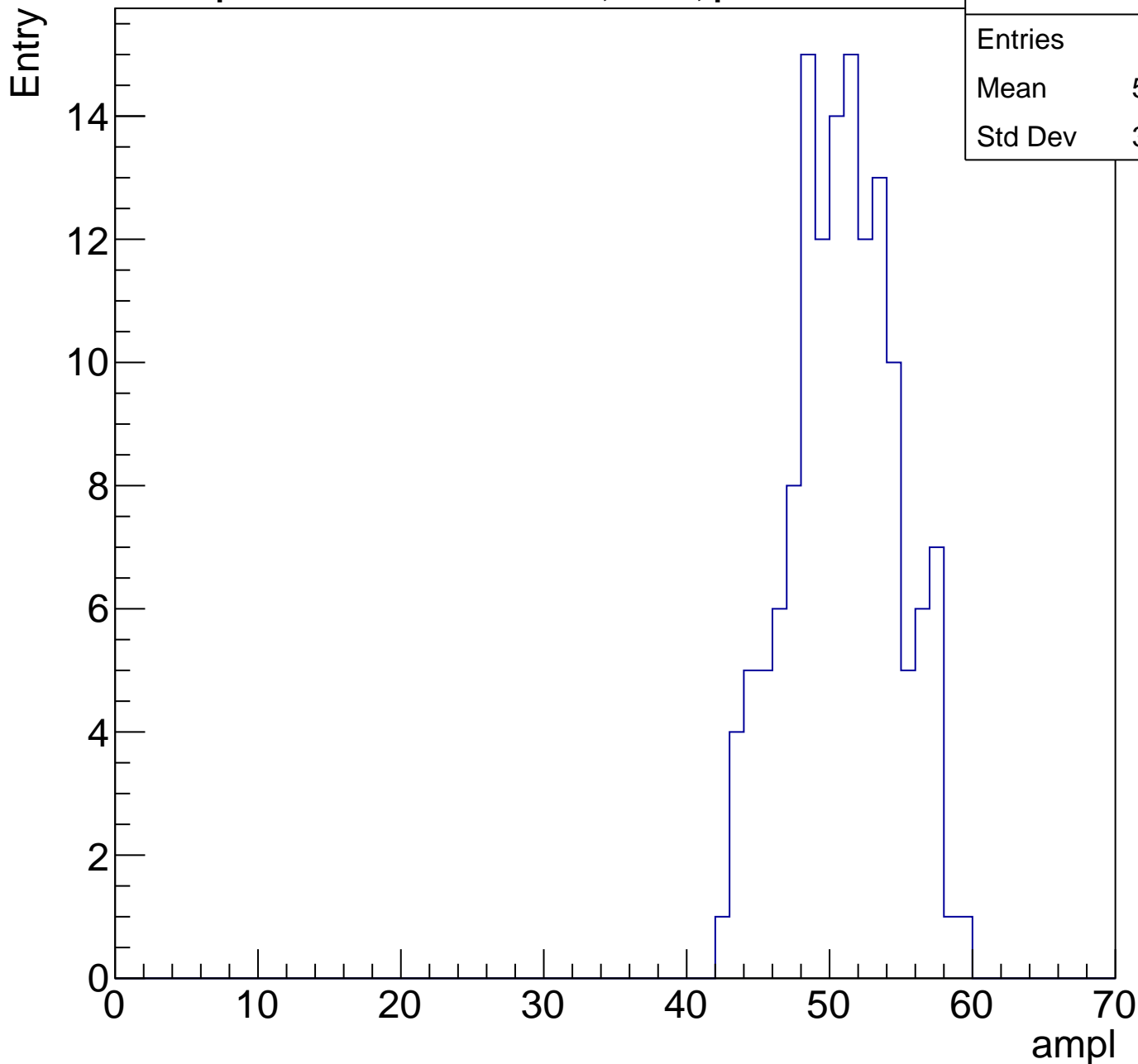
ampl



B0L101S, U19-ch30, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	50.46
Std Dev	3.727



B0L101S, U19-ch30, adc4

calib_packv5_042523_0143.root, FC#1, port C1

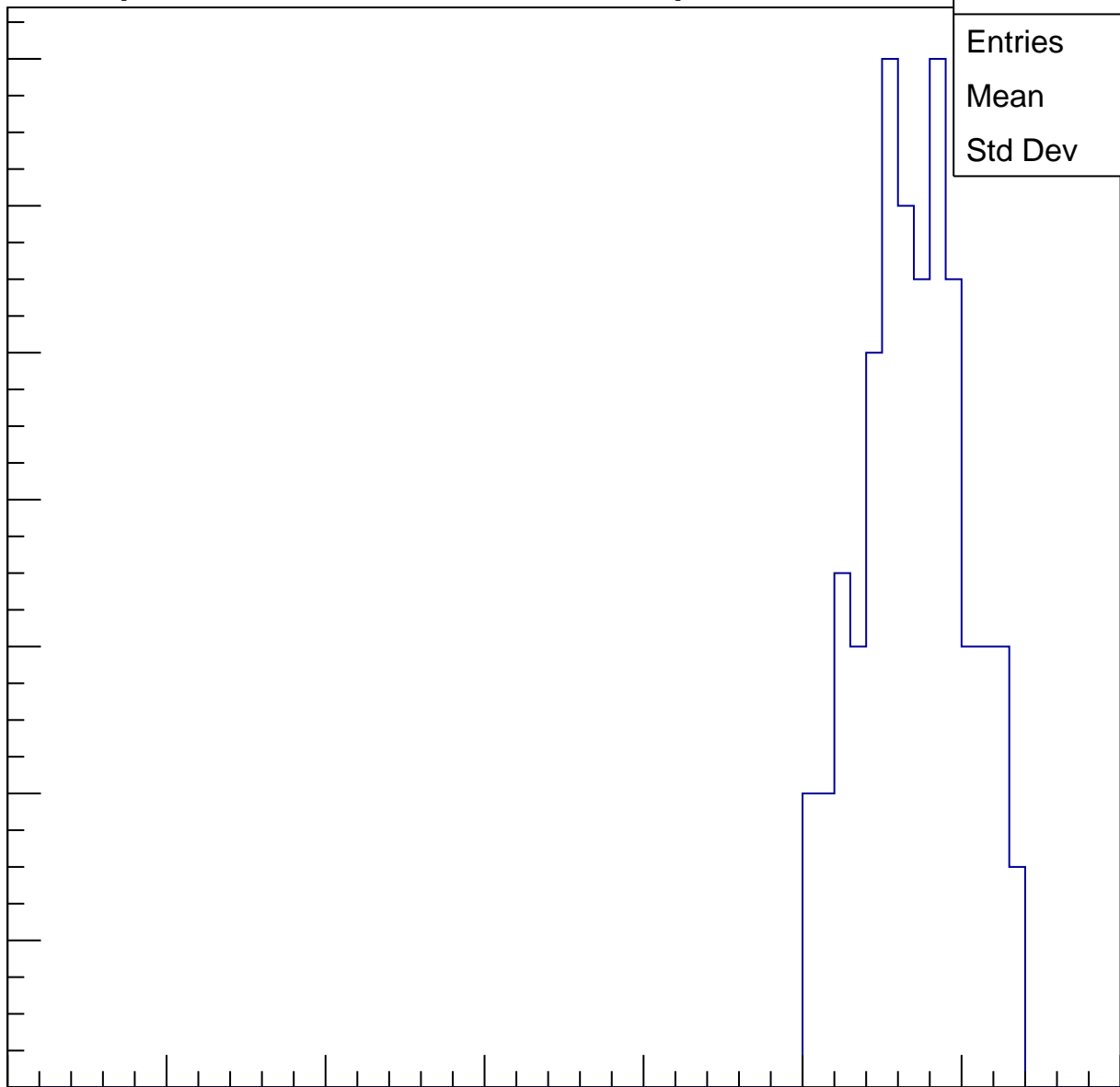
Entries	114
Mean	56.52
Std Dev	3.256

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch30, adc5

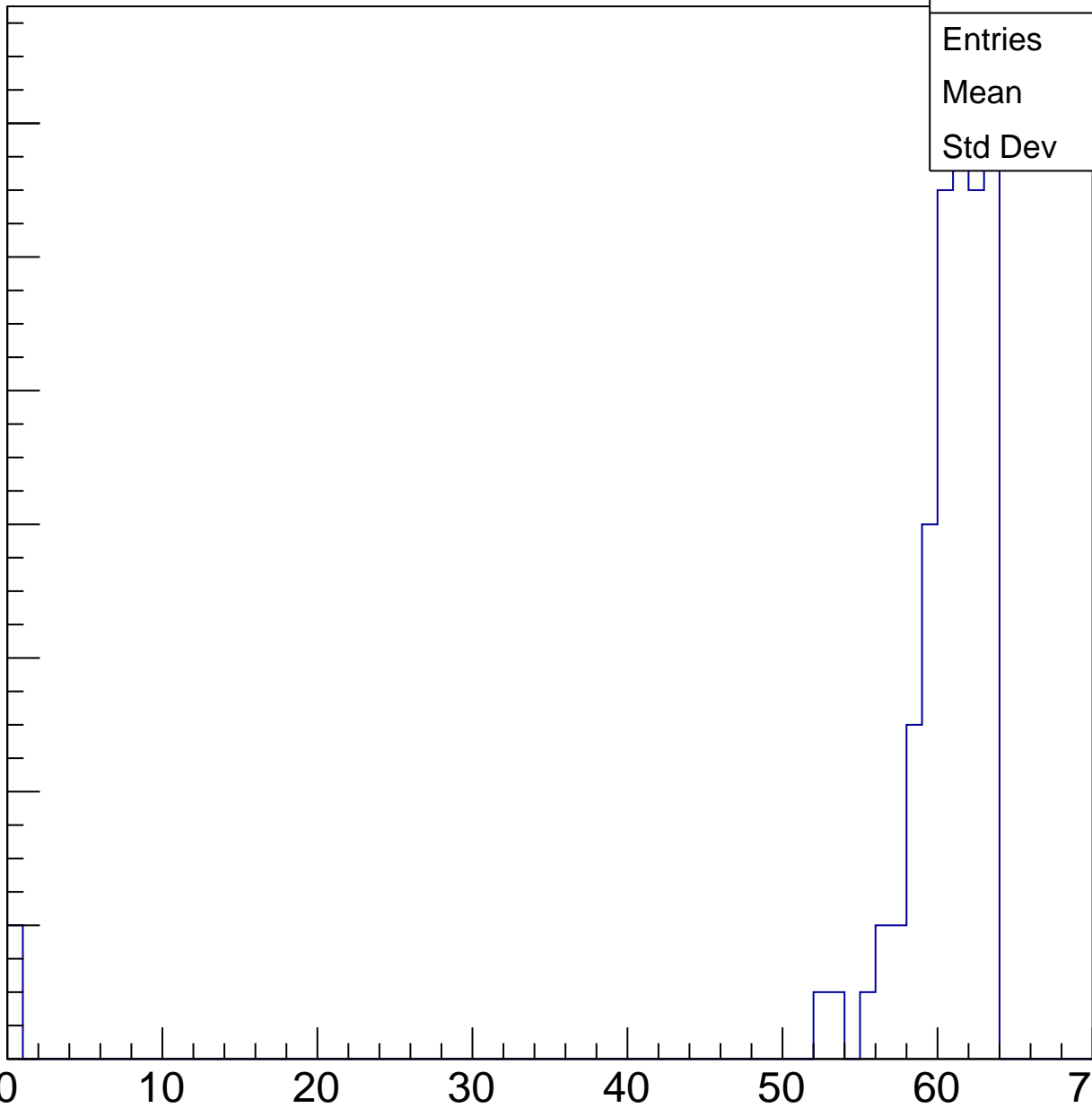
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	58.87
Std Dev	9.882

ampl



B0L101S, U19-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch31, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	29.12
Std Dev	4.18

Gaus mean : 29.4303

Gaus Width: 3.2717

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

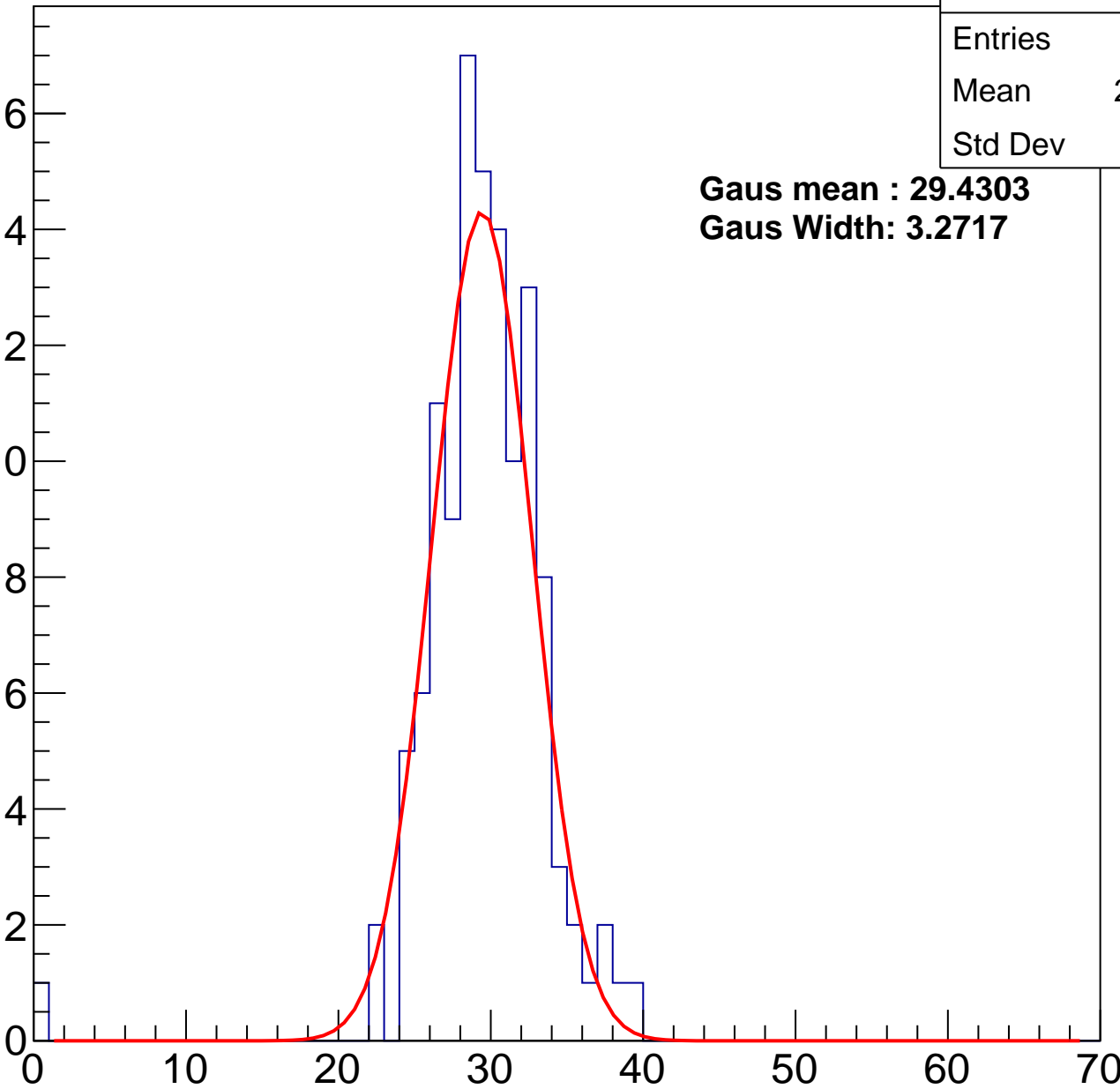
40

50

60

70

ampl



B0L101S, U19-ch31, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	36.06
Std Dev	3.746

Gaus mean : 36.6217

Gaus Width: 3.8746

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

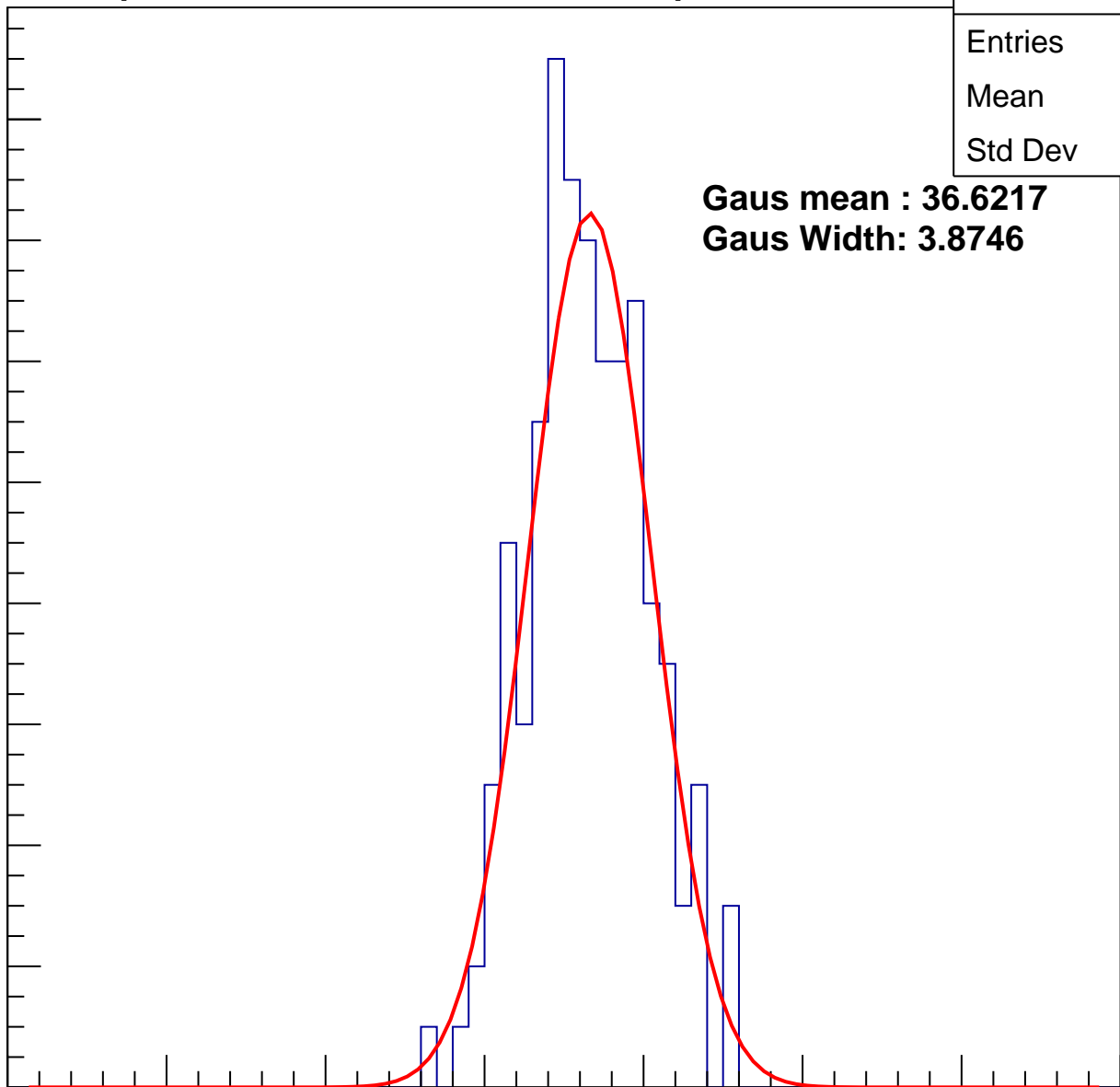
40

50

60

70

ampl



B0L101S, U19-ch31, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	43.17
Std Dev	3.843

Gaus mean : 43.5457

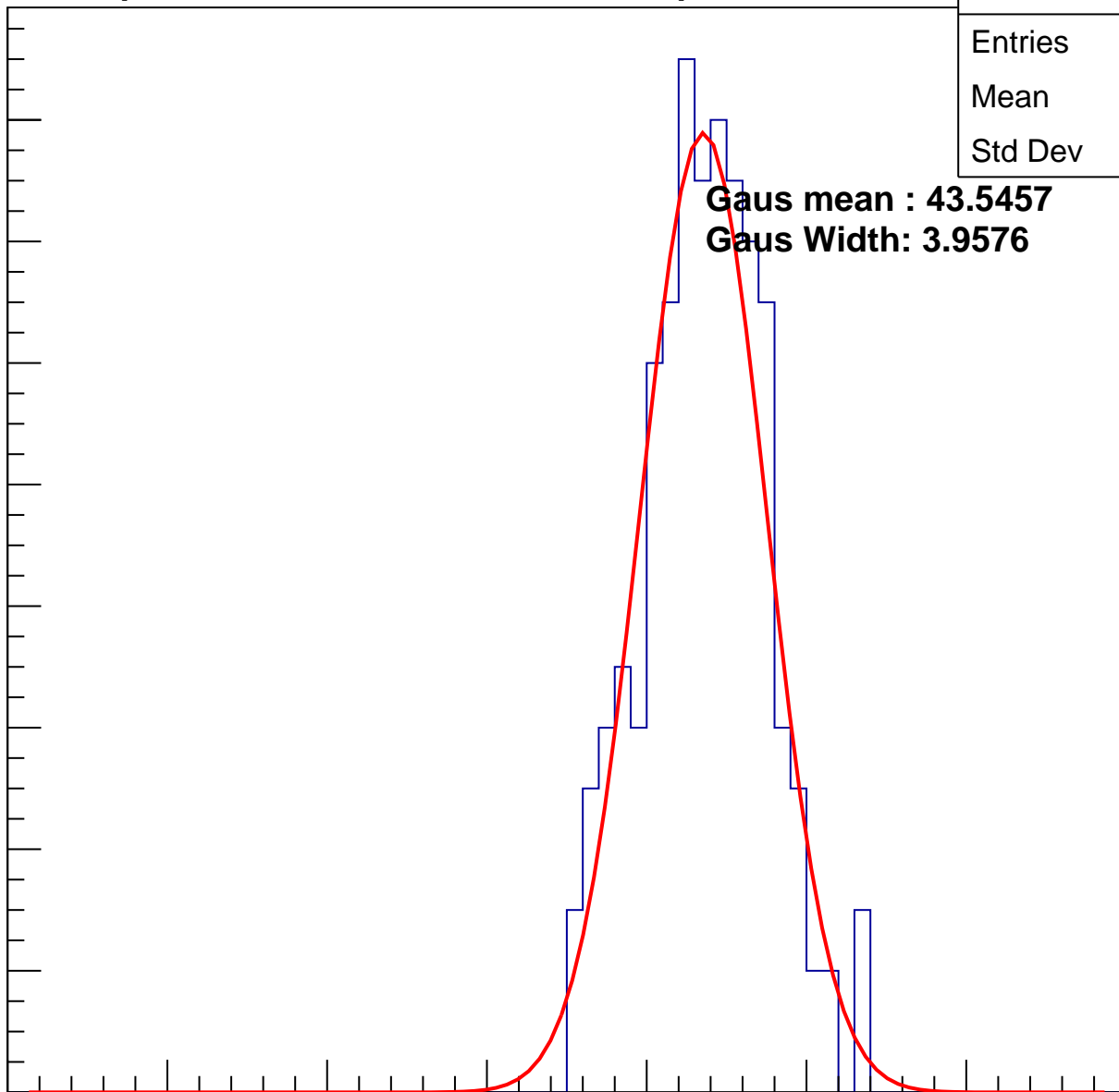
Gaus Width: 3.9576

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

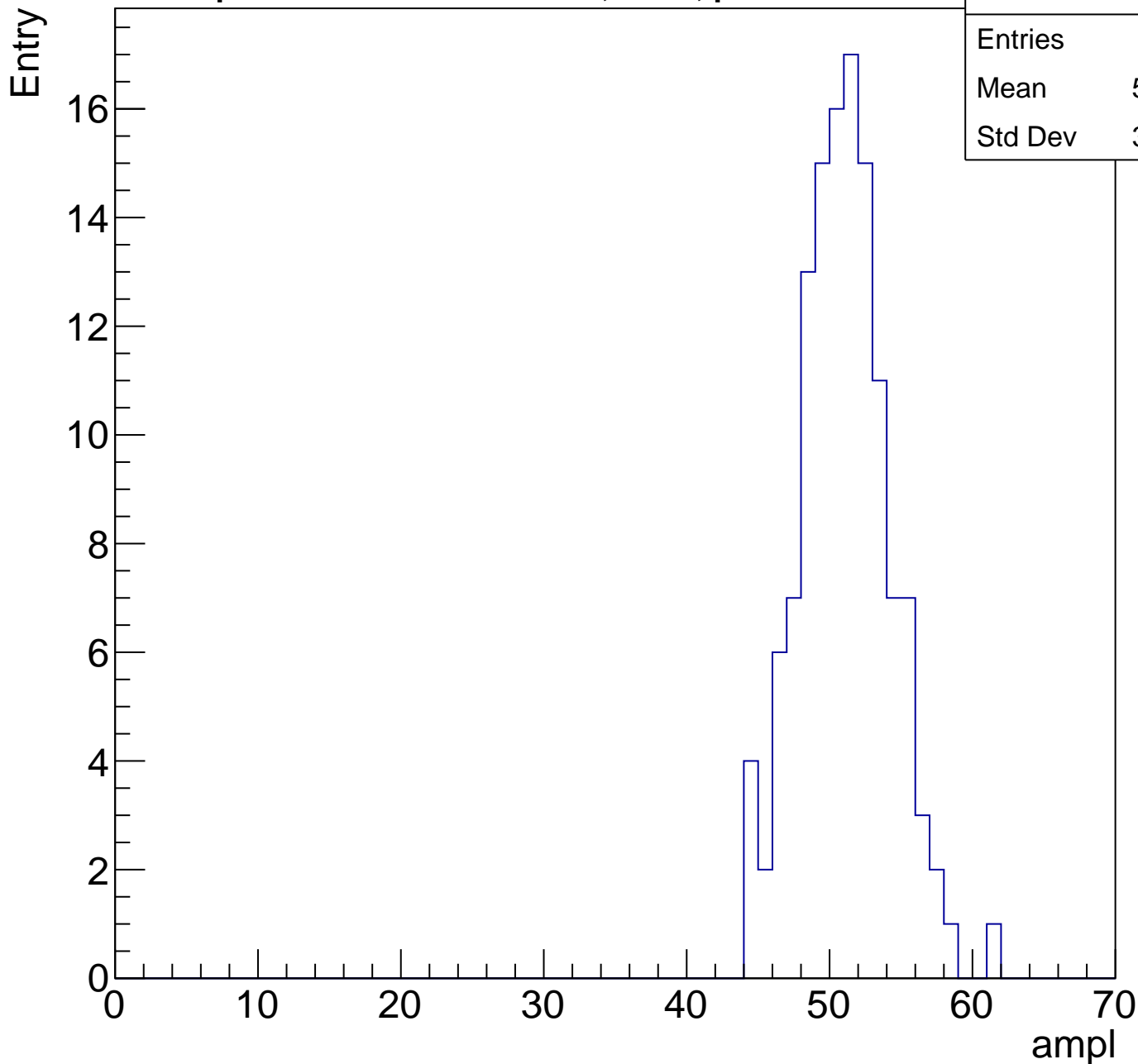
ampl



B0L101S, U19-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	50.58
Std Dev	3.113



B0L101S, U19-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

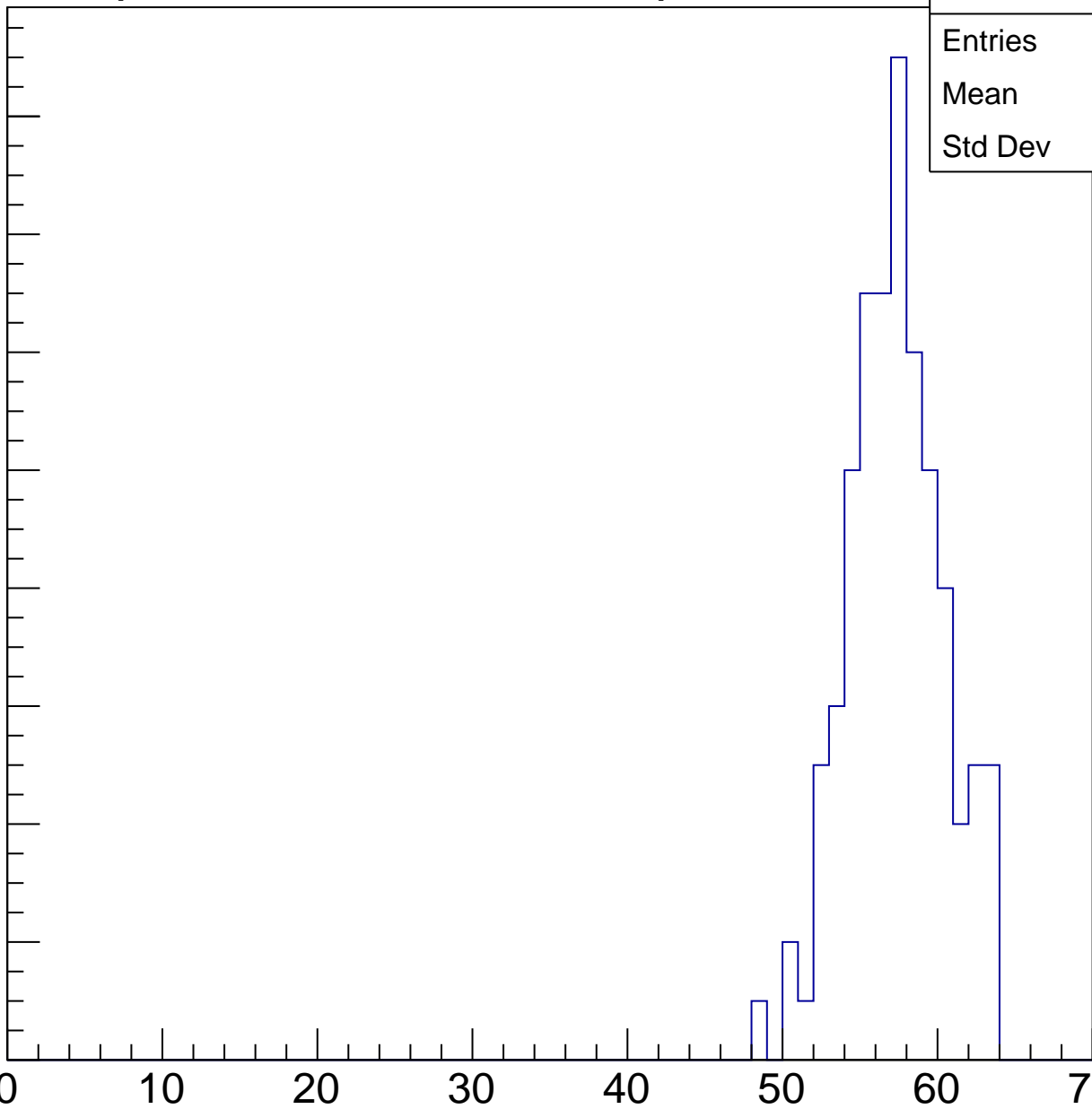
Entries	112
Mean	56.82
Std Dev	3.106

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

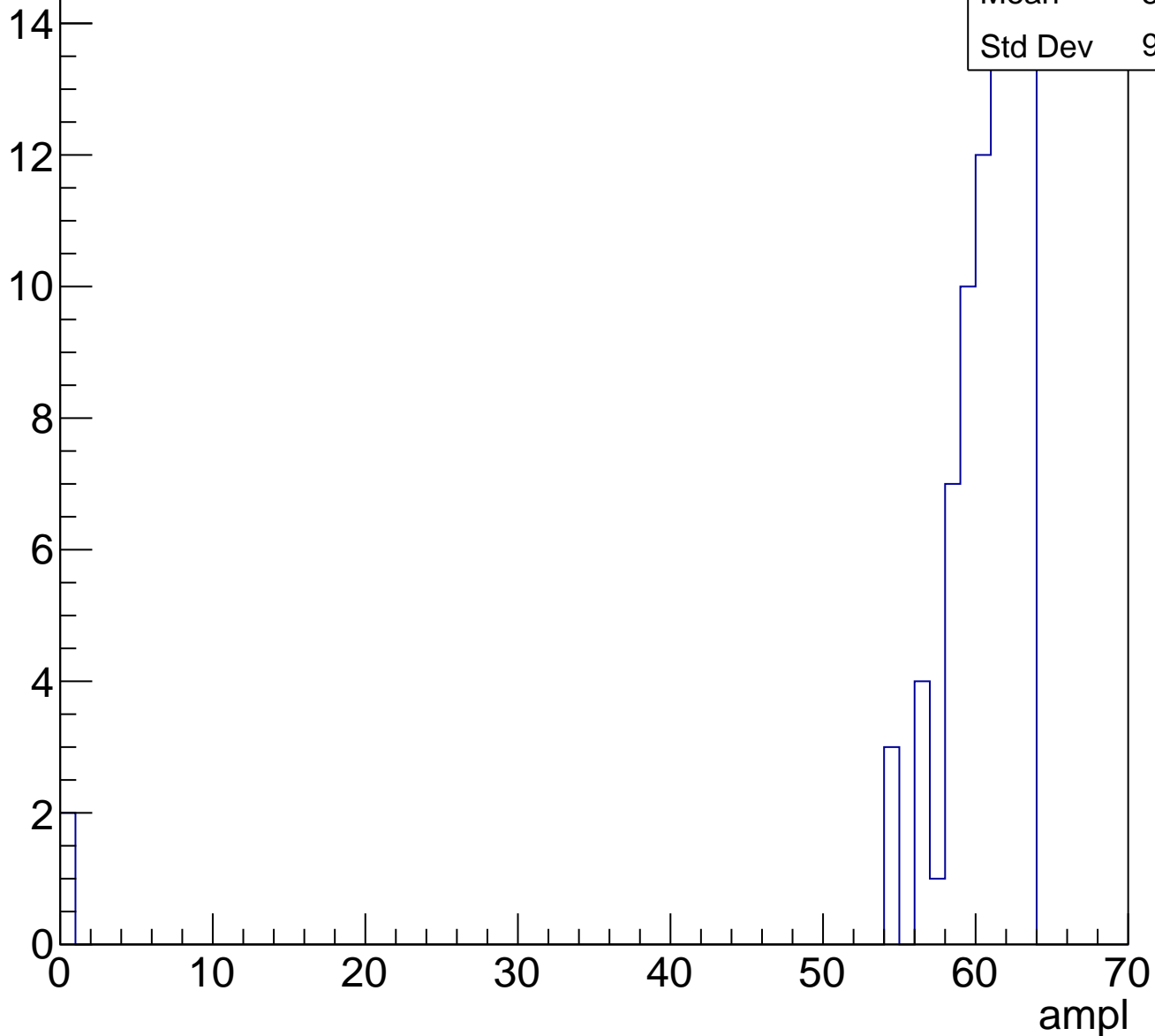
ampl



B0L101S, U19-ch31, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch32, adc0

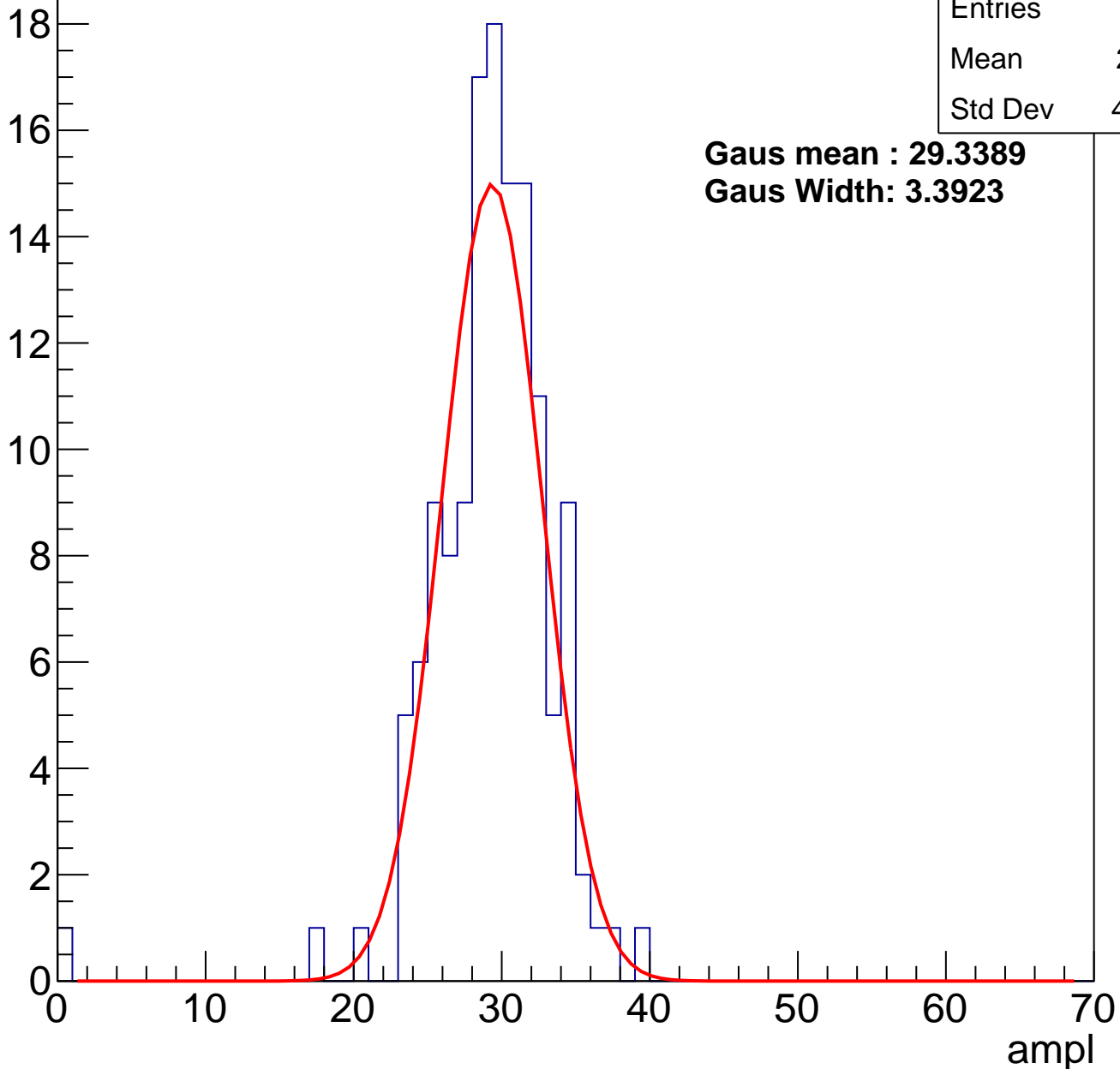
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	28.81
Std Dev	4.238

Gaus mean : 29.3389

Gaus Width: 3.3923

Entry



B0L101S, U19-ch32, adc1

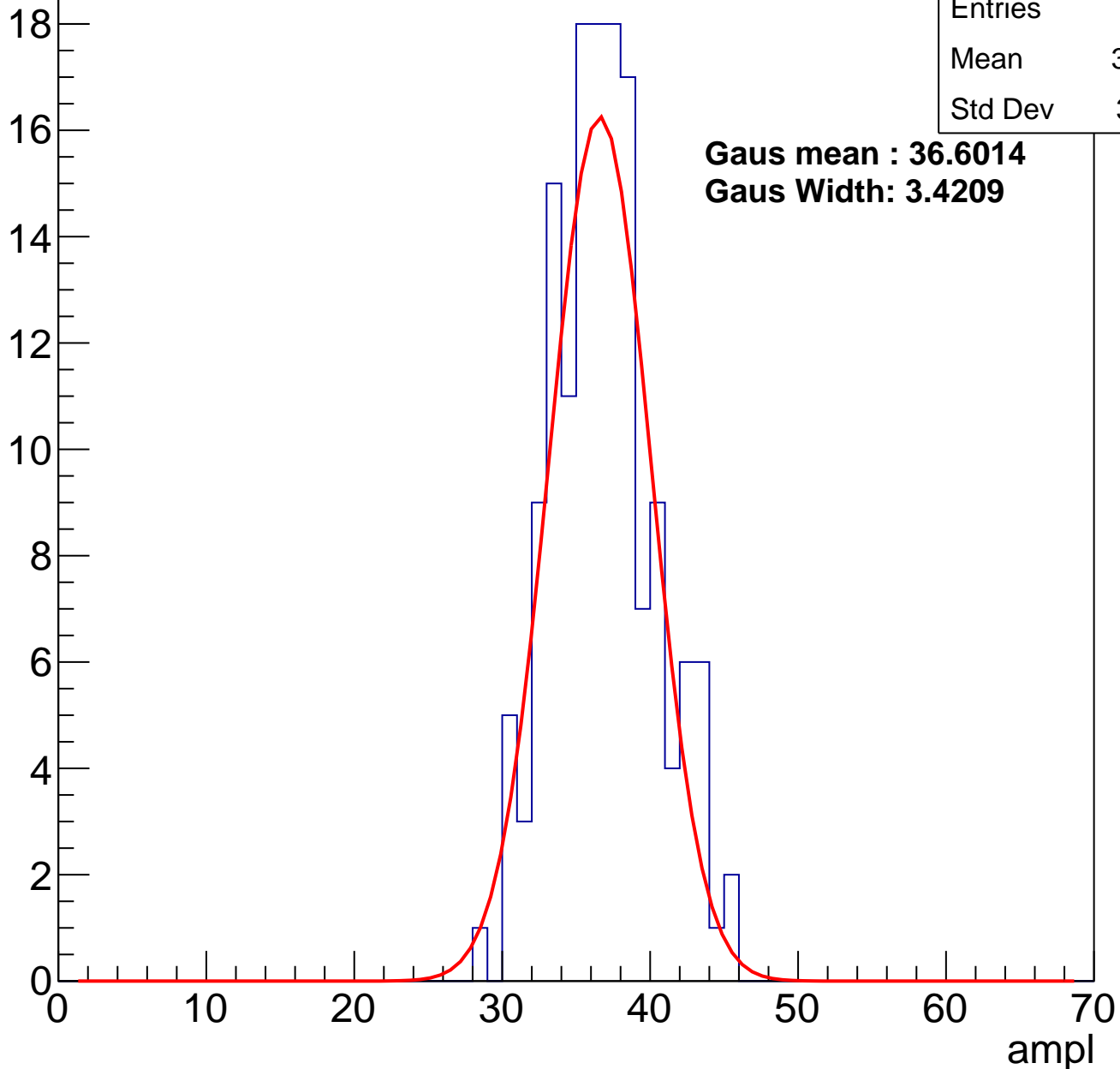
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	36.39
Std Dev	3.431

Gaus mean : 36.6014

Gaus Width: 3.4209

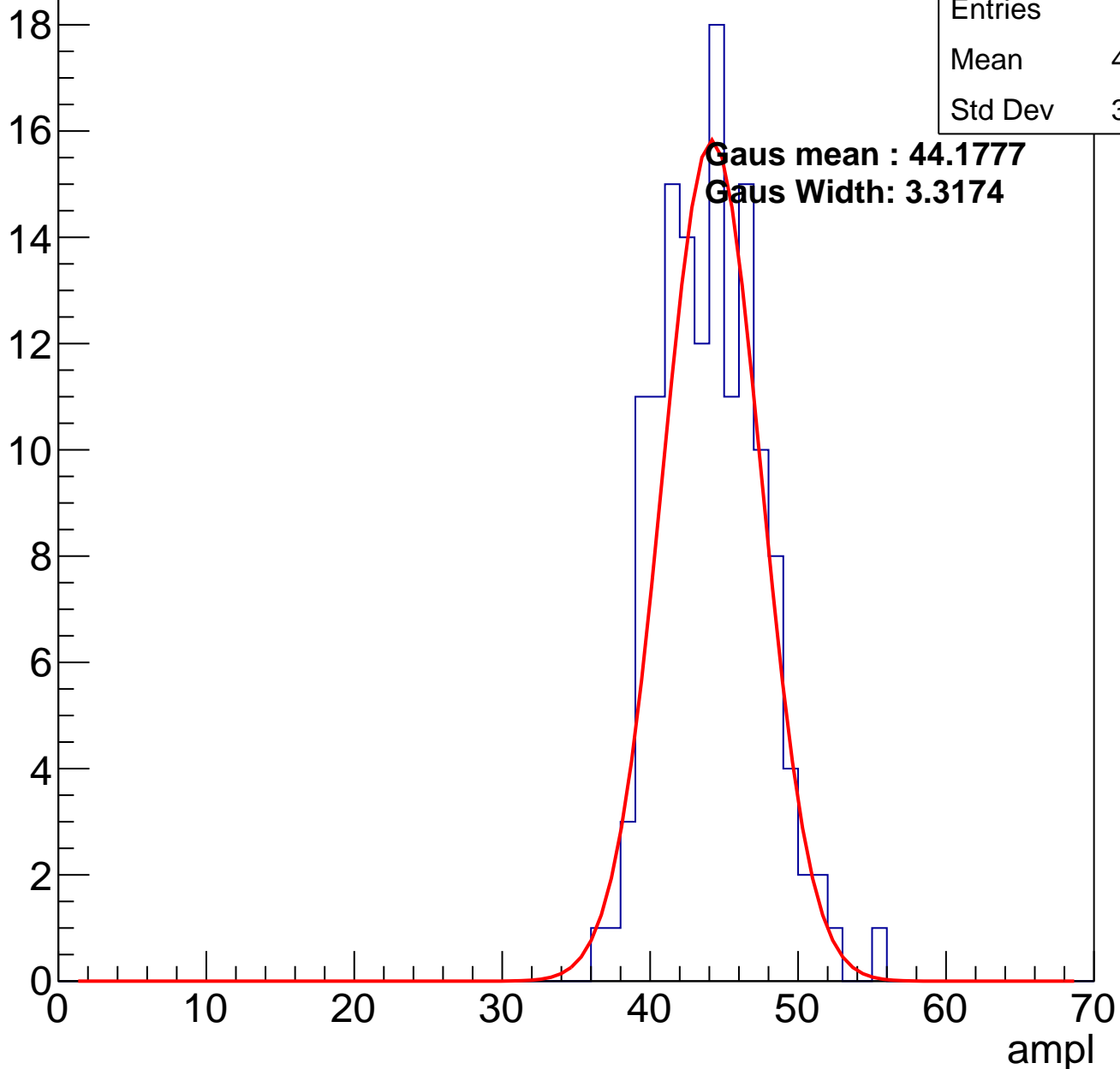
Entry



B0L101S, U19-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch32, adc3

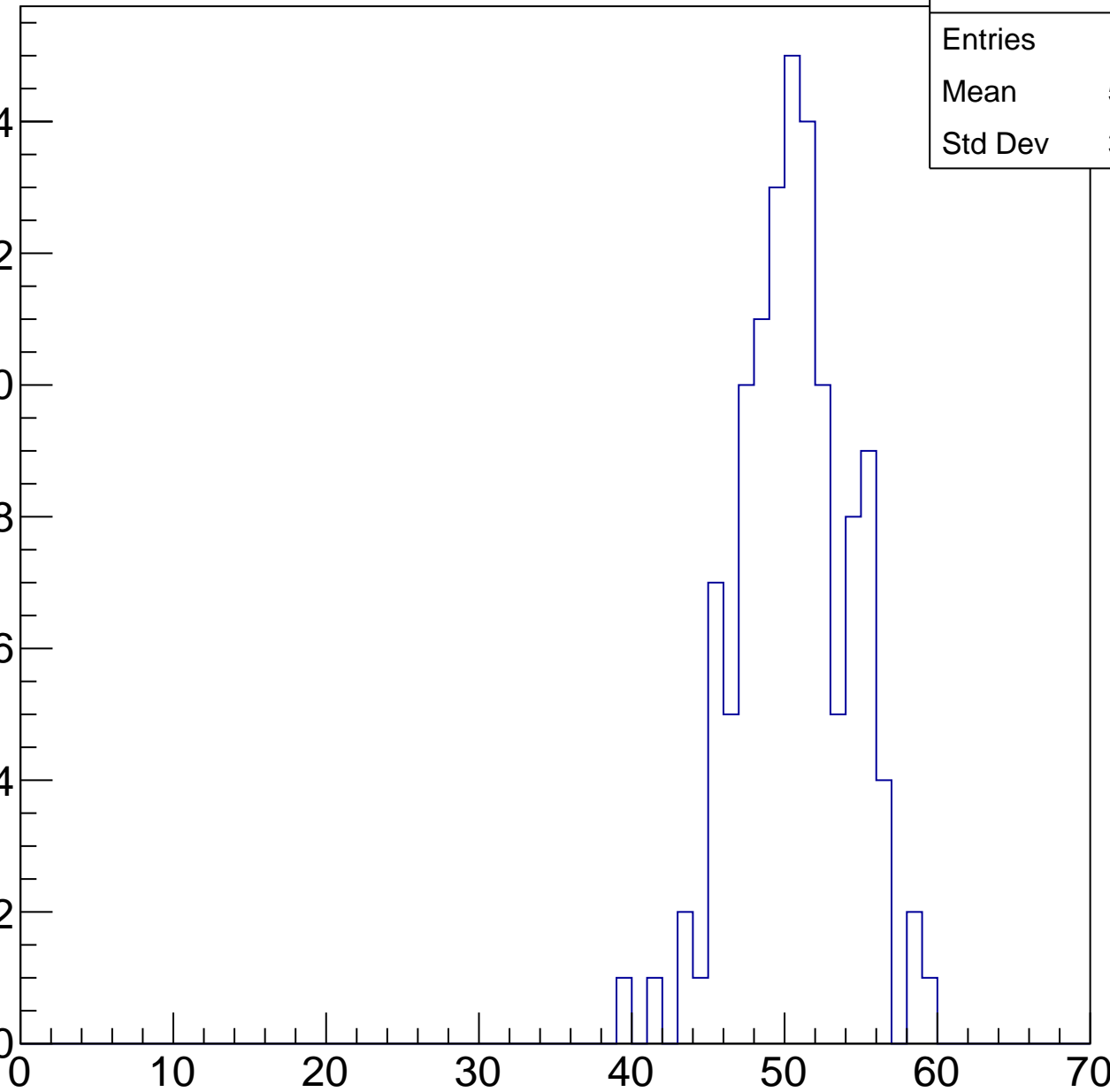
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	50.13
Std Dev	3.599

Entry

14
12
10
8
6
4
2
0

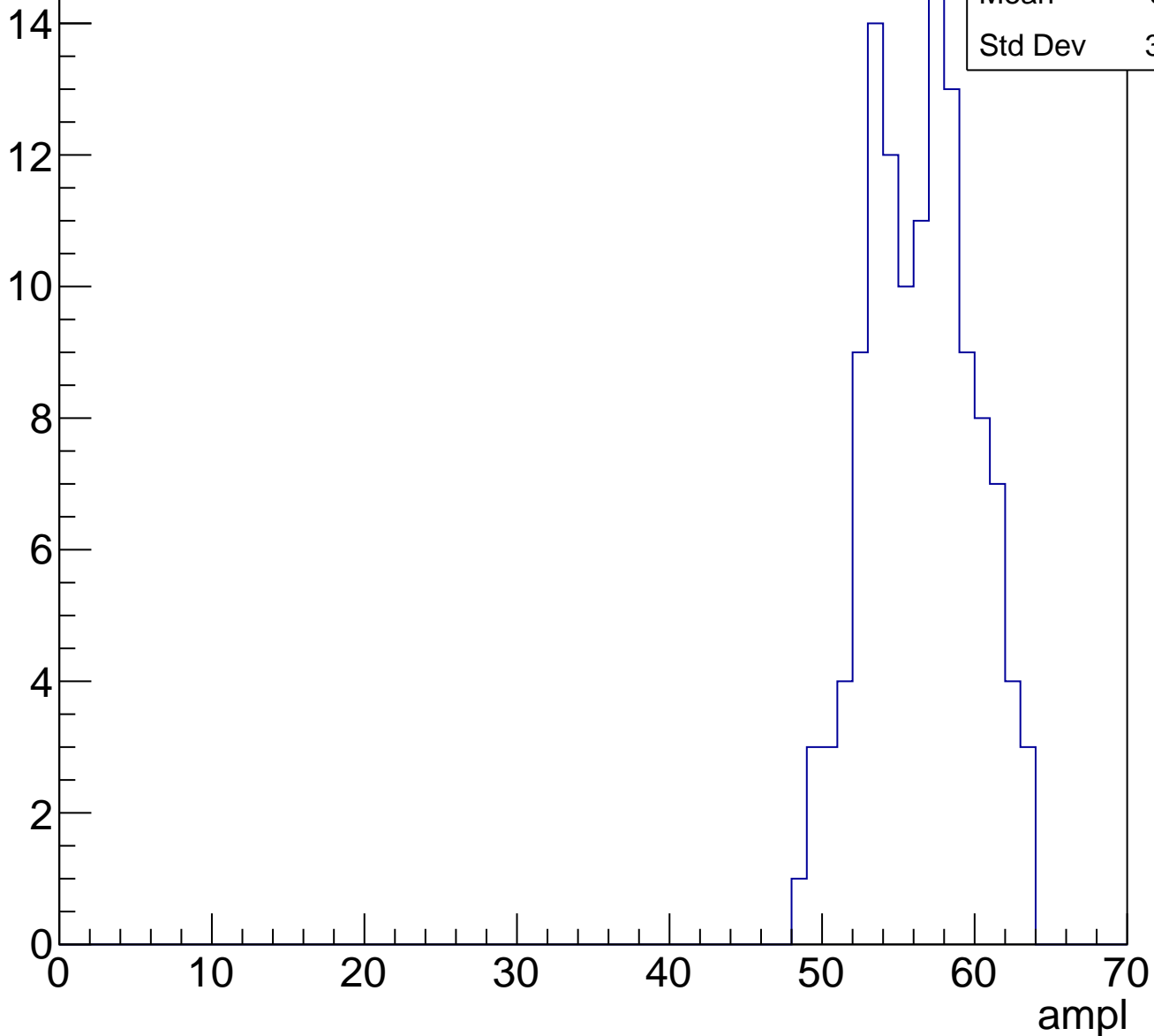
ampl



B0L101S, U19-ch32, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	126
Mean	56.01
Std Dev	3.438

B0L101S, U19-ch32, adc5

calib_packv5_042523_0143.root, FC#1, port C1

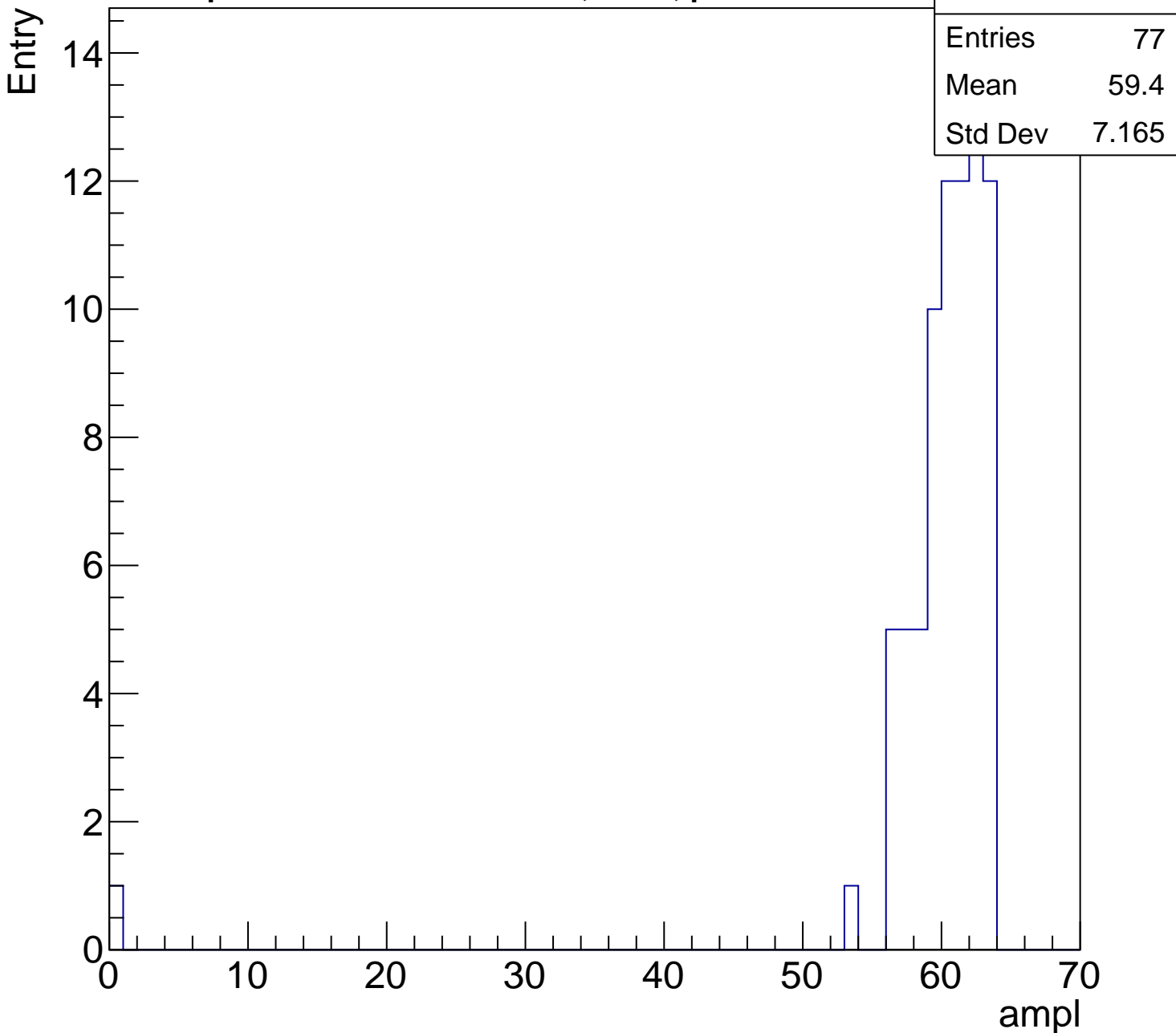
Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	59.4
Std Dev	7.165

ampl

0 10 20 30 40 50 60 70

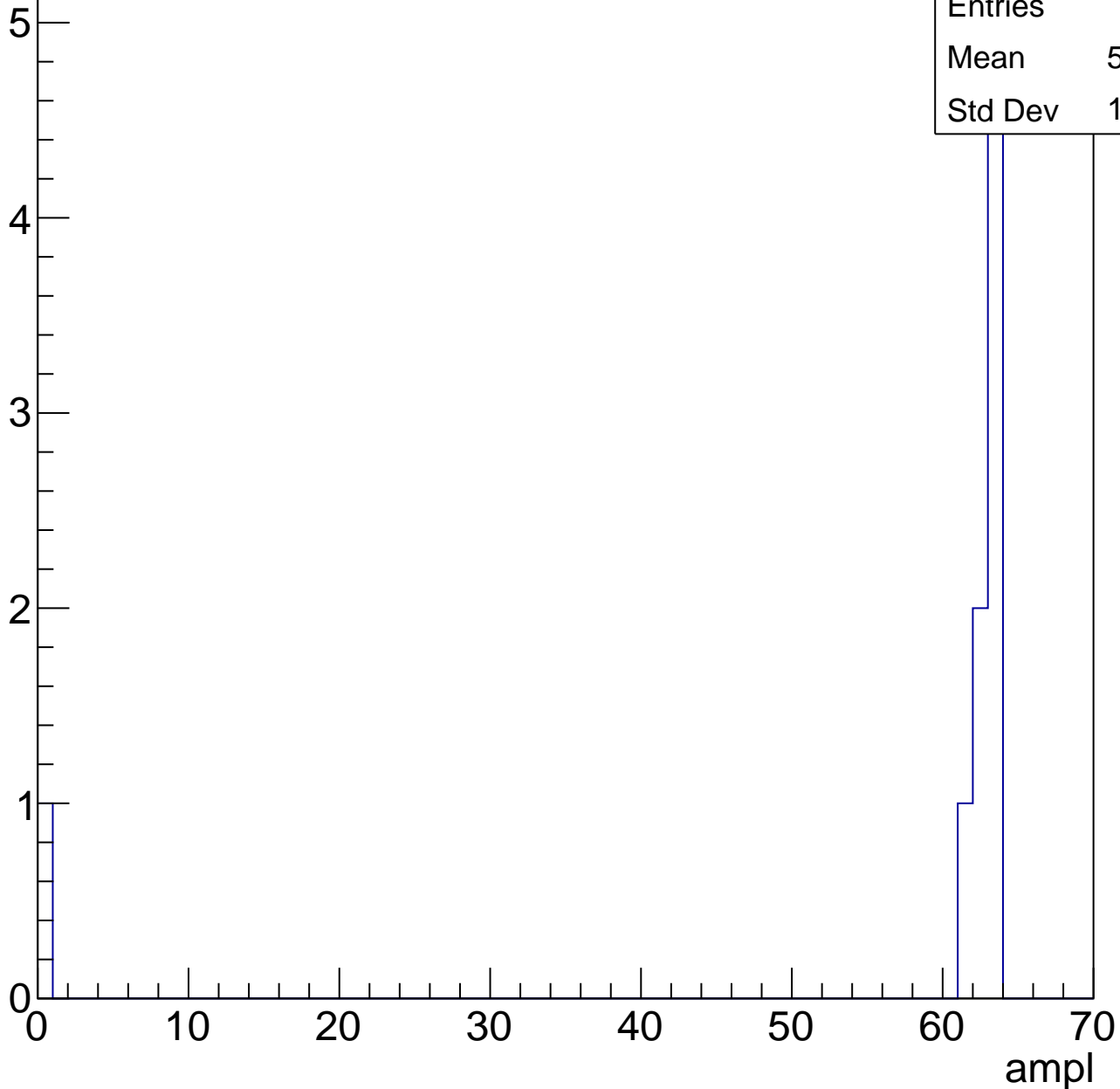


B0L101S, U19-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	55.56
Std Dev	19.65



B0L101S, U19-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch33, adc0

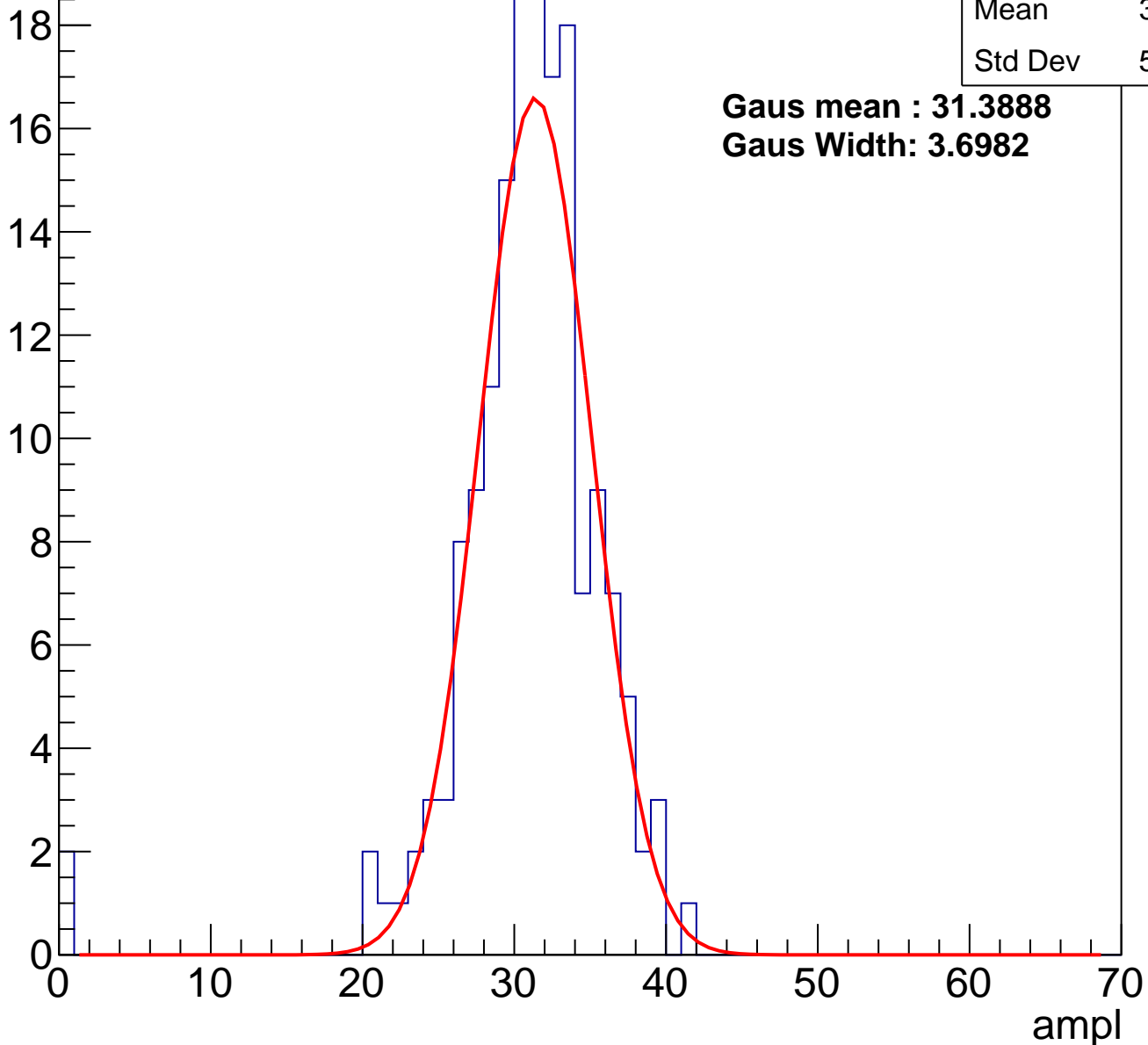
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	30.43
Std Dev	5.084

Gaus mean : 31.3888

Gaus Width: 3.6982

Entry



B0L101S, U19-ch33, adc1

calib_packv5_042523_0143.root, FC#1, port C1

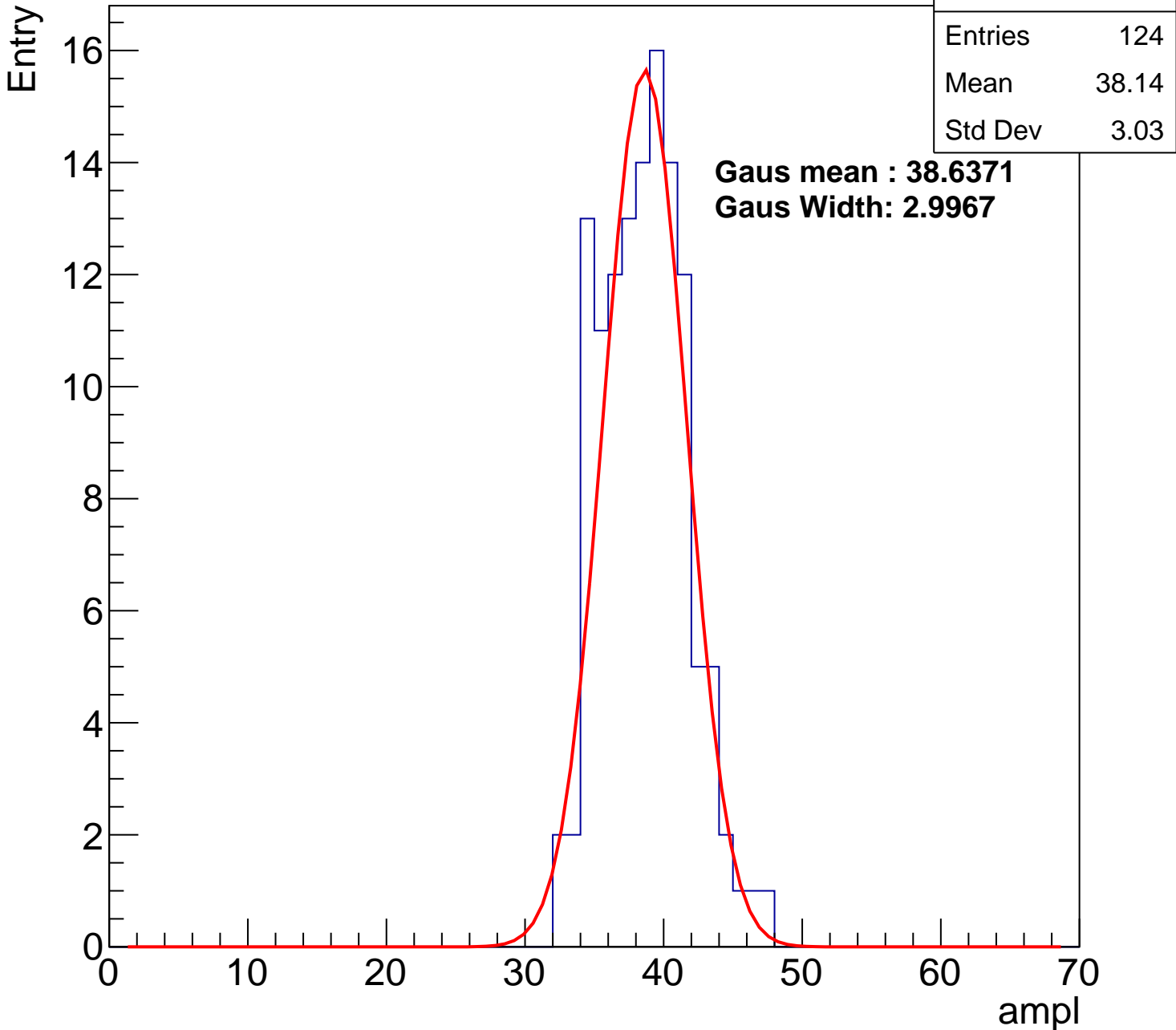
Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	38.14
Std Dev	3.03

Gaus mean : 38.6371
Gaus Width: 2.9967

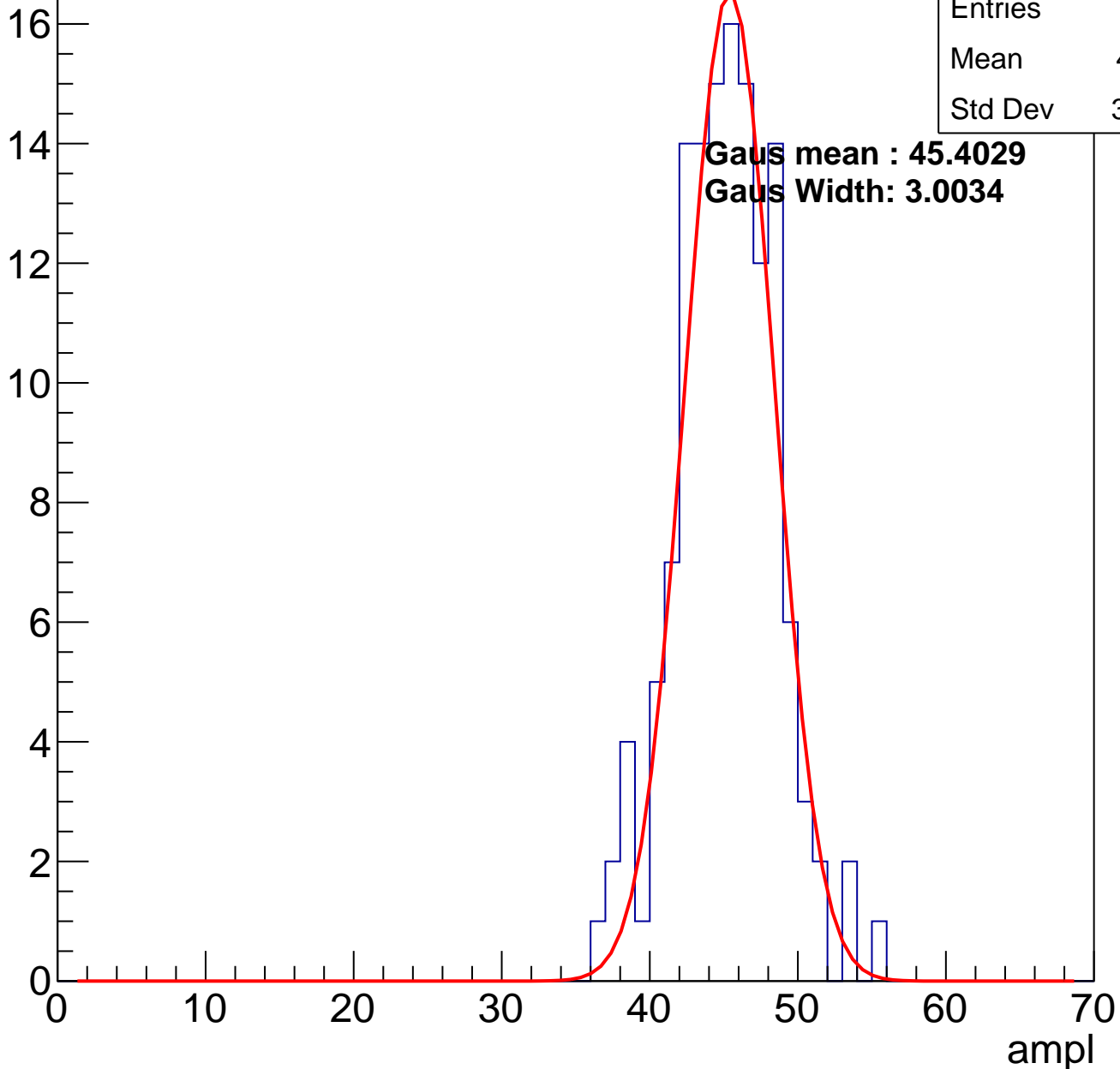
0 10 20 30 40 50 60 70
ampl



B0L101S, U19-ch33, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	134
Mean	44.71
Std Dev	3.372

Gaus mean : 45.4029

Gaus Width: 3.0034

B0L101S, U19-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

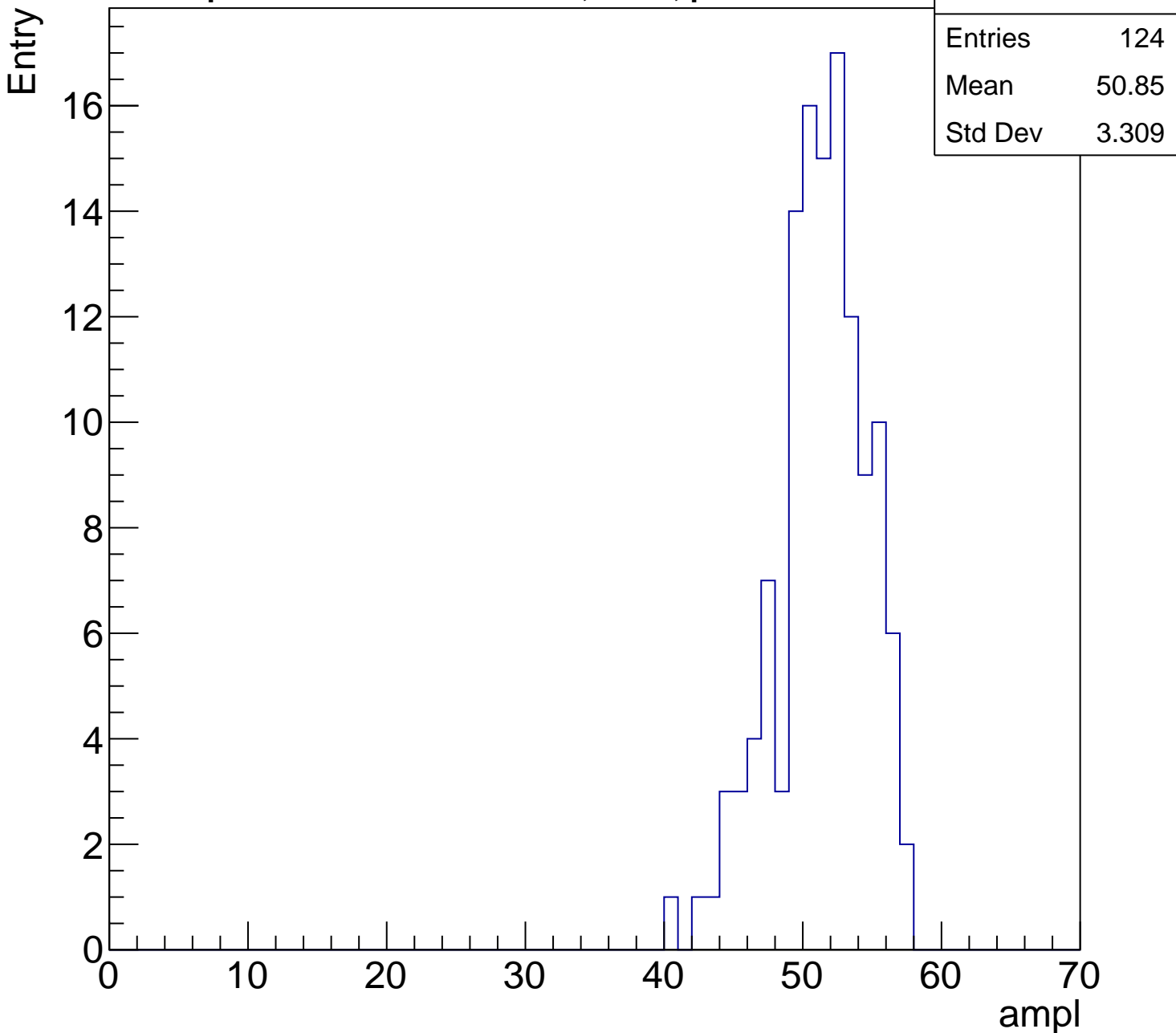
Entries	124
Mean	50.85
Std Dev	3.309

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch33, adc4

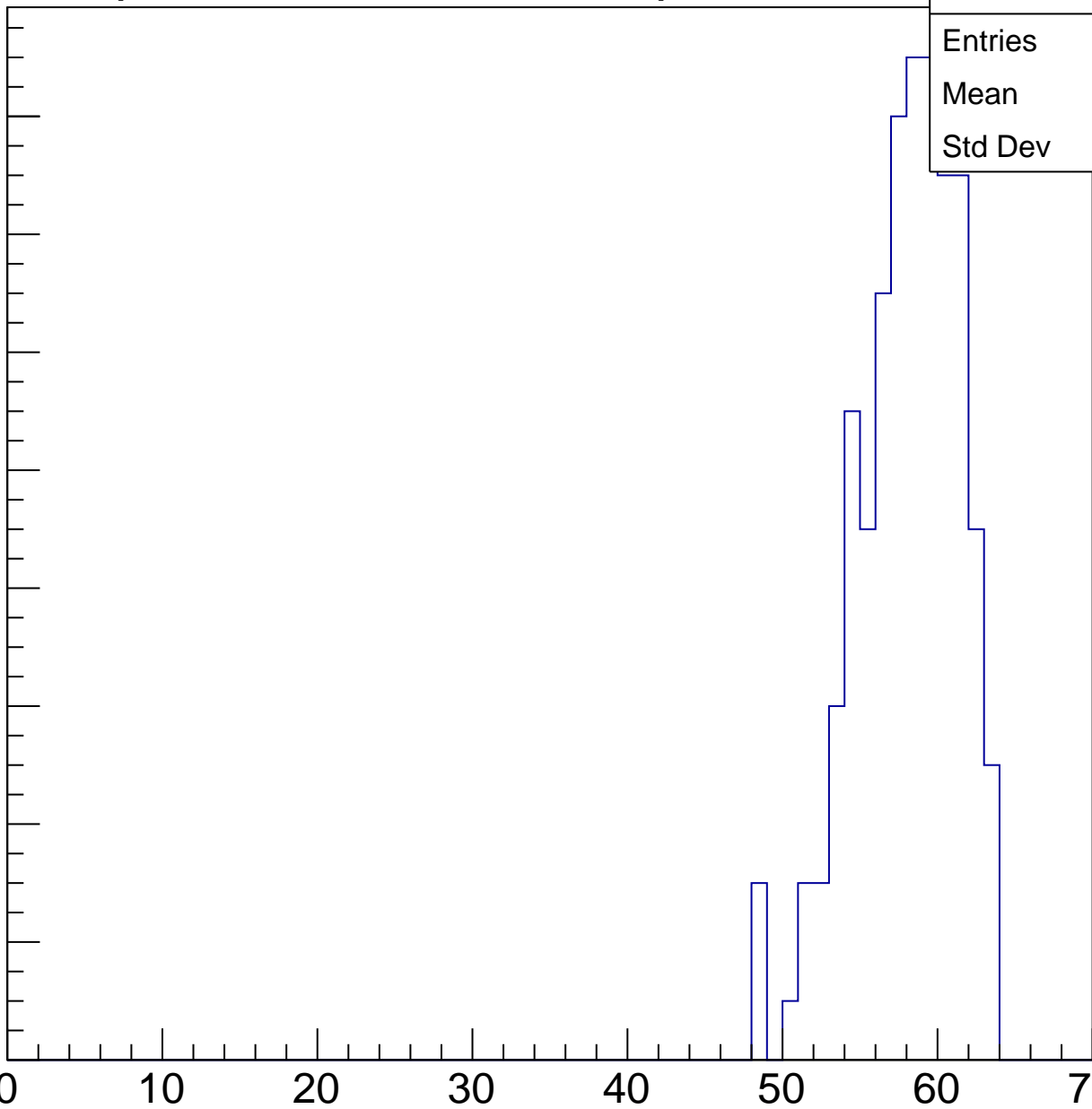
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	143
Mean	57.53
Std Dev	3.285

ampl



B0L101S, U19-ch33, adc5

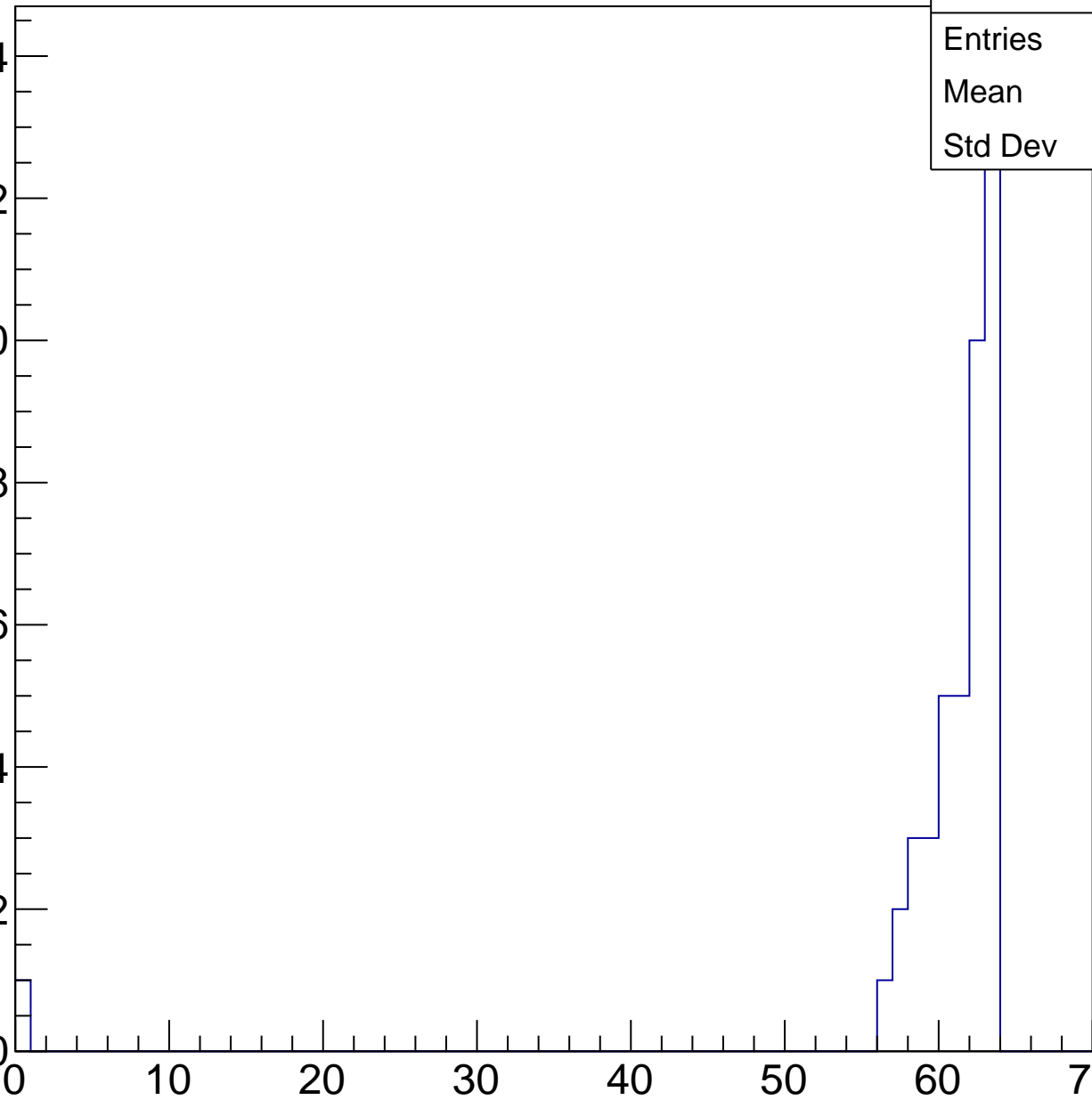
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	44
Mean	59.73
Std Dev	9.314

ampl



B0L101S, U19-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch34, adc0

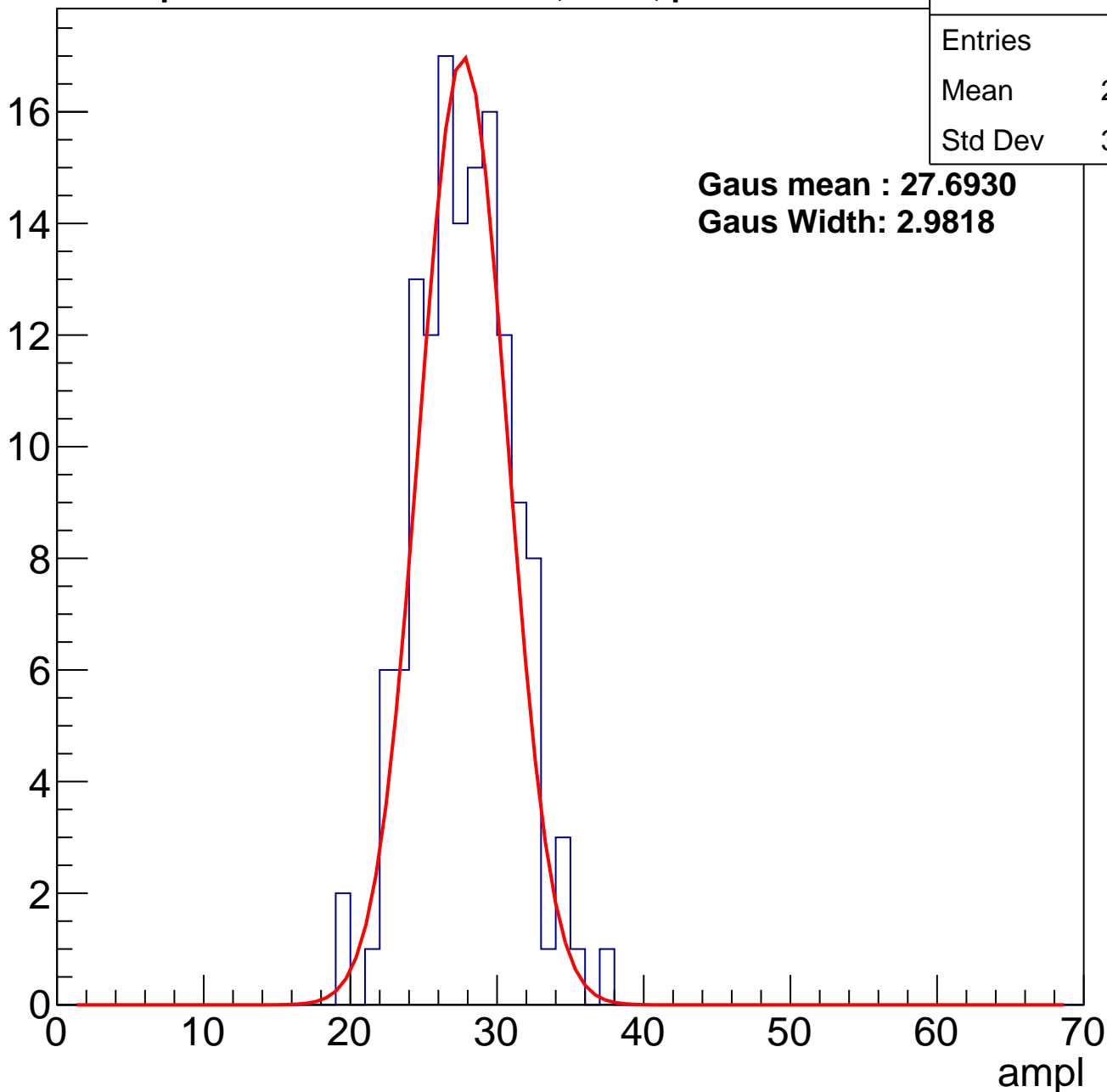
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	27.35
Std Dev	3.257

Gaus mean : 27.6930

Gaus Width: 2.9818

Entry



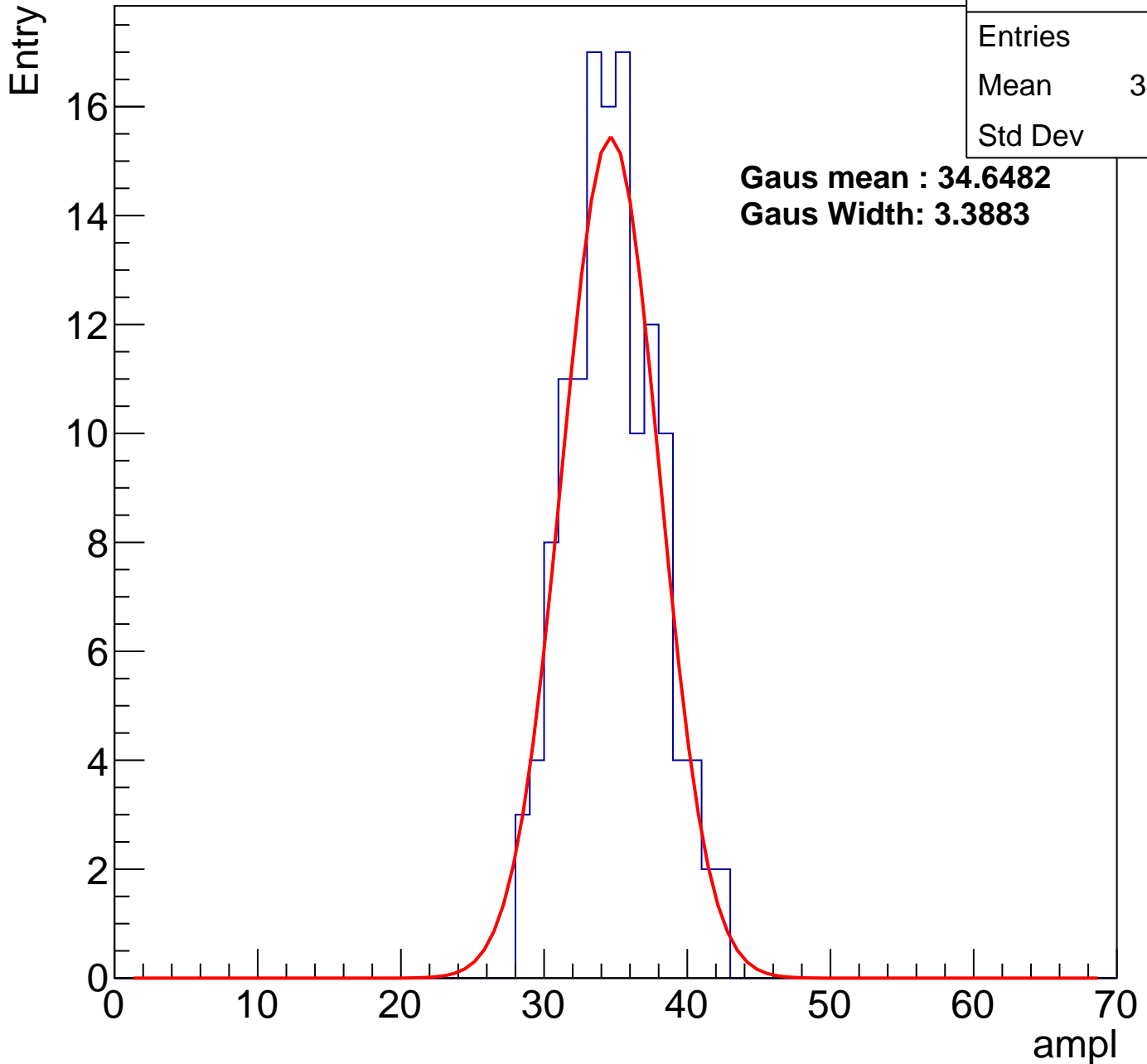
B0L101S, U19-ch34, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	34.34
Std Dev	3.12

Gaus mean : 34.6482

Gaus Width: 3.3883



B0L101S, U19-ch34, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

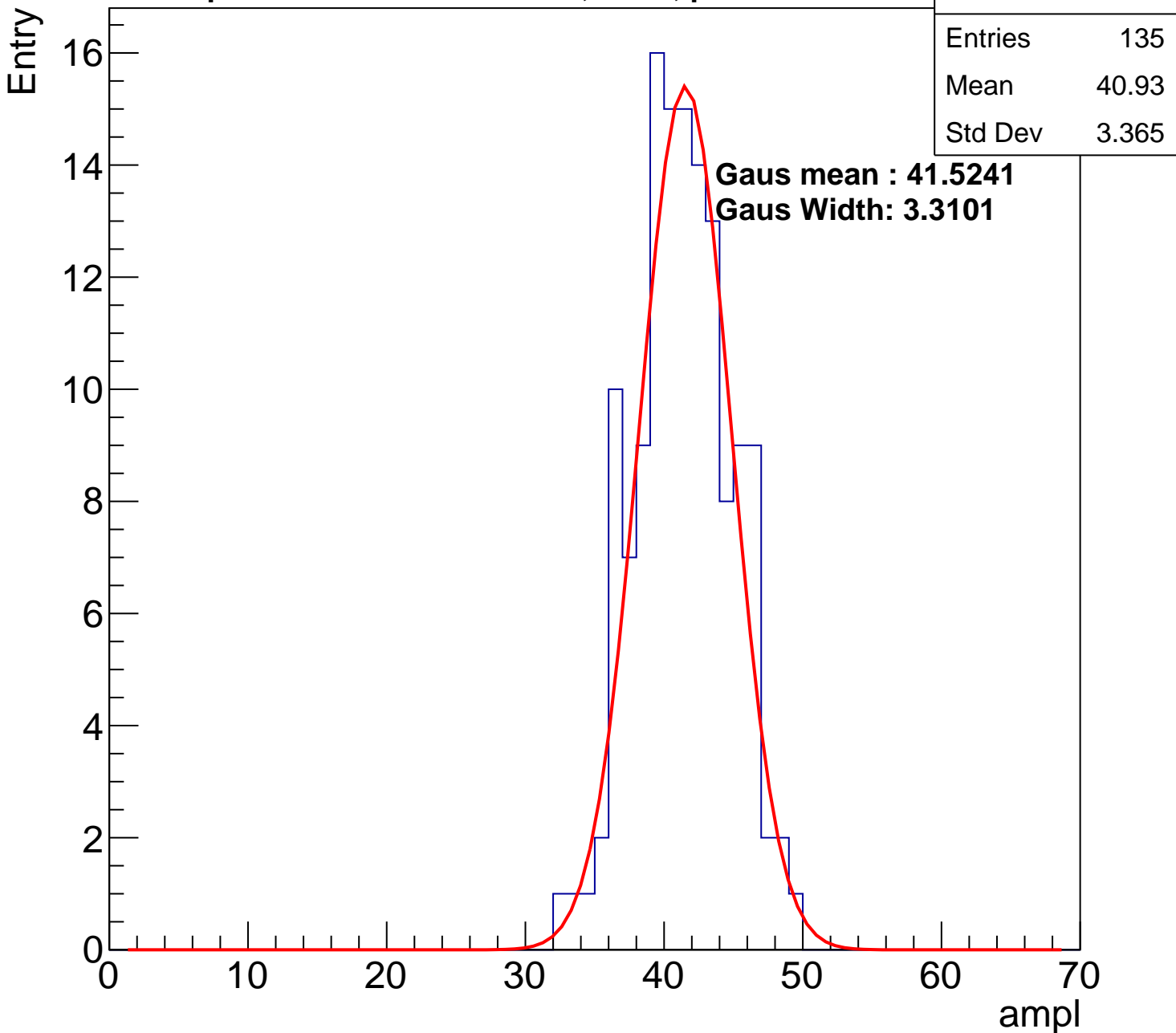
Entries	135
Mean	40.93
Std Dev	3.365

Gaus mean : 41.5241

Gaus Width: 3.3101

ampl

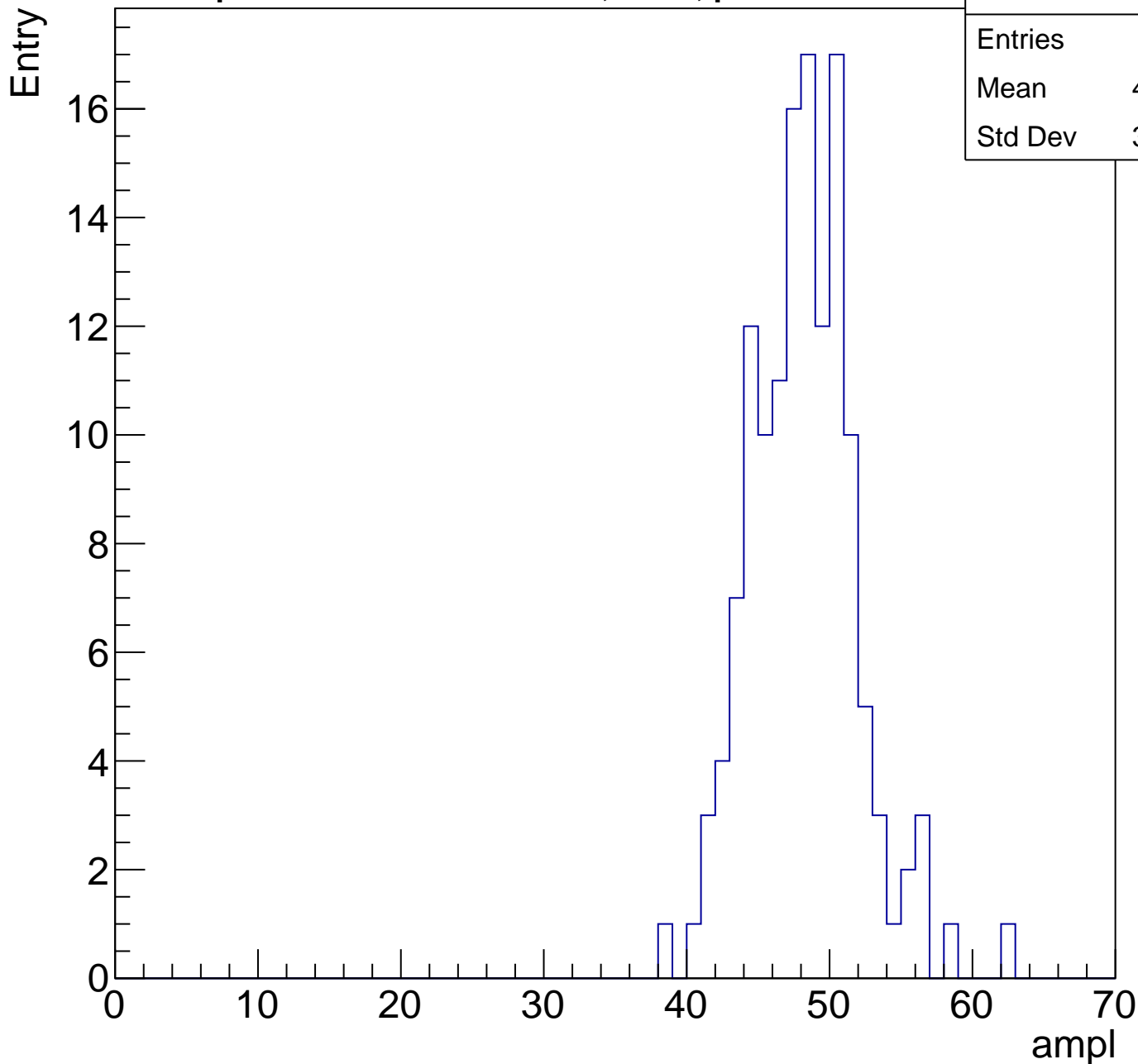
0 10 20 30 40 50 60 70



B0L101S, U19-ch34, adc3

calib_packv5_042523_0143.root, FC#1, port C1

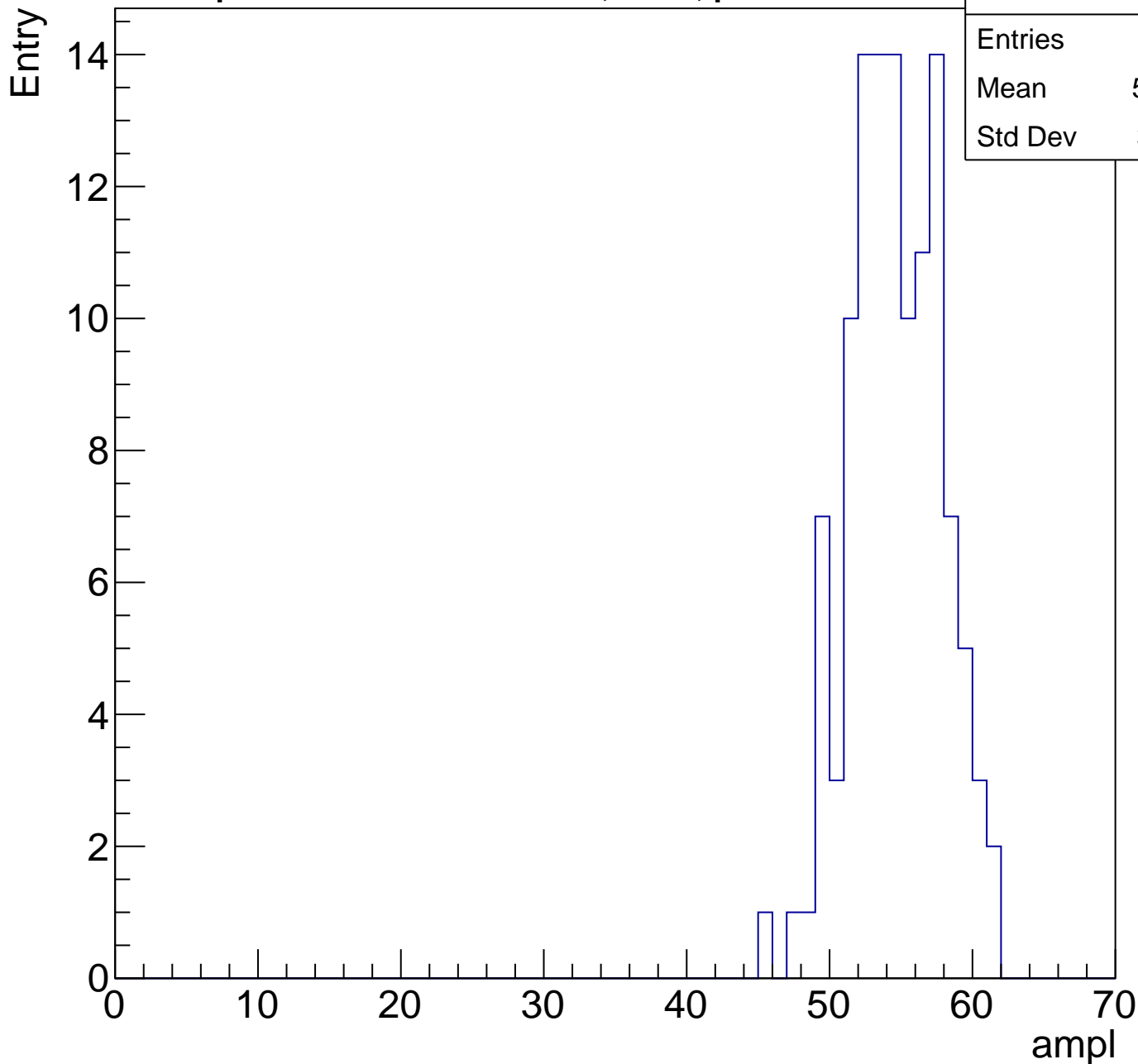
Entries	137
Mean	47.74
Std Dev	3.734



B0L101S, U19-ch34, adc4

calib_packv5_042523_0143.root, FC#1, port C1

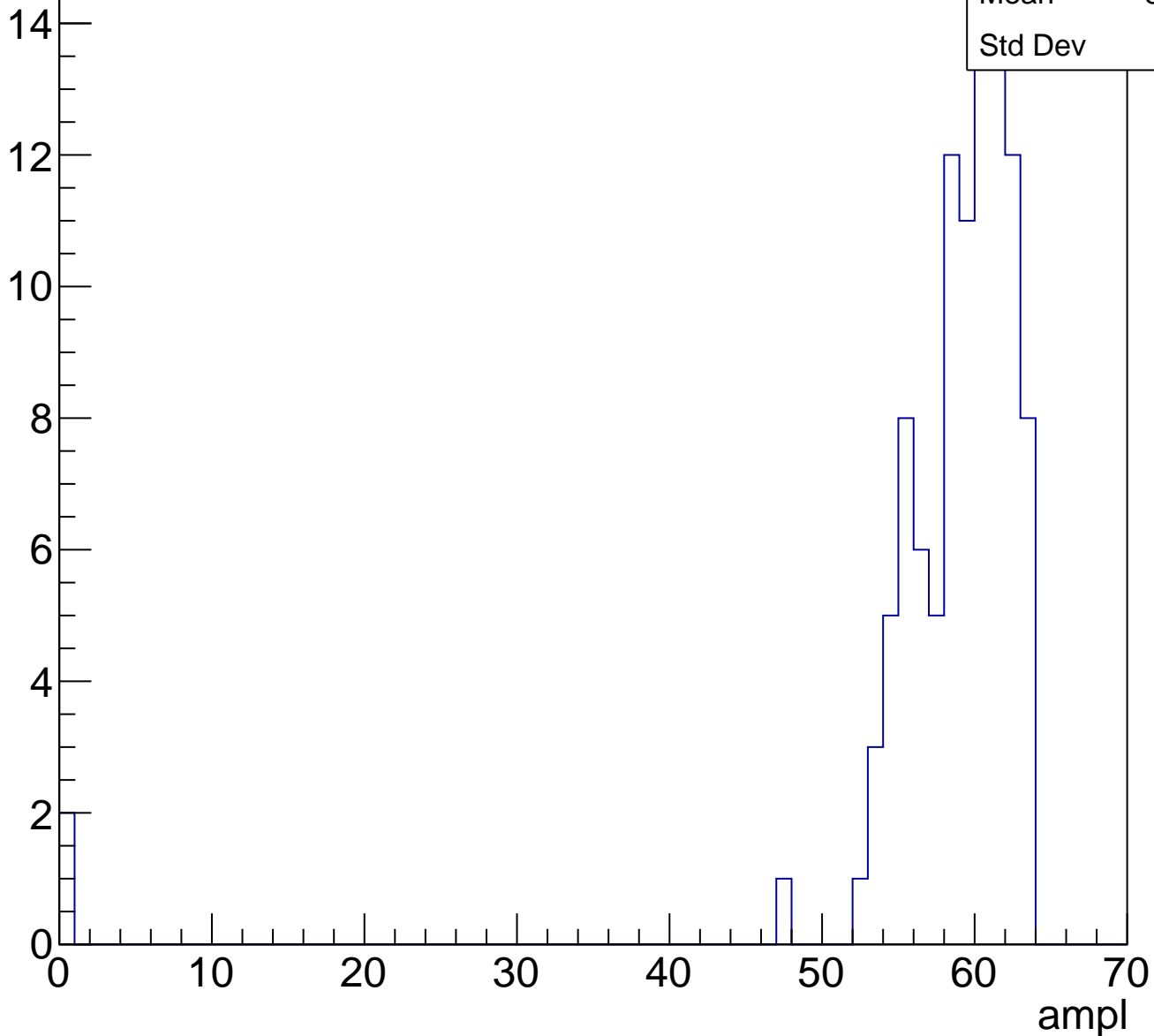
Entries	117
Mean	54.15
Std Dev	3.161



B0L101S, U19-ch34, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

29

Mean

61.59

Std Dev

1.651

ampl

0

10

20

30

40

50

60

70

B0L101S, U19-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch35, adc0

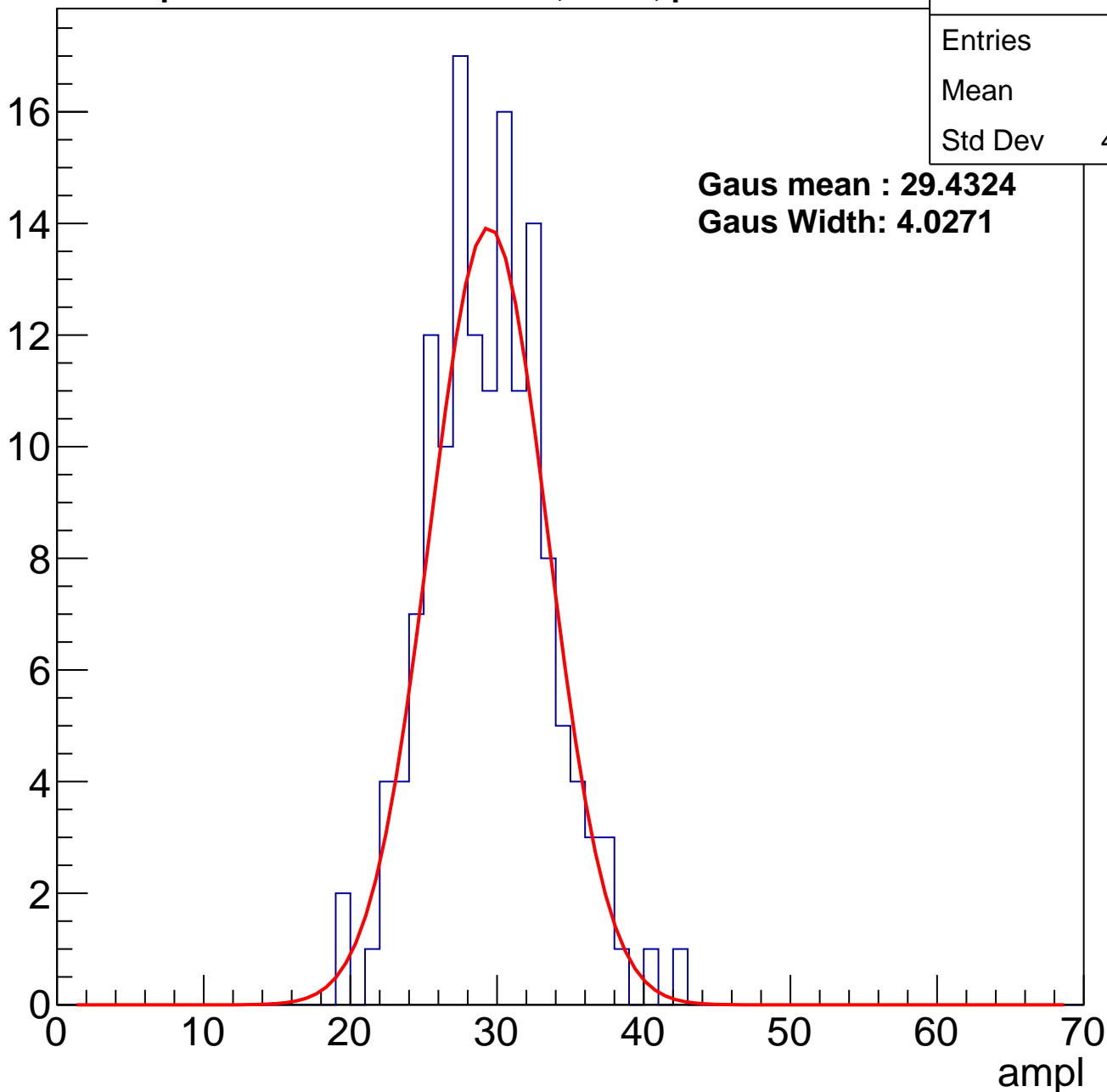
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29
Std Dev	4.069

Gaus mean : 29.4324

Gaus Width: 4.0271

Entry



B0L101S, U19-ch35, adc1

calib_packv5_042523_0143.root, FC#1, port C1

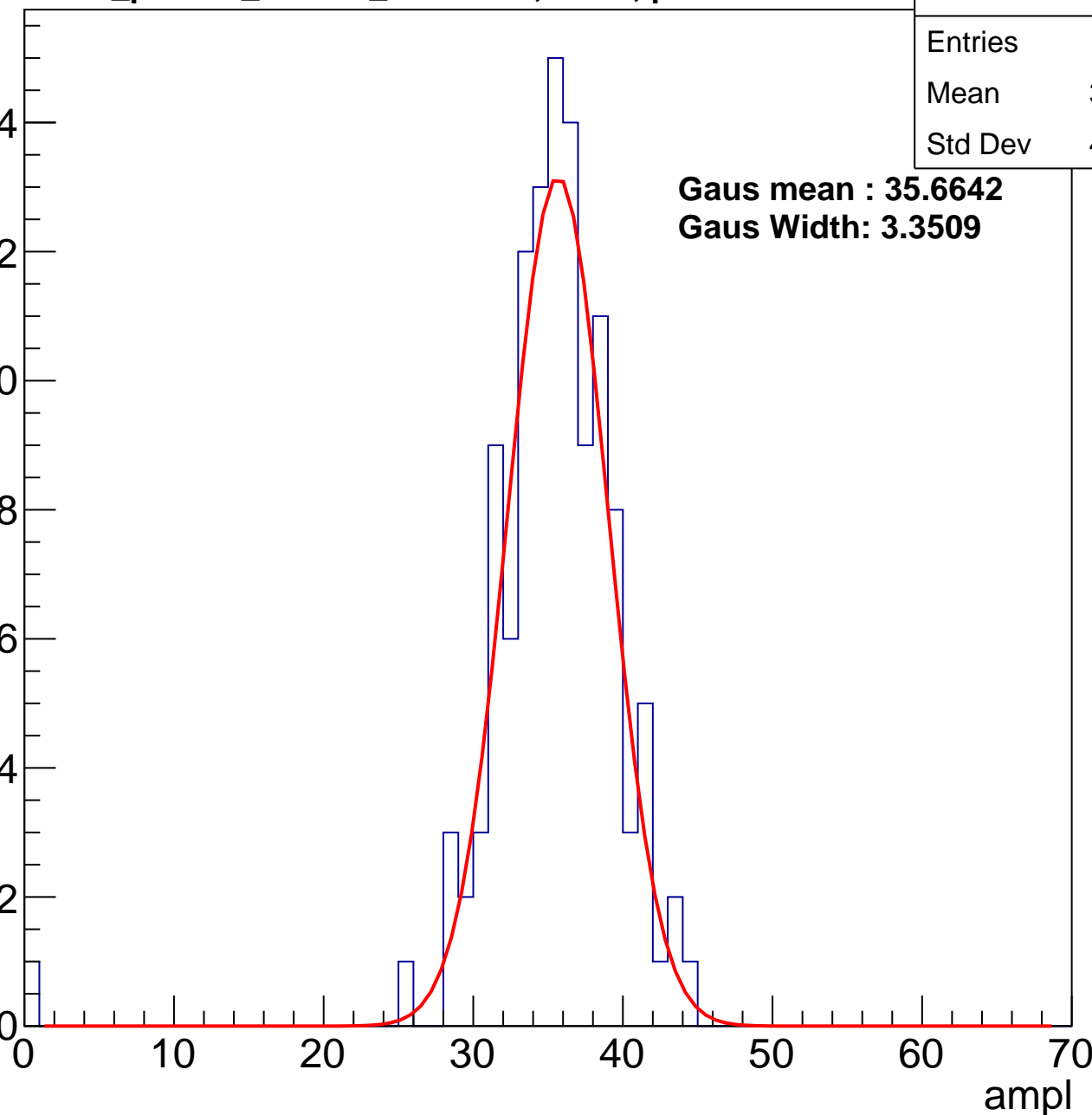
Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	34.92
Std Dev	4.734

Gaus mean : 35.6642

Gaus Width: 3.3509



B0L101S, U19-ch35, adc2

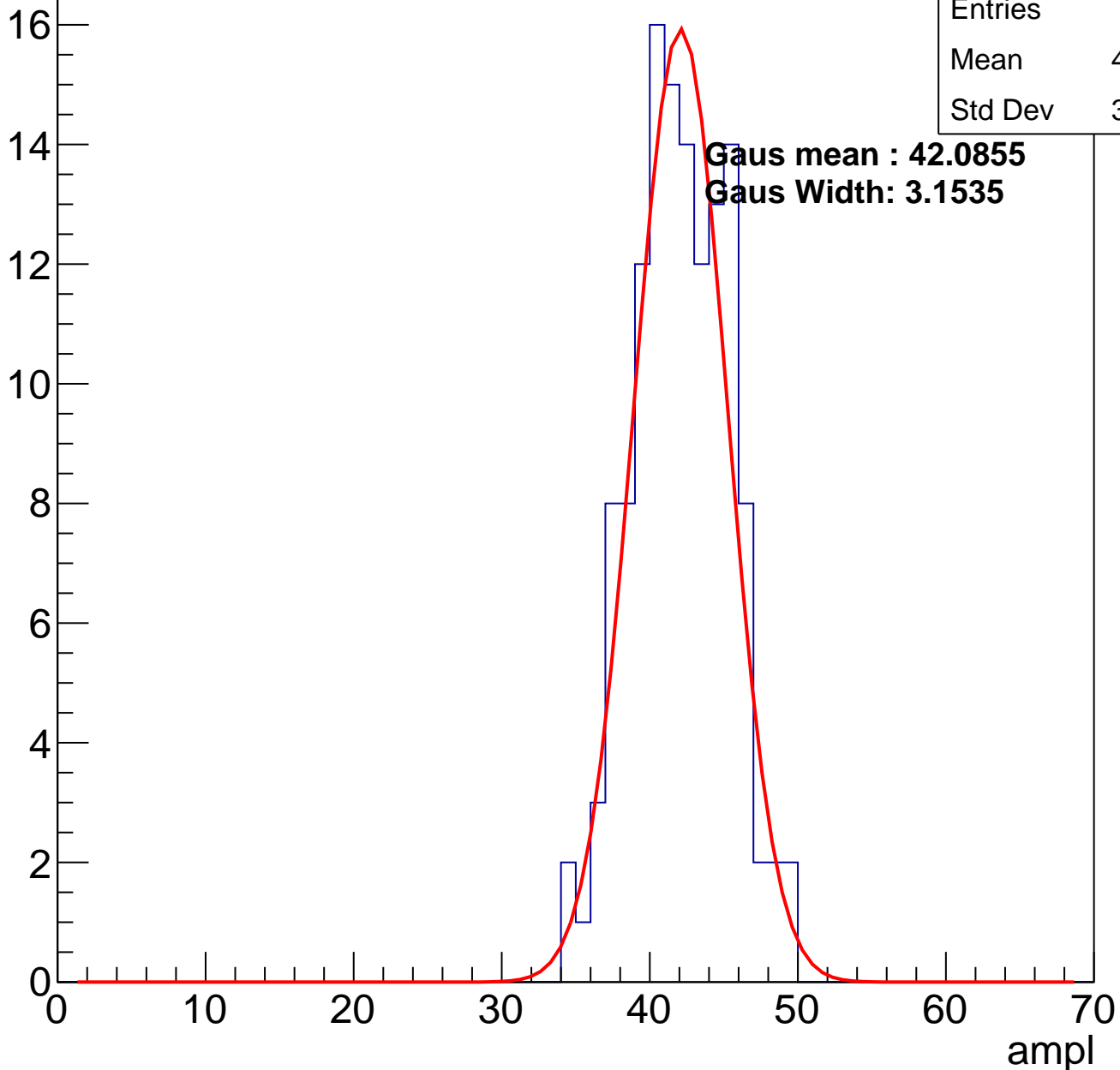
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	41.64
Std Dev	3.163

Gaus mean : 42.0855

Gaus Width: 3.1535

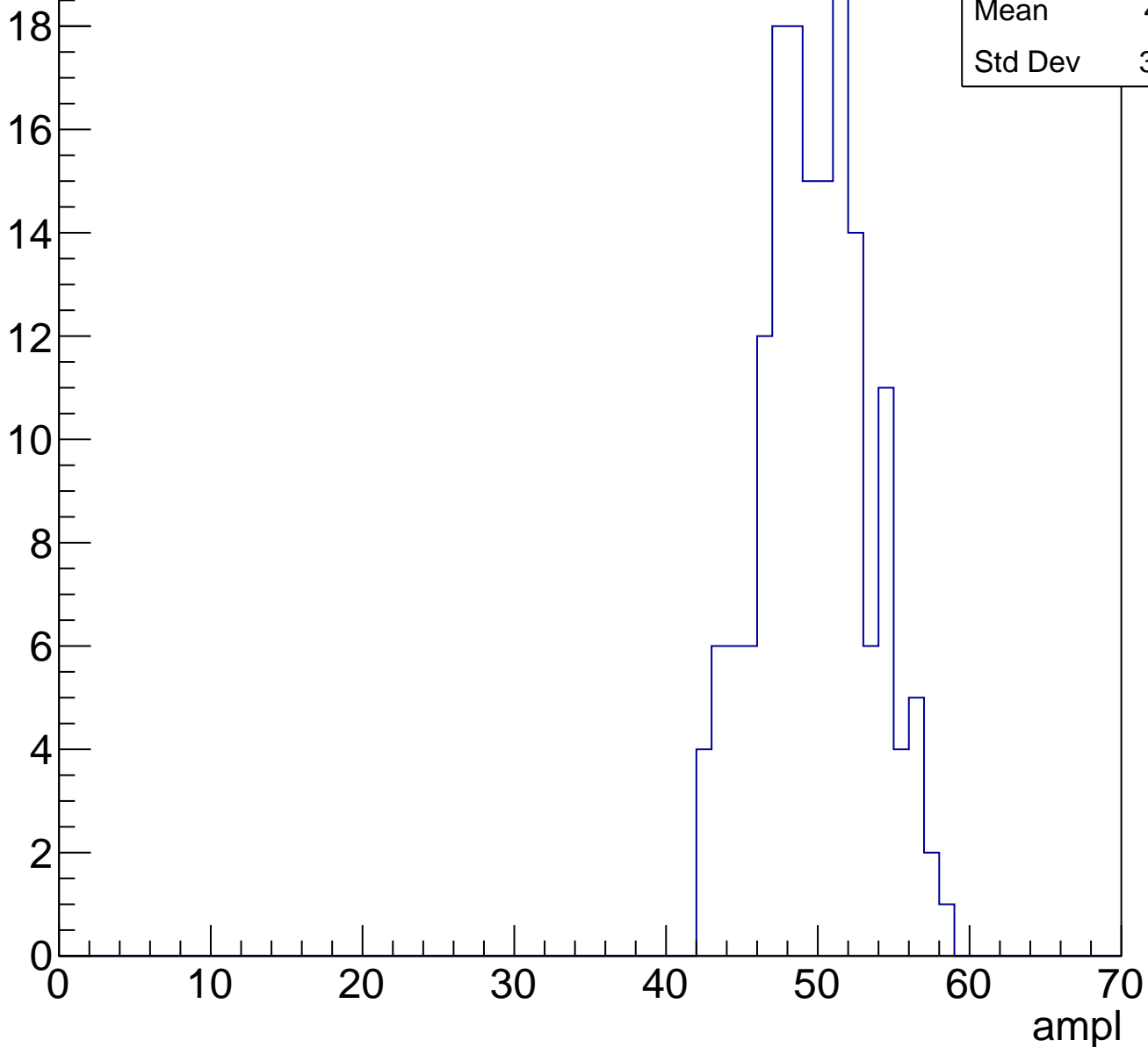
Entry



B0L101S, U19-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch35, adc4

calib_packv5_042523_0143.root, FC#1, port C1

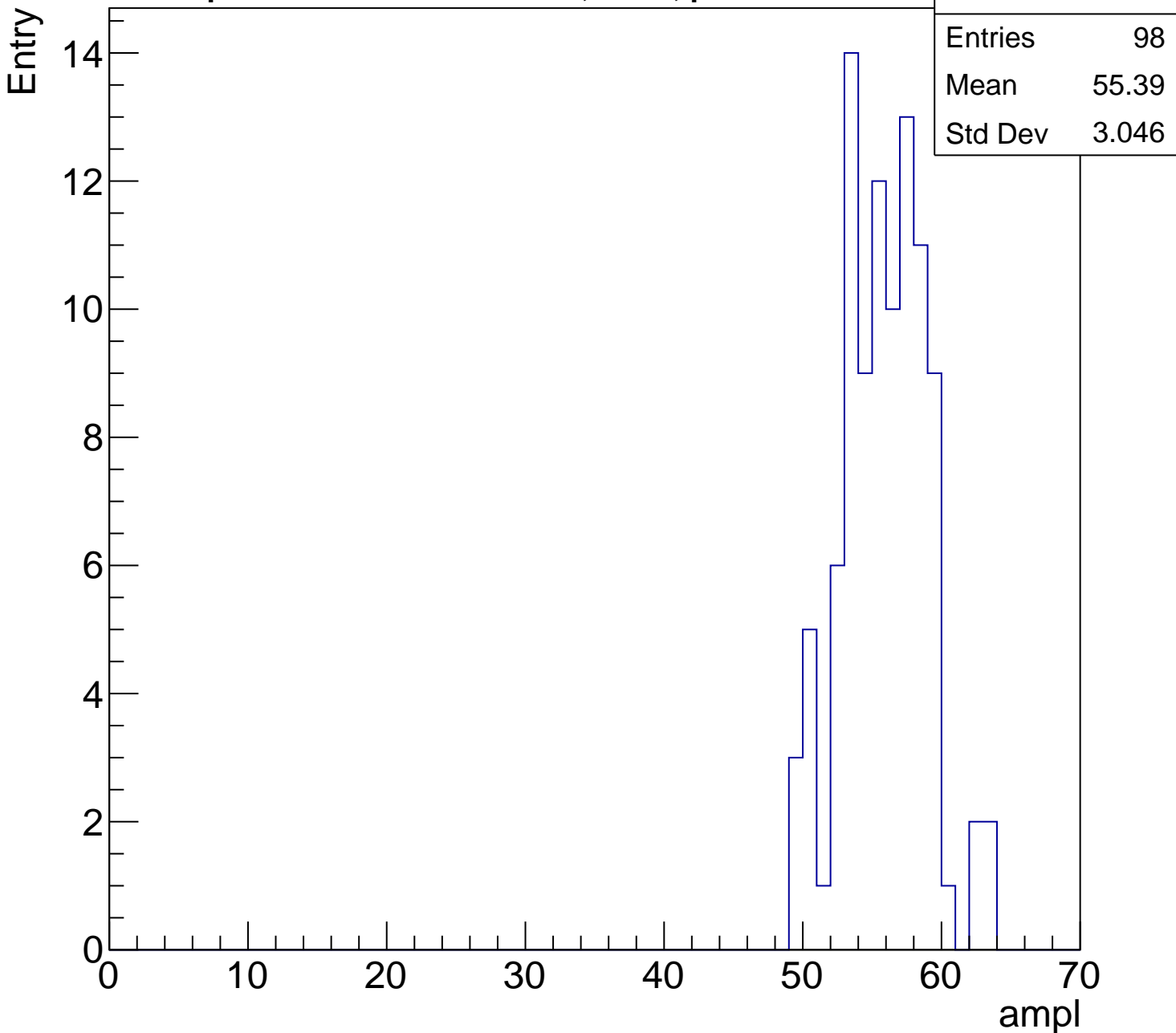
Entries	98
Mean	55.39
Std Dev	3.046

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch35, adc5

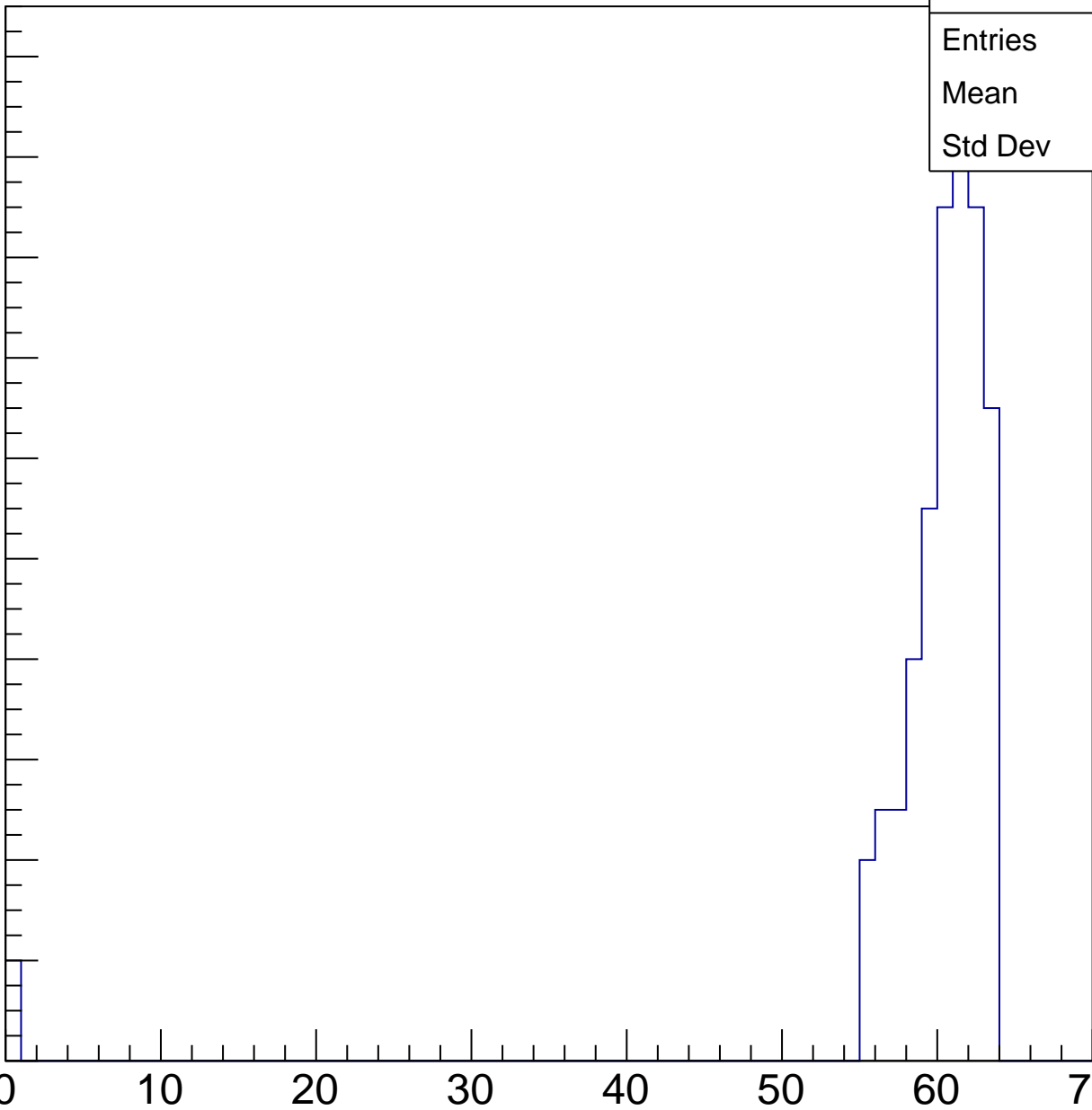
calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.93
Std Dev	8.607

ampl

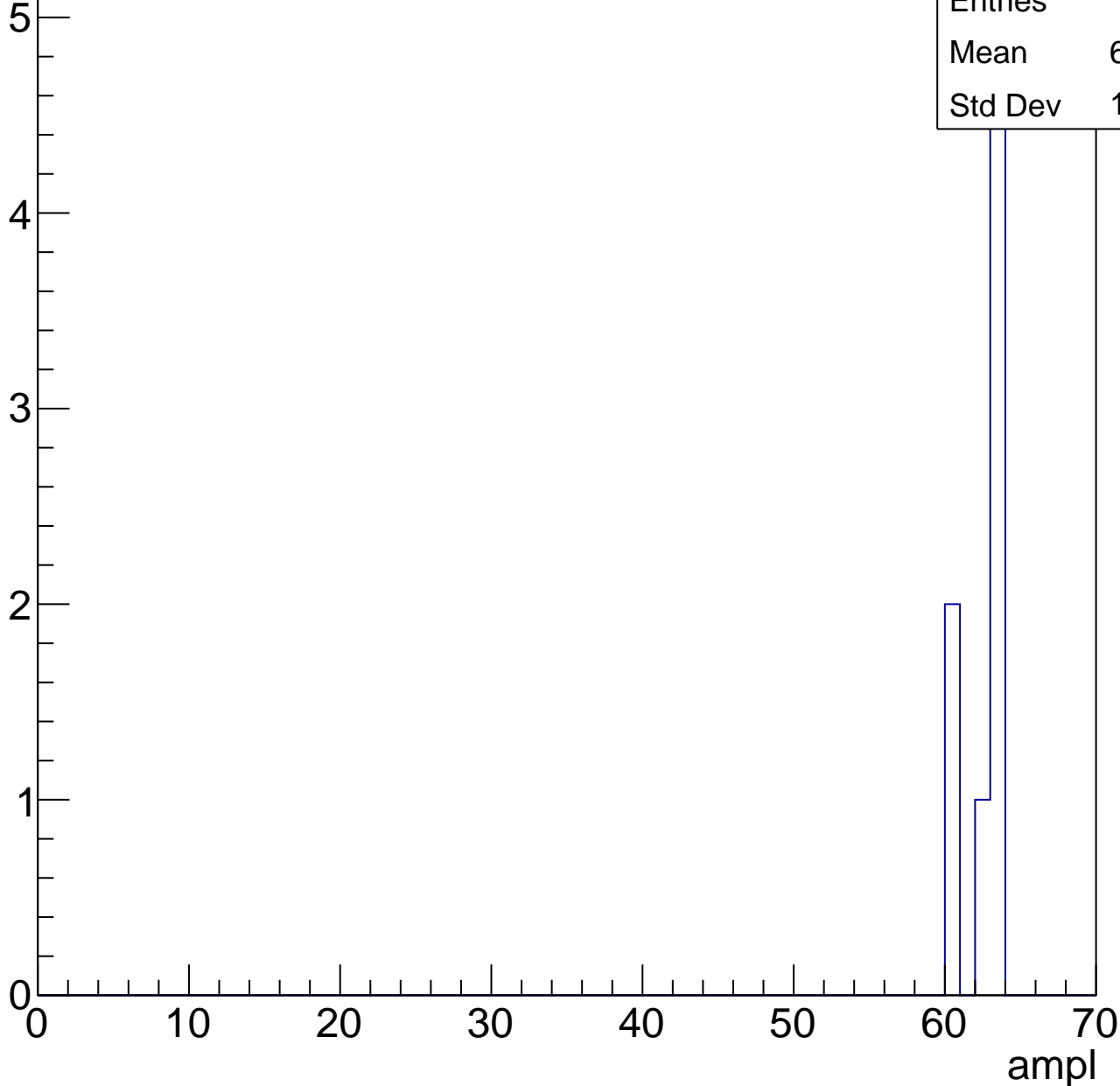


B0L101S, U19-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	62.12
Std Dev	1.269



B0L101S, U19-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L101S, U19-ch36, adc0

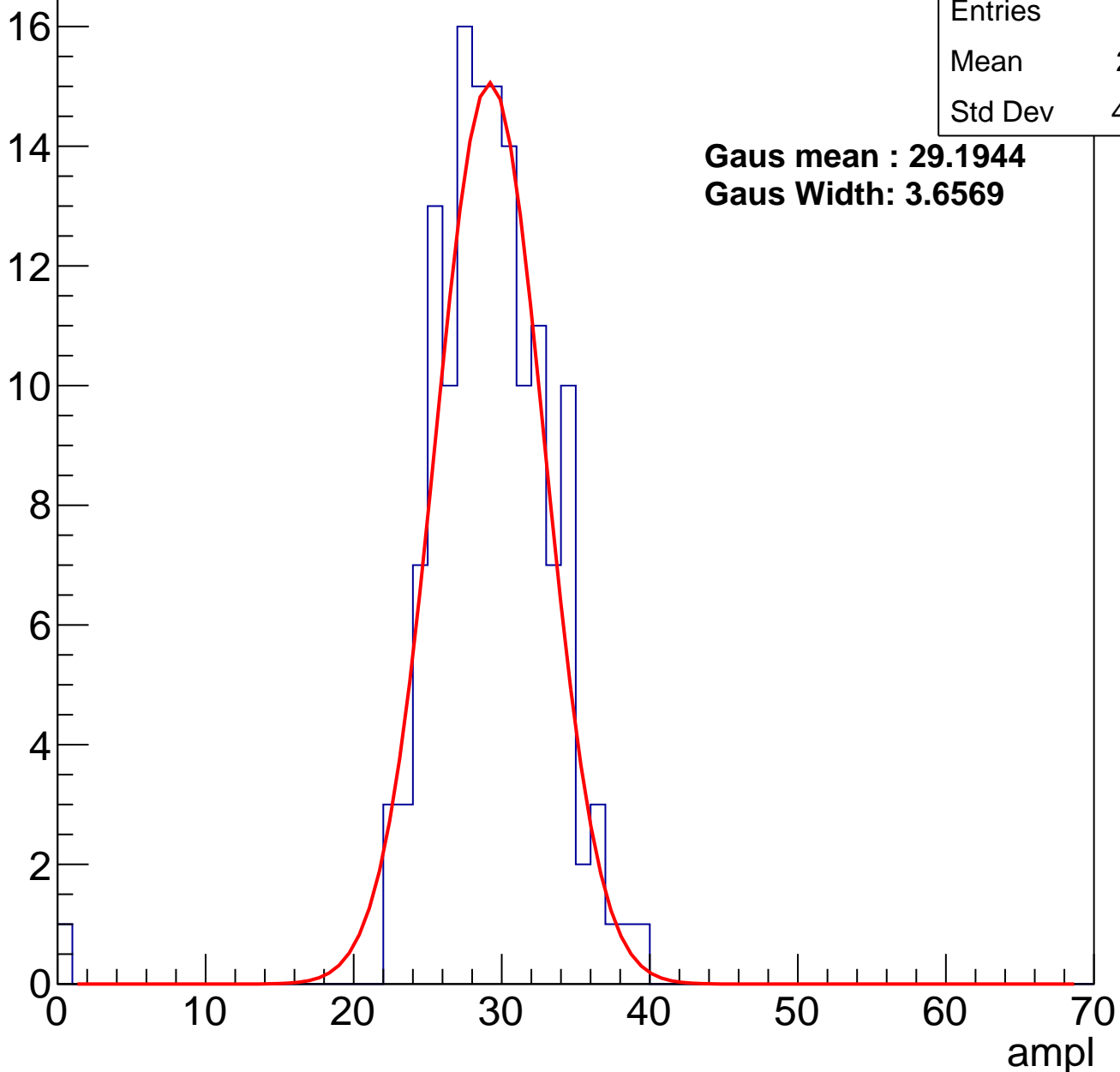
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	28.81
Std Dev	4.282

Gaus mean : 29.144

Gaus Width: 3.6569

Entry



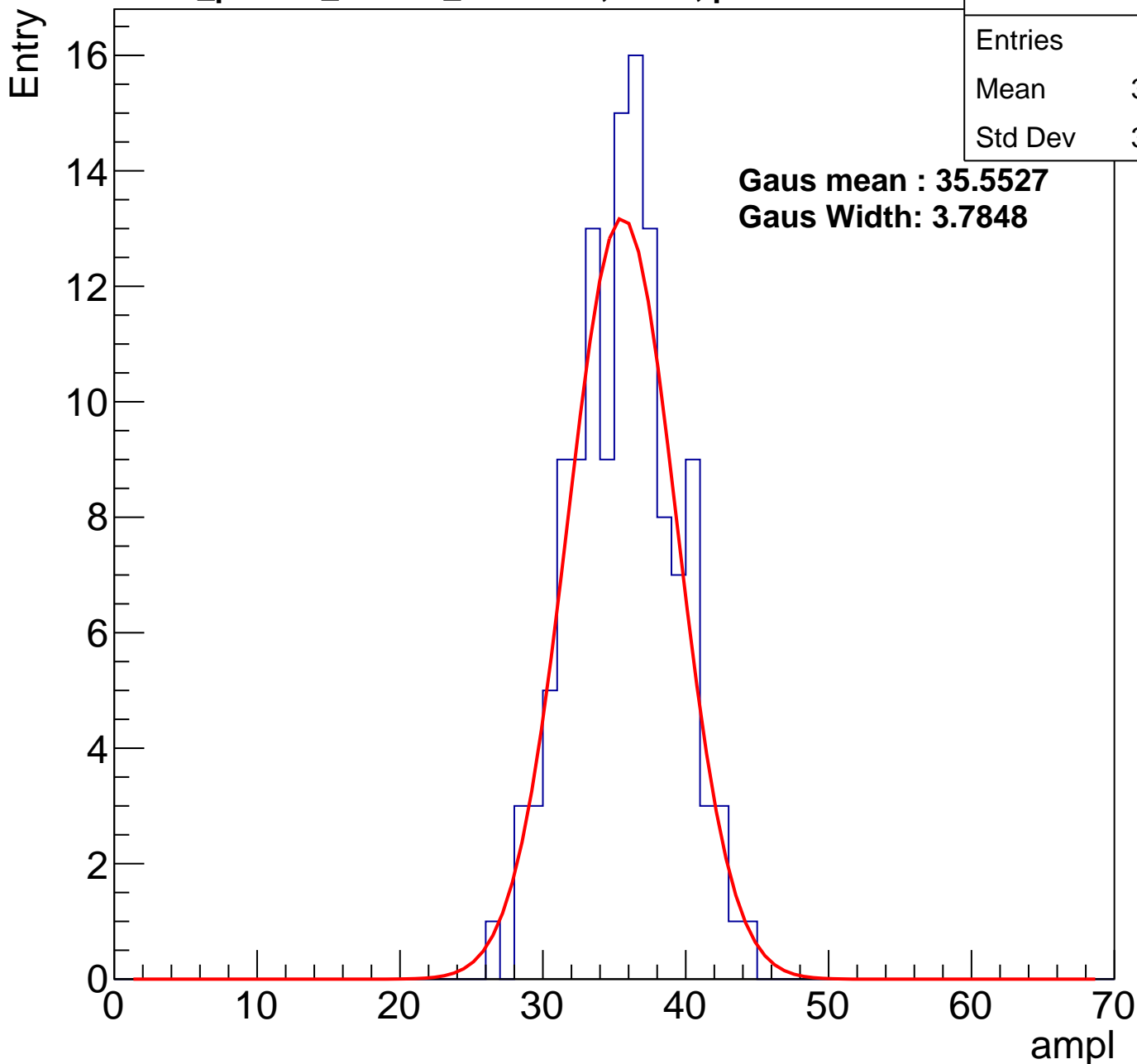
B0L101S, U19-ch36, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	35.19
Std Dev	3.557

Gaus mean : 35.5527

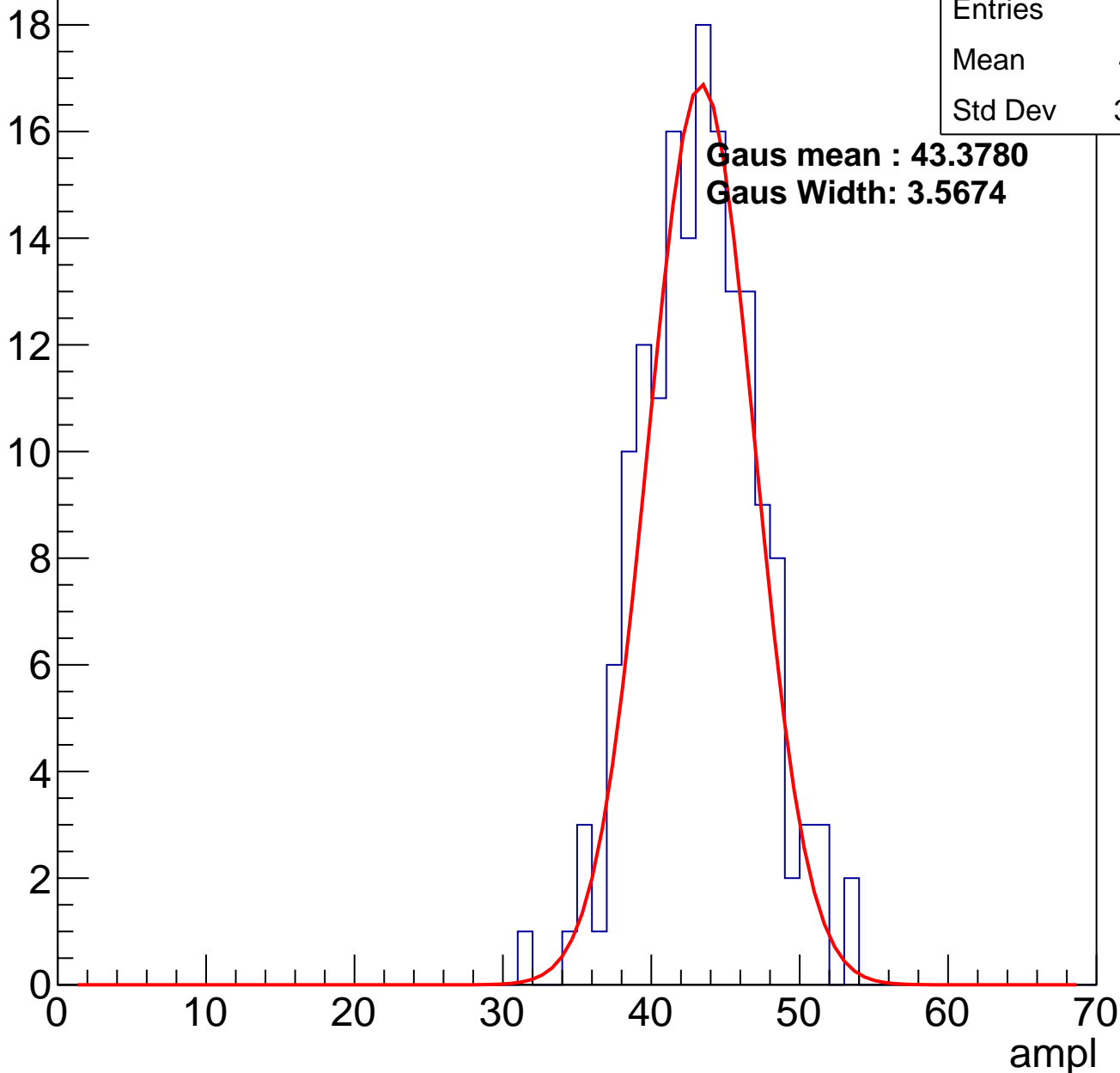
Gaus Width: 3.7848



B0L101S, U19-ch36, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

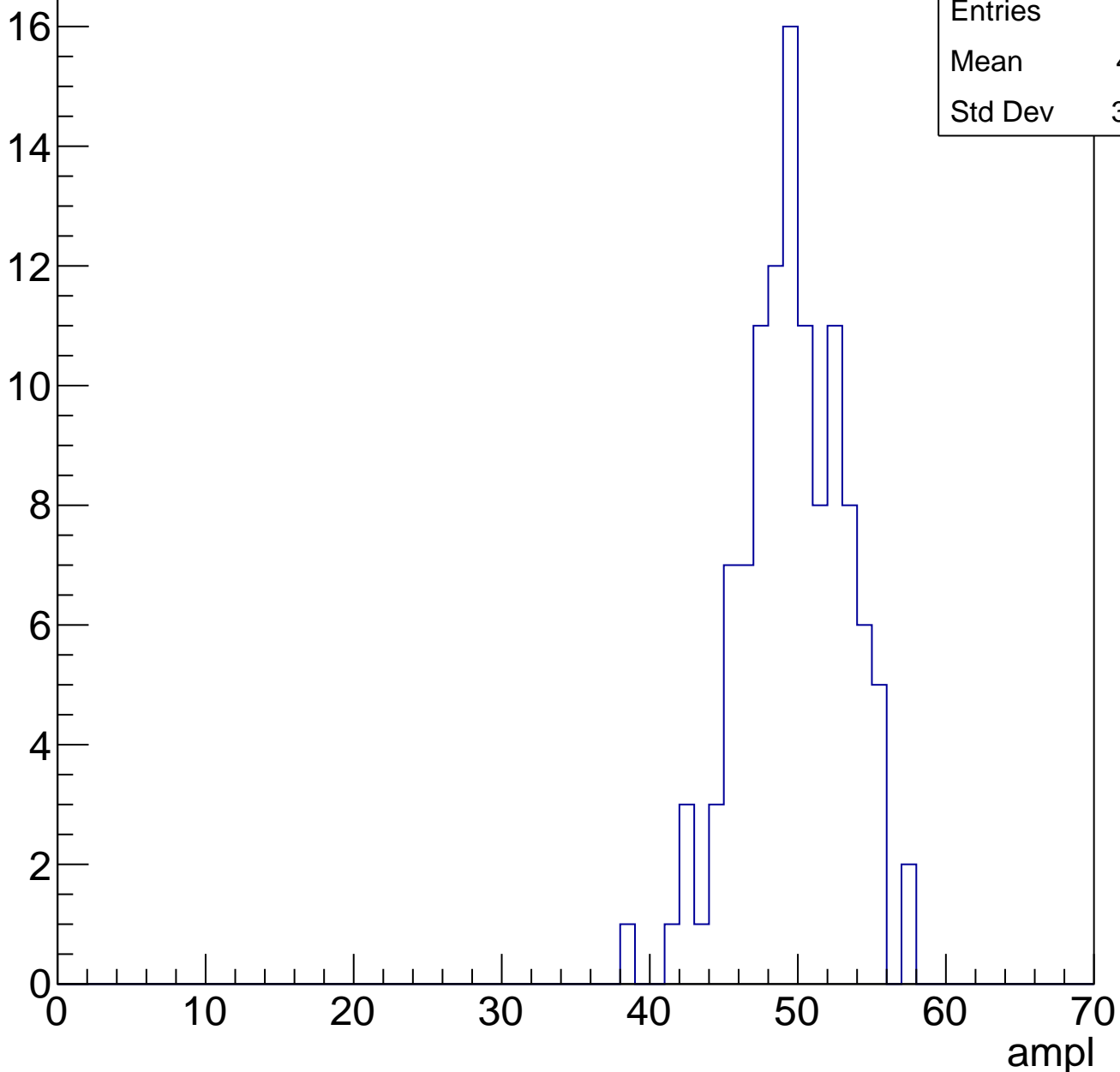


B0L101S, U19-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	49.21
Std Dev	3.516

Entry



B0L101S, U19-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

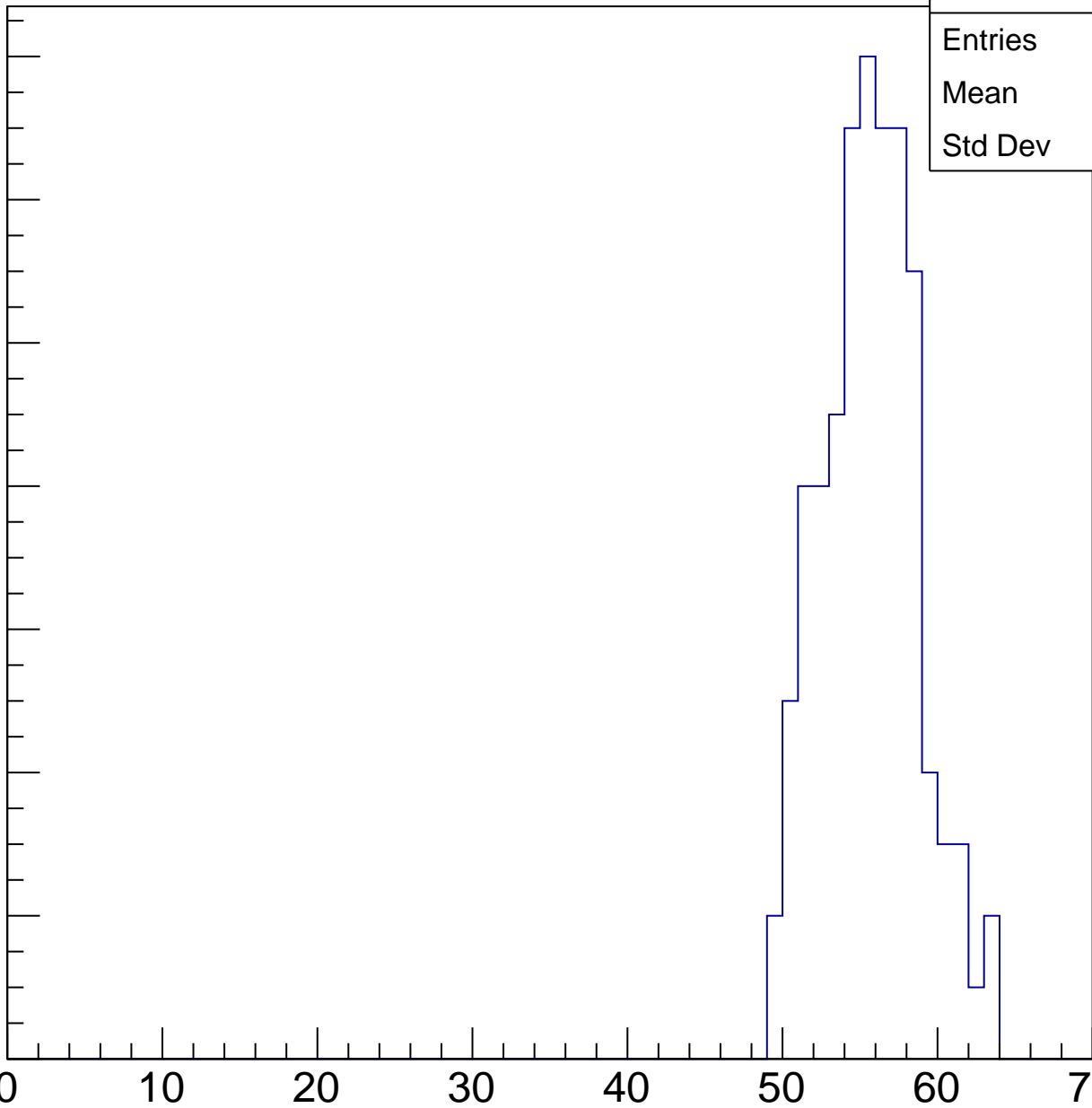
Entries	109
Mean	55.18
Std Dev	3.075

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch36, adc5

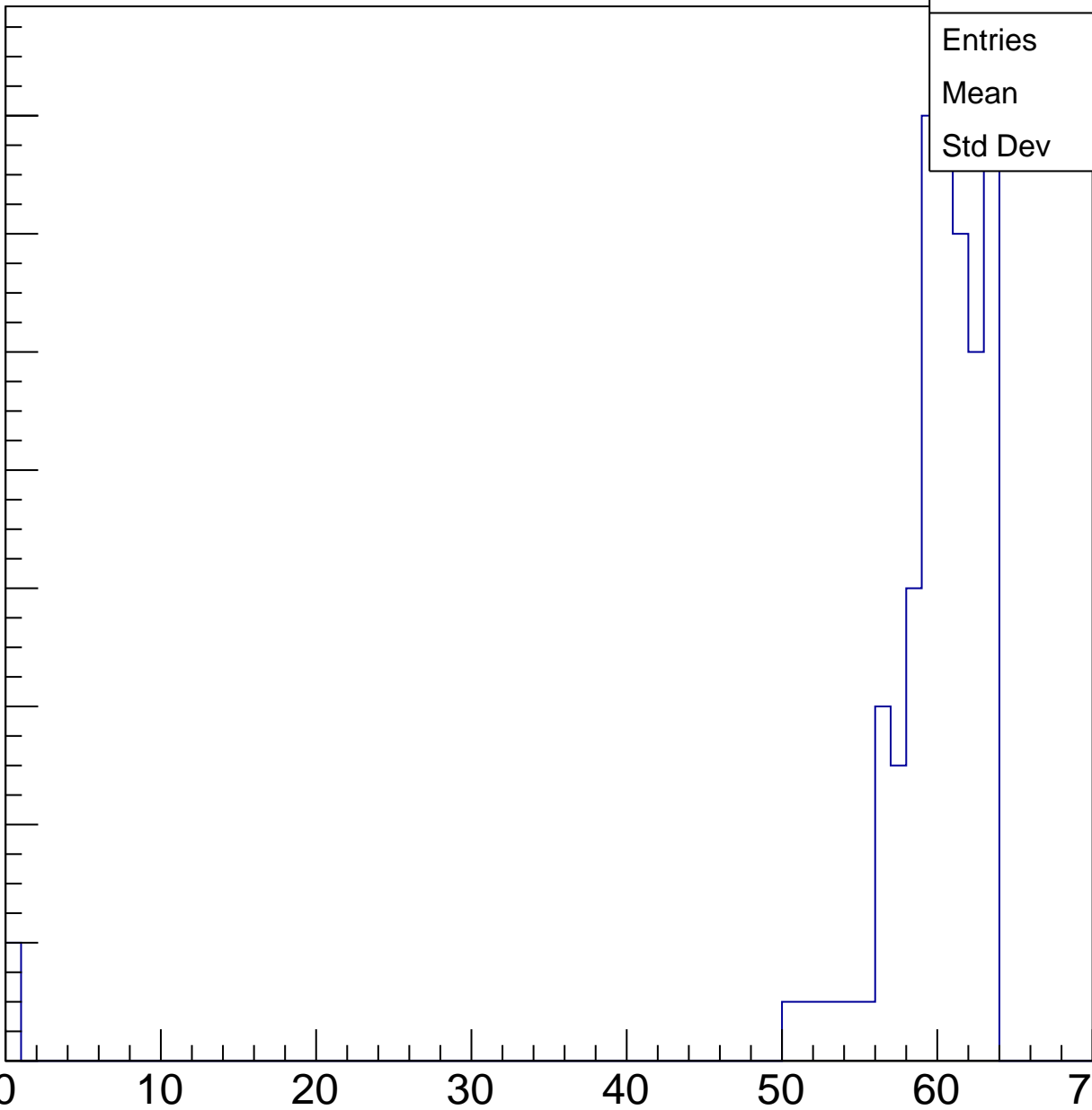
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.53
Std Dev	8.705

ampl

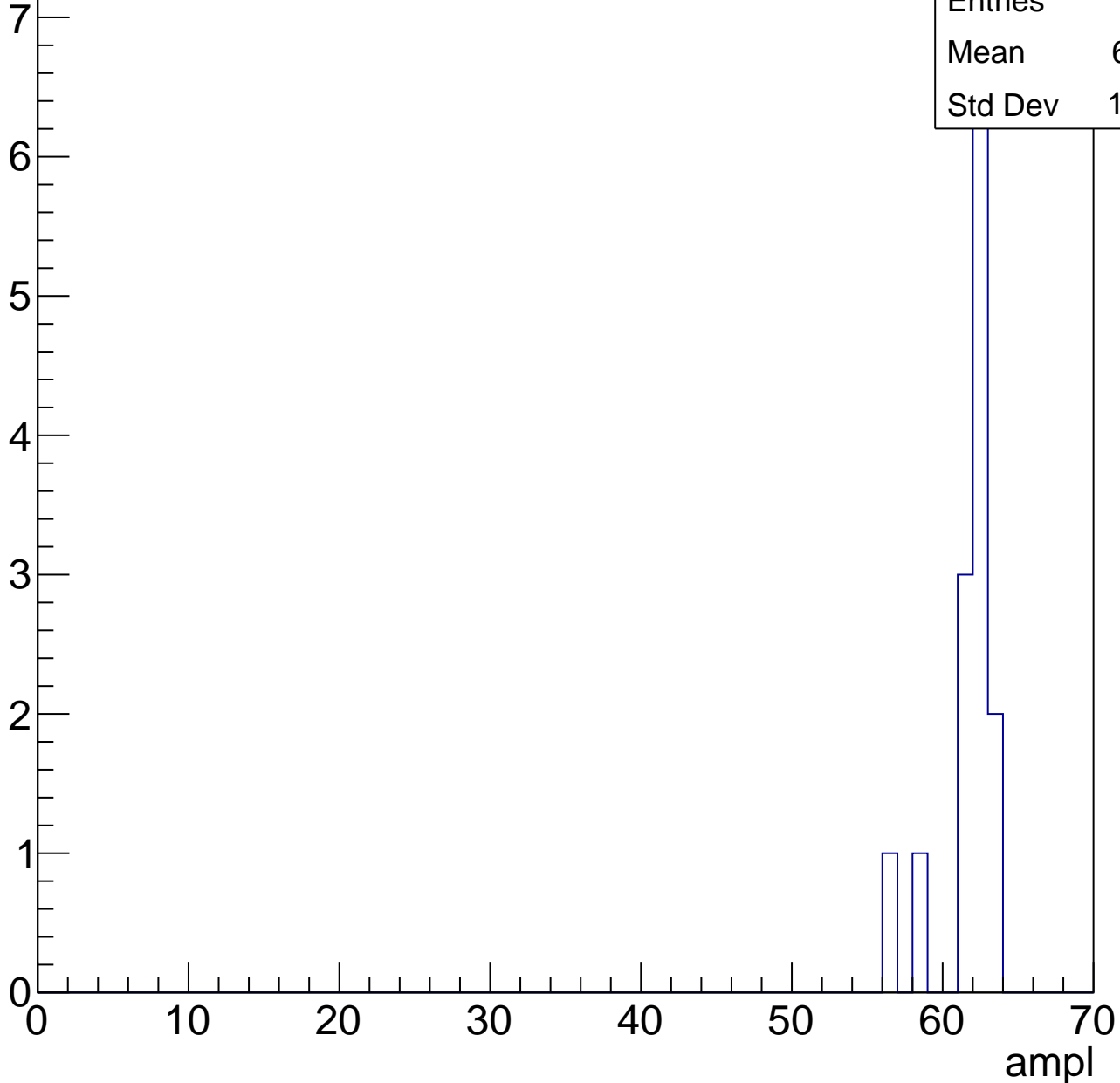


B0L101S, U19-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	61.21
Std Dev	1.859



B0L101S, U19-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch37, adc0

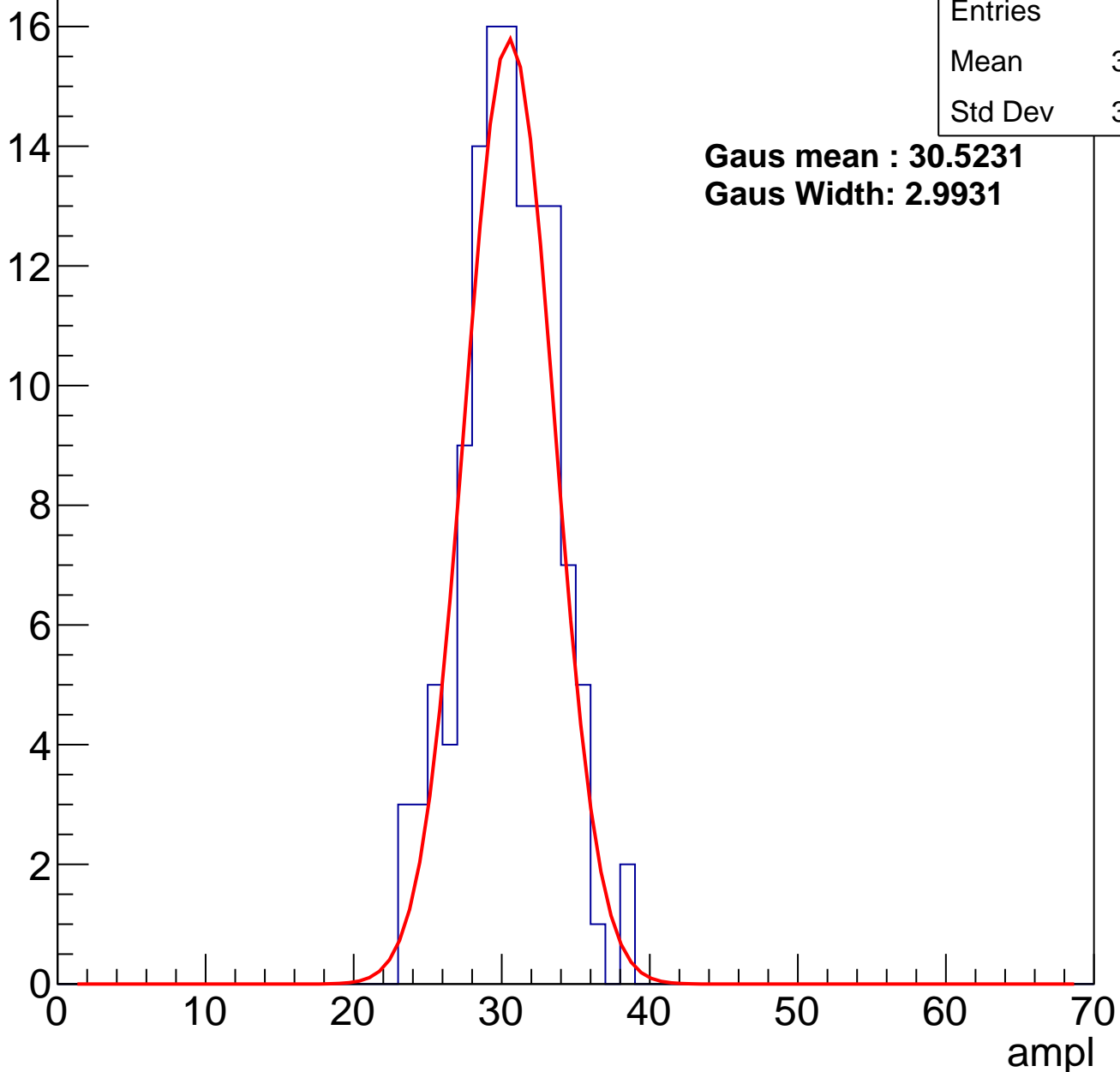
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	30.02
Std Dev	3.085

Gaus mean : 30.5231

Gaus Width: 2.9931

Entry



B0L101S, U19-ch37, adc1

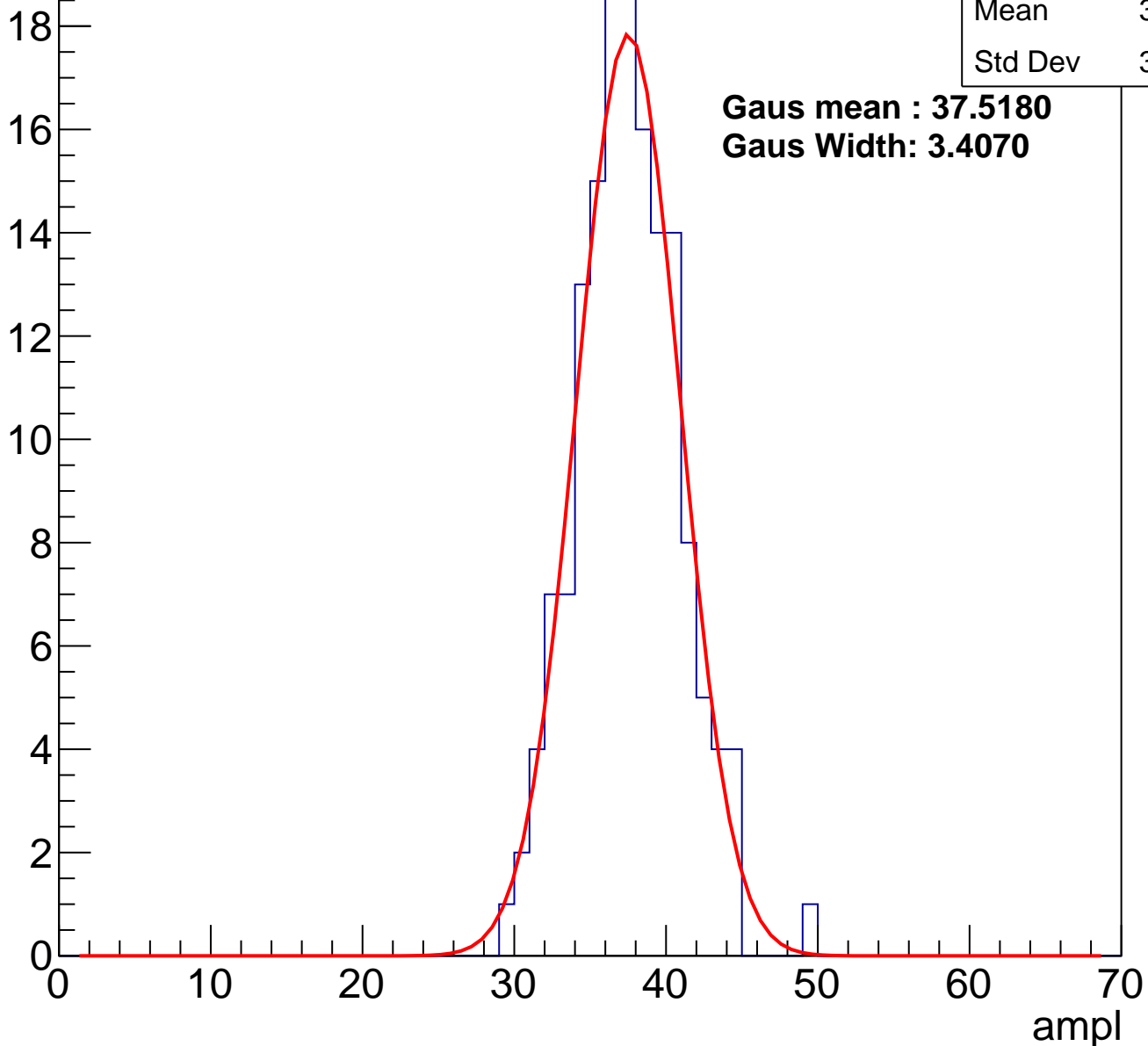
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	37.07
Std Dev	3.356

Gaus mean : 37.5180

Gaus Width: 3.4070

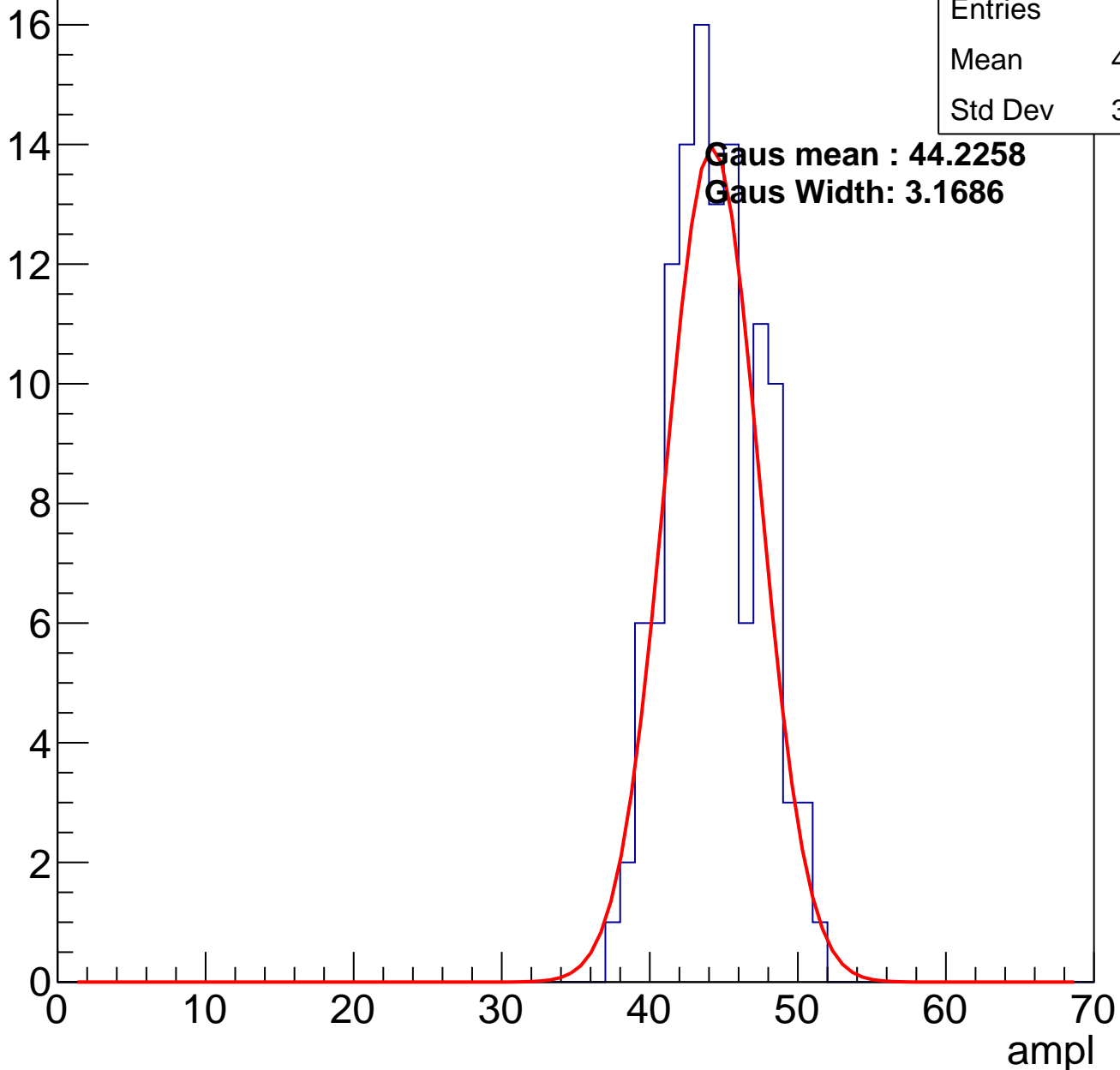
Entry



B0L101S, U19-ch37, adc2

calib_packv5_042523_0143.root, FC#1, port C1

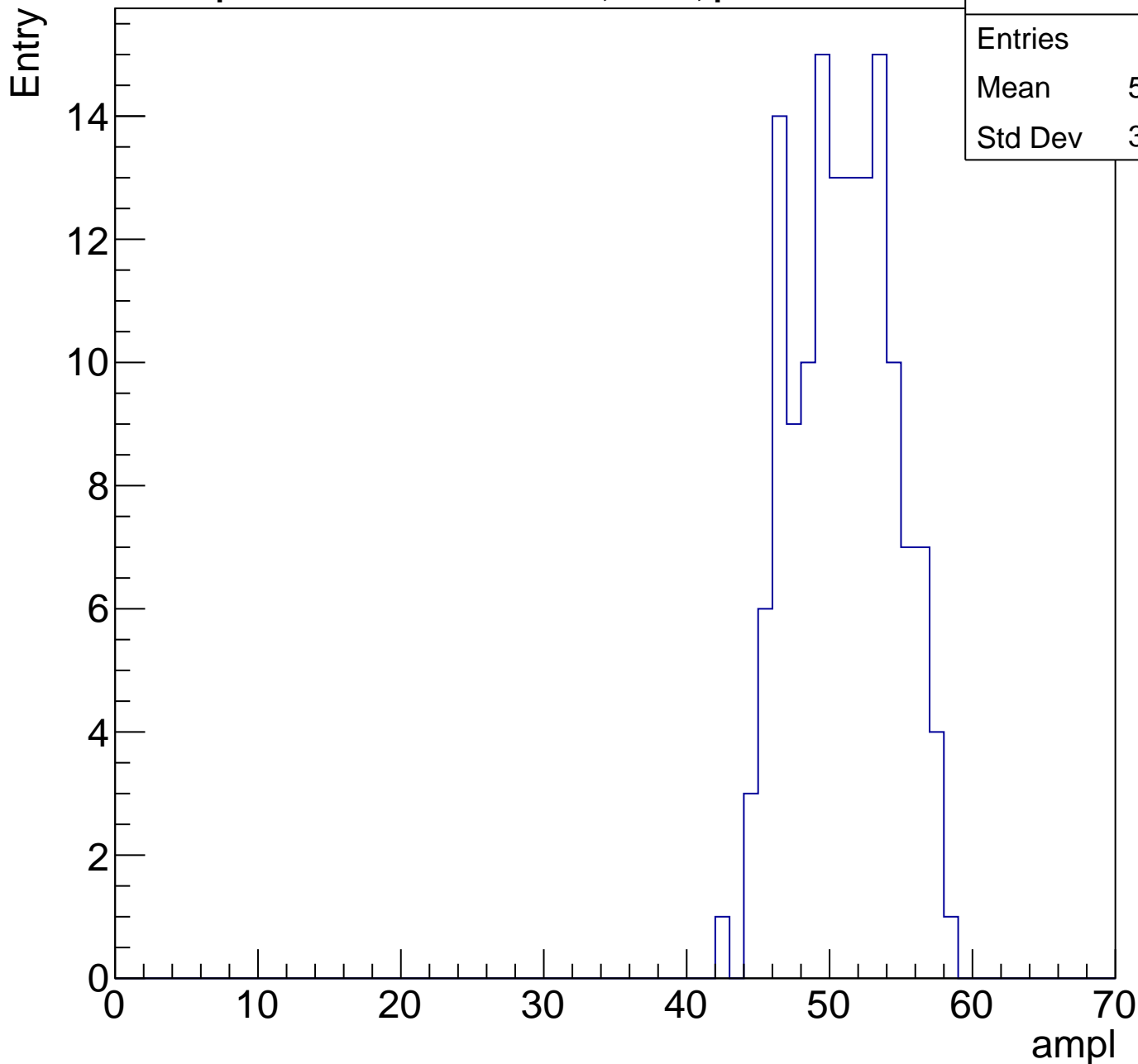
Entry



B0L101S, U19-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	50.45
Std Dev	3.473



B0L101S, U19-ch37, adc4

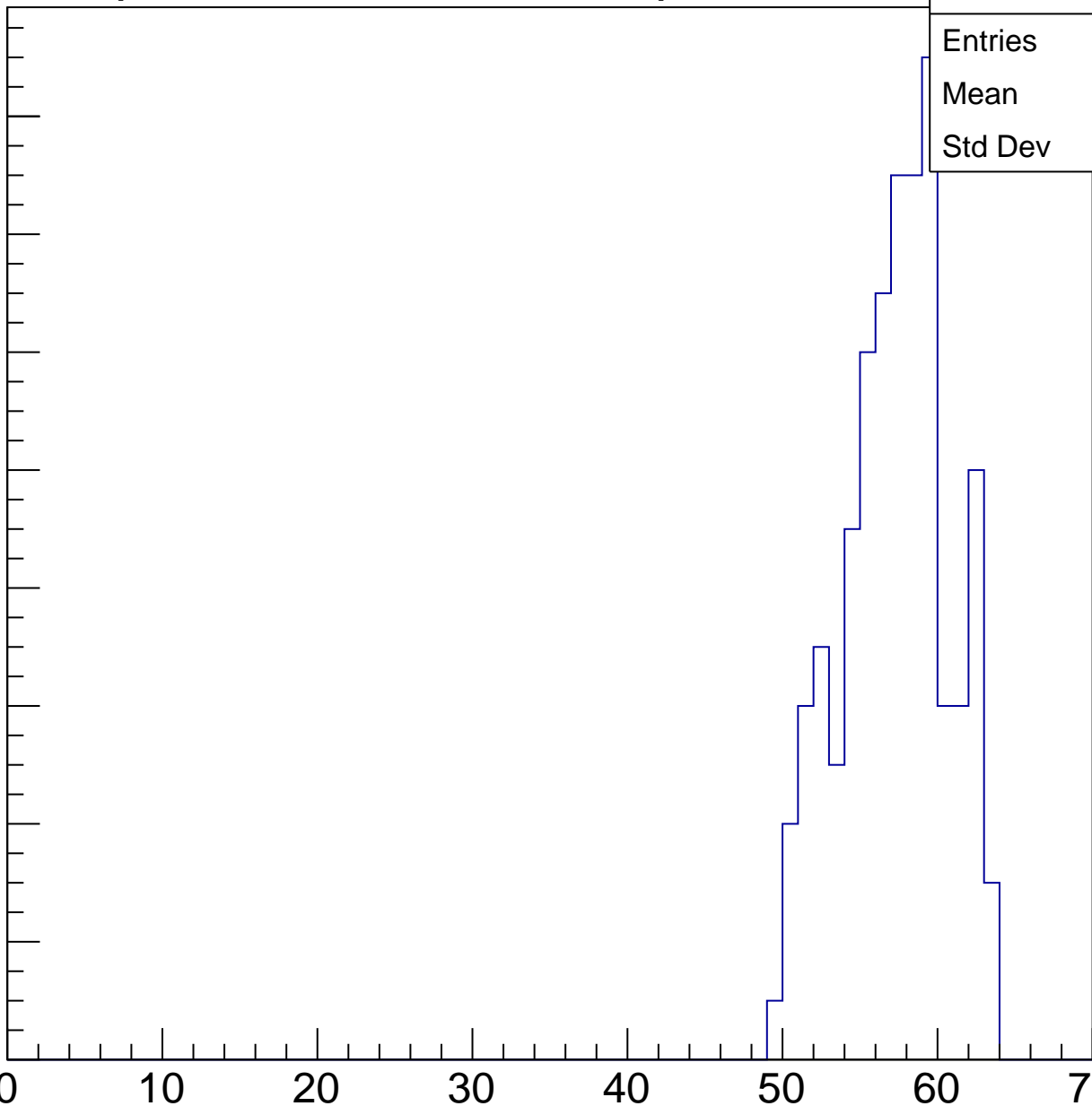
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	56.75
Std Dev	3.36

ampl



B0L101S, U19-ch37, adc5

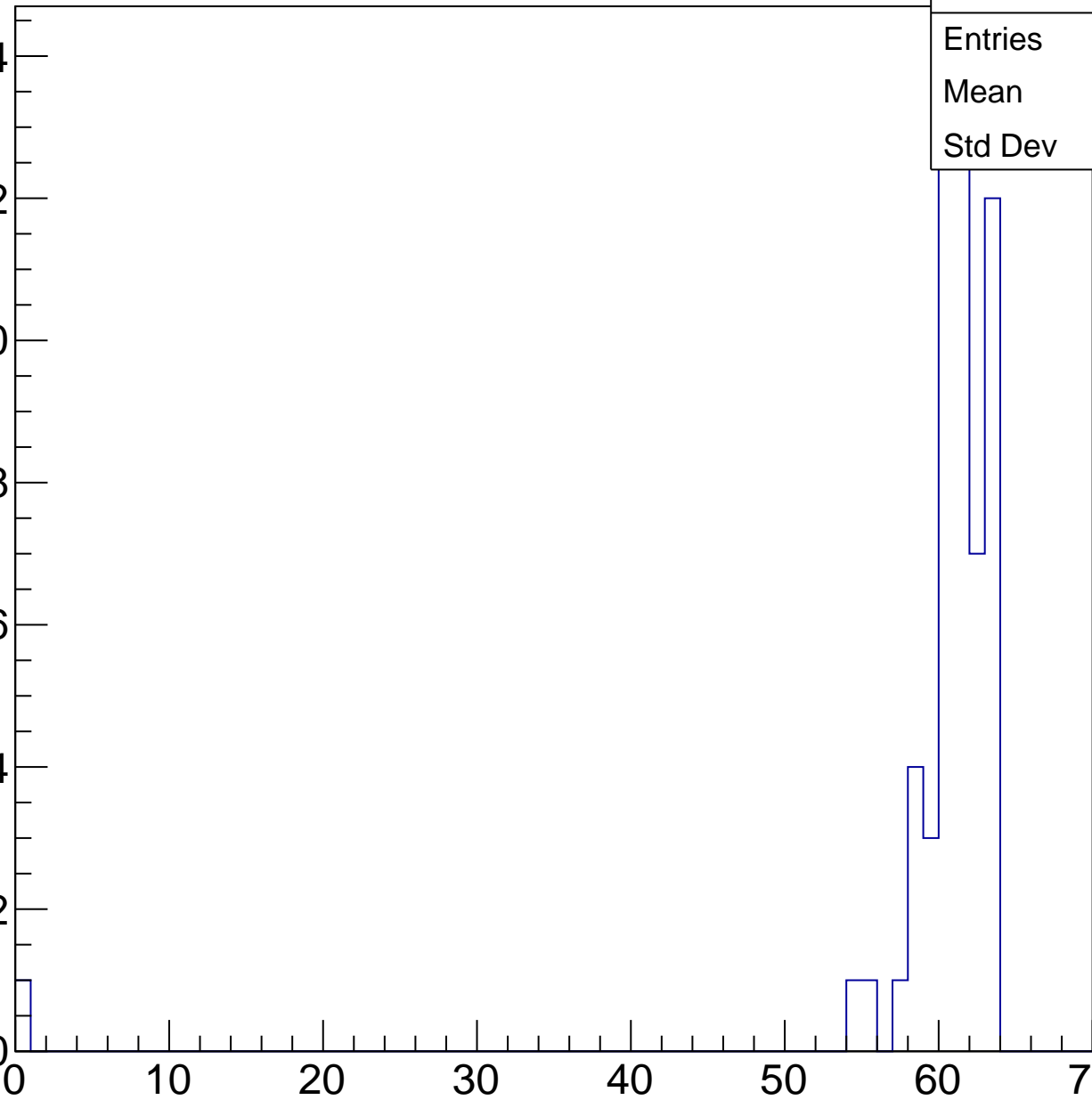
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	59.64
Std Dev	8.128

ampl

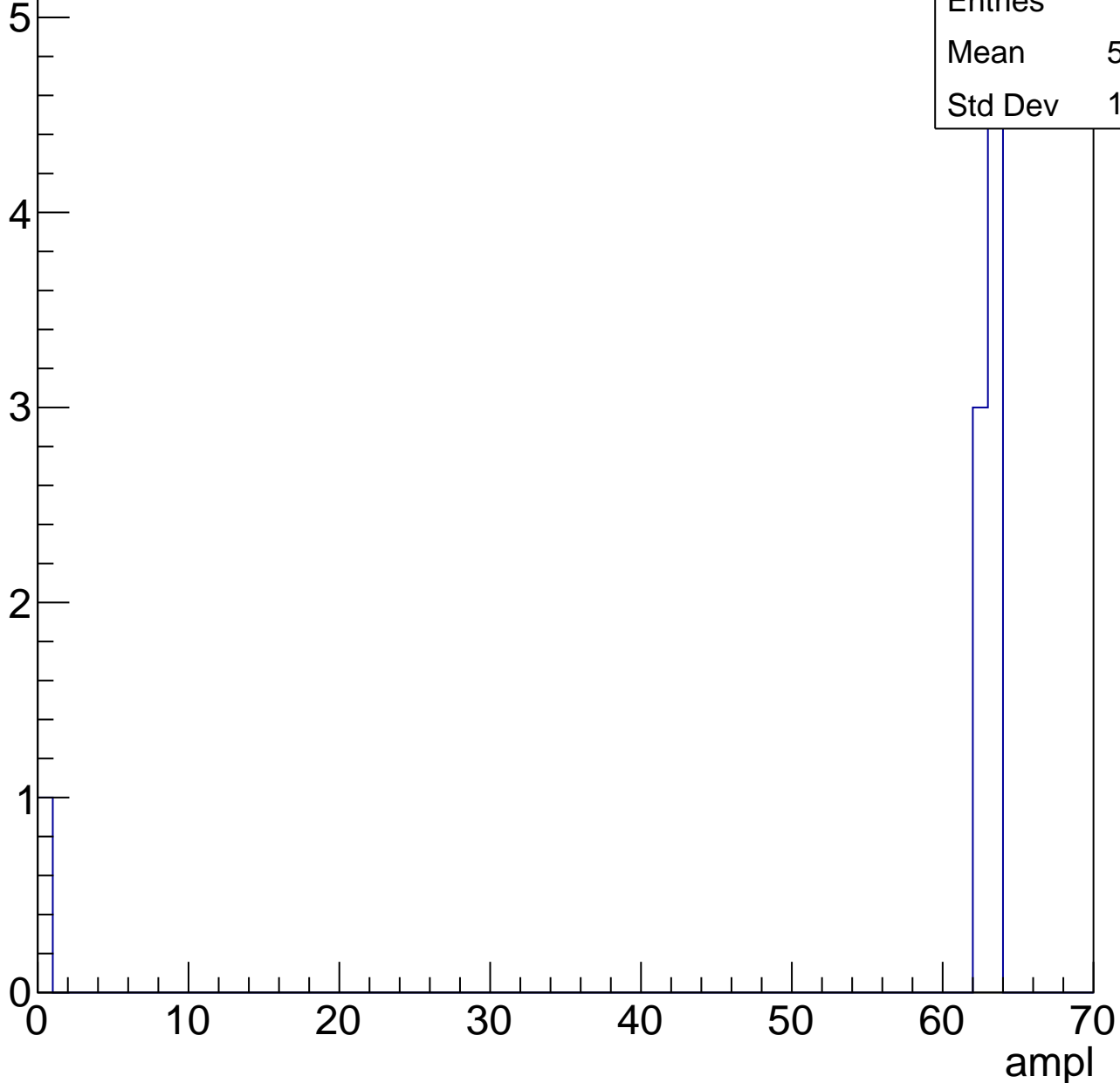


B0L101S, U19-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

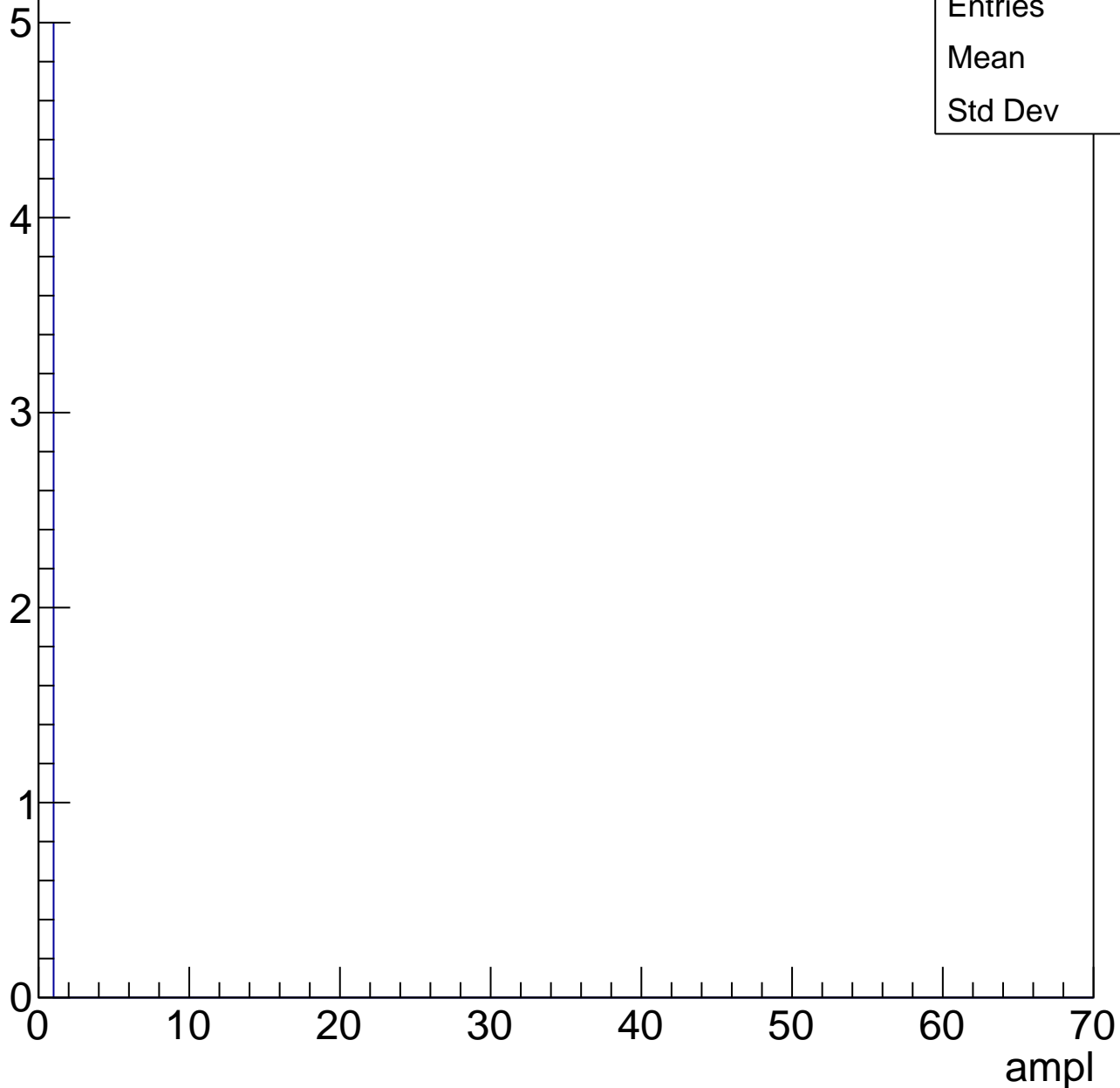
Entries	9
Mean	55.67
Std Dev	19.69



B0L101S, U19-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	0
Std Dev	0

B0L101S, U19-ch38, adc0

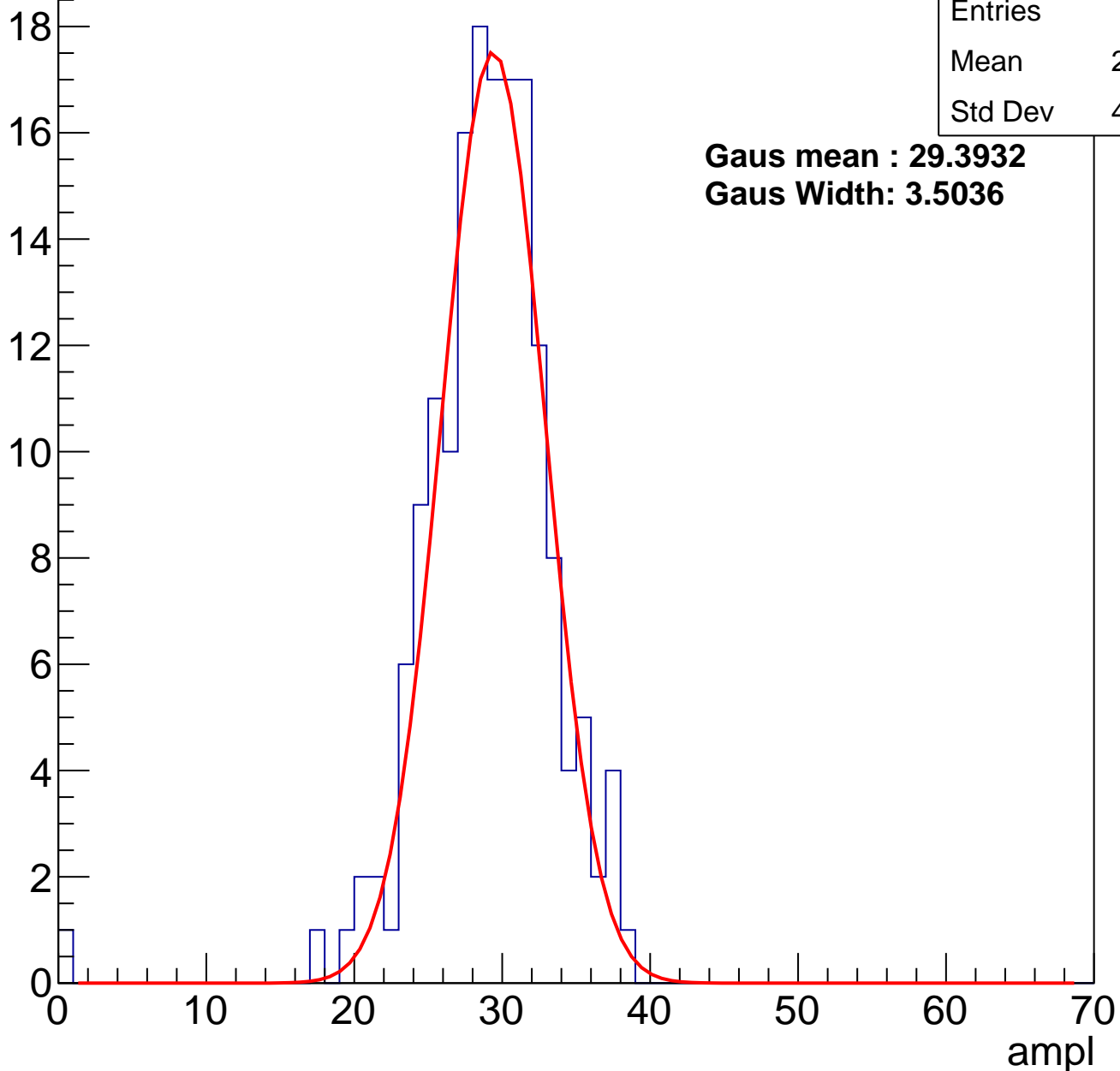
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	28.56
Std Dev	4.415

Gaus mean : 29.3932

Gaus Width: 3.5036

Entry

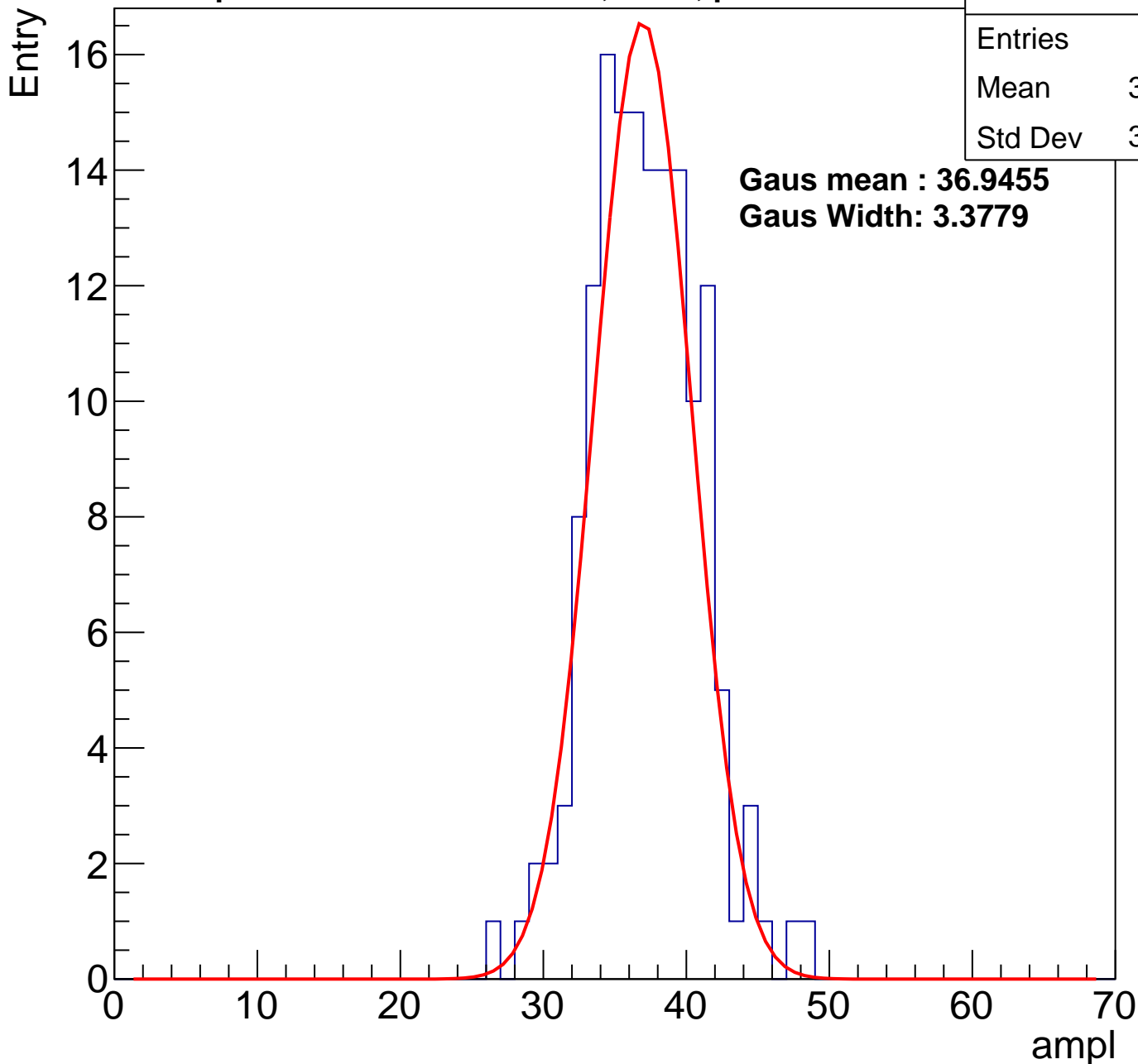


B0L101S, U19-ch38, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	36.68
Std Dev	3.709

Gaus mean : 36.9455
Gaus Width: 3.3779



B0L101S, U19-ch38, adc2

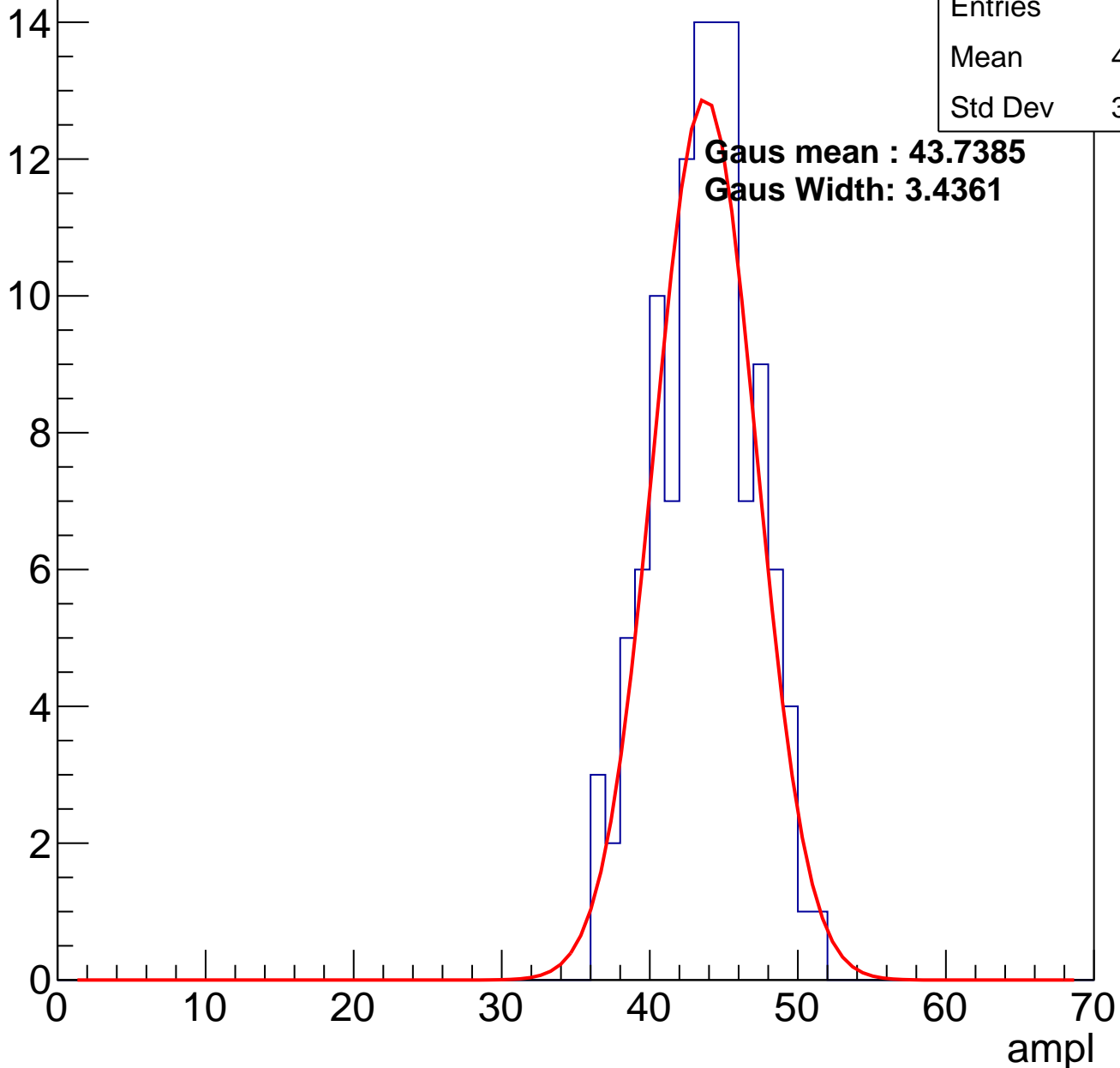
calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	43.26
Std Dev	3.292

Gaus mean : 43.7385

Gaus Width: 3.4361

Entry



B0L101S, U19-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	48.67
Std Dev	3.442

Entry

12

10

8

6

4

2

0

0

10

20

30

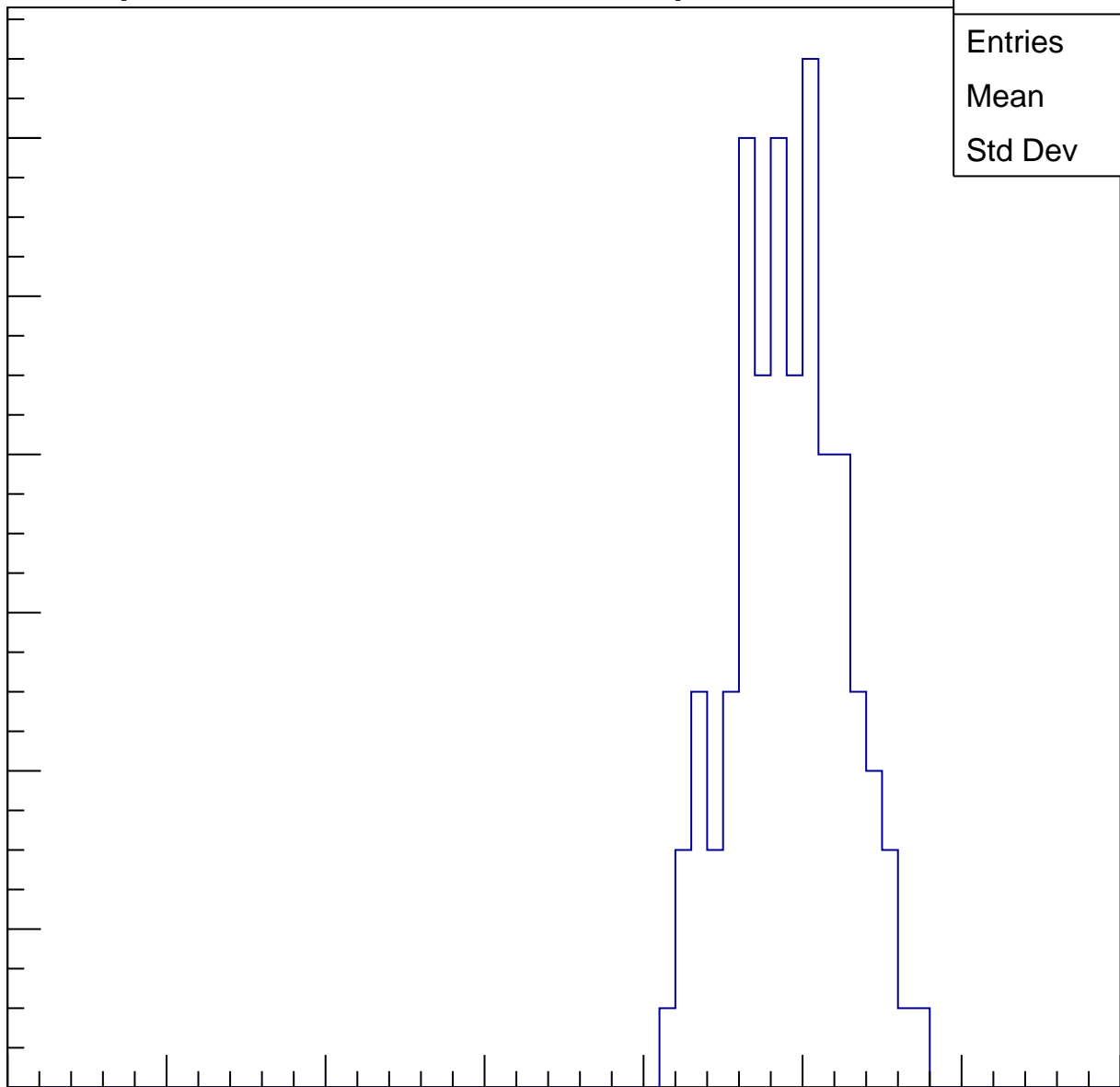
40

50

60

70

ampl



B0L101S, U19-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

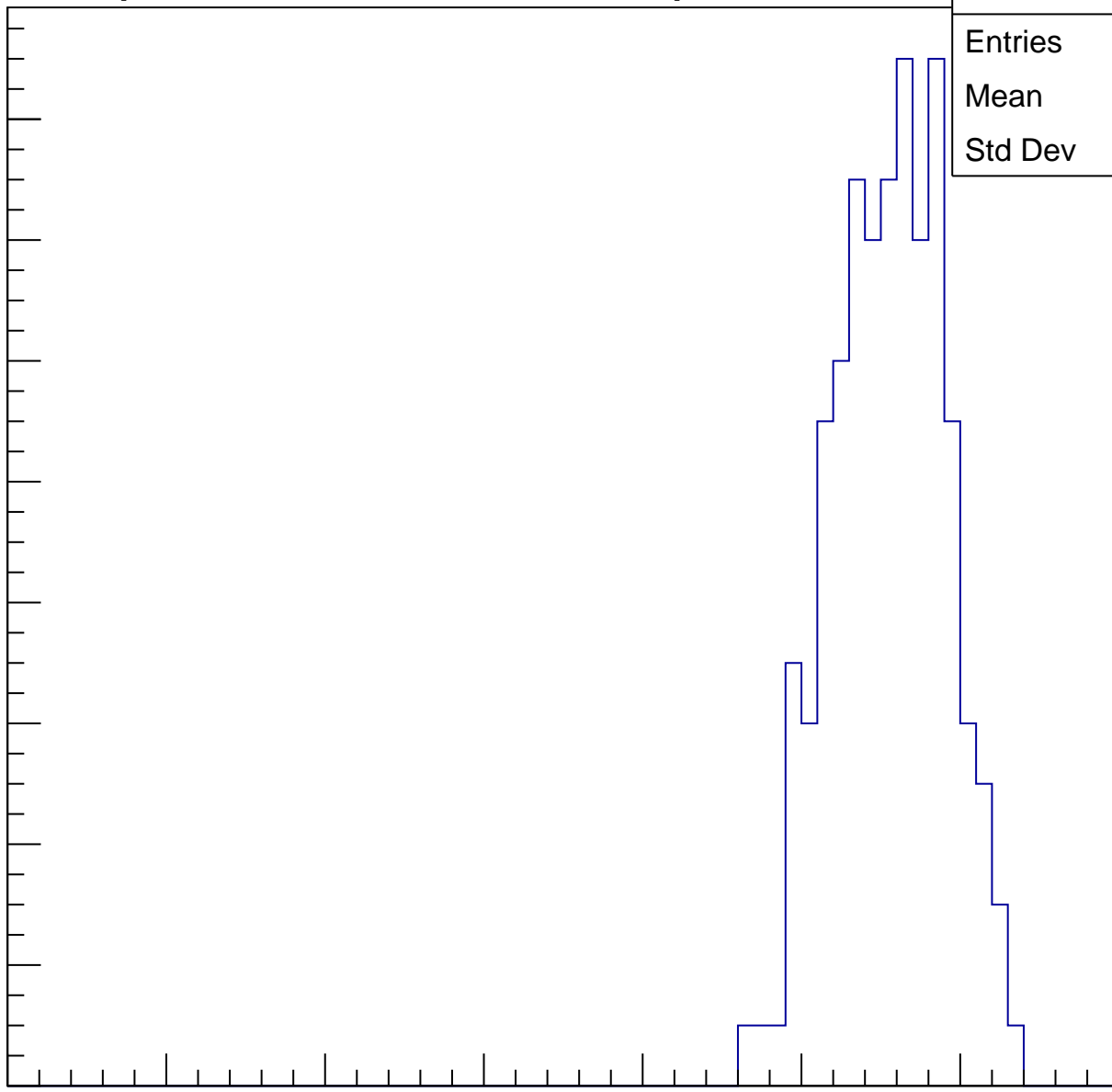
Entries	157
Mean	55.06
Std Dev	3.476

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

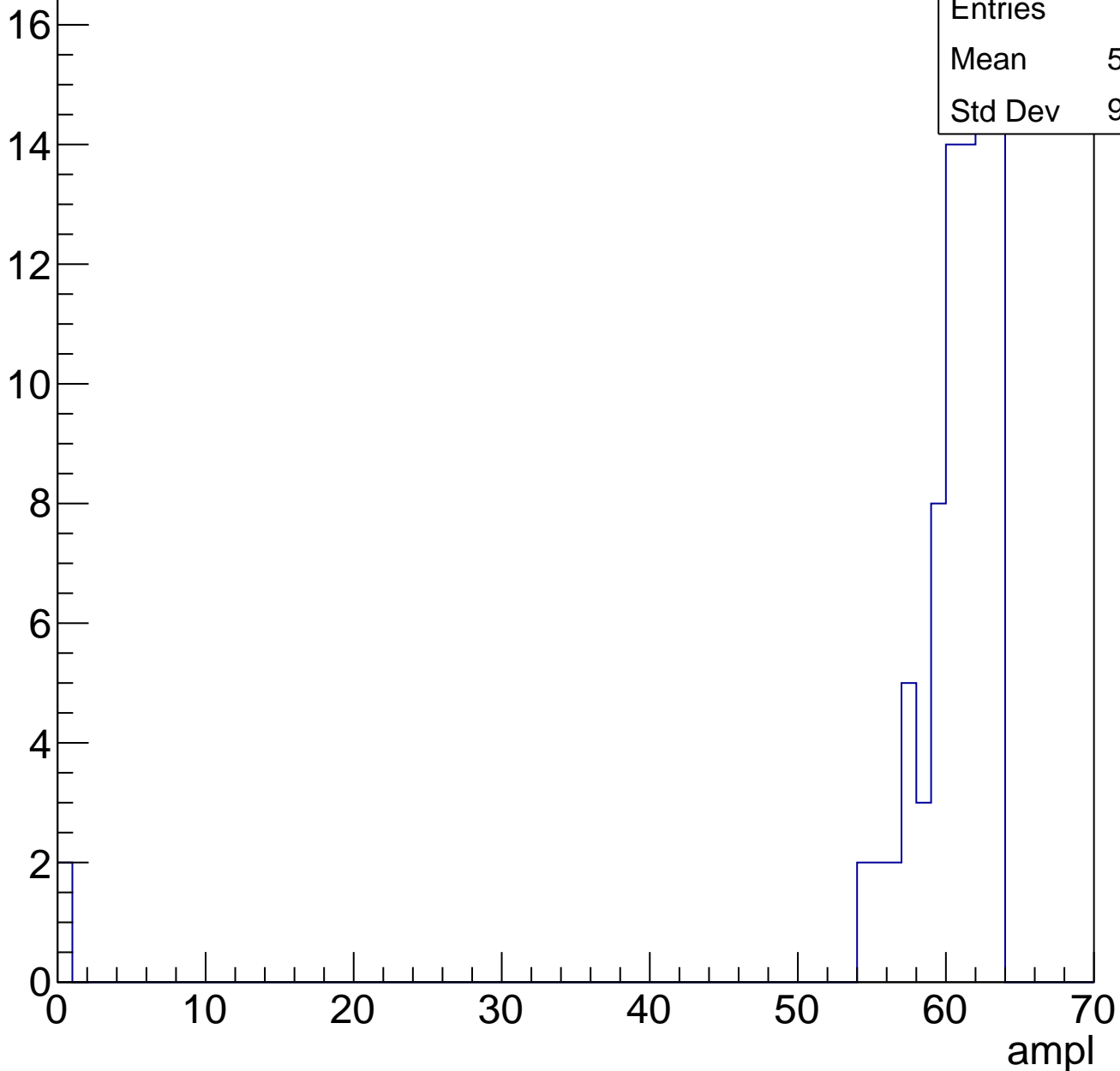
ampl



B0L101S, U19-ch38, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

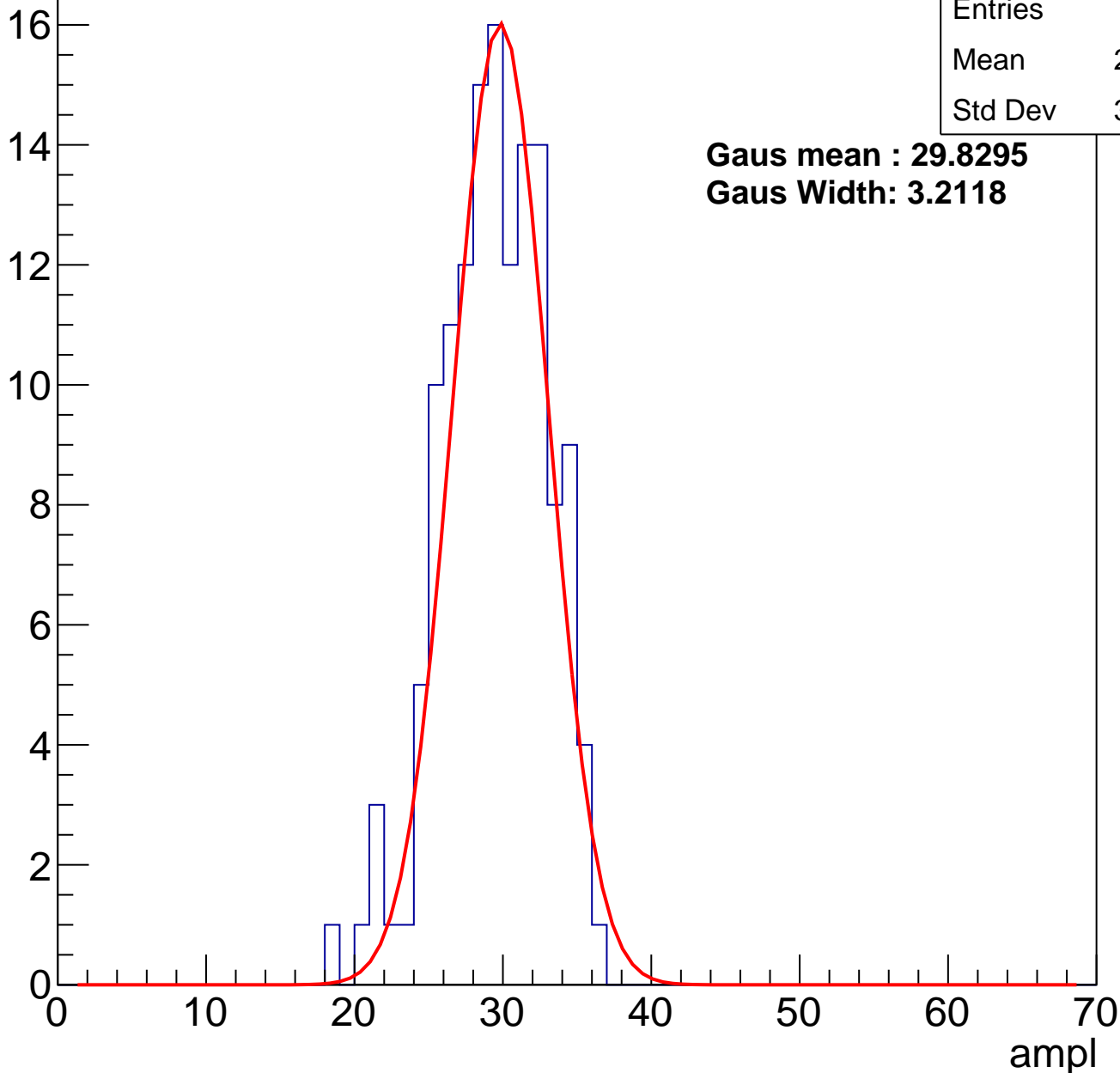


Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch39, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch39, adc1

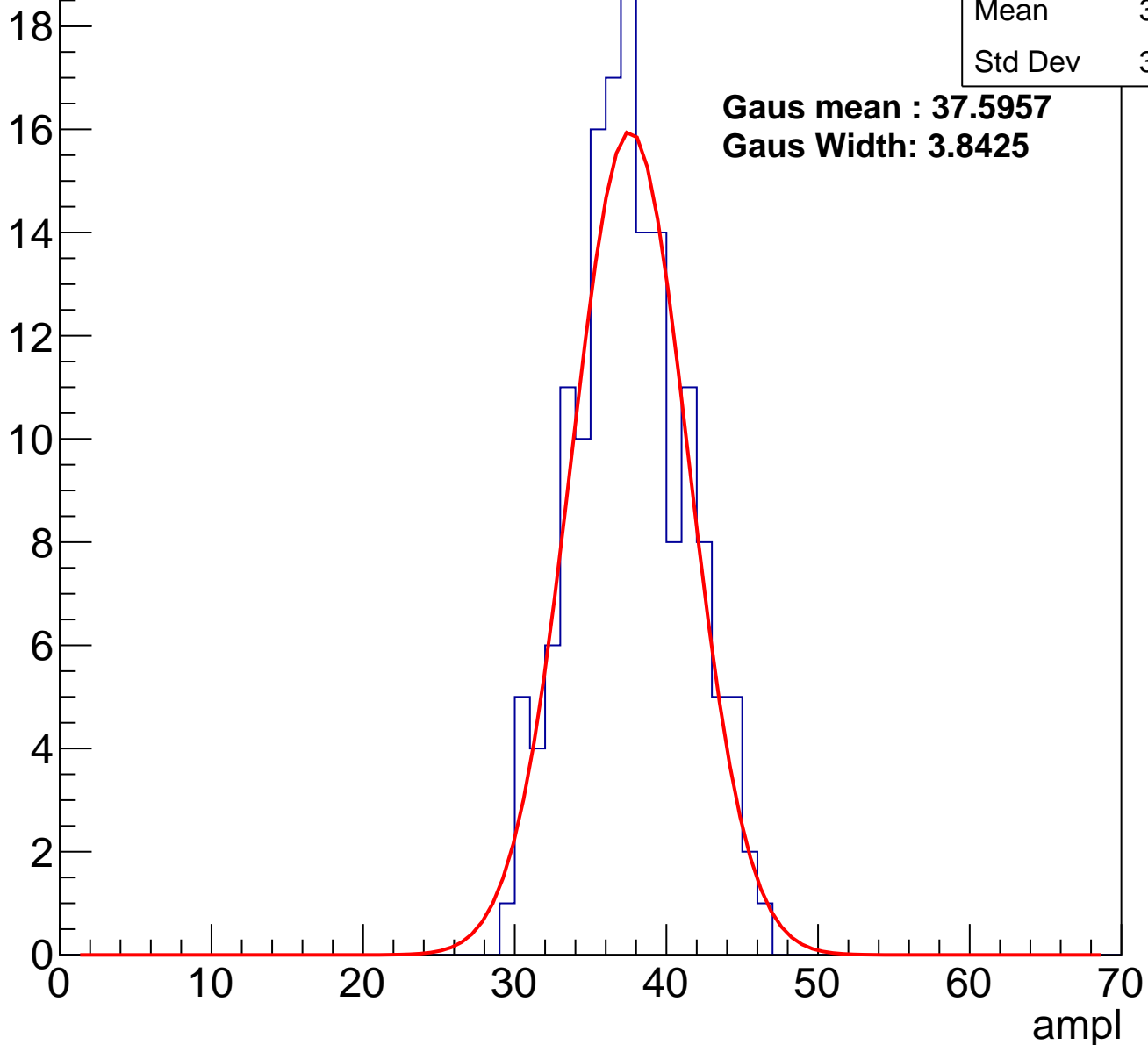
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	37.13
Std Dev	3.657

Gaus mean : 37.5957

Gaus Width: 3.8425

Entry



B0L101S, U19-ch39, adc2

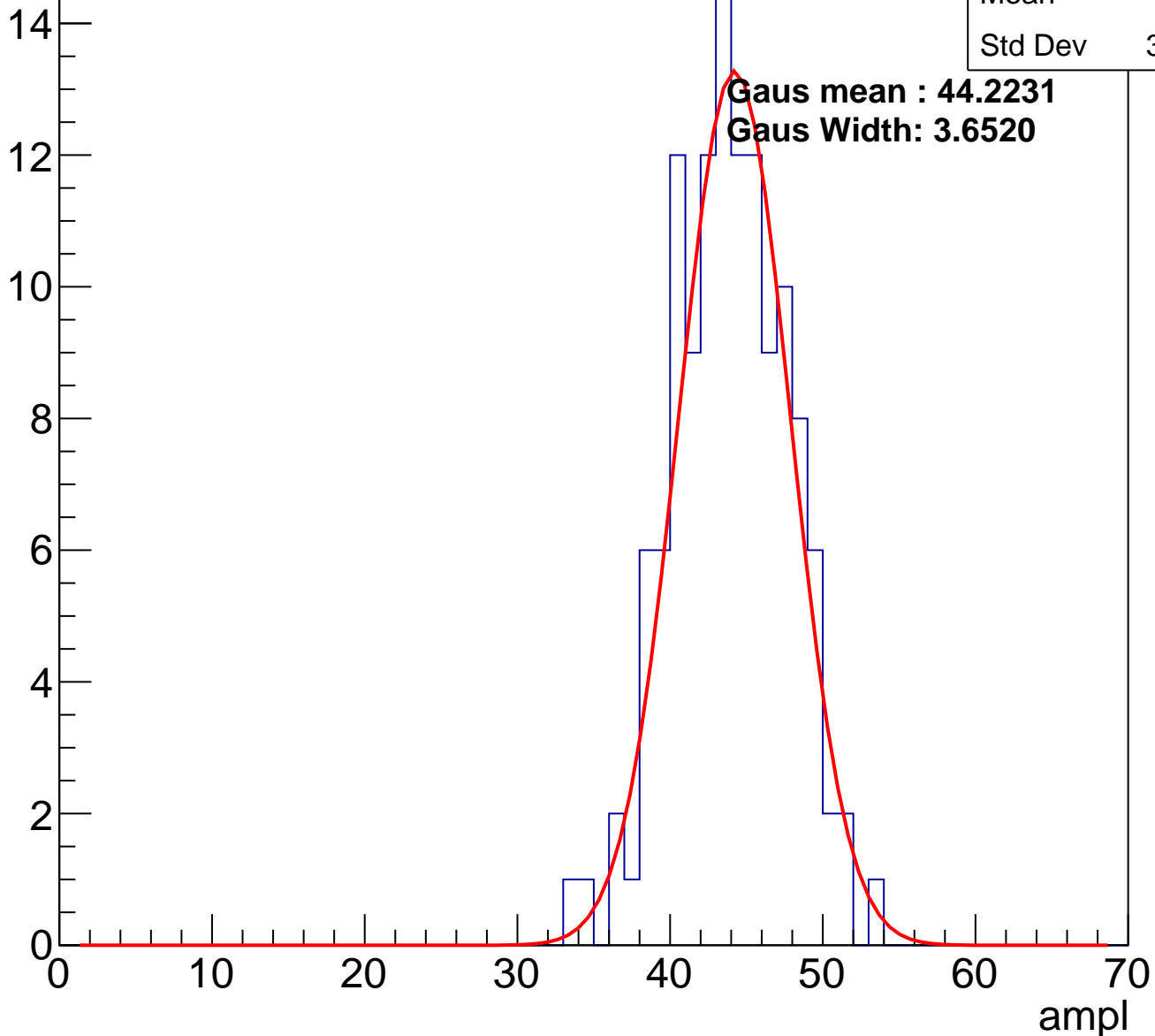
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	43.47
Std Dev	3.693

Gaus mean : 44.2231

Gaus Width: 3.6520

Entry



B0L101S, U19-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

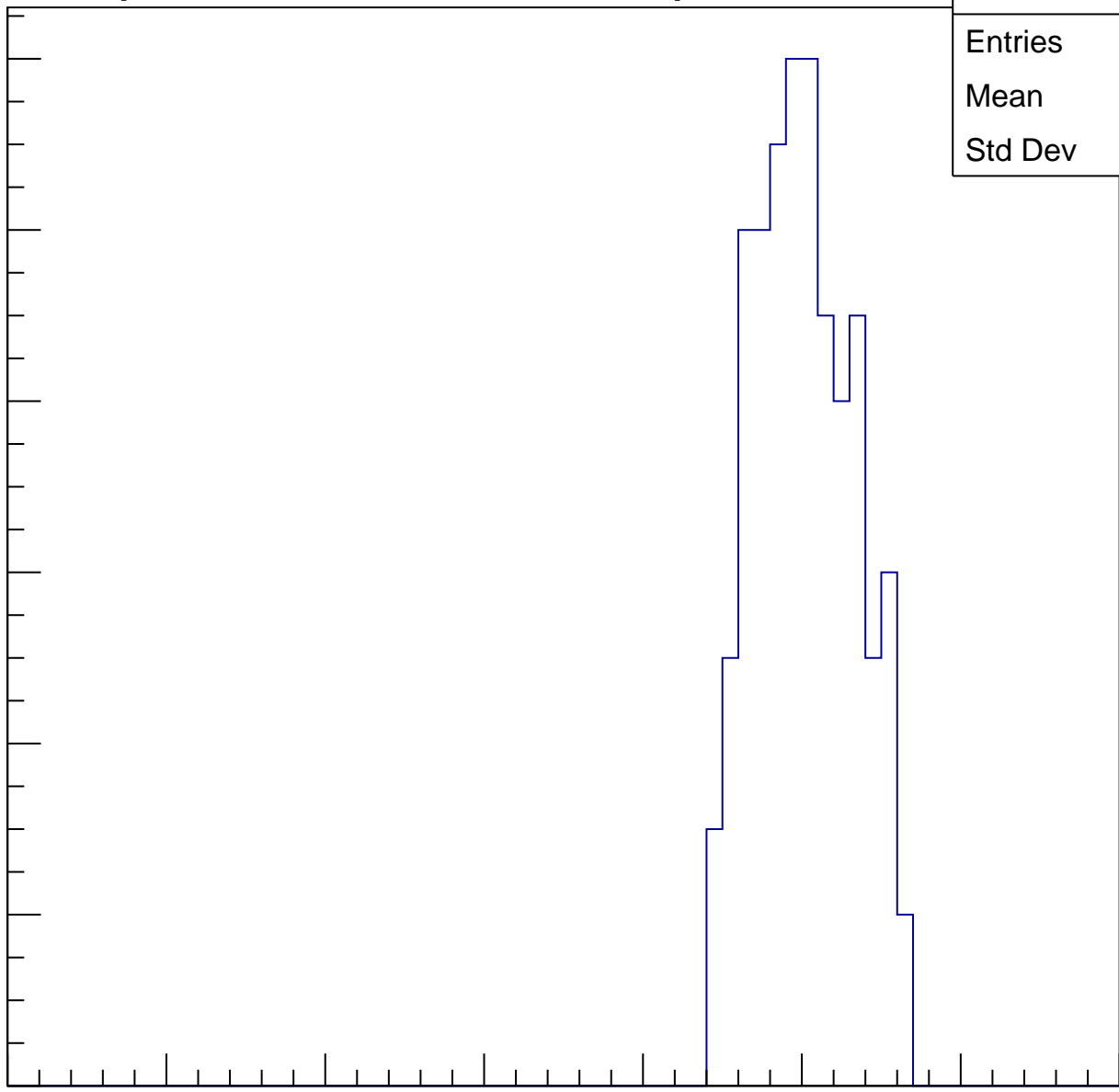
Entries	102
Mean	49.68
Std Dev	3.056

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch39, adc4

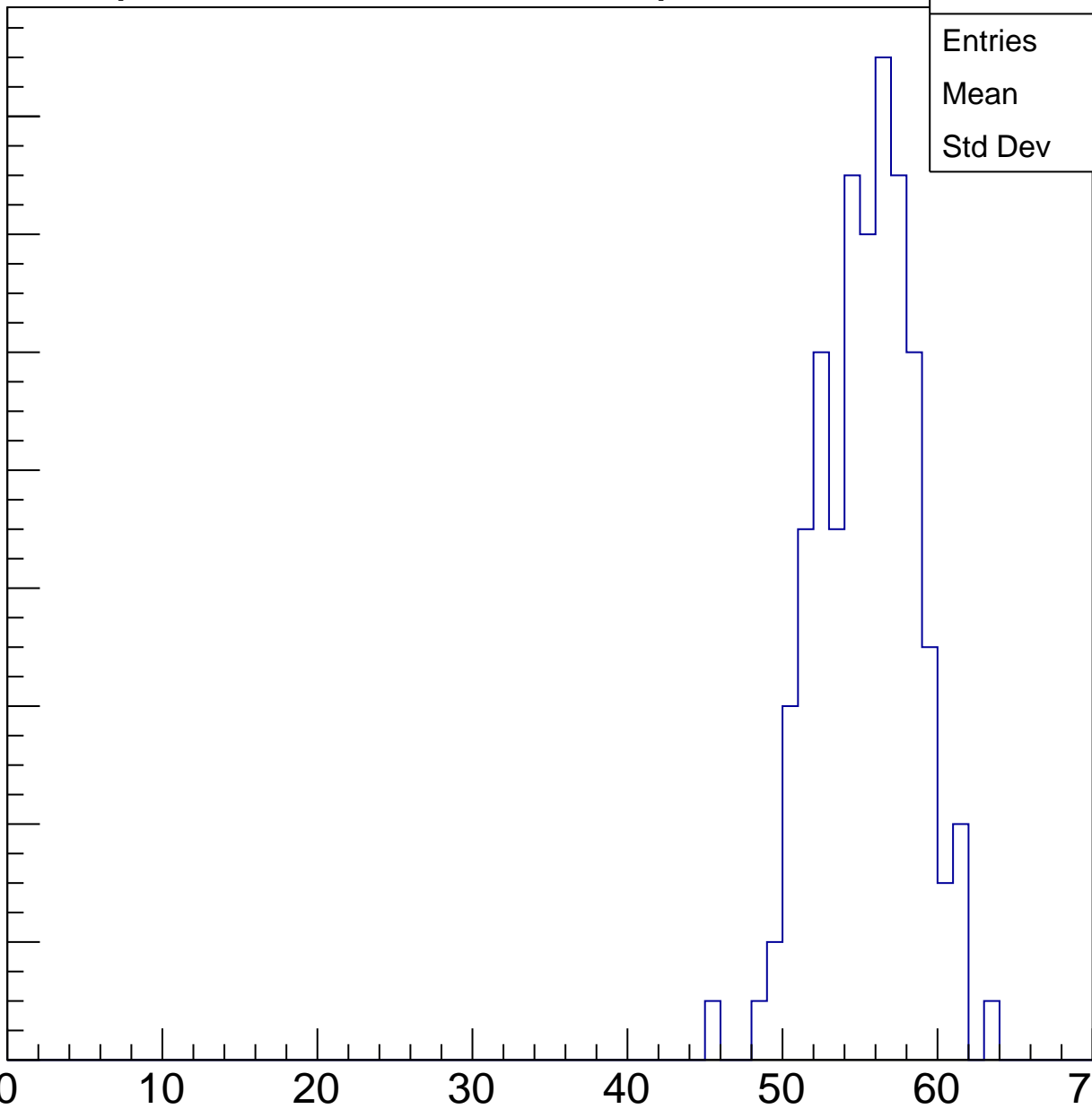
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	54.95
Std Dev	3.13

ampl



B0L101S, U19-ch39, adc5

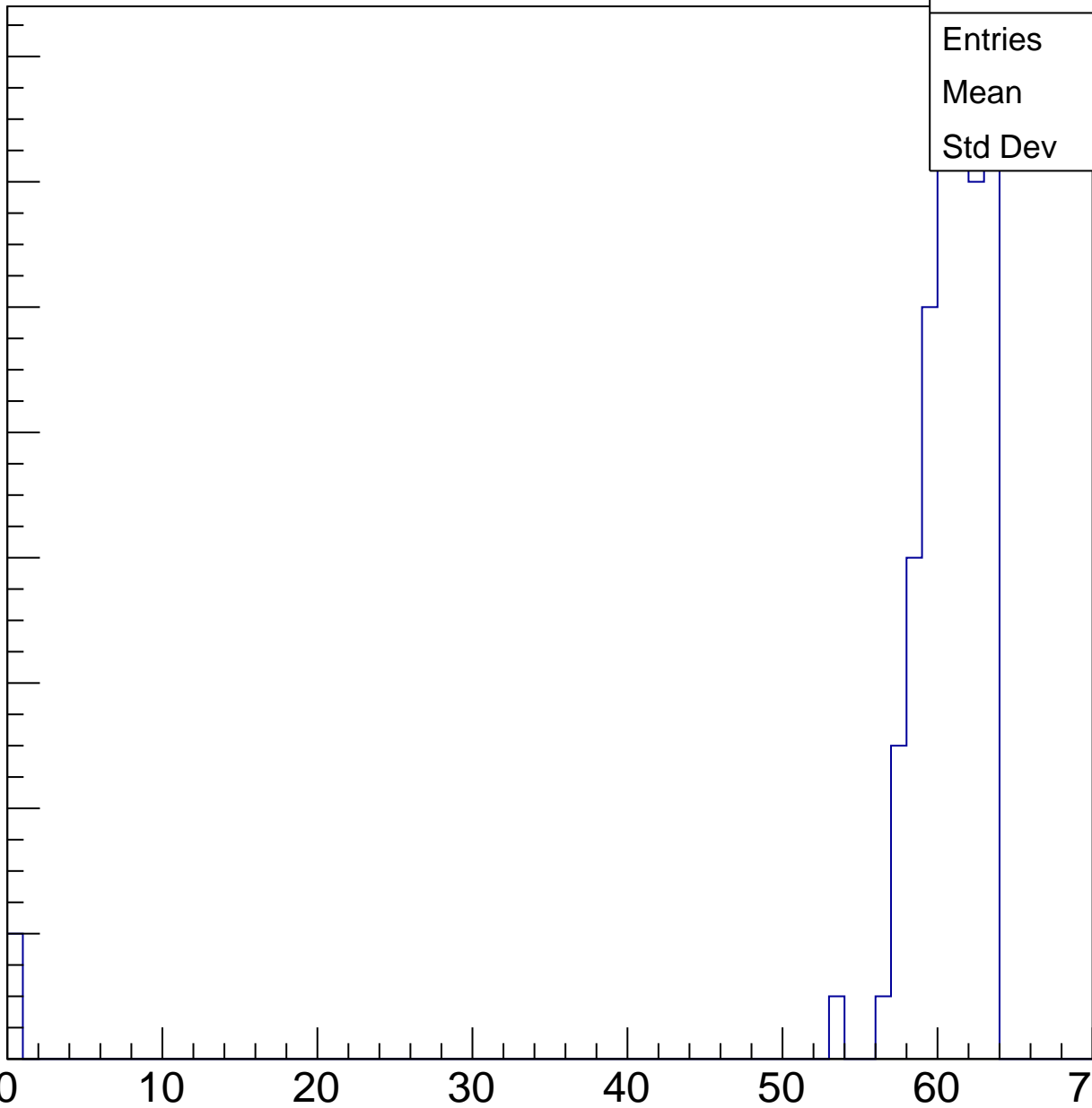
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.03
Std Dev	9.166

ampl

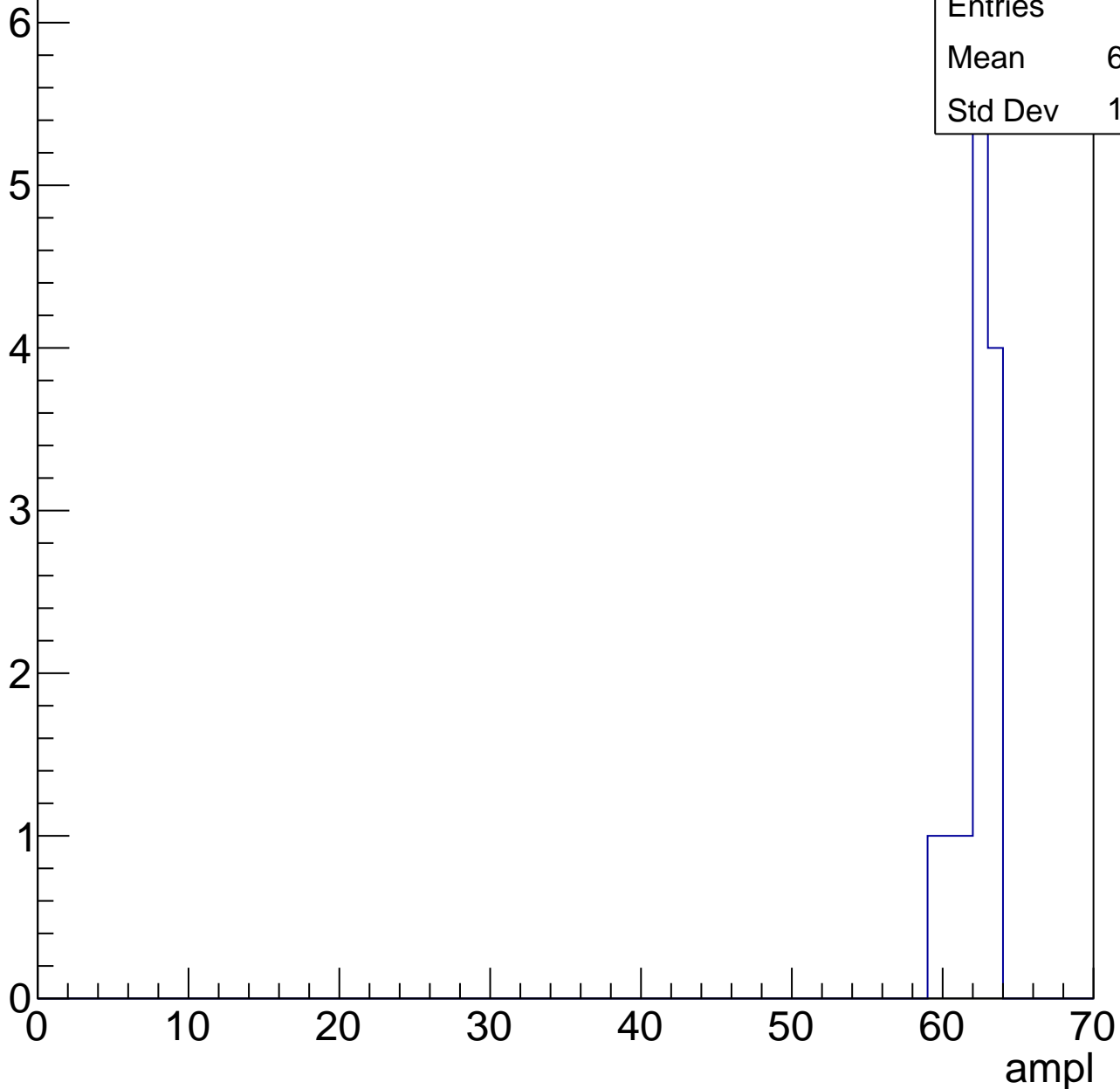


B0L101S, U19-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	61.85
Std Dev	1.167



B0L101S, U19-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch40, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

142

Mean

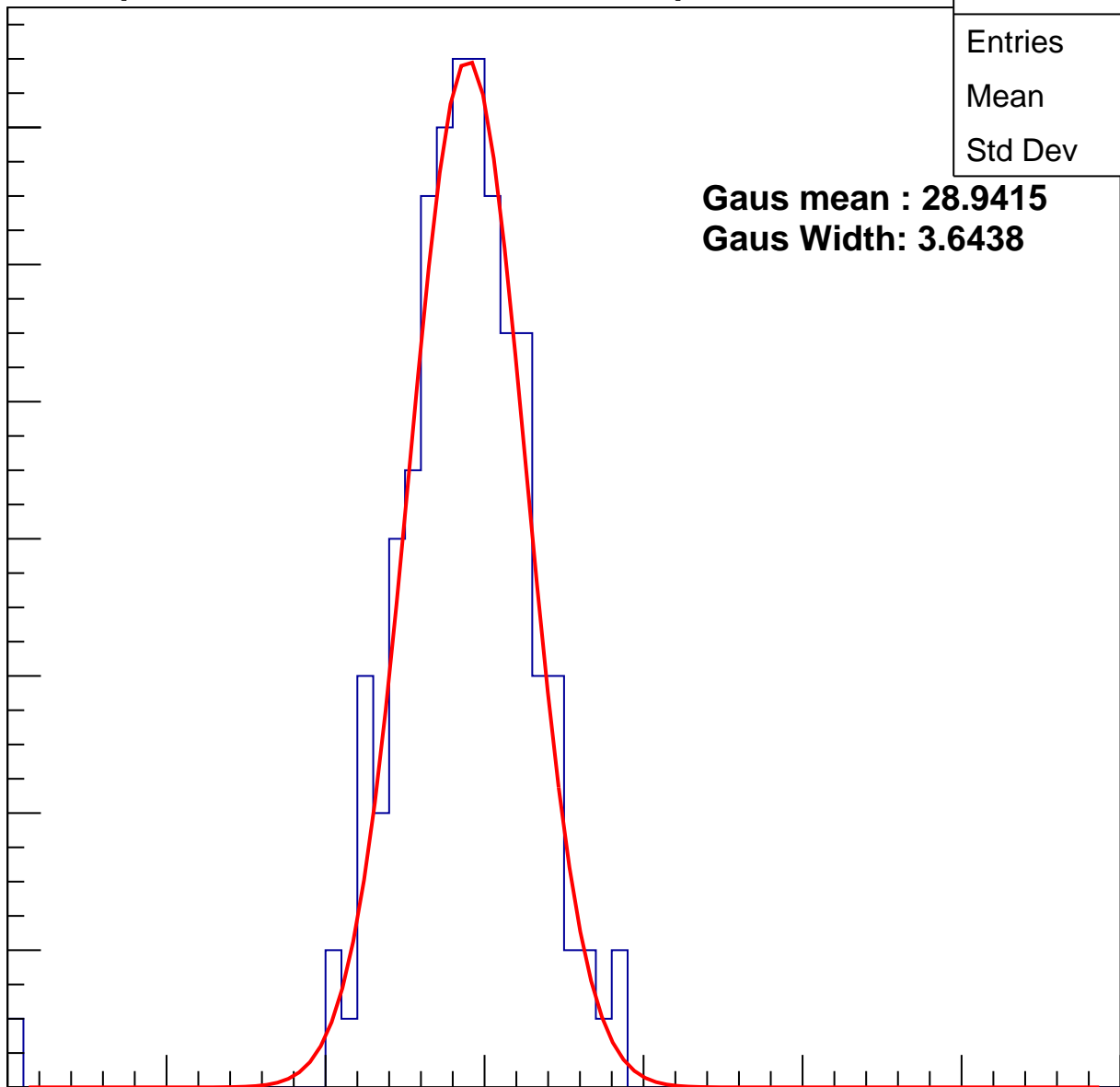
28.26

Std Dev

4.407

Gaus mean : 28.9415

Gaus Width: 3.6438



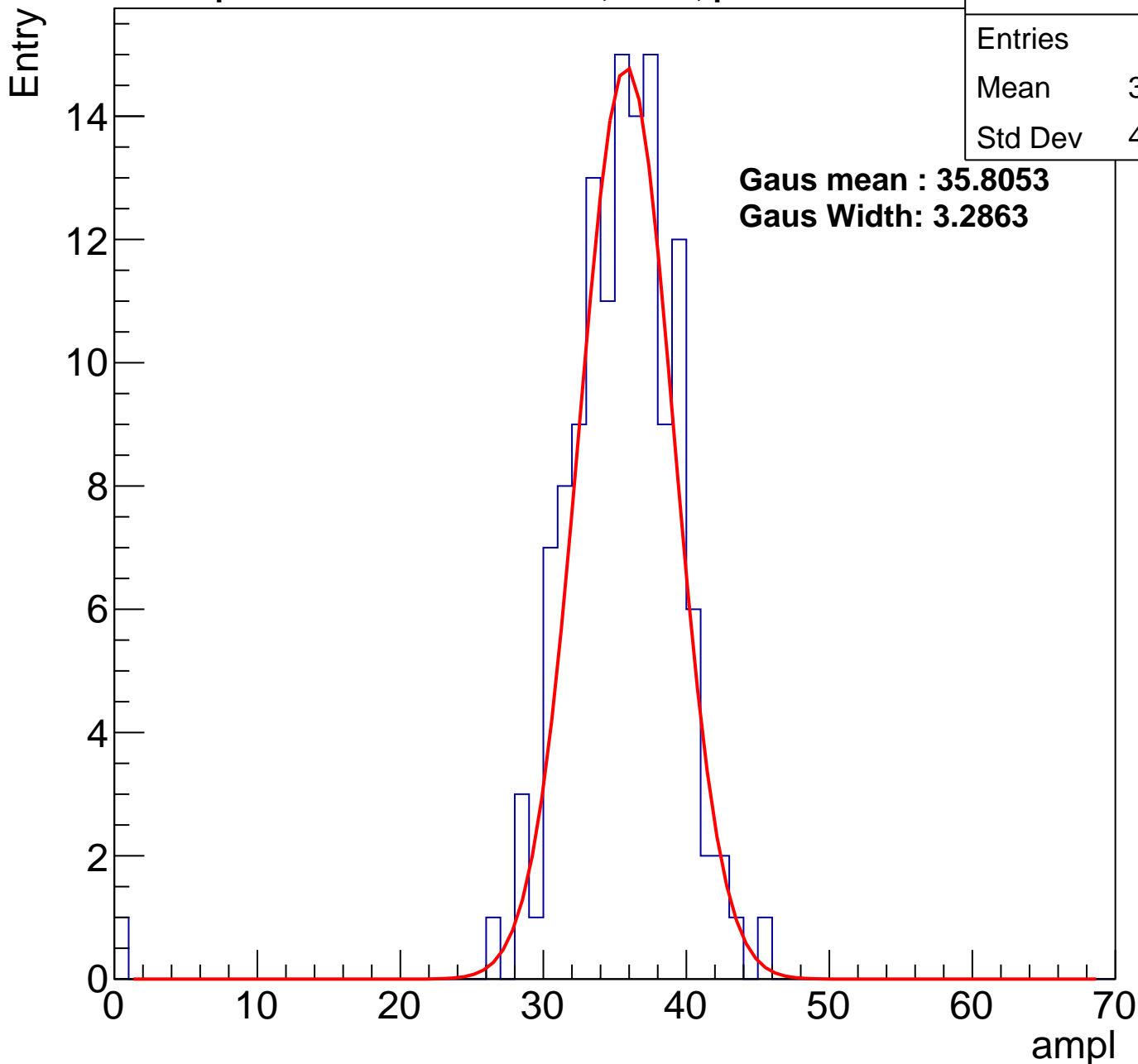
B0L101S, U19-ch40, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	34.93
Std Dev	4.595

Gaus mean : 35.8053

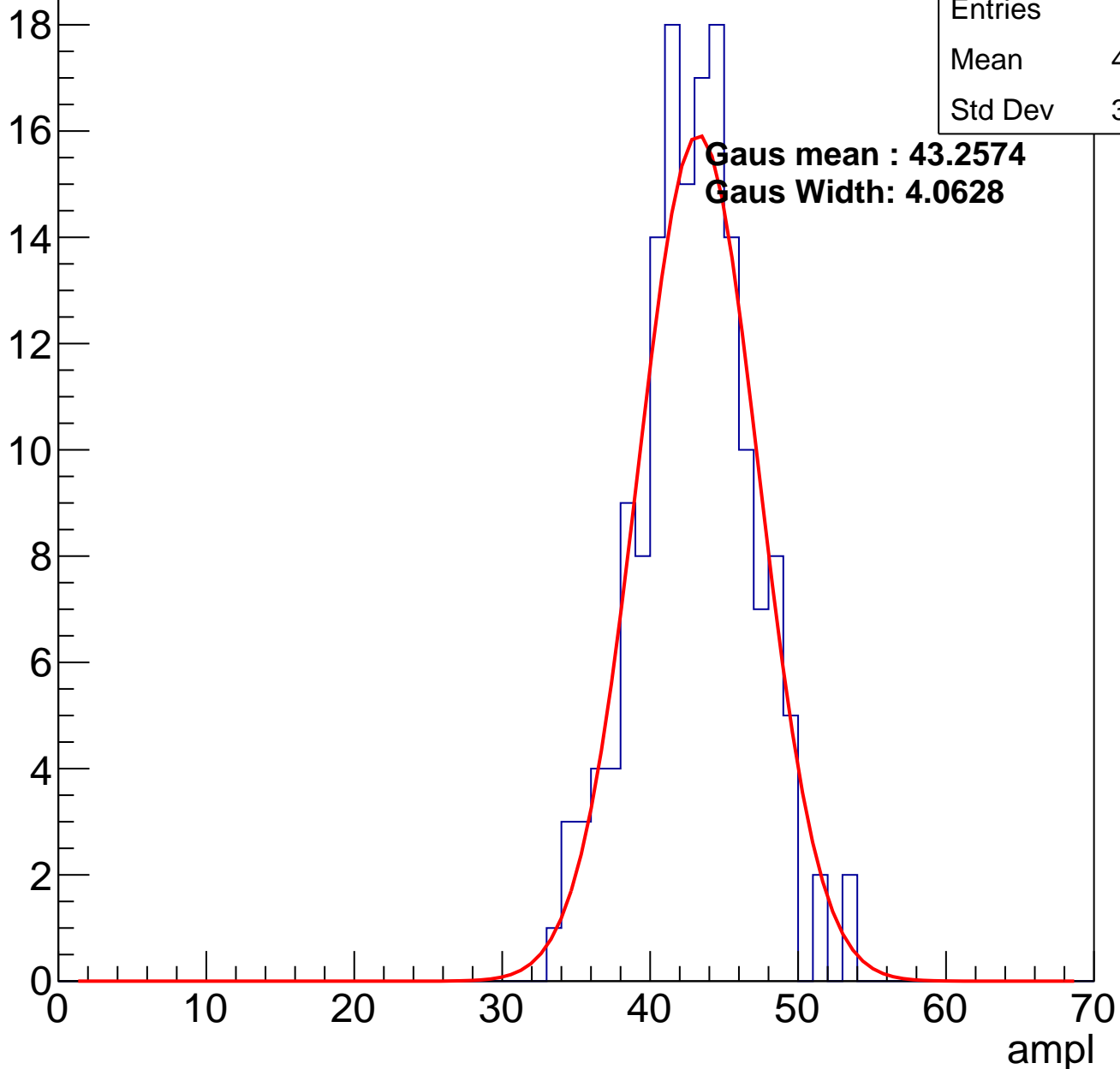
Gaus Width: 3.2863



B0L101S, U19-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

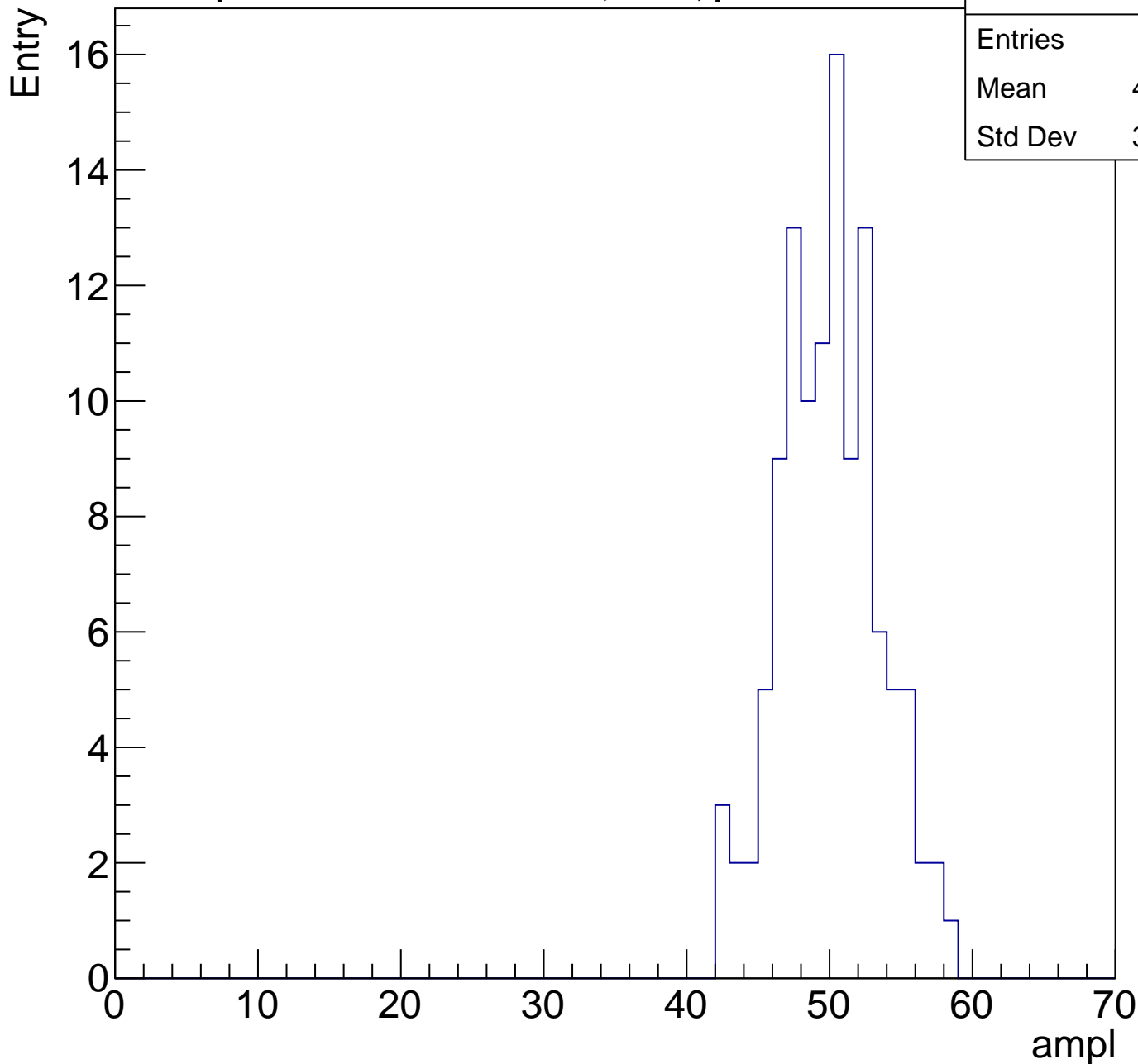
Entry



B0L101S, U19-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

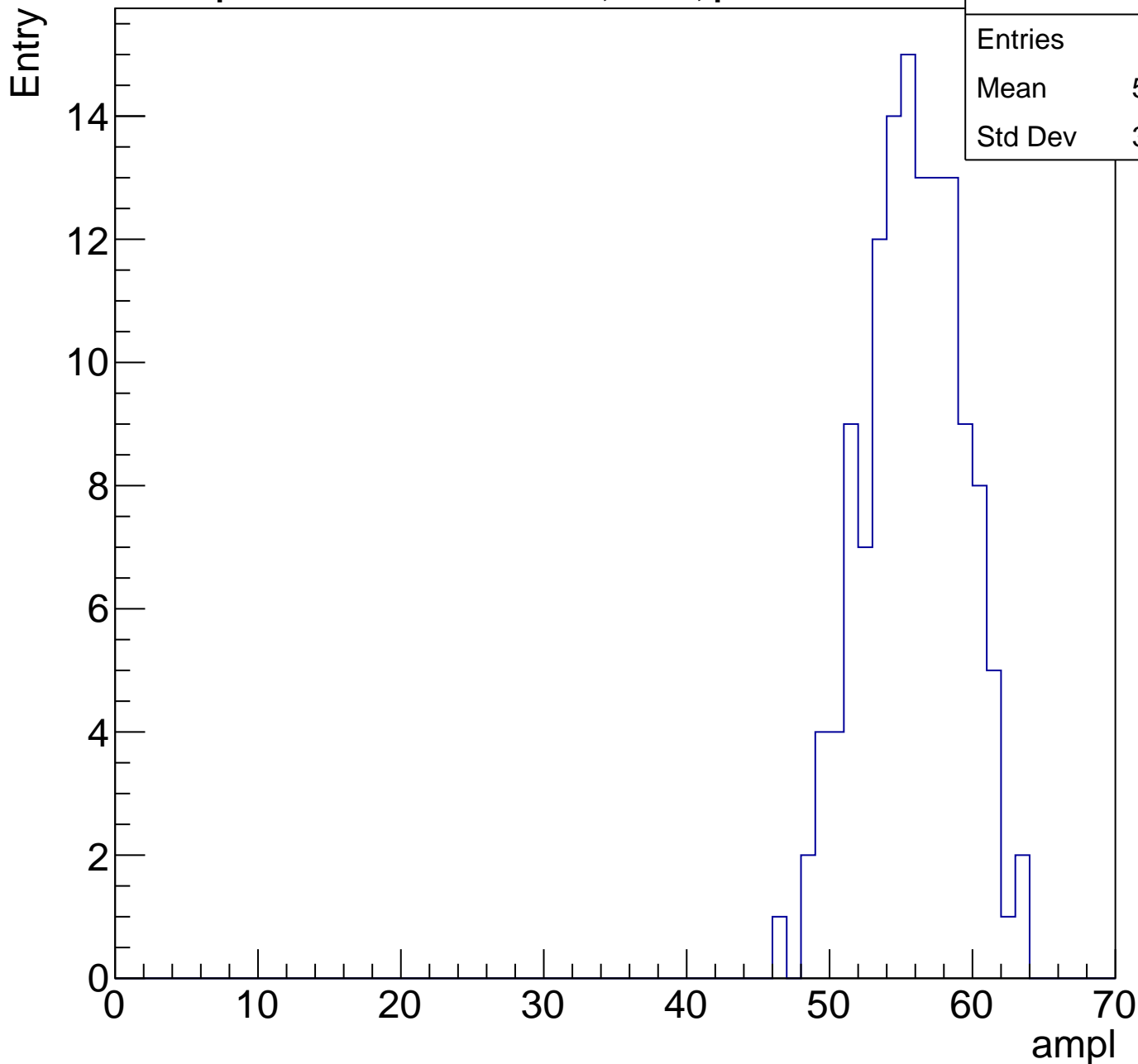
Entries	114
Mean	49.57
Std Dev	3.418



B0L101S, U19-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	55.34
Std Dev	3.437



B0L101S, U19-ch40, adc5

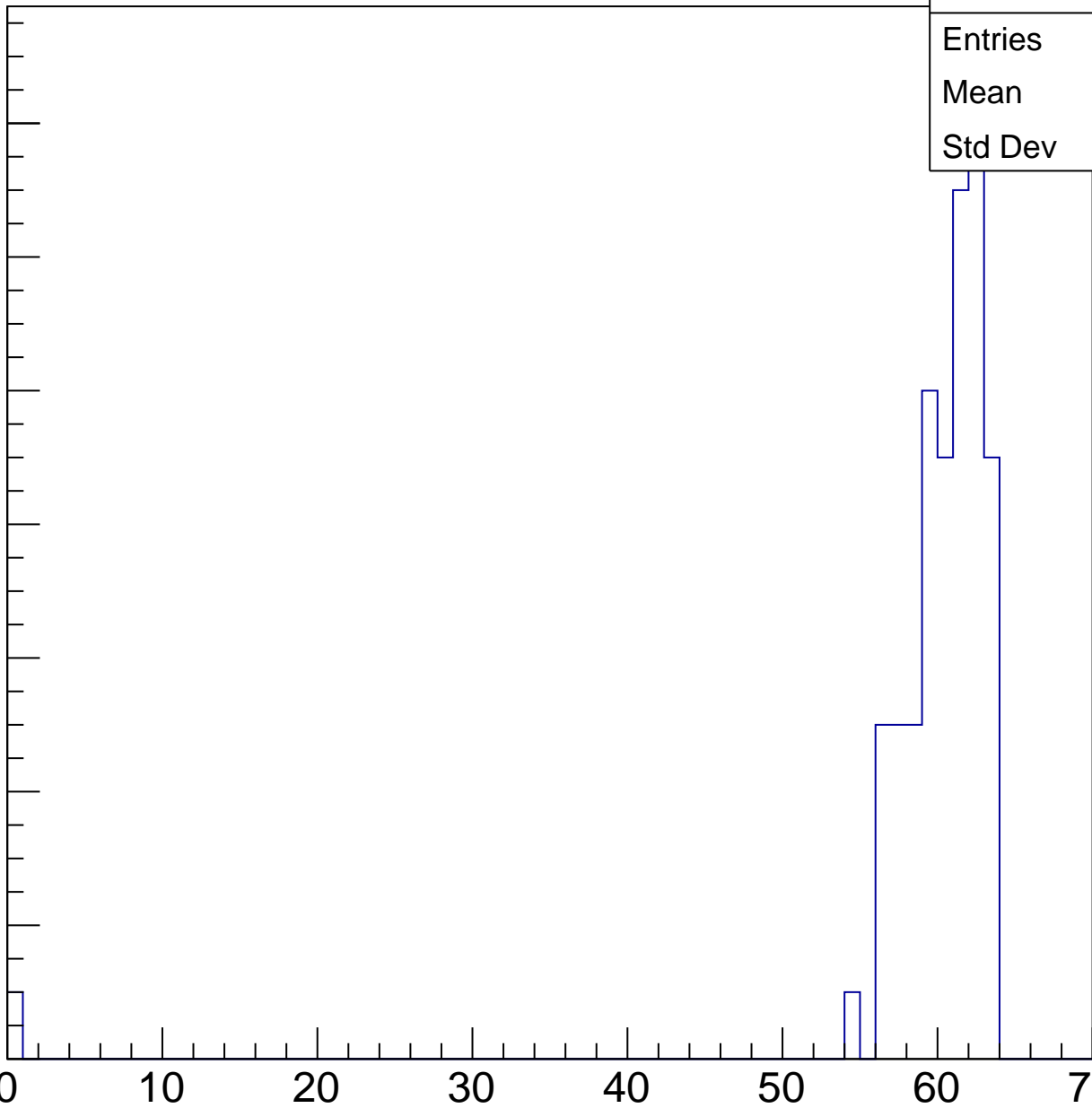
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	59.3
Std Dev	7.318

ampl



B0L101S, U19-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

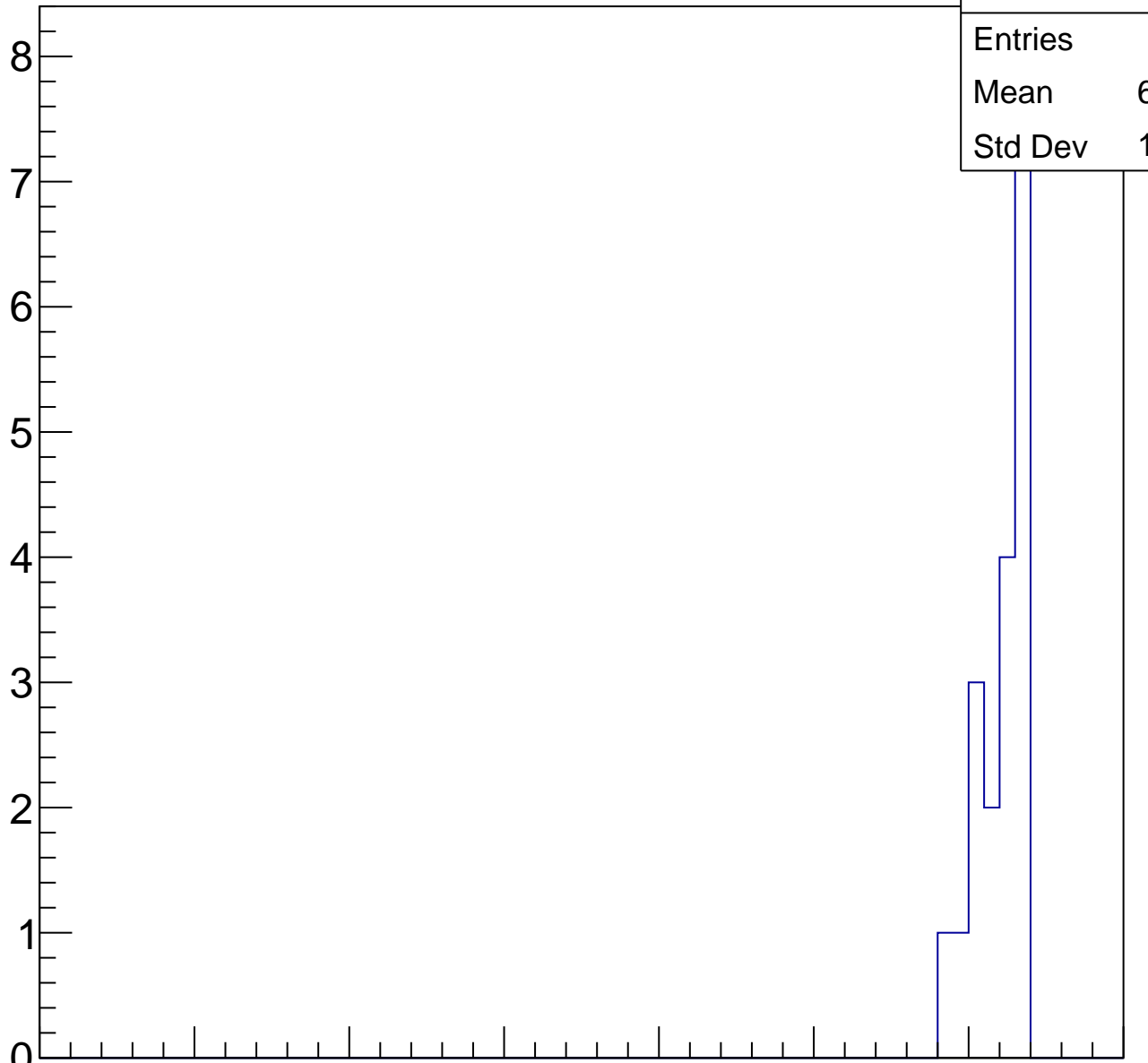
Entry

8
7
6
5
4
3
2
1
0

Entries	19
Mean	61.63
Std Dev	1.529

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

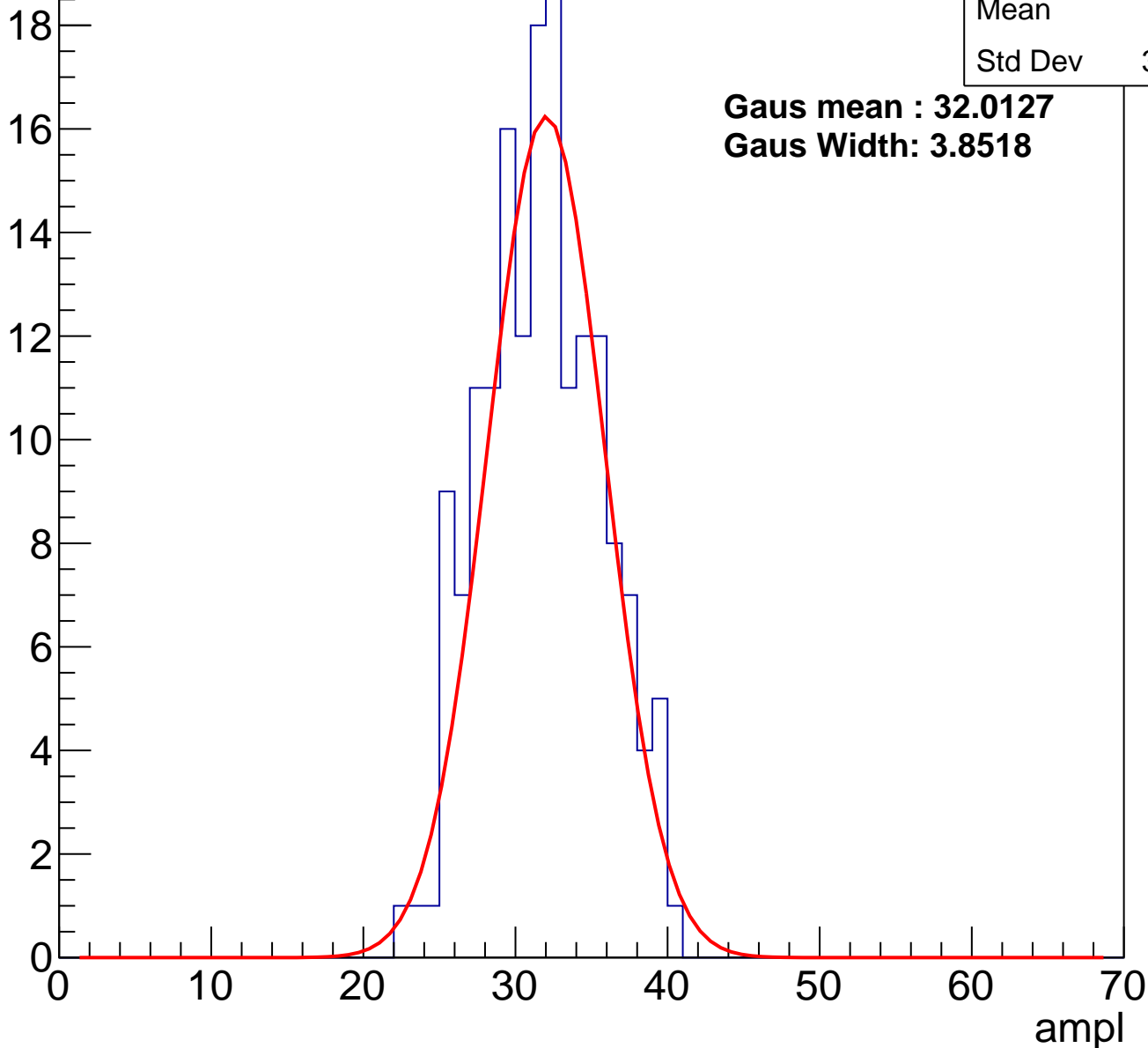
Entry



B0L101S, U19-ch41, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch41, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	39.14
Std Dev	3.757

Gaus mean : 39.9552

Gaus Width: 3.8589

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

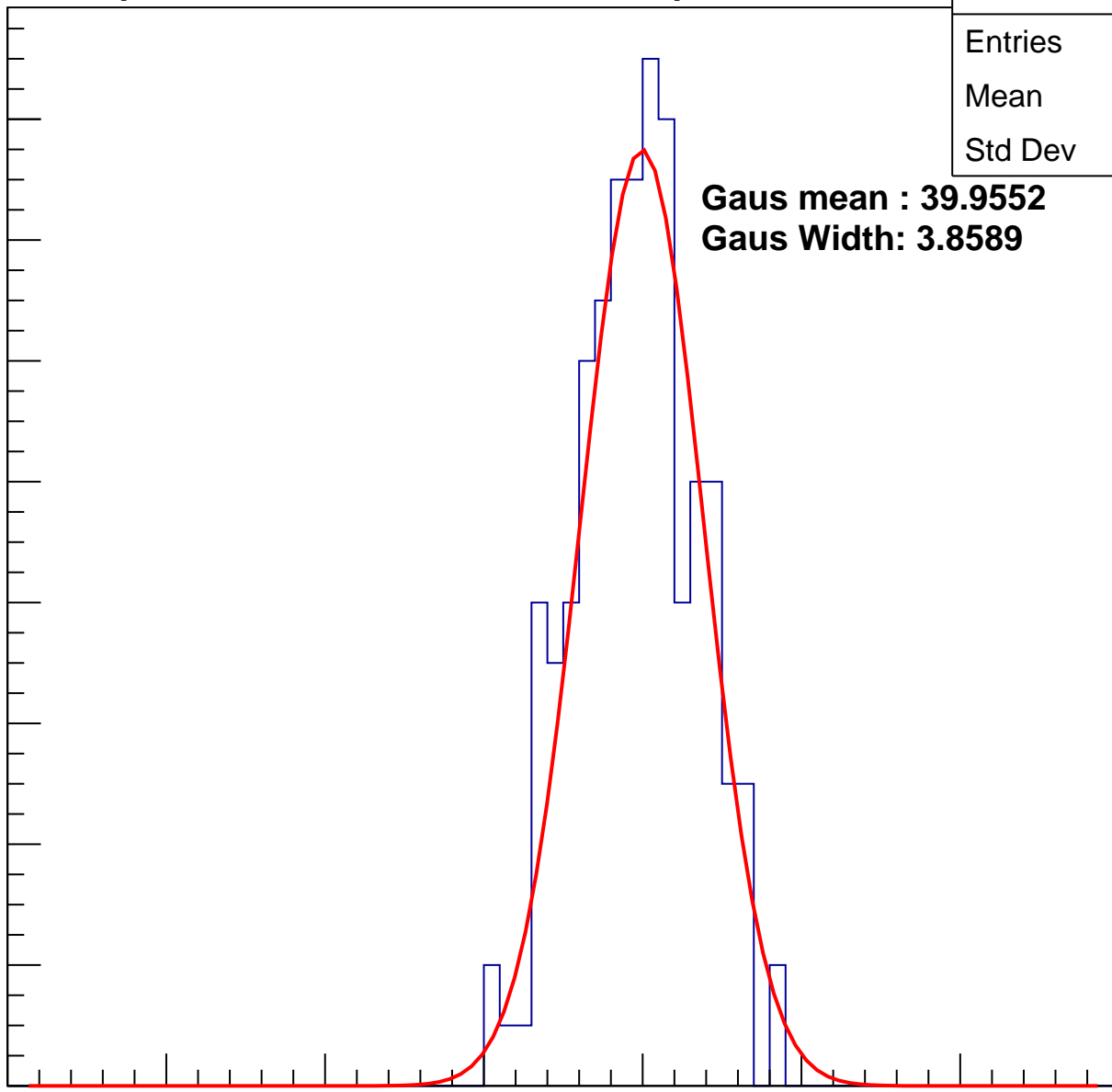
40

50

60

70

ampl



B0L101S, U19-ch41, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	45.58
Std Dev	3.195

Entry

14

12

10

8

6

4

2

0

Gaus mean : 46.0520

Gaus Width: 3.1461

0

10

20

30

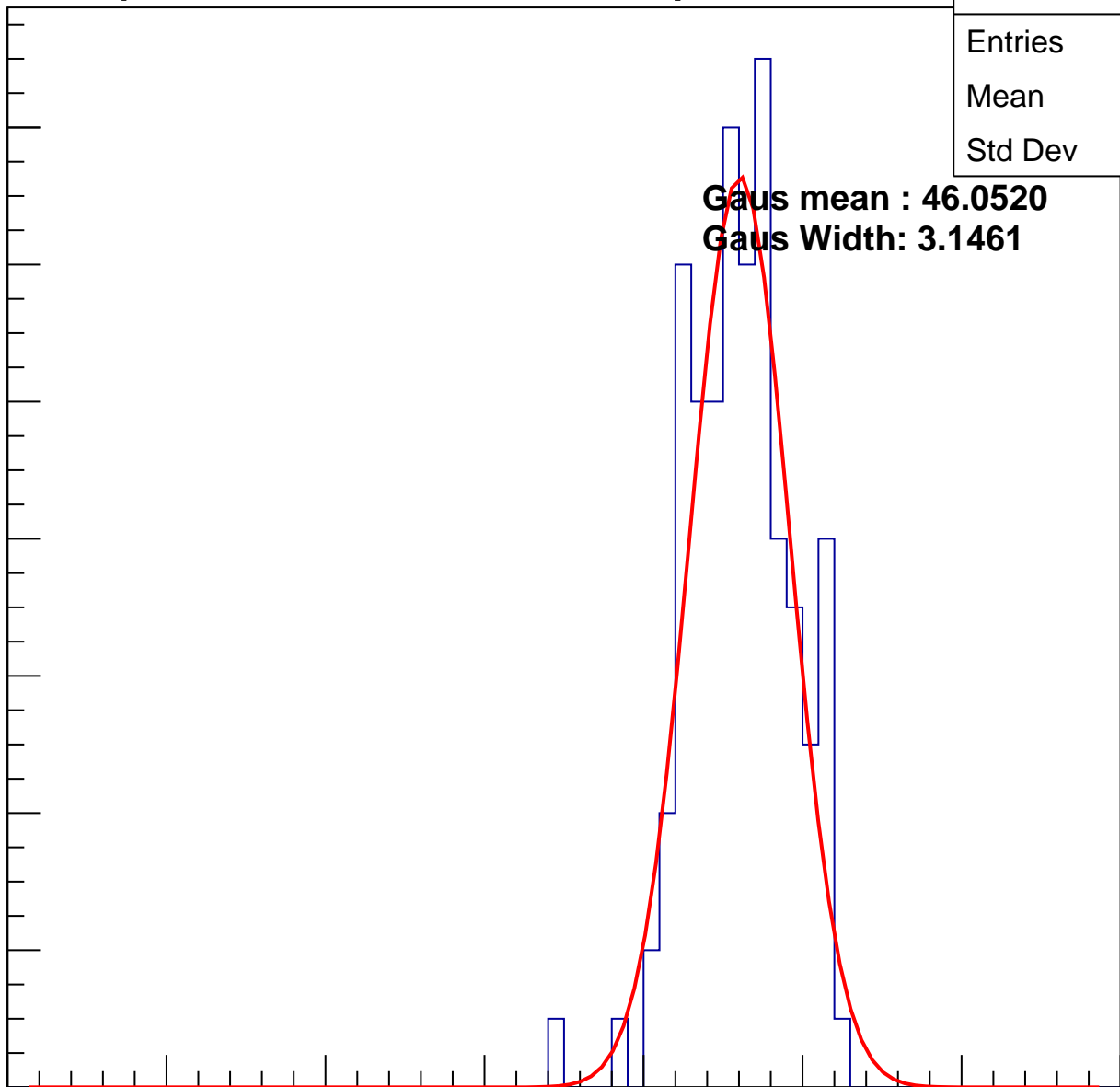
40

50

60

70

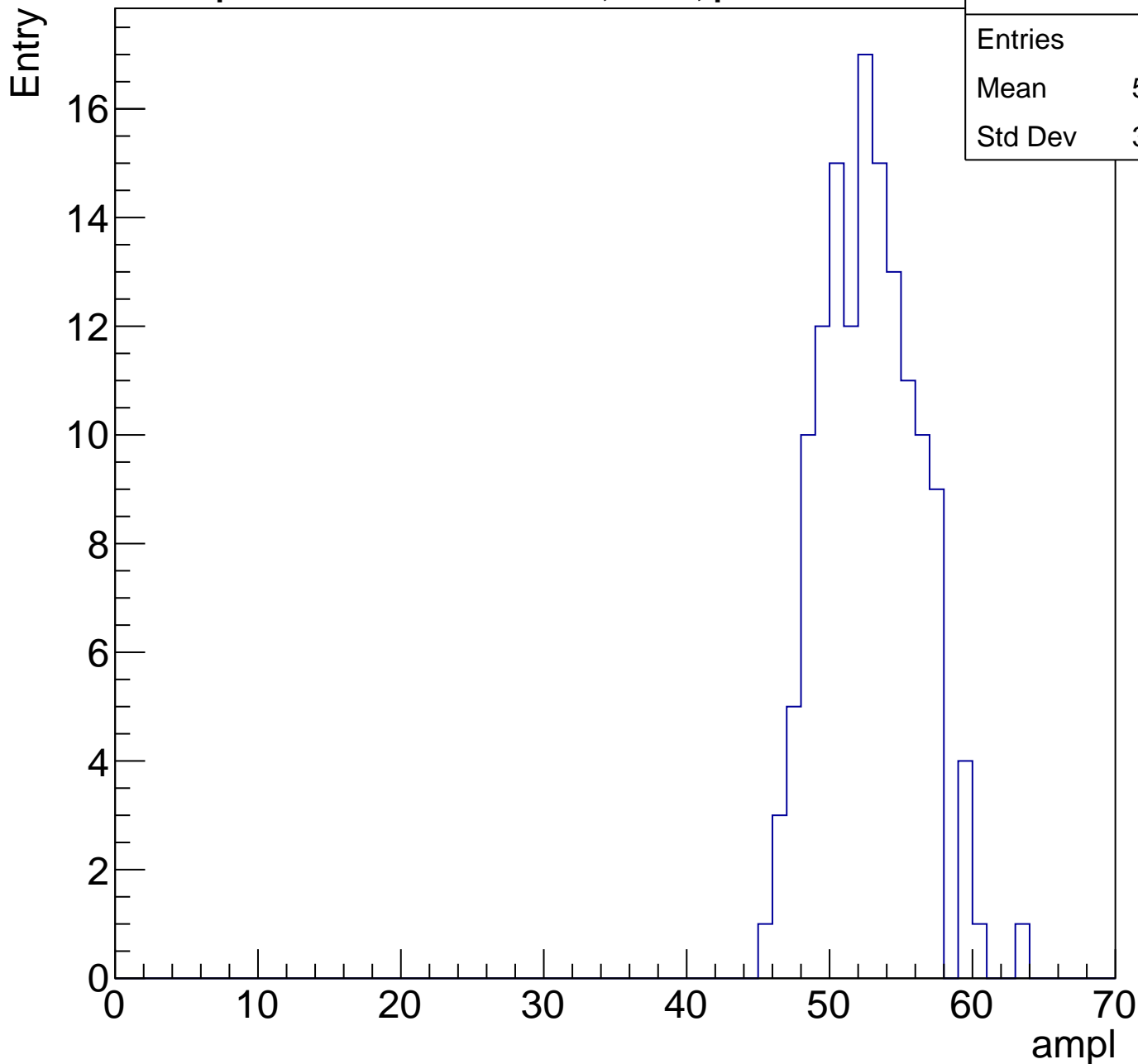
ampl



B0L101S, U19-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	52.27
Std Dev	3.332

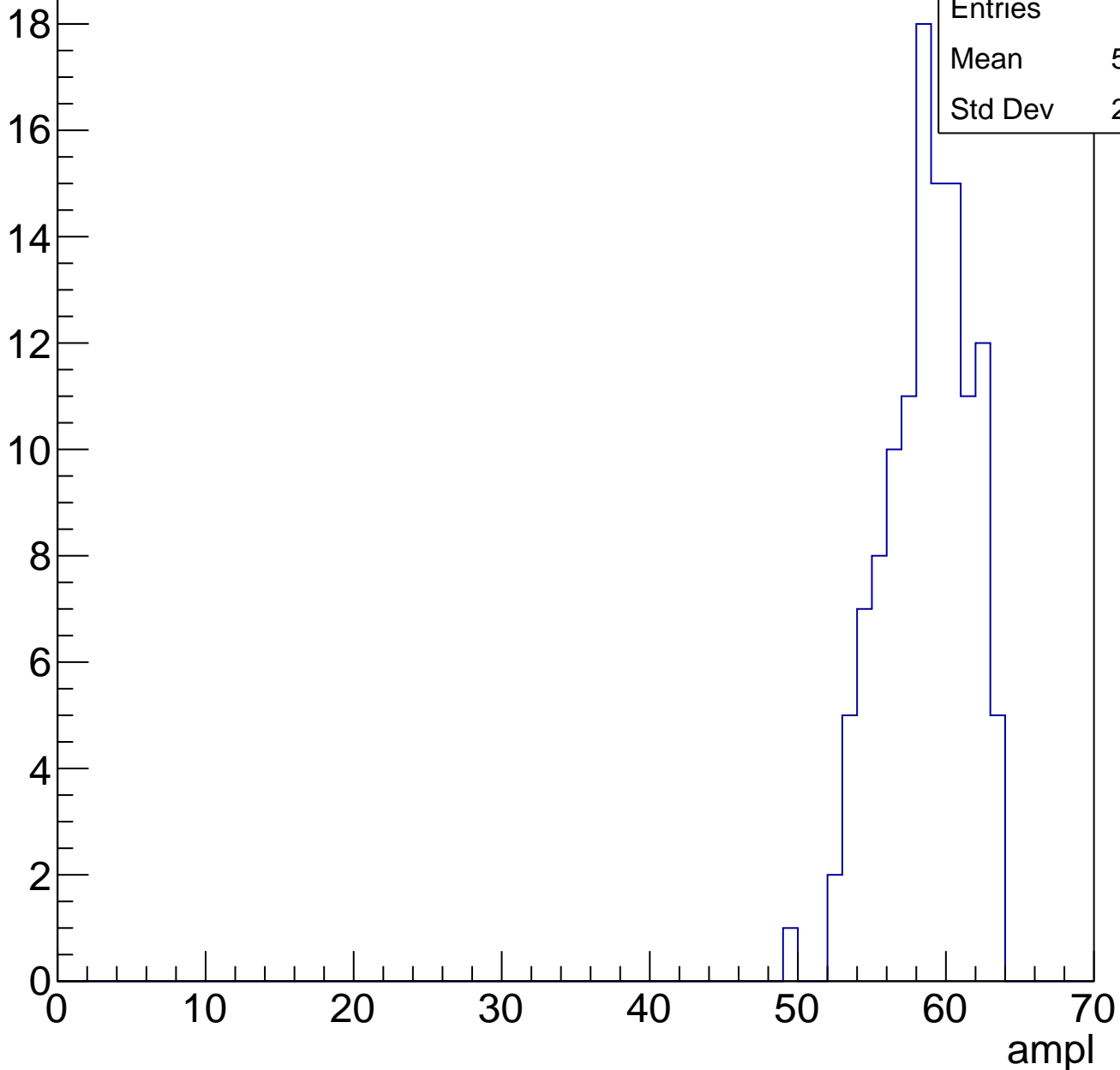


B0L101S, U19-ch41, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	58.18
Std Dev	2.887

Entry



B0L101S, U19-ch41, adc5

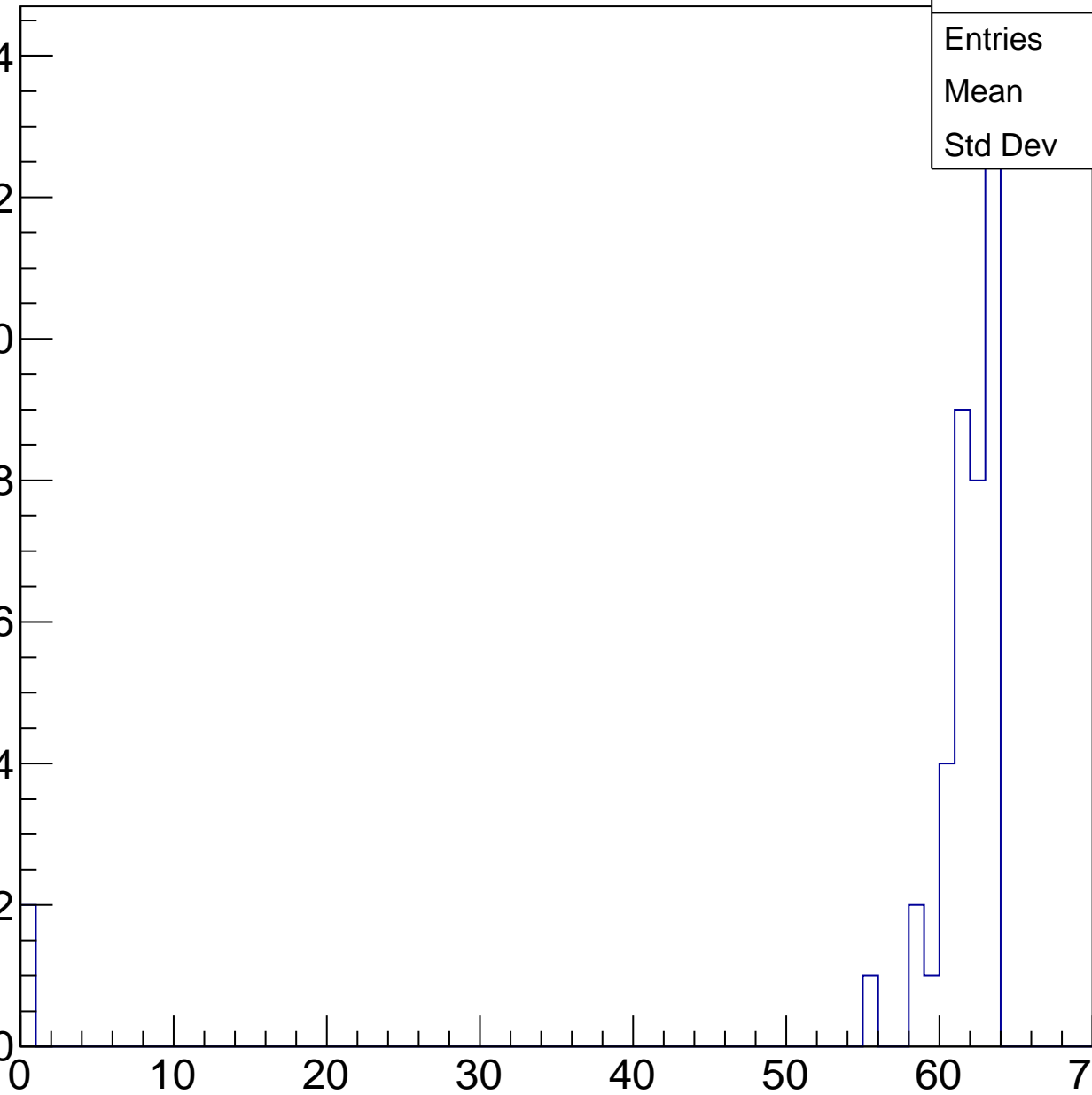
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	41
Mean	58.46
Std Dev	13.35

ampl



B0L101S, U19-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch42, adc0

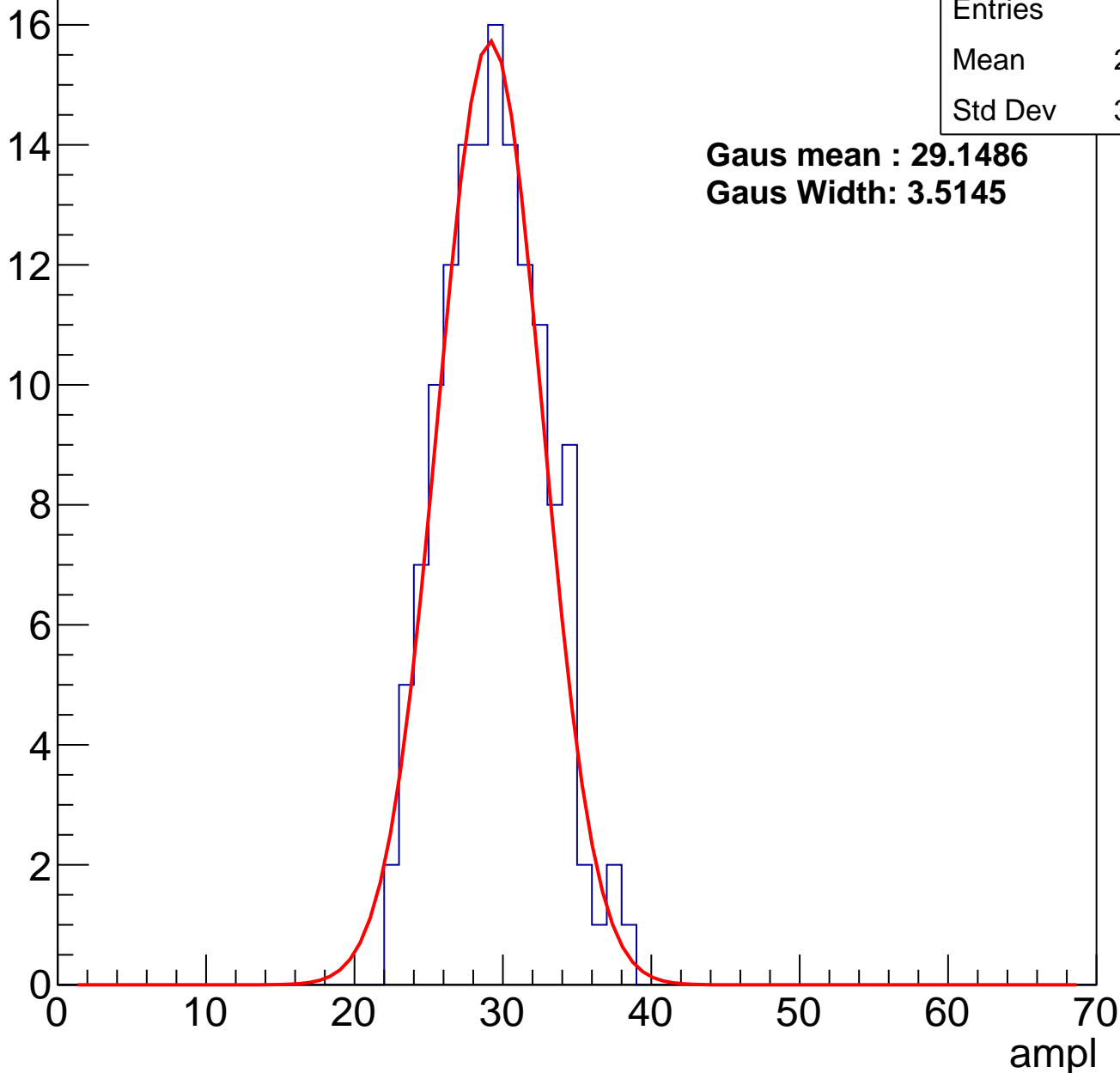
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	28.96
Std Dev	3.417

Gaus mean : 29.1486

Gaus Width: 3.5145

Entry



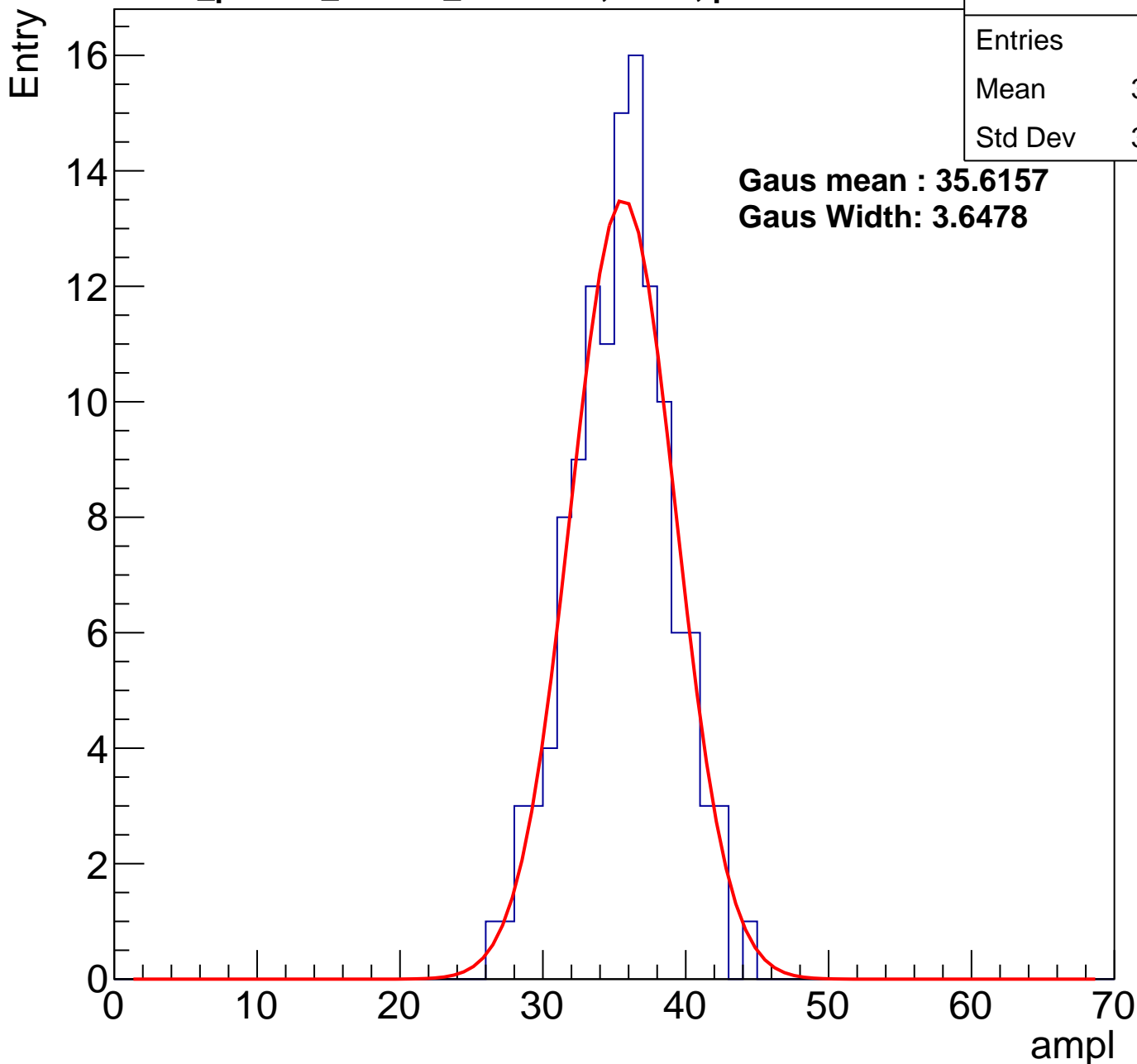
B0L101S, U19-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	35.02
Std Dev	3.483

Gaus mean : 35.6157

Gaus Width: 3.6478



B0L101S, U19-ch42, adc2

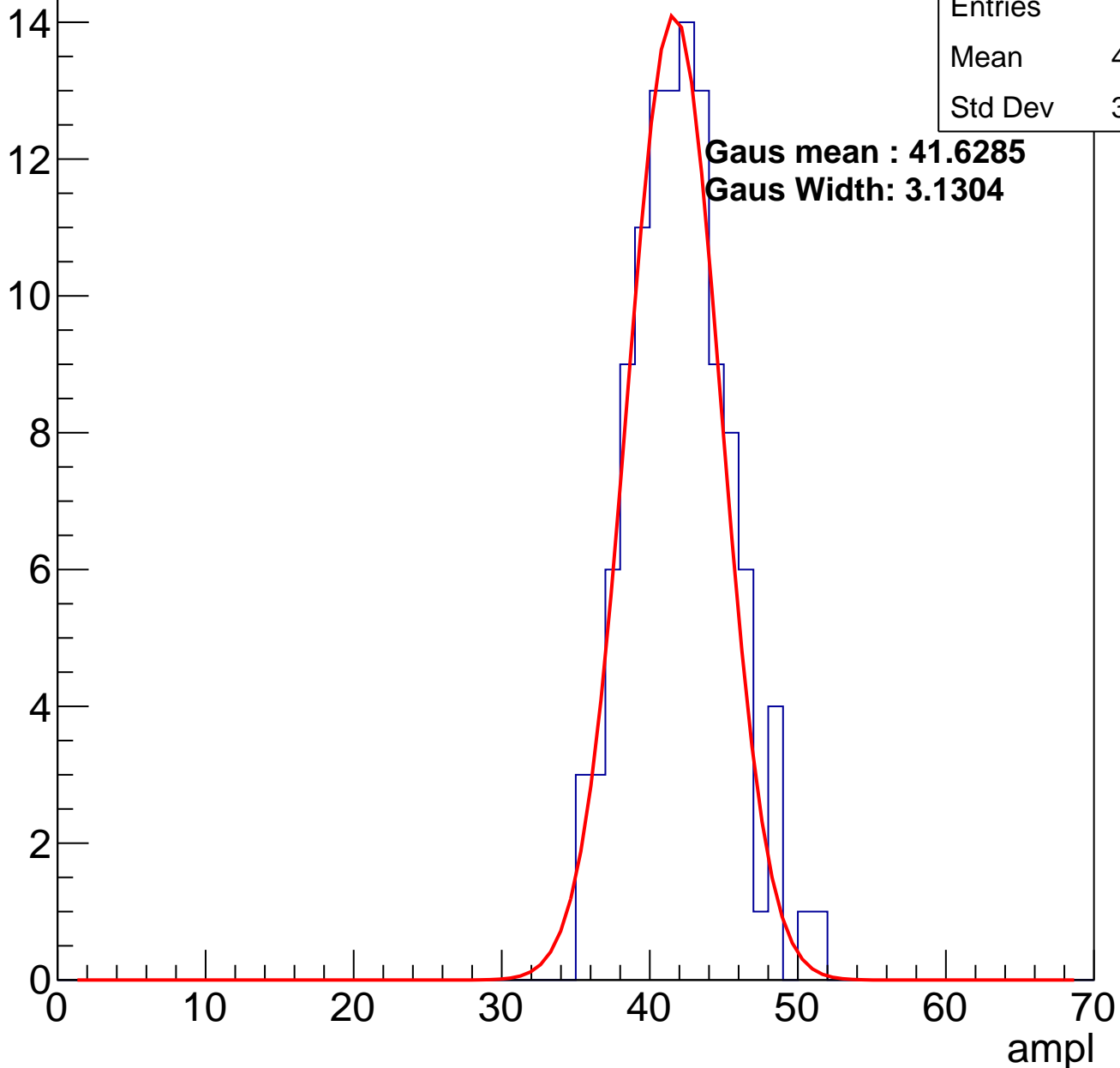
calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	41.55
Std Dev	3.258

Gaus mean : 41.6285

Gaus Width: 3.1304

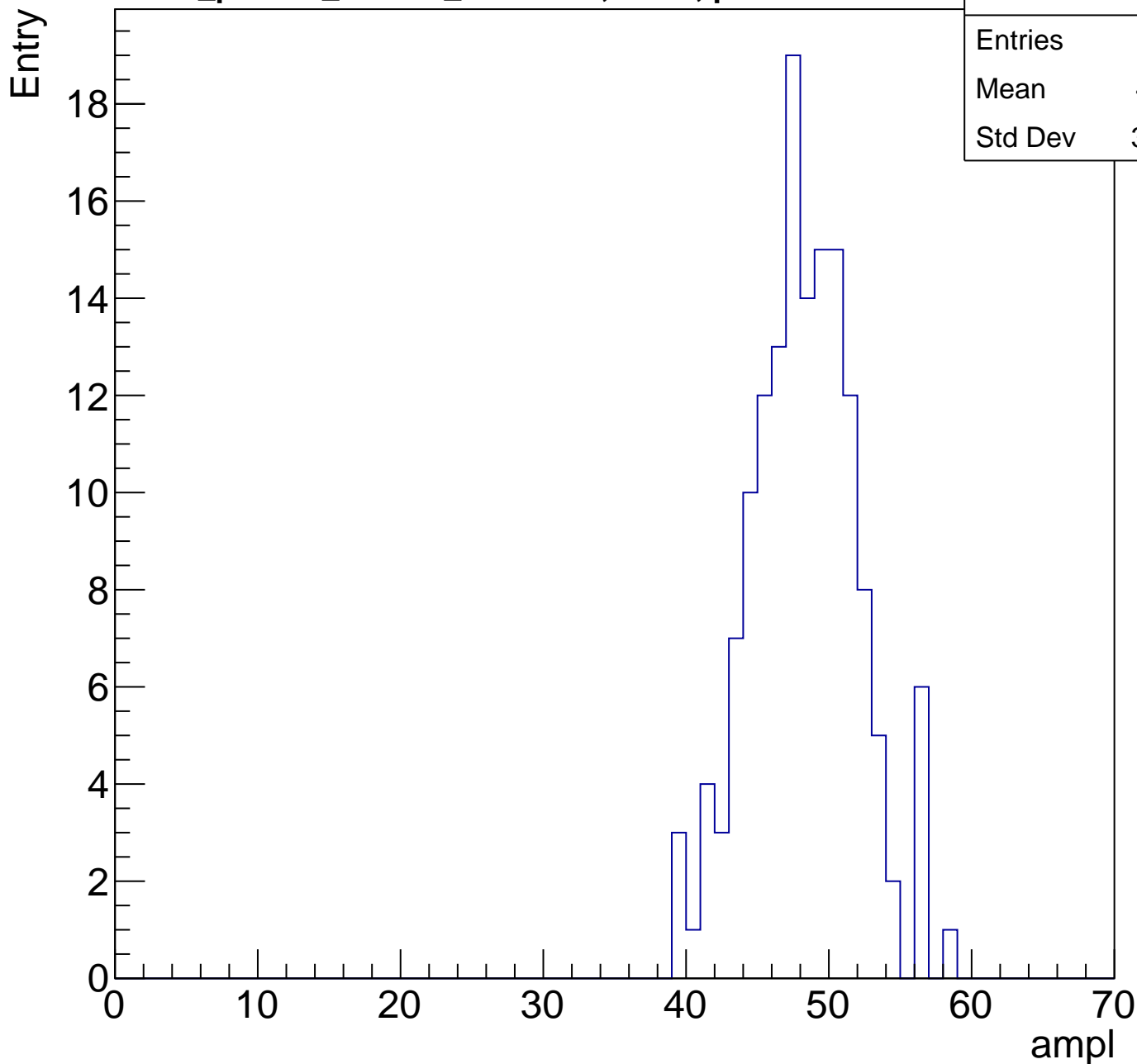
Entry



B0L101S, U19-ch42, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	47.81
Std Dev	3.752



B0L101S, U19-ch42, adc4

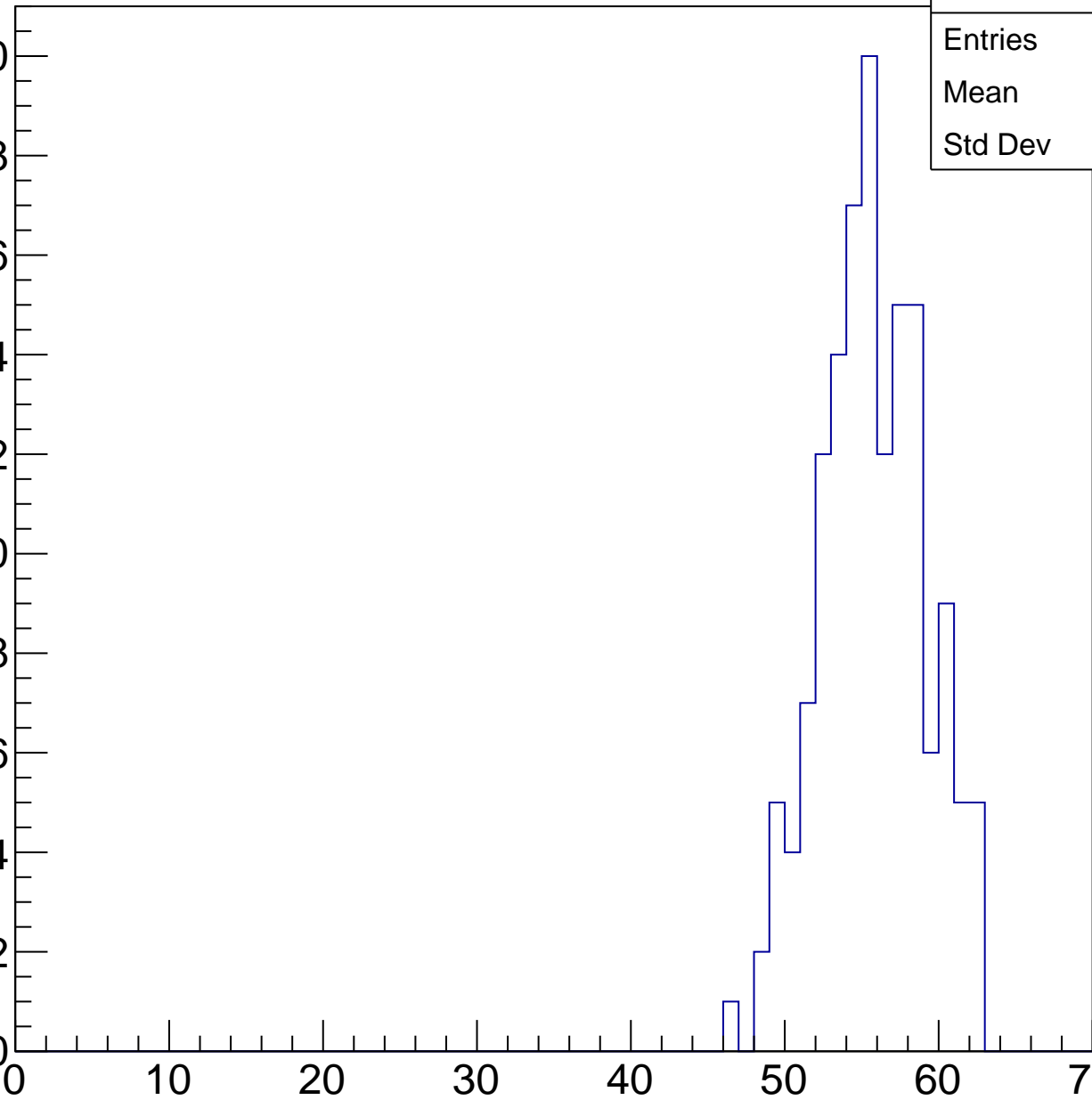
calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	149
Mean	55.26
Std Dev	3.373

ampl



B0L101S, U19-ch42, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

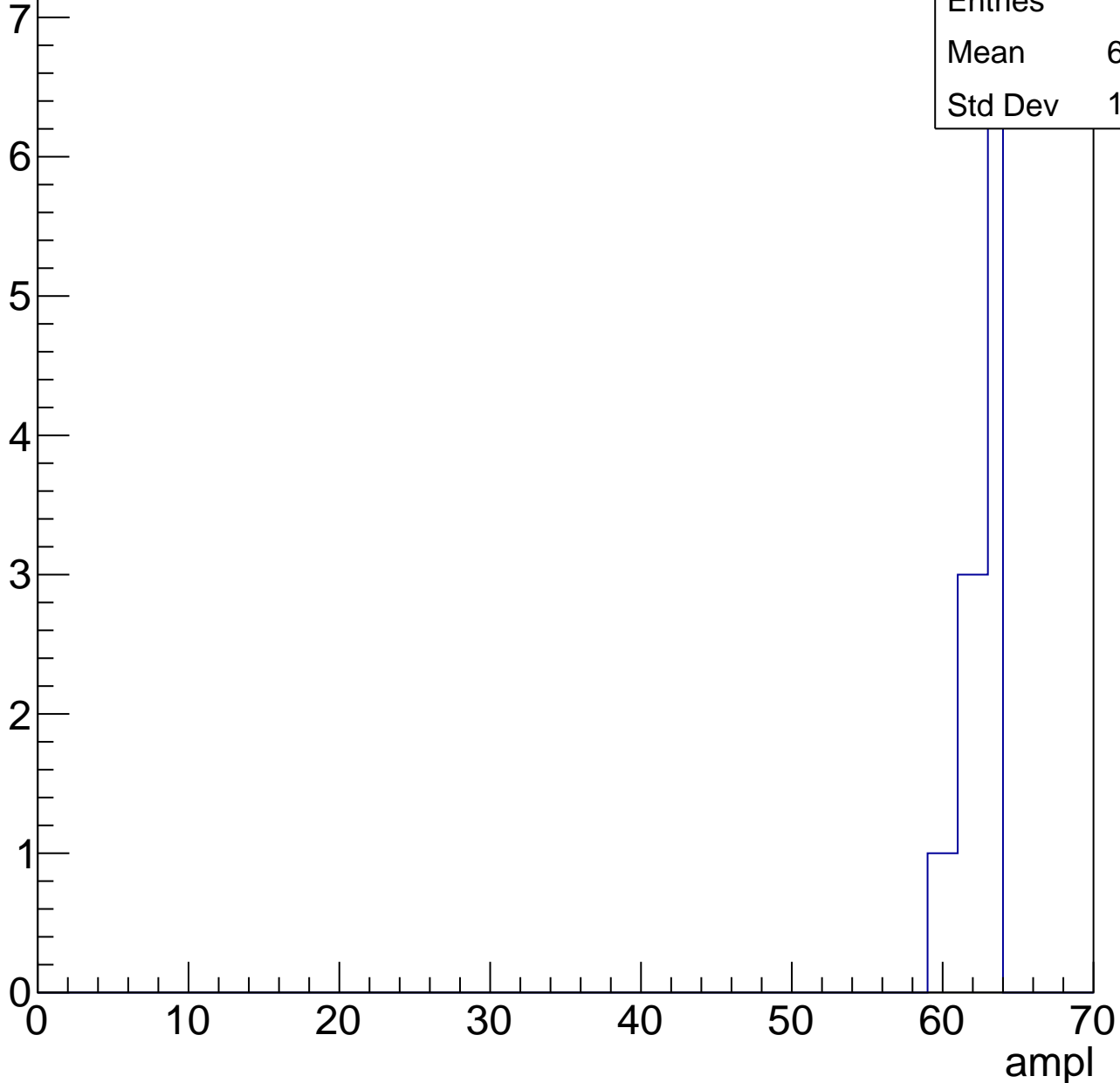
Entries	75
Mean	58.64
Std Dev	9.953

B0L101S, U19-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

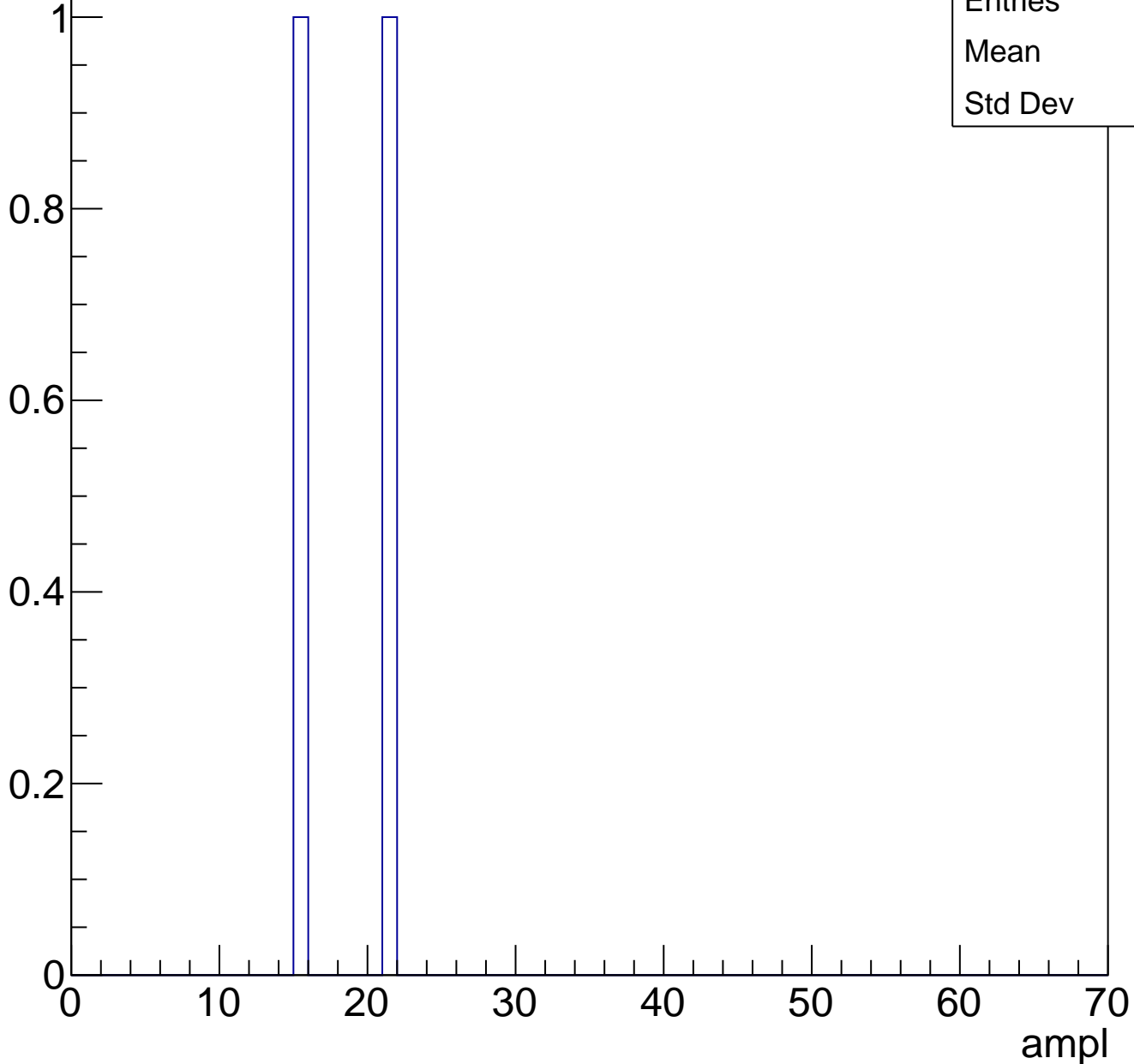
Entries	15
Mean	61.93
Std Dev	1.236



B0L101S, U19-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



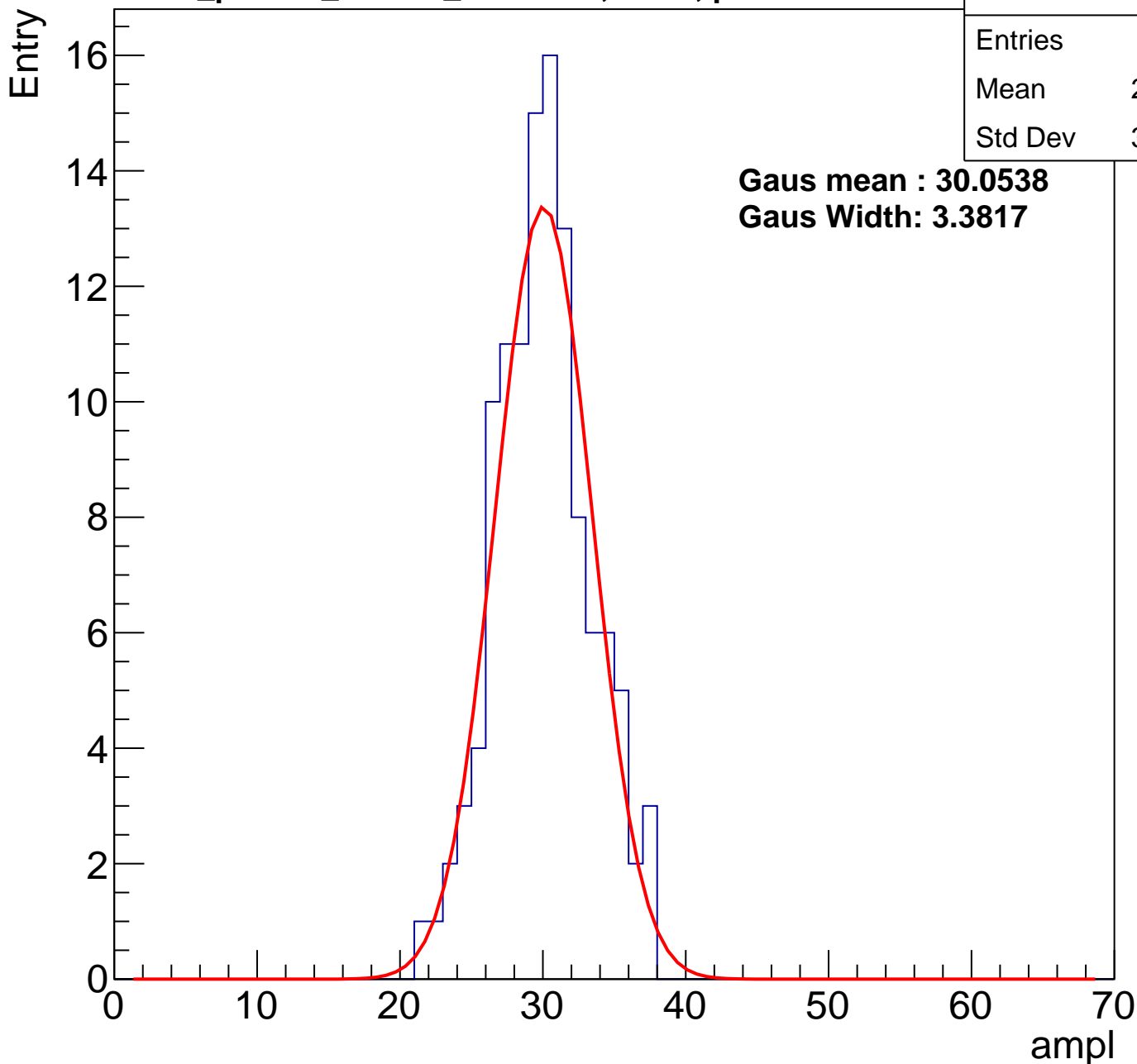
B0L101S, U19-ch43, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	29.57
Std Dev	3.316

Gaus mean : 30.0538

Gaus Width: 3.3817



B0L101S, U19-ch43, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	36.35
Std Dev	3.502

Gaus mean : 37.0246

Gaus Width: 3.4755

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

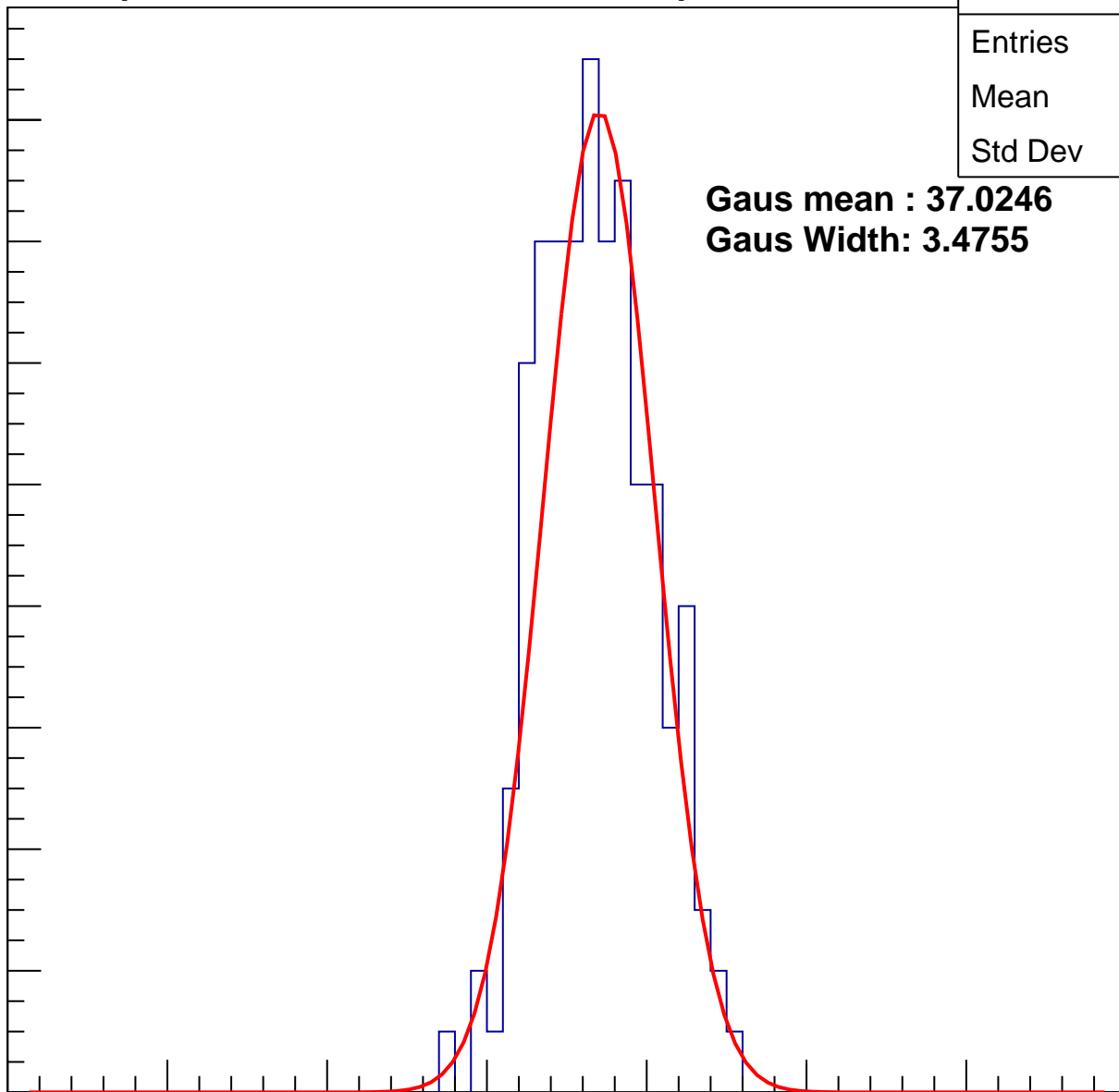
40

50

60

70

ampl



B0L101S, U19-ch43, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	43.92
Std Dev	3.656

Gaus mean : 44.4400

Gaus Width: 3.7560

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

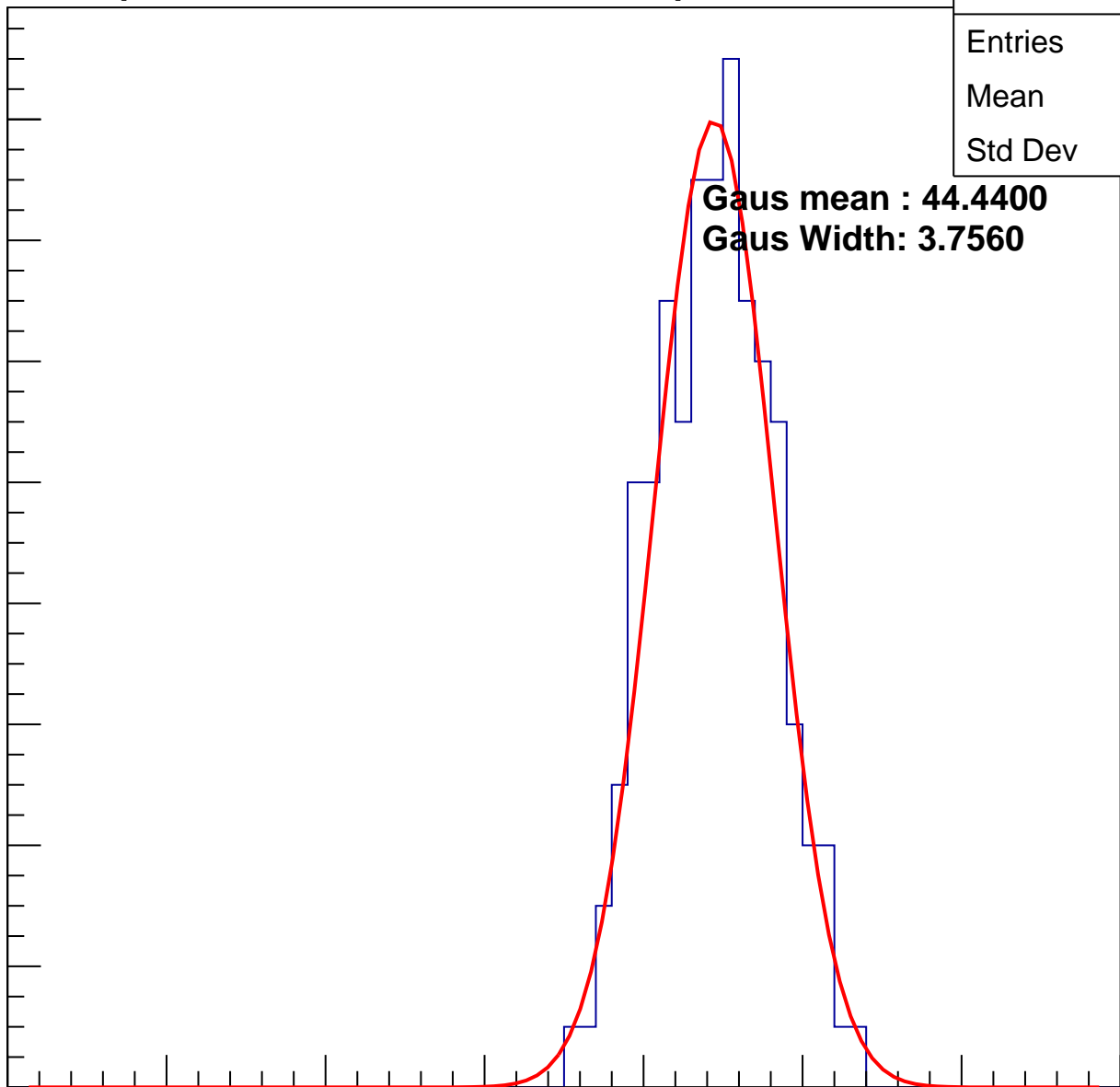
40

50

60

70

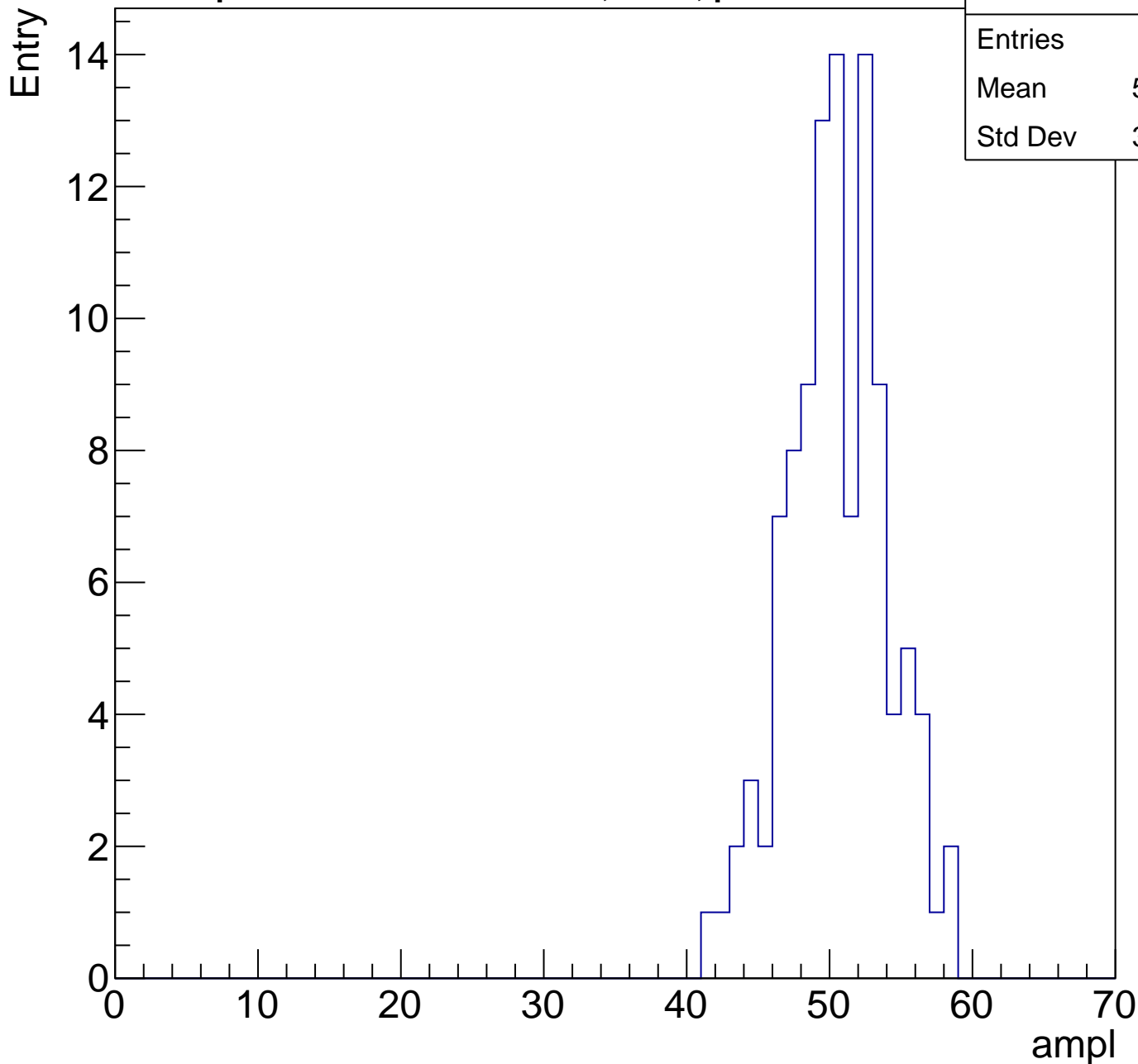
ampl



B0L101S, U19-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	50.08
Std Dev	3.485



B0L101S, U19-ch43, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	55.98
Std Dev	5.61

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

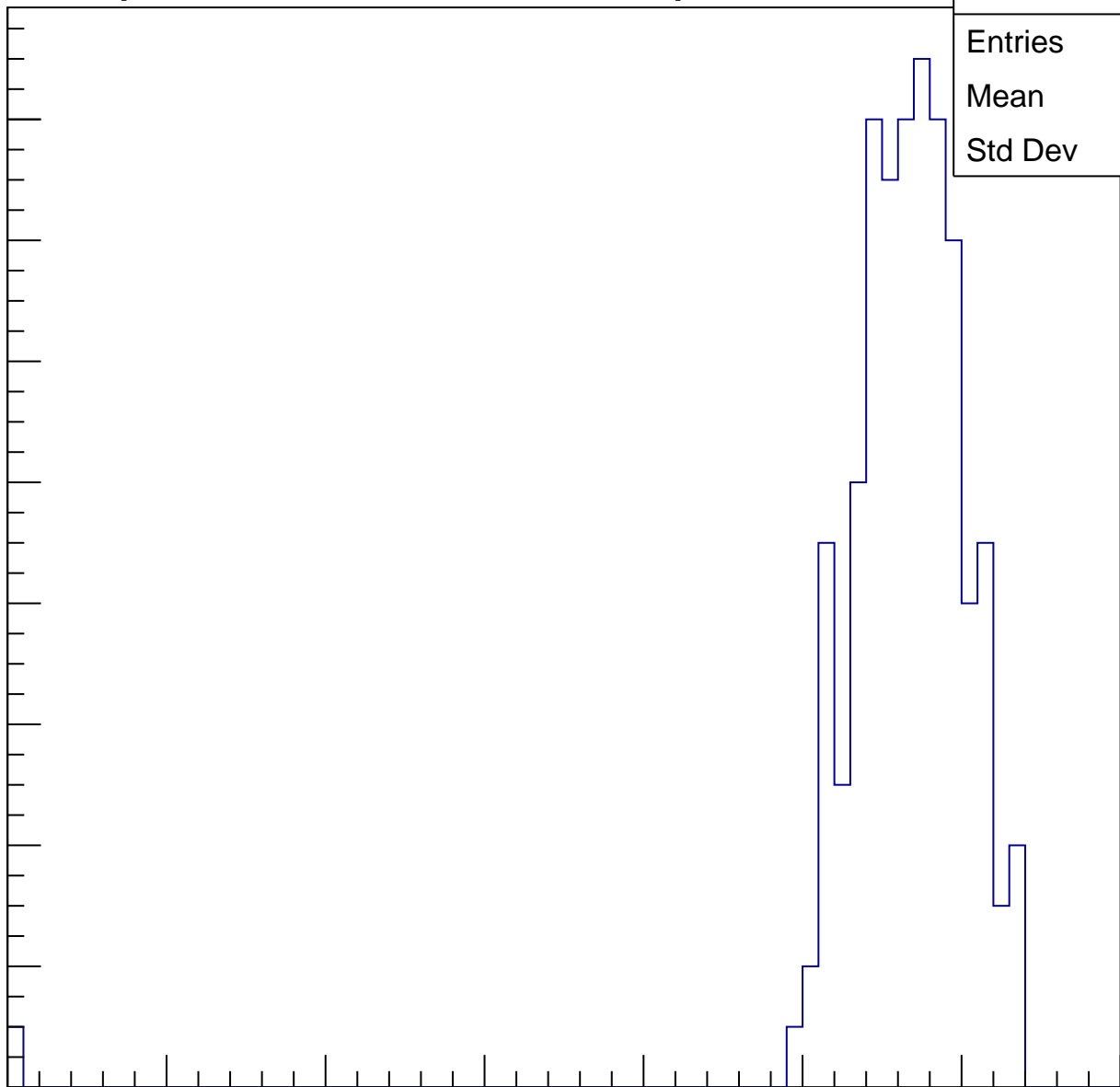
40

50

60

70

ampl



B0L101S, U19-ch43, adc5

calib_packv5_042523_0143.root, FC#1, port C1

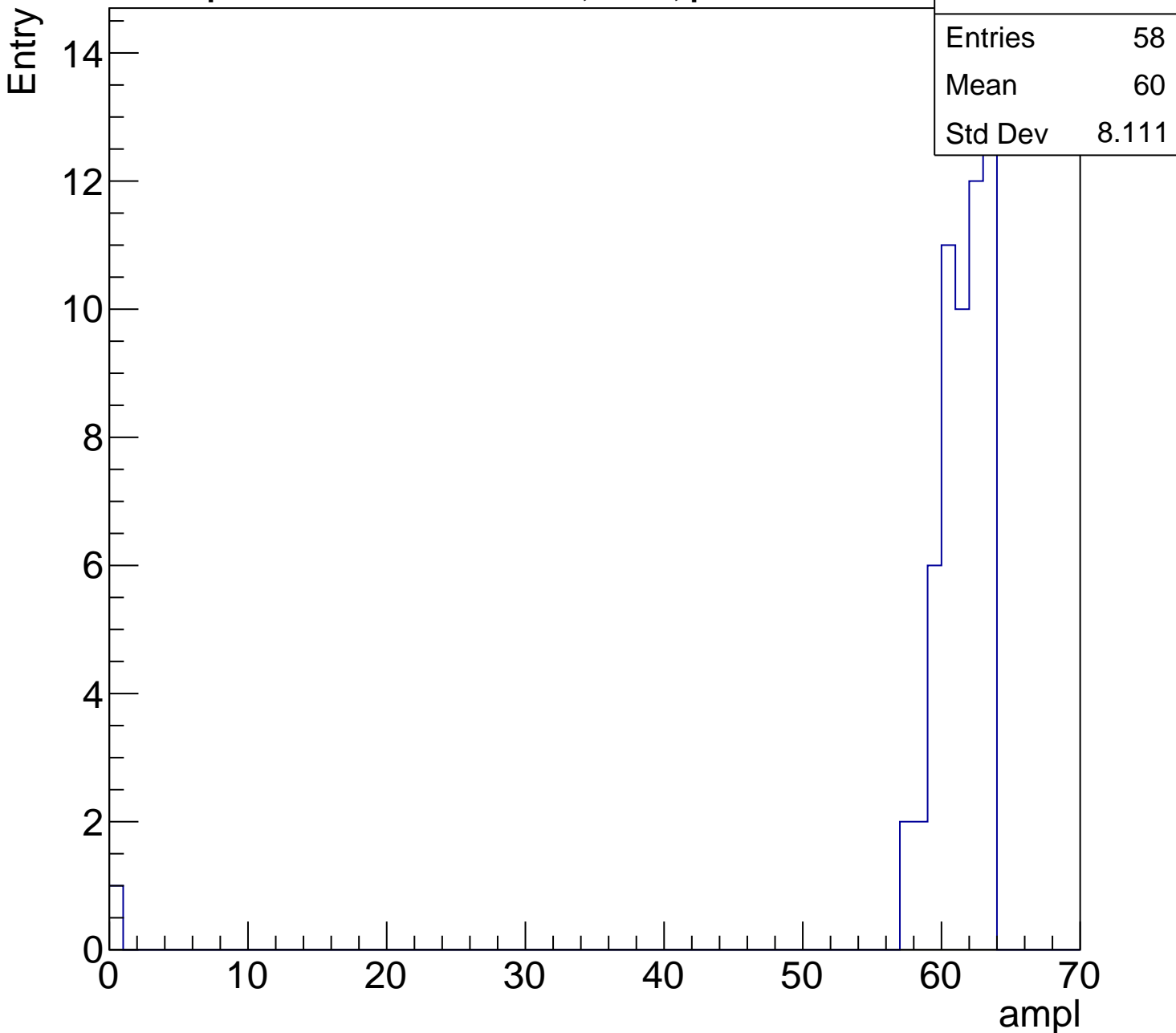
Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	60
Std Dev	8.111

ampl

0 10 20 30 40 50 60 70

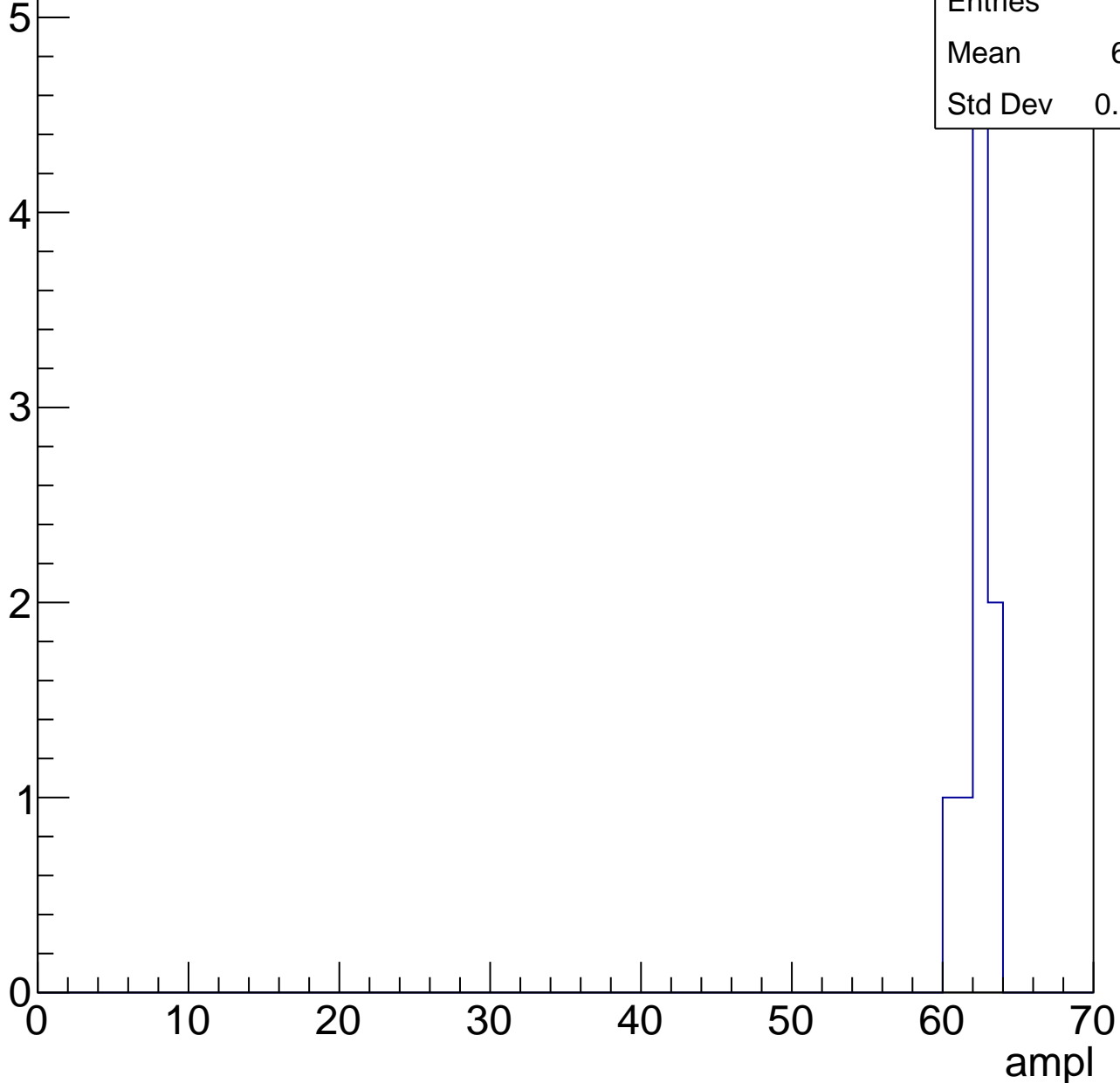


B0L101S, U19-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

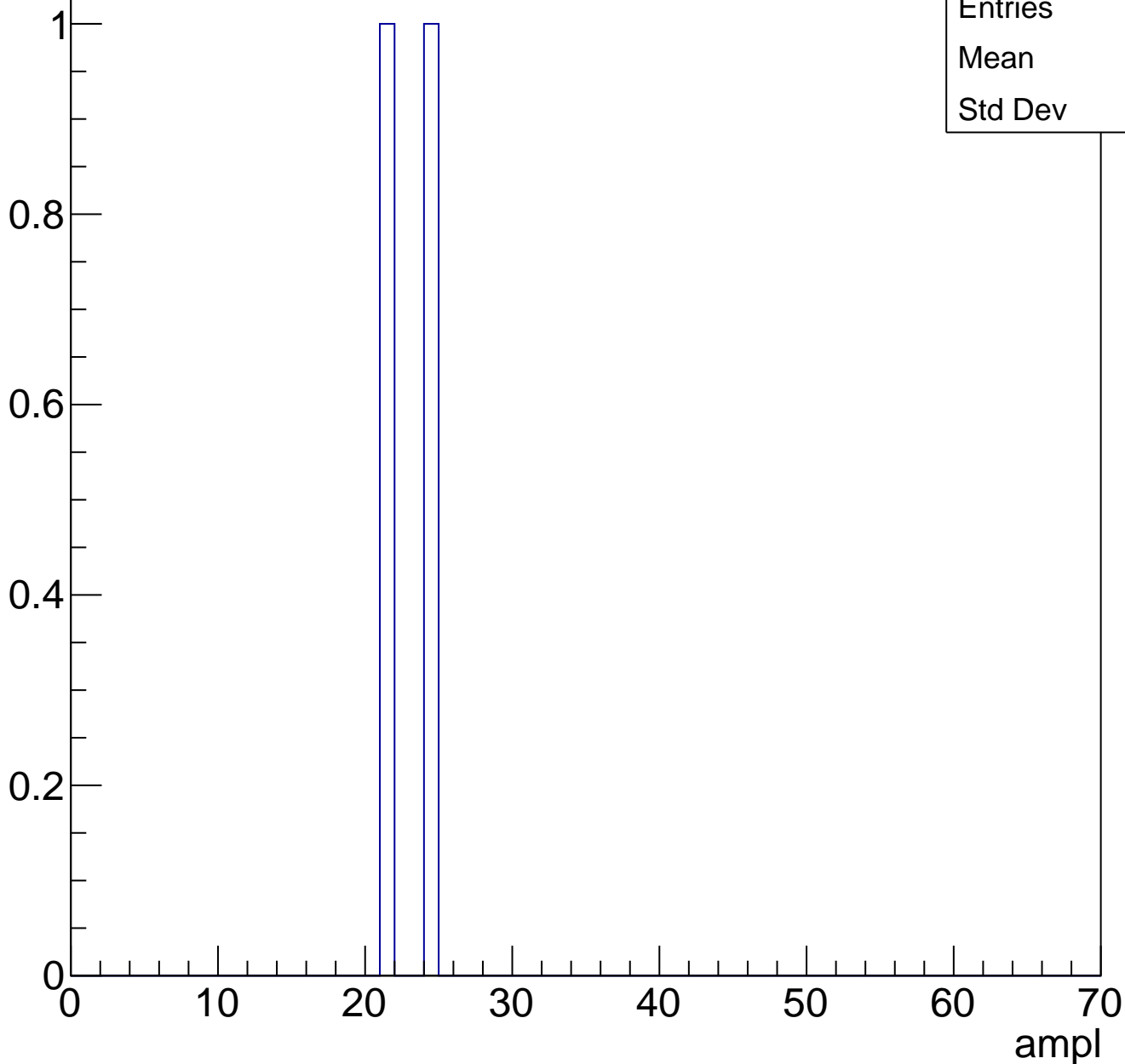
Entries	9
Mean	61.89
Std Dev	0.8749



B0L101S, U19-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch44, adc0

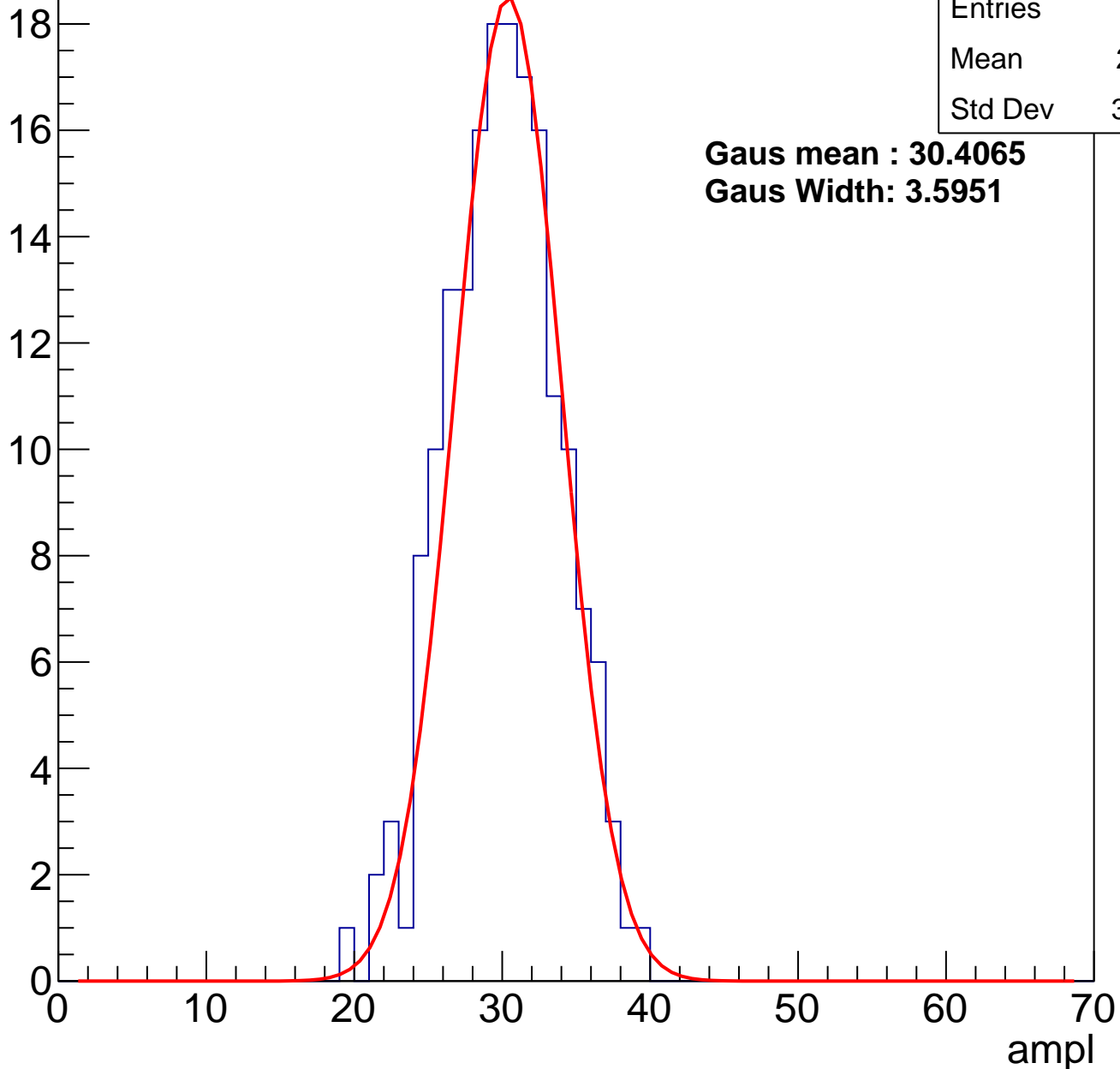
calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	29.61
Std Dev	3.754

Gaus mean : 30.4065

Gaus Width: 3.5951

Entry



B0L101S, U19-ch44, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	37.64
Std Dev	3.691

Gaus mean : 38.1207

Gaus Width: 3.4891

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

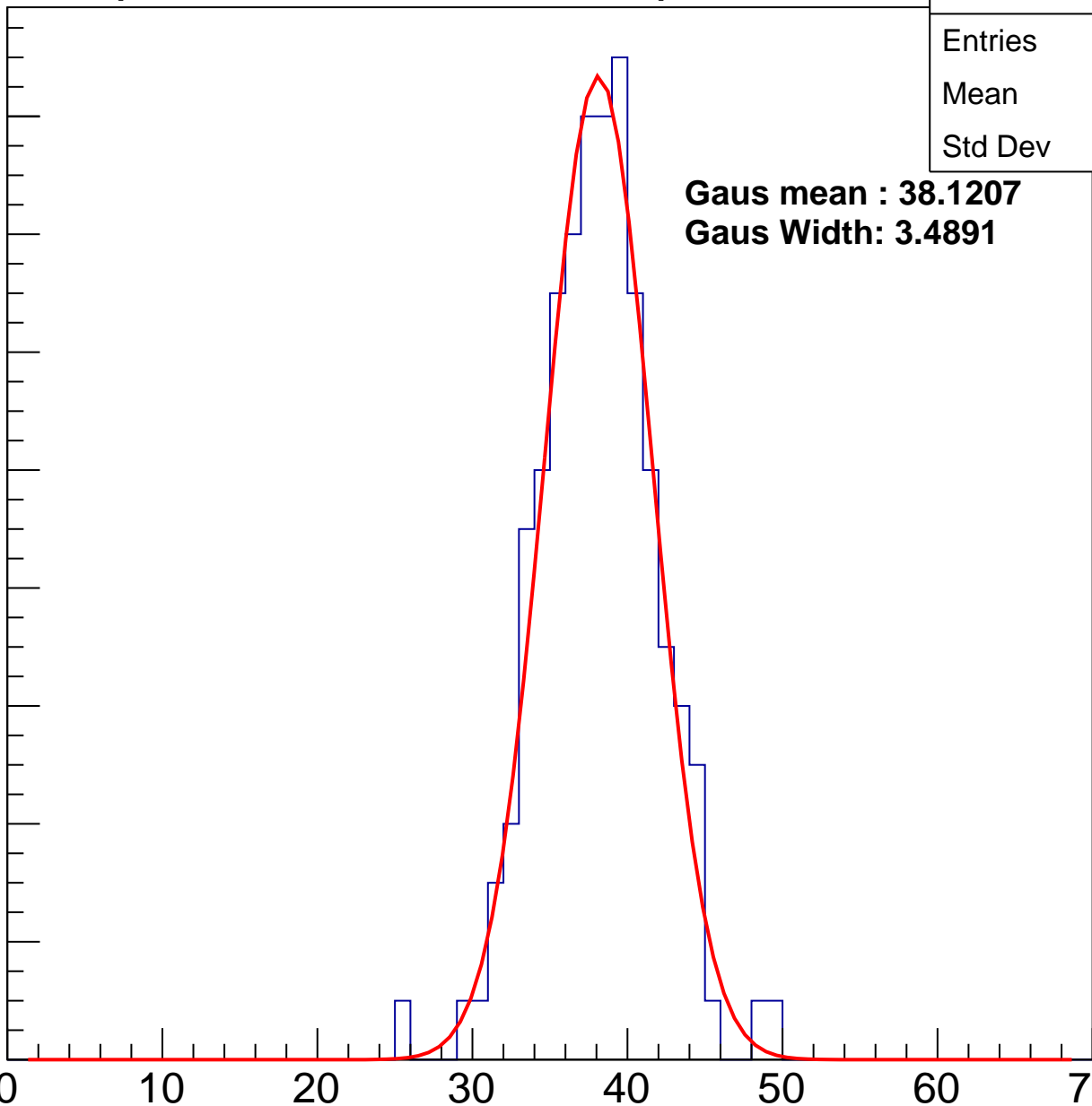
40

50

60

70

ampl



B0L101S, U19-ch44, adc2

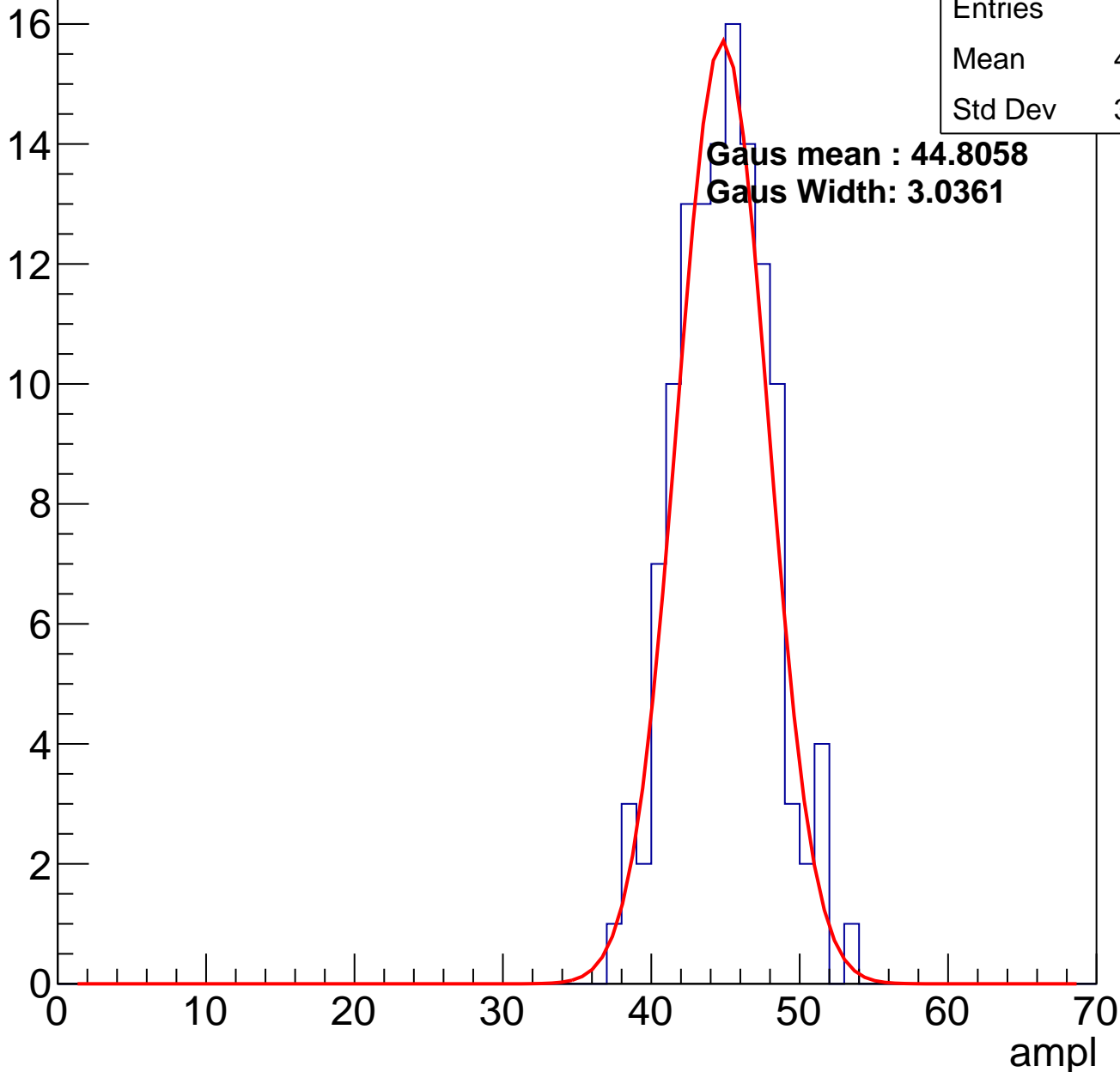
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	44.42
Std Dev	3.119

Gaus mean : 44.8058

Gaus Width: 3.0361

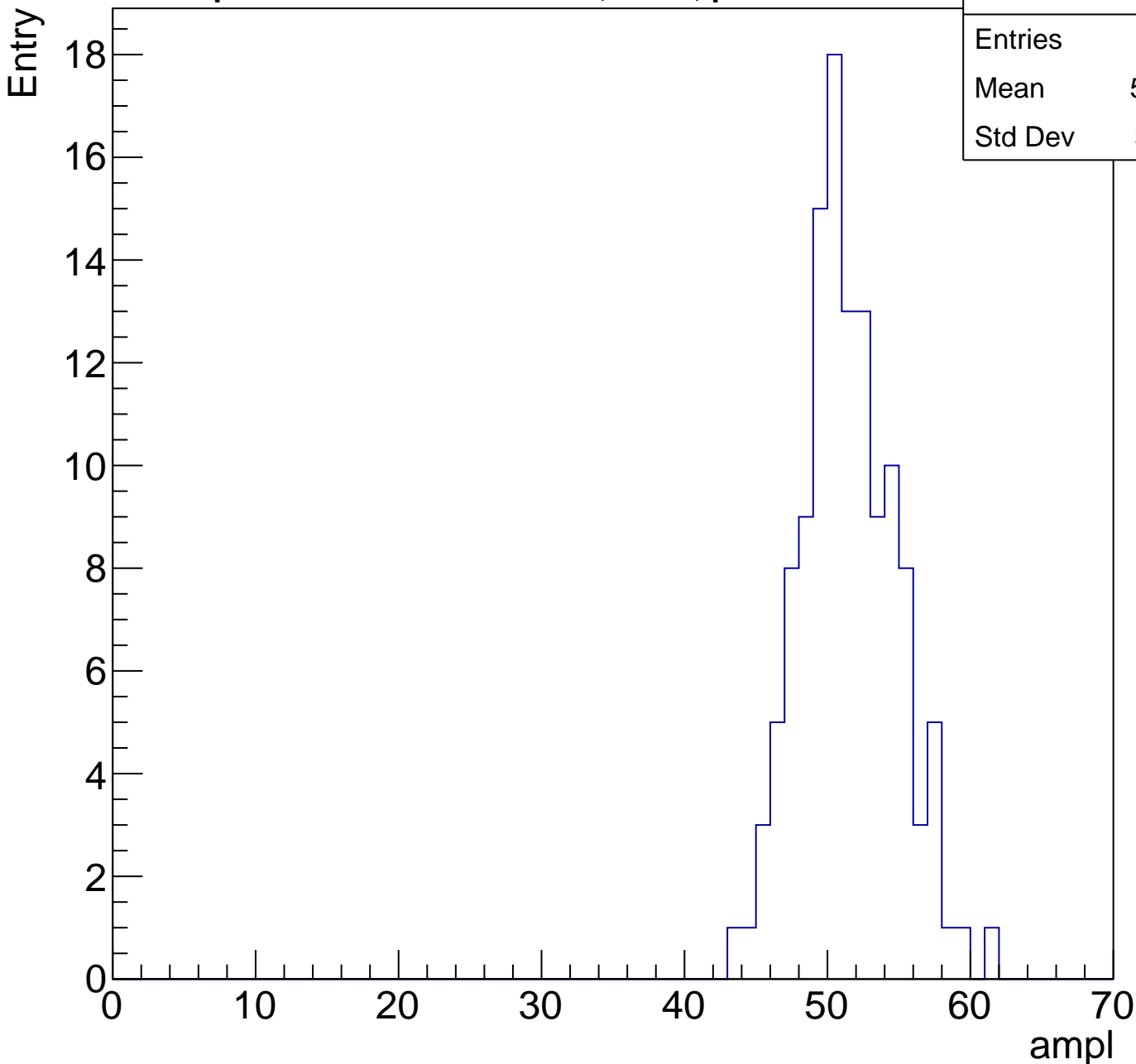
Entry



B0L101S, U19-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	50.98
Std Dev	3.321



B0L101S, U19-ch44, adc4

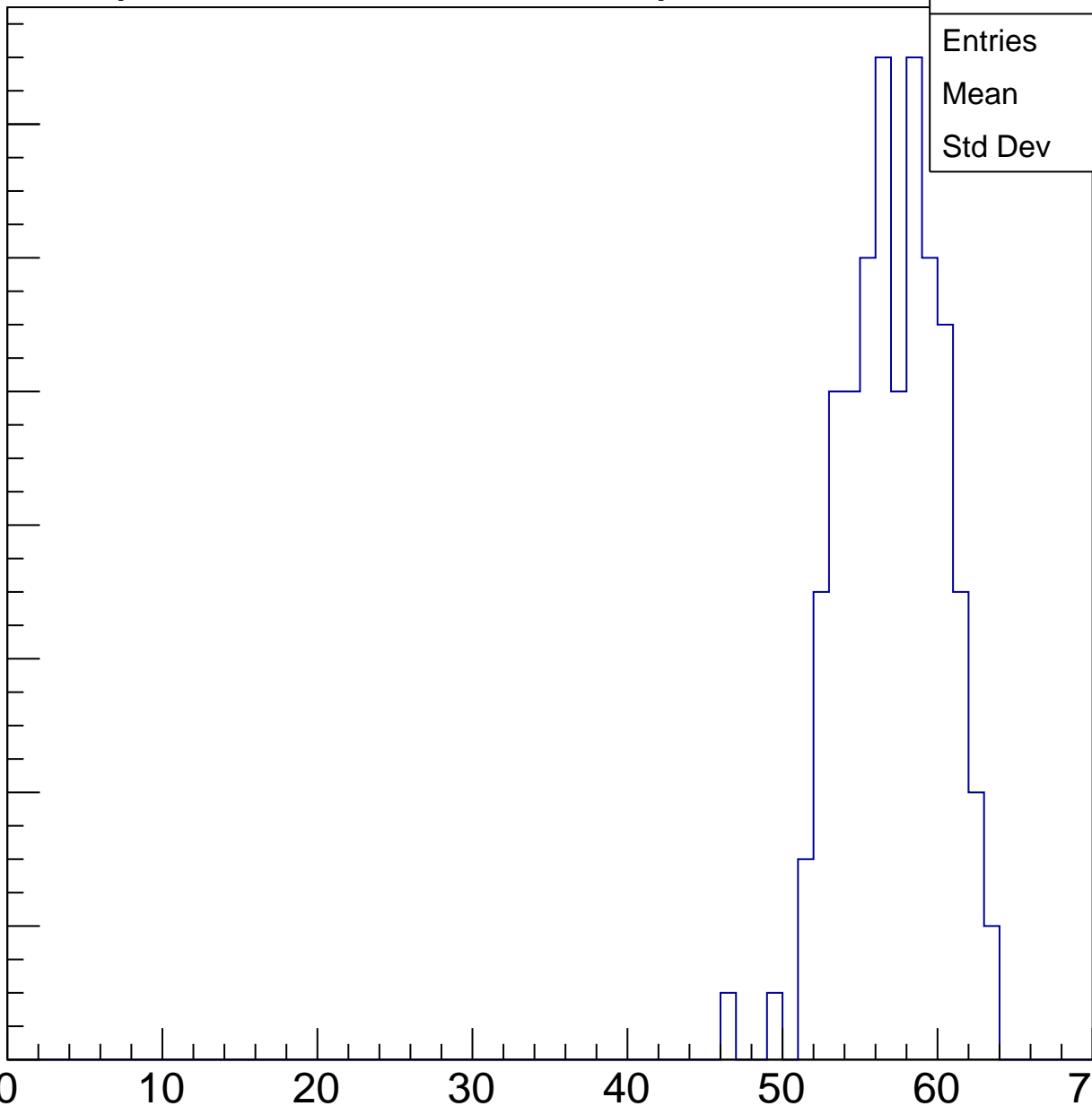
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	56.59
Std Dev	3.171

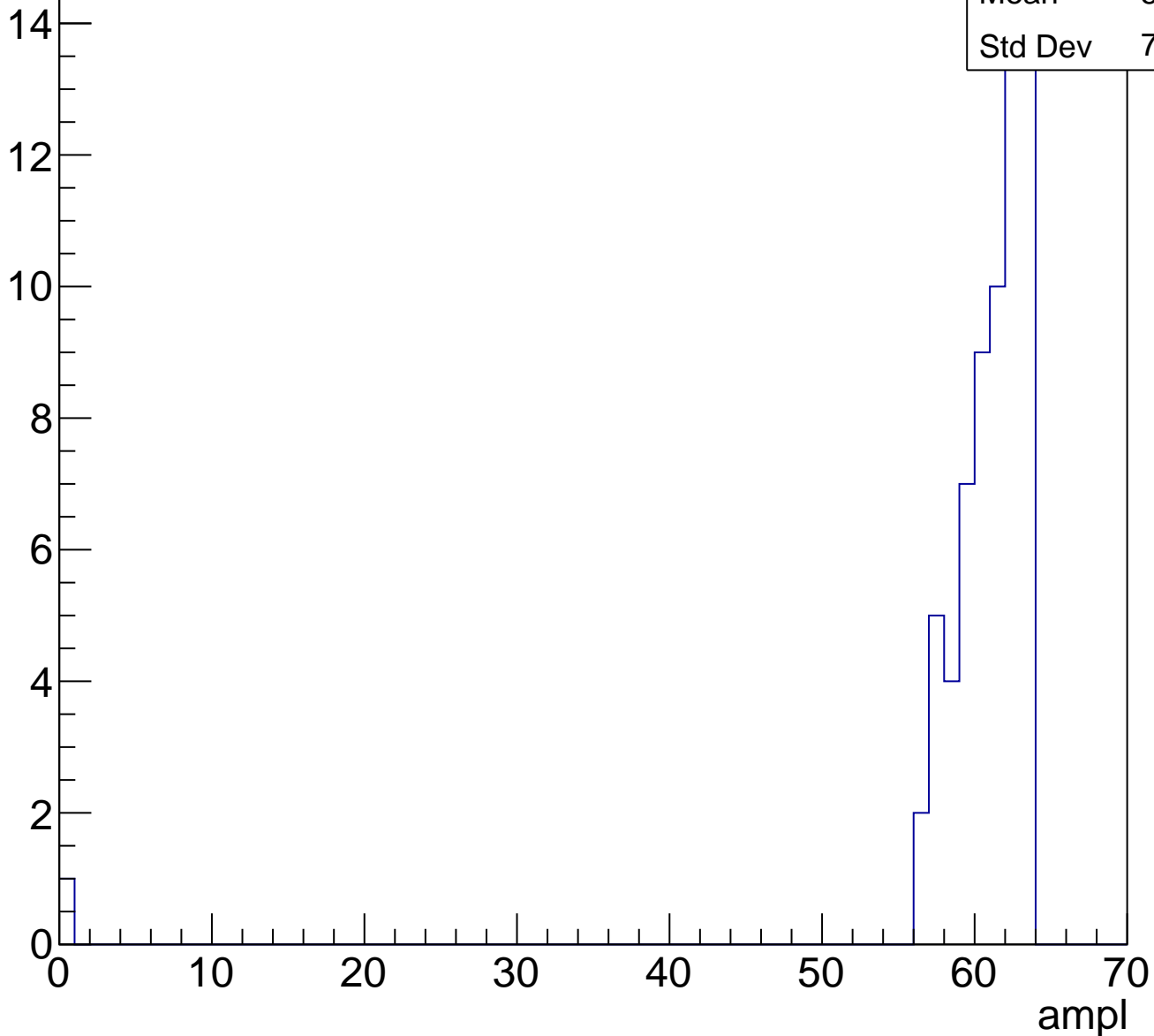
ampl



B0L101S, U19-ch44, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U19-ch45, adc0

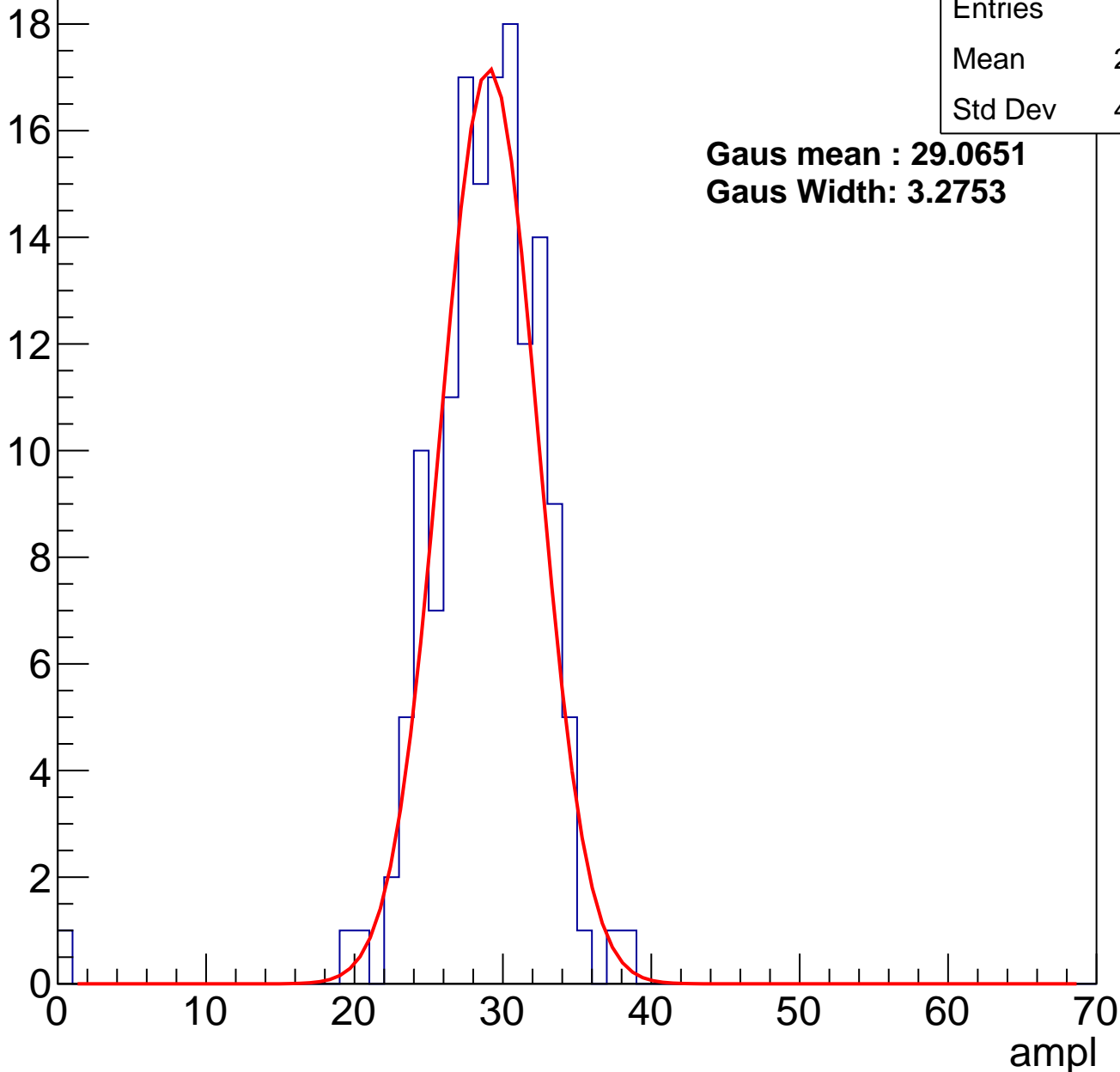
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	28.43
Std Dev	4.059

Gaus mean : 29.0651

Gaus Width: 3.2753

Entry



B0L101S, U19-ch45, adc1

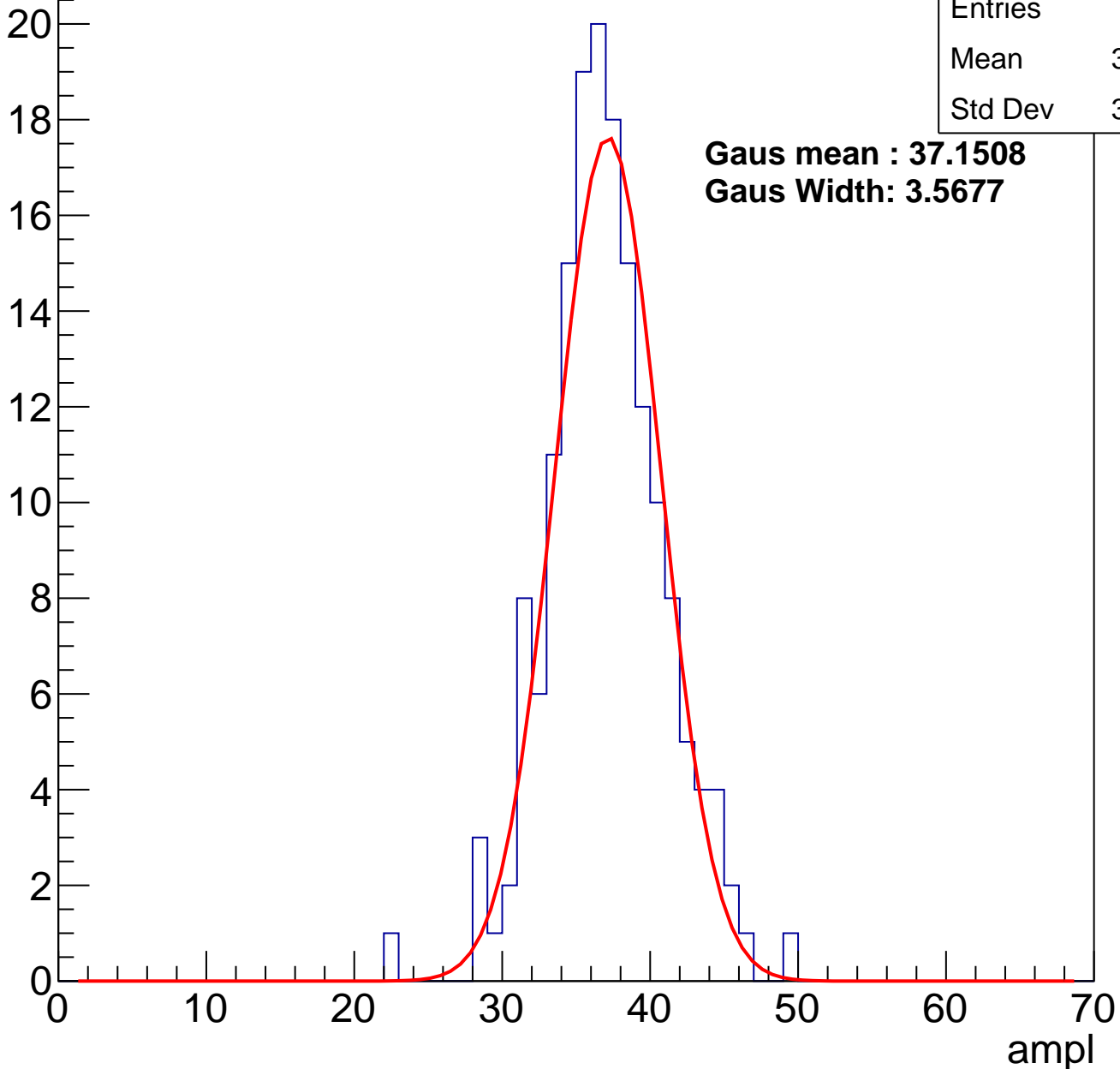
calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	36.55
Std Dev	3.912

Gaus mean : 37.1508

Gaus Width: 3.5677

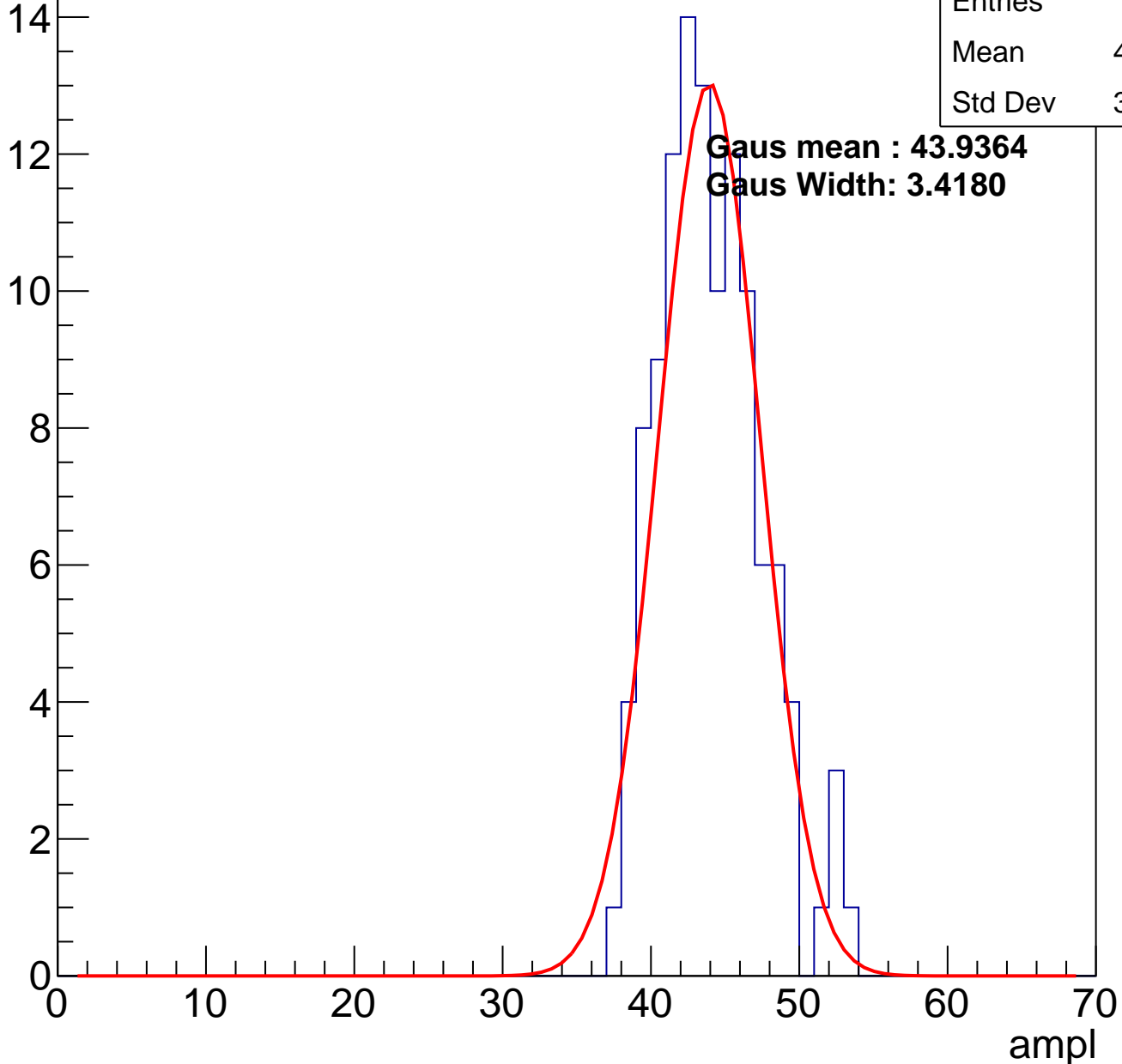
Entry



B0L101S, U19-ch45, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

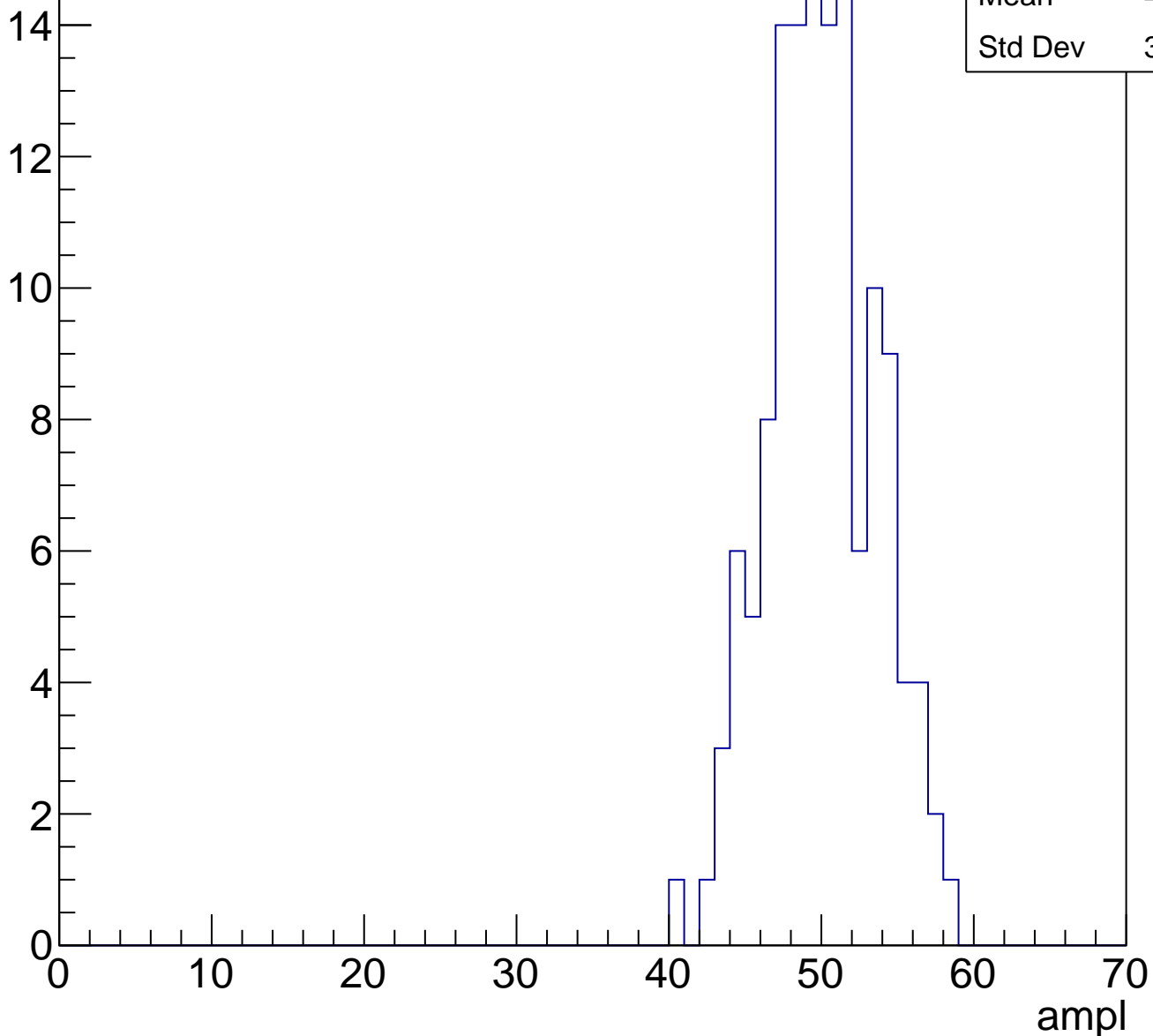


B0L101S, U19-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	49.56
Std Dev	3.515

Entry



B0L101S, U19-ch45, adc4

calib_packv5_042523_0143.root, FC#1, port C1

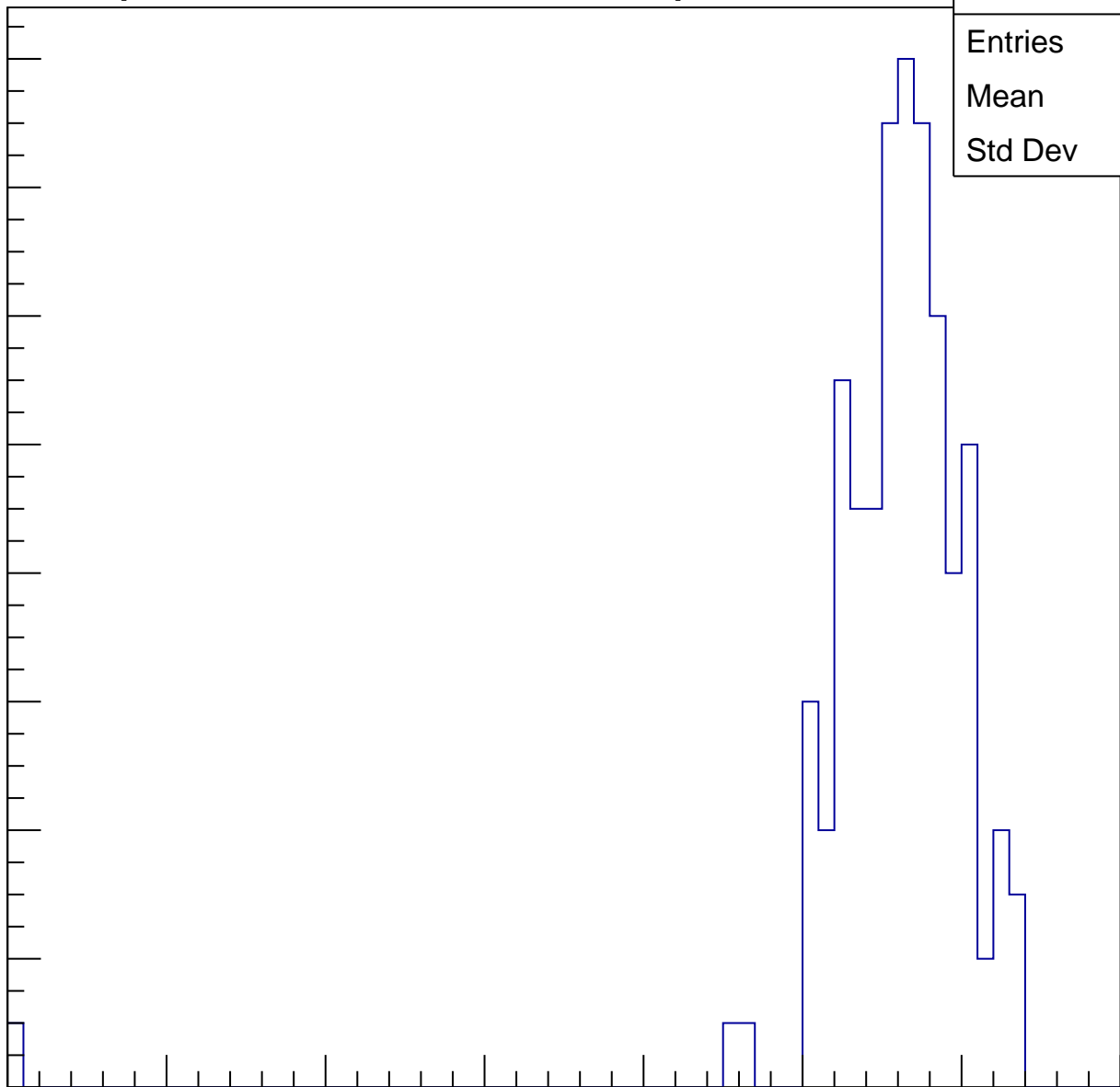
Entries	127
Mean	55.38
Std Dev	5.997

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch45, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

79

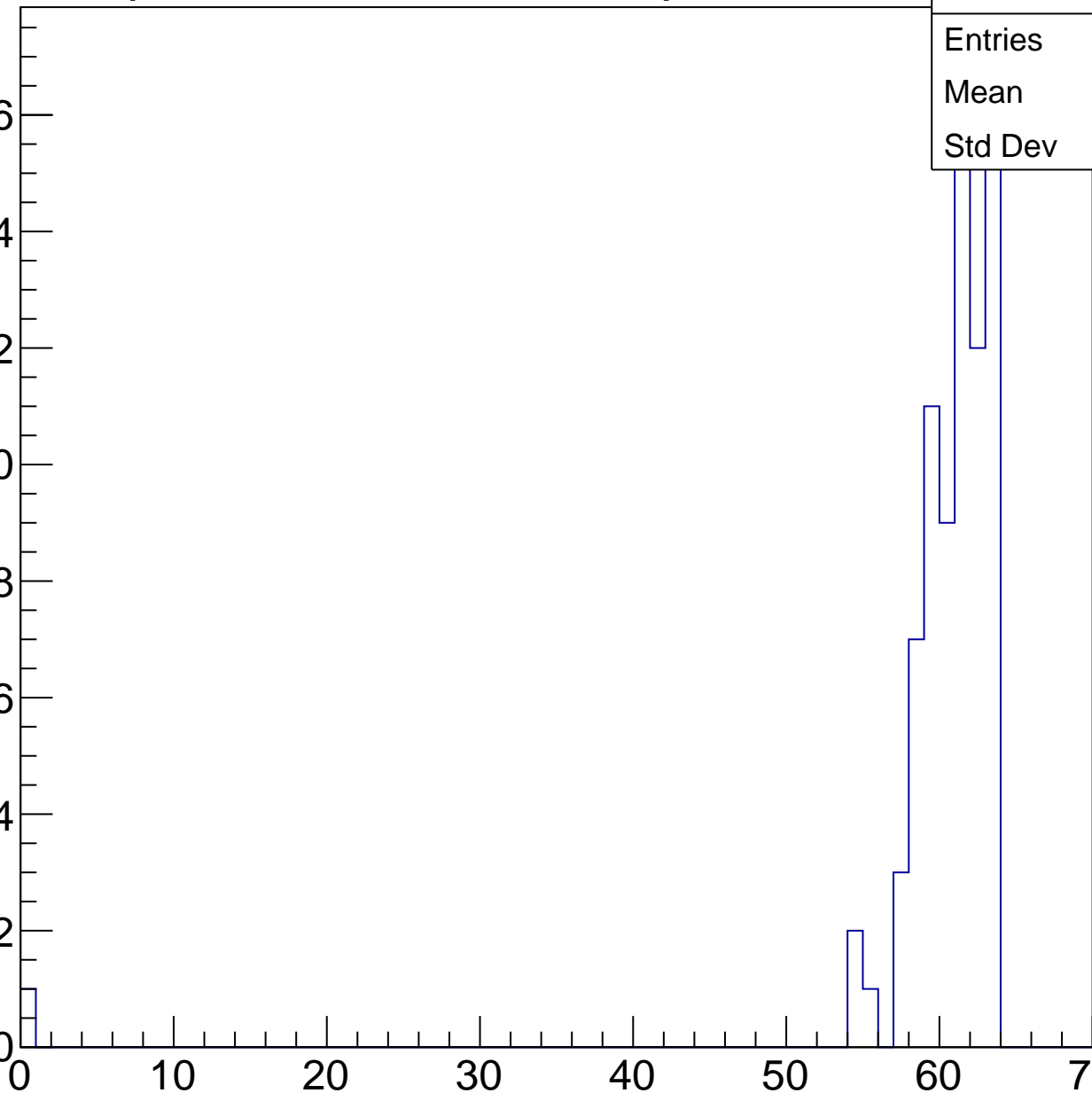
Mean

59.72

Std Dev

7.085

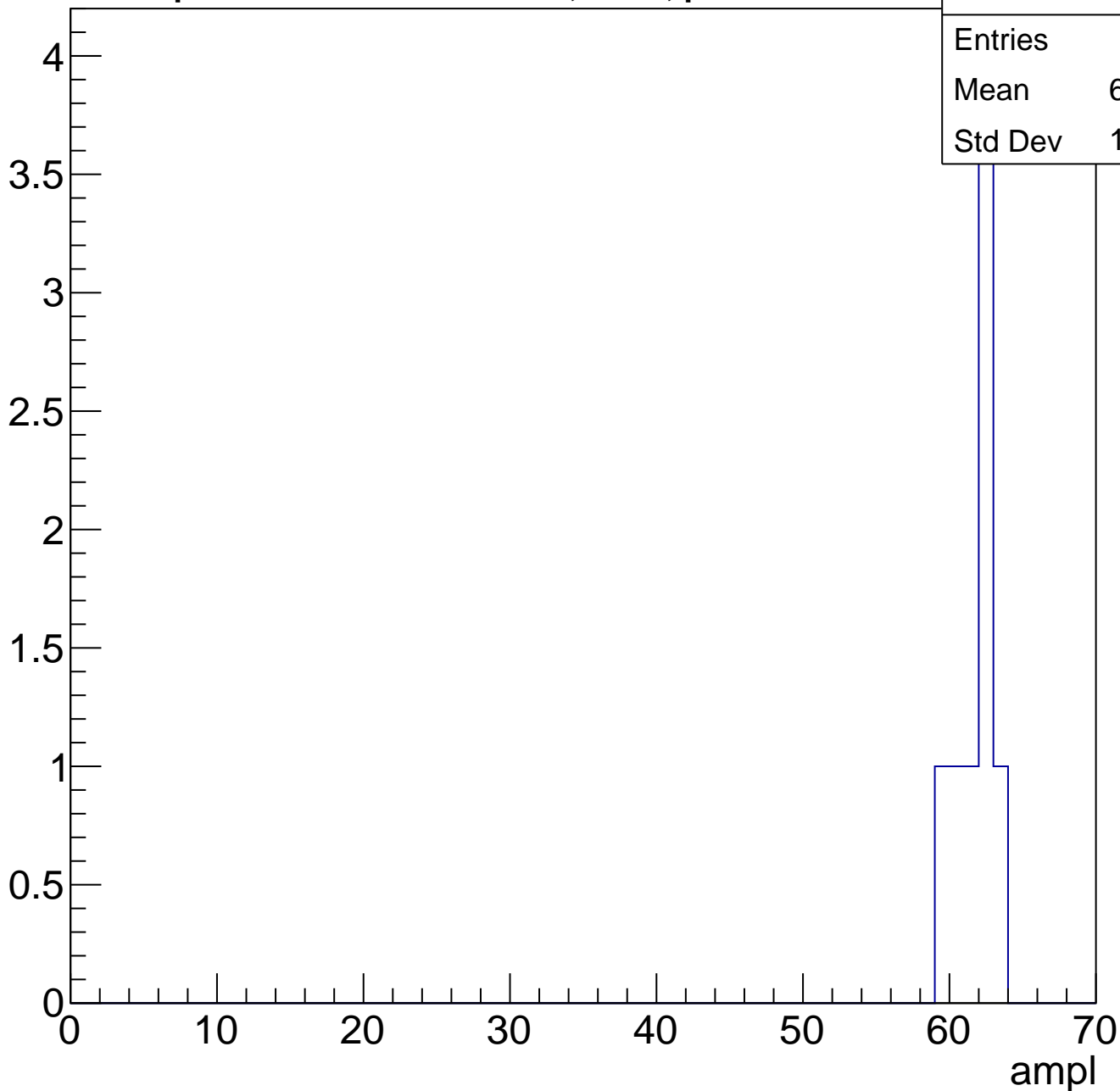
ampl



B0L101S, U19-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch46, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.44
Std Dev	3.948

Gaus mean : 30.0268

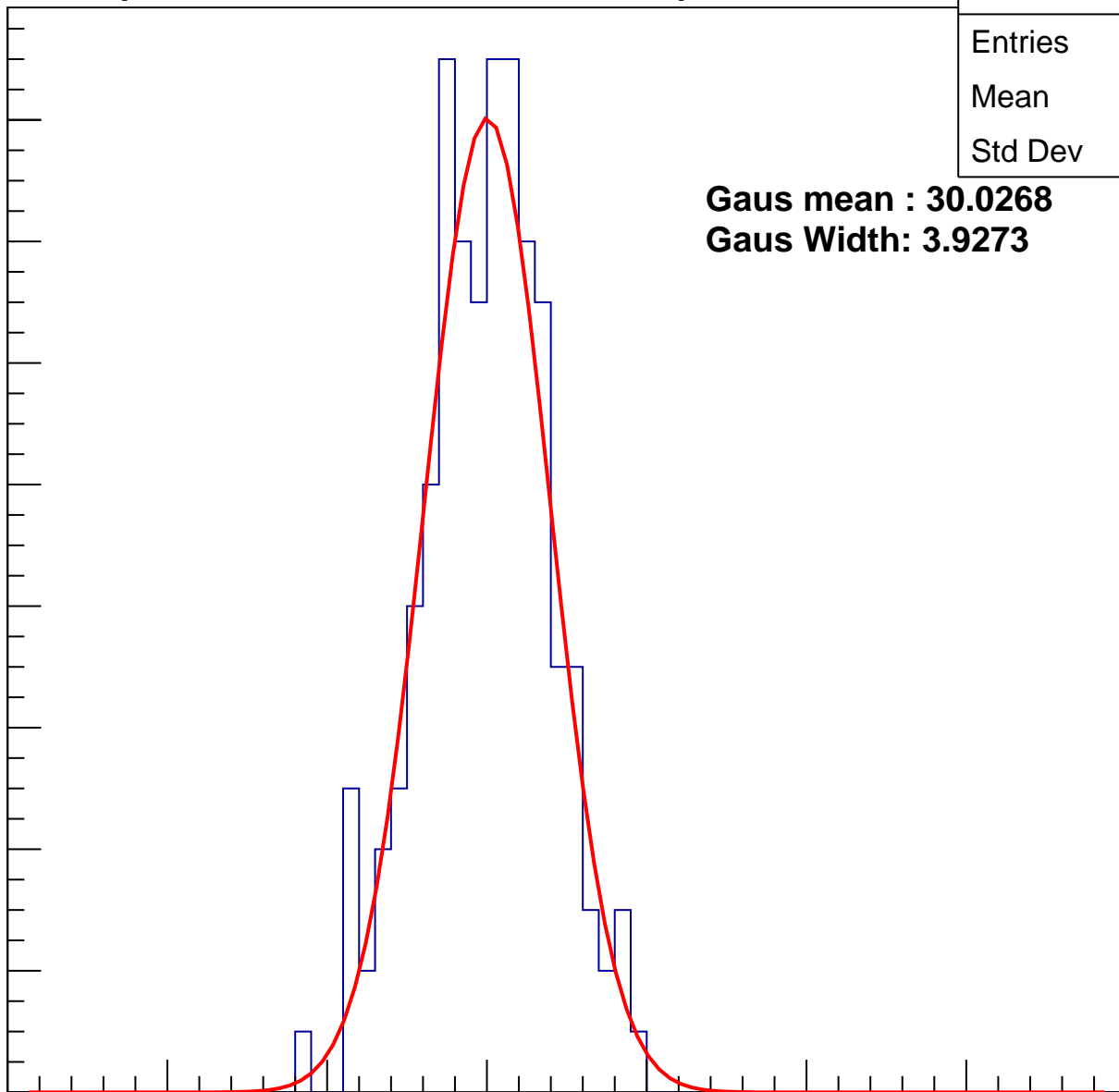
Gaus Width: 3.9273

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

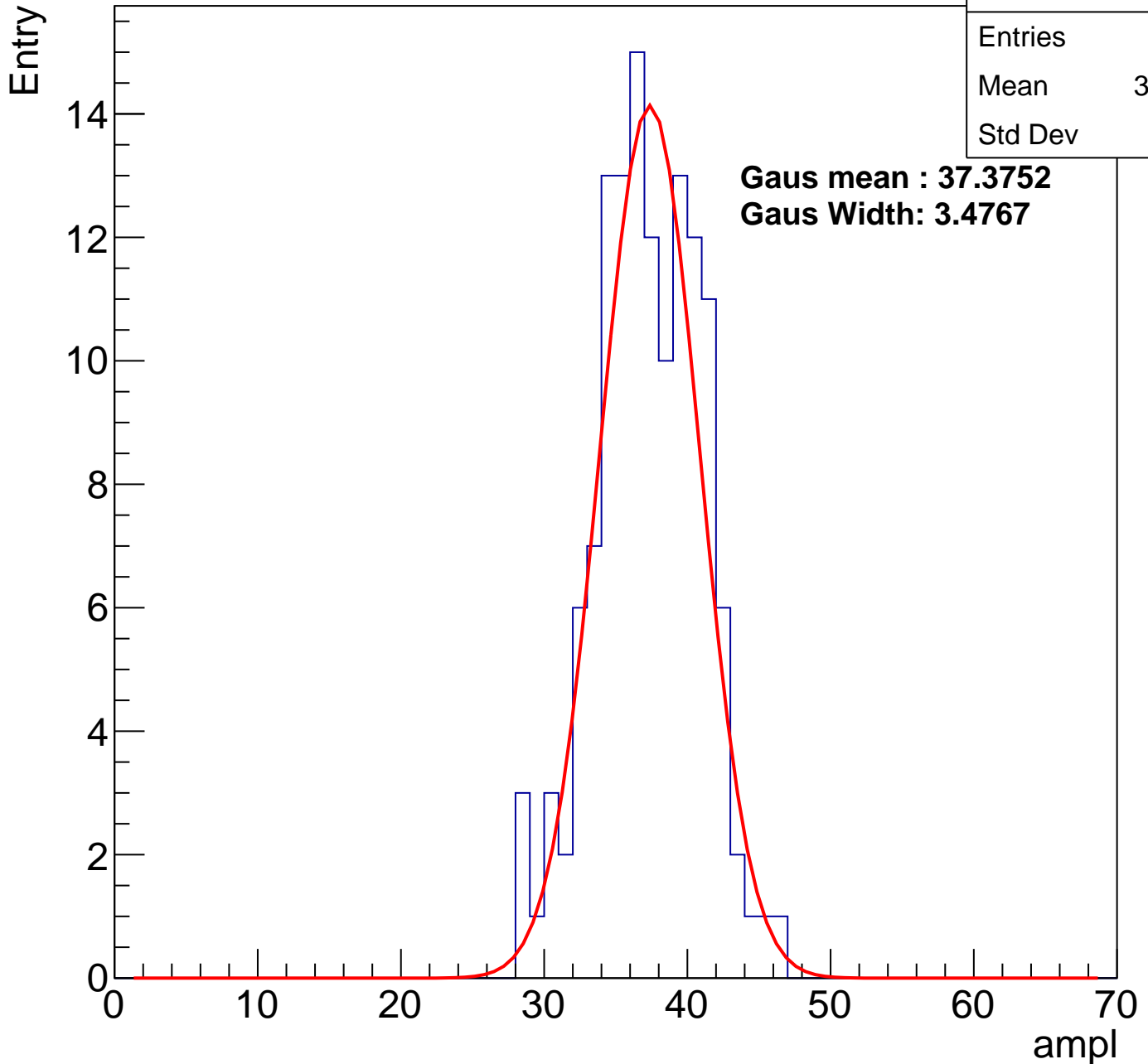


B0L101S, U19-ch46, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	36.82
Std Dev	3.62

Gaus mean : 37.3752
Gaus Width: 3.4767



B0L101S, U19-ch46, adc2

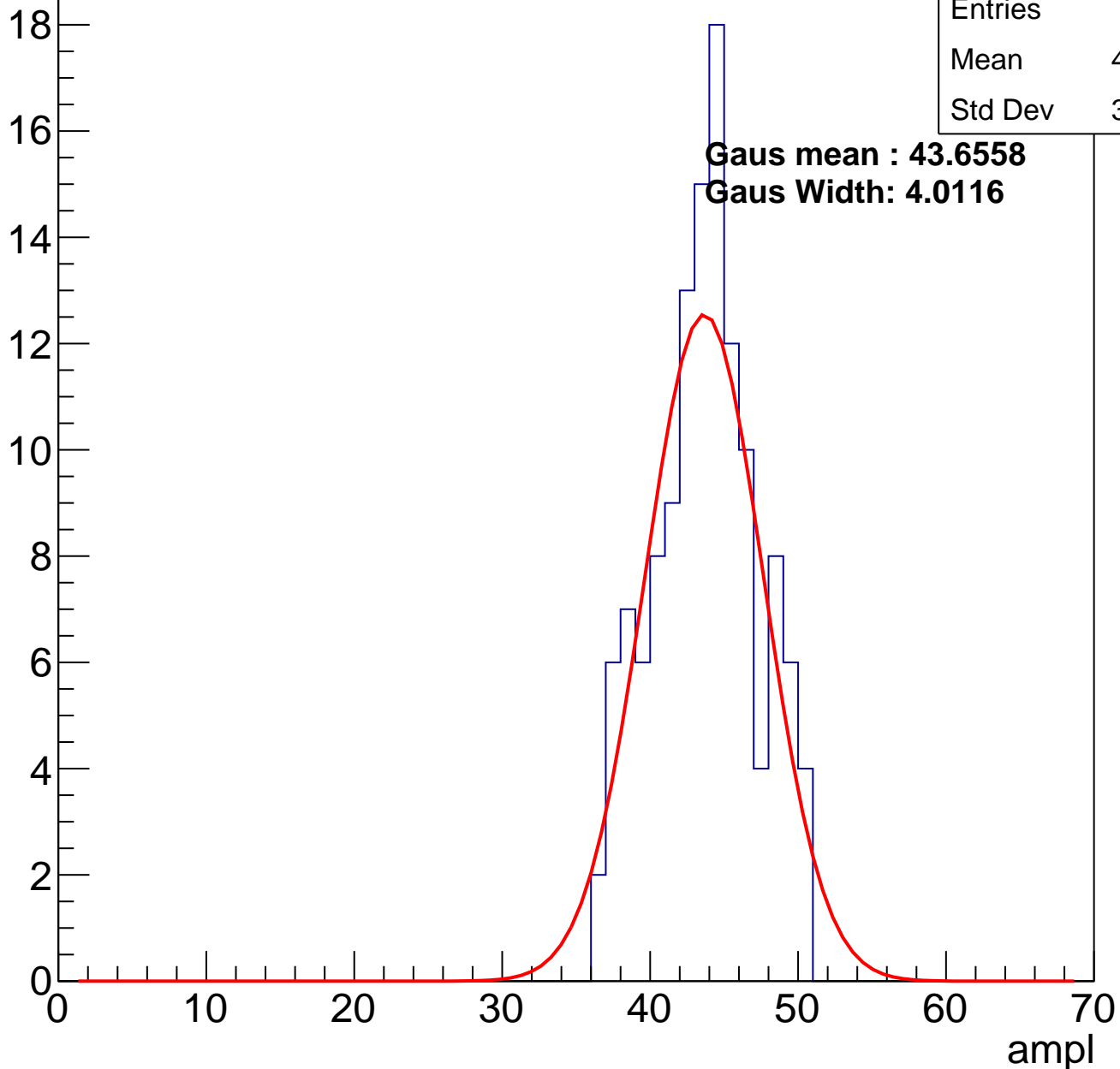
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	43.22
Std Dev	3.459

Gaus mean : 43.6558

Gaus Width: 4.0116

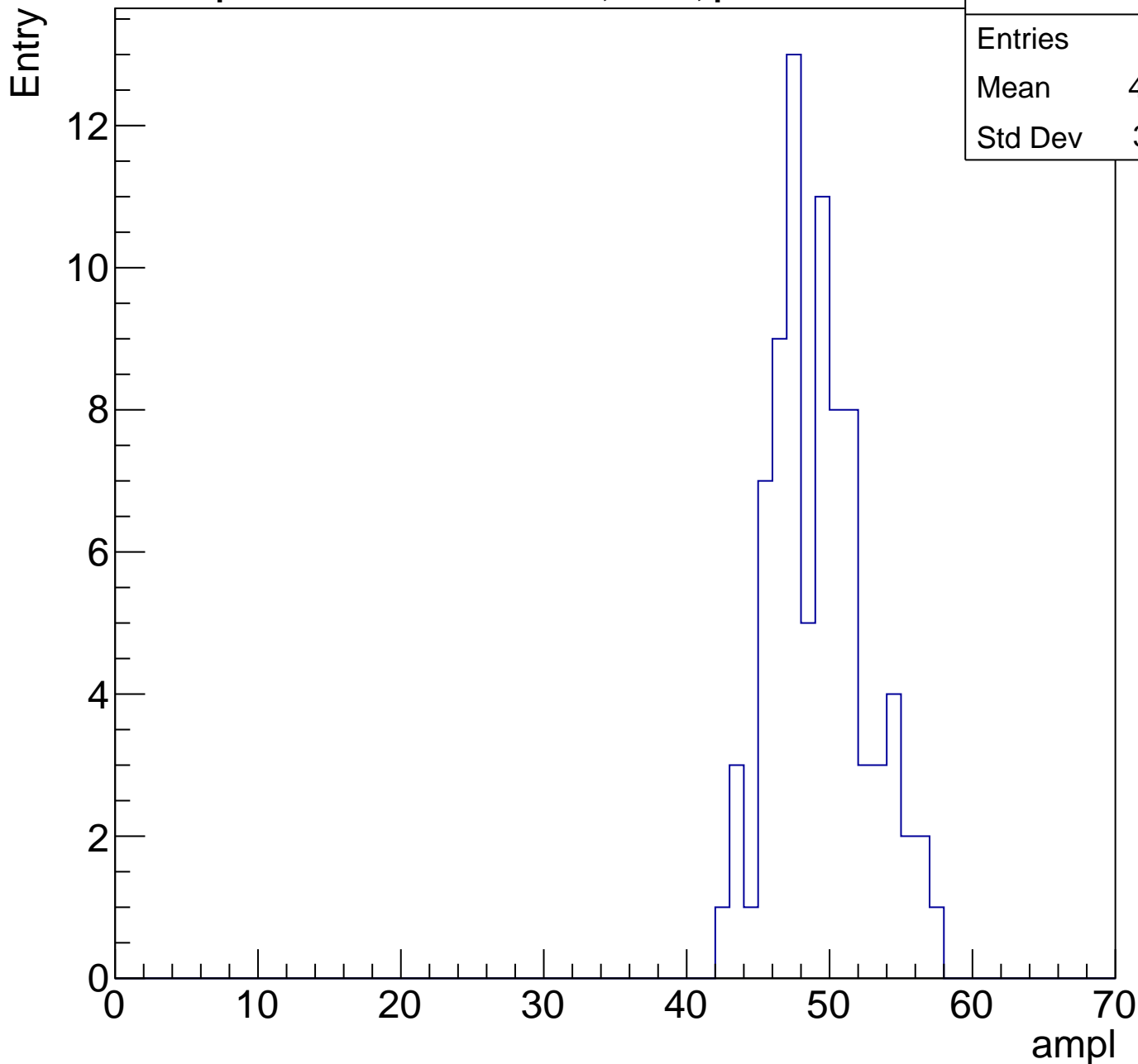
Entry



B0L101S, U19-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	81
Mean	48.79
Std Dev	3.291



B0L101S, U19-ch46, adc4

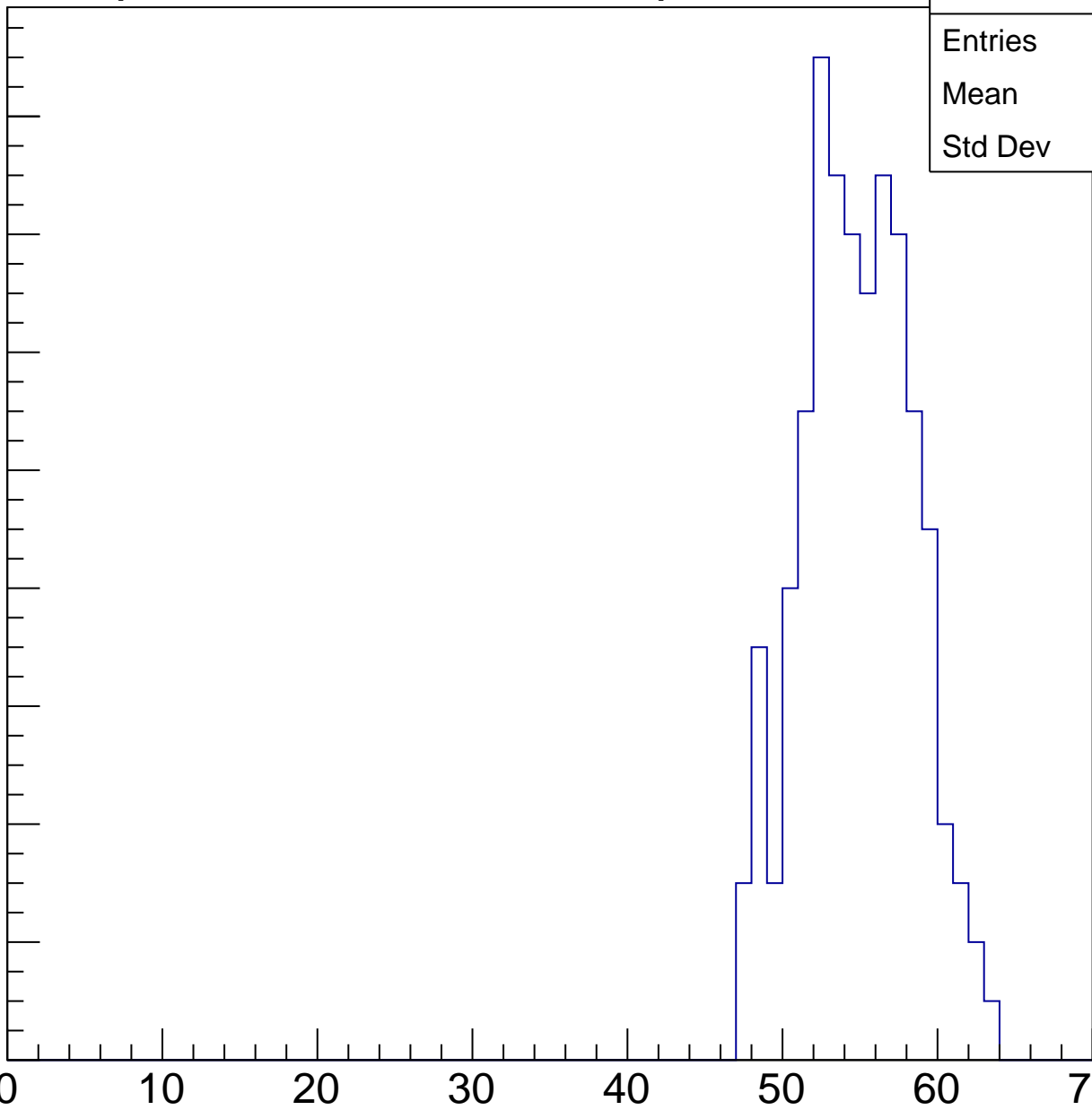
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	54.35
Std Dev	3.527

Entry

16
14
12
10
8
6
4
2
0

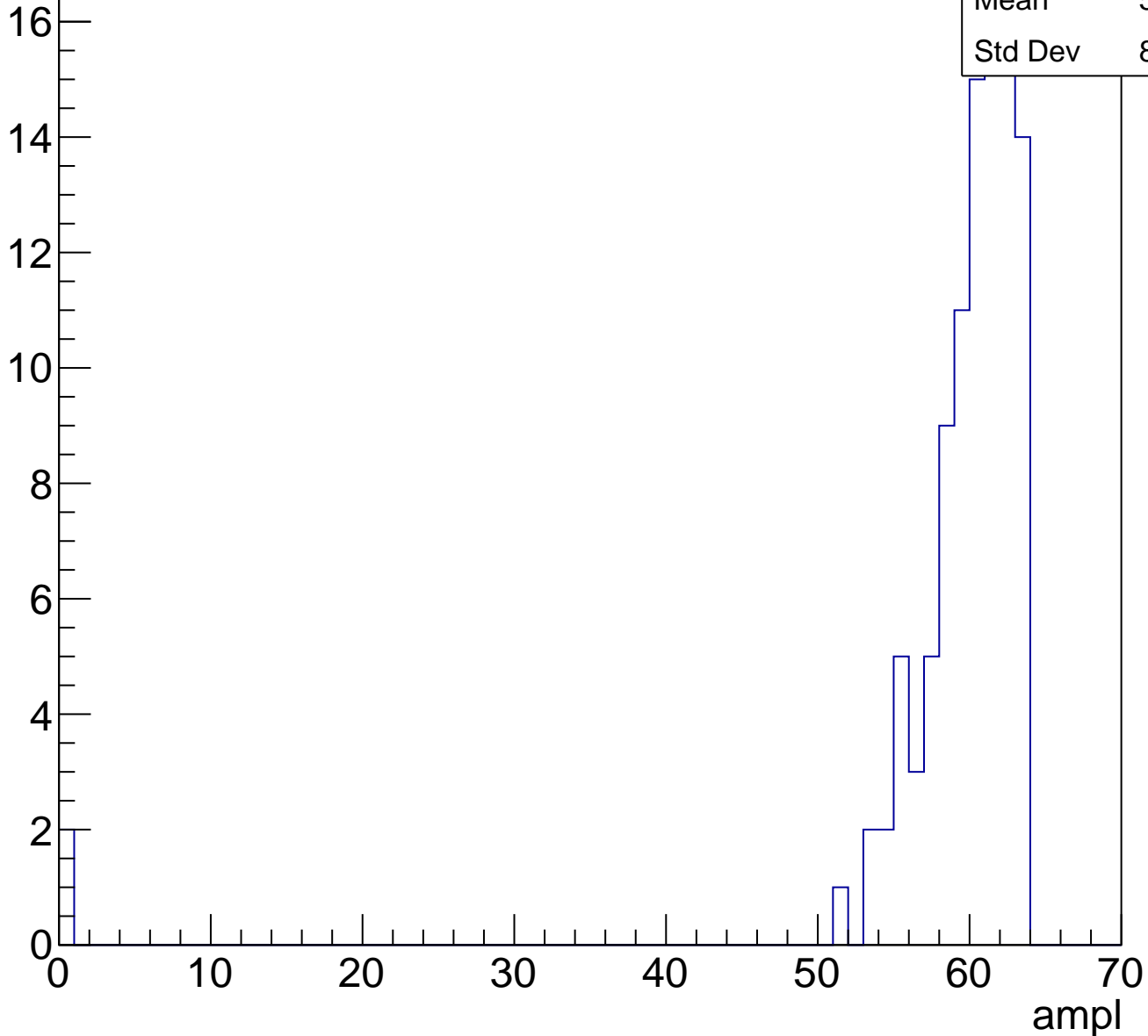
ampl



B0L101S, U19-ch46, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

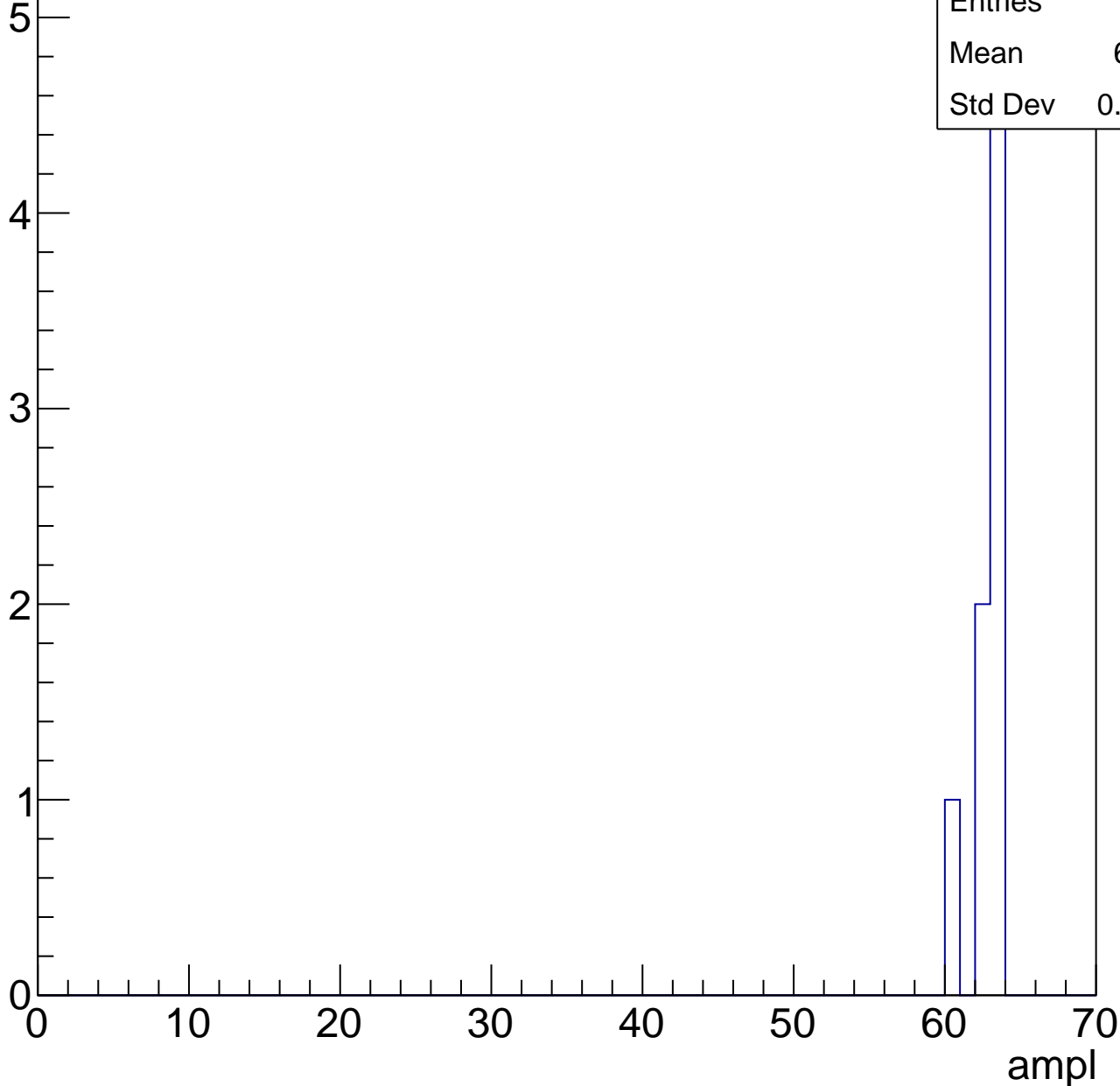


B0L101S, U19-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B0L101S, U19-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch47, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	29.91
Std Dev	3.685

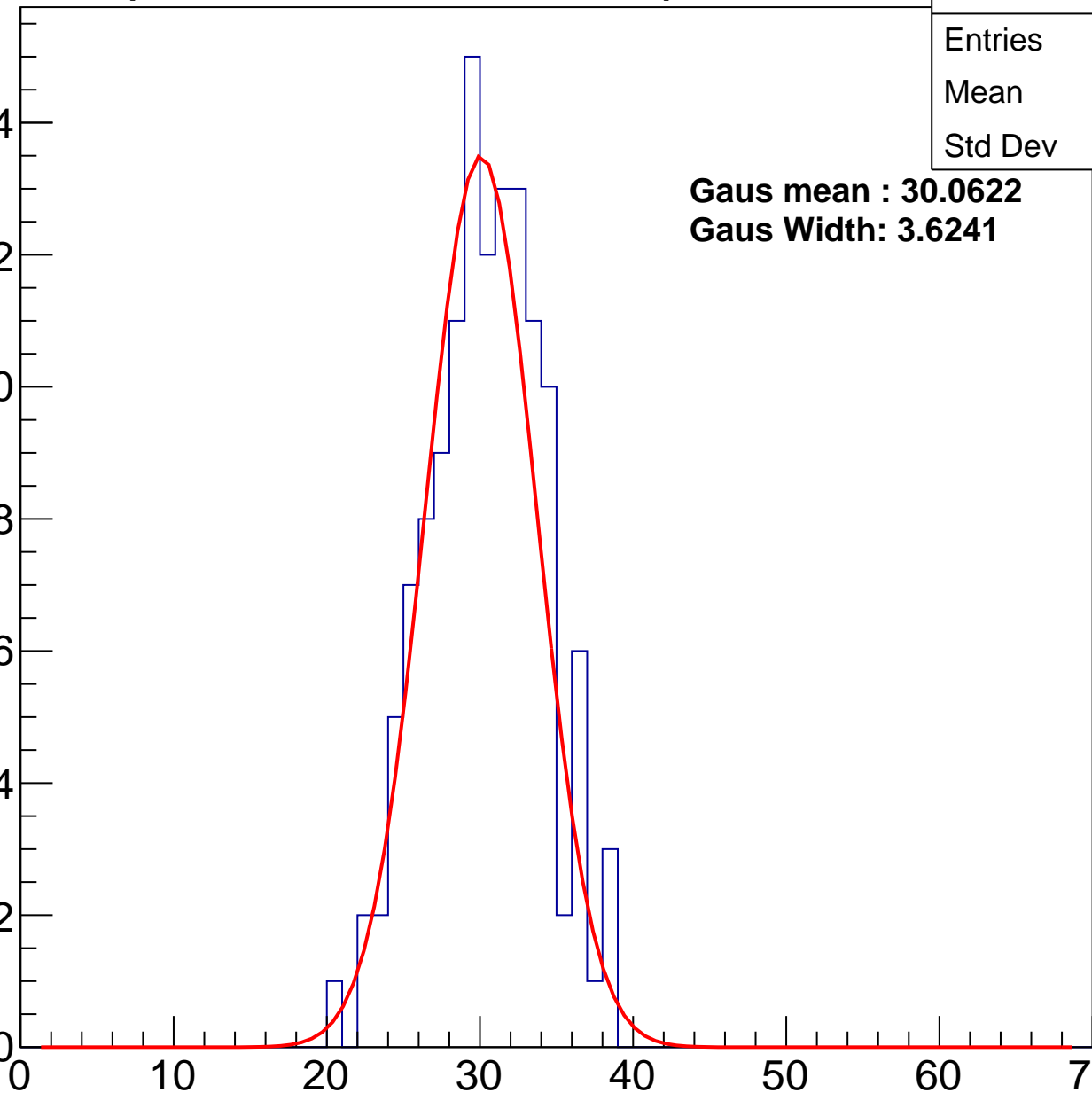
Gaus mean : 30.0622

Gaus Width: 3.6241

Entry

14
12
10
8
6
4
2
0

ampl



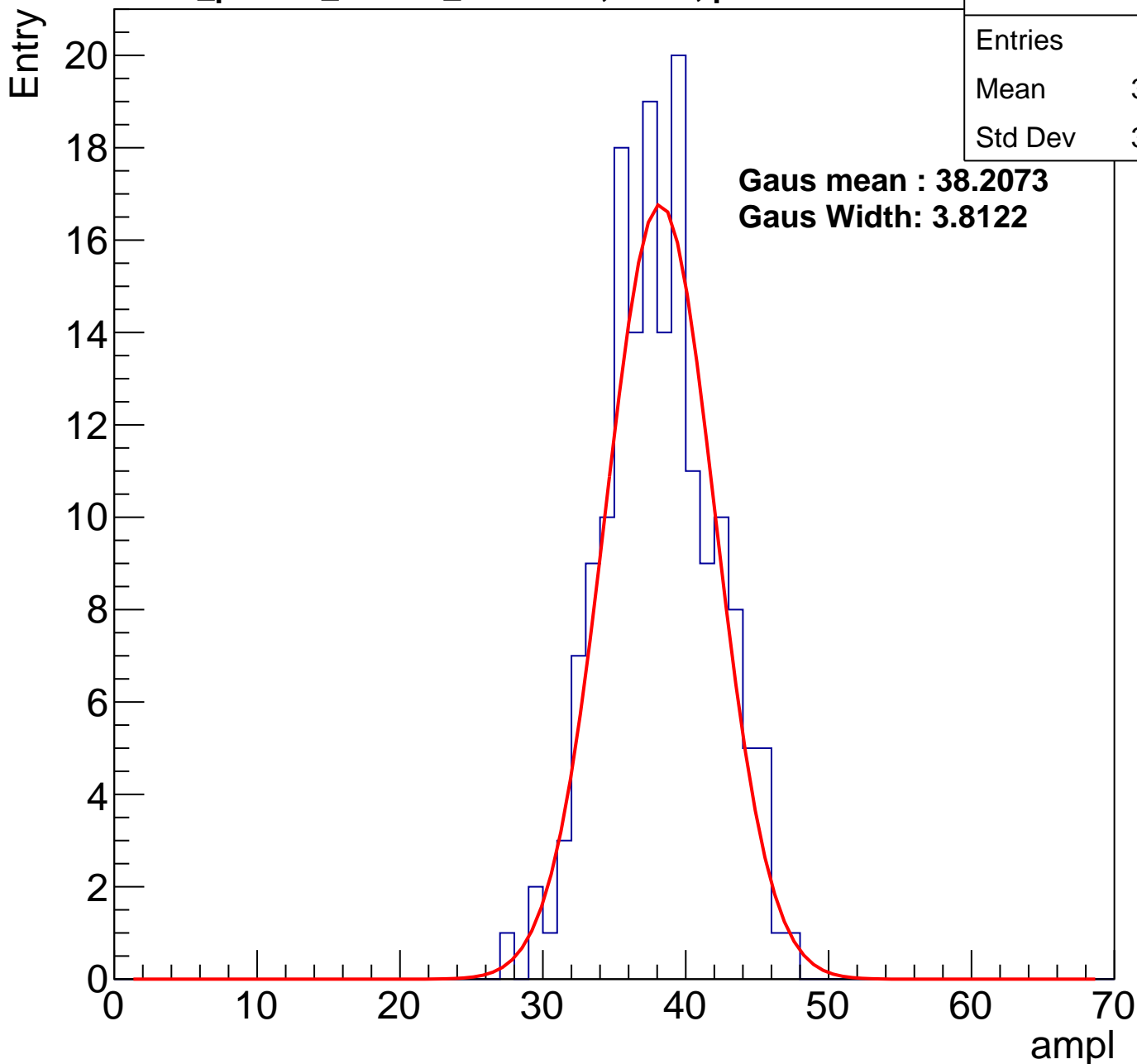
B0L101S, U19-ch47, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	37.67
Std Dev	3.799

Gaus mean : 38.2073

Gaus Width: 3.8122



B0L101S, U19-ch47, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

115

Mean

44.59

Std Dev

3.198

Gaus mean : 44.3087

Gaus Width: 3.5261

0

10

20

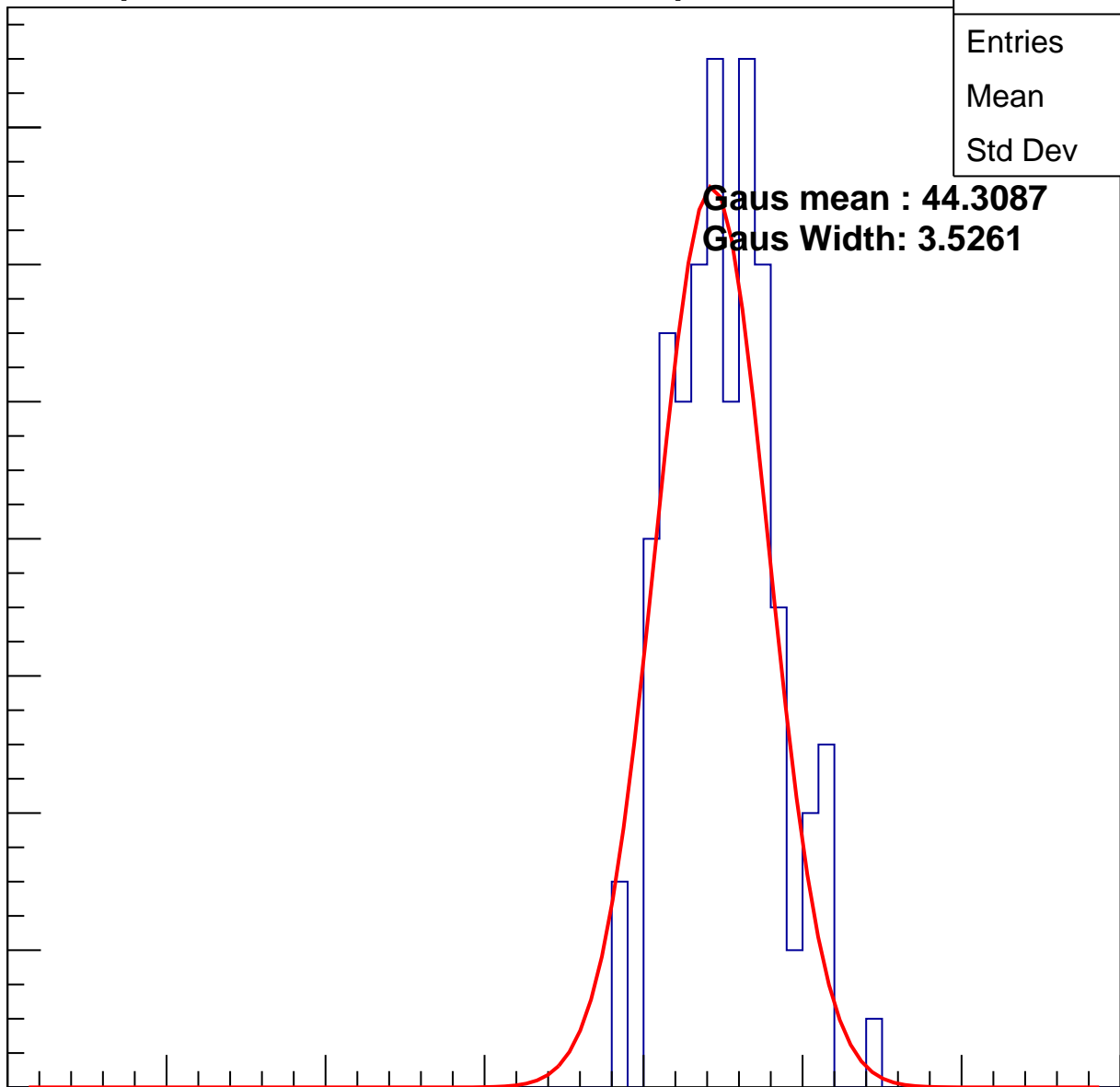
30

40

50

60

ampl

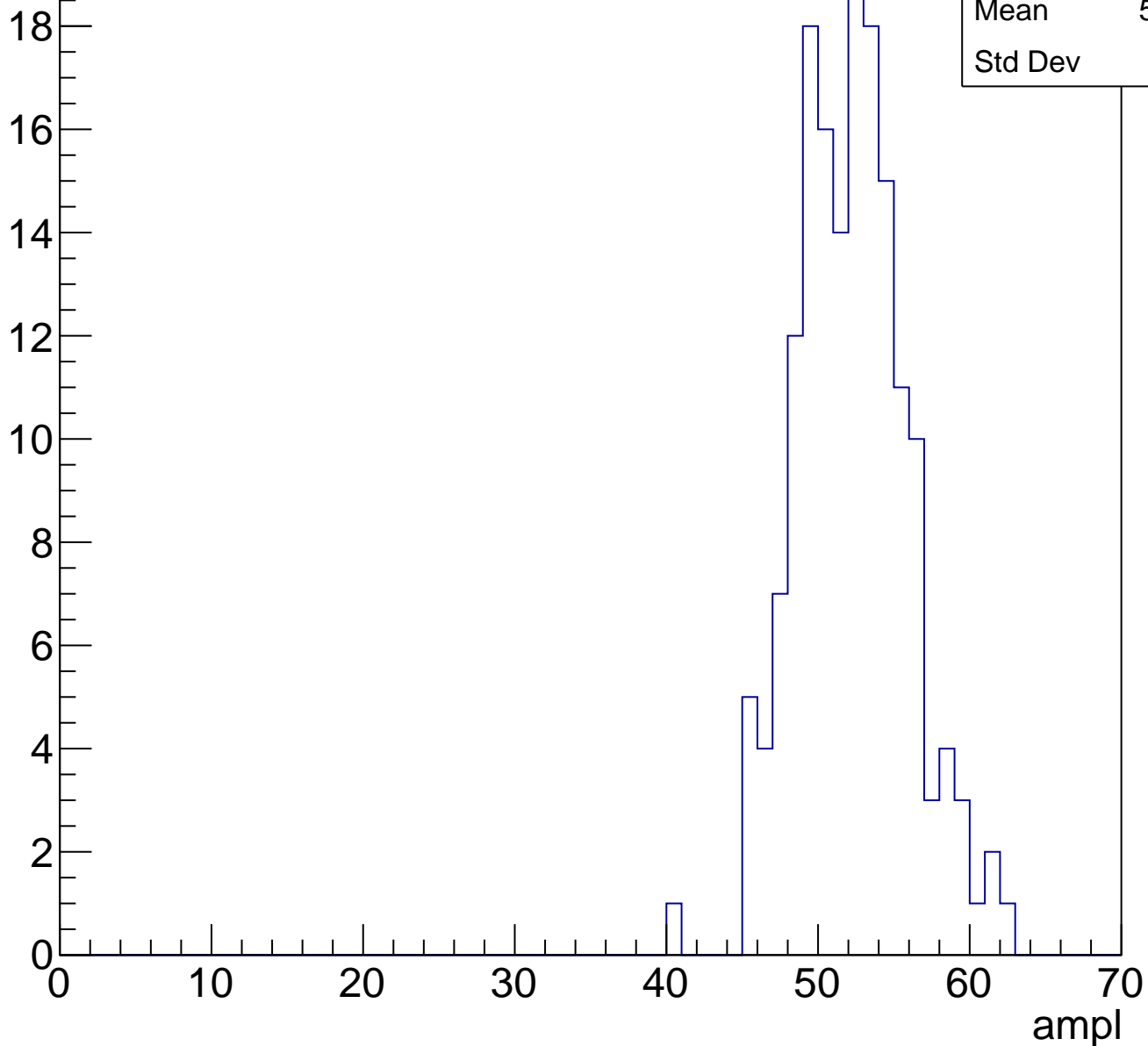


B0L101S, U19-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	51.77
Std Dev	3.63

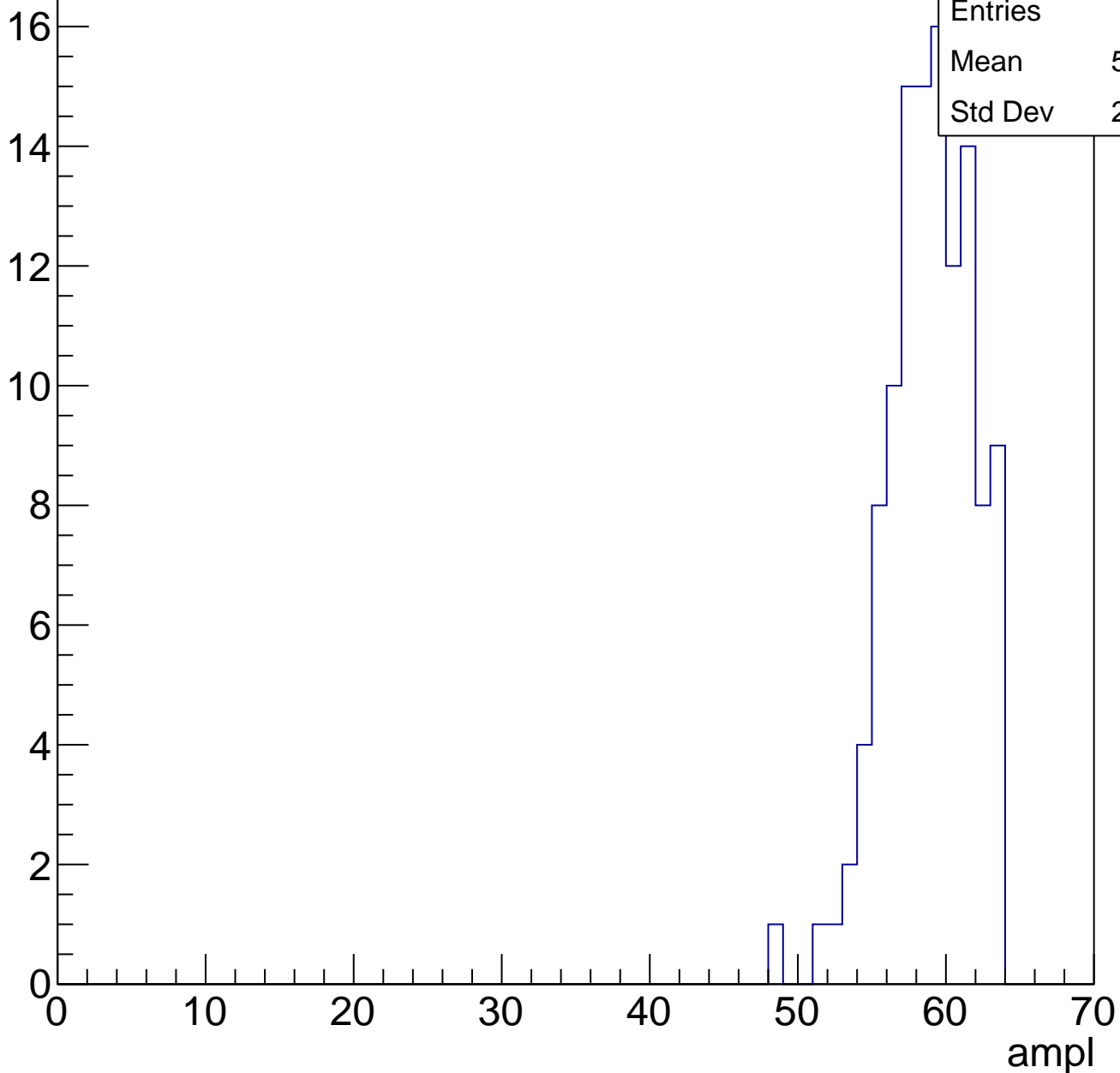
Entry



B0L101S, U19-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch47, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

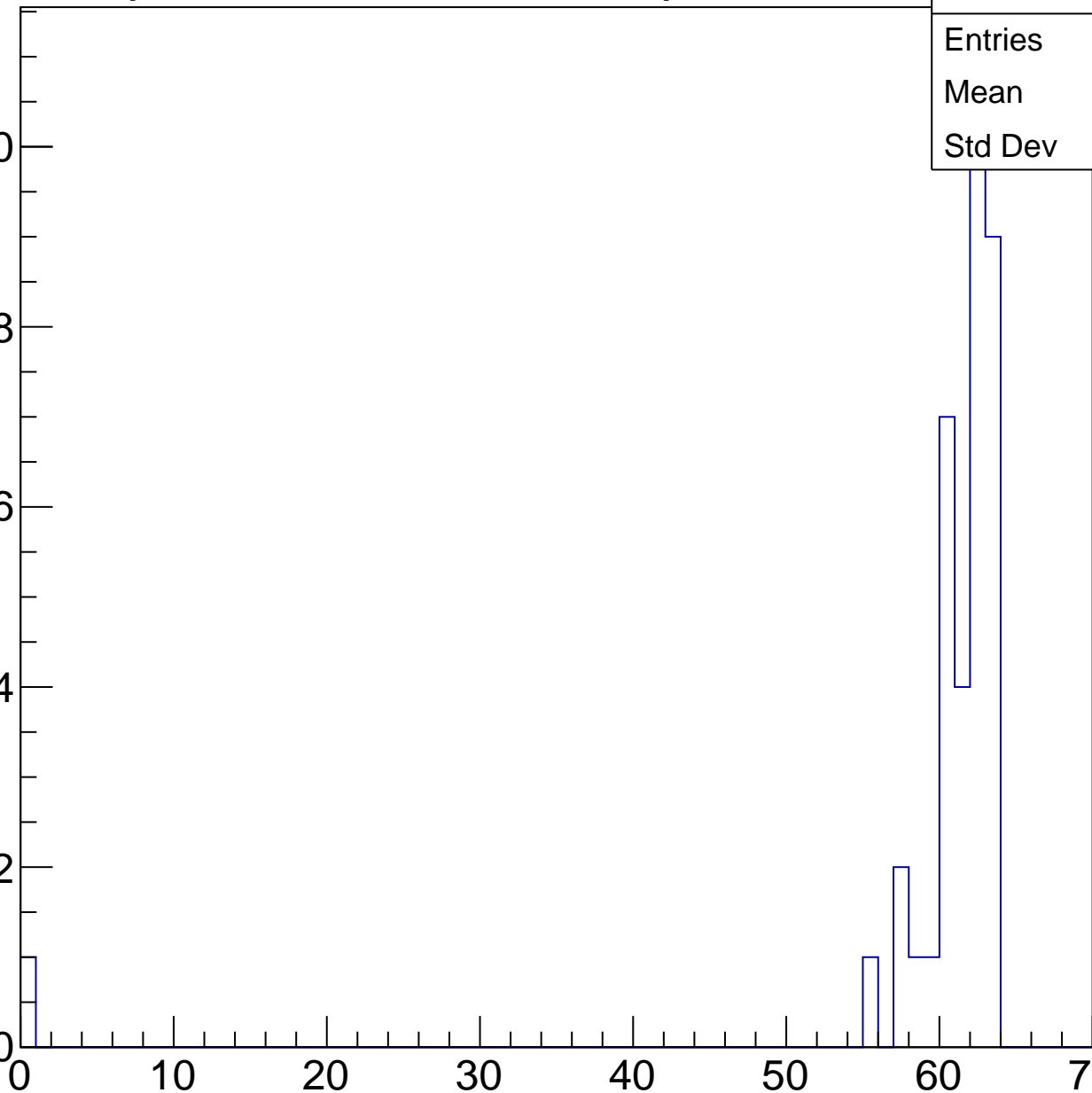
4

2

0

Entries	37
Mean	59.43
Std Dev	10.09

ampl



B0L101S, U19-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch48, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.72
Std Dev	5.165

Gaus mean : 30.7217

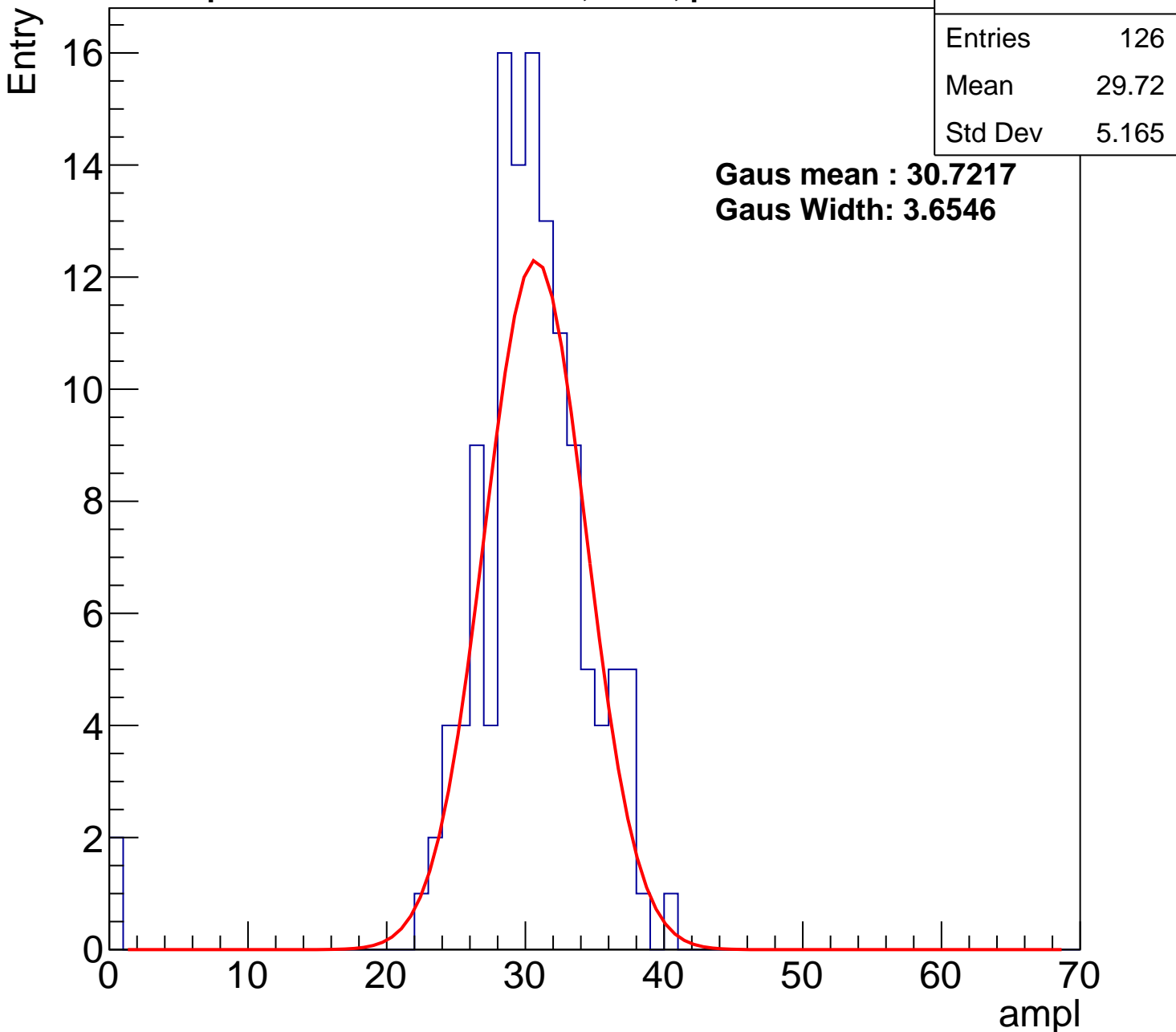
Gaus Width: 3.6546

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



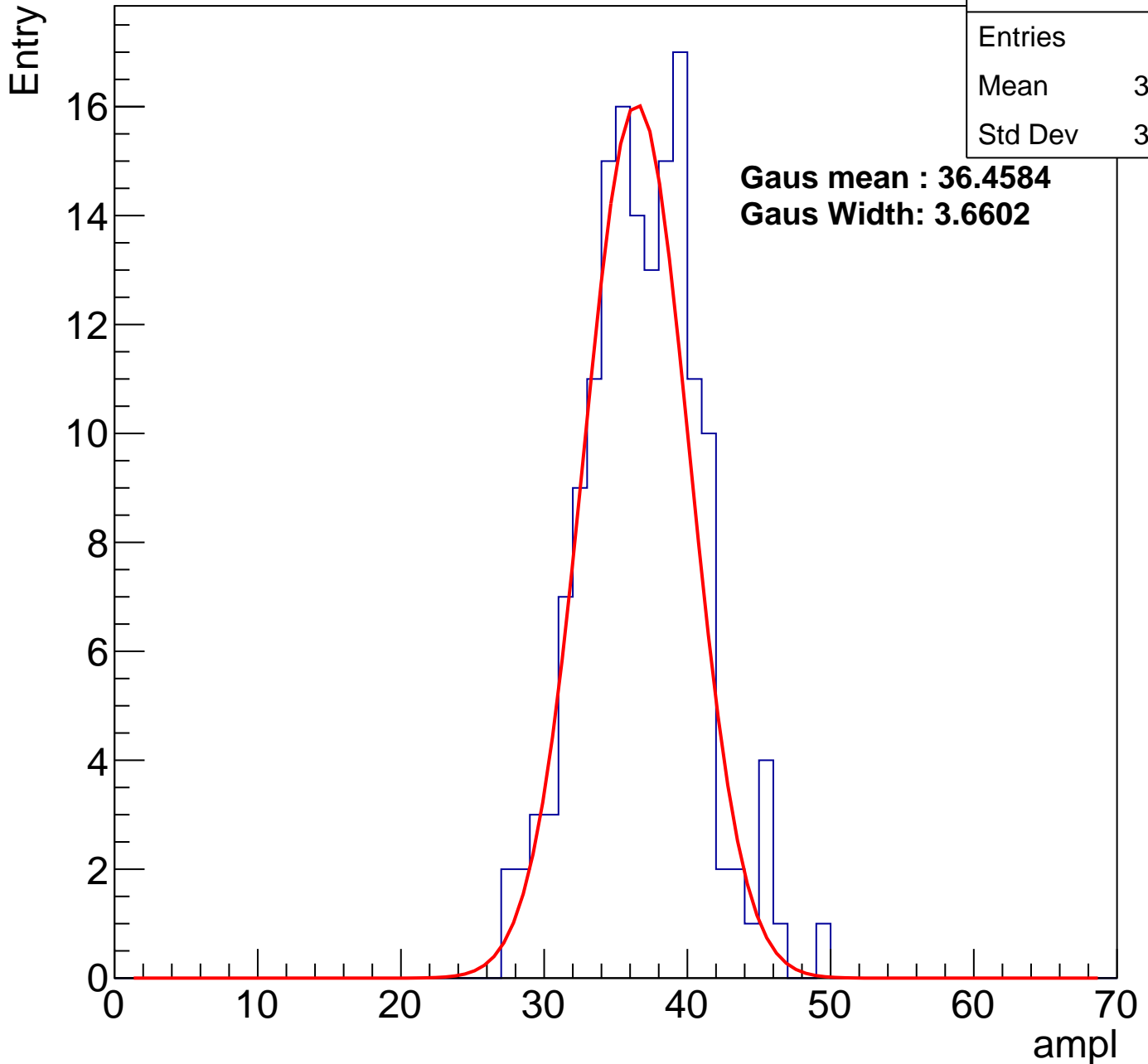
B0L101S, U19-ch48, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	36.36
Std Dev	3.963

Gaus mean : 36.4584

Gaus Width: 3.6602



B0L101S, U19-ch48, adc2

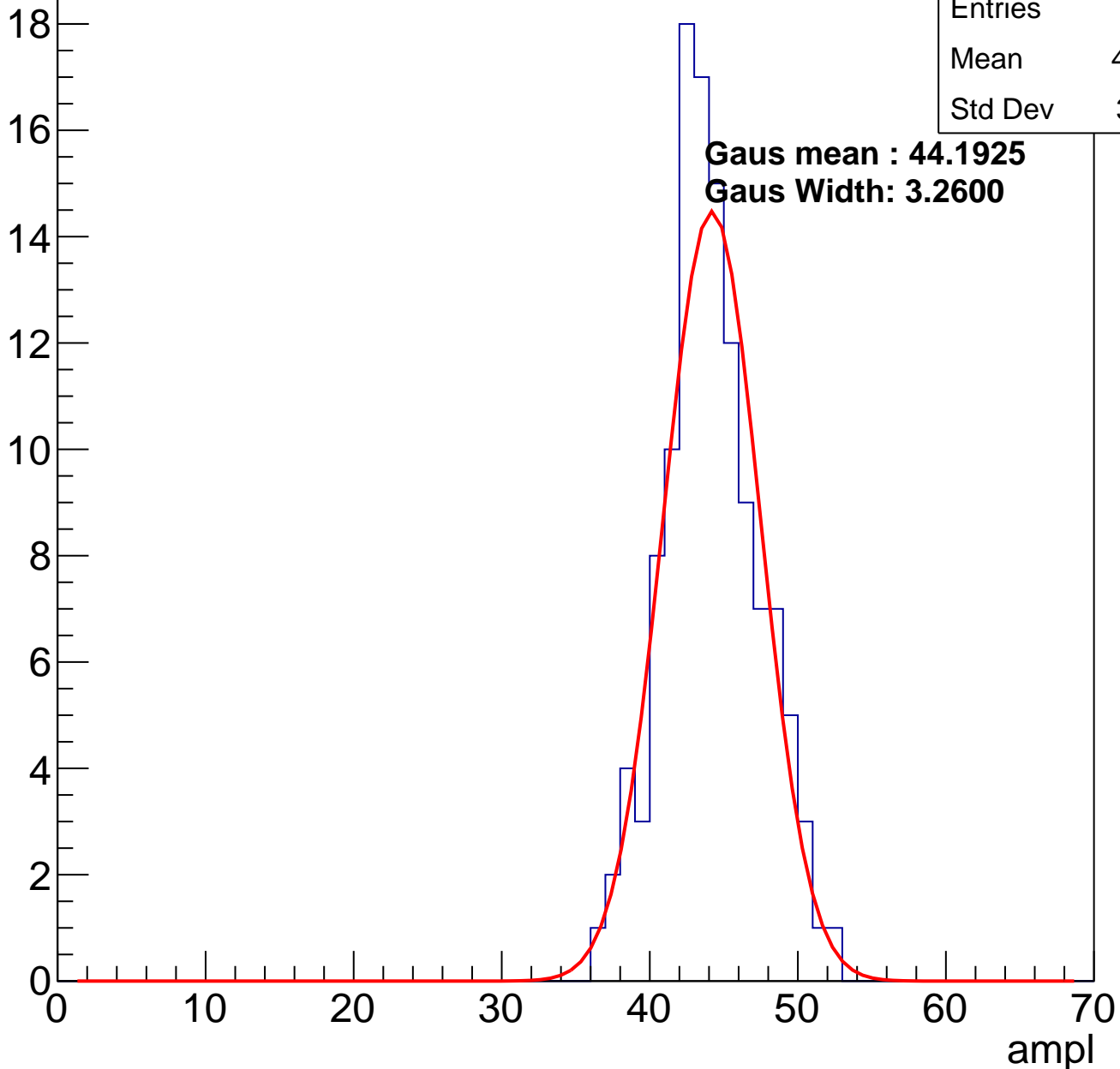
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	43.68
Std Dev	3.181

Gaus mean : 44.1925

Gaus Width: 3.2600

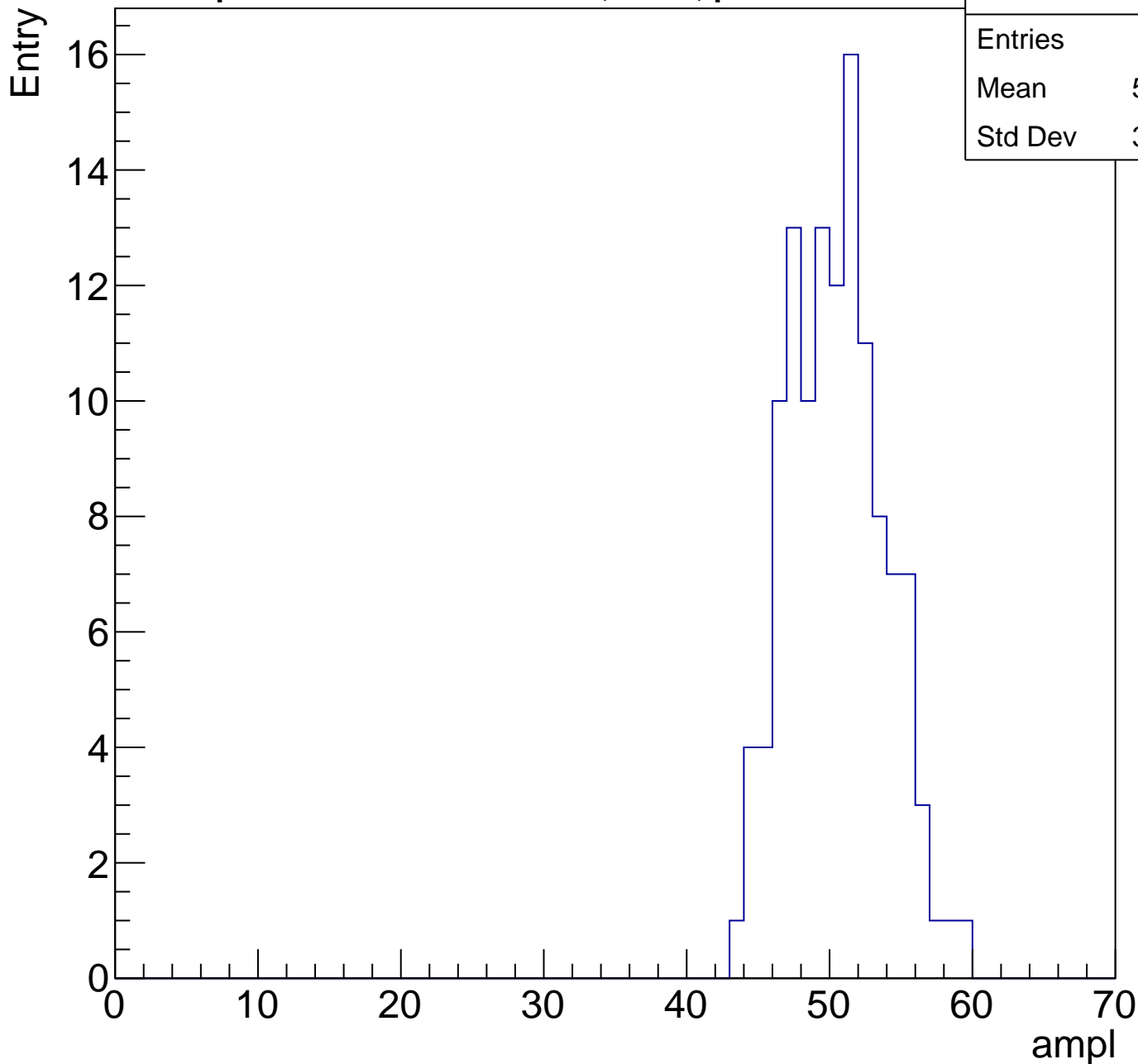
Entry



B0L101S, U19-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	50.03
Std Dev	3.319



B0L101S, U19-ch48, adc4

calib_packv5_042523_0143.root, FC#1, port C1

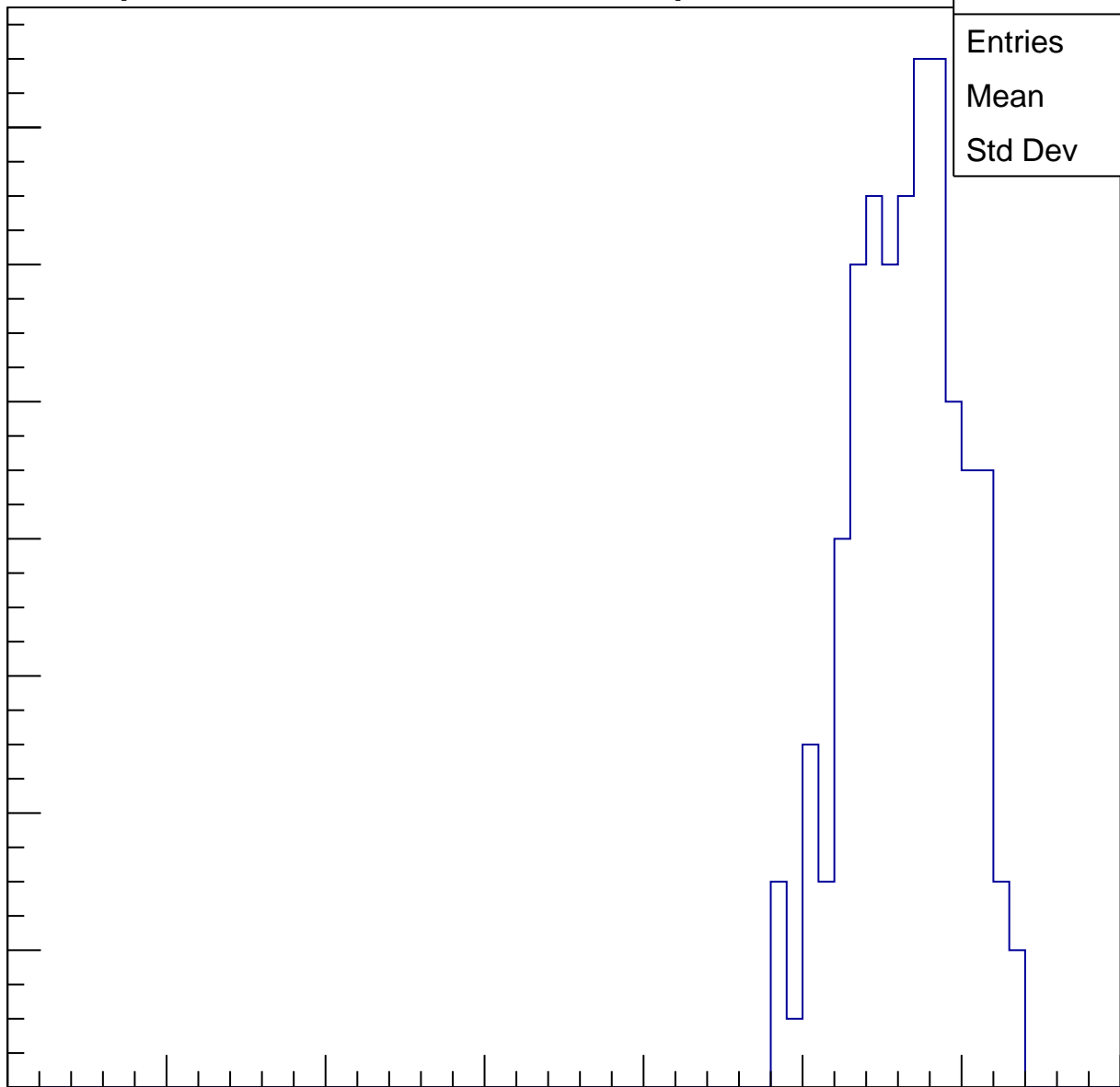
Entries	133
Mean	56.05
Std Dev	3.398

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch48, adc5

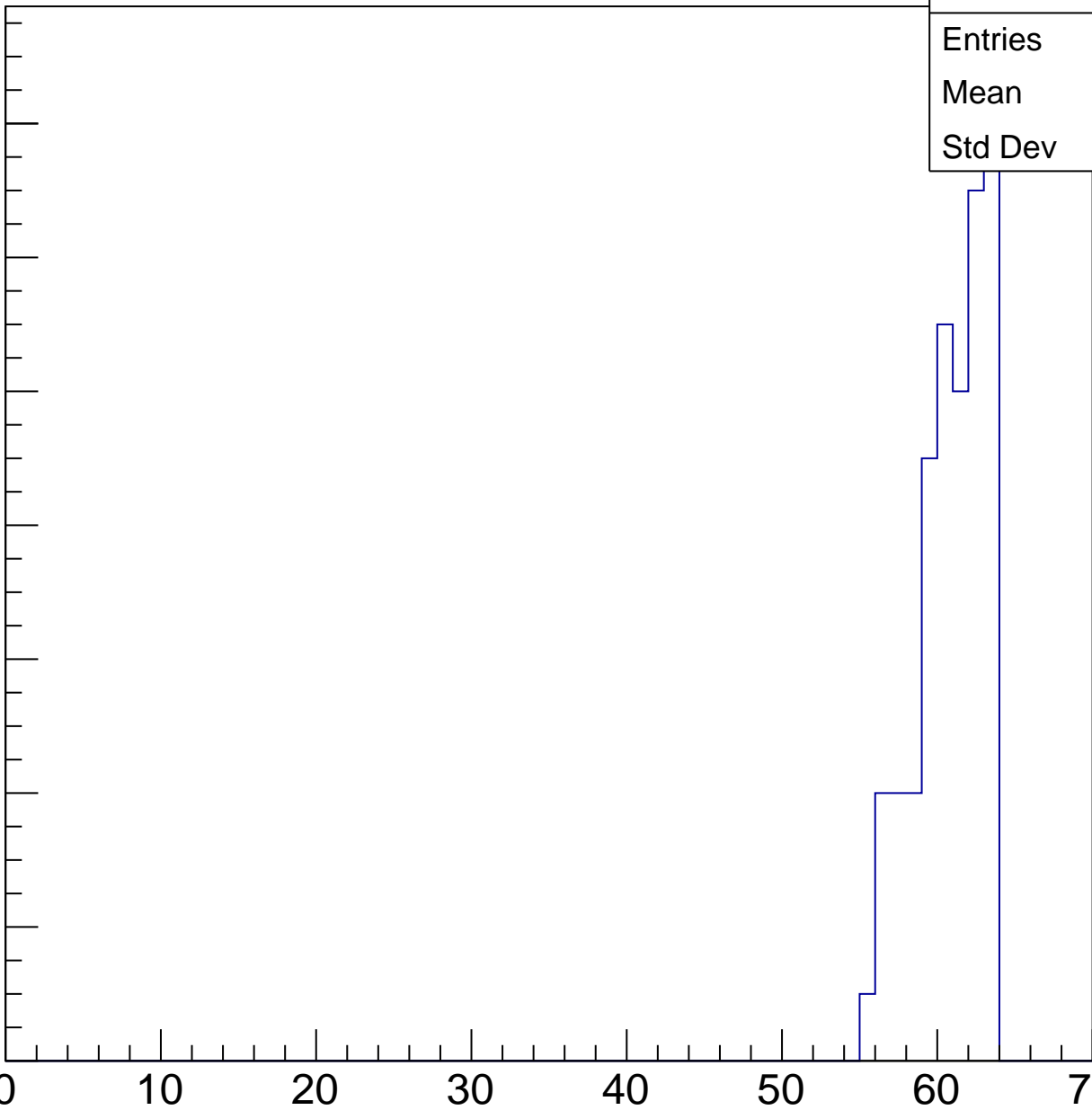
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	60.44
Std Dev	2.167

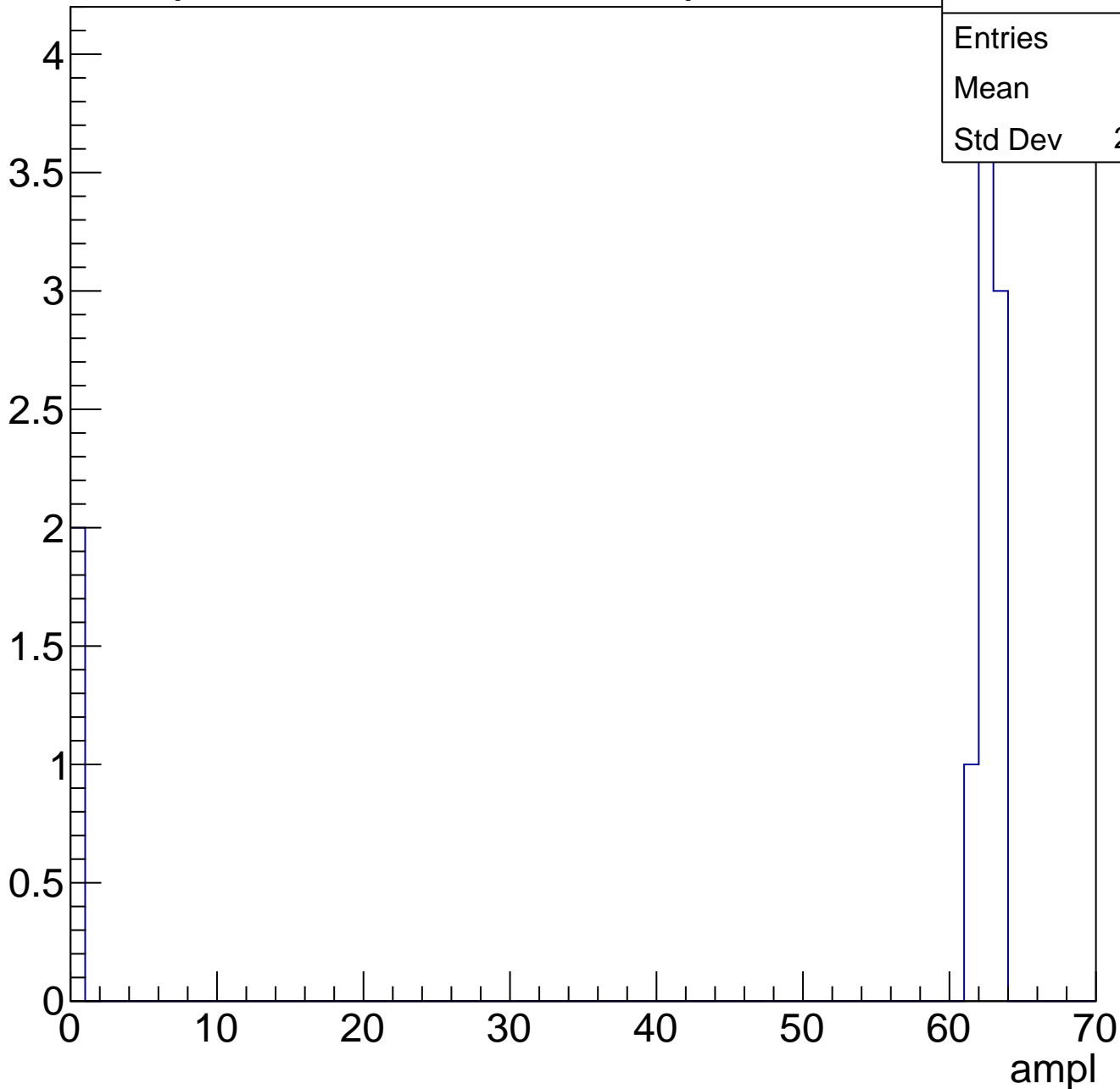
ampl



B0L101S, U19-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L101S, U19-ch49, adc0

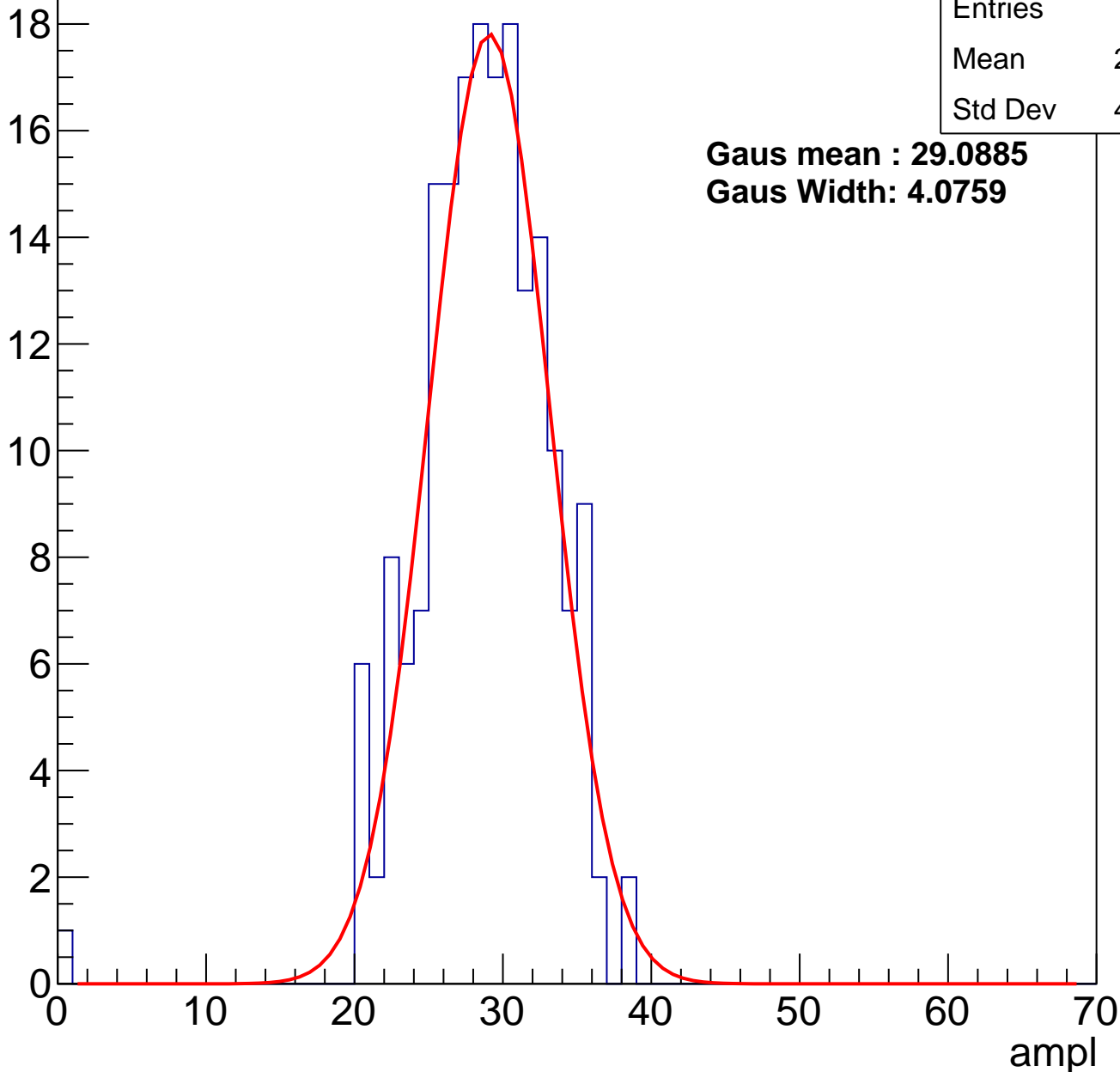
calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	28.27
Std Dev	4.457

Gaus mean : 29.0885

Gaus Width: 4.0759

Entry



B0L101S, U19-ch49, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	36.36
Std Dev	3.476

Gaus mean : 37.0200

Gaus Width: 3.7257

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

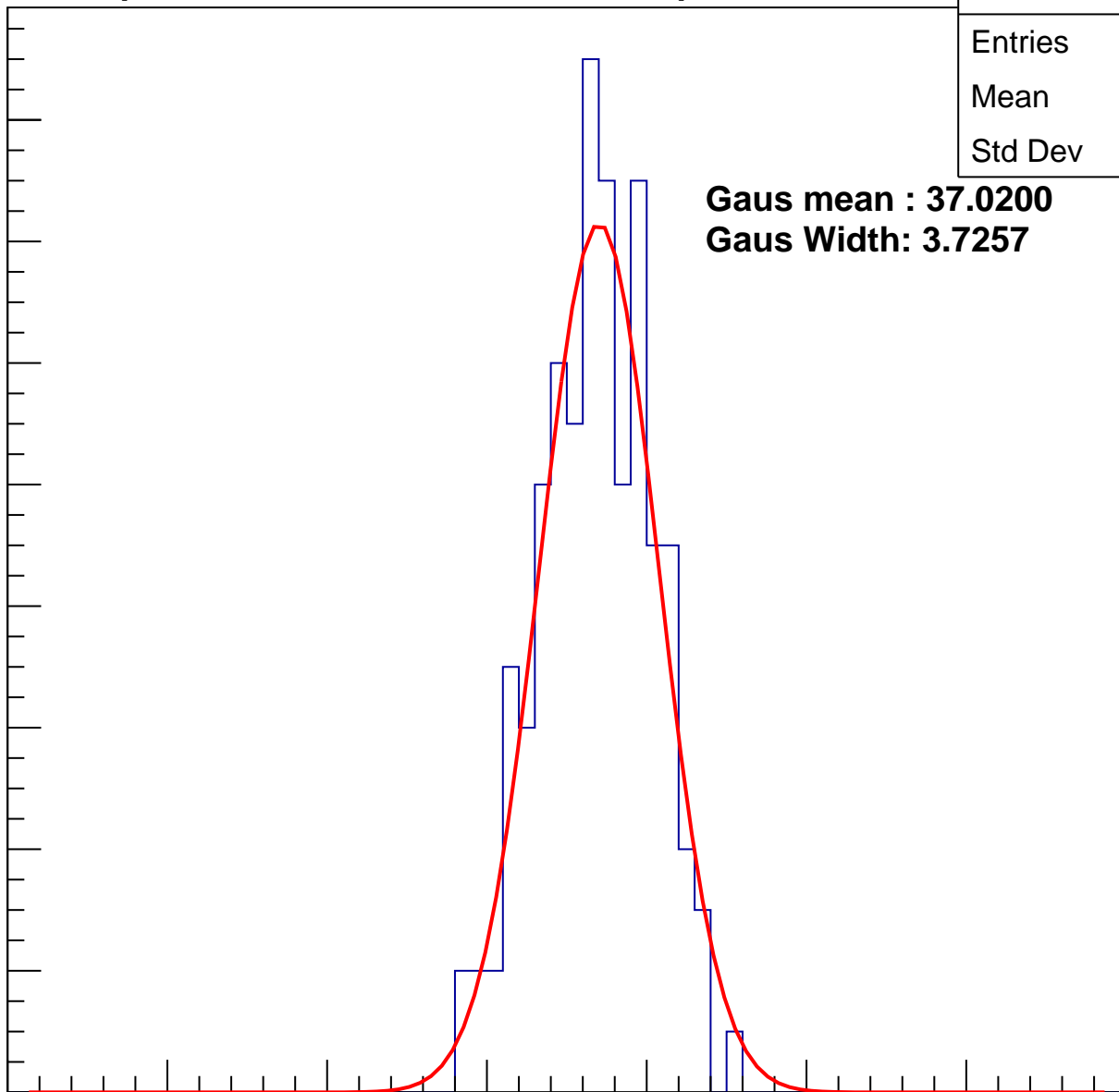
40

50

60

70

ampl



B0L101S, U19-ch49, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

149

Mean

43.38

Std Dev

3.363

Gaus mean : 43.9486

Gaus Width: 3.3727

0

10

20

30

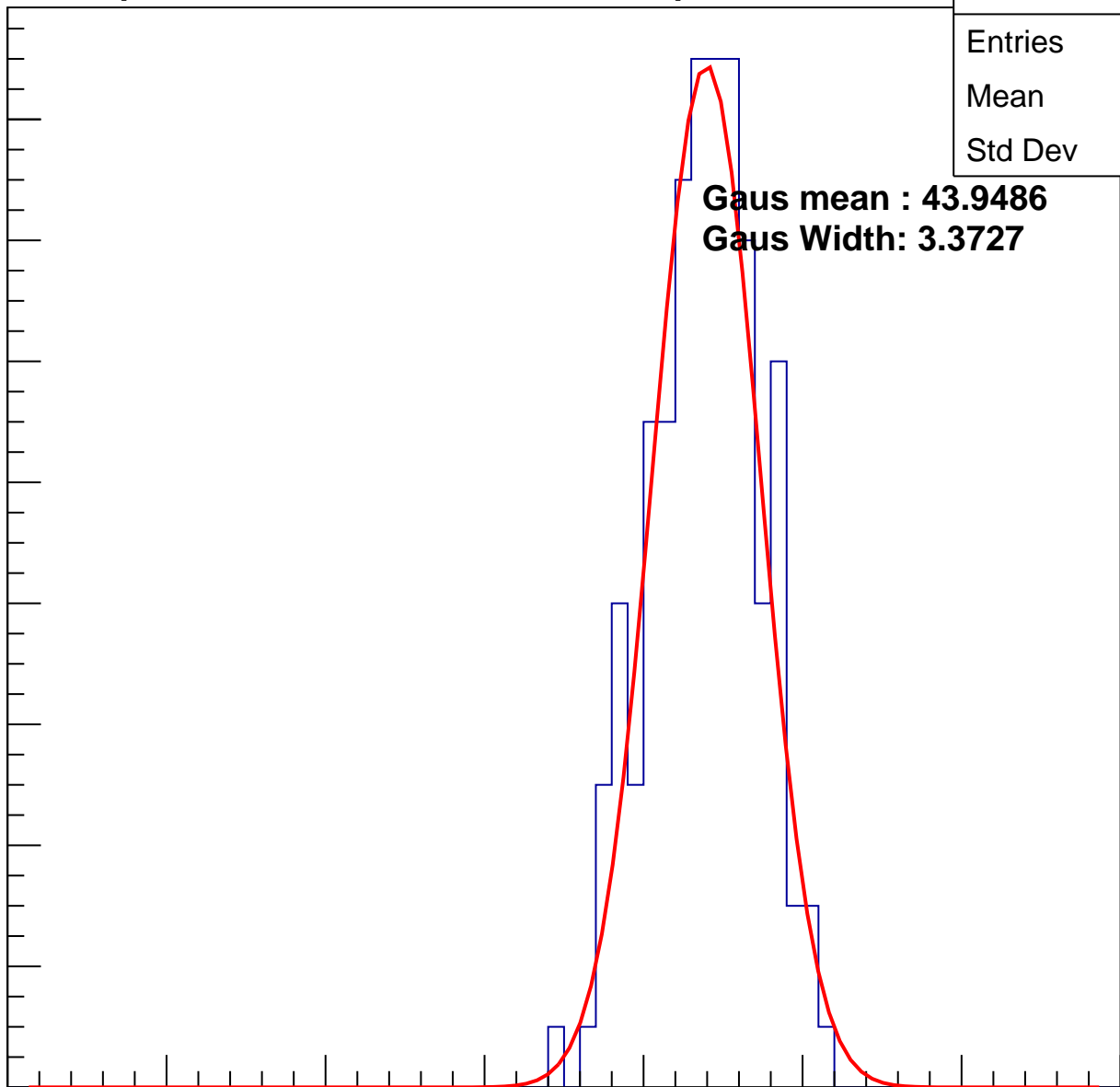
40

50

60

70

ampl



B0L101S, U19-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

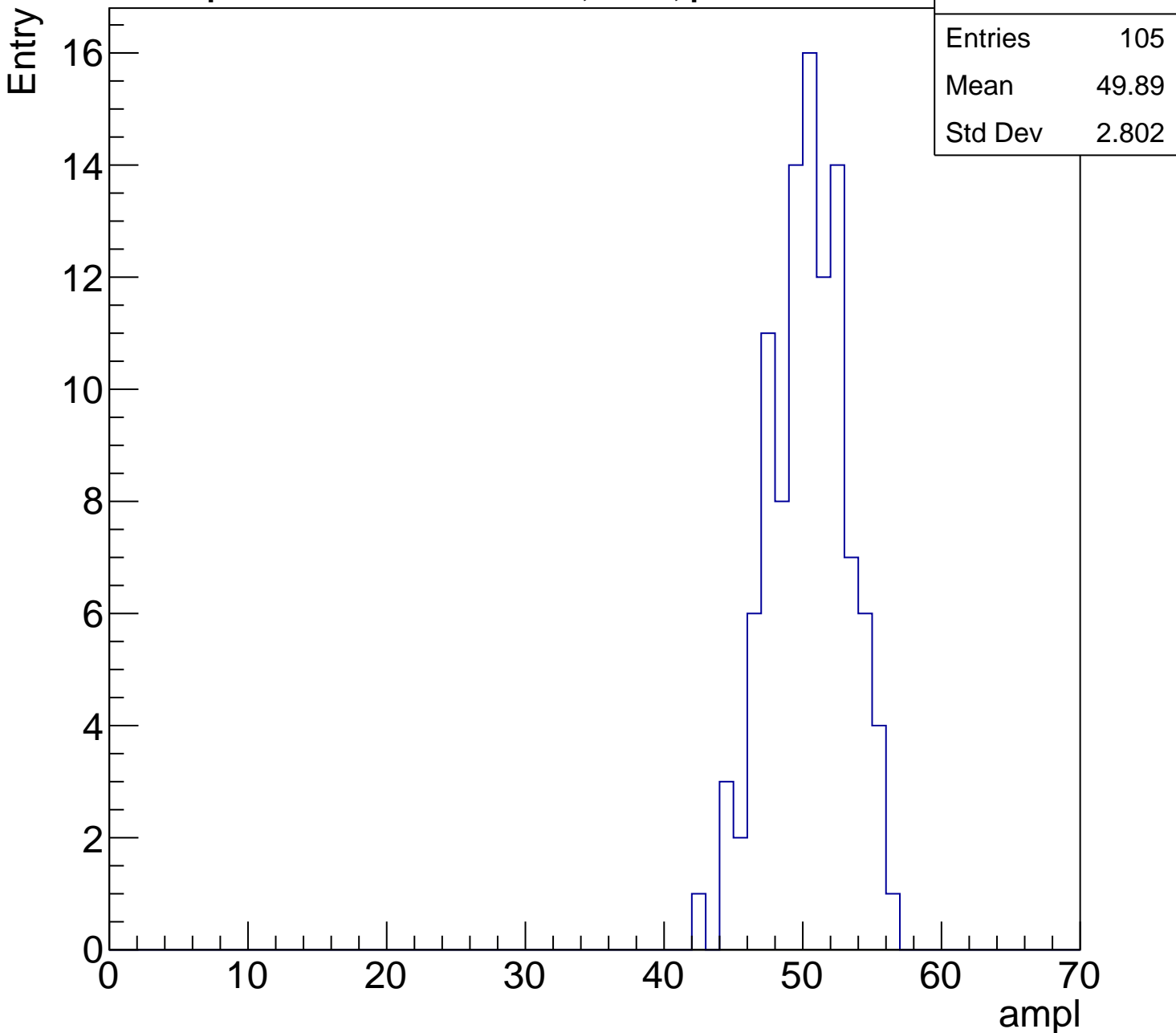
Entries	105
Mean	49.89
Std Dev	2.802

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

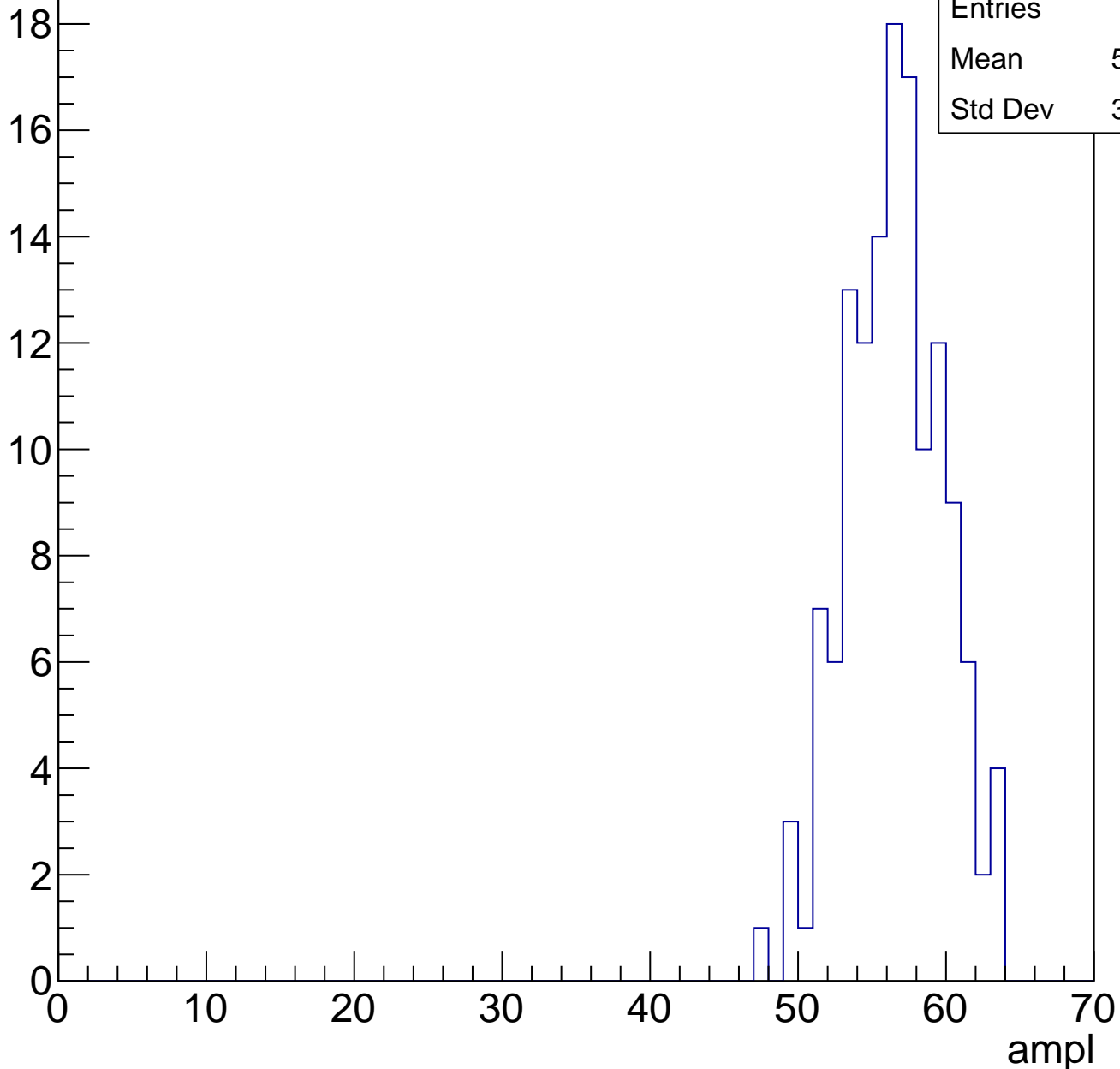


B0L101S, U19-ch49, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	56.05
Std Dev	3.267

Entry



B0L101S, U19-ch49, adc5

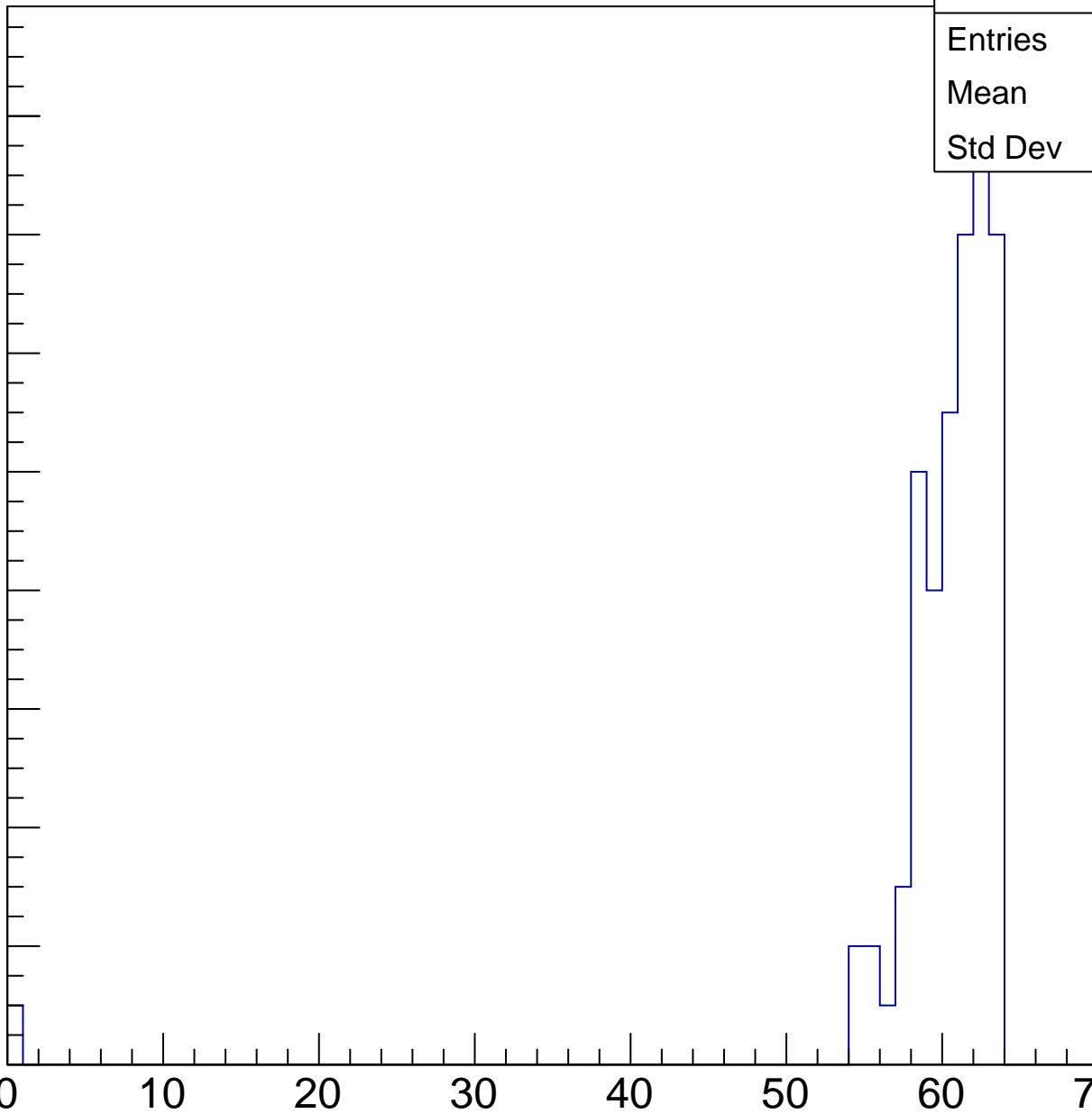
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.6
Std Dev	6.949

ampl



B0L101S, U19-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch50, adc0

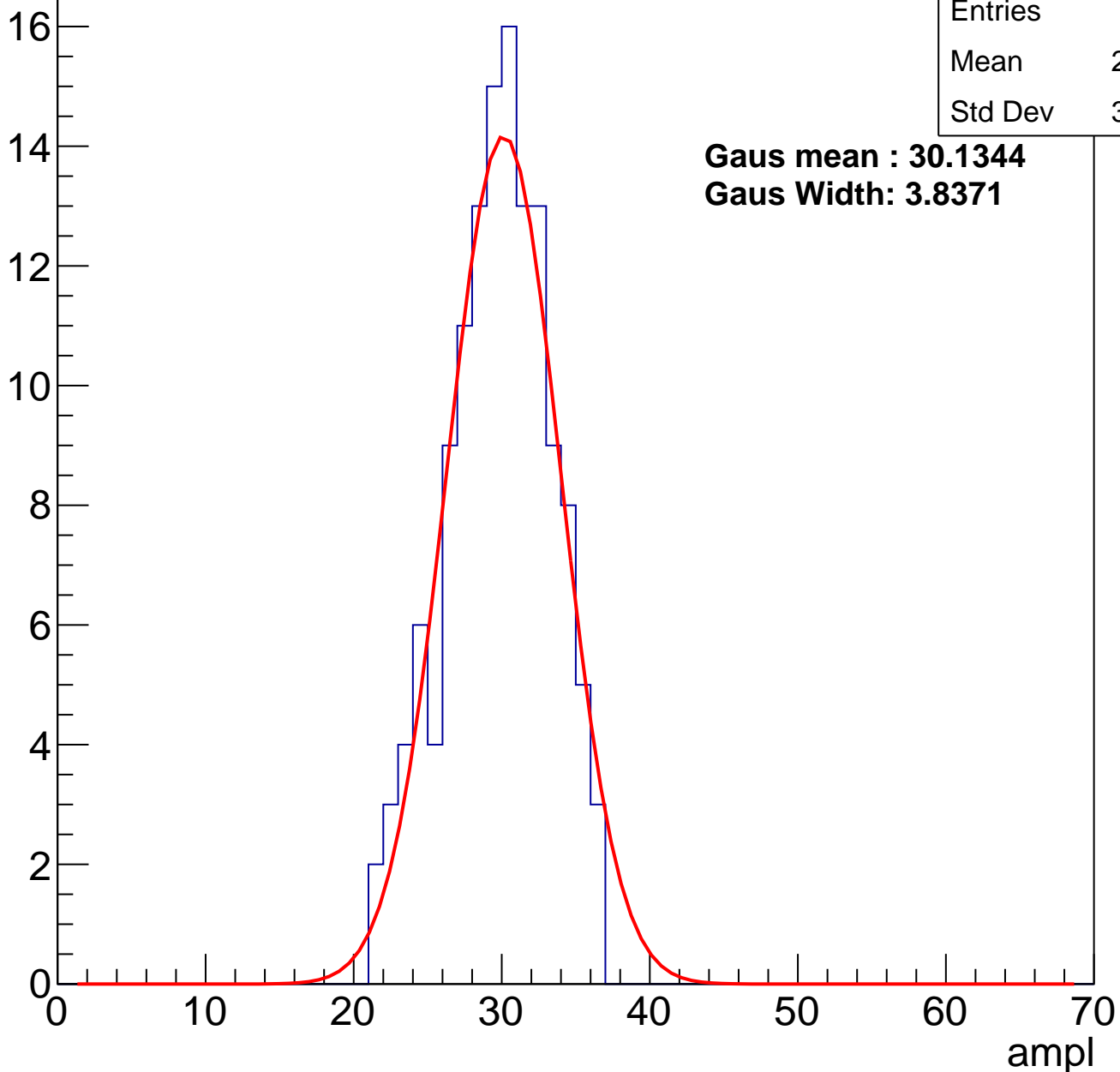
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	29.29
Std Dev	3.472

Gaus mean : 30.1344

Gaus Width: 3.8371

Entry



B0L101S, U19-ch50, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	36.46
Std Dev	4.64

Gaus mean : 37.1030

Gaus Width: 3.5505

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

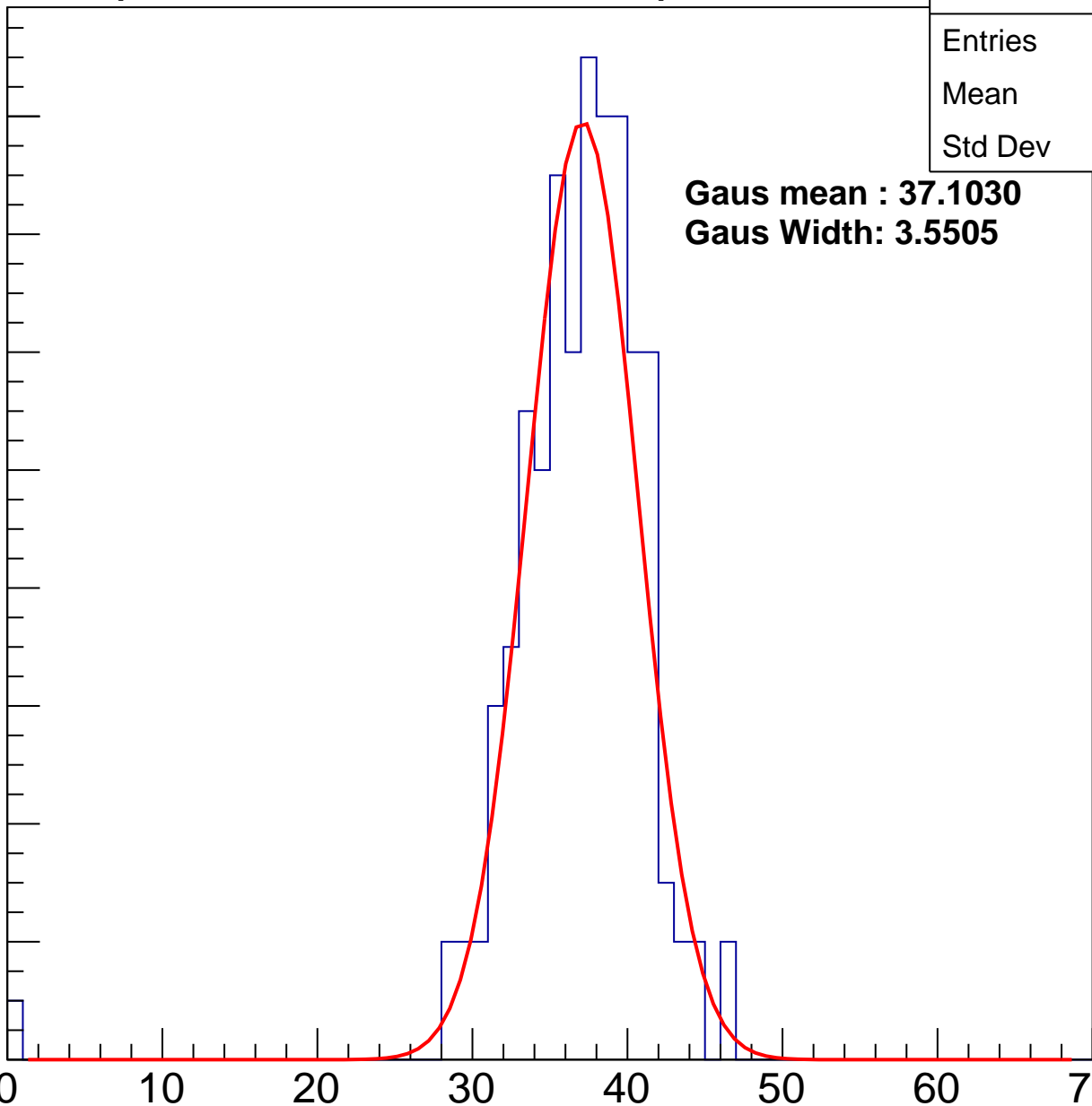
40

50

60

70

ampl



B0L101S, U19-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

105

Mean

42.89

Std Dev

3.605

Gaus mean : 43.4010

Gaus Width: 3.4317

0

10

20

30

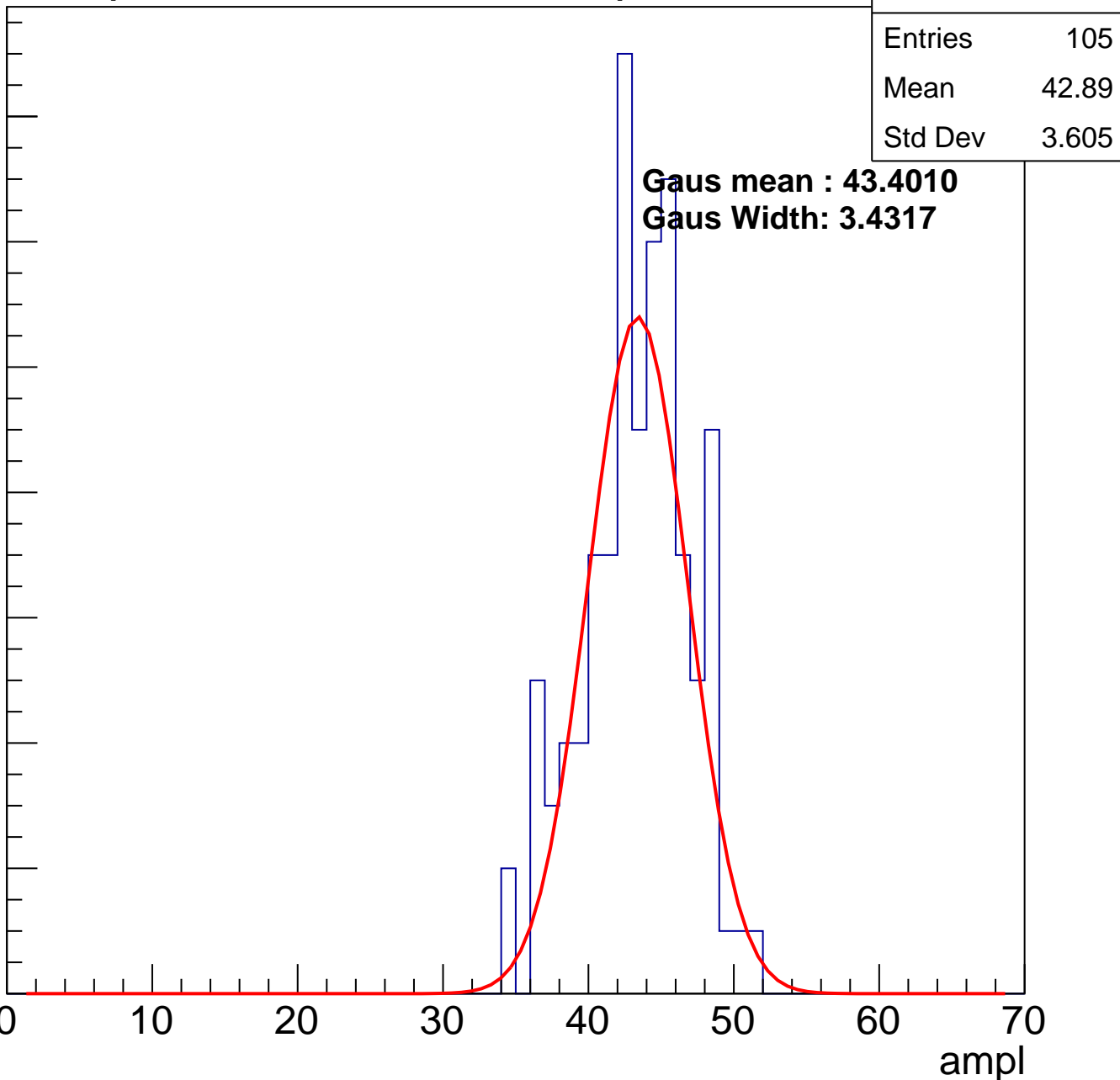
40

50

60

70

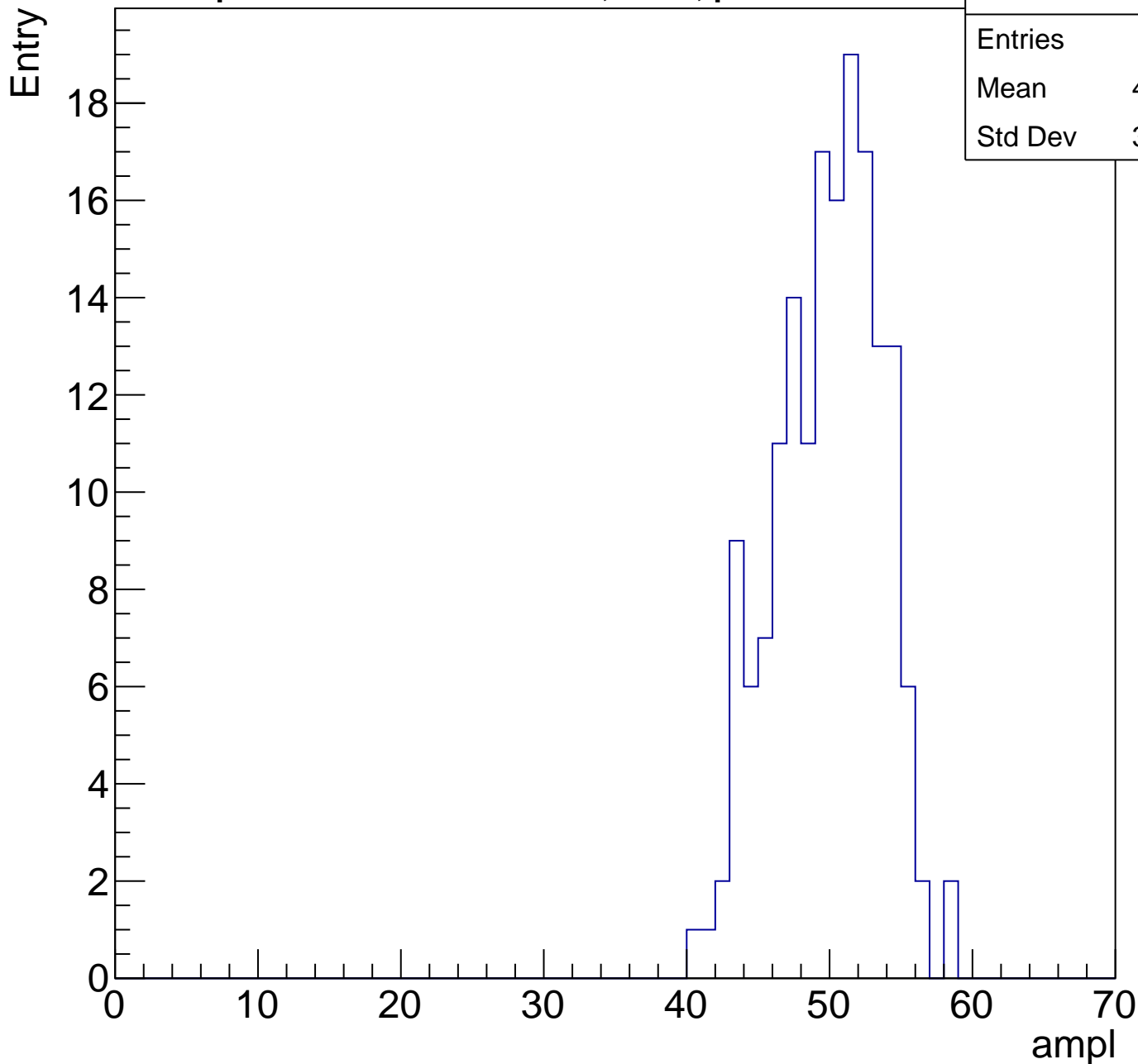
ampl



B0L101S, U19-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	49.45
Std Dev	3.642



B0L101S, U19-ch50, adc4

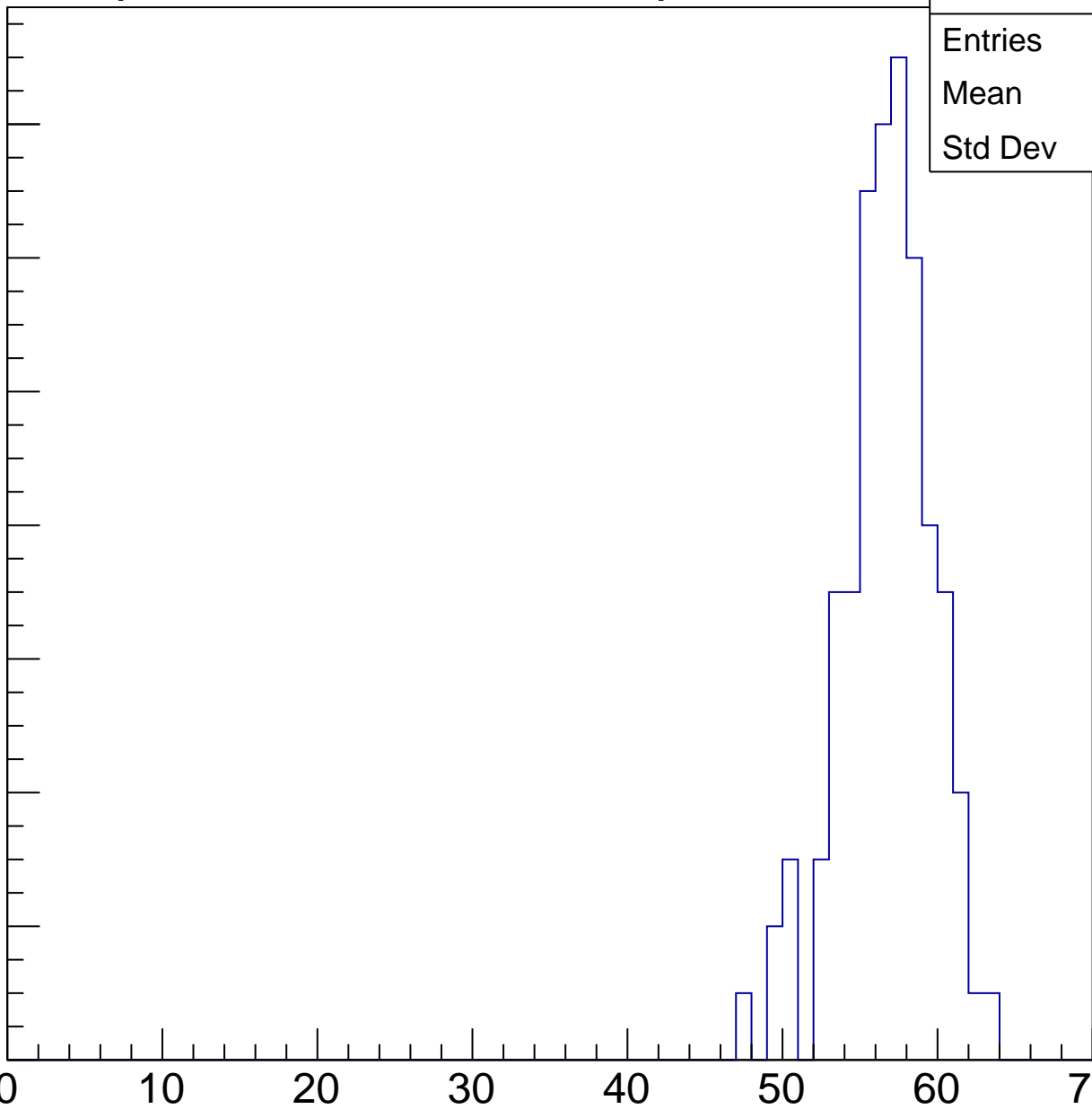
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	56.23
Std Dev	2.972

ampl



B0L101S, U19-ch50, adc5

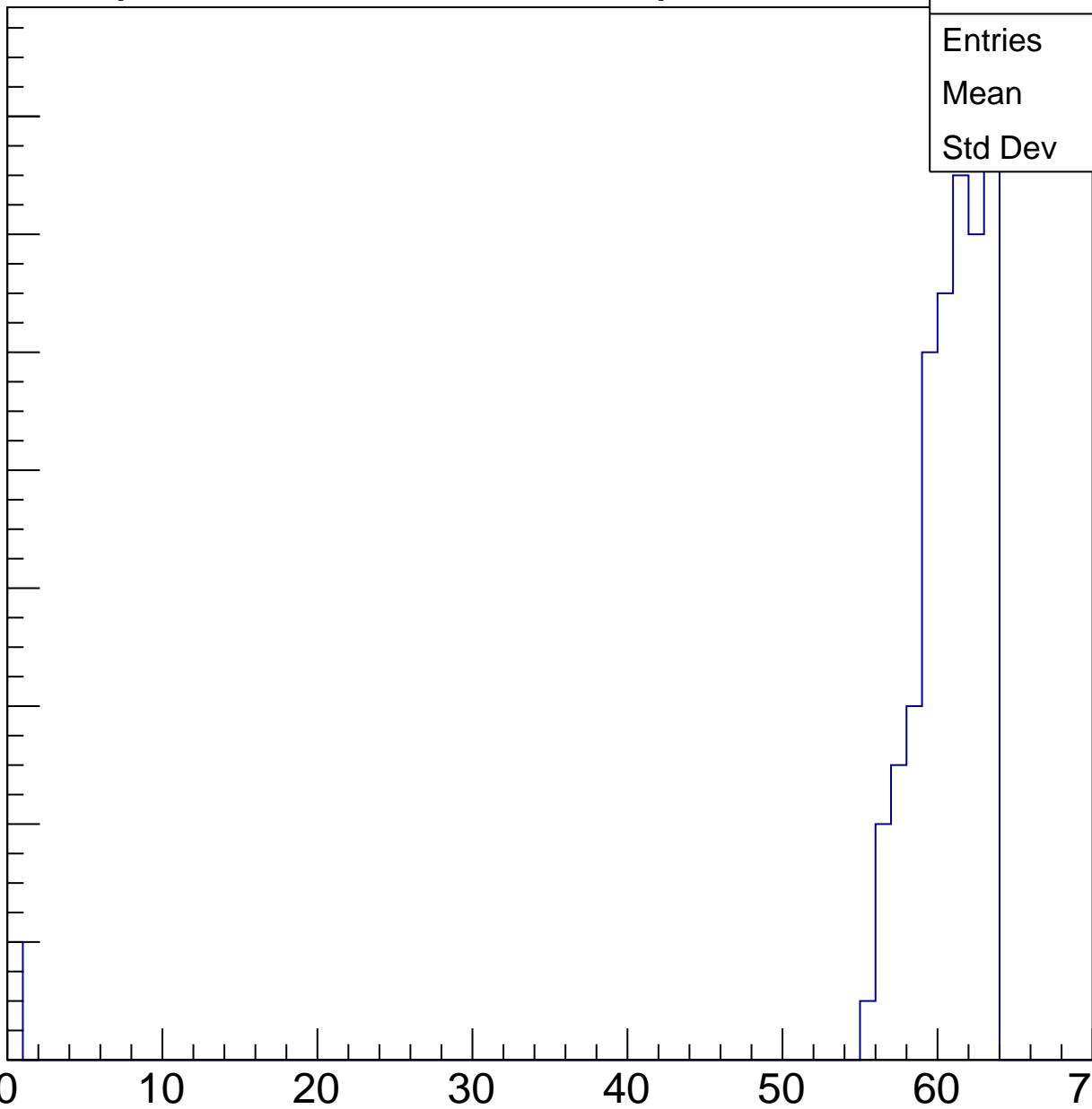
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.03
Std Dev	9.187

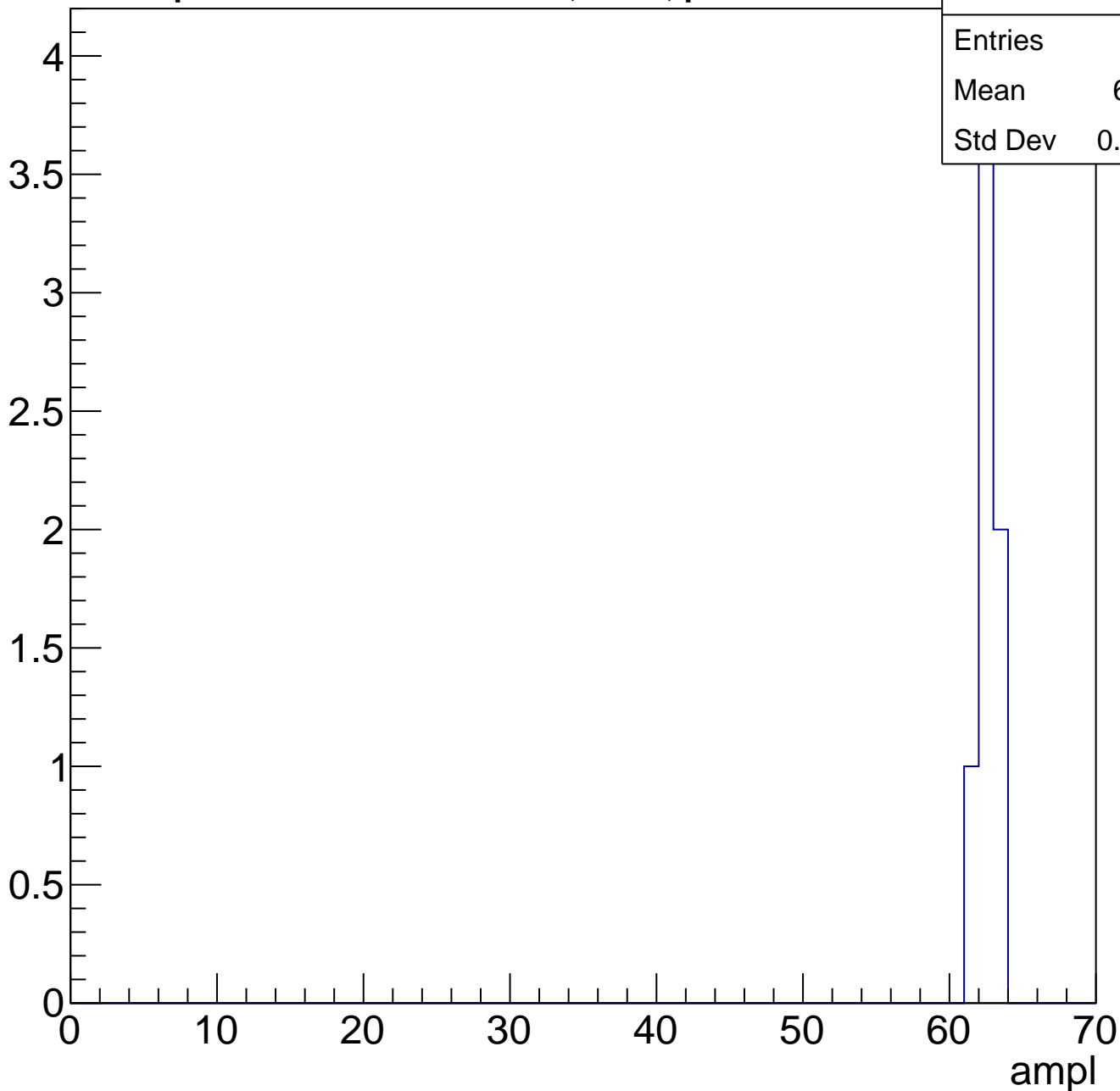
ampl



B0L101S, U19-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

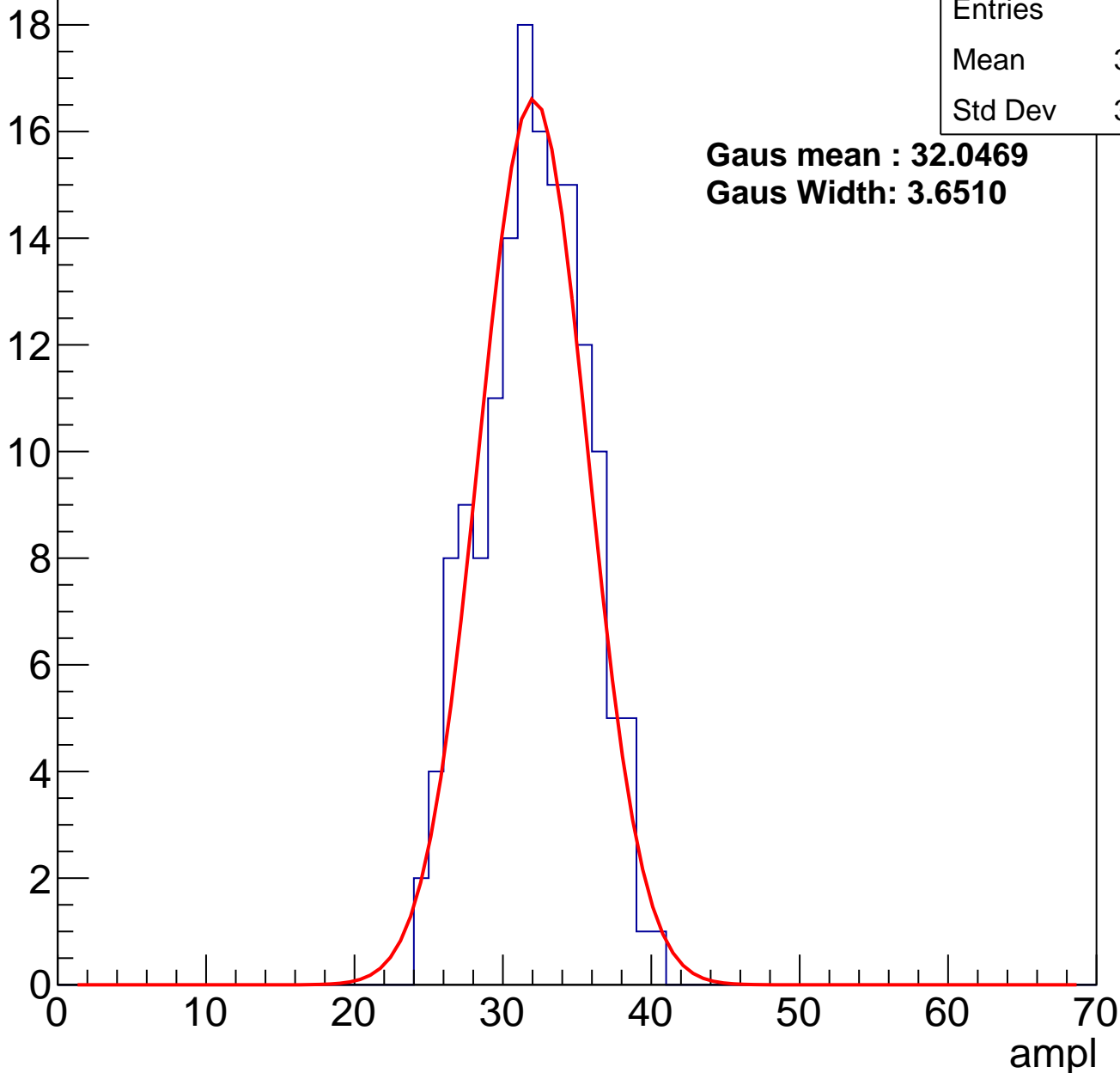
Entry



B0L101S, U19-ch51, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



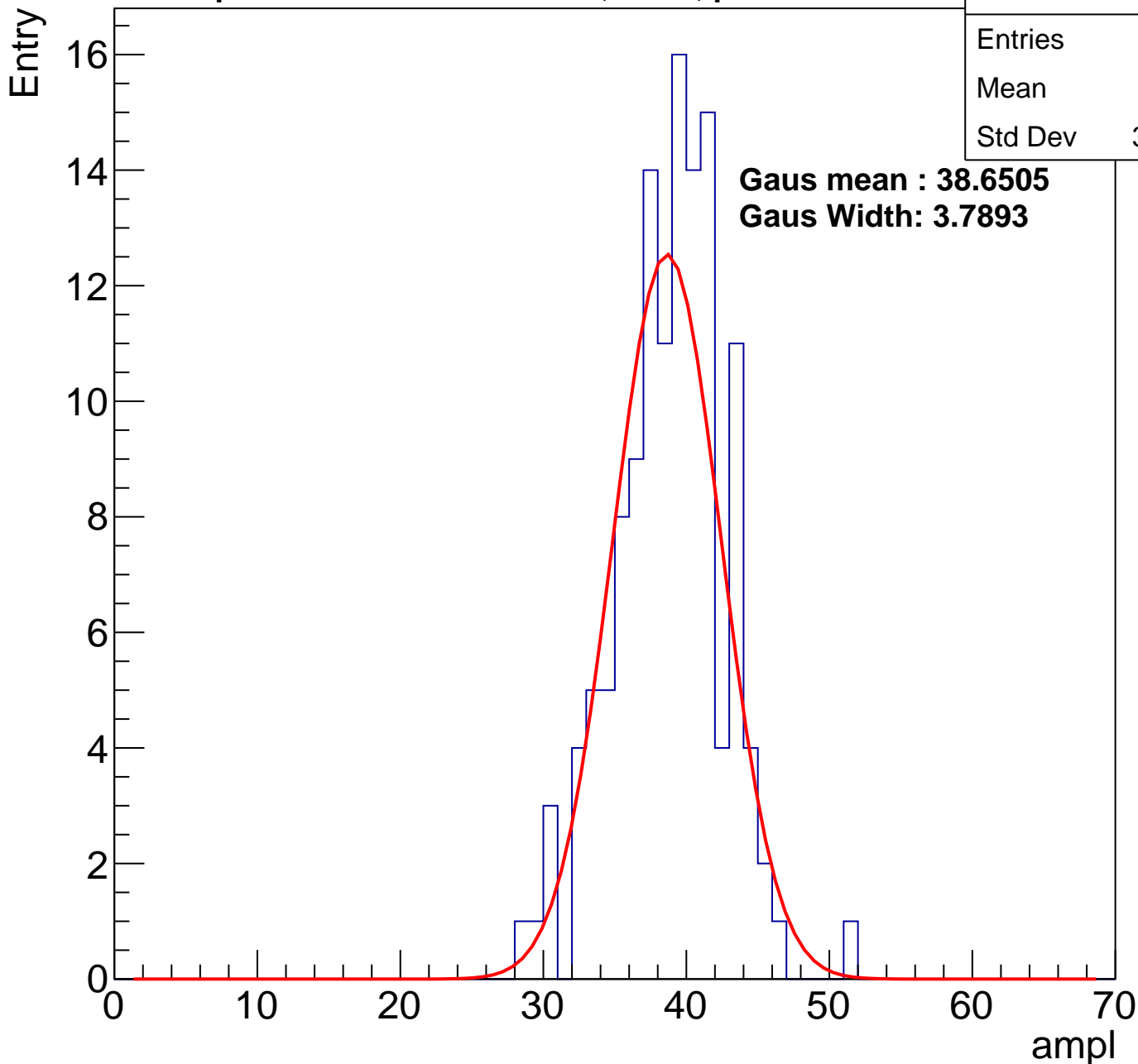
B0L101S, U19-ch51, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	38.4
Std Dev	3.782

Gaus mean : 38.6505

Gaus Width: 3.7893



B0L101S, U19-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

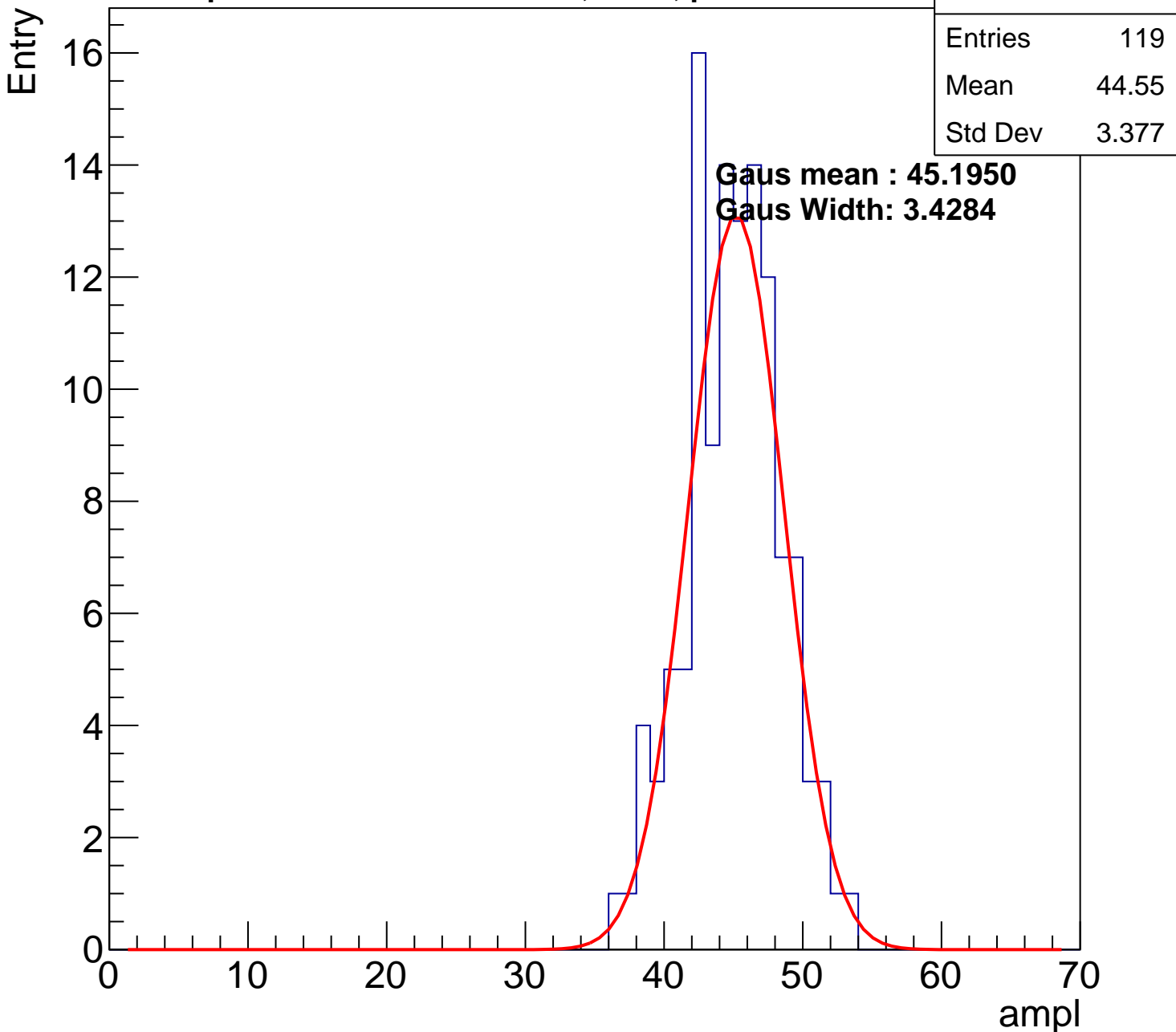
Entries	119
Mean	44.55
Std Dev	3.377

Gaus mean : 45.1950

Gaus Width: 3.4284

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

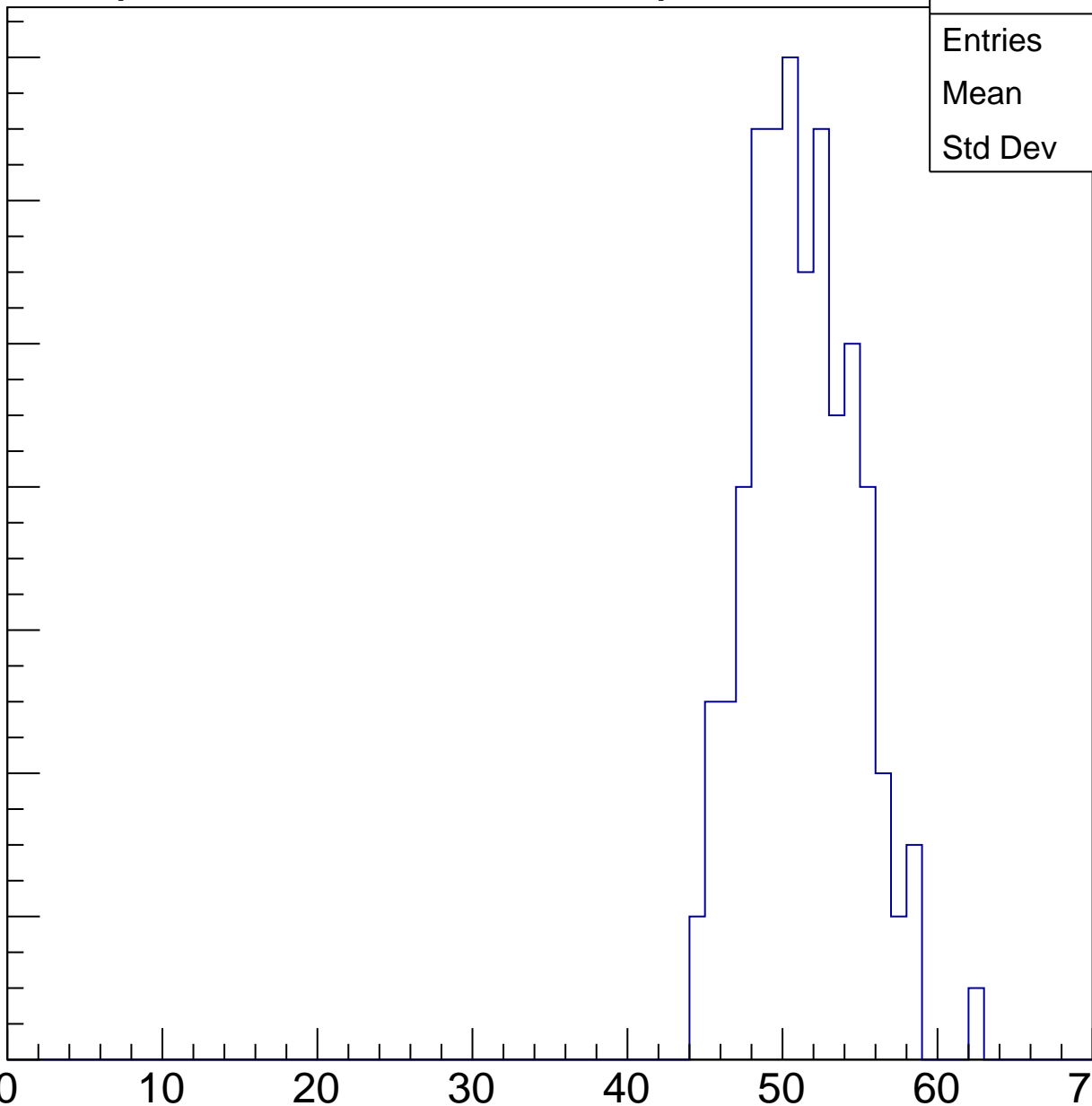
Entries	121
Mean	50.81
Std Dev	3.422

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

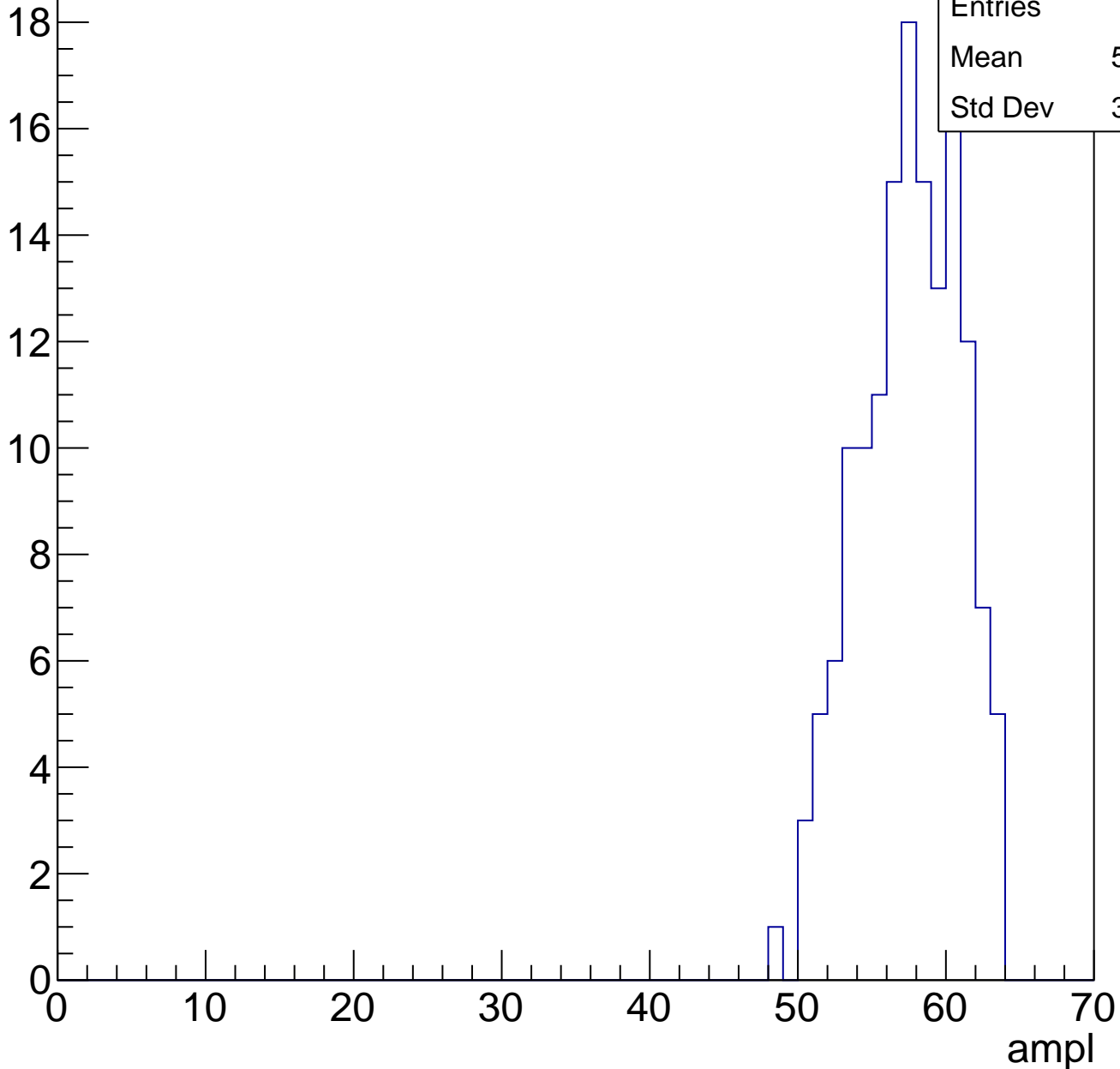
ampl



B0L101S, U19-ch51, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch51, adc5

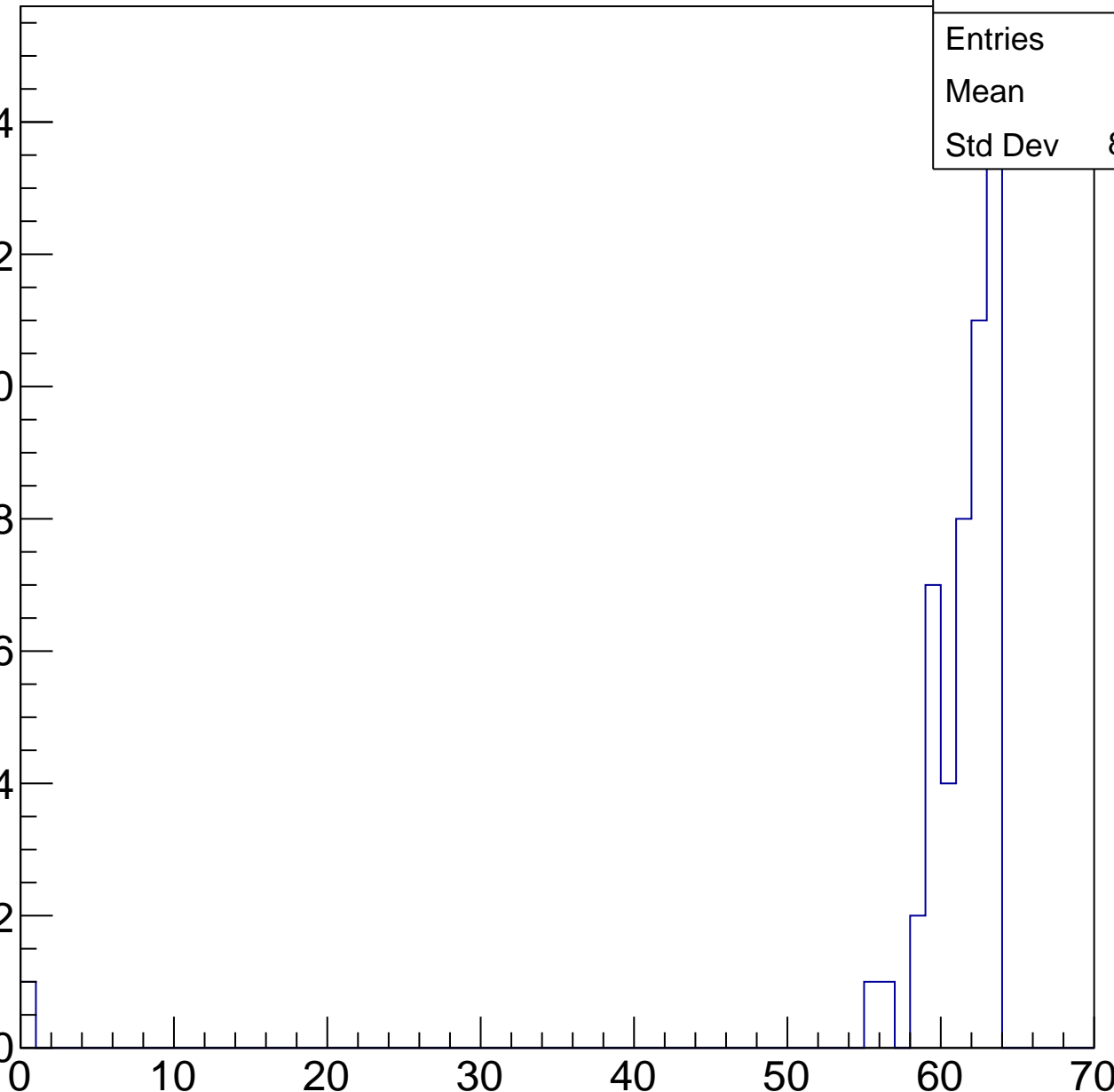
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	50
Mean	59.9
Std Dev	8.766

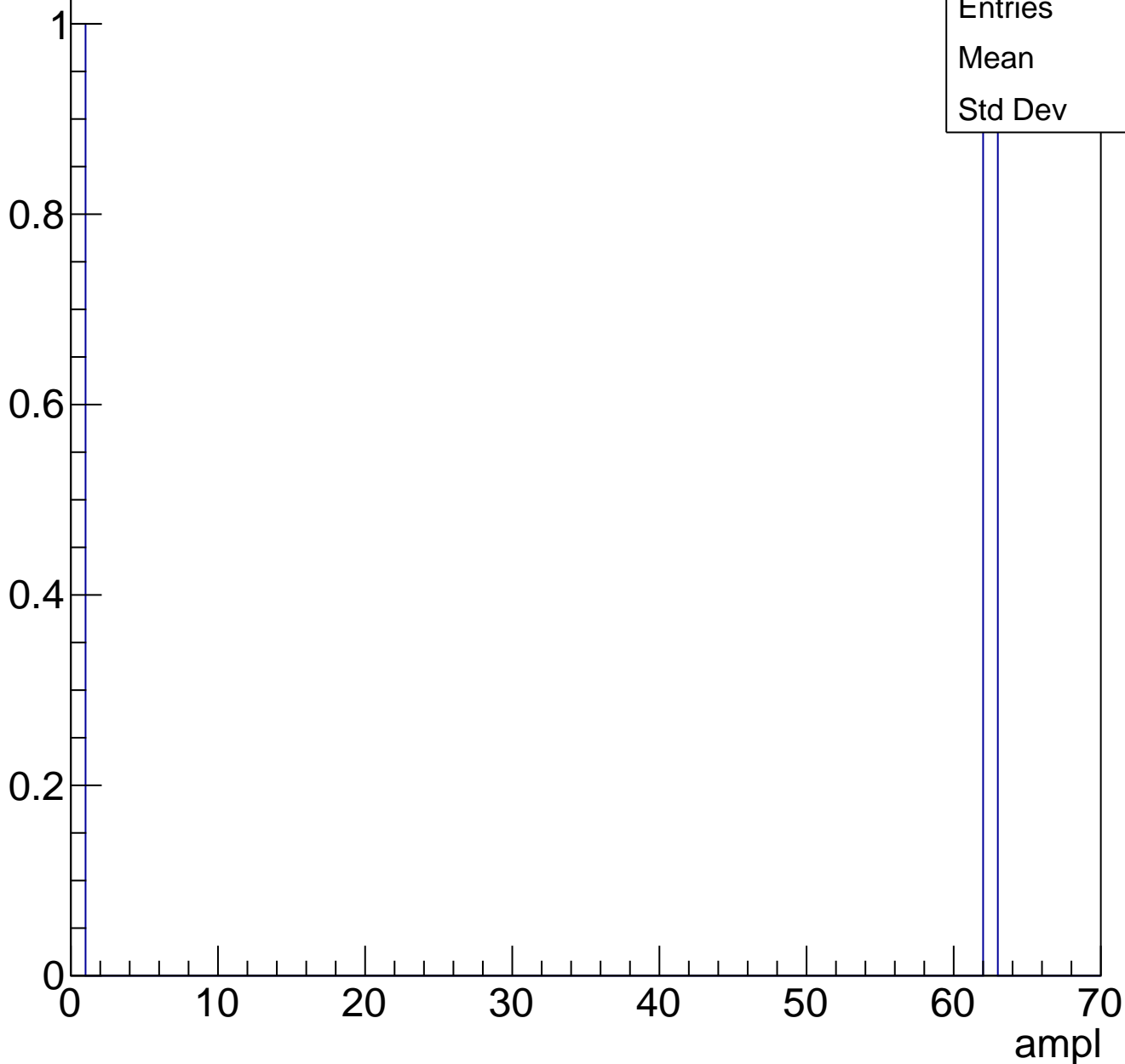
ampl



B0L101S, U19-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch52, adc0

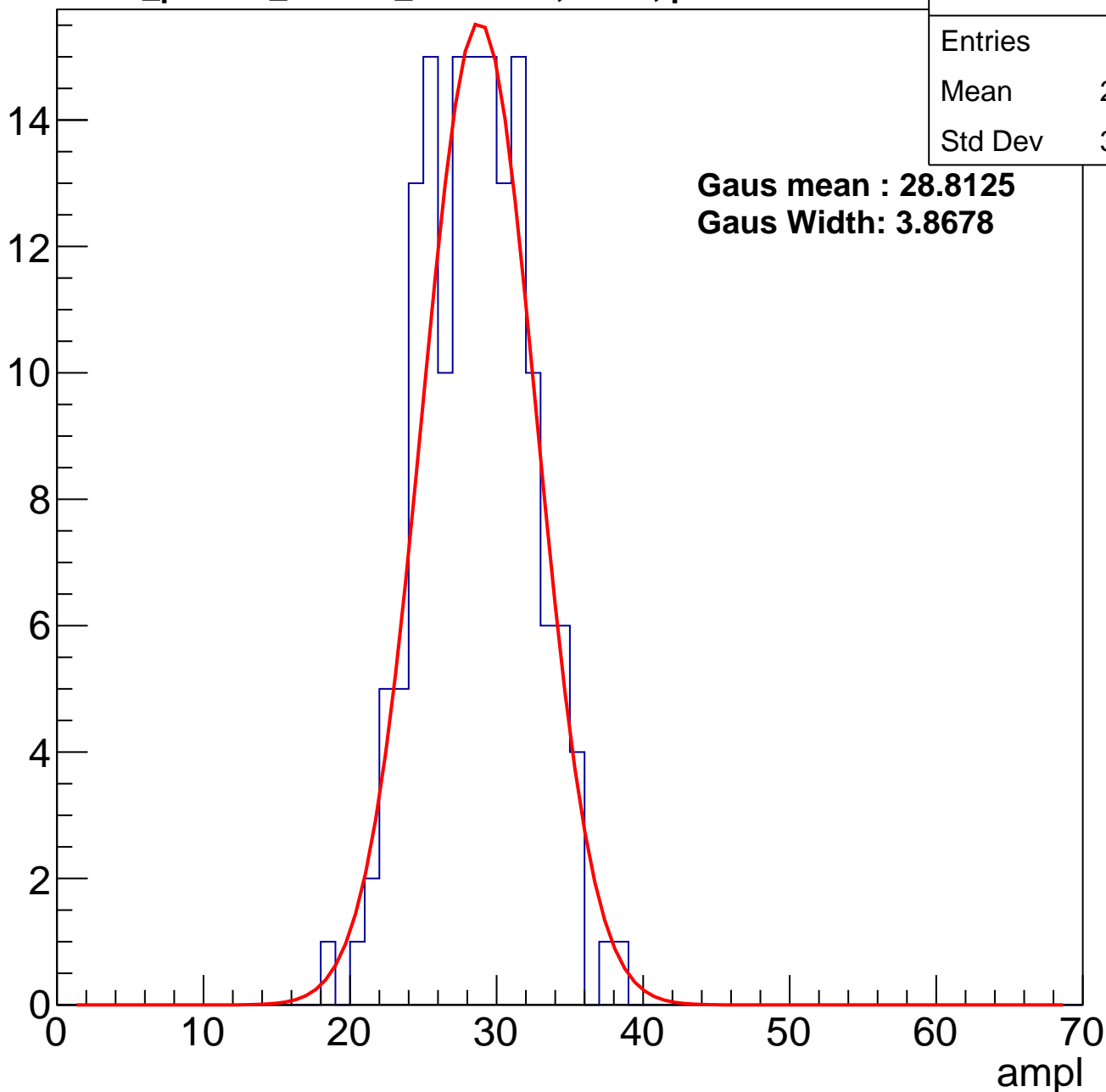
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.13
Std Dev	3.676

Gaus mean : 28.8125

Gaus Width: 3.8678

Entry



B0L101S, U19-ch52, adc1

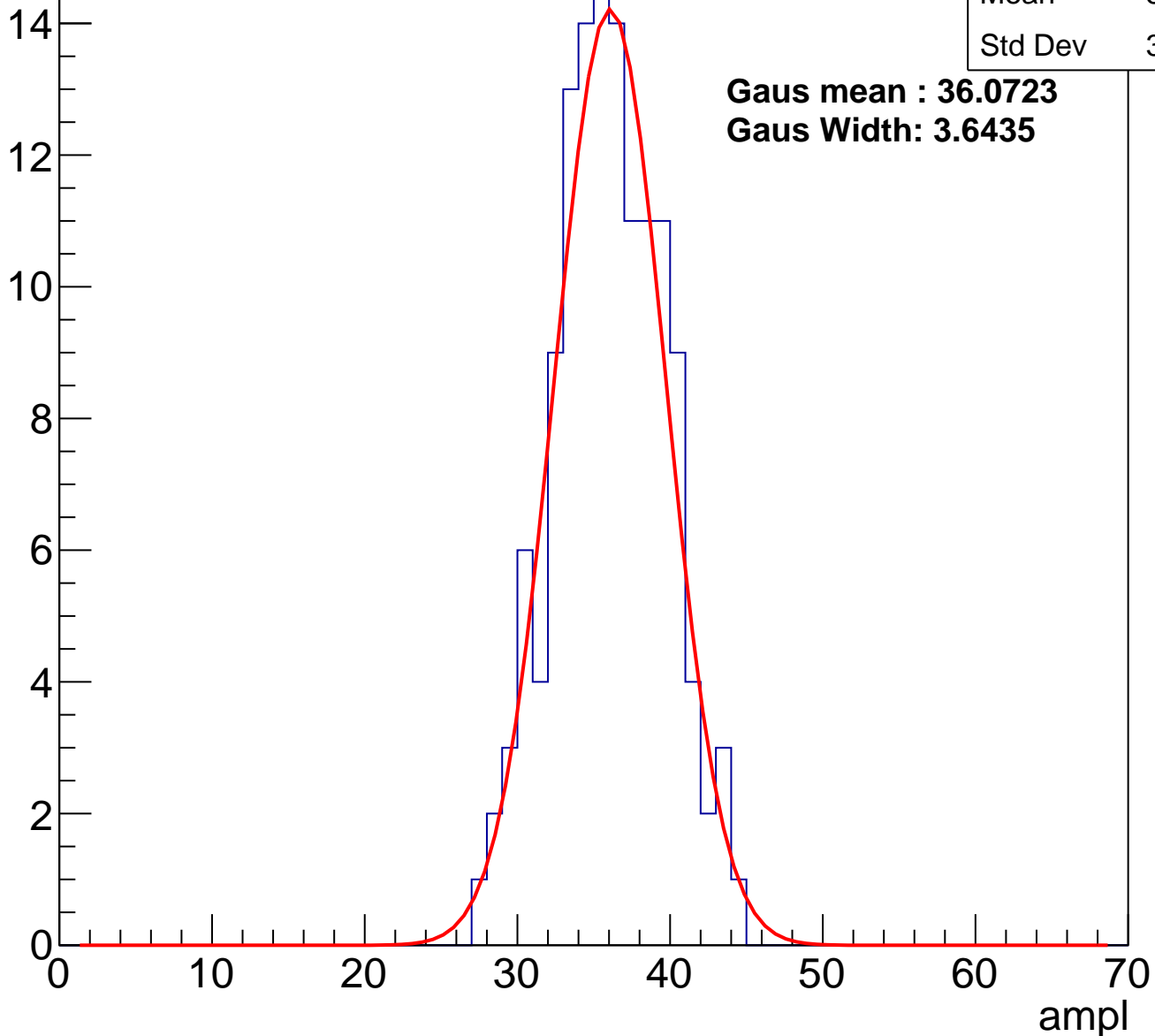
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	35.57
Std Dev	3.533

Gaus mean : 36.0723

Gaus Width: 3.6435

Entry



B0L101S, U19-ch52, adc2

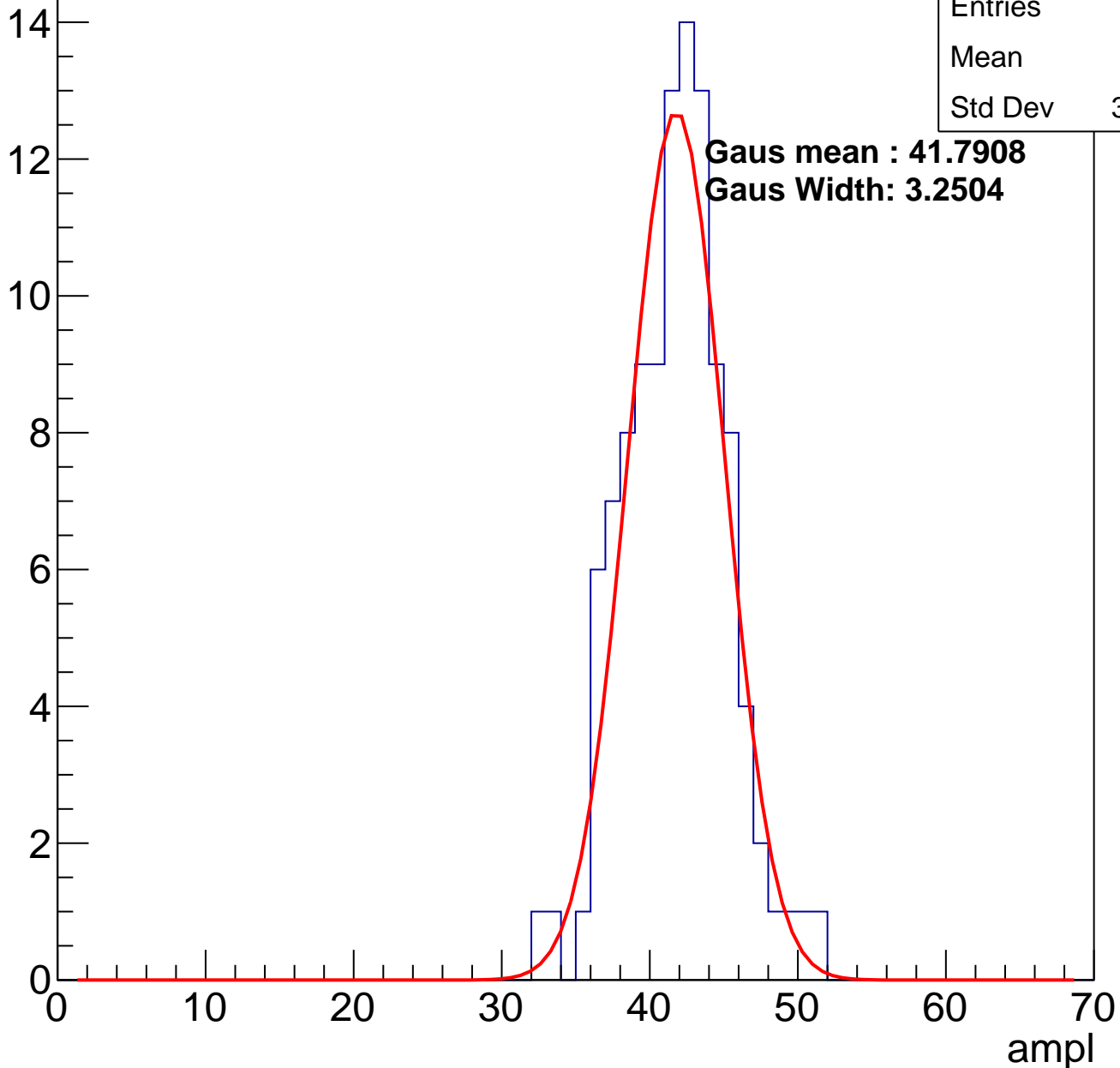
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	41.3
Std Dev	3.442

Gaus mean : 41.7908

Gaus Width: 3.2504

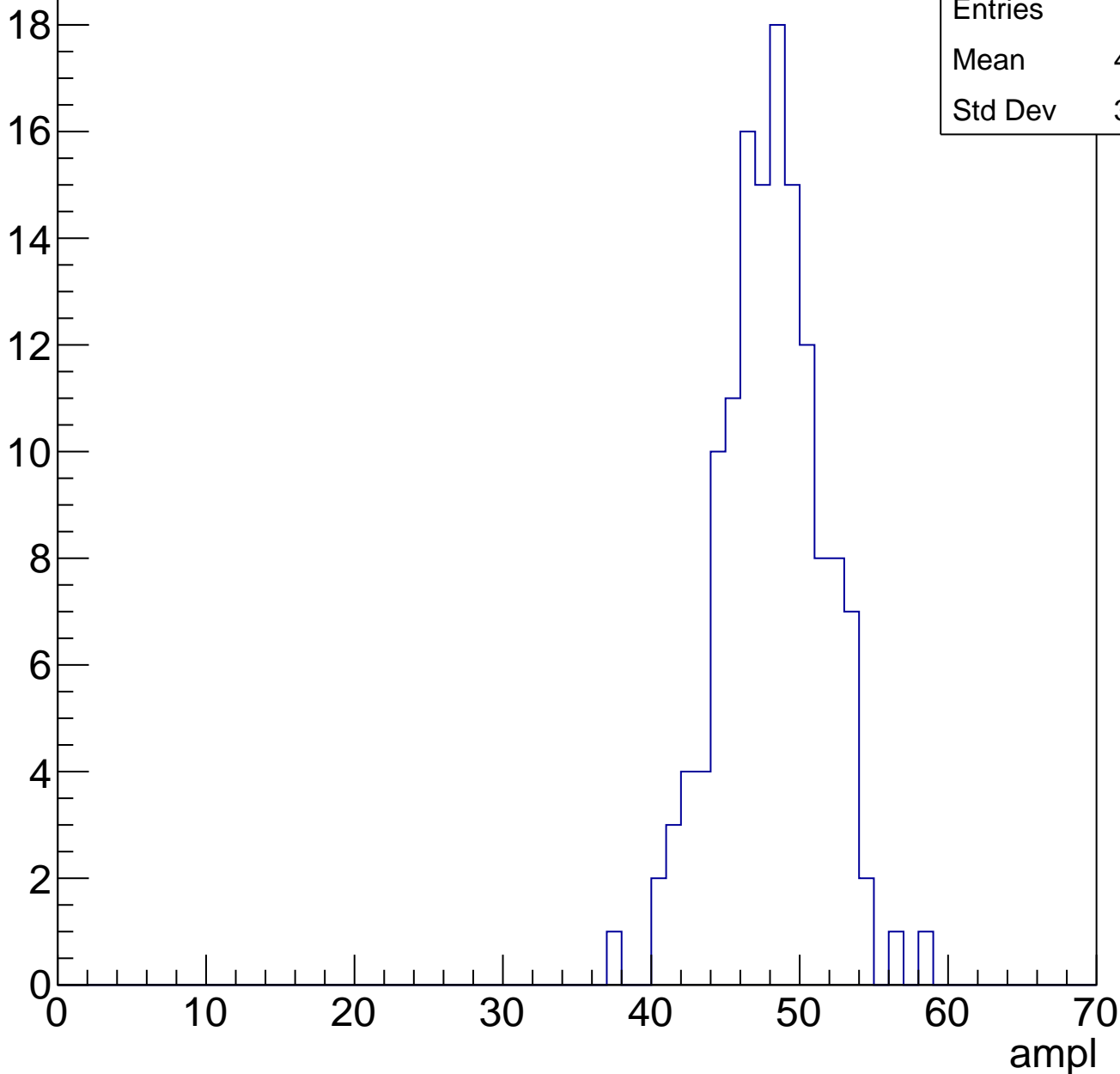
Entry



B0L101S, U19-ch52, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

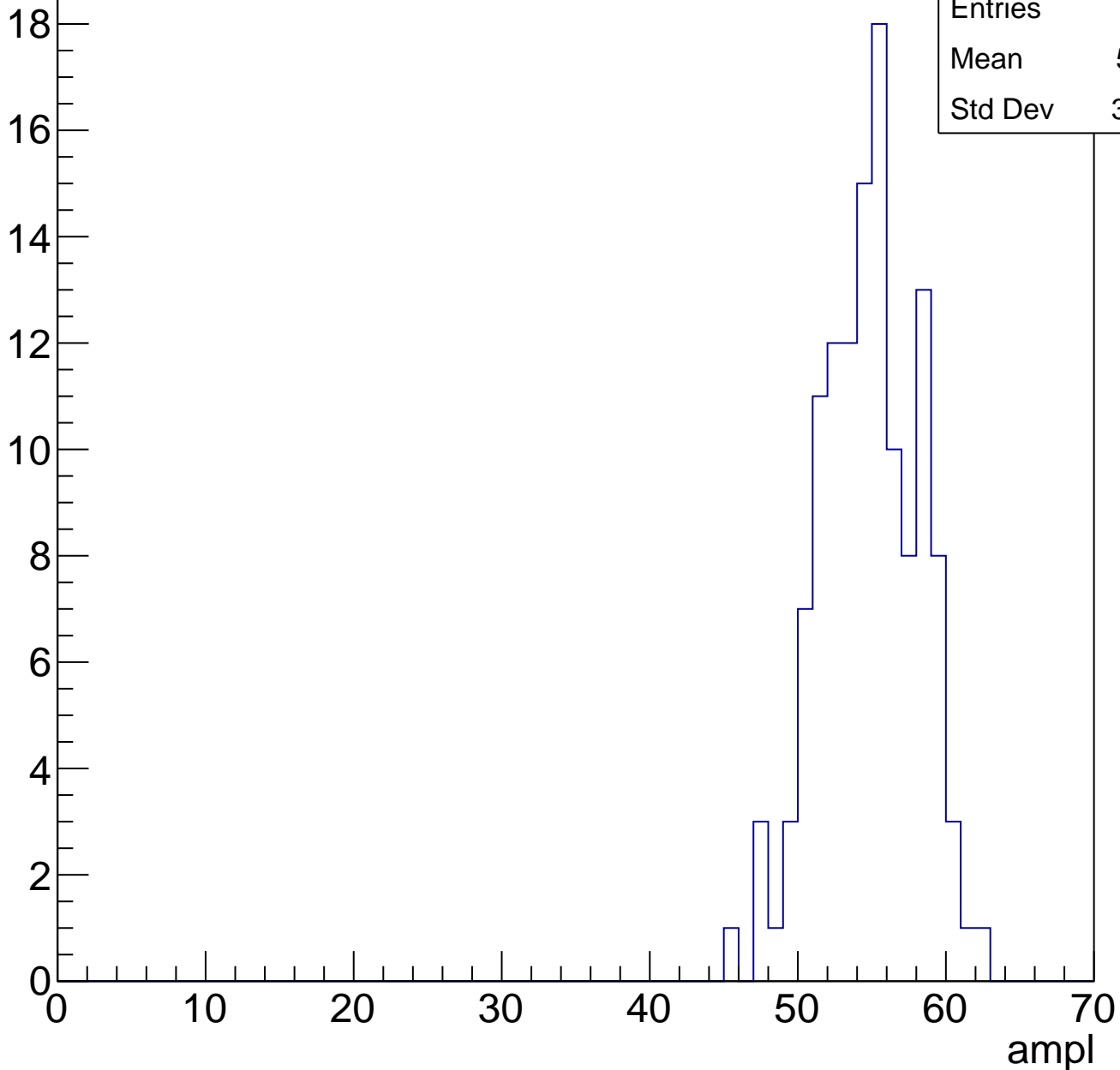


B0L101S, U19-ch52, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	54.31
Std Dev	3.269

Entry



B0L101S, U19-ch52, adc5

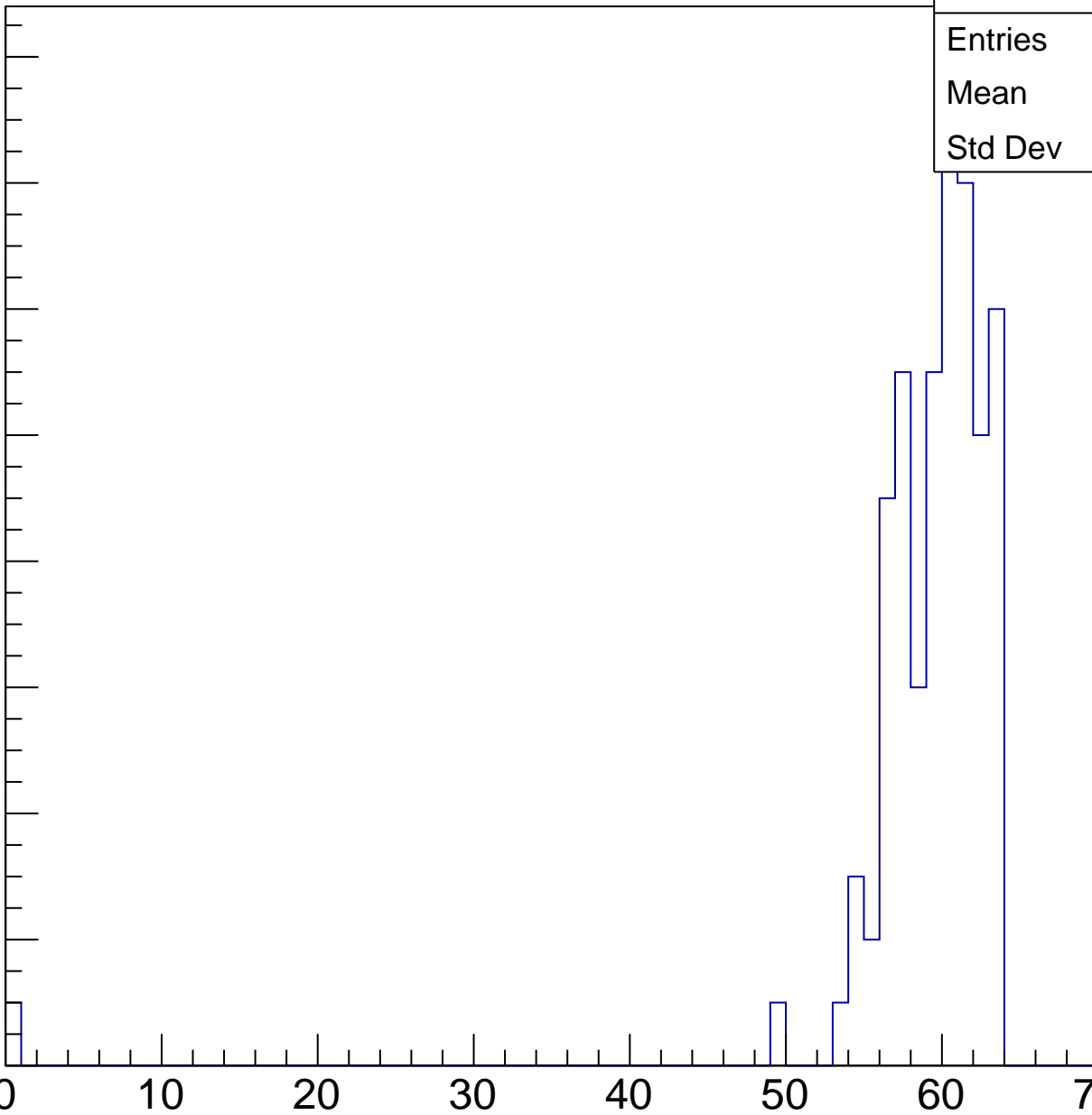
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.68
Std Dev	6.579

ampl

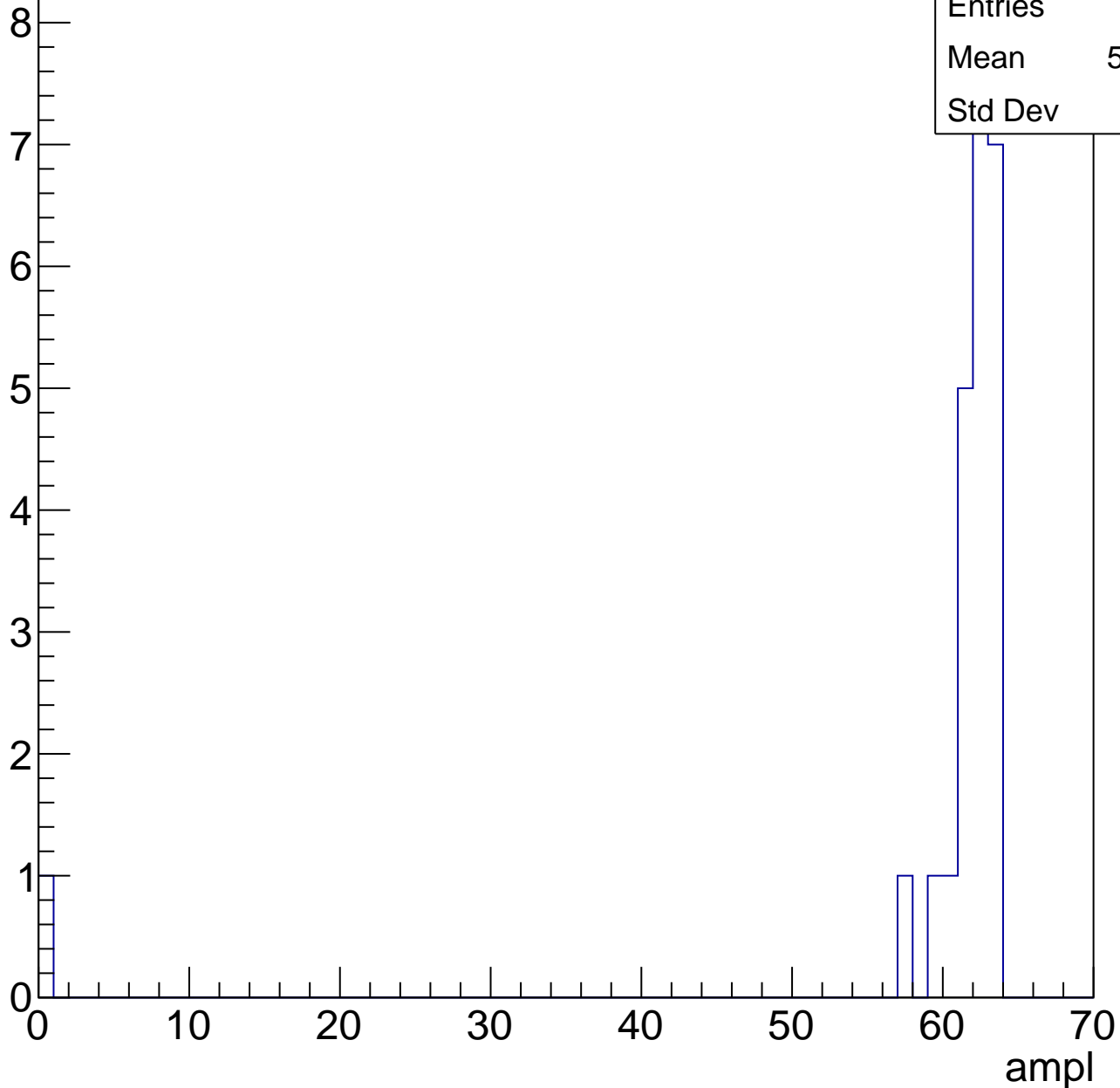


B0L101S, U19-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

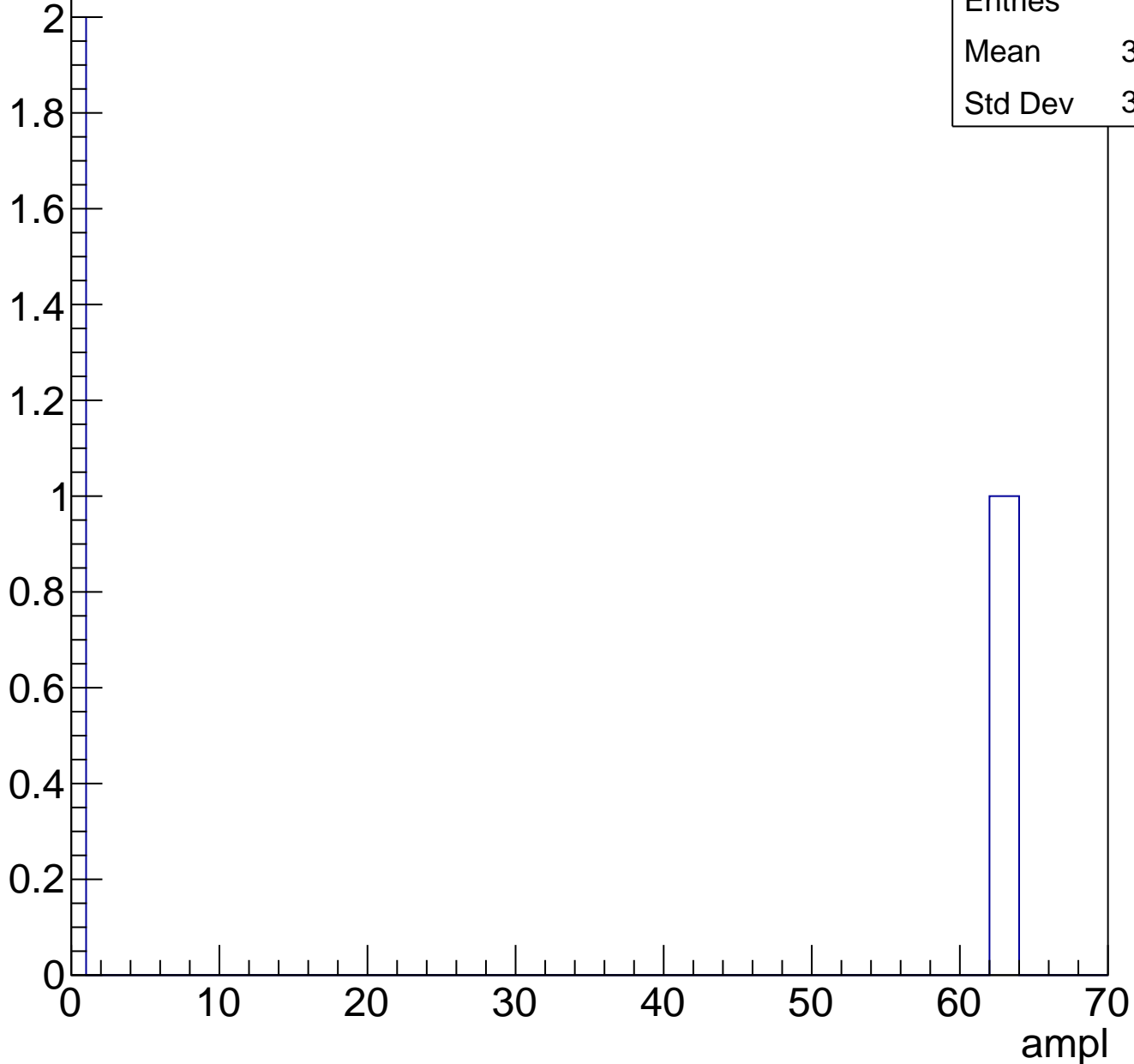
Entries	24
Mean	59.08
Std Dev	12.4



B0L101S, U19-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



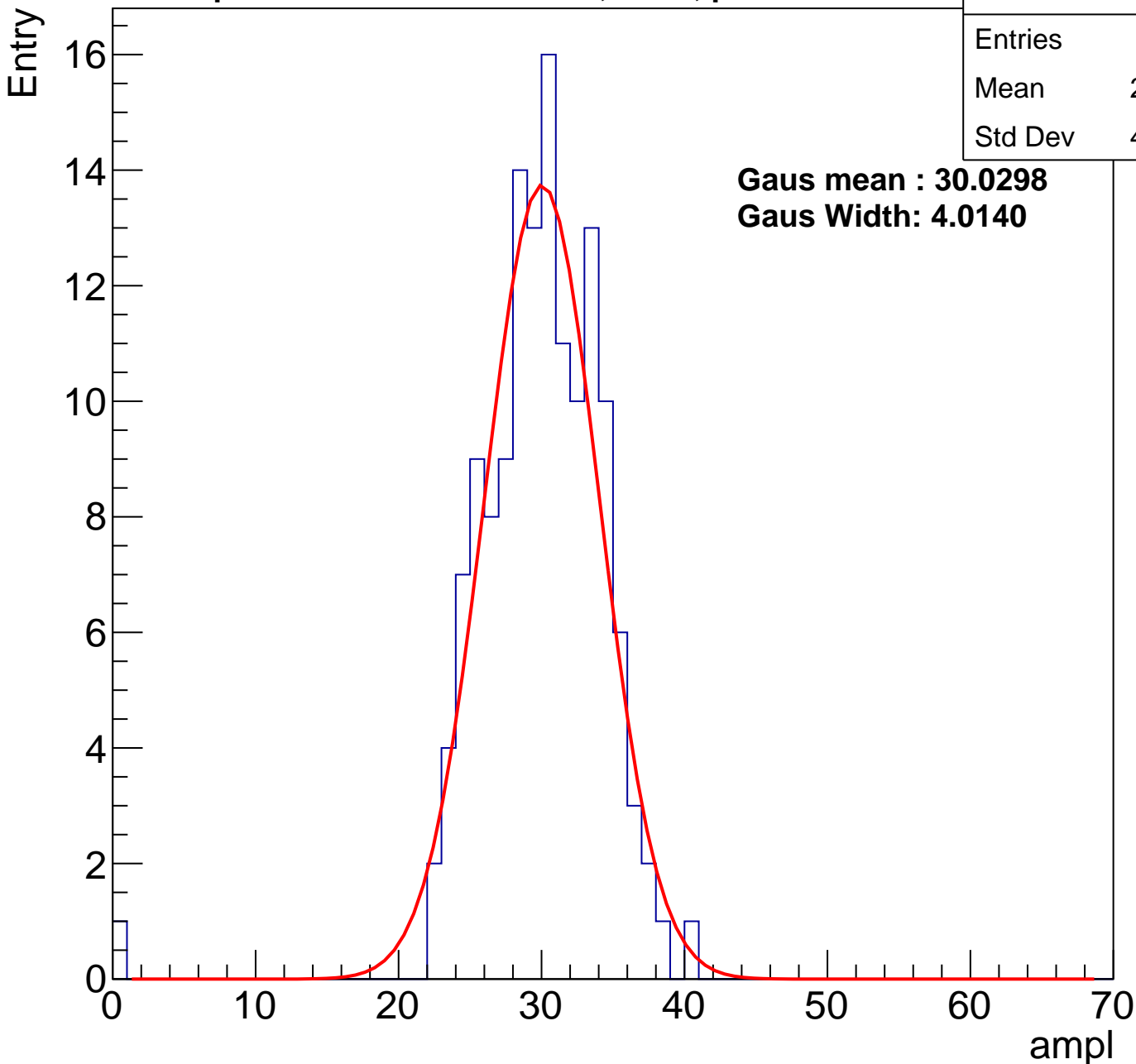
B0L101S, U19-ch53, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	29.49
Std Dev	4.465

Gaus mean : 30.0298

Gaus Width: 4.0140



B0L101S, U19-ch53, adc1

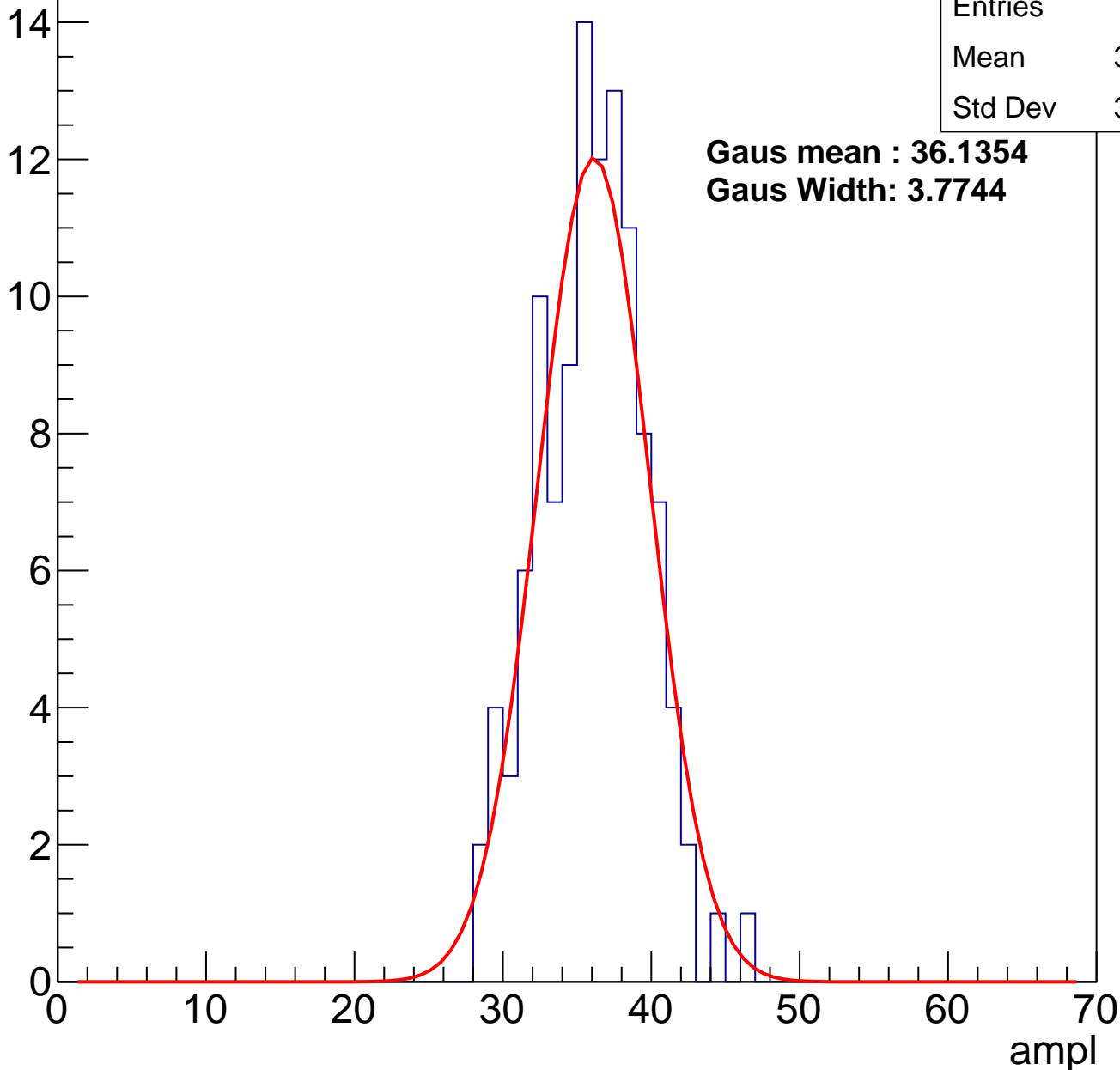
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	35.58
Std Dev	3.519

Gaus mean : 36.1354

Gaus Width: 3.7744

Entry



B0L101S, U19-ch53, adc2

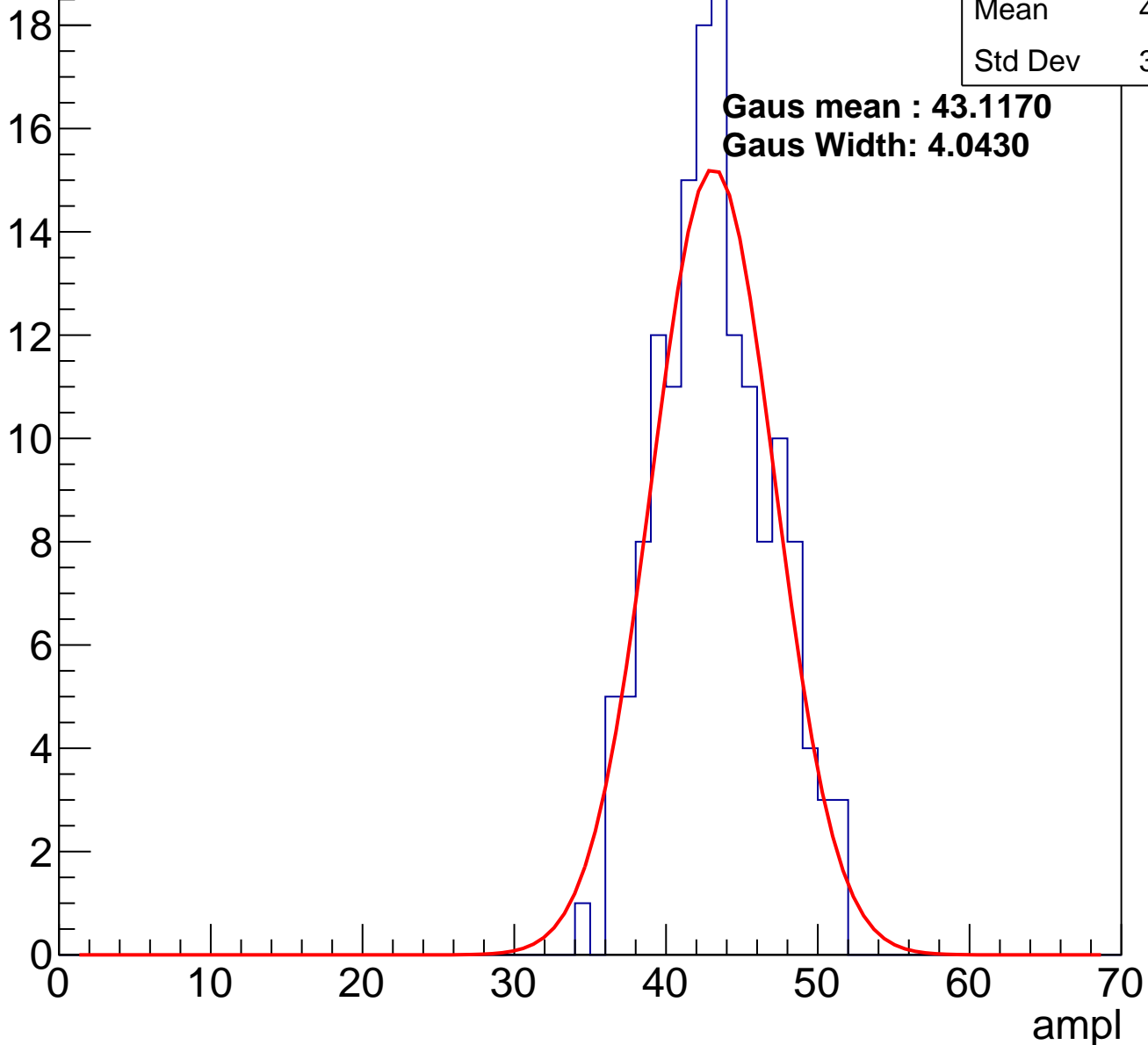
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	42.76
Std Dev	3.649

Gaus mean : 43.1170

Gaus Width: 4.0430

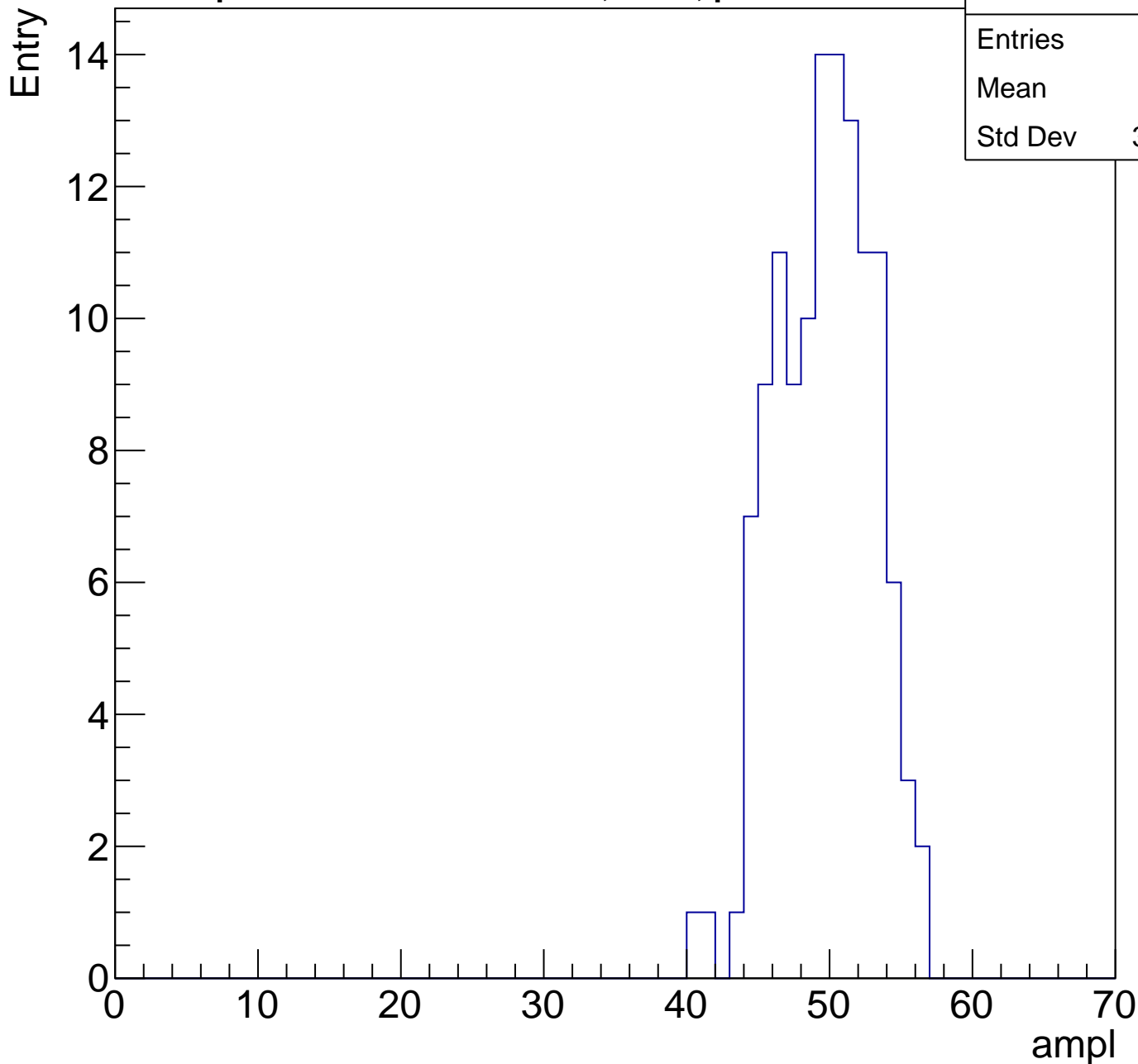
Entry



B0L101S, U19-ch53, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	49.2
Std Dev	3.297



B0L101S, U19-ch53, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	56.08
Std Dev	3.334

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

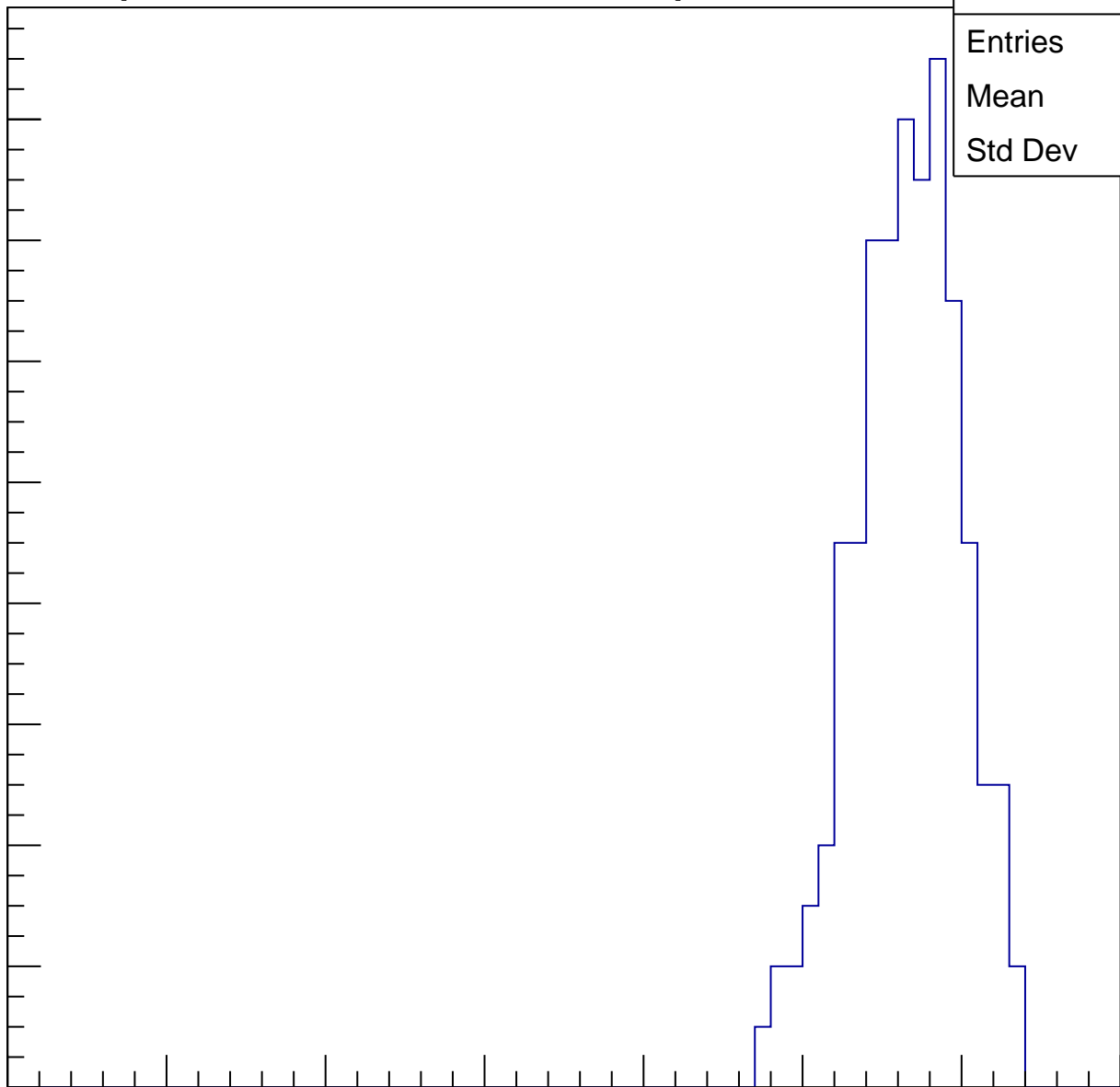
40

50

60

70

ampl



B0L101S, U19-ch53, adc5

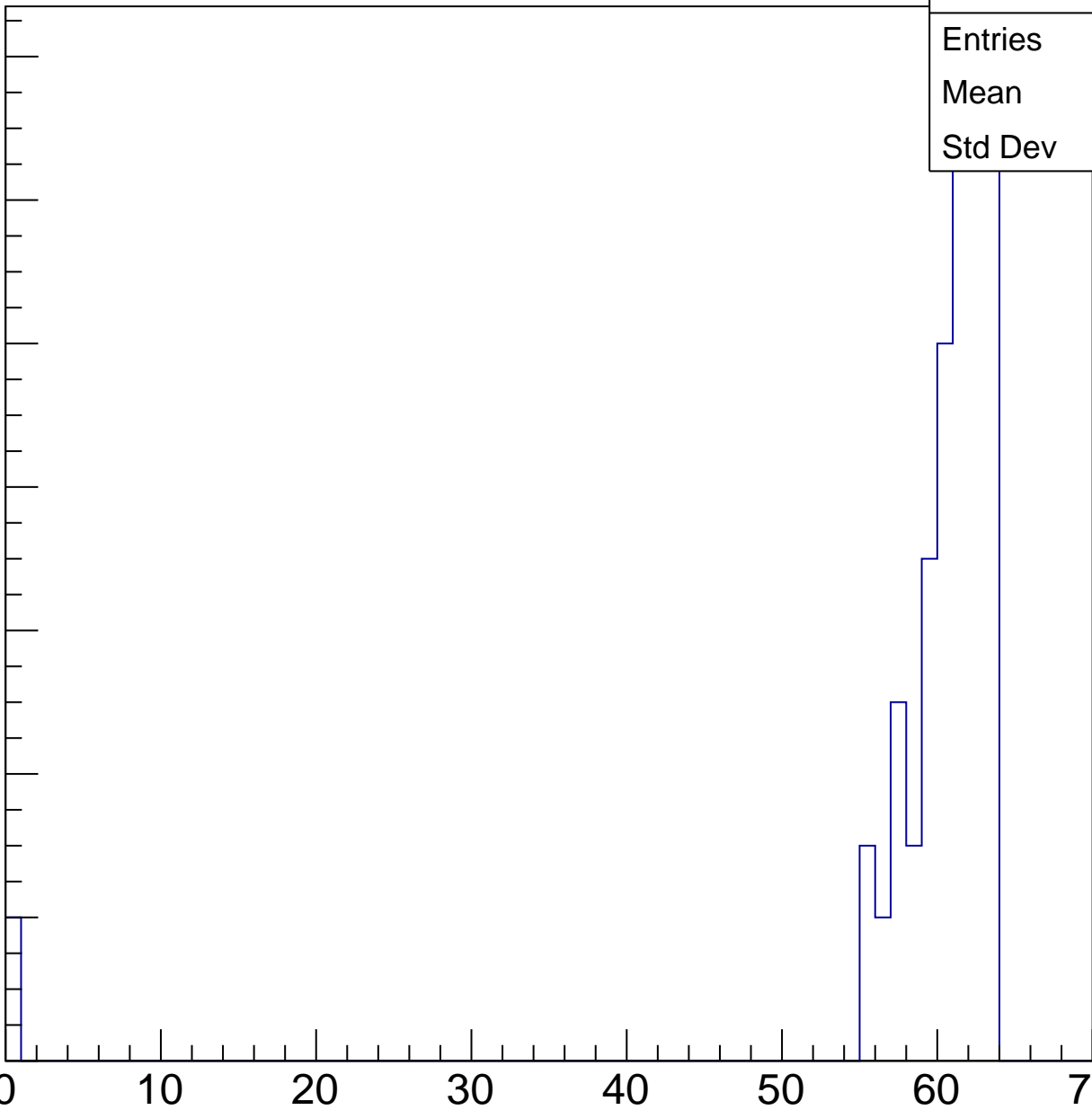
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.75
Std Dev	10.17

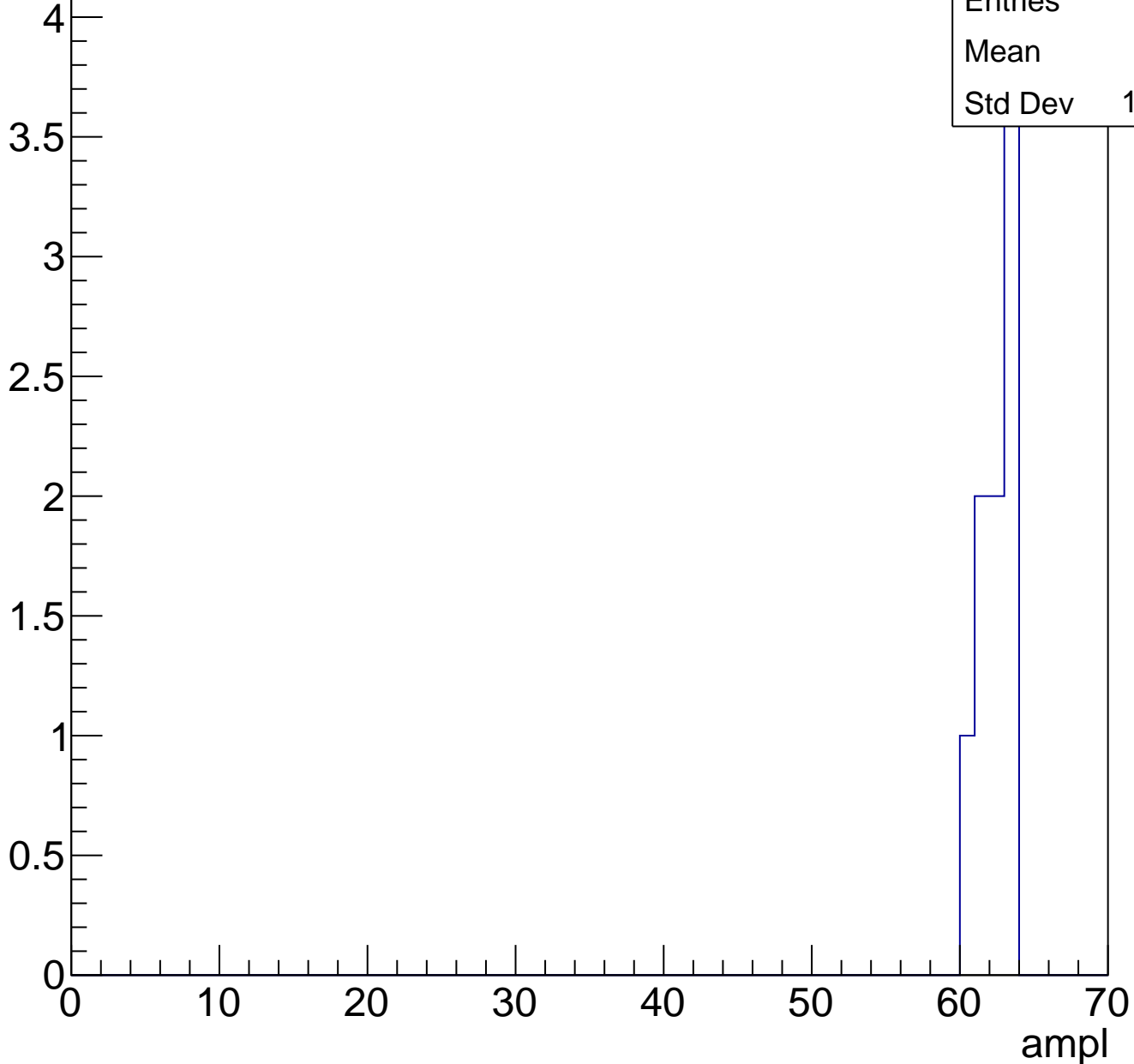
ampl



B0L101S, U19-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

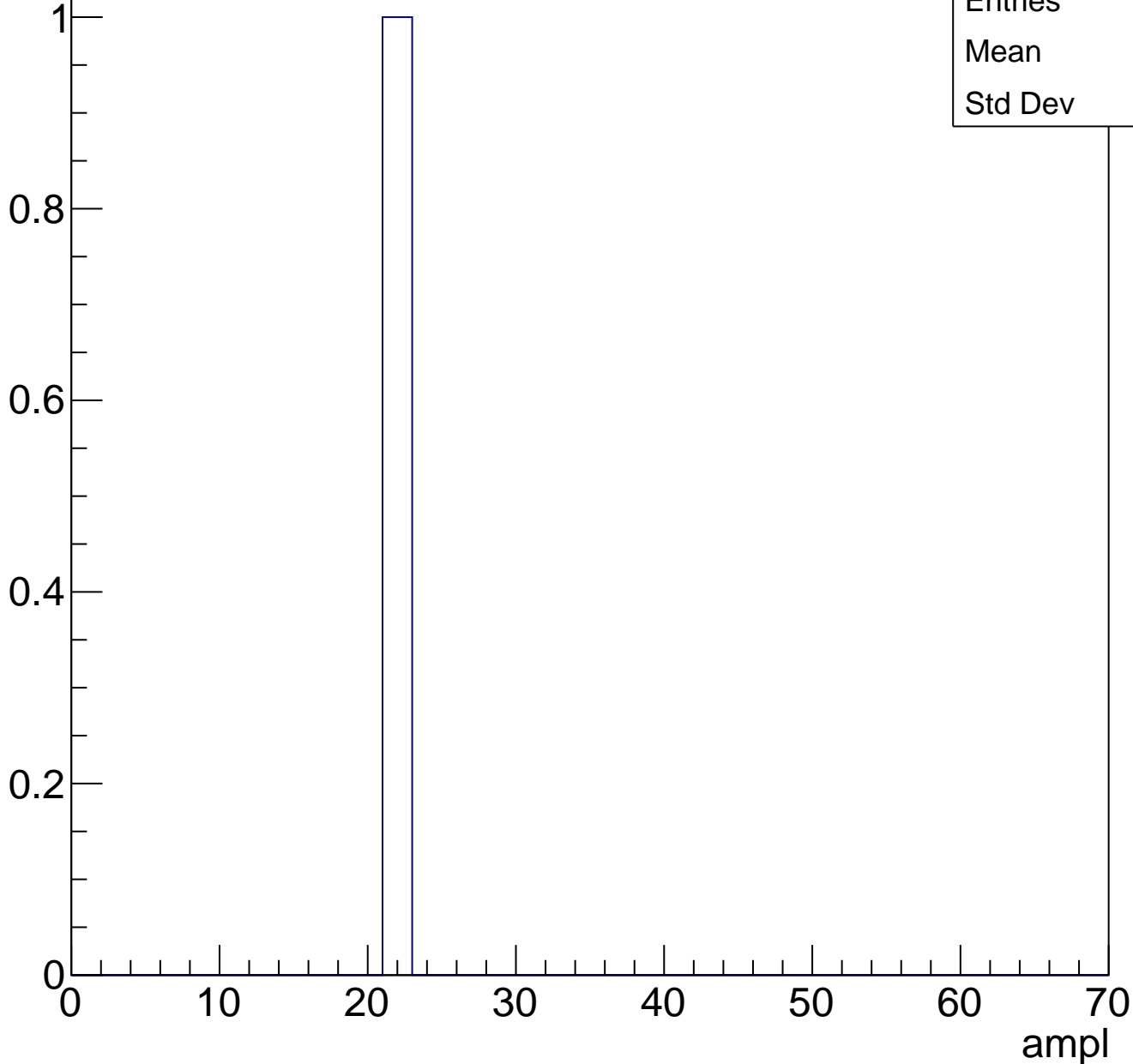
Entry



B0L101S, U19-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch54, adc0

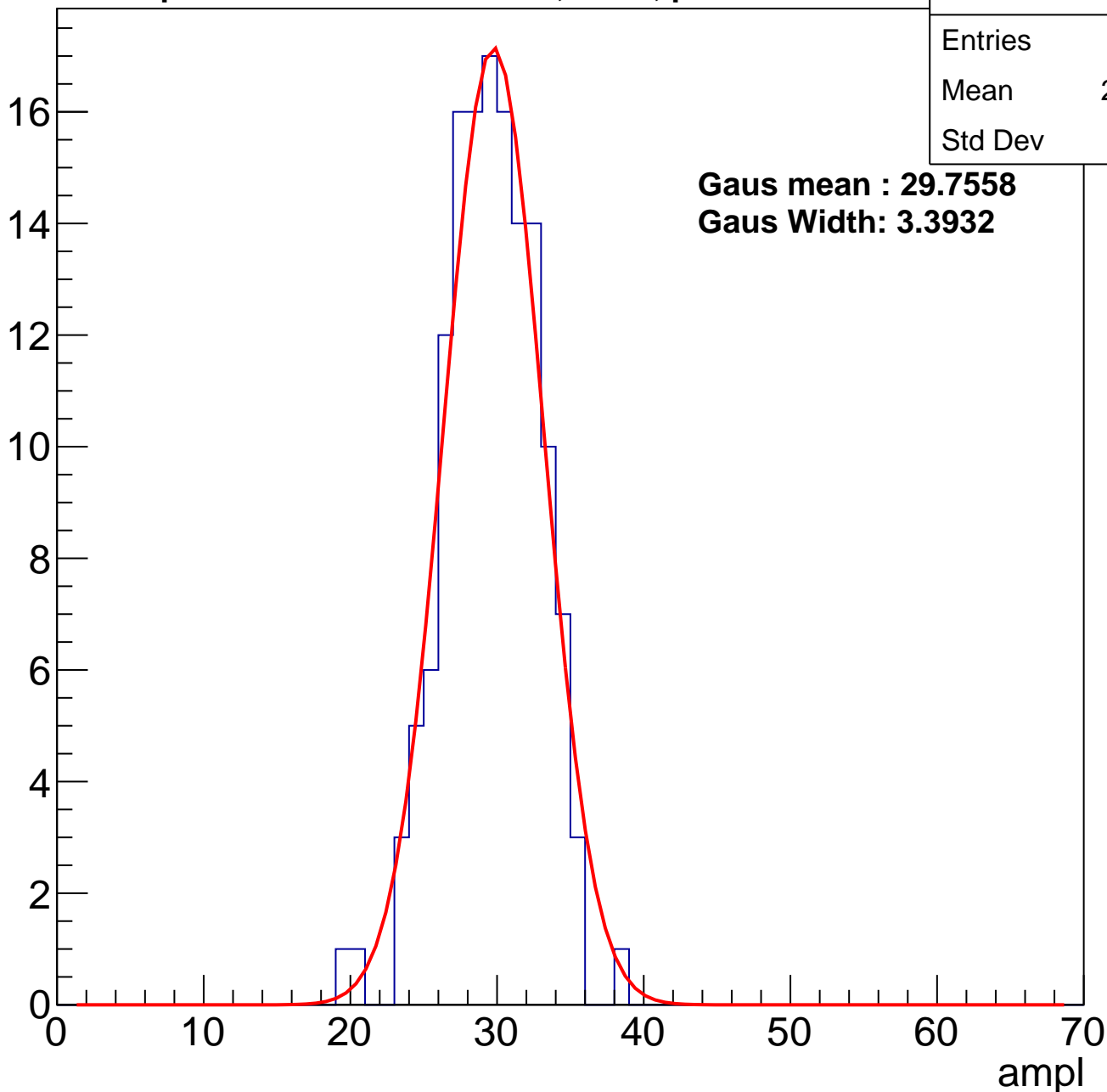
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	29.13
Std Dev	3.16

Gaus mean : 29.7558

Gaus Width: 3.3932

Entry



B0L101S, U19-ch54, adc1

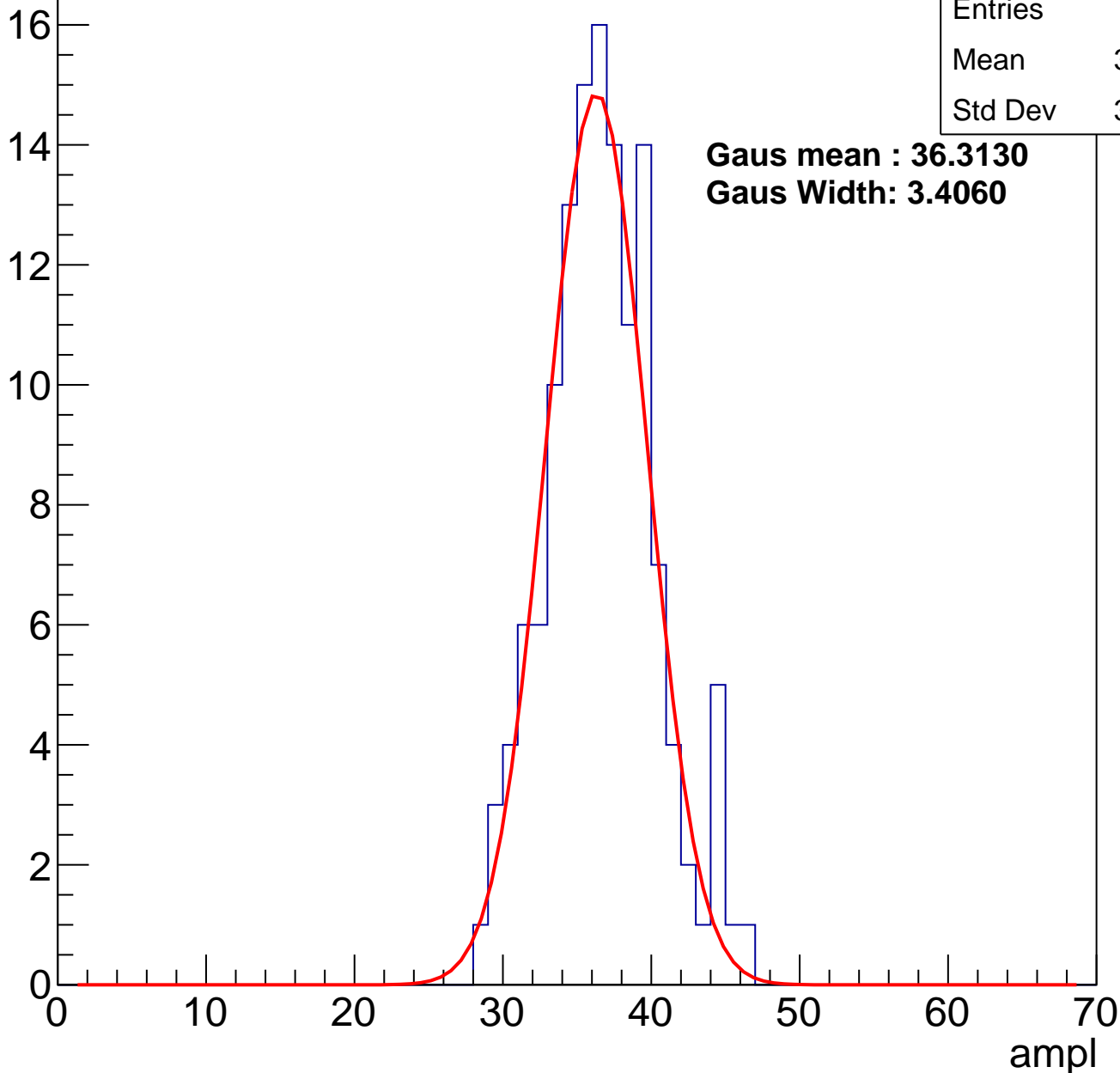
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	36.19
Std Dev	3.635

Gaus mean : 36.3130

Gaus Width: 3.4060

Entry



B0L101S, U19-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

154

Mean

43.03

Std Dev

3.796

Gaus mean : 43.4153

Gaus Width: 3.9906

0

10

20

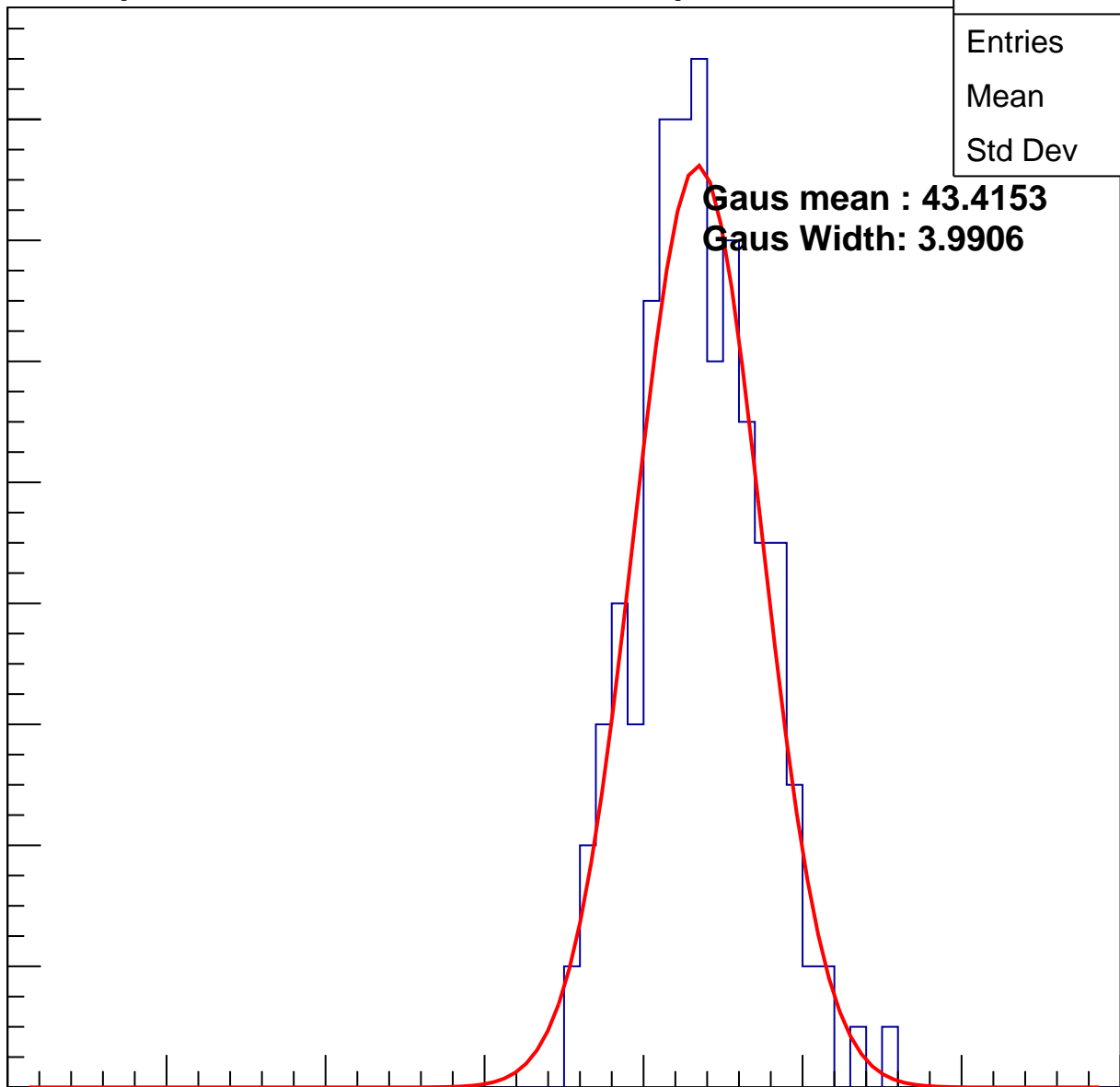
30

40

50

60

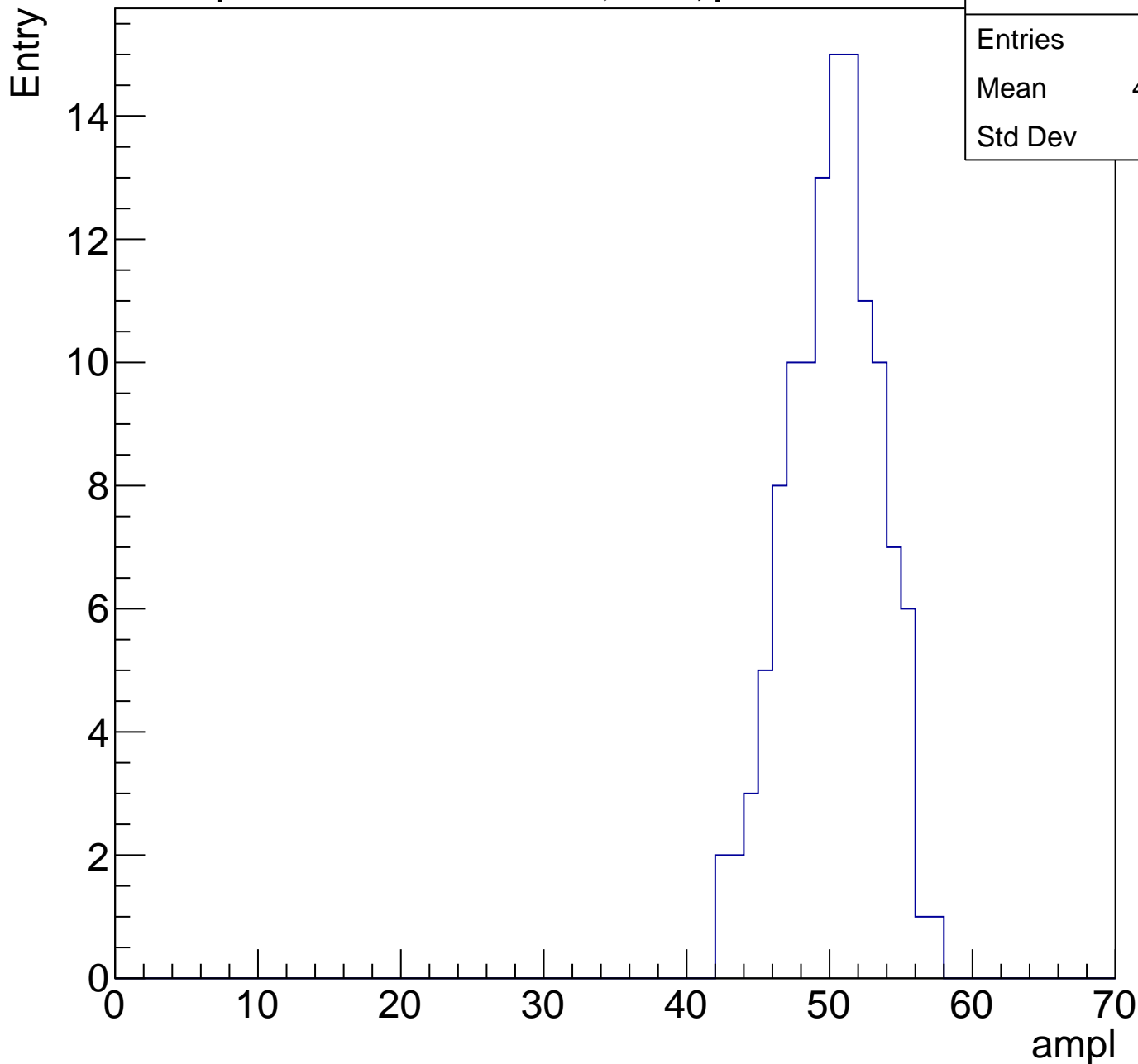
ampl



B0L101S, U19-ch54, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	49.75
Std Dev	3.2



B0L101S, U19-ch54, adc4

calib_packv5_042523_0143.root, FC#1, port C1

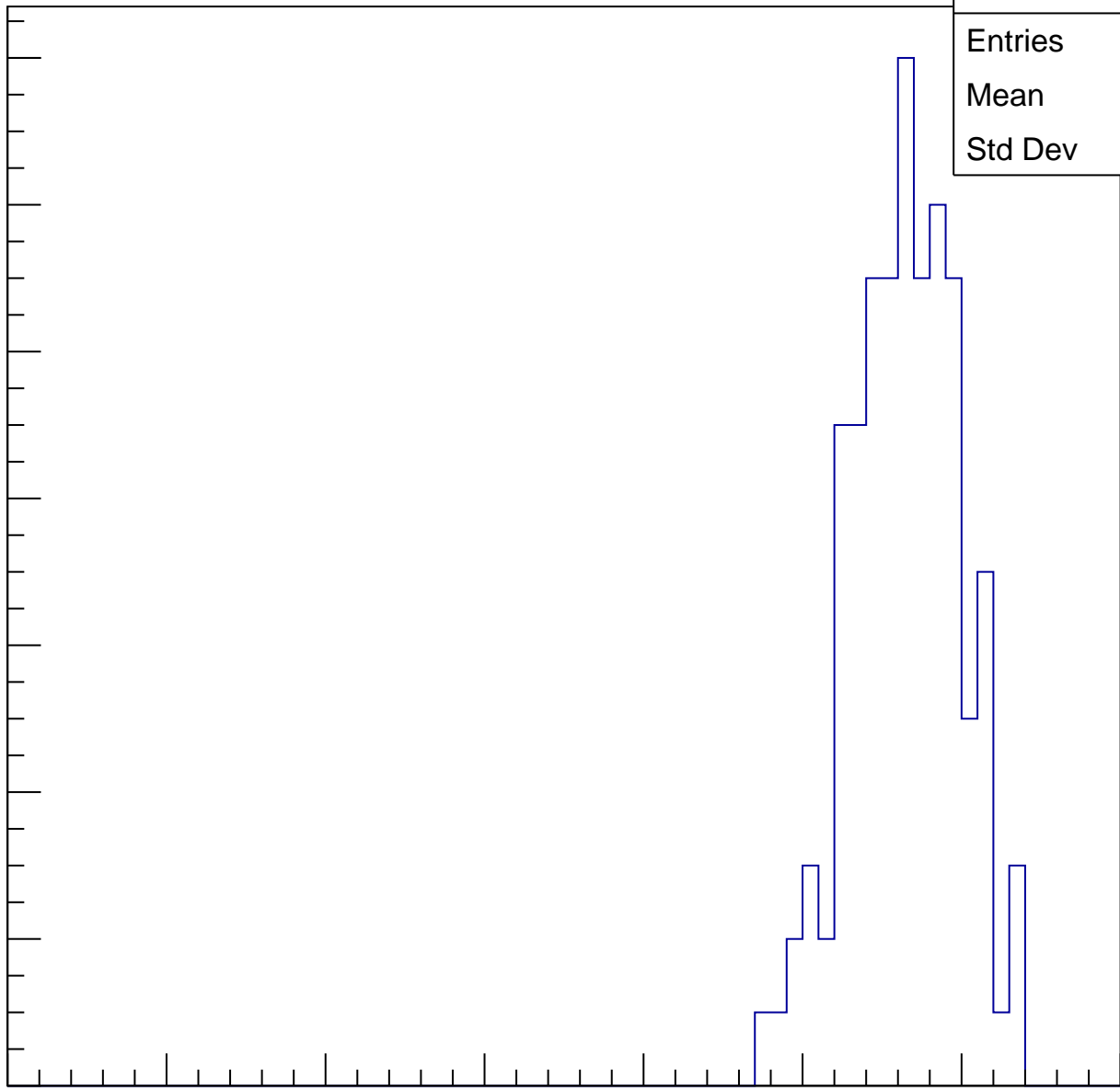
Entries	113
Mean	55.96
Std Dev	3.361

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch54, adc5

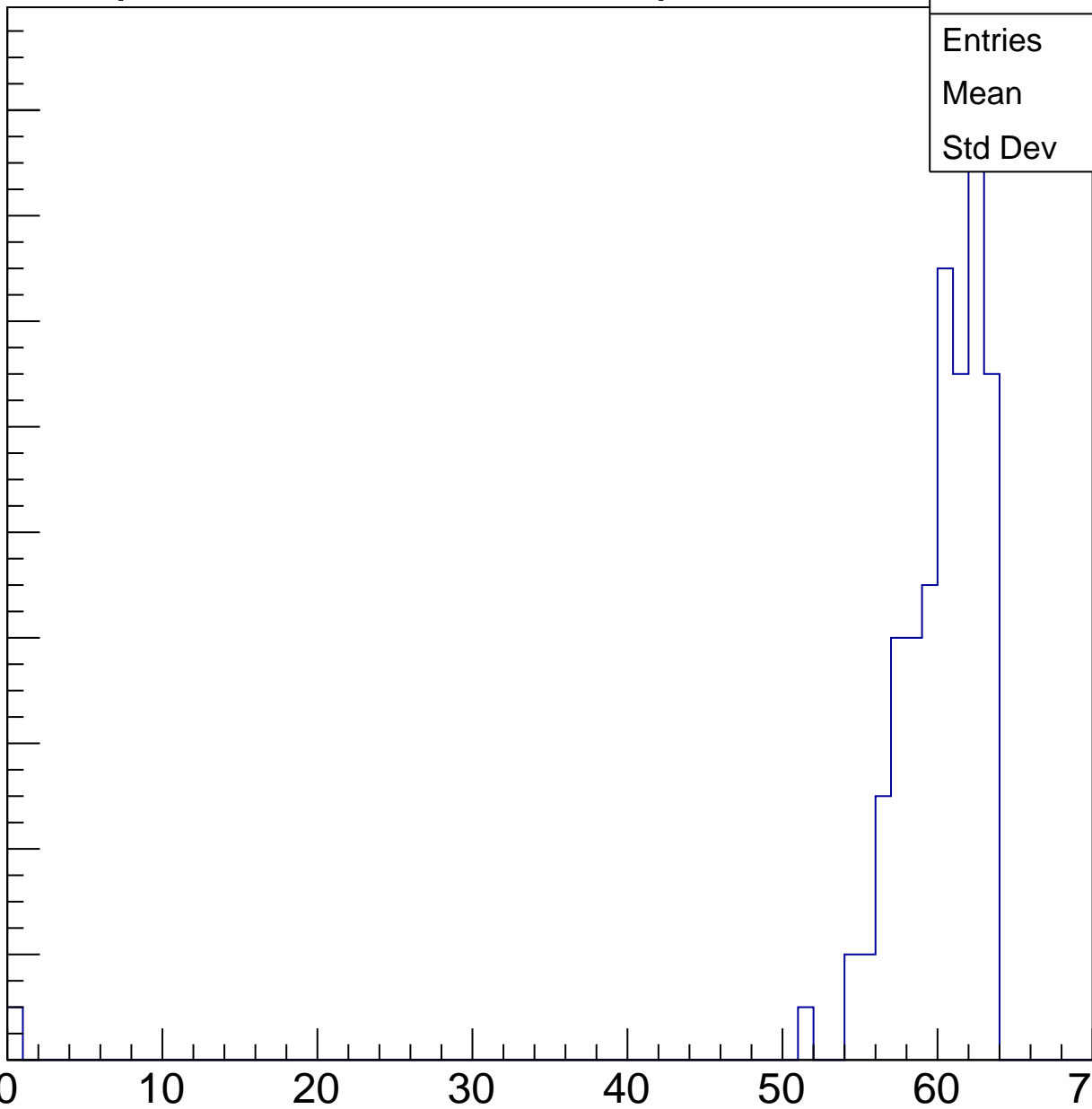
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.27
Std Dev	6.576

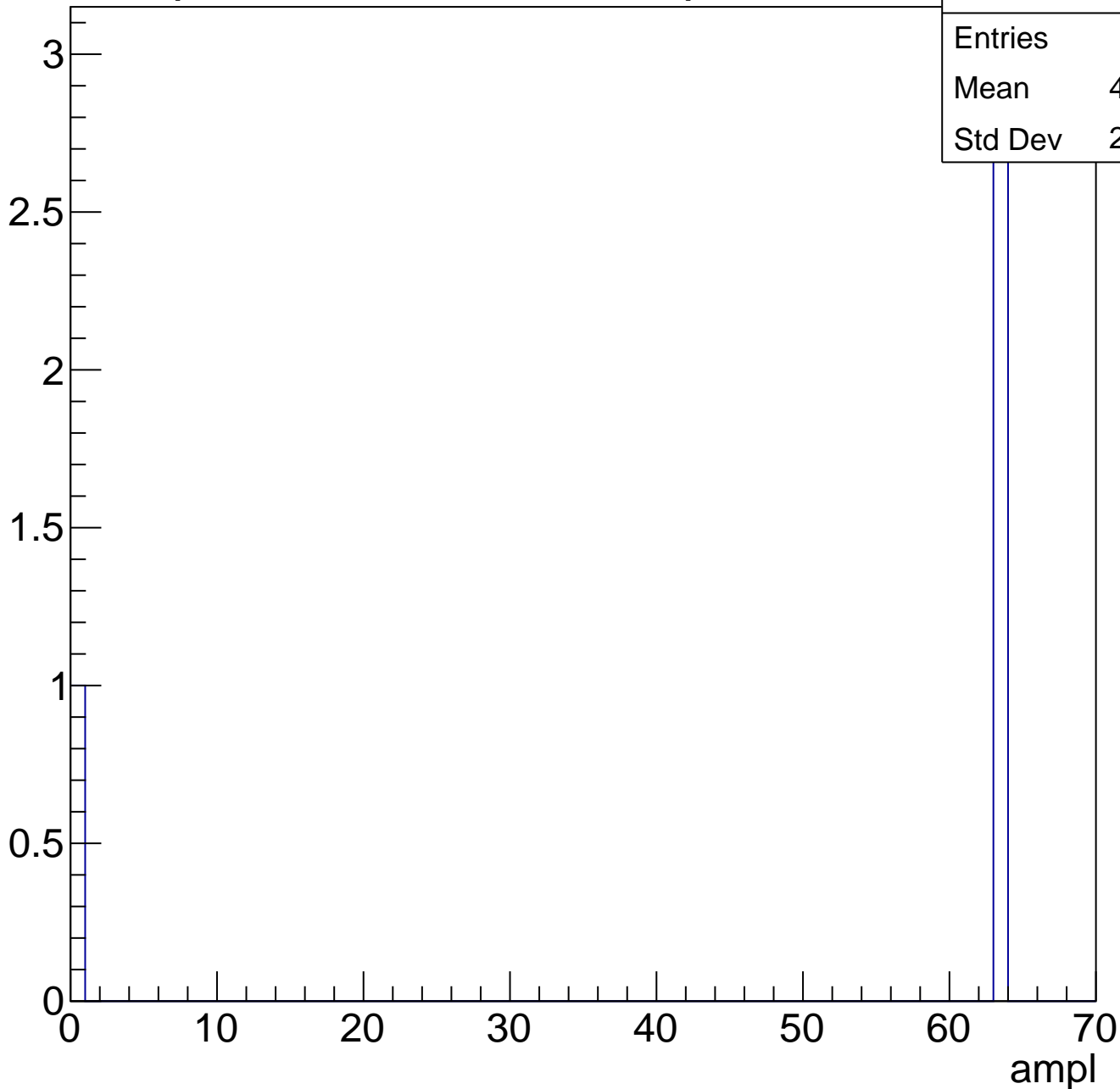
ampl



B0L101S, U19-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

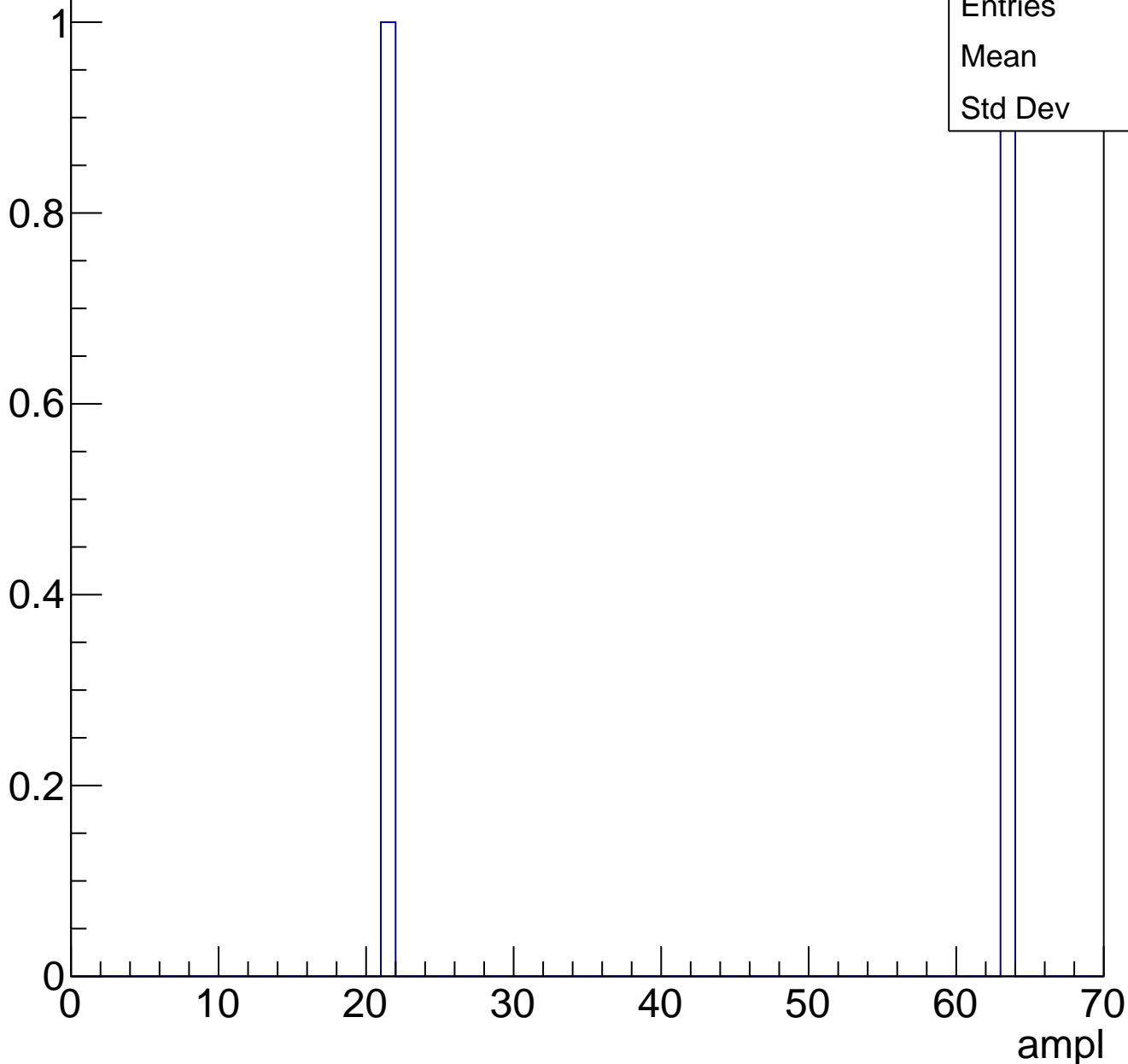
Entry



B0L101S, U19-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch55, adc0

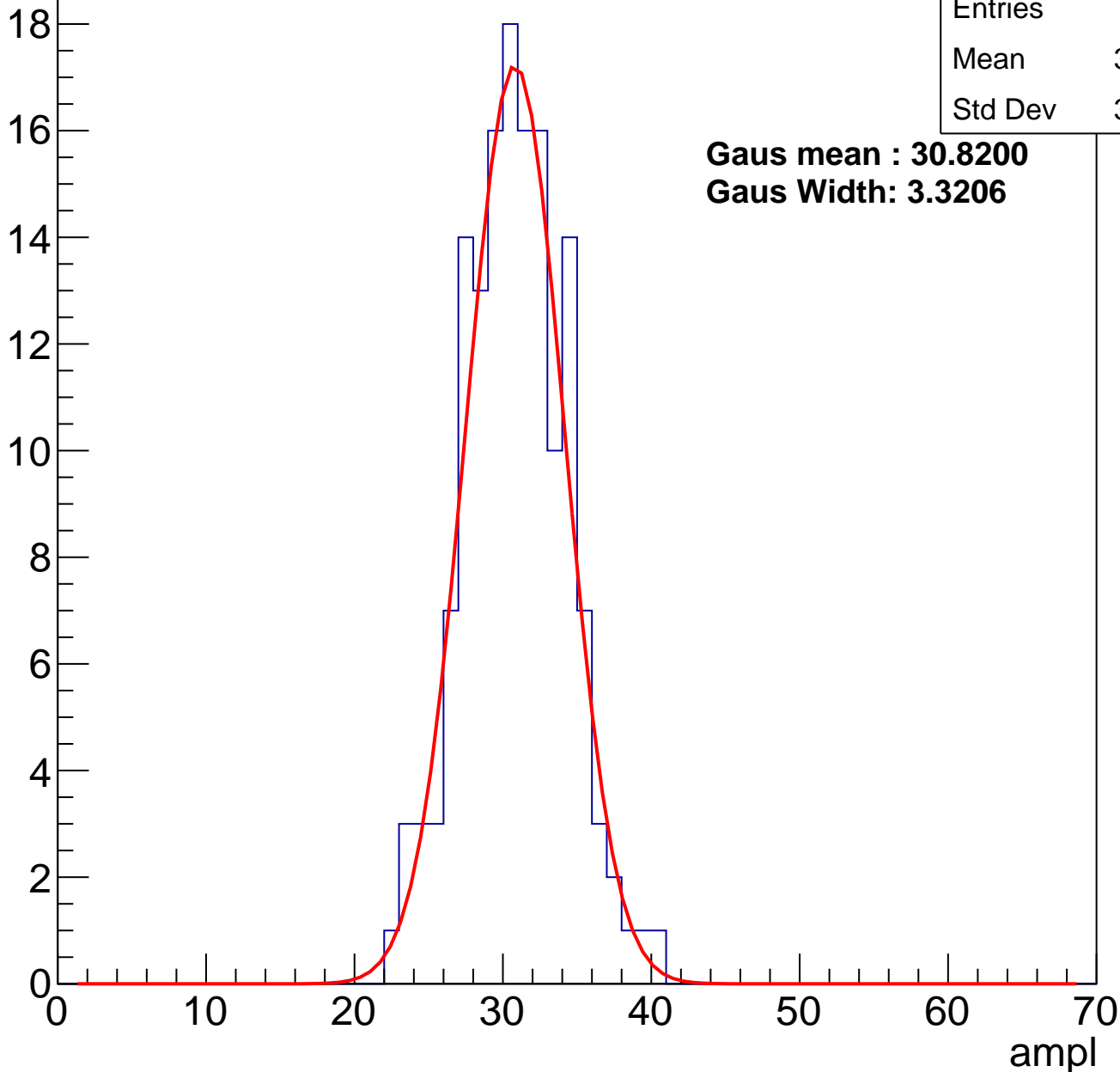
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	30.36
Std Dev	3.378

Gaus mean : 30.8200

Gaus Width: 3.3206

Entry



B0L101S, U19-ch55, adc1

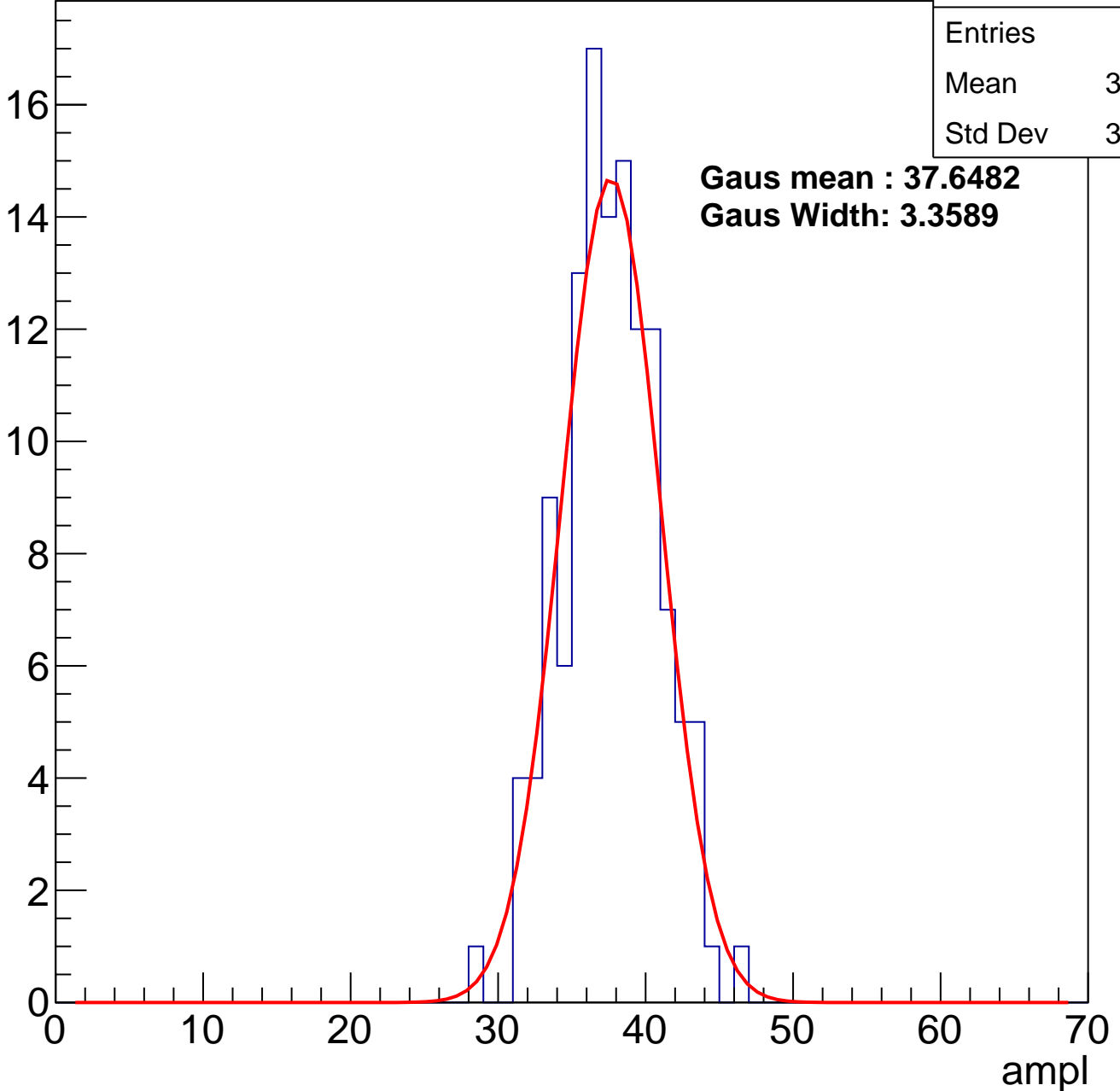
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	126
Mean	37.19
Std Dev	3.219

Gaus mean : 37.6482

Gaus Width: 3.3589



B0L101S, U19-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries 151

Mean 44.26

Std Dev 3.633

Gaus mean : 44.4456

Gaus Width: 3.8606

0

10

20

30

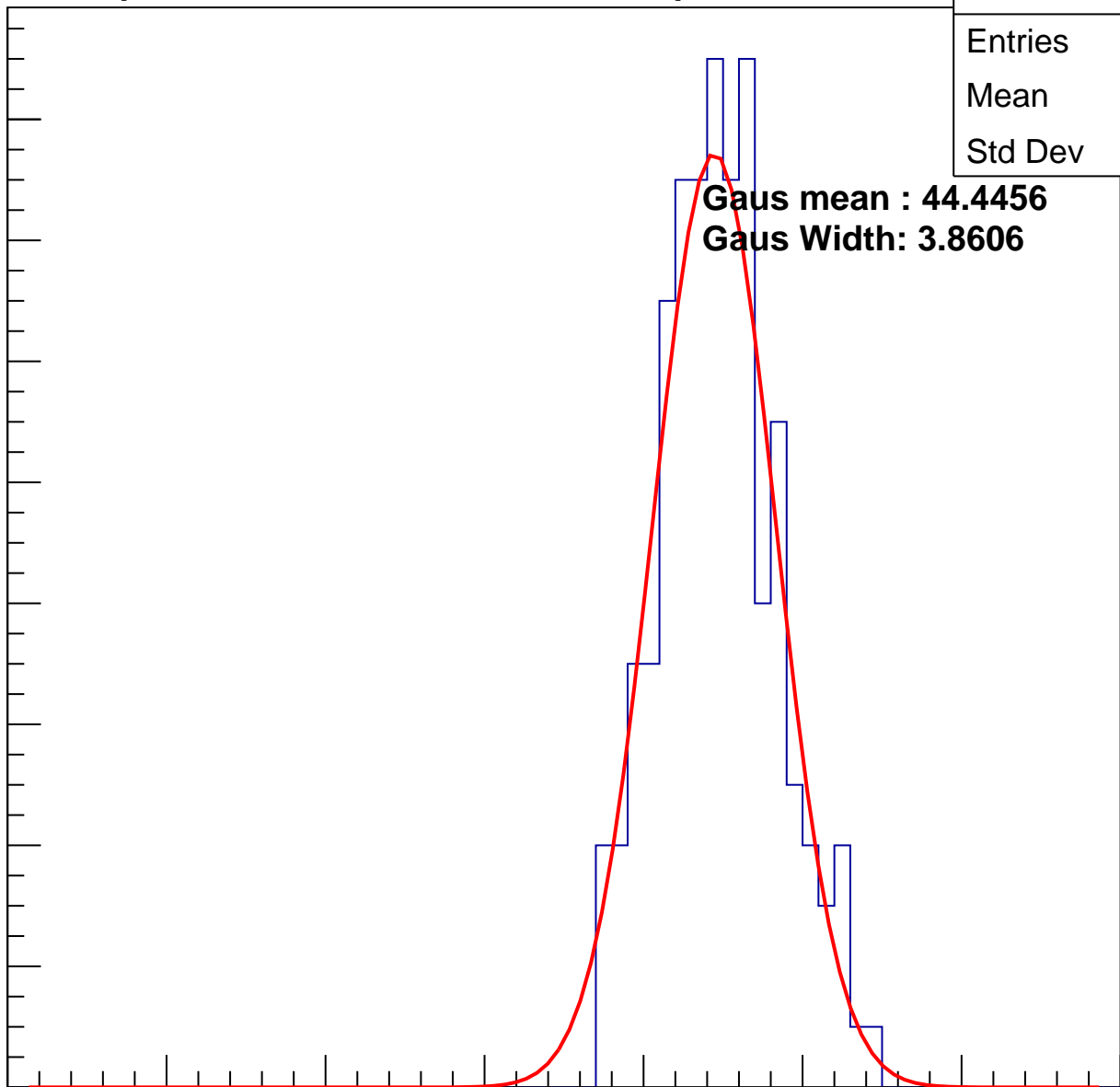
40

50

60

70

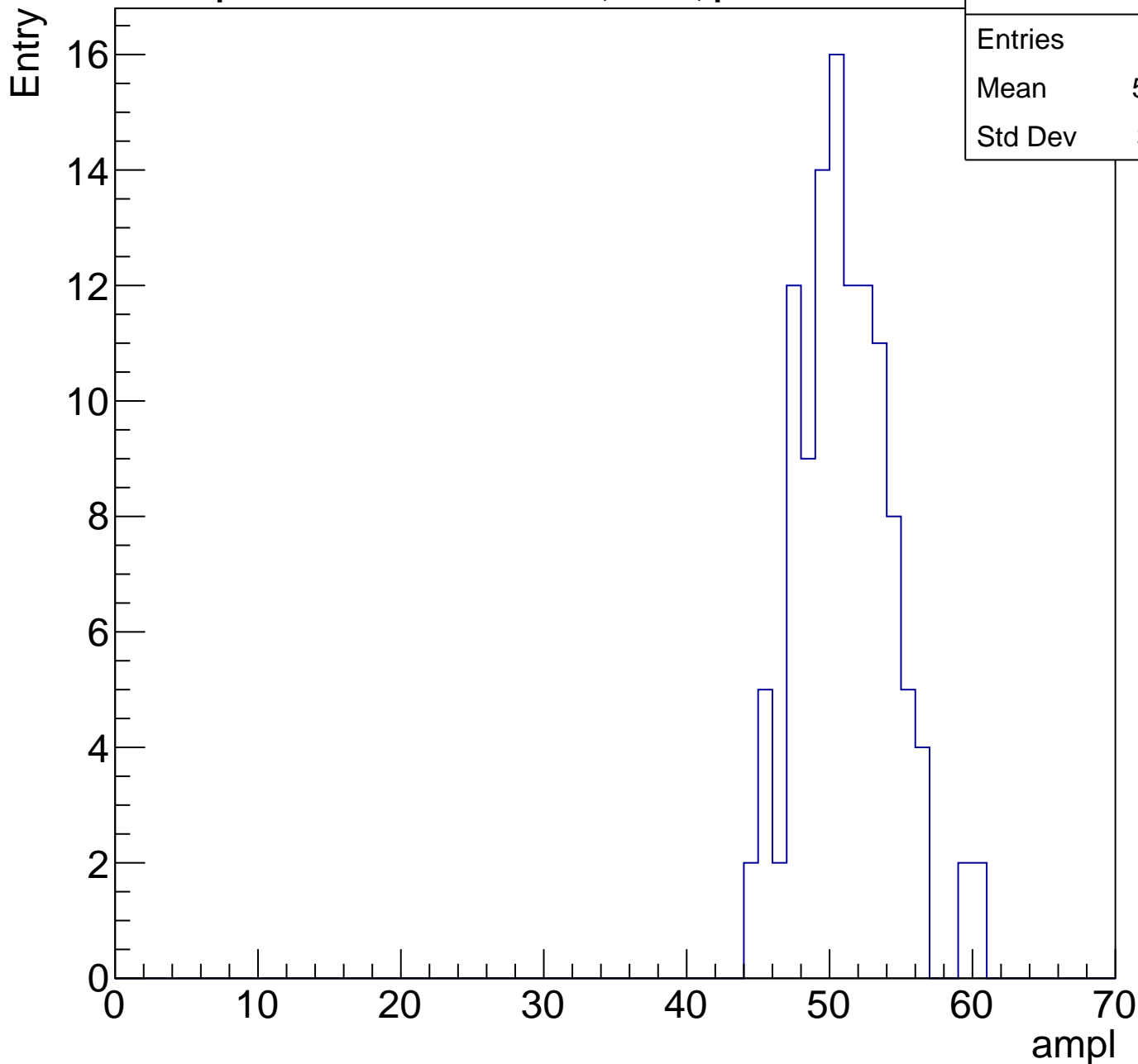
ampl



B0L101S, U19-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	50.65
Std Dev	3.291

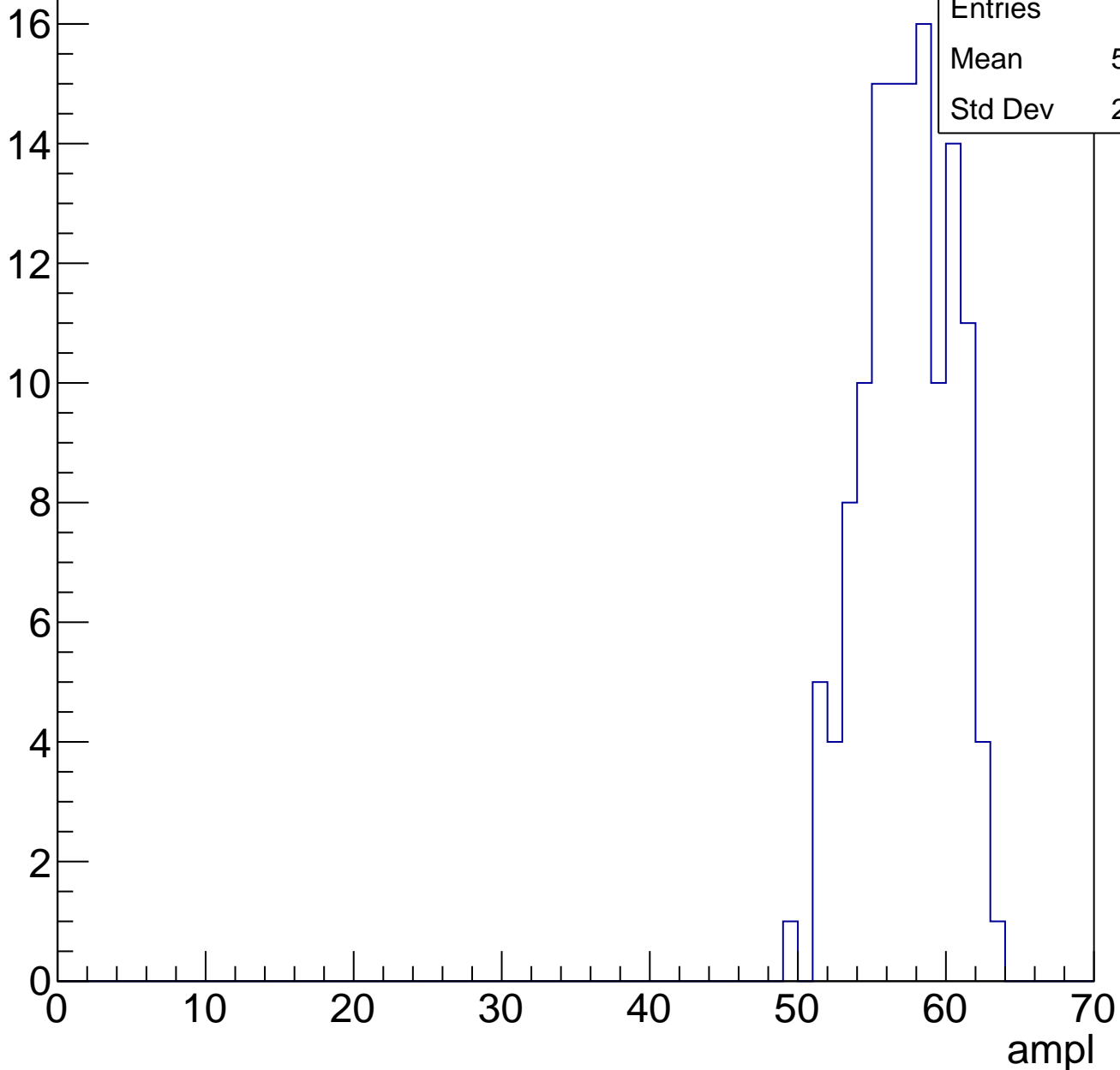


B0L101S, U19-ch55, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	56.87
Std Dev	2.958

Entry



B0L101S, U19-ch55, adc5

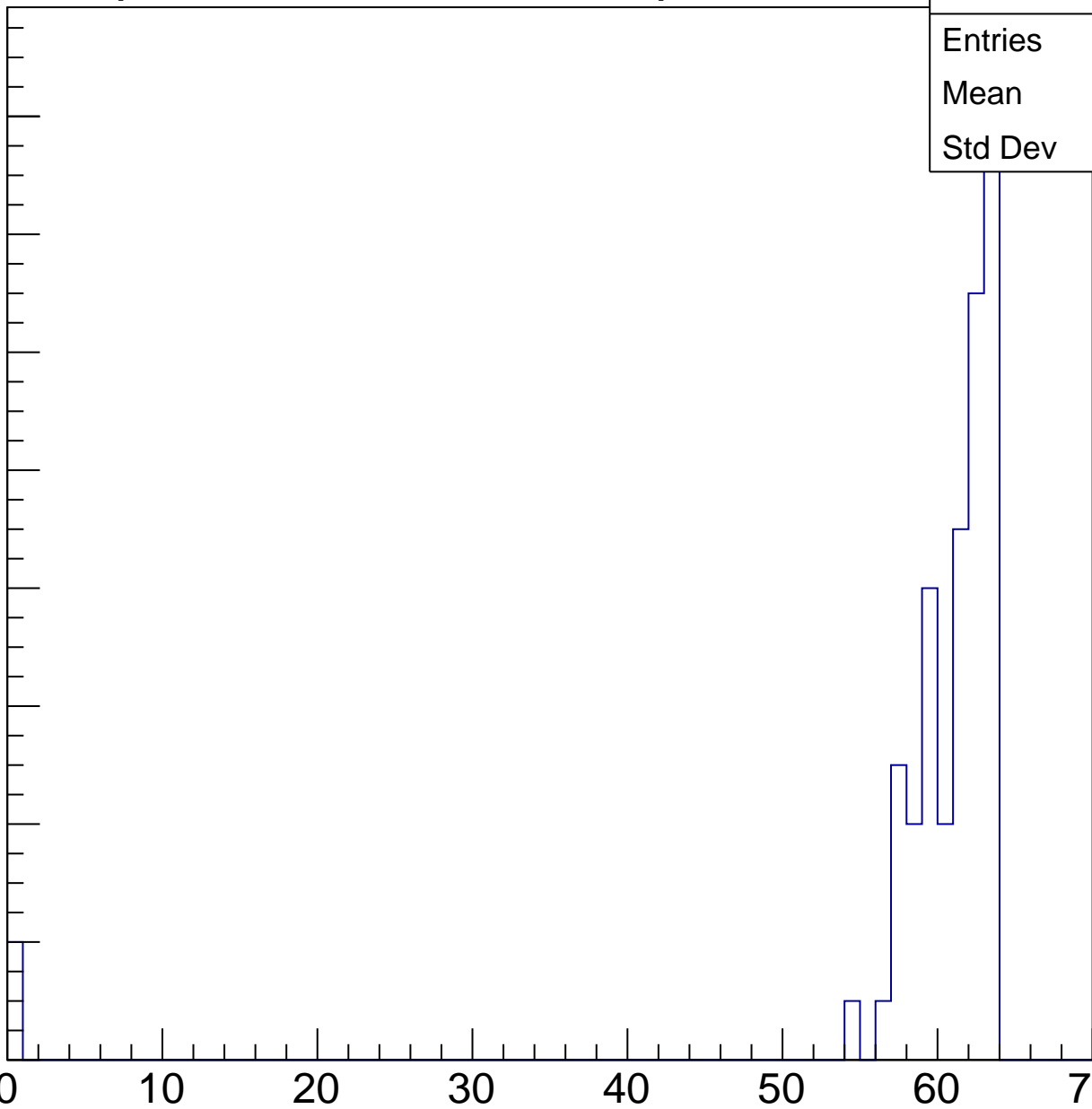
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	64
Mean	58.83
Std Dev	10.79

ampl



B0L101S, U19-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch56, adc0

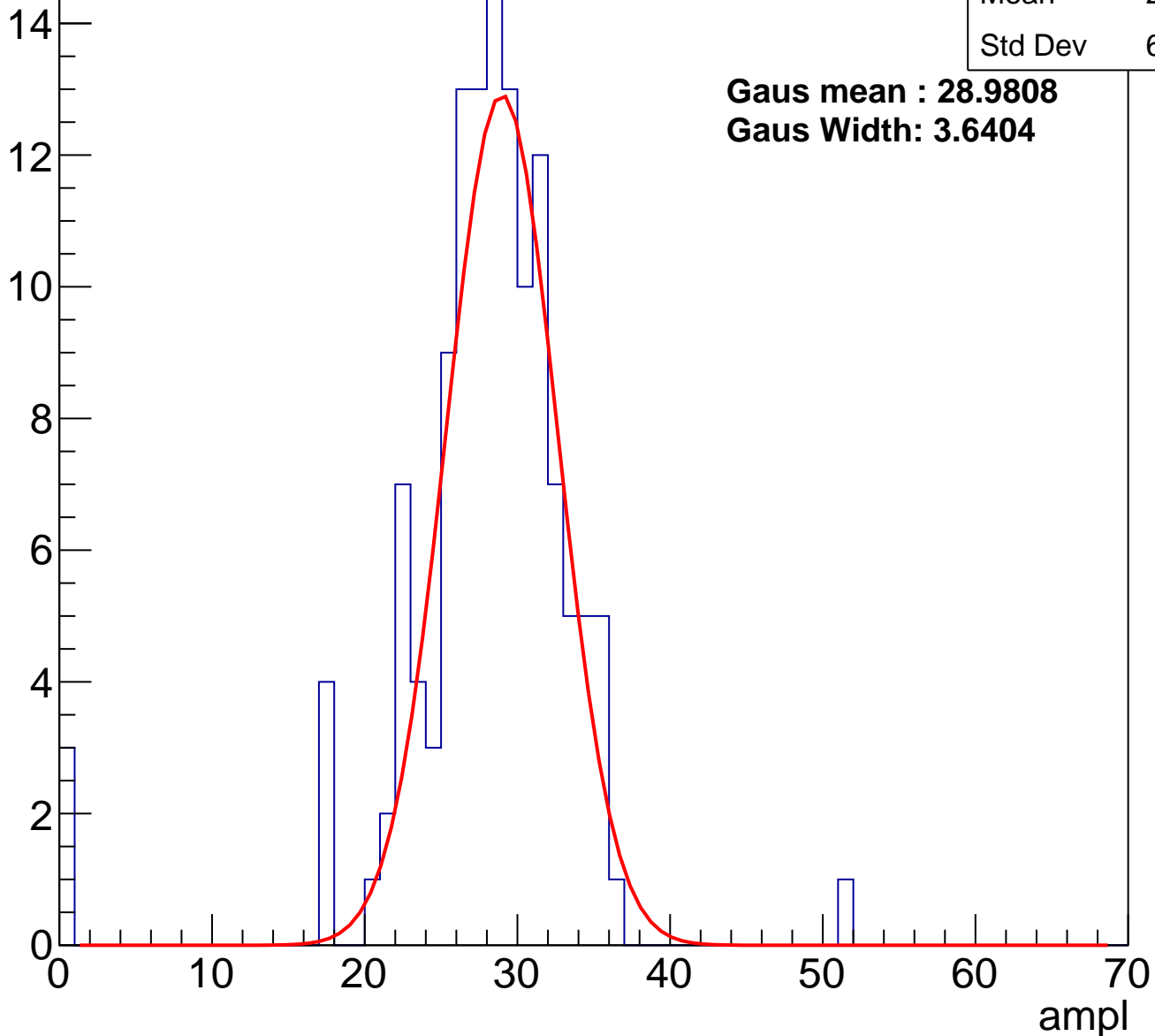
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	27.46
Std Dev	6.083

Gaus mean : 28.9808

Gaus Width: 3.6404

Entry



B0L101S, U19-ch56, adc1

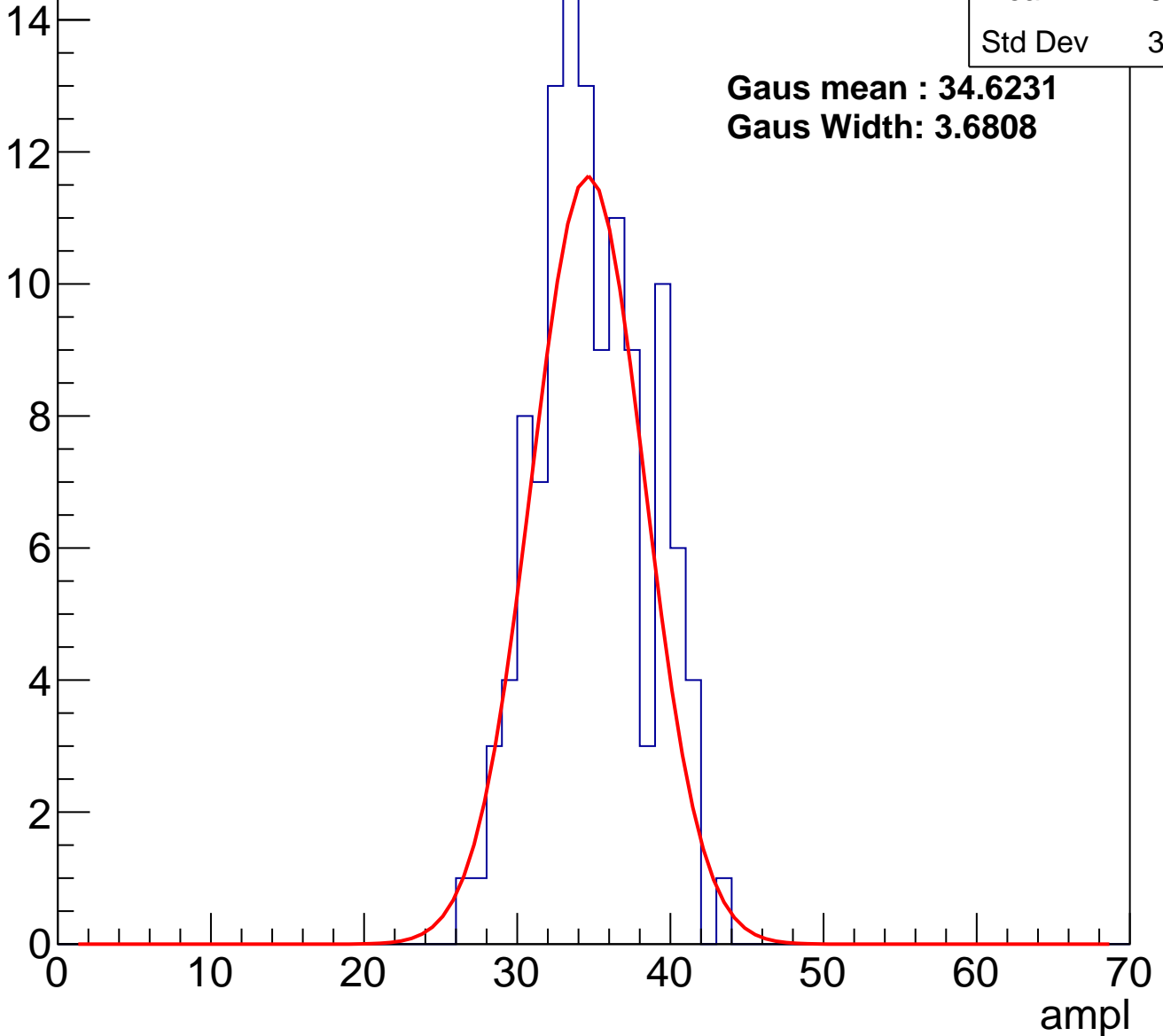
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	118
Mean	34.39
Std Dev	3.549

Gaus mean : 34.6231

Gaus Width: 3.6808



B0L101S, U19-ch56, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	40.74
Std Dev	3.828

Gaus mean : 41.0640

Gaus Width: 3.7518

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

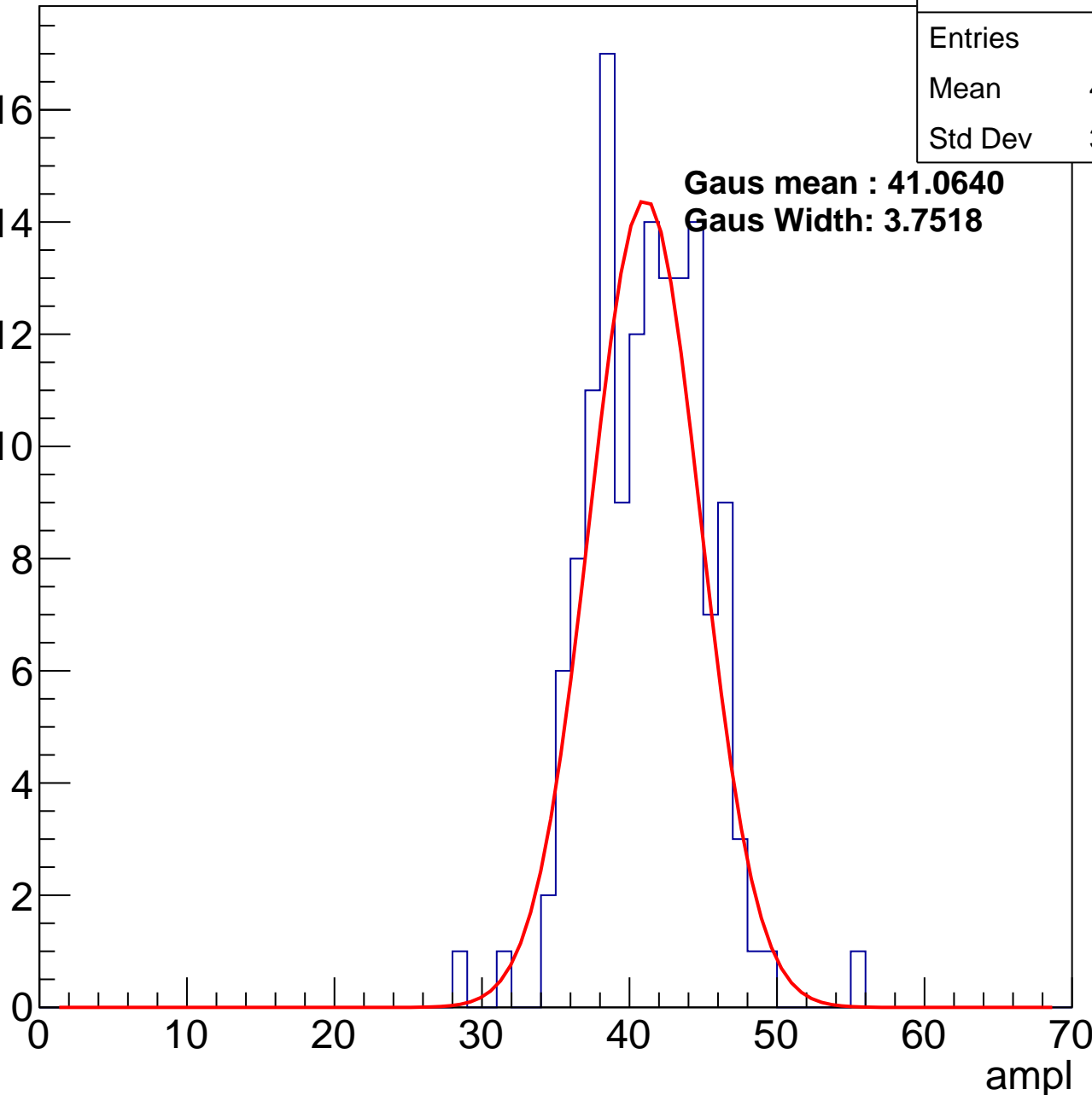
40

50

60

70

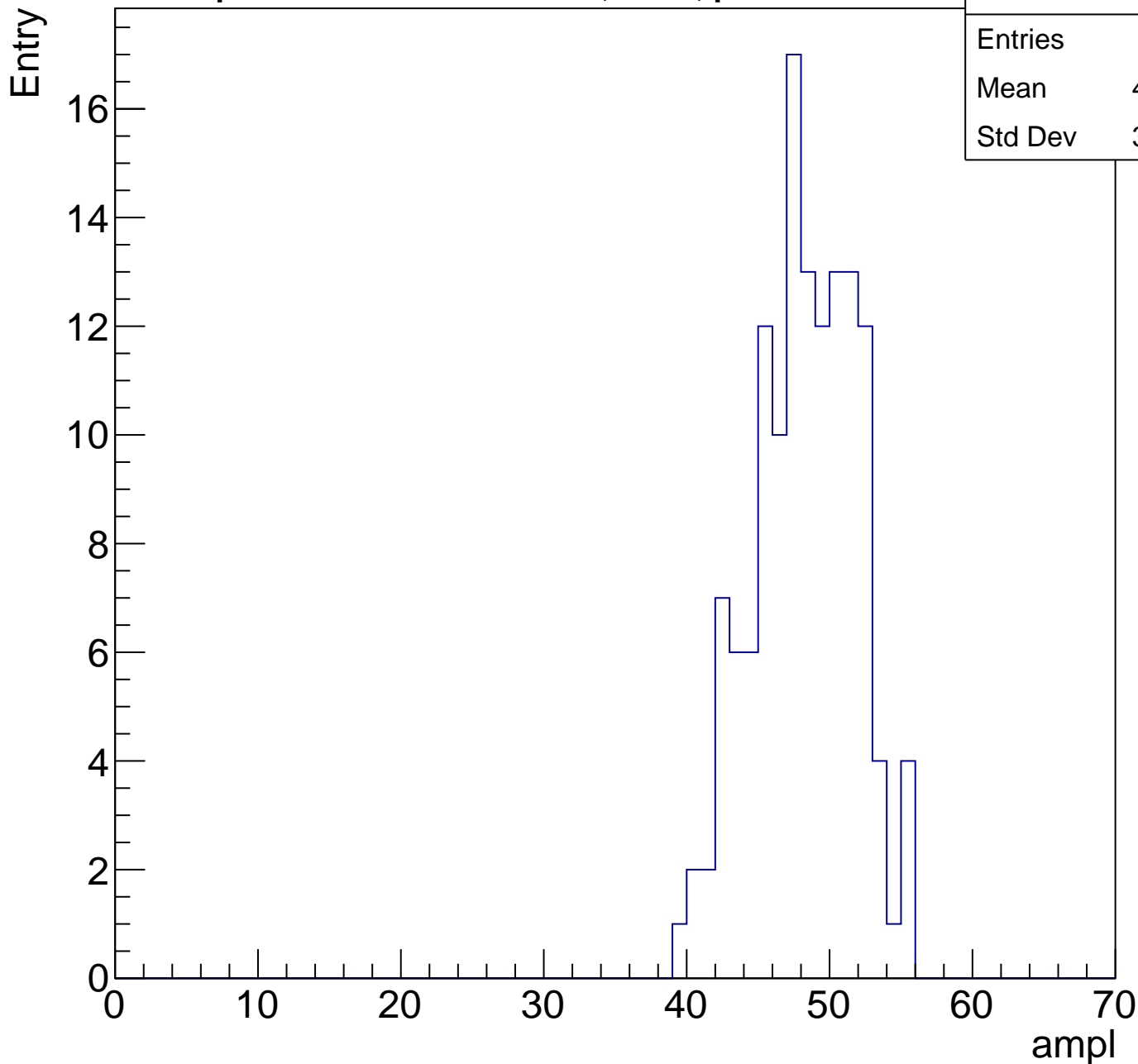
ampl



B0L101S, U19-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	47.79
Std Dev	3.516



B0L101S, U19-ch56, adc4

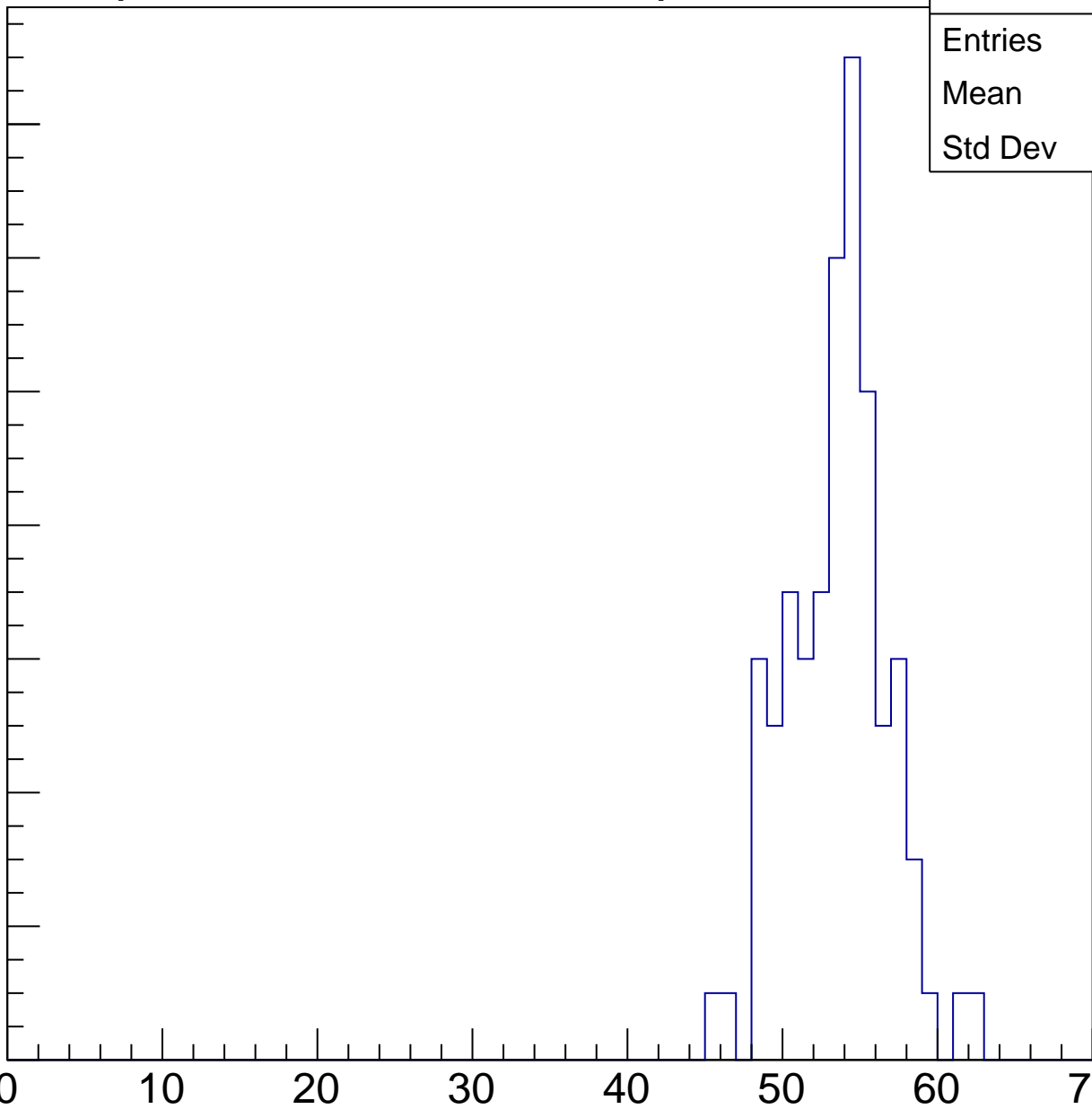
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	53.08
Std Dev	3.196

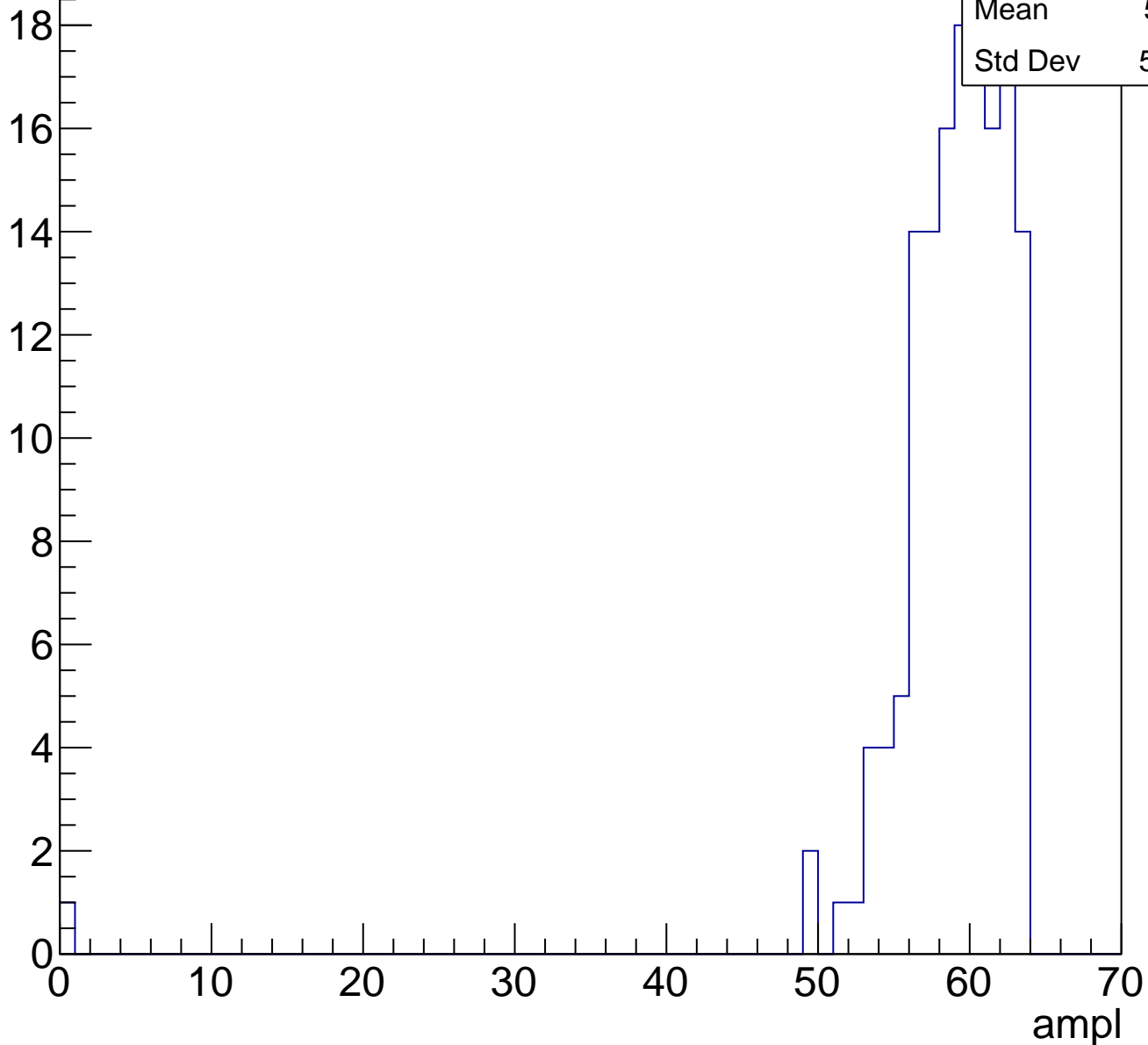
ampl



B0L101S, U19-ch56, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

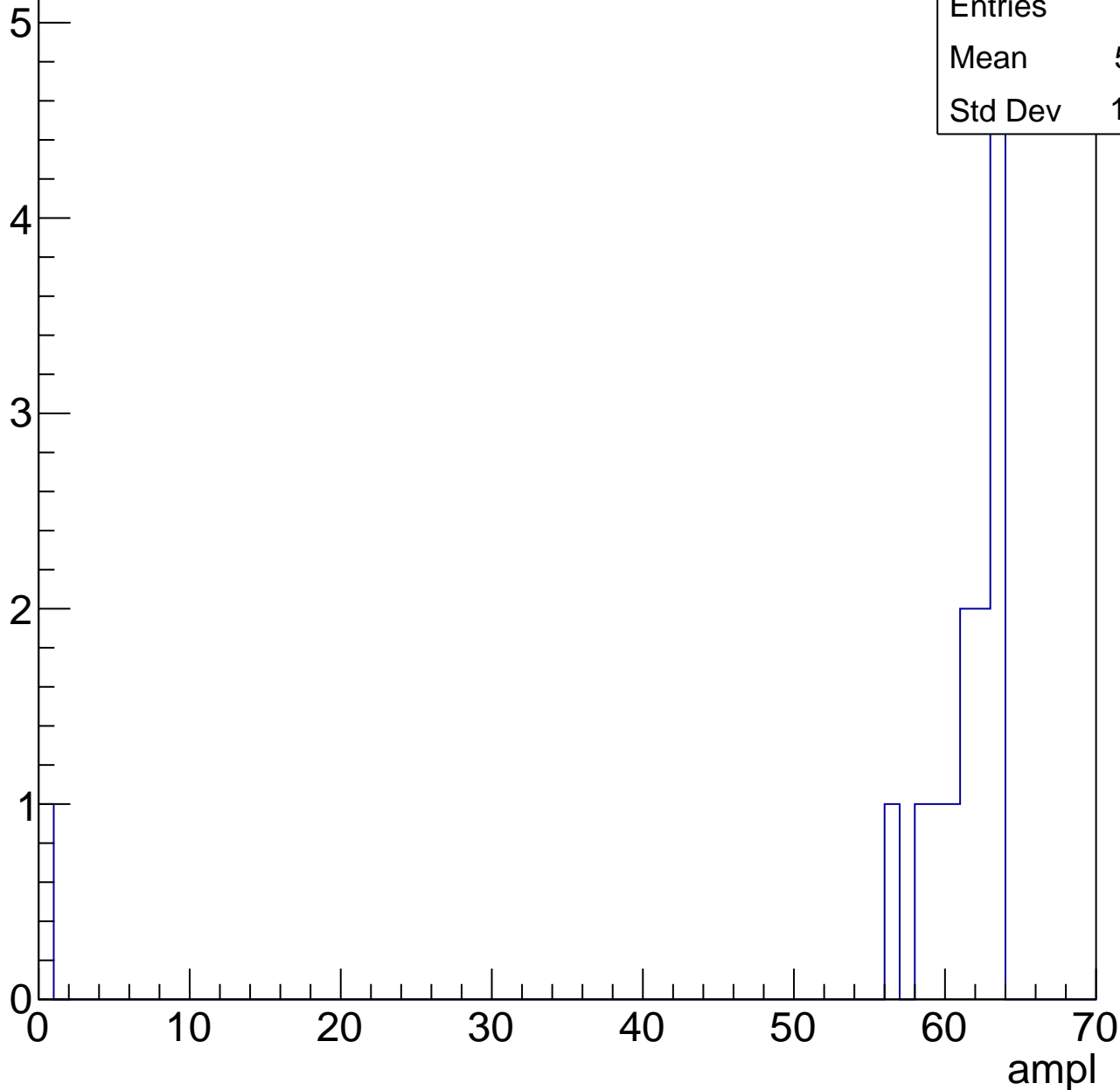


B0L101S, U19-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	56.71
Std Dev	15.87



B0L101S, U19-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch57, adc0

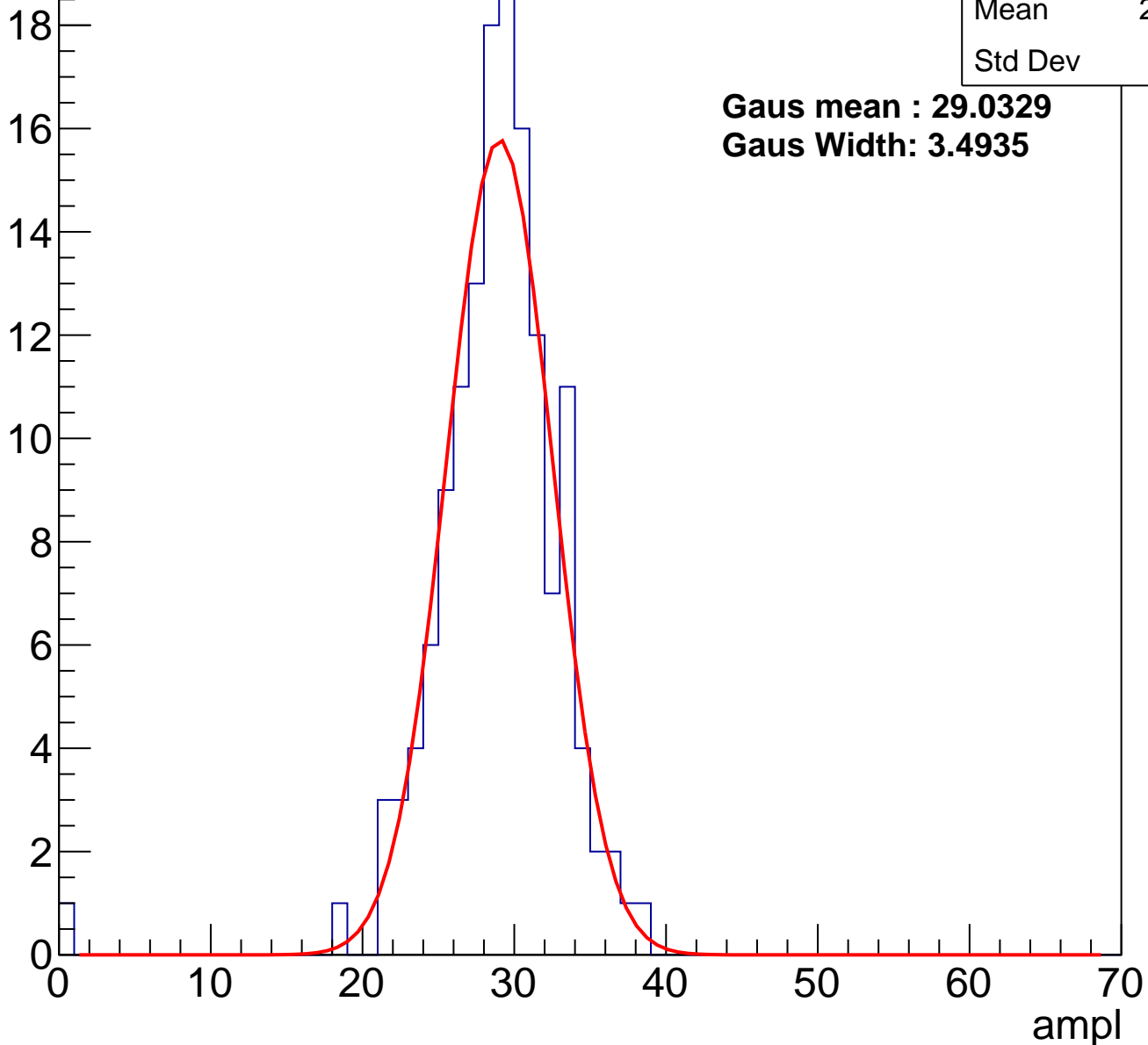
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	28.42
Std Dev	4.23

Gaus mean : 29.0329

Gaus Width: 3.4935

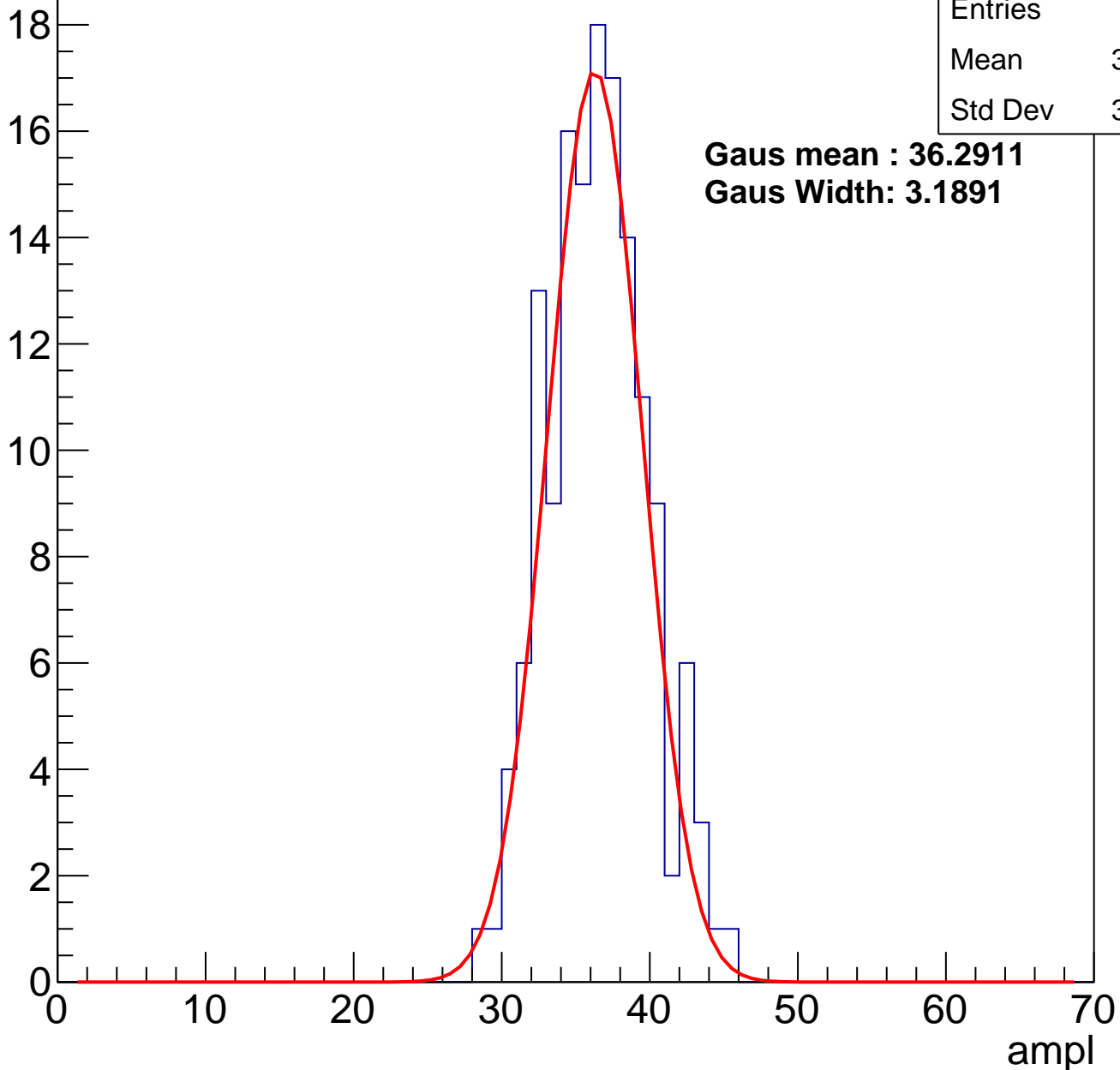
Entry



B0L101S, U19-ch57, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch57, adc2

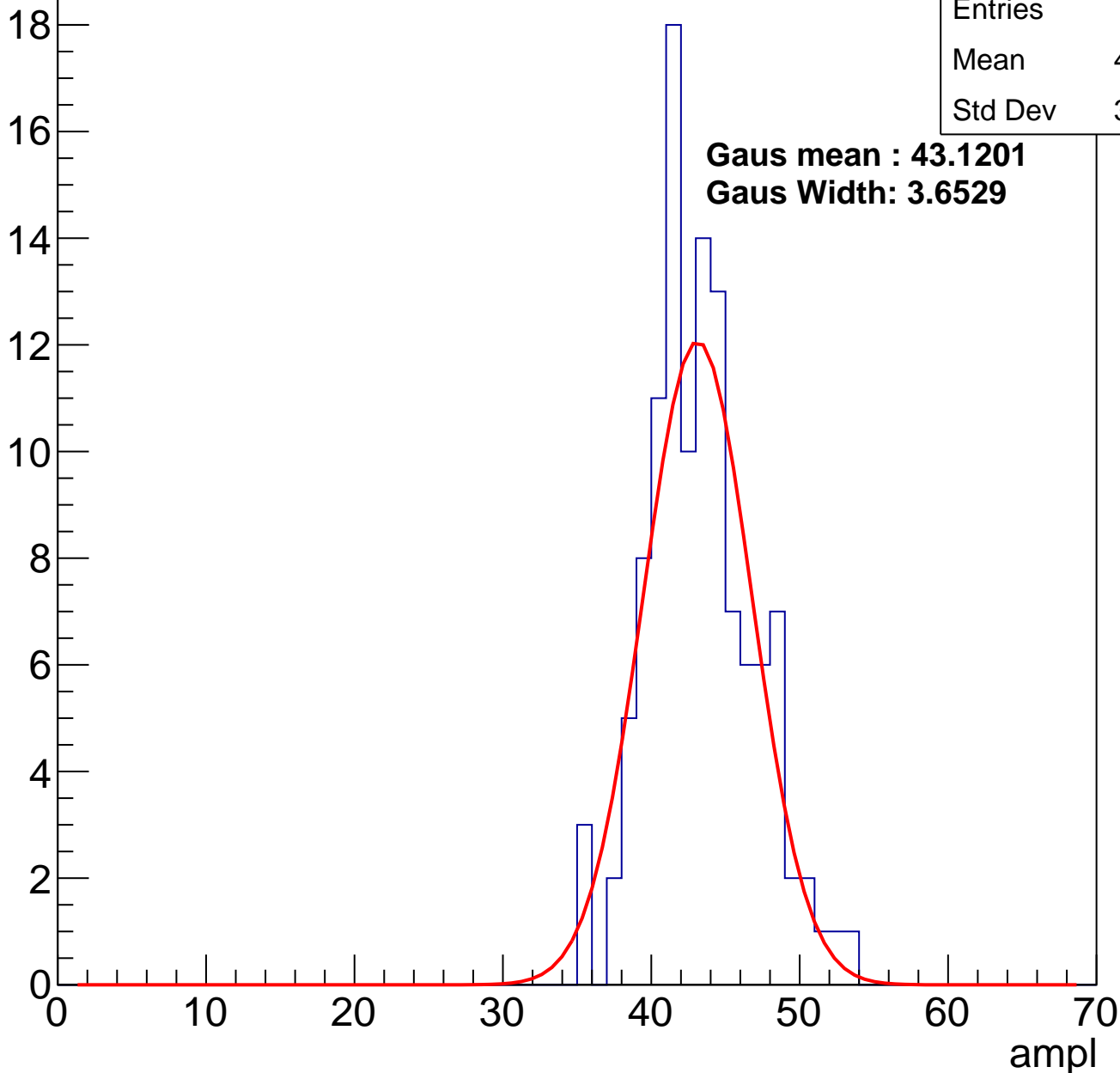
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	117
Mean	42.87
Std Dev	3.553

Gaus mean : 43.1201

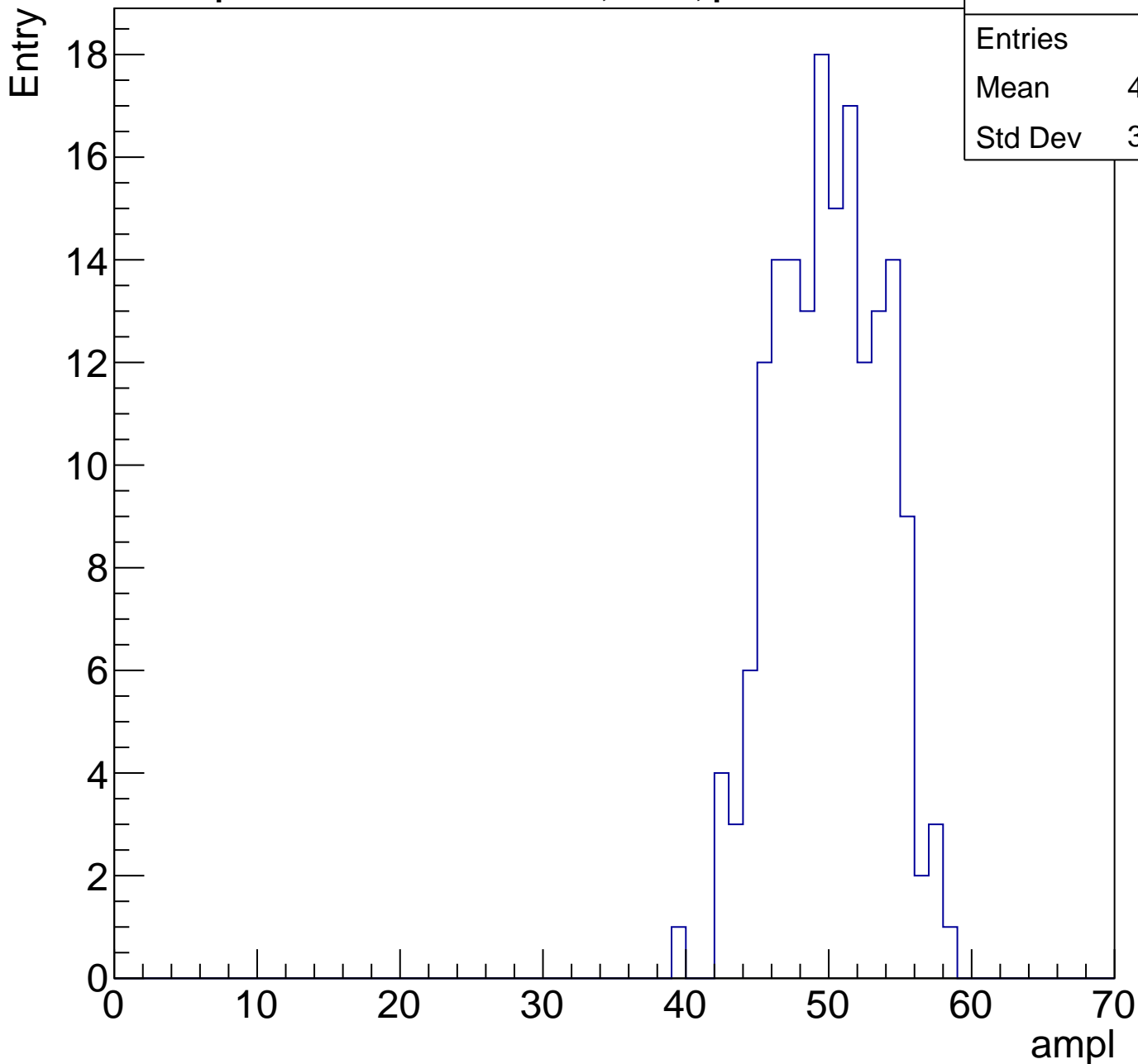
Gaus Width: 3.6529



B0L101S, U19-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	49.53
Std Dev	3.697

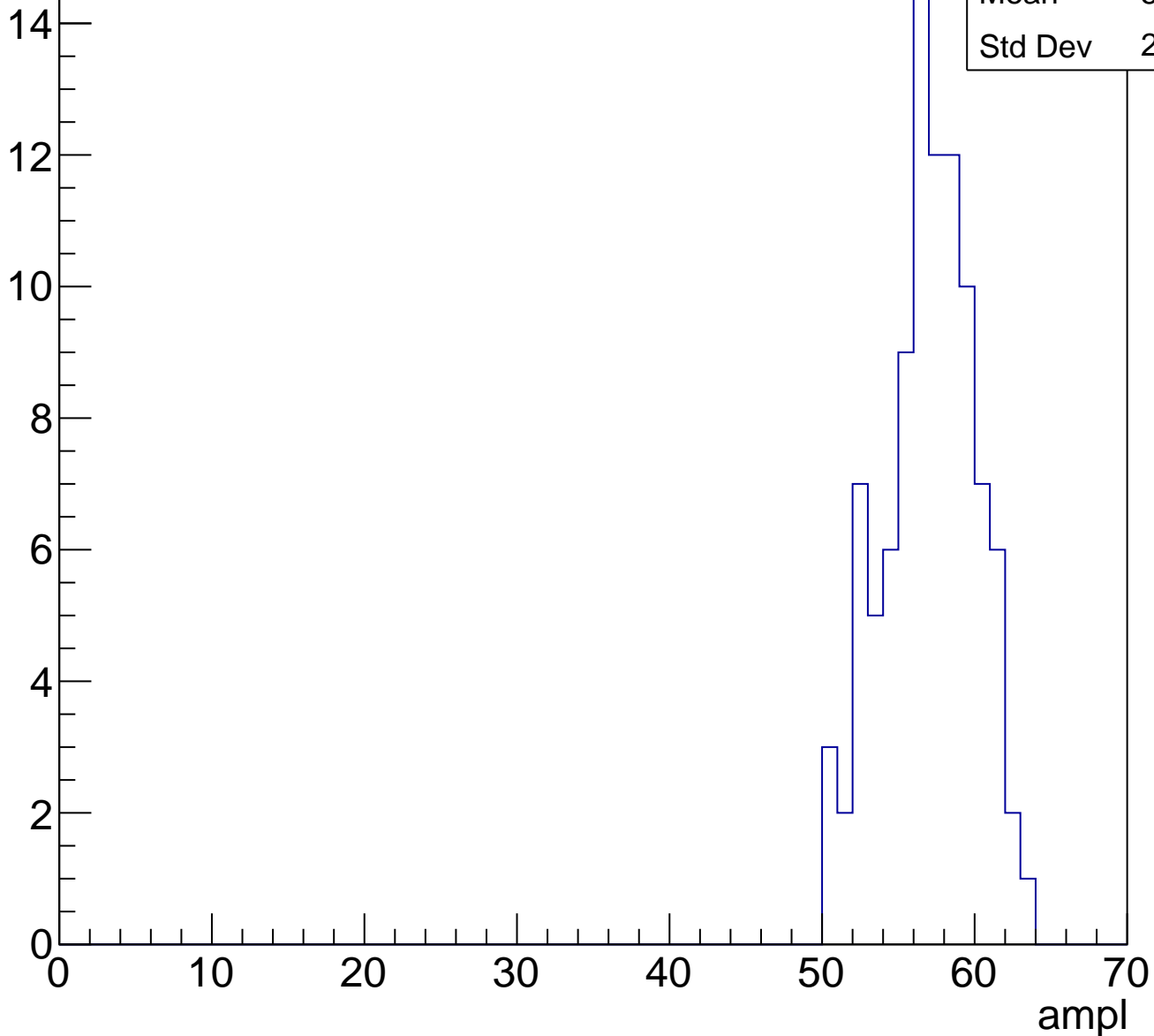


B0L101S, U19-ch57, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	56.53
Std Dev	2.967

Entry



B0L101S, U19-ch57, adc5

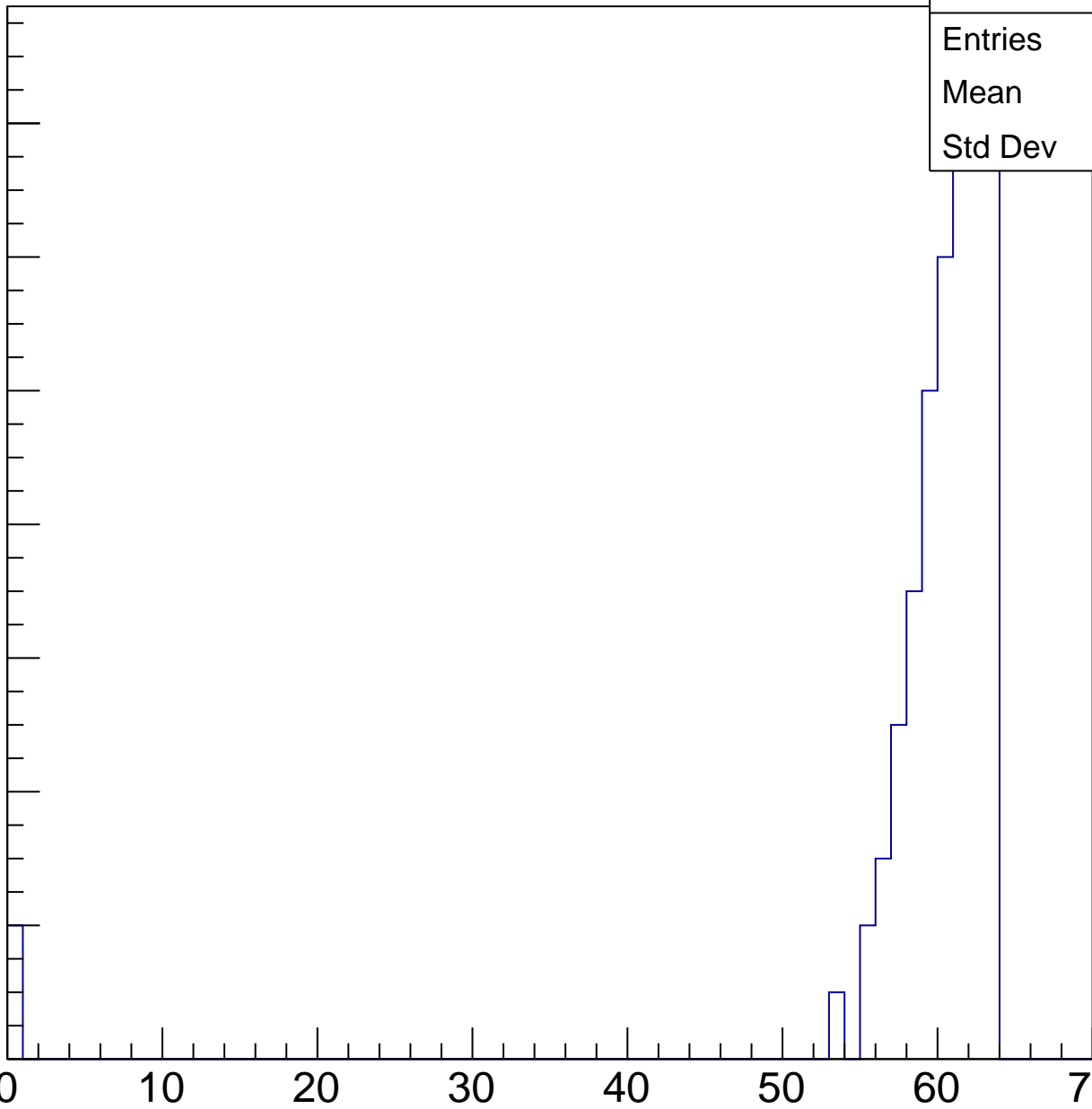
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	58.85
Std Dev	9.354

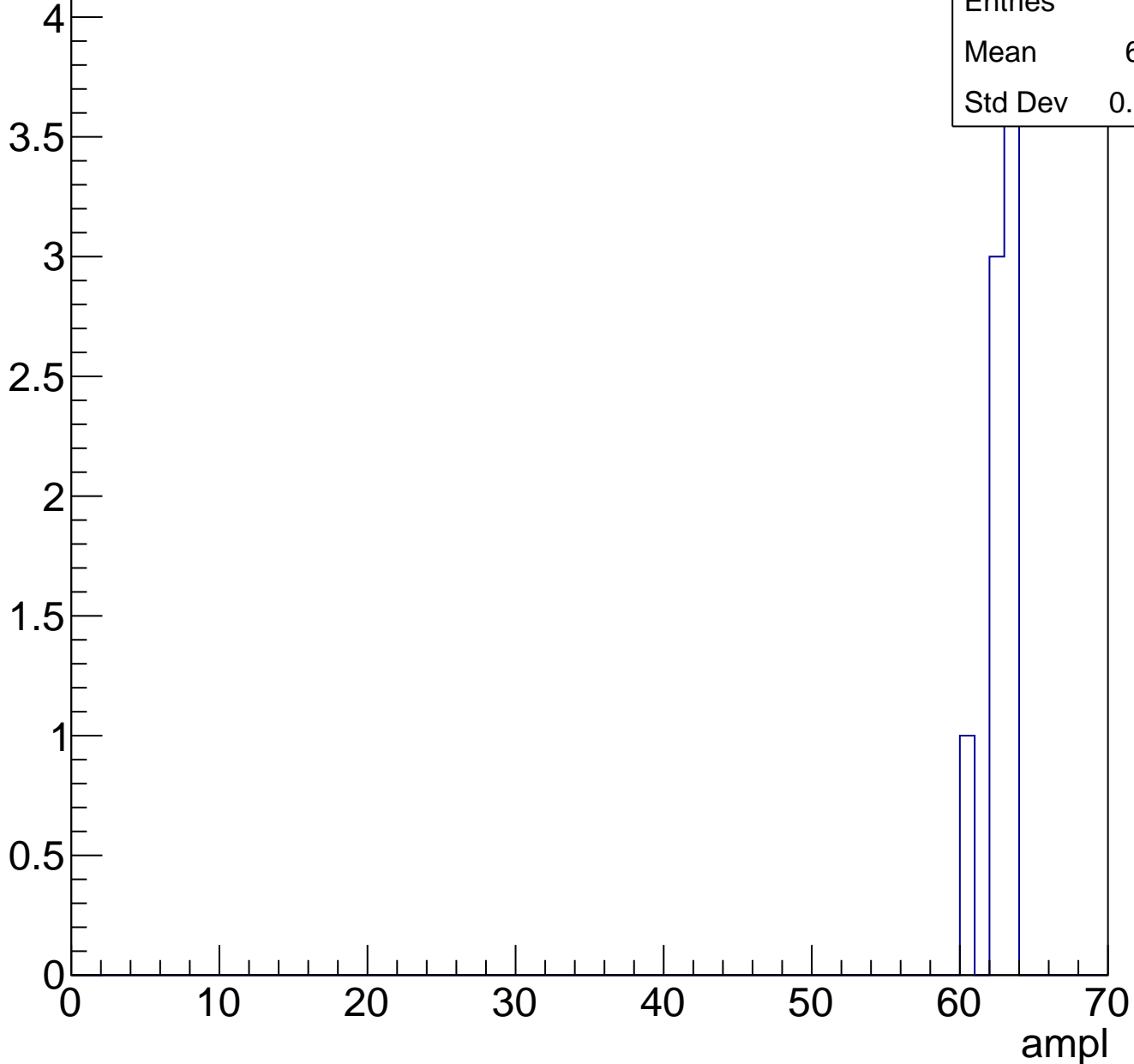
ampl



B0L101S, U19-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

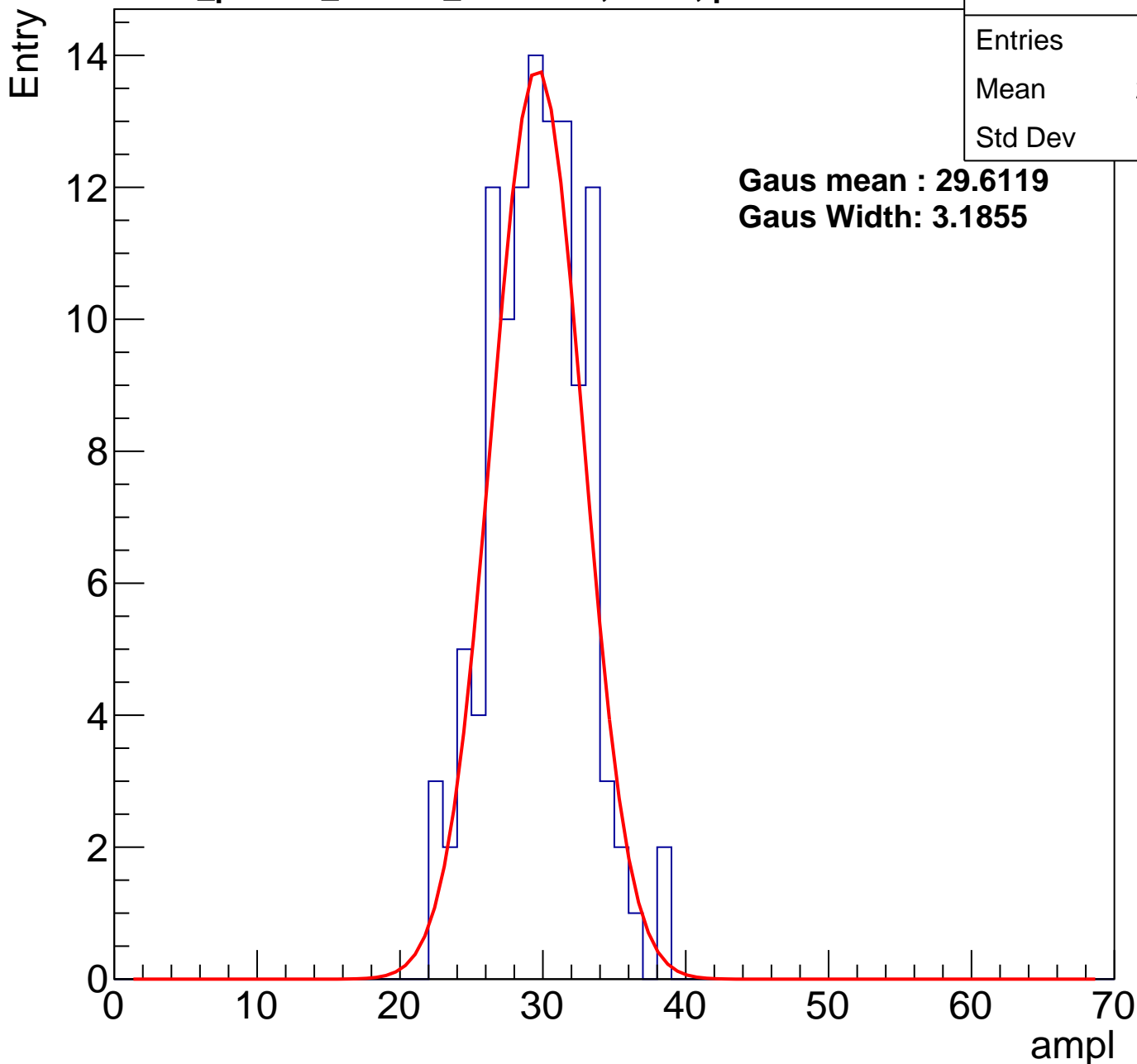
B0L101S, U19-ch58, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	29.21
Std Dev	3.28

Gaus mean : 29.6119

Gaus Width: 3.1855



B0L101S, U19-ch58, adc1

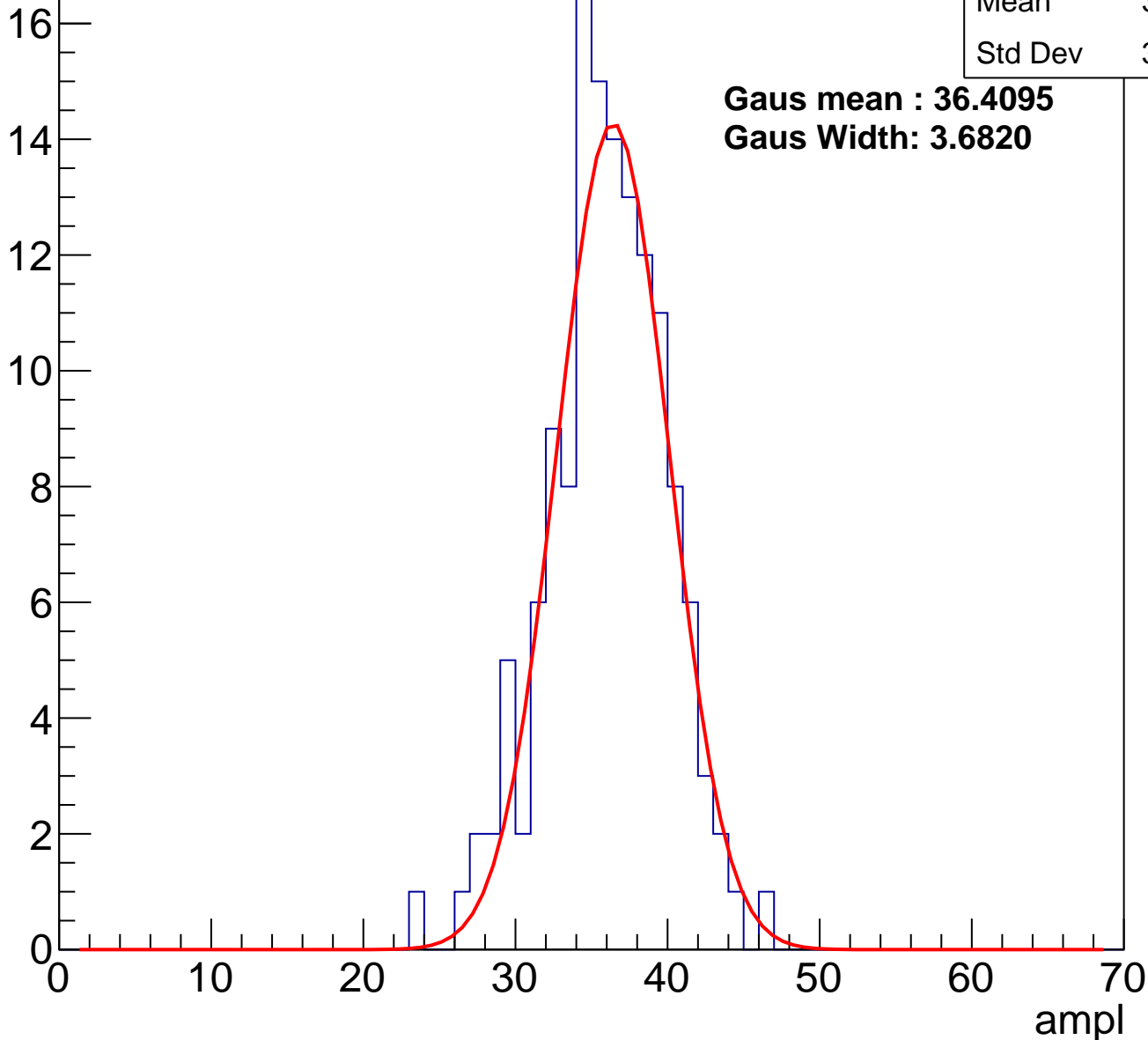
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	139
Mean	35.56
Std Dev	3.893

Gaus mean : 36.4095

Gaus Width: 3.6820



B0L101S, U19-ch58, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

42.36

Std Dev

3.647

Gaus mean : 42.7937

Gaus Width: 3.8194

0

10

20

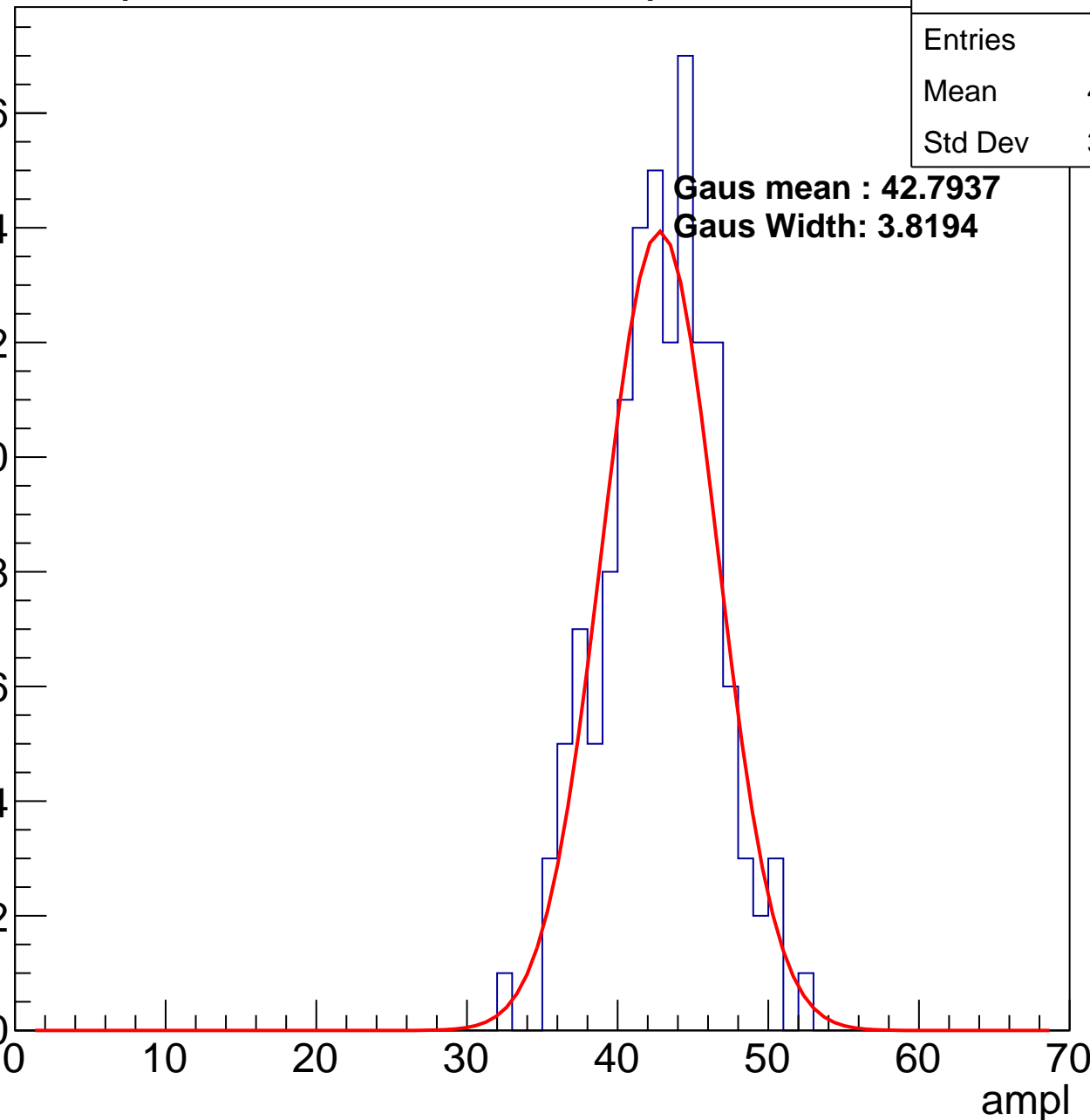
30

40

50

60

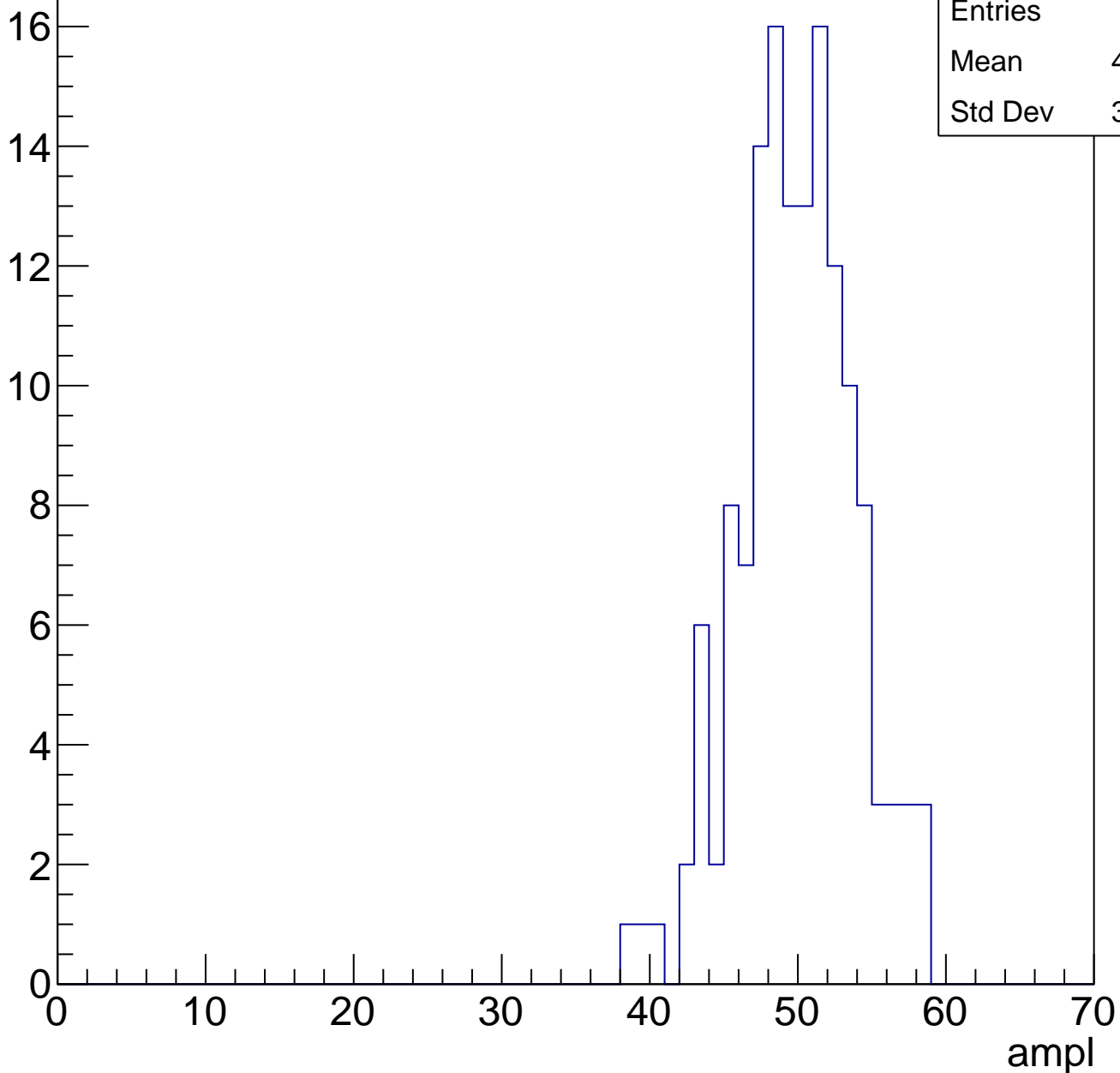
ampl



B0L101S, U19-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	142
Mean	49.45
Std Dev	3.878

B0L101S, U19-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

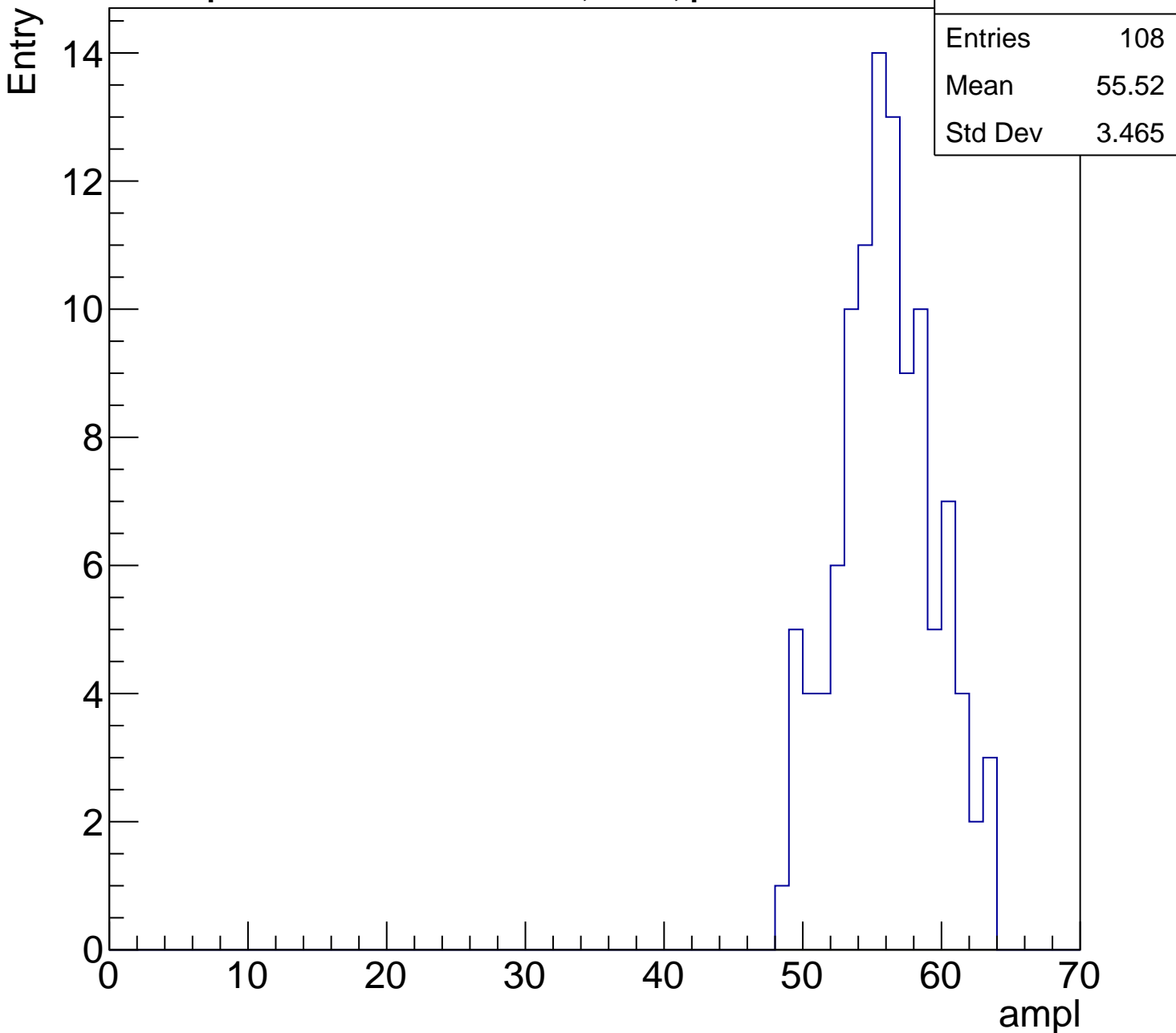
Entries	108
Mean	55.52
Std Dev	3.465

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch58, adc5

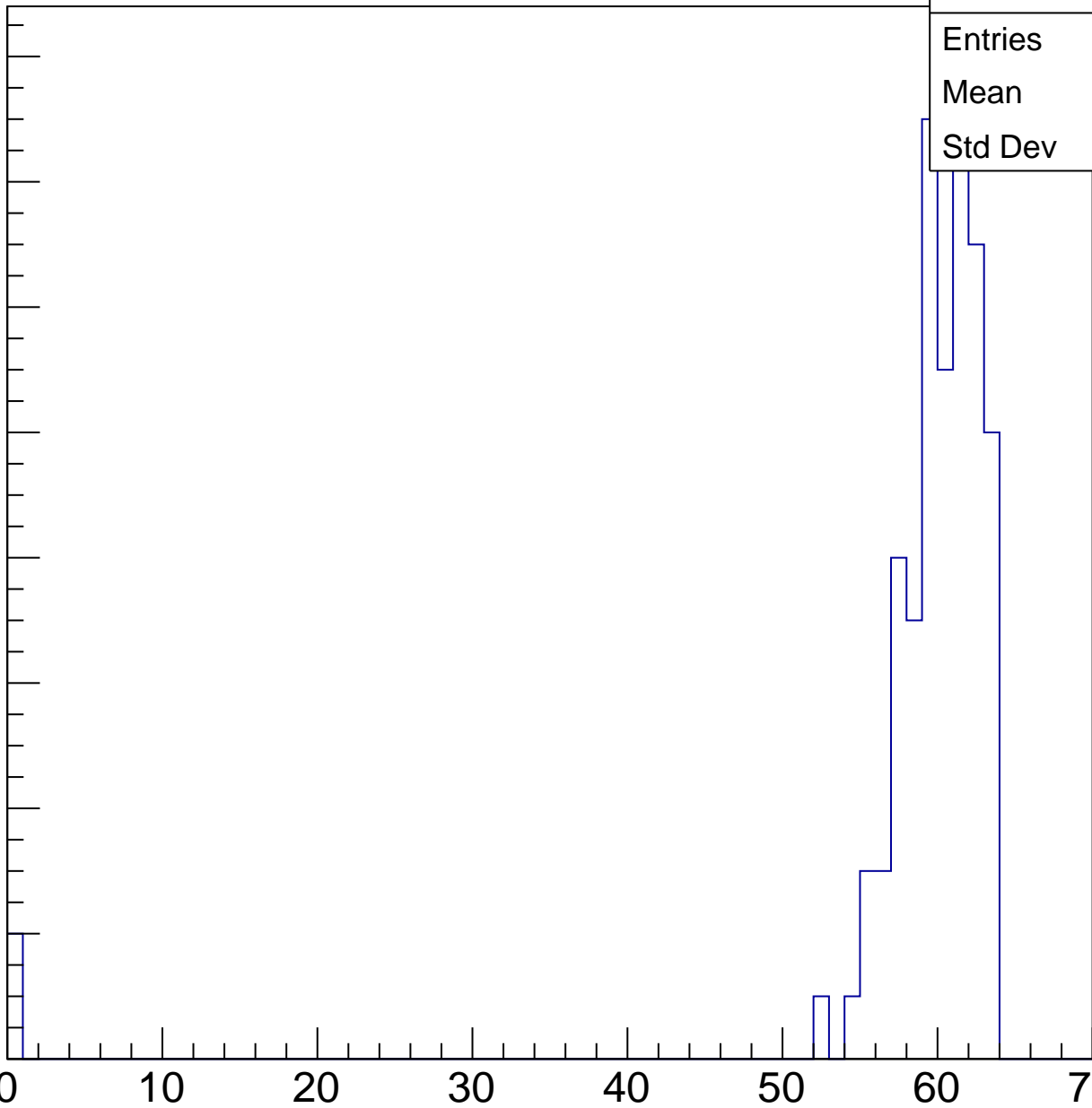
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	90
Mean	58.42
Std Dev	9.112

ampl

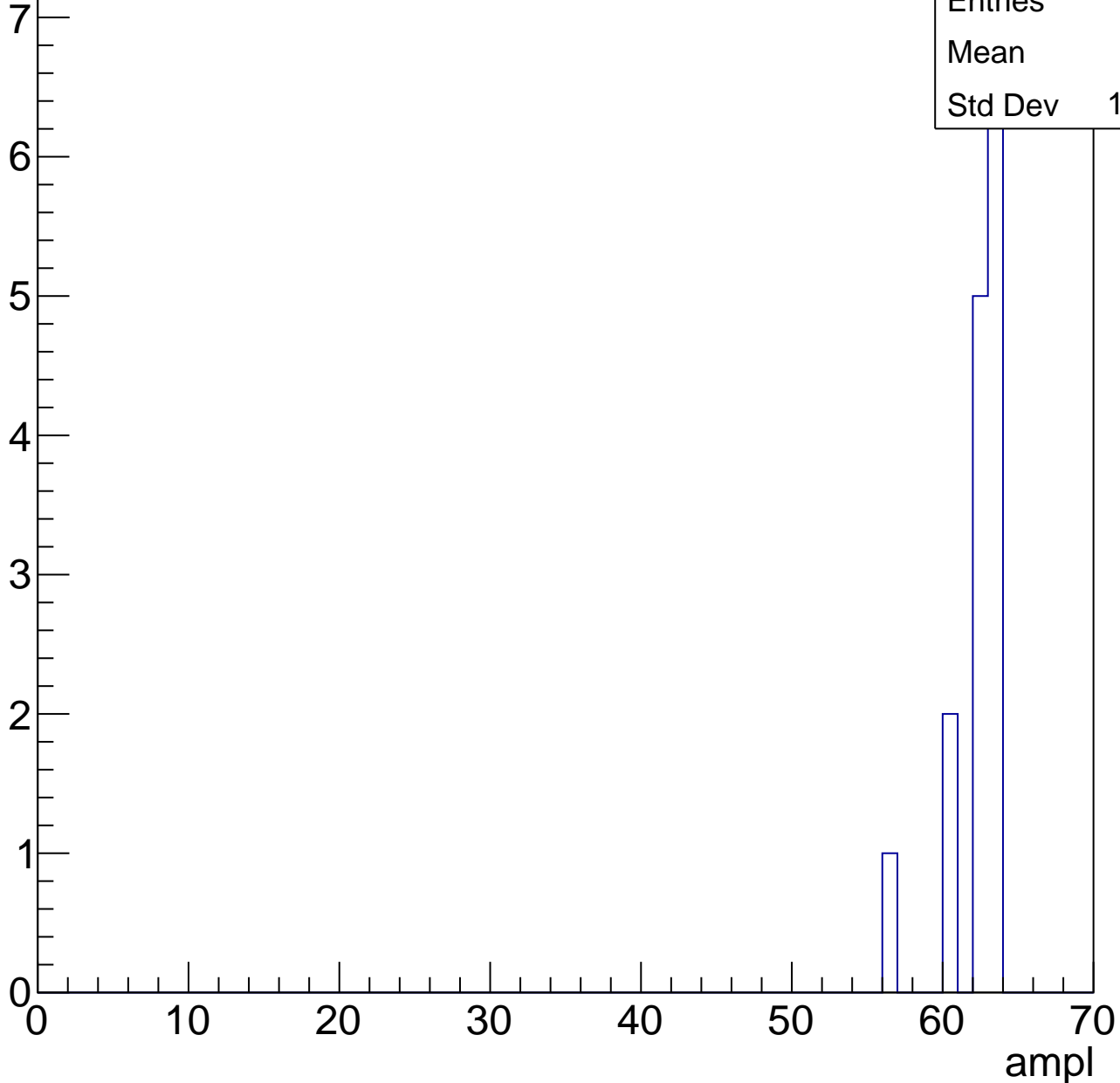


B0L101S, U19-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.8
Std Dev	1.833



B0L101S, U19-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

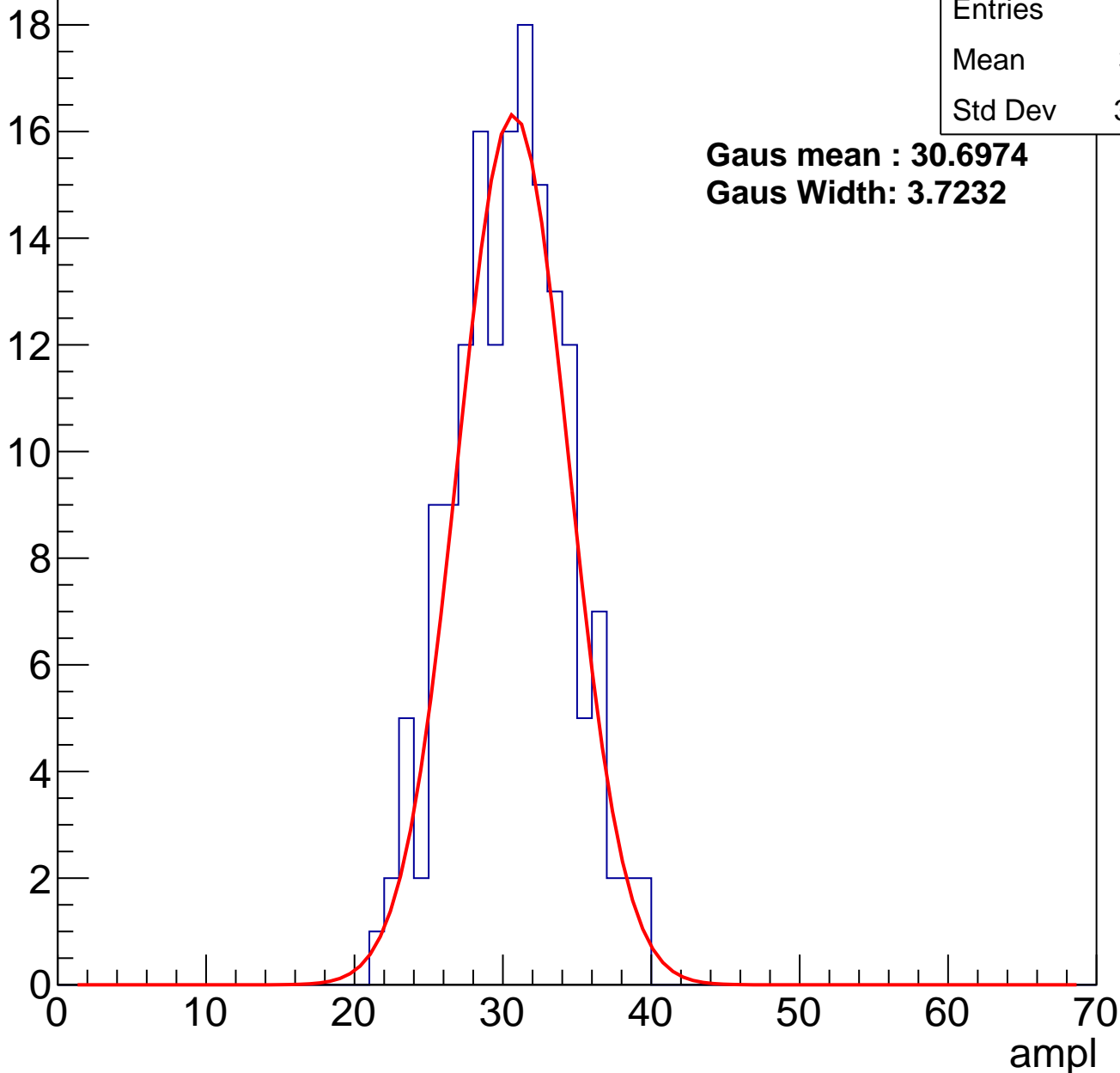
Entry



B0L101S, U19-ch59, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	160
Mean	30.11
Std Dev	3.733

B0L101S, U19-ch59, adc1

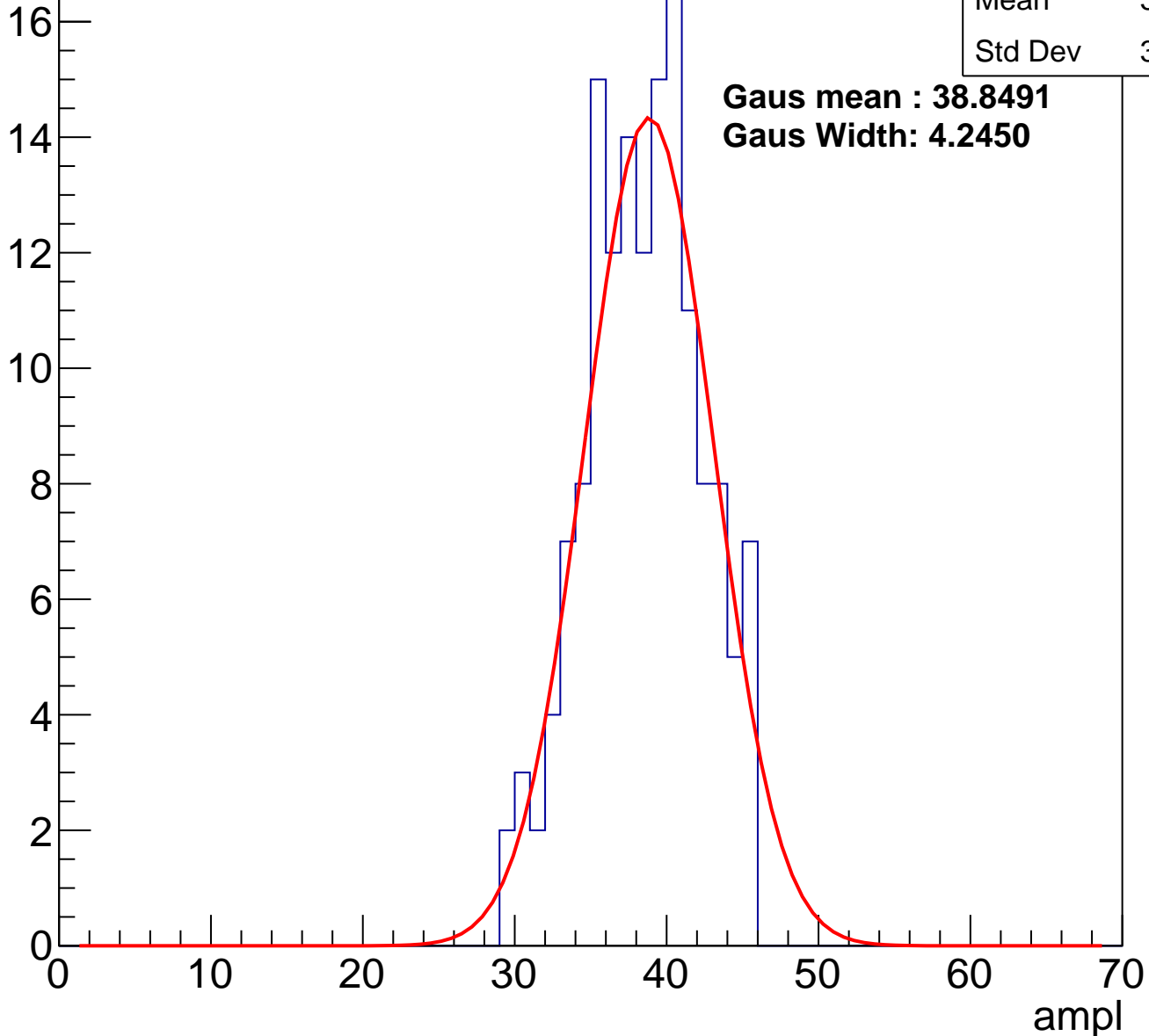
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	38.02
Std Dev	3.767

Gaus mean : 38.8491

Gaus Width: 4.2450

Entry



B0L101S, U19-ch59, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

114

Mean

44.11

Std Dev

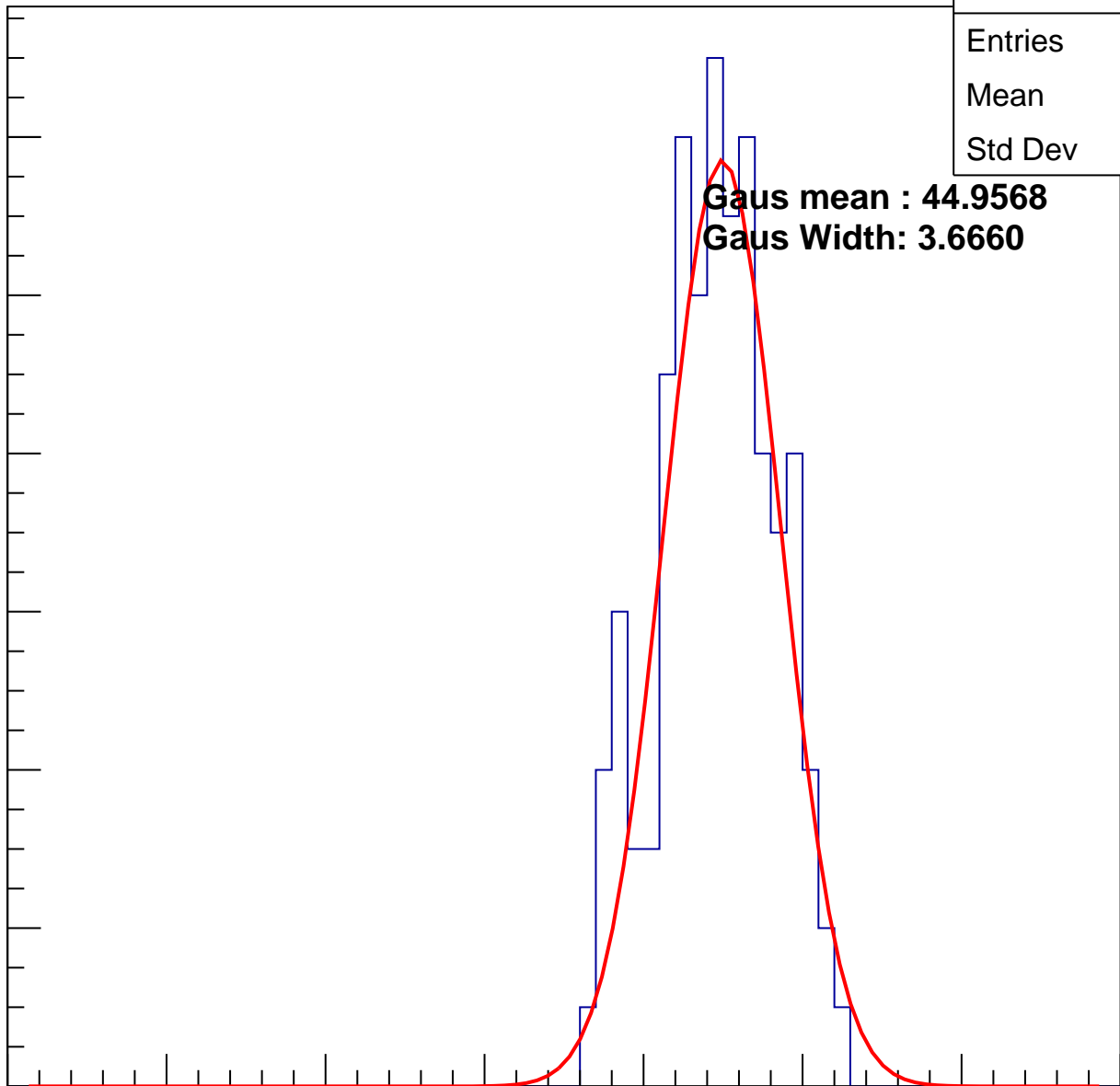
3.61

Gaus mean : 44.9568

Gaus Width: 3.6660

0 10 20 30 40 50 60 70

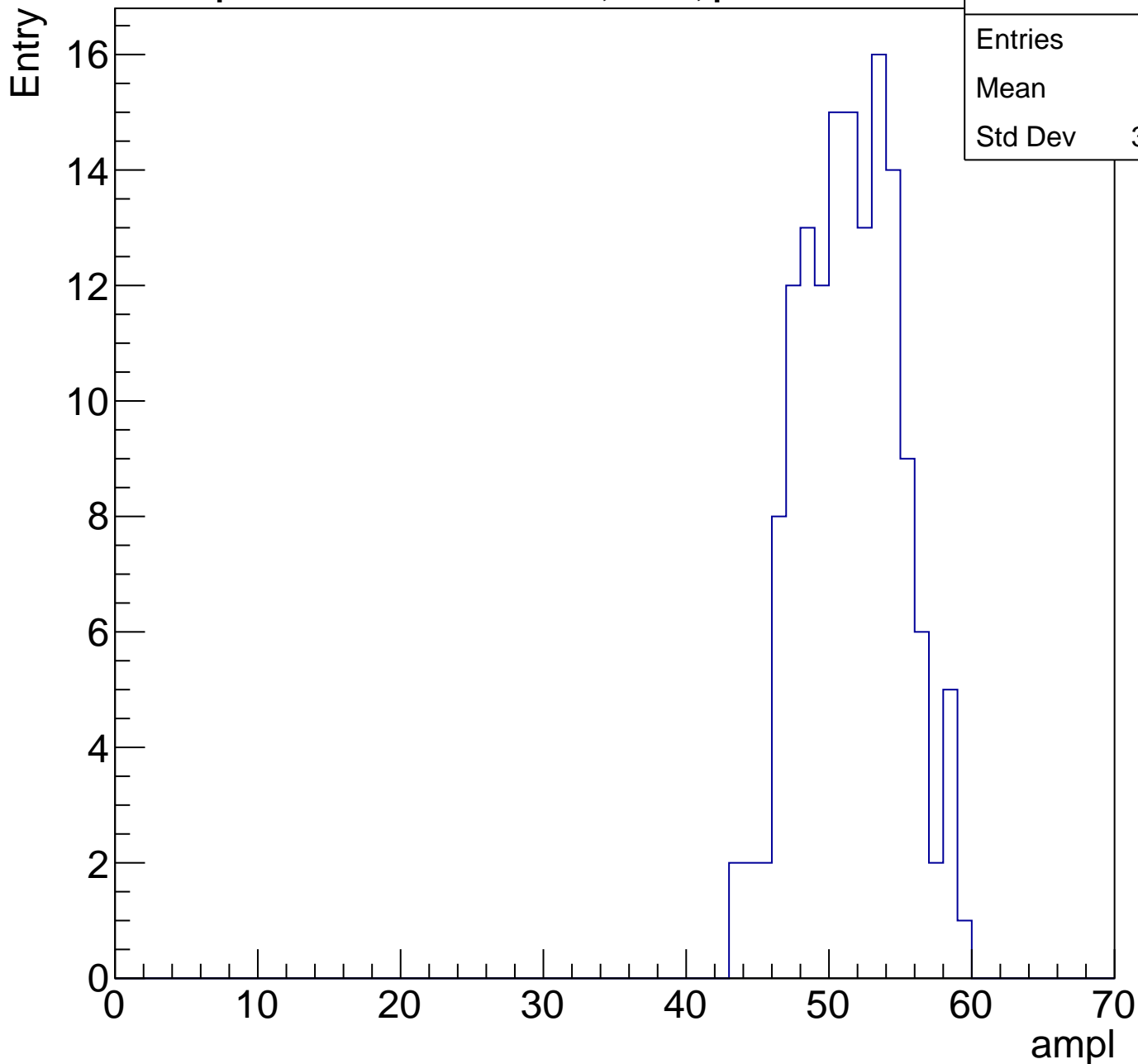
ampl



B0L101S, U19-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	51
Std Dev	3.448



B0L101S, U19-ch59, adc4

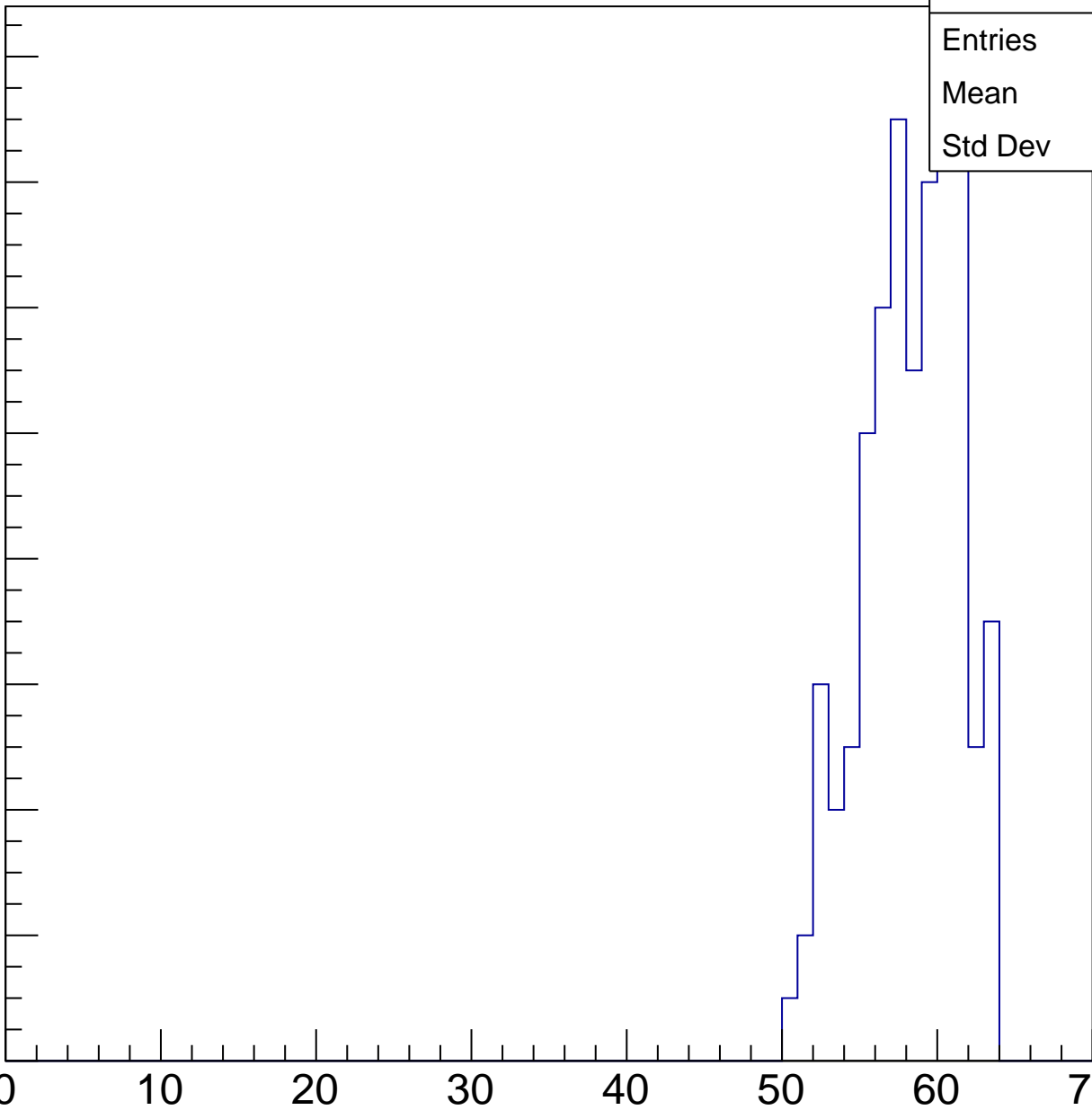
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	57.85
Std Dev	3.092

ampl



B0L101S, U19-ch59, adc5

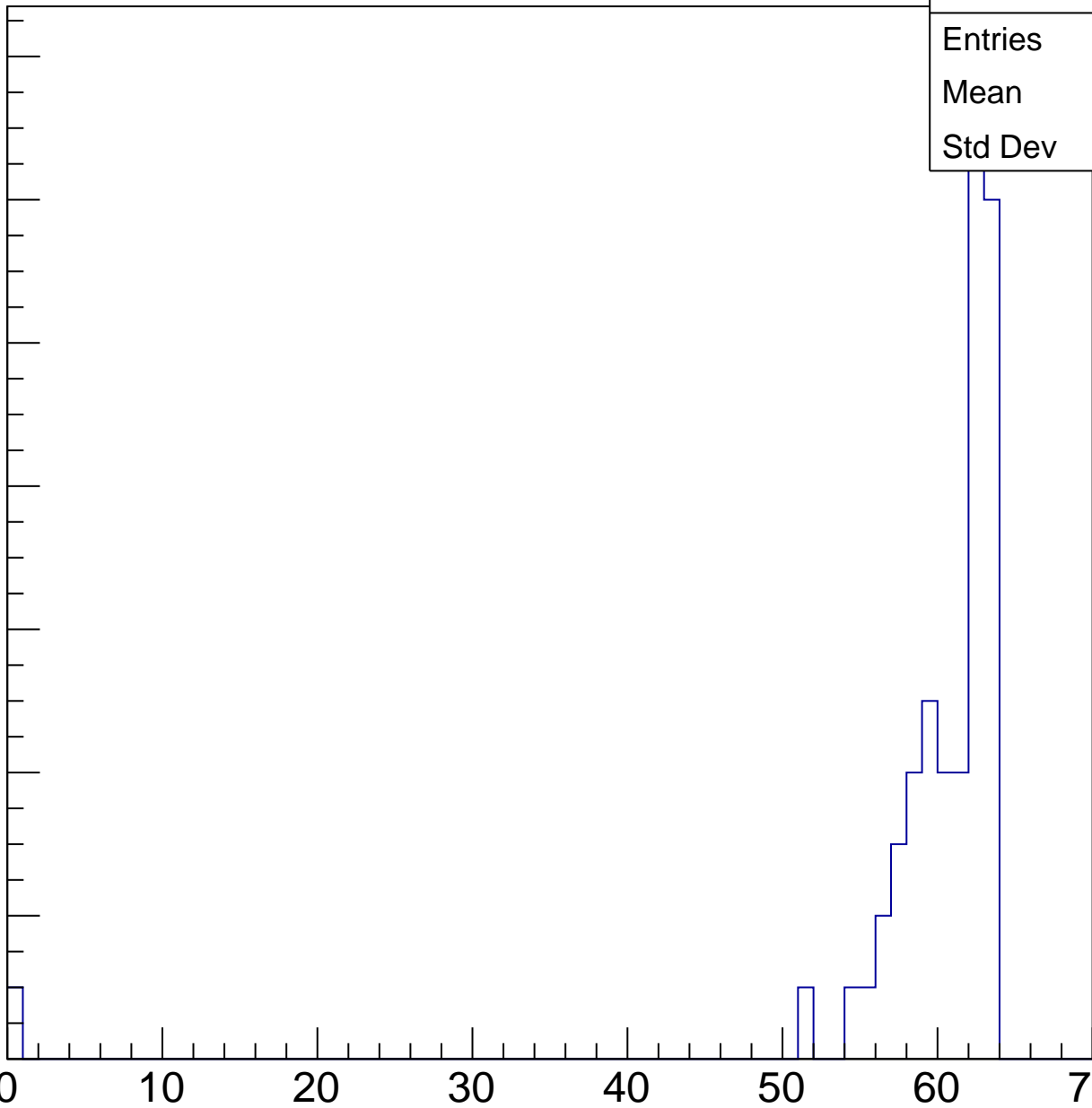
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	52
Mean	59.19
Std Dev	8.722

ampl



B0L101S, U19-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch60, adc0

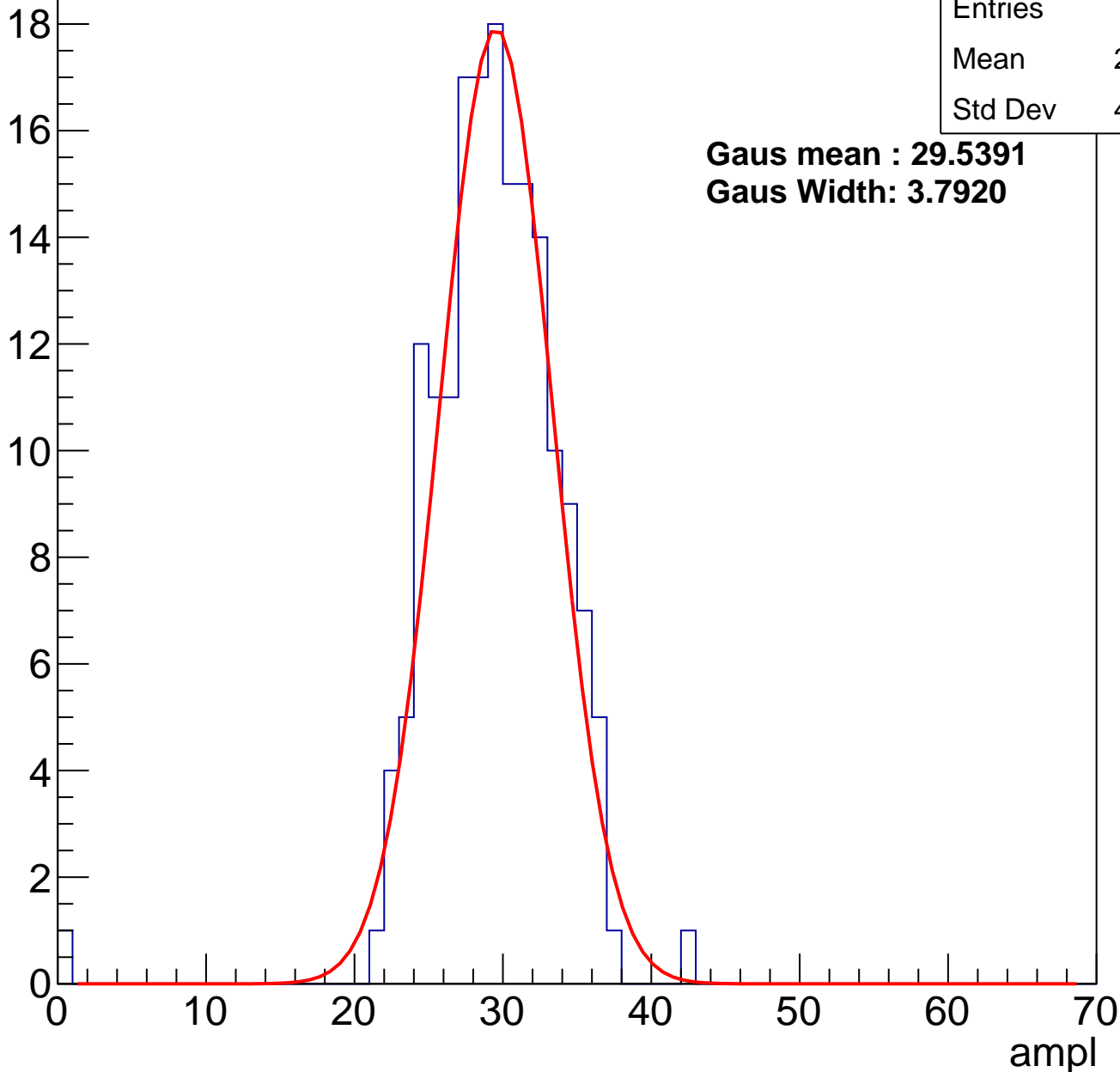
calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	28.93
Std Dev	4.326

Gaus mean : 29.5391

Gaus Width: 3.7920

Entry



B0L101S, U19-ch60, adc1

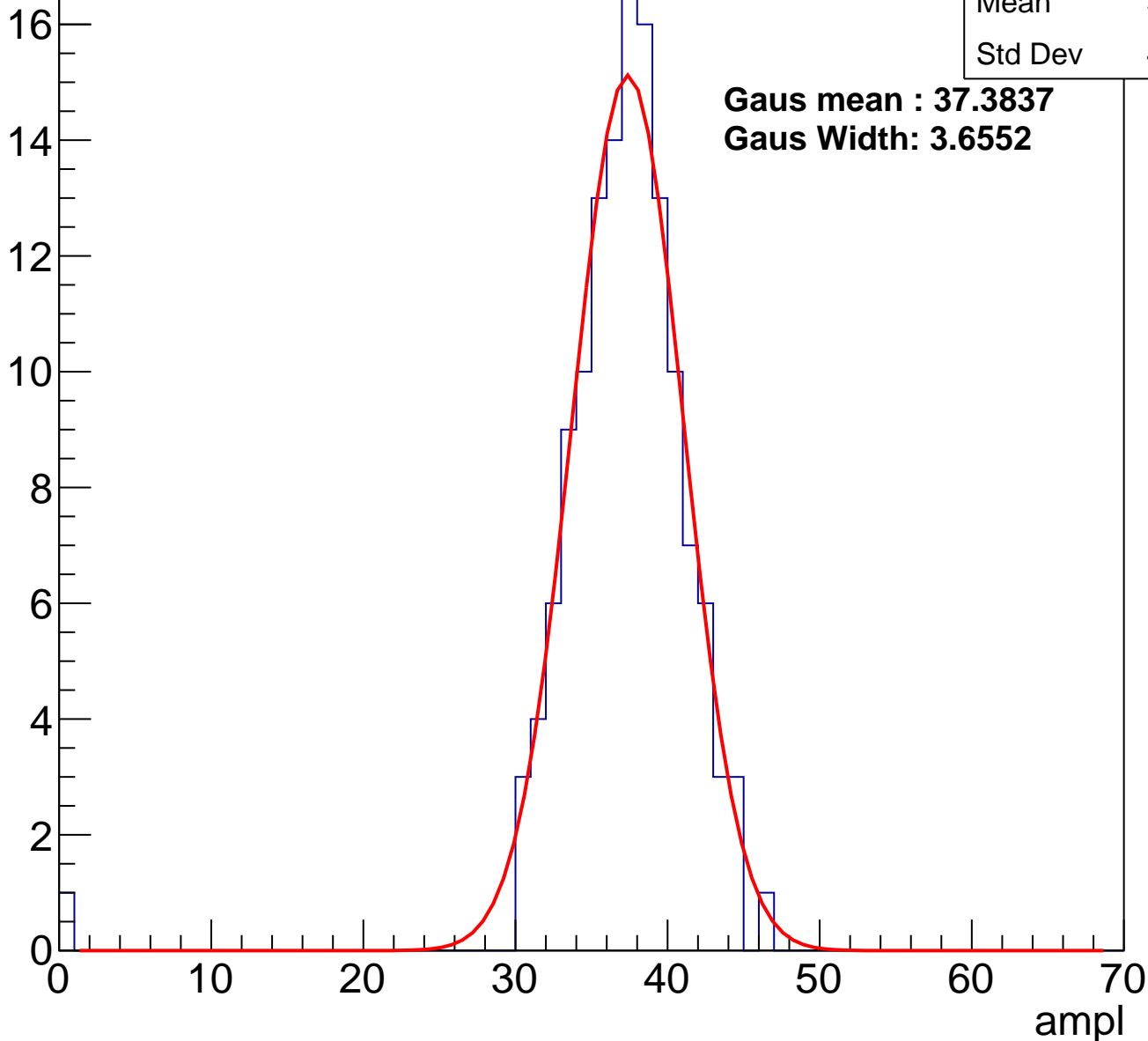
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	136
Mean	36.71
Std Dev	4.581

Gaus mean : 37.3837

Gaus Width: 3.6552



B0L101S, U19-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

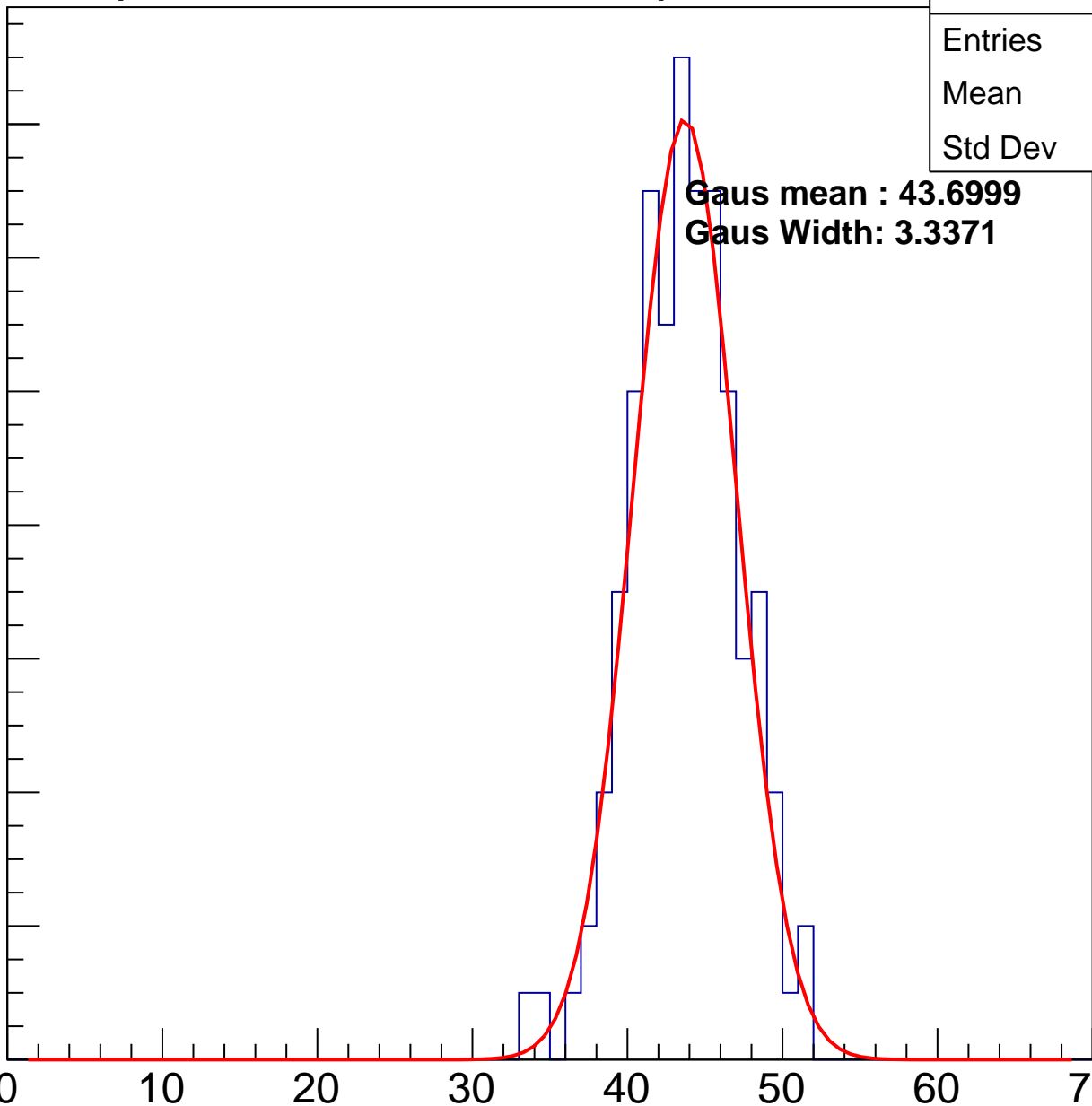
14
12
10
8
6
4
2
0

Entries	121
Mean	43.18
Std Dev	3.414

Gaus mean : 43.6999

Gaus Width: 3.3371

0 10 20 30 40 50 60 70
ampl



B0L101S, U19-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

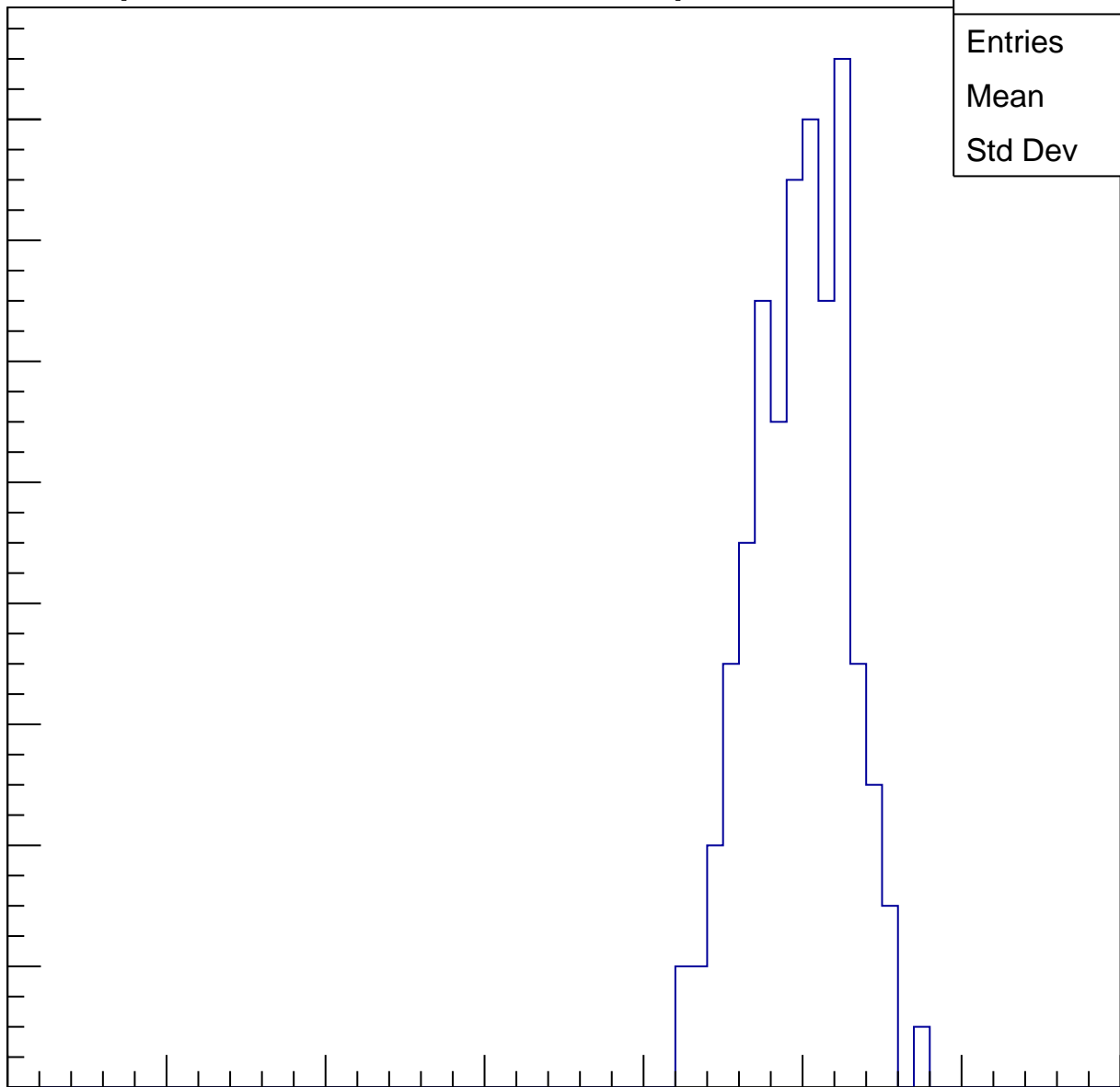
Entries	125
Mean	49.27
Std Dev	3.029

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch60, adc4

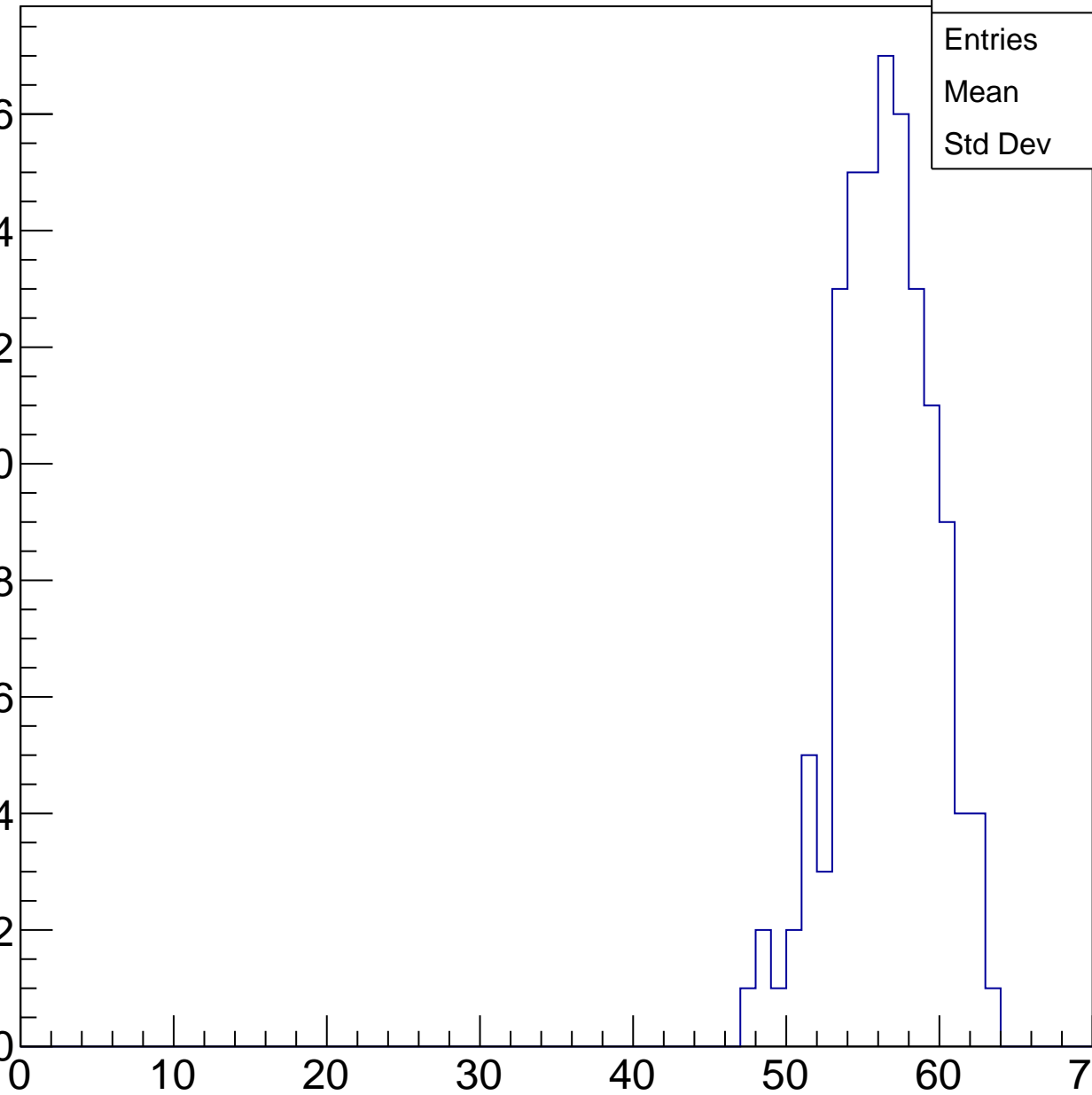
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	55.98
Std Dev	3.137

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch60, adc5

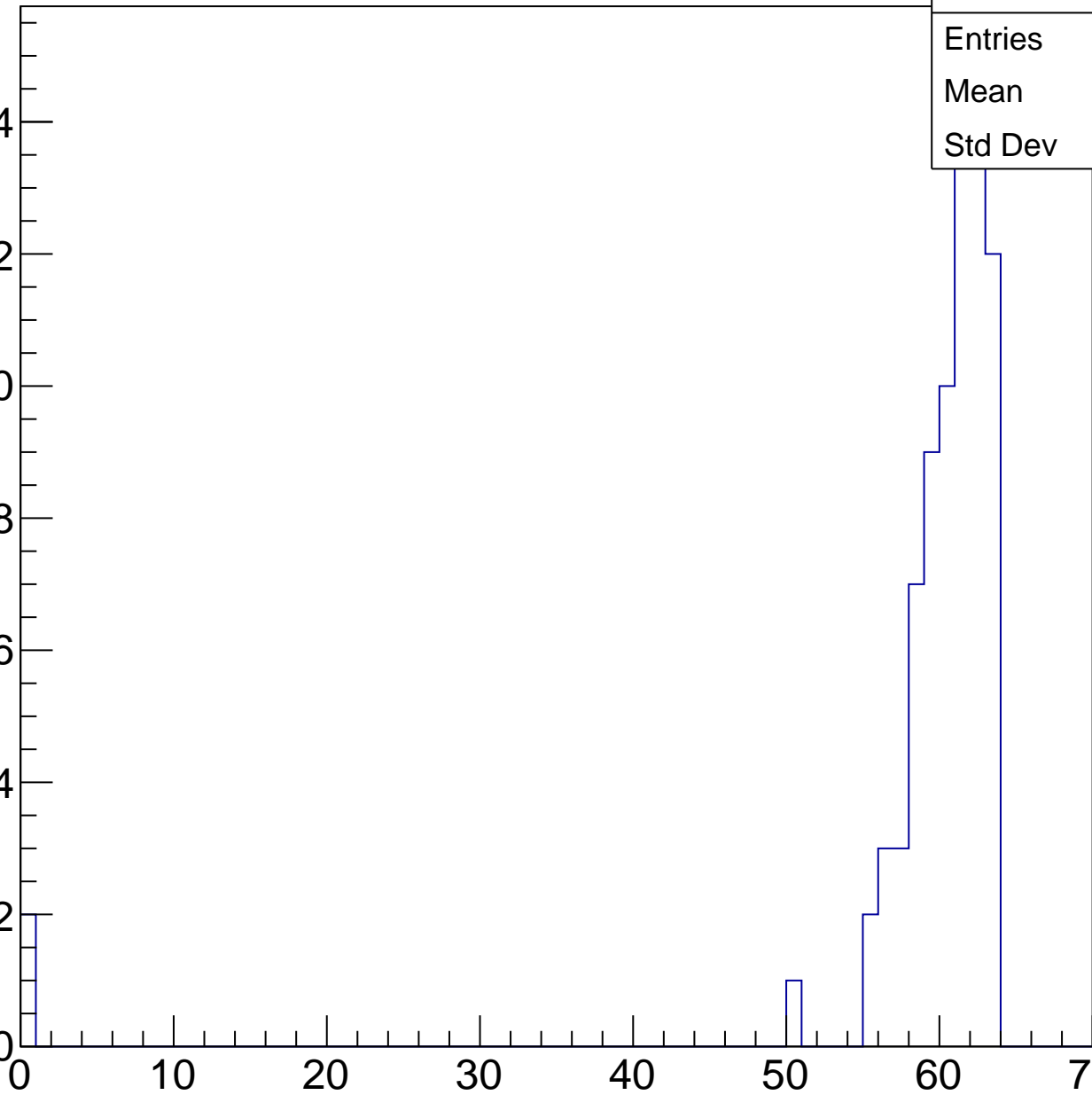
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	58.65
Std Dev	9.806

ampl

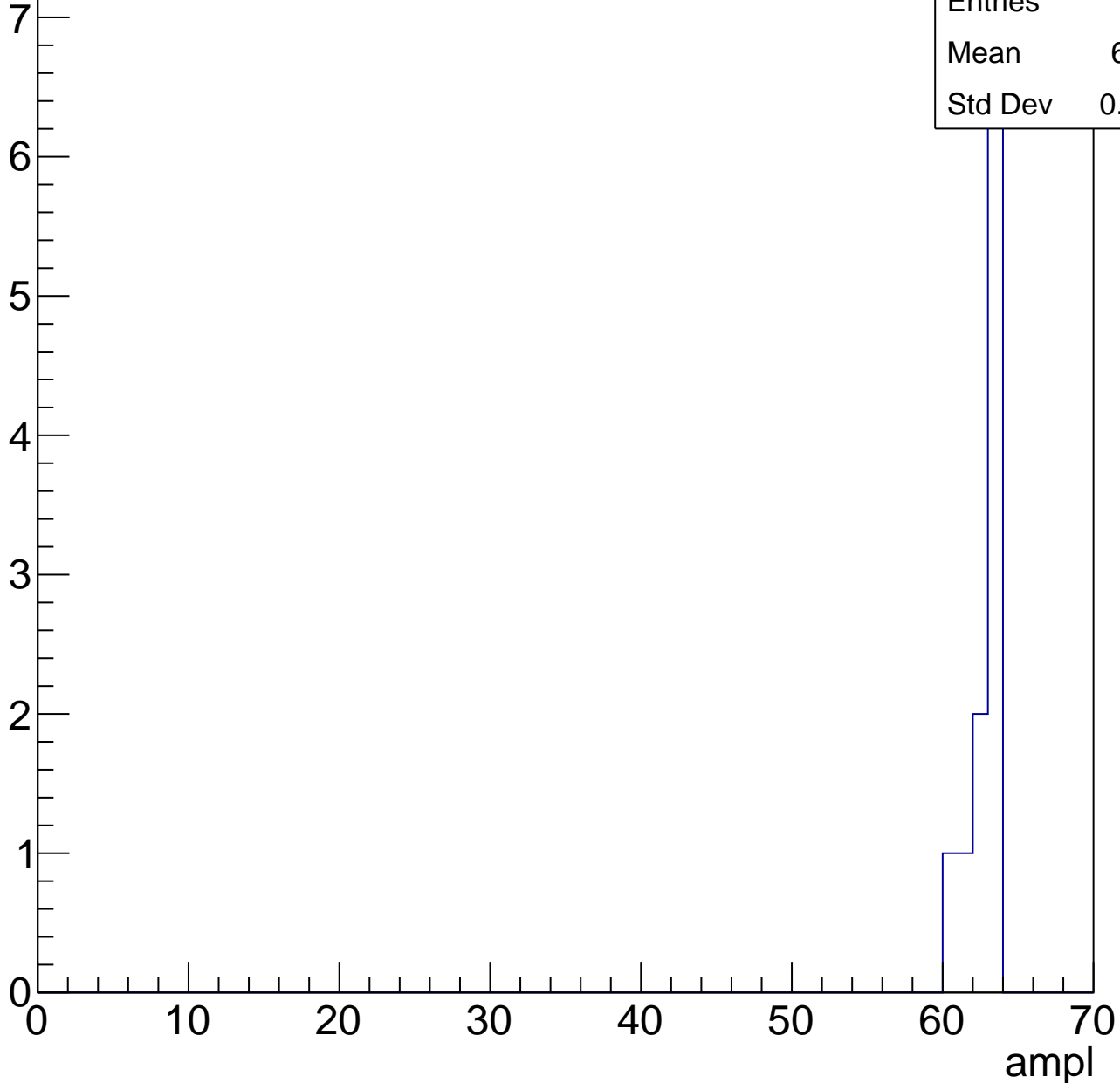


B0L101S, U19-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62.36
Std Dev	0.9791



B0L101S, U19-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch61, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	30.48
Std Dev	4.429

Gaus mean : 31.2916

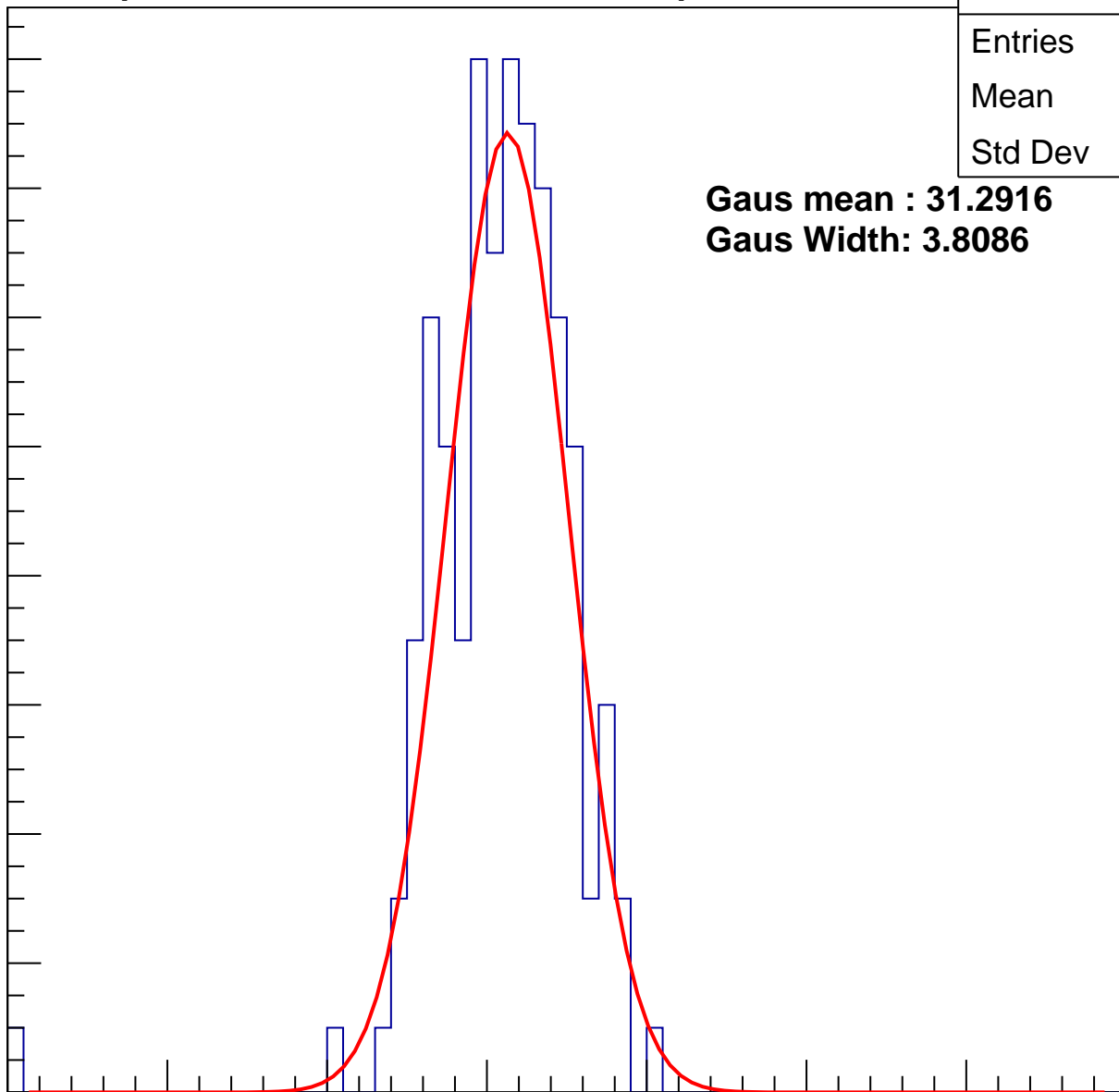
Gaus Width: 3.8086

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch61, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	37.83
Std Dev	3.961

Gaus mean : 38.2711

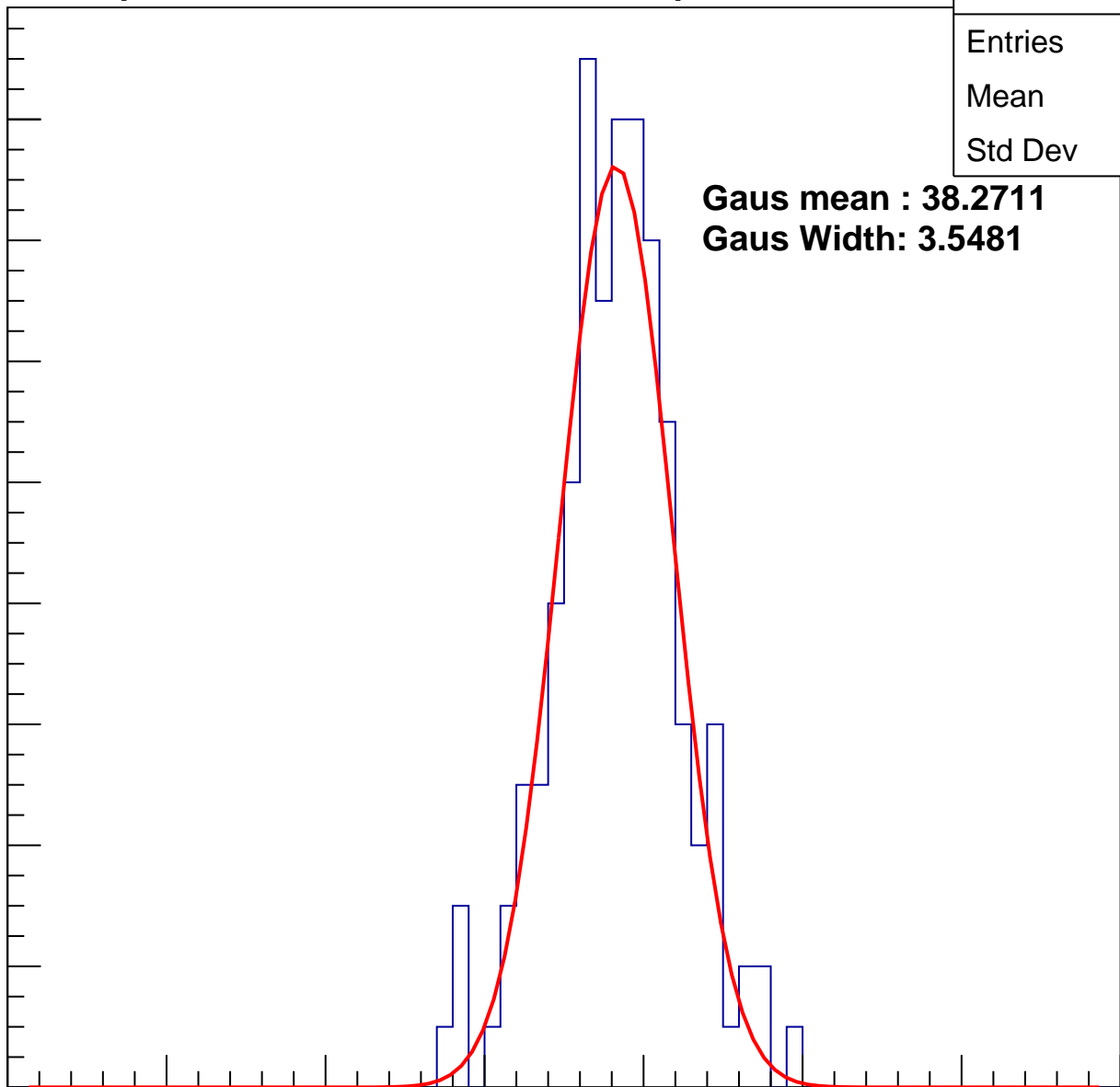
Gaus Width: 3.5481

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

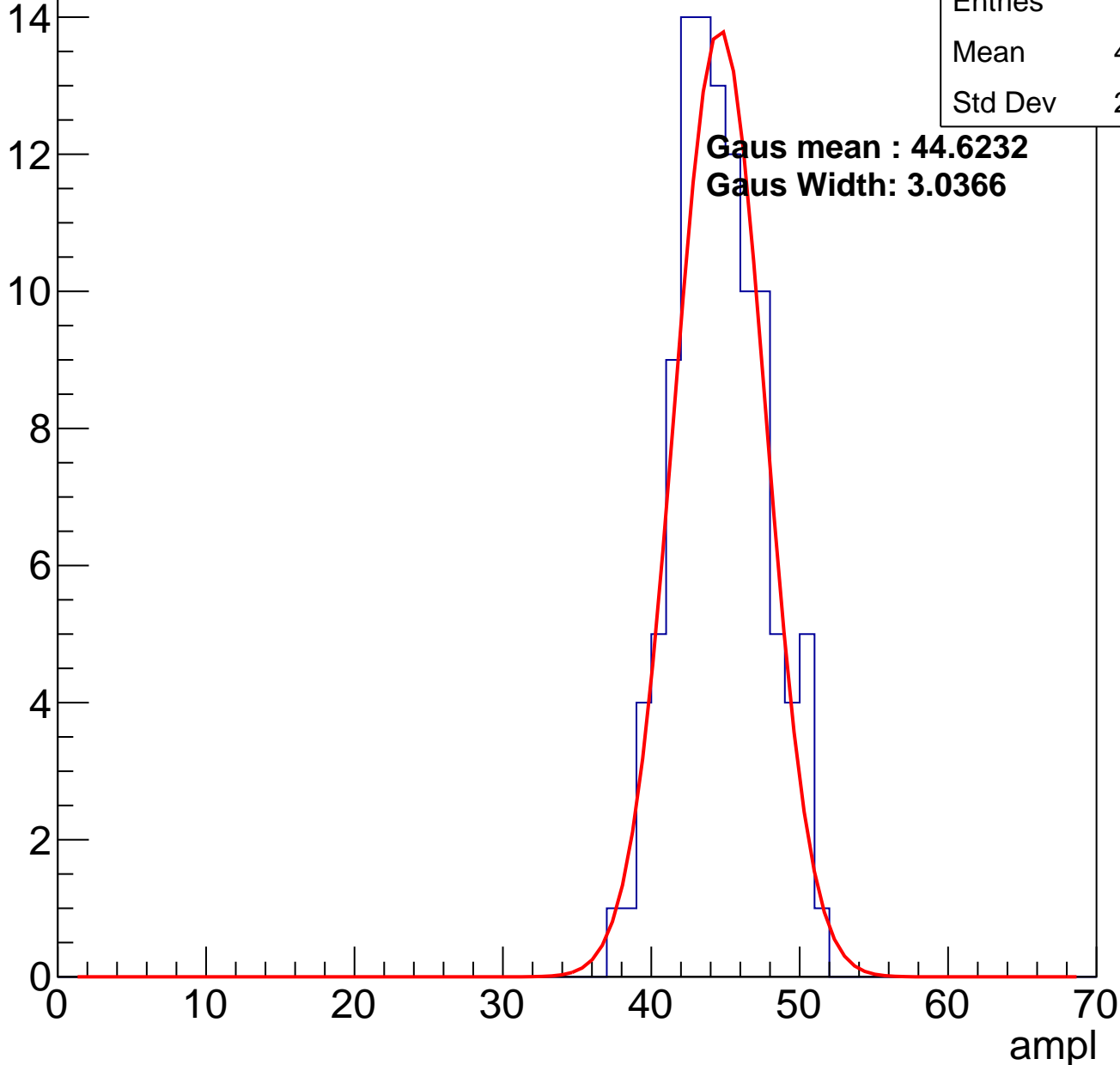
ampl



B0L101S, U19-ch61, adc2

calib_packv5_042523_0143.root, FC#1, port C1

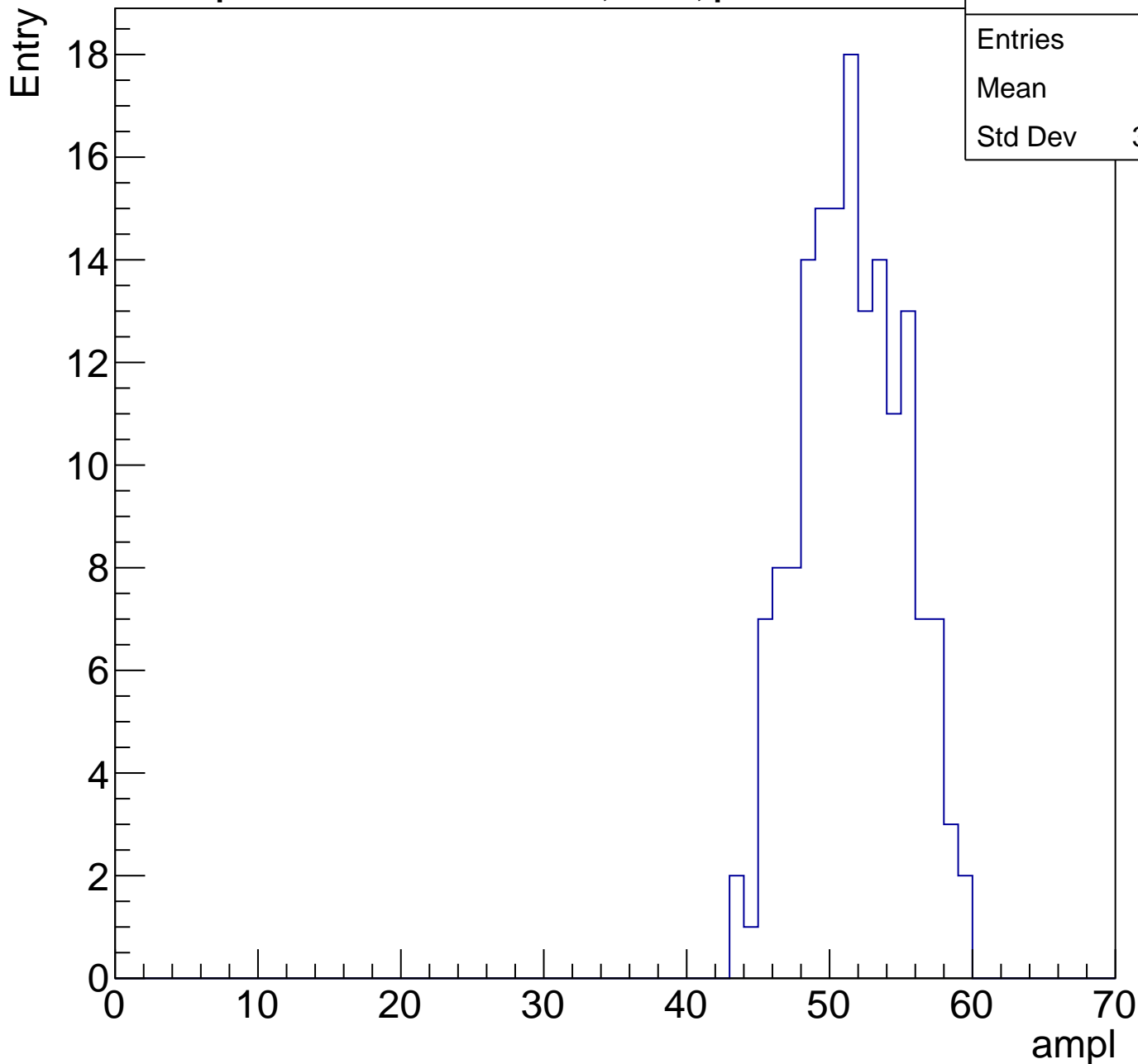
Entry



B0L101S, U19-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	51.1
Std Dev	3.588



B0L101S, U19-ch61, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

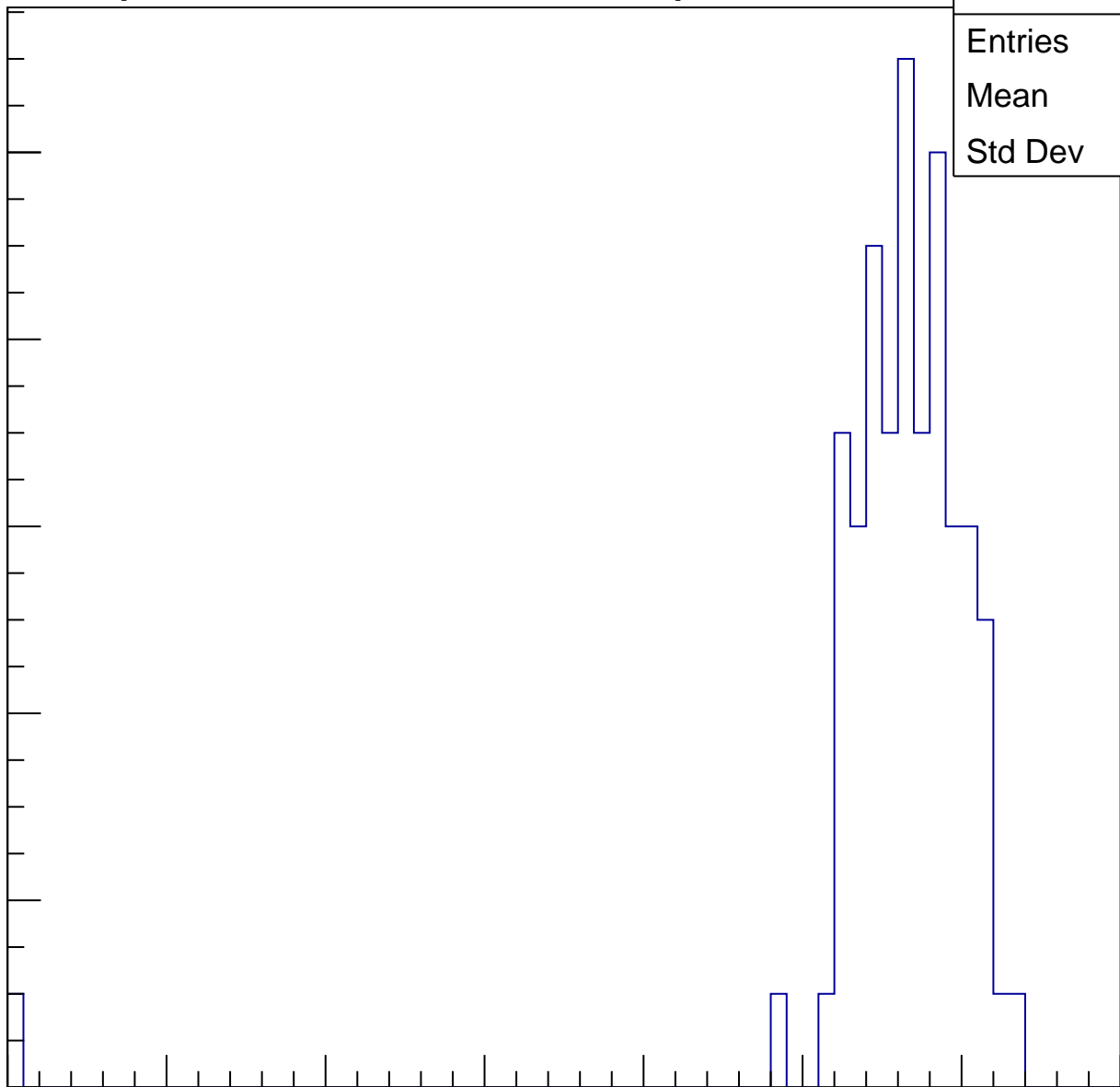
50

60

70

ampl

Entries	79
Mean	55.58
Std Dev	6.96



B0L101S, U19-ch61, adc5

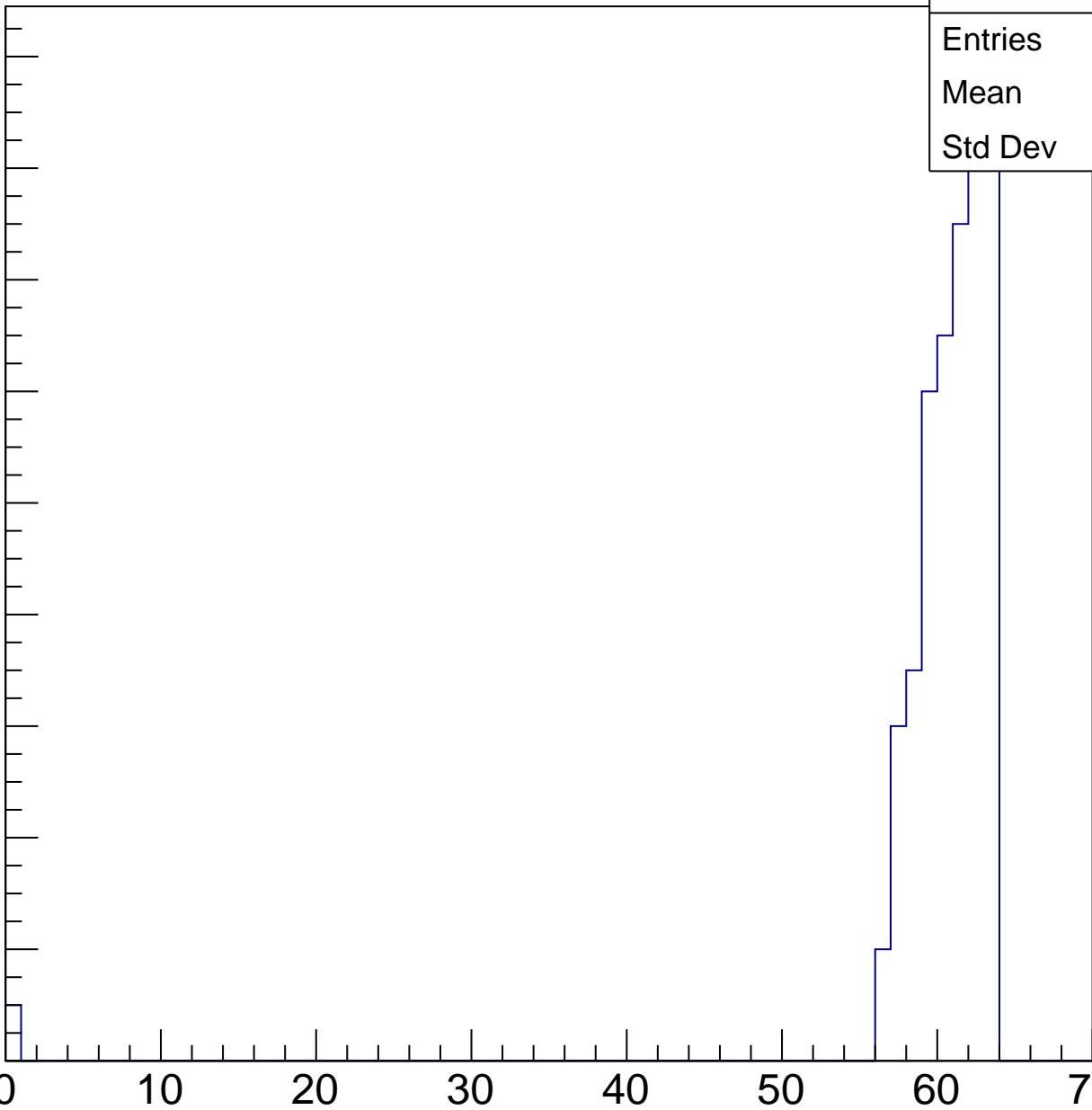
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.89
Std Dev	6.601

ampl



B0L101S, U19-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch62, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	26.59
Std Dev	5.166

Gaus mean : 27.8220

Gaus Width: 3.9388

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

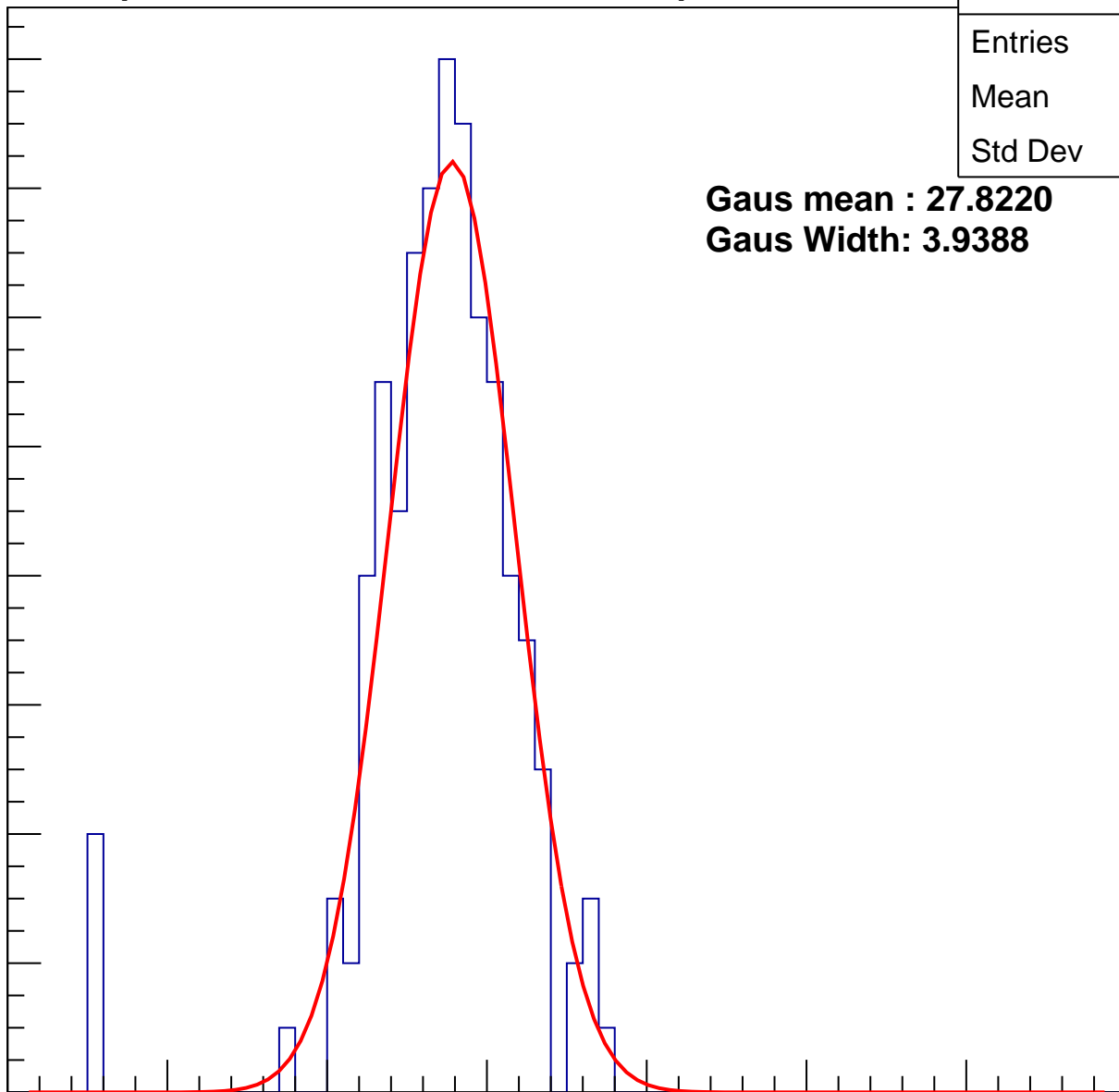
40

50

60

70

ampl



B0L101S, U19-ch62, adc1

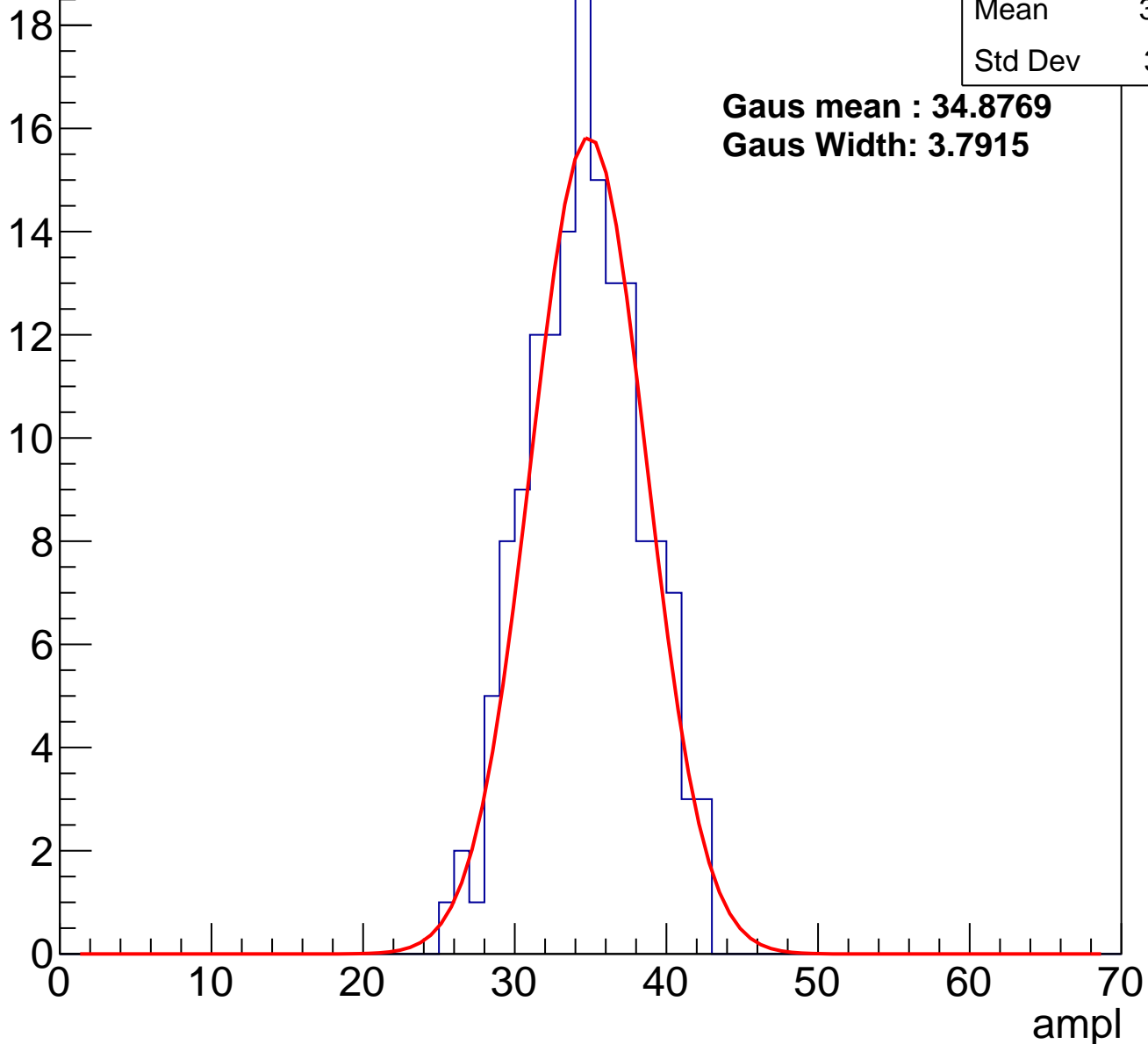
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	34.18
Std Dev	3.661

Gaus mean : 34.8769

Gaus Width: 3.7915

Entry



B0L101S, U19-ch62, adc2

calib_packv5_042523_0143.root, FC#1, port C1

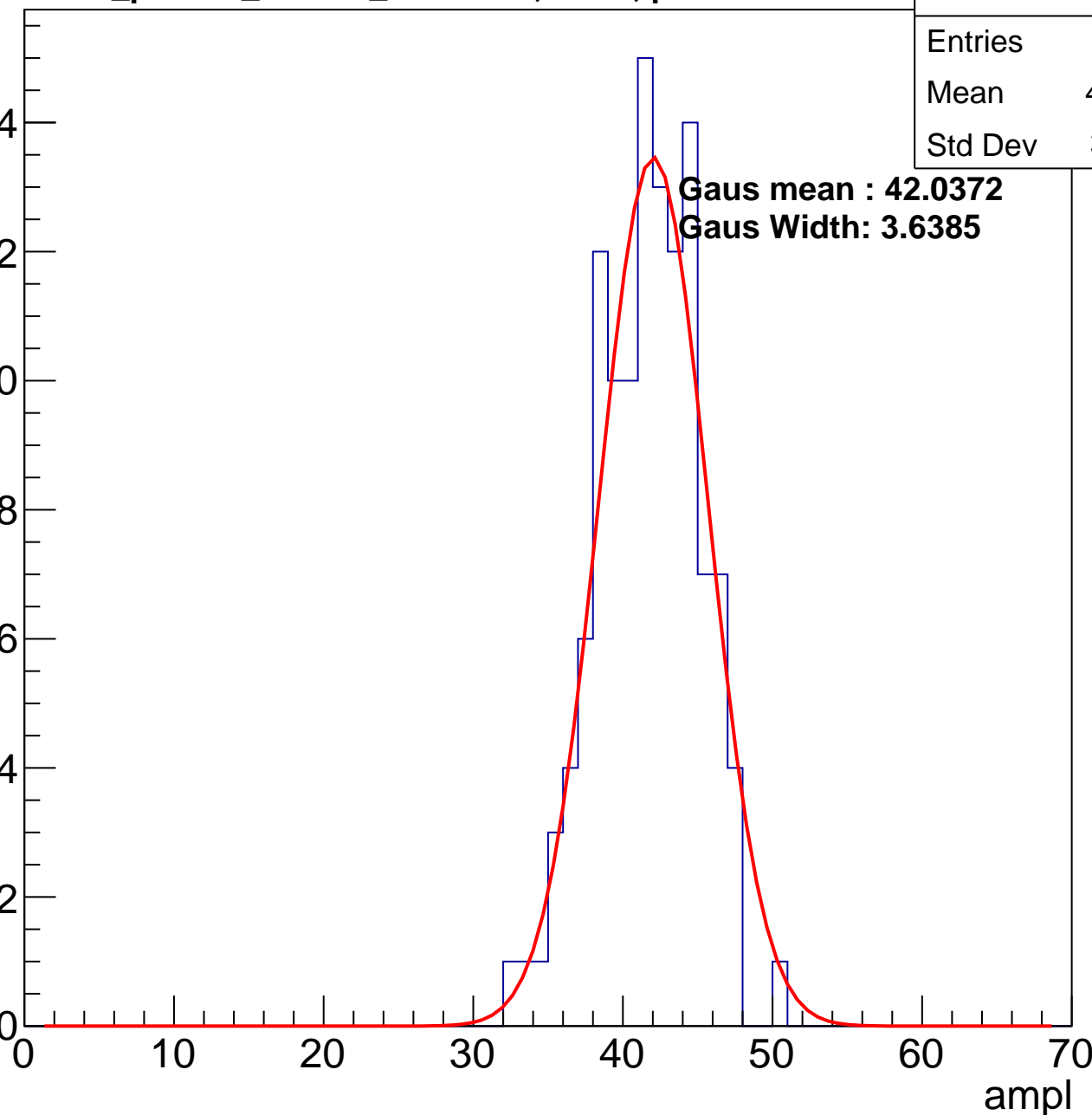
Entry

14
12
10
8
6
4
2
0

Entries	121
Mean	41.19
Std Dev	3.341

Gaus mean : 42.0372

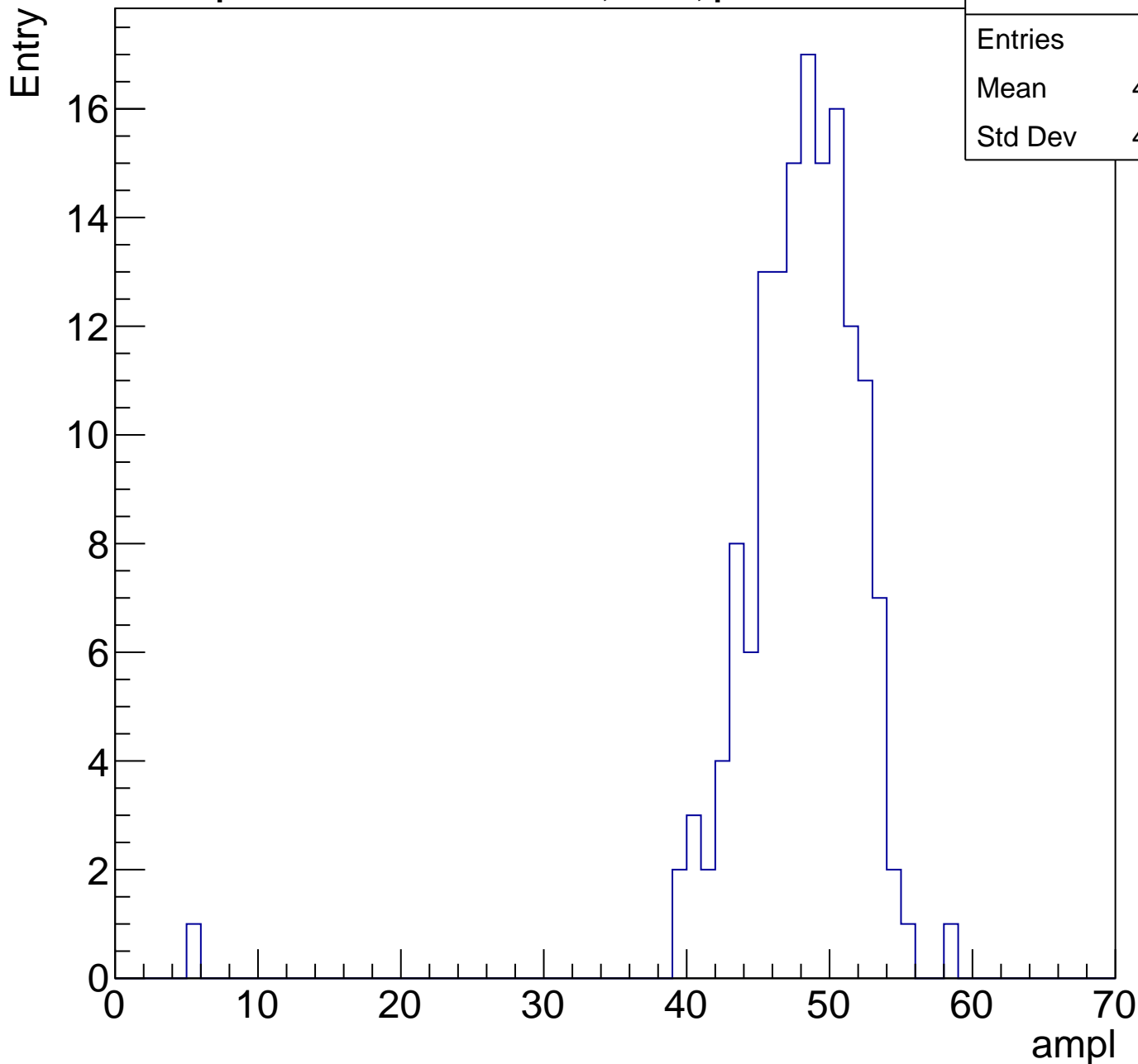
Gaus Width: 3.6385



B0L101S, U19-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	47.49
Std Dev	4.946



B0L101S, U19-ch62, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

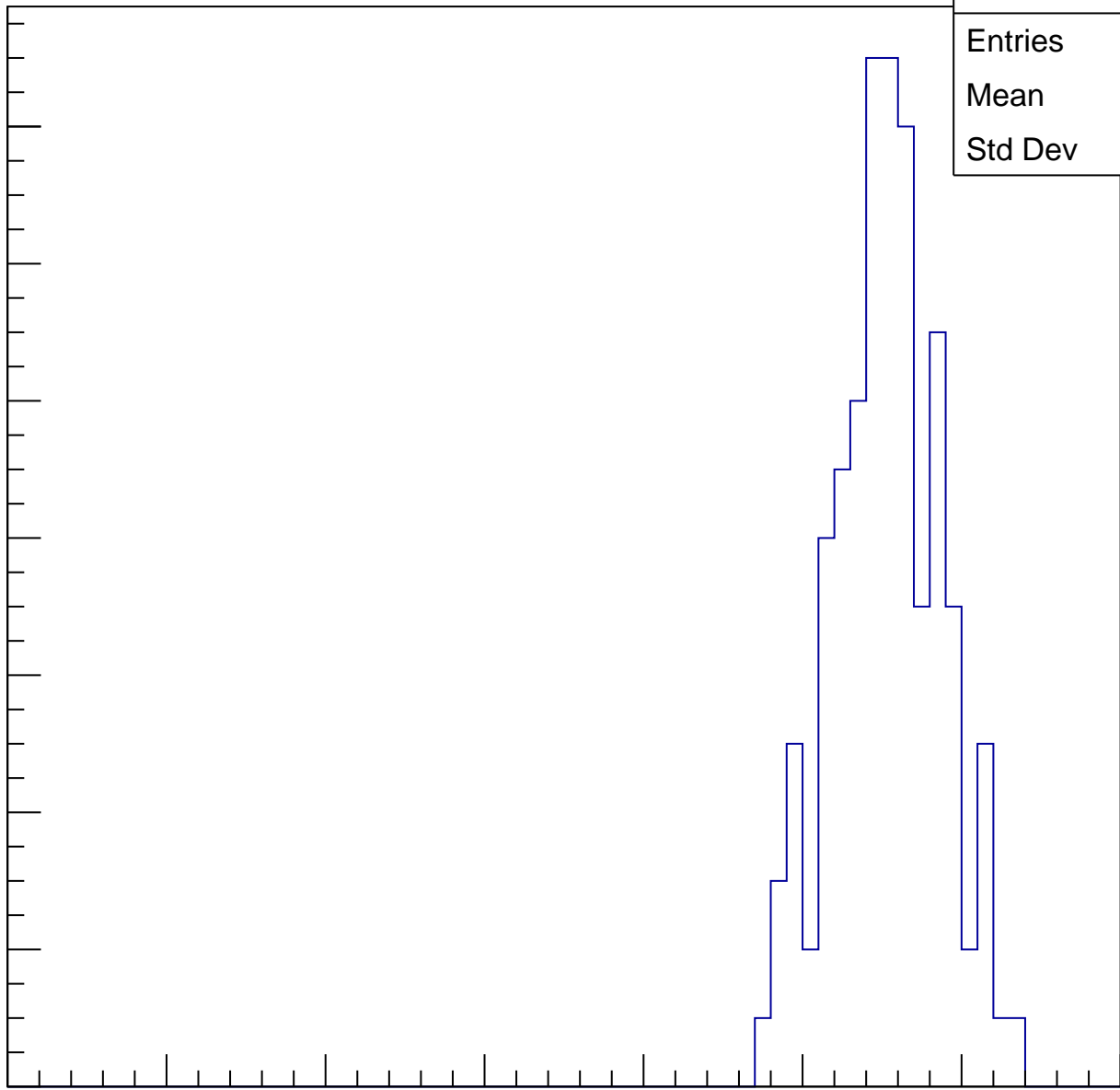
2

0

Entries	116
Mean	54.84
Std Dev	3.345

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch62, adc5

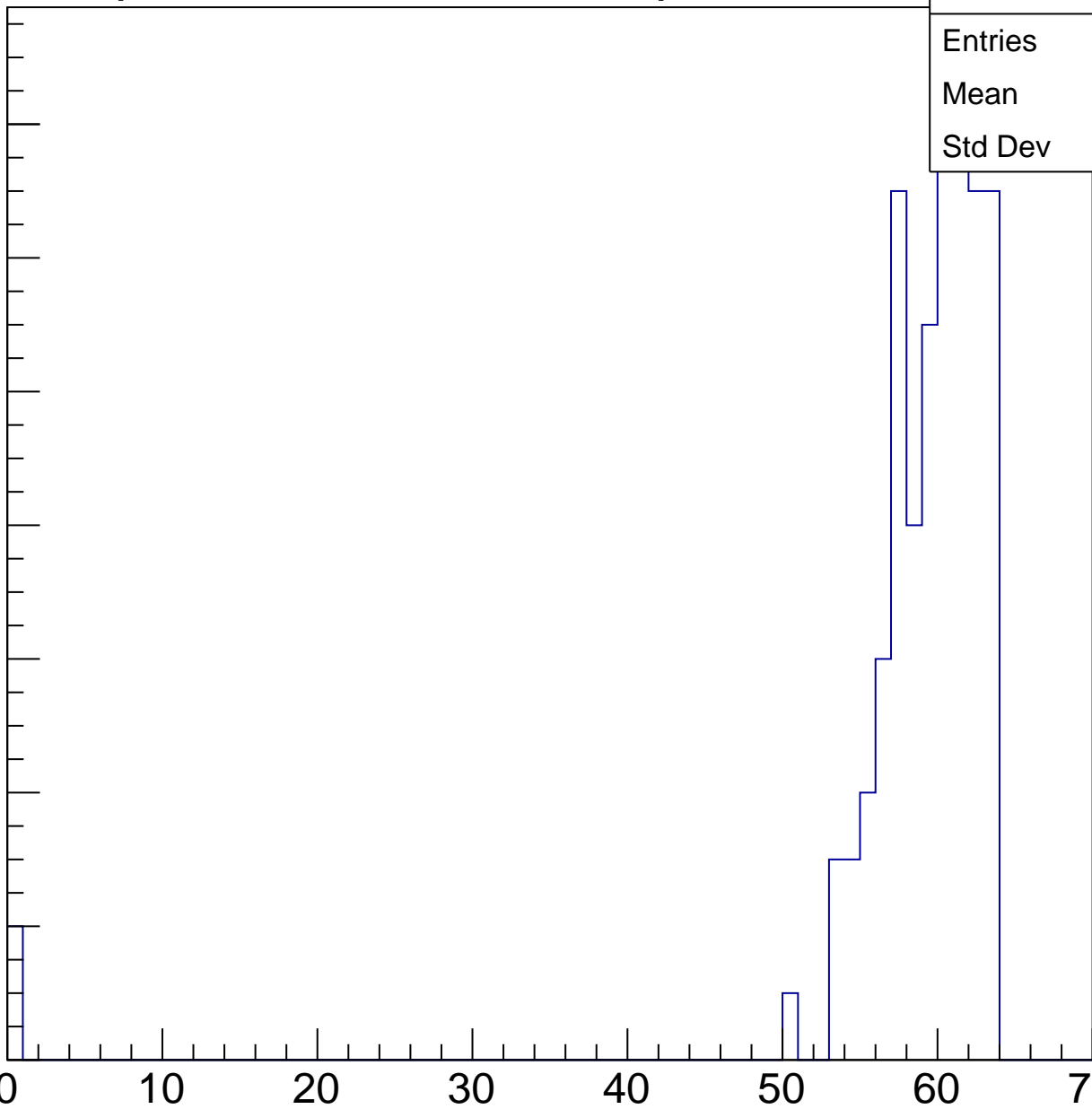
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	58.11
Std Dev	8.532

ampl



B0L101S, U19-ch62, adc6

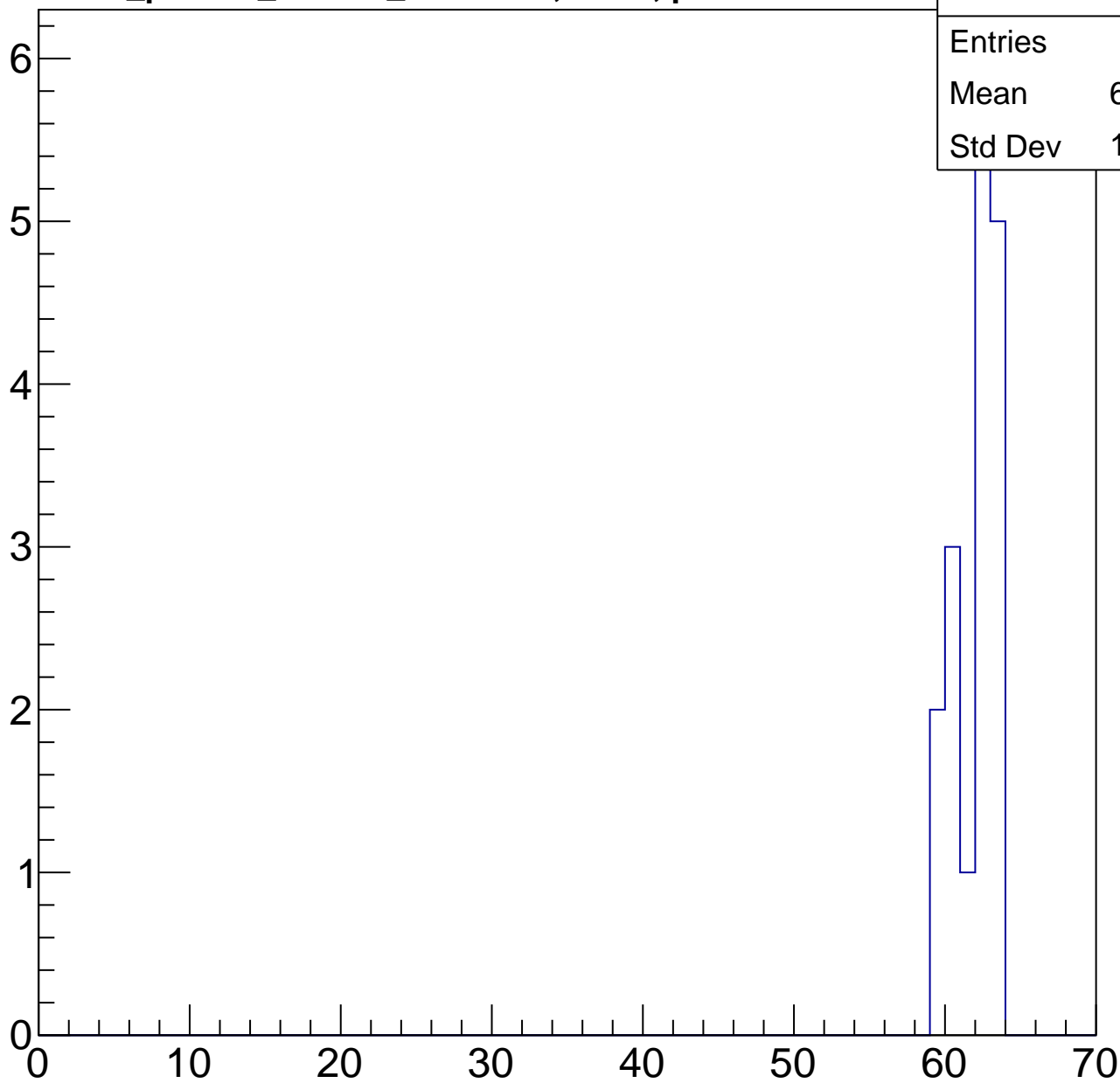
calib_packv5_042523_0143.root, FC#1, port C1

Entry

6
5
4
3
2
1
0

Entries	17
Mean	61.53
Std Dev	1.377

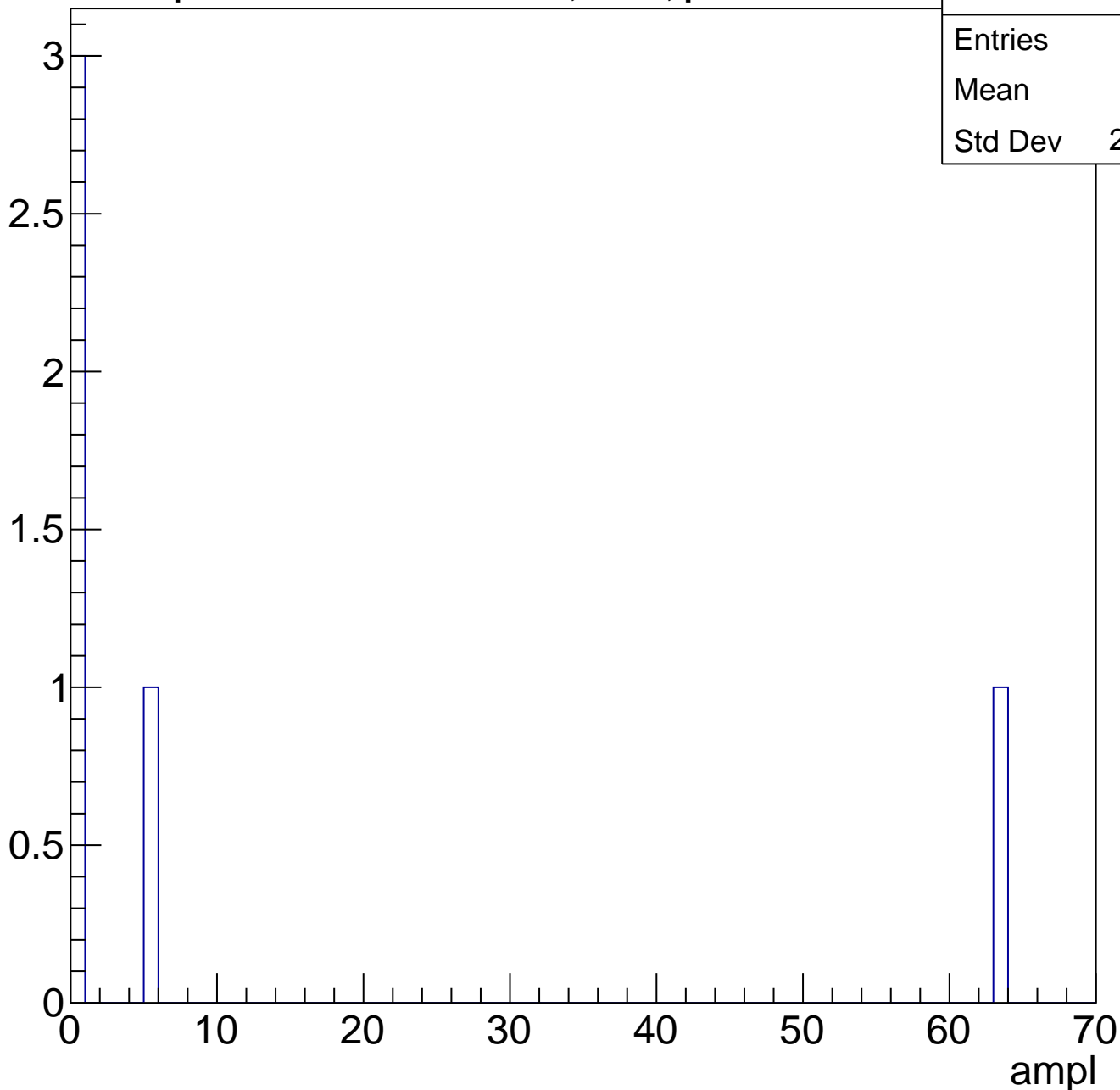
ampl



B0L101S, U19-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	13.6
Std Dev	24.78

B0L101S, U19-ch63, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	29.63
Std Dev	3.126

Gaus mean : 30.0846

Gaus Width: 3.3249

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

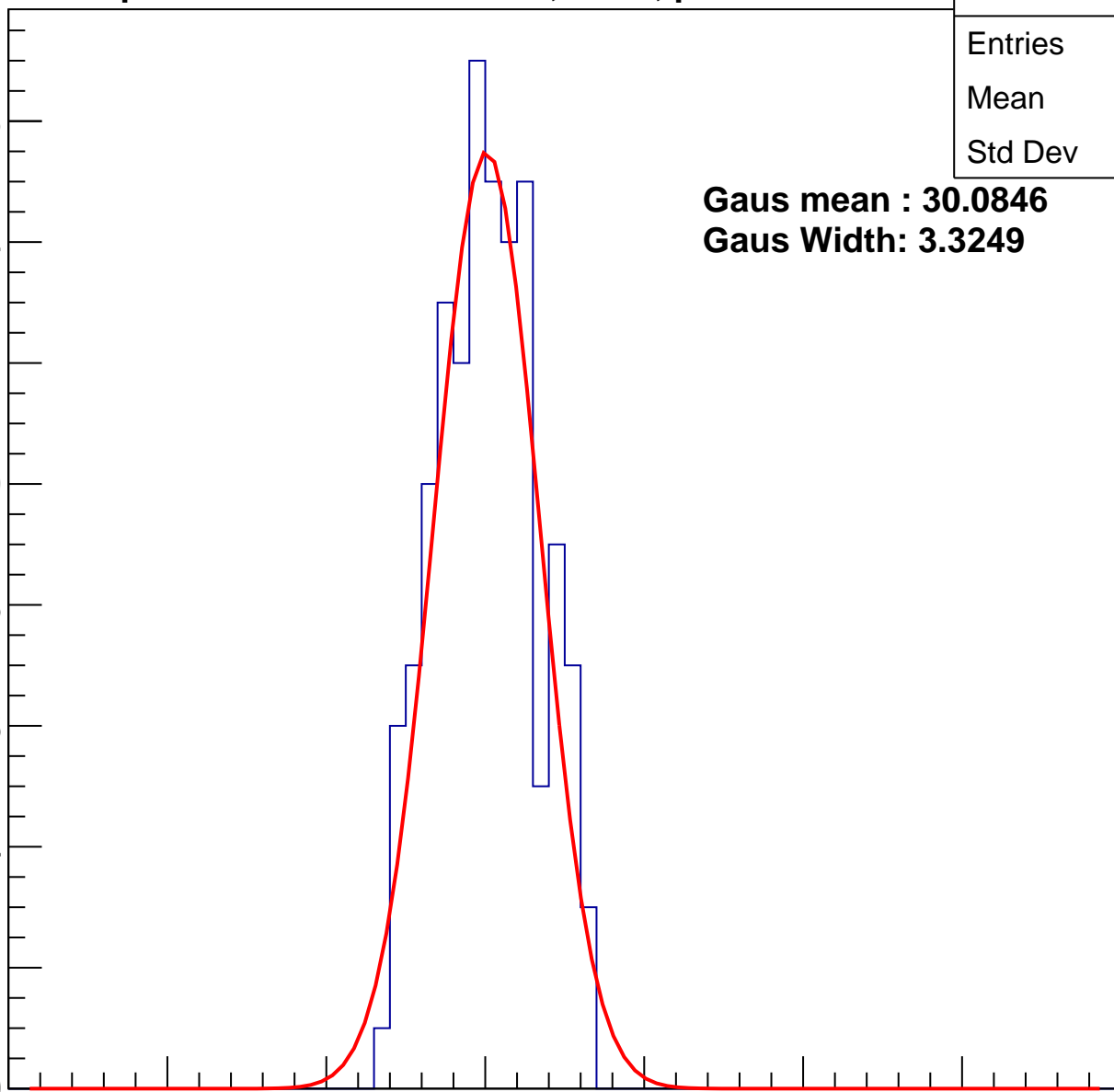
40

50

60

70

ampl



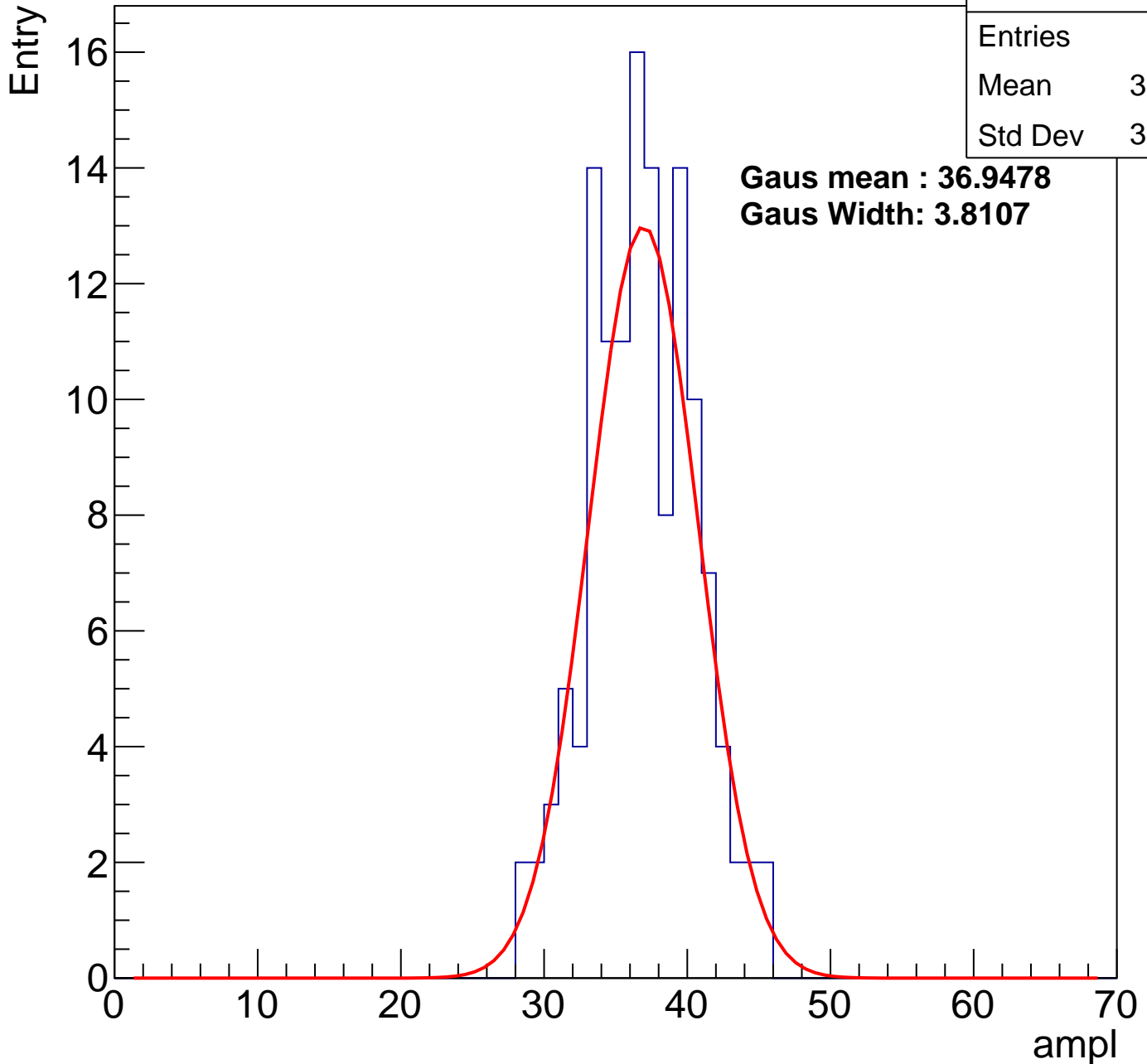
B0L101S, U19-ch63, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.42
Std Dev	3.623

Gaus mean : 36.9478

Gaus Width: 3.8107



B0L101S, U19-ch63, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

110

Mean

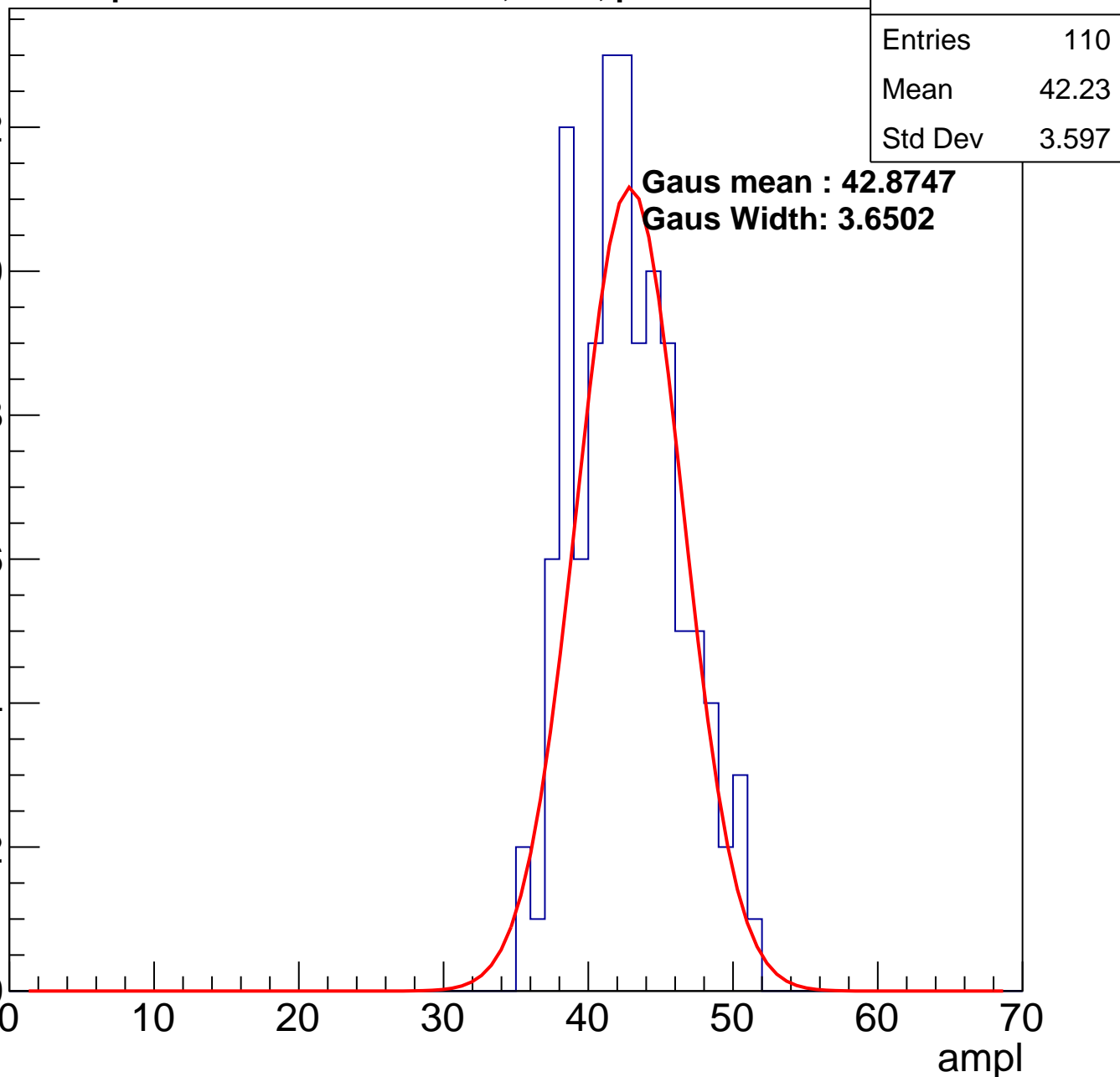
42.23

Std Dev

3.597

Gaus mean : 42.8747

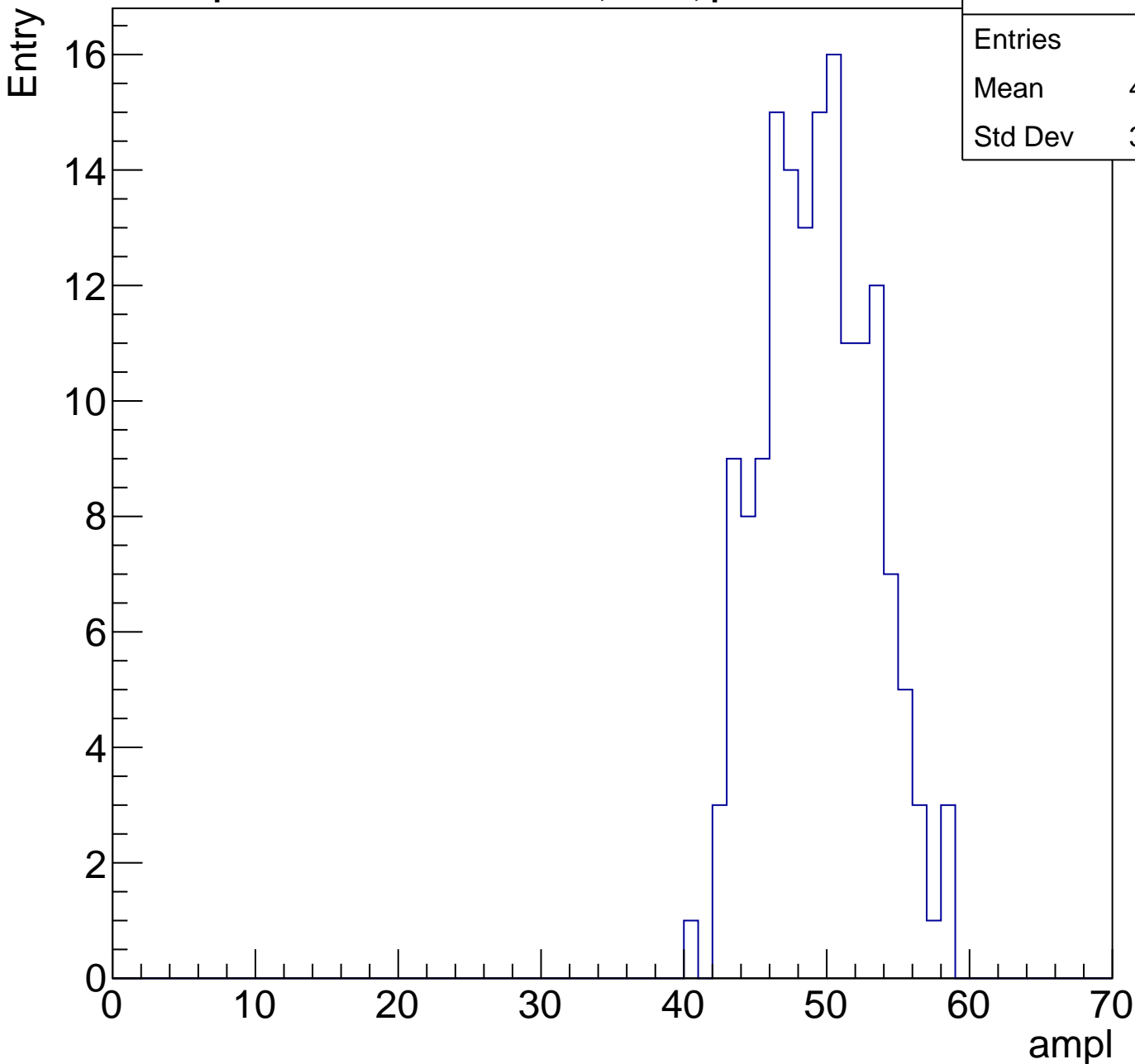
Gaus Width: 3.6502



B0L101S, U19-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	48.96
Std Dev	3.806

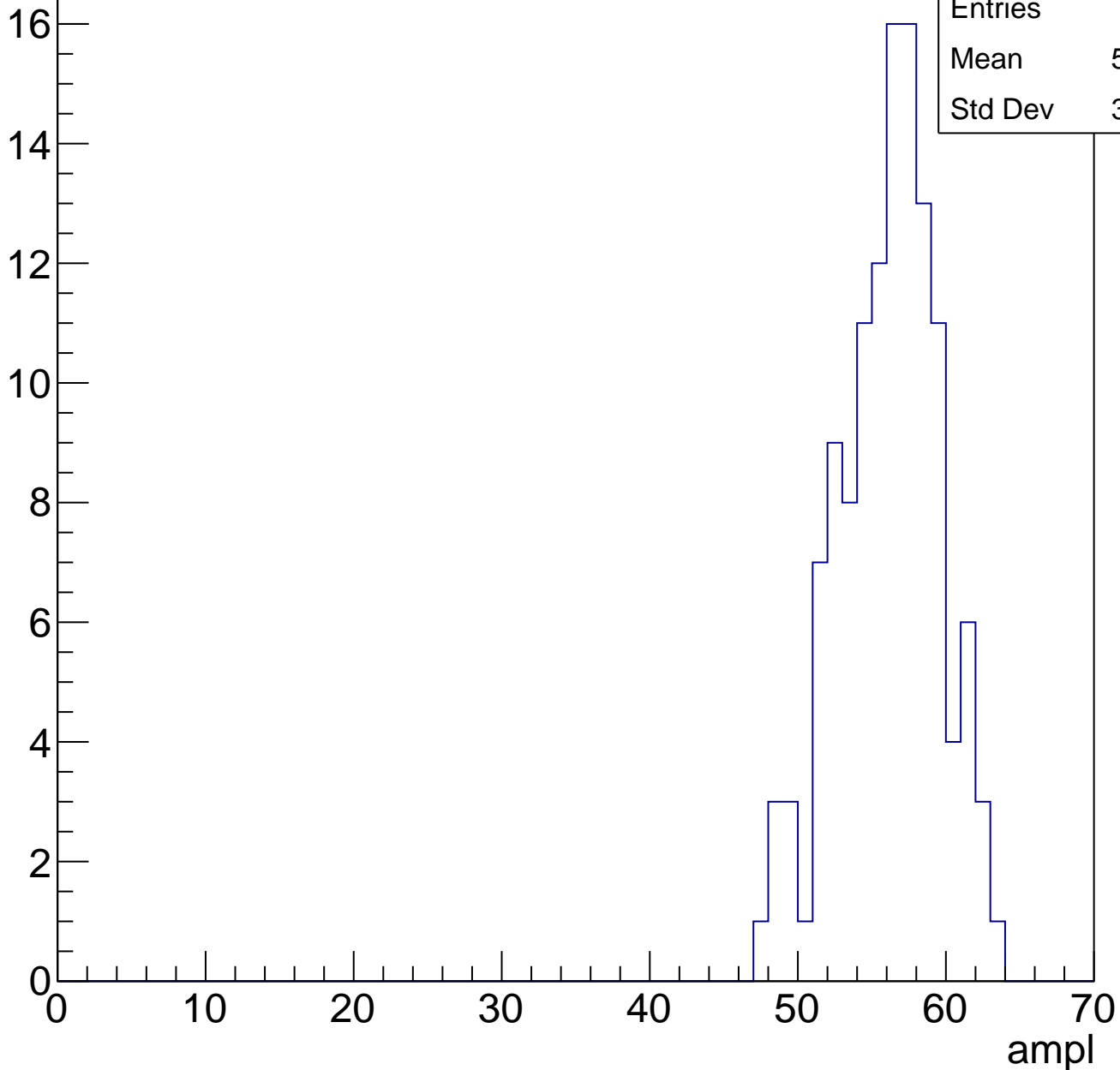


B0L101S, U19-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.66
Std Dev	3.378

Entry



B0L101S, U19-ch63, adc5

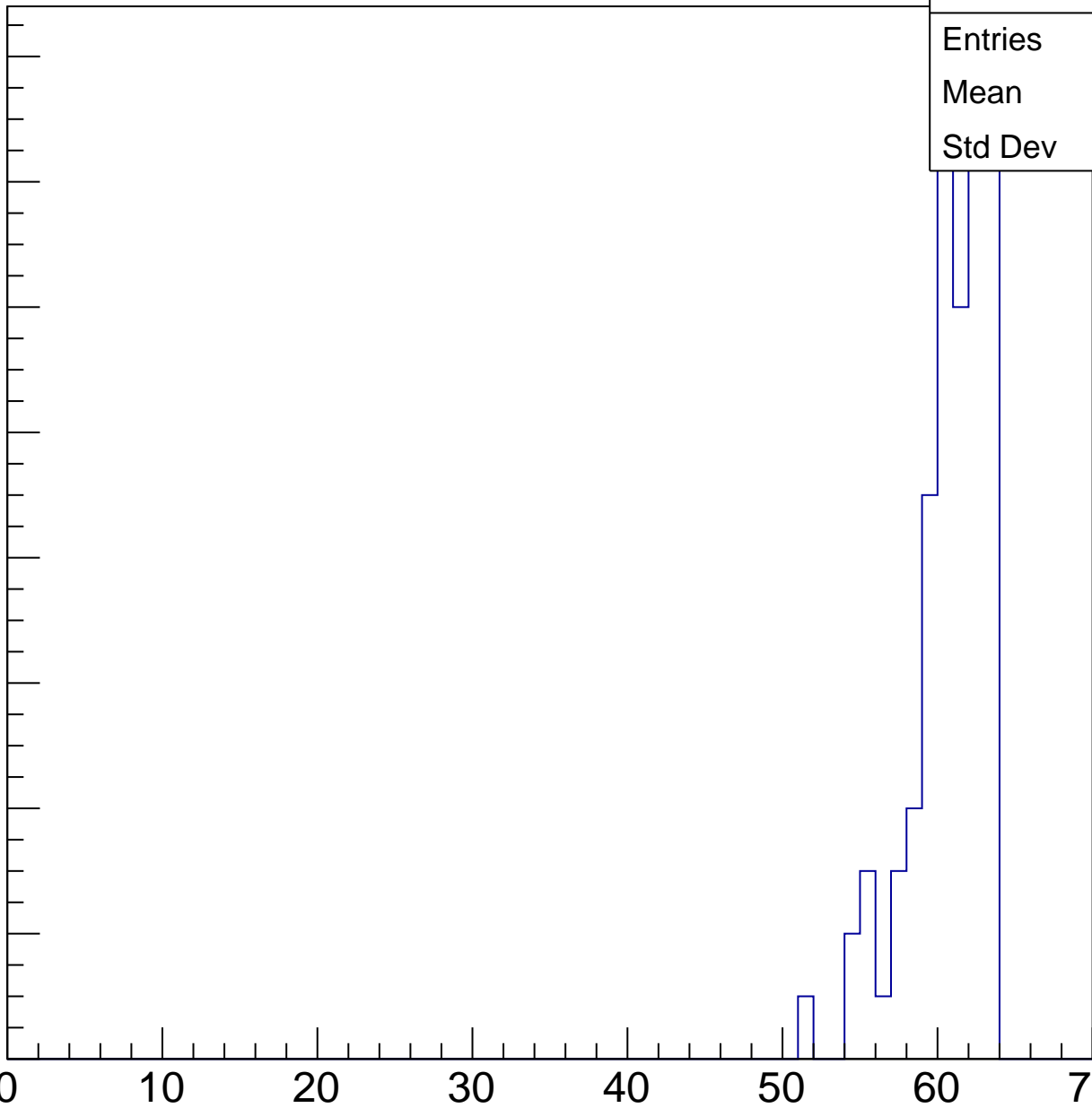
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	60.29
Std Dev	2.482

ampl



B0L101S, U19-ch63, adc6

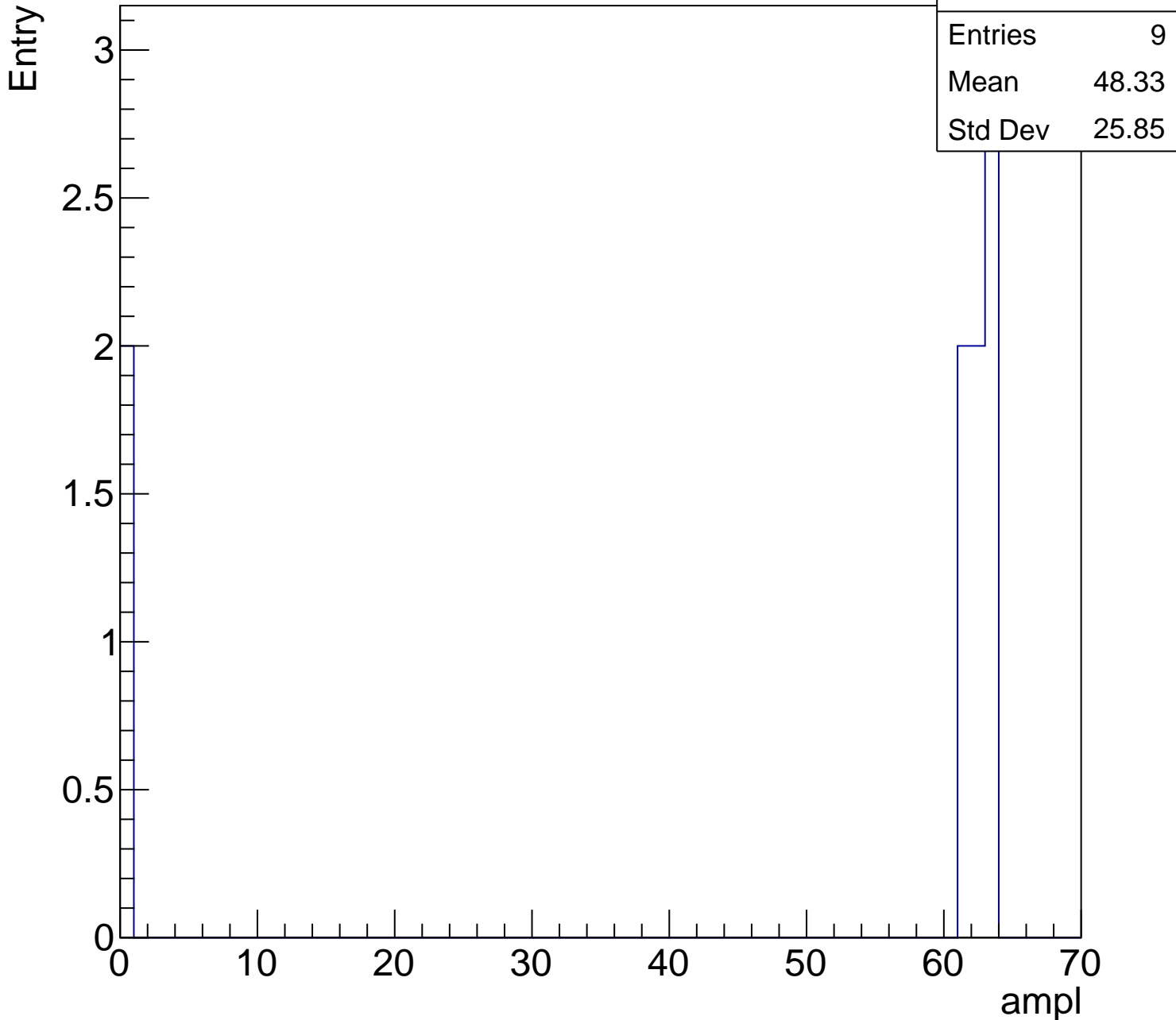
calib_packv5_042523_0143.root, FC#1, port C1

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	48.33
Std Dev	25.85

ampl



B0L101S, U19-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L101S, U19-ch64, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	28.63
Std Dev	5.417

Gaus mean : 29.5113

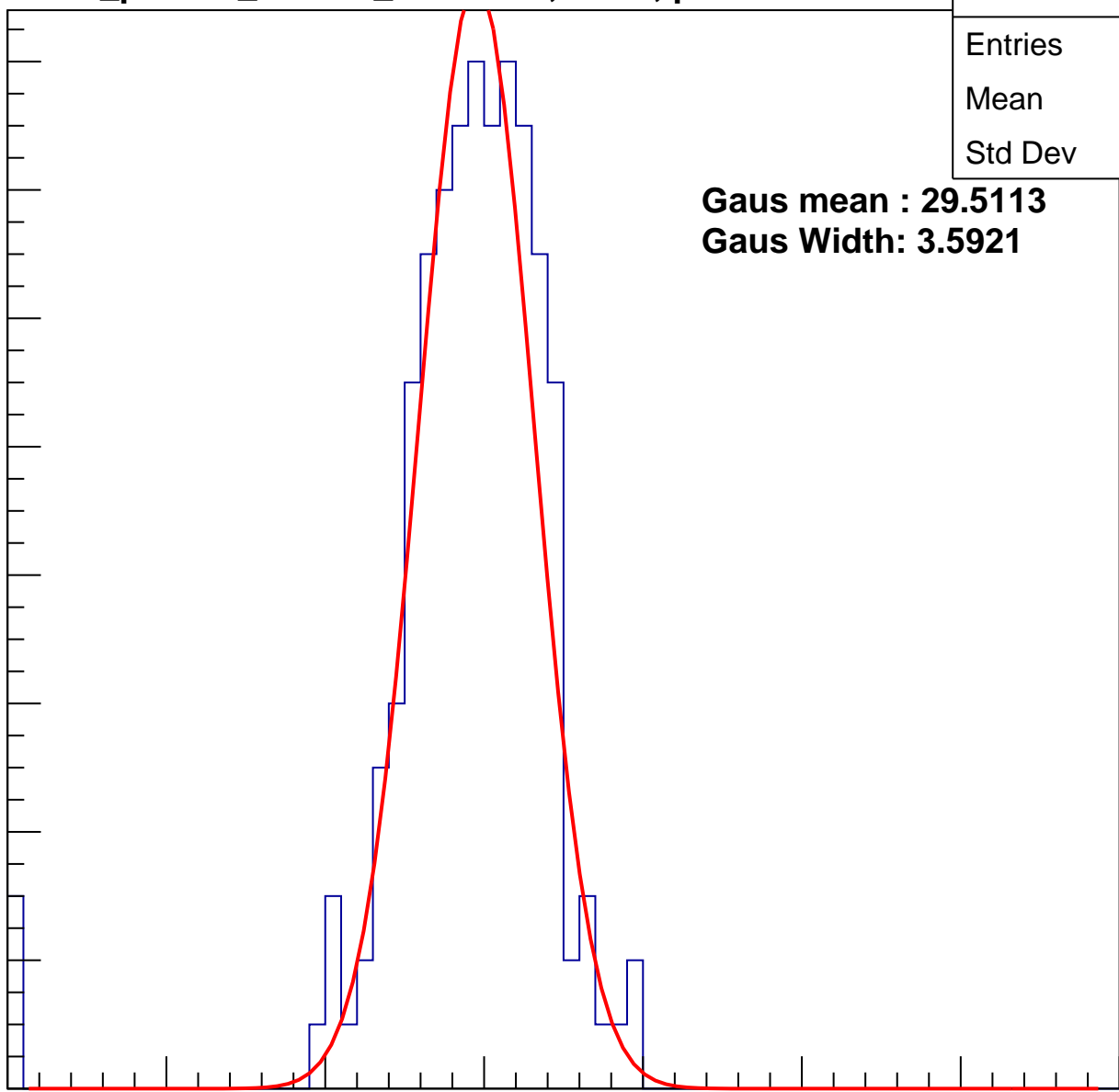
Gaus Width: 3.5921

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



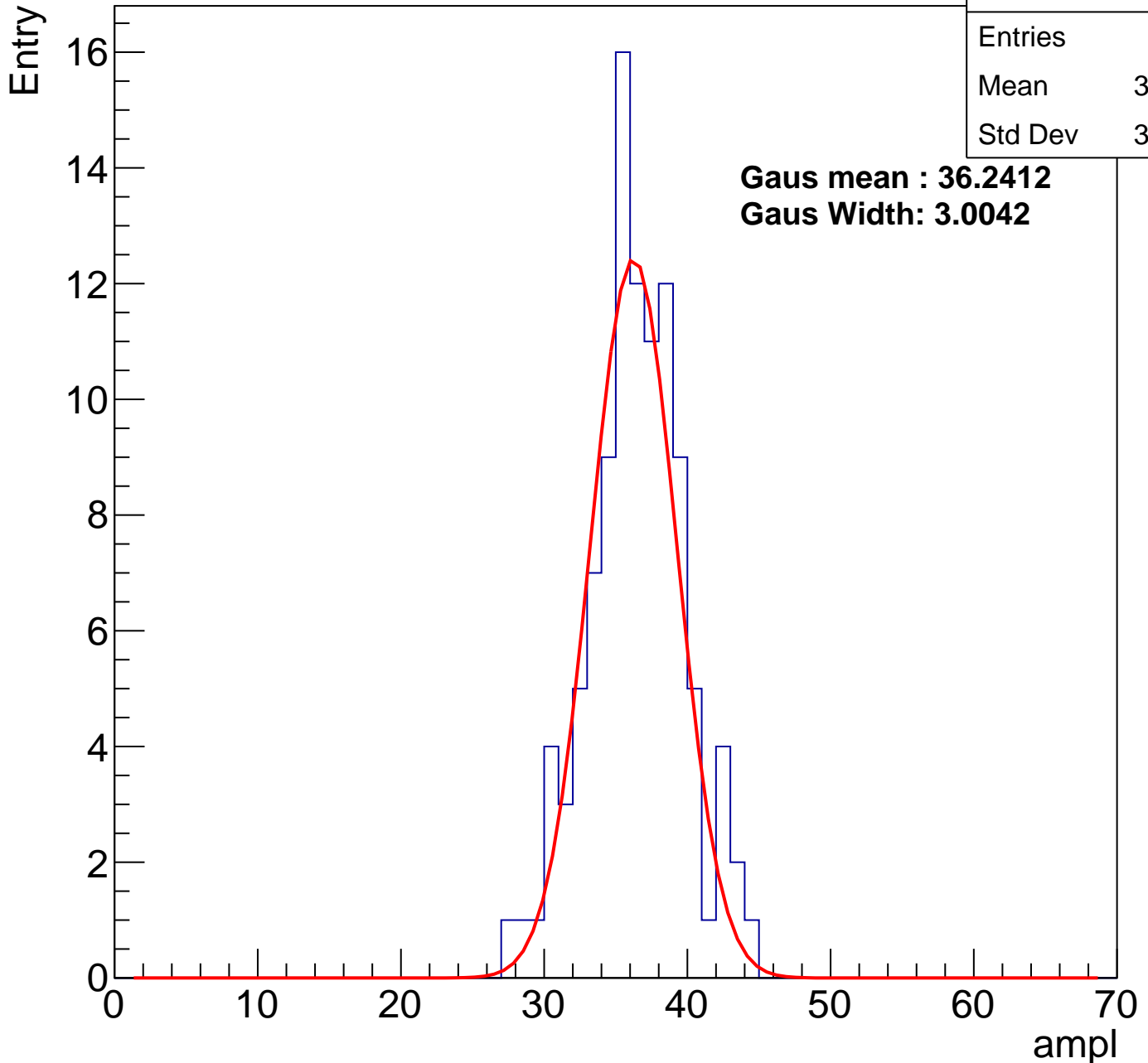
B0L101S, U19-ch64, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	35.95
Std Dev	3.338

Gaus mean : 36.2412

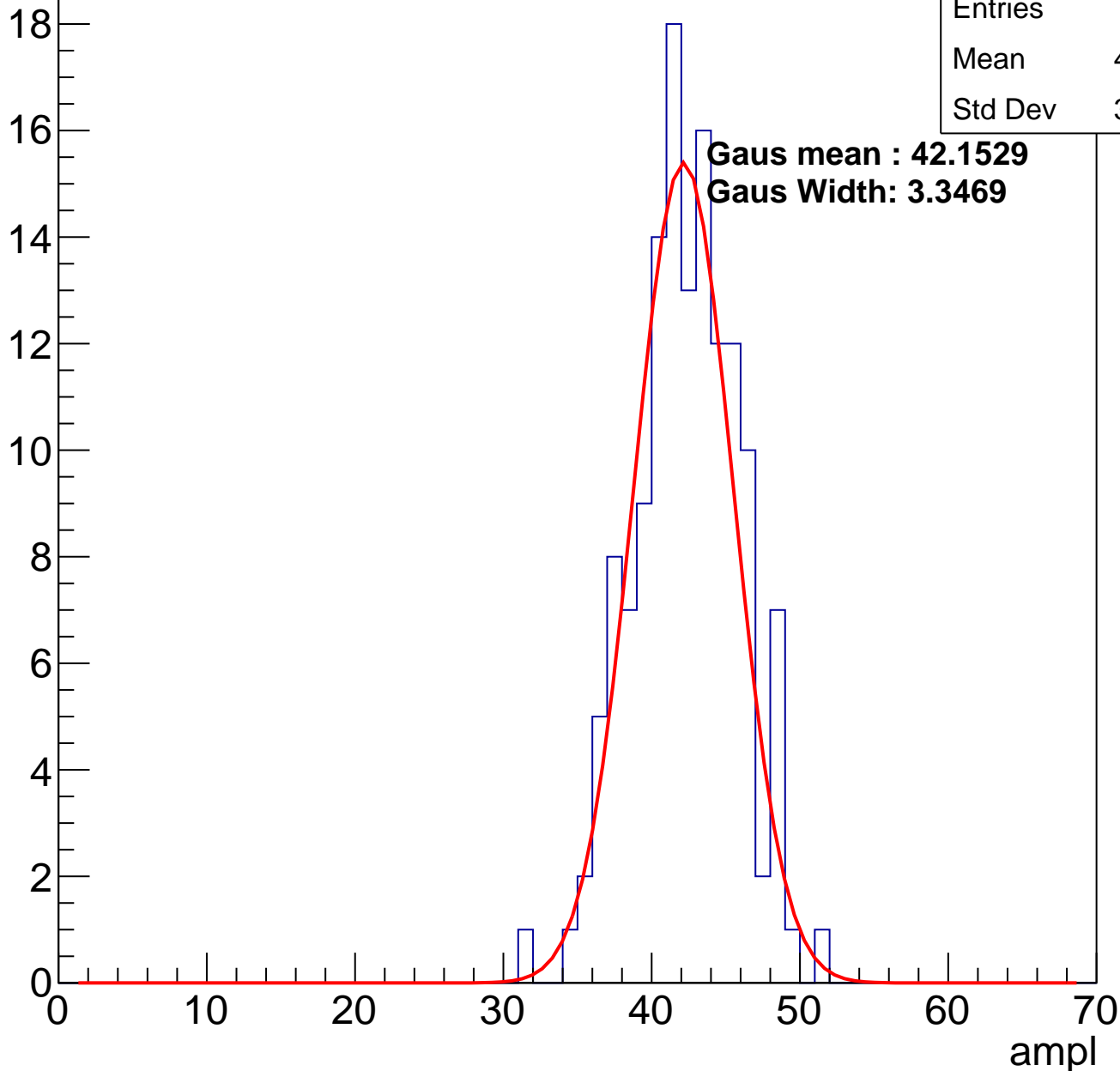
Gaus Width: 3.0042



B0L101S, U19-ch64, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch64, adc3

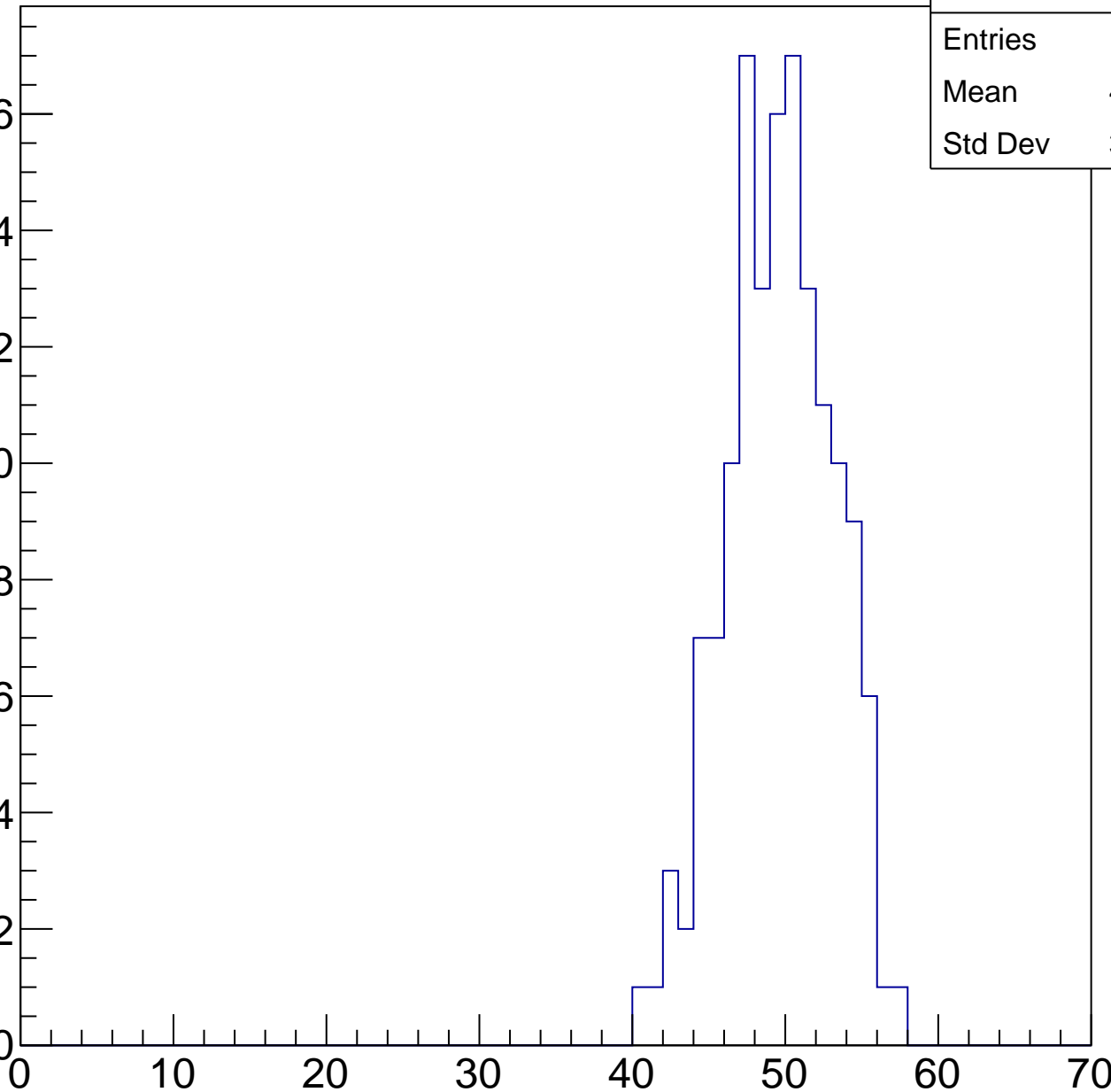
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	49.15
Std Dev	3.425

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

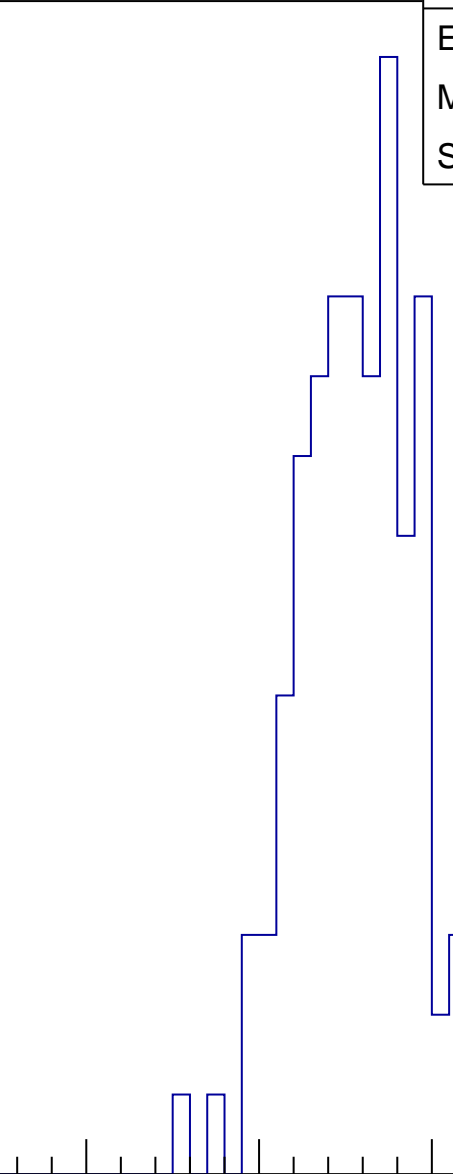
Entries	104
Mean	55.08
Std Dev	3.245

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch64, adc5

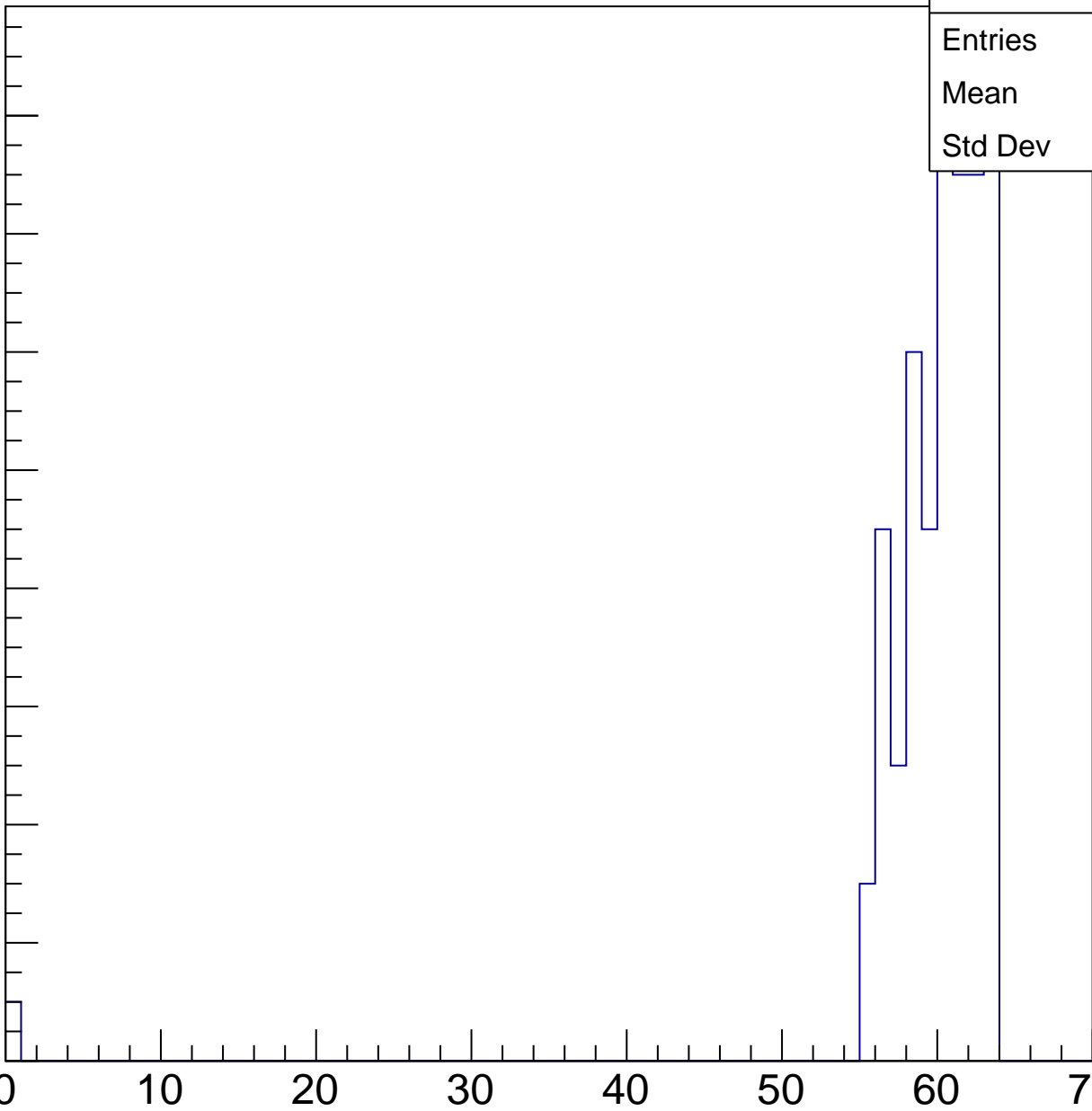
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	59.38
Std Dev	6.348

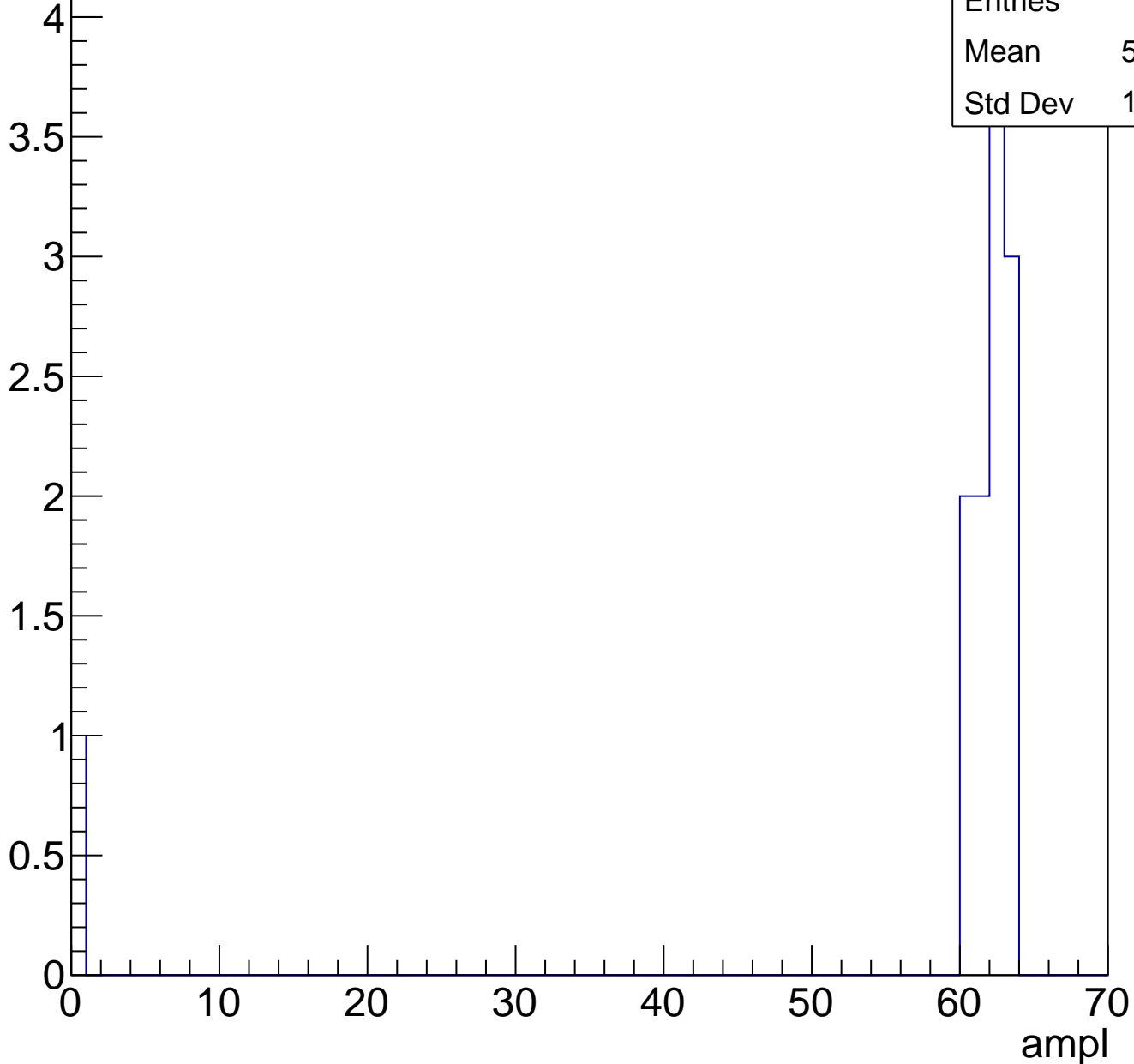
ampl



B0L101S, U19-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch65, adc0

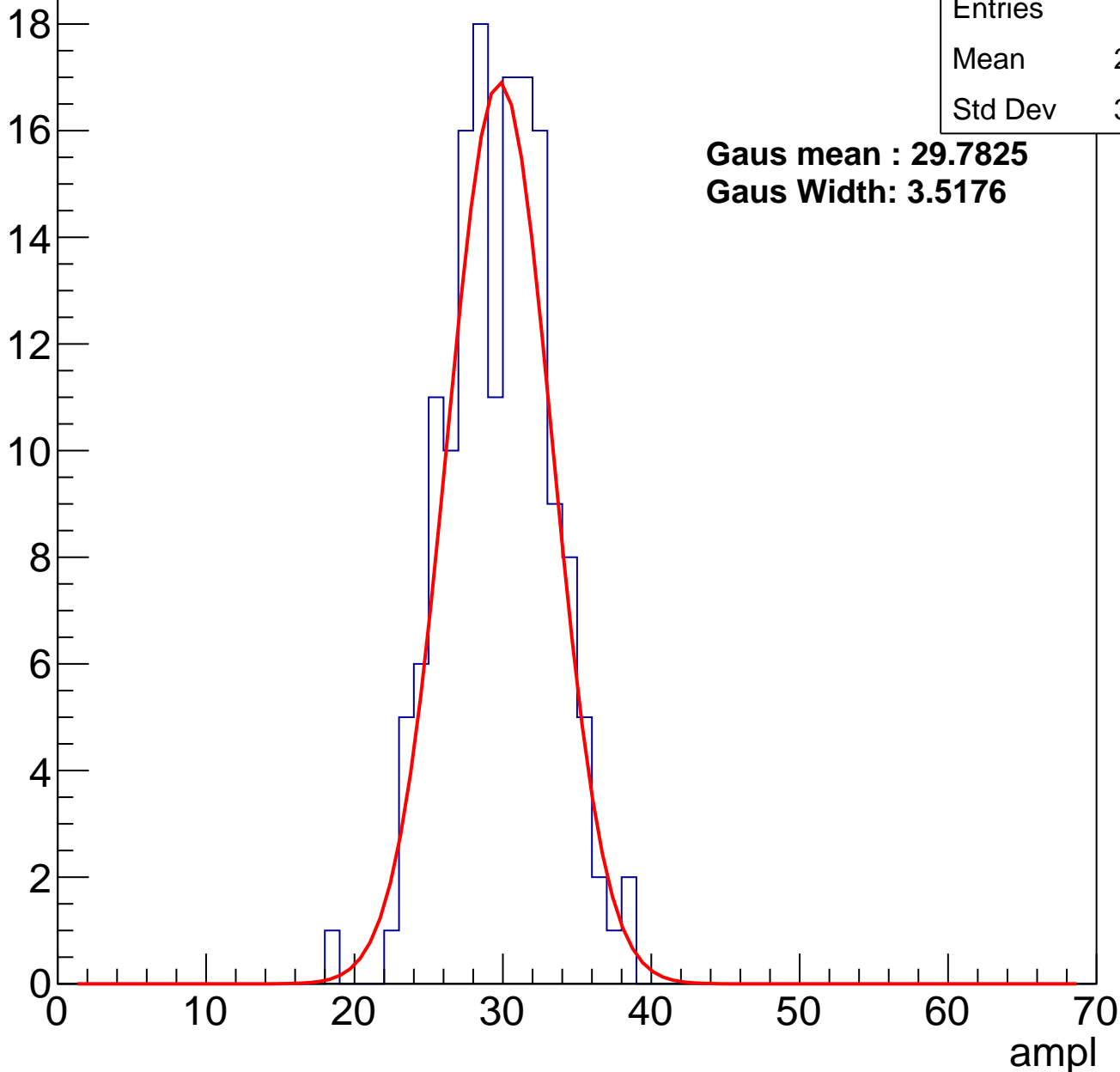
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	29.28
Std Dev	3.502

Gaus mean : 29.7825

Gaus Width: 3.5176

Entry



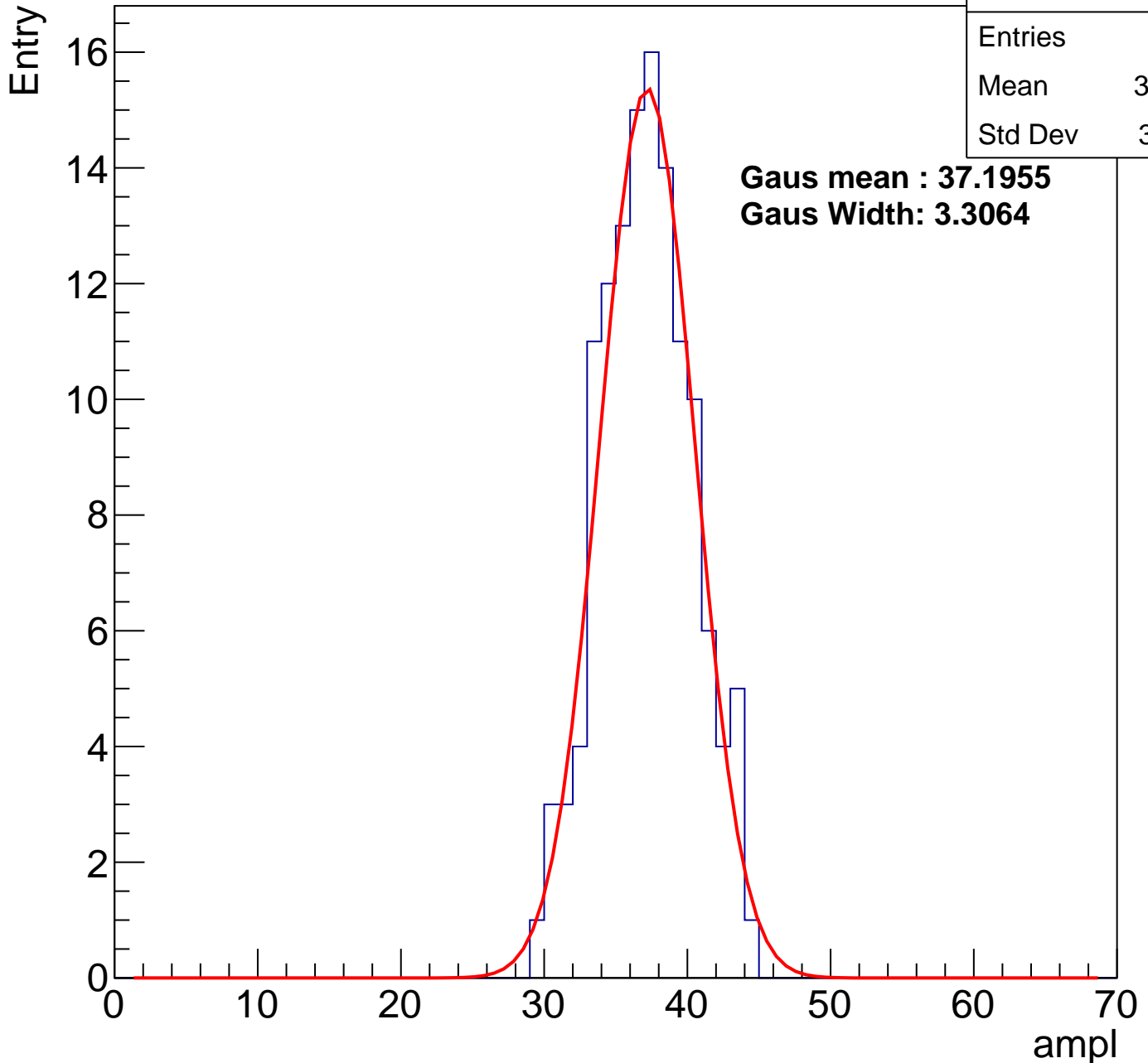
B0L101S, U19-ch65, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	36.68
Std Dev	3.211

Gaus mean : 37.1955

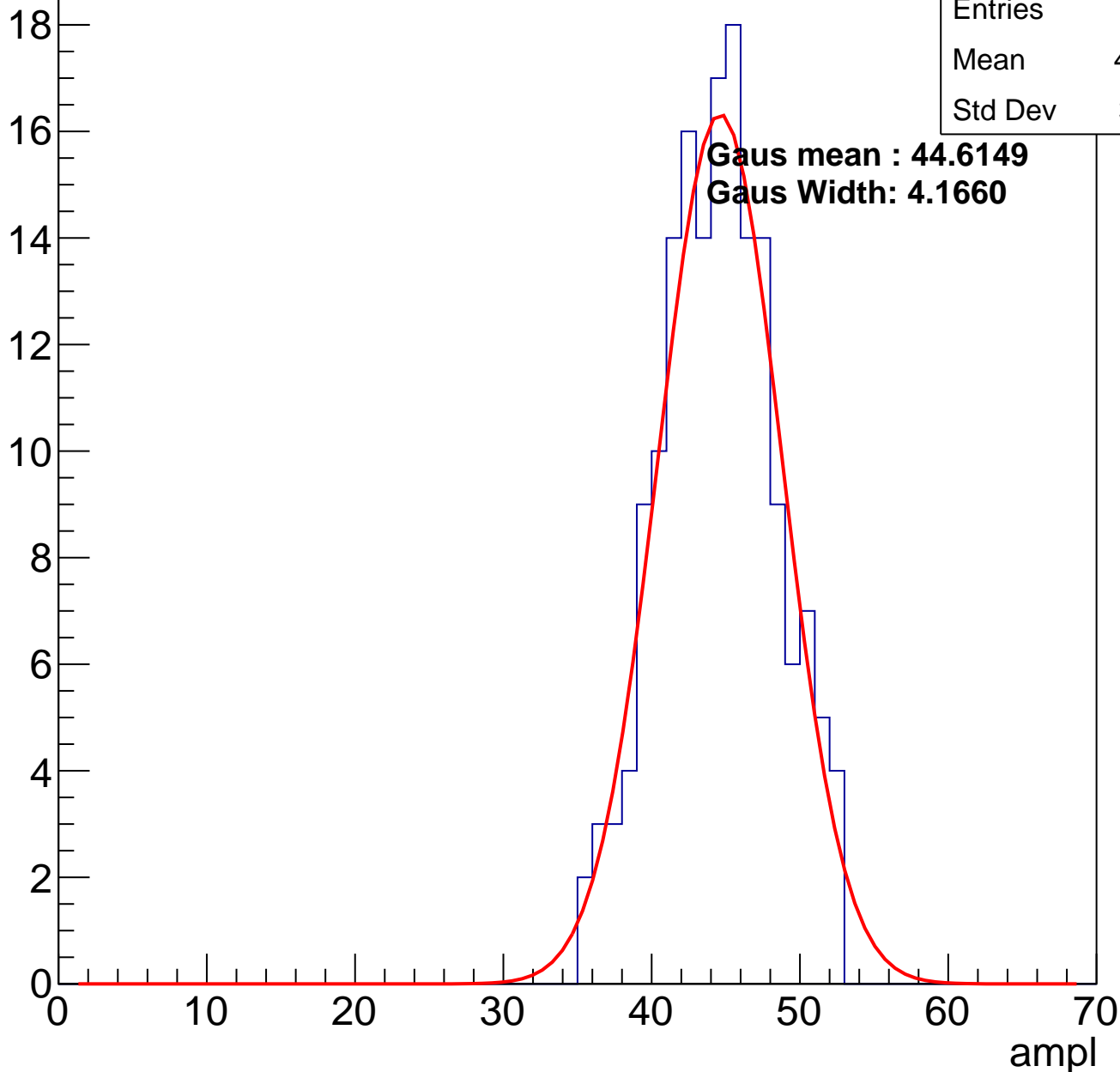
Gaus Width: 3.3064



B0L101S, U19-ch65, adc2

calib_packv5_042523_0143.root, FC#1, port C1

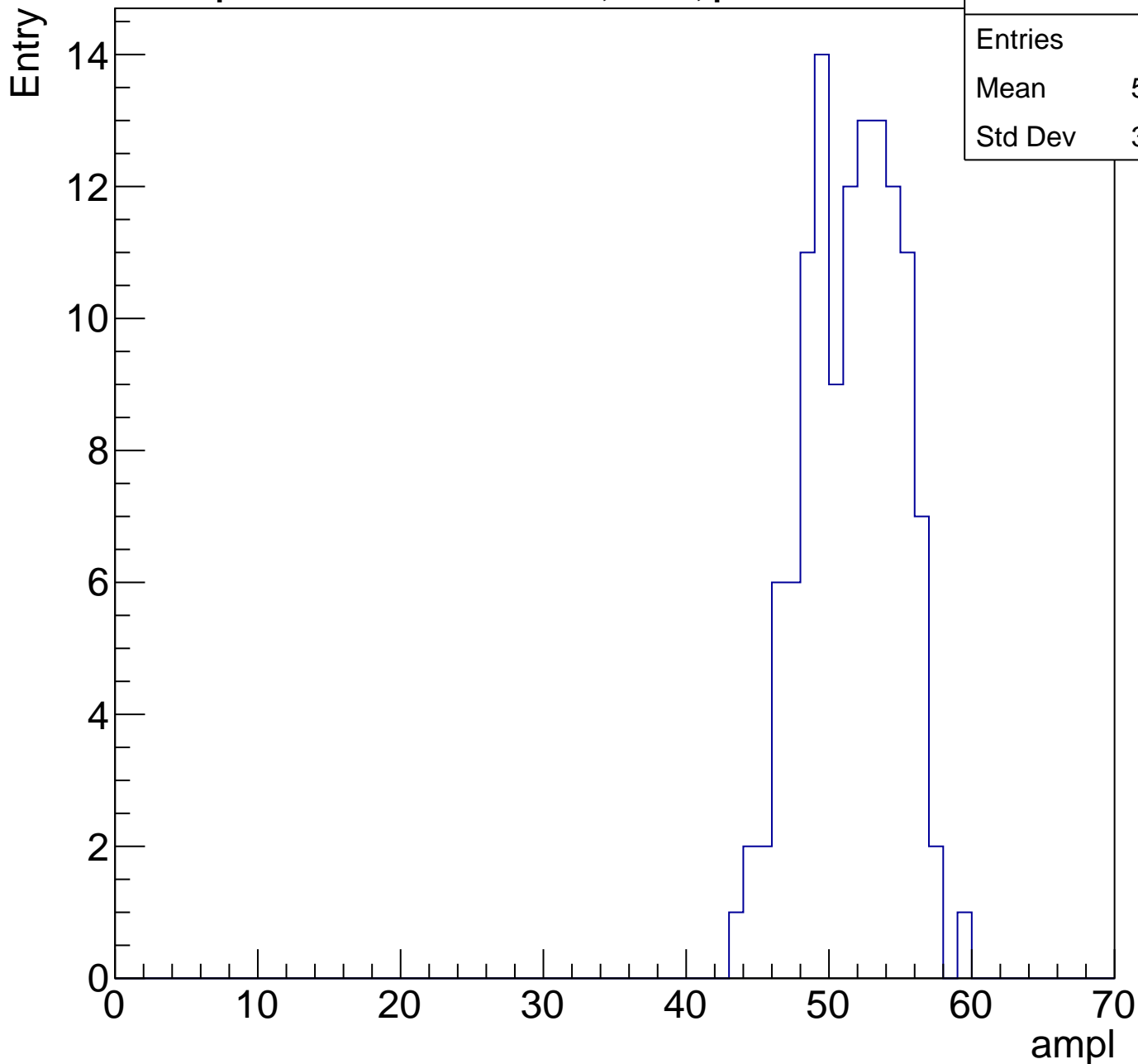
Entry



B0L101S, U19-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	51.13
Std Dev	3.267

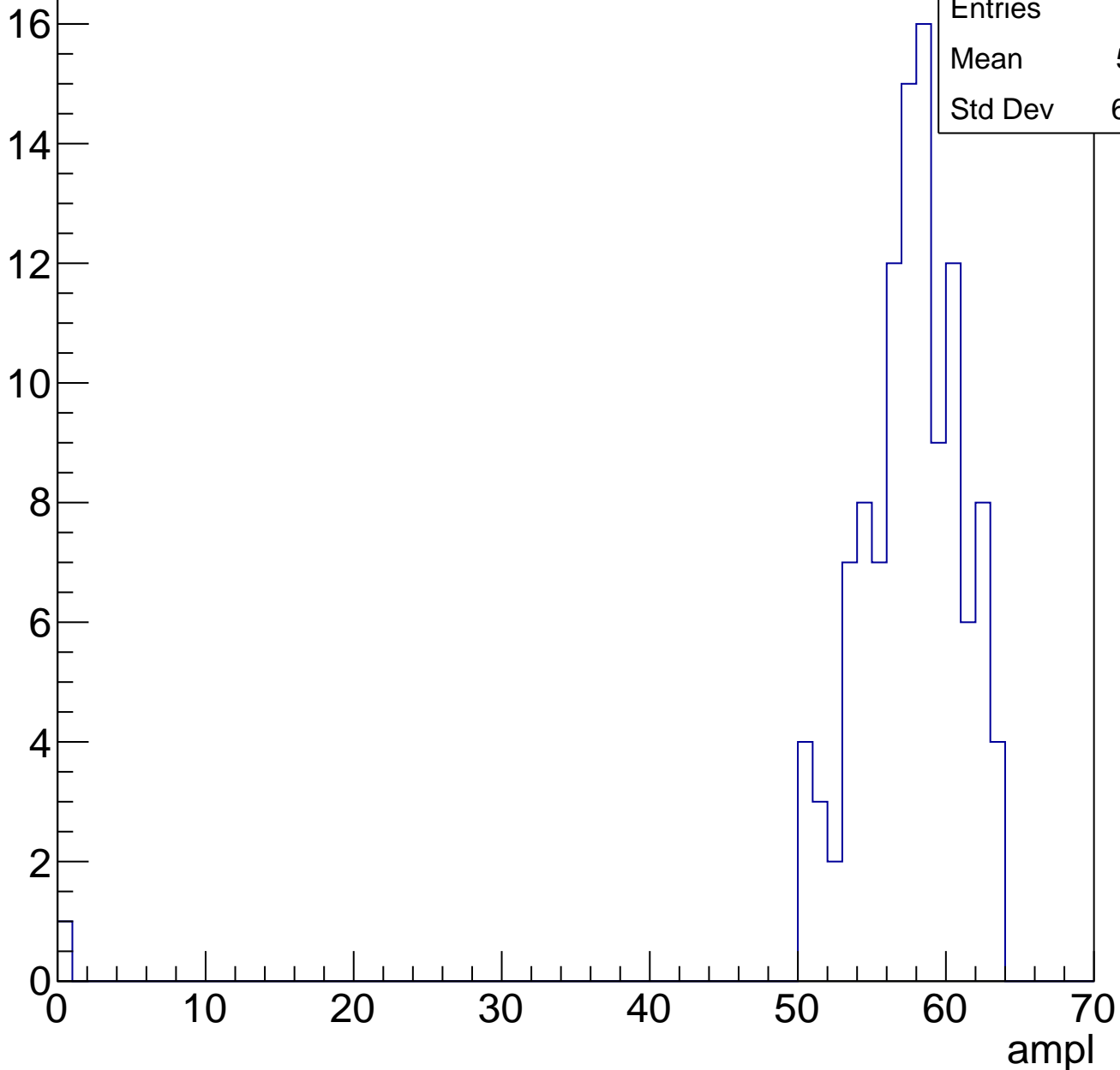


B0L101S, U19-ch65, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	56.71
Std Dev	6.225

Entry



B0L101S, U19-ch65, adc5

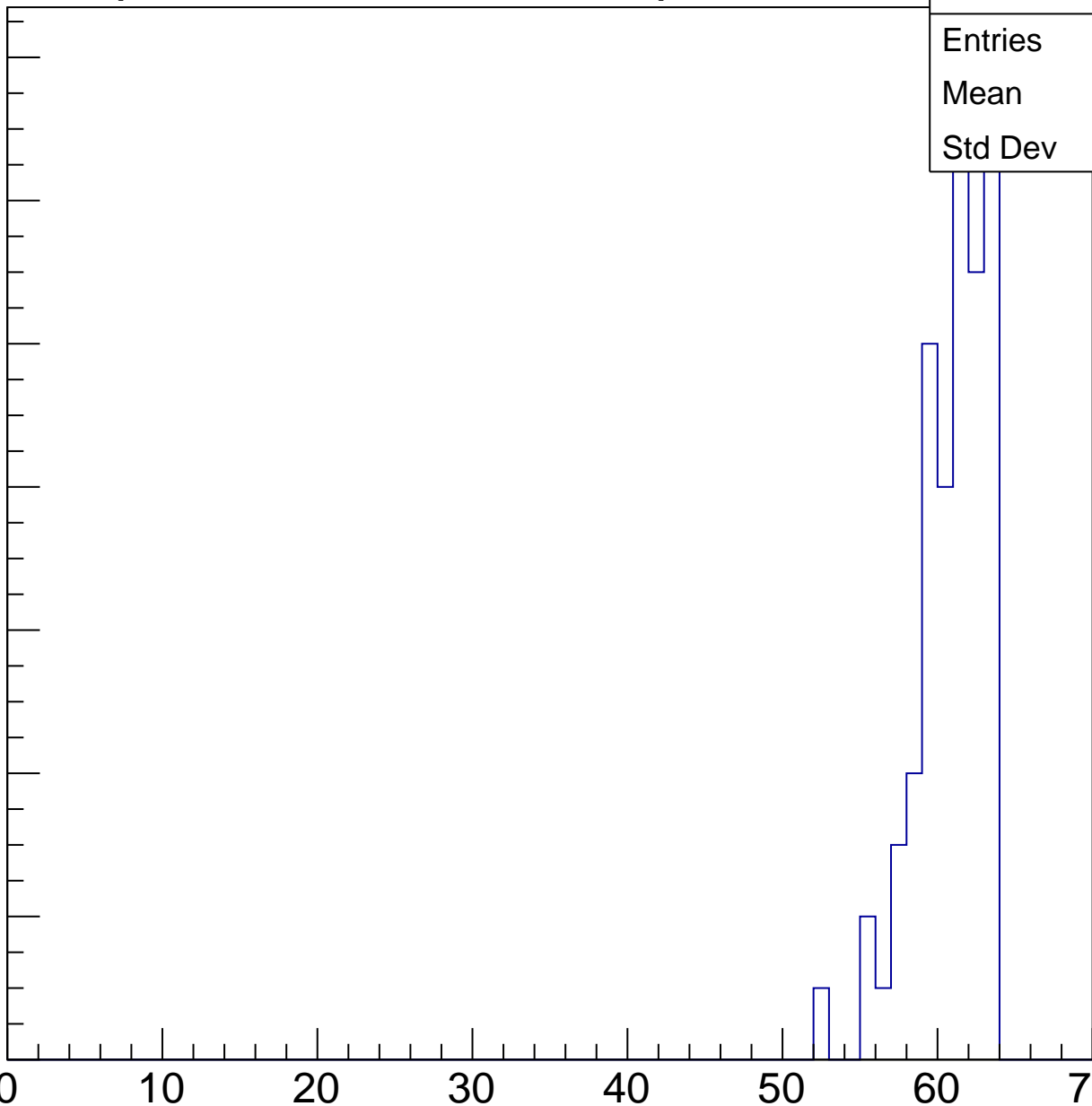
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	60.43
Std Dev	2.284

ampl



B0L101S, U19-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L101S, U19-ch66, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	28.03
Std Dev	3.638

Gaus mean : 28.7591

Gaus Width: 3.3900

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

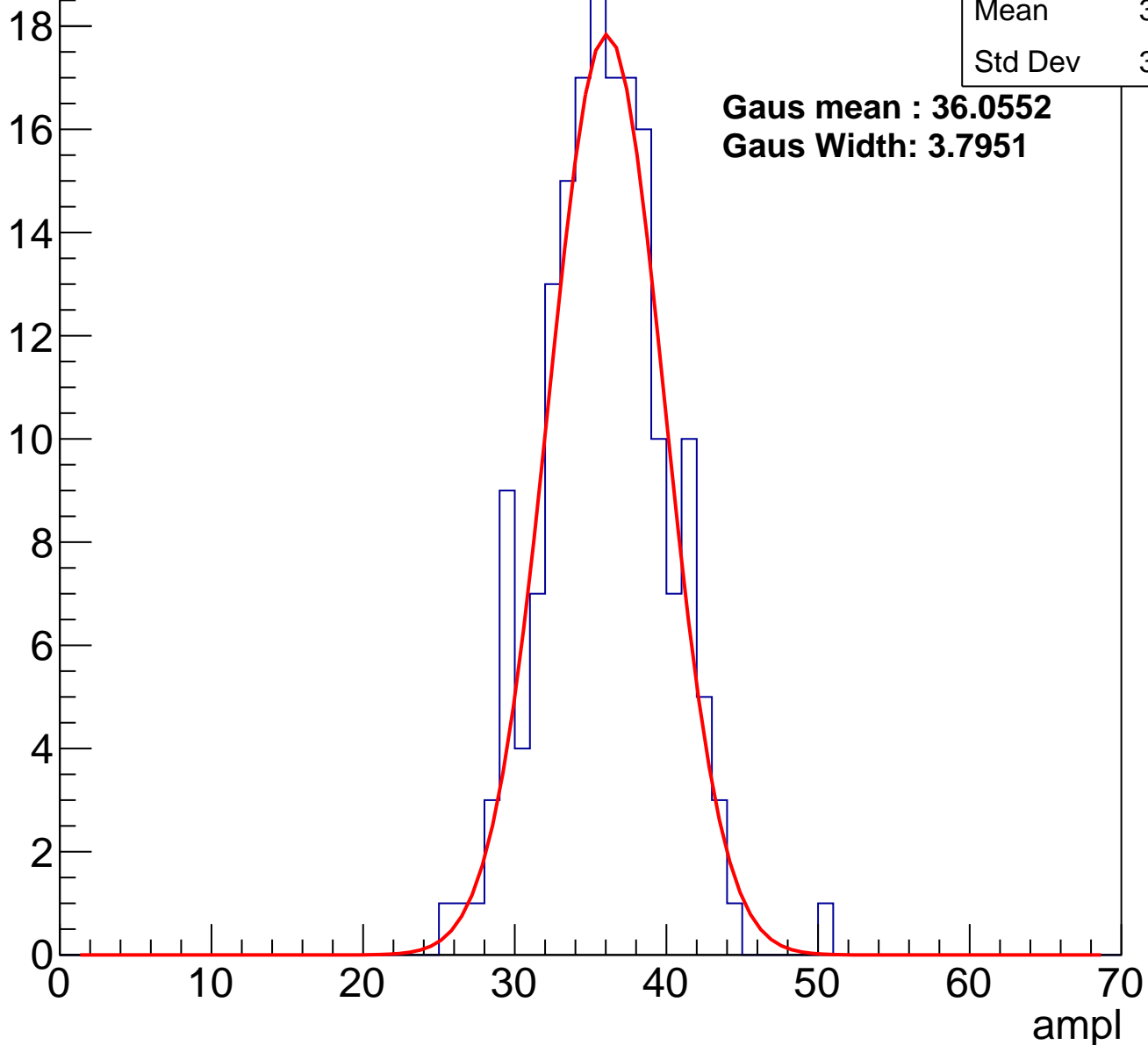
60

70

B0L101S, U19-ch66, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch66, adc2

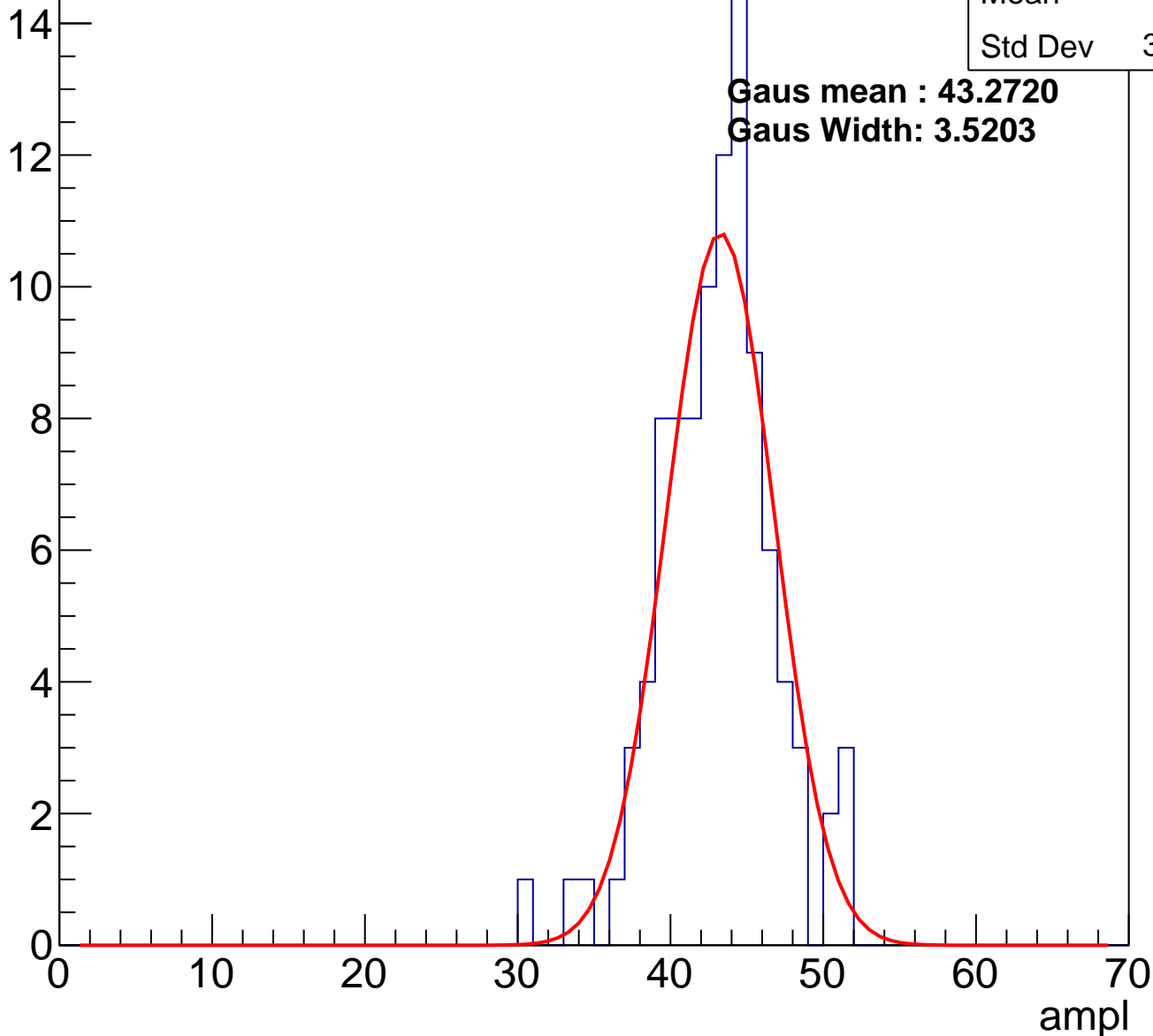
calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	42.61
Std Dev	3.717

Gaus mean : 43.2720

Gaus Width: 3.5203

Entry



B0L101S, U19-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

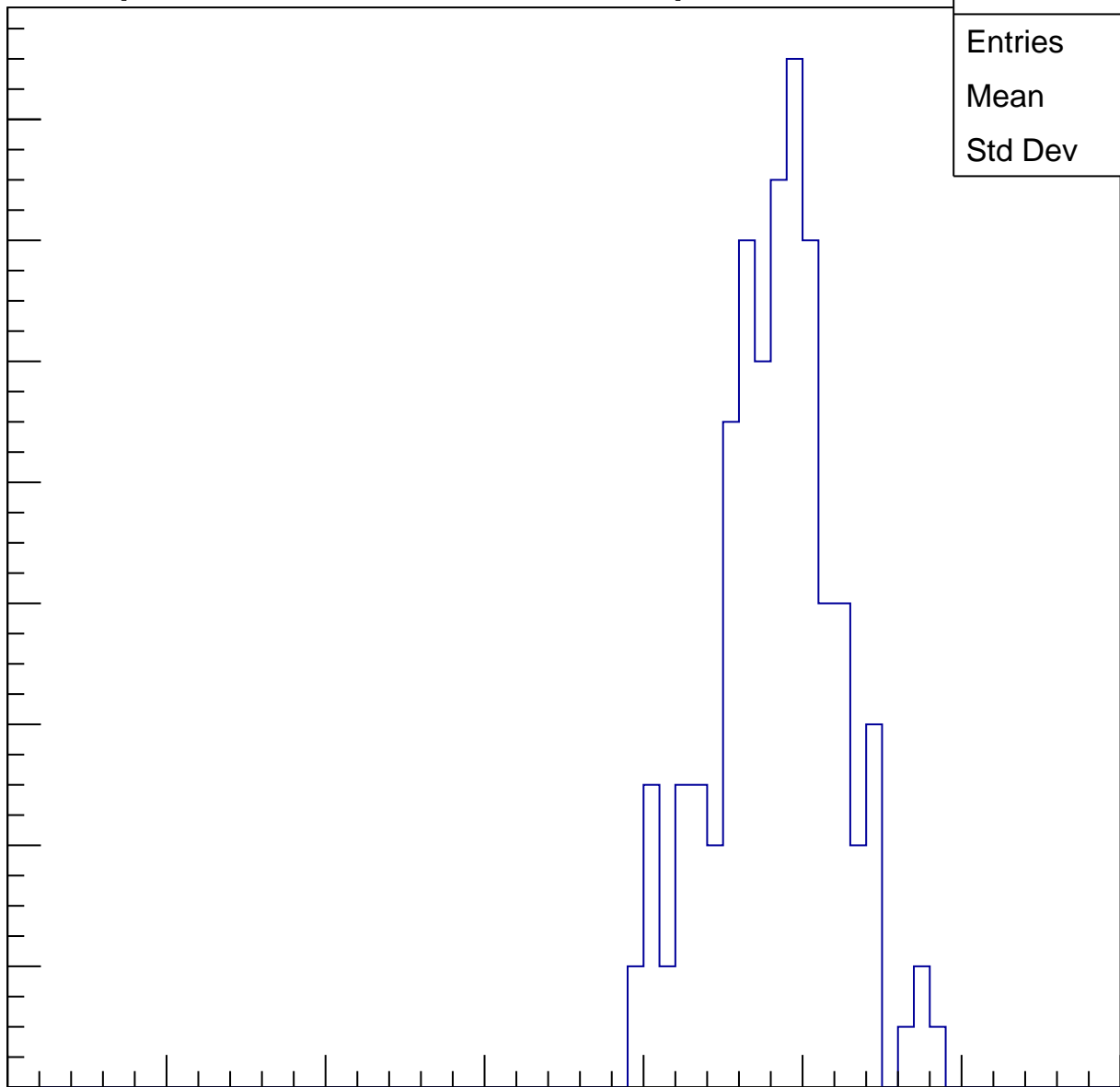
Entries	136
Mean	47.83
Std Dev	3.861

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

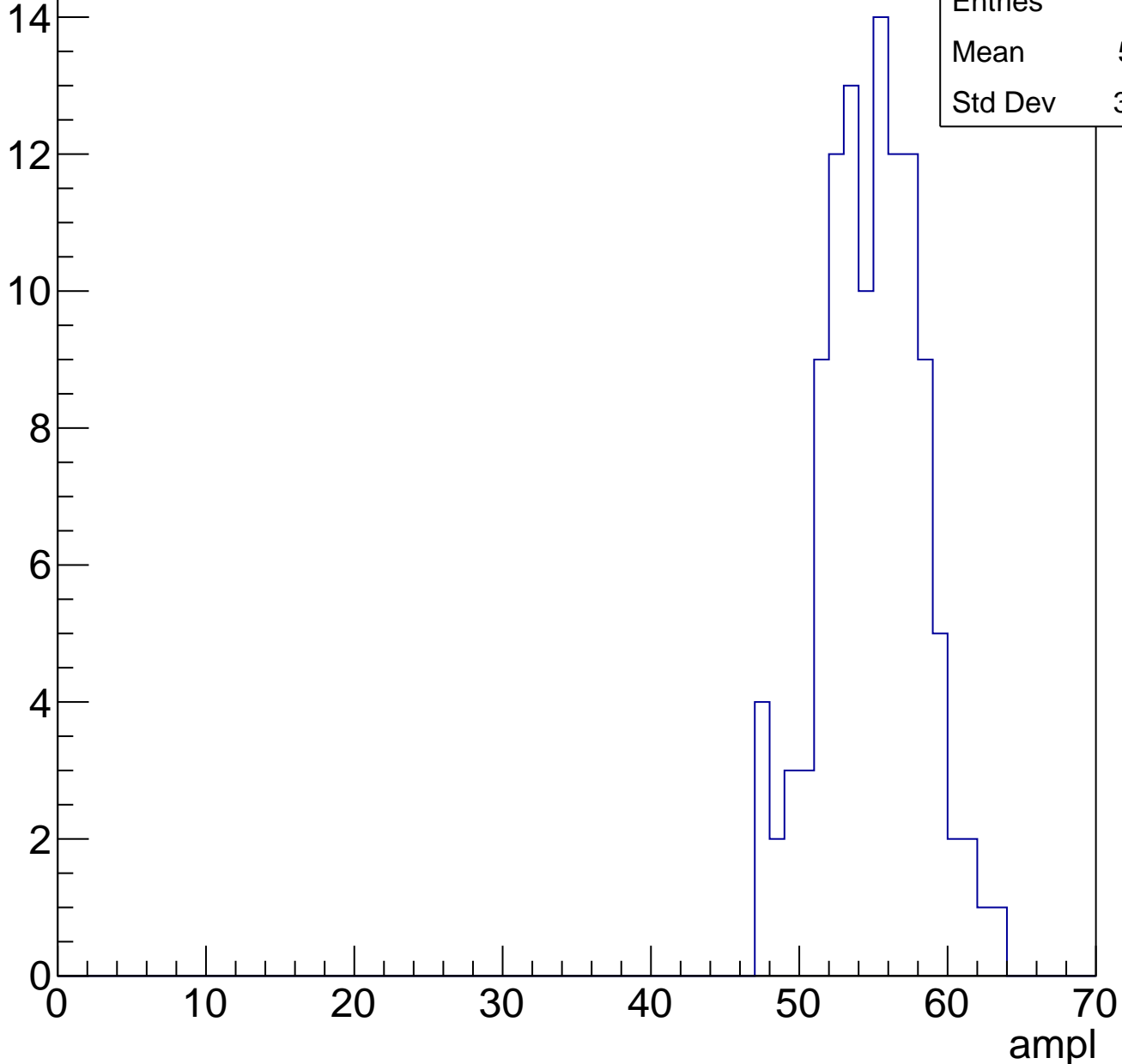
ampl



B0L101S, U19-ch66, adc4

calib_packv5_042523_0143.root, FC#1, port C1

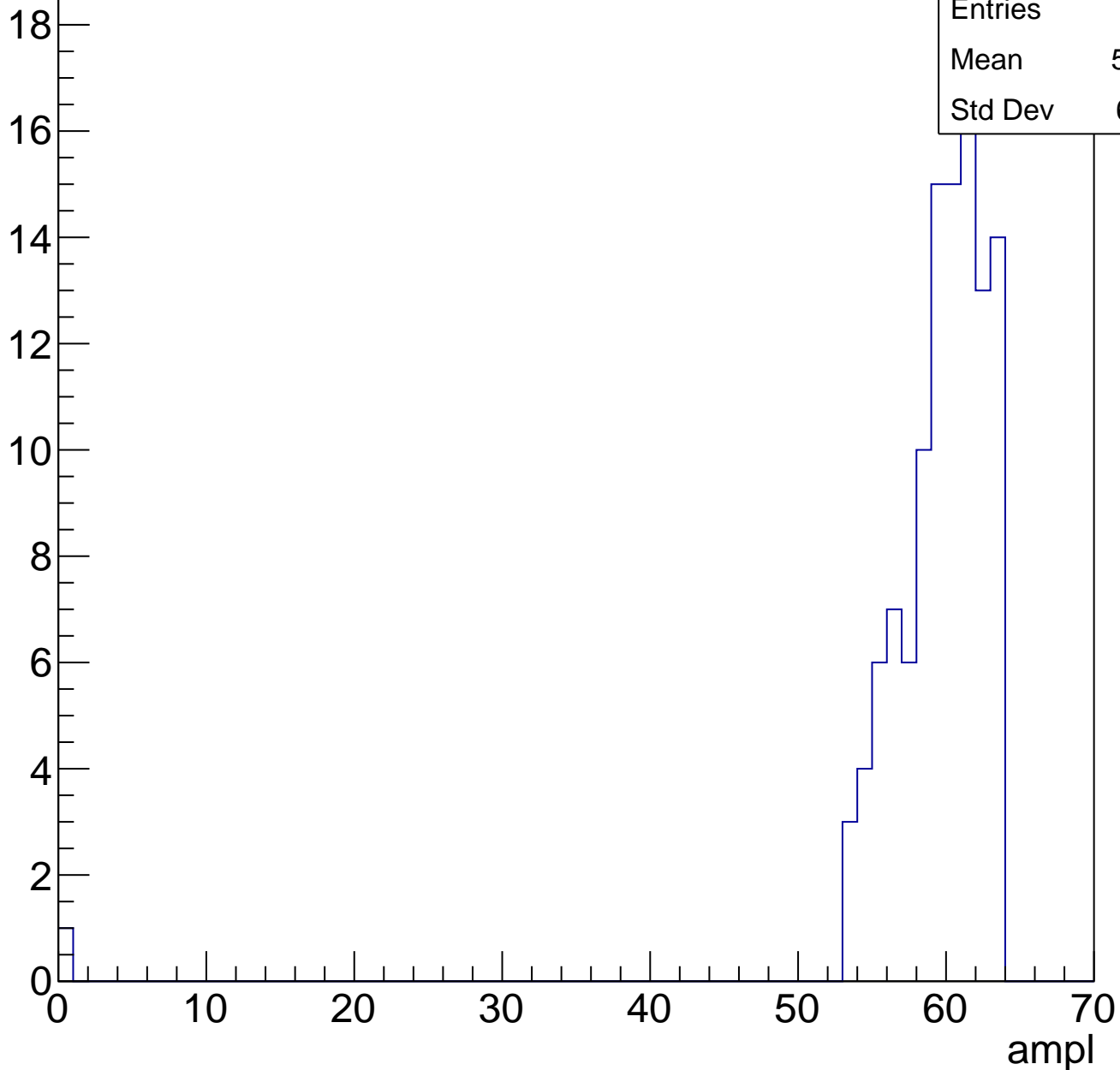
Entry



B0L101S, U19-ch66, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

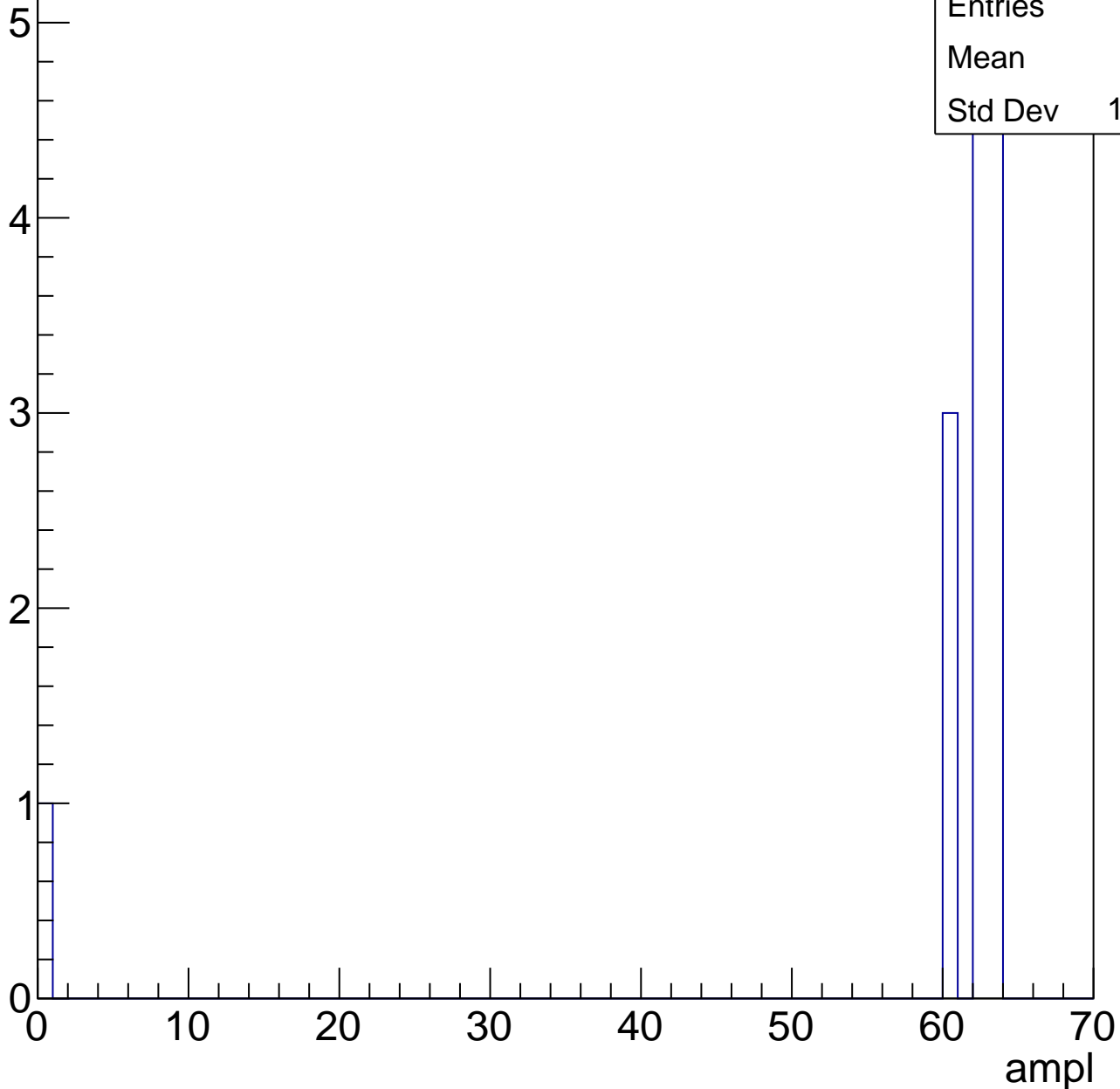


B0L101S, U19-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.5
Std Dev	15.99



B0L101S, U19-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch67, adc0

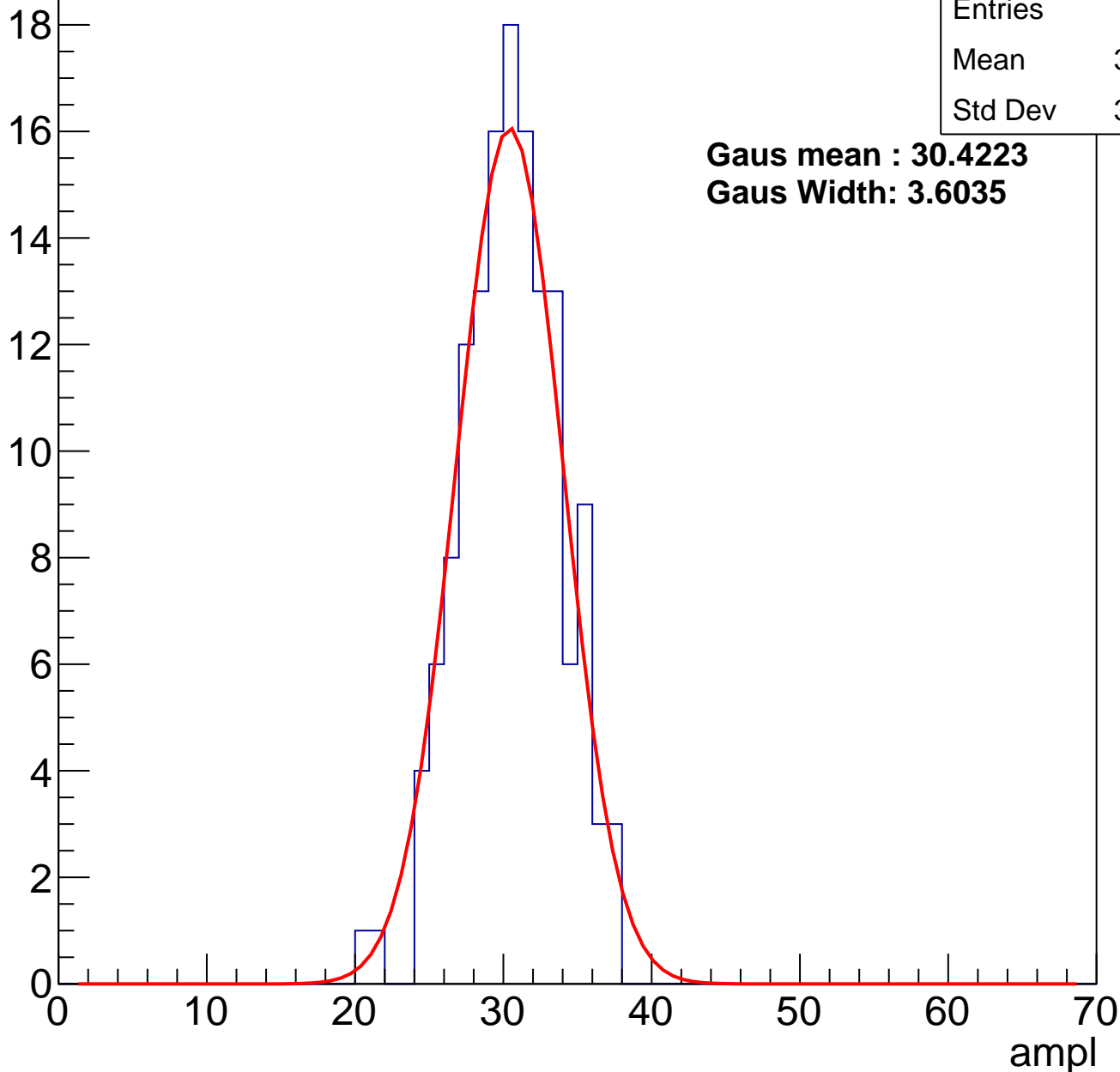
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	30.04
Std Dev	3.293

Gaus mean : 30.4223

Gaus Width: 3.6035

Entry

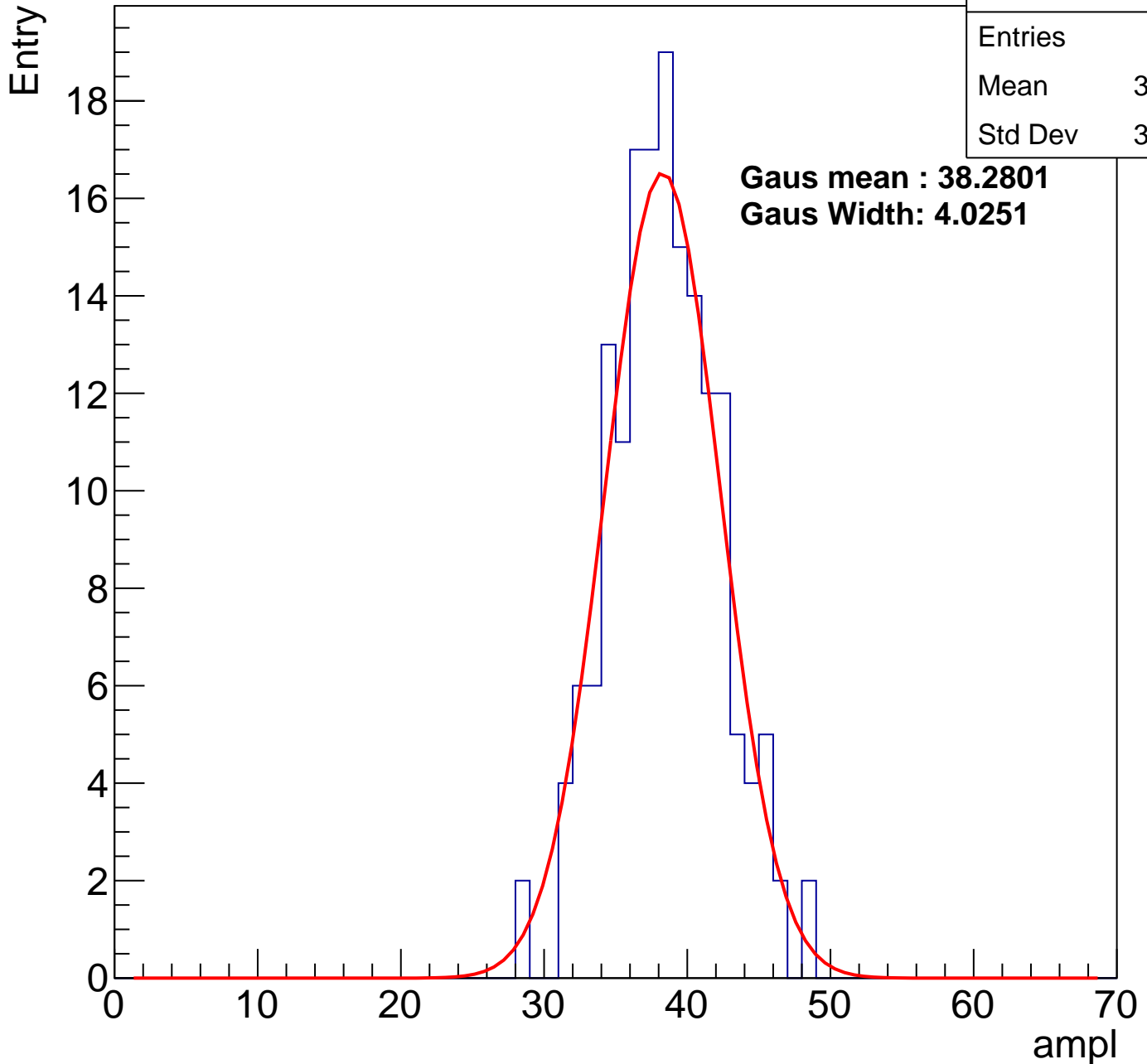


B0L101S, U19-ch67, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	37.98
Std Dev	3.776

Gaus mean : 38.2801
Gaus Width: 4.0251



B0L101S, U19-ch67, adc2

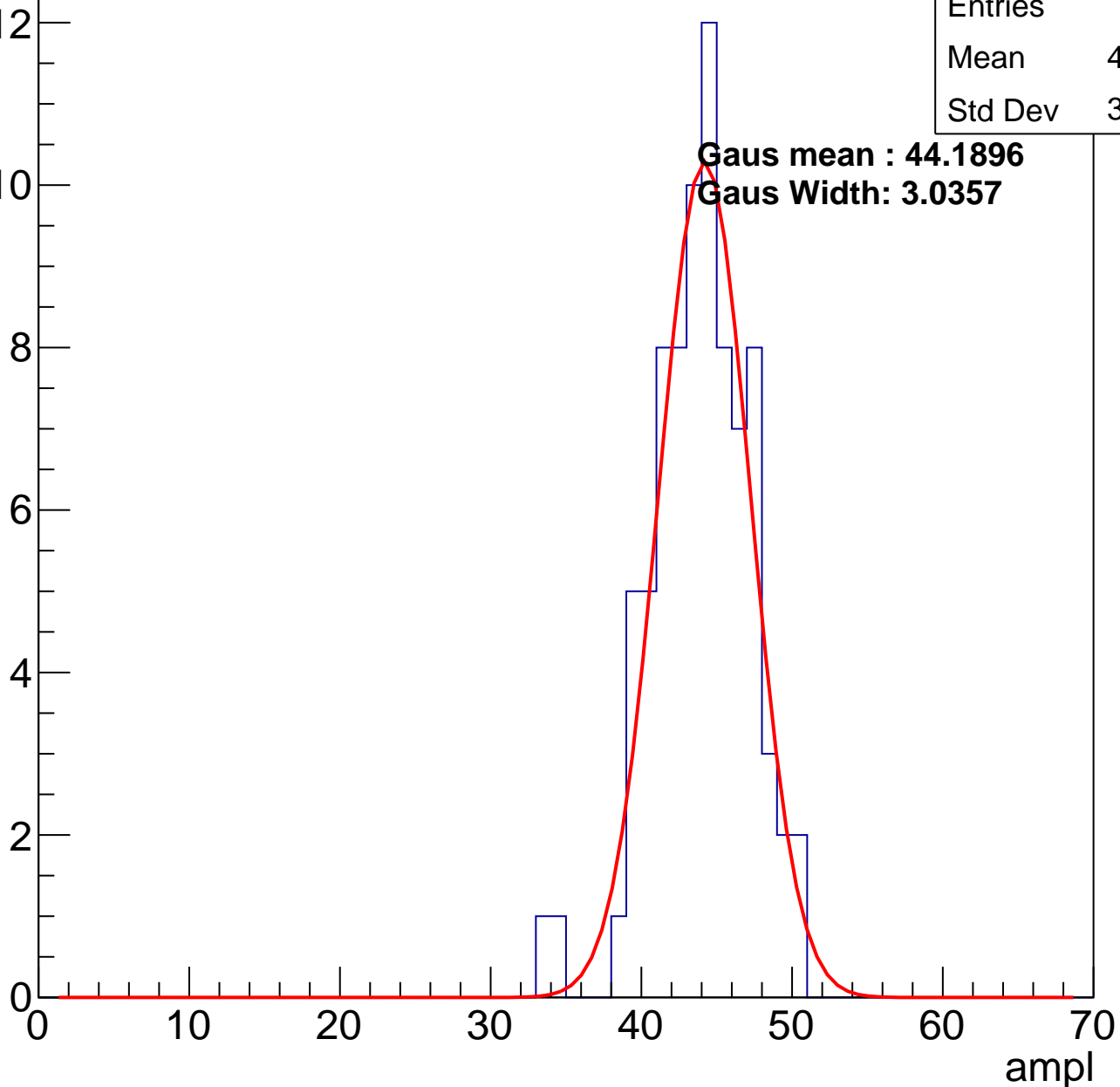
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	81
Mean	43.48
Std Dev	3.205

Gaus mean : 44.1896

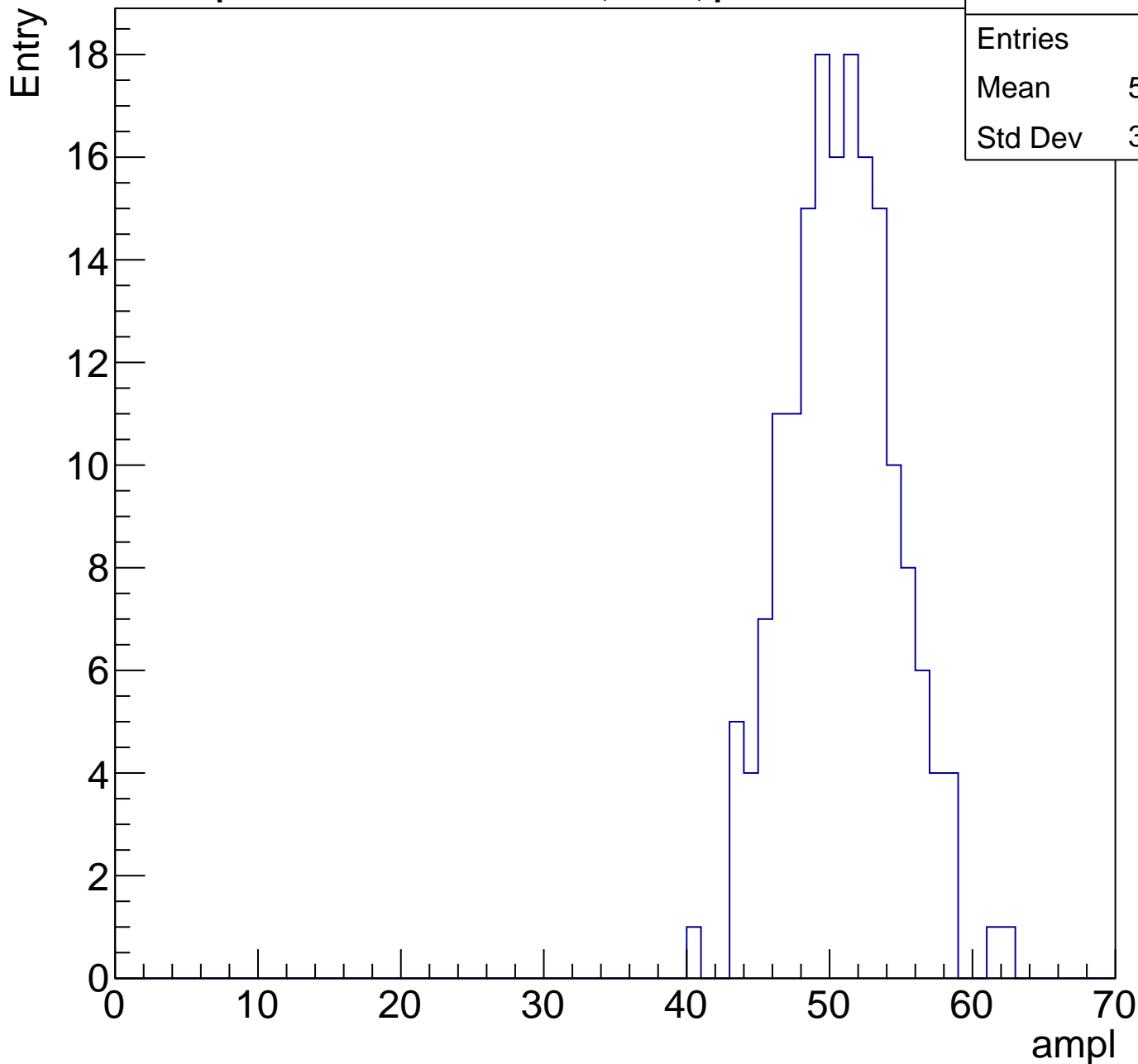
Gaus Width: 3.0357



B0L101S, U19-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	50.38
Std Dev	3.825



B0L101S, U19-ch67, adc4

calib_packv5_042523_0143.root, FC#1, port C1

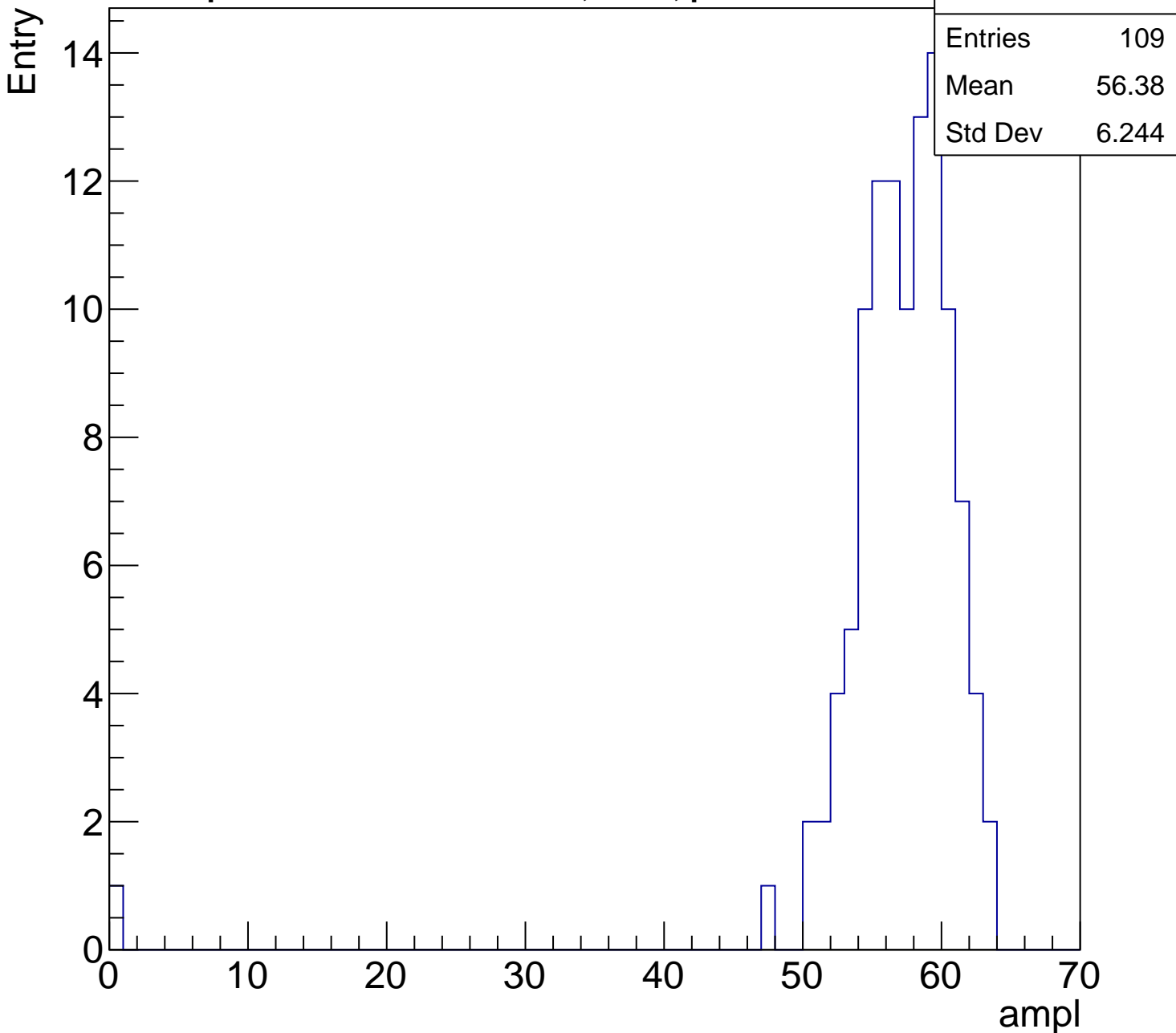
Entries	109
Mean	56.38
Std Dev	6.244

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch67, adc5

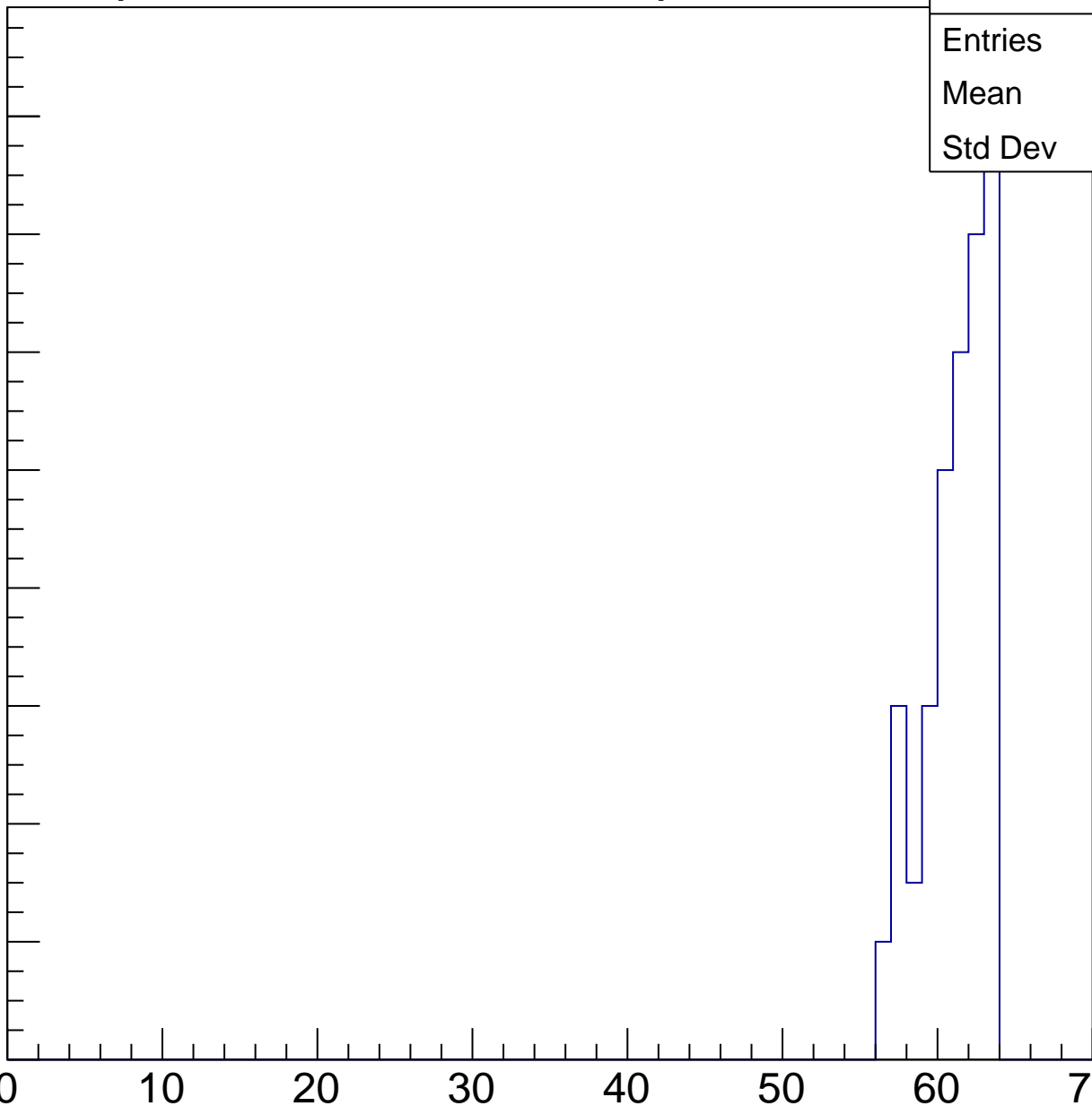
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	70
Mean	60.76
Std Dev	2.017

ampl



B0L101S, U19-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch68, adc0

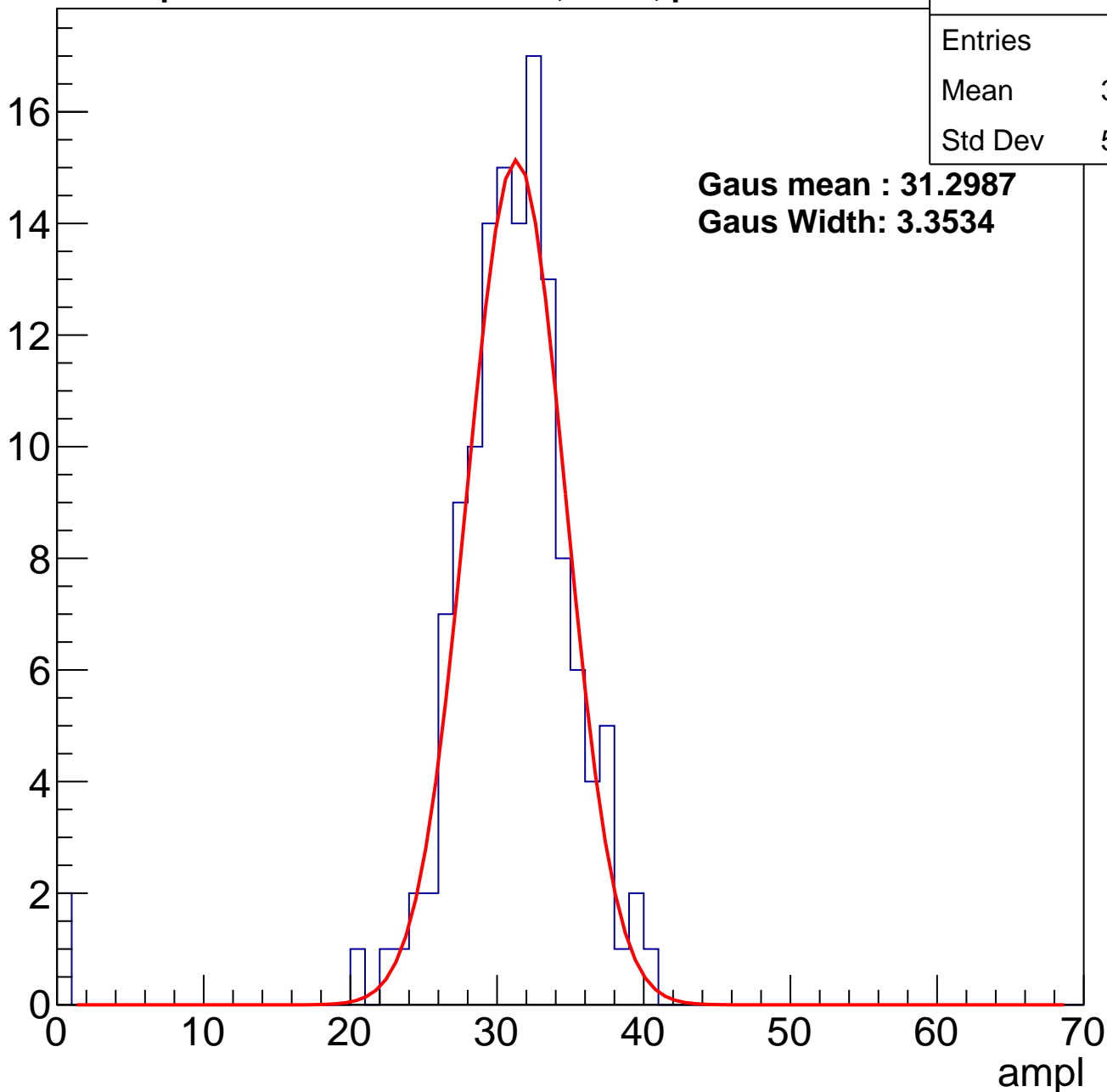
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	30.36
Std Dev	5.133

Gaus mean : 31.2987

Gaus Width: 3.3534

Entry



B0L101S, U19-ch68, adc1

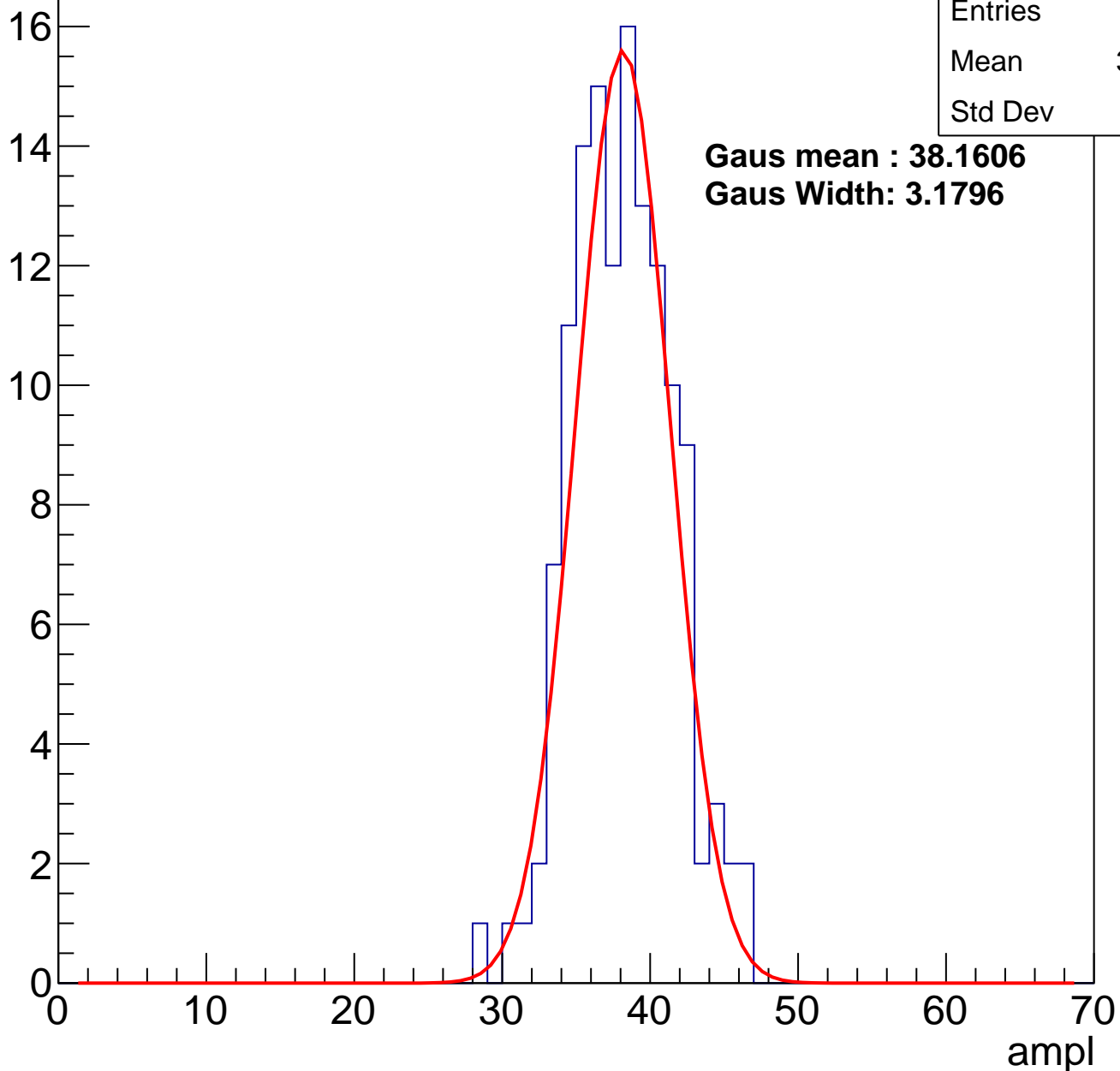
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	37.71
Std Dev	3.36

Gaus mean : 38.1606

Gaus Width: 3.1796

Entry



B0L101S, U19-ch68, adc2

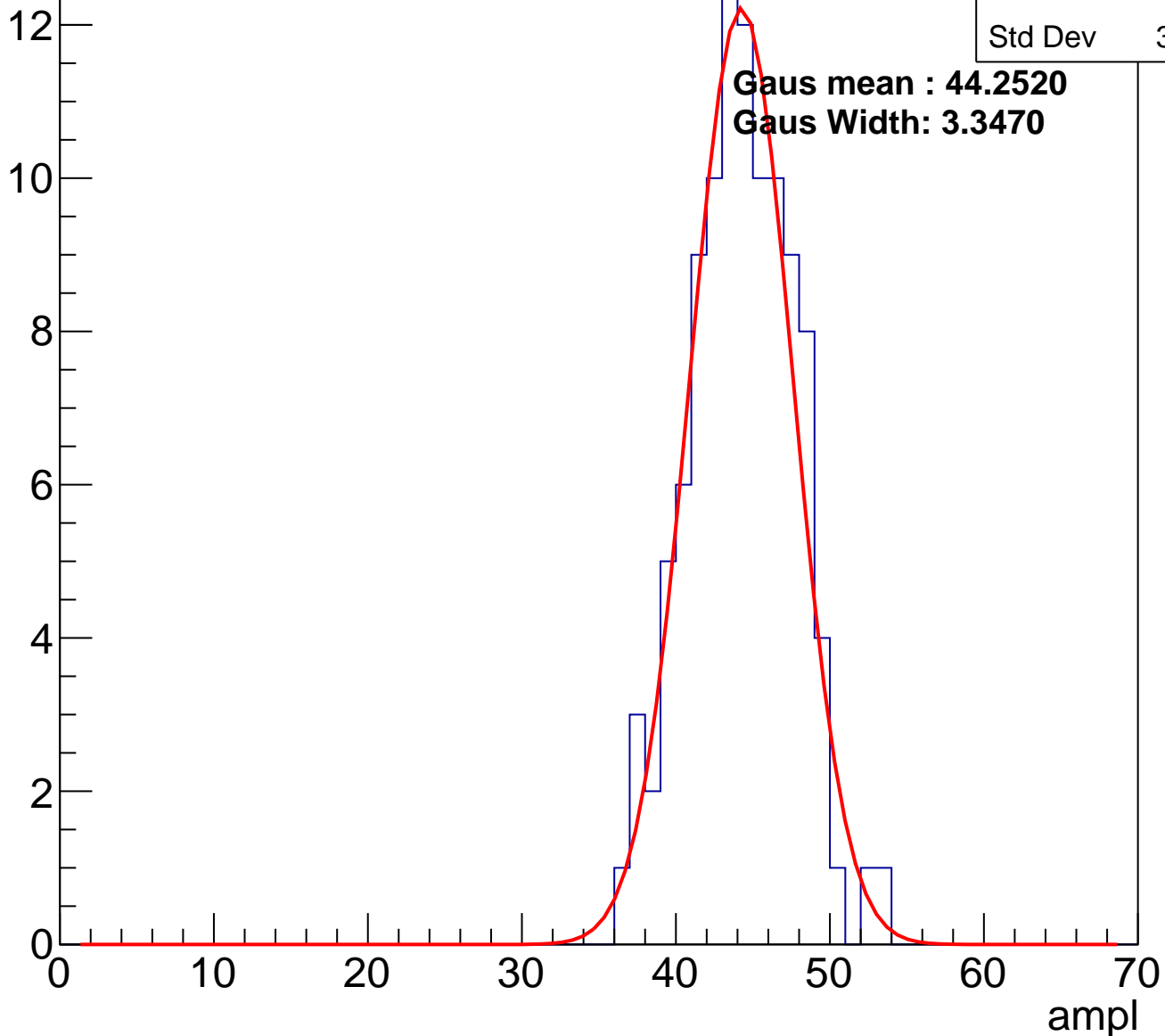
calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	43.83
Std Dev	3.345

Gaus mean : 44.2520

Gaus Width: 3.3470

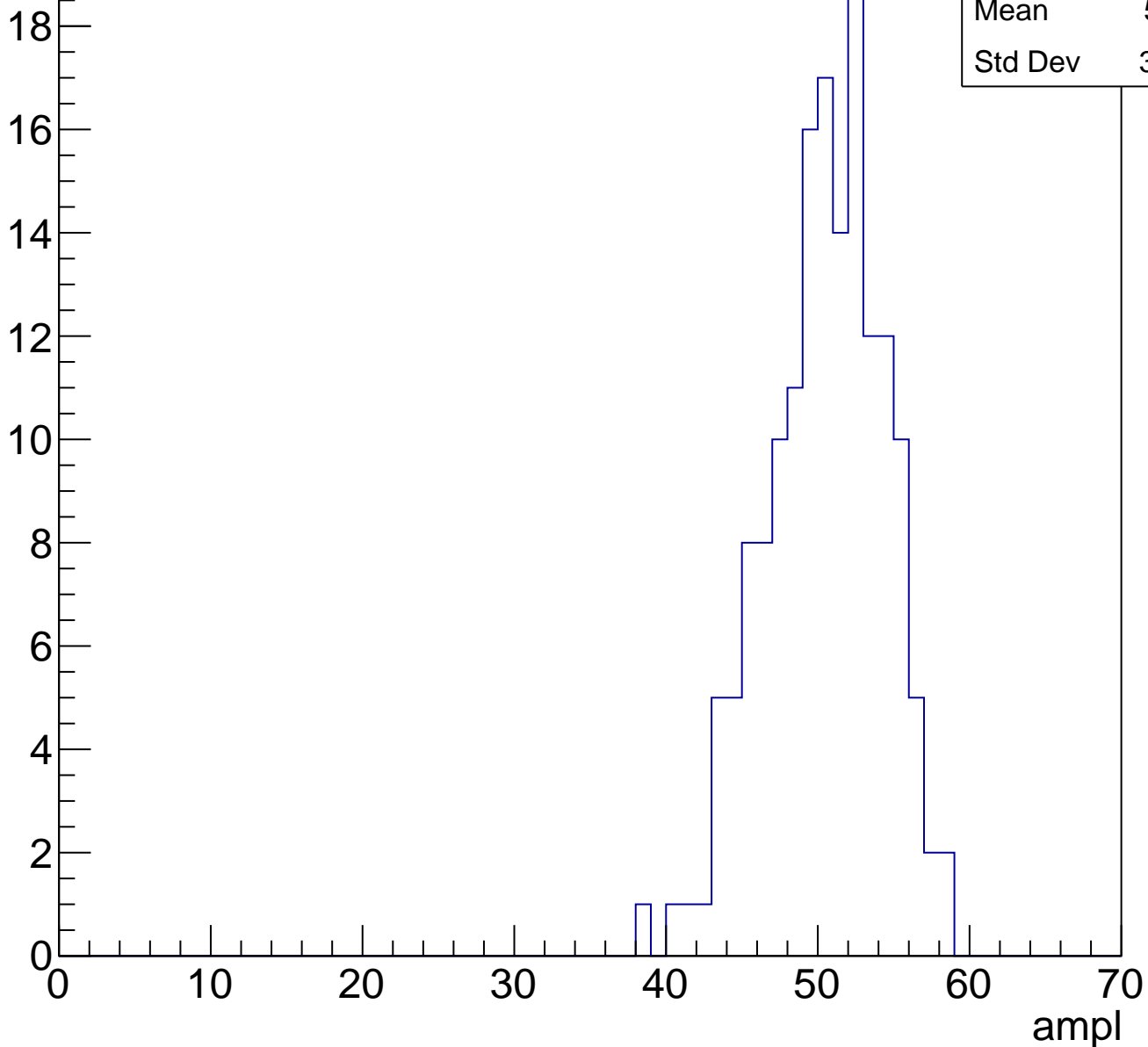
Entry



B0L101S, U19-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

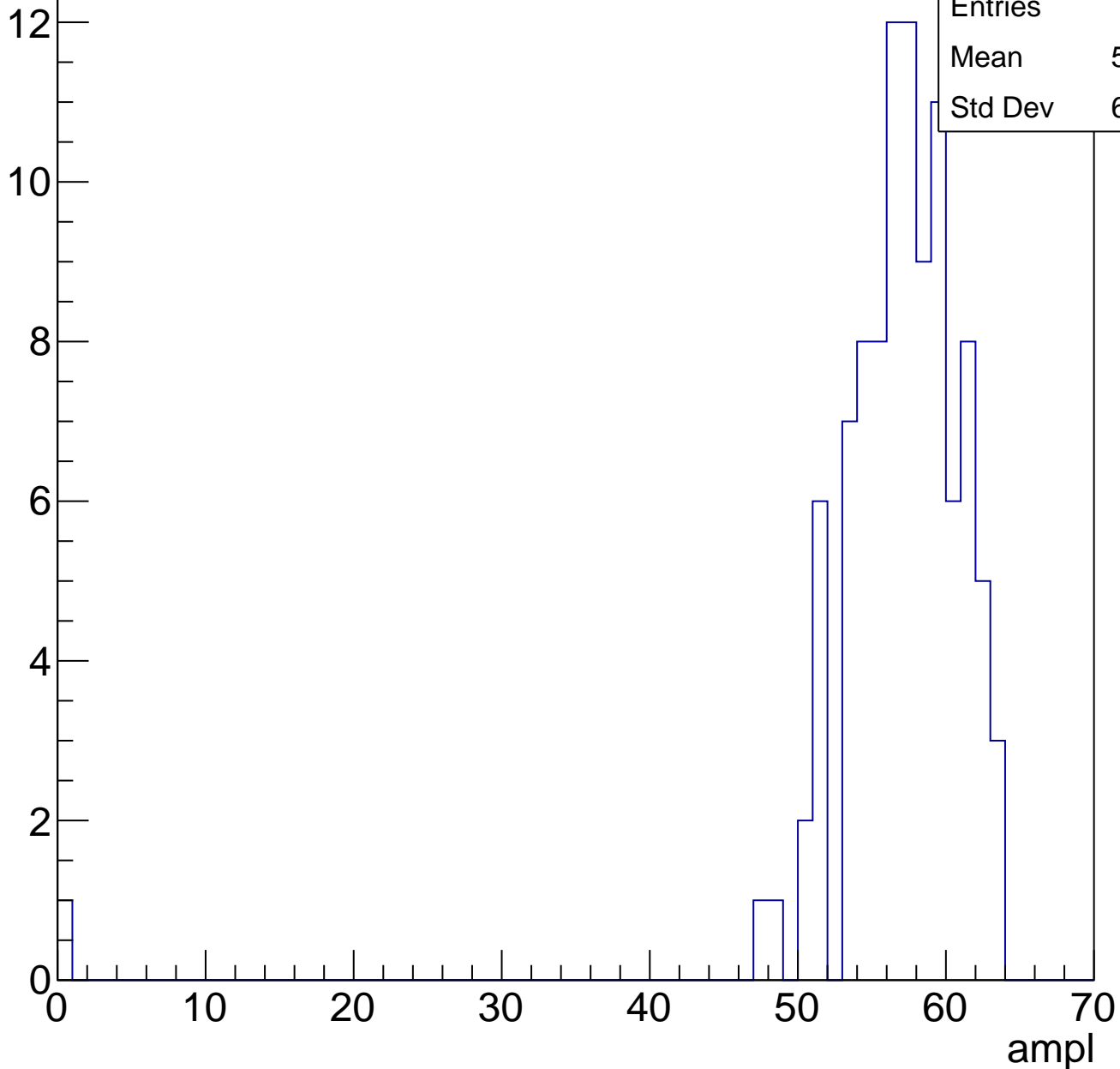


B0L101S, U19-ch68, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	56.18
Std Dev	6.613

Entry



B0L101S, U19-ch68, adc5

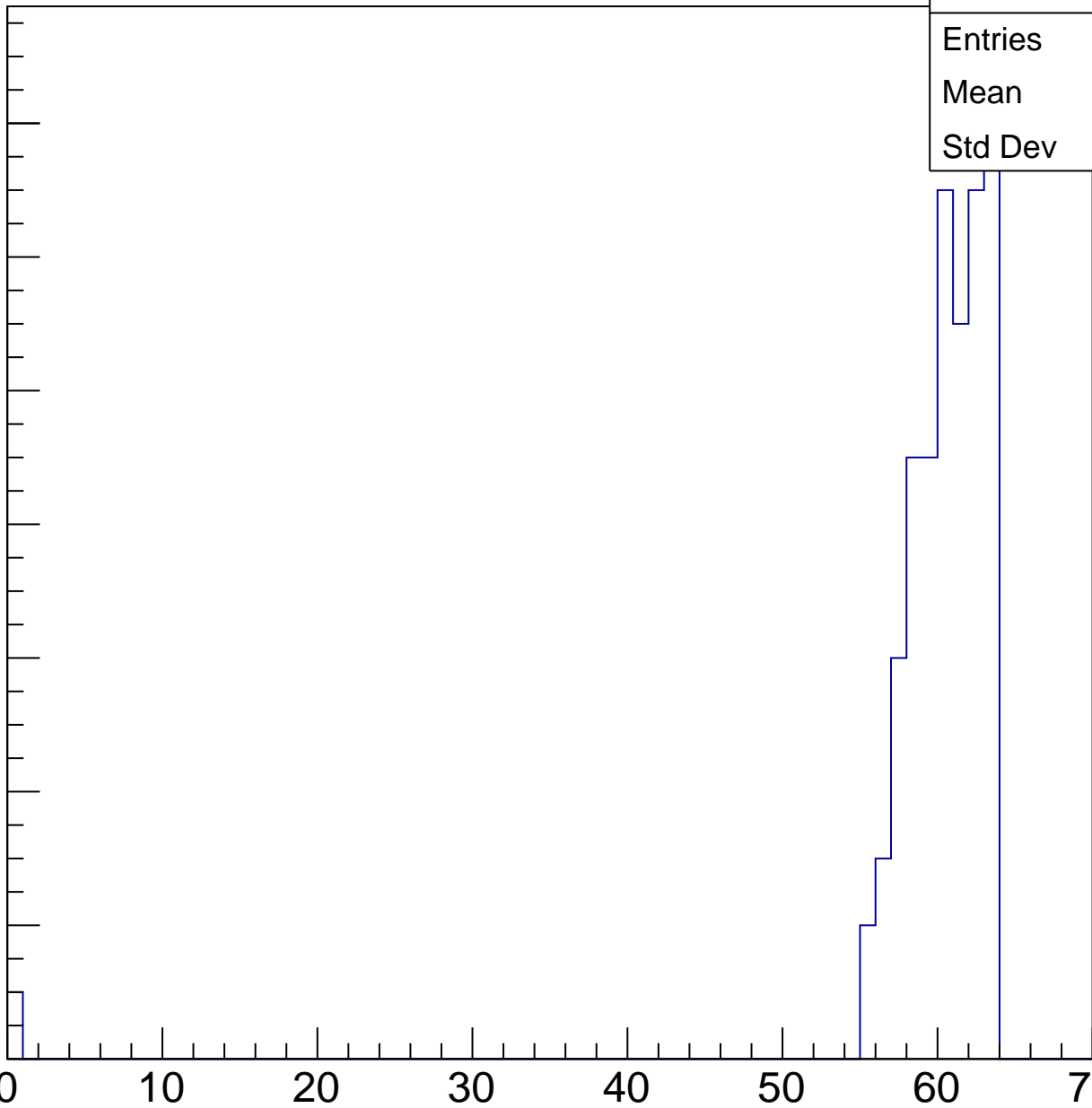
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	82
Mean	59.45
Std Dev	6.958

ampl



B0L101S, U19-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	61.6
Std Dev	1.02

B0L101S, U19-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



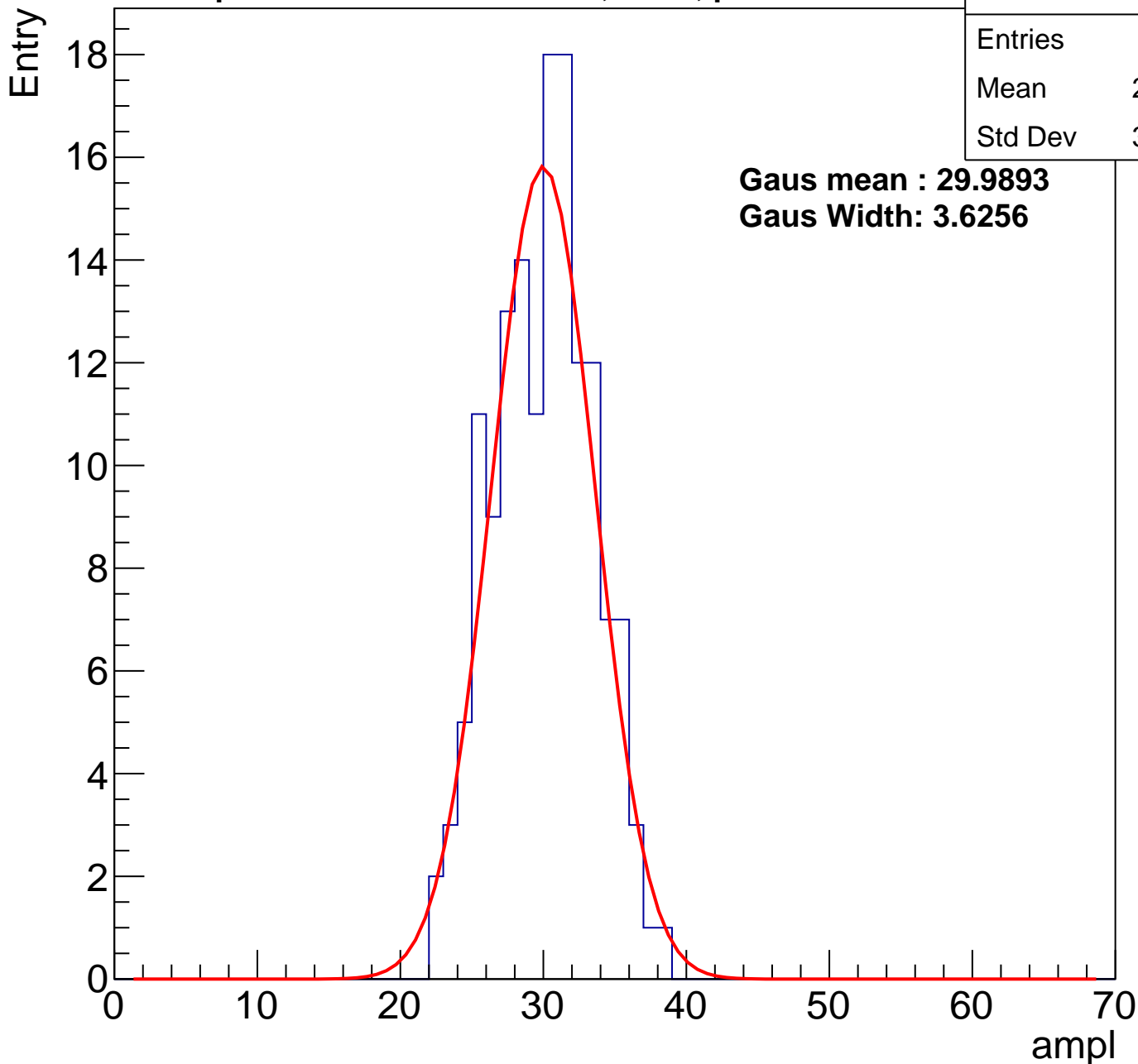
B0L101S, U19-ch69, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.58
Std Dev	3.415

Gaus mean : 29.9893

Gaus Width: 3.6256



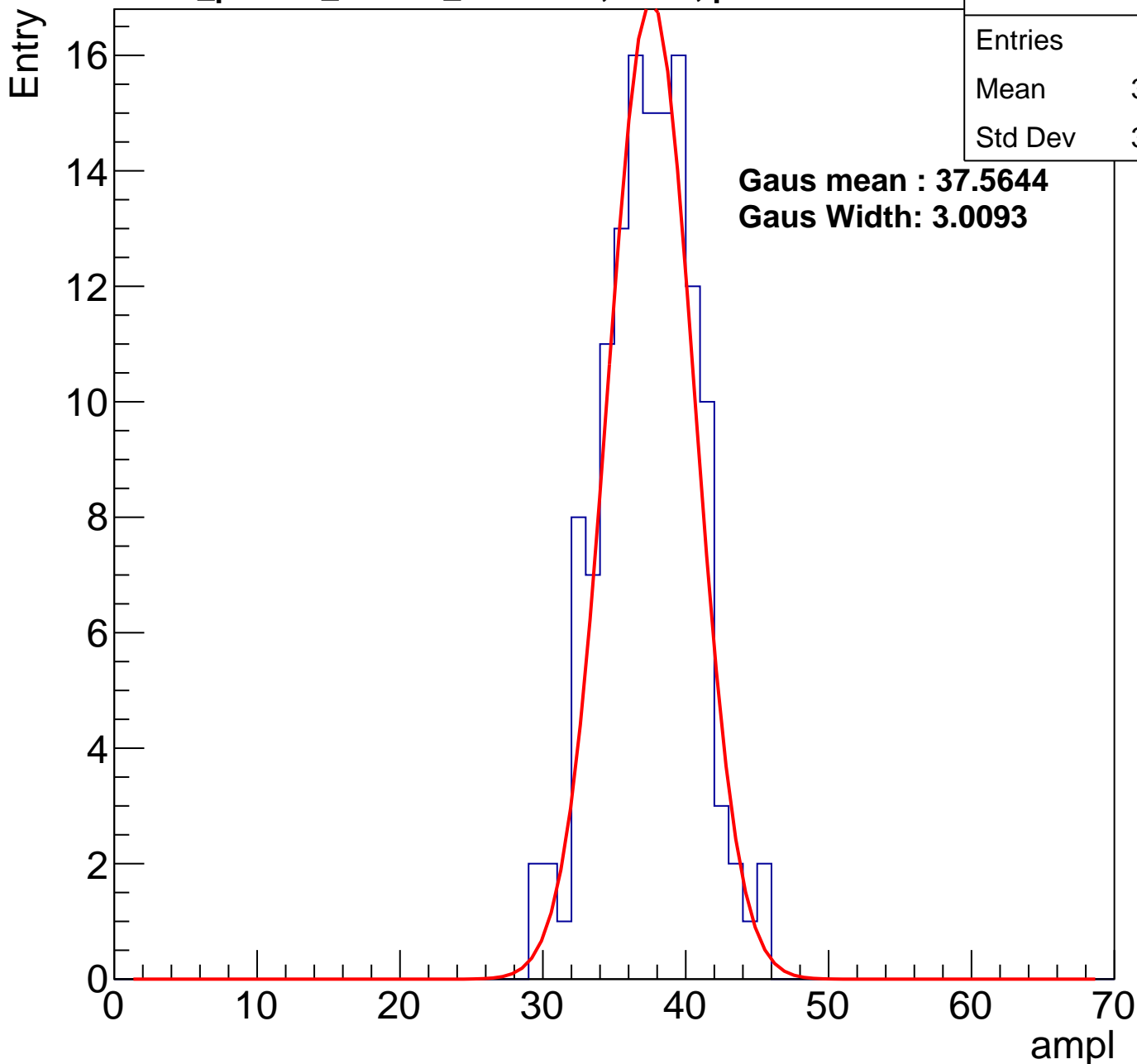
B0L101S, U19-ch69, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	36.96
Std Dev	3.222

Gaus mean : 37.5644

Gaus Width: 3.0093



B0L101S, U19-ch69, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	43.73
Std Dev	3.508

Gaus mean : 44.1853

Gaus Width: 3.4646

Entry

16

14

12

10

8

6

4

2

0

0

10

20

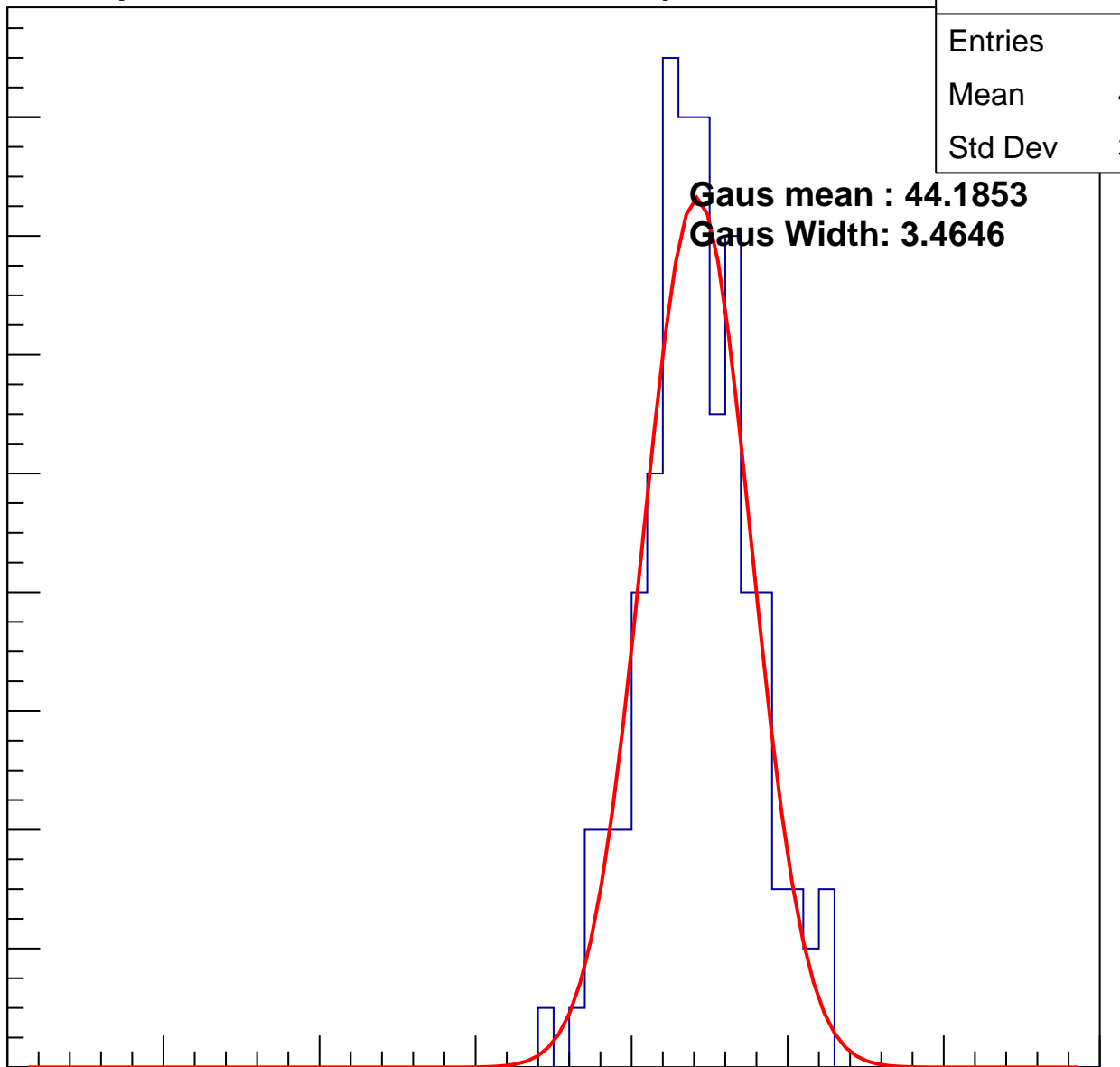
30

40

50

60

ampl

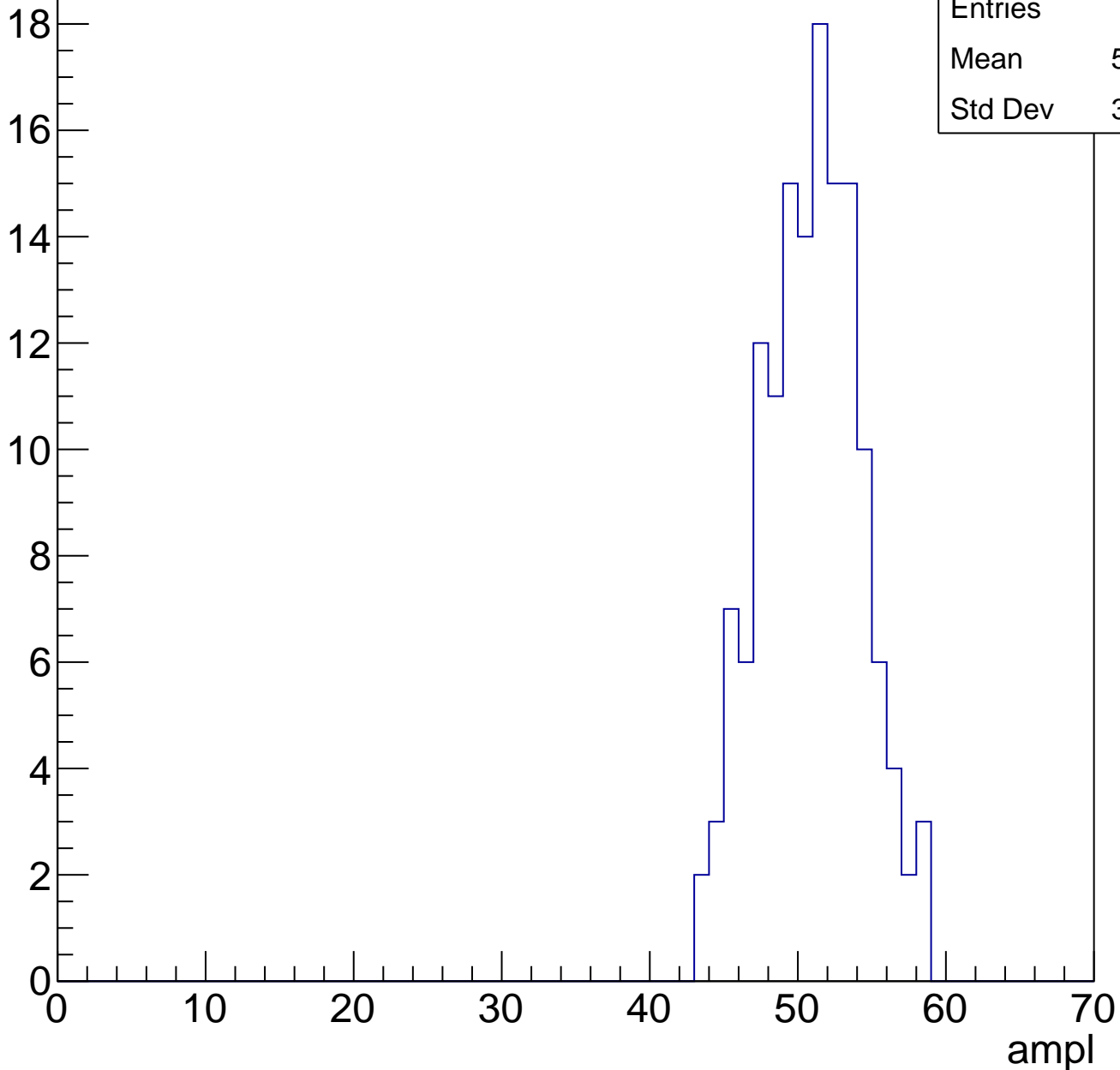


B0L101S, U19-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	50.43
Std Dev	3.319

Entry



B0L101S, U19-ch69, adc4

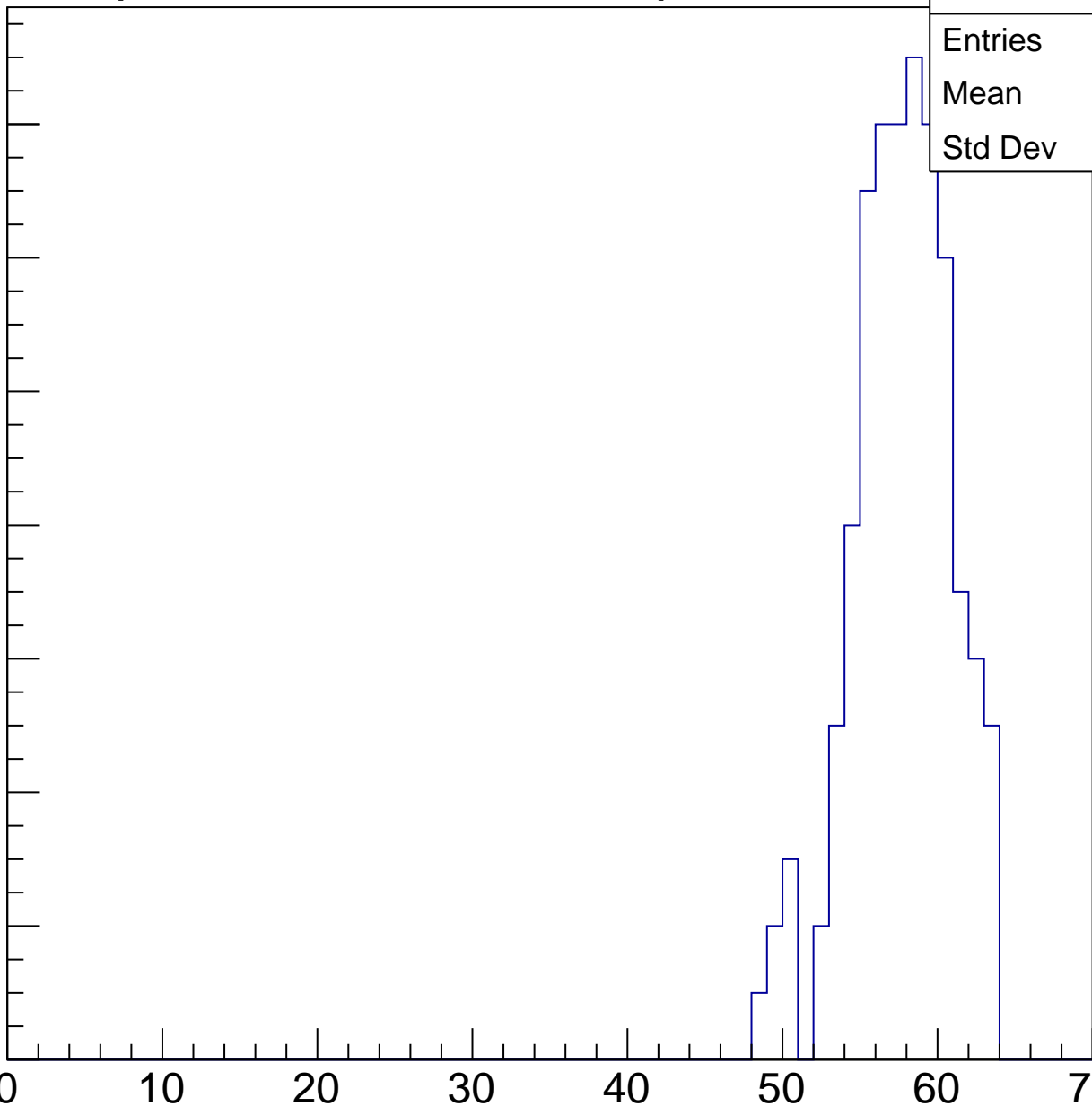
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	121
Mean	57.22
Std Dev	3.2

ampl



B0L101S, U19-ch69, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	59
Mean	58.34
Std Dev	11.18

Entry

12

10

8

6

4

2

0

0

10

20

30

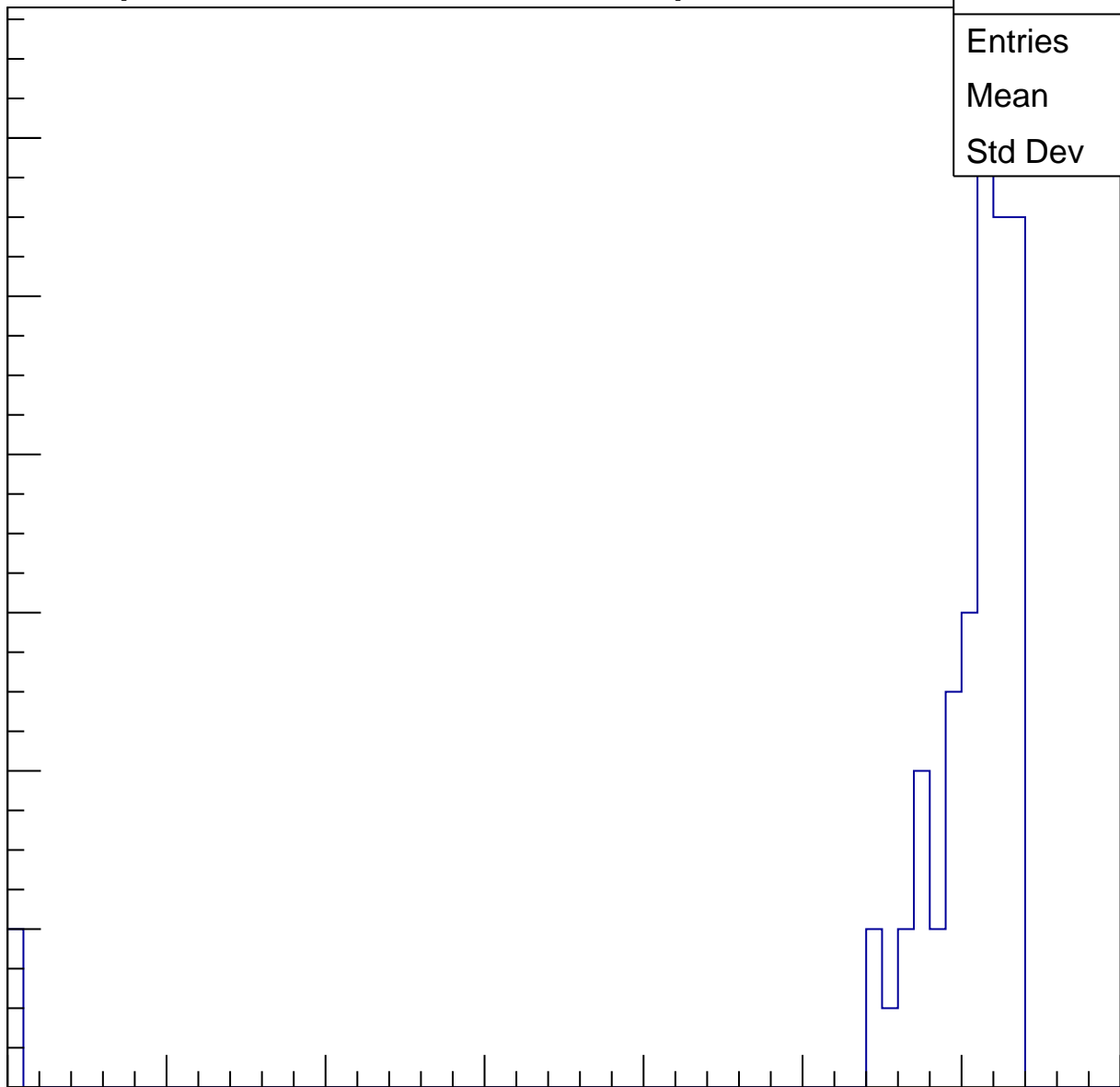
40

50

60

70

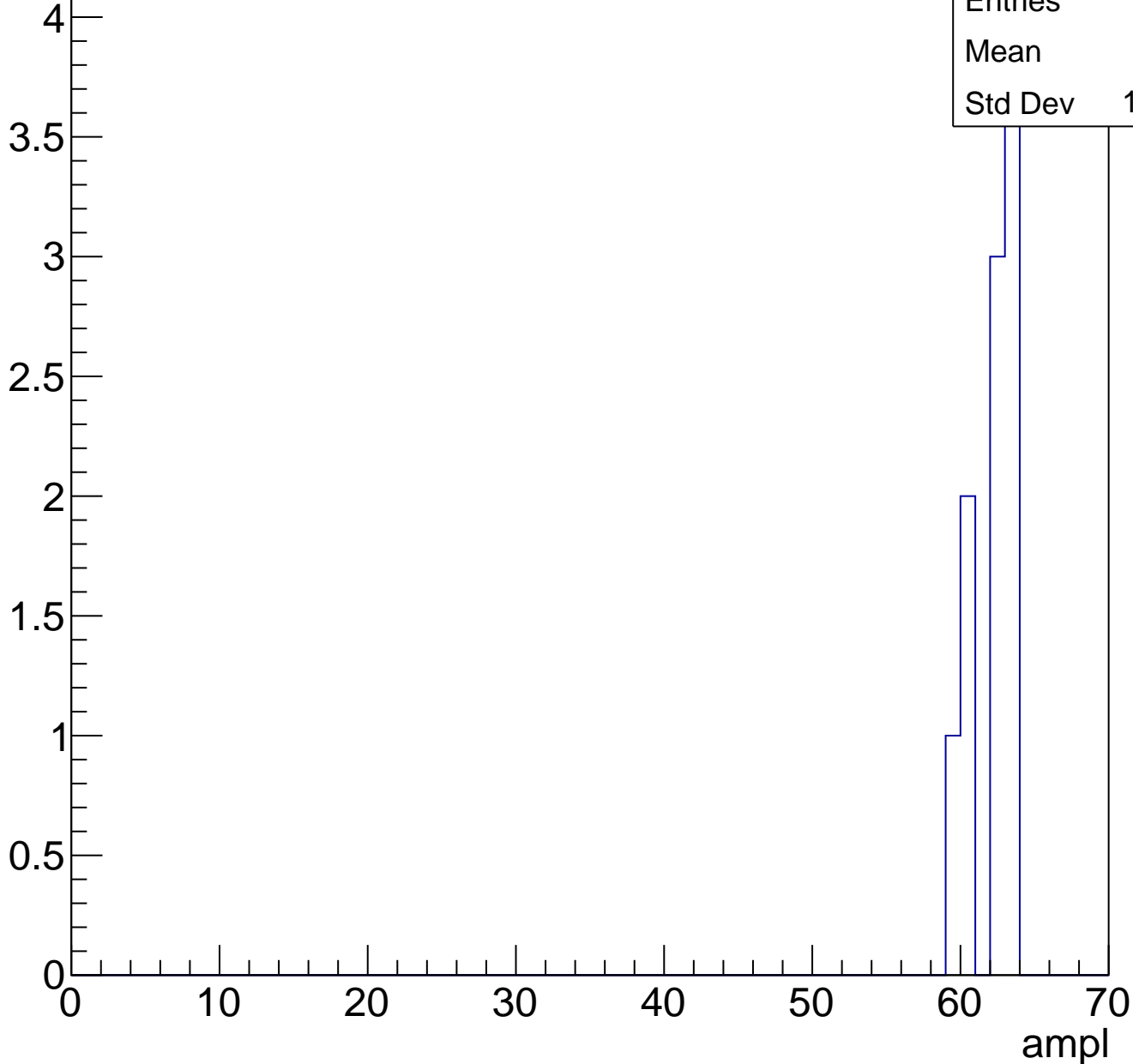
ampl



B0L101S, U19-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch70, adc0

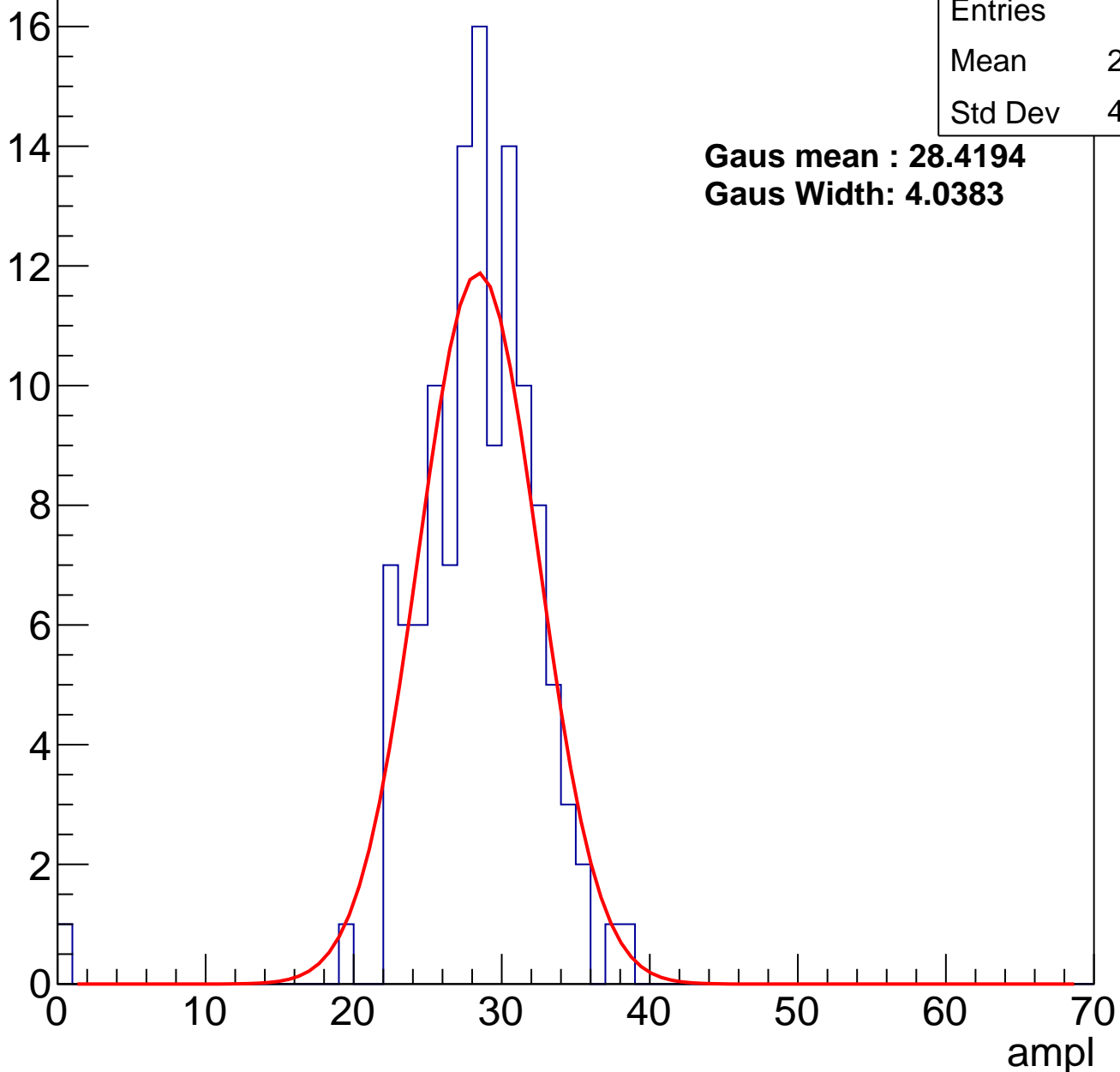
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	27.87
Std Dev	4.348

Gaus mean : 28.4194

Gaus Width: 4.0383

Entry



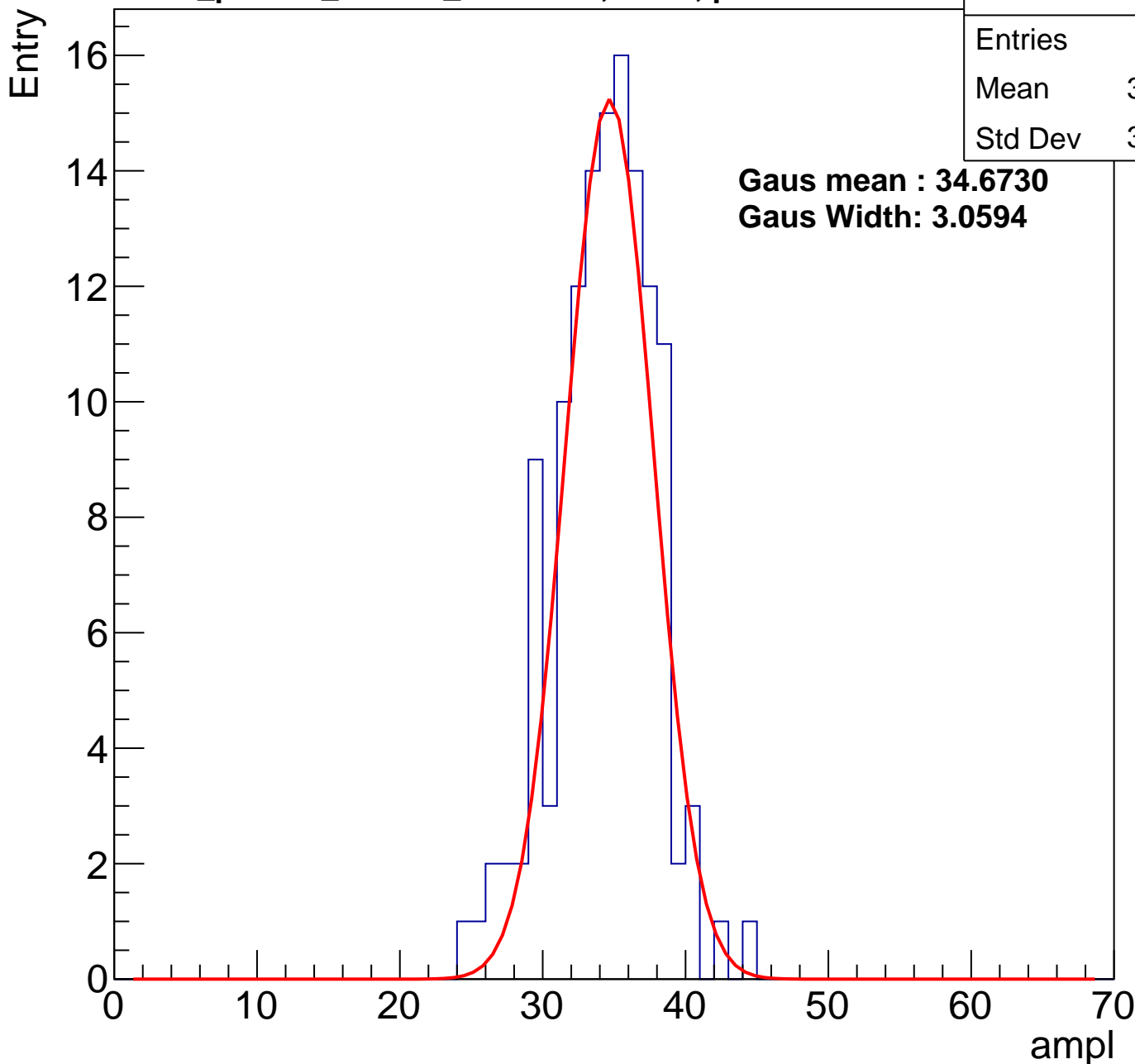
B0L101S, U19-ch70, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	33.88
Std Dev	3.473

Gaus mean : 34.6730

Gaus Width: 3.0594



B0L101S, U19-ch70, adc2

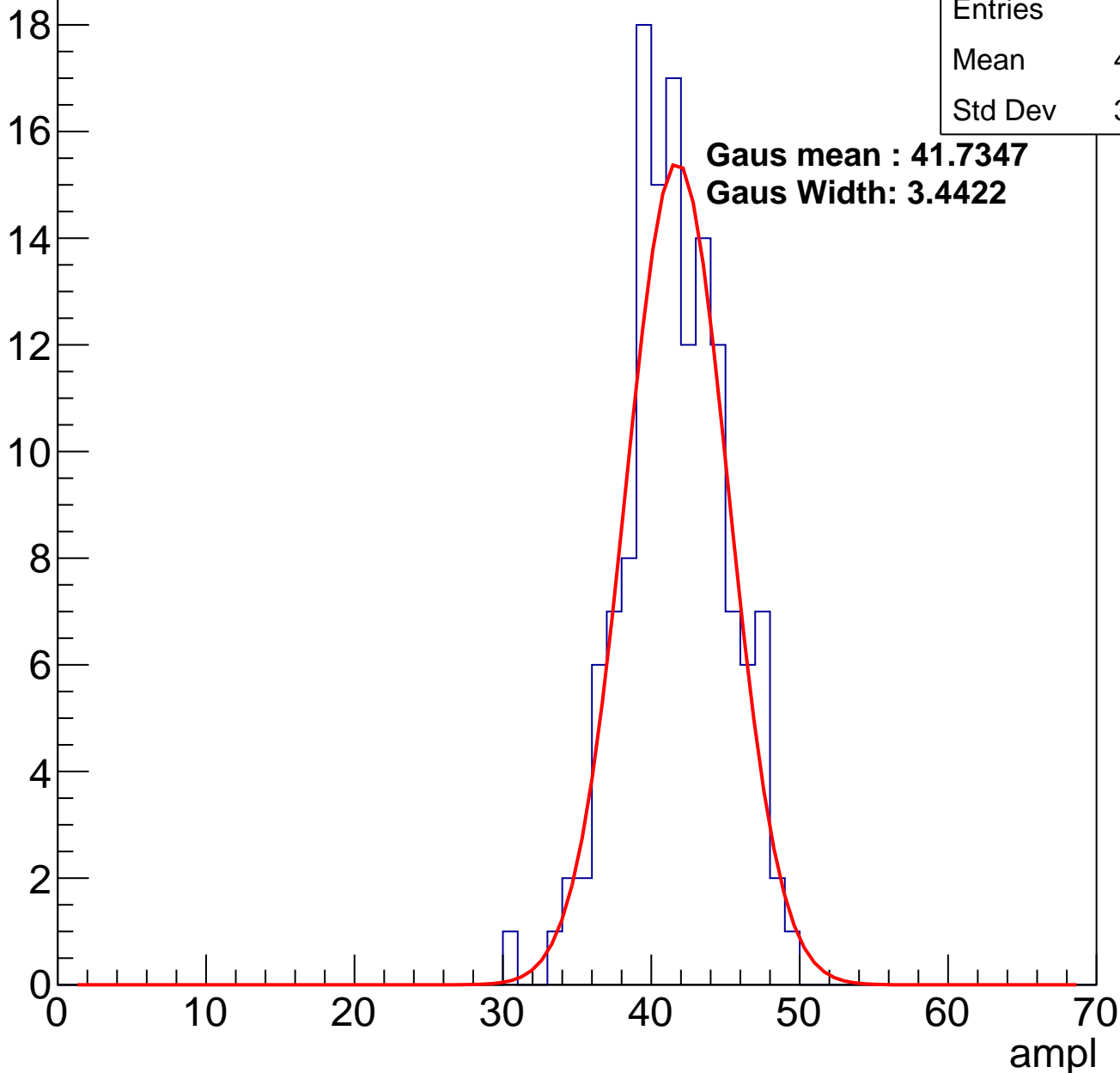
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	41.14
Std Dev	3.432

Gaus mean : 41.7347

Gaus Width: 3.4422

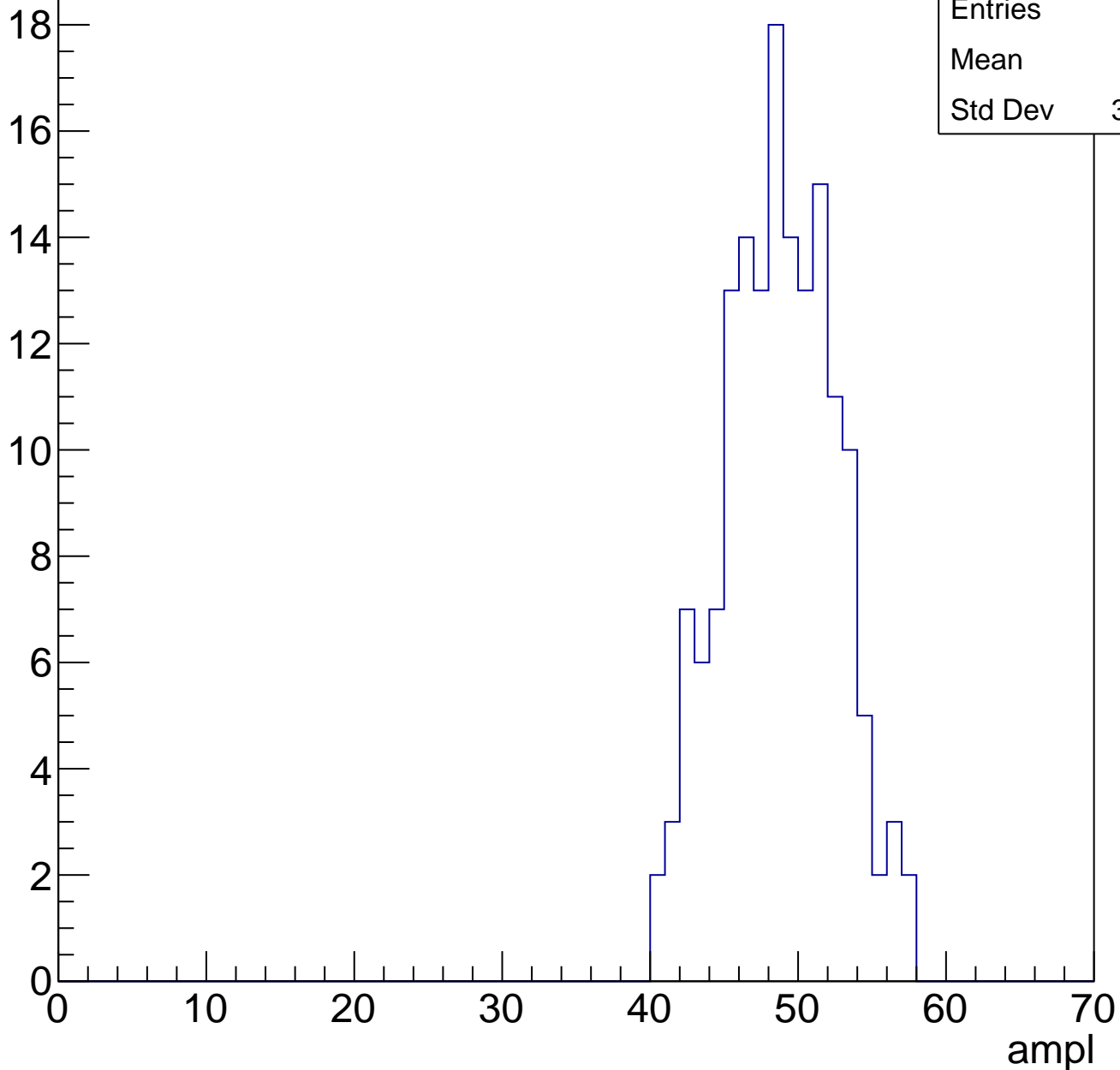
Entry



B0L101S, U19-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

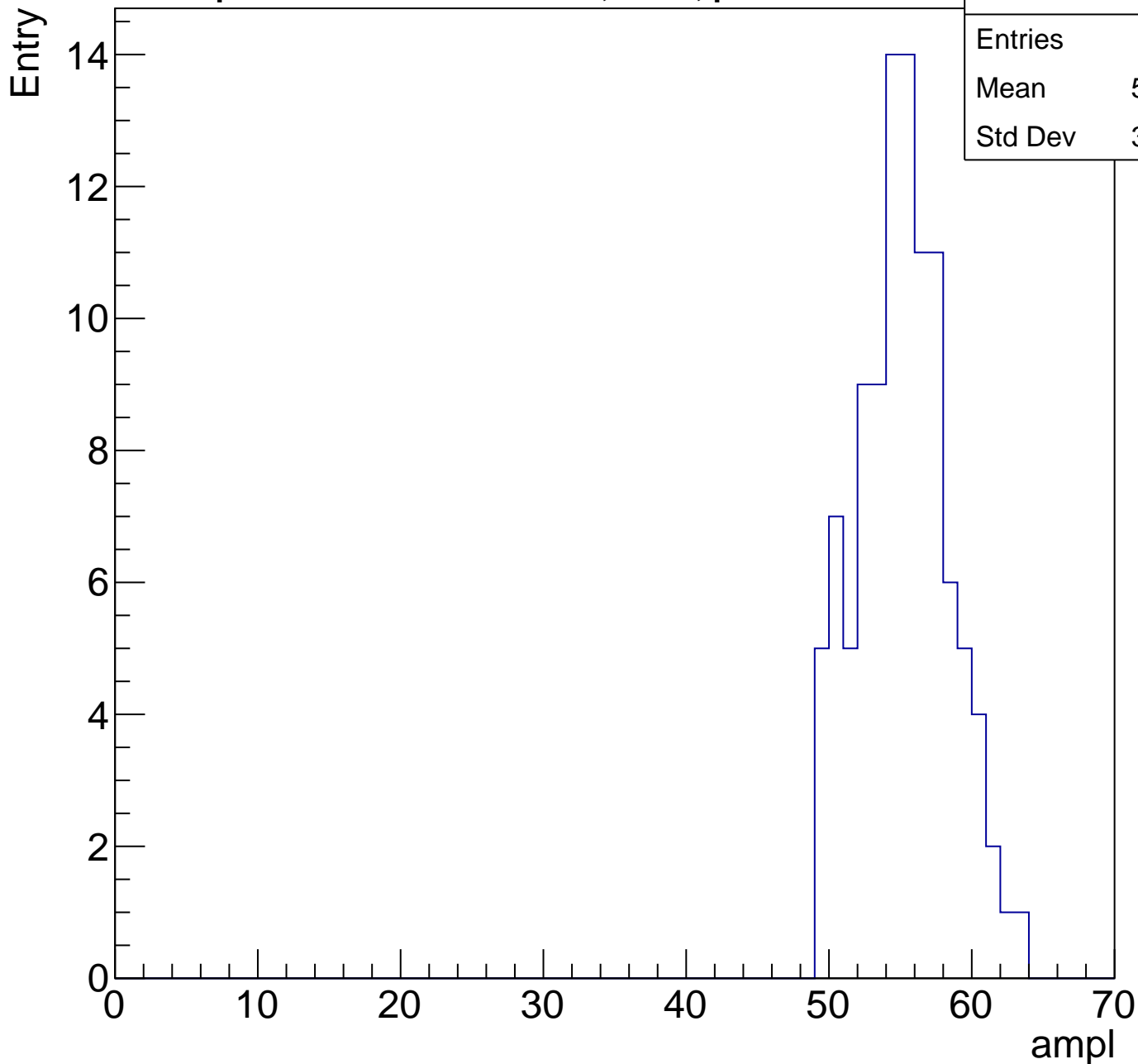
Entry



B0L101S, U19-ch70, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	54.75
Std Dev	3.152



B0L101S, U19-ch70, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	59.03
Std Dev	6.319

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

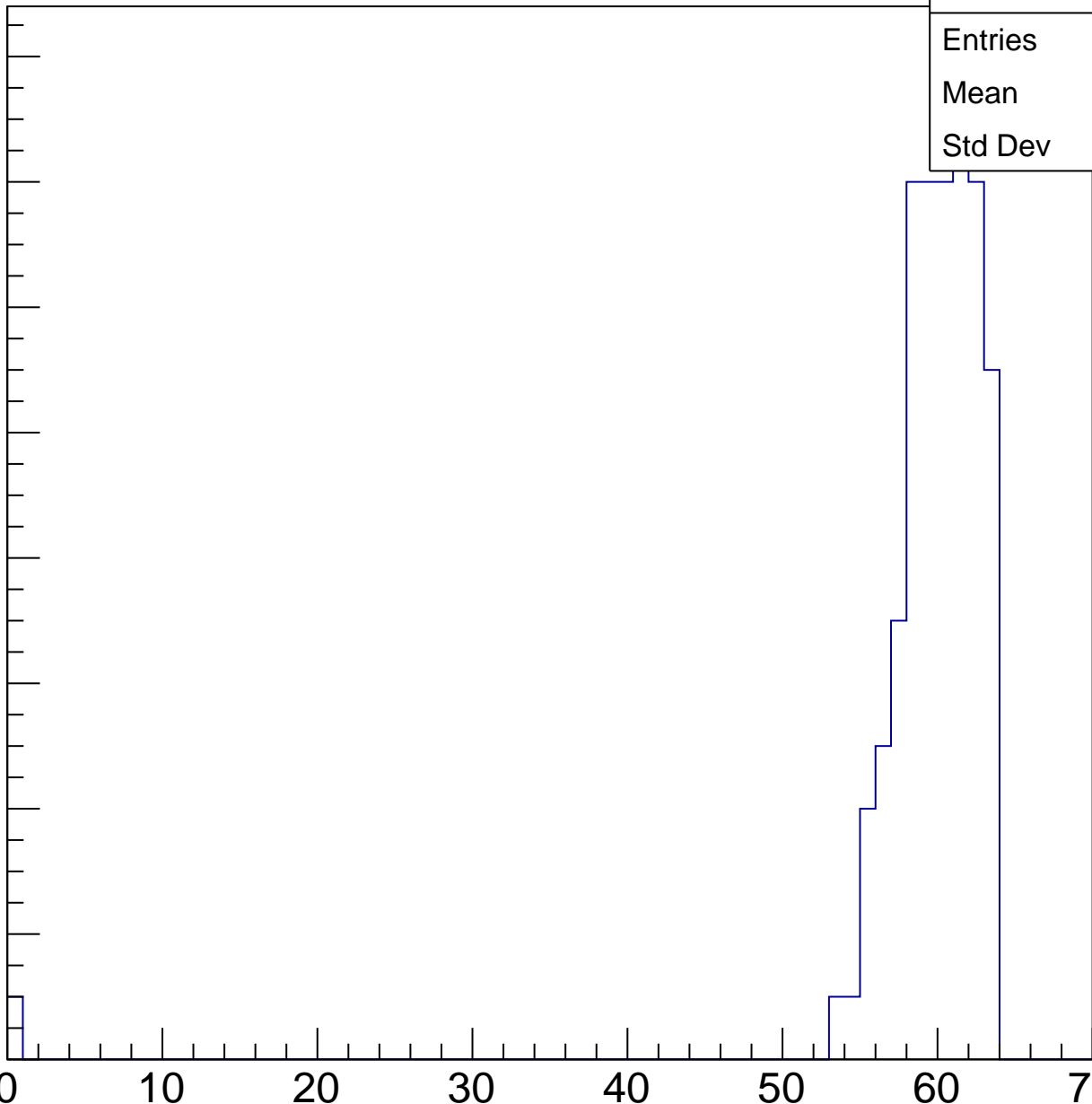
40

50

60

70

ampl

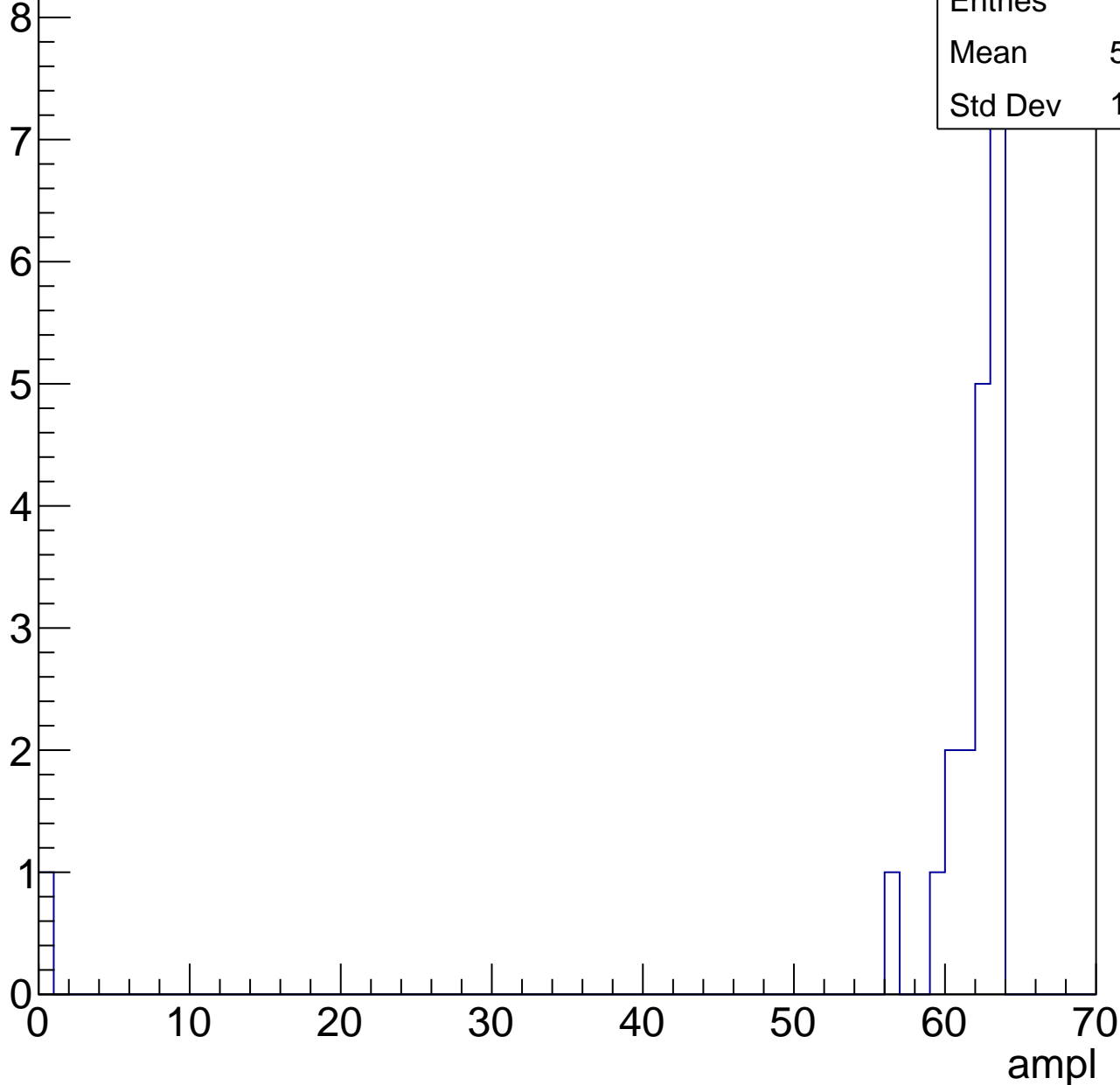


B0L101S, U19-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	58.55
Std Dev	13.54



B0L101S, U19-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

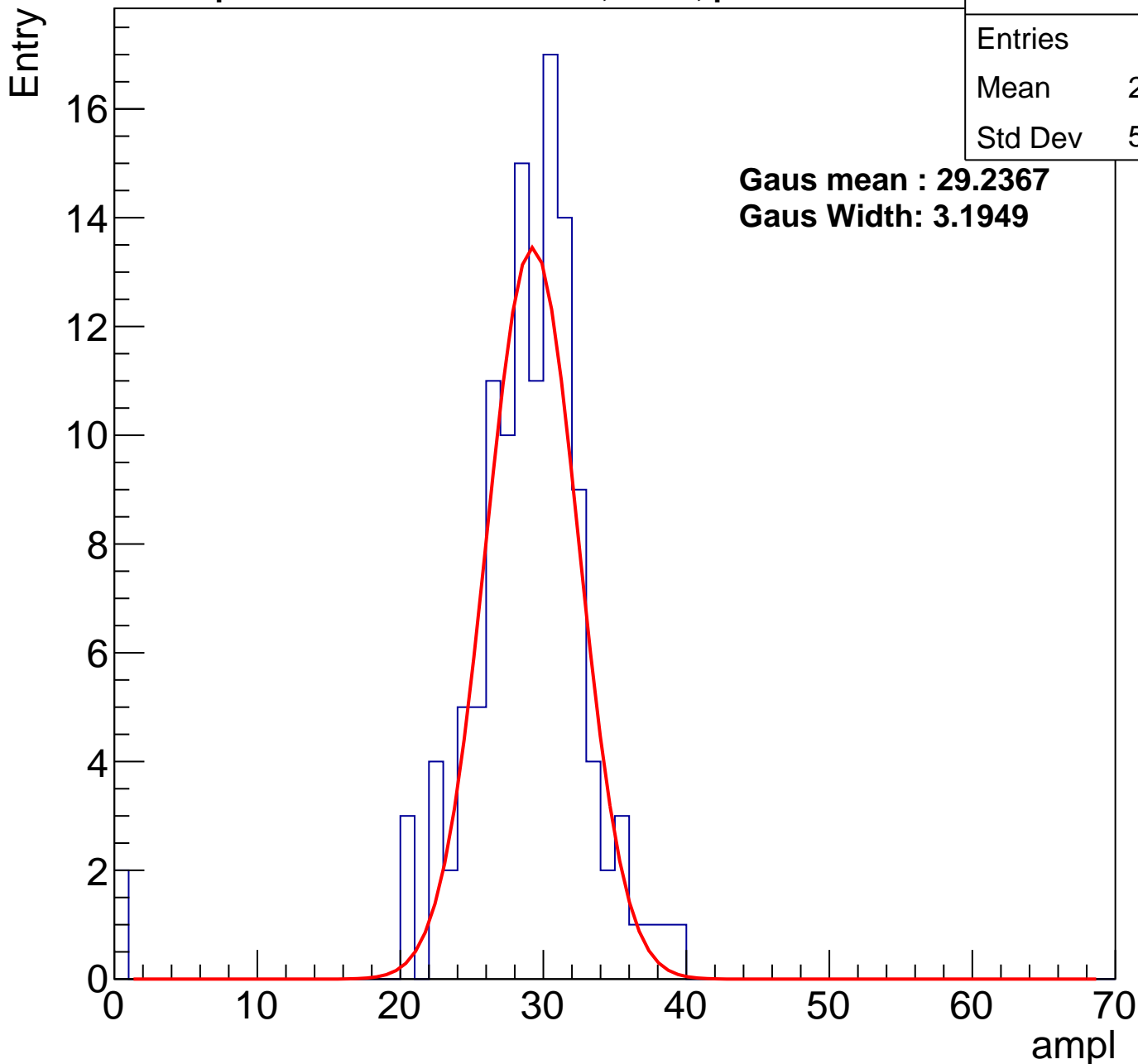
B0L101S, U19-ch71, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	28.27
Std Dev	5.109

Gaus mean : 29.2367

Gaus Width: 3.1949



B0L101S, U19-ch71, adc1

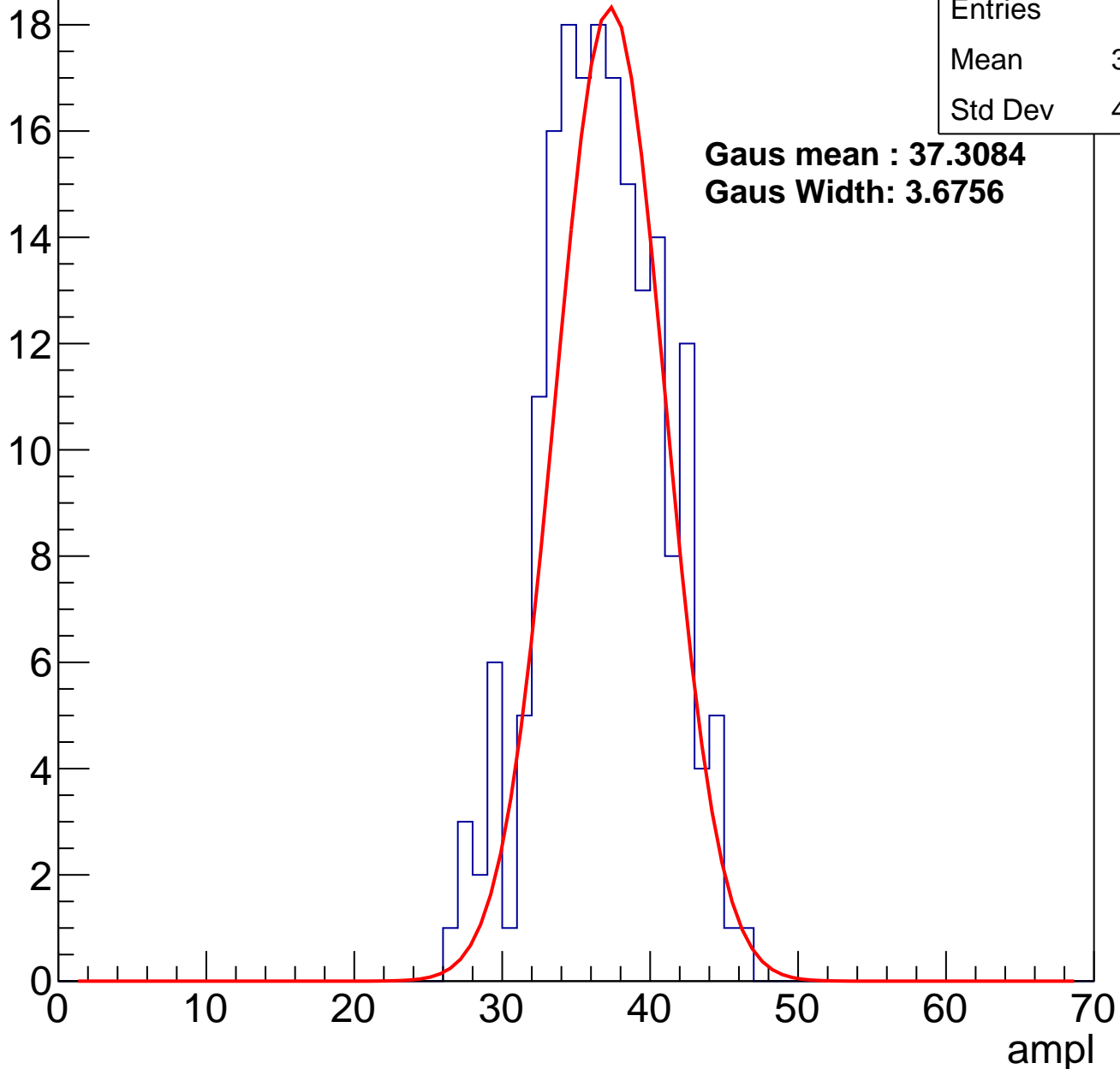
calib_packv5_042523_0143.root, FC#1, port C1

Entries	188
Mean	36.37
Std Dev	4.032

Gaus mean : 37.3084

Gaus Width: 3.6756

Entry



B0L101S, U19-ch71, adc2

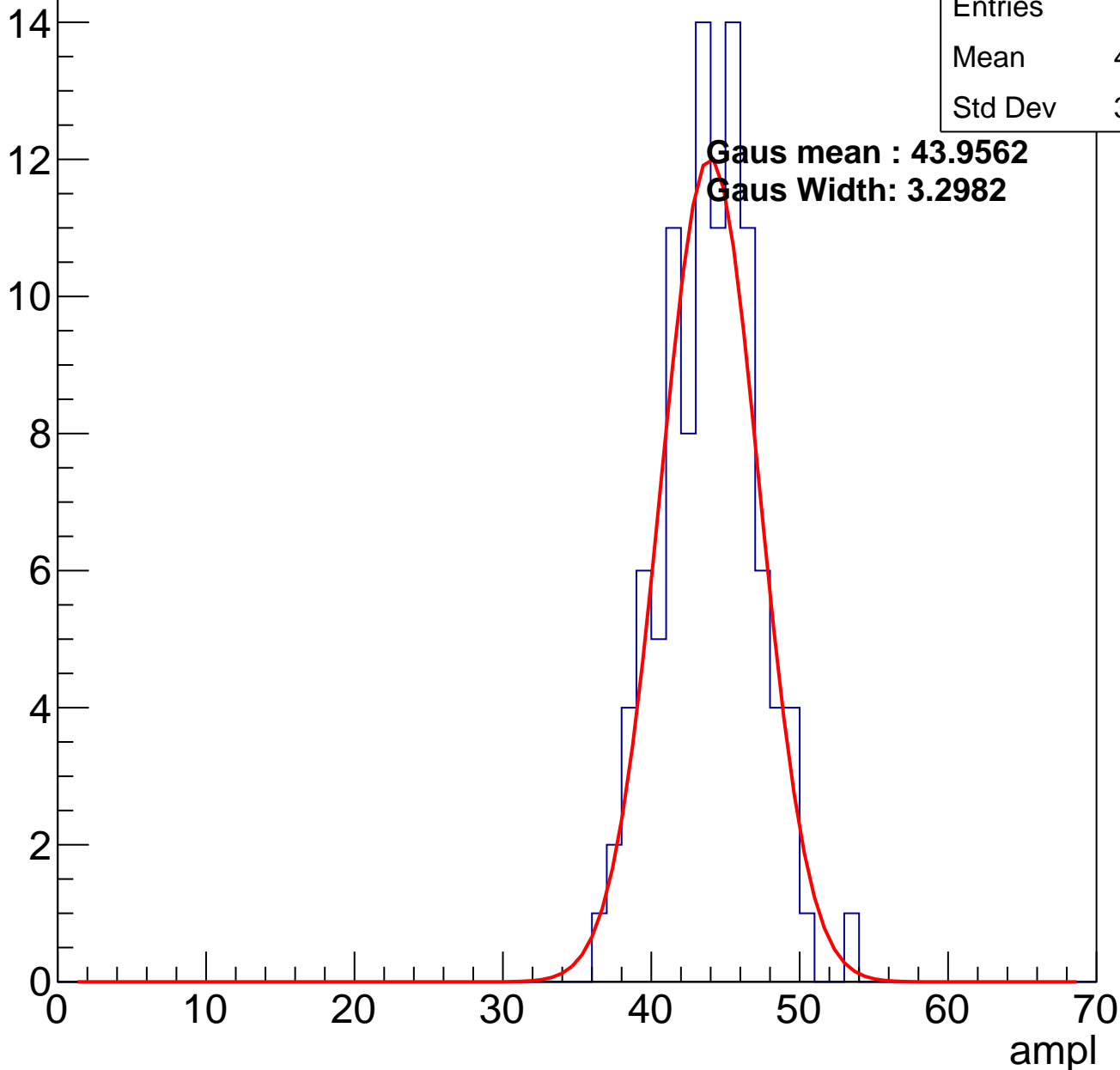
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	103
Mean	43.48
Std Dev	3.186

Gaus mean : 43.9562

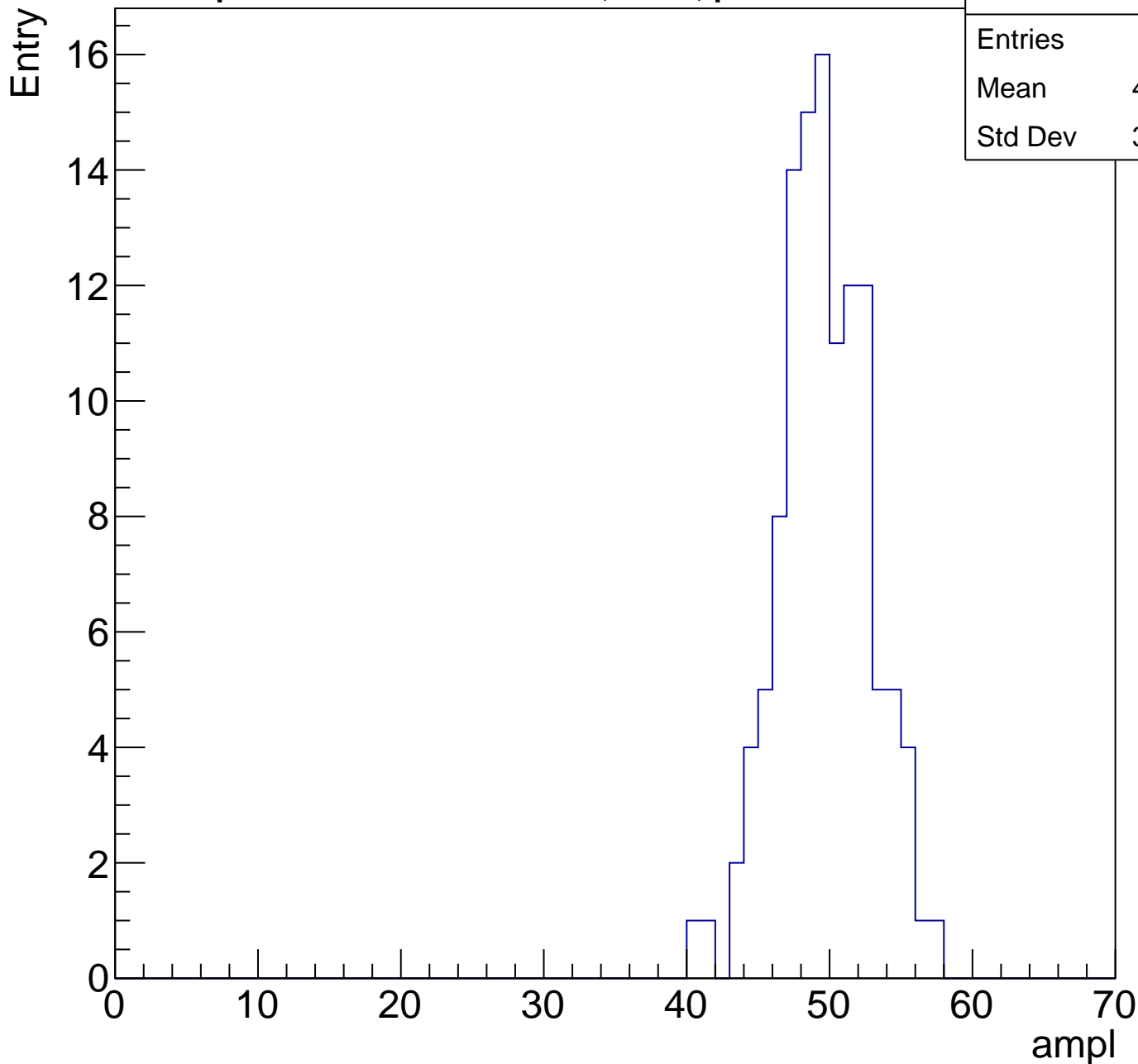
Gaus Width: 3.2982



B0L101S, U19-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	49.16
Std Dev	3.157



B0L101S, U19-ch71, adc4

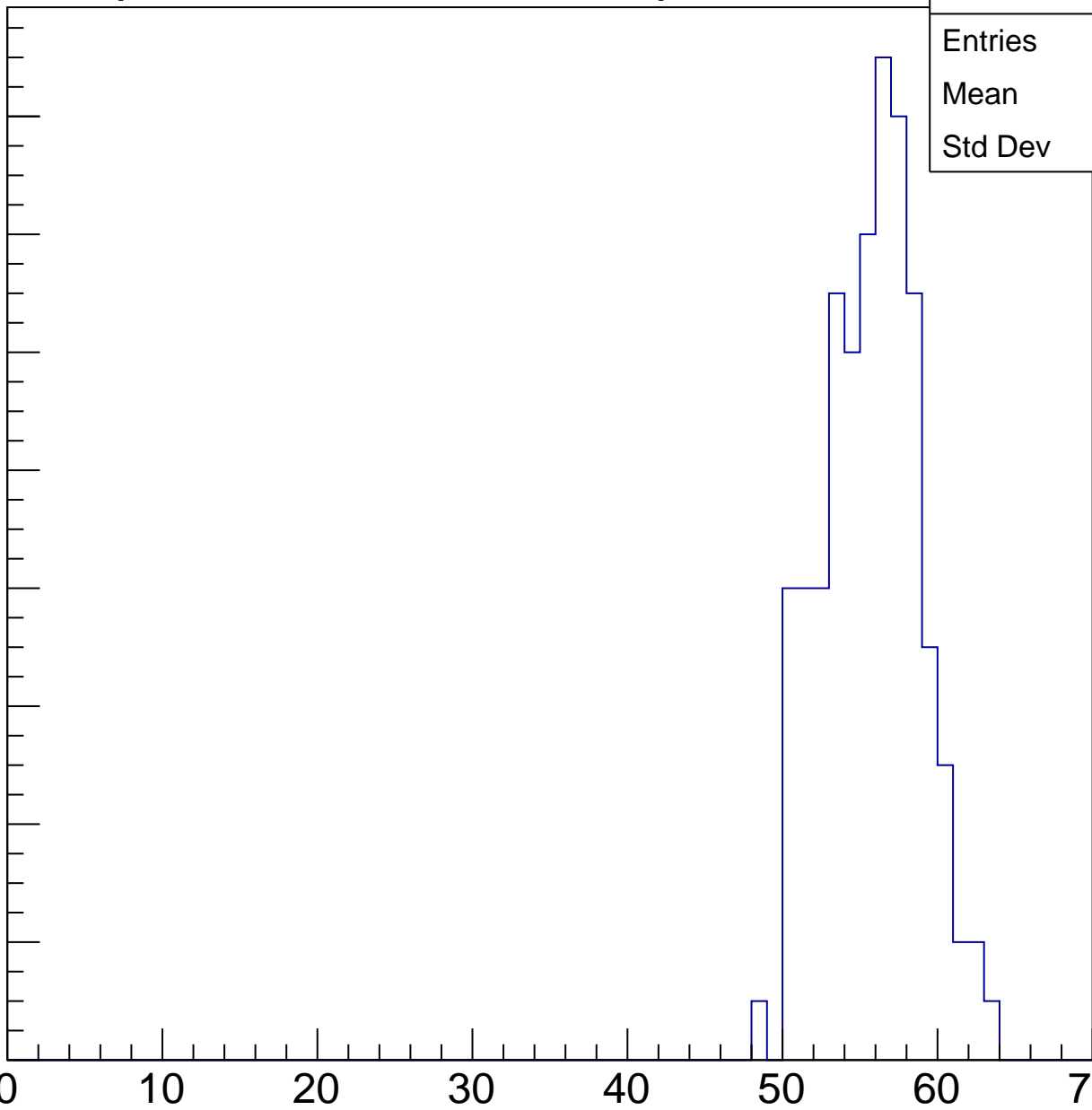
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	55.27
Std Dev	3.039

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch71, adc5

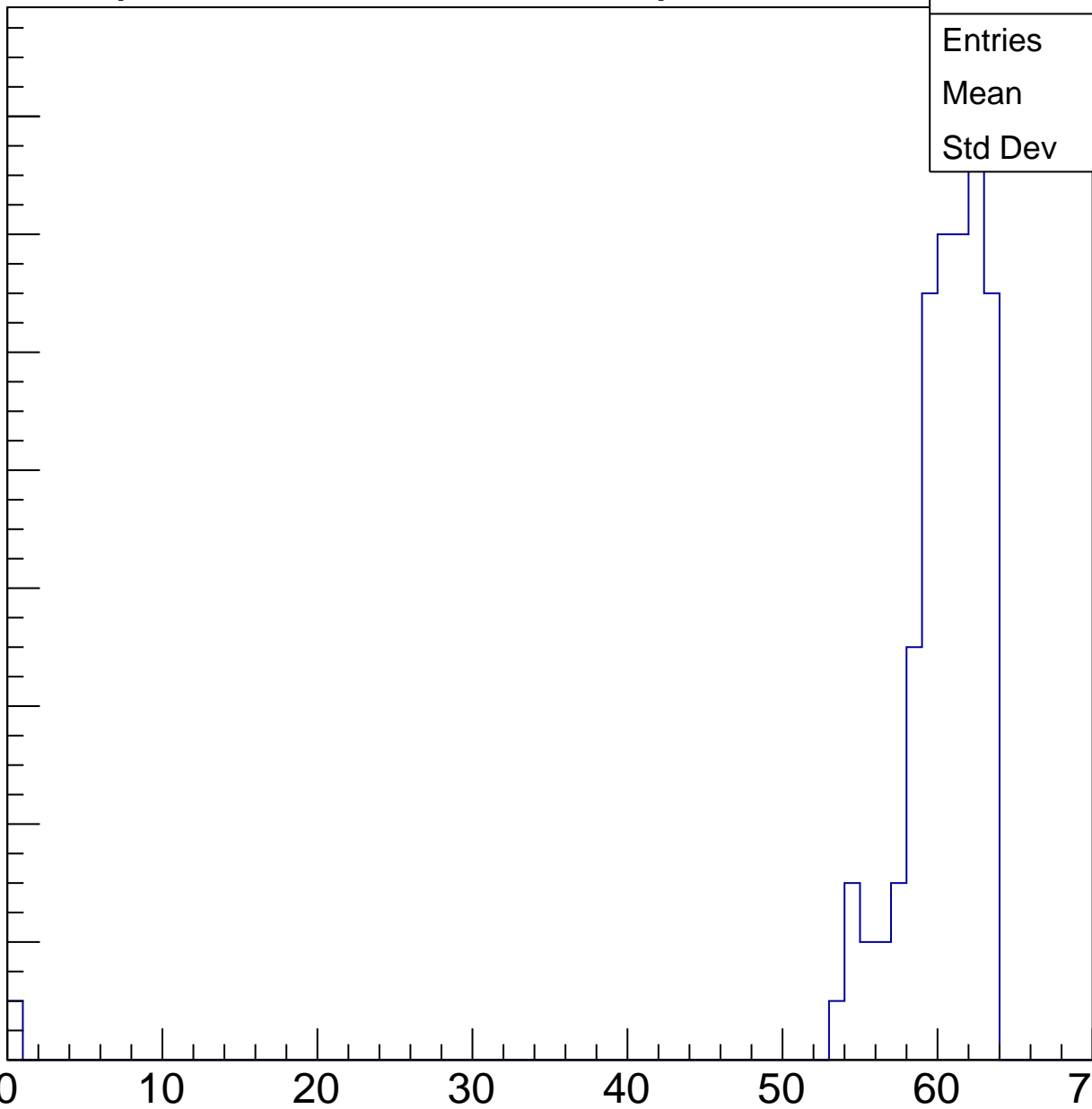
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	90
Mean	59.42
Std Dev	6.73

ampl

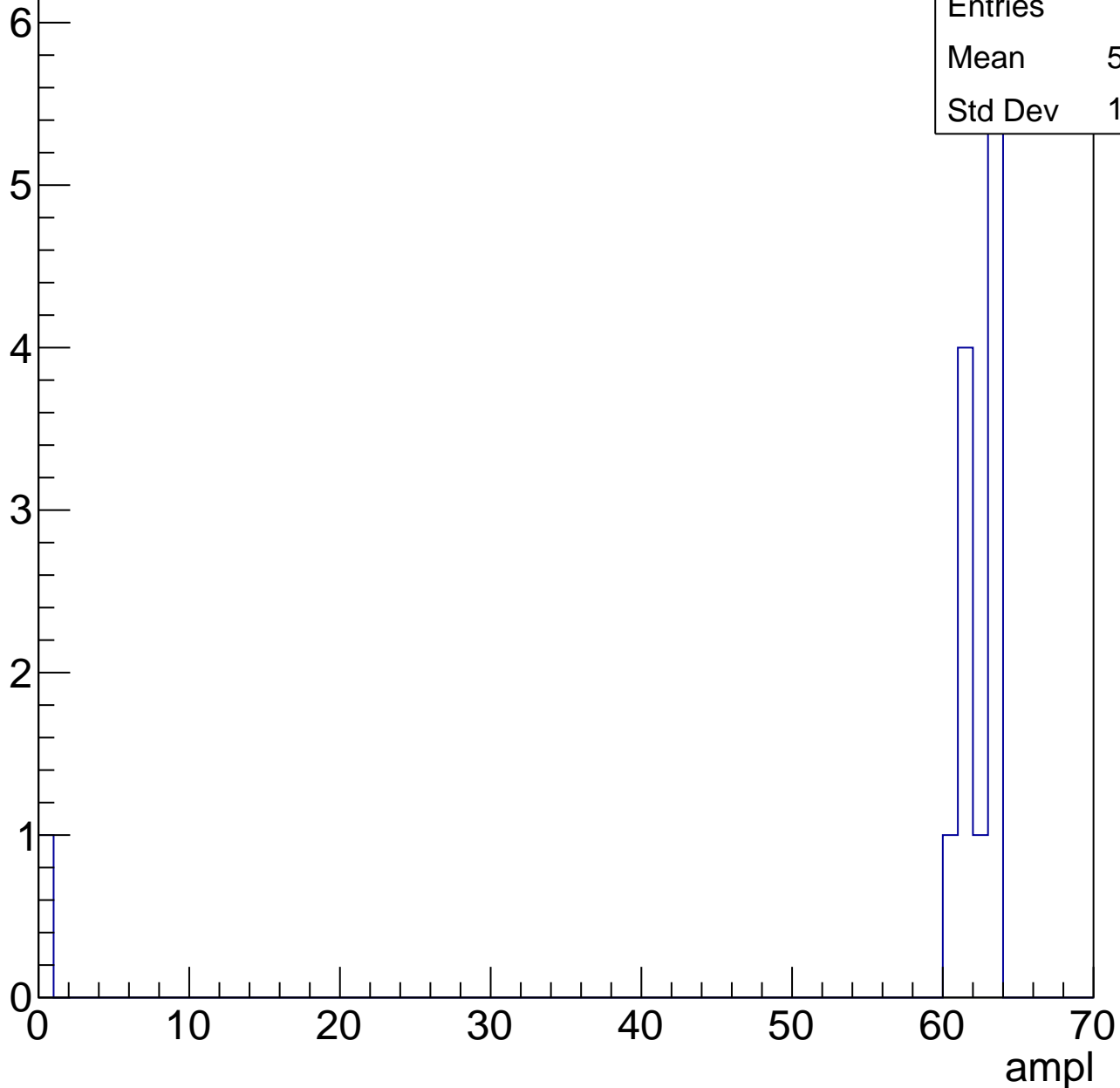


B0L101S, U19-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.23
Std Dev	16.55



B0L101S, U19-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch72, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	30.42
Std Dev	3.719

Gaus mean : 30.7993

Gaus Width: 3.8880

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

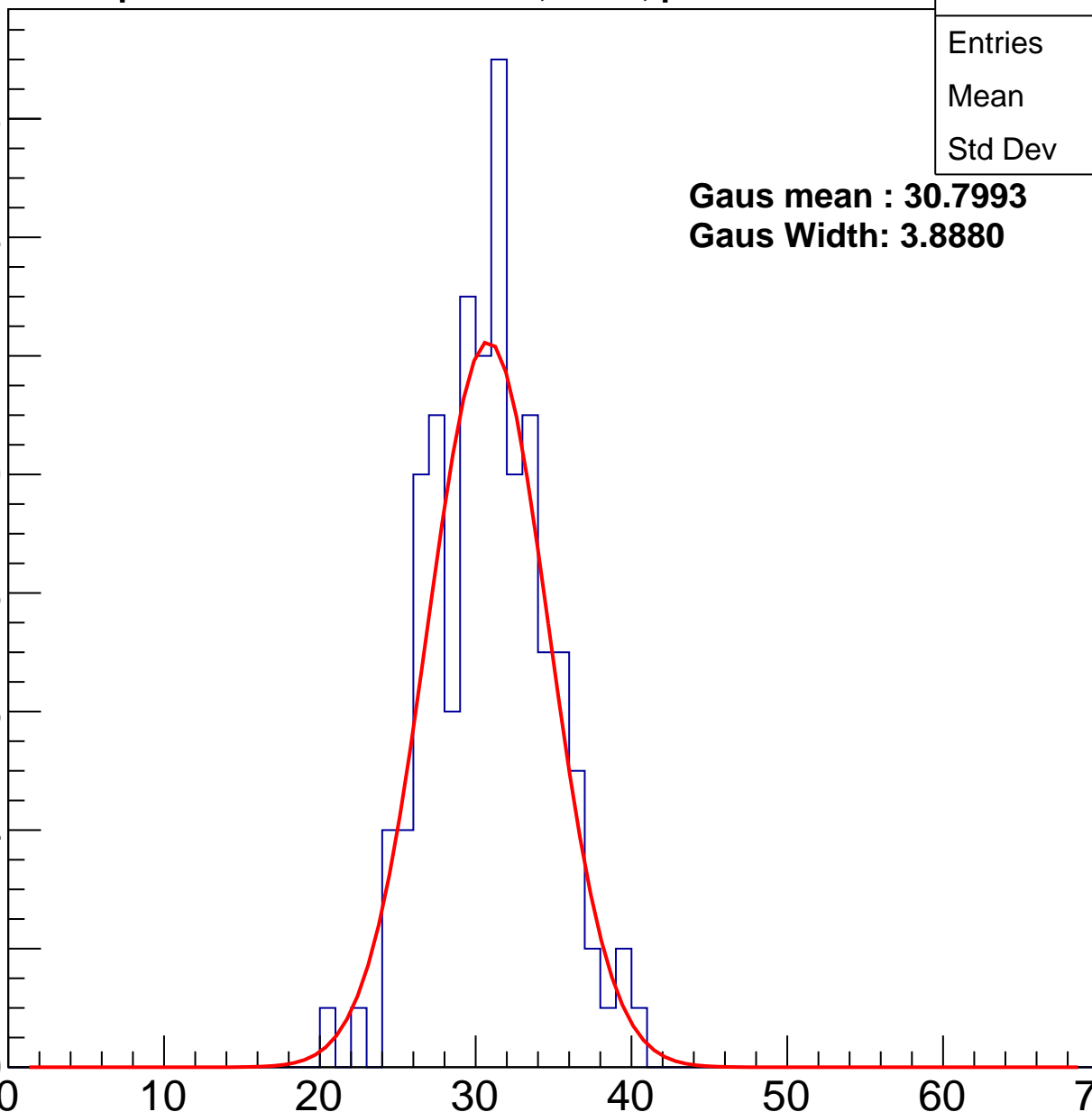
40

50

60

70

ampl



B0L101S, U19-ch72, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	37.22
Std Dev	4.117

Gaus mean : 37.7012

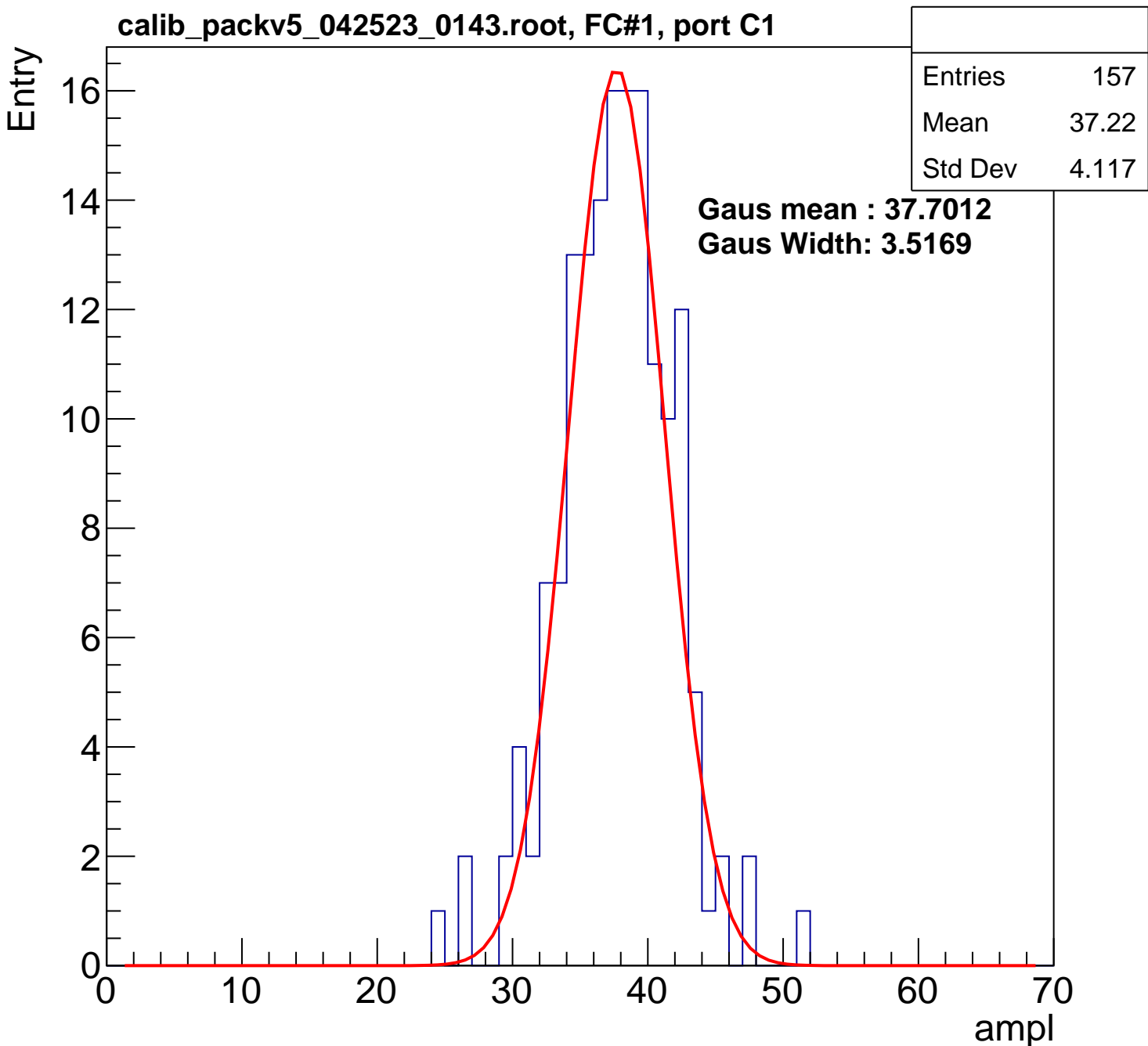
Gaus Width: 3.5169

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

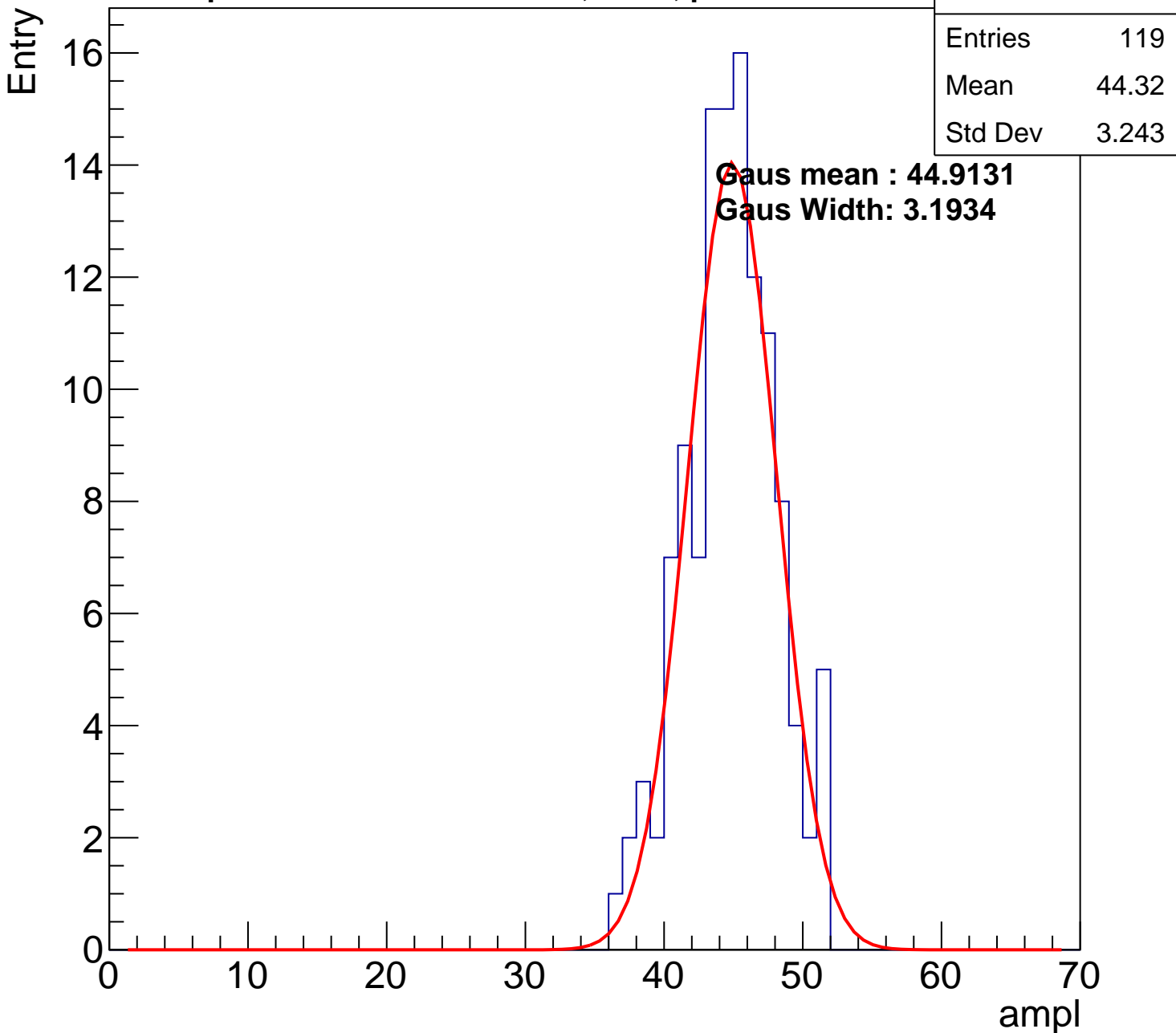
Entries	119
Mean	44.32
Std Dev	3.243

Gaus mean : 44.9131

Gaus Width: 3.1934

ampl

0 10 20 30 40 50 60 70

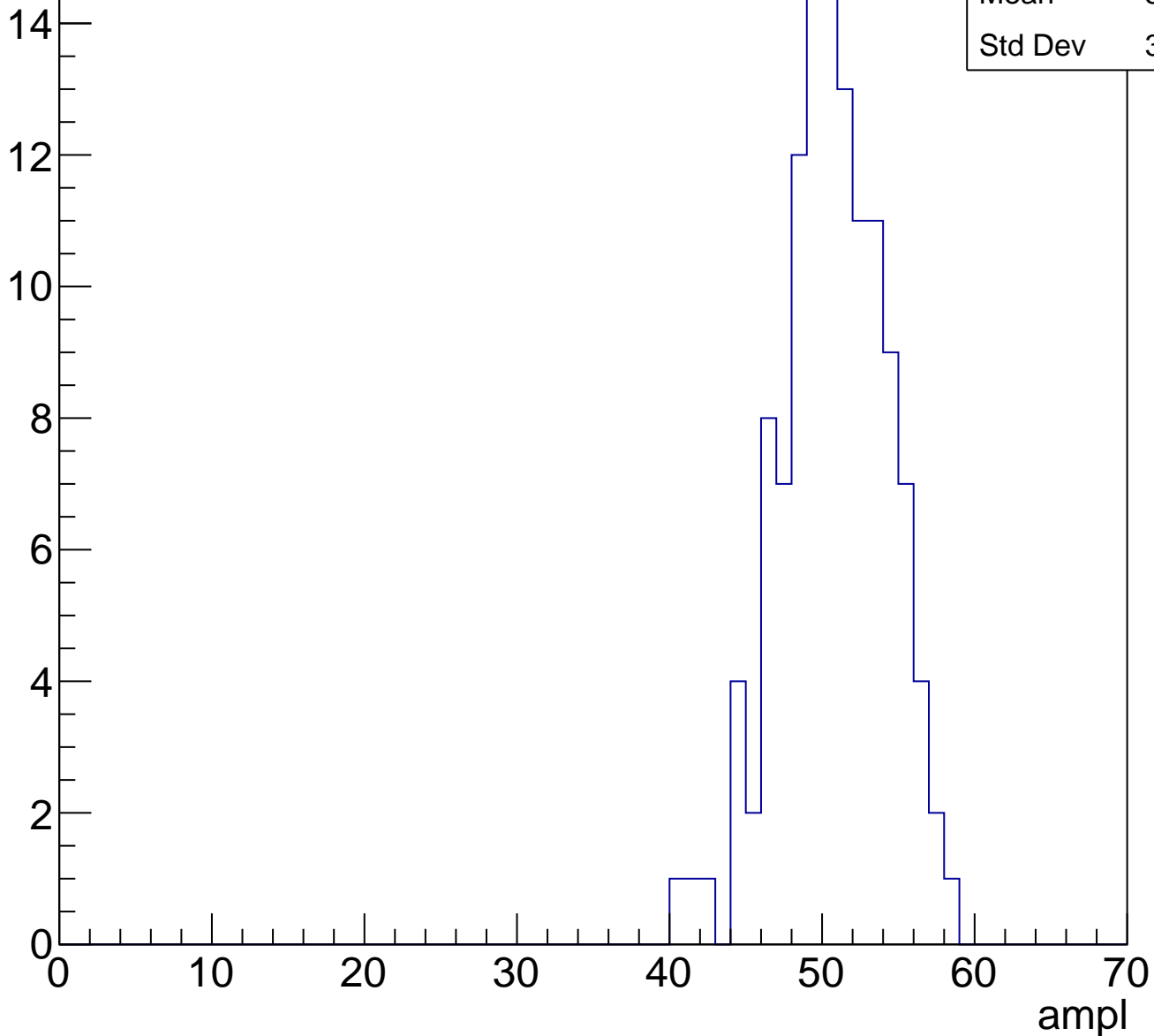


B0L101S, U19-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	50.26
Std Dev	3.438

Entry

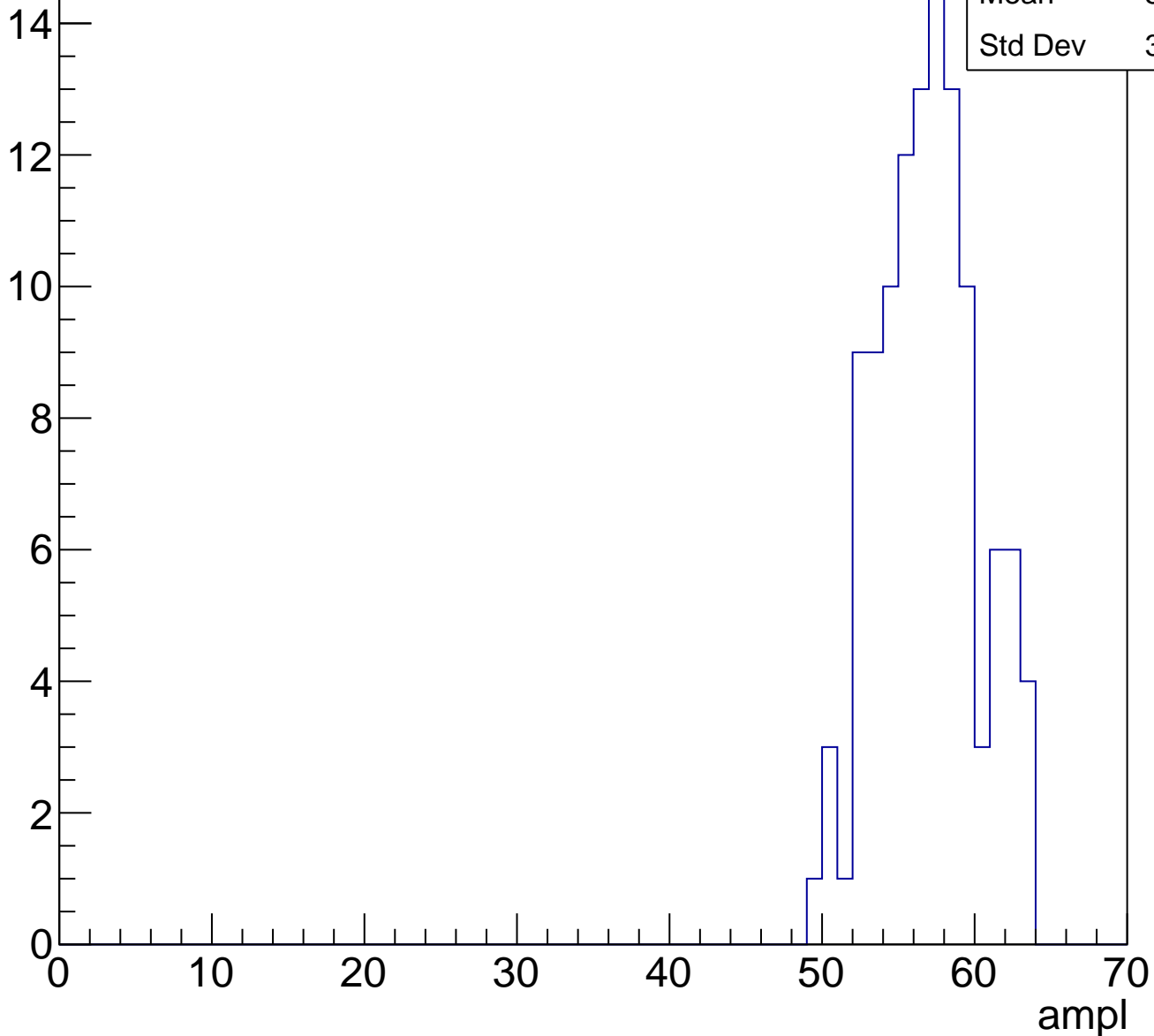


B0L101S, U19-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	56.45
Std Dev	3.234

Entry



B0L101S, U19-ch72, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	76
Mean	59.45
Std Dev	7.181

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

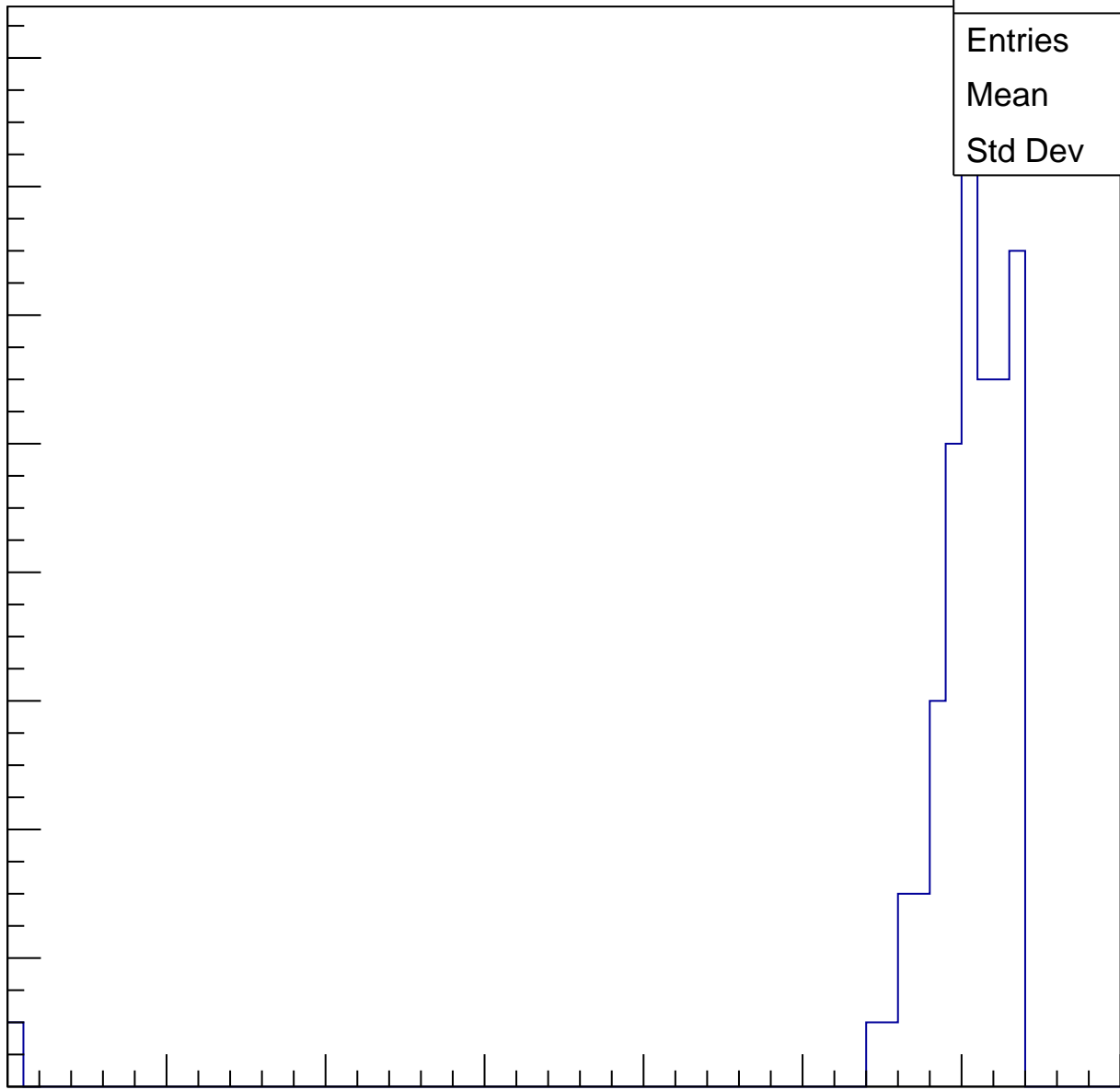
40

50

60

70

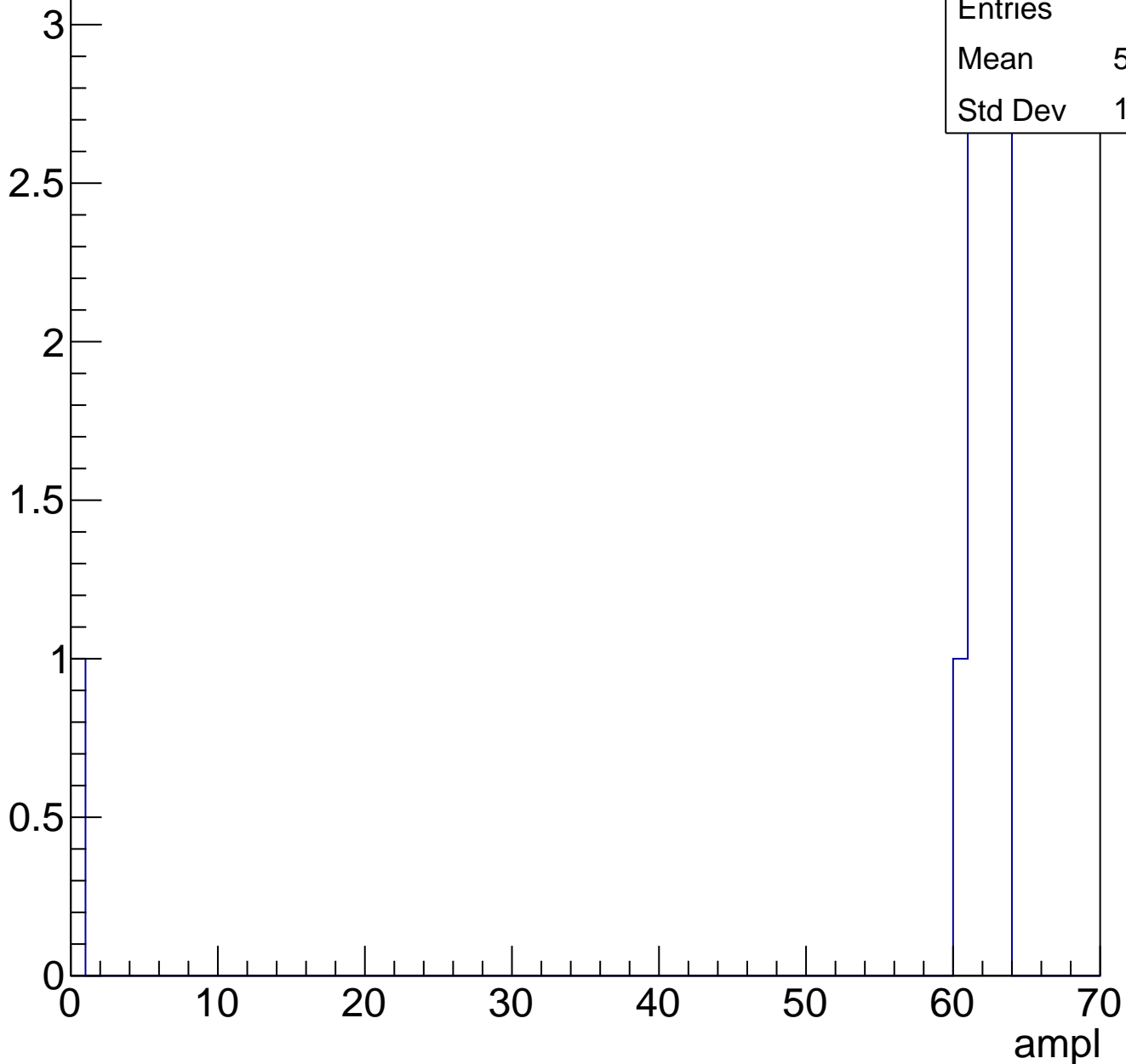
ampl



B0L101S, U19-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

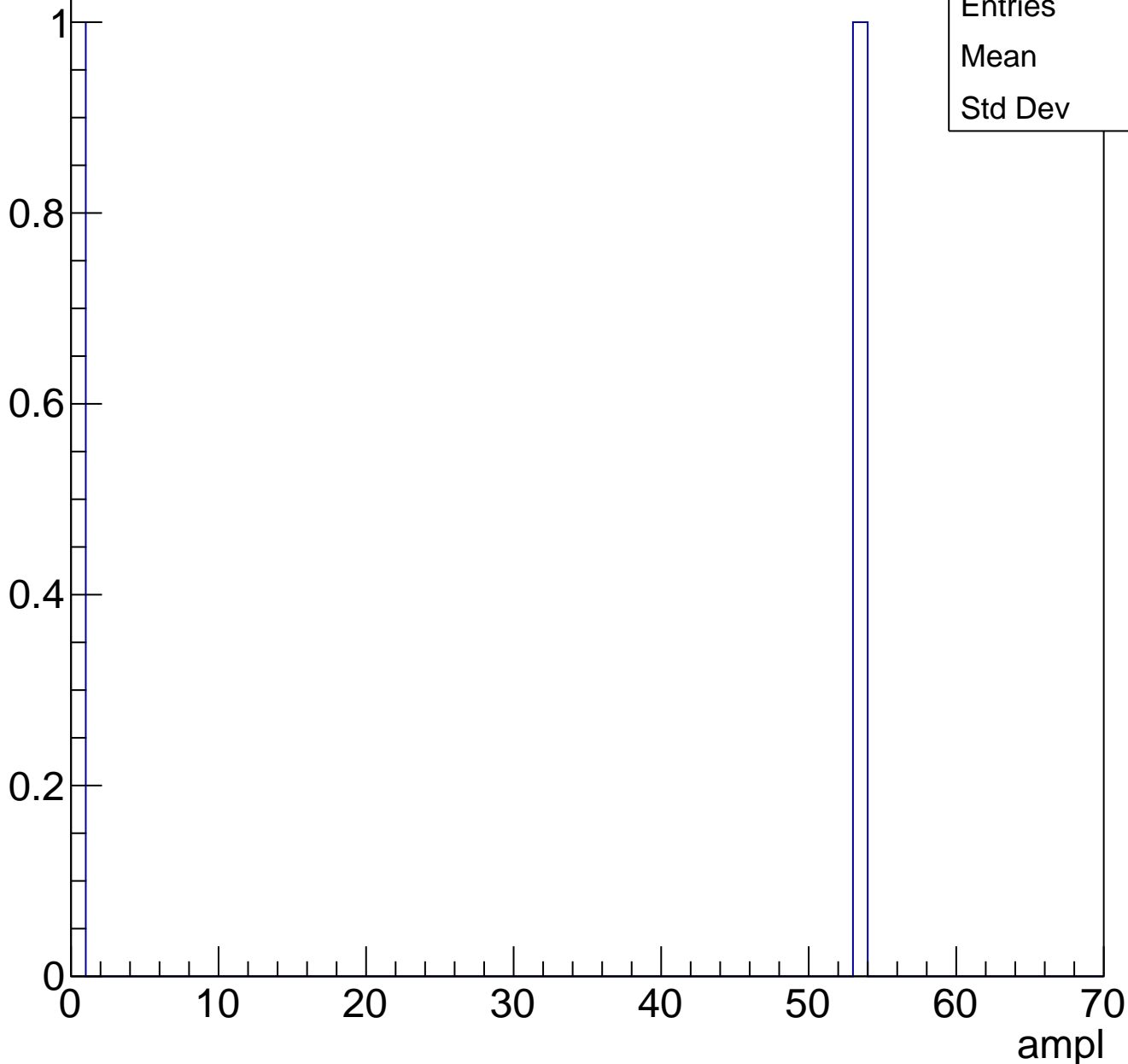
Entry



B0L101S, U19-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

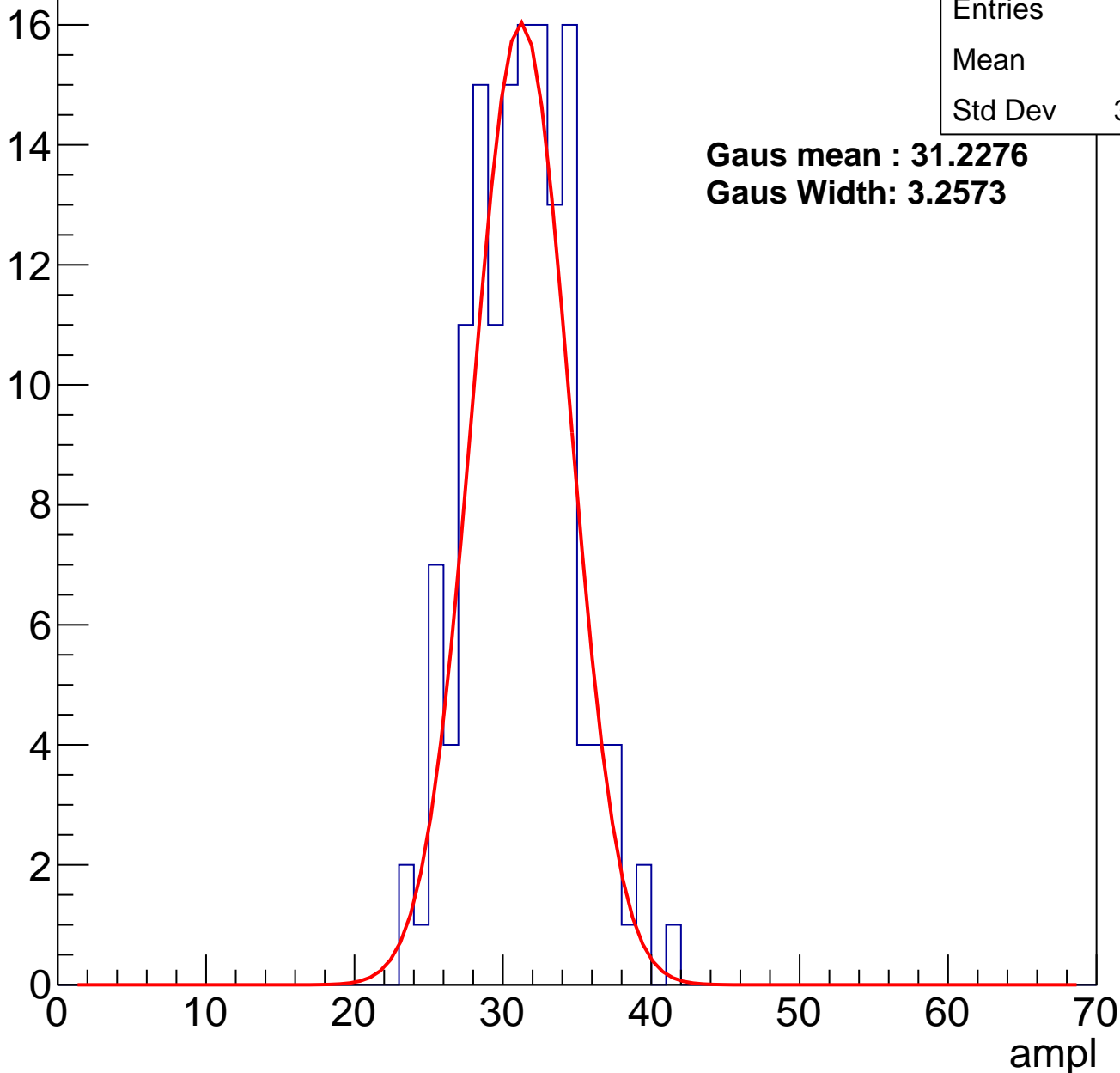
Entry



B0L101S, U19-ch73, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



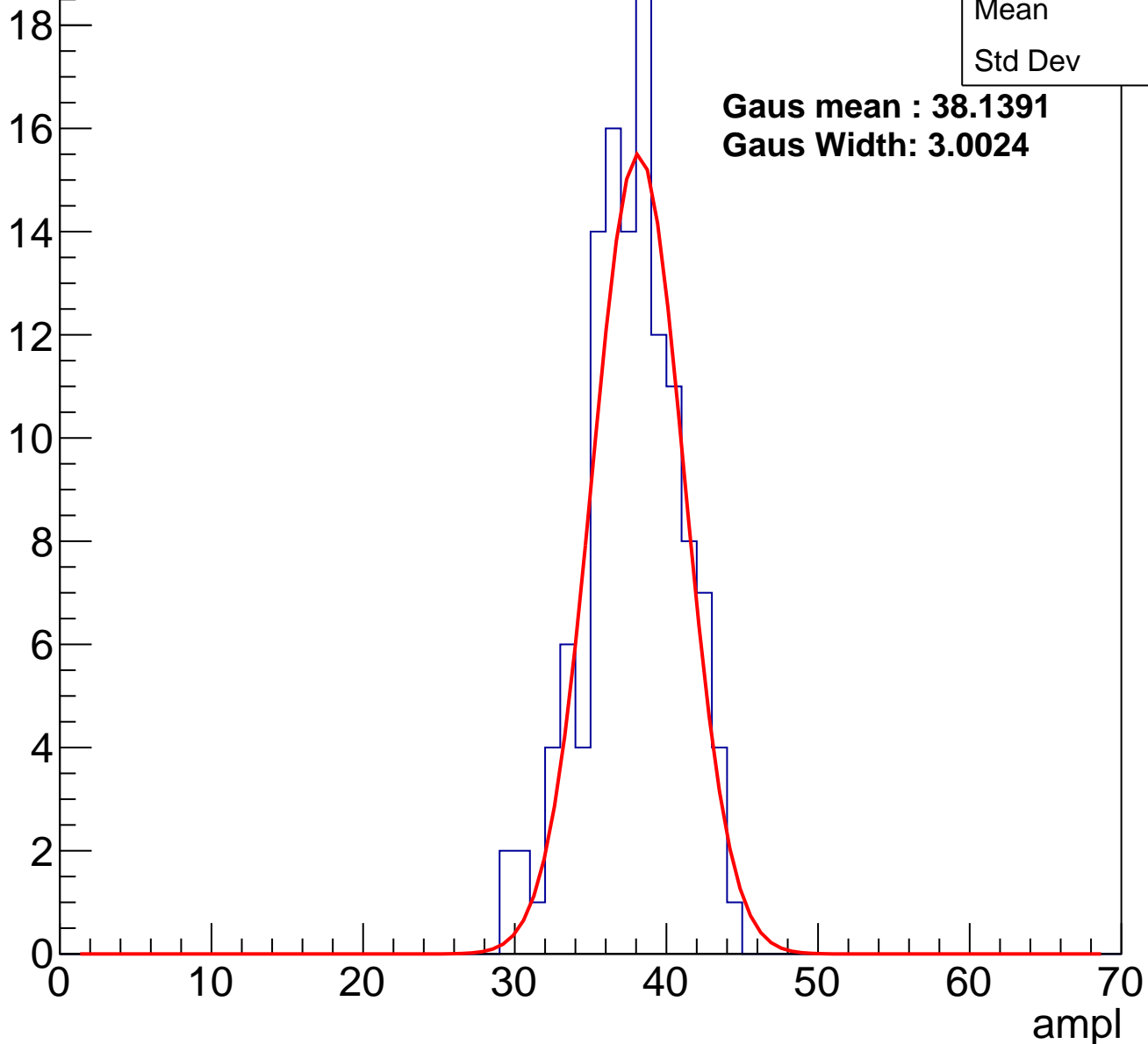
B0L101S, U19-ch73, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	37.3
Std Dev	3.12

Gaus mean : 38.1391
Gaus Width: 3.0024

Entry



B0L101S, U19-ch73, adc2

calib_packv5_042523_0143.root, FC#1, port C1

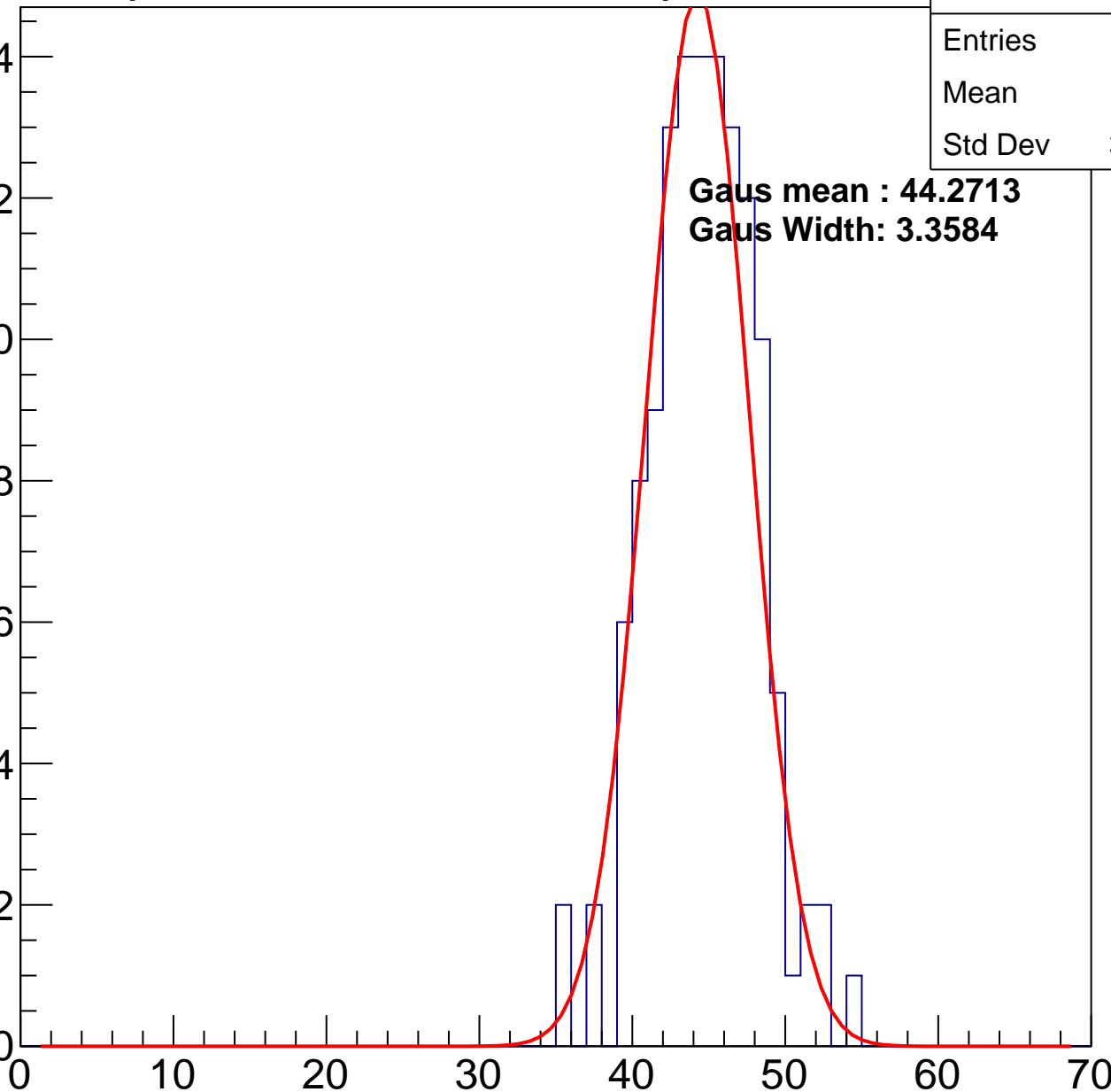
Entry

14
12
10
8
6
4
2
0

Entries	128
Mean	44.2
Std Dev	3.424

Gaus mean : 44.2713
Gaus Width: 3.3584

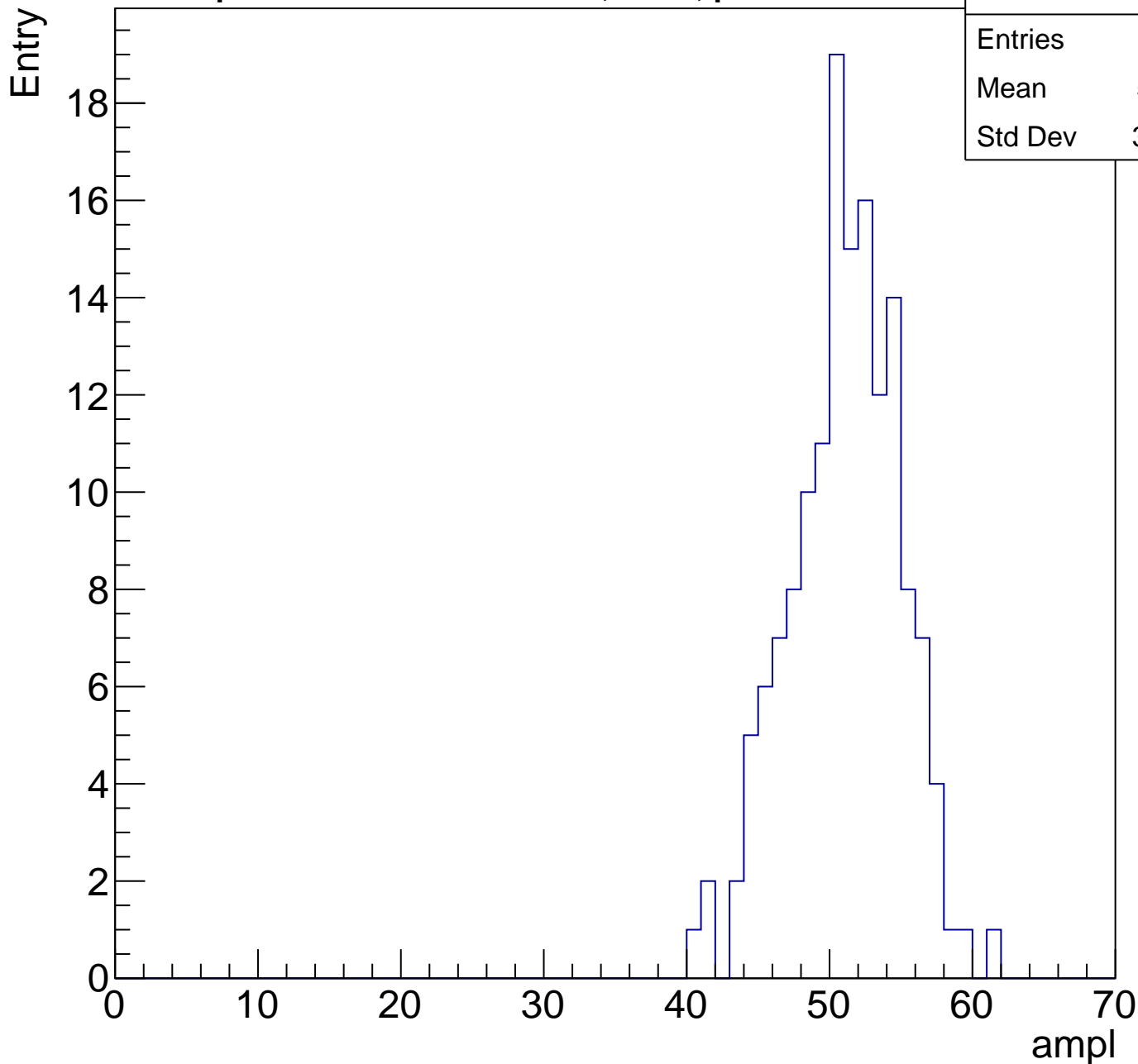
ampl



B0L101S, U19-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	50.61
Std Dev	3.824



B0L101S, U19-ch73, adc4

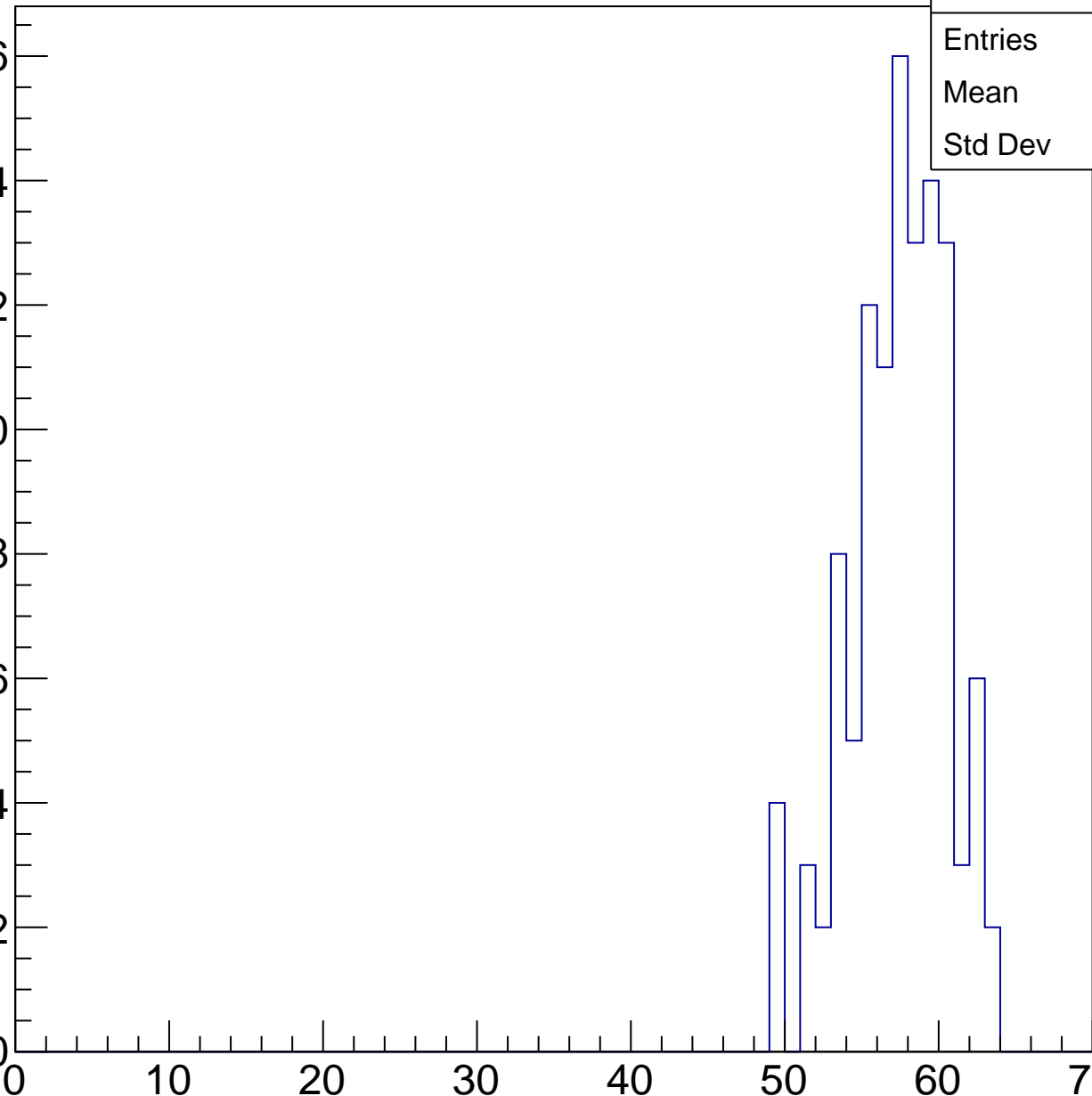
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	56.93
Std Dev	3.136

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch73, adc5

calib_packv5_042523_0143.root, FC#1, port C1

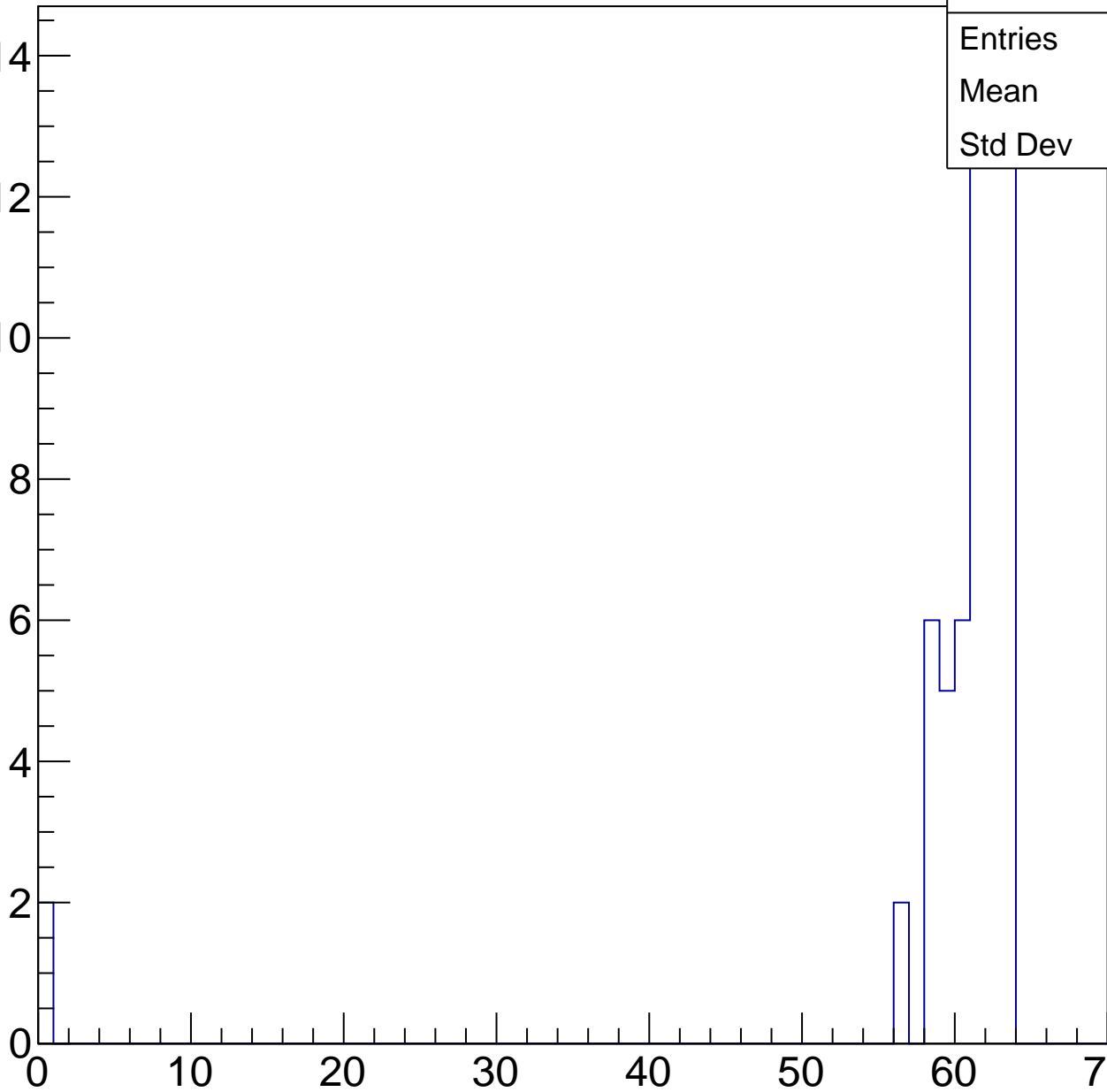
Entries	62
Mean	58.97
Std Dev	10.91

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

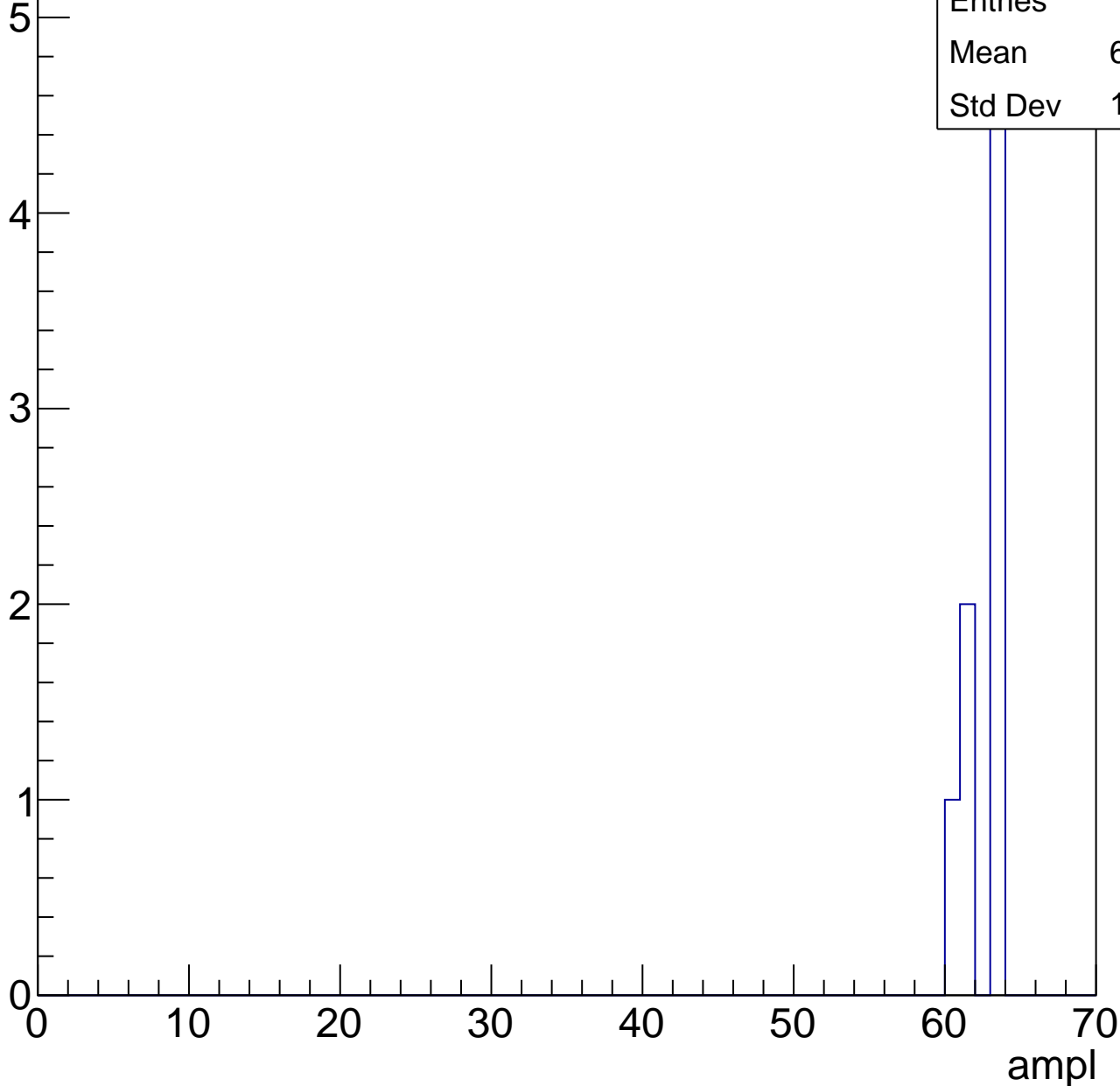


B0L101S, U19-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	62.12
Std Dev	1.166



B0L101S, U19-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch74, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	26.22
Std Dev	6.271

Gaus mean : 27.4176

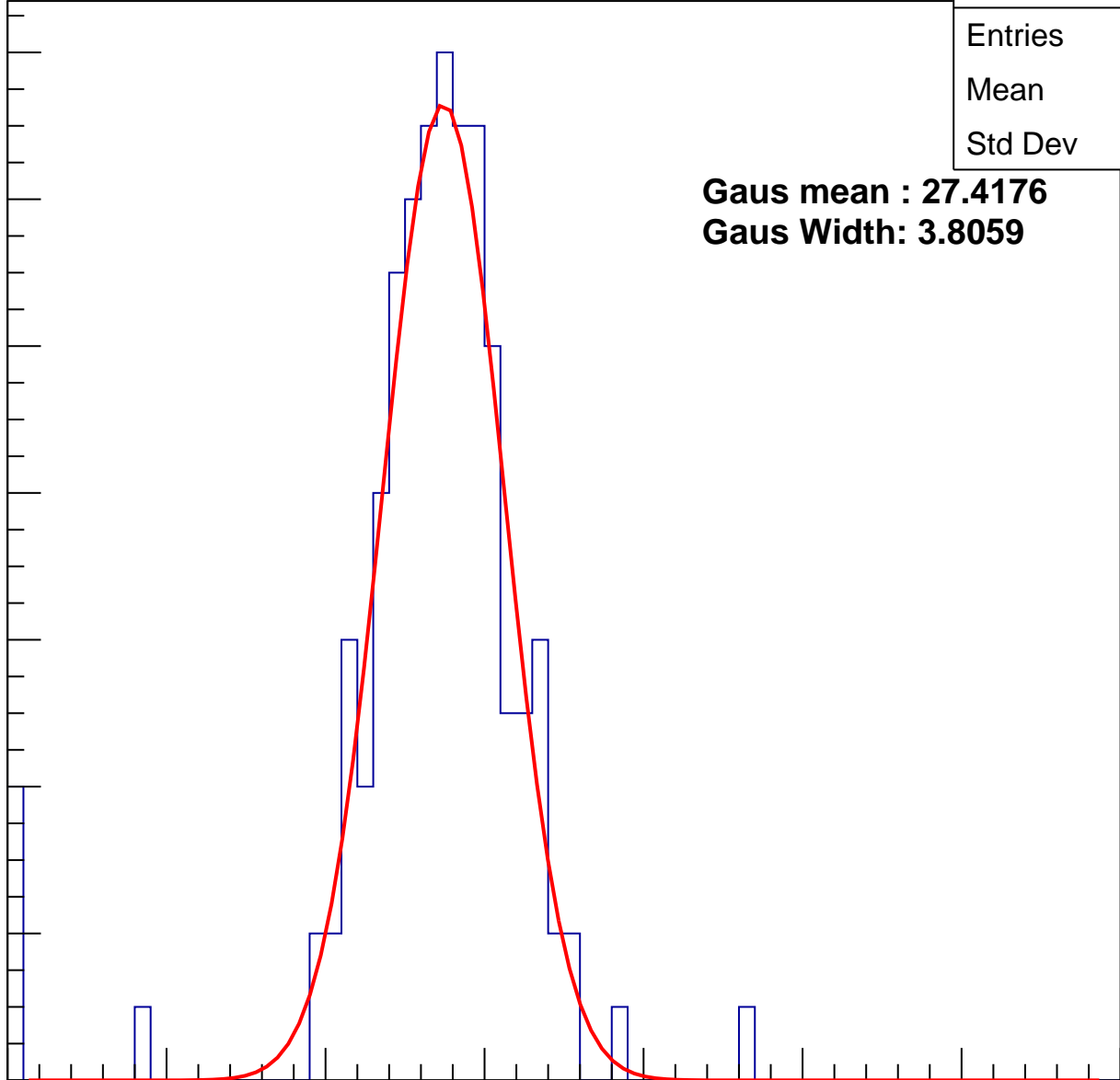
Gaus Width: 3.8059

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	33.59
Std Dev	3.989

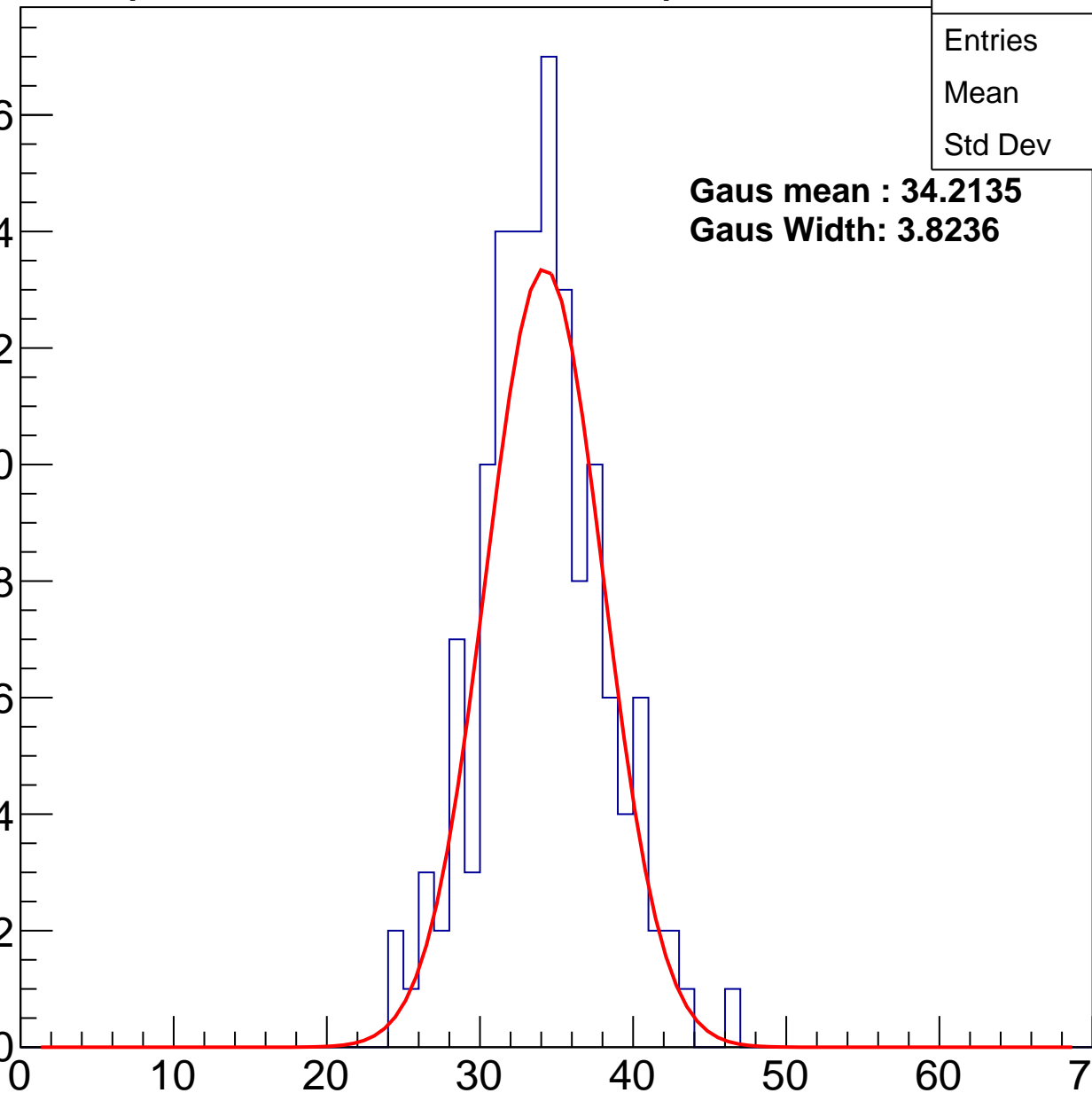
Gaus mean : 34.2135

Gaus Width: 3.8236

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch74, adc2

calib_packv5_042523_0143.root, FC#1, port C1

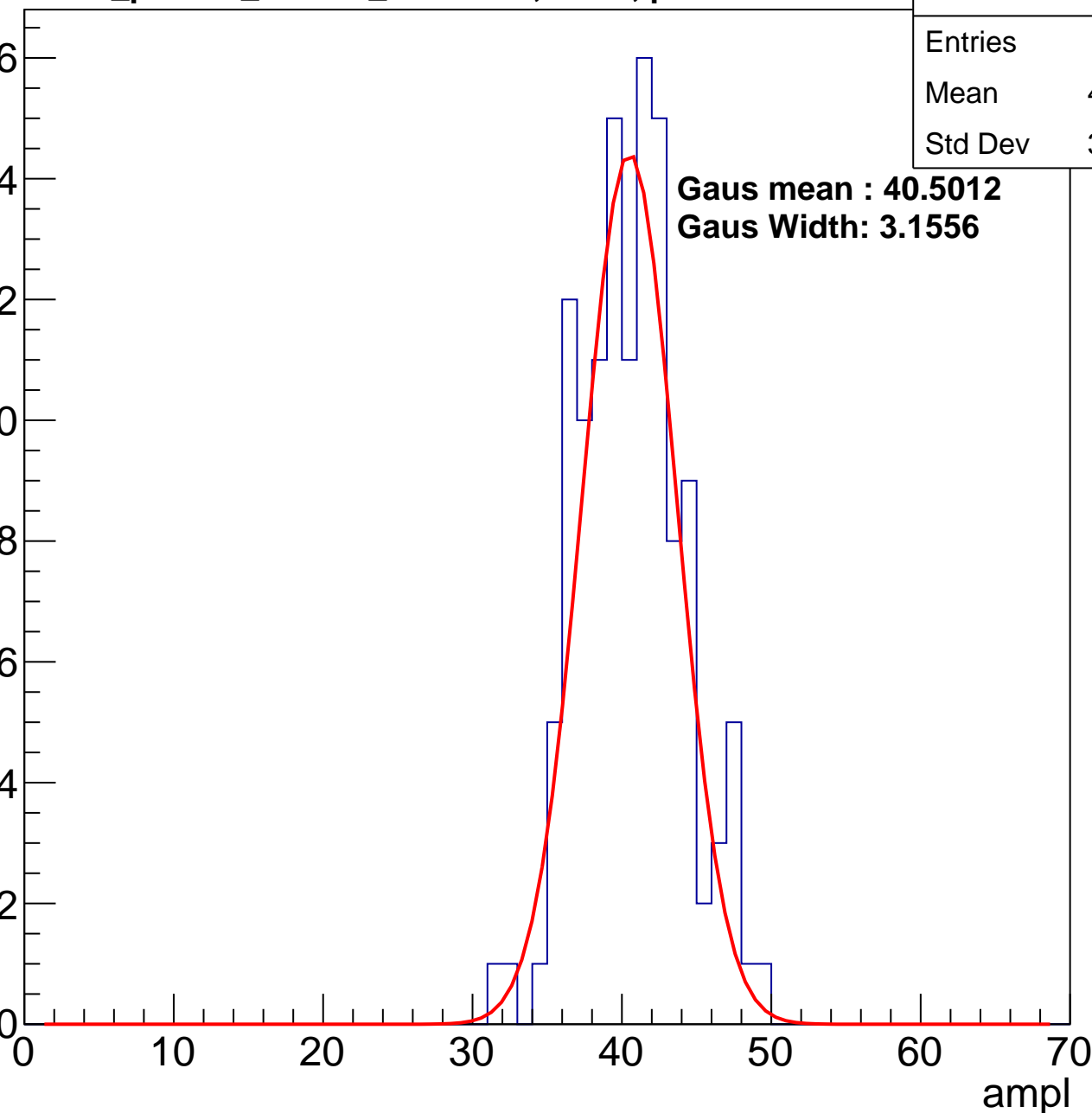
Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	40.18
Std Dev	3.417

Gaus mean : 40.5012

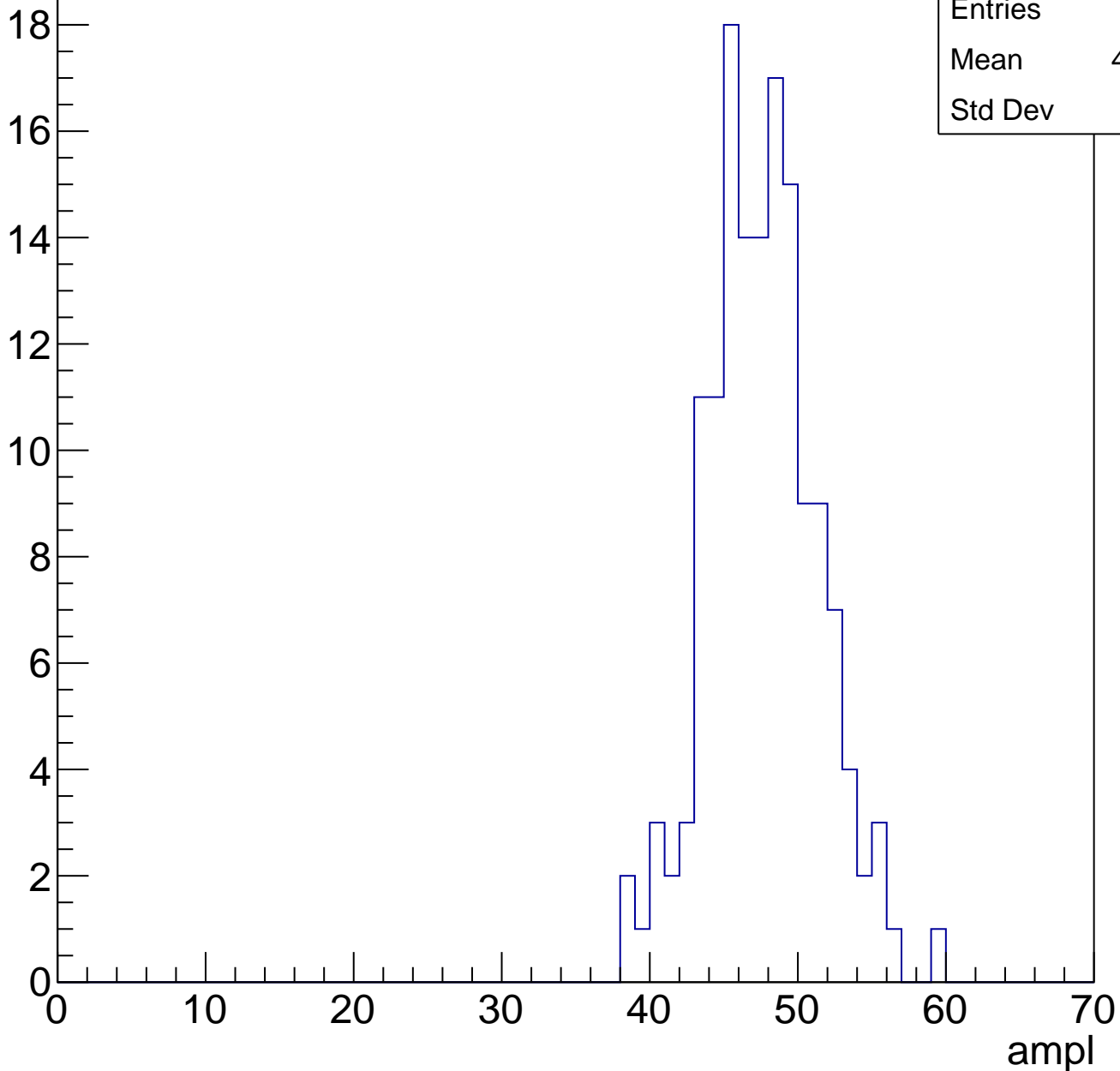
Gaus Width: 3.1556



B0L101S, U19-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch74, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

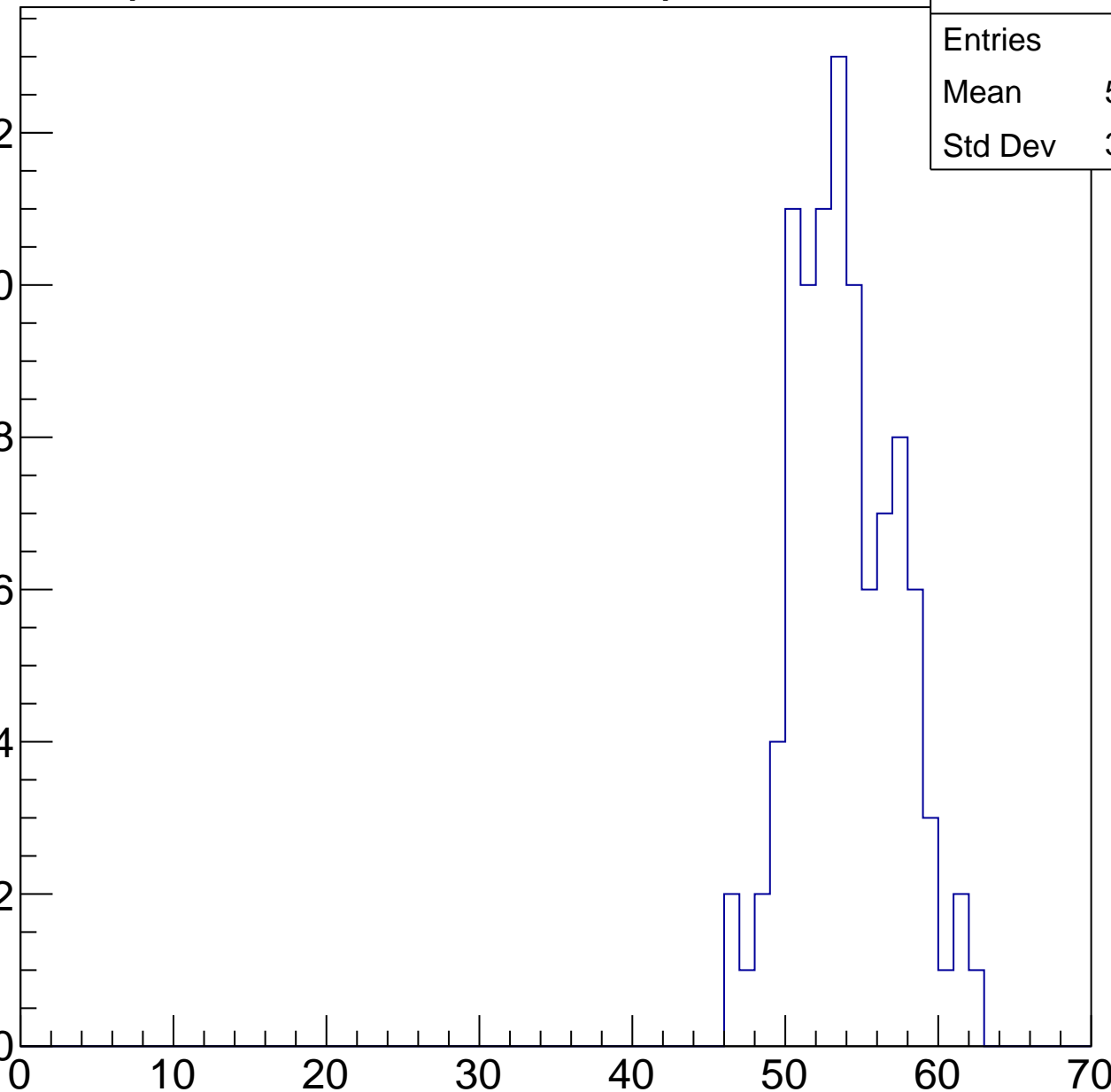
4

2

0

Entries	98
Mean	53.46
Std Dev	3.378

ampl



B0L101S, U19-ch74, adc5

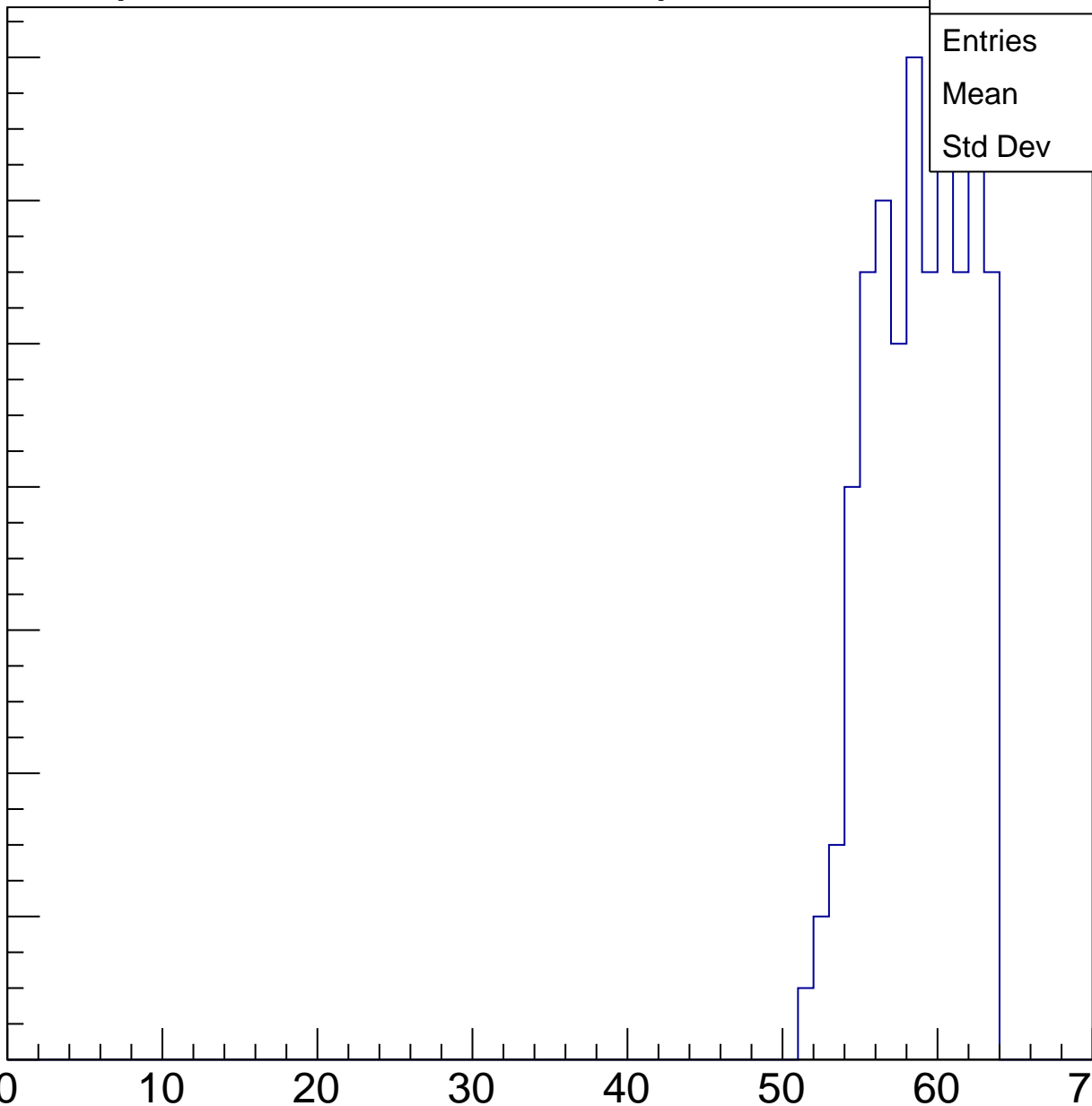
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	58.41
Std Dev	3.037

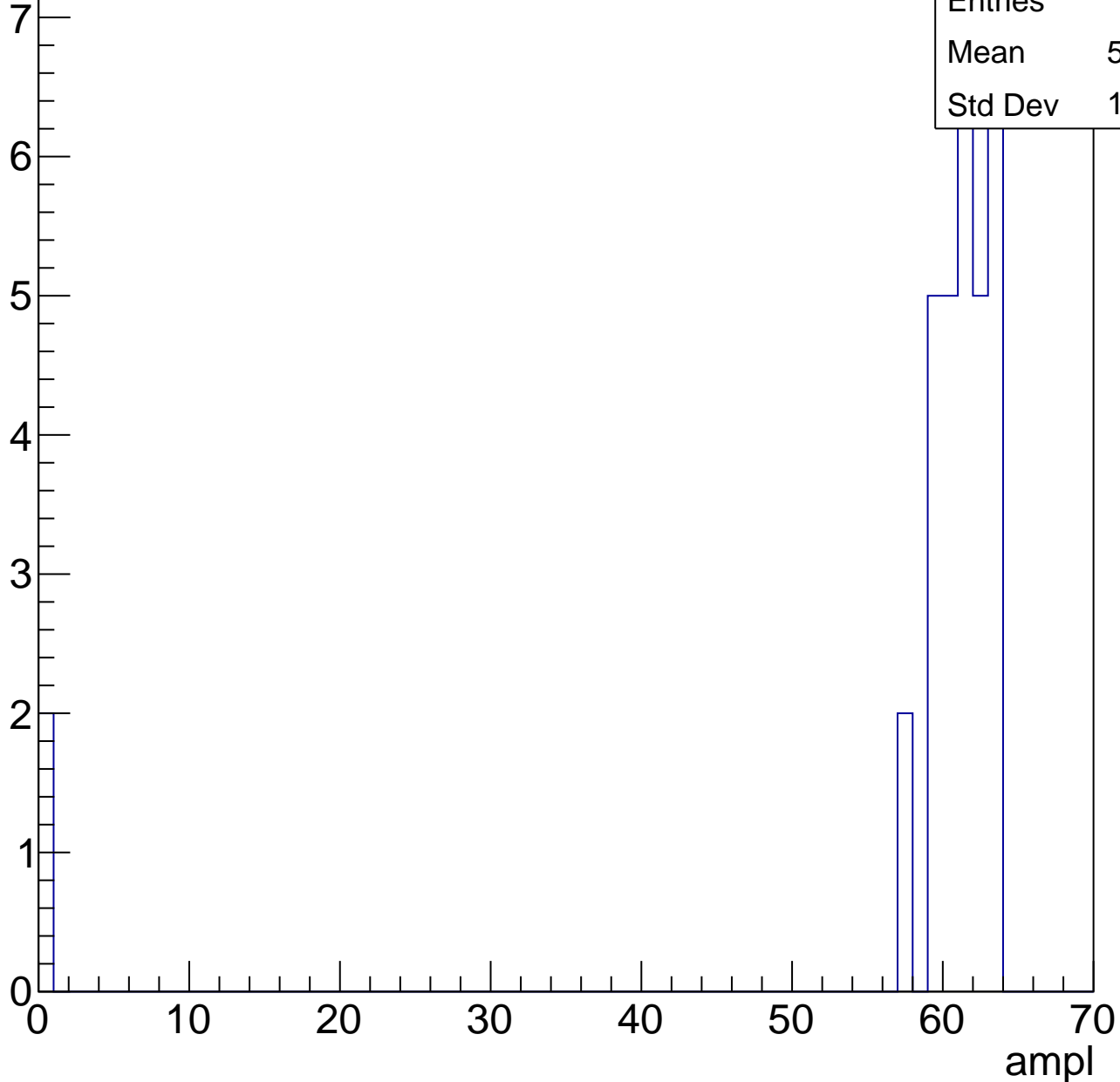
ampl



B0L101S, U19-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

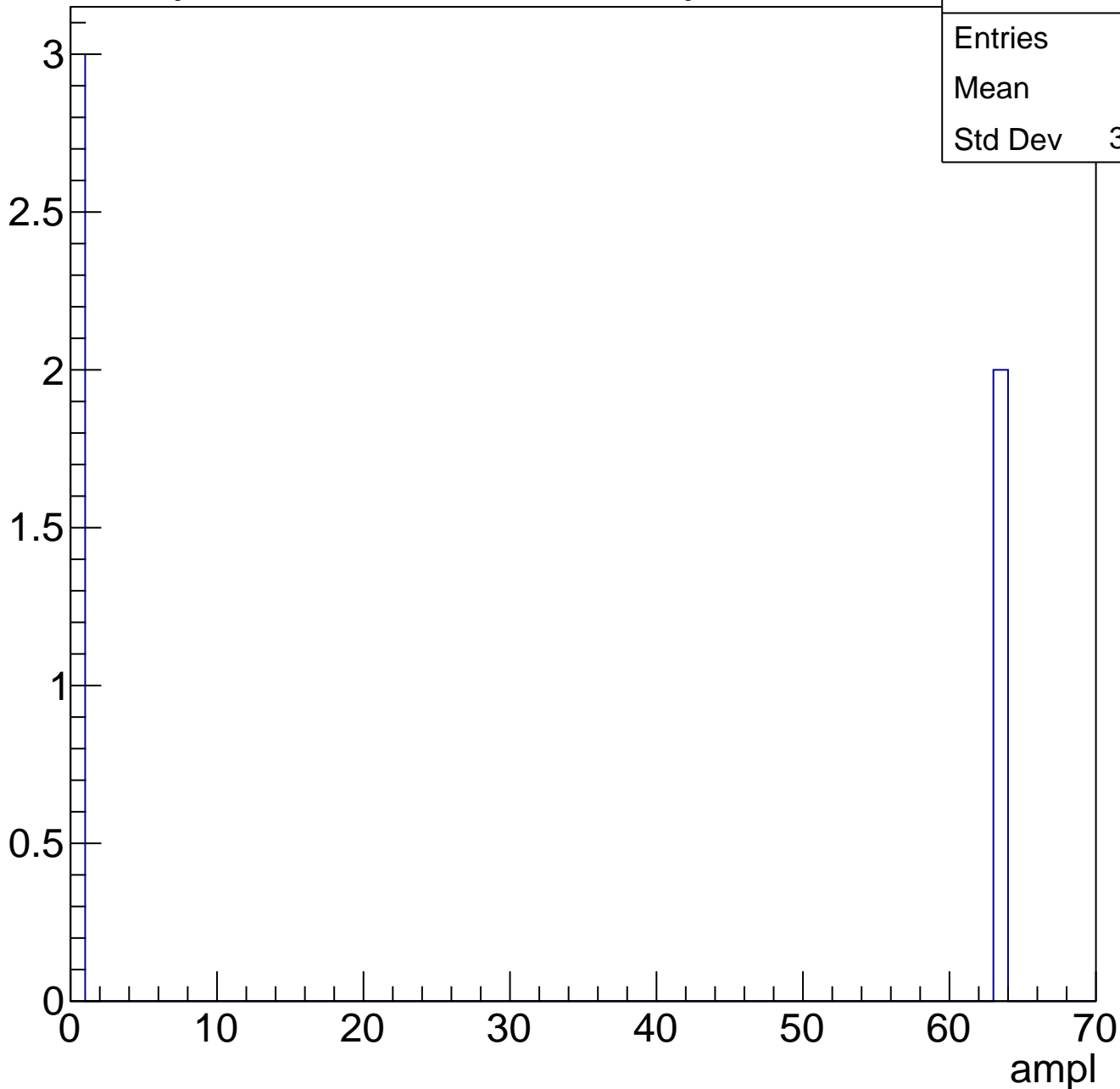
Entry



B0L101S, U19-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

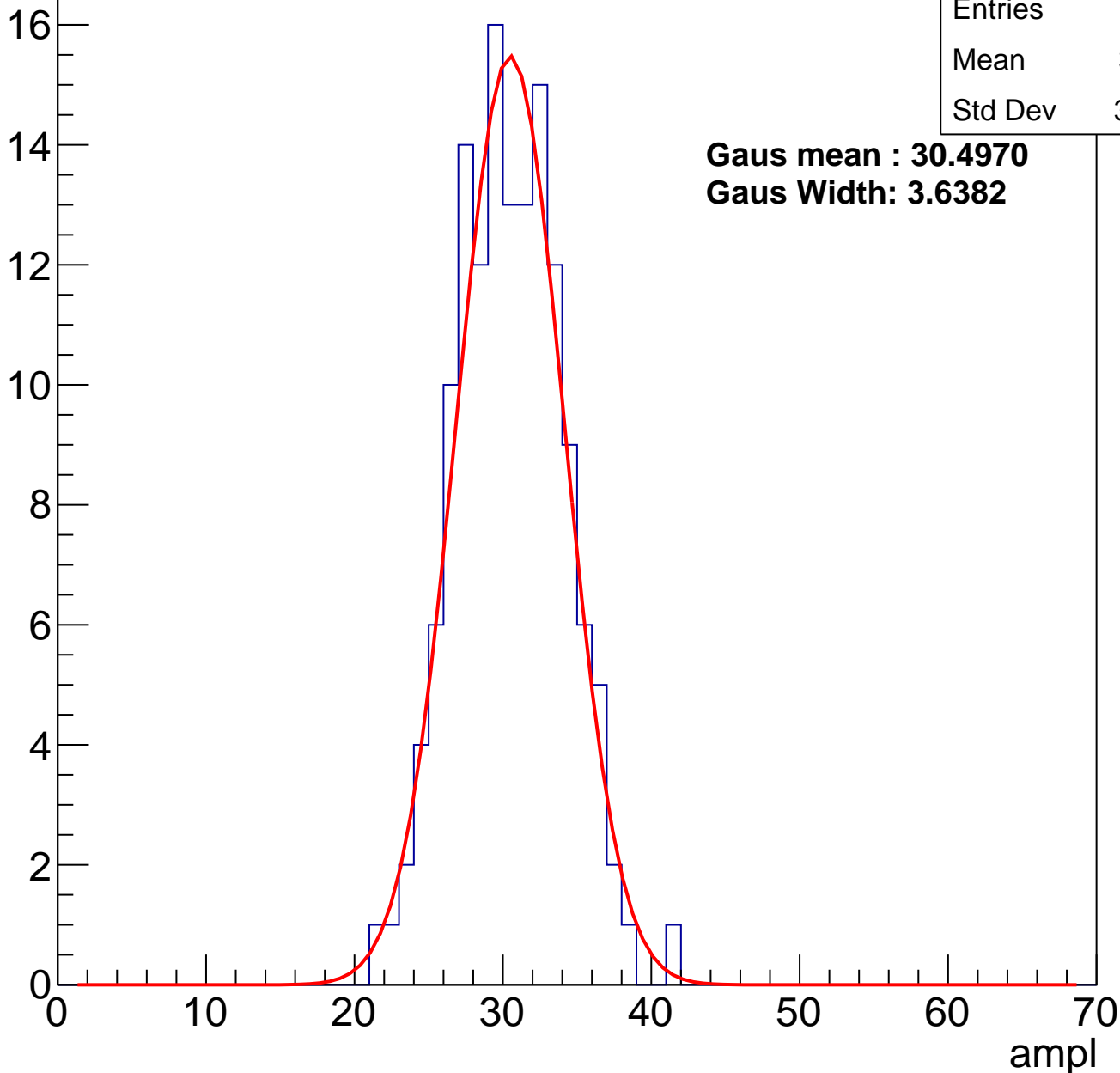
Entry



B0L101S, U19-ch75, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch75, adc1

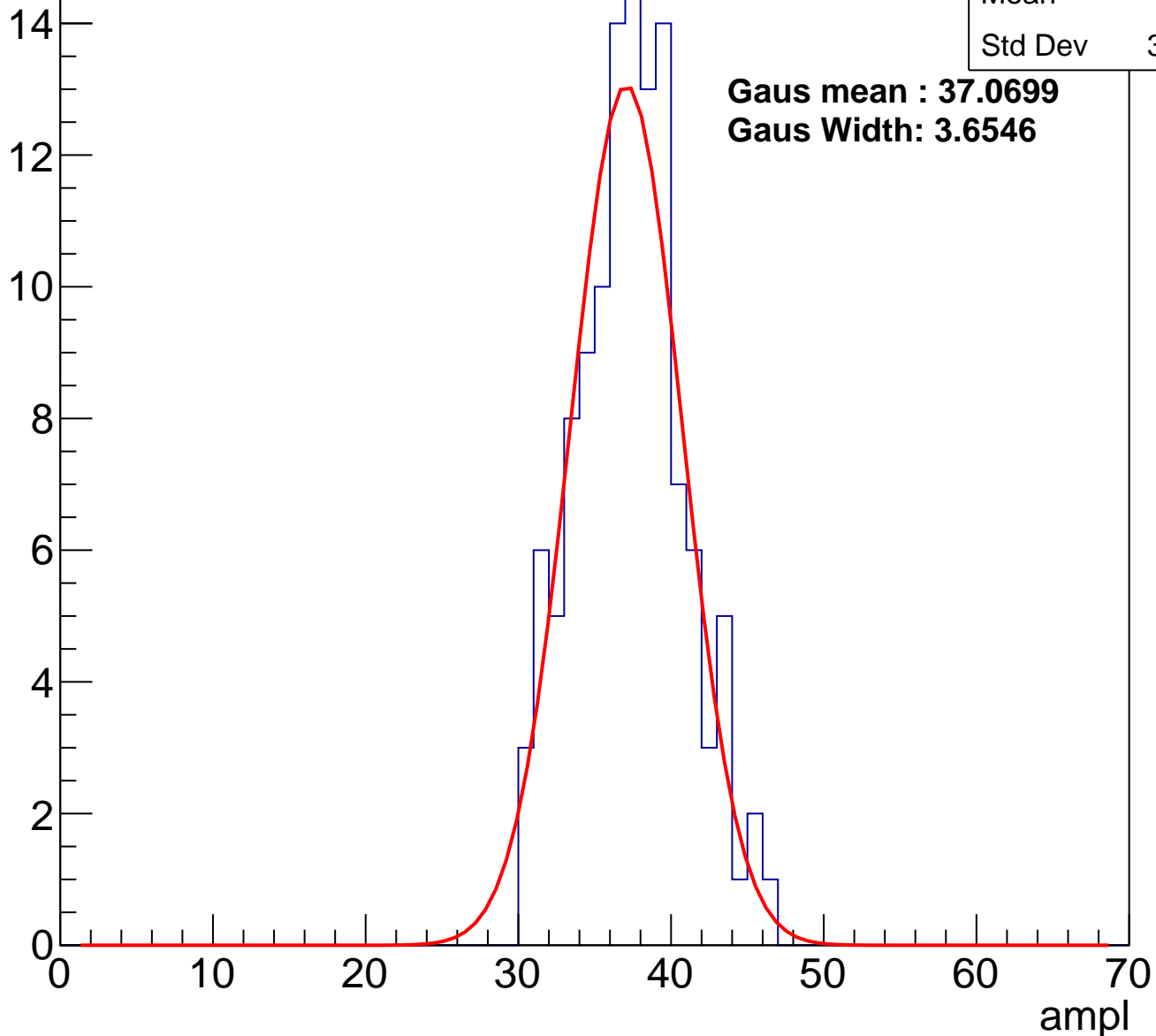
calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	36.9
Std Dev	3.496

Gaus mean : 37.0699

Gaus Width: 3.6546

Entry



B0L101S, U19-ch75, adc2

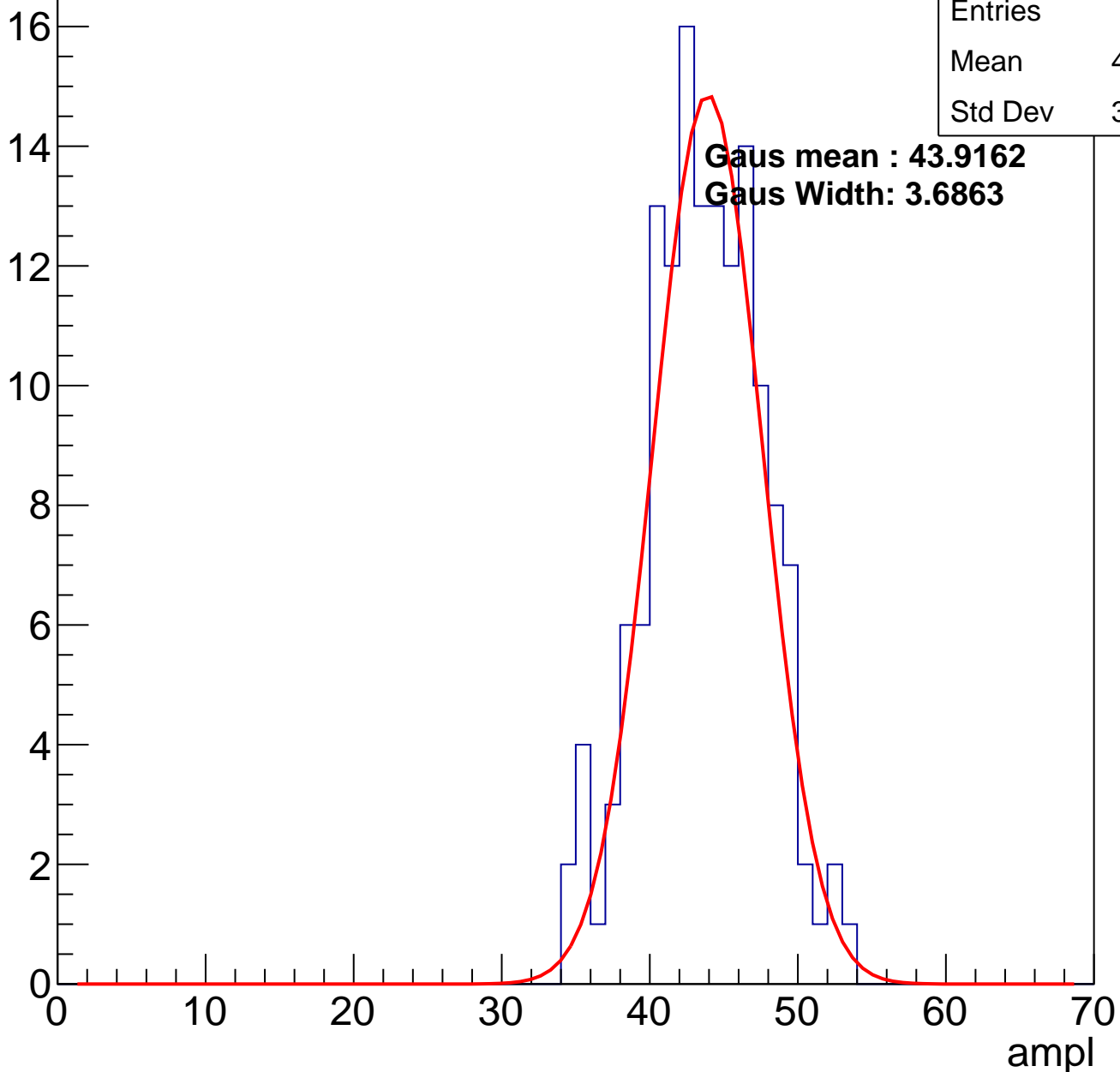
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	43.29
Std Dev	3.893

Gaus mean : 43.9162

Gaus Width: 3.6863

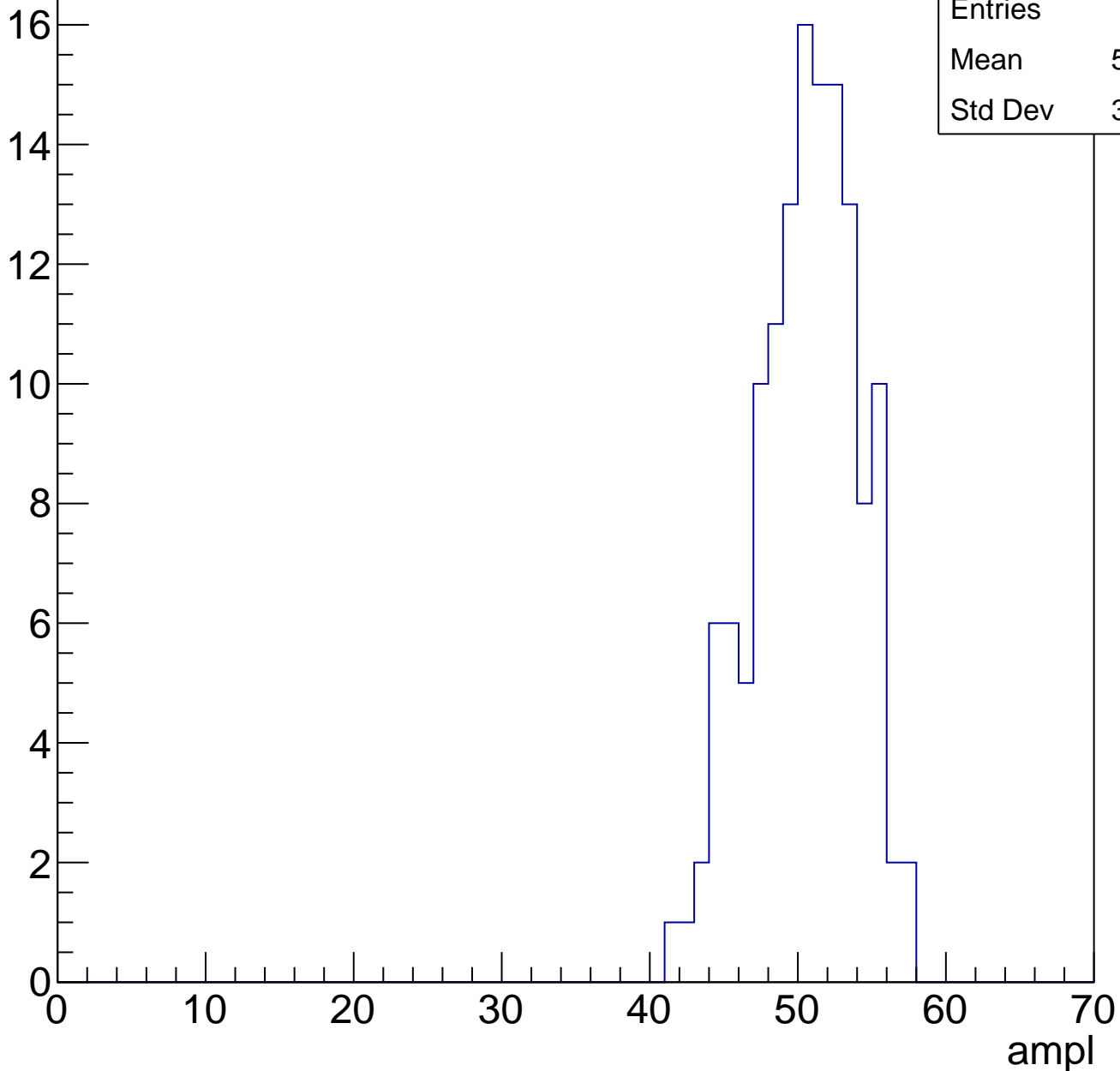
Entry



B0L101S, U19-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



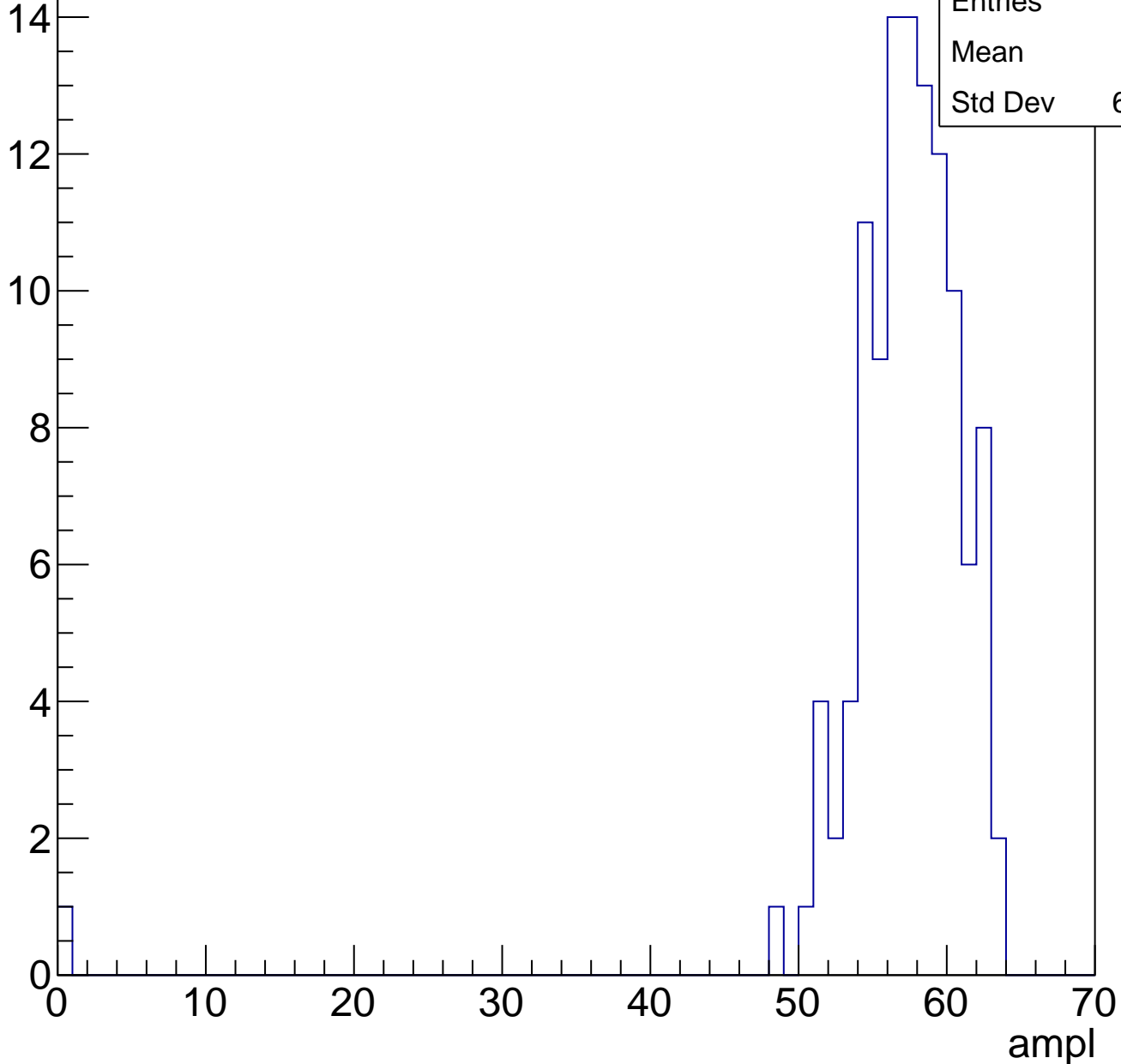
Entries	136
Mean	50.07
Std Dev	3.403

B0L101S, U19-ch75, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	56.6
Std Dev	6.187

Entry



B0L101S, U19-ch75, adc5

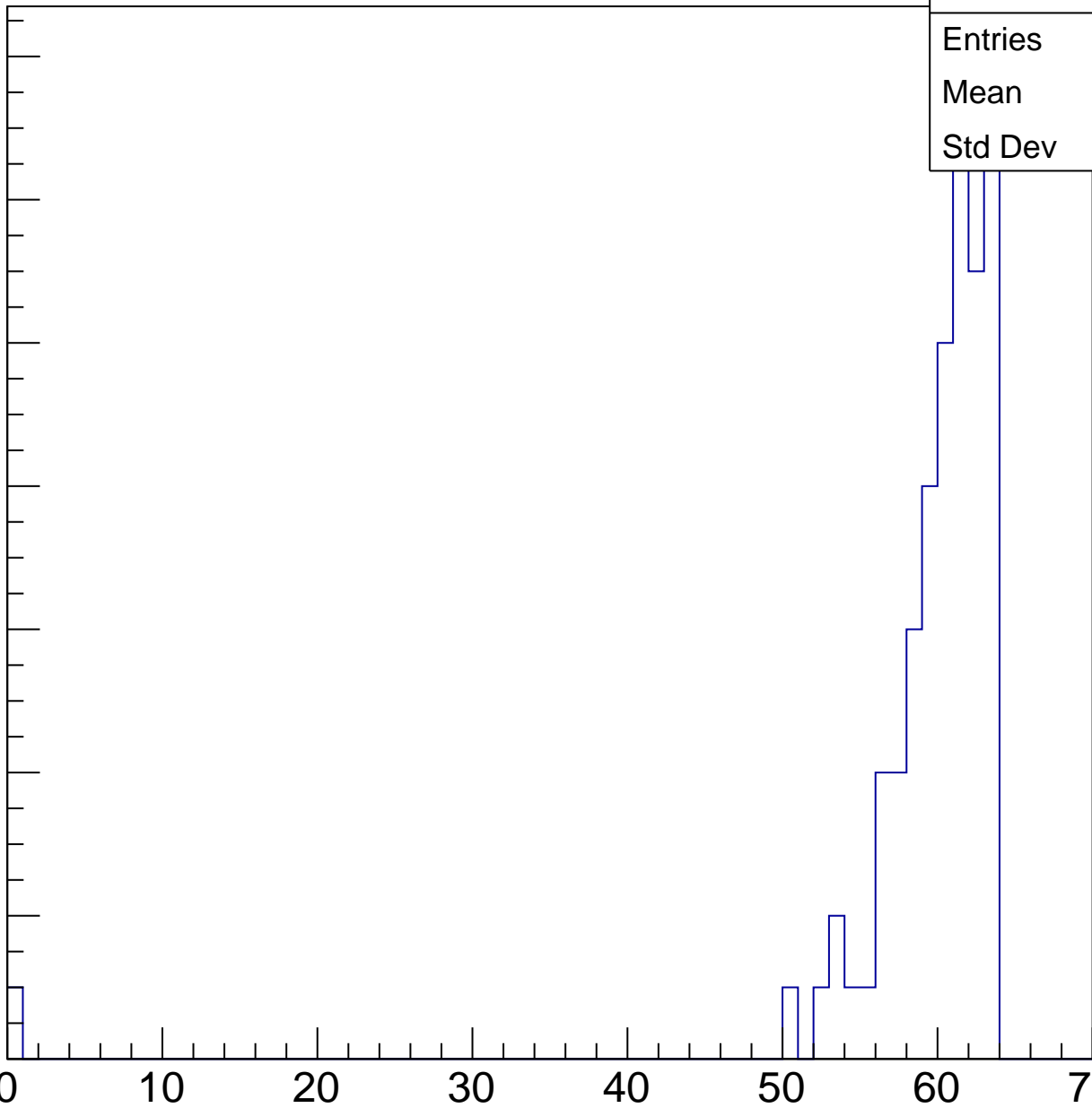
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	59.04
Std Dev	7.356

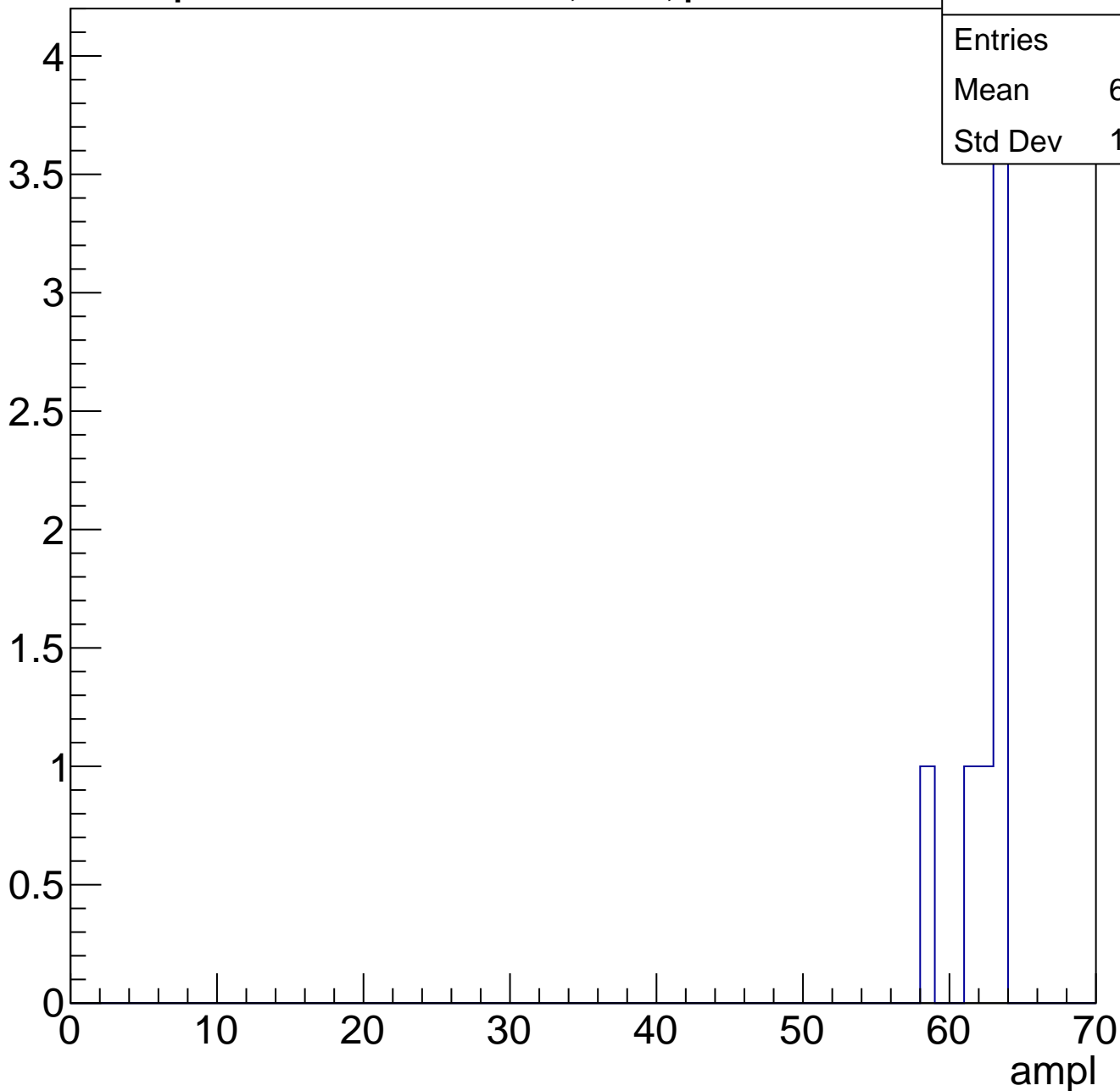
ampl



B0L101S, U19-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

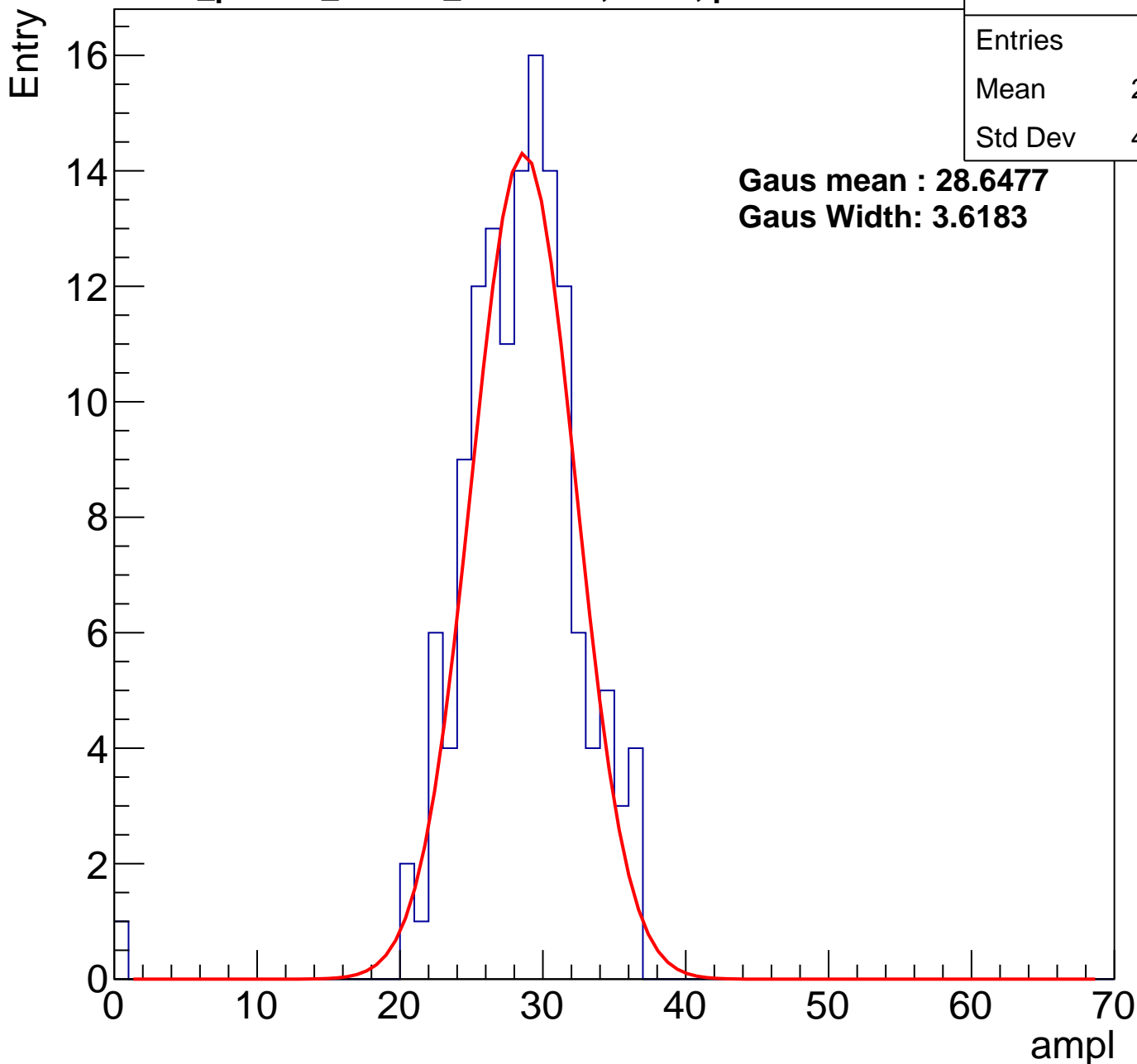
B0L101S, U19-ch76, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	27.93
Std Dev	4.315

Gaus mean : 28.6477

Gaus Width: 3.6183



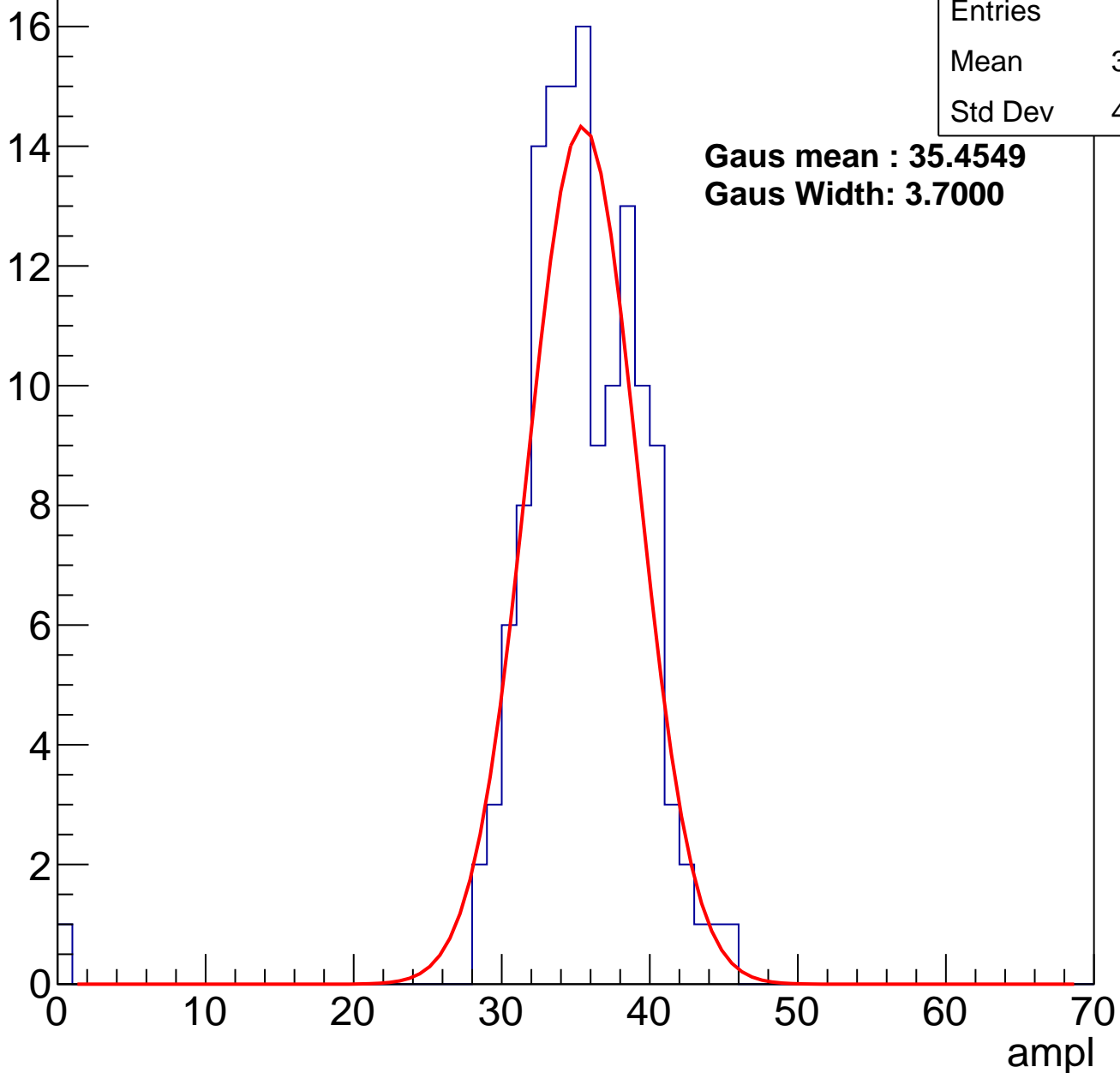
B0L101S, U19-ch76, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	34.97
Std Dev	4.577

Gaus mean : 35.4549
Gaus Width: 3.7000

Entry



B0L101S, U19-ch76, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

126

Mean

41.85

Std Dev

3.742

Gaus mean : 42.4951

Gaus Width: 3.8132

0

10

20

30

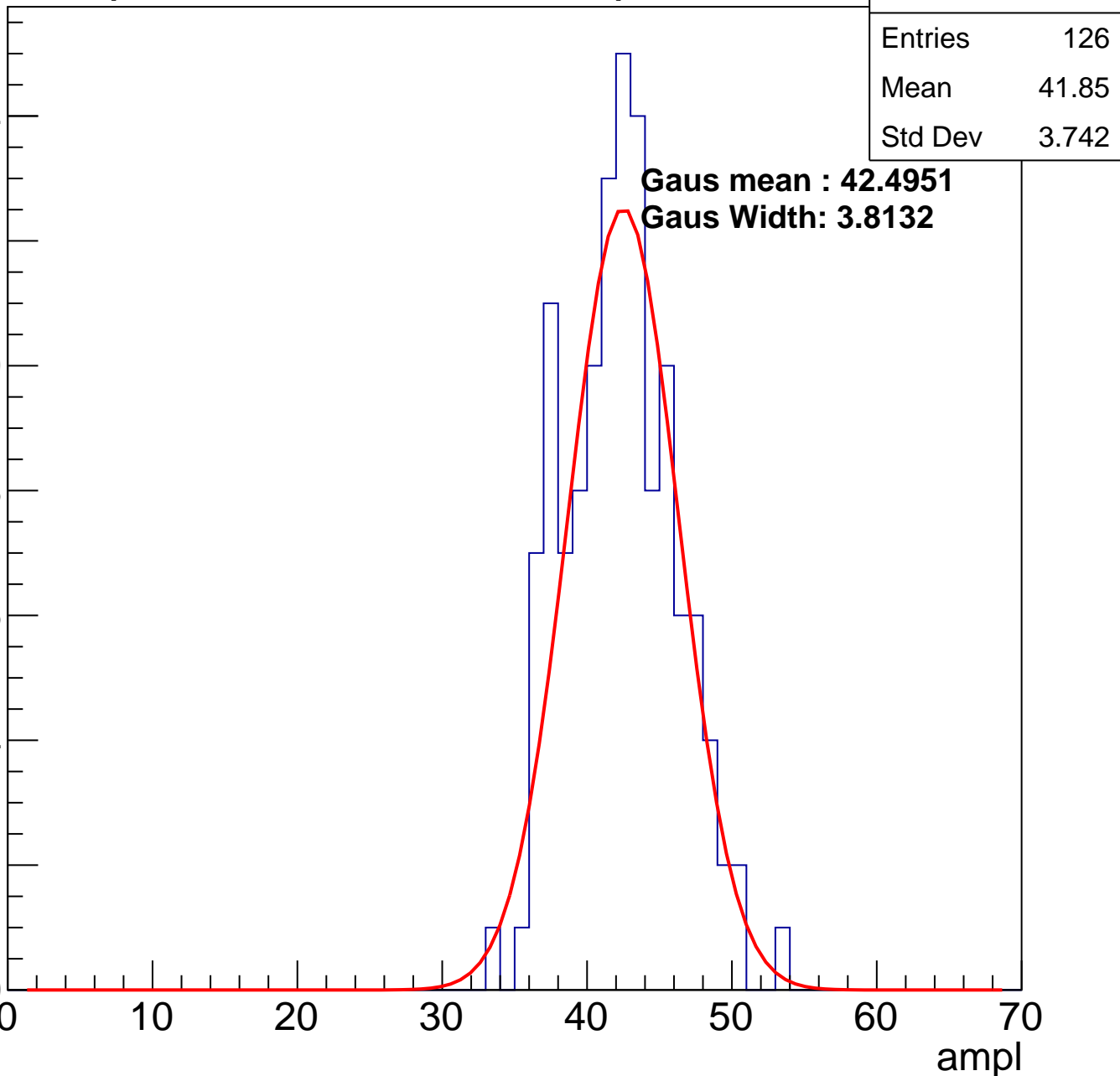
40

50

60

70

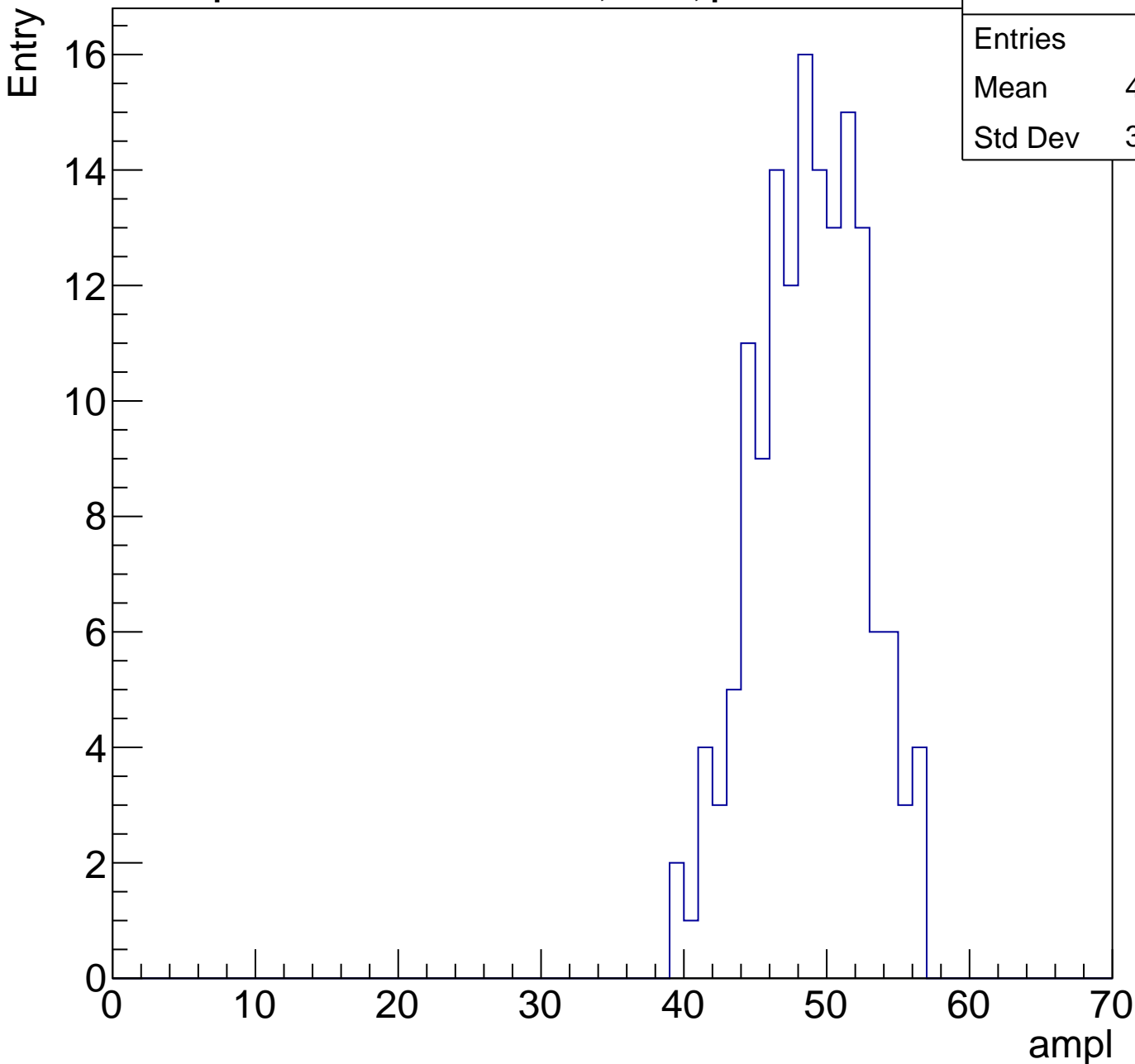
ampl



B0L101S, U19-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

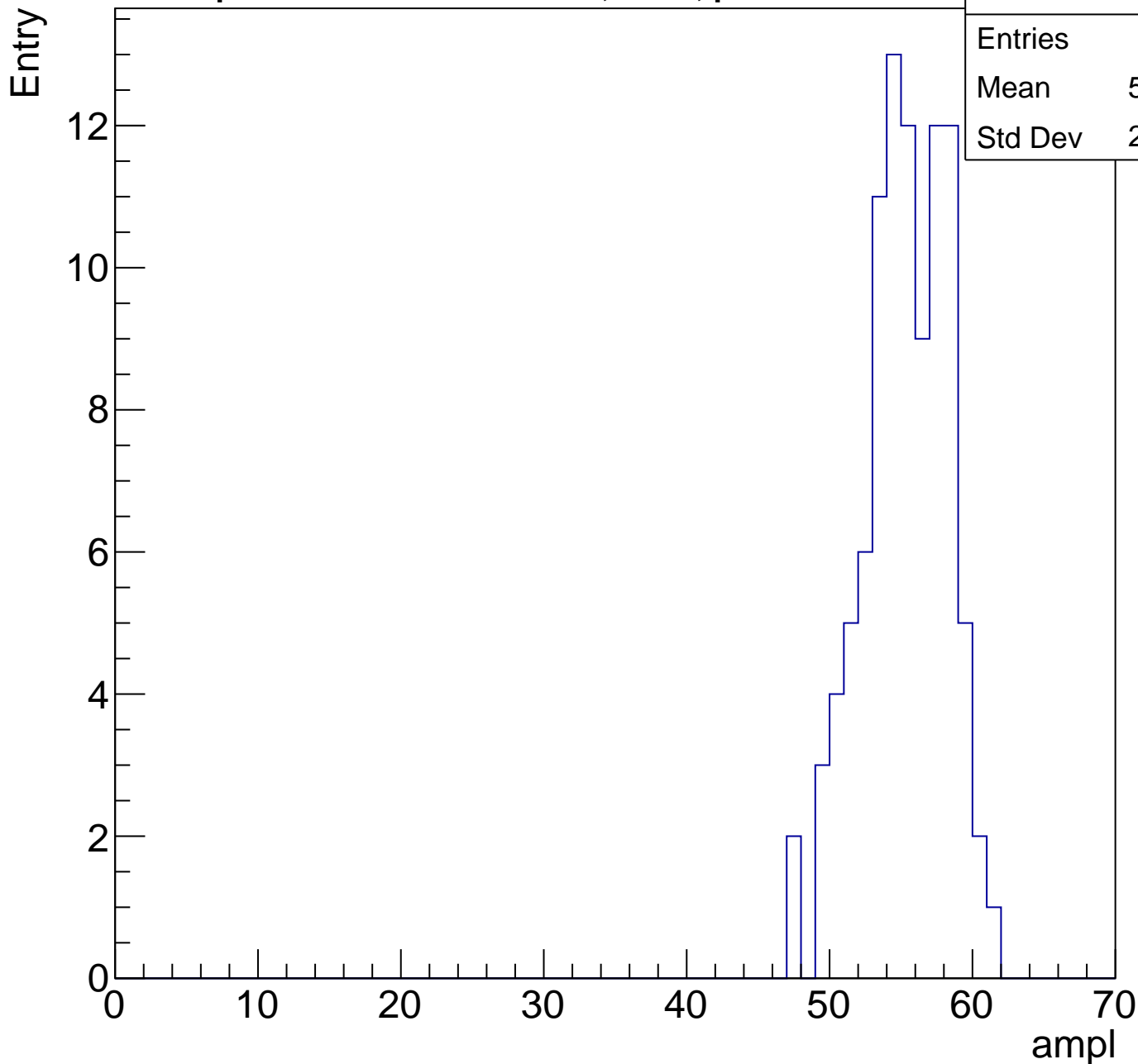
Entries	151
Mean	48.32
Std Dev	3.767



B0L101S, U19-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	54.77
Std Dev	2.965



B0L101S, U19-ch76, adc5

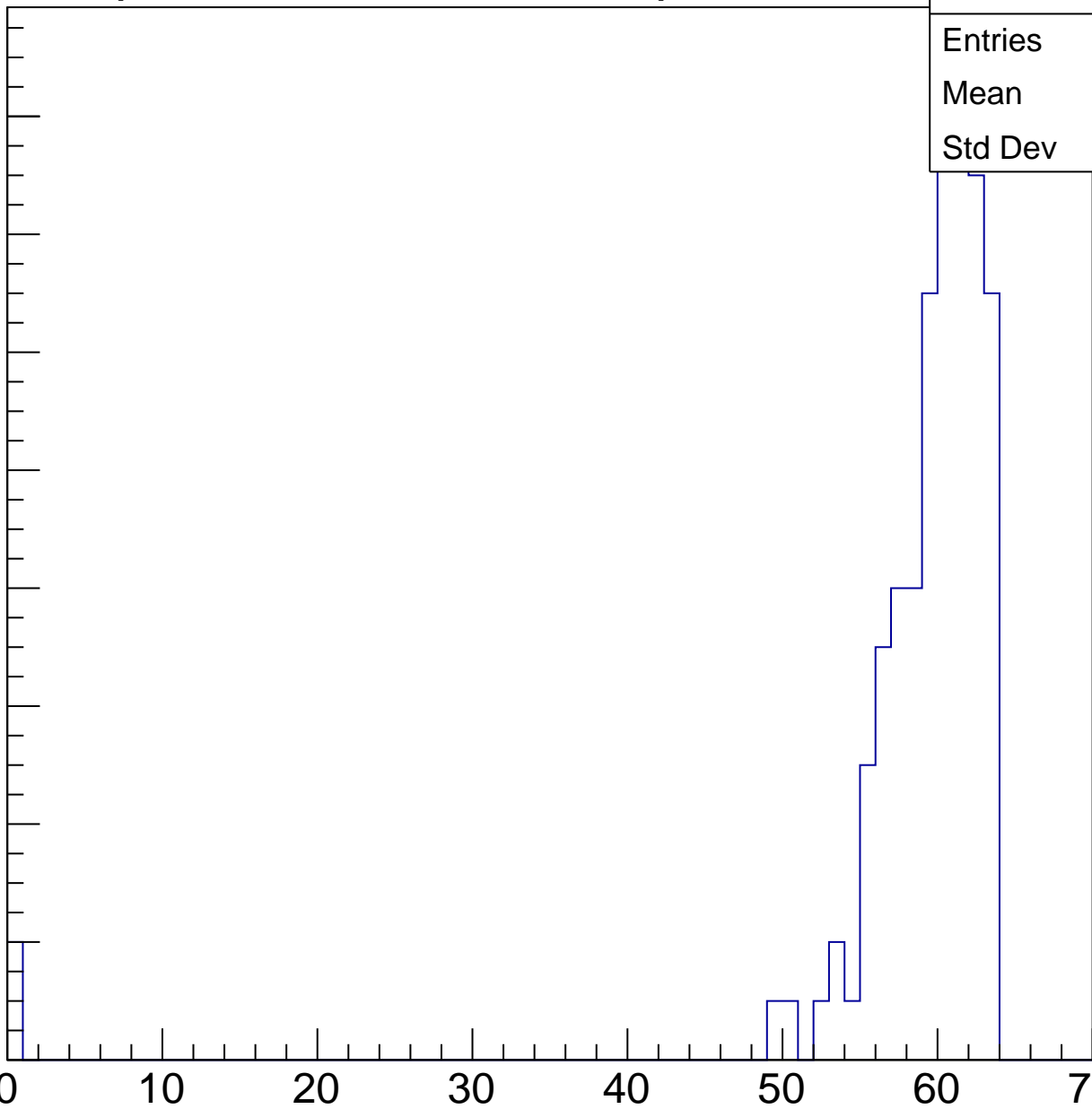
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.3
Std Dev	8.407

ampl

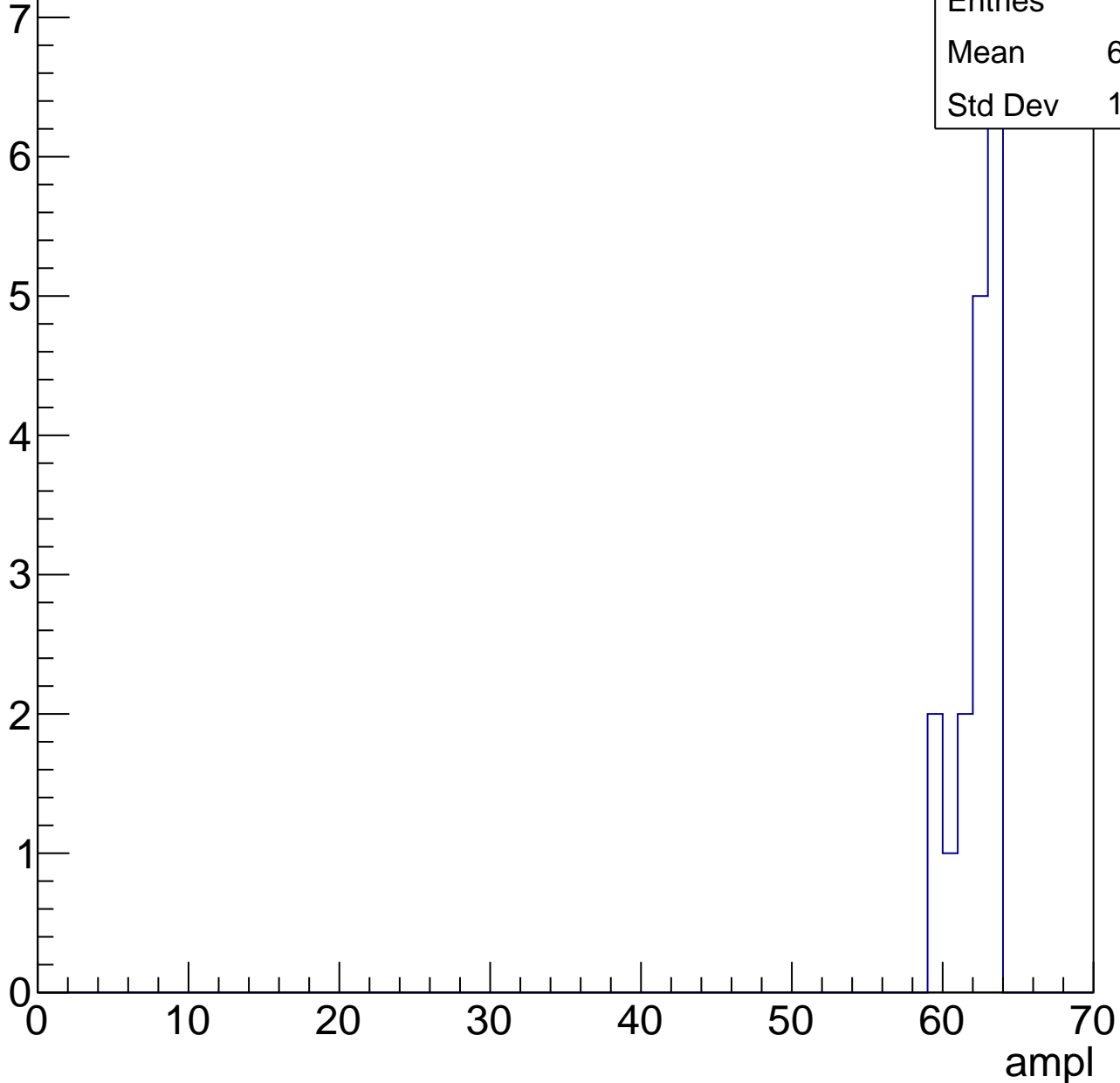


B0L101S, U19-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	61.82
Std Dev	1.339



B0L101S, U19-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch77, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	30.15
Std Dev	3.301

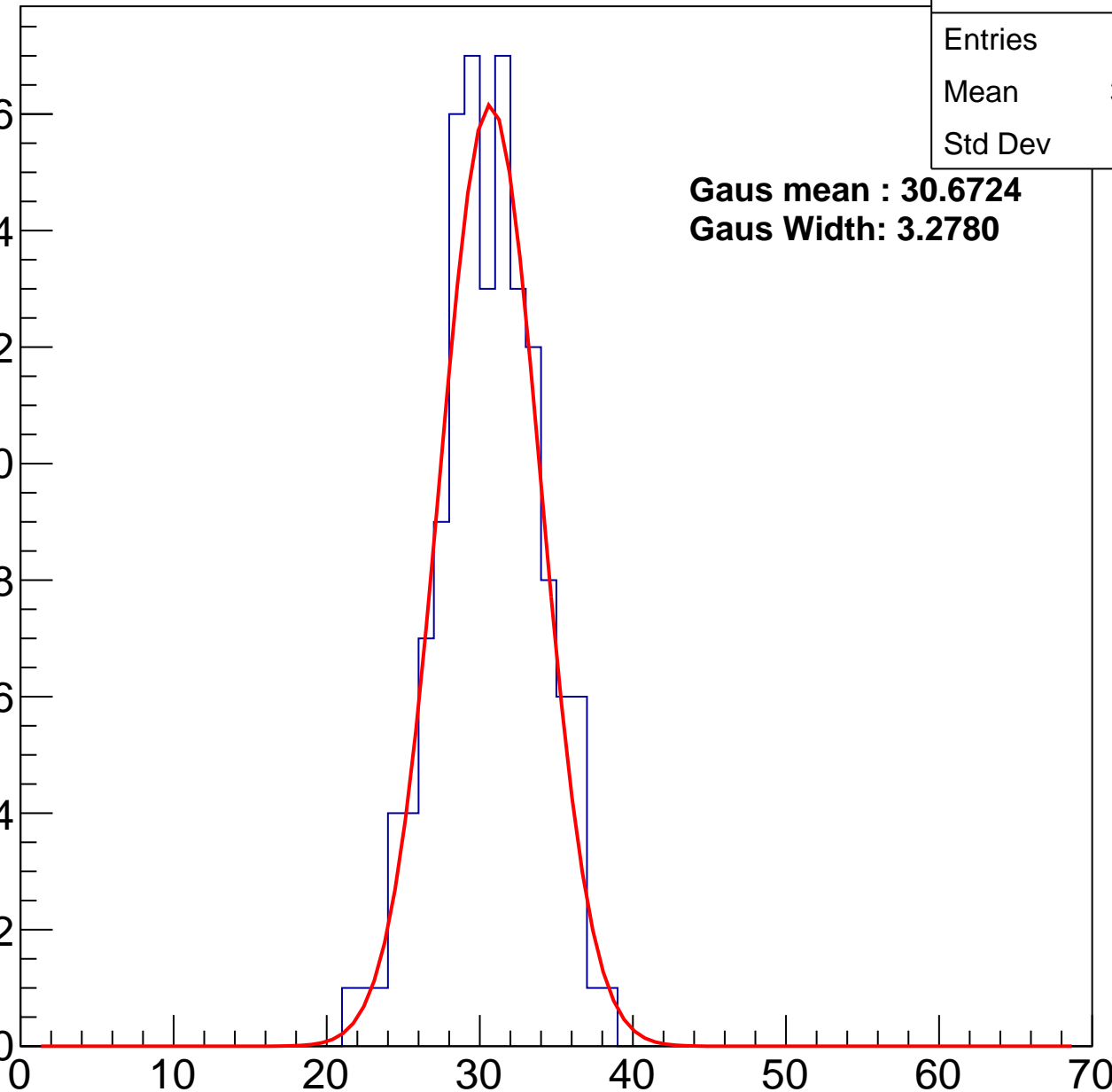
Gaus mean : 30.6724

Gaus Width: 3.2780

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch77, adc1

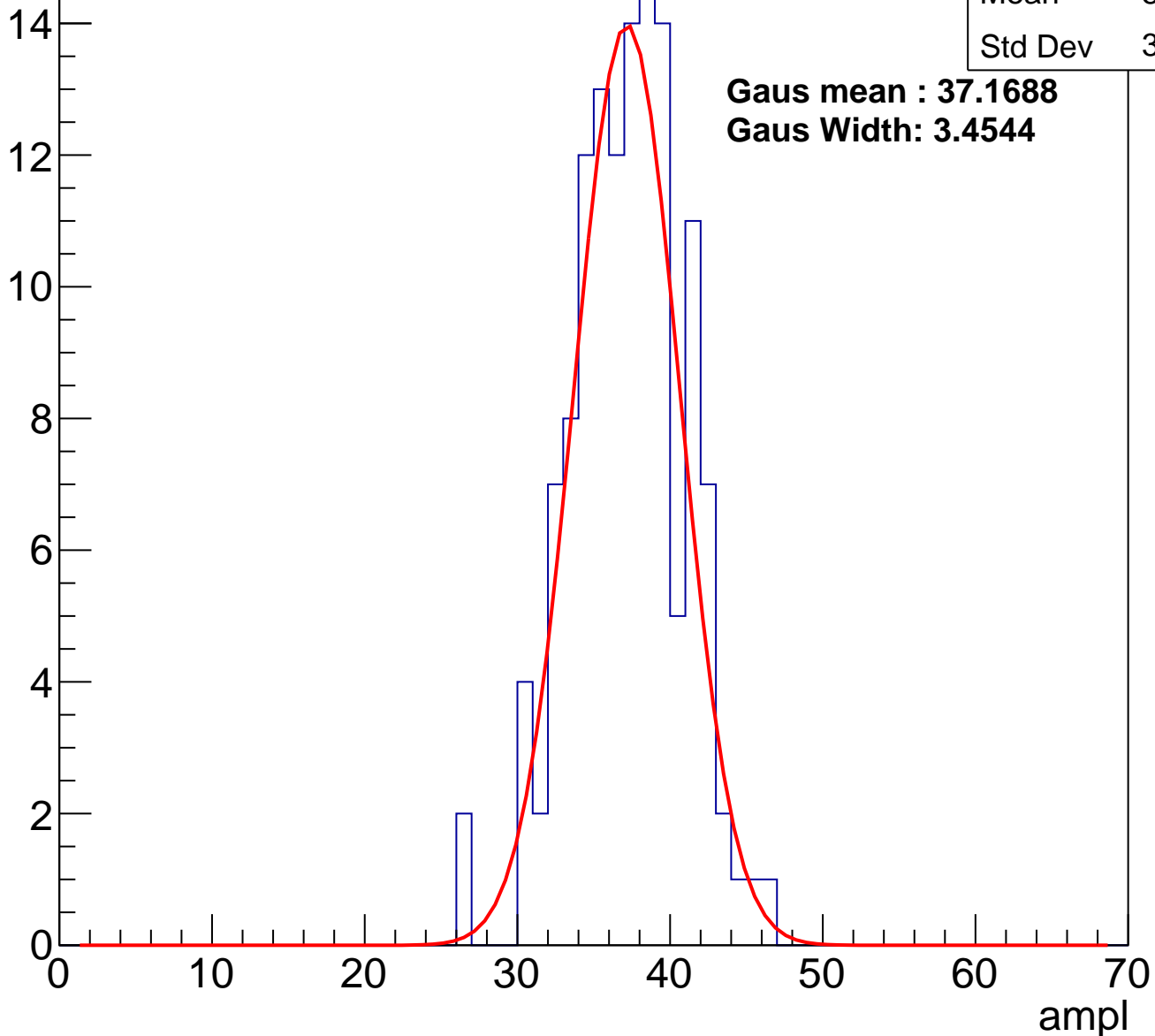
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.77
Std Dev	3.614

Gaus mean : 37.1688

Gaus Width: 3.4544

Entry



B0L101S, U19-ch77, adc2

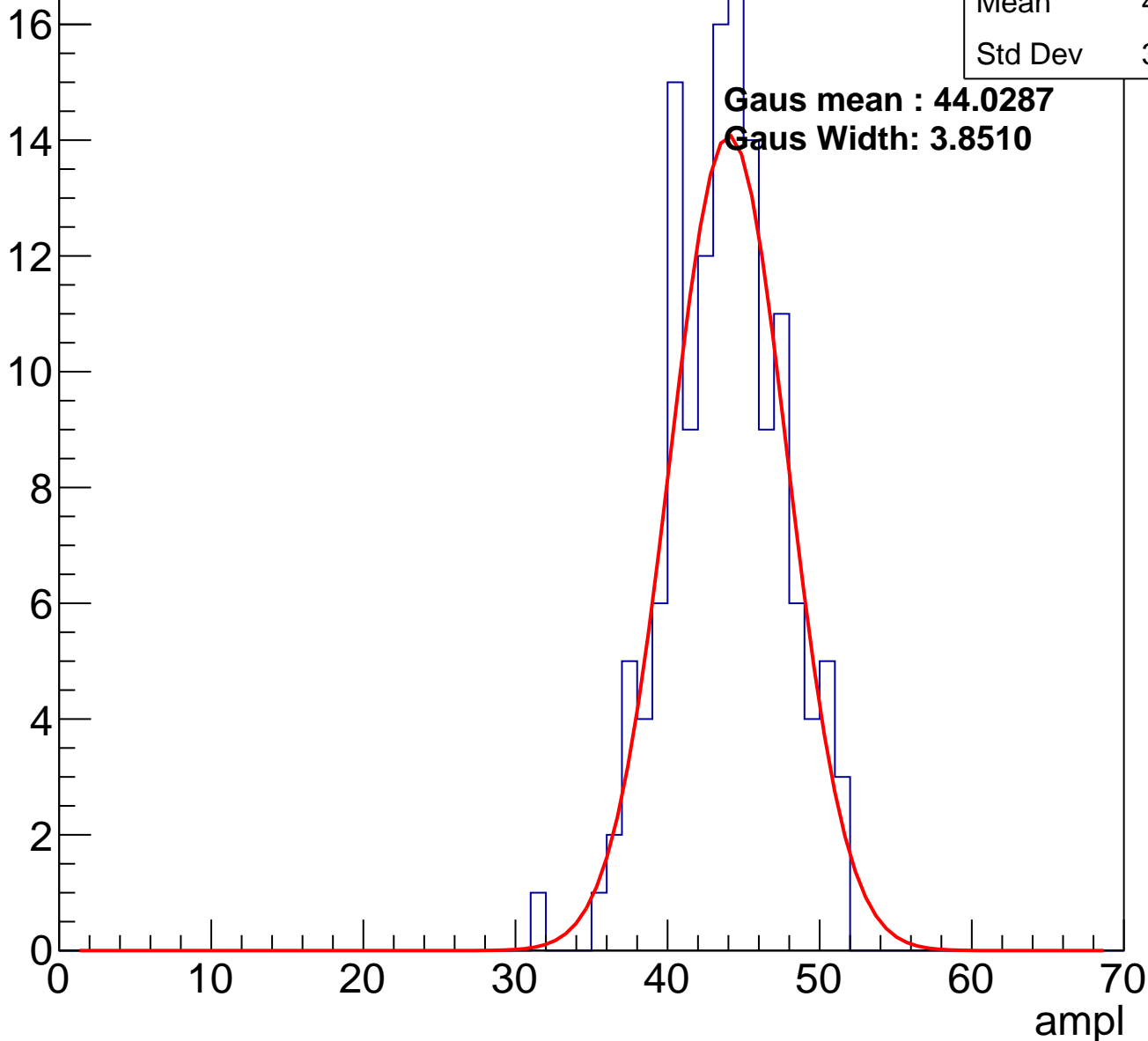
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	140
Mean	43.33
Std Dev	3.694

Gaus mean : 44.0287

Gaus Width: 3.8510



B0L101S, U19-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

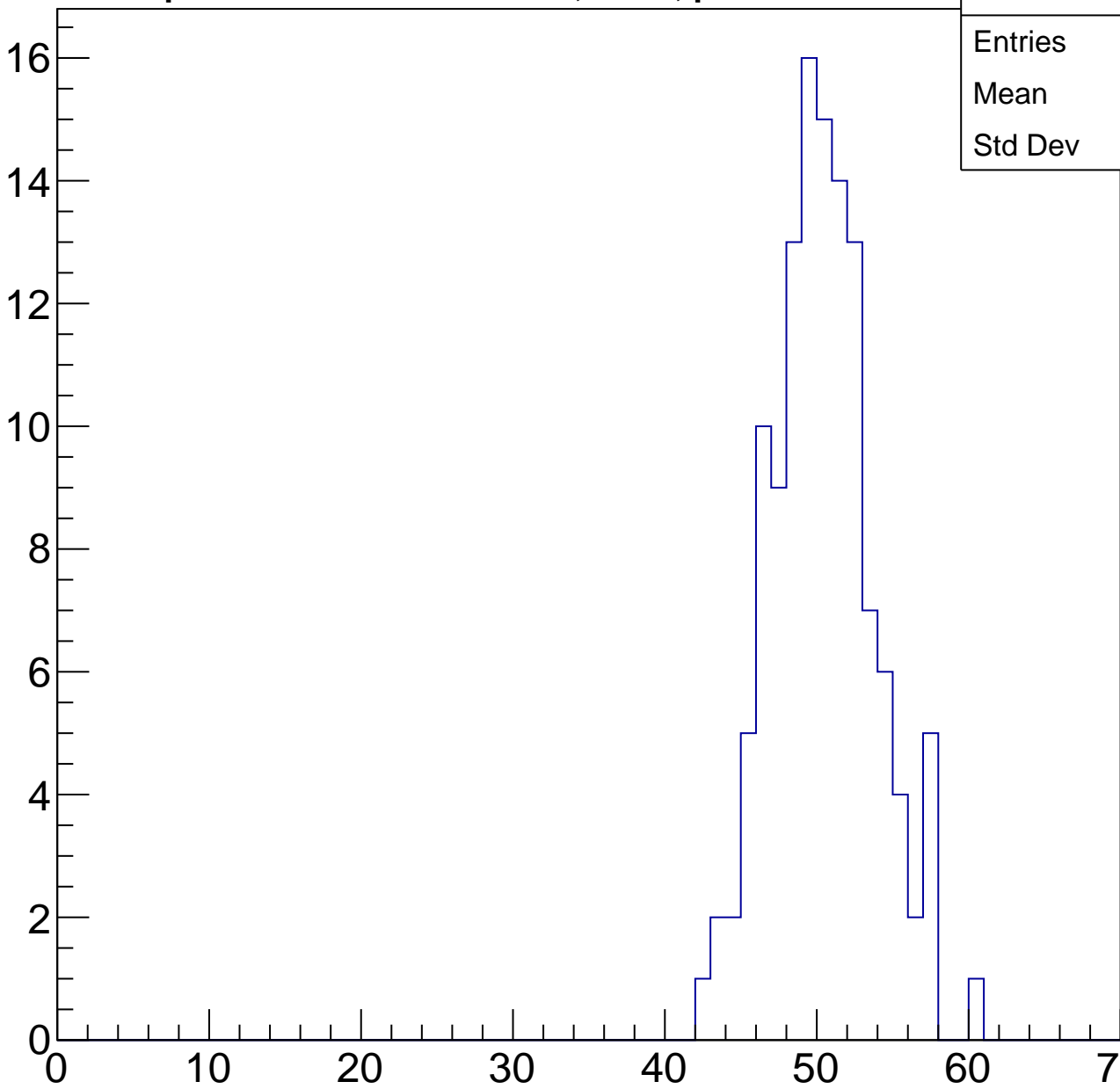
Entries	125
Mean	49.95
Std Dev	3.358

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

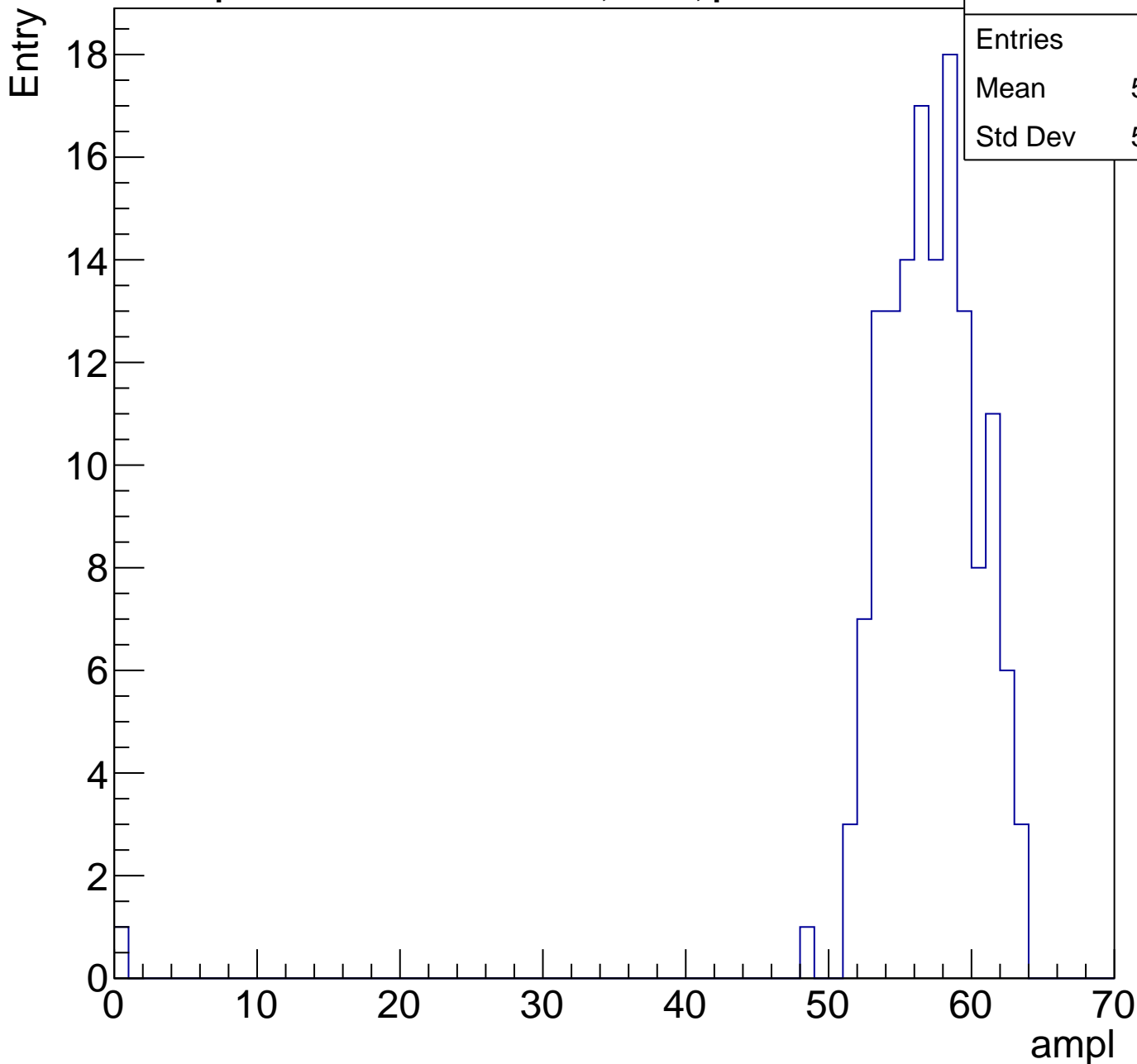
ampl



B0L101S, U19-ch77, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	56.33
Std Dev	5.644



B0L101S, U19-ch77, adc5

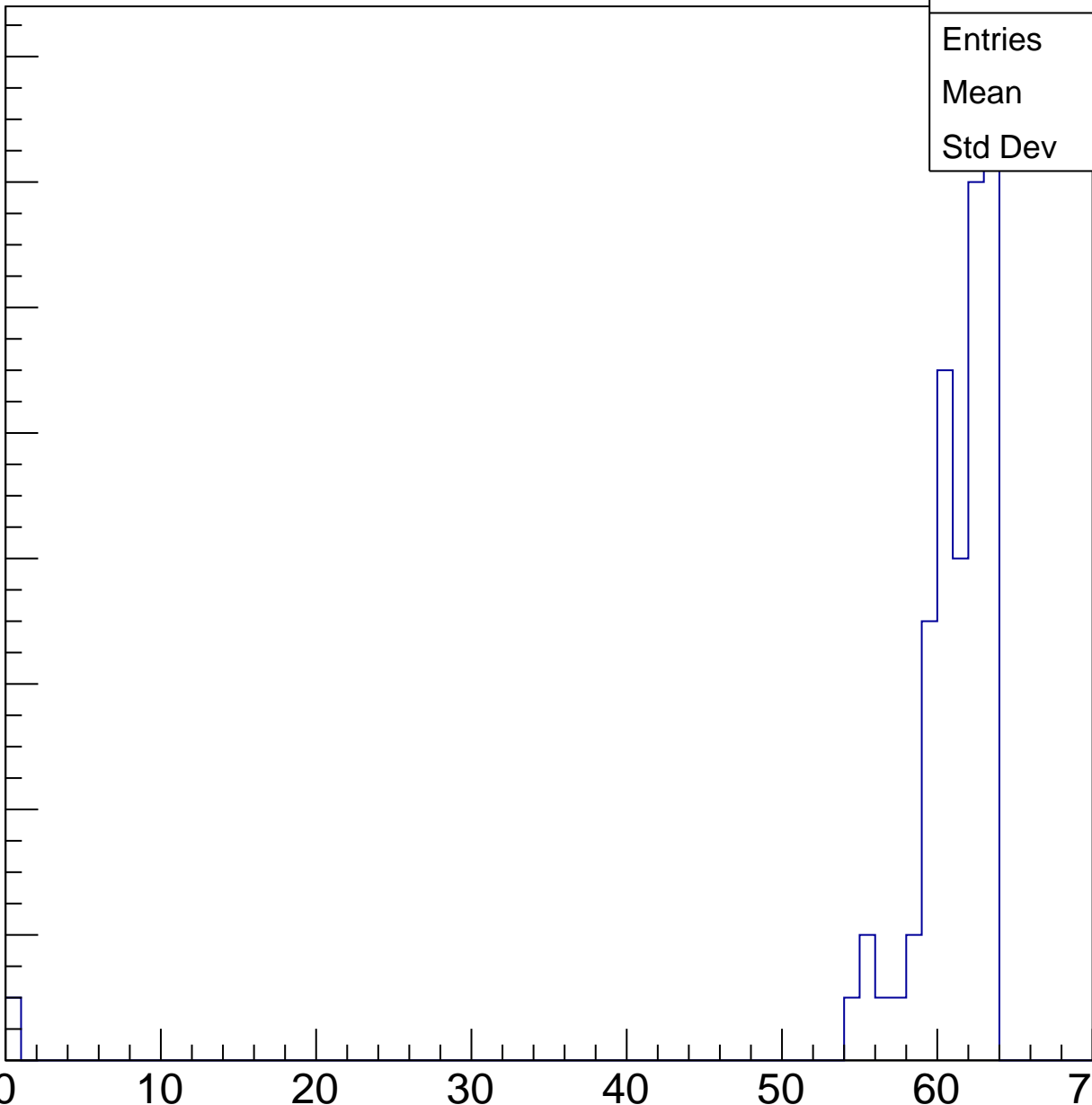
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	64
Mean	59.84
Std Dev	7.839

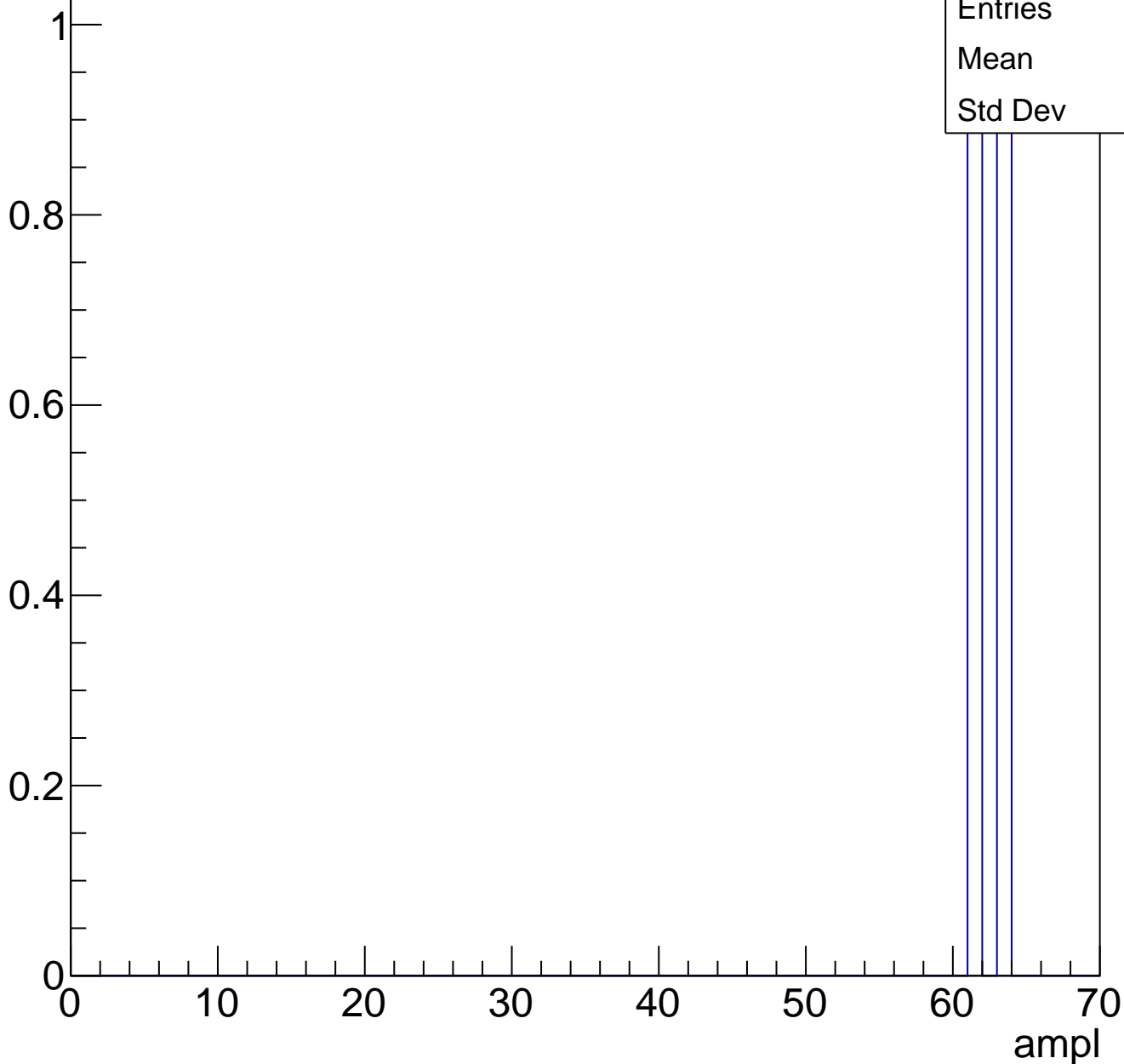
ampl



B0L101S, U19-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



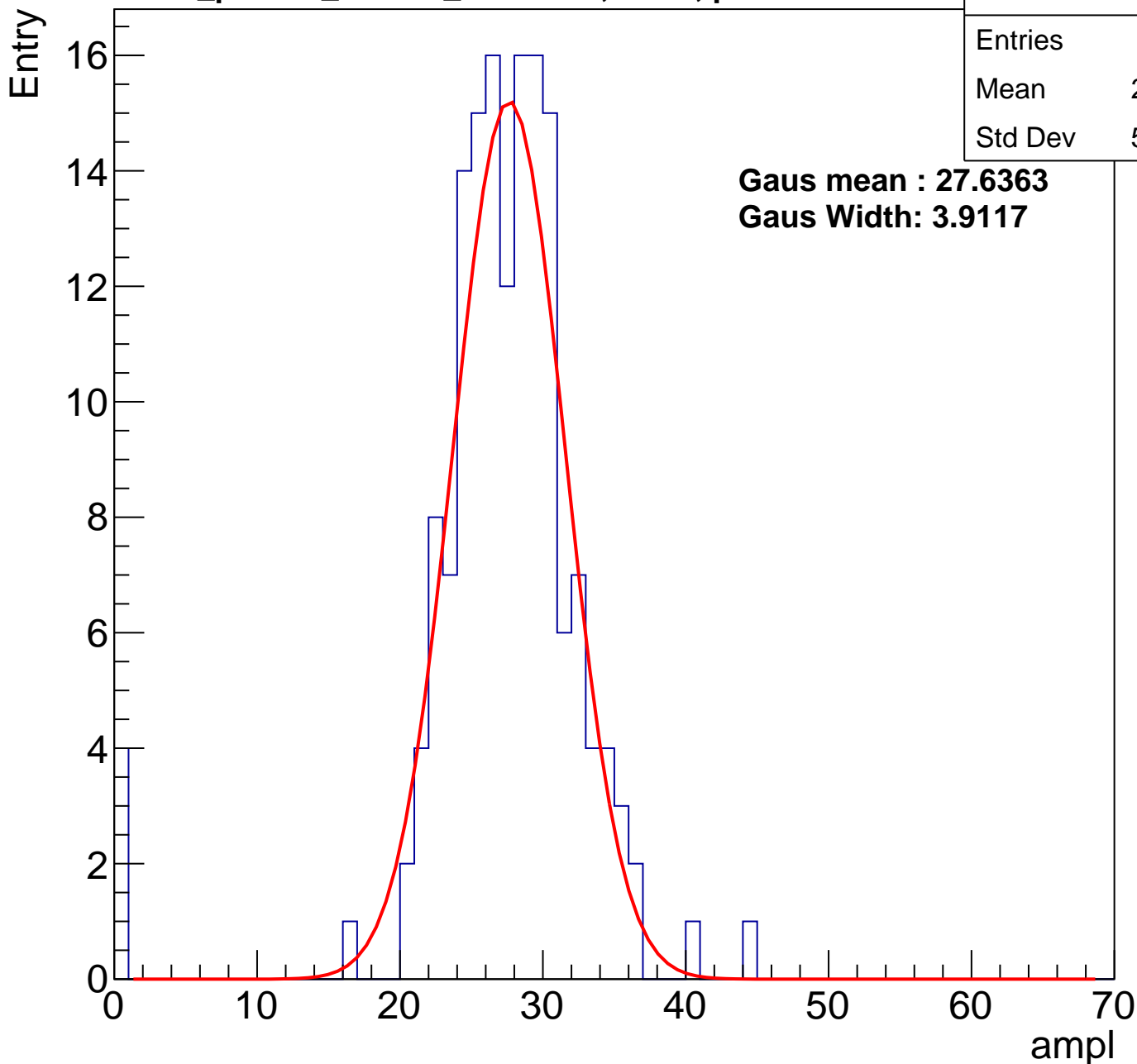
B0L101S, U19-ch78, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	26.77
Std Dev	5.858

Gaus mean : 27.6363

Gaus Width: 3.9117



B0L101S, U19-ch78, adc1

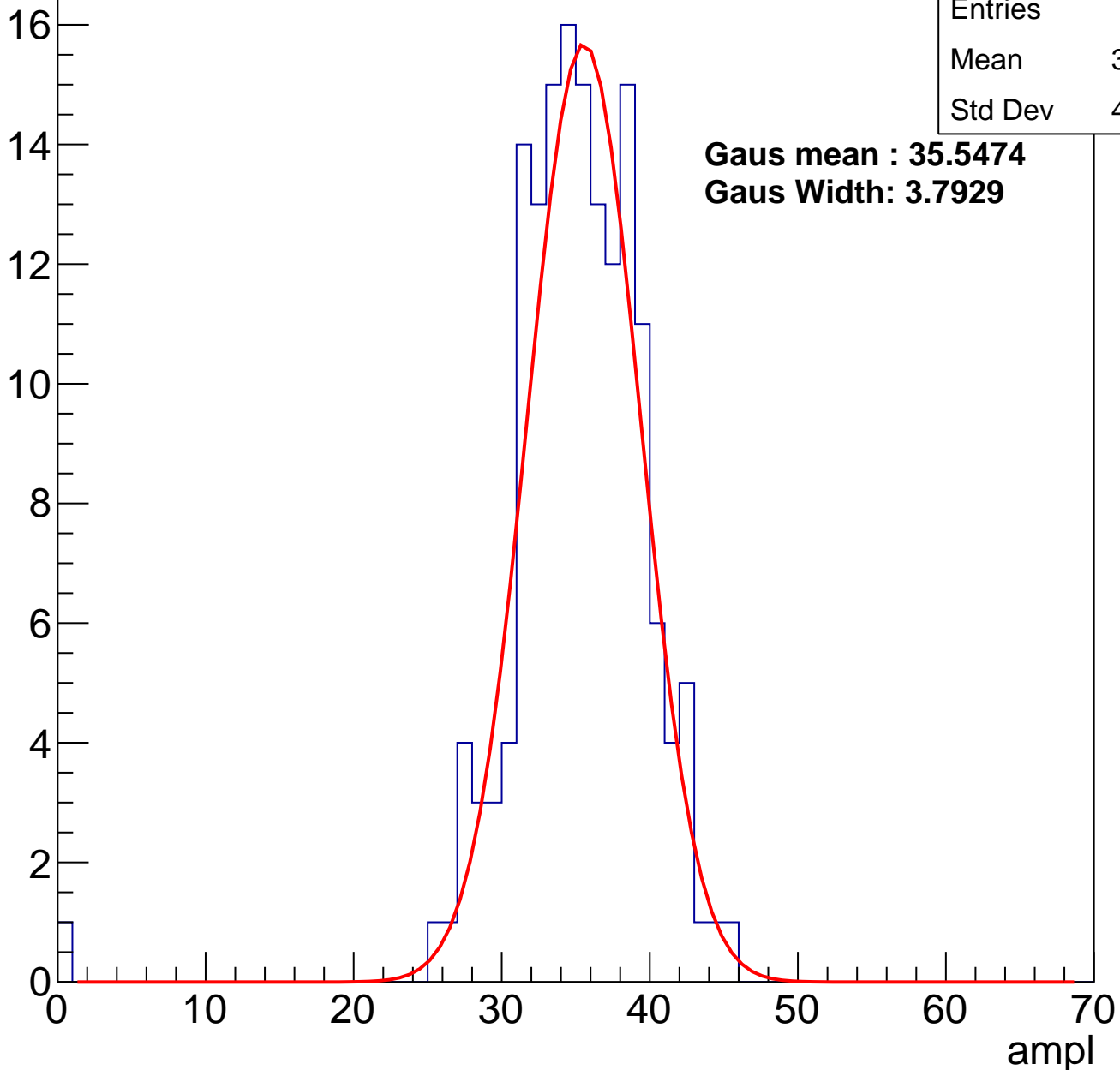
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	34.72
Std Dev	4.744

Gaus mean : 35.5474

Gaus Width: 3.7929

Entry



B0L101S, U19-ch78, adc2

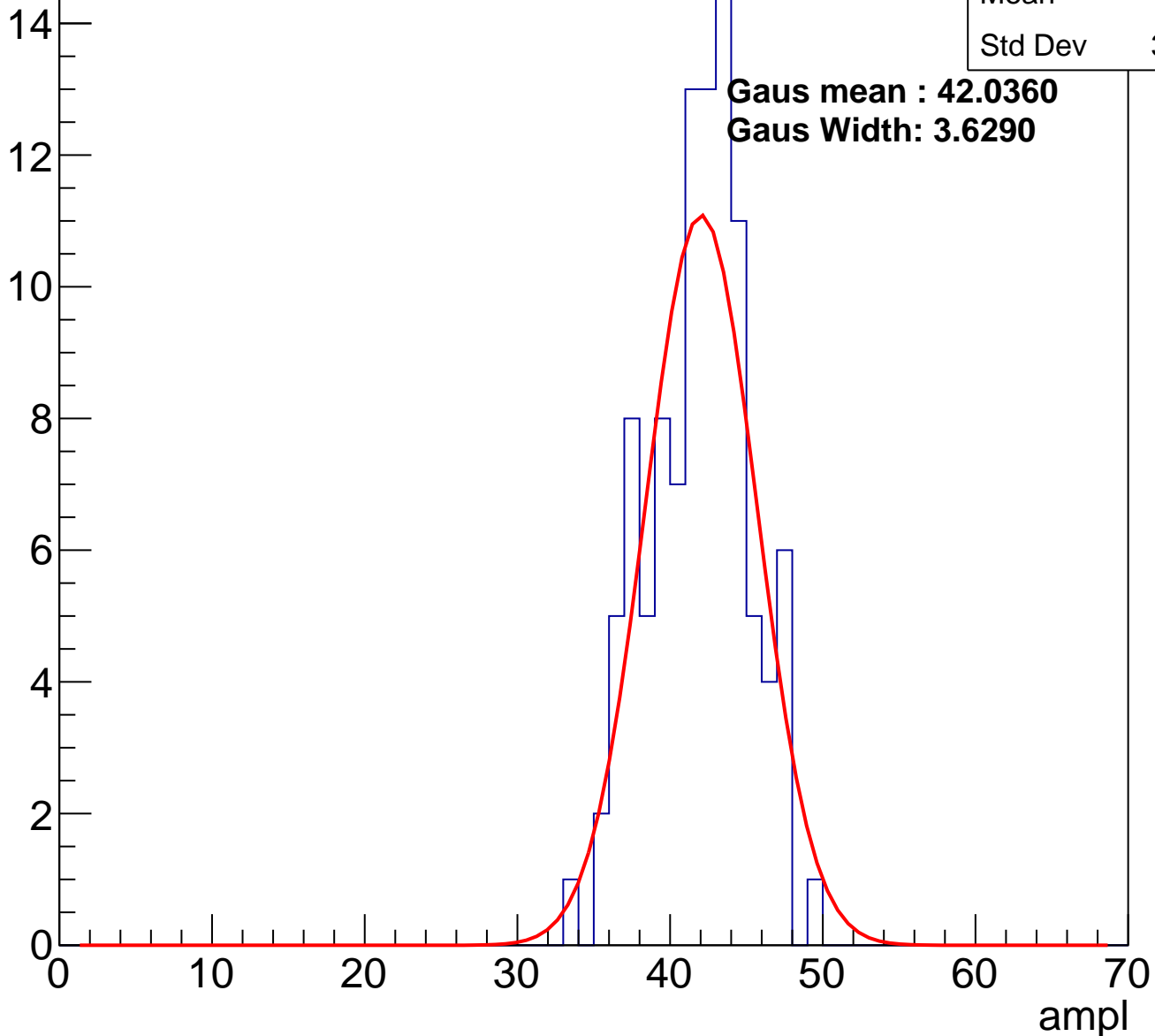
calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	41.43
Std Dev	3.231

Gaus mean : 42.0360

Gaus Width: 3.6290

Entry



B0L101S, U19-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

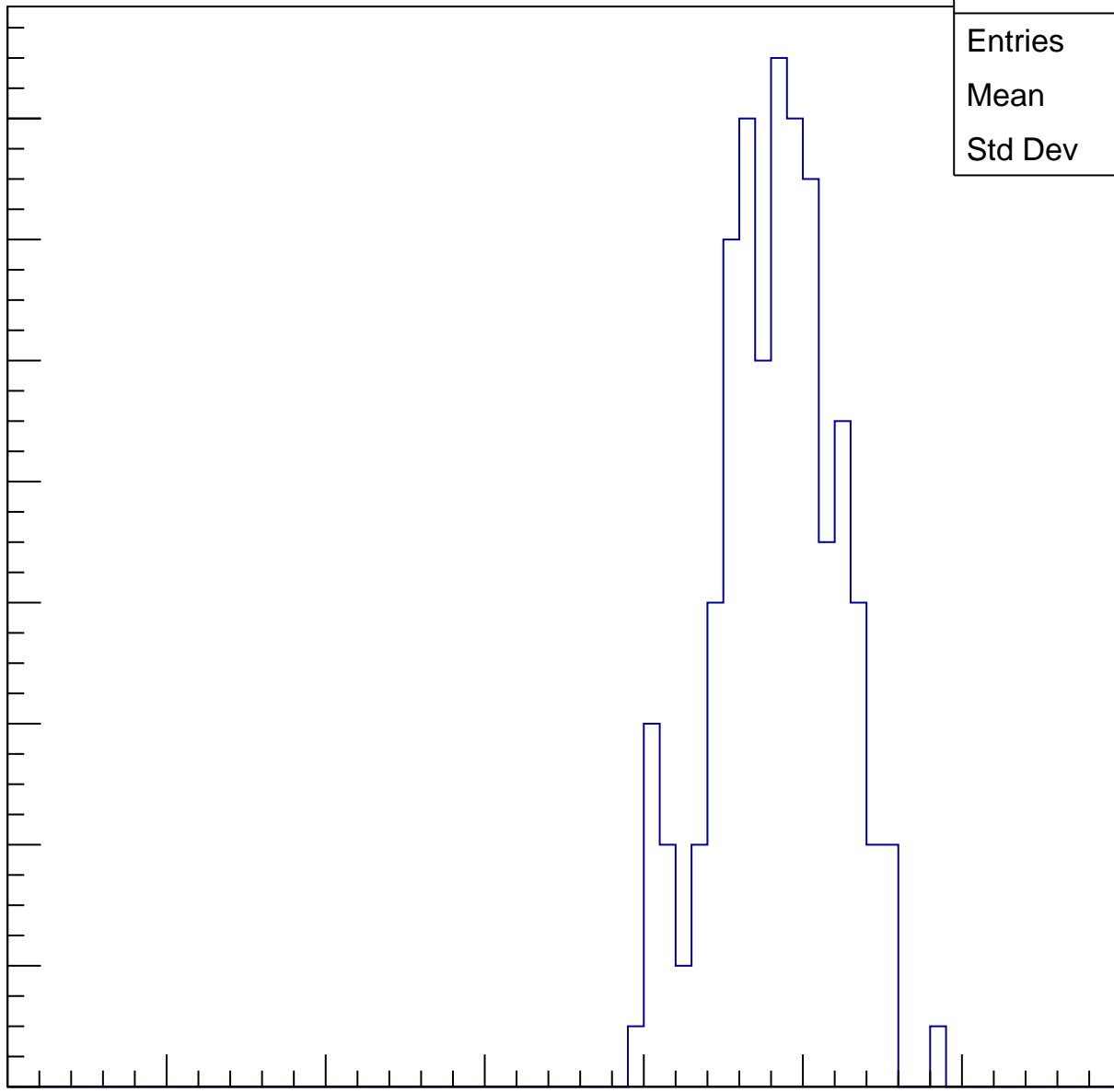
Entries	152
Mean	47.89
Std Dev	3.747

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch78, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	92
Mean	54.08
Std Dev	3.376

Entry

10

8

6

4

2

0

0

10

20

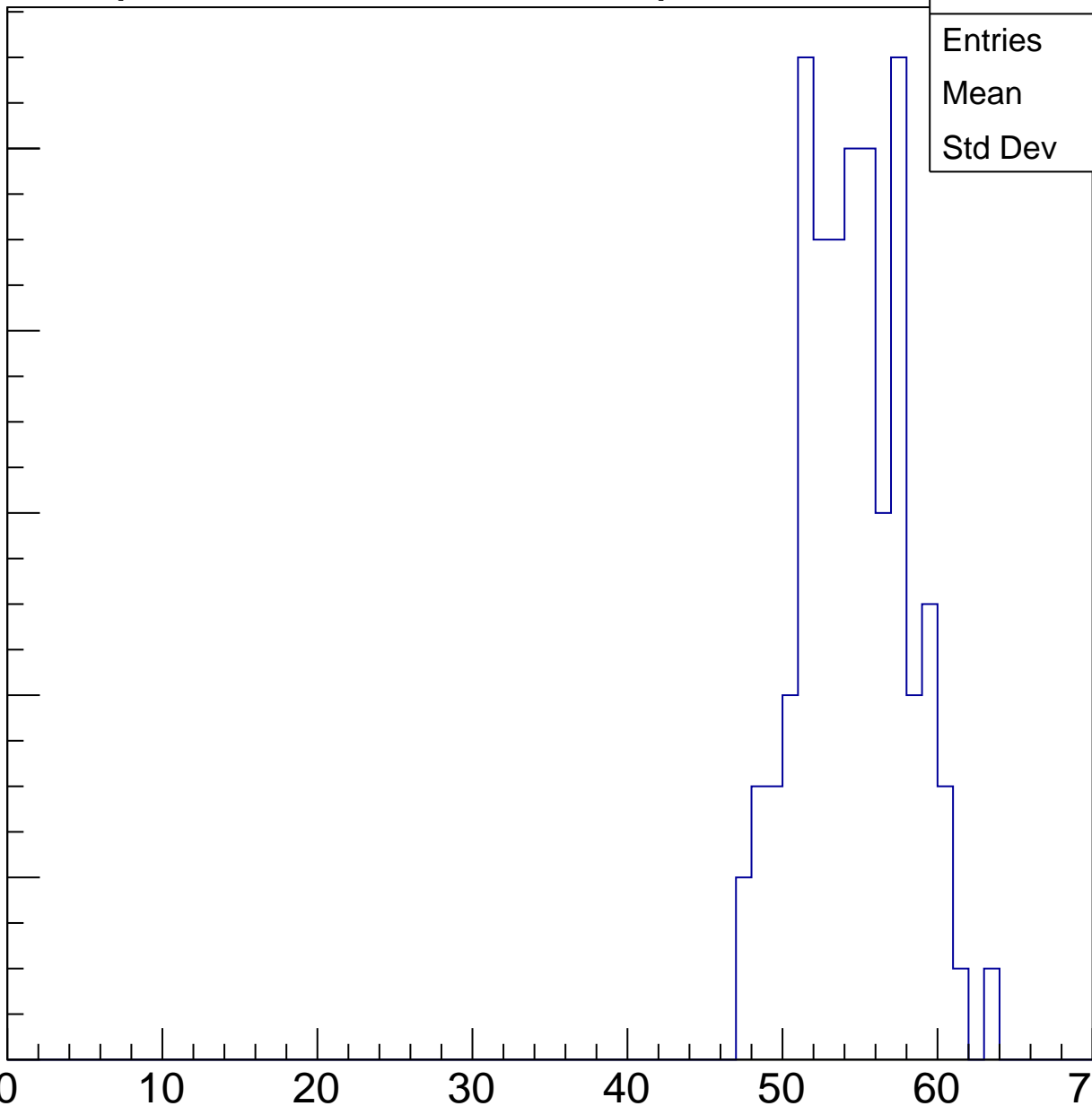
30

40

50

60

ampl



B0L101S, U19-ch78, adc5

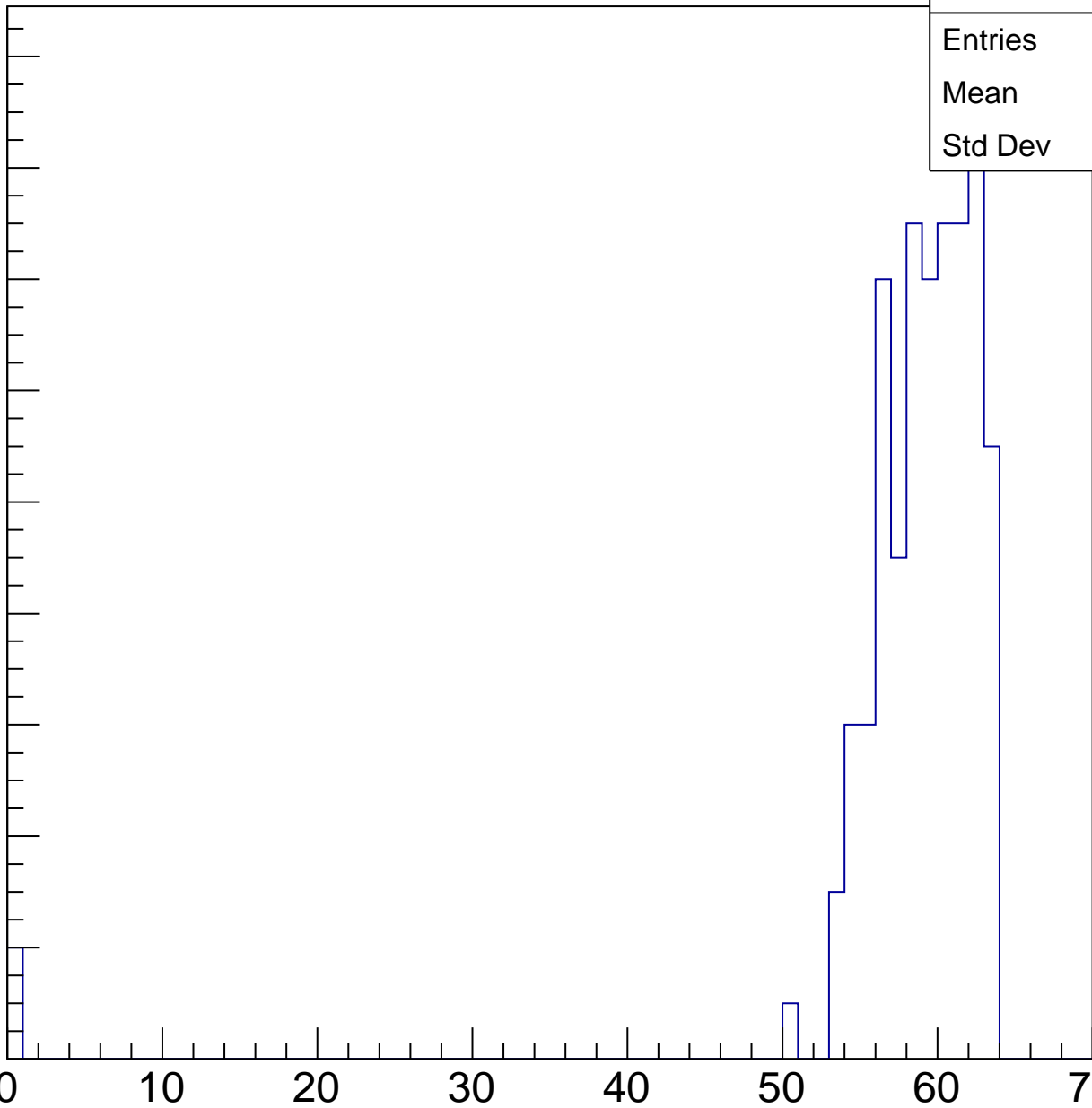
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	129
Mean	57.98
Std Dev	7.803

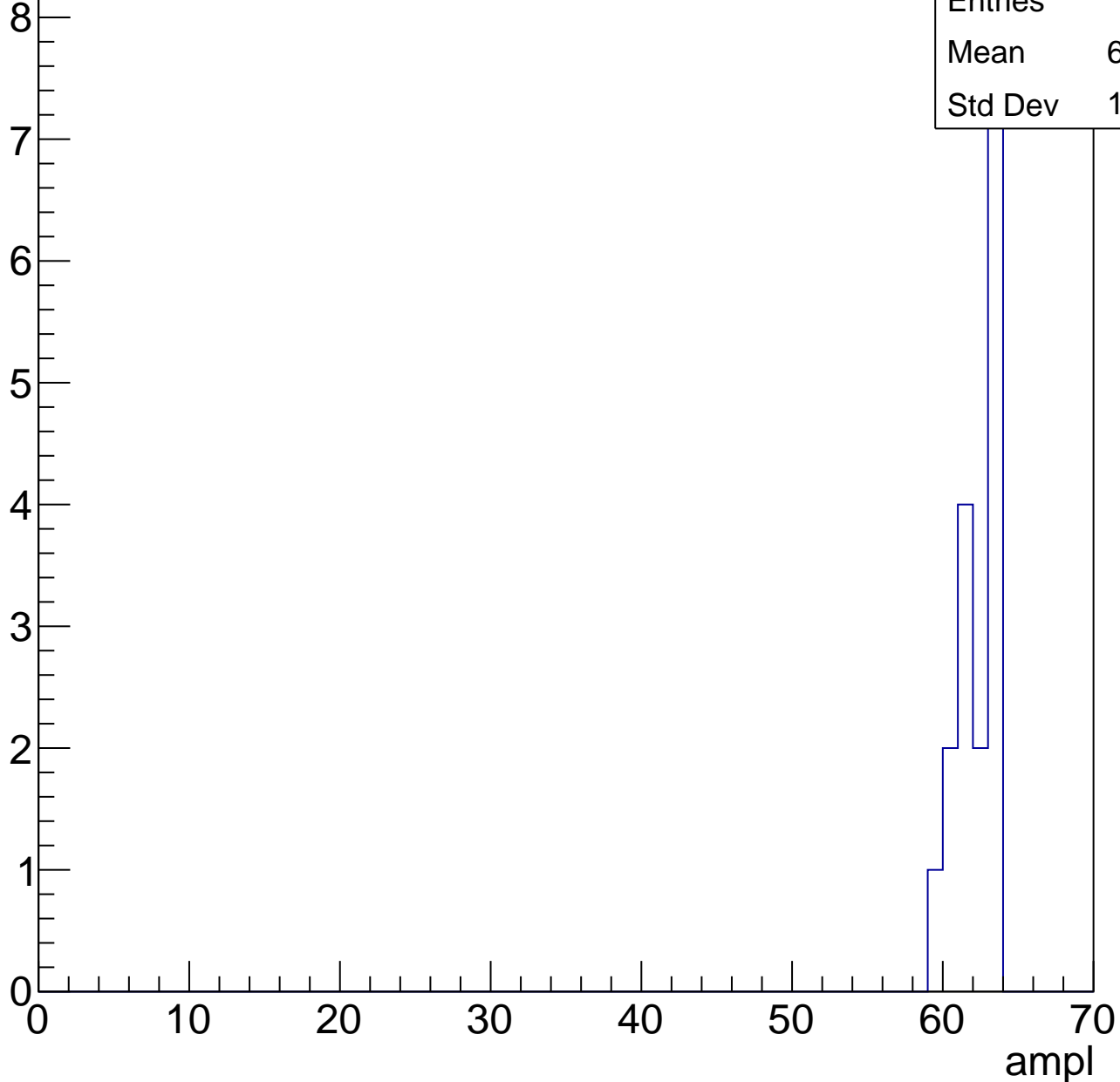
ampl



B0L101S, U19-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	17
Mean	61.82
Std Dev	1.294

B0L101S, U19-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch79, adc0

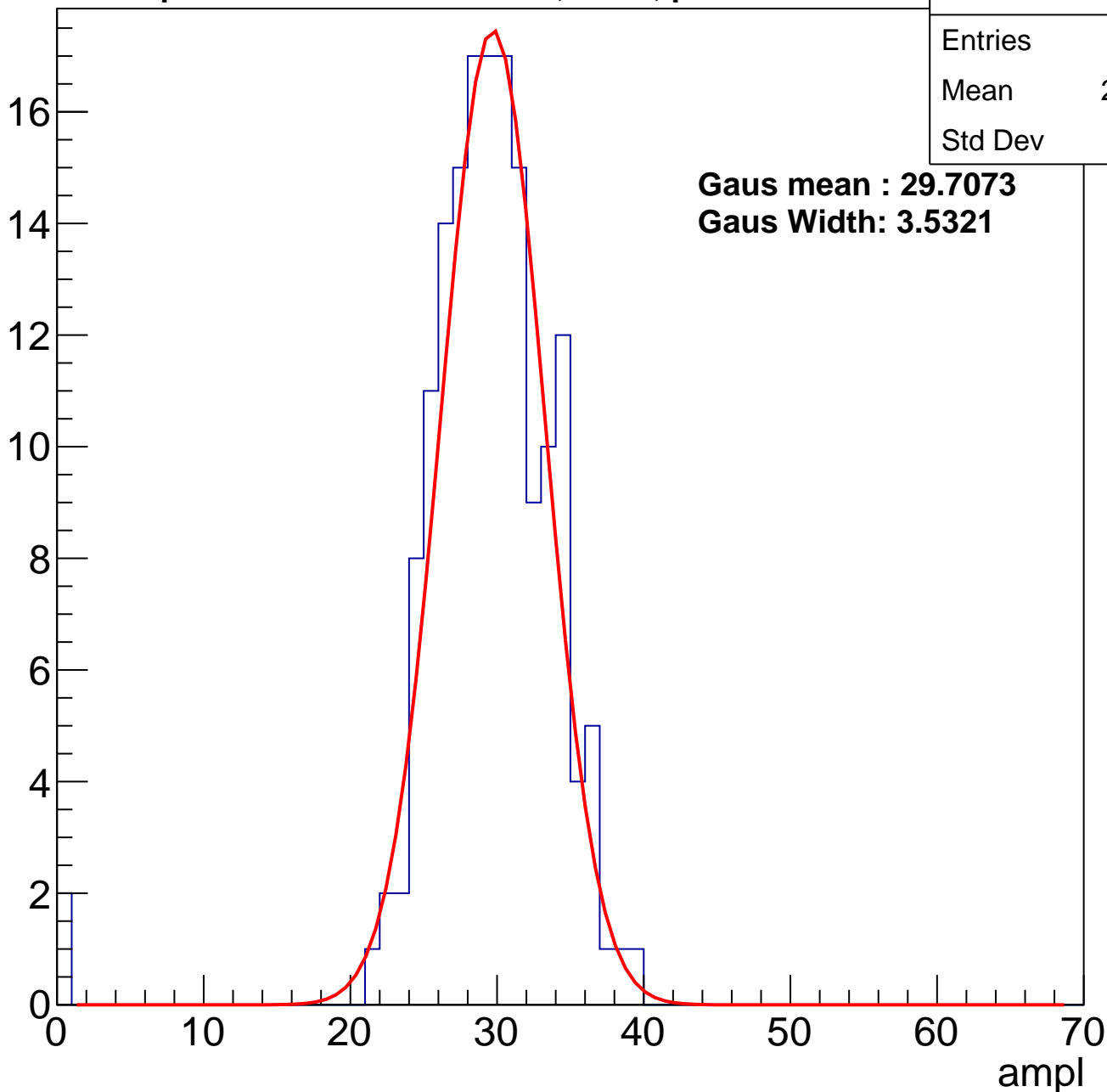
calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	28.97
Std Dev	4.78

Gaus mean : 29.7073

Gaus Width: 3.5321

Entry



B0L101S, U19-ch79, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	37.01
Std Dev	3.554

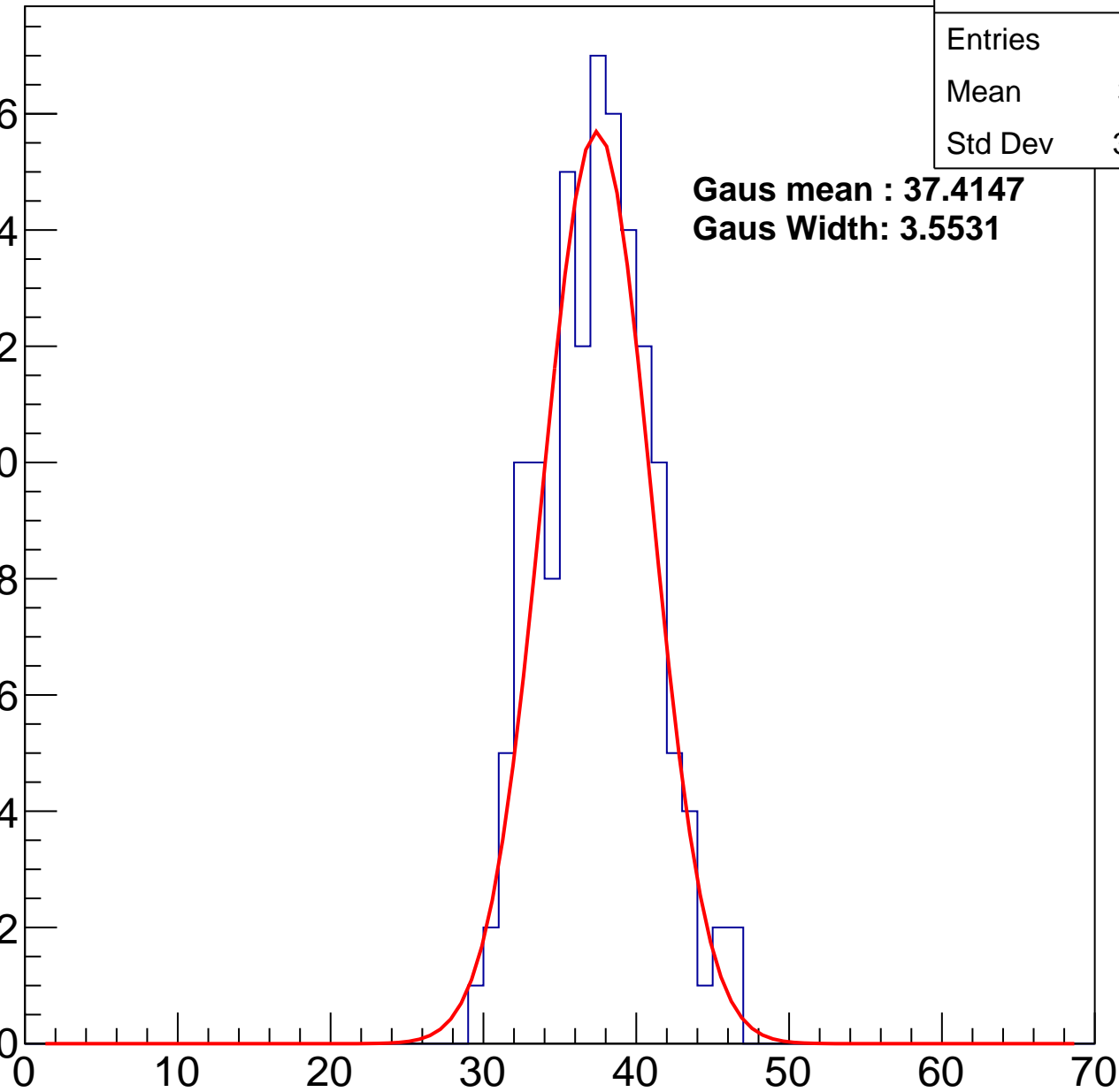
Gaus mean : 37.4147

Gaus Width: 3.5531

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch79, adc2

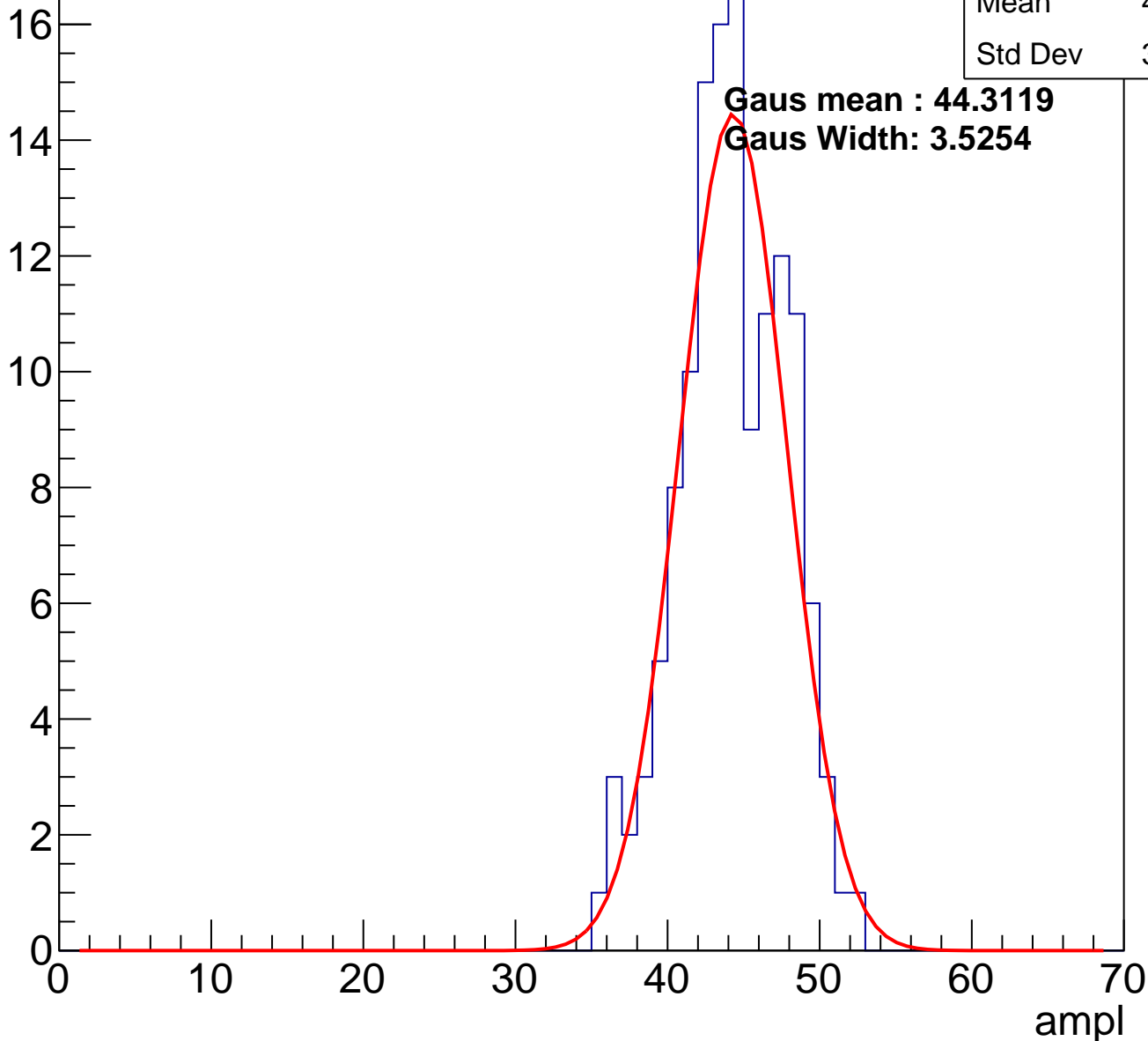
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	134
Mean	43.82
Std Dev	3.457

Gaus mean : 44.3119

Gaus Width: 3.5254

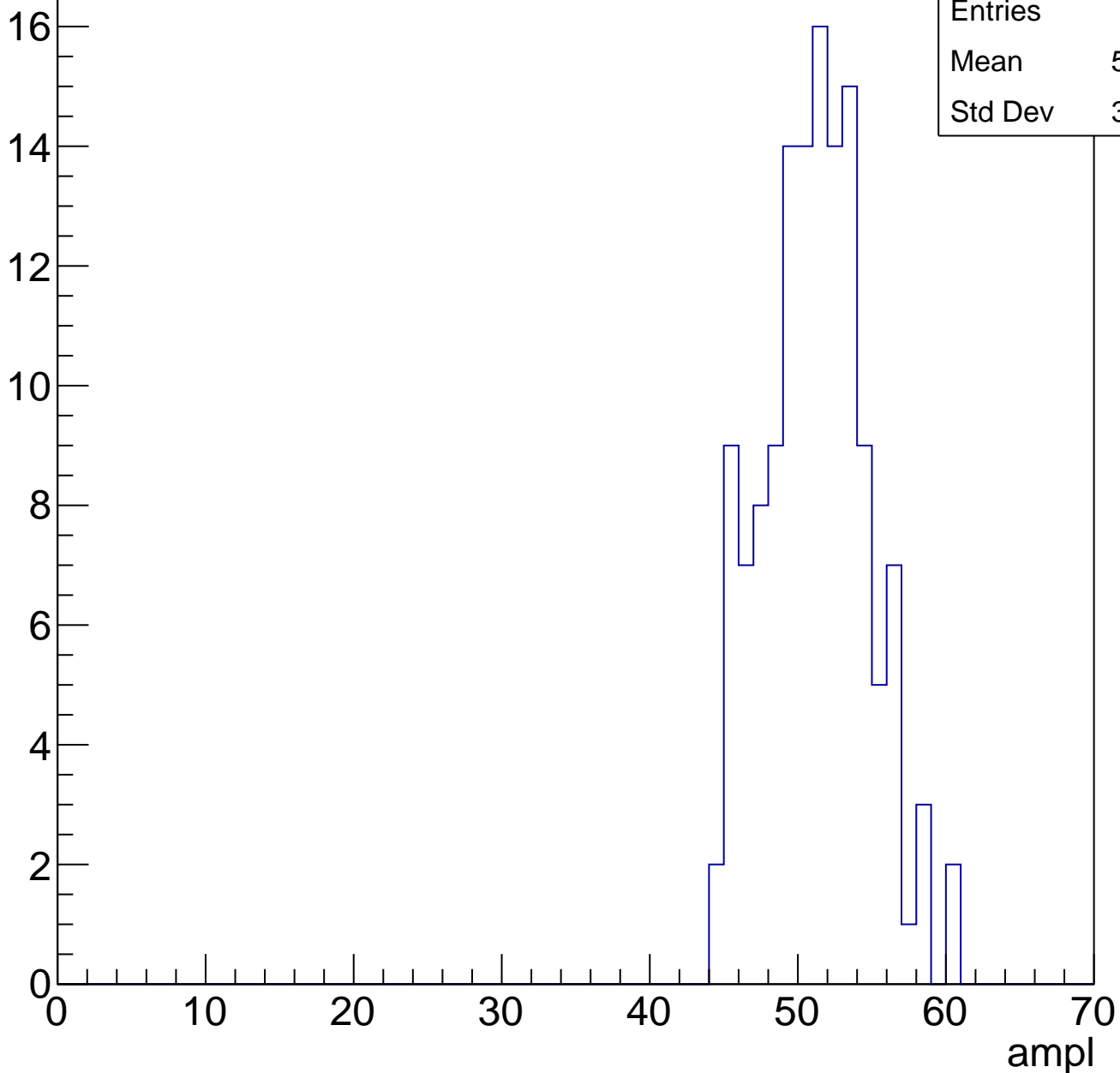


B0L101S, U19-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	50.76
Std Dev	3.467

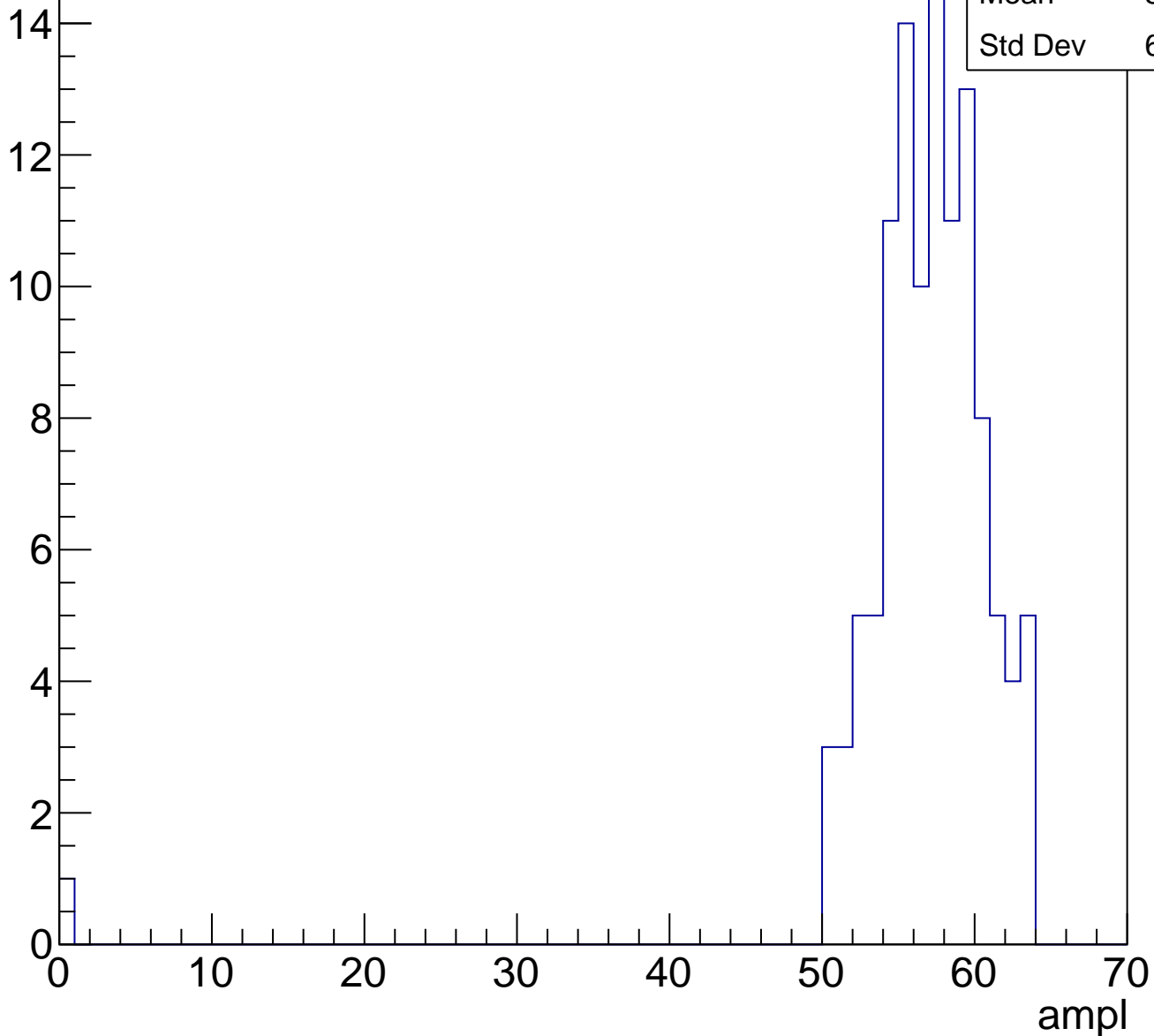
Entry



B0L101S, U19-ch79, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch79, adc5

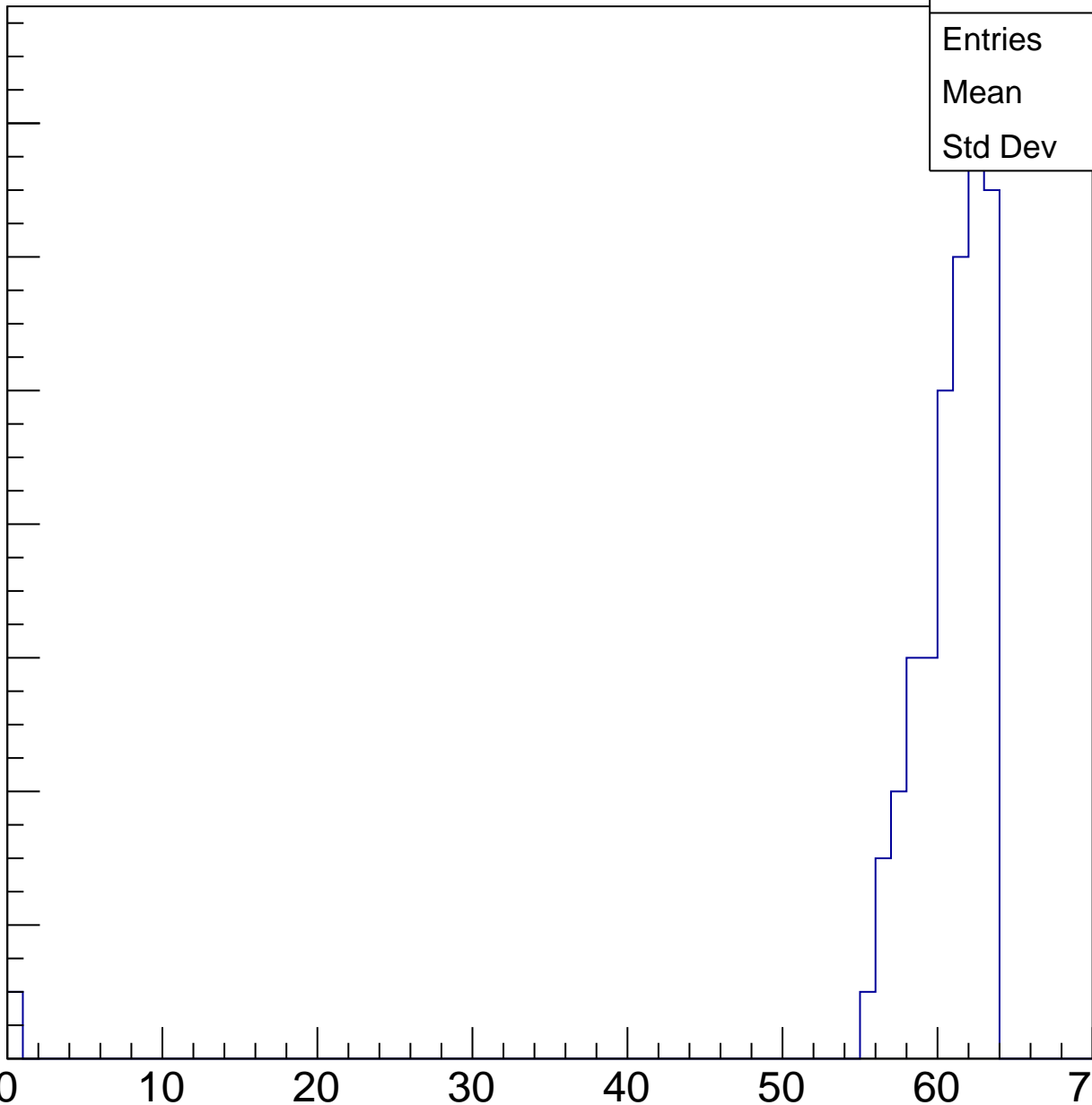
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	59.63
Std Dev	7.429

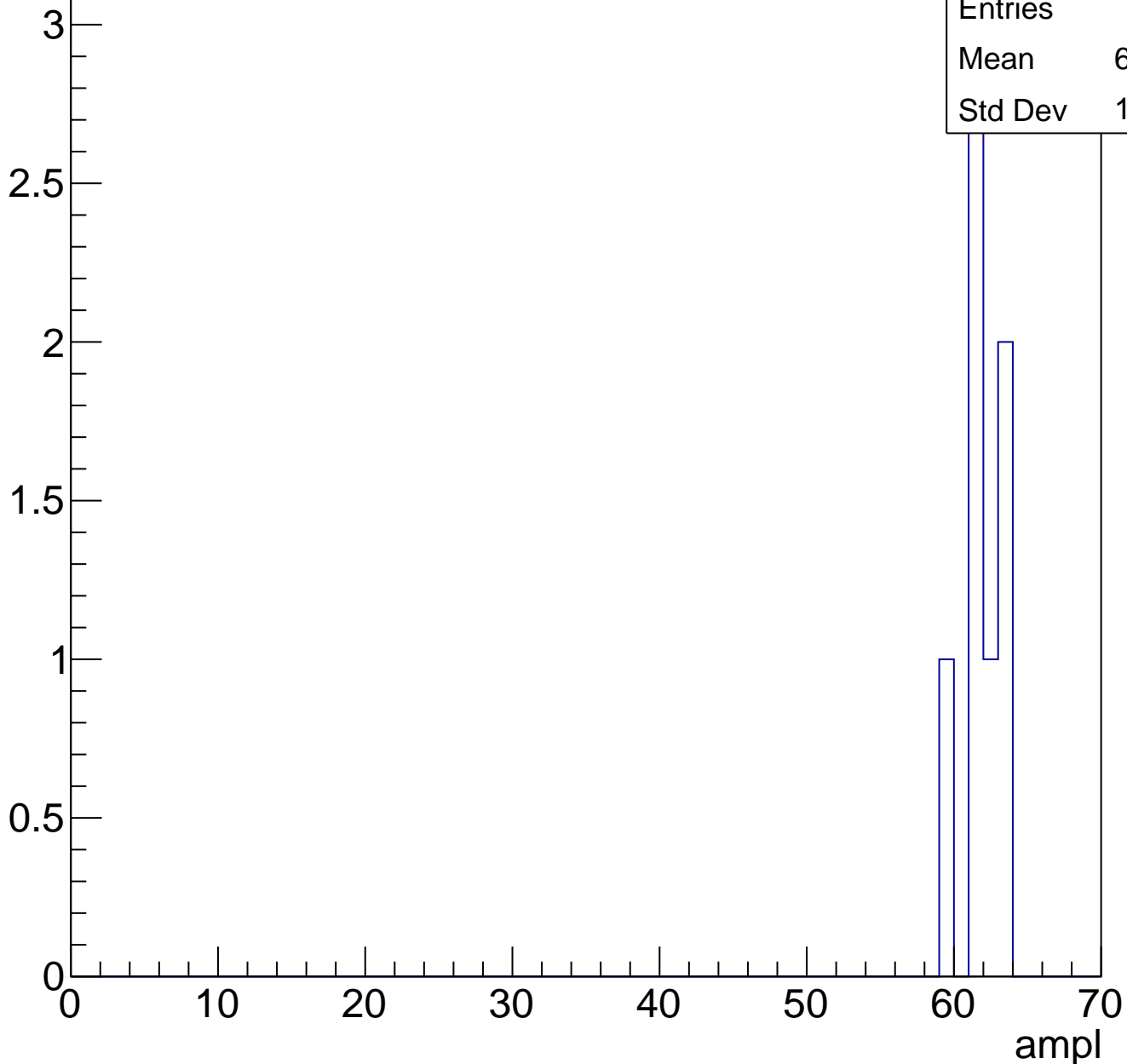
ampl



B0L101S, U19-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

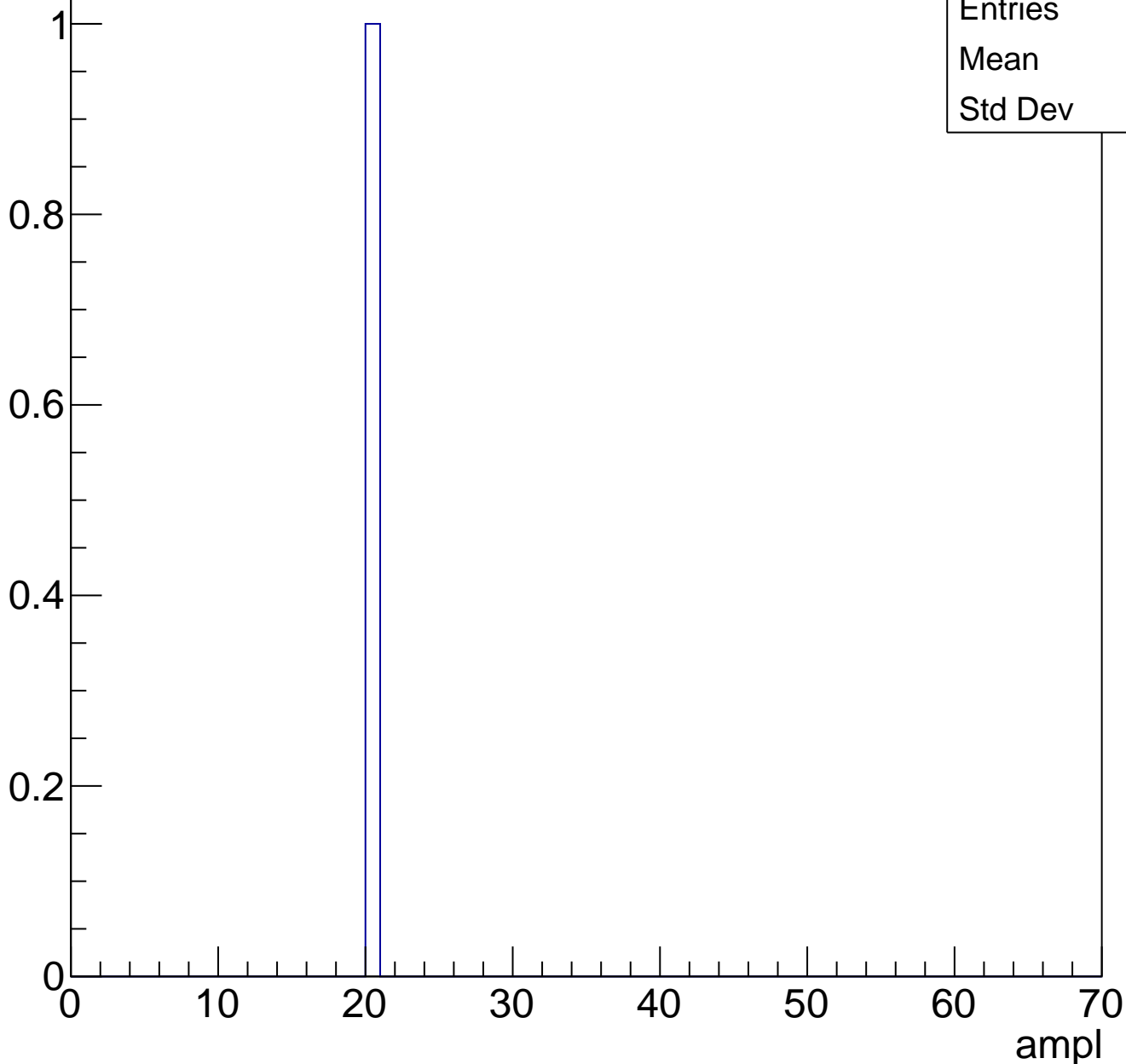
Entry



B0L101S, U19-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	20
Std Dev	0

B0L101S, U19-ch80, adc0

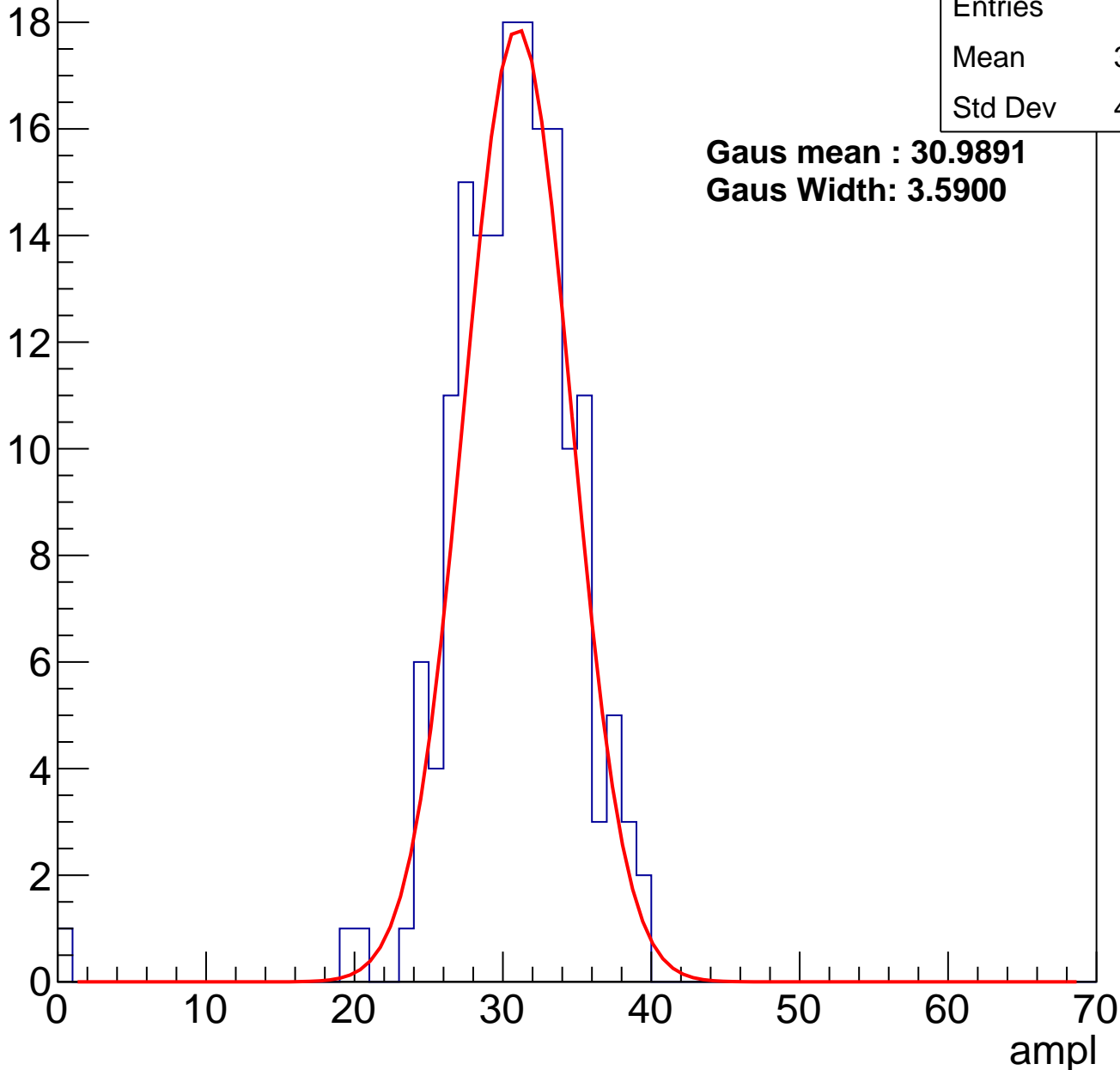
calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	30.25
Std Dev	4.355

Gaus mean : 30.9891

Gaus Width: 3.5900

Entry



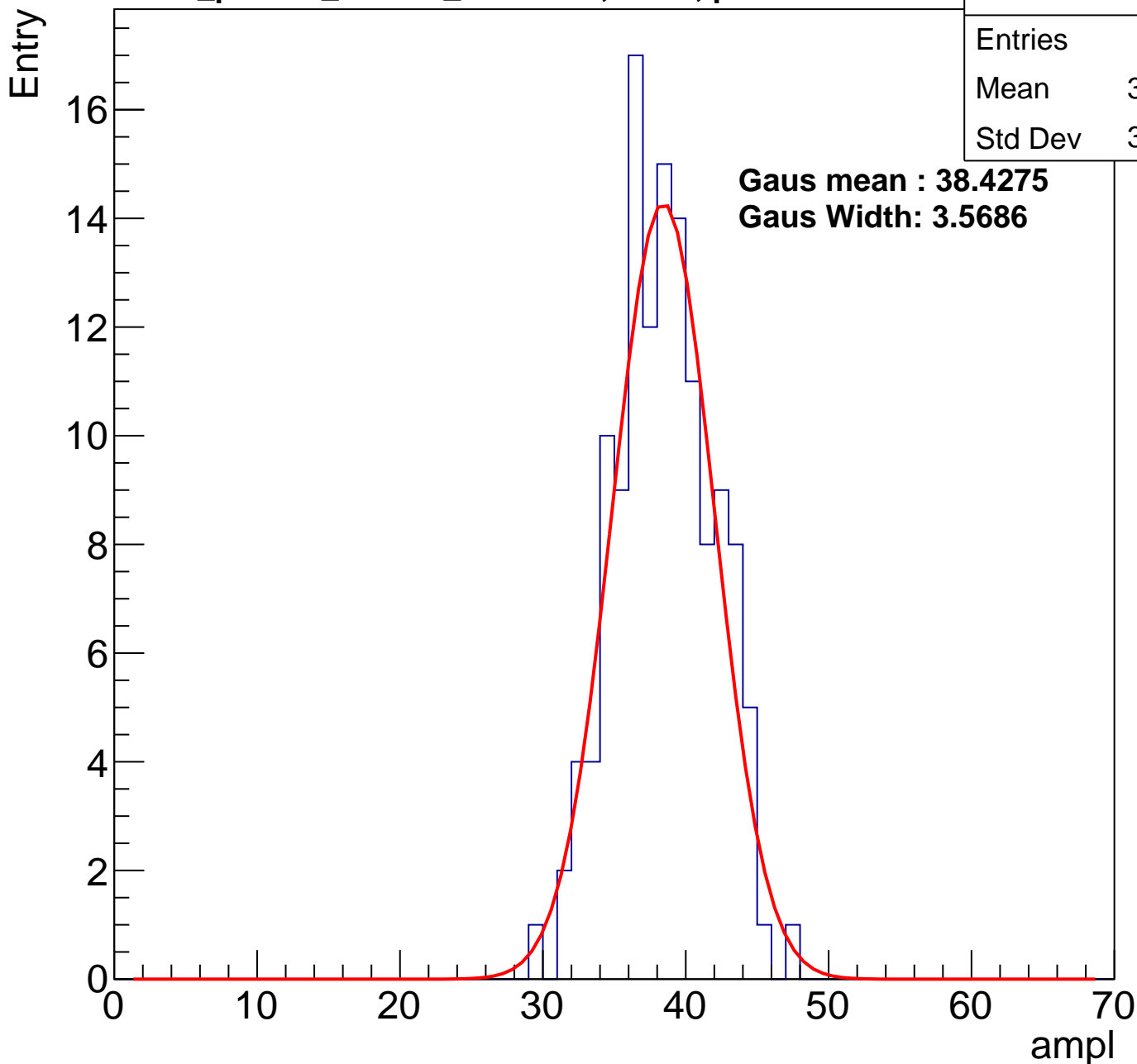
B0L101S, U19-ch80, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	38.02
Std Dev	3.415

Gaus mean : 38.4275

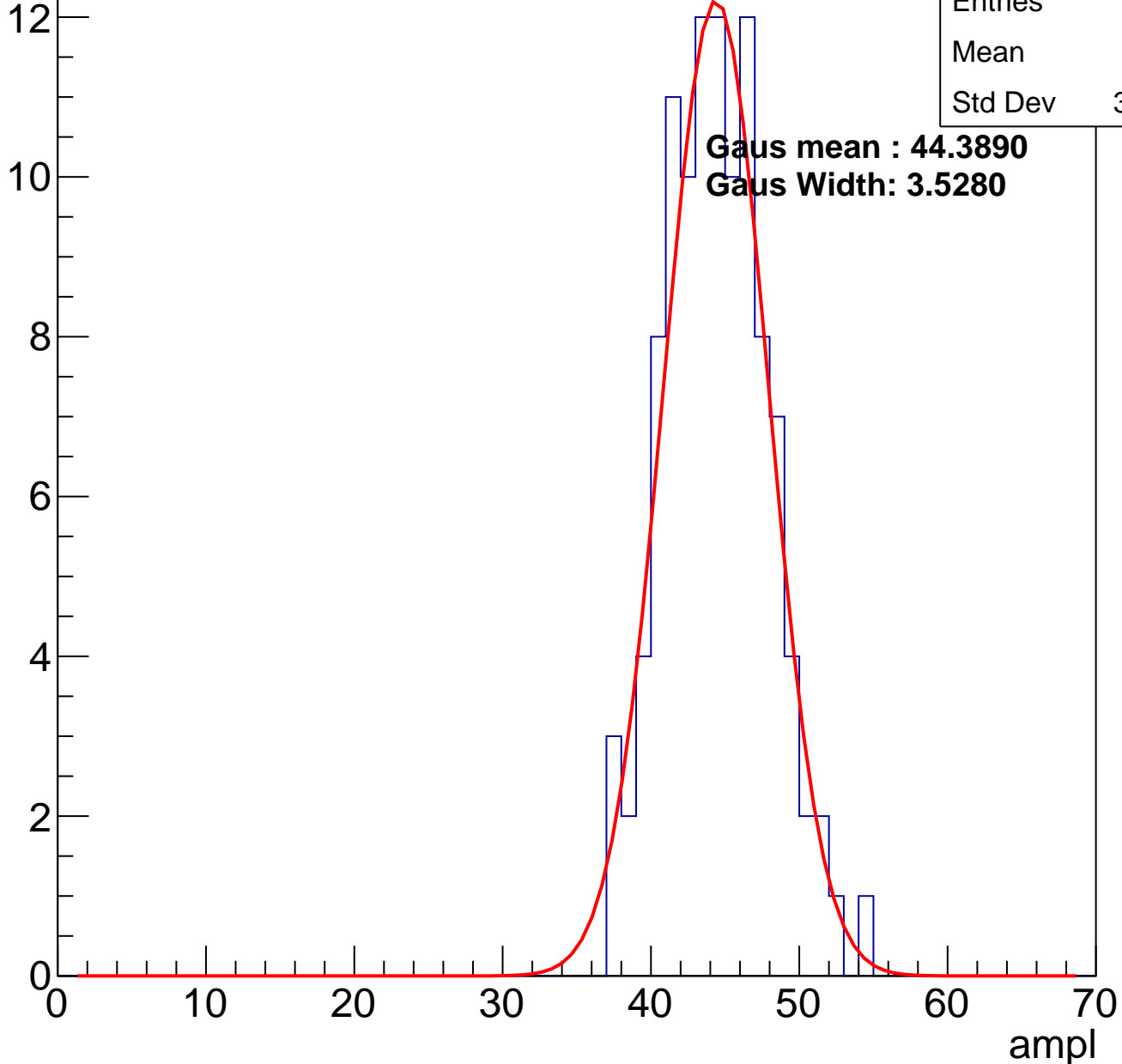
Gaus Width: 3.5686



B0L101S, U19-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

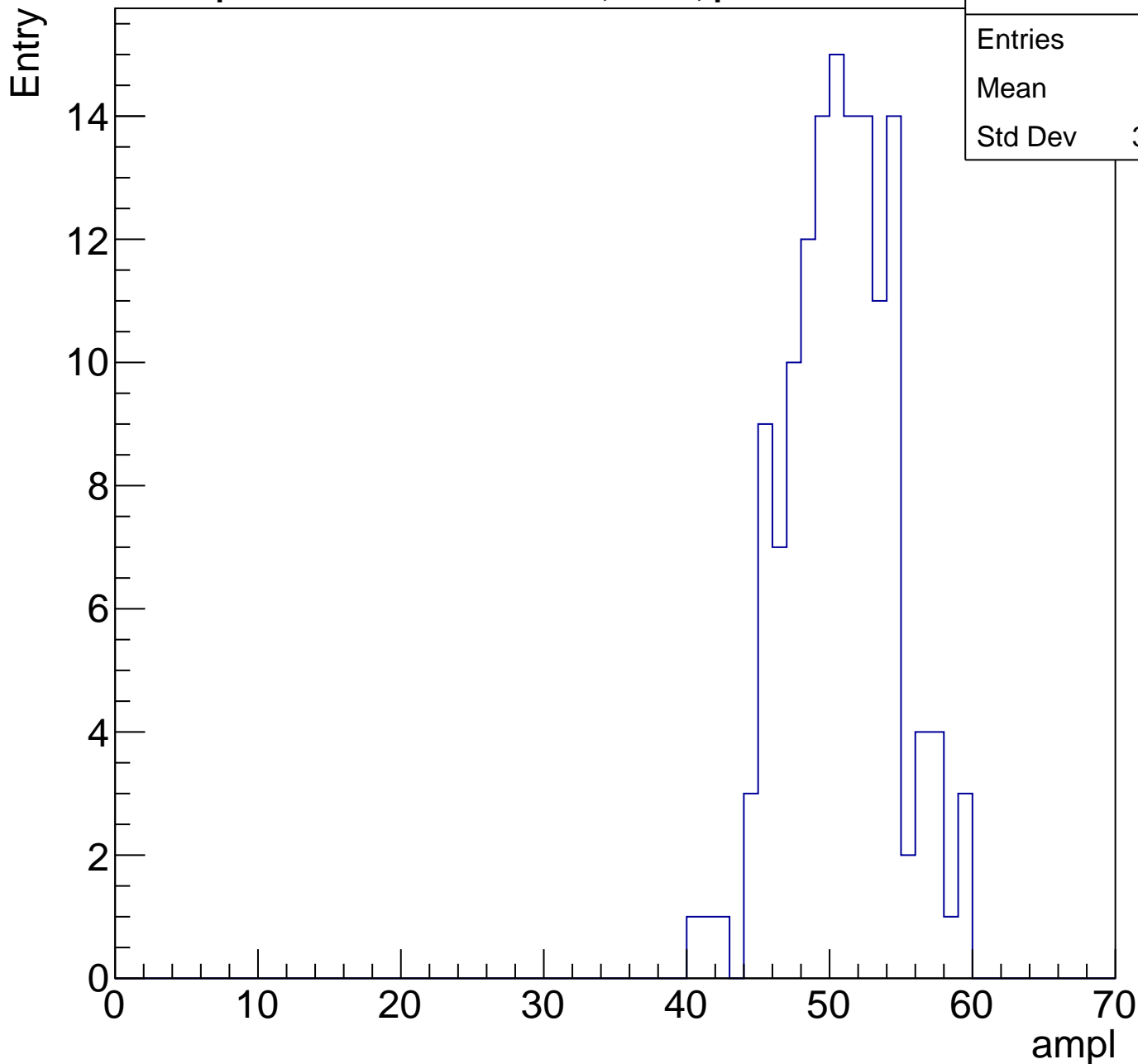
Entry



B0L101S, U19-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	50.3
Std Dev	3.703

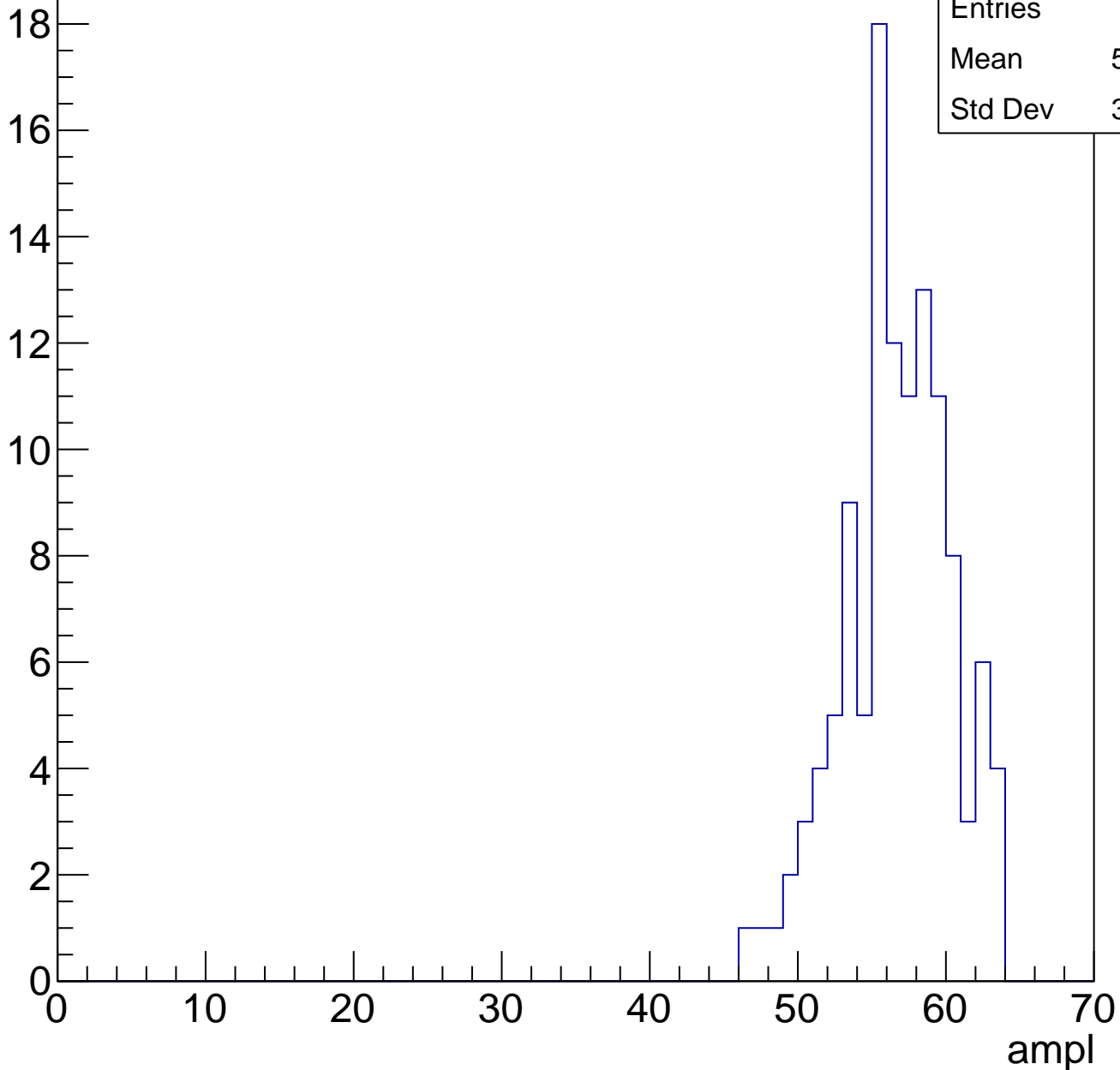


B0L101S, U19-ch80, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	56.23
Std Dev	3.605

Entry



B0L101S, U19-ch80, adc5

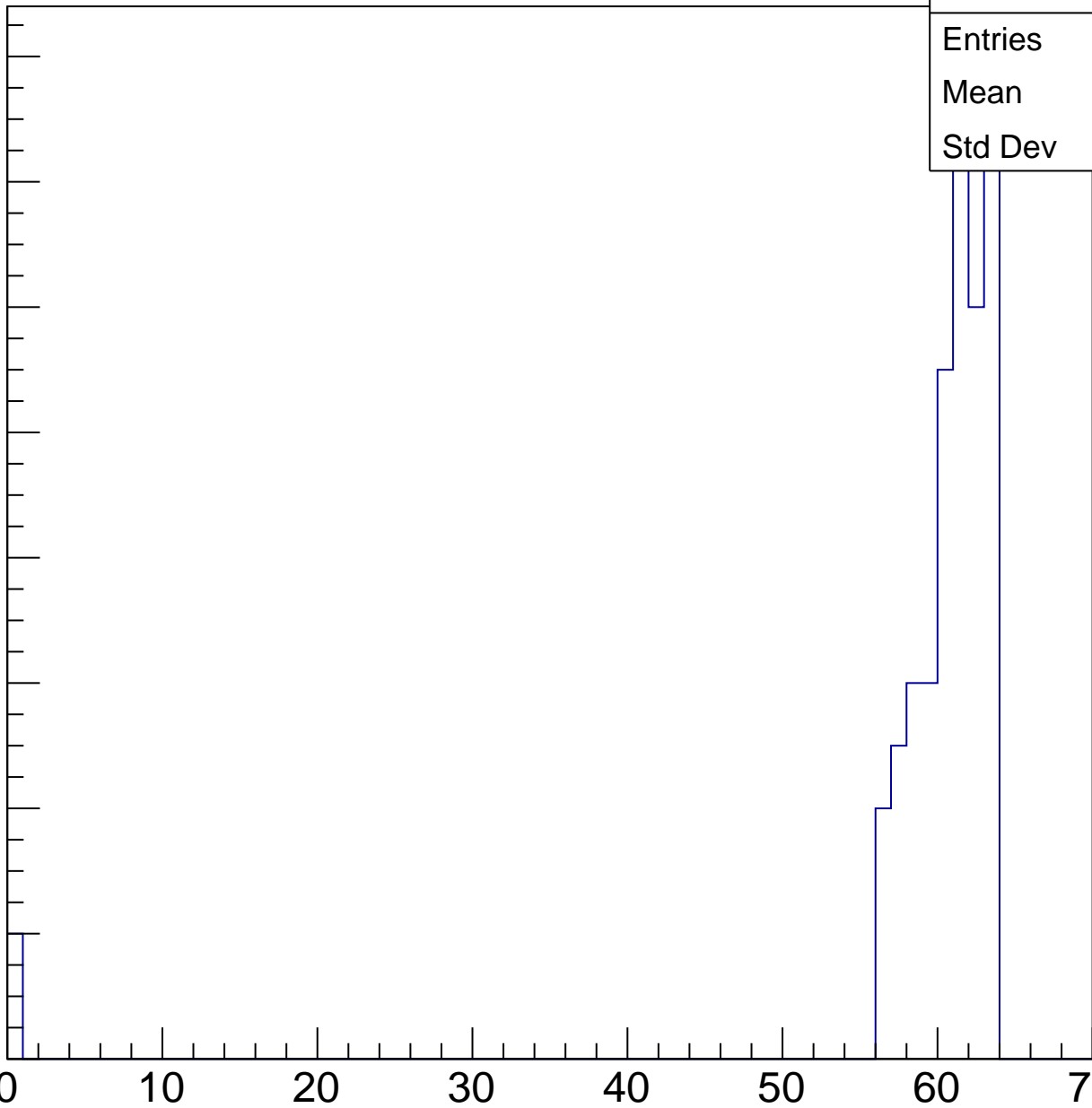
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	58.91
Std Dev	9.834

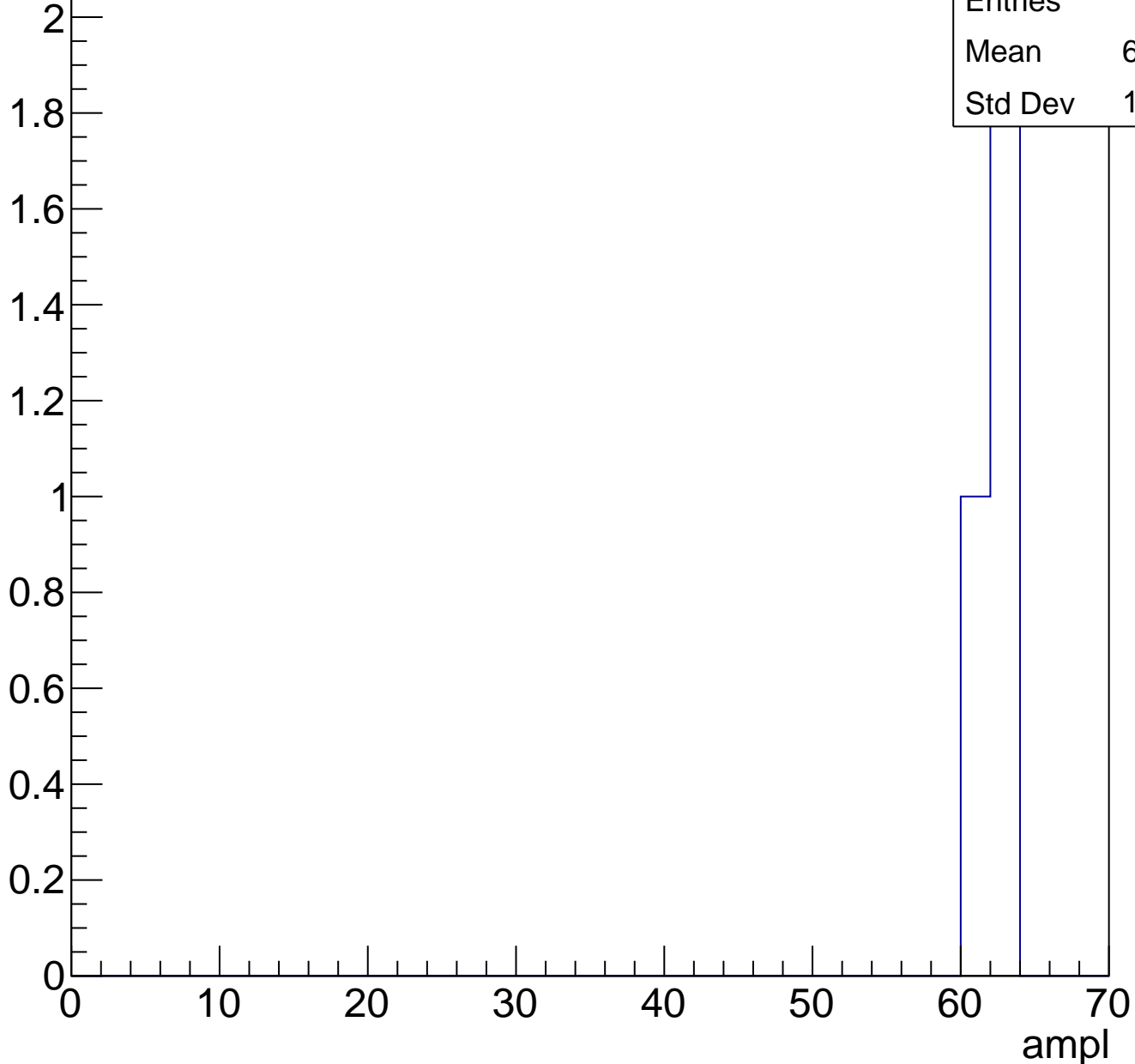
ampl



B0L101S, U19-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

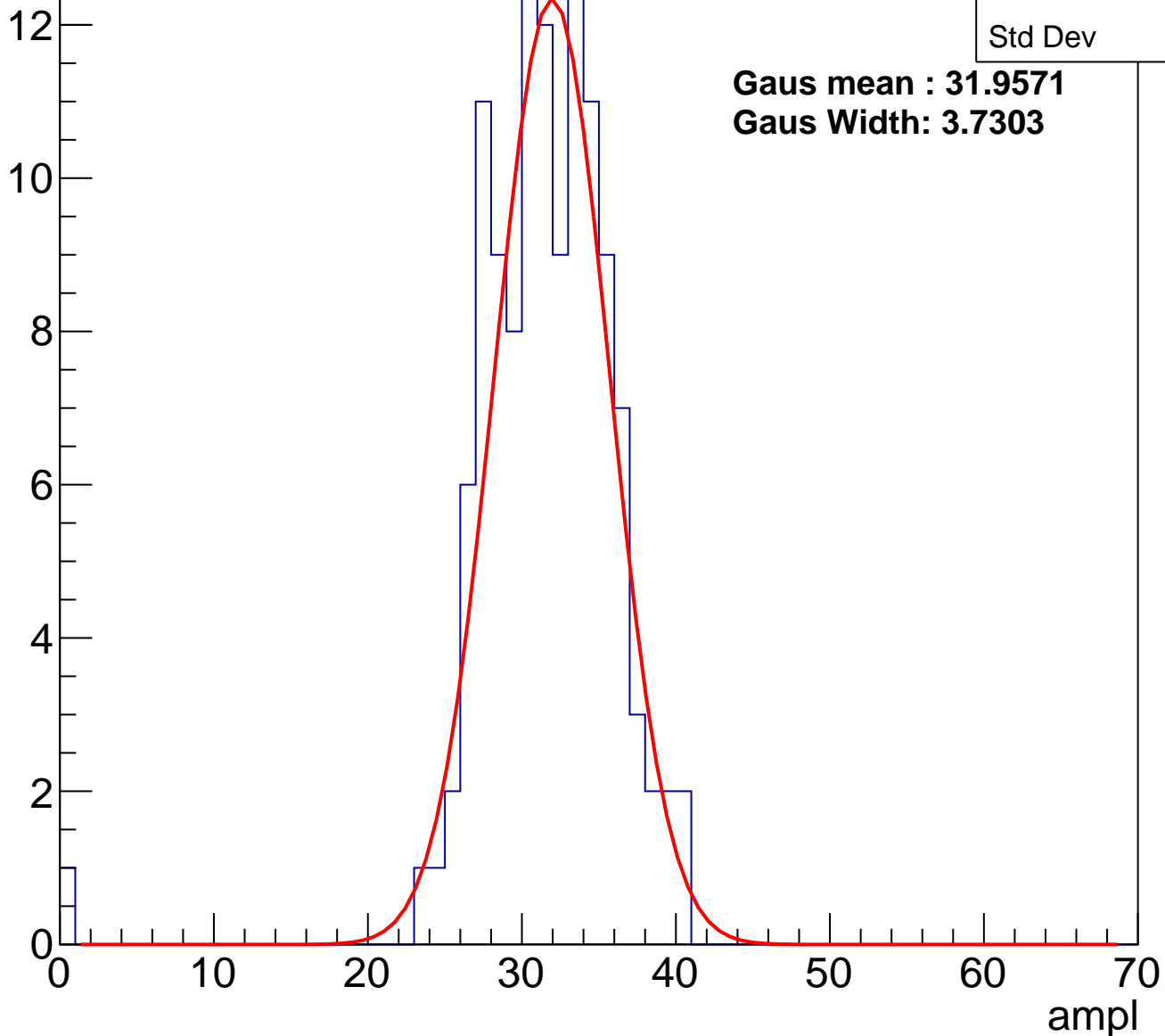
B0L101S, U19-ch81, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	31.14
Std Dev	4.59

Gaus mean : 31.9571
Gaus Width: 3.7303

Entry



B0L101S, U19-ch81, adc1

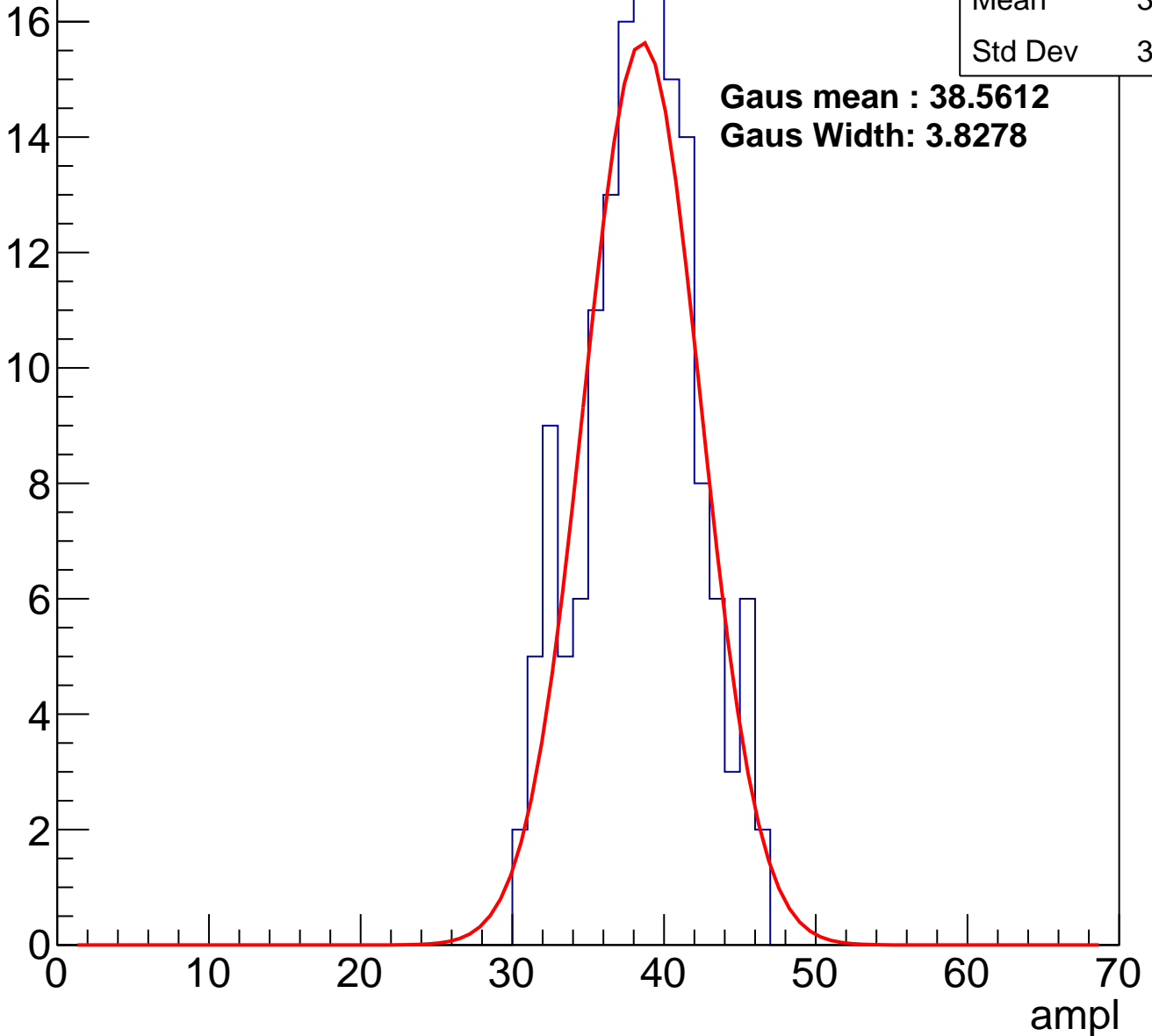
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	155
Mean	37.99
Std Dev	3.677

Gaus mean : 38.5612

Gaus Width: 3.8278



B0L101S, U19-ch81, adc2

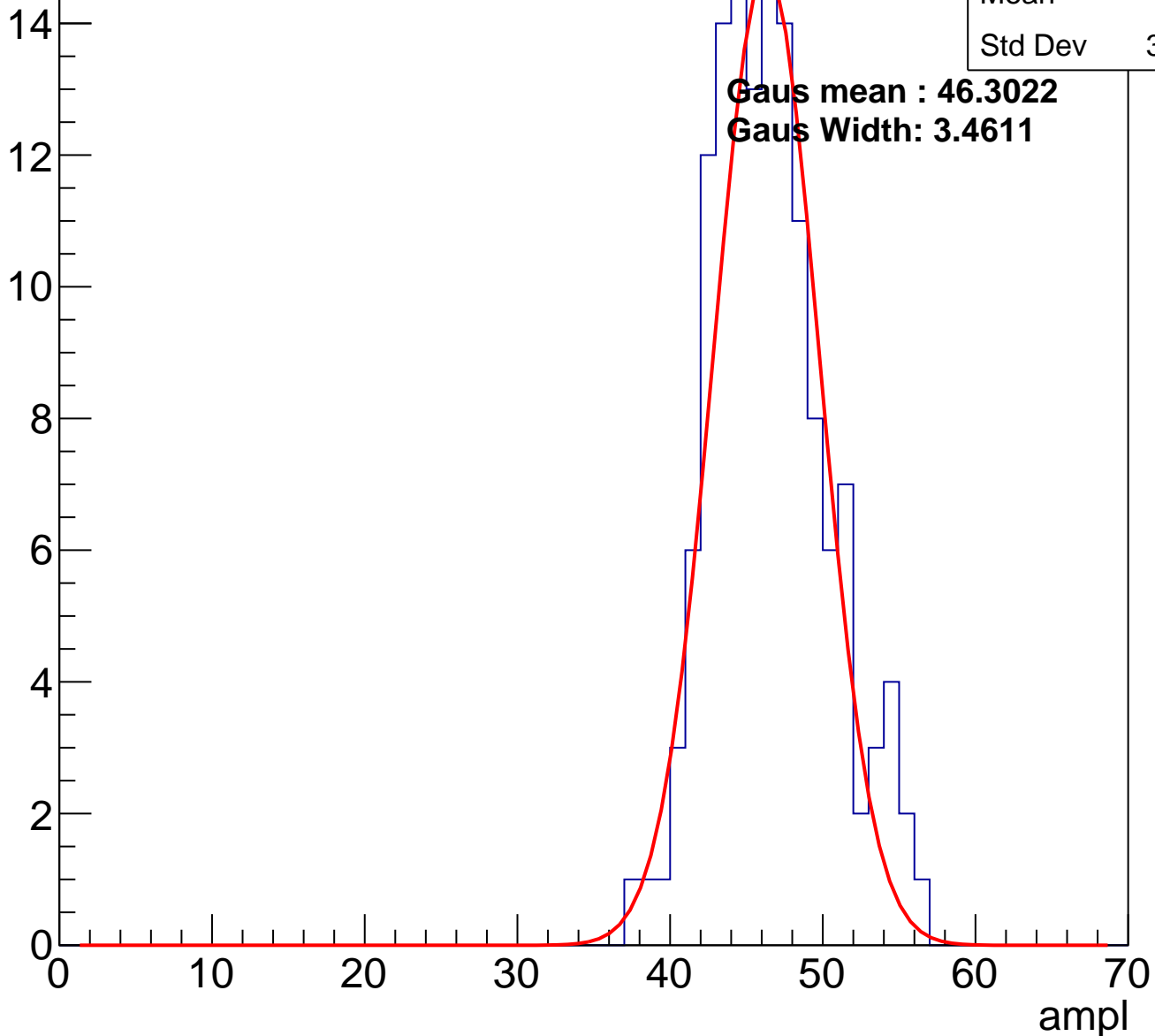
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	46.05
Std Dev	3.775

Gaus mean : 46.3022

Gaus Width: 3.4611

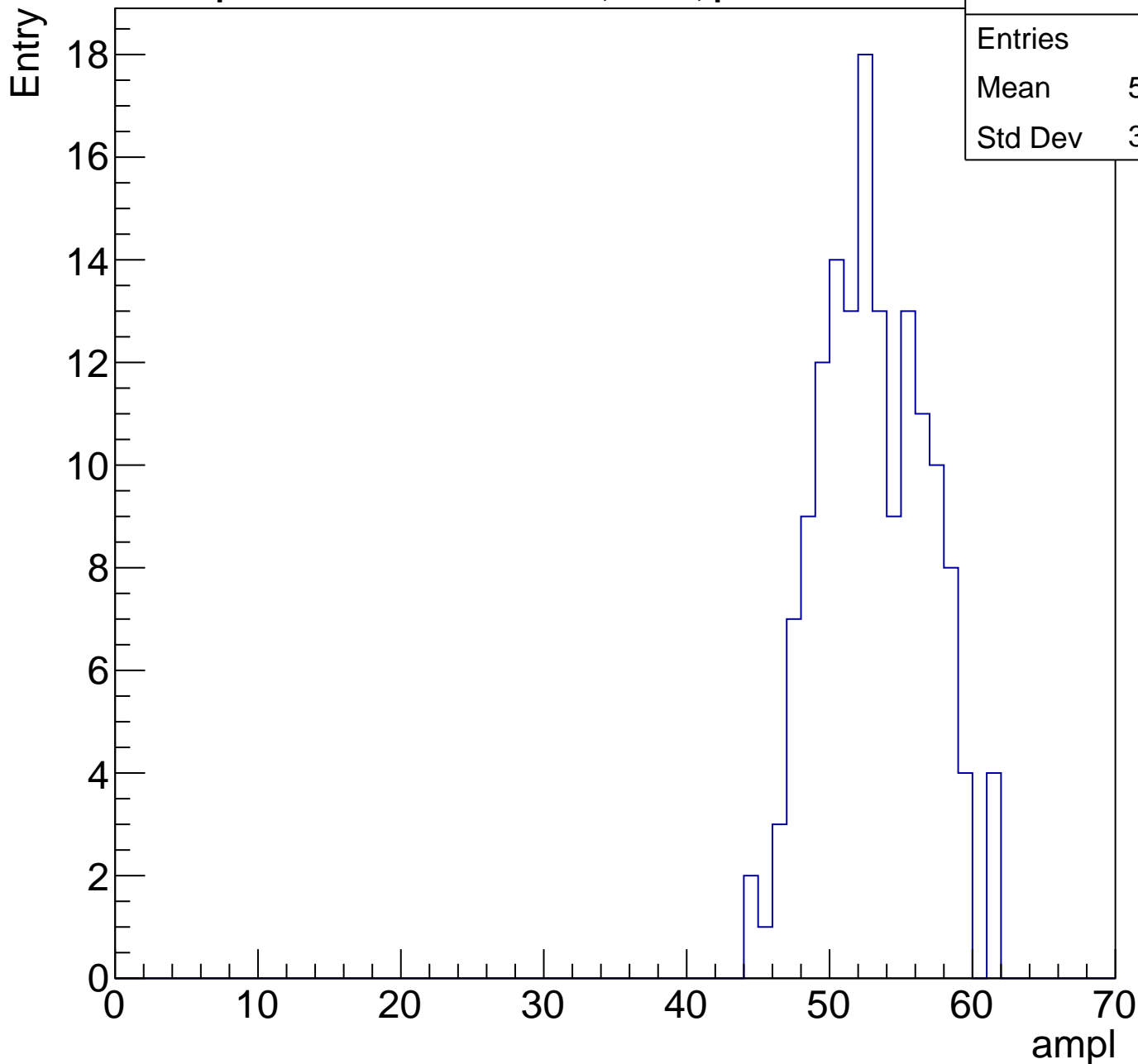
Entry



B0L101S, U19-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	52.58
Std Dev	3.757



B0L101S, U19-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	66
Mean	57.85
Std Dev	2.93

ampl

0 10 20 30 40 50 60 70

B0L101S, U19-ch81, adc5

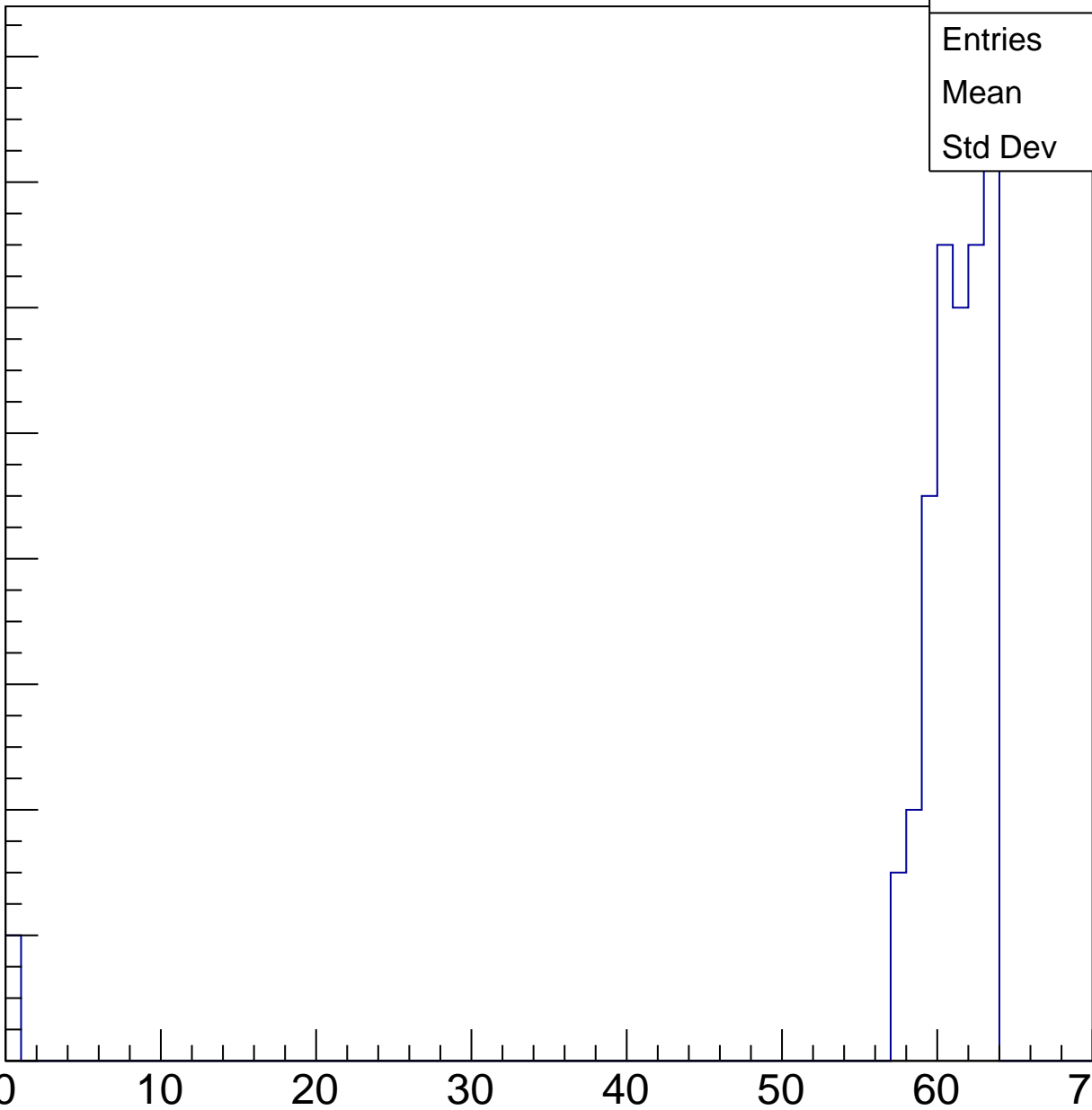
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	72
Mean	59.17
Std Dev	10.14

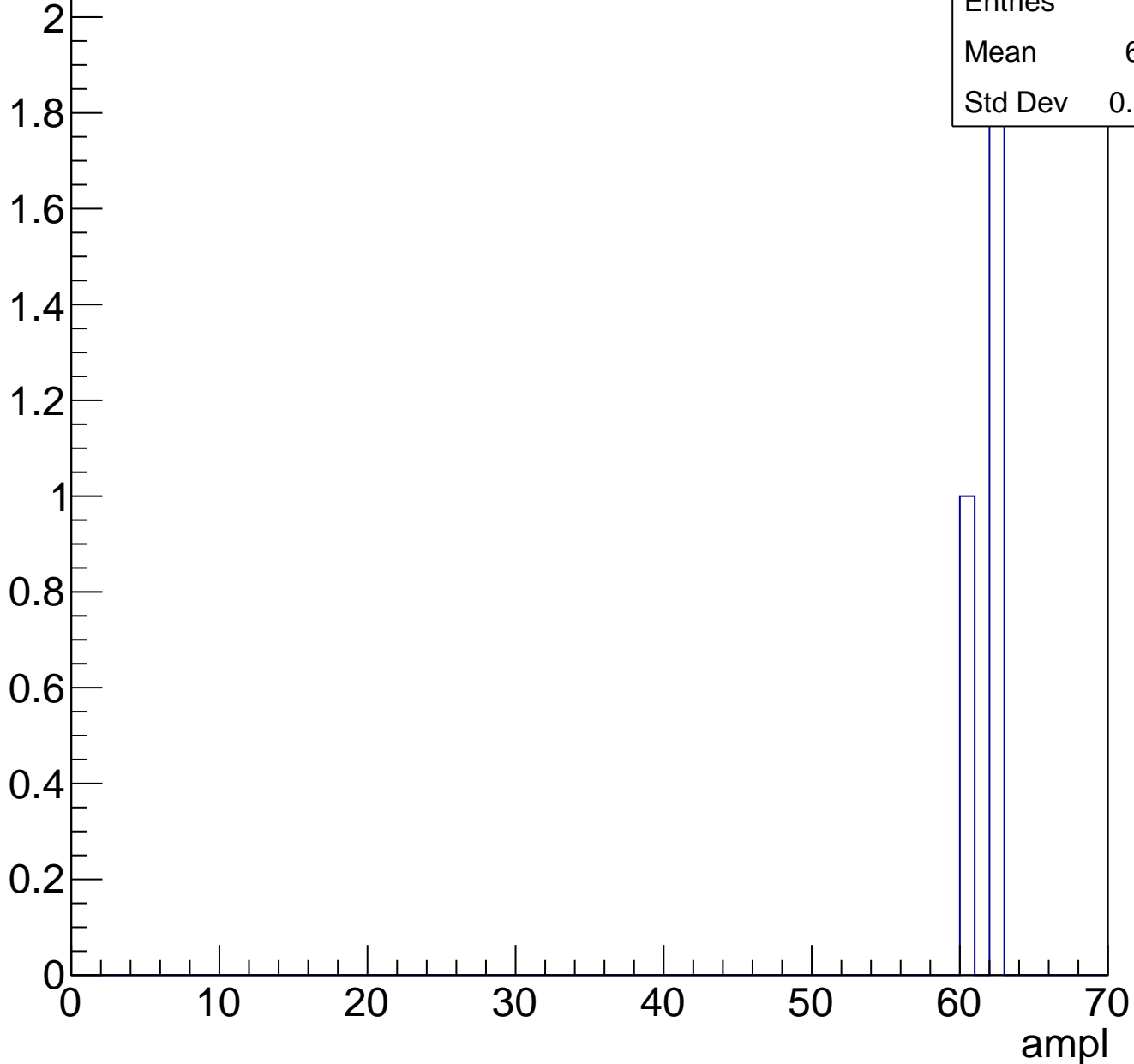
ampl



B0L101S, U19-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch82, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	26.43
Std Dev	4.541

Gaus mean : 26.9001

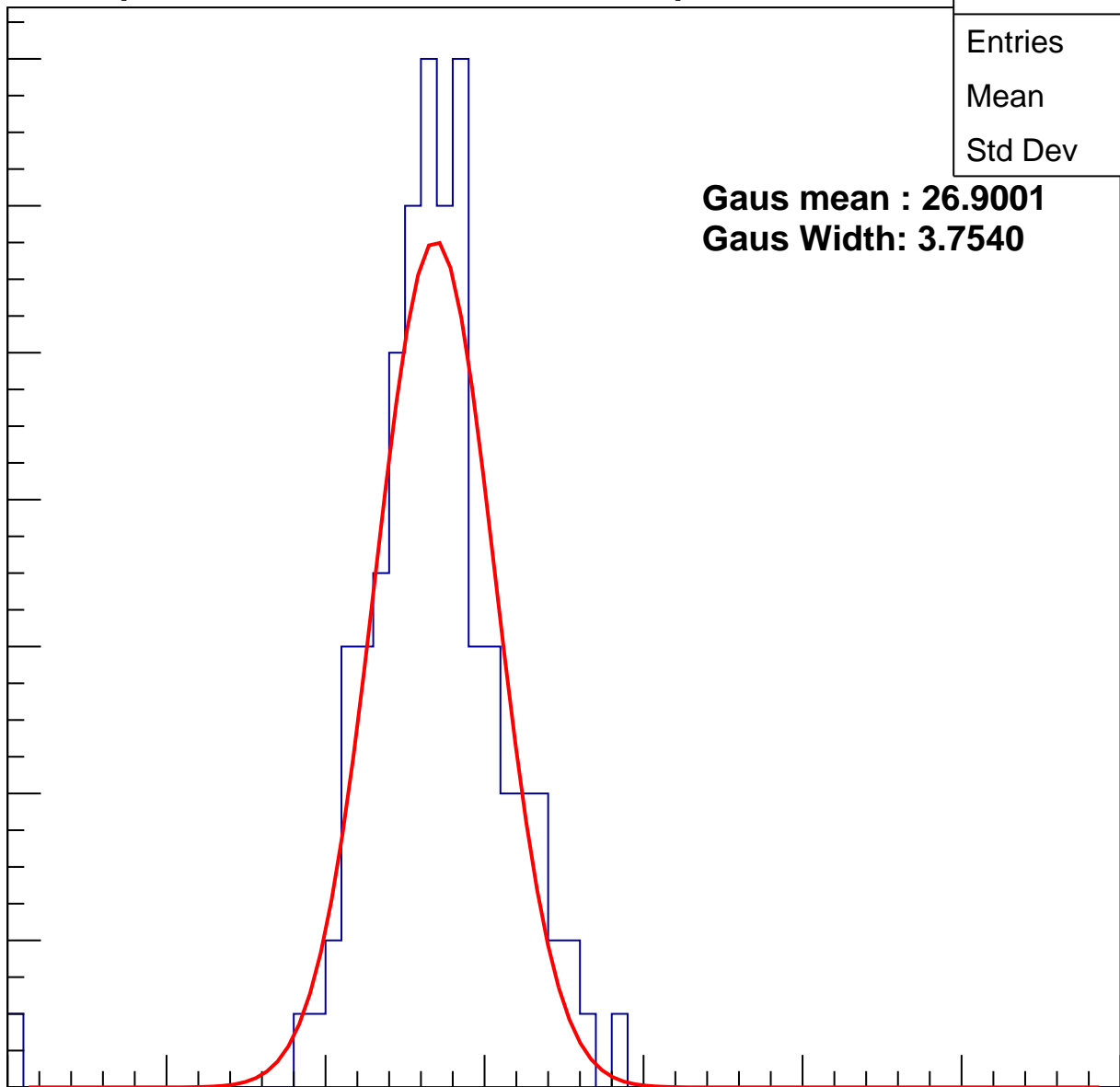
Gaus Width: 3.7540

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch82, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	32.63
Std Dev	3.387

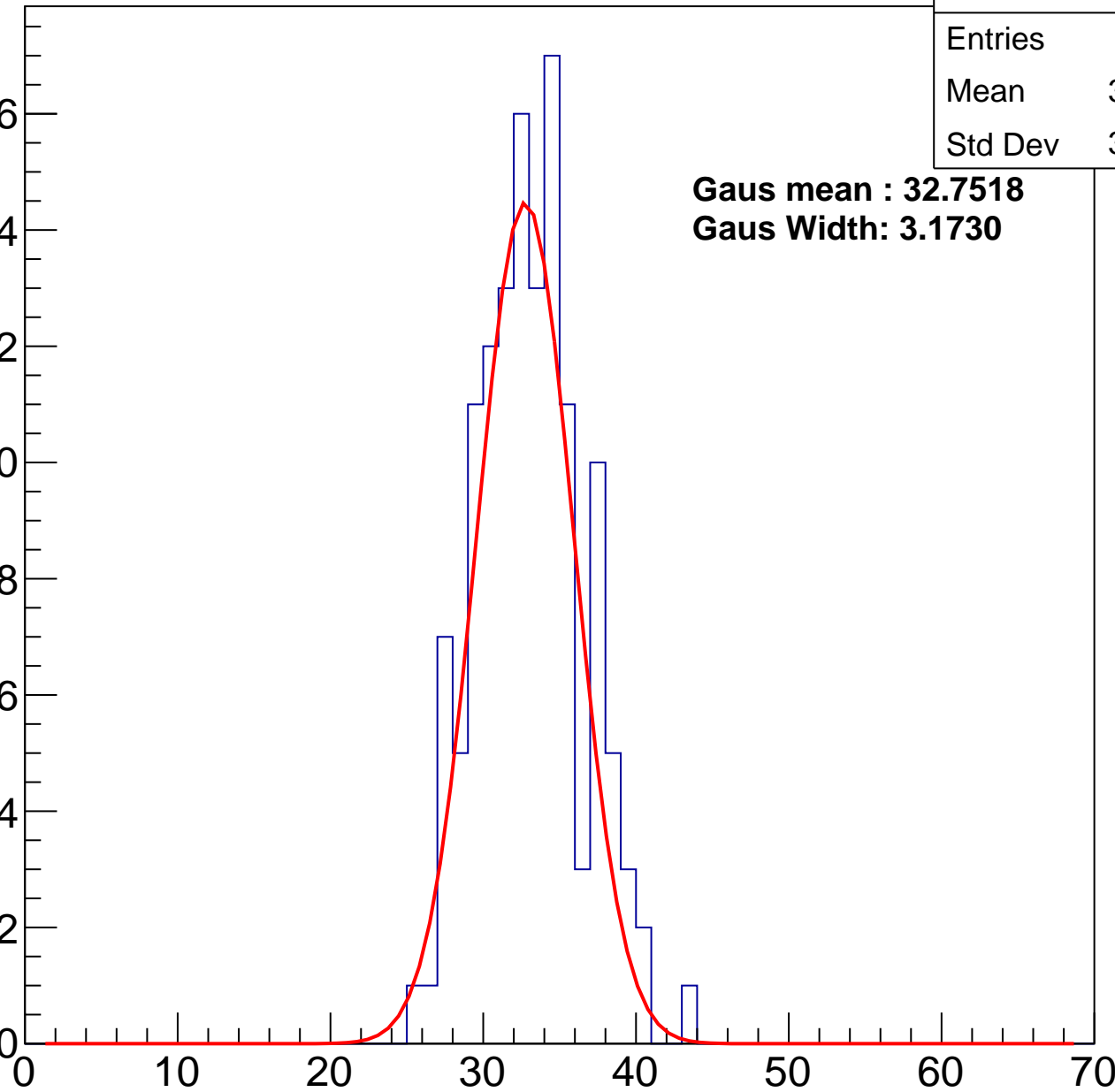
Gaus mean : 32.7518

Gaus Width: 3.1730

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch82, adc2

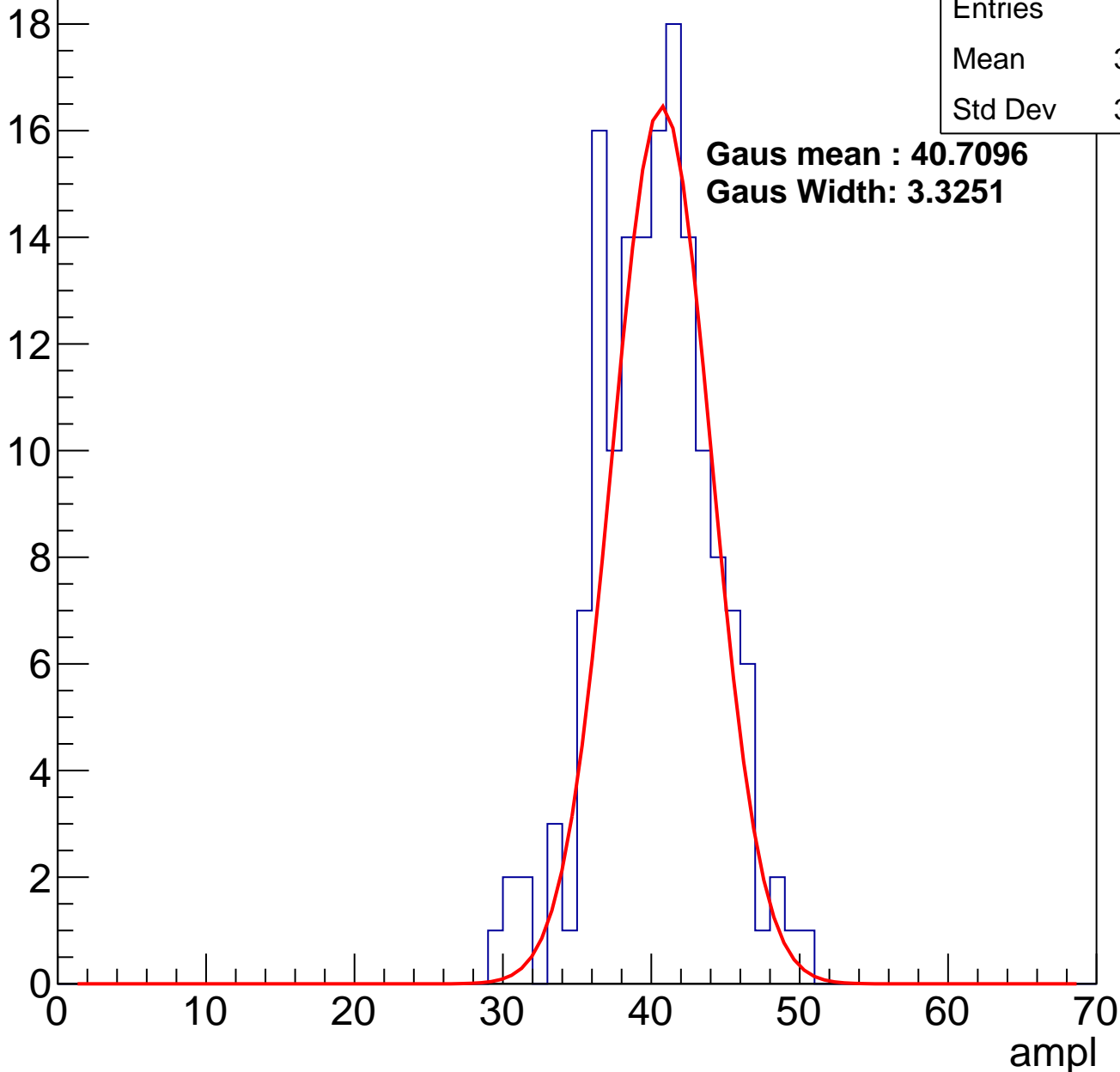
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	39.83
Std Dev	3.859

Gaus mean : 40.7096

Gaus Width: 3.3251

Entry



B0L101S, U19-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

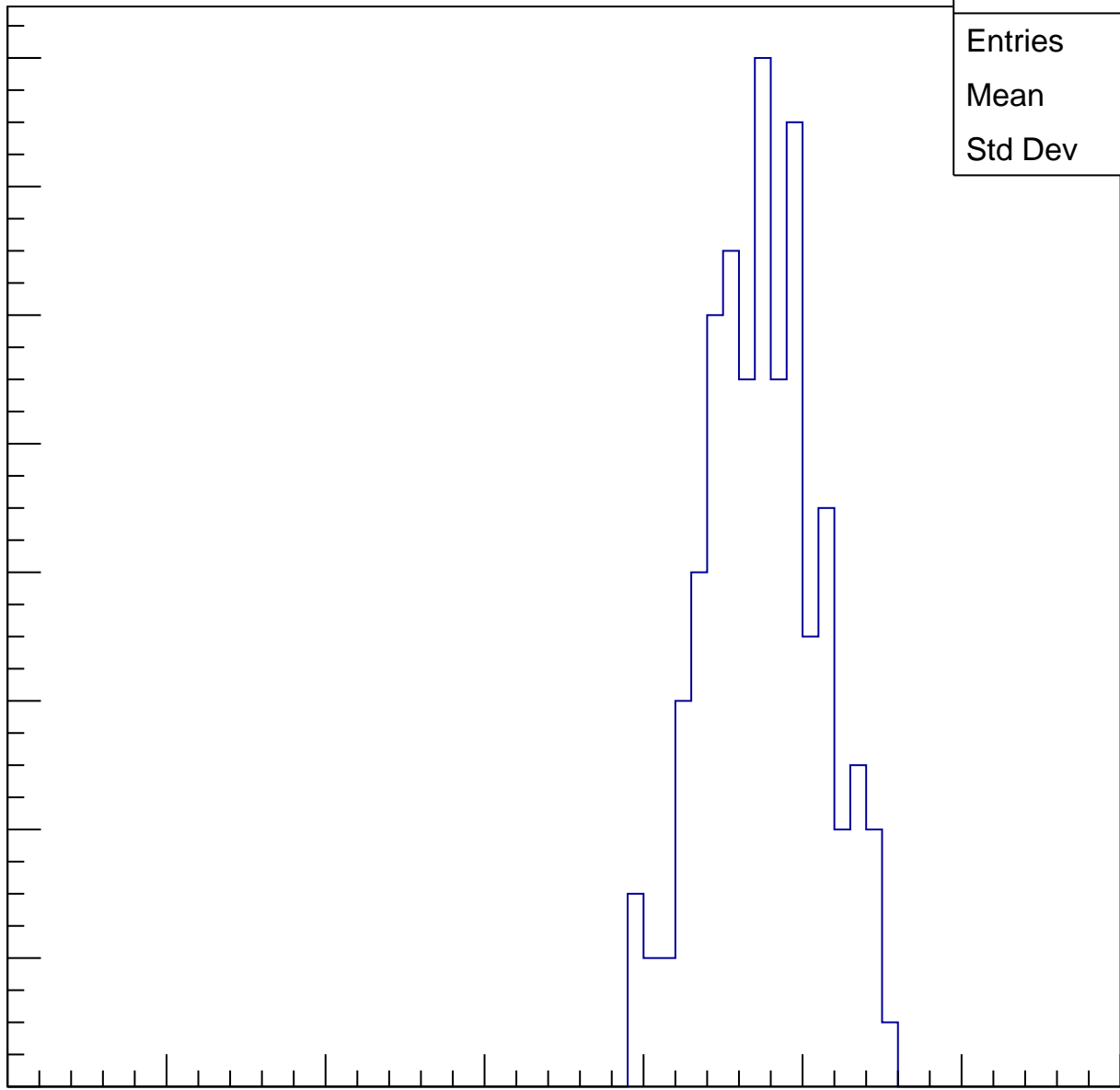
Entries	129
Mean	46.99
Std Dev	3.554

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	53.29
Std Dev	3.583

Entry

14
12
10
8
6
4
2
0

0

10

20

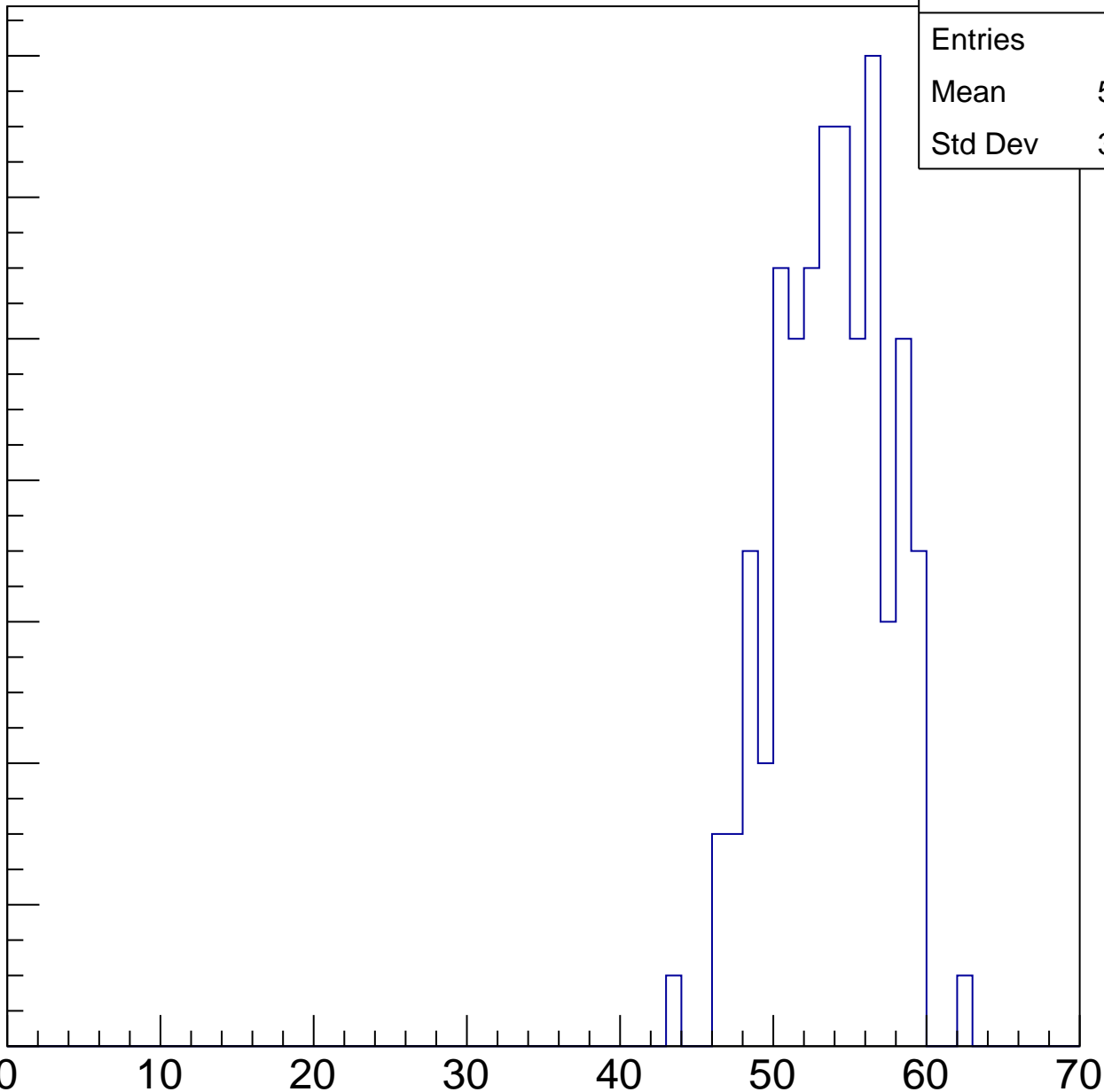
30

40

50

60

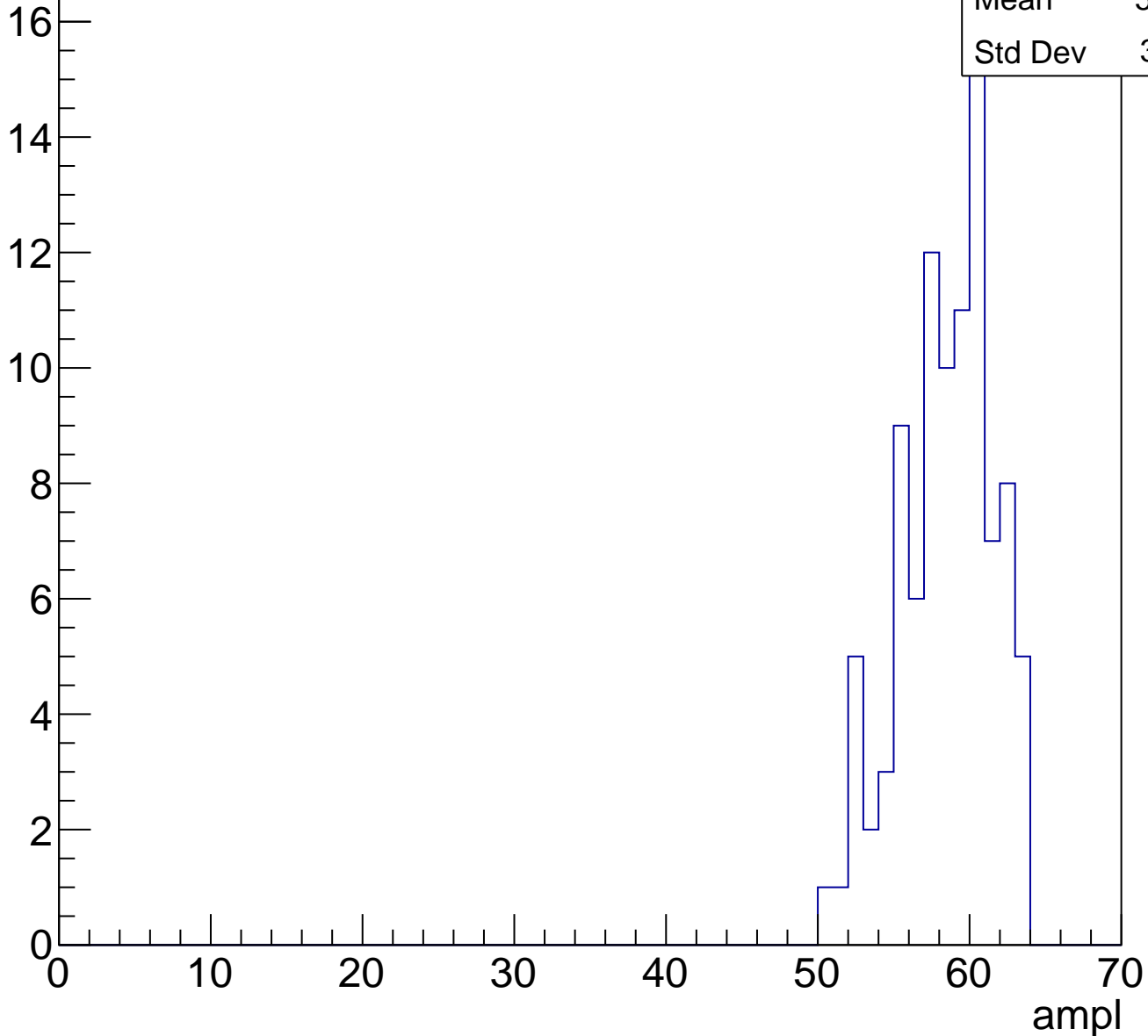
ampl



B0L101S, U19-ch82, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

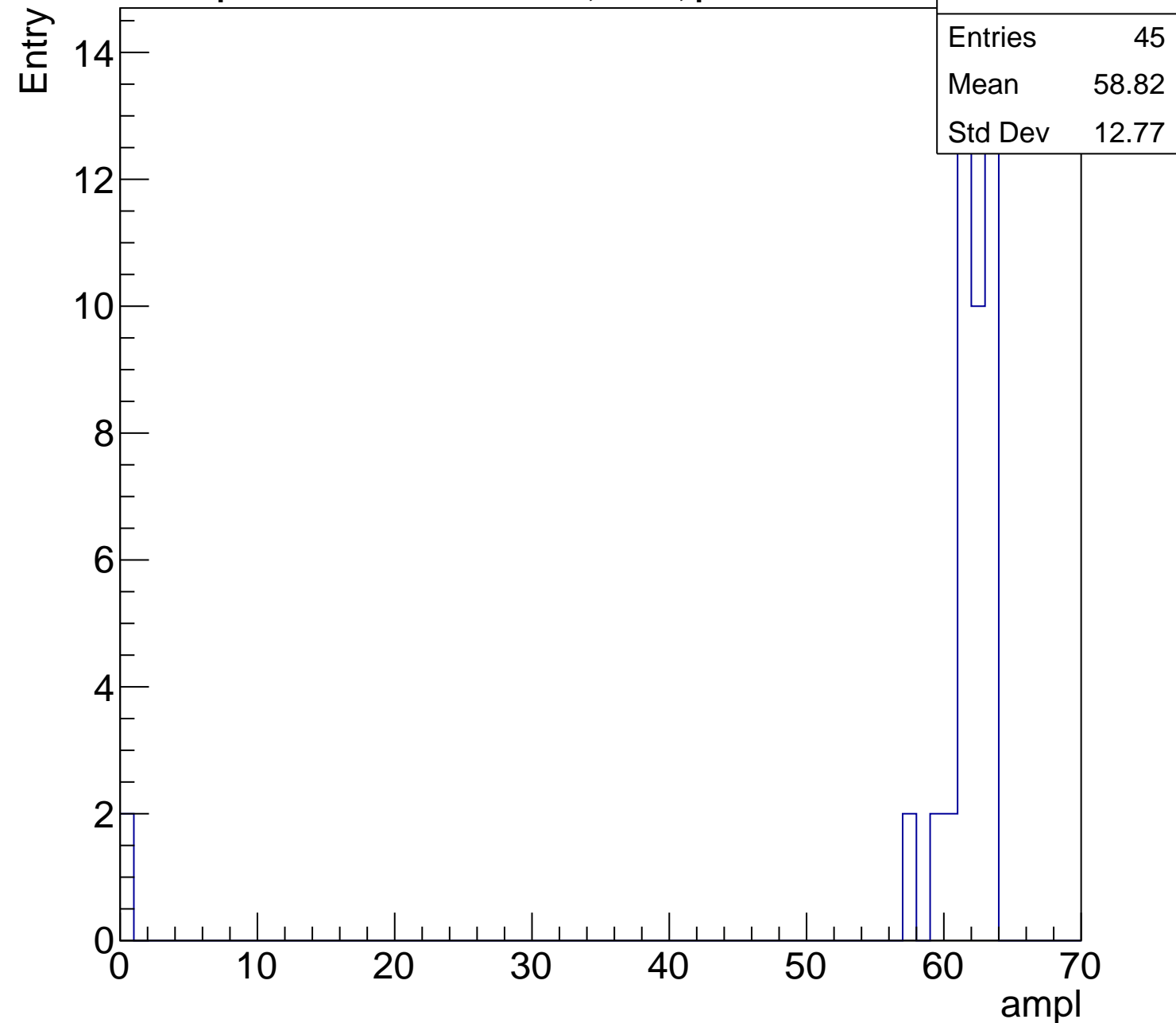
Entry

14
12
10
8
6
4
2
0

Entries	45
Mean	58.82
Std Dev	12.77

ampl

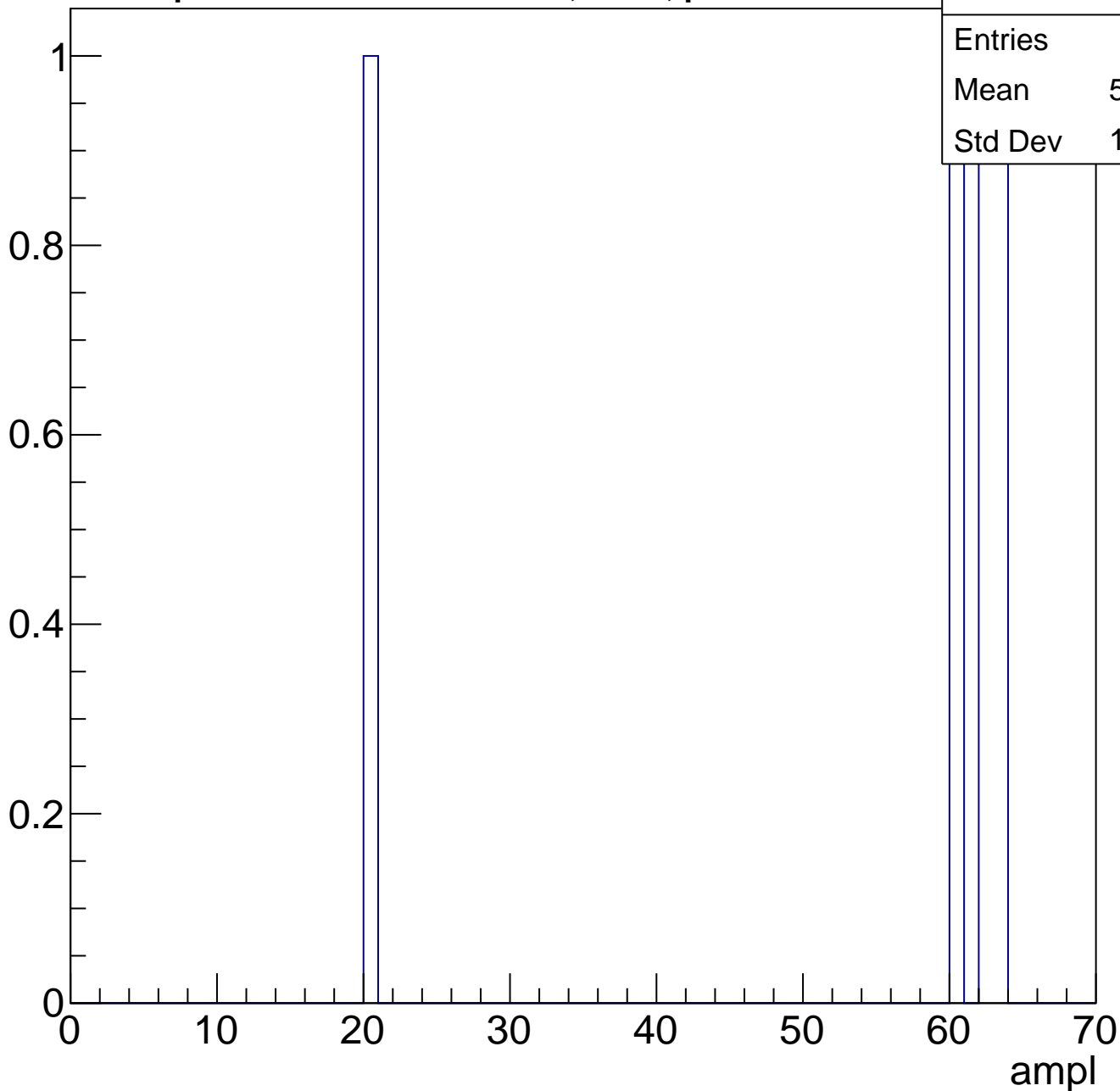
0 10 20 30 40 50 60 70



B0L101S, U19-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch83, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	28.87
Std Dev	4.322

Gaus mean : 29.6190

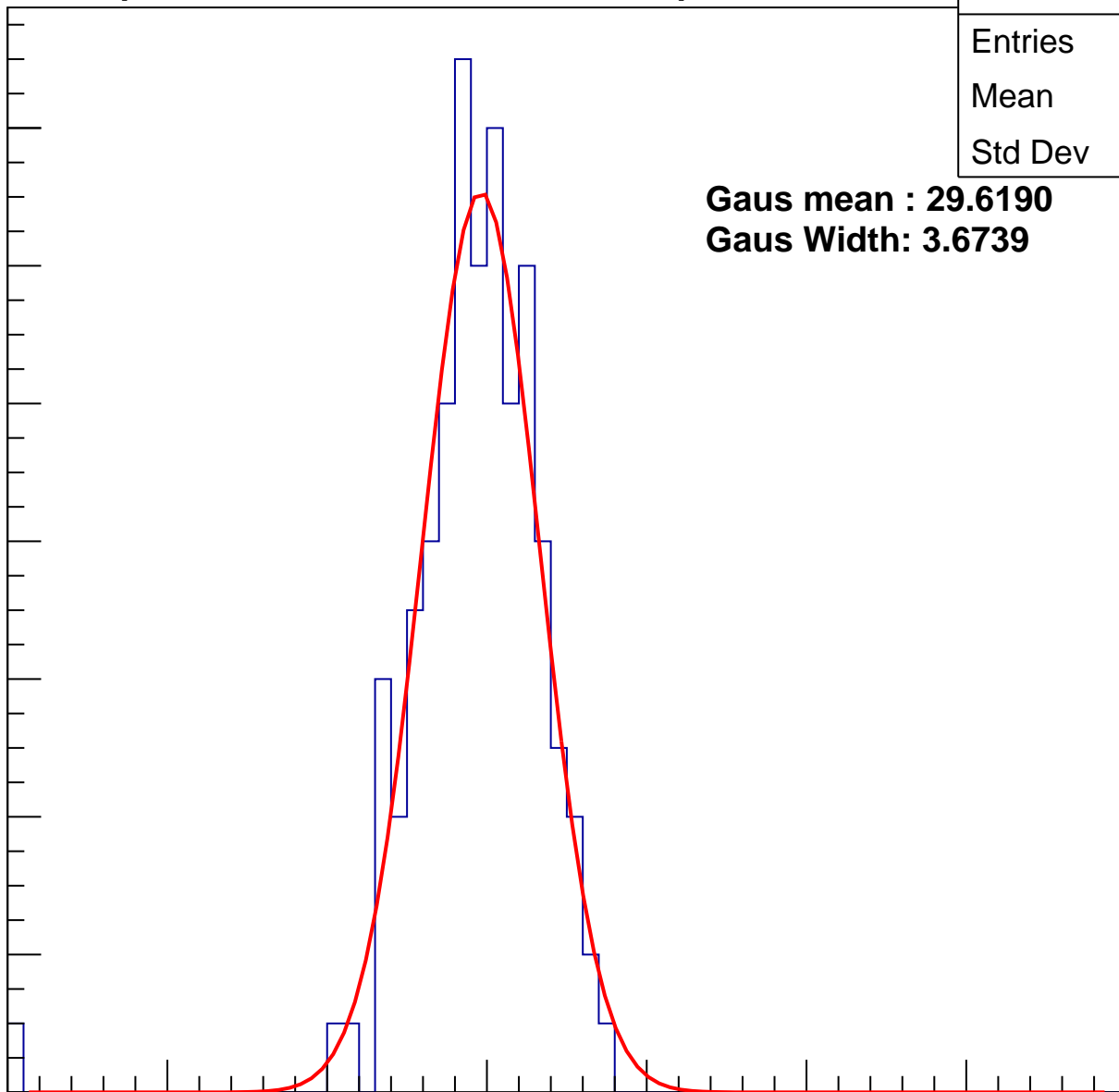
Gaus Width: 3.6739

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

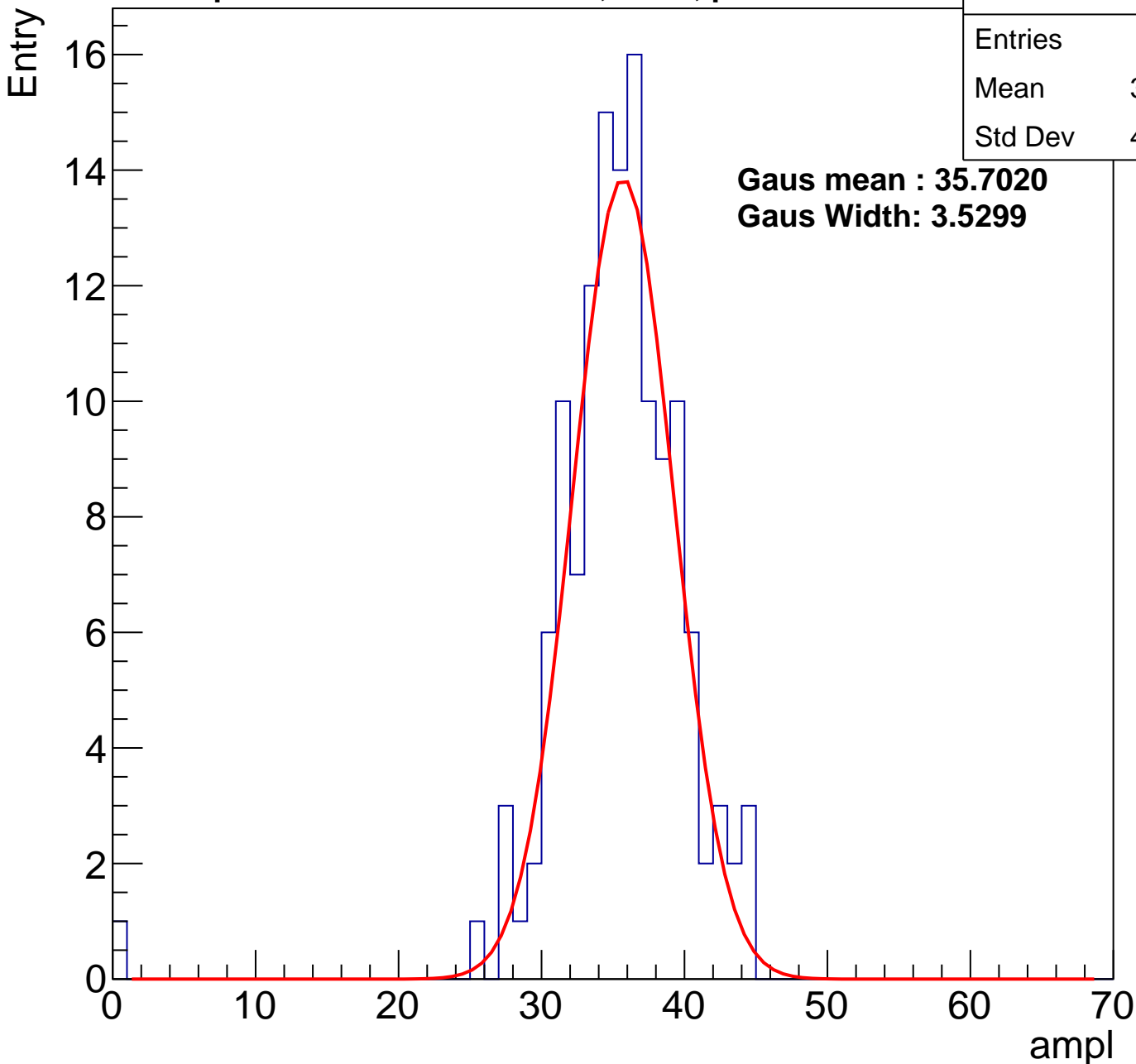


B0L101S, U19-ch83, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	34.93
Std Dev	4.836

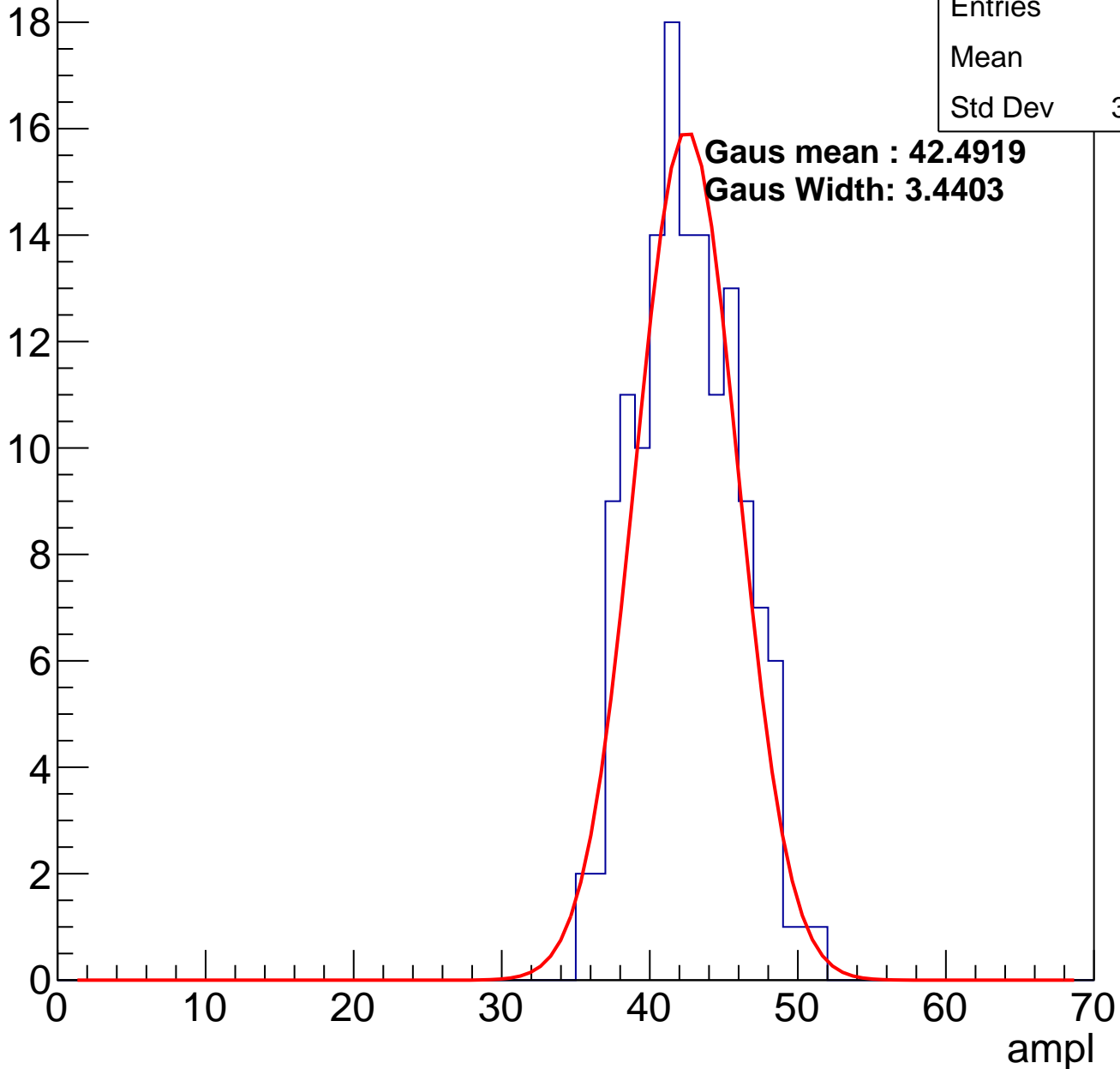
Gaus mean : 35.7020
Gaus Width: 3.5299



B0L101S, U19-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

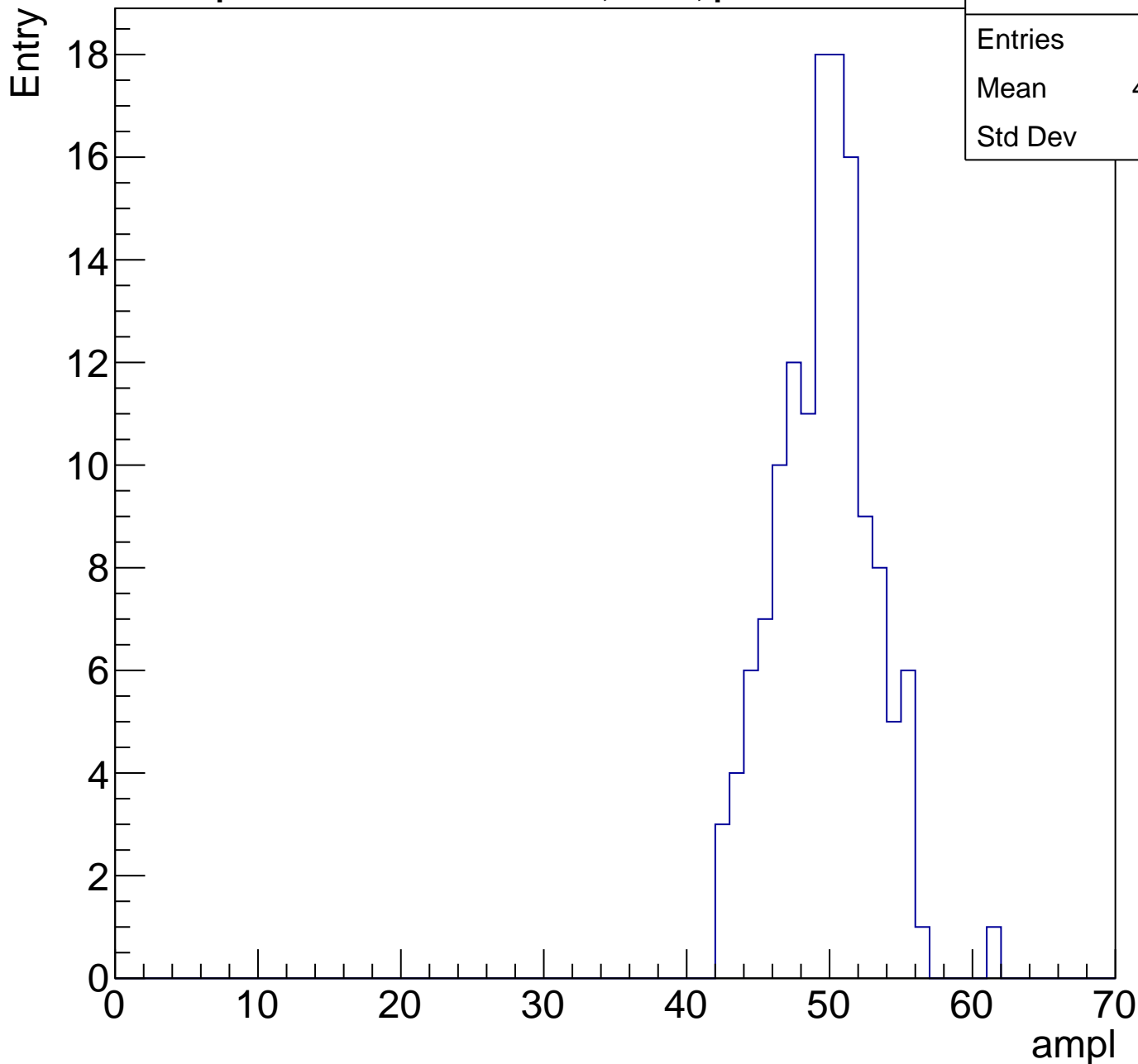
Entry



B0L101S, U19-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	49.16
Std Dev	3.36



B0L101S, U19-ch83, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	55.89
Std Dev	3.43

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

40

50

60

70

ampl

B0L101S, U19-ch83, adc5

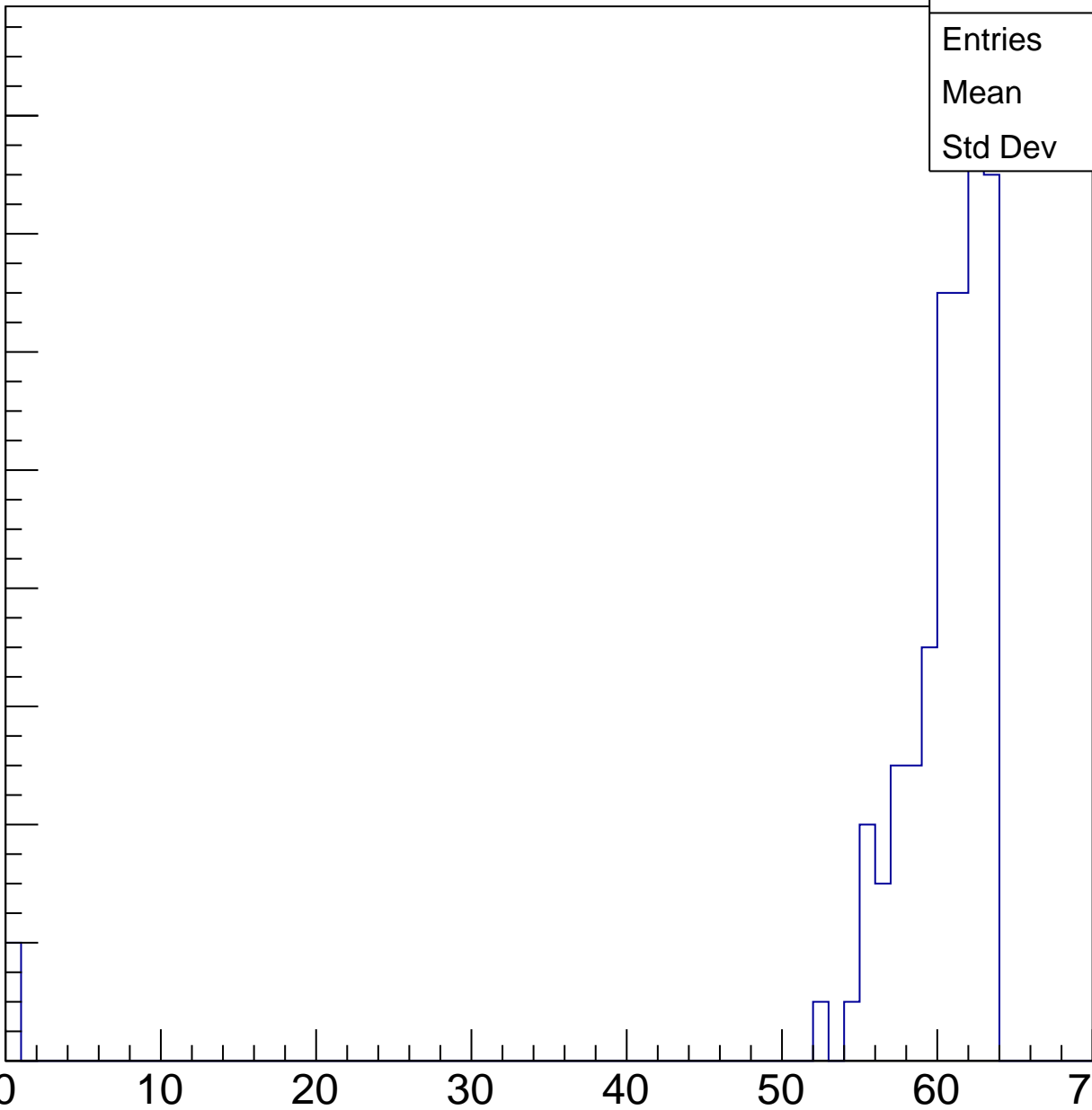
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.77
Std Dev	9.404

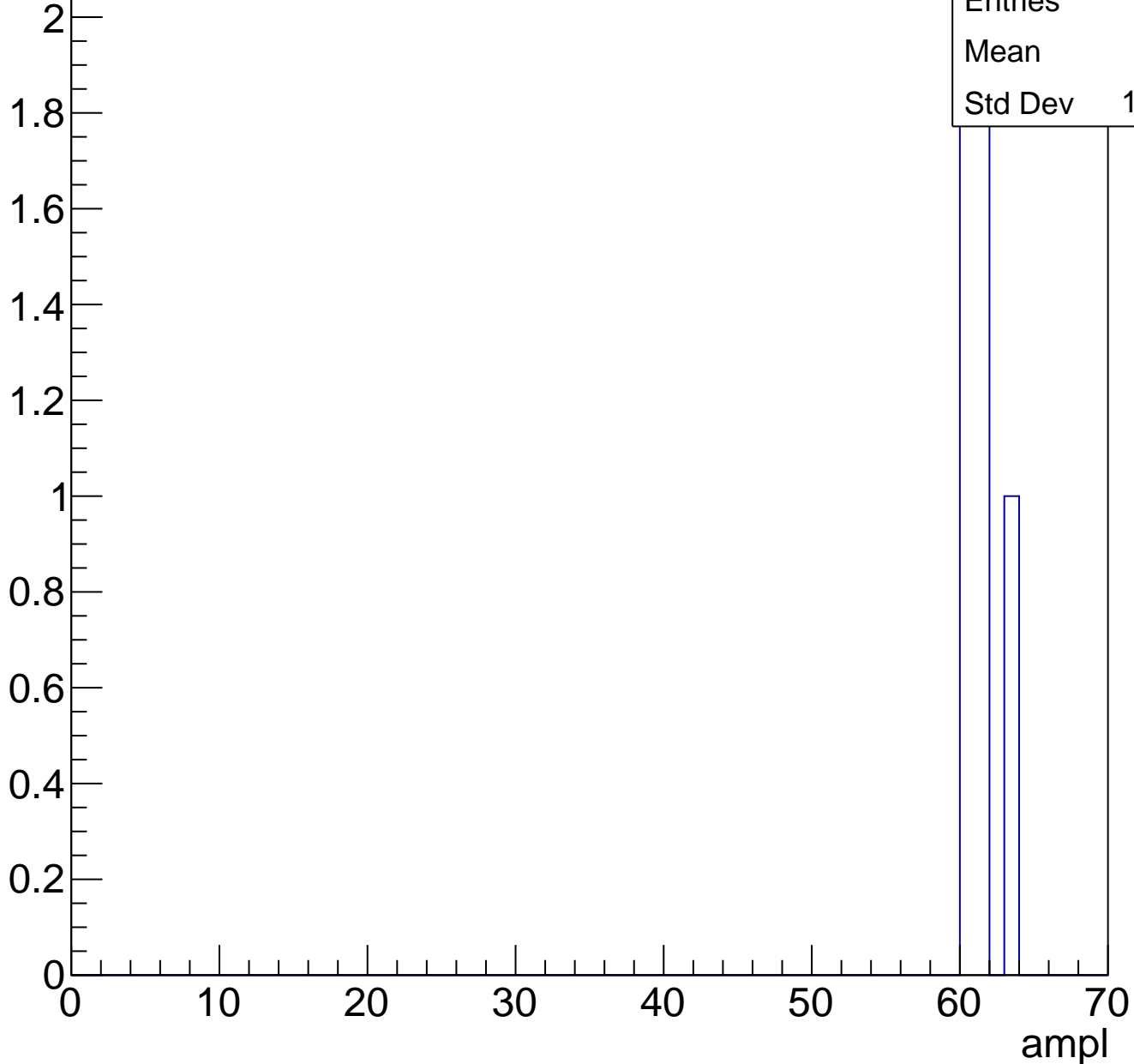
ampl



B0L101S, U19-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



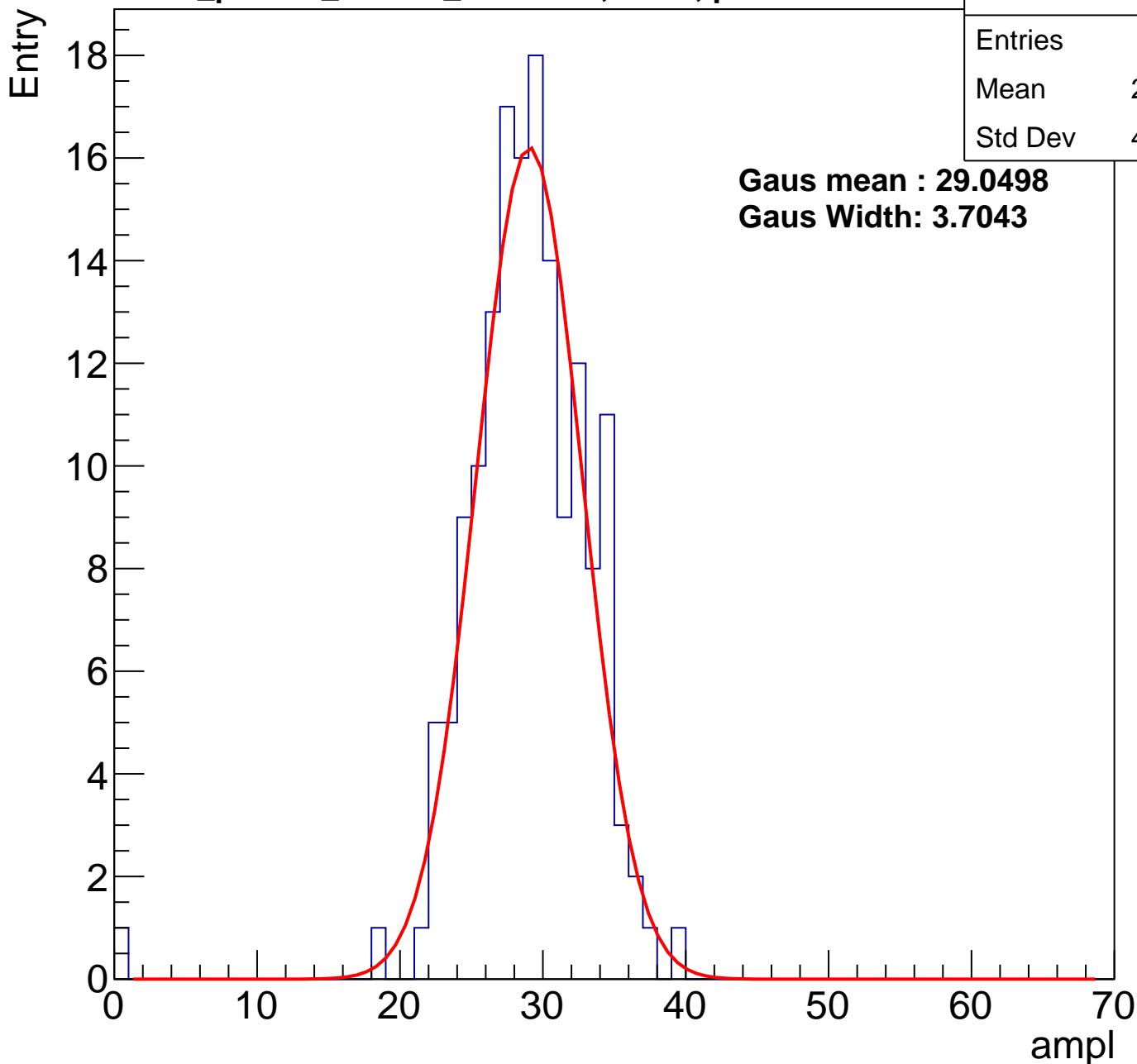
B0L101S, U19-ch84, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	28.48
Std Dev	4.316

Gaus mean : 29.0498

Gaus Width: 3.7043



B0L101S, U19-ch84, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	34.59
Std Dev	3.454

Gaus mean : 35.4869

Gaus Width: 3.4694

Entry

12

10

8

6

4

2

0

0

10

20

30

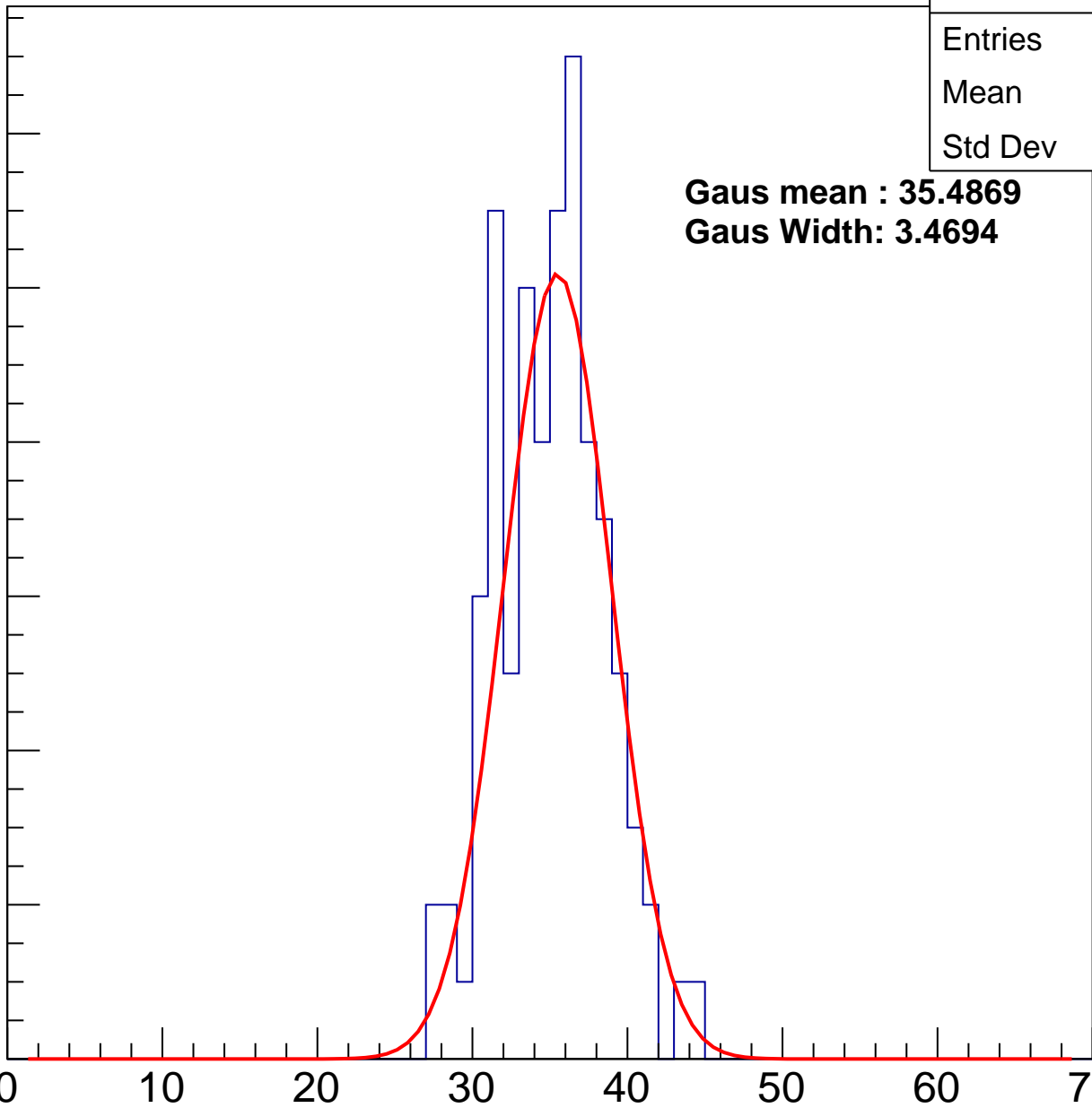
40

50

60

70

ampl



B0L101S, U19-ch84, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	40.5
Std Dev	3.381

Gaus mean : 41.4258

Gaus Width: 3.4219

Entry

16

14

12

10

8

6

4

2

0

0

10

20

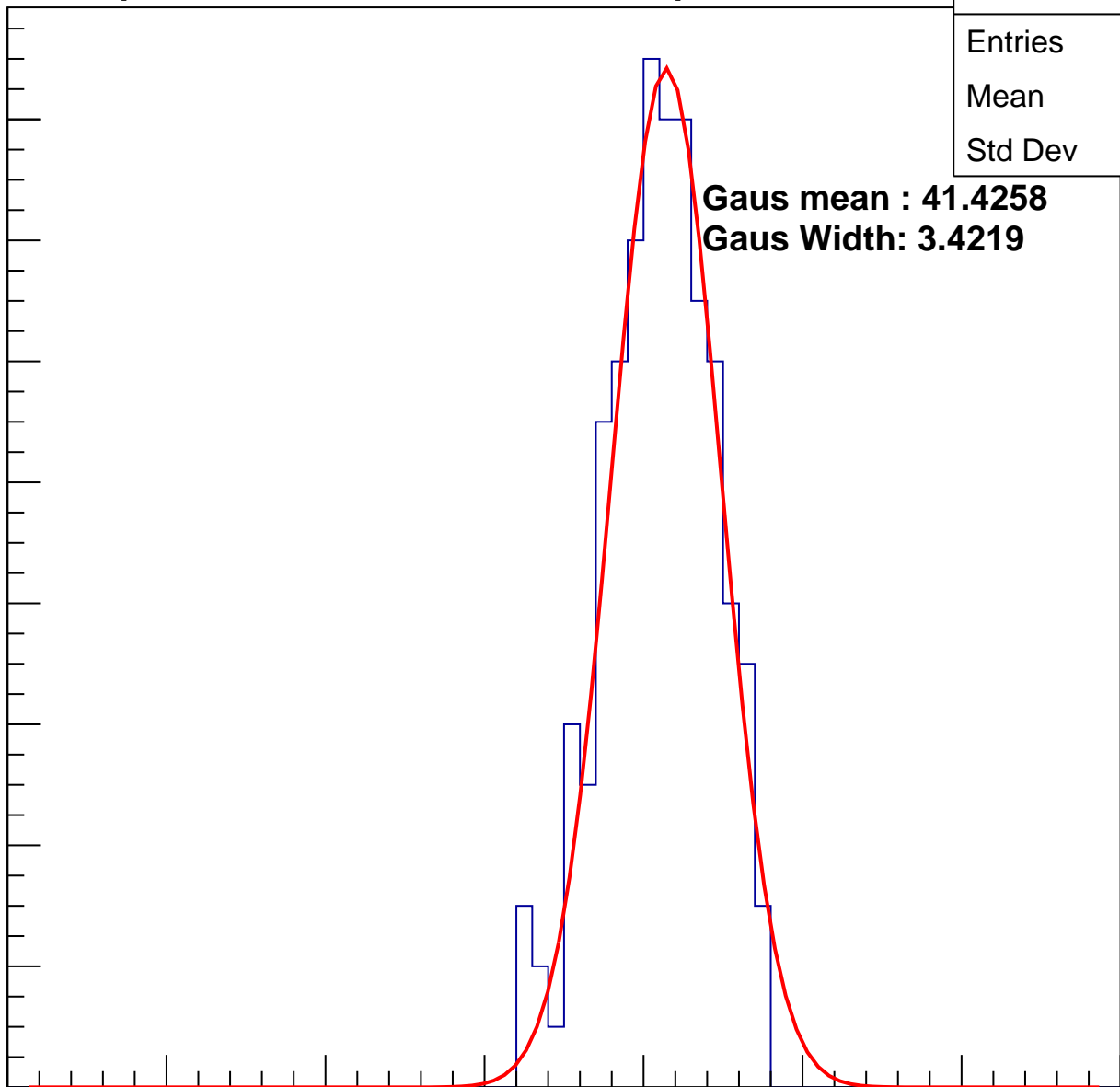
30

40

50

60

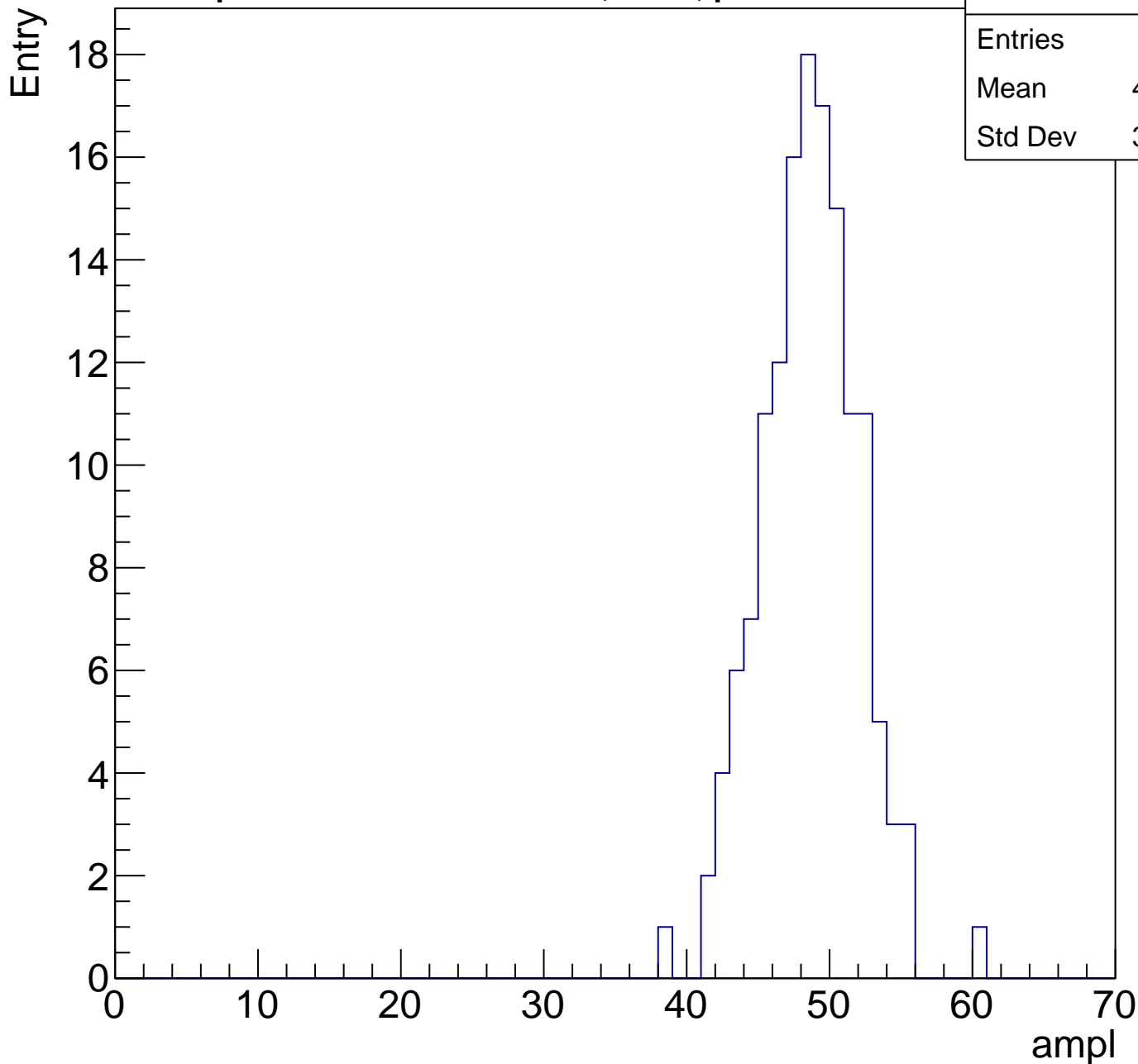
ampl



B0L101S, U19-ch84, adc3

calib_packv5_042523_0143.root, FC#1, port C1

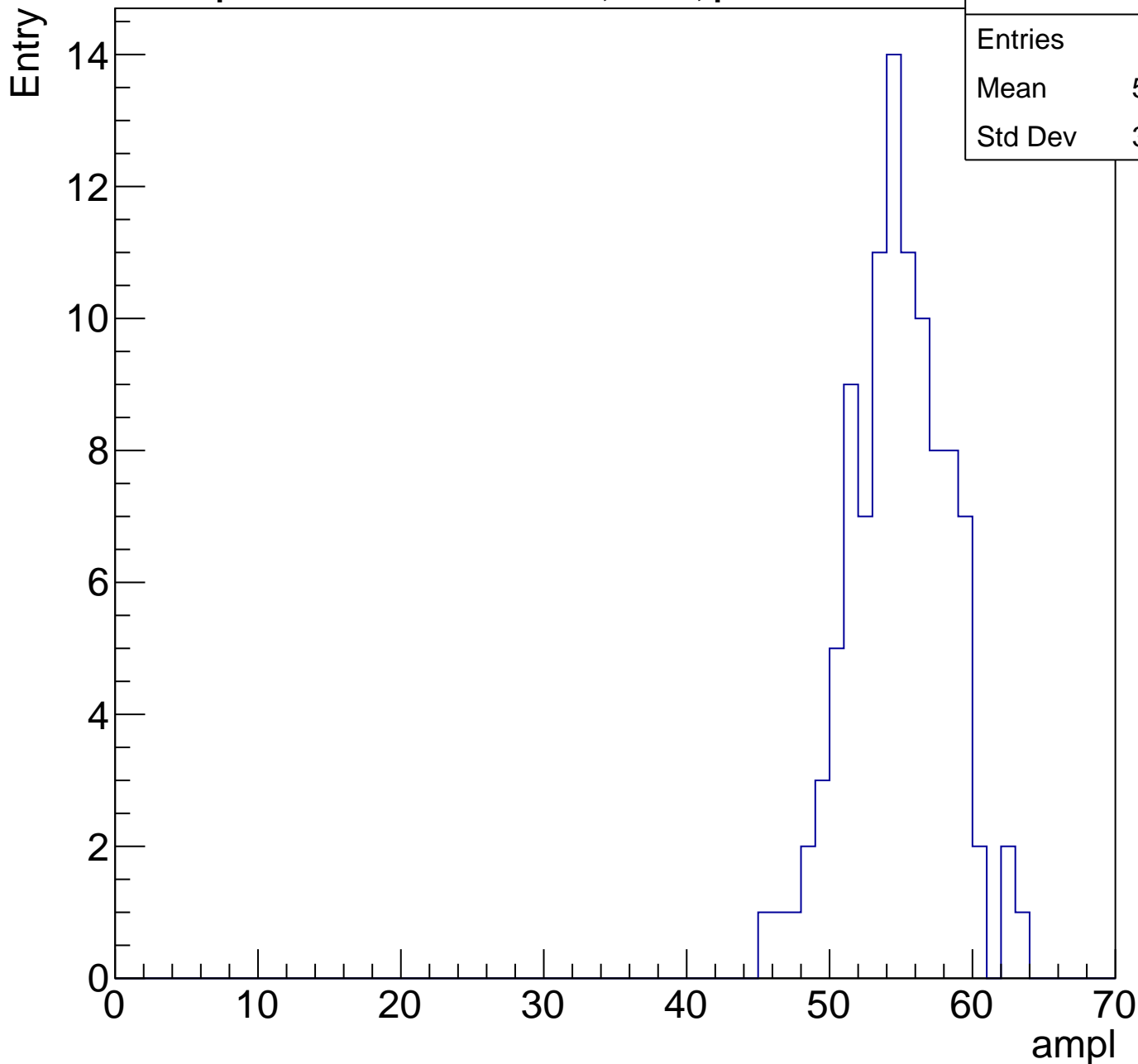
Entries	143
Mean	48.15
Std Dev	3.386



B0L101S, U19-ch84, adc4

calib_packv5_042523_0143.root, FC#1, port C1

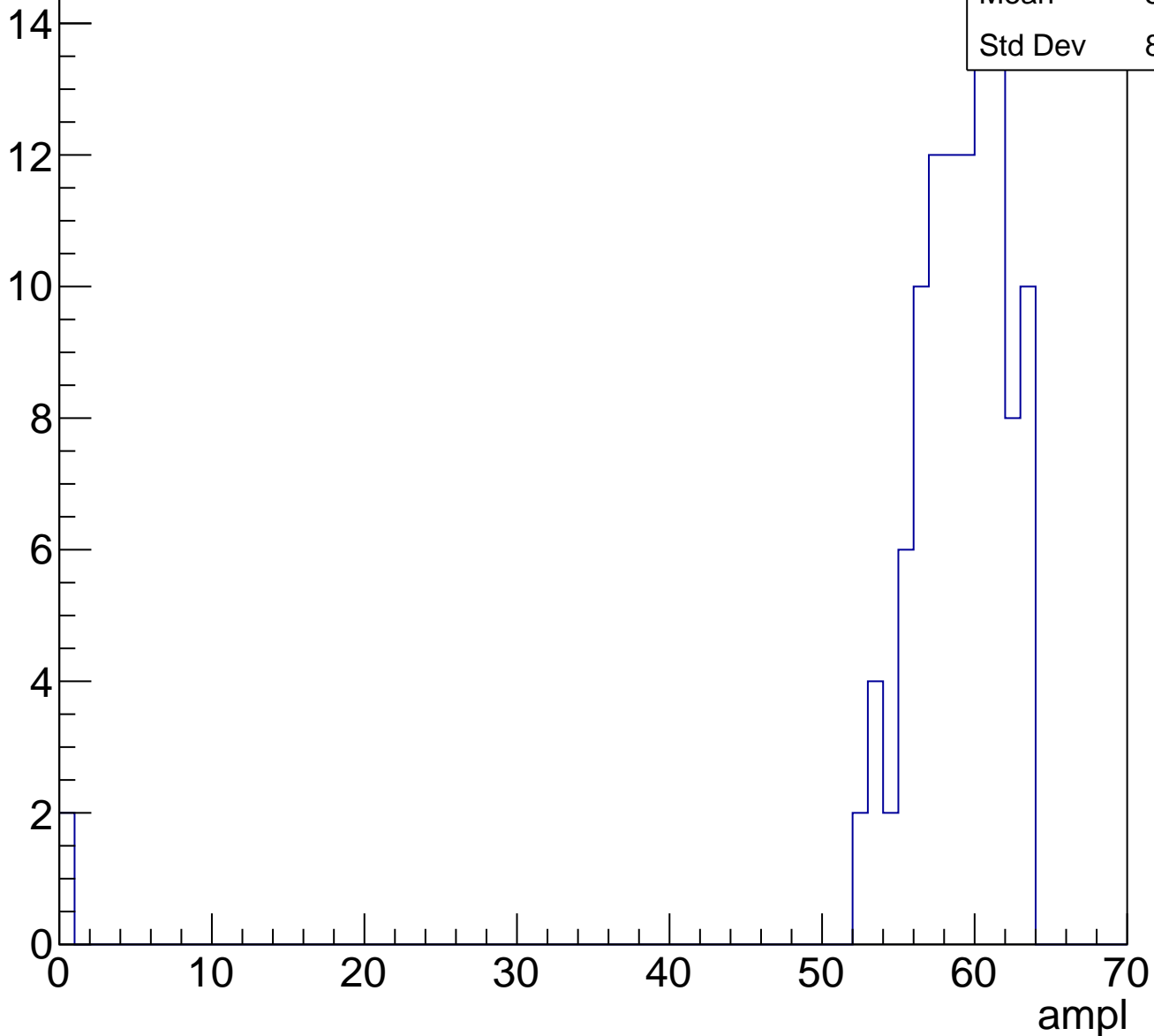
Entries	103
Mean	54.35
Std Dev	3.463



B0L101S, U19-ch84, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	26
Mean	61.58
Std Dev	1.843

Entry

10

8

6

4

2

0

0

10

20

30

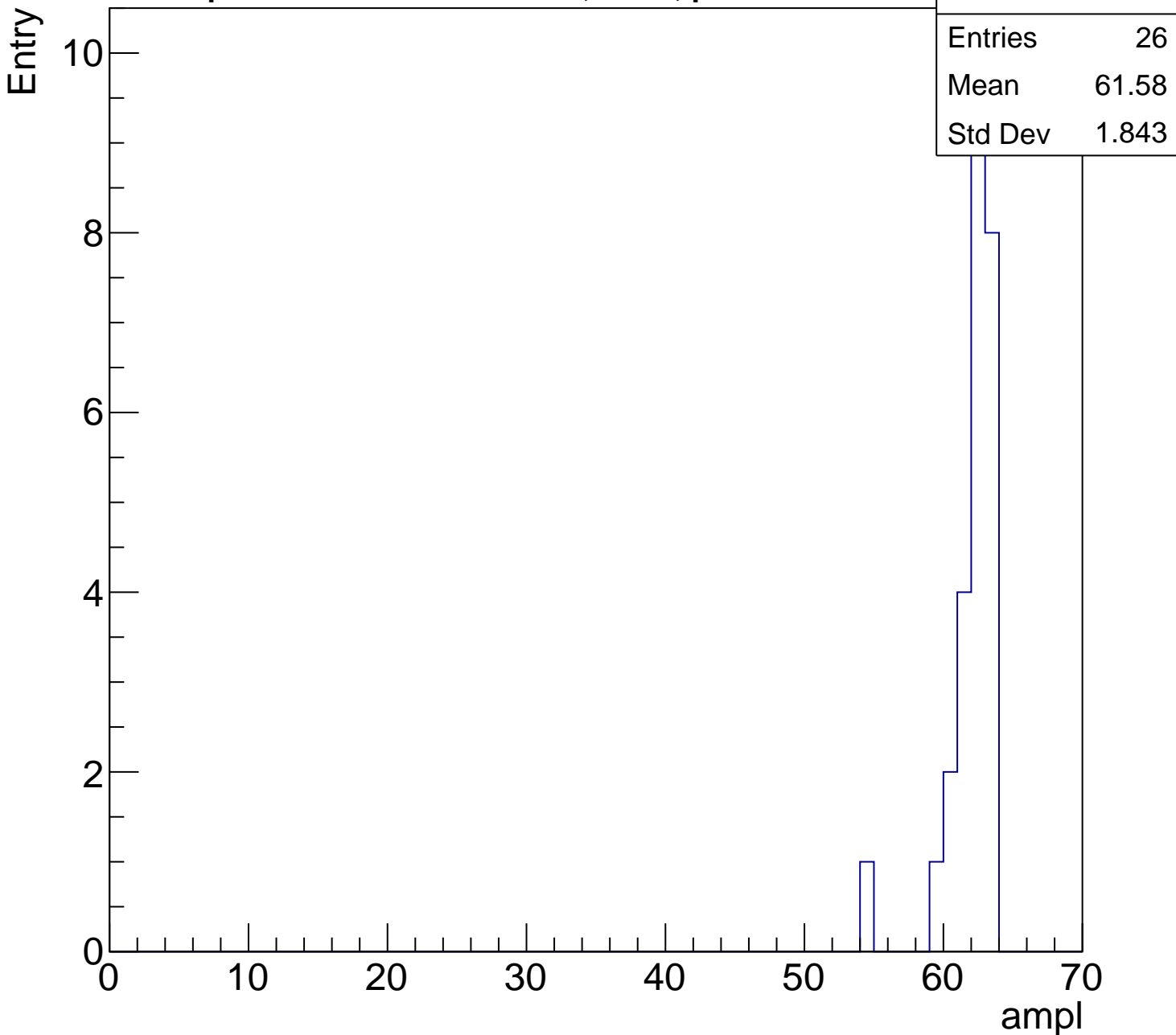
40

50

60

70

ampl



B0L101S, U19-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch85, adc0

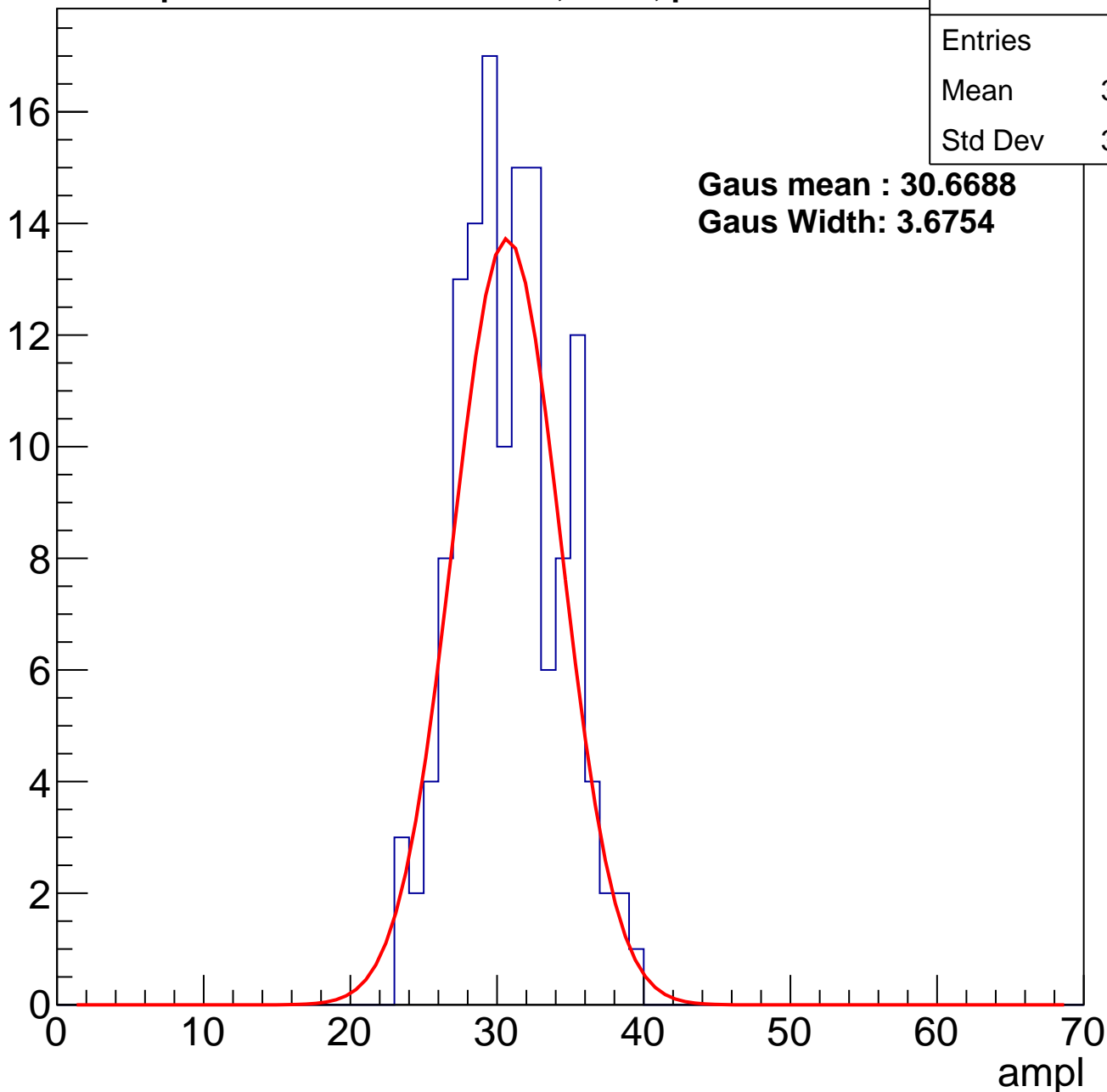
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	30.36
Std Dev	3.459

Gaus mean : 30.6688

Gaus Width: 3.6754

Entry



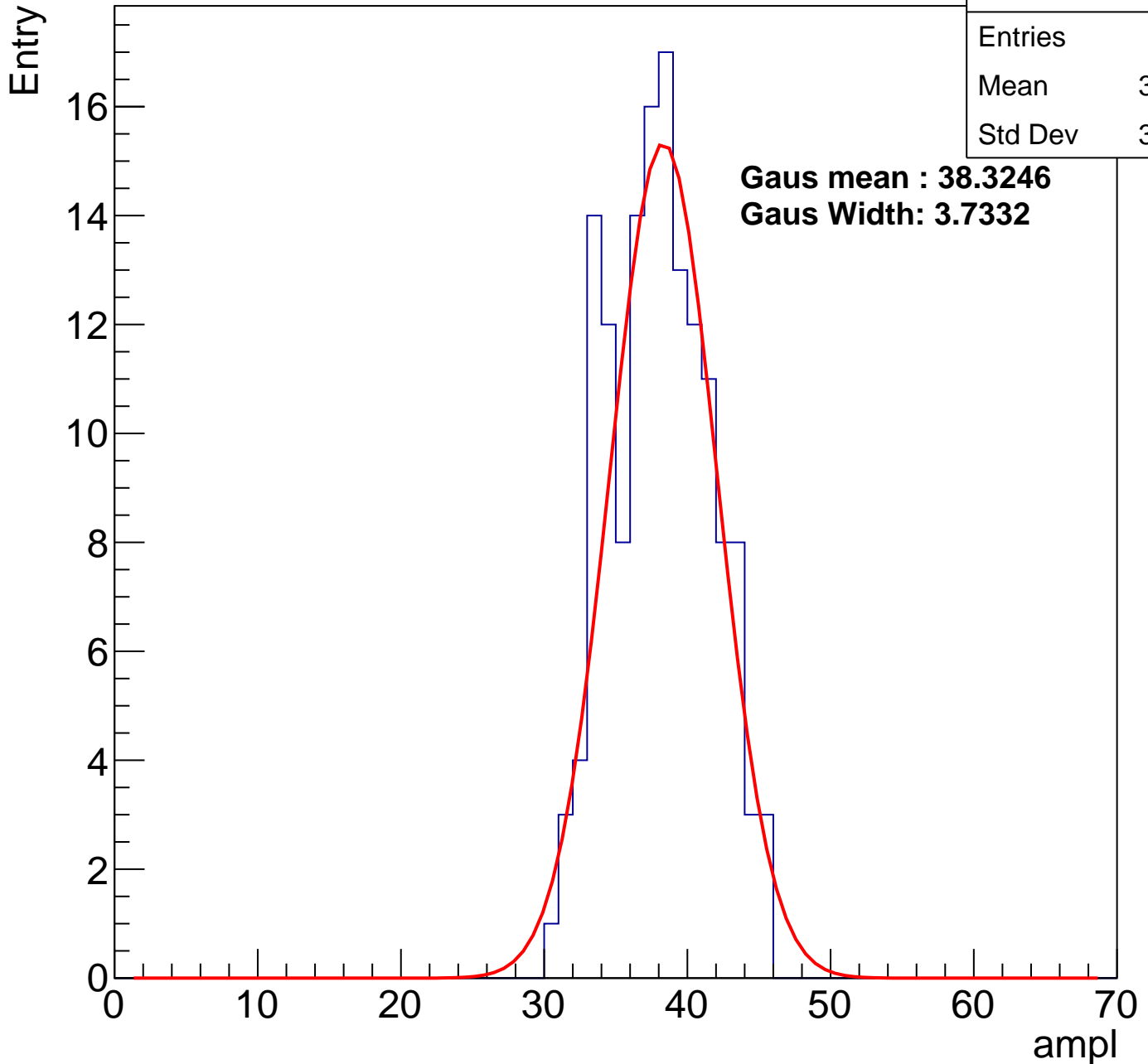
B0L101S, U19-ch85, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	37.61
Std Dev	3.461

Gaus mean : 38.3246

Gaus Width: 3.7332



B0L101S, U19-ch85, adc2

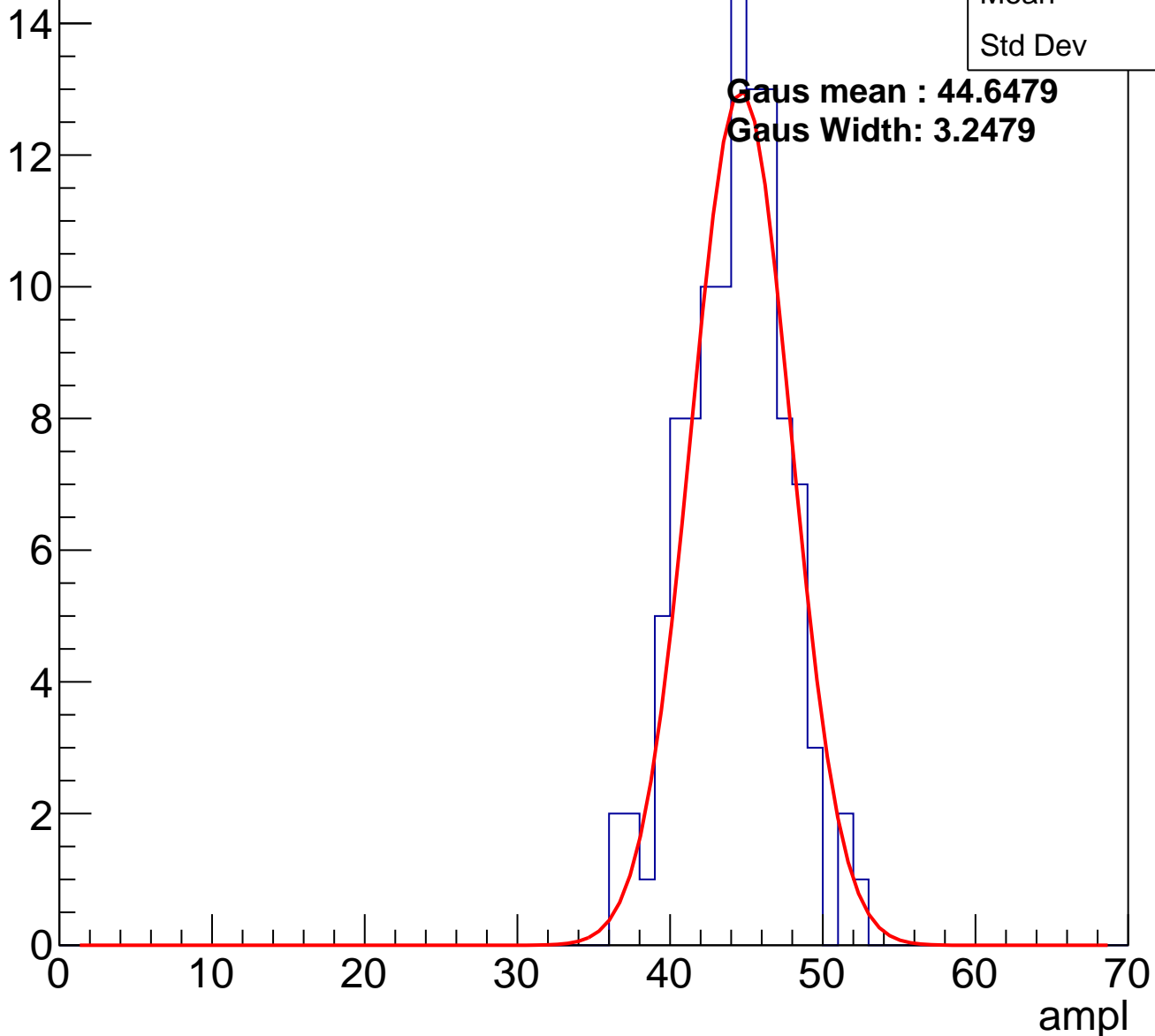
calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	43.82
Std Dev	3.22

Gaus mean : 44.6479

Gaus Width: 3.2479

Entry

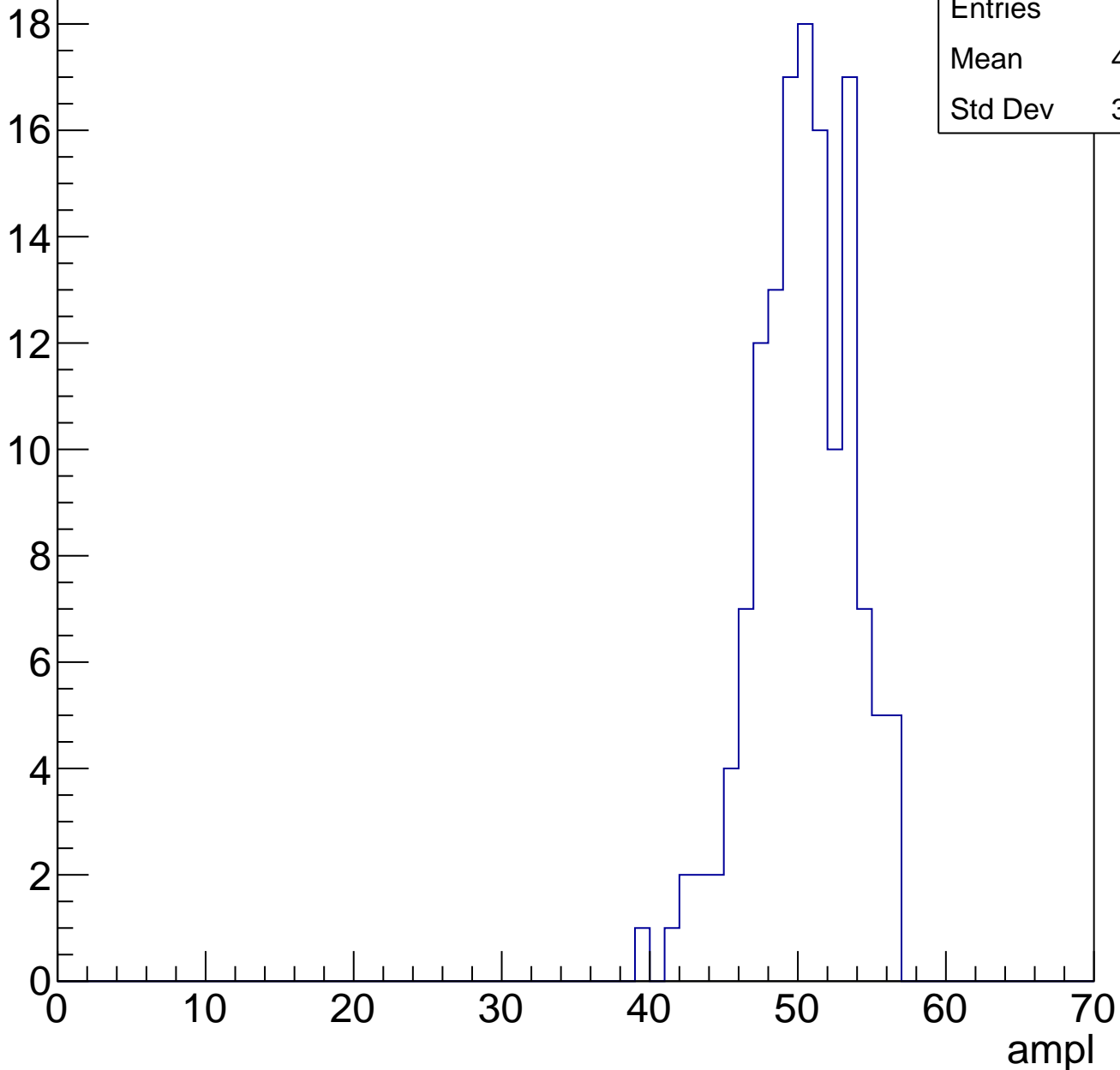


B0L101S, U19-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	49.86
Std Dev	3.299

Entry



B0L101S, U19-ch85, adc4

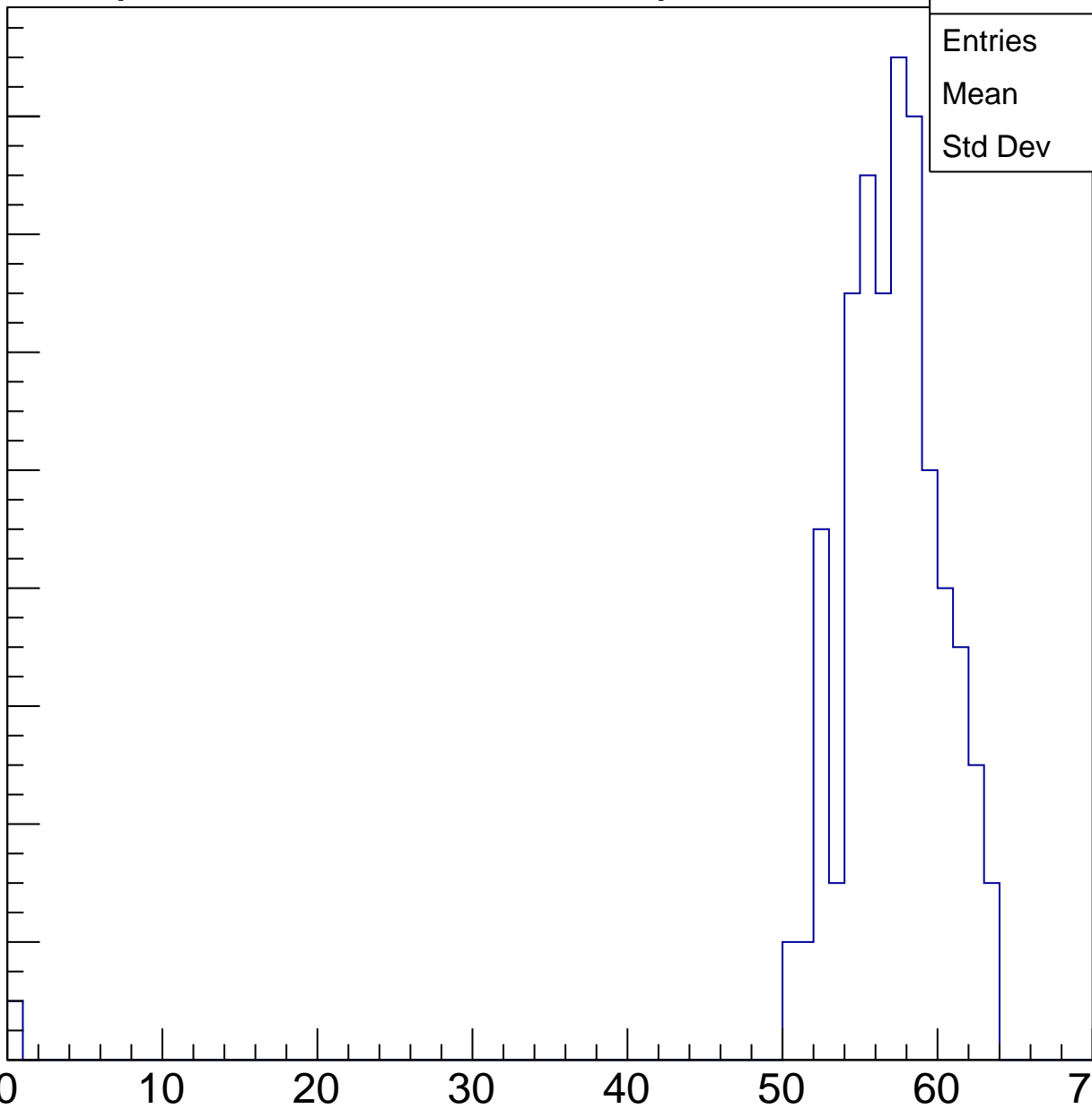
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	56.27
Std Dev	5.881

ampl



B0L101S, U19-ch85, adc5

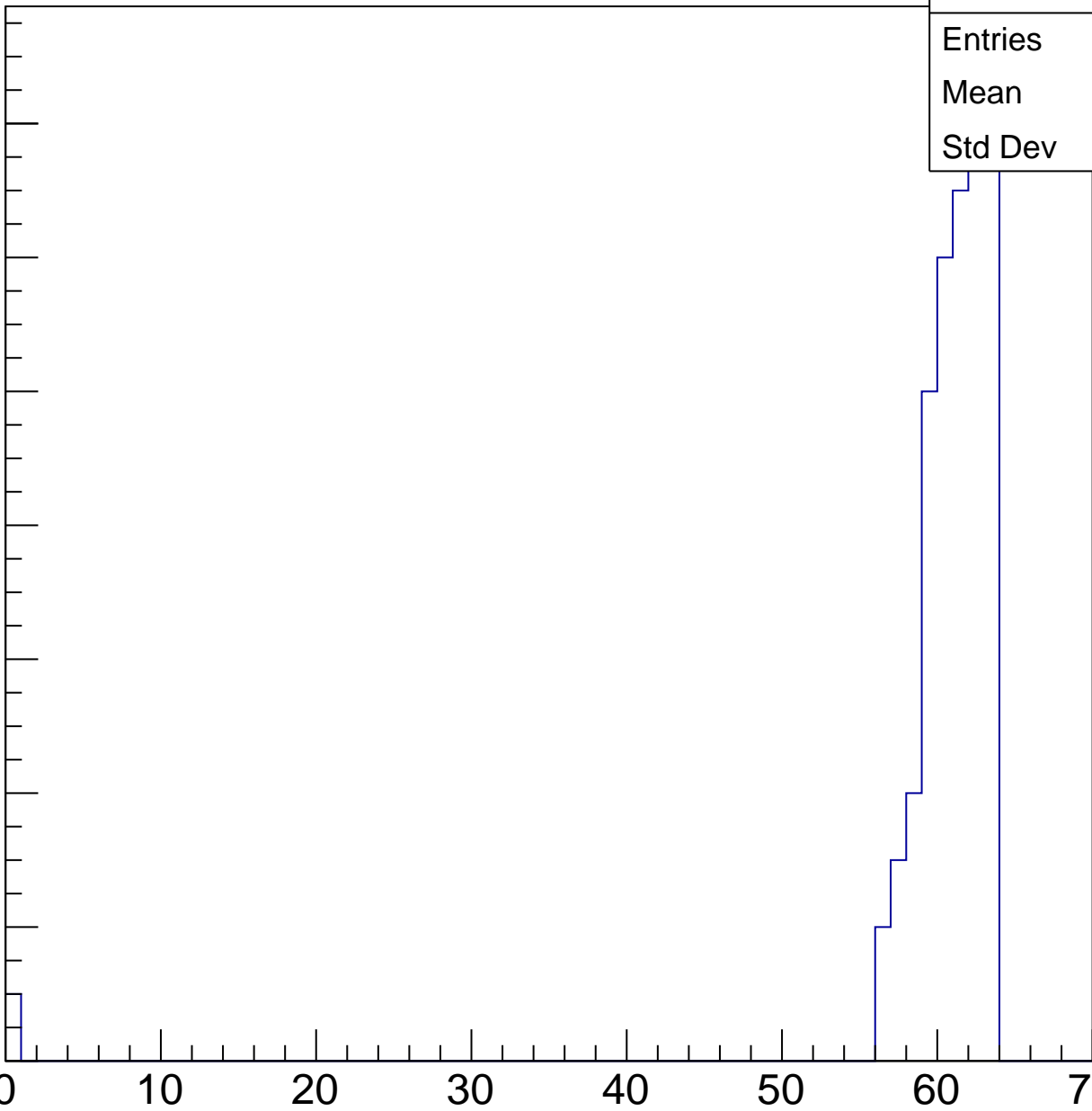
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	59.88
Std Dev	7.248

ampl



B0L101S, U19-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch86, adc0

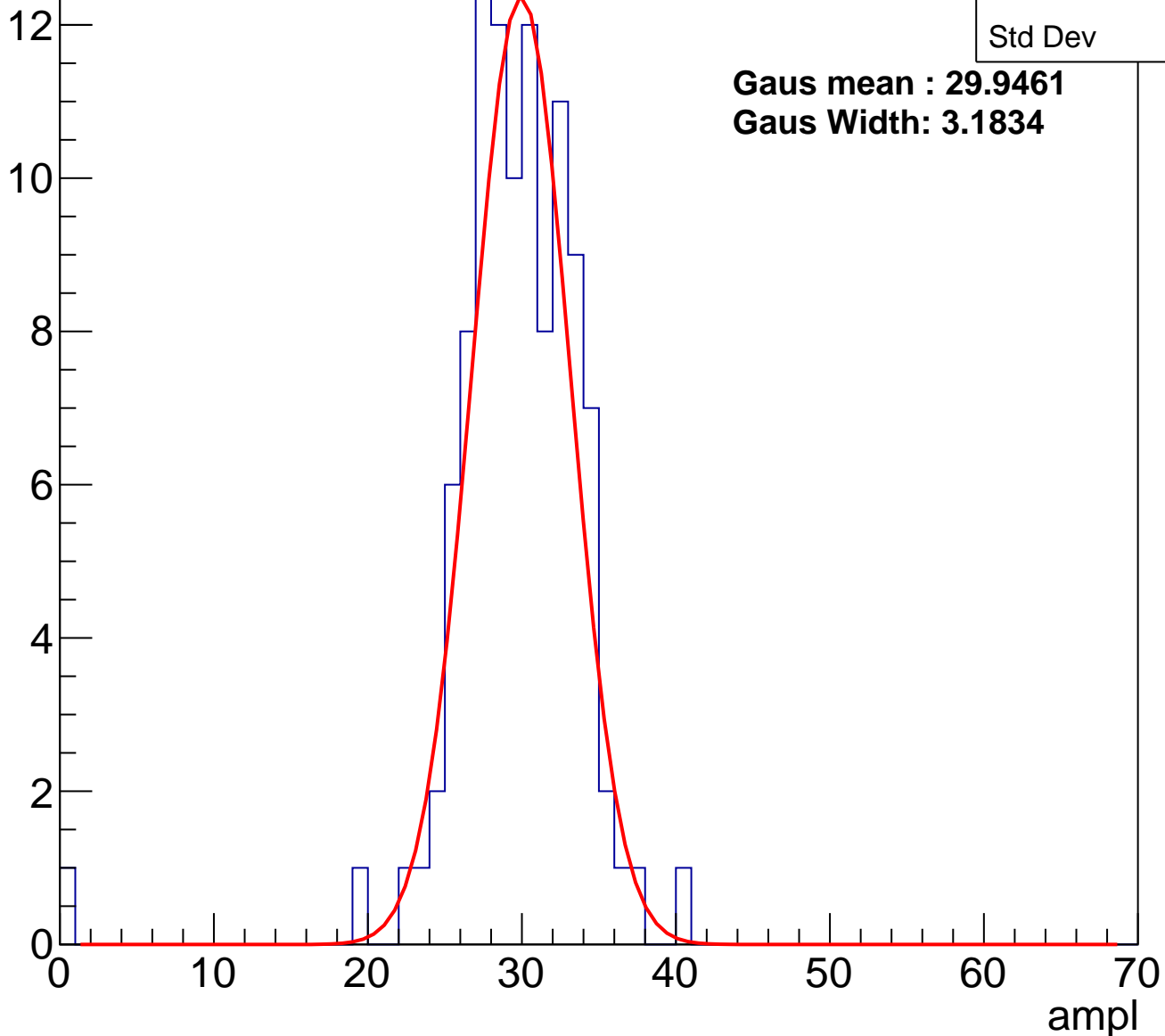
calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	29.21
Std Dev	4.4

Gaus mean : 29.9461

Gaus Width: 3.1834

Entry



B0L101S, U19-ch86, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	36.15
Std Dev	3.543

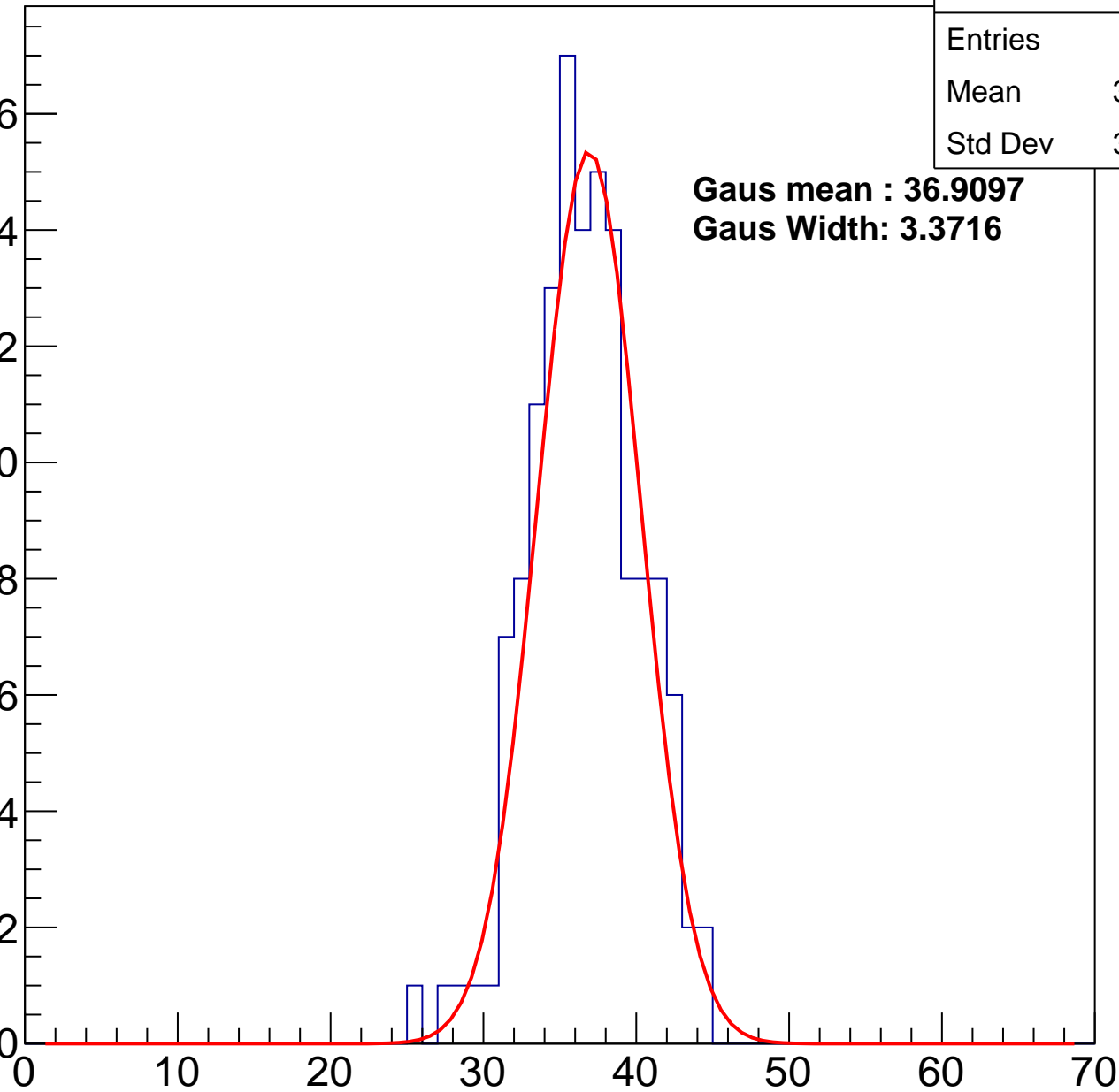
Gaus mean : 36.9097

Gaus Width: 3.3716

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

144

Mean

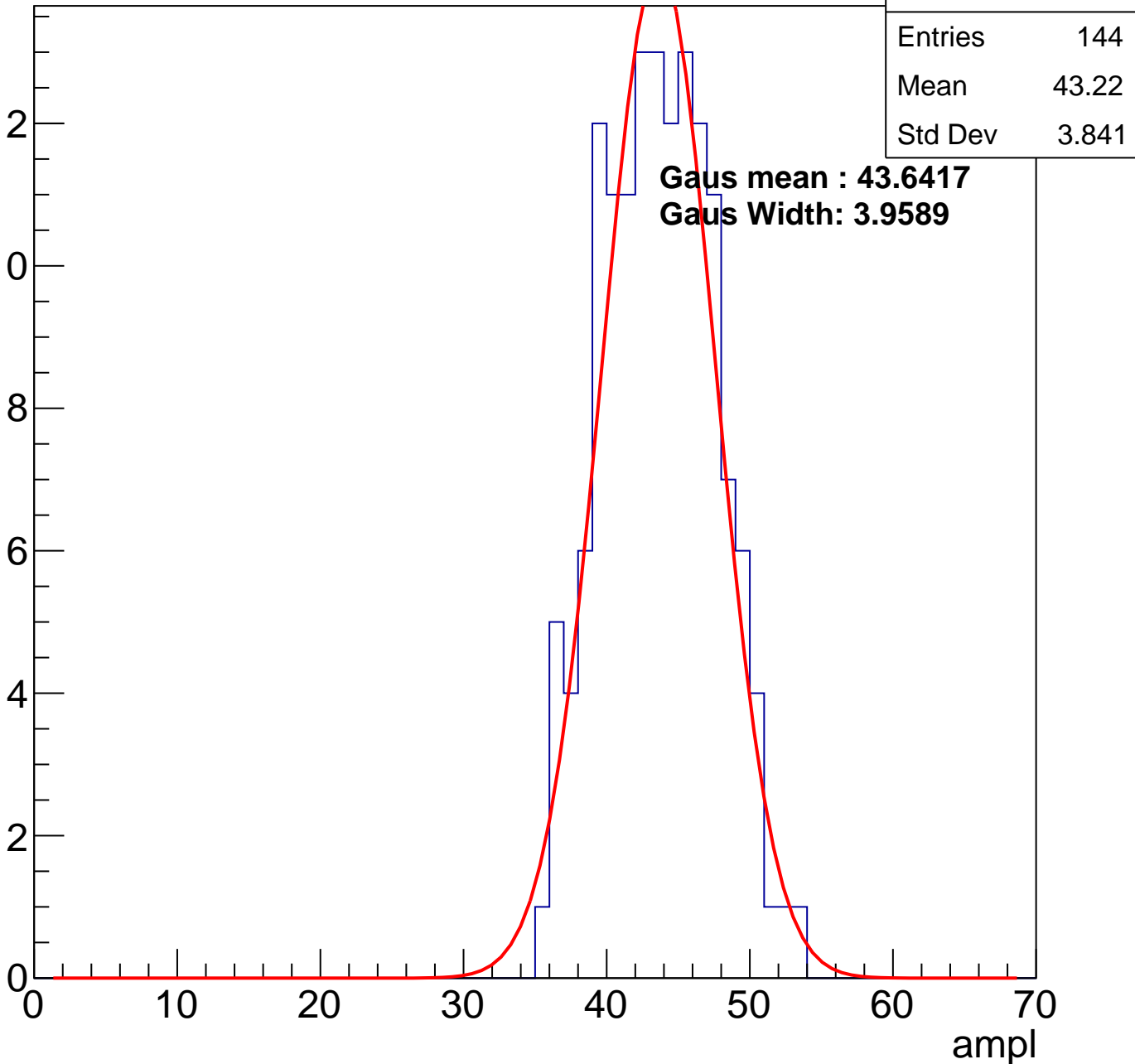
43.22

Std Dev

3.841

Gaus mean : 43.6417

Gaus Width: 3.9589



B0L101S, U19-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

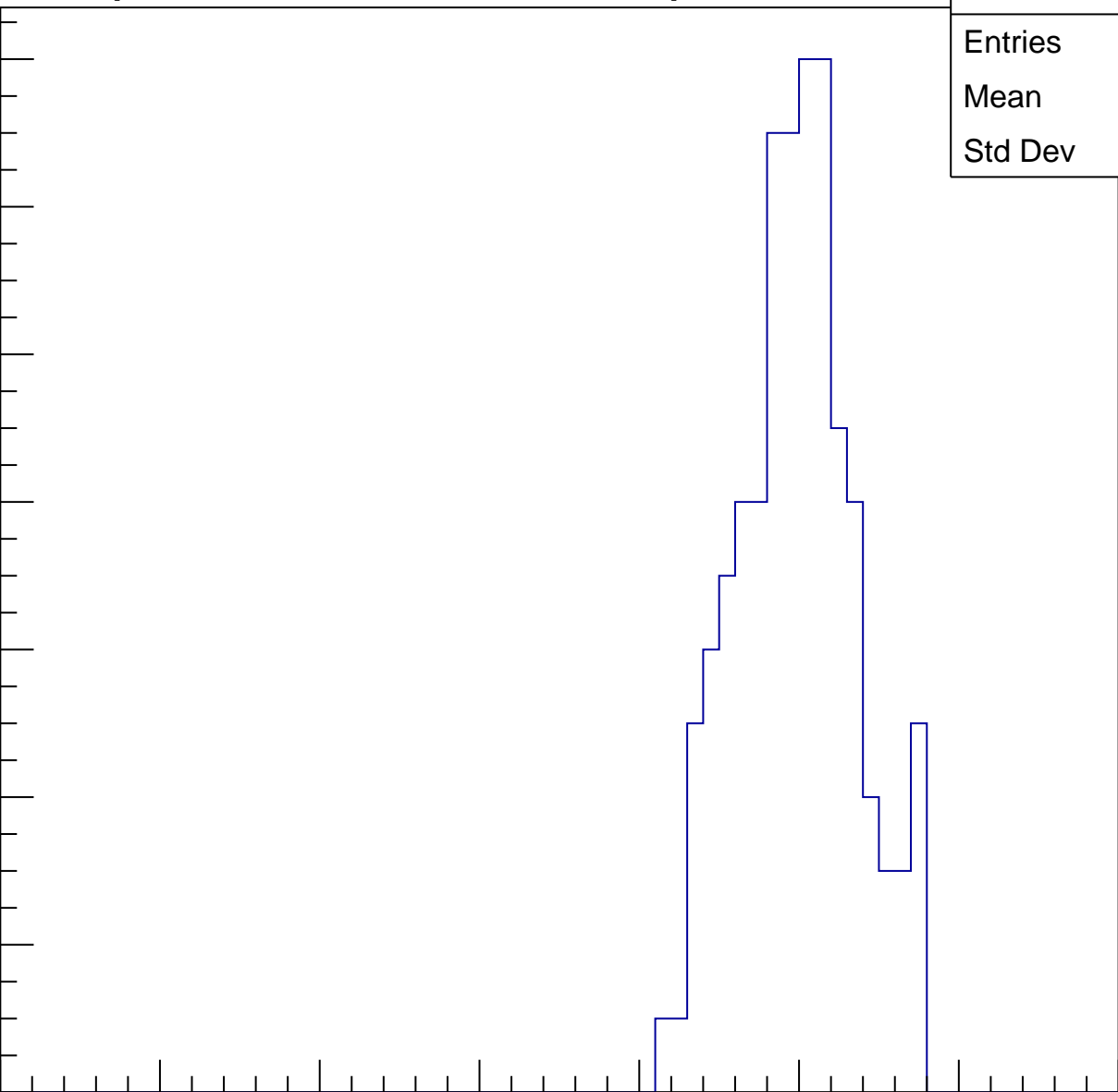
Entries	122
Mean	49.36
Std Dev	3.633

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	80
Mean	54.73
Std Dev	2.872

Entry

12

10

8

6

4

2

0

0

10

20

30

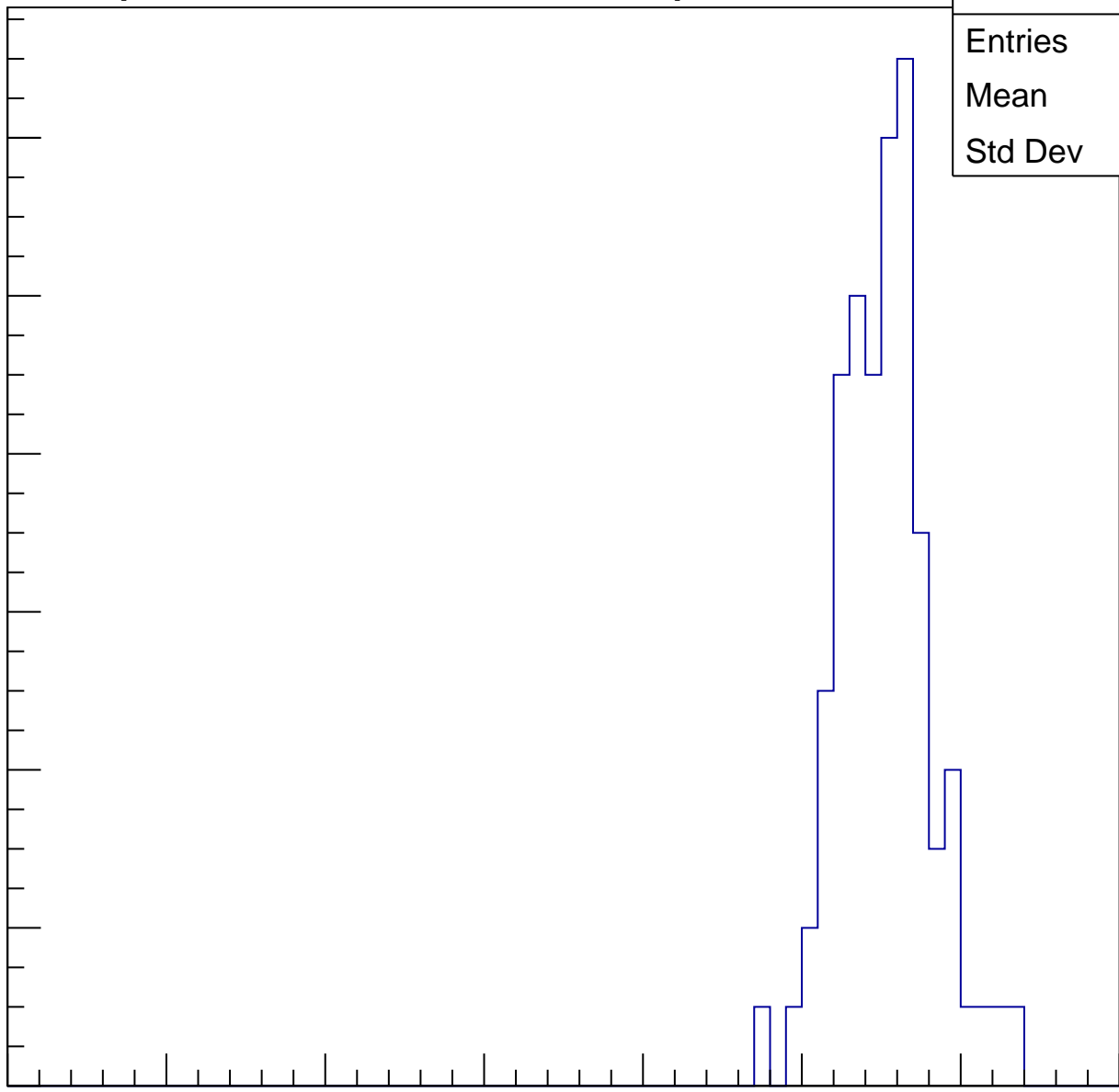
40

50

60

70

ampl



B0L101S, U19-ch86, adc5

calib_packv5_042523_0143.root, FC#1, port C1

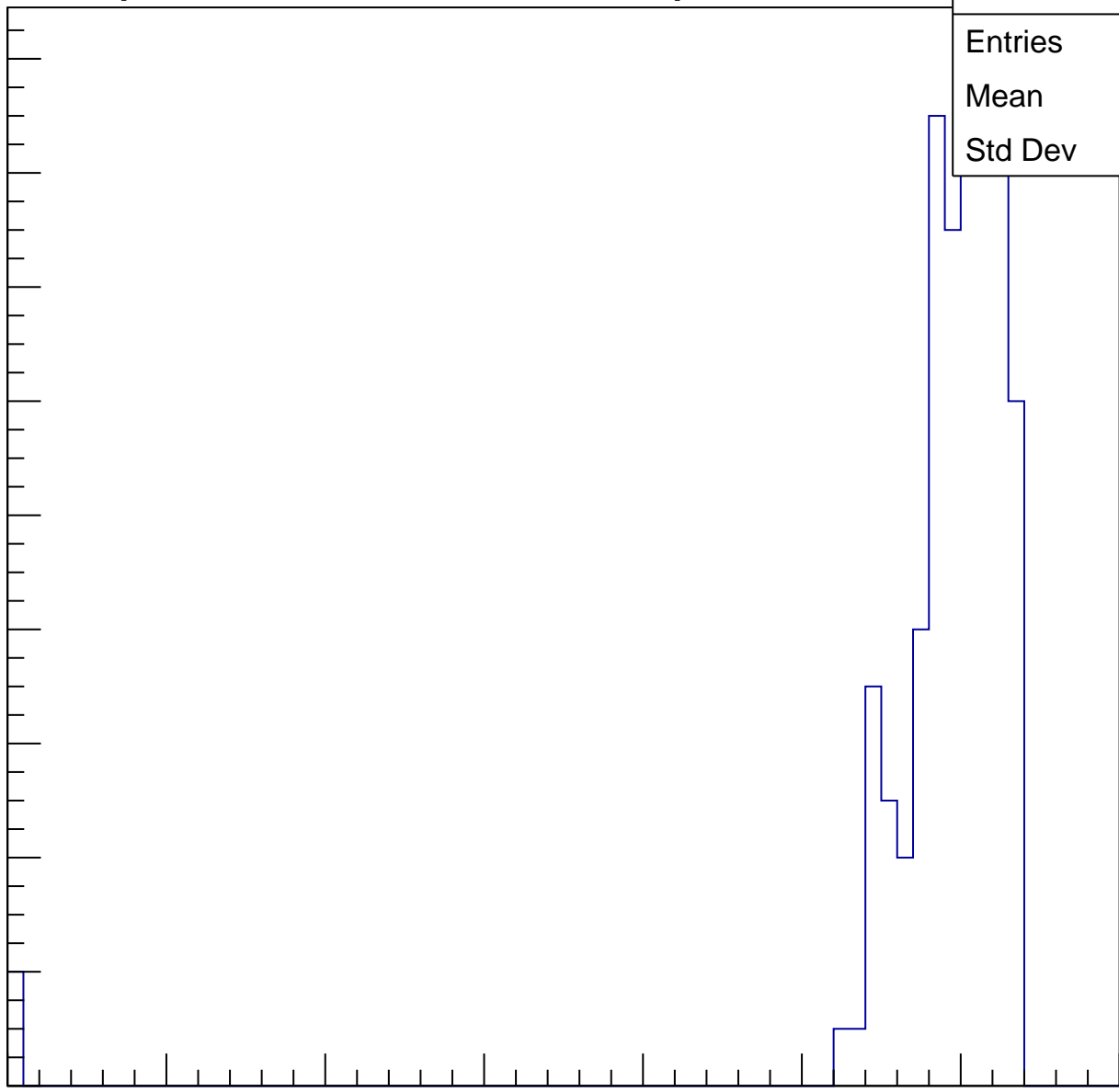
Entries	123
Mean	58.33
Std Dev	7.939

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

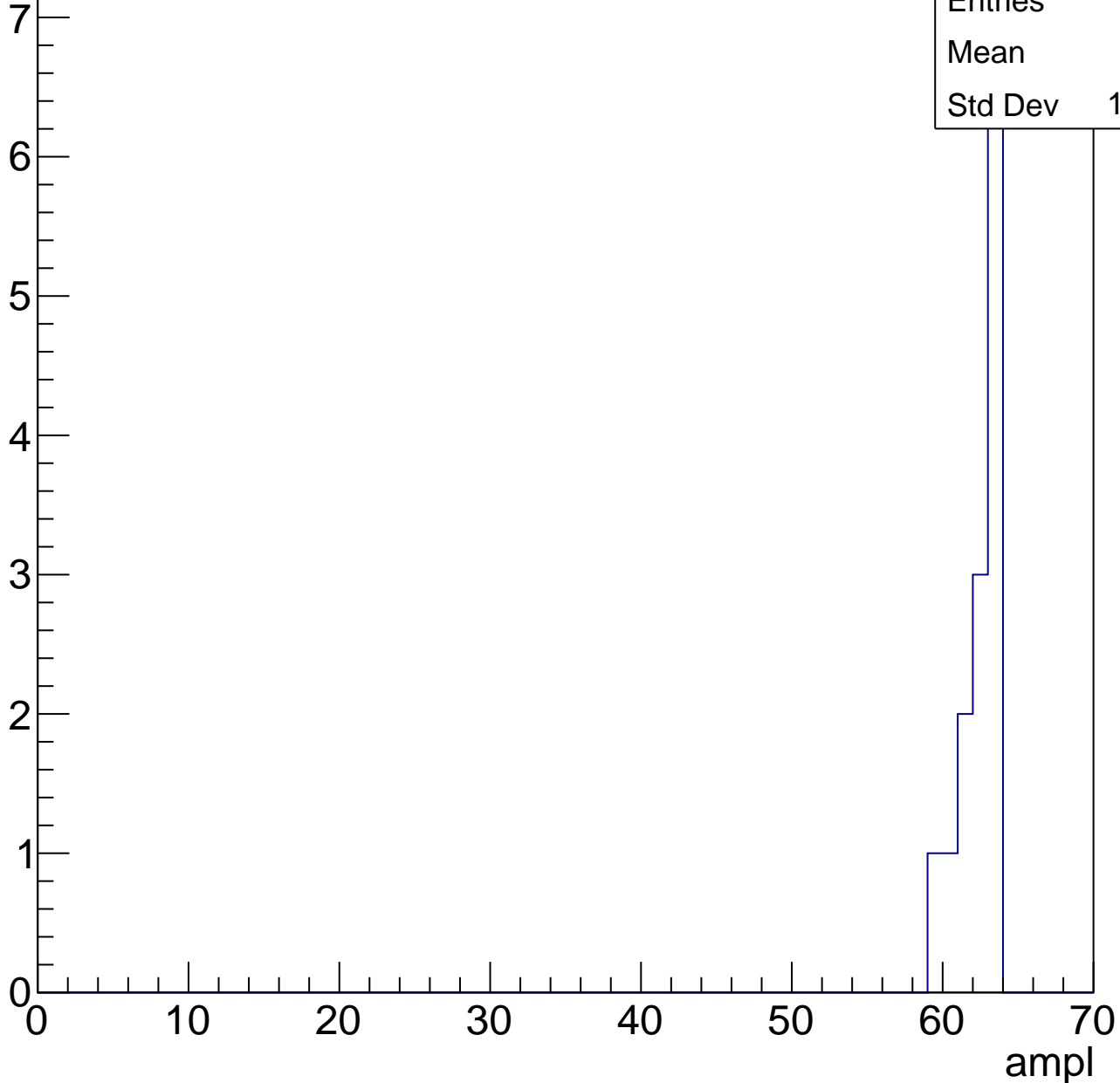
ampl



B0L101S, U19-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch87, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.42
Std Dev	4.434

Gaus mean : 29.9613

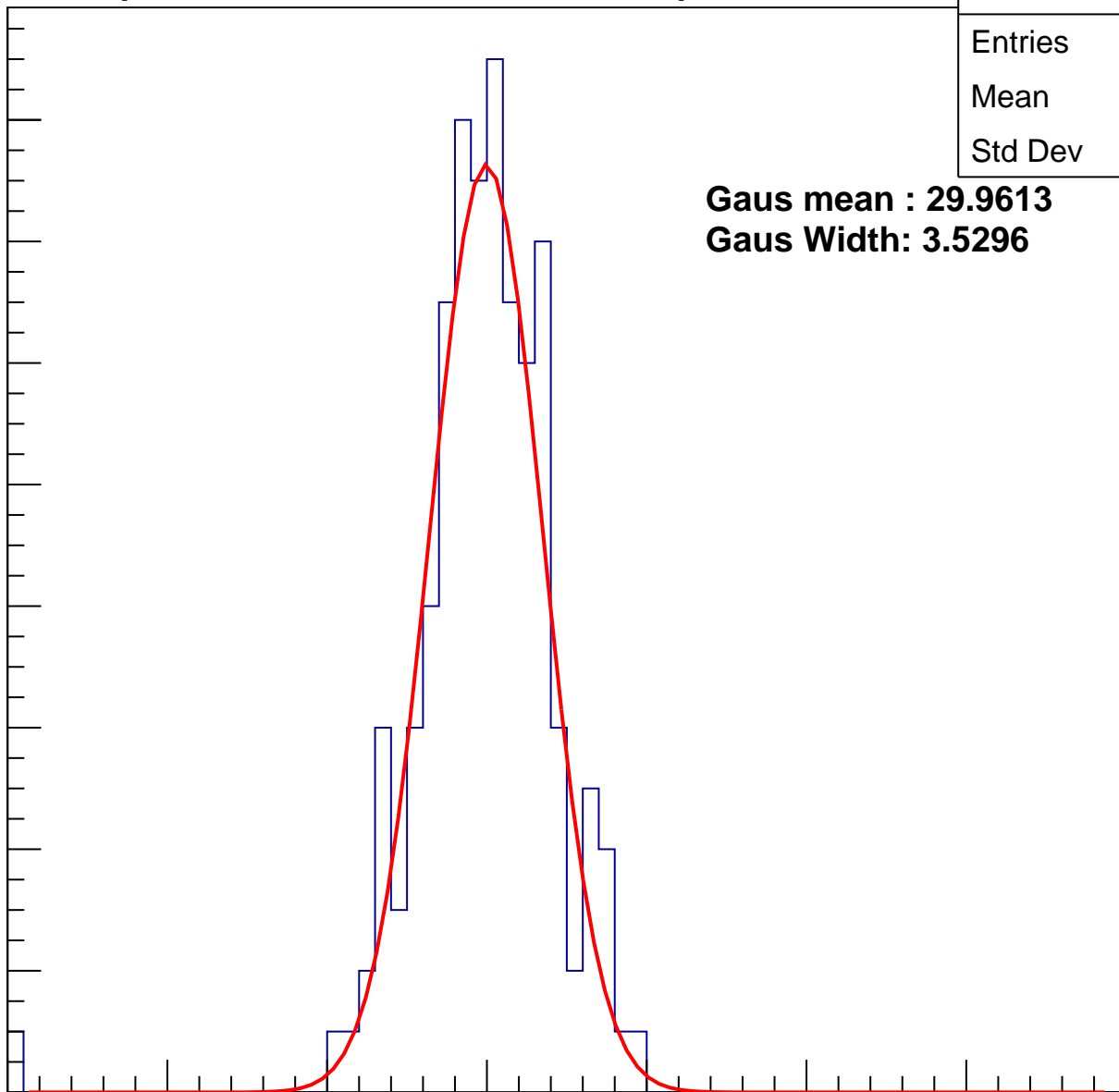
Gaus Width: 3.5296

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



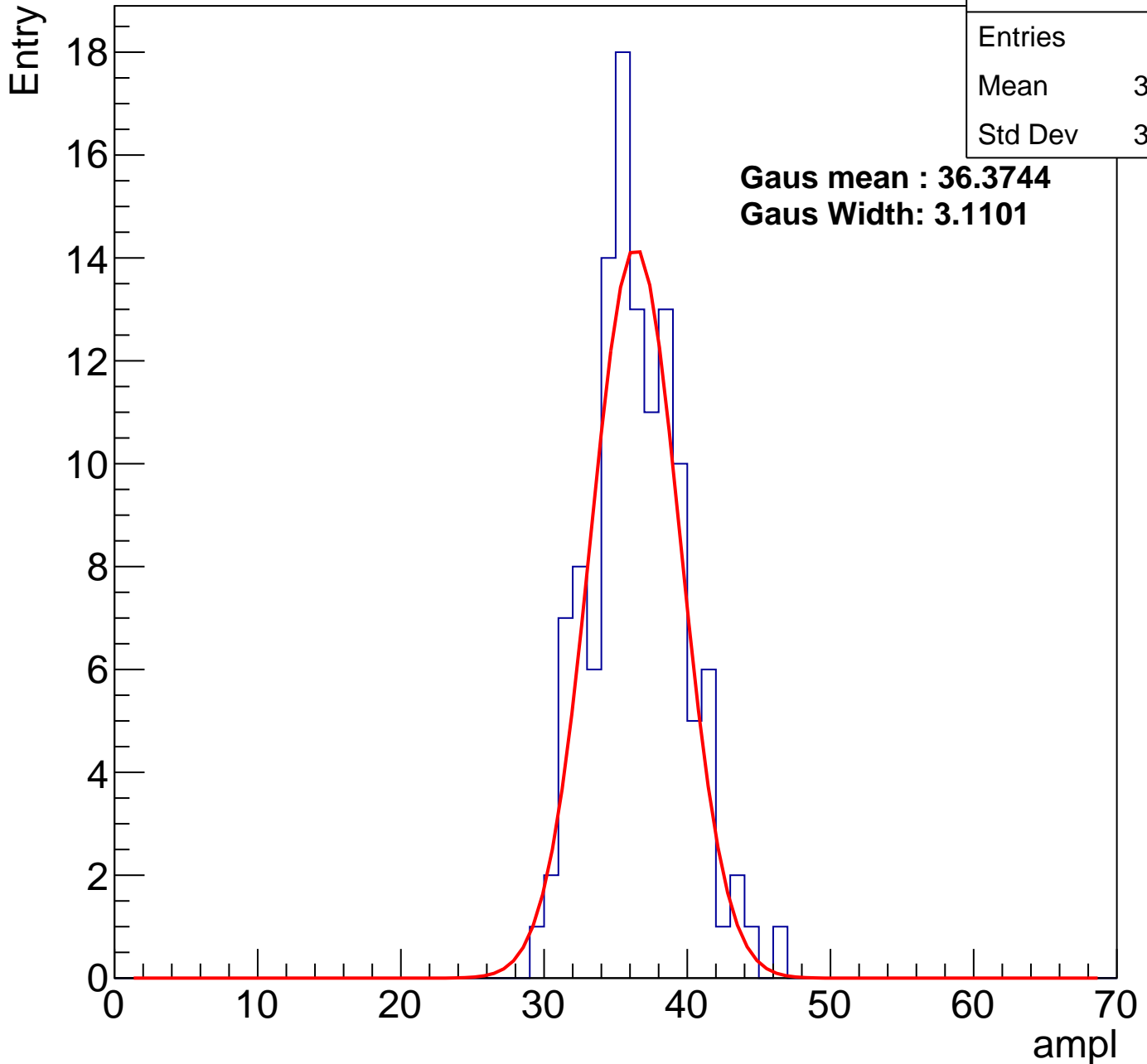
B0L101S, U19-ch87, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	36.04
Std Dev	3.219

Gaus mean : 36.3744

Gaus Width: 3.1101



B0L101S, U19-ch87, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	43.06
Std Dev	3.45

Gaus mean : 43.2426

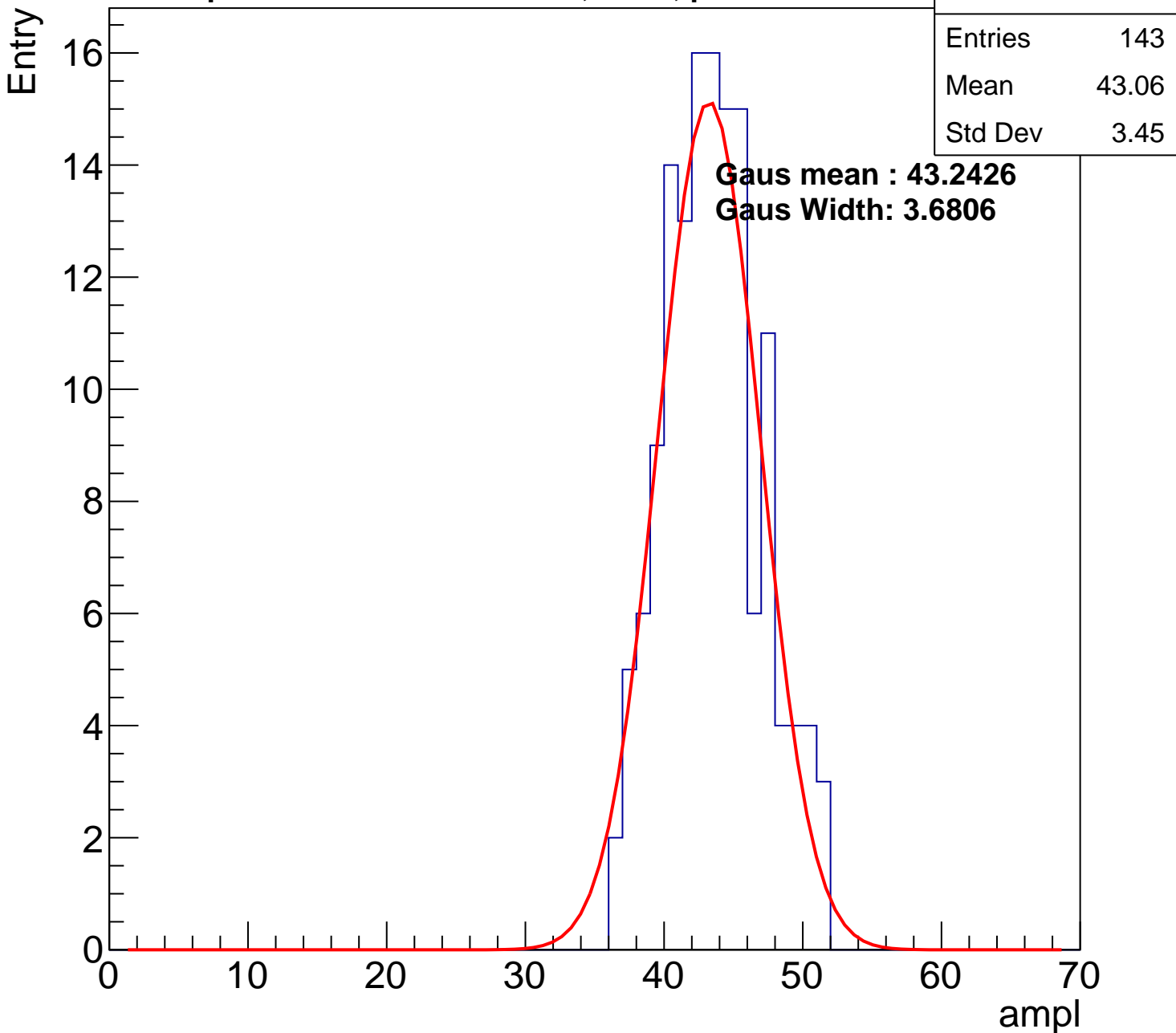
Gaus Width: 3.6806

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch87, adc3

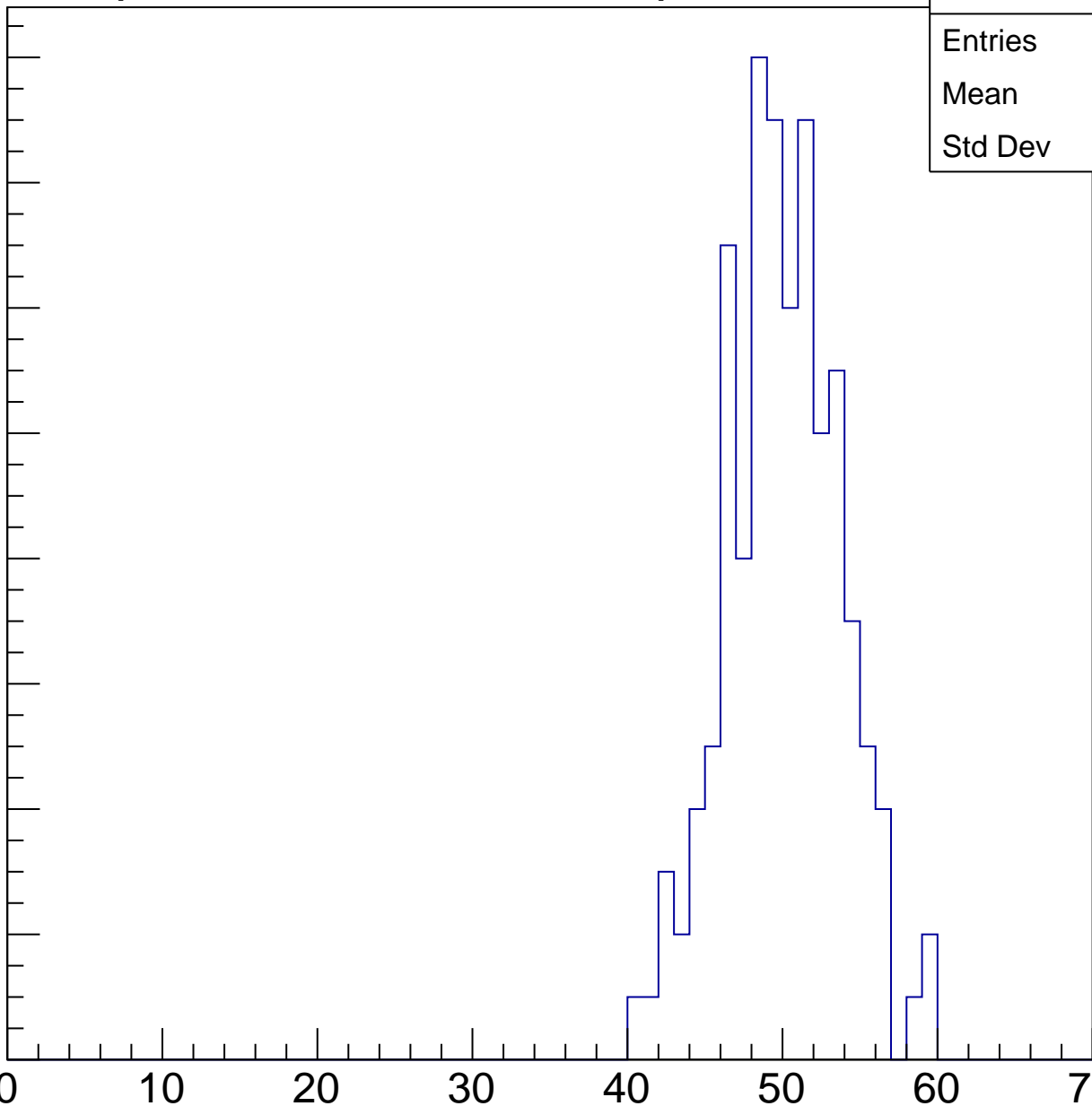
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	49.57
Std Dev	3.687

ampl



B0L101S, U19-ch87, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.9
Std Dev	3.062

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

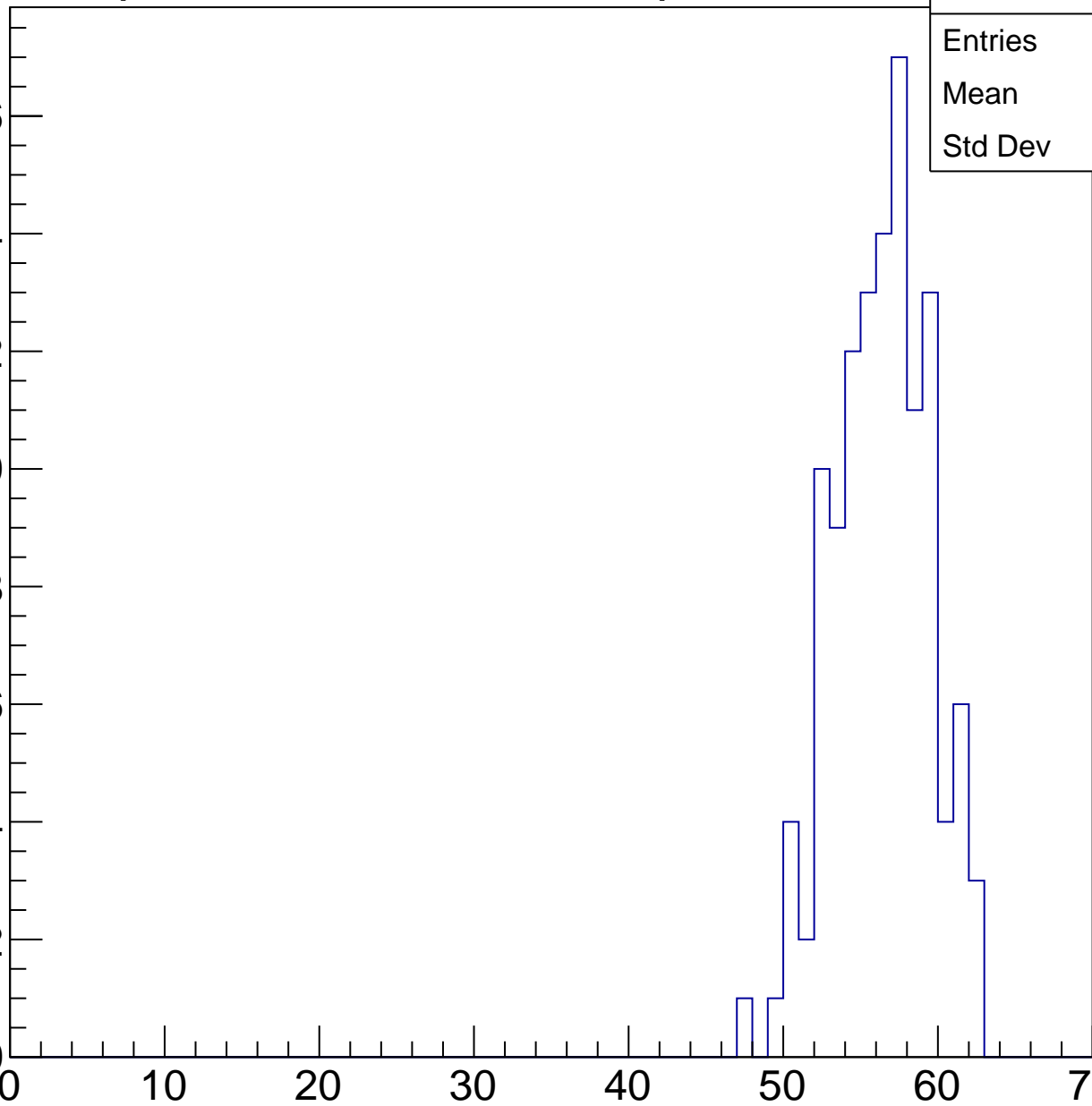
40

50

60

70

ampl



B0L101S, U19-ch87, adc5

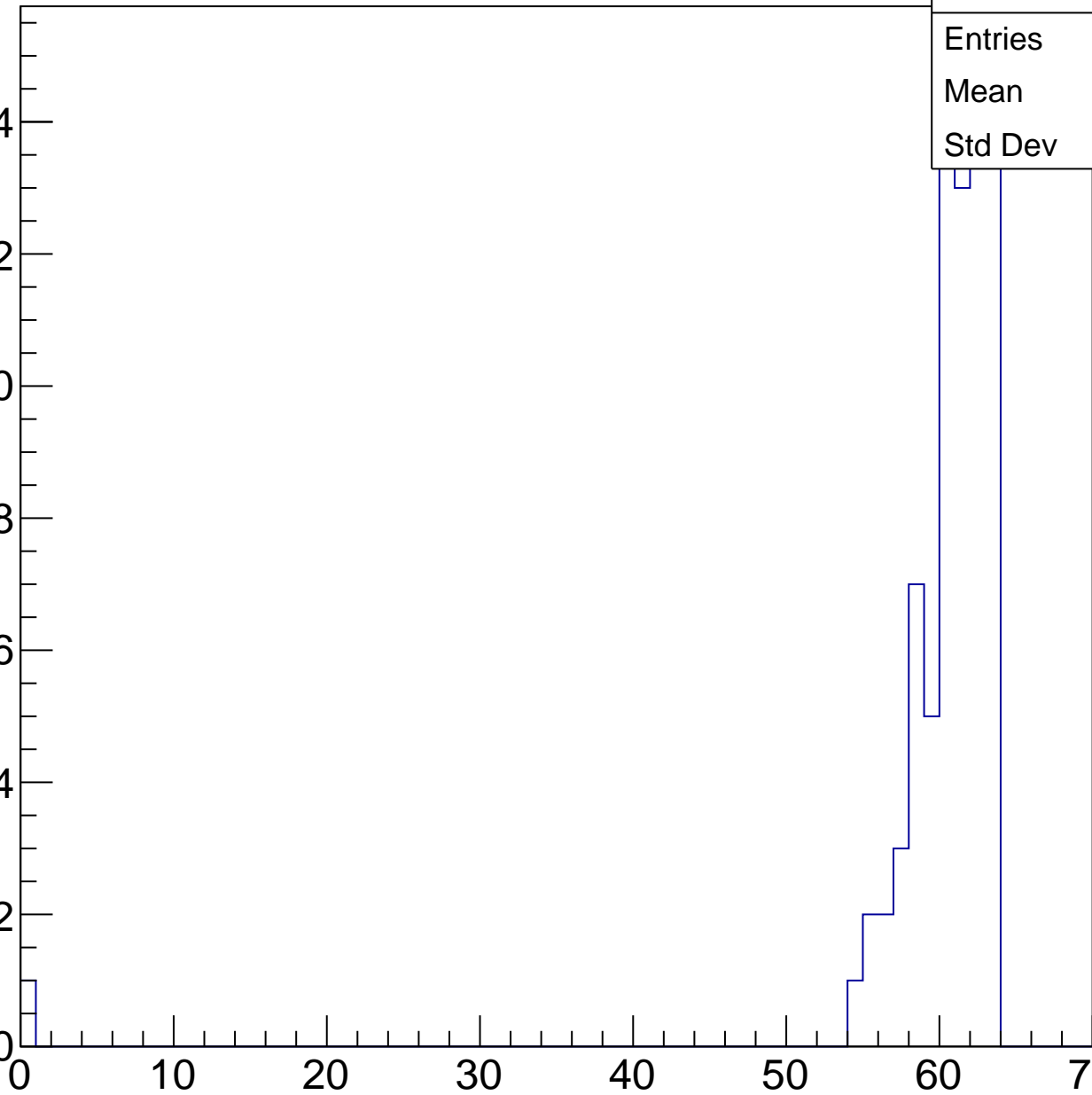
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	59.69
Std Dev	7.142

ampl

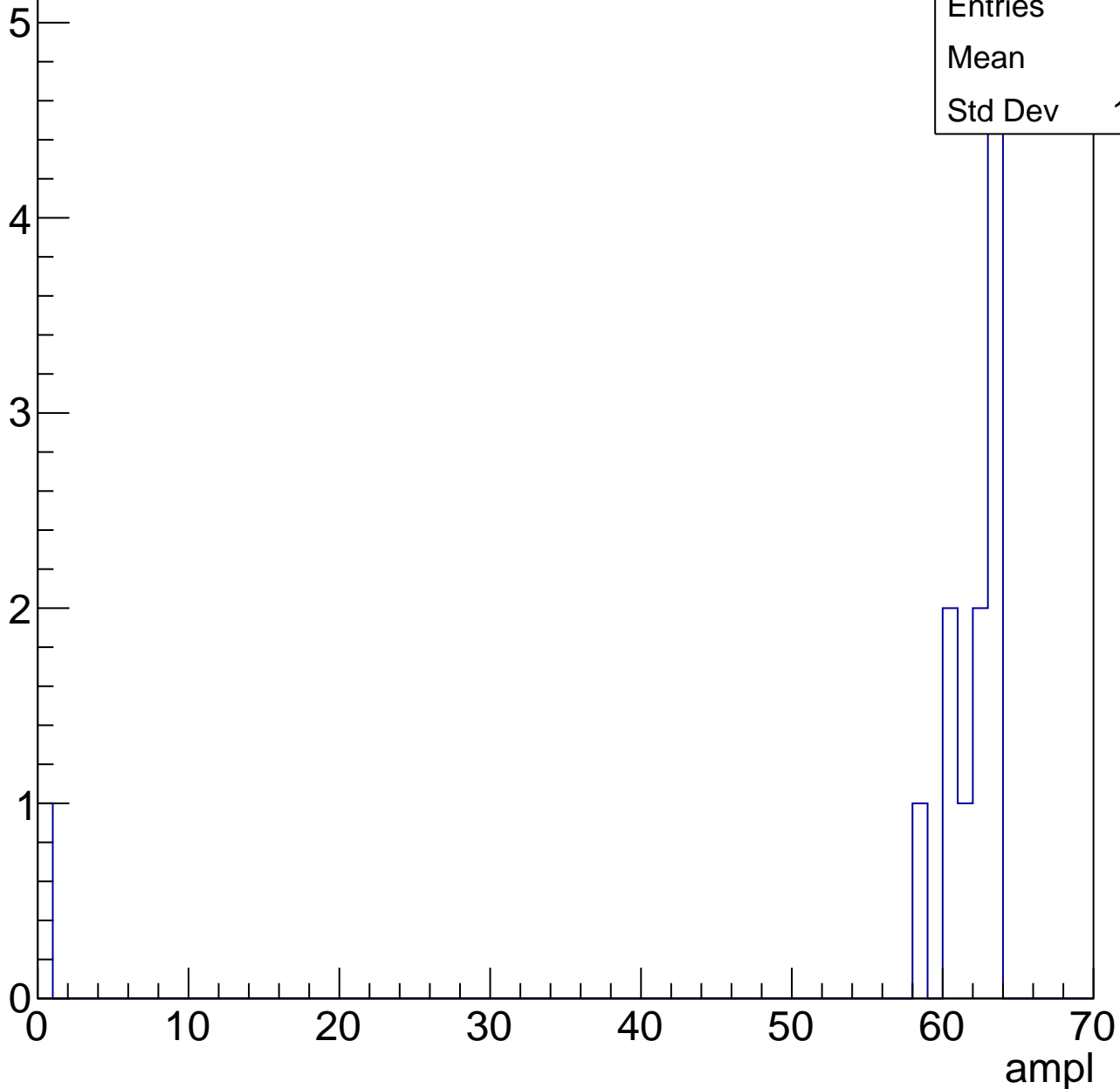


B0L101S, U19-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.5
Std Dev	17.11



B0L101S, U19-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch88, adc0

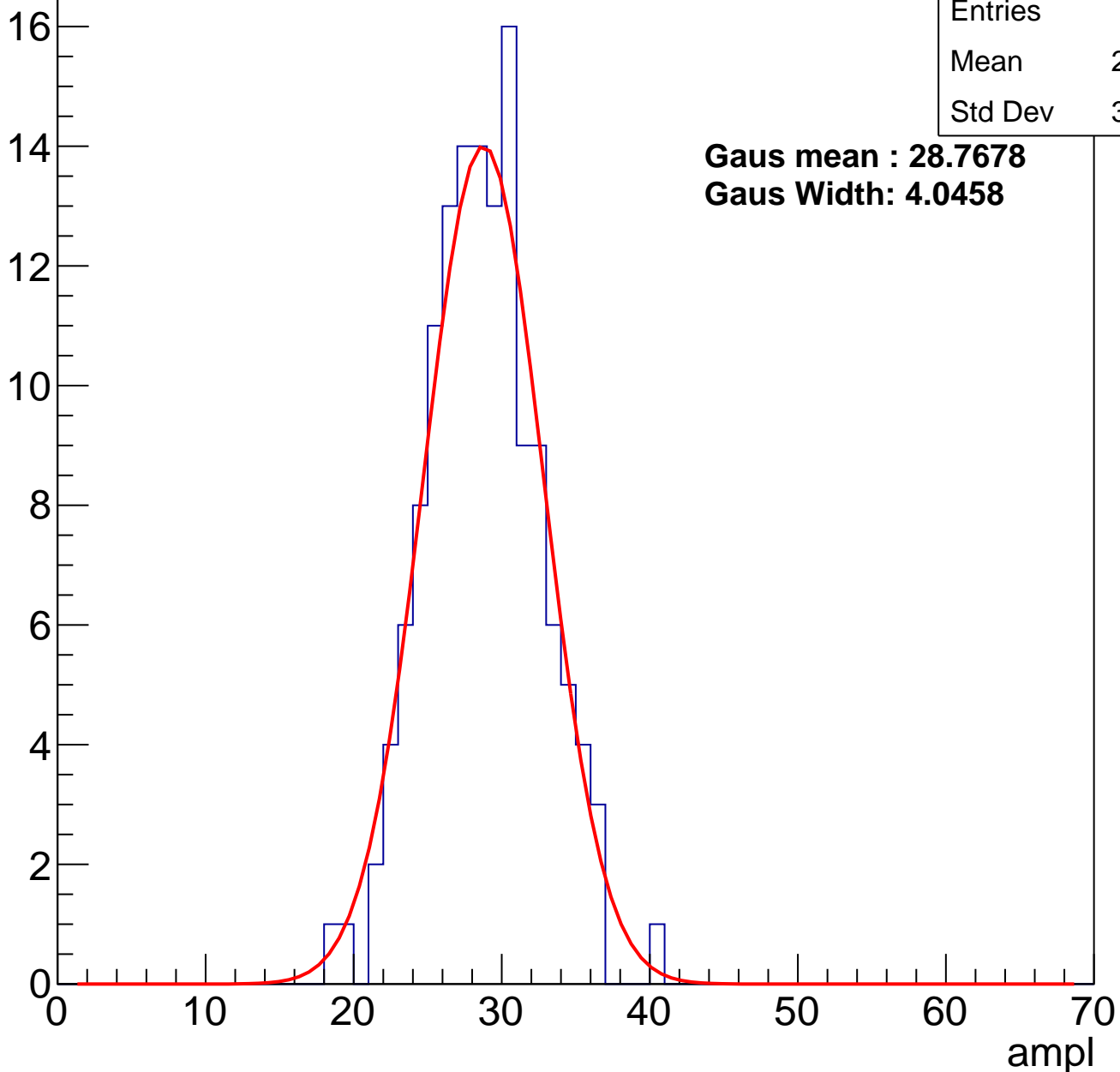
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	28.29
Std Dev	3.797

Gaus mean : 28.7678

Gaus Width: 4.0458

Entry

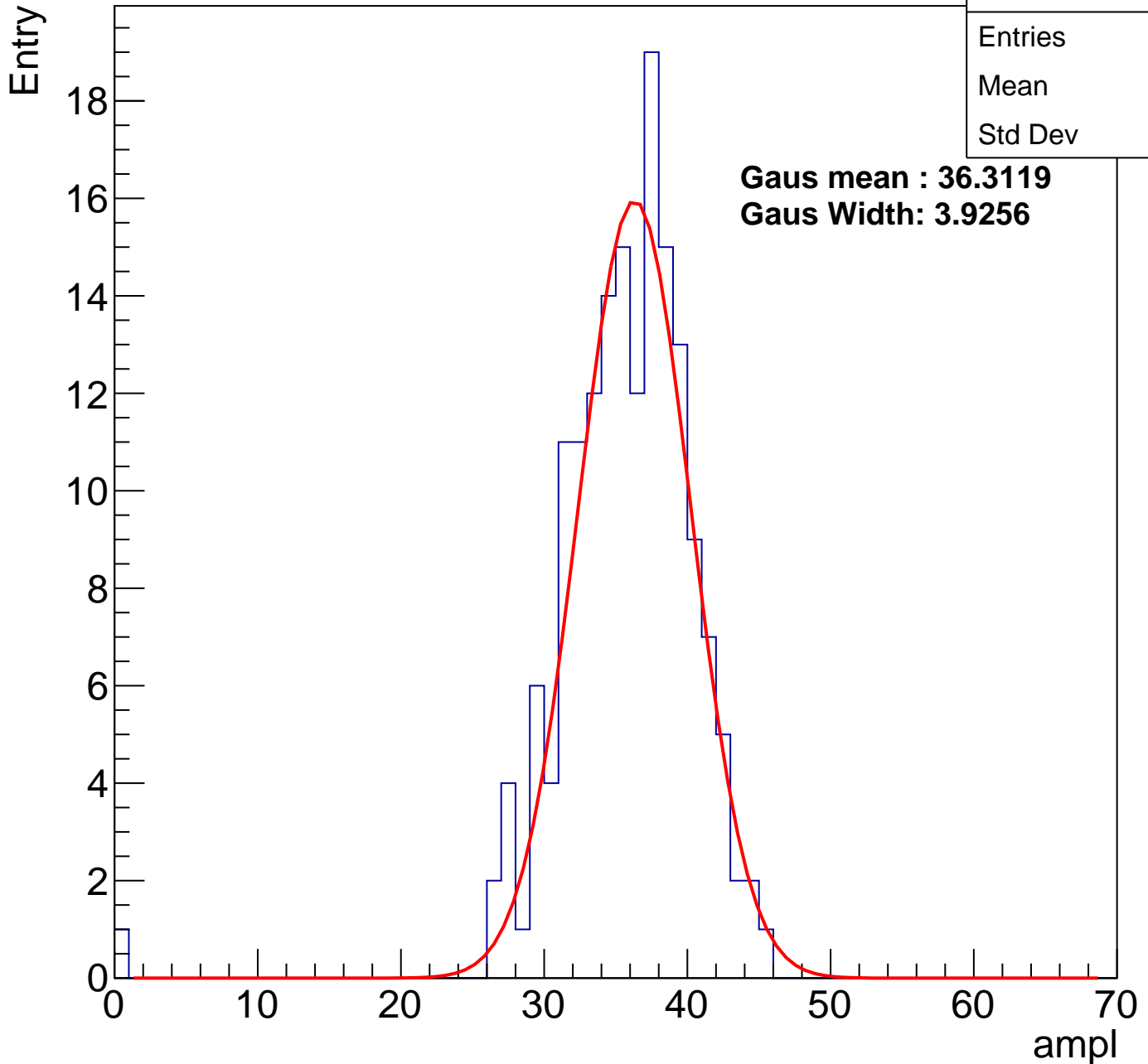


B0L101S, U19-ch88, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	35.3
Std Dev	4.83

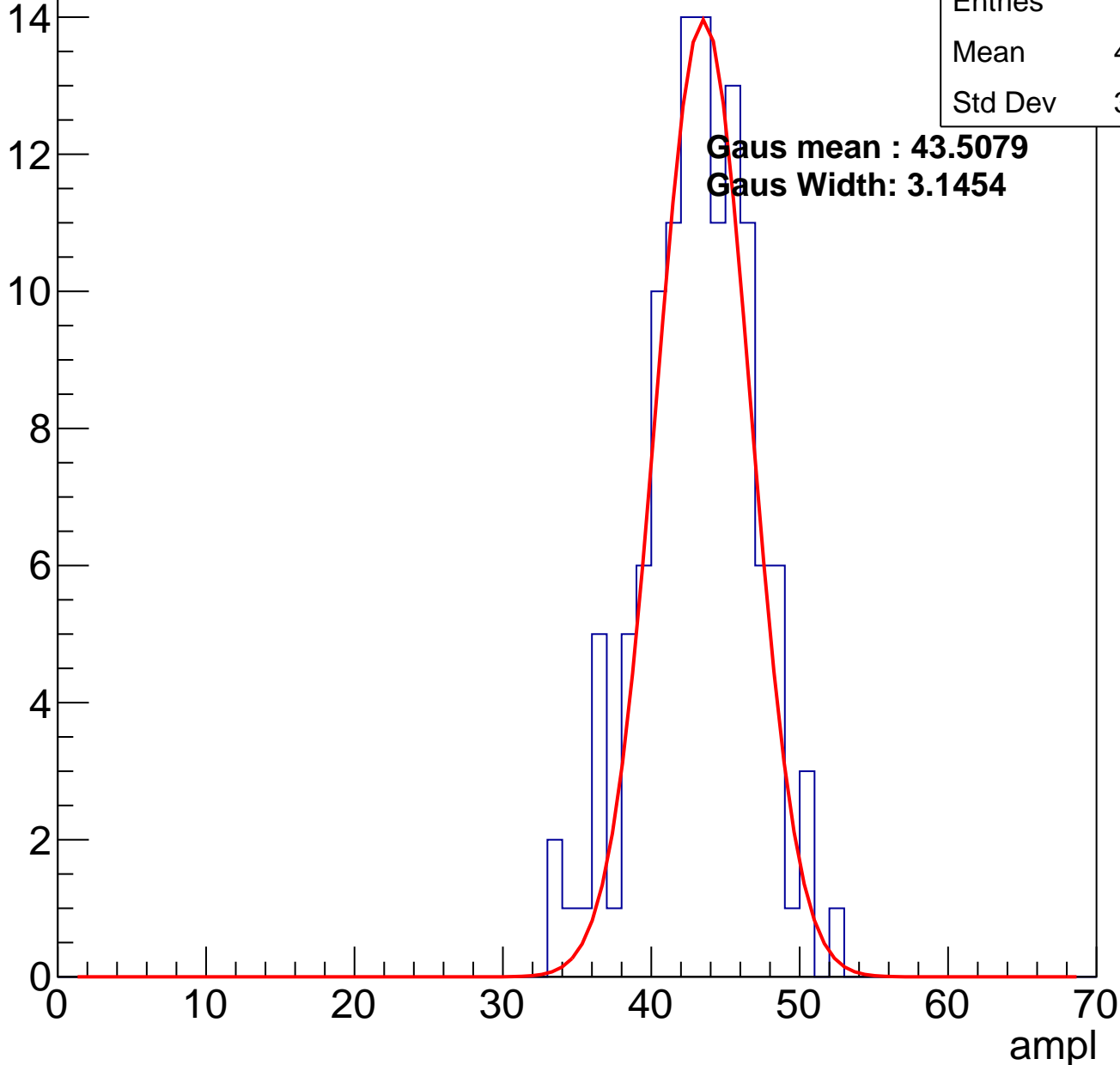
Gaus mean : 36.3119
Gaus Width: 3.9256



B0L101S, U19-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

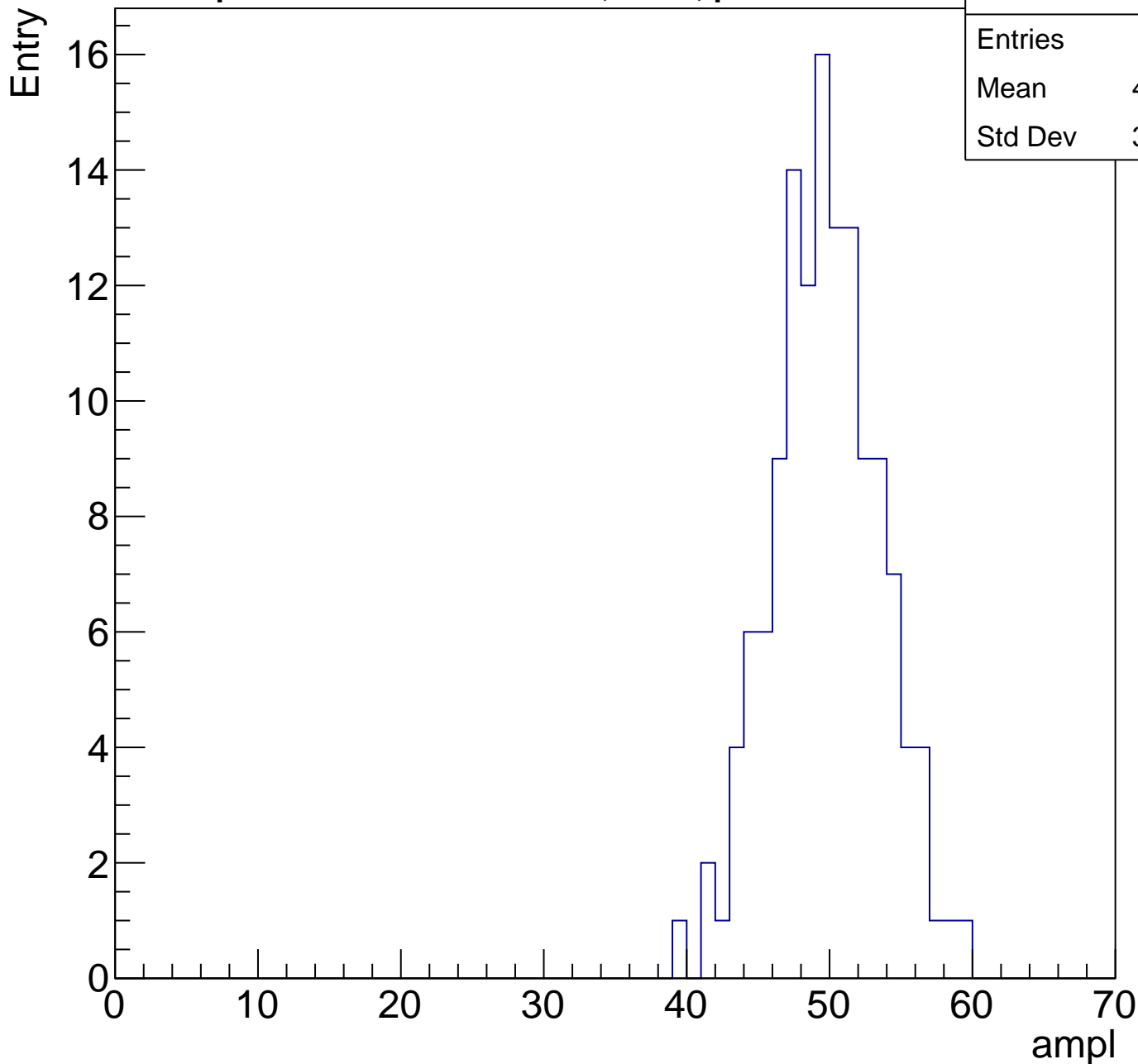
Entry



B0L101S, U19-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	49.29
Std Dev	3.735



B0L101S, U19-ch88, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	55.71
Std Dev	3.502

Entry

14

12

10

8

6

4

2

0

0

10

20

30

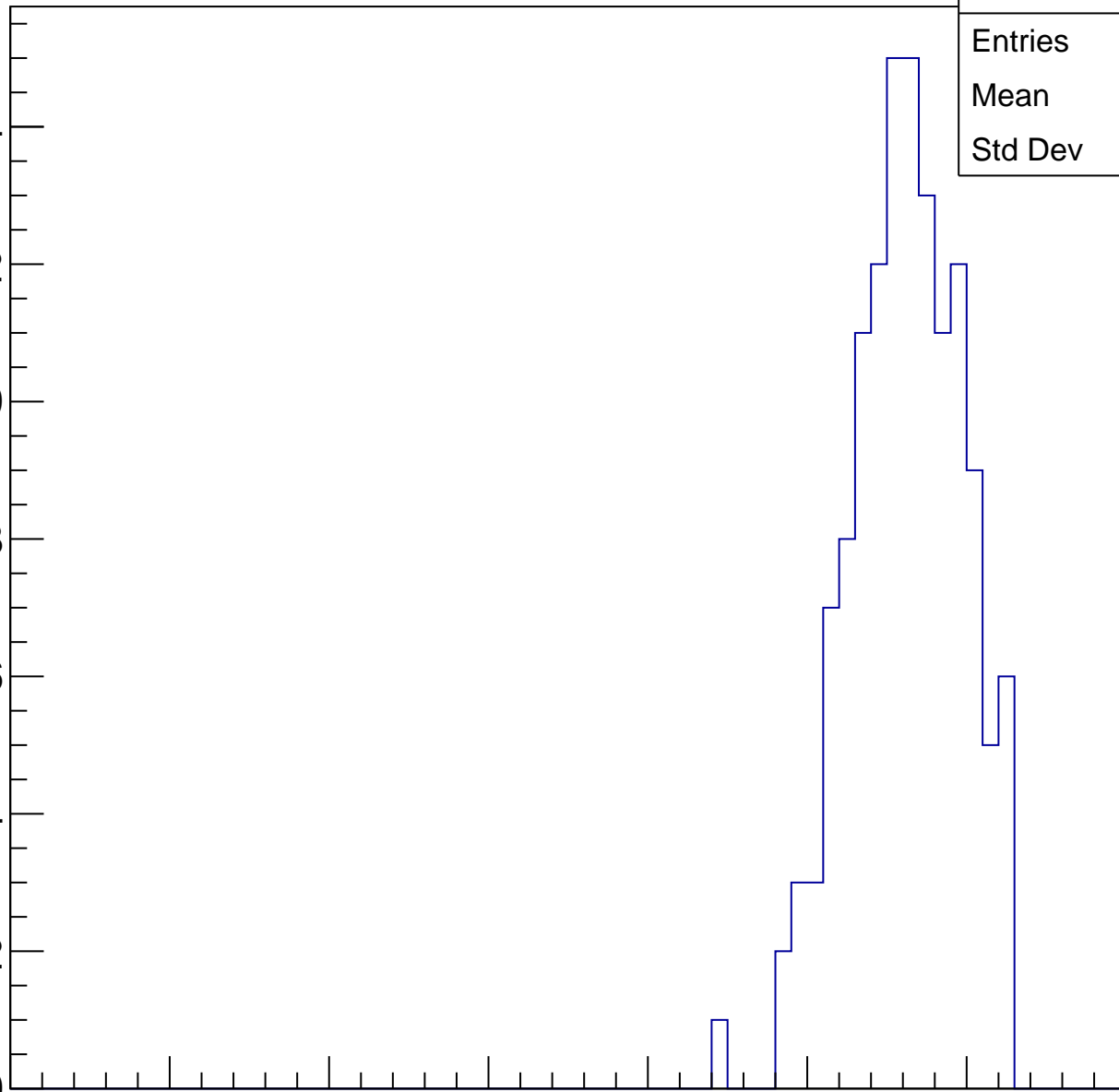
40

50

60

70

ampl



B0L101S, U19-ch88, adc5

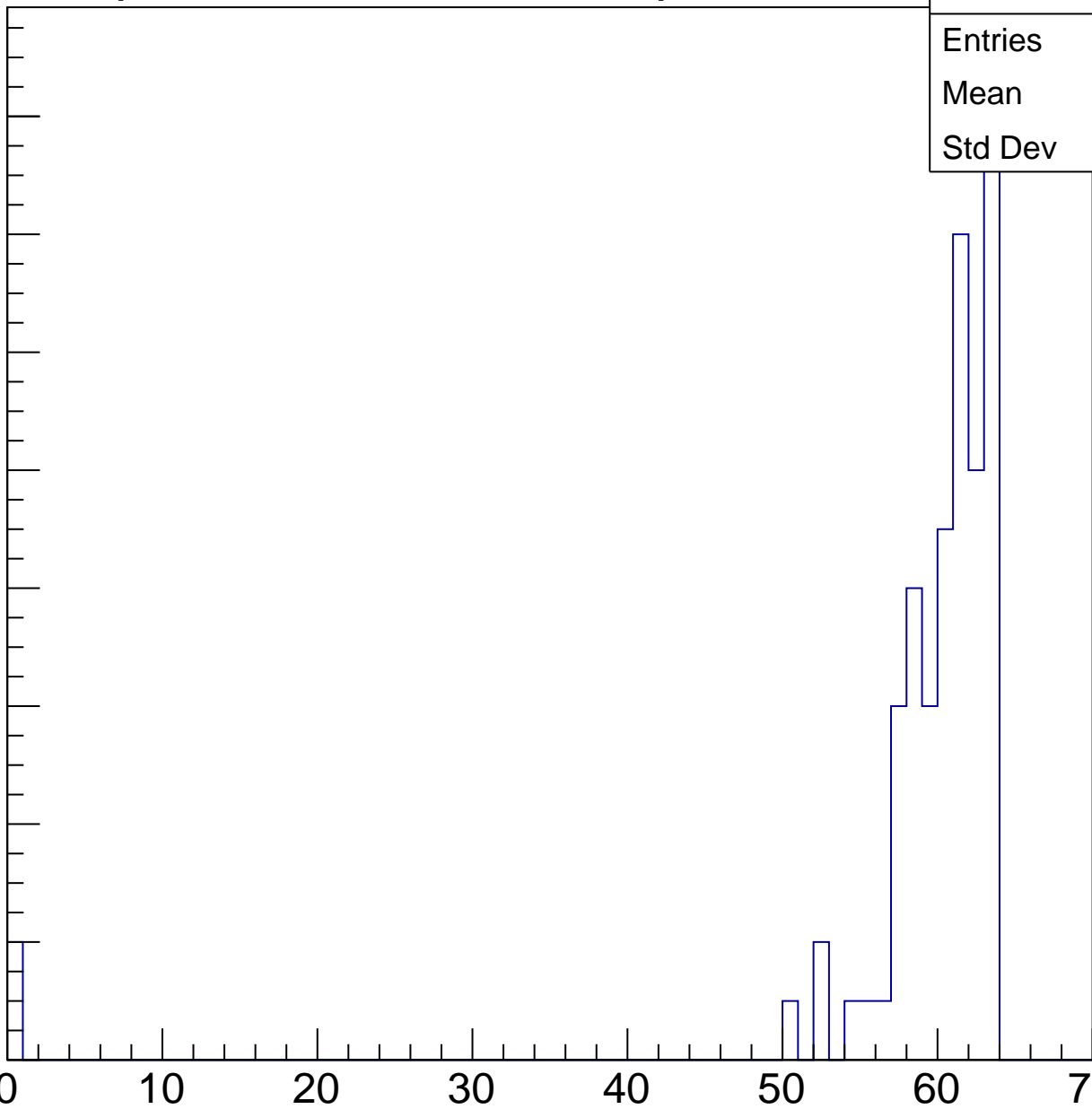
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	58.51
Std Dev	9.891

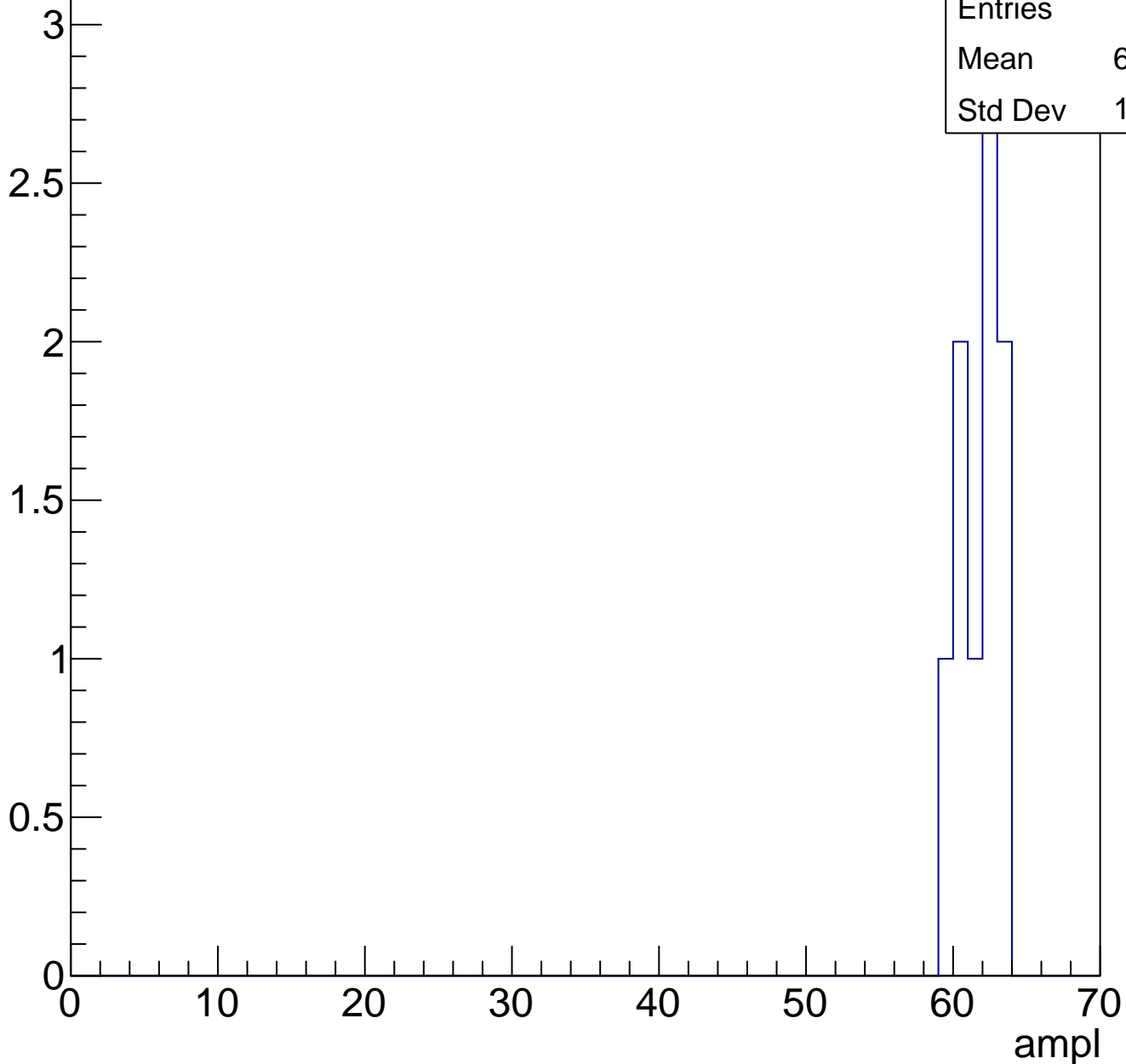
ampl



B0L101S, U19-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

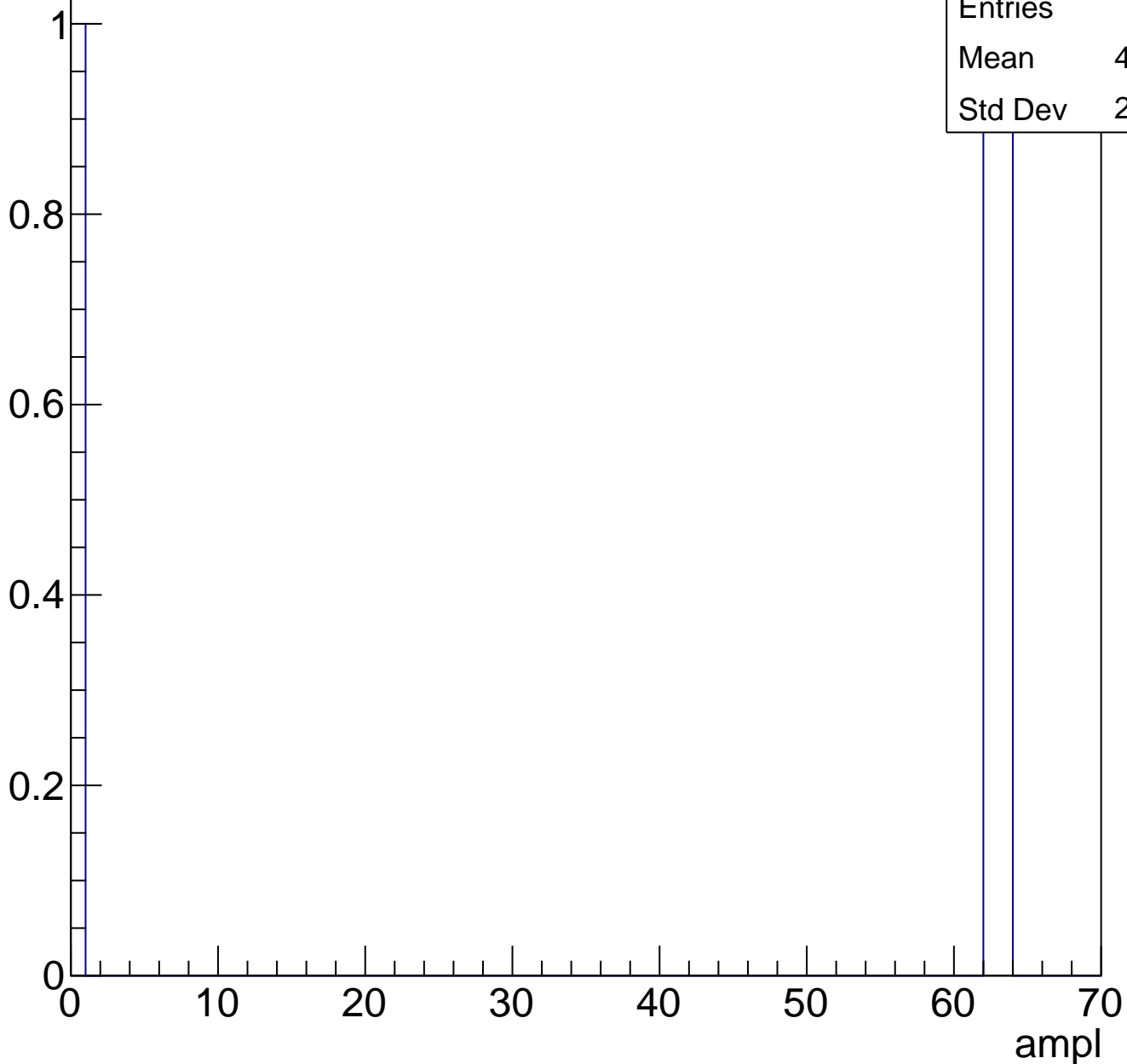
Entry



B0L101S, U19-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch89, adc0

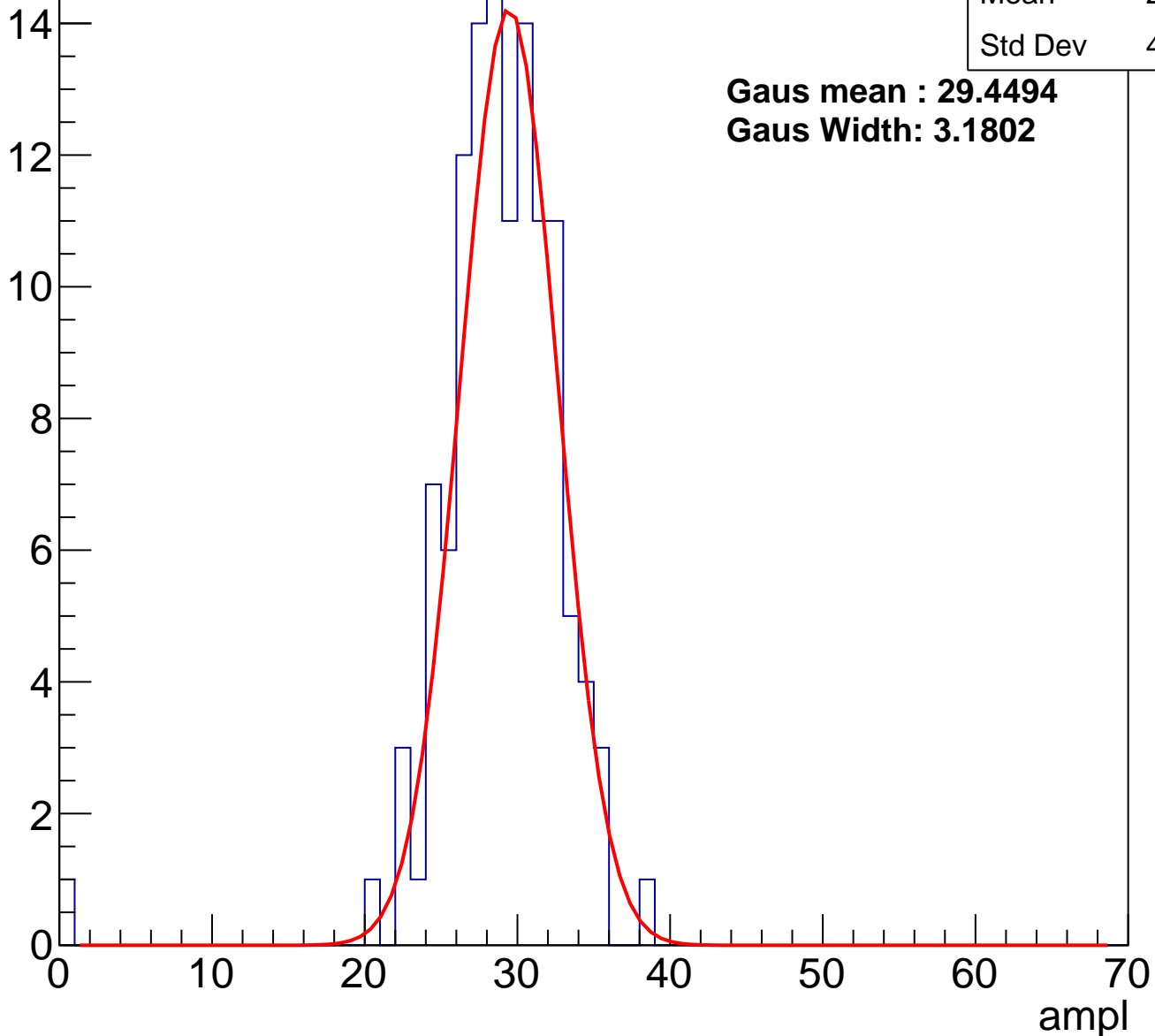
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	28.44
Std Dev	4.127

Gaus mean : 29.4494

Gaus Width: 3.1802

Entry



B0L101S, U19-ch89, adc1

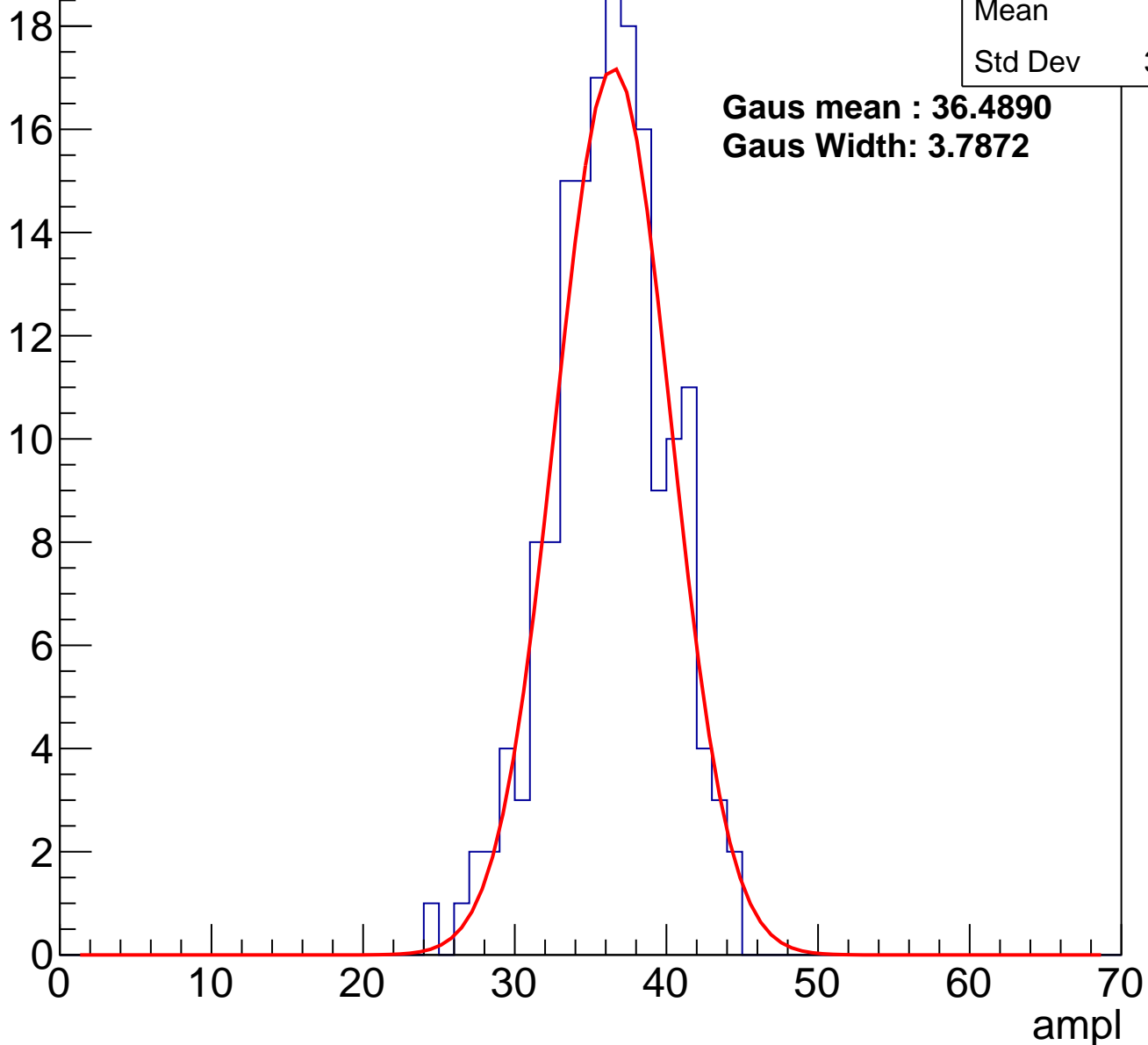
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	35.8
Std Dev	3.771

Gaus mean : 36.4890

Gaus Width: 3.7872

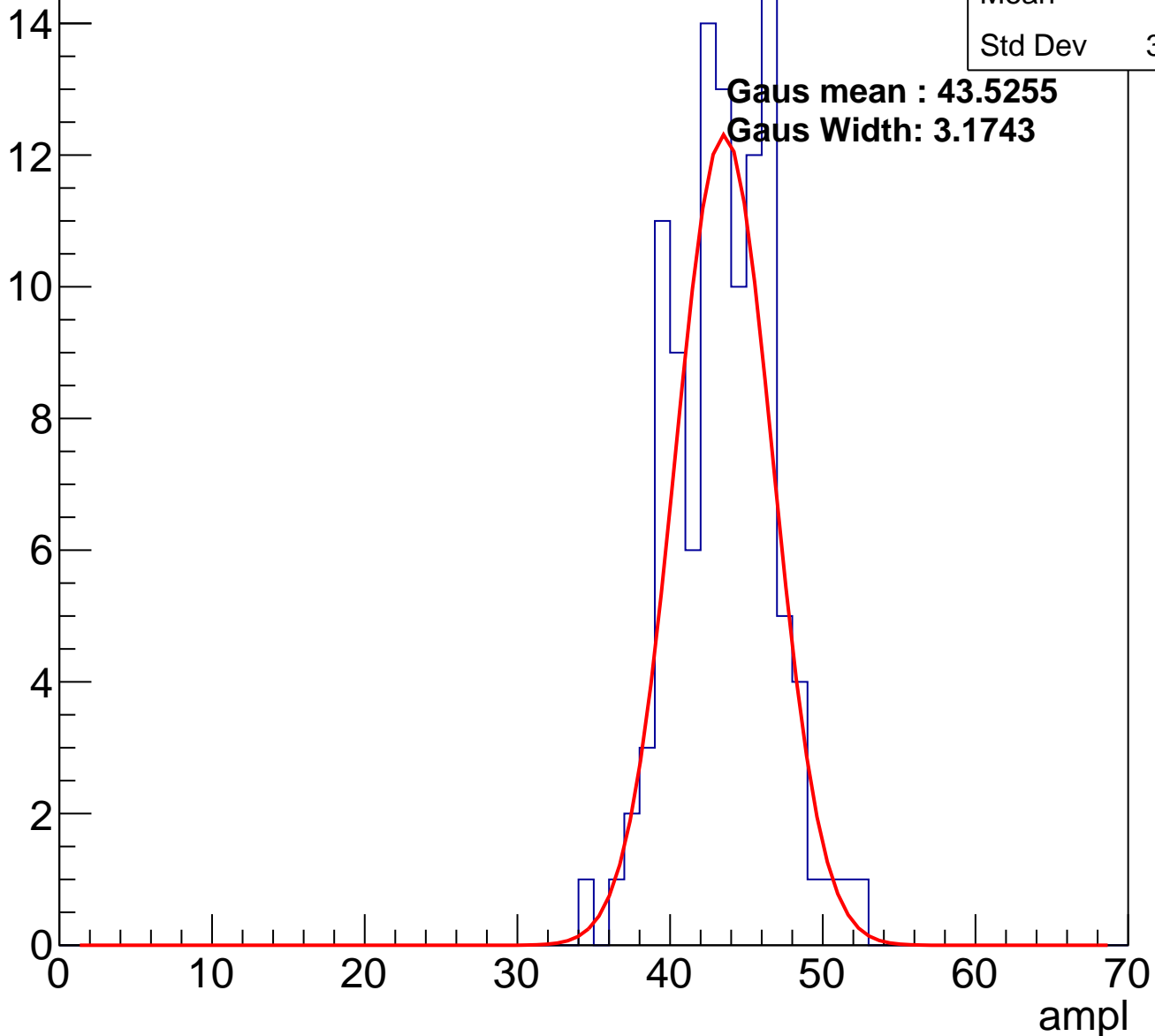
Entry



B0L101S, U19-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

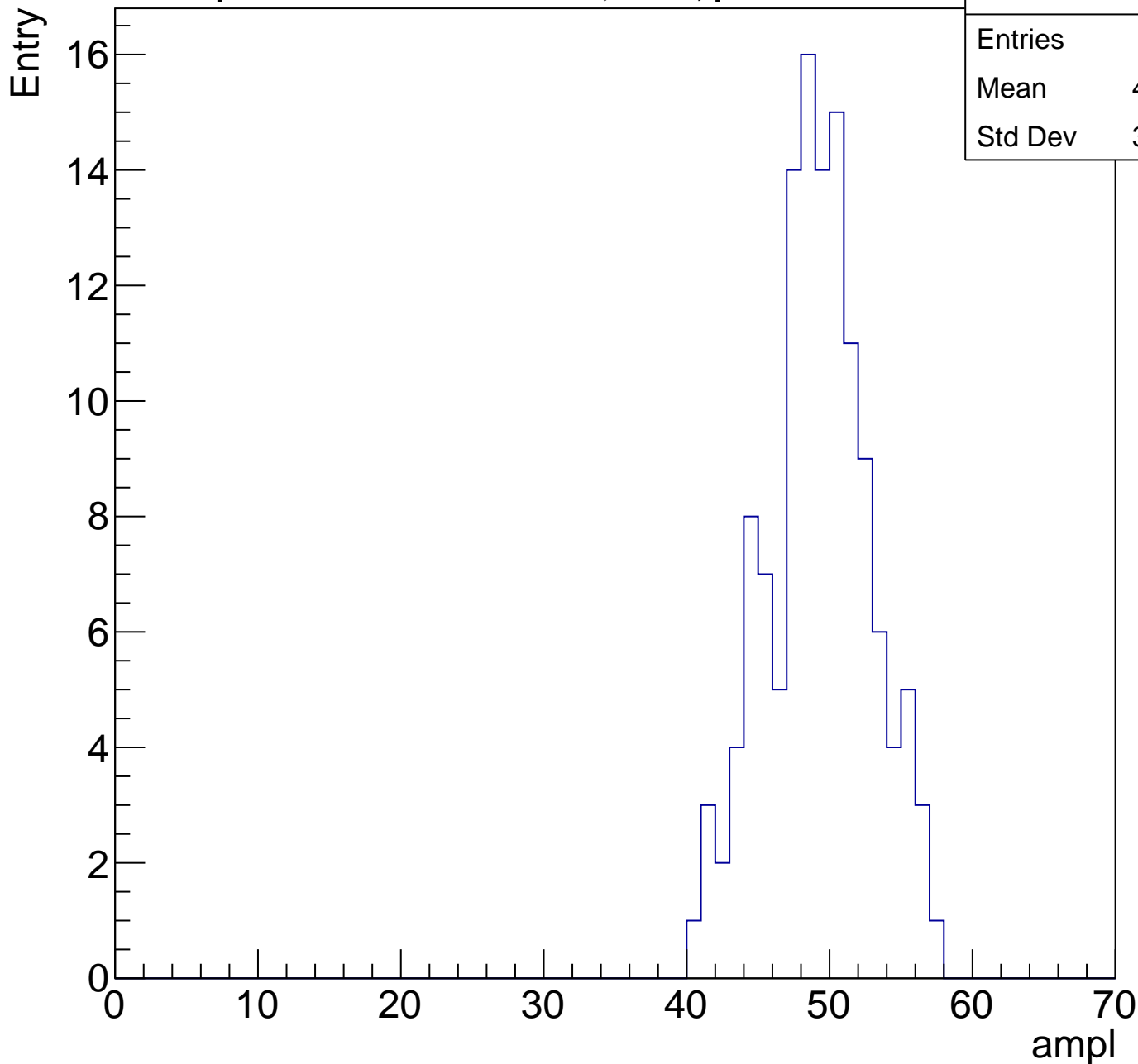
Entry



B0L101S, U19-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	48.76
Std Dev	3.605

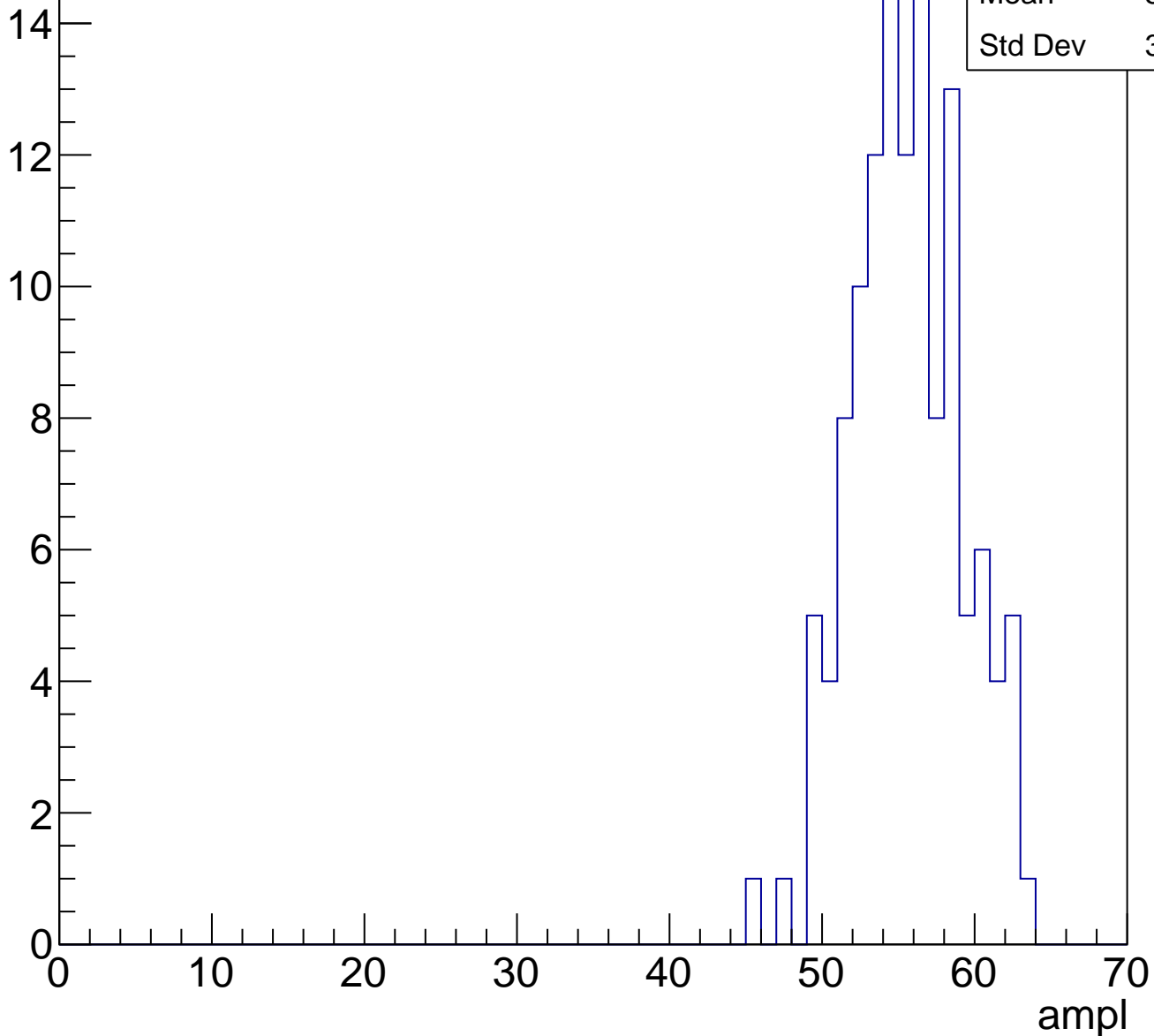


B0L101S, U19-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.14
Std Dev	3.543

Entry



B0L101S, U19-ch89, adc5

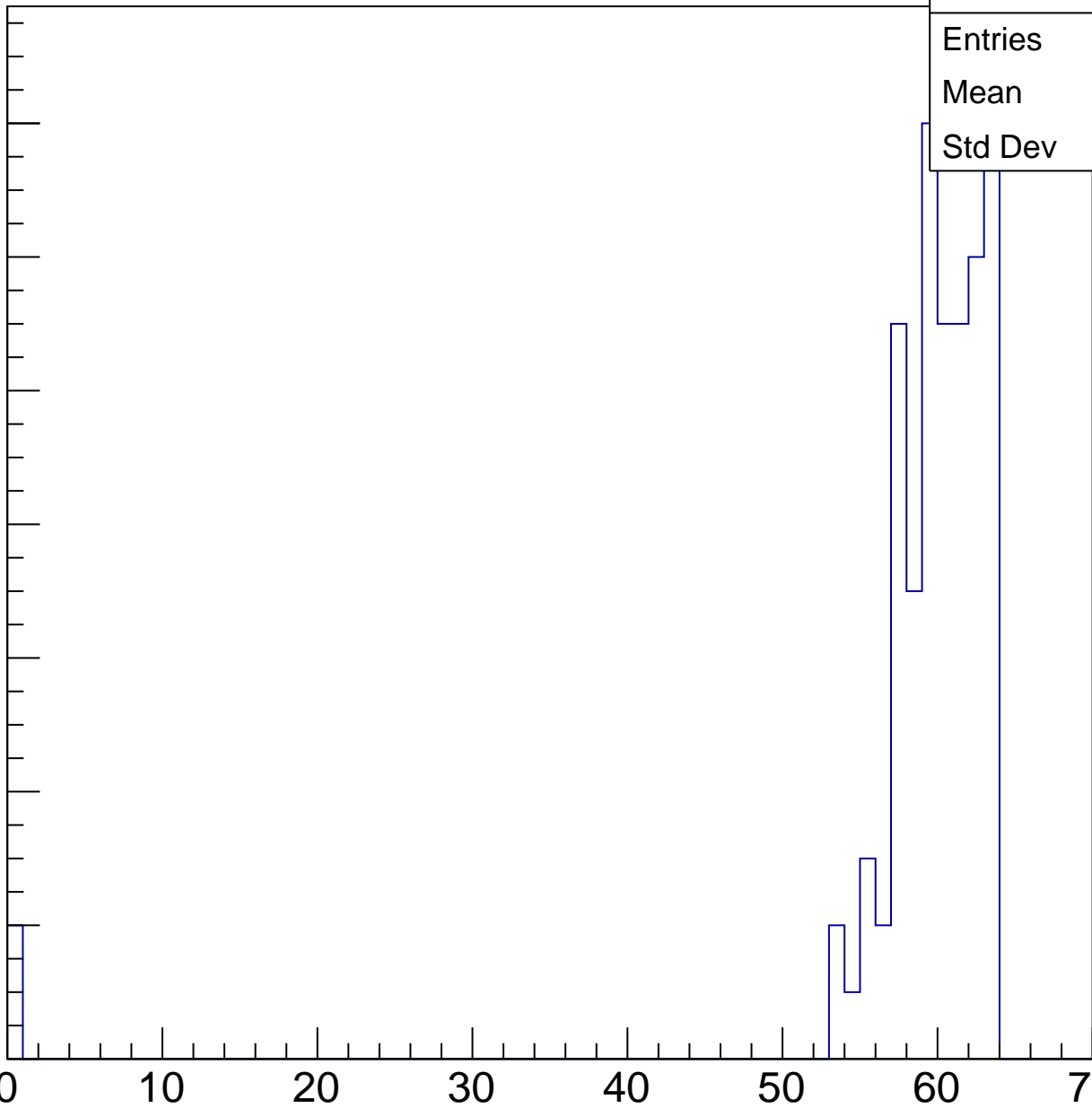
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.42
Std Dev	9.107

ampl

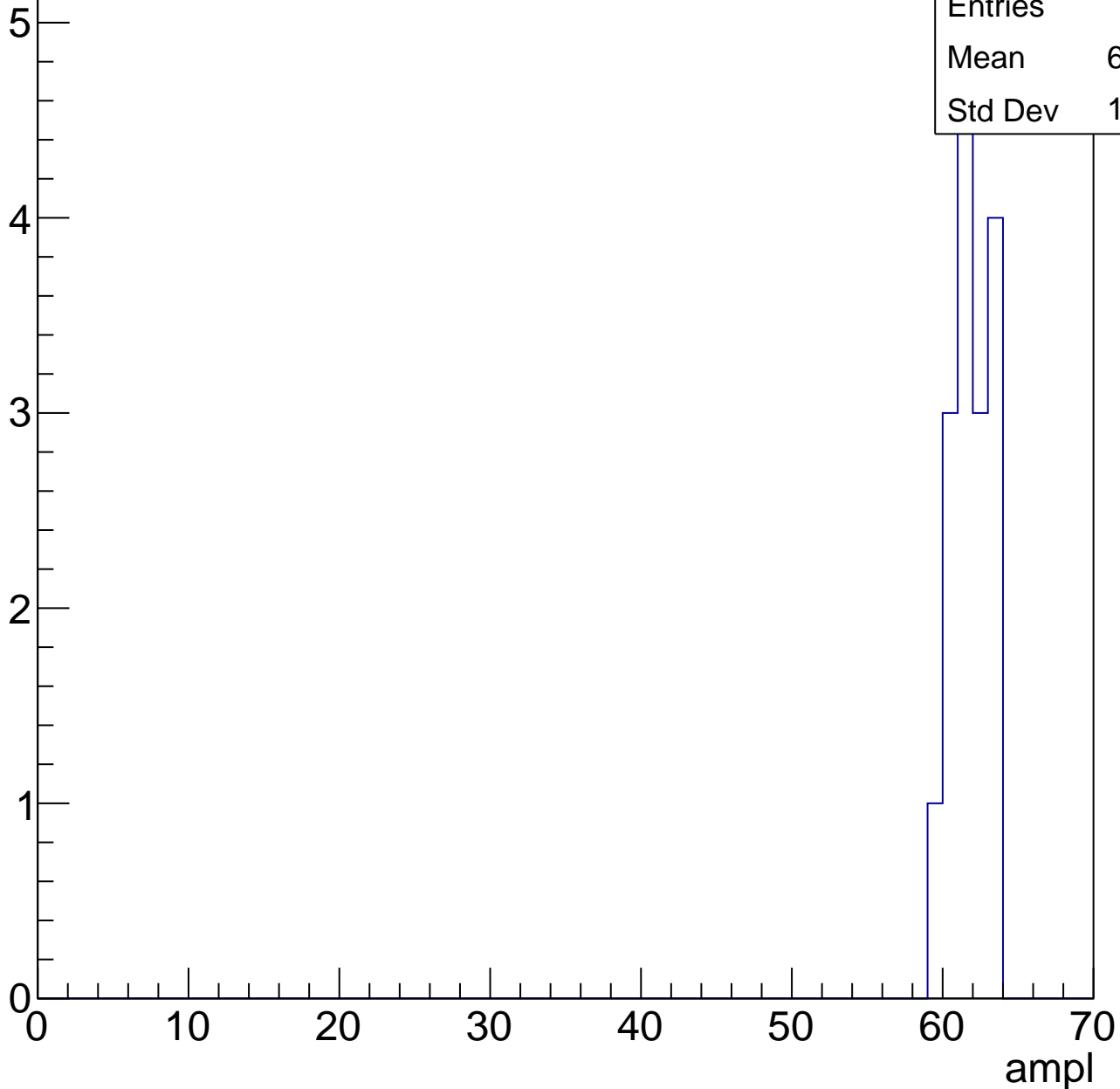


B0L101S, U19-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.38
Std Dev	1.218



B0L101S, U19-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



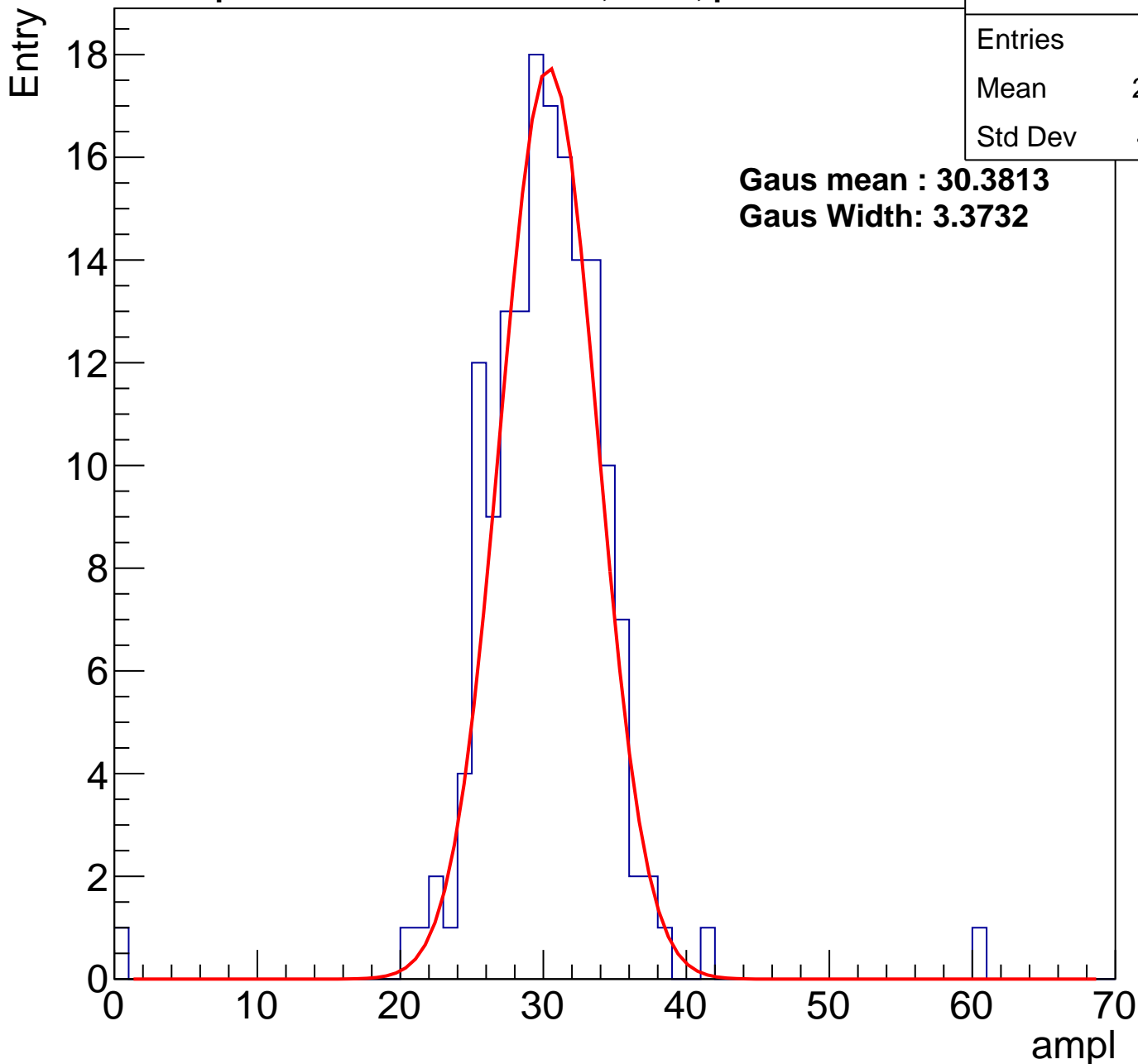
B0L101S, U19-ch90, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	29.76
Std Dev	4.881

Gaus mean : 30.3813

Gaus Width: 3.3732



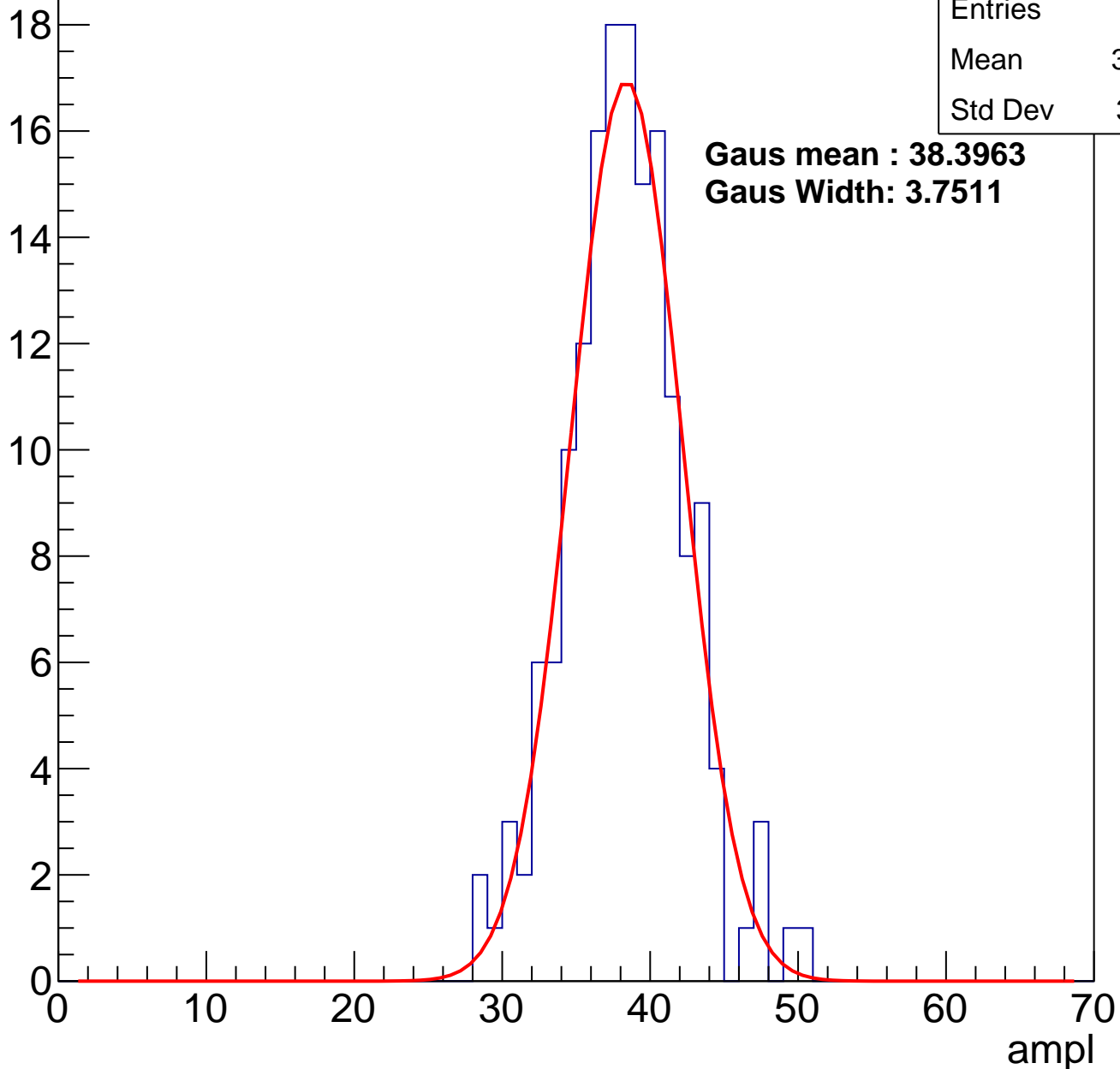
B0L101S, U19-ch90, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	37.88
Std Dev	3.941

Gaus mean : 38.3963
Gaus Width: 3.7511

Entry



B0L101S, U19-ch90, adc2

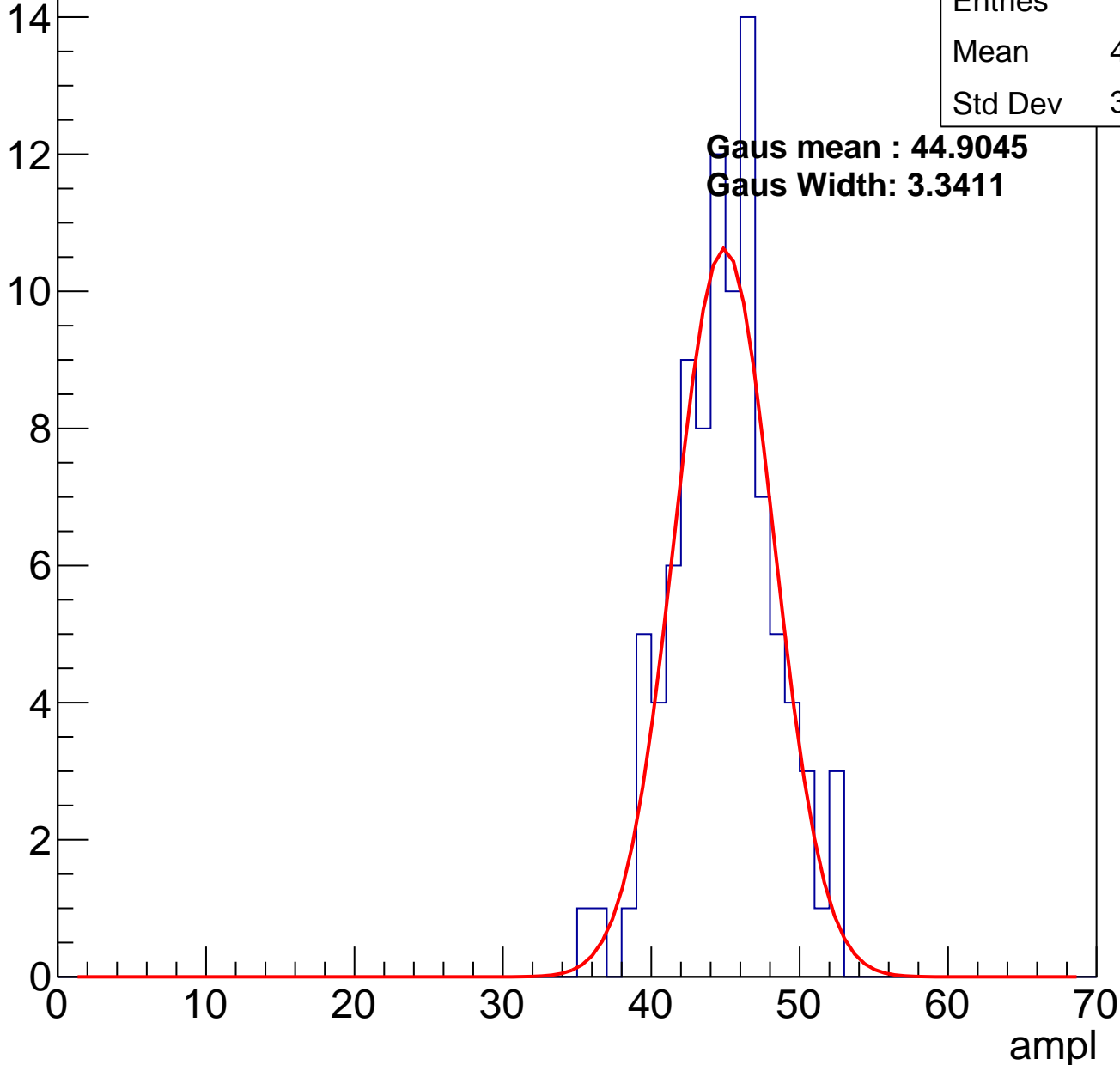
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	94
Mean	44.43
Std Dev	3.435

Gaus mean : 44.9045

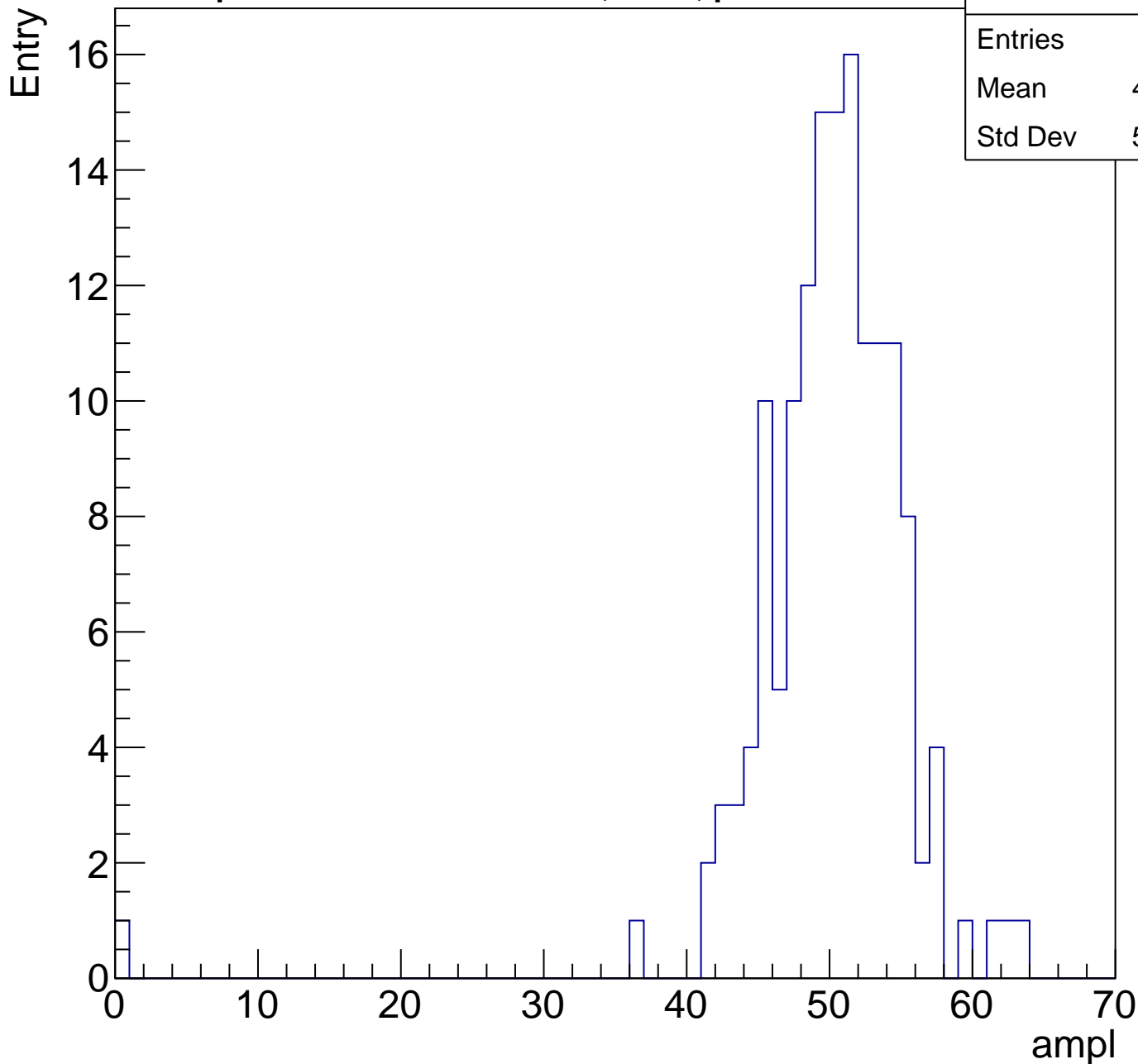
Gaus Width: 3.3411



B0L101S, U19-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	49.66
Std Dev	5.885



B0L101S, U19-ch90, adc4

calib_packv5_042523_0143.root, FC#1, port C1

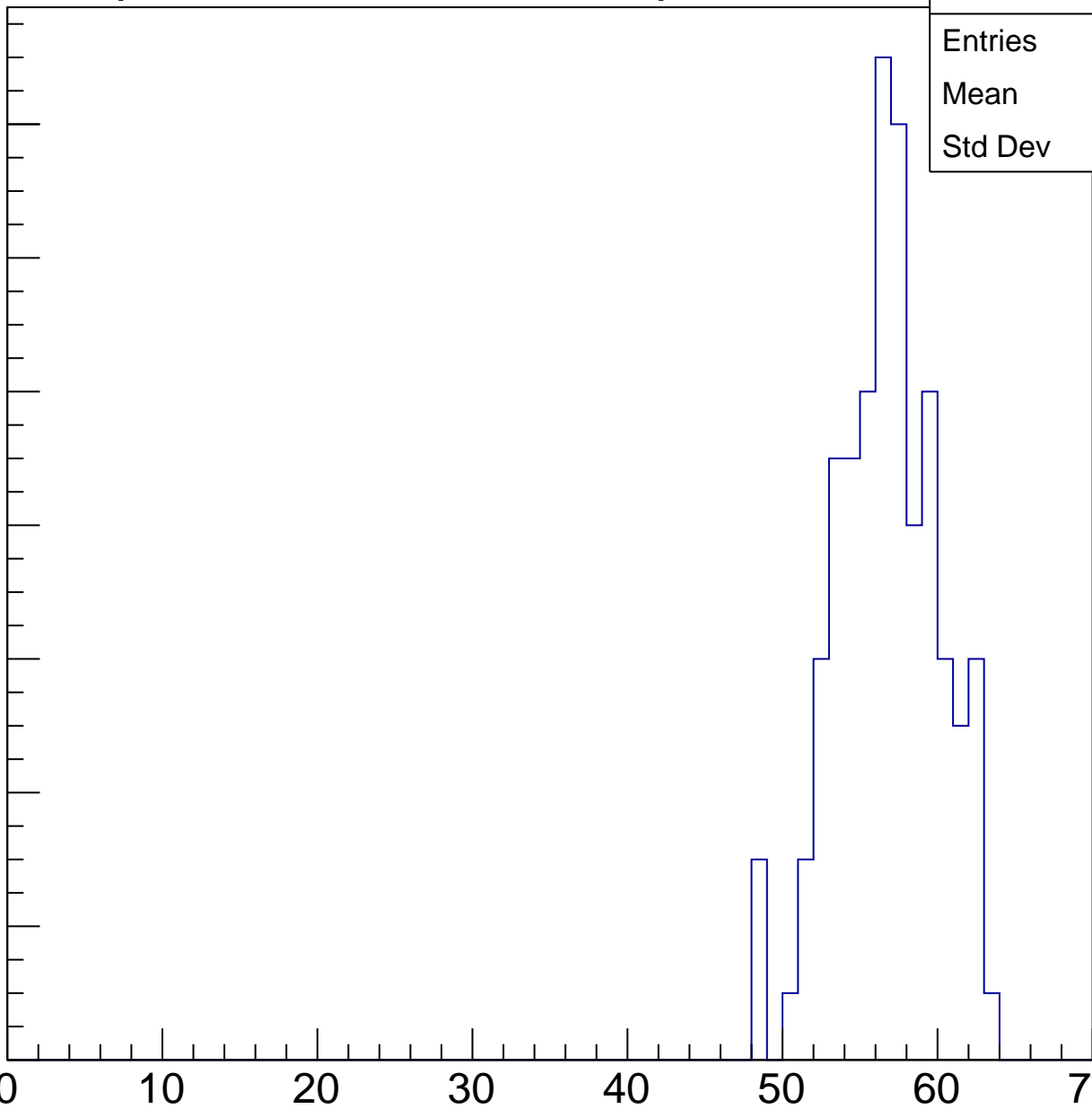
Entries	106
Mean	56.26
Std Dev	3.28

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch90, adc5

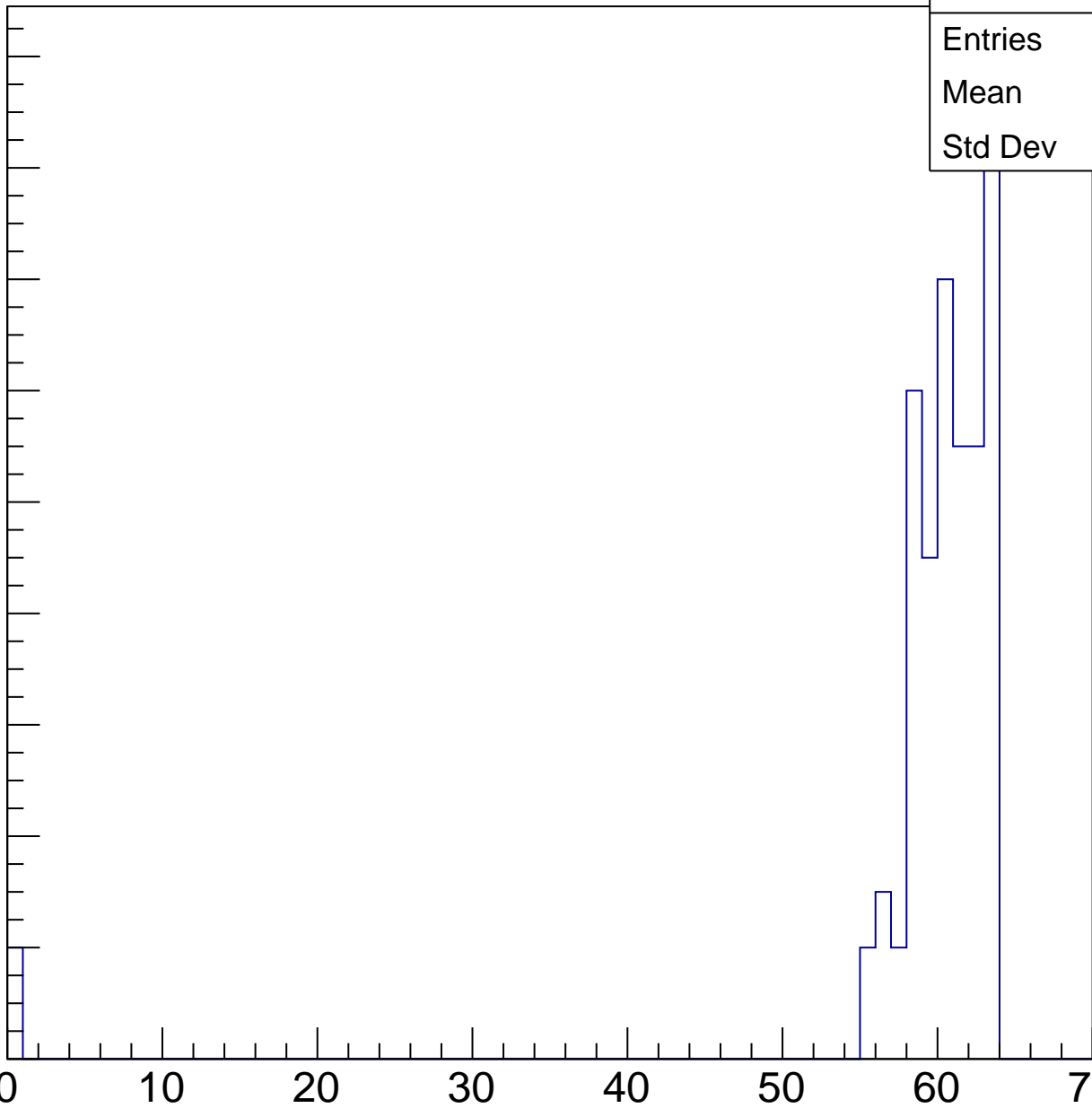
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.88
Std Dev	9.44

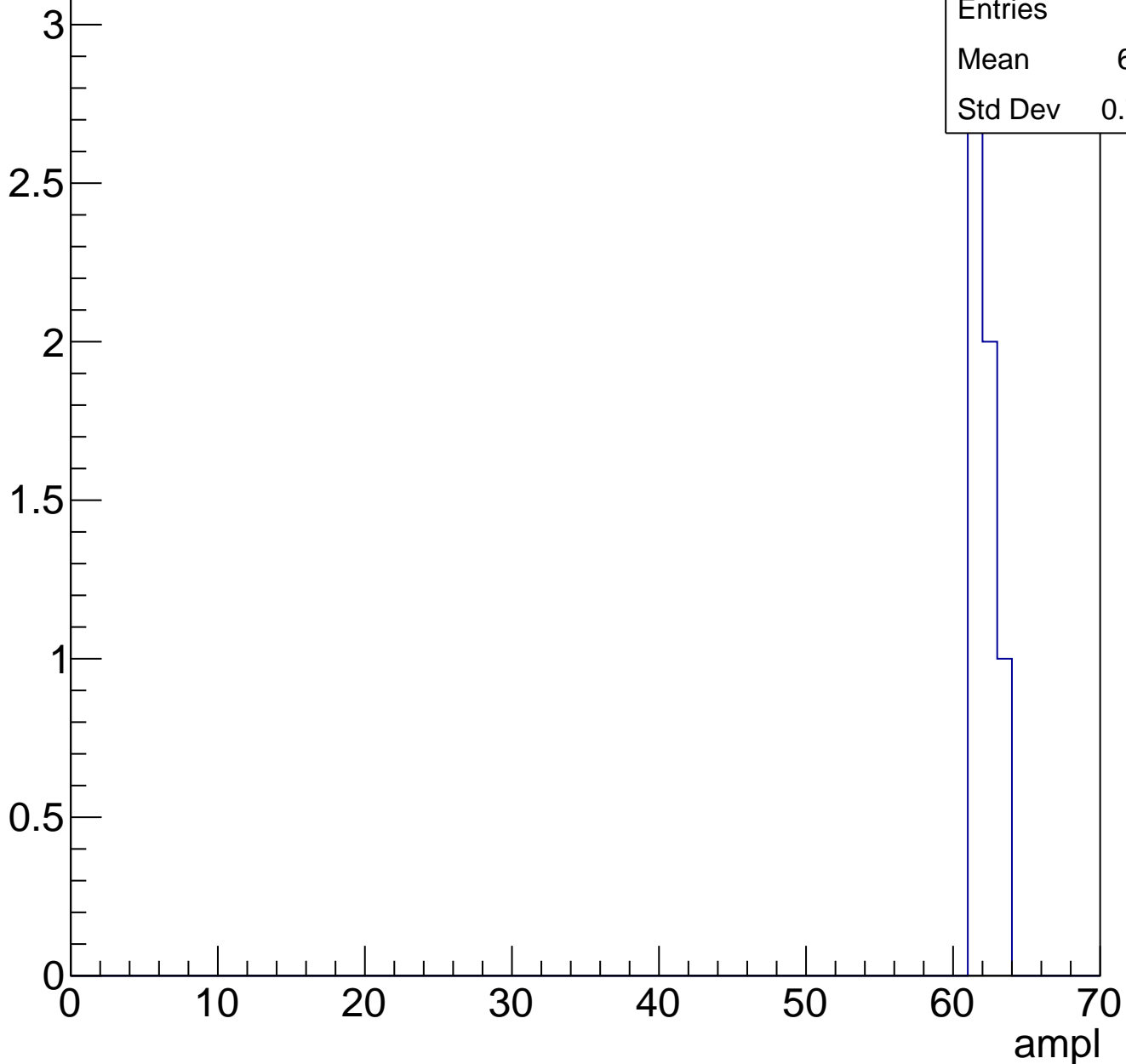
ampl



B0L101S, U19-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7
Std Dev	9.899

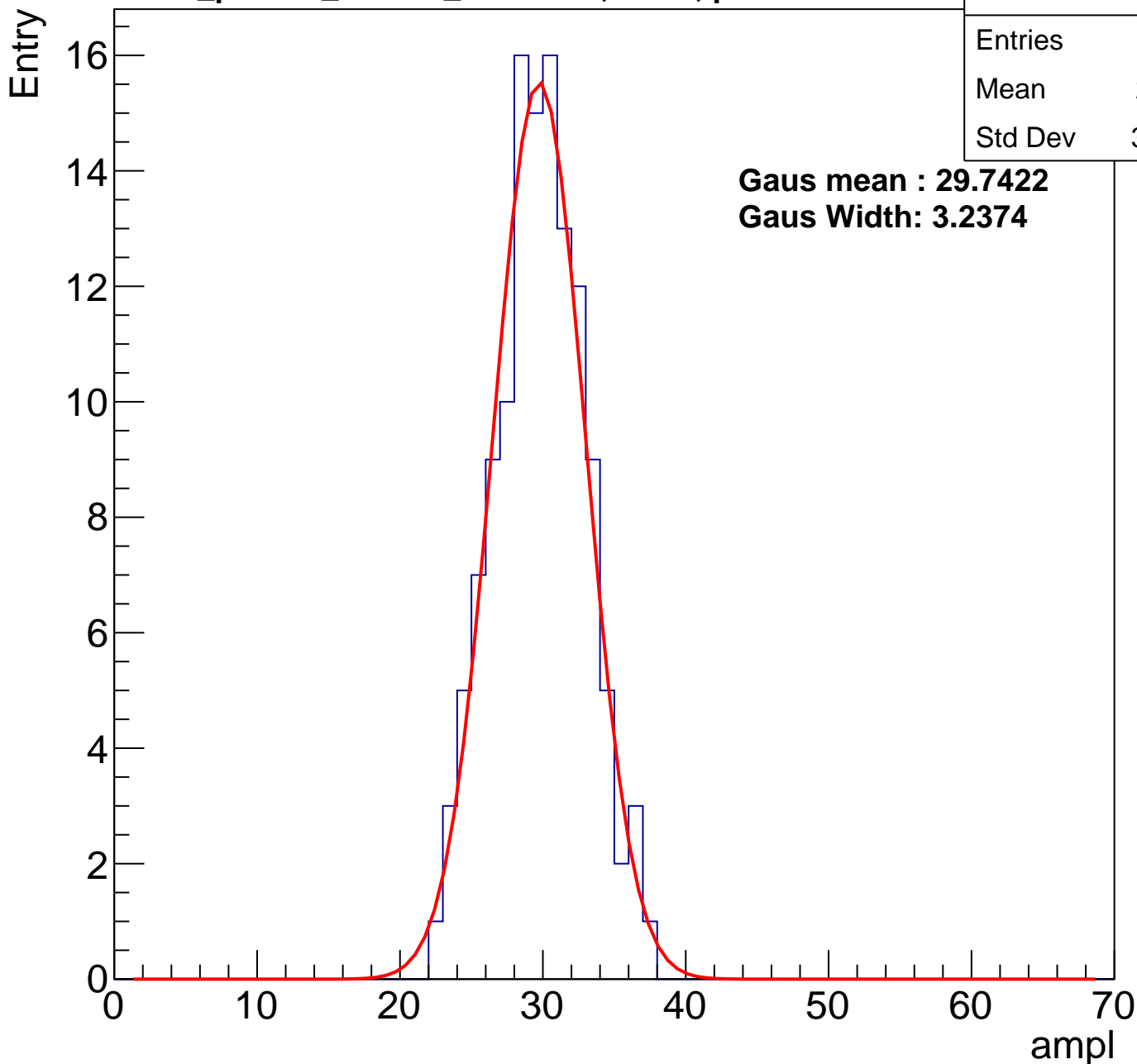
B0L101S, U19-ch91, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	29.31
Std Dev	3.134

Gaus mean : 29.7422

Gaus Width: 3.2374



B0L101S, U19-ch91, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	36.52
Std Dev	3.529

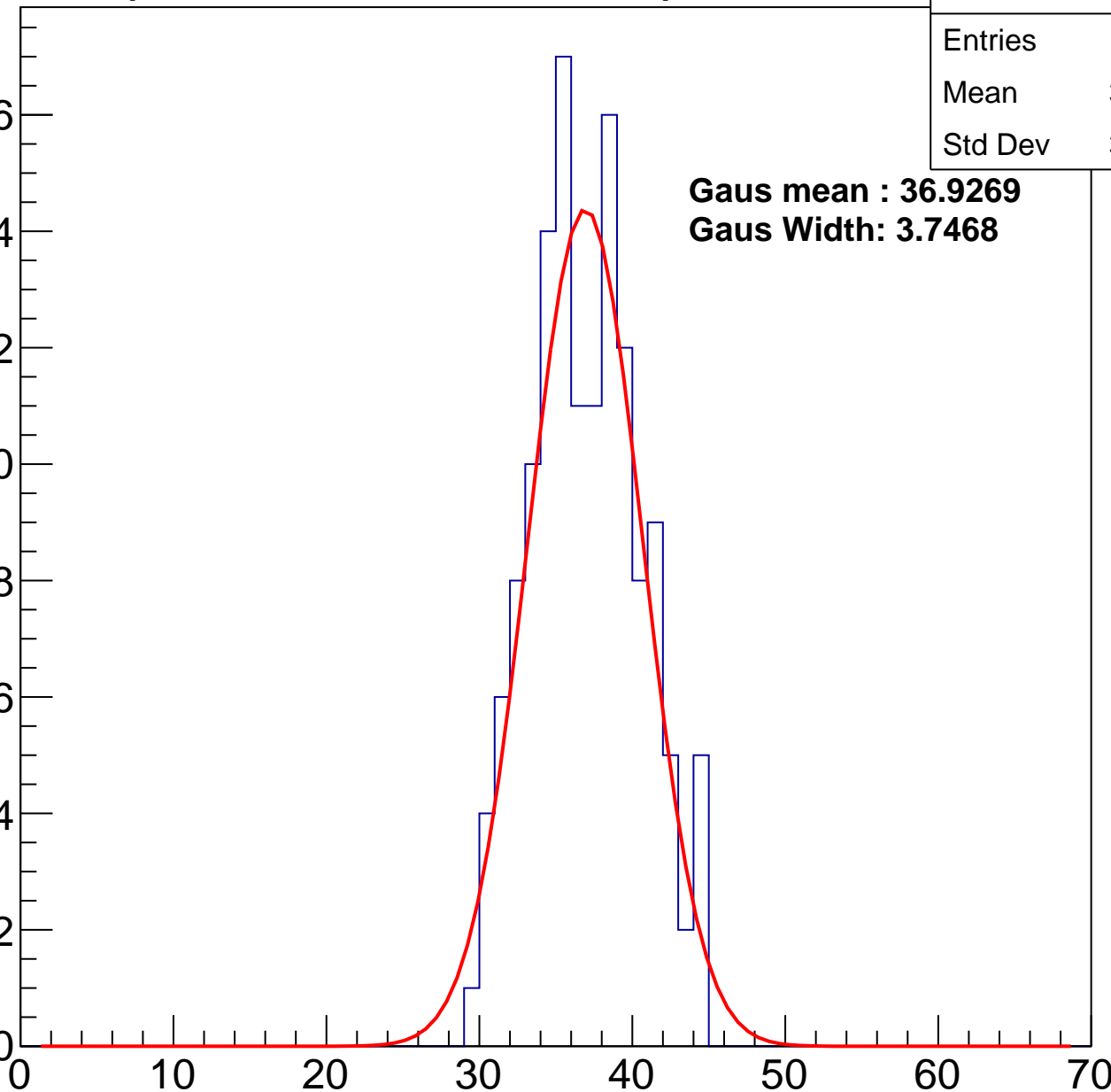
Gaus mean : 36.9269

Gaus Width: 3.7468

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch91, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	42.69
Std Dev	3.764

Gaus mean : 43.2909

Gaus Width: 3.7061

Entry

16

14

12

10

8

6

4

2

0

0

10

20

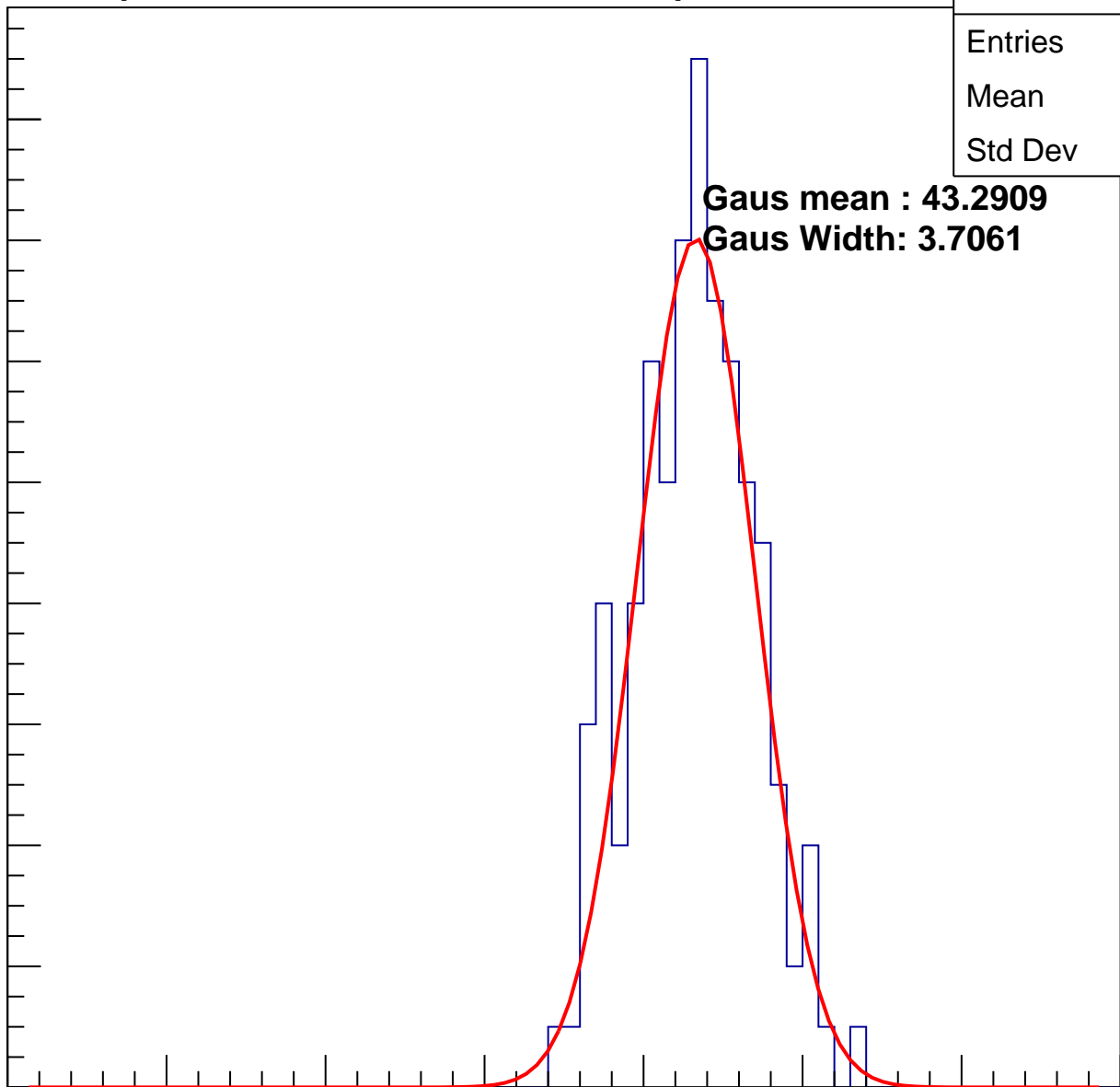
30

40

50

60

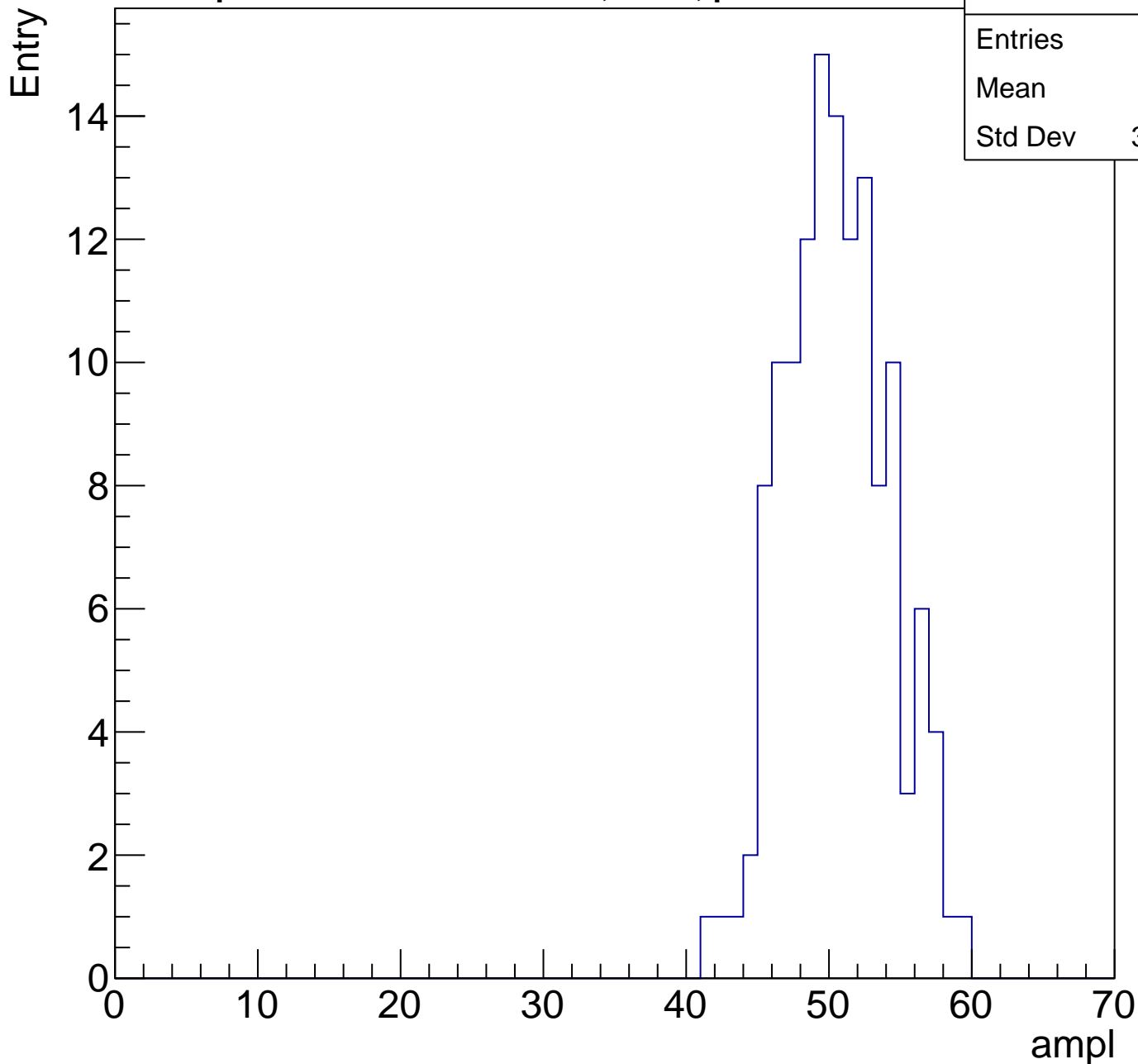
ampl



B0L101S, U19-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	50.1
Std Dev	3.595



B0L101S, U19-ch91, adc4

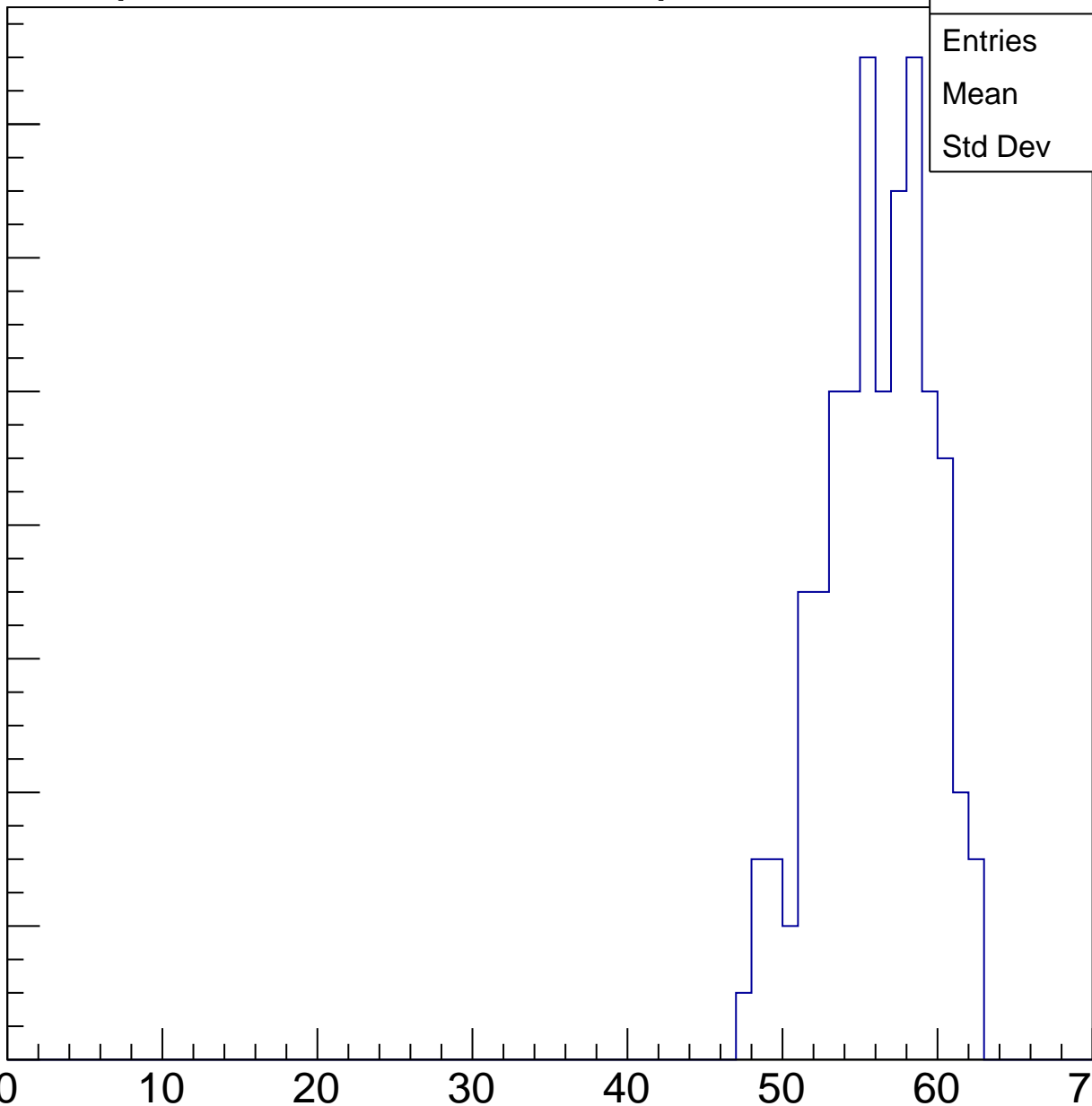
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	55.61
Std Dev	3.413

ampl



B0L101S, U19-ch91, adc5

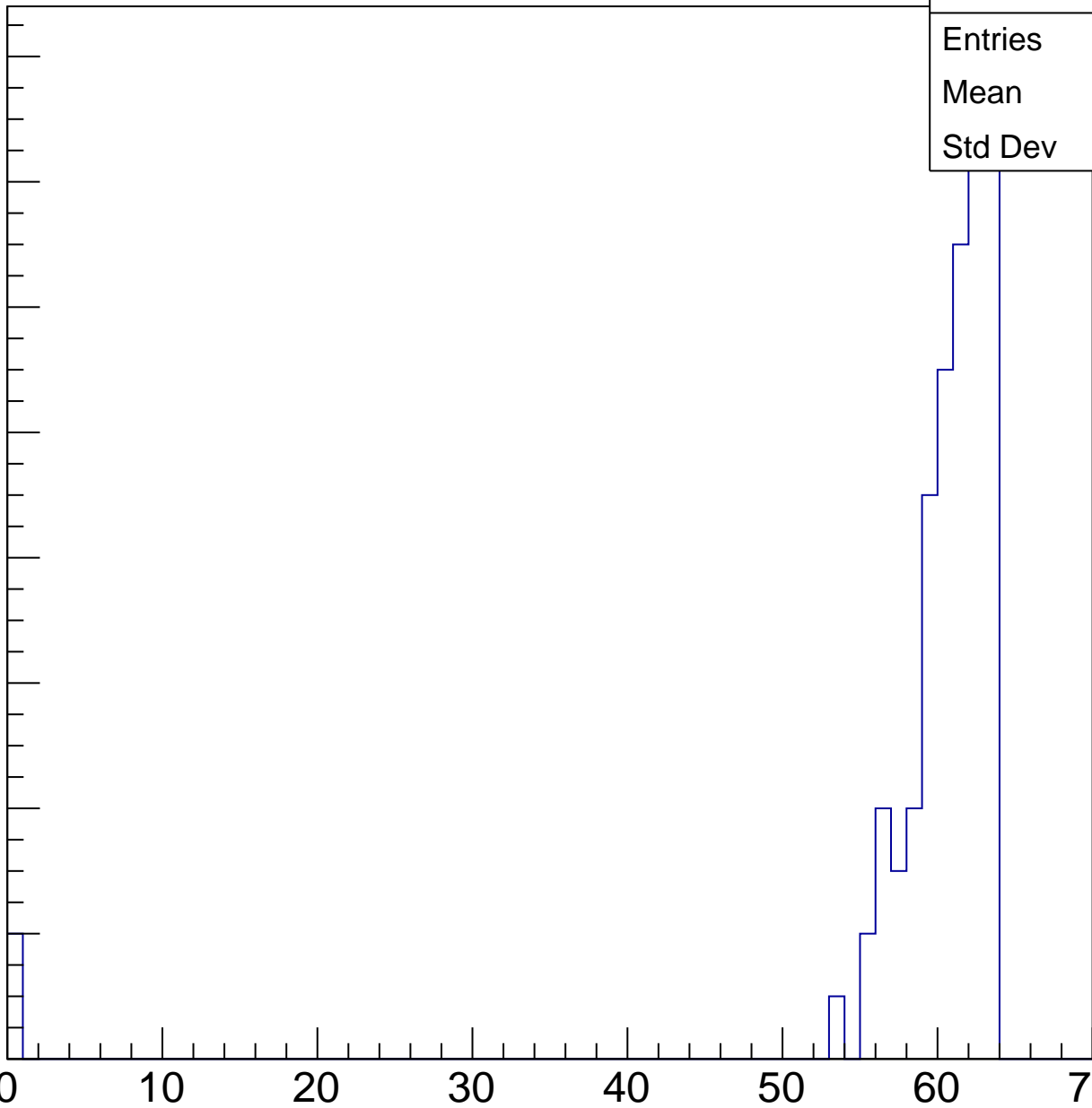
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	58.9
Std Dev	9.705

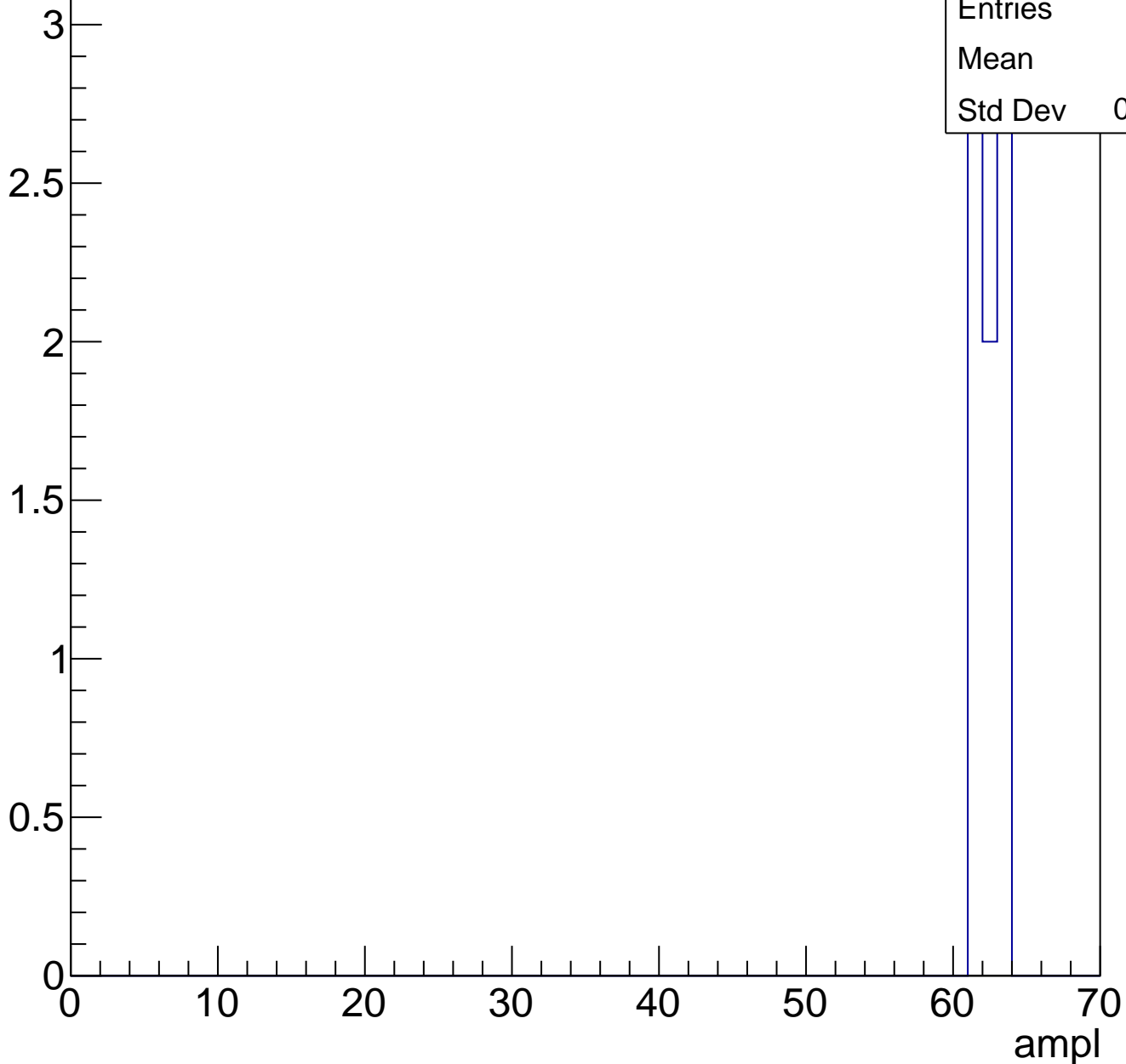
ampl



B0L101S, U19-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

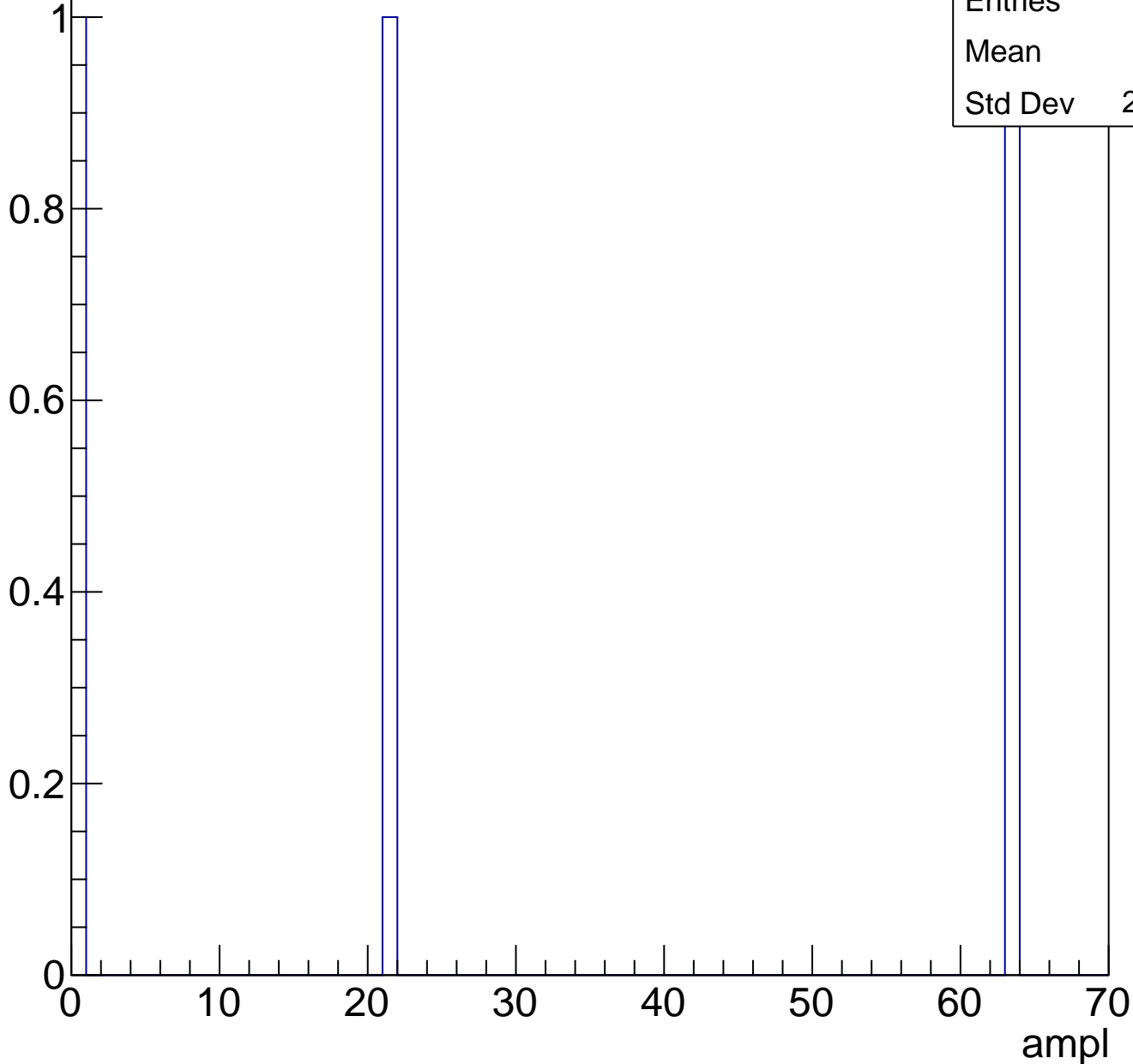
Entry



B0L101S, U19-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



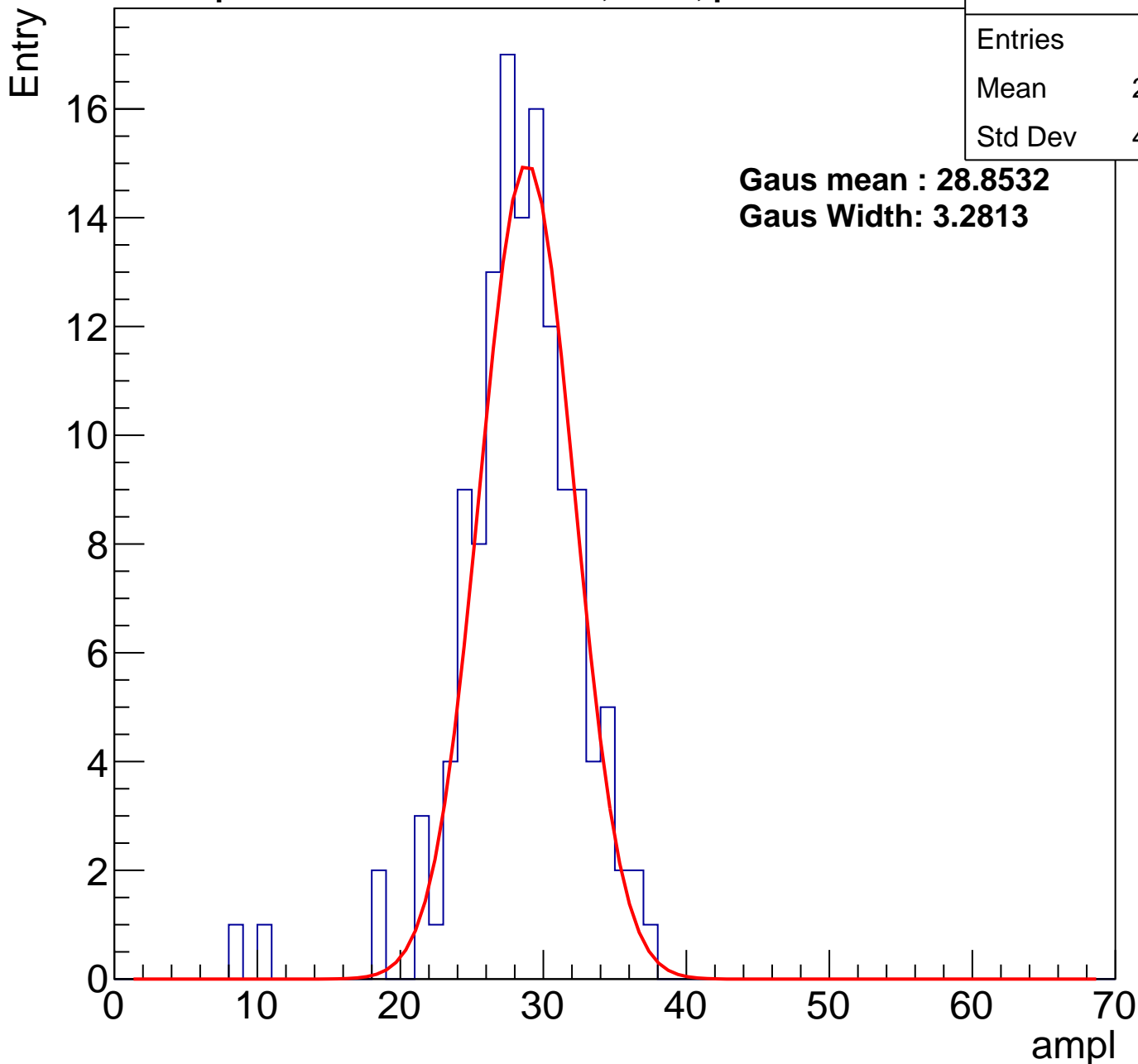
B0L101S, U19-ch92, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	27.88
Std Dev	4.218

Gaus mean : 28.8532

Gaus Width: 3.2813



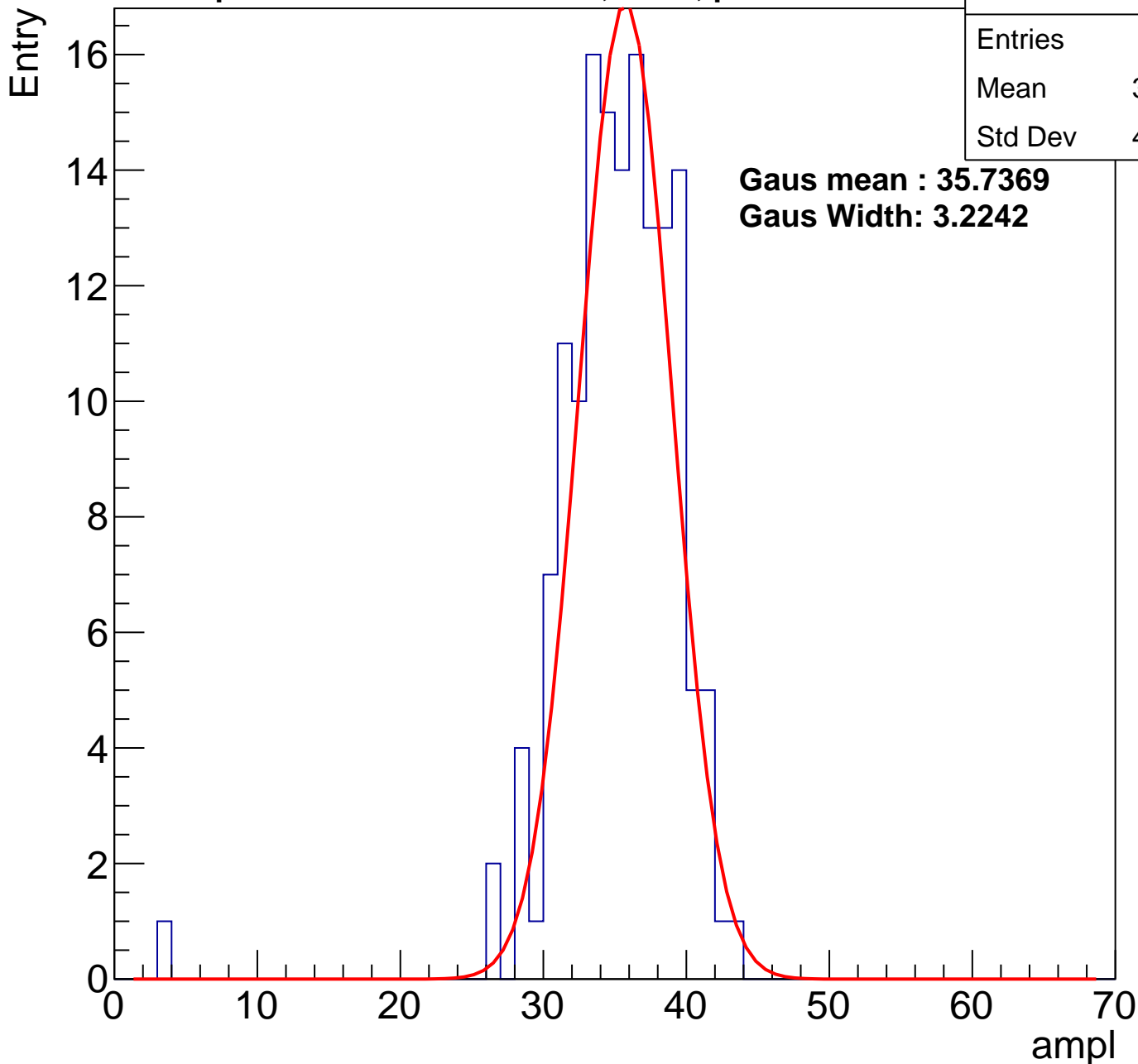
B0L101S, U19-ch92, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	34.78
Std Dev	4.302

Gaus mean : 35.7369

Gaus Width: 3.2242



B0L101S, U19-ch92, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	103
Mean	41.61
Std Dev	3.523

Gaus mean : 41.7548

Gaus Width: 3.6631

12

10

8

6

4

2

0

0

10

20

30

40

50

60

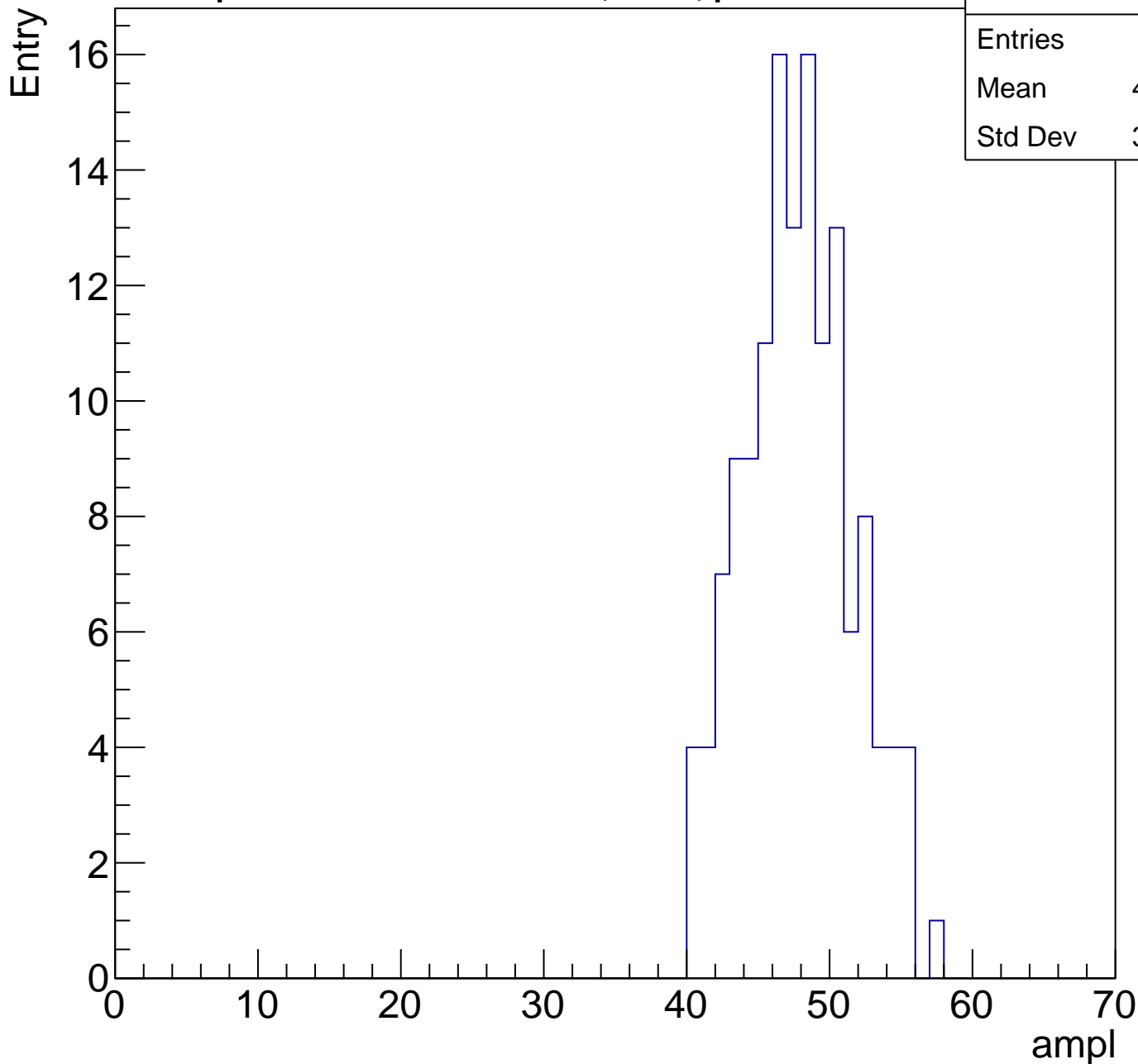
70

ampl

B0L101S, U19-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	47.34
Std Dev	3.735



B0L101S, U19-ch92, adc4

calib_packv5_042523_0143.root, FC#1, port C1

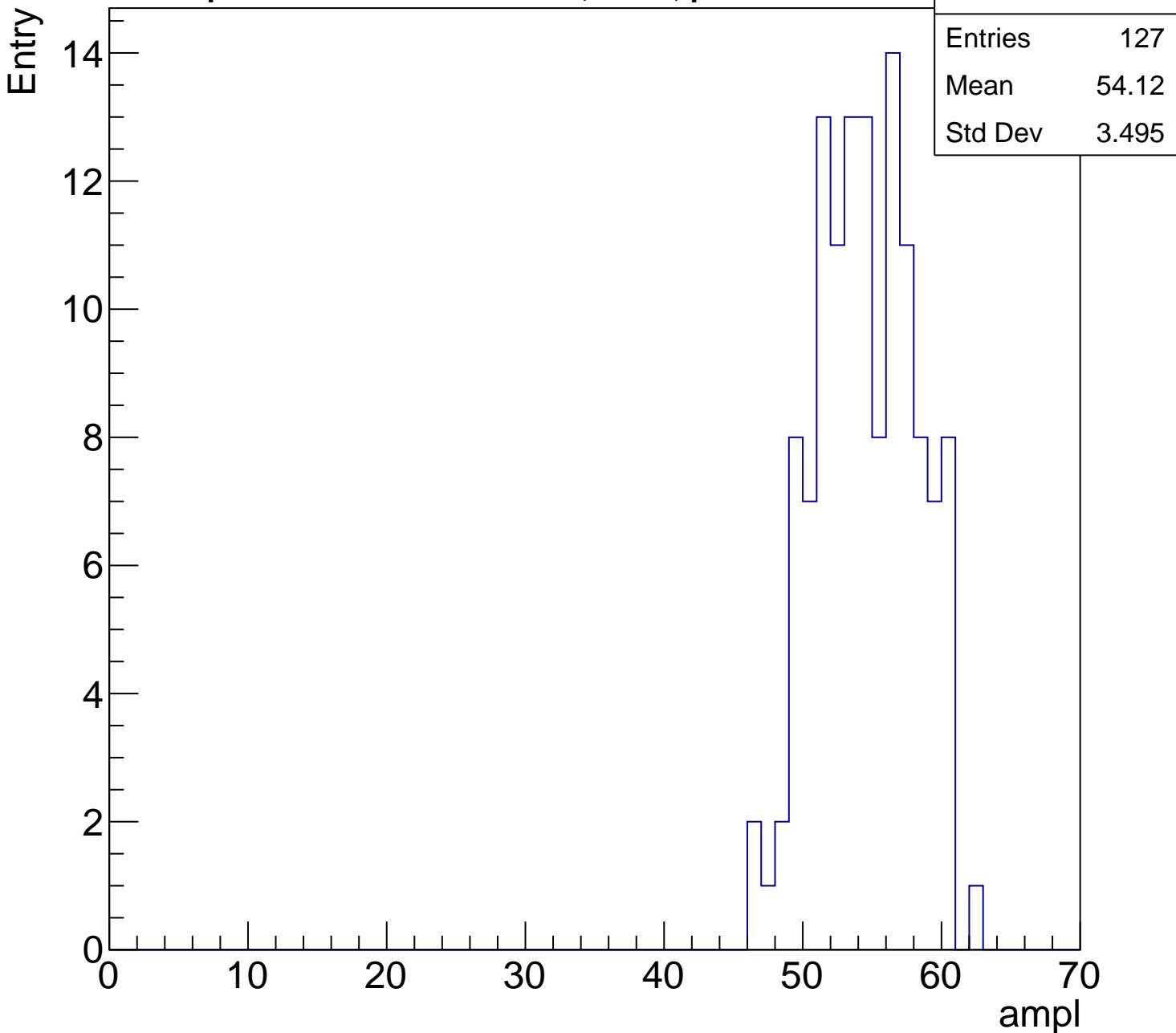
Entries	127
Mean	54.12
Std Dev	3.495

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch92, adc5

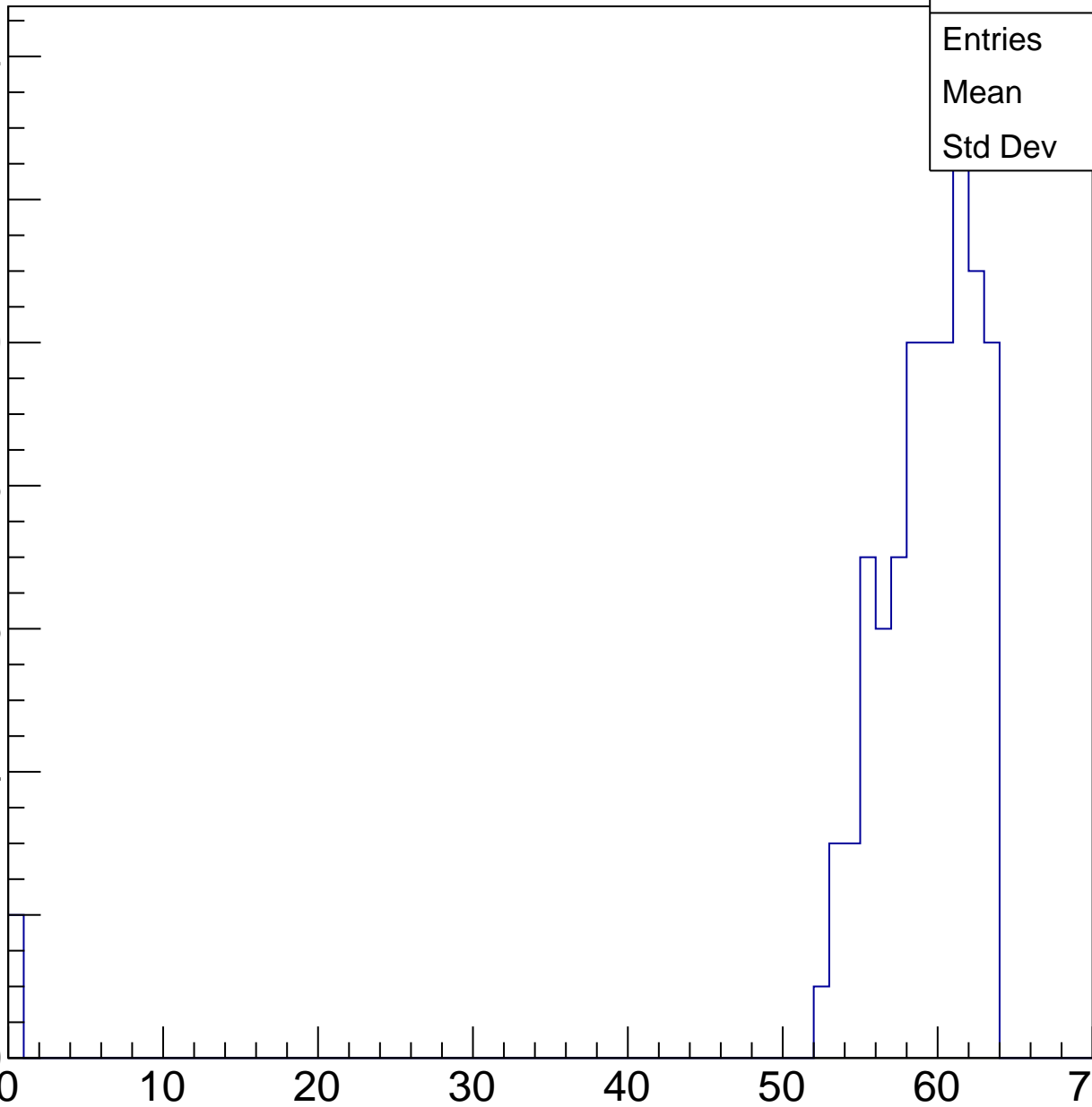
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	57.76
Std Dev	8.978

ampl

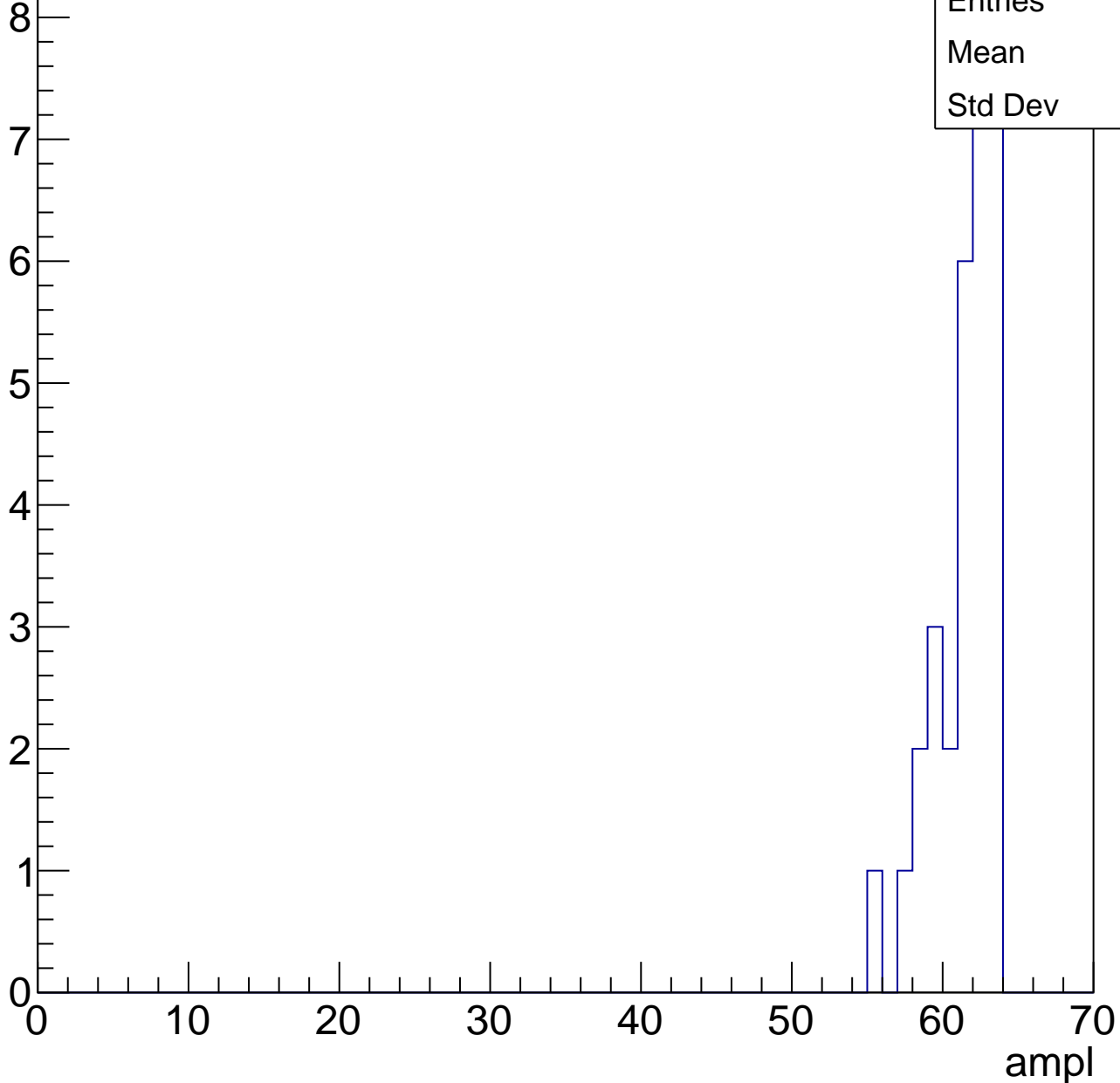


B0L101S, U19-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	31
Mean	61
Std Dev	2



B0L101S, U19-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

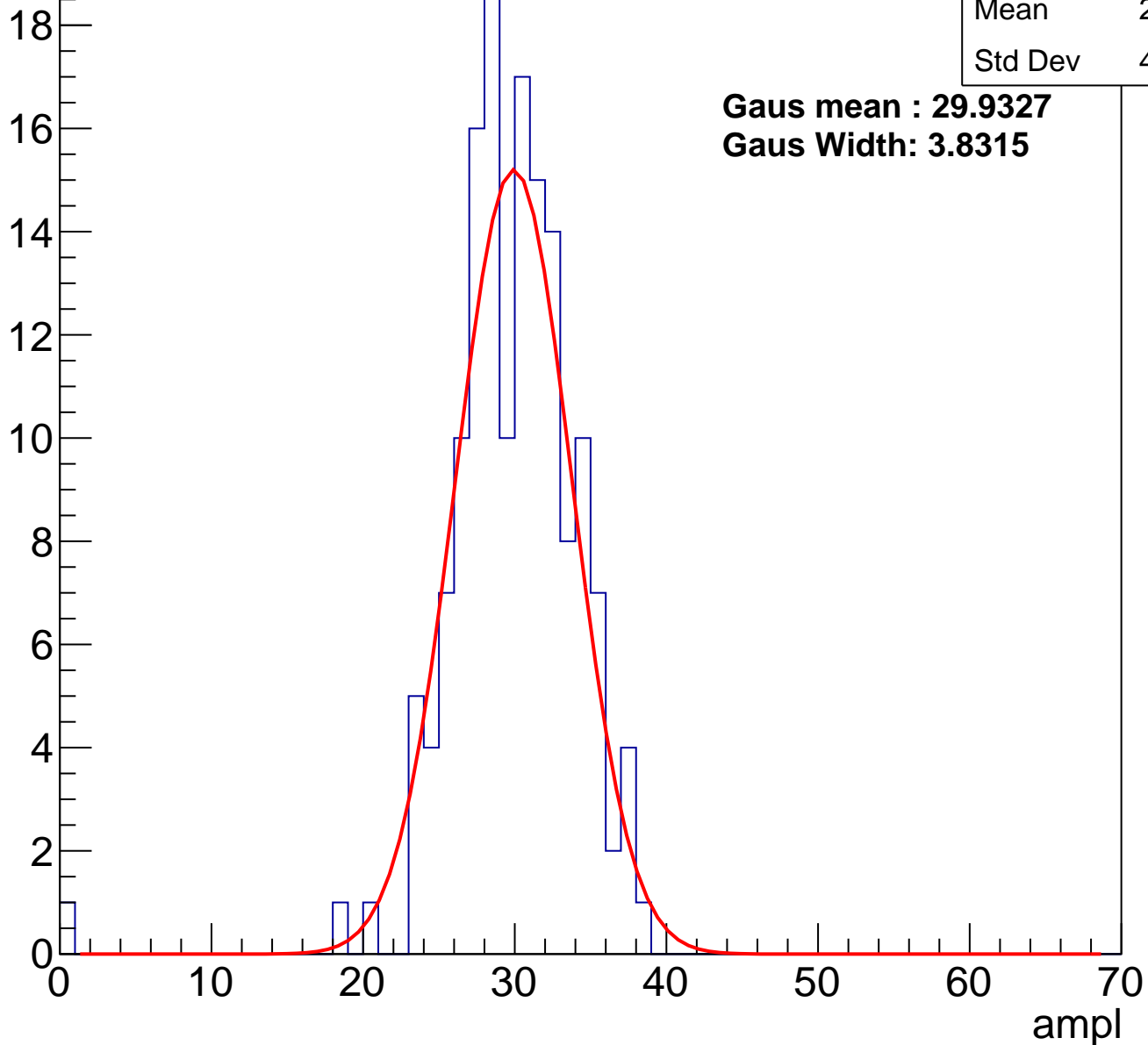
Entries	2
Mean	63
Std Dev	0

ampl

B0L101S, U19-ch93, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



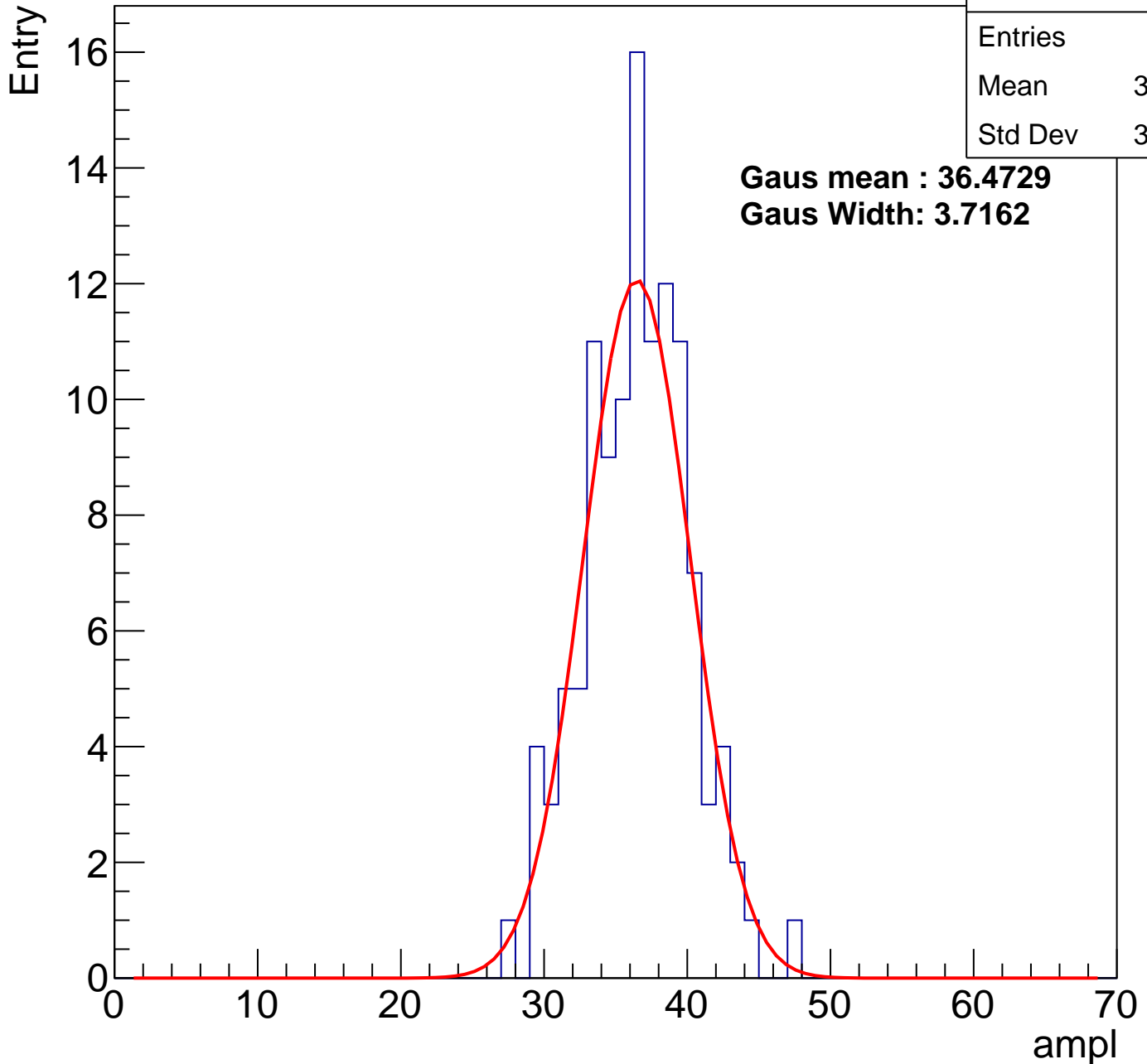
B0L101S, U19-ch93, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	36.06
Std Dev	3.604

Gaus mean : 36.4729

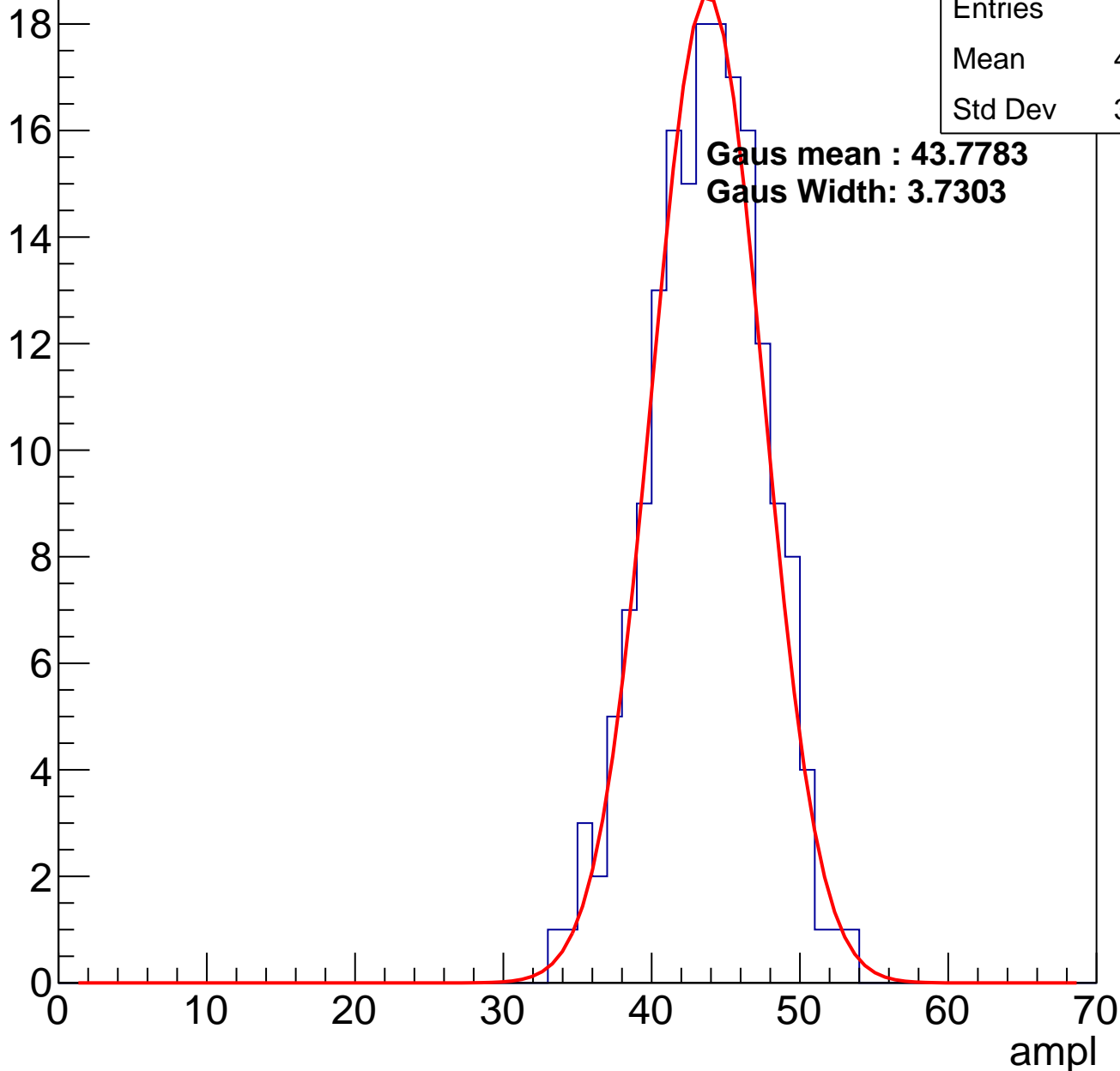
Gaus Width: 3.7162



B0L101S, U19-ch93, adc2

calib_packv5_042523_0143.root, FC#1, port C1

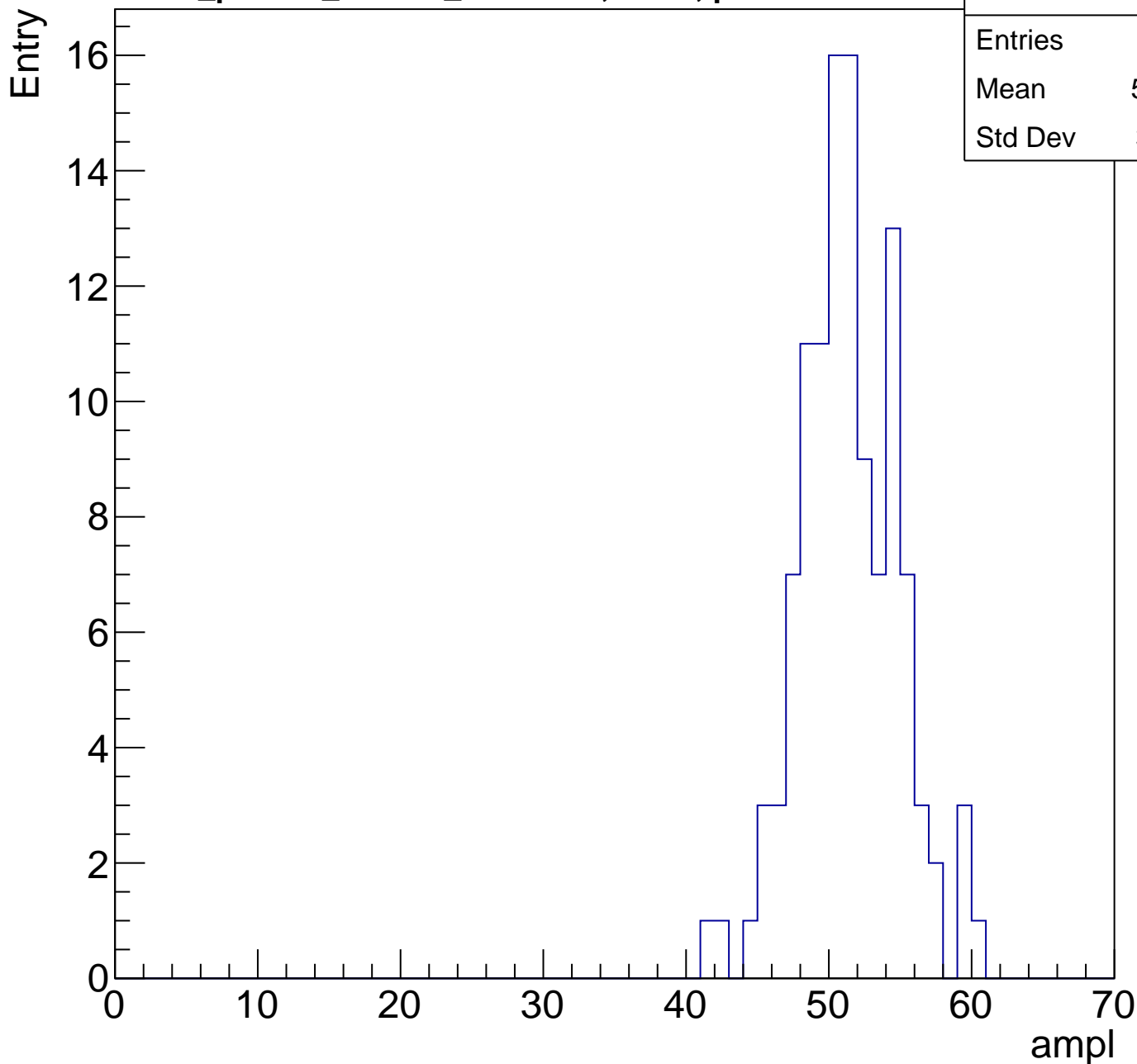
Entry



B0L101S, U19-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	50.93
Std Dev	3.451



B0L101S, U19-ch93, adc4

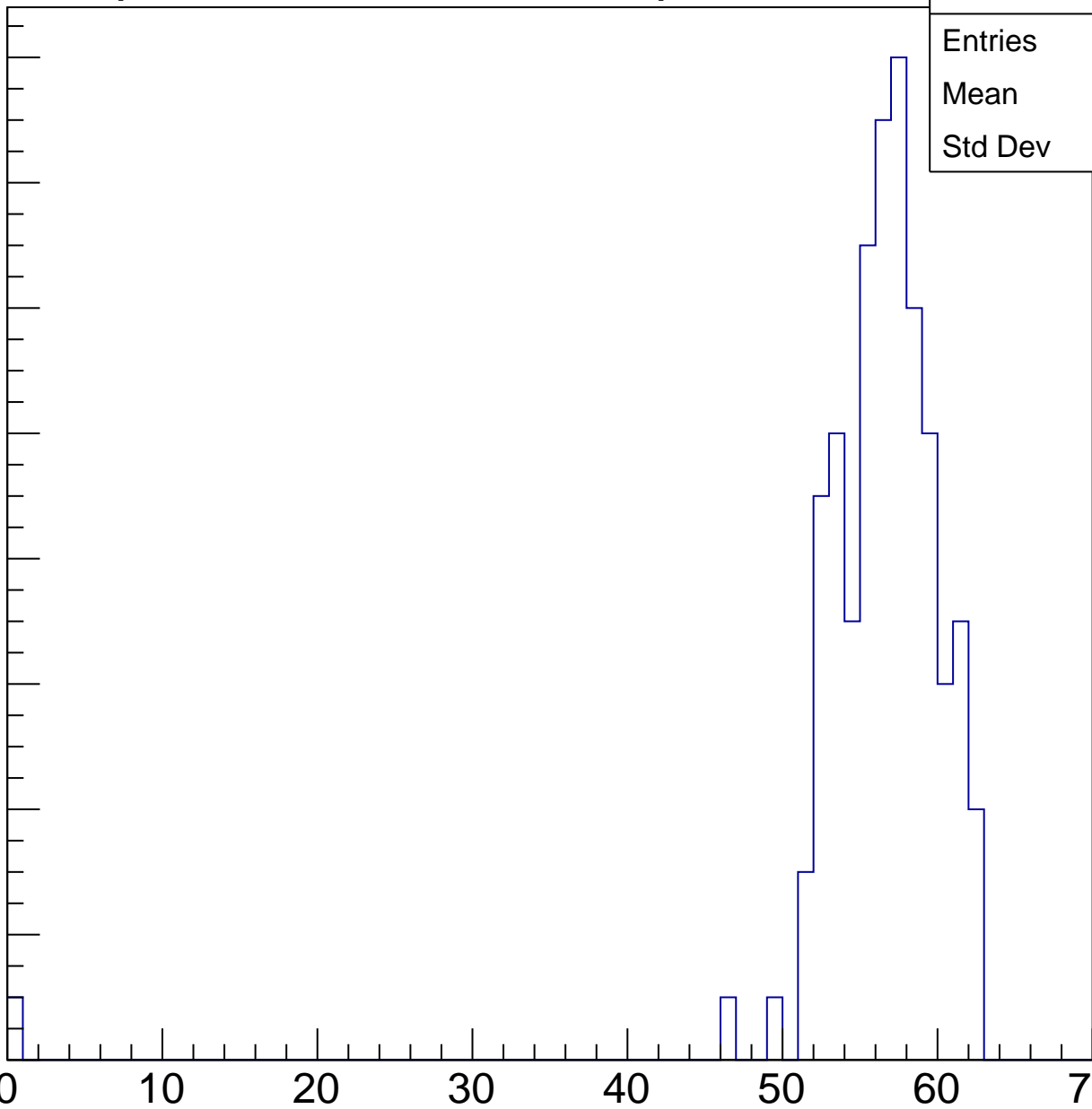
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	55.76
Std Dev	6.041

ampl



B0L101S, U19-ch93, adc5

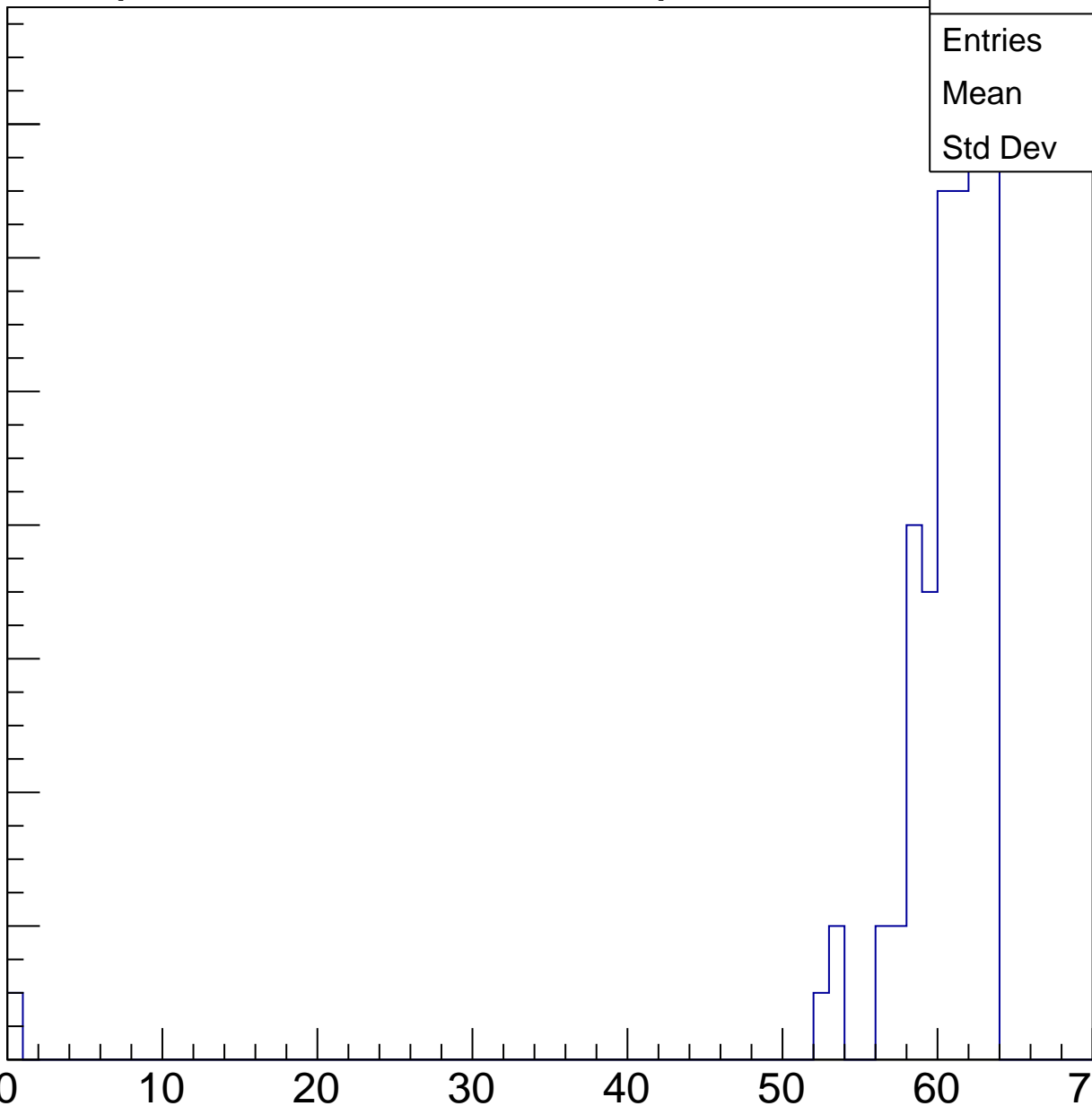
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	59.61
Std Dev	7.157

ampl

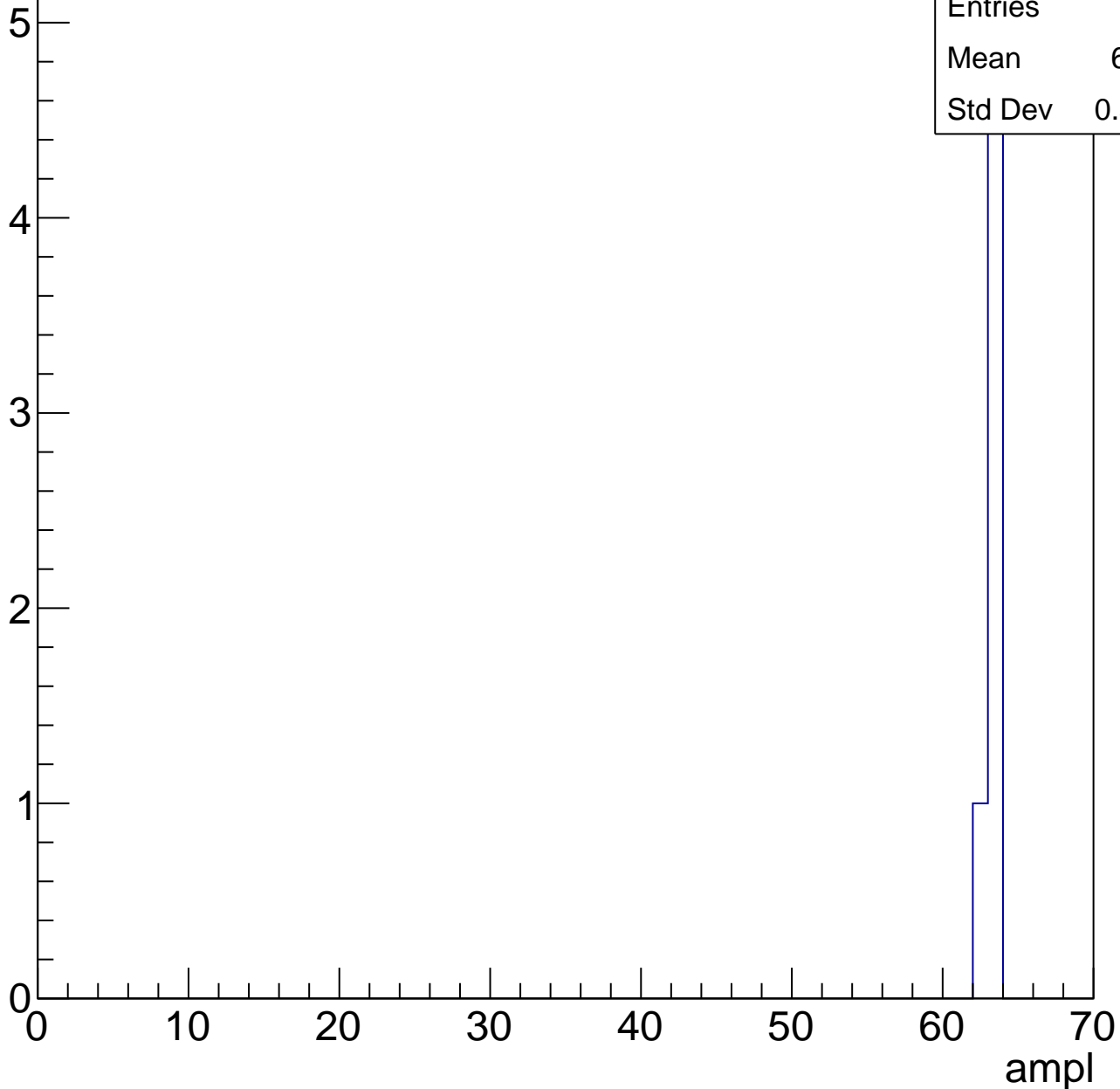


B0L101S, U19-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B0L101S, U19-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch94, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	27.43
Std Dev	6.165

Gaus mean : 28.2854

Gaus Width: 3.6169

Entry

12

10

8

6

4

2

0

0

10

20

30

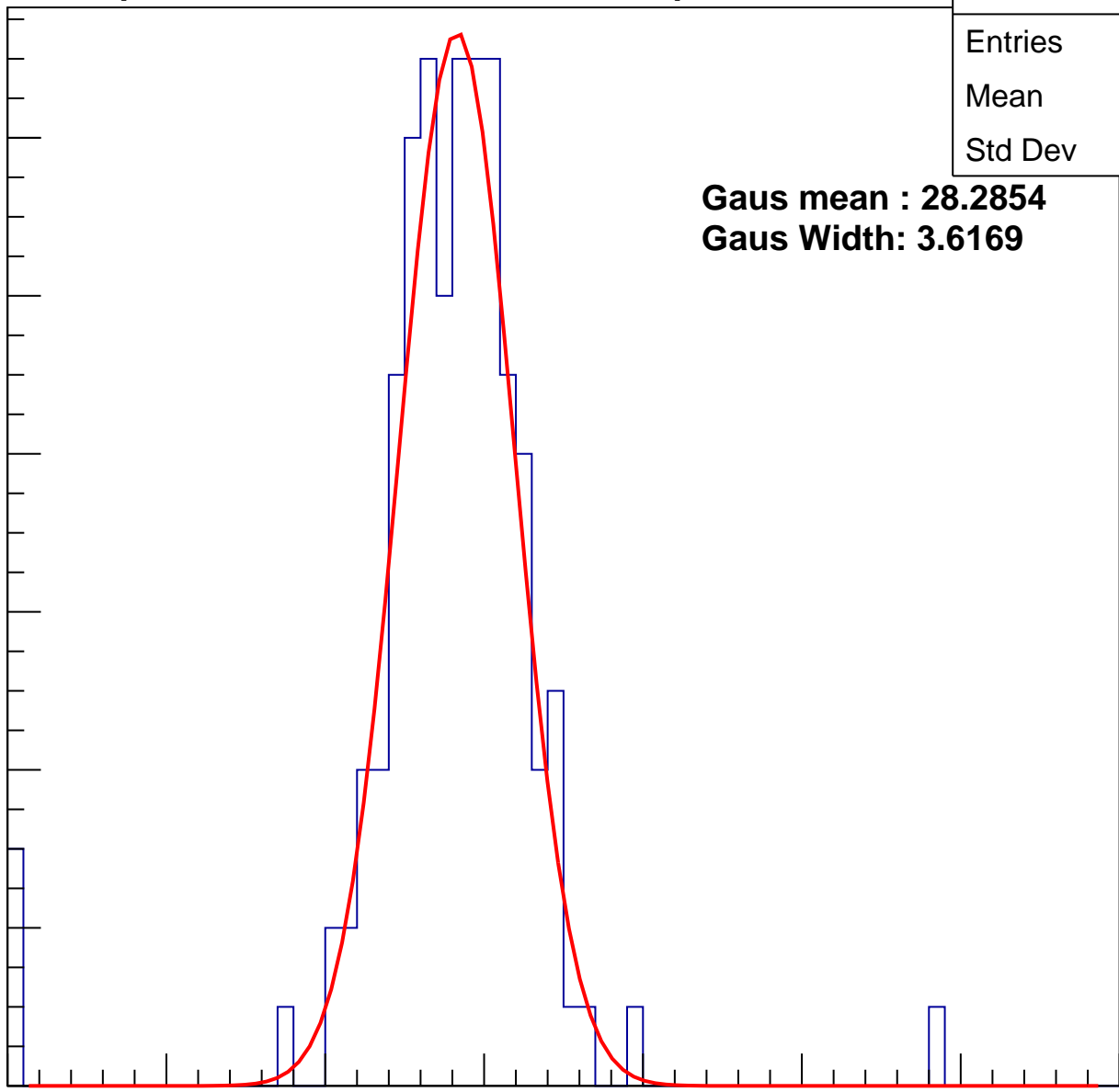
40

50

60

70

ampl



B0L101S, U19-ch94, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	34.14
Std Dev	4.75

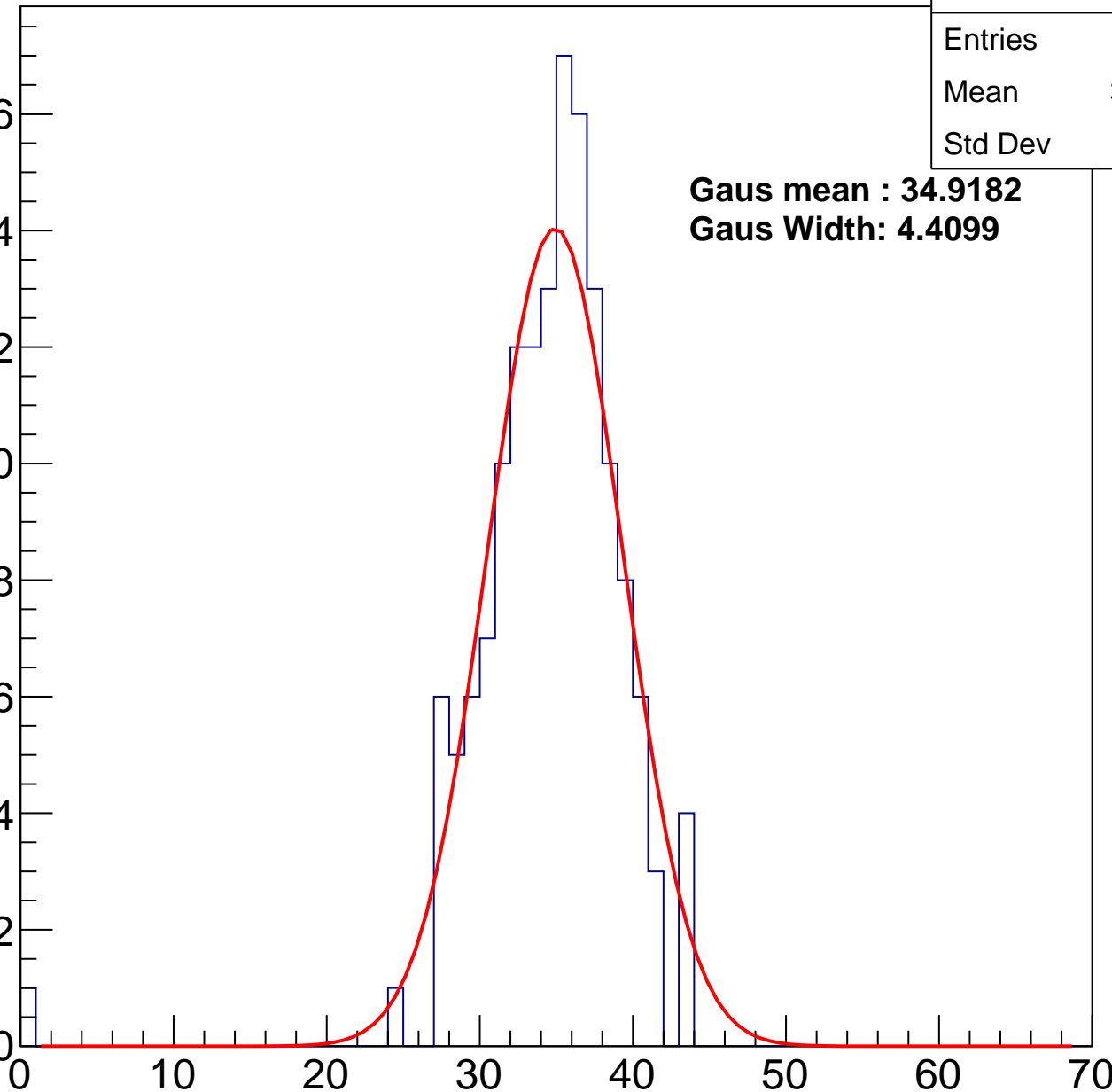
Gaus mean : 34.9182

Gaus Width: 4.4099

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch94, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

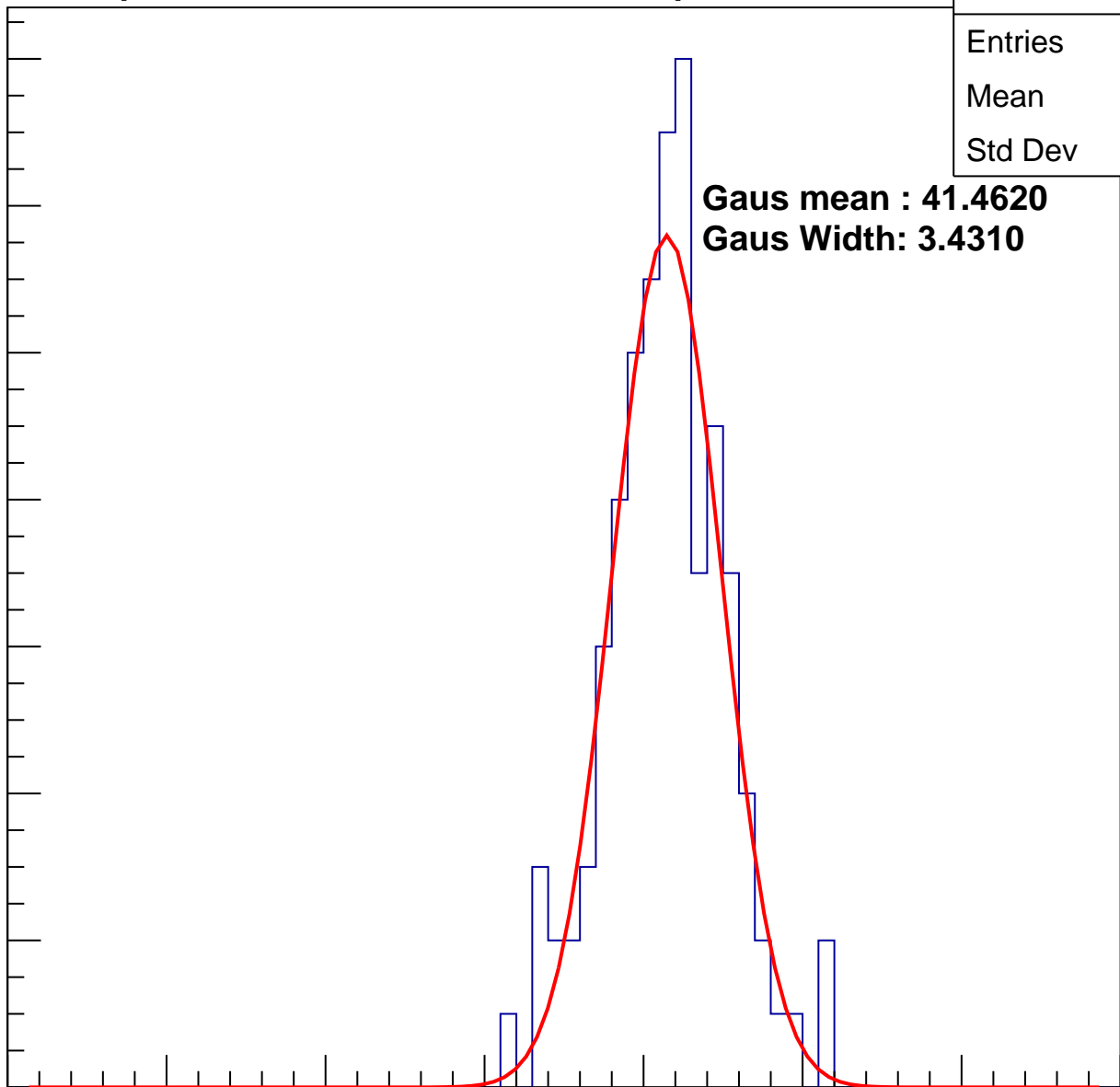
Entries	106
Mean	40.96
Std Dev	3.731

Gaus mean : 41.4620

Gaus Width: 3.4310

0 10 20 30 40 50 60 70

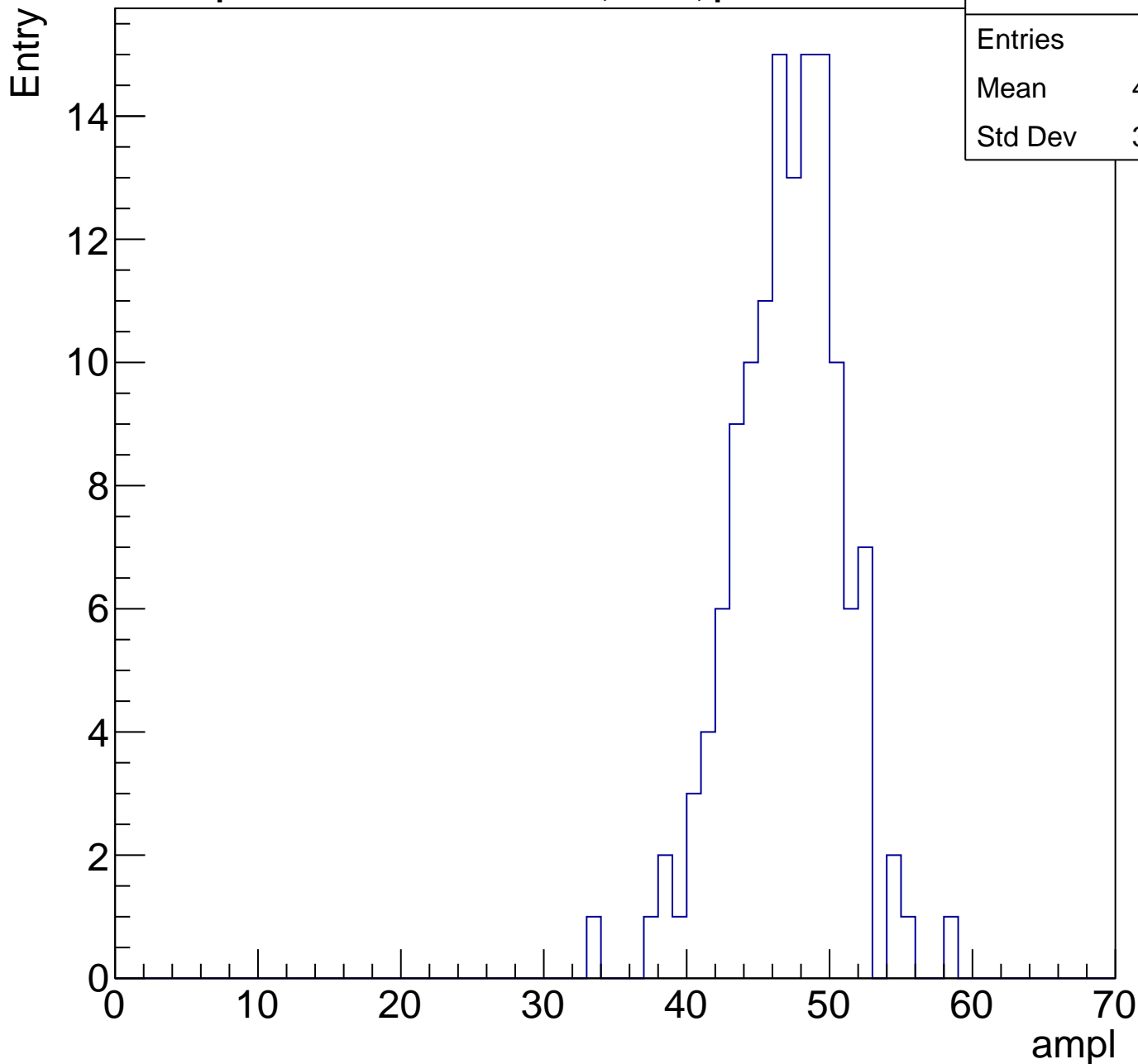
ampl



B0L101S, U19-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

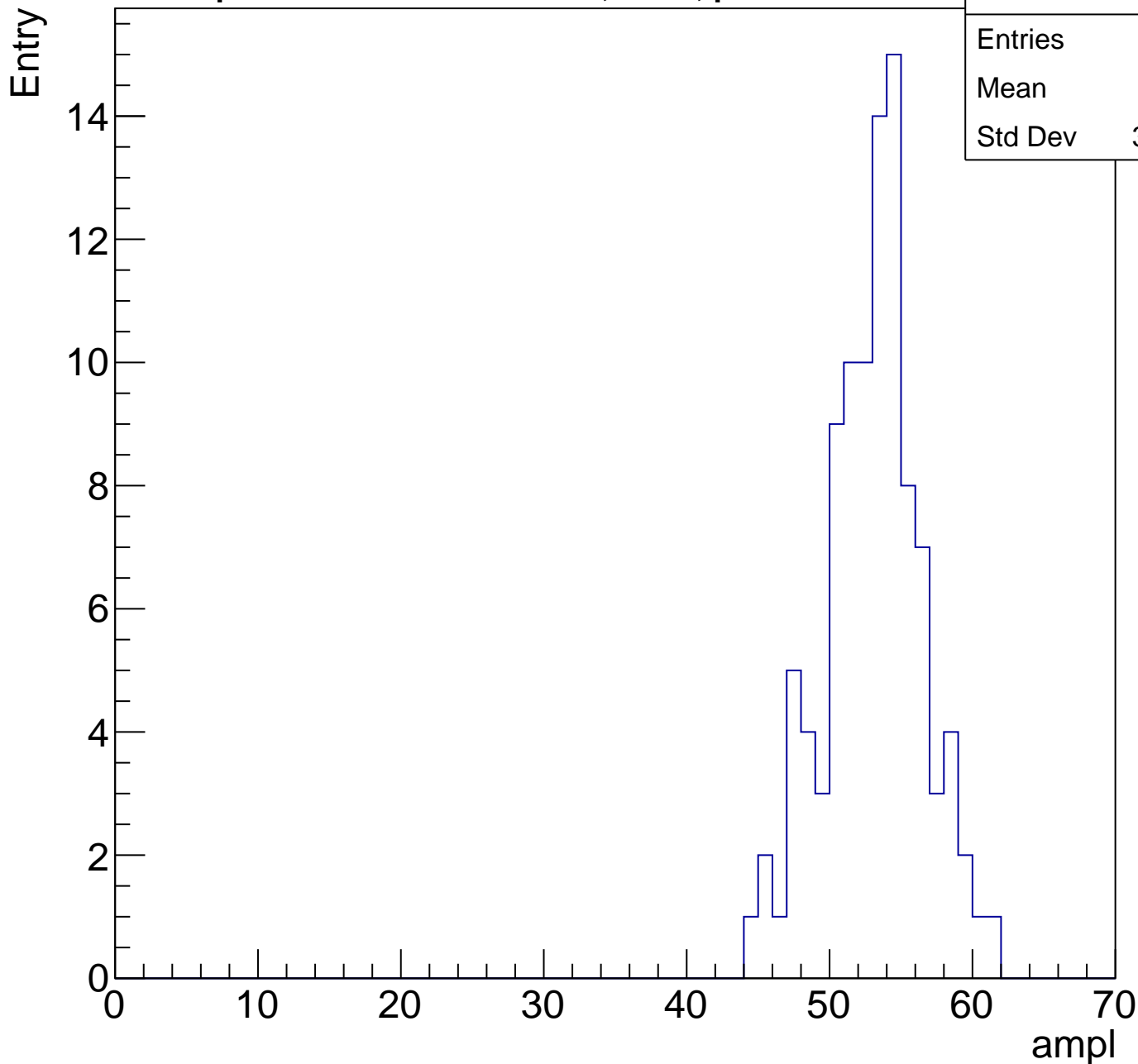
Entries	133
Mean	46.54
Std Dev	3.826



B0L101S, U19-ch94, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	52.6
Std Dev	3.382



B0L101S, U19-ch94, adc5

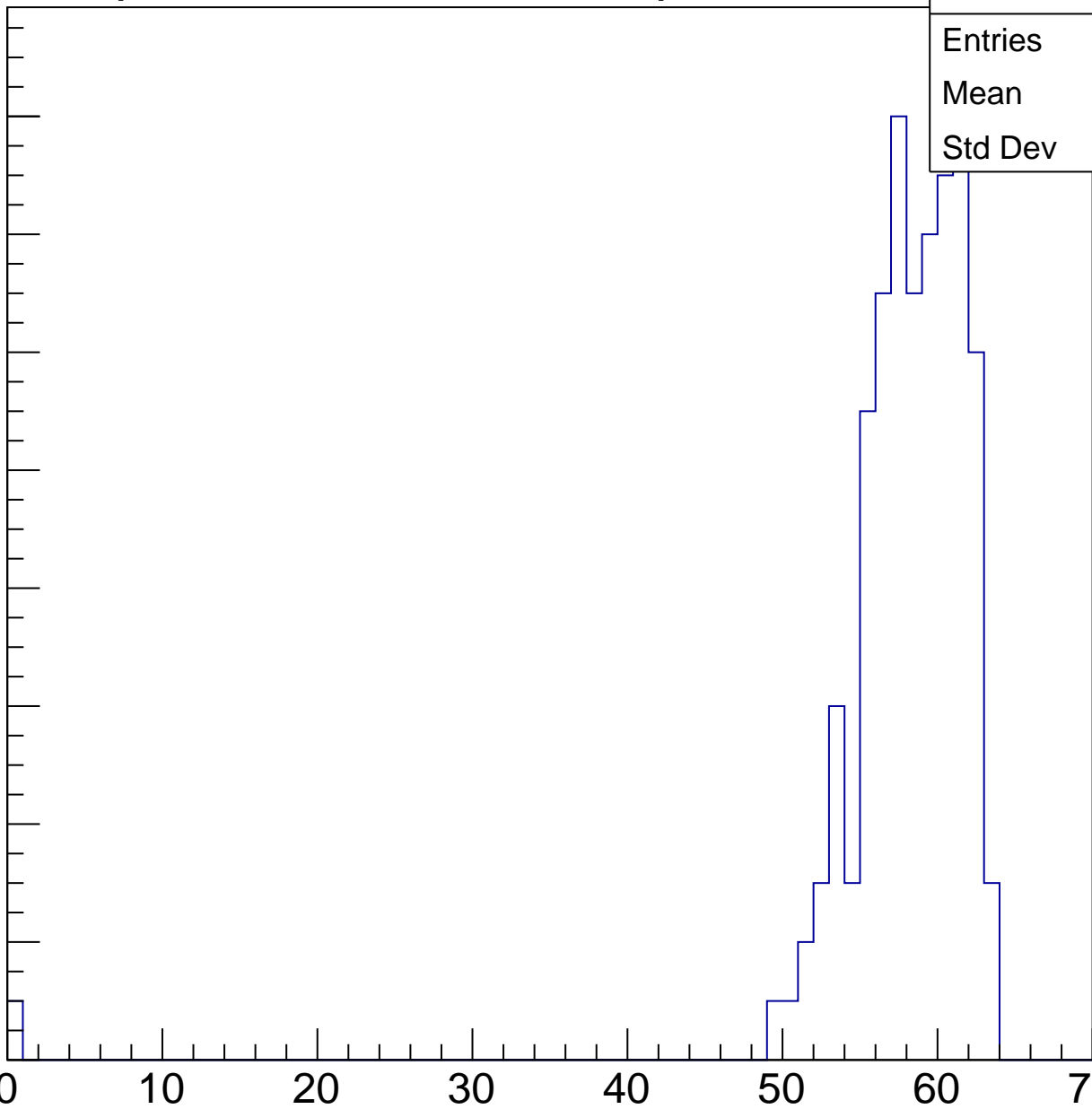
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	131
Mean	57.5
Std Dev	5.887

ampl



B0L101S, U19-ch94, adc6

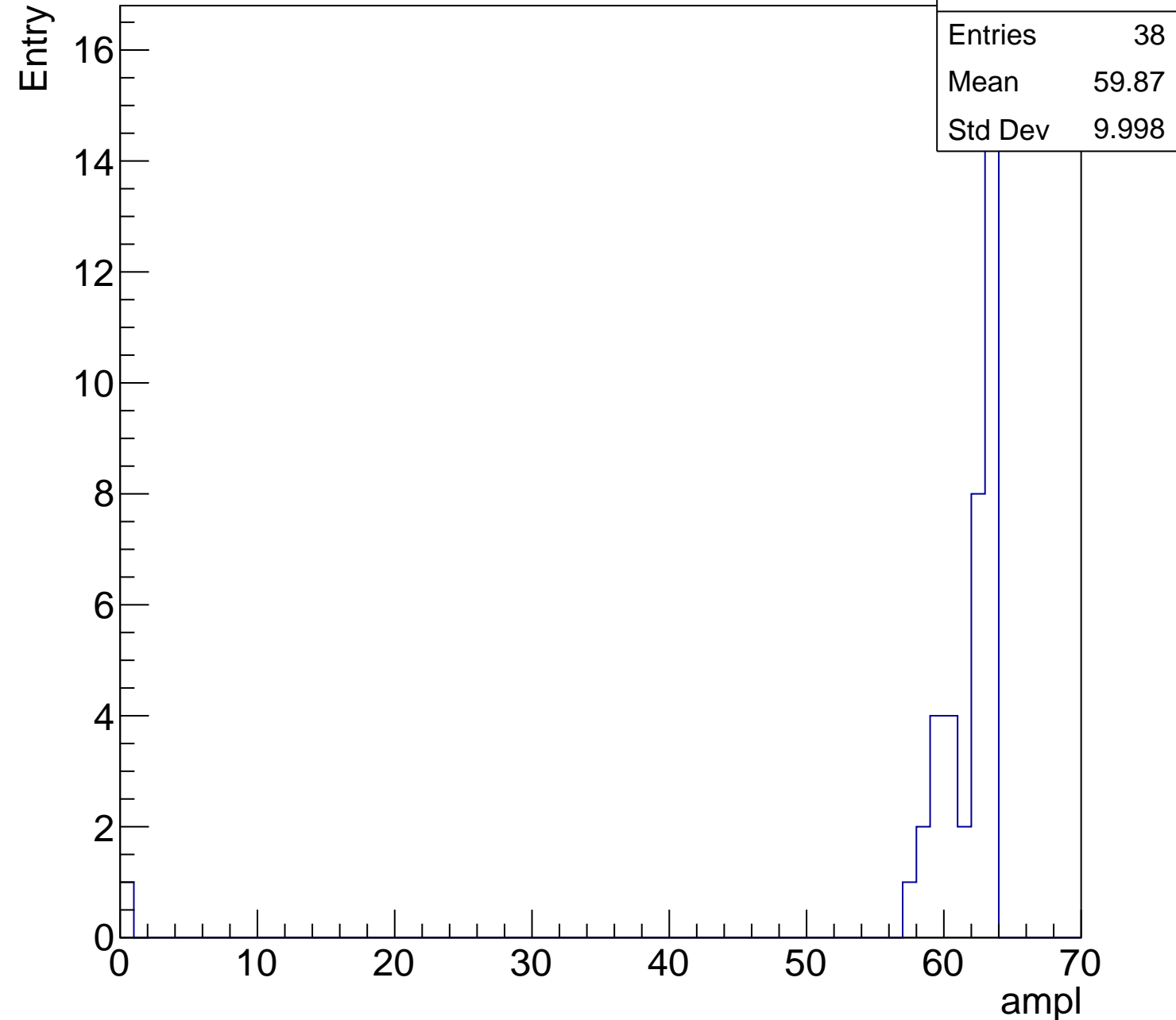
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	38
Mean	59.87
Std Dev	9.998

ampl



B0L101S, U19-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch95, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	29.07
Std Dev	3.411

Gaus mean : 29.6367

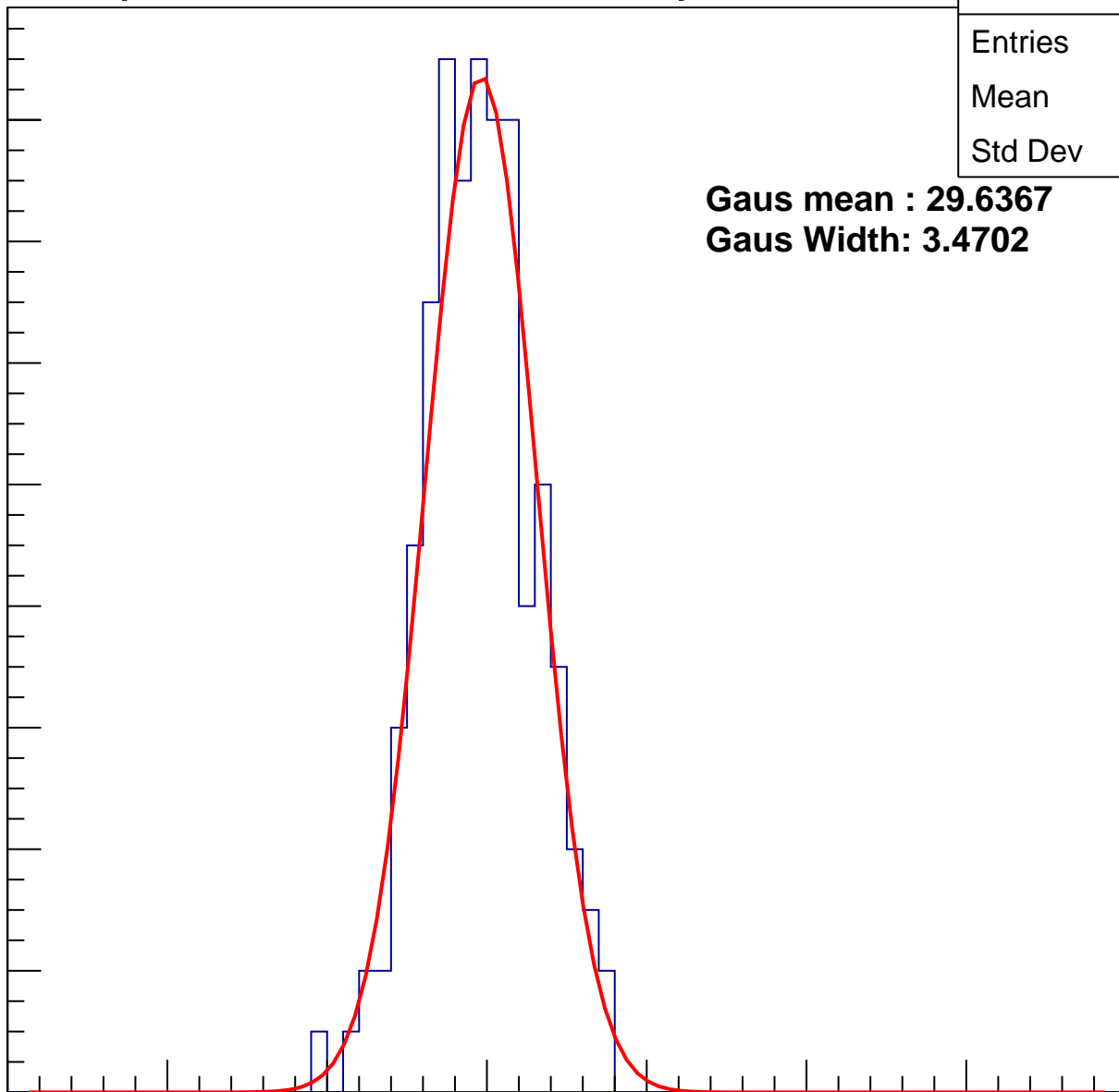
Gaus Width: 3.4702

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



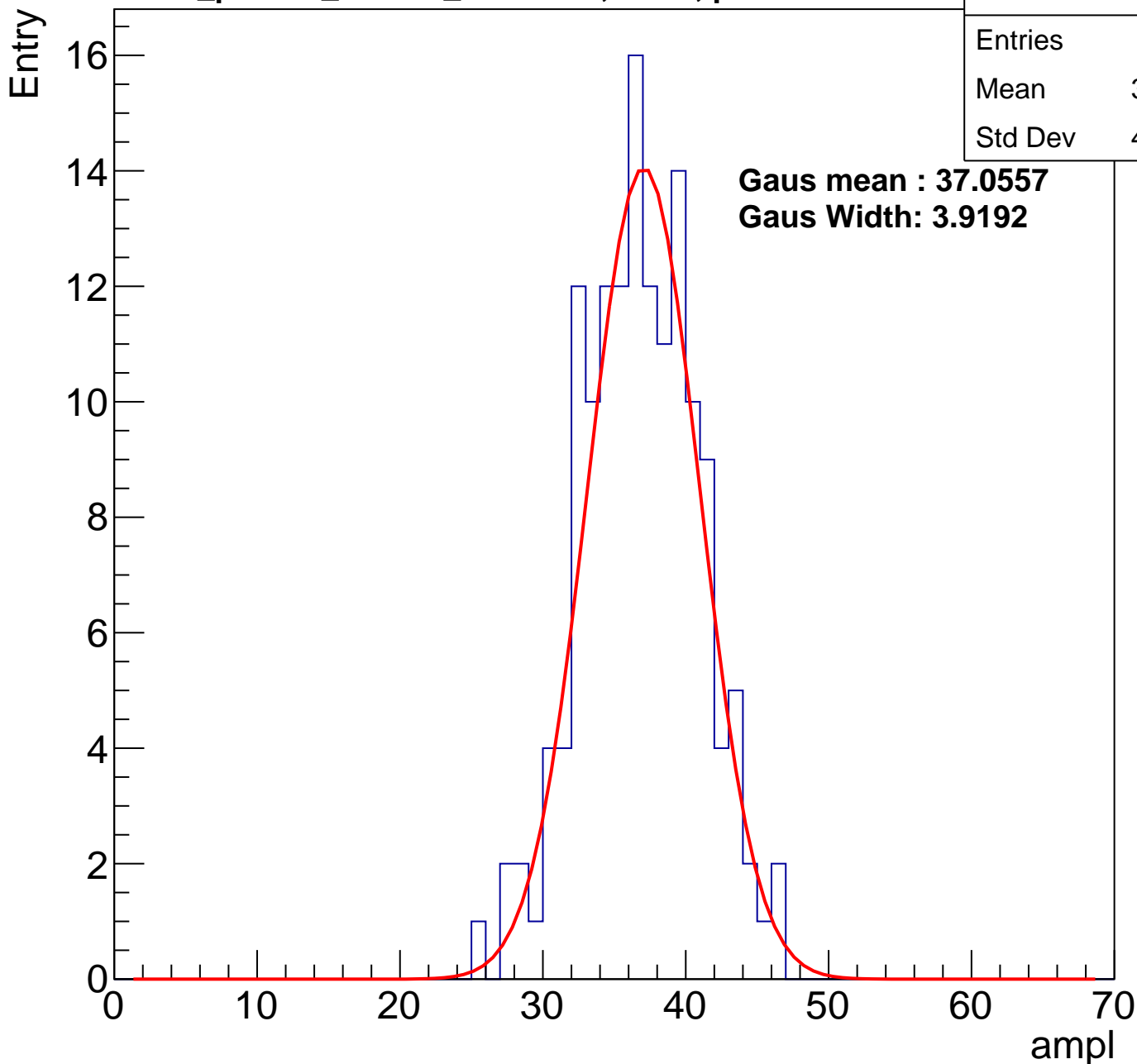
B0L101S, U19-ch95, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	36.38
Std Dev	4.046

Gaus mean : 37.0557

Gaus Width: 3.9192



B0L101S, U19-ch95, adc2

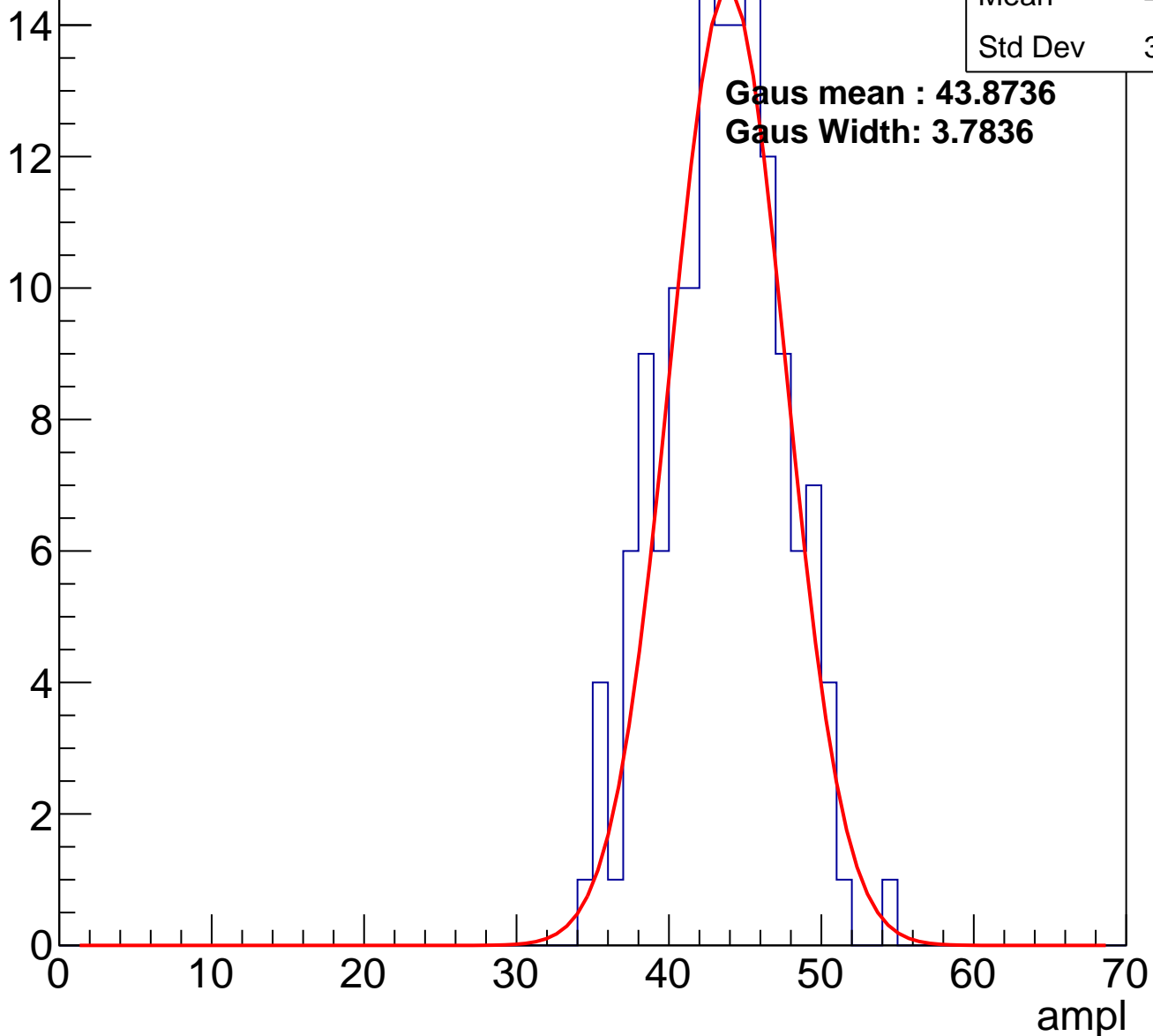
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	43.12
Std Dev	3.873

Gaus mean : 43.8736

Gaus Width: 3.7836

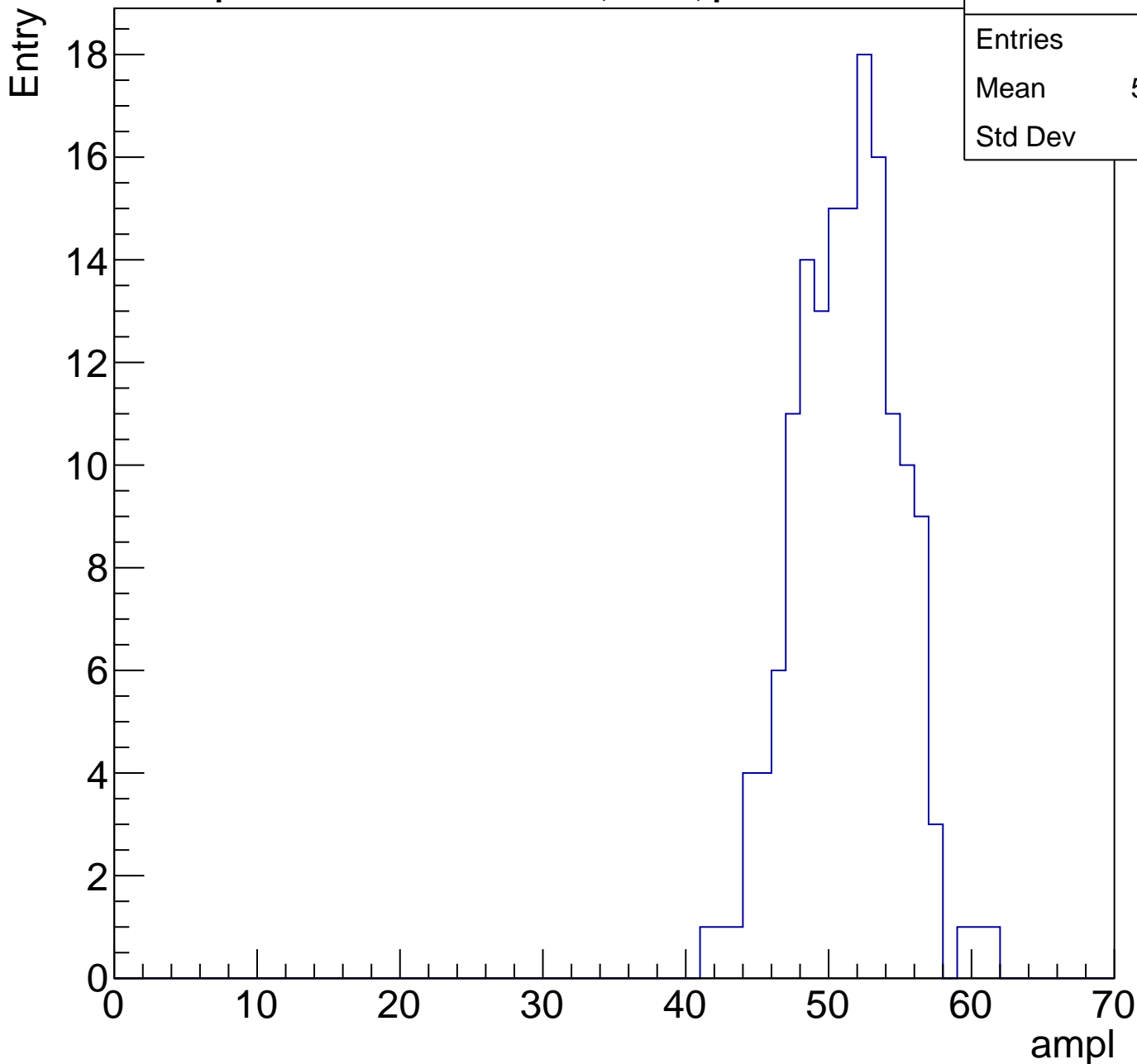
Entry



B0L101S, U19-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	50.85
Std Dev	3.61



B0L101S, U19-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

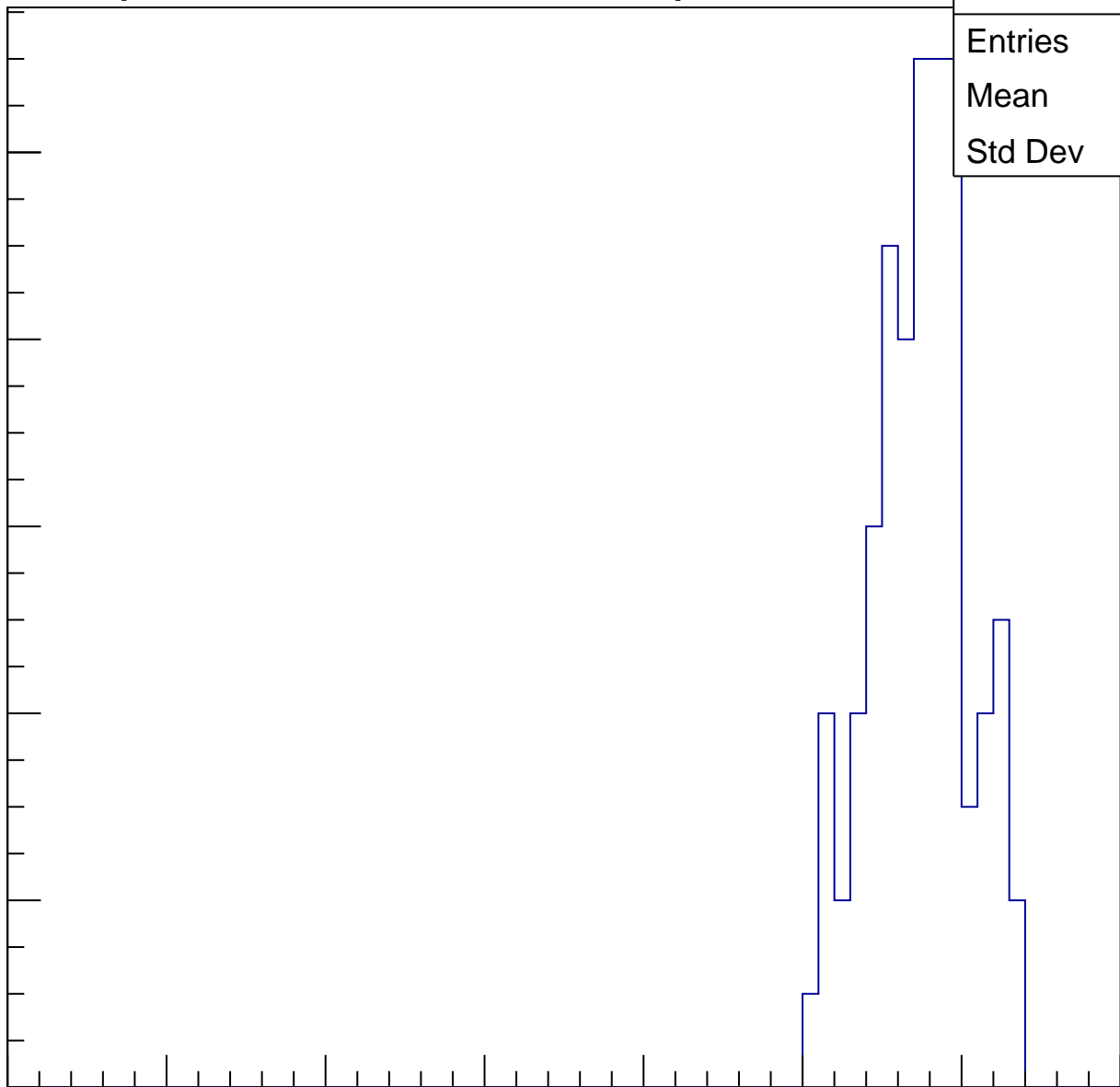
50

60

70

ampl

Entries	81
Mean	56.93
Std Dev	3.038



B0L101S, U19-ch95, adc5

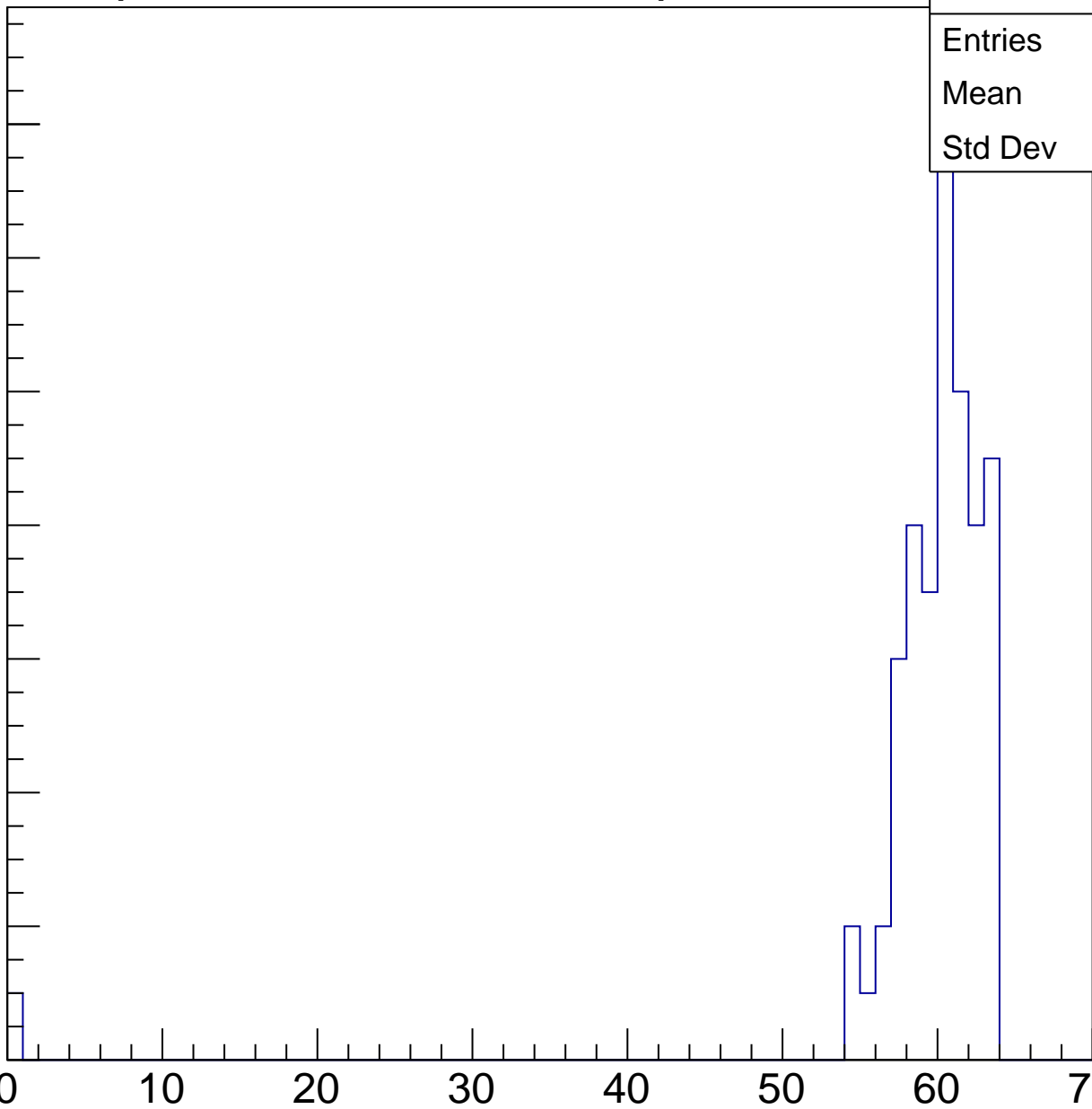
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	58.94
Std Dev	7.487

ampl

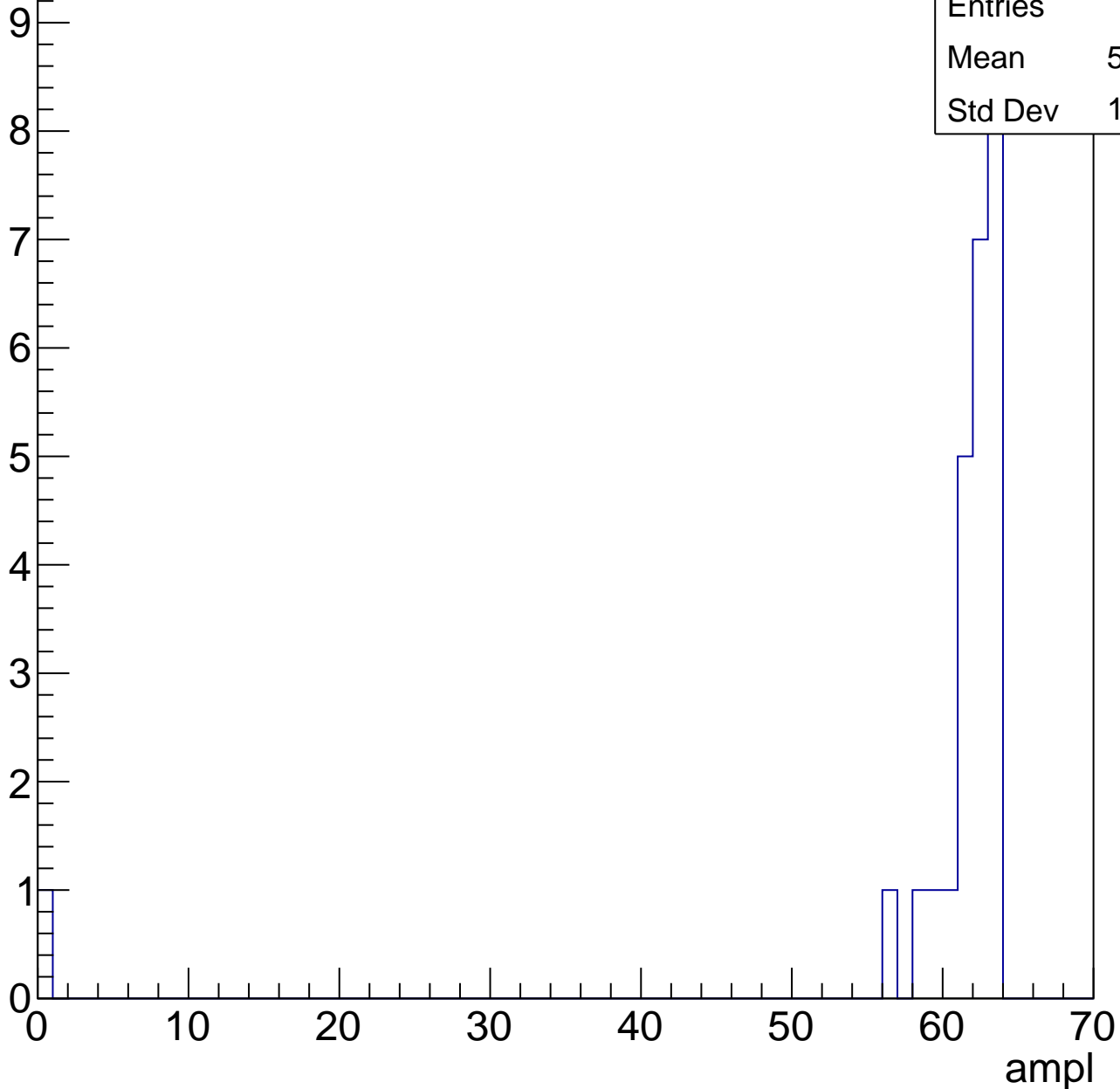


B0L101S, U19-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	26
Mean	59.19
Std Dev	11.96



B0L101S, U19-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

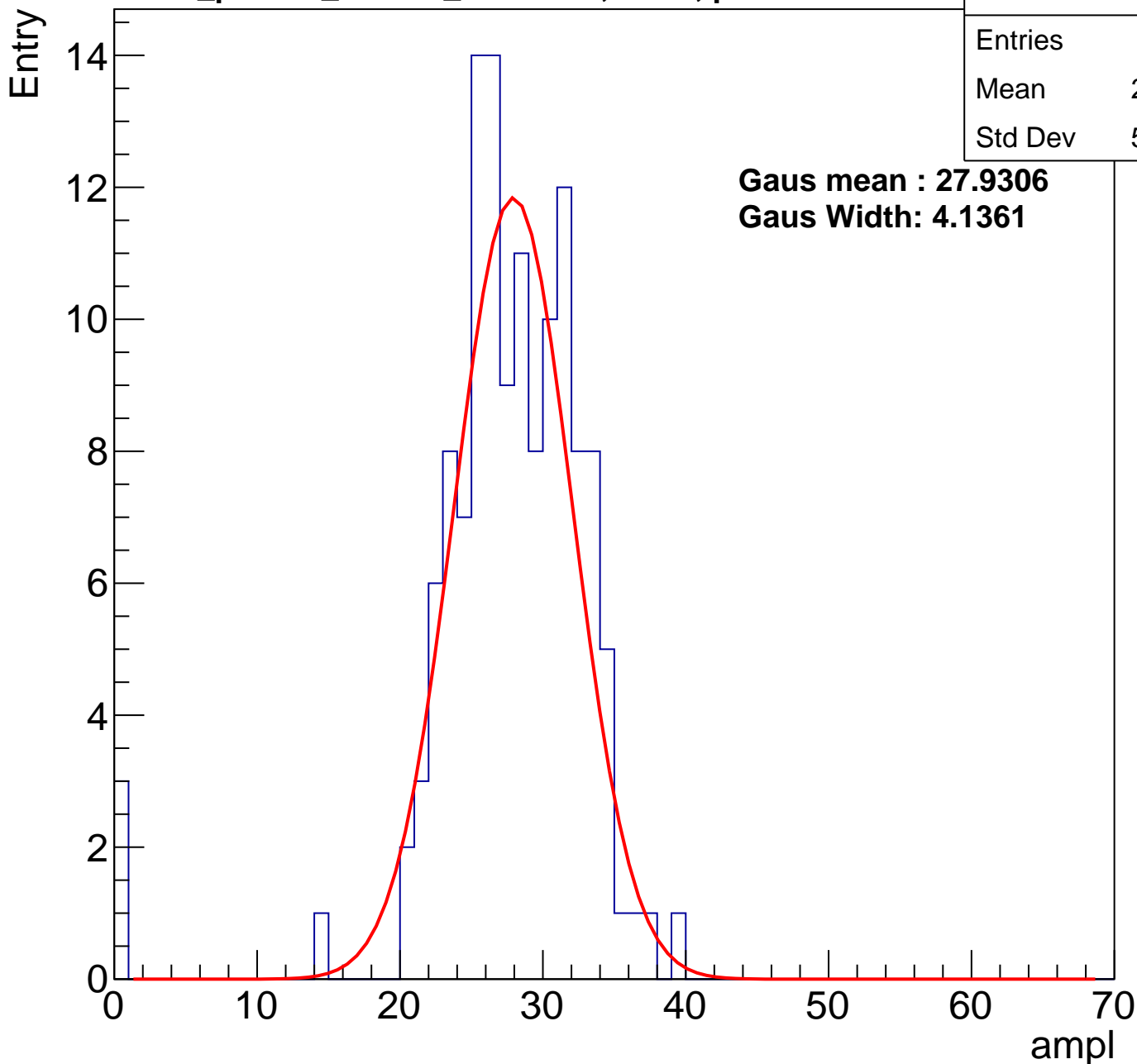
B0L101S, U19-ch96, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	27.12
Std Dev	5.764

Gaus mean : 27.9306

Gaus Width: 4.1361



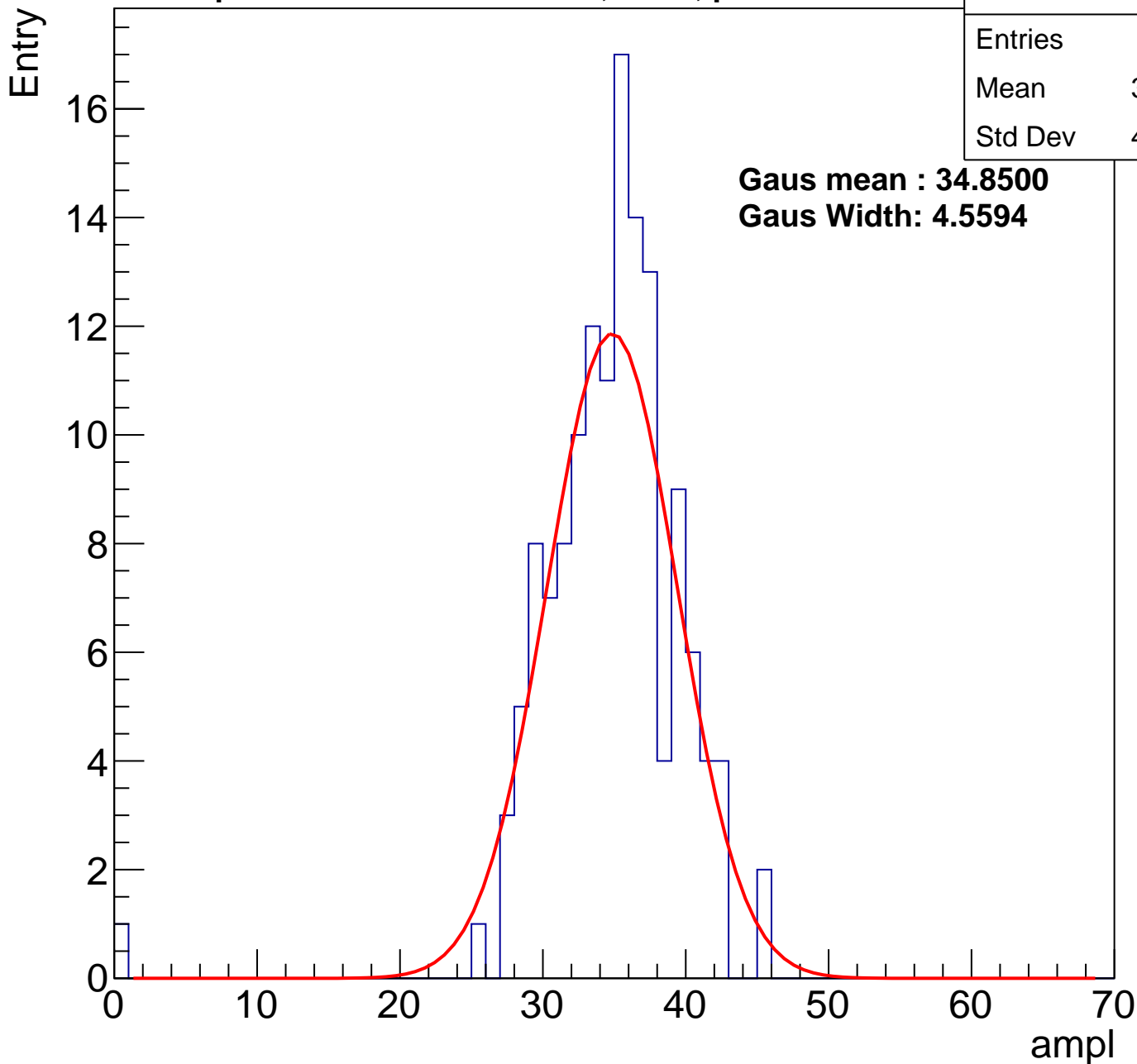
B0L101S, U19-ch96, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	34.32
Std Dev	4.927

Gaus mean : 34.8500

Gaus Width: 4.5594



B0L101S, U19-ch96, adc2

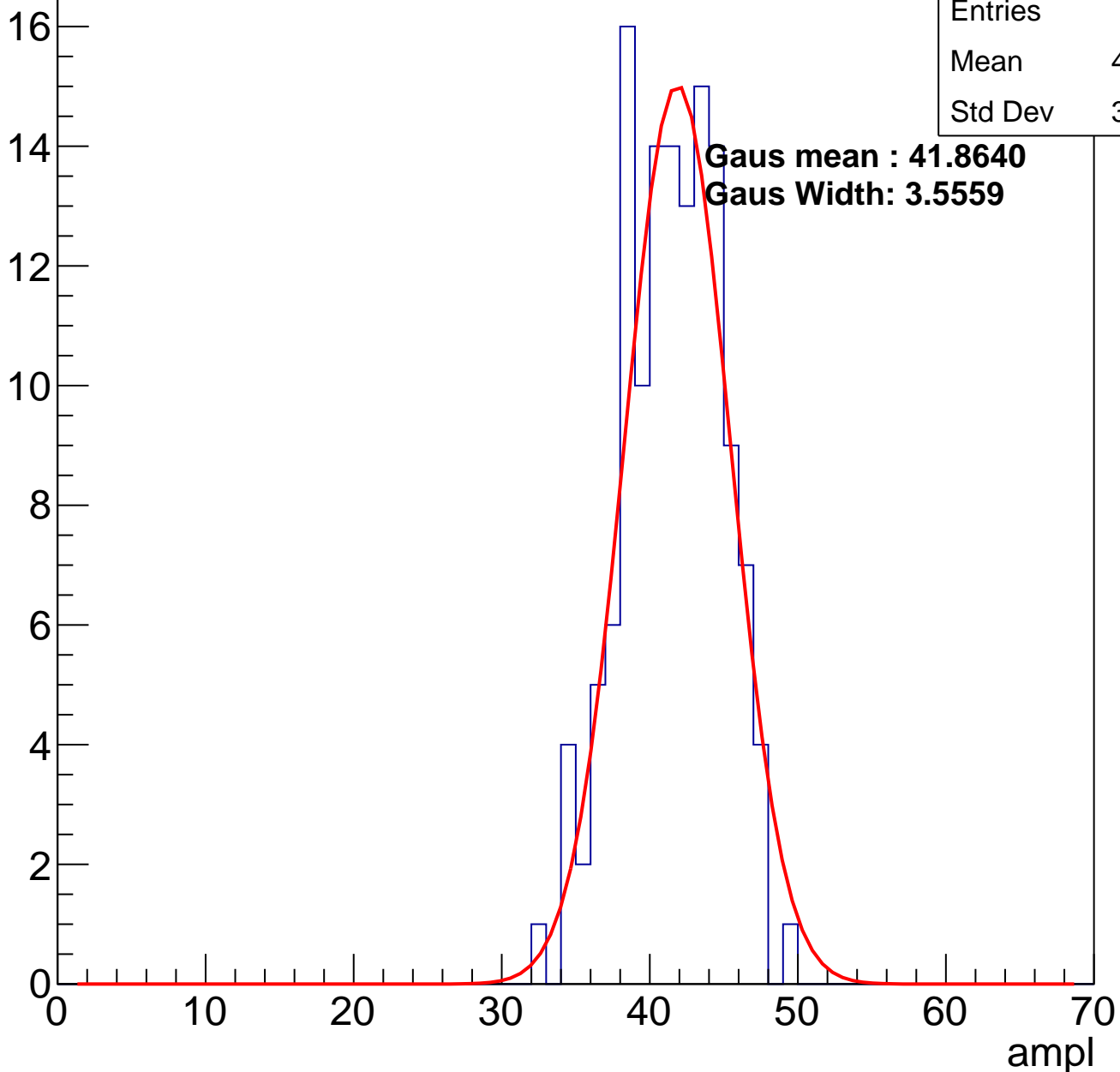
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	41.06
Std Dev	3.326

Gaus mean : 41.8640

Gaus Width: 3.5559

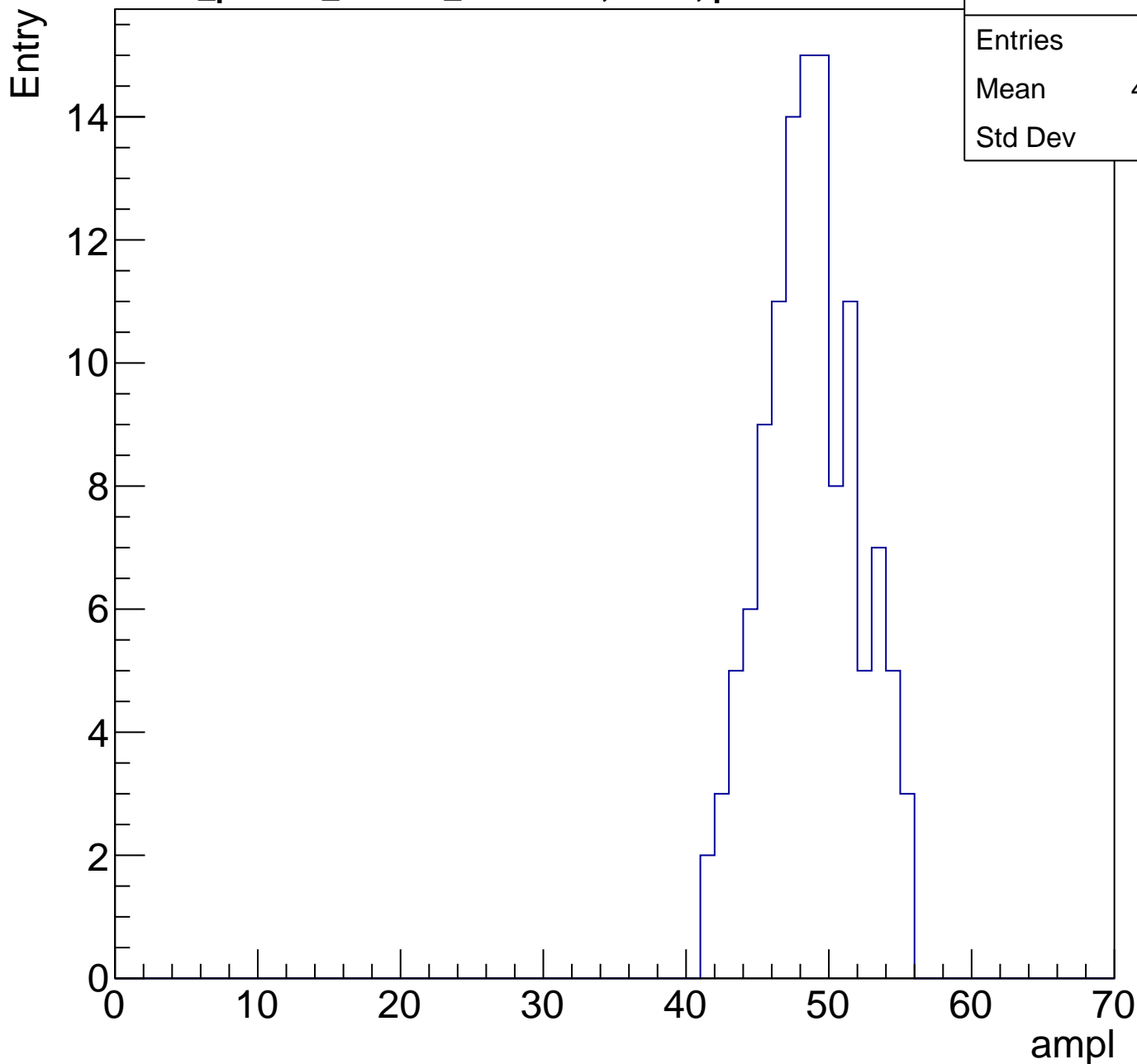
Entry



B0L101S, U19-ch96, adc3

calib_packv5_042523_0143.root, FC#1, port C1

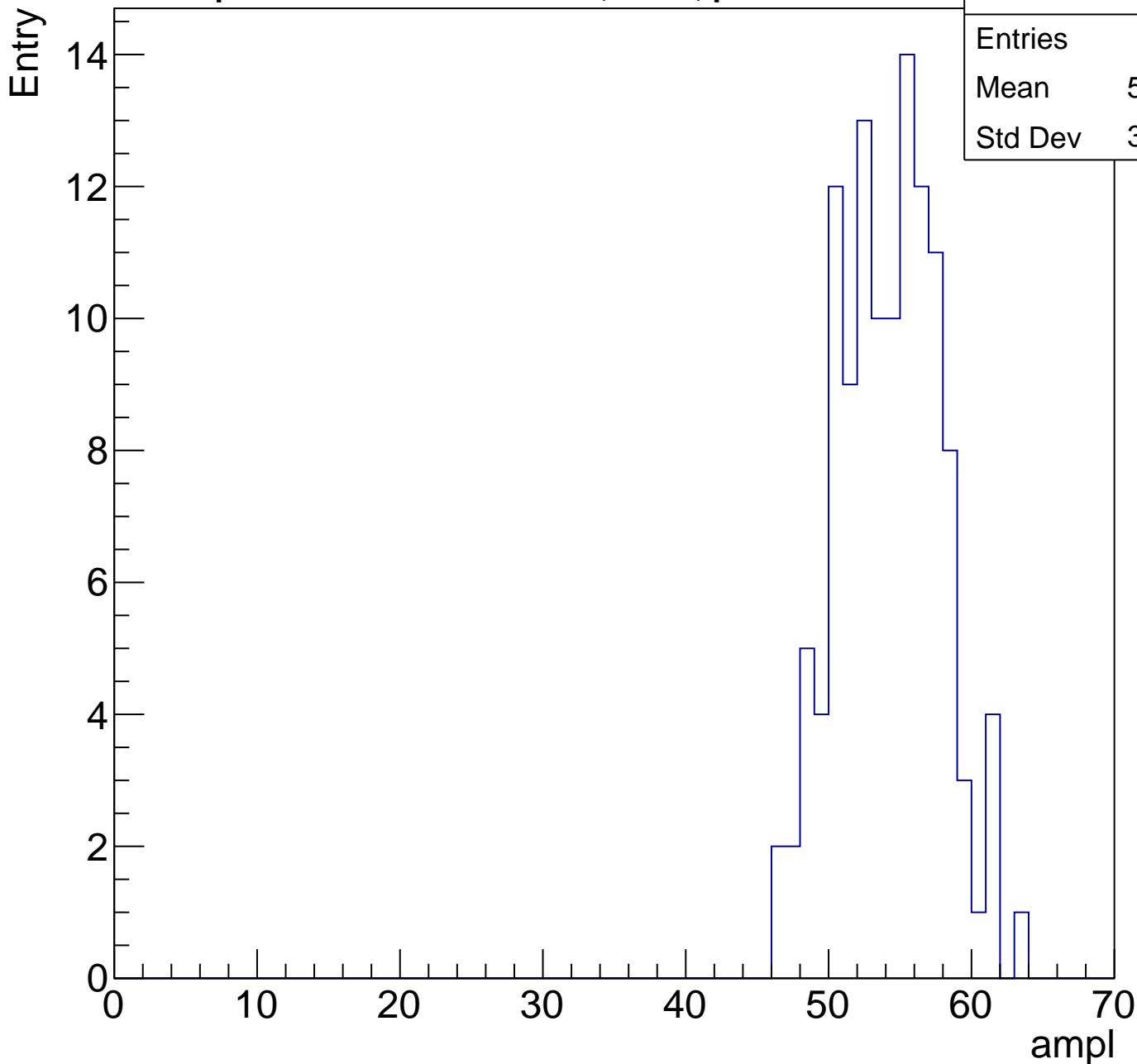
Entries	119
Mean	48.22
Std Dev	3.29



B0L101S, U19-ch96, adc4

calib_packv5_042523_0143.root, FC#1, port C1

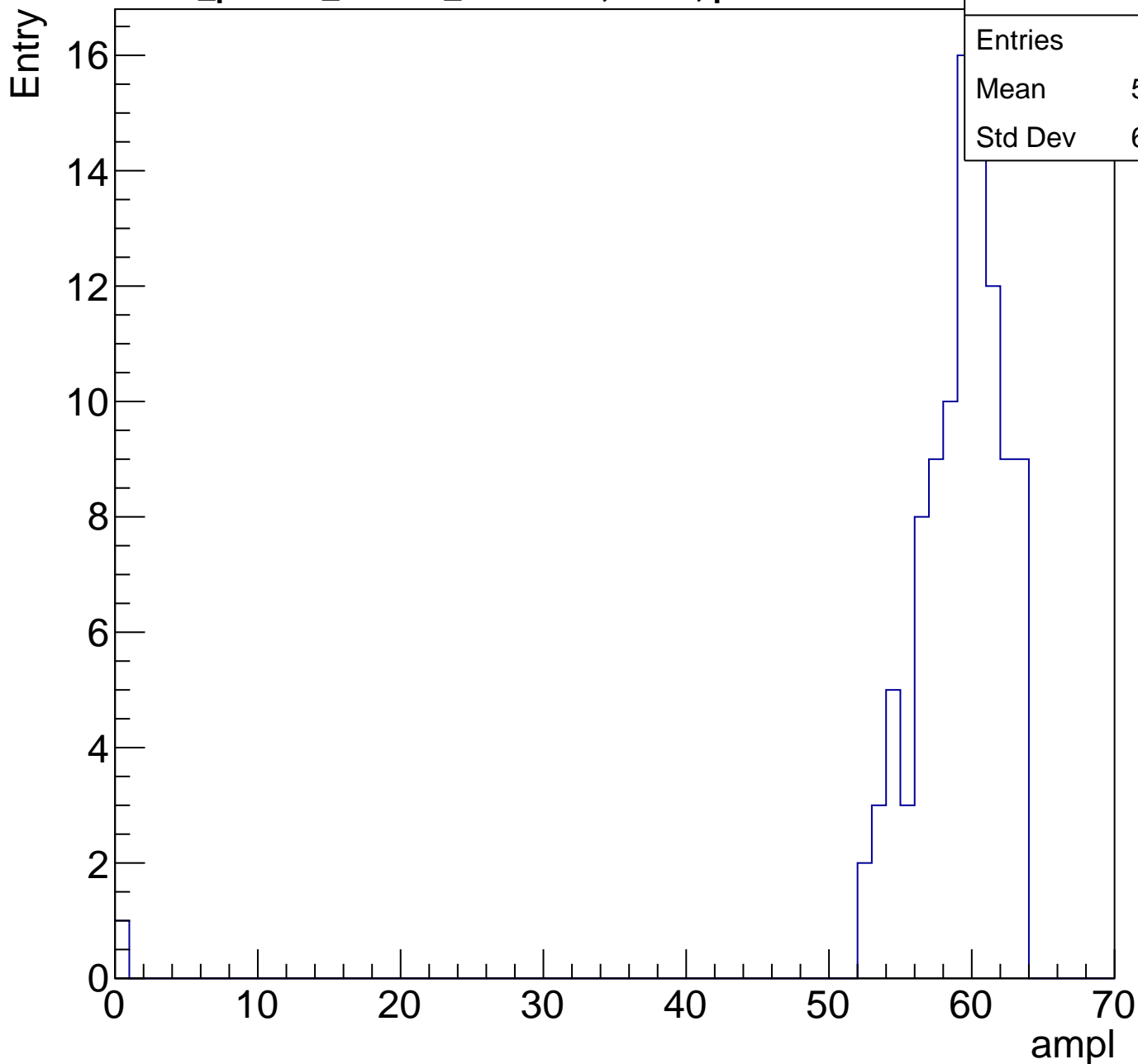
Entries	121
Mean	53.75
Std Dev	3.533



B0L101S, U19-ch96, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	58.24
Std Dev	6.422



B0L101S, U19-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	32
Mean	59.62
Std Dev	10.8

0

B0L101S, U19-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch97, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	30.01
Std Dev	4.471

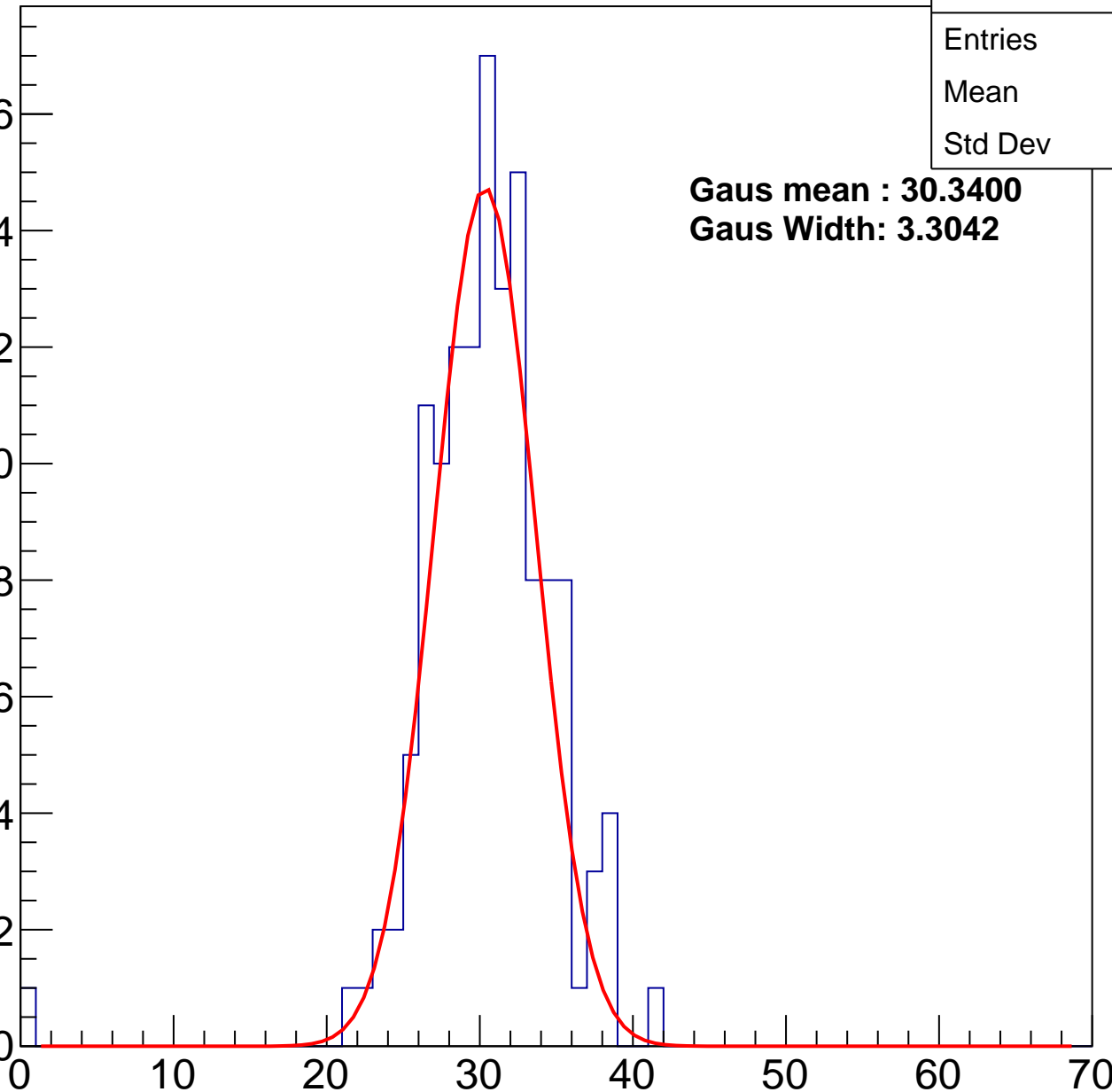
Gaus mean : 30.3400

Gaus Width: 3.3042

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch97, adc1

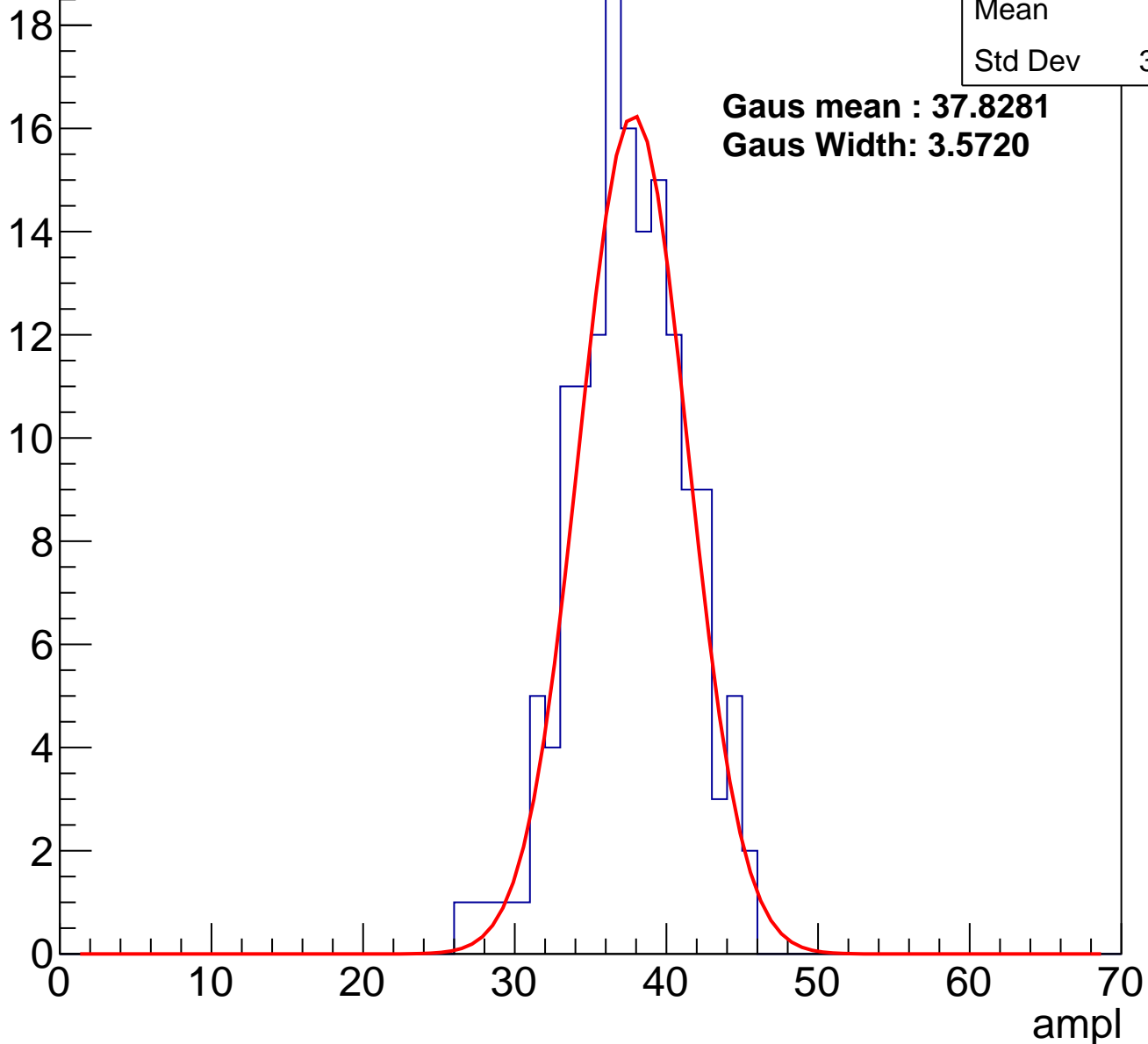
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	37.1
Std Dev	3.688

Gaus mean : 37.8281

Gaus Width: 3.5720

Entry



B0L101S, U19-ch97, adc2

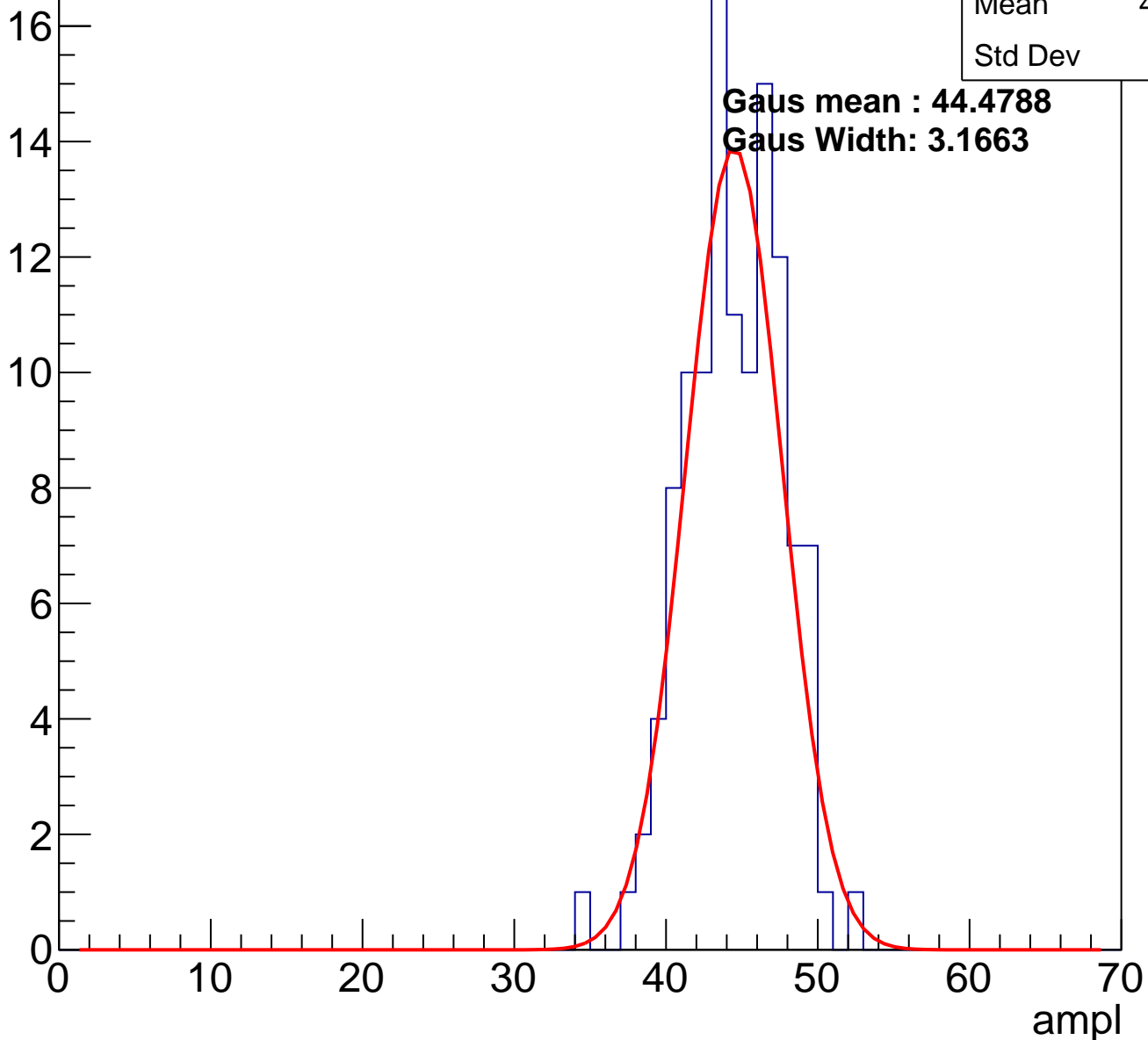
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	117
Mean	44.04
Std Dev	3.15

Gaus mean : 44.4788

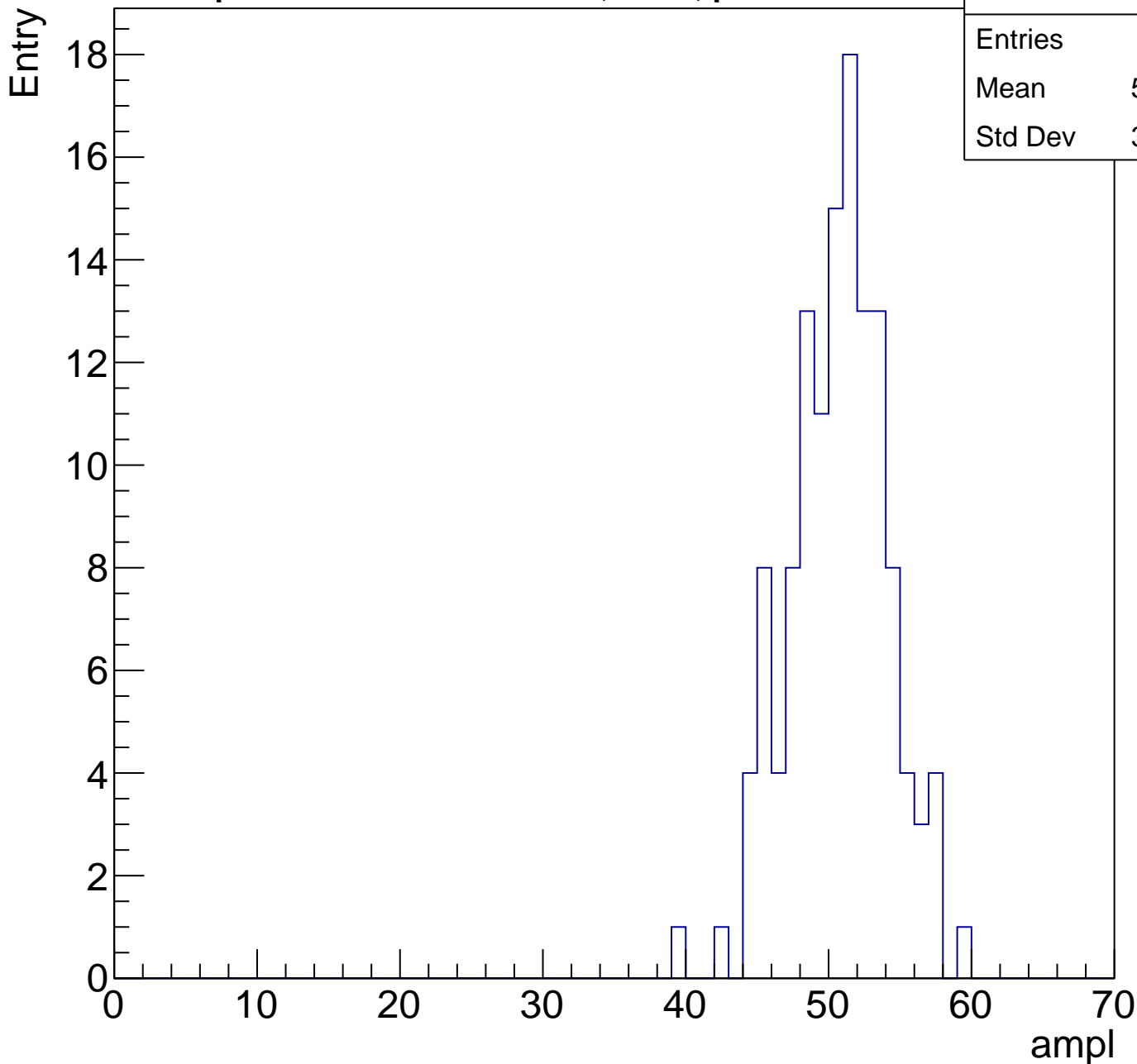
Gaus Width: 3.1663



B0L101S, U19-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	50.23
Std Dev	3.434



B0L101S, U19-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

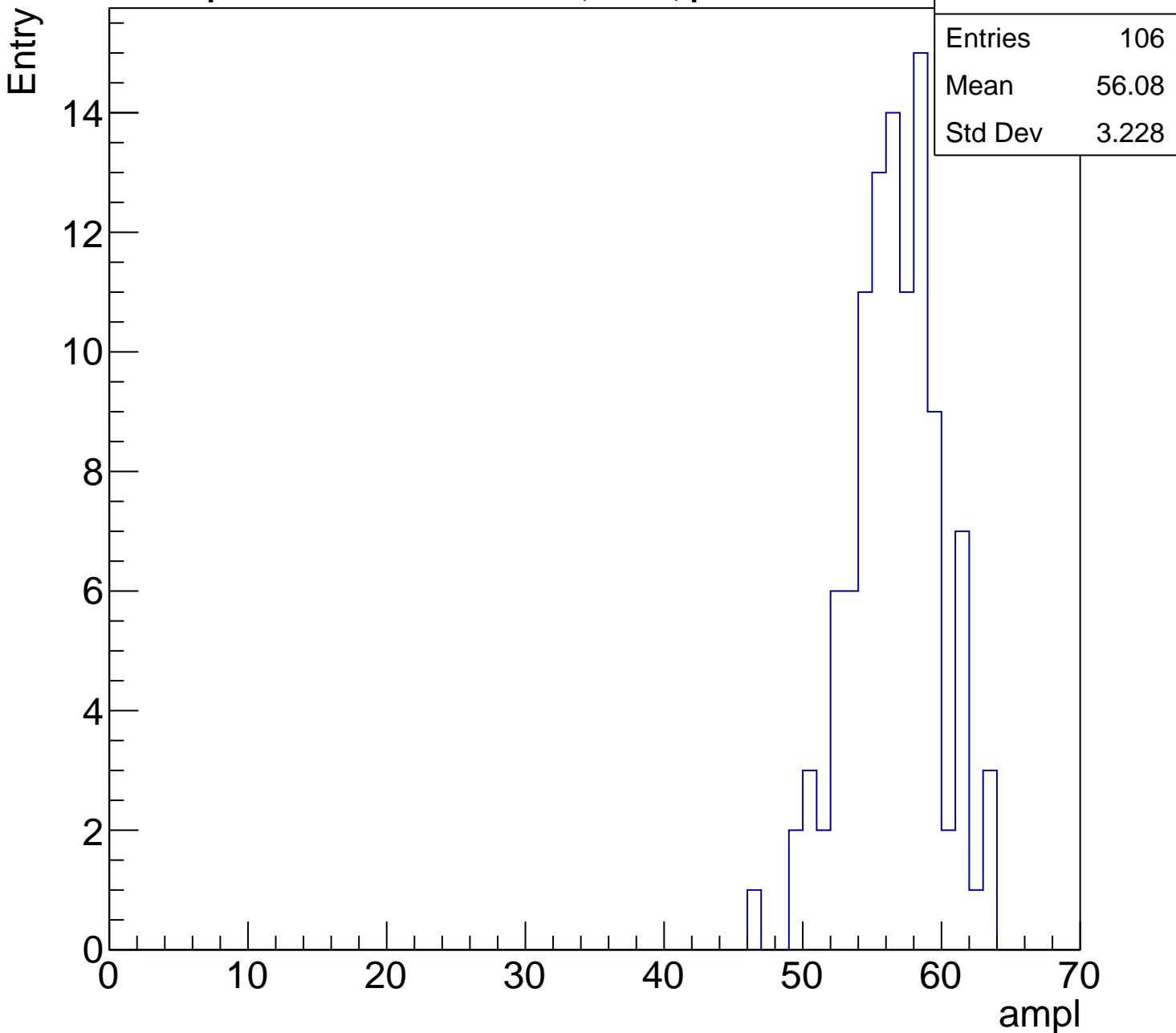
Entries	106
Mean	56.08
Std Dev	3.228

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch97, adc5

calib_packv5_042523_0143.root, FC#1, port C1

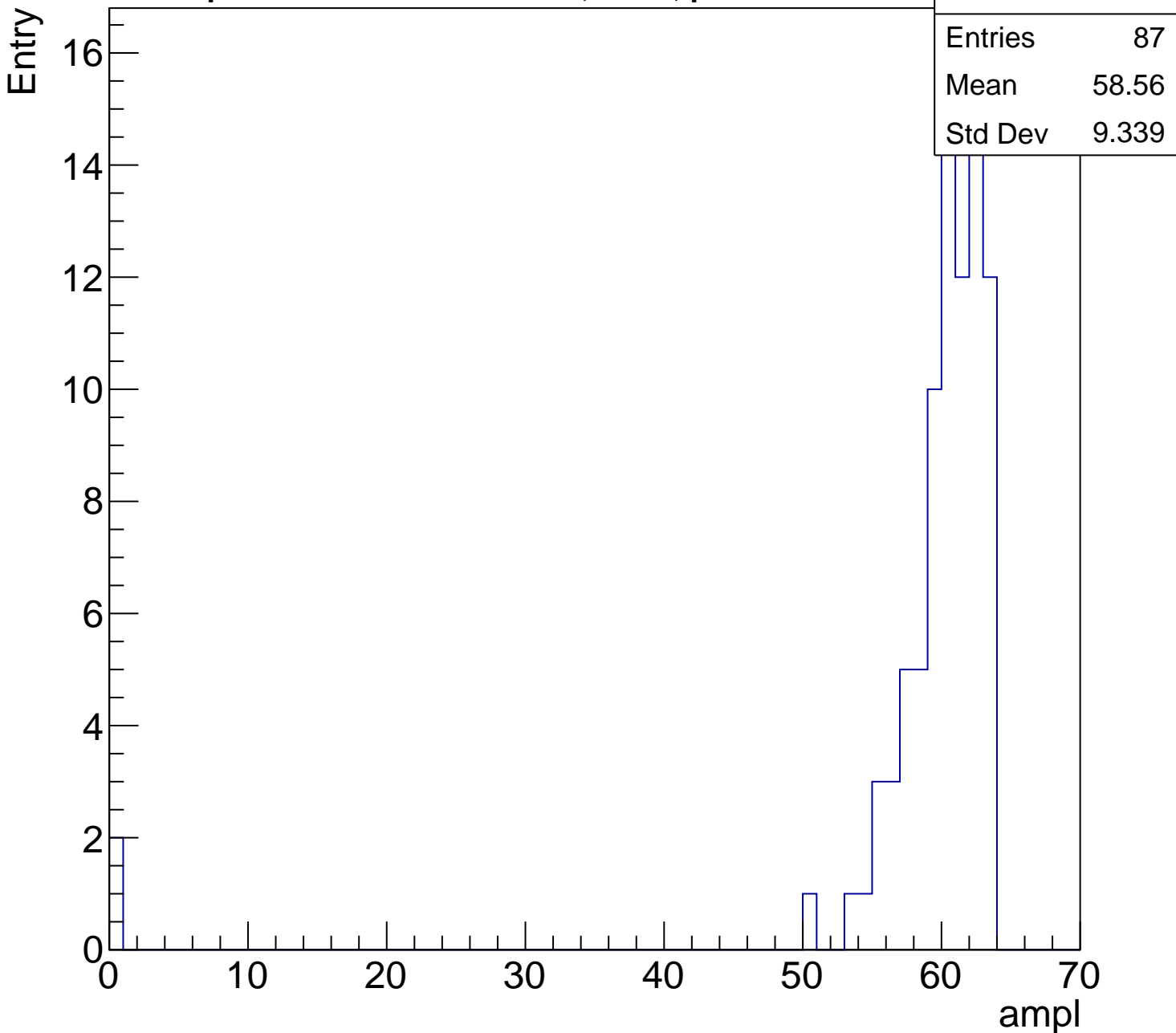
Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.56
Std Dev	9.339

ampl

0 10 20 30 40 50 60 70

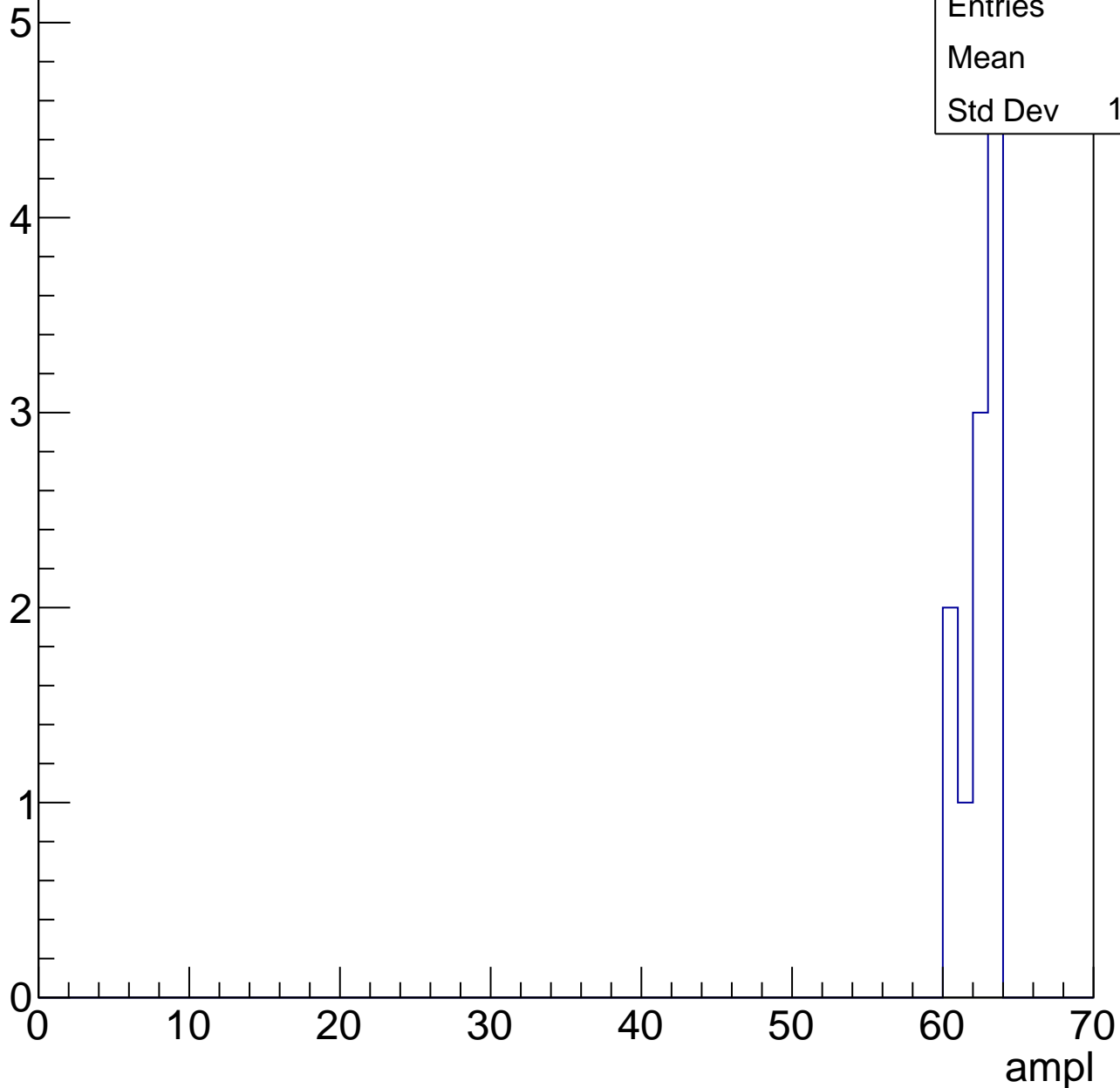


B0L101S, U19-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	62
Std Dev	1.128



B0L101S, U19-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch98, adc0

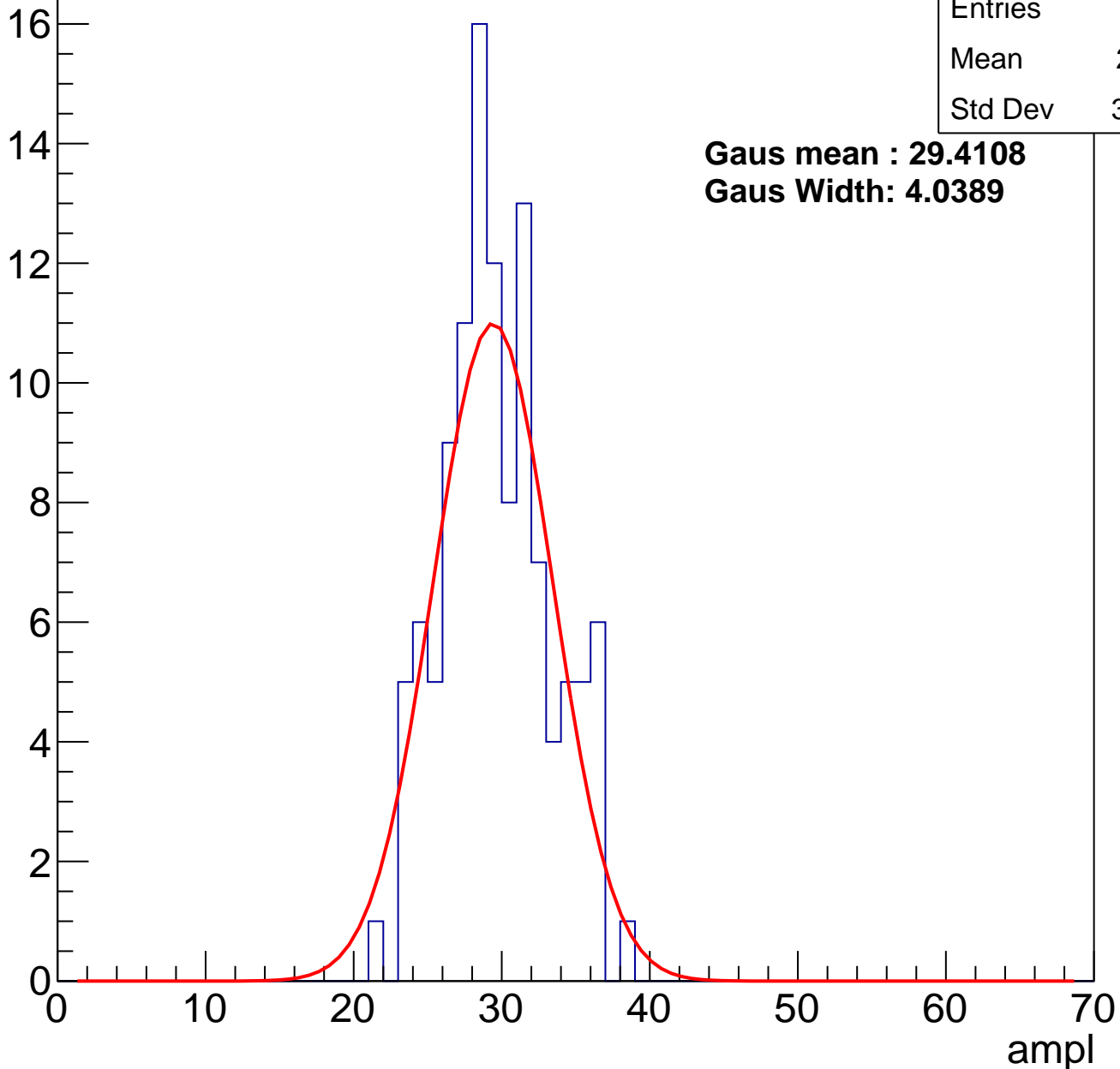
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	29.21
Std Dev	3.599

Gaus mean : 29.4108

Gaus Width: 4.0389

Entry



B0L101S, U19-ch98, adc1

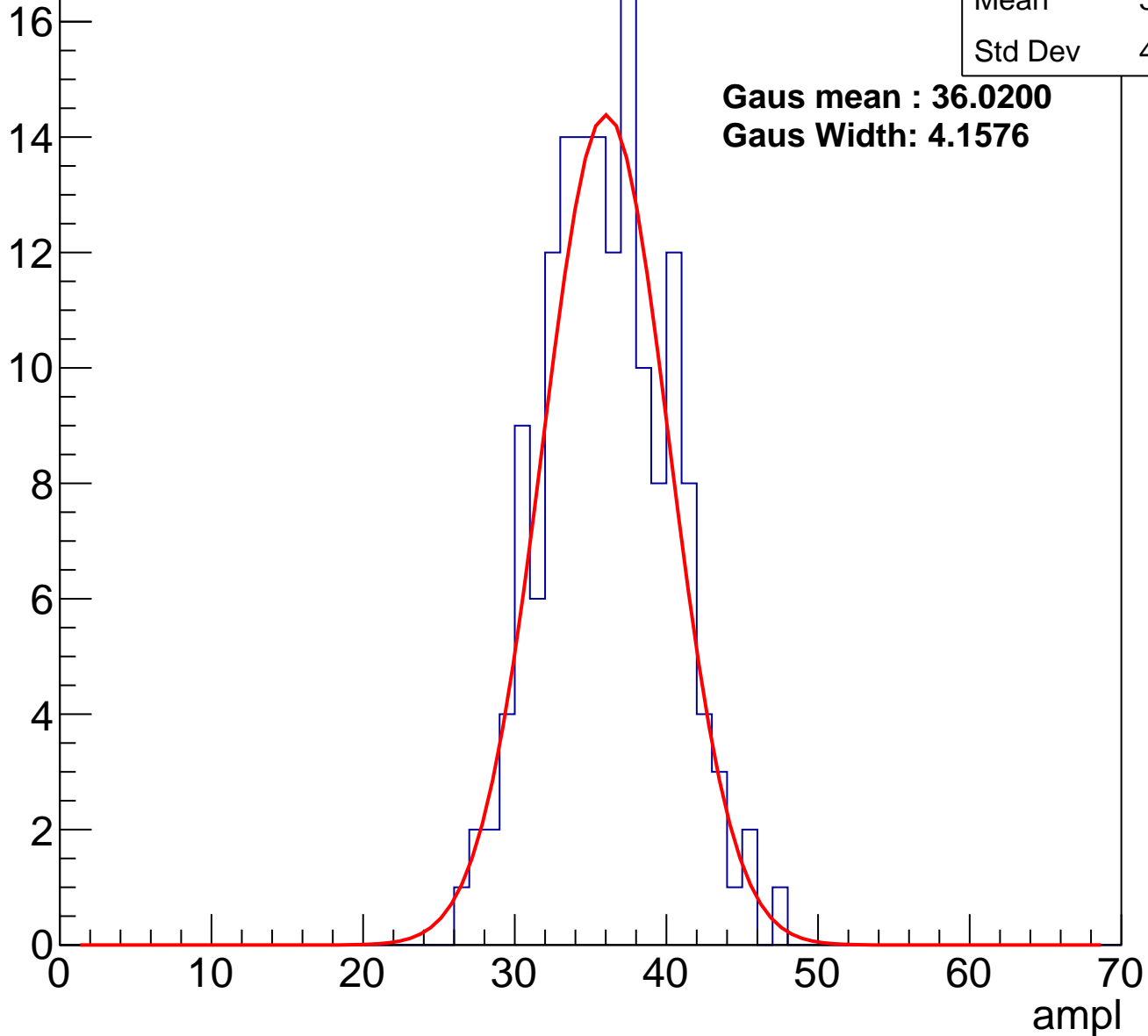
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	35.63
Std Dev	4.072

Gaus mean : 36.0200

Gaus Width: 4.1576

Entry



B0L101S, U19-ch98, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	42.48
Std Dev	3.842

Gaus mean : 42.9436

Gaus Width: 3.7408

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U19-ch98, adc3

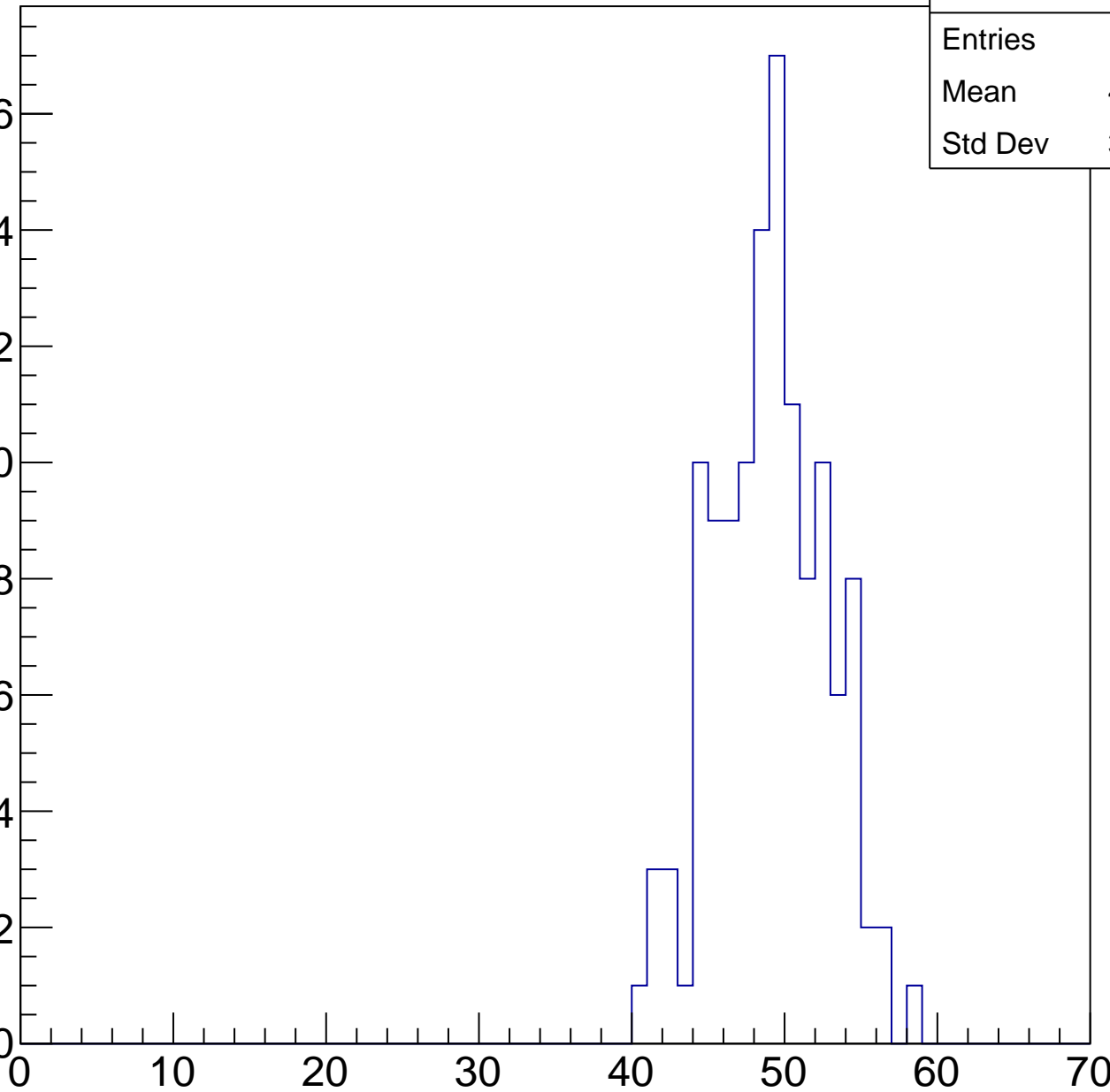
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	125
Mean	48.59
Std Dev	3.627

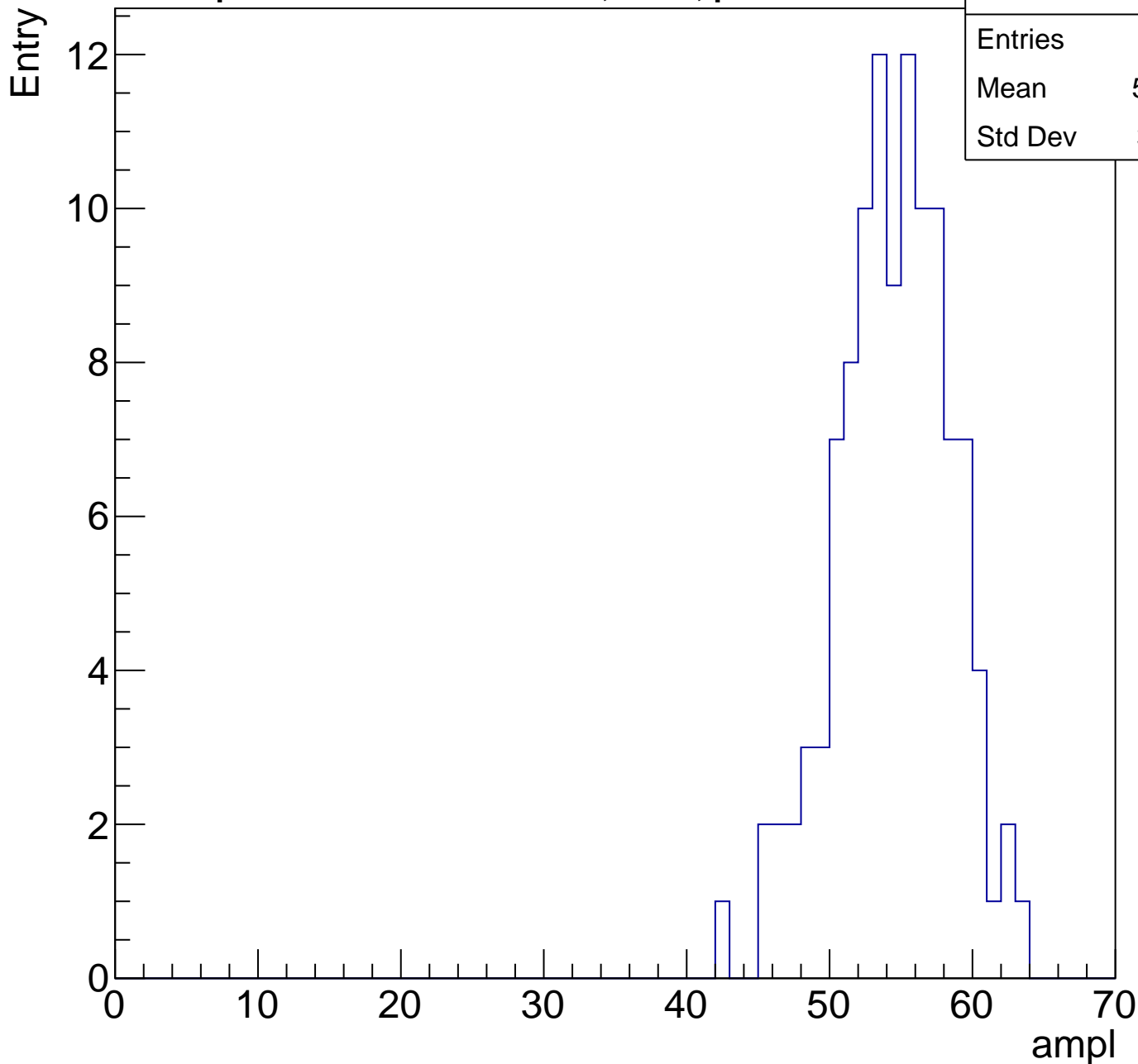
ampl



B0L101S, U19-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

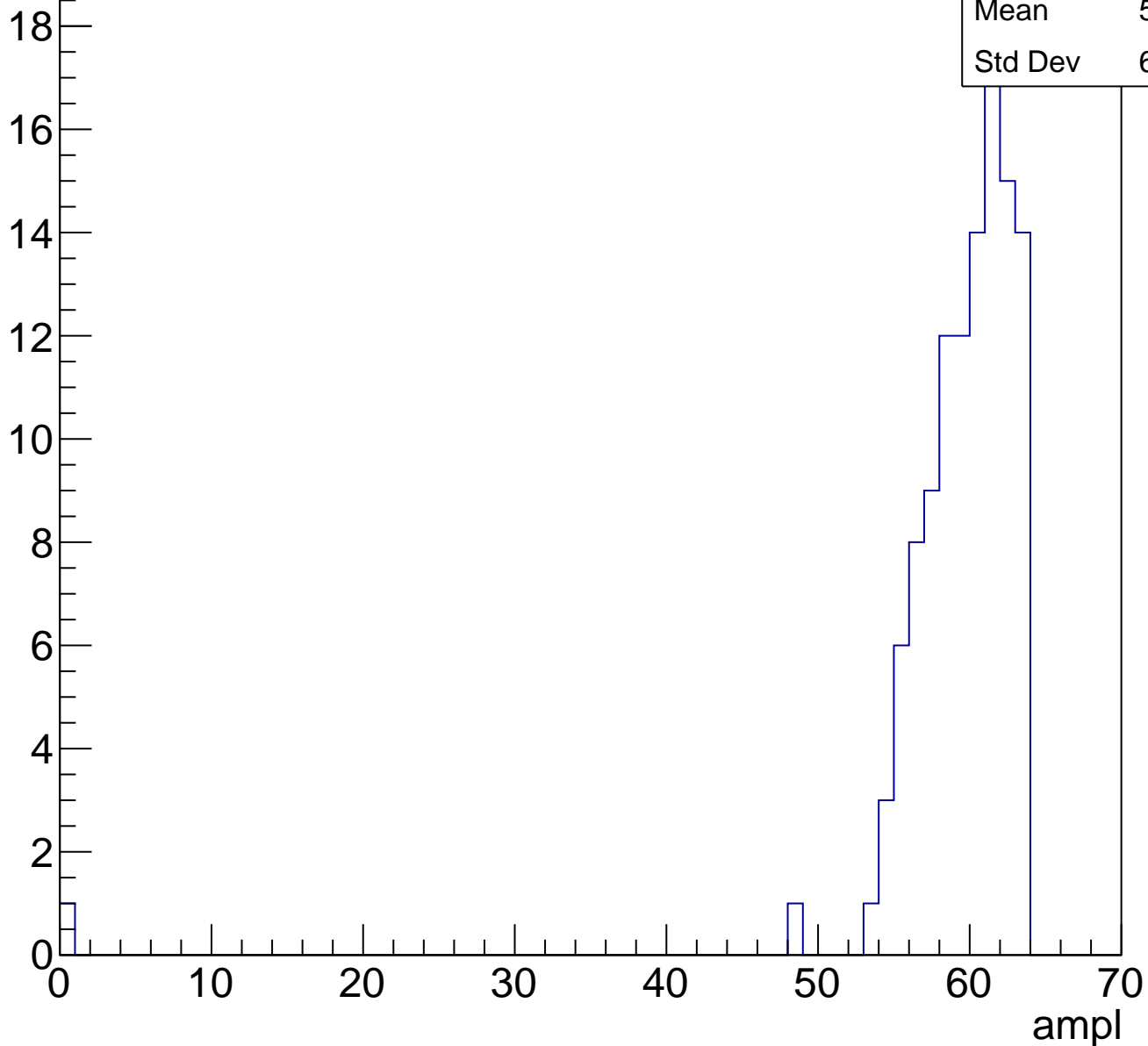
Entries	113
Mean	54.04
Std Dev	3.971



B0L101S, U19-ch98, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

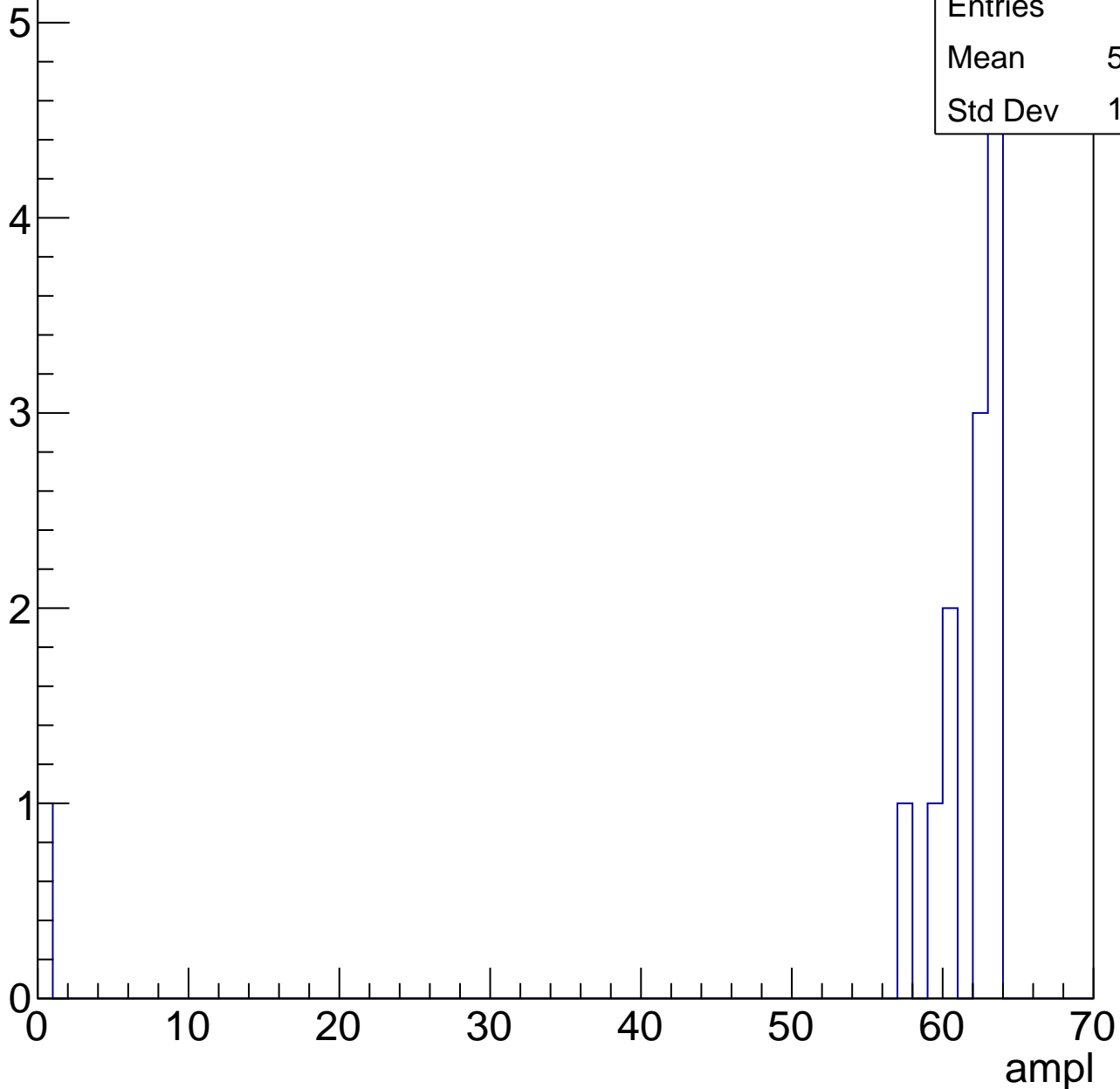


B0L101S, U19-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	56.69
Std Dev	16.47



B0L101S, U19-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch99, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	29.14
Std Dev	3.224

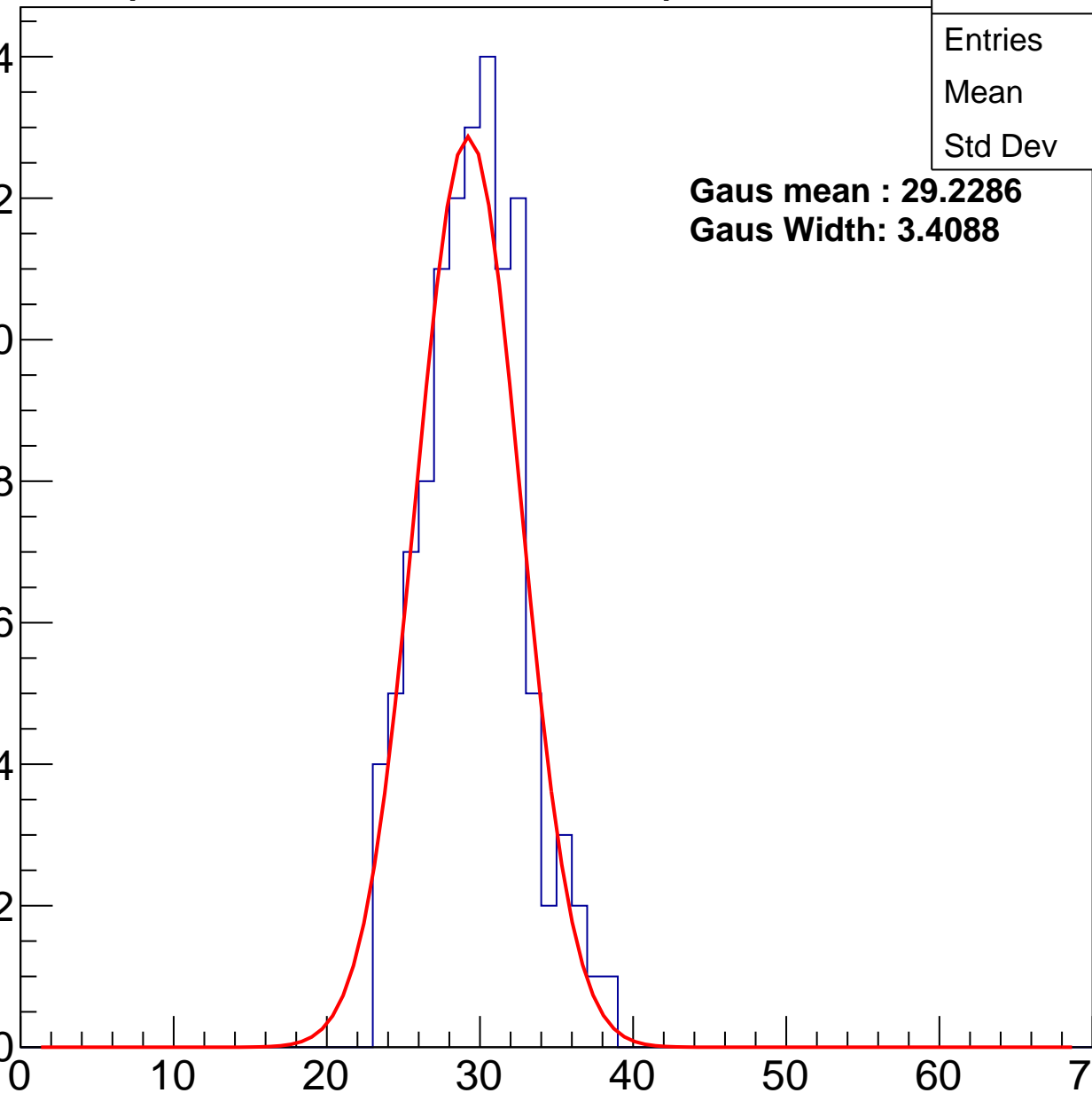
Gaus mean : 29.2286

Gaus Width: 3.4088

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch99, adc1

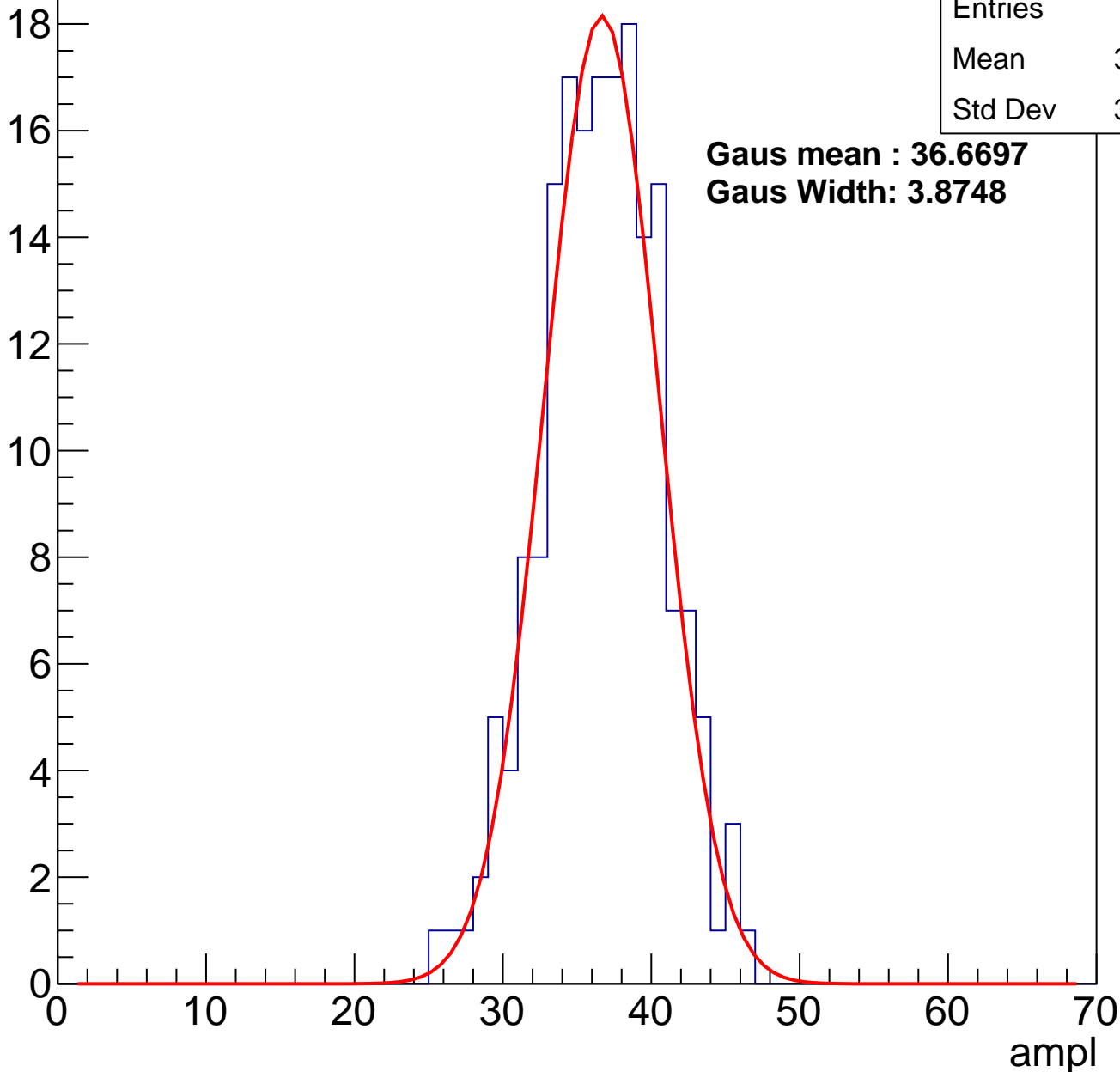
calib_packv5_042523_0143.root, FC#1, port C1

Entries	183
Mean	36.22
Std Dev	3.975

Gaus mean : 36.6697

Gaus Width: 3.8748

Entry



B0L101S, U19-ch99, adc2

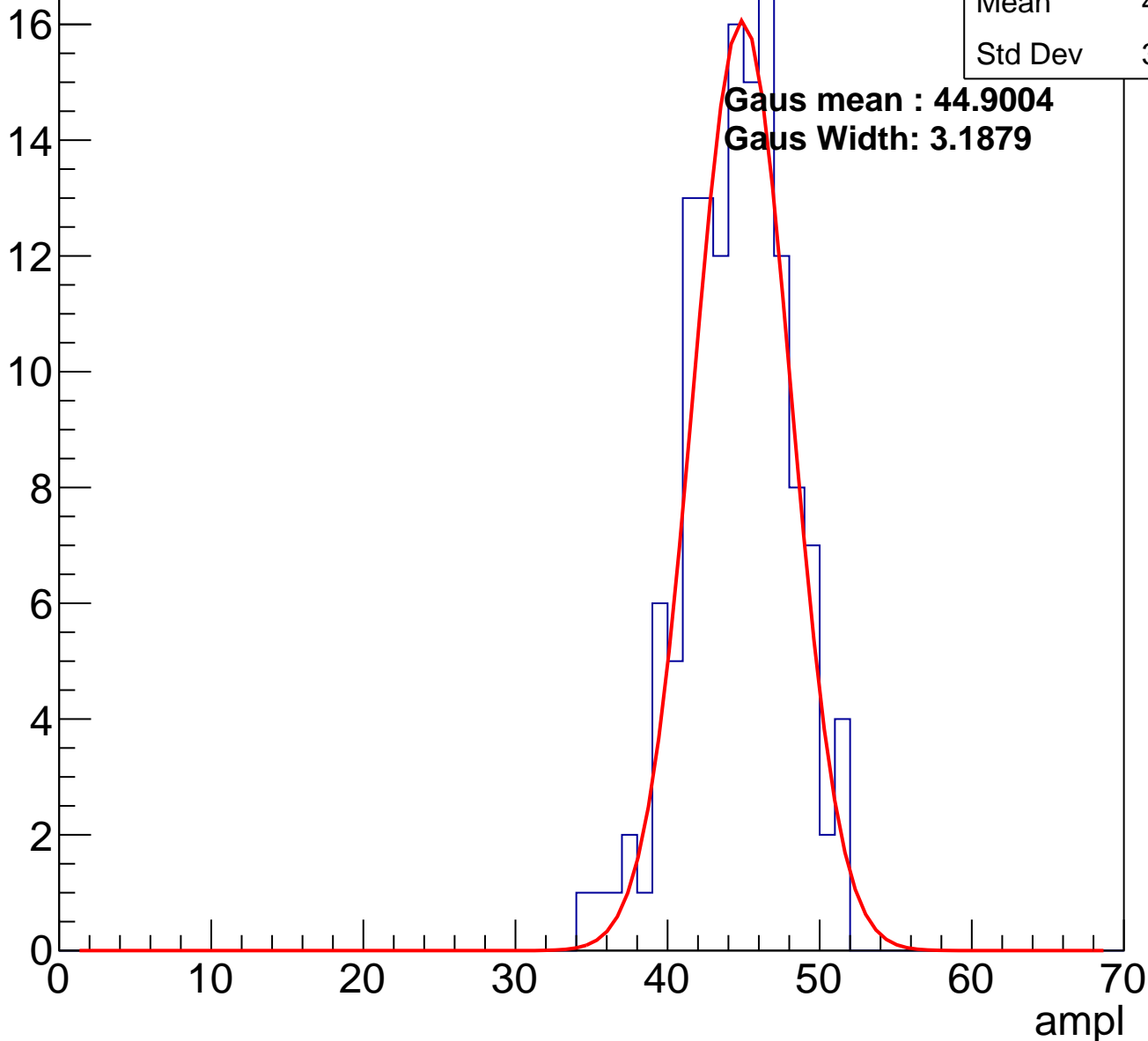
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	136
Mean	44.13
Std Dev	3.376

Gaus mean : 44.9004

Gaus Width: 3.1879



B0L101S, U19-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

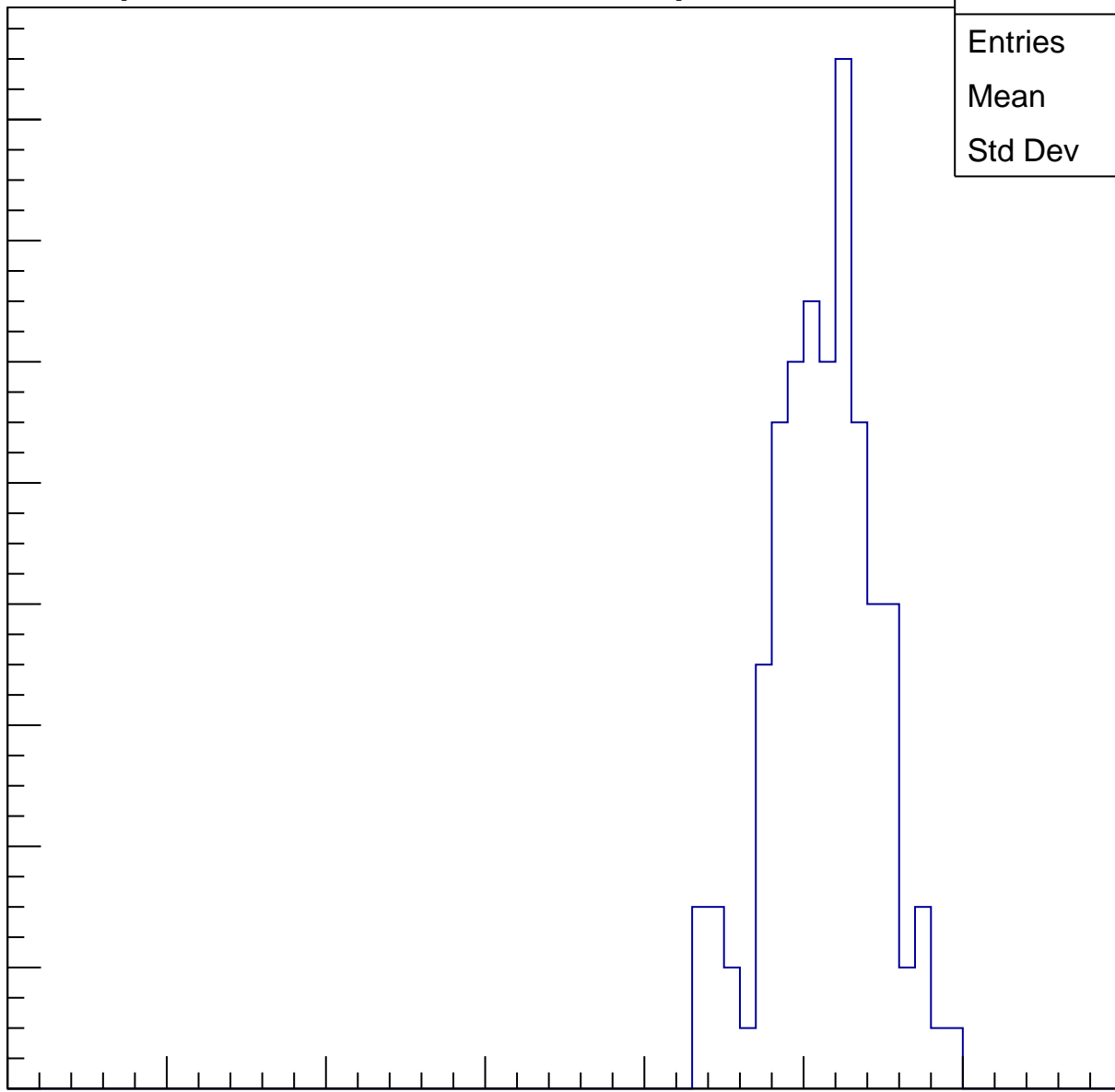
Entries	115
Mean	50.81
Std Dev	3.278

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

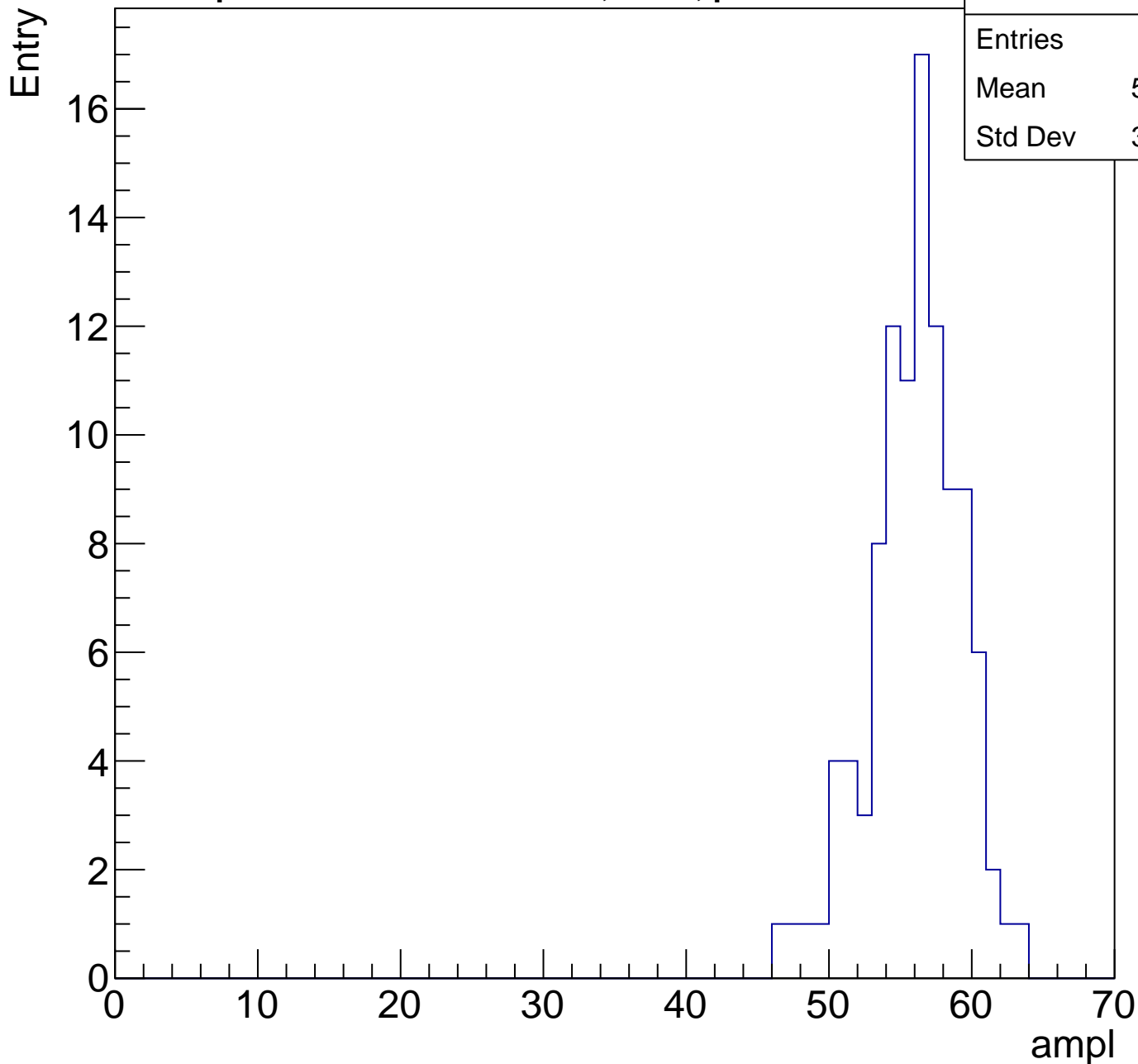
ampl



B0L101S, U19-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	55.56
Std Dev	3.204



B0L101S, U19-ch99, adc5

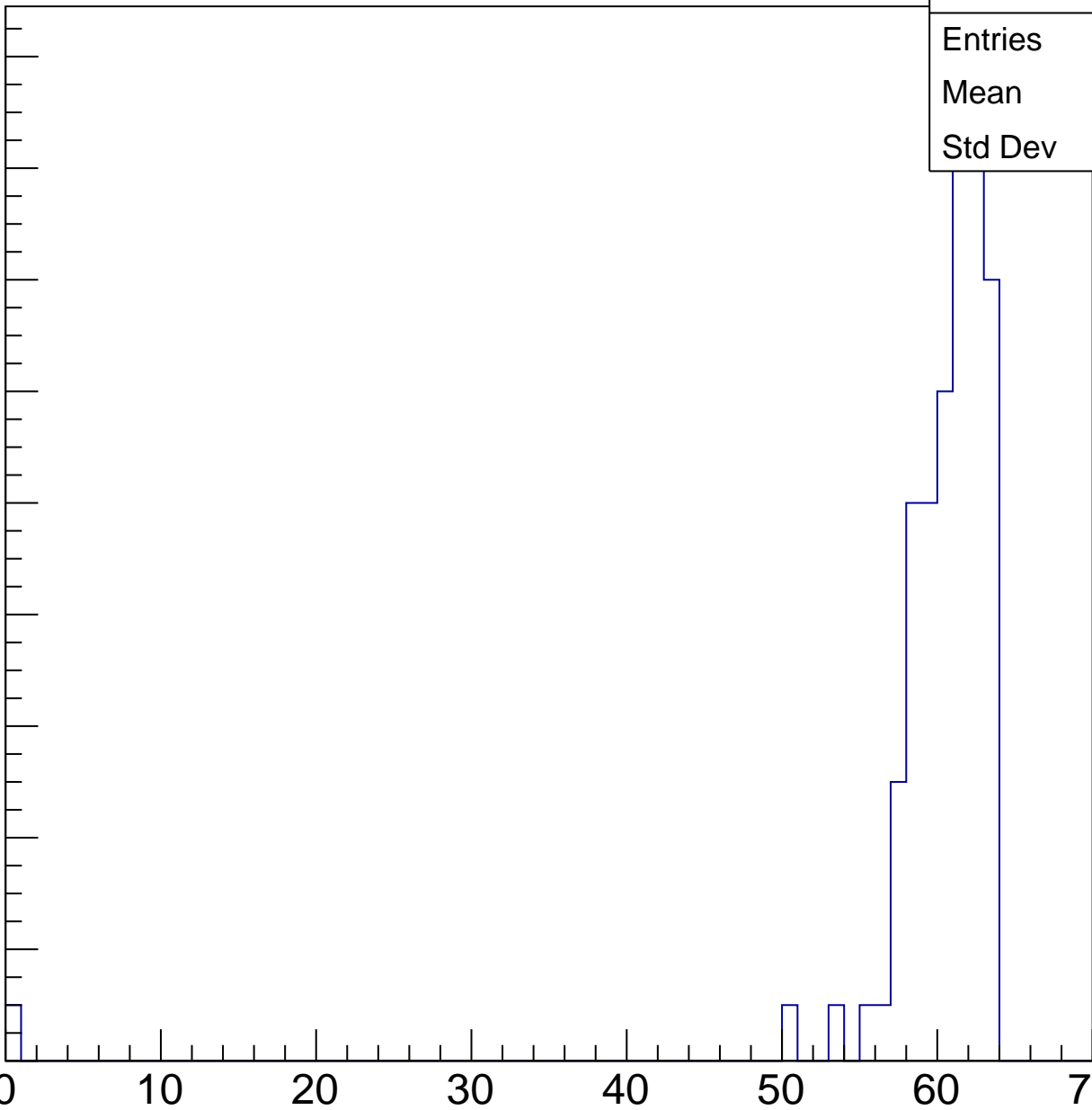
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.6
Std Dev	6.701

ampl

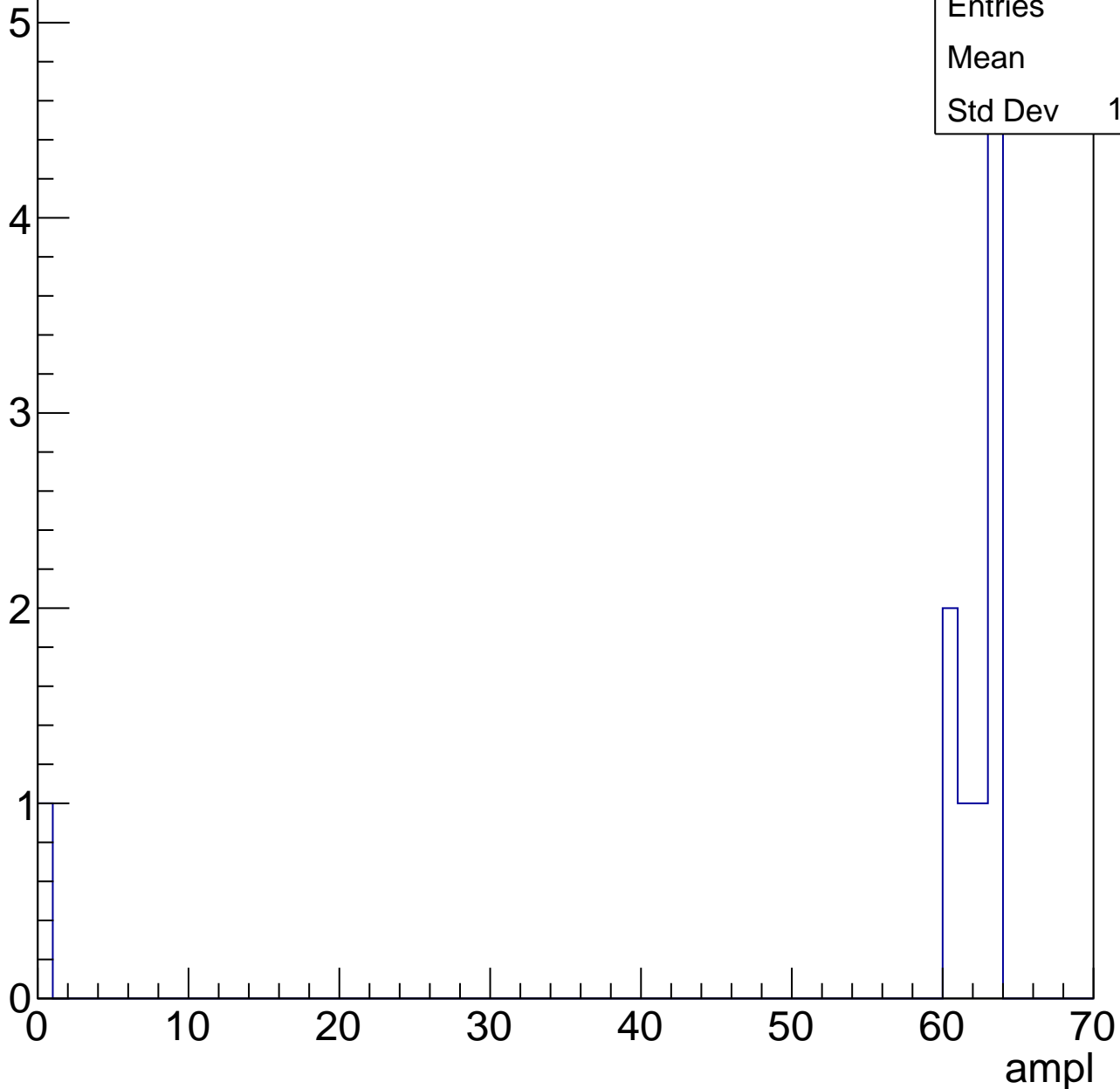


B0L101S, U19-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	55.8
Std Dev	18.64



B0L101S, U19-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U19-ch100, adc0

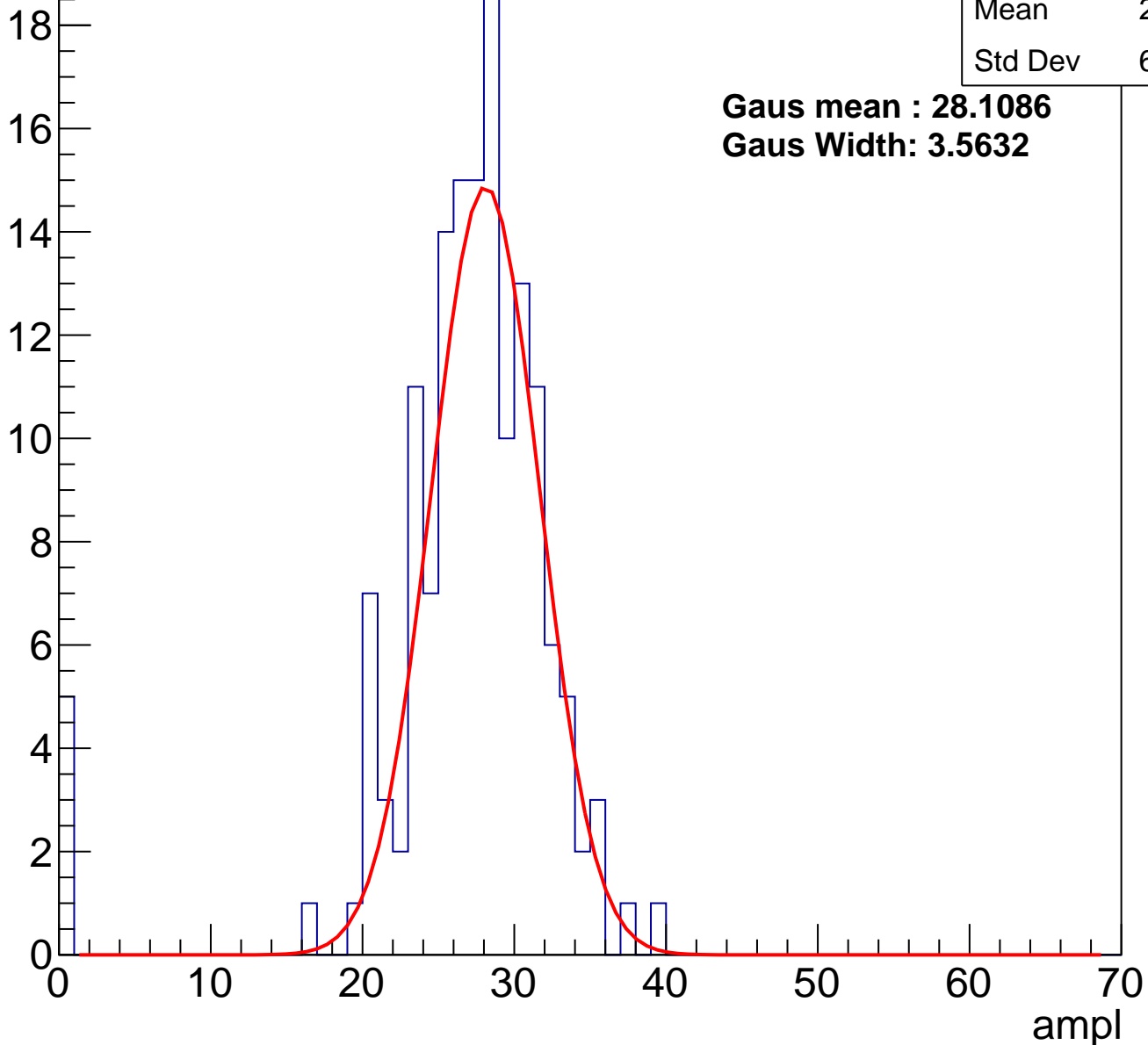
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	26.36
Std Dev	6.175

Gaus mean : 28.1086

Gaus Width: 3.5632

Entry



B0L101S, U19-ch100, adc1

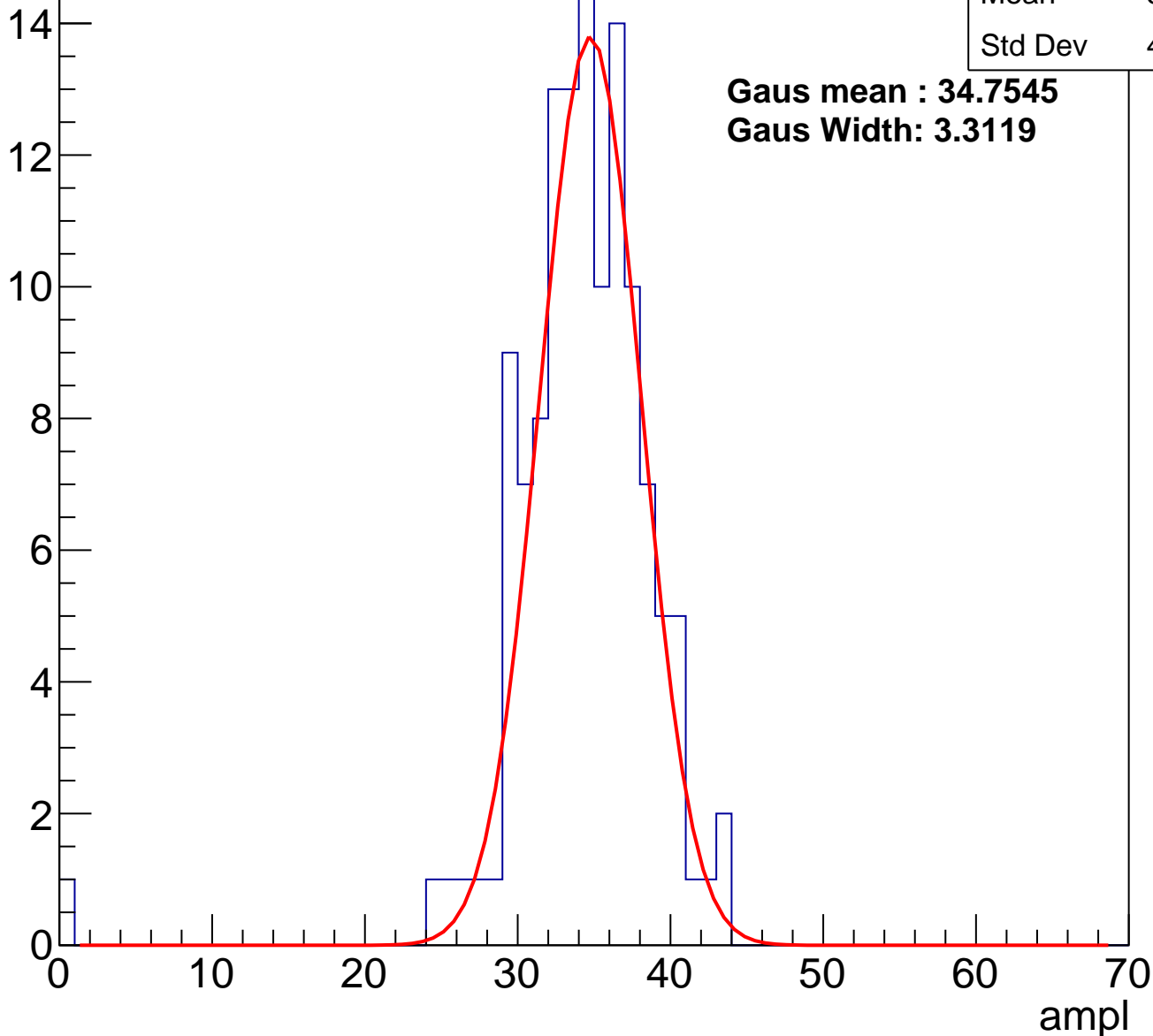
calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	33.79
Std Dev	4.716

Gaus mean : 34.7545

Gaus Width: 3.3119

Entry



B0L101S, U19-ch100, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

134

Mean

40.31

Std Dev

3.652

Gaus mean : 40.7590

Gaus Width: 3.6588

0

10

20

30

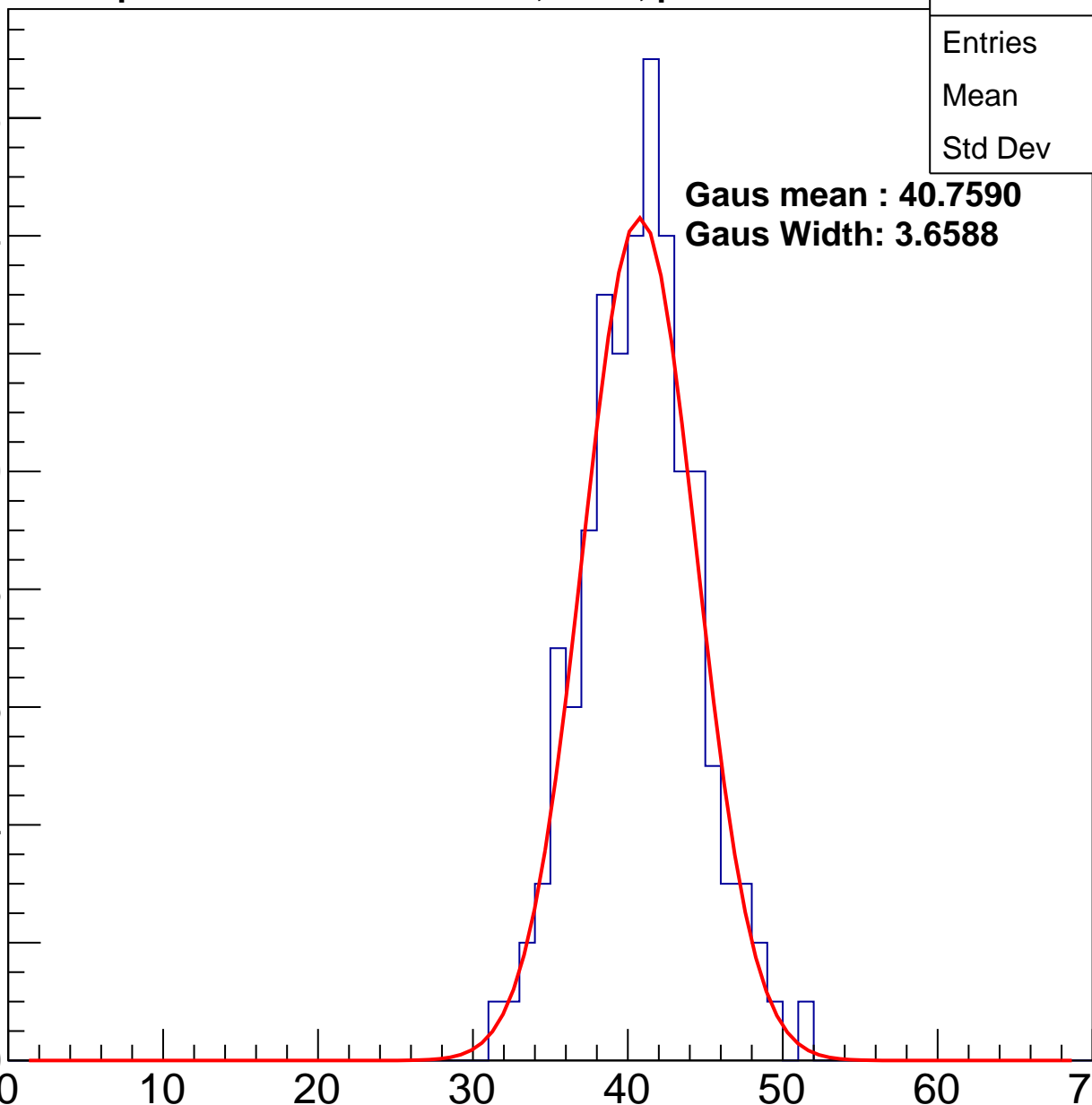
40

50

60

70

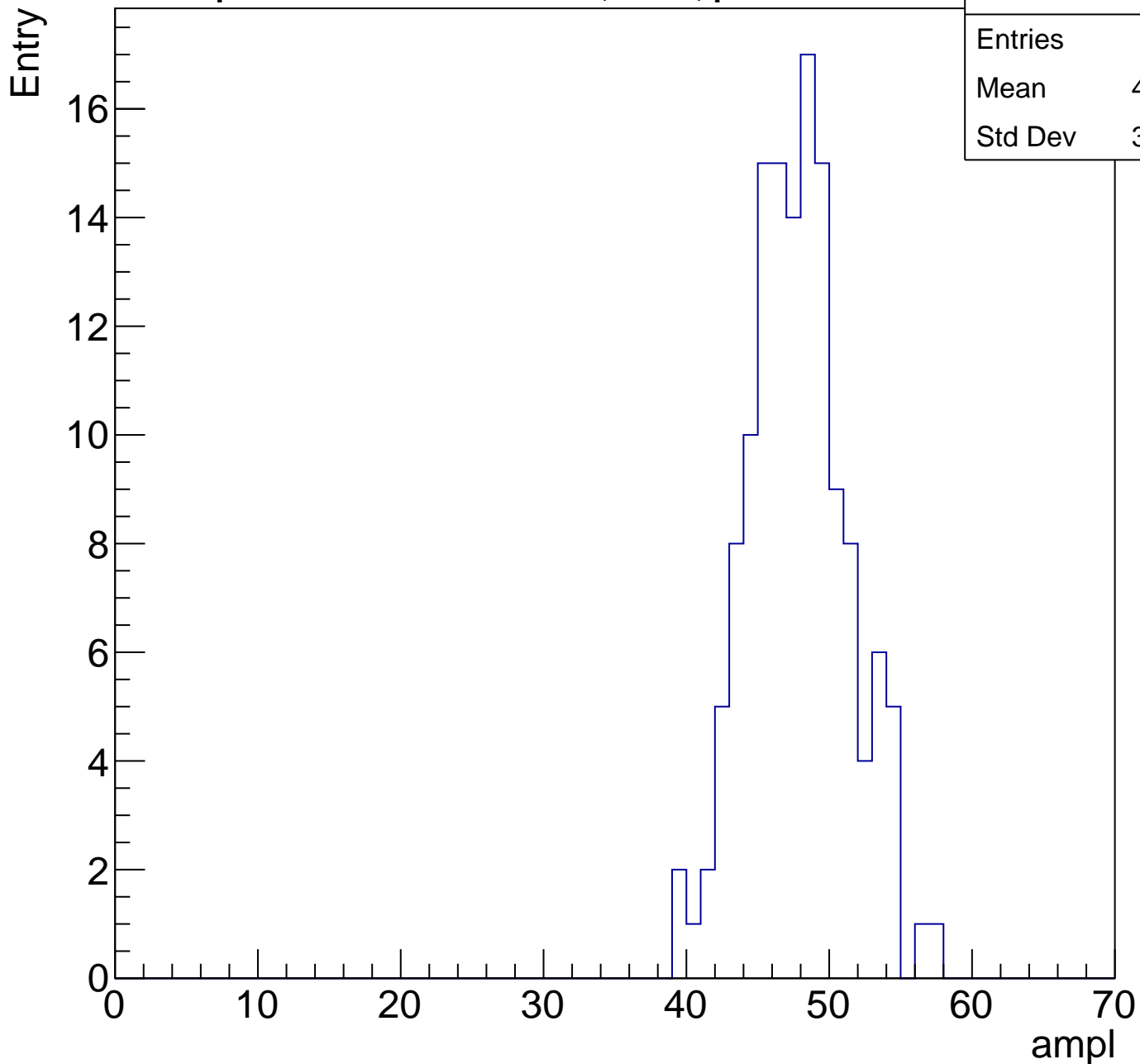
ampl



B0L101S, U19-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

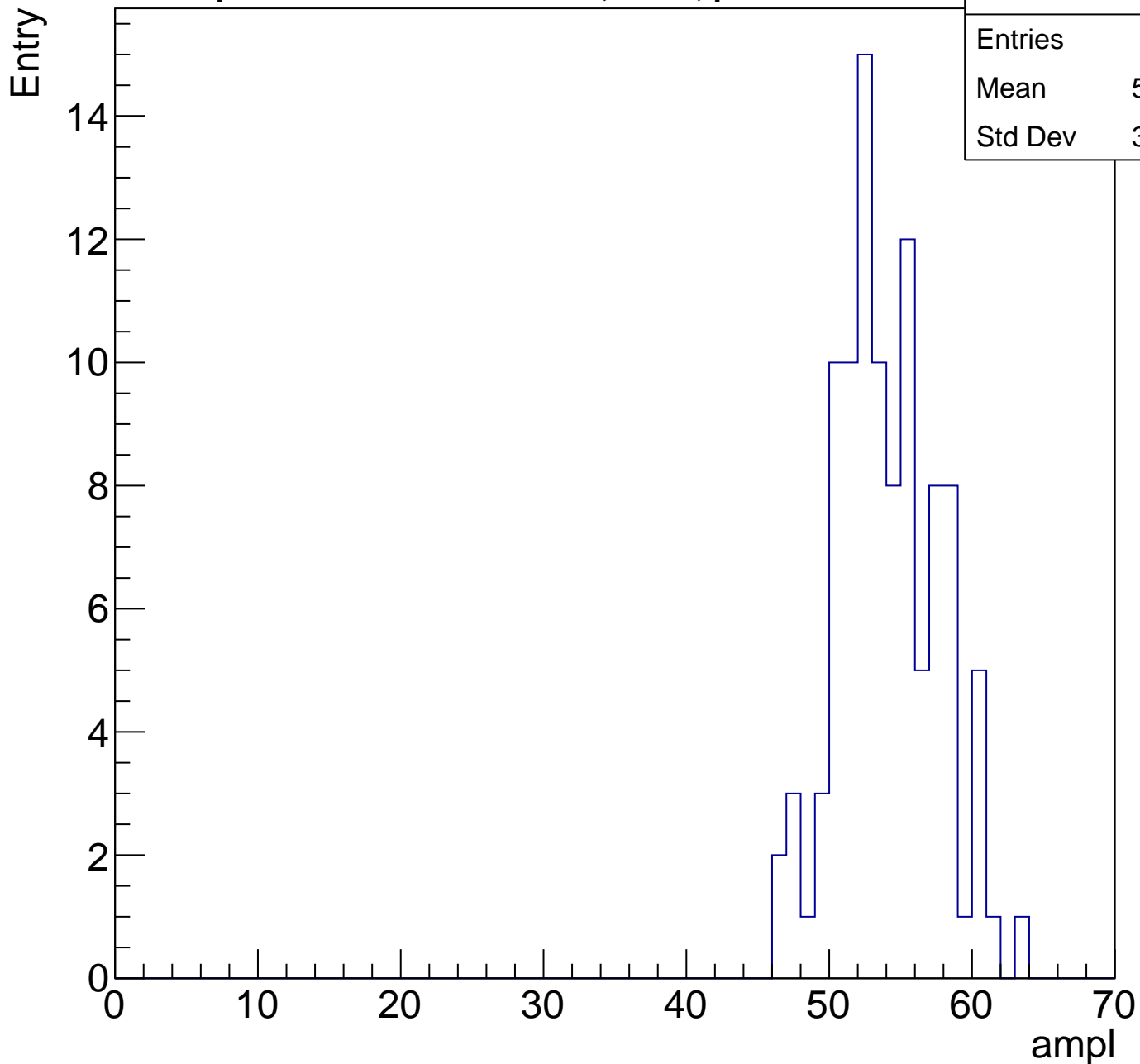
Entries	138
Mean	47.36
Std Dev	3.487



B0L101S, U19-ch100, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	53.62
Std Dev	3.495



B0L101S, U19-ch100, adc5

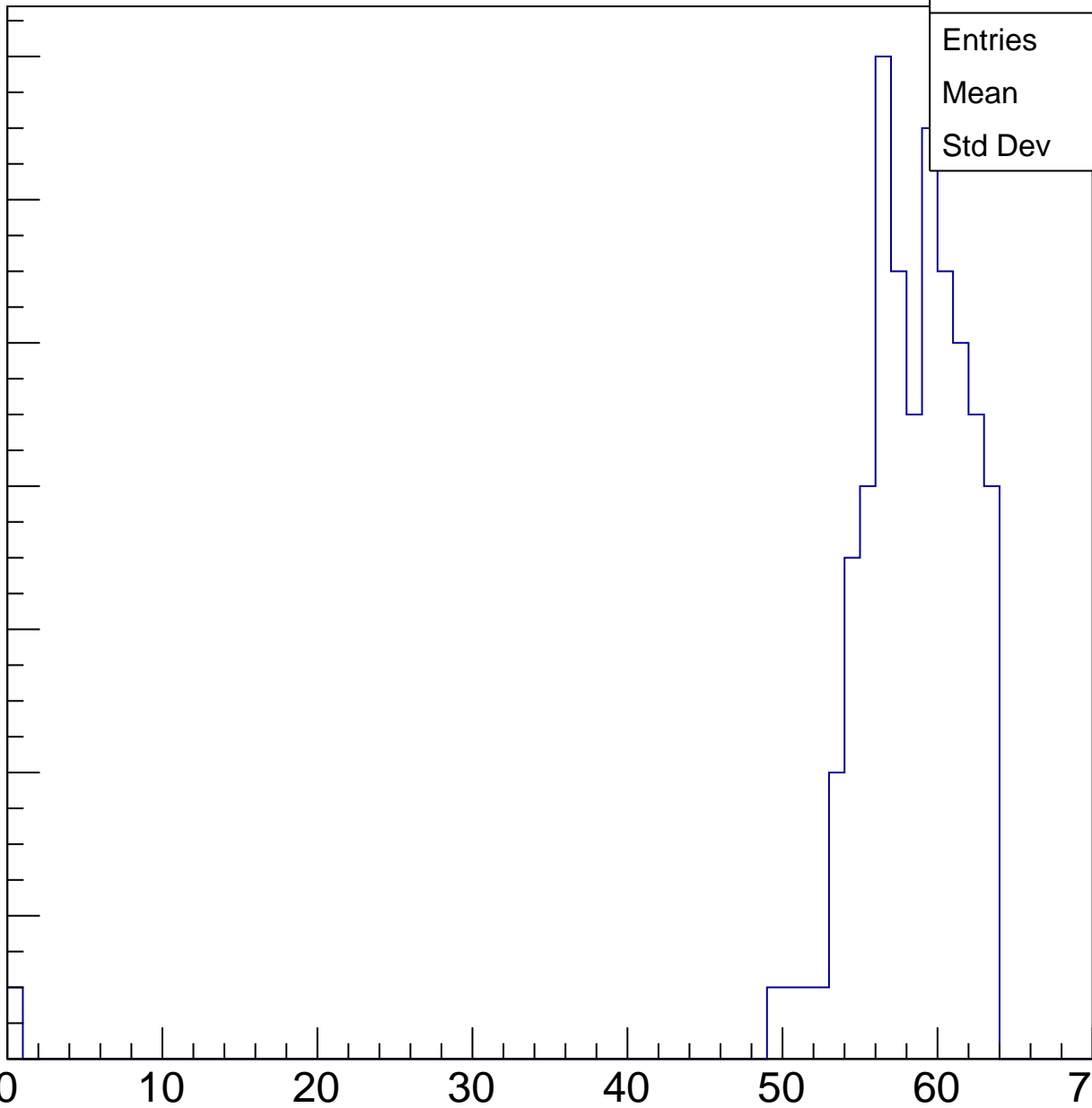
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	57.47
Std Dev	6.357

ampl



B0L101S, U19-ch100, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	40
Mean	59.58
Std Dev	9.672

Entry

10

8

6

4

2

0

0

10

20

30

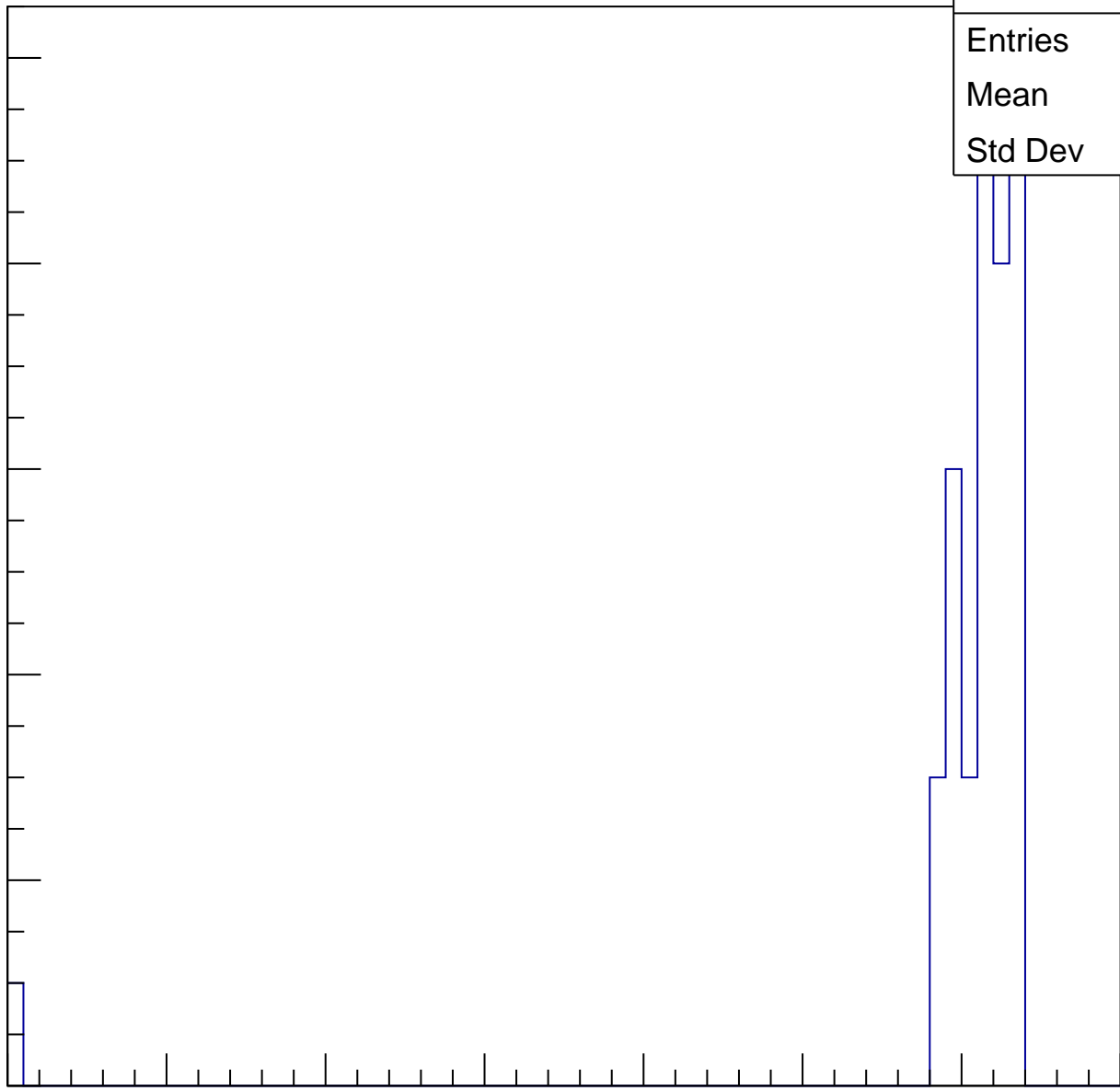
40

50

60

70

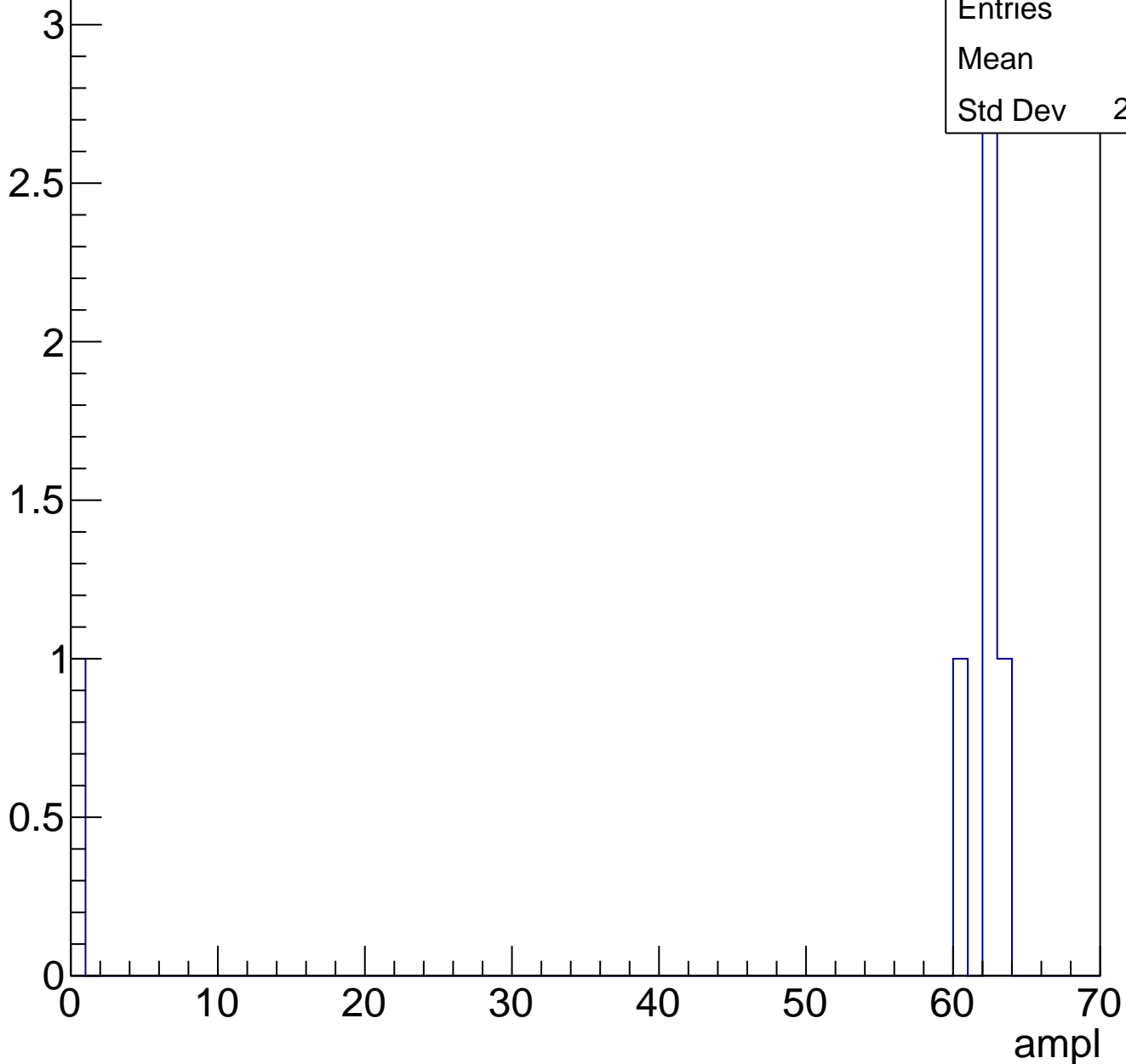
ampl



B0L101S, U19-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



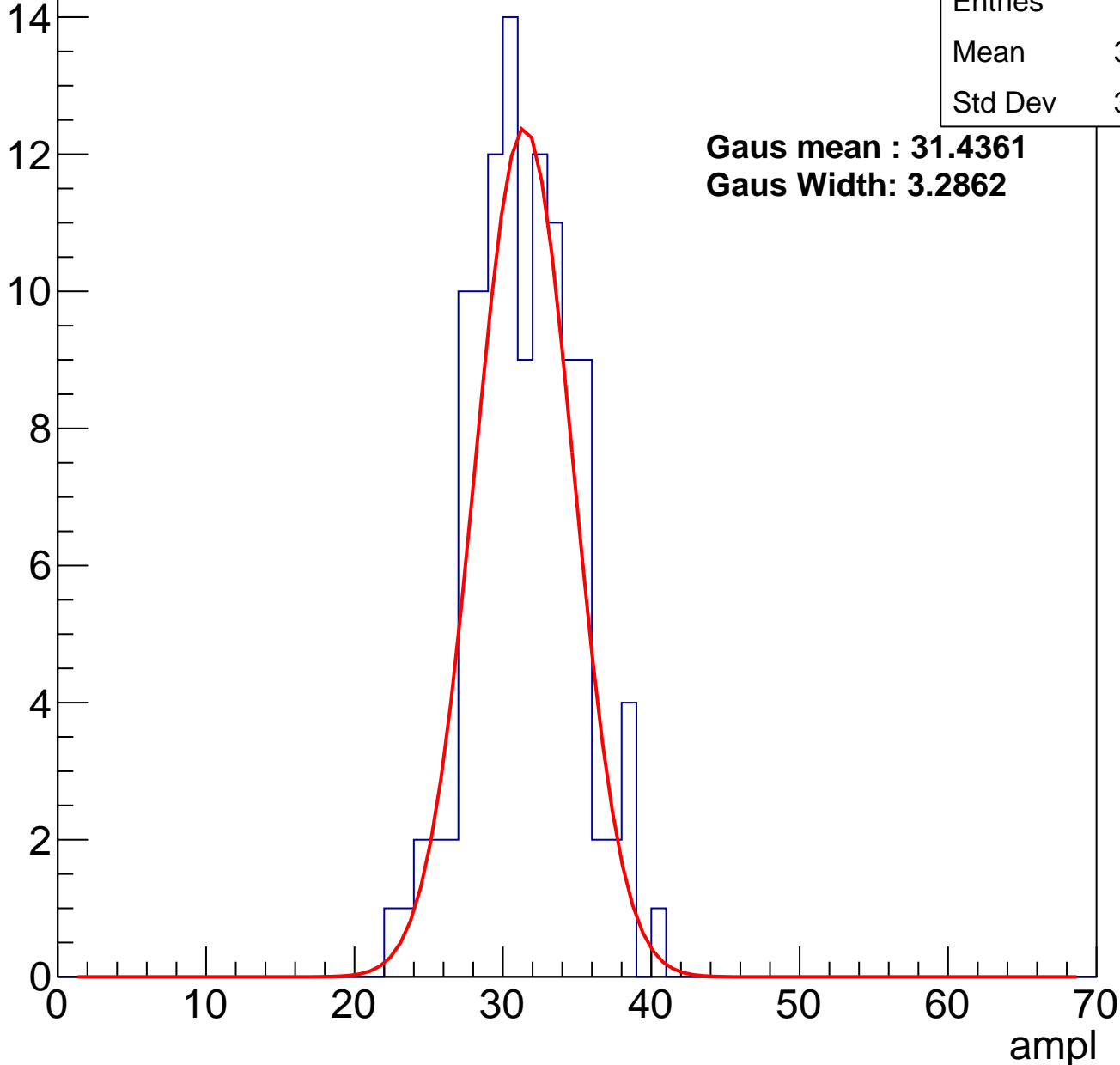
B0L101S, U19-ch101, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	30.96
Std Dev	3.465

Gaus mean : 31.4361
Gaus Width: 3.2862

Entry



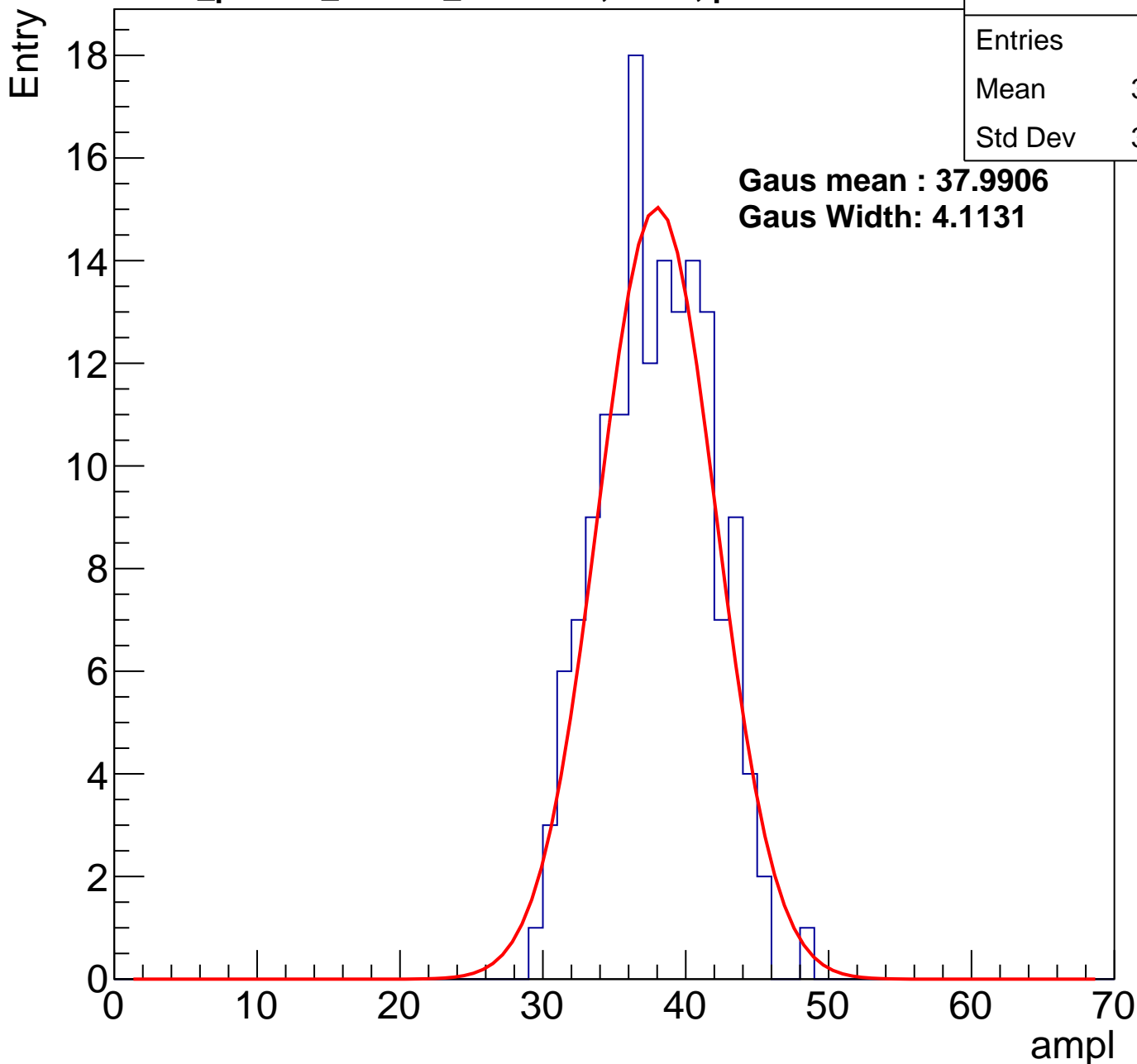
B0L101S, U19-ch101, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	37.45
Std Dev	3.785

Gaus mean : 37.9906

Gaus Width: 4.1131



B0L101S, U19-ch101, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

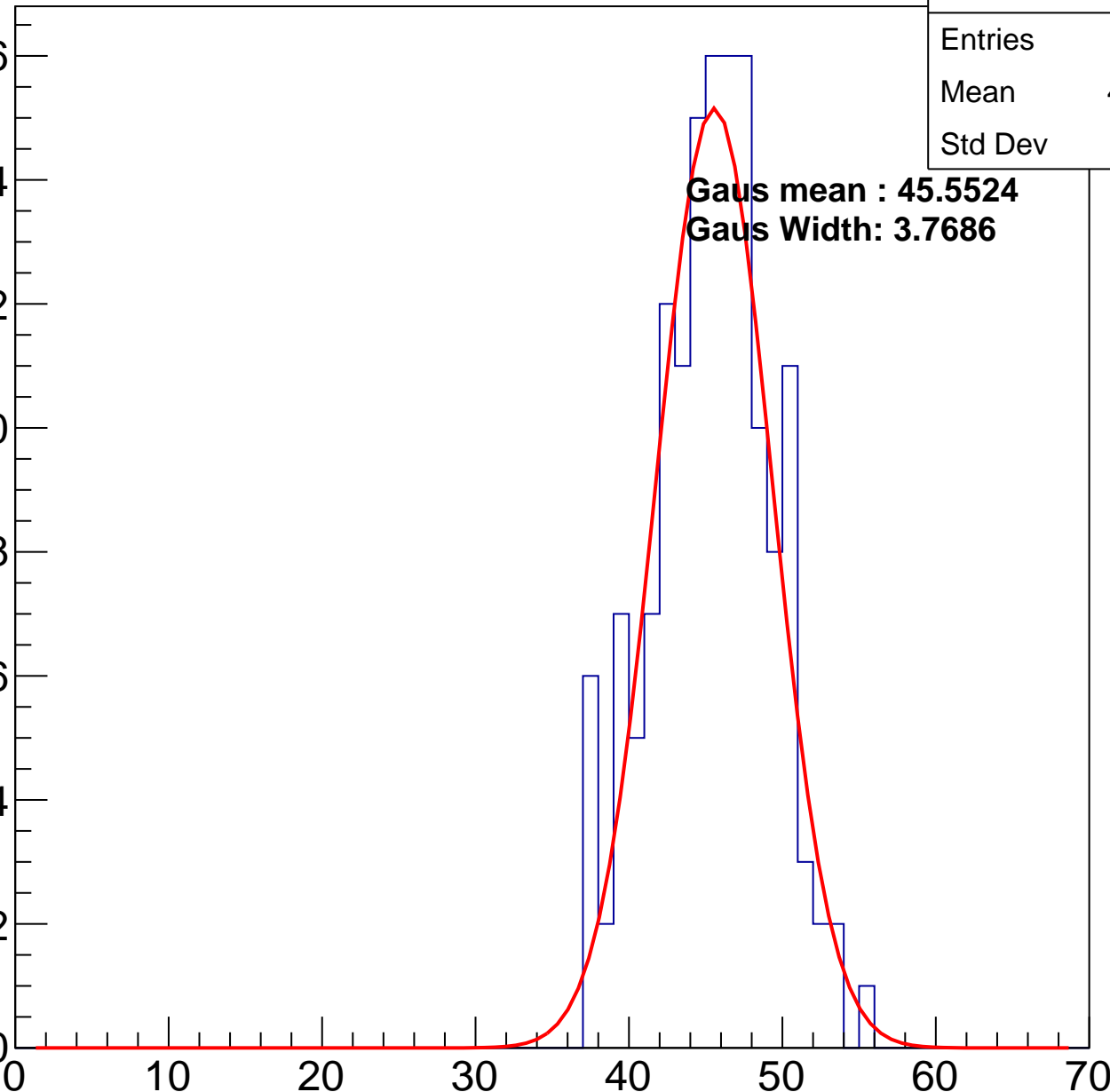
16
14
12
10
8
6
4
2
0

Entries	150
Mean	44.95
Std Dev	3.781

Gaus mean : 45.5524

Gaus Width: 3.7686

ampl

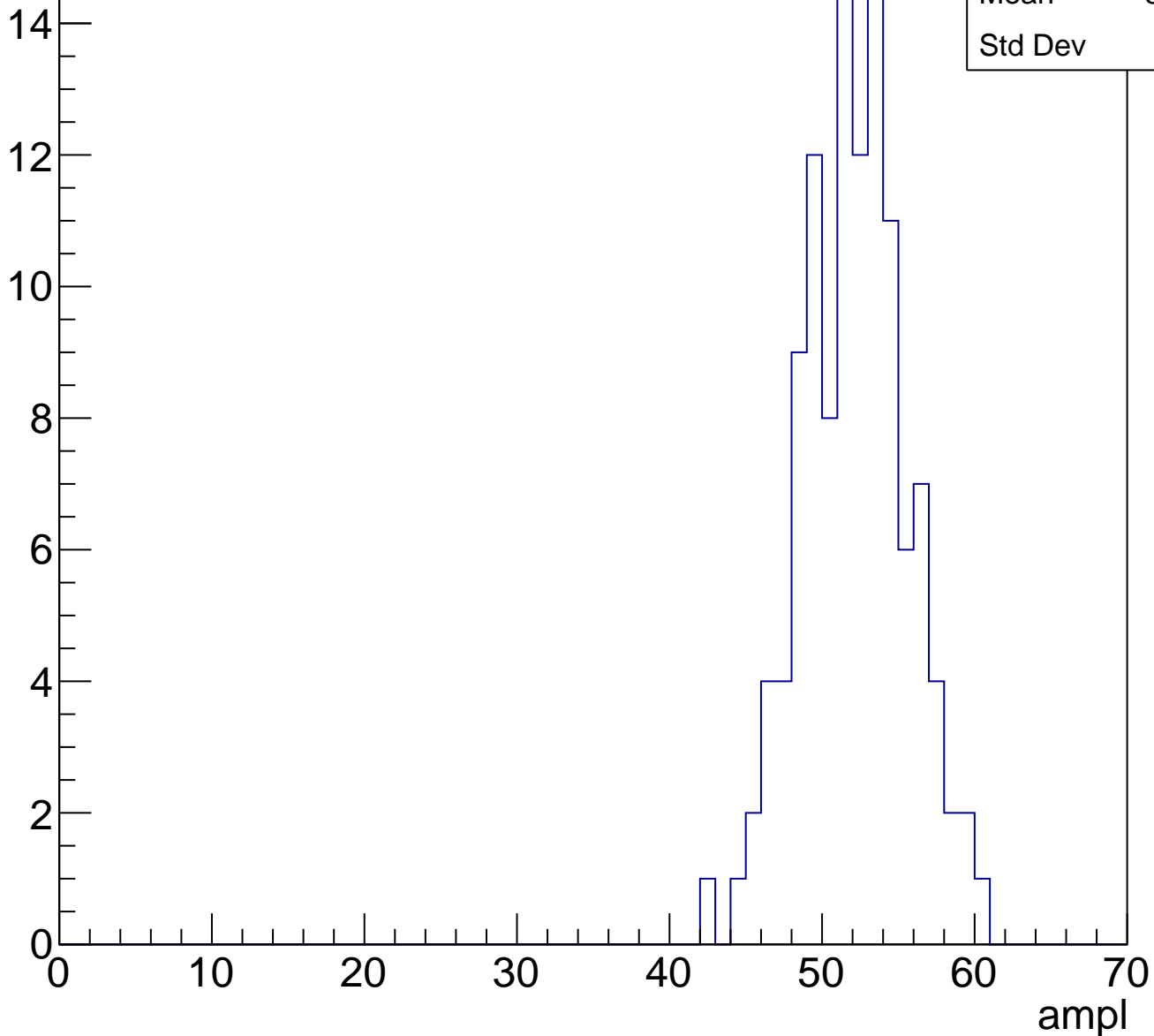


B0L101S, U19-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	51.64
Std Dev	3.41

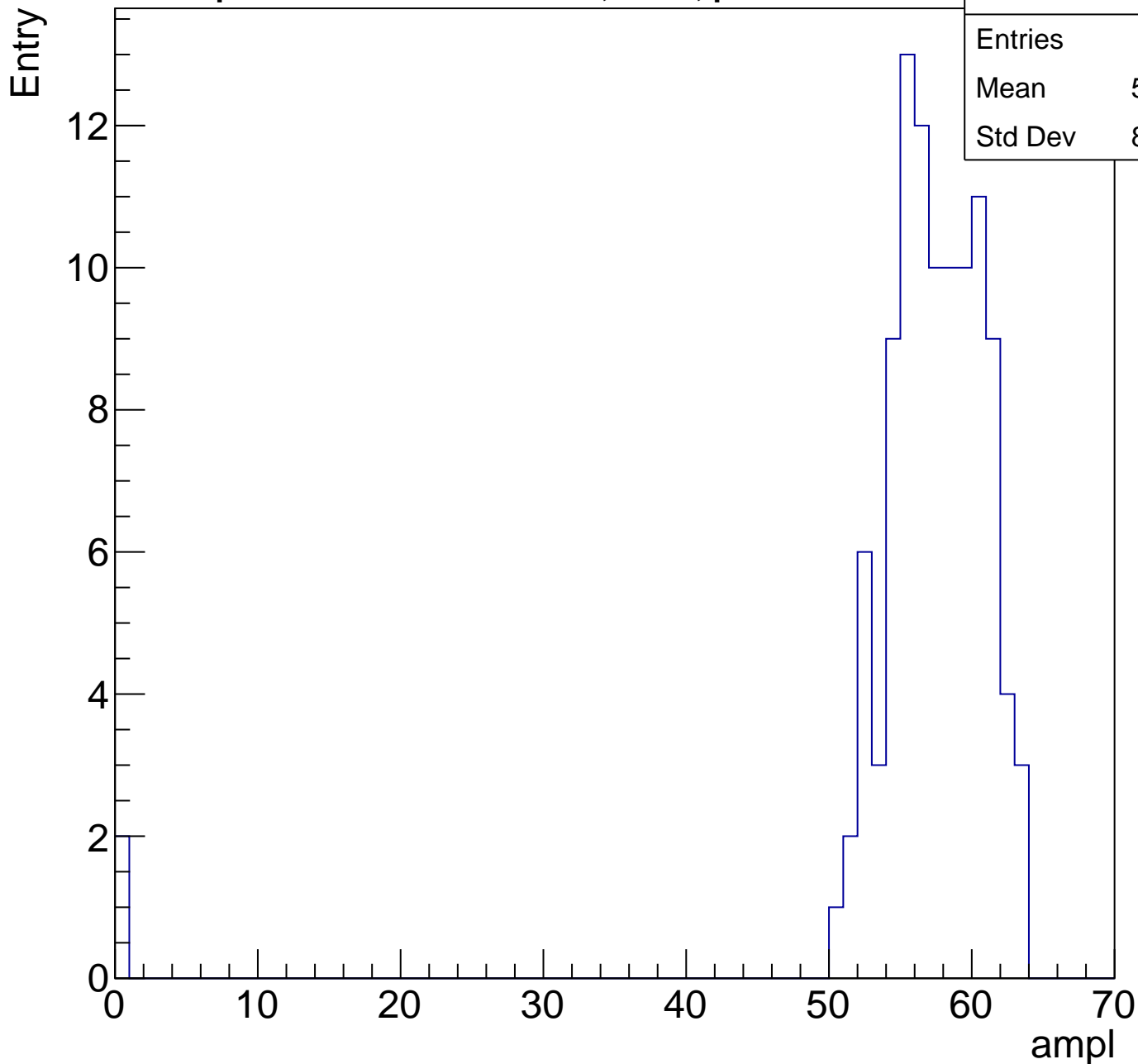
Entry



B0L101S, U19-ch101, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	56.02
Std Dev	8.373



B0L101S, U19-ch101, adc5

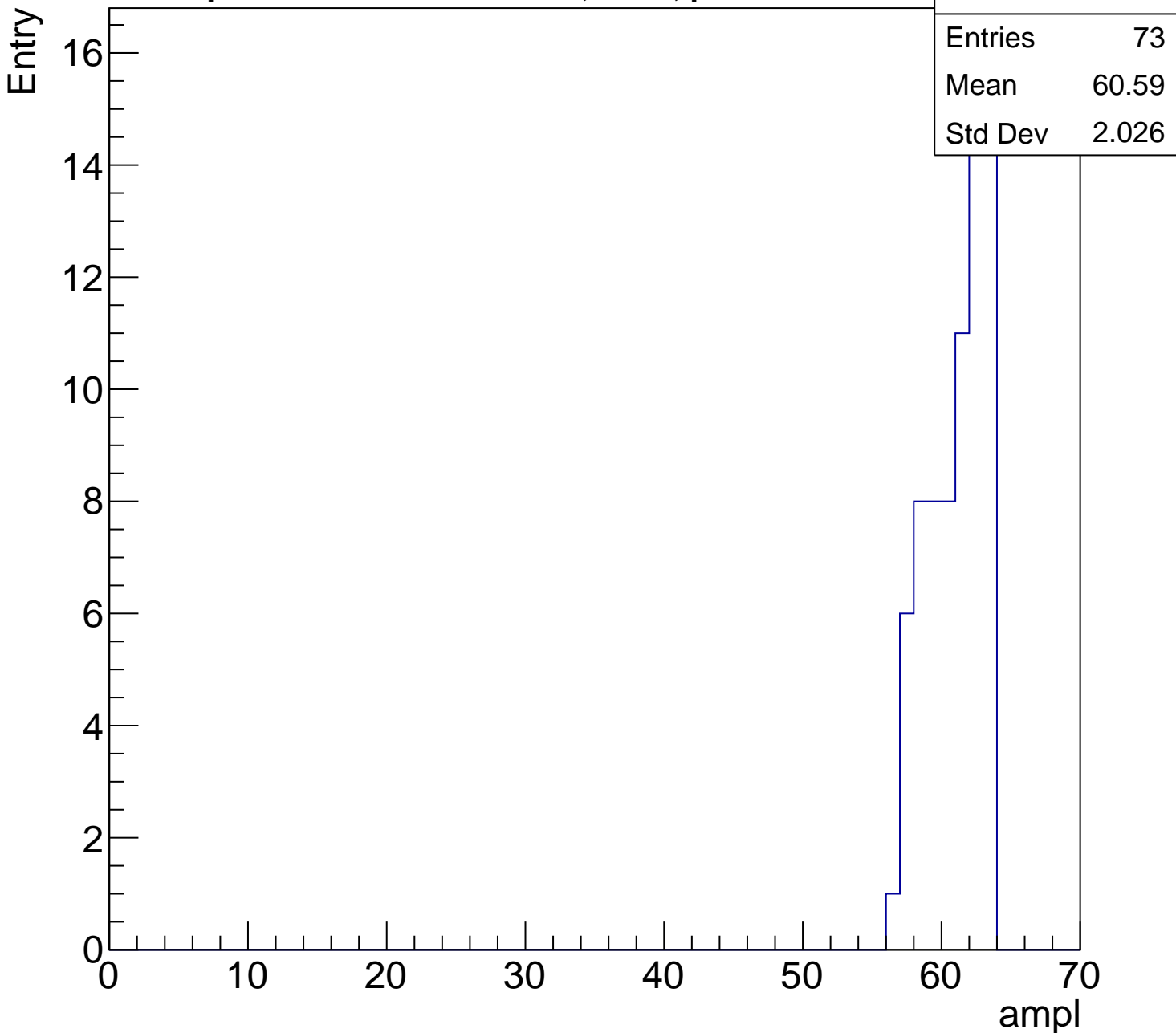
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	60.59
Std Dev	2.026

ampl



B0L101S, U19-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	22
Std Dev	1

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch102, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	28.14
Std Dev	4.303

Gaus mean : 28.6867

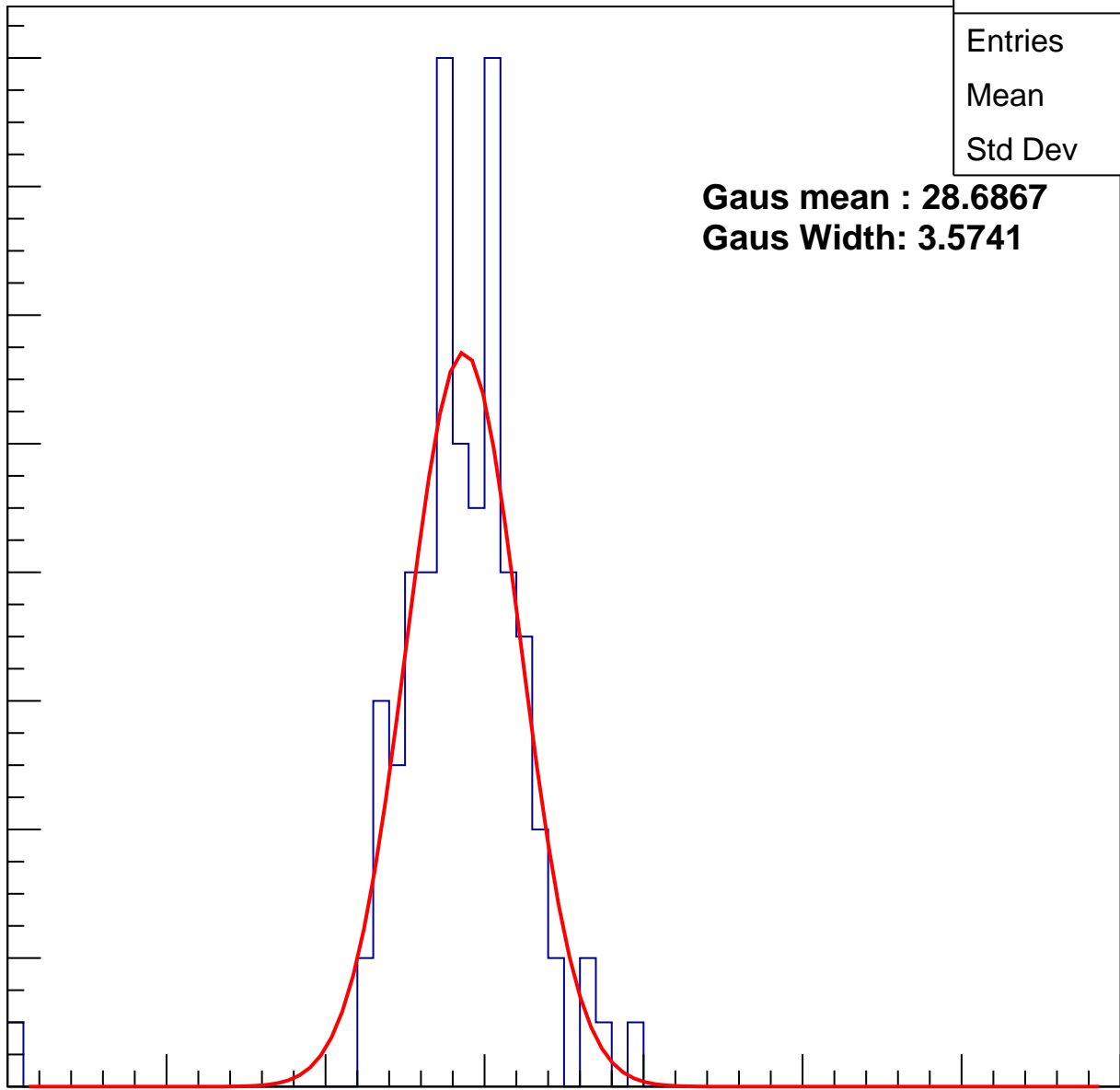
Gaus Width: 3.5741

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



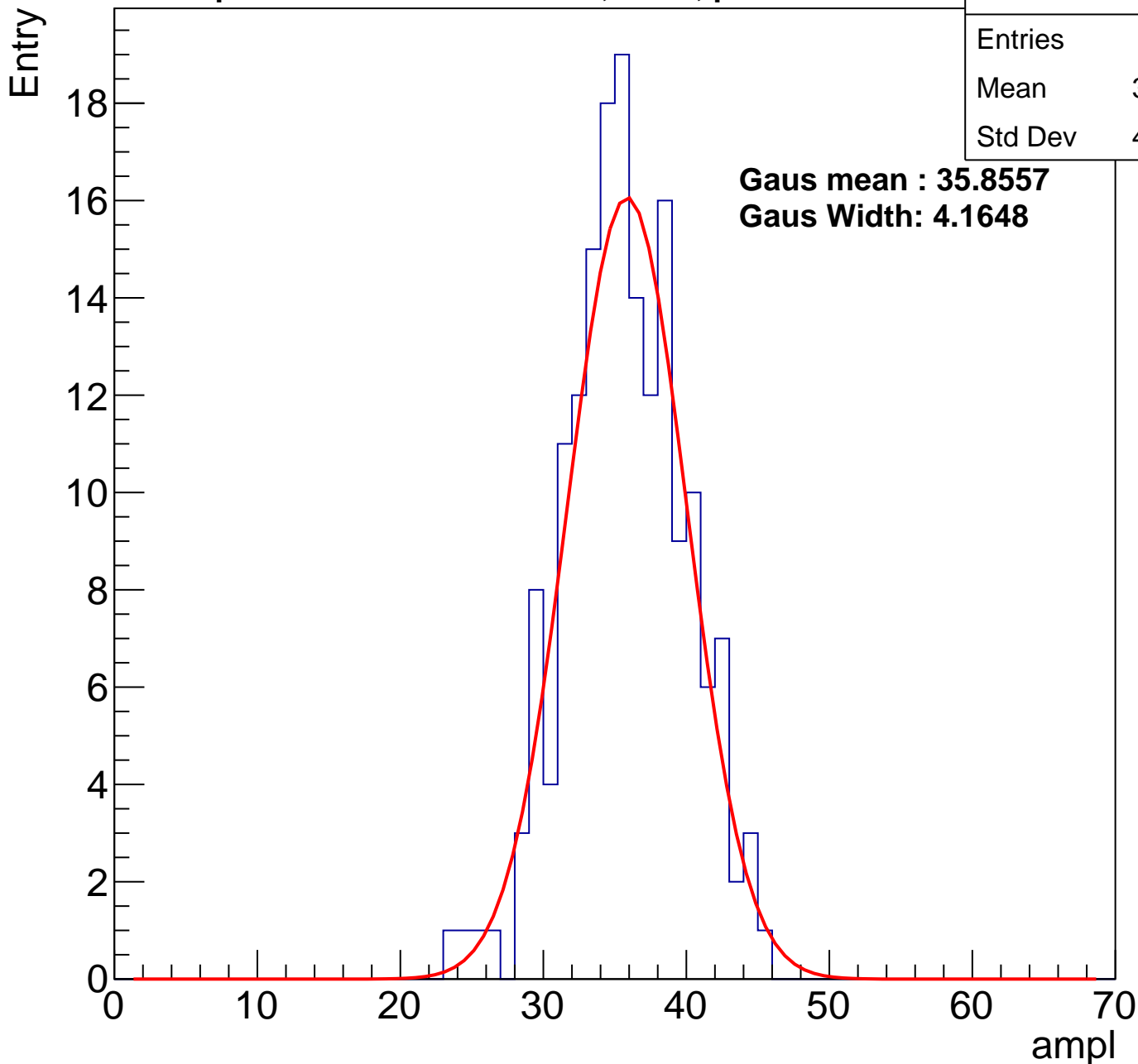
B0L101S, U19-ch102, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	35.29
Std Dev	4.123

Gaus mean : 35.8557

Gaus Width: 4.1648



B0L101S, U19-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

116

Mean

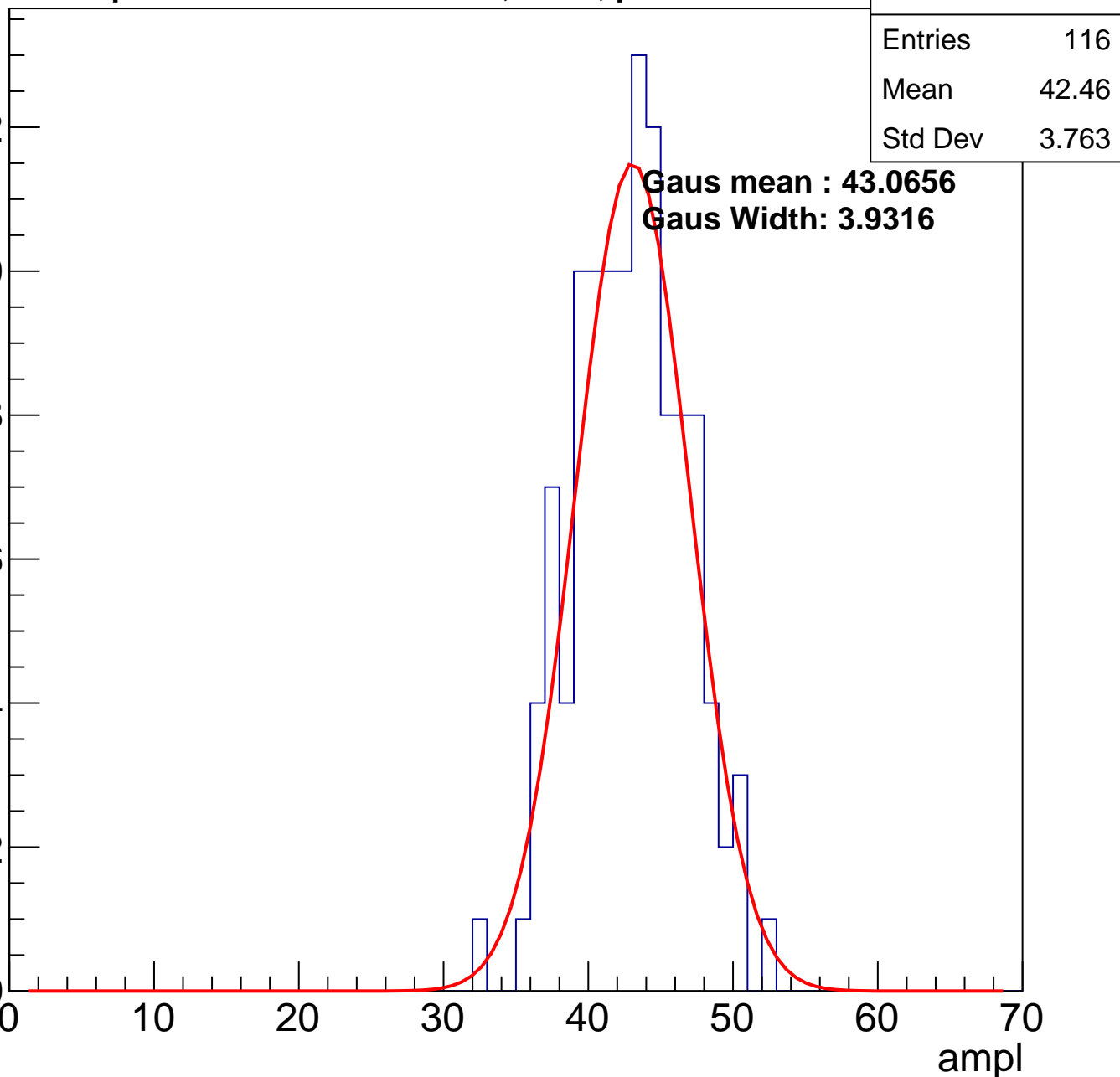
42.46

Std Dev

3.763

Gaus mean : 43.0656

Gaus Width: 3.9316



B0L101S, U19-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

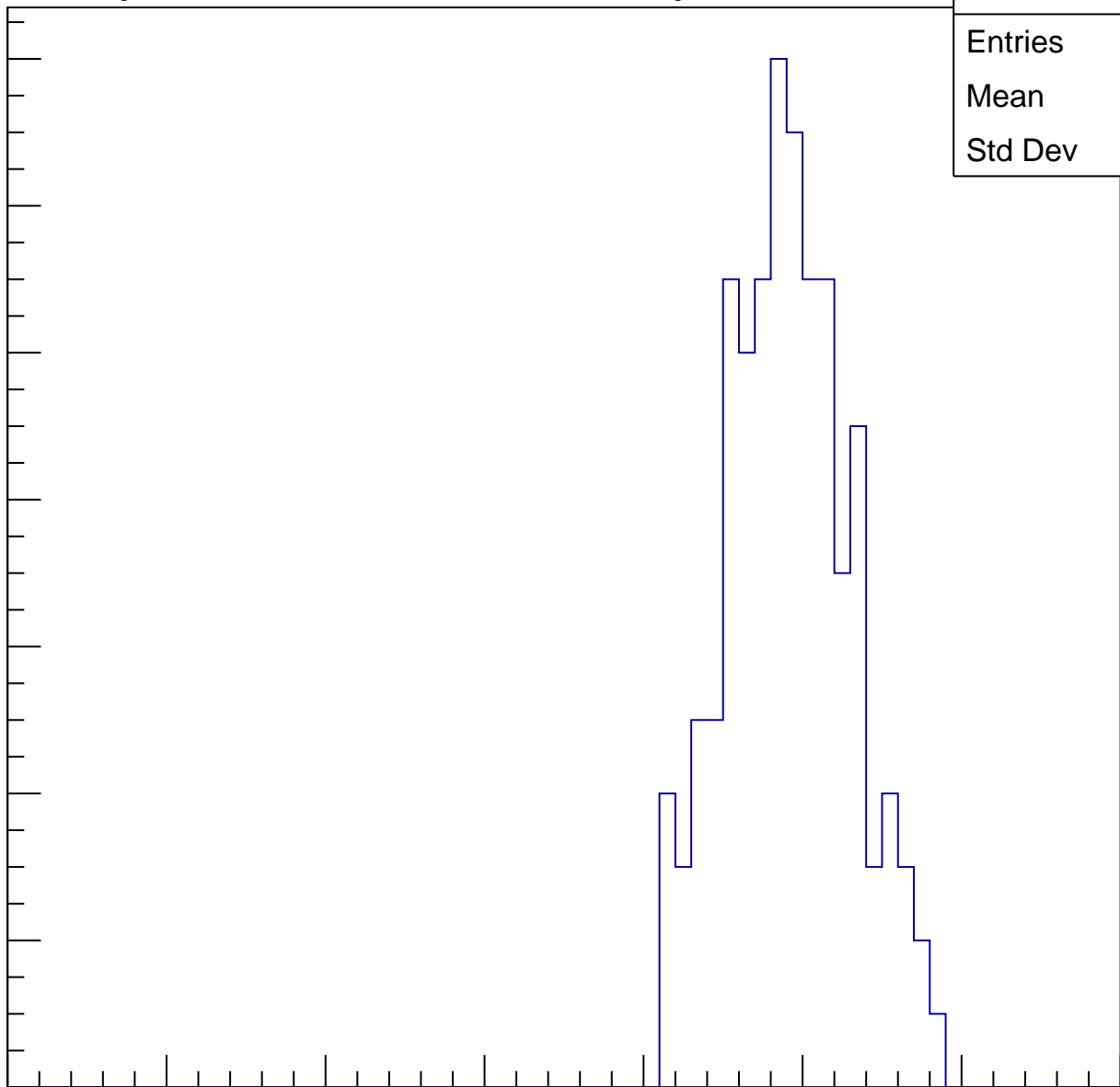
Entries	127
Mean	48.66
Std Dev	3.821

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

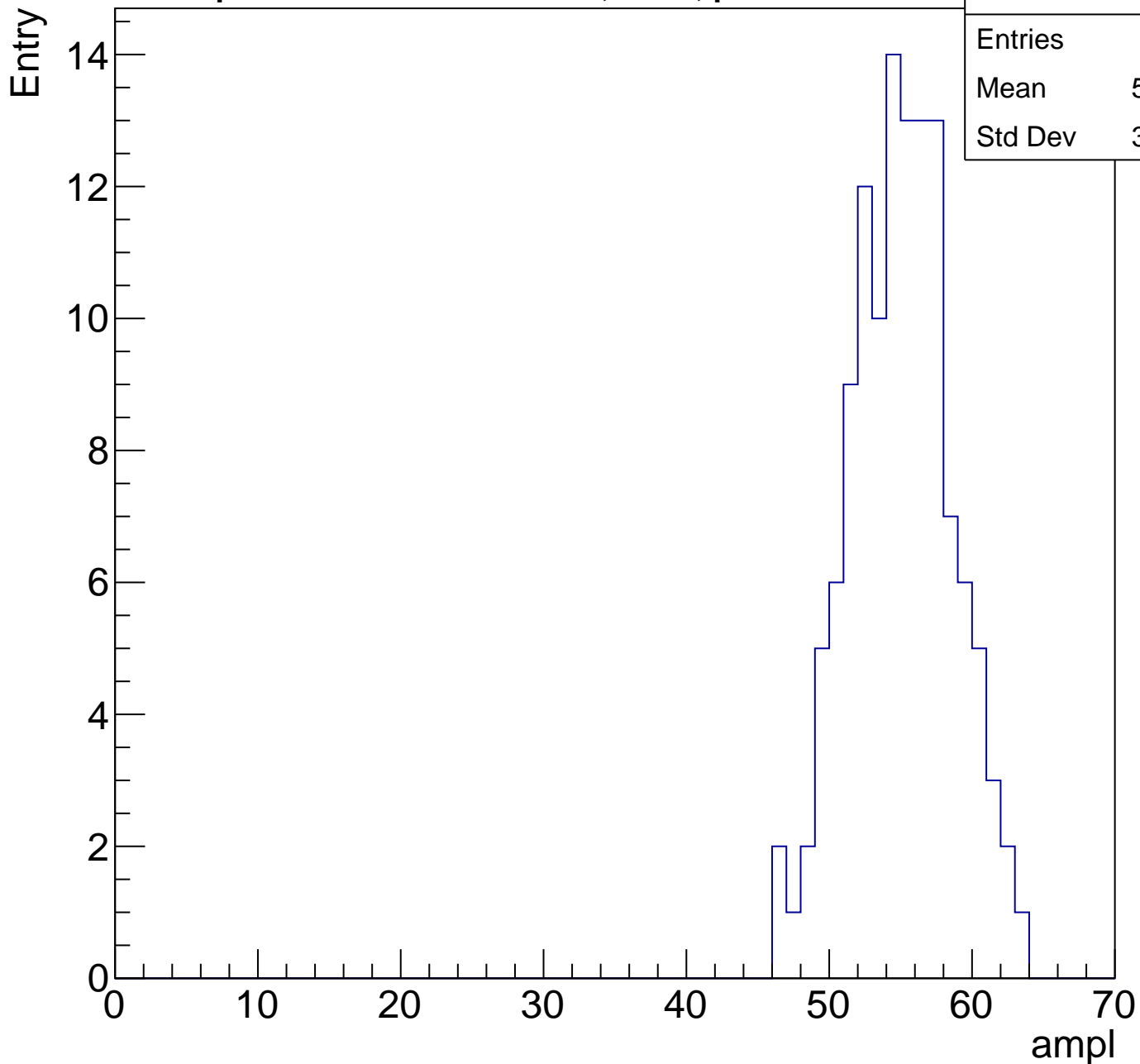
ampl



B0L101S, U19-ch102, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	54.54
Std Dev	3.539



B0L101S, U19-ch102, adc5

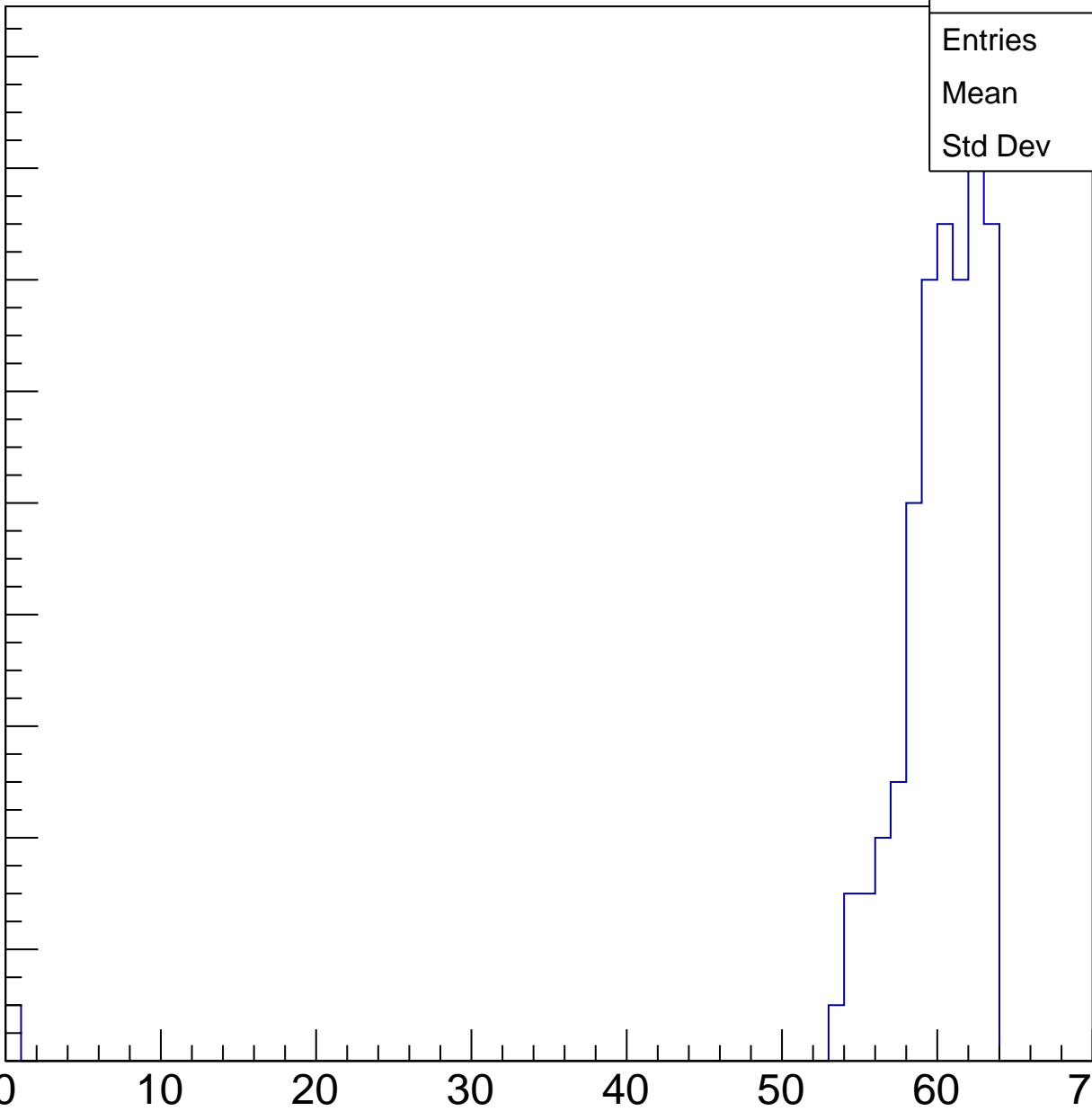
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	103
Mean	59.32
Std Dev	6.362

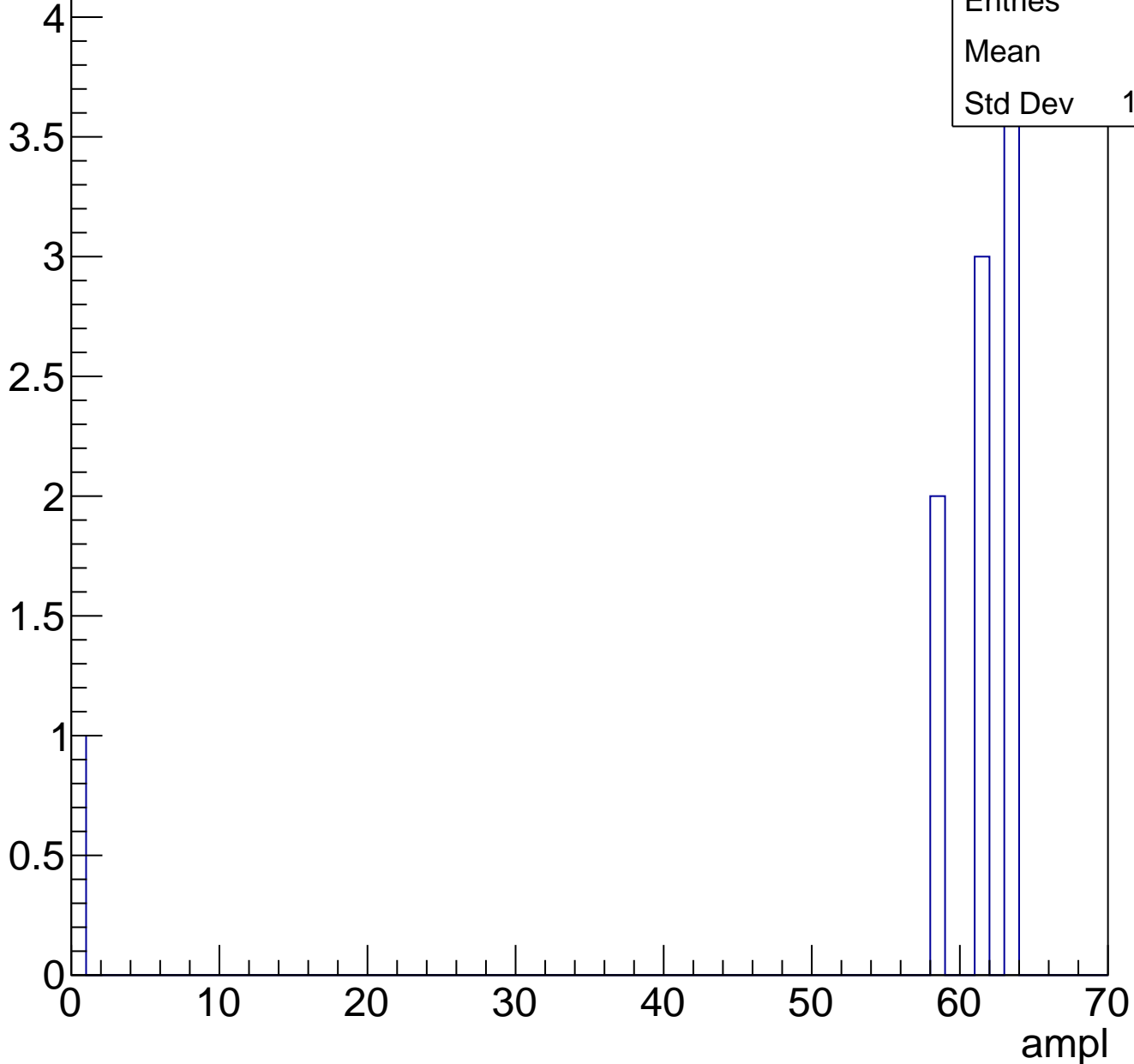
ampl



B0L101S, U19-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

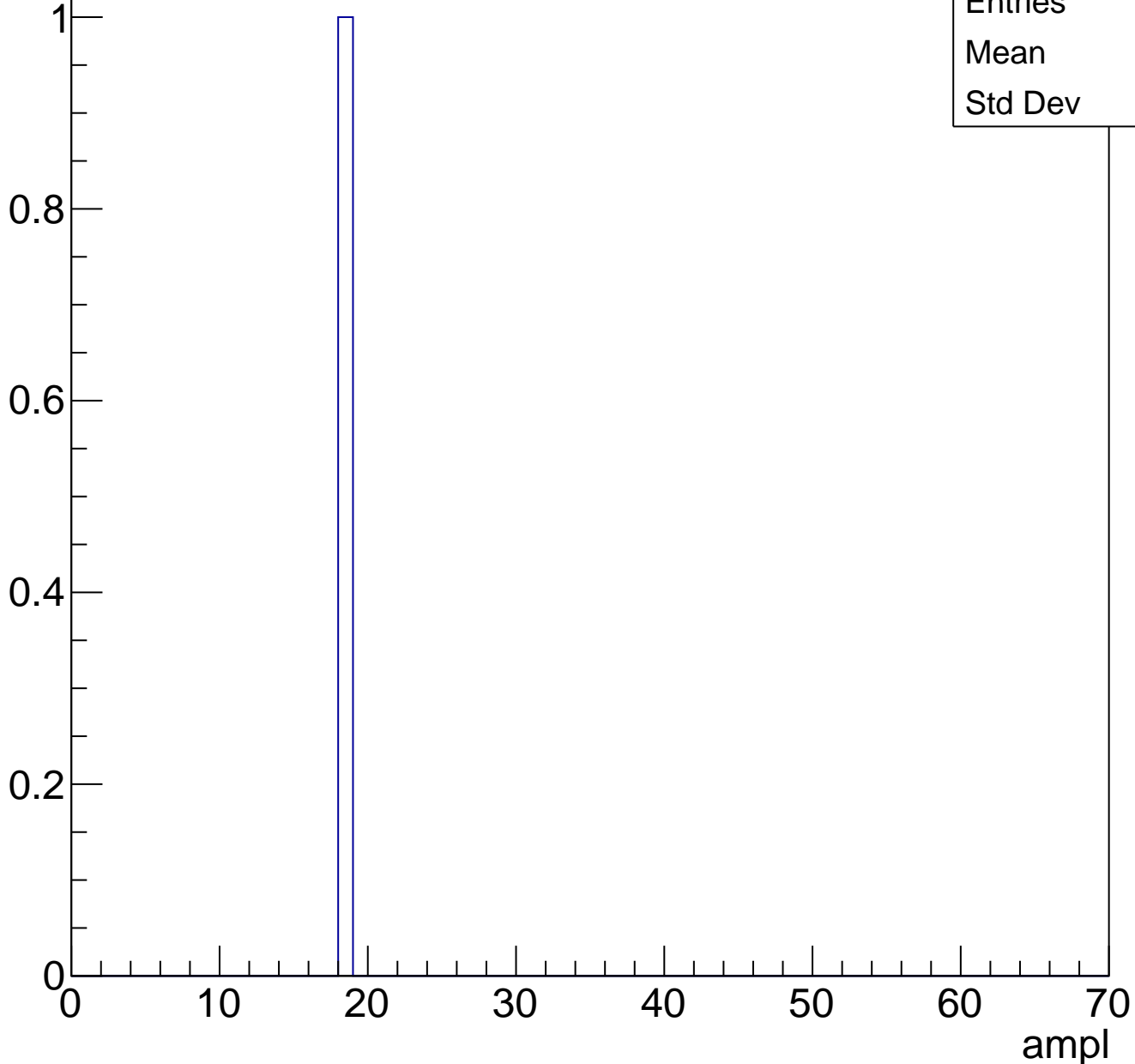
Entry



B0L101S, U19-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	18
Std Dev	0

B0L101S, U19-ch103, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	29.07
Std Dev	3.552

Gaus mean : 29.7875

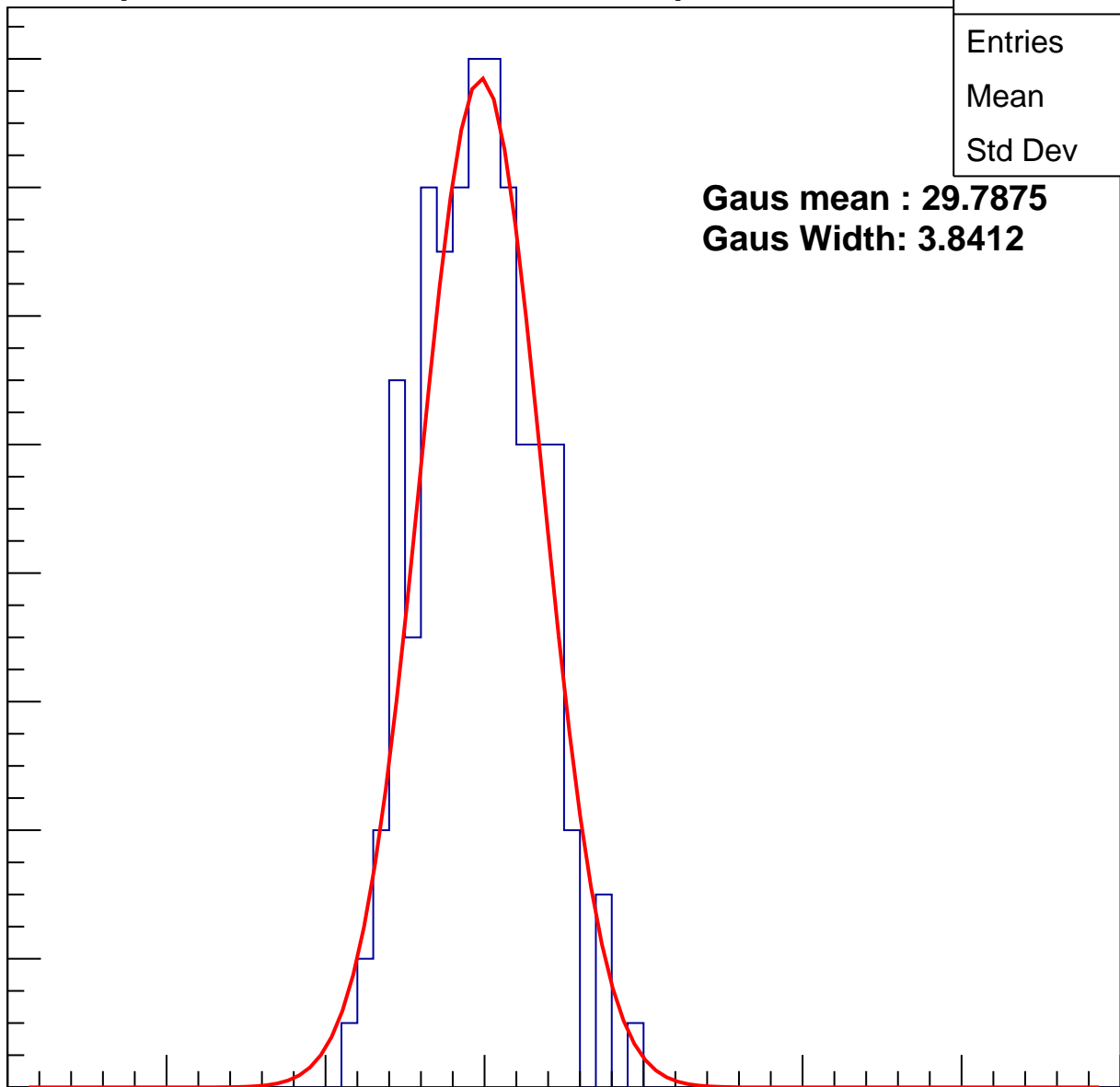
Gaus Width: 3.8412

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch103, adc1

calib_packv5_042523_0143.root, FC#1, port C1

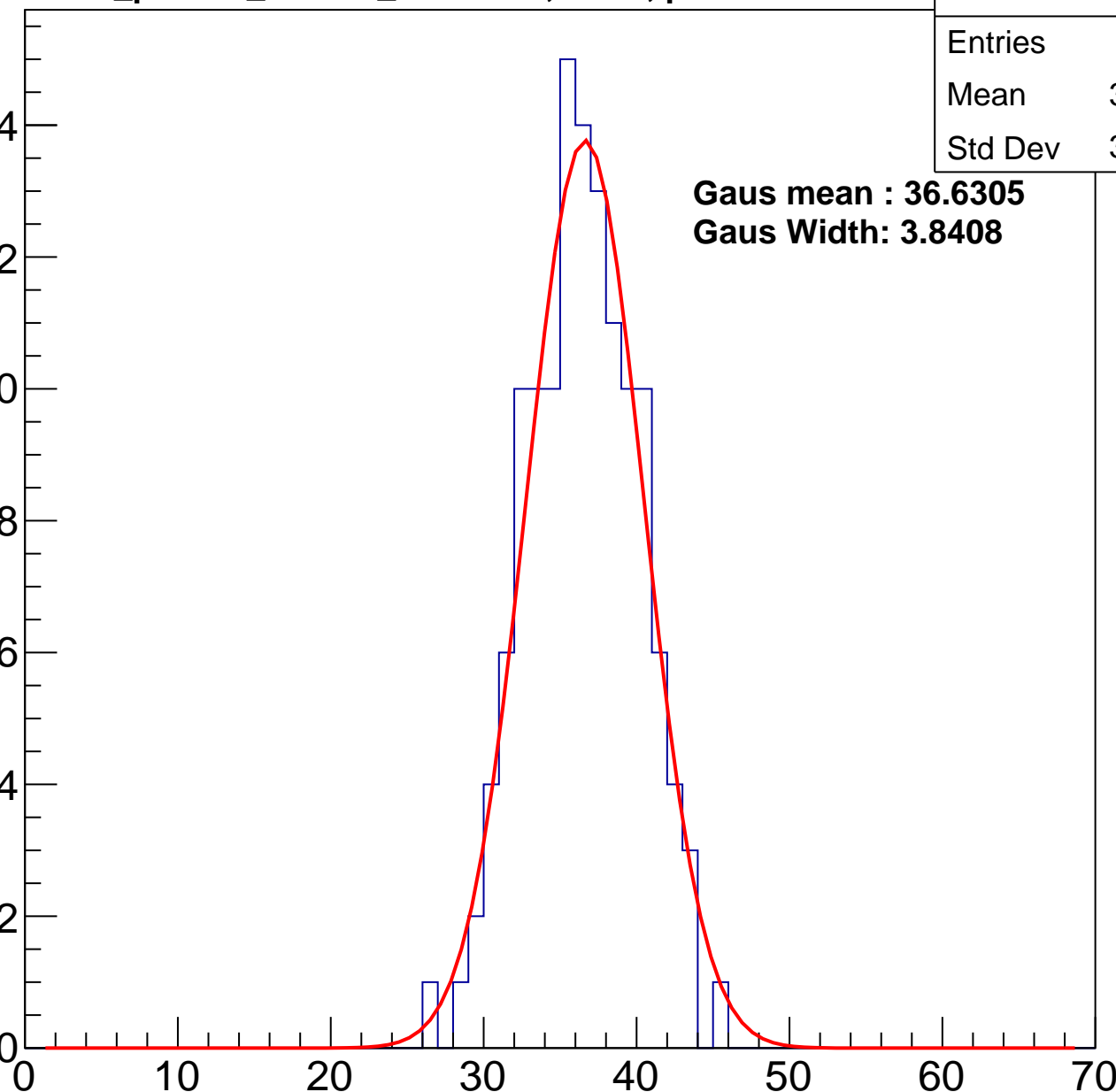
Entry

14
12
10
8
6
4
2
0

Entries	131
Mean	35.98
Std Dev	3.592

Gaus mean : 36.6305

Gaus Width: 3.8408



ampl

B0L101S, U19-ch103, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

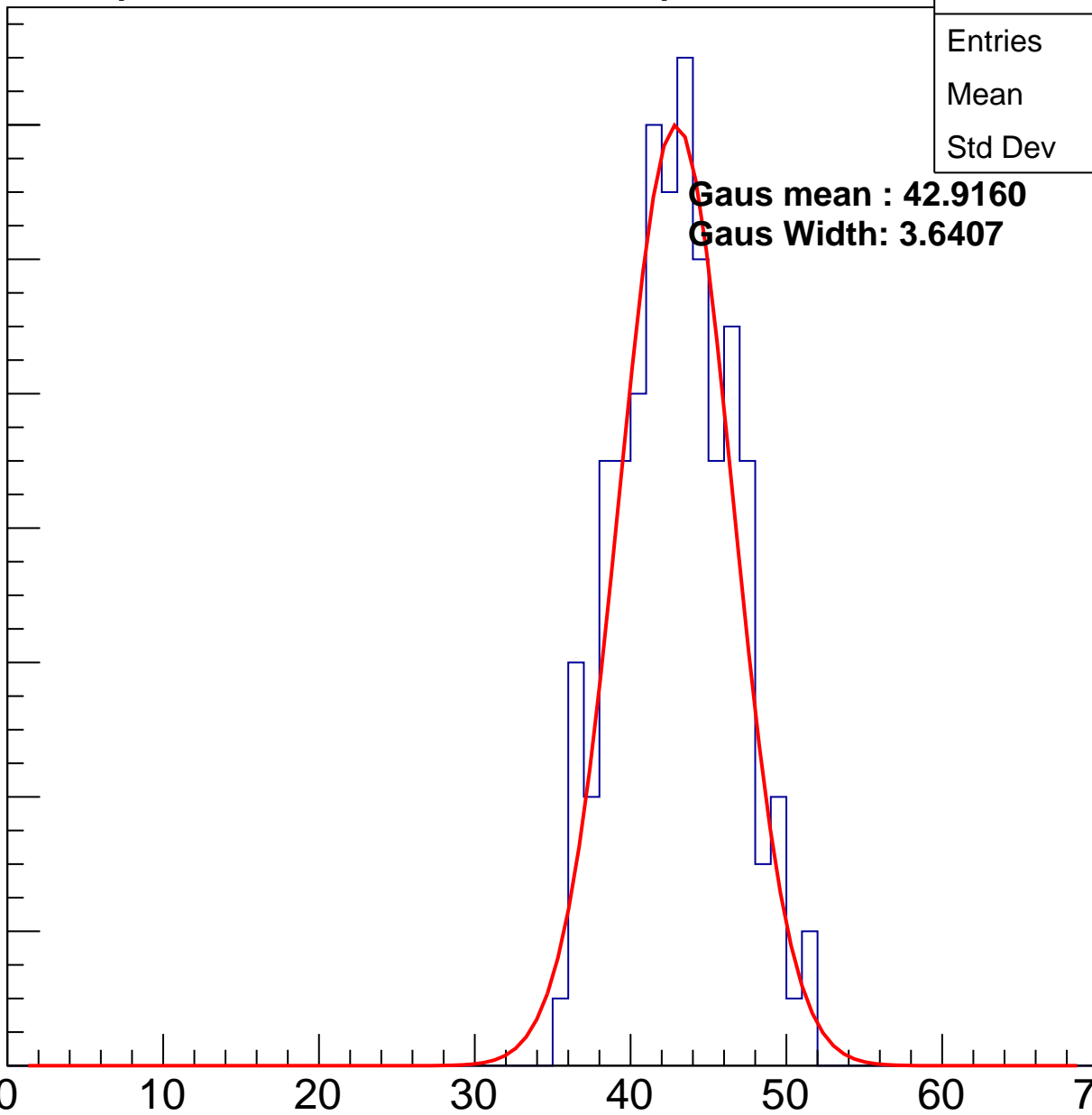
14
12
10
8
6
4
2
0

Entries	132
Mean	42.51
Std Dev	3.573

Gaus mean : 42.9160

Gaus Width: 3.6407

ampl



B0L101S, U19-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	49.72
Std Dev	3.719

Entry

16

14

12

10

8

6

4

2

0

0

10

20

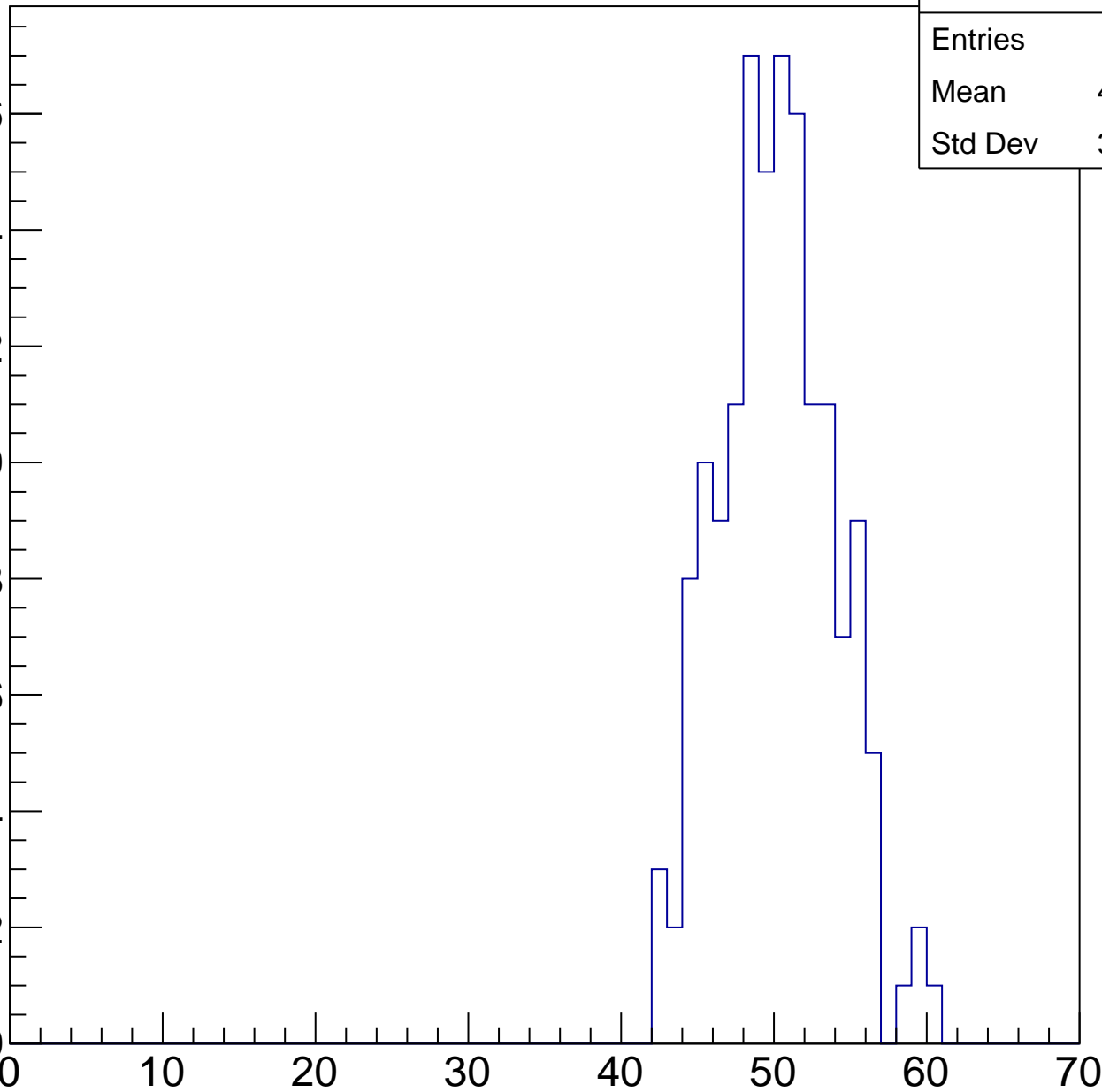
30

40

50

60

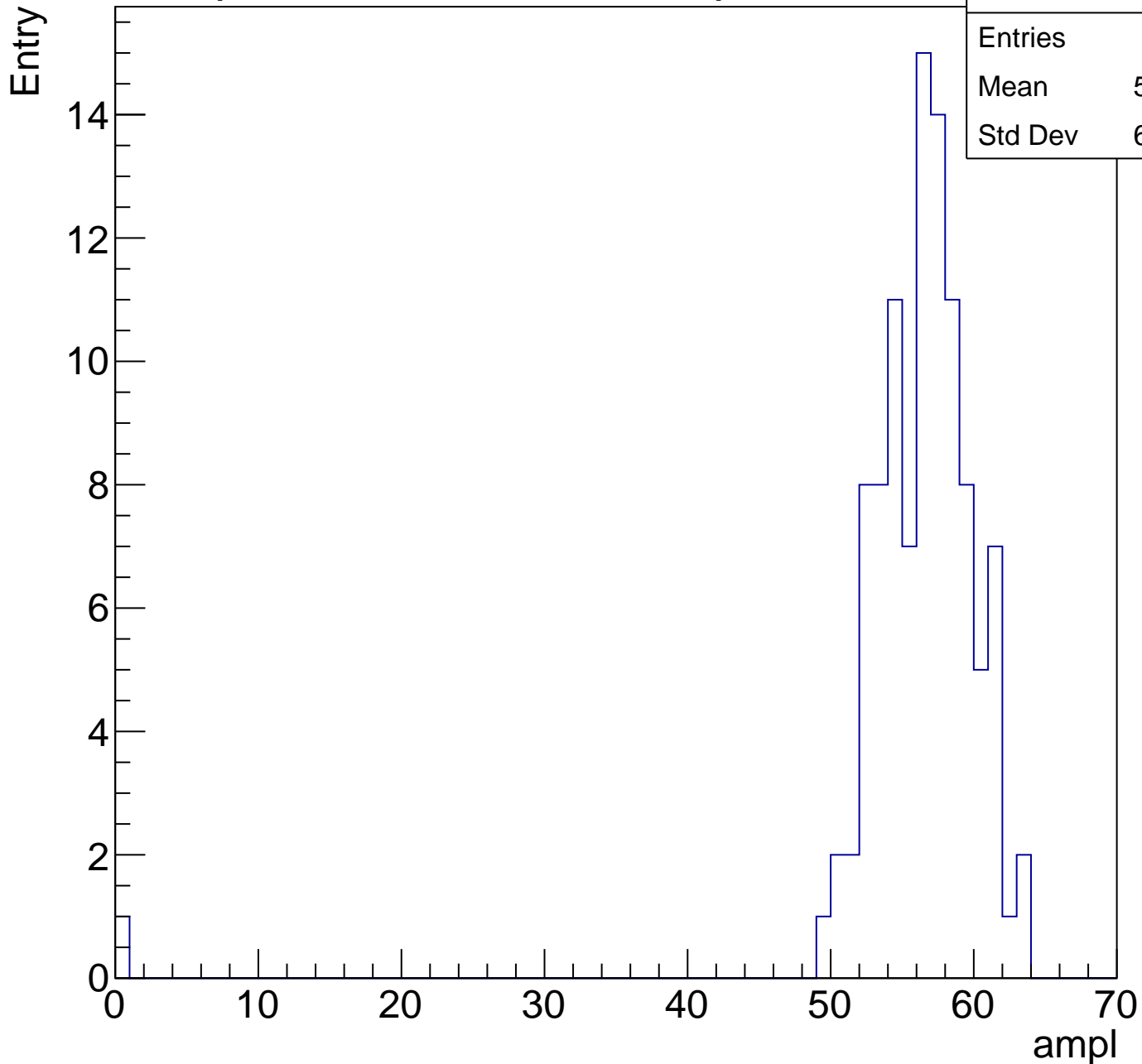
ampl



B0L101S, U19-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	55.66
Std Dev	6.282



B0L101S, U19-ch103, adc5

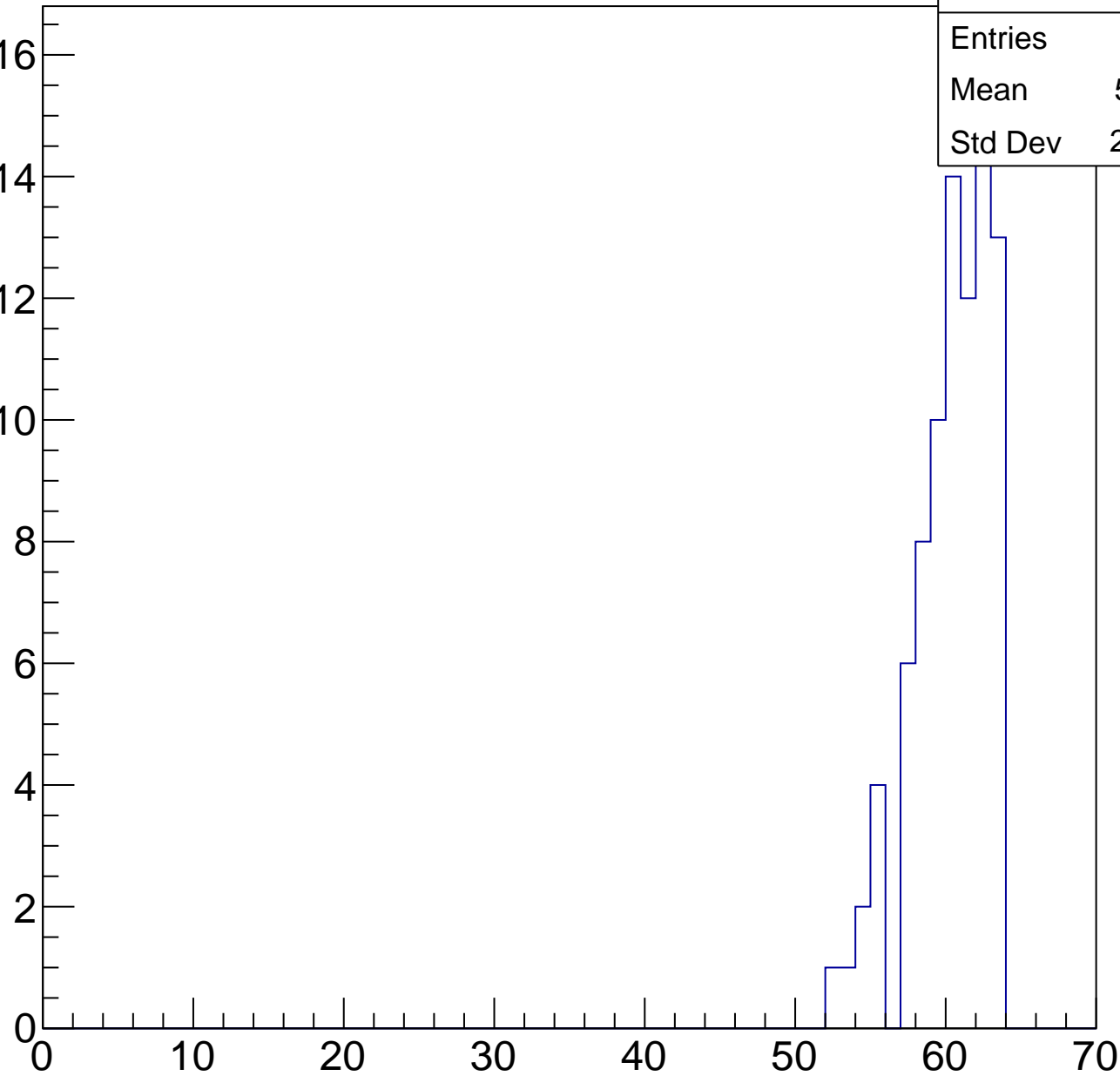
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.91
Std Dev	2.567

ampl

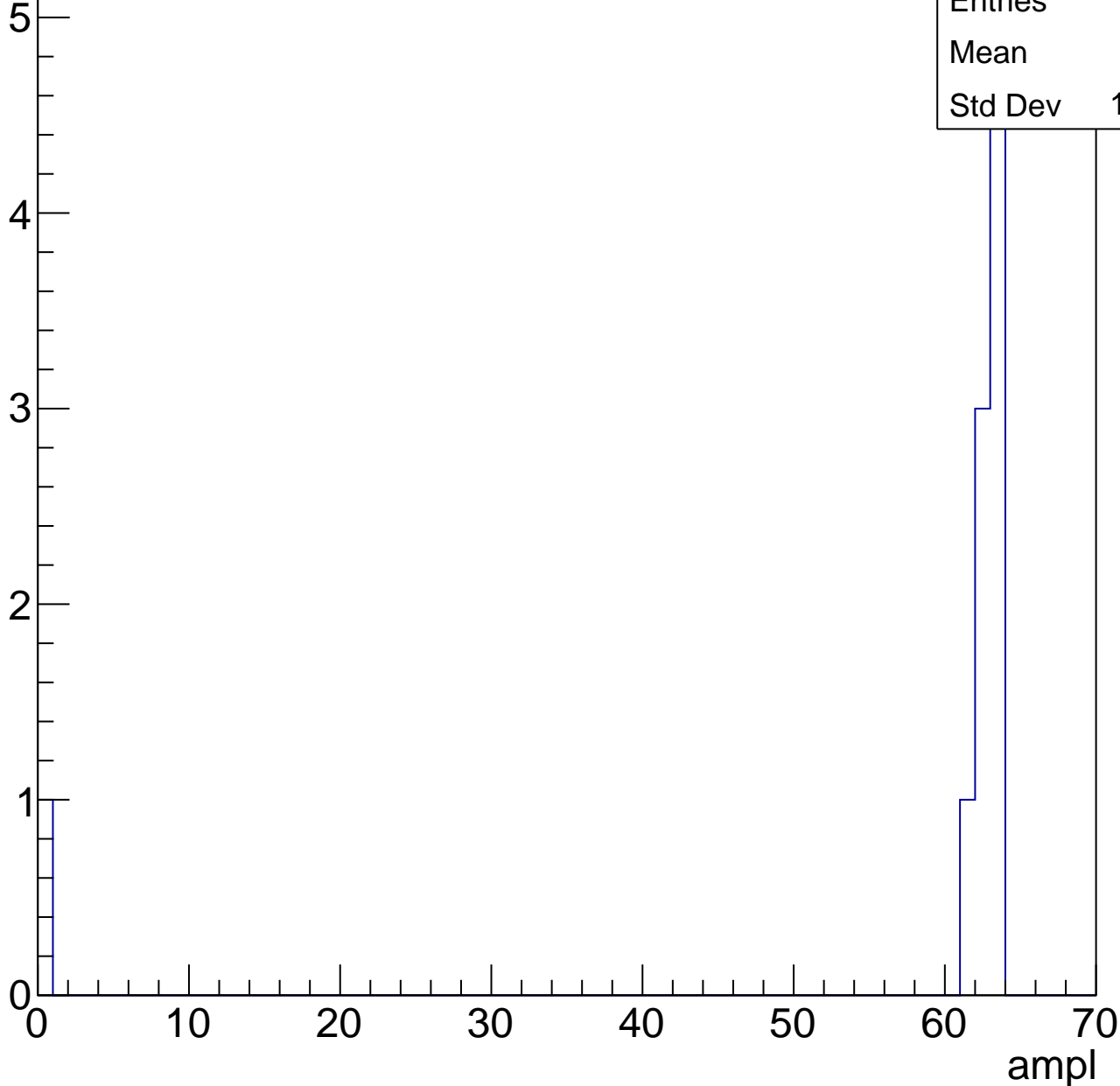


B0L101S, U19-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

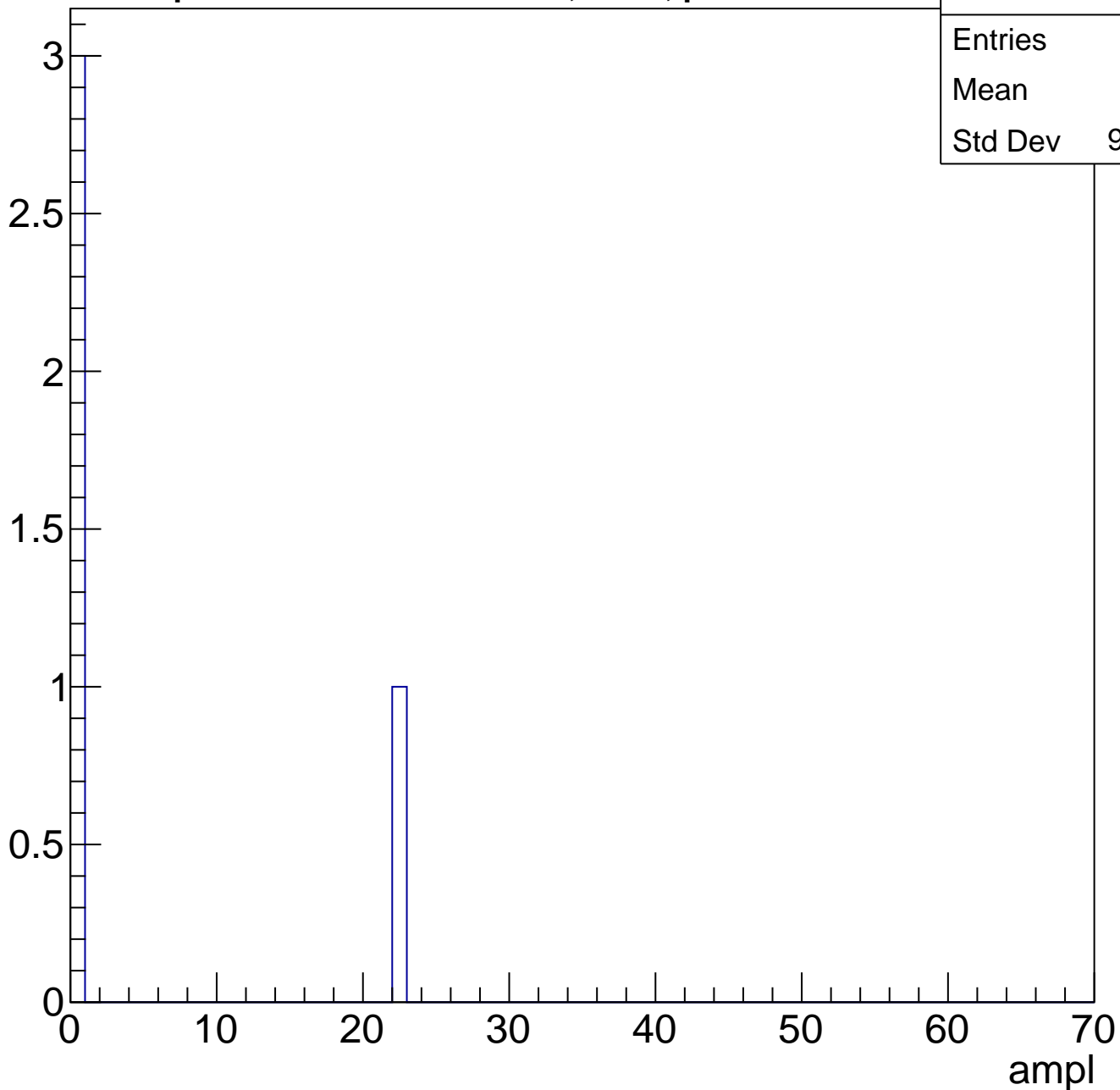
Entries	10
Mean	56.2
Std Dev	18.74



B0L101S, U19-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch104, adc0

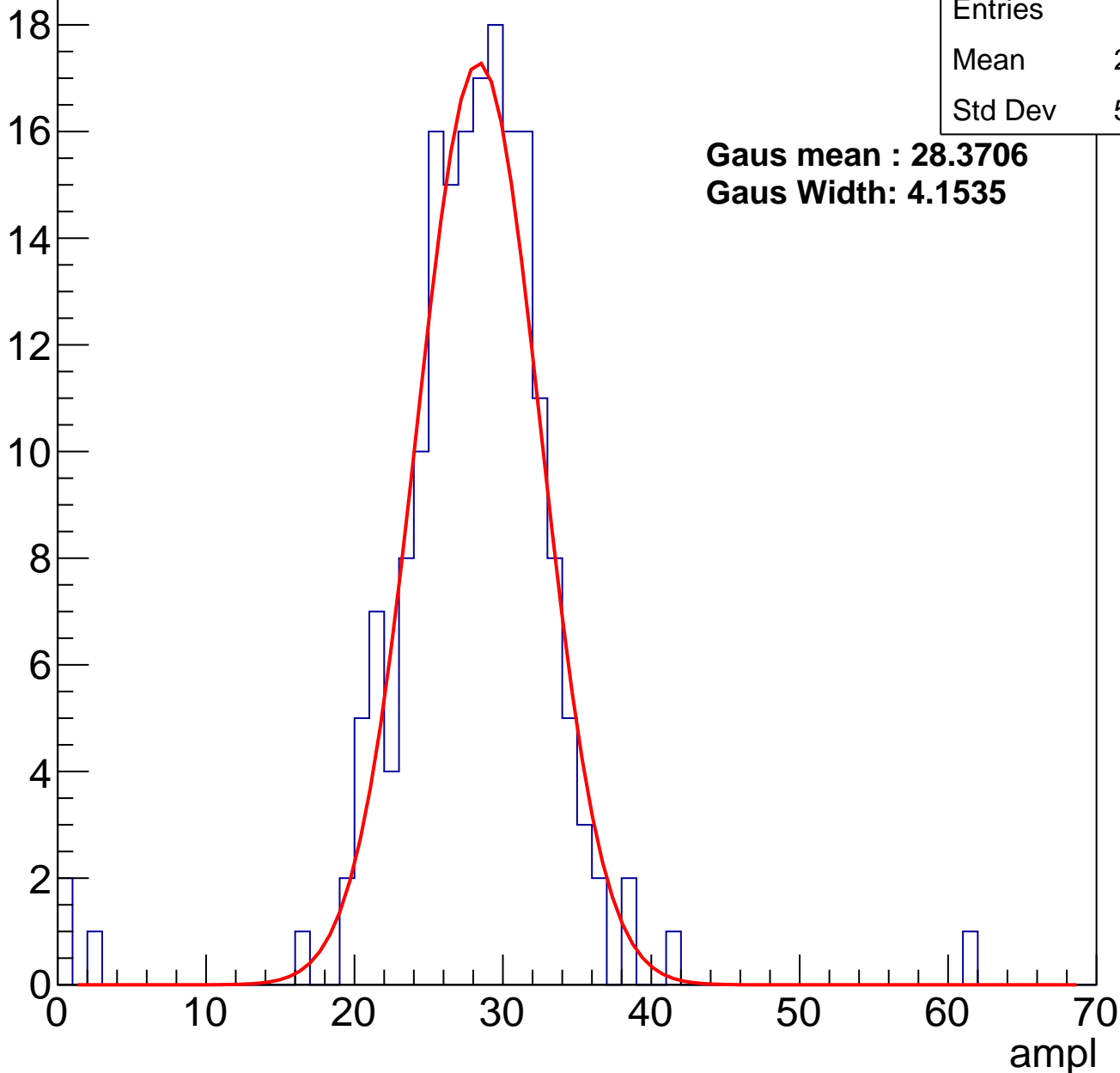
calib_packv5_042523_0143.root, FC#1, port C1

Entries	187
Mean	27.55
Std Dev	5.857

Gaus mean : 28.3706

Gaus Width: 4.1535

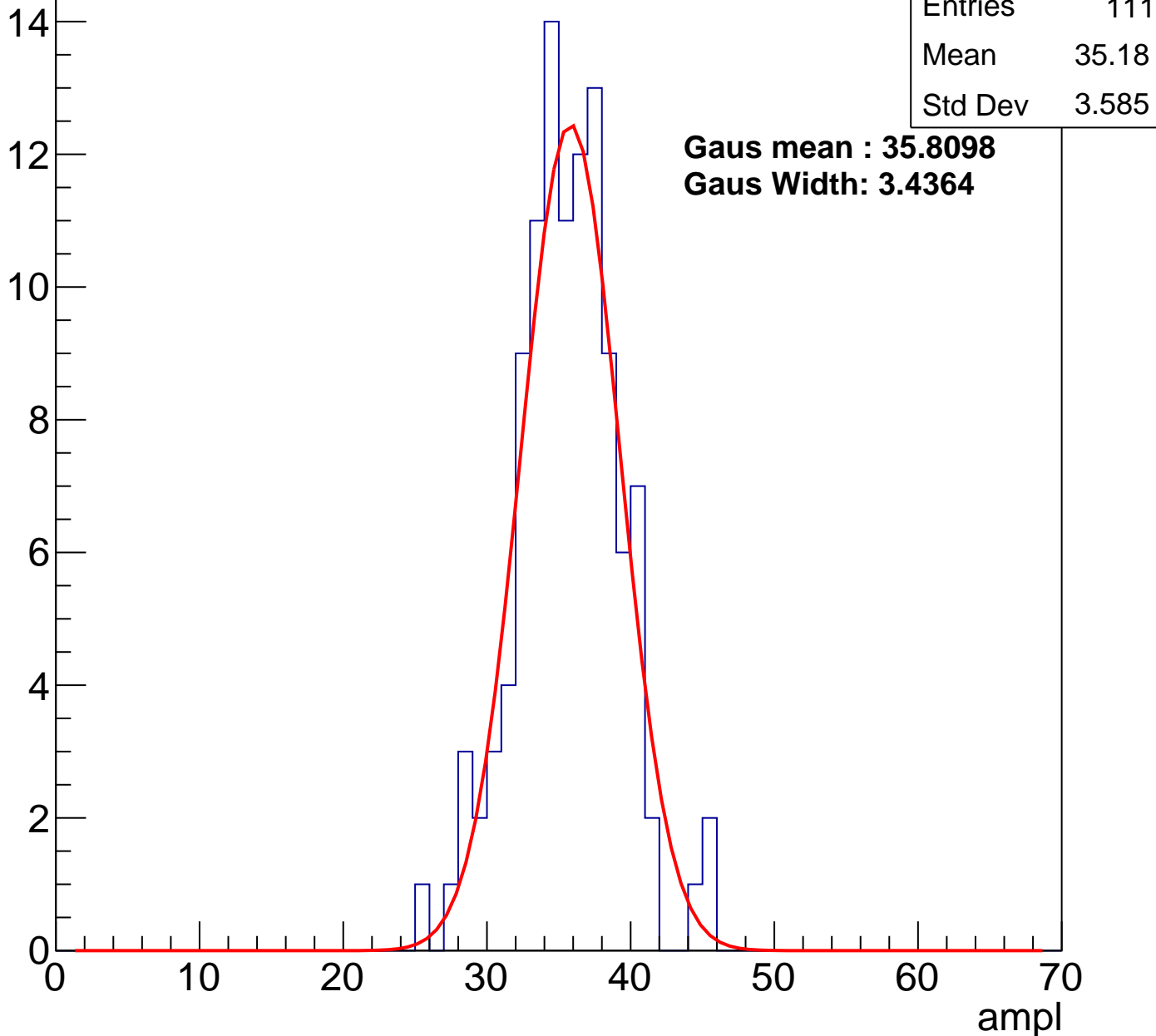
Entry



B0L101S, U19-ch104, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch104, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	121
Mean	41.03
Std Dev	3.657

Gaus mean : 41.4221

Gaus Width: 3.5797

0

B0L101S, U19-ch104, adc3

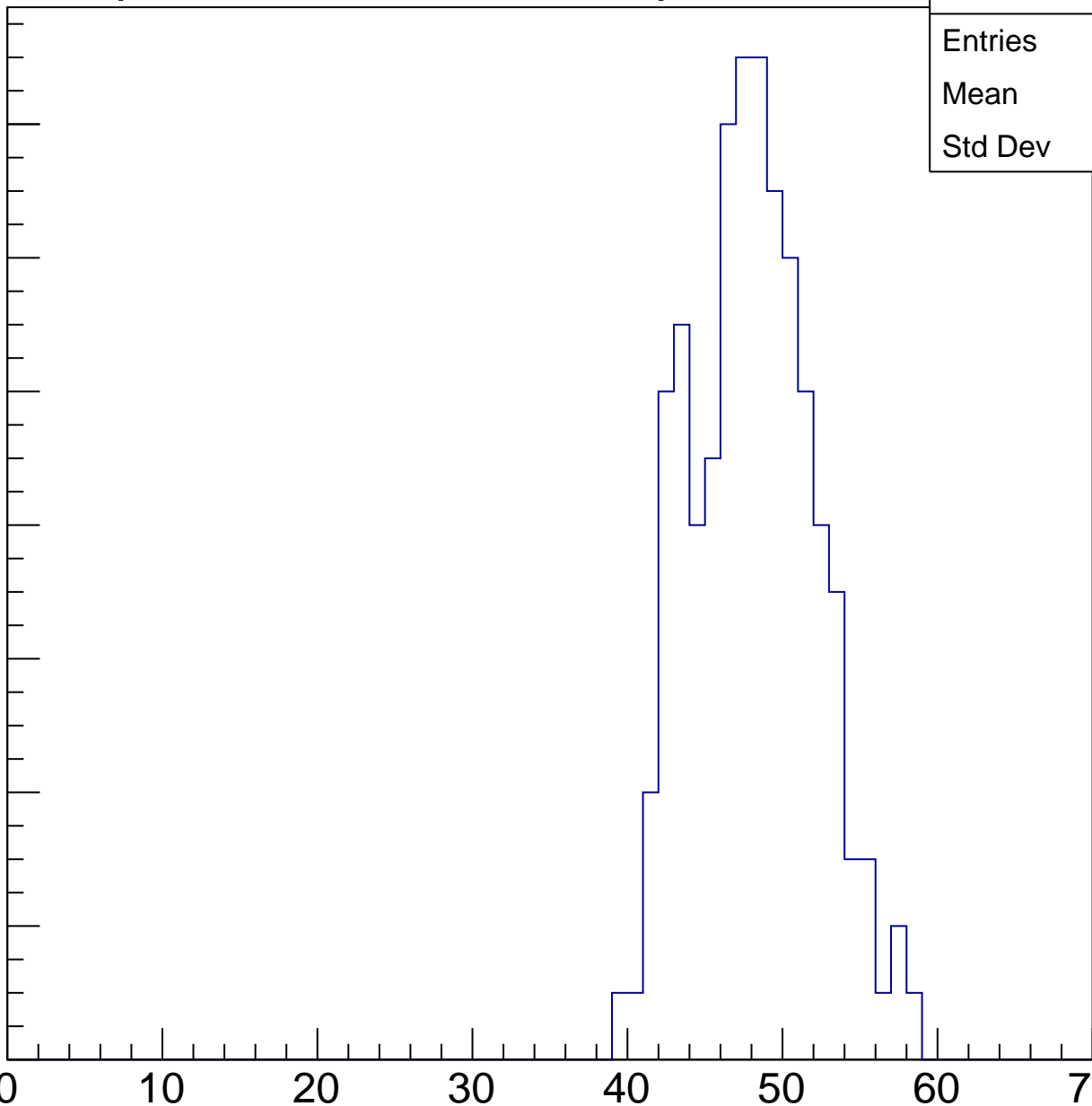
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	148
Mean	47.64
Std Dev	3.918

ampl



B0L101S, U19-ch104, adc4

calib_packv5_042523_0143.root, FC#1, port C1

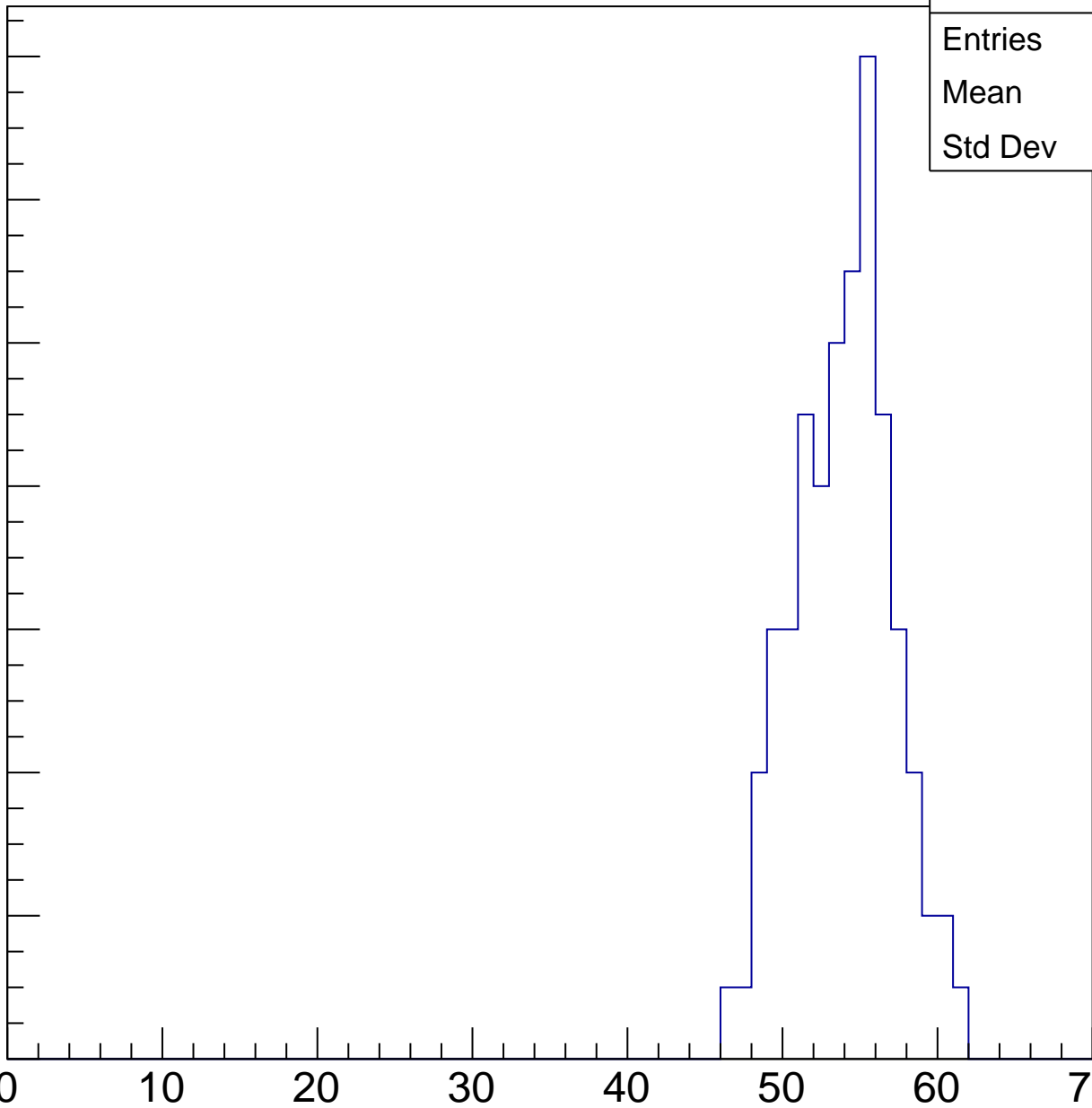
Entries	94
Mean	53.46
Std Dev	3.158

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch104, adc5

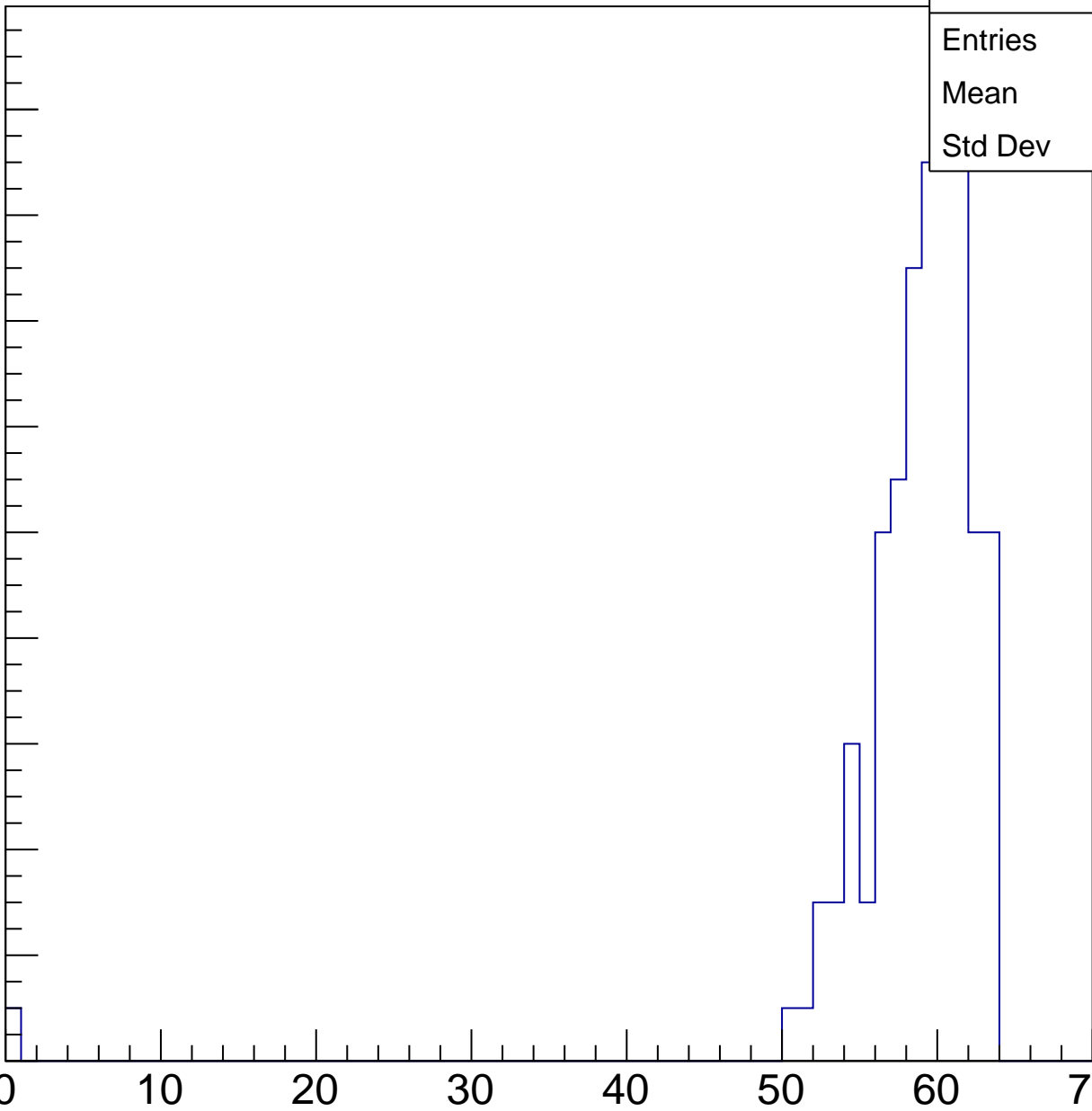
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	127
Mean	58.22
Std Dev	5.948

ampl

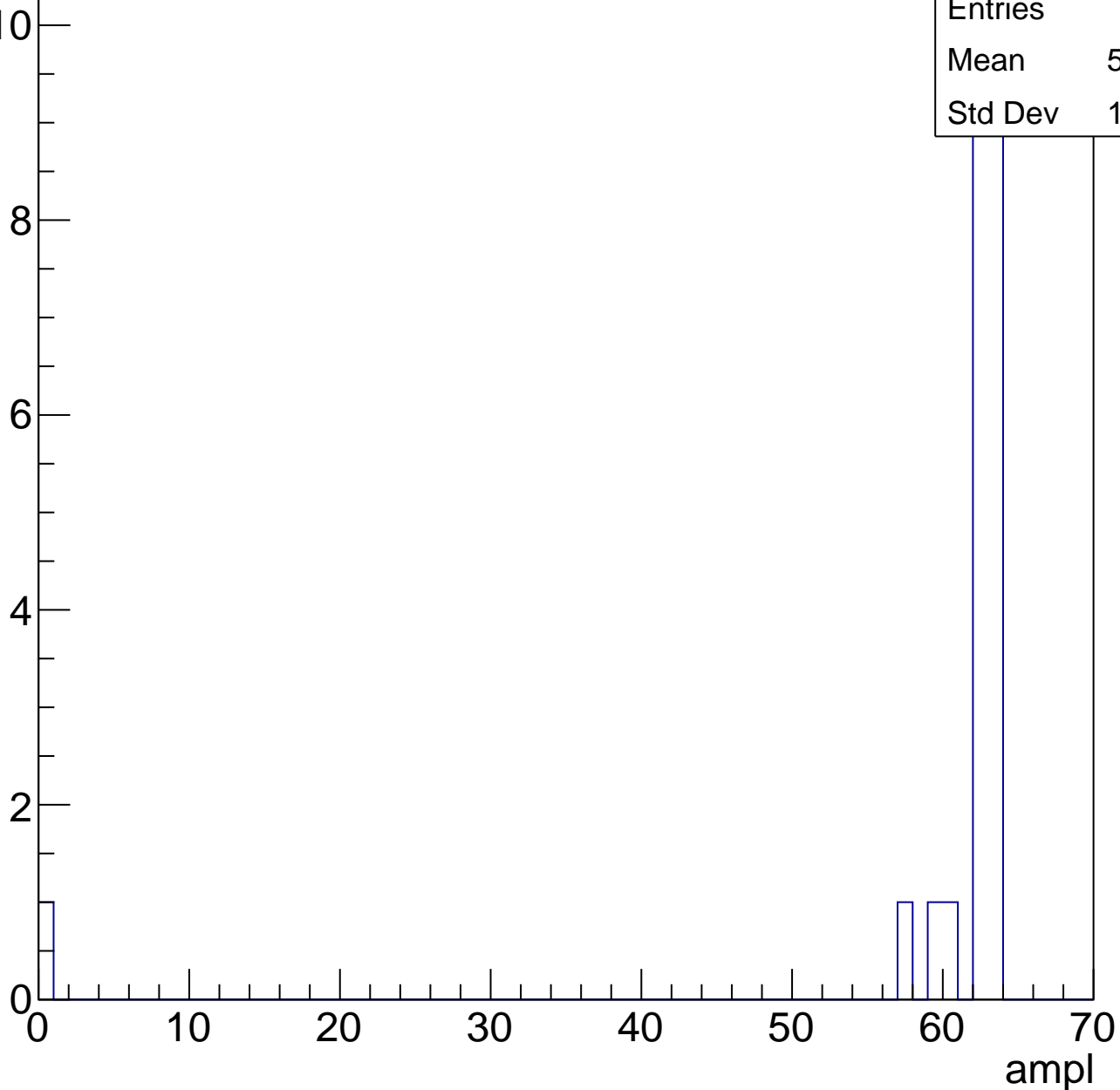


B0L101S, U19-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	24
Mean	59.42
Std Dev	12.47



B0L101S, U19-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

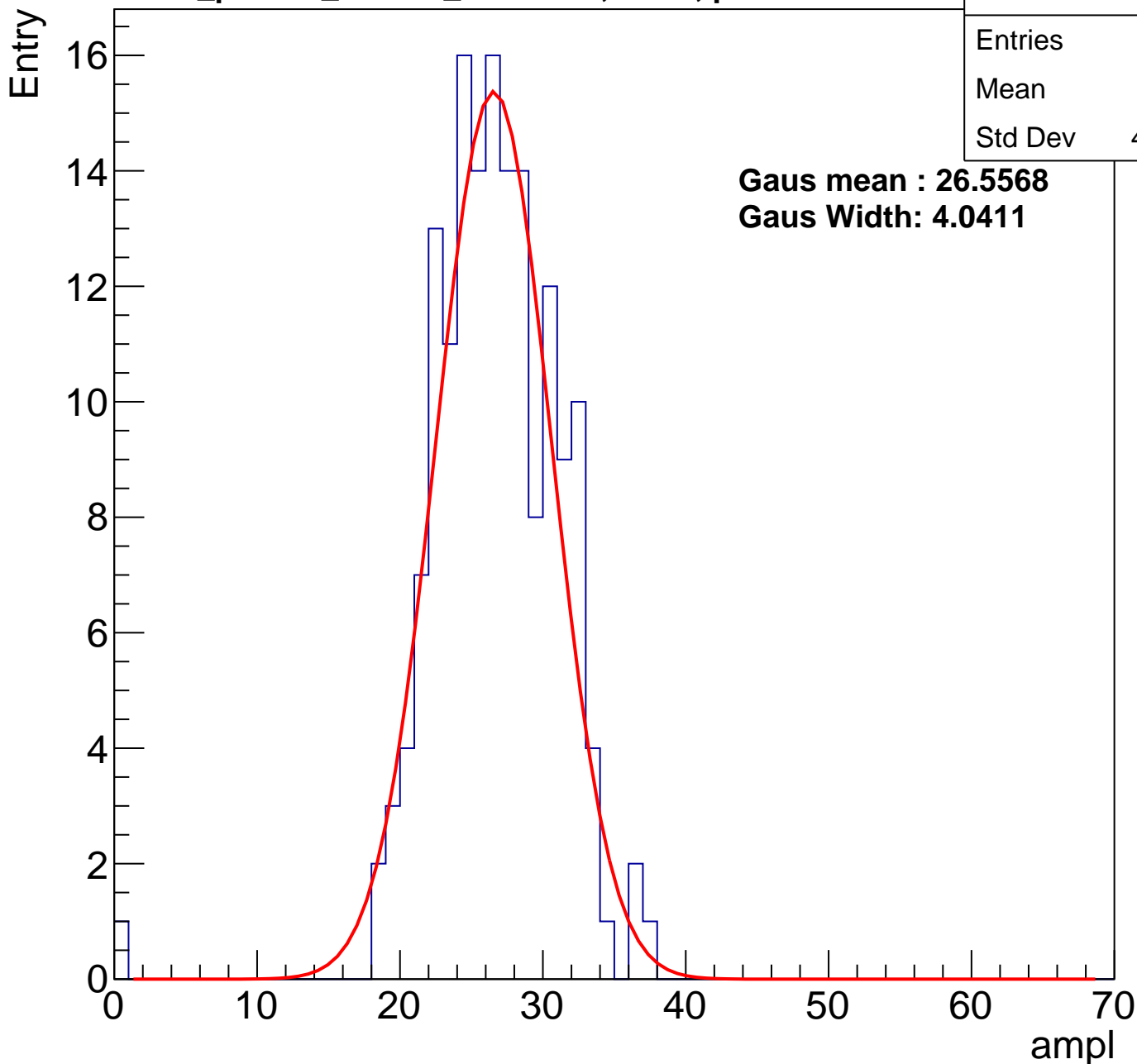
B0L101S, U19-ch105, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	26.2
Std Dev	4.408

Gaus mean : 26.5568

Gaus Width: 4.0411



B0L101S, U19-ch105, adc1

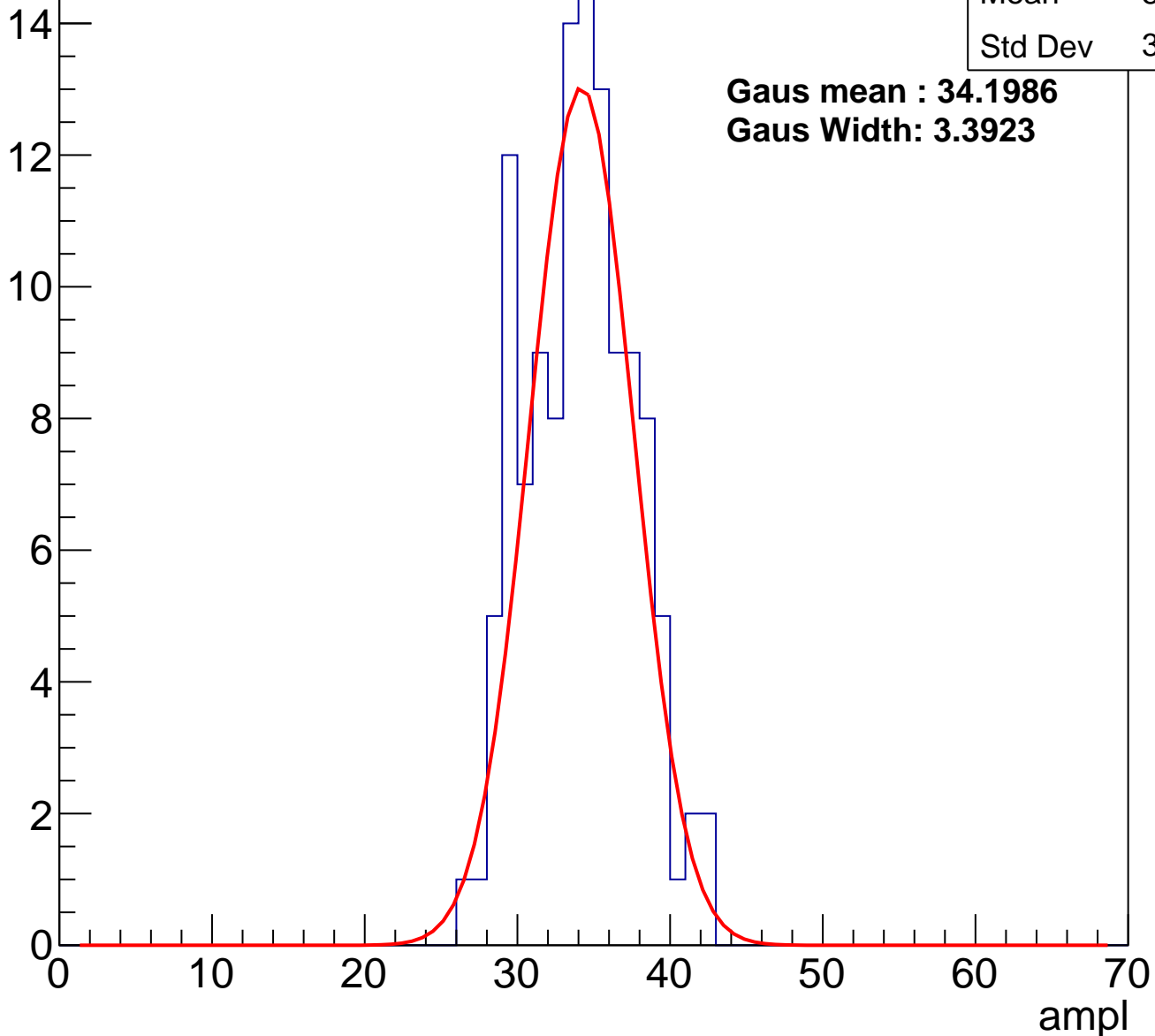
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	33.68
Std Dev	3.479

Gaus mean : 34.1986

Gaus Width: 3.3923

Entry



B0L101S, U19-ch105, adc2

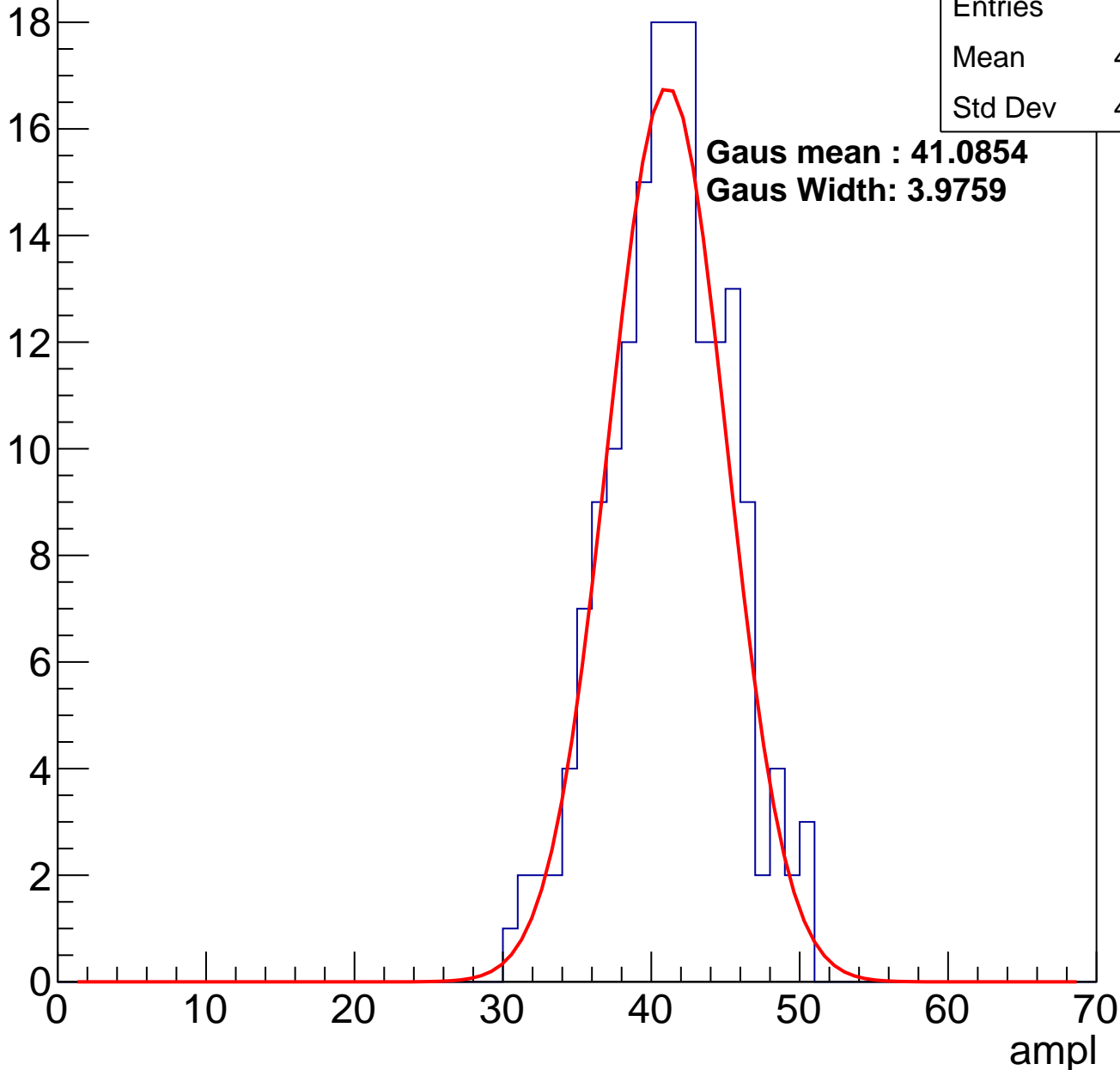
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	175
Mean	40.74
Std Dev	4.045

Gaus mean : 41.0854

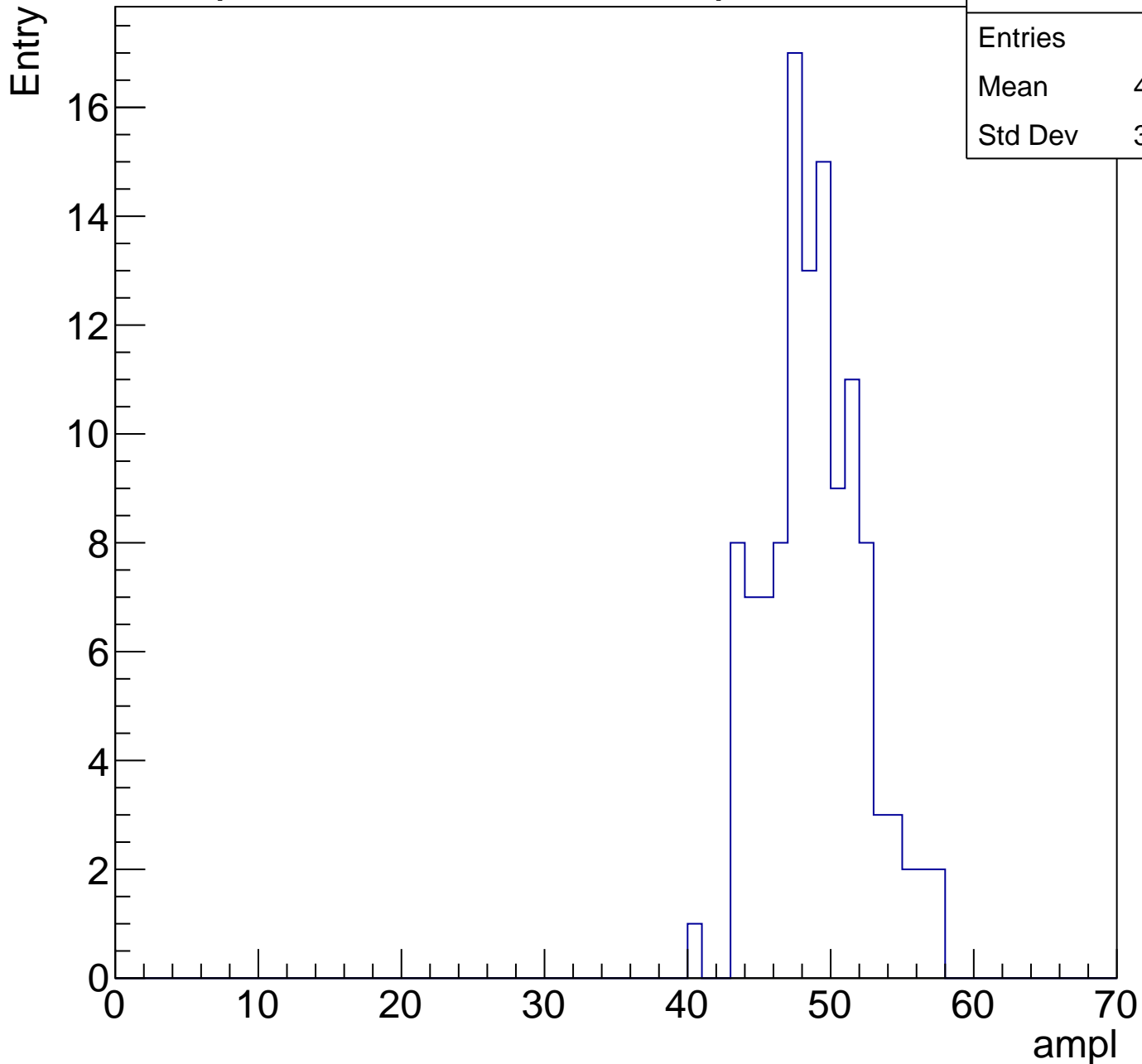
Gaus Width: 3.9759



B0L101S, U19-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

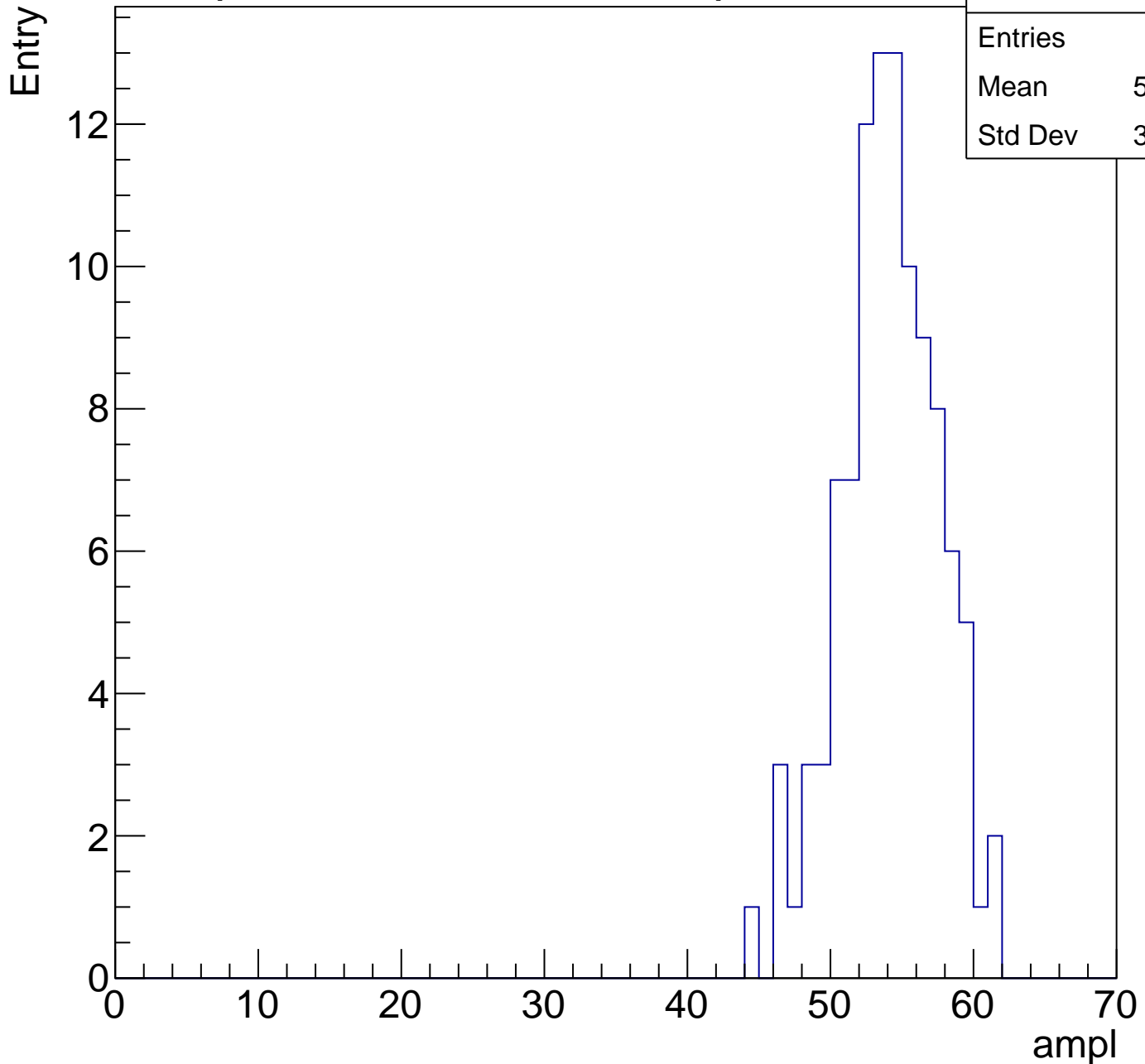
Entries	116
Mean	48.42
Std Dev	3.369



B0L101S, U19-ch105, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	53.62
Std Dev	3.423



B0L101S, U19-ch105, adc5

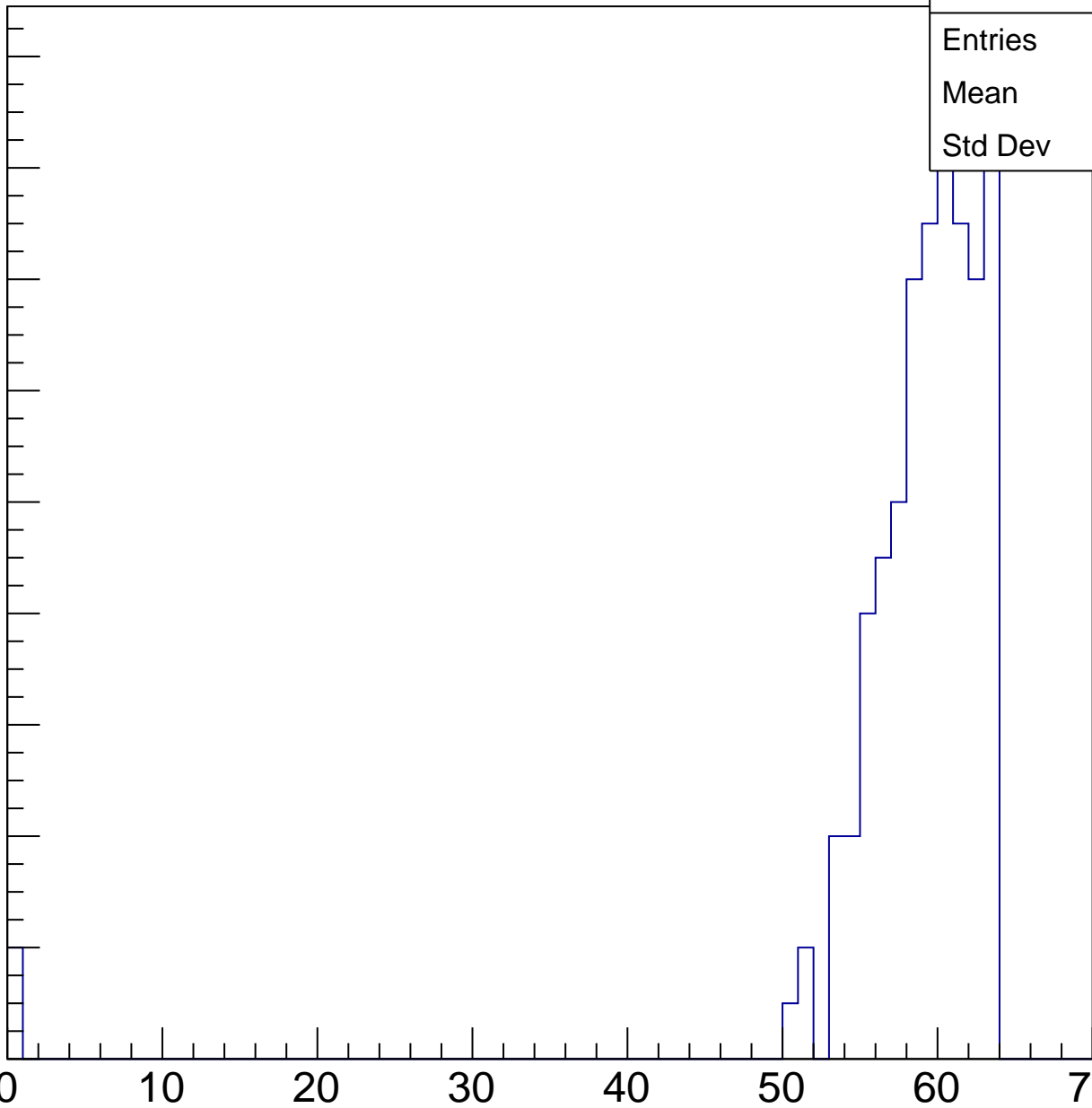
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	134
Mean	58.12
Std Dev	7.759

ampl

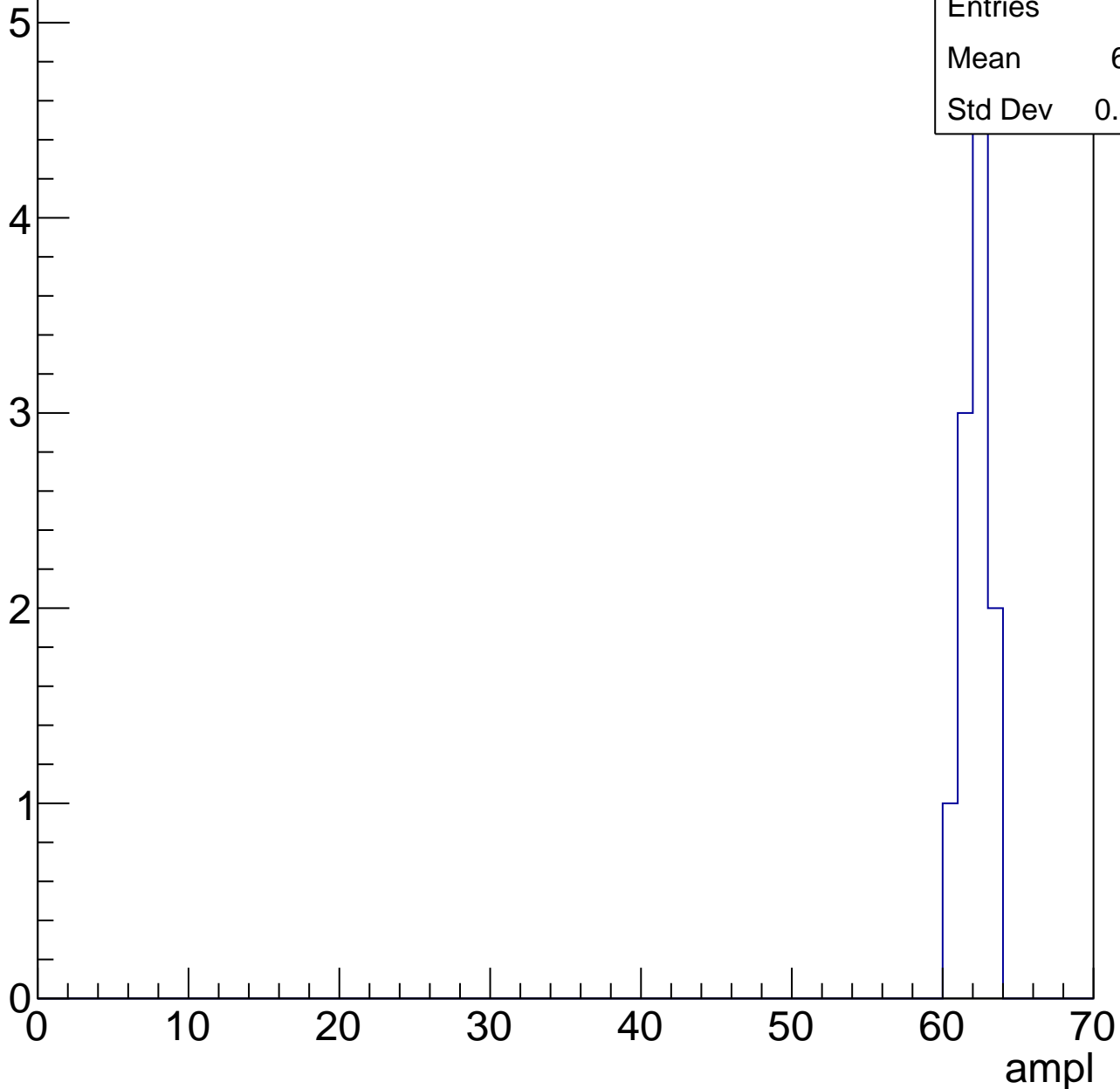


B0L101S, U19-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	61.73
Std Dev	0.8624



B0L101S, U19-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



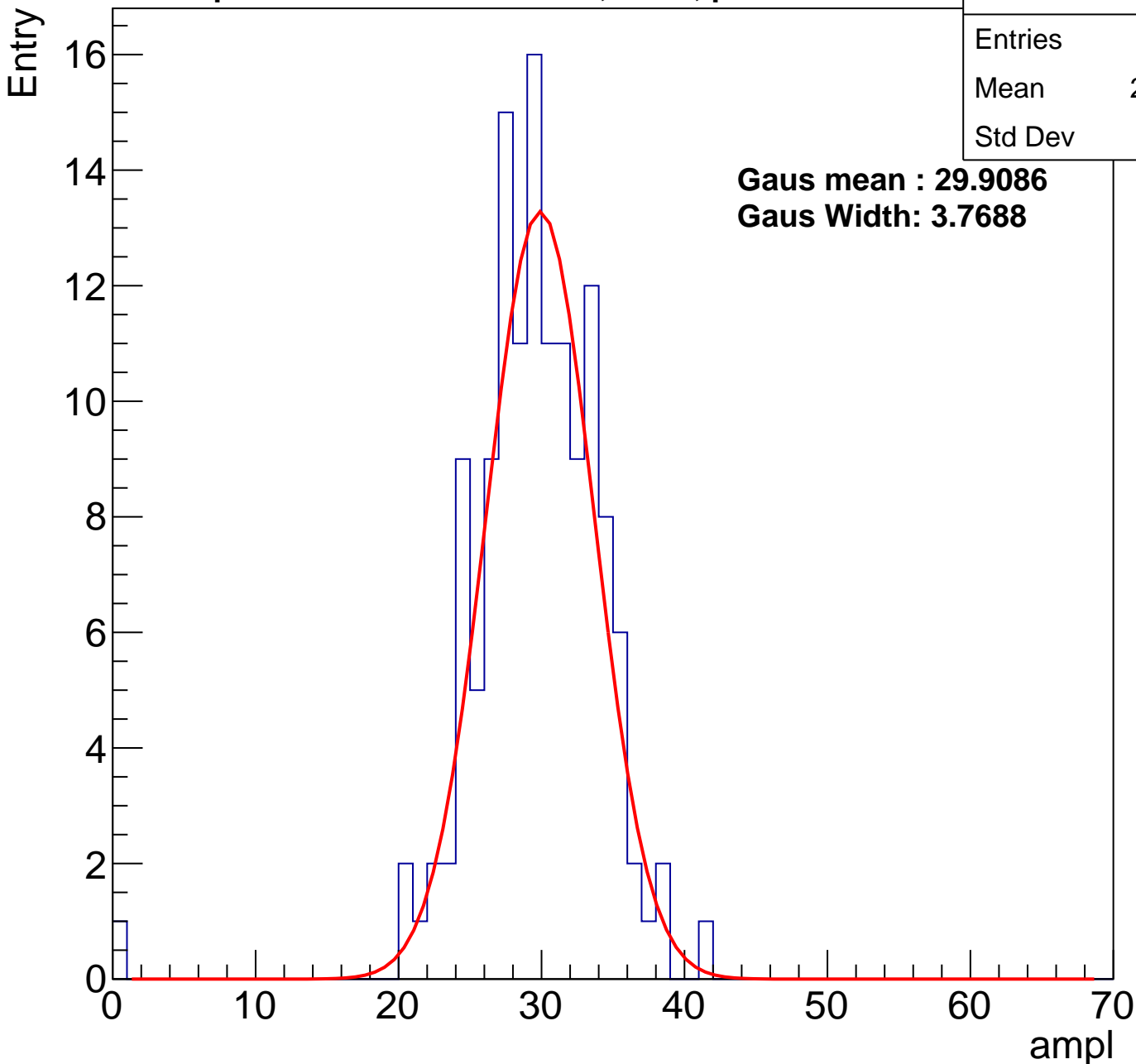
B0L101S, U19-ch106, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	29.16
Std Dev	4.62

Gaus mean : 29.9086

Gaus Width: 3.7688



B0L101S, U19-ch106, adc1

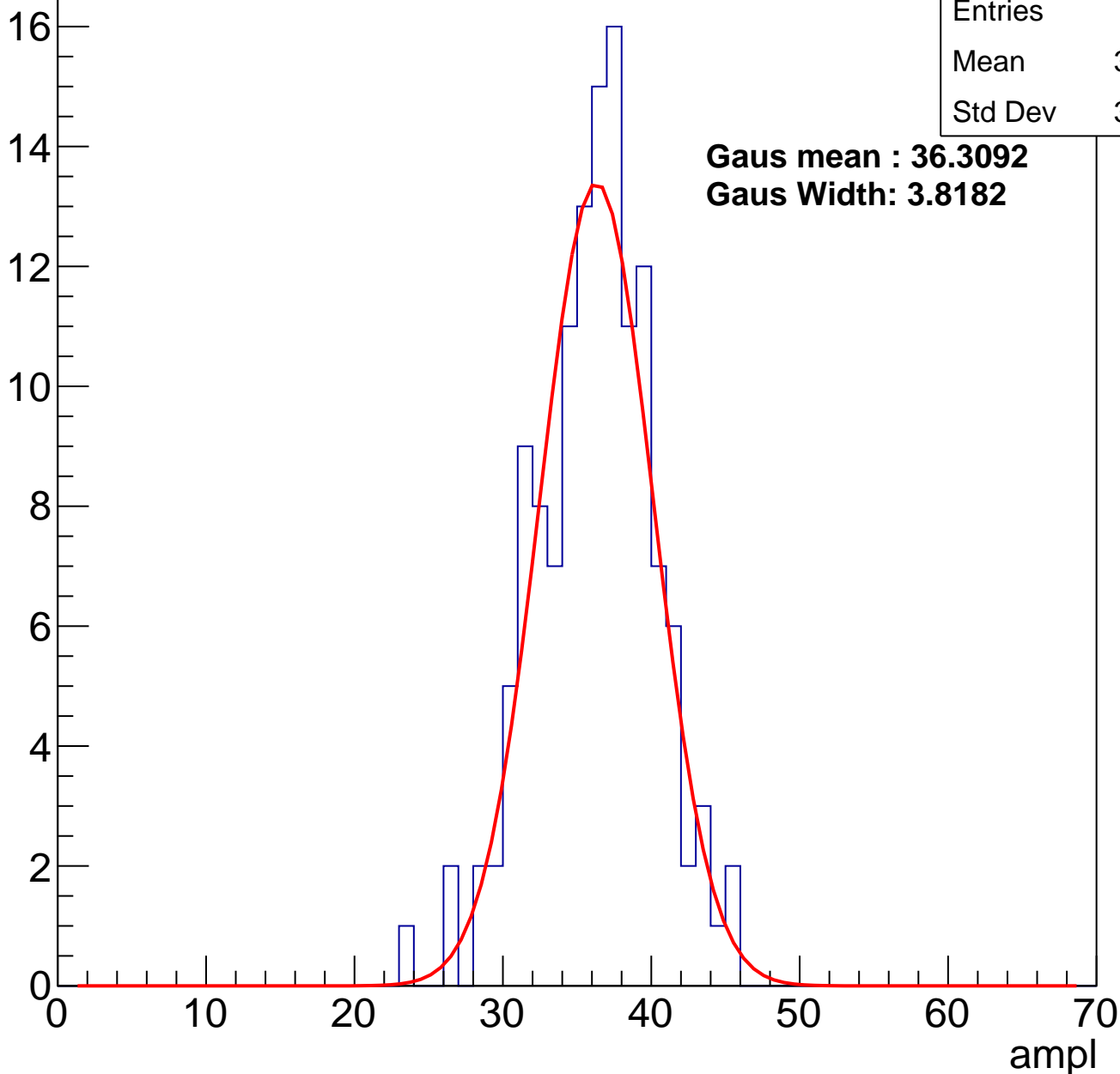
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	35.74
Std Dev	3.957

Gaus mean : 36.3092

Gaus Width: 3.8182

Entry



B0L101S, U19-ch106, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

42.8

Std Dev

3.588

Gaus mean : 43.3837

Gaus Width: 3.5263

0

10

20

30

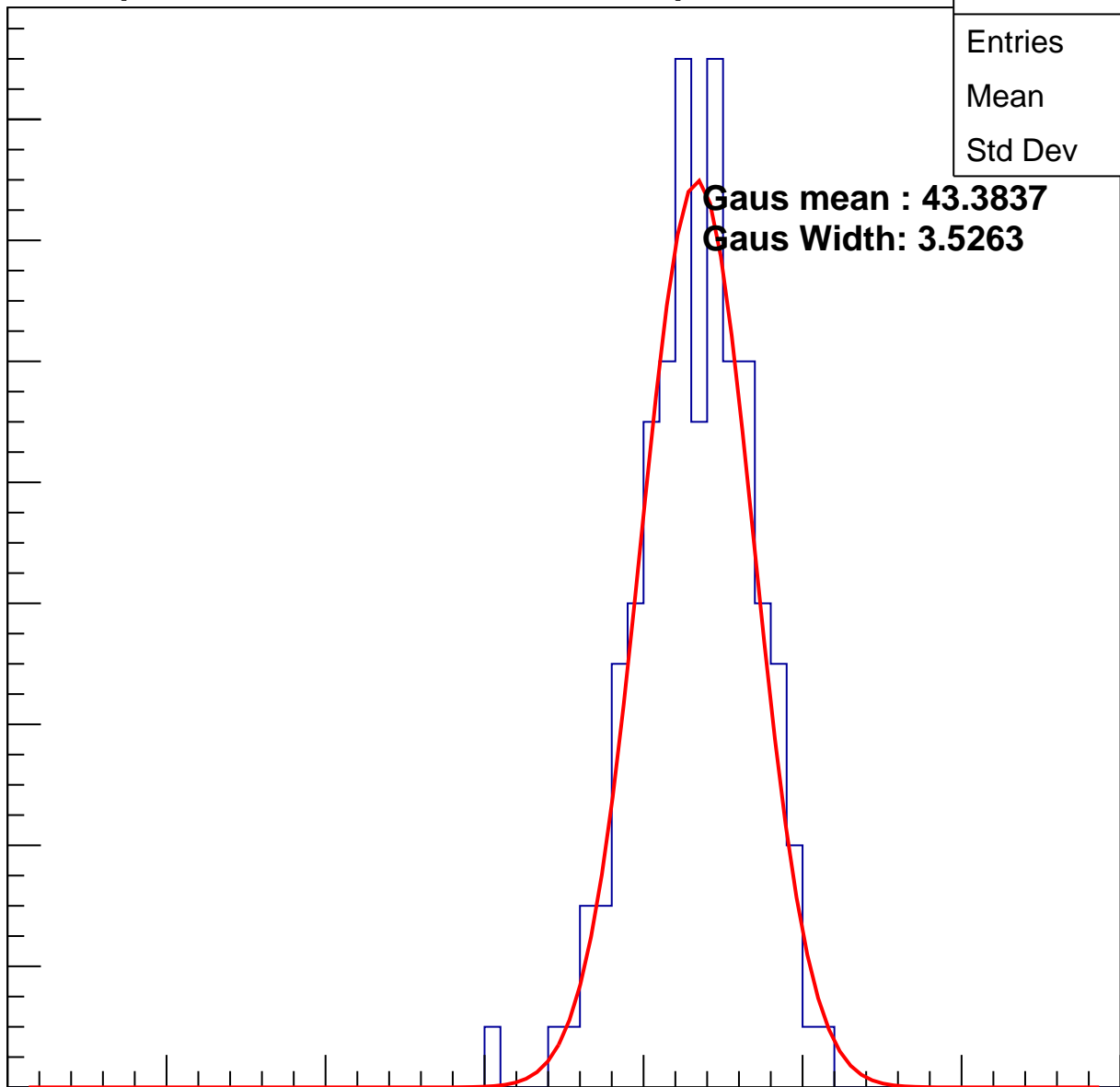
40

50

60

70

ampl



B0L101S, U19-ch106, adc3

calib_packv5_042523_0143.root, FC#1, port C1

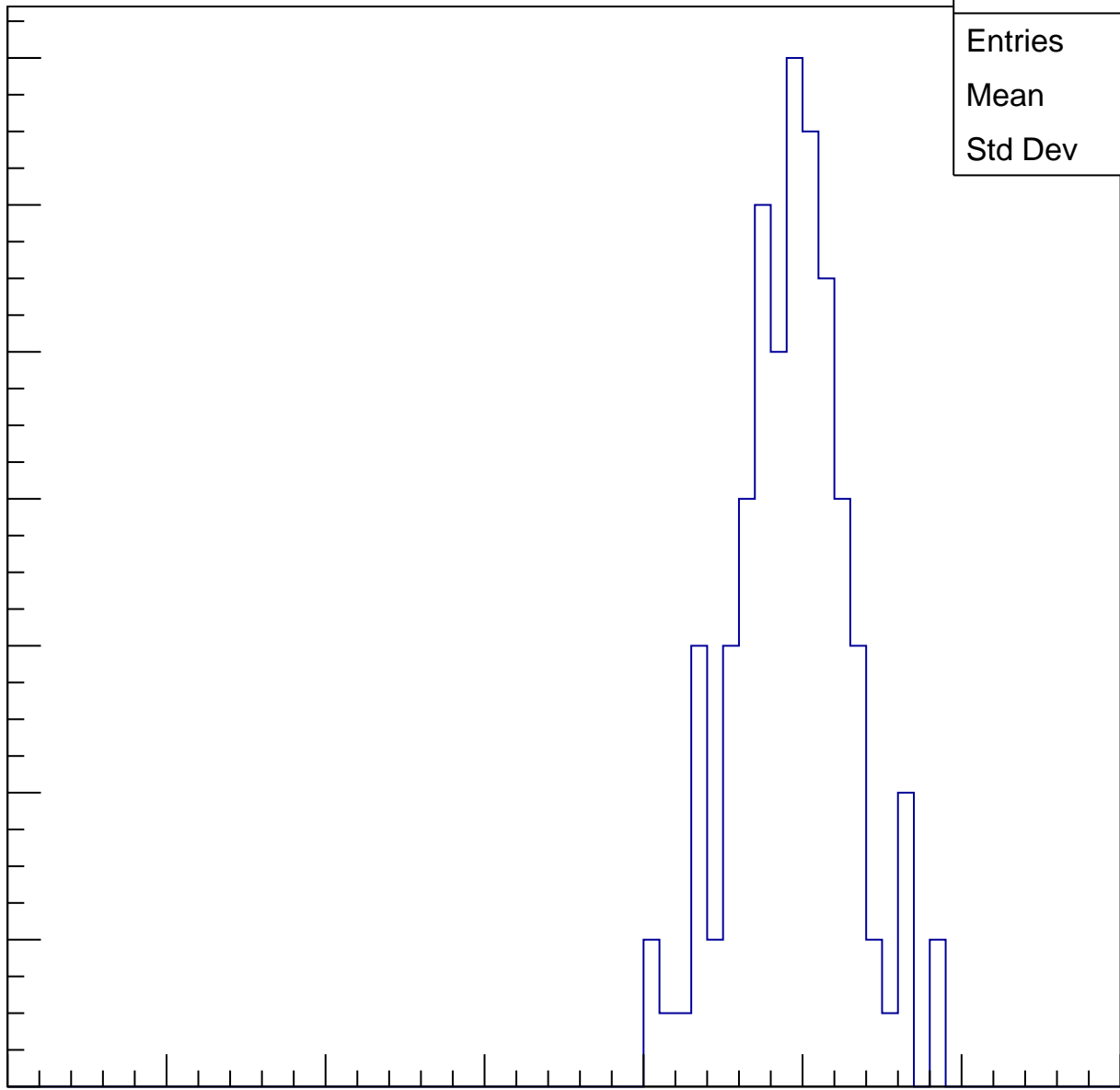
Entries	109
Mean	48.85
Std Dev	3.627

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch106, adc4

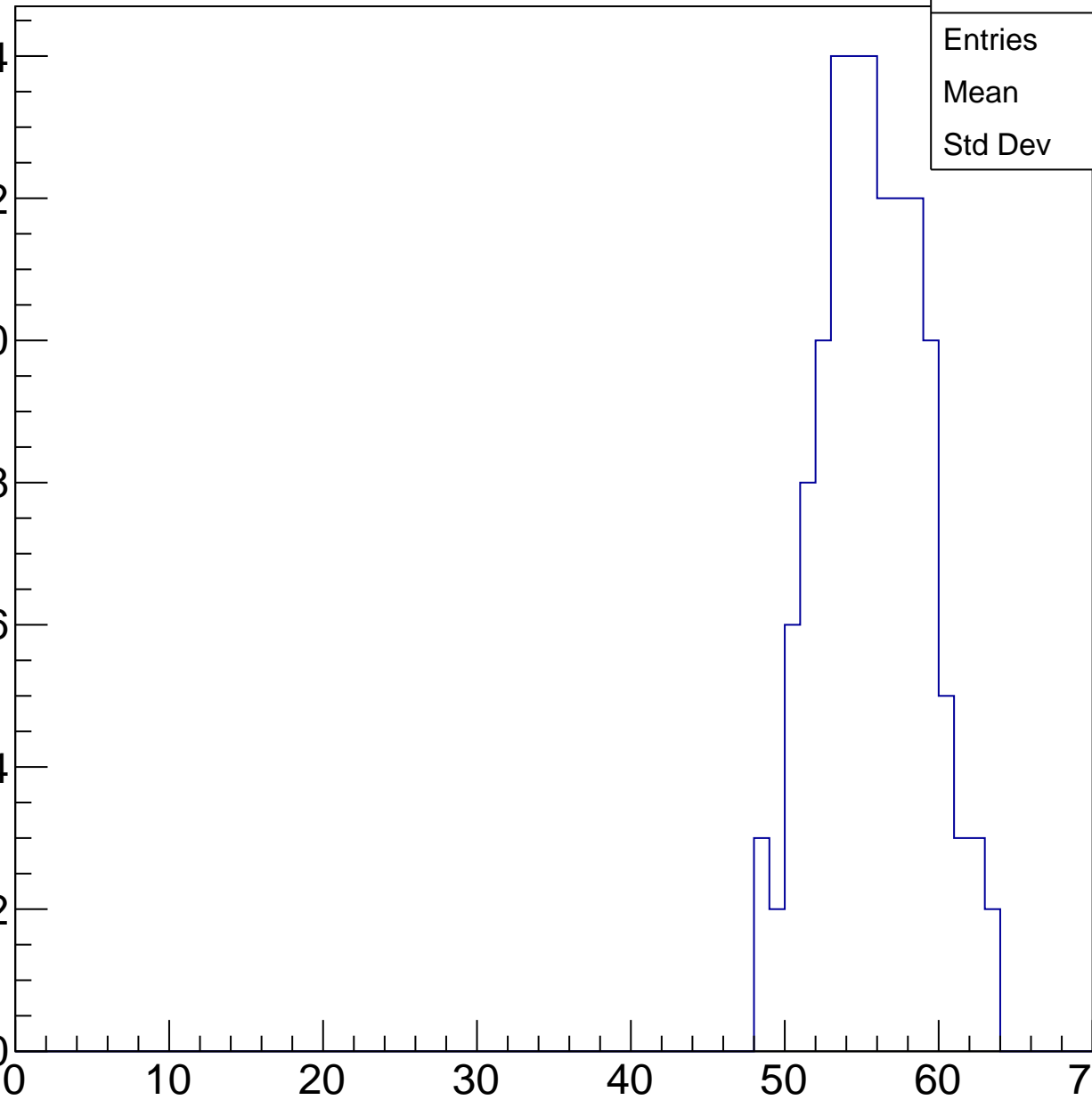
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	55.19
Std Dev	3.383

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch106, adc5

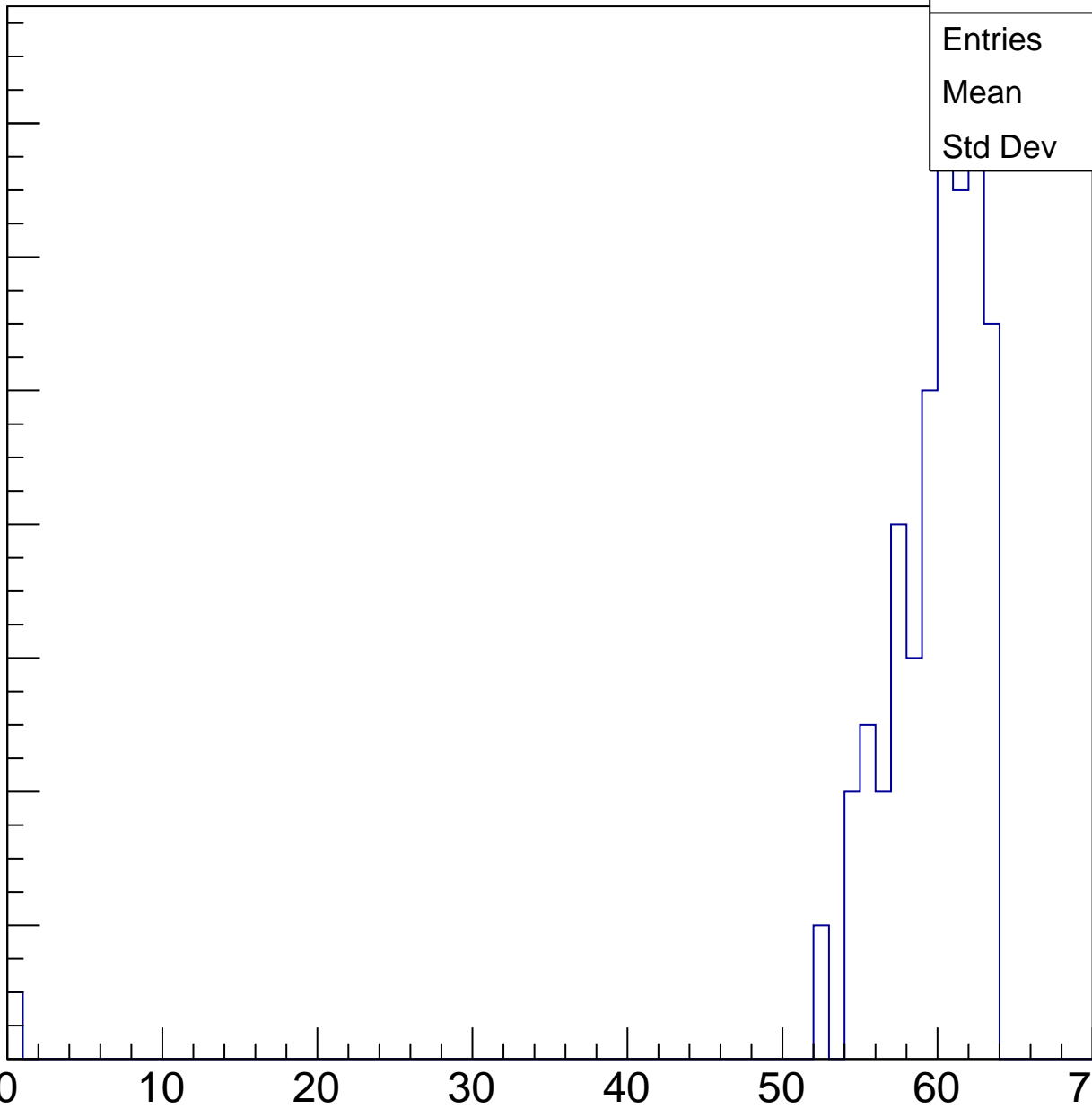
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	58.81
Std Dev	6.725

ampl

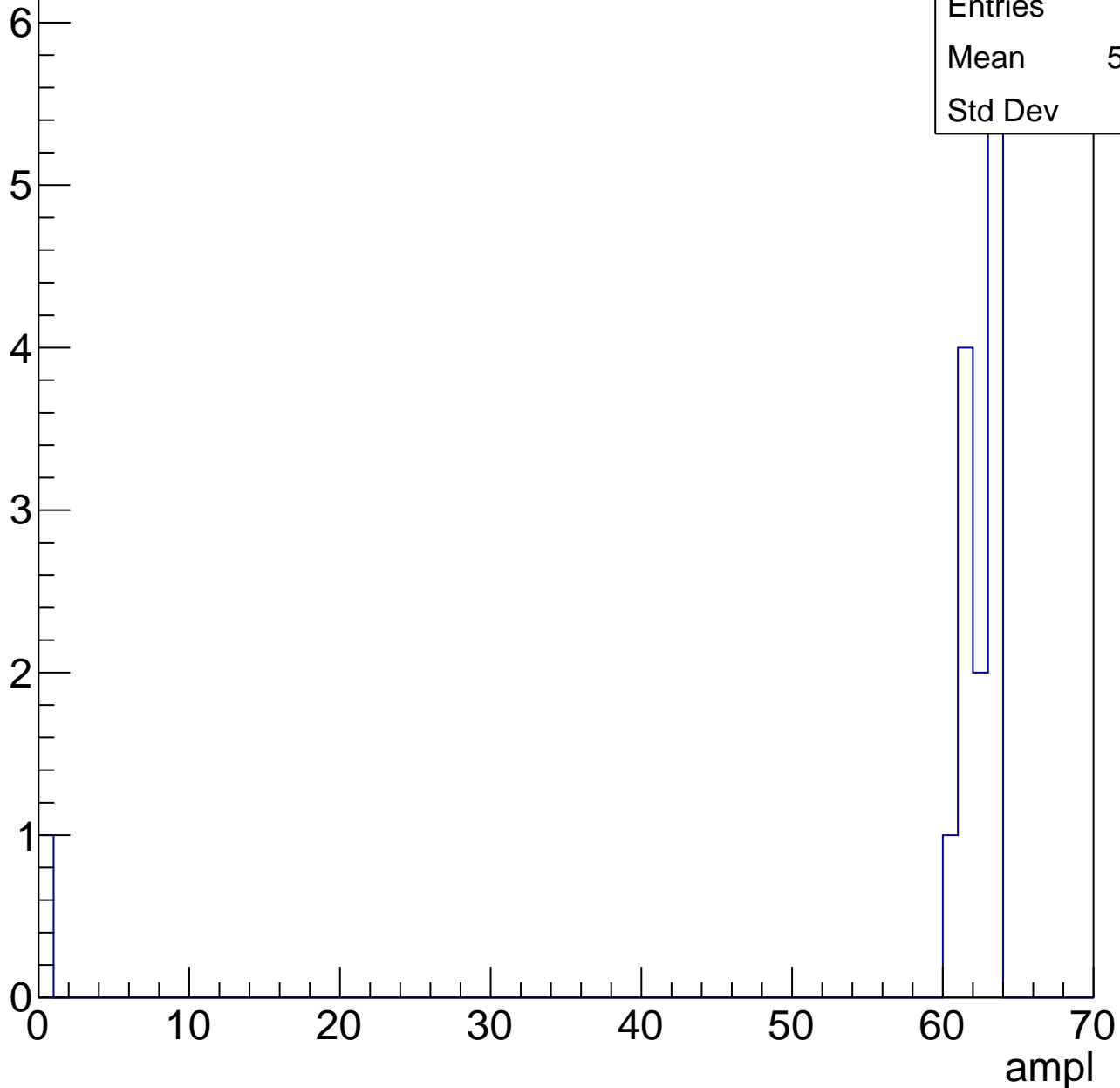


B0L101S, U19-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.57
Std Dev	16



B0L101S, U19-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch107, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	29.91
Std Dev	3.49

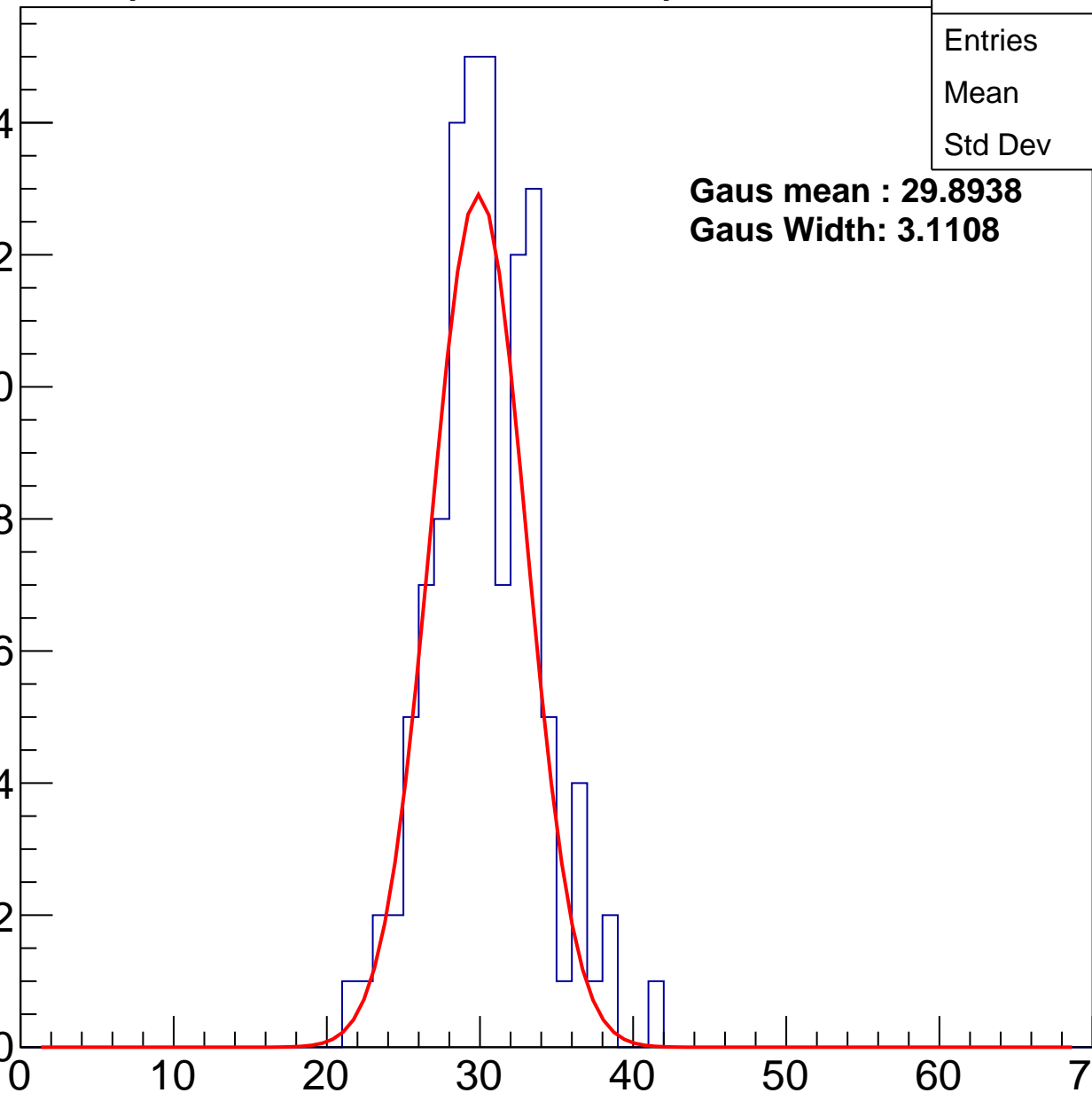
Gaus mean : 29.8938

Gaus Width: 3.1108

Entry

14
12
10
8
6
4
2
0

ampl



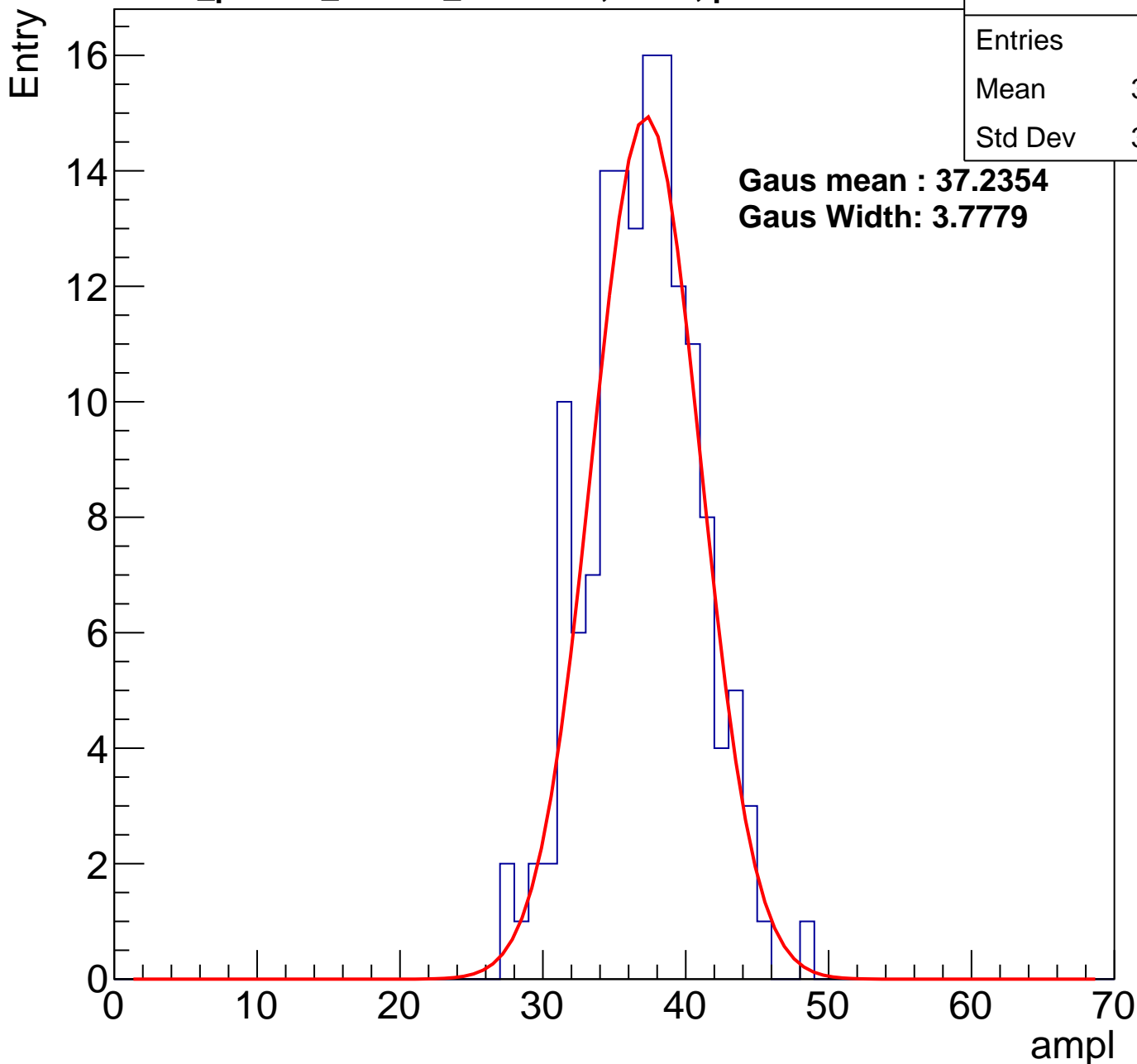
B0L101S, U19-ch107, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	36.56
Std Dev	3.854

Gaus mean : 37.2354

Gaus Width: 3.7779



B0L101S, U19-ch107, adc2

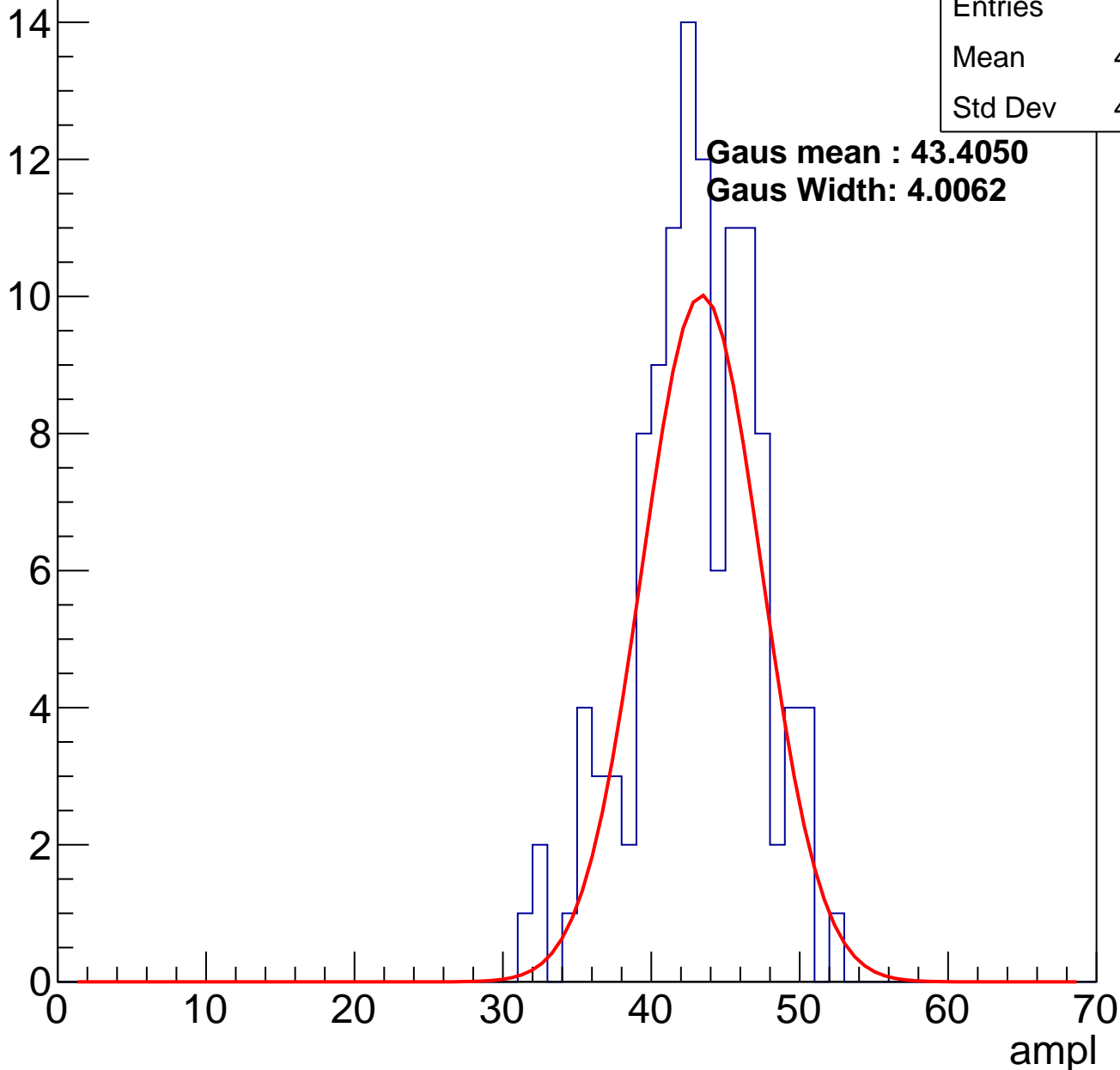
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	42.53
Std Dev	4.152

Gaus mean : 43.4050

Gaus Width: 4.0062

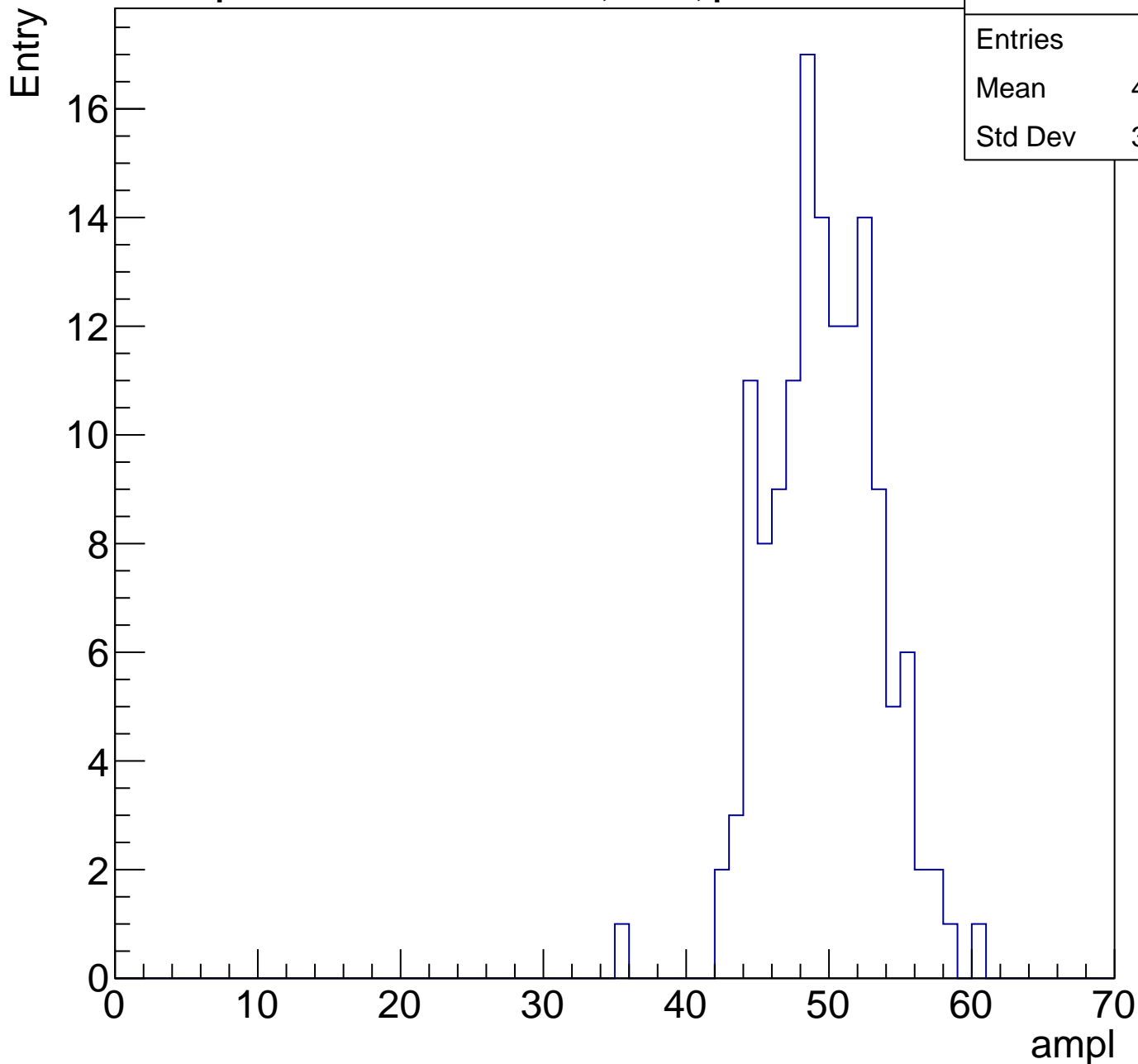
Entry



B0L101S, U19-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

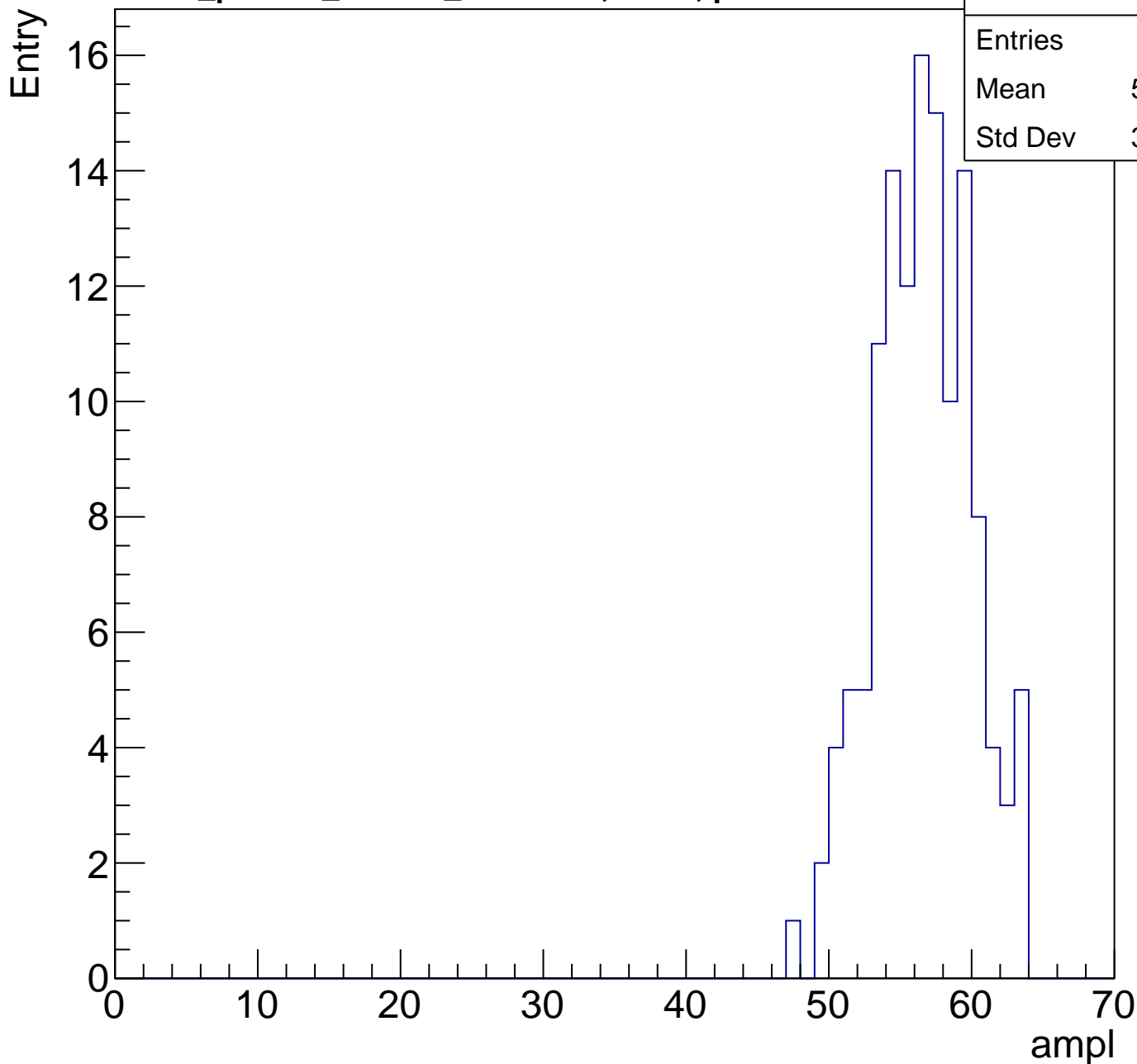
Entries	140
Mean	49.19
Std Dev	3.818



B0L101S, U19-ch107, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	56.13
Std Dev	3.358



B0L101S, U19-ch107, adc5

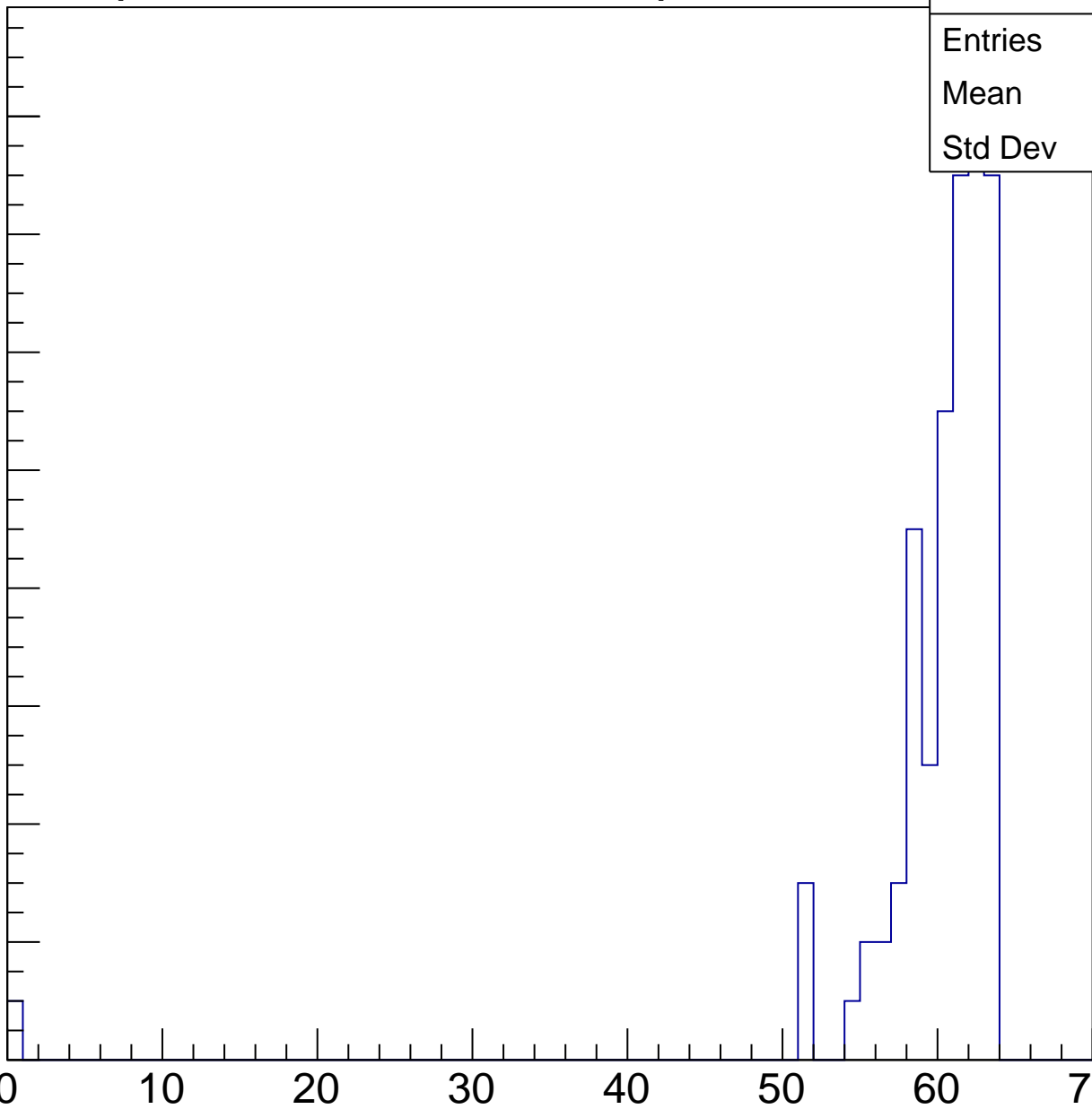
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.42
Std Dev	7.087

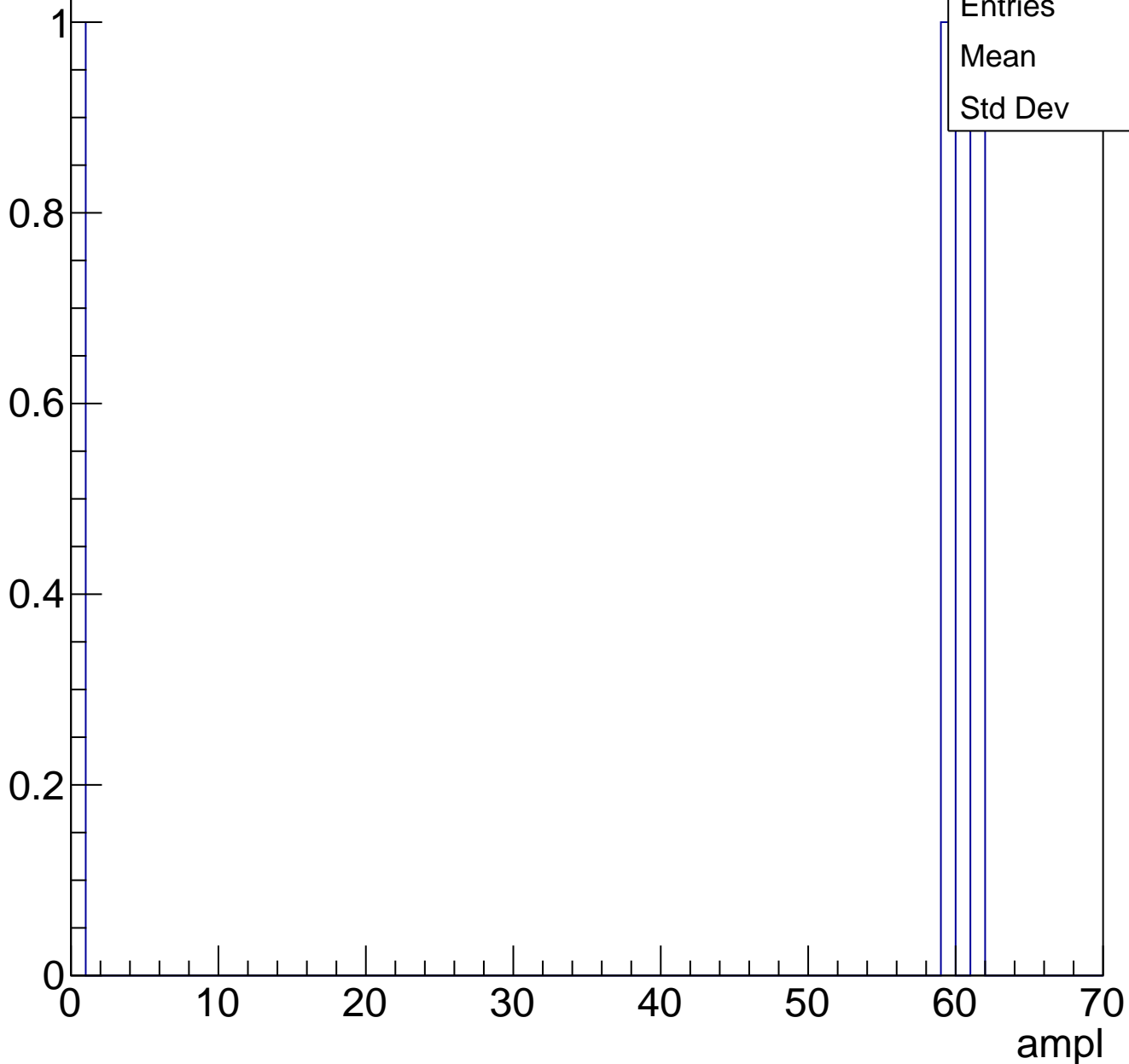
ampl



B0L101S, U19-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

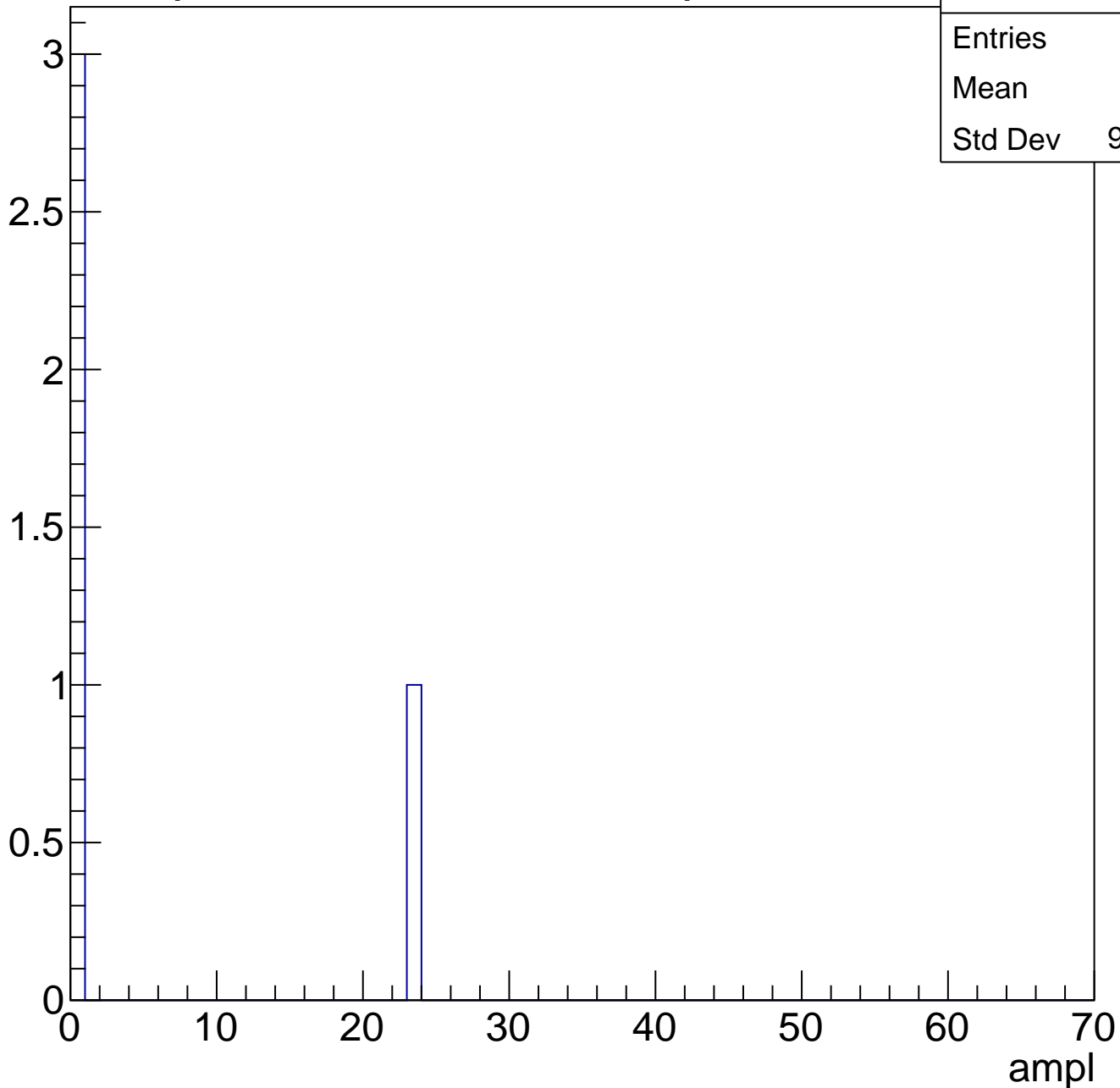
Entry



B0L101S, U19-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch108, adc0

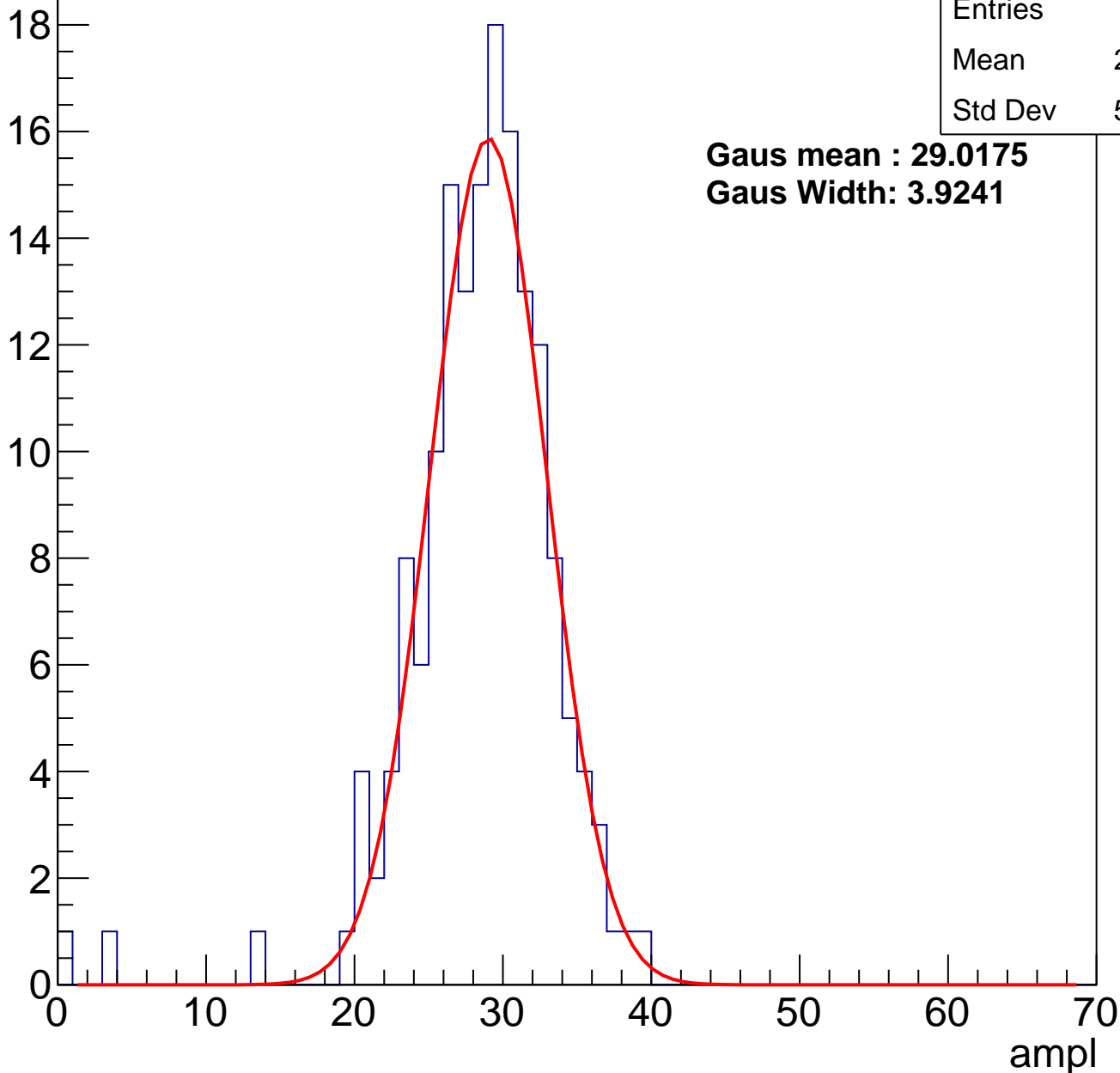
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	28.03
Std Dev	5.035

Gaus mean : 29.0175

Gaus Width: 3.9241

Entry



B0L101S, U19-ch108, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	35.74
Std Dev	4.889

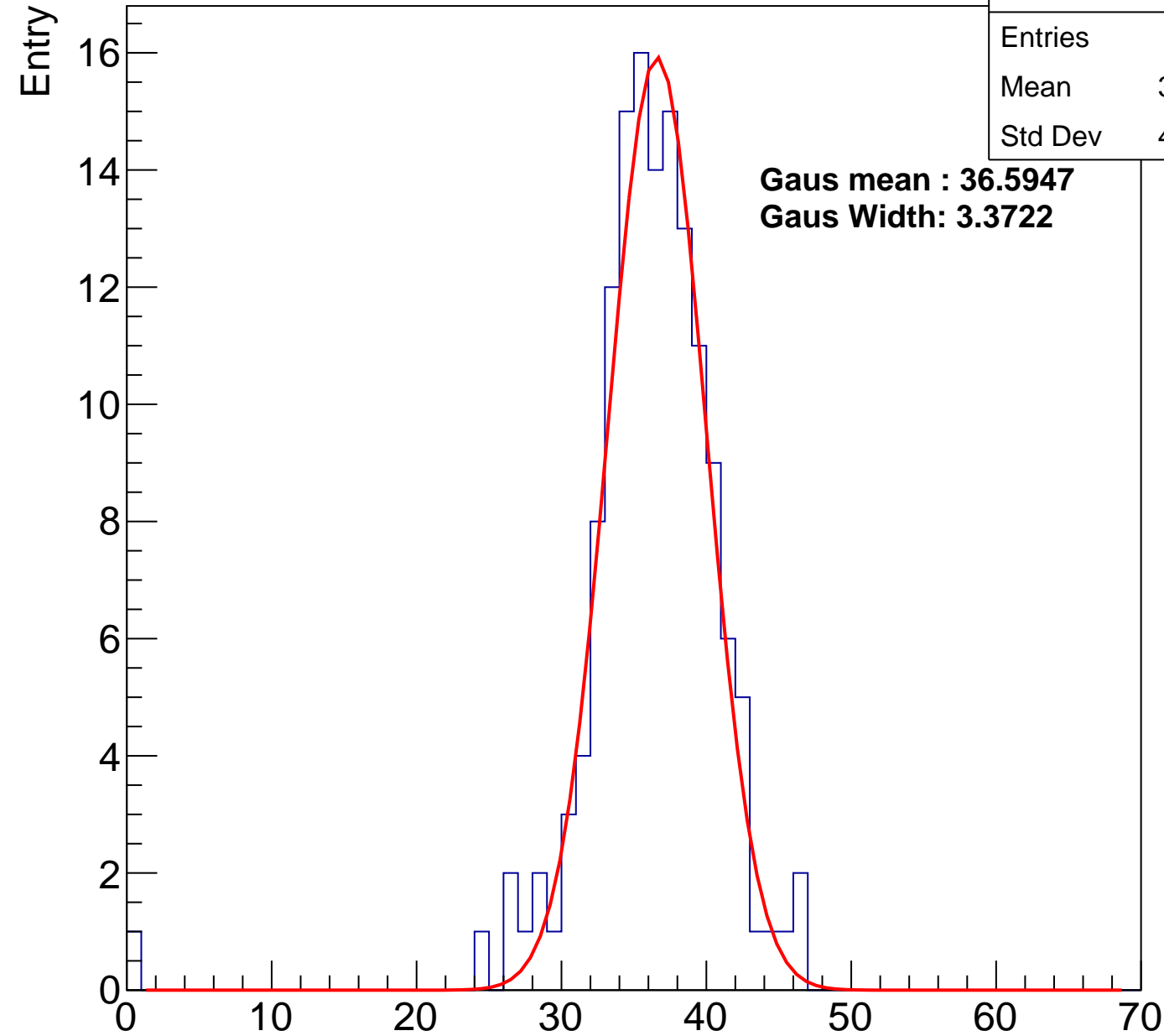
Gaus mean : 36.5947

Gaus Width: 3.3722

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch108, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

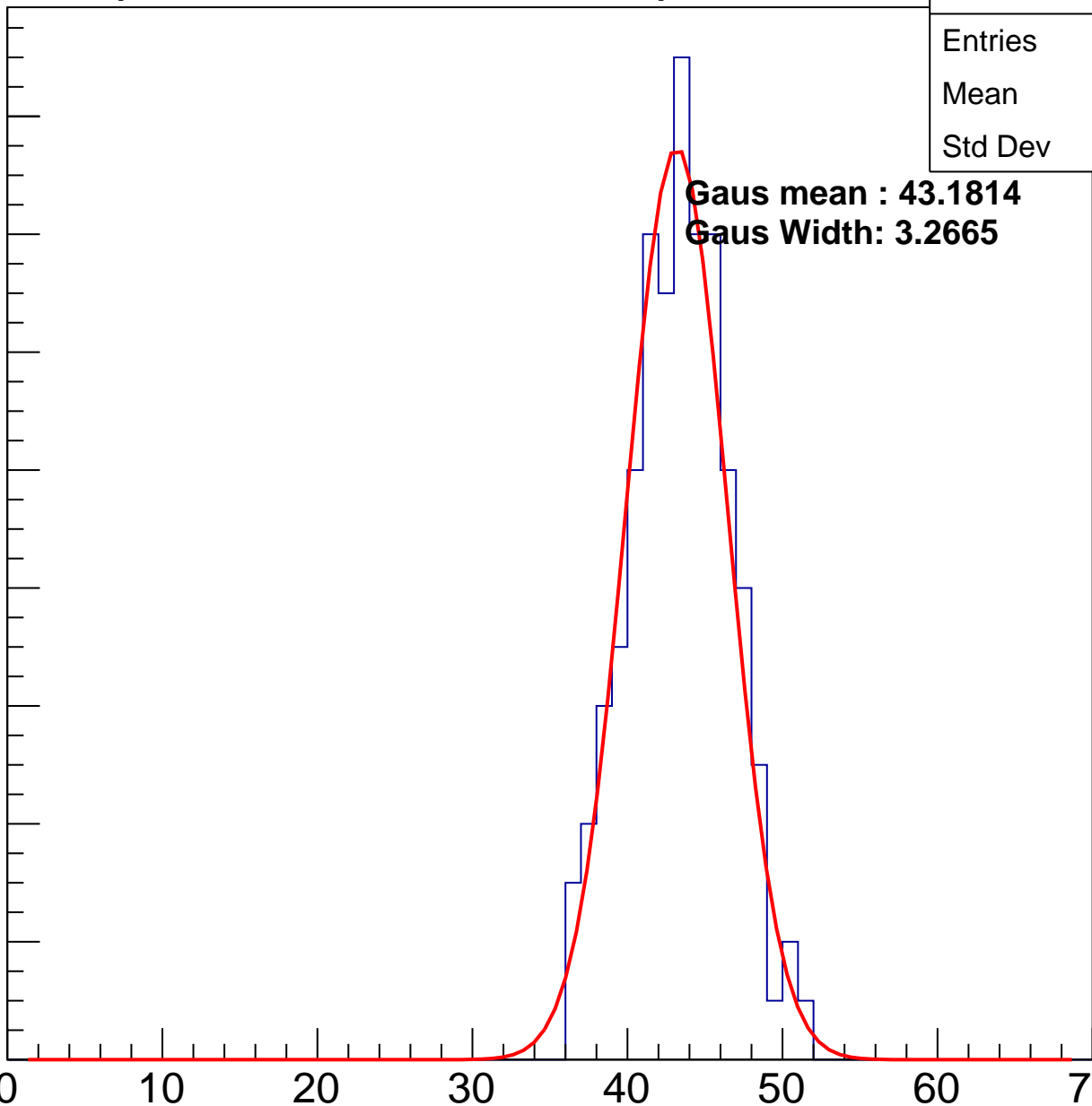
16
14
12
10
8
6
4
2
0

Entries	129
Mean	42.87
Std Dev	3.2

Gaus mean : 43.1814

Gaus Width: 3.2665

ampl



B0L101S, U19-ch108, adc3

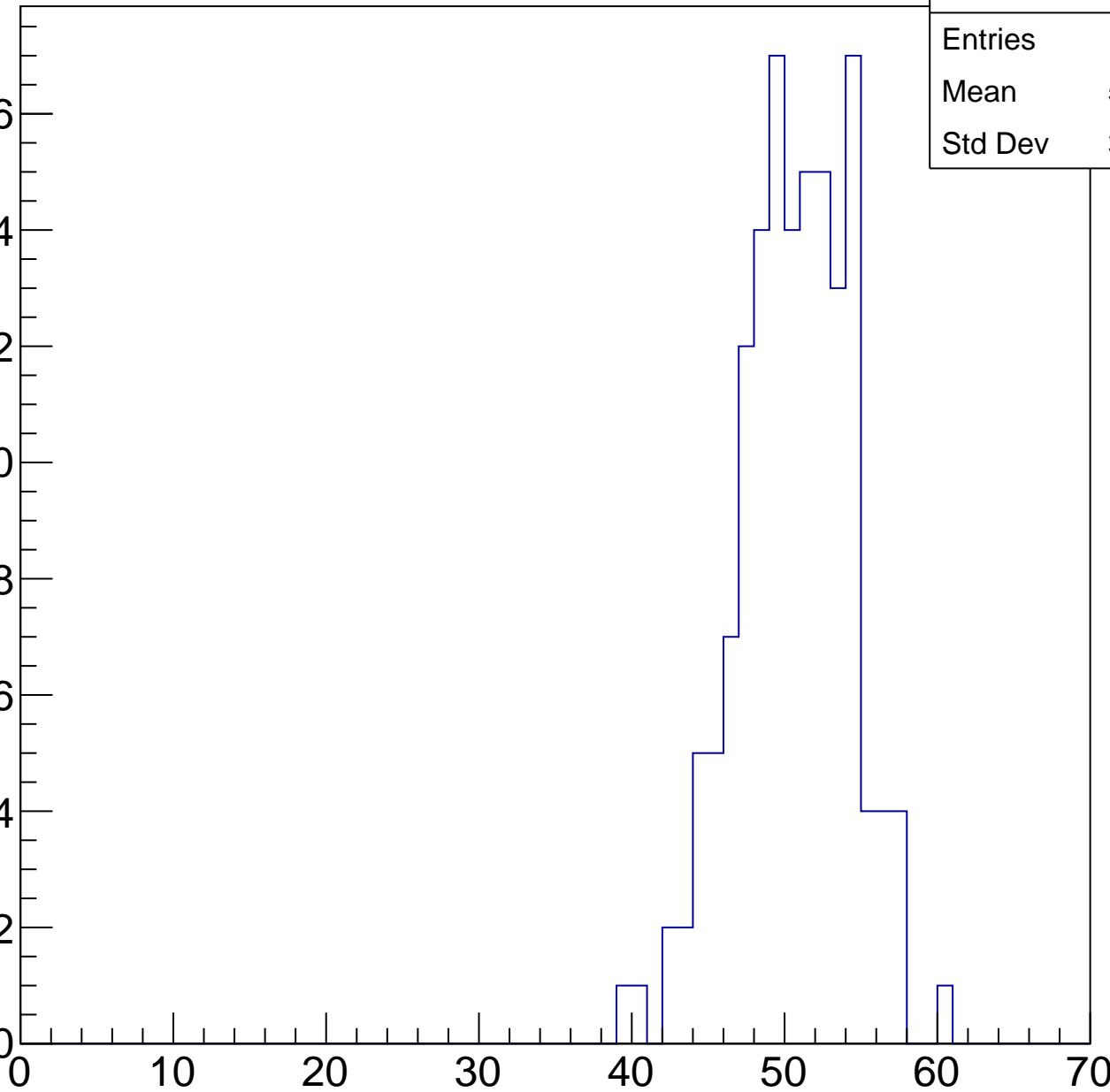
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	153
Mean	50.12
Std Dev	3.657

ampl



B0L101S, U19-ch108, adc4

calib_packv5_042523_0143.root, FC#1, port C1

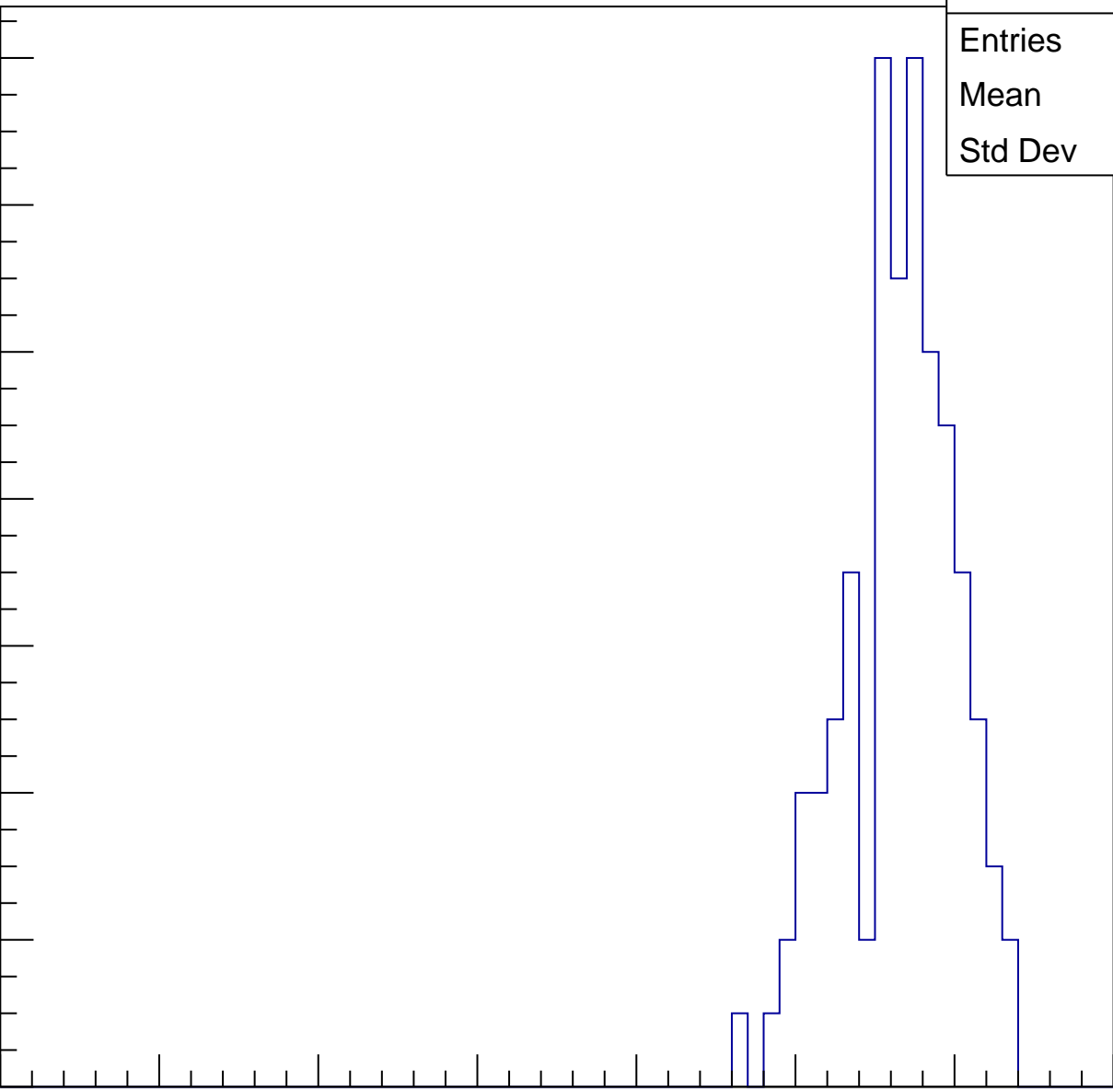
Entries	101
Mean	56.11
Std Dev	3.515

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch108, adc5

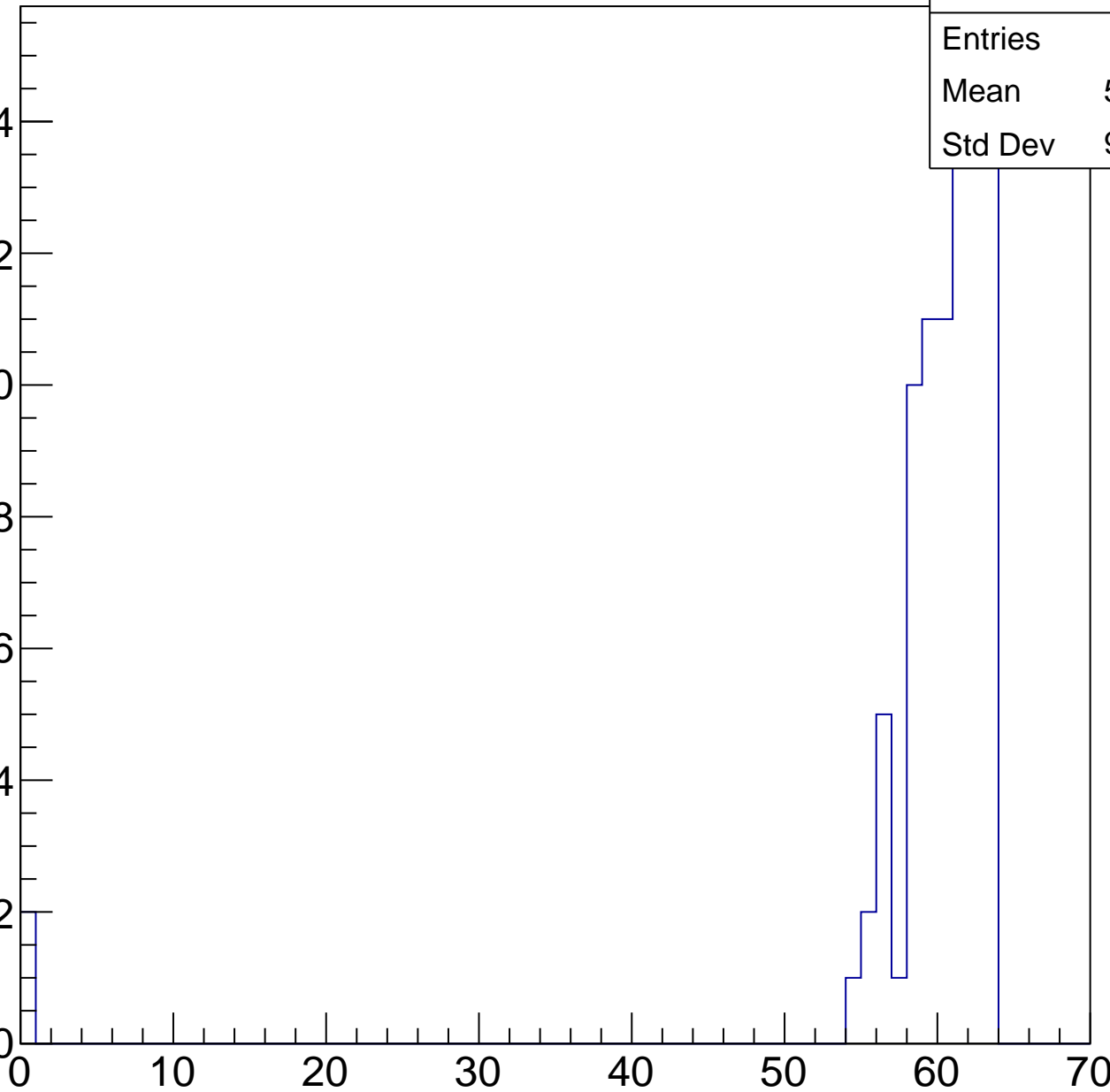
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	58.83
Std Dev	9.292

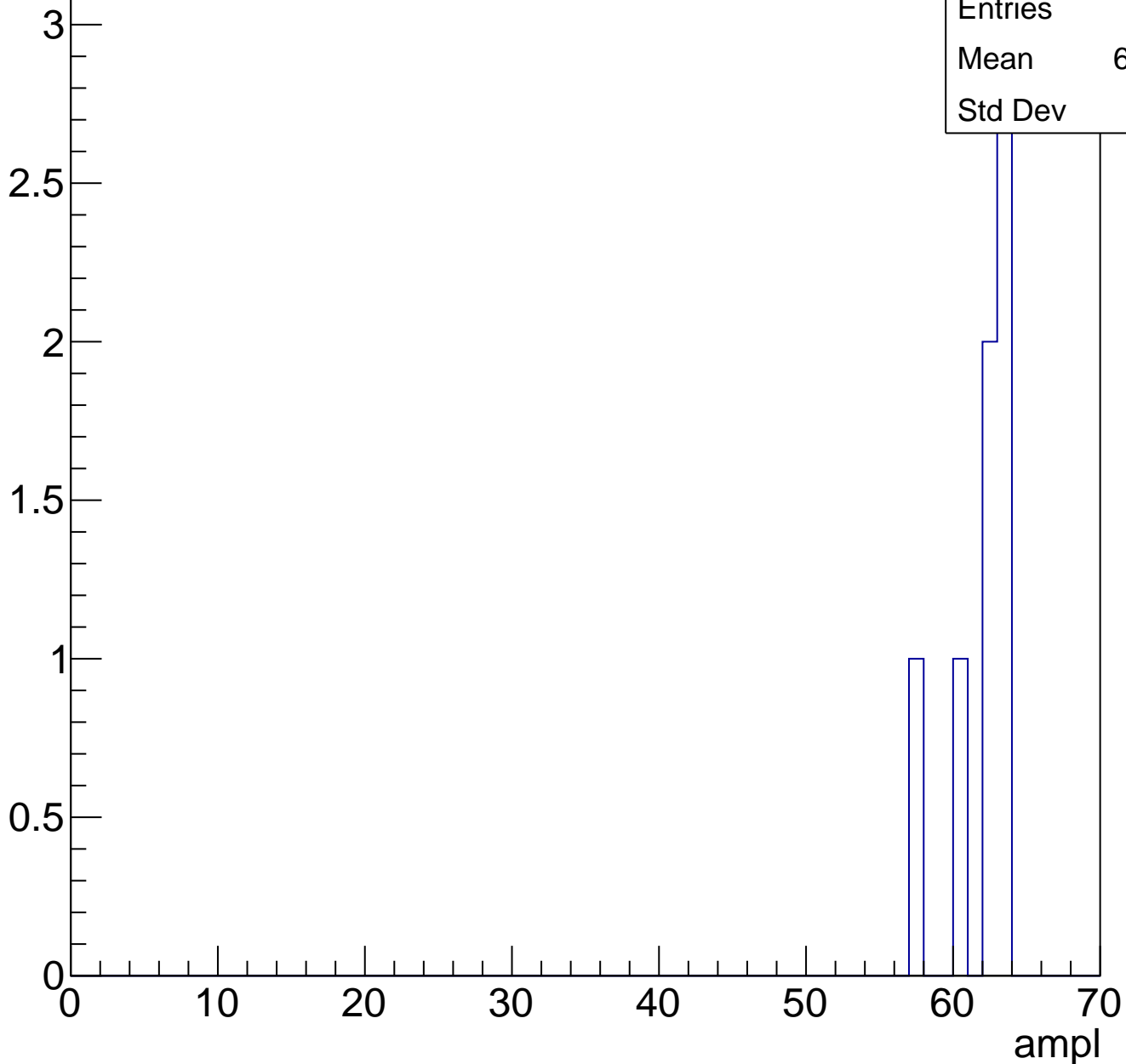
ampl



B0L101S, U19-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch109, adc0

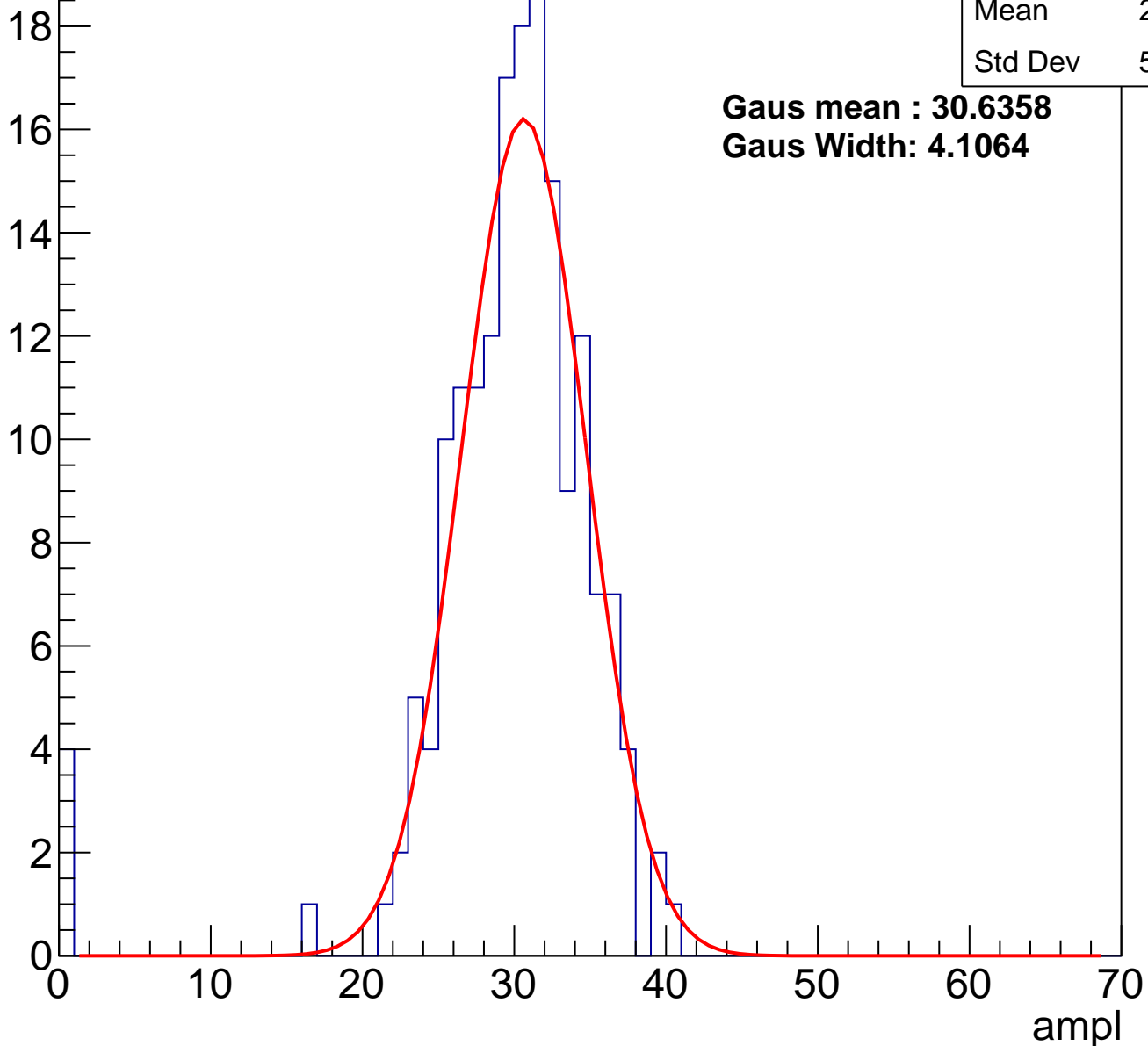
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	29.25
Std Dev	5.967

Gaus mean : 30.6358

Gaus Width: 4.1064

Entry



B0L101S, U19-ch109, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	37.17
Std Dev	3.391

Gaus mean : 37.9303

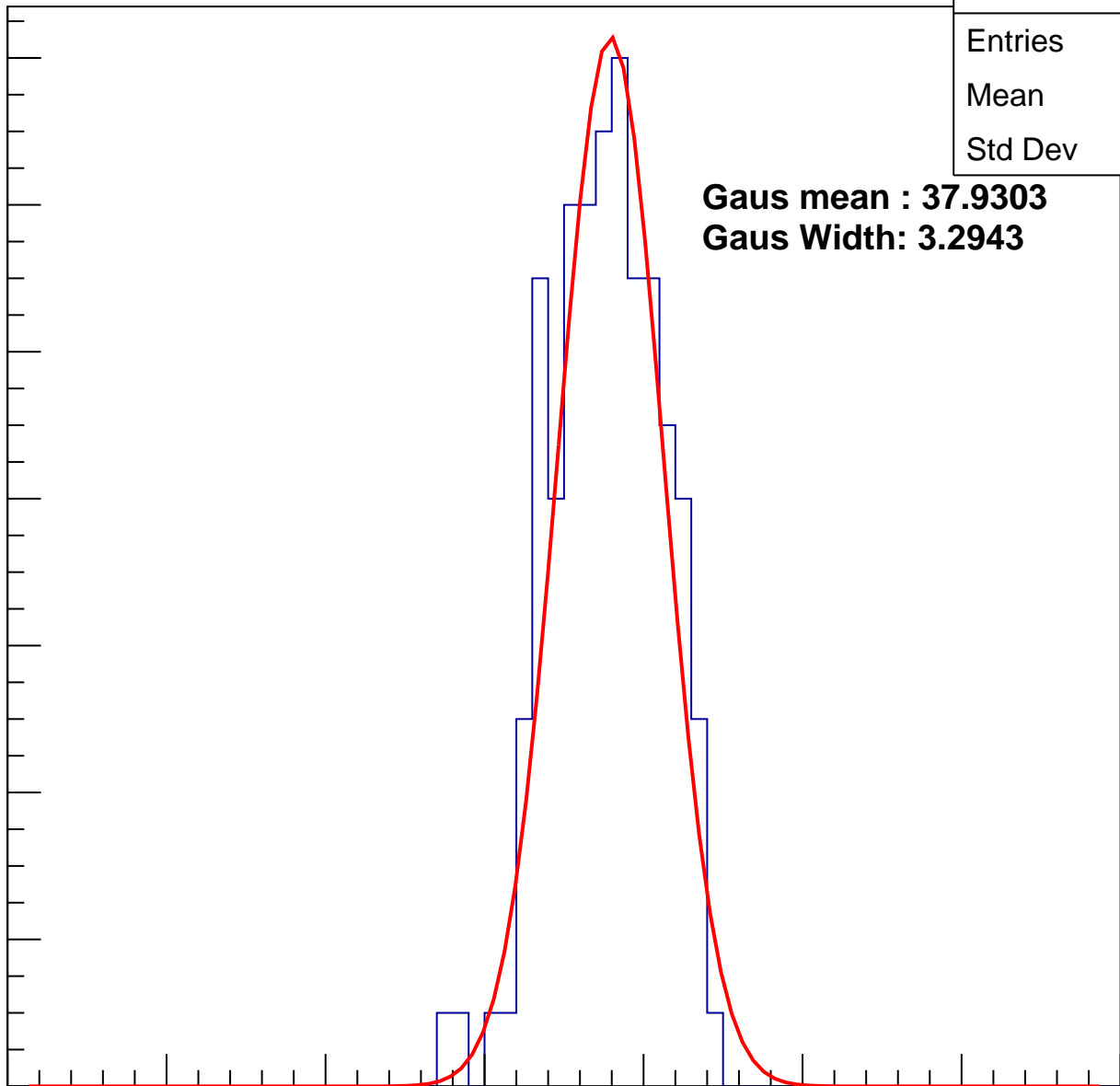
Gaus Width: 3.2943

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch109, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

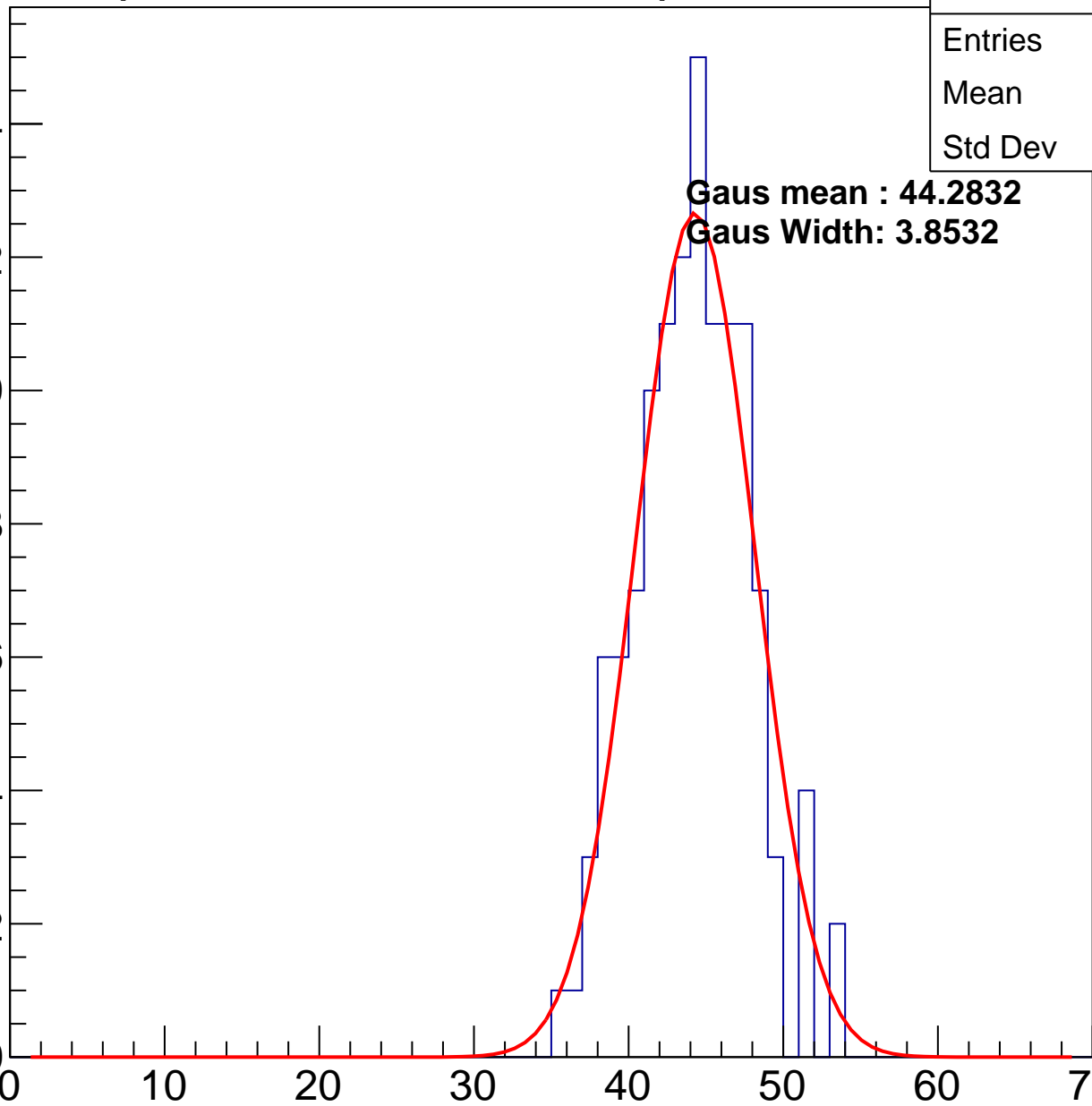
60

ampl

Entries	121
Mean	43.66
Std Dev	3.638

Gaus mean : 44.2832

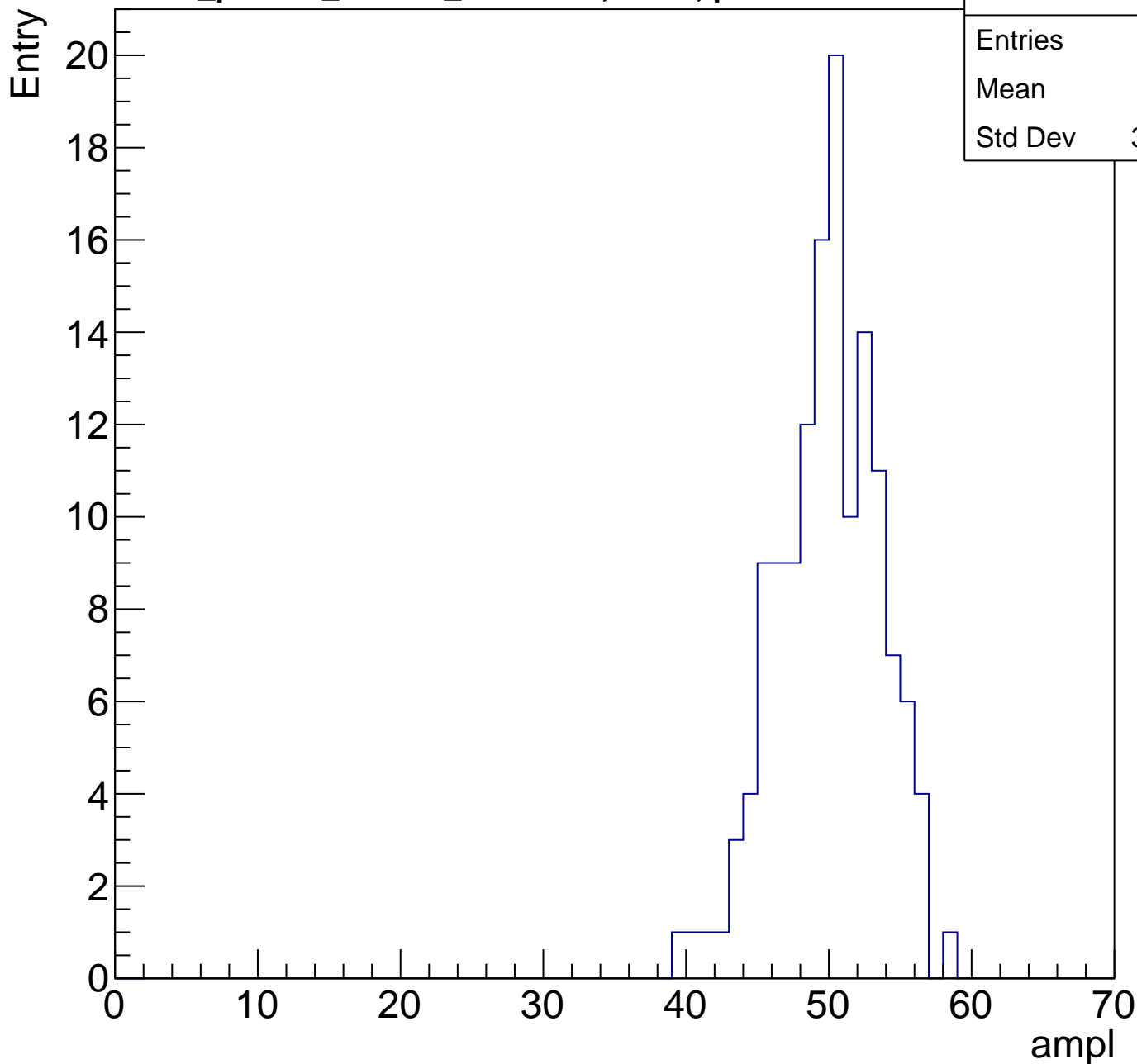
Gaus Width: 3.8532



B0L101S, U19-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

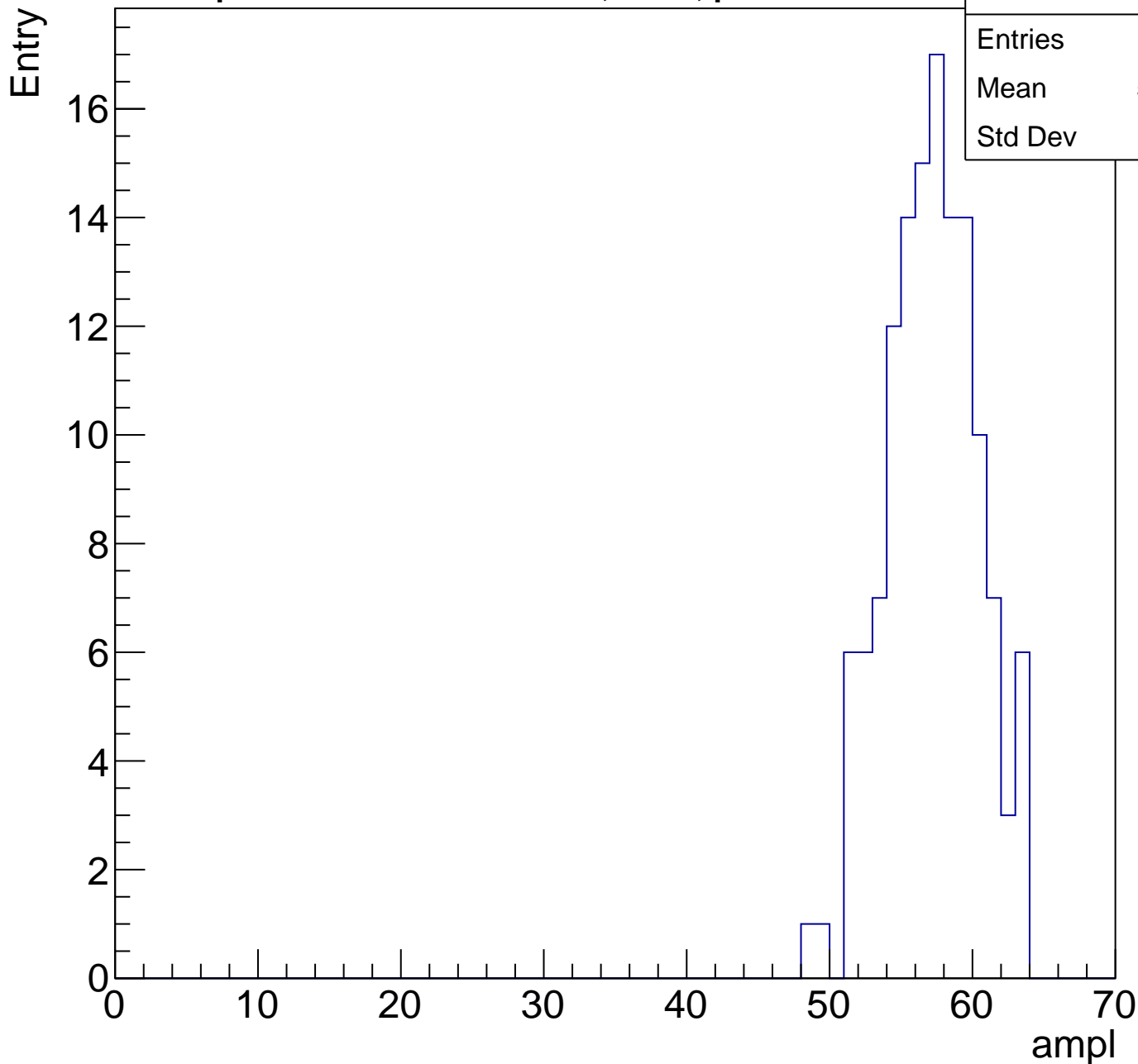
Entries	139
Mean	49.5
Std Dev	3.569



B0L101S, U19-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	56.71
Std Dev	3.19



B0L101S, U19-ch109, adc5

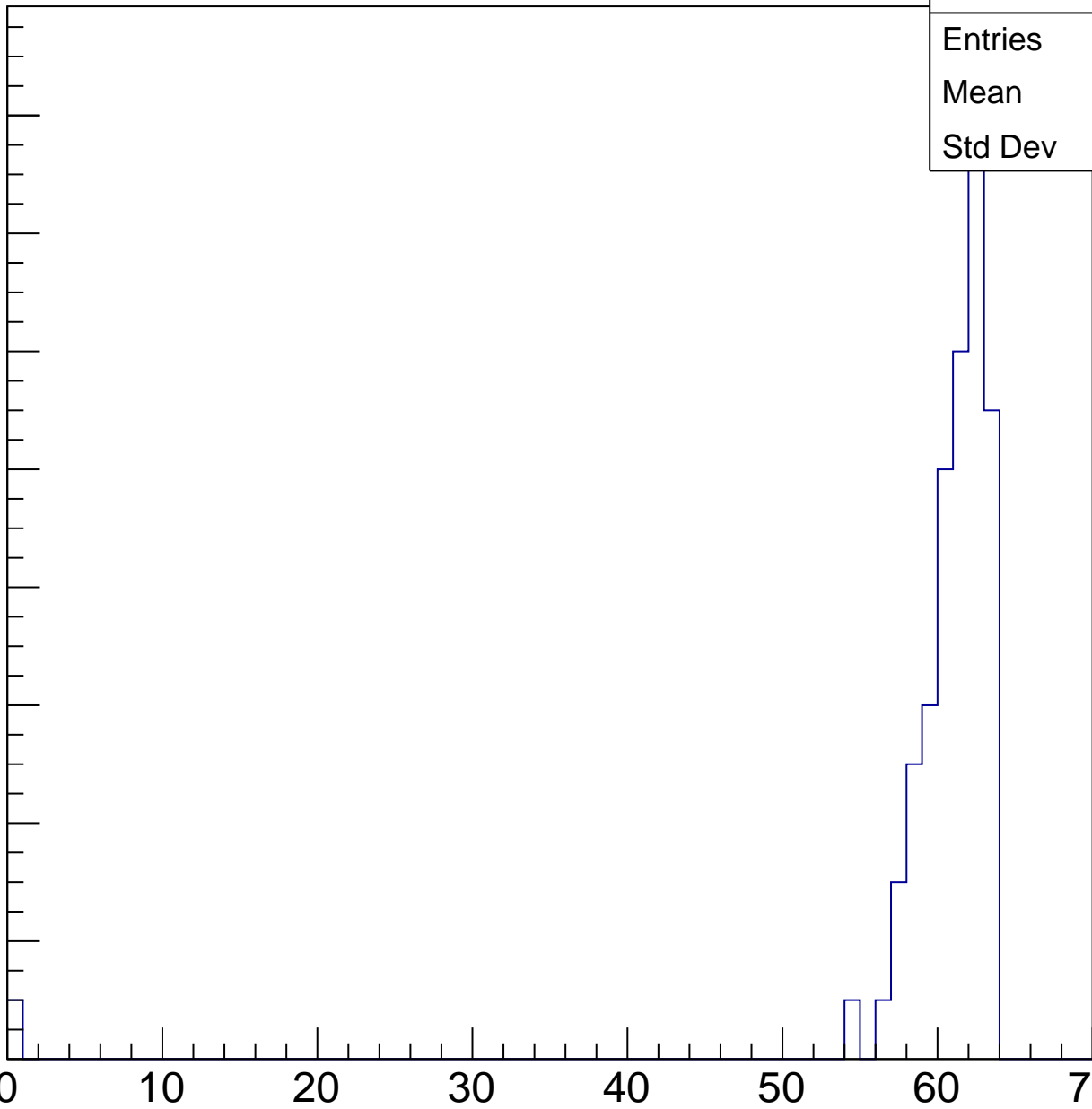
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	67
Mean	59.76
Std Dev	7.61

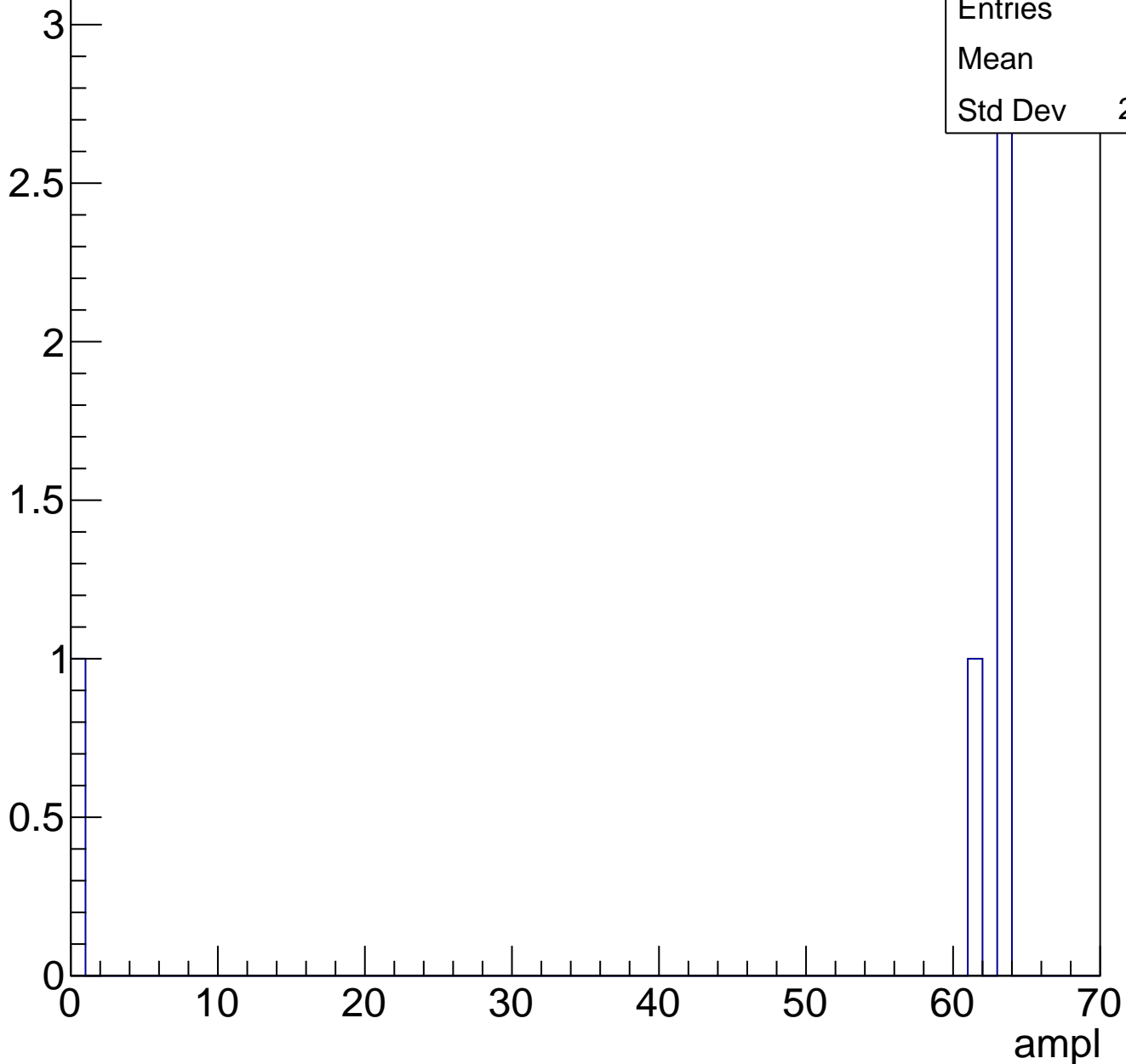
ampl



B0L101S, U19-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

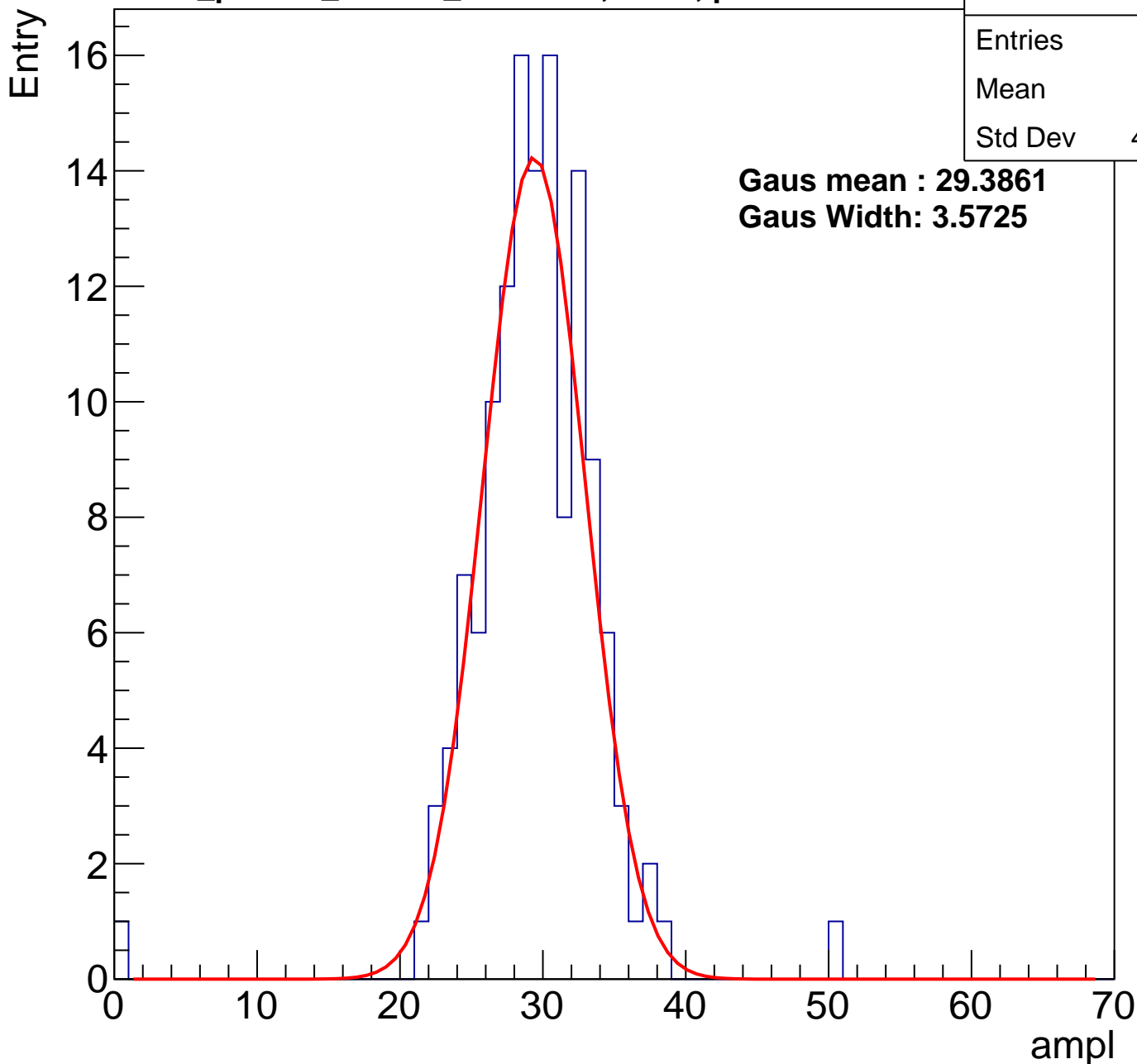
B0L101S, U19-ch110, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	29
Std Dev	4.632

Gaus mean : 29.3861

Gaus Width: 3.5725



B0L101S, U19-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	35.14
Std Dev	4.582

Gaus mean : 35.8539

Gaus Width: 3.5416

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

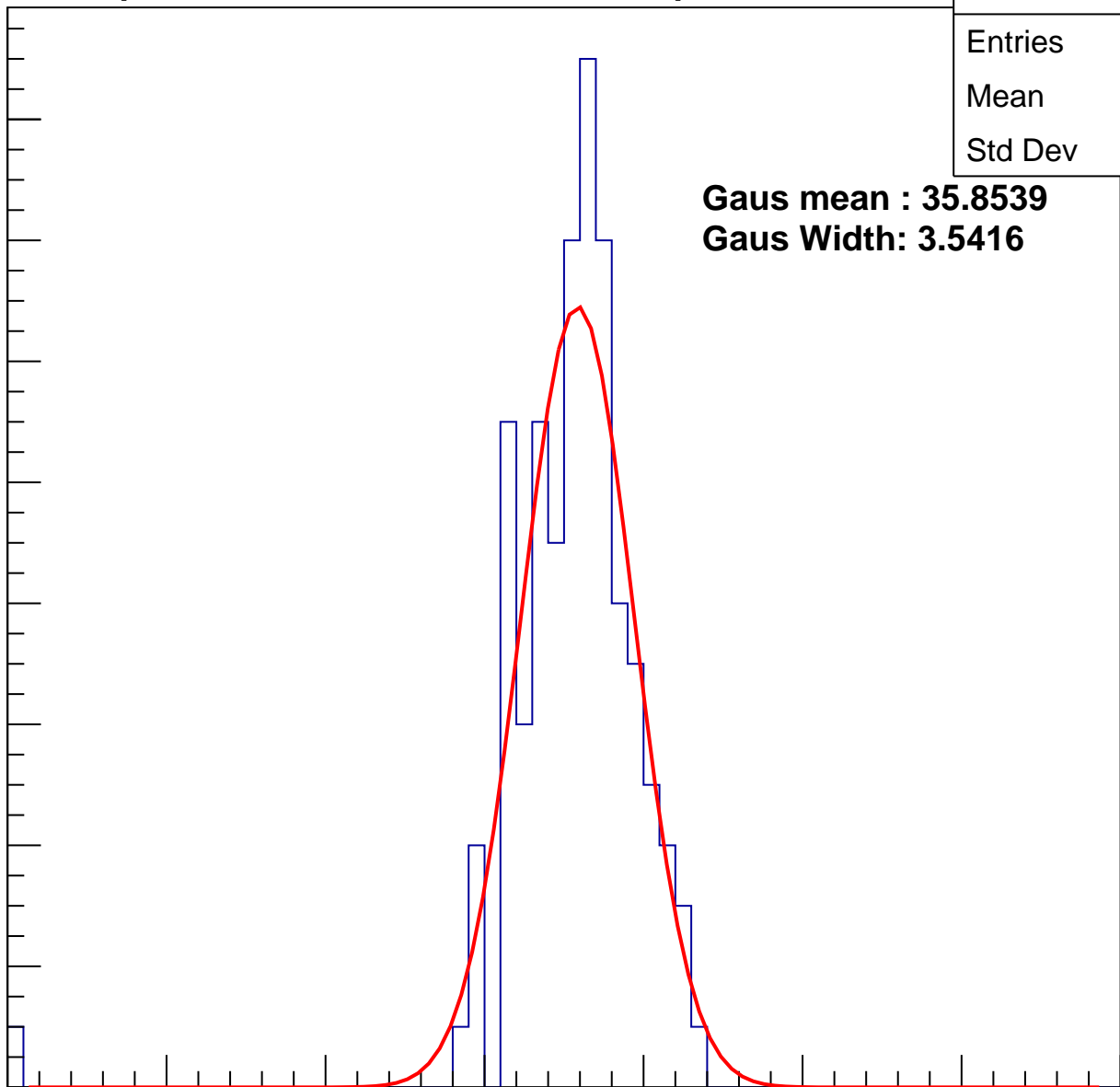
40

50

60

70

ampl



B0L101S, U19-ch110, adc2

calib_packv5_042523_0143.root, FC#1, port C1

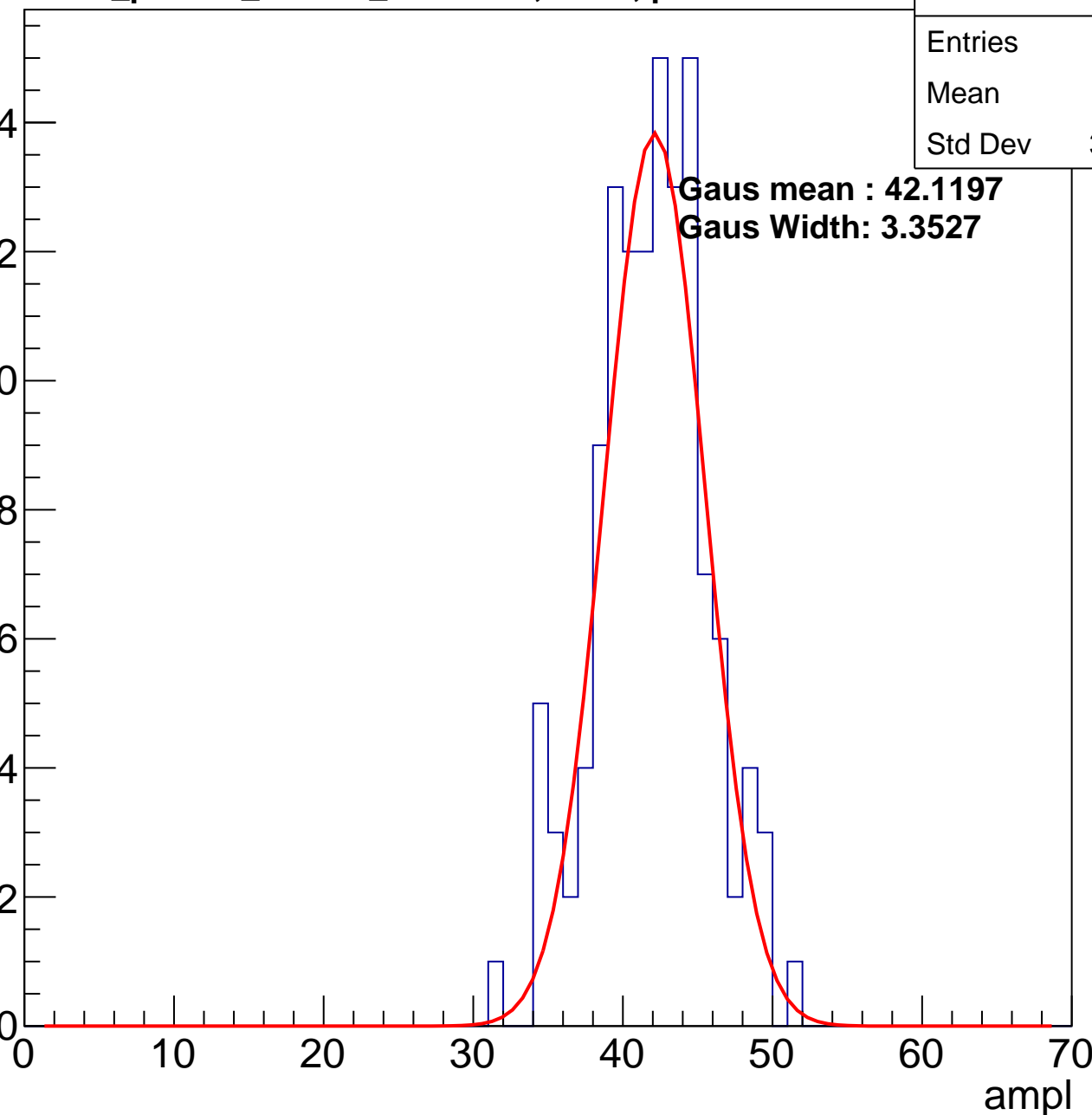
Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	41.5
Std Dev	3.663

Gaus mean : 42.1197

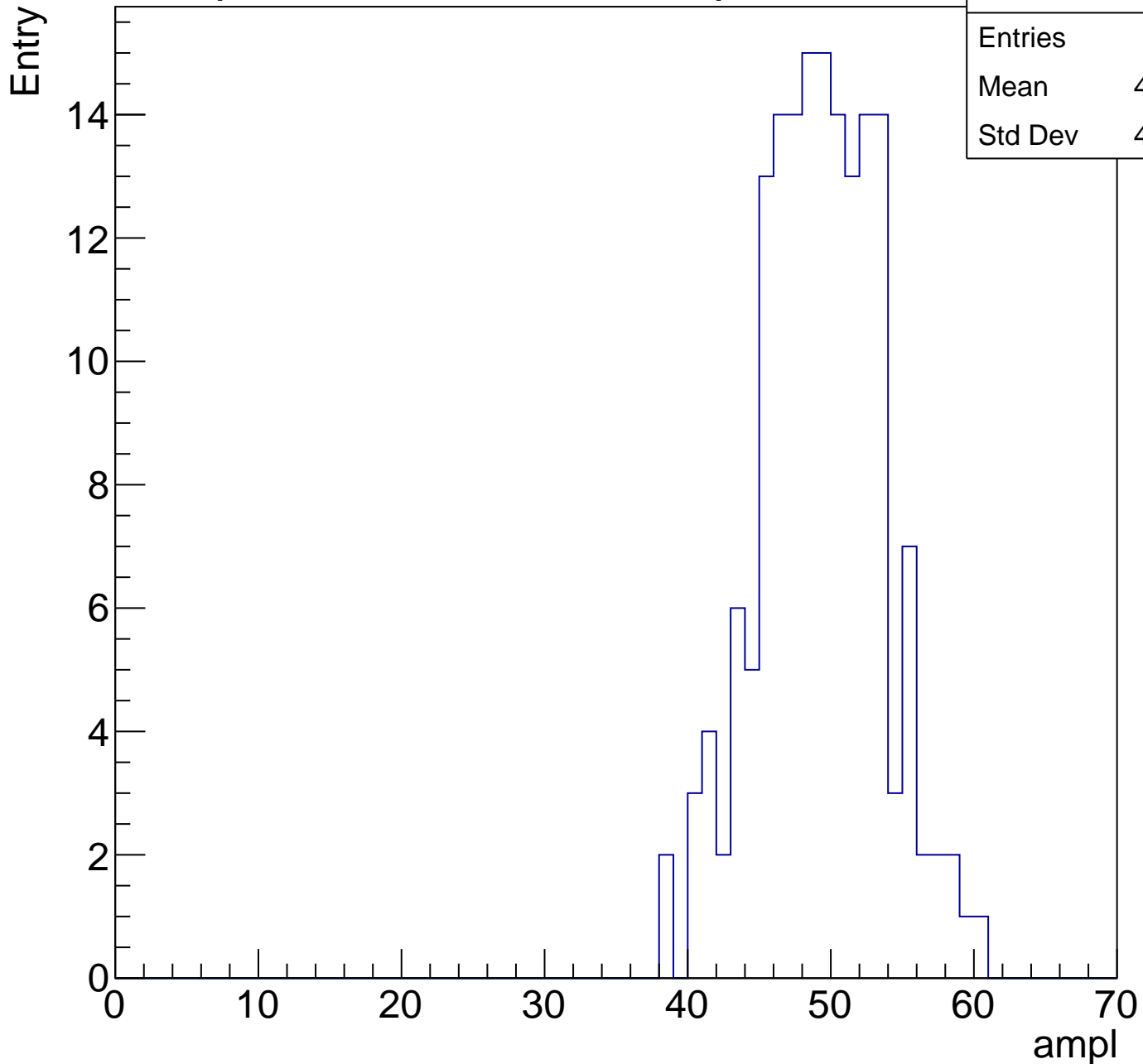
Gaus Width: 3.3527



B0L101S, U19-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	48.83
Std Dev	4.203

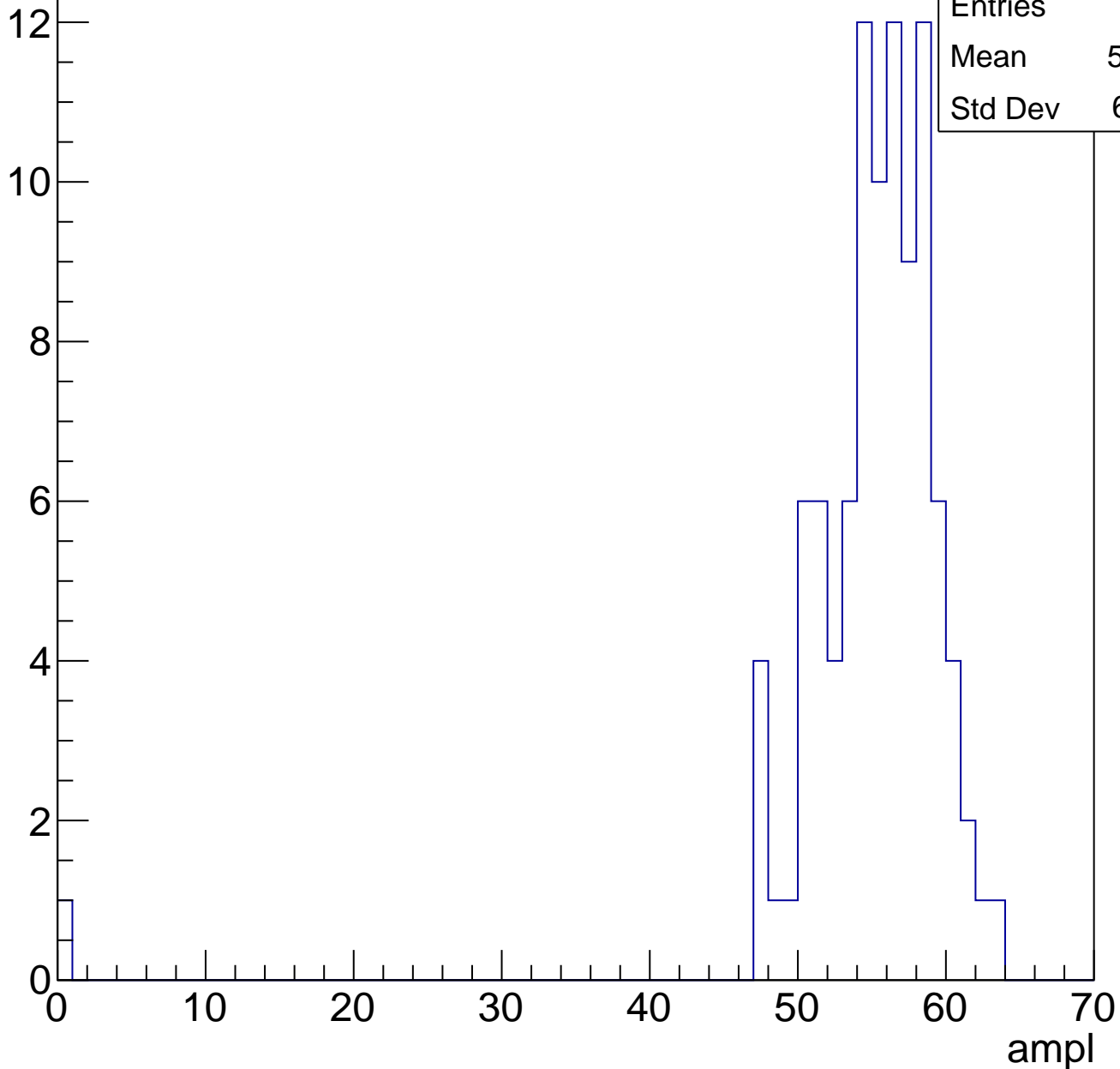


B0L101S, U19-ch110, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	98
Mean	54.46
Std Dev	6.541

Entry



B0L101S, U19-ch110, adc5

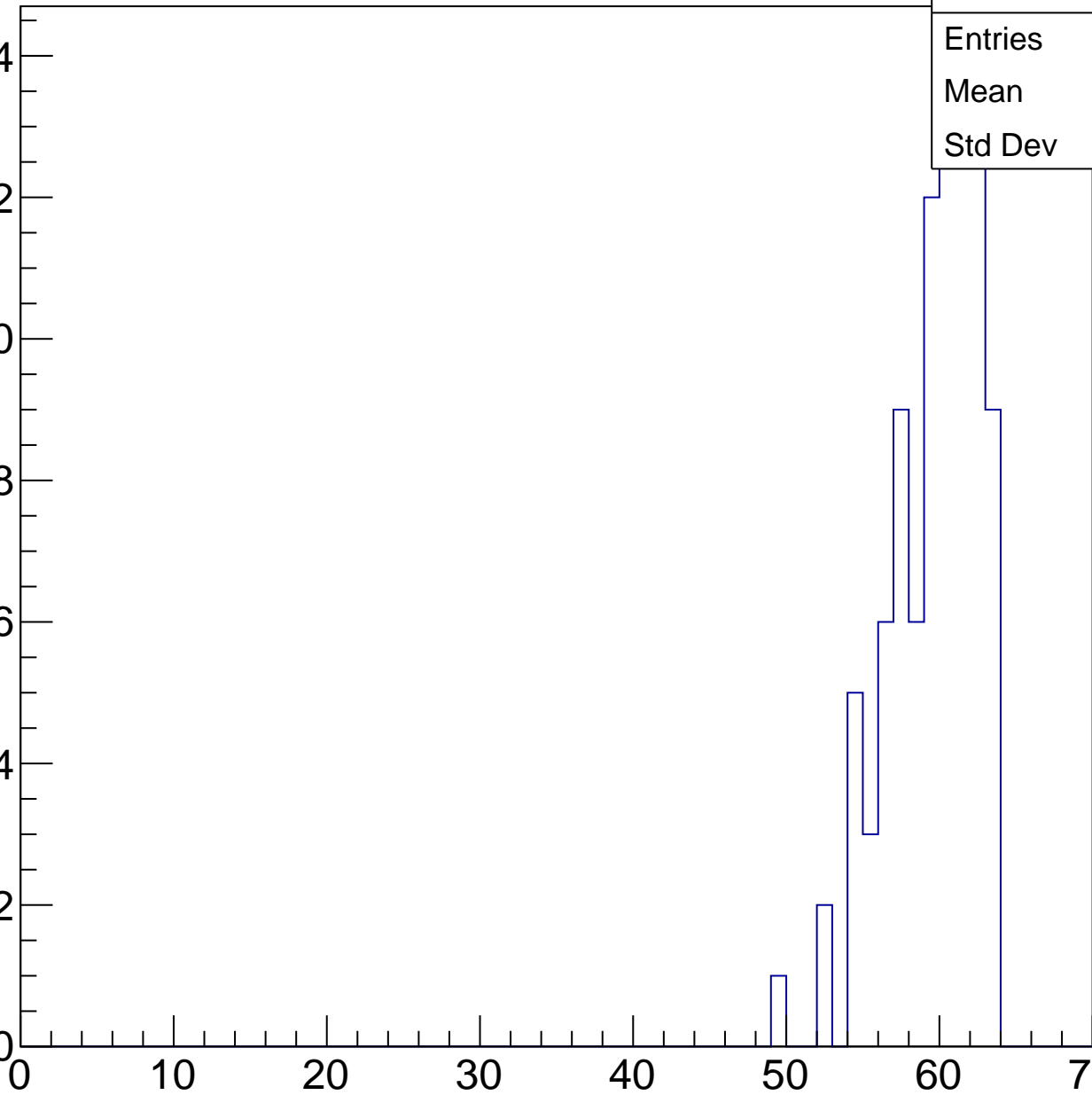
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	59.13
Std Dev	2.919

ampl



B0L101S, U19-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	23
Mean	59.35
Std Dev	12.7

Entry

10

8

6

4

2

0

0

10

20

30

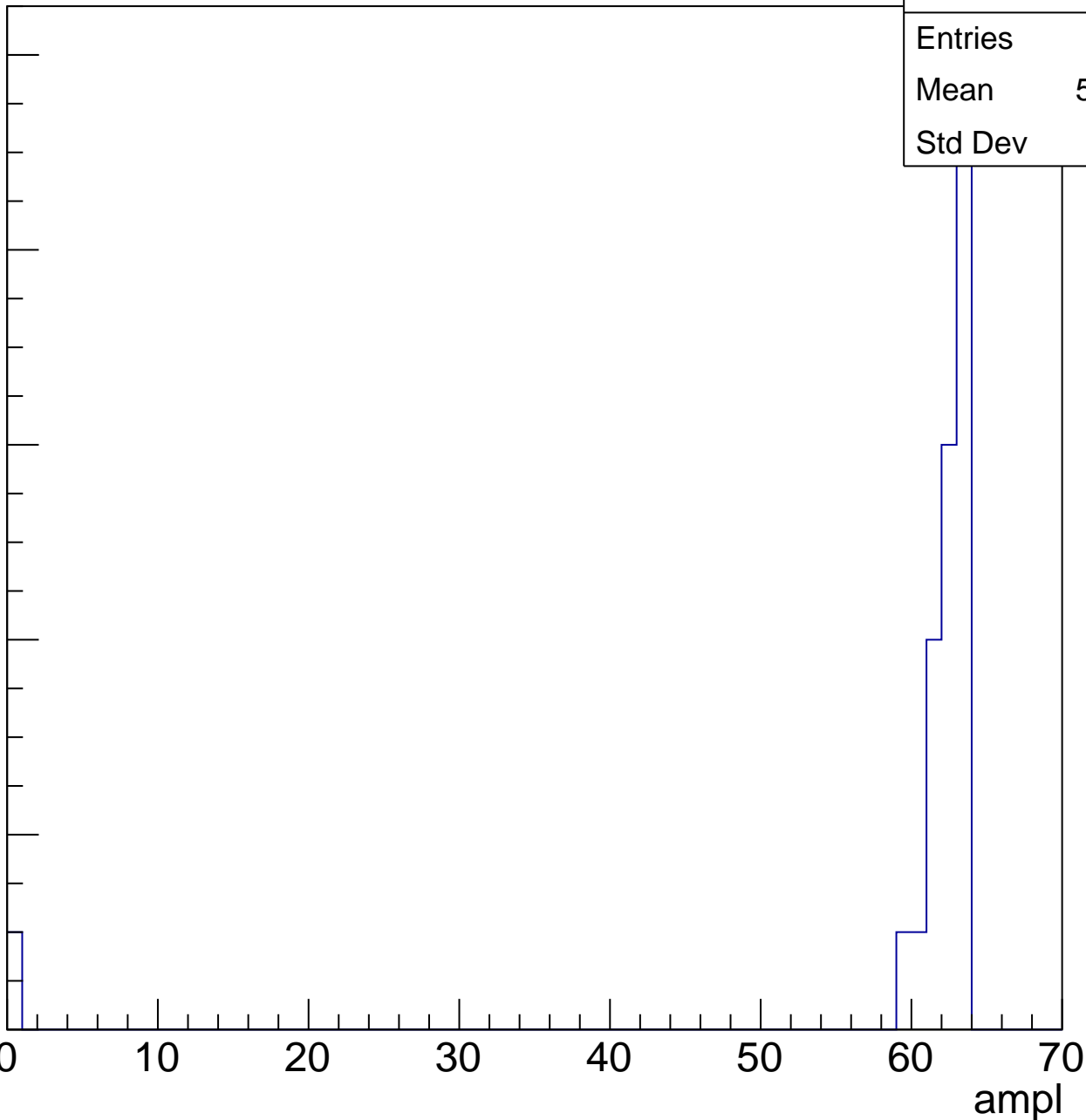
40

50

60

70

ampl



B0L101S, U19-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch111, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	27.69
Std Dev	4.855

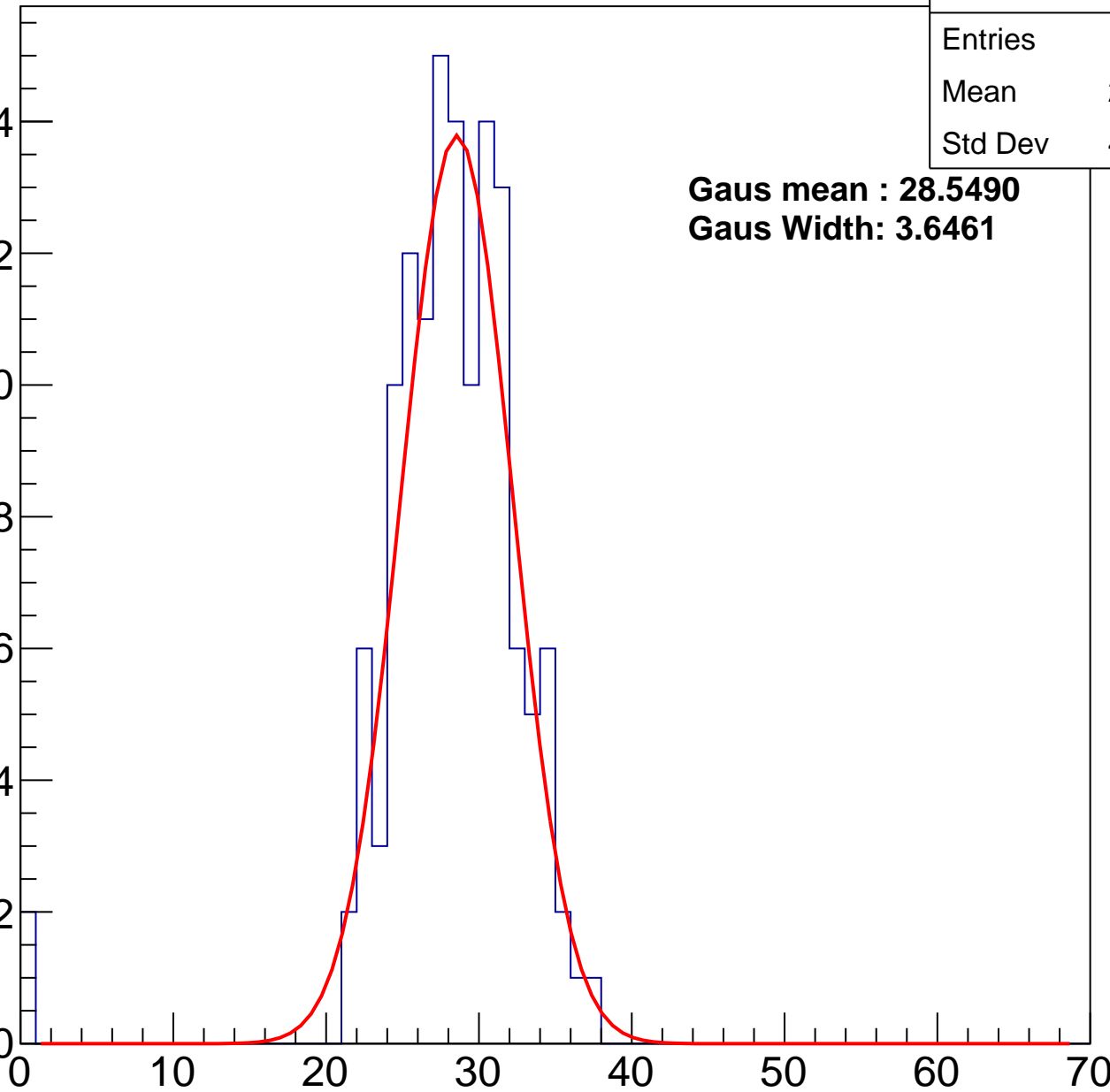
Gaus mean : 28.5490

Gaus Width: 3.6461

Entry

14
12
10
8
6
4
2
0

ampl

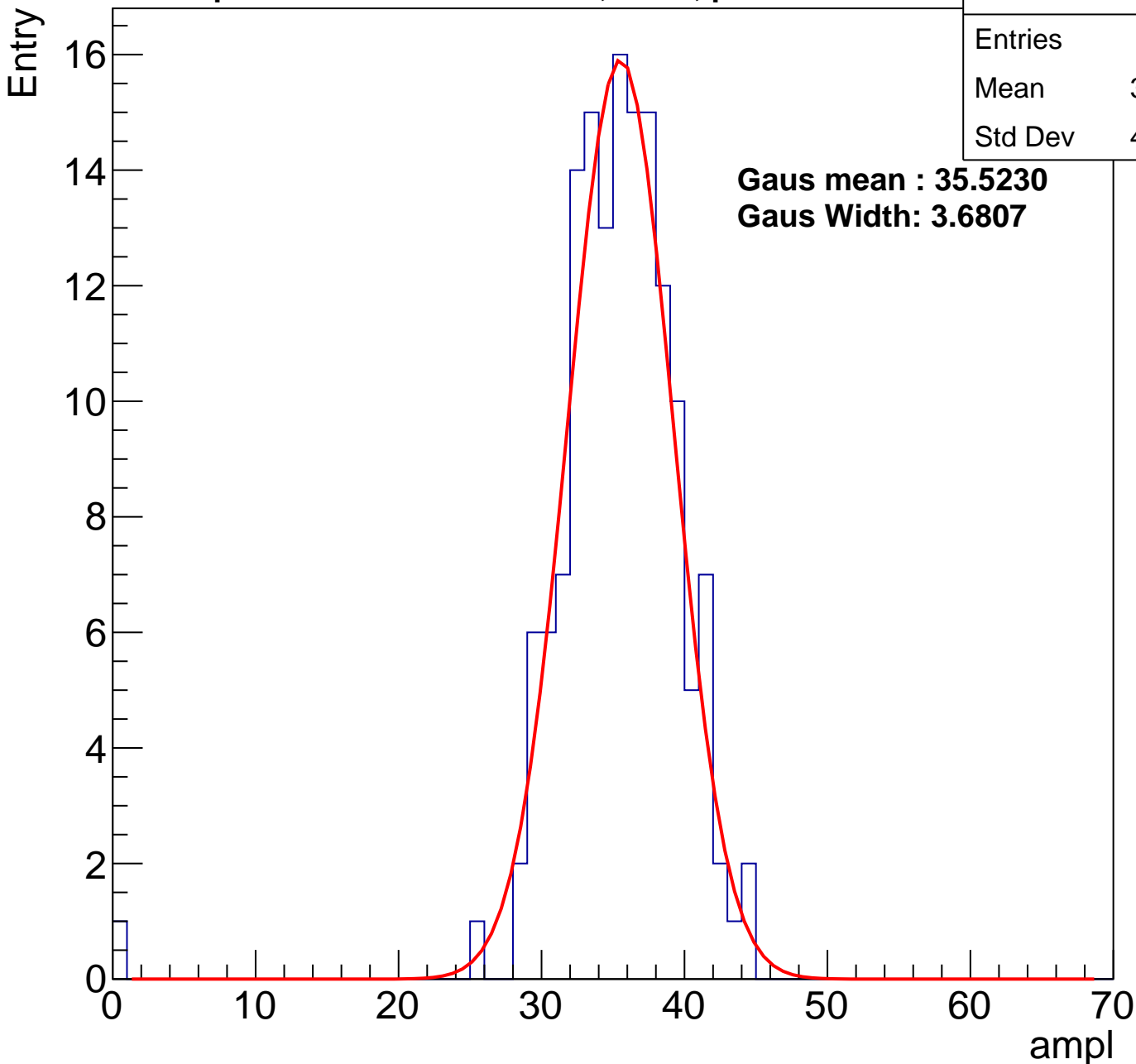


B0L101S, U19-ch111, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	34.93
Std Dev	4.578

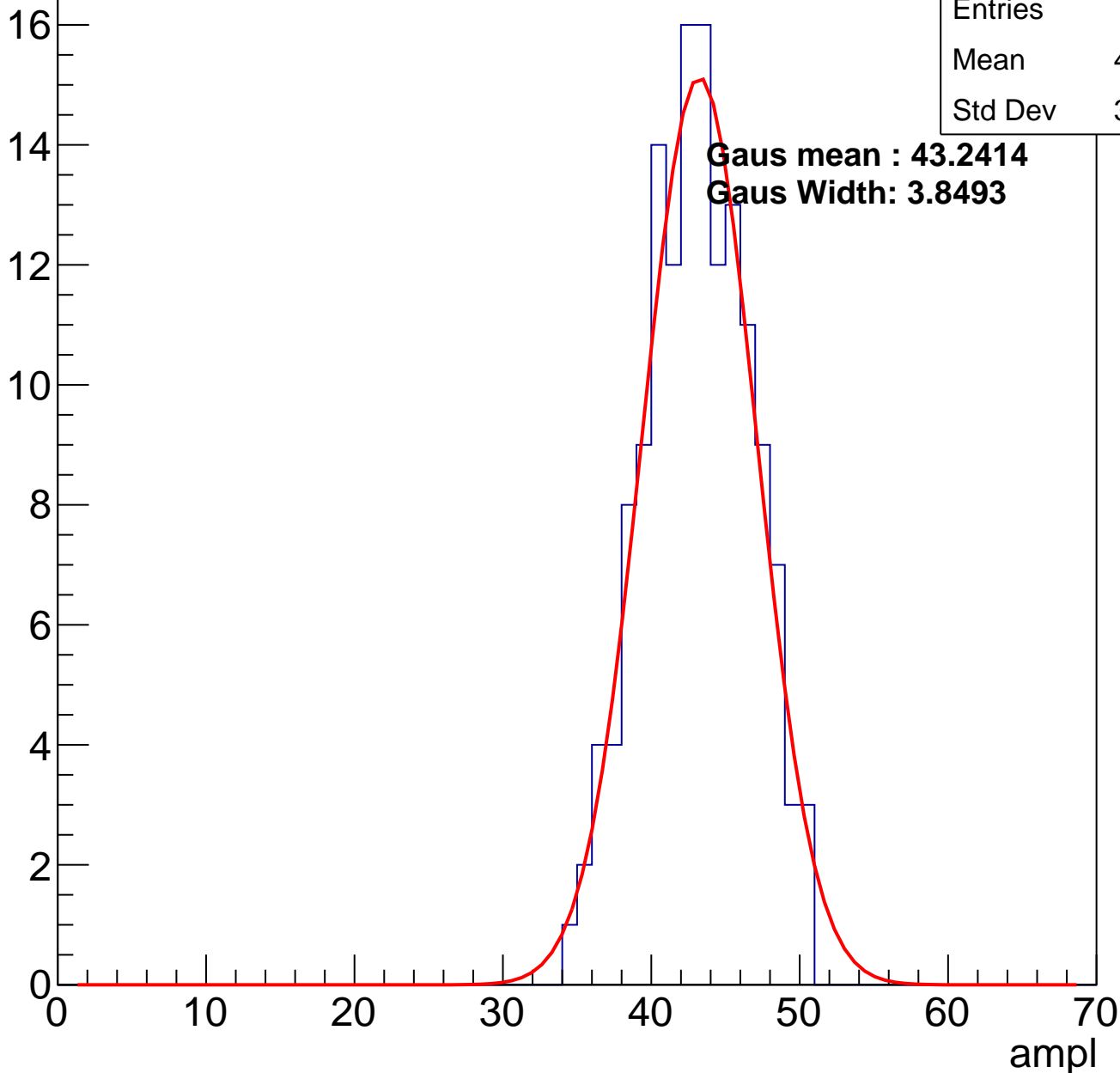
Gaus mean : 35.5230
Gaus Width: 3.6807



B0L101S, U19-ch111, adc2

calib_packv5_042523_0143.root, FC#1, port C1

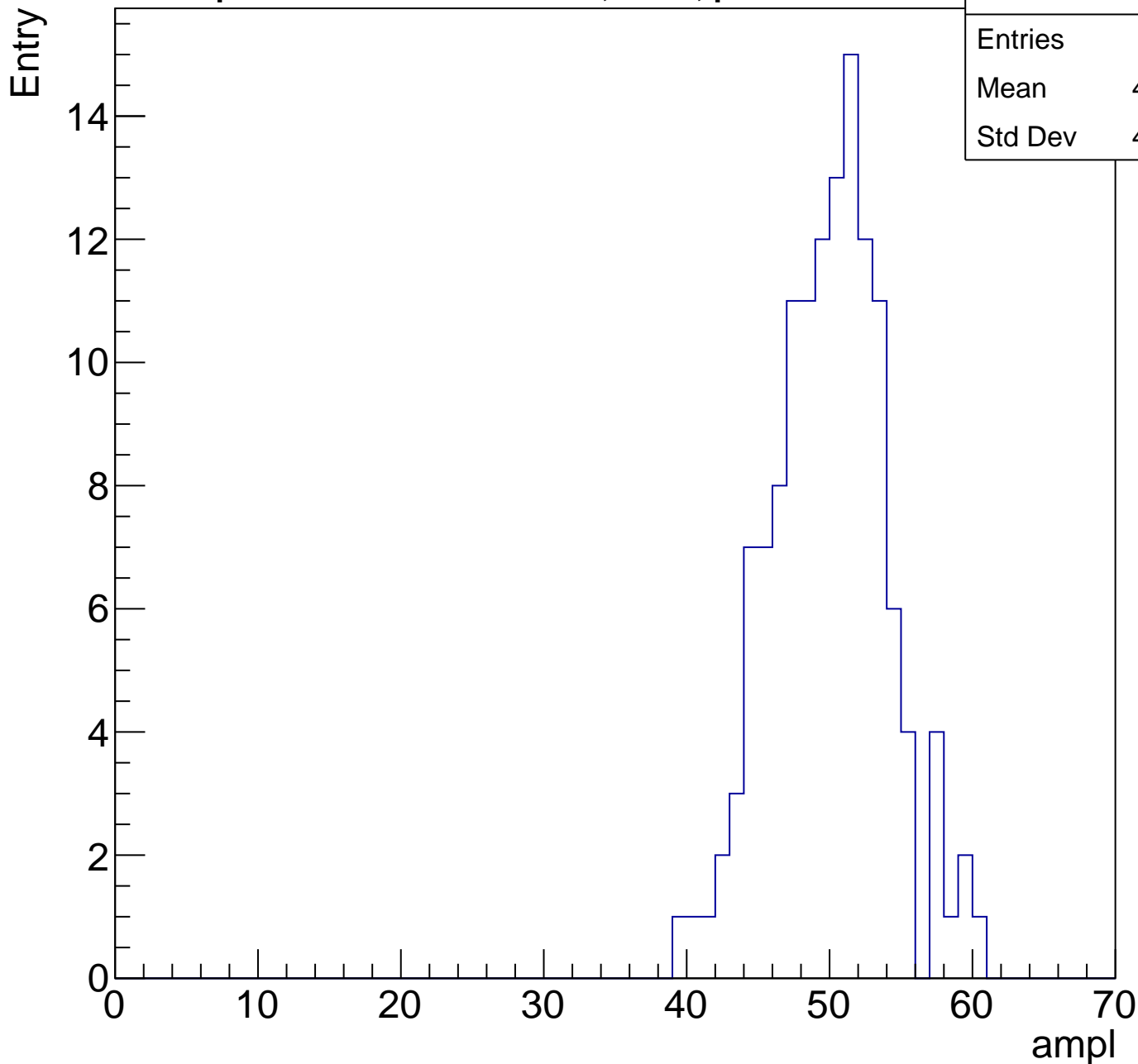
Entry



B0L101S, U19-ch111, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	49.53
Std Dev	4.009



B0L101S, U19-ch111, adc4

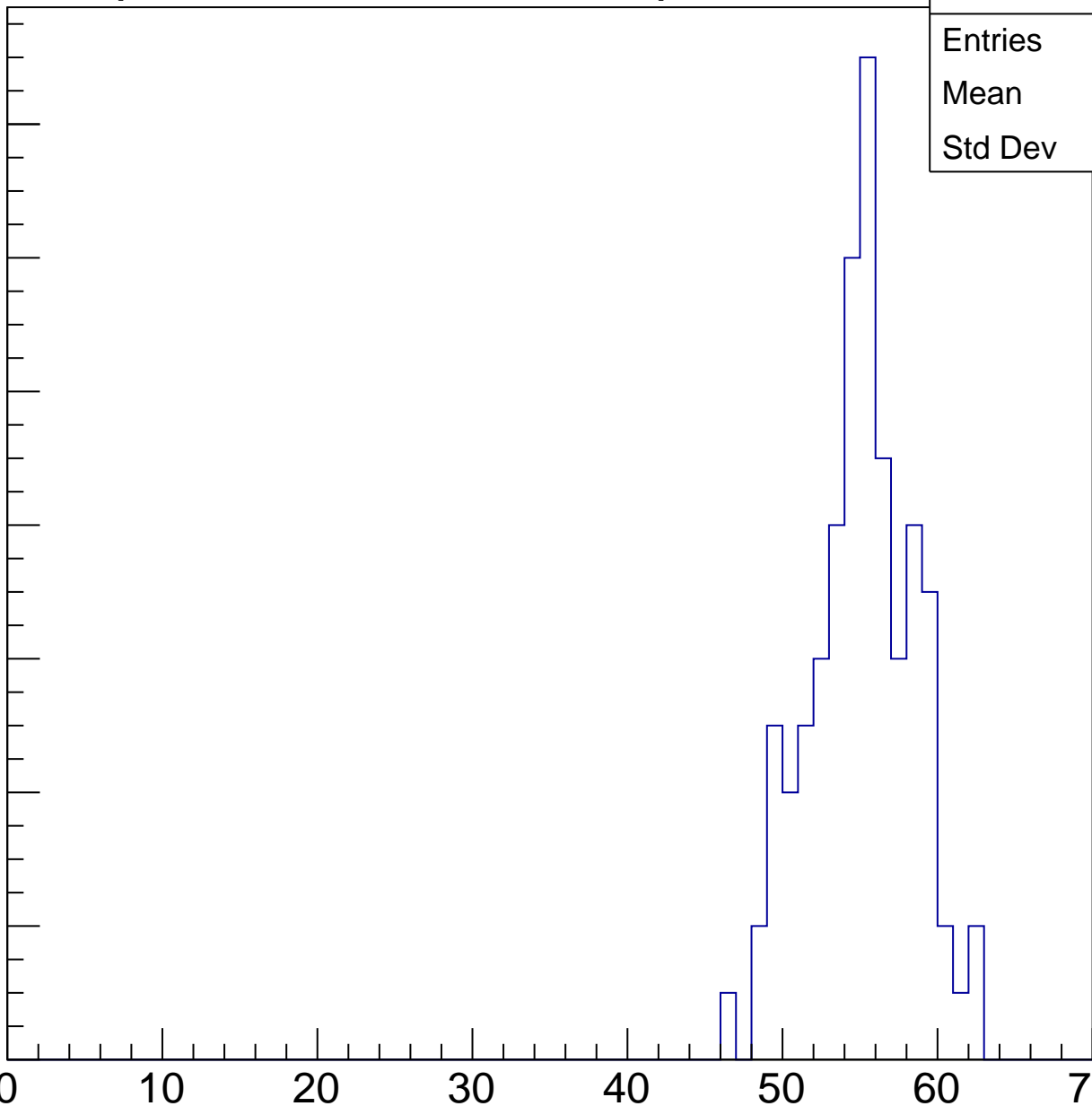
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	54.61
Std Dev	3.318

ampl



B0L101S, U19-ch111, adc5

calib_packv5_042523_0143.root, FC#1, port C1

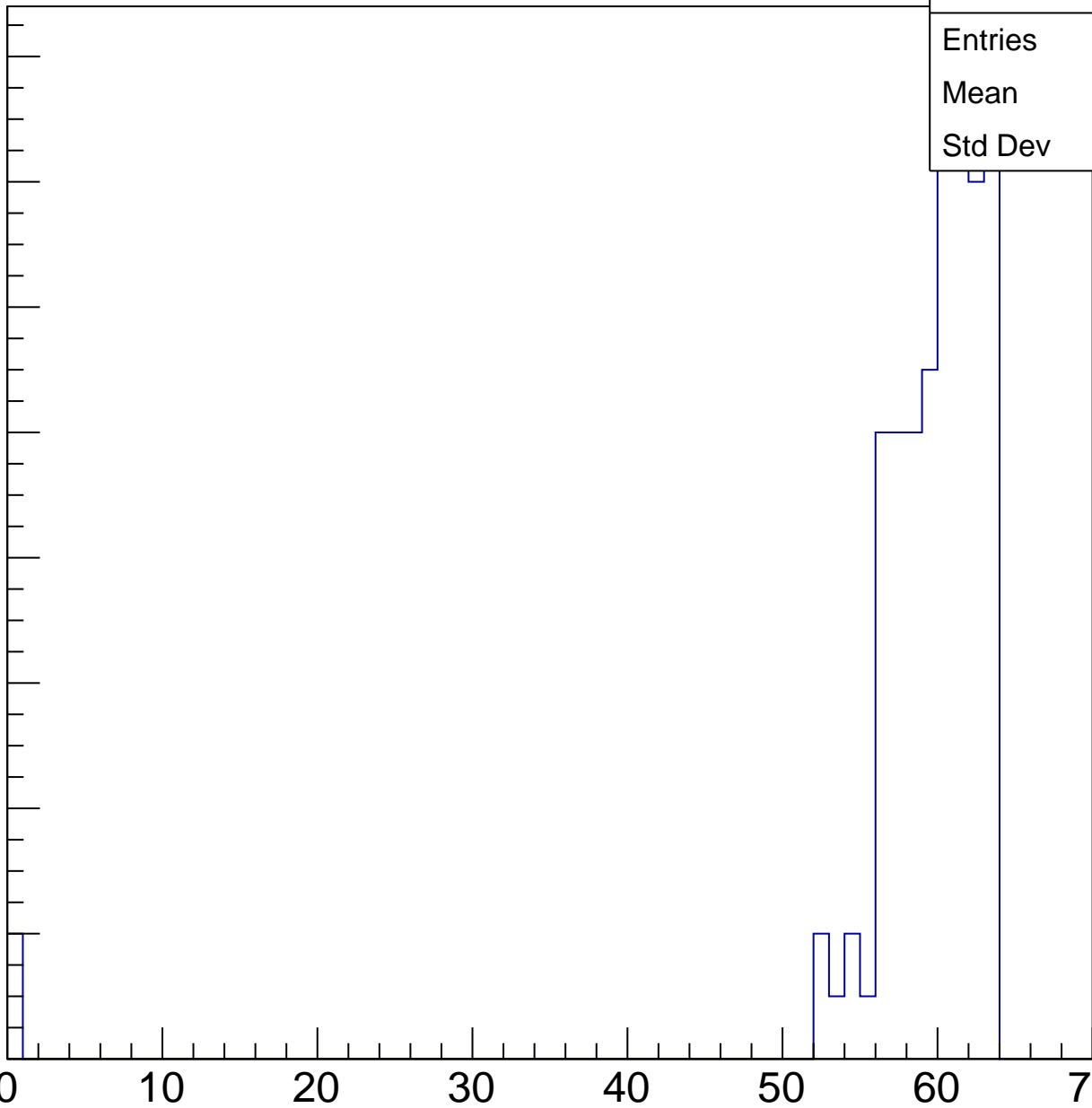
Entries	109
Mean	58.42
Std Dev	8.411

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

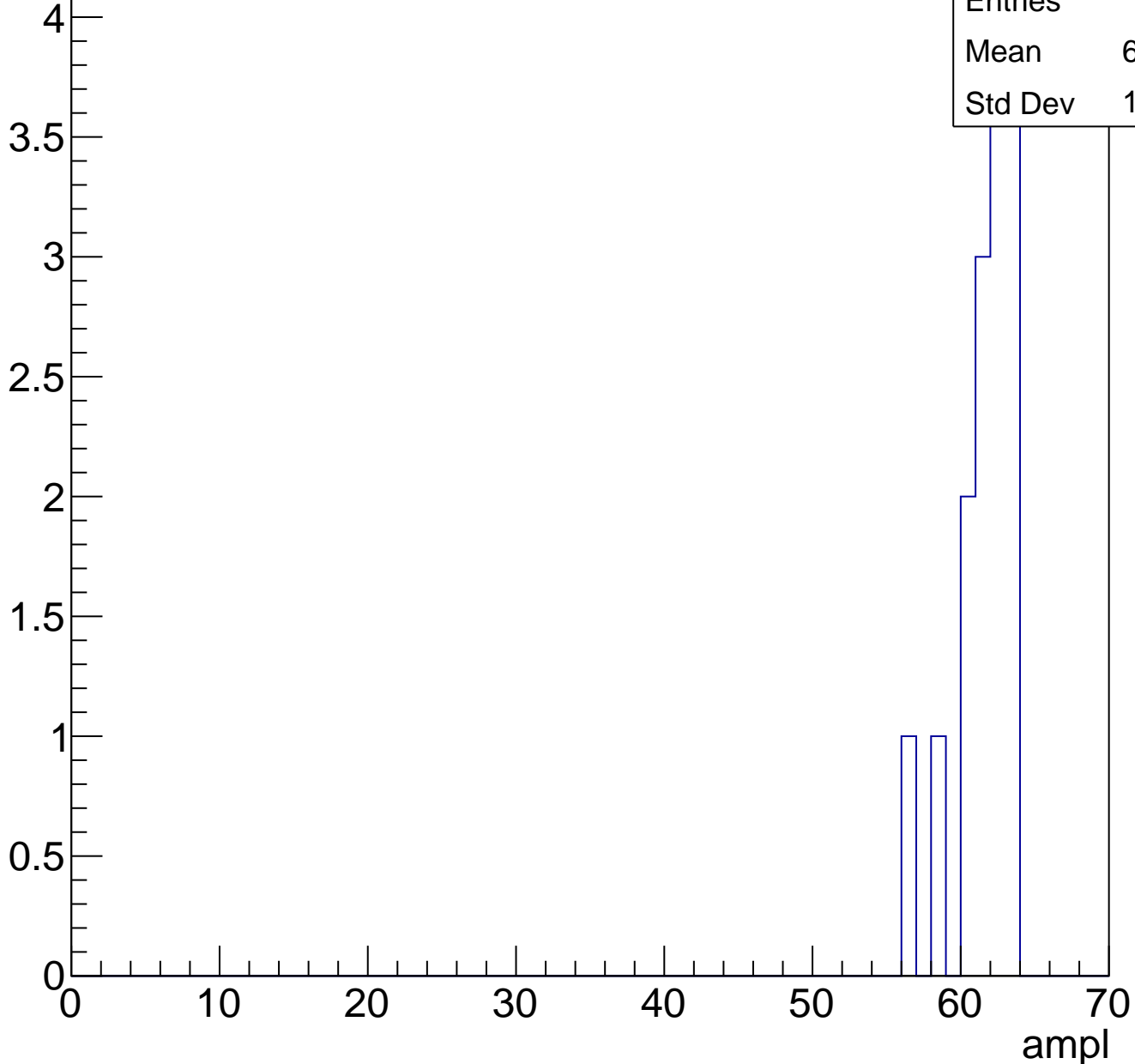
ampl



B0L101S, U19-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	41.5
Std Dev	21.5

ampl

0 10 20 30 40 50 60 70

B0L101S, U19-ch112, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	28.61
Std Dev	5.658

Gaus mean : 29.0850

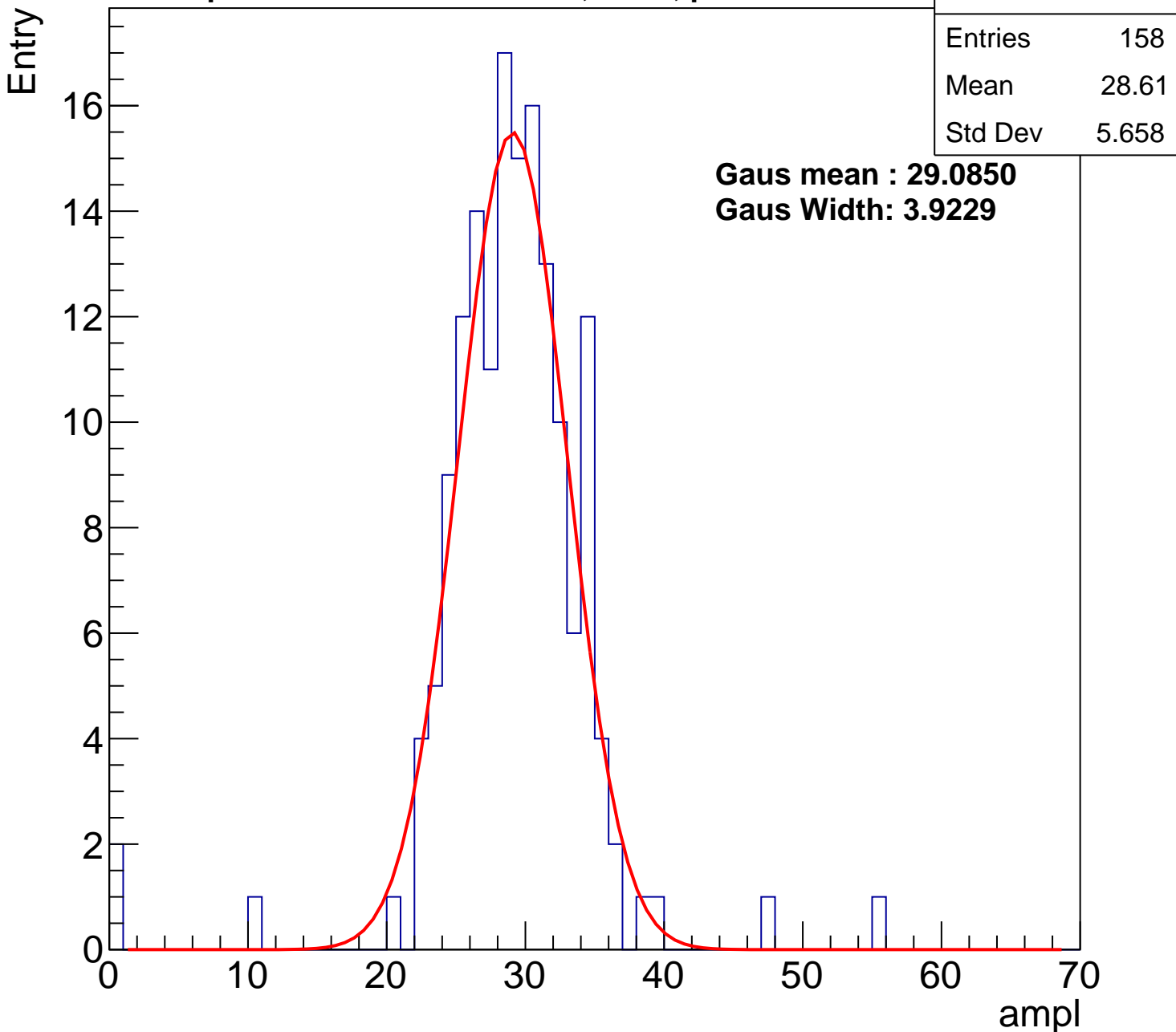
Gaus Width: 3.9229

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch112, adc1

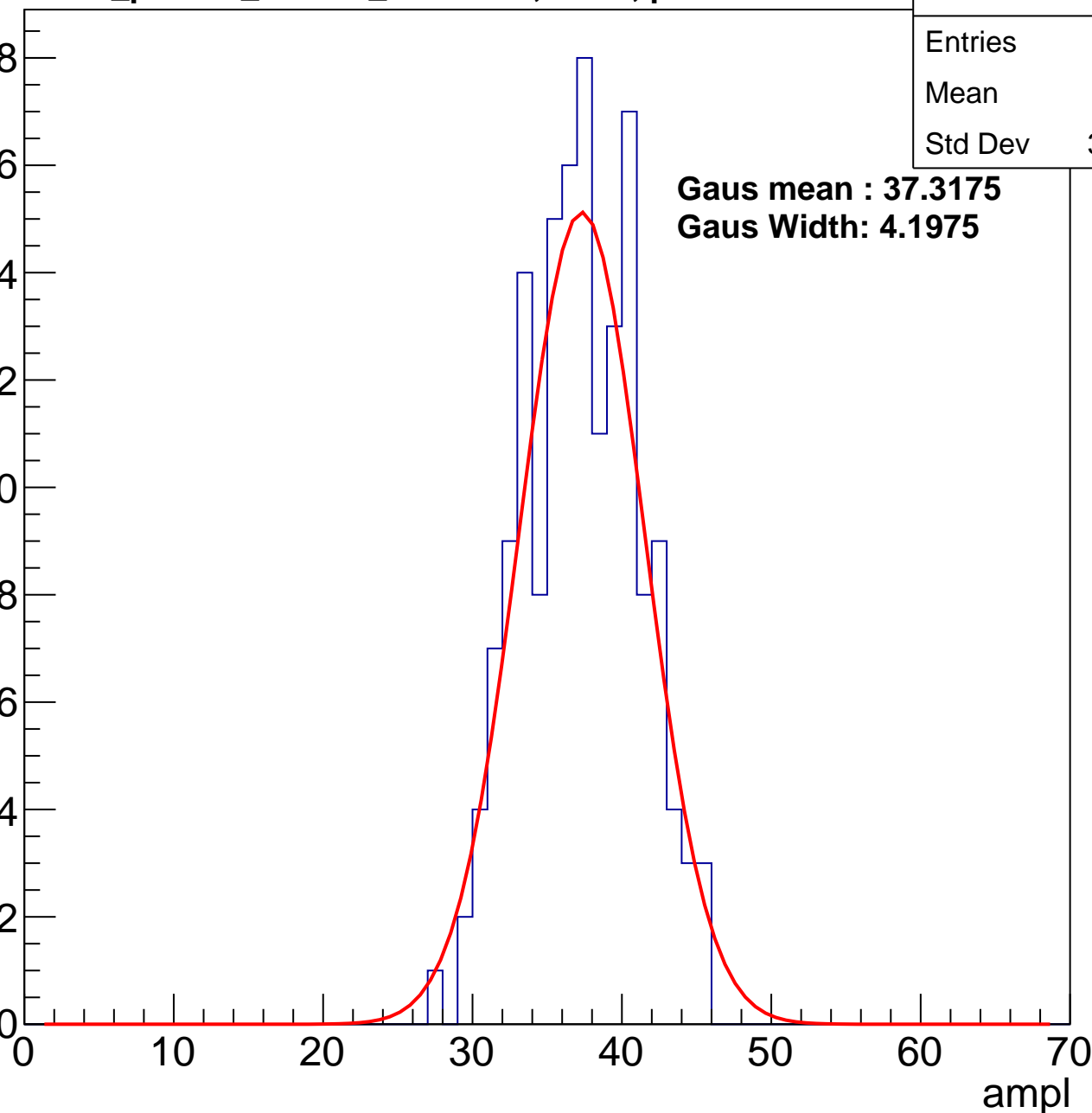
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	162
Mean	36.8
Std Dev	3.783

Gaus mean : 37.3175
Gaus Width: 4.1975



B0L101S, U19-ch112, adc2

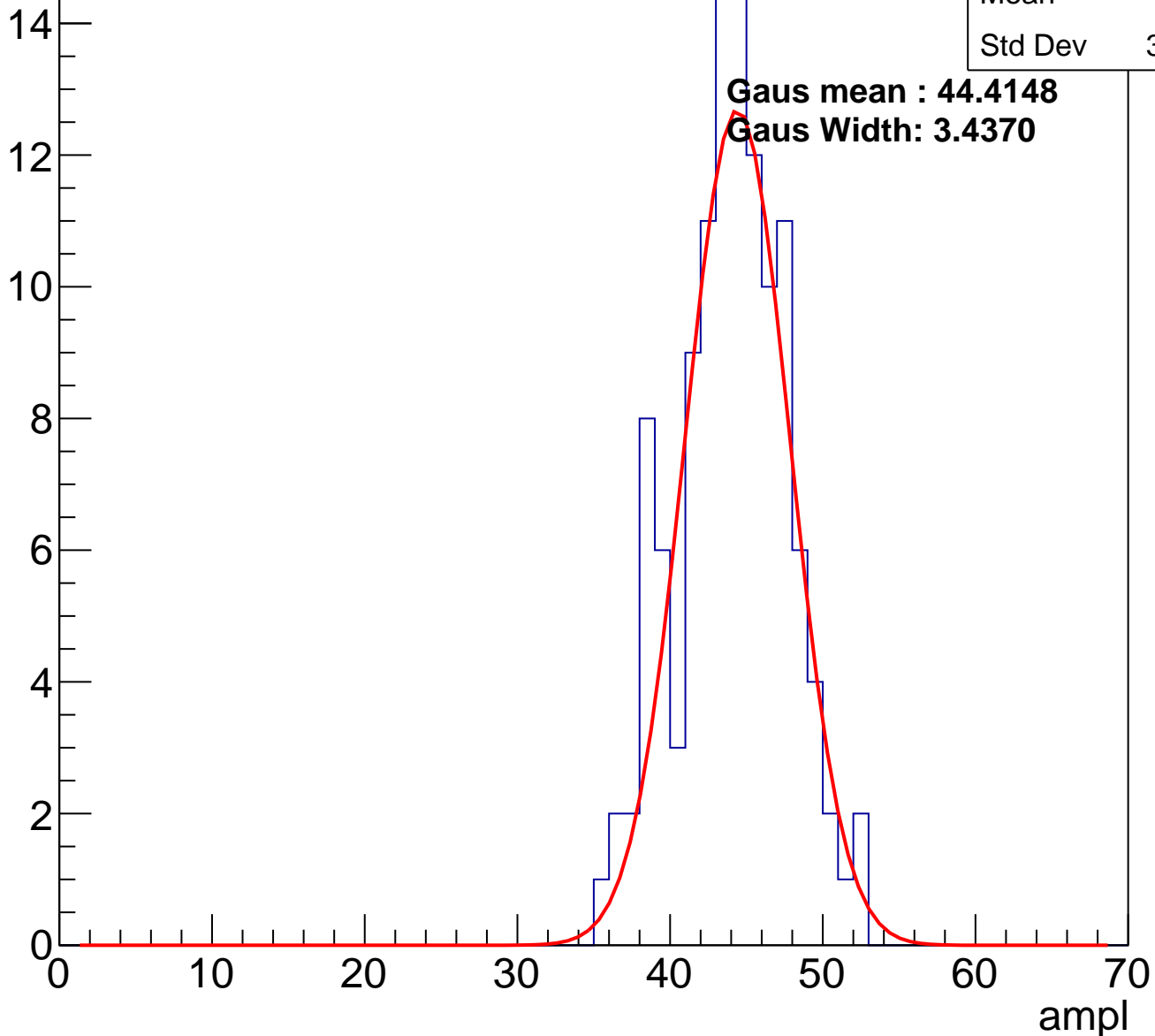
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	43.59
Std Dev	3.546

Gaus mean : 44.4148

Gaus Width: 3.4370

Entry

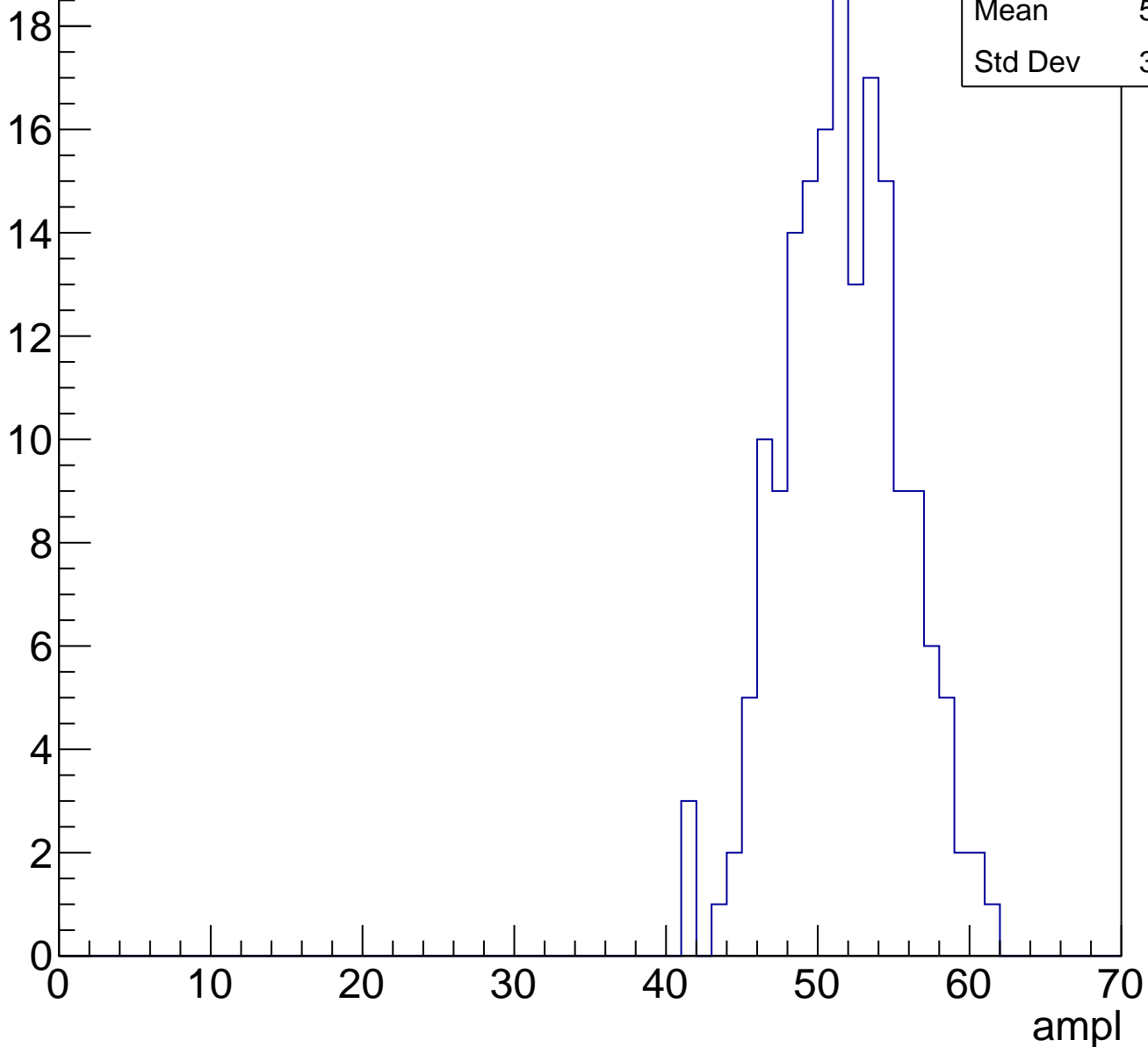


B0L101S, U19-ch112, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	51.18
Std Dev	3.927

Entry

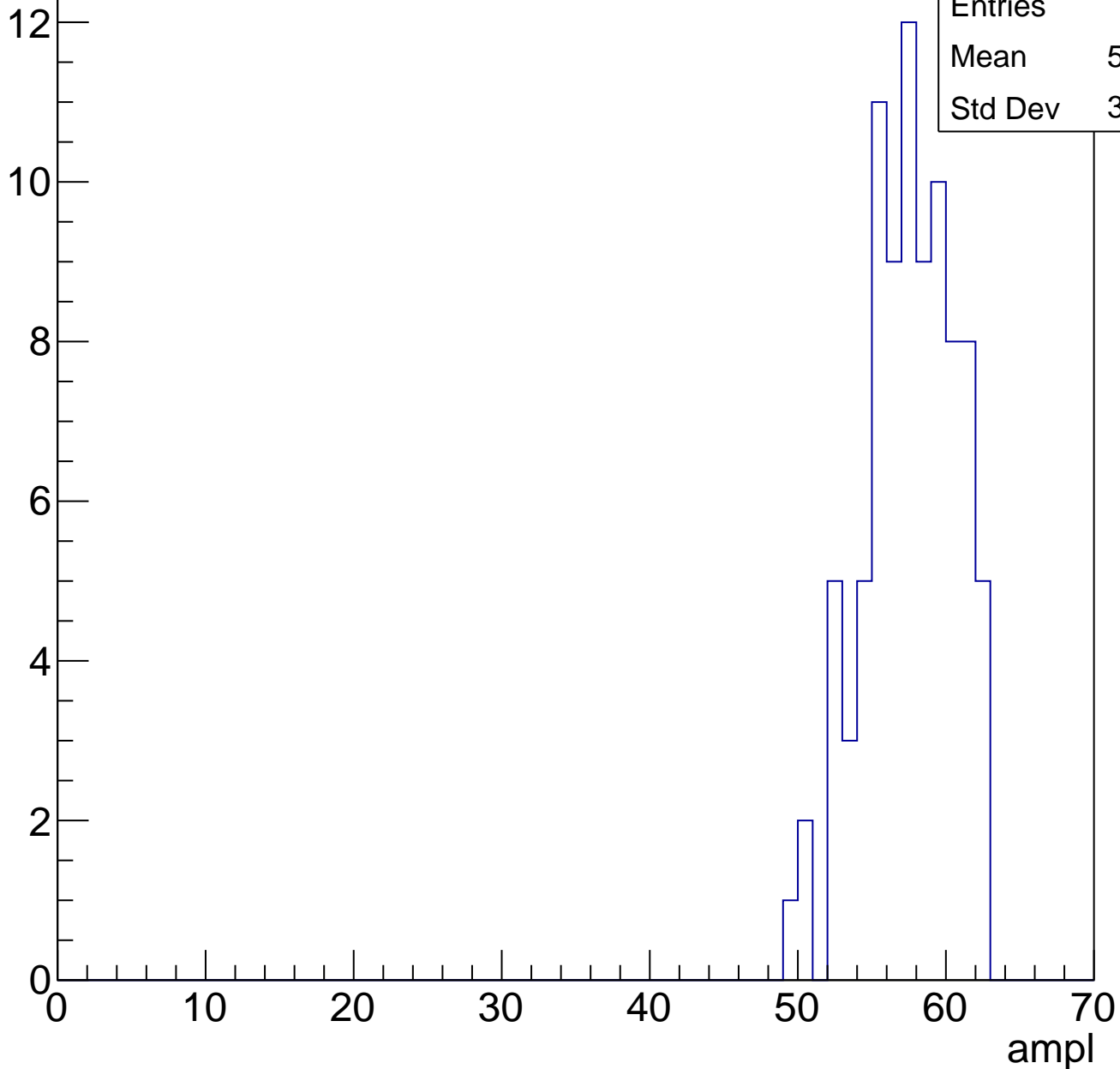


B0L101S, U19-ch112, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	88
Mean	57.06
Std Dev	3.028

Entry



B0L101S, U19-ch112, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

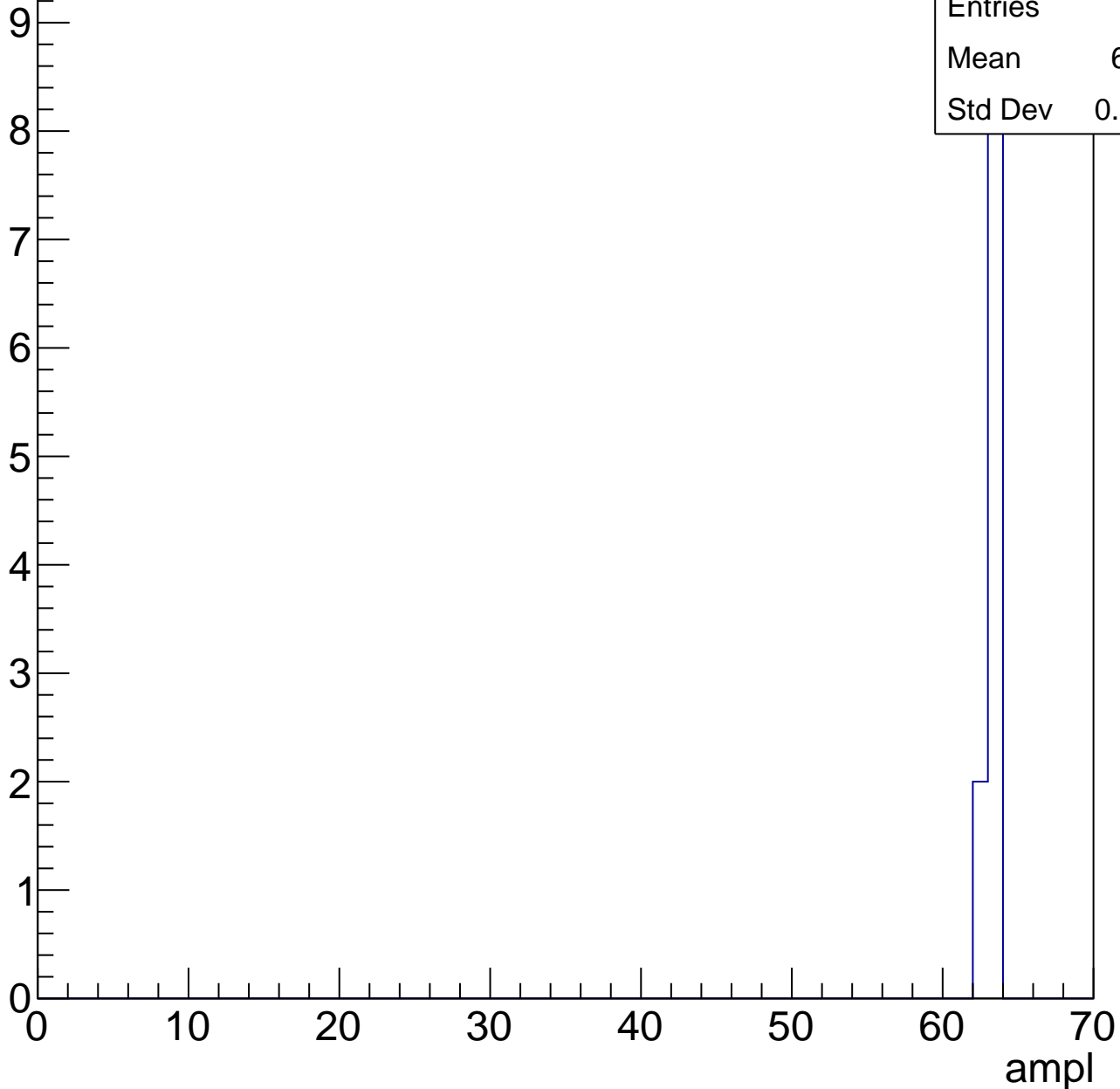
Entries	64
Mean	58.67
Std Dev	10.7

B0L101S, U19-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

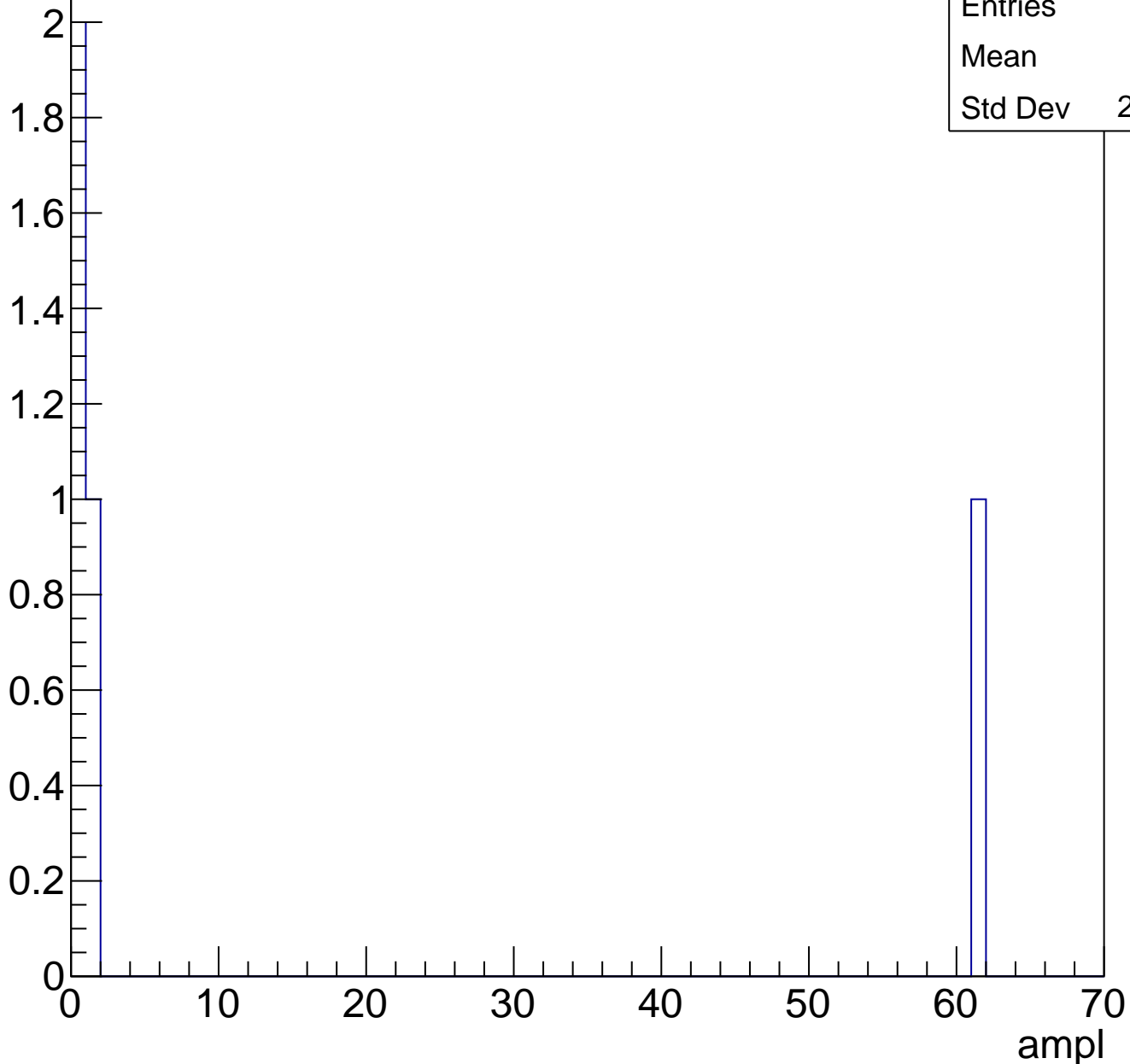
Entries	11
Mean	62.82
Std Dev	0.3857



B0L101S, U19-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch113, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

117

Mean

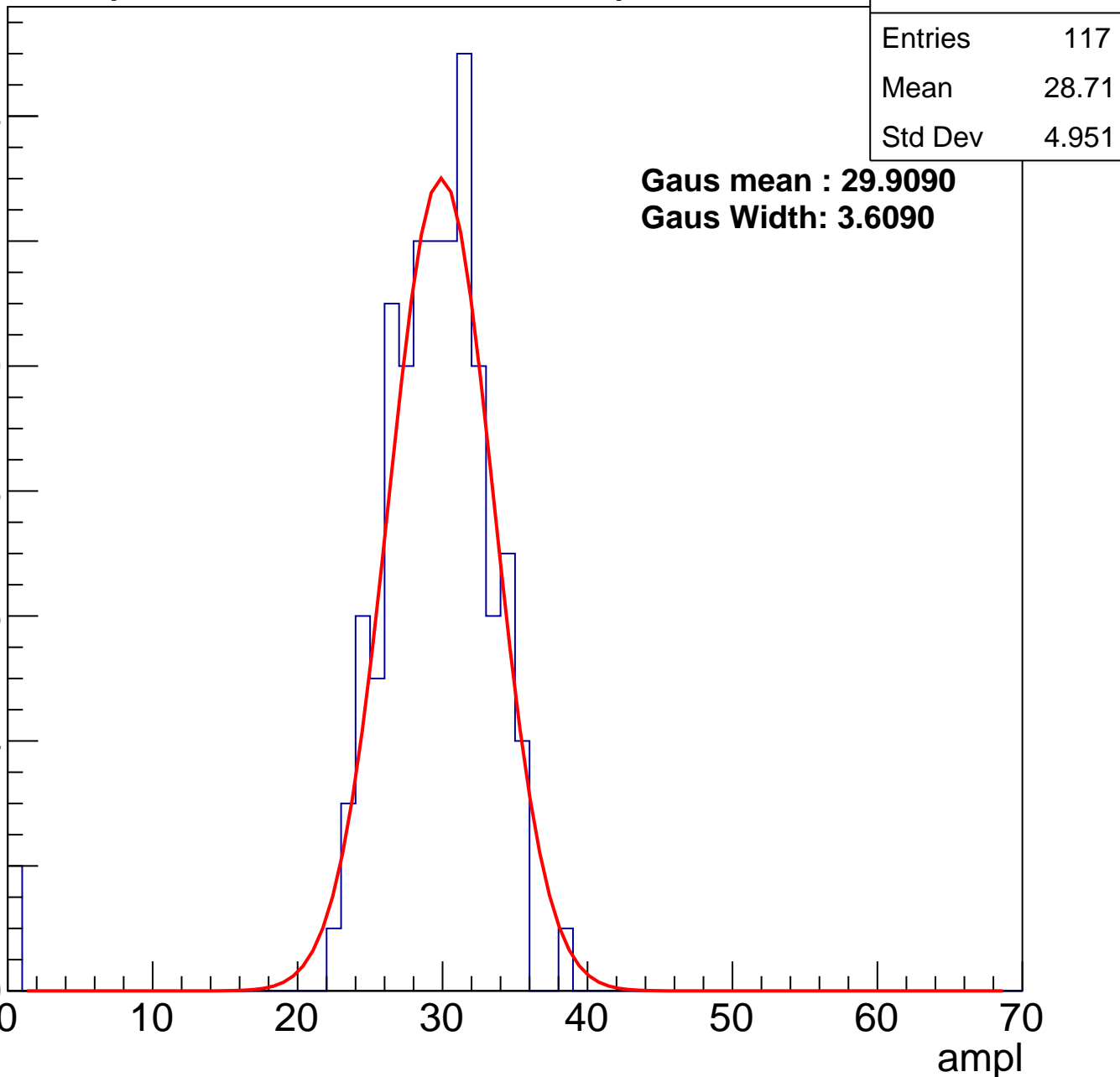
28.71

Std Dev

4.951

Gaus mean : 29.9090

Gaus Width: 3.6090



B0L101S, U19-ch113, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	36.39
Std Dev	3.504

Gaus mean : 36.8782

Gaus Width: 3.8896

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U19-ch113, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	131
Mean	43.18
Std Dev	3.629

Gaus mean : 43.7590

Gaus Width: 3.9980

0

2

4

6

8

10

12

Entry

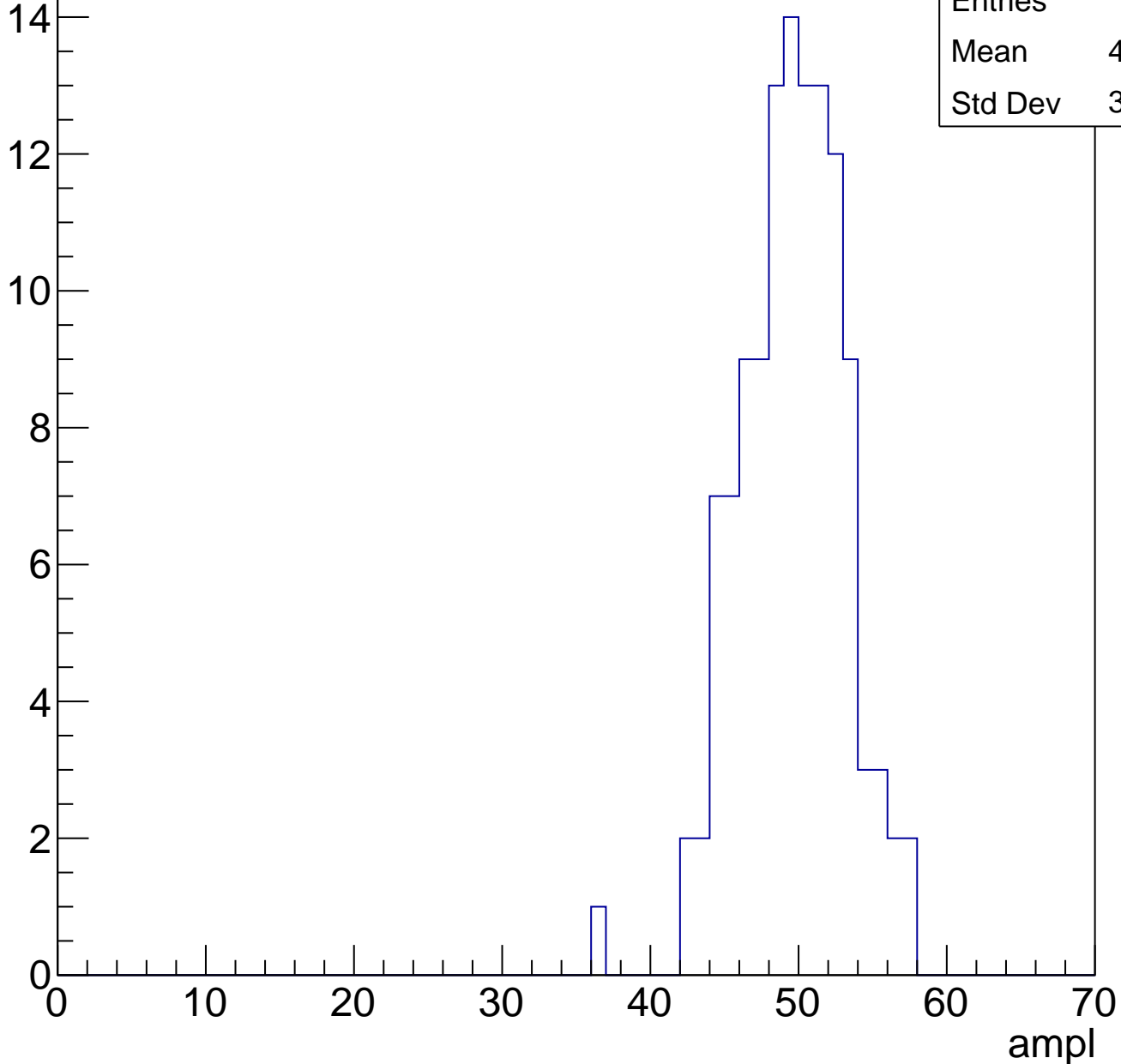
calib_packv5_042523_0143.root, FC#1, port C1

B0L101S, U19-ch113, adc2

B0L101S, U19-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

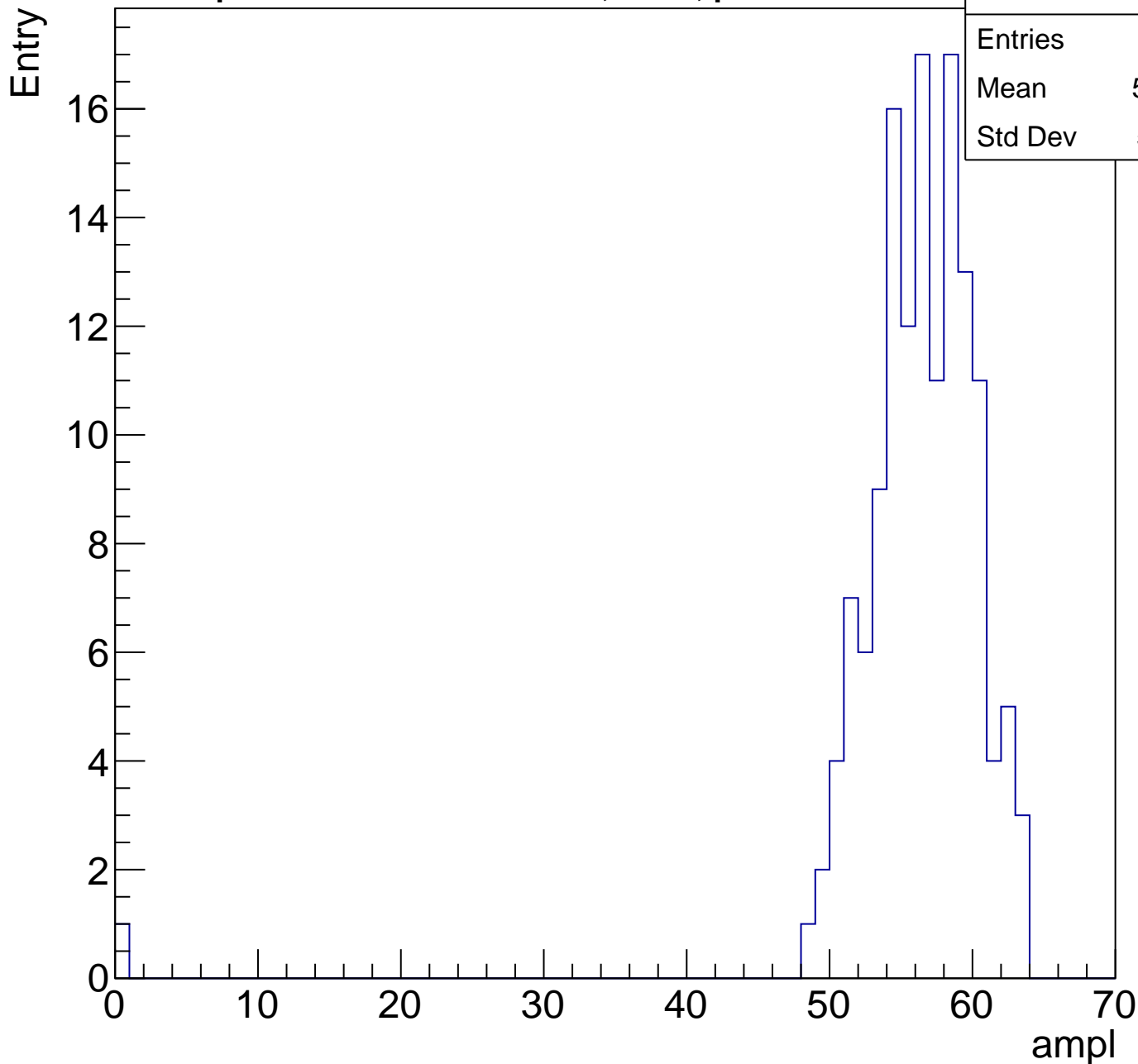
Entry



B0L101S, U19-ch113, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	55.76
Std Dev	5.791



B0L101S, U19-ch113, adc5

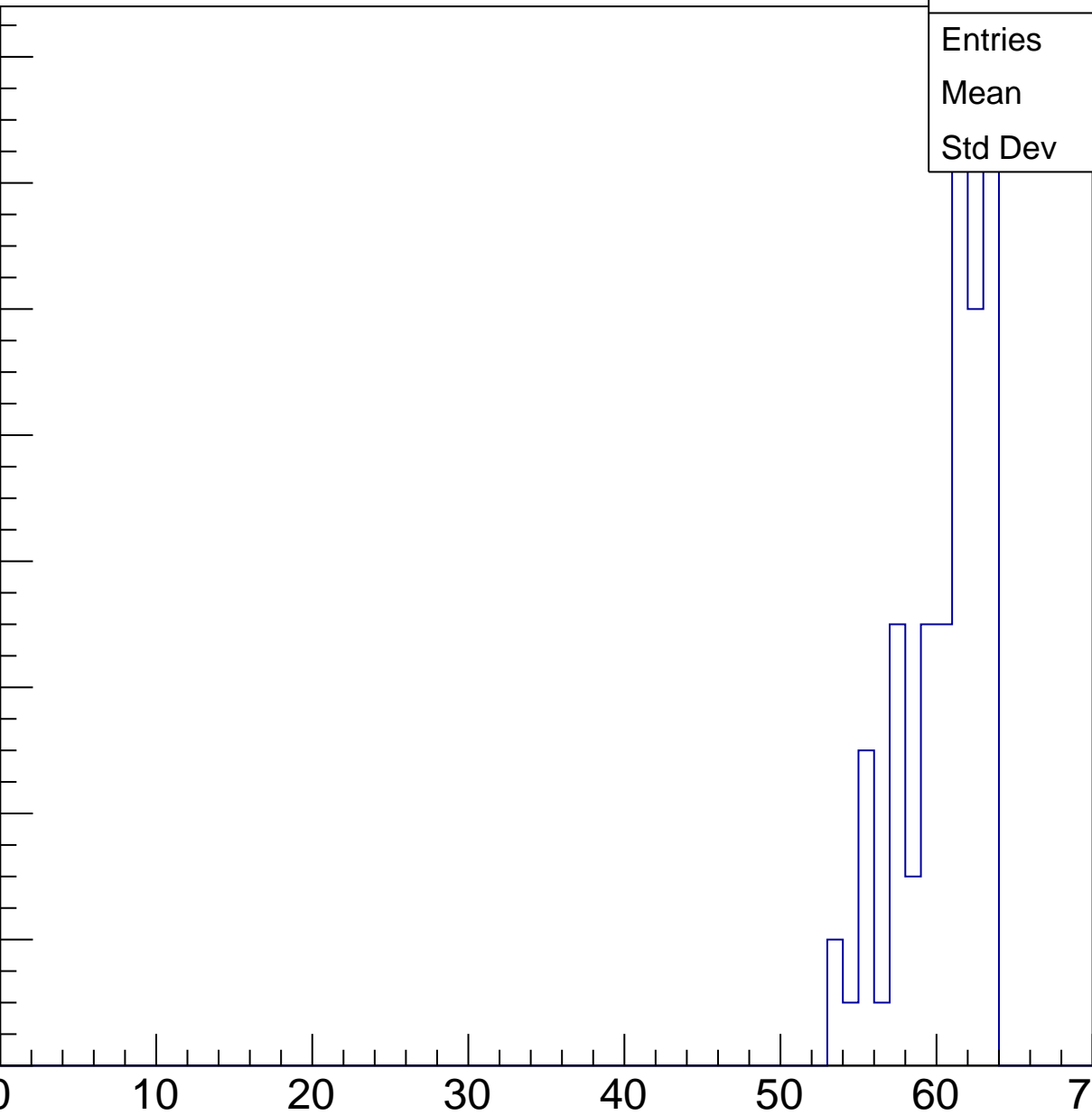
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	60.03
Std Dev	2.705

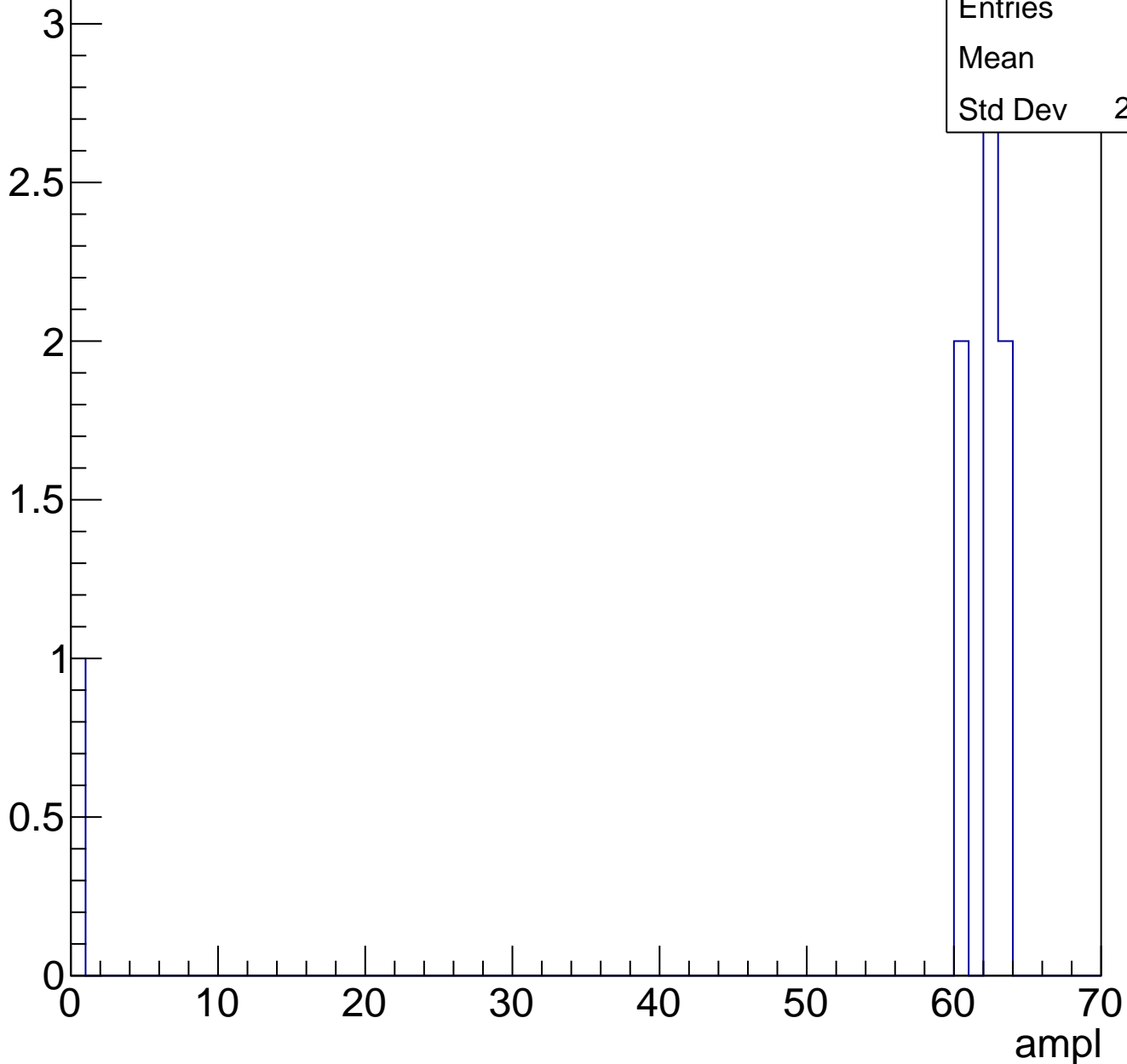
ampl



B0L101S, U19-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch114, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	28.49
Std Dev	5.547

Gaus mean : 29.1987

Gaus Width: 3.6777

Entry

16

14

12

10

8

6

4

2

0

0

10

20

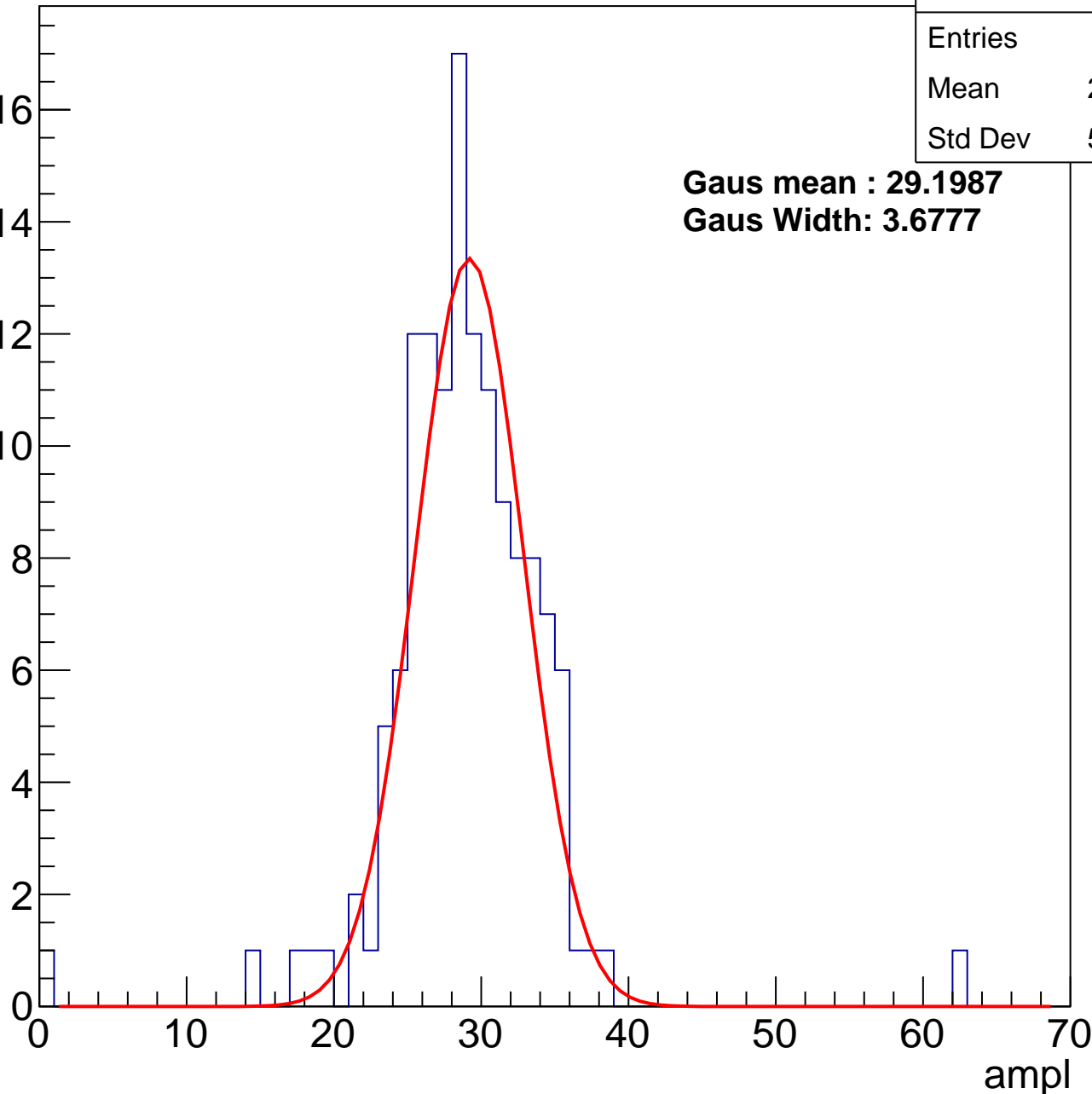
30

40

50

60

ampl



B0L101S, U19-ch114, adc1

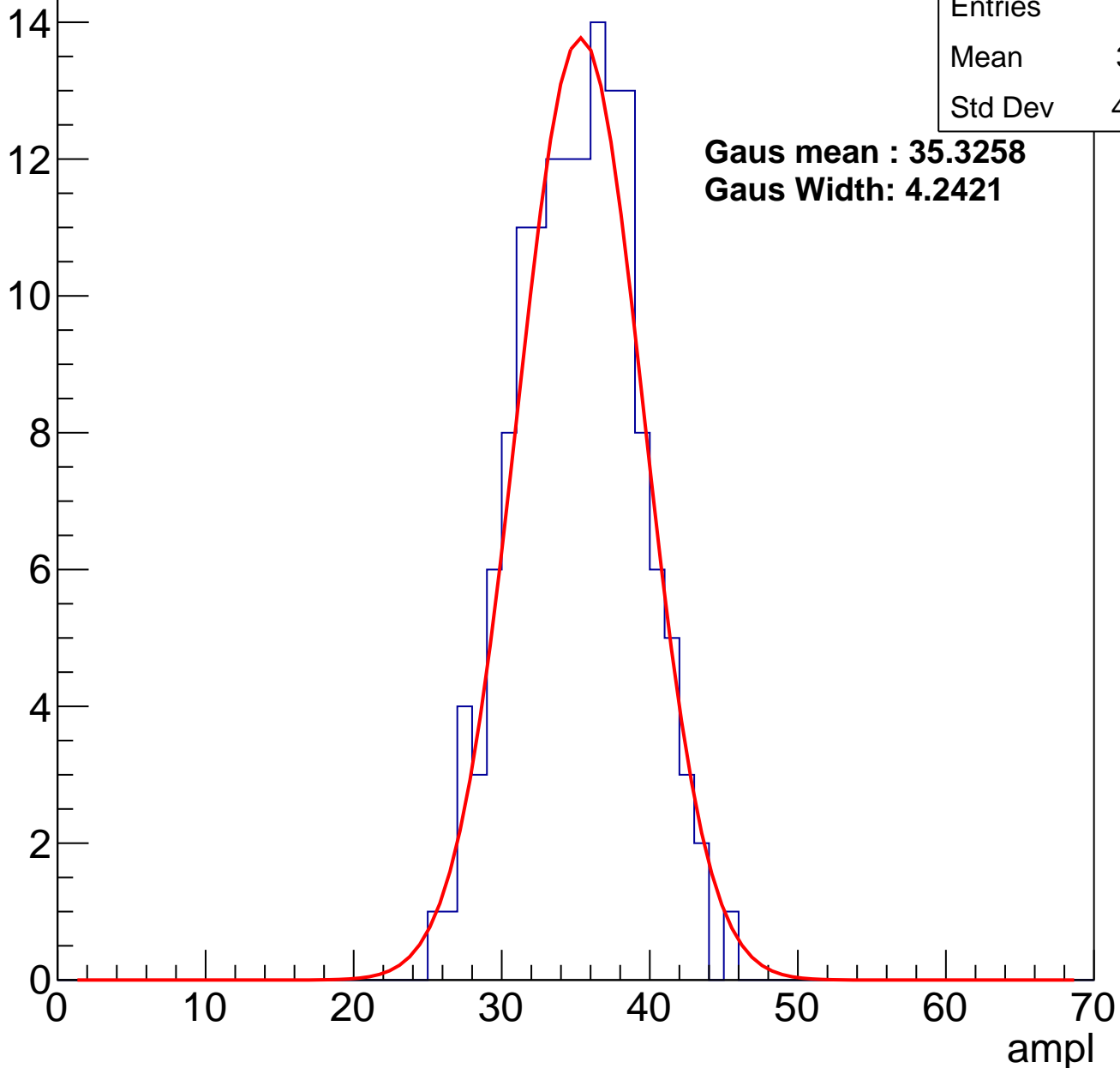
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	34.71
Std Dev	4.002

Gaus mean : 35.3258

Gaus Width: 4.2421

Entry



B0L101S, U19-ch114, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	42.27
Std Dev	3.743

Gaus mean : 42.8662

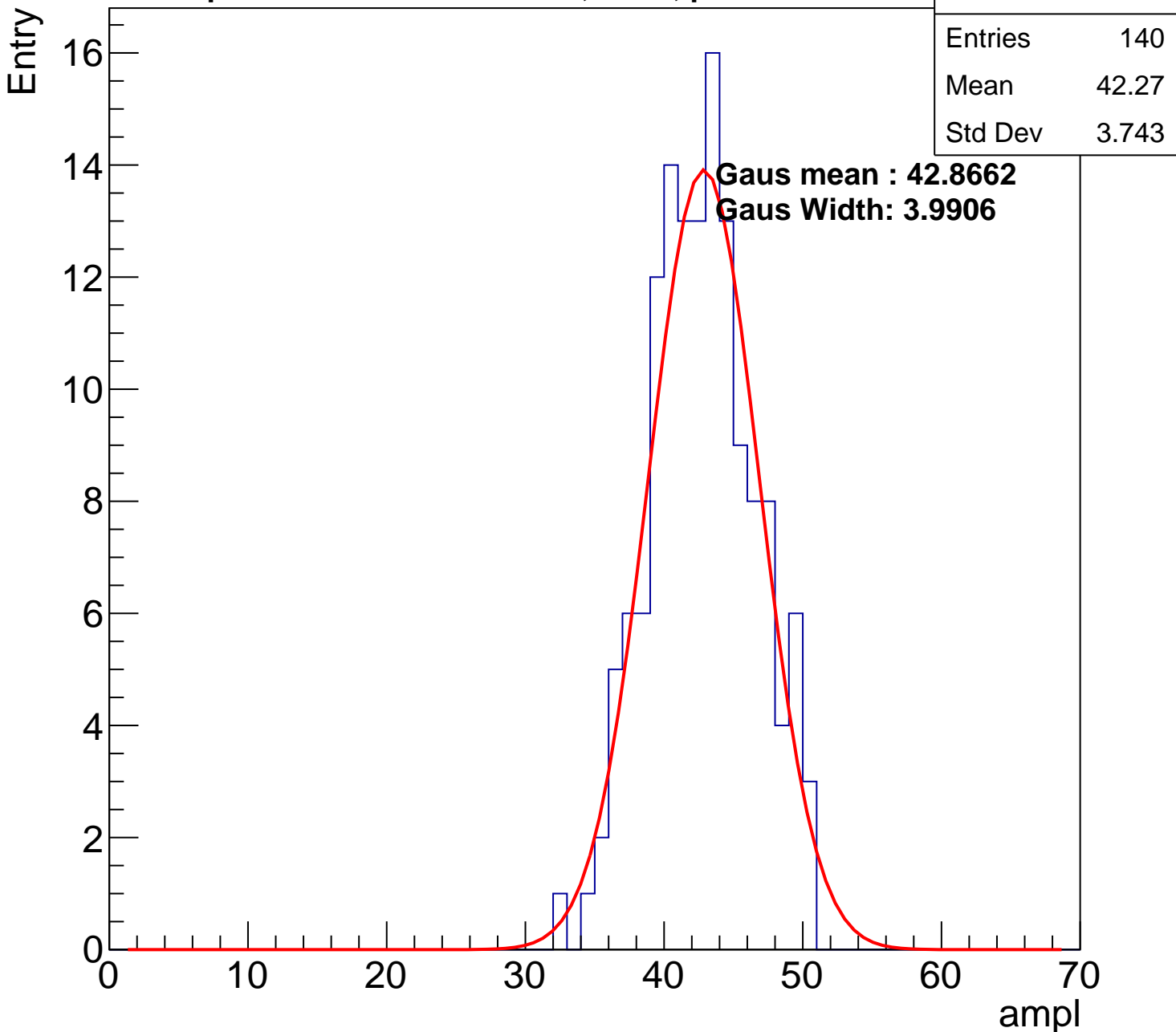
Gaus Width: 3.9906

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch114, adc3

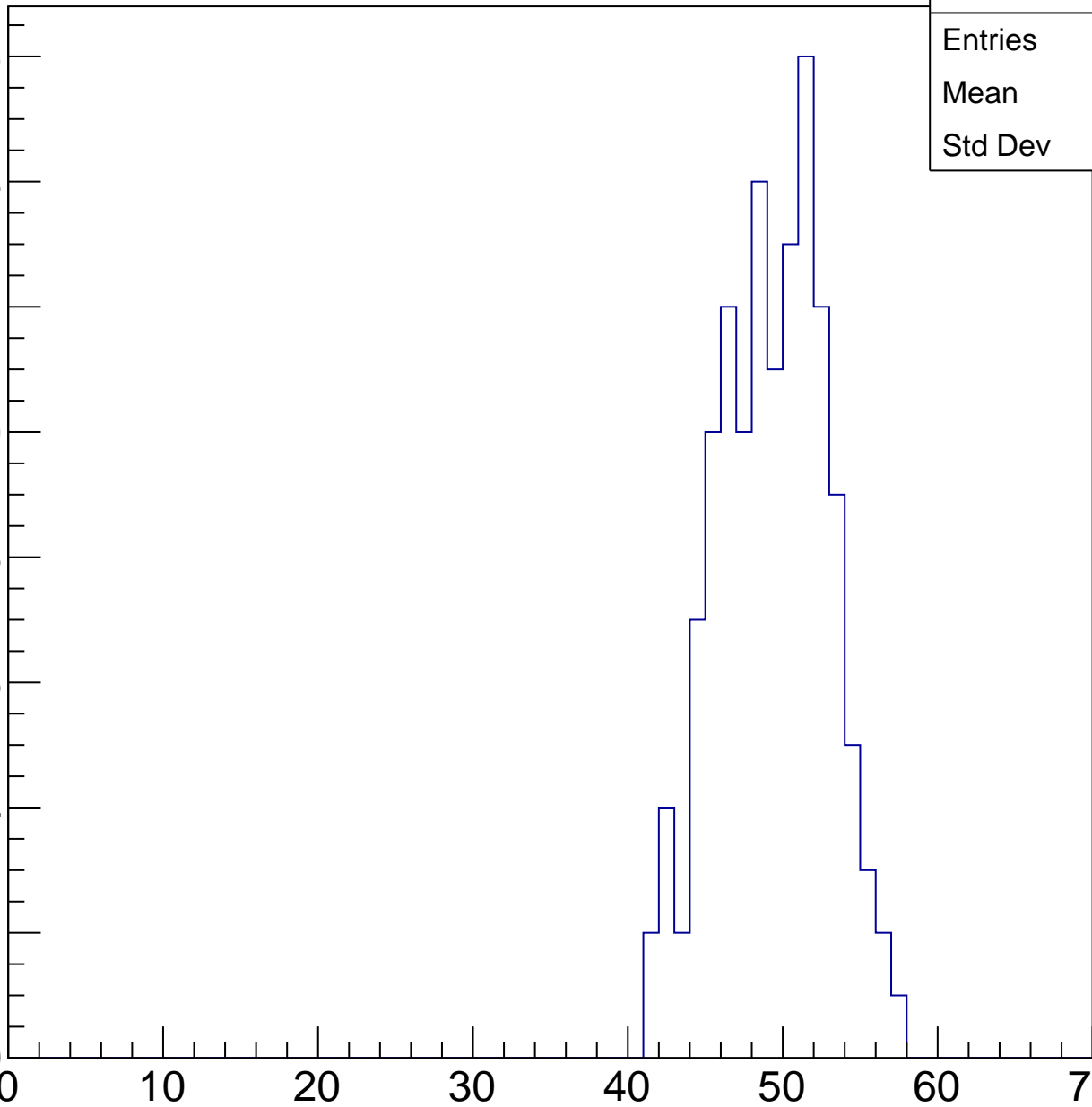
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	48.86
Std Dev	3.484

ampl



B0L101S, U19-ch114, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.46
Std Dev	3.316

Entry

14

12

10

8

6

4

2

0

0

10

20

30

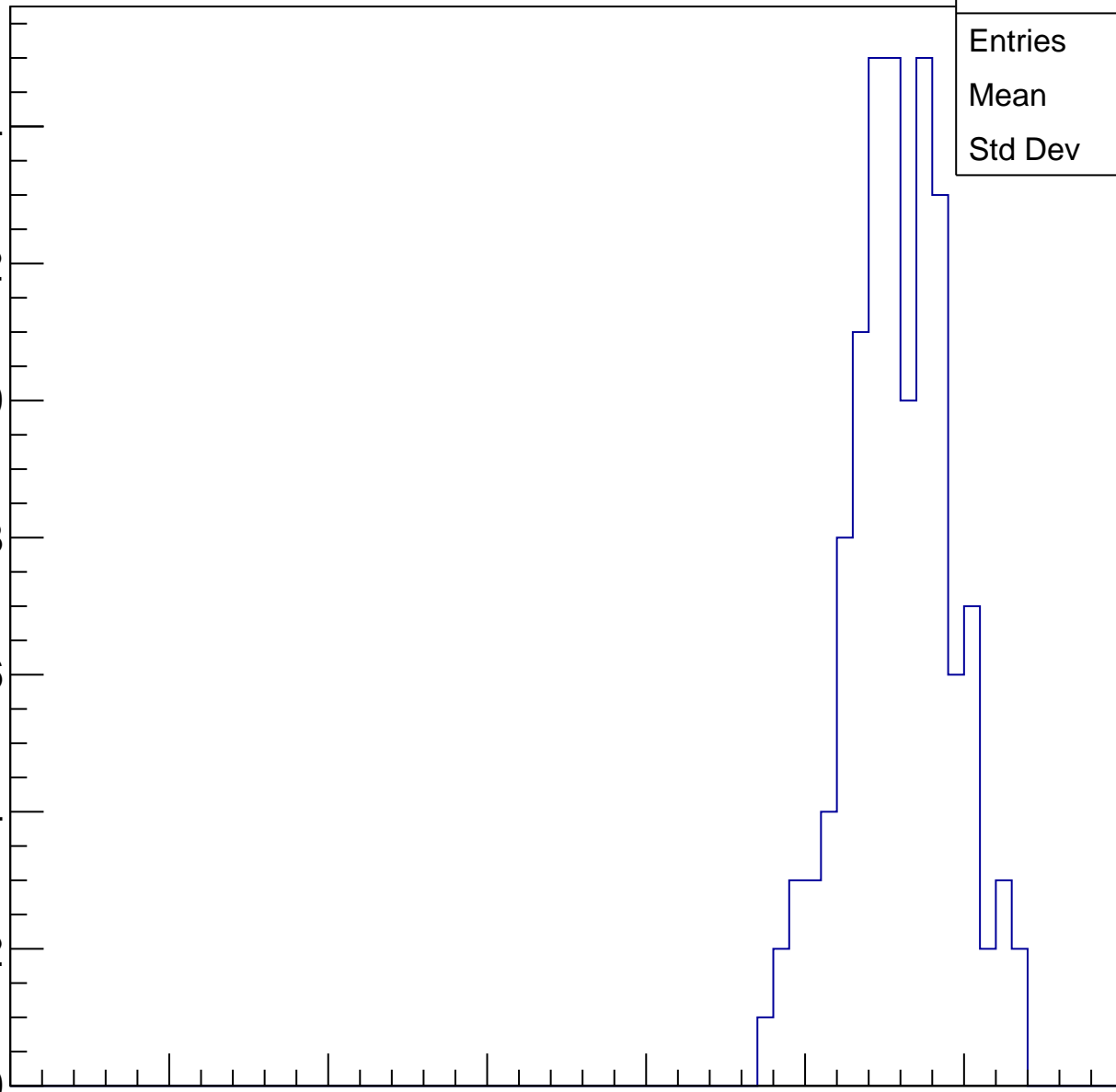
40

50

60

70

ampl

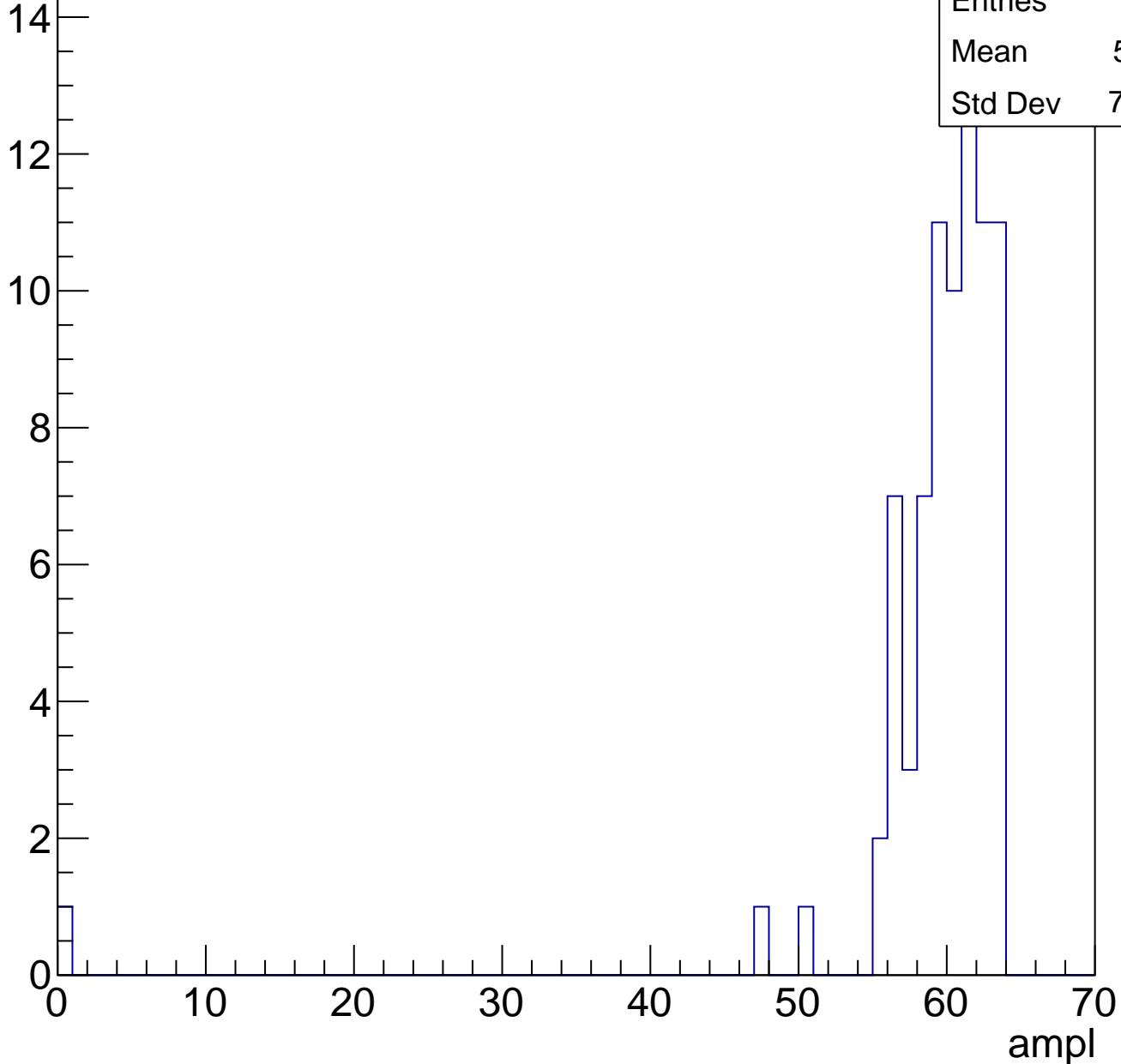


B0L101S, U19-ch114, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	79
Mean	58.91
Std Dev	7.257

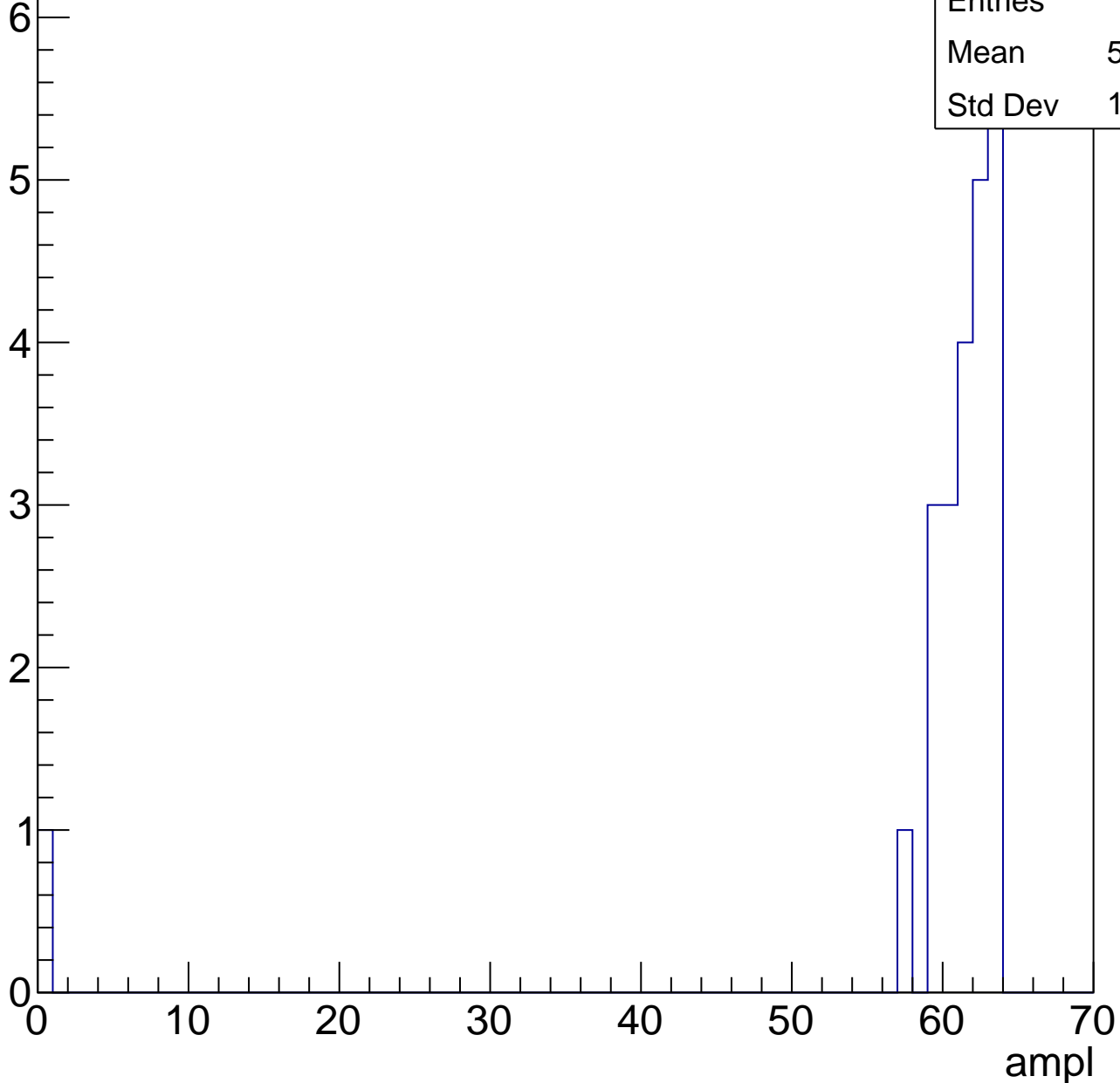


B0L101S, U19-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

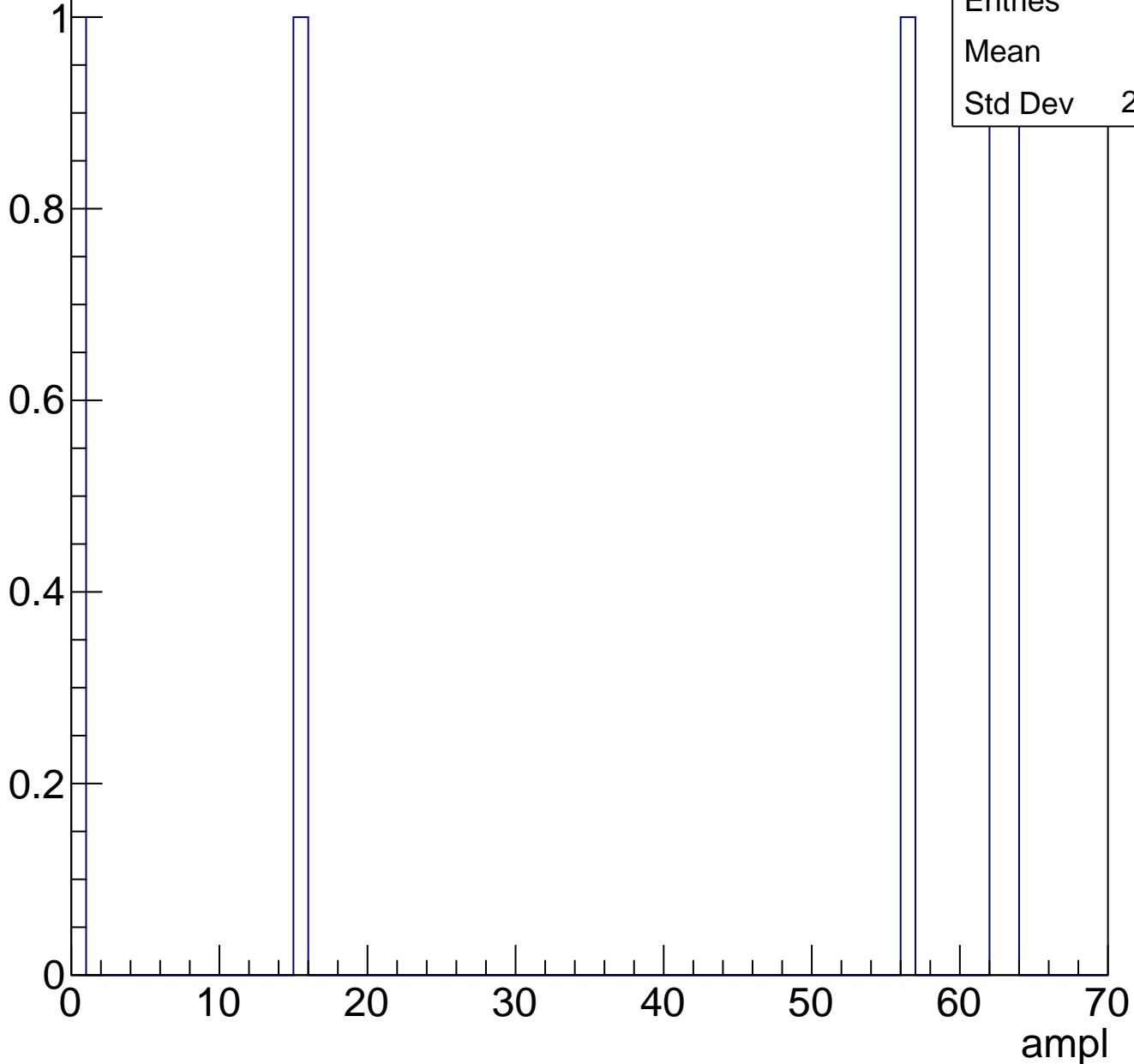
Entries	23
Mean	58.52
Std Dev	12.58



B0L101S, U19-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	39.2
Std Dev	26.42

B0L101S, U19-ch115, adc0

calib_packv5_042523_0143.root, FC#1, port C1

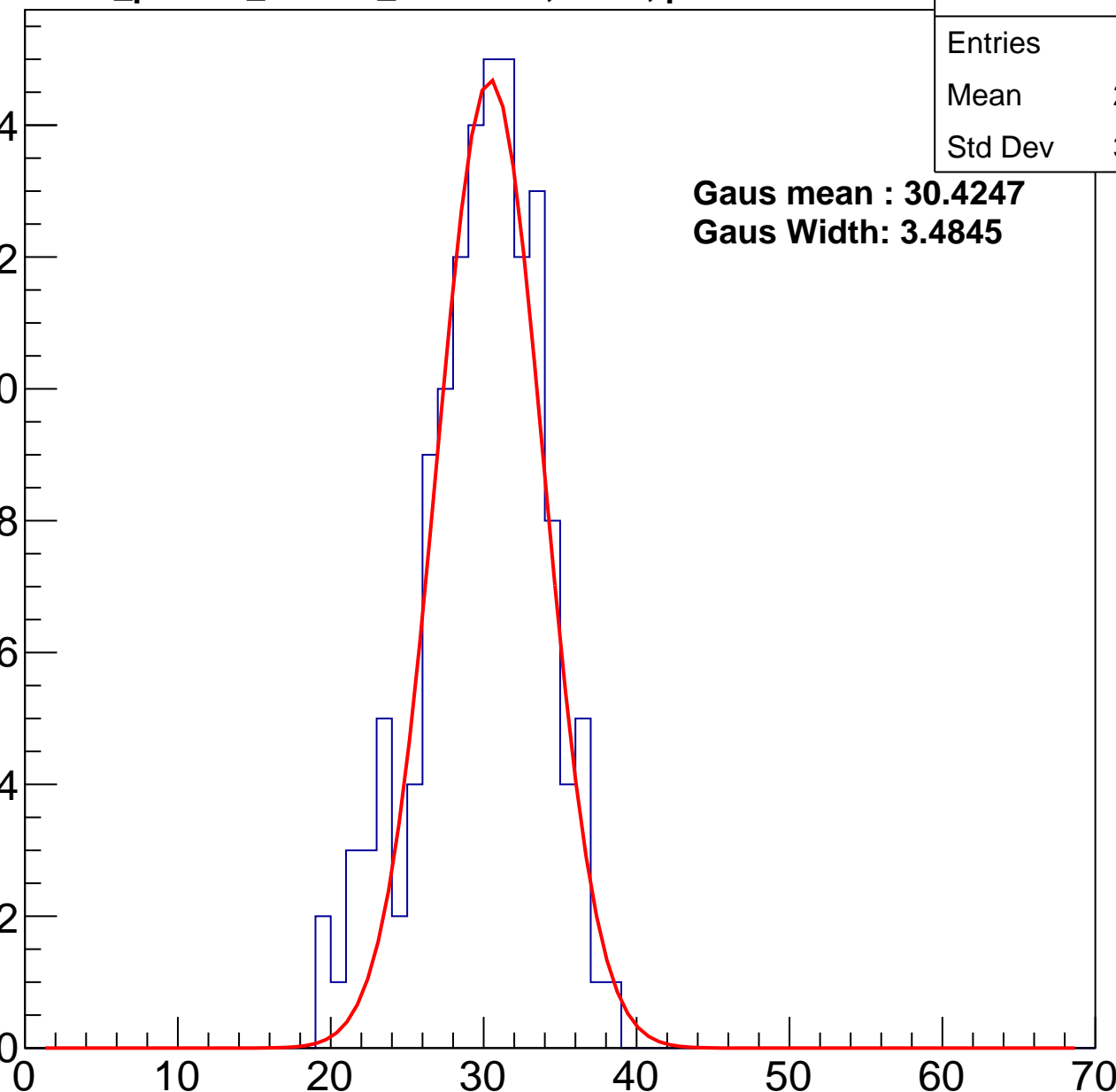
Entry

14
12
10
8
6
4
2
0

Entries	139
Mean	29.43
Std Dev	3.929

Gaus mean : 30.4247

Gaus Width: 3.4845



ampl

B0L101S, U19-ch115, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	36.12
Std Dev	3.159

Gaus mean : 36.4709

Gaus Width: 2.9528

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

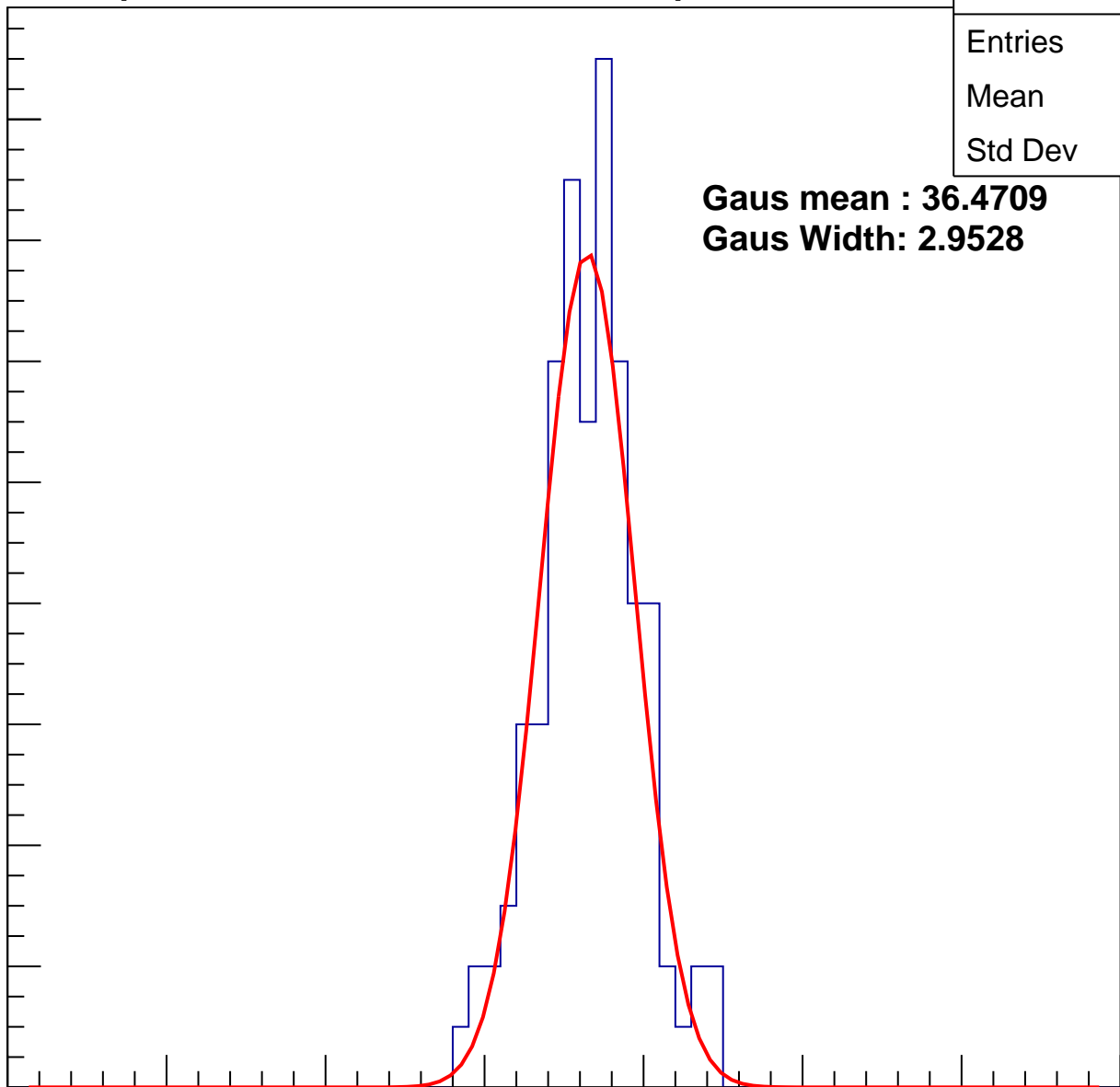
40

50

60

70

ampl



B0L101S, U19-ch115, adc2

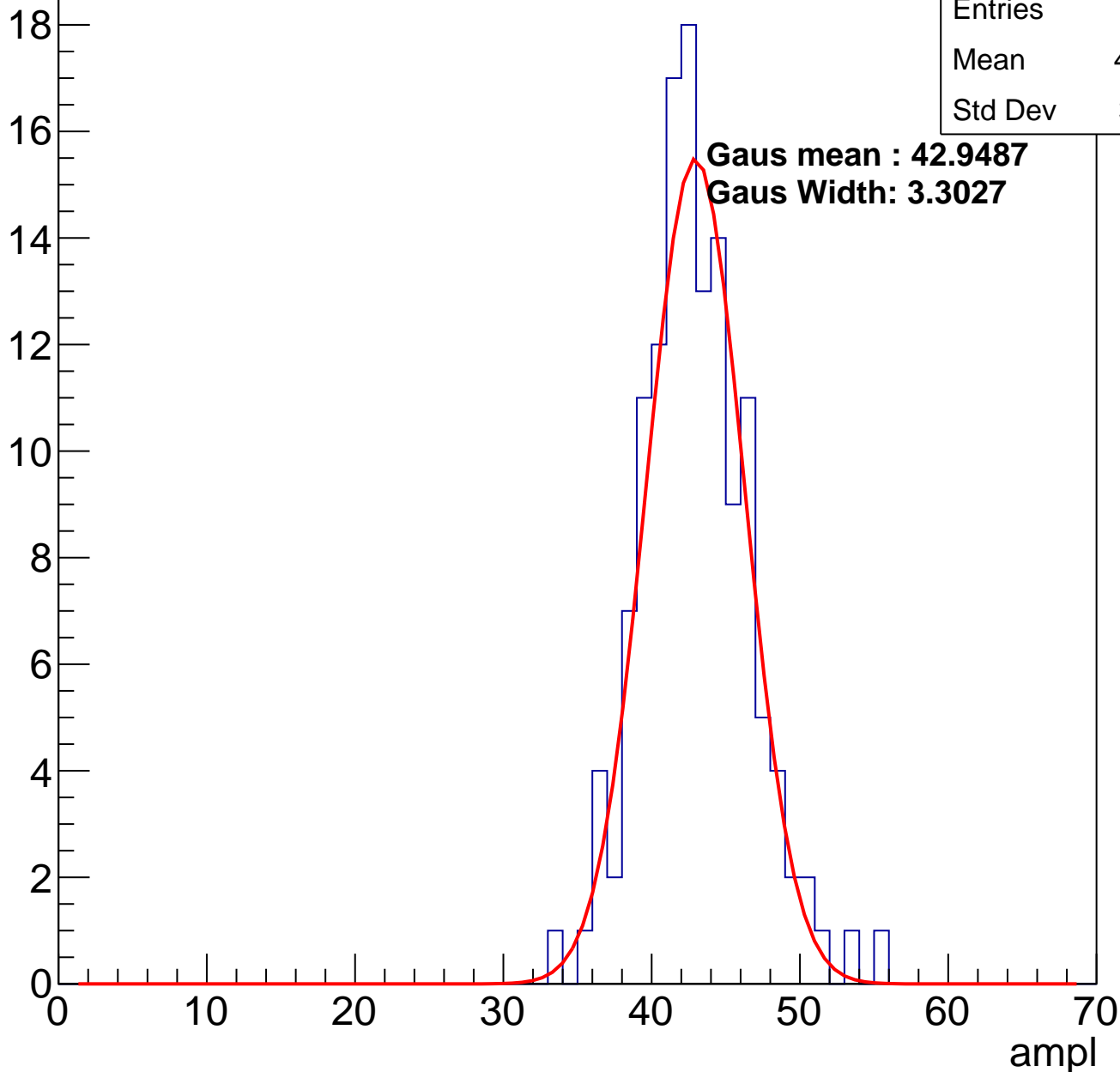
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	42.53
Std Dev	3.591

Gaus mean : 42.9487

Gaus Width: 3.3027

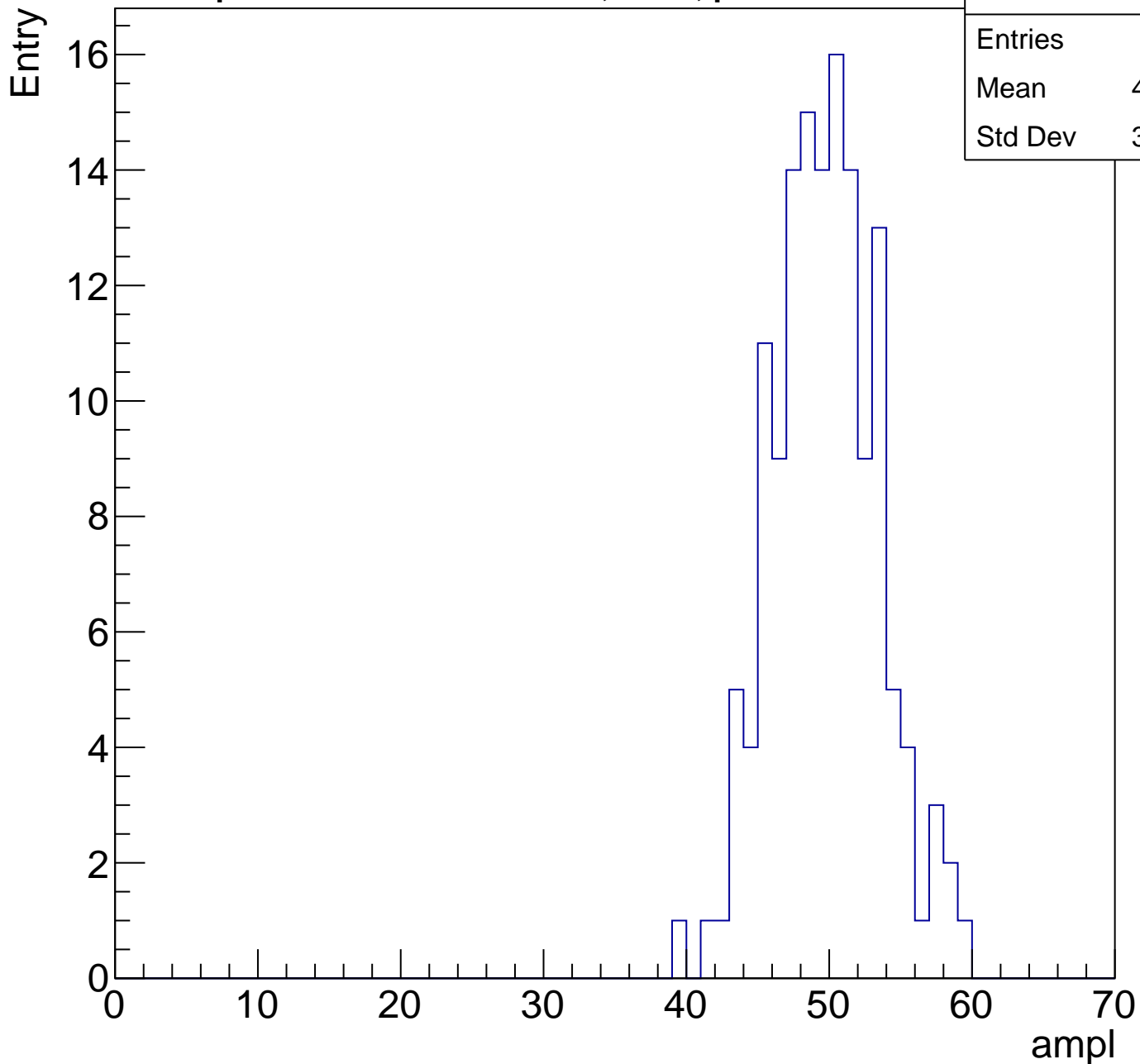
Entry



B0L101S, U19-ch115, adc3

calib_packv5_042523_0143.root, FC#1, port C1

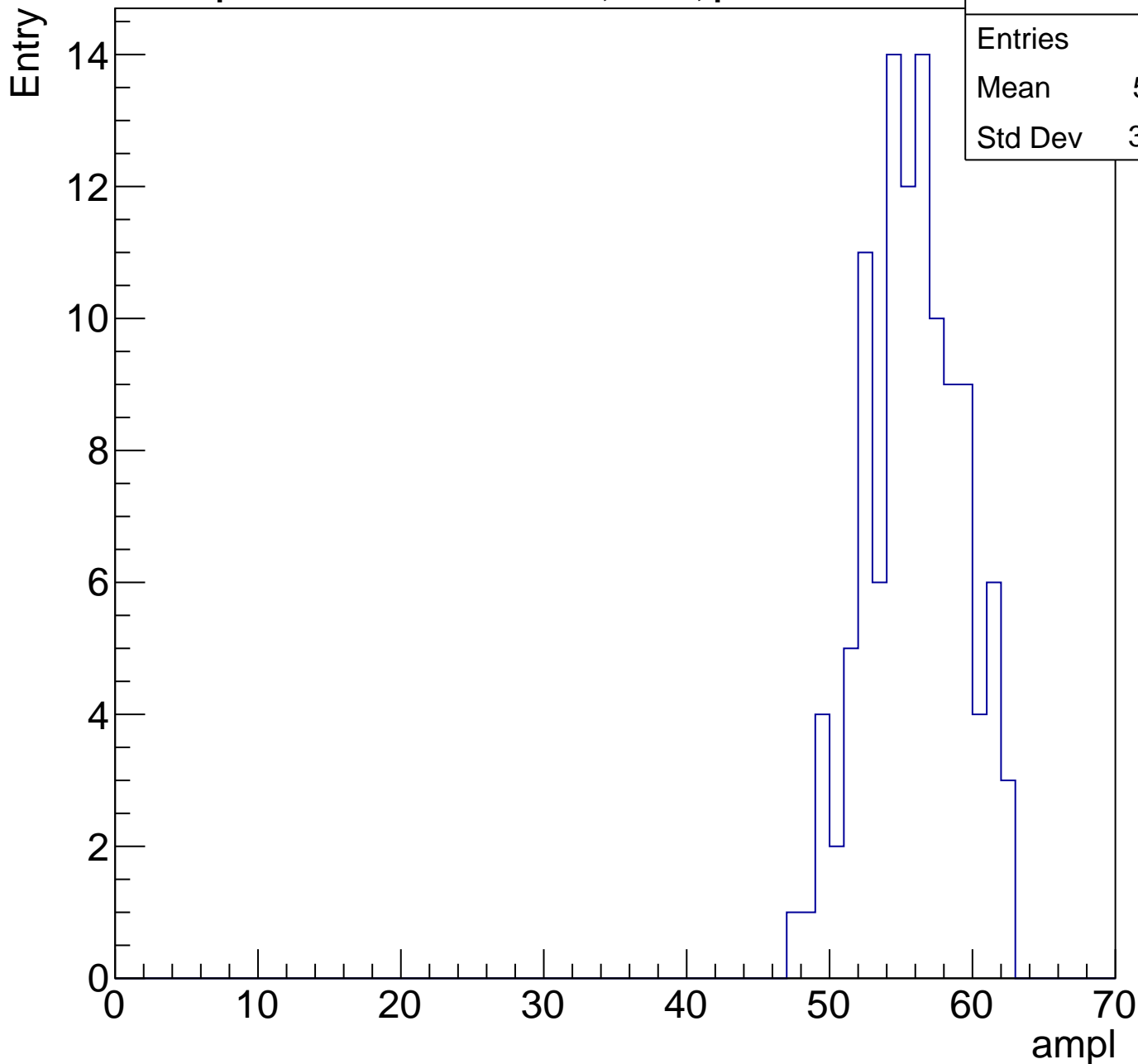
Entries	143
Mean	49.29
Std Dev	3.685



B0L101S, U19-ch115, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	55.41
Std Dev	3.352



B0L101S, U19-ch115, adc5

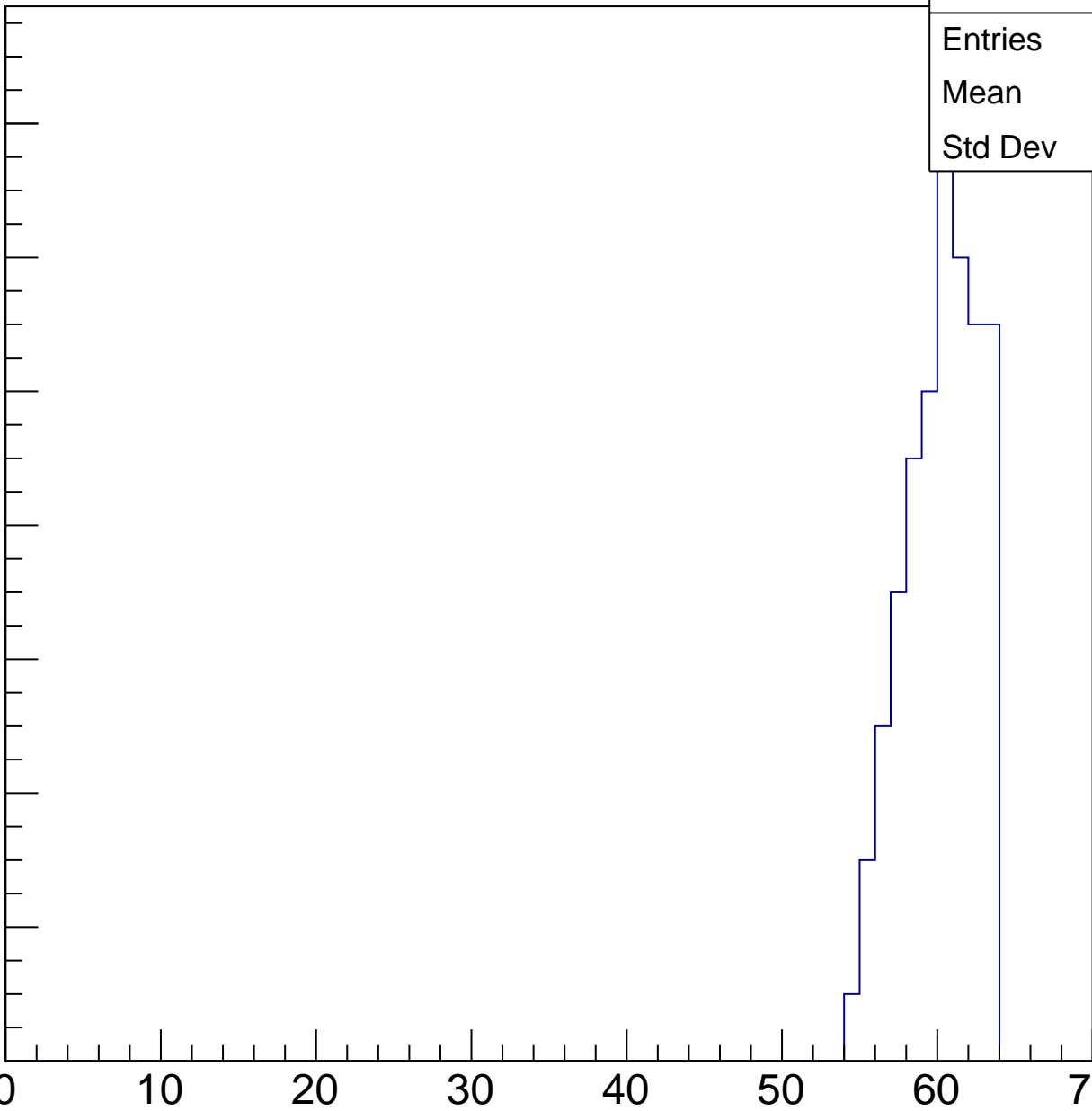
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	84
Mean	59.73
Std Dev	2.311

ampl

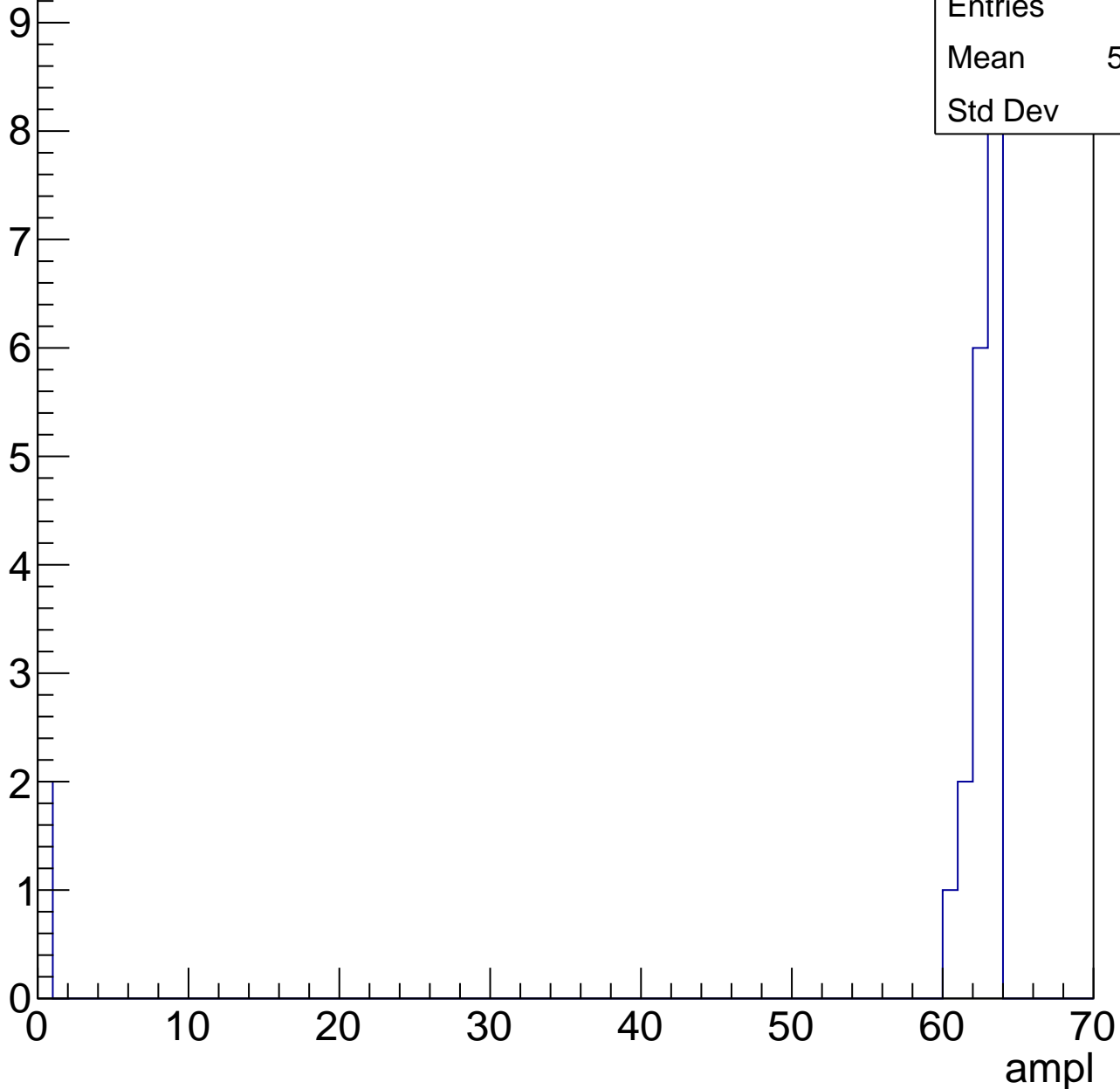


B0L101S, U19-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	56.05
Std Dev	18.7



B0L101S, U19-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



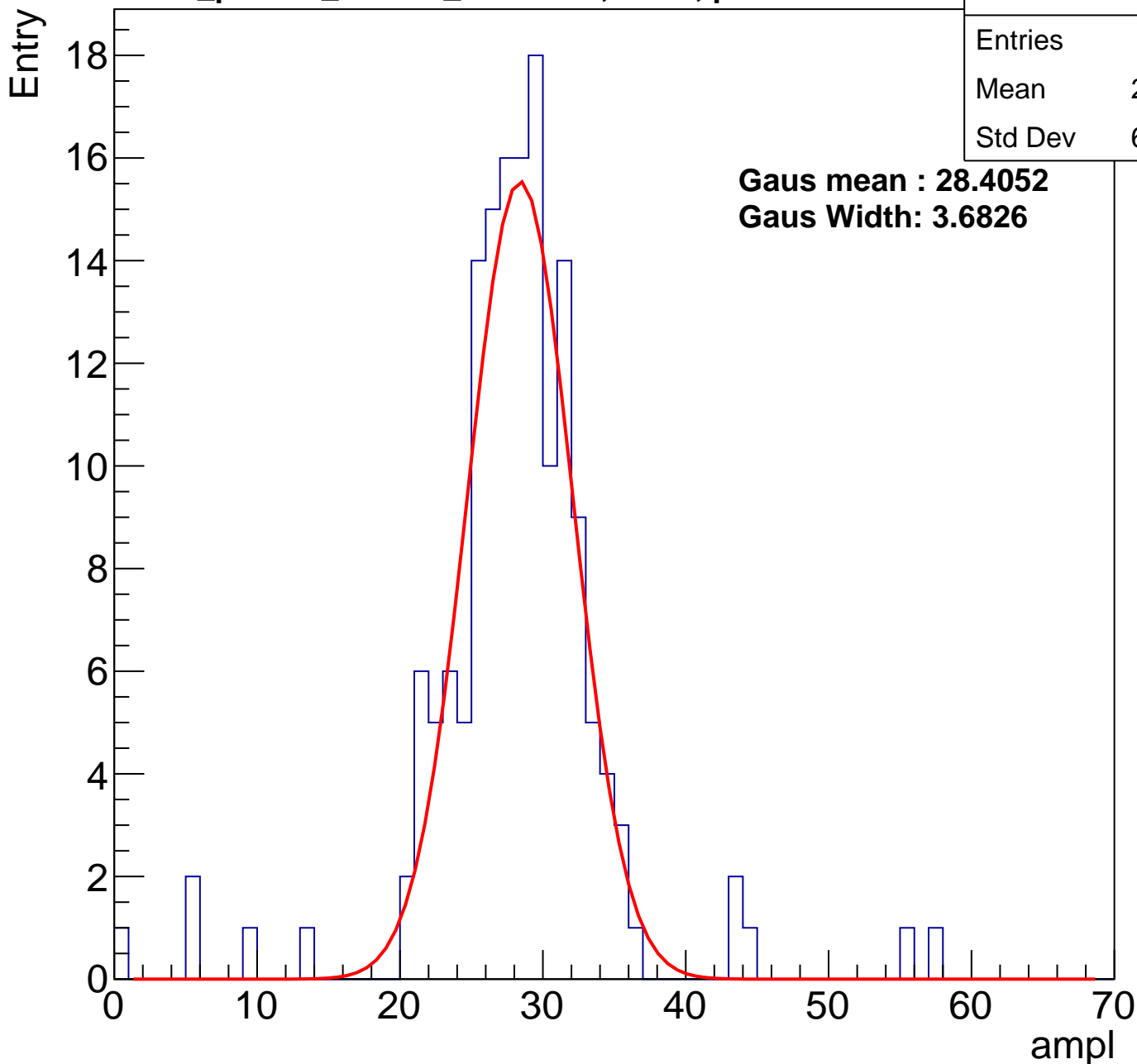
B0L101S, U19-ch116, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	27.76
Std Dev	6.412

Gaus mean : 28.4052

Gaus Width: 3.6826



B0L101S, U19-ch116, adc1

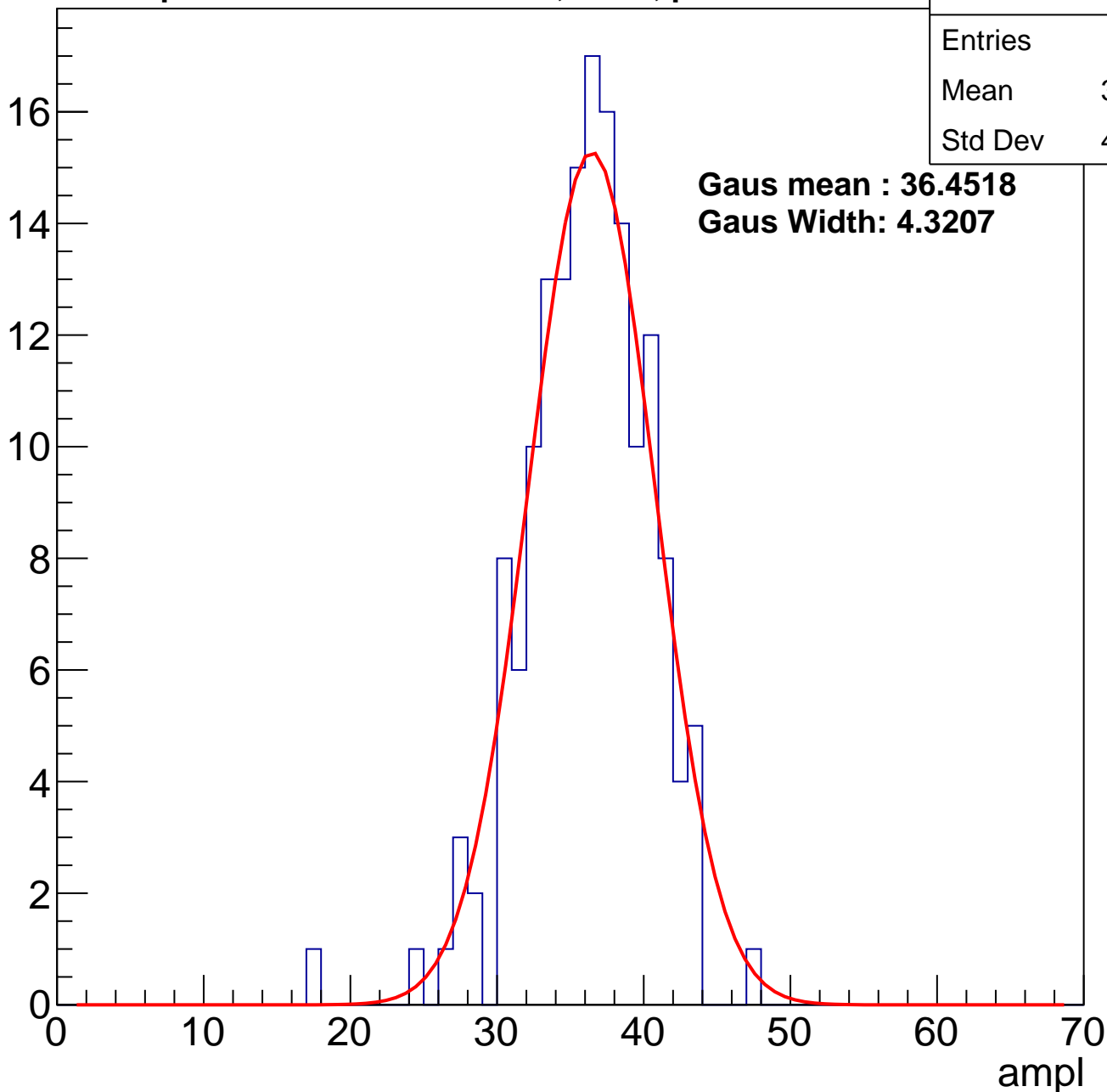
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	35.69
Std Dev	4.188

Gaus mean : 36.4518

Gaus Width: 4.3207

Entry



B0L101S, U19-ch116, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

114

Mean

42.08

Std Dev

3.623

Gaus mean : 43.0579

Gaus Width: 3.7196

0

10

20

30

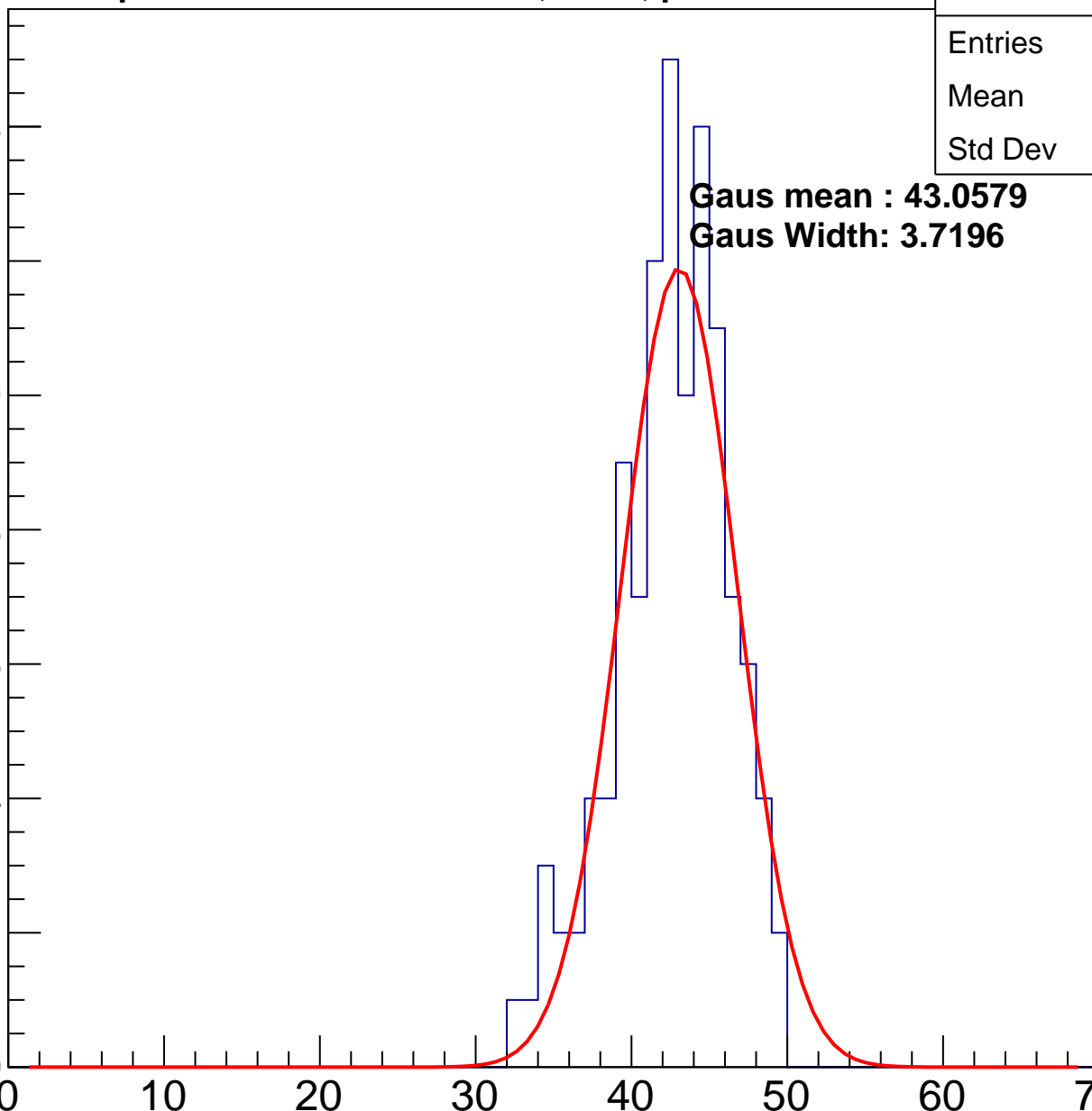
40

50

60

70

ampl



B0L101S, U19-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	48.61
Std Dev	3.708

Entry

12

10

8

6

4

2

0

0

10

20

30

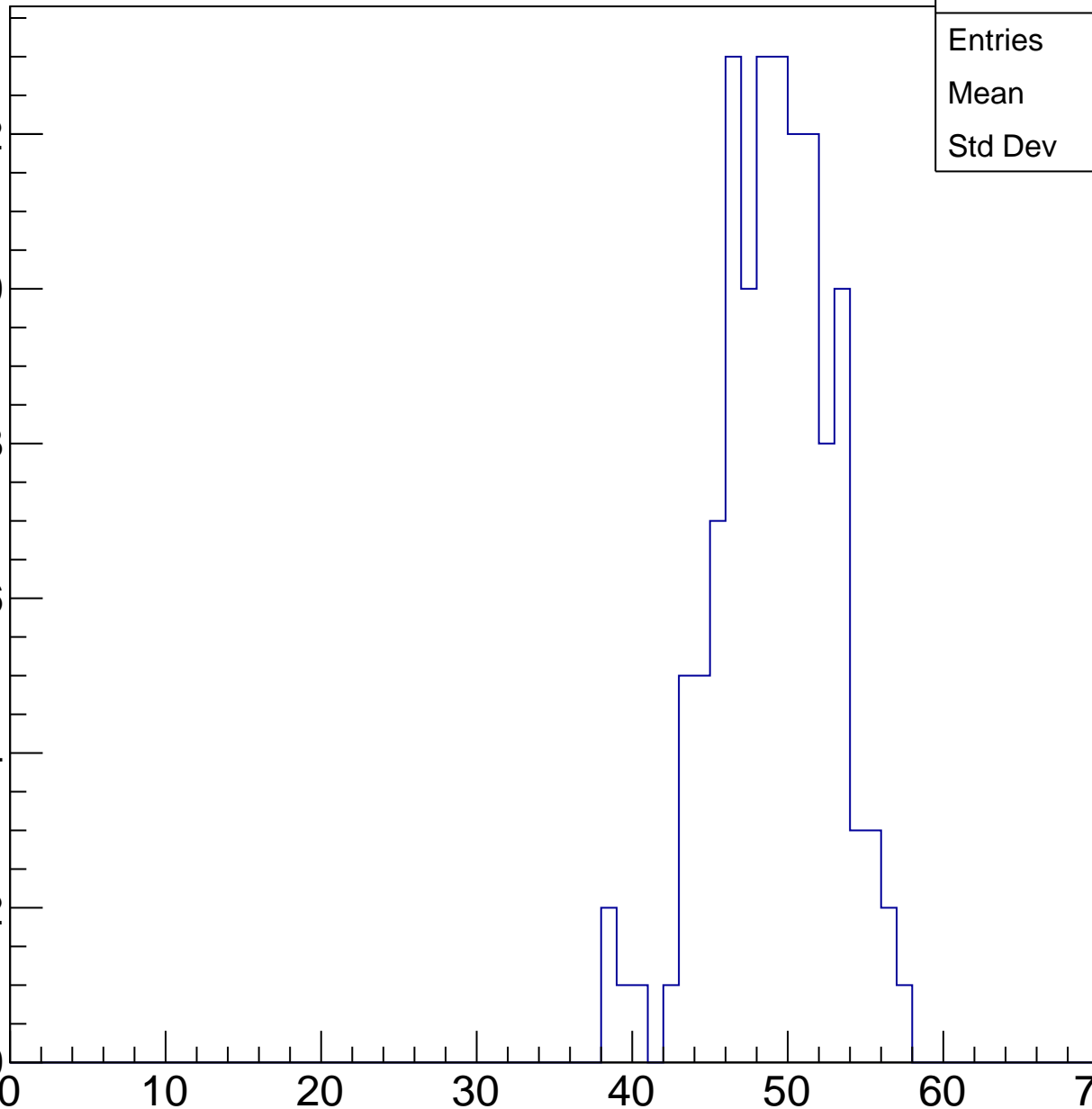
40

50

60

70

ampl



B0L101S, U19-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

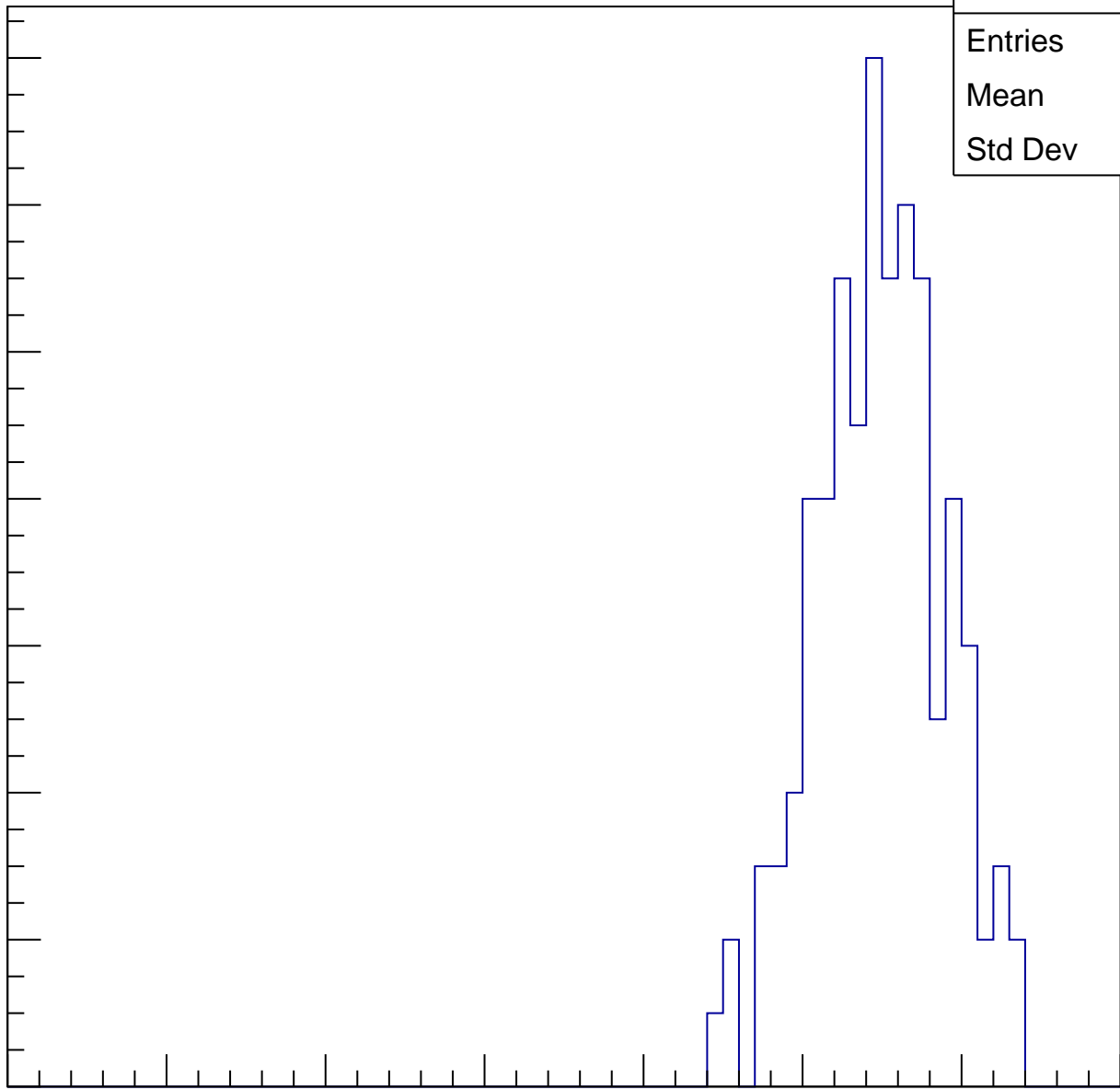
Entries	123
Mean	54.37
Std Dev	3.991

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U19-ch116, adc5

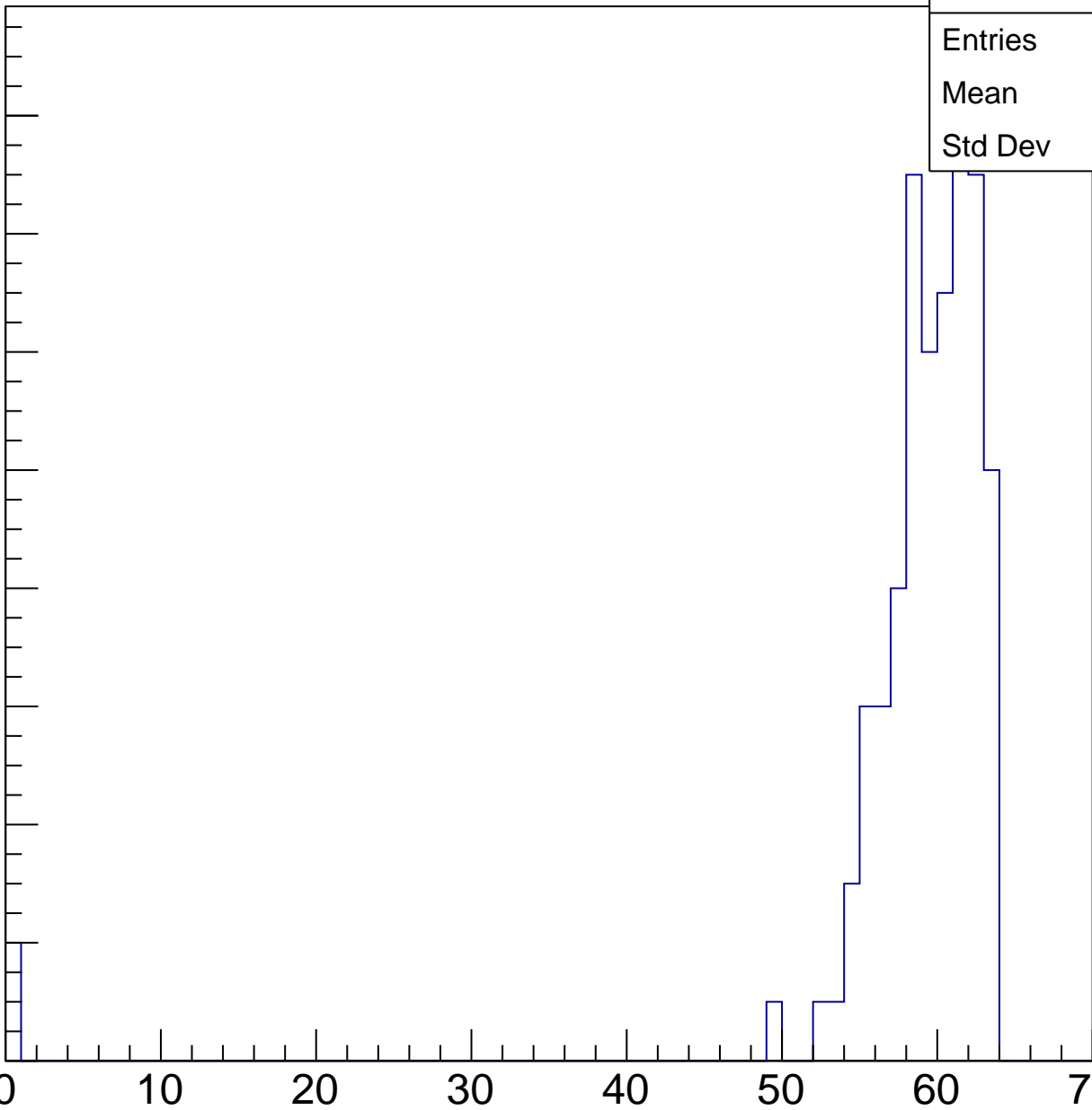
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.12
Std Dev	8.372

ampl

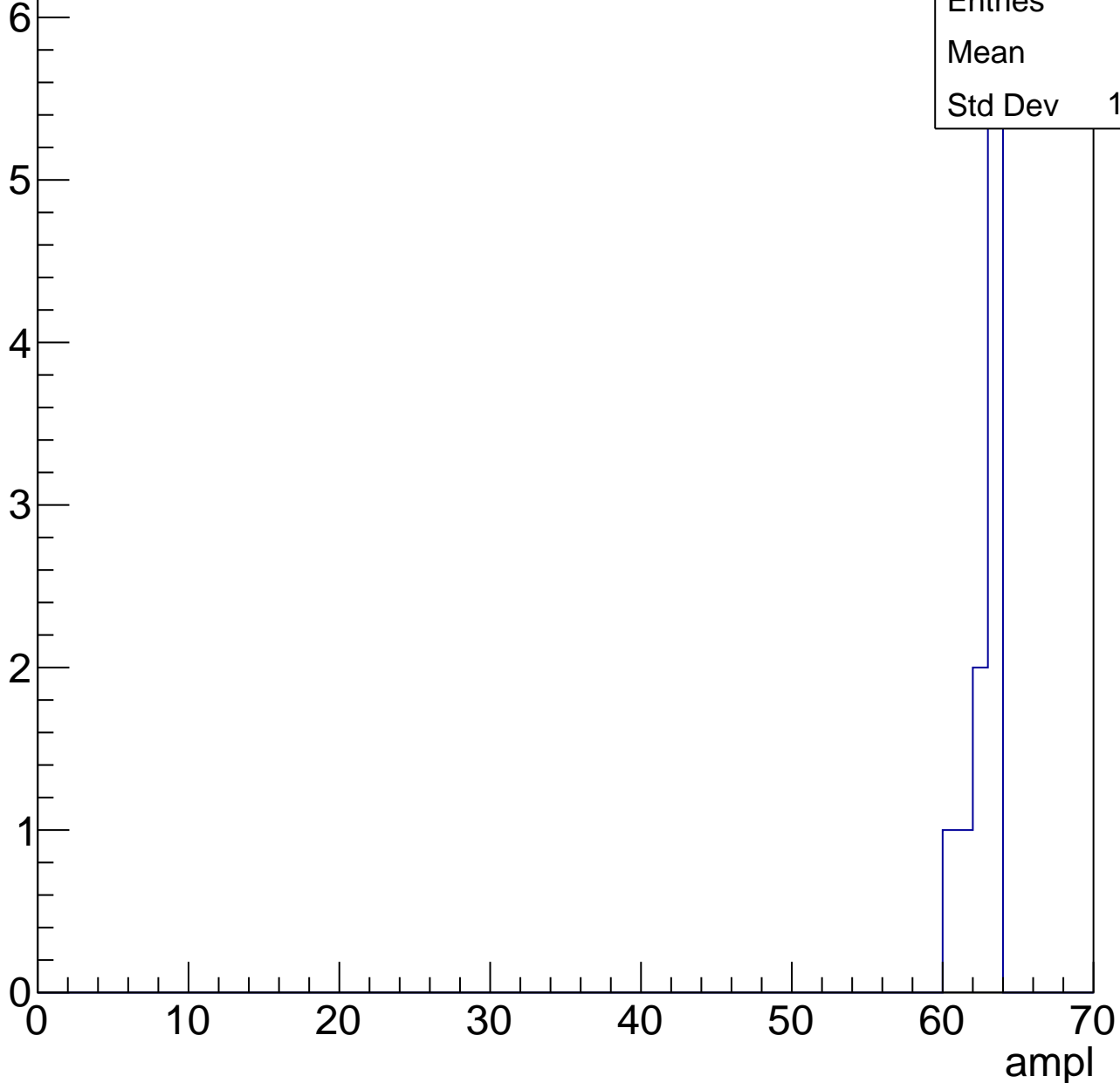


B0L101S, U19-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.3
Std Dev	1.005



B0L101S, U19-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L101S, U19-ch117, adc0

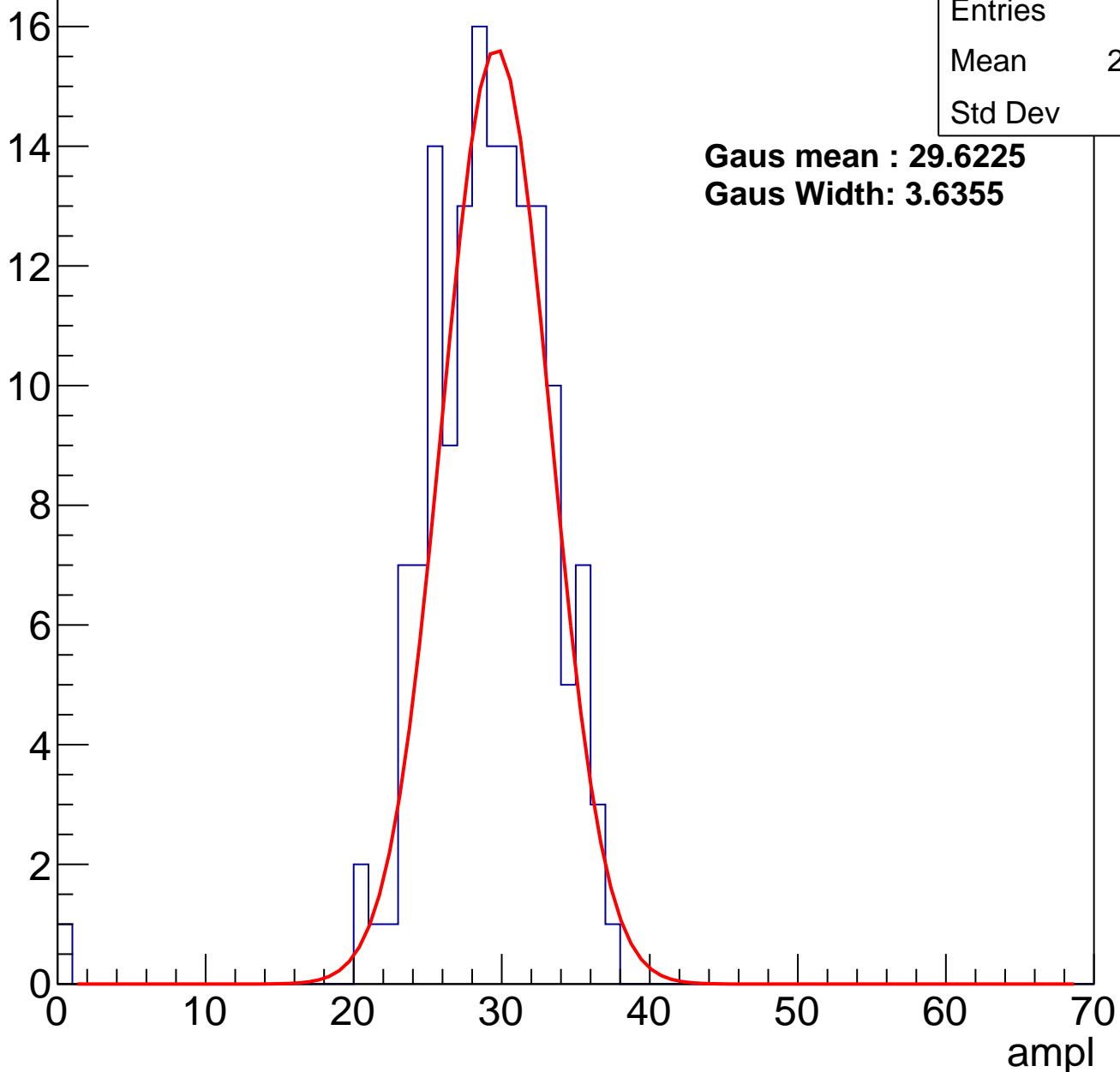
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	28.68
Std Dev	4.33

Gaus mean : 29.6225

Gaus Width: 3.6355

Entry



B0L101S, U19-ch117, adc1

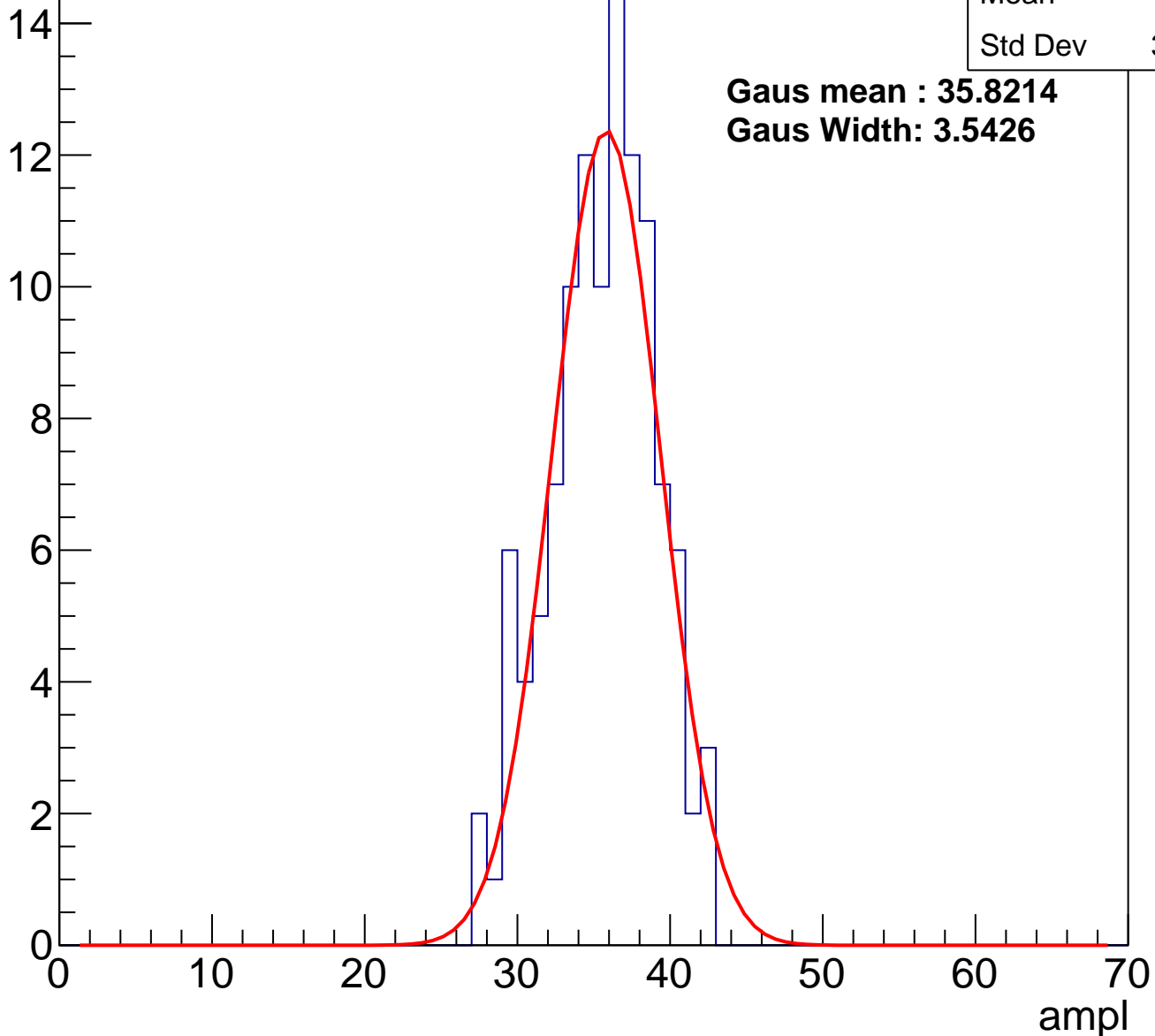
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	35.1
Std Dev	3.431

Gaus mean : 35.8214

Gaus Width: 3.5426

Entry



B0L101S, U19-ch117, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

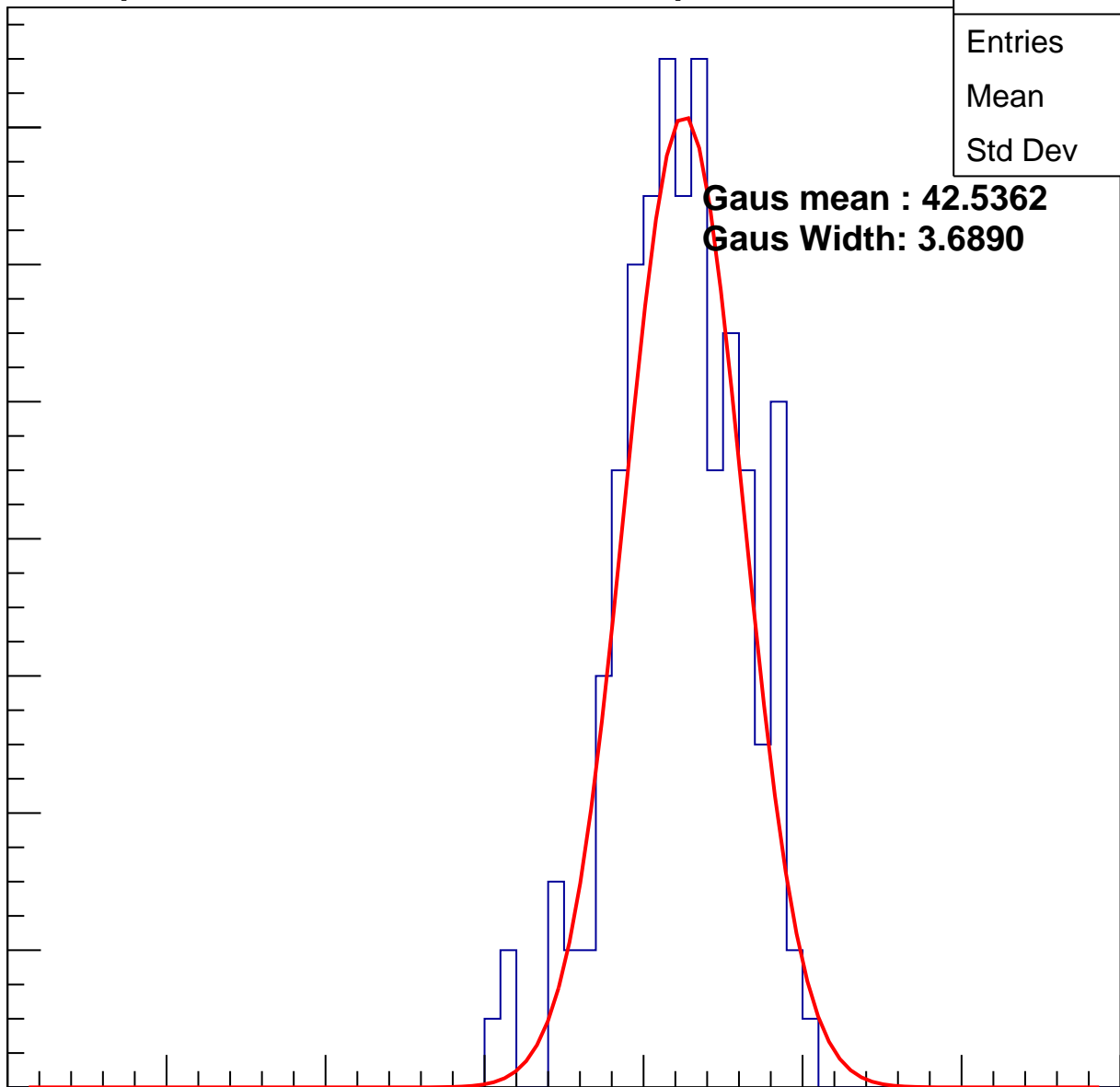
70

ampl

Entries	140
Mean	41.87
Std Dev	3.924

Gaus mean : 42.5362

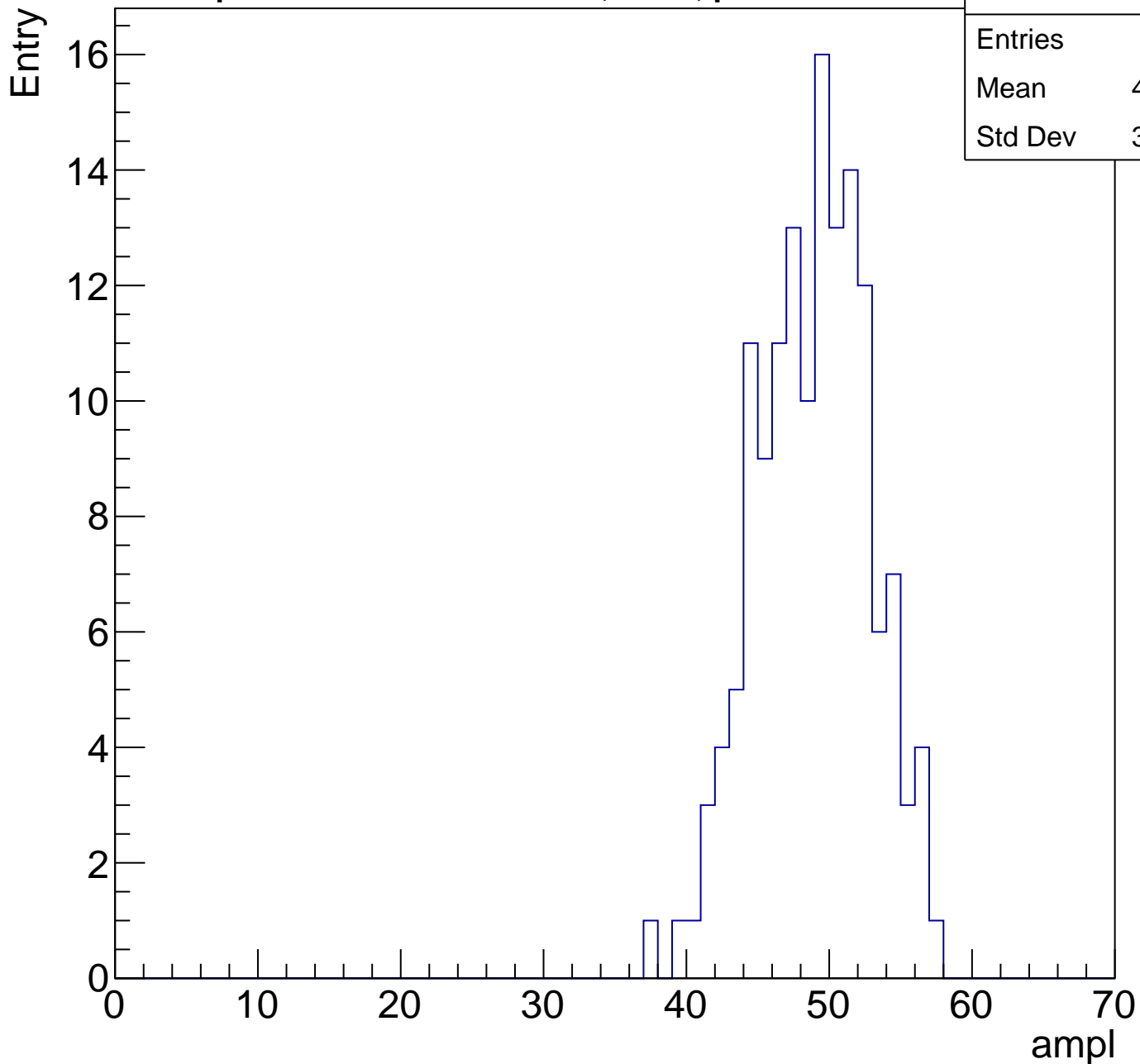
Gaus Width: 3.6890



B0L101S, U19-ch117, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	48.43
Std Dev	3.932



B0L101S, U19-ch117, adc4

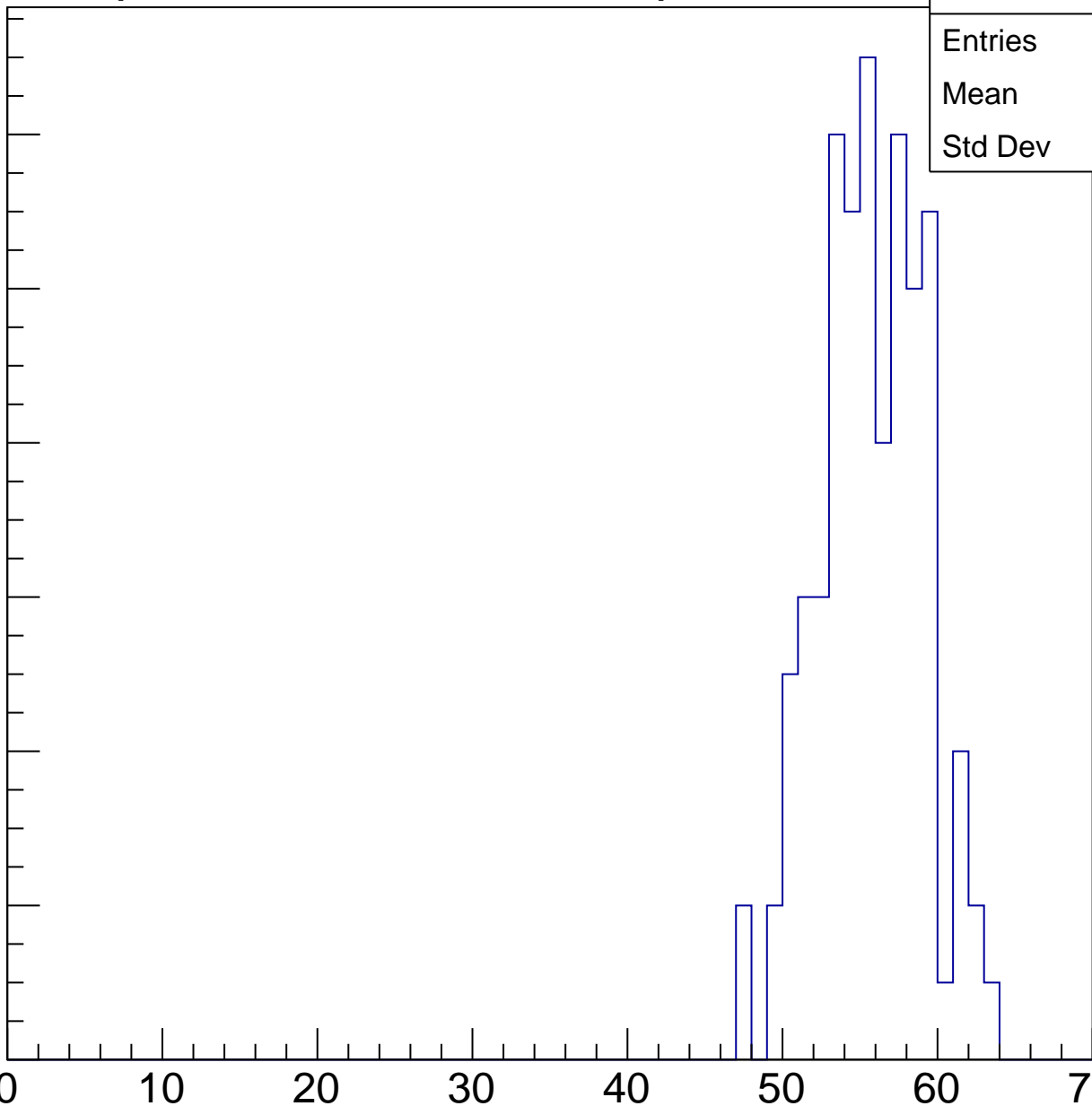
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	106
Mean	55.25
Std Dev	3.337

ampl



B0L101S, U19-ch117, adc5

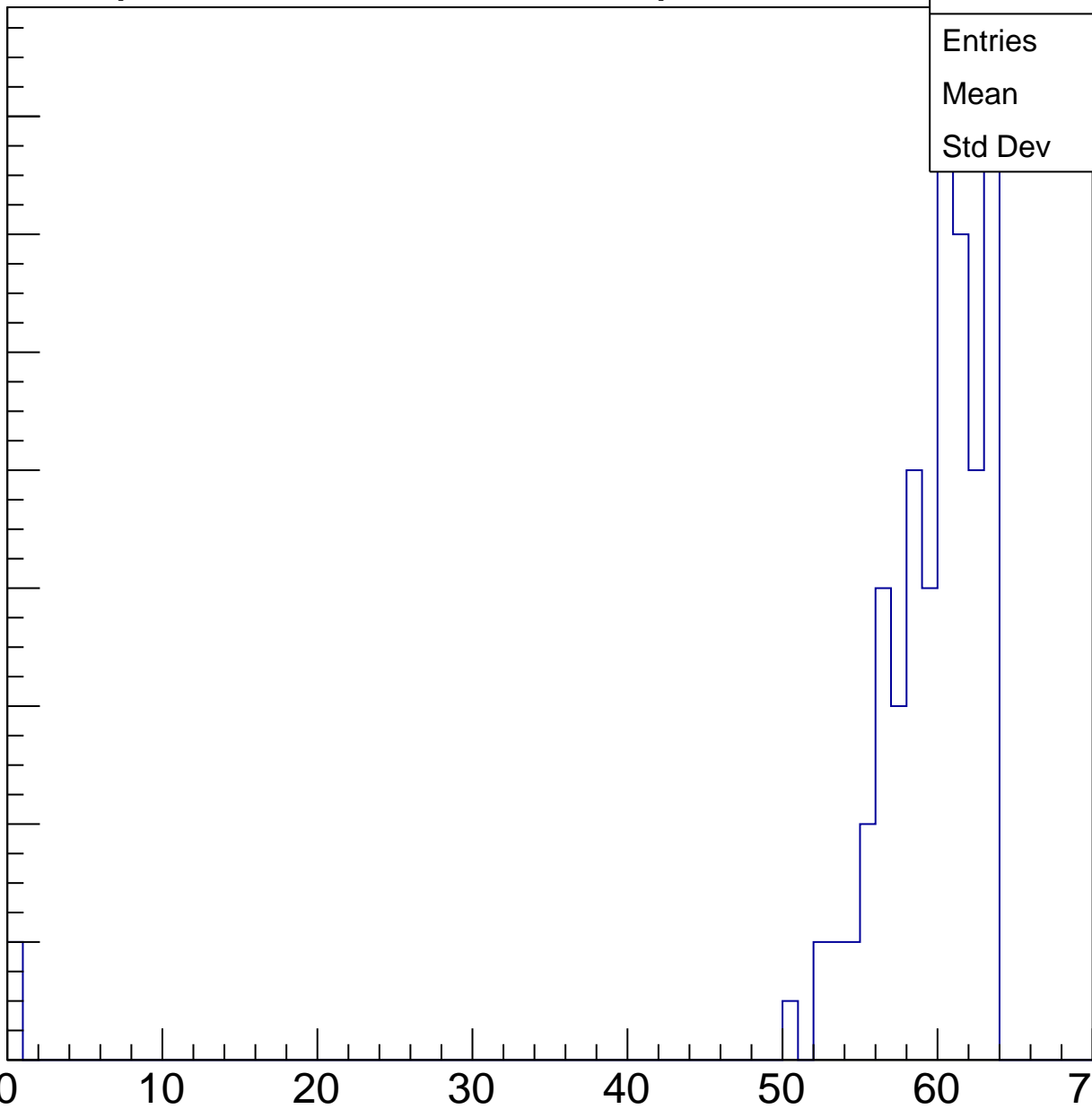
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.2
Std Dev	8.704

ampl

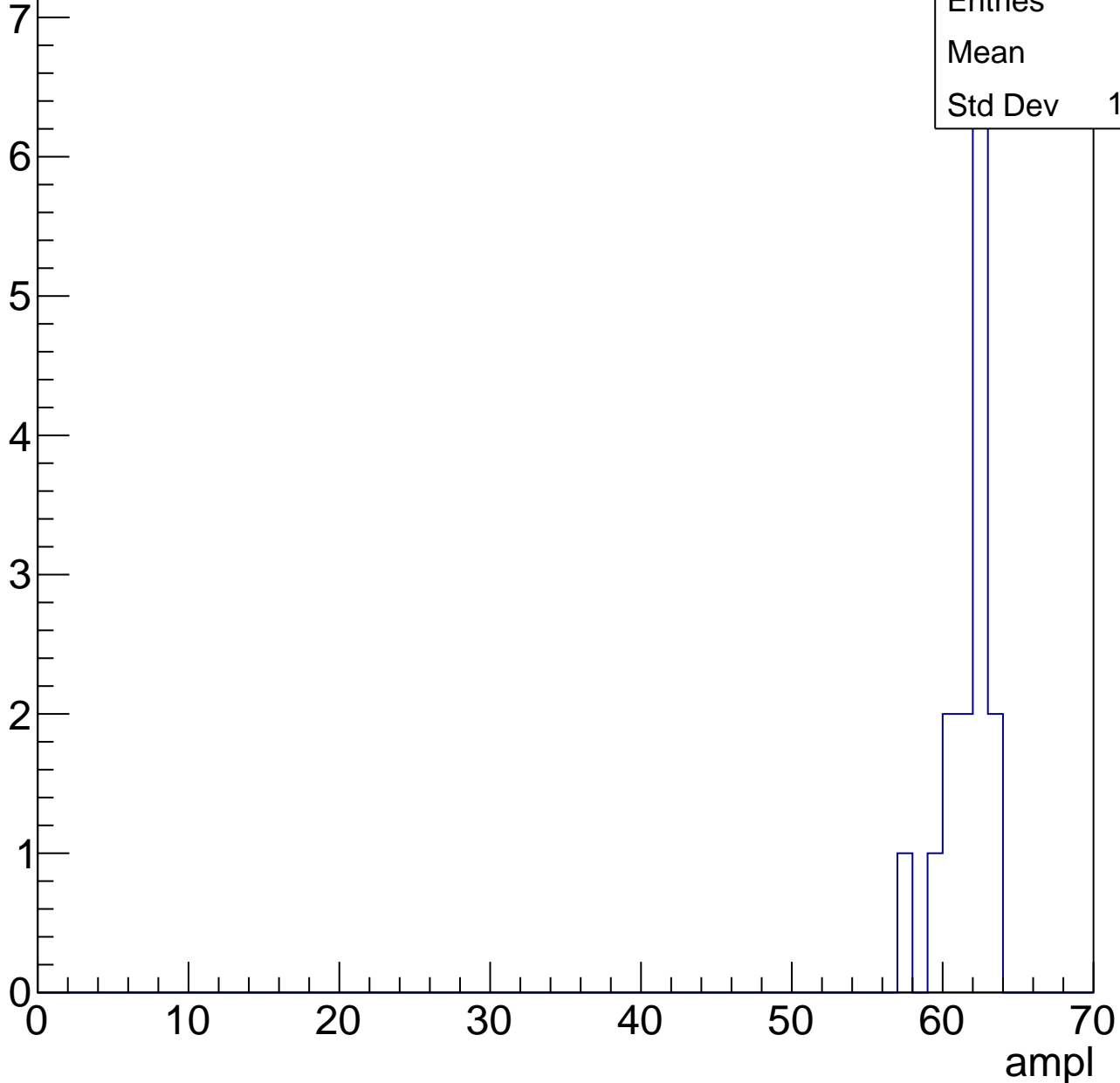


B0L101S, U19-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

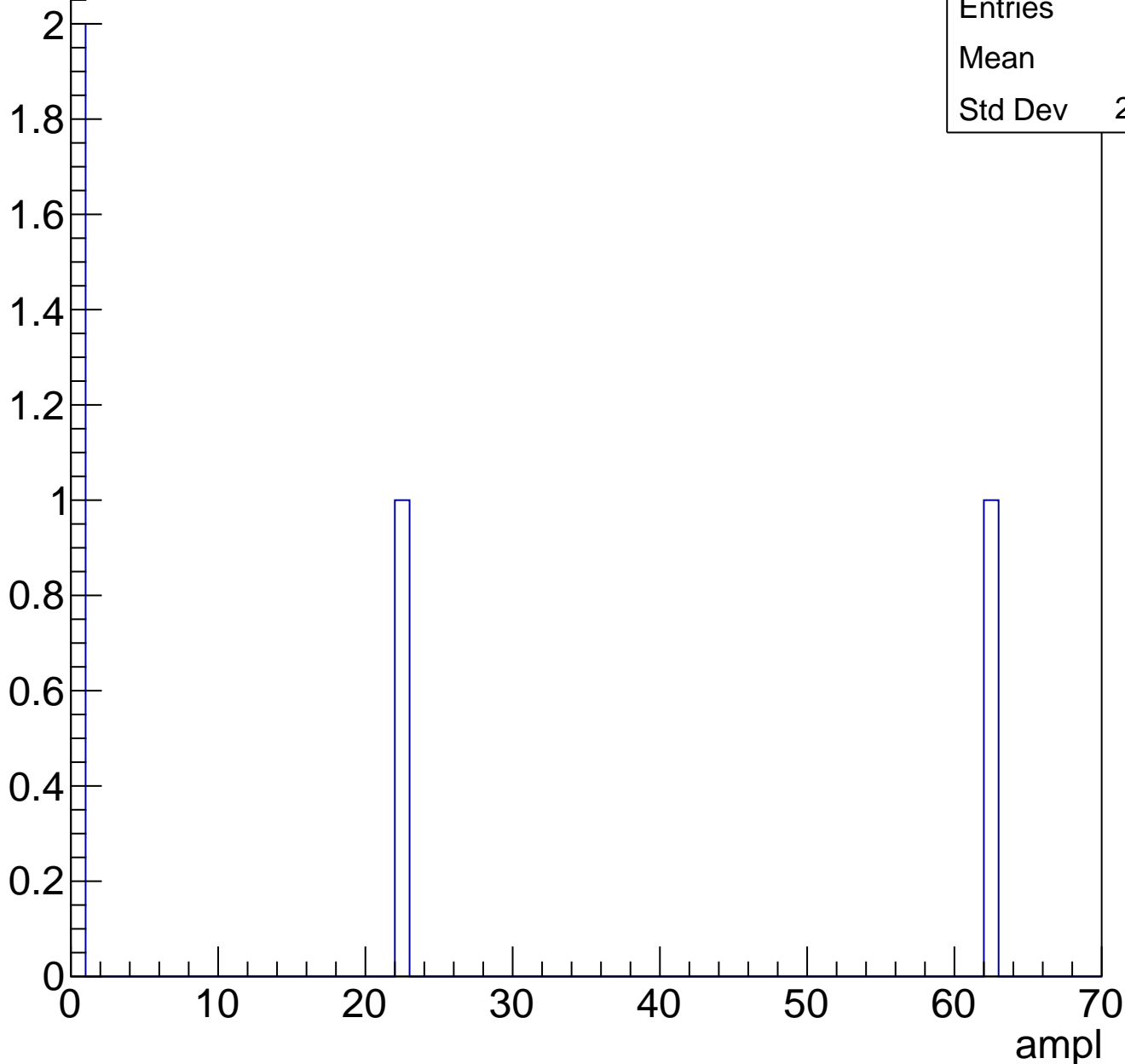
Entries	15
Mean	61.2
Std Dev	1.558



B0L101S, U19-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	21
Std Dev	25.32

B0L101S, U19-ch118, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	25.91
Std Dev	5.758

Gaus mean : 26.7841

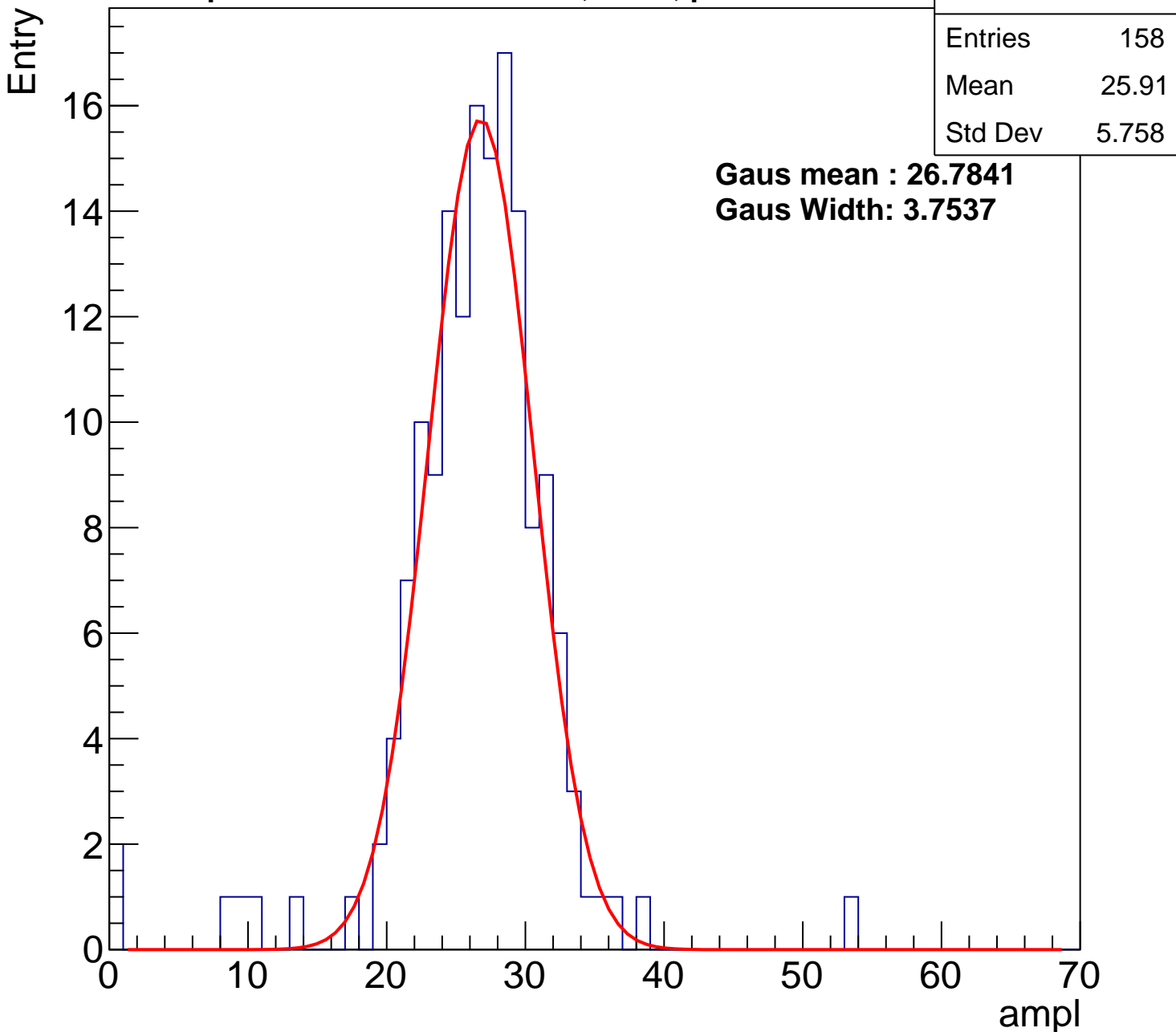
Gaus Width: 3.7537

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

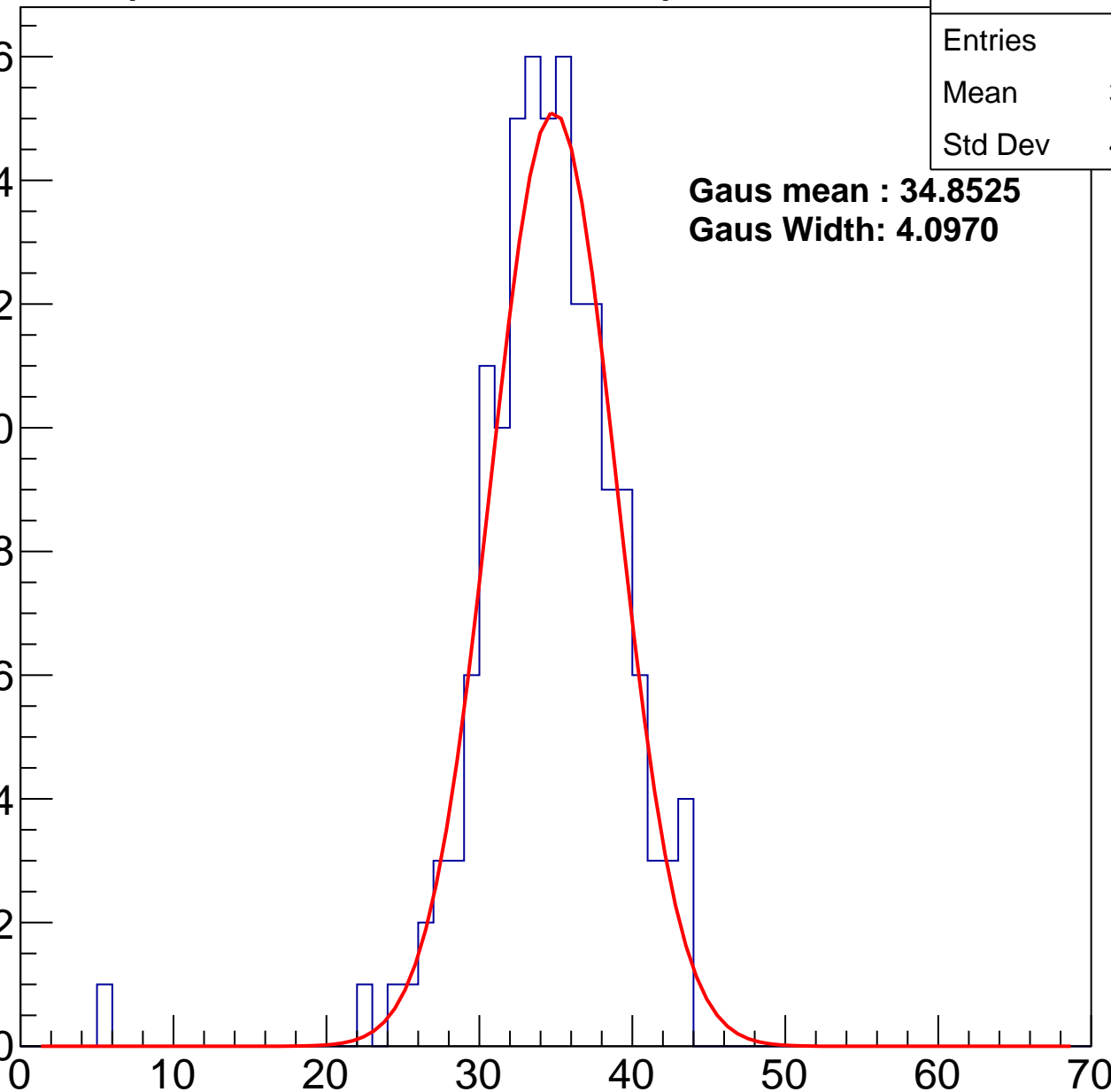
Entry

16
14
12
10
8
6
4
2
0

Entries	159
Mean	34.06
Std Dev	4.665

Gaus mean : 34.8525
Gaus Width: 4.0970

ampl



B0L101S, U19-ch118, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

122

Mean

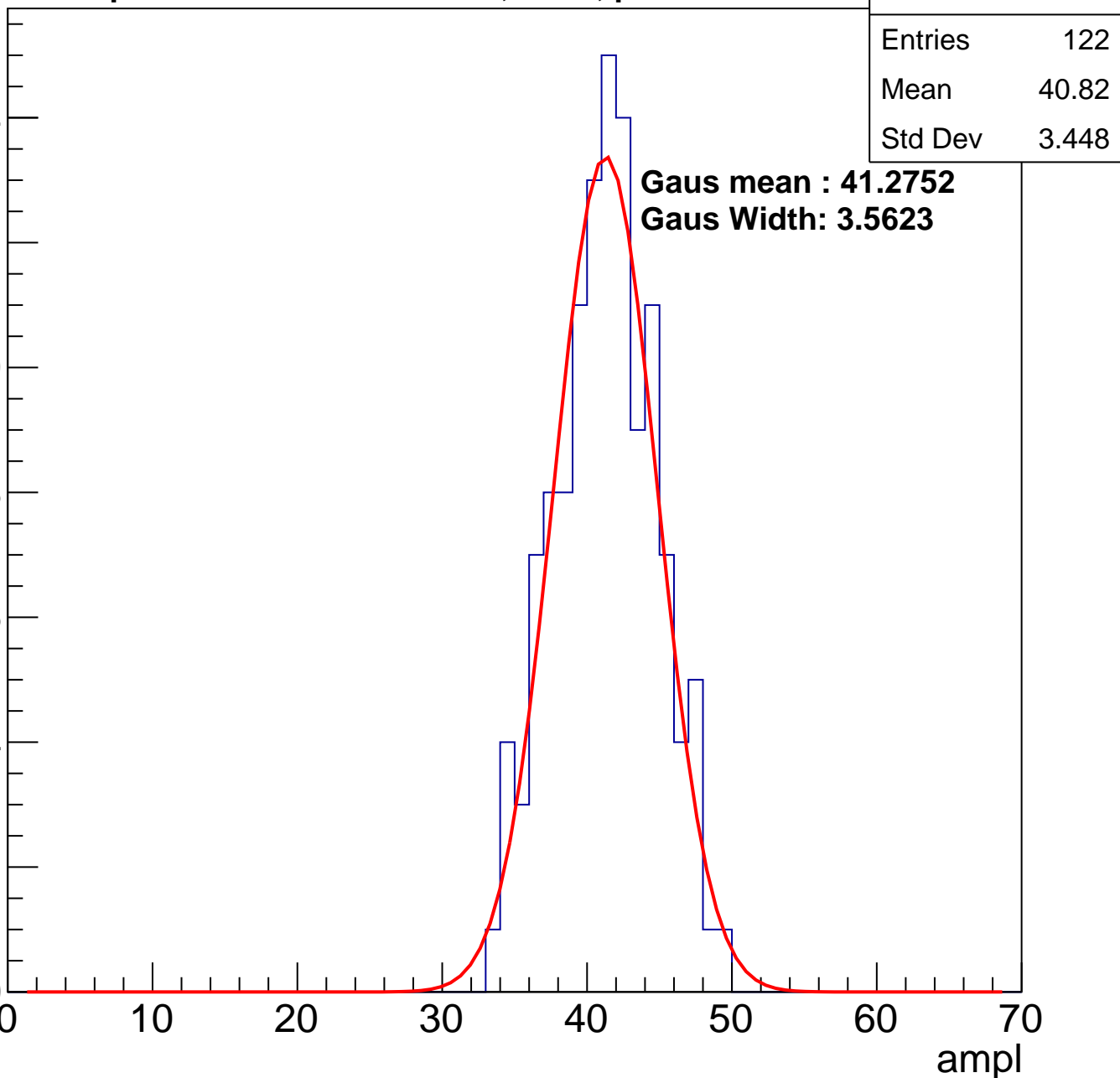
40.82

Std Dev

3.448

Gaus mean : 41.2752

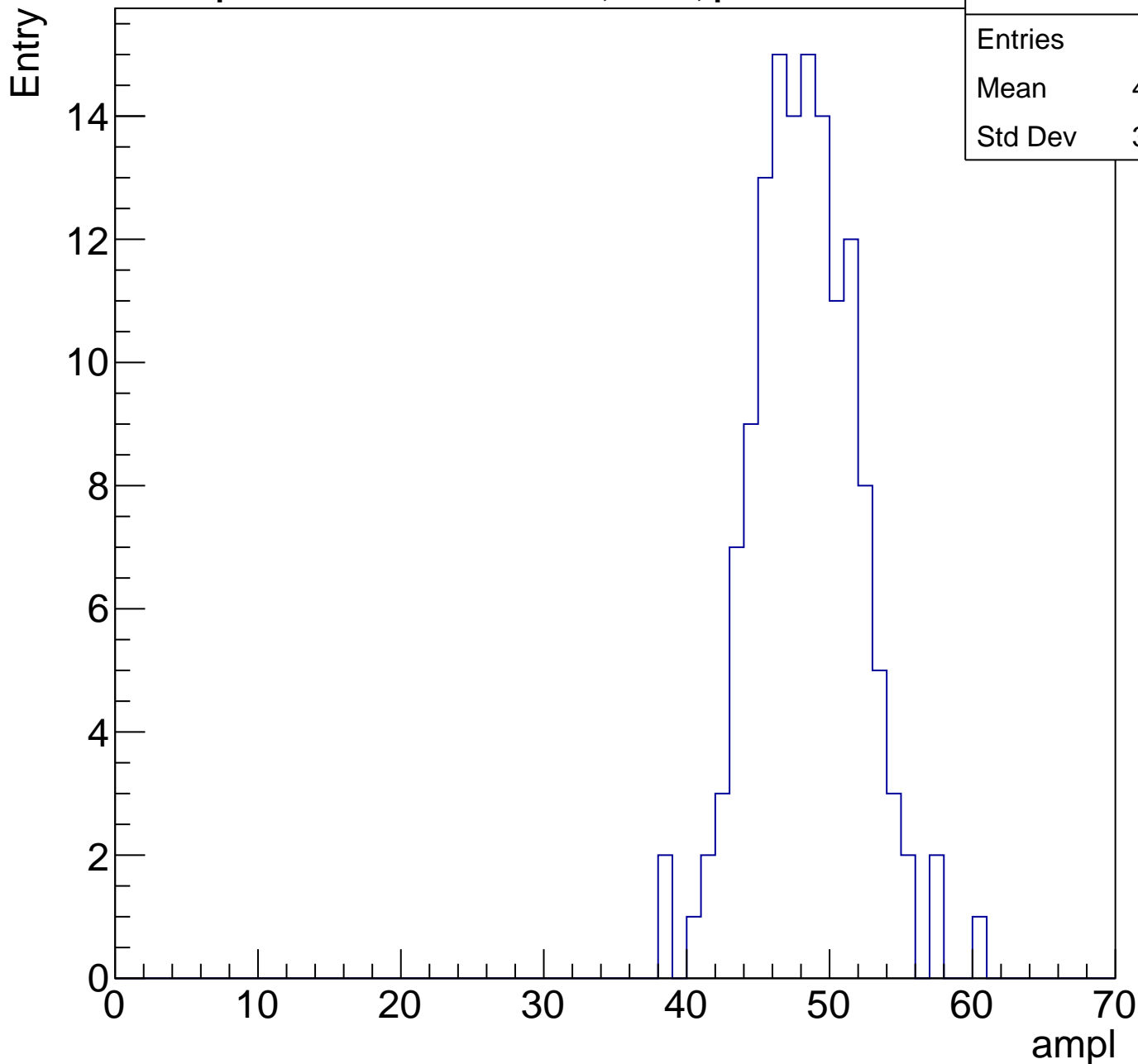
Gaus Width: 3.5623



B0L101S, U19-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	47.83
Std Dev	3.704



B0L101S, U19-ch118, adc4

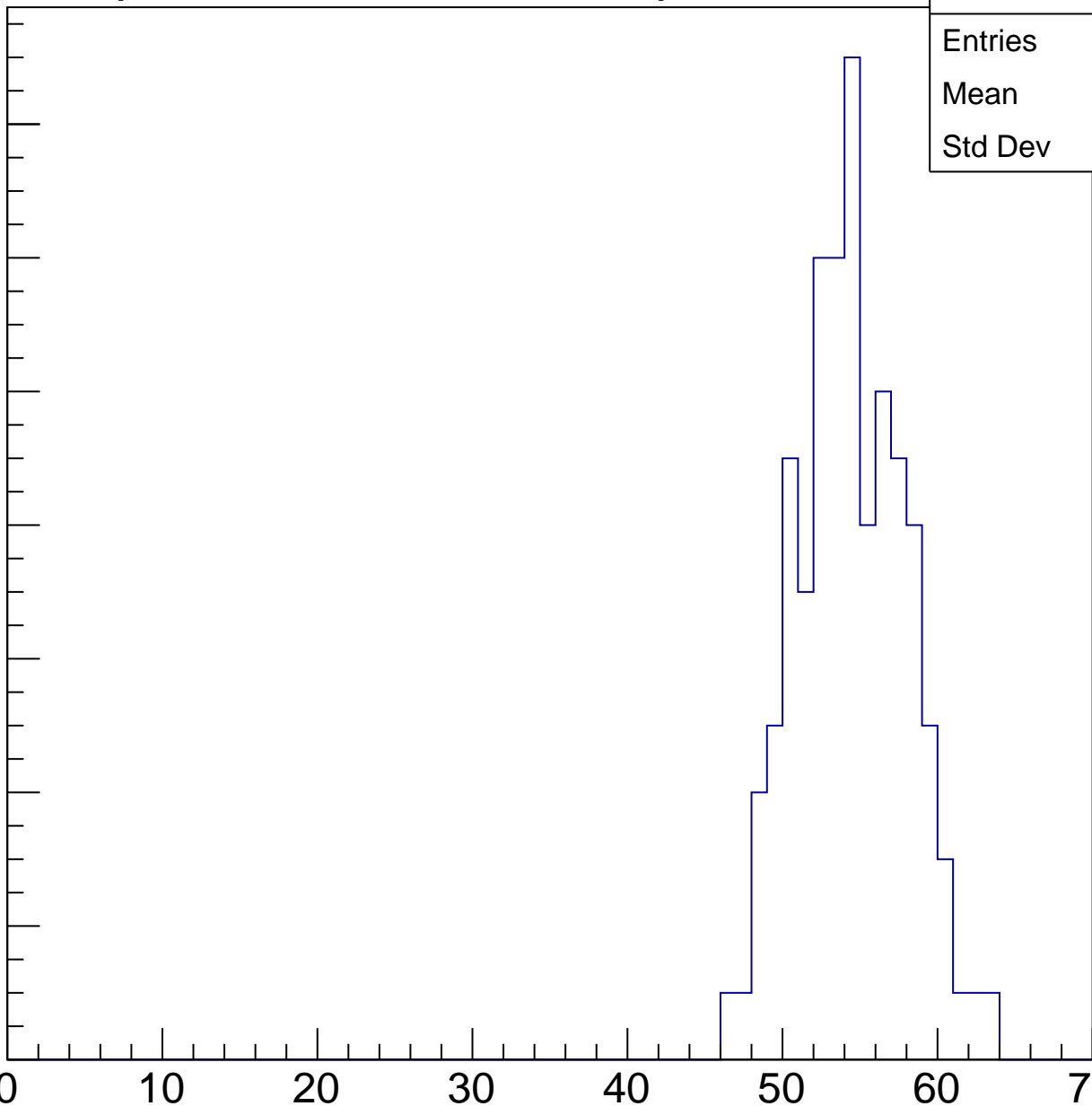
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	112
Mean	53.97
Std Dev	3.45

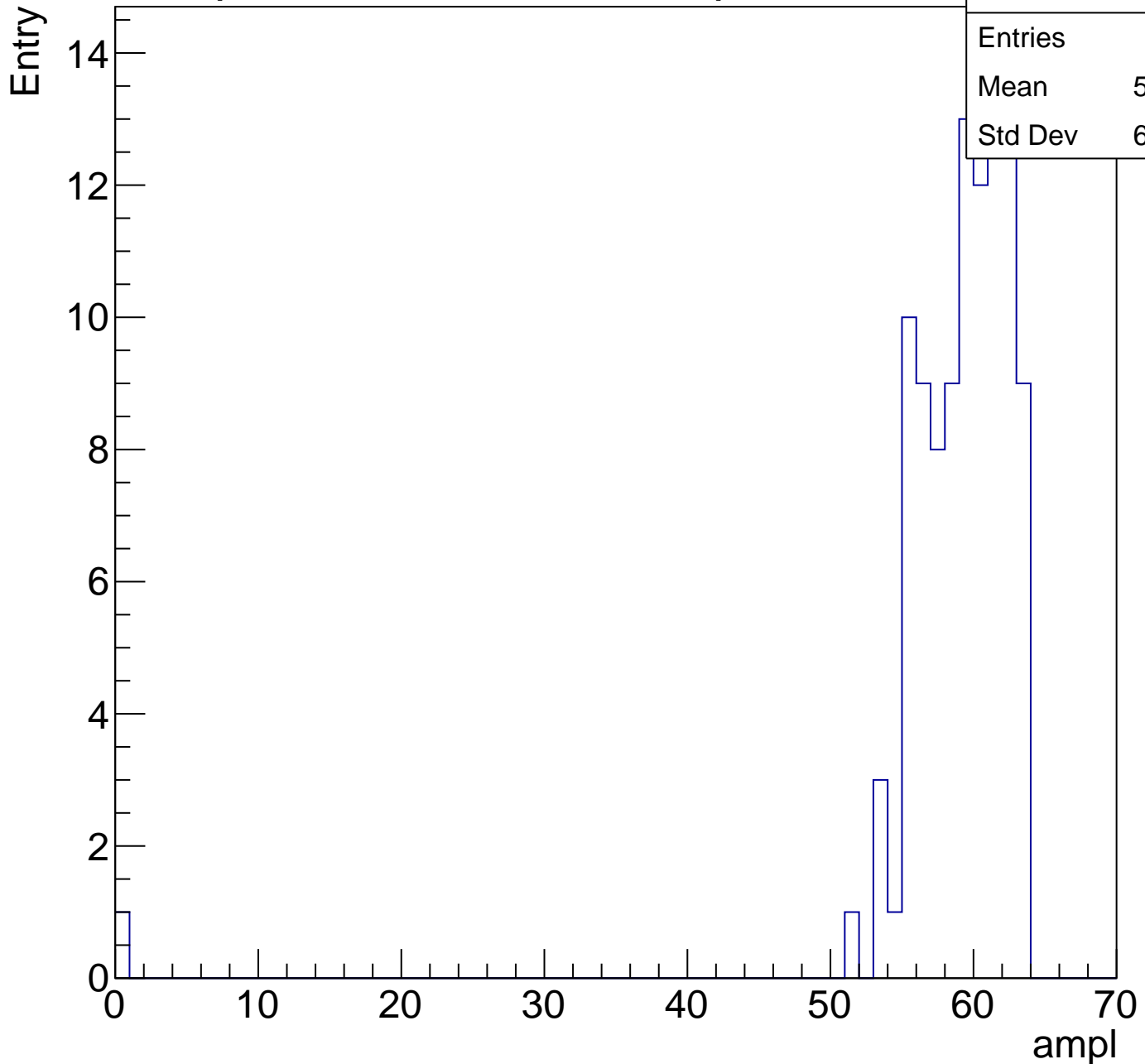
ampl



B0L101S, U19-ch118, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	58.36
Std Dev	6.425



B0L101S, U19-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

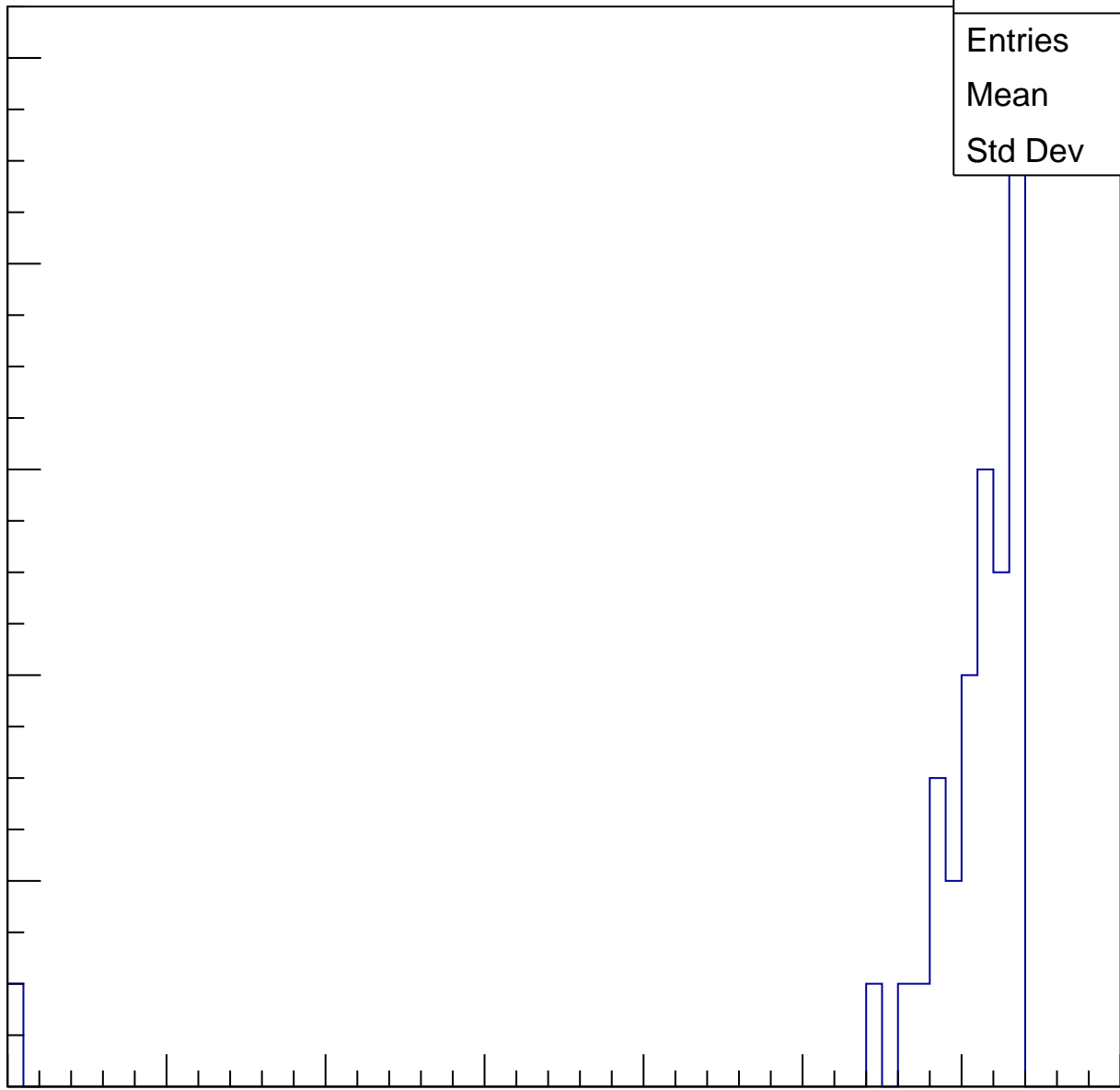
Entries	34
Mean	58.97
Std Dev	10.51

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U19-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U19-ch119, adc0

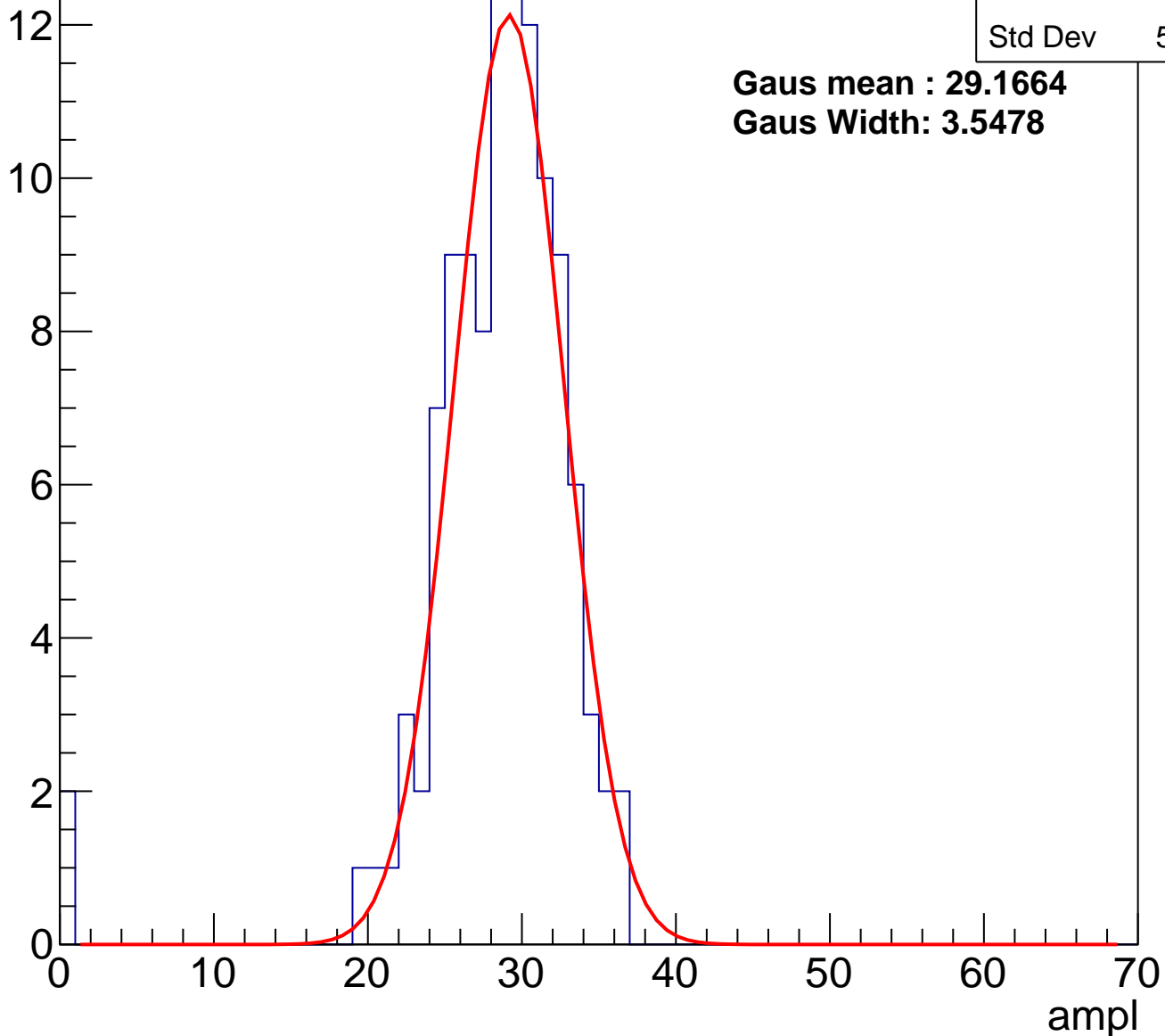
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	27.93
Std Dev	5.097

Gaus mean : 29.1664

Gaus Width: 3.5478

Entry



B0L101S, U19-ch119, adc1

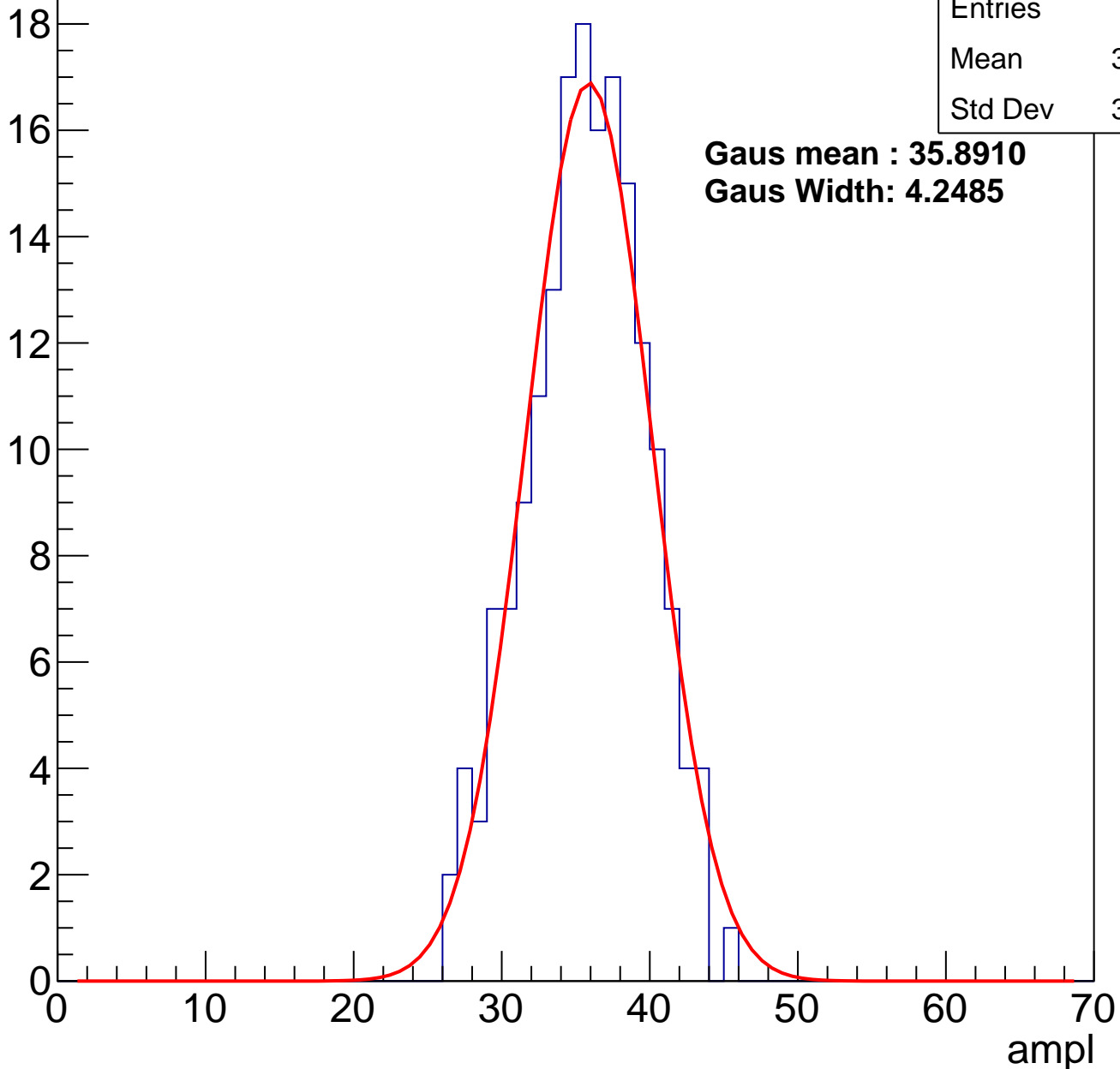
calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	35.25
Std Dev	3.947

Gaus mean : 35.8910

Gaus Width: 4.2485

Entry



B0L101S, U19-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	145
Mean	43.66
Std Dev	3.771

Gaus mean : 43.9320

Gaus Width: 3.7580

0

10

20

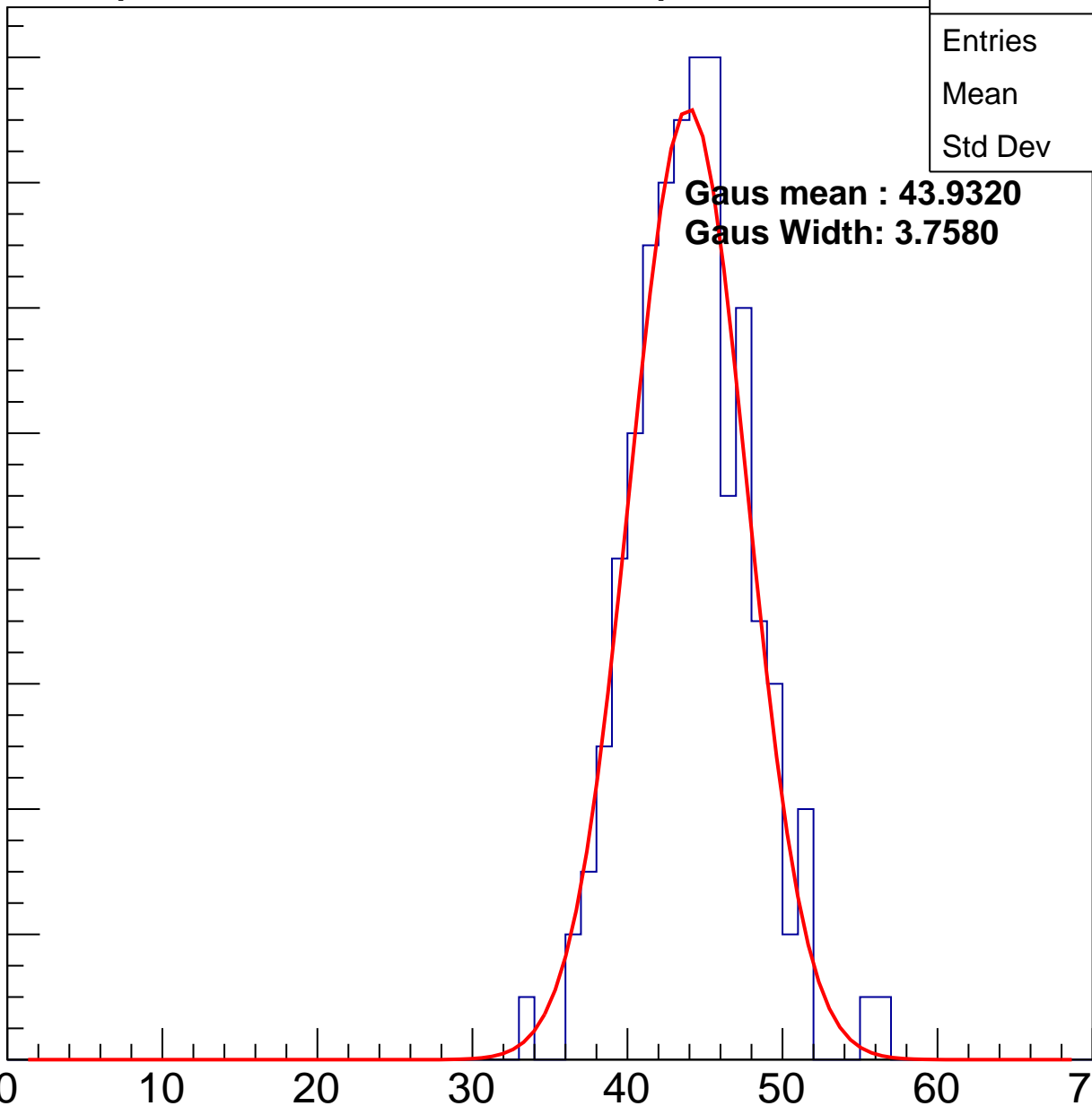
30

40

50

60

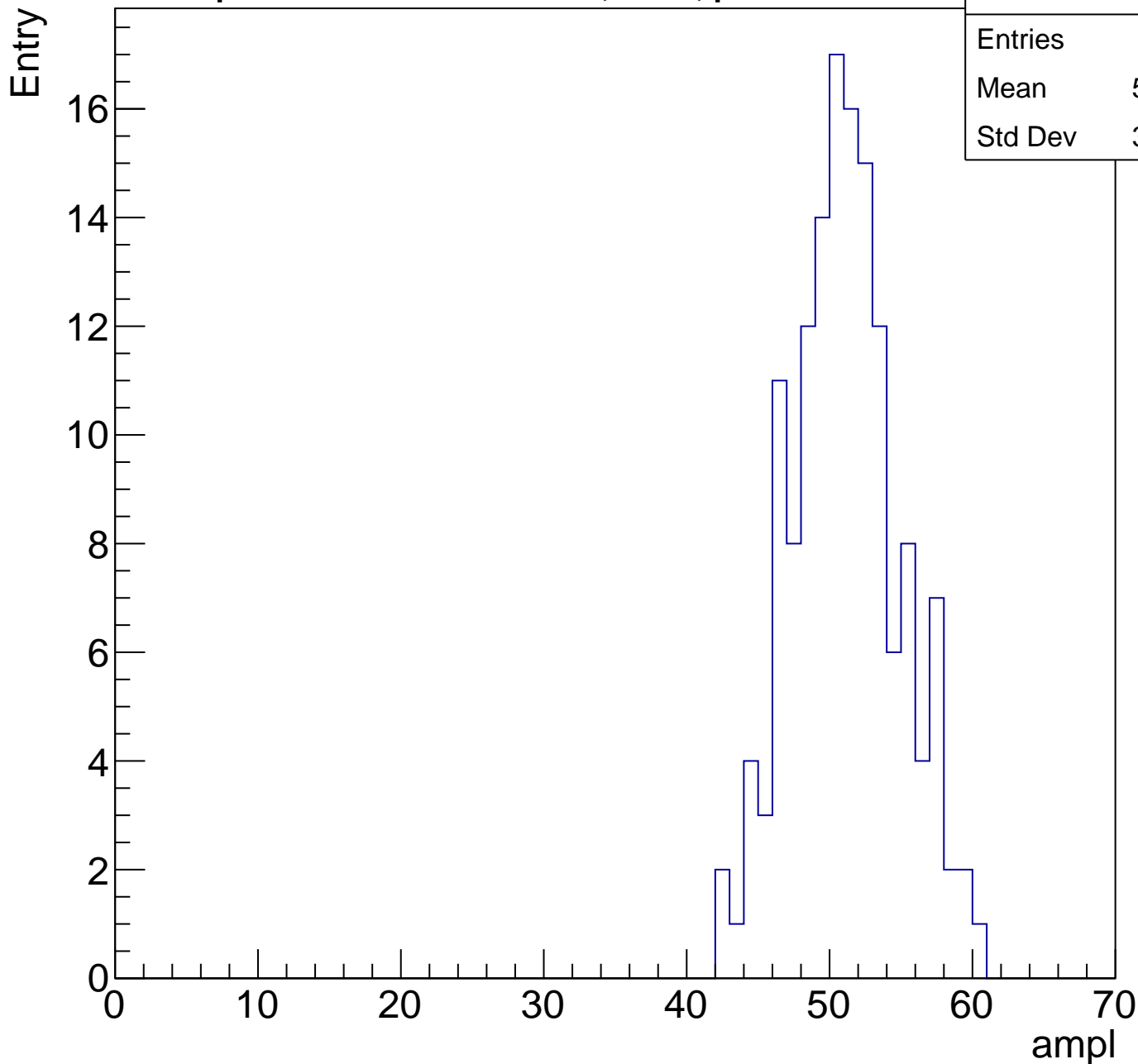
ampl



B0L101S, U19-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	50.66
Std Dev	3.715



B0L101S, U19-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

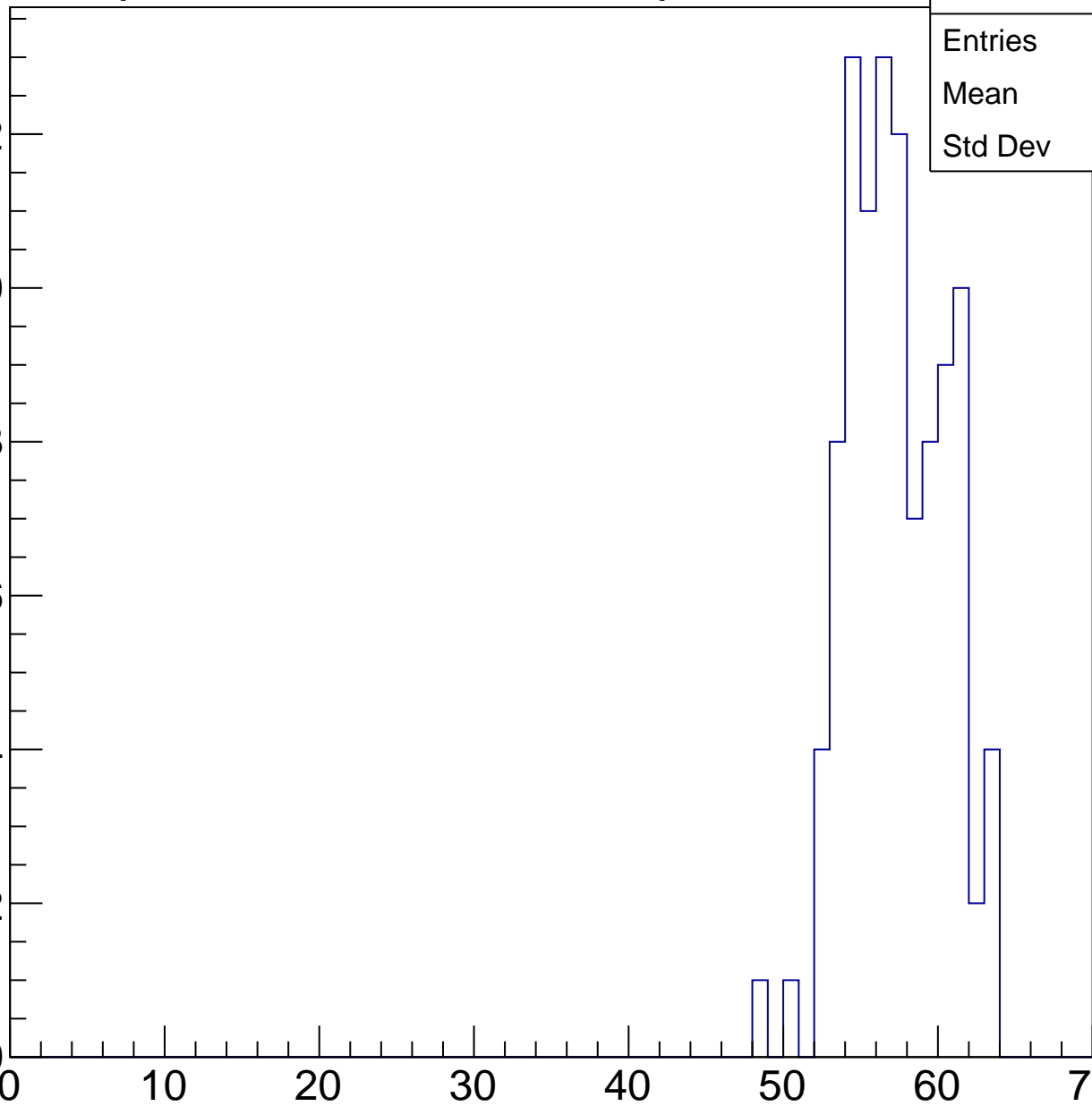
50

60

70

ampl

Entries	103
Mean	56.83
Std Dev	3.12



B0L101S, U19-ch119, adc5

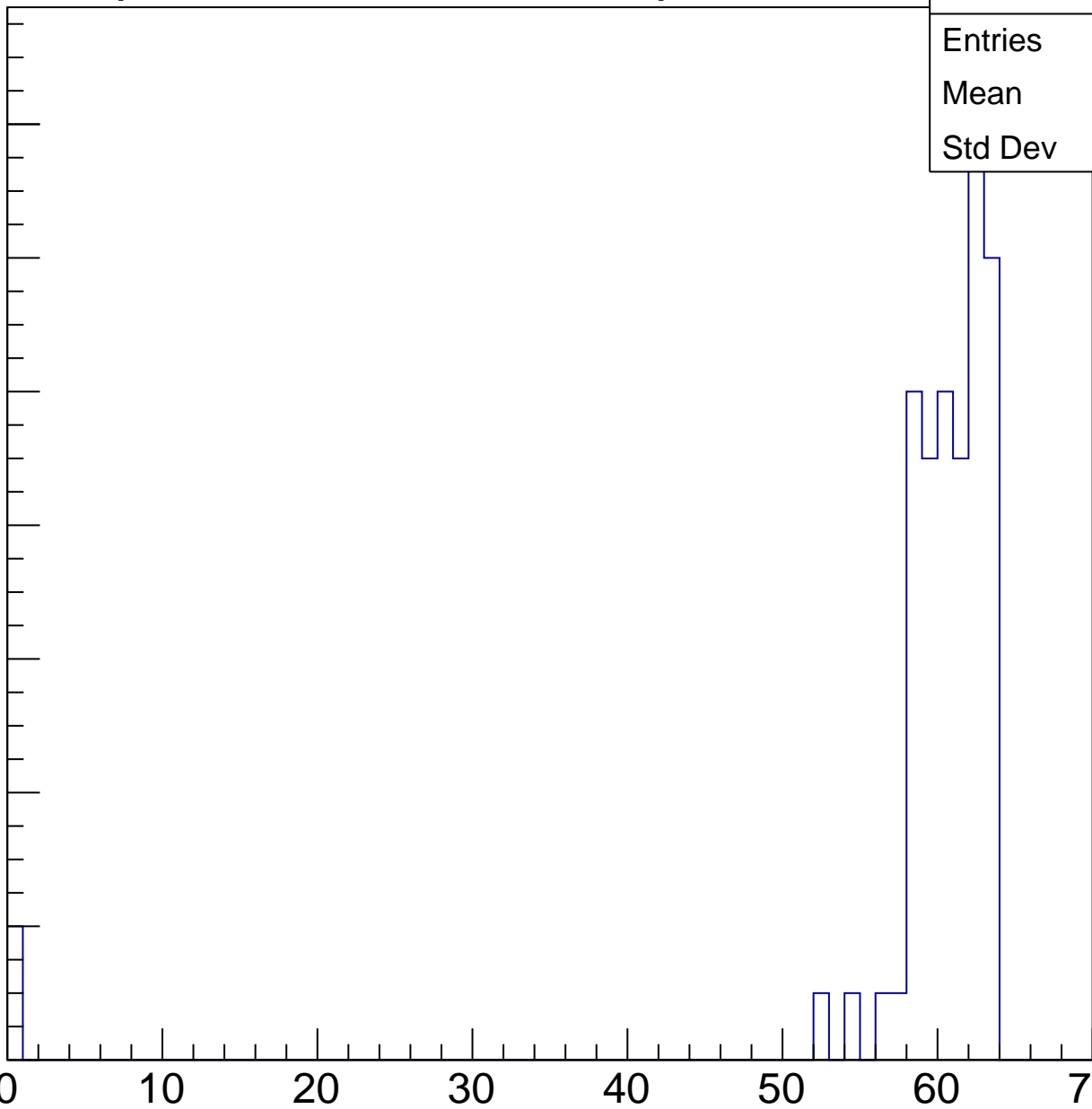
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.66
Std Dev	10.23

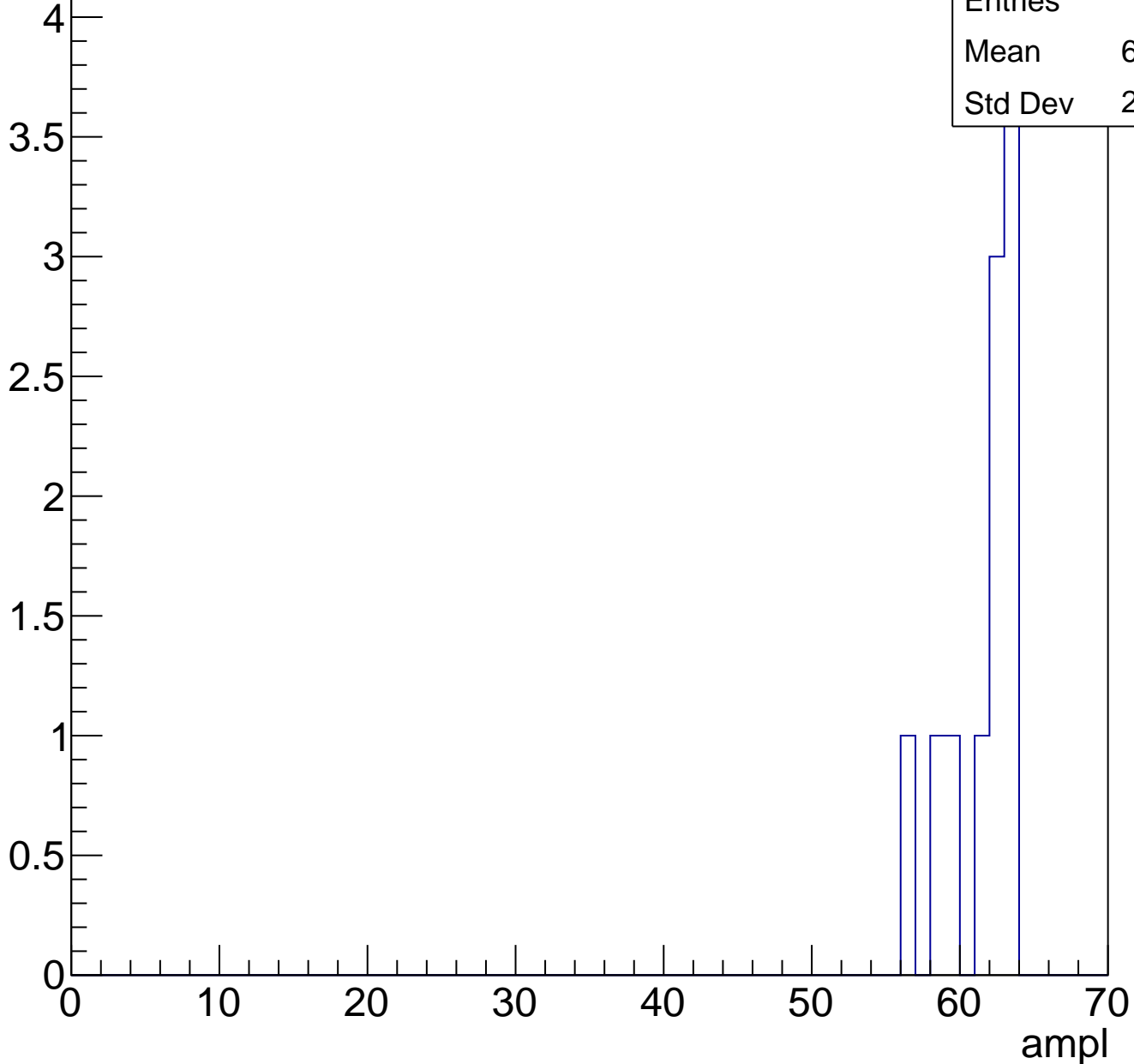
ampl



B0L101S, U19-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	11
Mean	61.09
Std Dev	2.275

B0L101S, U19-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch120, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	26.5
Std Dev	5.355

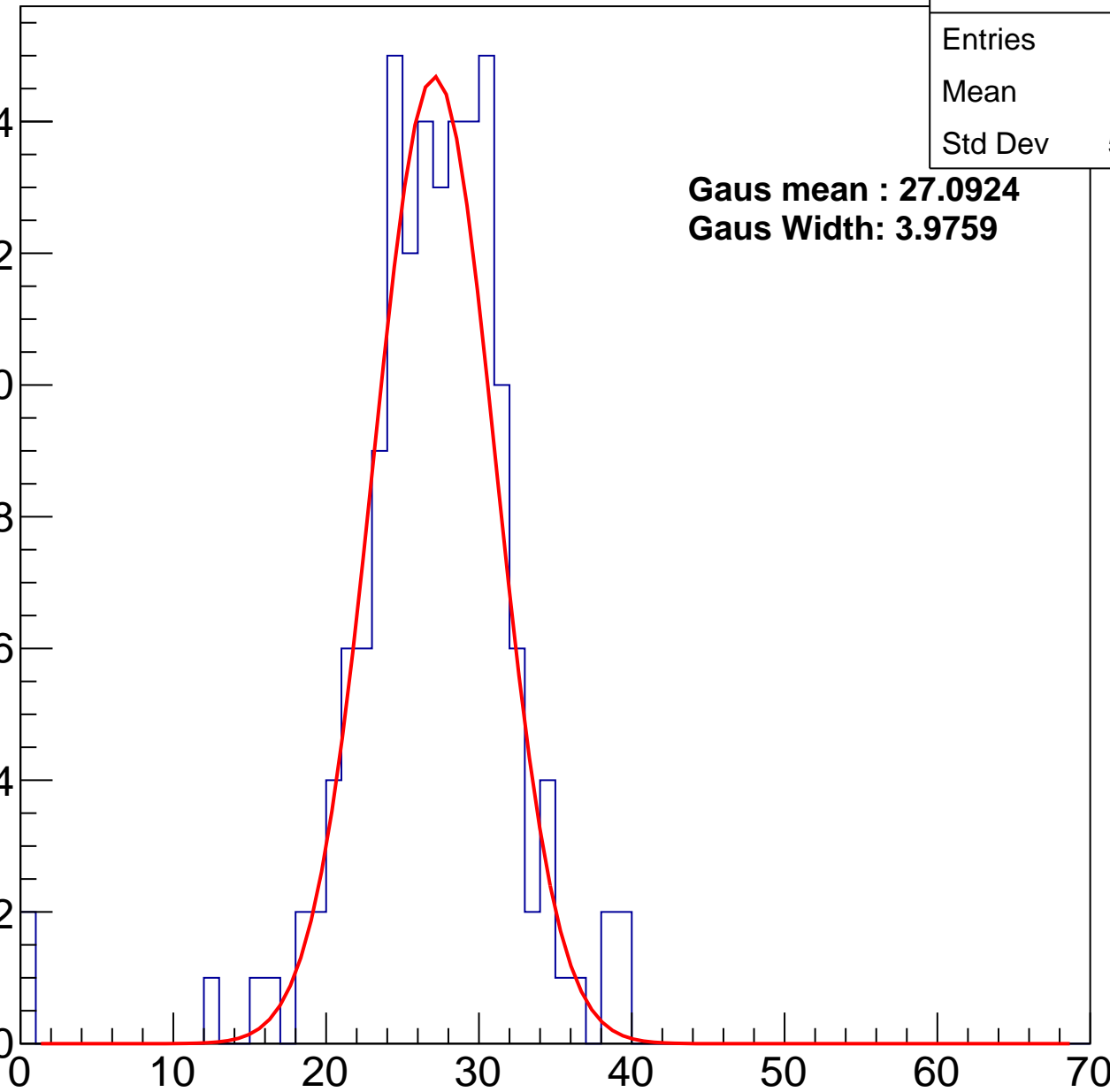
Gaus mean : 27.0924

Gaus Width: 3.9759

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch120, adc1

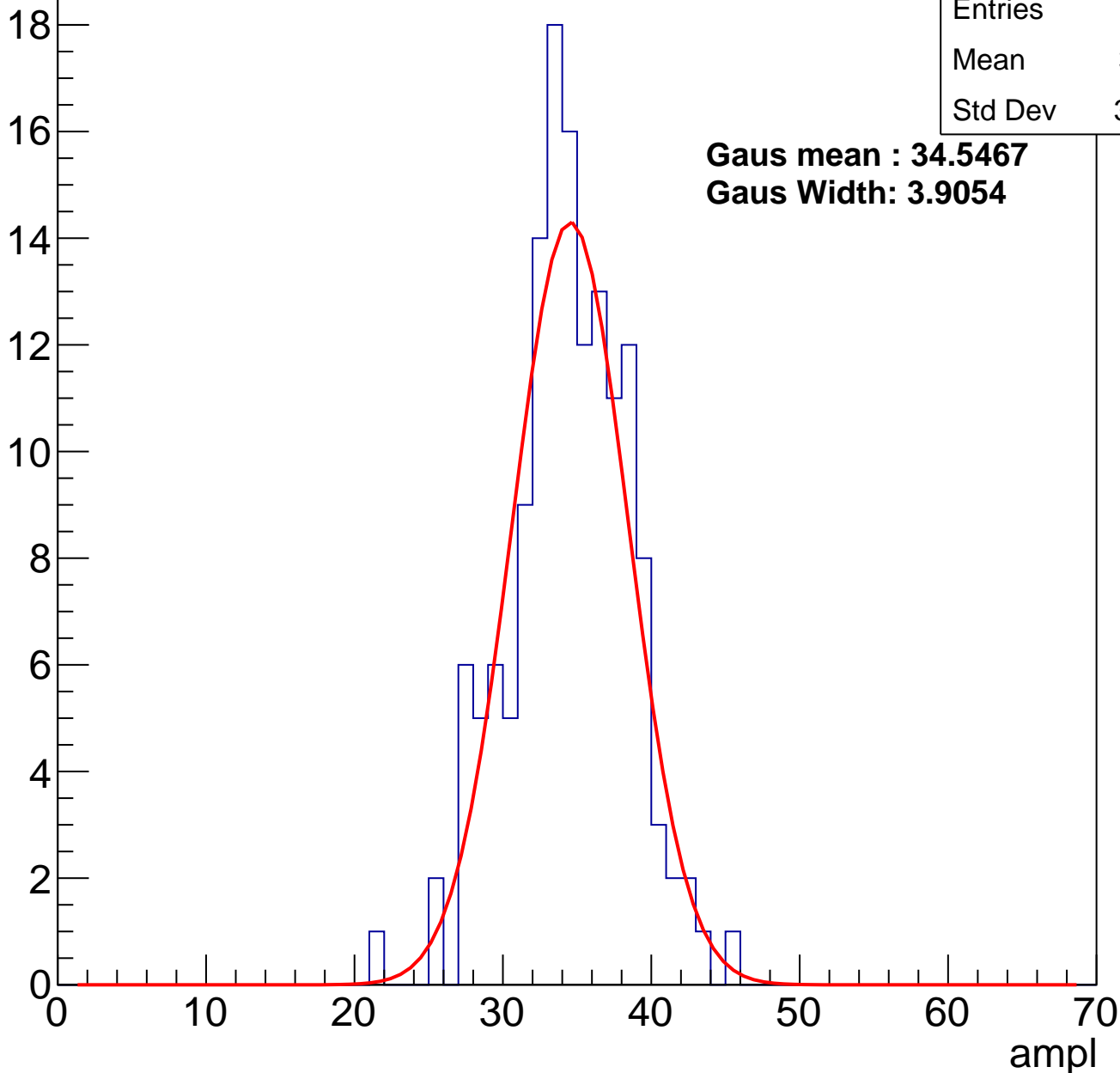
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	34.01
Std Dev	3.939

Gaus mean : 34.5467

Gaus Width: 3.9054

Entry



B0L101S, U19-ch120, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

109

Mean

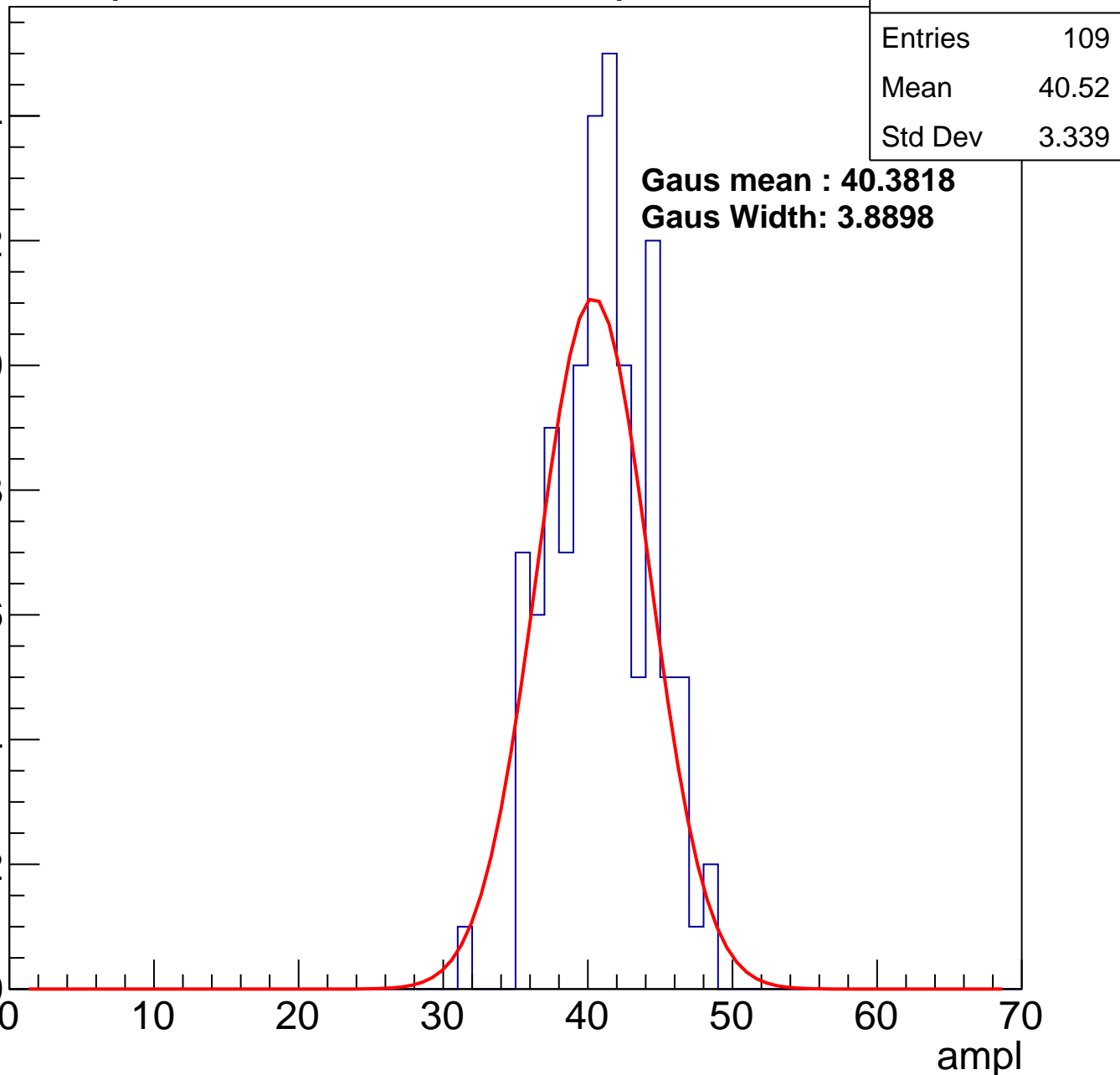
40.52

Std Dev

3.339

Gaus mean : 40.3818

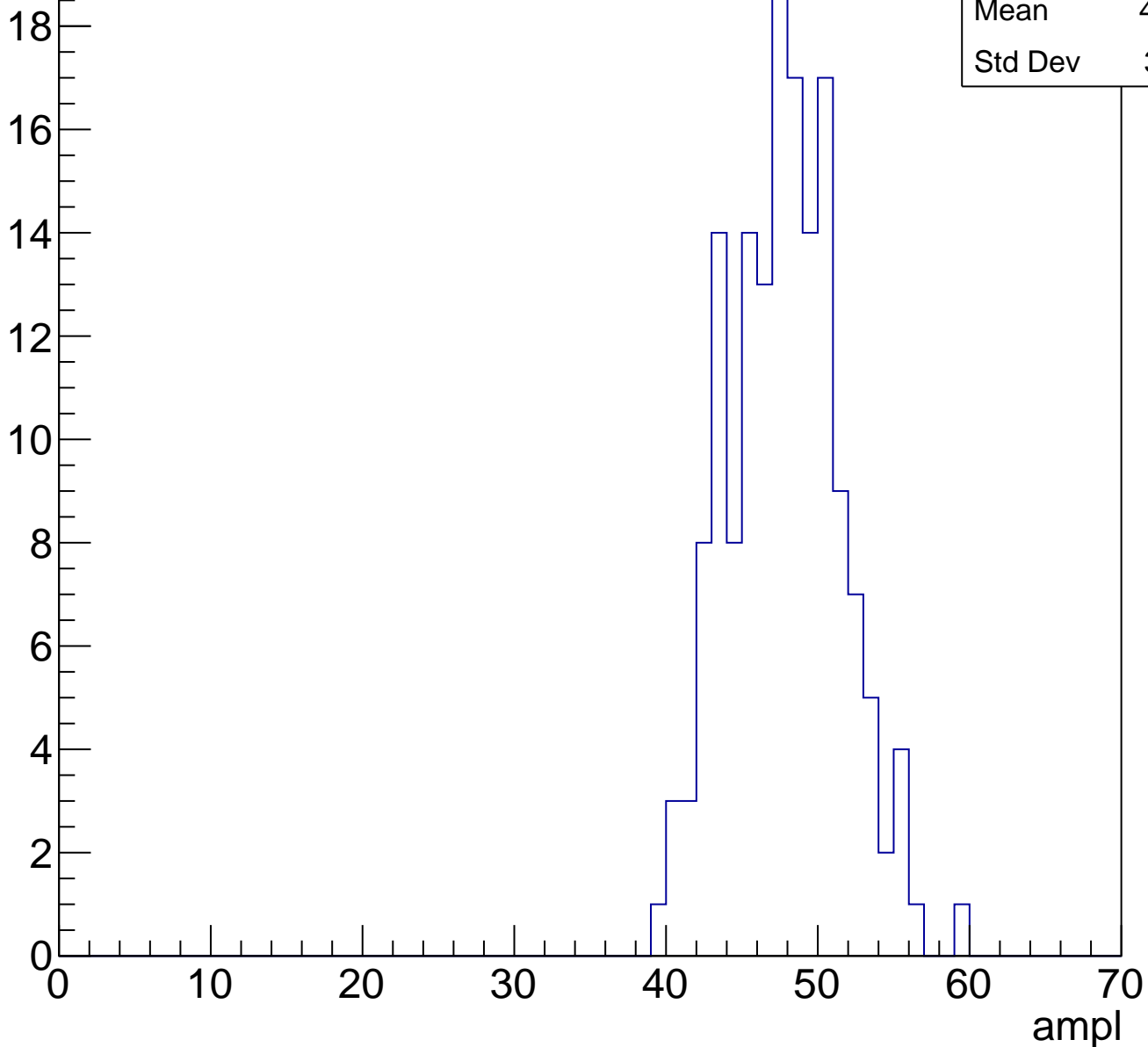
Gaus Width: 3.8898



B0L101S, U19-ch120, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

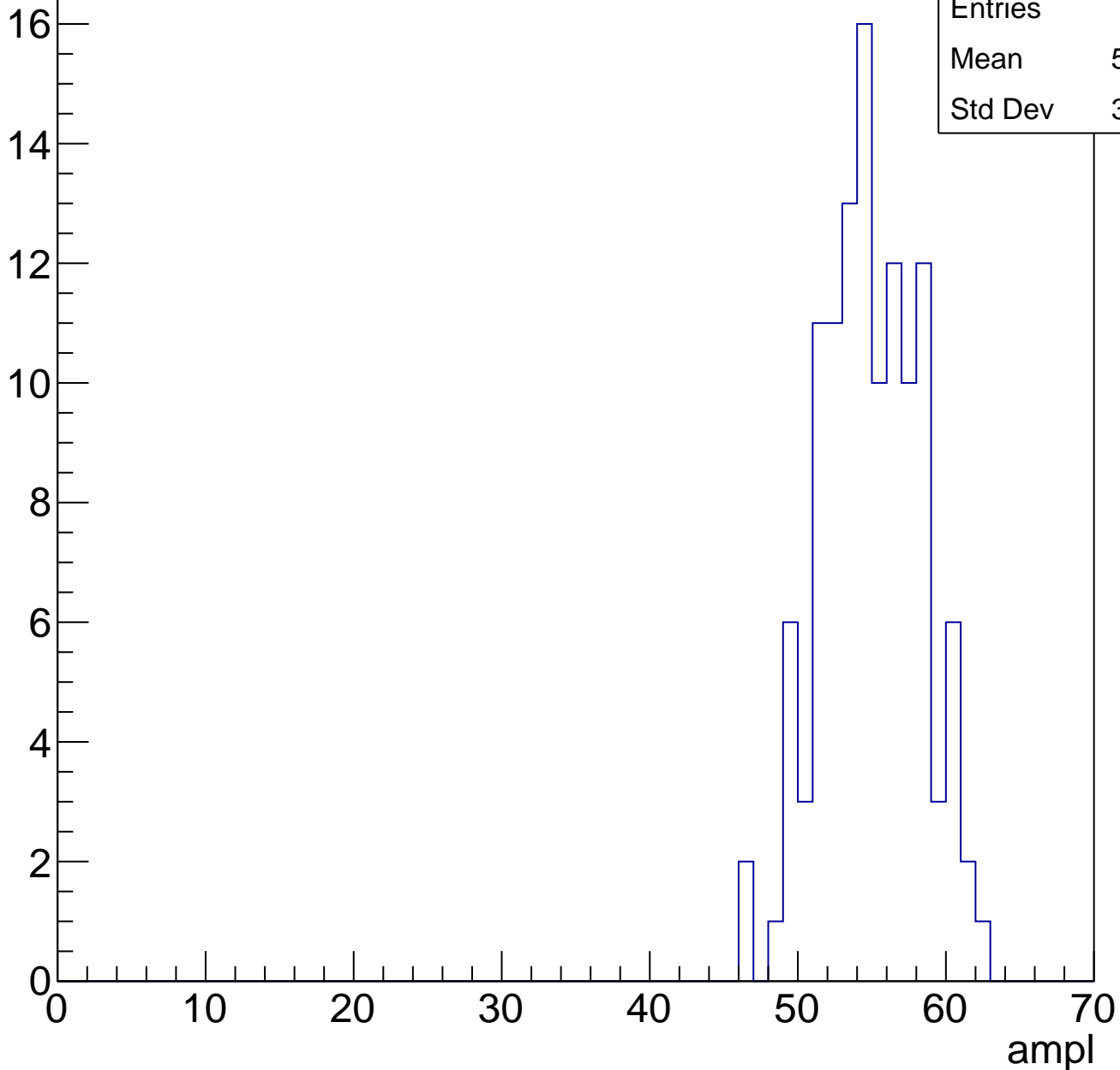


B0L101S, U19-ch120, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	119
Mean	54.45
Std Dev	3.289



B0L101S, U19-ch120, adc5

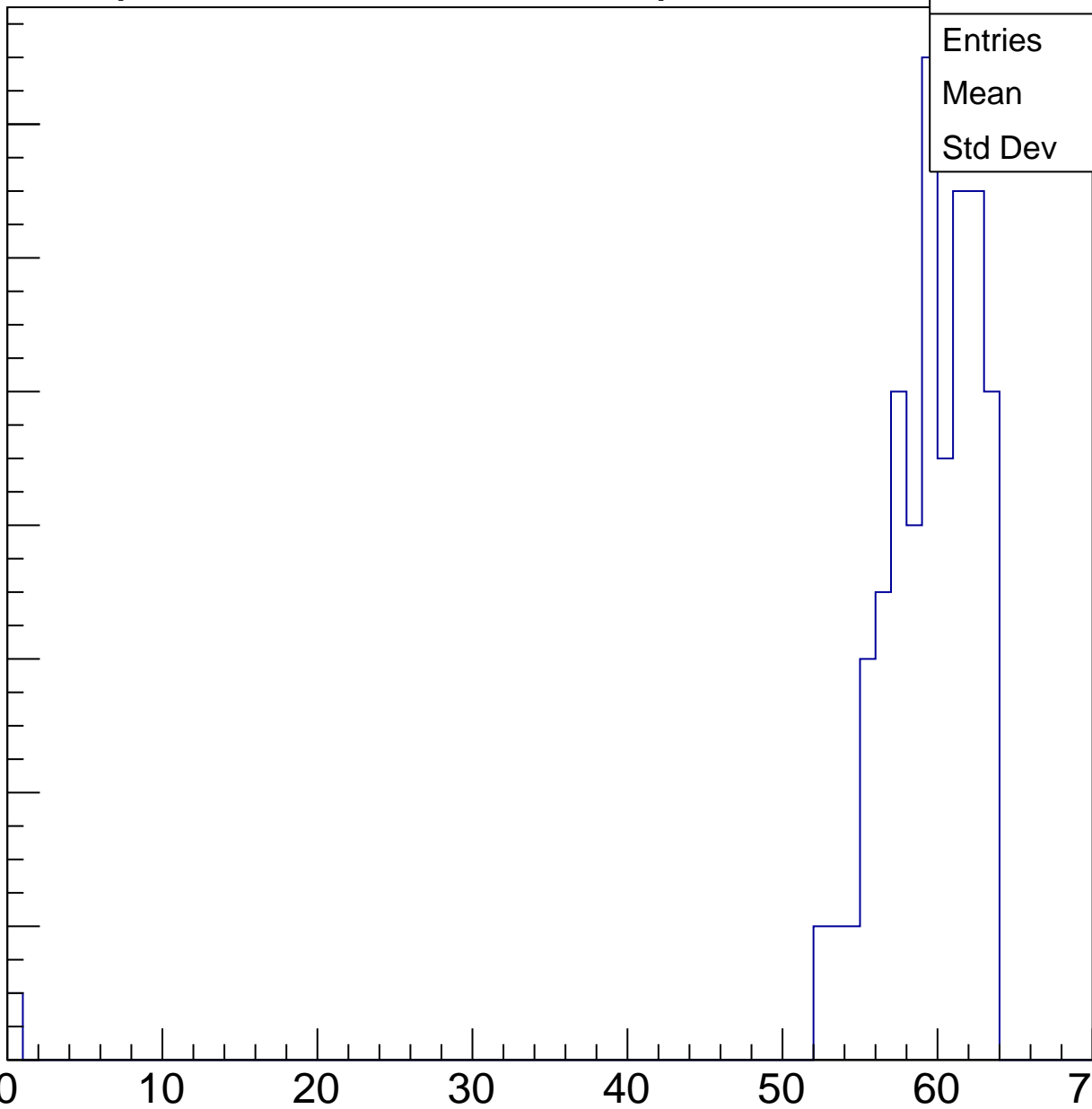
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	58.45
Std Dev	6.559

ampl

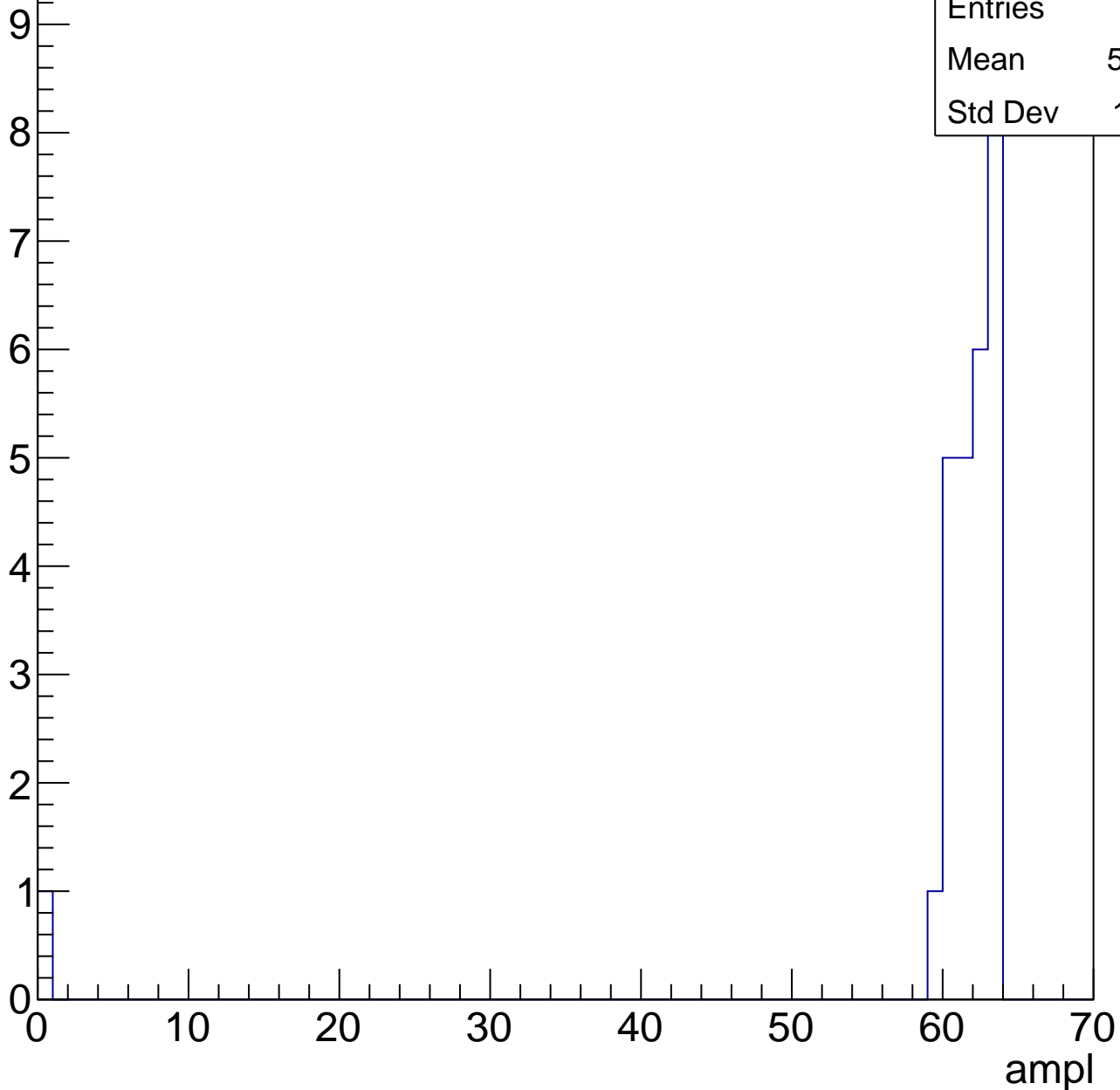


B0L101S, U19-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	27
Mean	59.37
Std Dev	11.71



B0L101S, U19-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

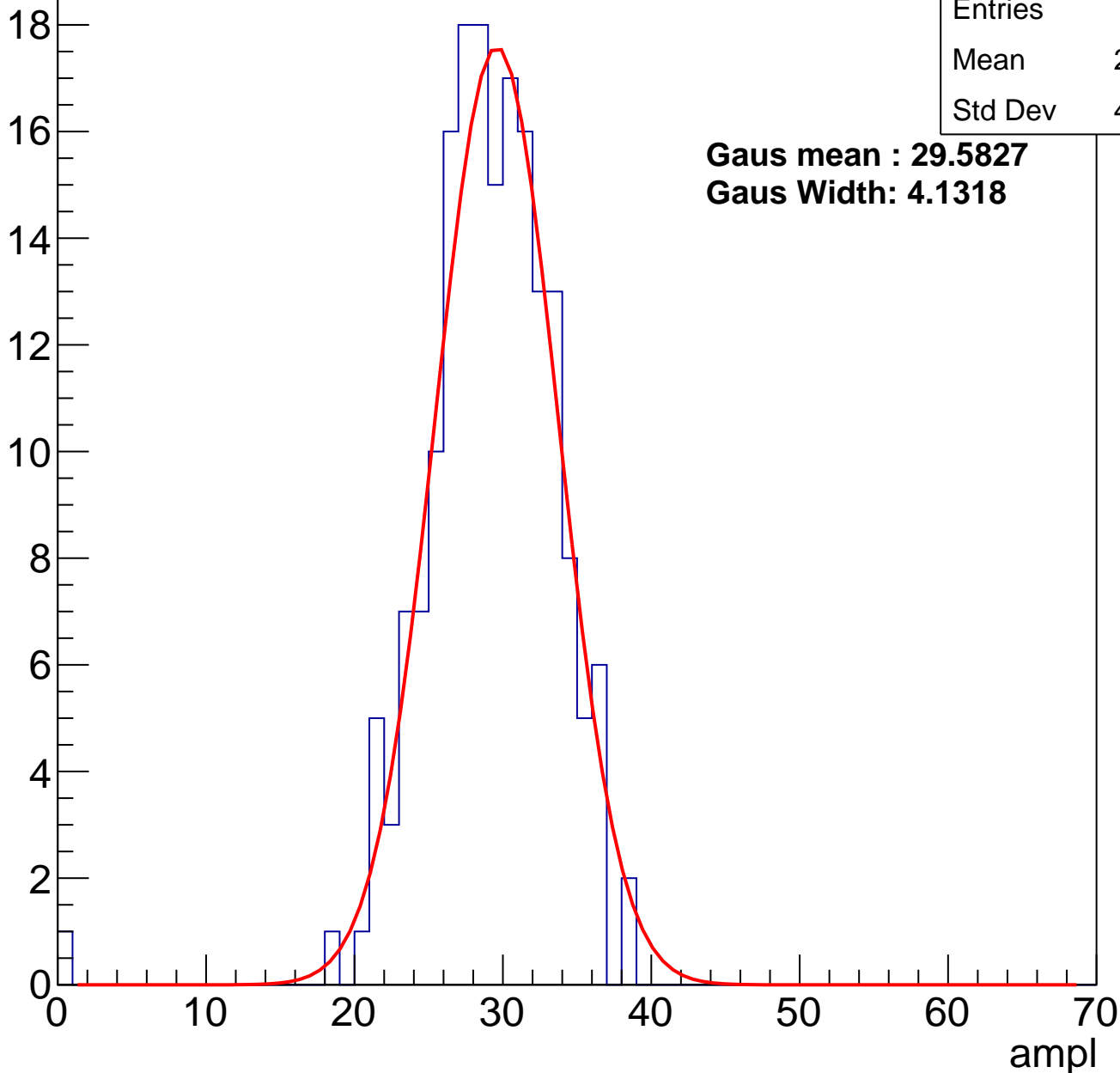
Std Dev

0

B0L101S, U19-ch121, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch121, adc1

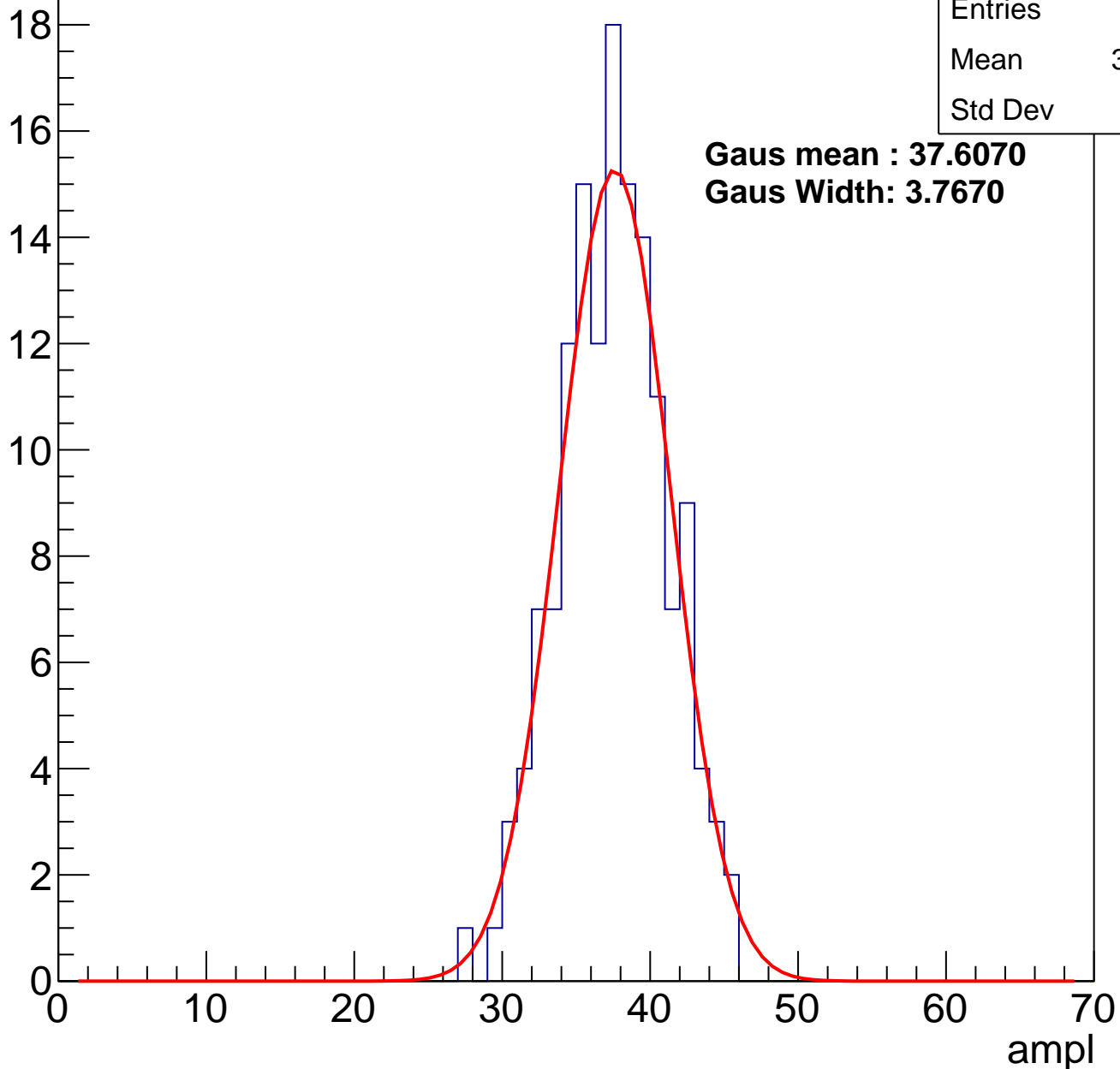
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	37.04
Std Dev	3.57

Gaus mean : 37.6070

Gaus Width: 3.7670

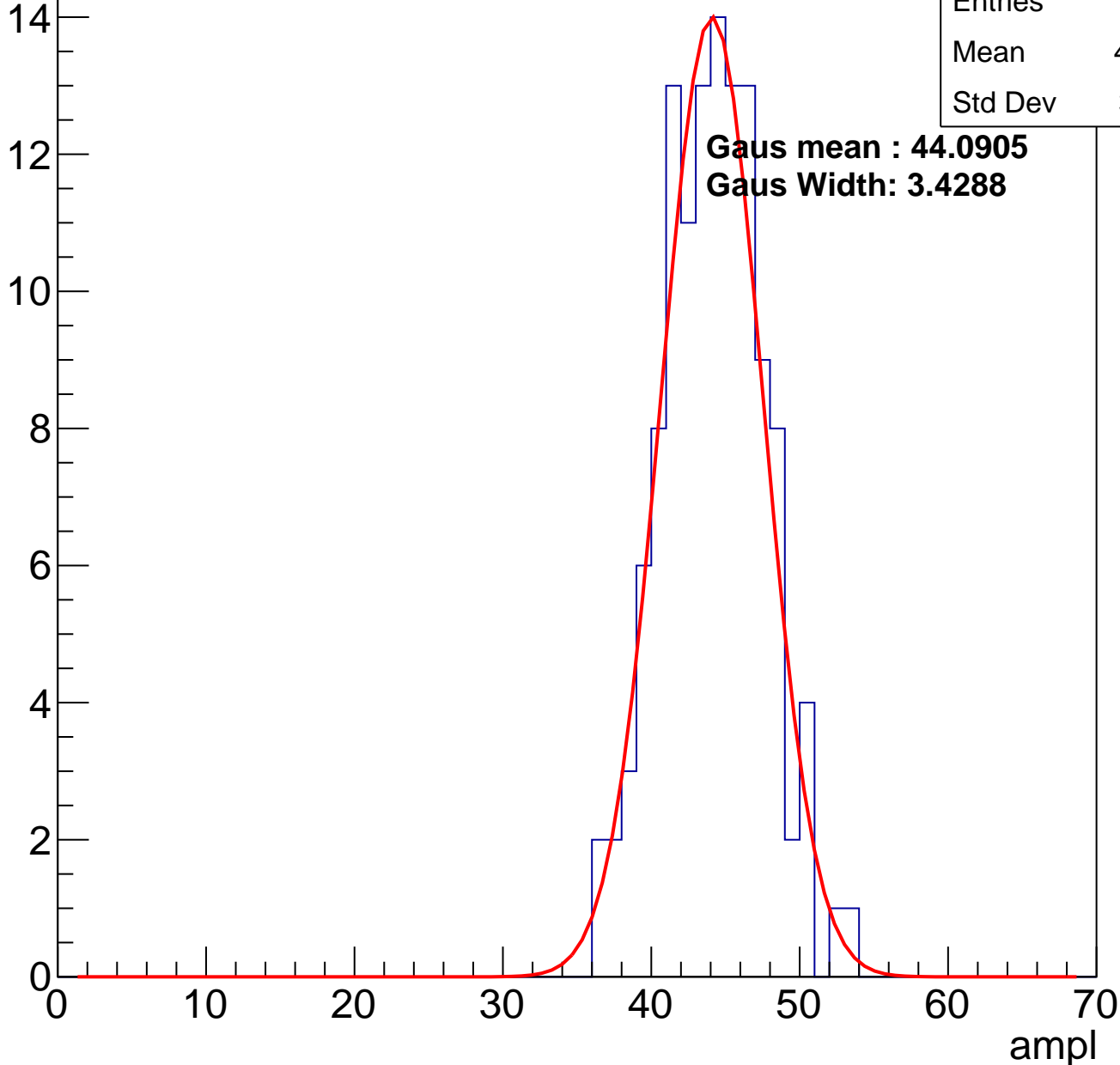
Entry



B0L101S, U19-ch121, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch121, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

119

Mean

49.74

Std Dev

3.311

ampl

0

10

20

30

40

50

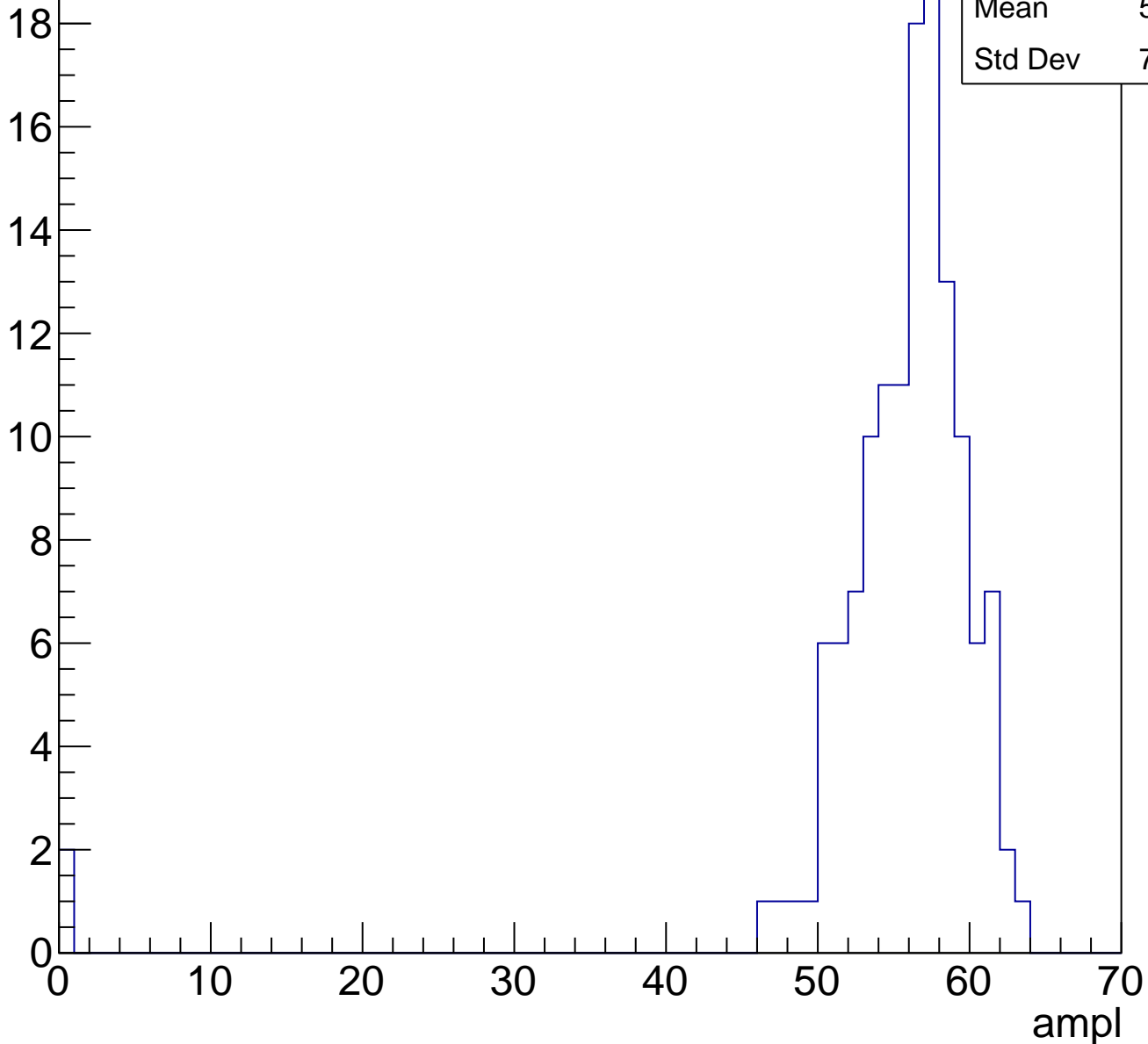
60

70

B0L101S, U19-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

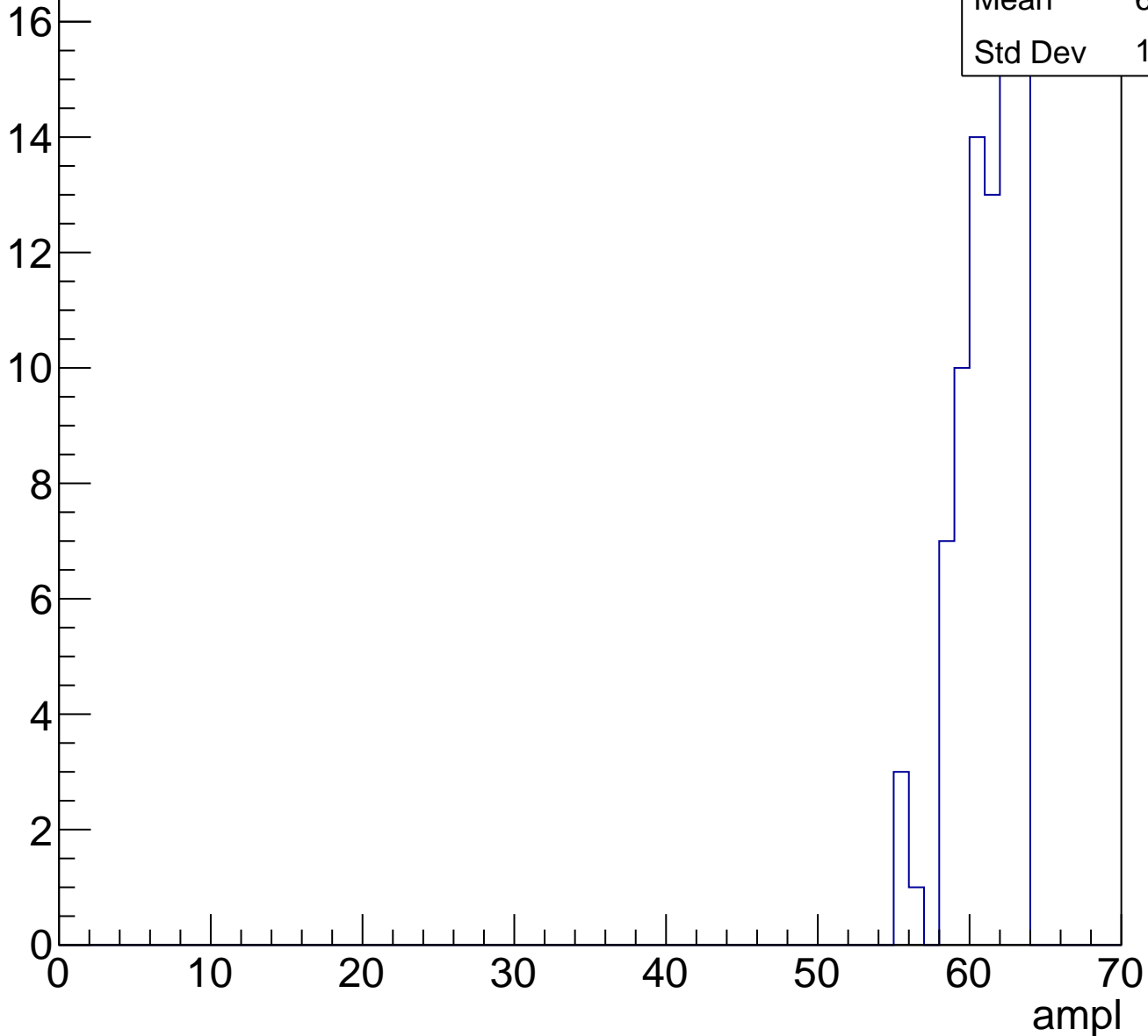
Entry



B0L101S, U19-ch121, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U19-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U19-ch122, adc0

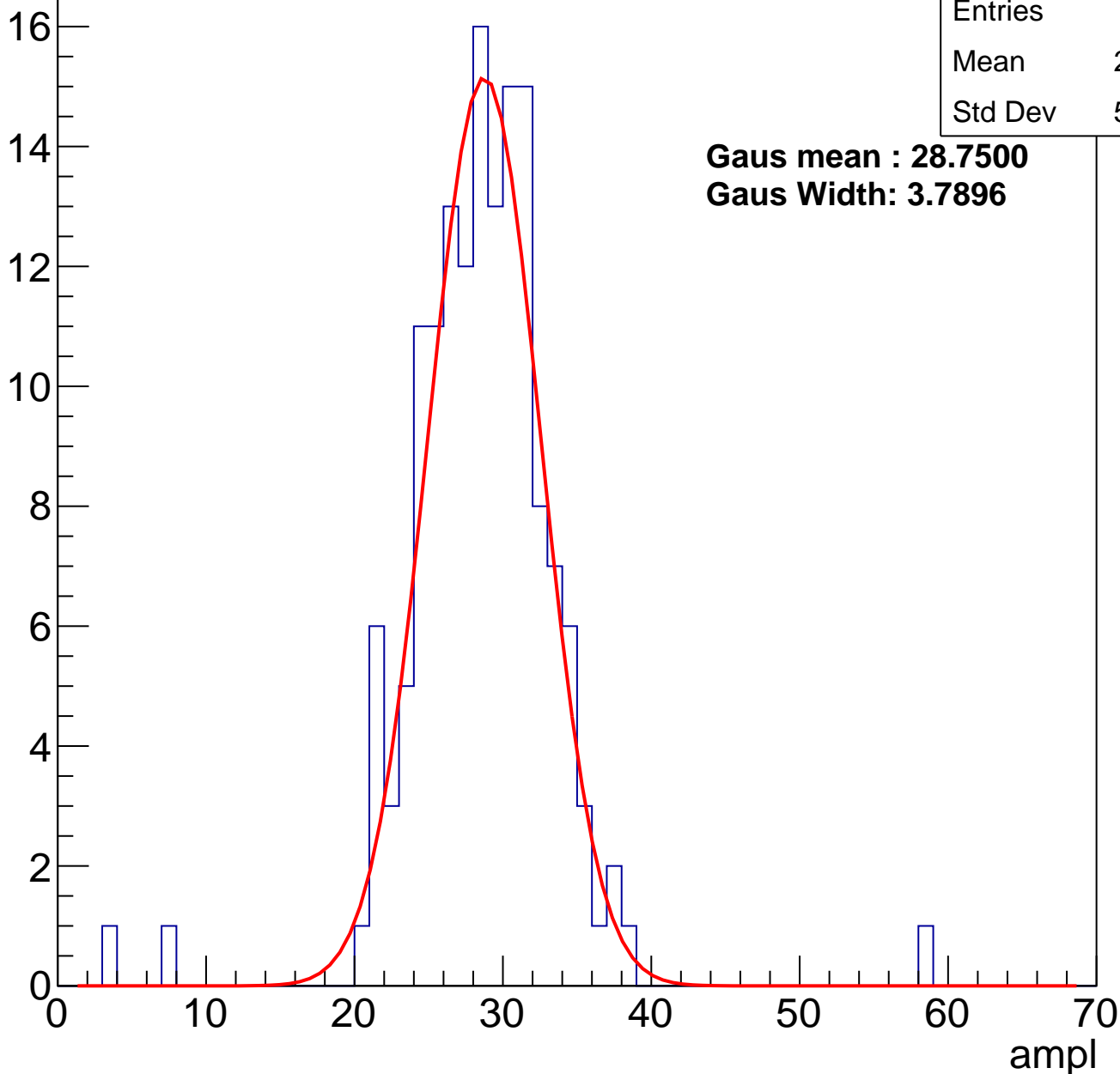
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.16
Std Dev	5.189

Gaus mean : 28.7500

Gaus Width: 3.7896

Entry



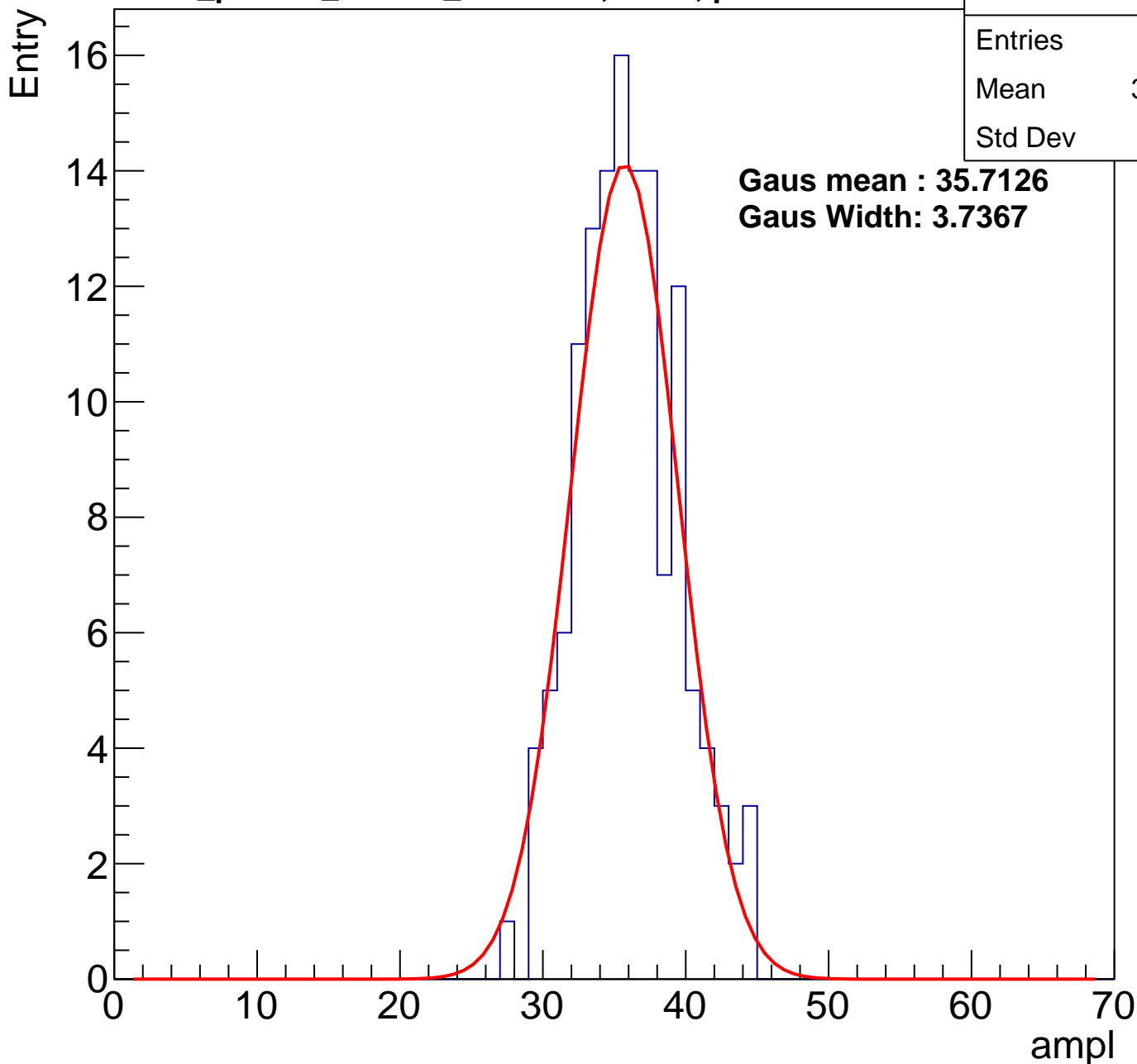
B0L101S, U19-ch122, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	35.52
Std Dev	3.53

Gaus mean : 35.7126

Gaus Width: 3.7367



B0L101S, U19-ch122, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

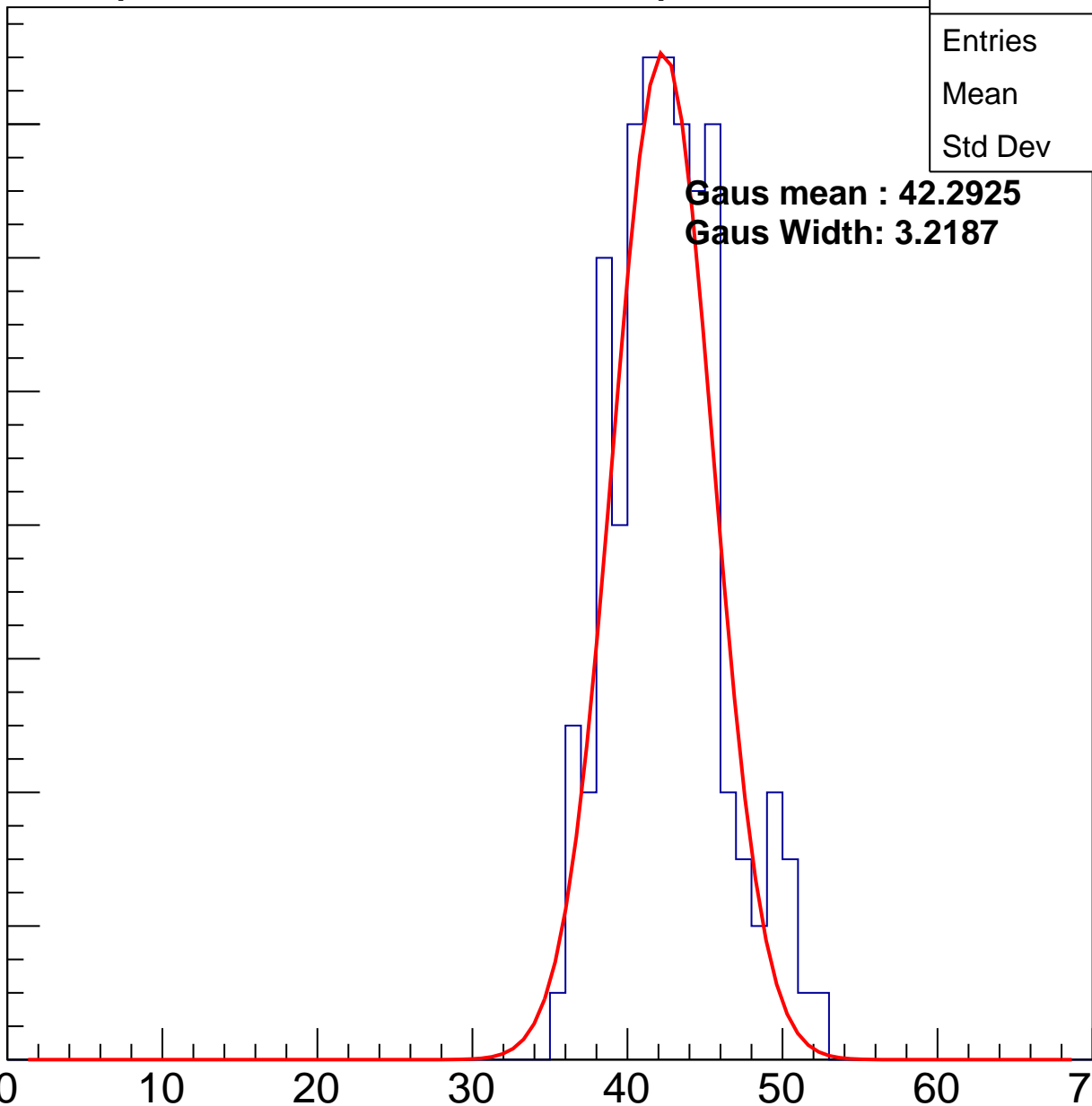
14
12
10
8
6
4
2
0

Entries	133
Mean	42.18
Std Dev	3.503

Gaus mean : 42.2925

Gaus Width: 3.2187

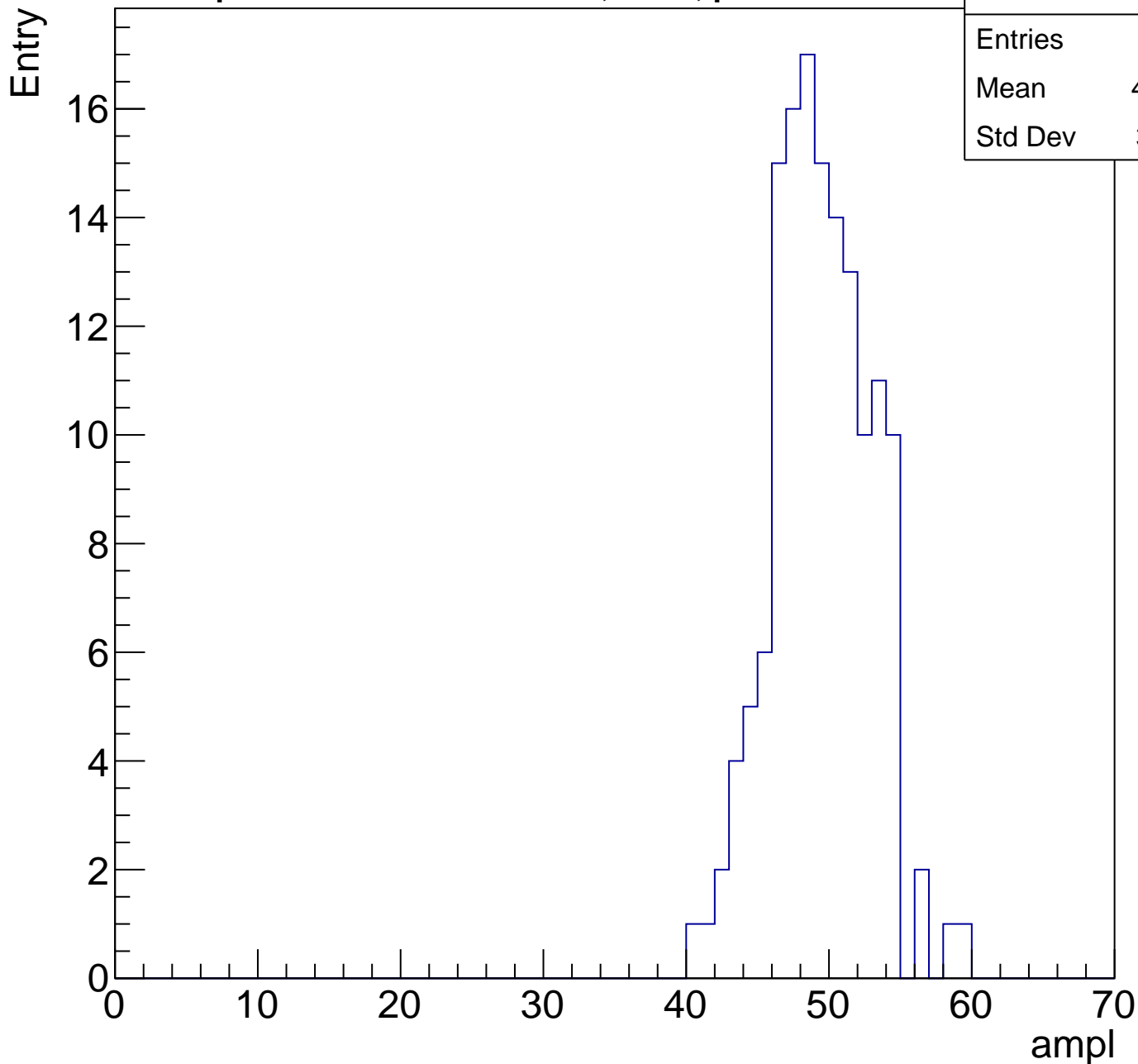
ampl



B0L101S, U19-ch122, adc3

calib_packv5_042523_0143.root, FC#1, port C1

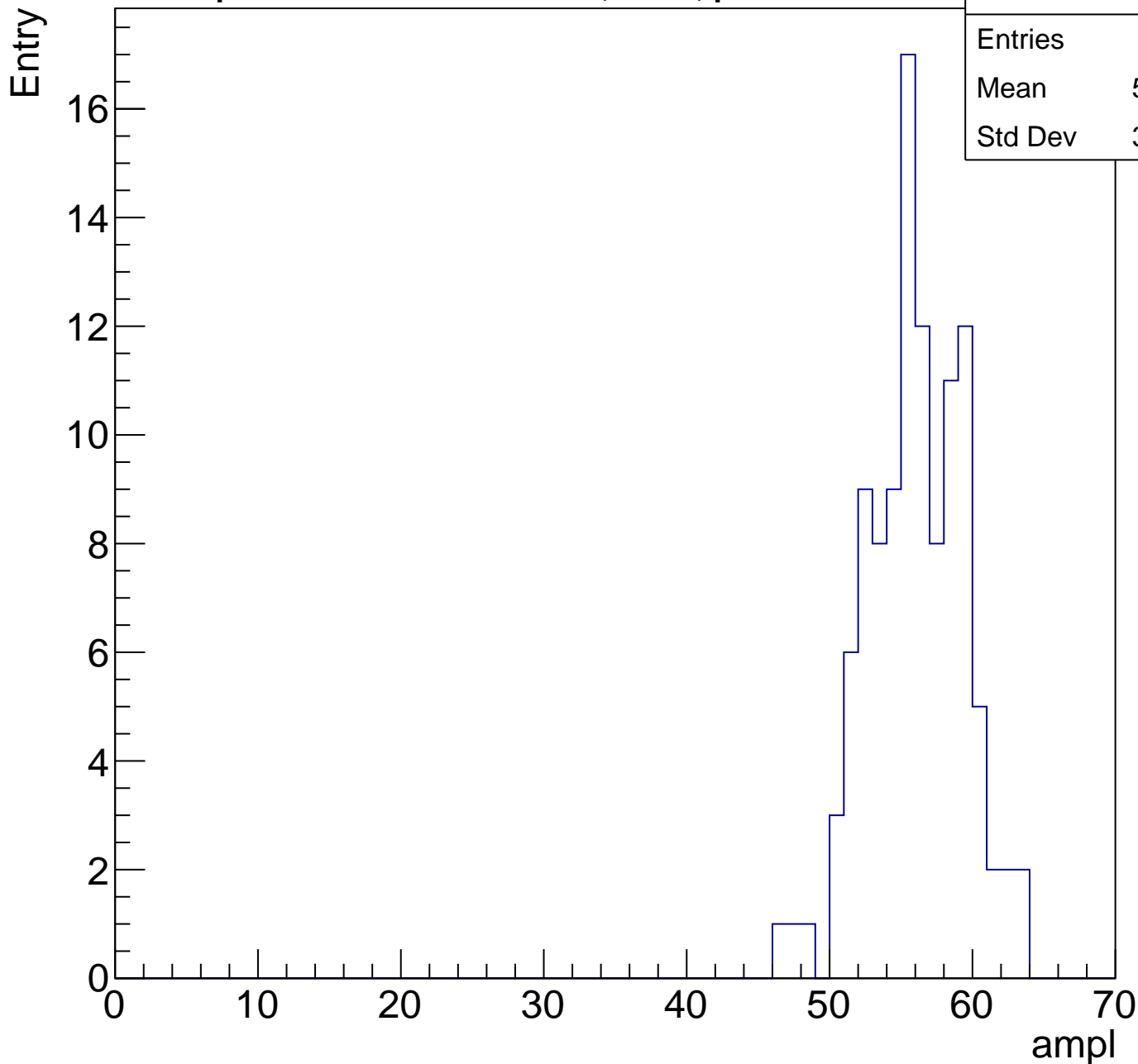
Entries	144
Mean	48.99
Std Dev	3.421



B0L101S, U19-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.56
Std Dev	3.333



B0L101S, U19-ch122, adc5

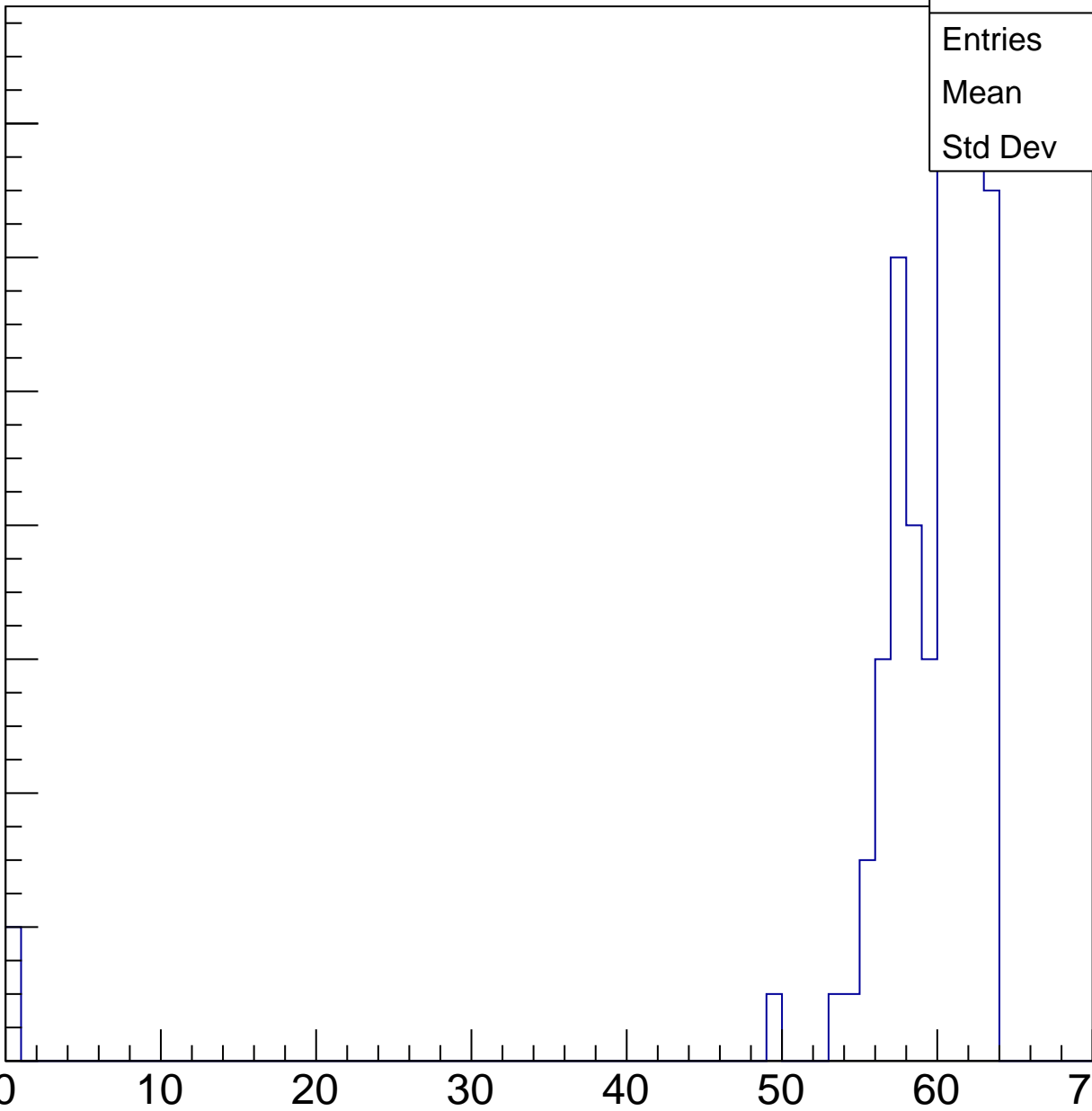
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	58.35
Std Dev	8.929

ampl

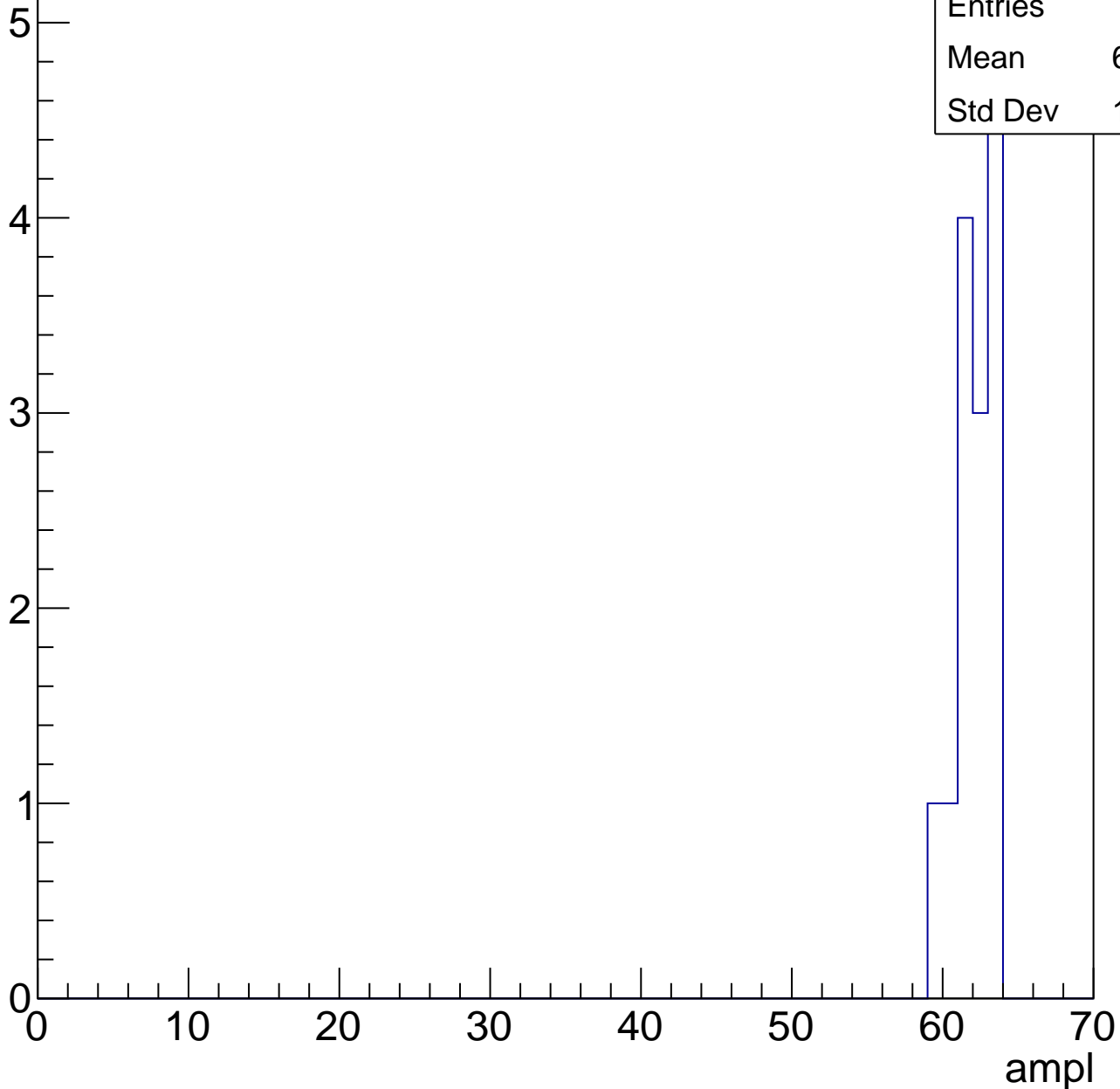


B0L101S, U19-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	61.71
Std Dev	1.221



B0L101S, U19-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch123, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

124

Mean

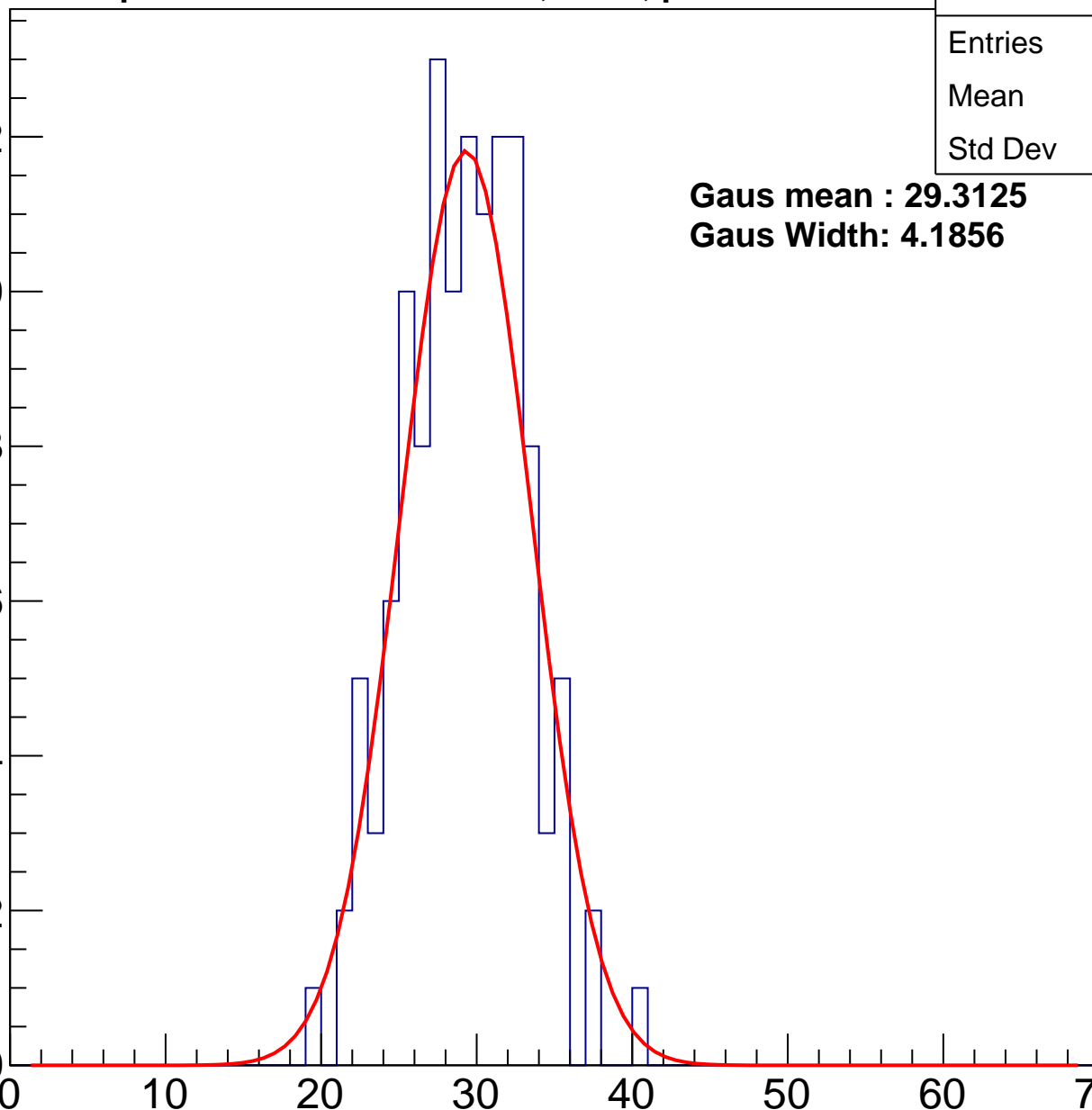
28.73

Std Dev

3.826

Gaus mean : 29.3125

Gaus Width: 4.1856



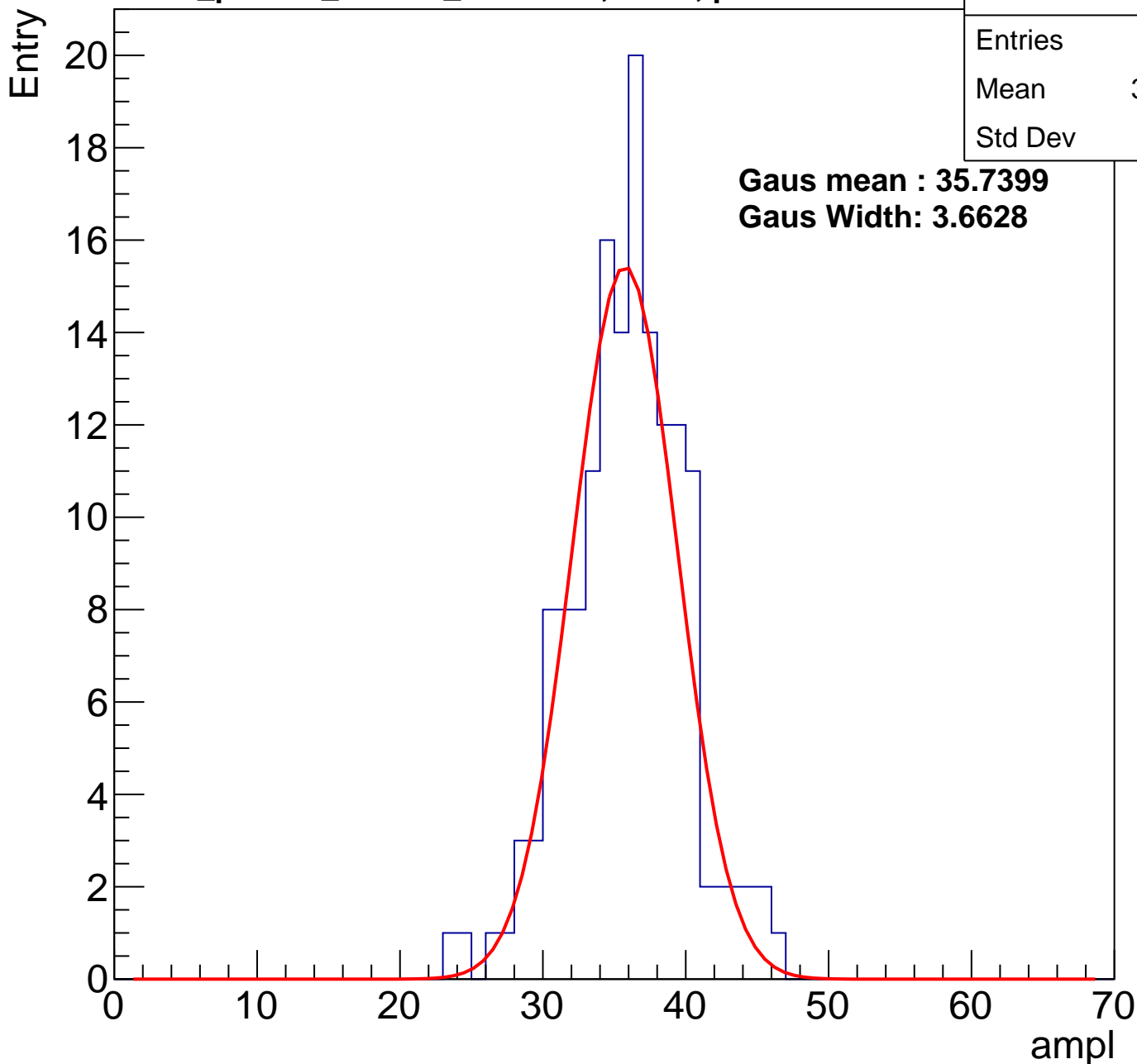
B0L101S, U19-ch123, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	35.42
Std Dev	4.04

Gaus mean : 35.7399

Gaus Width: 3.6628



B0L101S, U19-ch123, adc2

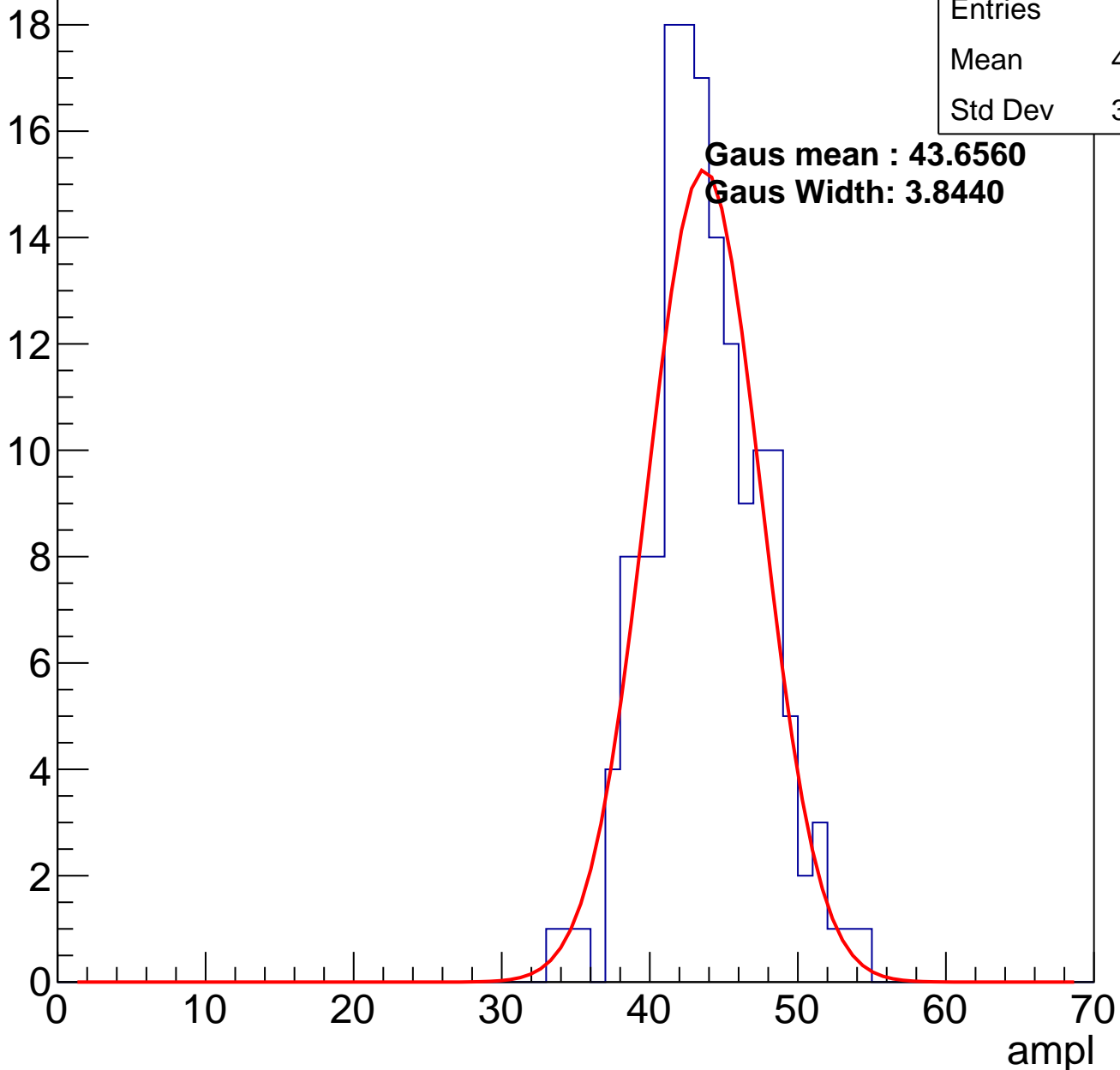
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	43.34
Std Dev	3.792

Entry

Gaus mean : 43.6560

Gaus Width: 3.8440



B0L101S, U19-ch123, adc3

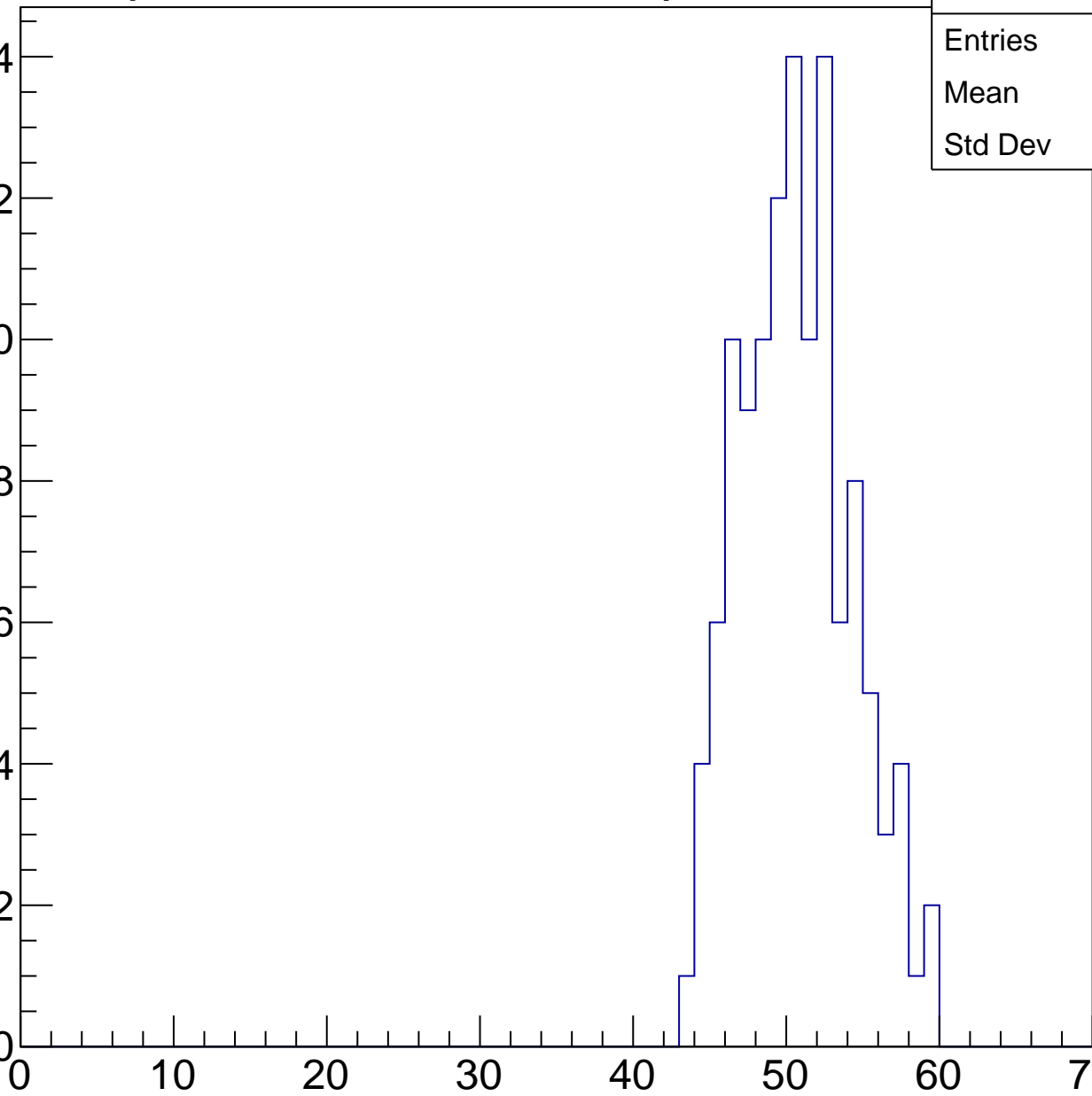
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	50.21
Std Dev	3.59

Entry

14
12
10
8
6
4
2
0

ampl

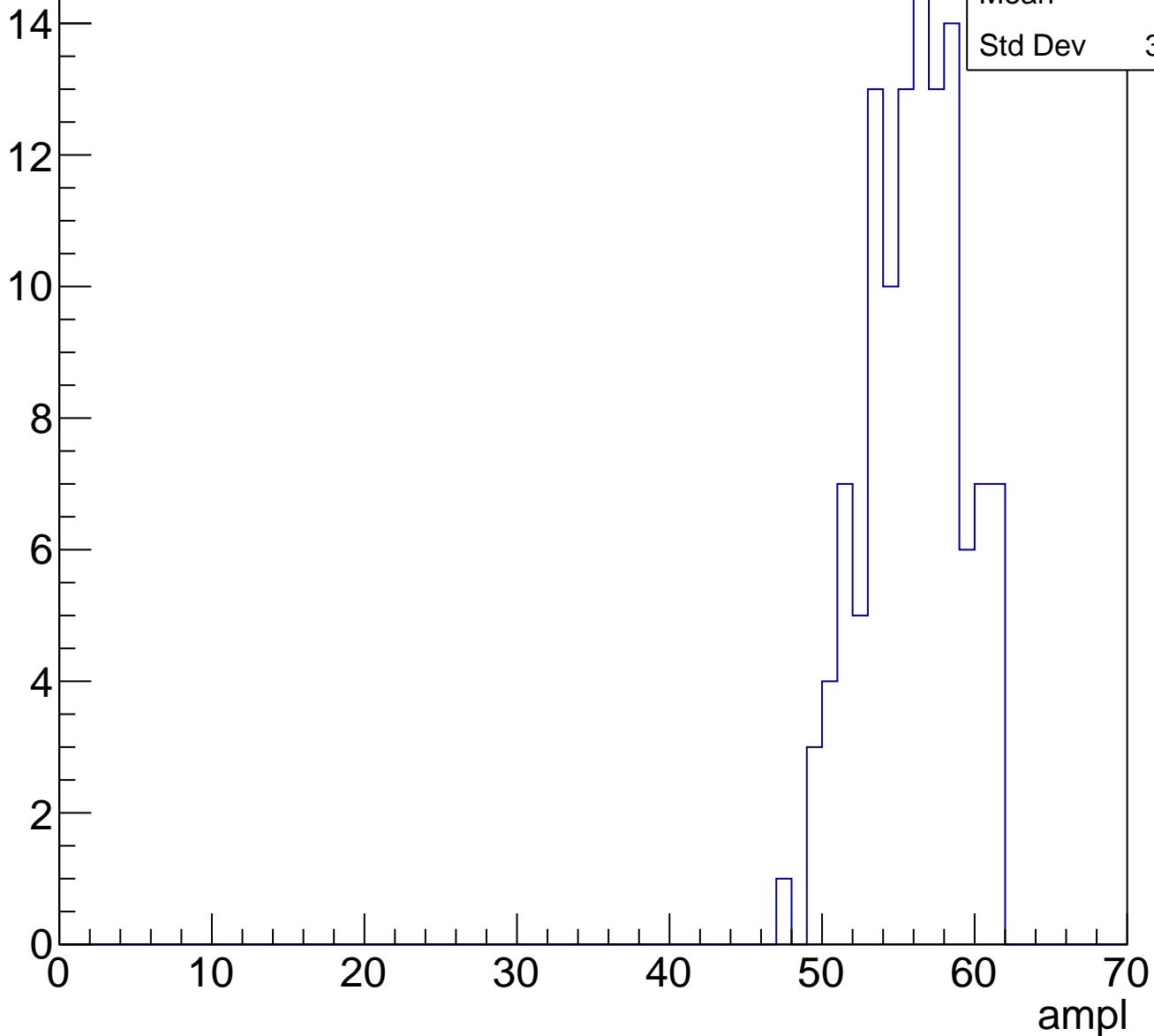


B0L101S, U19-ch123, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	55.5
Std Dev	3.159

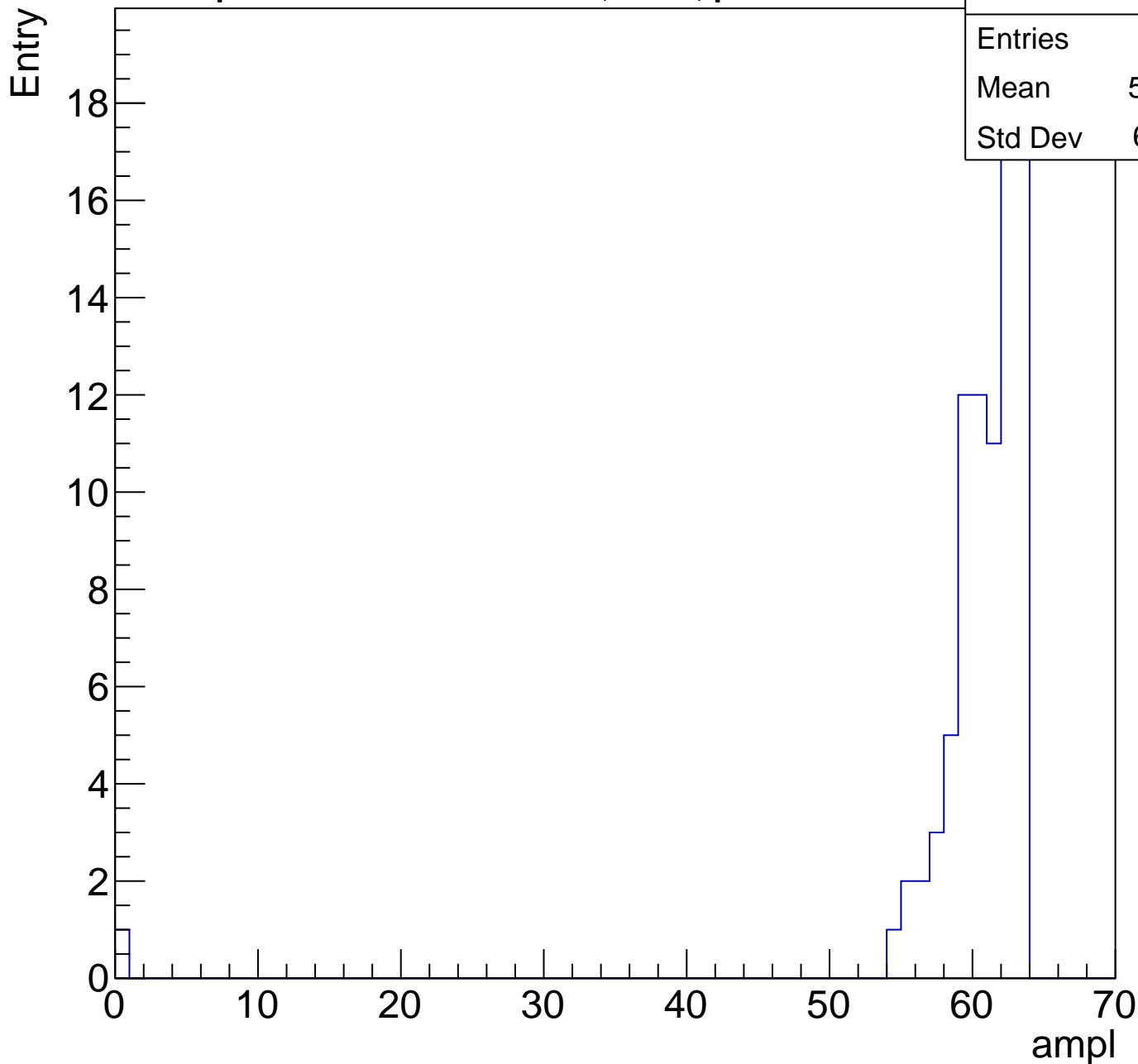
Entry



B0L101S, U19-ch123, adc5

calib_packv5_042523_0143.root, FC#1, port C1

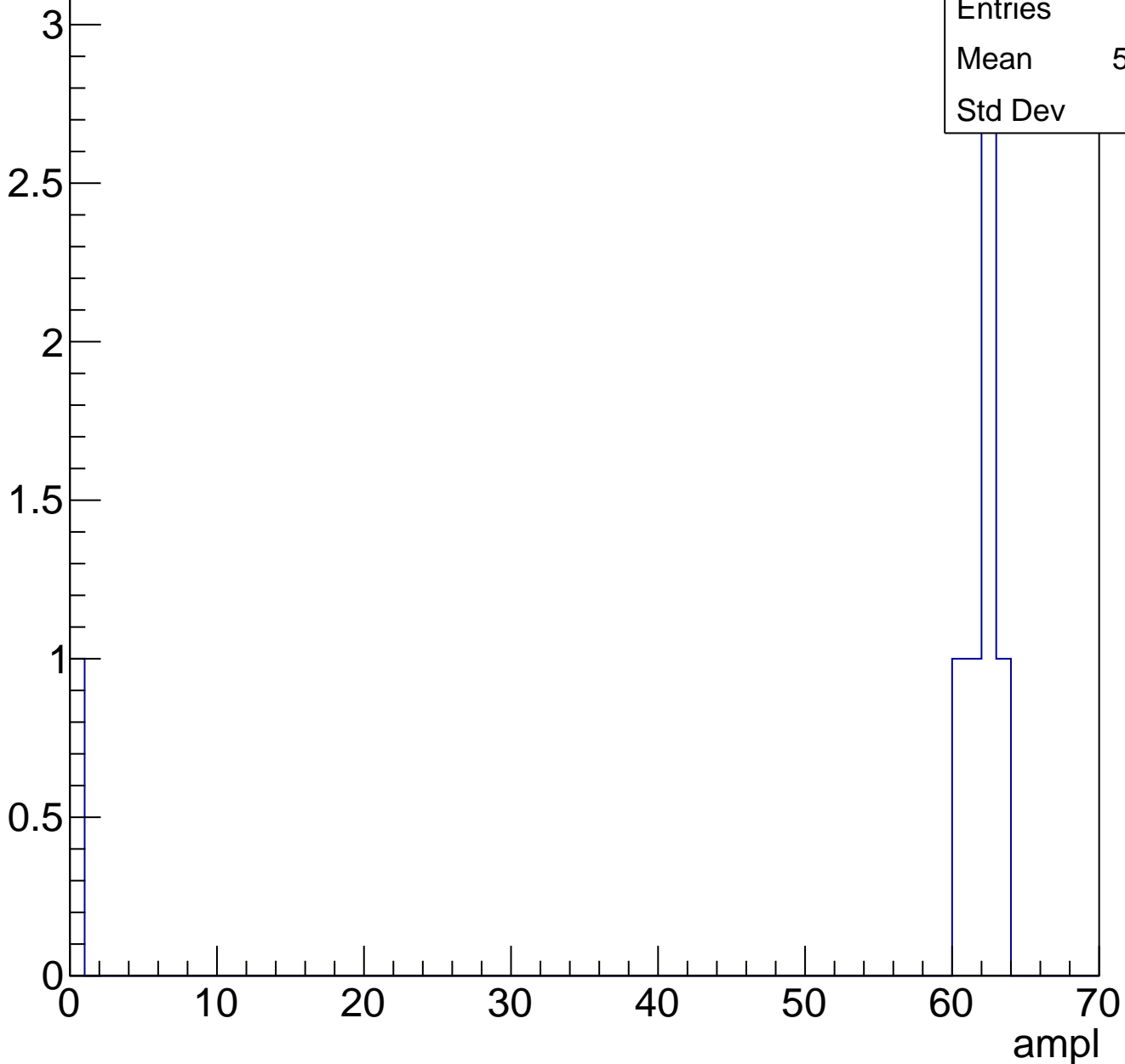
Entries	85
Mean	59.85
Std Dev	6.881



B0L101S, U19-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

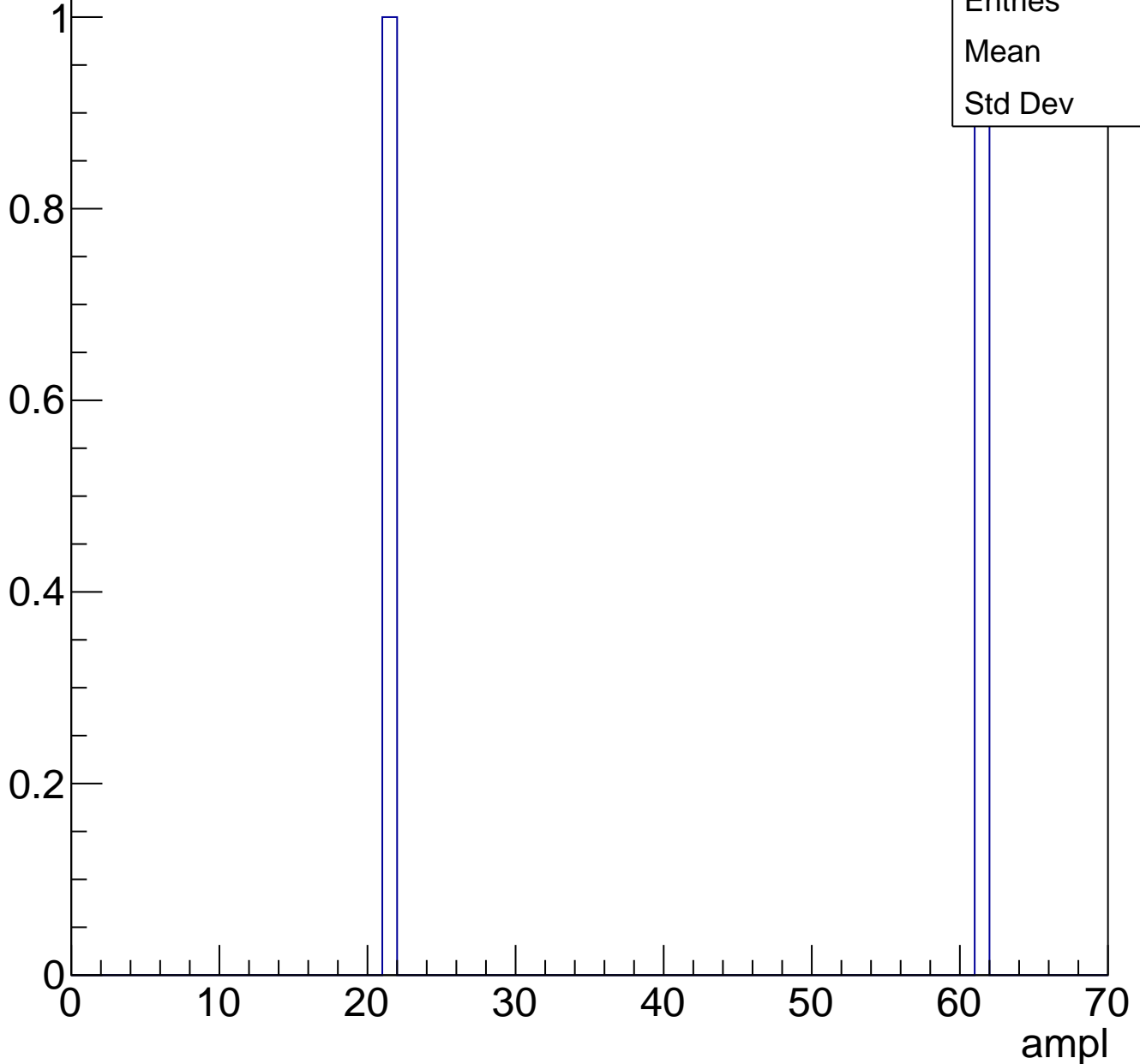
Entry



B0L101S, U19-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



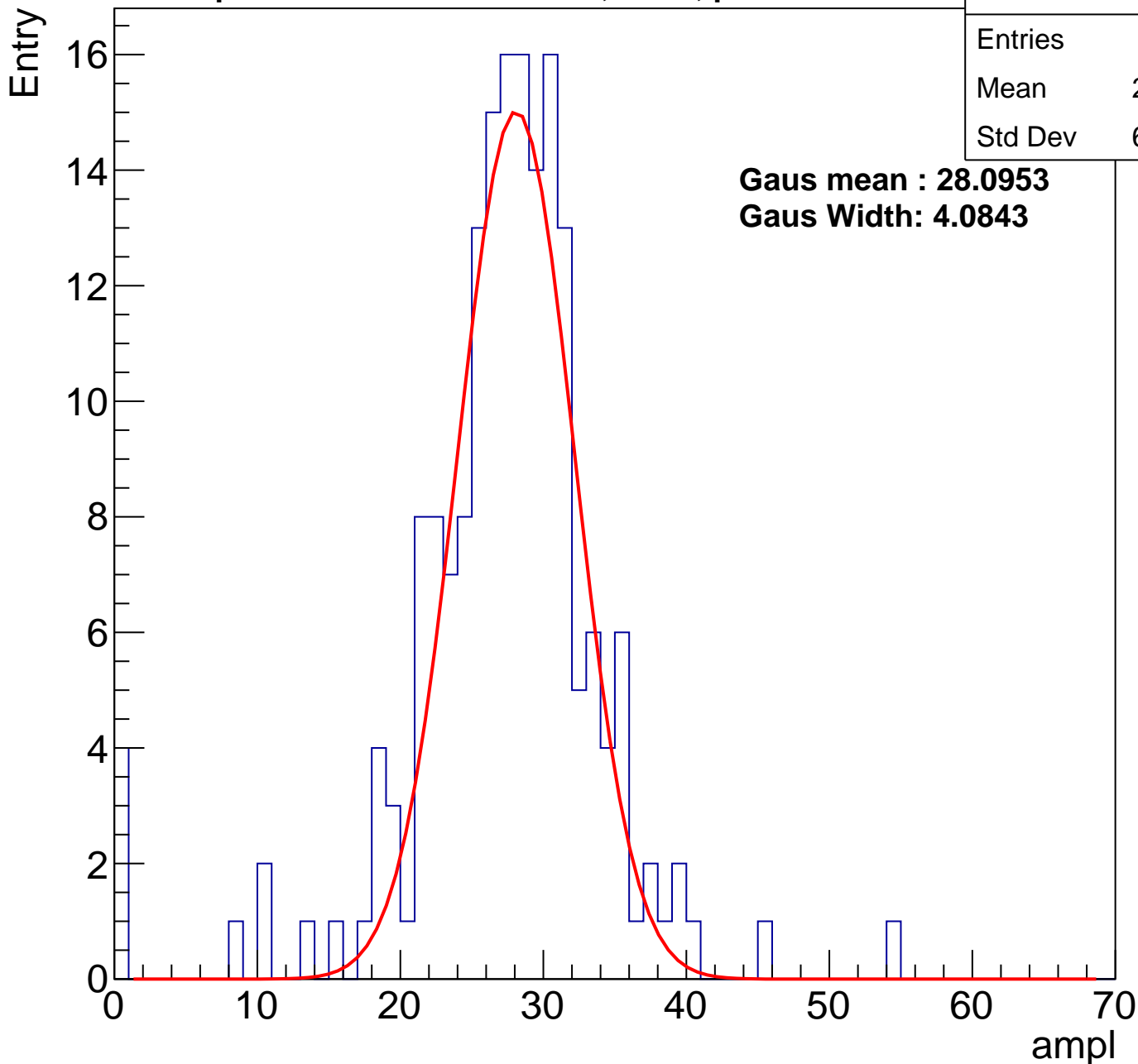
B0L101S, U19-ch124, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	26.77
Std Dev	6.972

Gaus mean : 28.0953

Gaus Width: 4.0843



B0L101S, U19-ch124, adc1

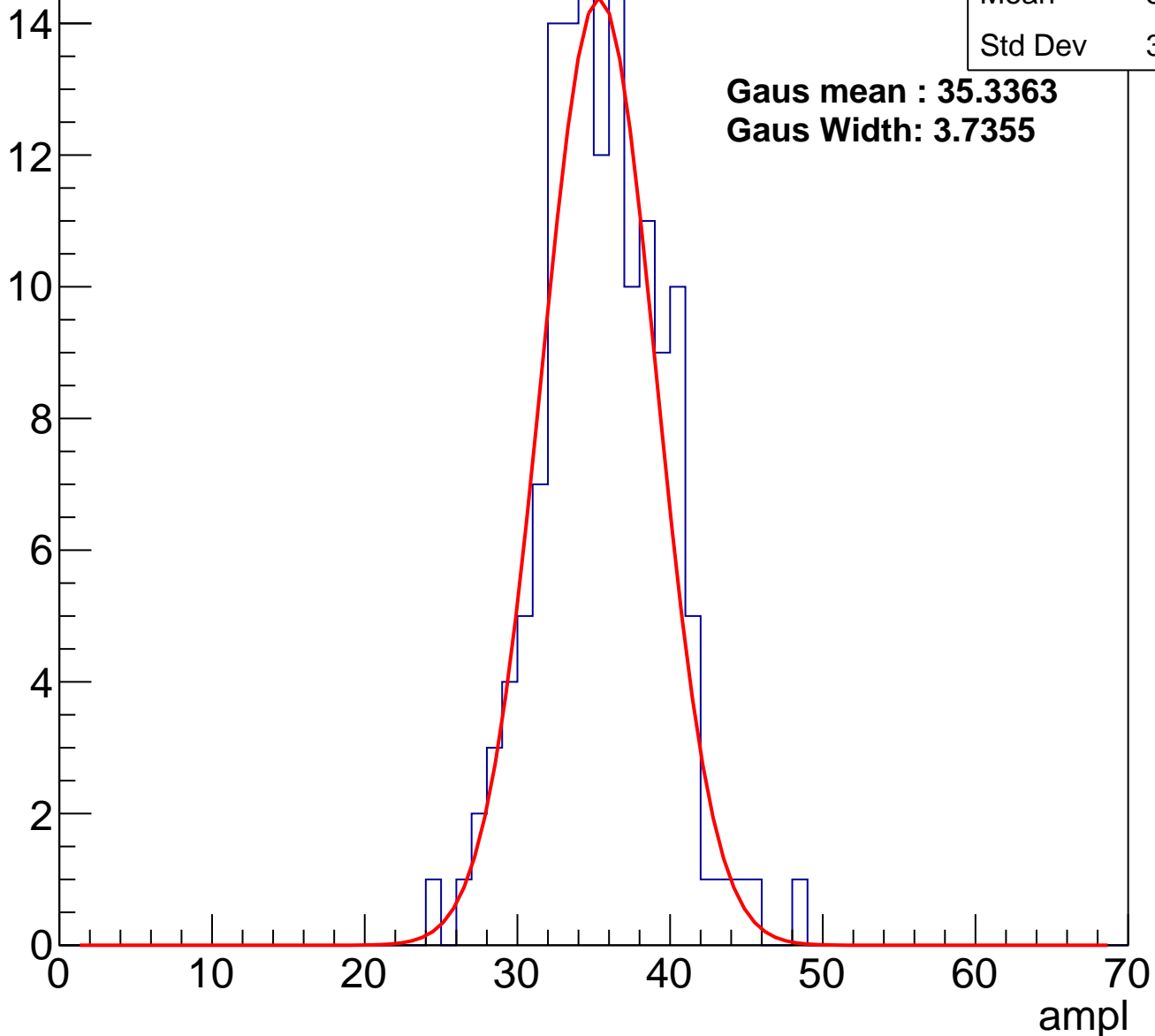
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	35.08
Std Dev	3.962

Gaus mean : 35.3363

Gaus Width: 3.7355

Entry



B0L101S, U19-ch124, adc2

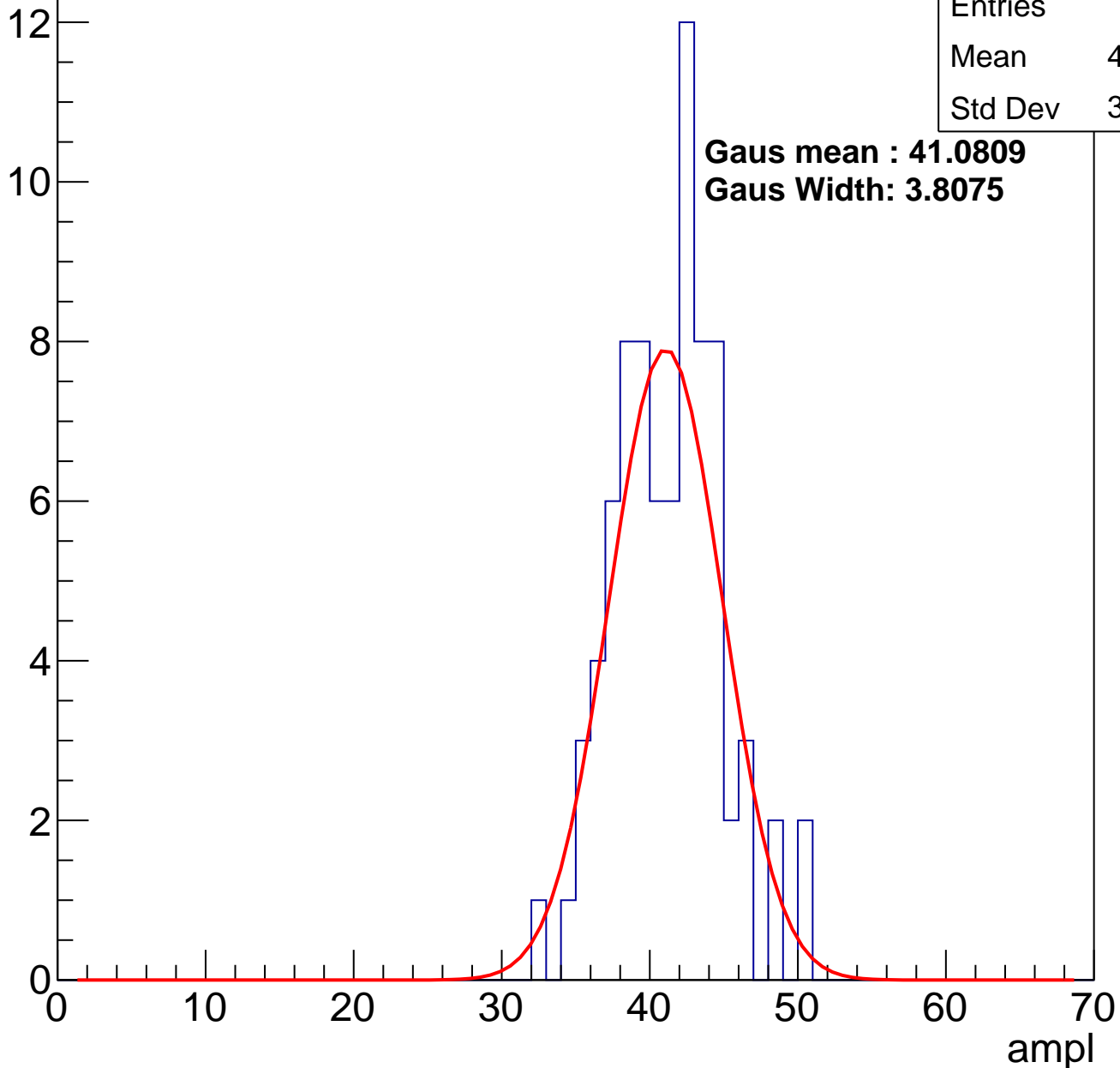
calib_packv5_042523_0143.root, FC#1, port C1

Entries	80
Mean	40.79
Std Dev	3.577

Gaus mean : 41.0809

Gaus Width: 3.8075

Entry

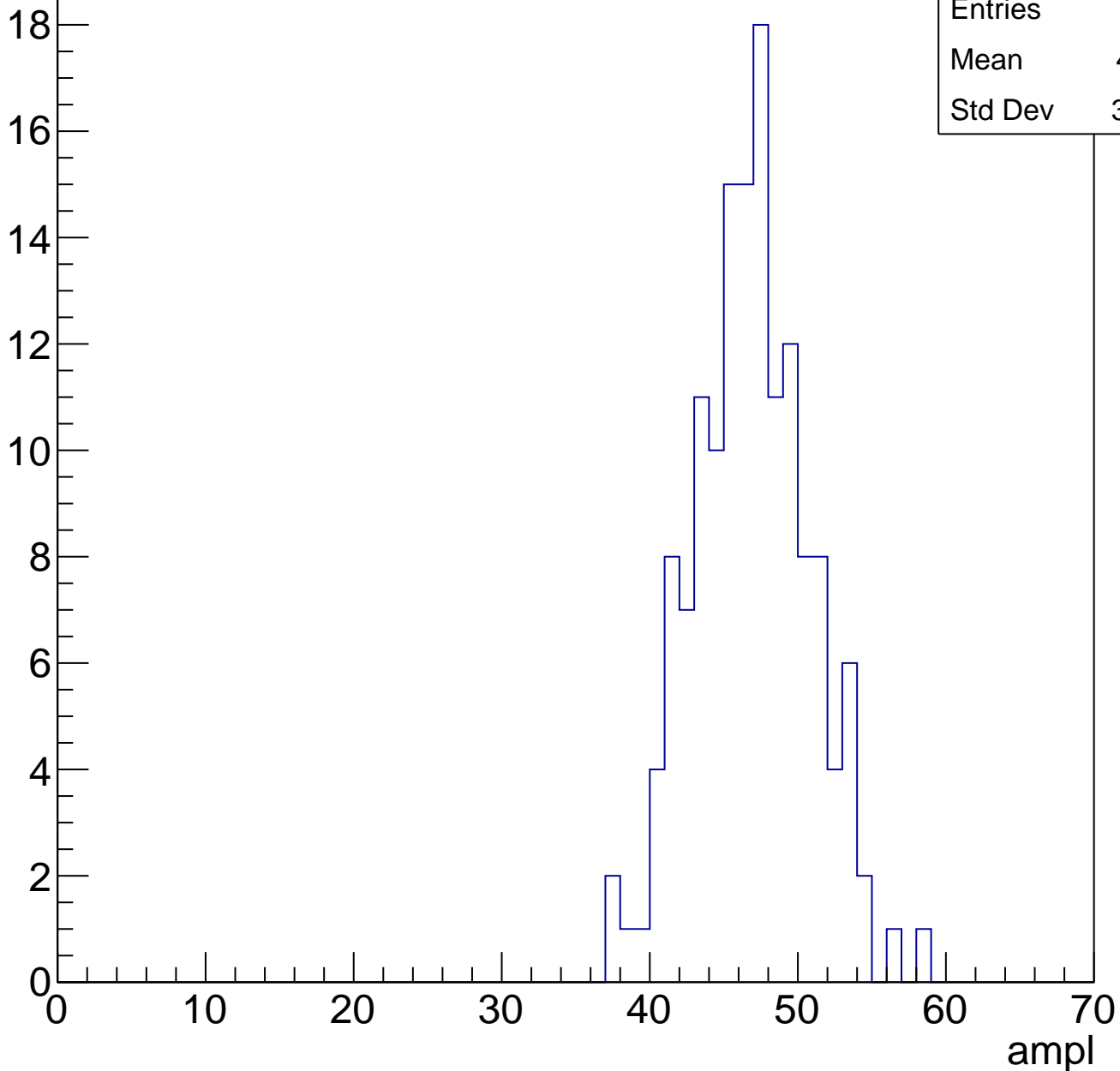


B0L101S, U19-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

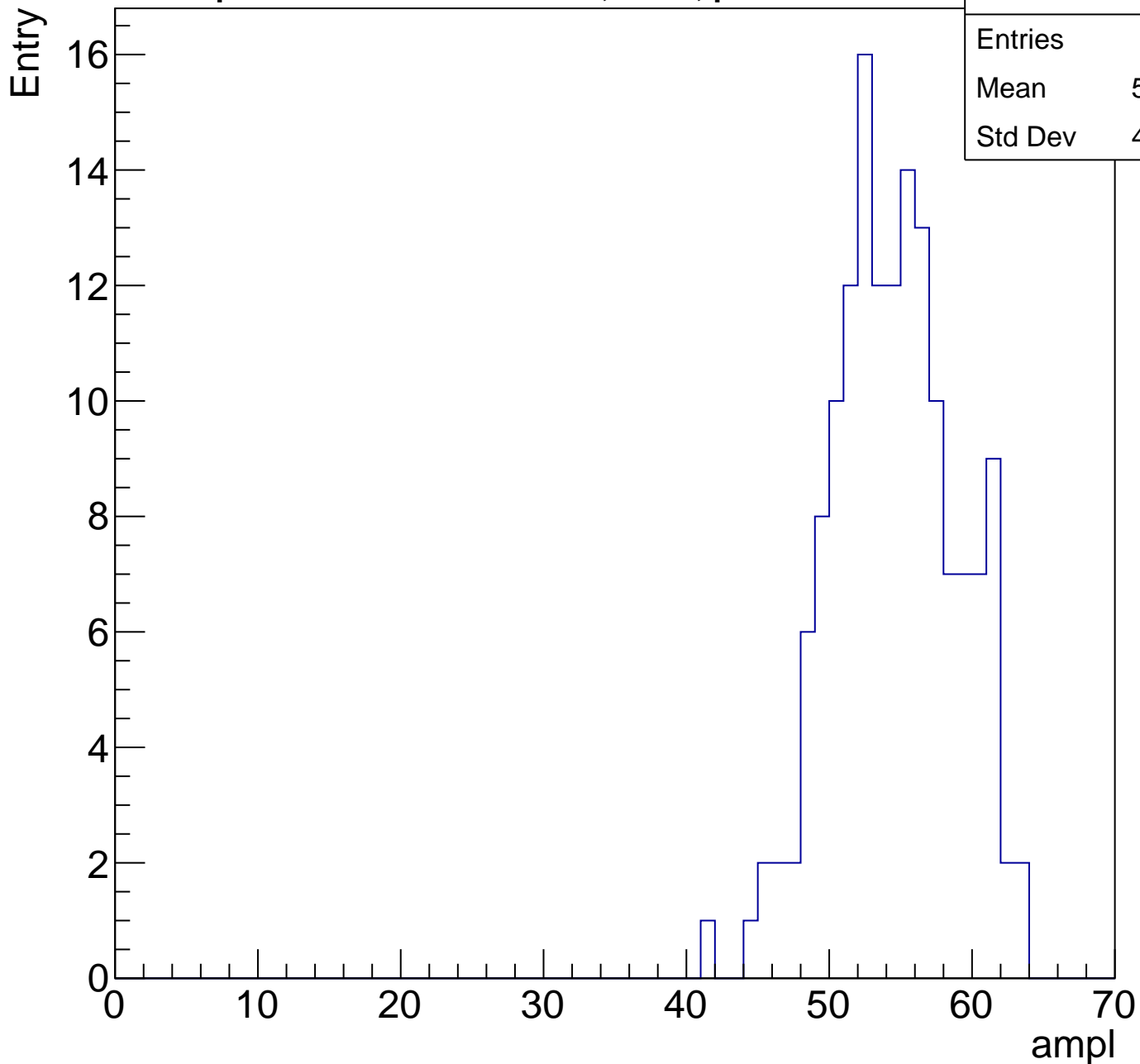
Entries	145
Mean	46.41
Std Dev	3.868



B0L101S, U19-ch124, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	54.03
Std Dev	4.283



B0L101S, U19-ch124, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

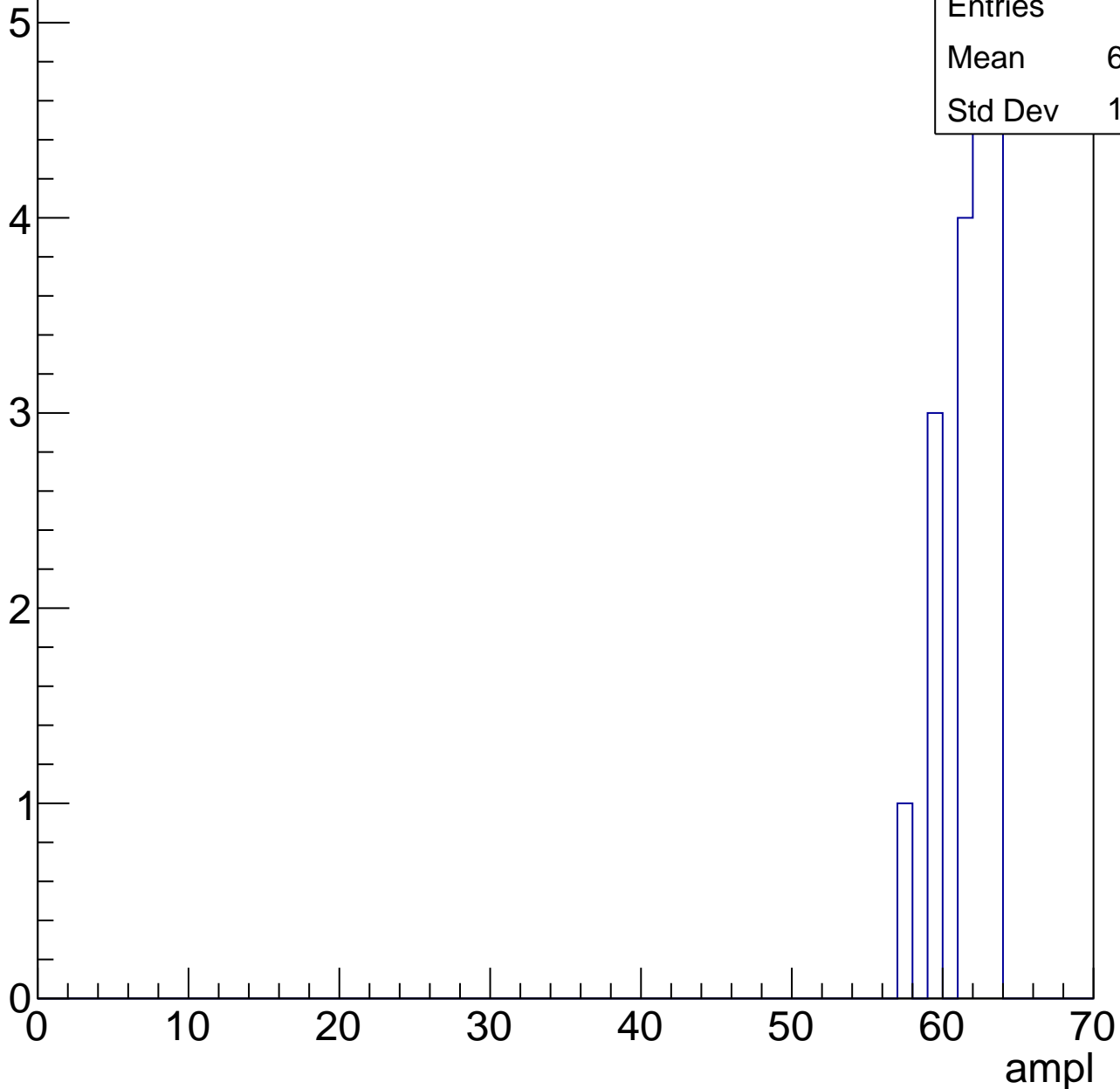
Entries	98
Mean	57.81
Std Dev	8.806

B0L101S, U19-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	61.28
Std Dev	1.693



B0L101S, U19-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch125, adc0

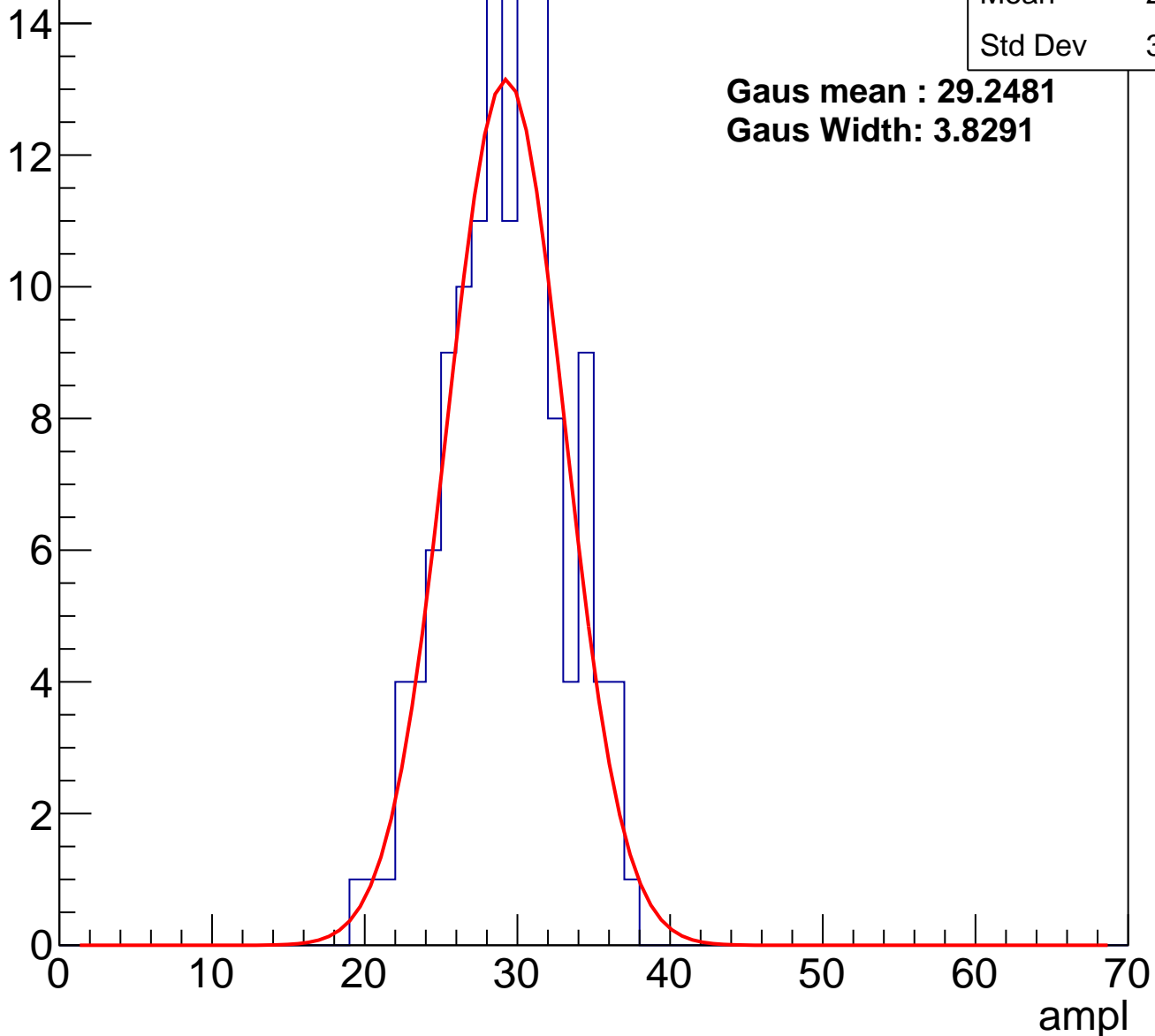
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	28.83
Std Dev	3.744

Gaus mean : 29.2481

Gaus Width: 3.8291

Entry



B0L101S, U19-ch125, adc1

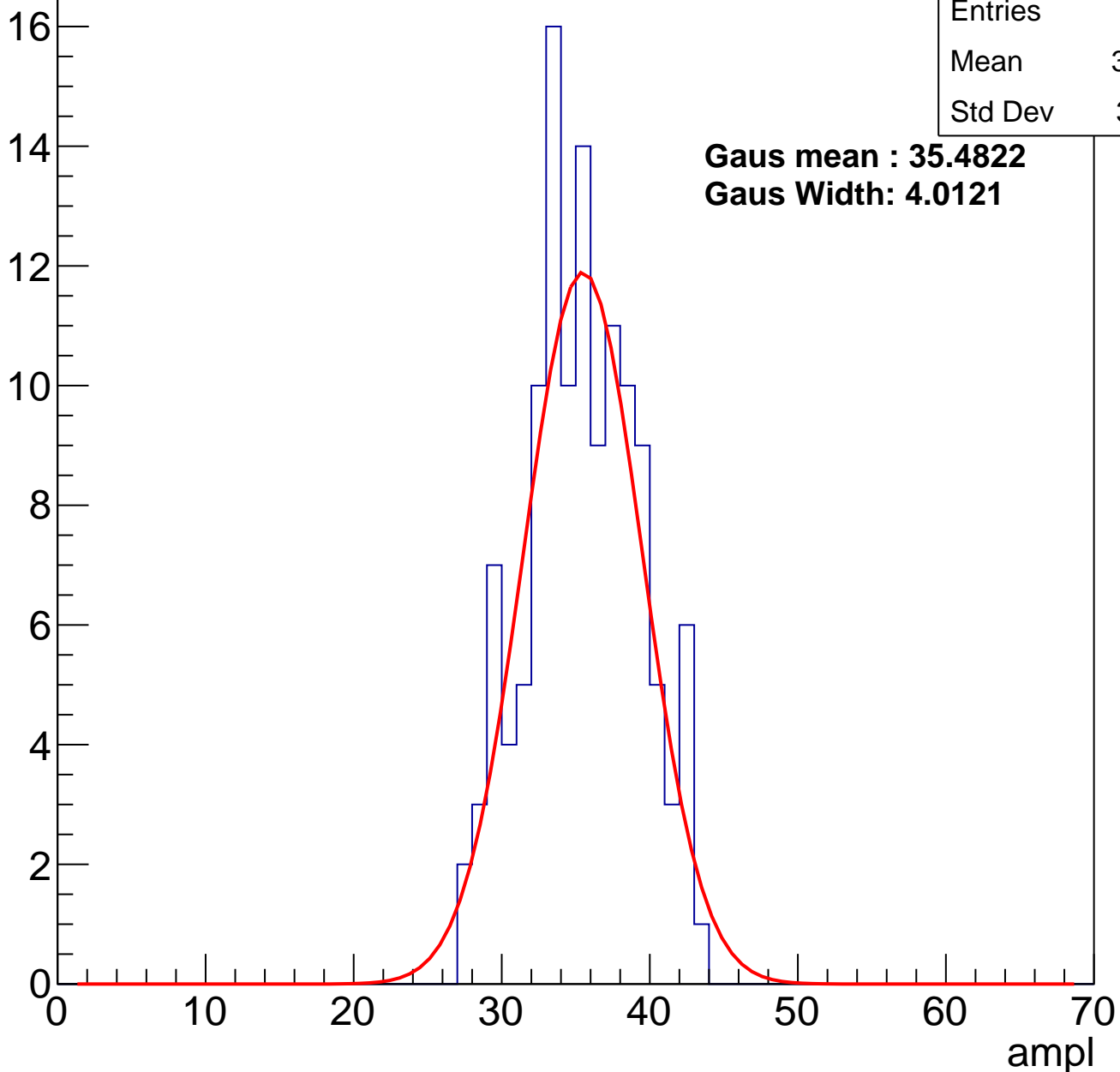
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	34.99
Std Dev	3.741

Gaus mean : 35.4822

Gaus Width: 4.0121

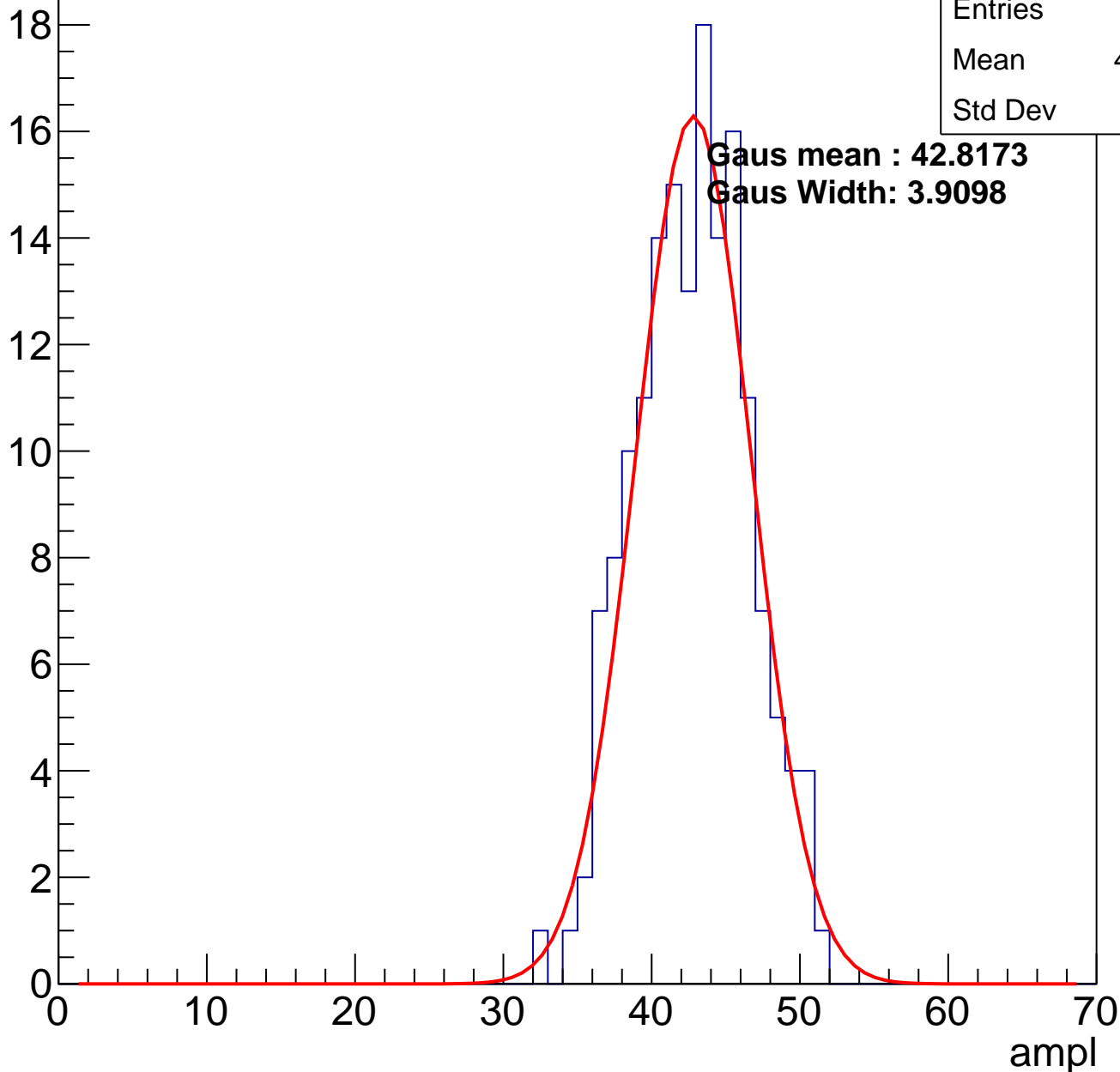
Entry



B0L101S, U19-ch125, adc2

calib_packv5_042523_0143.root, FC#1, port C1

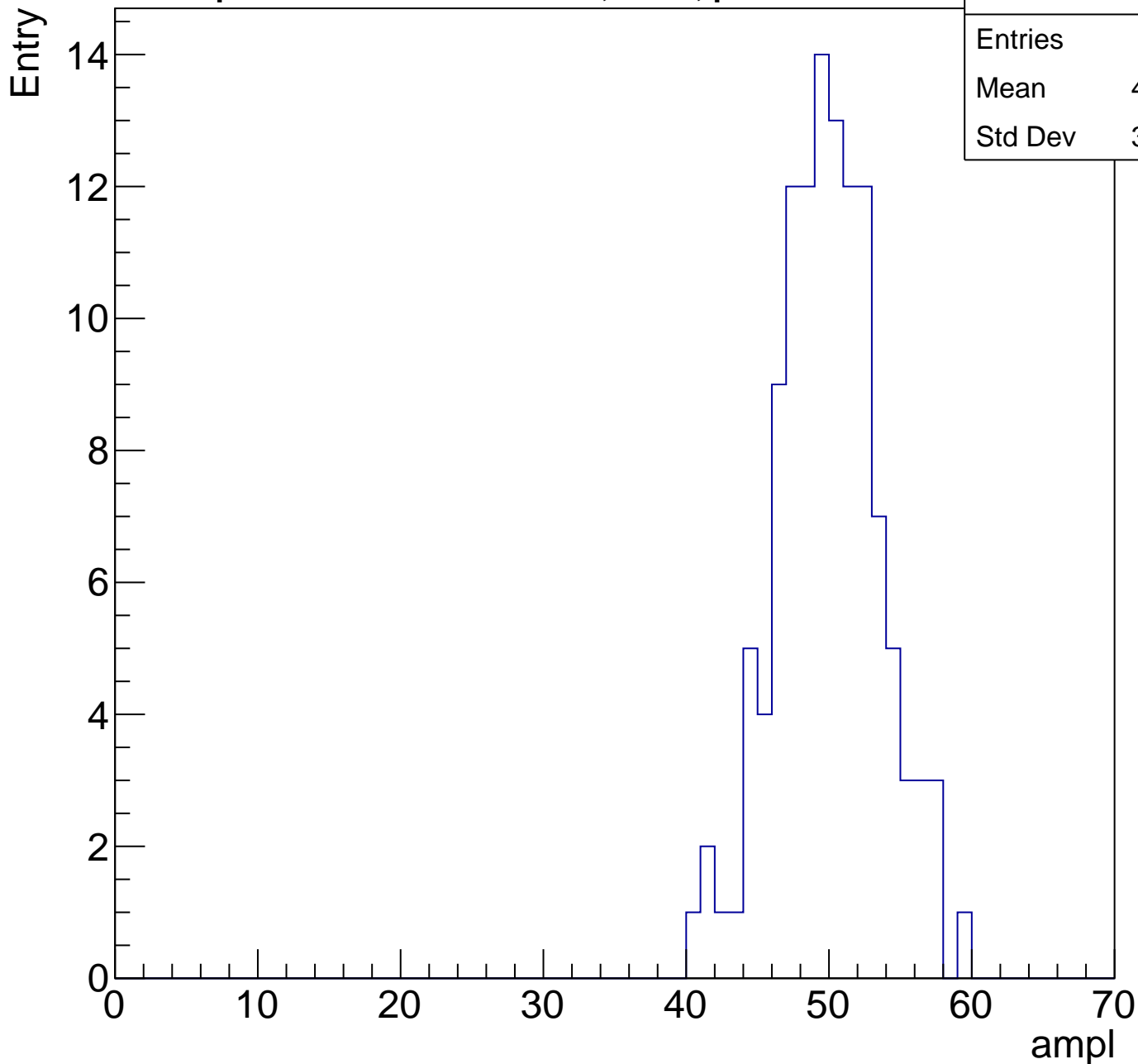
Entry



B0L101S, U19-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	49.48
Std Dev	3.594



B0L101S, U19-ch125, adc4

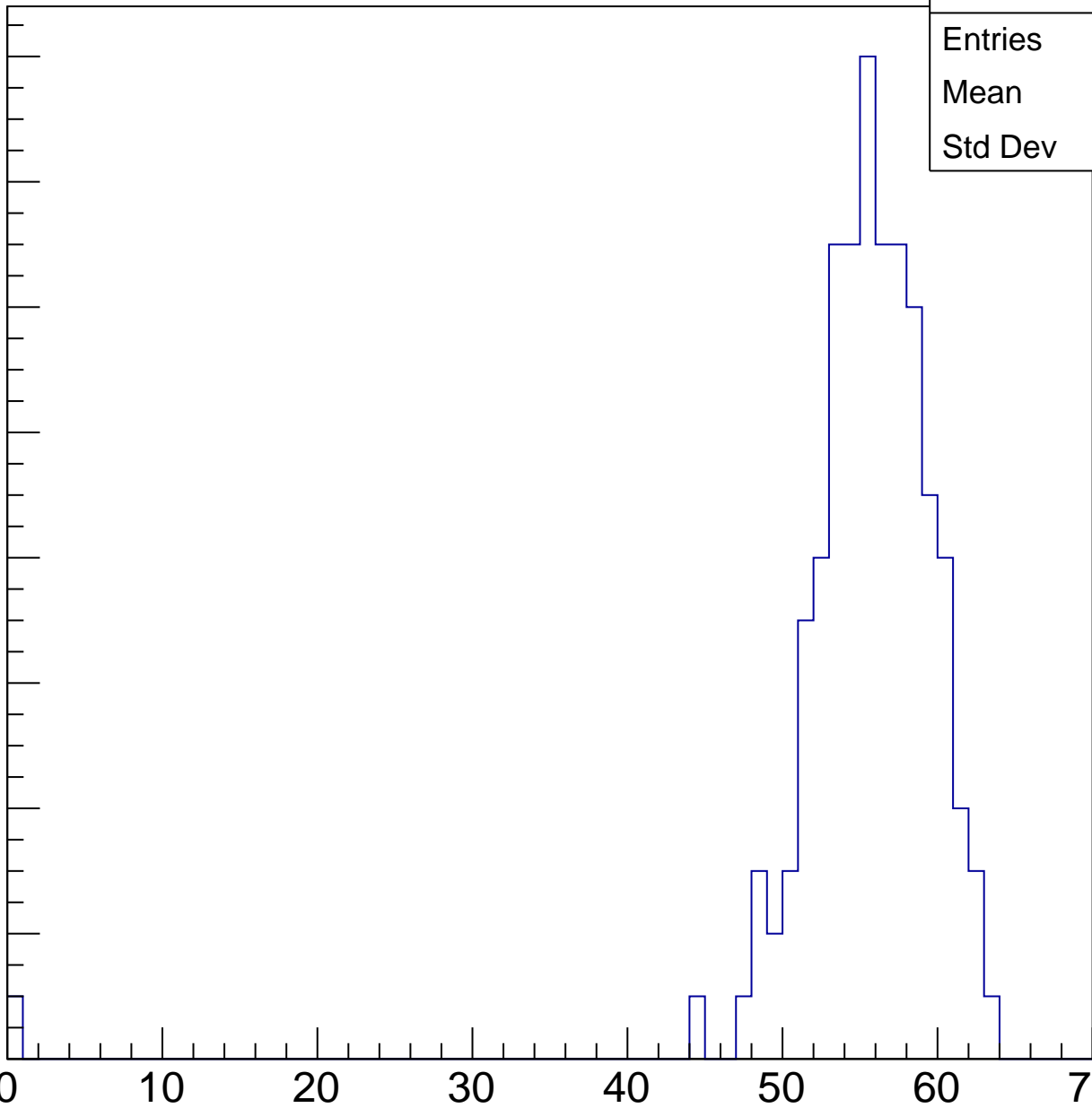
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	131
Mean	54.93
Std Dev	5.941

ampl



B0L101S, U19-ch125, adc5

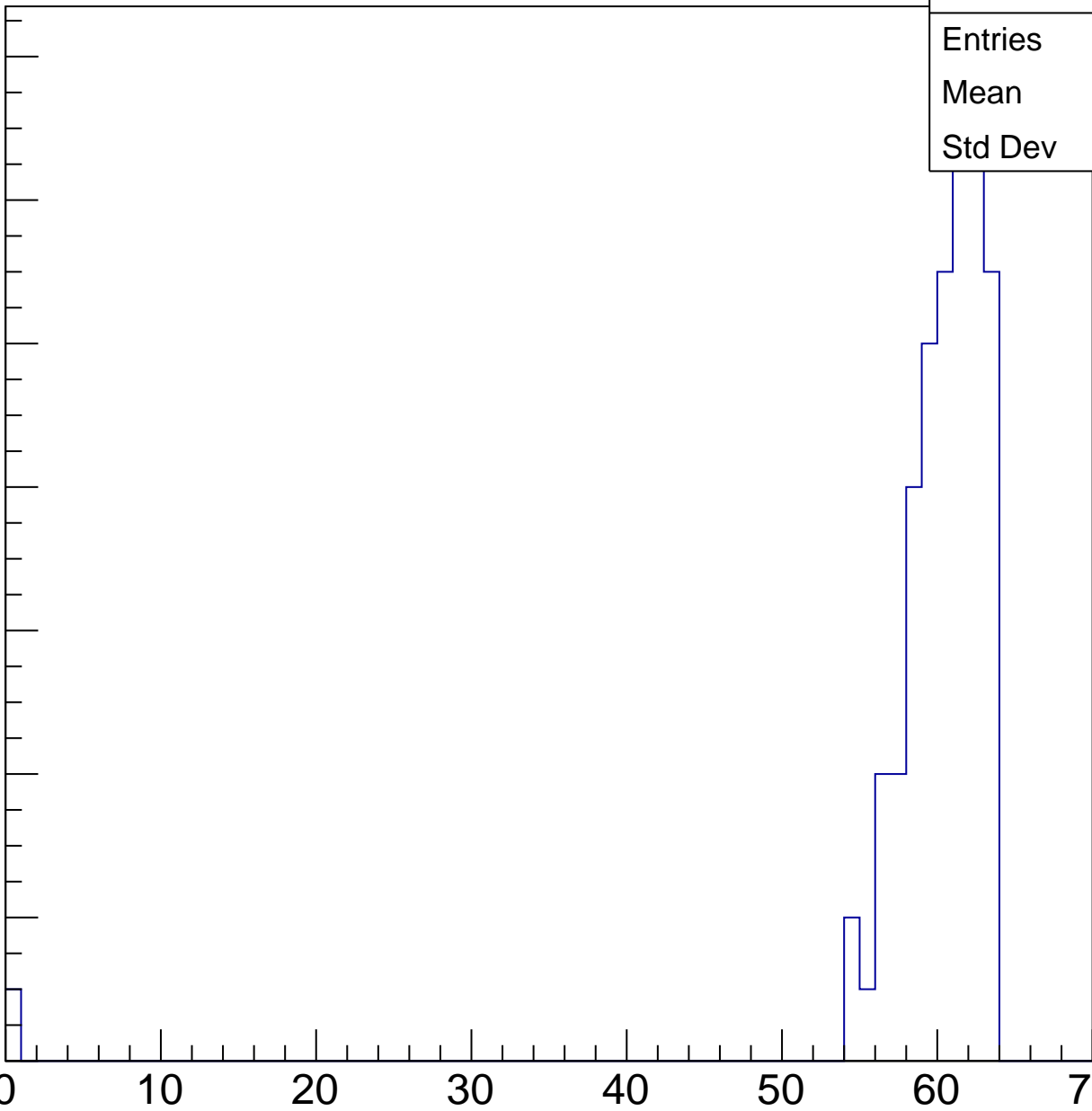
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	59.3
Std Dev	7.043

ampl

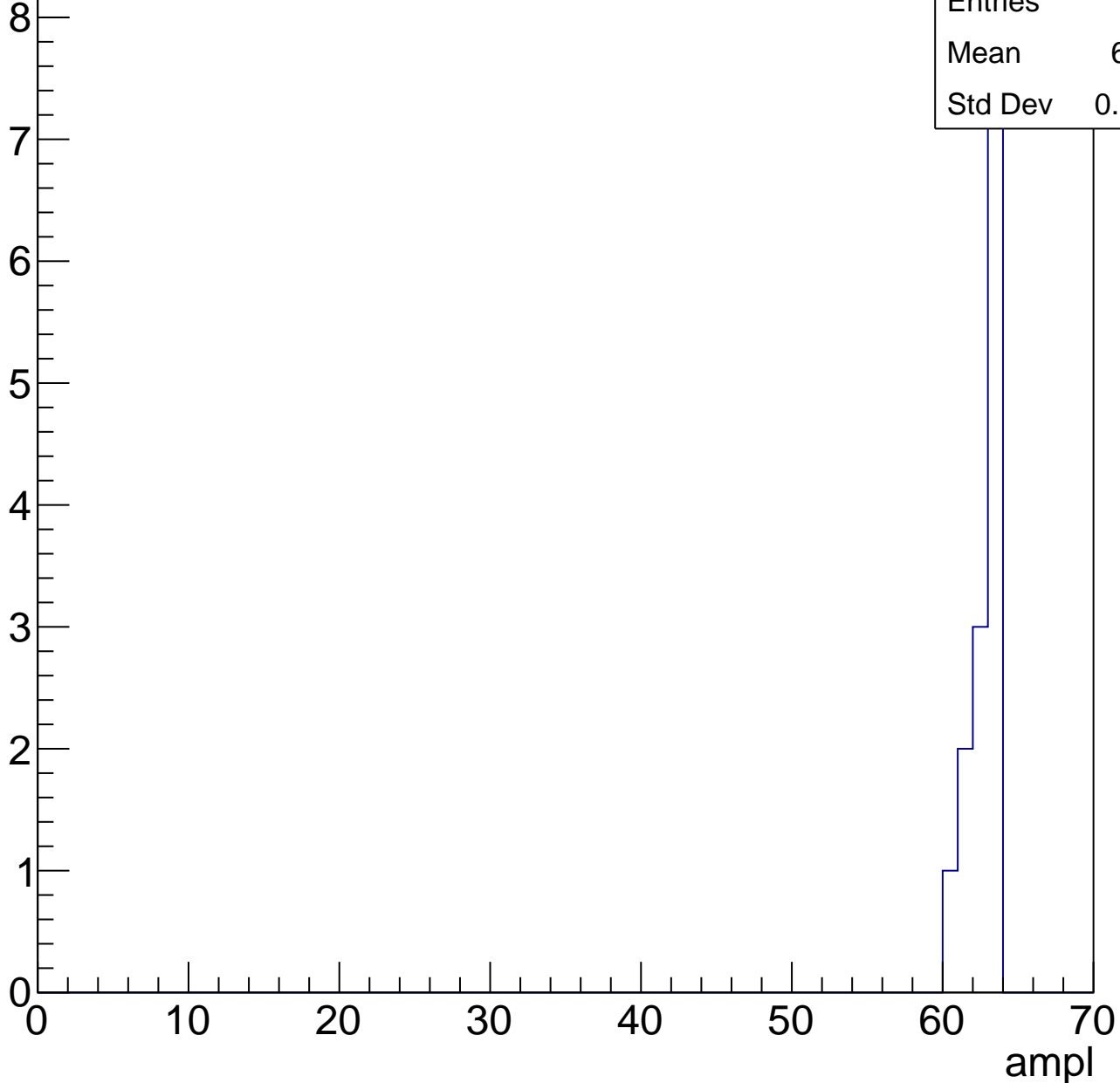


B0L101S, U19-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

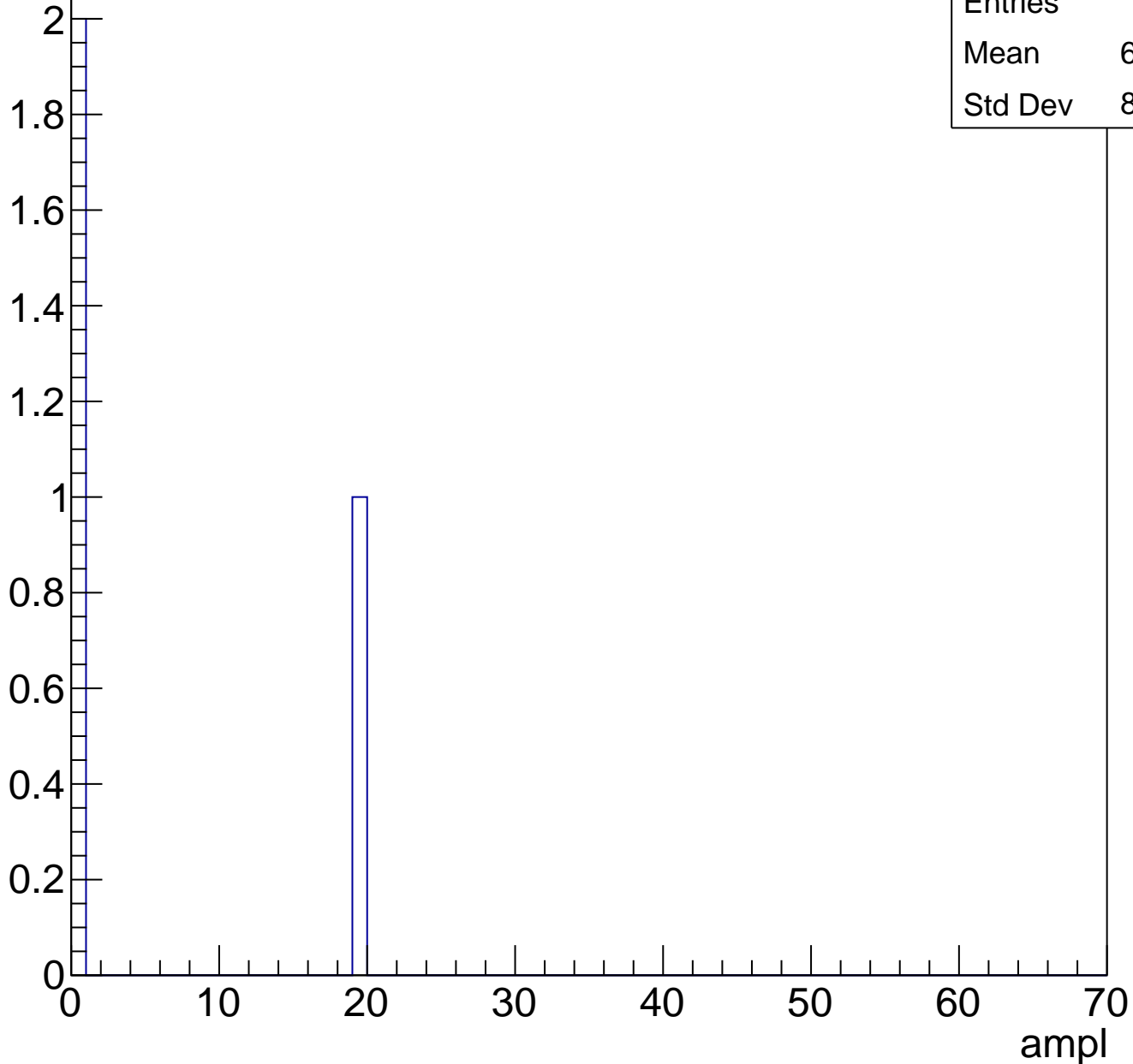
Entries	14
Mean	62.29
Std Dev	0.9583



B0L101S, U19-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B0L101S, U19-ch126, adc0

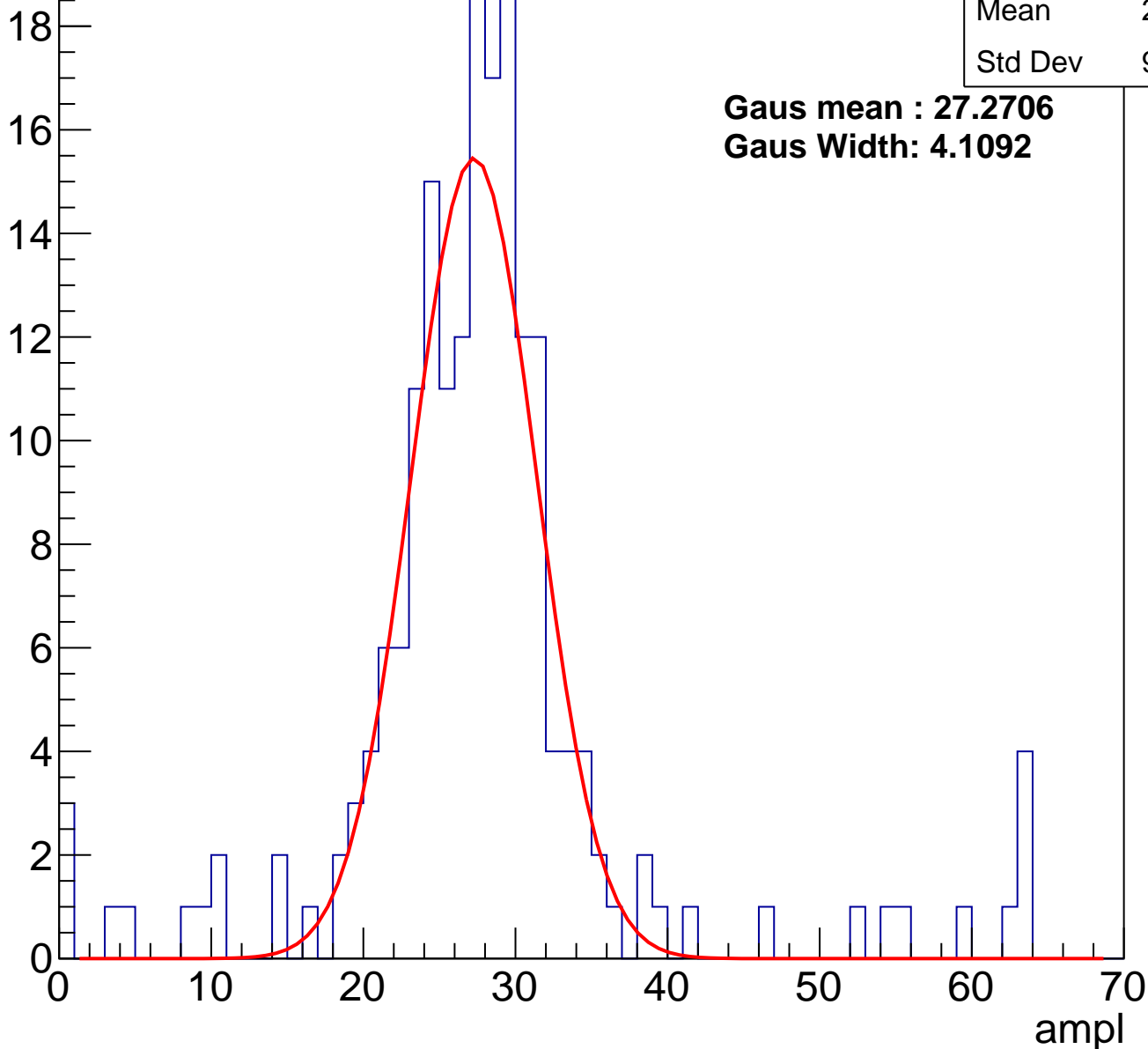
calib_packv5_042523_0143.root, FC#1, port C1

Entries	190
Mean	27.52
Std Dev	9.679

Gaus mean : 27.2706

Gaus Width: 4.1092

Entry



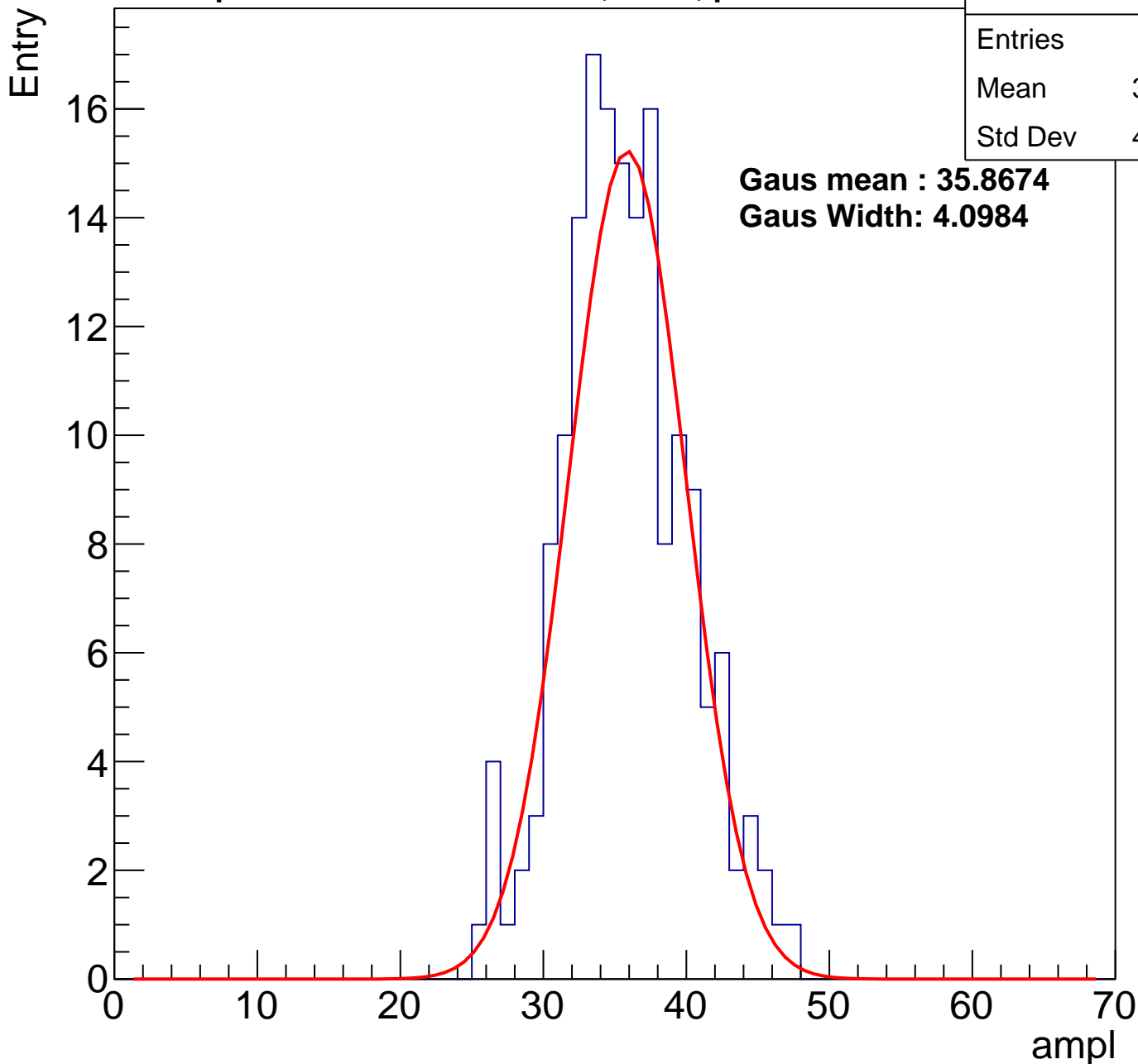
B0L101S, U19-ch126, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	35.33
Std Dev	4.289

Gaus mean : 35.8674

Gaus Width: 4.0984



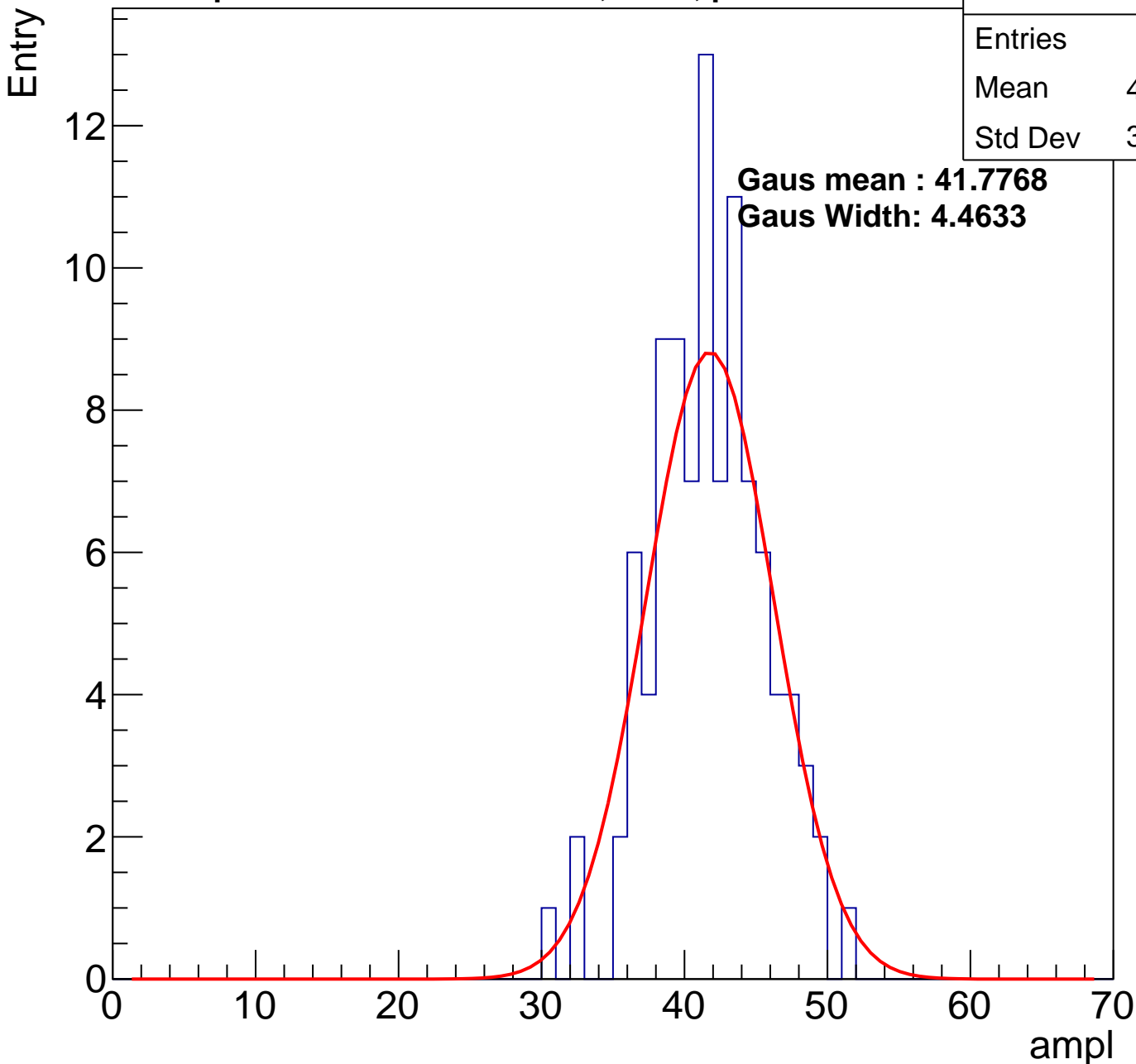
B0L101S, U19-ch126, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	98
Mean	41.27
Std Dev	3.932

Gaus mean : 41.7768

Gaus Width: 4.4633

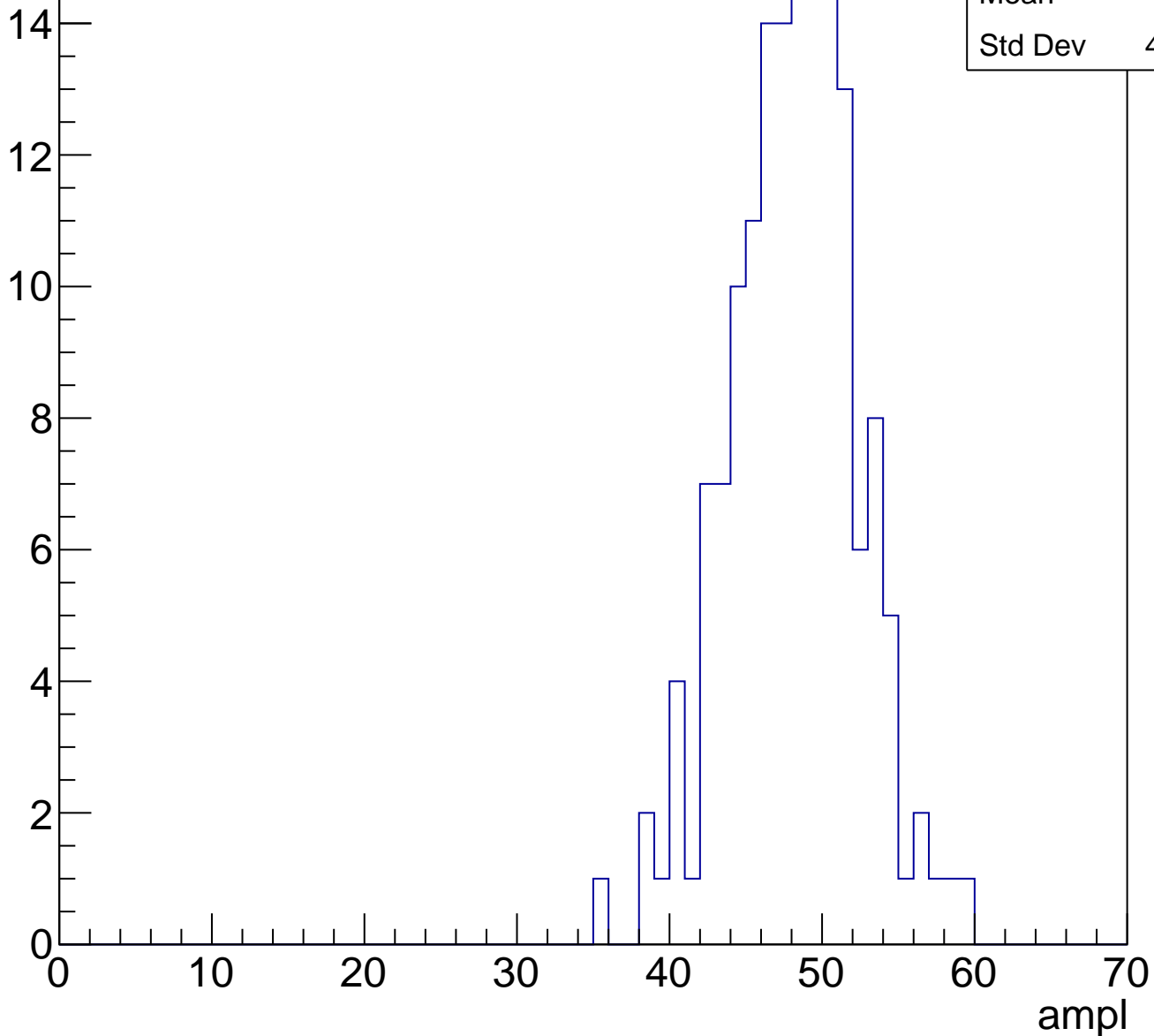


B0L101S, U19-ch126, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	47.73
Std Dev	4.132

Entry



B0L101S, U19-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

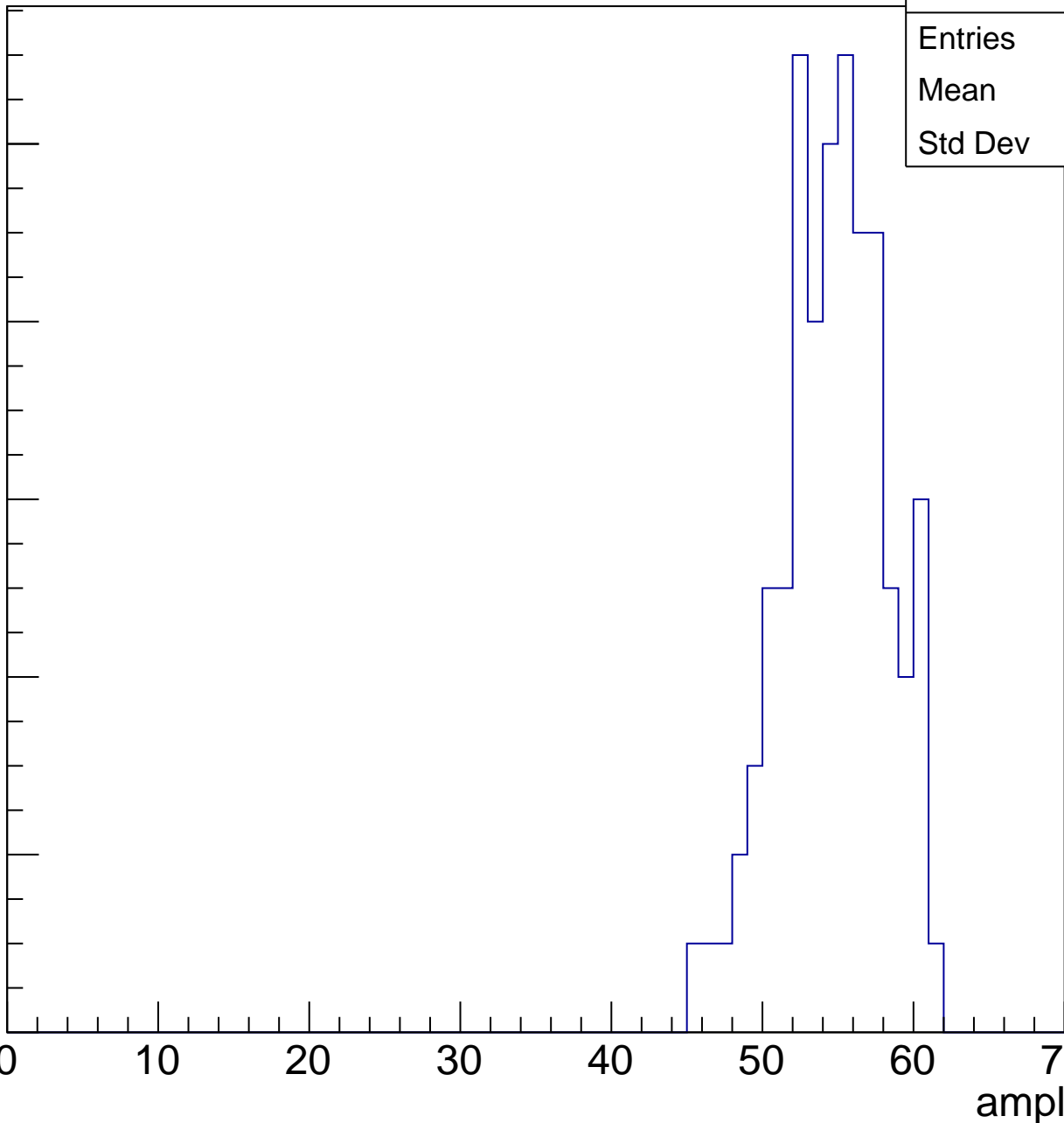
40

50

60

ampl

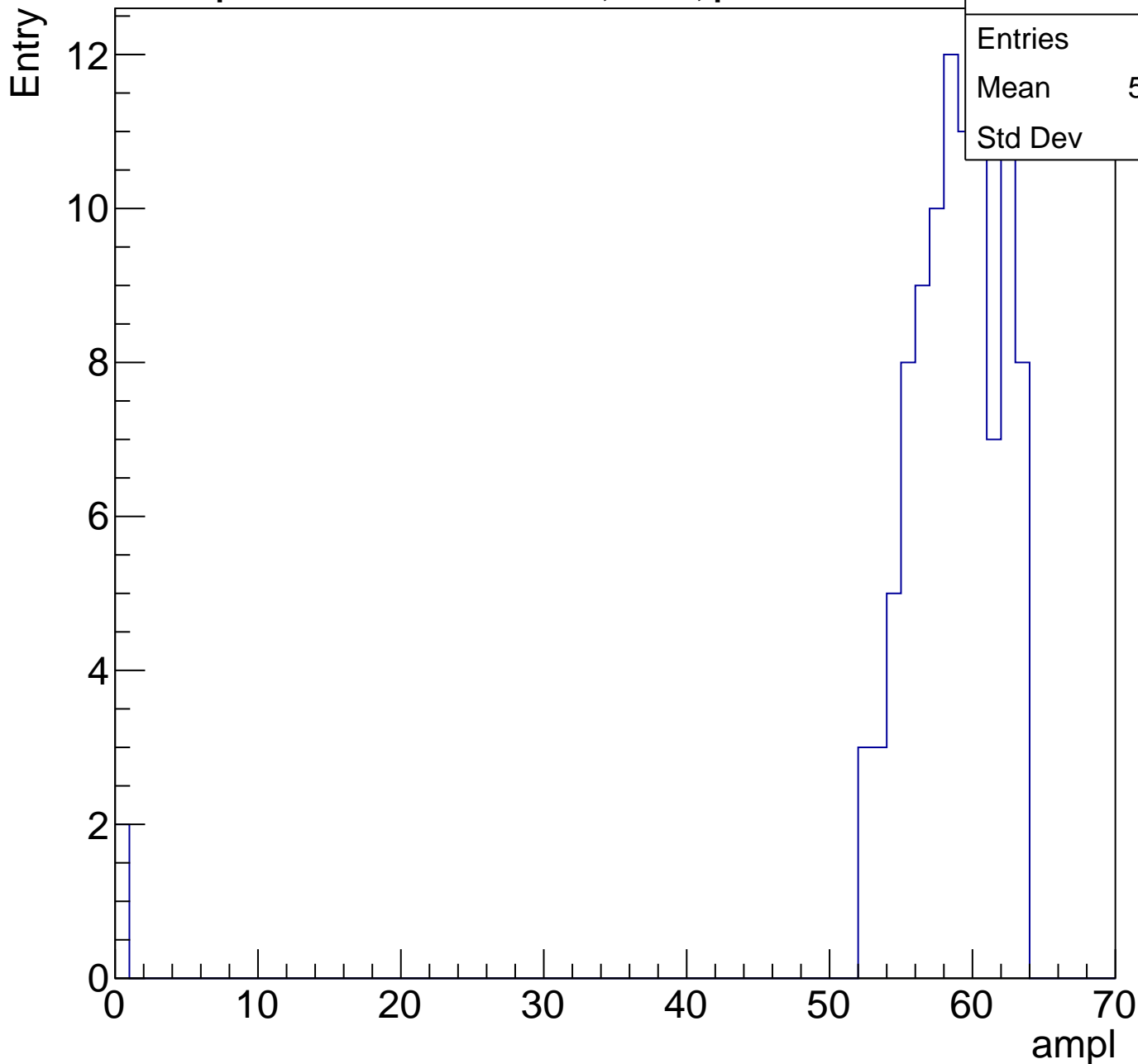
Entries	92
Mean	54.25
Std Dev	3.435



B0L101S, U19-ch126, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	57.22
Std Dev	8.65



B0L101S, U19-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

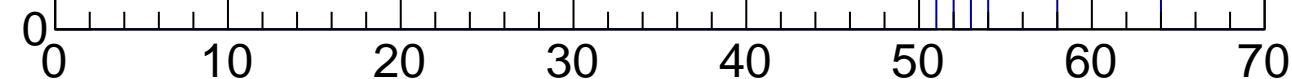
40

50

60

ampl

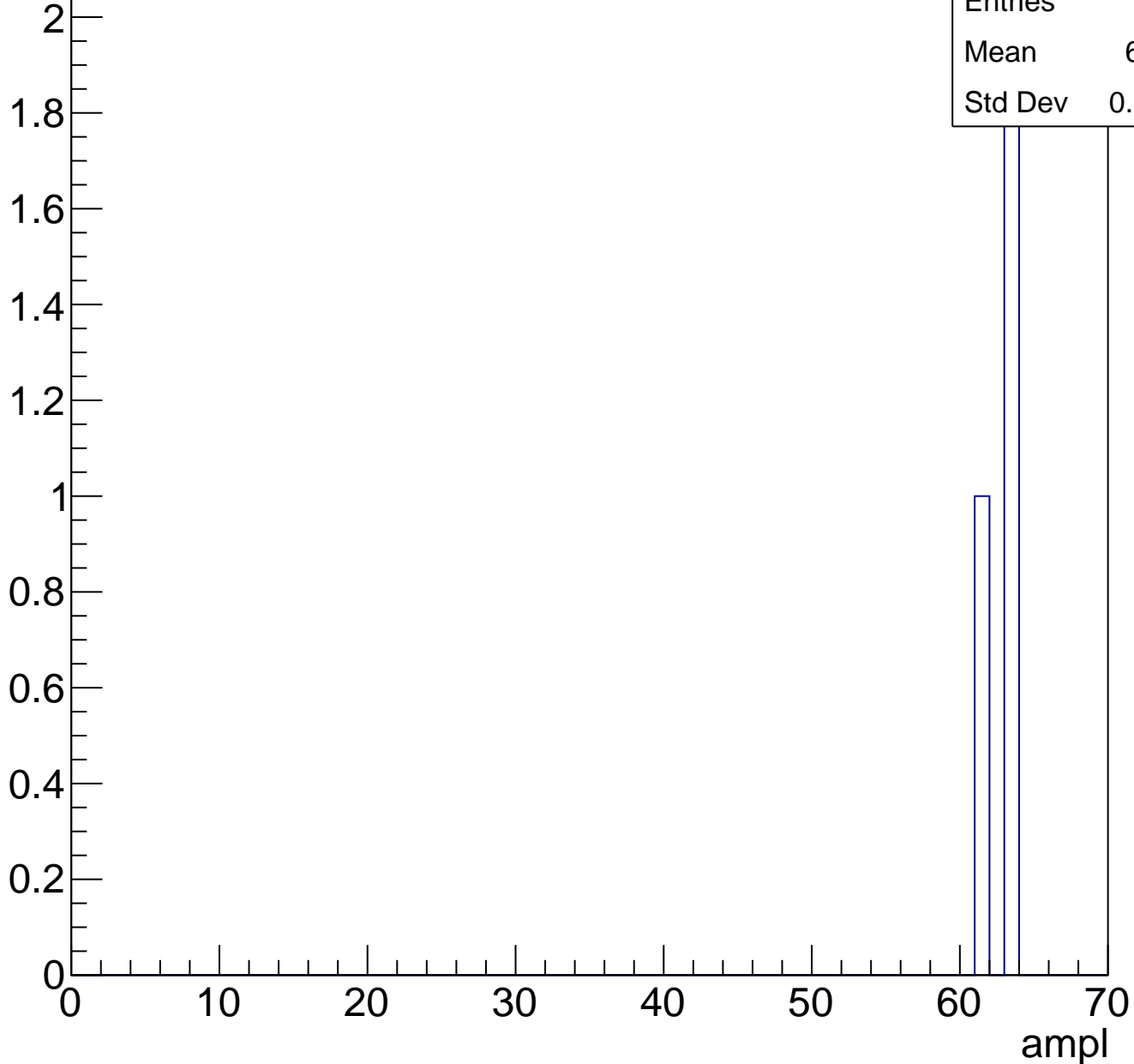
Entries	40
Mean	60.83
Std Dev	2.489



B0L101S, U19-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch127, adc0

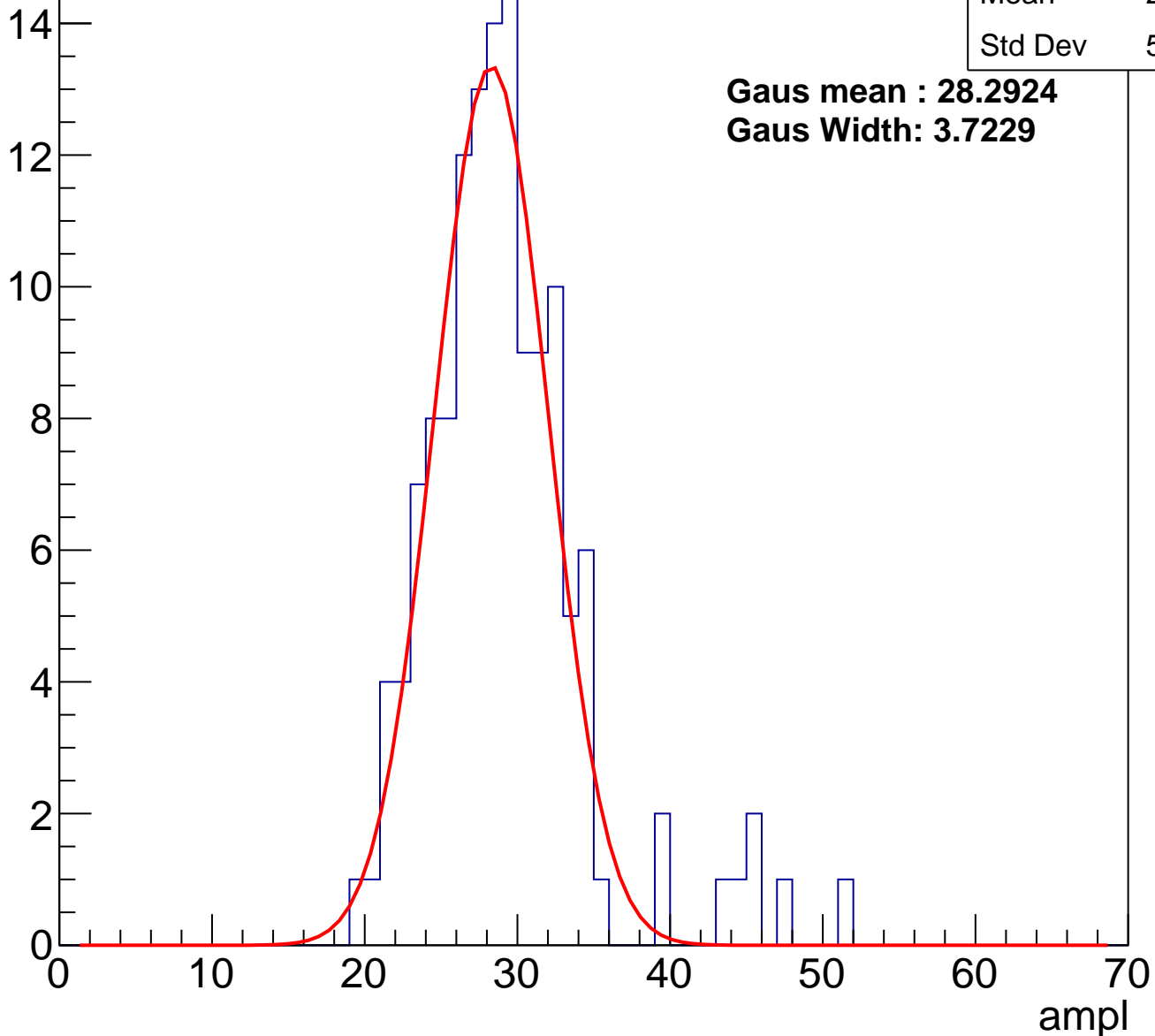
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	28.74
Std Dev	5.249

Gaus mean : 28.2924

Gaus Width: 3.7229

Entry



B0L101S, U19-ch127, adc1

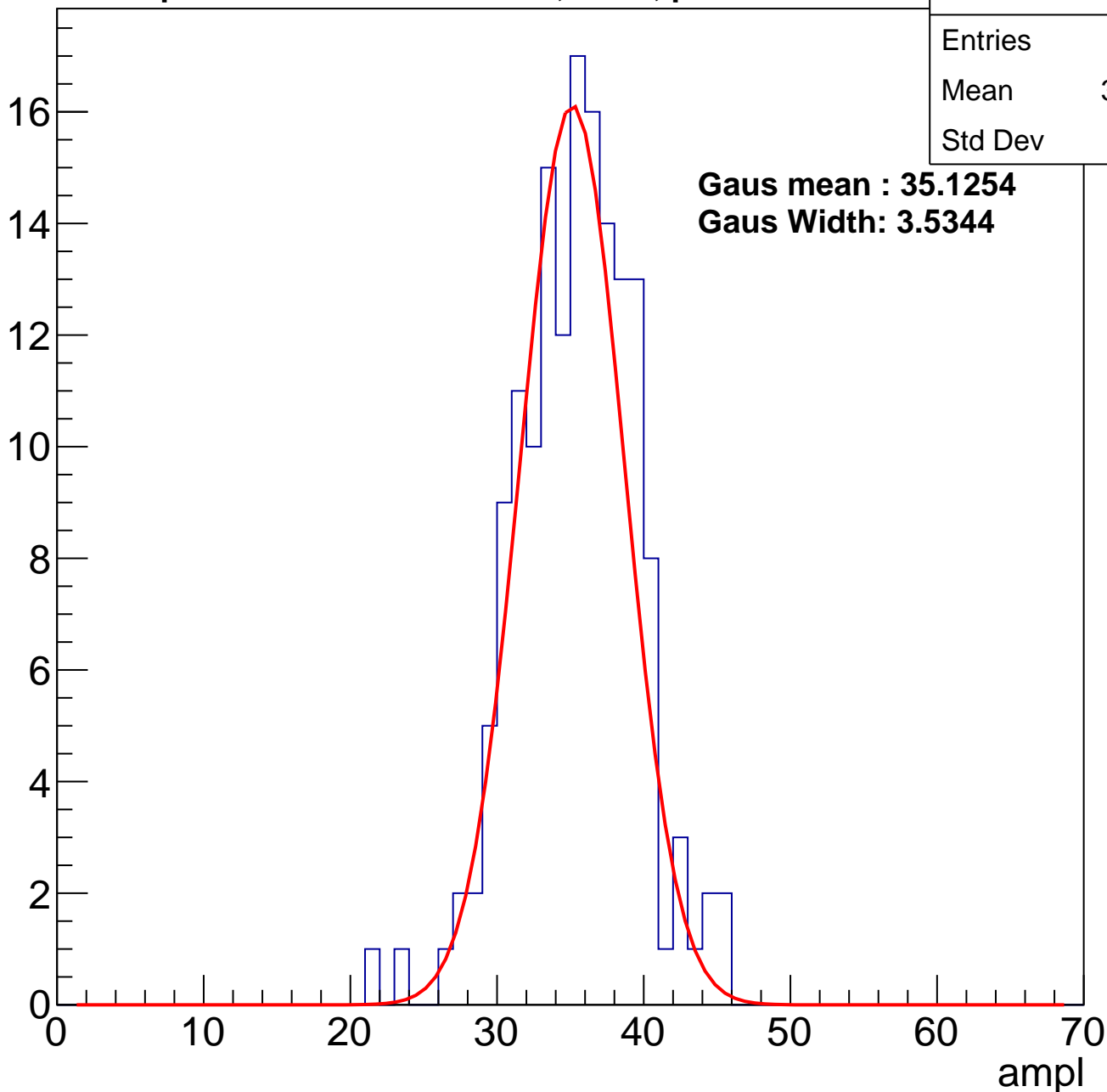
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	34.95
Std Dev	4.04

Gaus mean : 35.1254

Gaus Width: 3.5344

Entry



B0L101S, U19-ch127, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	42.28
Std Dev	3.352

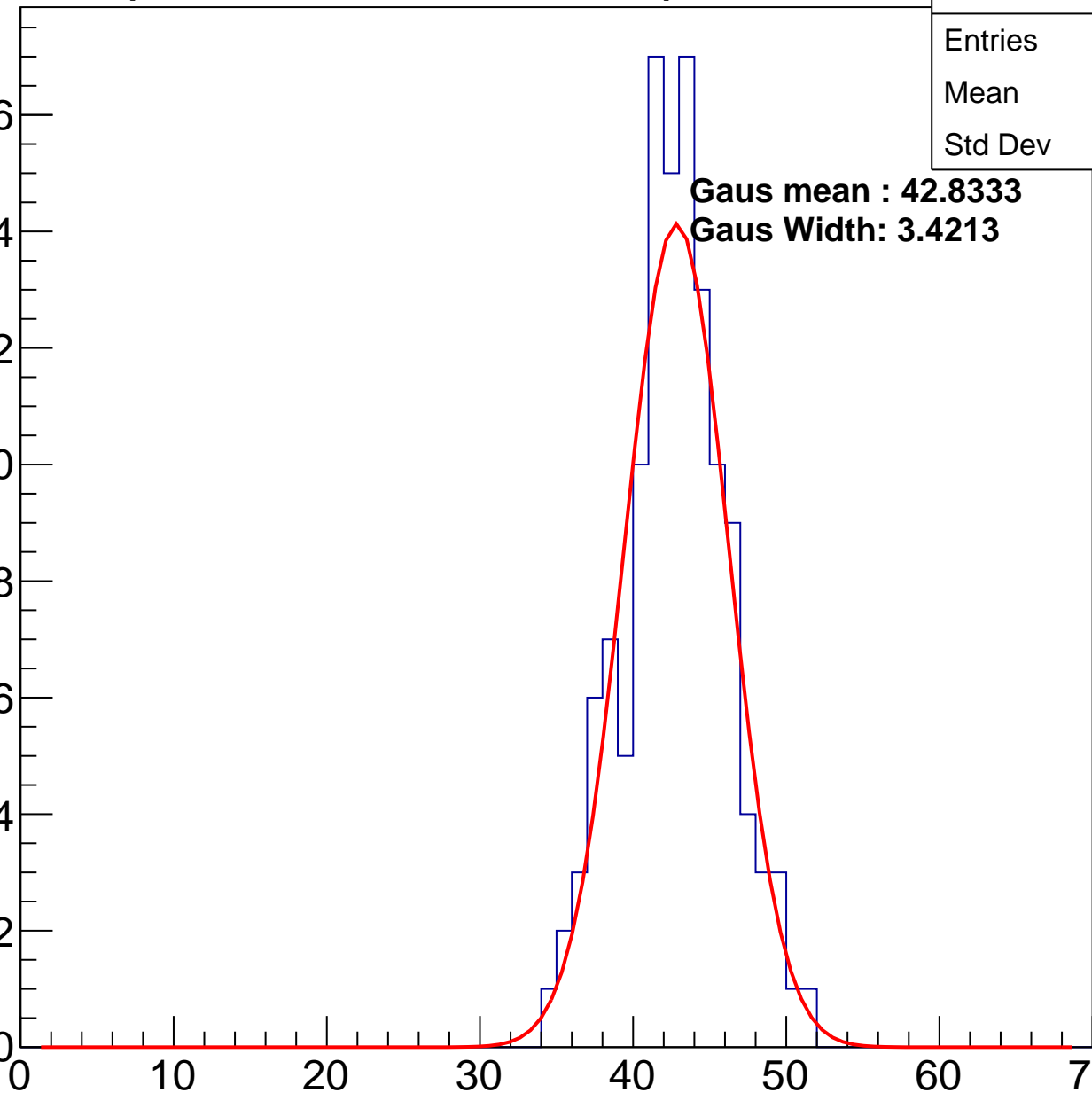
Entry

Gaus mean : 42.8333

Gaus Width: 3.4213

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U19-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	138
Mean	49.09
Std Dev	3.787

0

2

4

6

8

10

12

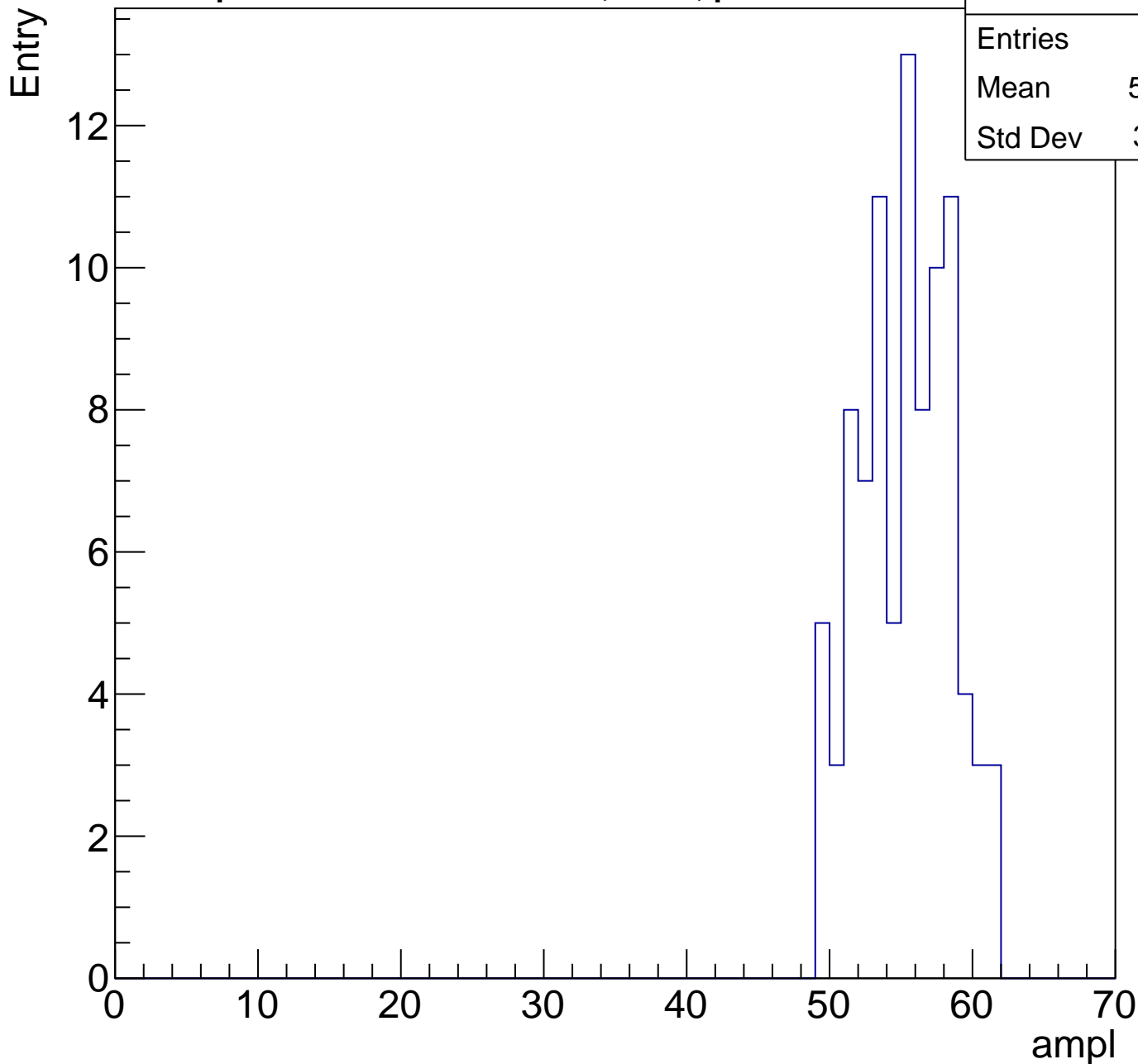
14

16

B0L101S, U19-ch127, adc4

calib_packv5_042523_0143.root, FC#1, port C1

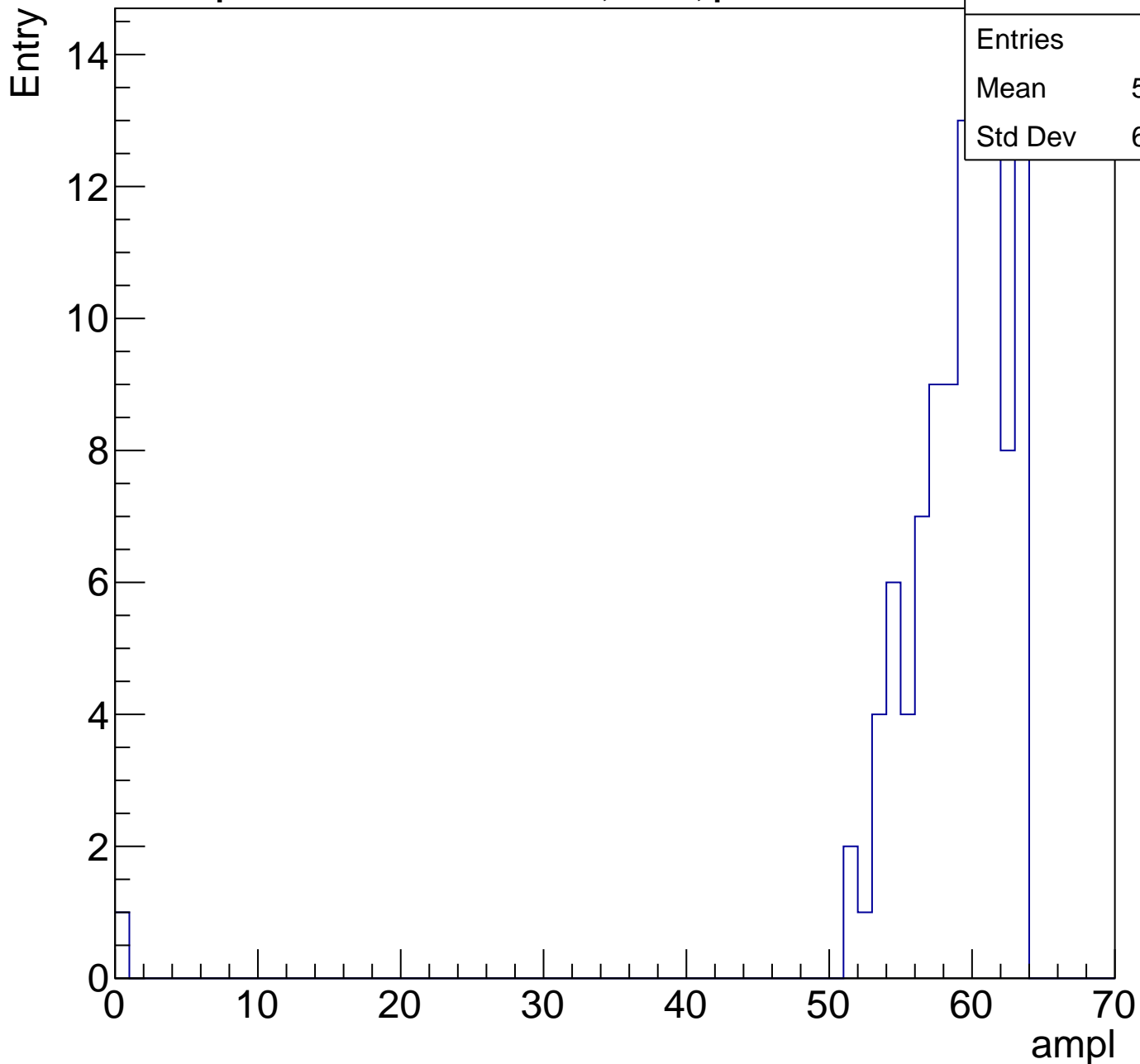
Entries	91
Mean	54.84
Std Dev	3.121



B0L101S, U19-ch127, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	58.24
Std Dev	6.492

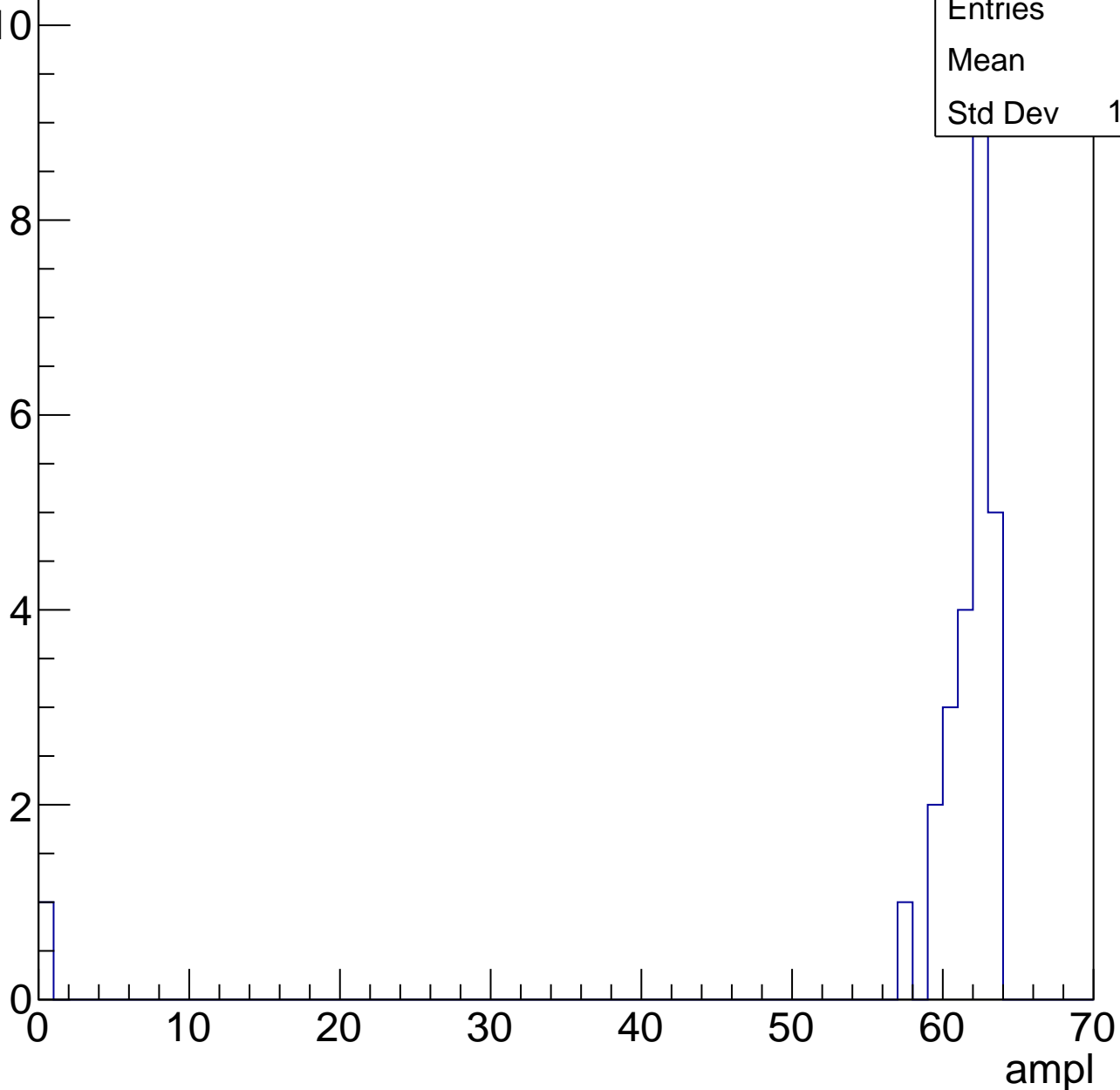


B0L101S, U19-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

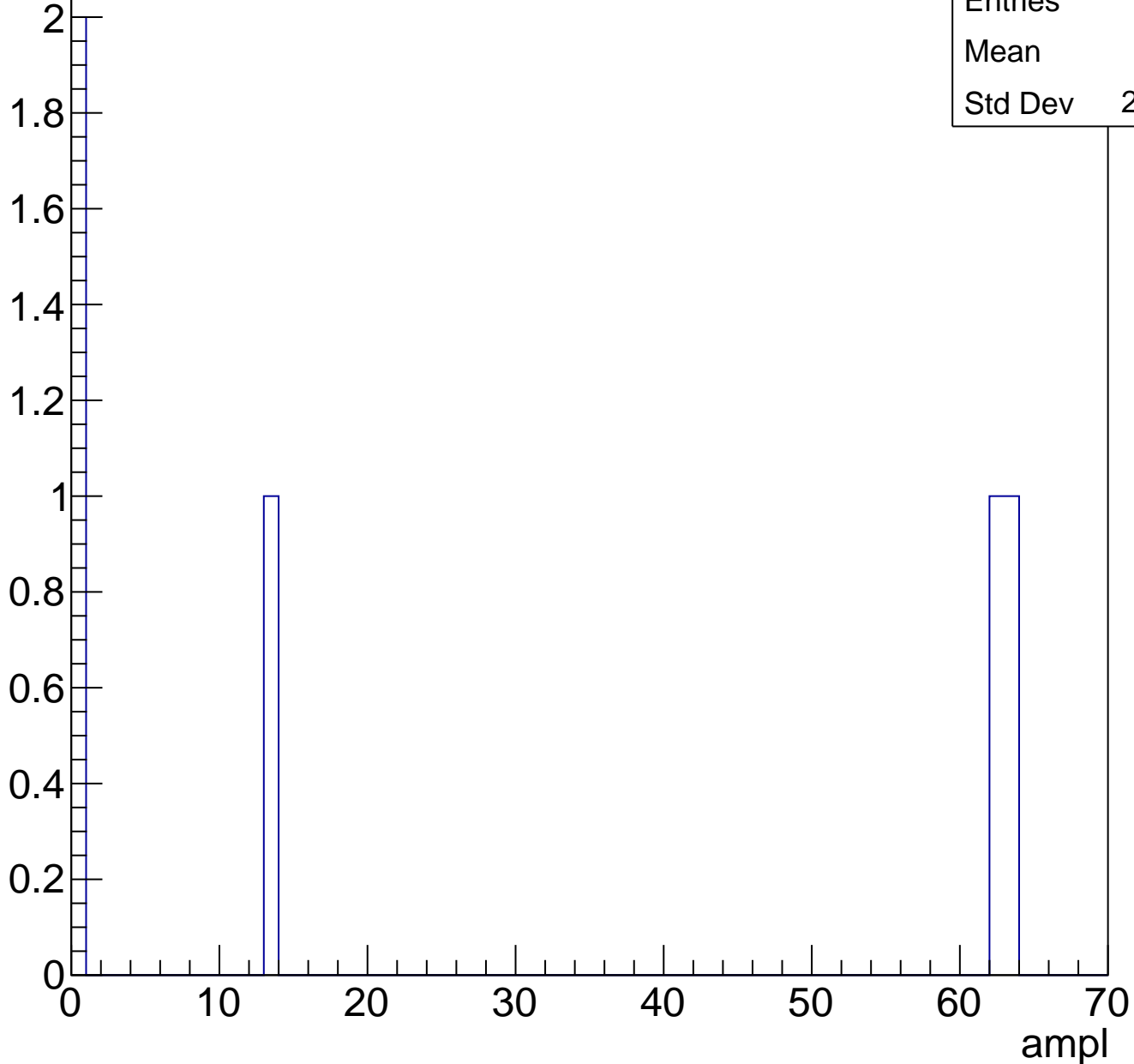
Entries	26
Mean	59
Std Dev	11.89



B0L101S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

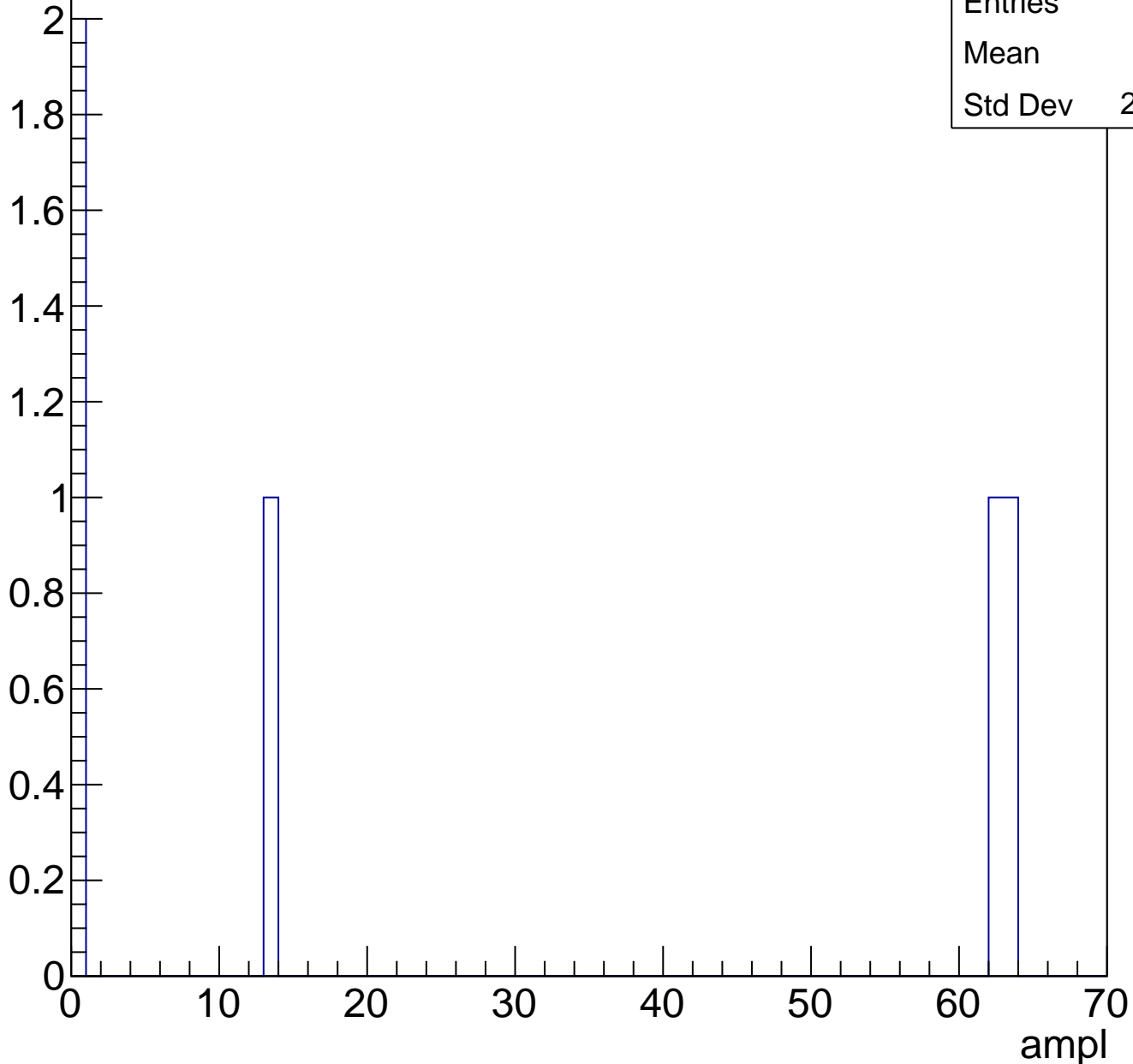


Entries	5
Mean	27.6
Std Dev	28.89

B0L101S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	27.6
Std Dev	28.89