

B1L101S, U25-ch0, adc0

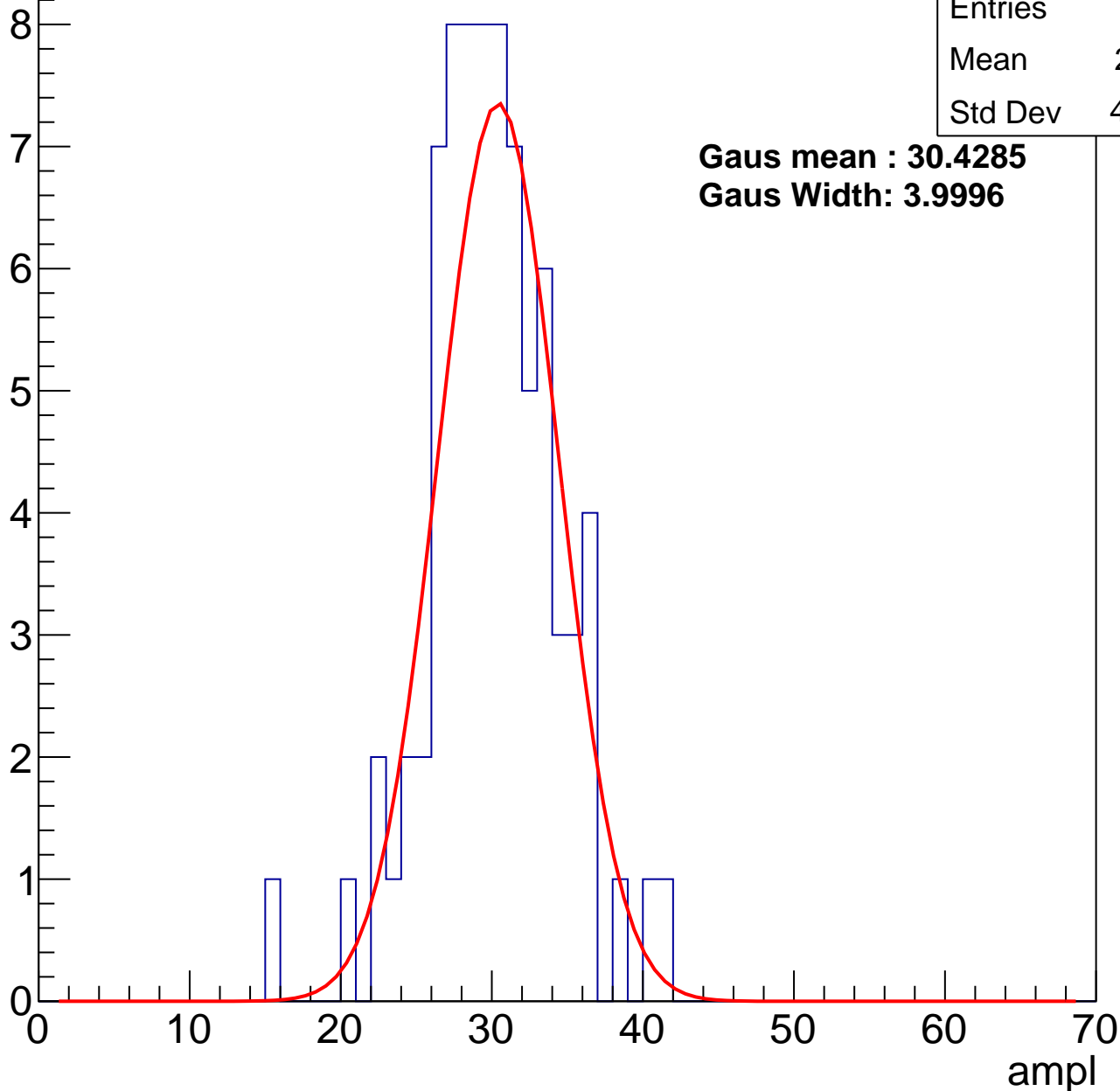
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.61
Std Dev	4.312

Gaus mean : 30.4285

Gaus Width: 3.9996



B1L101S, U25-ch0, adc1

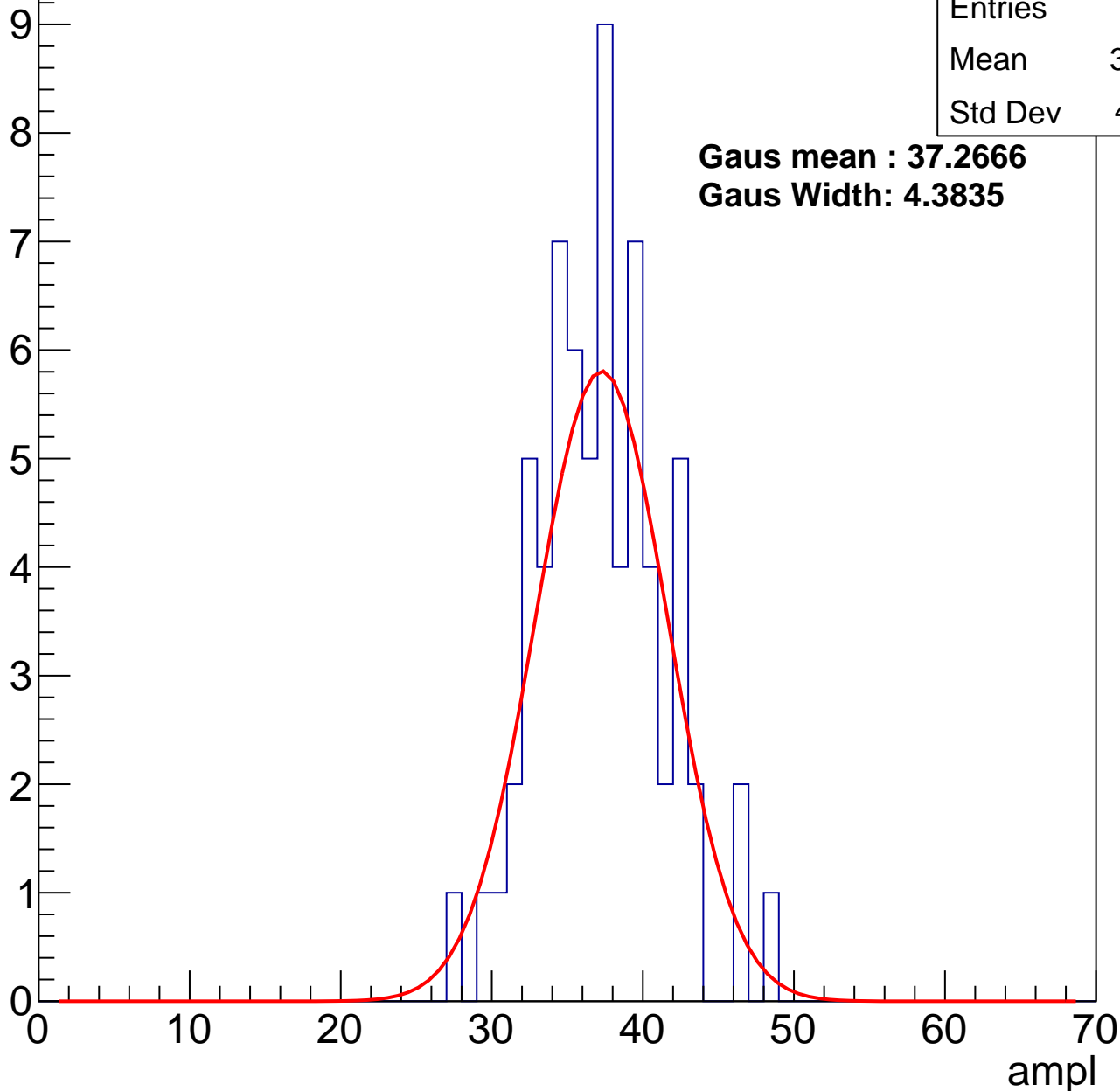
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.82
Std Dev	4.101

Gaus mean : 37.2666

Gaus Width: 4.3835

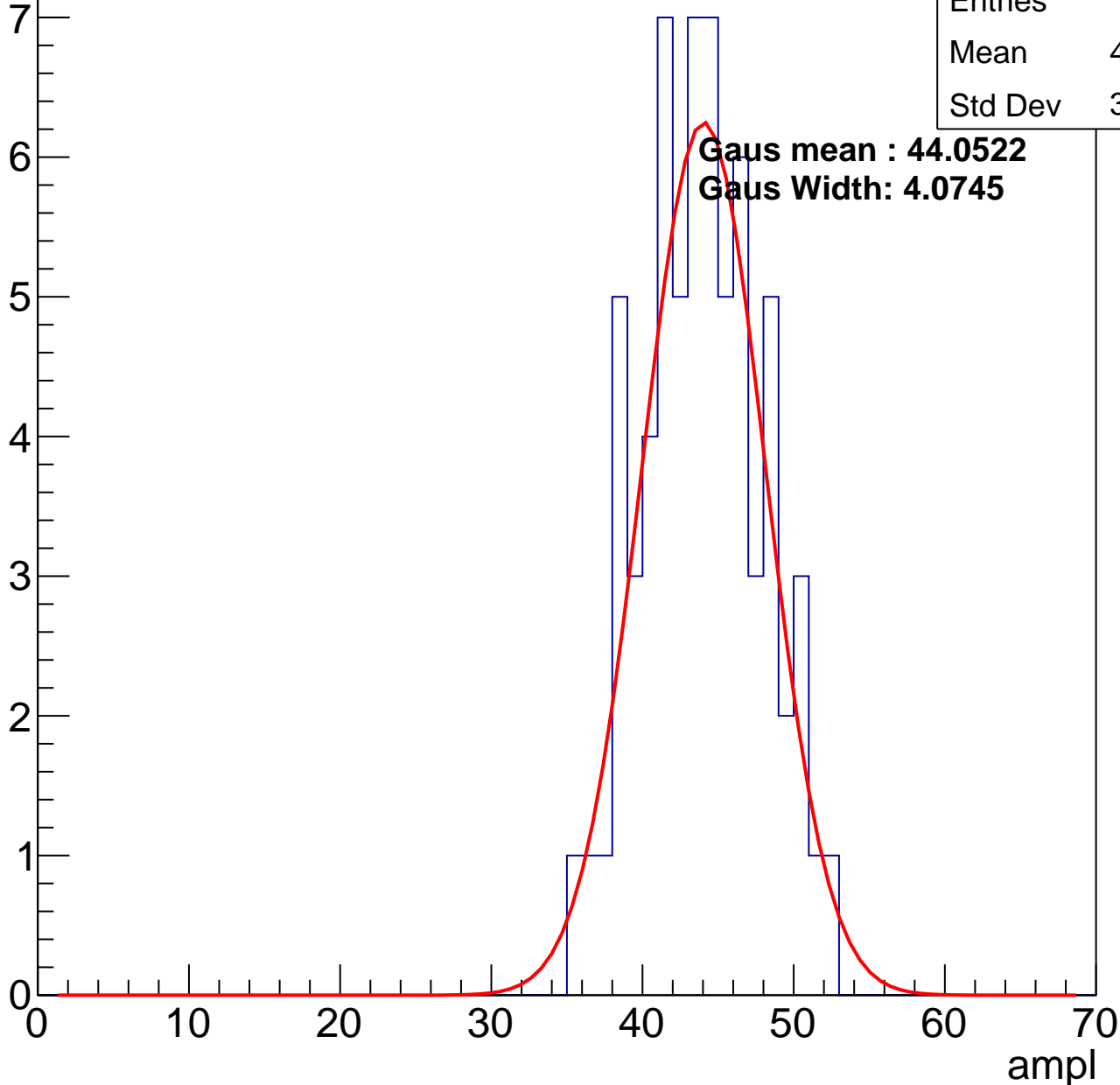


B1L101S, U25-ch0, adc2

calib_packv5_042523_0143.root, FC#0, port D2

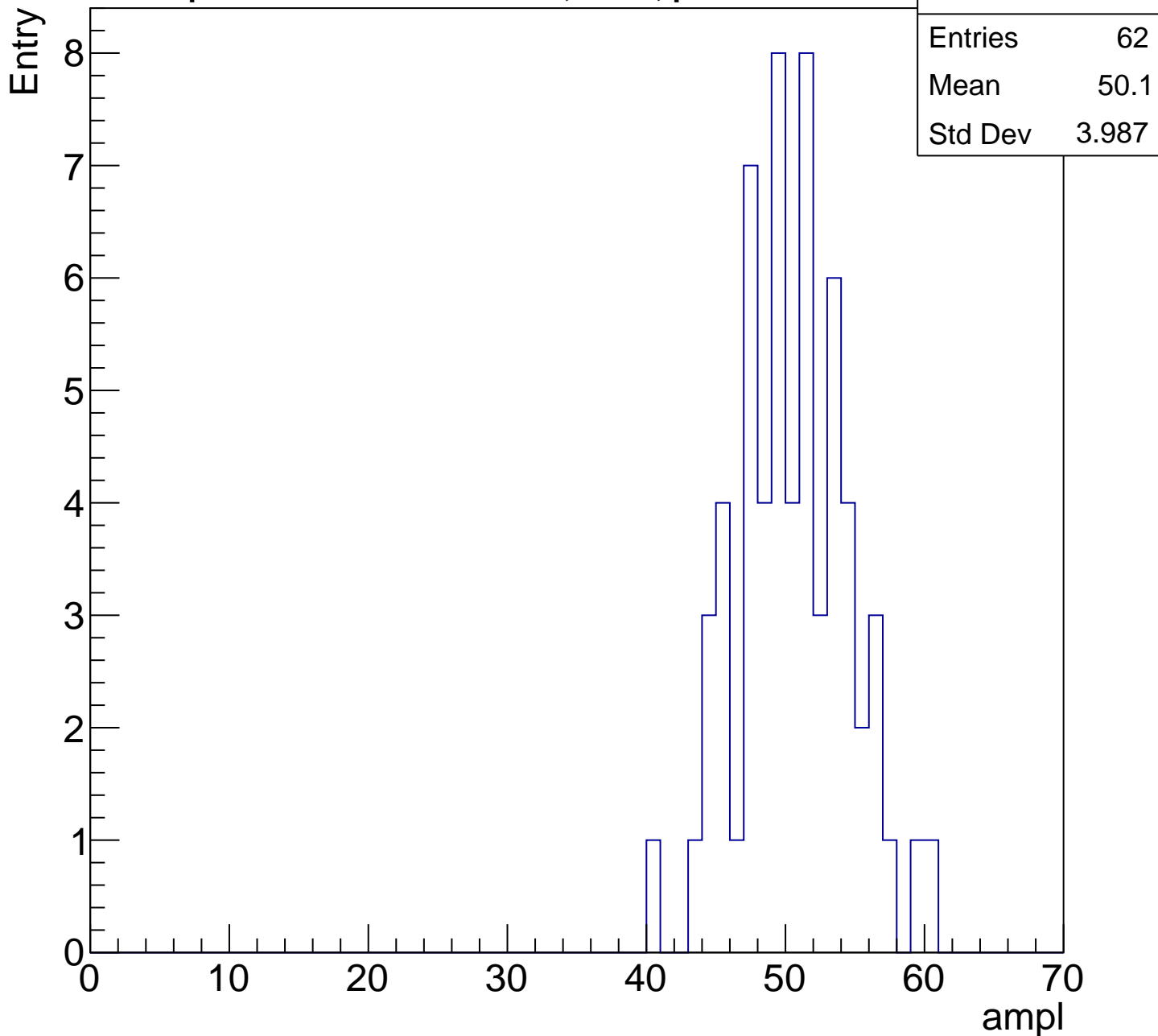
Entry

Entries	67
Mean	43.49
Std Dev	3.849



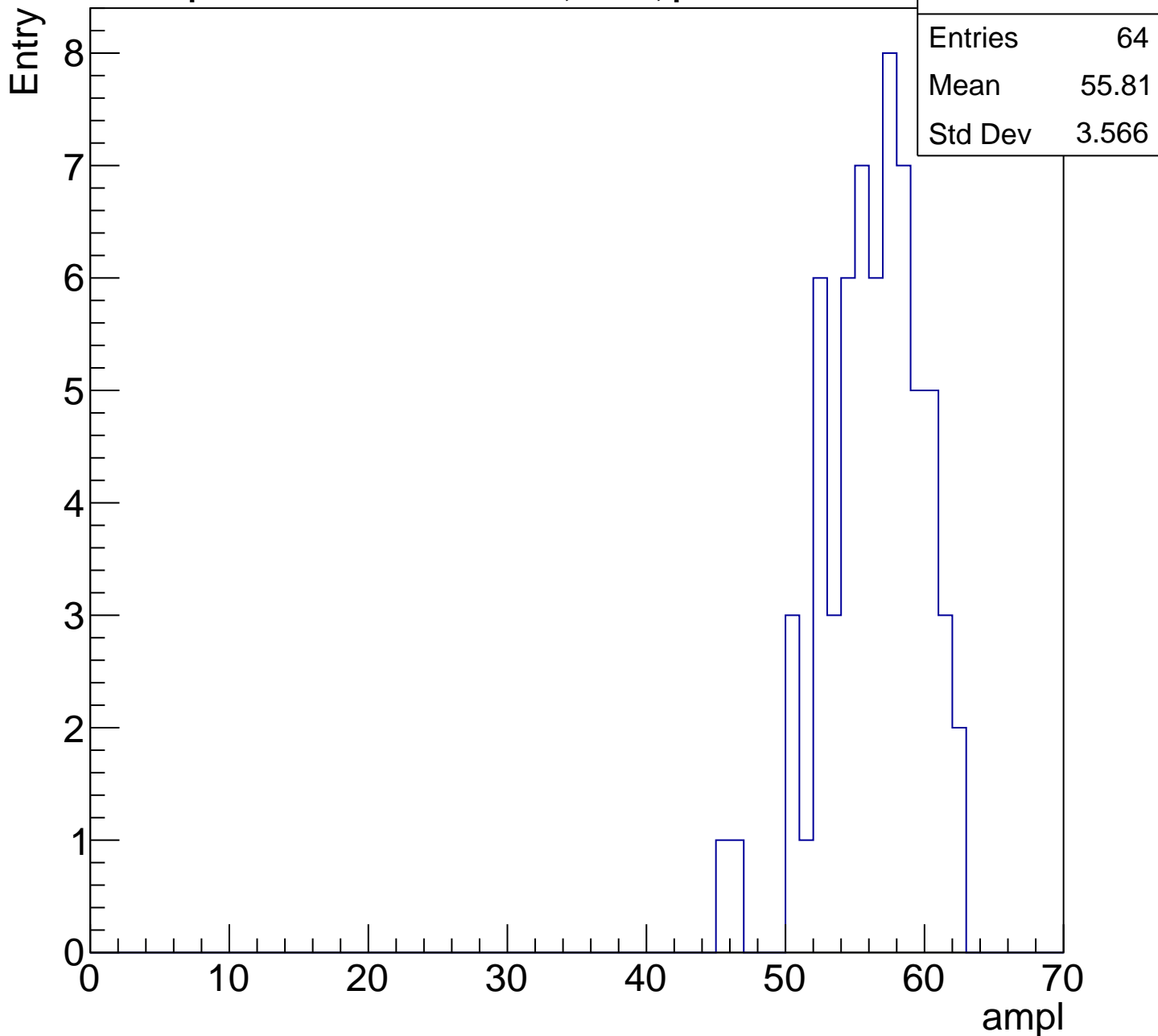
B1L101S, U25-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

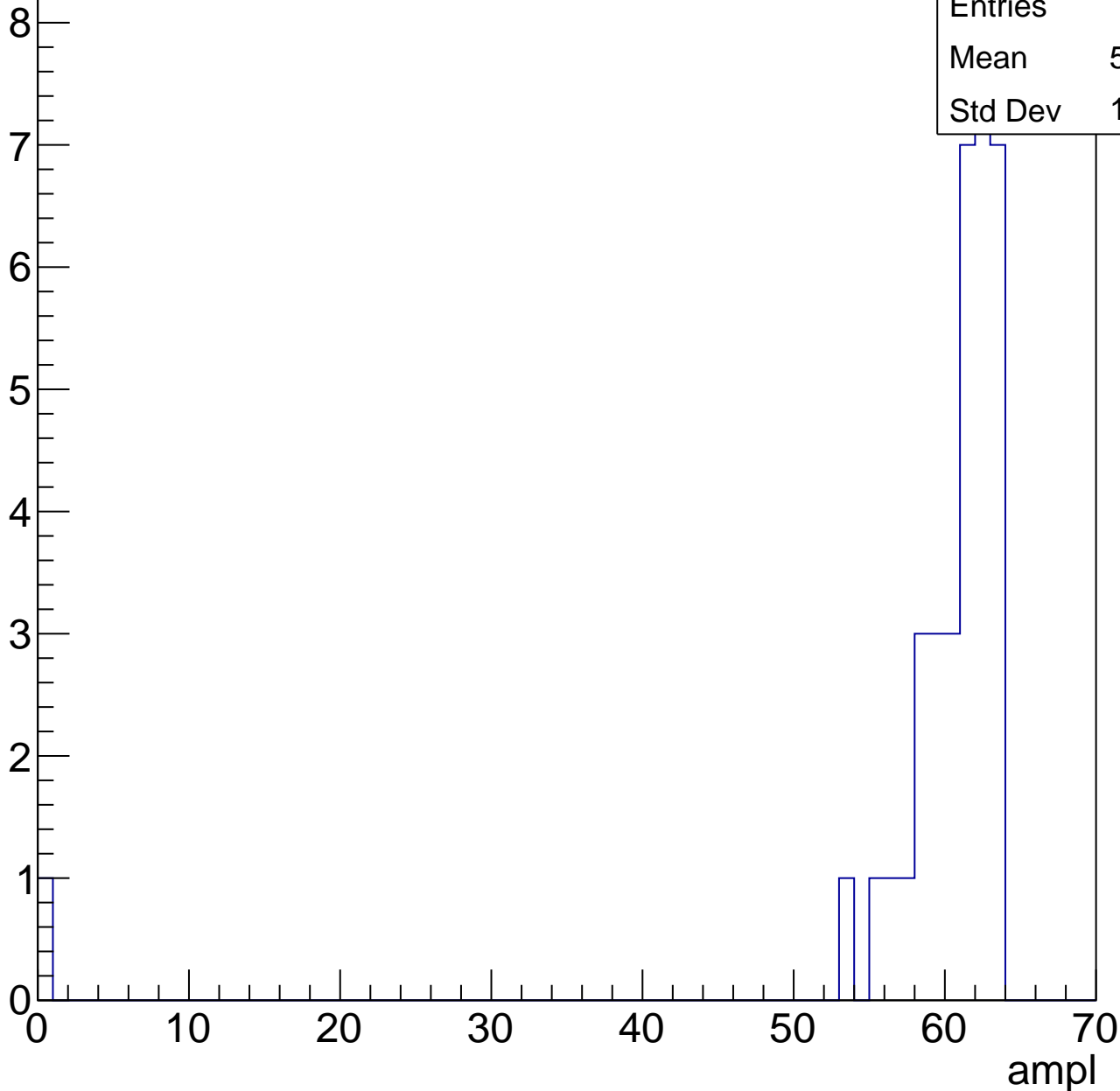


B1L101S, U25-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

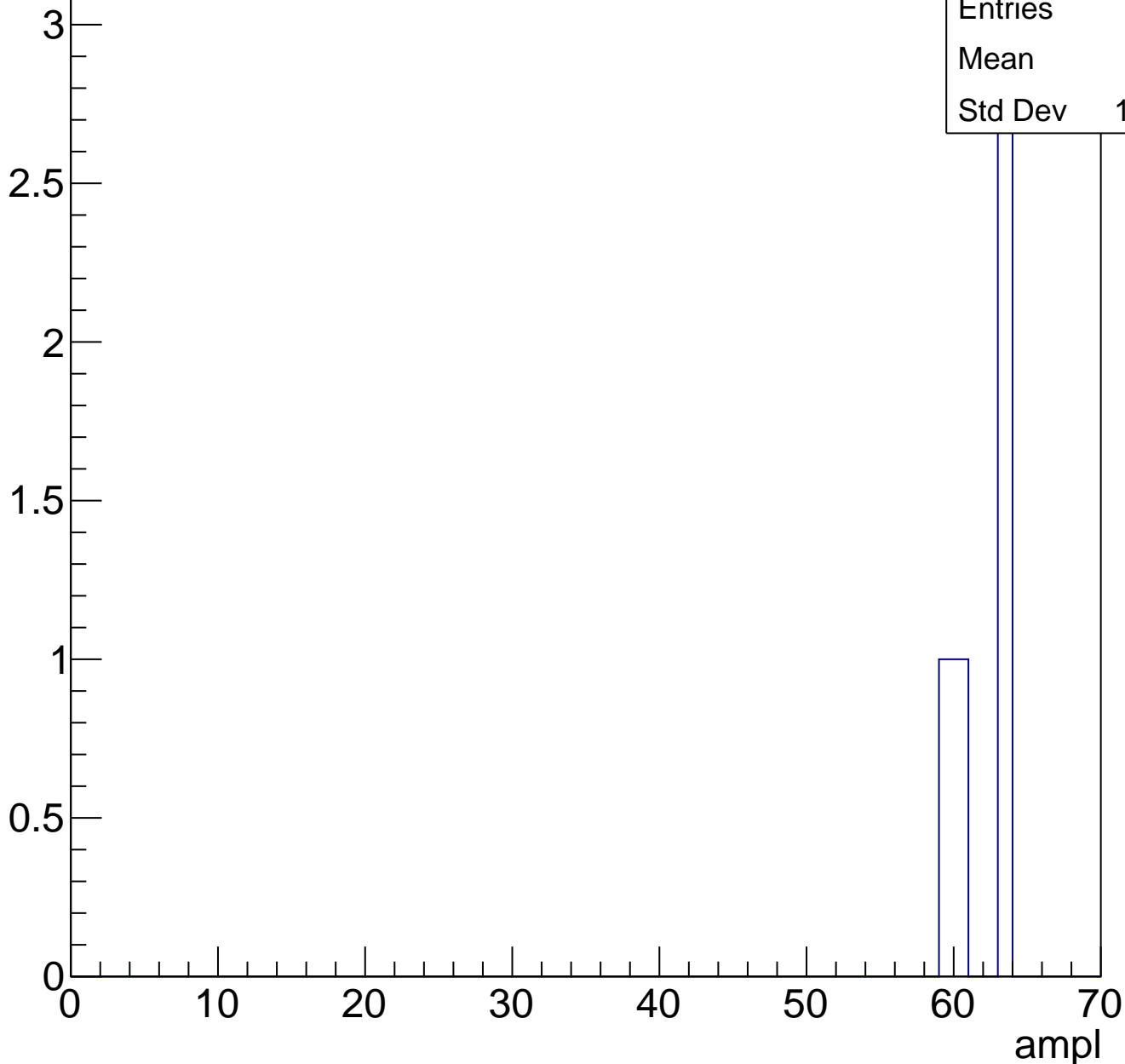
Entries	36
Mean	58.78
Std Dev	10.22



B1L101S, U25-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch1, adc0

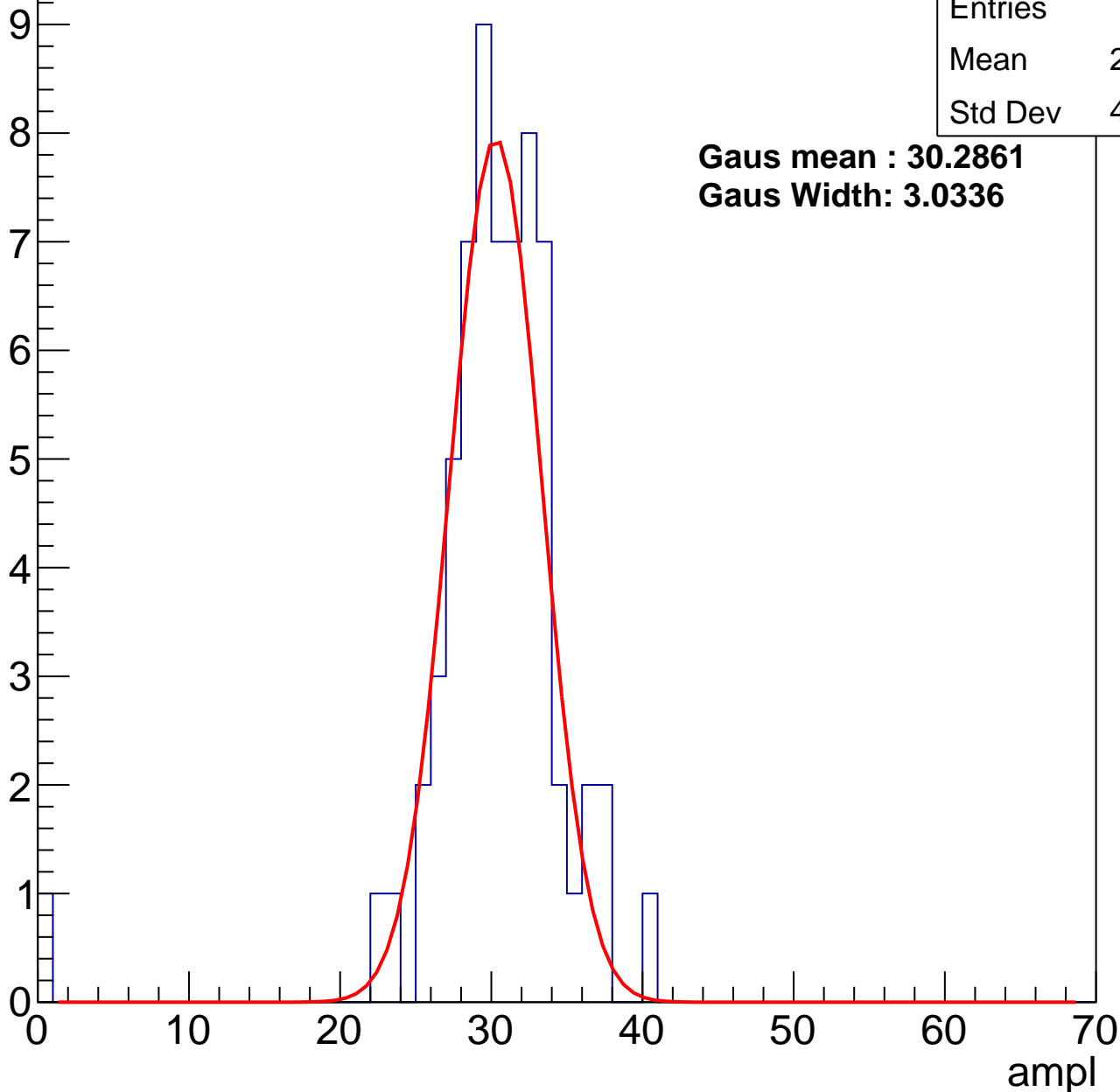
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.82
Std Dev	4.954

Gaus mean : 30.2861

Gaus Width: 3.0336



B1L101S, U25-ch1, adc1

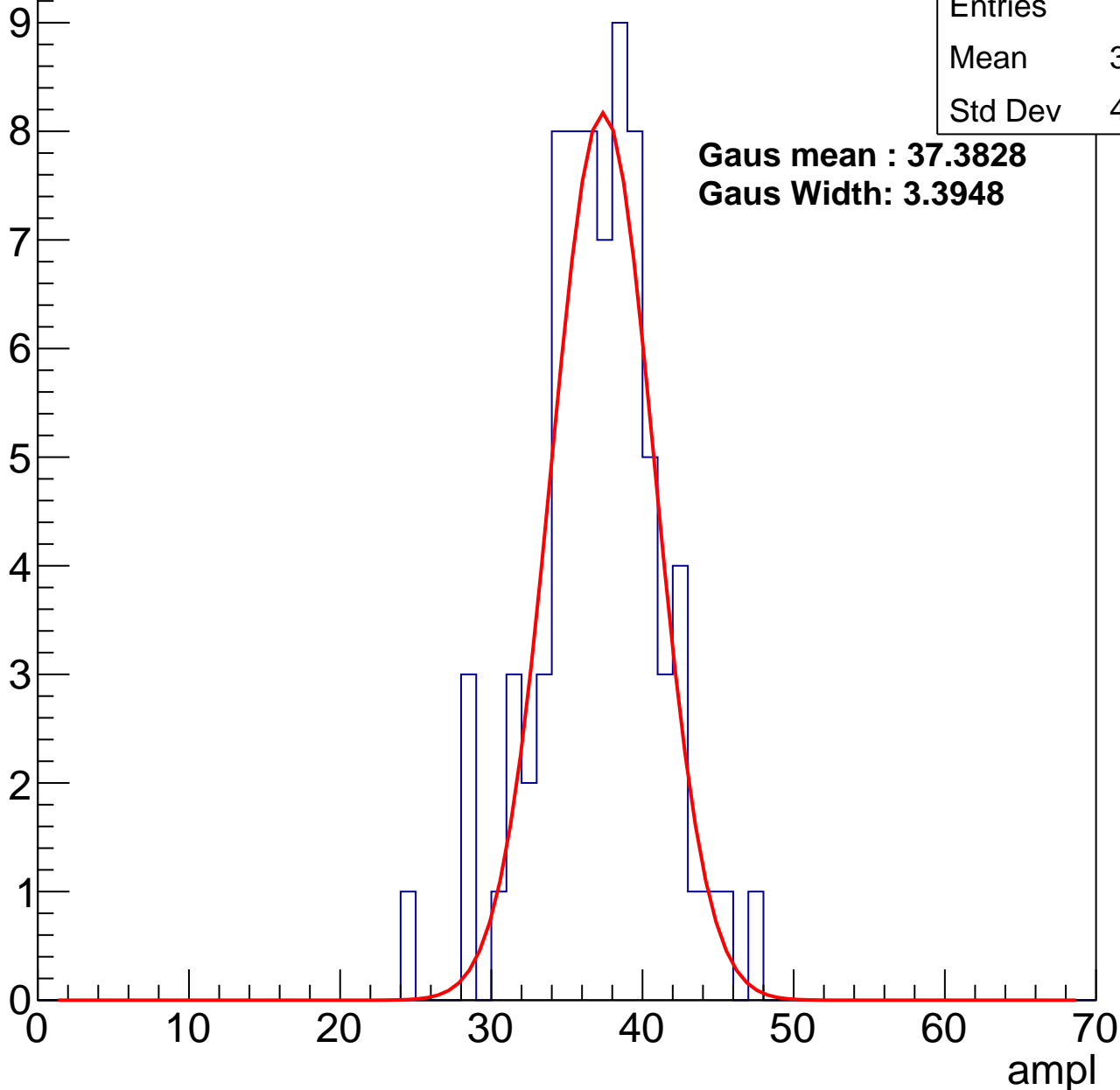
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.58
Std Dev	4.027

Gaus mean : 37.3828

Gaus Width: 3.3948



B1L101S, U25-ch1, adc2

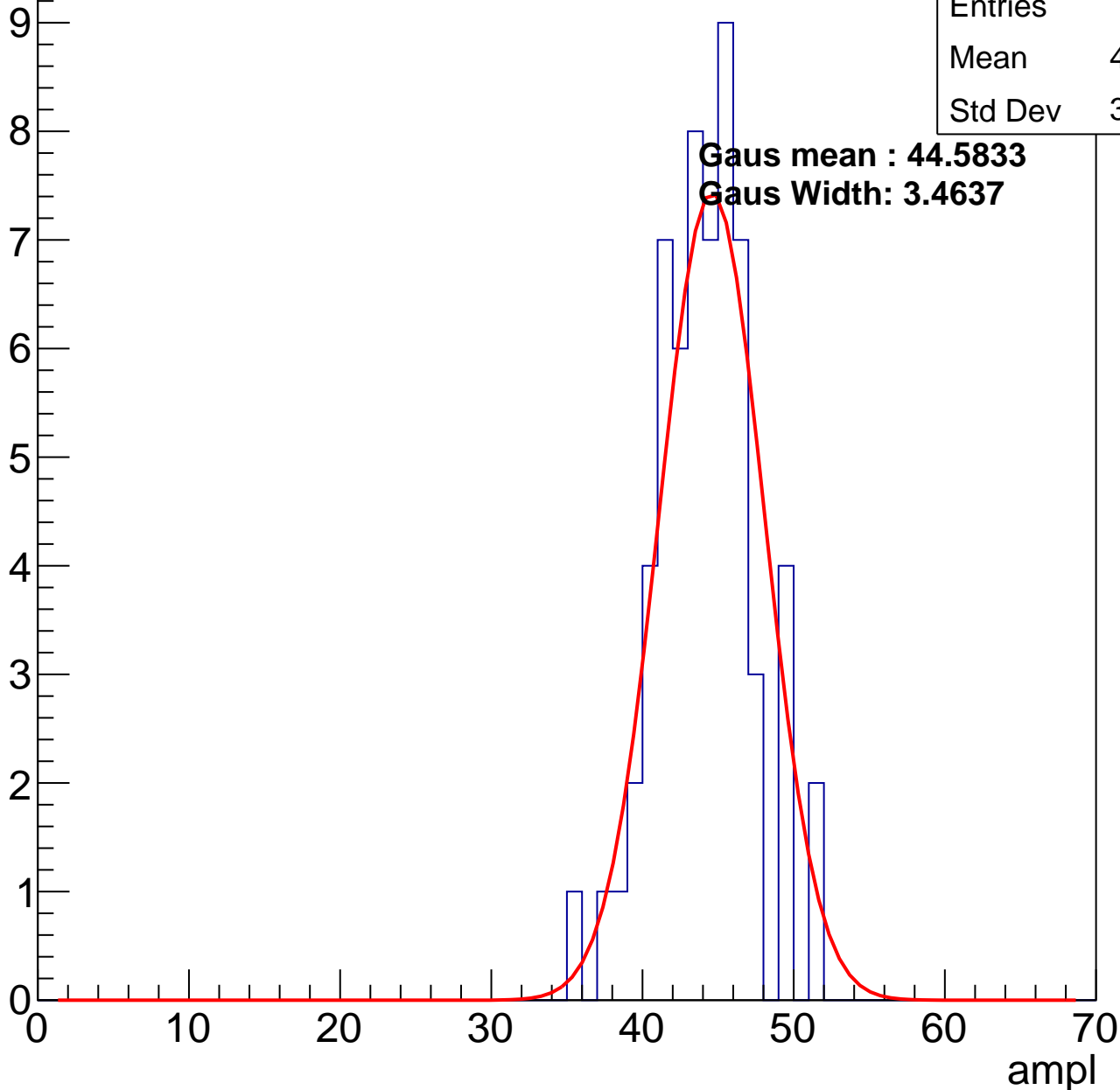
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.63
Std Dev	3.184

Gaus mean : 44.5833

Gaus Width: 3.4637

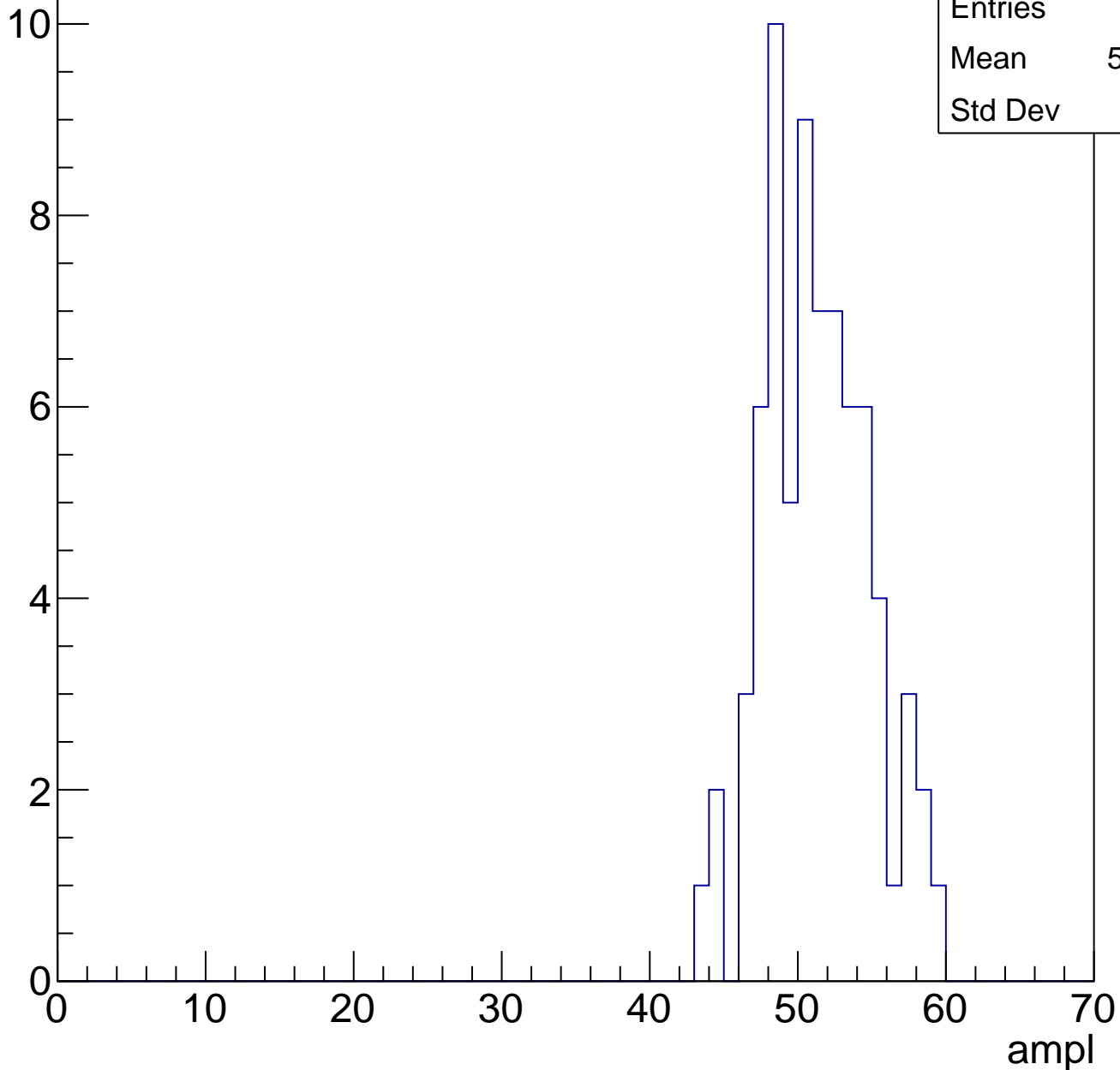


B1L101S, U25-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

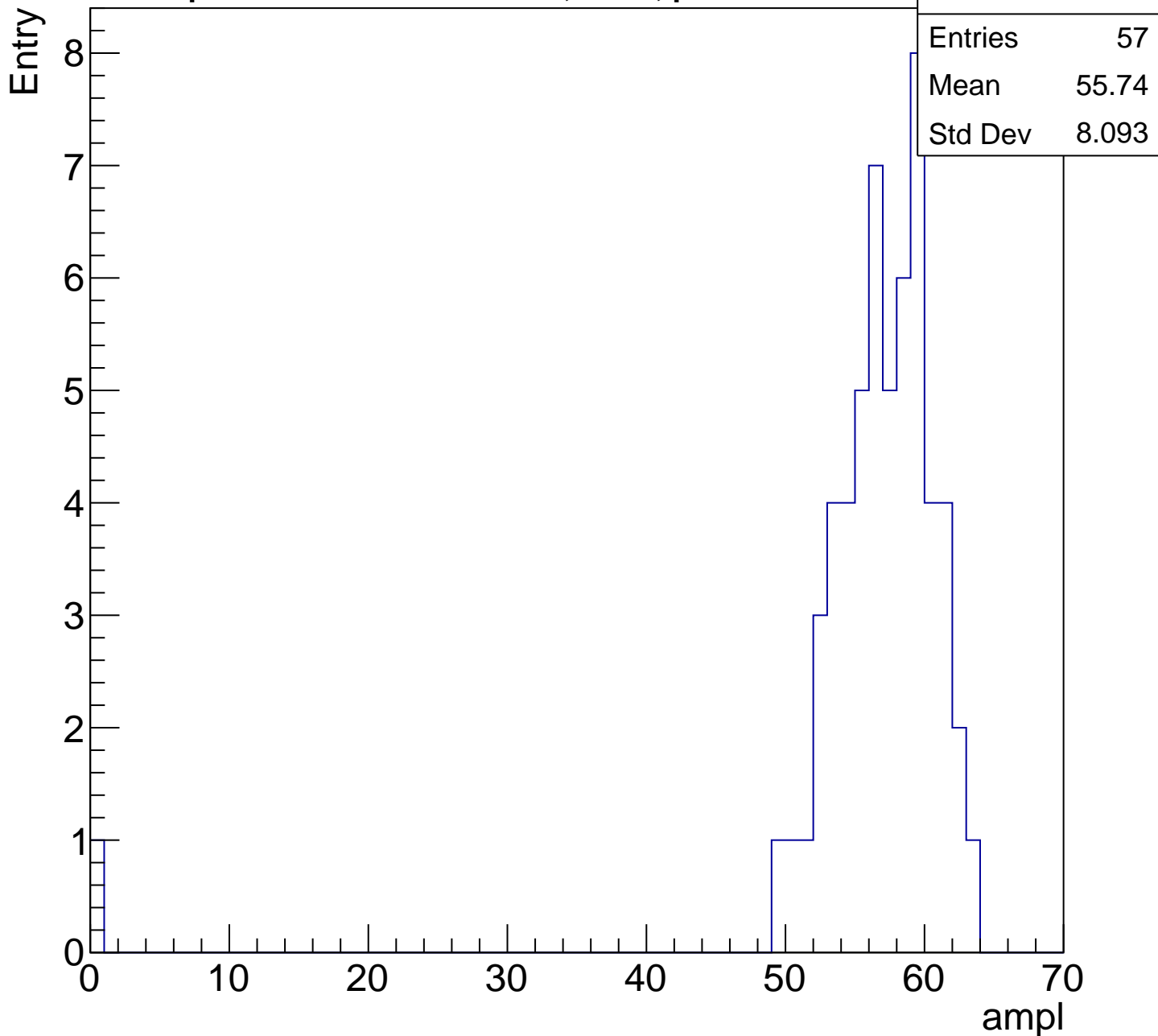
Entries	73
Mean	50.84
Std Dev	3.5

Entry



B1L101S, U25-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

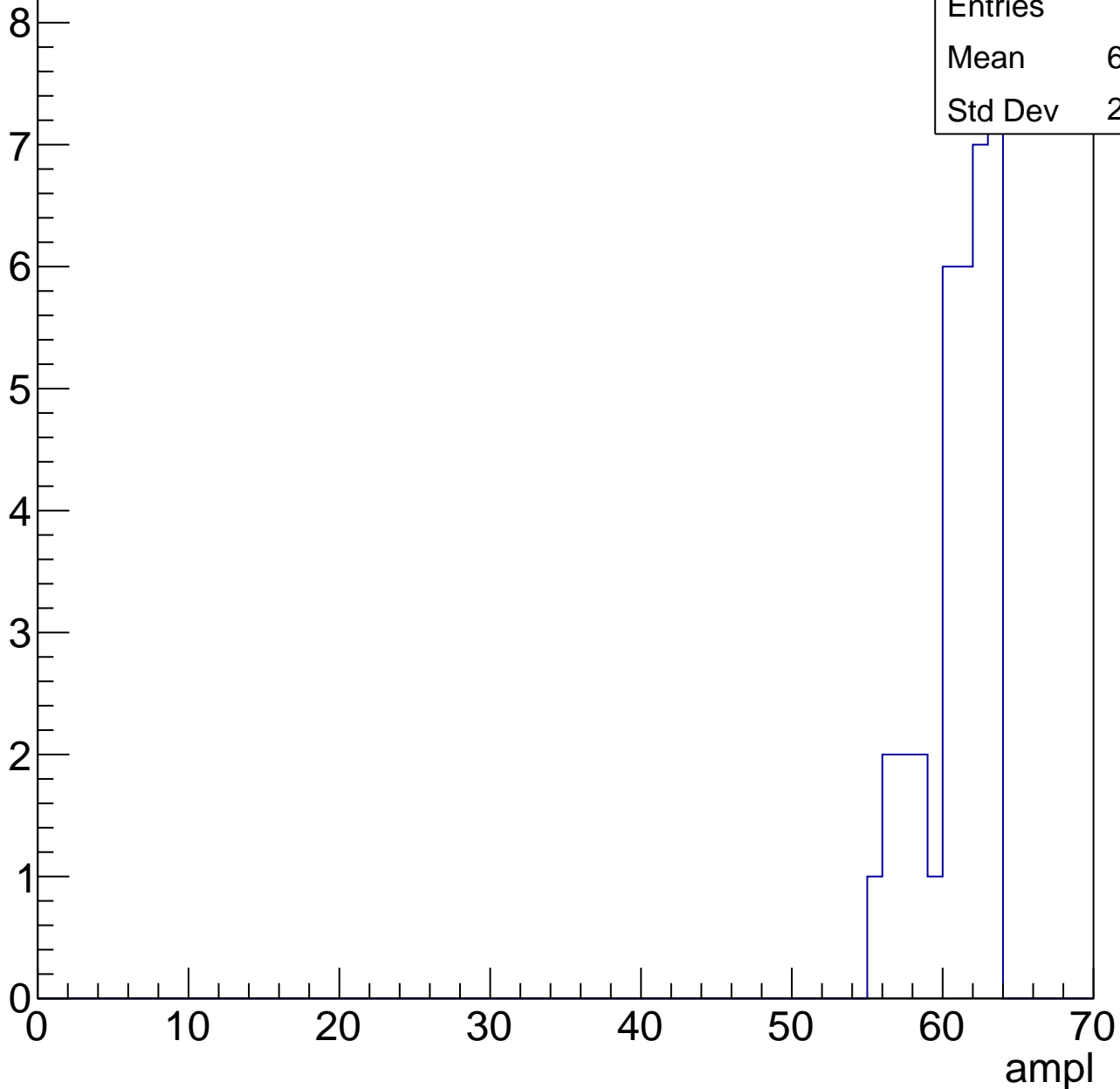


B1L101S, U25-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	60.57
Std Dev	2.259



B1L101S, U25-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch2, adc0

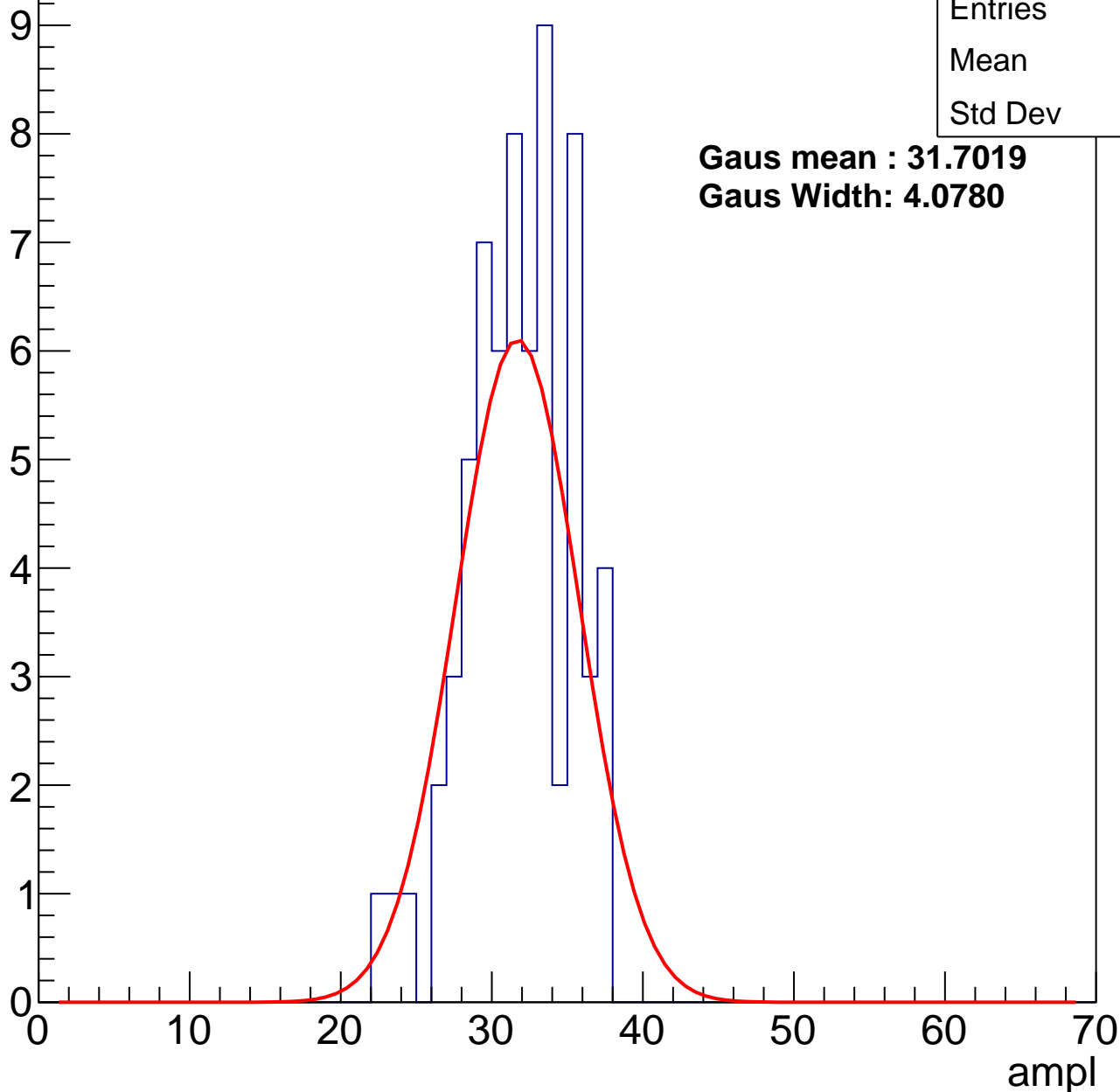
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	31.3
Std Dev	3.42

Gaus mean : 31.7019

Gaus Width: 4.0780



B1L101S, U25-ch2, adc1

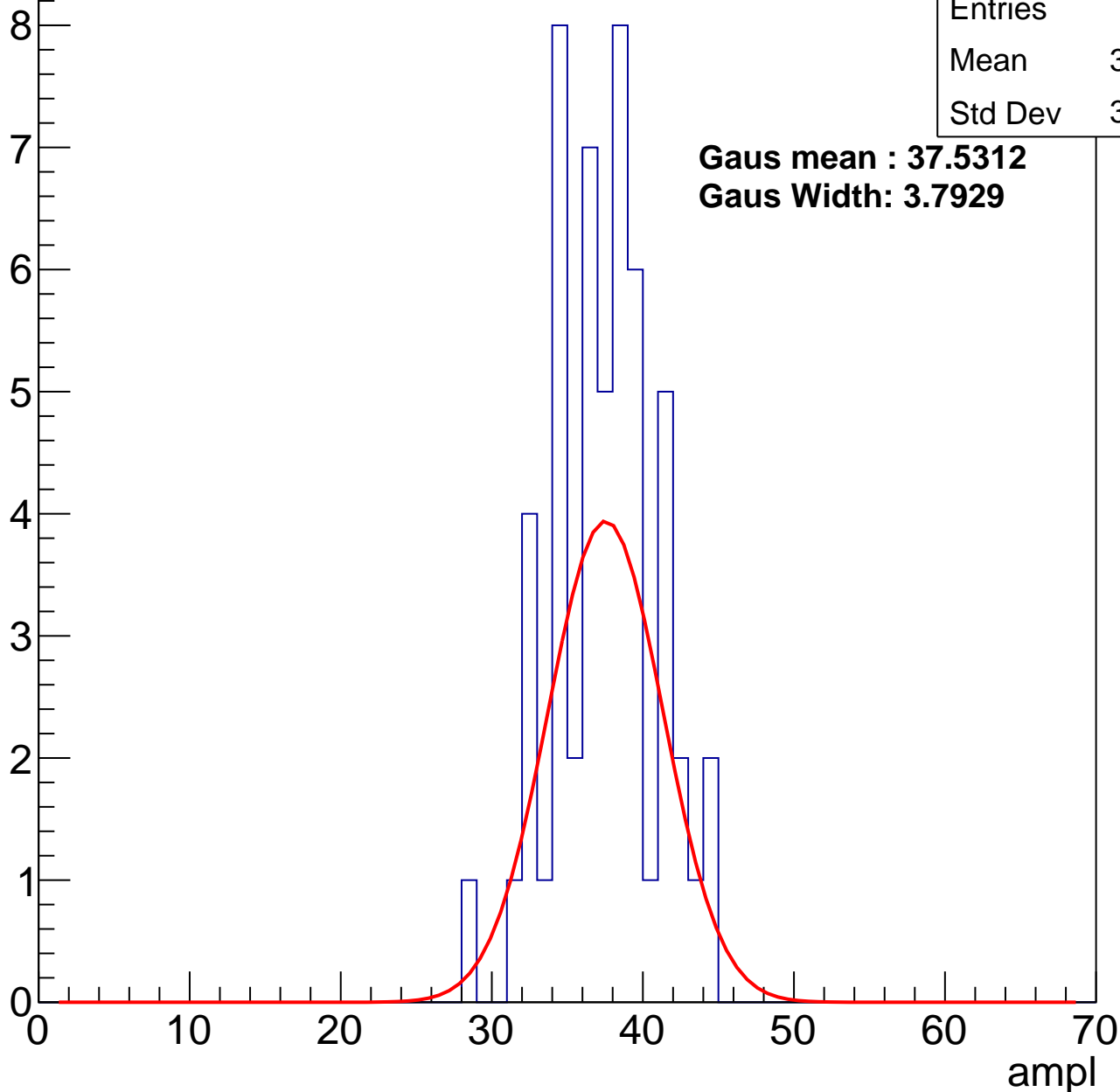
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.98
Std Dev	3.402

Gaus mean : 37.5312

Gaus Width: 3.7929



B1L101S, U25-ch2, adc2

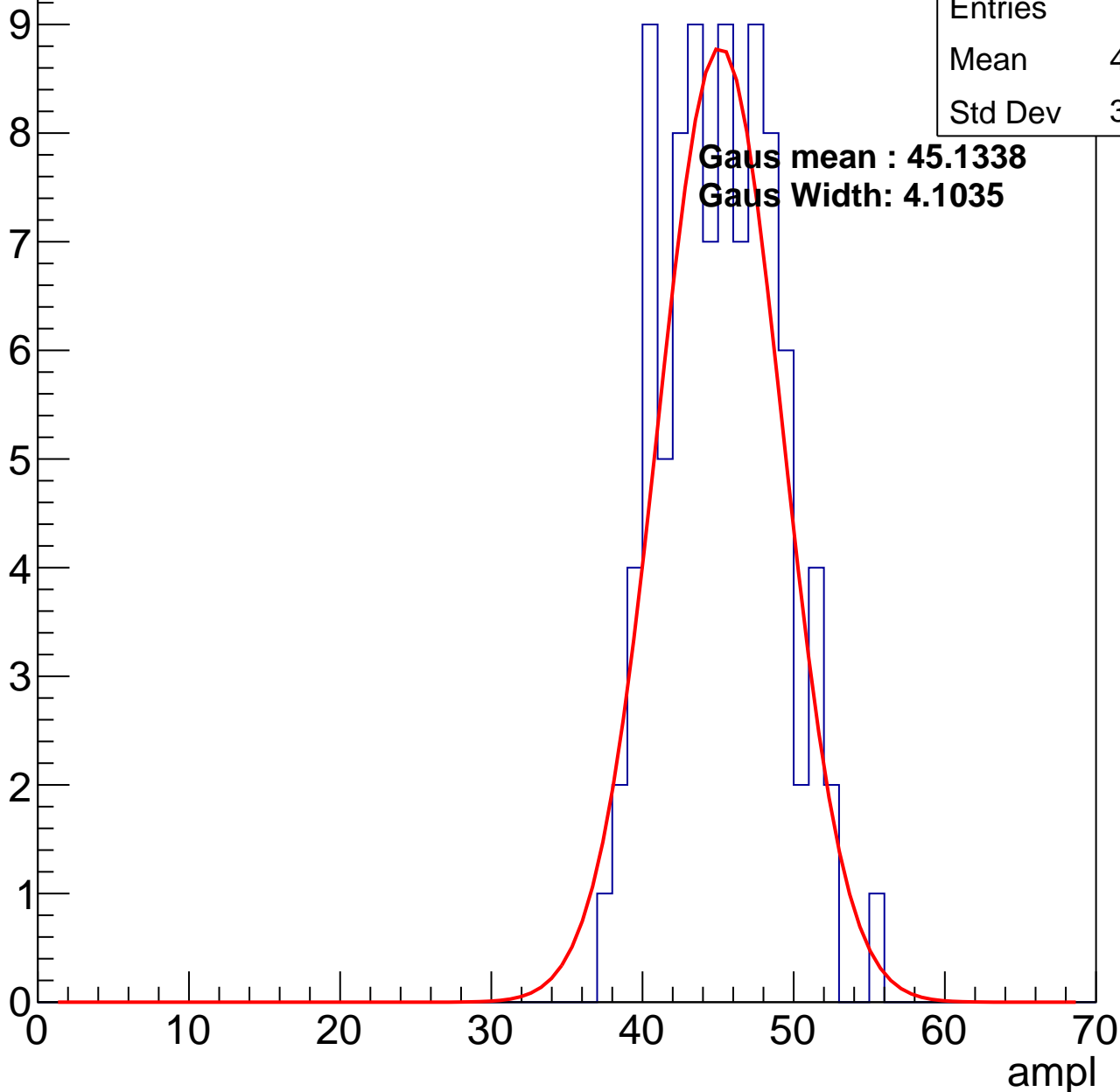
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	93
Mean	44.69
Std Dev	3.767

Gaus mean : 45.1338

Gaus Width: 4.1035

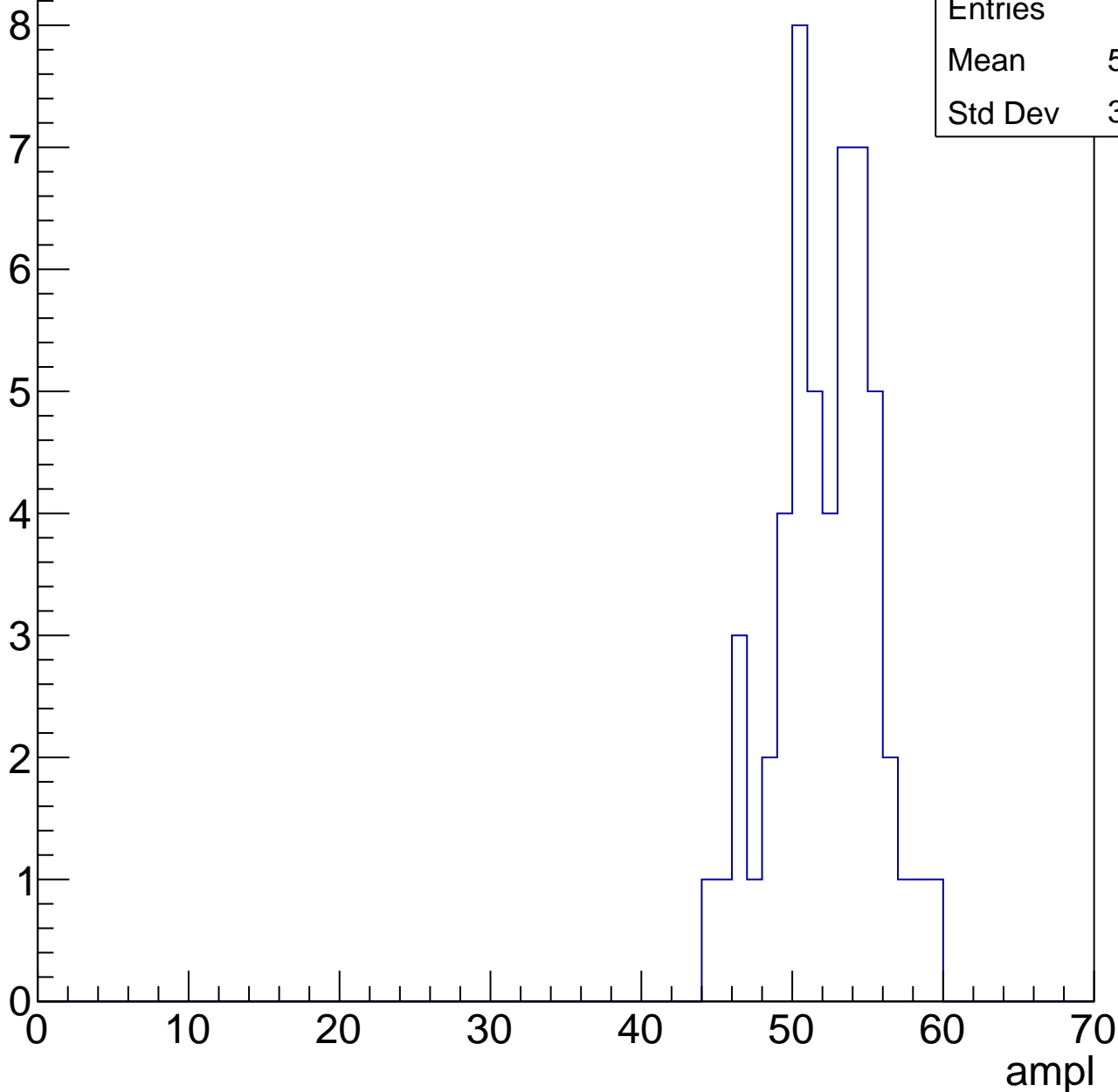


B1L101S, U25-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	51.68
Std Dev	3.272

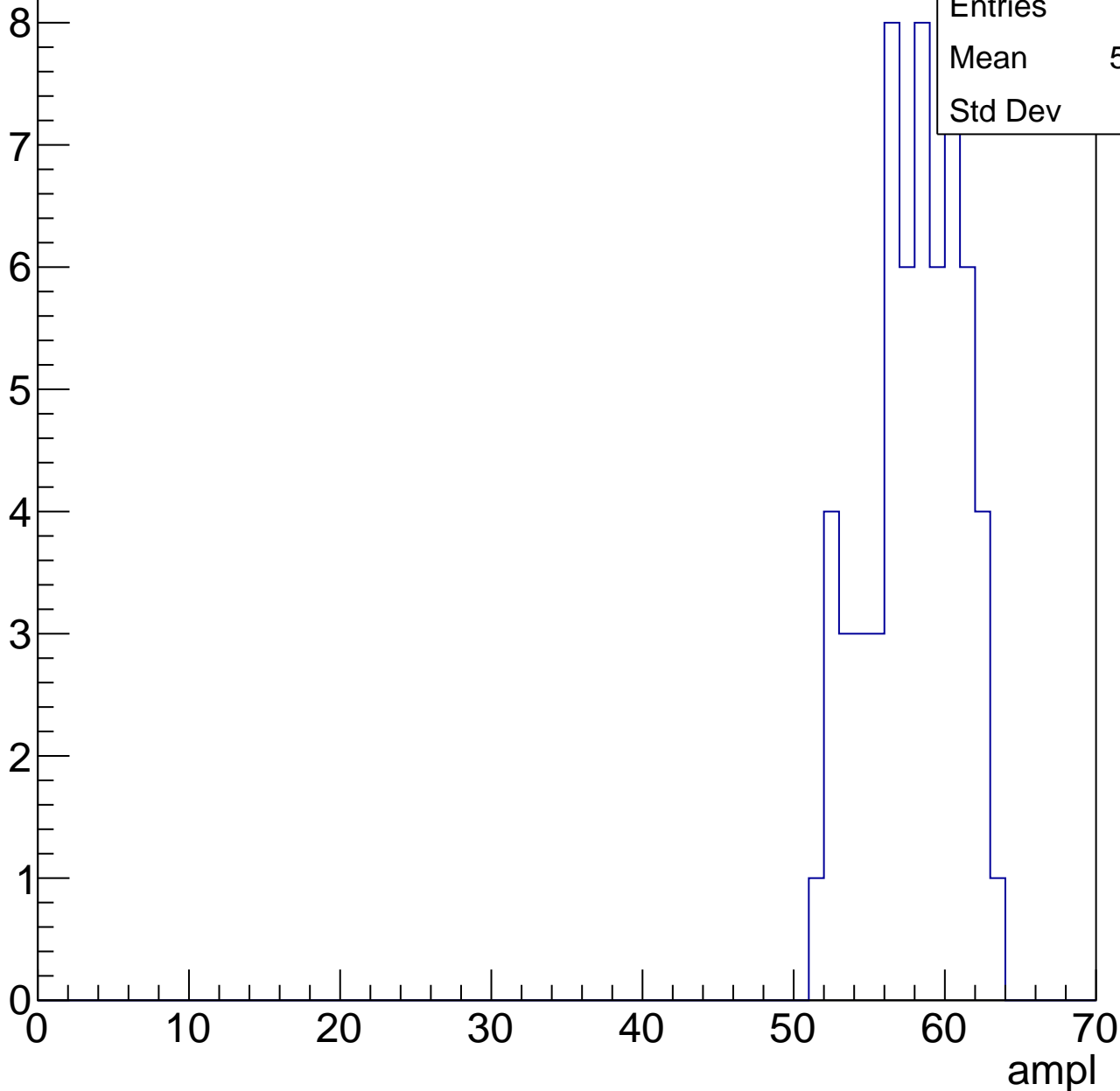


B1L101S, U25-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	57.54
Std Dev	3

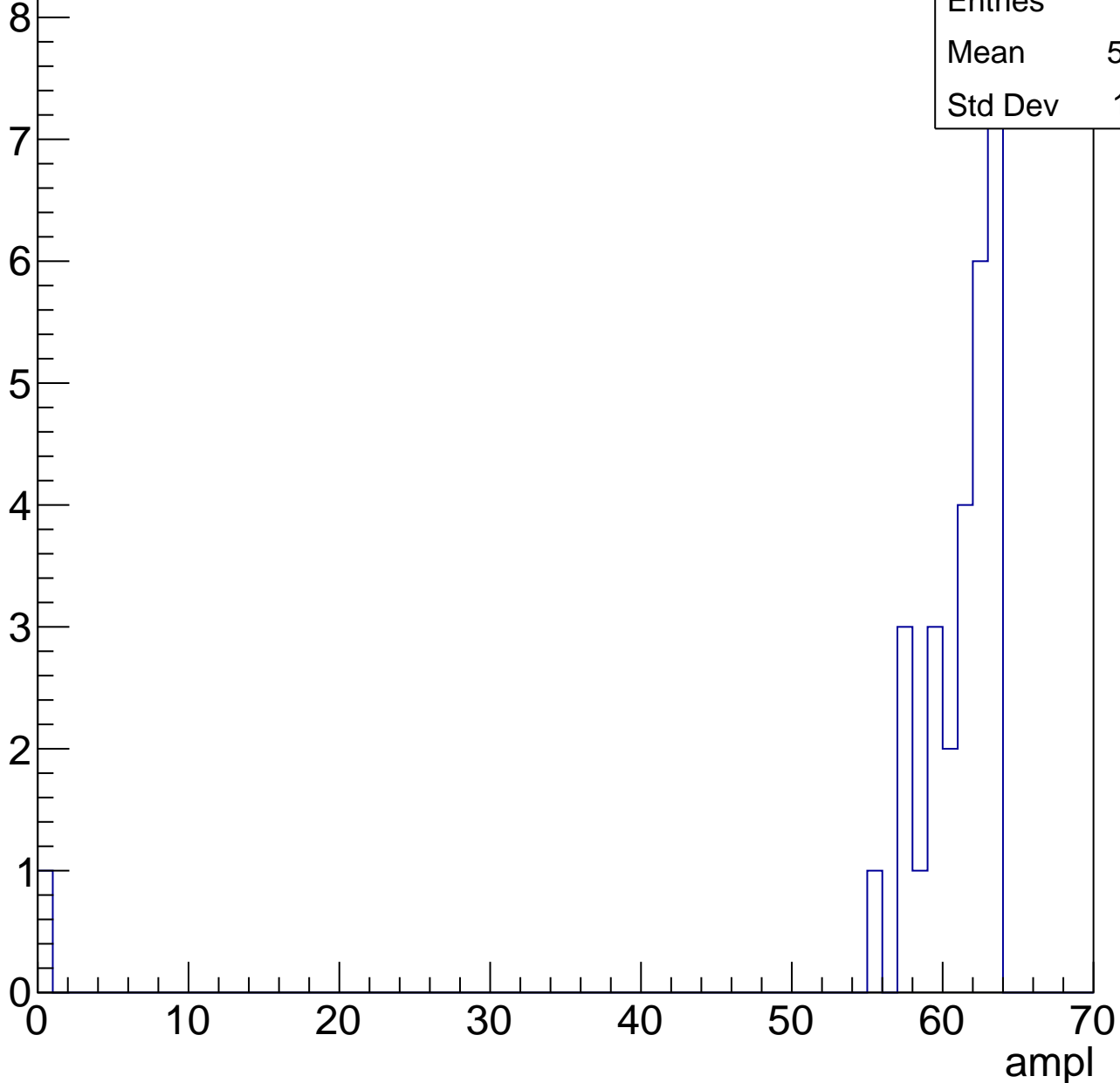


B1L101S, U25-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	58.66
Std Dev	11.31



B1L101S, U25-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U25-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch3, adc0

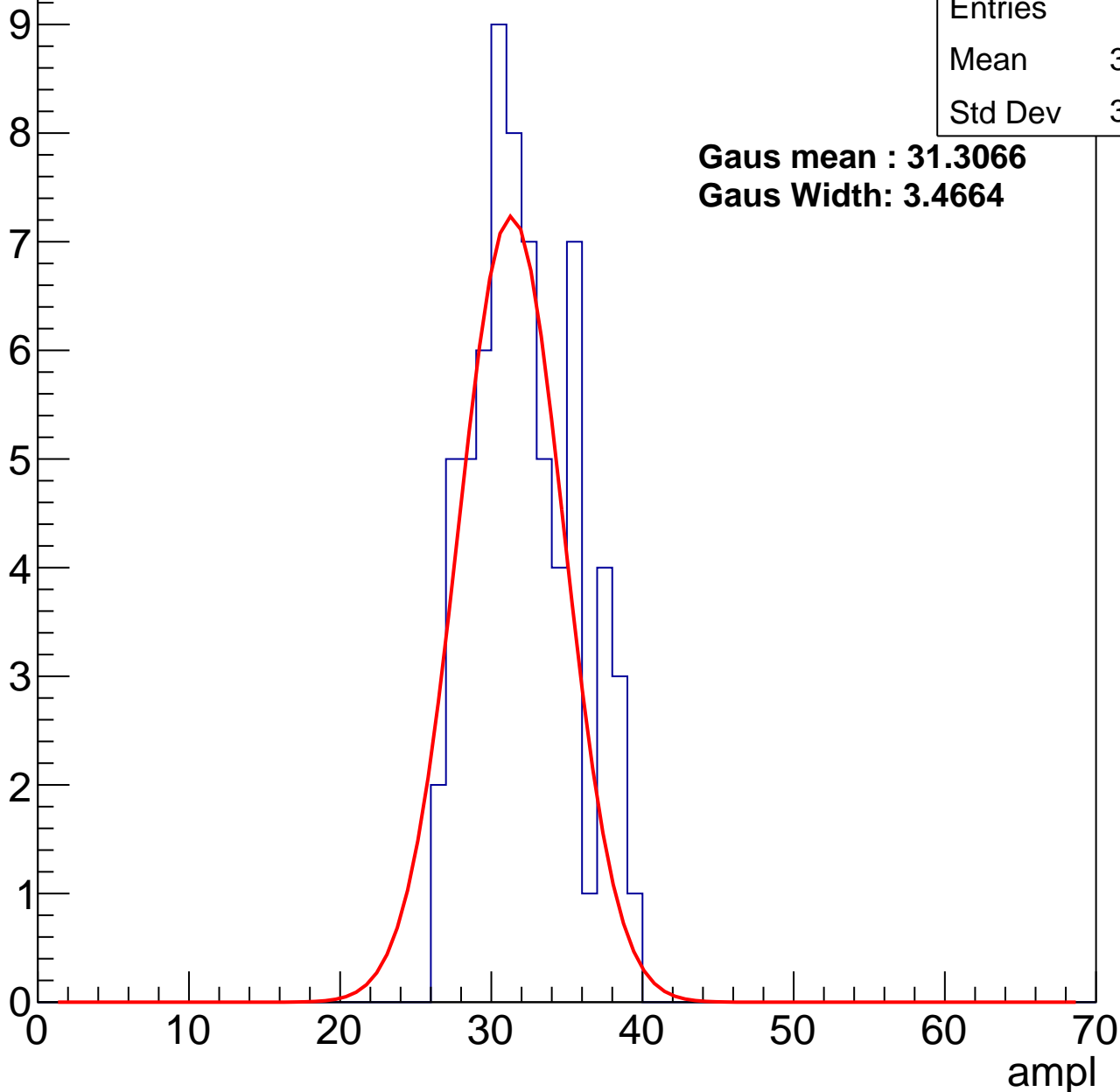
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	31.73
Std Dev	3.294

Gaus mean : 31.3066

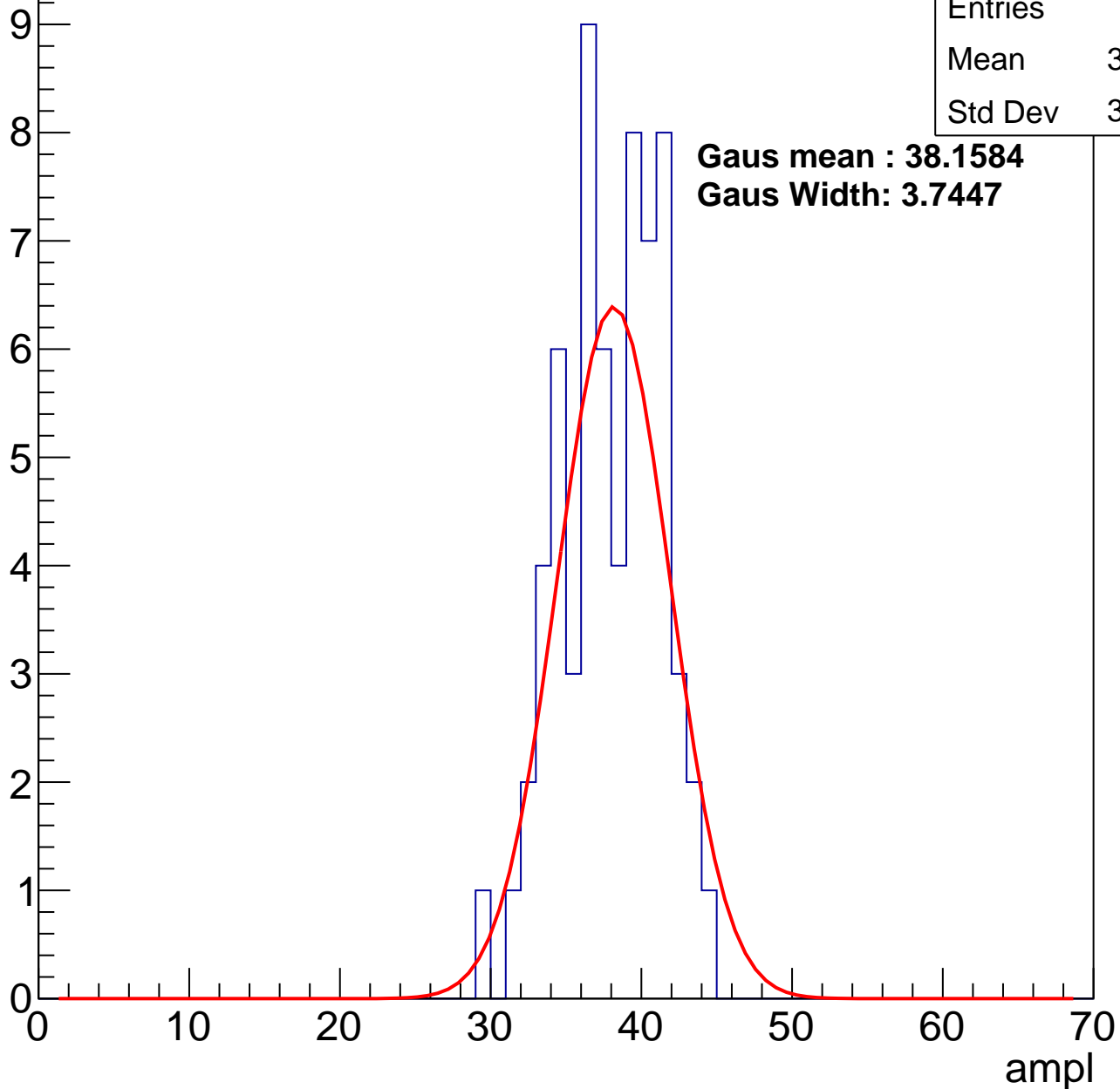
Gaus Width: 3.4664



B1L101S, U25-ch3, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch3, adc2

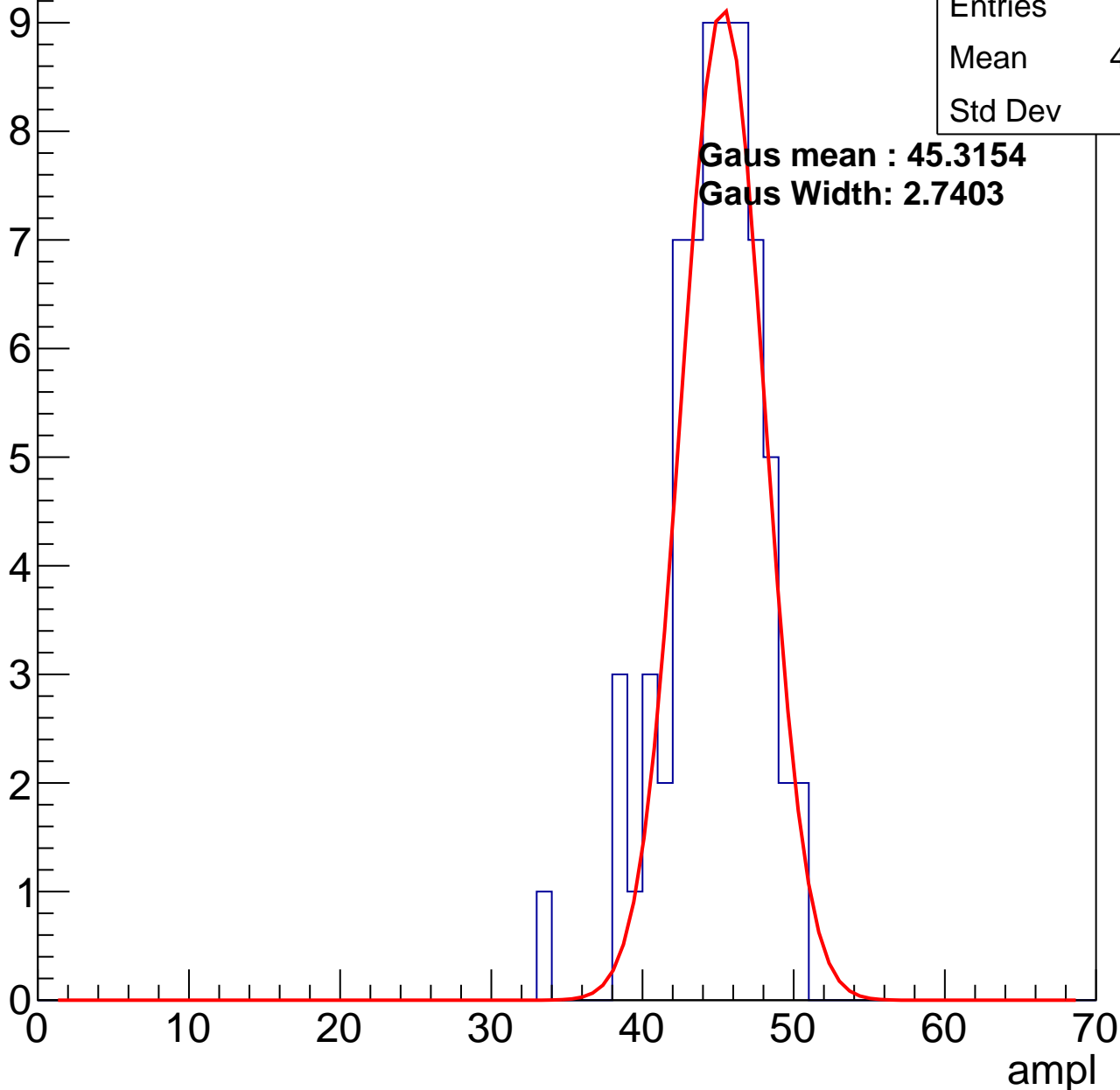
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	44.25
Std Dev	3.14

Gaus mean : 45.3154

Gaus Width: 2.7403

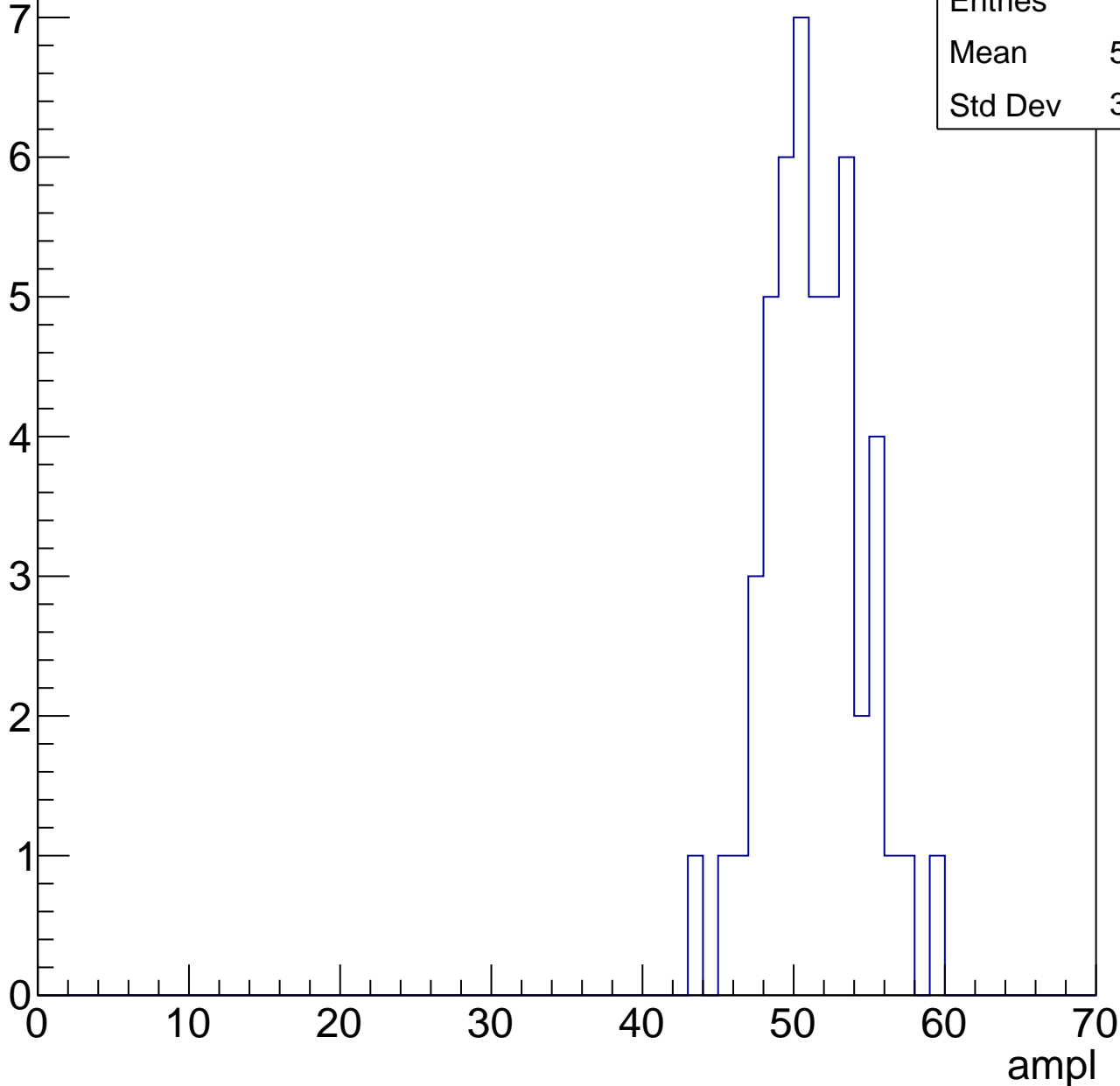


B1L101S, U25-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	50.86
Std Dev	3.143

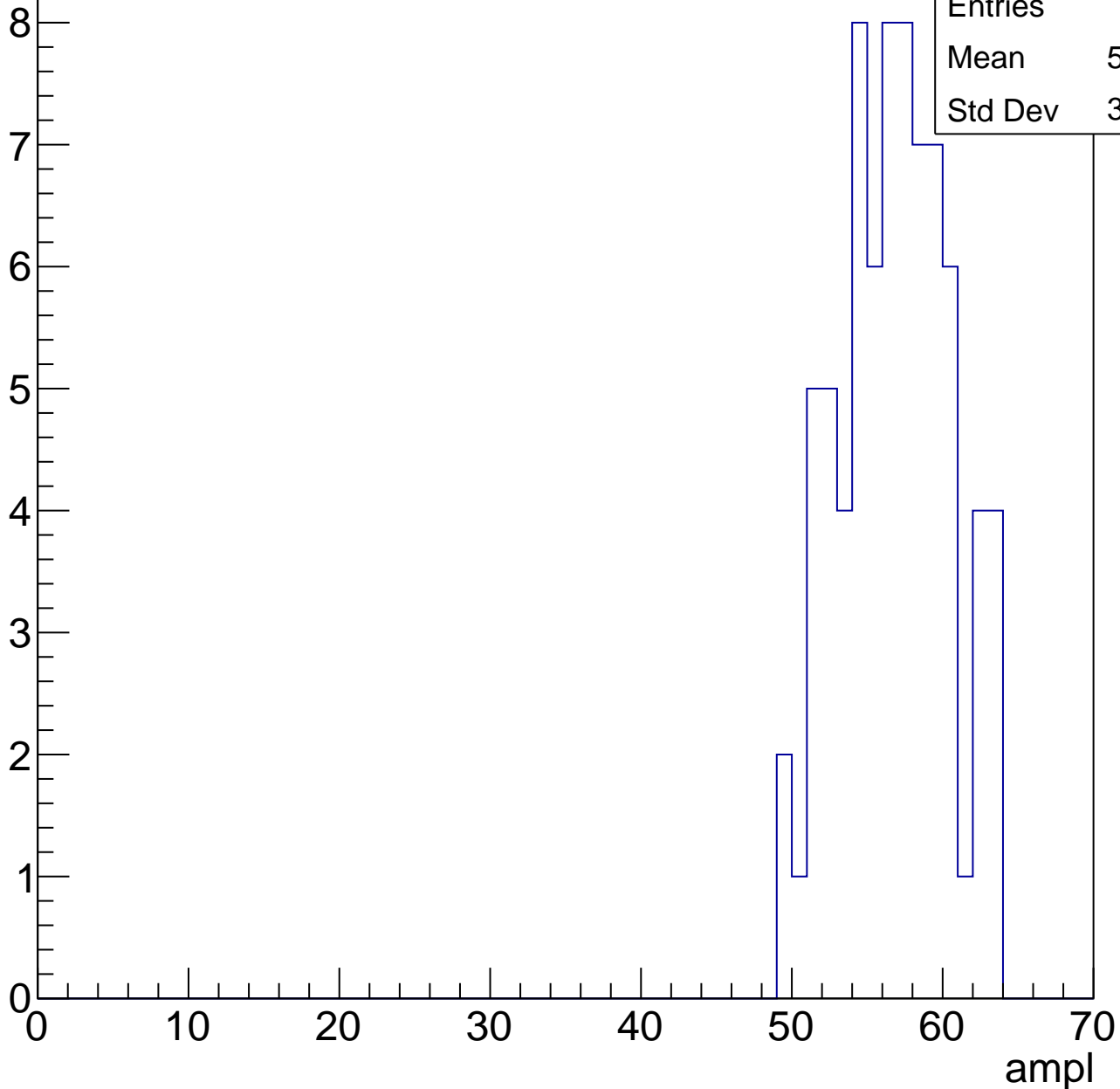


B1L101S, U25-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	56.33
Std Dev	3.563

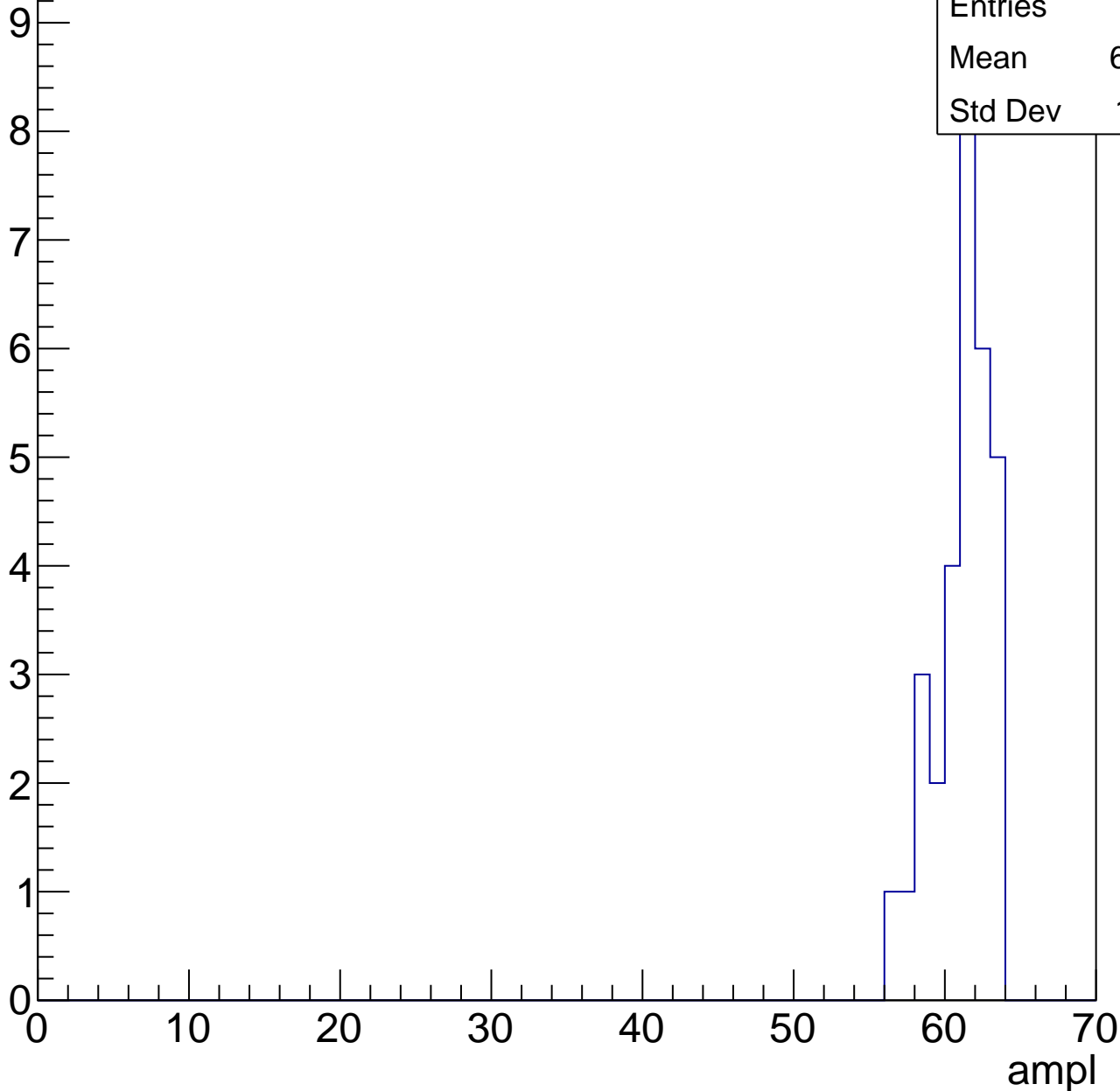


B1L101S, U25-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	60.68
Std Dev	1.821



B1L101S, U25-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L101S, U25-ch4, adc0

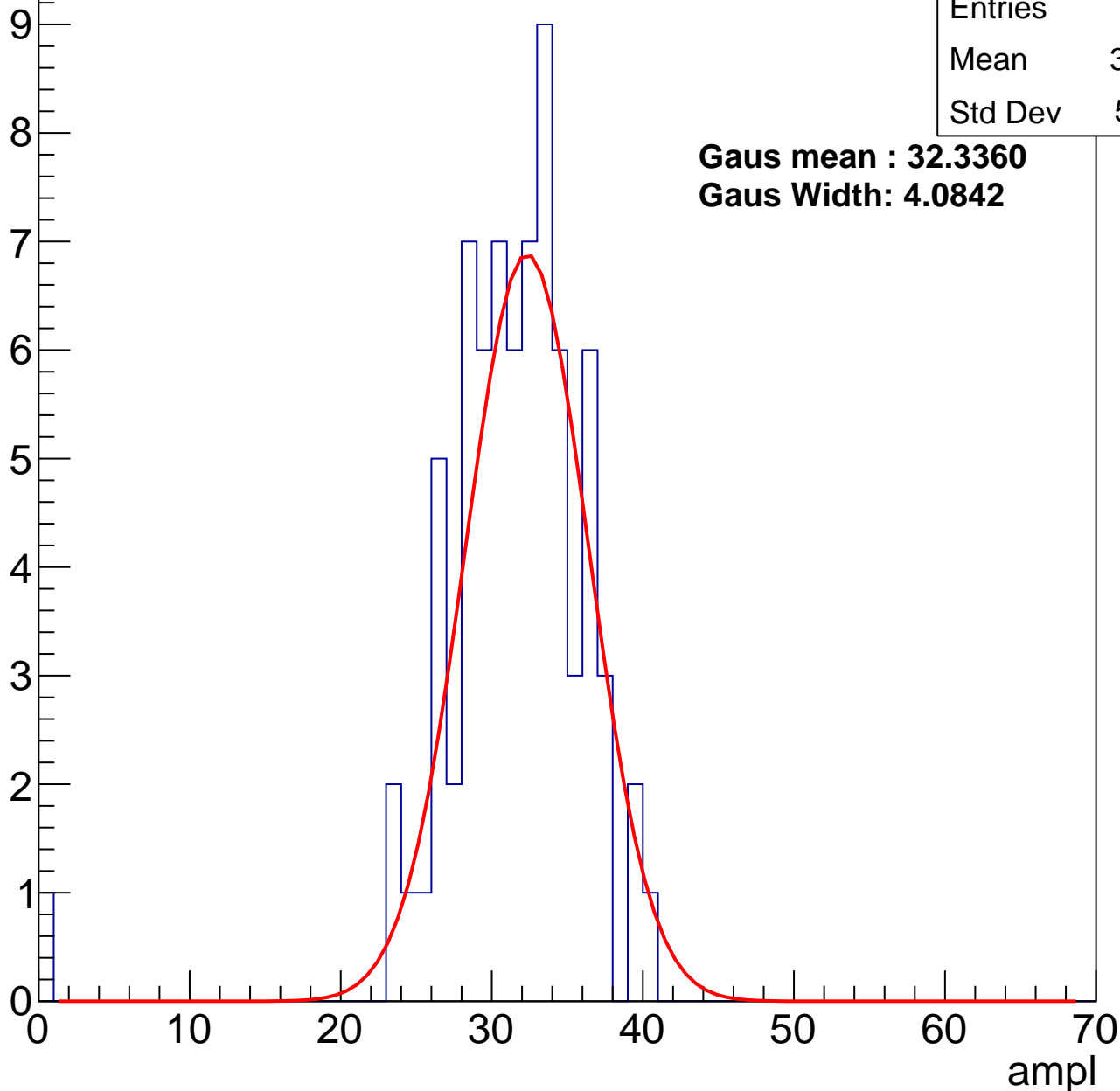
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.93
Std Dev	5.211

Gaus mean : 32.3360

Gaus Width: 4.0842



B1L101S, U25-ch4, adc1

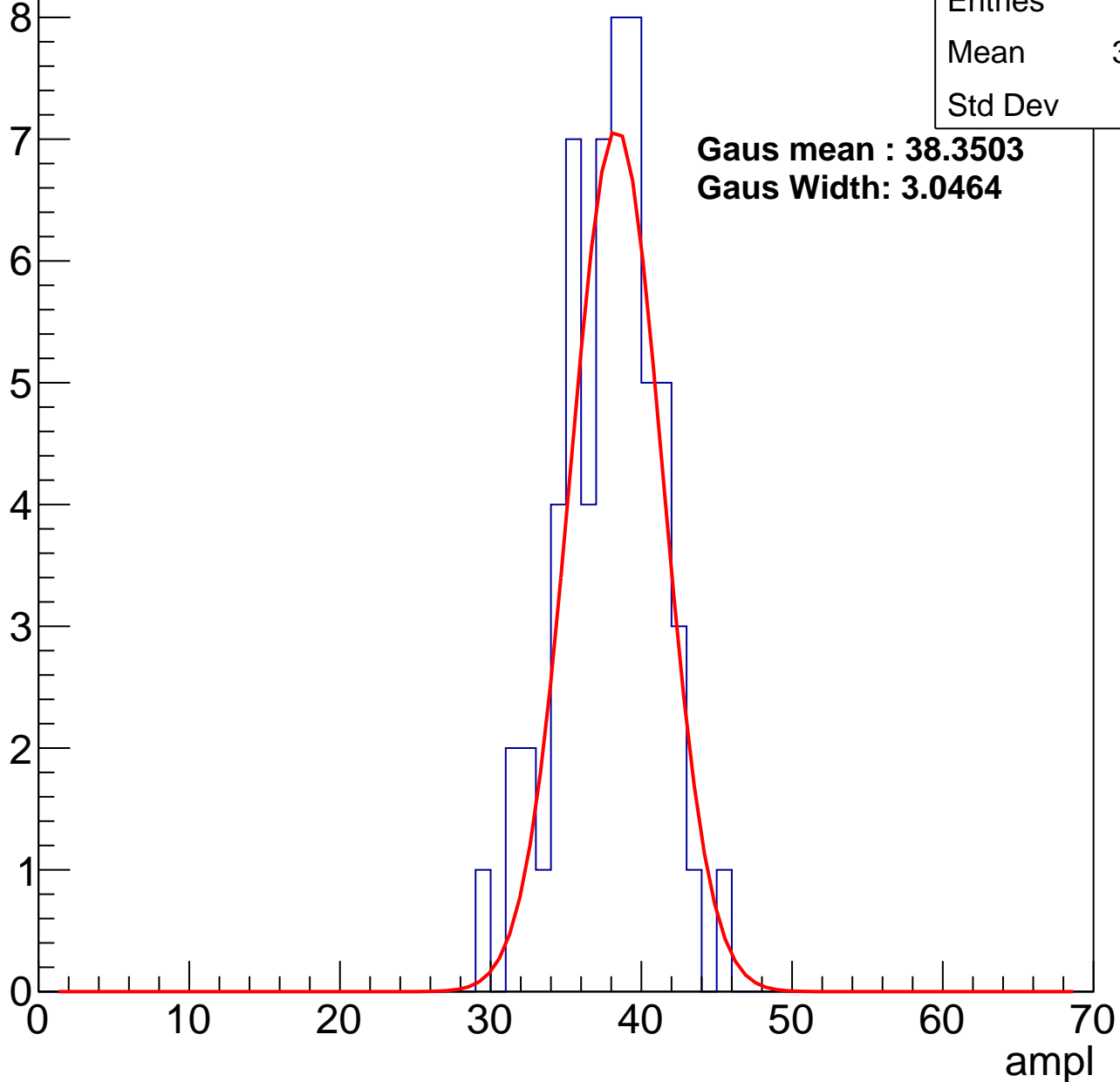
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	37.41
Std Dev	3.19

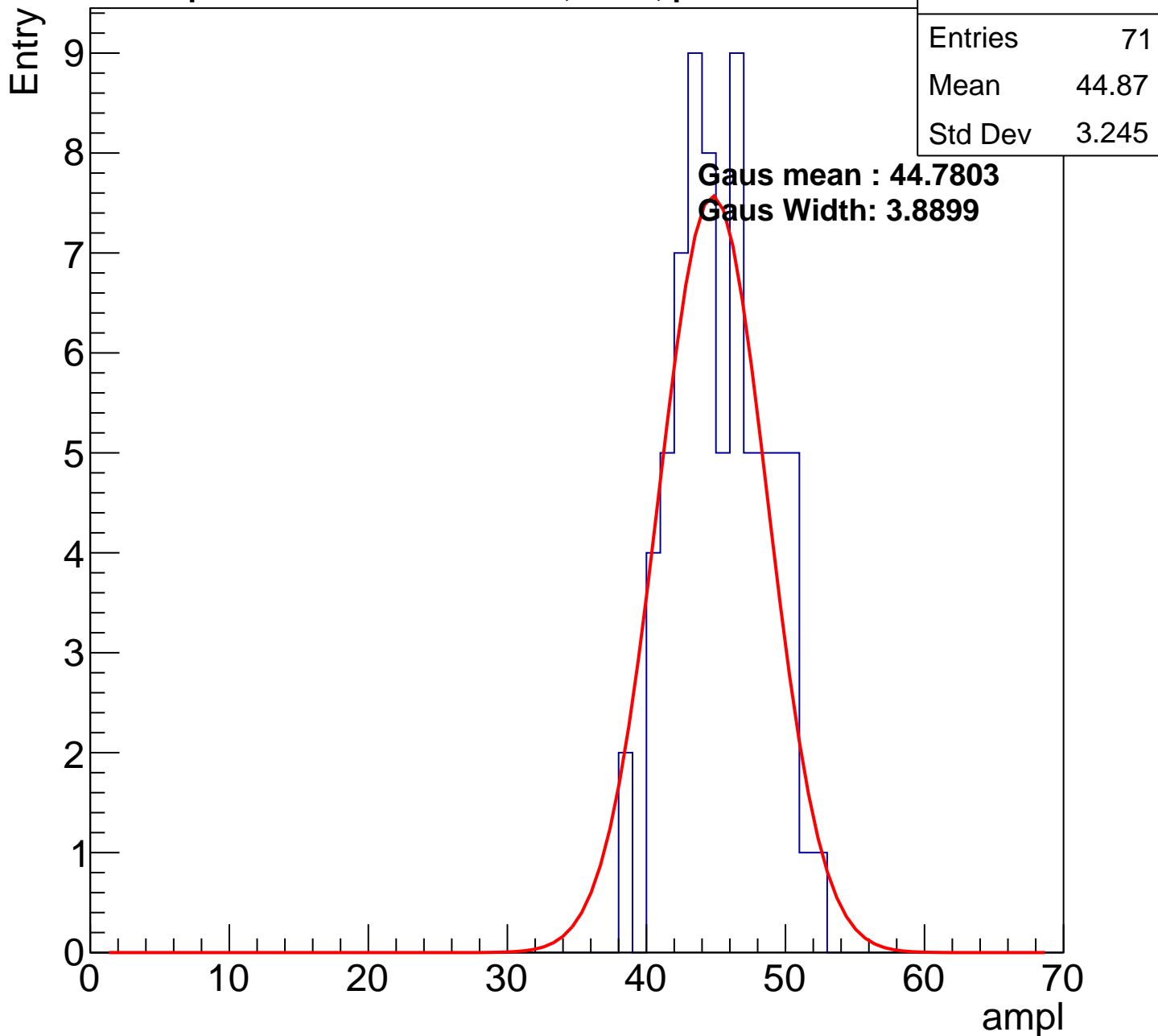
Gaus mean : 38.3503

Gaus Width: 3.0464



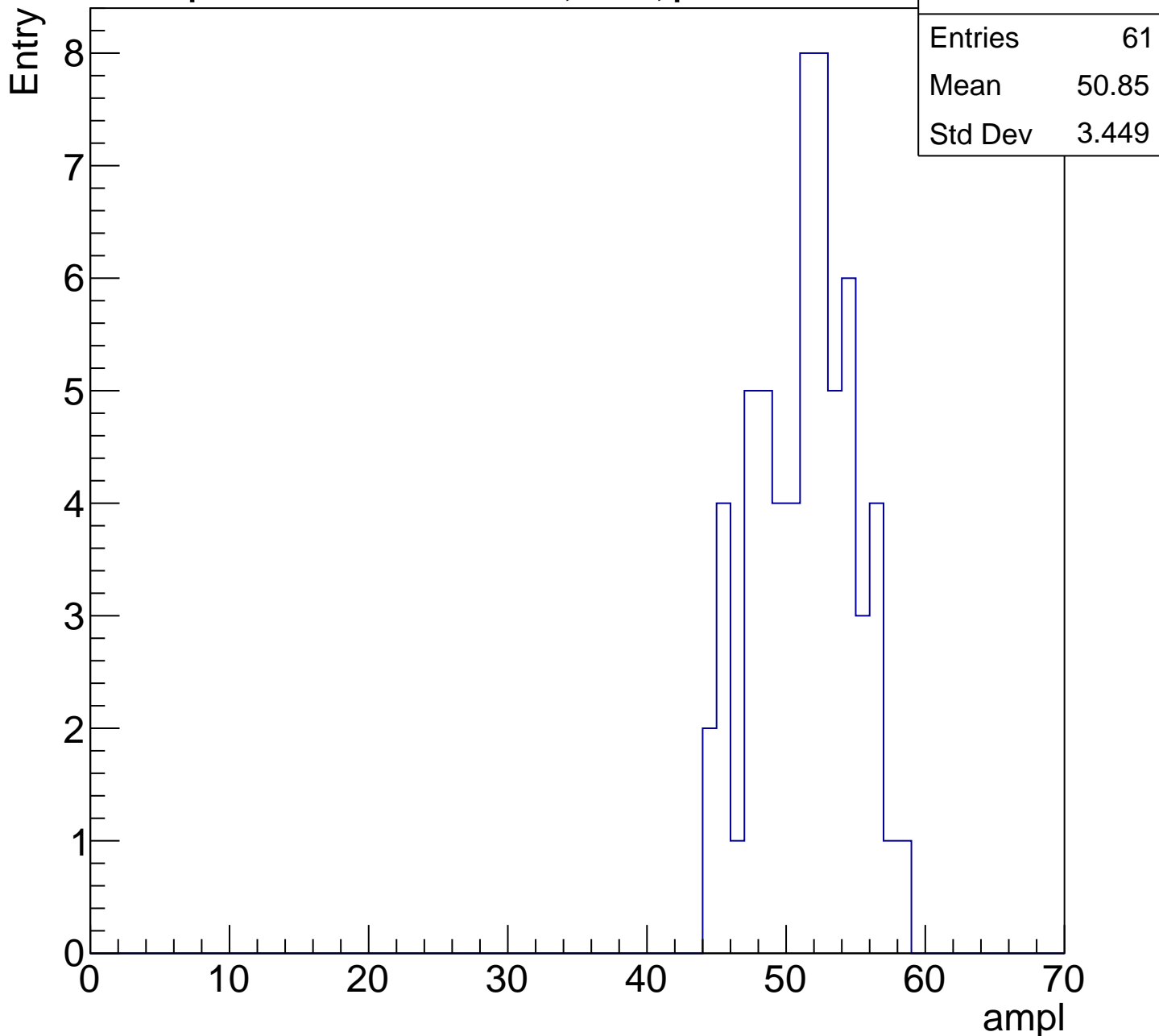
B1L101S, U25-ch4, adc2

calib_packv5_042523_0143.root, FC#0, port D2



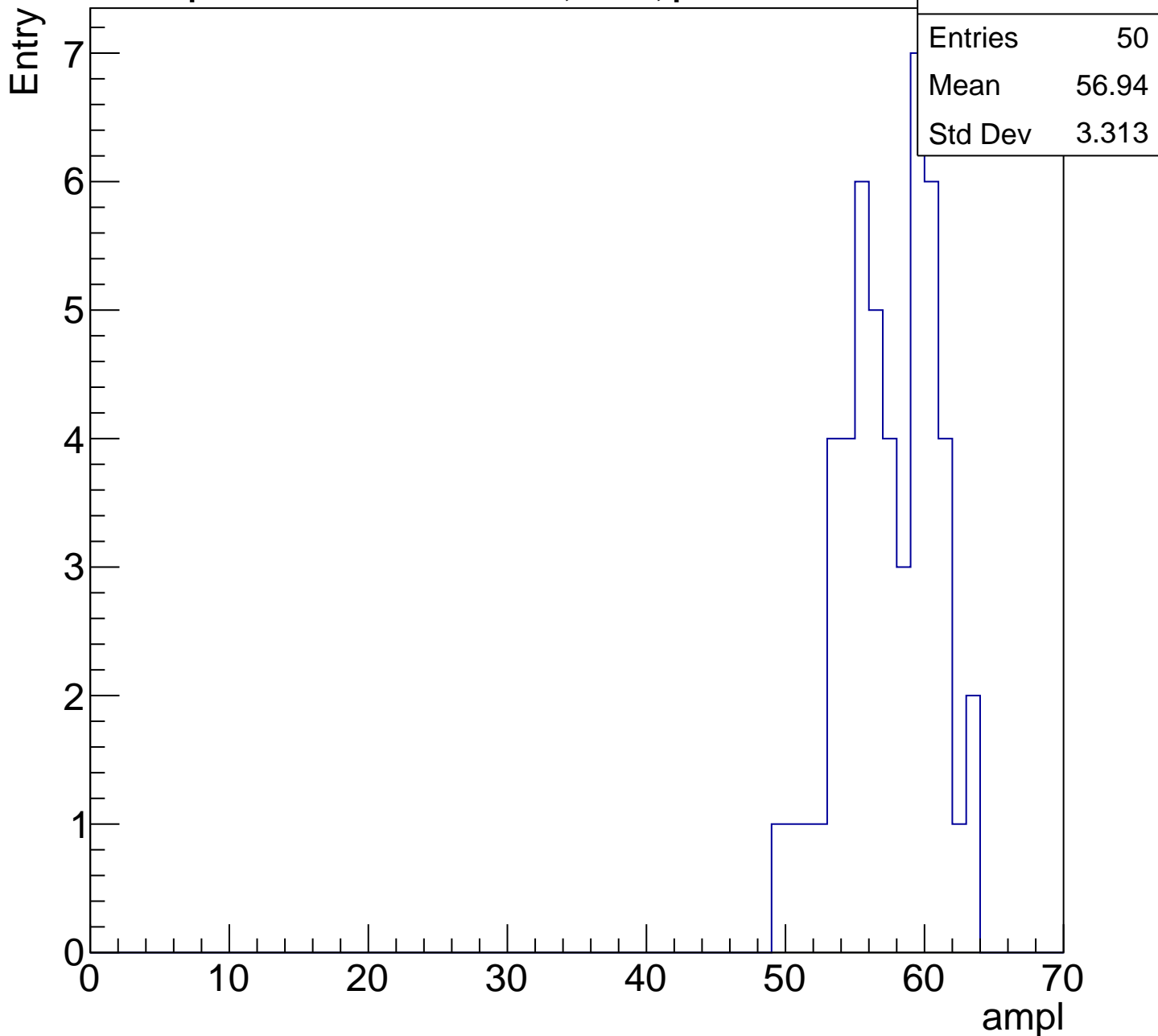
B1L101S, U25-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

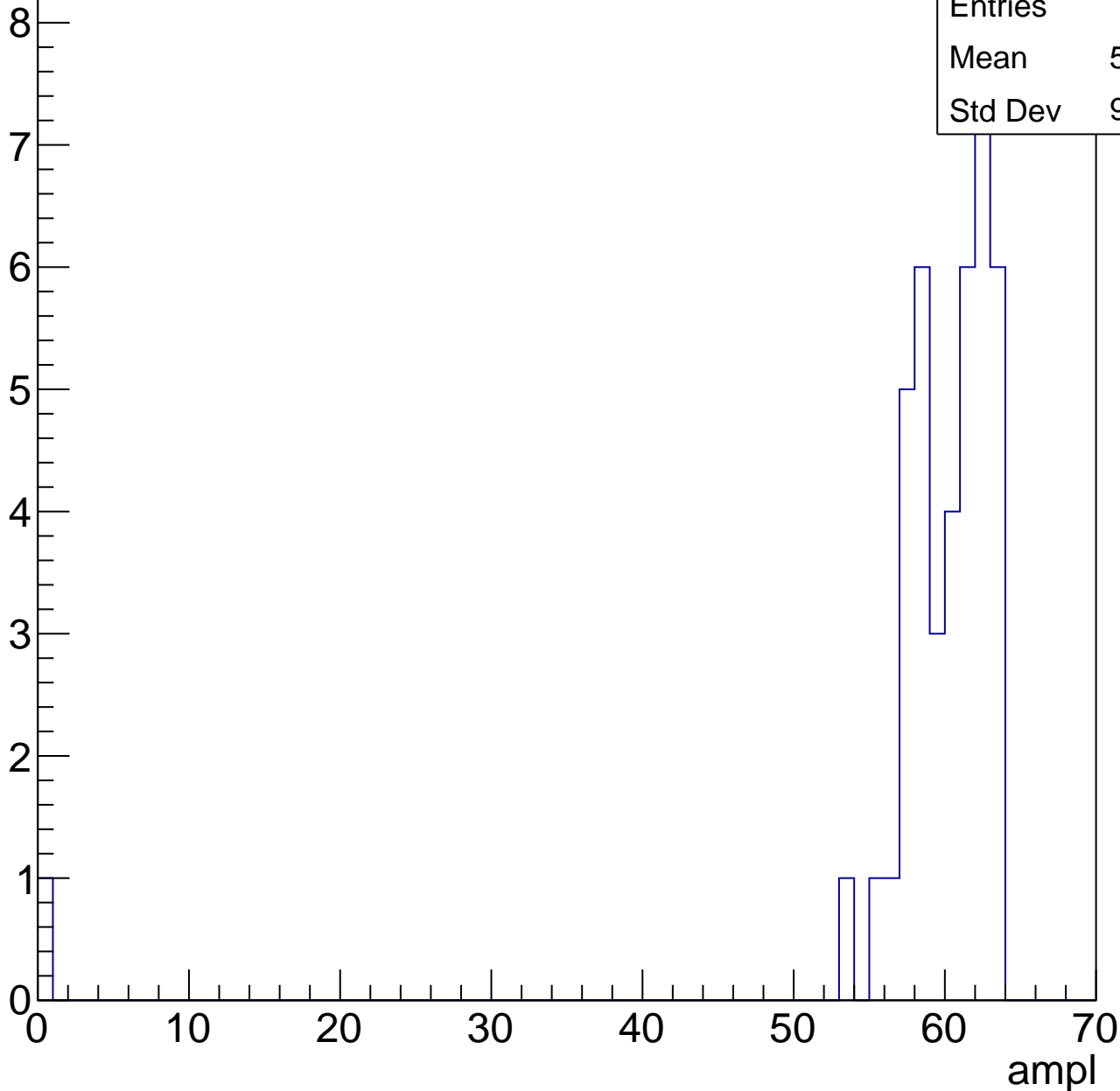


B1L101S, U25-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.43
Std Dev	9.449



B1L101S, U25-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch5, adc0

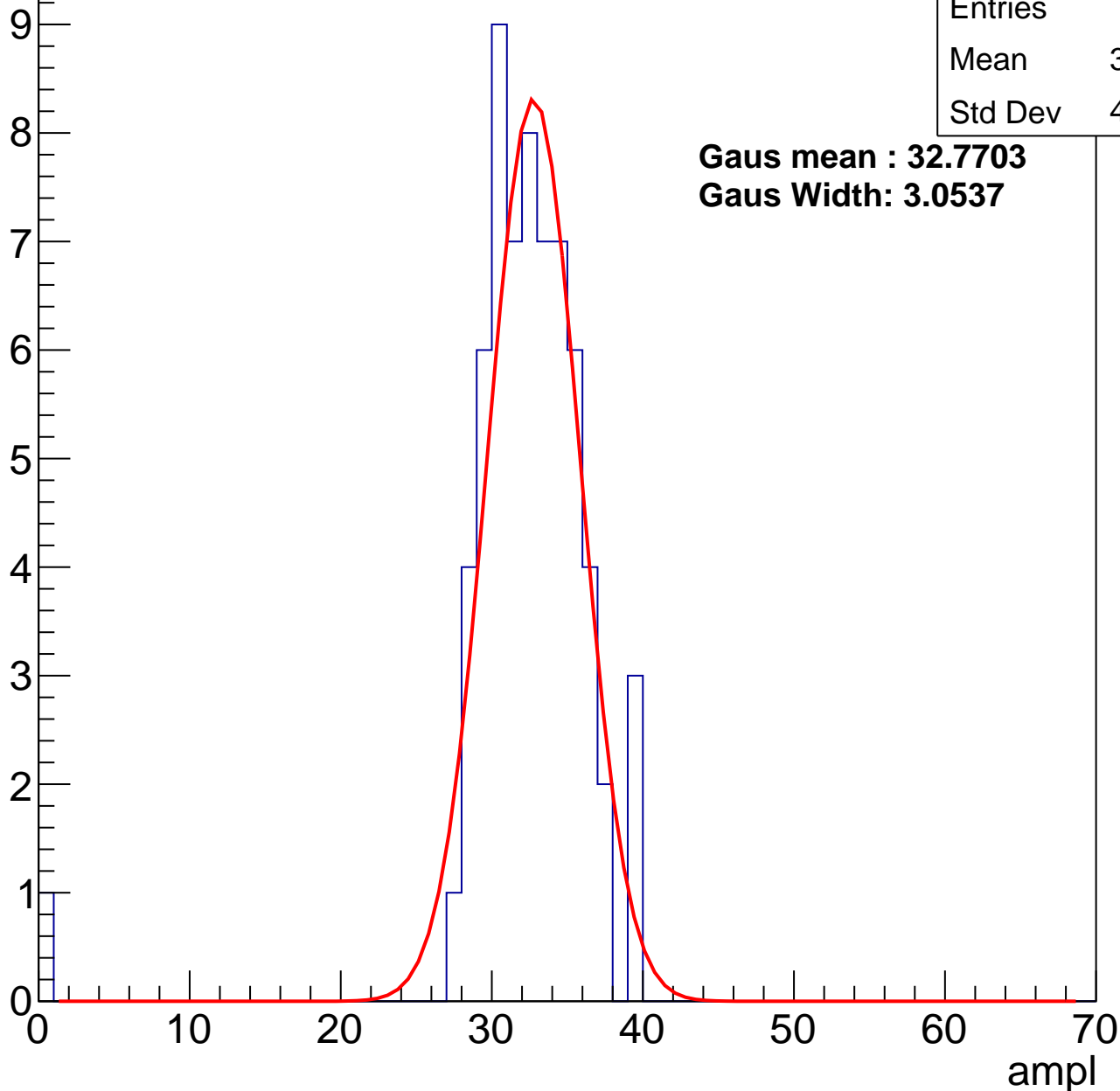
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	31.85
Std Dev	4.897

Gaus mean : 32.7703

Gaus Width: 3.0537



B1L101S, U25-ch5, adc1

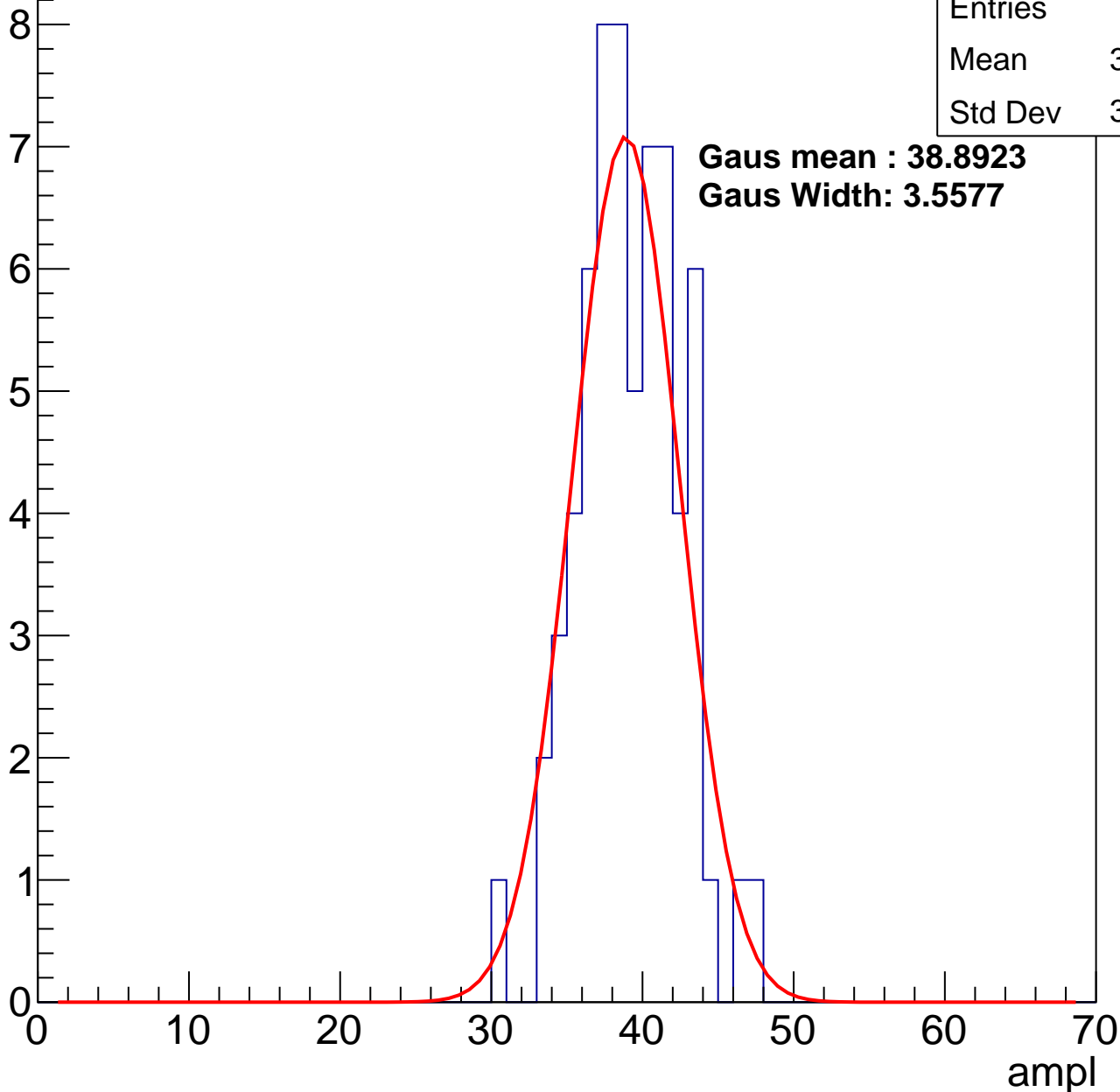
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	38.73
Std Dev	3.275

Gaus mean : 38.8923

Gaus Width: 3.5577



B1L101S, U25-ch5, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	44.48
Std Dev	2.89

Gaus mean : 45.5508

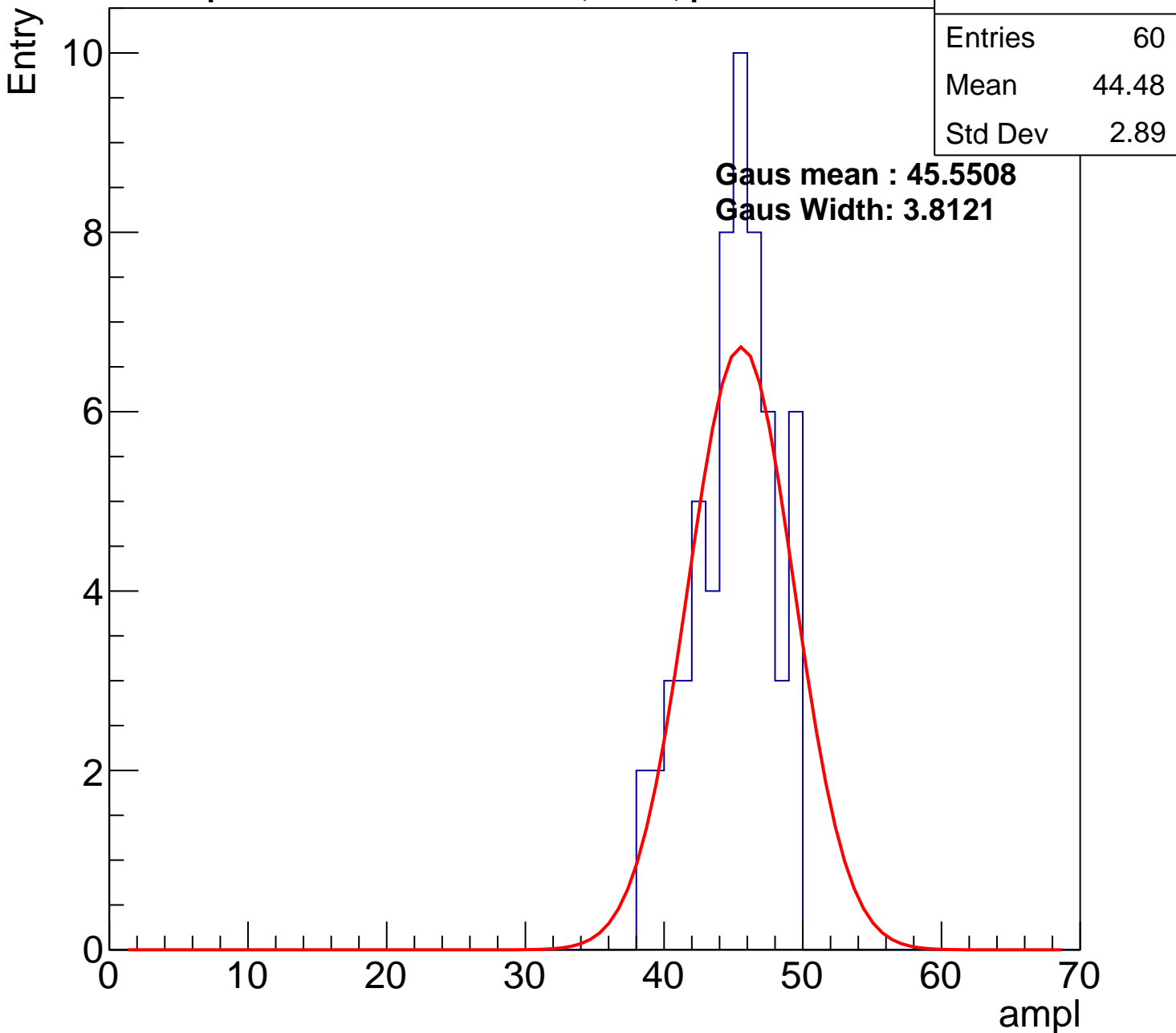
Gaus Width: 3.8121

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

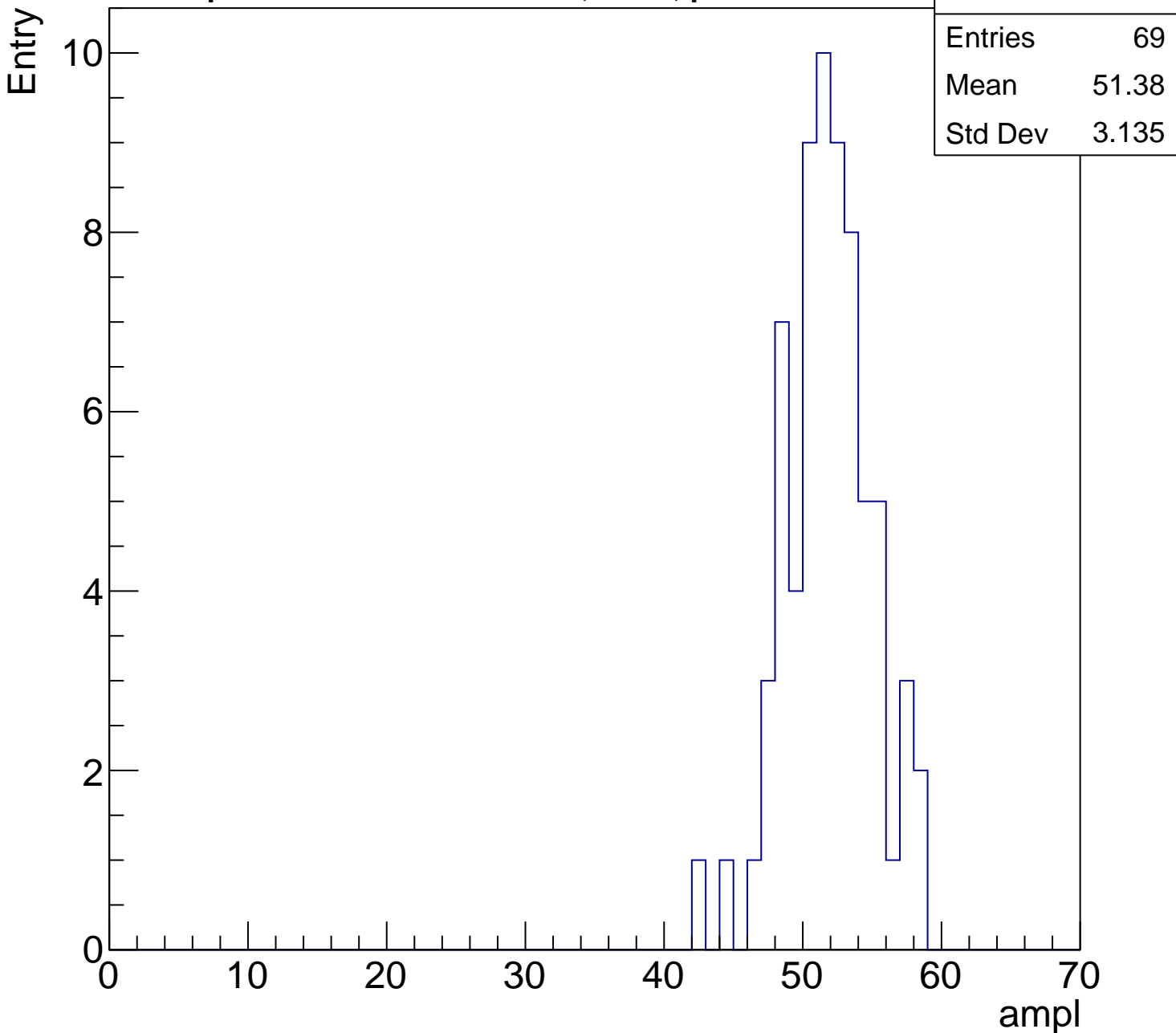
Entries	69
Mean	51.38
Std Dev	3.135

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

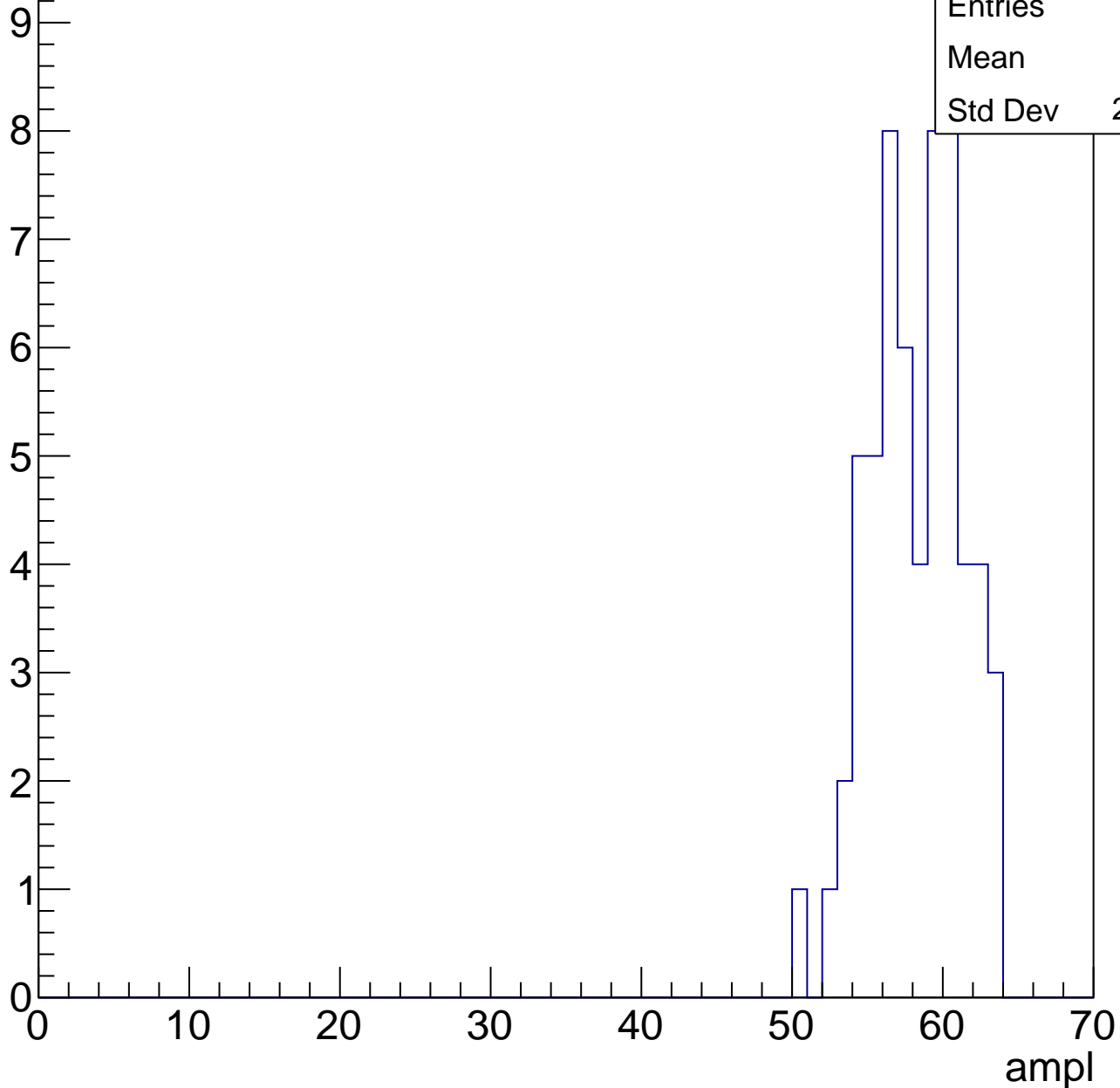
ampl



B1L101S, U25-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

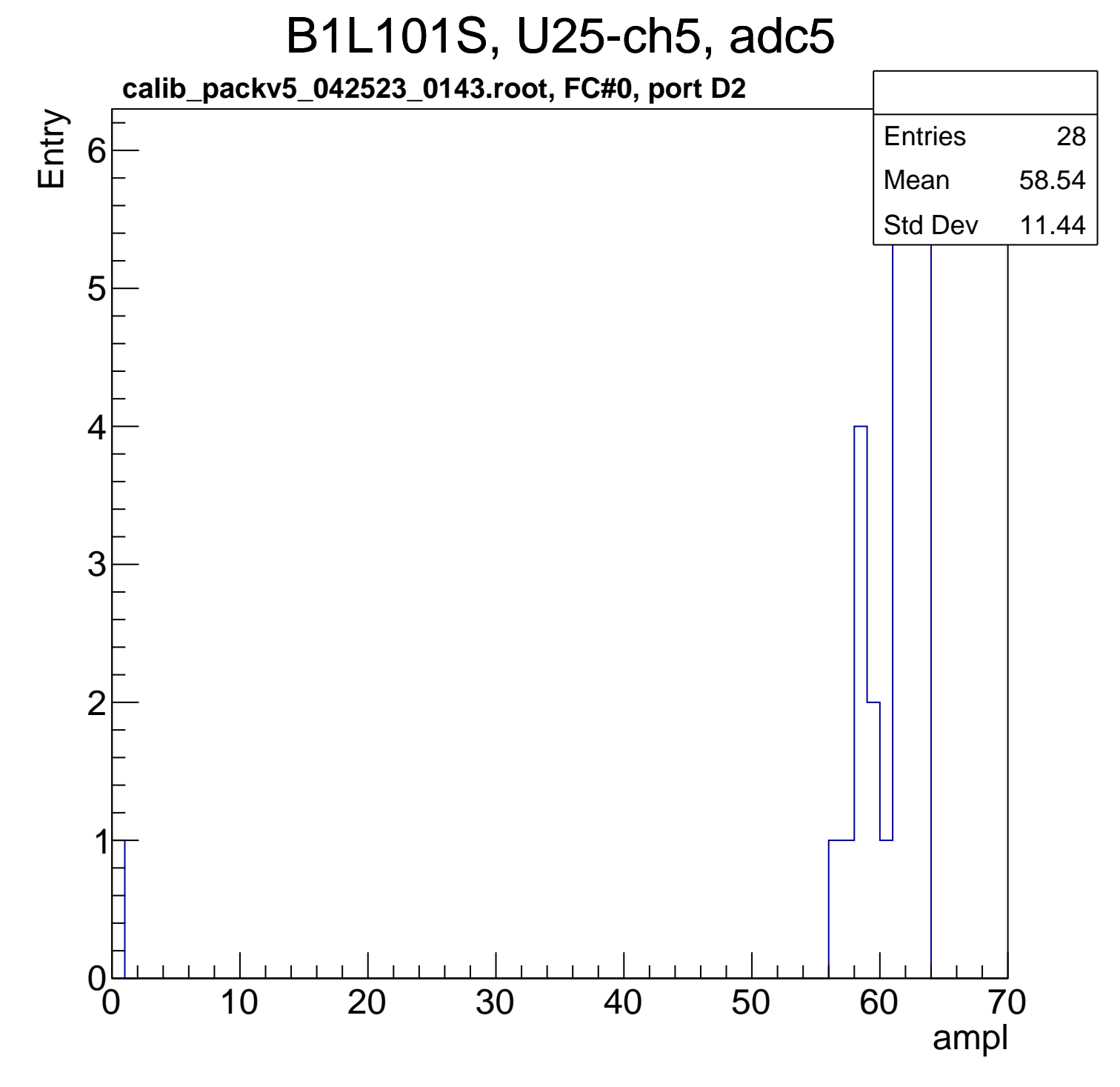
Entry

6
5
4
3
2
1
0

Entries	28
Mean	58.54
Std Dev	11.44

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U25-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	27
Std Dev	0

ampl

B1L101S, U25-ch6, adc0

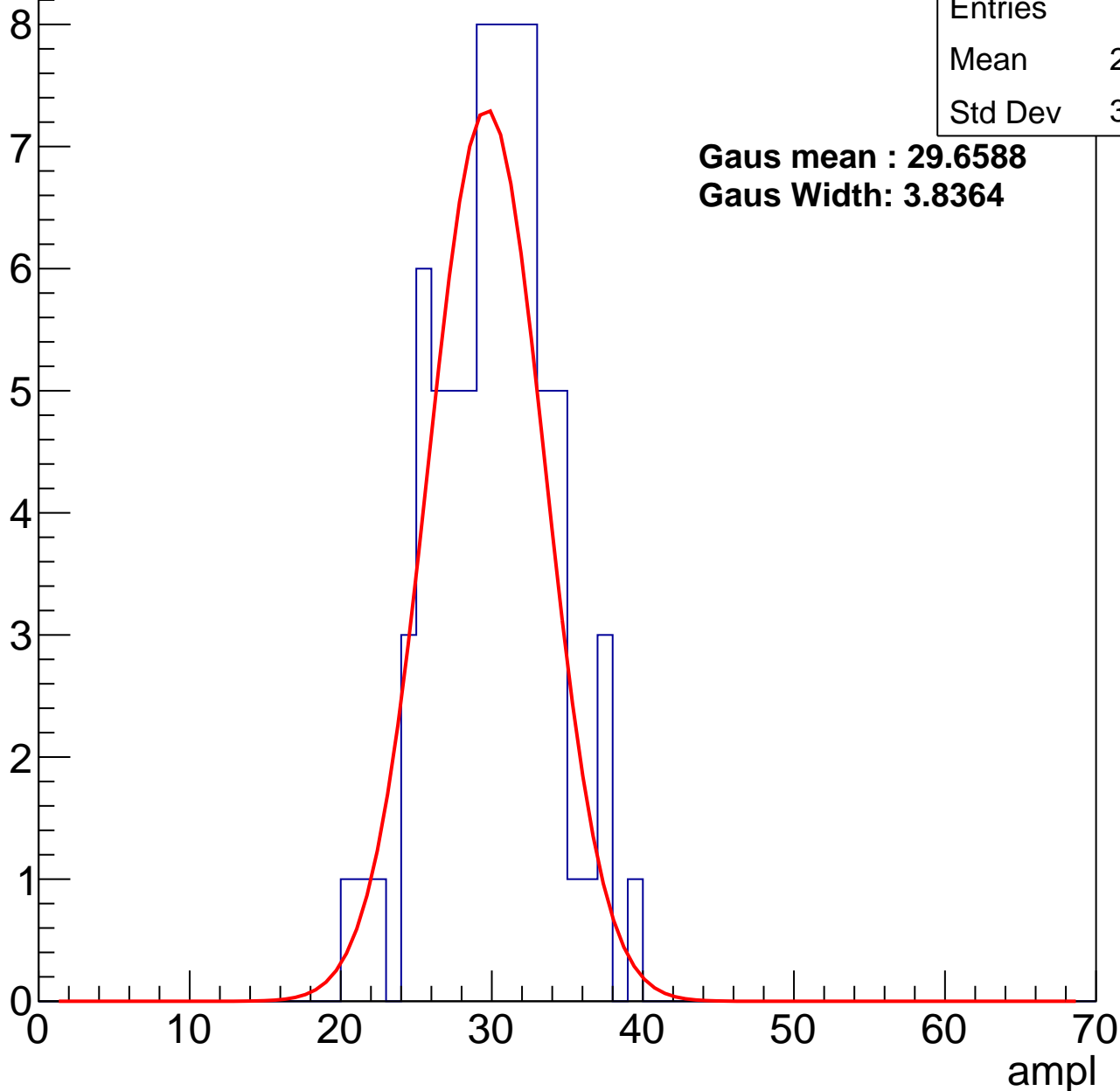
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.63
Std Dev	3.829

Gaus mean : 29.6588

Gaus Width: 3.8364



B1L101S, U25-ch6, adc1

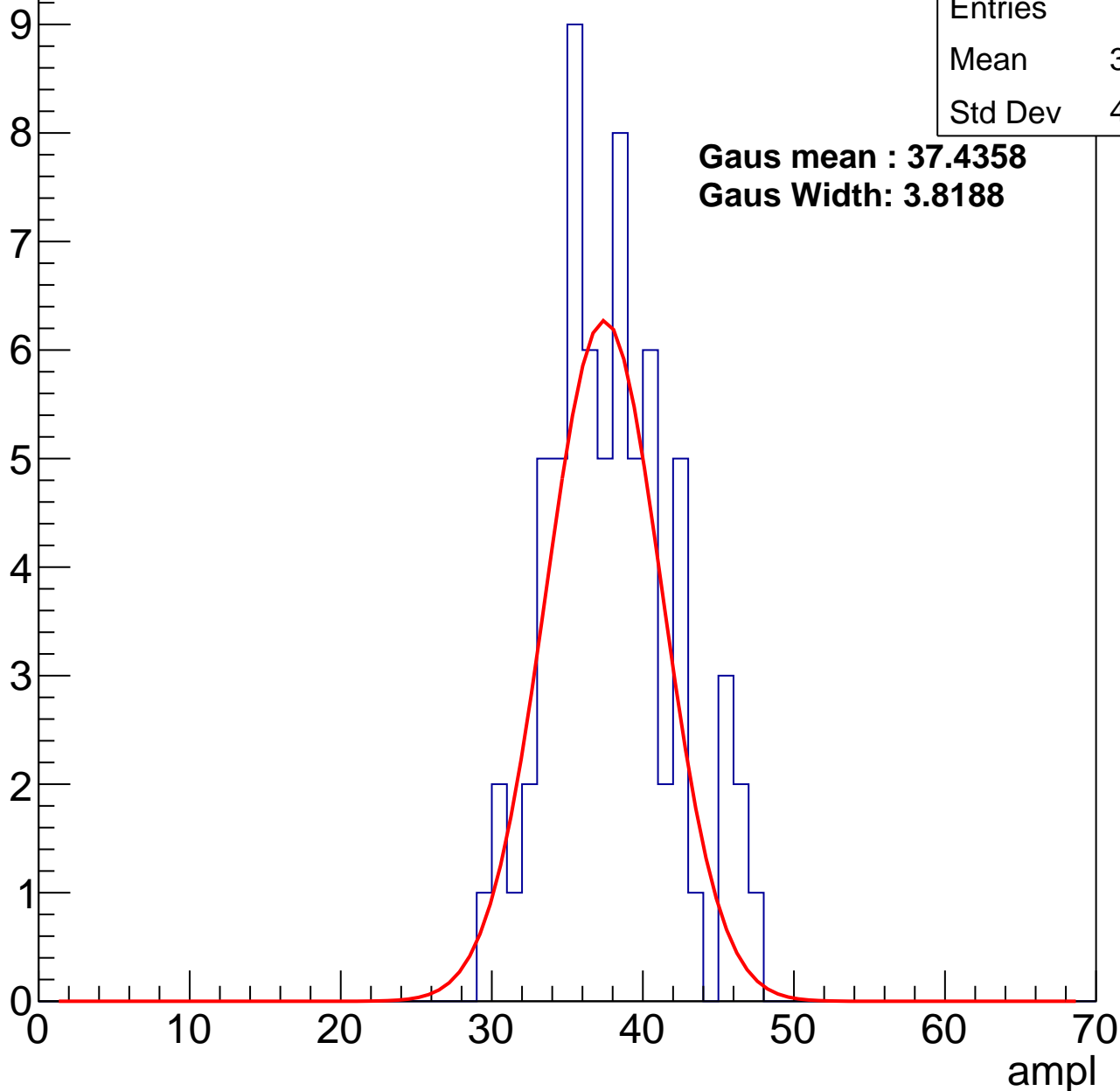
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.43
Std Dev	4.049

Gaus mean : 37.4358

Gaus Width: 3.8188



B1L101S, U25-ch6, adc2

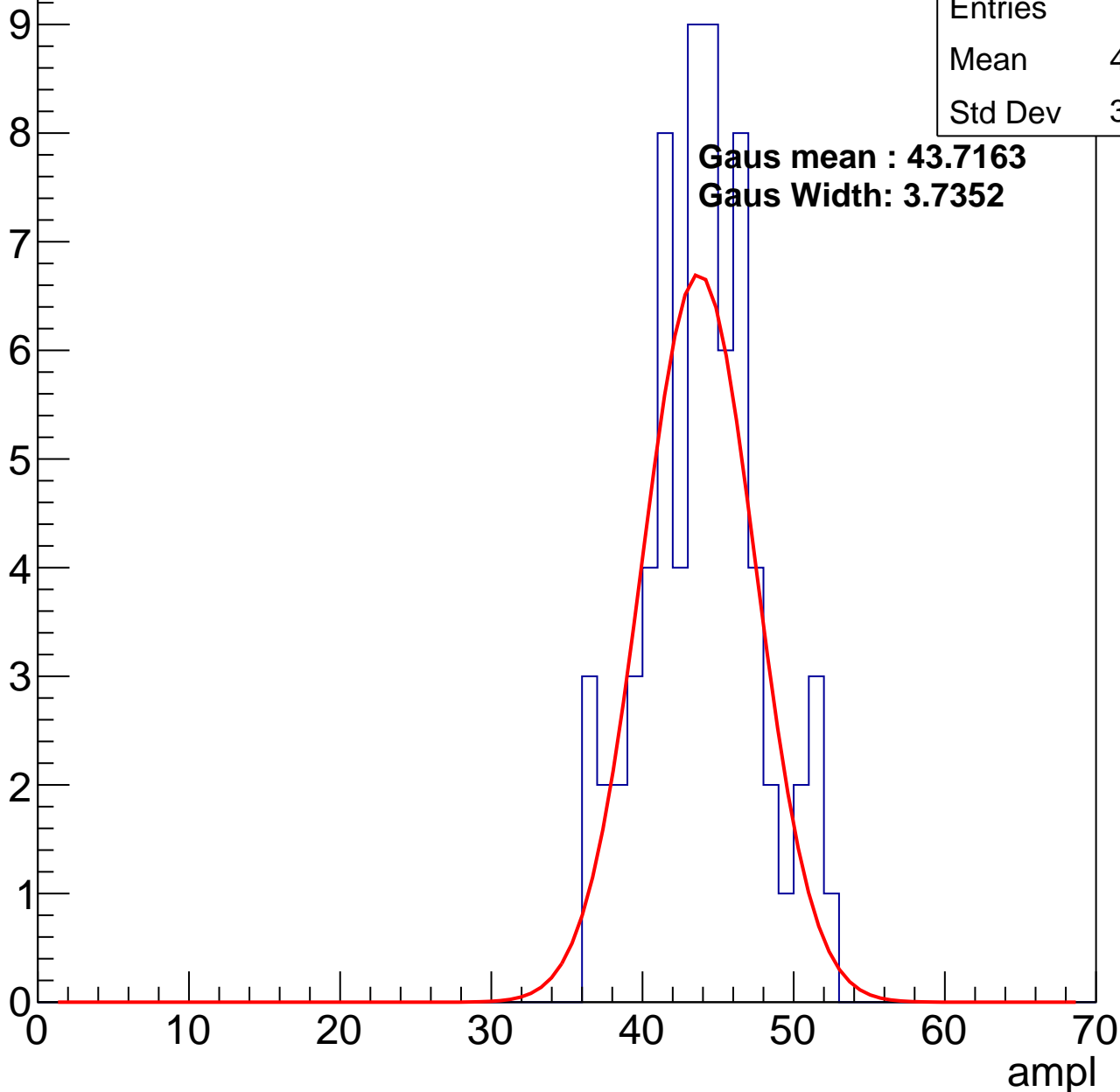
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	43.52
Std Dev	3.745

Gaus mean : 43.7163

Gaus Width: 3.7352

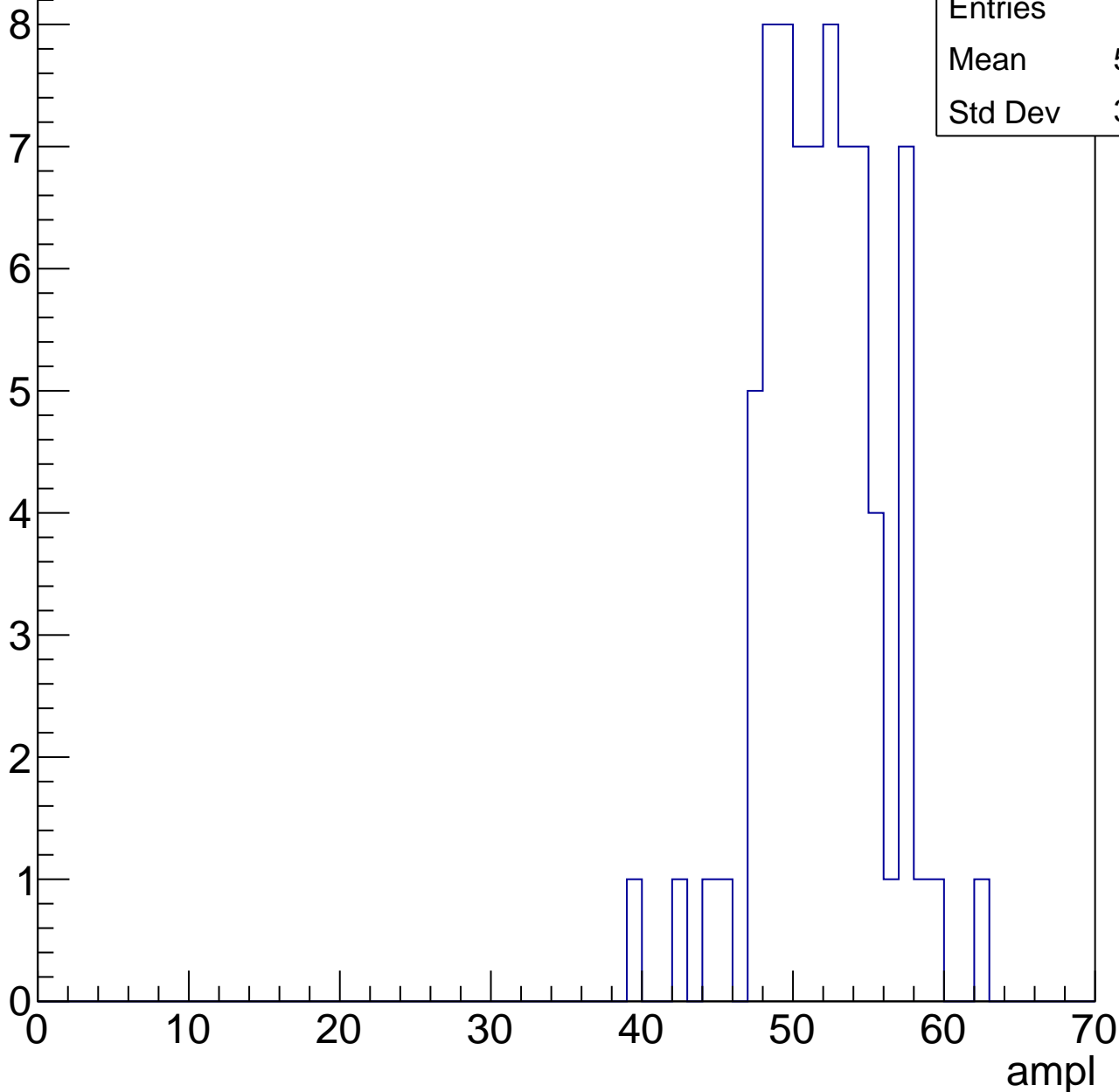


B1L101S, U25-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

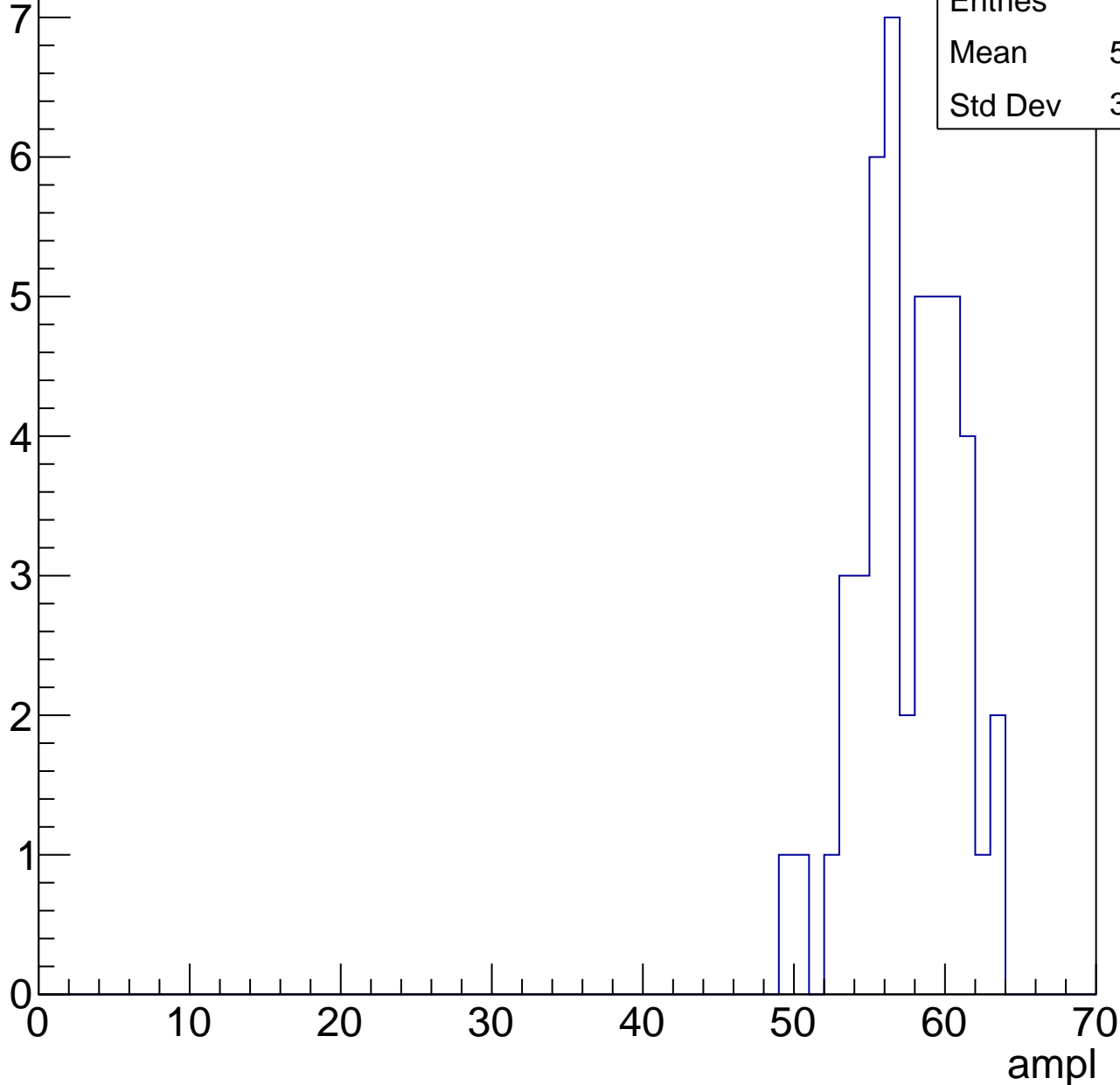
Entries	76
Mean	51.41
Std Dev	3.911



B1L101S, U25-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	46
Mean	57.07
Std Dev	3.213

B1L101S, U25-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	34
Mean	58.65
Std Dev	10.41

ampl

B1L101S, U25-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch7, adc0

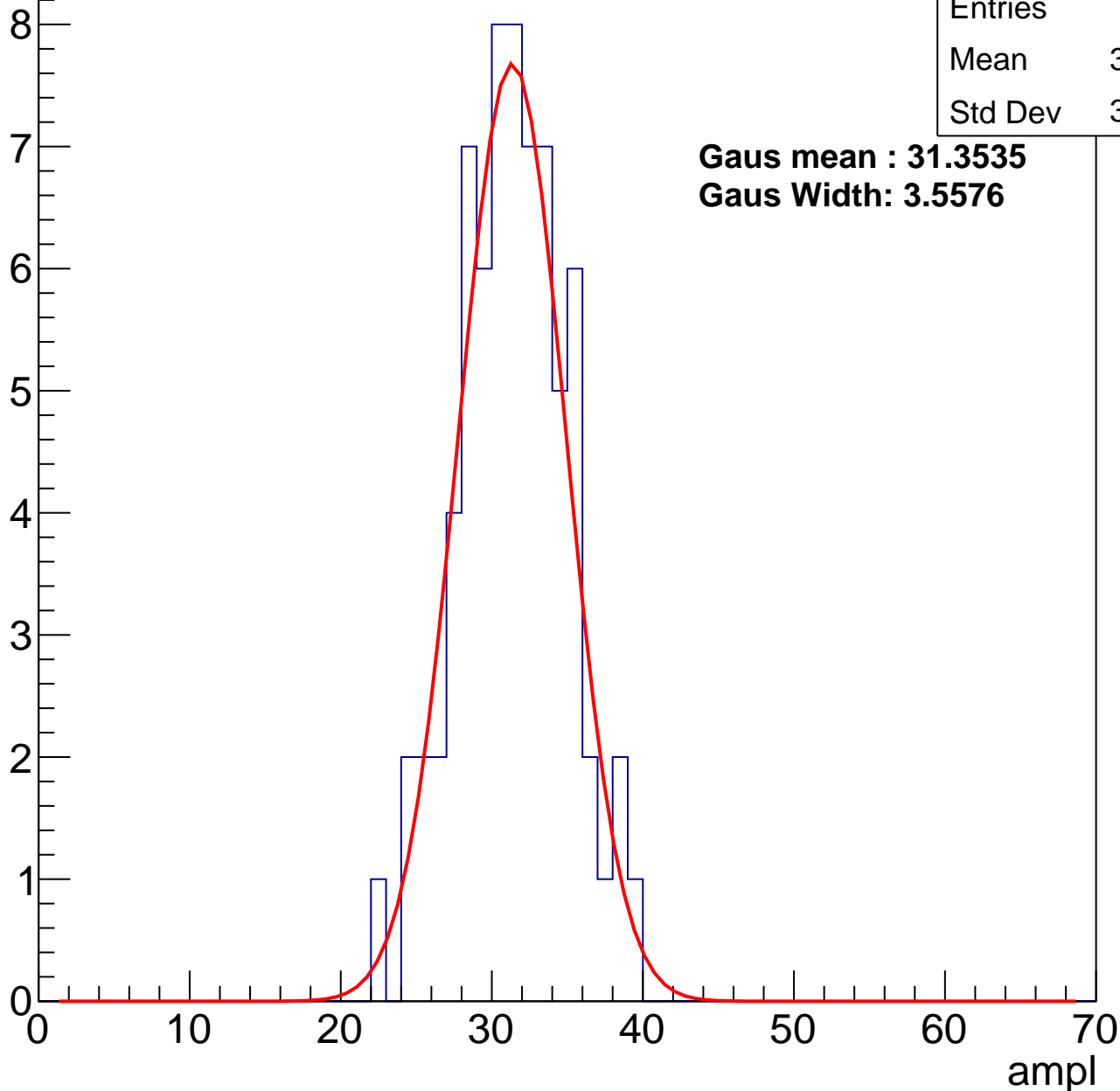
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	30.94
Std Dev	3.528

Gaus mean : 31.3535

Gaus Width: 3.5576



B1L101S, U25-ch7, adc1

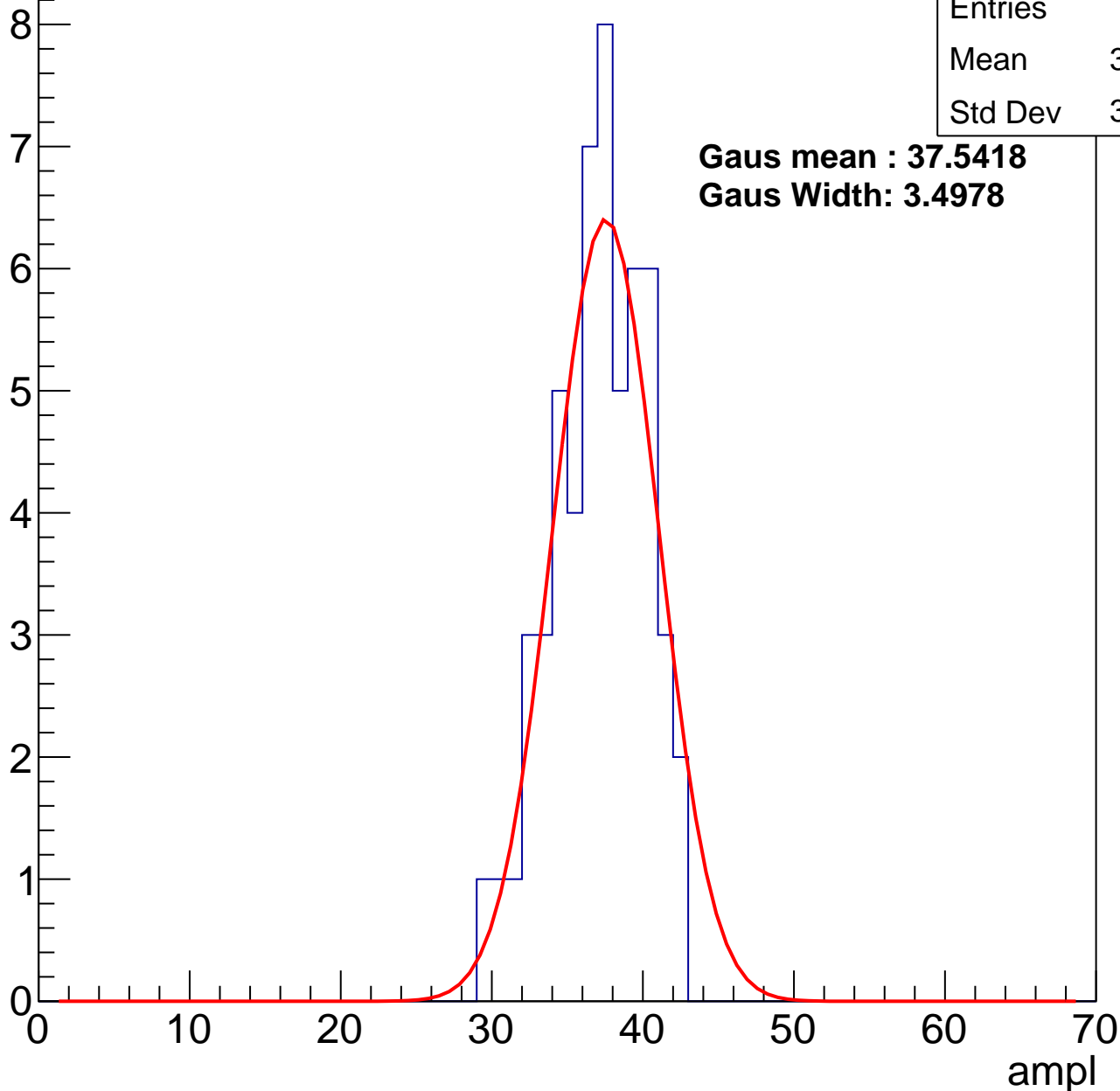
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.62
Std Dev	3.054

Gaus mean : 37.5418

Gaus Width: 3.4978



B1L101S, U25-ch7, adc2

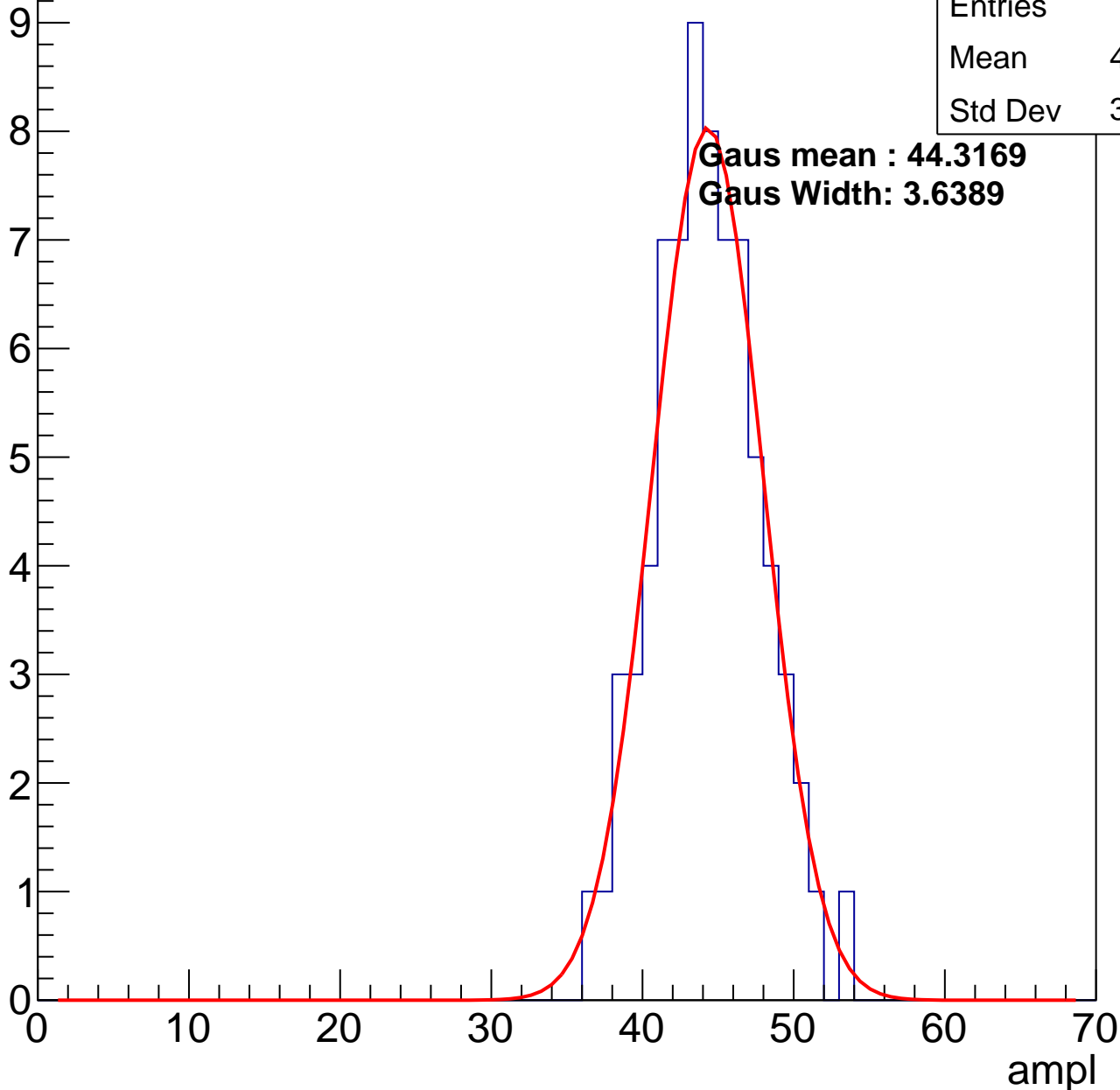
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.82
Std Dev	3.469

Gaus mean : 44.3169

Gaus Width: 3.6389

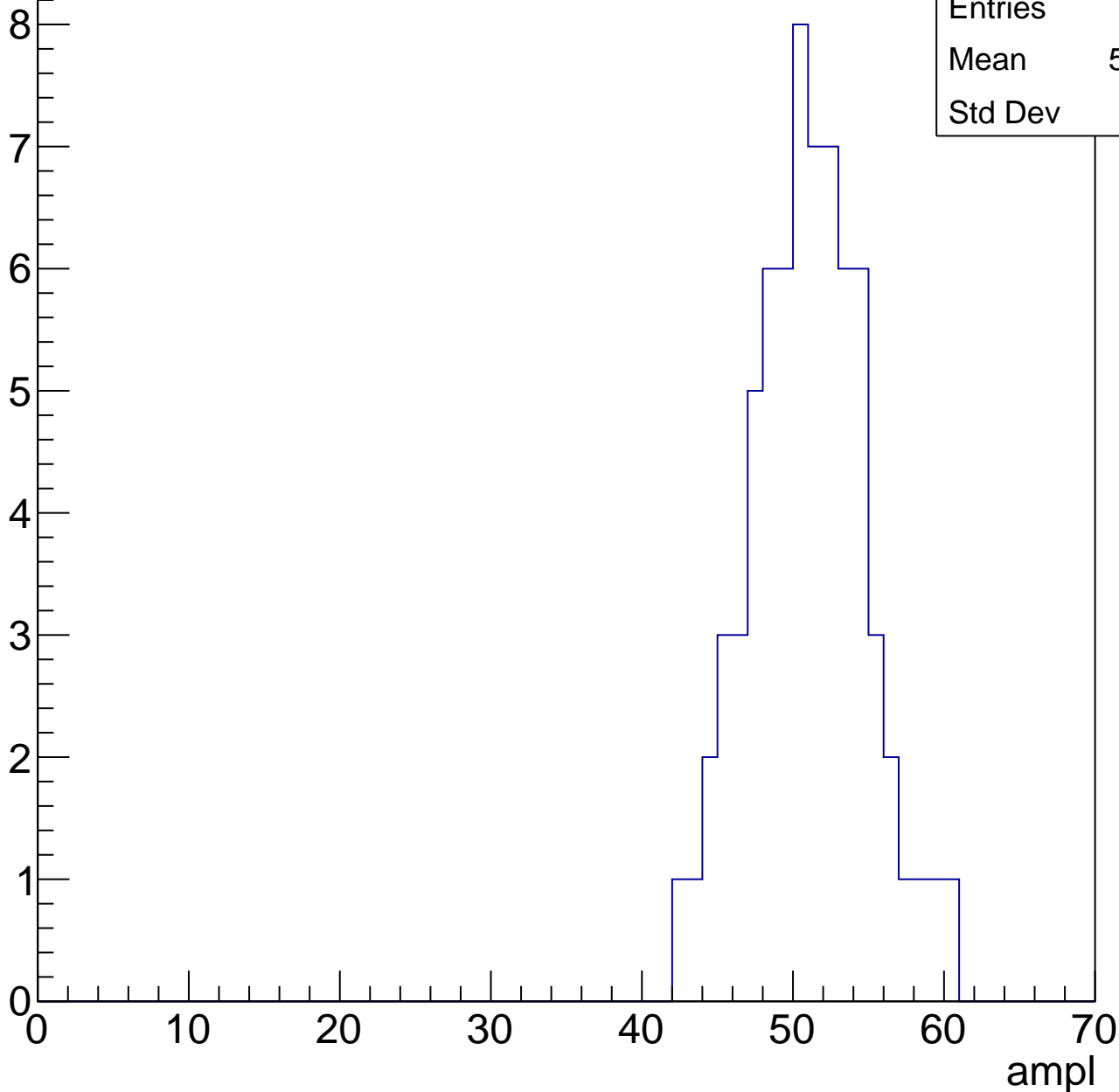


B1L101S, U25-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	50.53
Std Dev	3.76

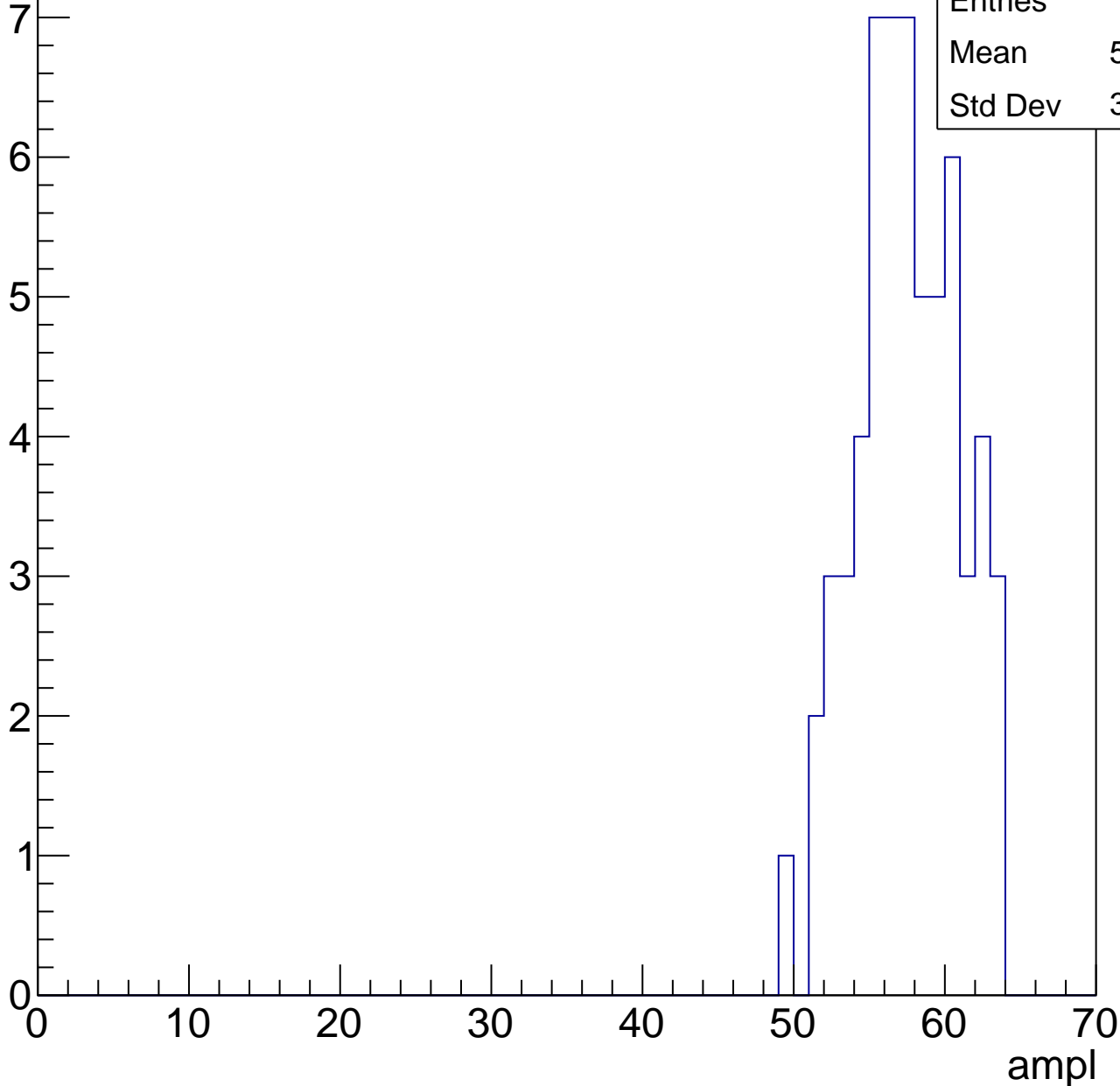


B1L101S, U25-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	57.05
Std Dev	3.329

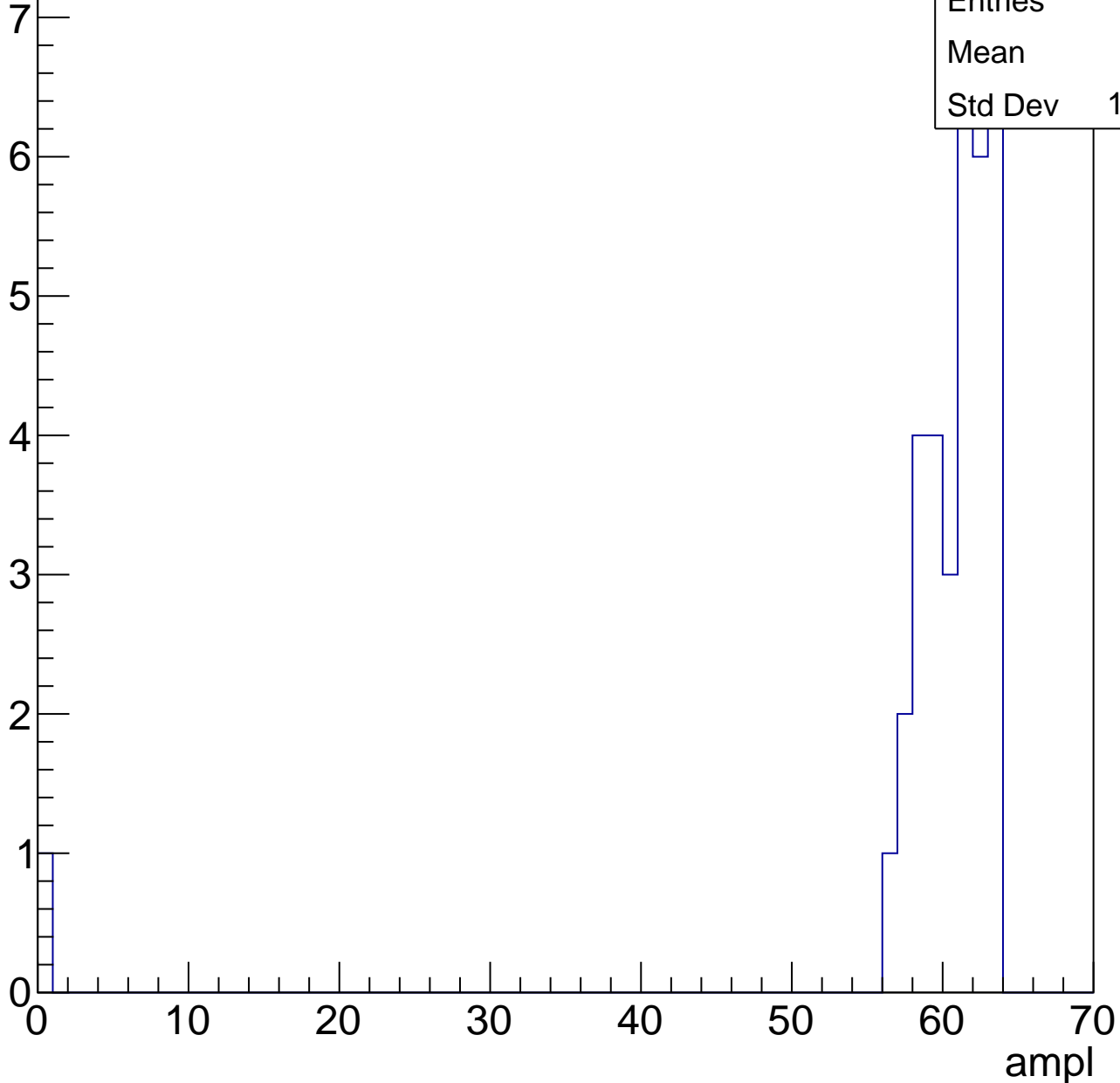


B1L101S, U25-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.8
Std Dev	10.28



B1L101S, U25-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch8, adc0

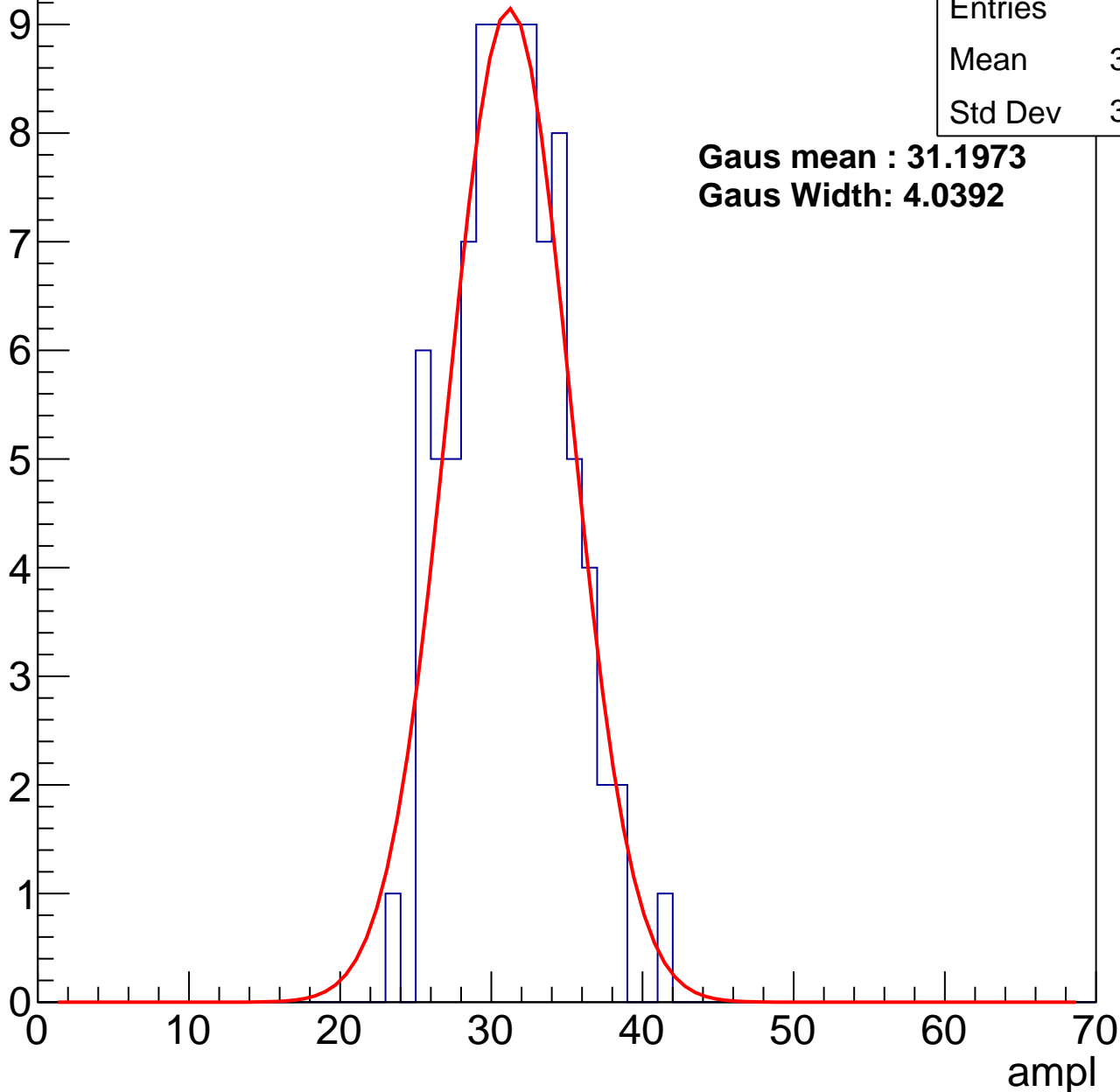
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	30.84
Std Dev	3.594

Gaus mean : 31.173

Gaus Width: 4.0392



B1L101S, U25-ch8, adc1

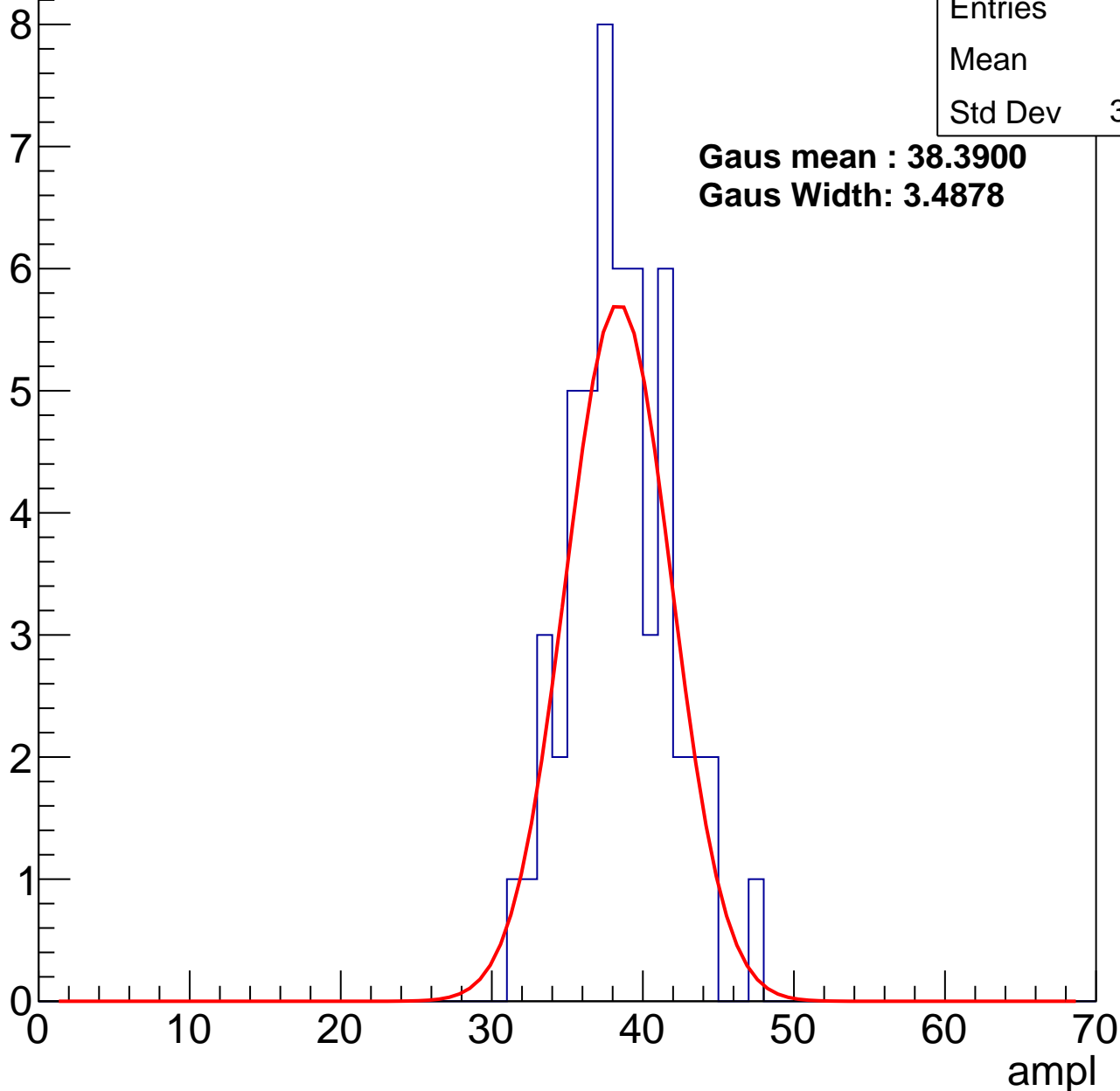
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	38
Std Dev	3.285

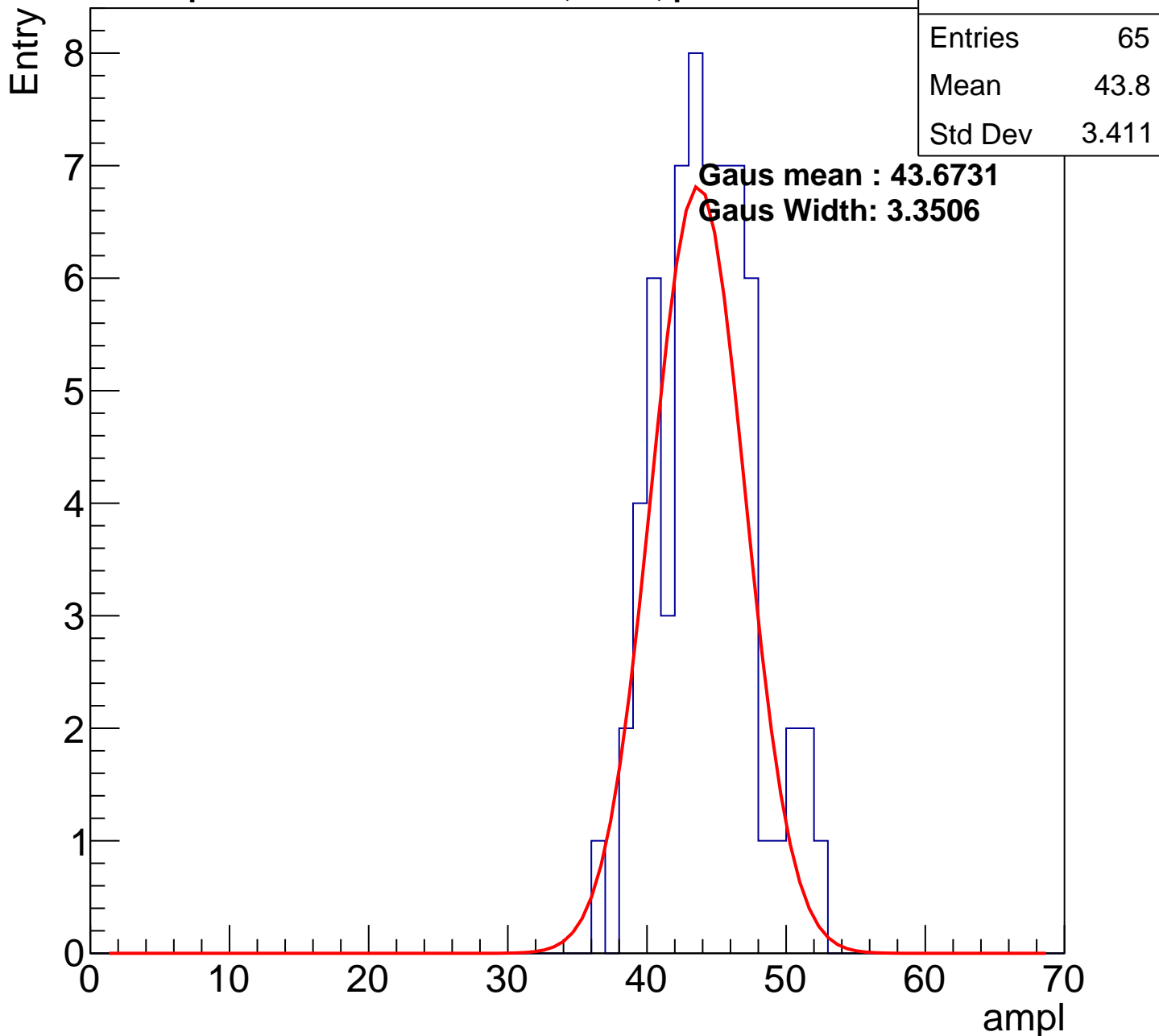
Gaus mean : 38.3900

Gaus Width: 3.4878



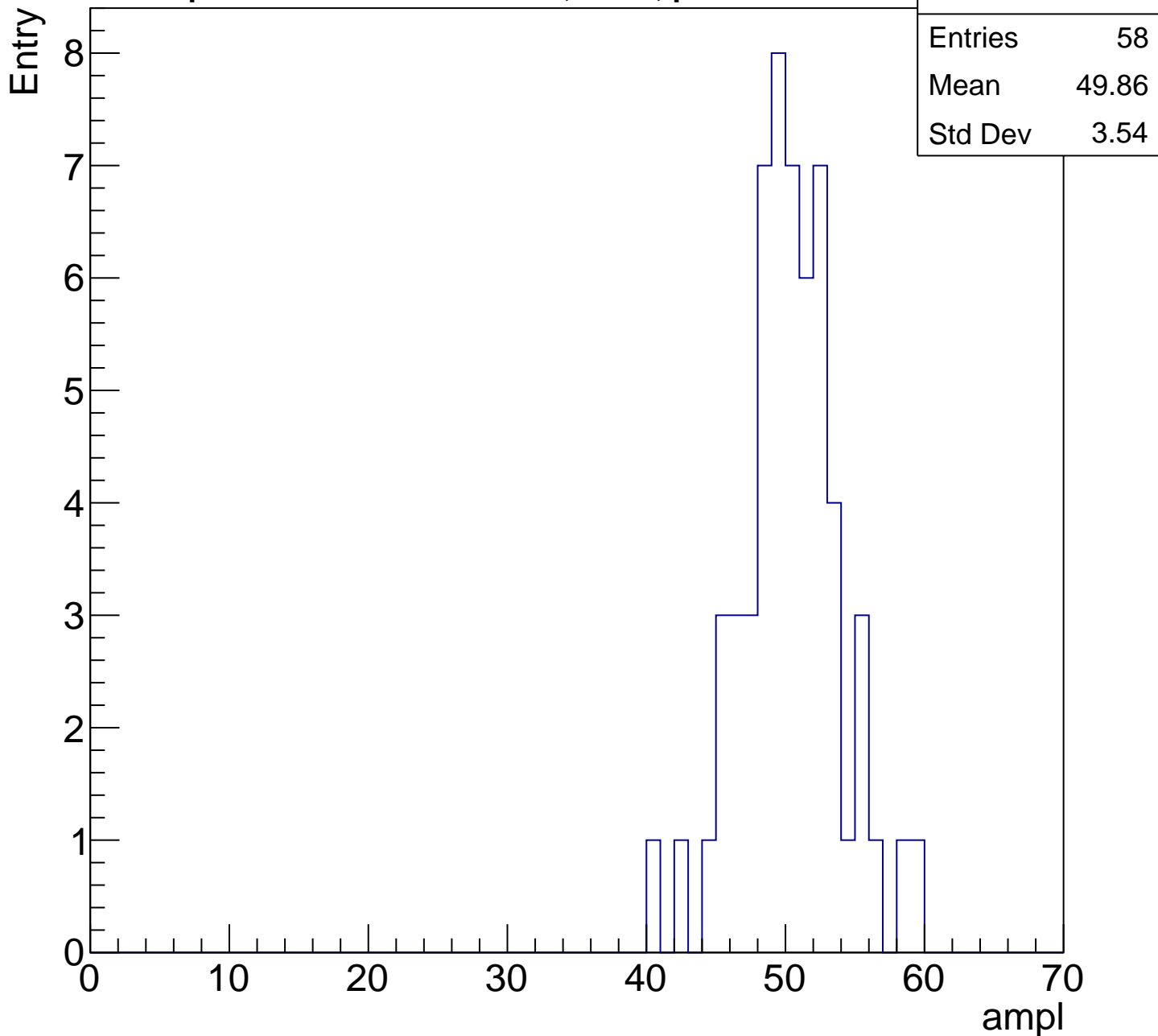
B1L101S, U25-ch8, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch8, adc3

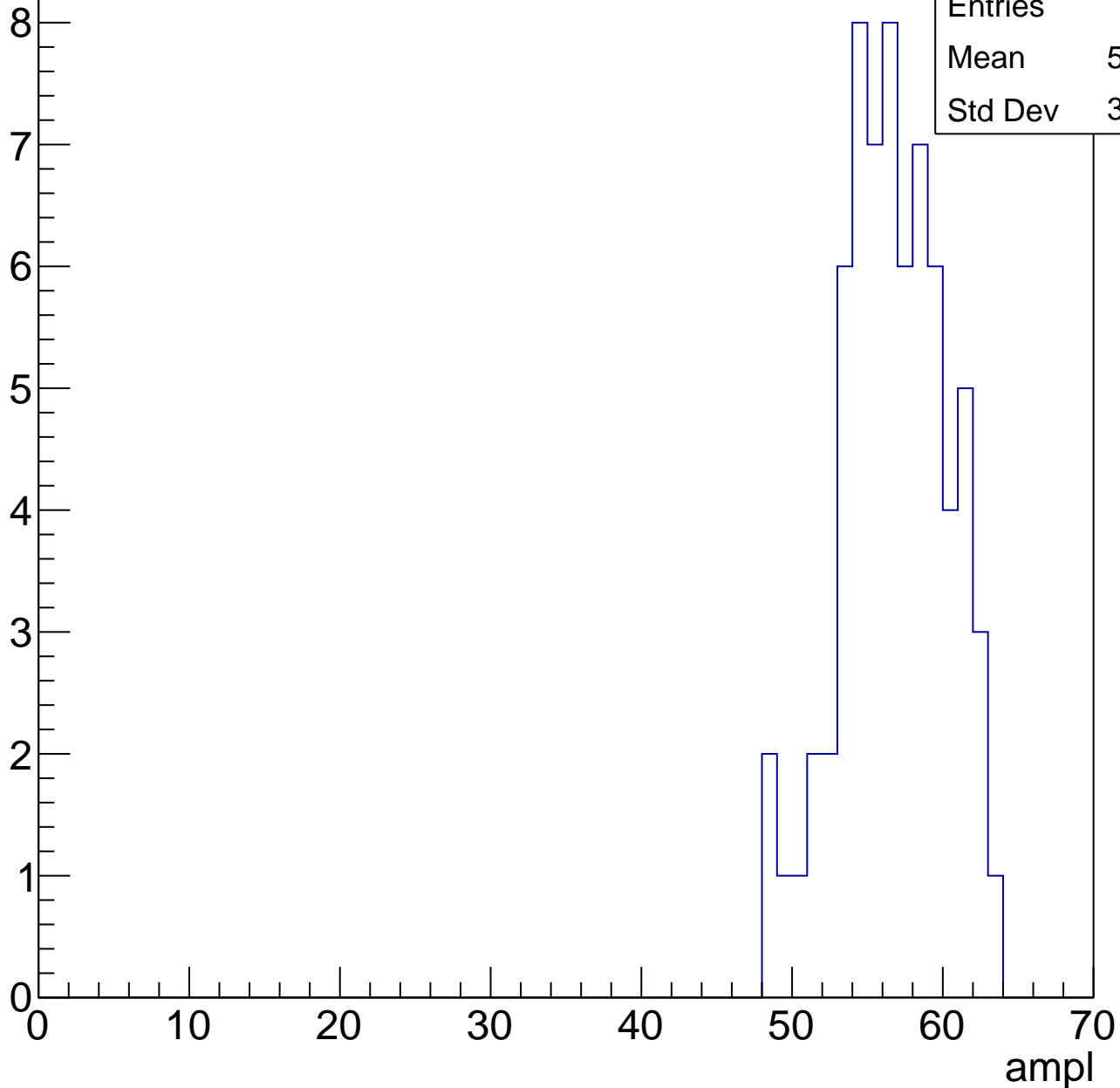
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



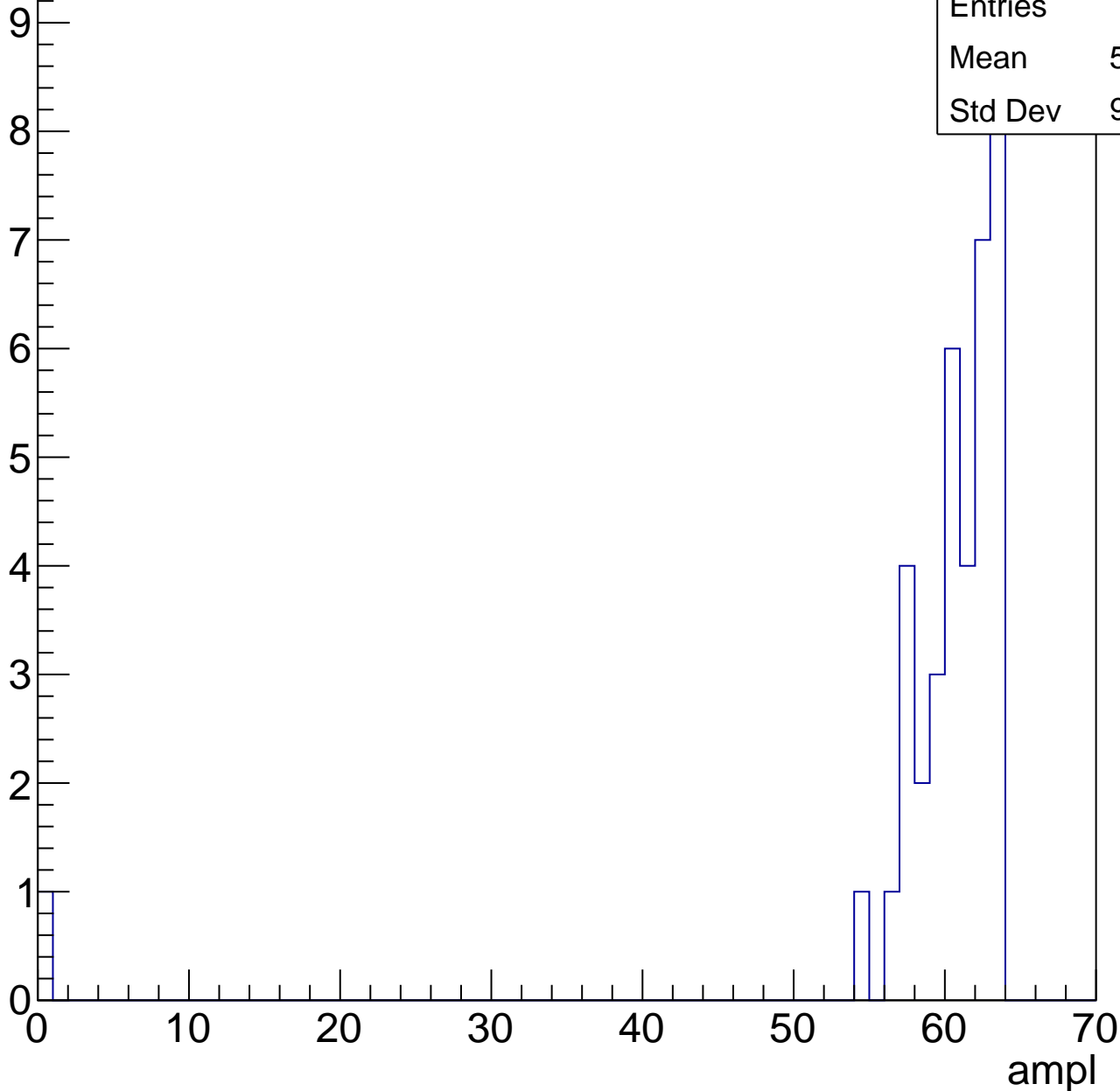
Entries	69
Mean	56.23
Std Dev	3.444

B1L101S, U25-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.84
Std Dev	9.949



B1L101S, U25-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	61
Std Dev	0

ampl

B1L101S, U25-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch9, adc0

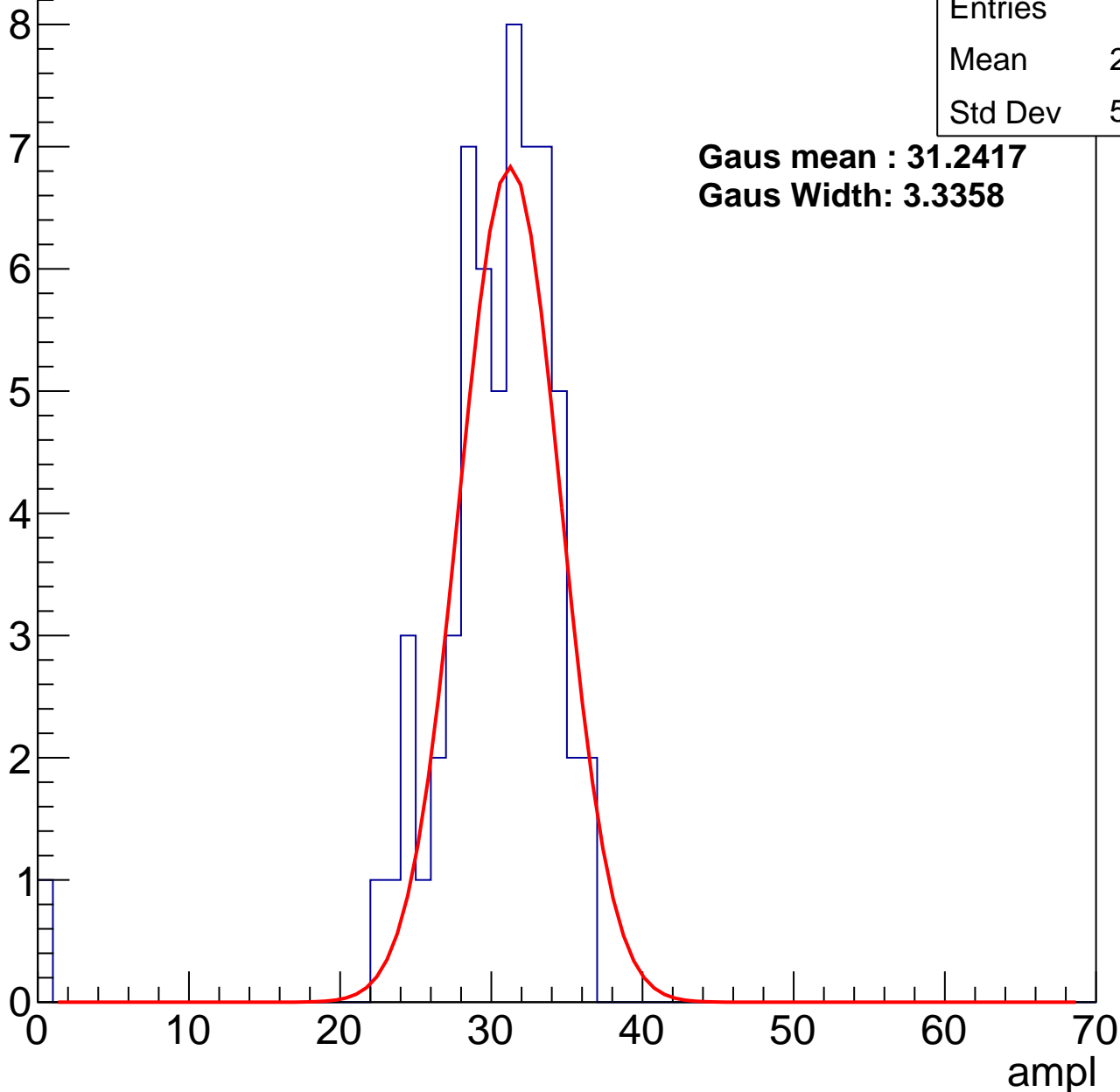
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.67
Std Dev	5.014

Gaus mean : 31.2417

Gaus Width: 3.3358



B1L101S, U25-ch9, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	37.46
Std Dev	3.694

Gaus mean : 37.9711

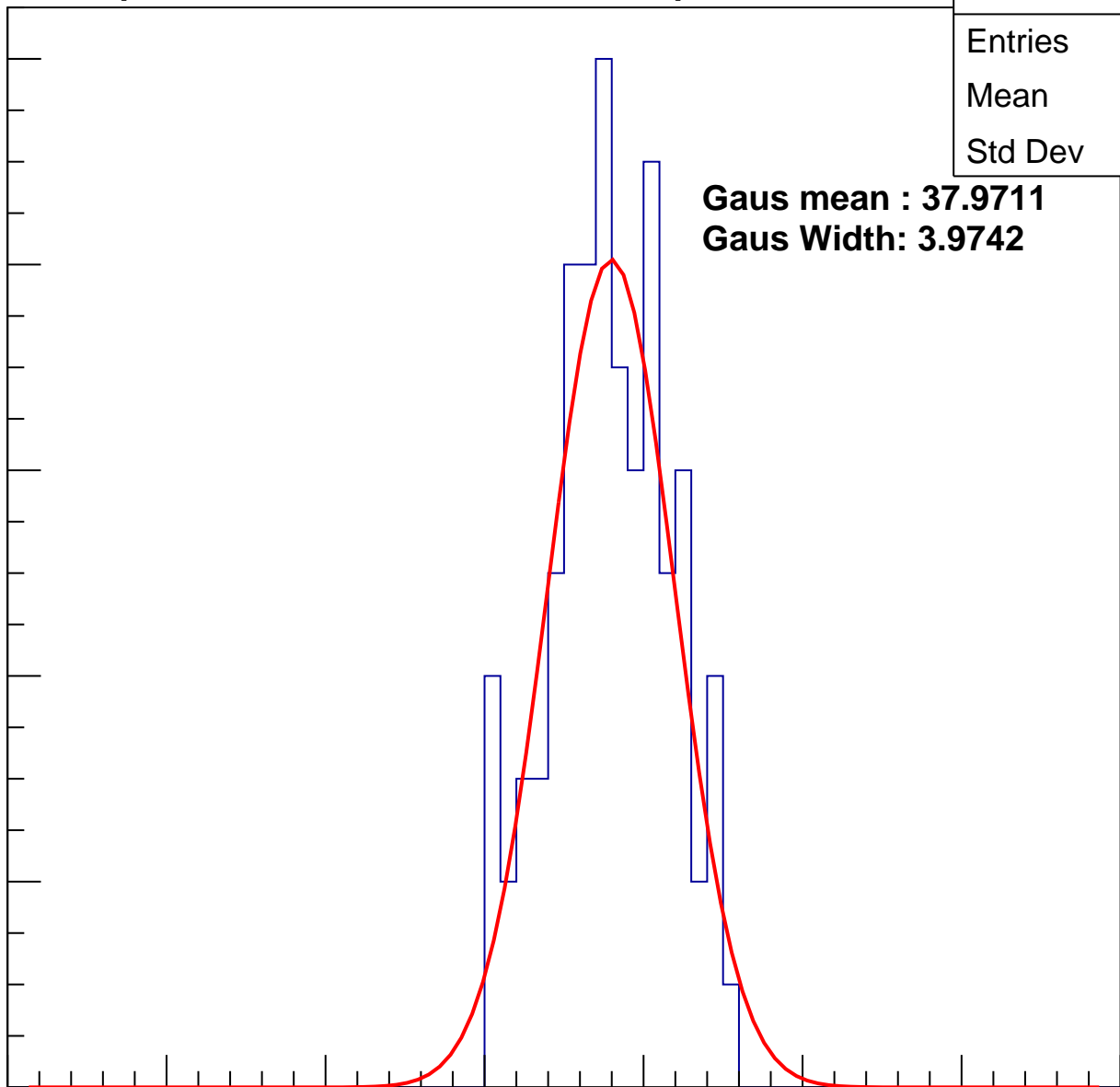
Gaus Width: 3.9742

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch9, adc2

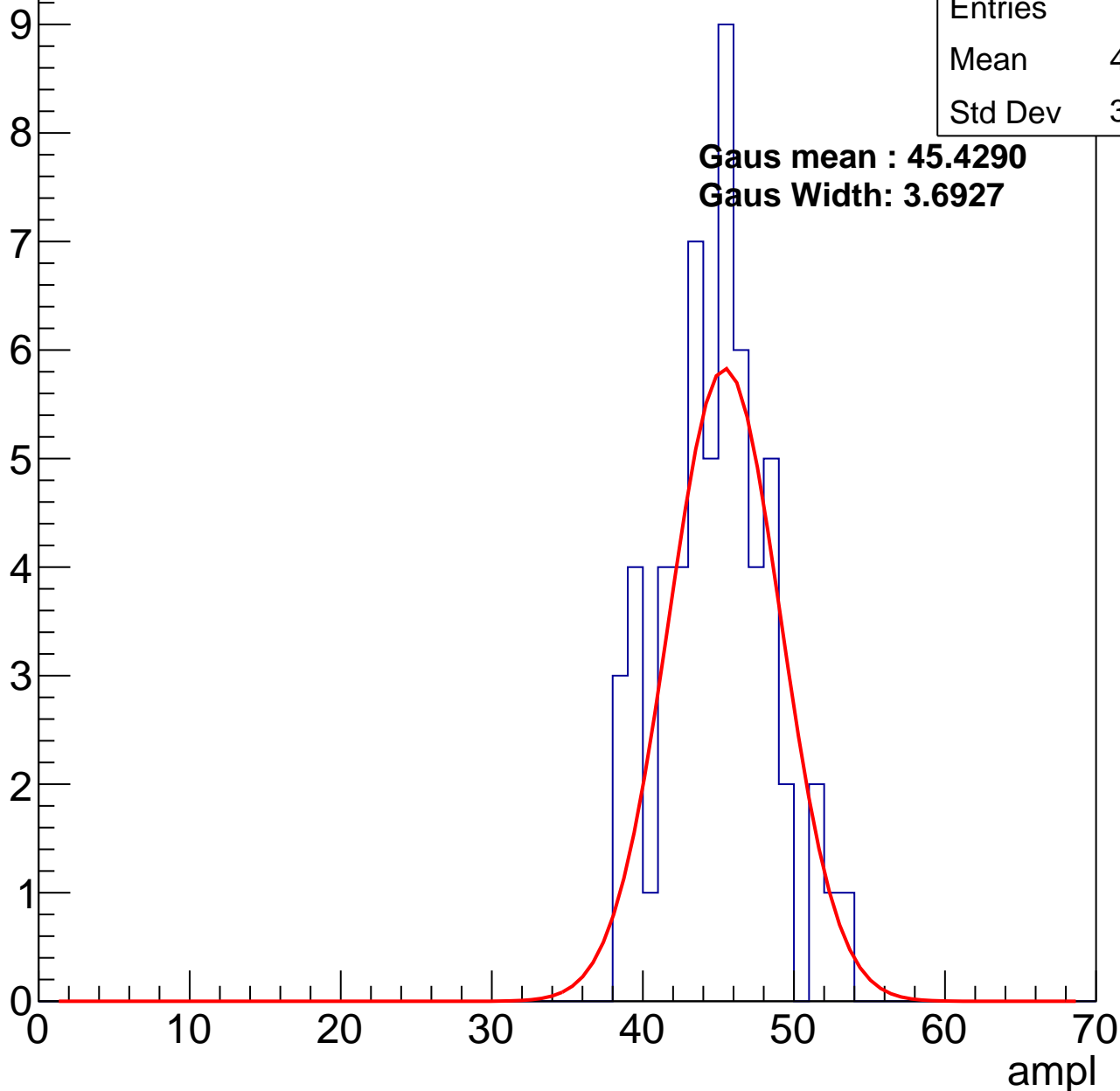
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.43
Std Dev	3.509

Gaus mean : 45.4290

Gaus Width: 3.6927

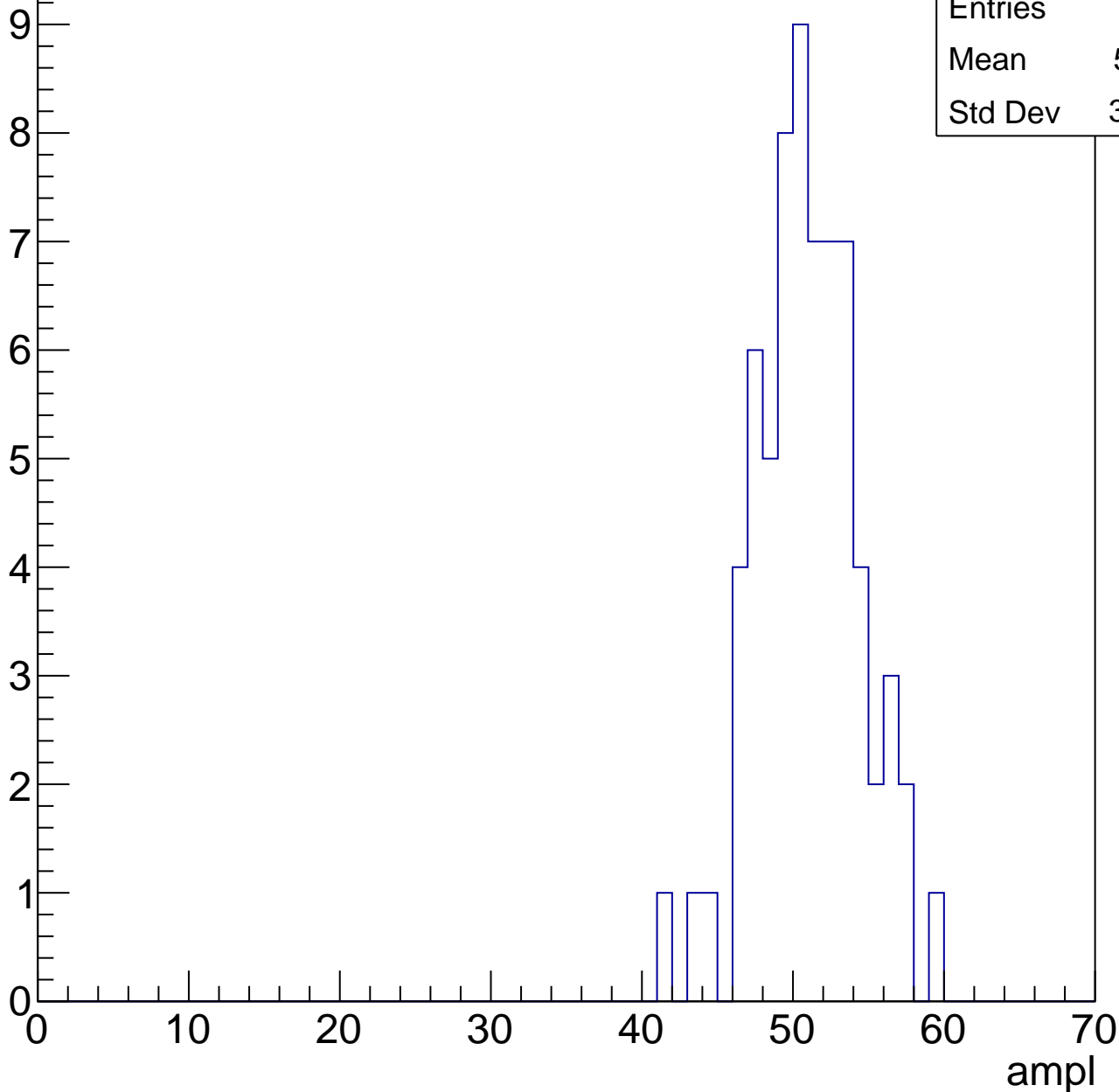


B1L101S, U25-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

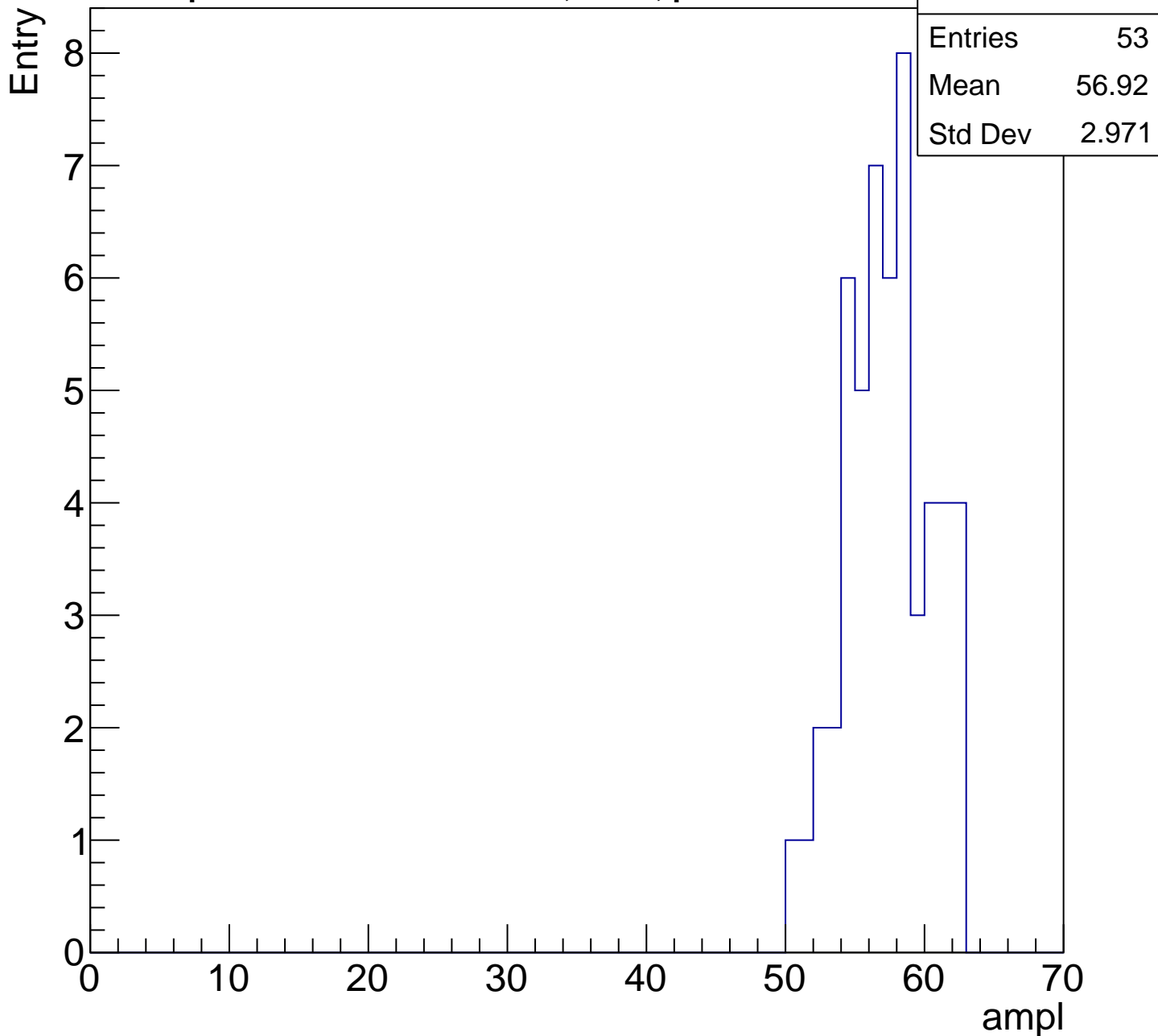
Entry

Entries	68
Mean	50.51
Std Dev	3.415



B1L101S, U25-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

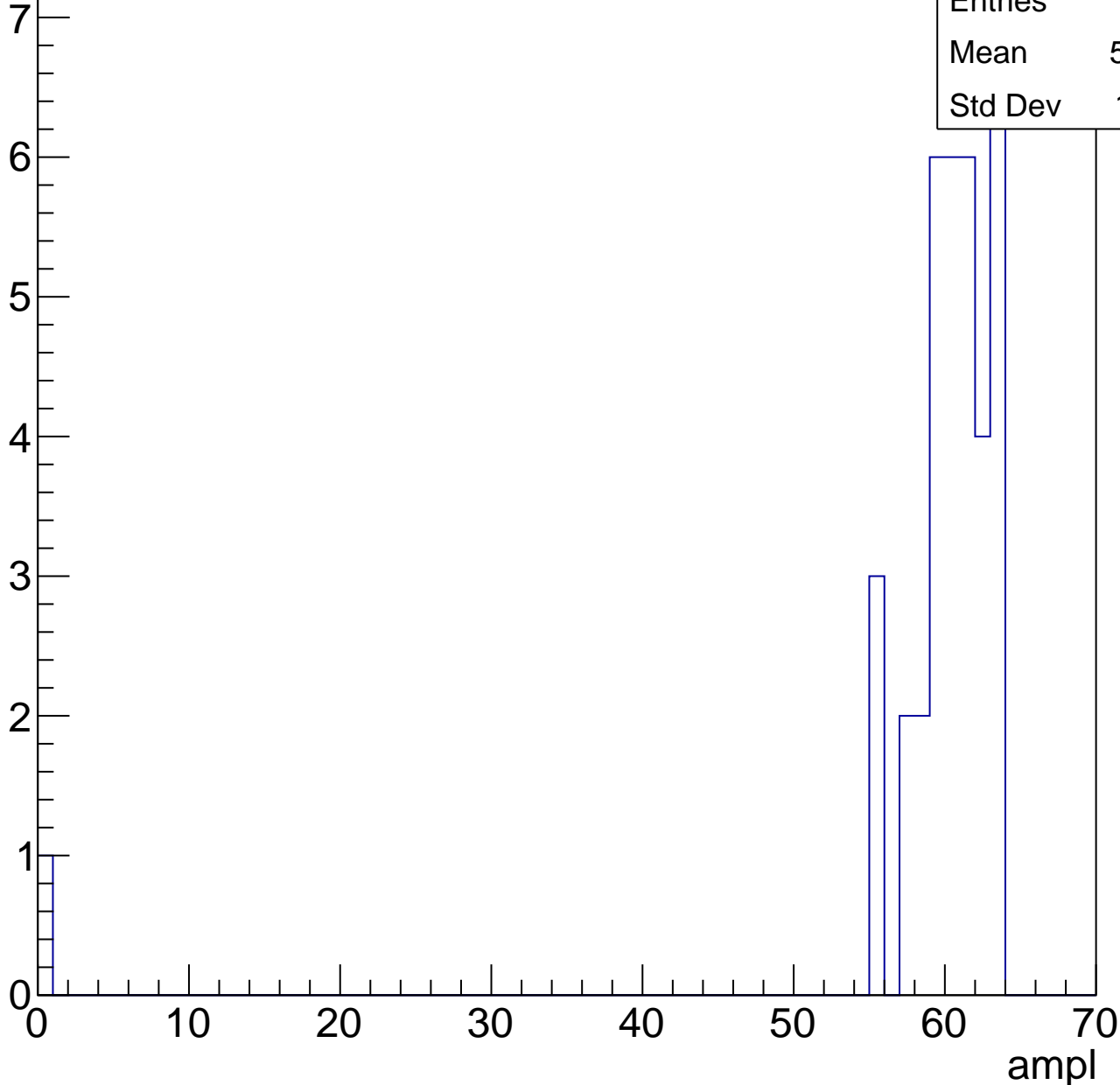


B1L101S, U25-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

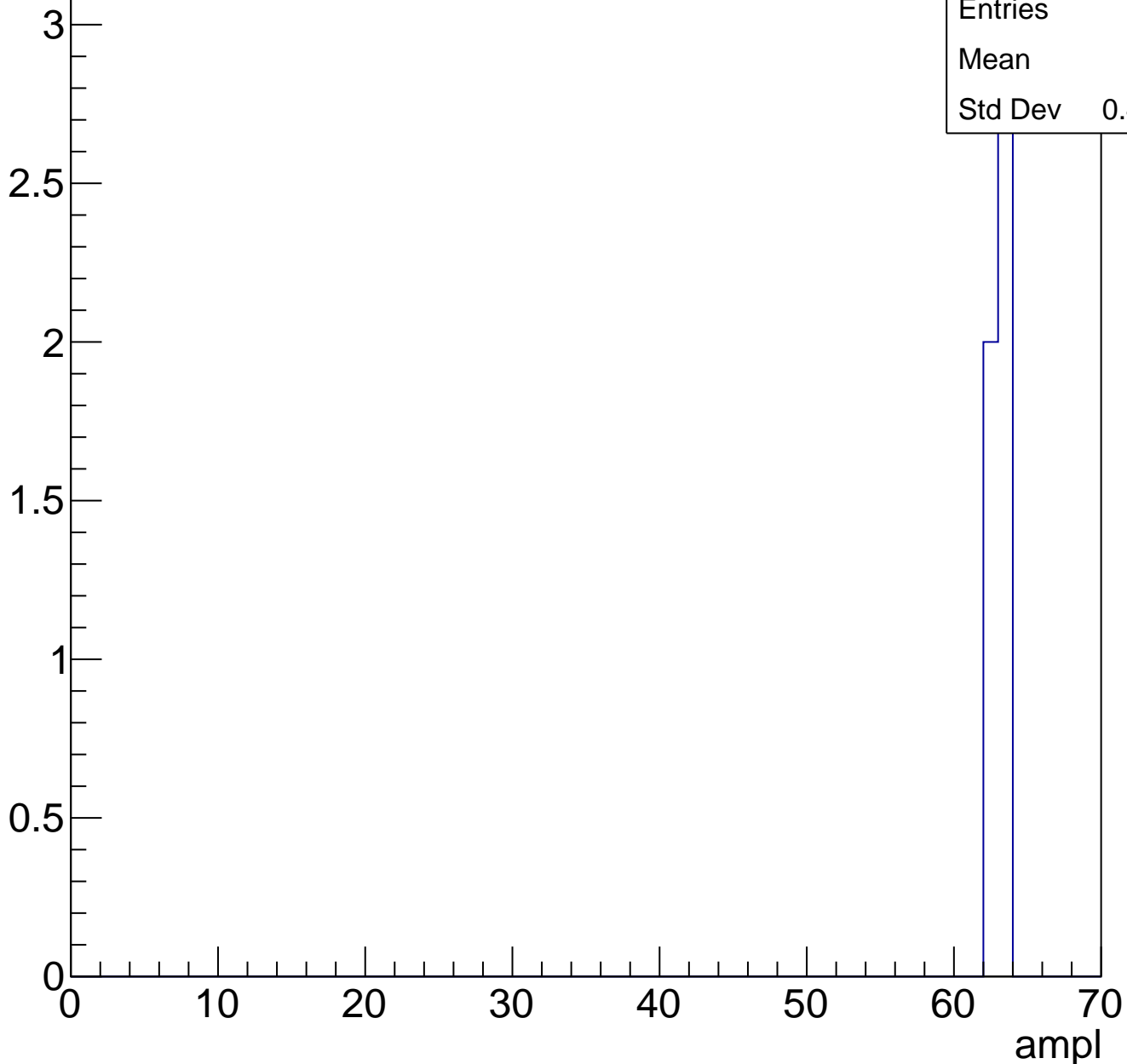
Entries	37
Mean	58.49
Std Dev	10.01



B1L101S, U25-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch10, adc0

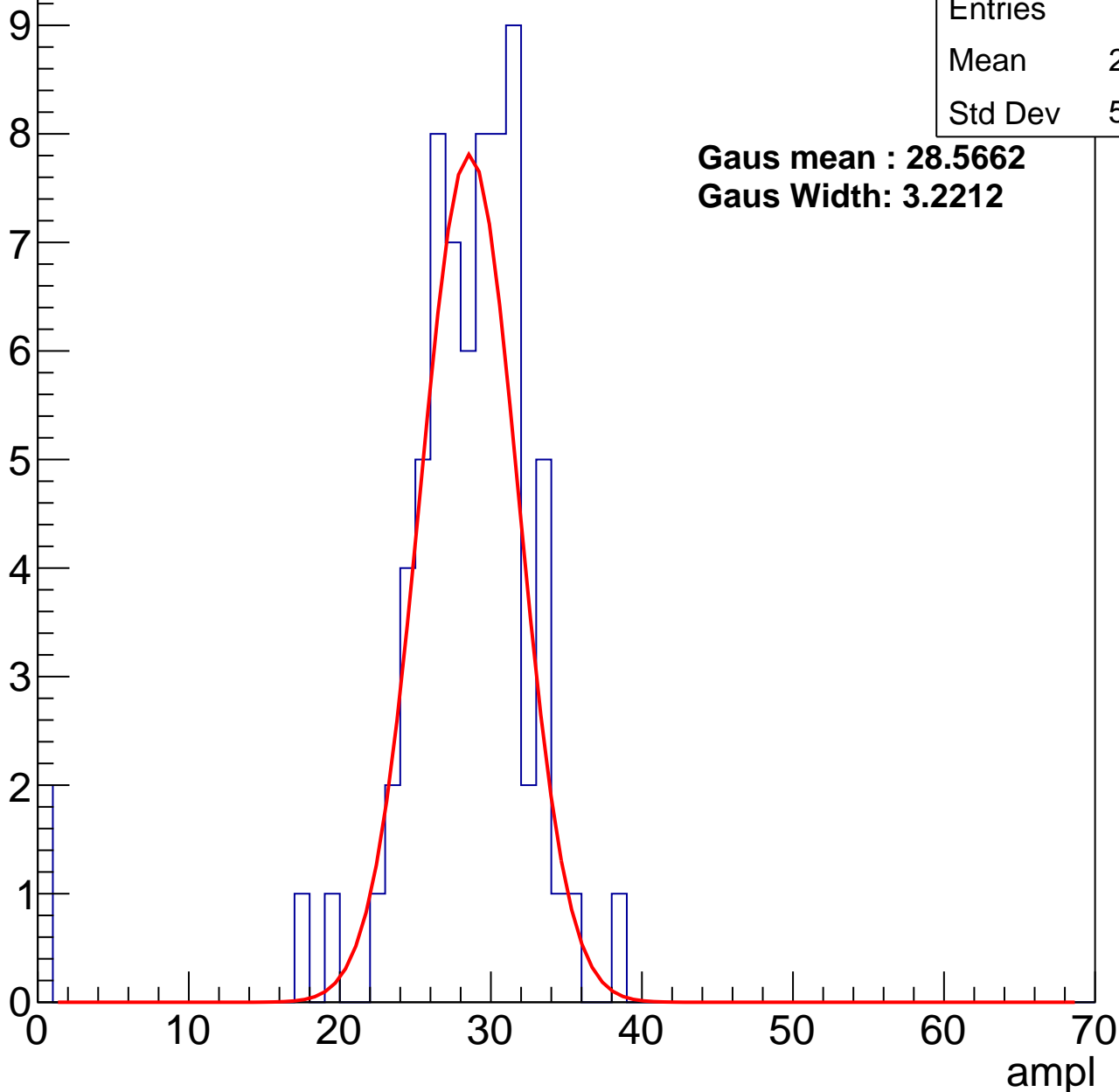
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.46
Std Dev	5.828

Gaus mean : 28.5662

Gaus Width: 3.2212



B1L101S, U25-ch10, adc1

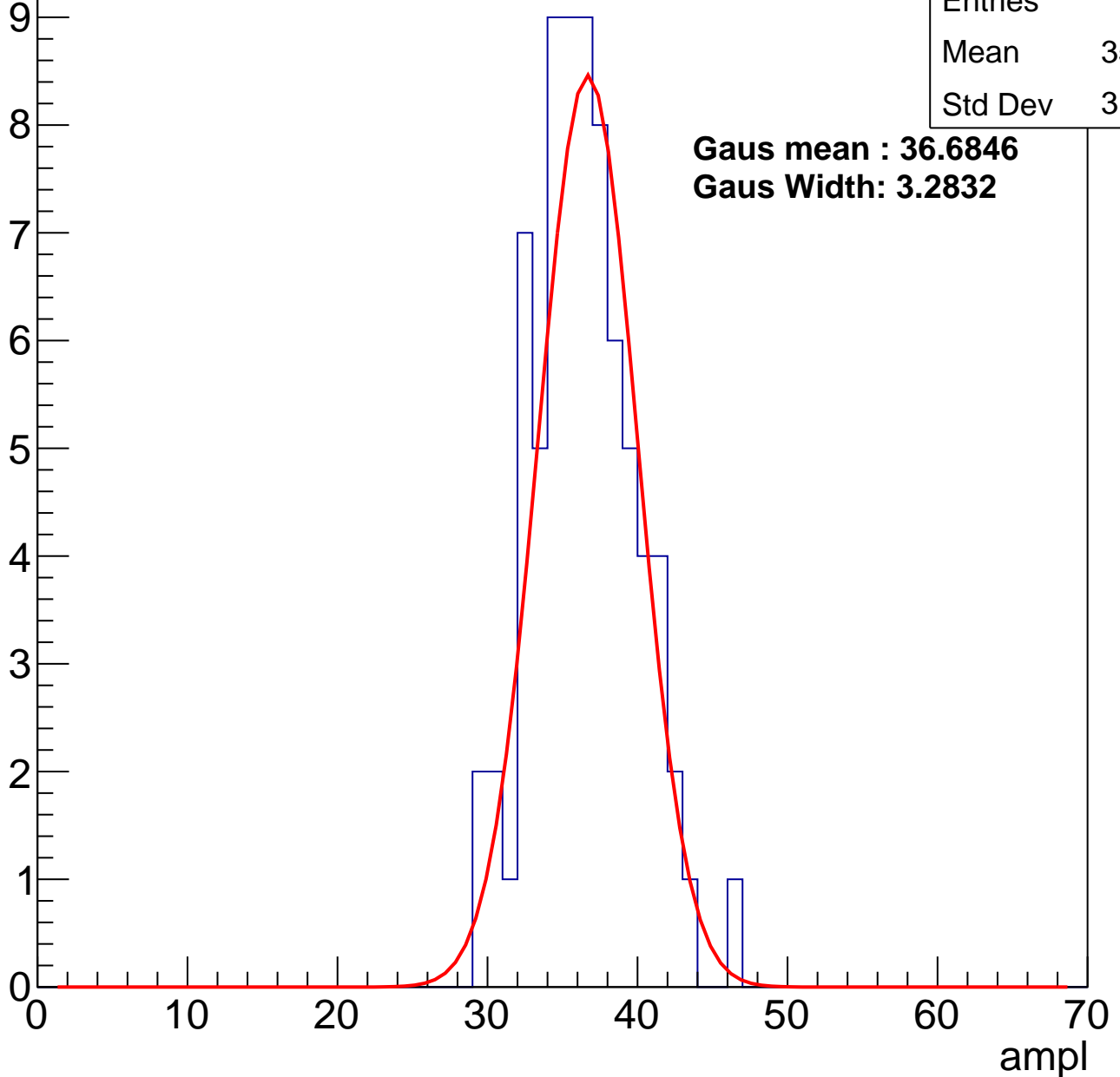
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	35.99
Std Dev	3.384

Gaus mean : 36.6846

Gaus Width: 3.2832



B1L101S, U25-ch10, adc2

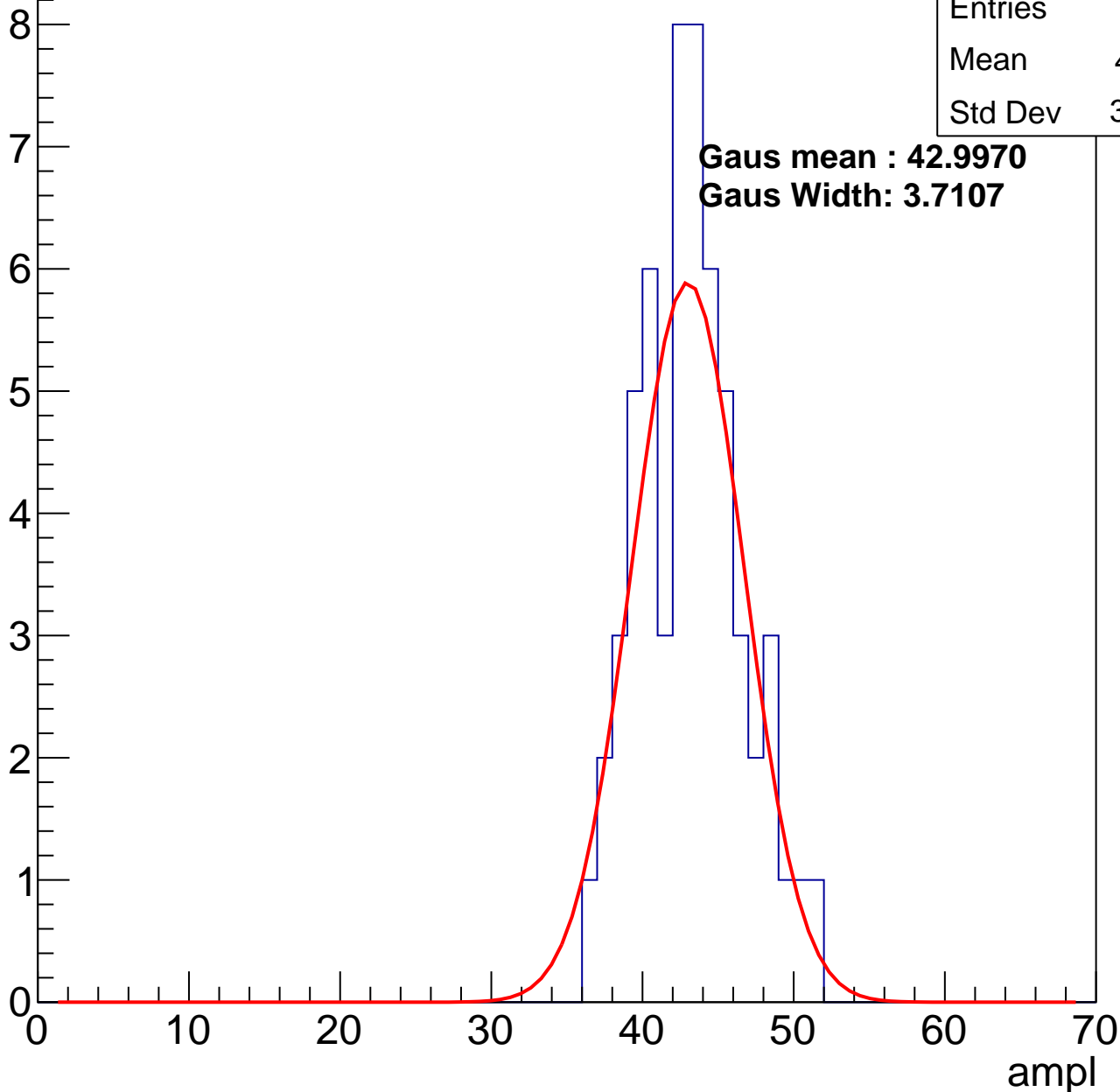
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.71
Std Dev	3.358

Gaus mean : 42.9970

Gaus Width: 3.7107

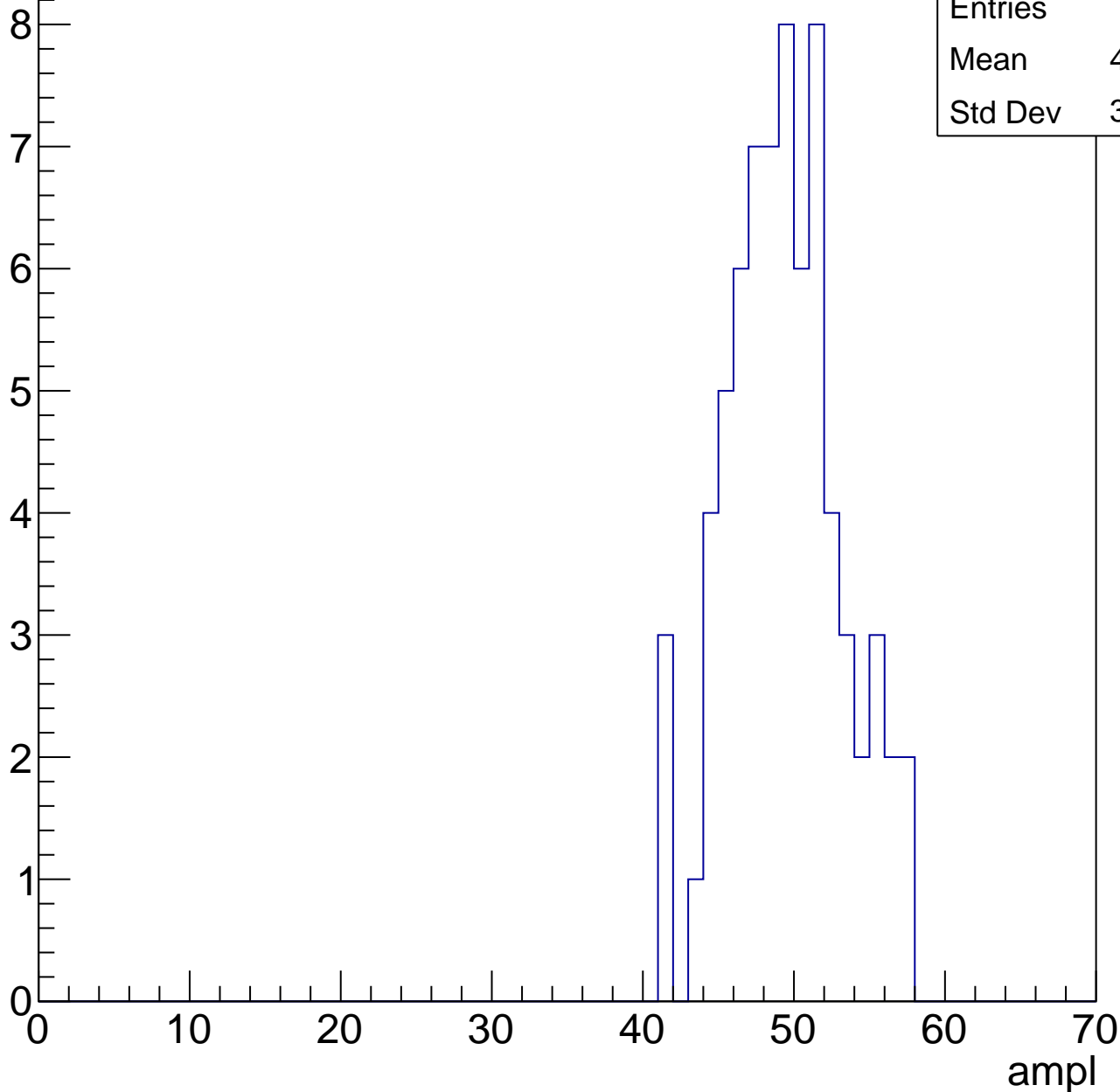


B1L101S, U25-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	48.93
Std Dev	3.758

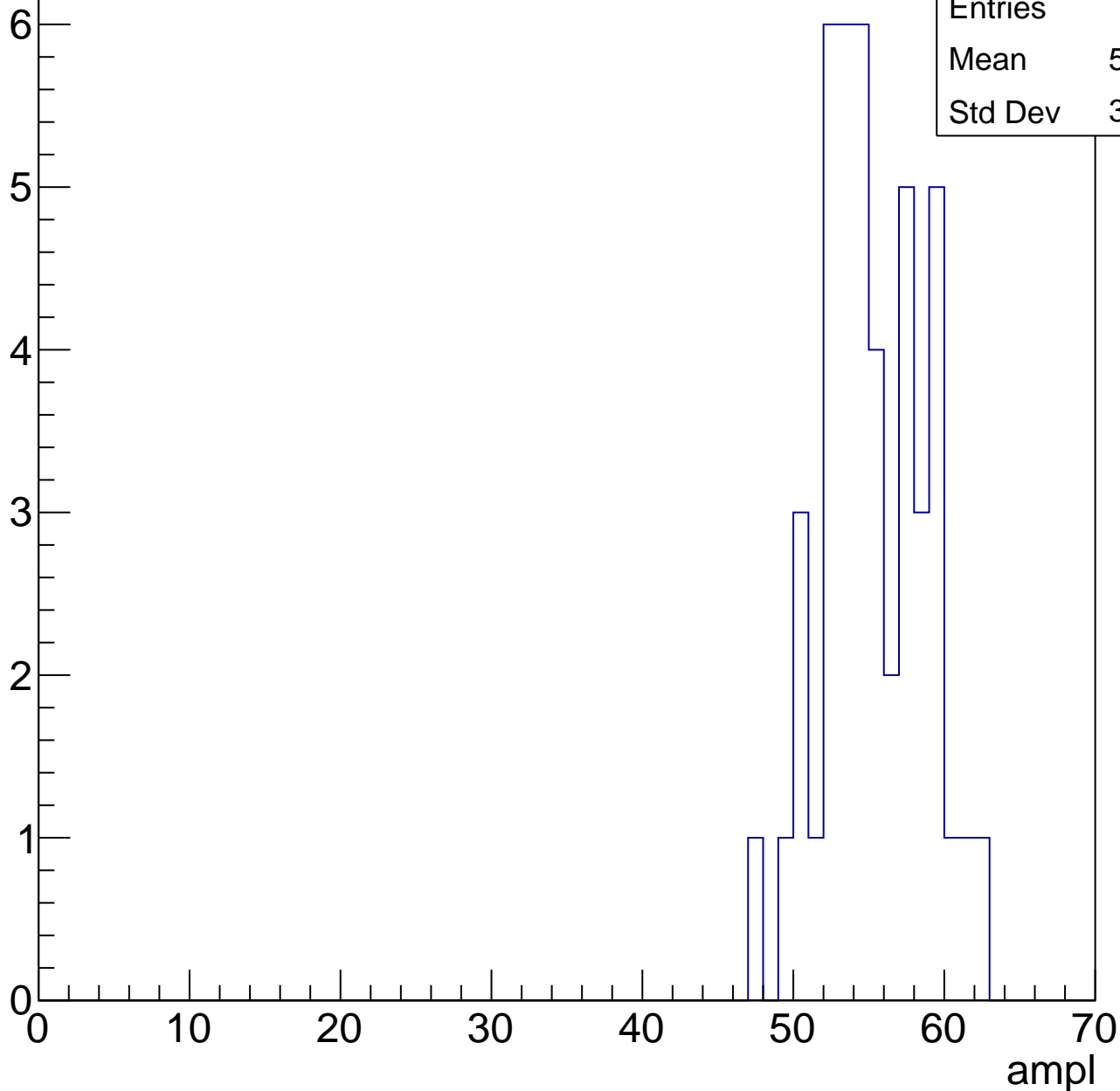


B1L101S, U25-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

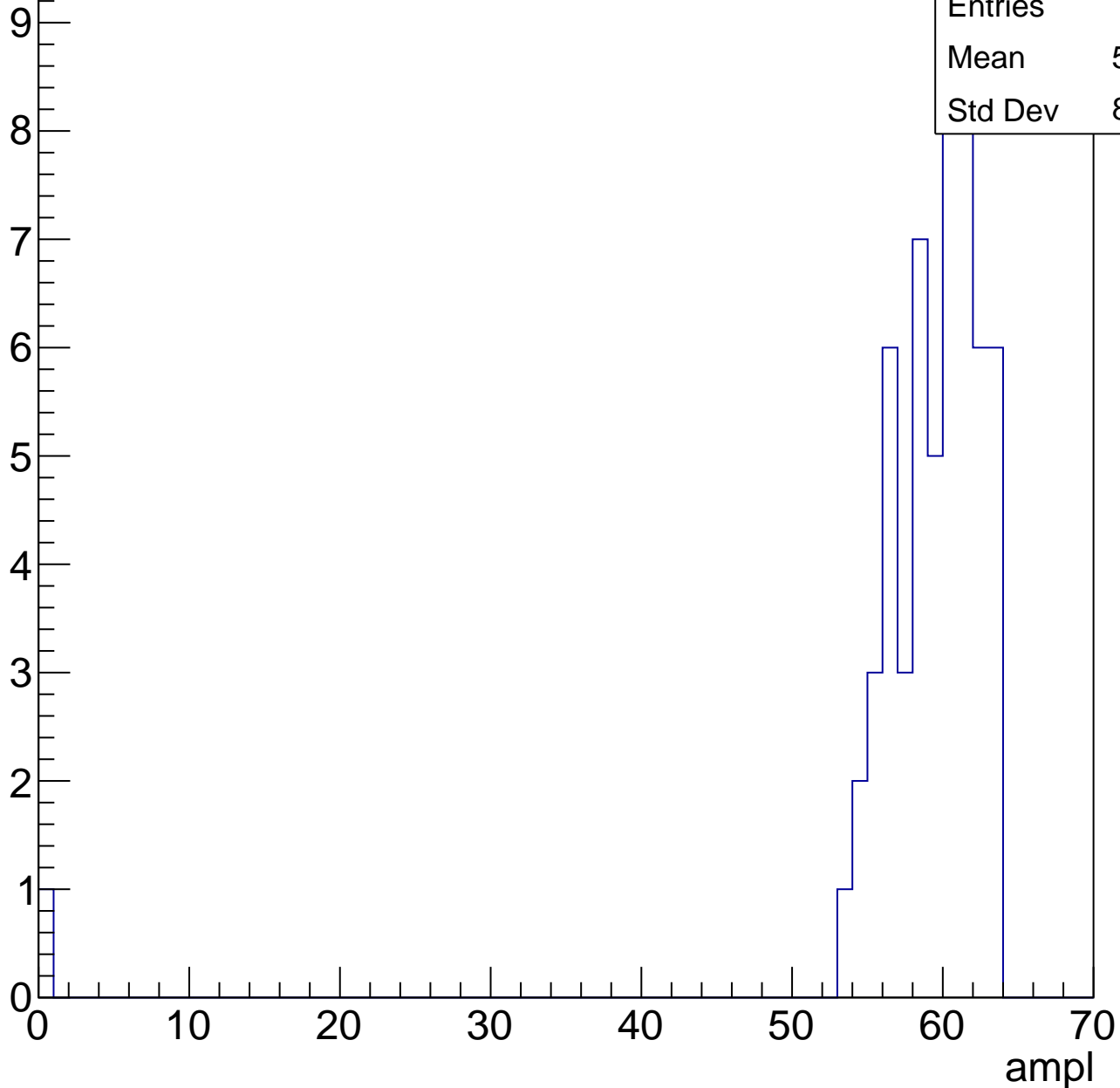
Entries	46
Mean	54.78
Std Dev	3.336



B1L101S, U25-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch11, adc0

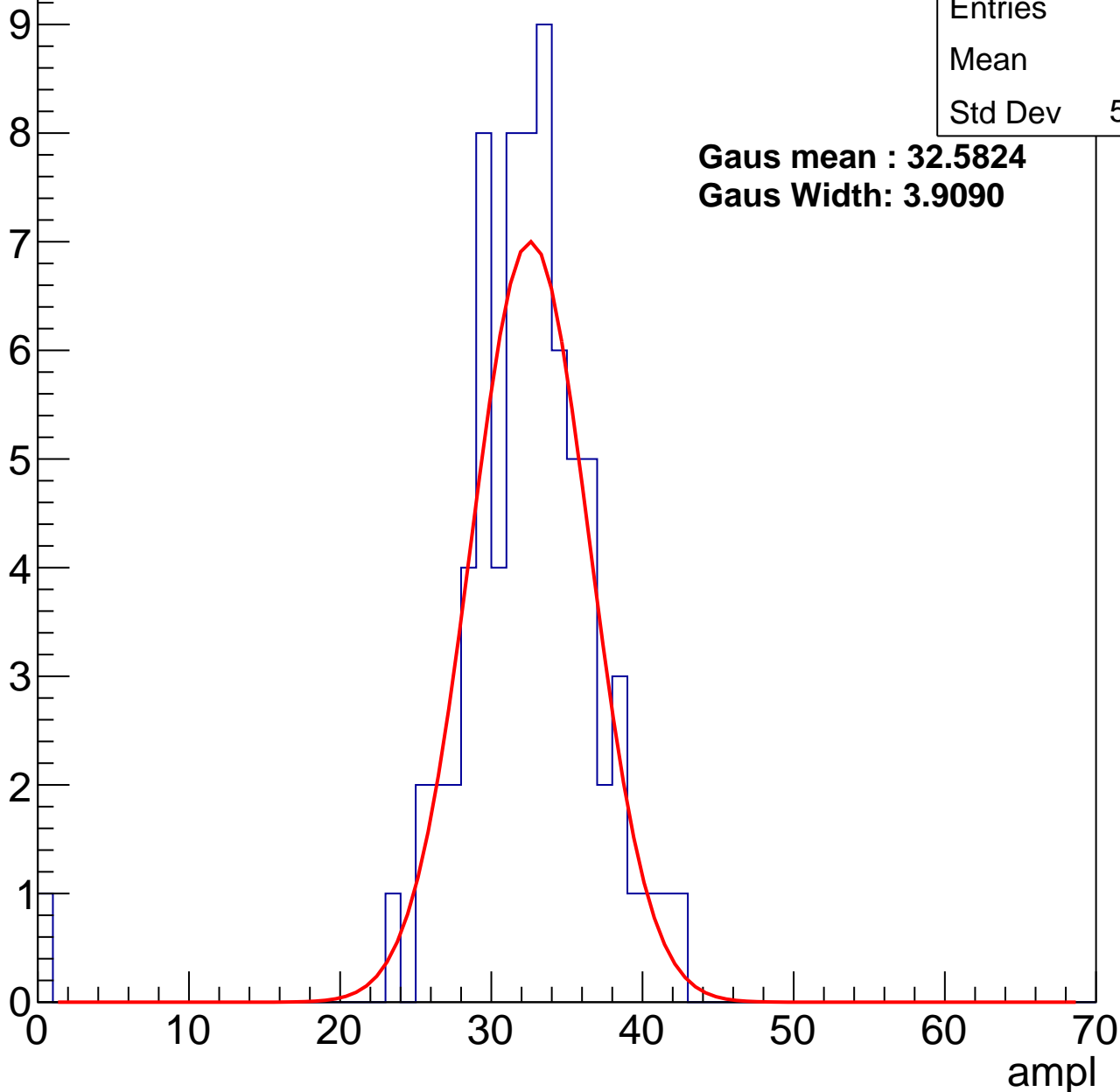
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	31.8
Std Dev	5.317

Gaus mean : 32.5824

Gaus Width: 3.9090



B1L101S, U25-ch11, adc1

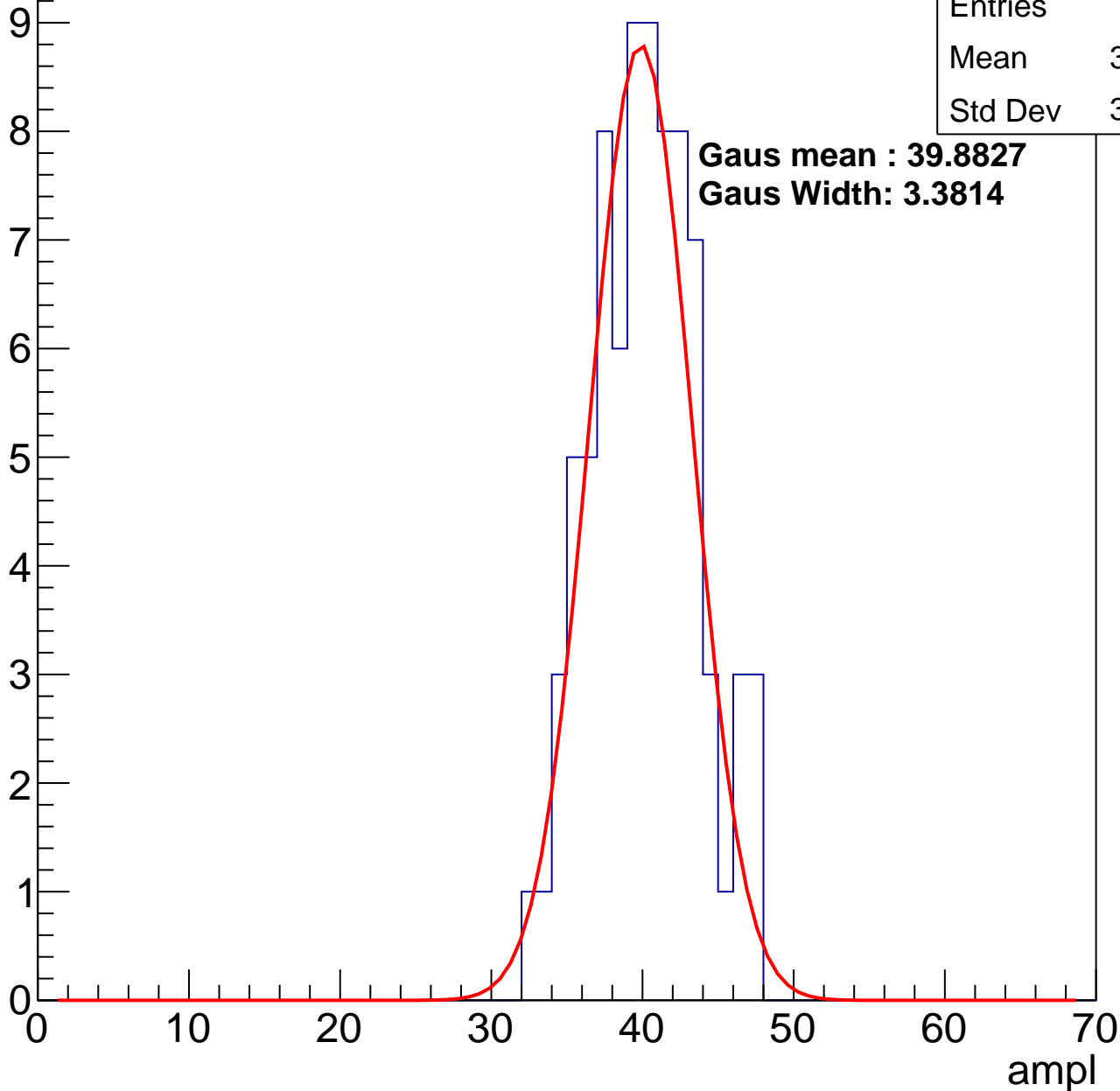
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	39.73
Std Dev	3.457

Gaus mean : 39.8827

Gaus Width: 3.3814



B1L101S, U25-ch11, adc2

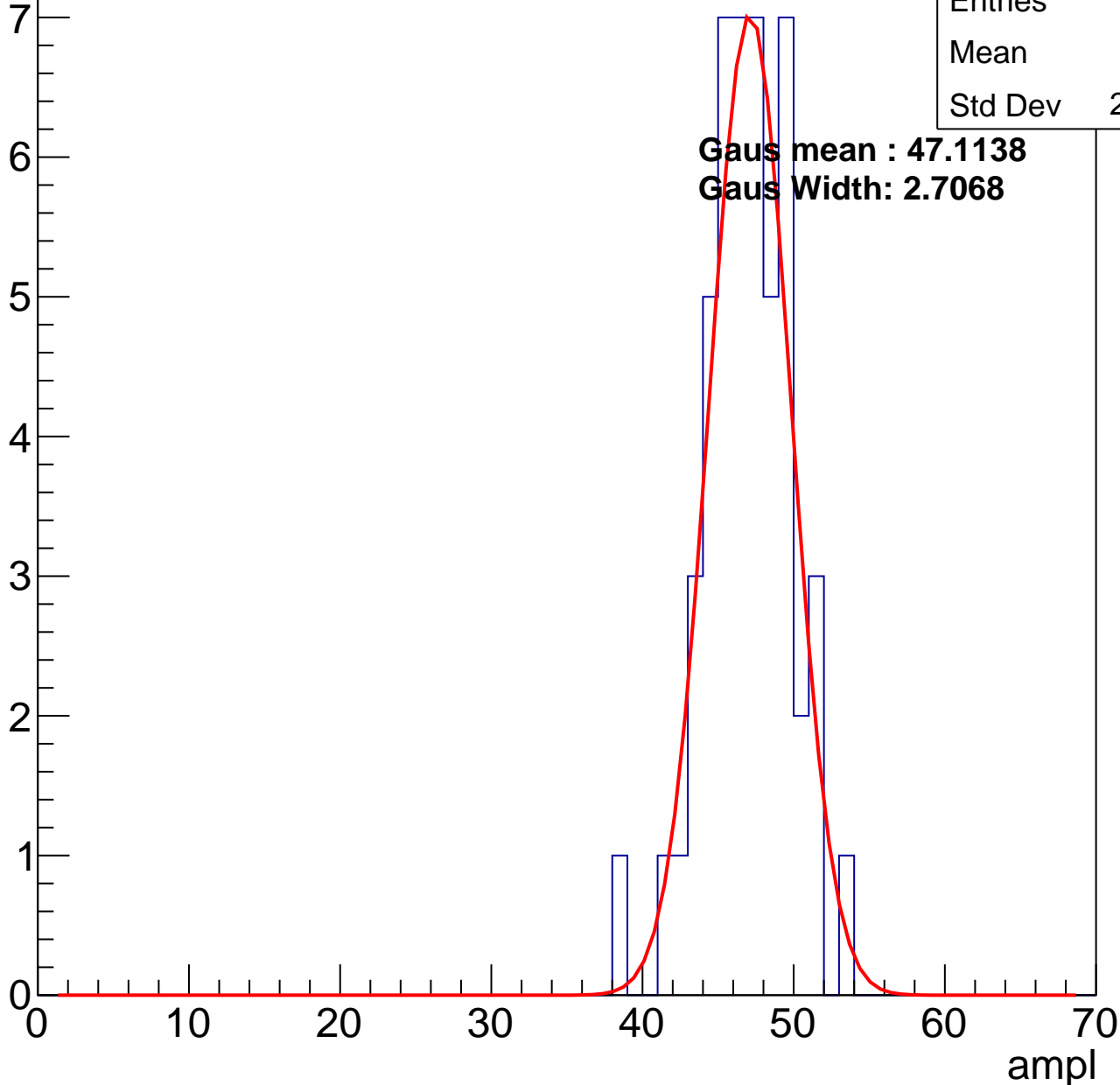
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	46.5
Std Dev	2.802

Gaus mean : 47.1138

Gaus Width: 2.7068



B1L101S, U25-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	52.36
Std Dev	3.43

Entry

10

8

6

4

2

0

0

10

20

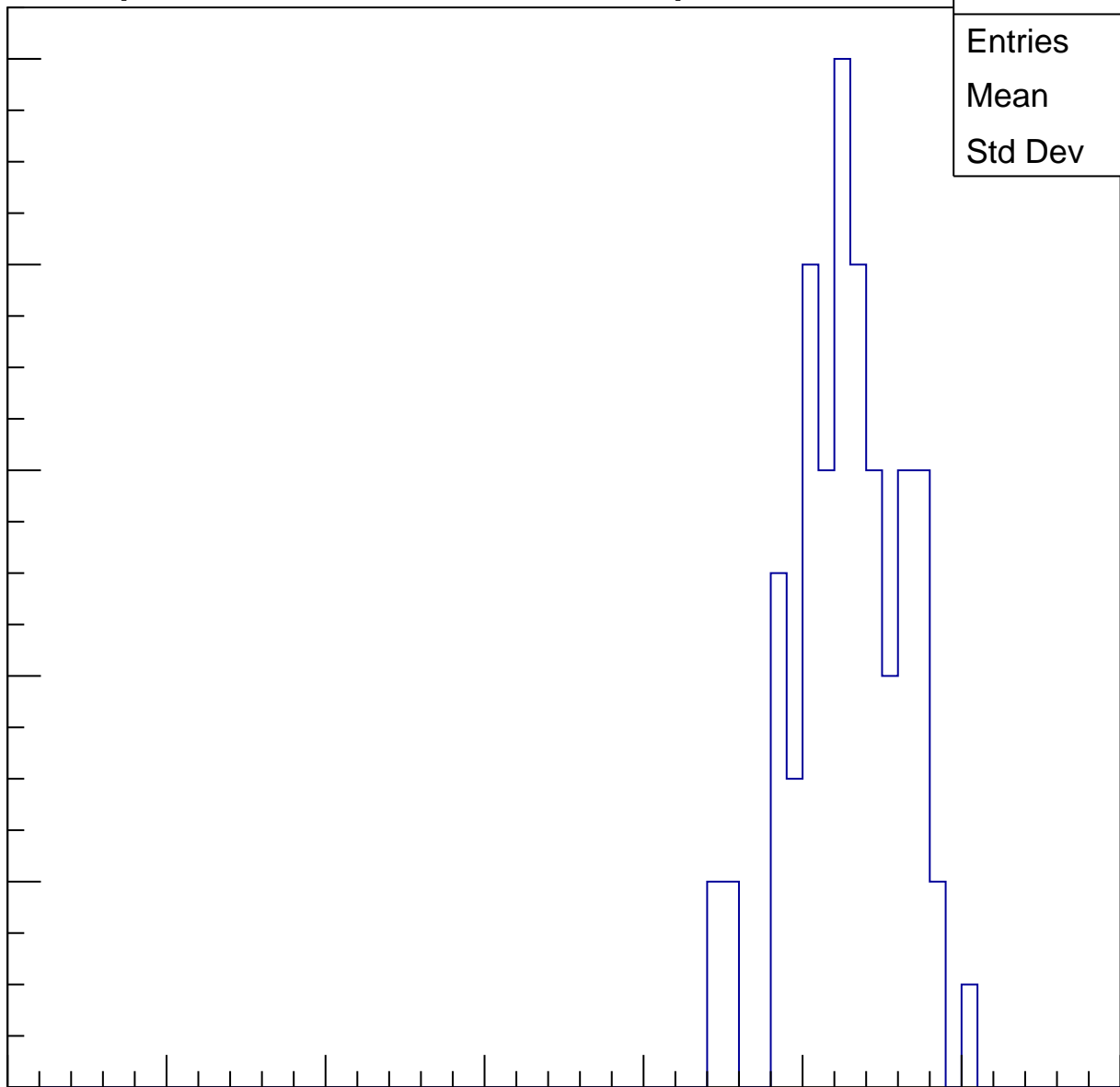
30

40

50

60

ampl

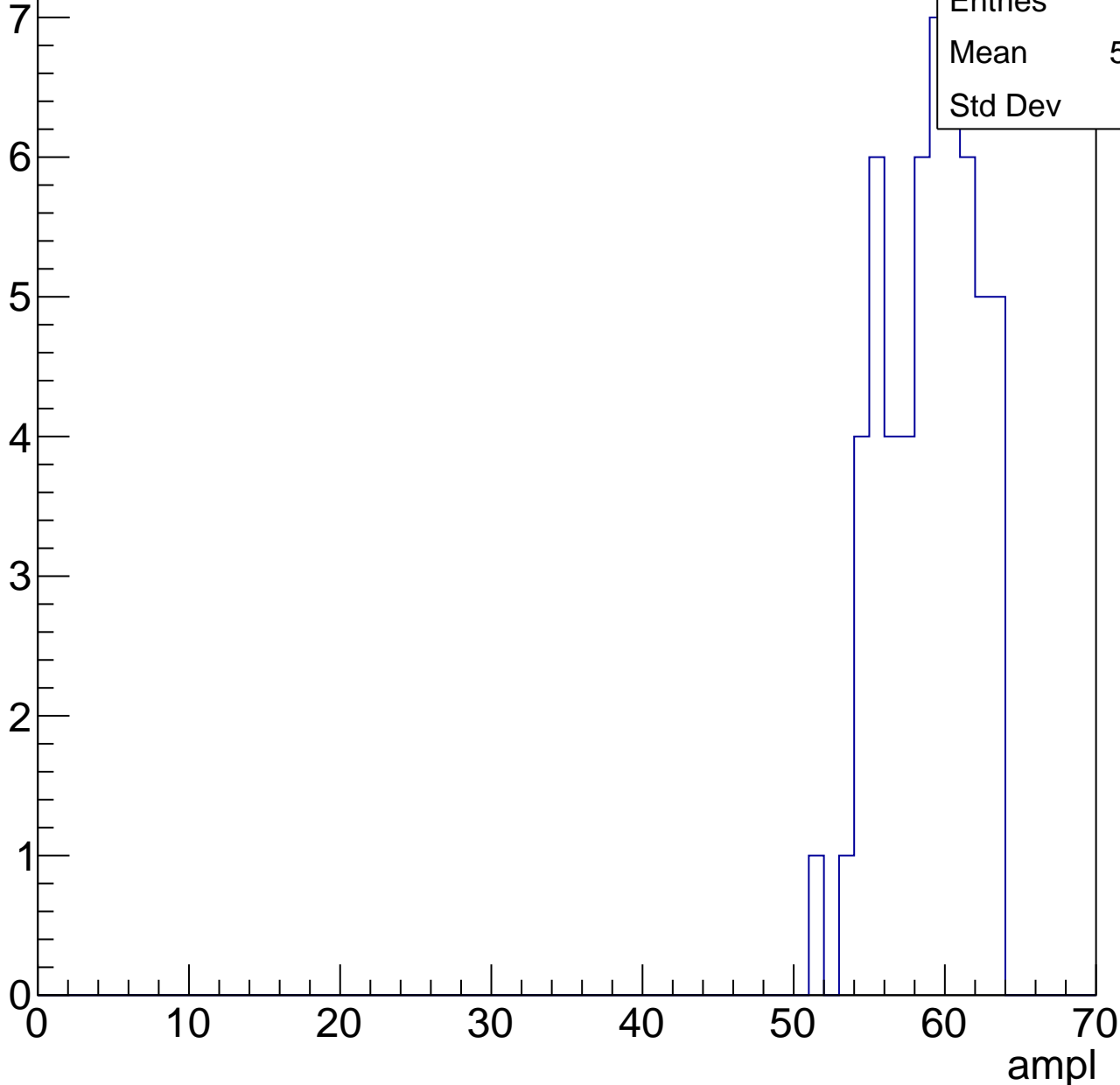


B1L101S, U25-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	58.46
Std Dev	2.97

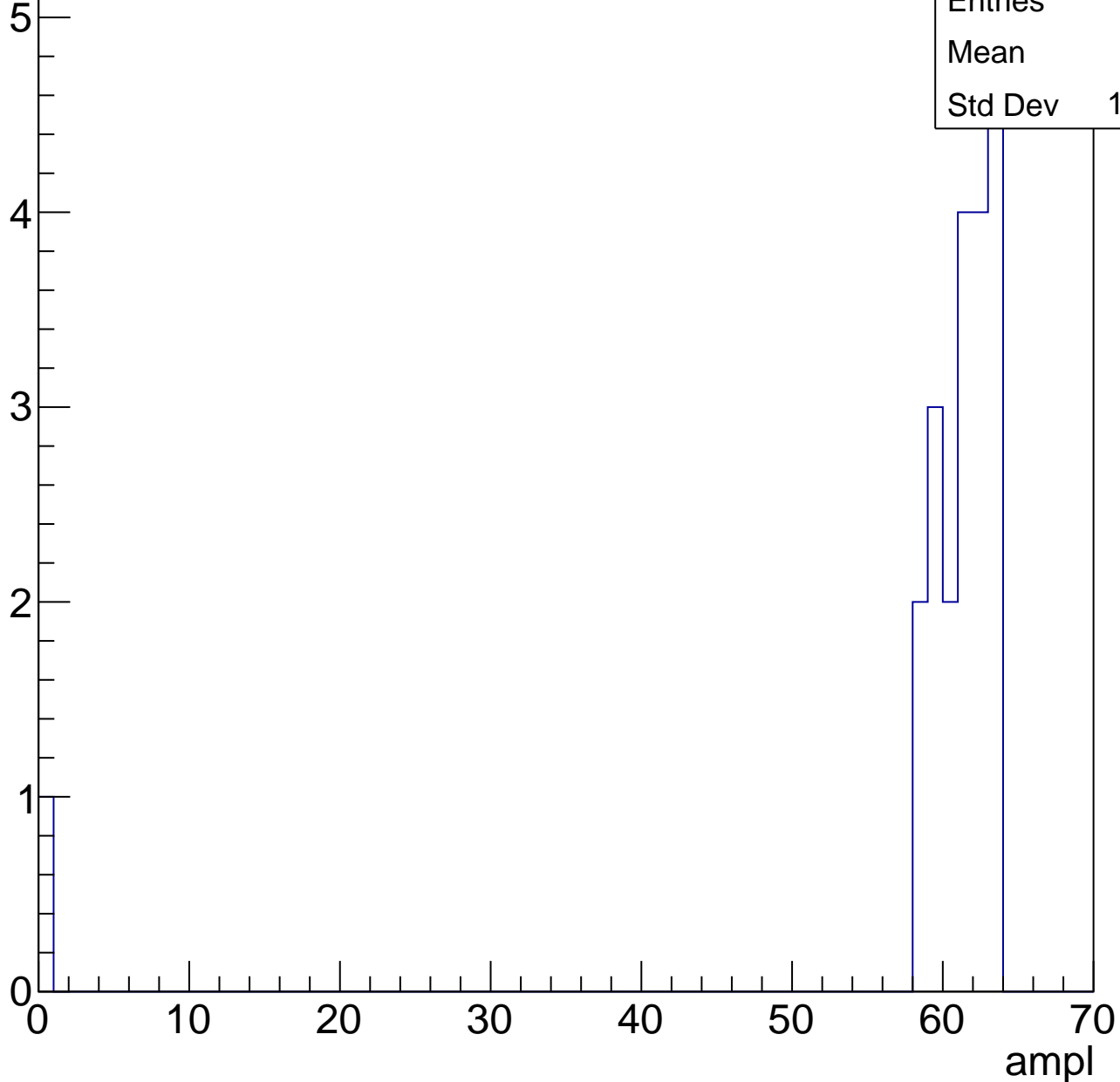


B1L101S, U25-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.1
Std Dev	13.09



B1L101S, U25-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch12, adc0

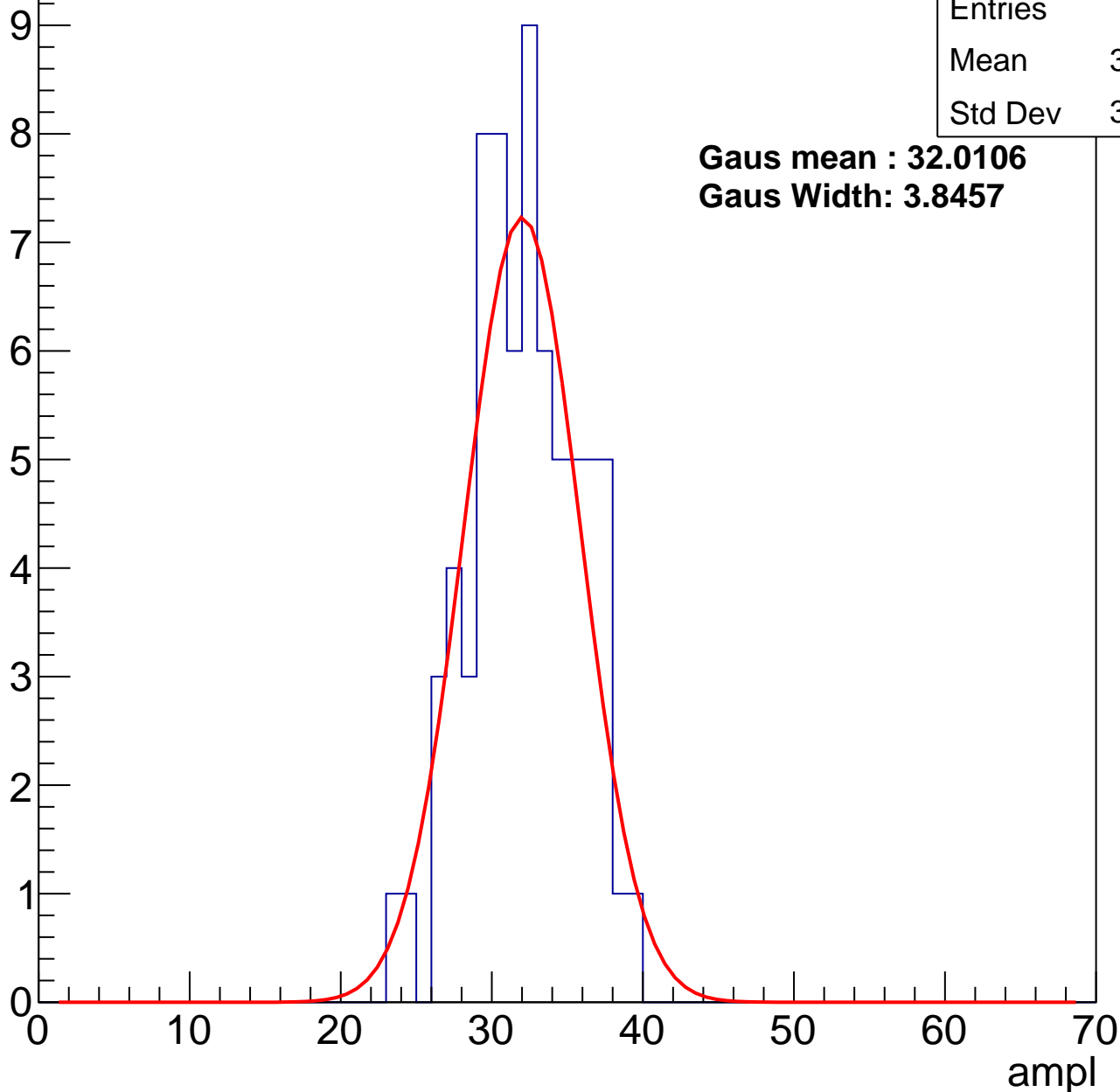
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	31.66
Std Dev	3.488

Gaus mean : 32.0106

Gaus Width: 3.8457



B1L101S, U25-ch12, adc1

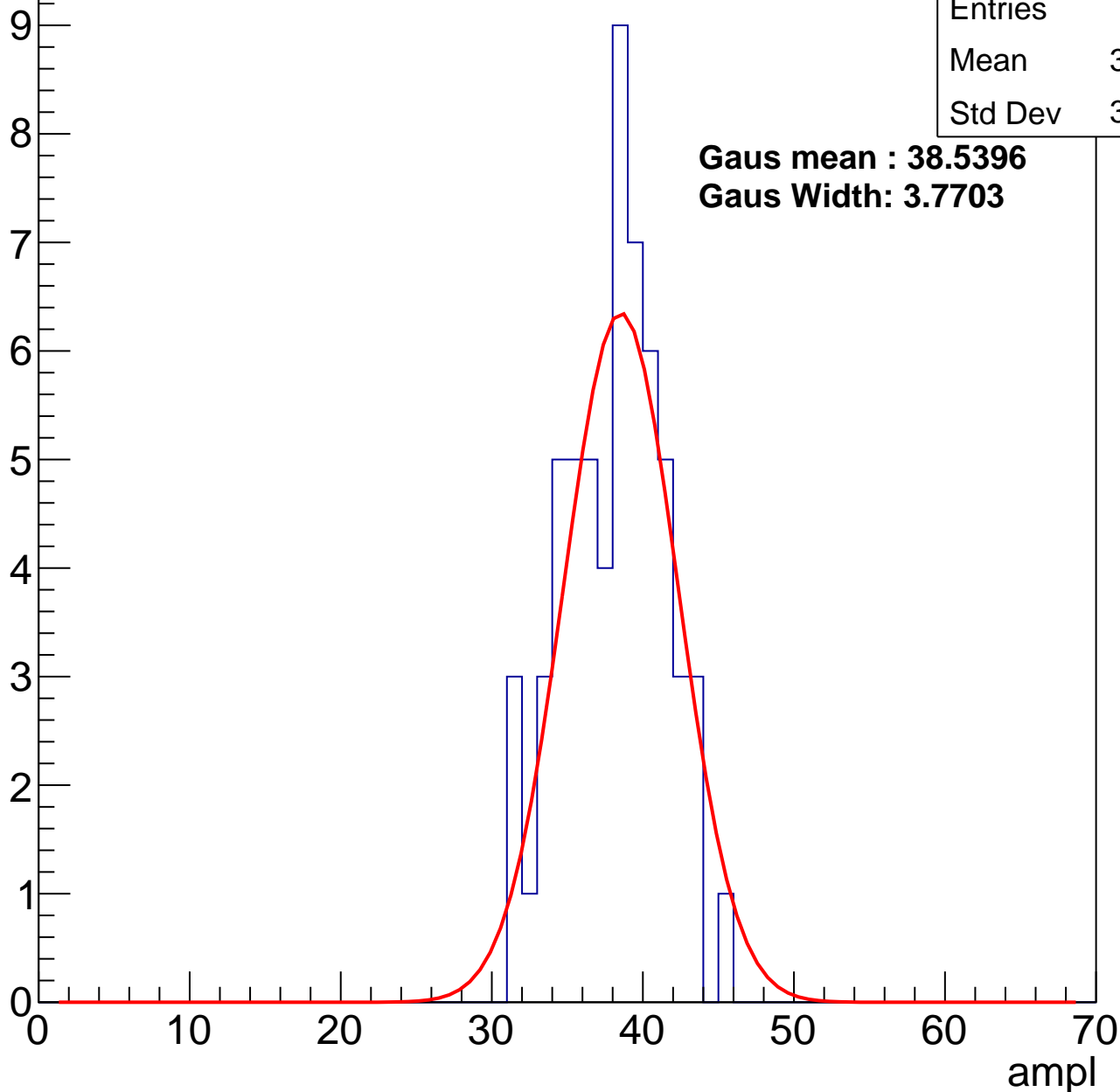
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.62
Std Dev	3.282

Gaus mean : 38.5396

Gaus Width: 3.7703



B1L101S, U25-ch12, adc2

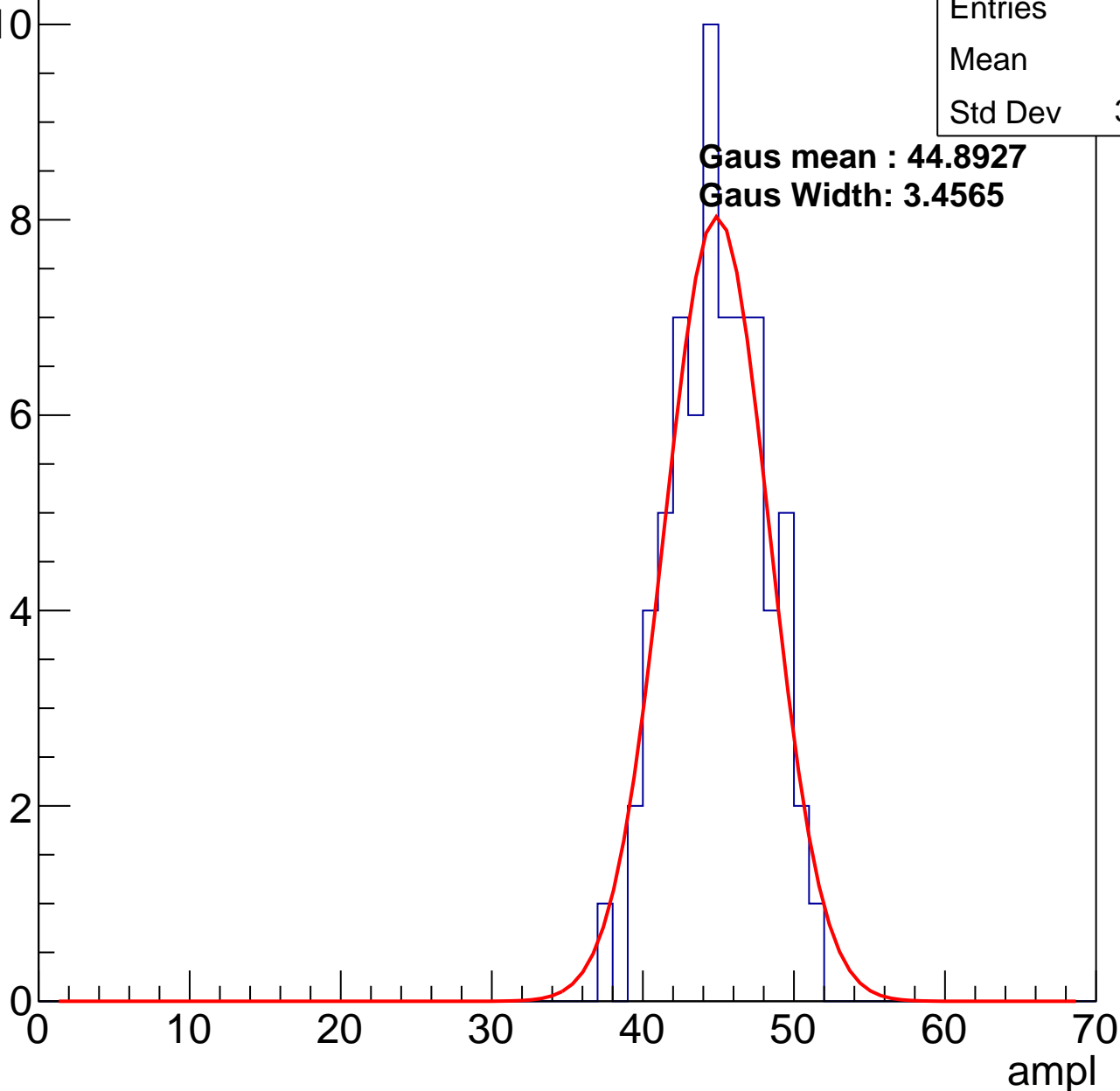
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.5
Std Dev	3.051

Gaus mean : 44.8927

Gaus Width: 3.4565

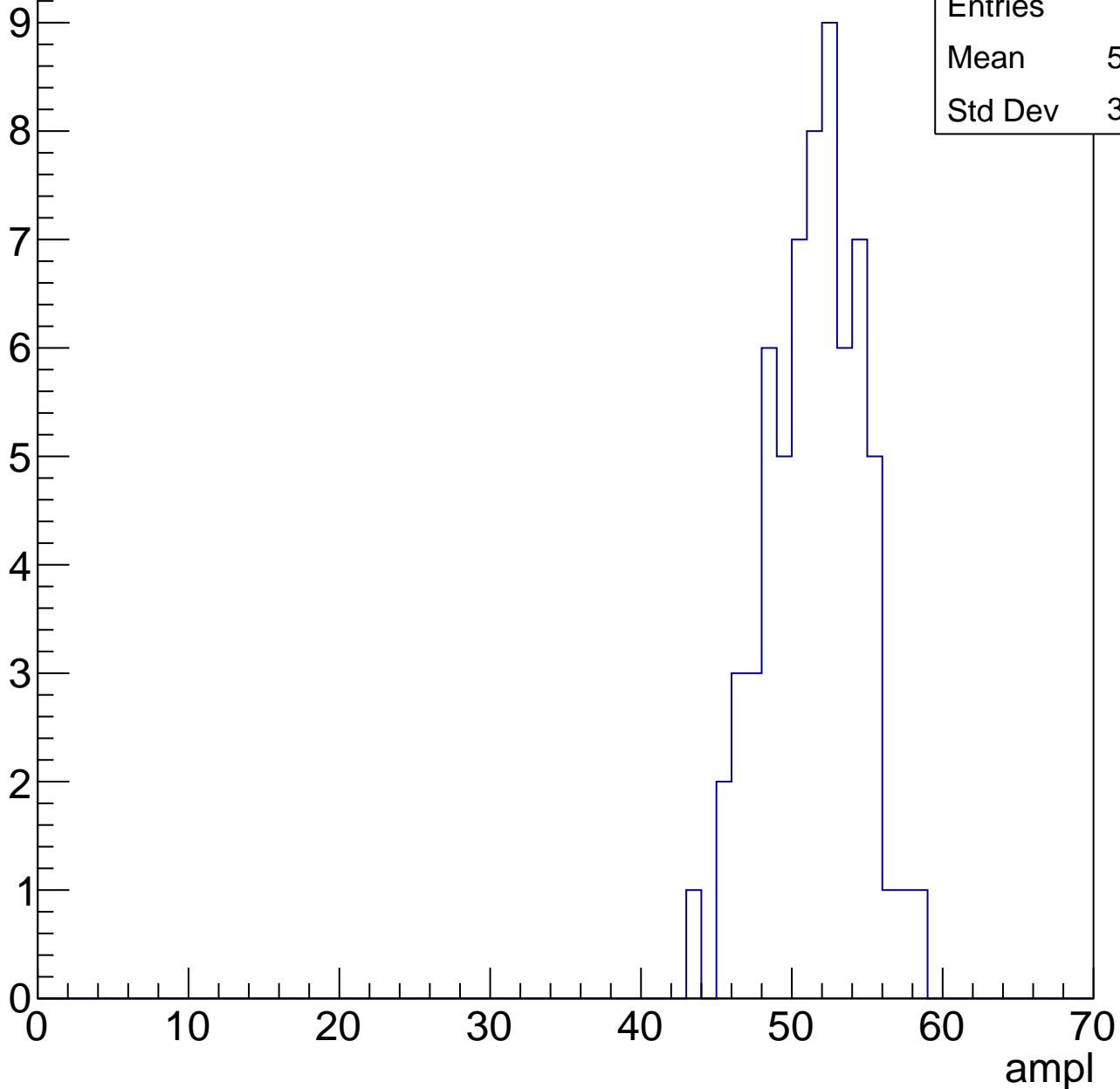


B1L101S, U25-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	50.97
Std Dev	3.103

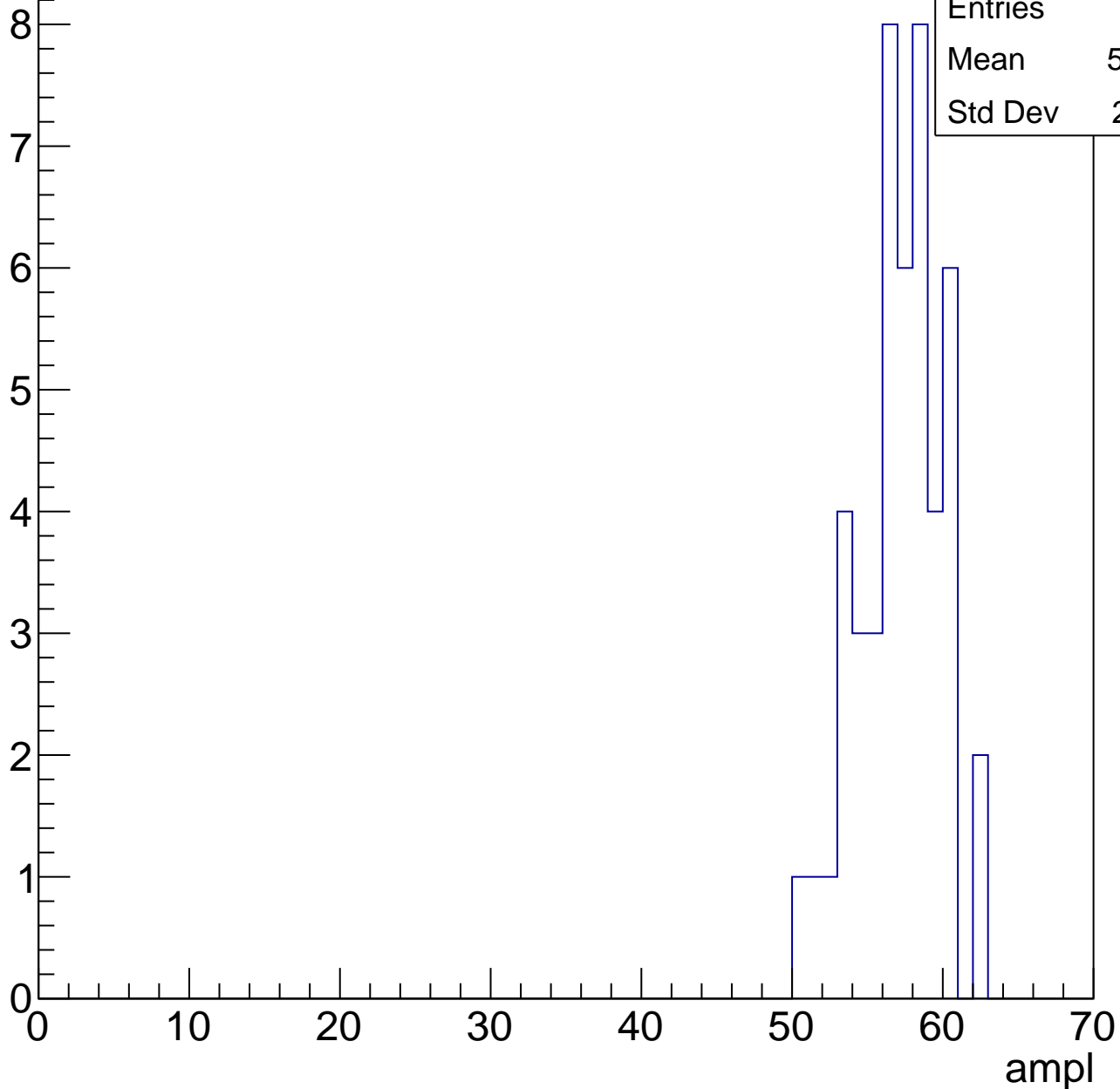


B1L101S, U25-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	56.72
Std Dev	2.711

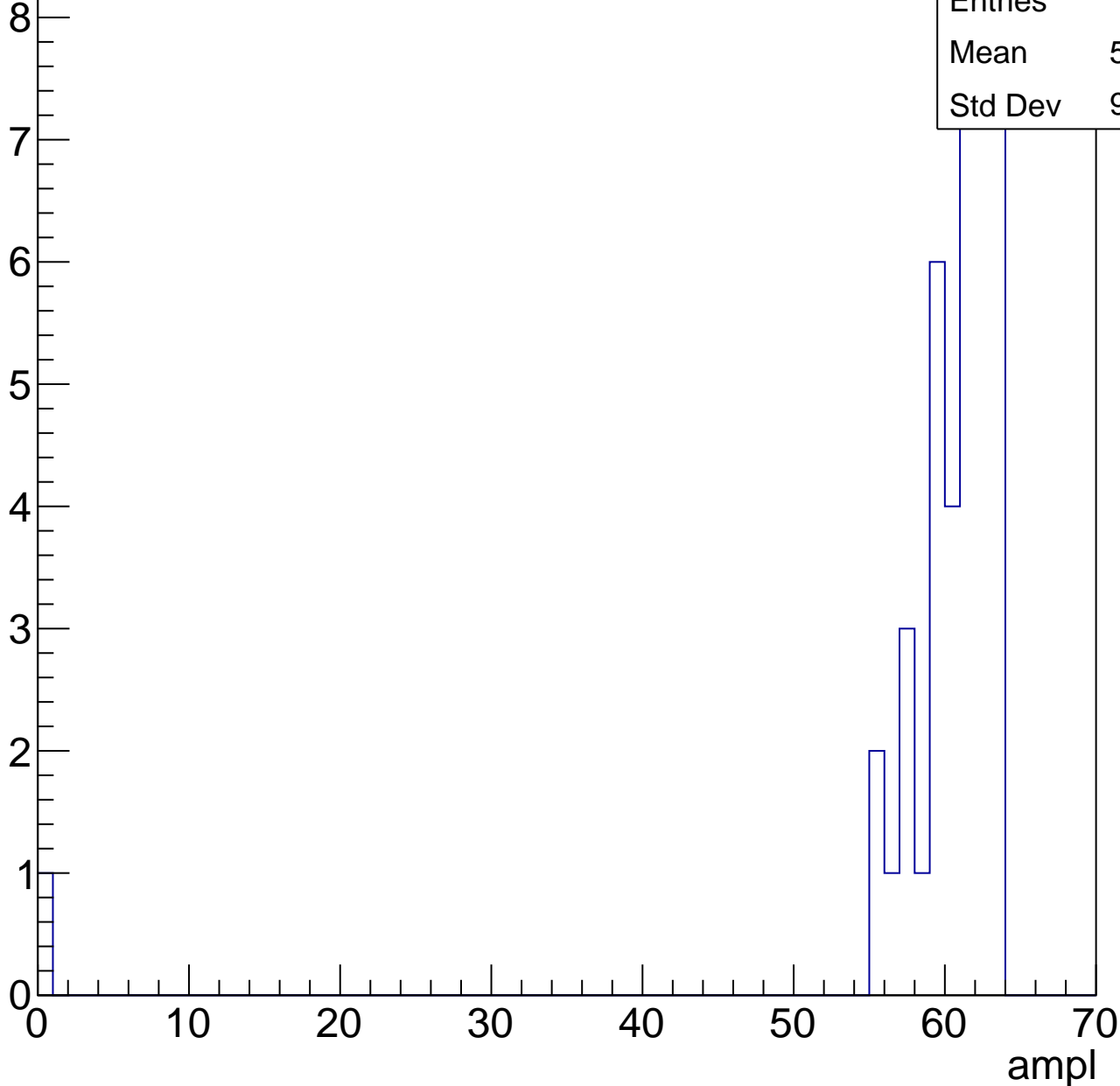


B1L101S, U25-ch12, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.98
Std Dev	9.476



B1L101S, U25-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B1L101S, U25-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch13, adc0

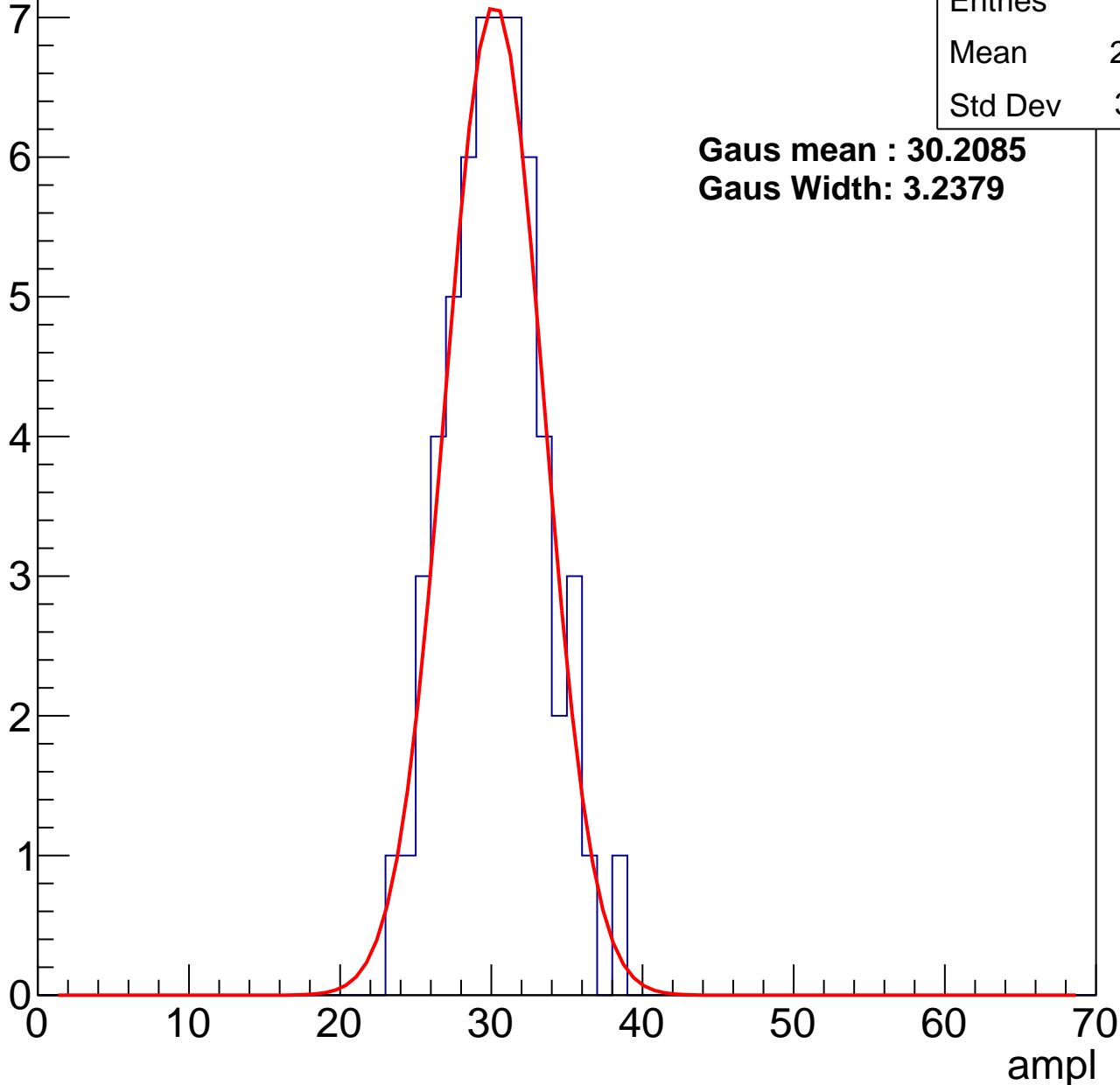
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.83
Std Dev	3.141

Gaus mean : 30.2085

Gaus Width: 3.2379



B1L101S, U25-ch13, adc1

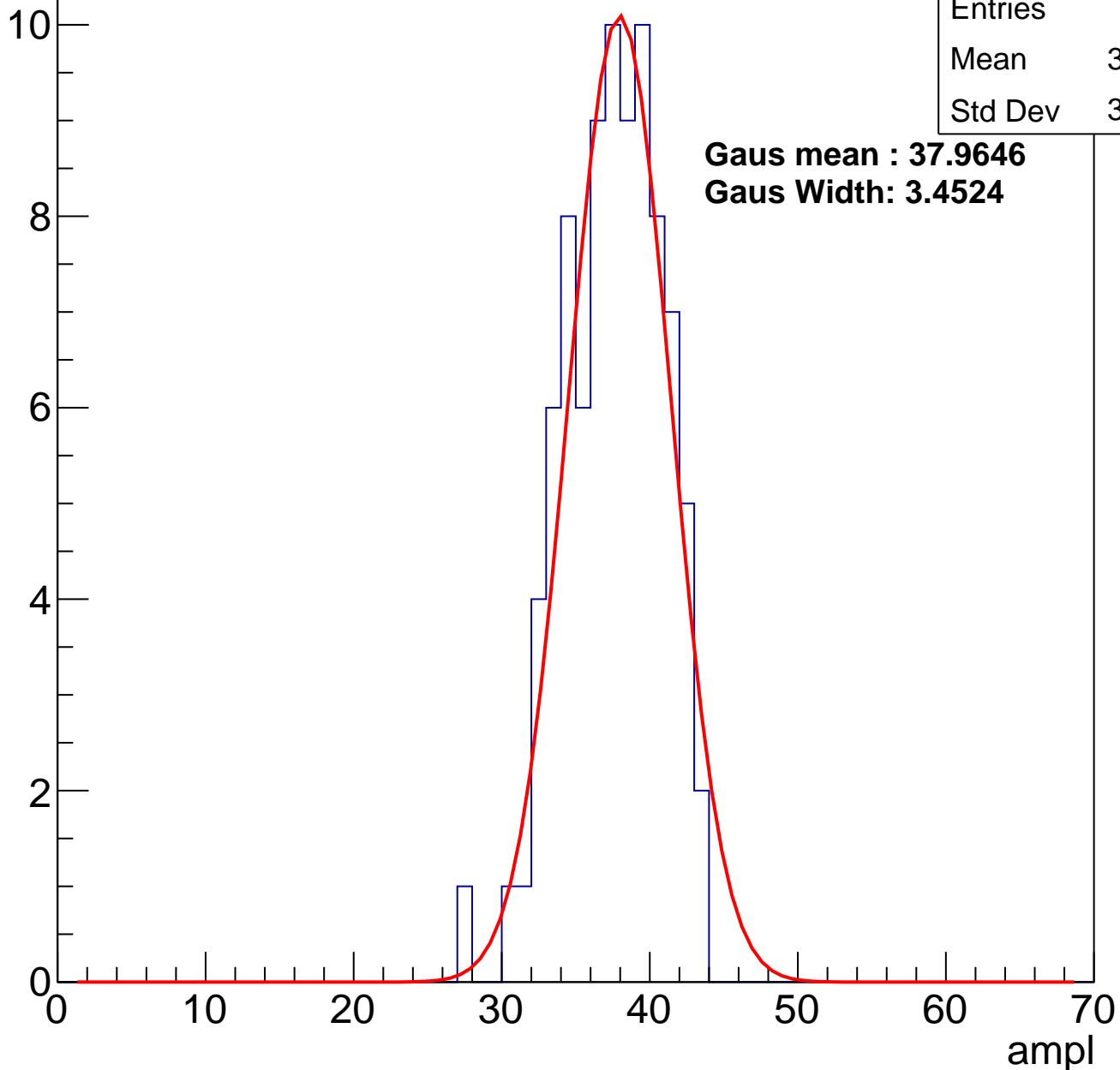
calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	37.07
Std Dev	3.244

Gaus mean : 37.9646

Gaus Width: 3.4524

Entry



B1L101S, U25-ch13, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	45.7
Std Dev	3.404

Gaus mean : 45.9798

Gaus Width: 3.2089

10

8

6

4

2

0

0

2

4

6

8

10

10

20

30

40

50

60

ampl

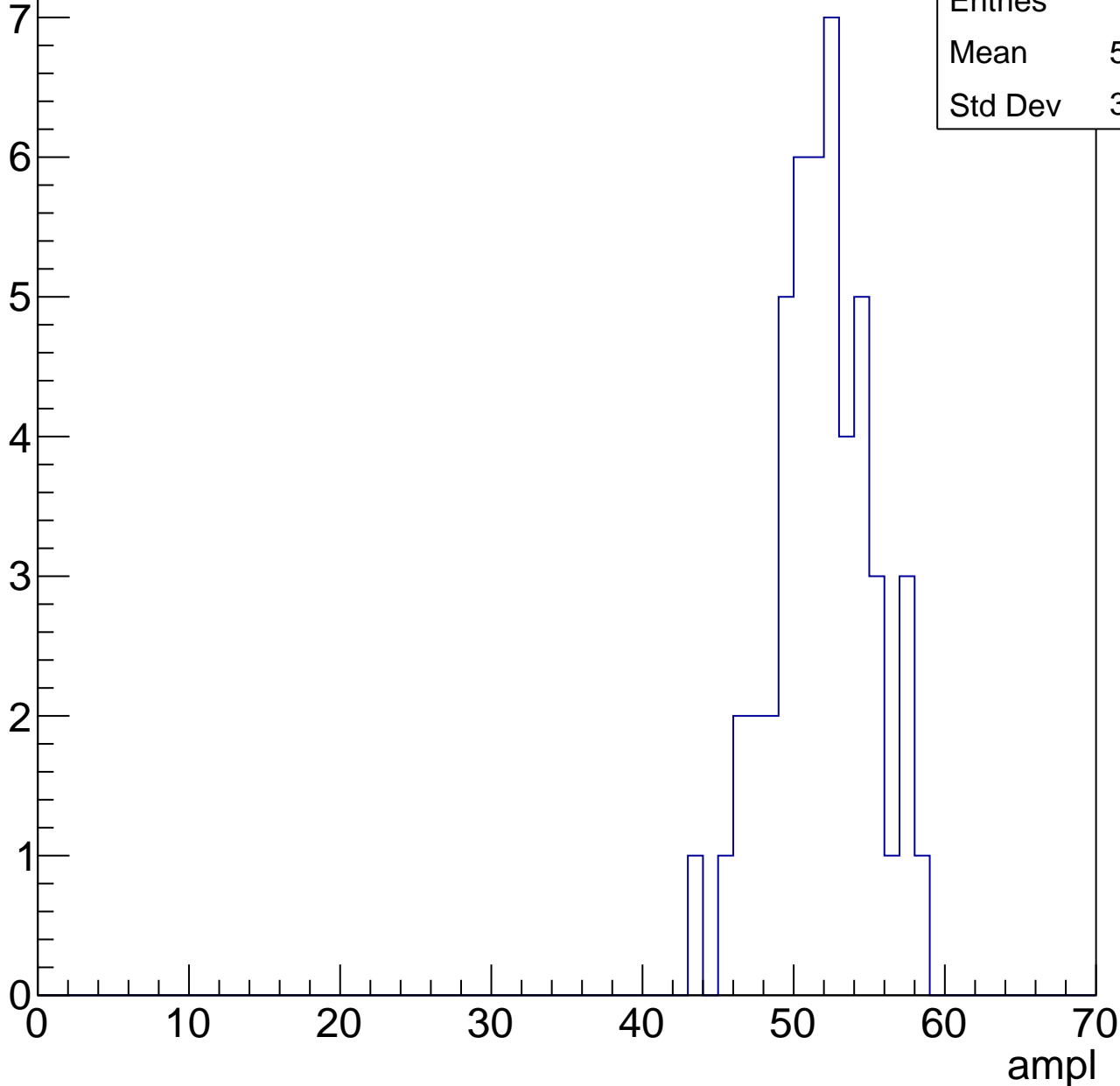
70

B1L101S, U25-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

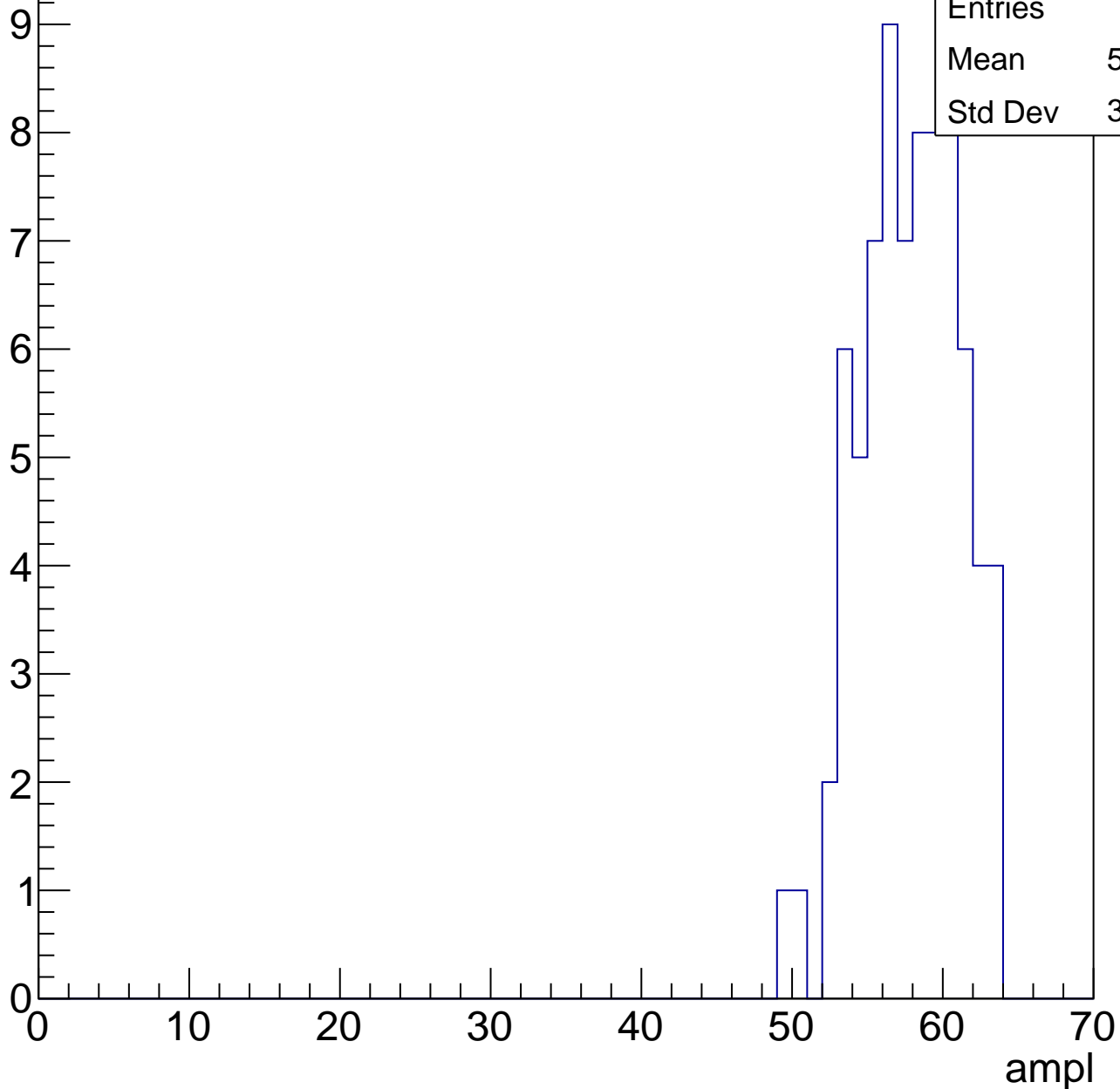
Entries	49
Mean	51.37
Std Dev	3.249



B1L101S, U25-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



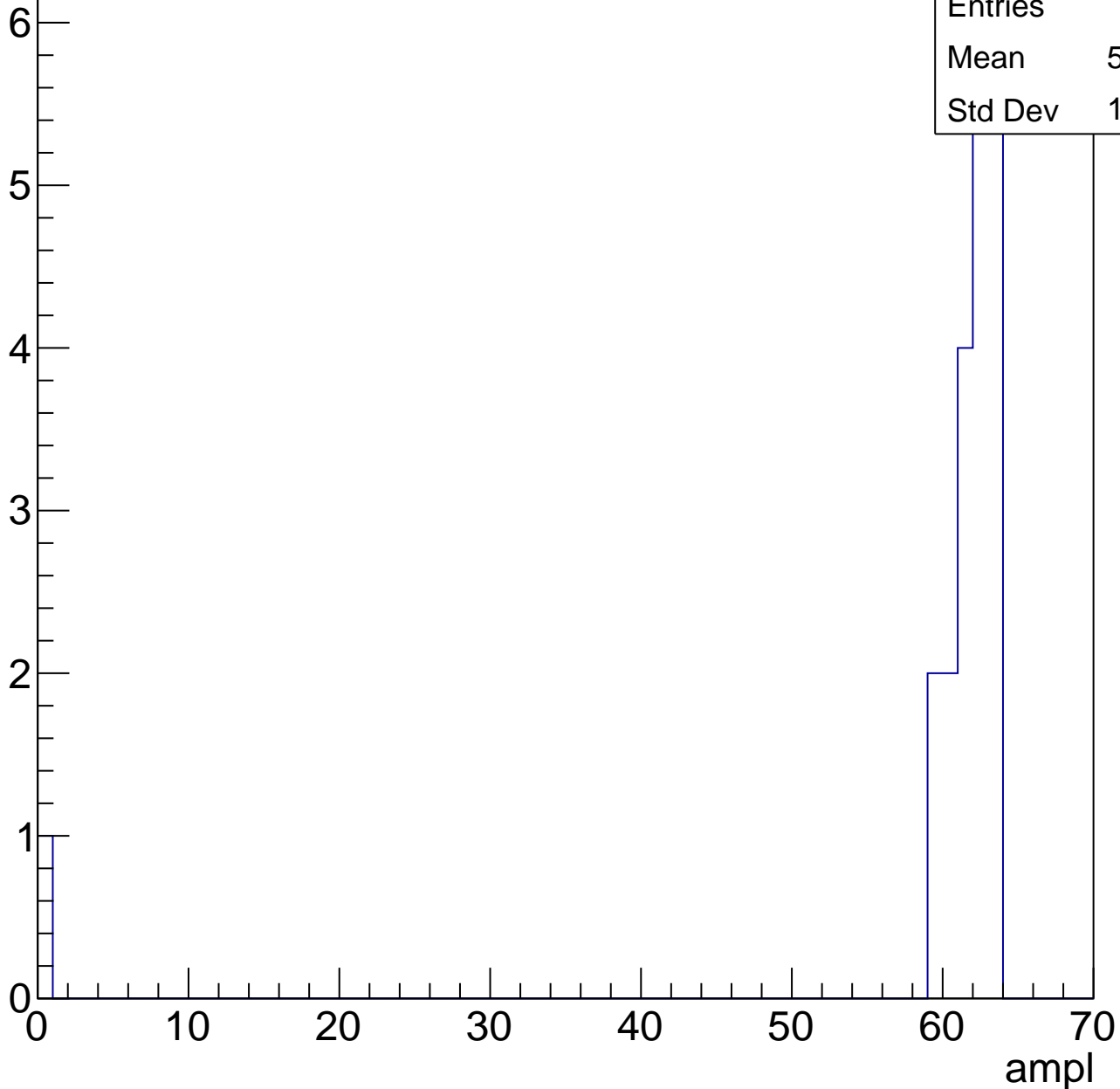
Entries	76
Mean	57.38
Std Dev	3.208

B1L101S, U25-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.67
Std Dev	13.18



B1L101S, U25-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch14, adc0

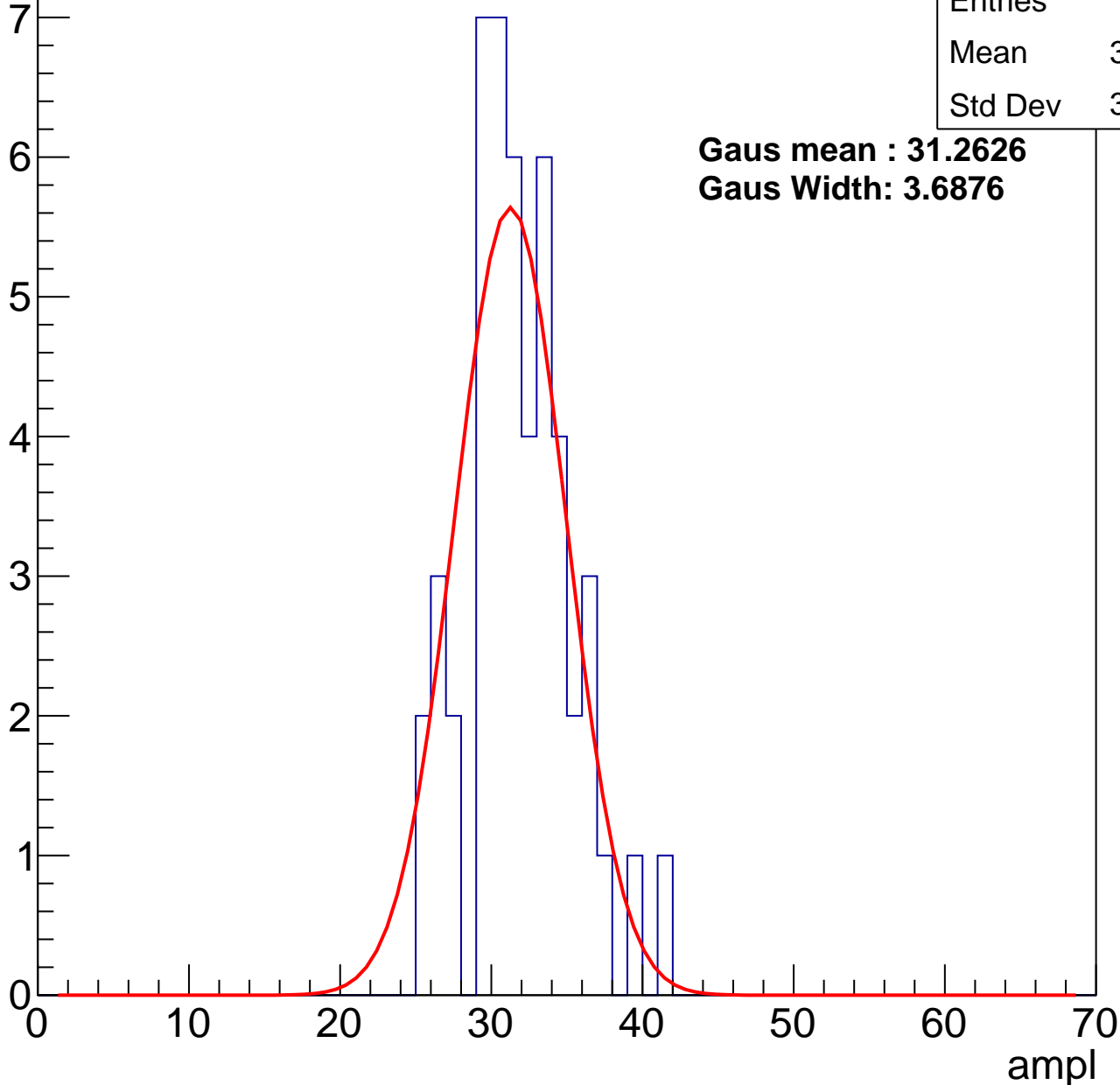
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	31.39
Std Dev	3.433

Gaus mean : 31.2626

Gaus Width: 3.6876



B1L101S, U25-ch14, adc1

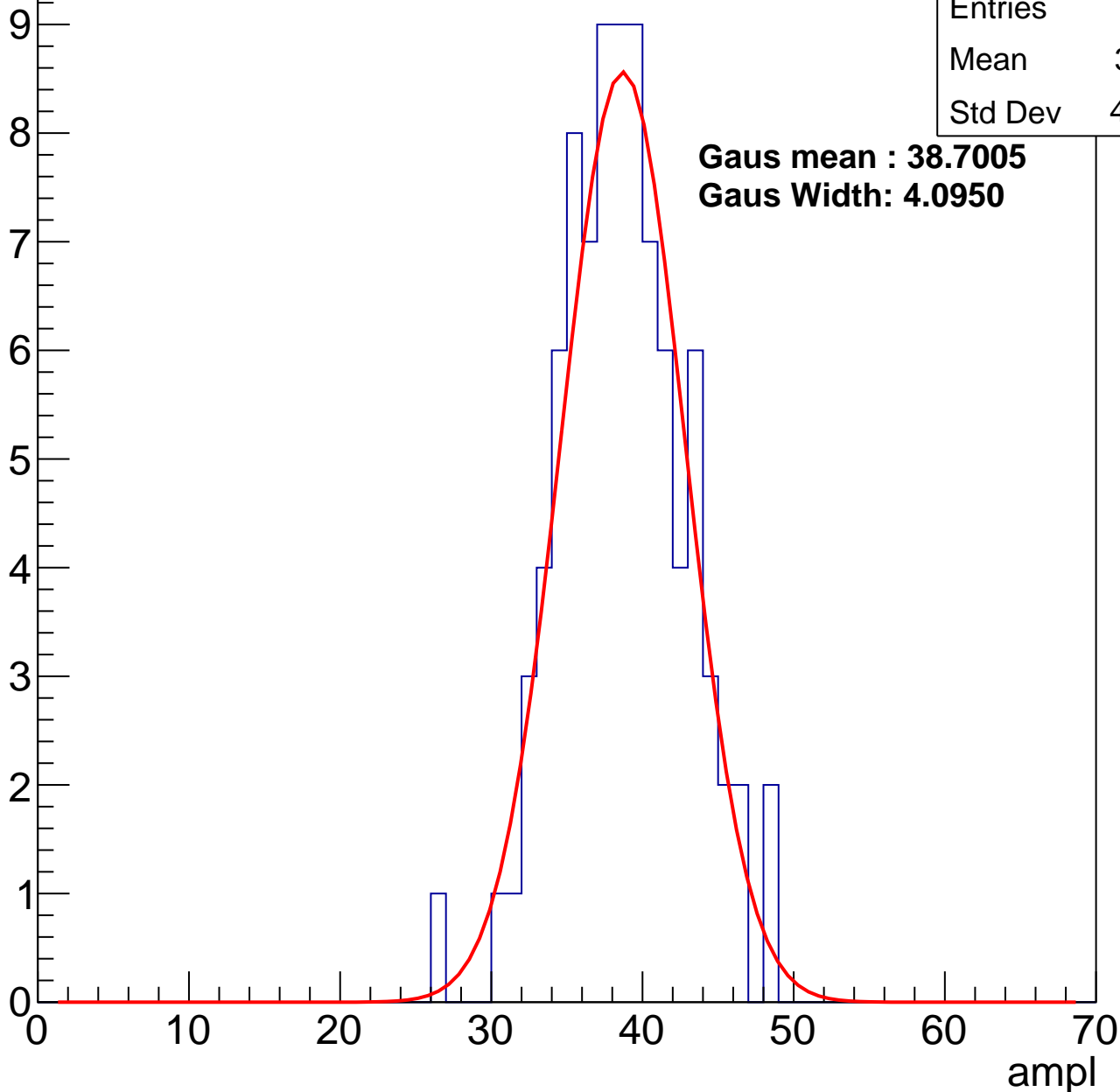
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	90
Mean	38.21
Std Dev	4.089

Gaus mean : 38.7005

Gaus Width: 4.0950



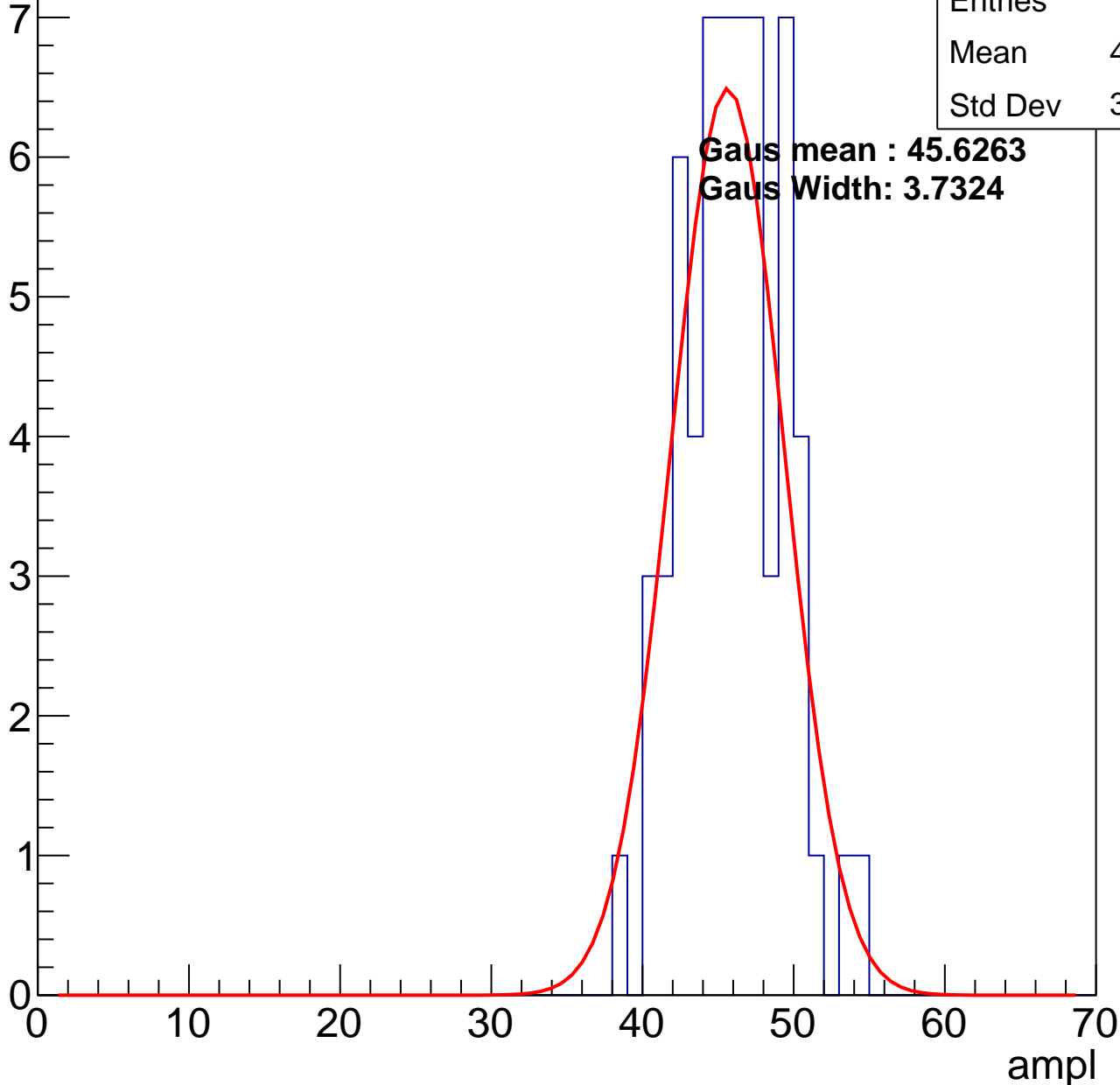
B1L101S, U25-ch14, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	45.55
Std Dev	3.325

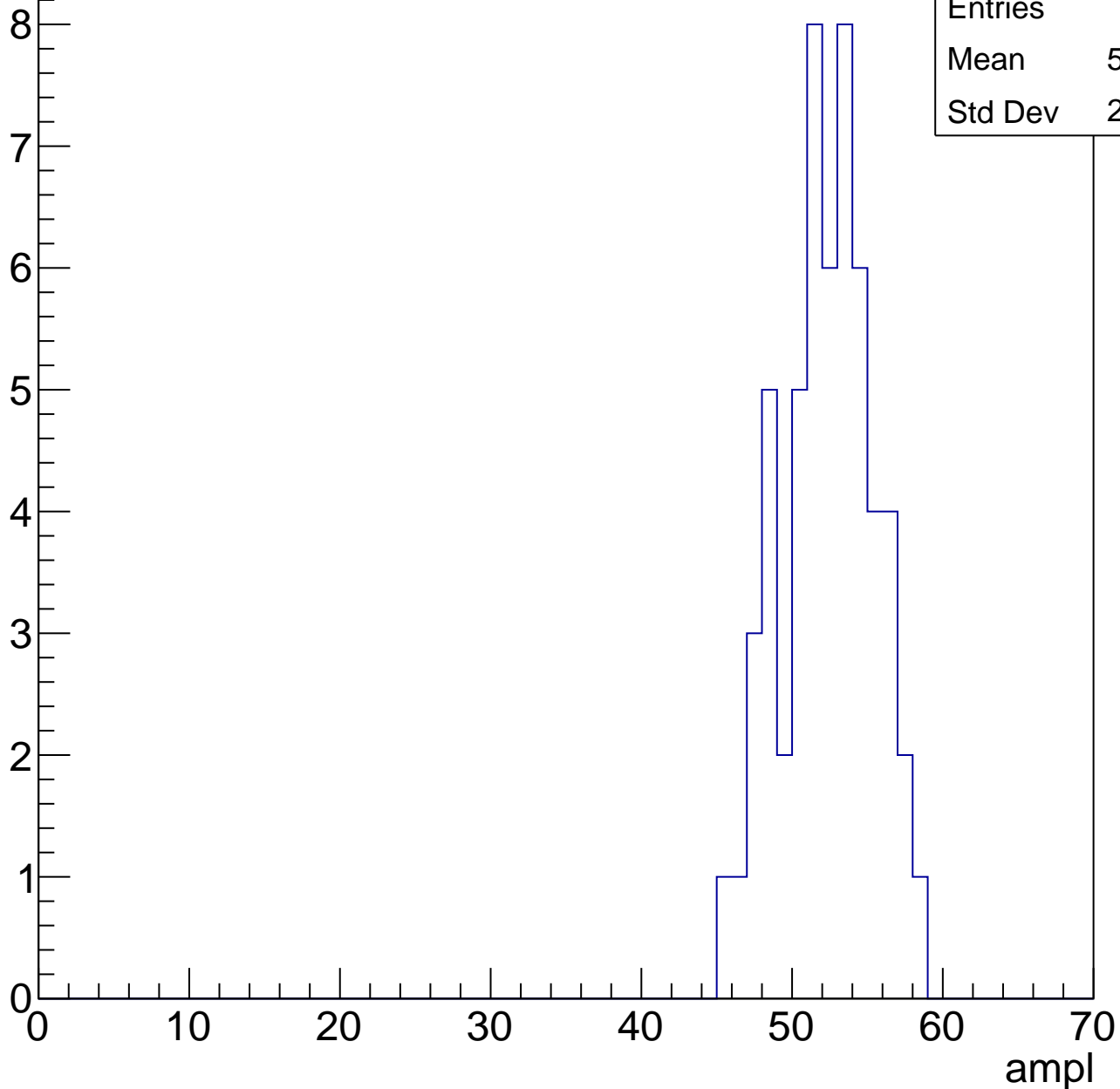
Gaus mean : 45.6263
Gaus Width: 3.7324



B1L101S, U25-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

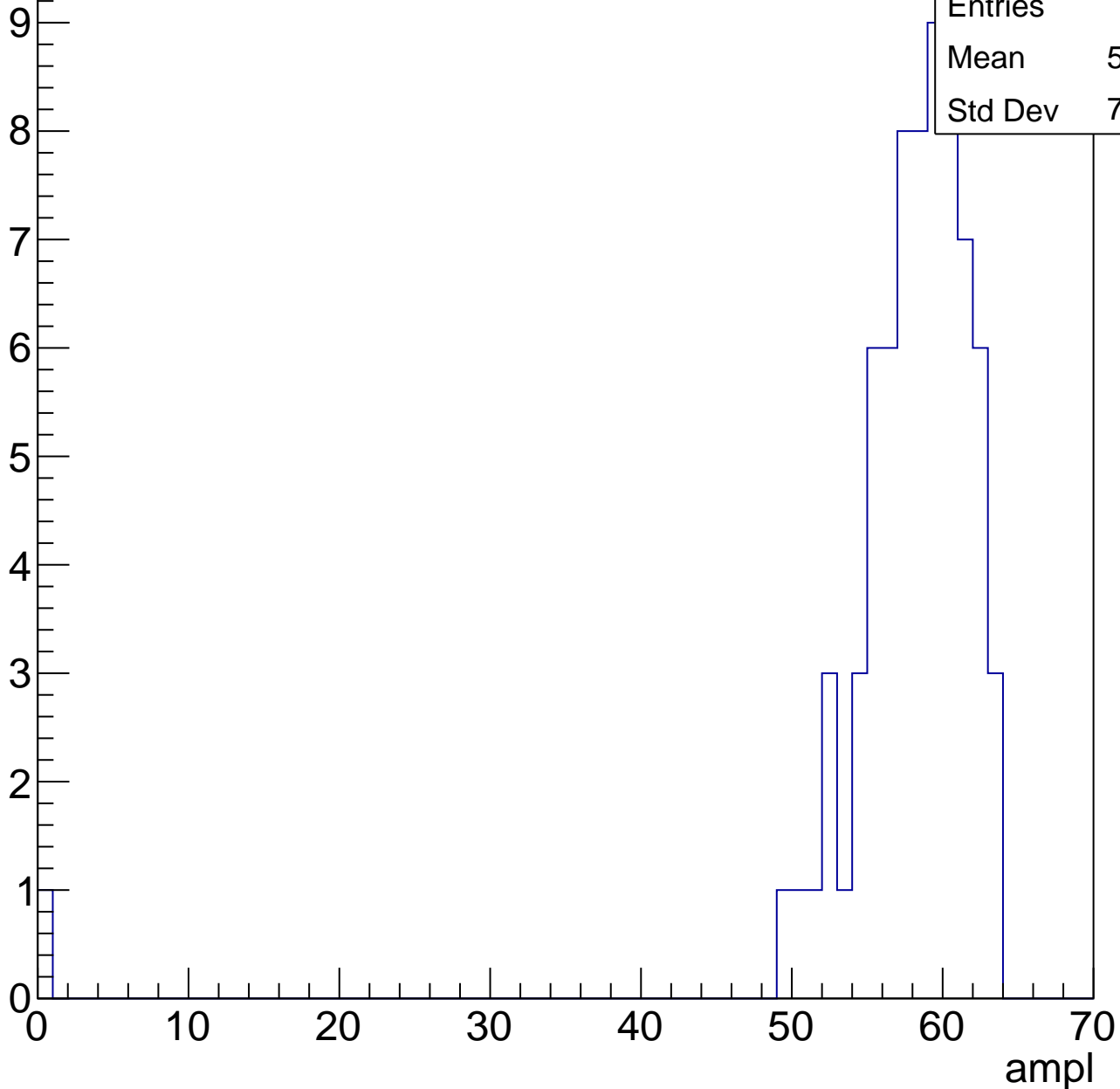


Entries	56
Mean	51.86
Std Dev	2.997

B1L101S, U25-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

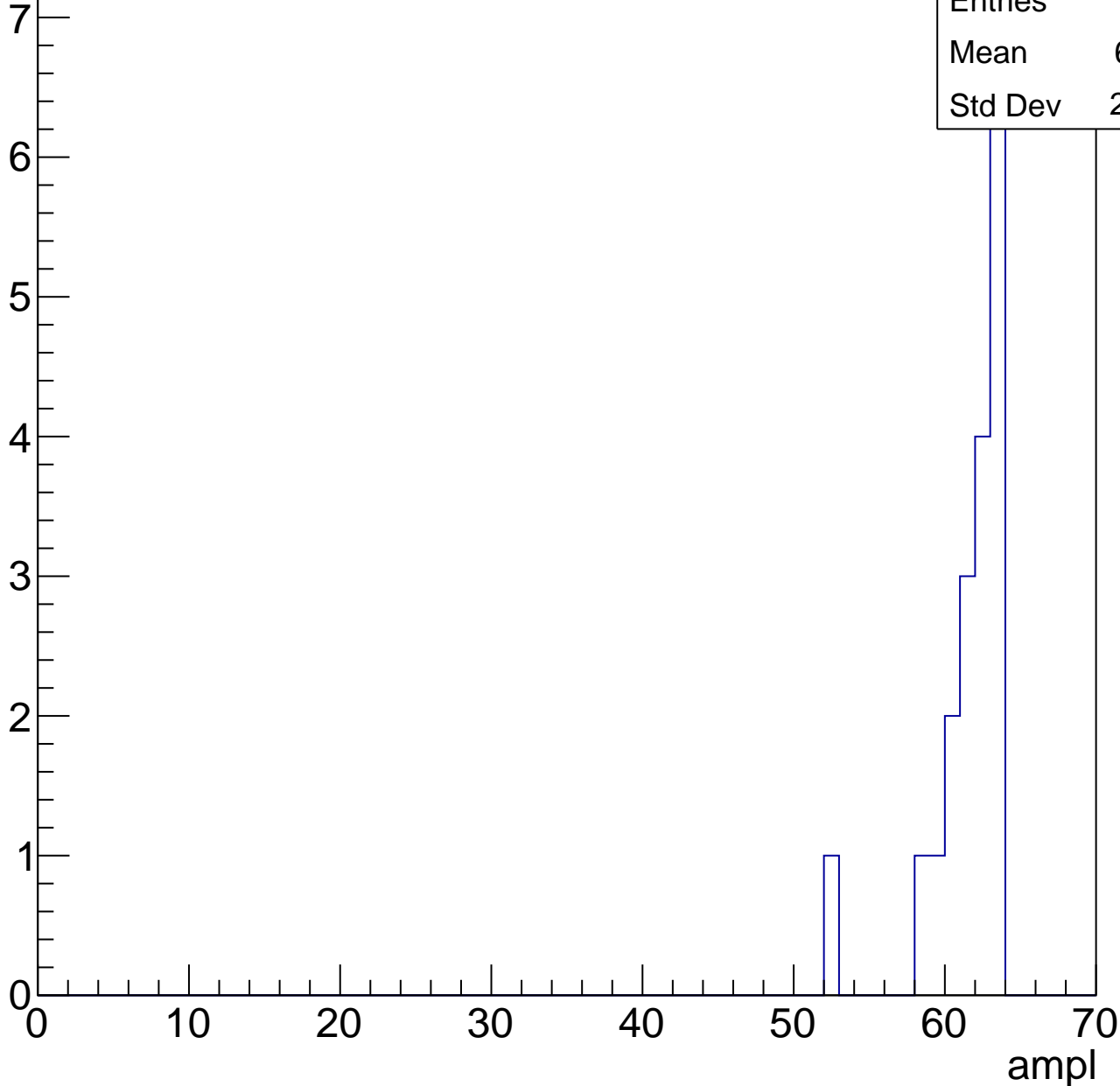


B1L101S, U25-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61.11
Std Dev	2.593



B1L101S, U25-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch15, adc0

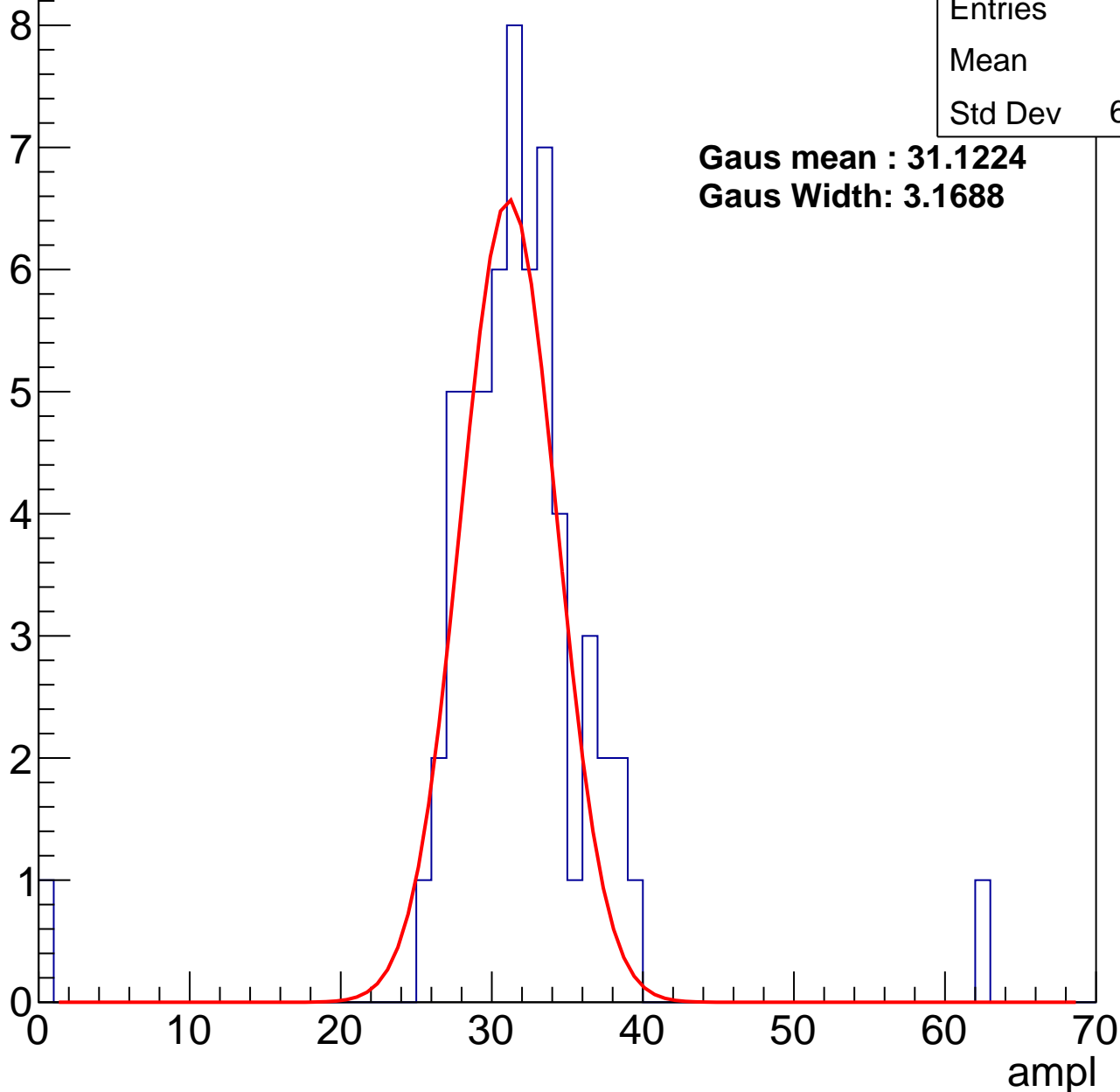
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	31.3
Std Dev	6.517

Gaus mean : 31.1224

Gaus Width: 3.1688



B1L101S, U25-ch15, adc1

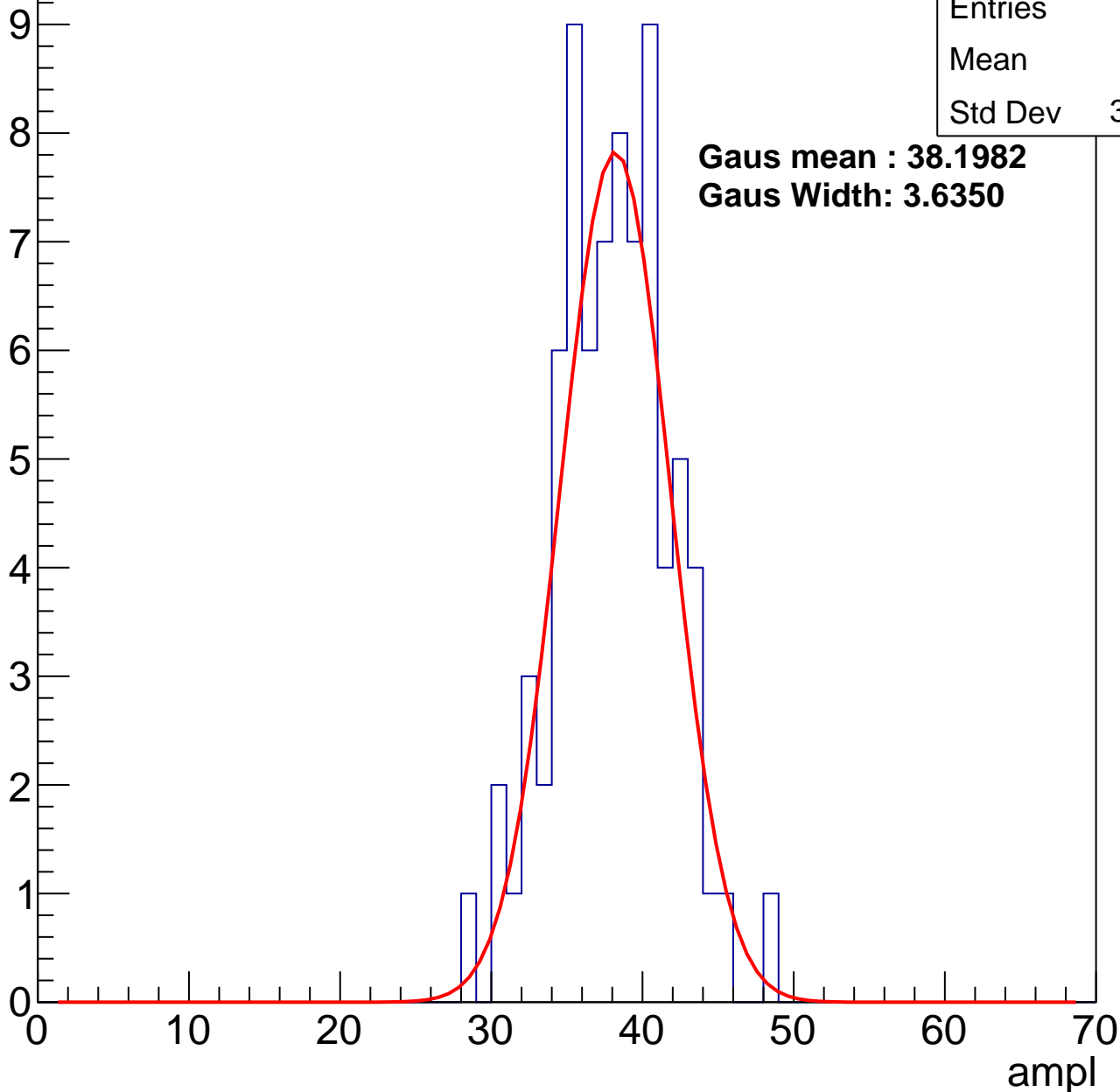
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	37.6
Std Dev	3.736

Gaus mean : 38.1982

Gaus Width: 3.6350



B1L101S, U25-ch15, adc2

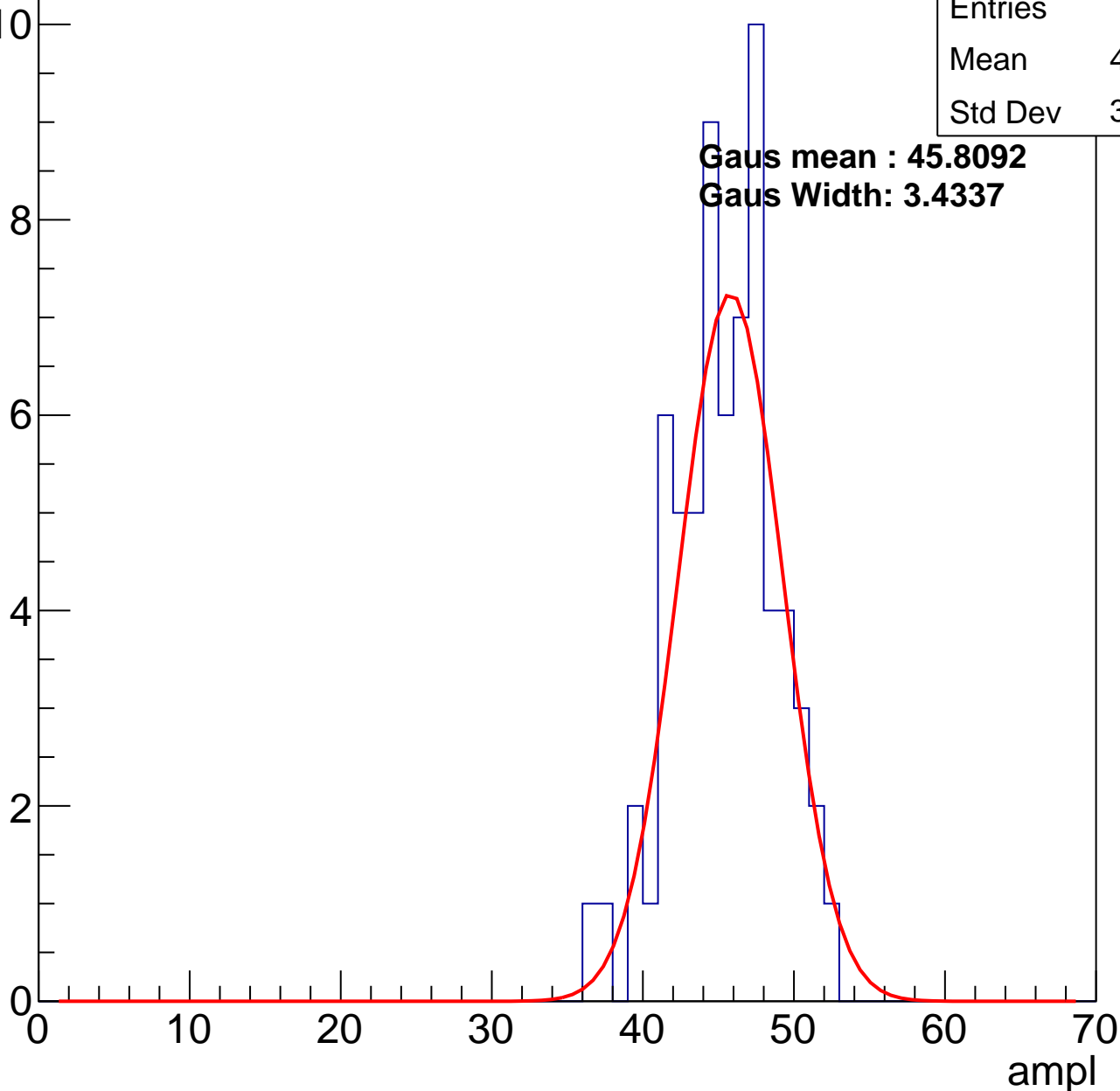
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	44.96
Std Dev	3.357

Gaus mean : 45.8092

Gaus Width: 3.4337

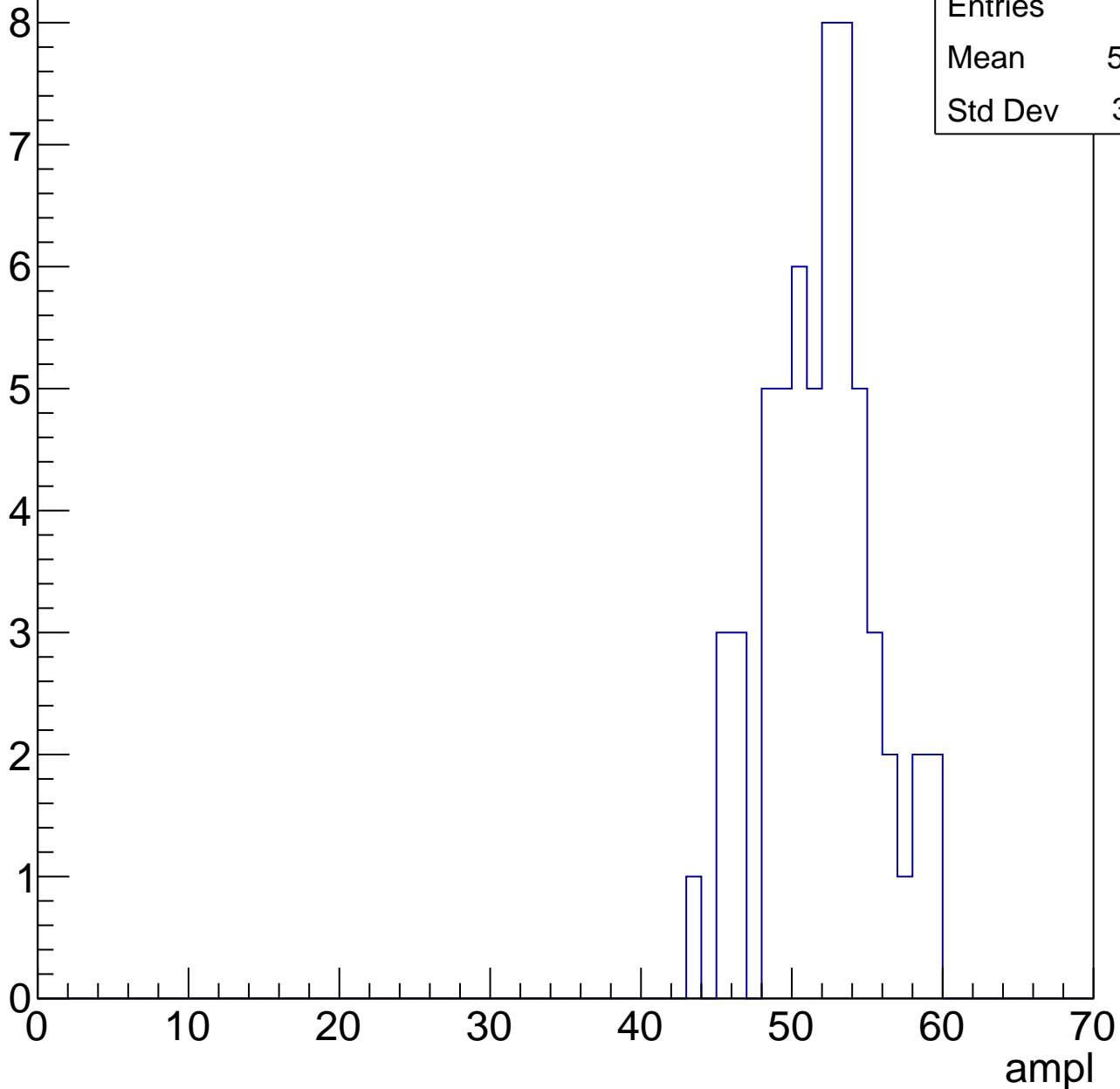


B1L101S, U25-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

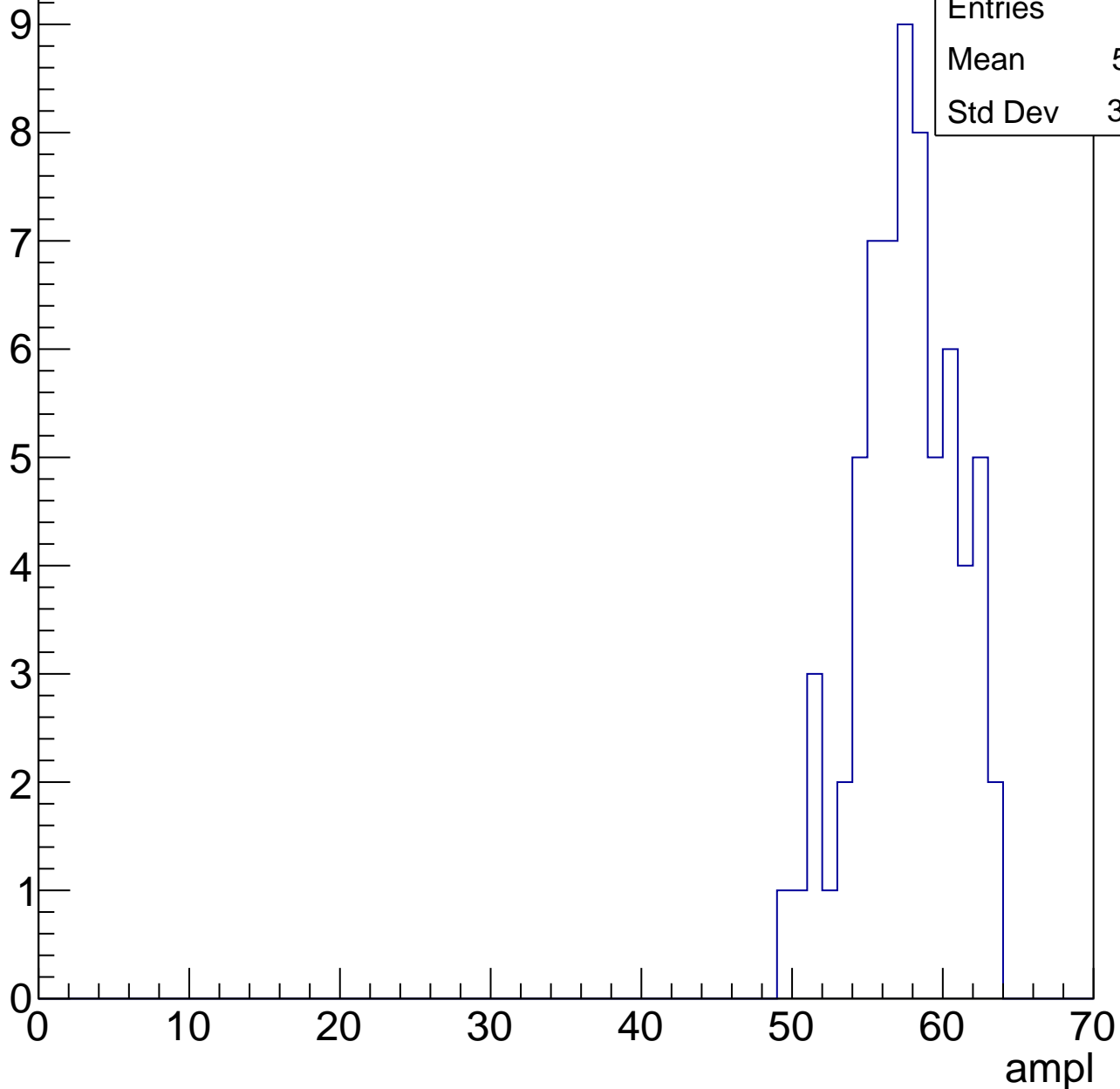
Entries	59
Mean	51.42
Std Dev	3.571



B1L101S, U25-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



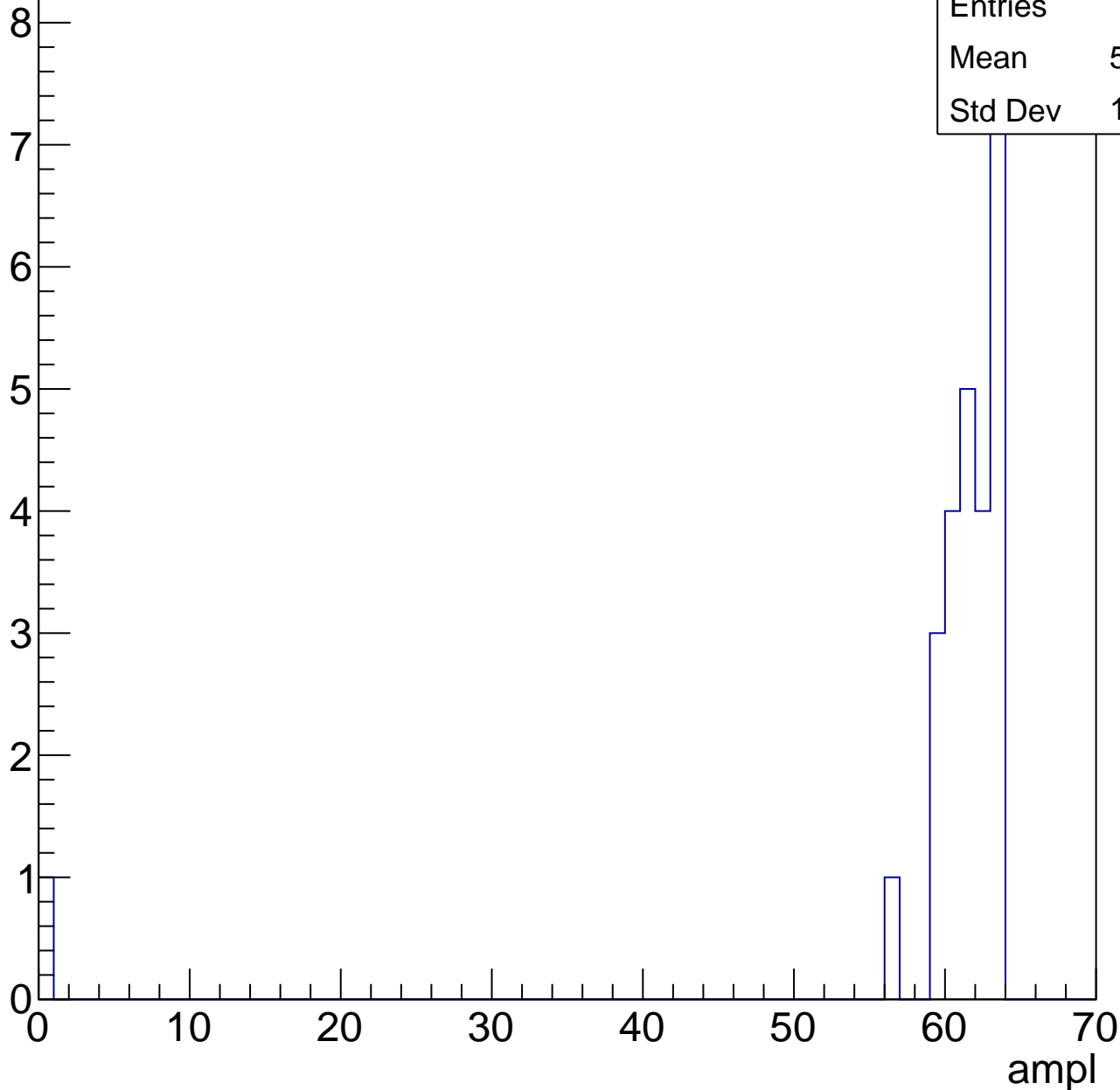
Entries	66
Mean	57.11
Std Dev	3.257

B1L101S, U25-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	58.85
Std Dev	11.89



B1L101S, U25-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

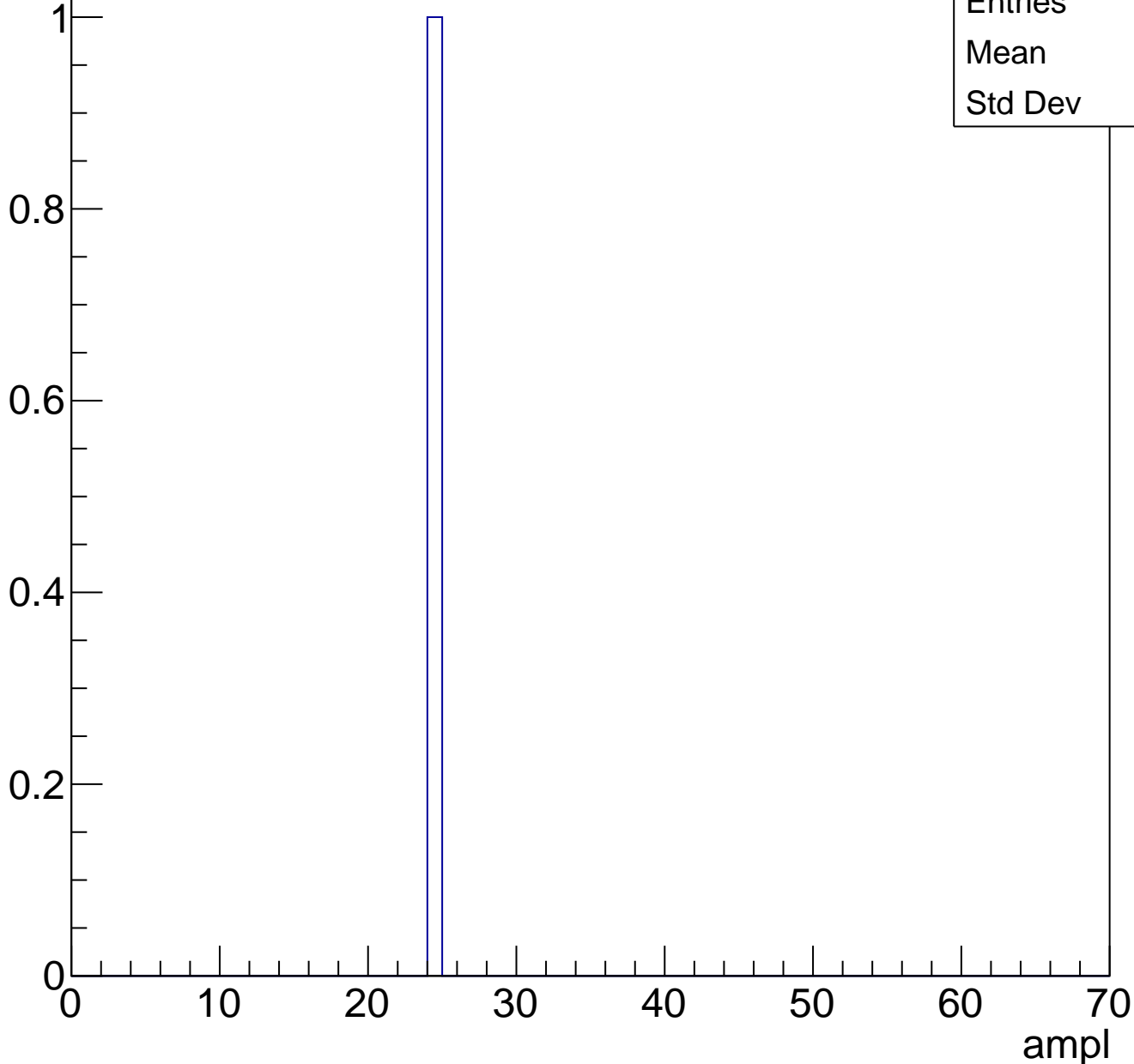
Entry



B1L101S, U25-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch16, adc0

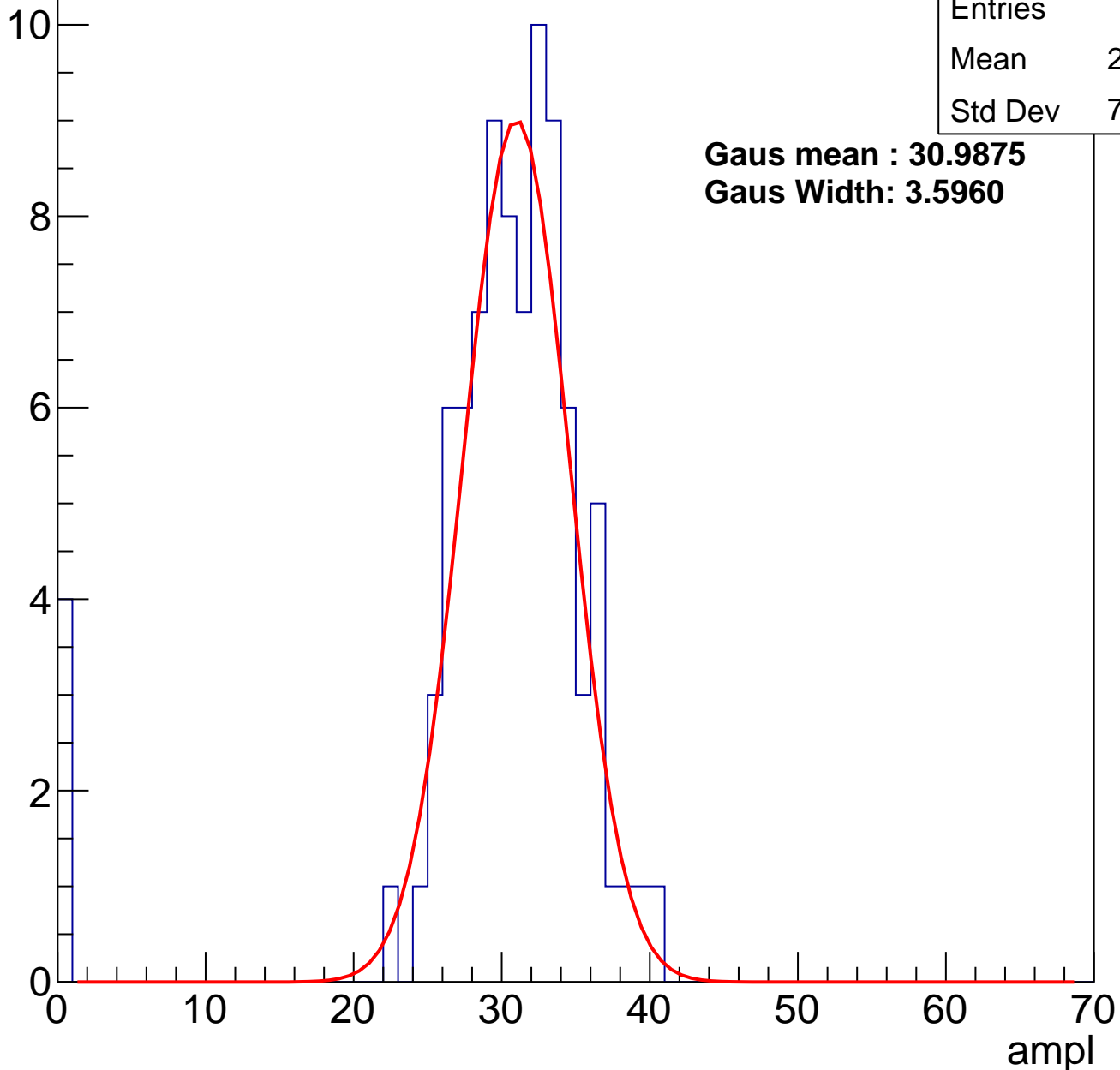
calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	29.36
Std Dev	7.266

Gaus mean : 30.9875

Gaus Width: 3.5960

Entry



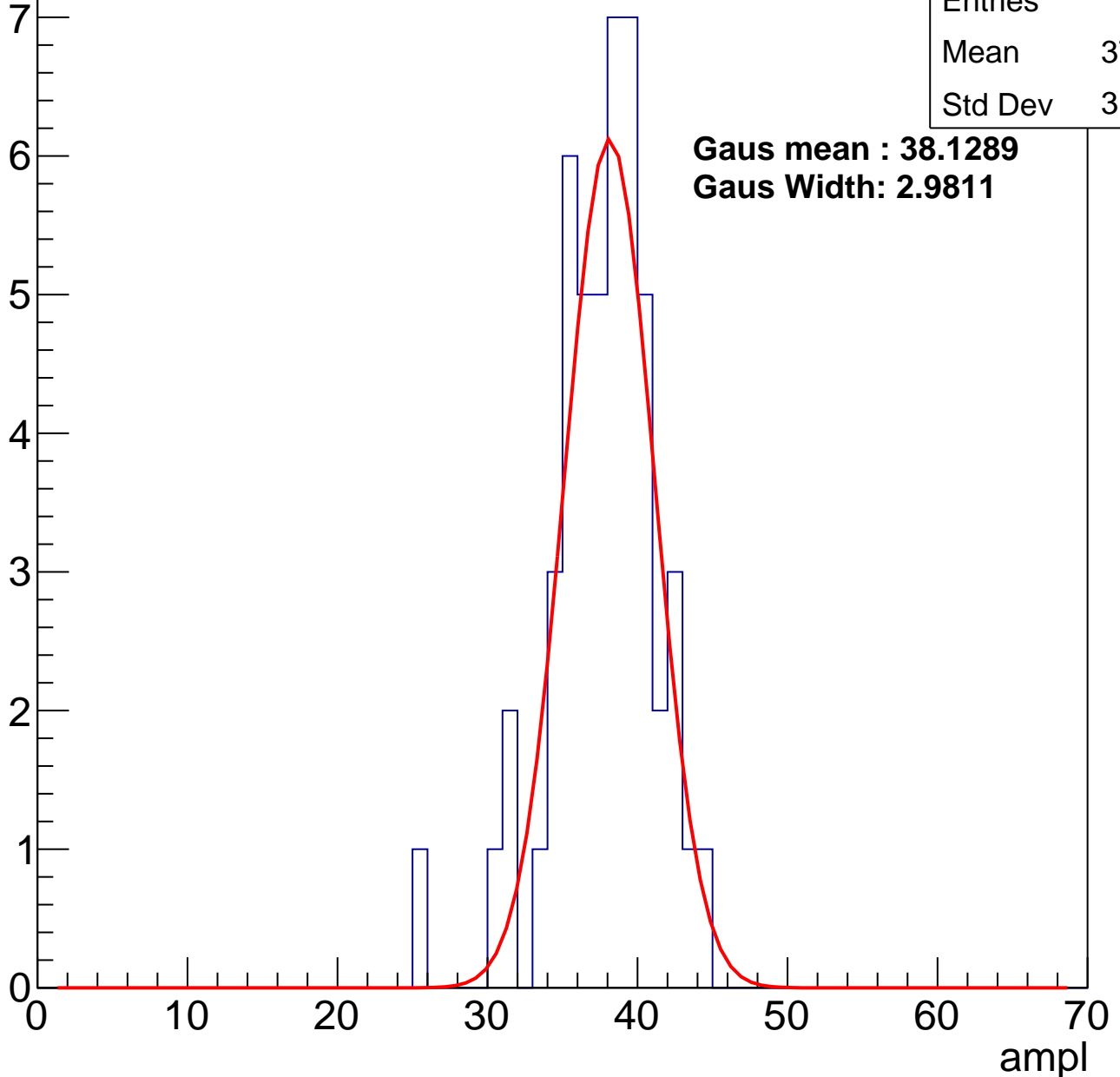
B1L101S, U25-ch16, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	37.22
Std Dev	3.483

Gaus mean : 38.1289
Gaus Width: 2.9811



B1L101S, U25-ch16, adc2

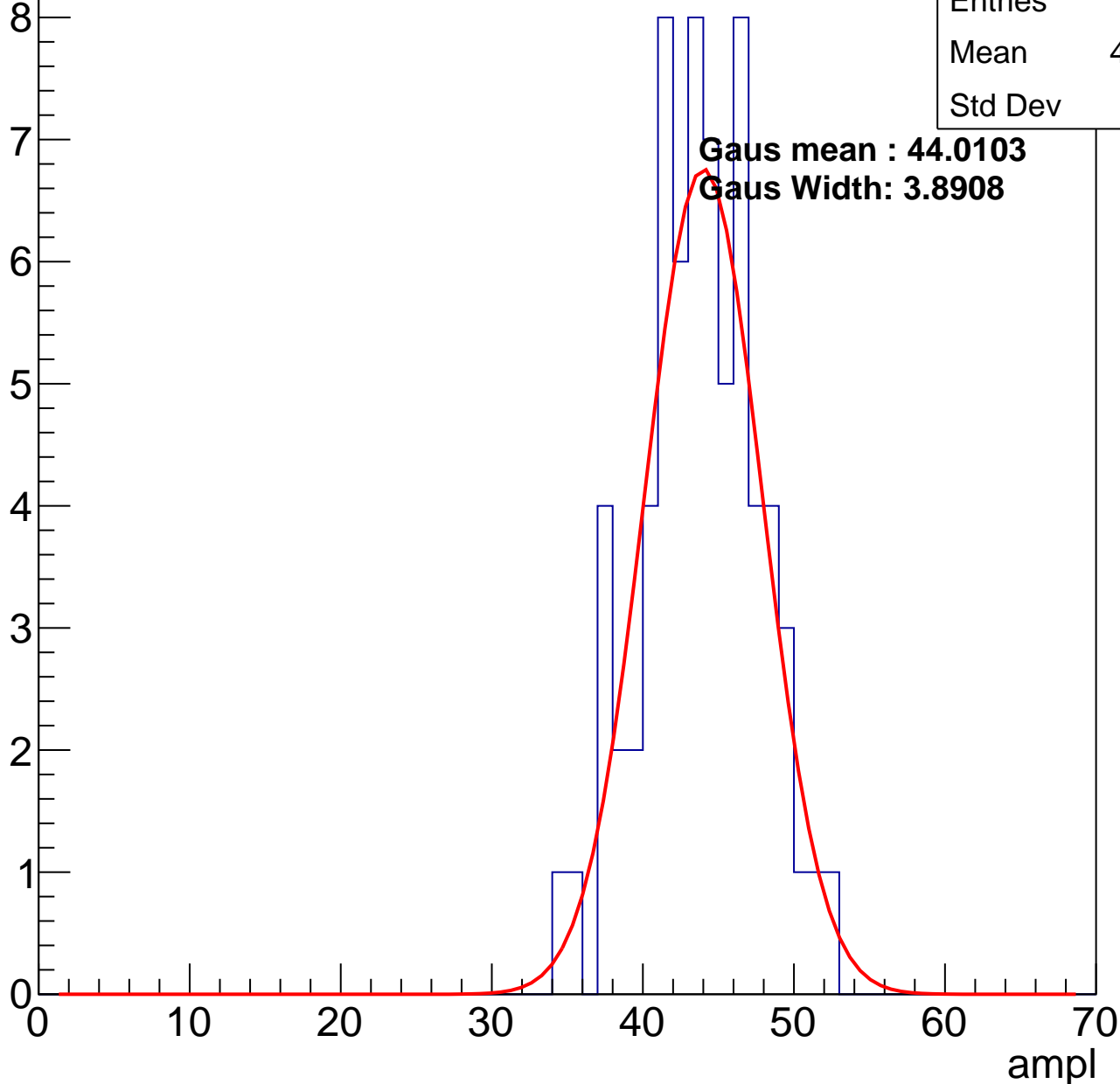
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.37
Std Dev	3.78

Gaus mean : 44.0103

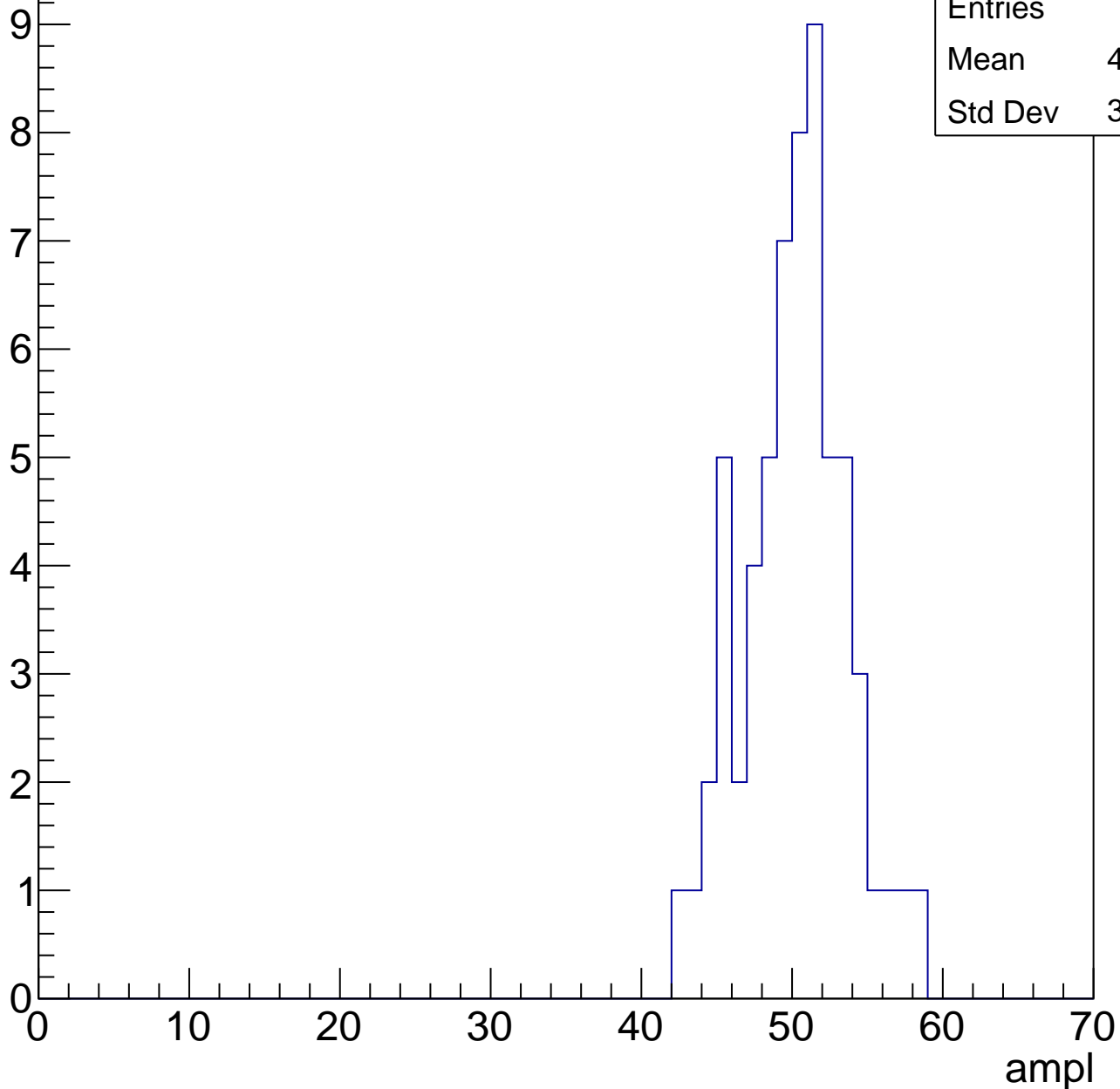
Gaus Width: 3.8908



B1L101S, U25-ch16, adc3

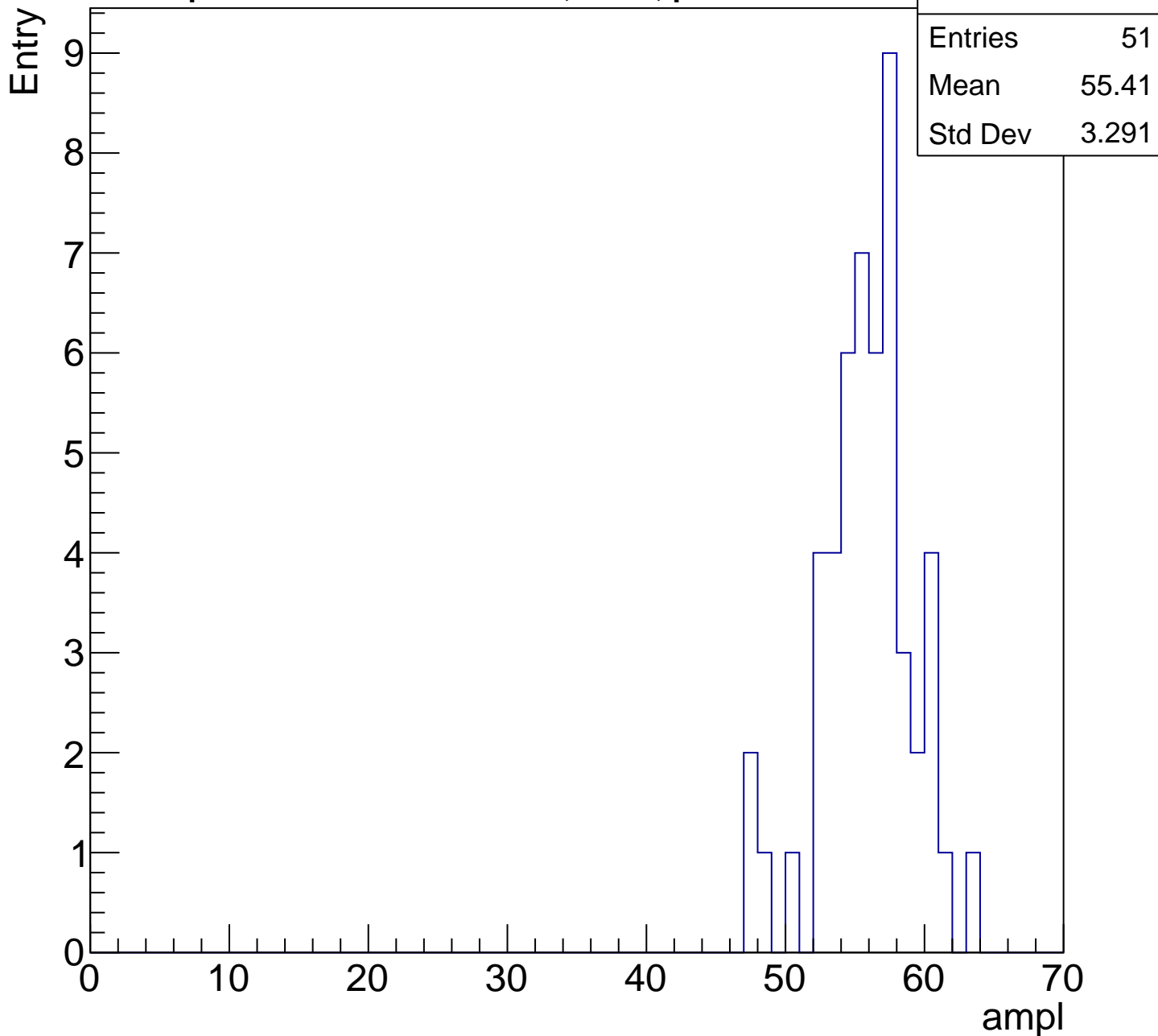
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

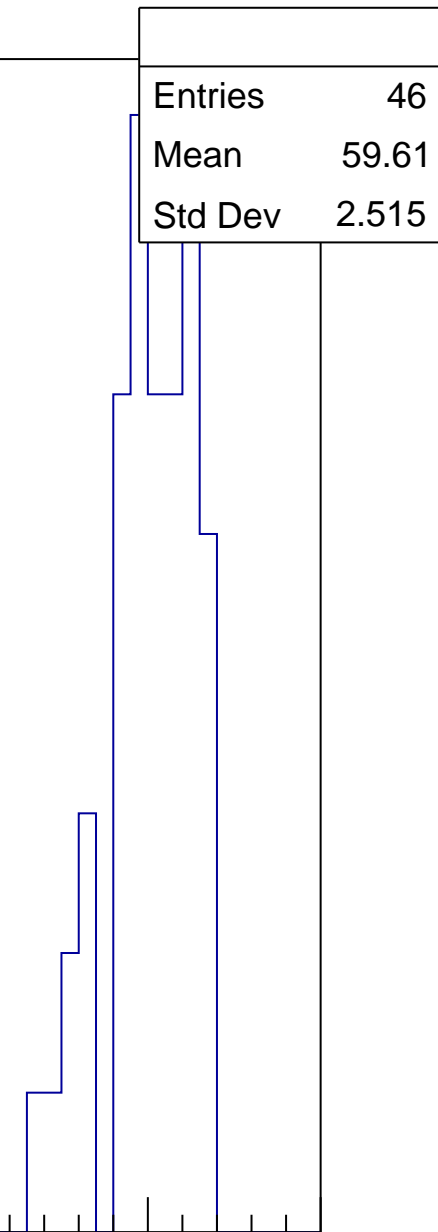
Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.61
Std Dev	2.515

ampl

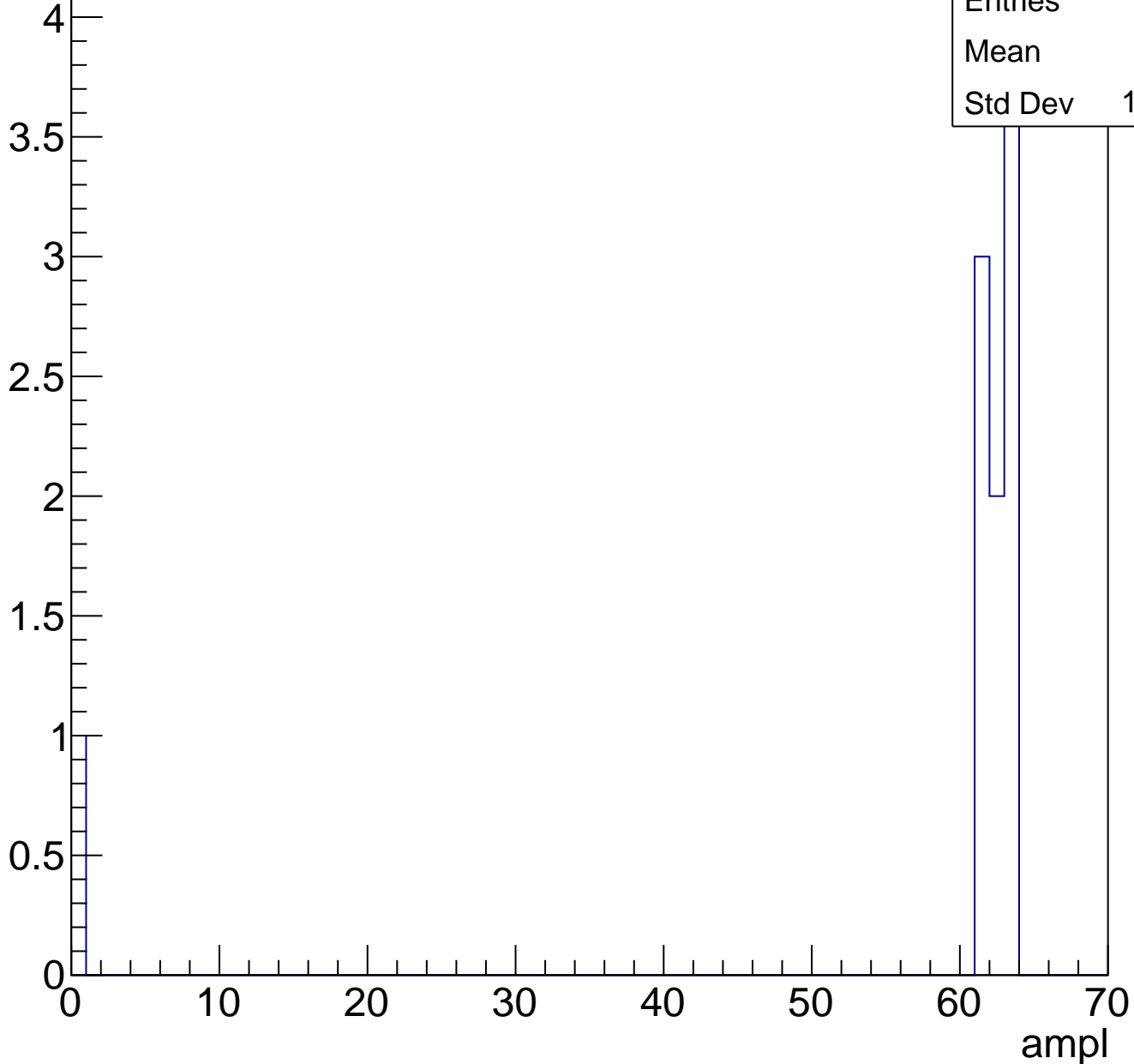
0 10 20 30 40 50 60 70



B1L101S, U25-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch17, adc0

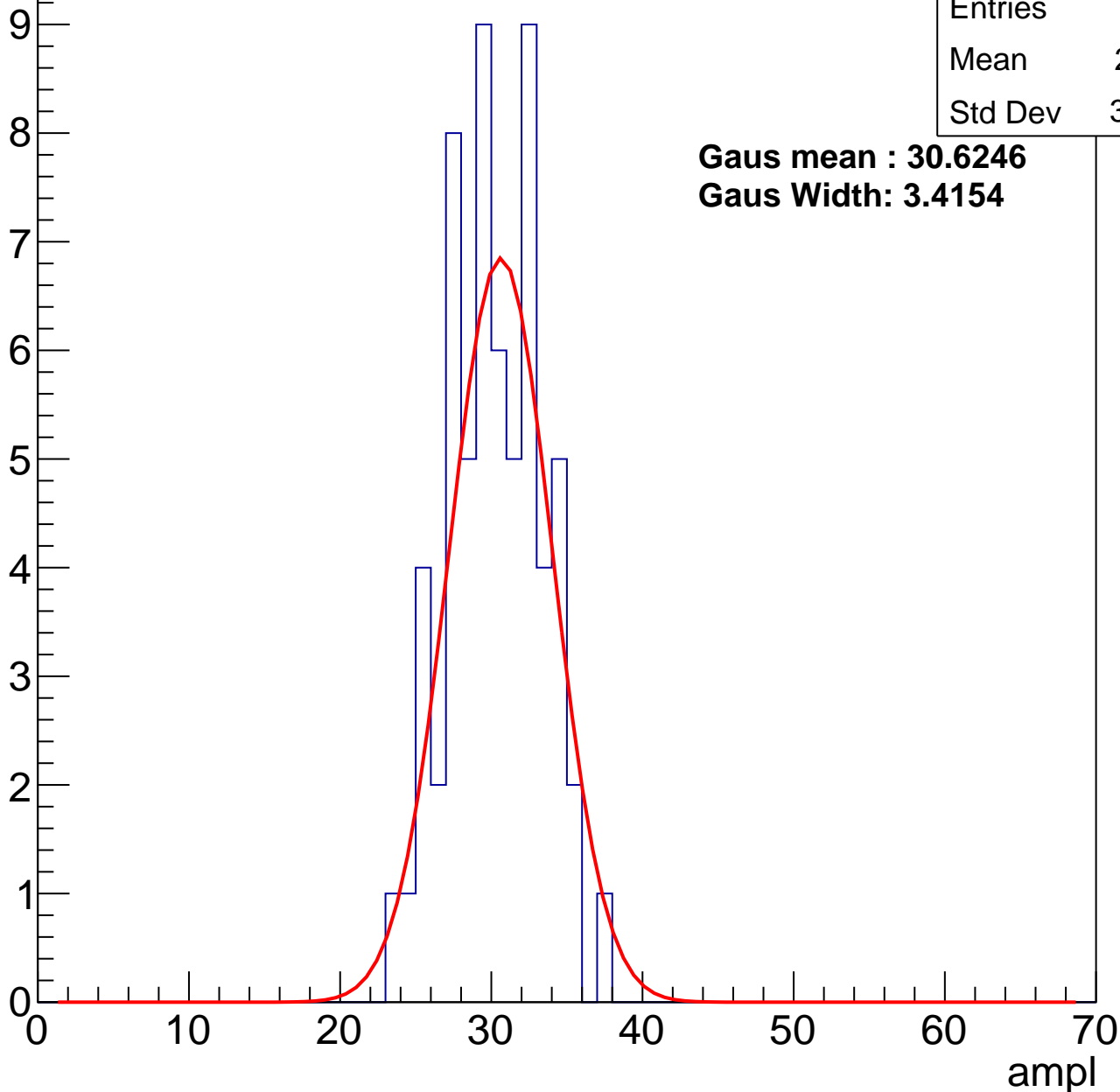
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.81
Std Dev	3.037

Gaus mean : 30.6246

Gaus Width: 3.4154



B1L101S, U25-ch17, adc1

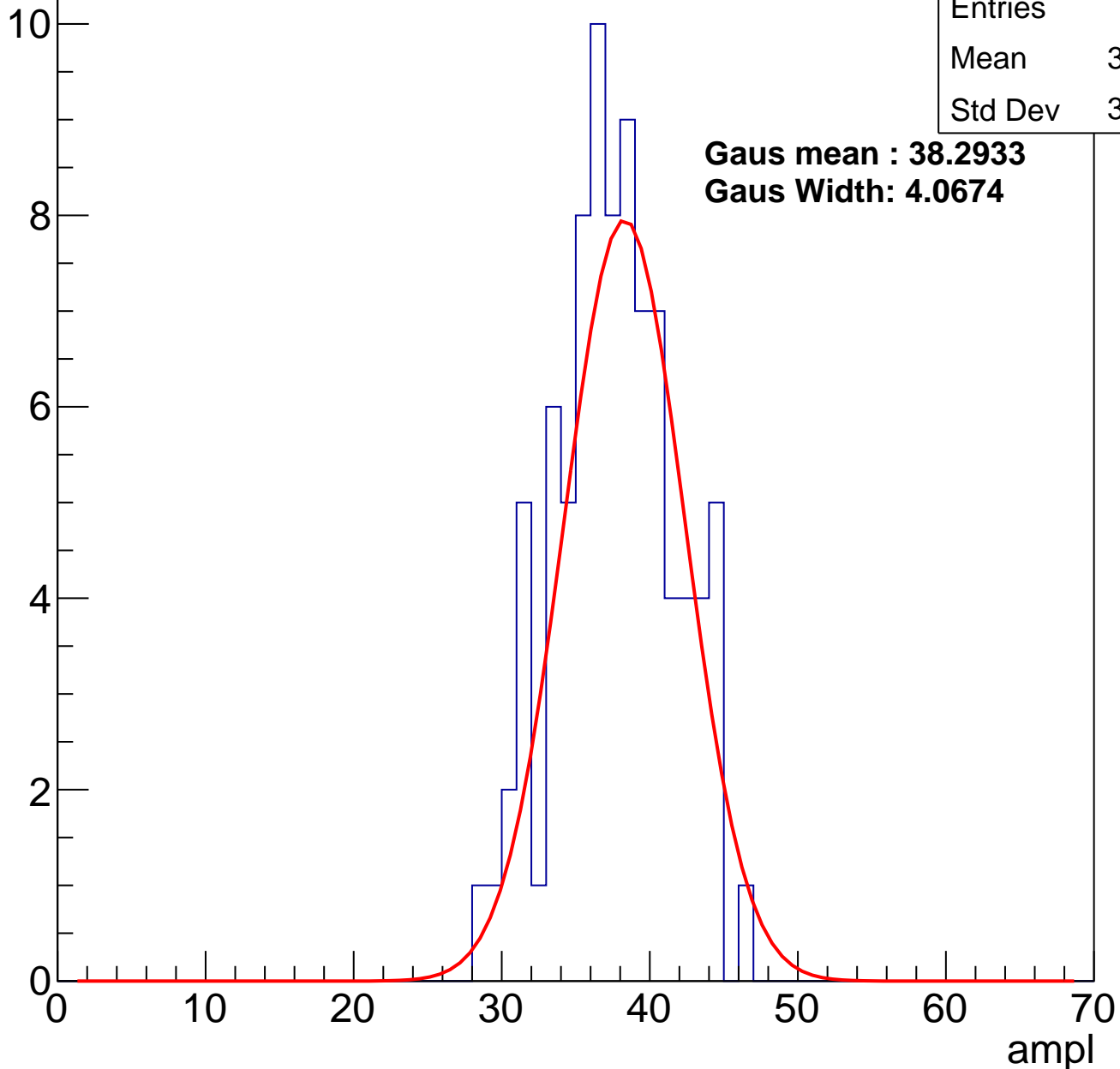
calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	37.19
Std Dev	3.934

Gaus mean : 38.2933

Gaus Width: 4.0674

Entry



B1L101S, U25-ch17, adc2

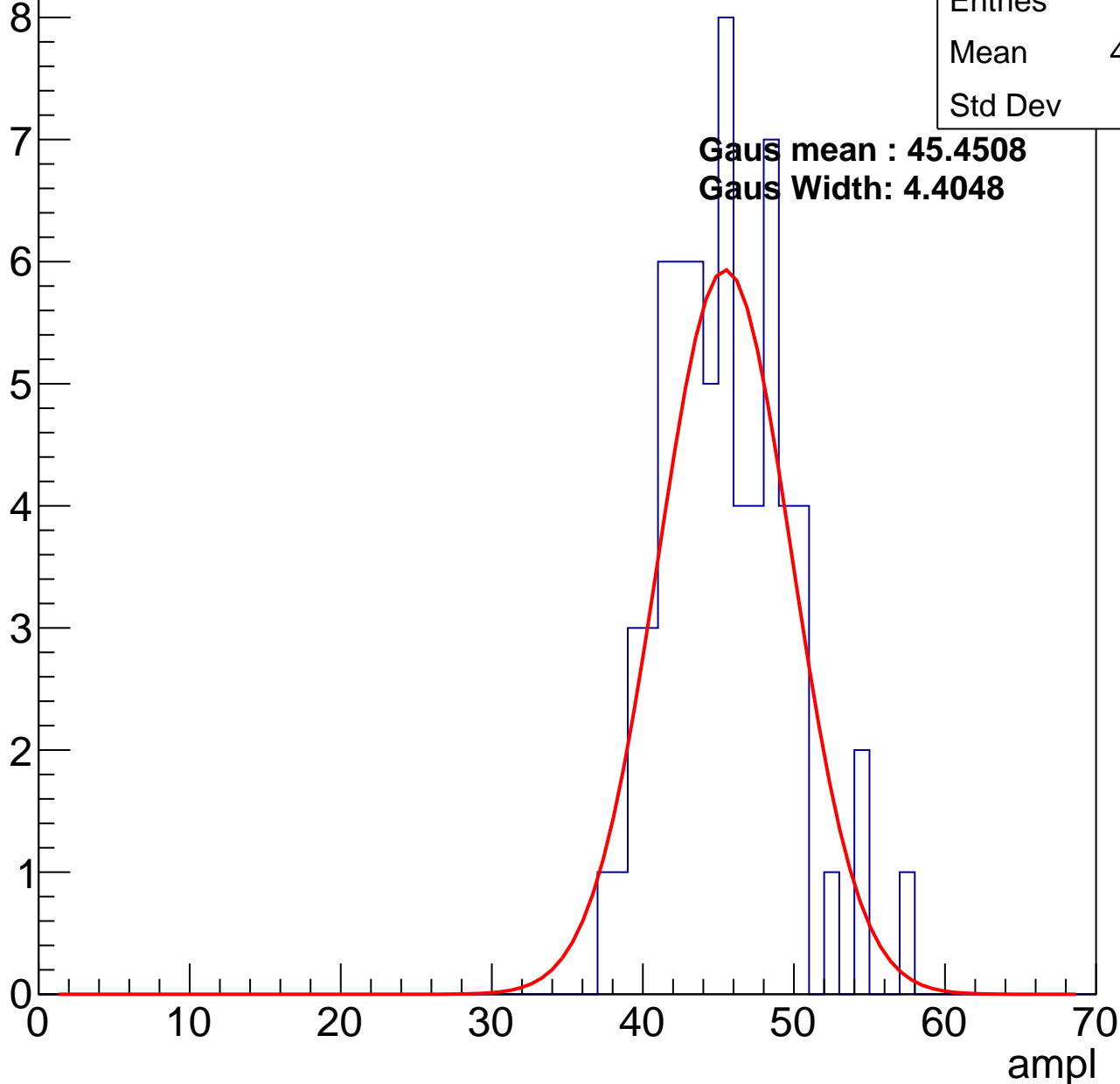
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.98
Std Dev	4.04

Gaus mean : 45.4508

Gaus Width: 4.4048

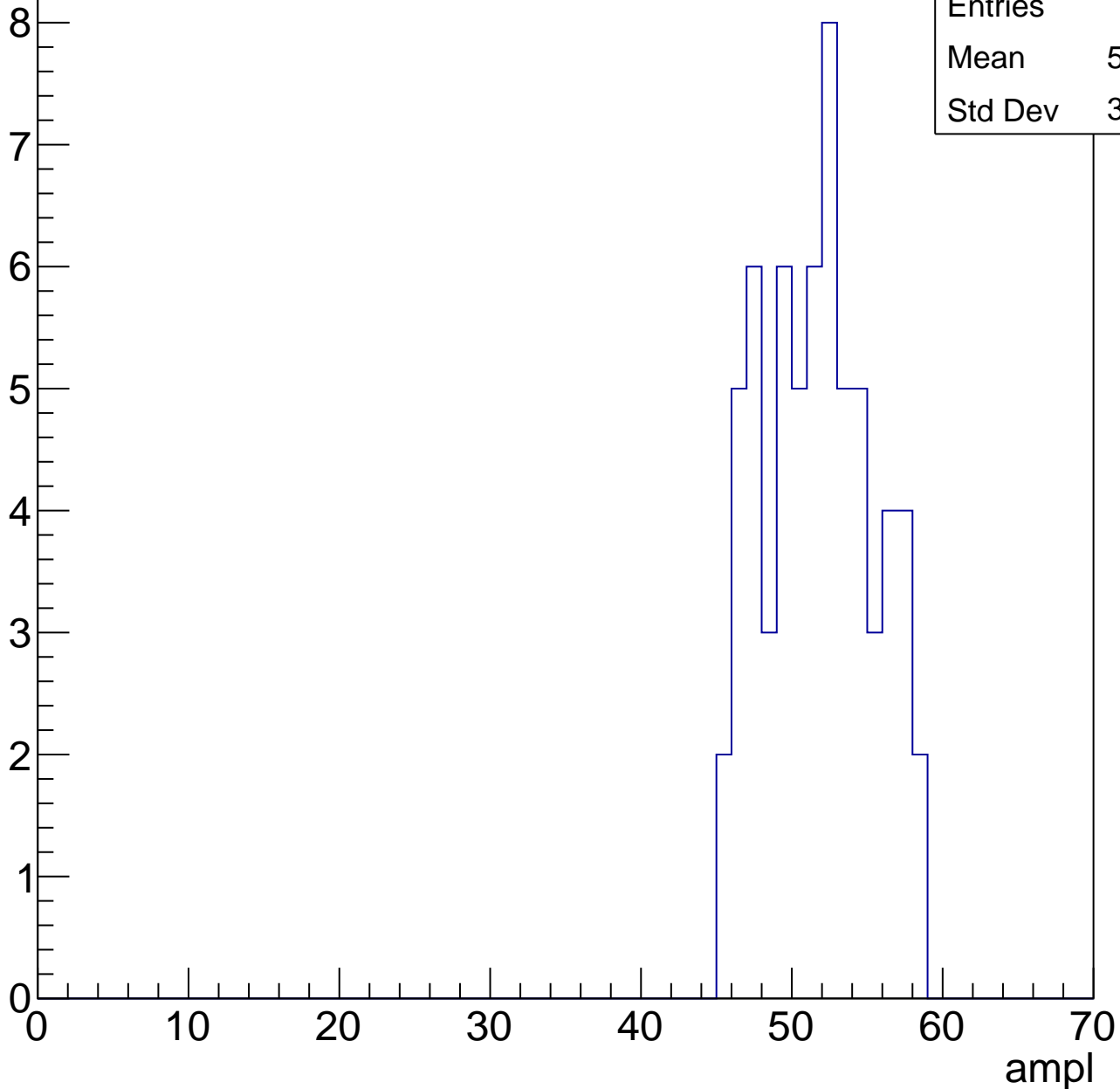


B1L101S, U25-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	51.25
Std Dev	3.553

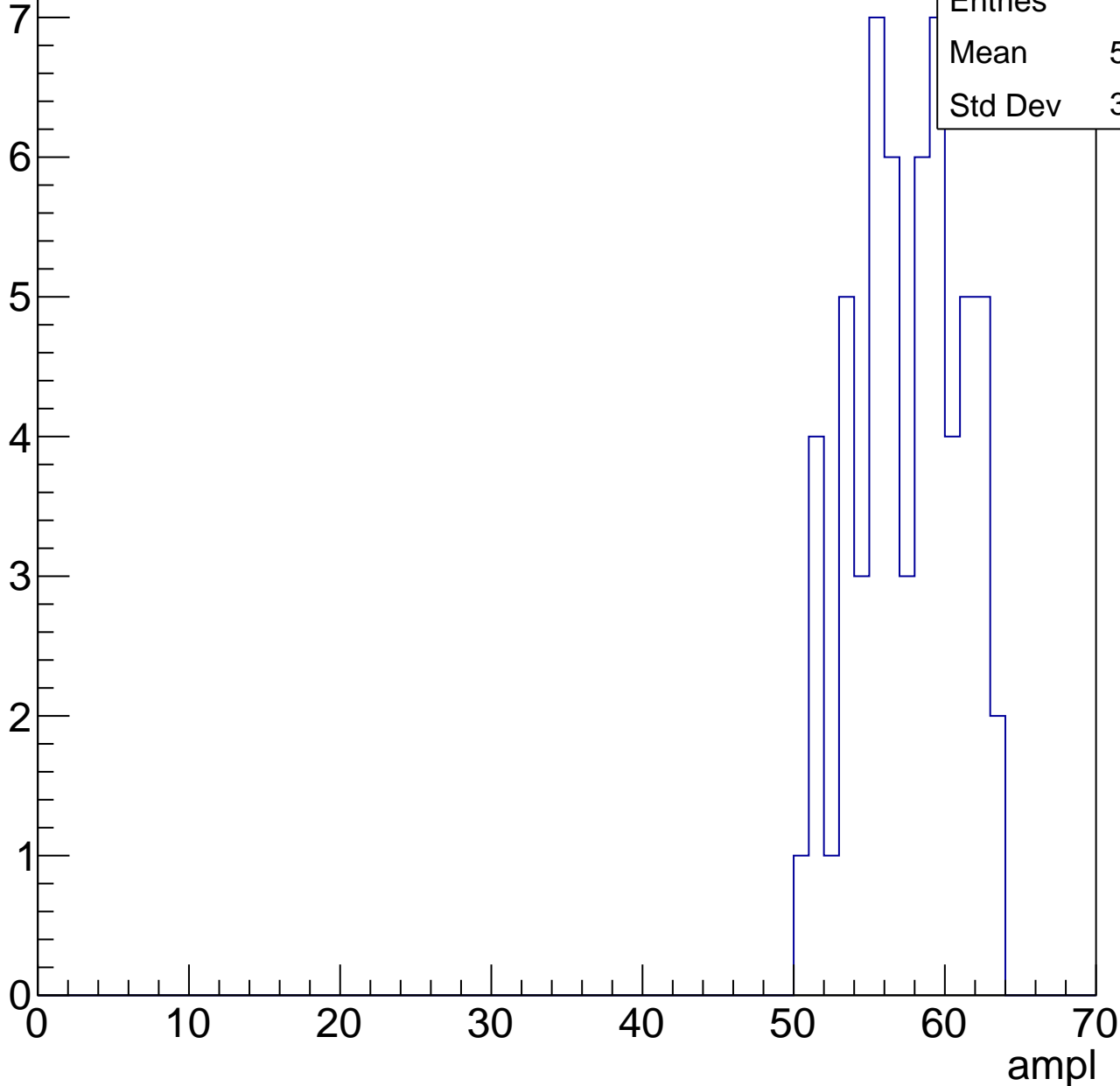


B1L101S, U25-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	57.07
Std Dev	3.459

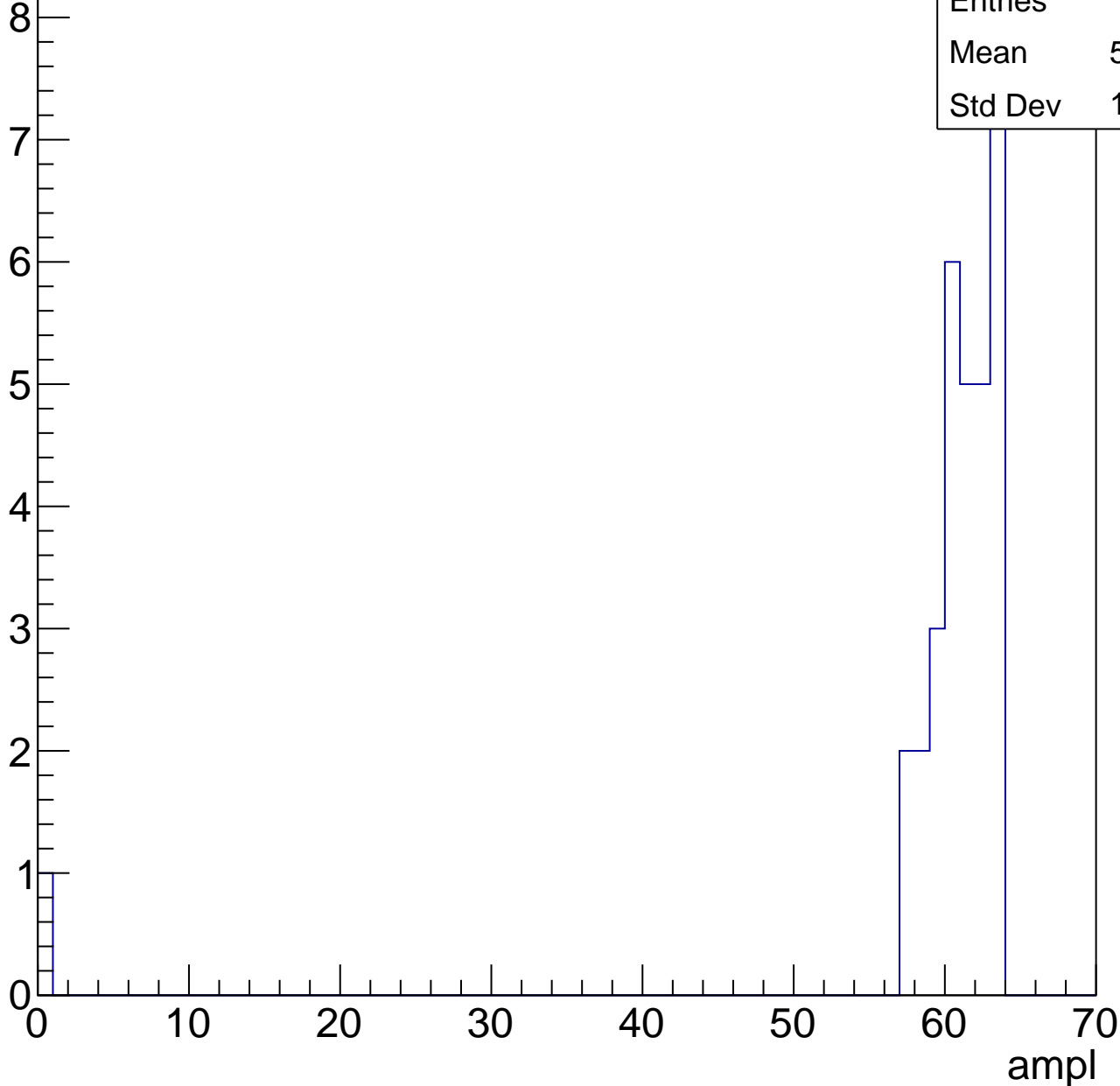


B1L101S, U25-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	58.94
Std Dev	10.74



B1L101S, U25-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch18, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	29.77
Std Dev	4.851

Gaus mean : 30.6986

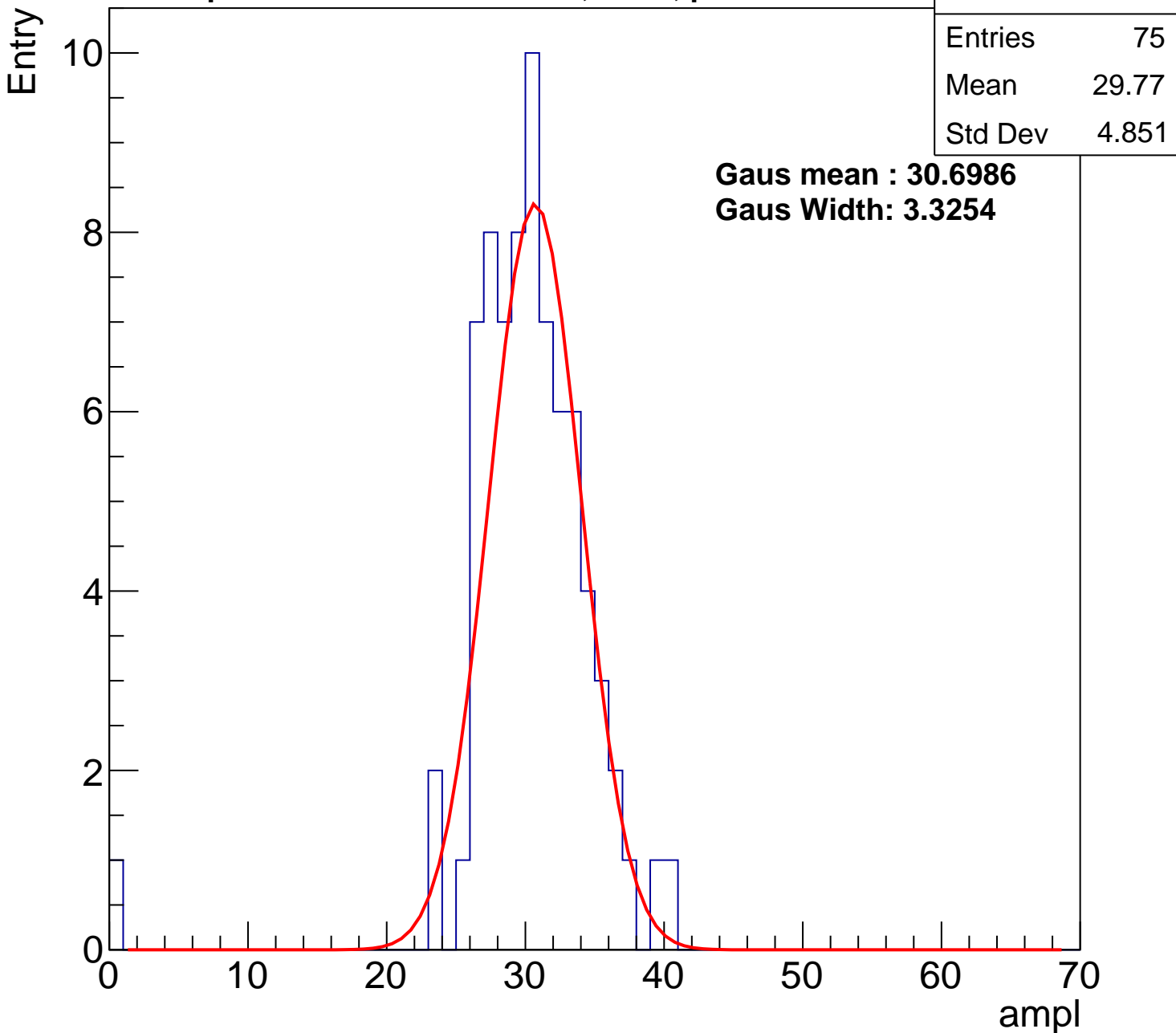
Gaus Width: 3.3254

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



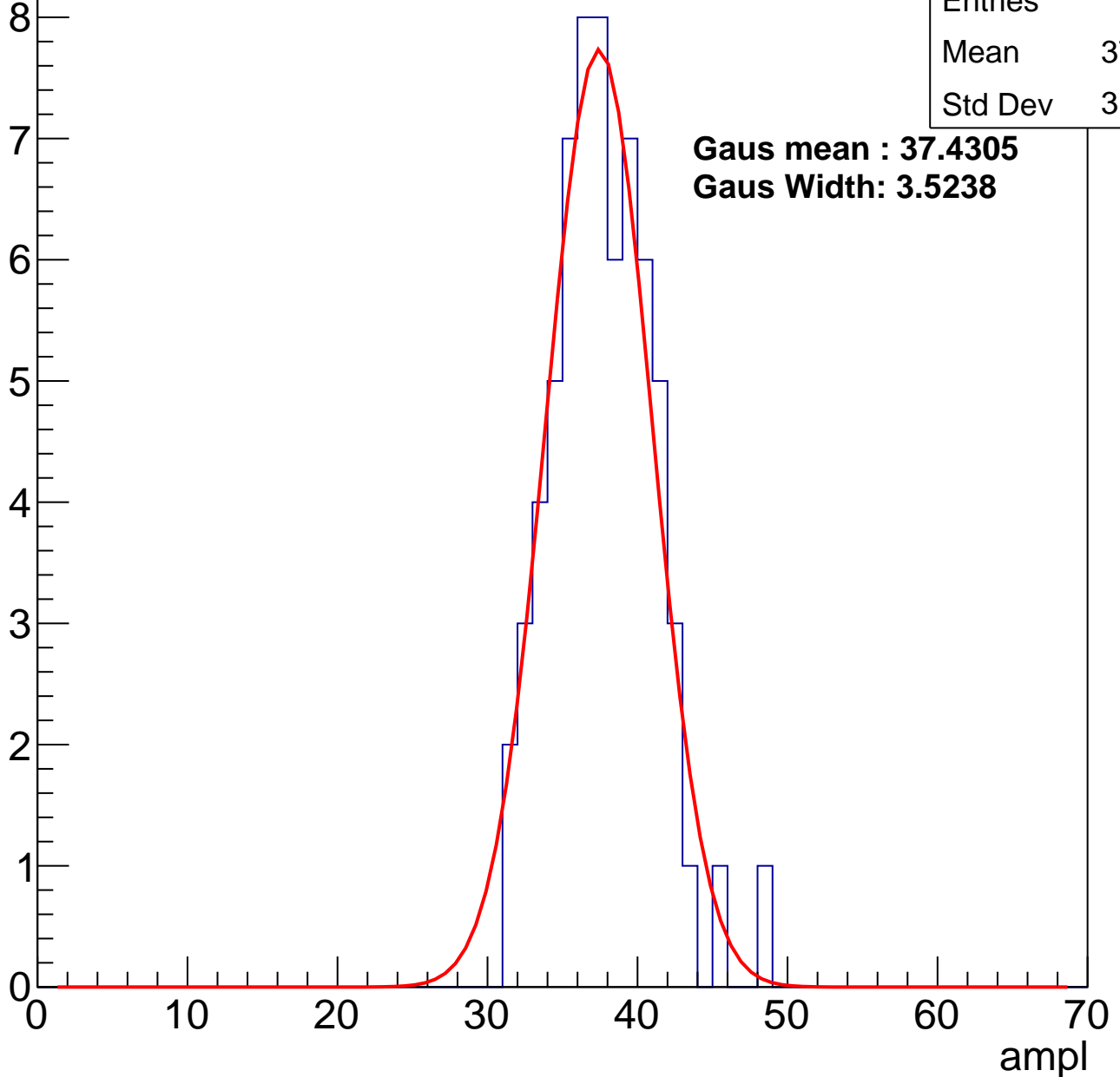
B1L101S, U25-ch18, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.27
Std Dev	3.348

Gaus mean : 37.4305
Gaus Width: 3.5238



B1L101S, U25-ch18, adc2

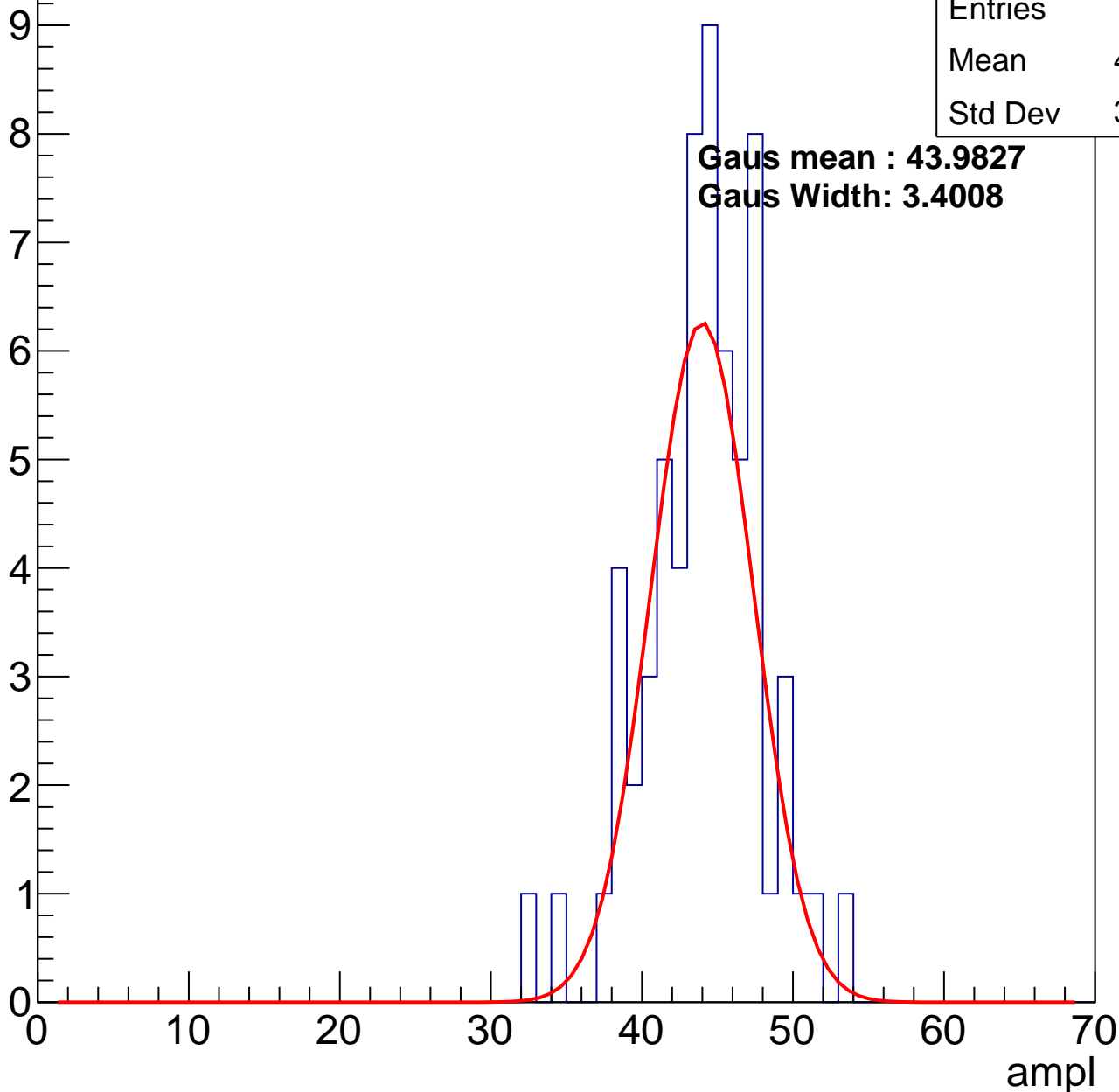
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.61
Std Dev	3.851

Gaus mean : 43.9827

Gaus Width: 3.4008

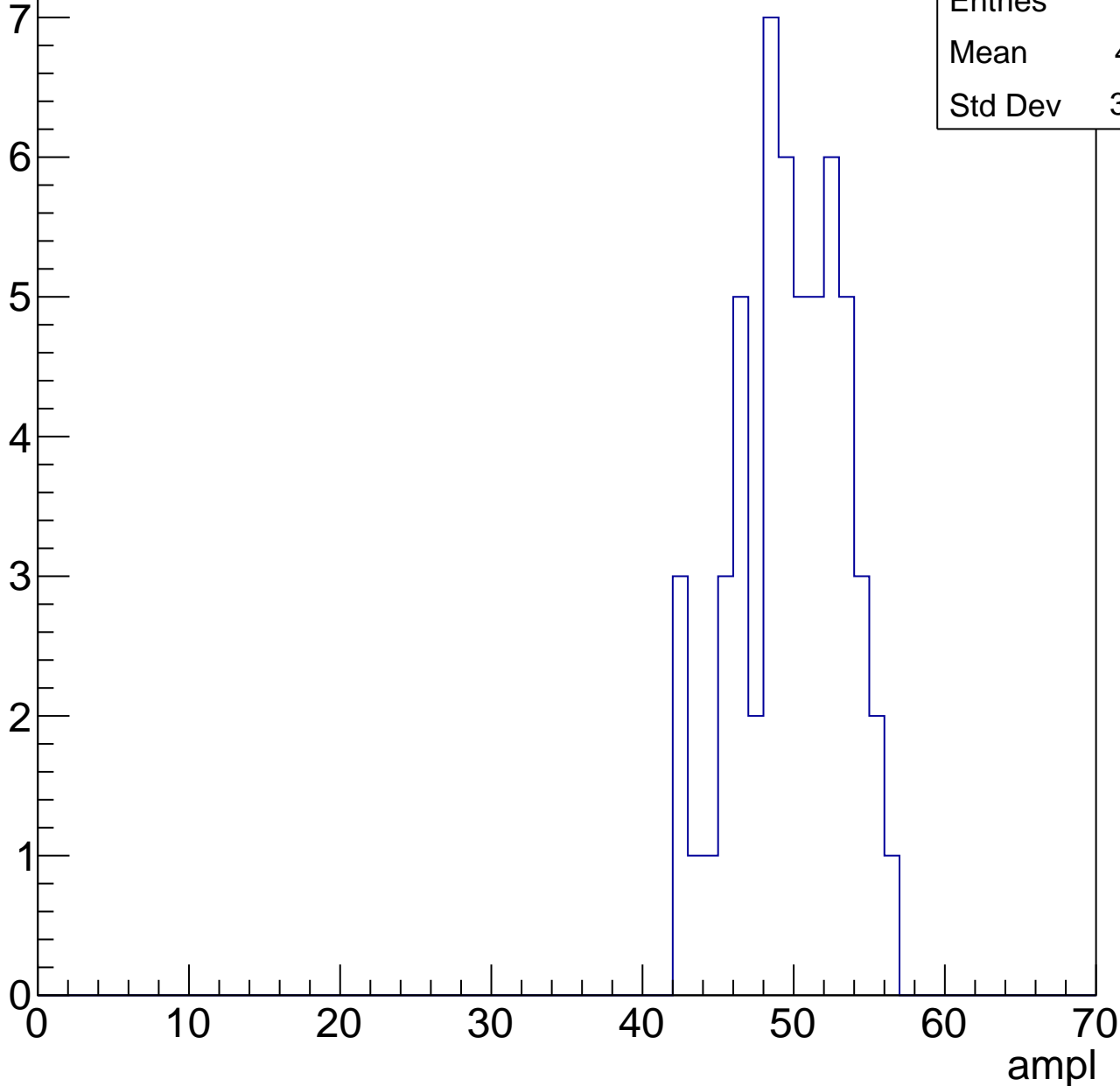


B1L101S, U25-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

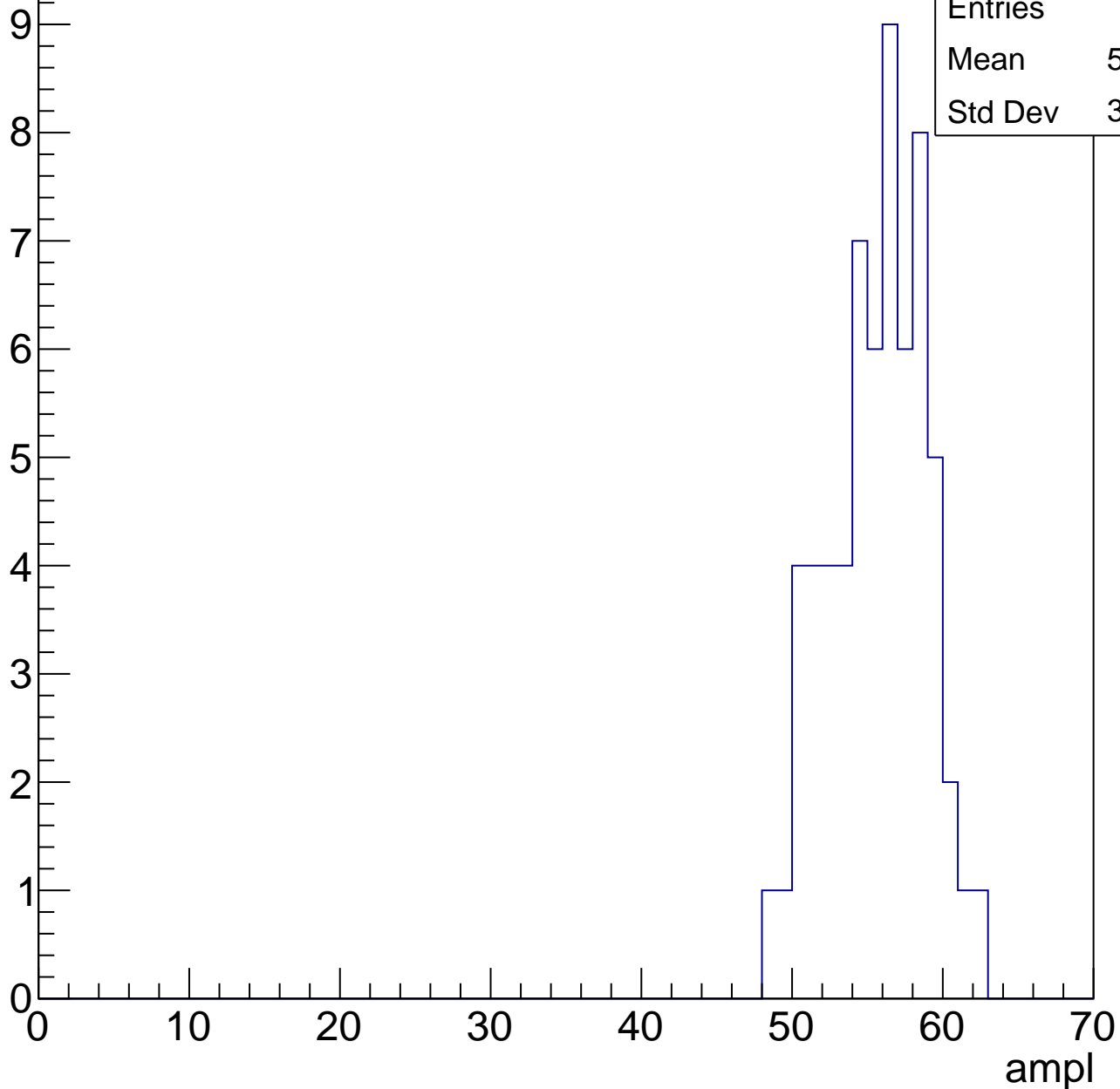
Entries	55
Mean	49.31
Std Dev	3.479



B1L101S, U25-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

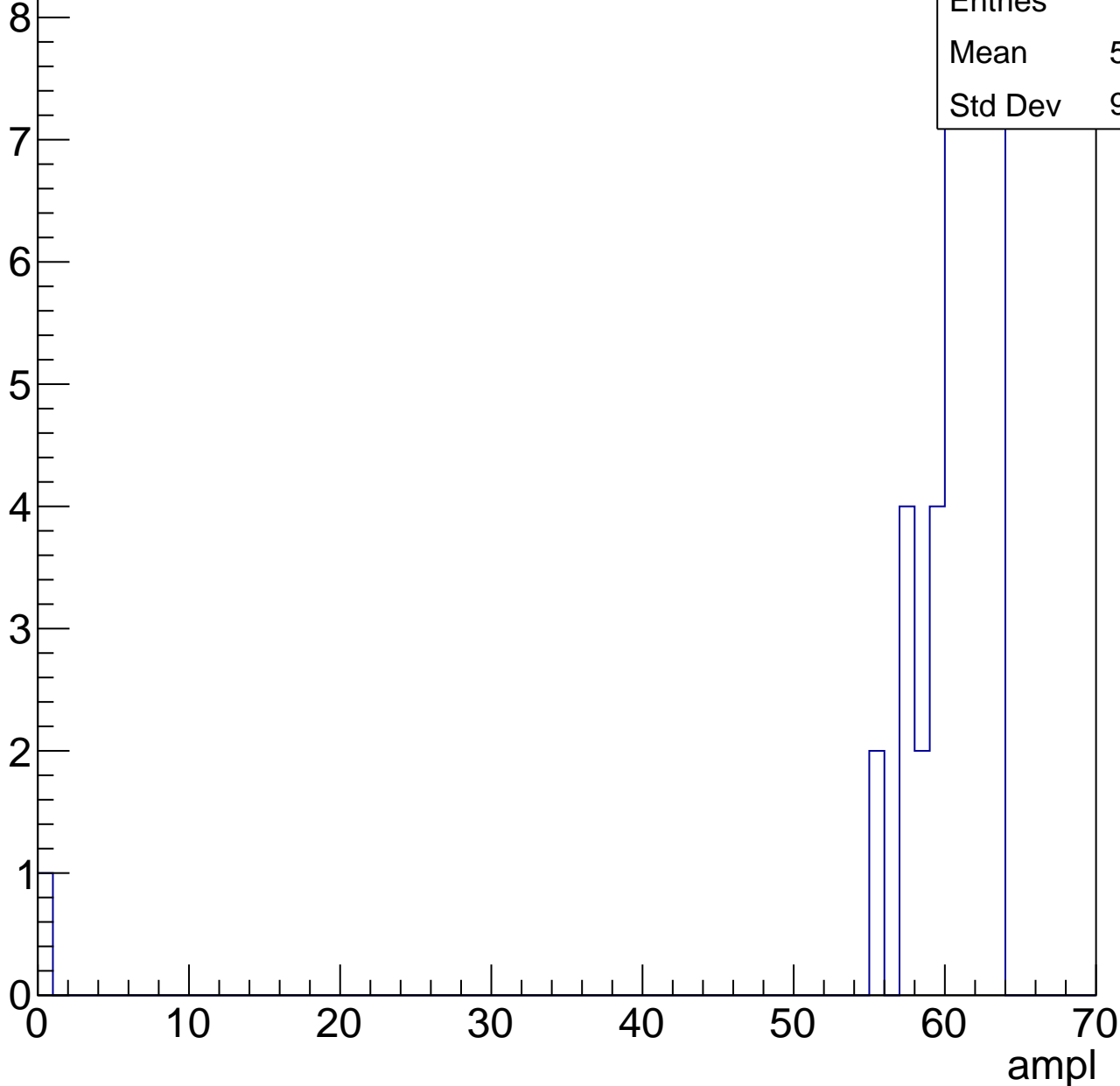


B1L101S, U25-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

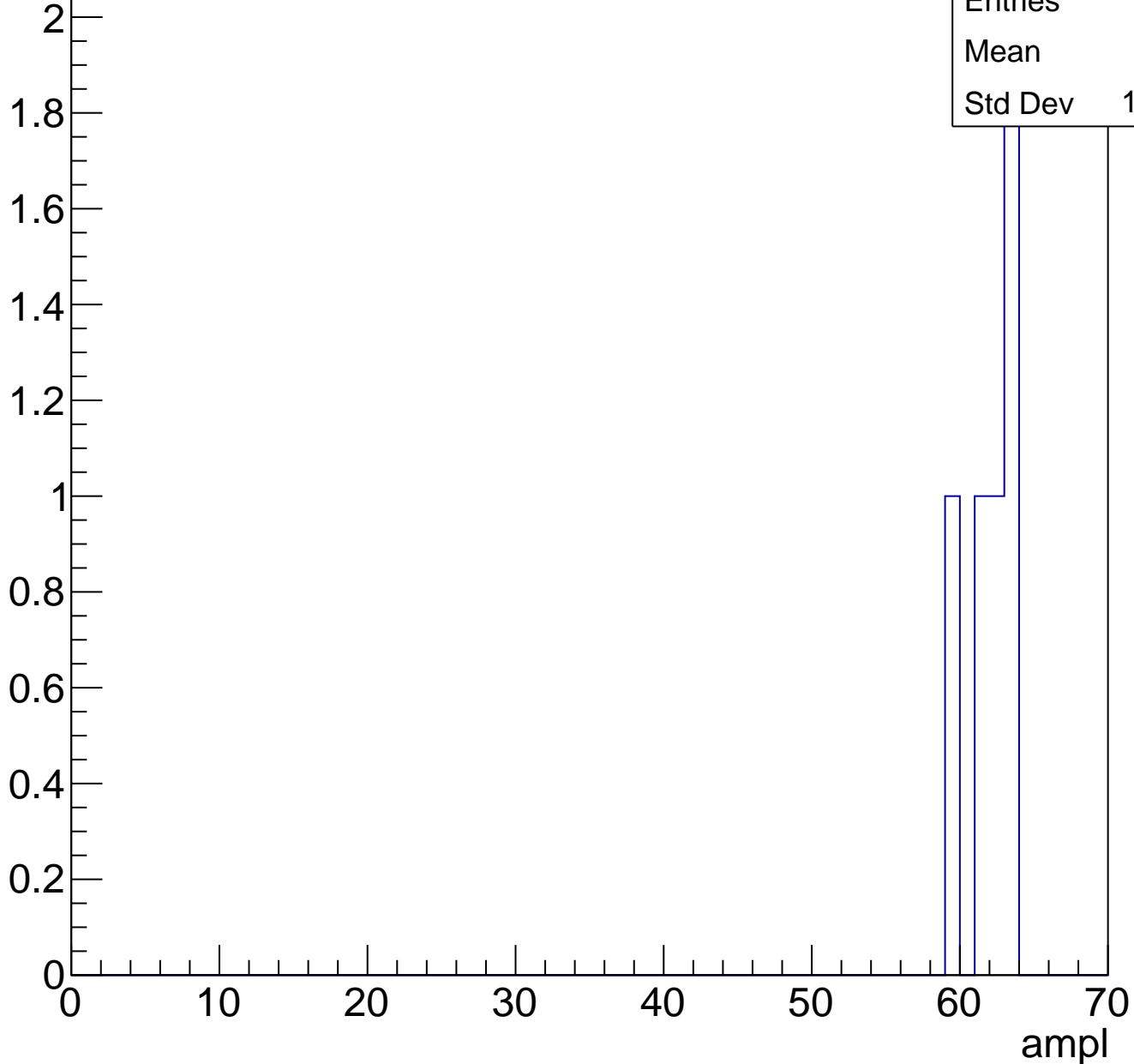
Entries	45
Mean	59.07
Std Dev	9.154



B1L101S, U25-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch19, adc0

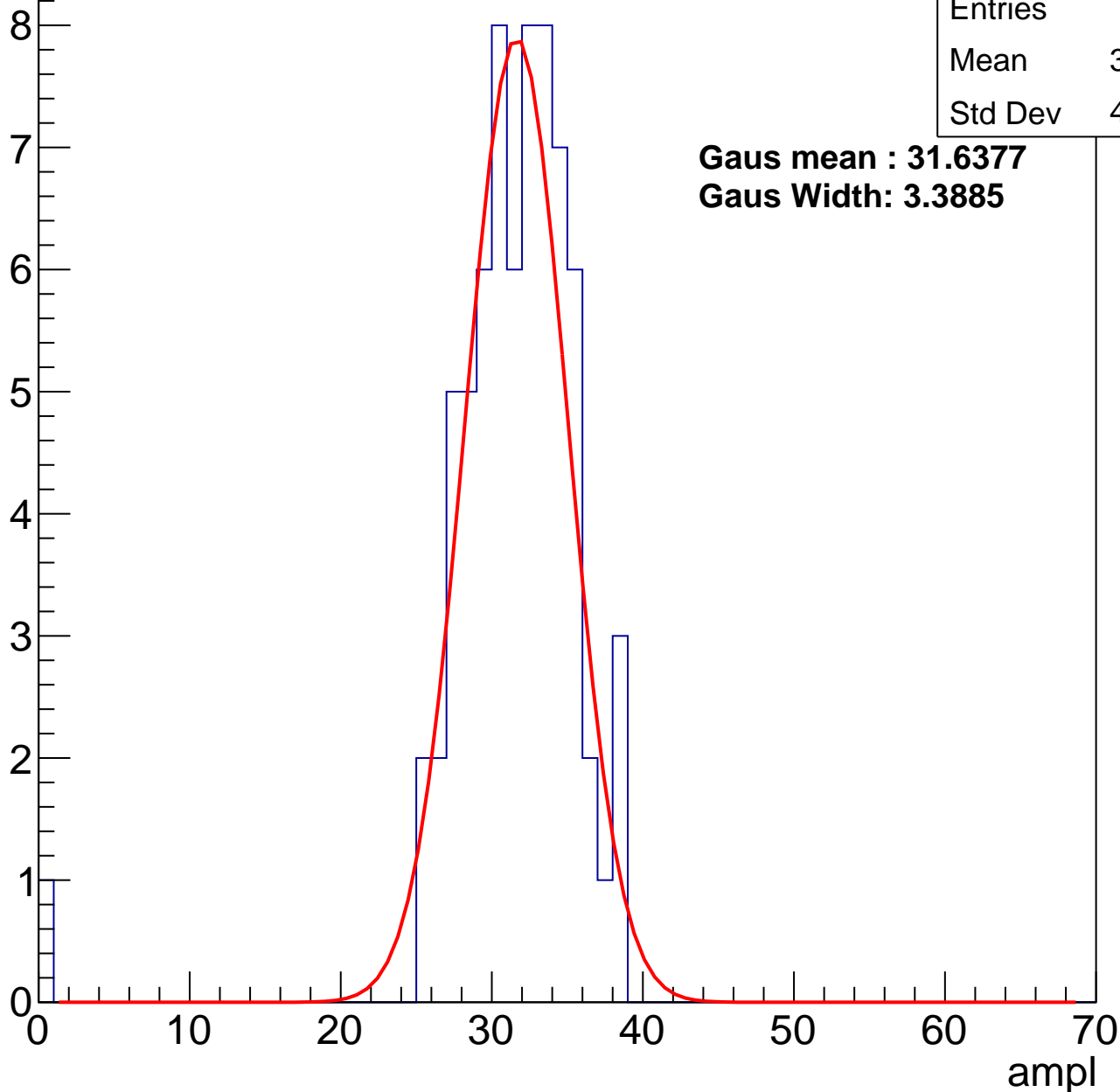
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.97
Std Dev	4.887

Gaus mean : 31.6377

Gaus Width: 3.3885



B1L101S, U25-ch19, adc1

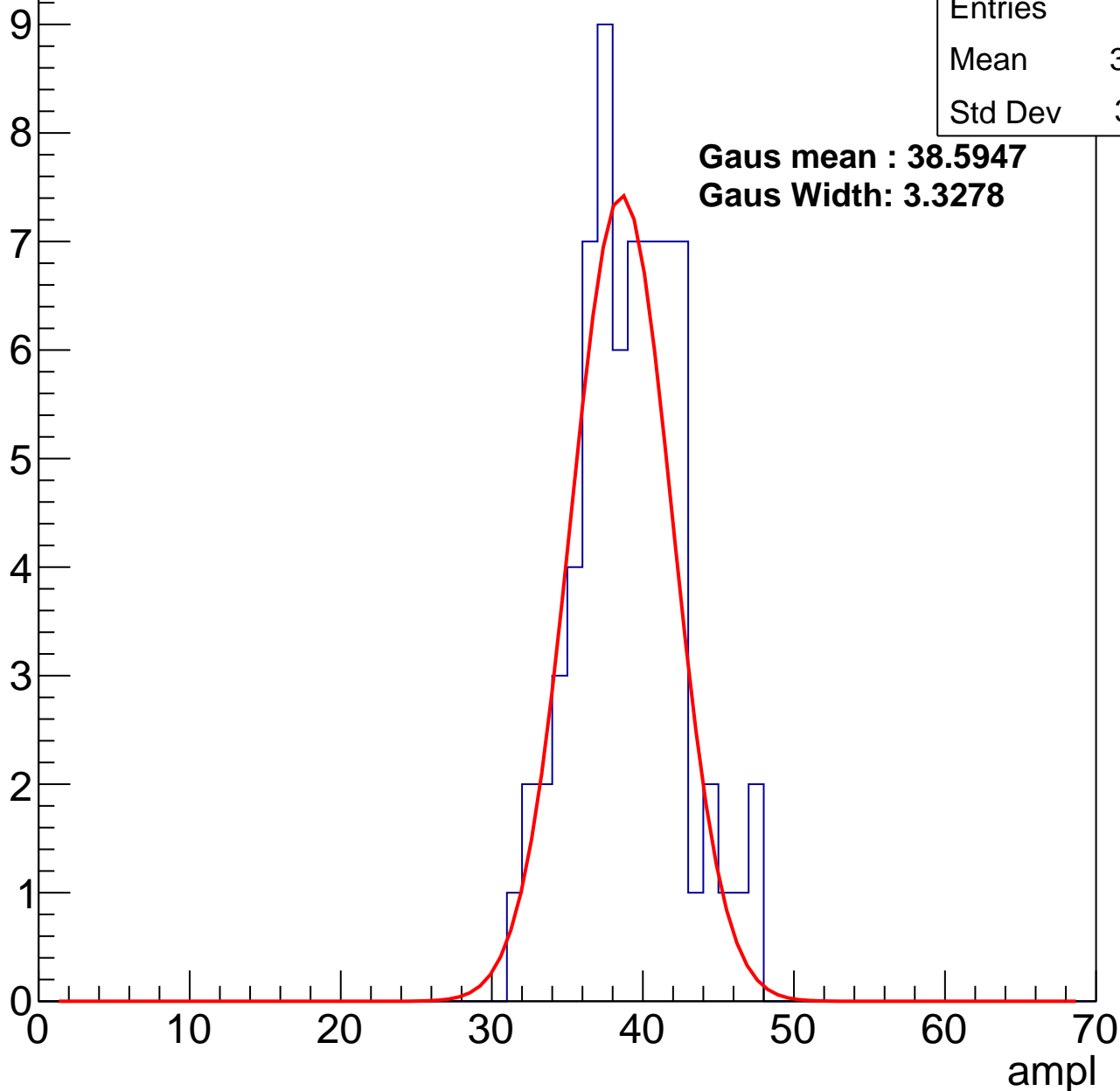
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.64
Std Dev	3.501

Gaus mean : 38.5947

Gaus Width: 3.3278



B1L101S, U25-ch19, adc2

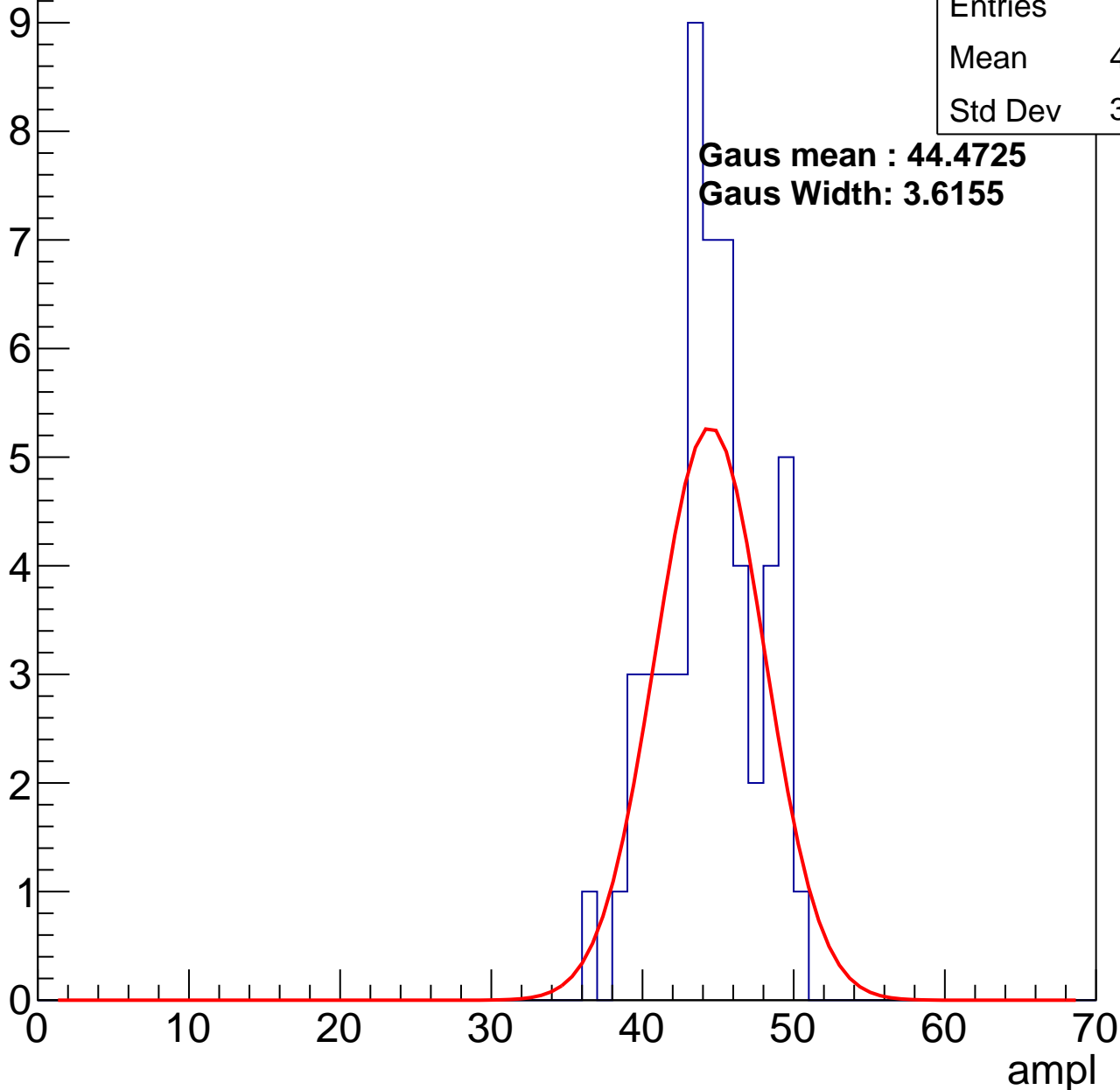
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	44.06
Std Dev	3.183

Gaus mean : 44.4725

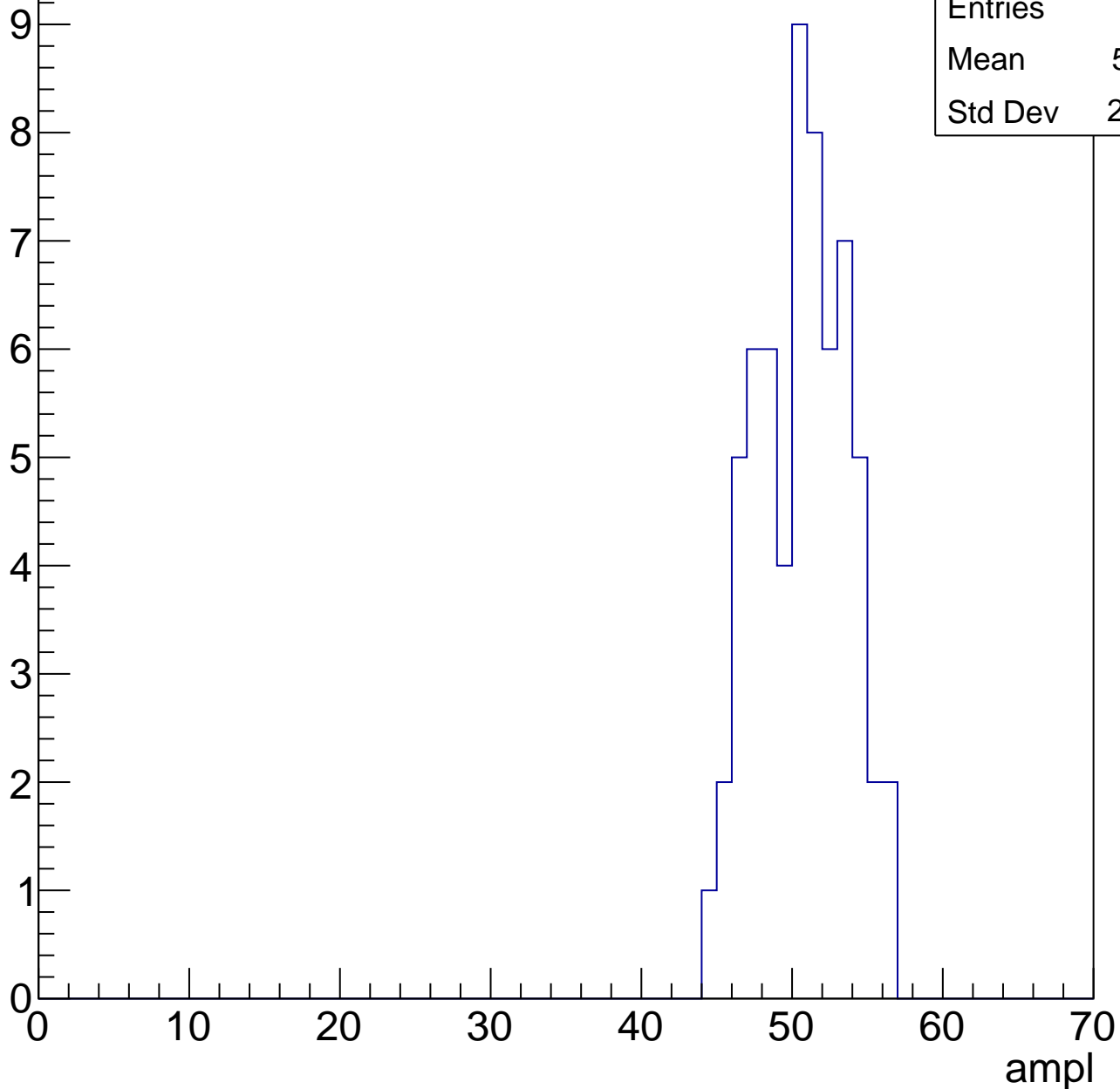
Gaus Width: 3.6155



B1L101S, U25-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

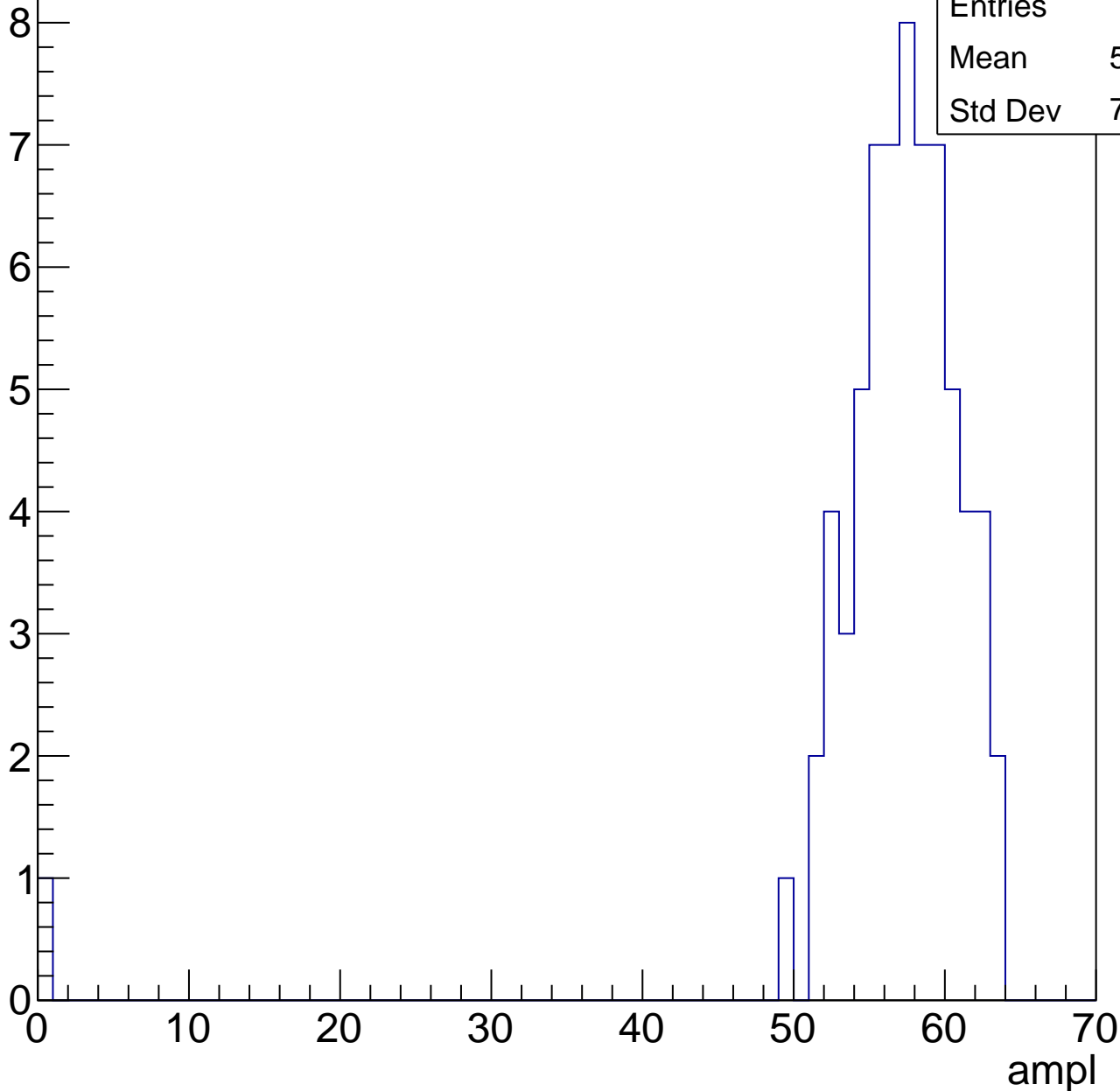


B1L101S, U25-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	56.09
Std Dev	7.603



B1L101S, U25-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

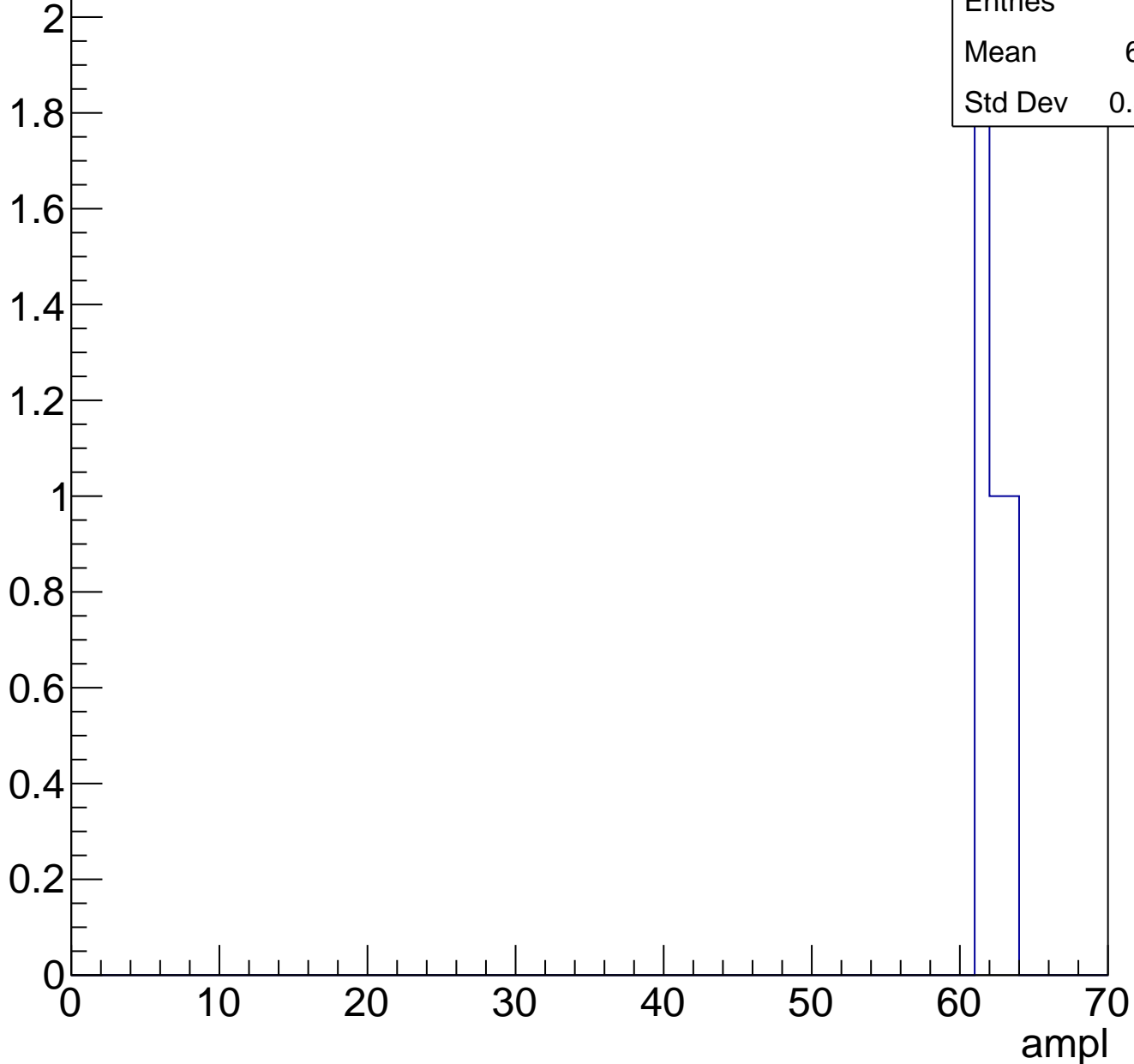
Entries	31
Mean	60.35
Std Dev	2.251

ampl

B1L101S, U25-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch20, adc0

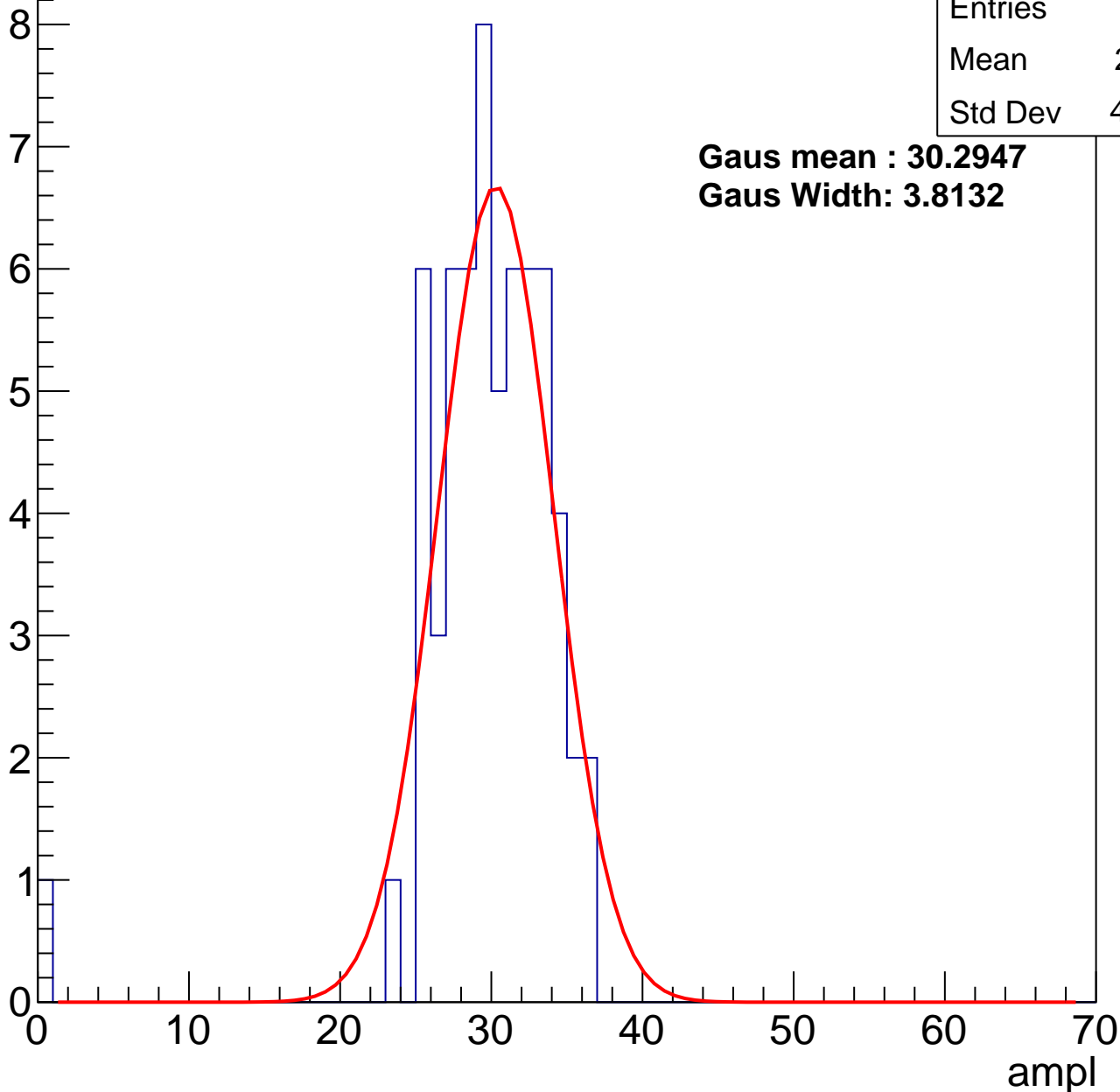
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.31
Std Dev	4.875

Gaus mean : 30.2947

Gaus Width: 3.8132



B1L101S, U25-ch20, adc1

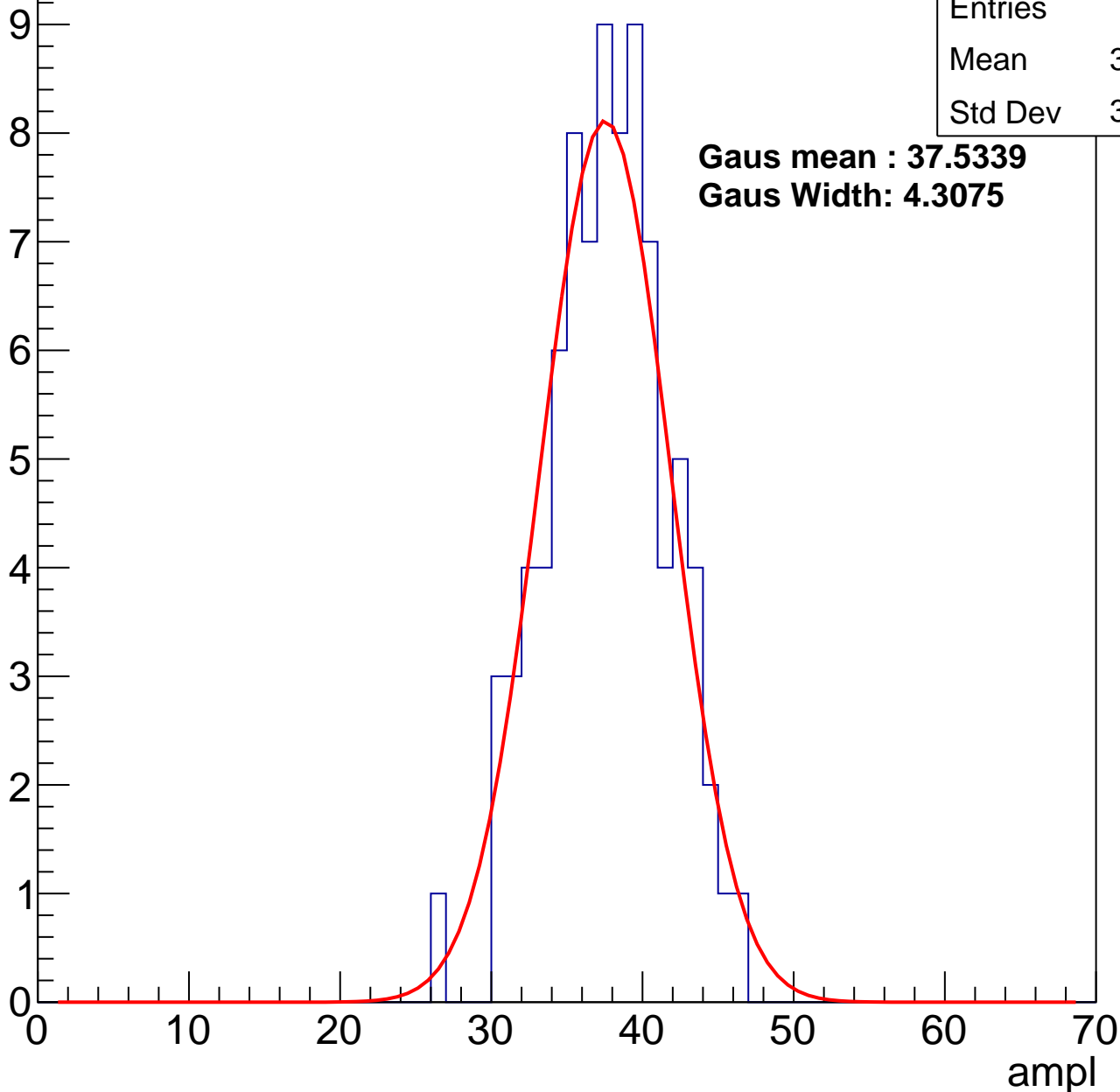
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	37.19
Std Dev	3.907

Gaus mean : 37.5339

Gaus Width: 4.3075



B1L101S, U25-ch20, adc2

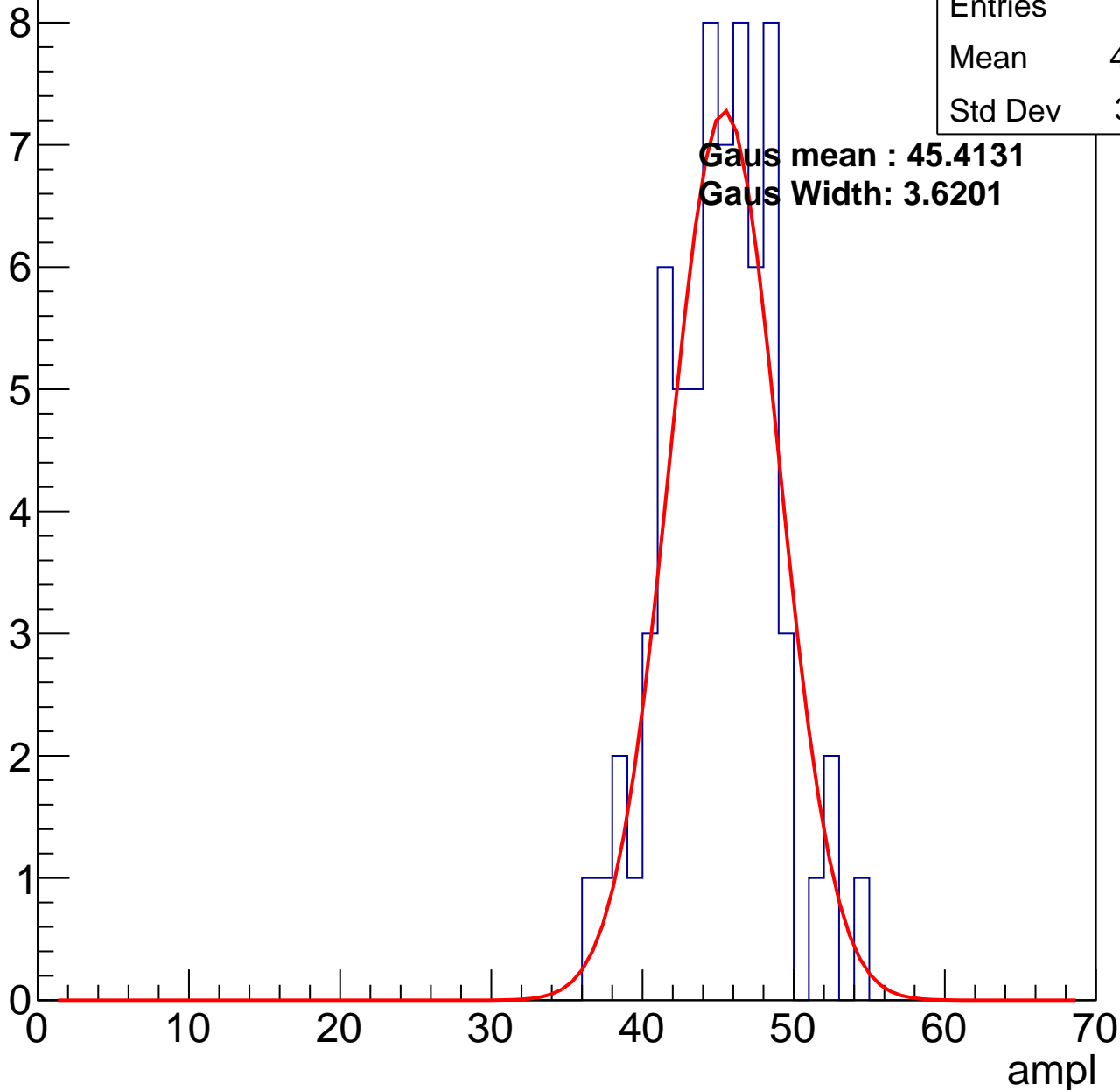
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.65
Std Dev	3.601

Gaus mean : 45.4131

Gaus Width: 3.6201

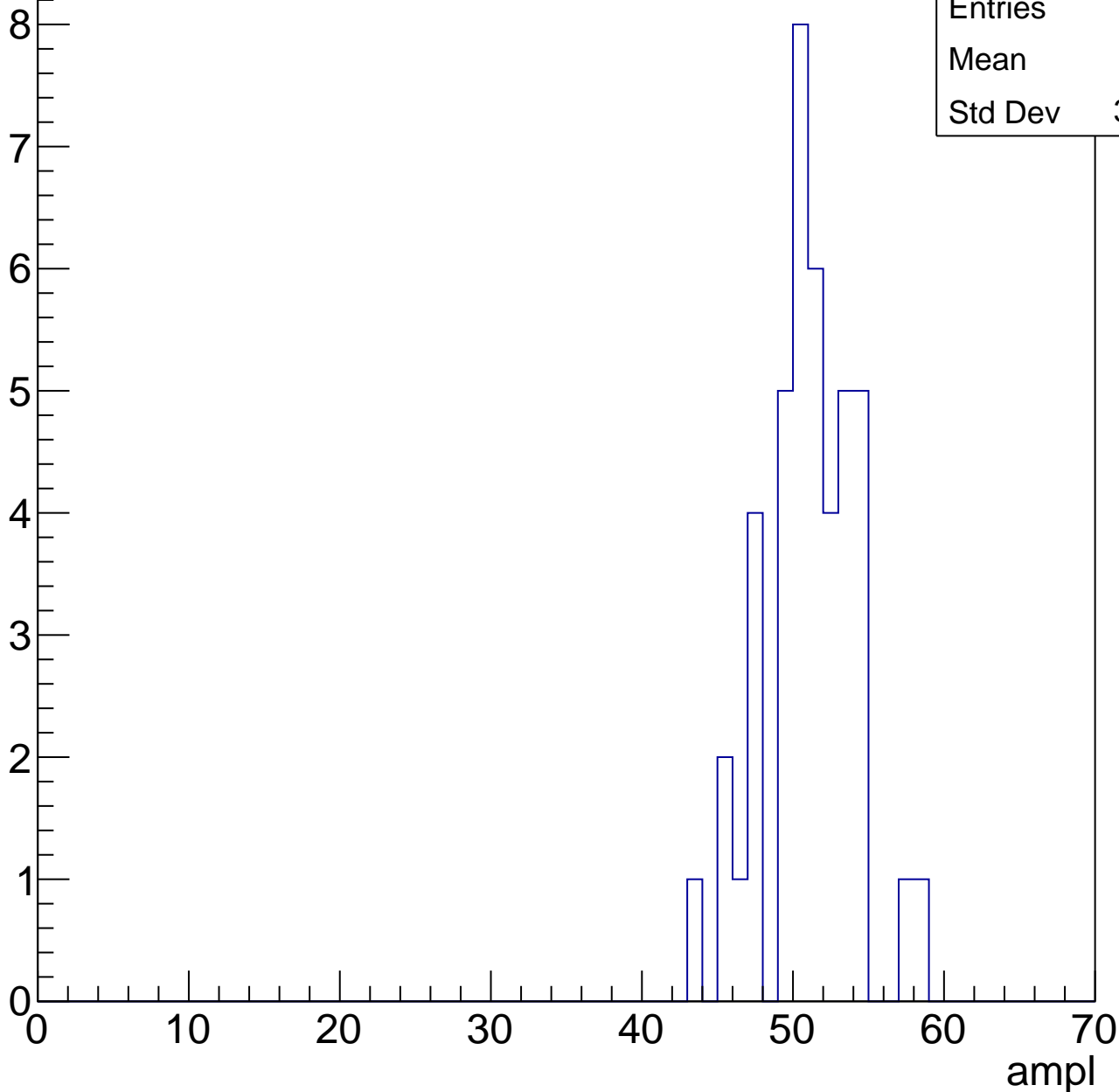


B1L101S, U25-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

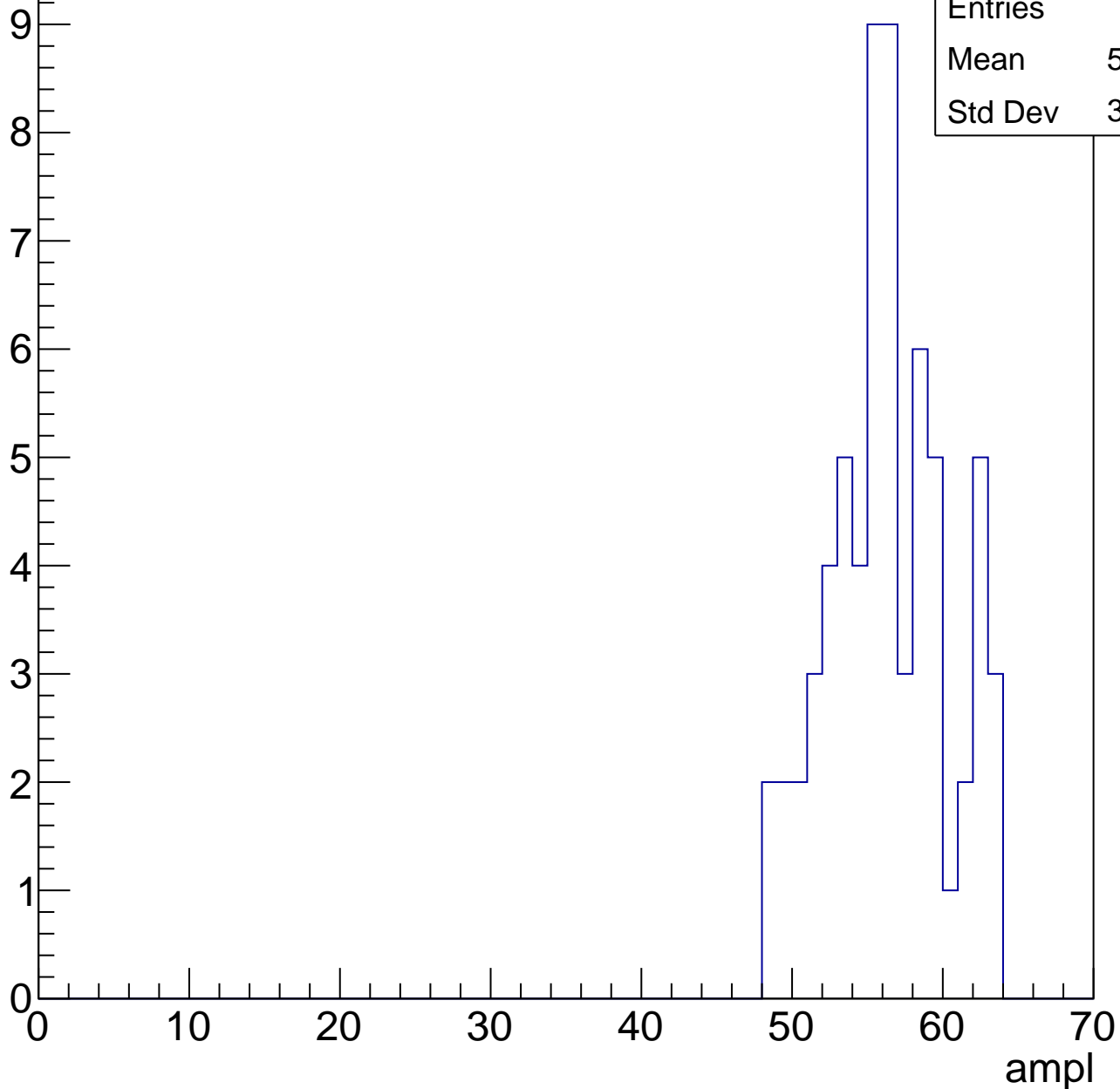
Entries	43
Mean	50.6
Std Dev	3.051



B1L101S, U25-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

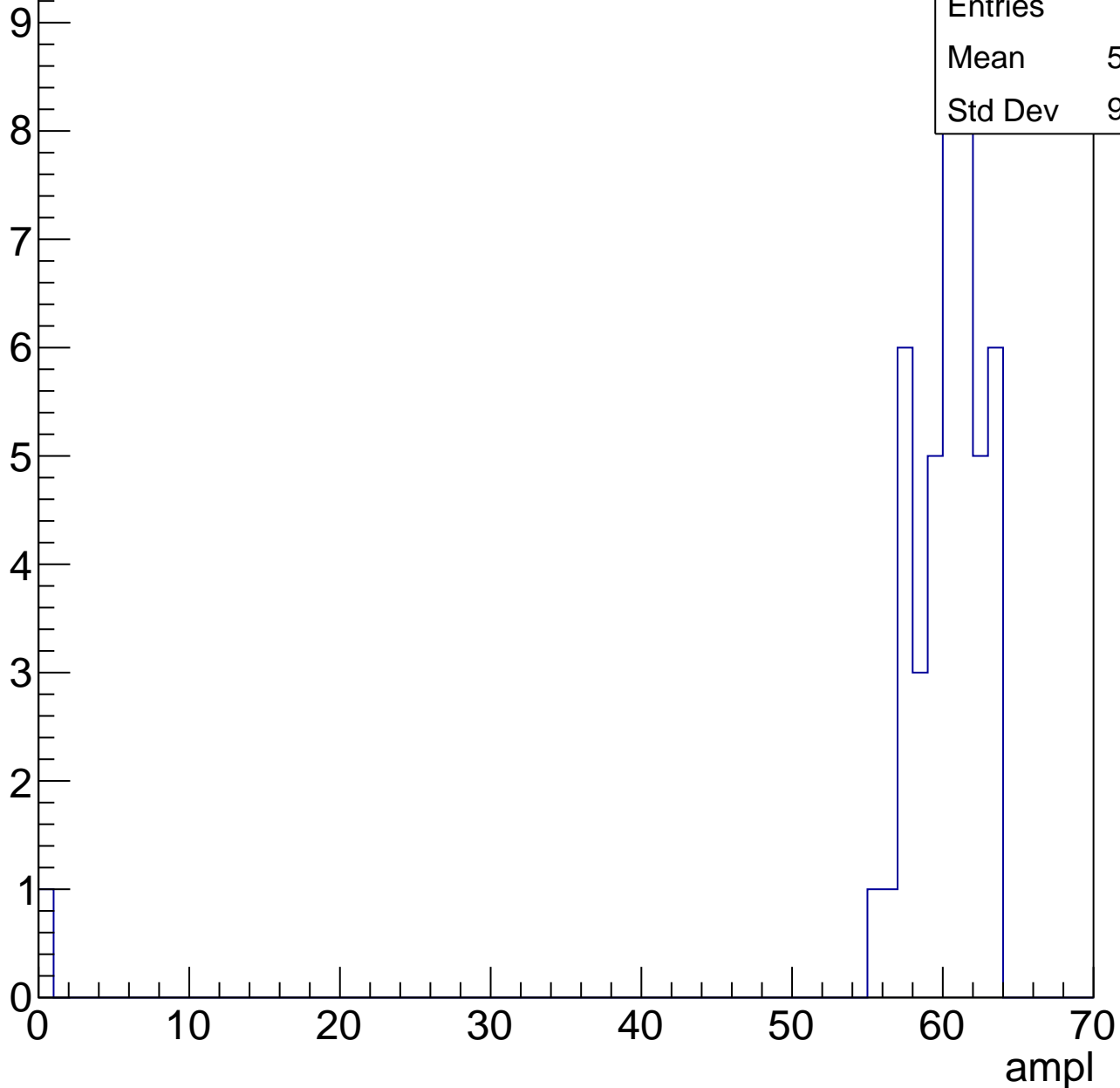


Entries	65
Mean	55.85
Std Dev	3.864

B1L101S, U25-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

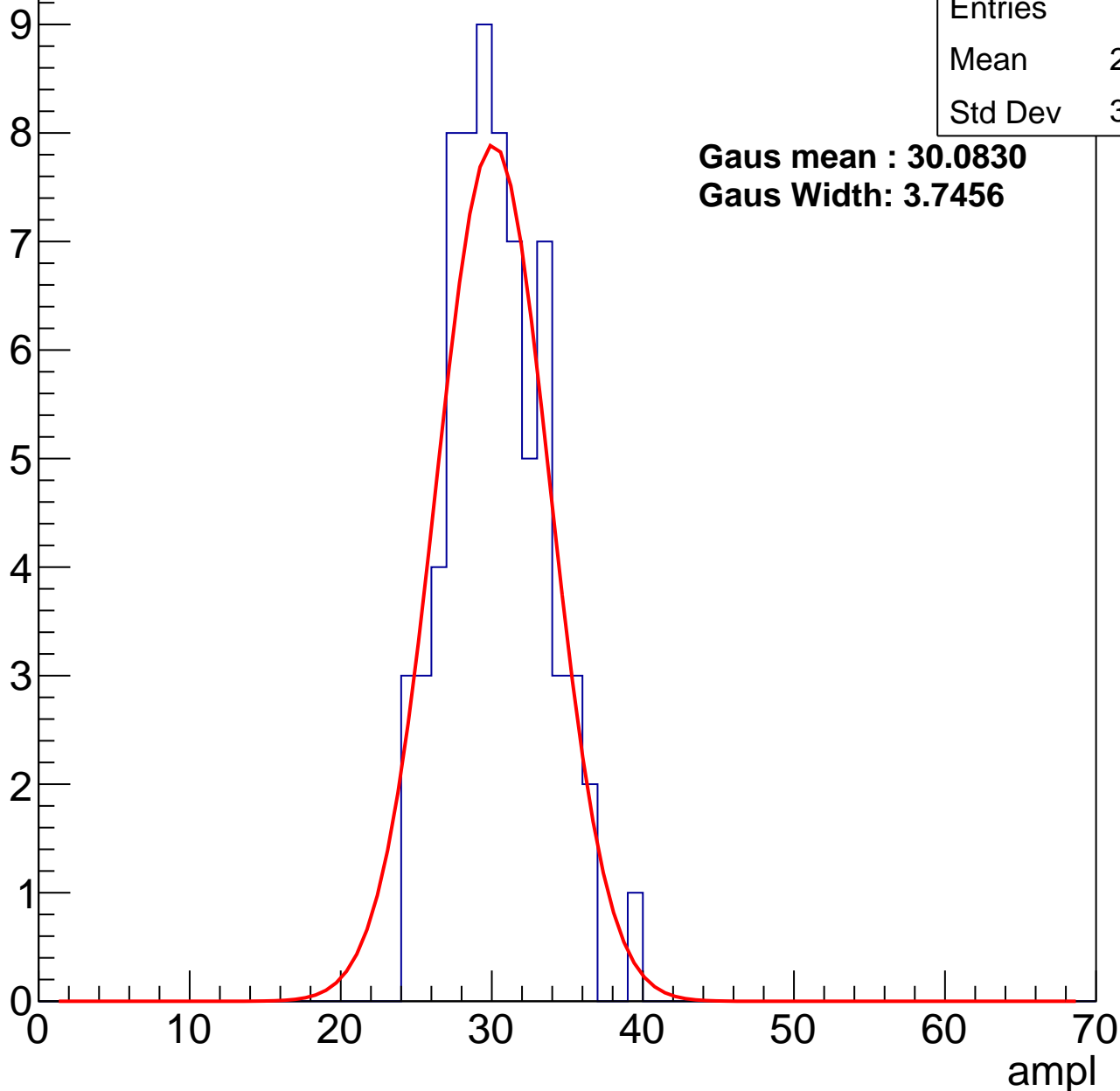
Entry



B1L101S, U25-ch21, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	71
Mean	29.83
Std Dev	3.193

B1L101S, U25-ch21, adc1

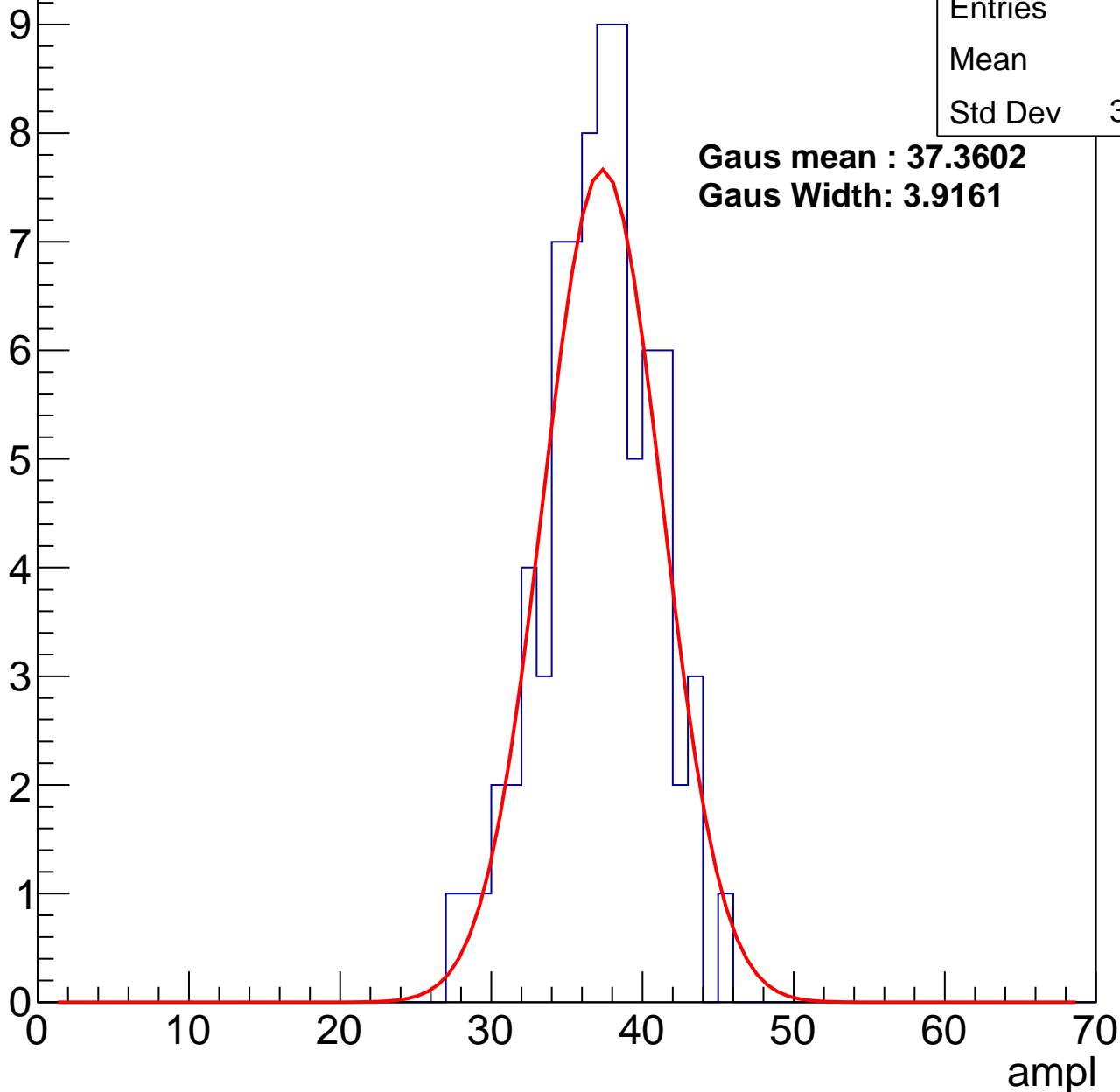
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.6
Std Dev	3.694

Gaus mean : 37.3602

Gaus Width: 3.9161



B1L101S, U25-ch21, adc2

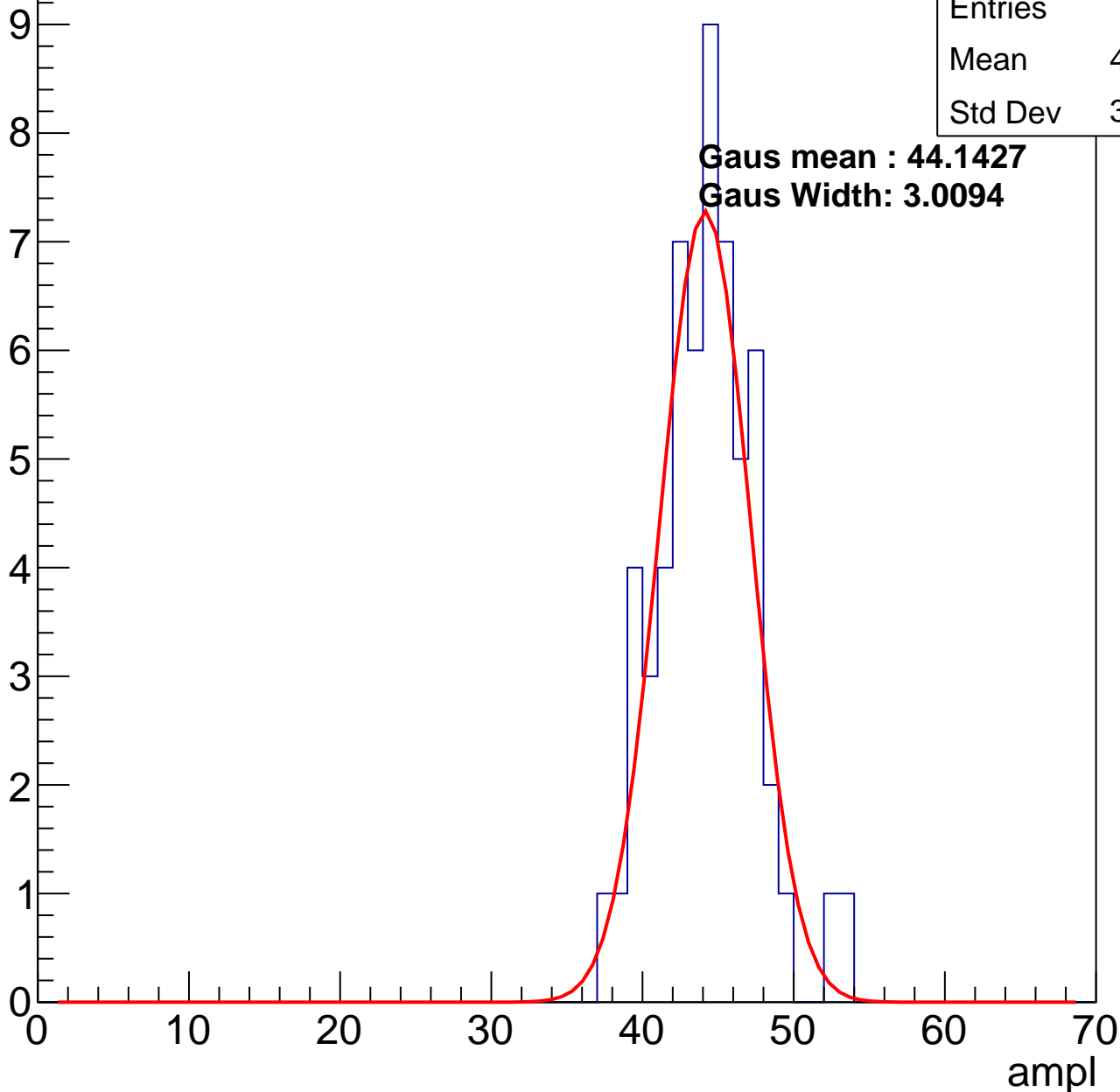
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.79
Std Dev	3.172

Gaus mean : 44.1427

Gaus Width: 3.0094

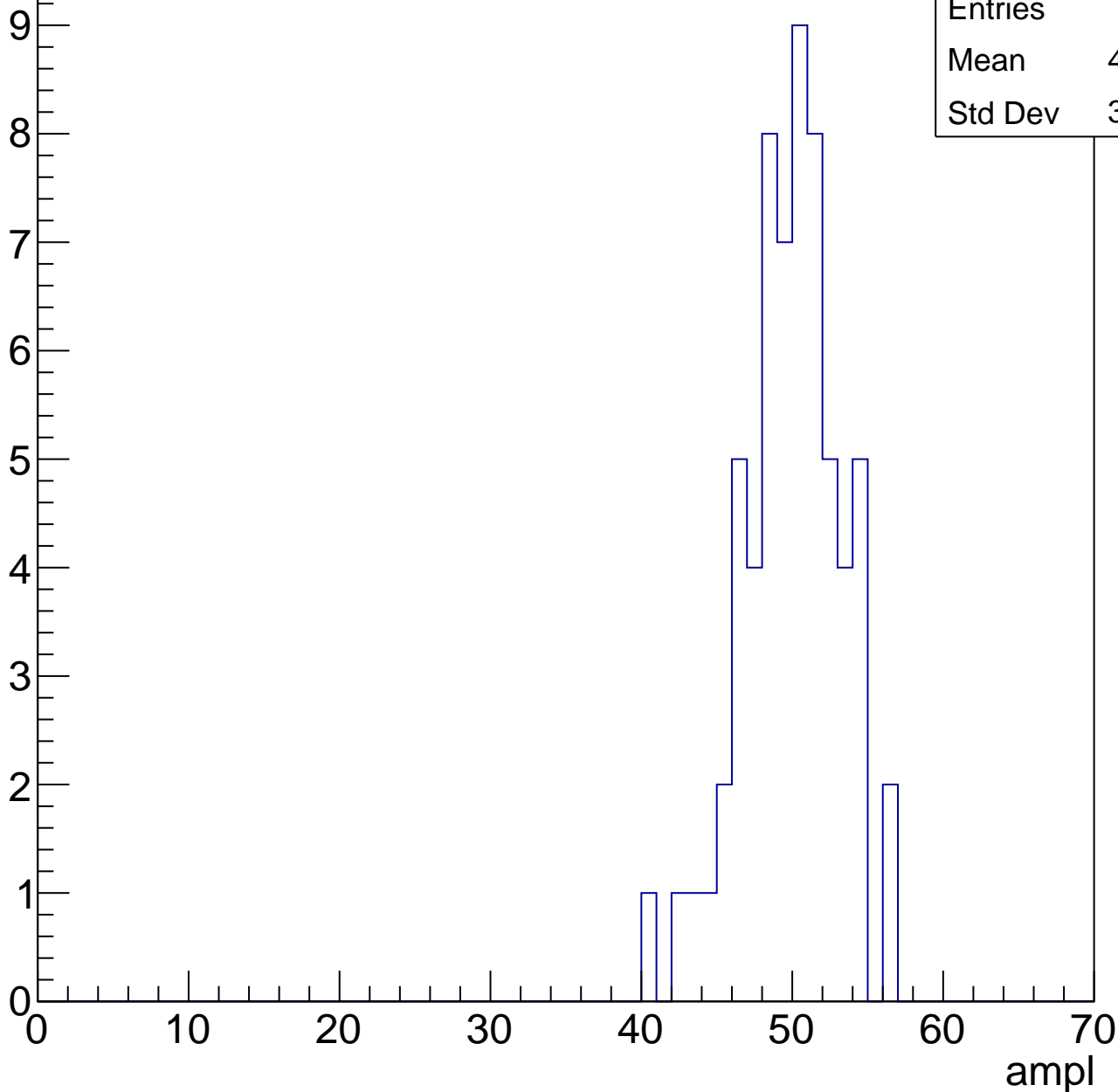


B1L101S, U25-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

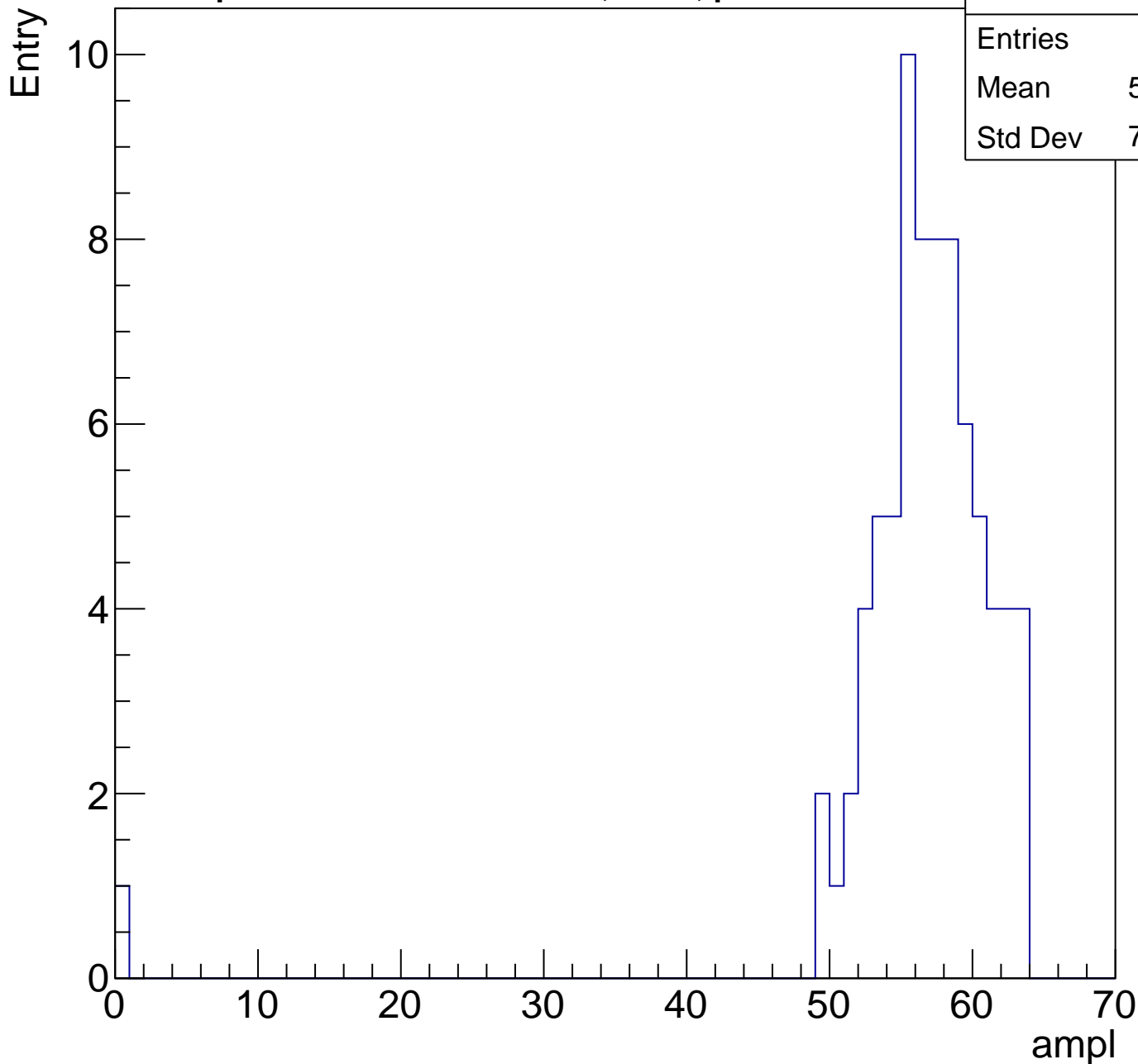
Entries	63
Mean	49.46
Std Dev	3.216



B1L101S, U25-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

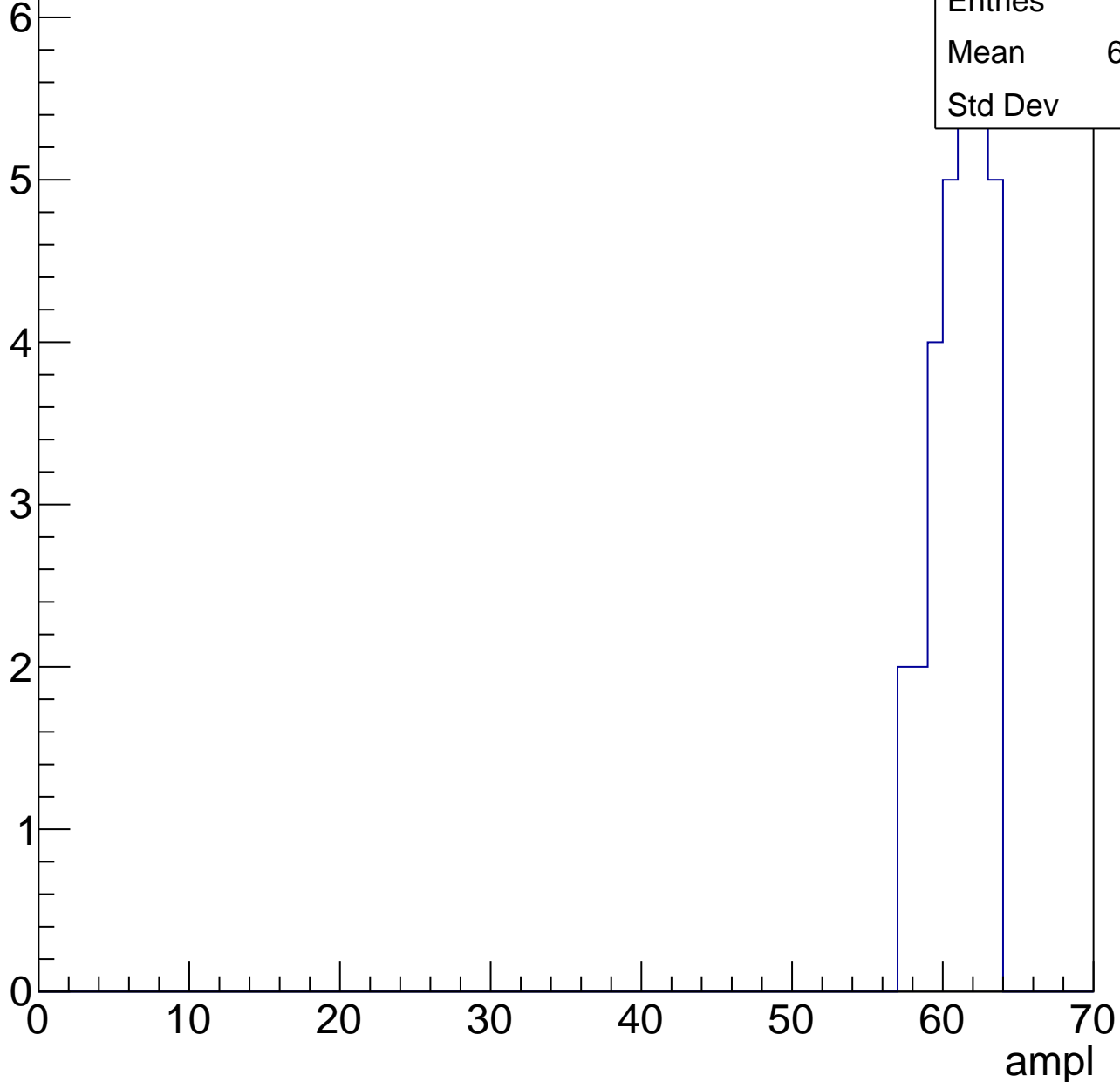
Entries	77
Mean	55.96
Std Dev	7.273



B1L101S, U25-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	30
Mean	60.63
Std Dev	1.76

B1L101S, U25-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L101S, U25-ch22, adc0

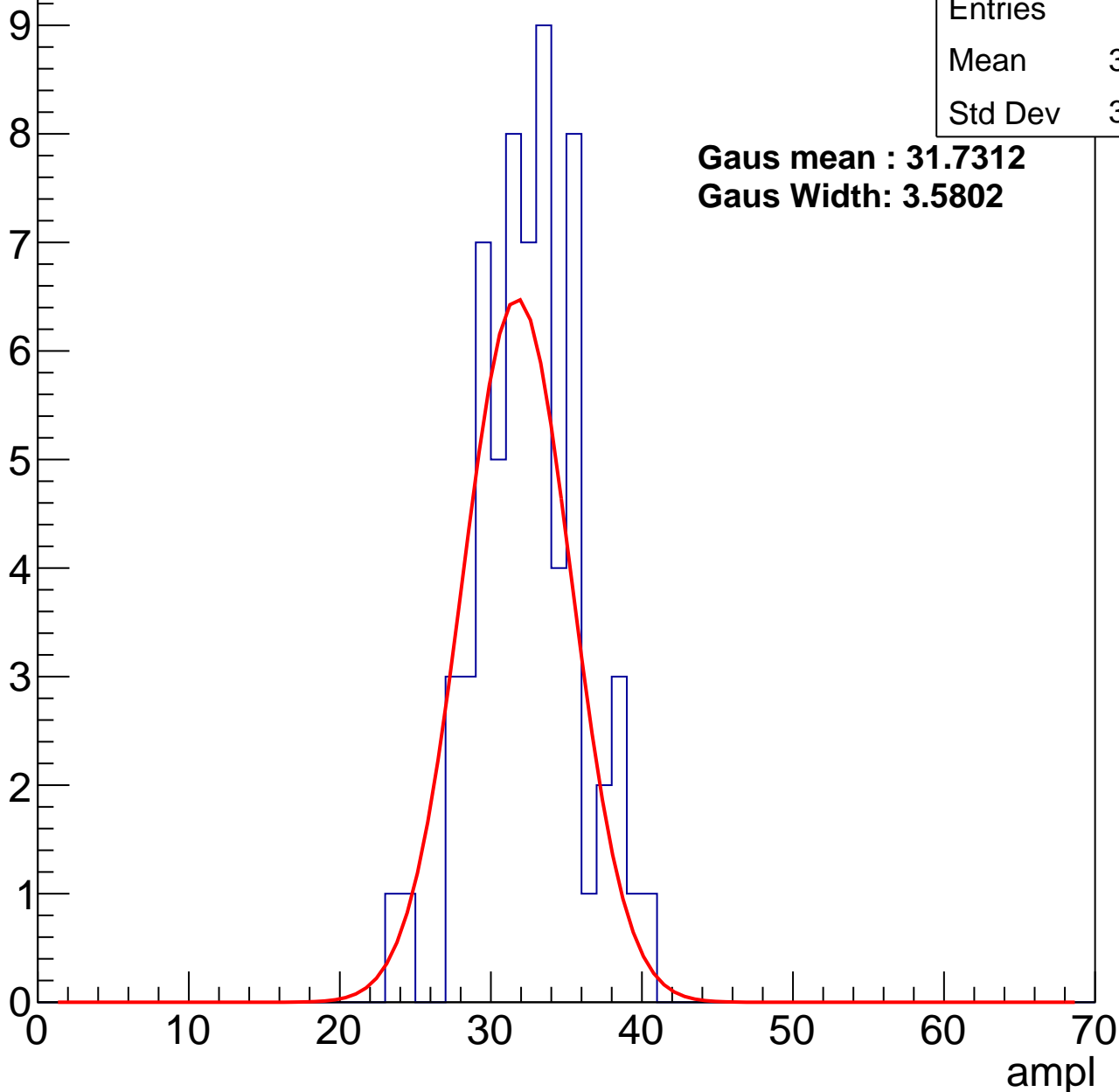
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	32.08
Std Dev	3.406

Gaus mean : 31.7312

Gaus Width: 3.5802



B1L101S, U25-ch22, adc1

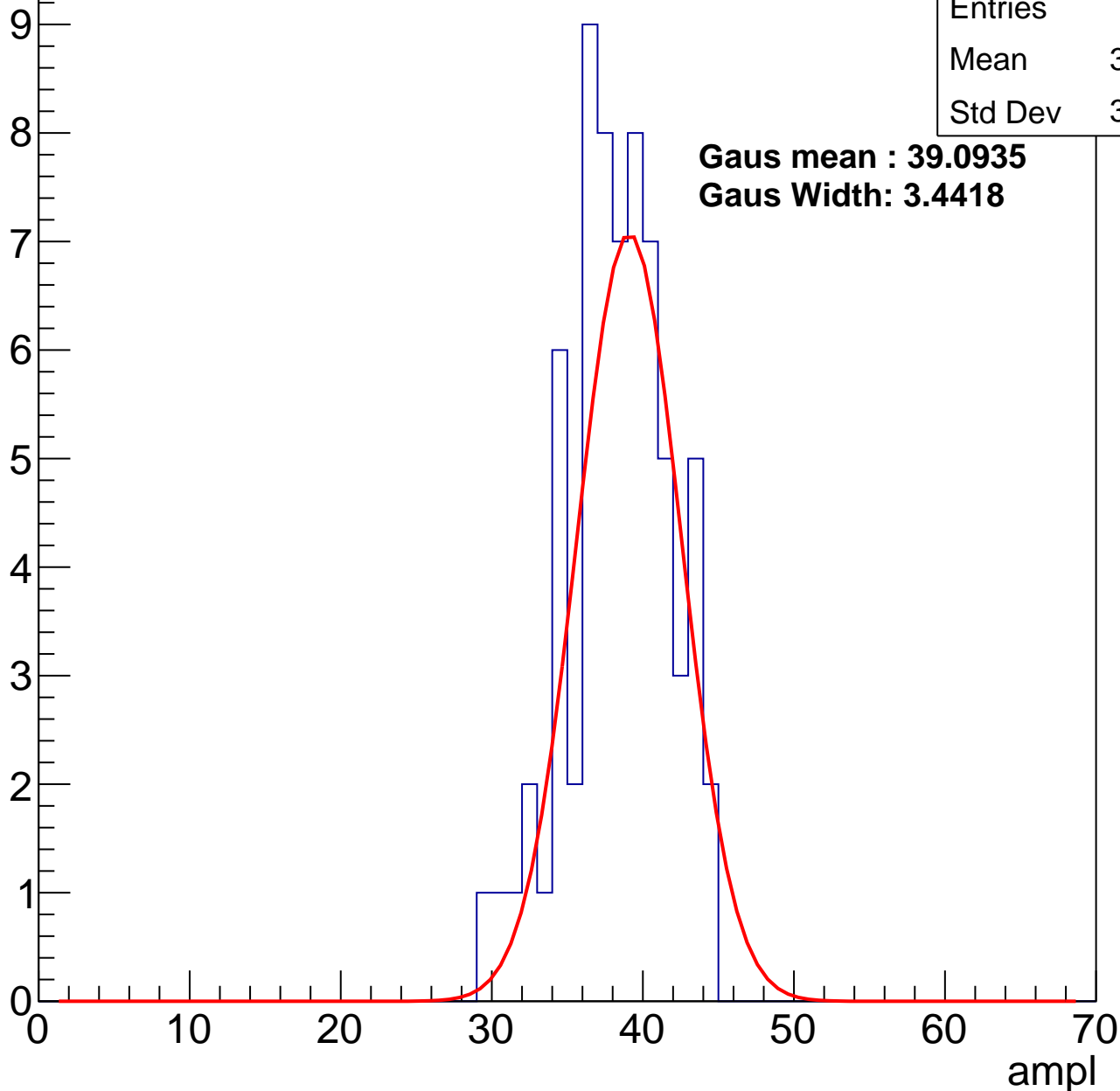
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.84
Std Dev	3.372

Gaus mean : 39.0935

Gaus Width: 3.4418



B1L101S, U25-ch22, adc2

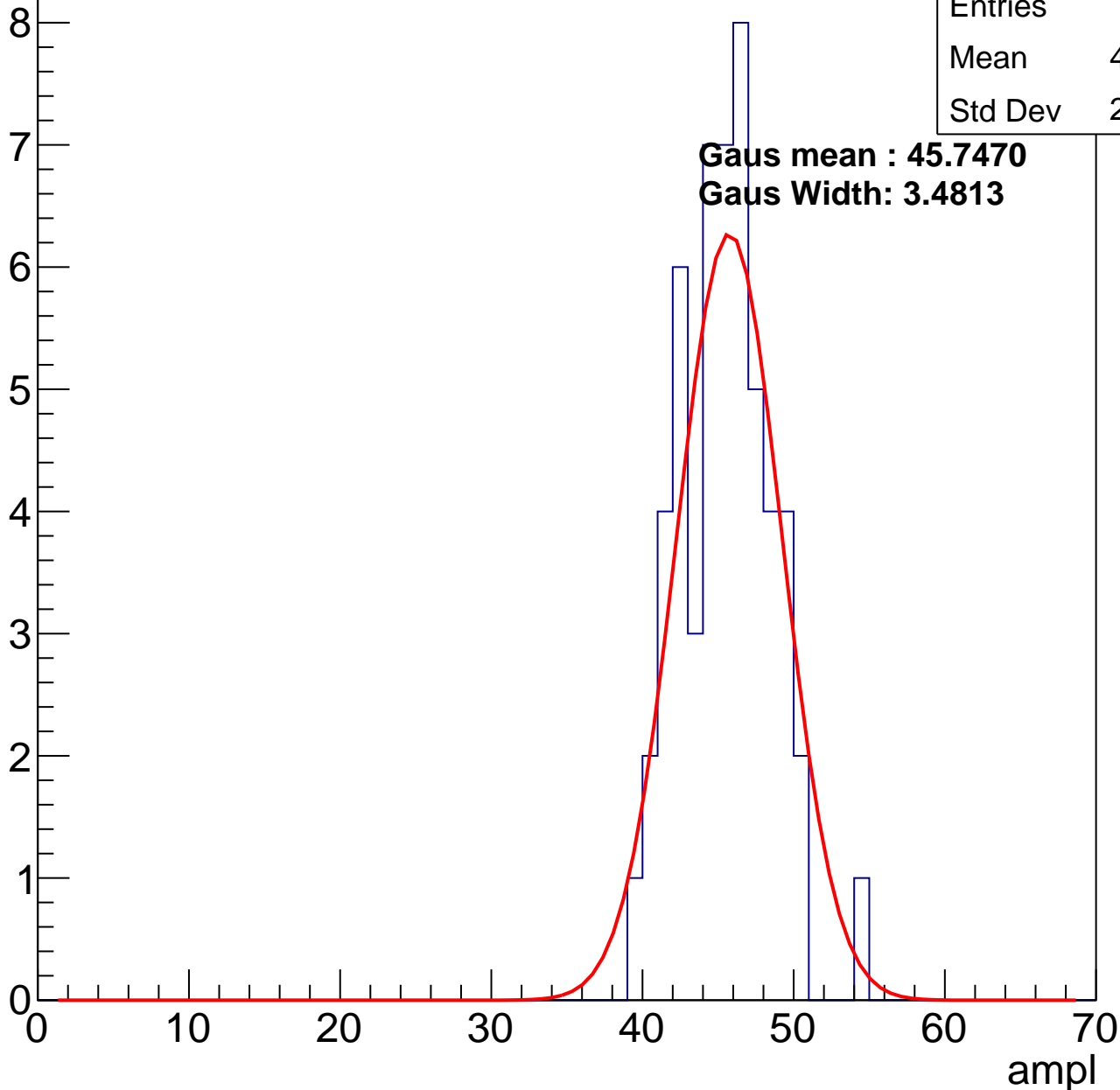
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	45.04
Std Dev	2.987

Gaus mean : 45.7470

Gaus Width: 3.4813

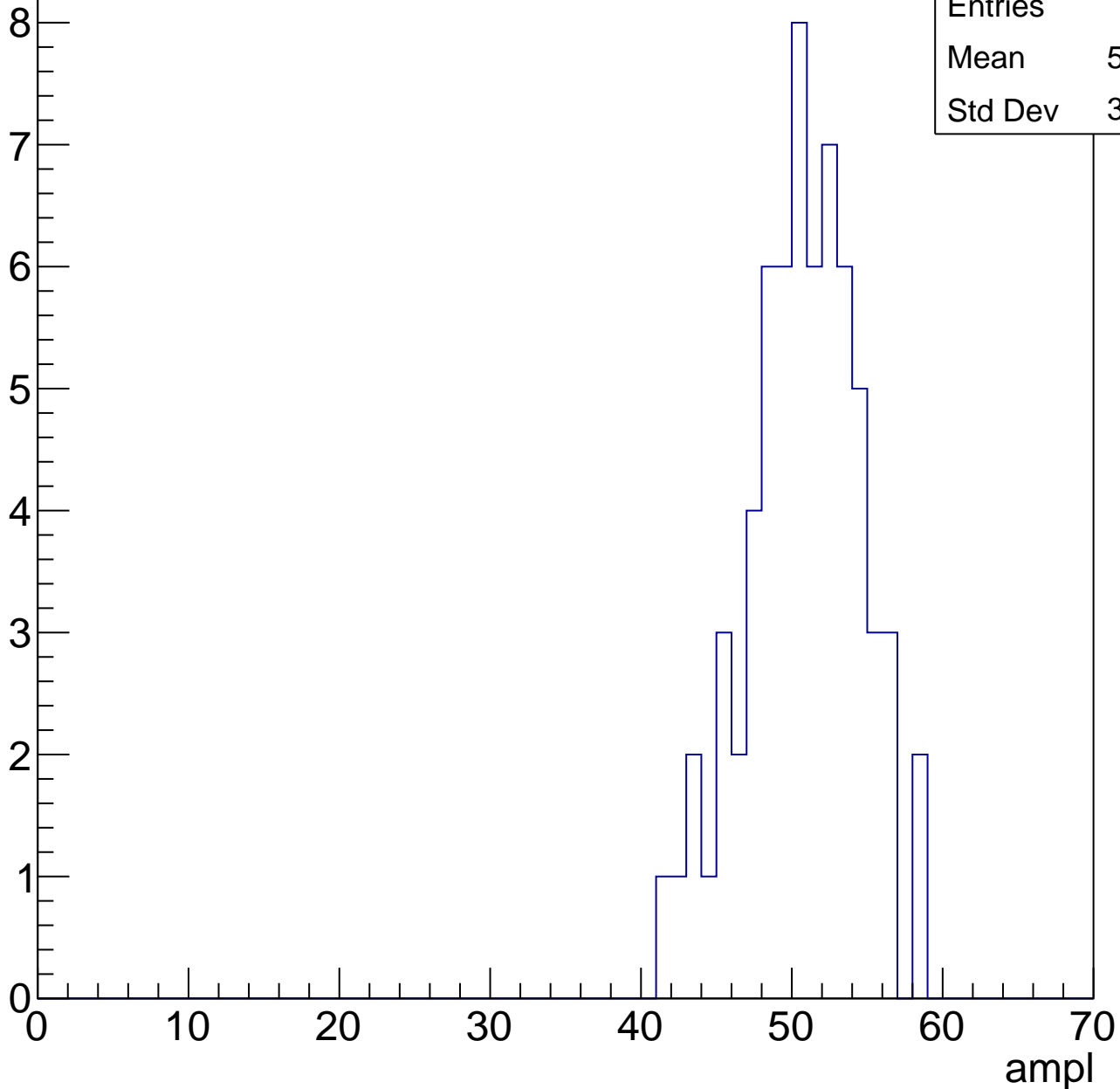


B1L101S, U25-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.26
Std Dev	3.747

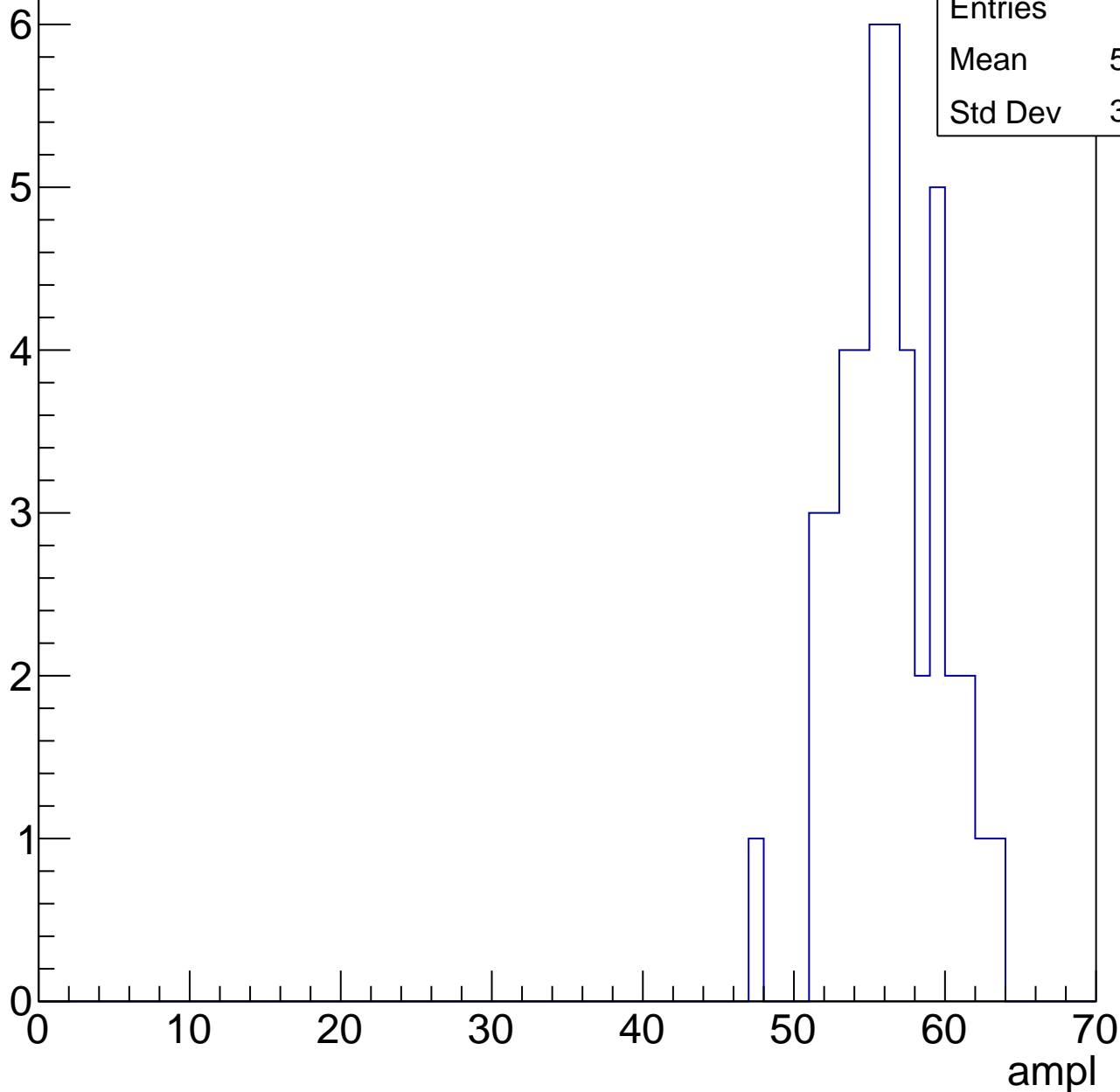


B1L101S, U25-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	55.82
Std Dev	3.318

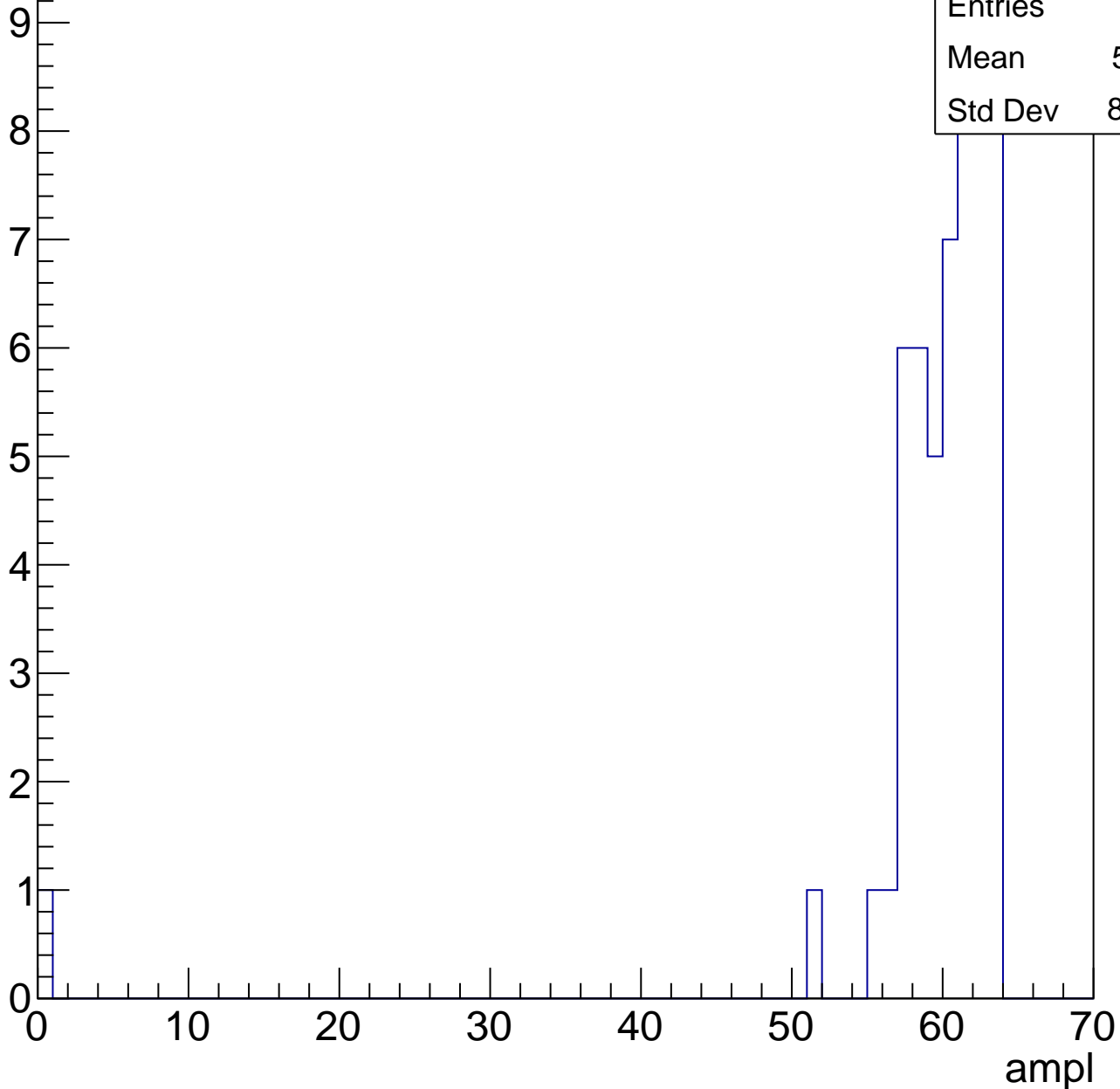


B1L101S, U25-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

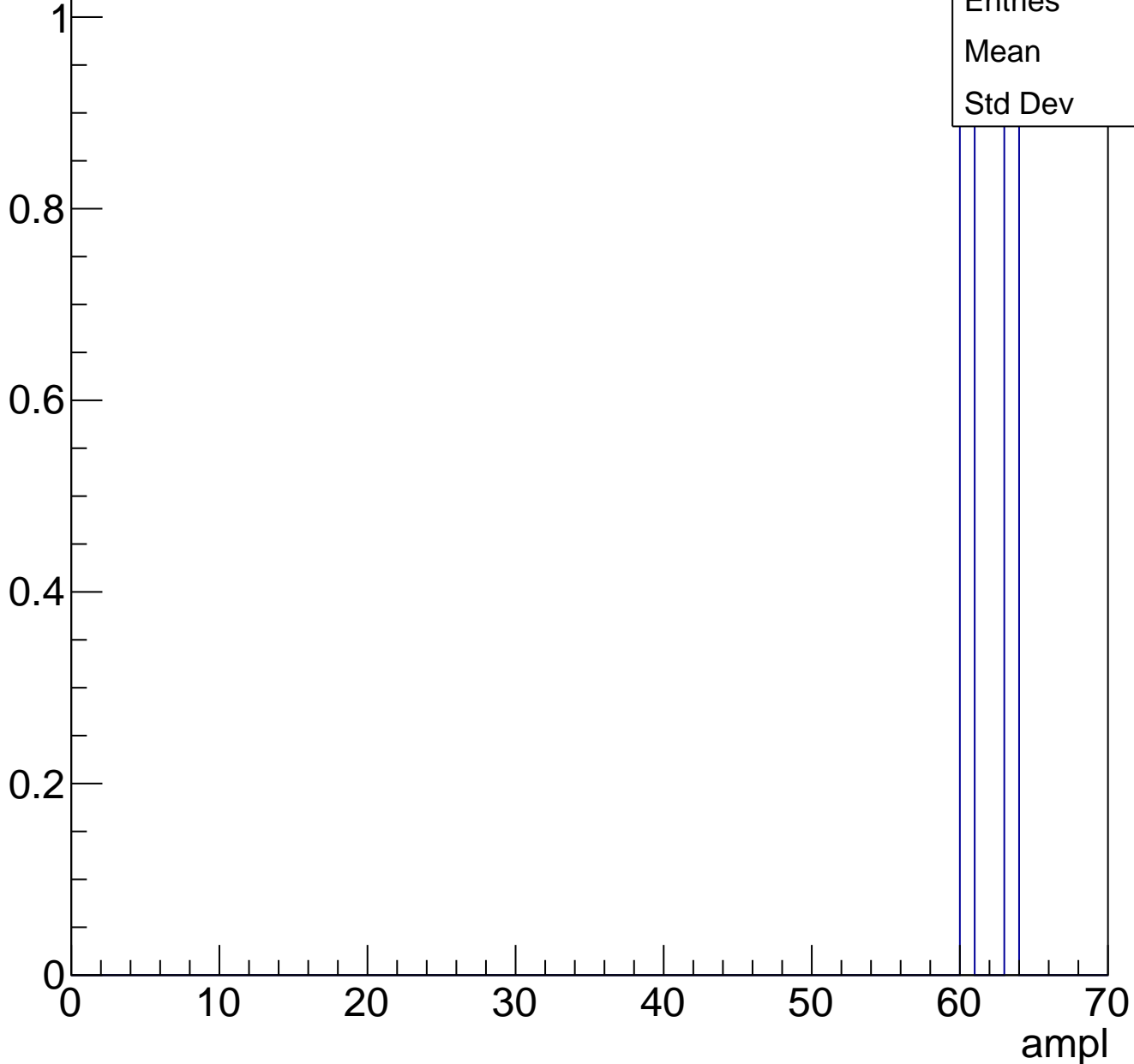
Entries	53
Mean	58.81
Std Dev	8.519



B1L101S, U25-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch23, adc0

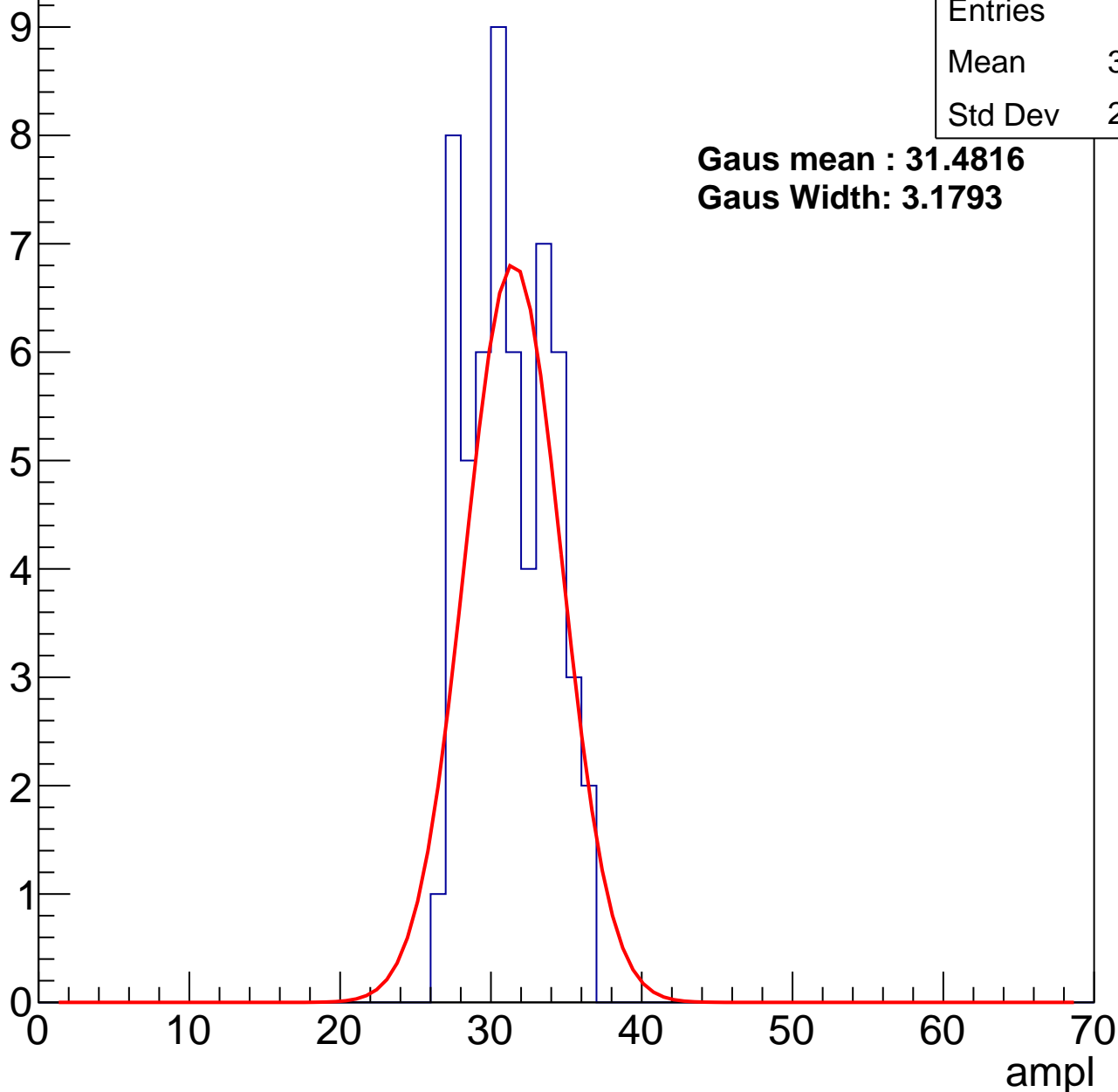
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	30.74
Std Dev	2.685

Gaus mean : 31.4816

Gaus Width: 3.1793



B1L101S, U25-ch23, adc1

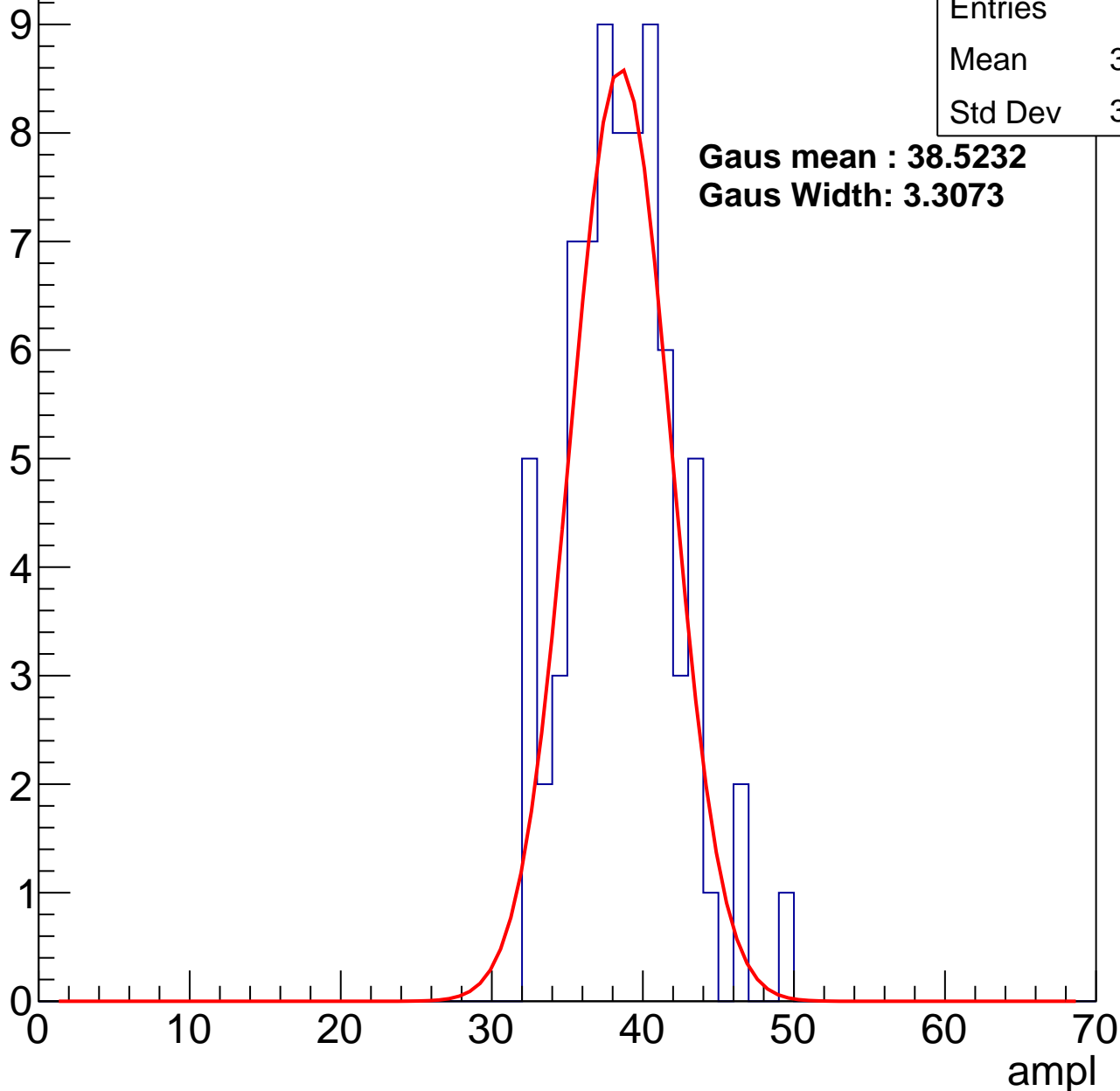
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	38.24
Std Dev	3.498

Gaus mean : 38.5232

Gaus Width: 3.3073



B1L101S, U25-ch23, adc2

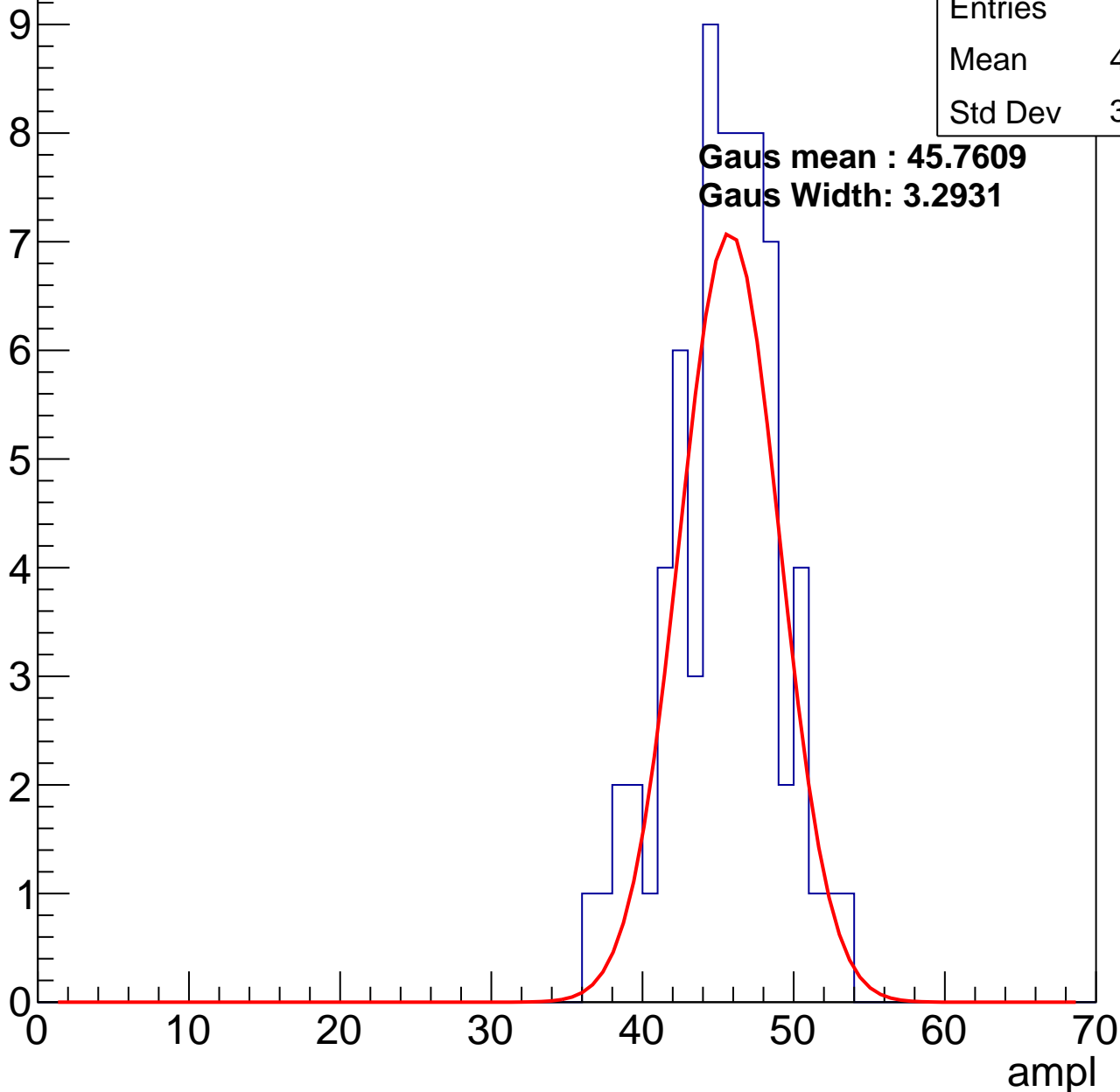
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.96
Std Dev	3.536

Gaus mean : 45.7609

Gaus Width: 3.2931

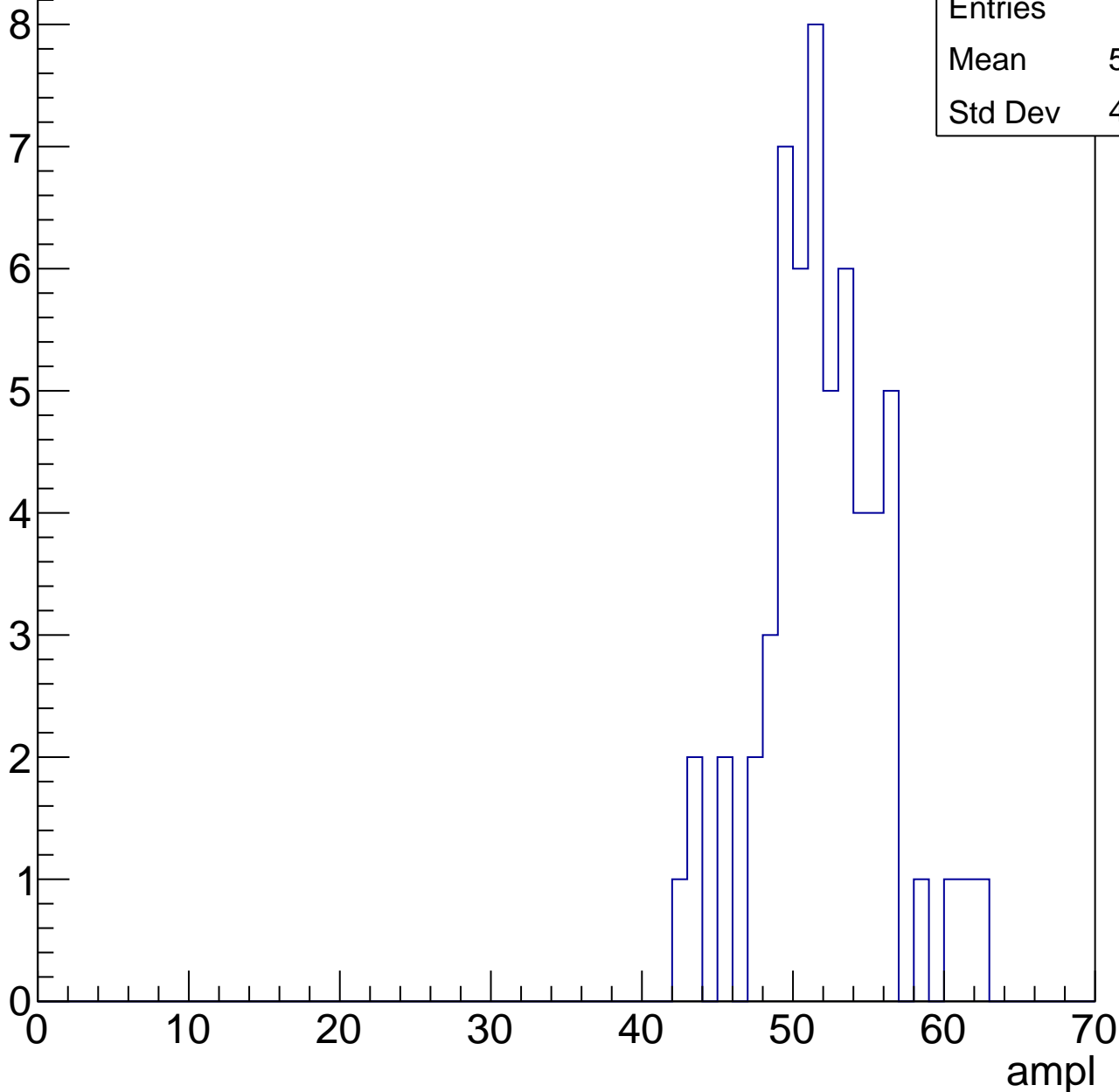


B1L101S, U25-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	51.56
Std Dev	4.035

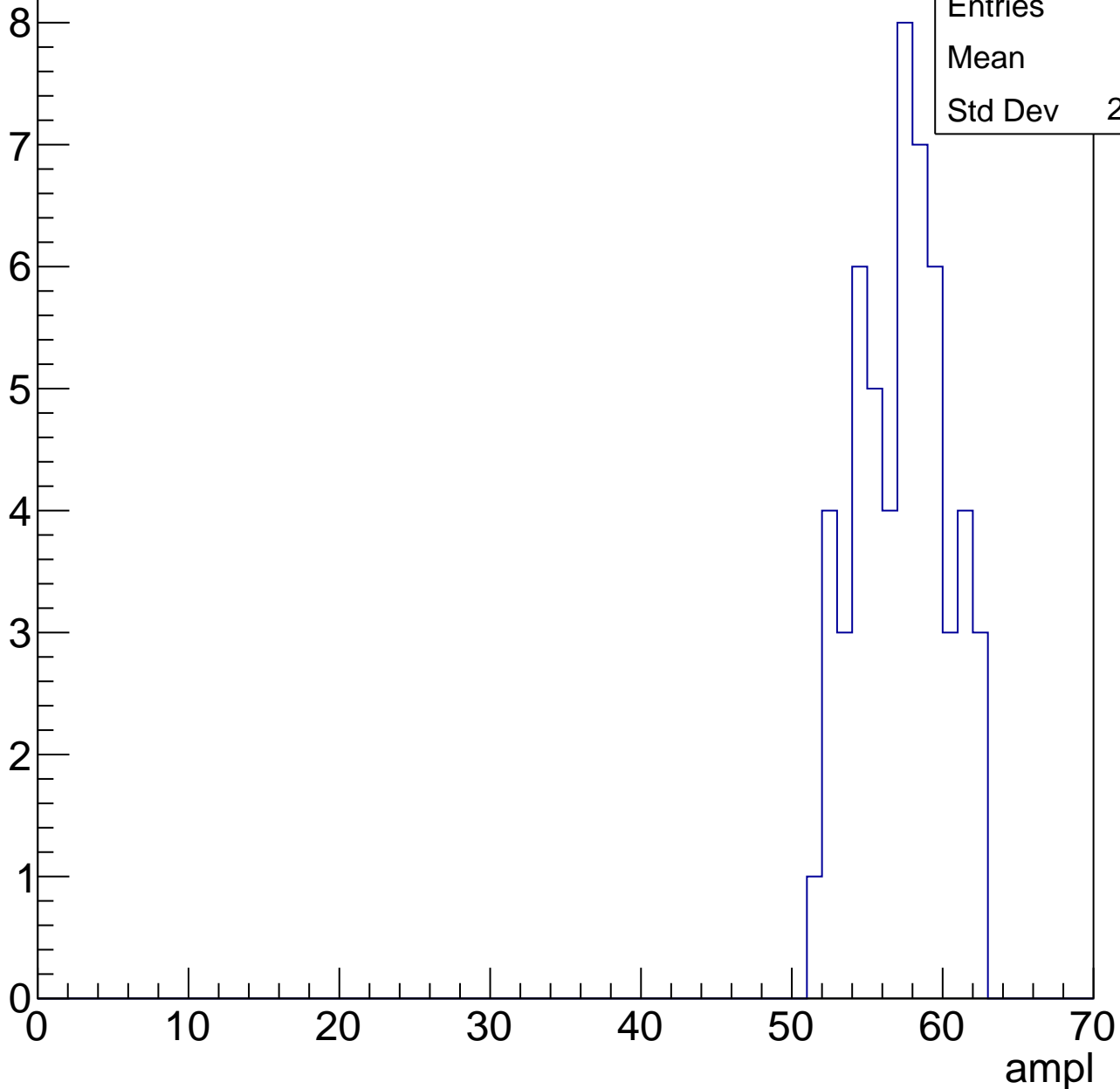


B1L101S, U25-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.8
Std Dev	2.908

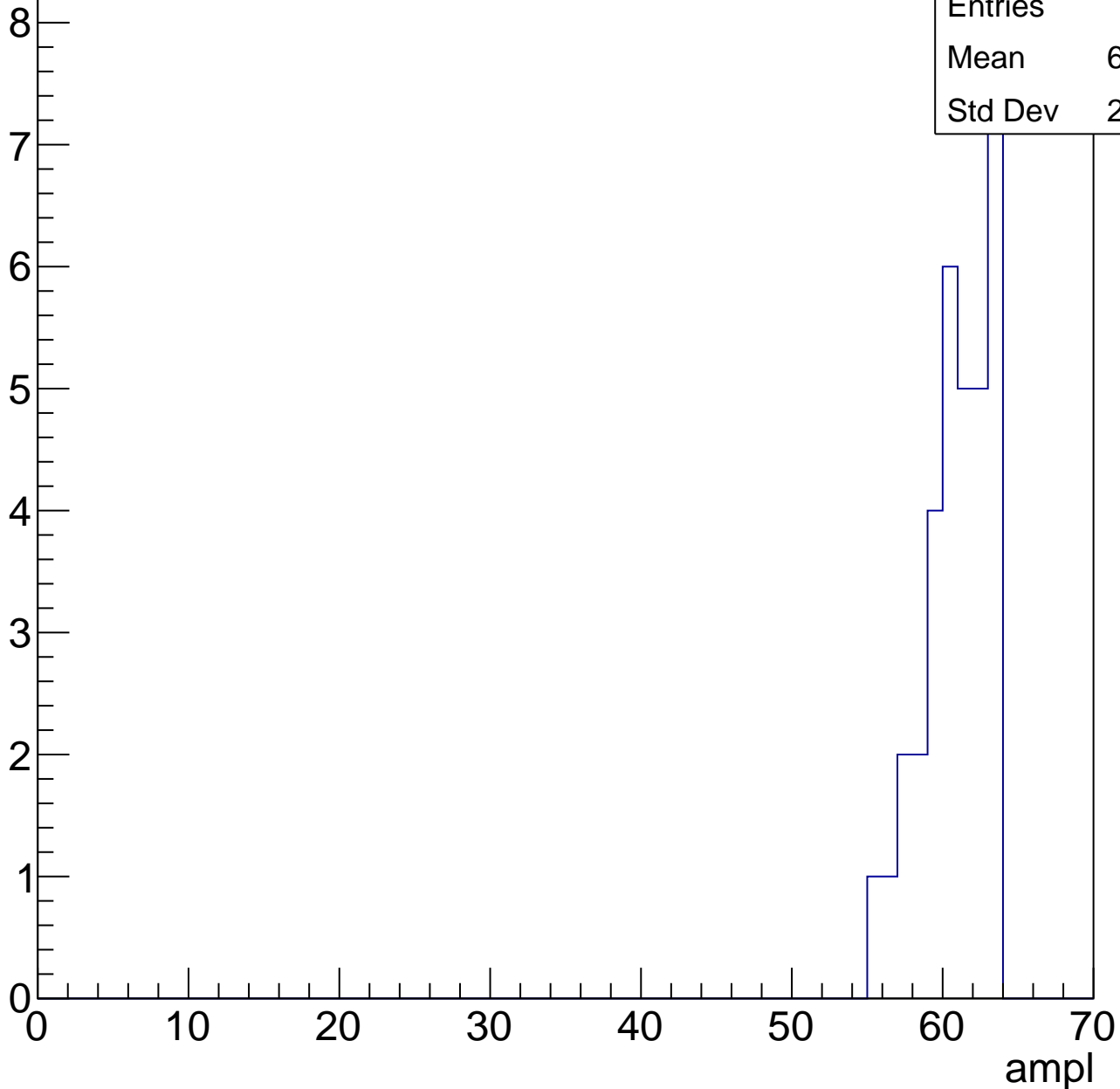


B1L101S, U25-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	60.47
Std Dev	2.172



B1L101S, U25-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch23, adc7

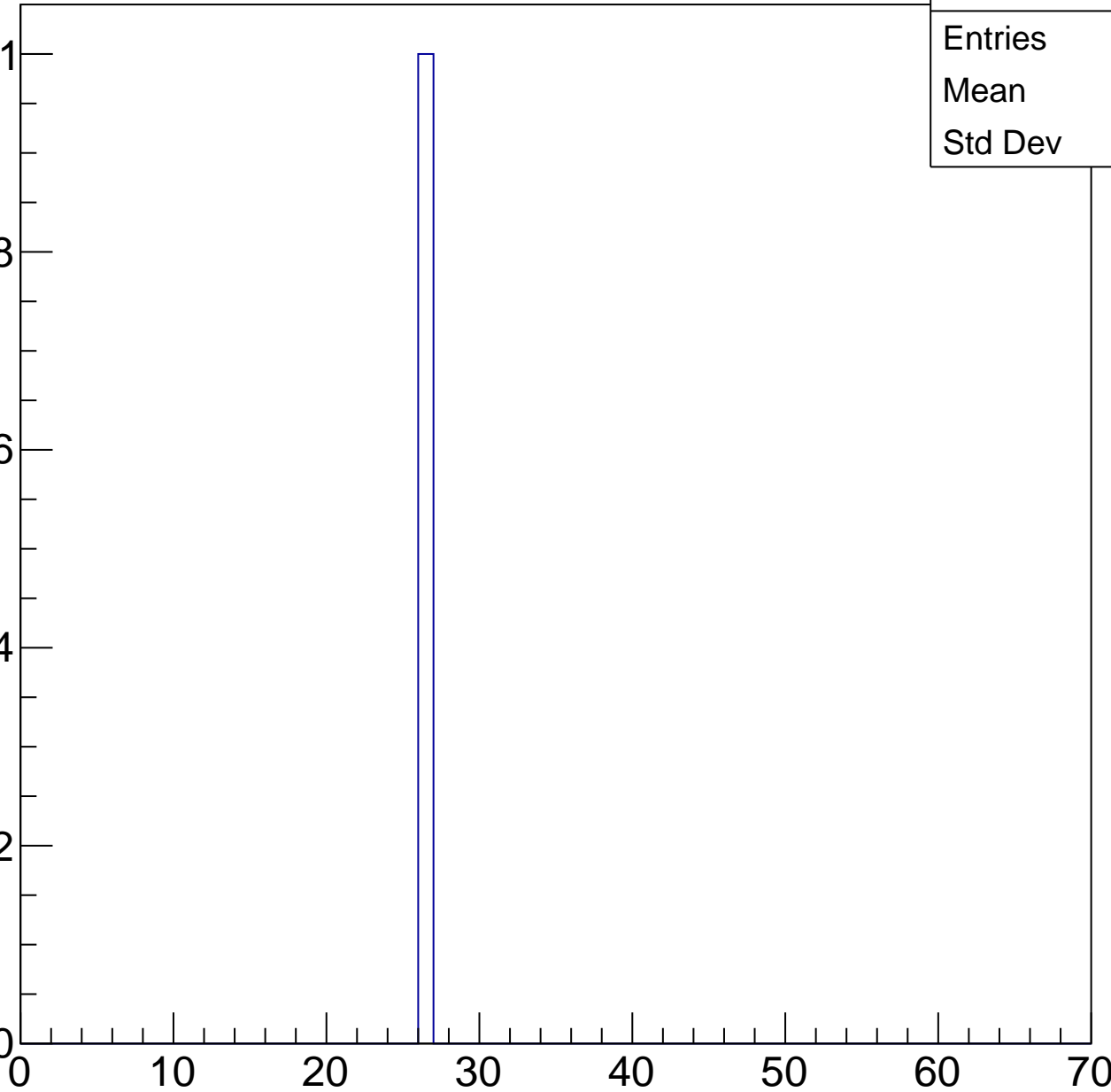
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl



B1L101S, U25-ch24, adc0

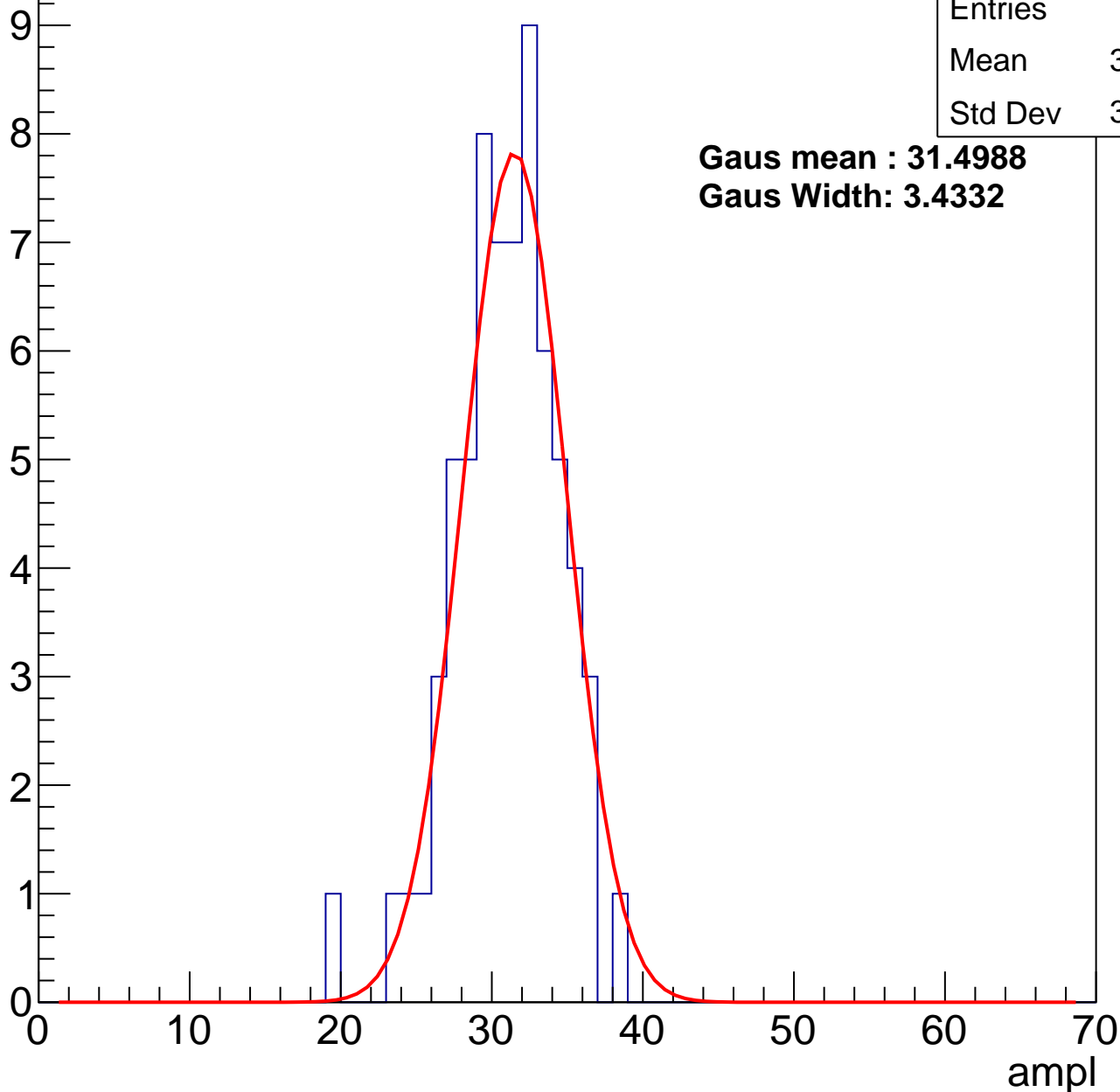
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.52
Std Dev	3.418

Gaus mean : 31.4988

Gaus Width: 3.4332



B1L101S, U25-ch24, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	38.11
Std Dev	3.497

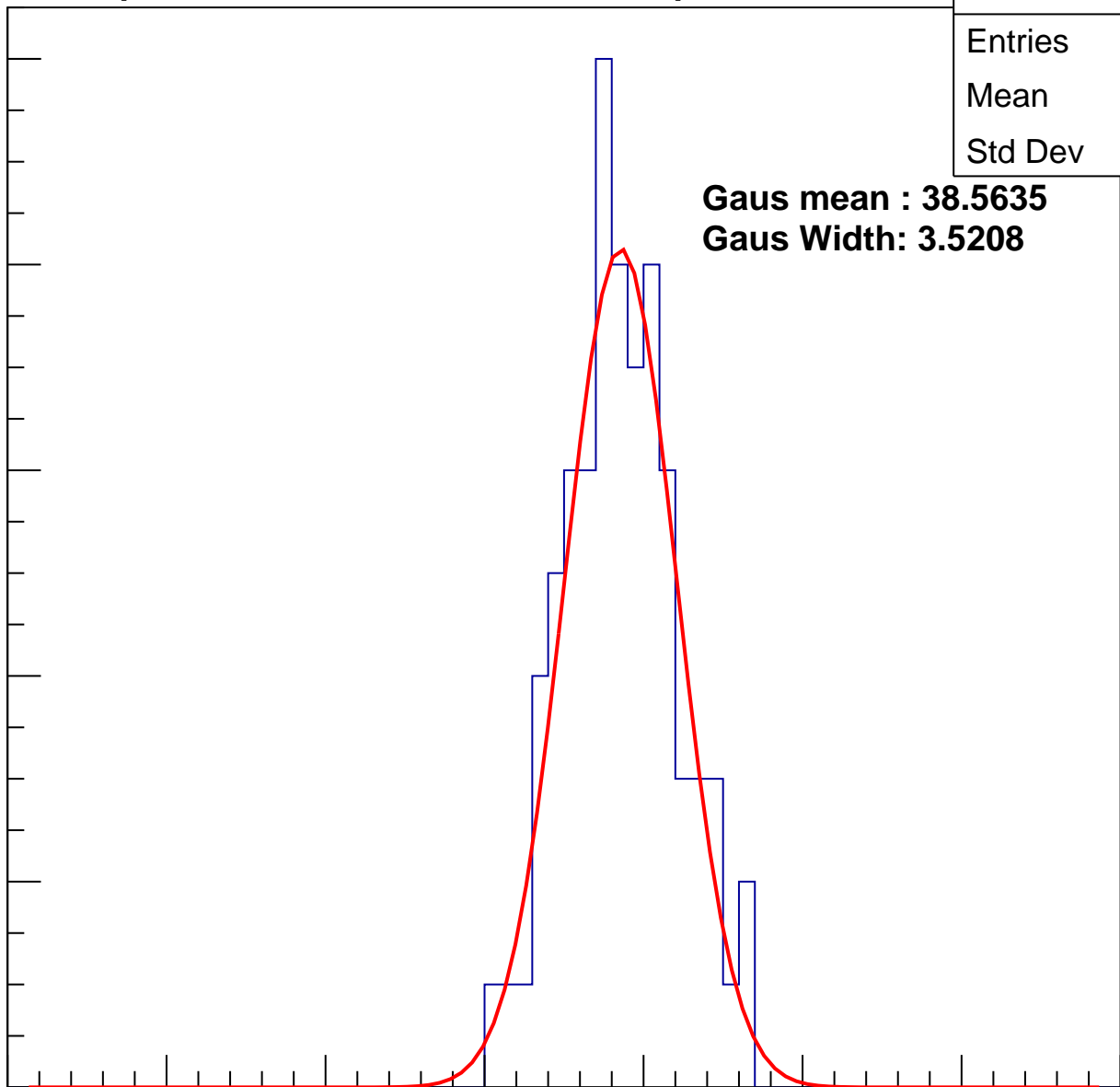
Gaus mean : 38.5635
Gaus Width: 3.5208

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch24, adc2

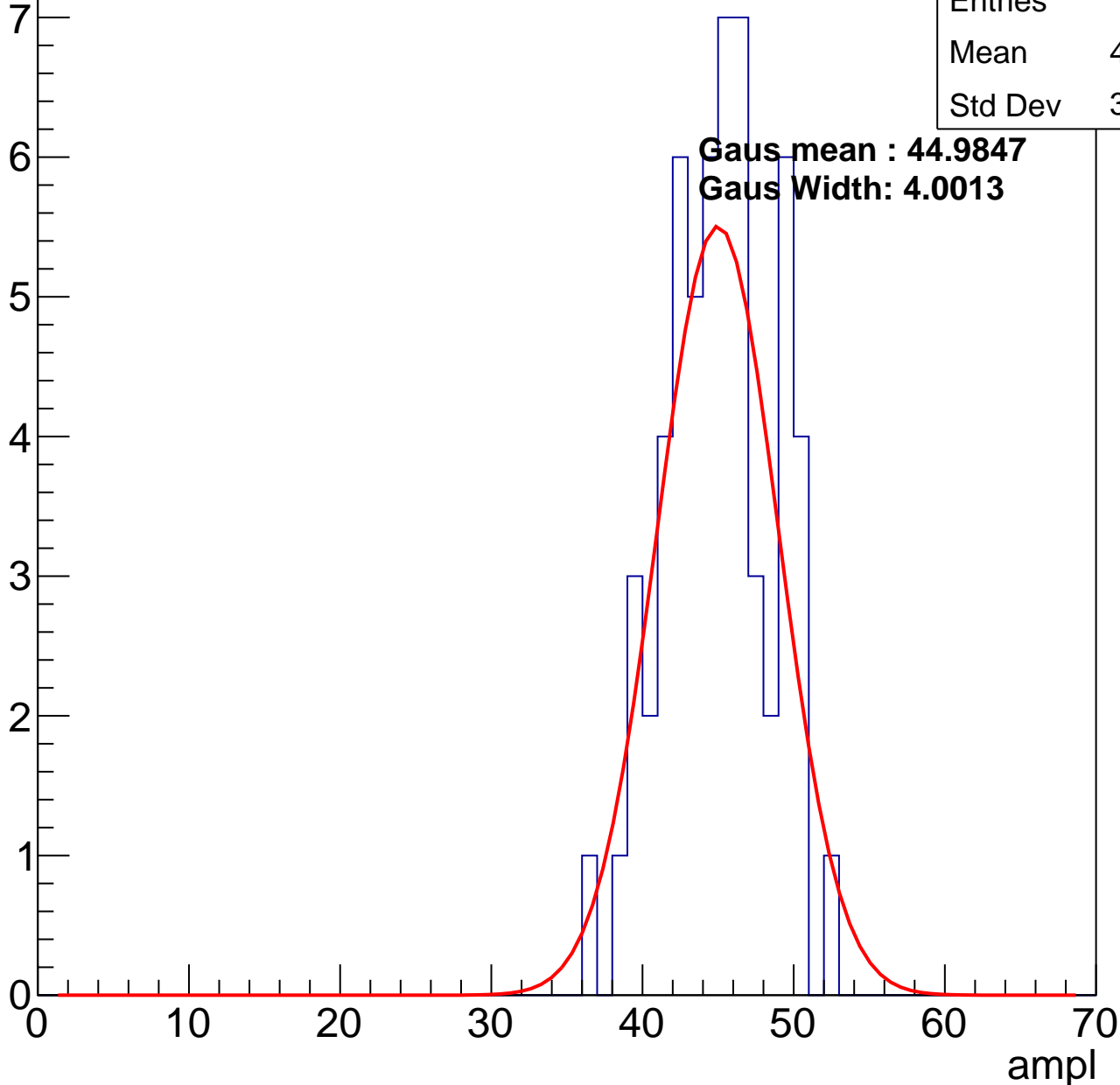
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.59
Std Dev	3.484

Gaus mean : 44.9847

Gaus Width: 4.0013



B1L101S, U25-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	50.68
Std Dev	3.662

Entry

10

8

6

4

2

0

0

10

20

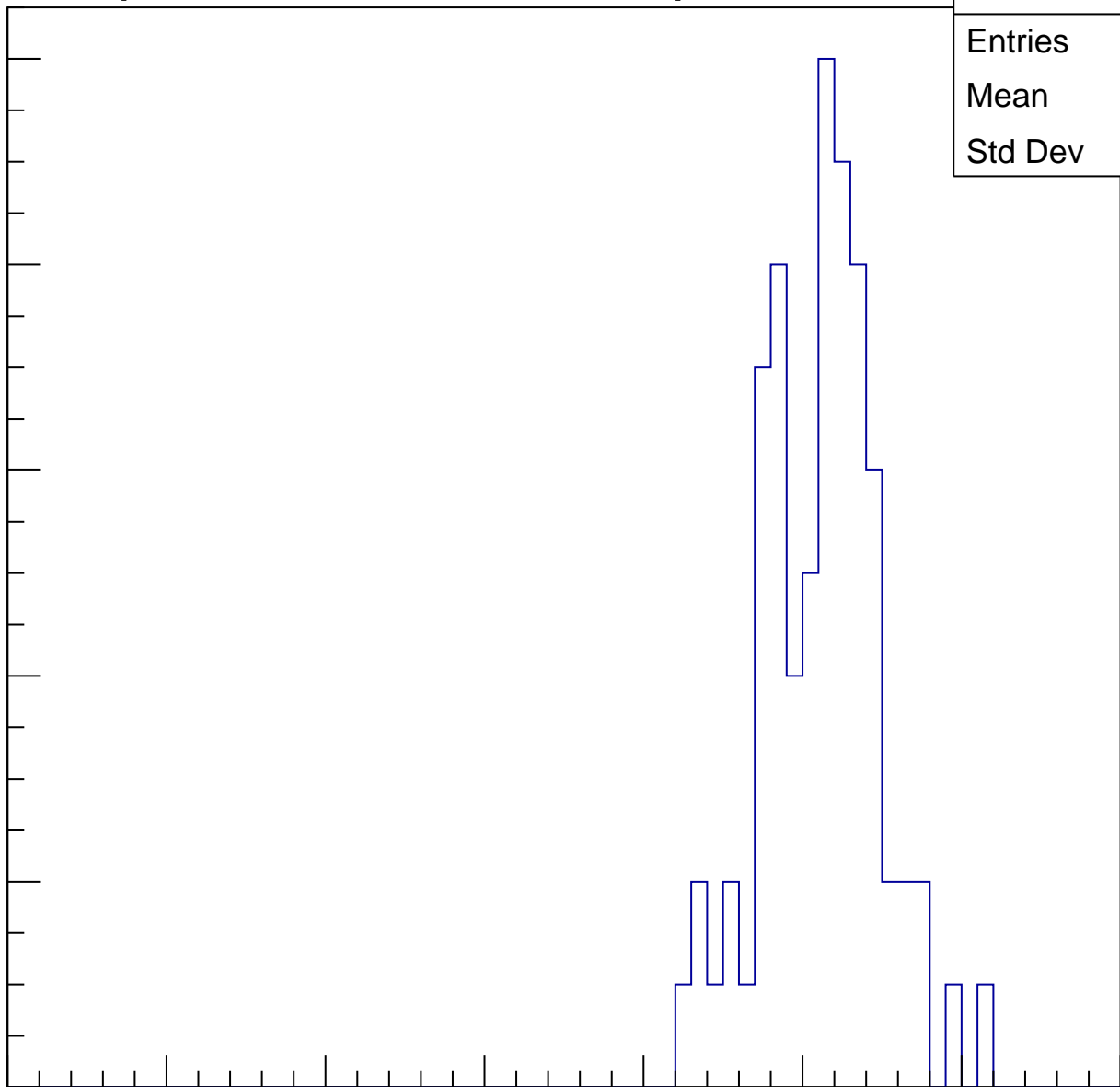
30

40

50

60

ampl

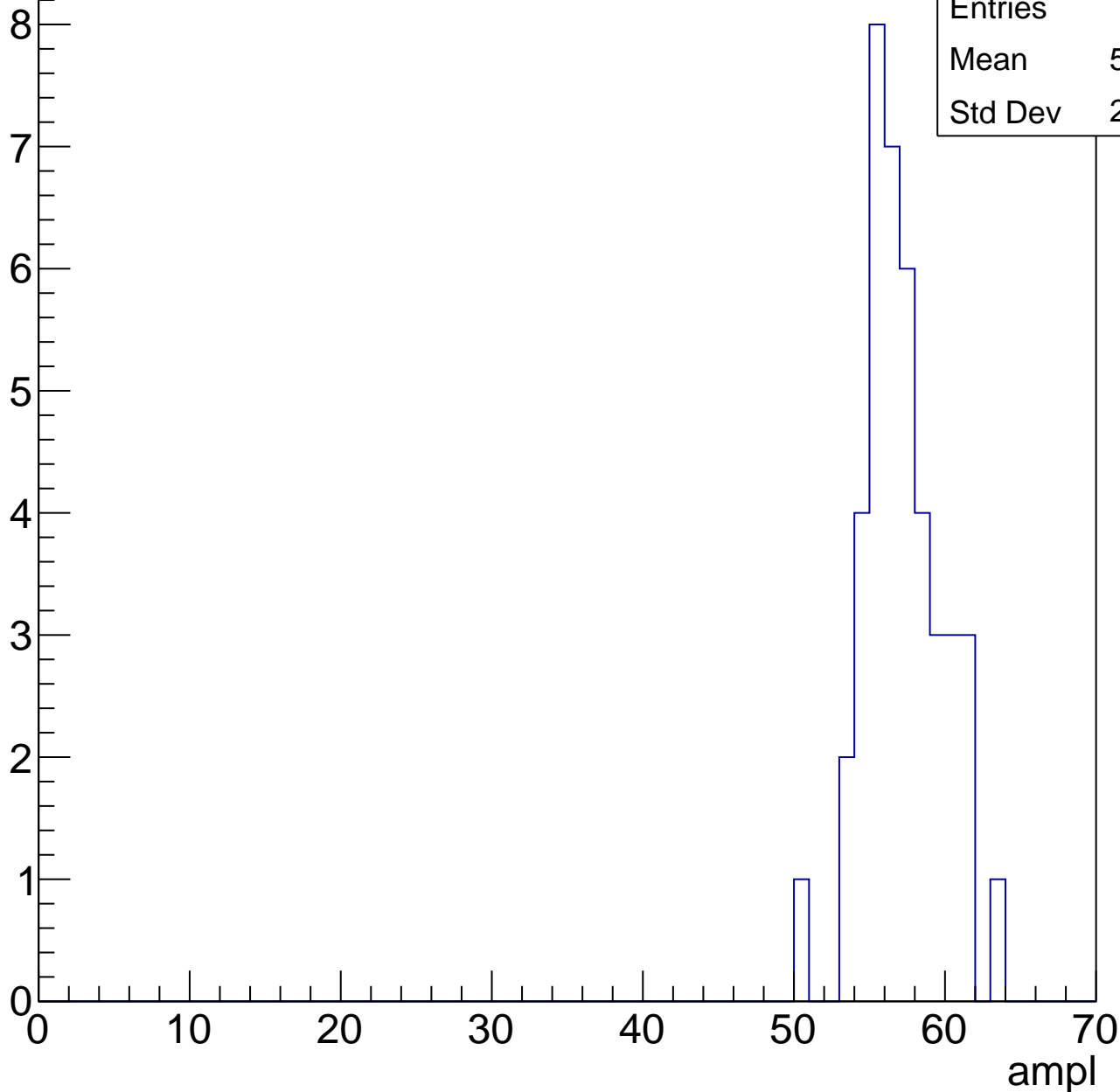


B1L101S, U25-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

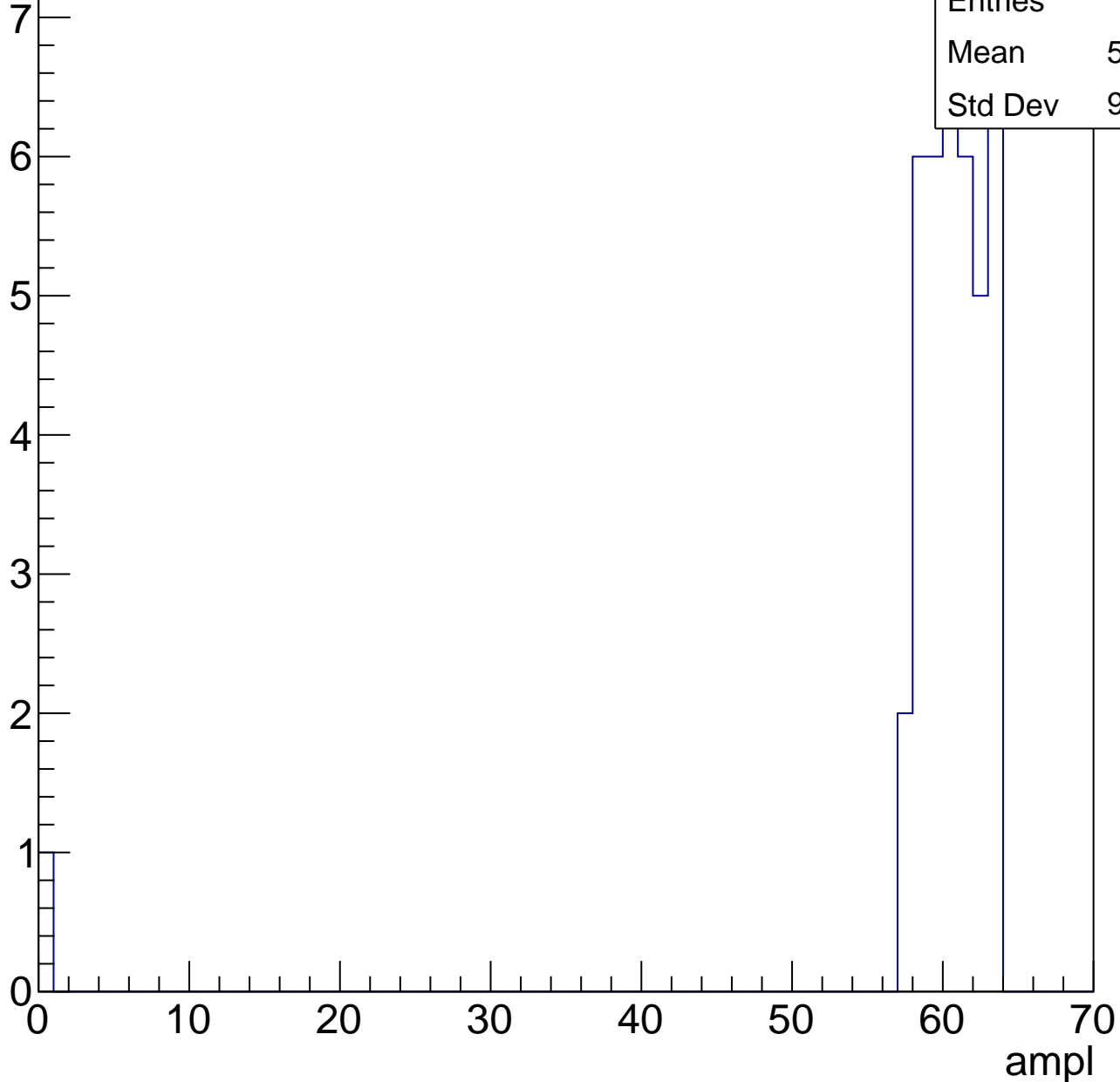
Entries	42
Mean	56.69
Std Dev	2.577



B1L101S, U25-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



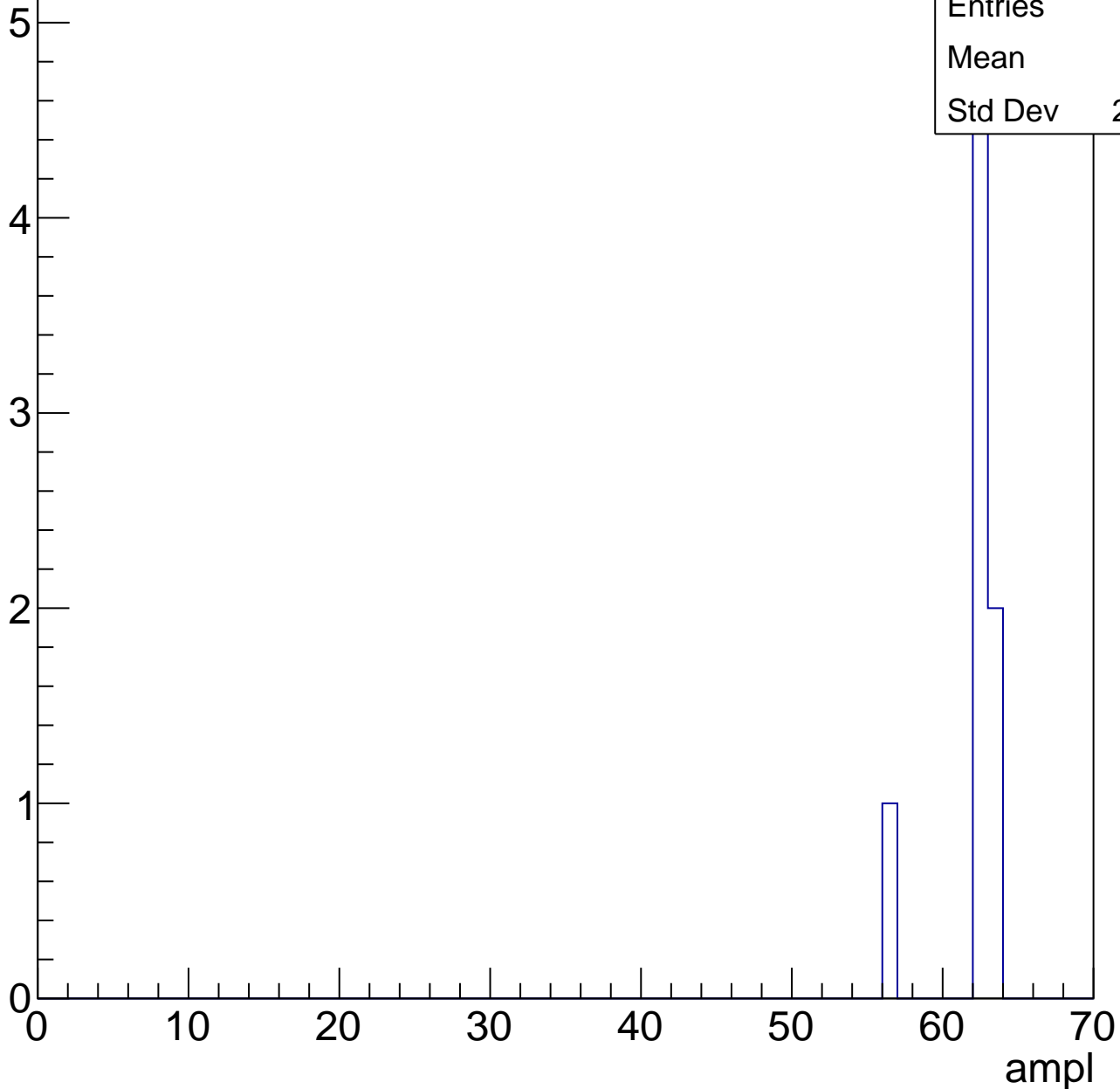
Entries	40
Mean	58.83
Std Dev	9.594

B1L101S, U25-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	61.5
Std Dev	2.121



B1L101S, U25-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch25, adc0

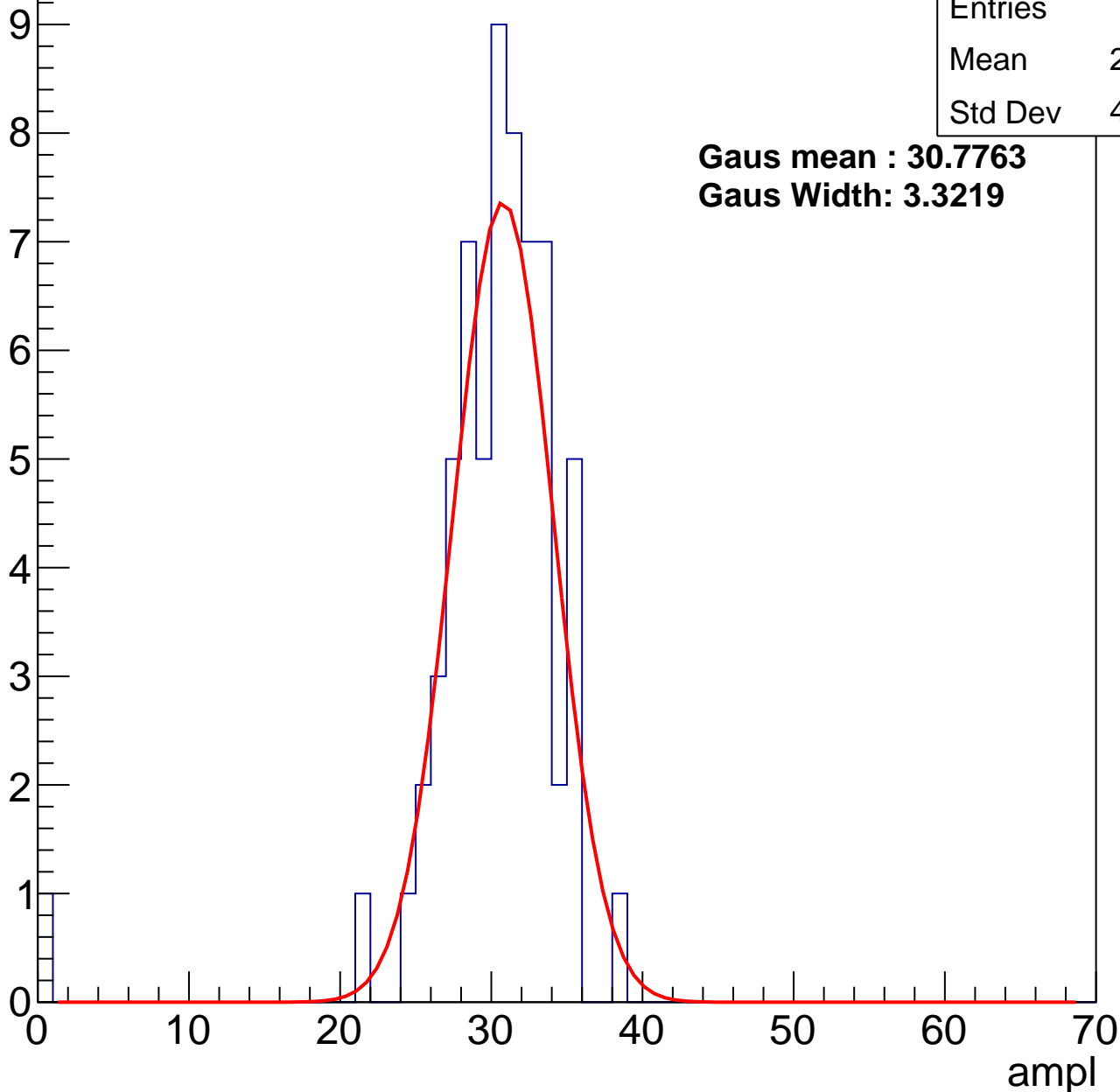
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.73
Std Dev	4.855

Gaus mean : 30.7763

Gaus Width: 3.3219



B1L101S, U25-ch25, adc1

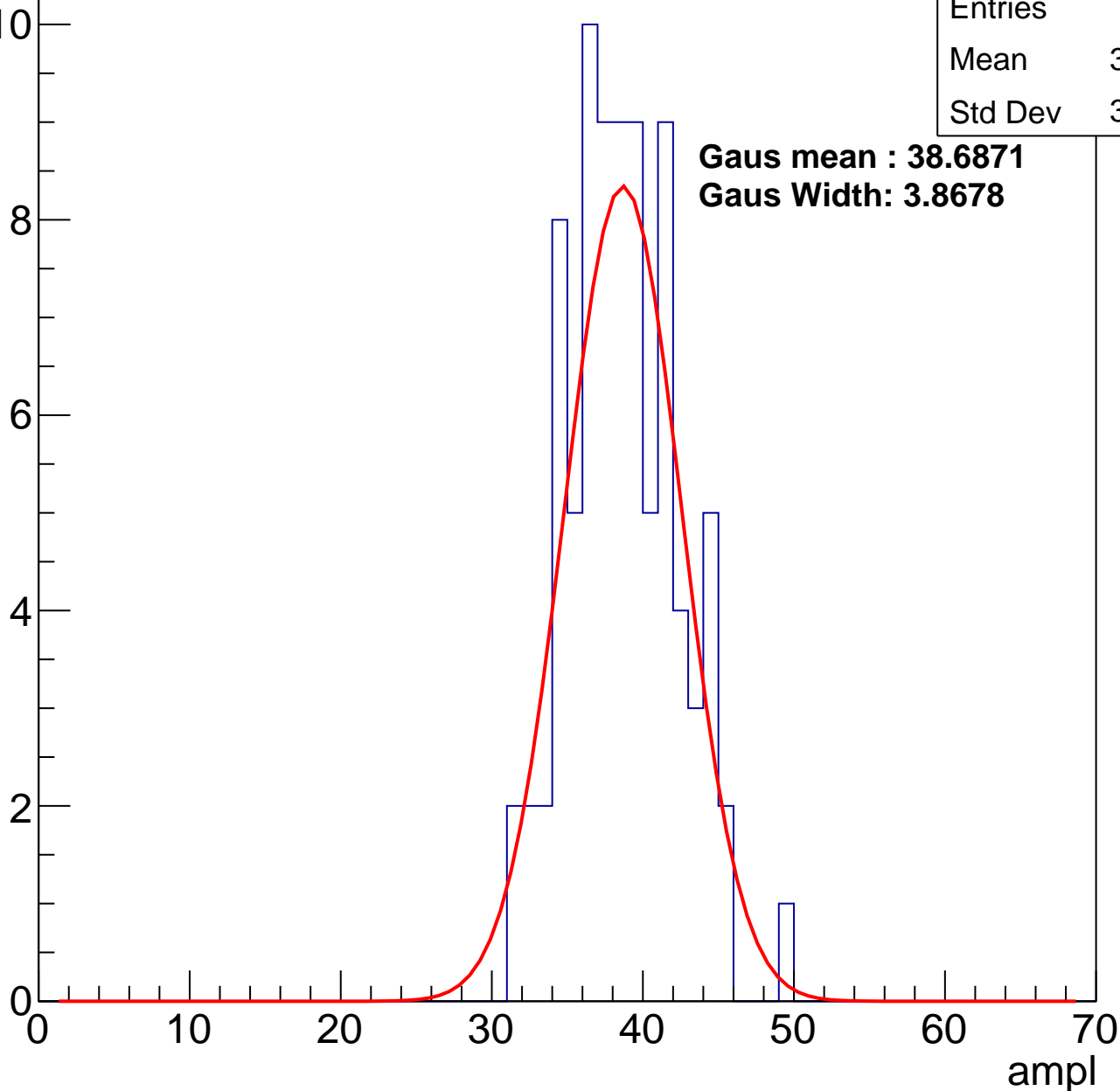
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	38.24
Std Dev	3.573

Gaus mean : 38.6871

Gaus Width: 3.8678



B1L101S, U25-ch25, adc2

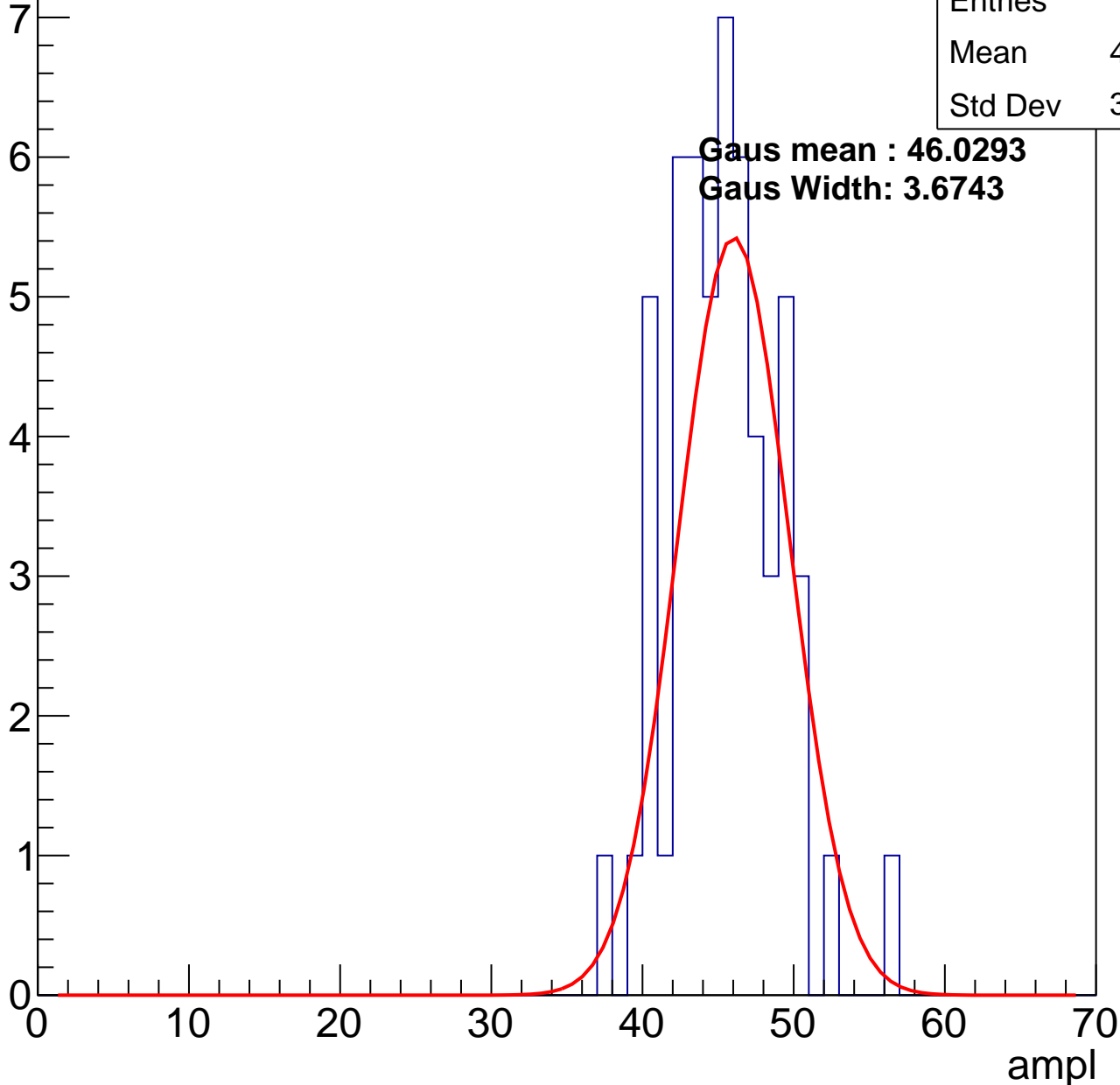
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	44.96
Std Dev	3.562

Gaus mean : 46.0293

Gaus Width: 3.6743

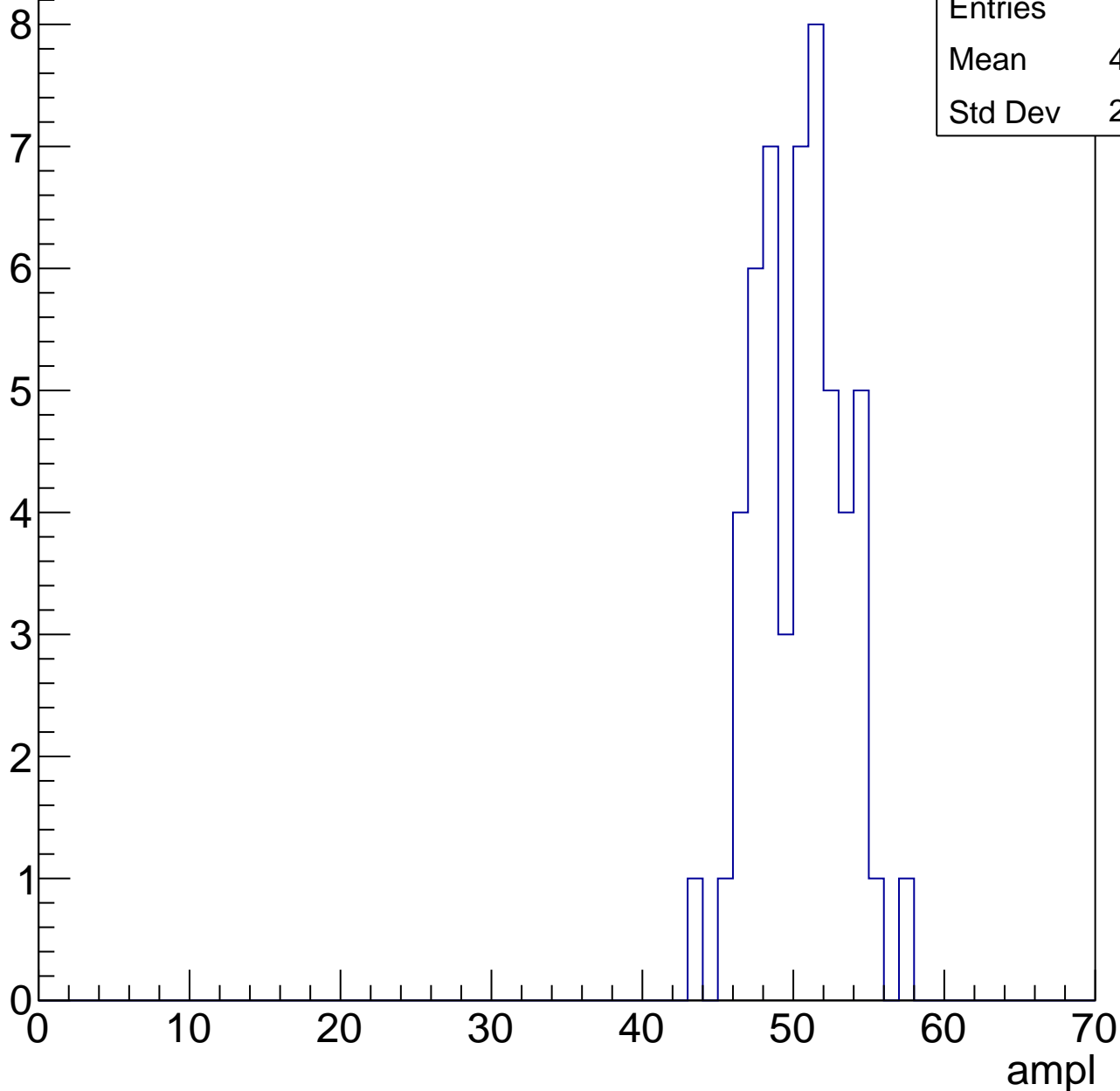


B1L101S, U25-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	49.98
Std Dev	2.885

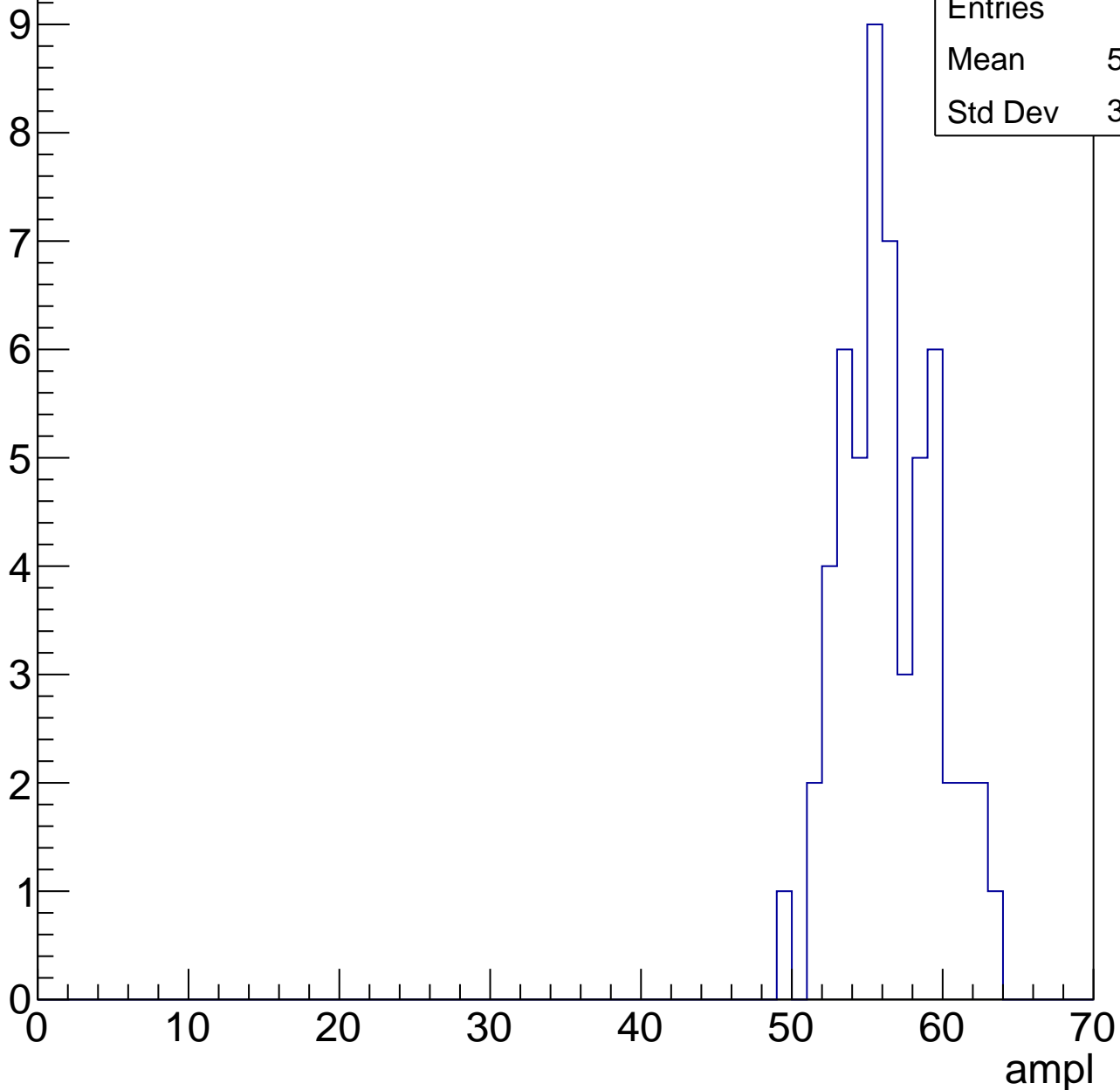


B1L101S, U25-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

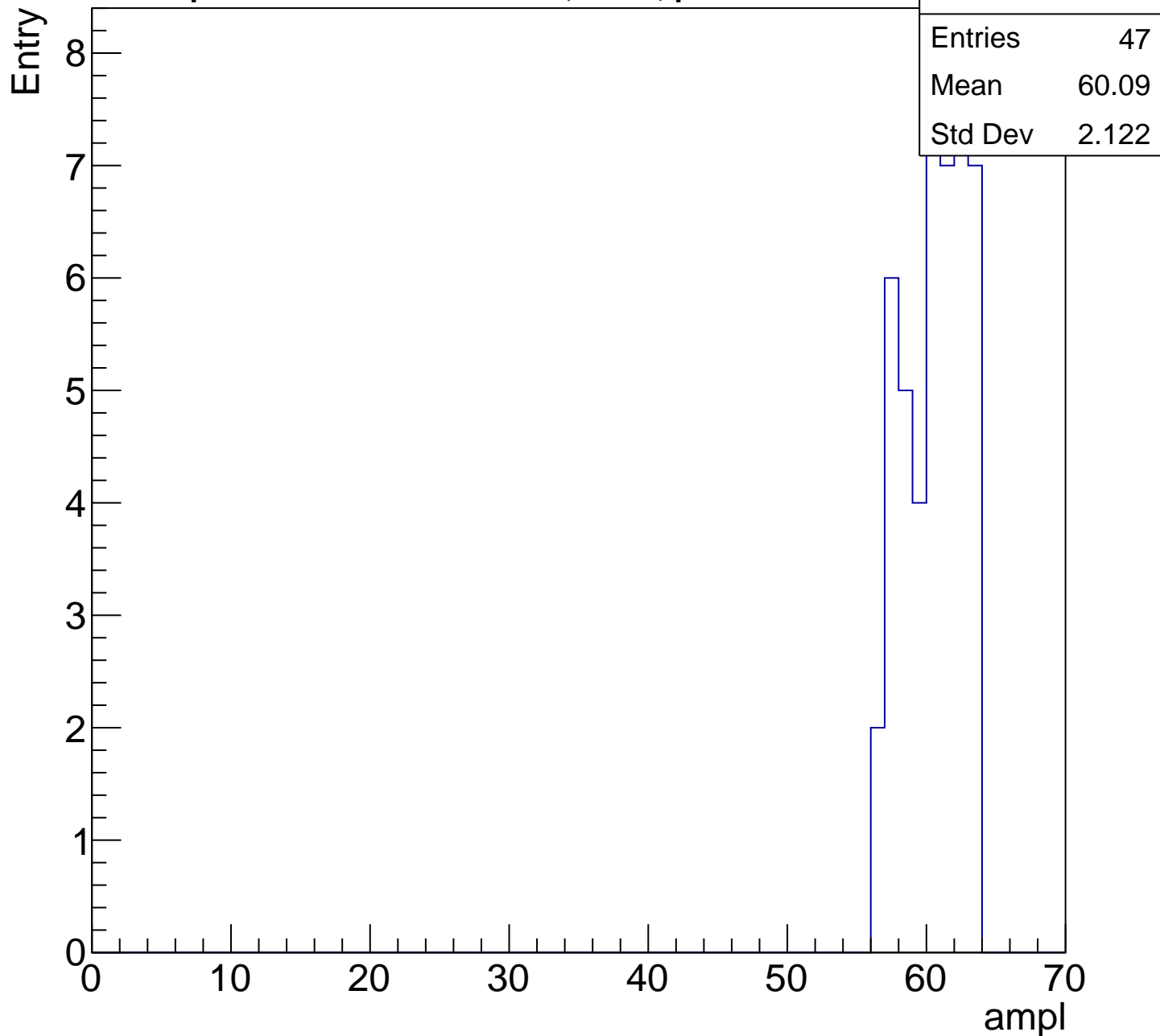
Entry

Entries	55
Mean	55.96
Std Dev	3.092



B1L101S, U25-ch25, adc5

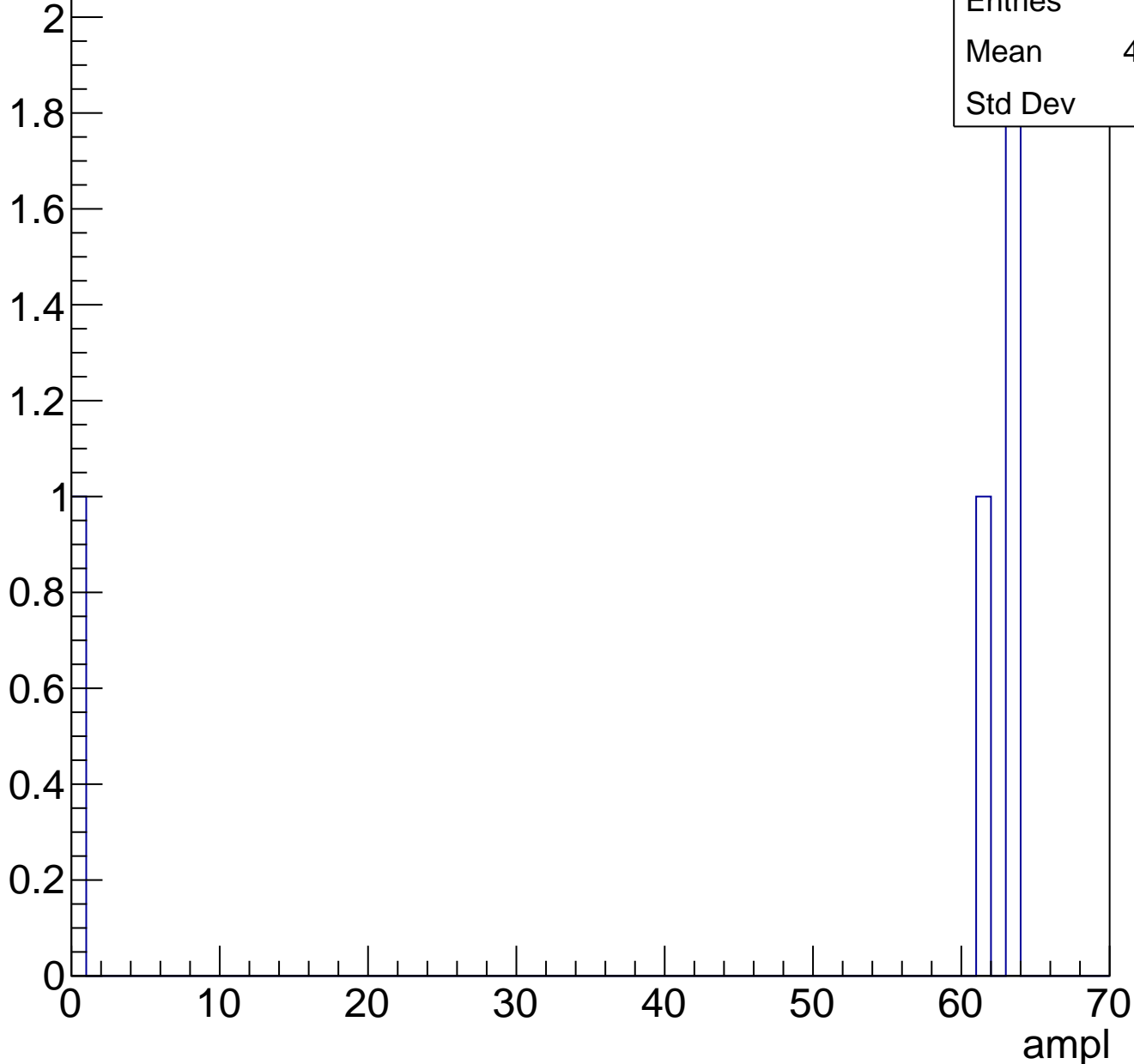
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch26, adc0

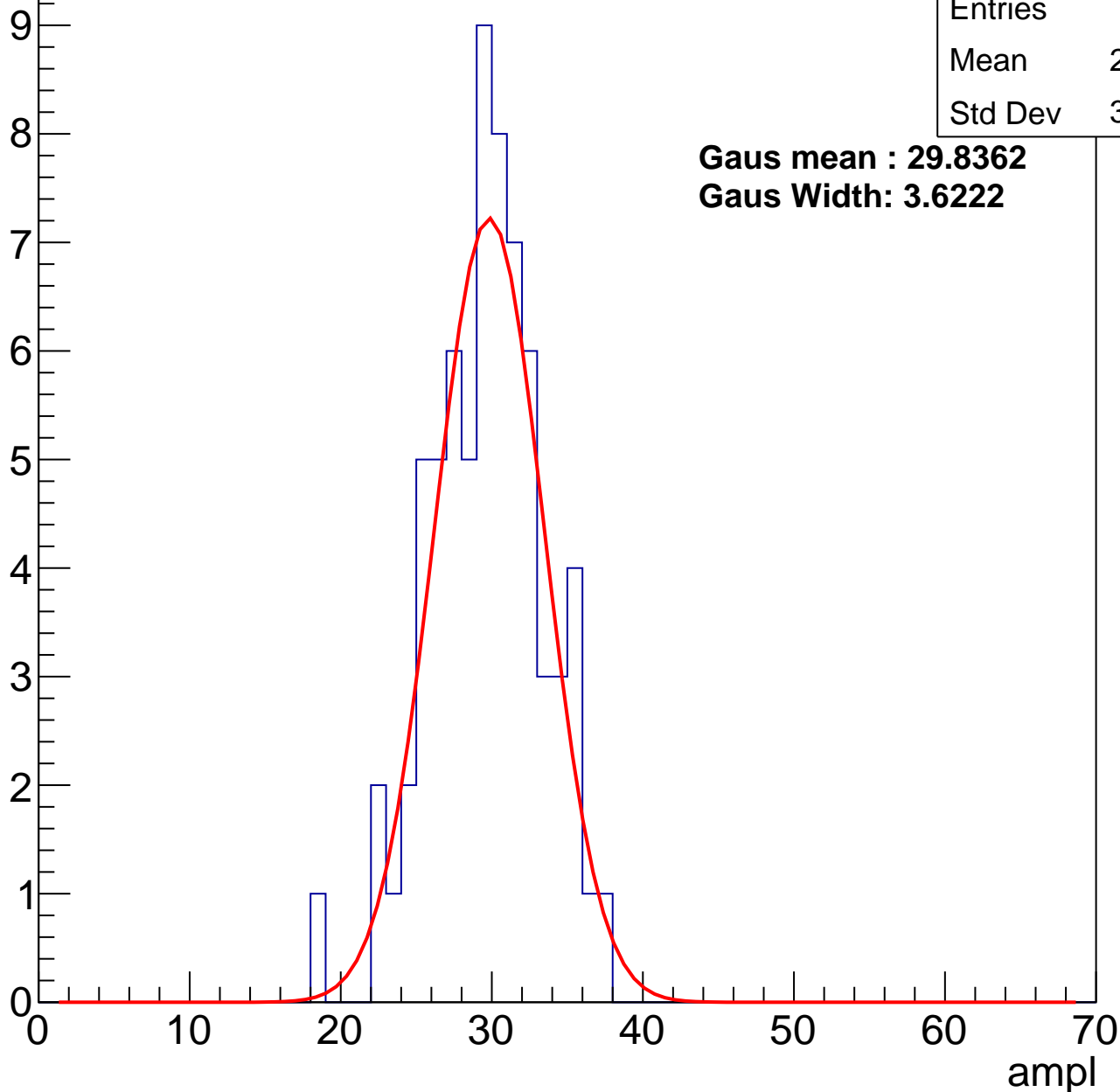
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.19
Std Dev	3.664

Gaus mean : 29.8362

Gaus Width: 3.6222



B1L101S, U25-ch26, adc1

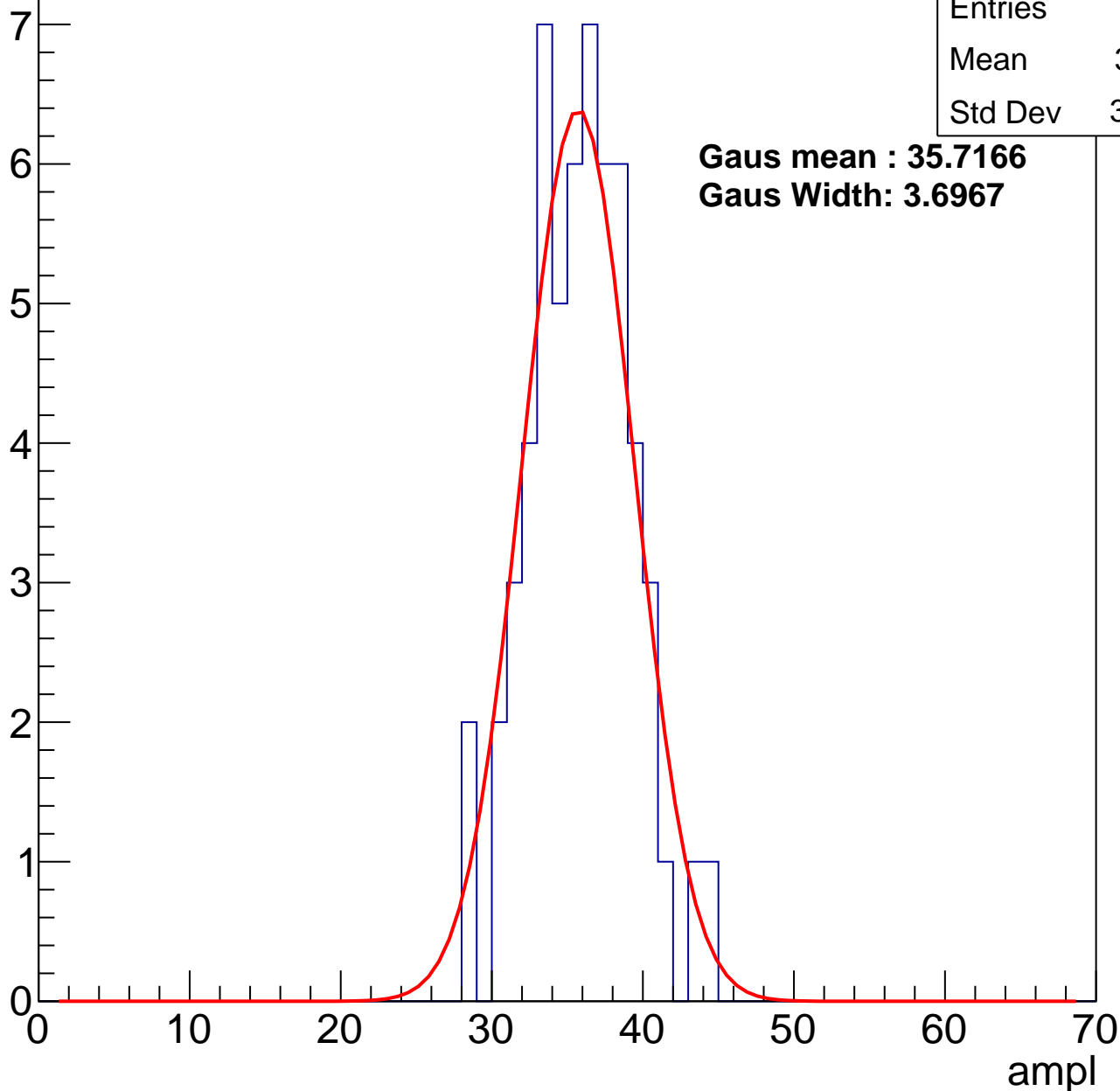
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	35.41
Std Dev	3.373

Gaus mean : 35.7166

Gaus Width: 3.6967



B1L101S, U25-ch26, adc2

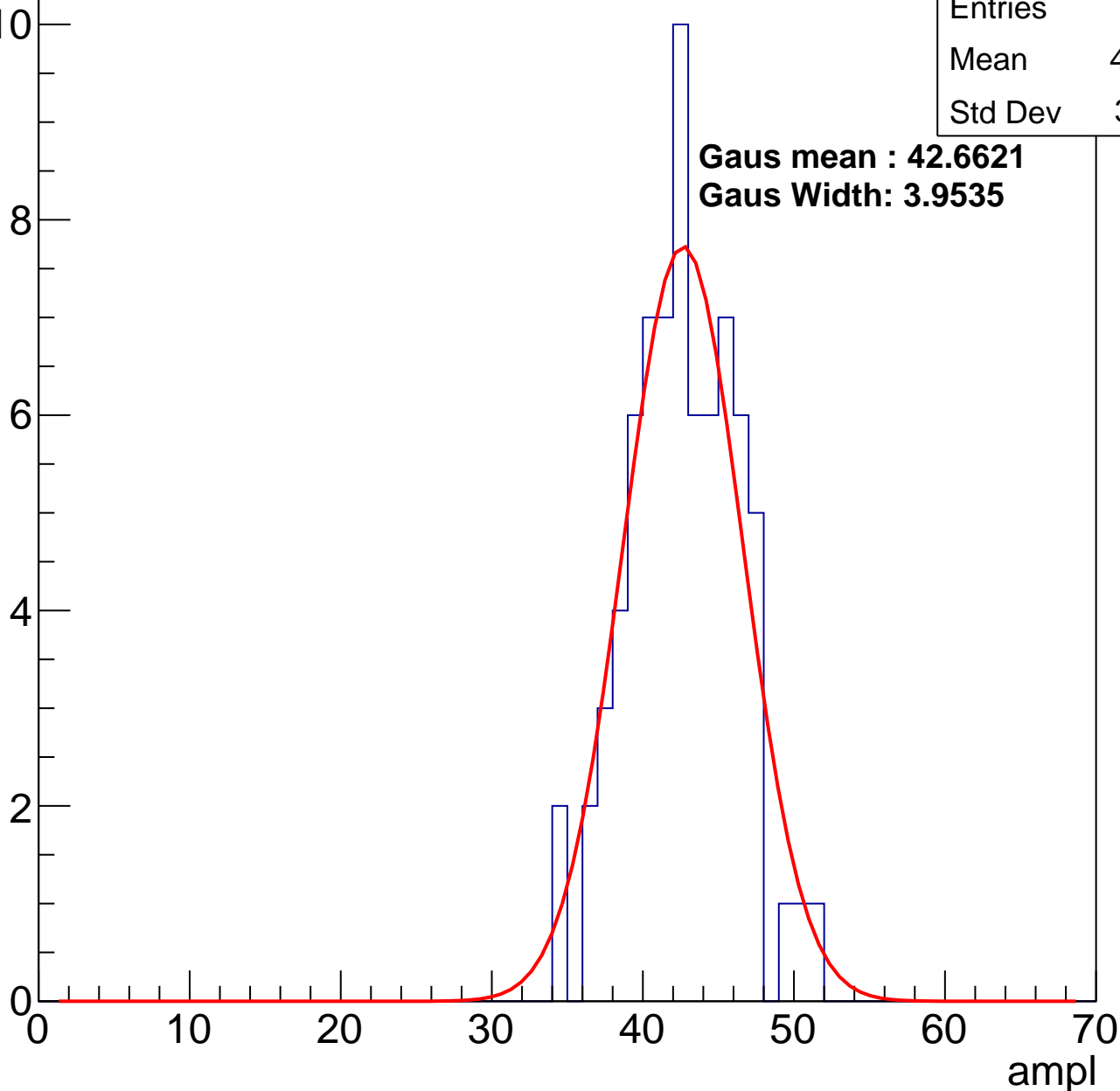
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	42.19
Std Dev	3.551

Gaus mean : 42.6621

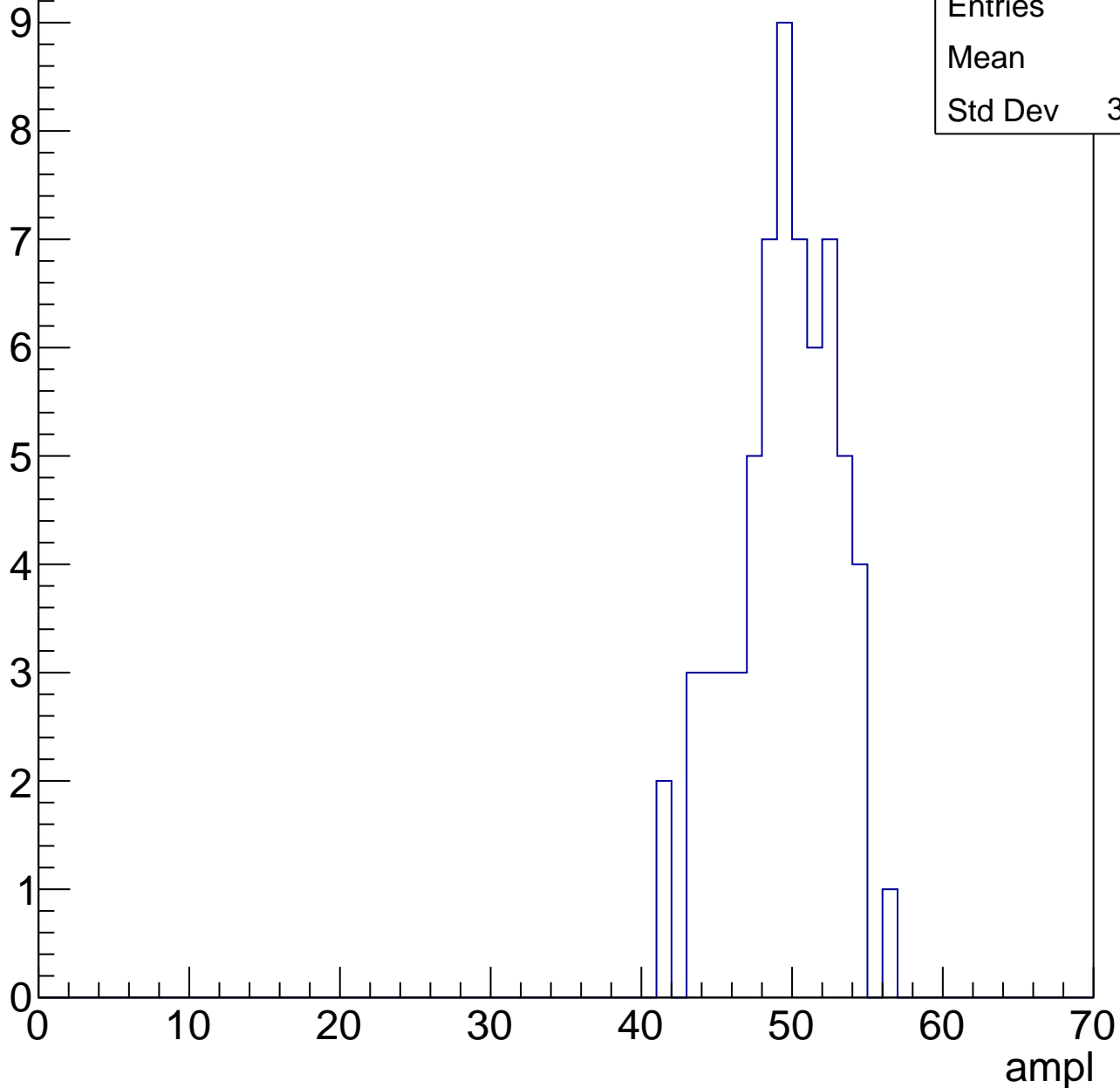
Gaus Width: 3.9535



B1L101S, U25-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

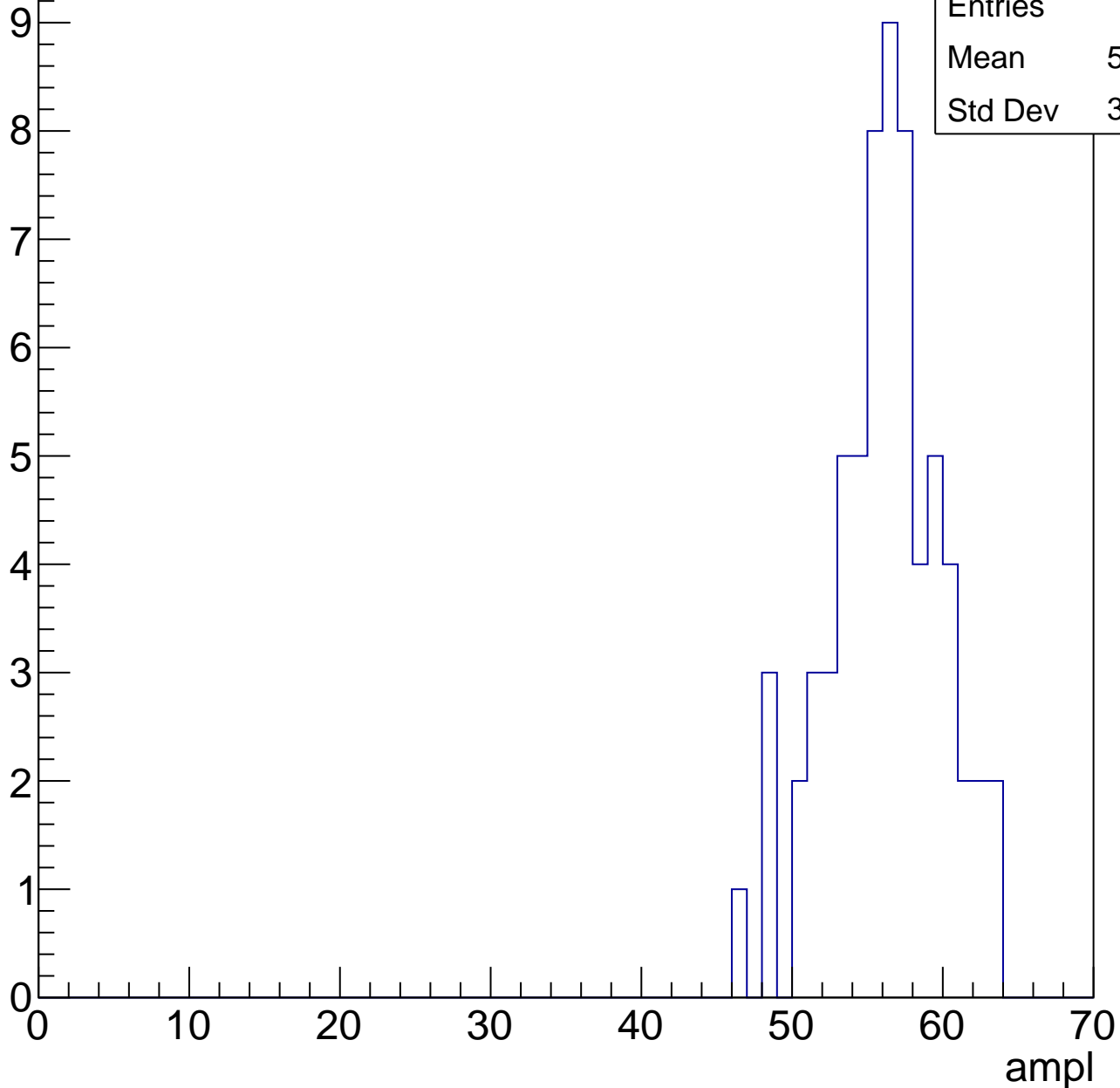
Entry



B1L101S, U25-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

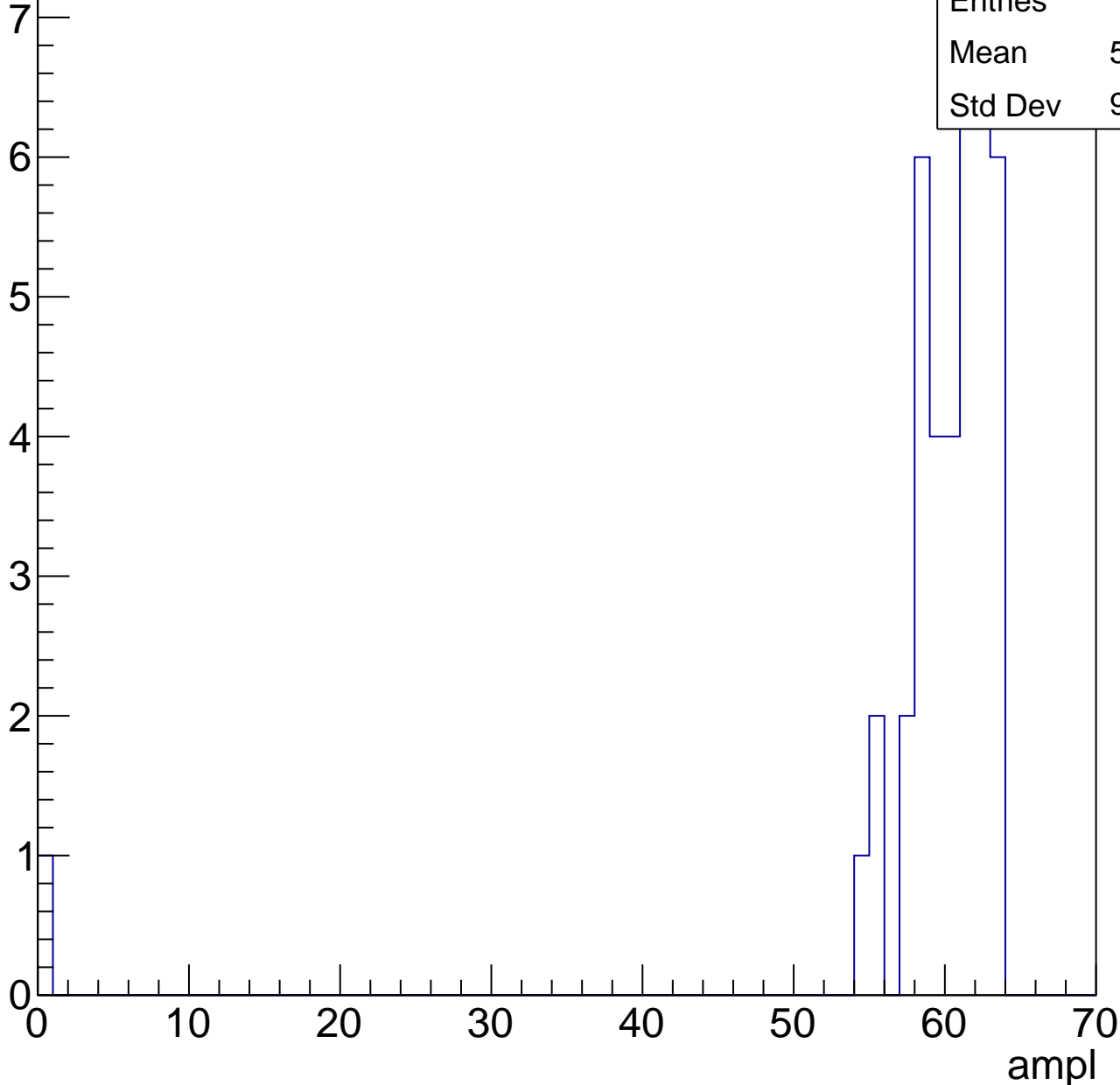


B1L101S, U25-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.52
Std Dev	9.662



B1L101S, U25-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.14
Std Dev	1.457

ampl

0 10 20 30 40 50 60 70

B1L101S, U25-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch27, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	32.02
Std Dev	3.404

Gaus mean : 32.8529

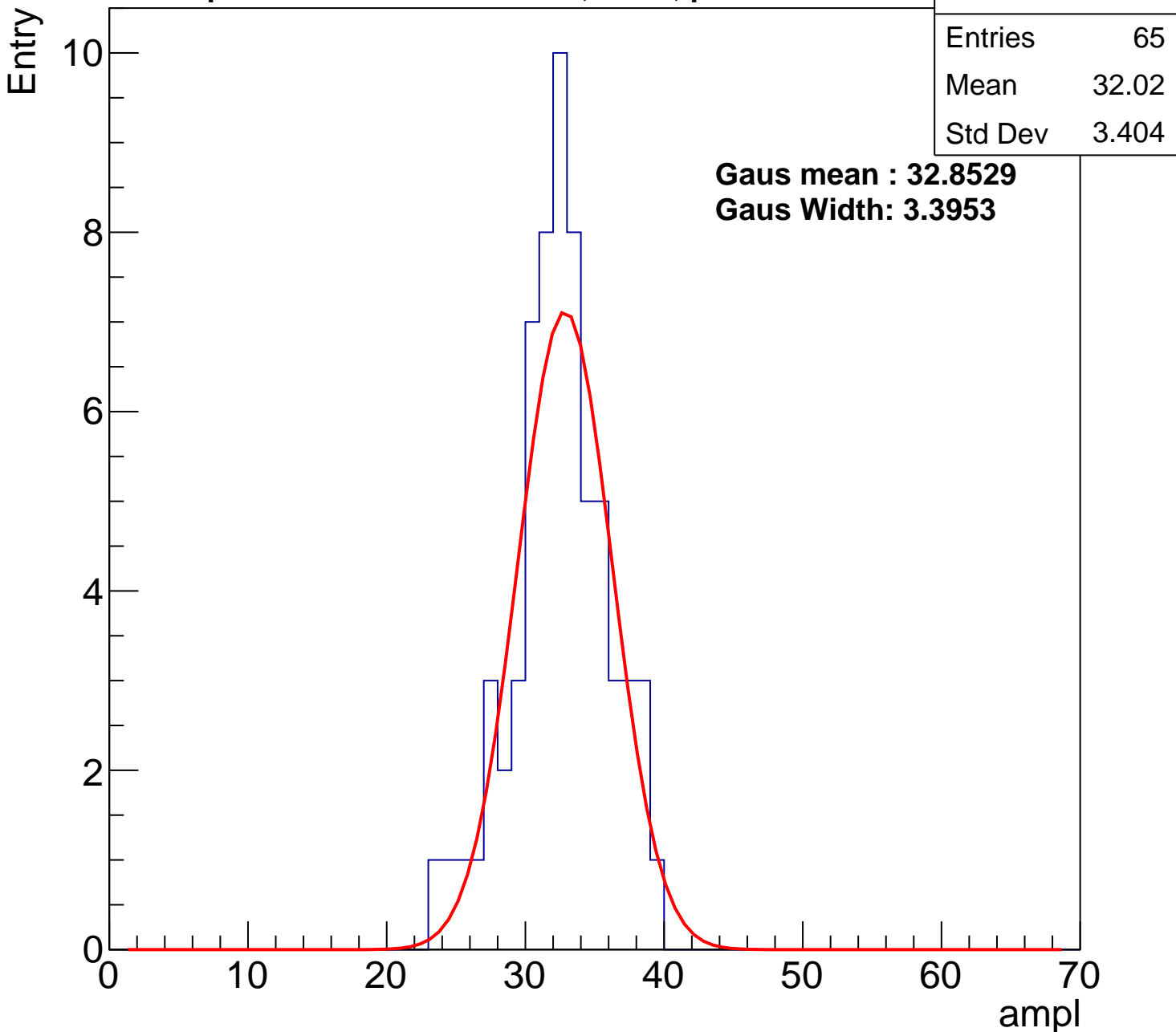
Gaus Width: 3.3953

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch27, adc1

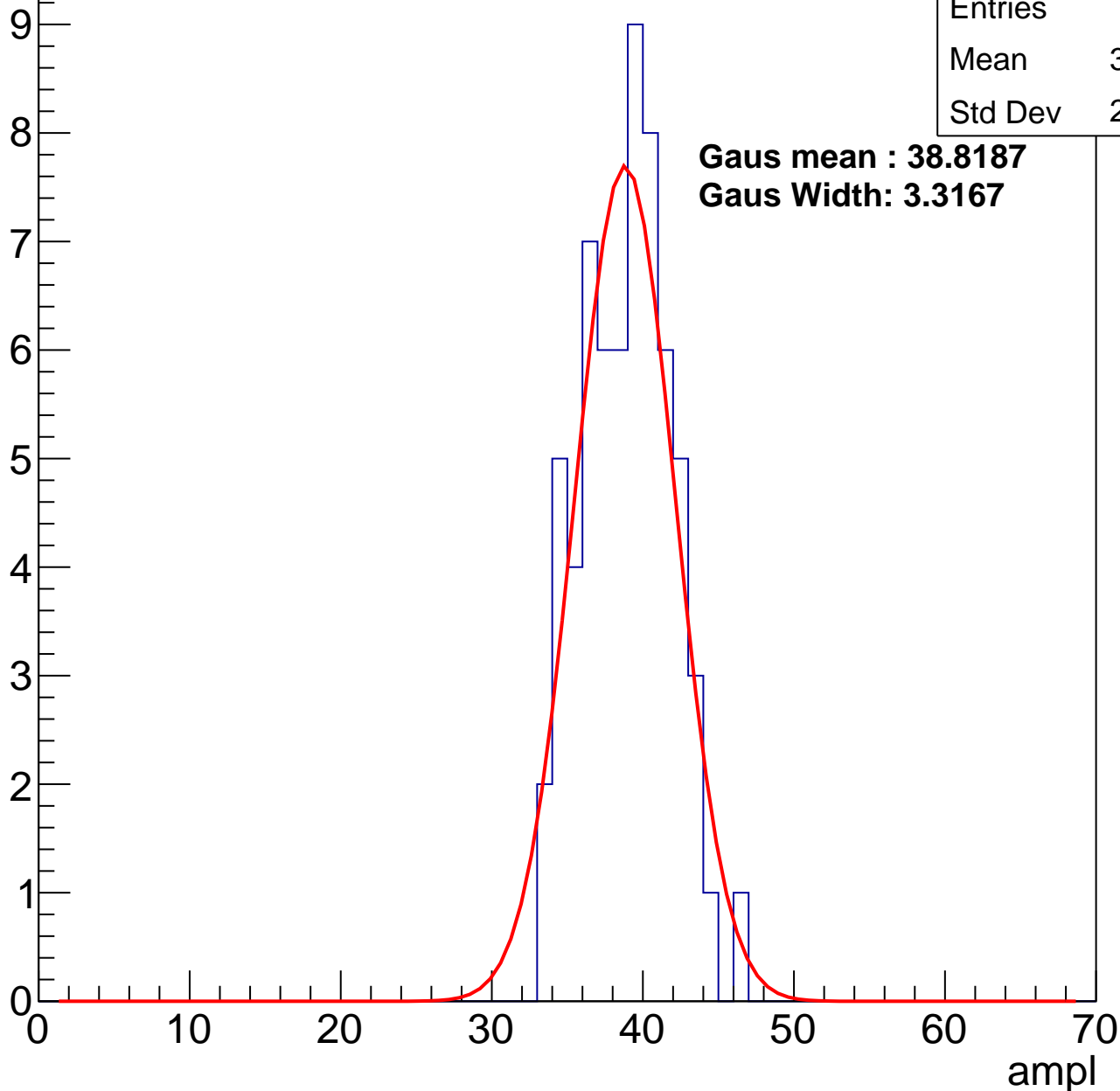
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.48
Std Dev	2.916

Gaus mean : 38.8187

Gaus Width: 3.3167



B1L101S, U25-ch27, adc2

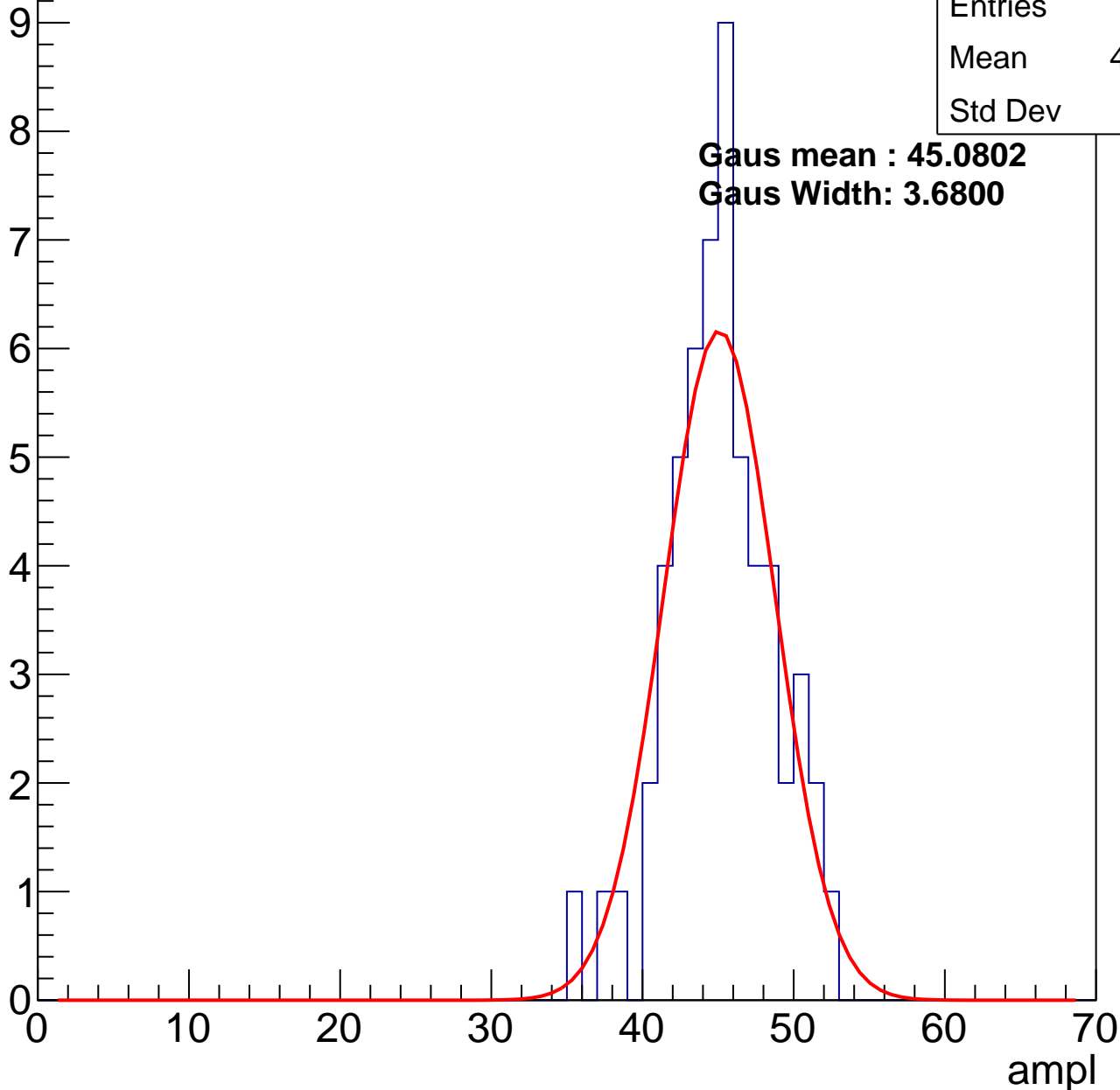
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.68
Std Dev	3.46

Gaus mean : 45.0802

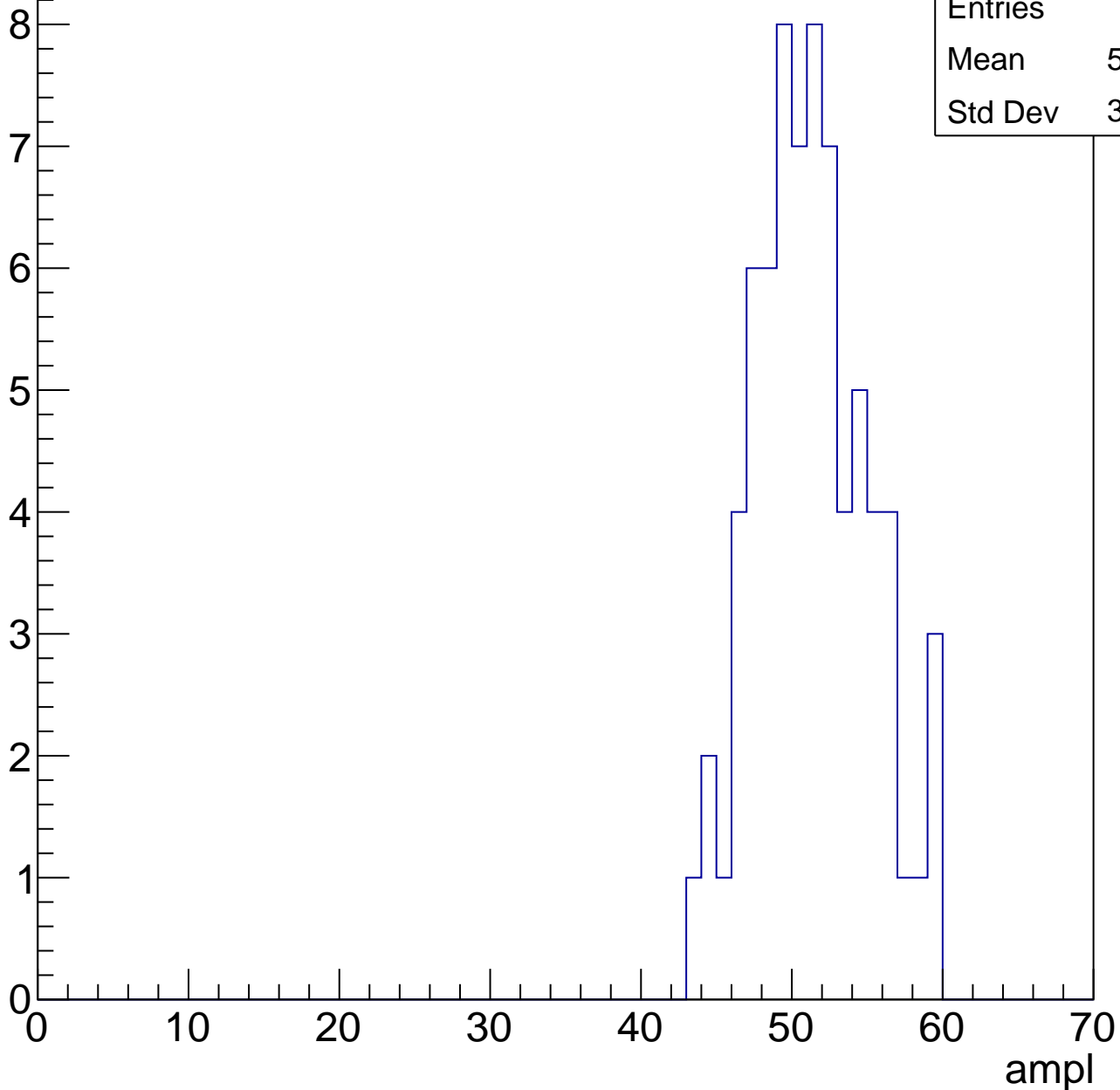
Gaus Width: 3.6800



B1L101S, U25-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



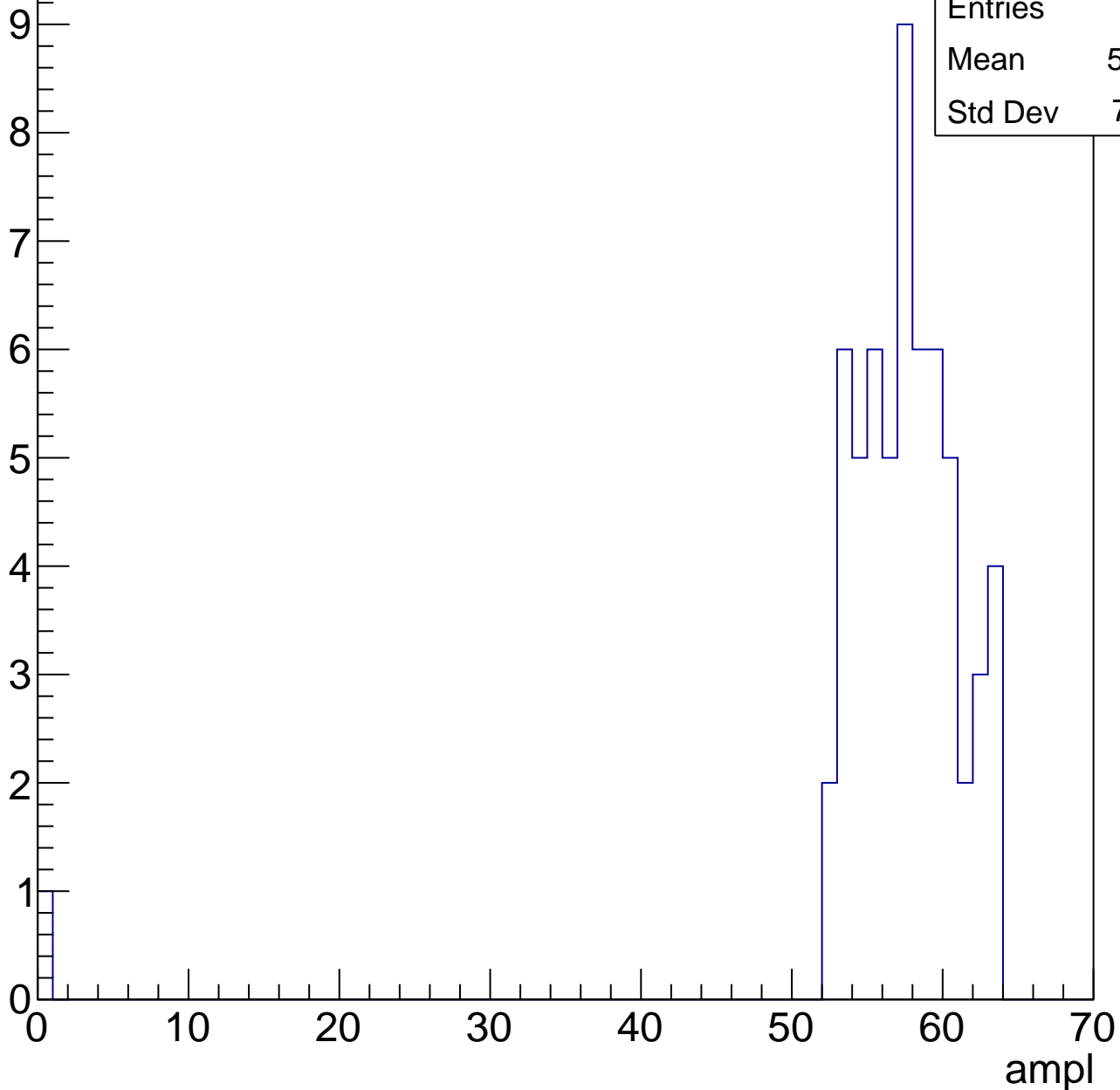
Entries	72
Mean	50.86
Std Dev	3.717

B1L101S, U25-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	56.28
Std Dev	7.921

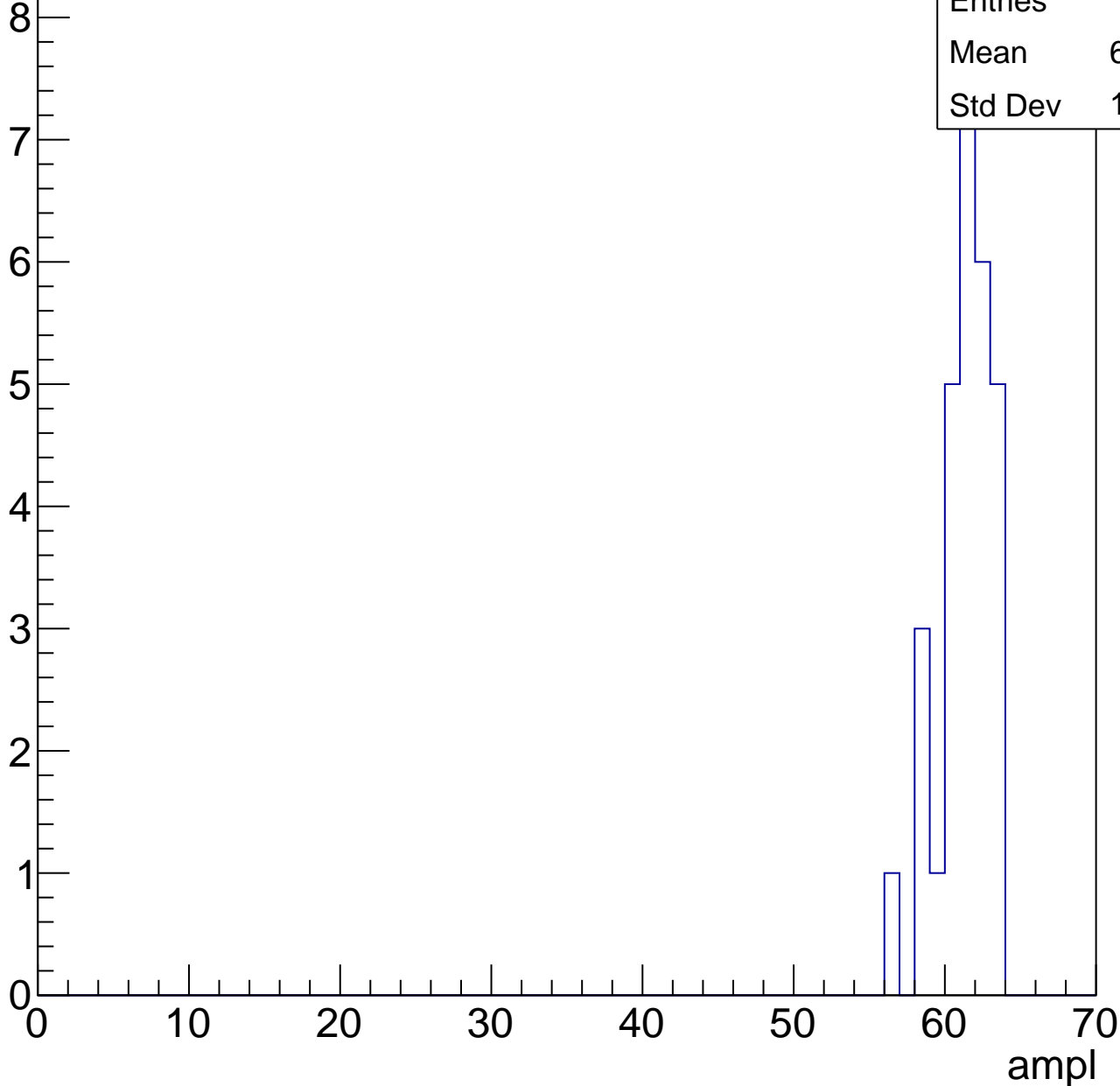


B1L101S, U25-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	60.83
Std Dev	1.723



B1L101S, U25-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch28, adc0

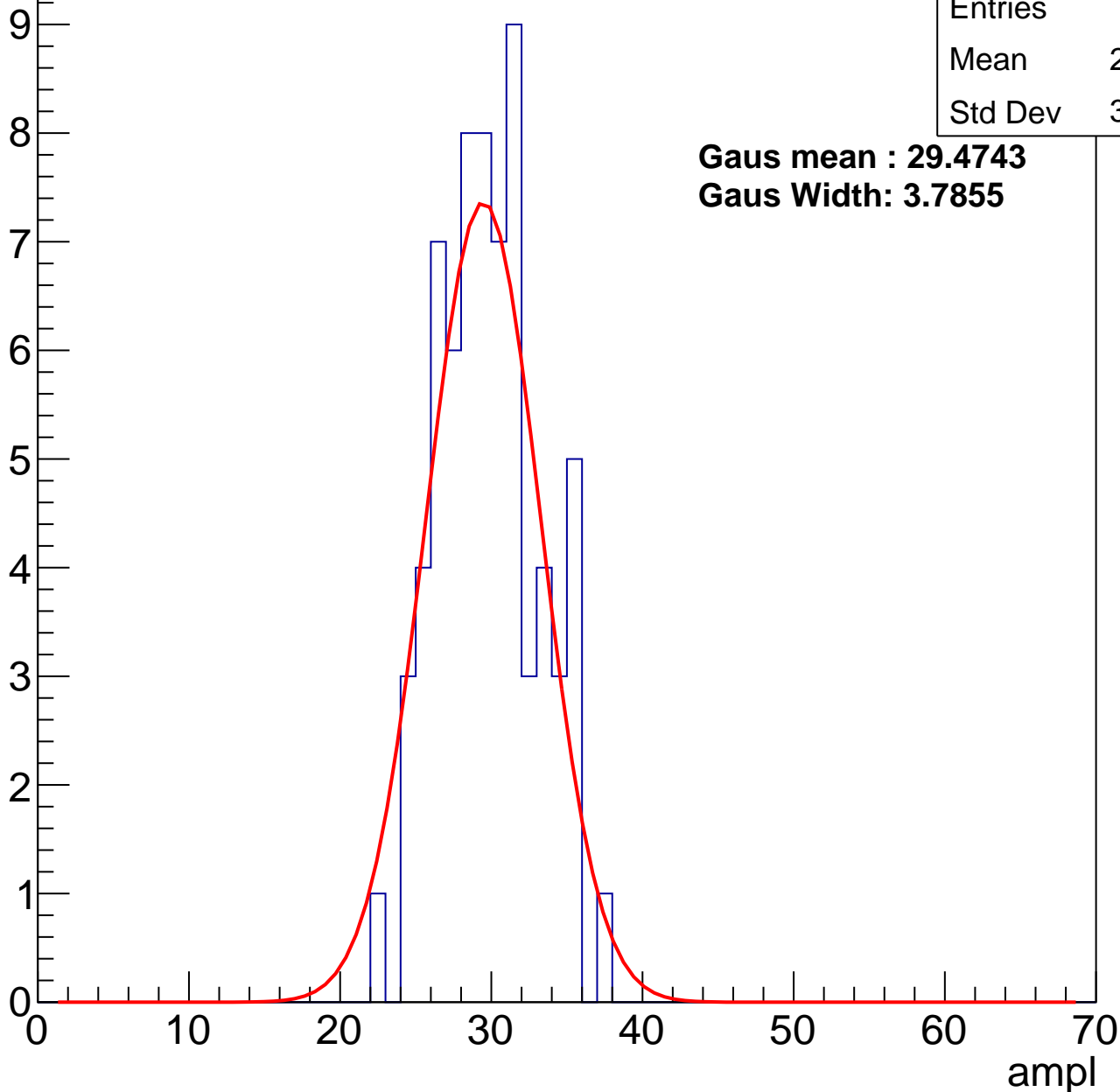
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.35
Std Dev	3.247

Gaus mean : 29.4743

Gaus Width: 3.7855



B1L101S, U25-ch28, adc1

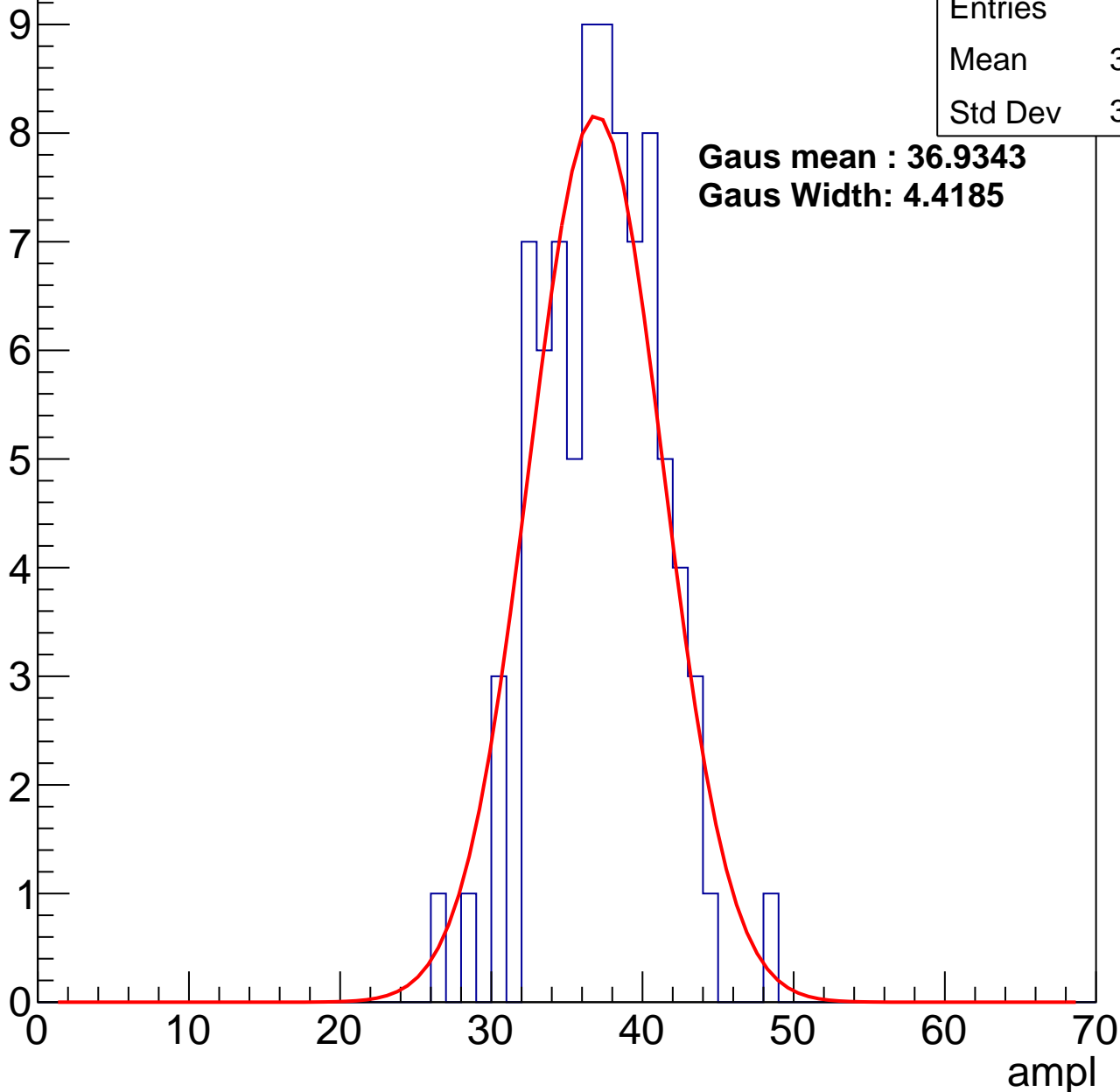
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	36.79
Std Dev	3.872

Gaus mean : 36.9343

Gaus Width: 4.4185



B1L101S, U25-ch28, adc2

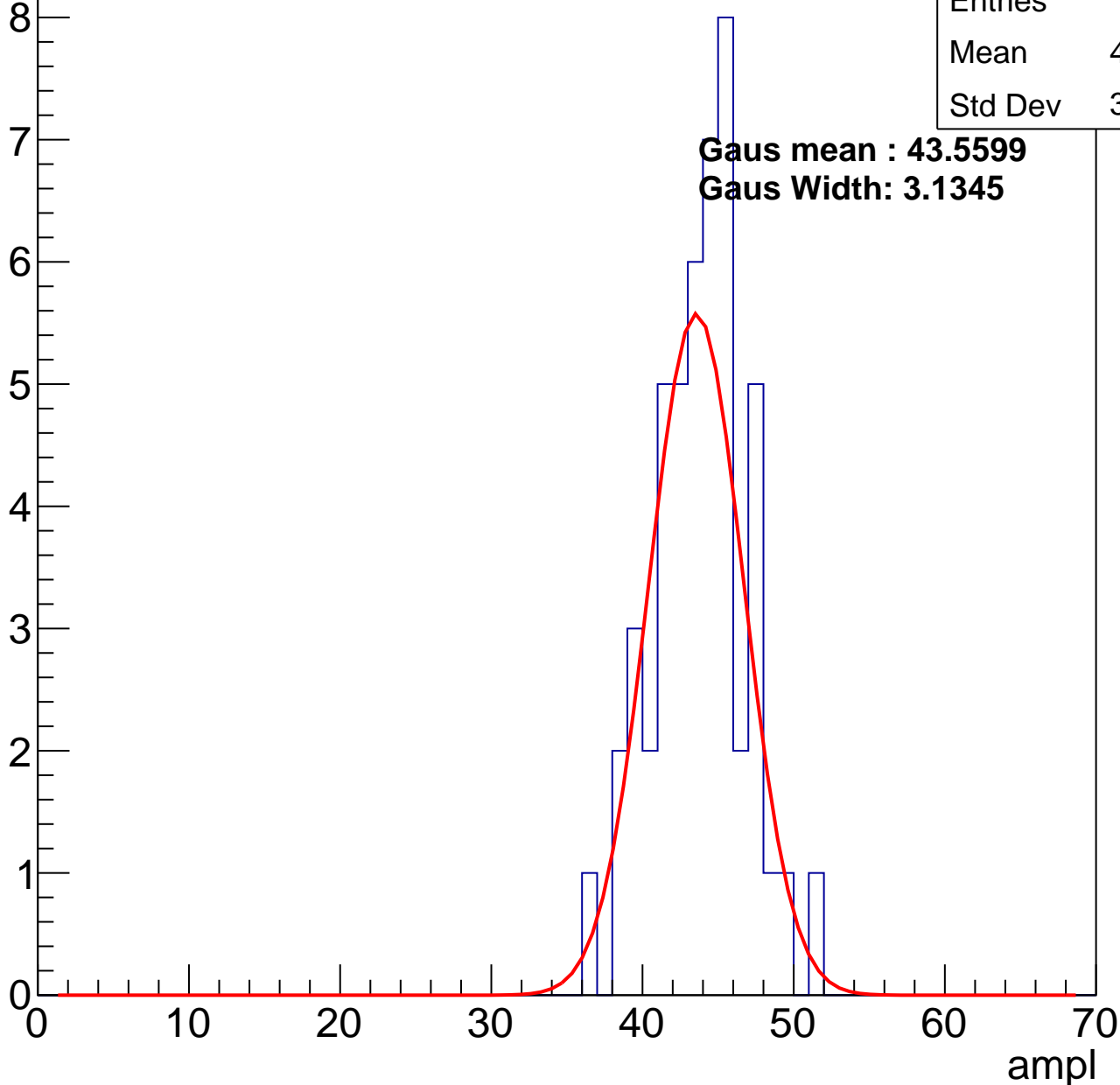
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	43.37
Std Dev	3.015

Gaus mean : 43.5599

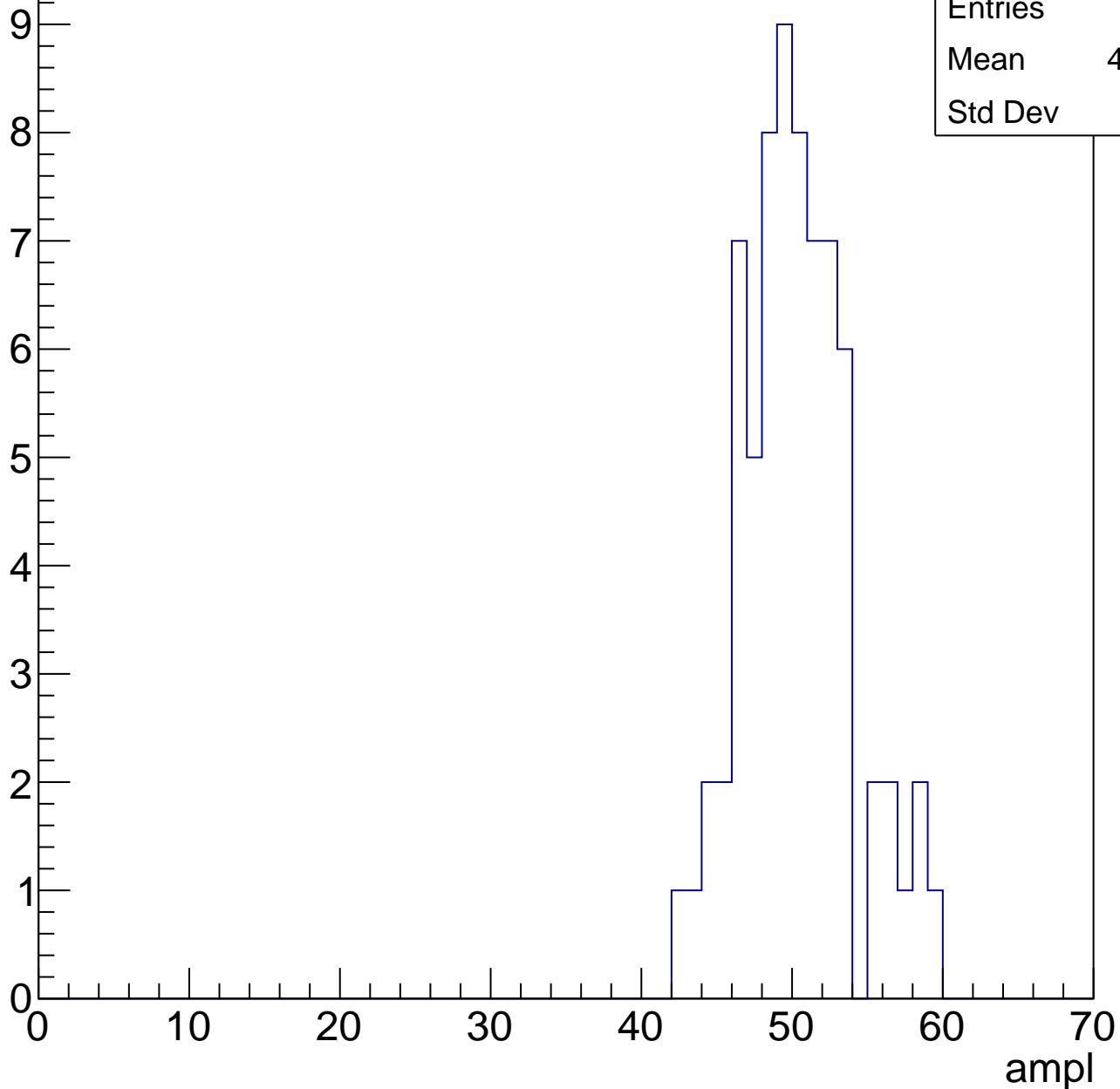
Gaus Width: 3.1345



B1L101S, U25-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

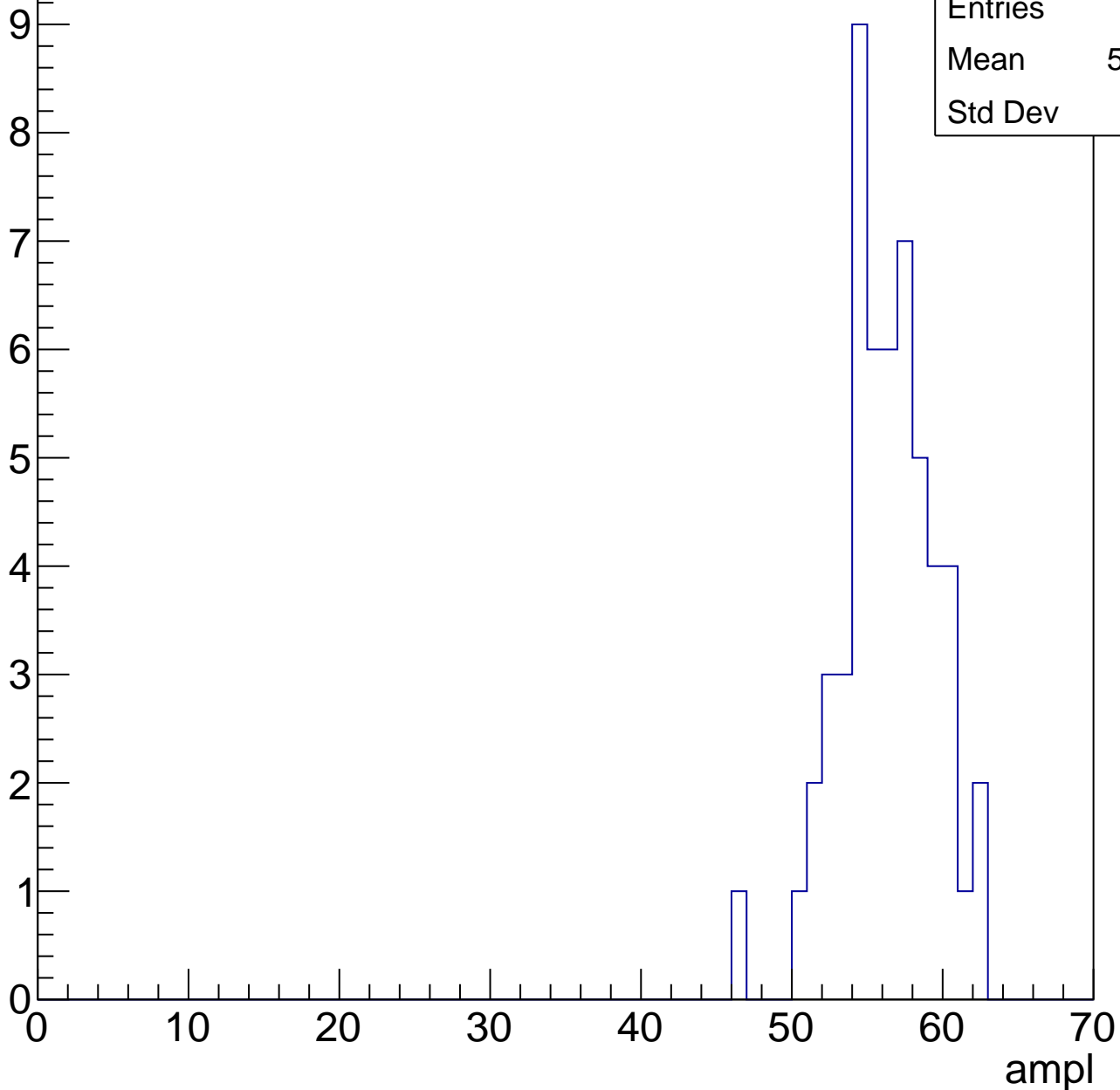


B1L101S, U25-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

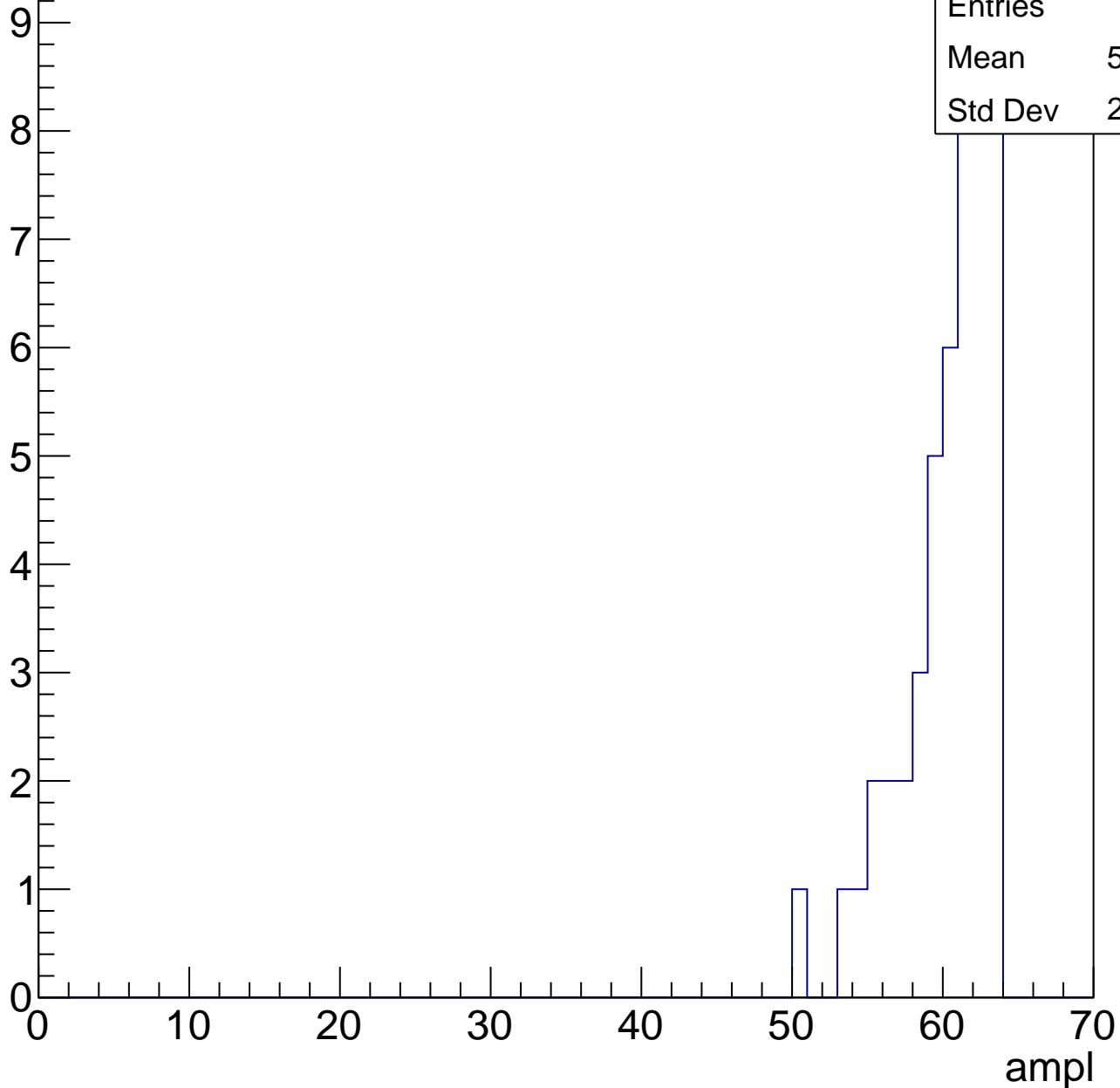
Entries	54
Mean	55.83
Std Dev	3.12



B1L101S, U25-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch29, adc0

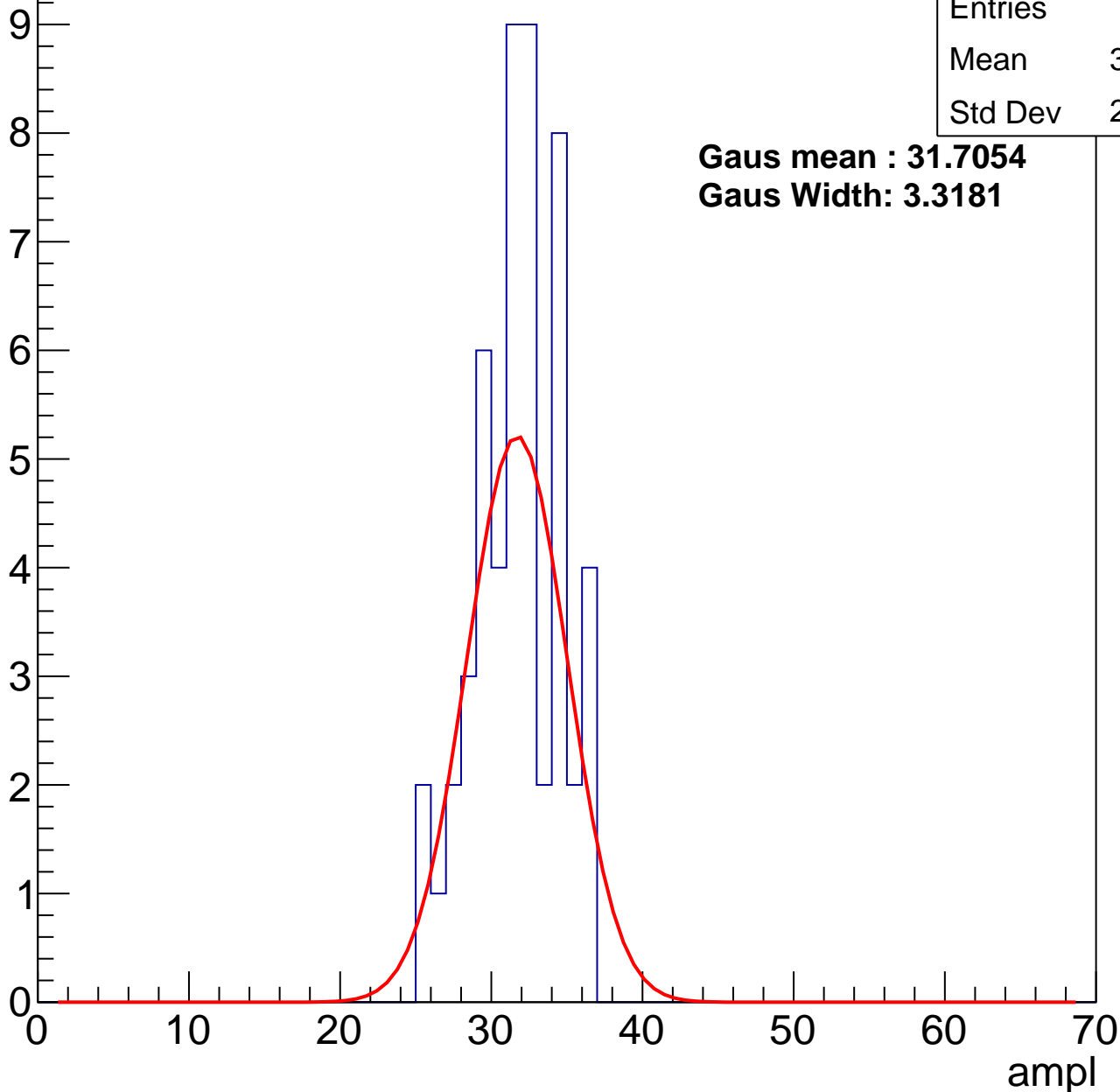
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	31.29
Std Dev	2.776

Gaus mean : 31.7054

Gaus Width: 3.3181



B1L101S, U25-ch29, adc1

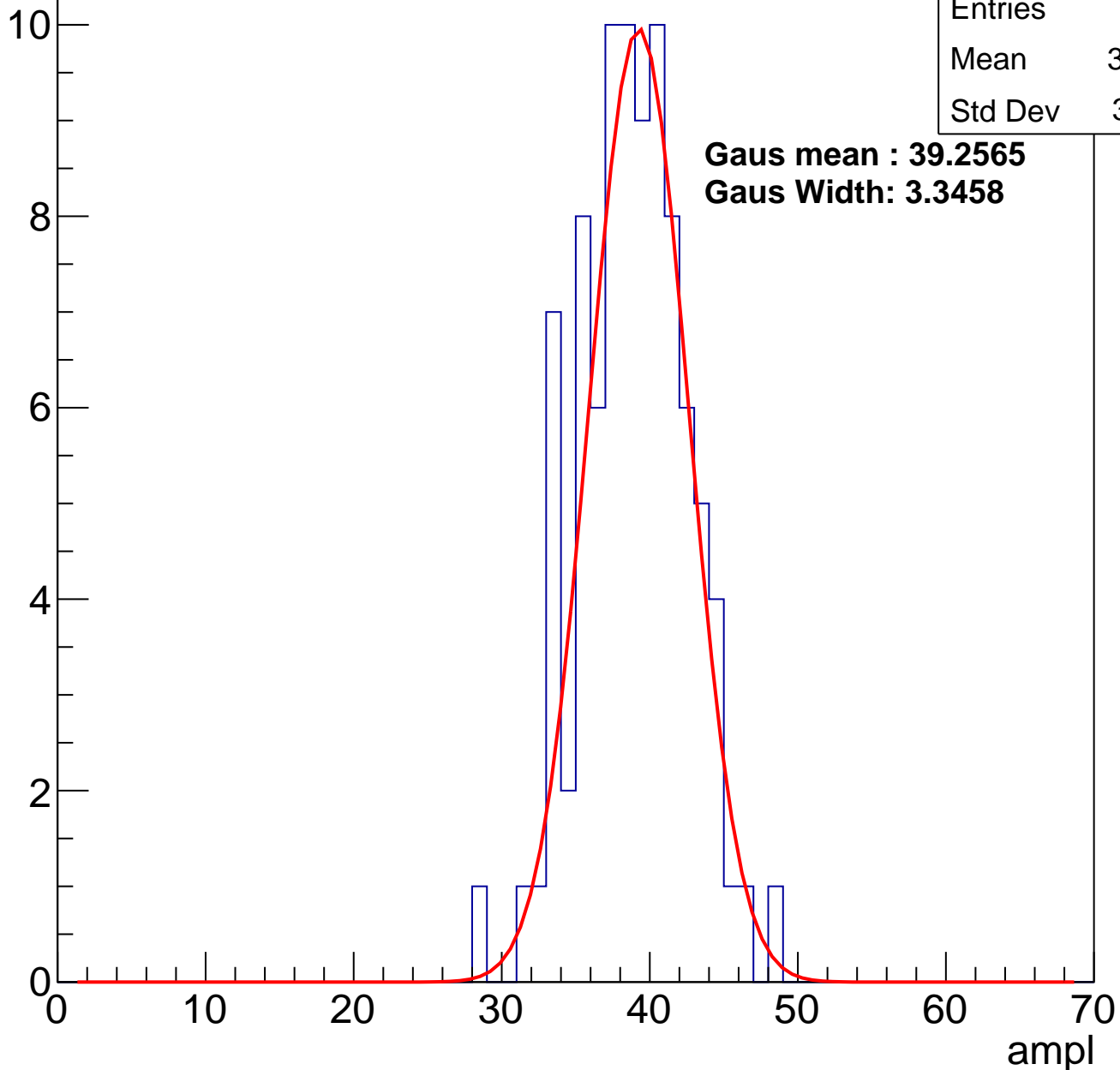
calib_packv5_042523_0143.root, FC#0, port D2

Entries	91
Mean	38.43
Std Dev	3.601

Gaus mean : 39.2565

Gaus Width: 3.3458

Entry



B1L101S, U25-ch29, adc2

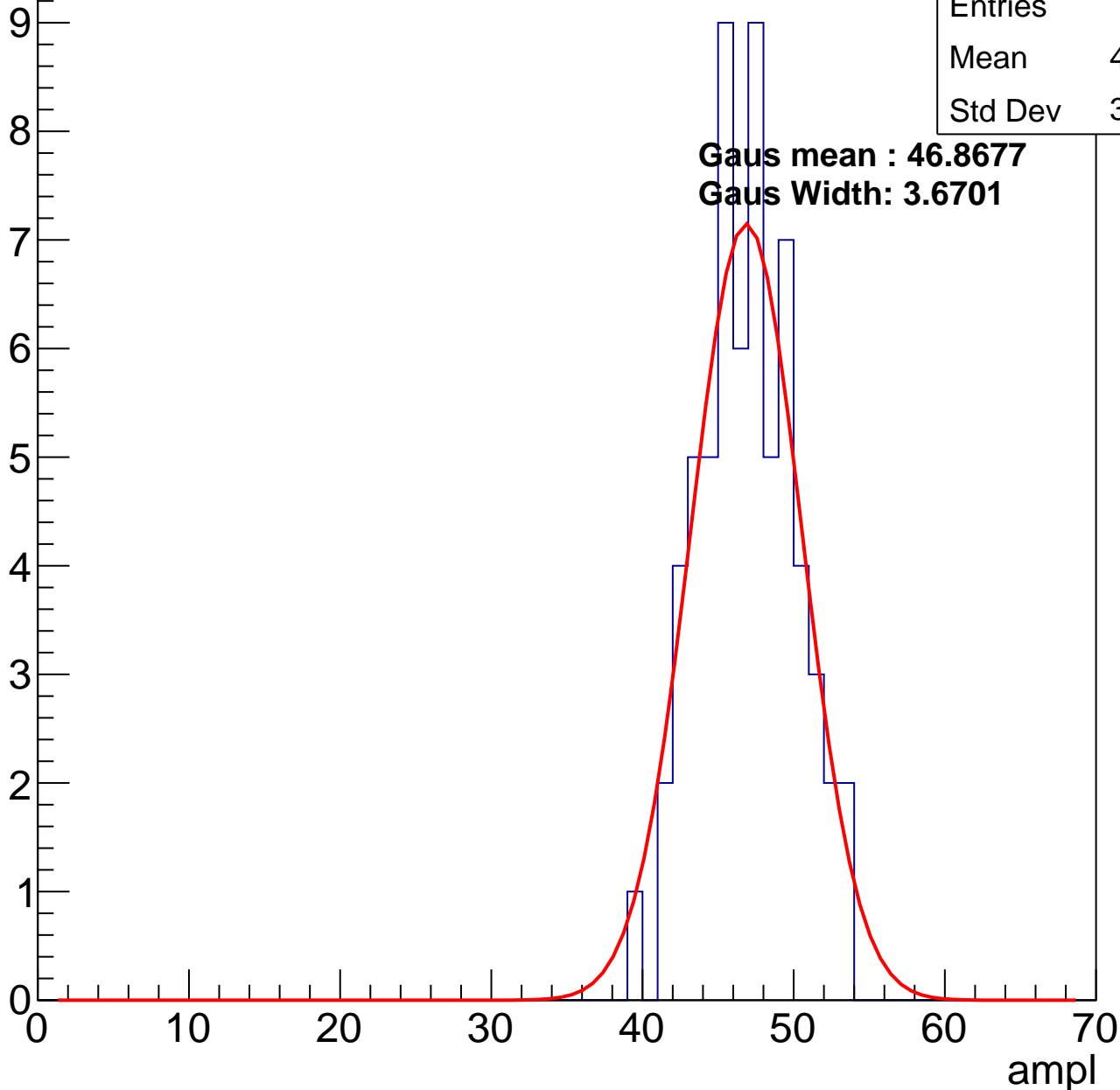
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	46.47
Std Dev	3.122

Gaus mean : 46.8677

Gaus Width: 3.6701

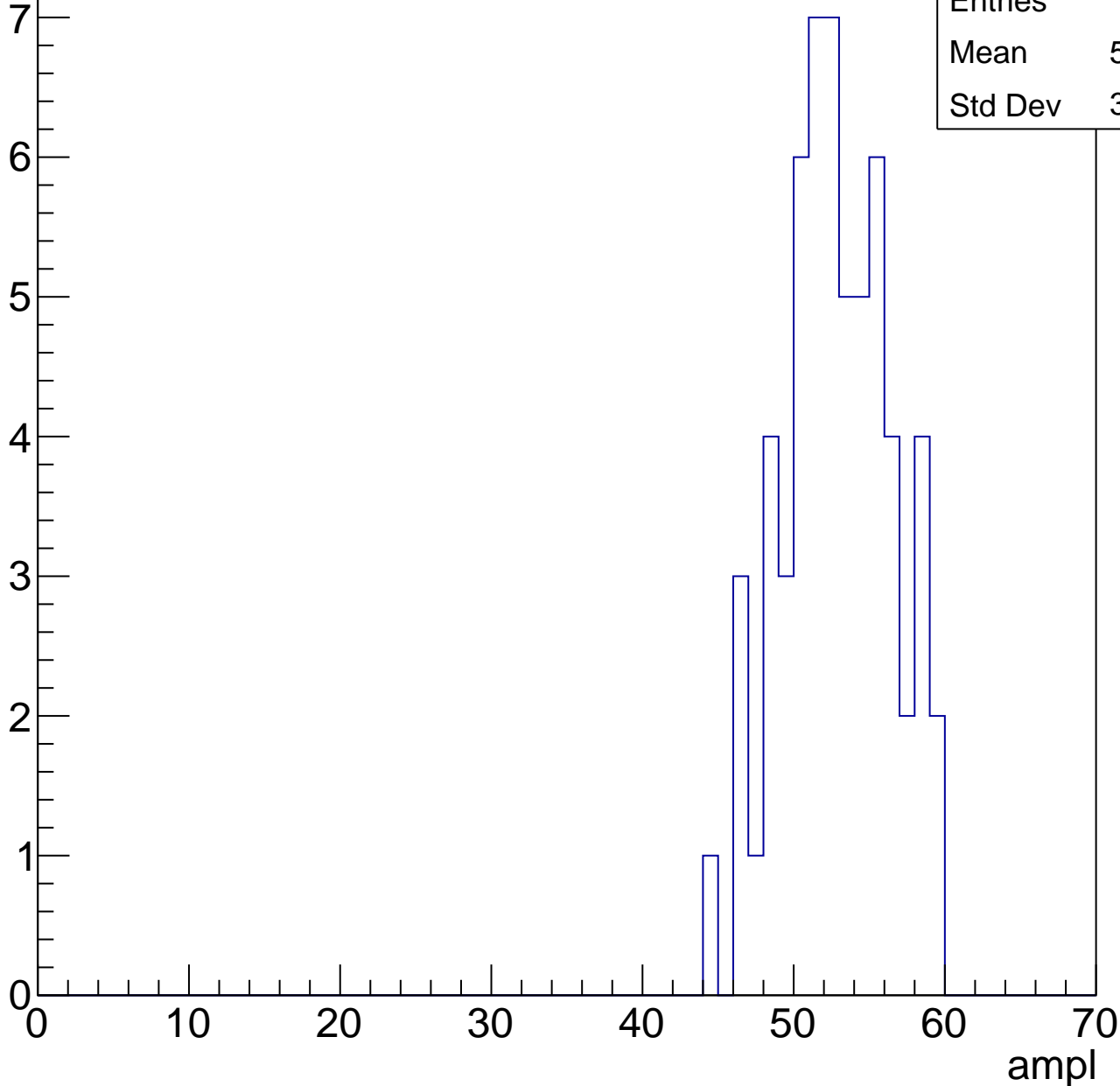


B1L101S, U25-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	52.37
Std Dev	3.526

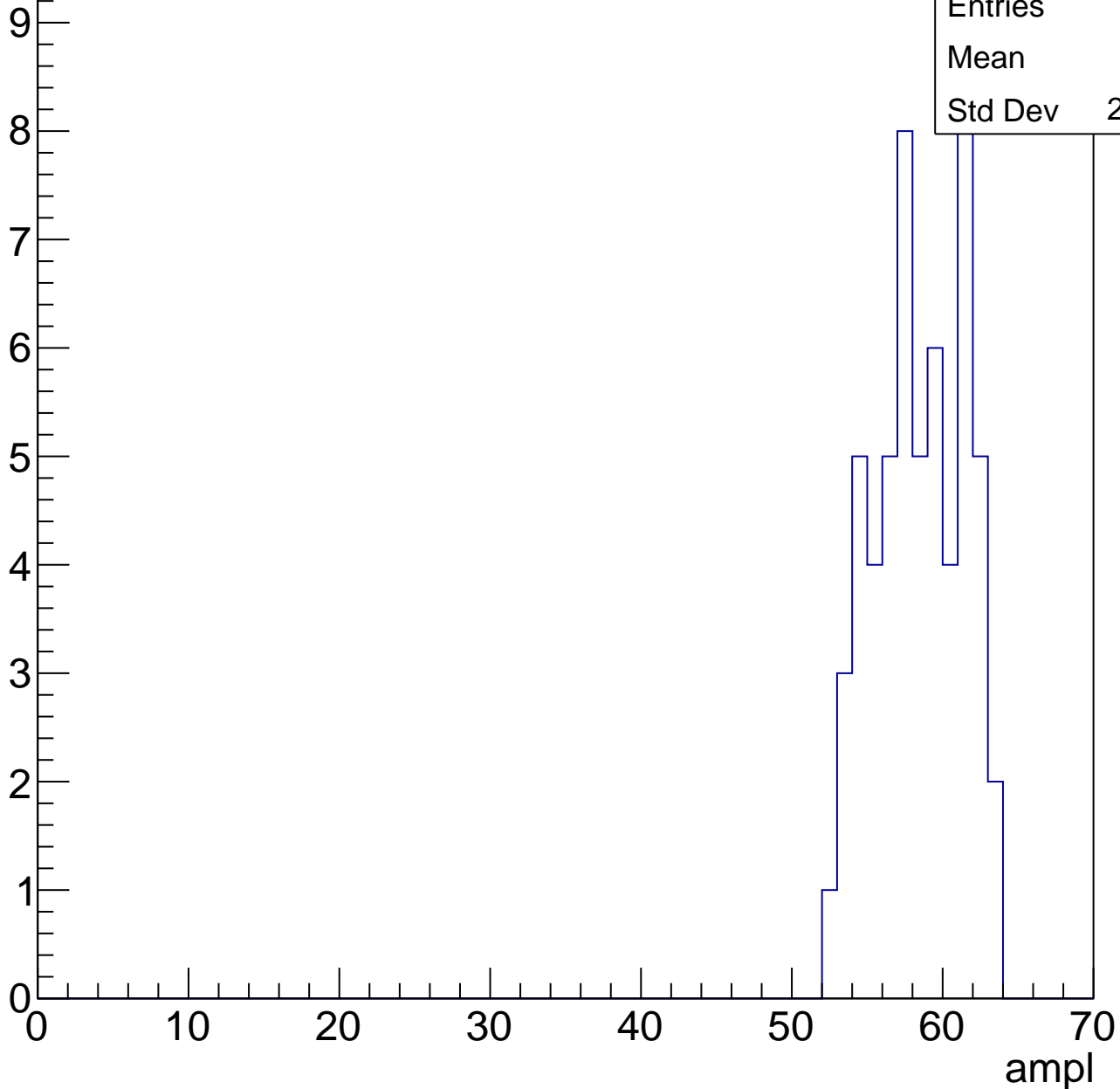


B1L101S, U25-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	58
Std Dev	2.926

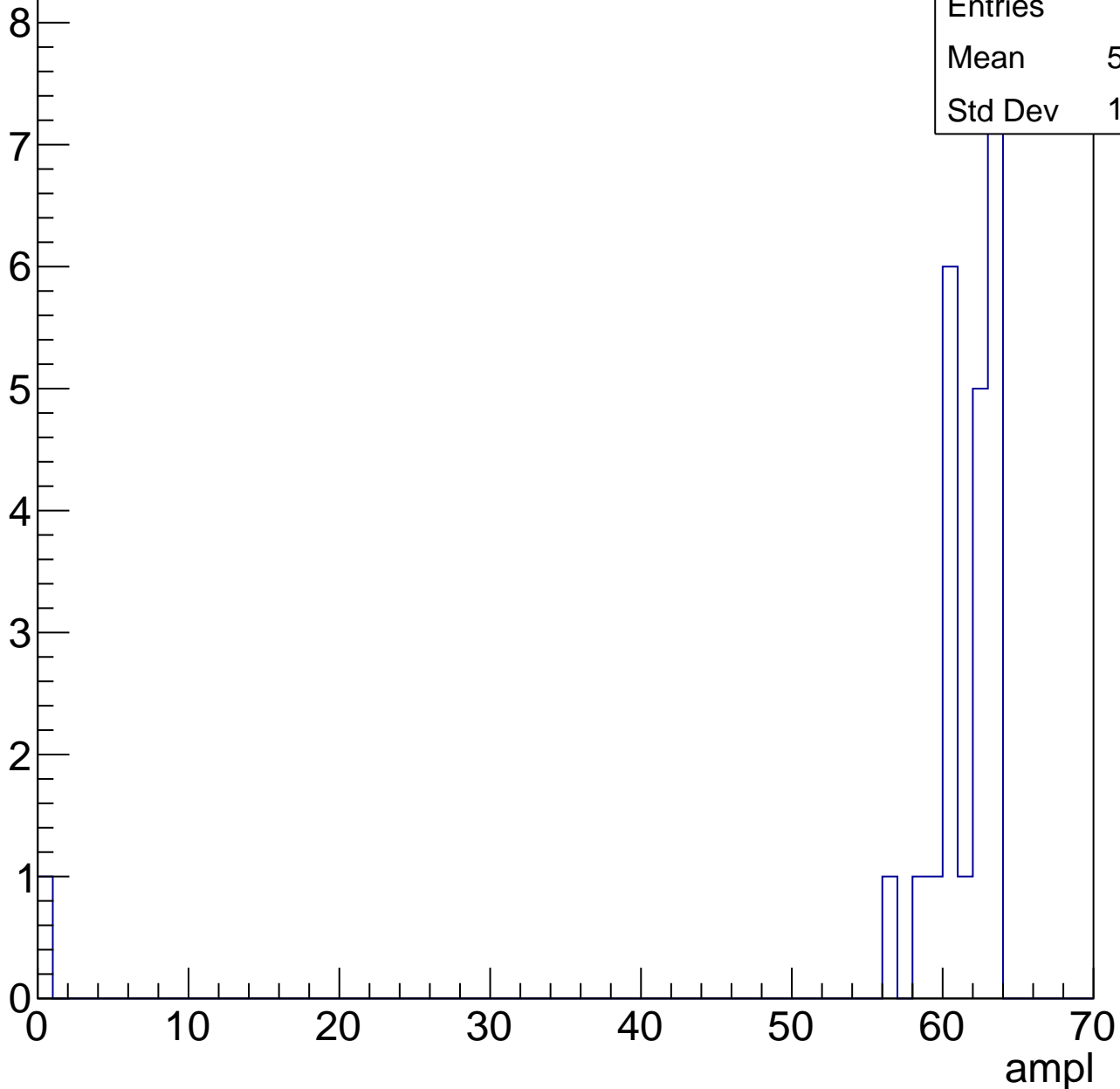


B1L101S, U25-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

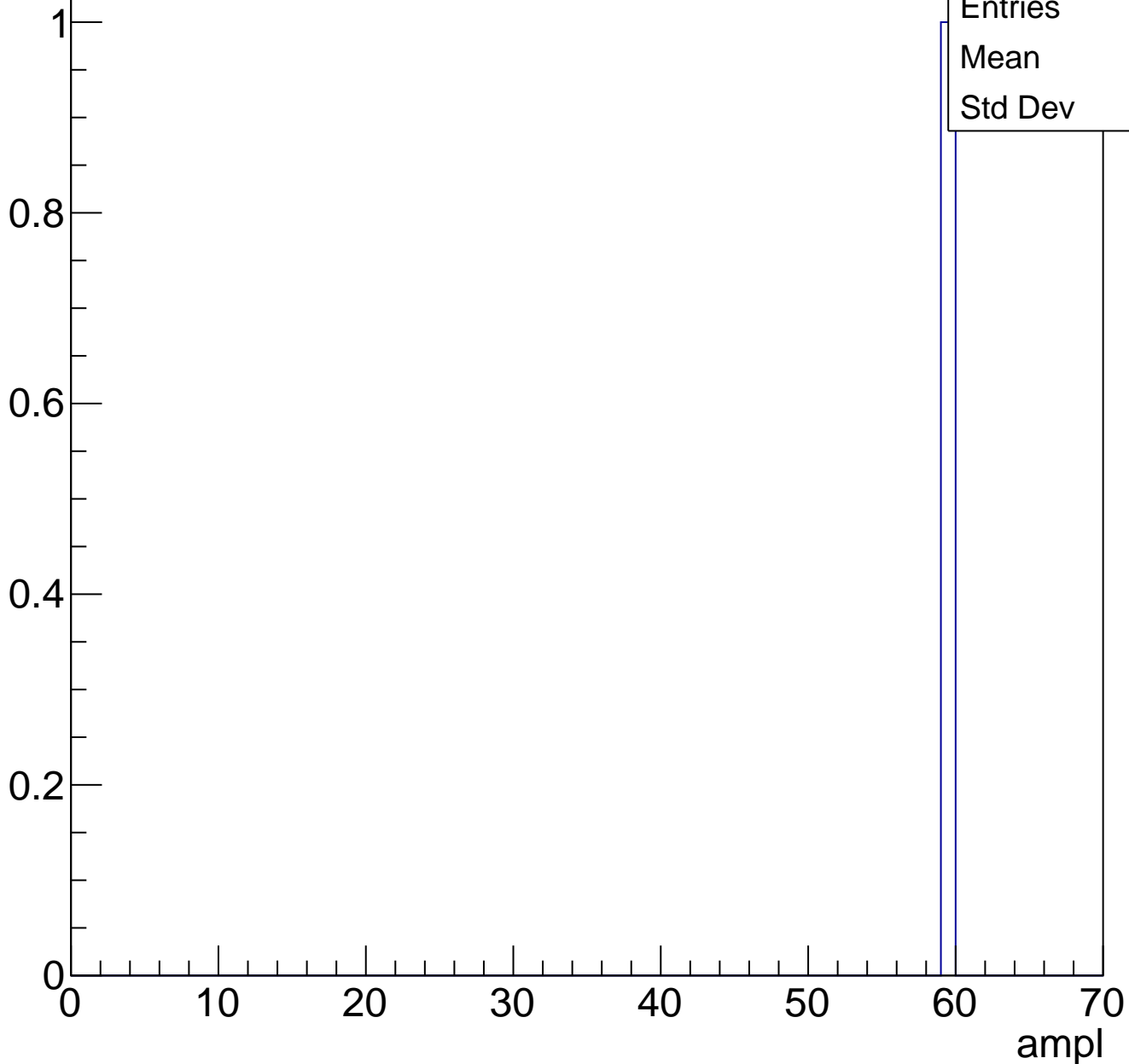
Entries	24
Mean	58.67
Std Dev	12.37



B1L101S, U25-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	59
Std Dev	0

B1L101S, U25-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	1
Mean	24
Std Dev	0

B1L101S, U25-ch30, adc0

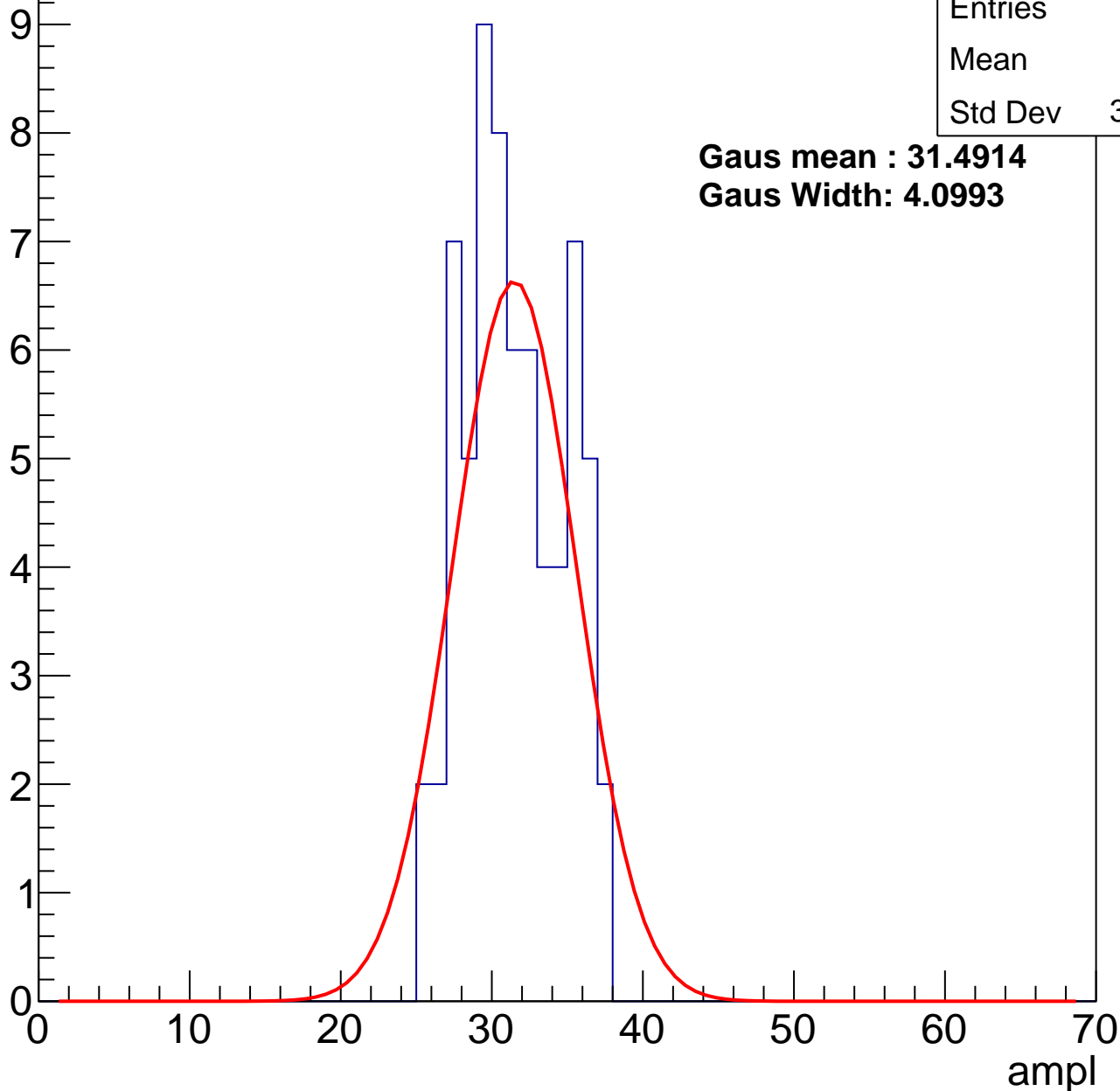
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	31
Std Dev	3.209

Gaus mean : 31.4914

Gaus Width: 4.0993



B1L101S, U25-ch30, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	37.85
Std Dev	3.885

Gaus mean : 38.0377

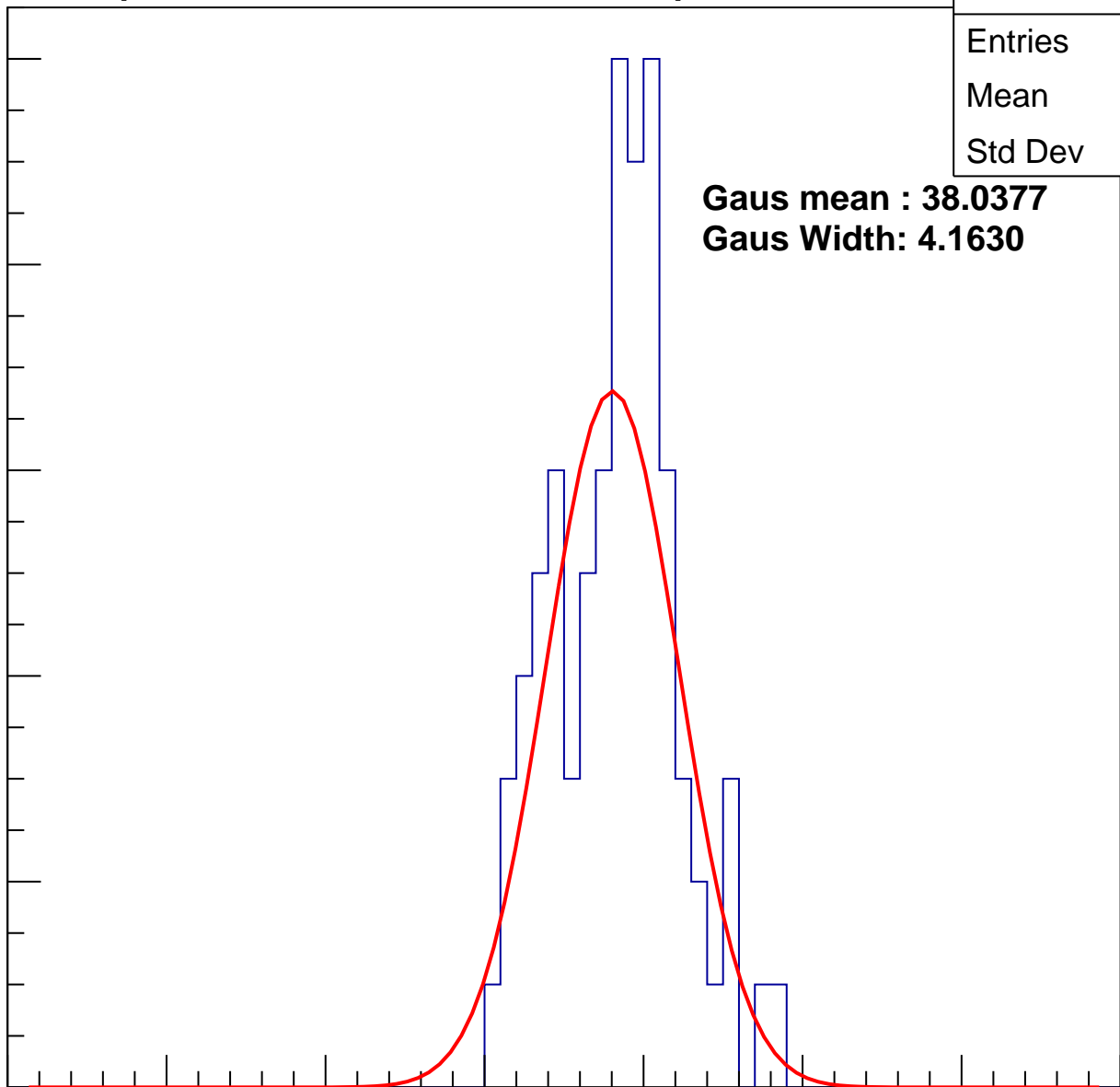
Gaus Width: 4.1630

Entry

10
8
6
4
2
0

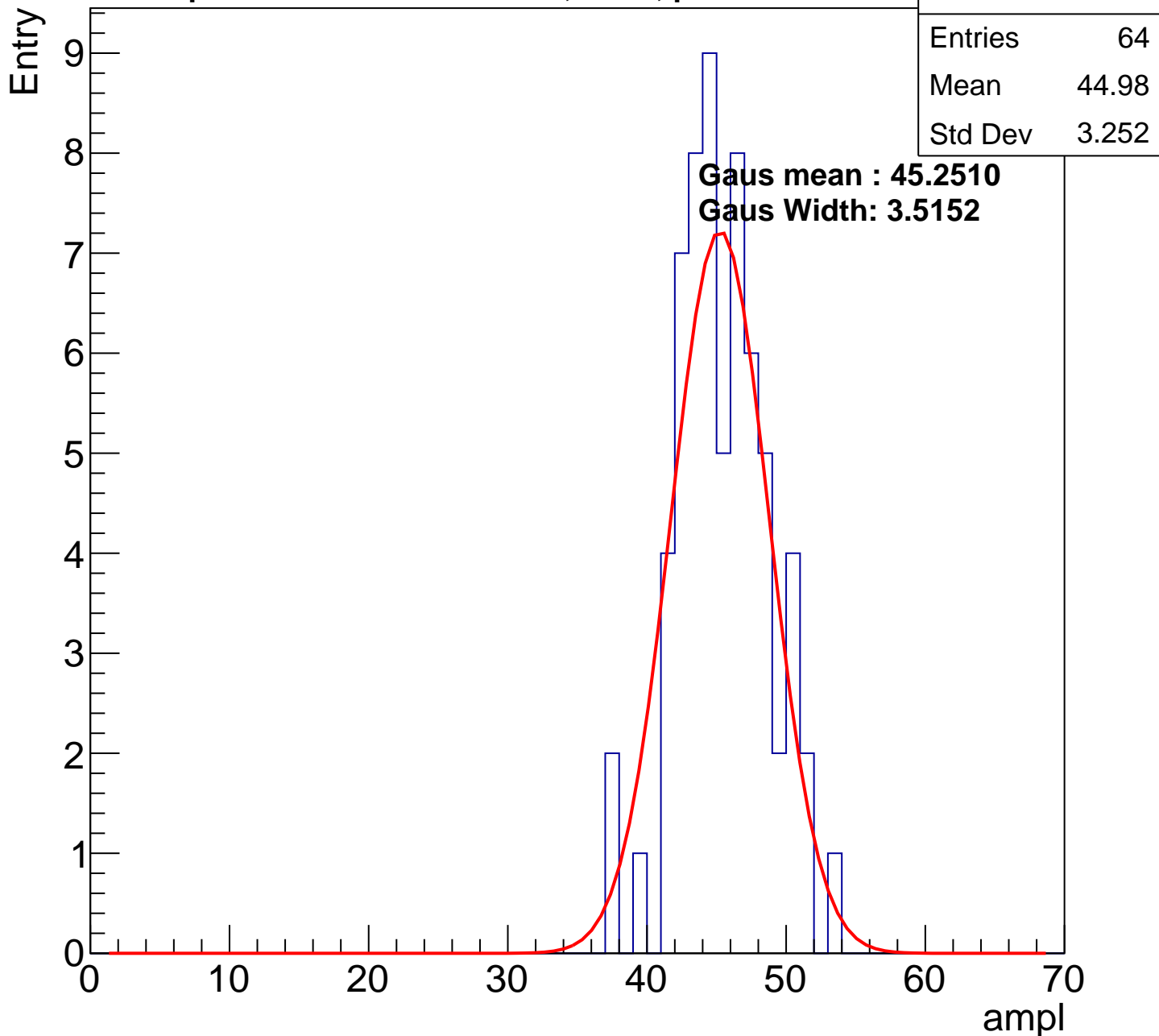
ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch30, adc2

calib_packv5_042523_0143.root, FC#0, port D2

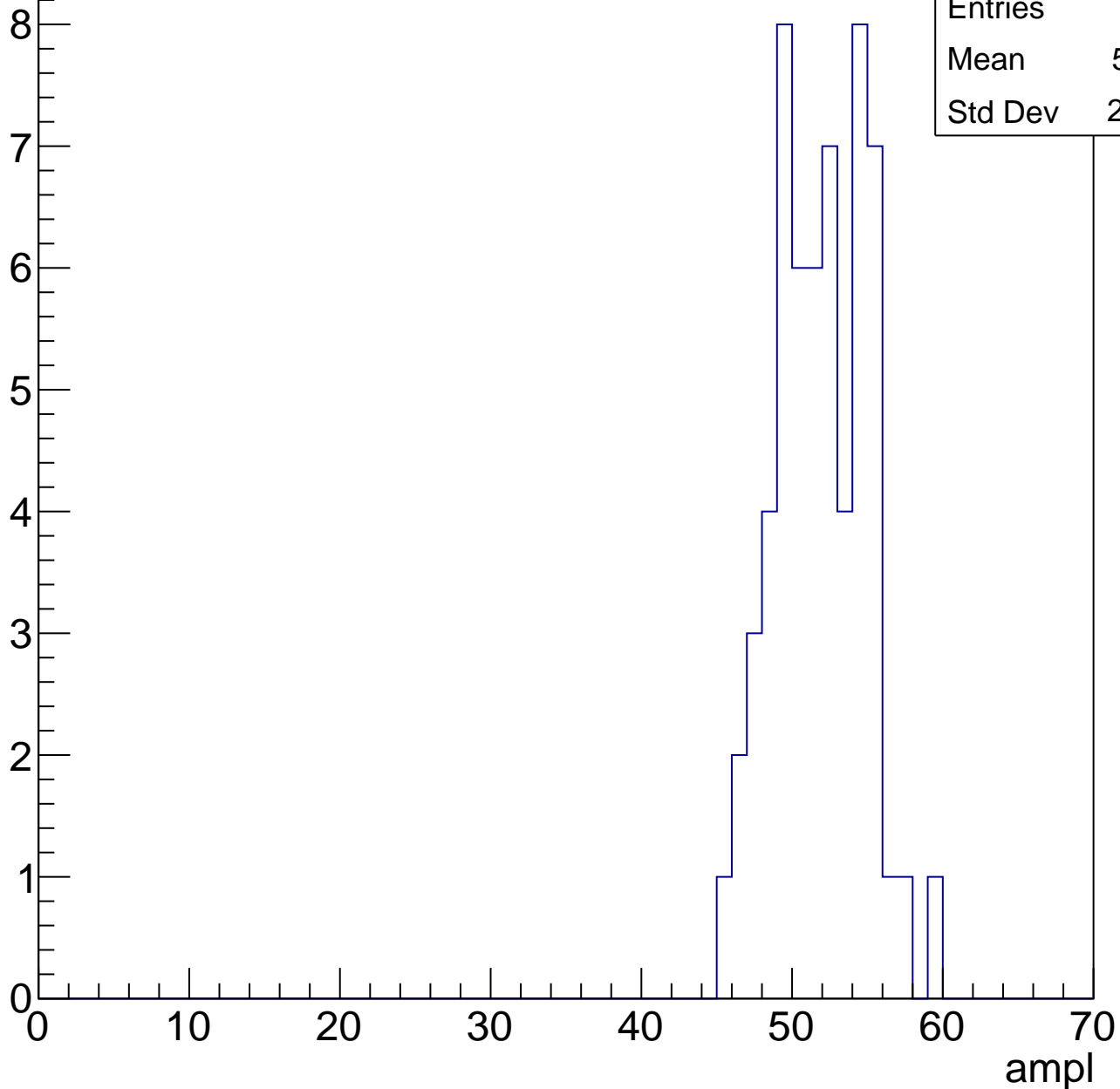


B1L101S, U25-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

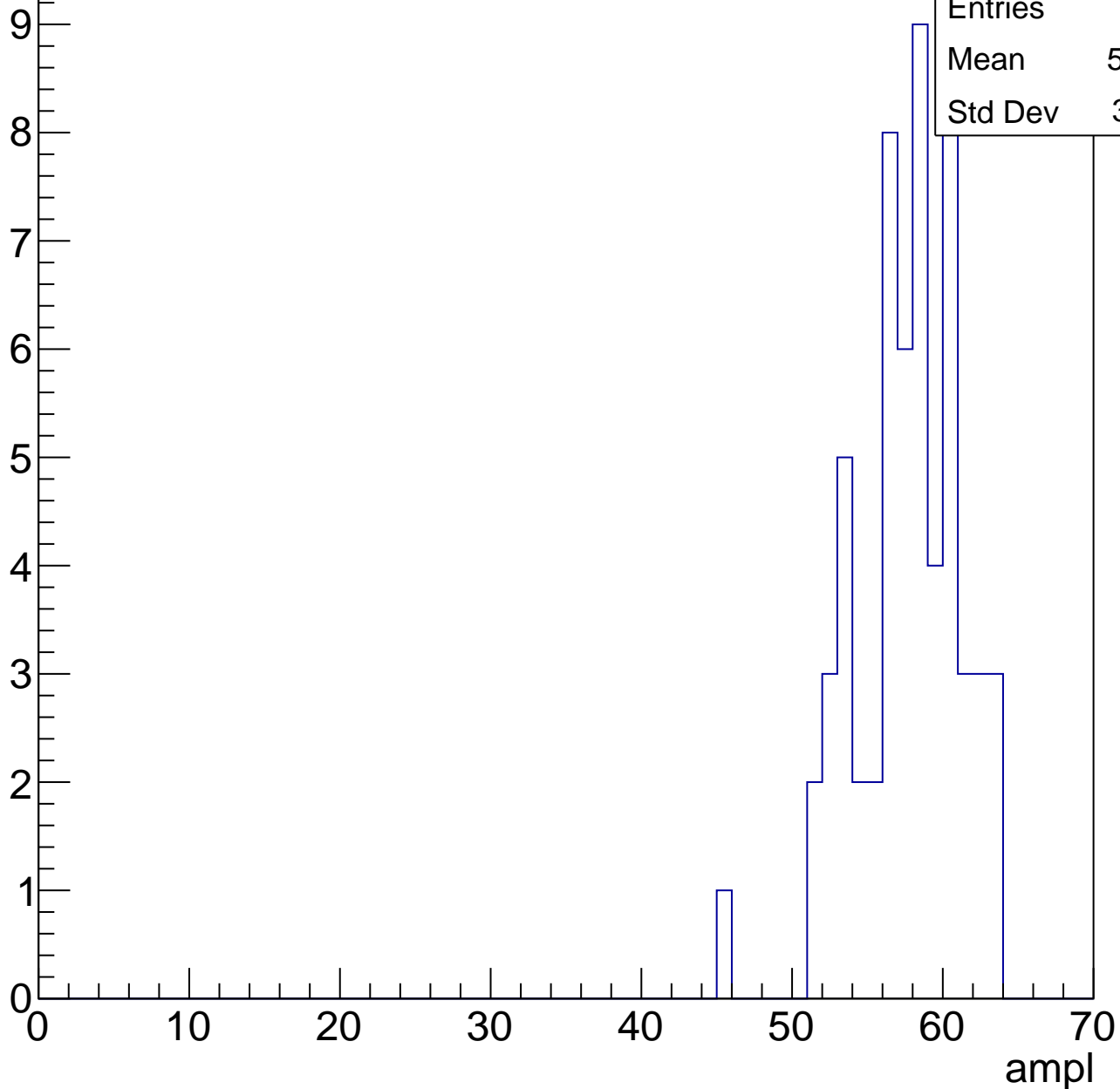
Entries	59
Mean	51.41
Std Dev	2.998



B1L101S, U25-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

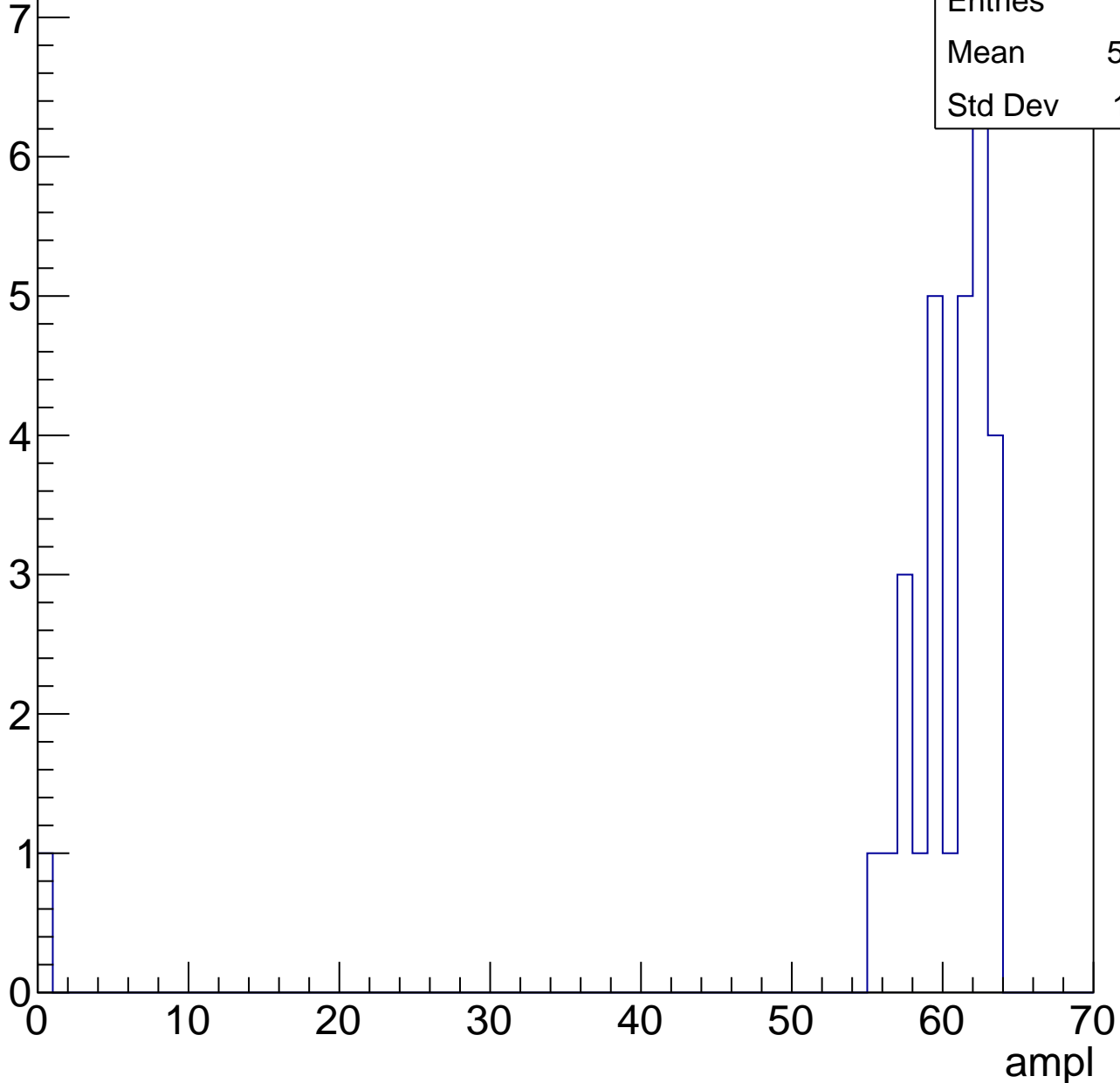


B1L101S, U25-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

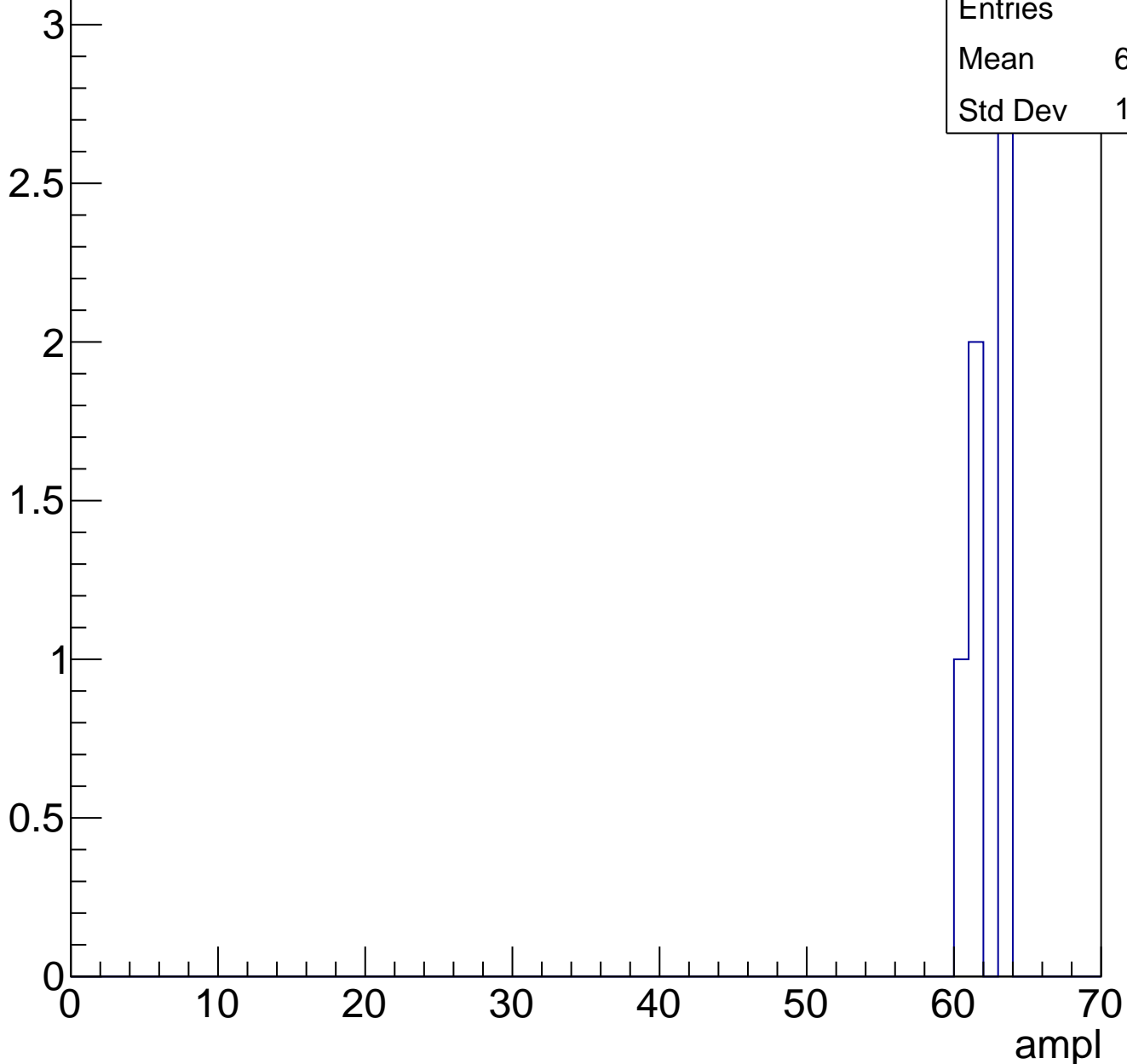
Entries	29
Mean	58.14
Std Dev	11.21



B1L101S, U25-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

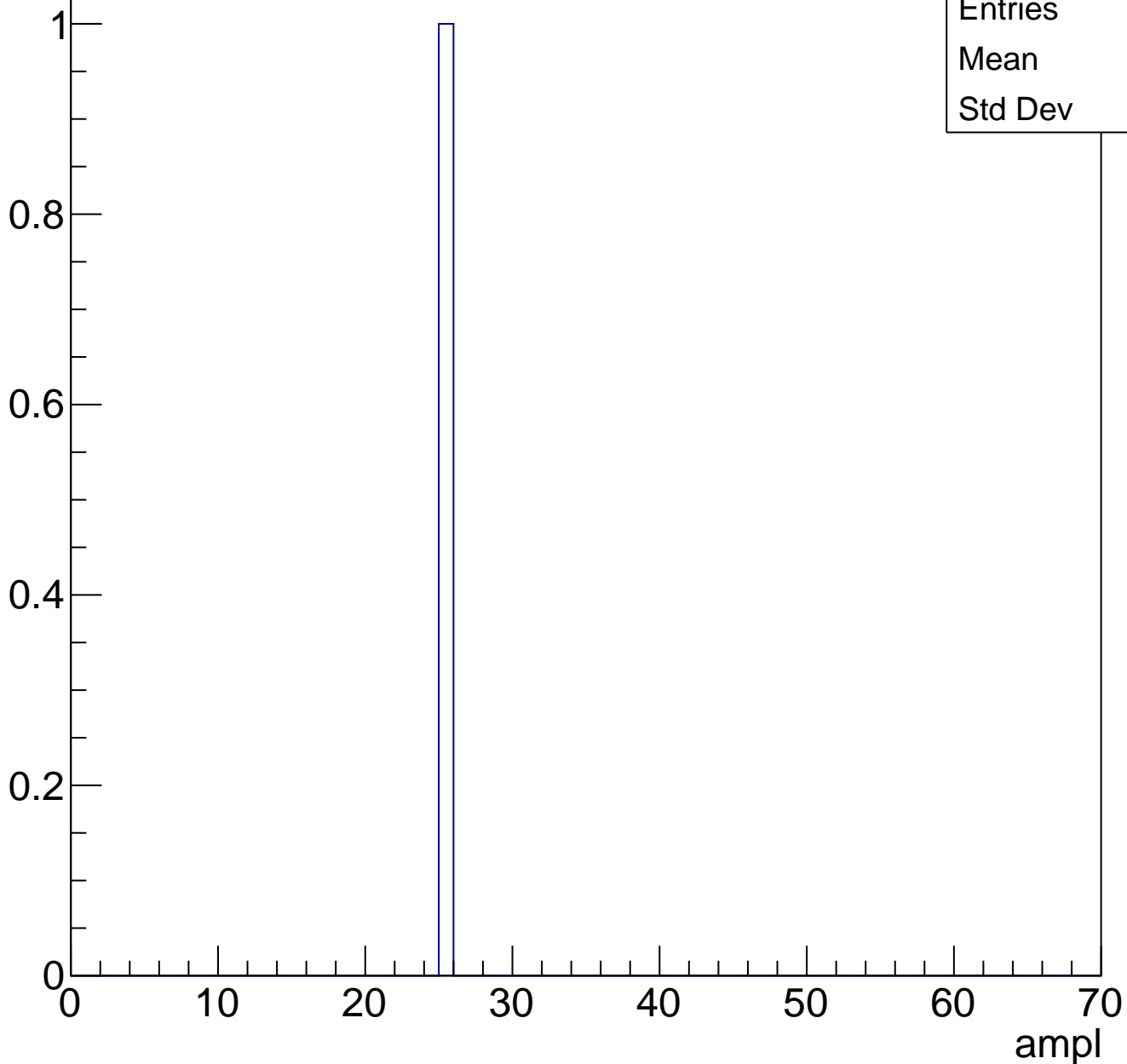
Entry



B1L101S, U25-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	25
Std Dev	0

B1L101S, U25-ch31, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	30.88
Std Dev	3.343

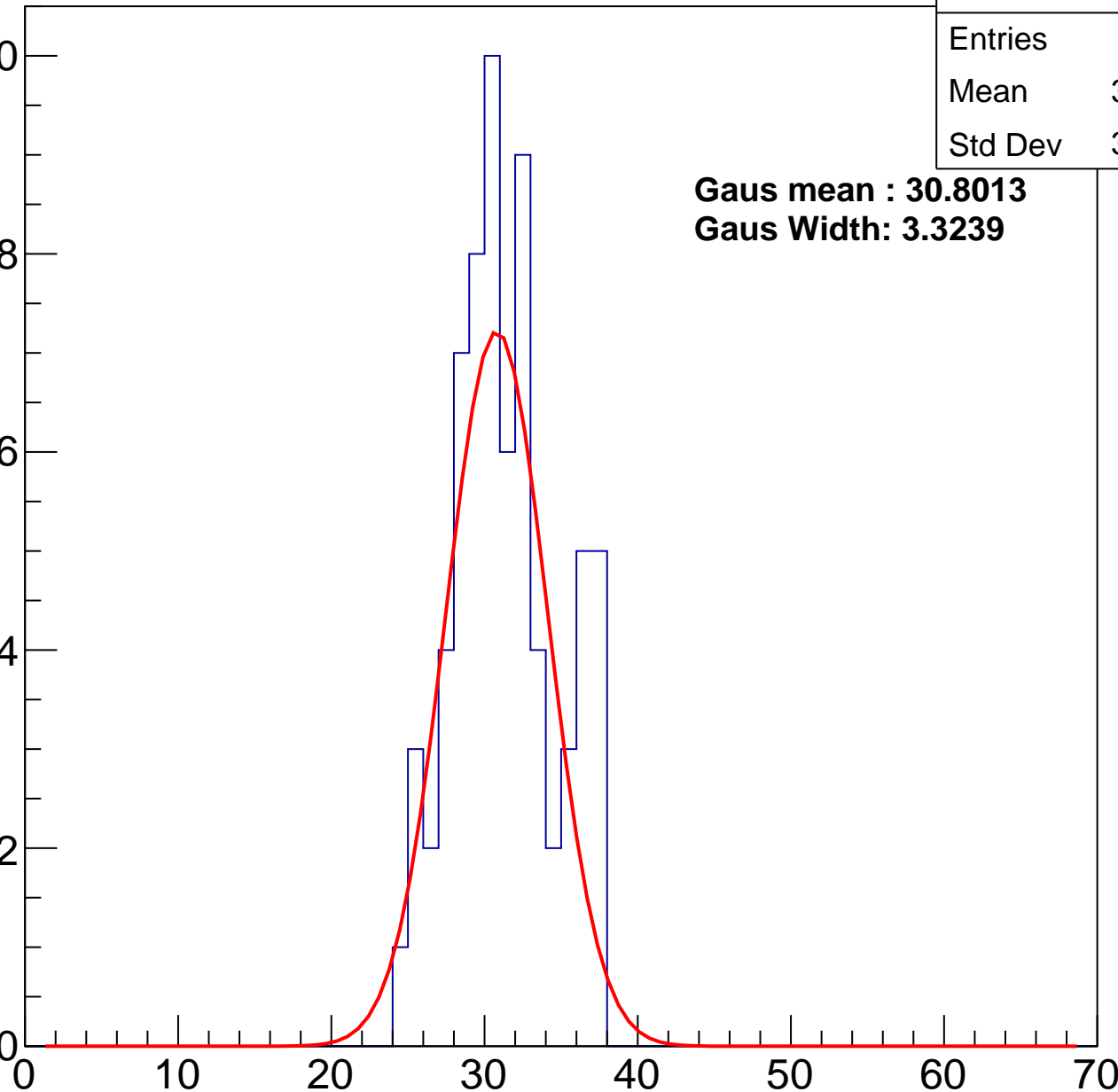
Gaus mean : 30.8013

Gaus Width: 3.3239

Entry

10
8
6
4
2
0

ampl



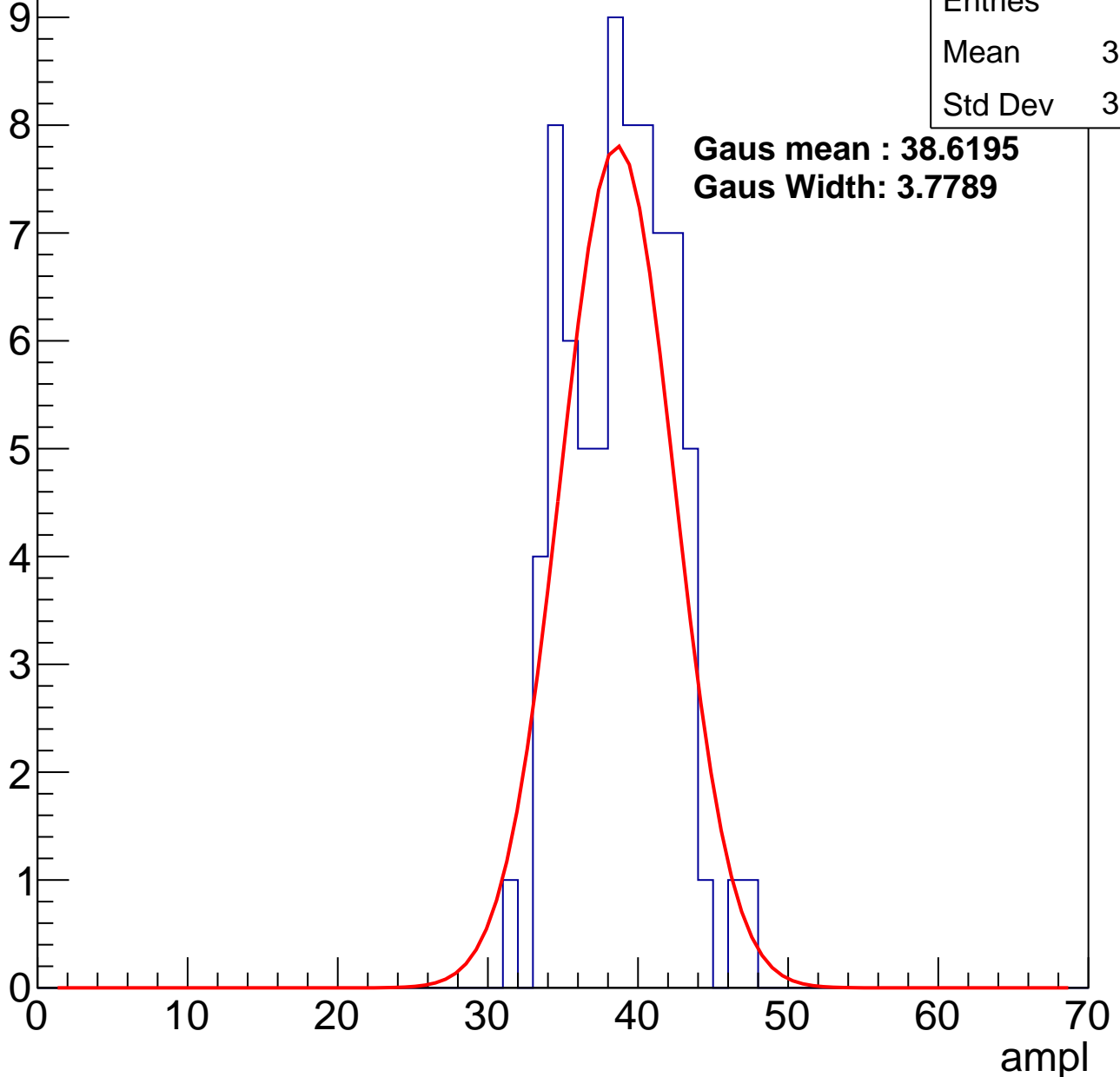
B1L101S, U25-ch31, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	38.38
Std Dev	3.375

Gaus mean : 38.6195
Gaus Width: 3.7789



B1L101S, U25-ch31, adc2

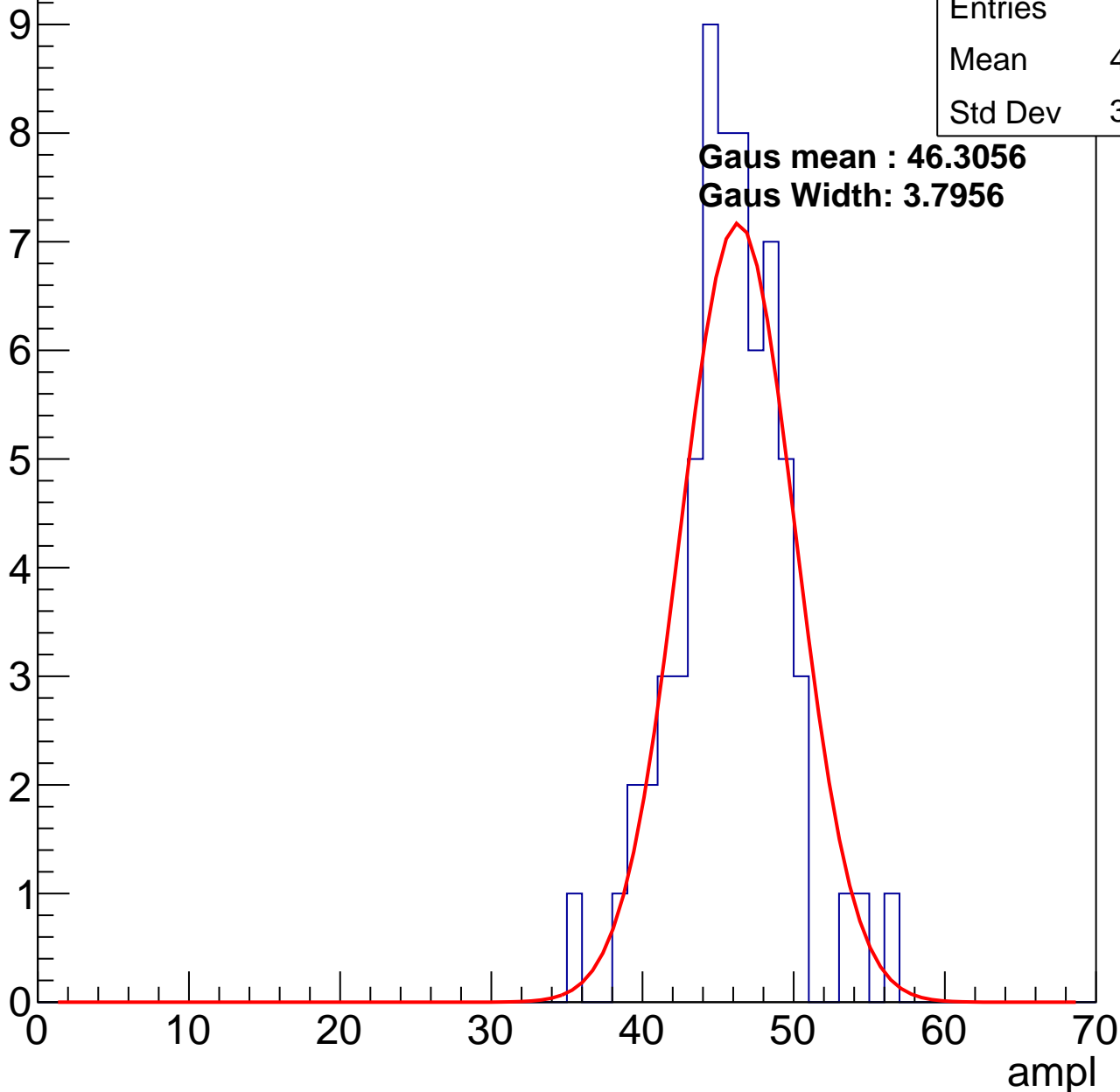
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	45.38
Std Dev	3.655

Gaus mean : 46.3056

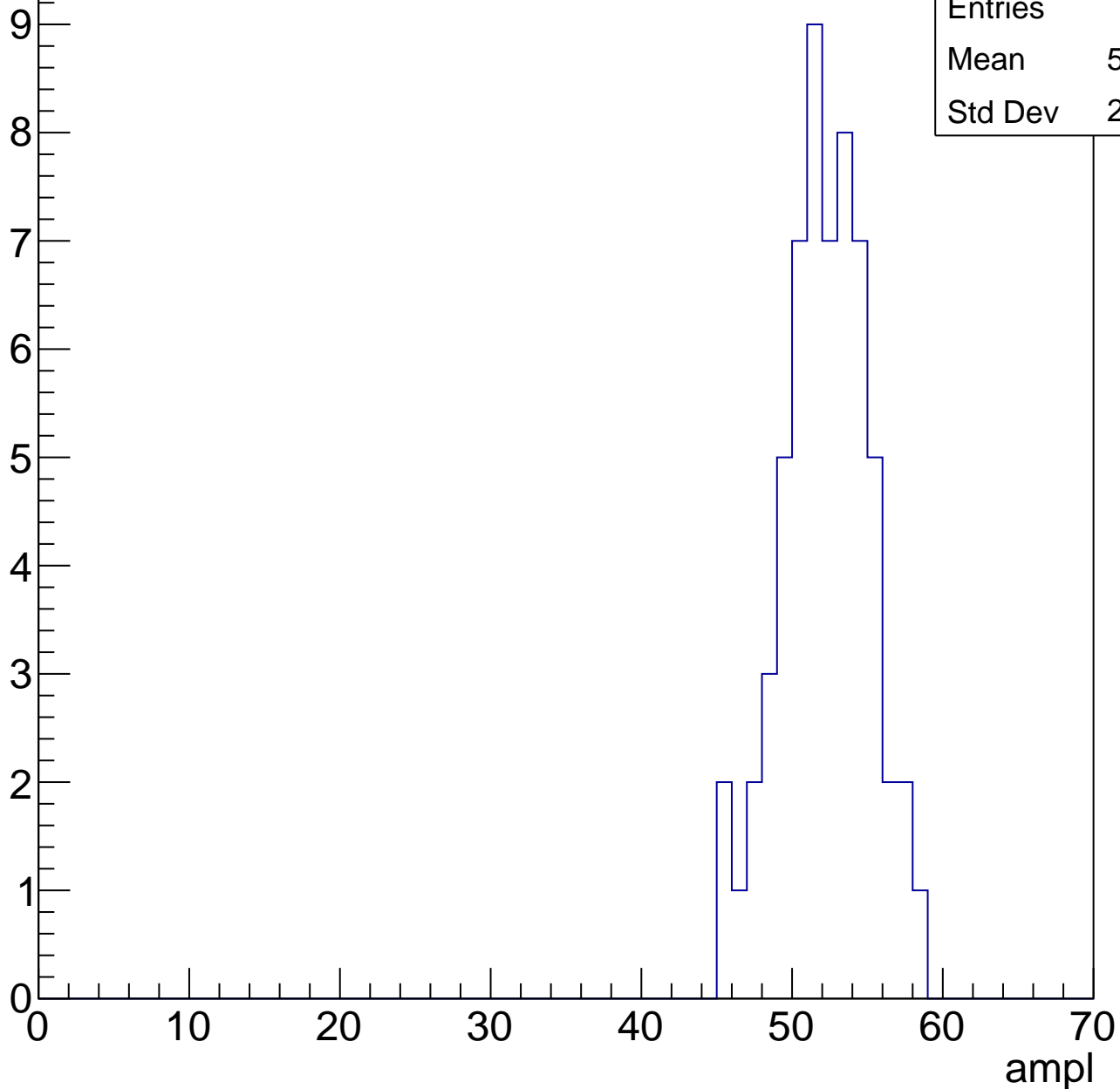
Gaus Width: 3.7956



B1L101S, U25-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

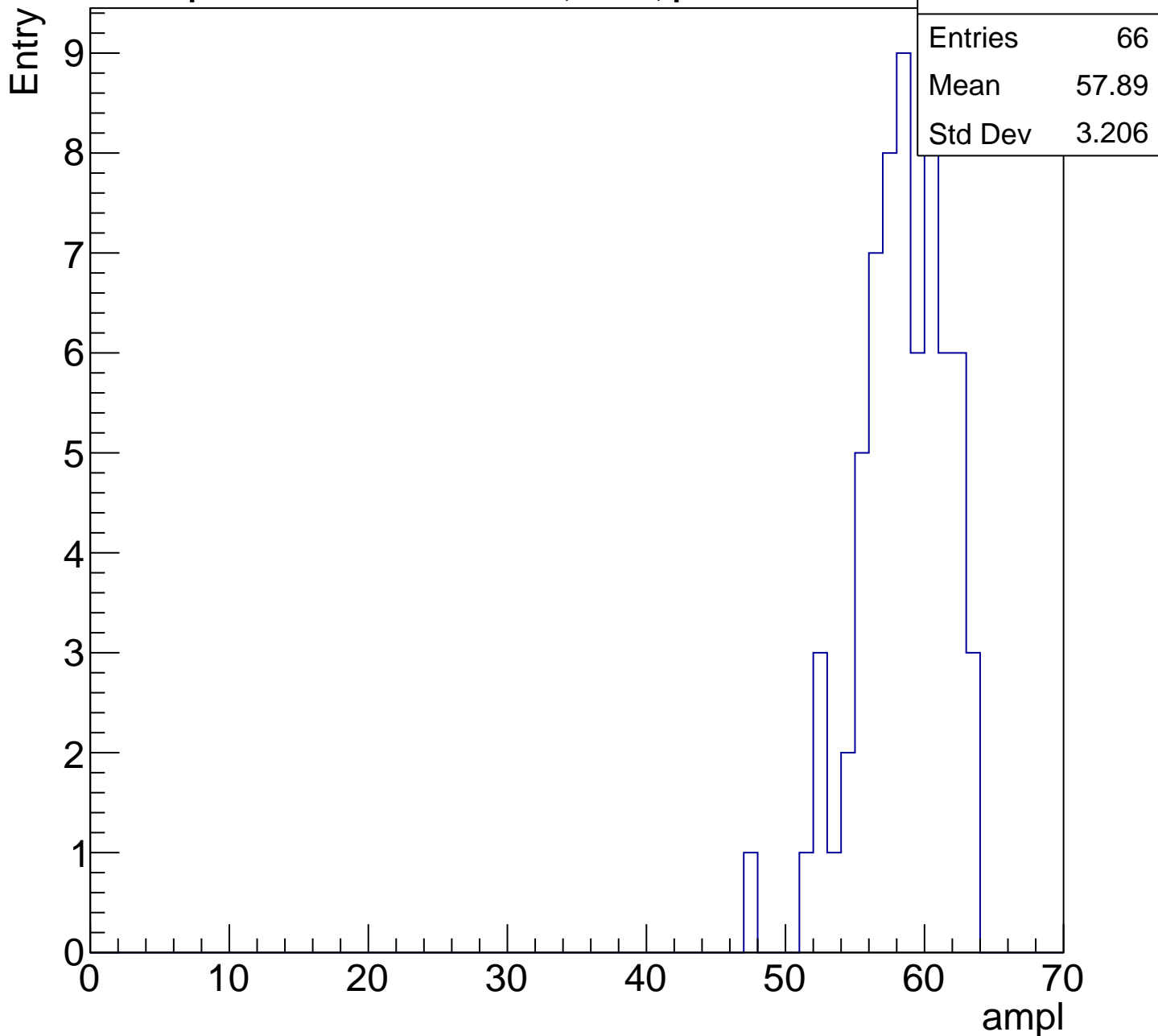
Entry



Entries	61
Mean	51.69
Std Dev	2.883

B1L101S, U25-ch31, adc4

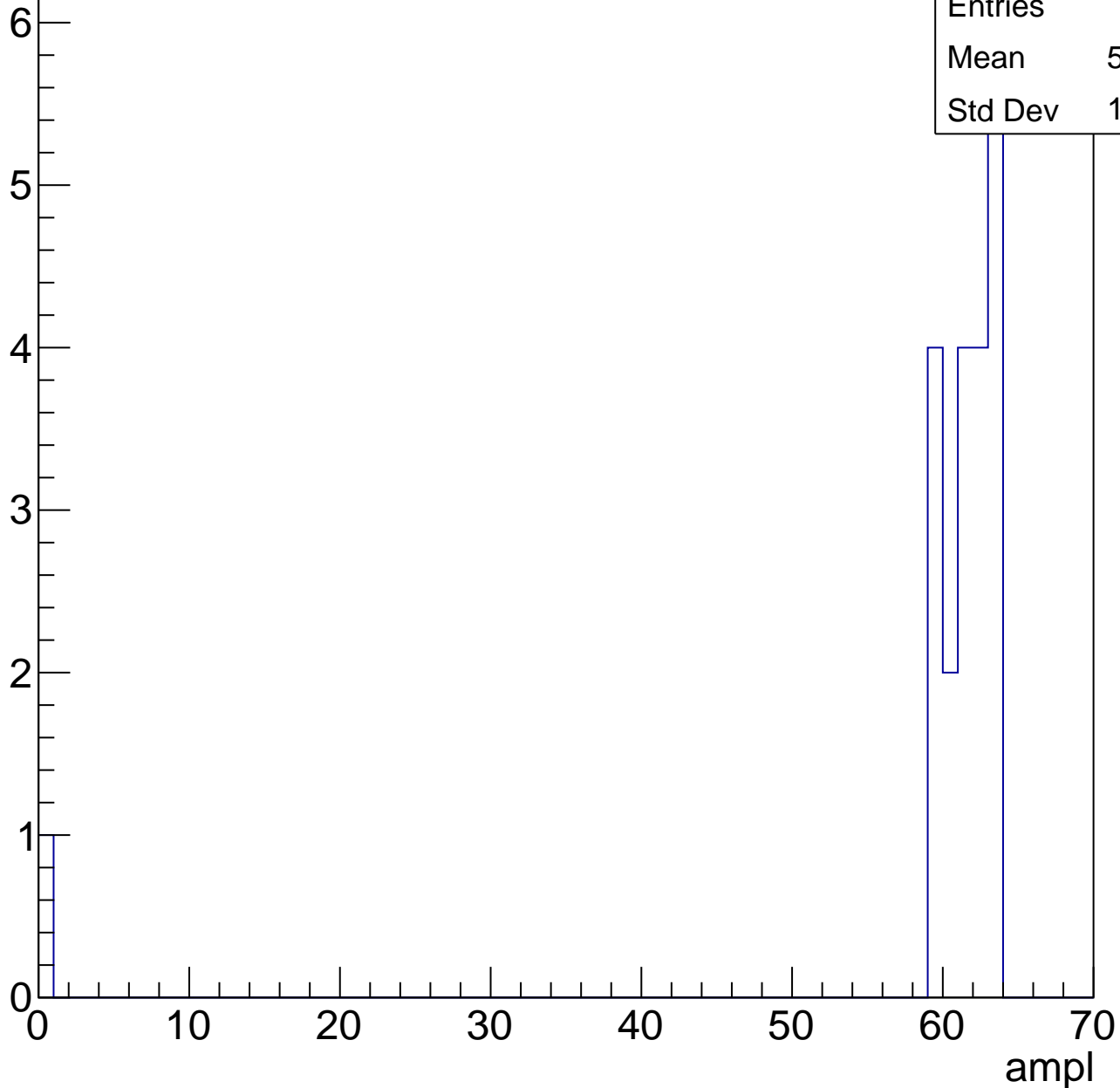
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

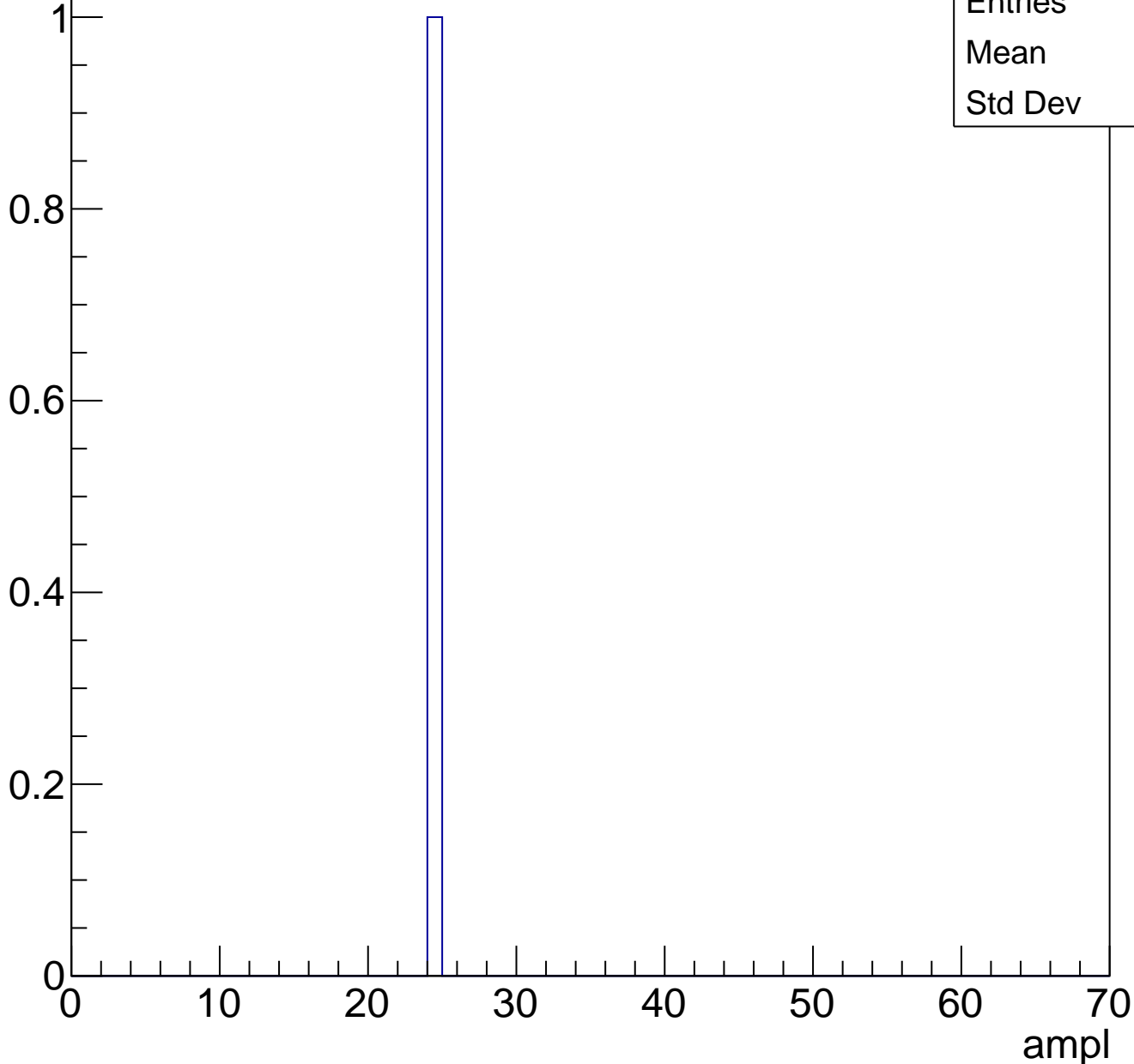
Entry



B1L101S, U25-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch32, adc0

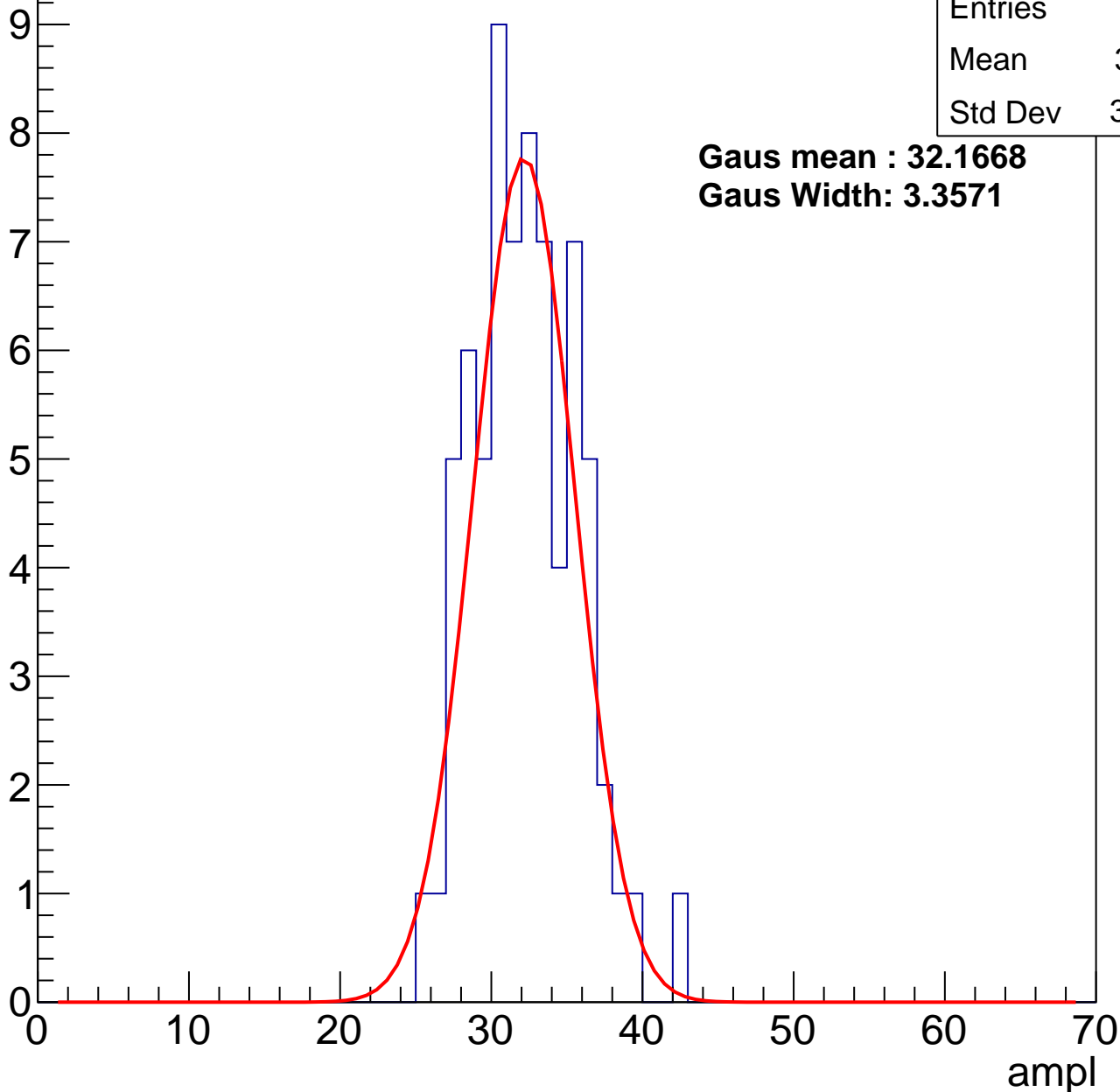
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	31.81
Std Dev	3.365

Gaus mean : 32.1668

Gaus Width: 3.3571



B1L101S, U25-ch32, adc1

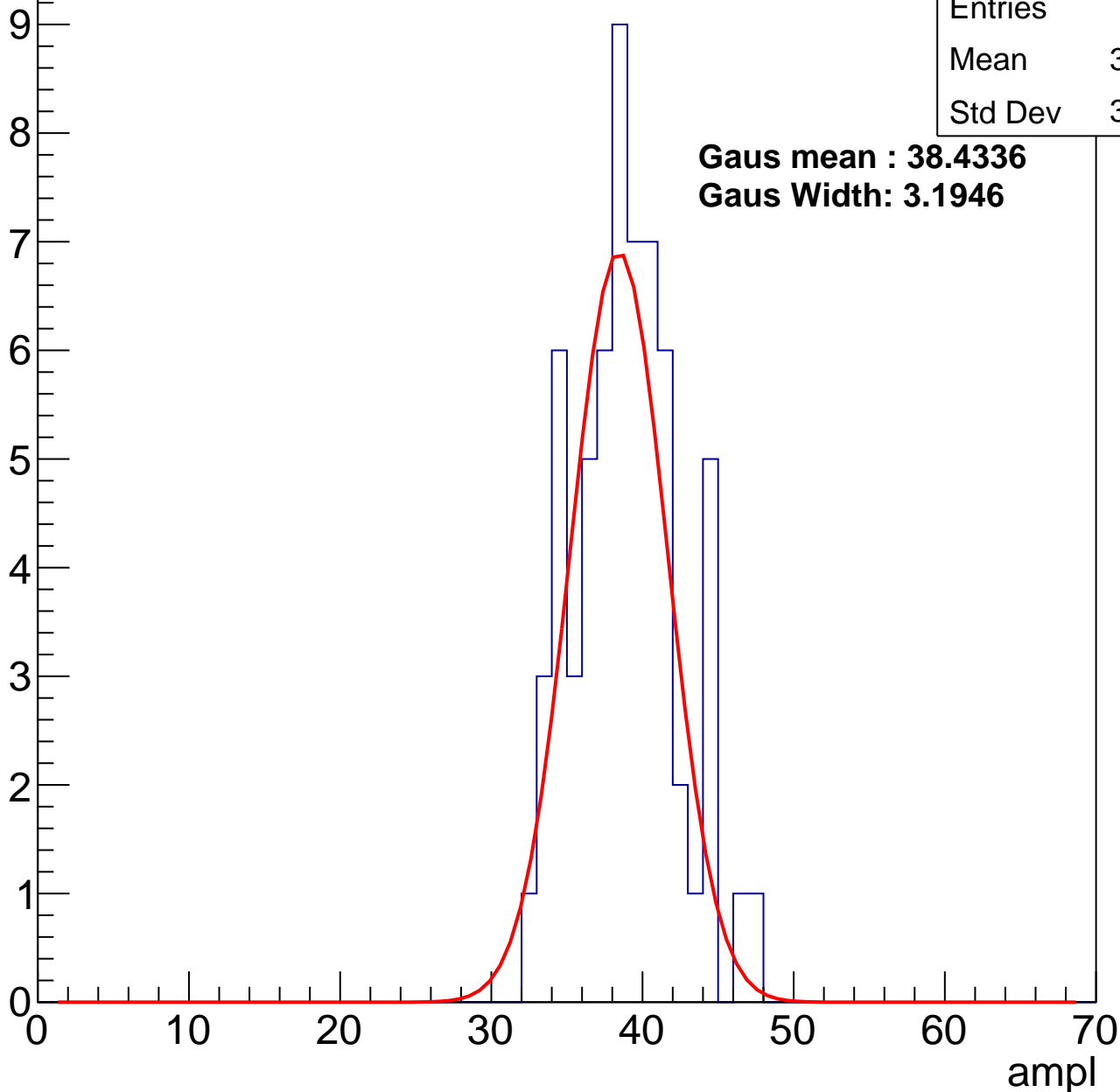
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.46
Std Dev	3.375

Gaus mean : 38.4336

Gaus Width: 3.1946



B1L101S, U25-ch32, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	45.25
Std Dev	3.524

Gaus mean : 45.8898

Gaus Width: 4.3066

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

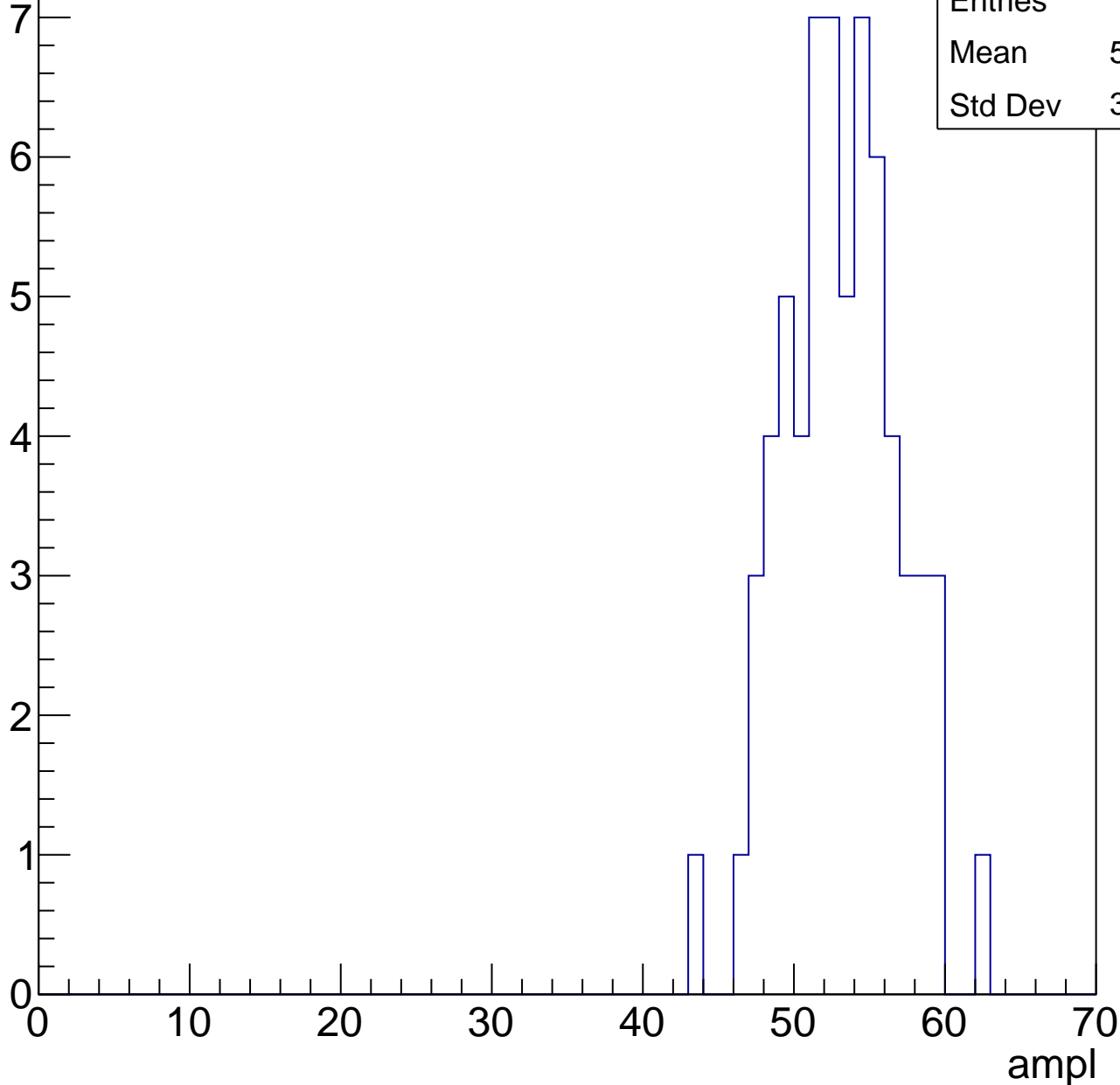
70

B1L101S, U25-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	52.64
Std Dev	3.705

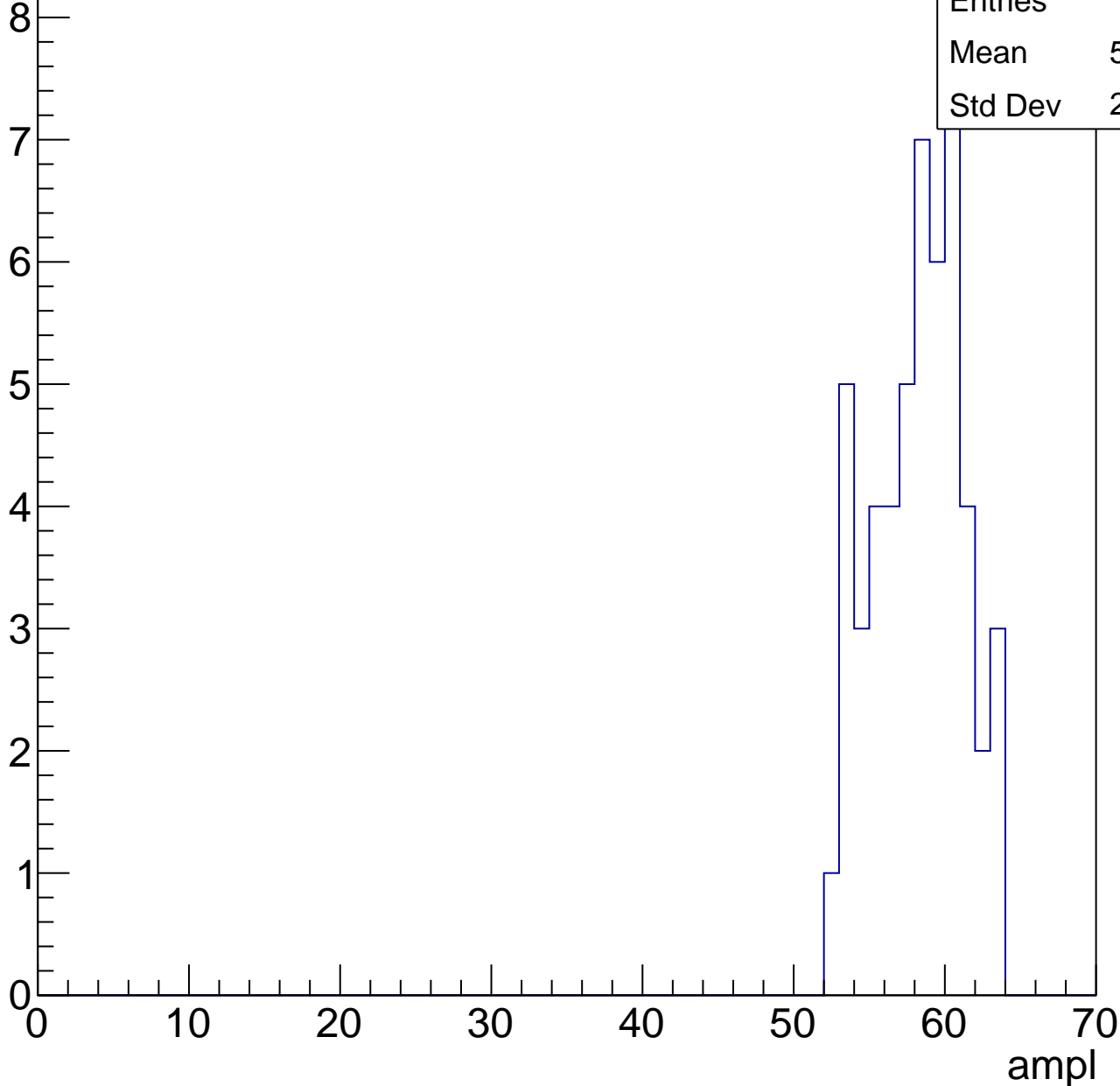


B1L101S, U25-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	57.79
Std Dev	2.924

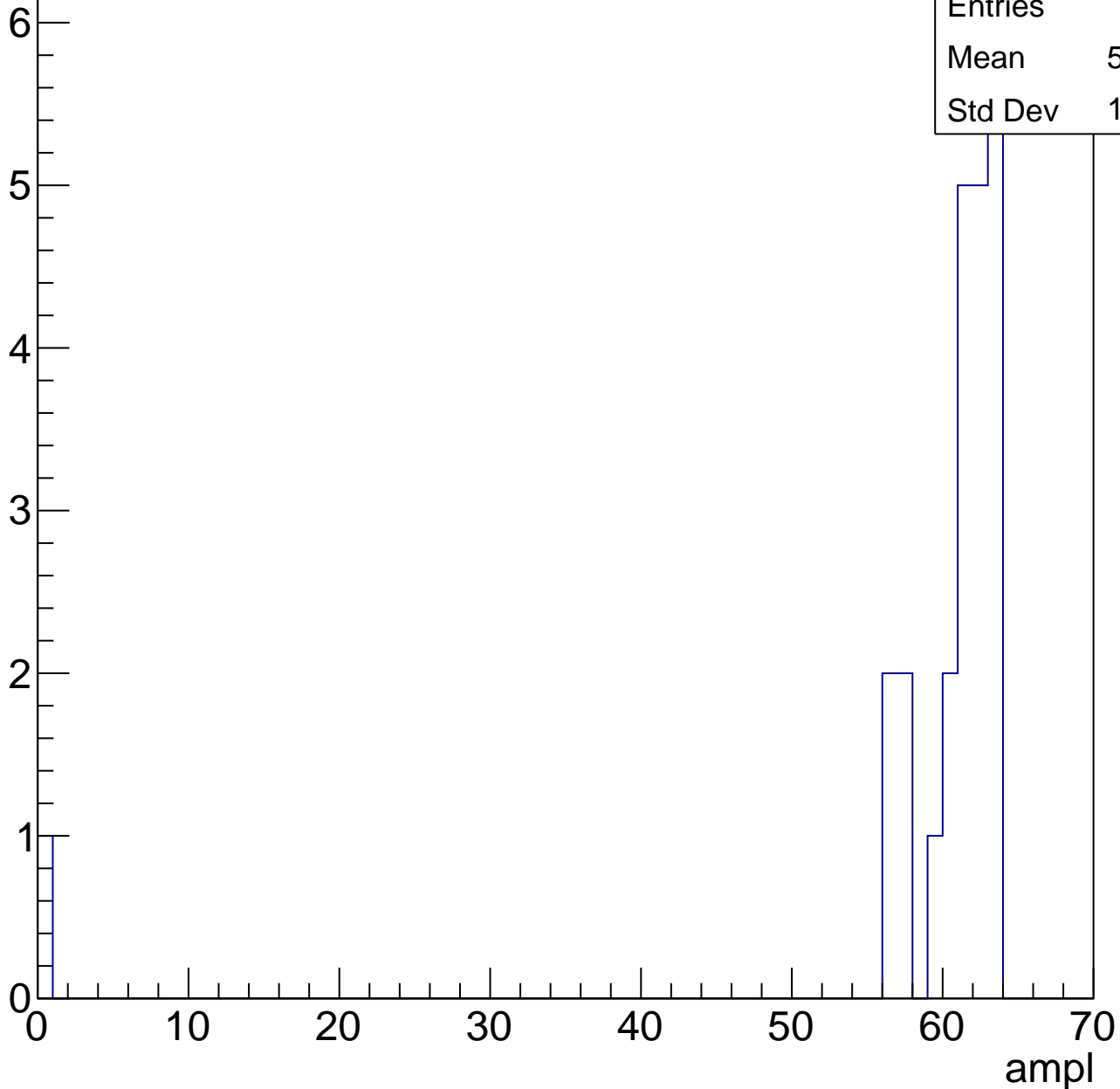


B1L101S, U25-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	58.25
Std Dev	12.34



B1L101S, U25-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B1L101S, U25-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch33, adc0

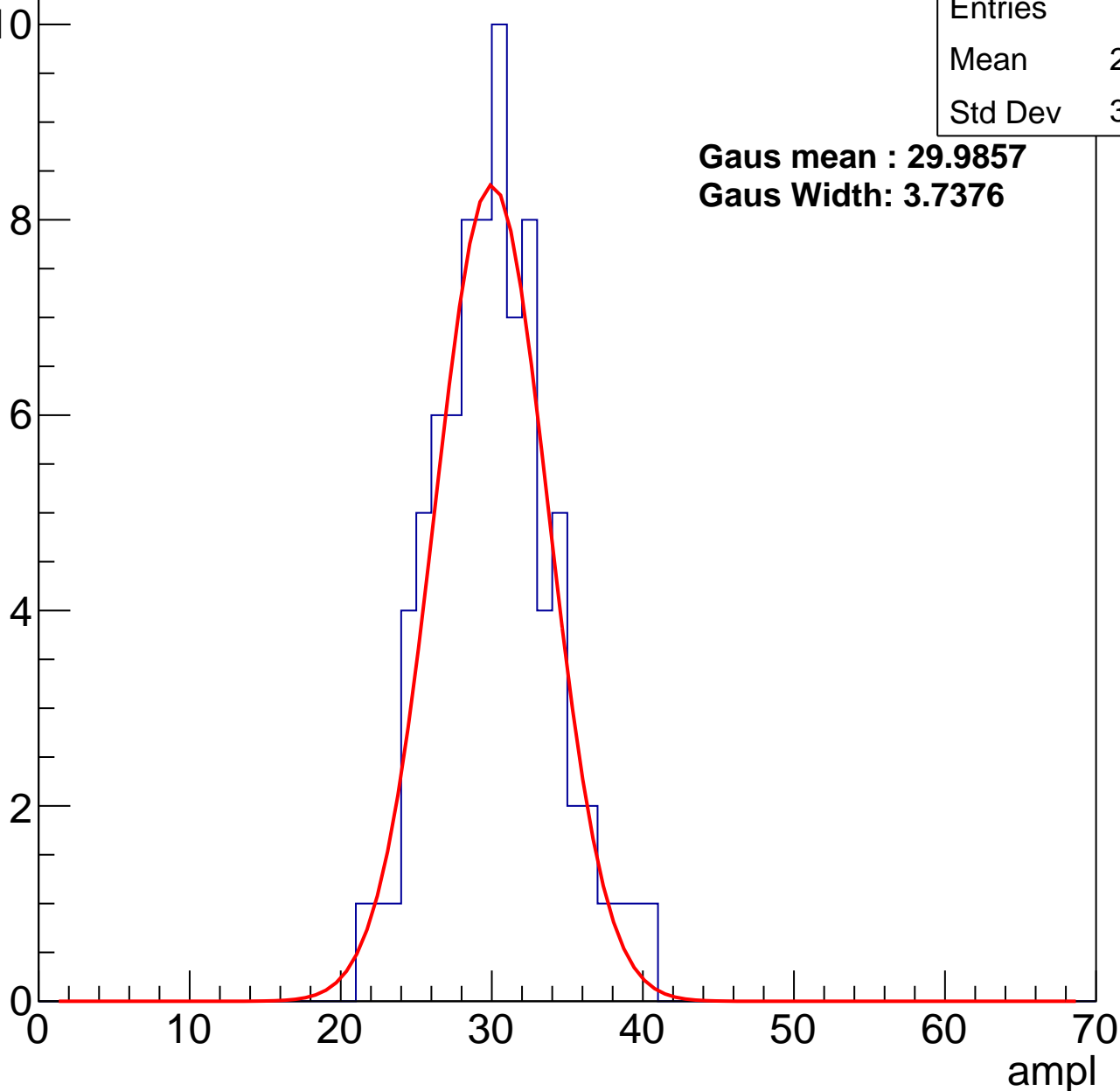
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	29.66
Std Dev	3.848

Gaus mean : 29.9857

Gaus Width: 3.7376



B1L101S, U25-ch33, adc1

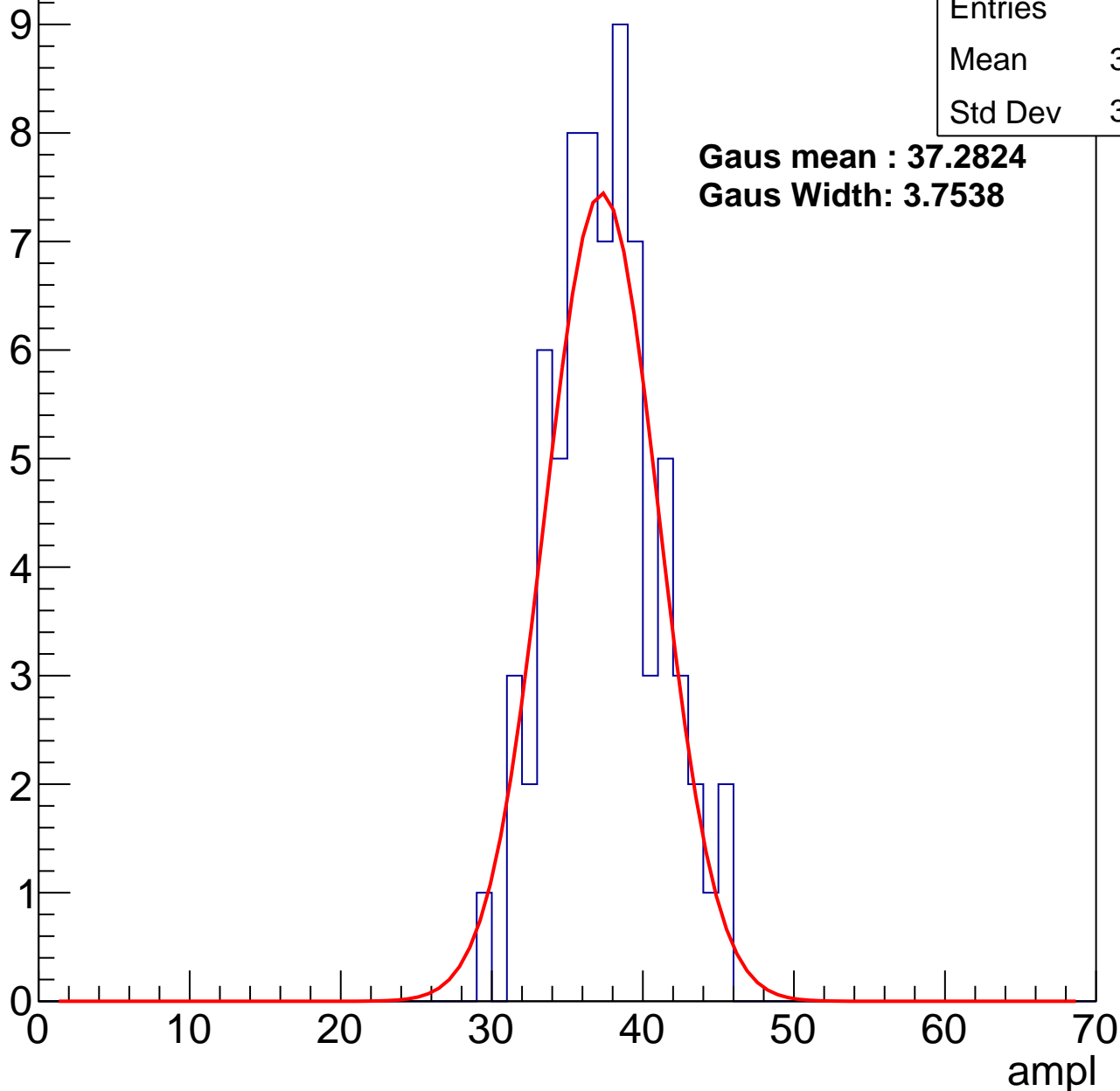
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.04
Std Dev	3.478

Gaus mean : 37.2824

Gaus Width: 3.7538



B1L101S, U25-ch33, adc2

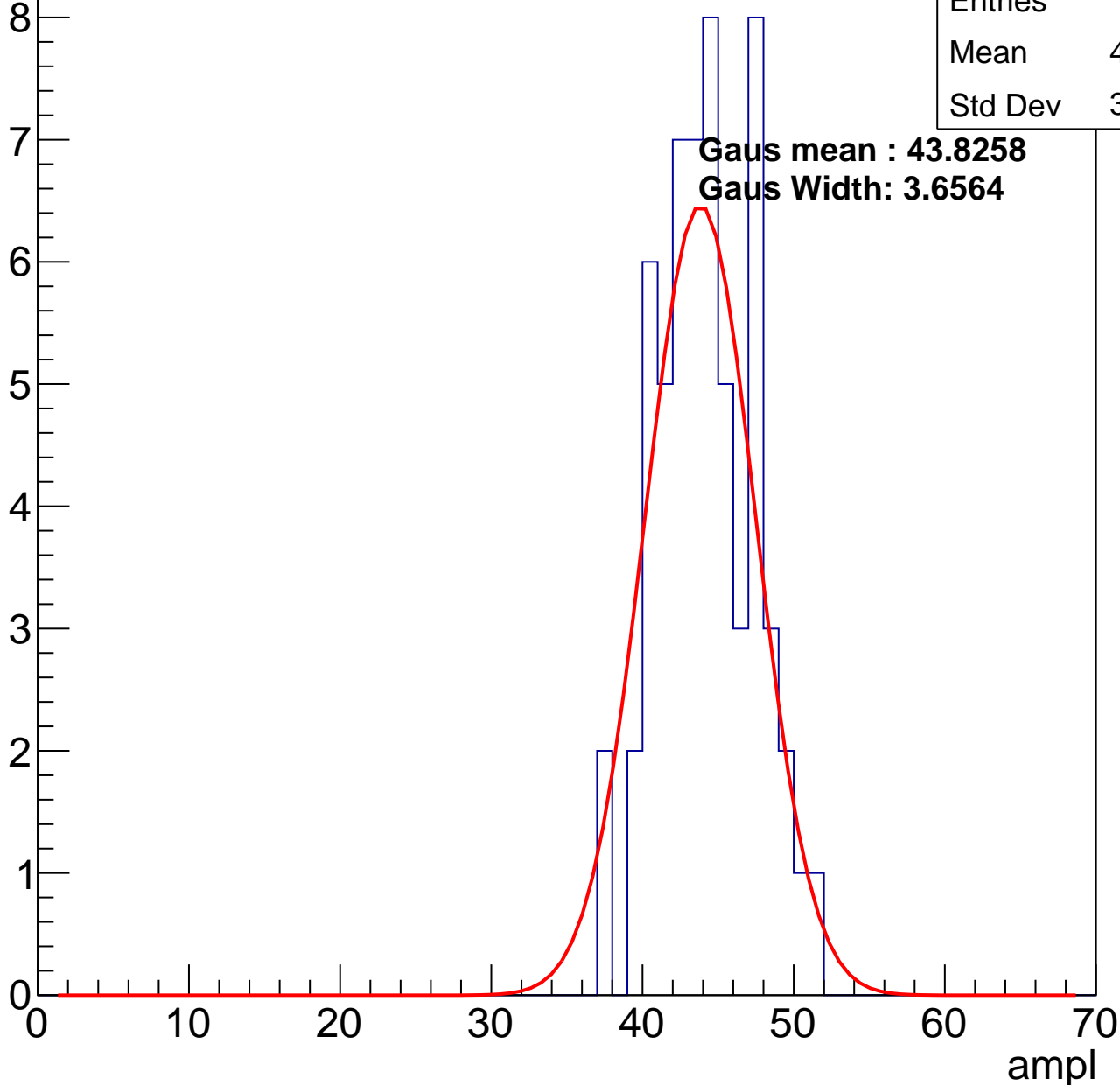
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.77
Std Dev	3.143

Gaus mean : 43.8258

Gaus Width: 3.6564

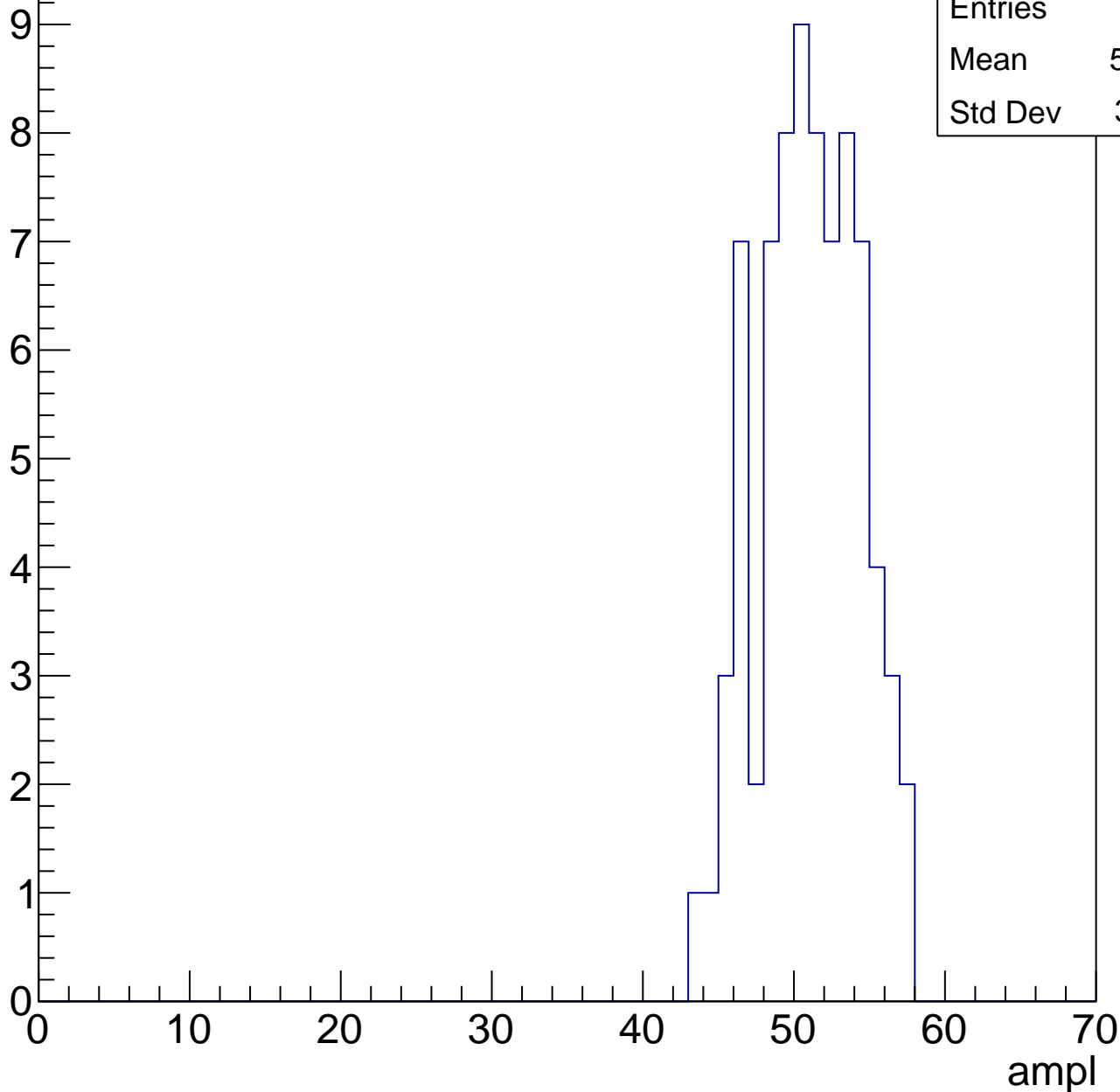


B1L101S, U25-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	50.55
Std Dev	3.281

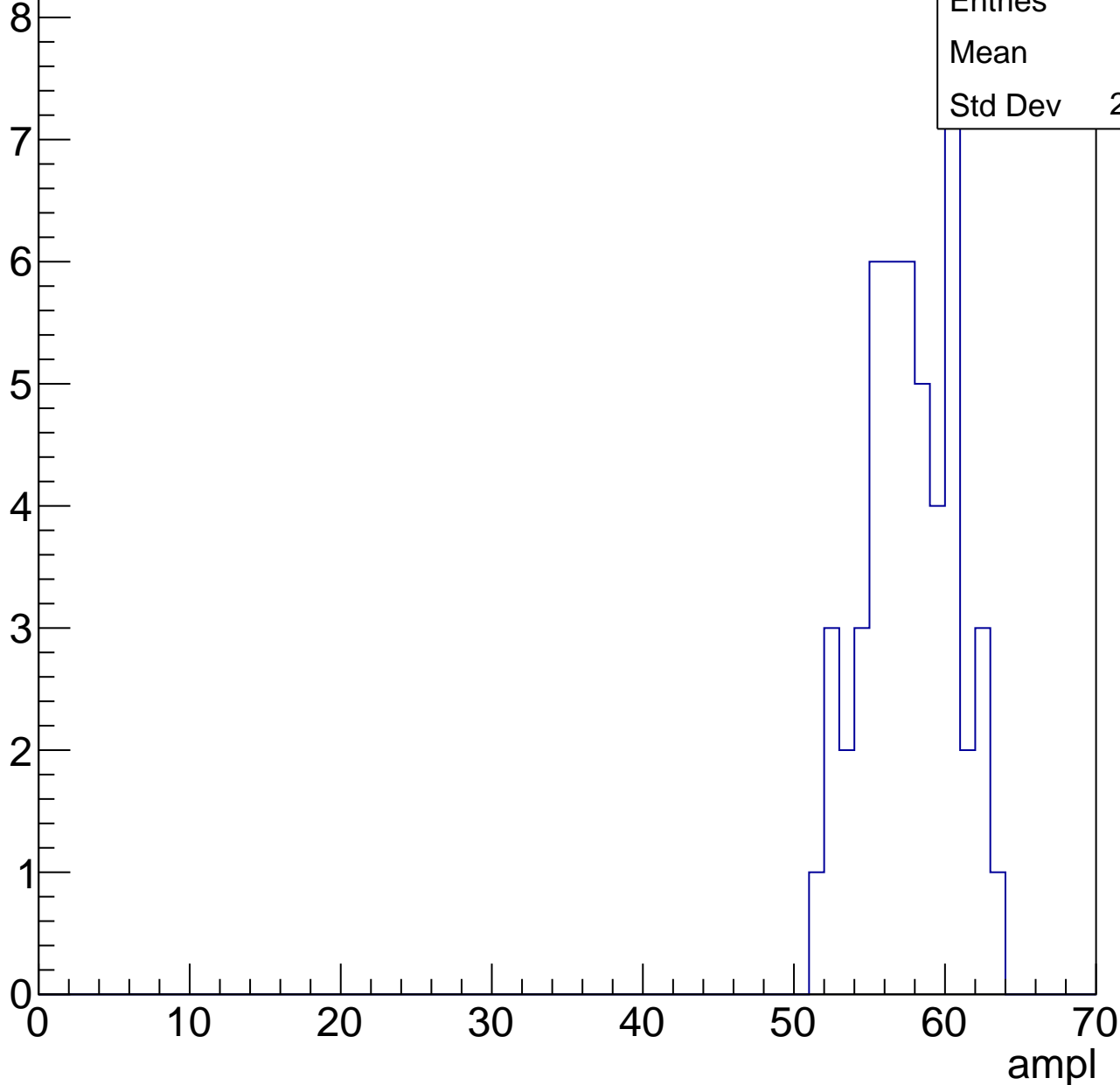


B1L101S, U25-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	57.2
Std Dev	2.946

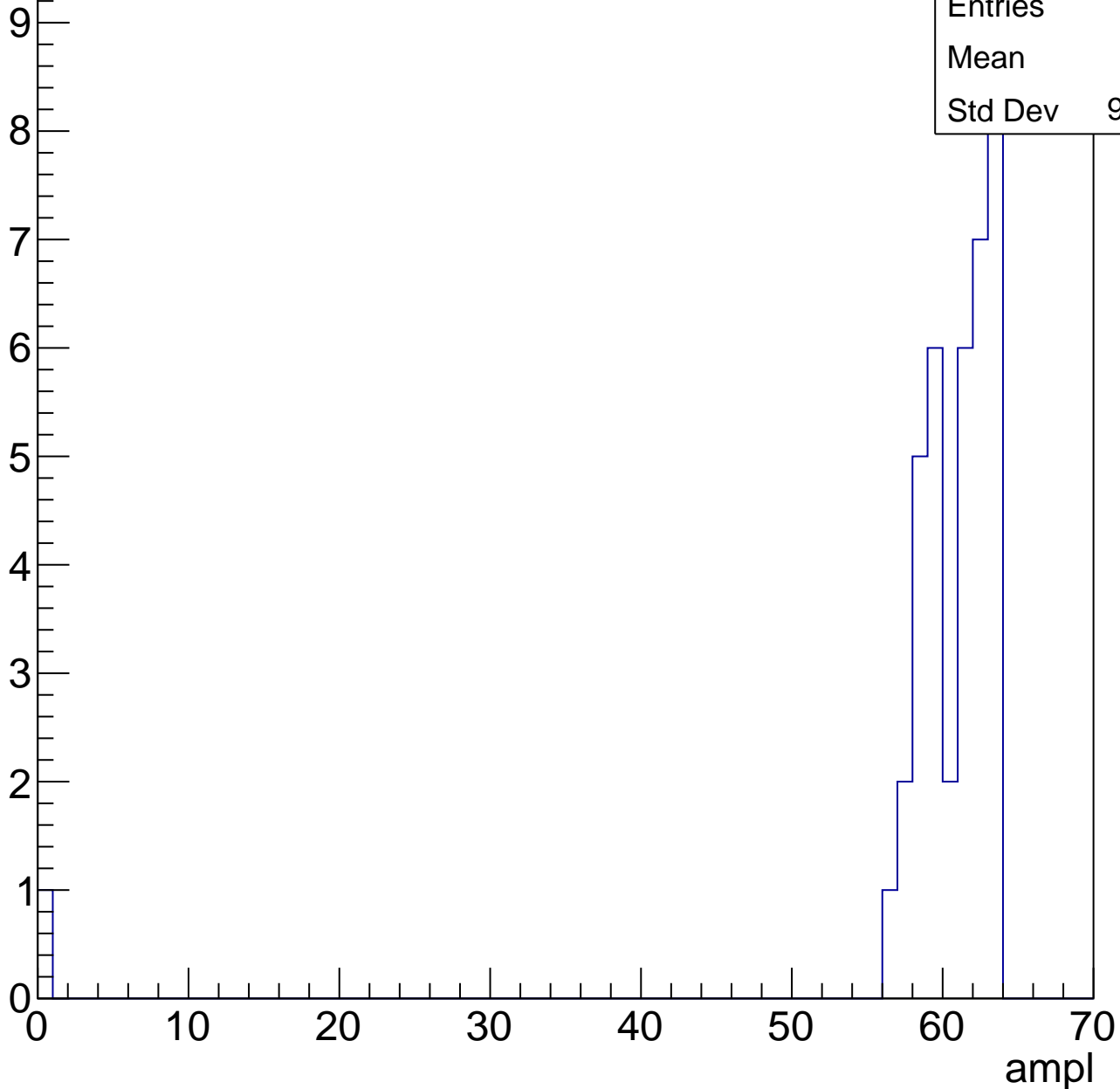


B1L101S, U25-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

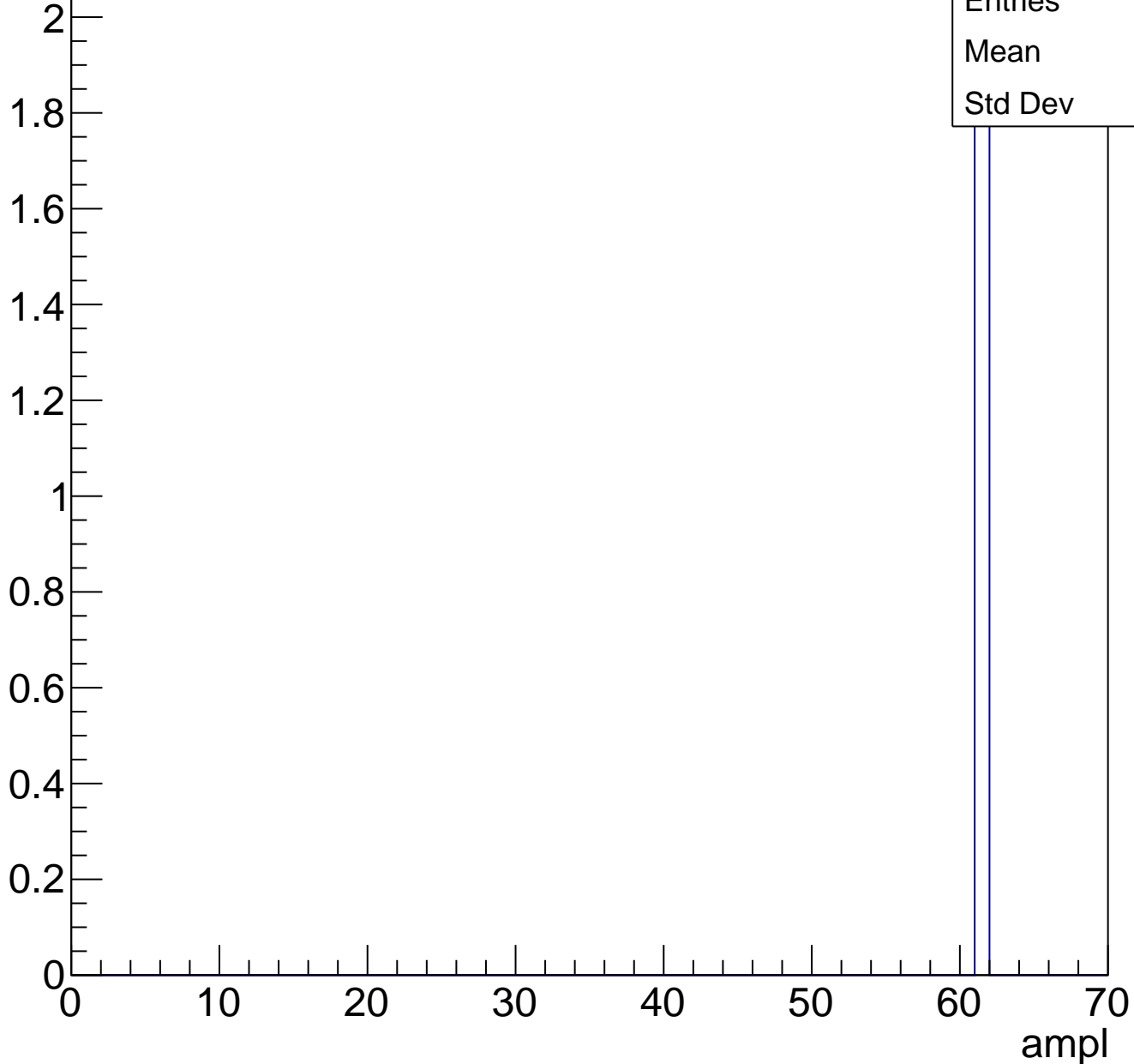
Entries	39
Mean	59
Std Dev	9.787



B1L101S, U25-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch34, adc0

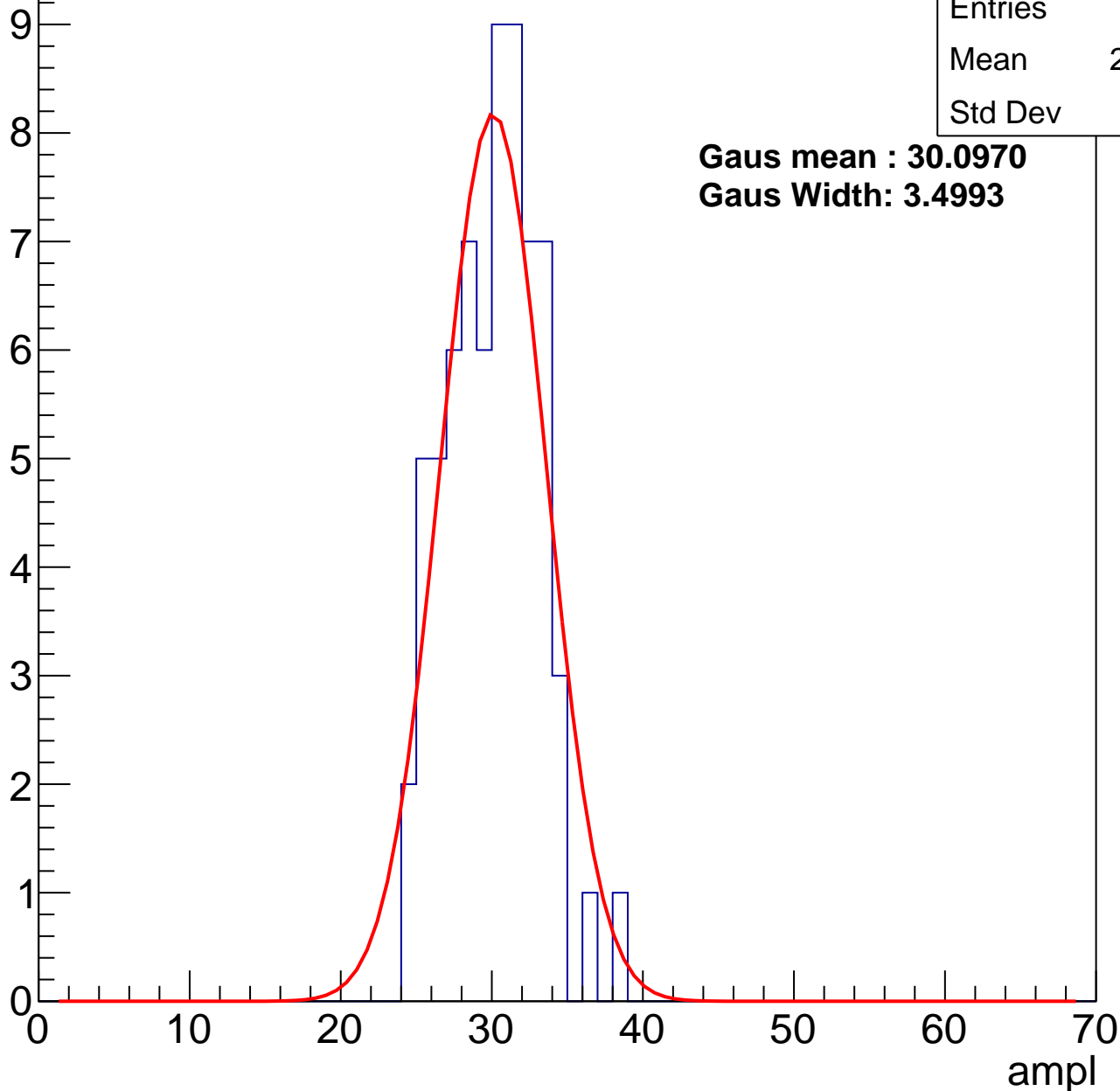
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.63
Std Dev	2.98

Gaus mean : 30.0970

Gaus Width: 3.4993



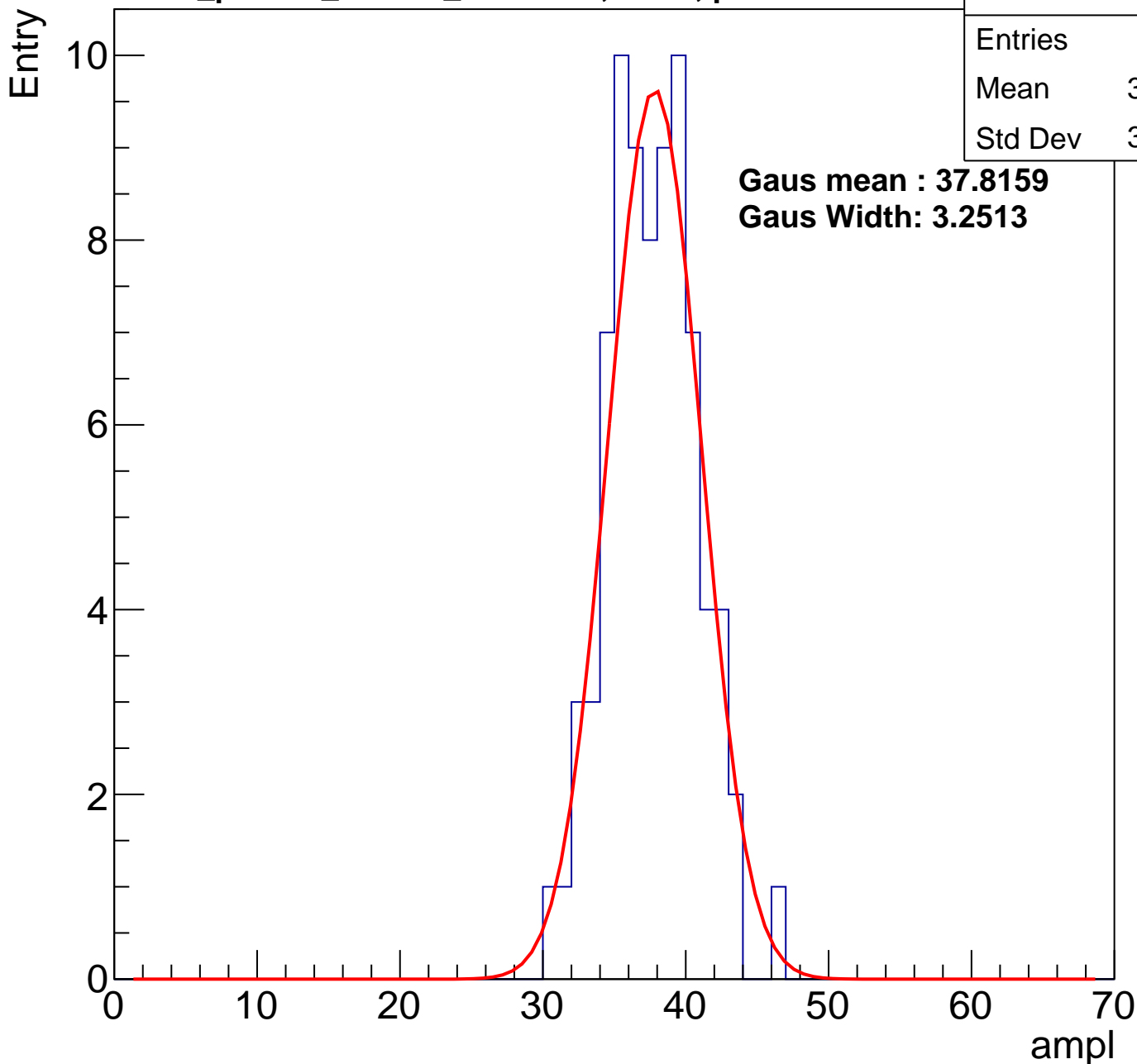
B1L101S, U25-ch34, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	37.22
Std Dev	3.072

Gaus mean : 37.8159

Gaus Width: 3.2513



B1L101S, U25-ch34, adc2

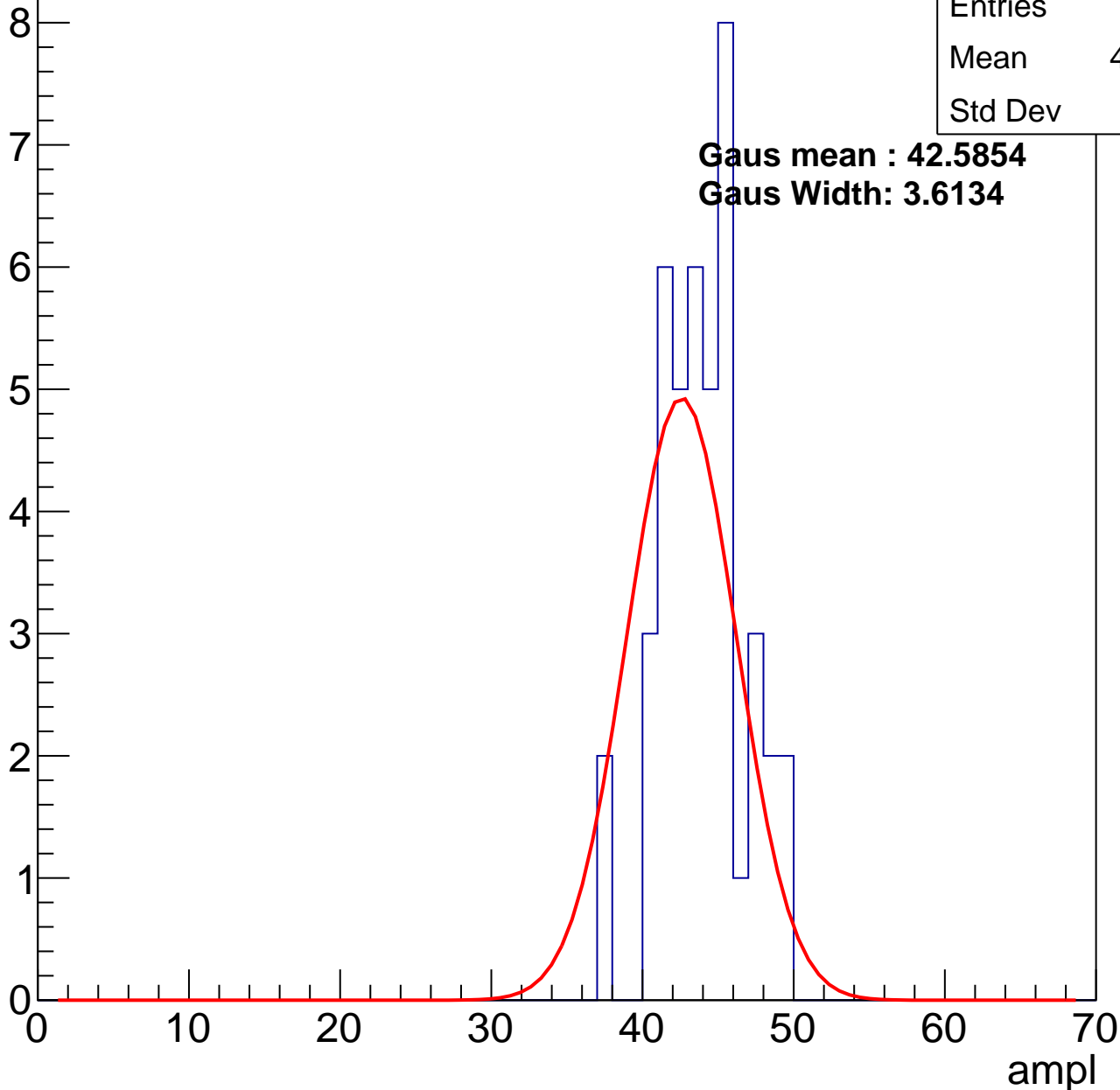
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	43.47
Std Dev	2.79

Gaus mean : 42.5854

Gaus Width: 3.6134

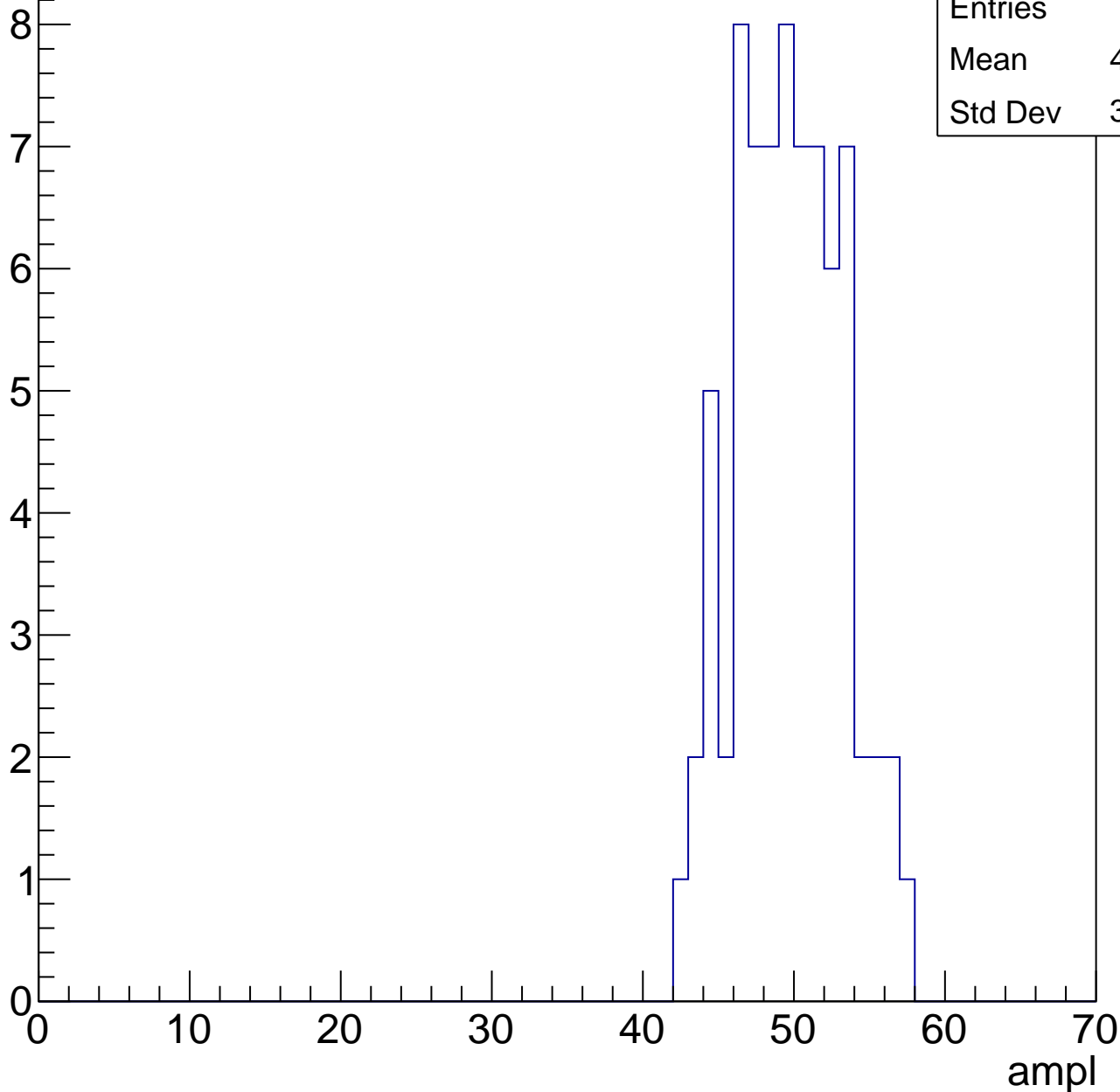


B1L101S, U25-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	49.19
Std Dev	3.424

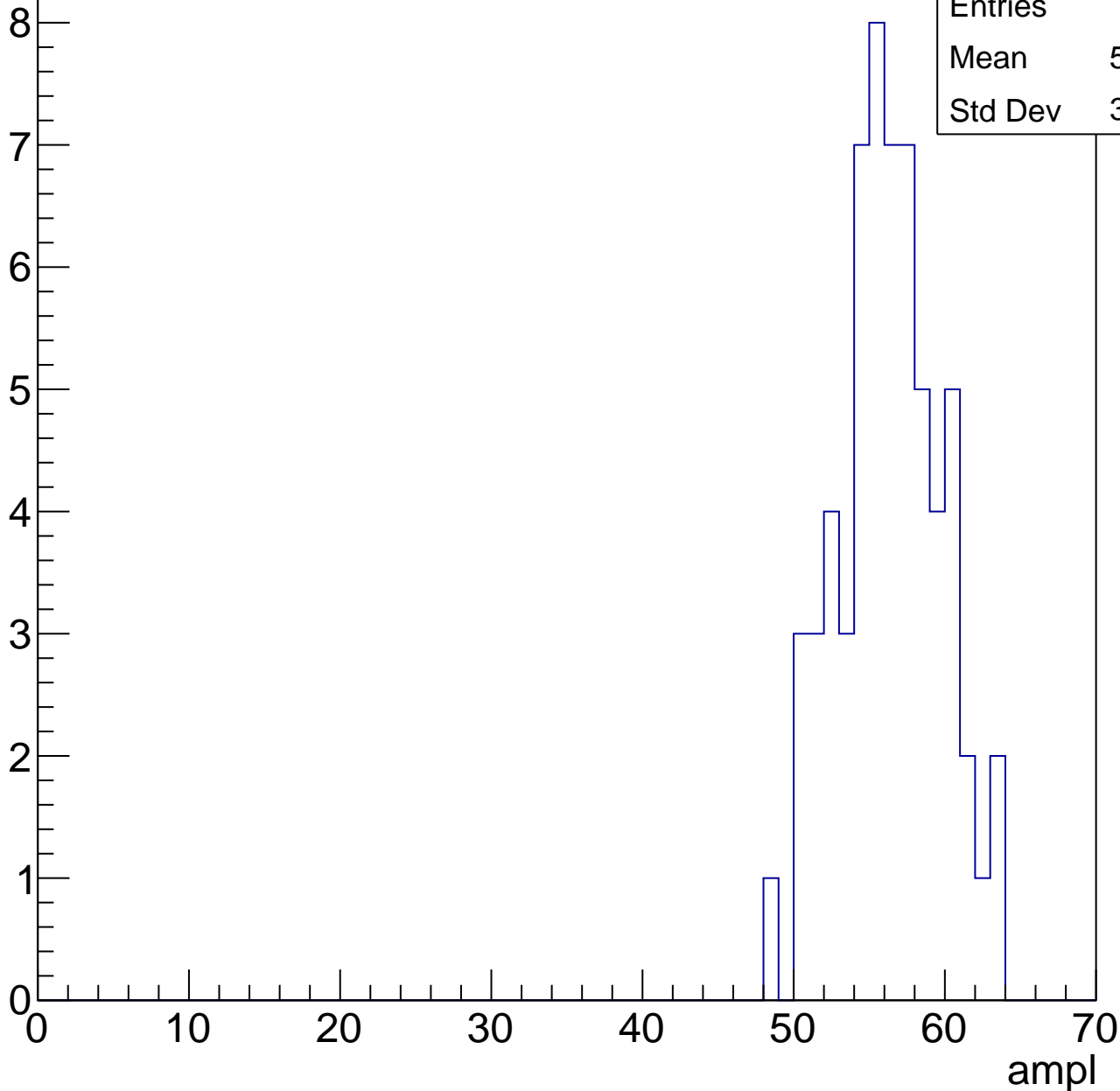


B1L101S, U25-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	55.85
Std Dev	3.359

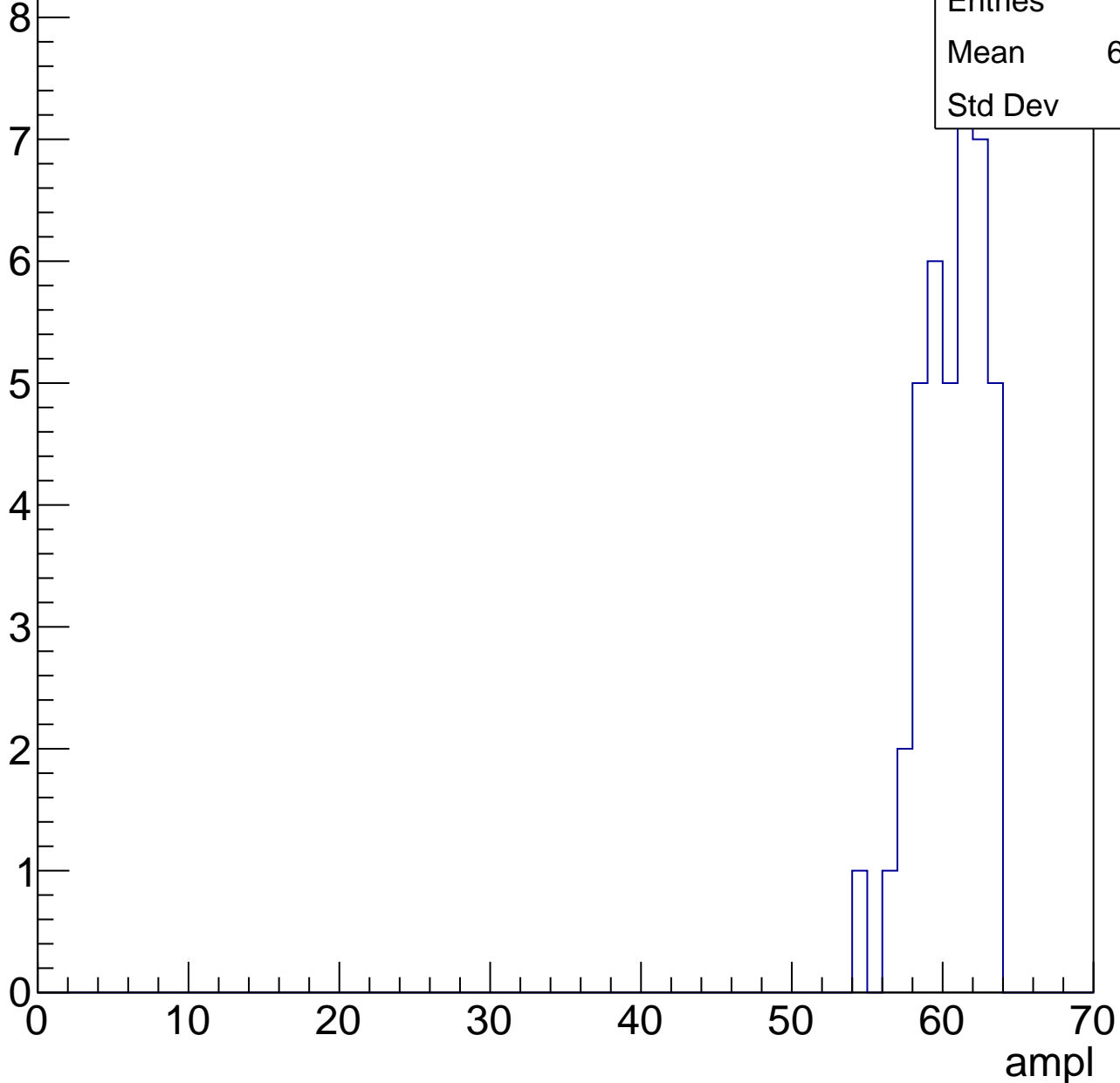


B1L101S, U25-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	60.12
Std Dev	2.1



B1L101S, U25-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L101S, U25-ch35, adc0

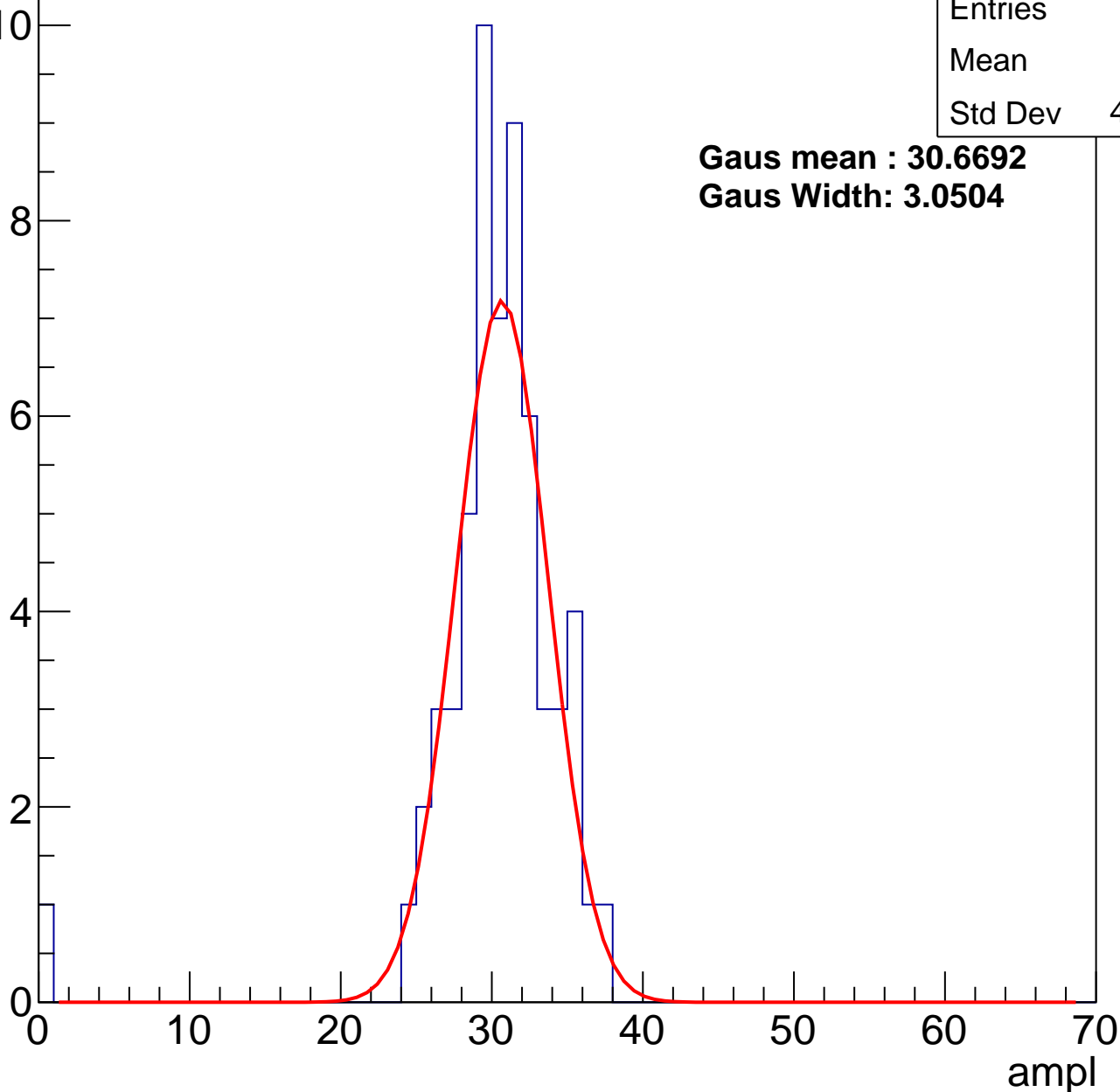
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.8
Std Dev	4.839

Gaus mean : 30.6692

Gaus Width: 3.0504



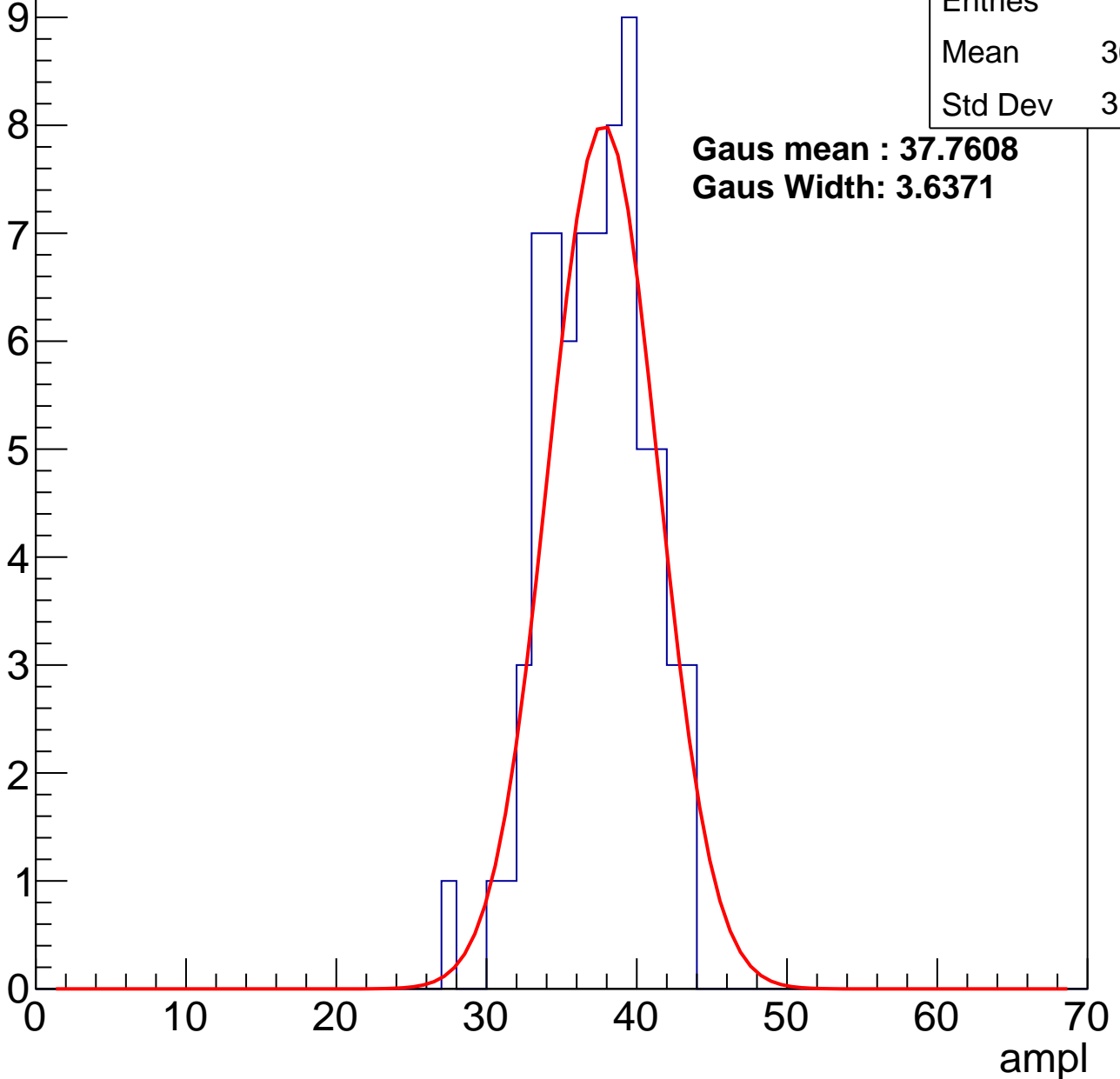
B1L101S, U25-ch35, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.84
Std Dev	3.343

Gaus mean : 37.7608
Gaus Width: 3.6371



B1L101S, U25-ch35, adc2

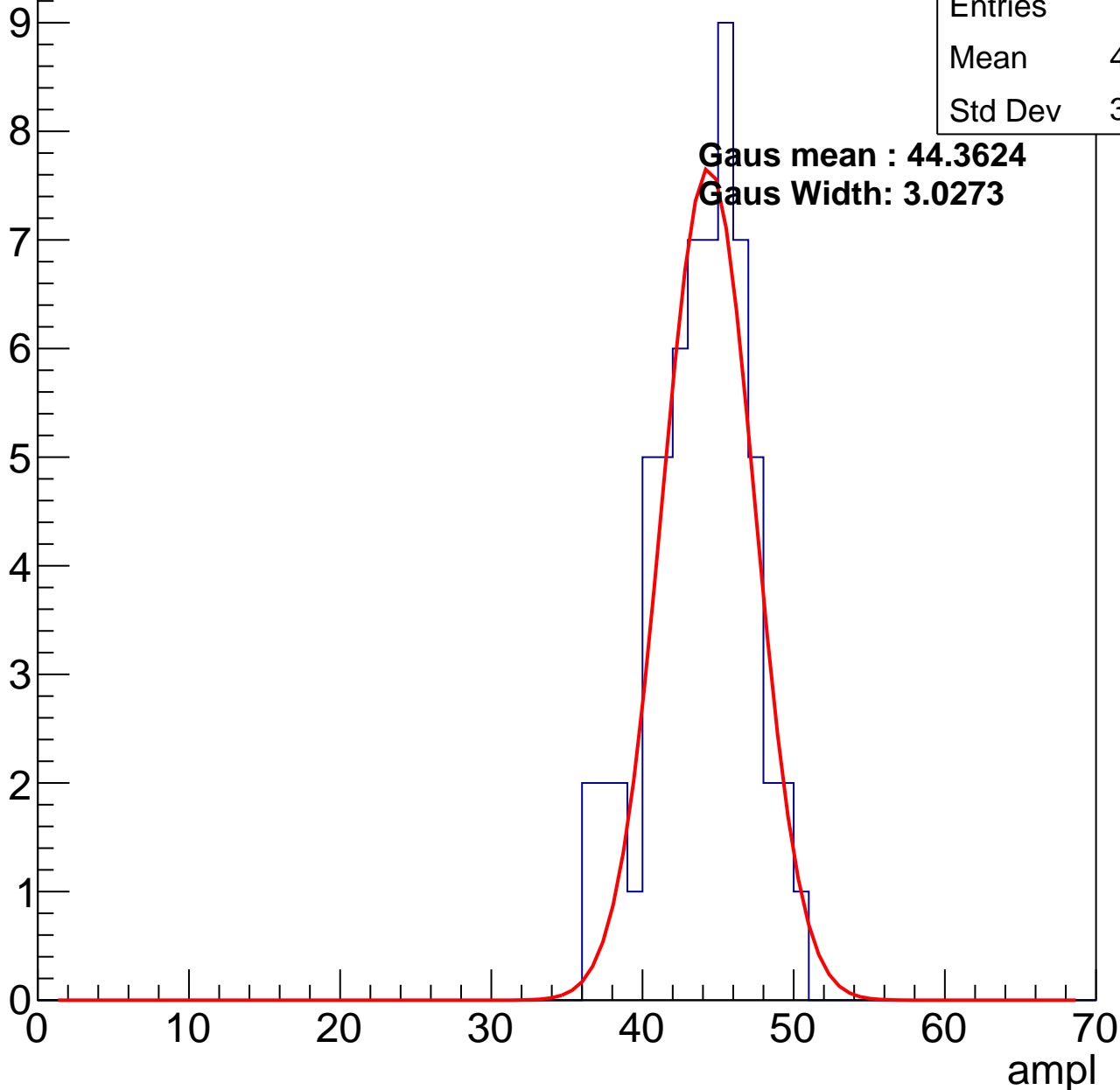
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.38
Std Dev	3.224

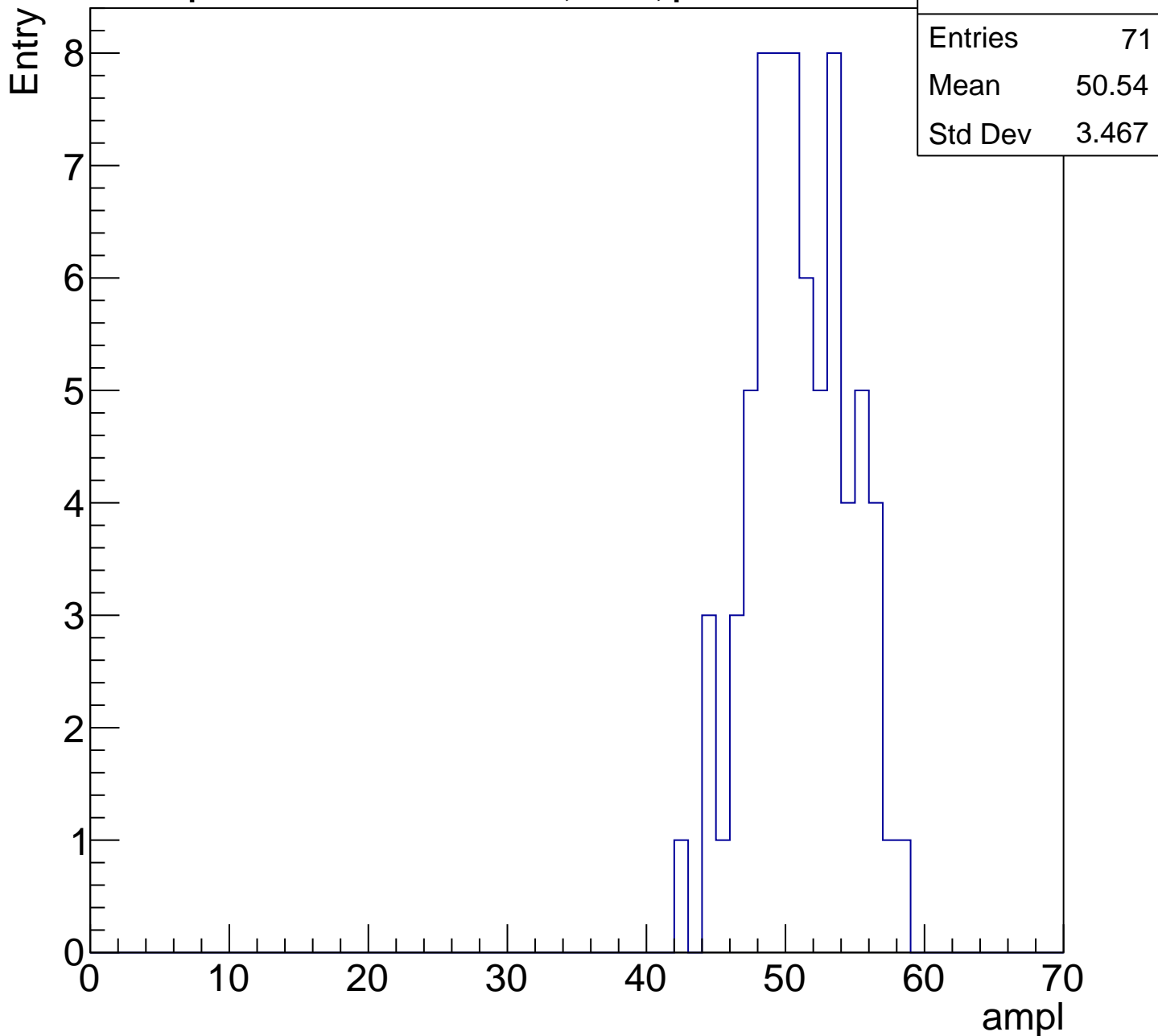
Gaus mean : 44.3624

Gaus Width: 3.0273



B1L101S, U25-ch35, adc3

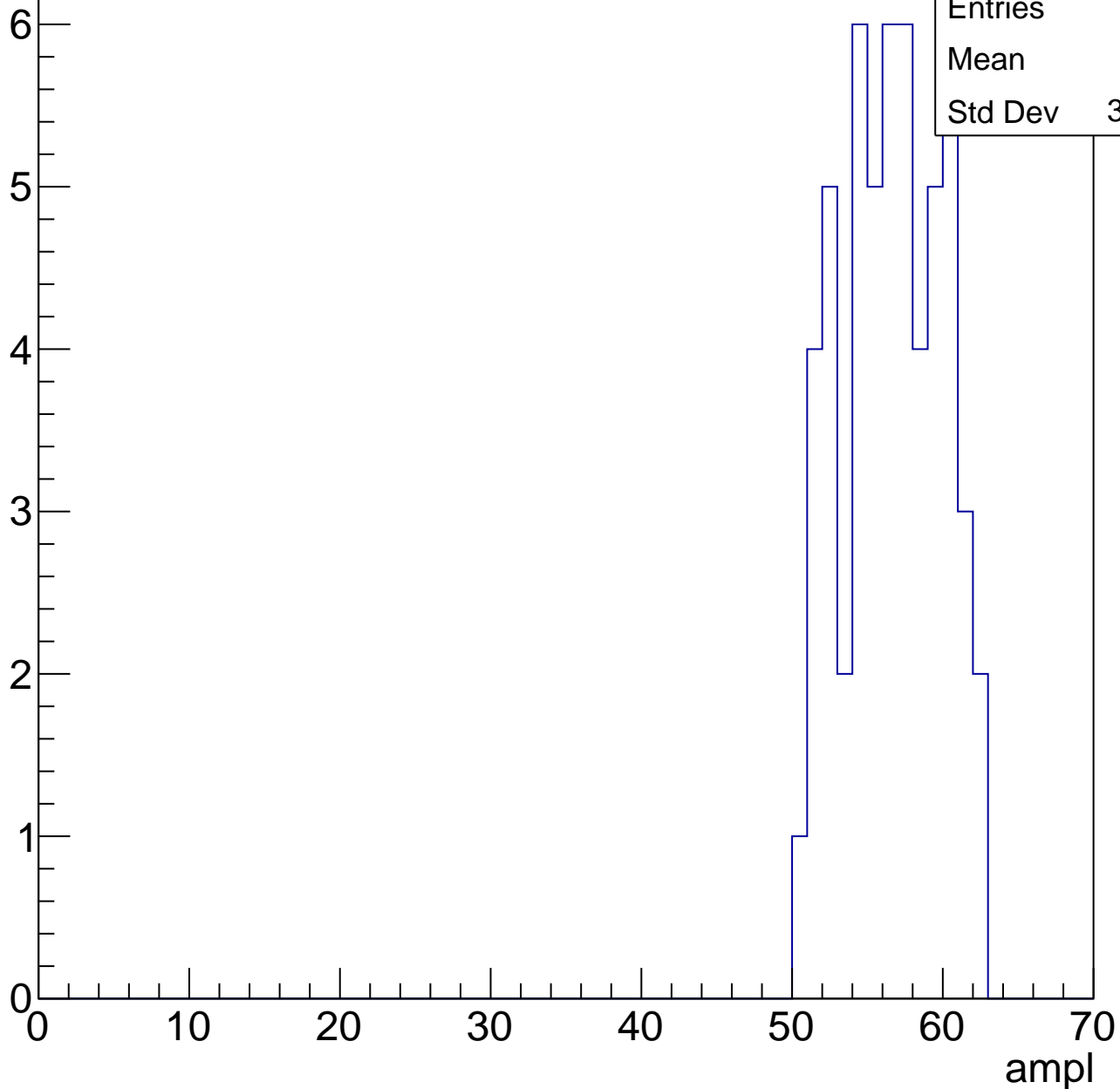
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	55
Mean	56.2
Std Dev	3.222

B1L101S, U25-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.1
Std Dev	9.538

ampl

0

10

20

30

40

50

60

70

B1L101S, U25-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

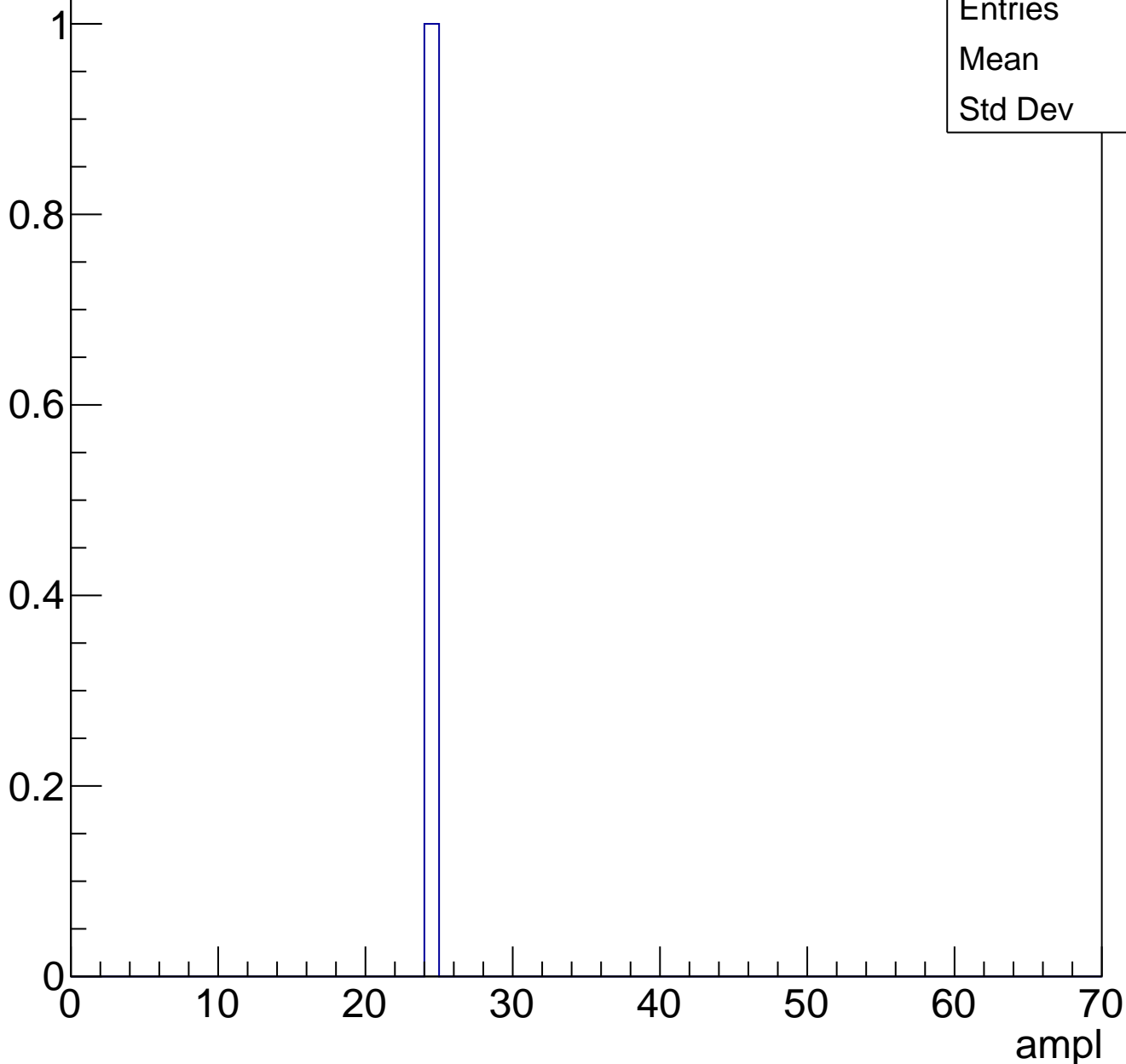
Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U25-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	24
Std Dev	0

B1L101S, U25-ch36, adc0

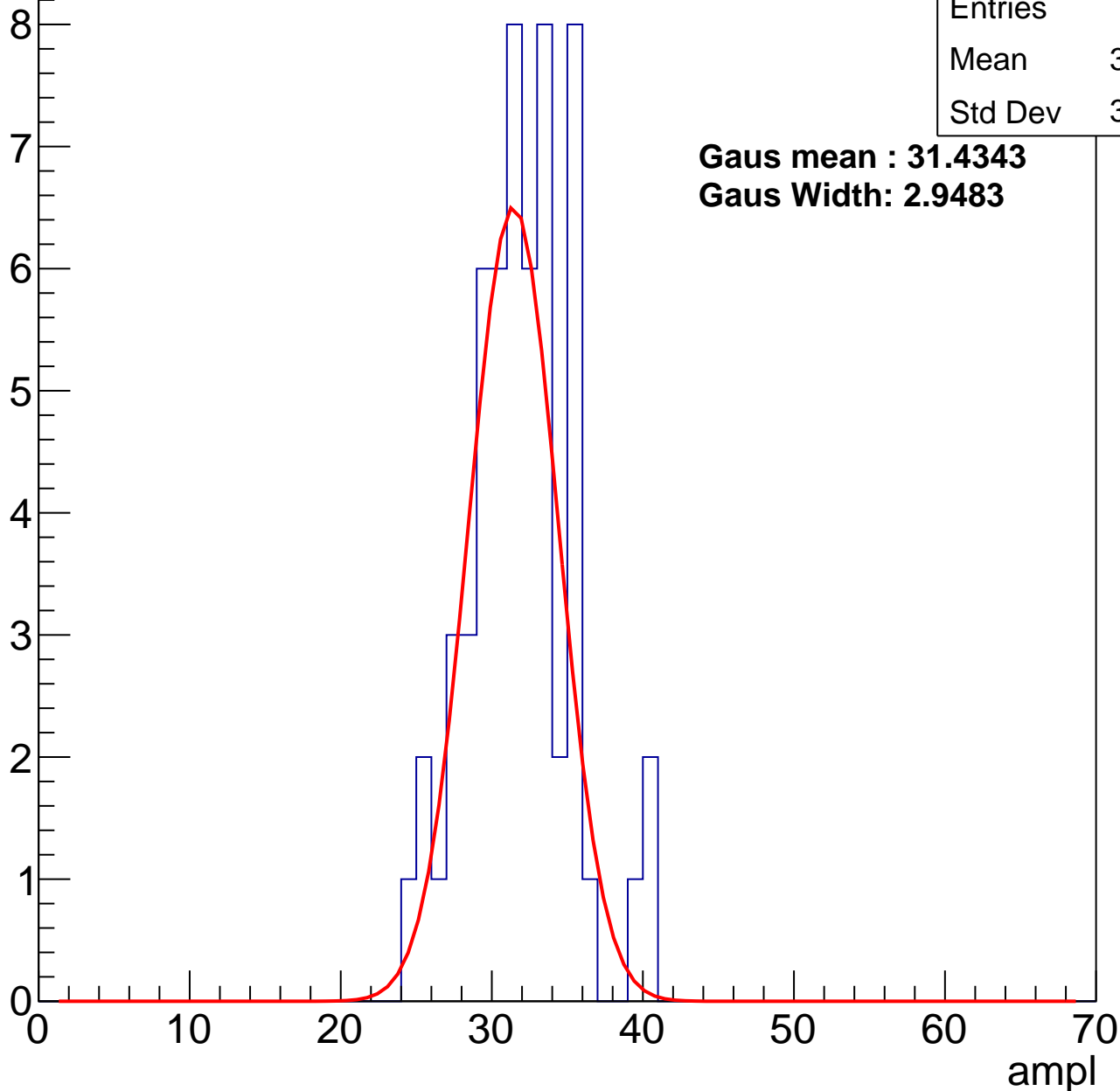
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	31.48
Std Dev	3.405

Gaus mean : 31.4343

Gaus Width: 2.9483



B1L101S, U25-ch36, adc1

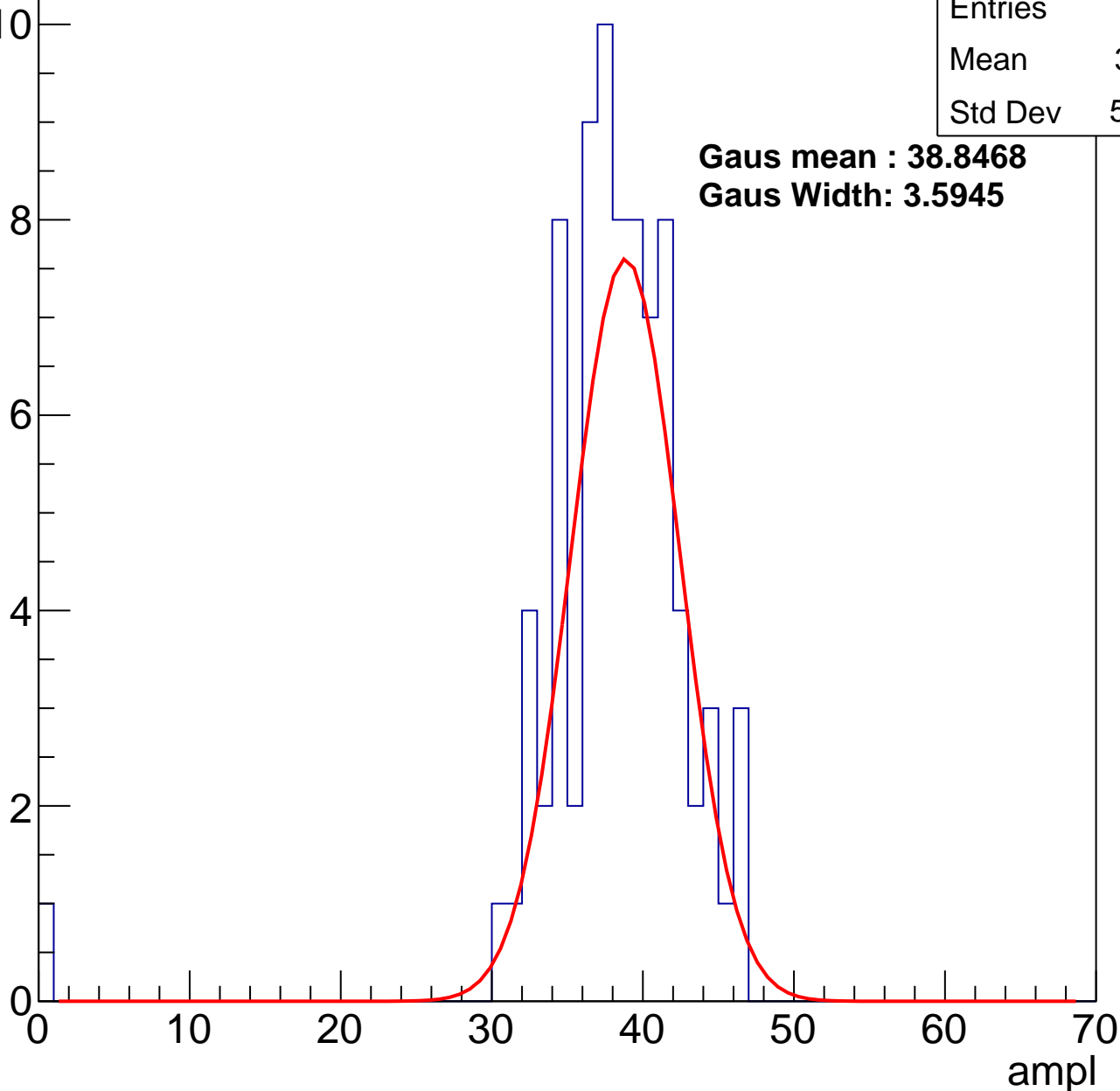
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	37.61
Std Dev	5.523

Gaus mean : 38.8468

Gaus Width: 3.5945



B1L101S, U25-ch36, adc2

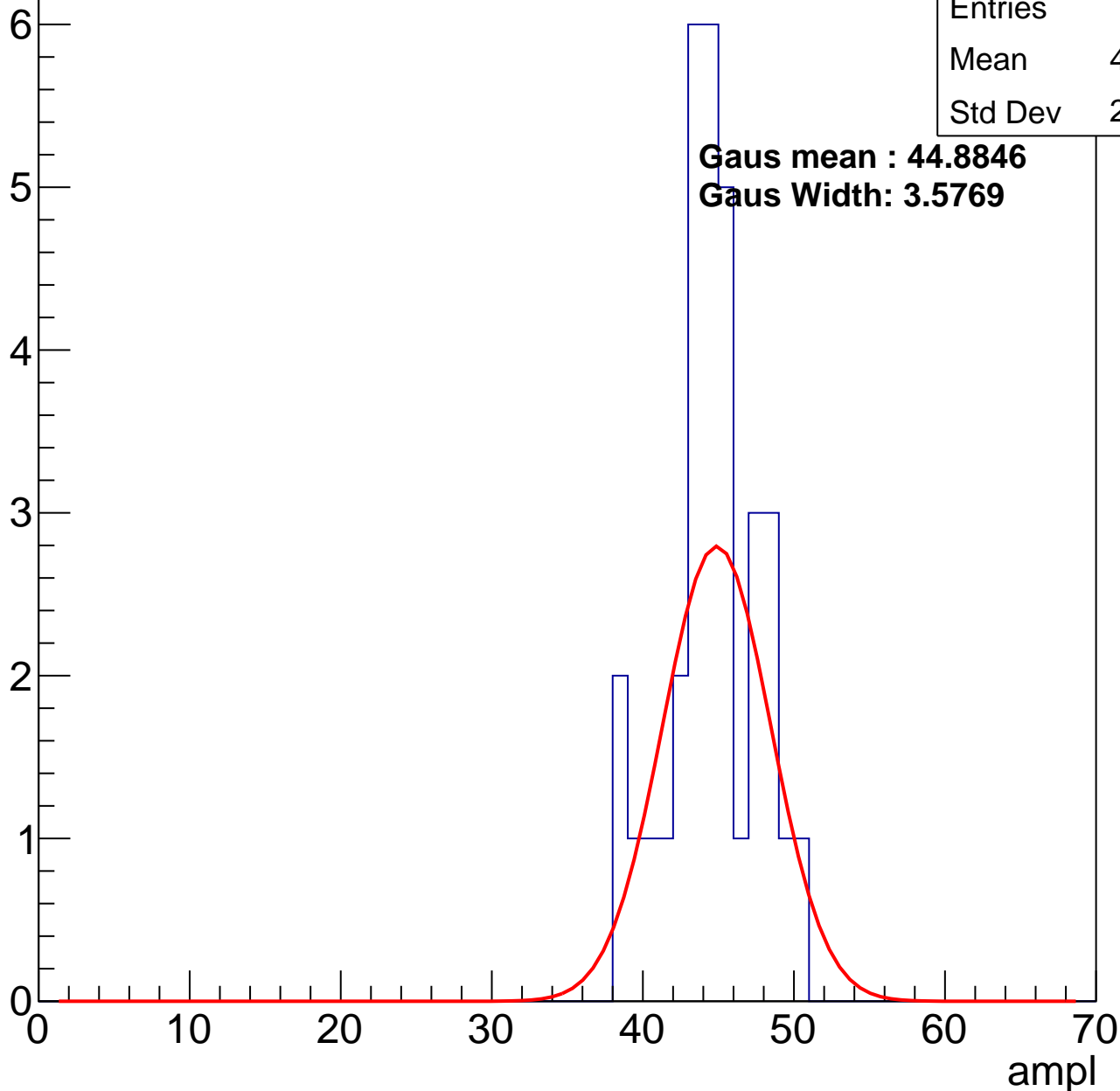
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	44.15
Std Dev	2.914

Gaus mean : 44.8846

Gaus Width: 3.5769



B1L101S, U25-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	49.92
Std Dev	3.903

Entry

10

8

6

4

2

0

0

10

20

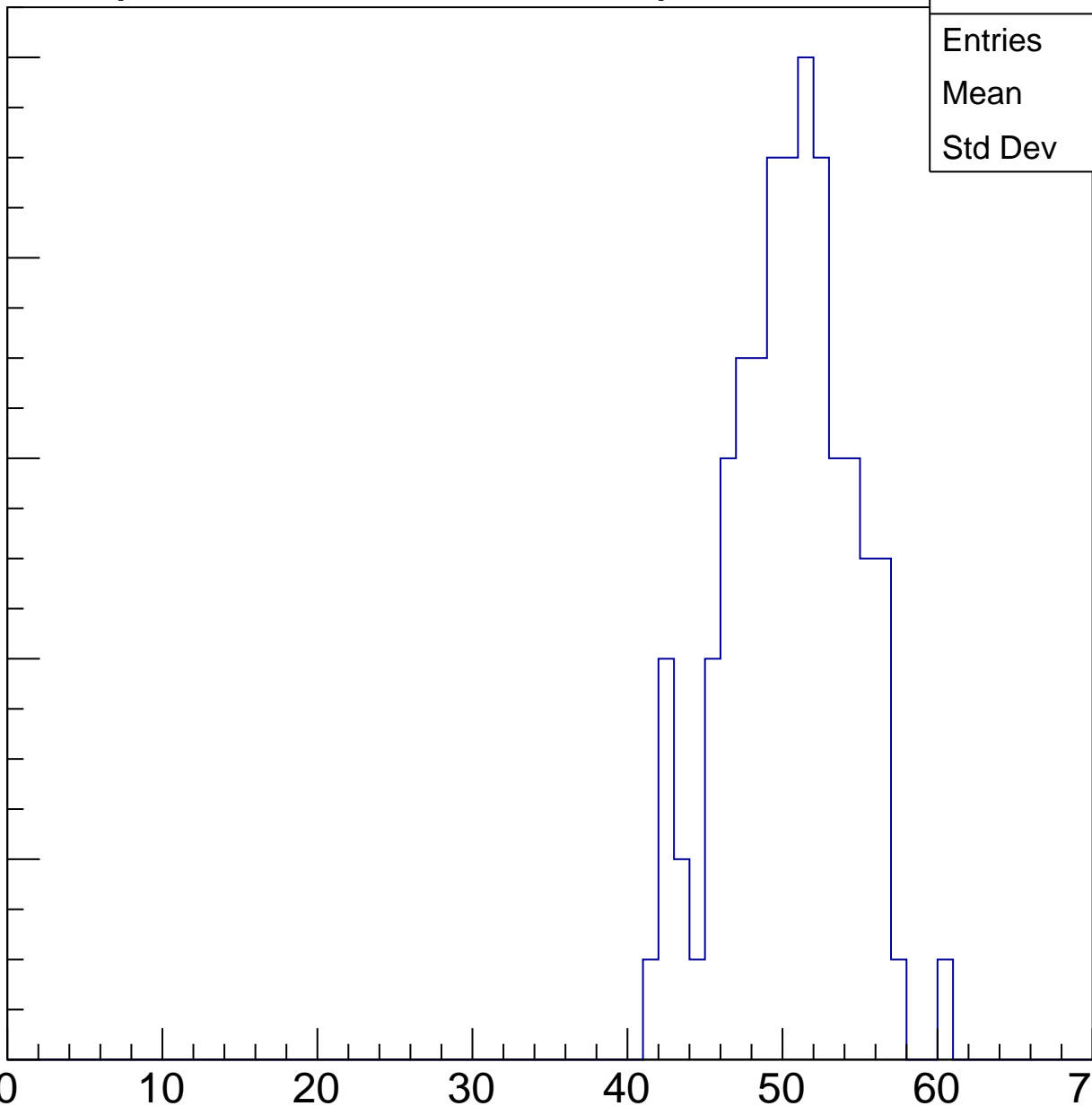
30

40

50

60

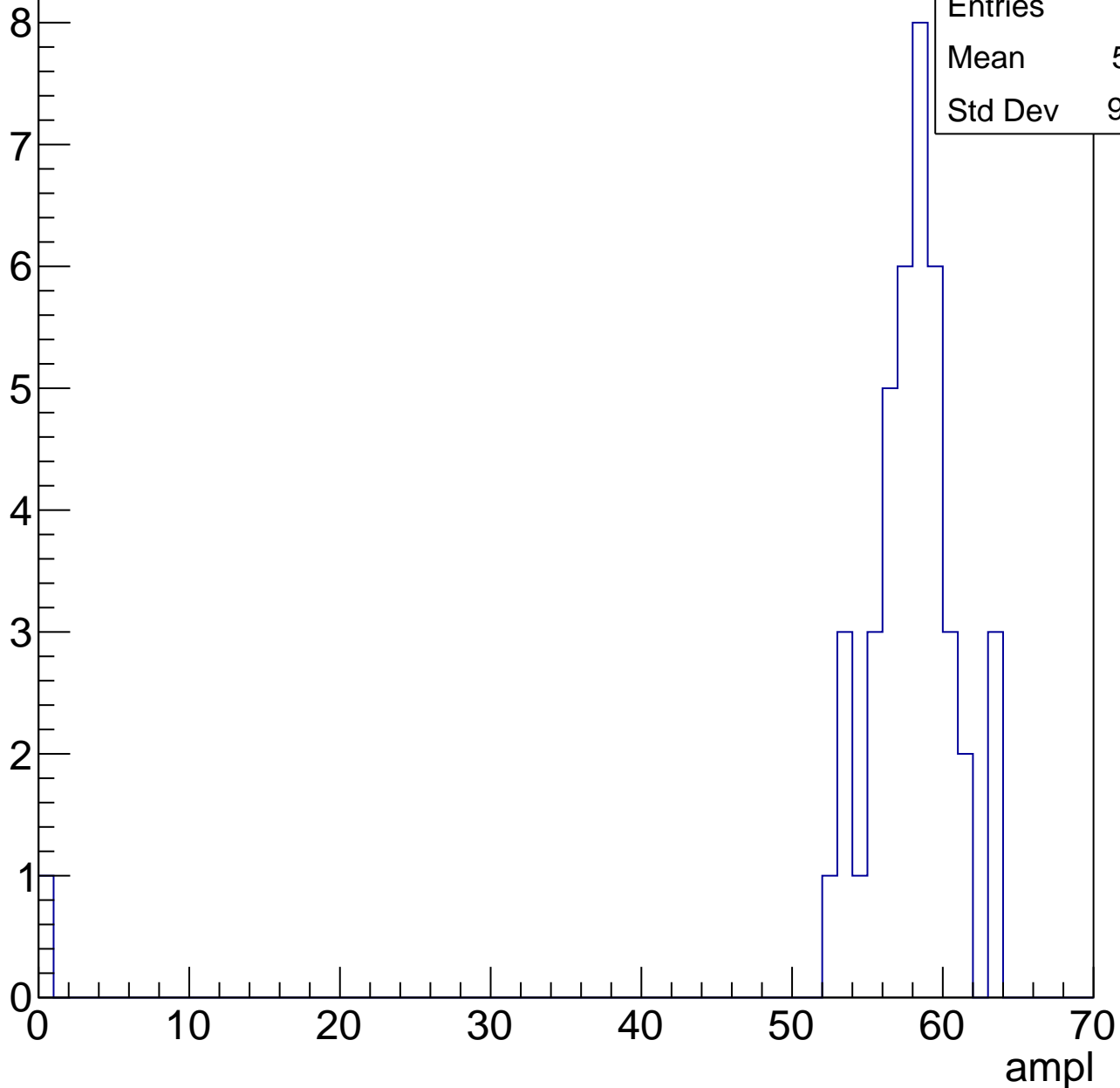
ampl



B1L101S, U25-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

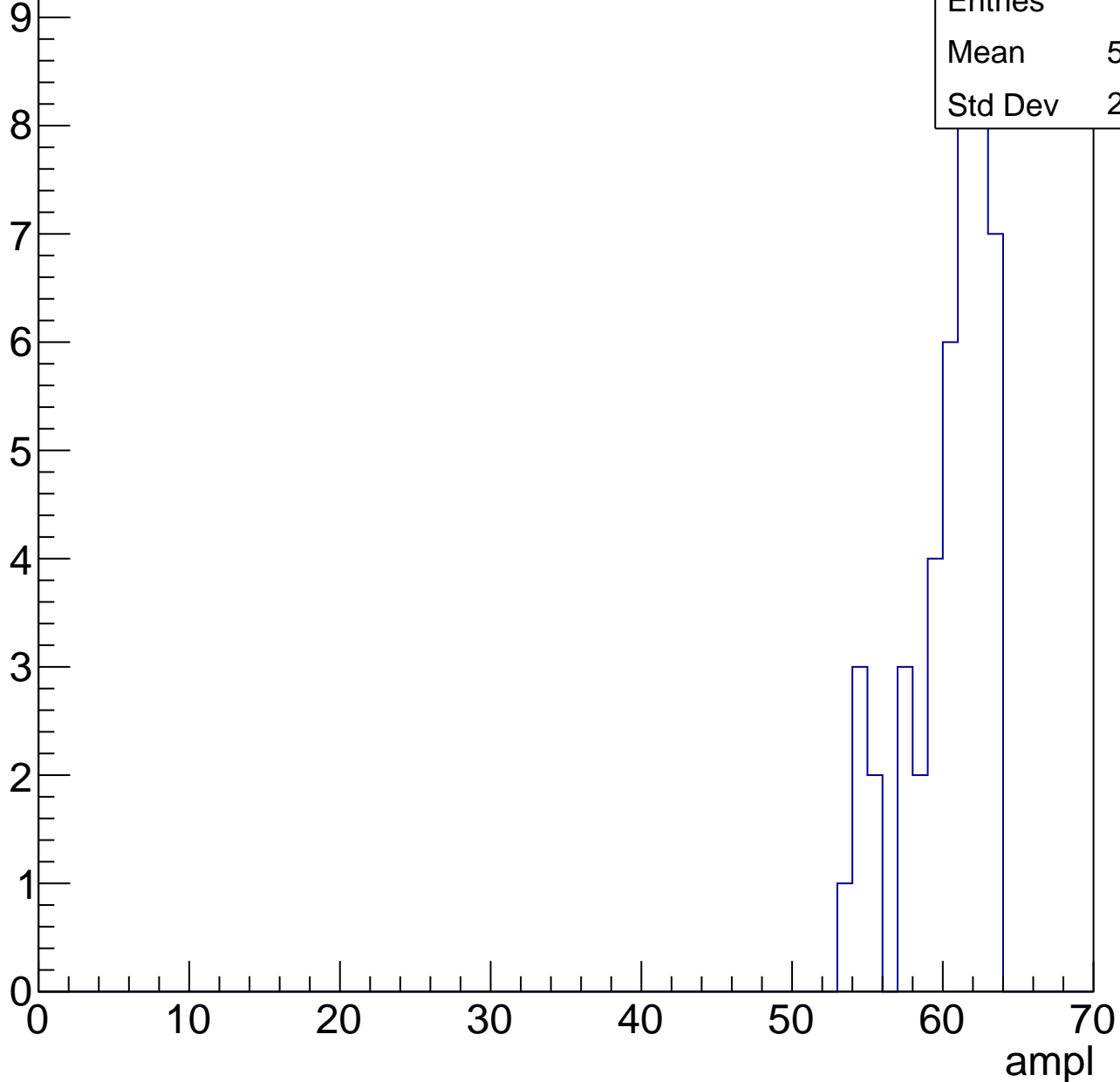
Entry



B1L101S, U25-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L101S, U25-ch37, adc0

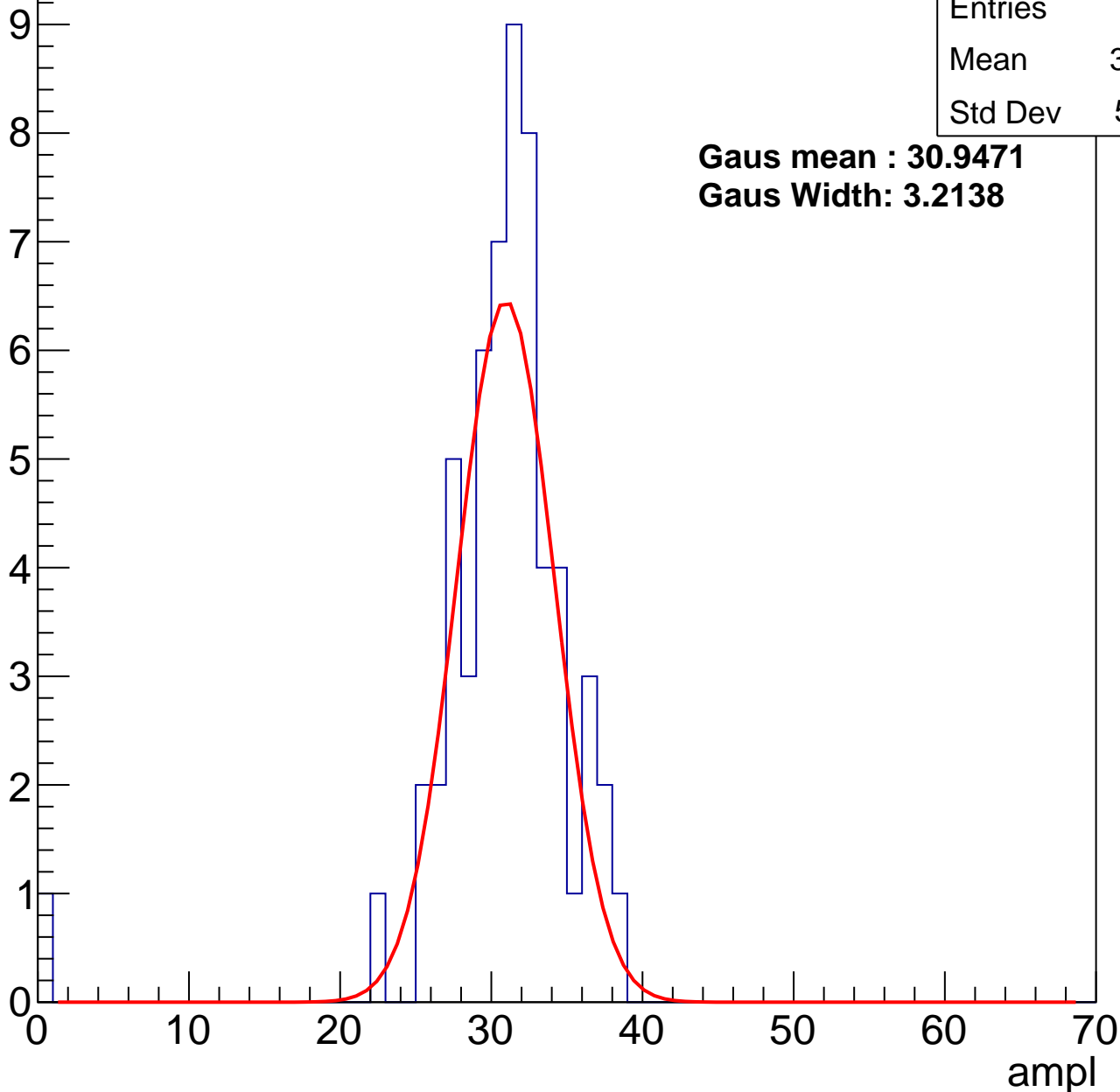
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	30.25
Std Dev	5.111

Gaus mean : 30.9471

Gaus Width: 3.2138



B1L101S, U25-ch37, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	38.41
Std Dev	3.794

Gaus mean : 38.6365

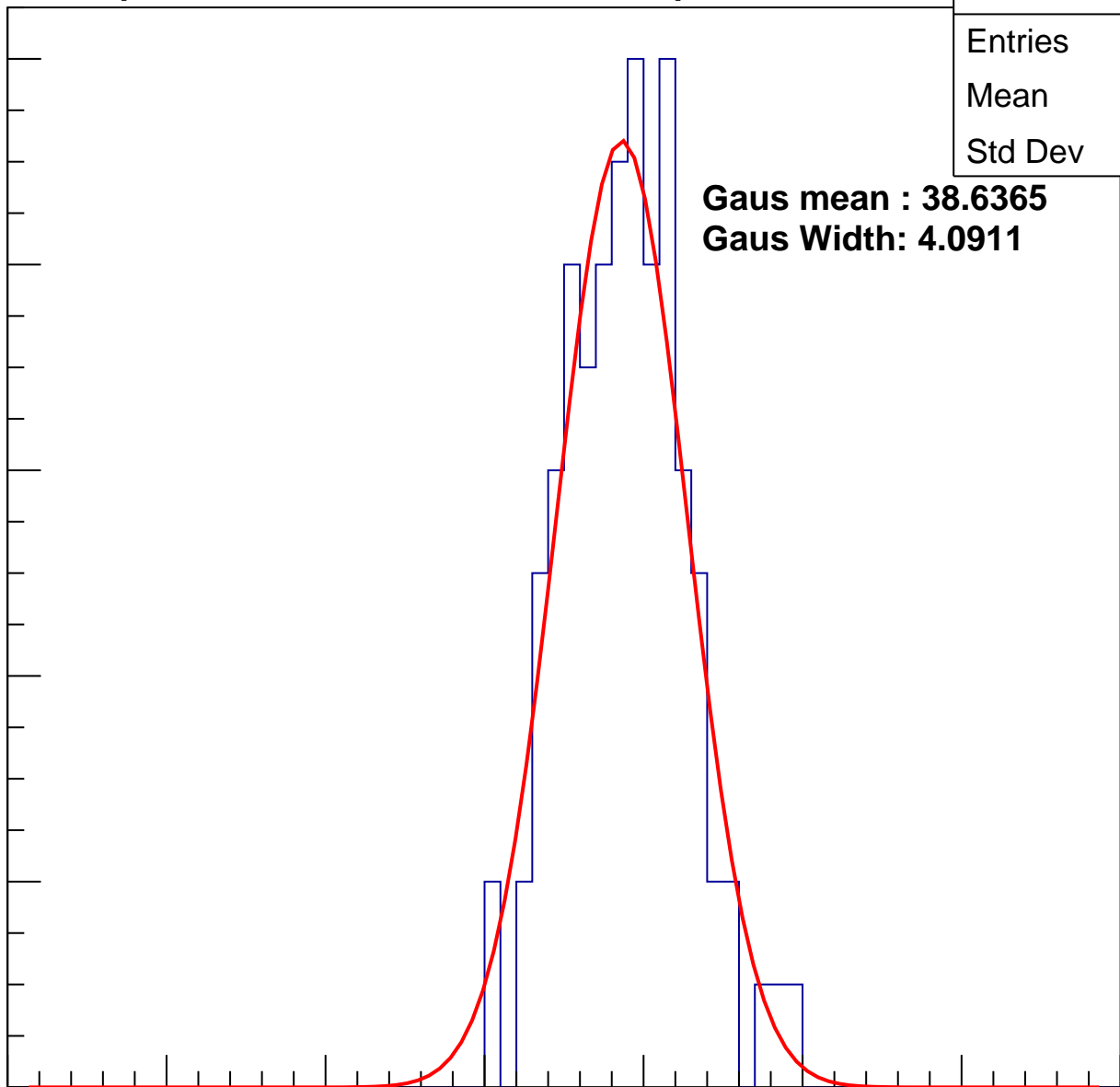
Gaus Width: 4.0911

Entry

10
8
6
4
2
0

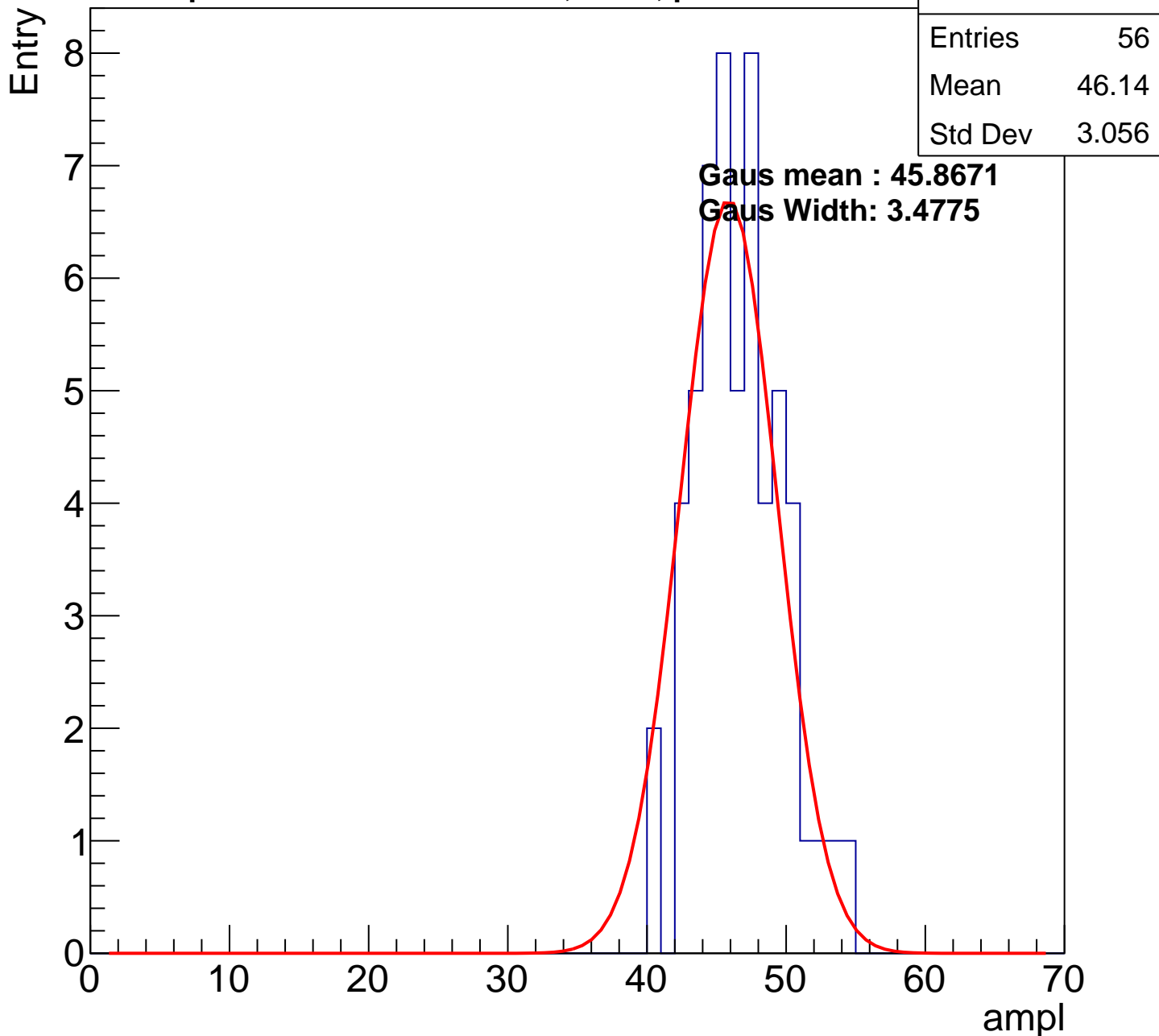
0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch37, adc2

calib_packv5_042523_0143.root, FC#0, port D2

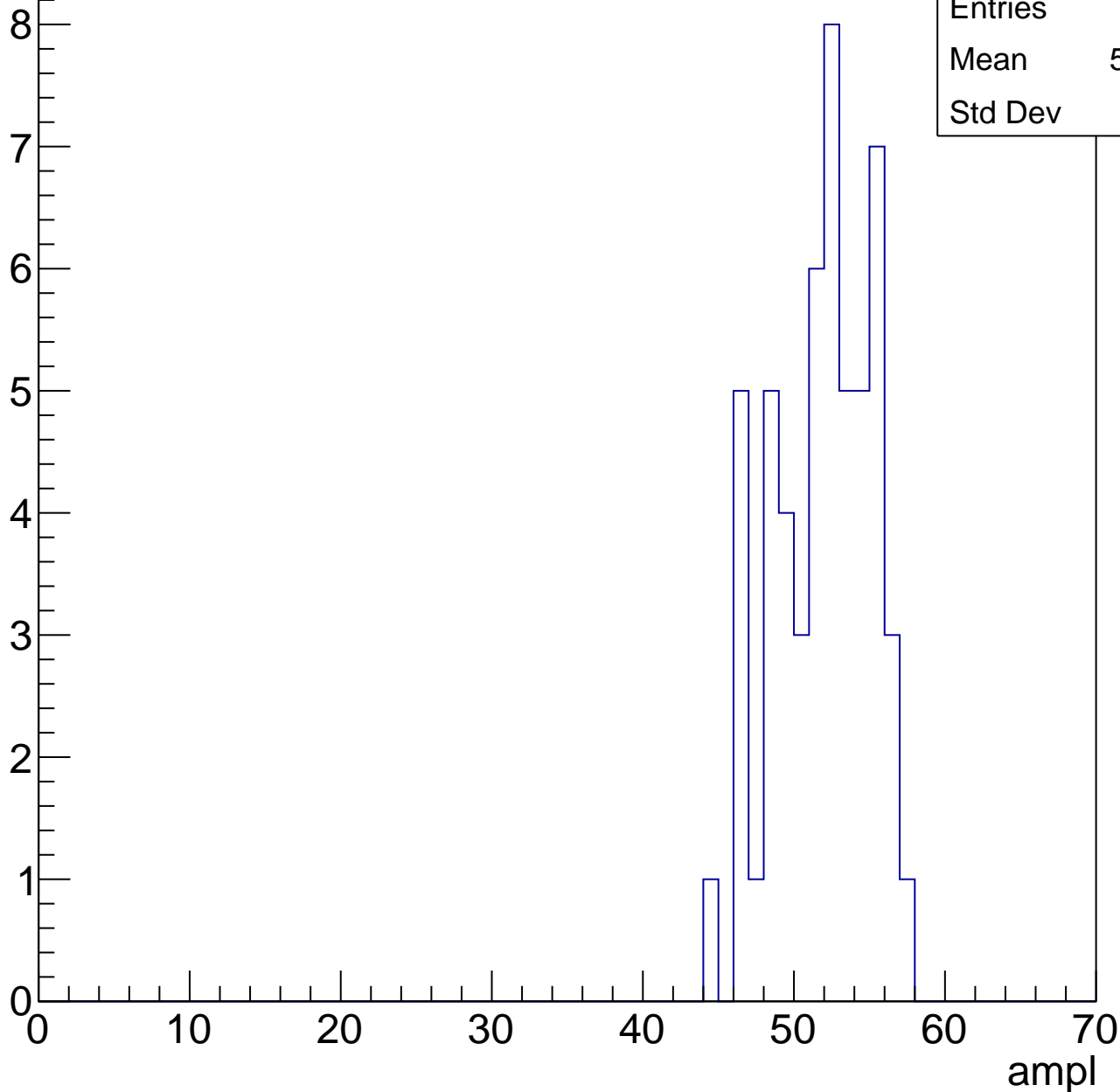


B1L101S, U25-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	51.37
Std Dev	3.17



B1L101S, U25-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 72

Mean 57.49

Std Dev 3.366

8

6

4

2

0

0

10

20

30

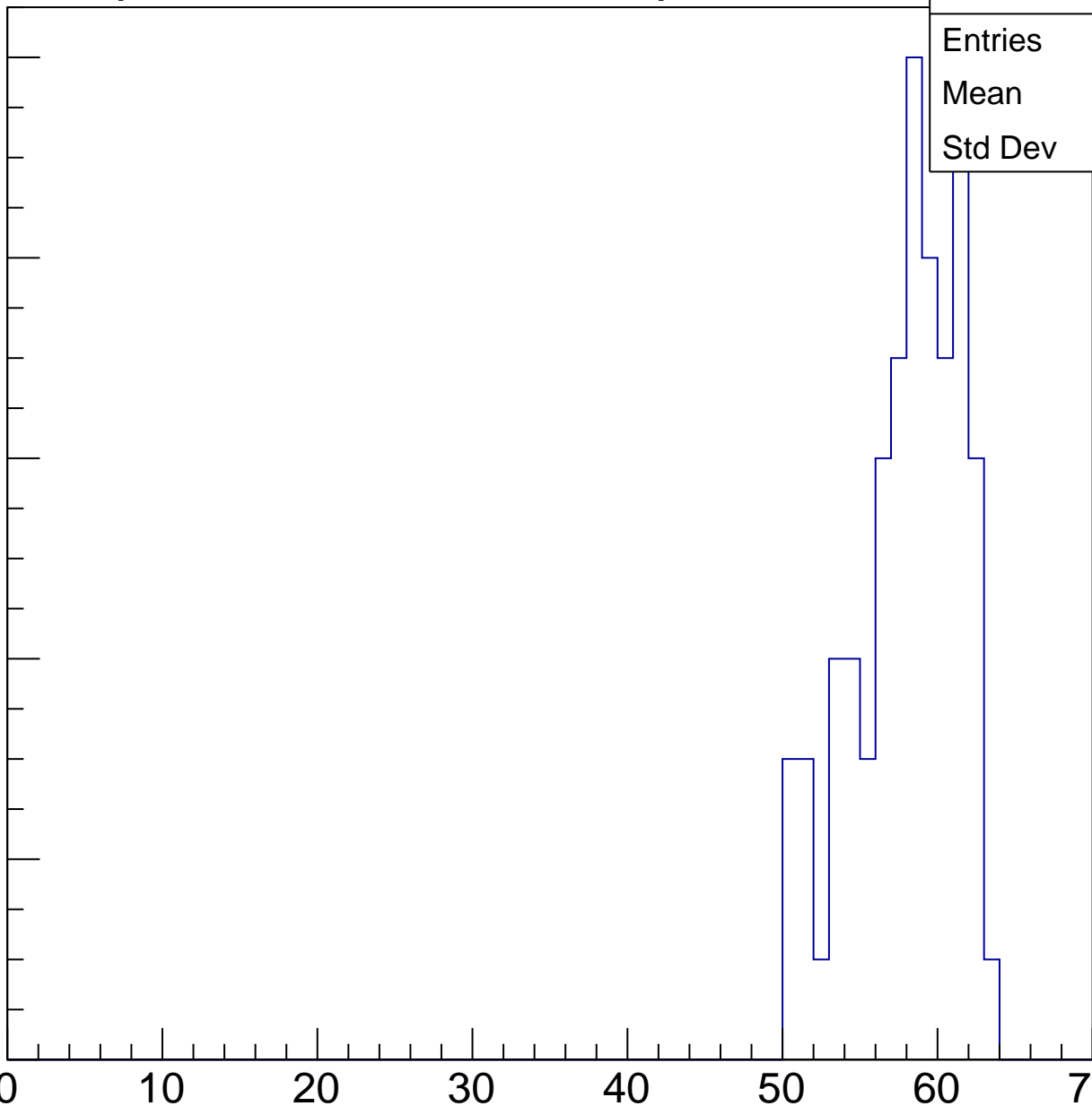
40

50

60

70

ampl

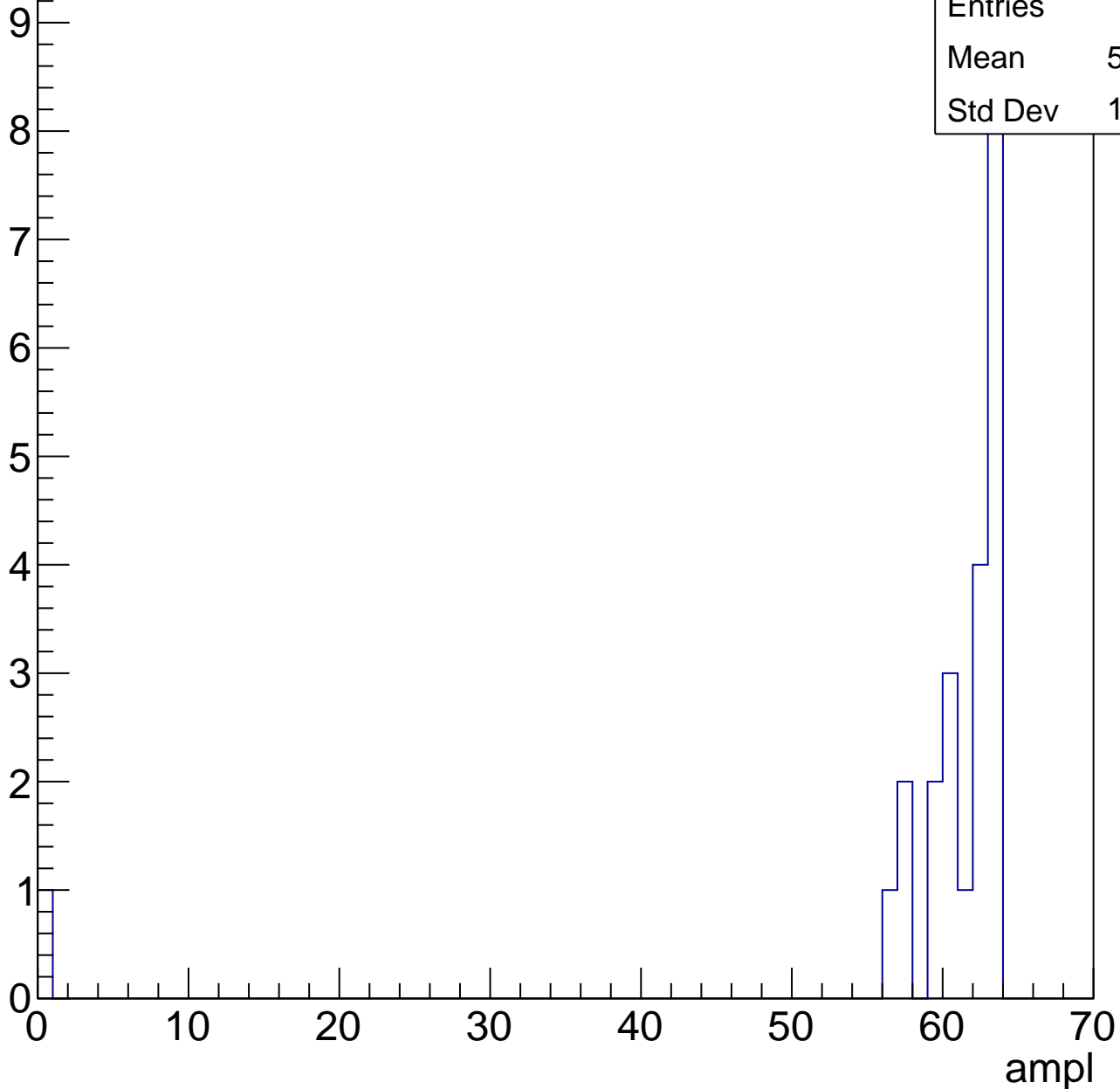


B1L101S, U25-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	58.43
Std Dev	12.65



B1L101S, U25-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch38, adc0

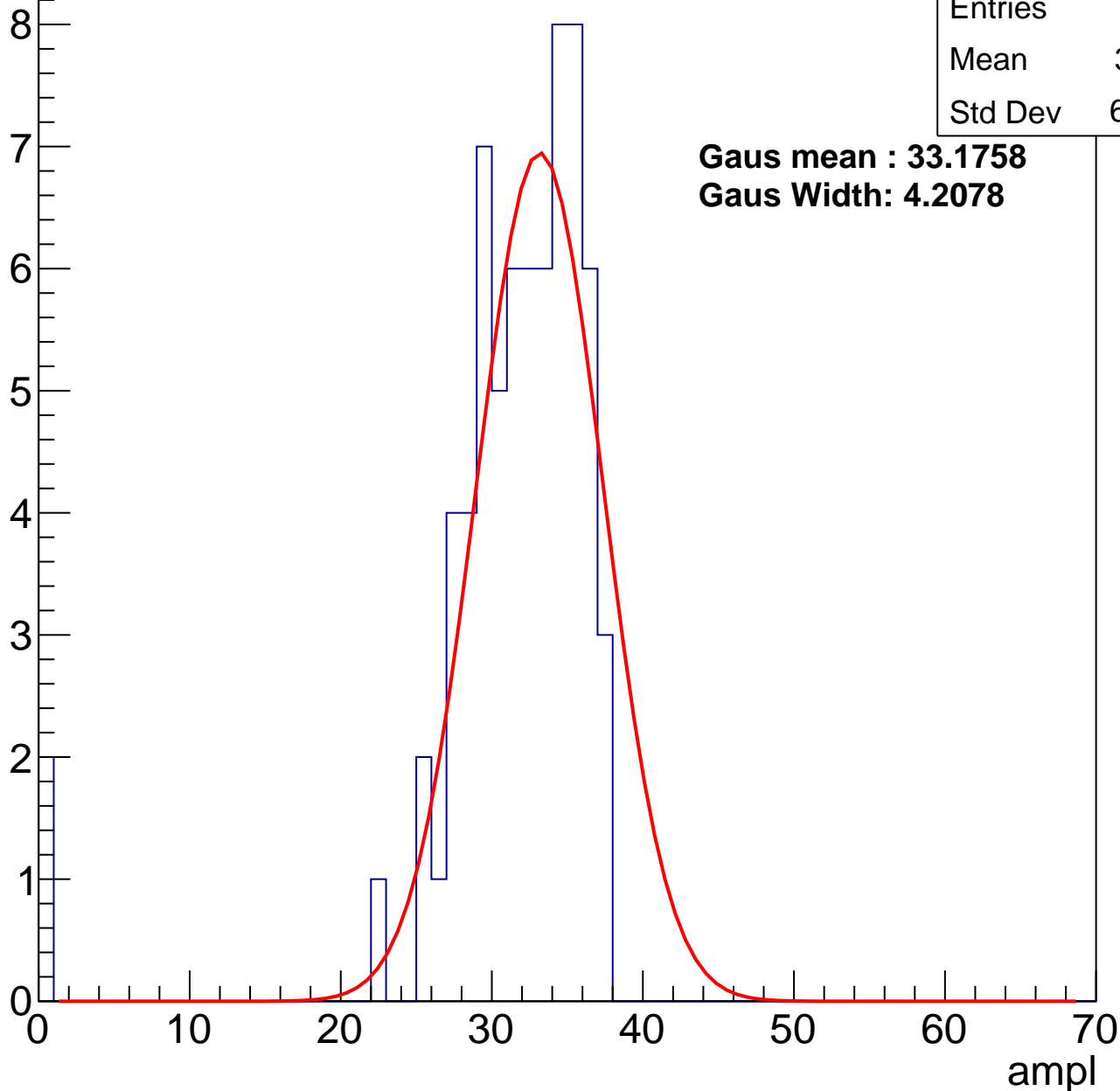
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.81
Std Dev	6.277

Gaus mean : 33.1758

Gaus Width: 4.2078



B1L101S, U25-ch38, adc1

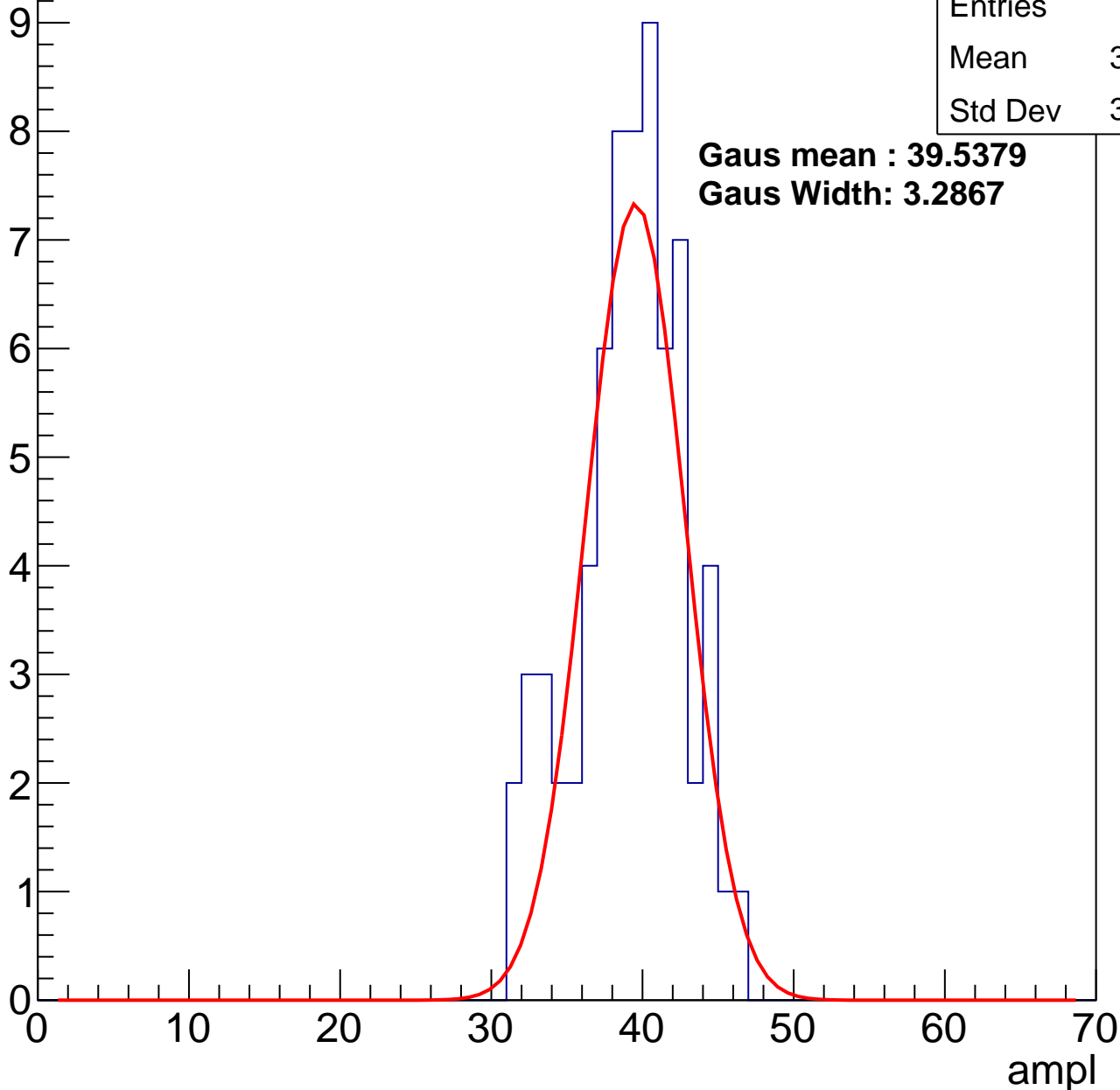
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	38.68
Std Dev	3.512

Gaus mean : 39.5379

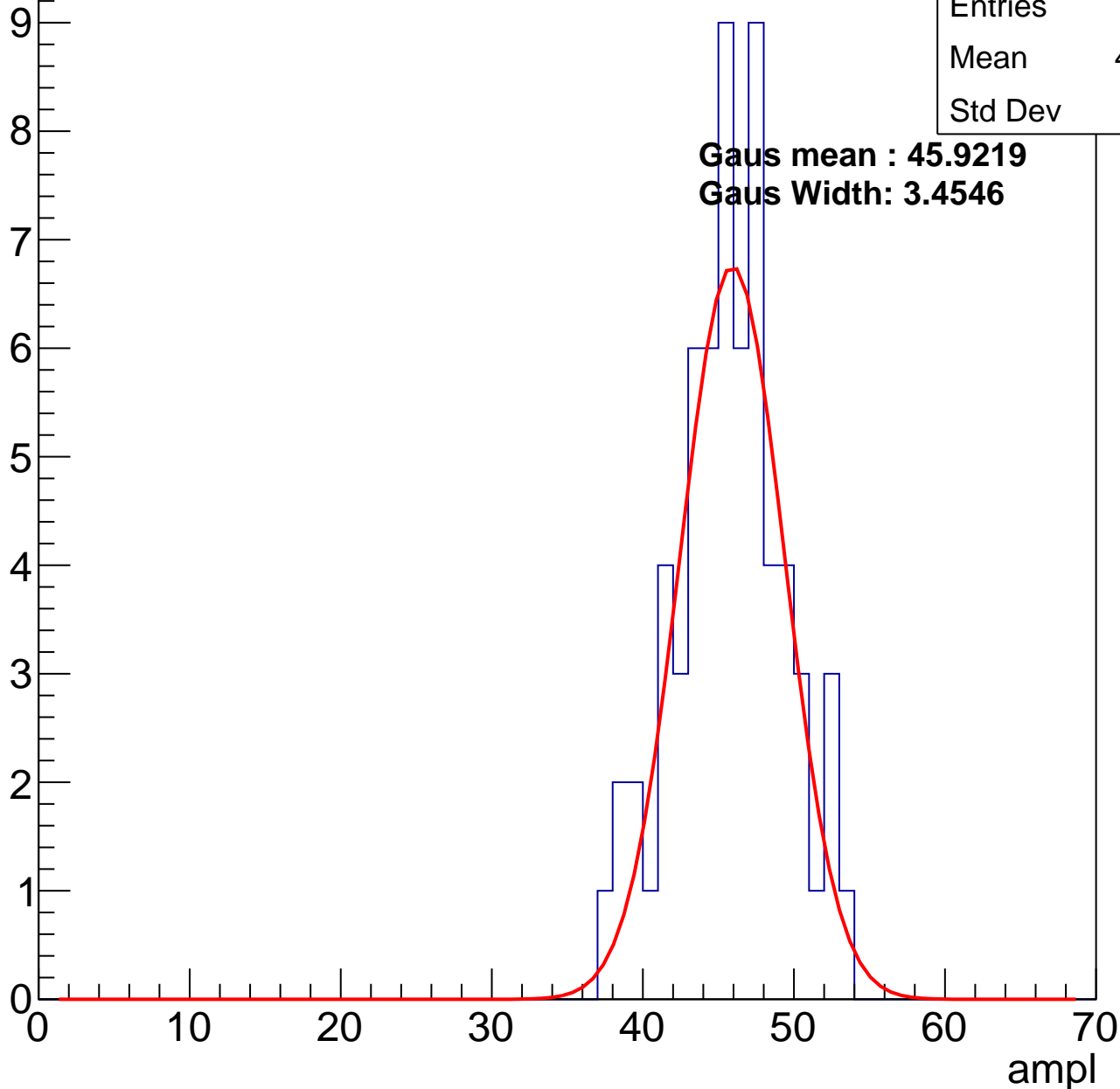
Gaus Width: 3.2867



B1L101S, U25-ch38, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

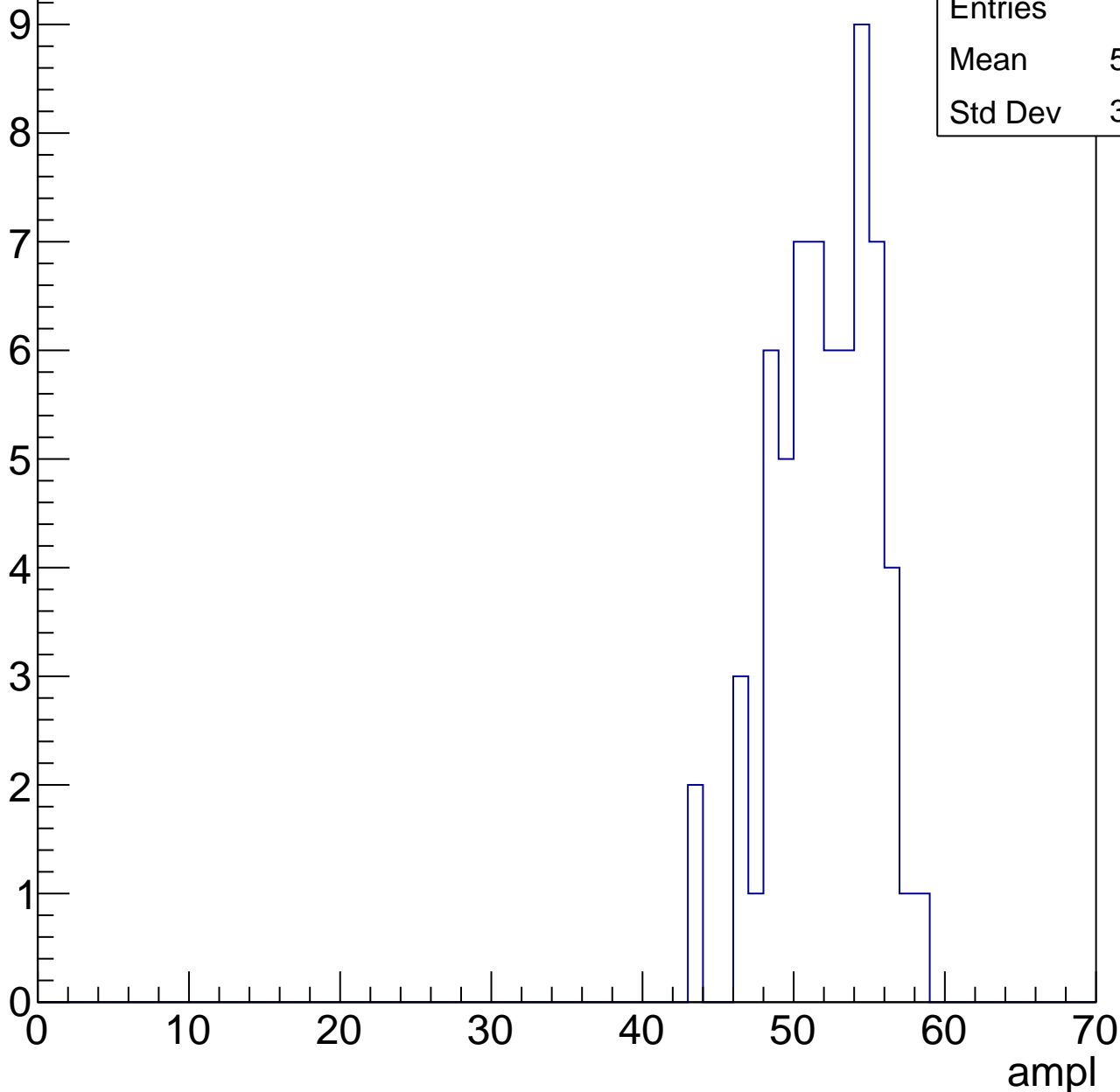


B1L101S, U25-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

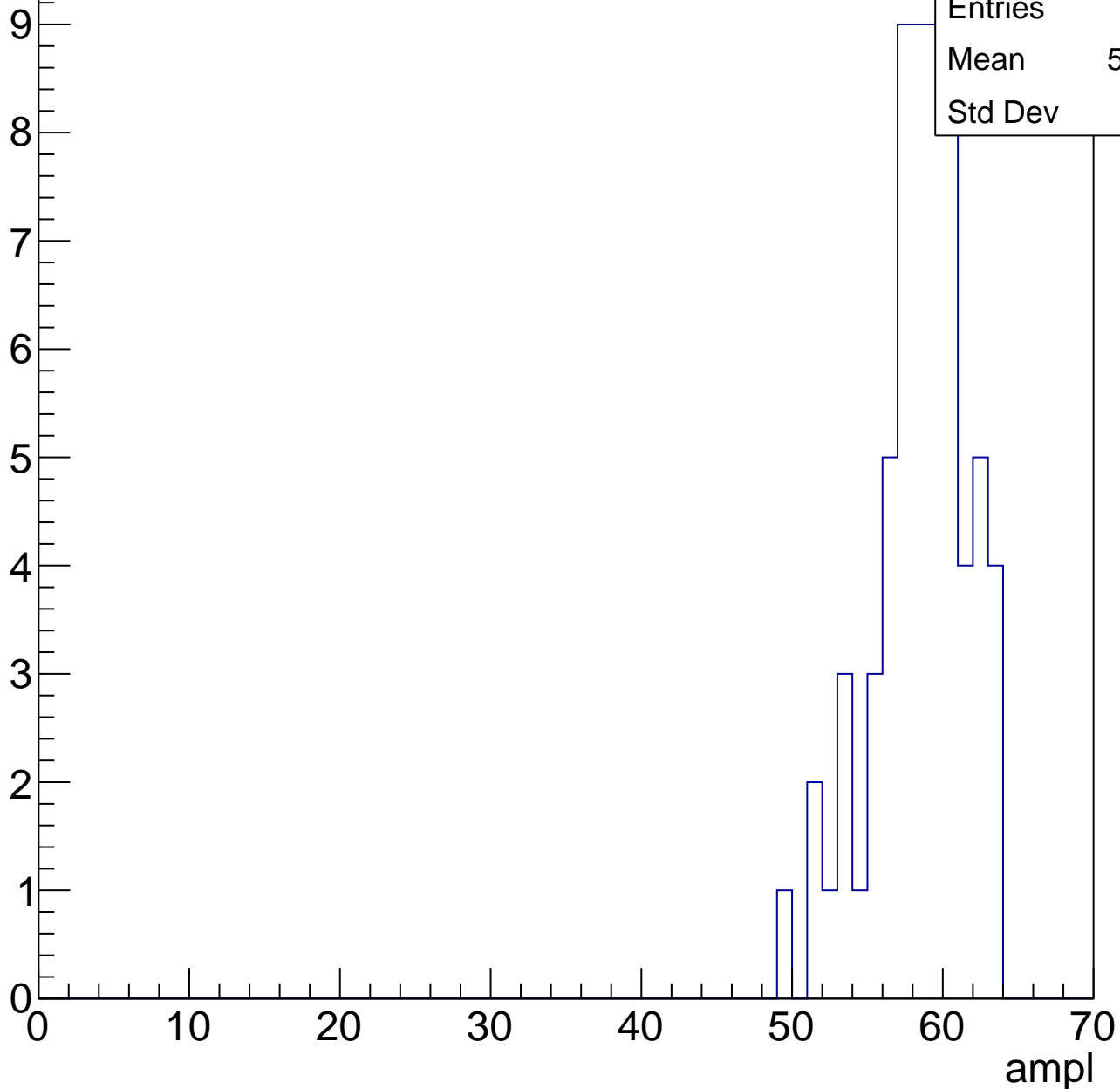
Entries	65
Mean	51.55
Std Dev	3.254



B1L101S, U25-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

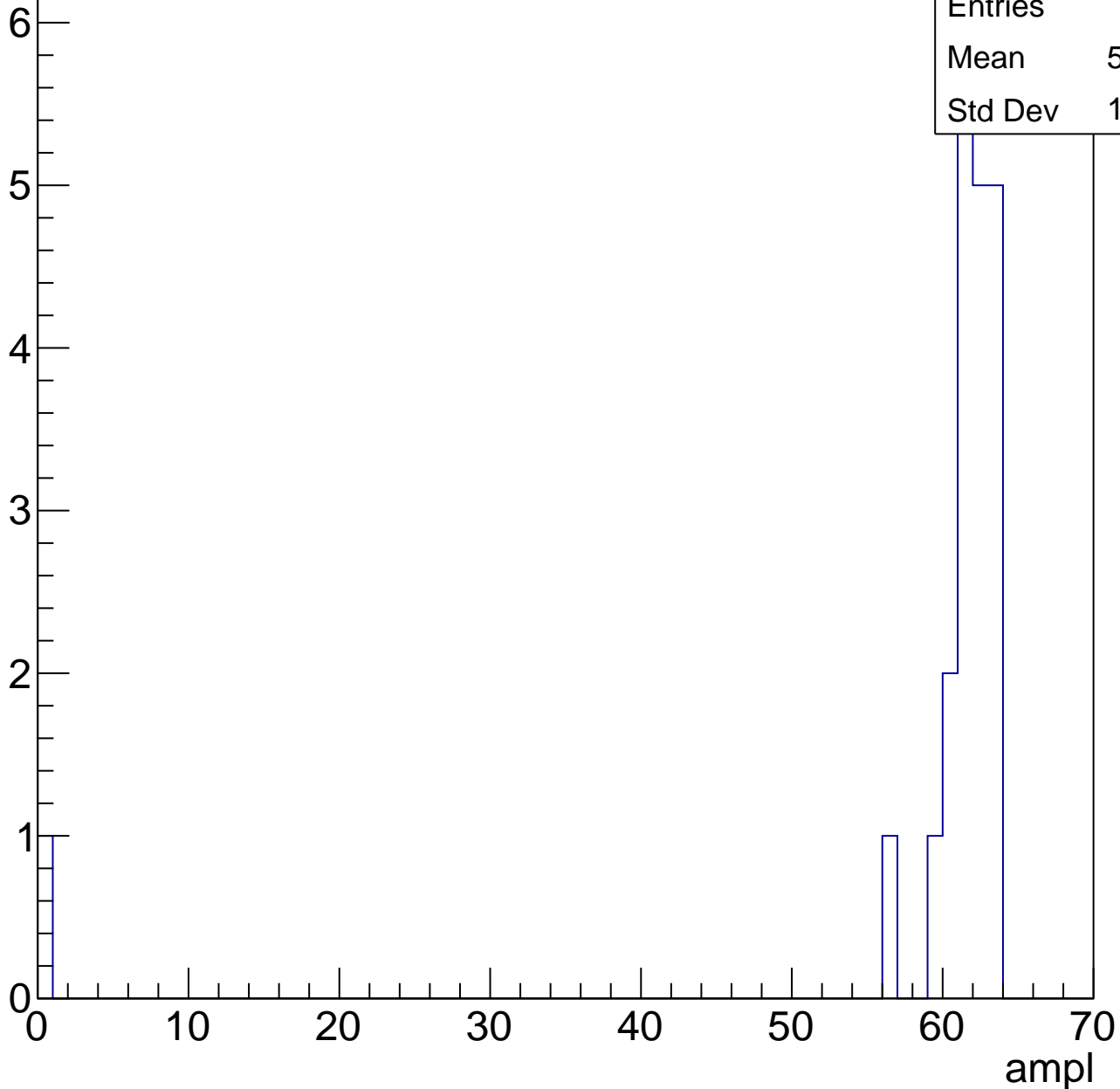


B1L101S, U25-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.38
Std Dev	13.15



B1L101S, U25-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U25-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch39, adc0

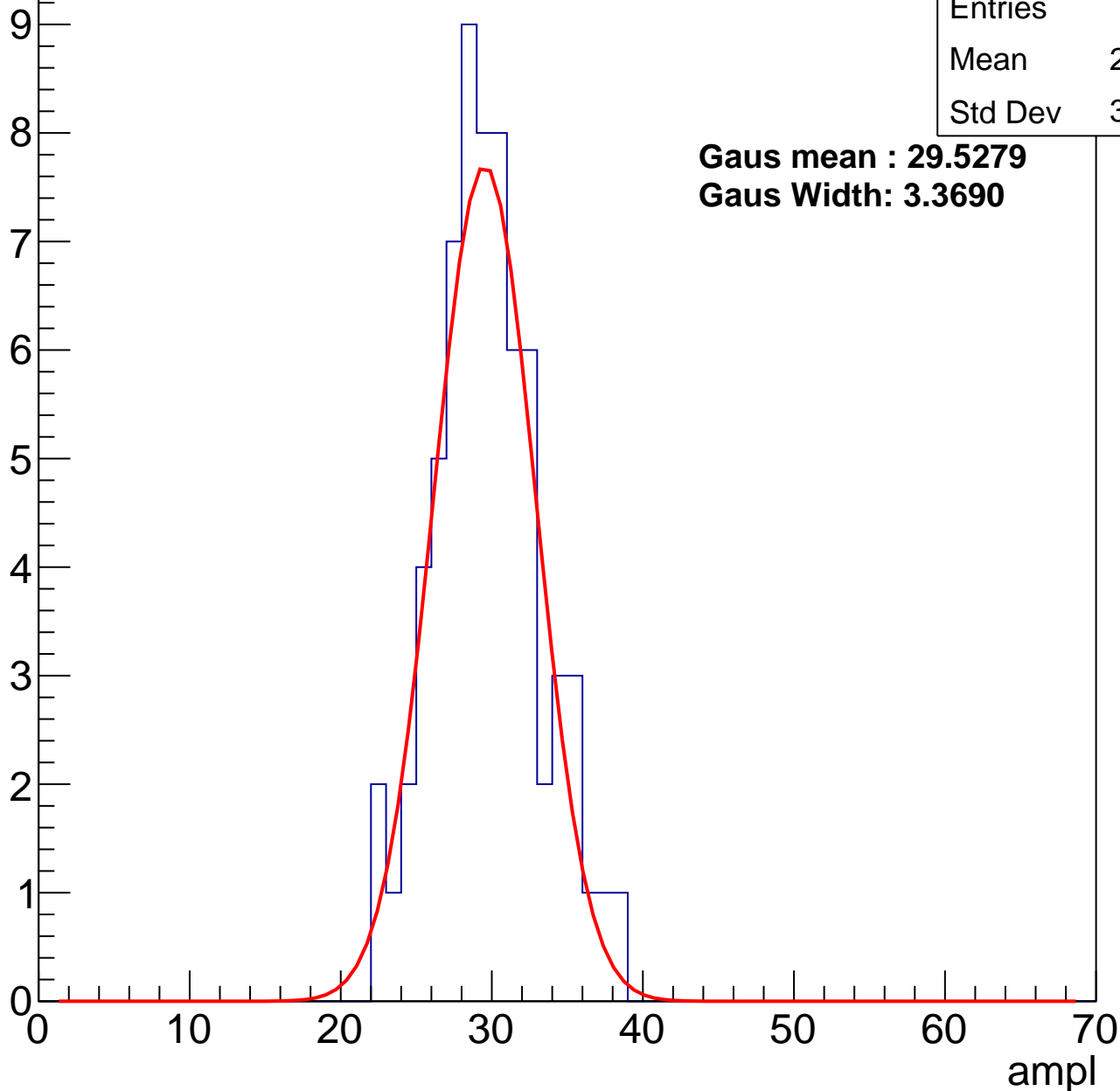
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.28
Std Dev	3.447

Gaus mean : 29.5279

Gaus Width: 3.3690



B1L101S, U25-ch39, adc1

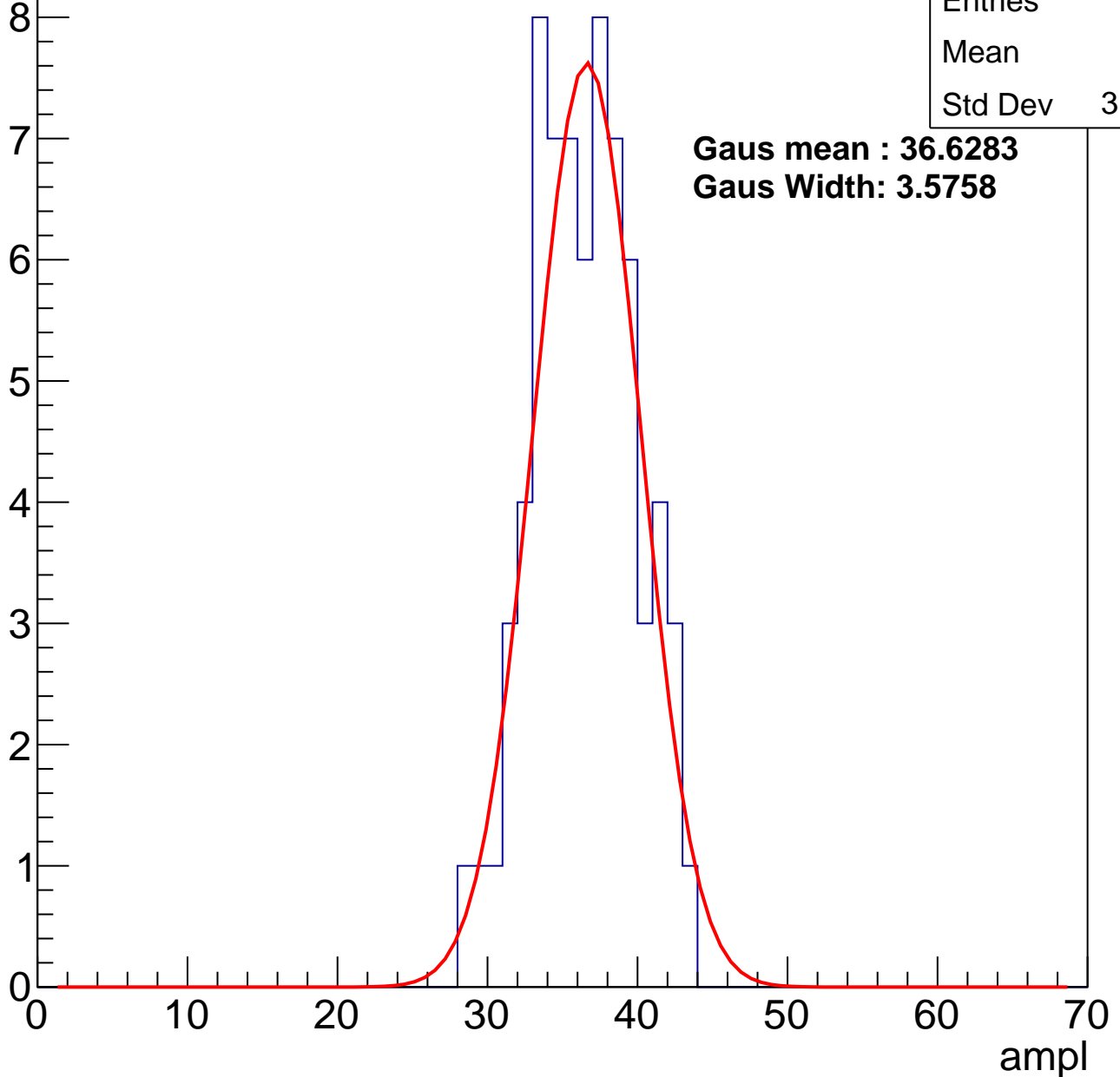
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	36
Std Dev	3.359

Gaus mean : 36.6283

Gaus Width: 3.5758

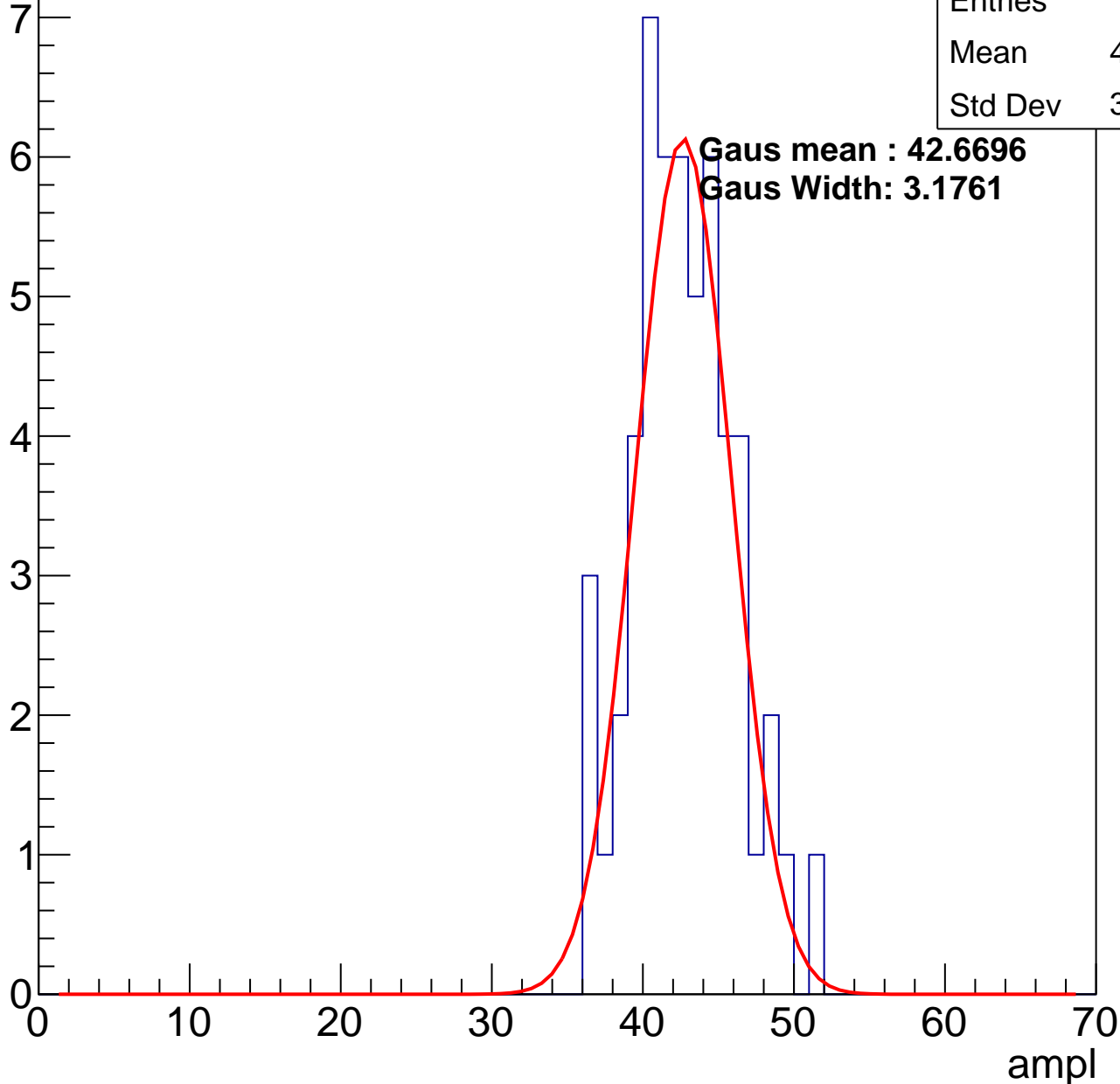


B1L101S, U25-ch39, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.28
Std Dev	3.327

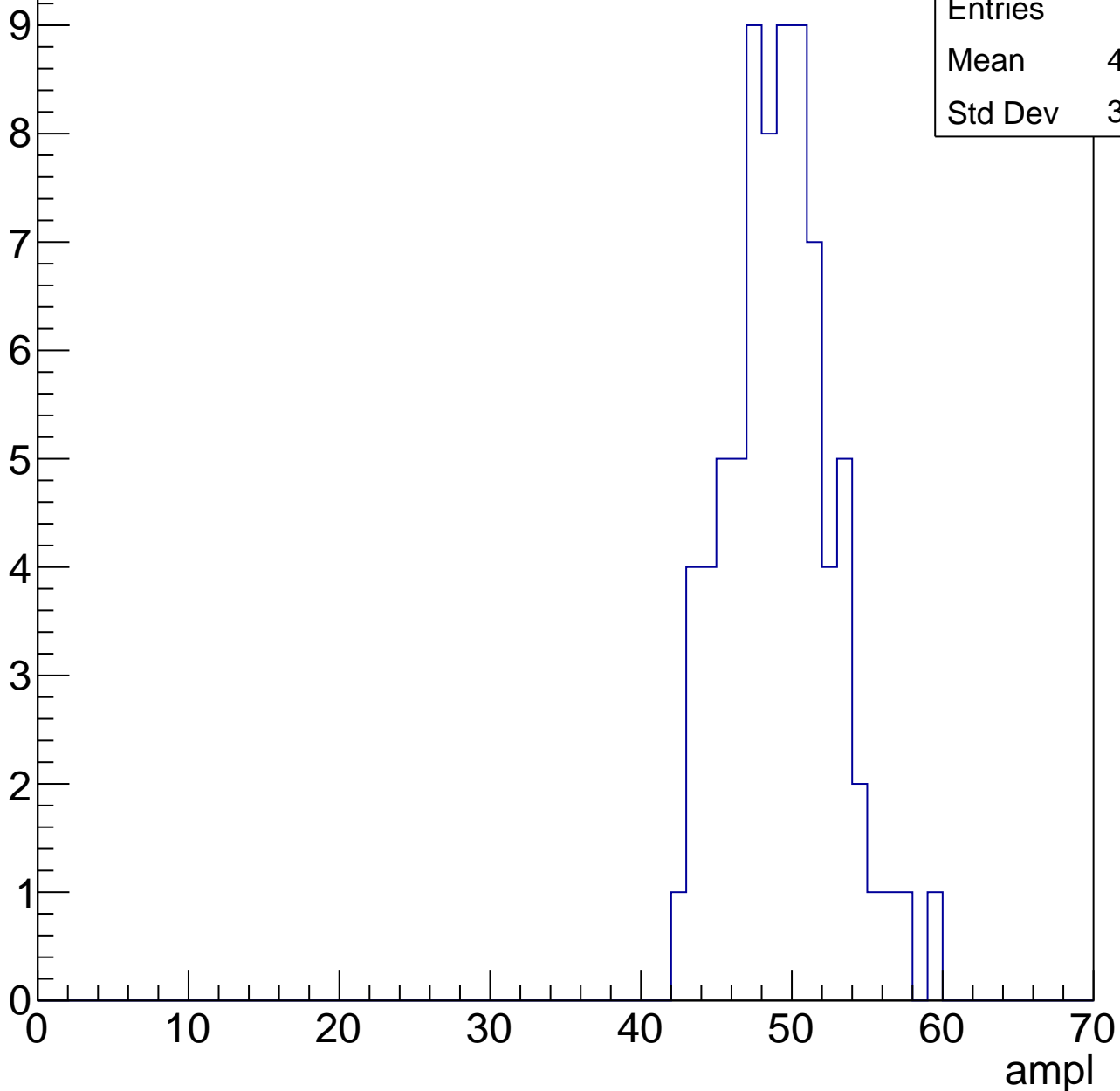


B1L101S, U25-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	48.79
Std Dev	3.462

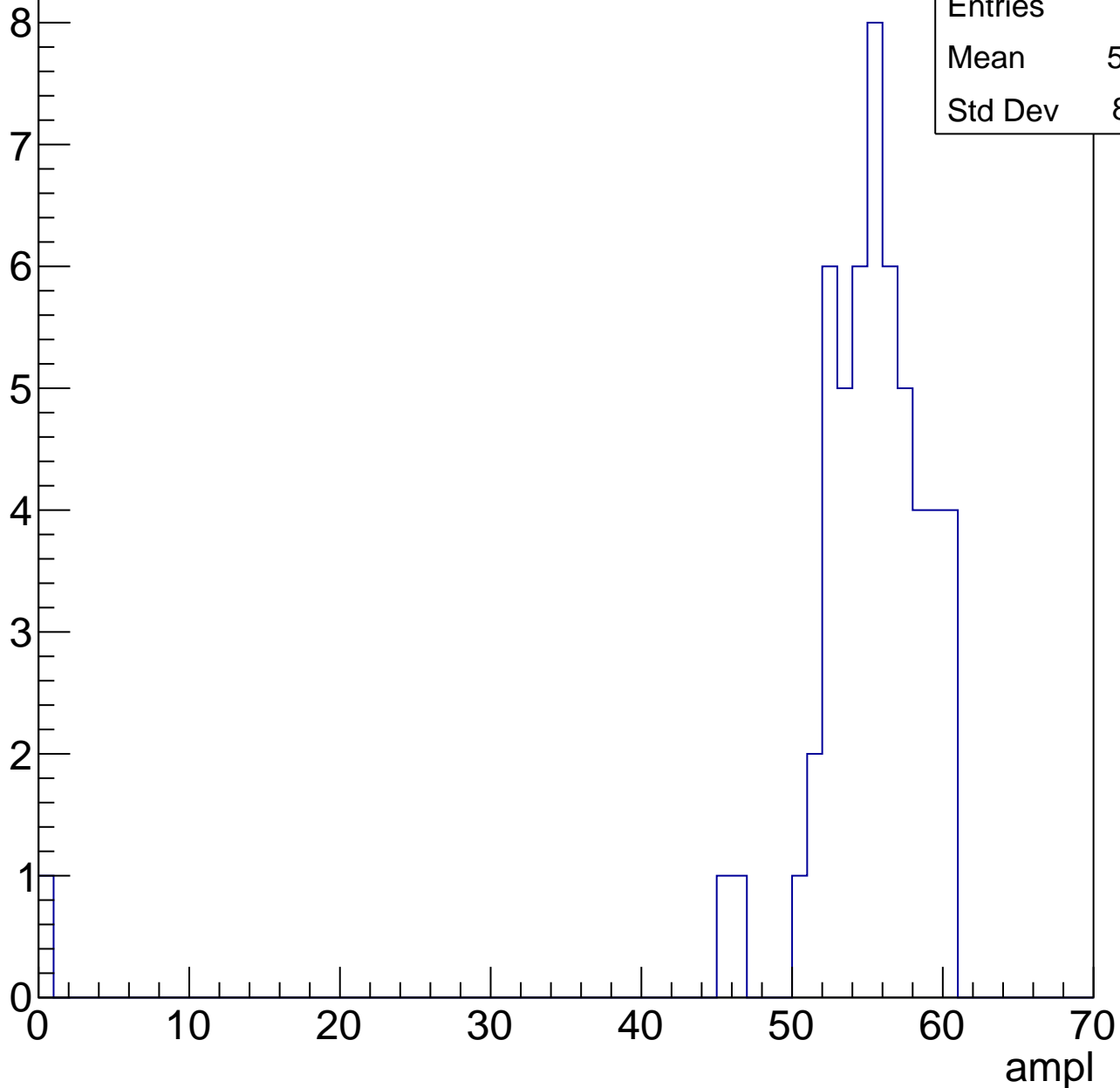


B1L101S, U25-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

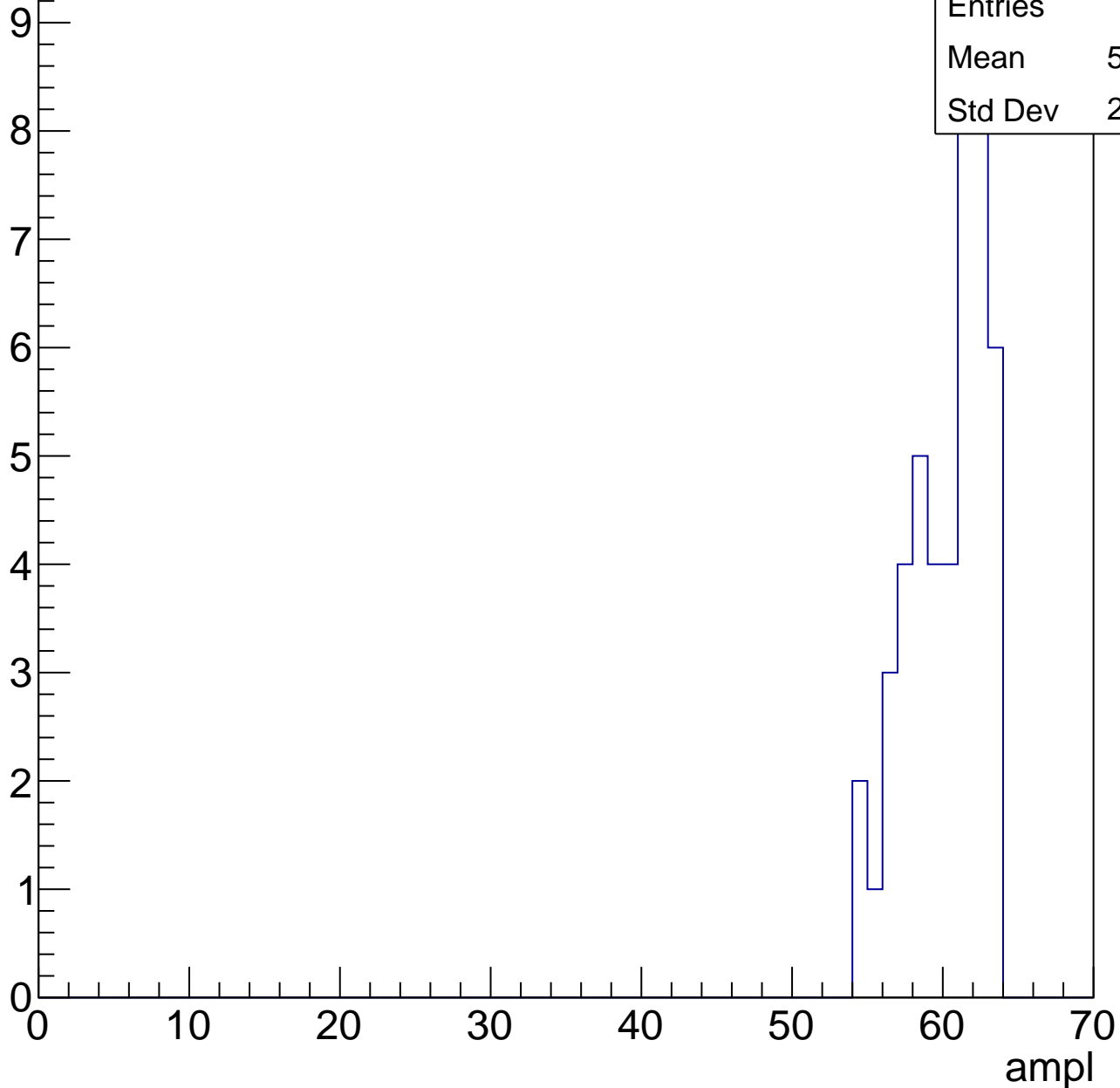
Entries	54
Mean	53.94
Std Dev	8.061



B1L101S, U25-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

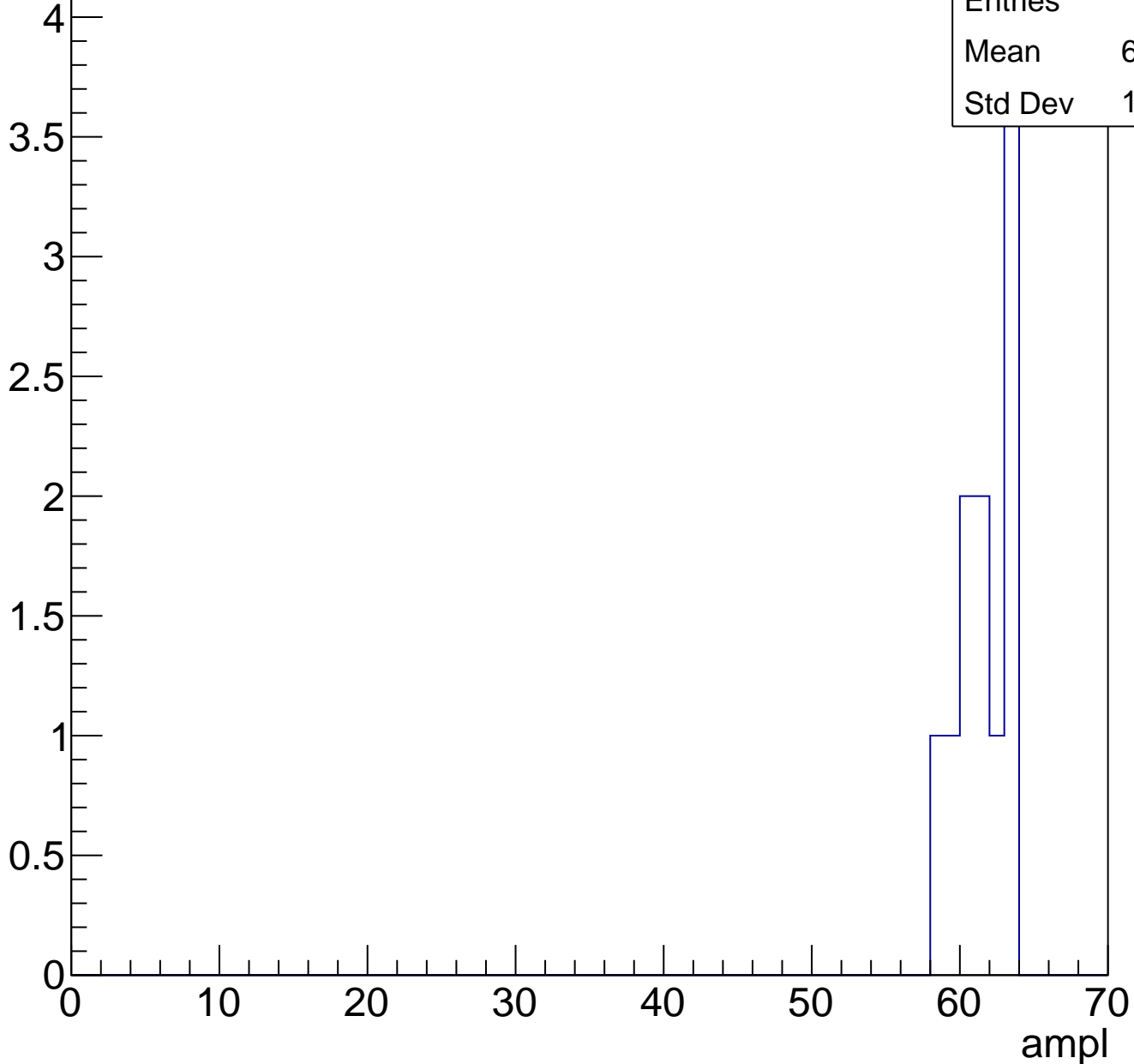
Entry



B1L101S, U25-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U25-ch40, adc0

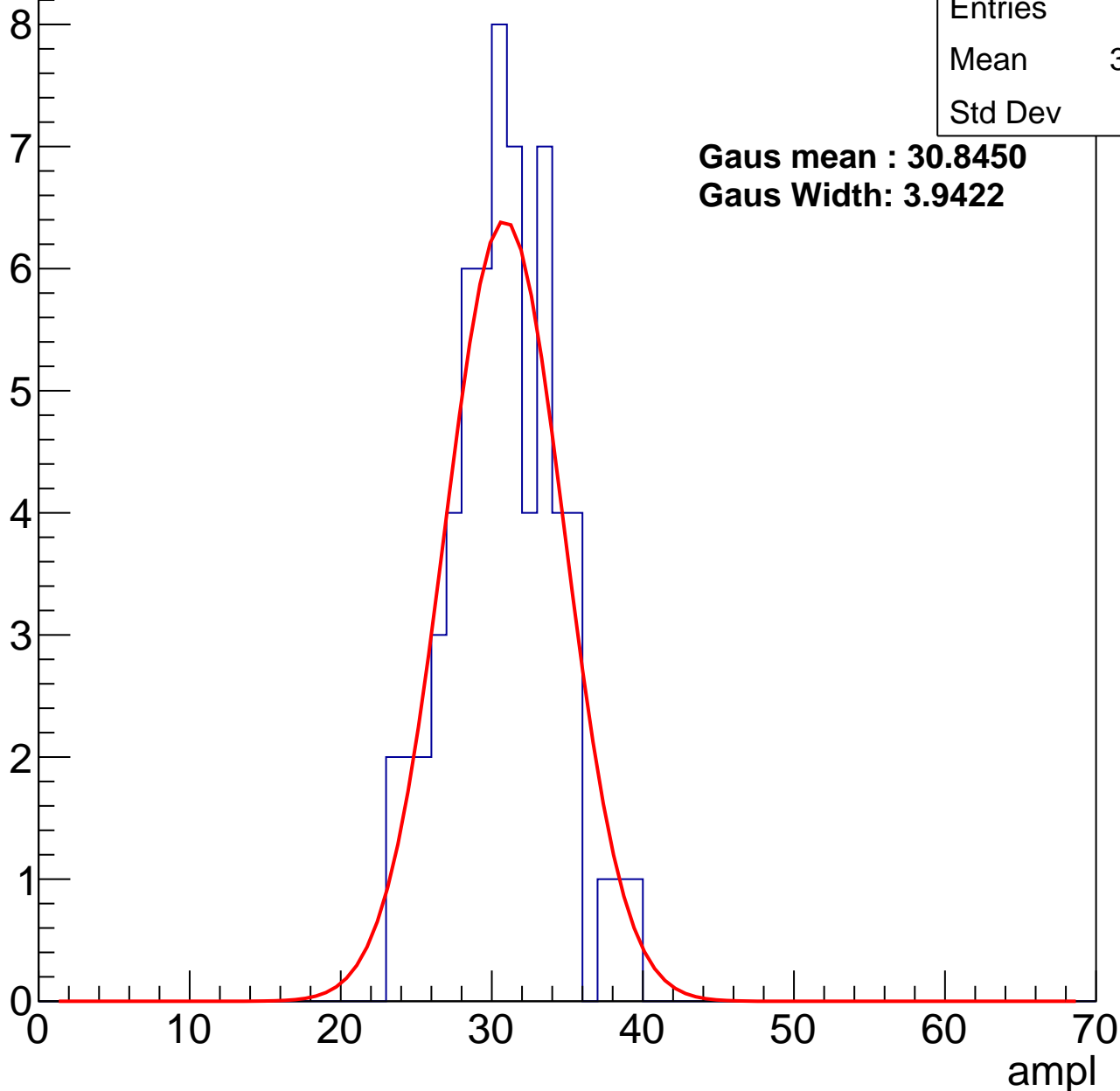
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	30.29
Std Dev	3.53

Gaus mean : 30.8450

Gaus Width: 3.9422



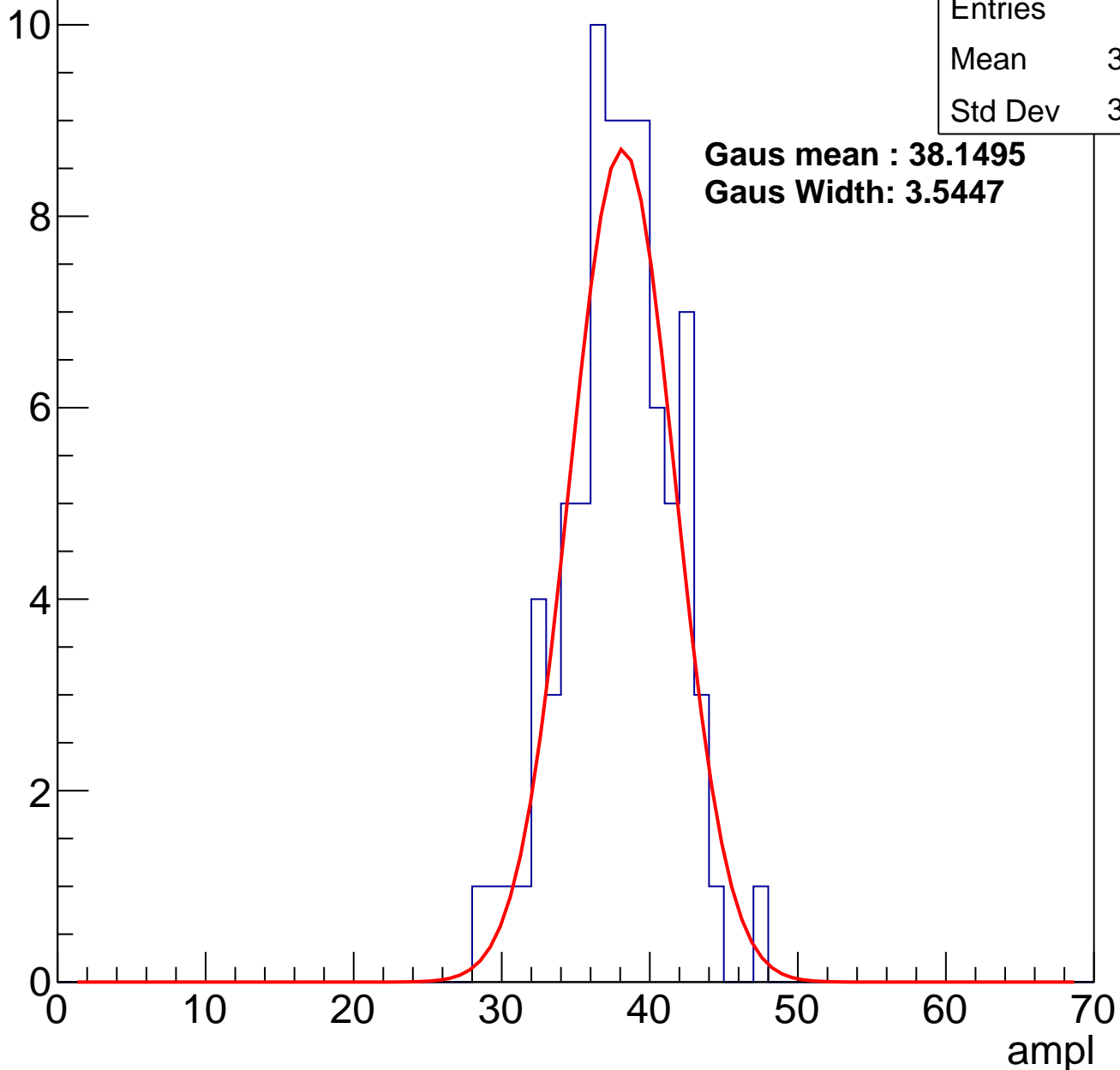
B1L101S, U25-ch40, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	37.47
Std Dev	3.594

Gaus mean : 38.1495
Gaus Width: 3.5447

Entry



B1L101S, U25-ch40, adc2

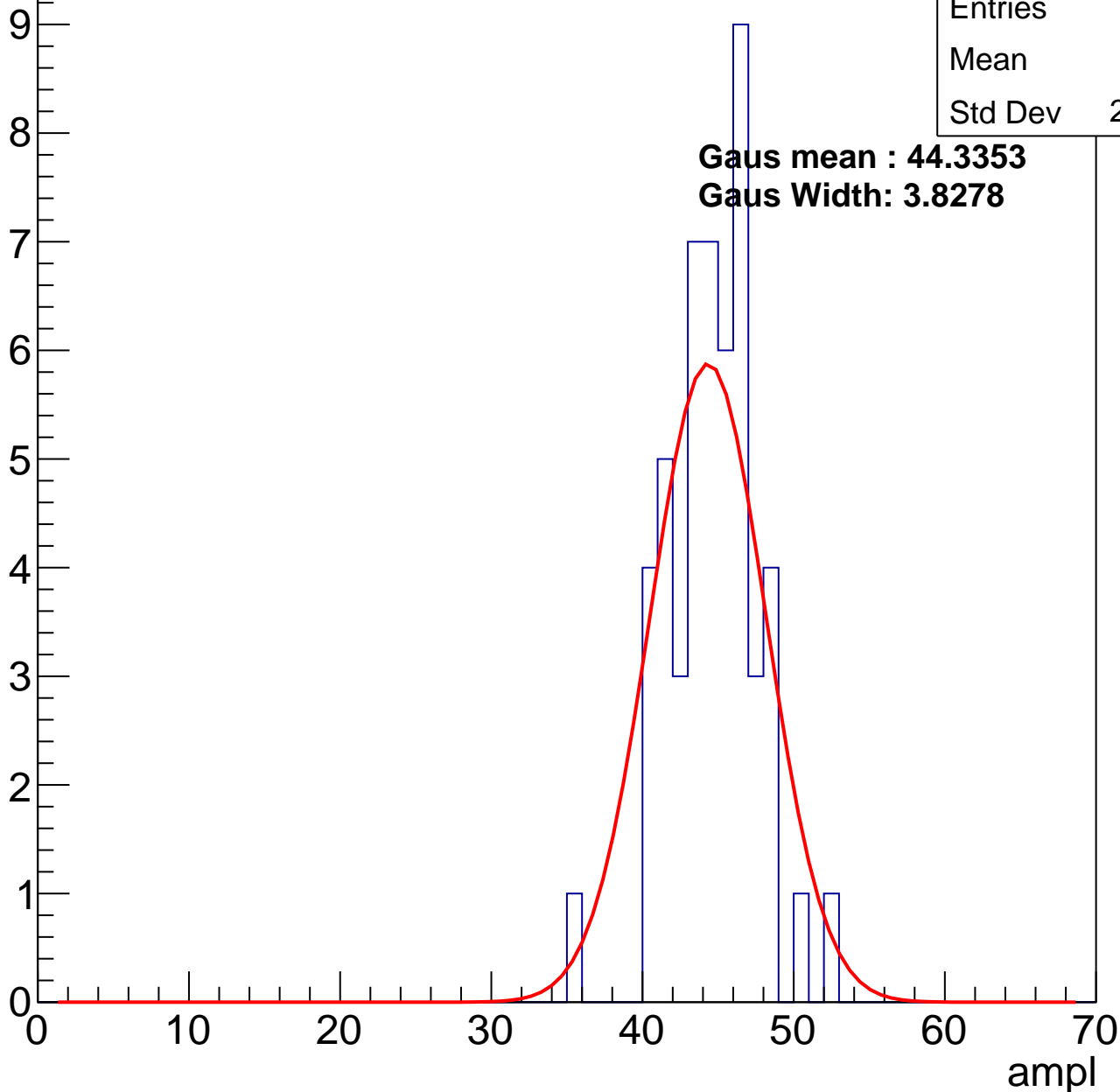
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	44.2
Std Dev	2.937

Gaus mean : 44.3353

Gaus Width: 3.8278



B1L101S, U25-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

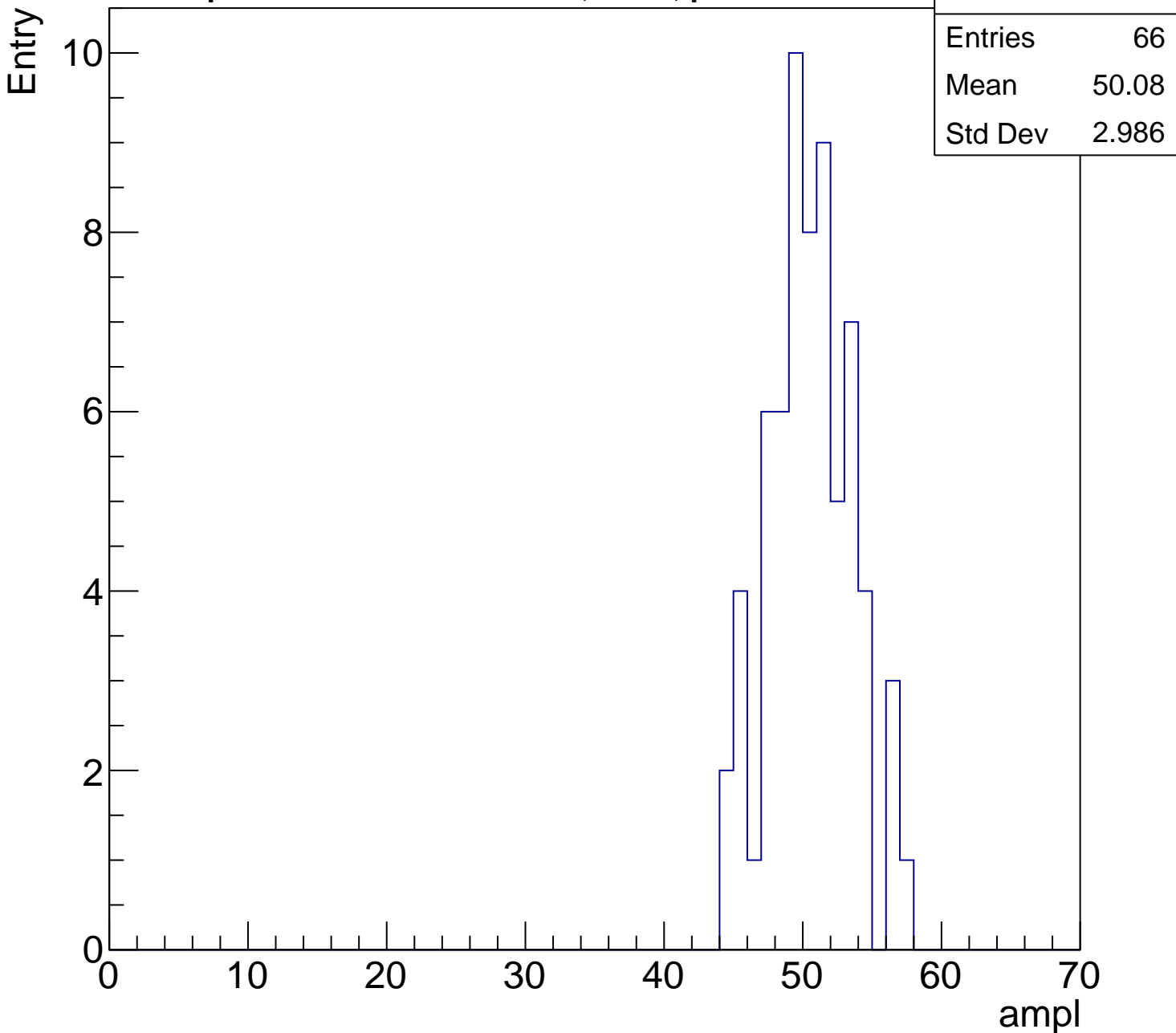
Entries	66
Mean	50.08
Std Dev	2.986

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

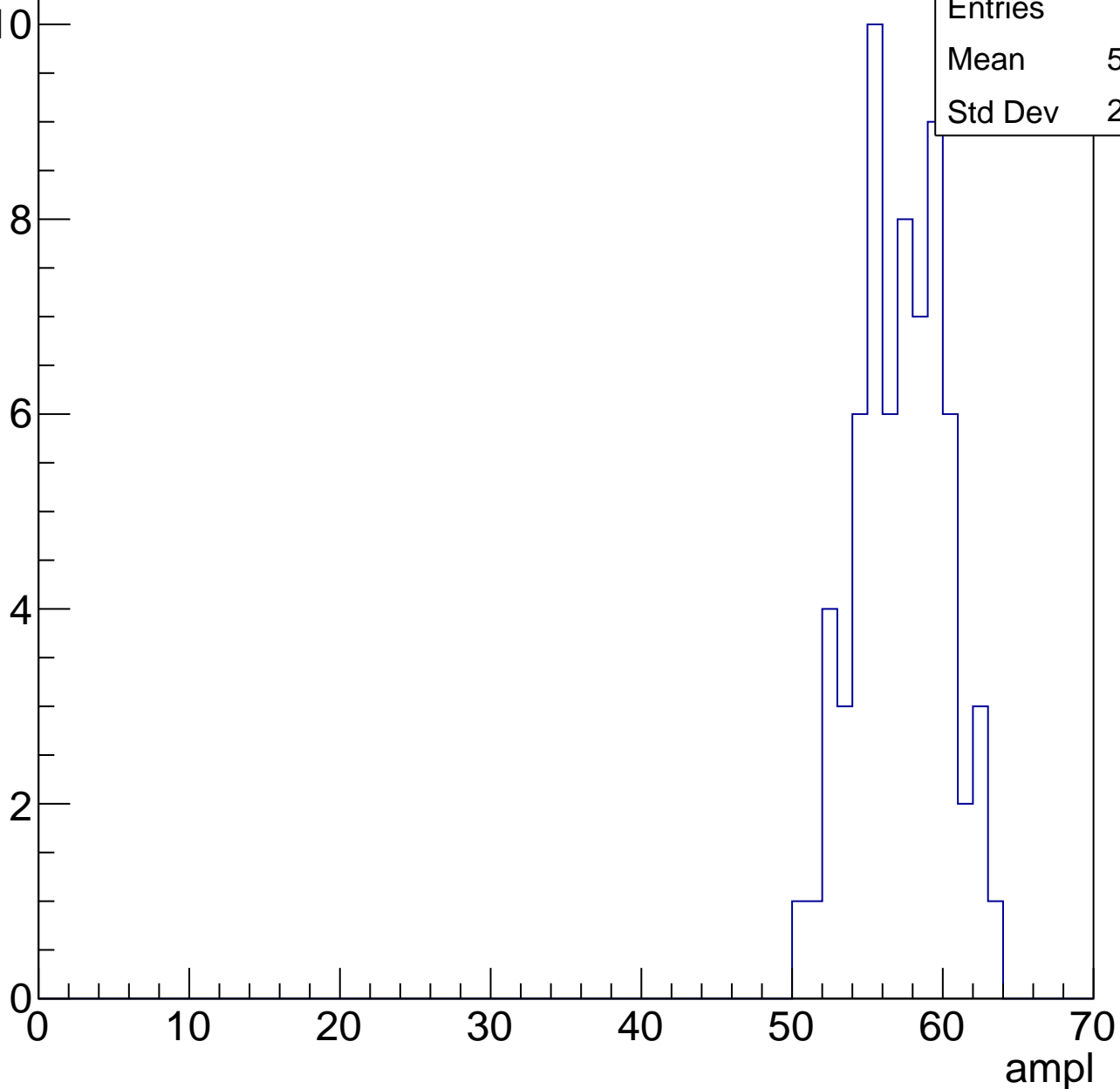


B1L101S, U25-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

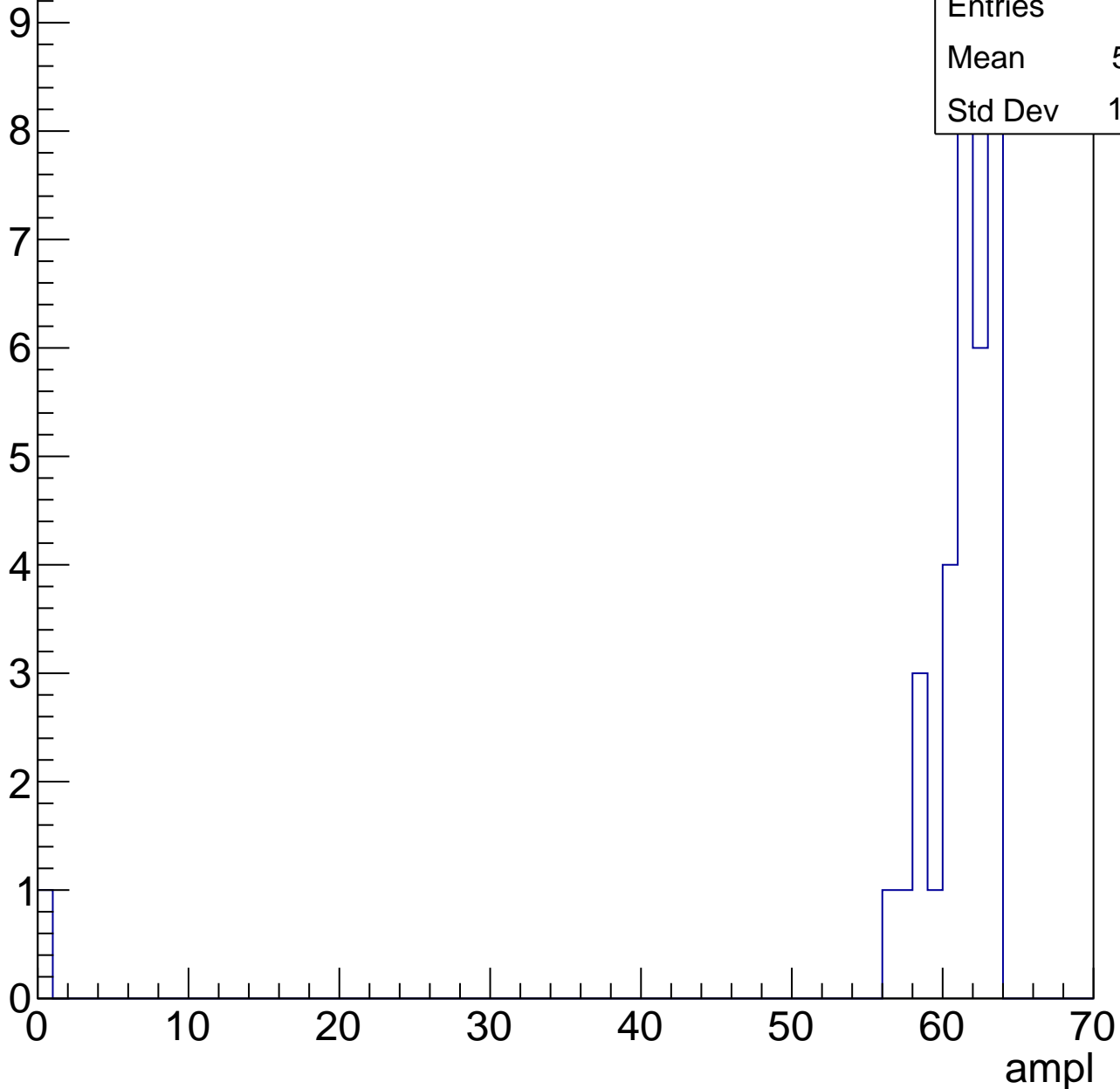
Entries	67
Mean	56.75
Std Dev	2.913



B1L101S, U25-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch41, adc0

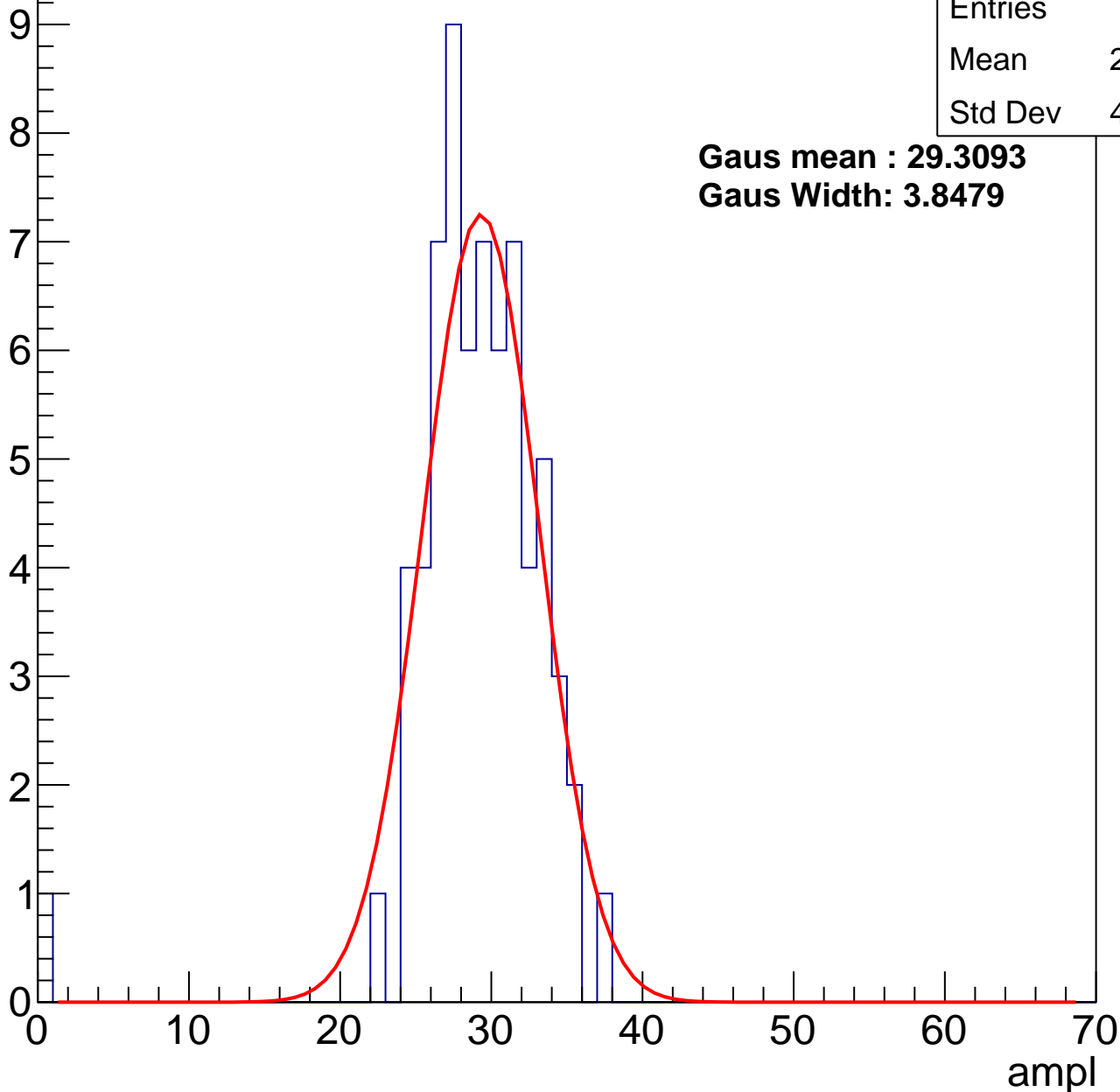
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.55
Std Dev	4.742

Gaus mean : 29.3093

Gaus Width: 3.8479



B1L101S, U25-ch41, adc1

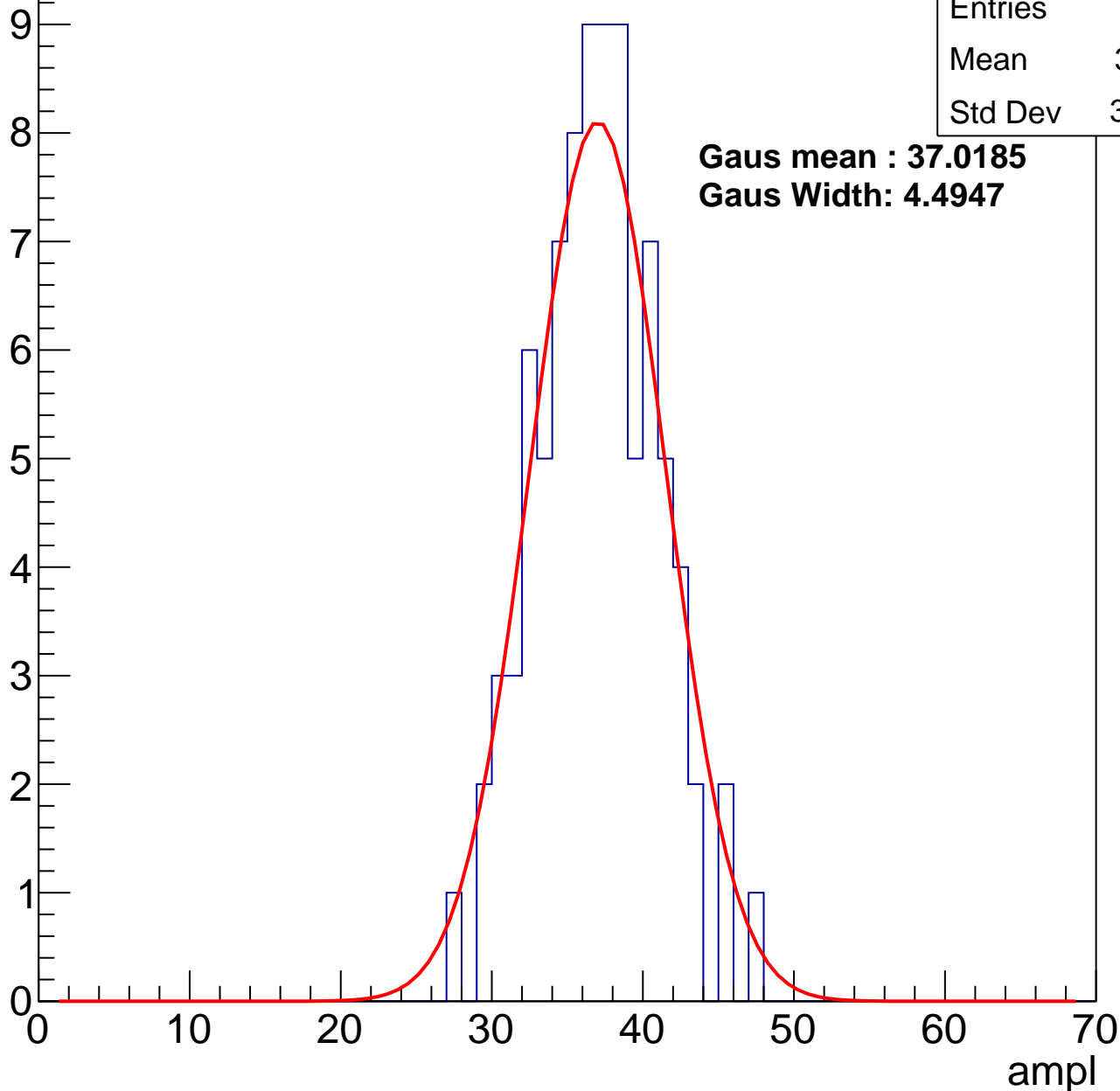
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	36.51
Std Dev	3.943

Gaus mean : 37.0185

Gaus Width: 4.4947



B1L101S, U25-ch41, adc2

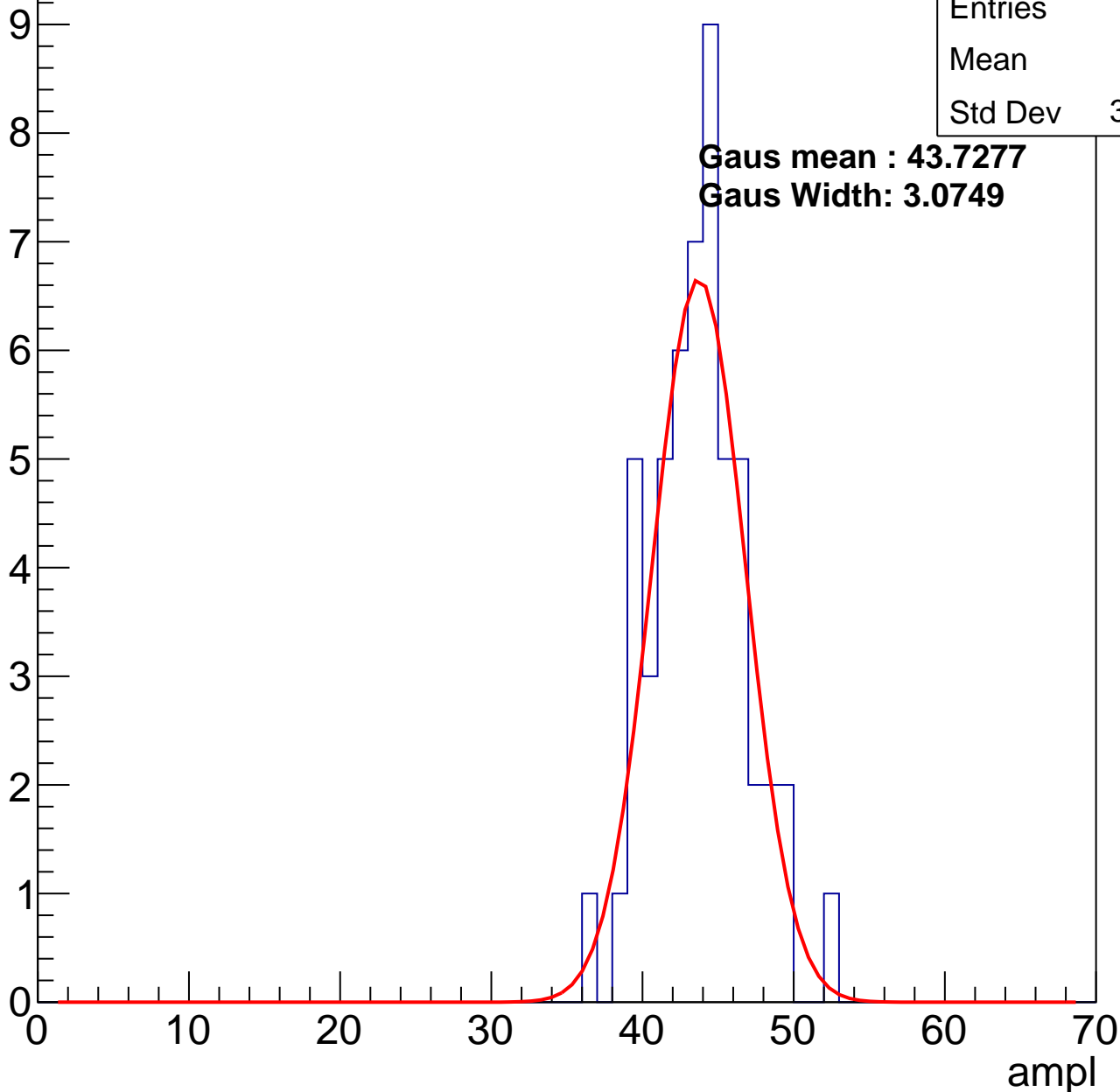
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.3
Std Dev	3.077

Gaus mean : 43.7277

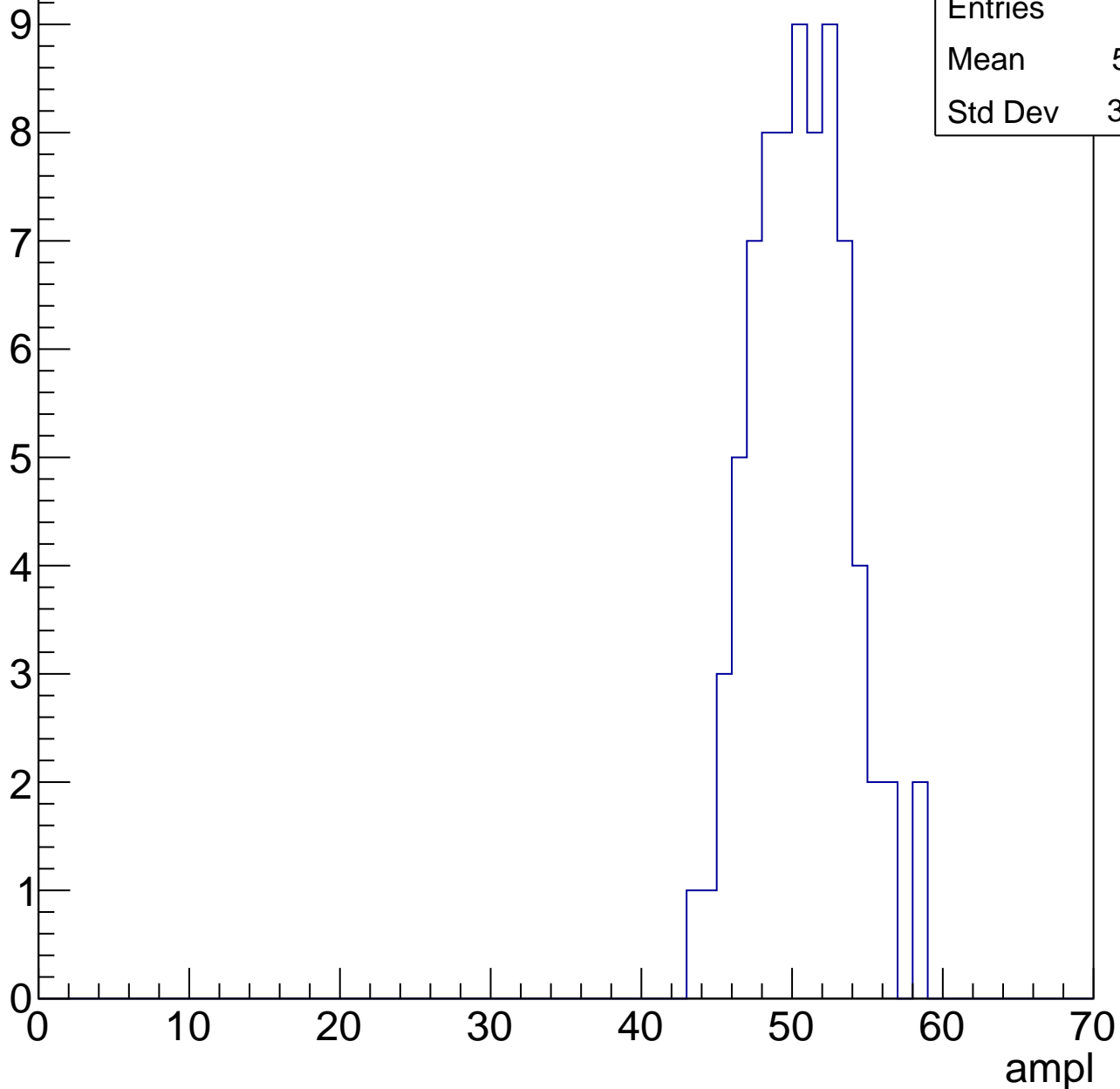
Gaus Width: 3.0749



B1L101S, U25-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

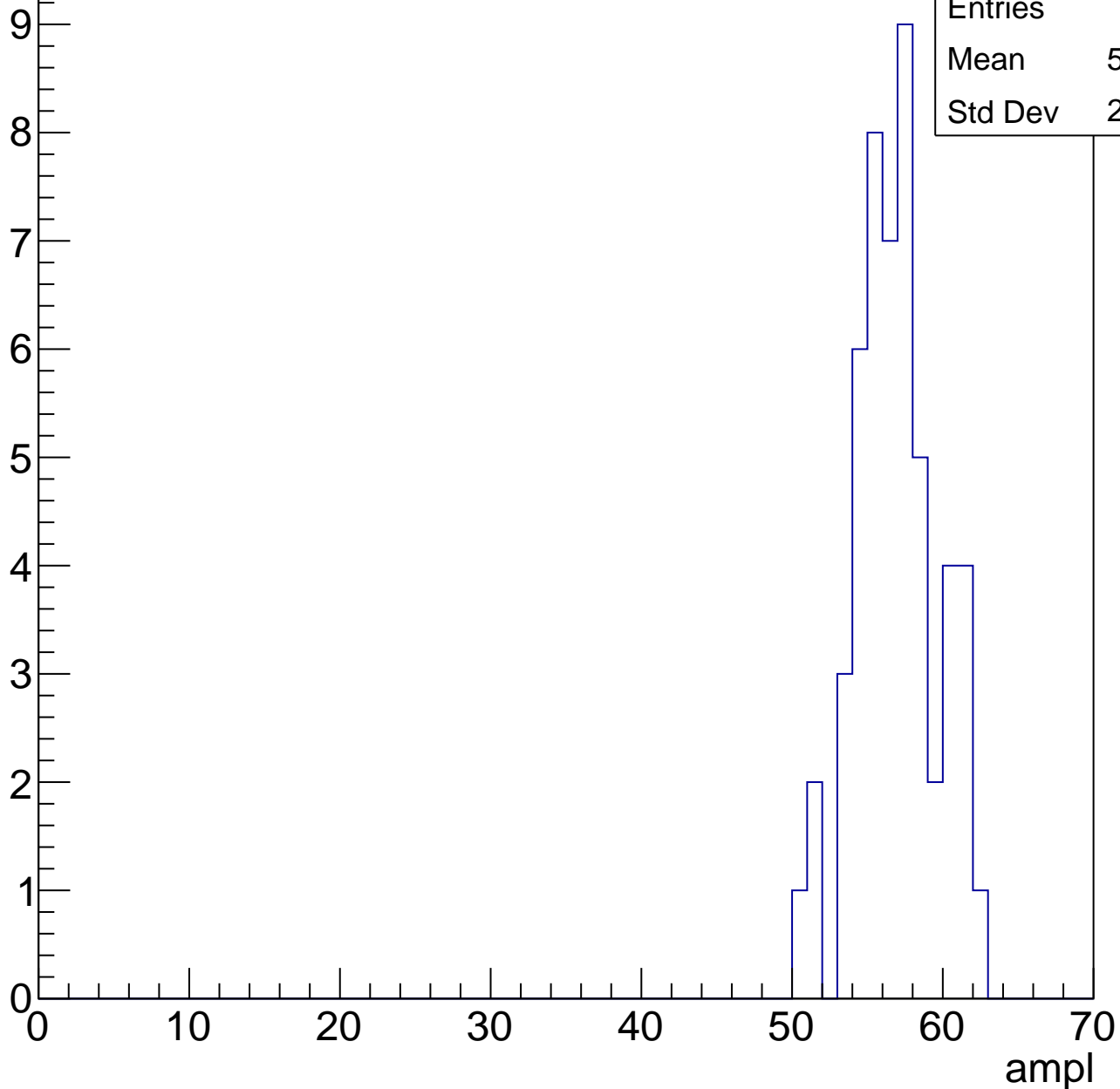


Entries	76
Mean	50.11
Std Dev	3.169

B1L101S, U25-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	52
Mean	56.42
Std Dev	2.713

B1L101S, U25-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

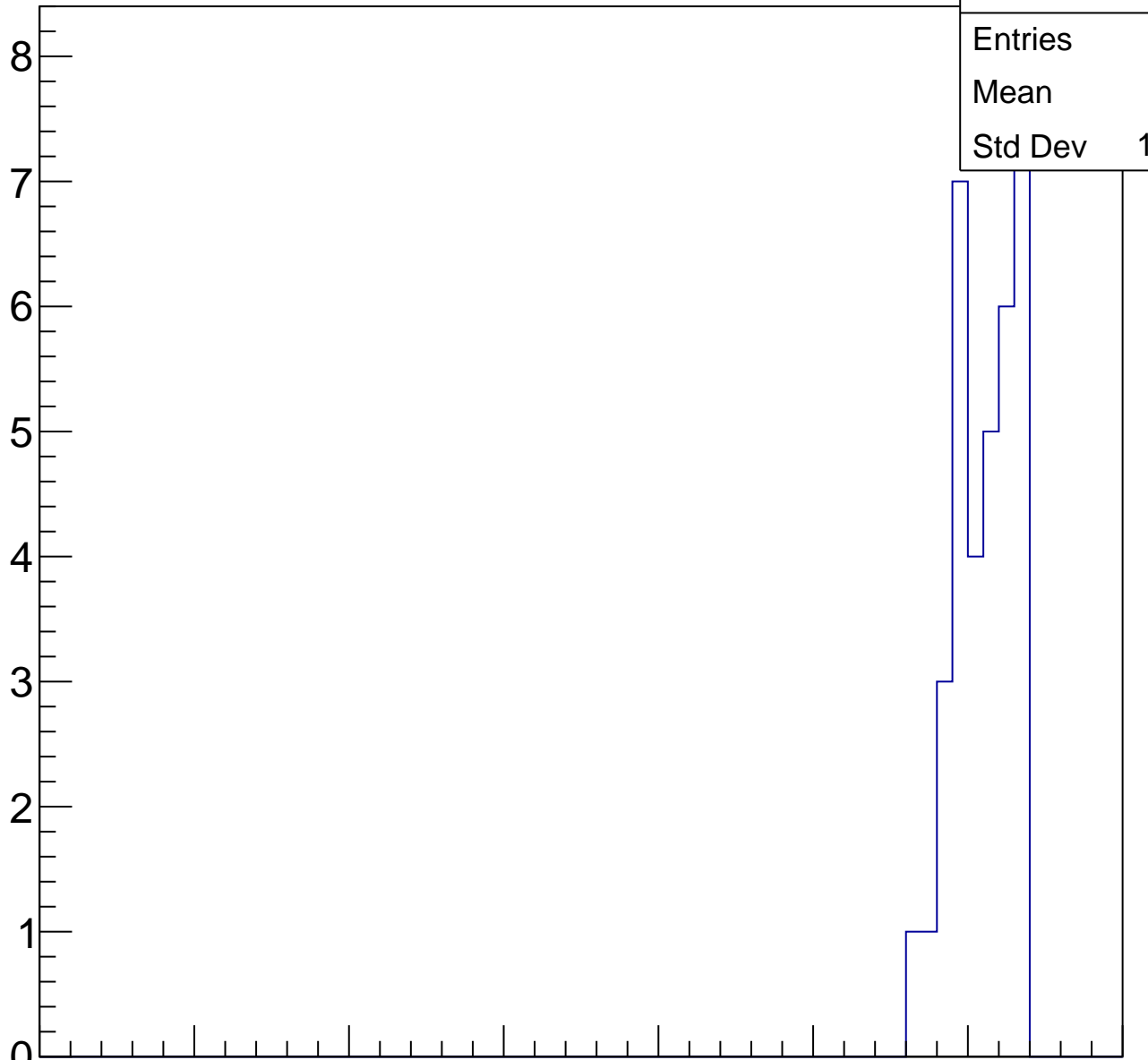
Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.6
Std Dev	1.945

ampl

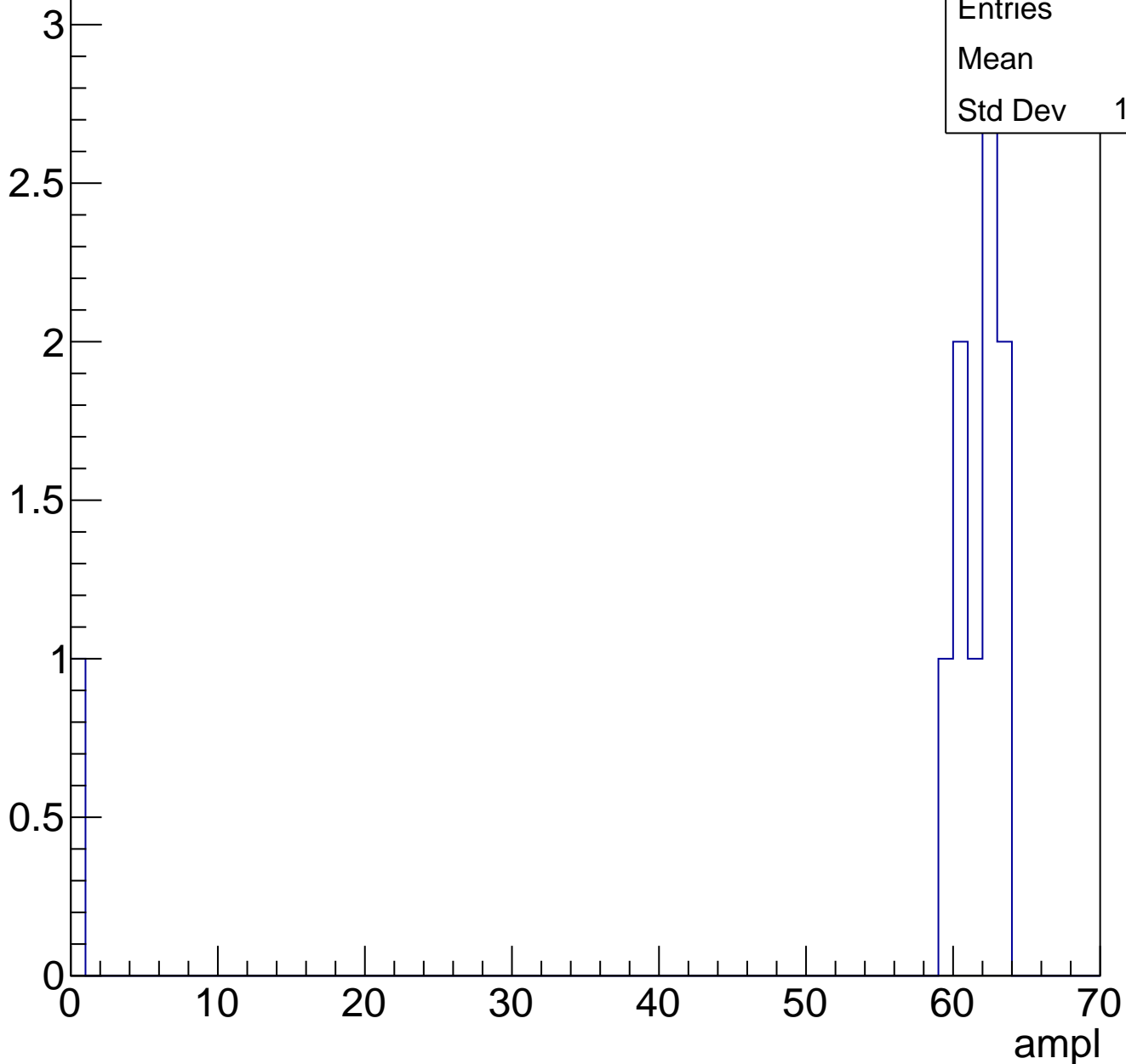
0 10 20 30 40 50 60 70



B1L101S, U25-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch42, adc0

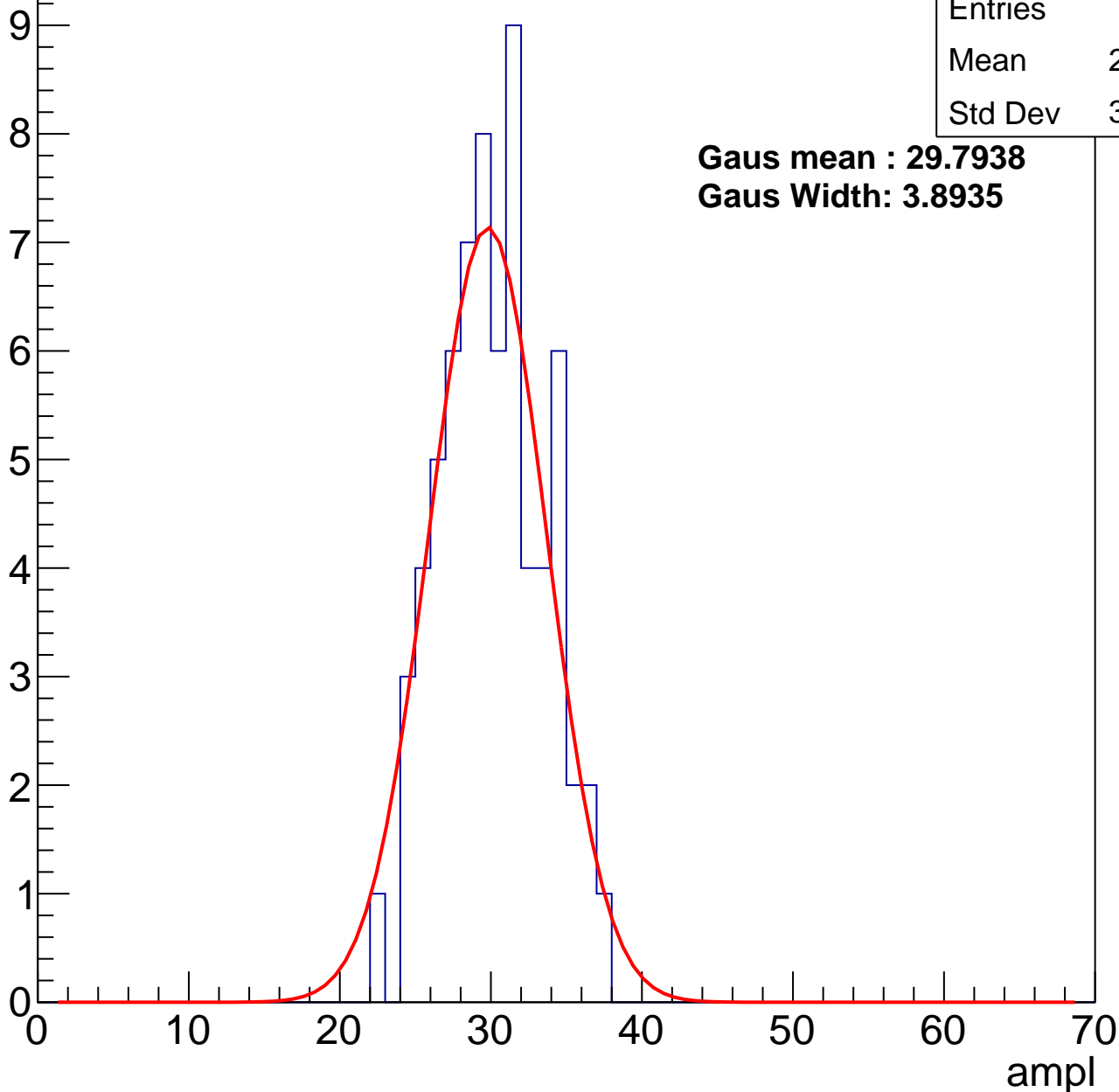
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.65
Std Dev	3.346

Gaus mean : 29.7938

Gaus Width: 3.8935



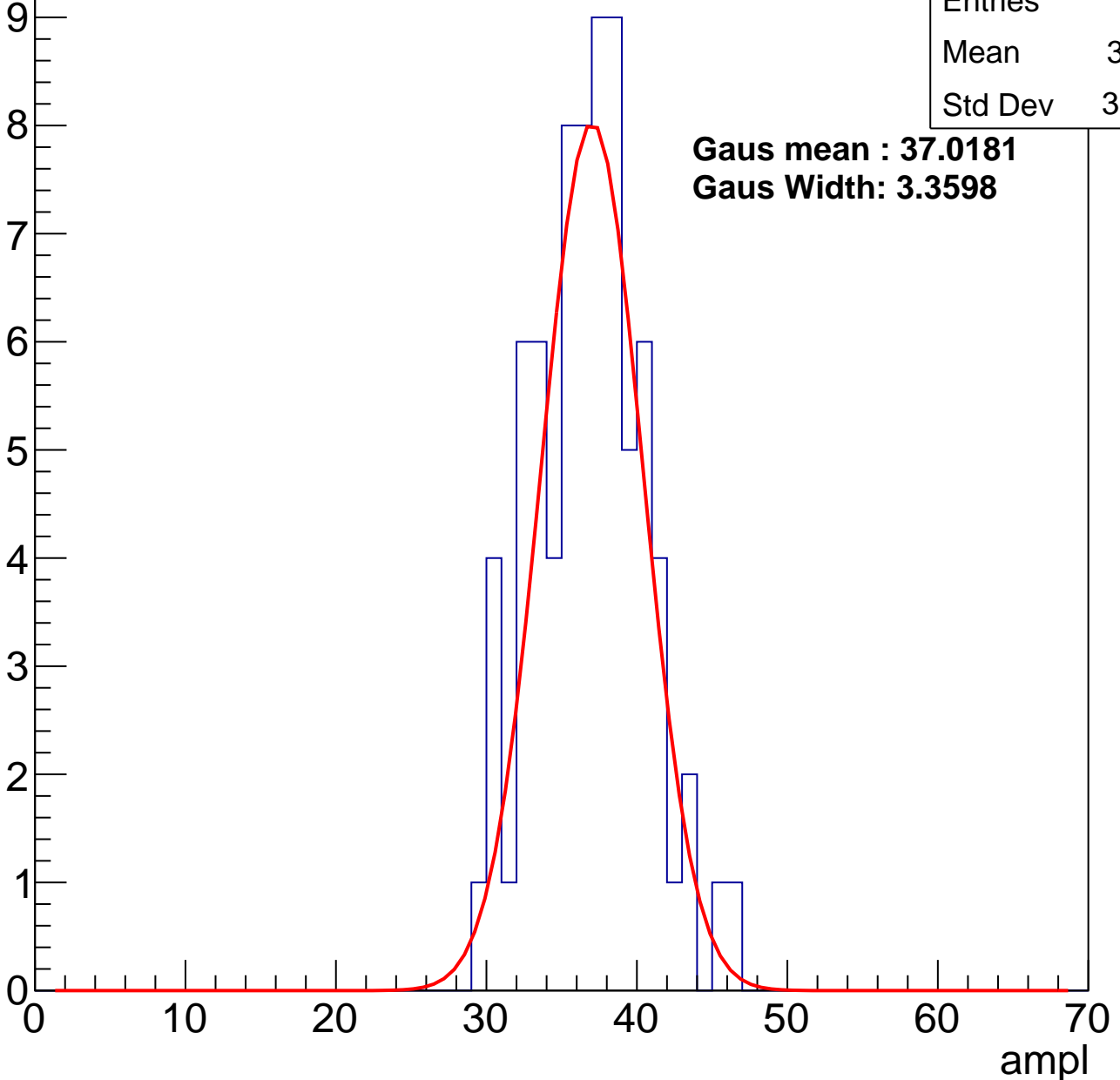
B1L101S, U25-ch42, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.41
Std Dev	3.592

Gaus mean : 37.0181
Gaus Width: 3.3598



B1L101S, U25-ch42, adc2

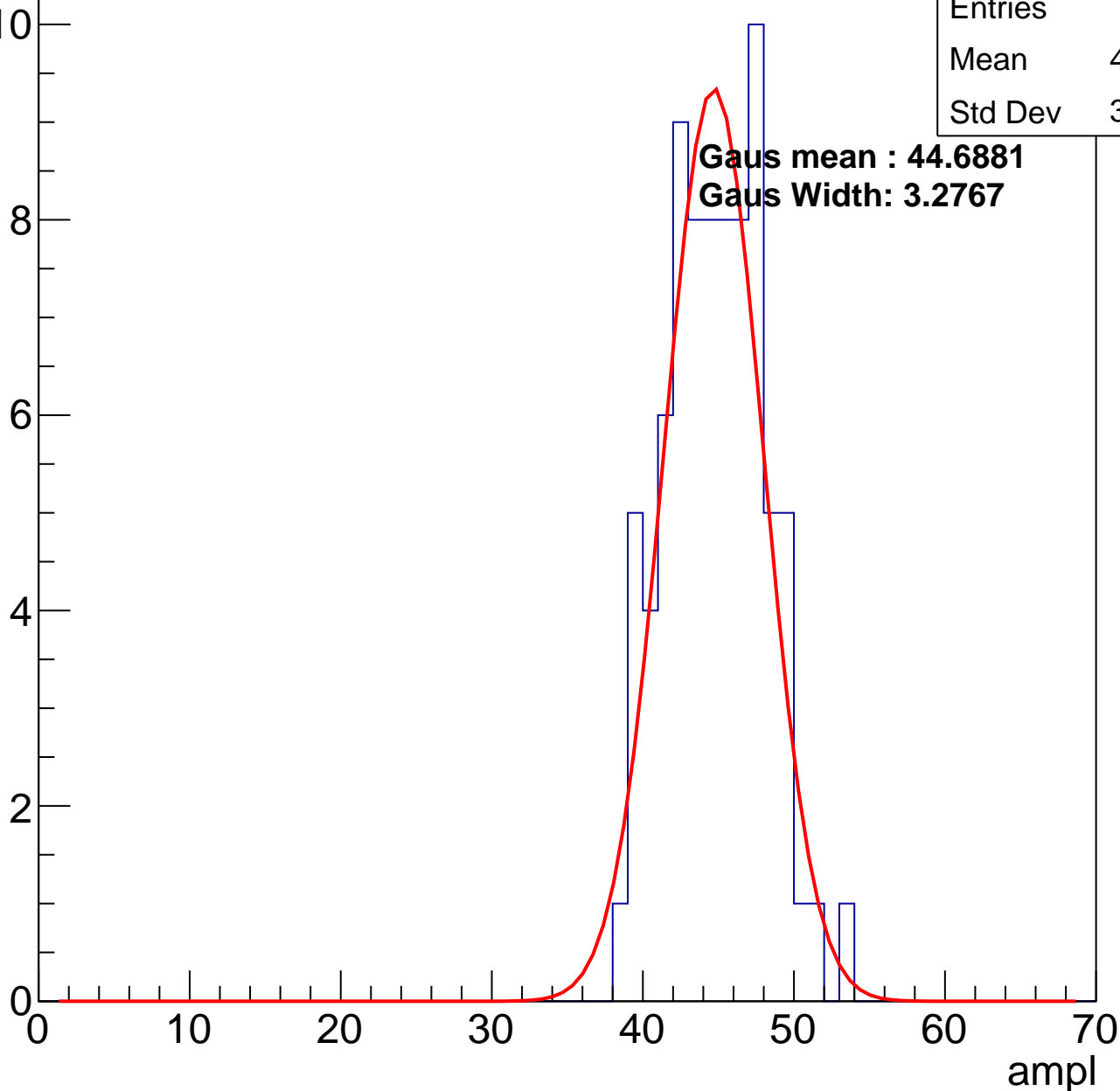
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	44.38
Std Dev	3.187

Gaus mean : 44.6881

Gaus Width: 3.2767

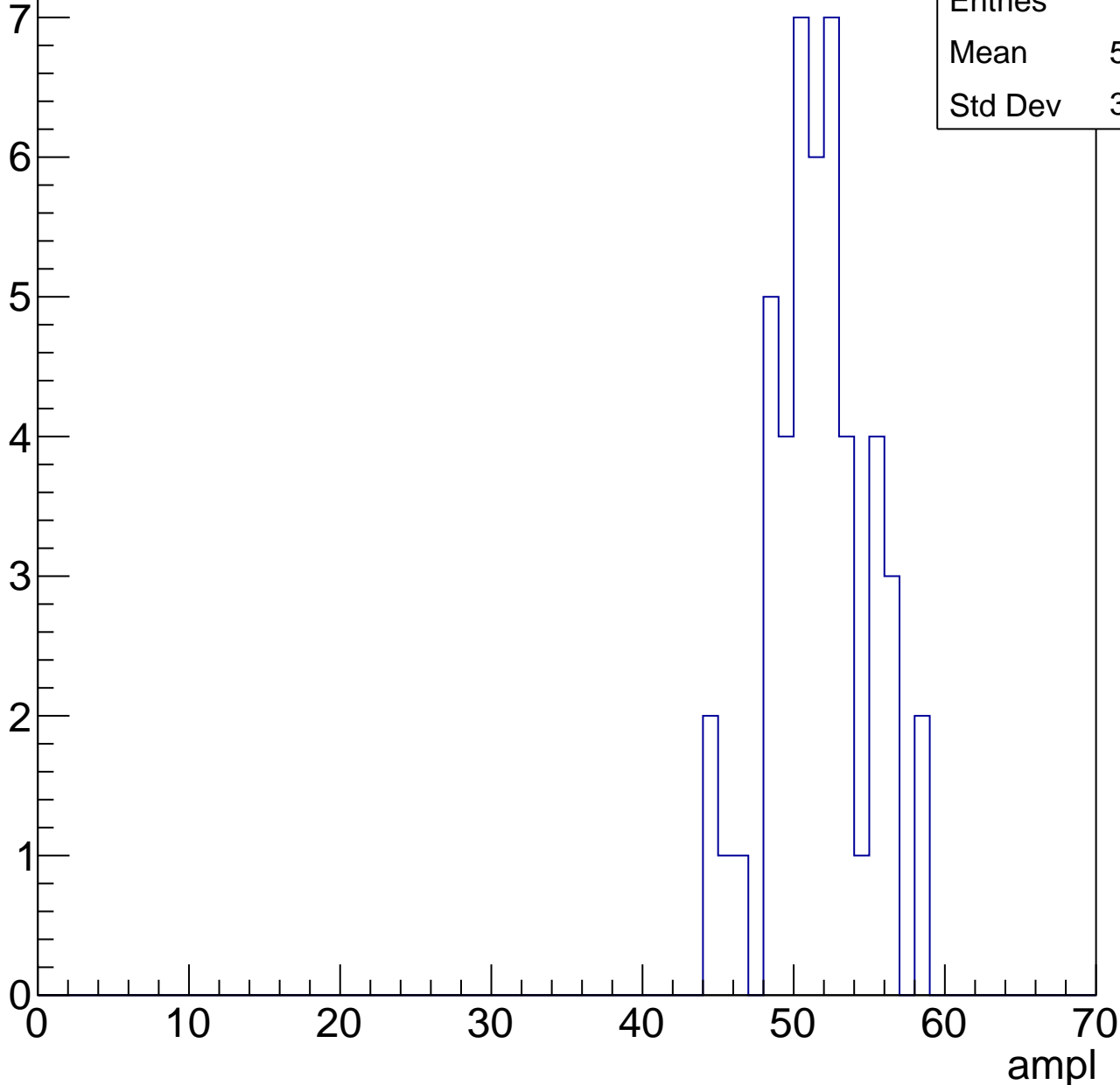


B1L101S, U25-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

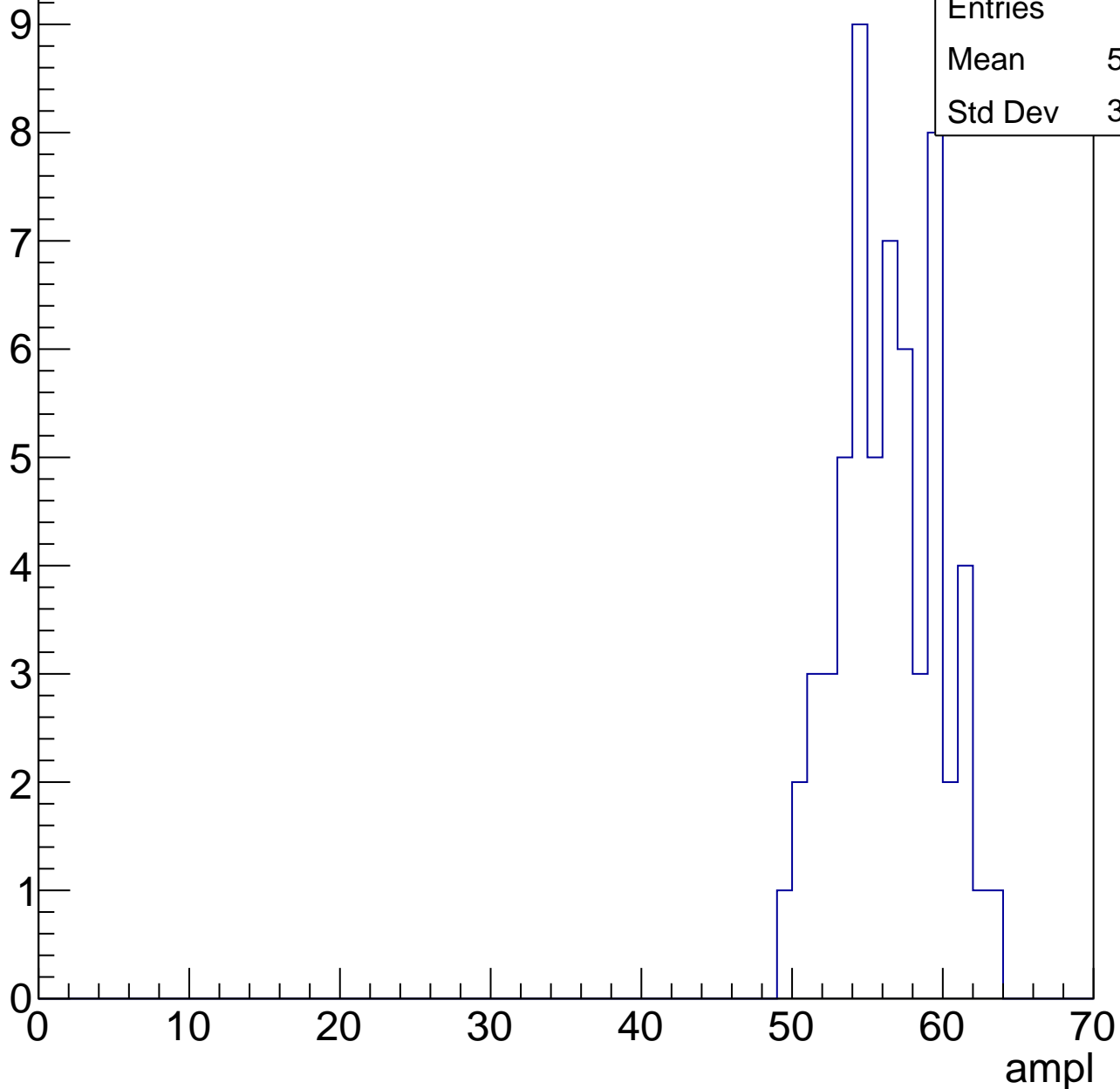
Entries	47
Mean	51.17
Std Dev	3.244



B1L101S, U25-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

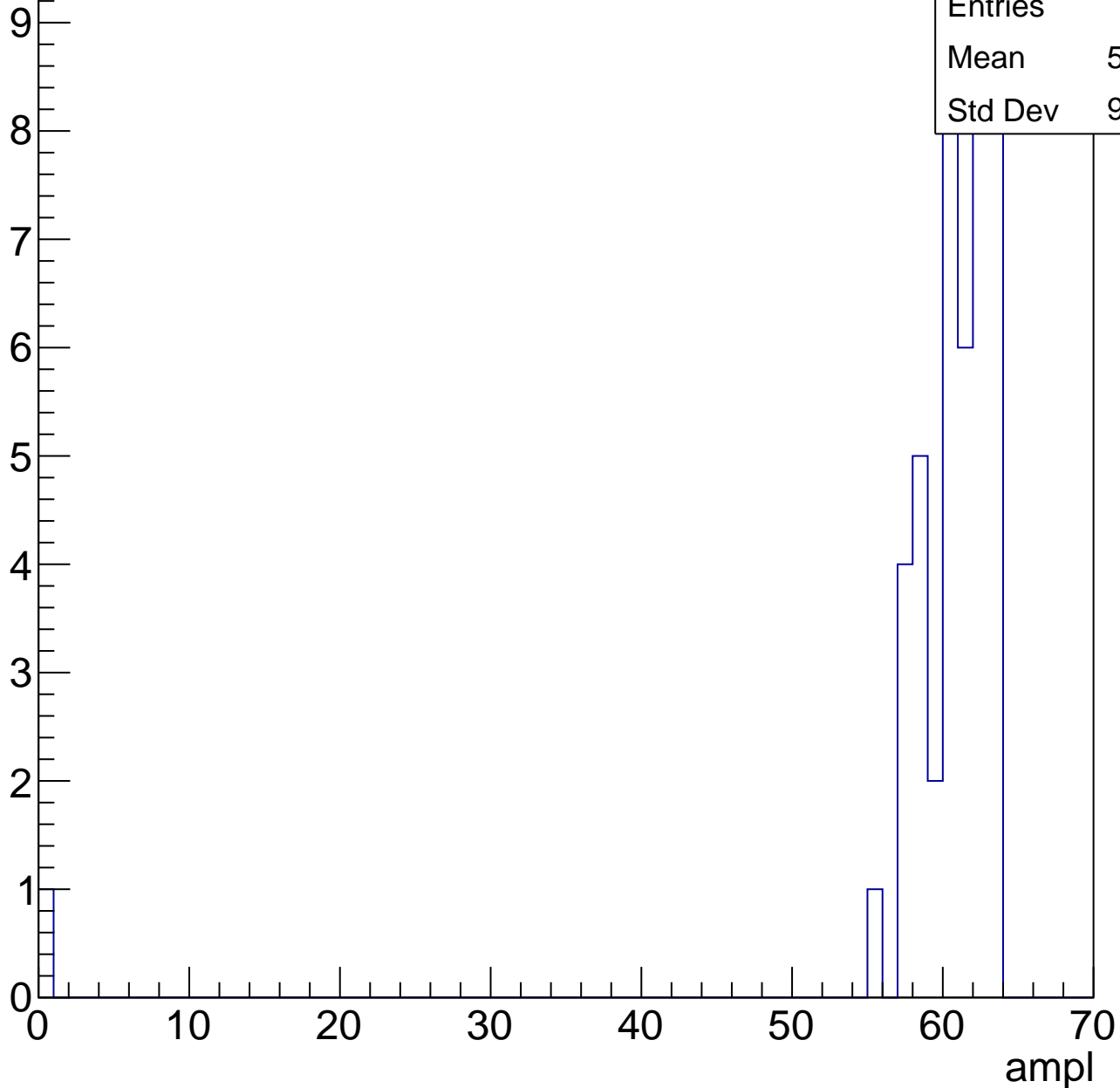


Entries	60
Mean	55.88
Std Dev	3.256

B1L101S, U25-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U25-ch43, adc0

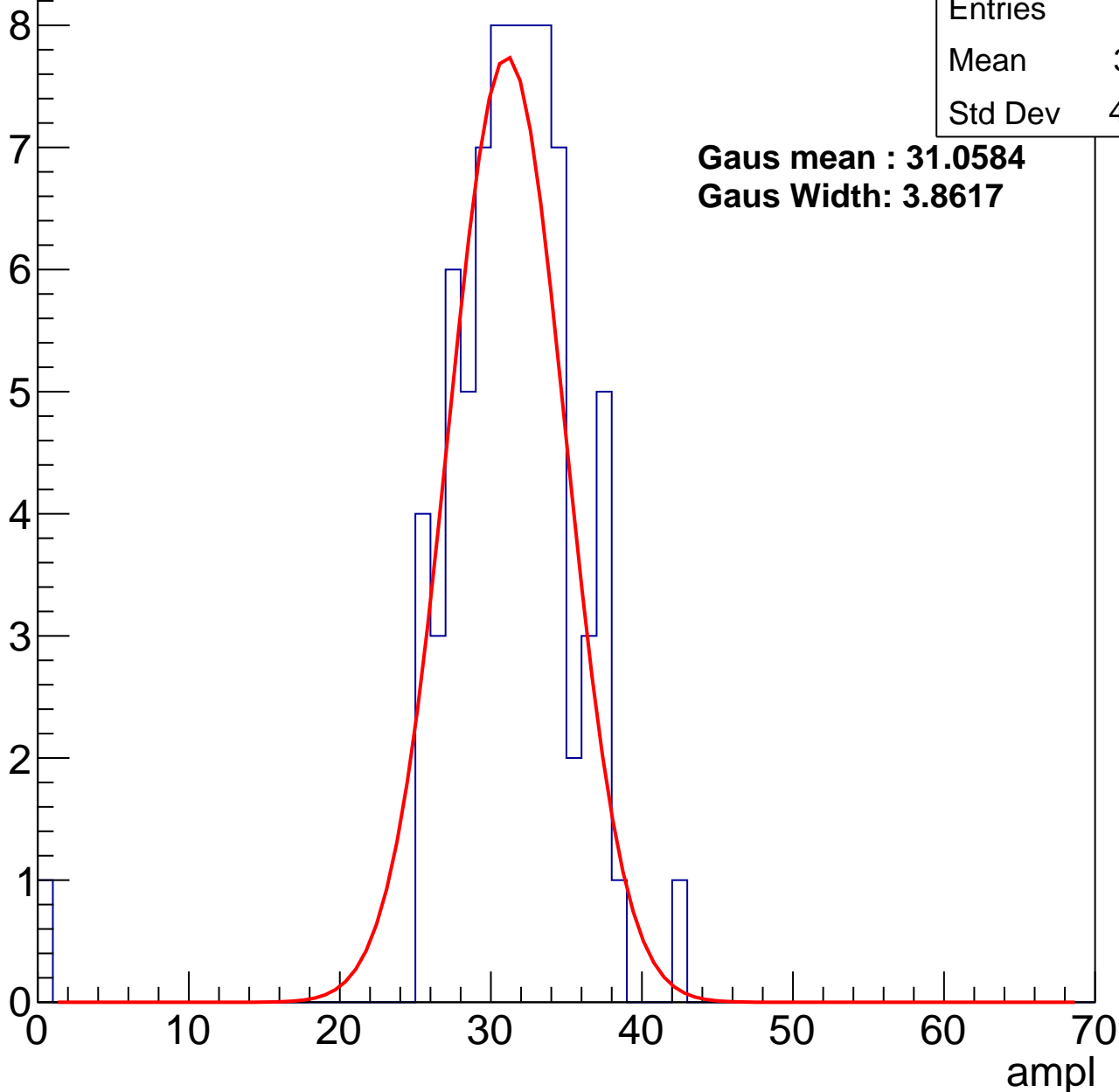
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	30.81
Std Dev	4.986

Gaus mean : 31.0584

Gaus Width: 3.8617



B1L101S, U25-ch43, adc1

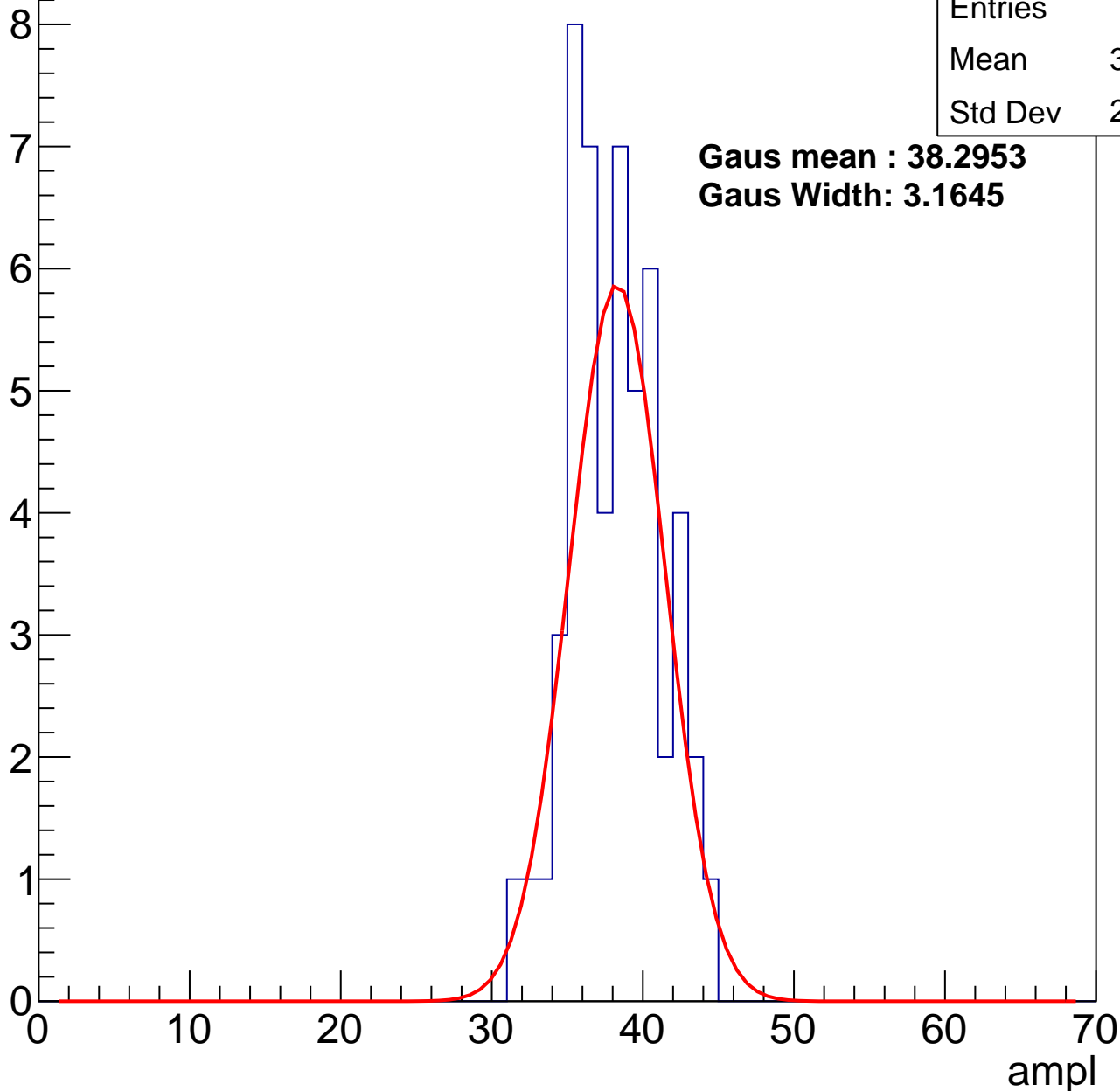
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	37.67
Std Dev	2.953

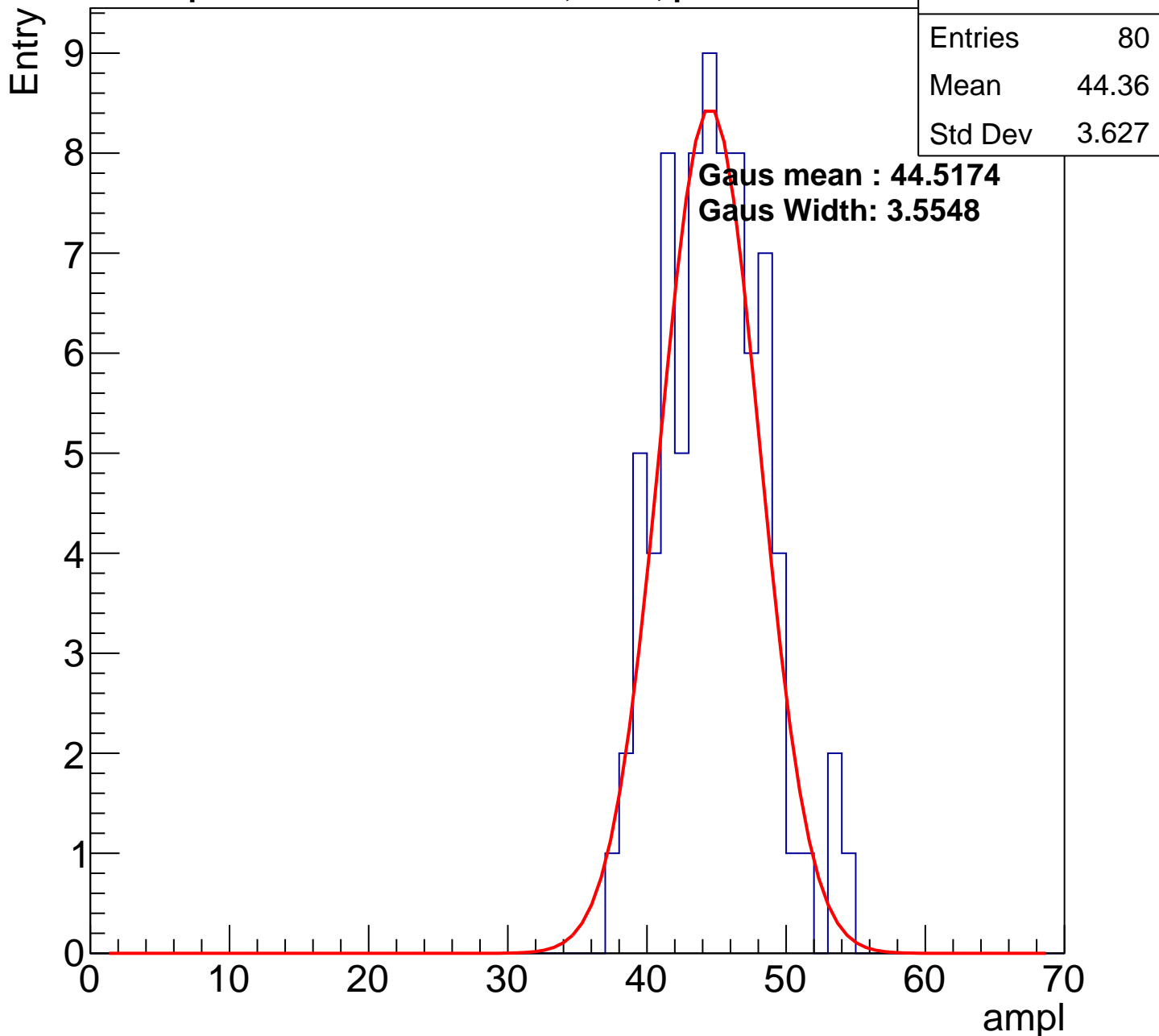
Gaus mean : 38.2953

Gaus Width: 3.1645



B1L101S, U25-ch43, adc2

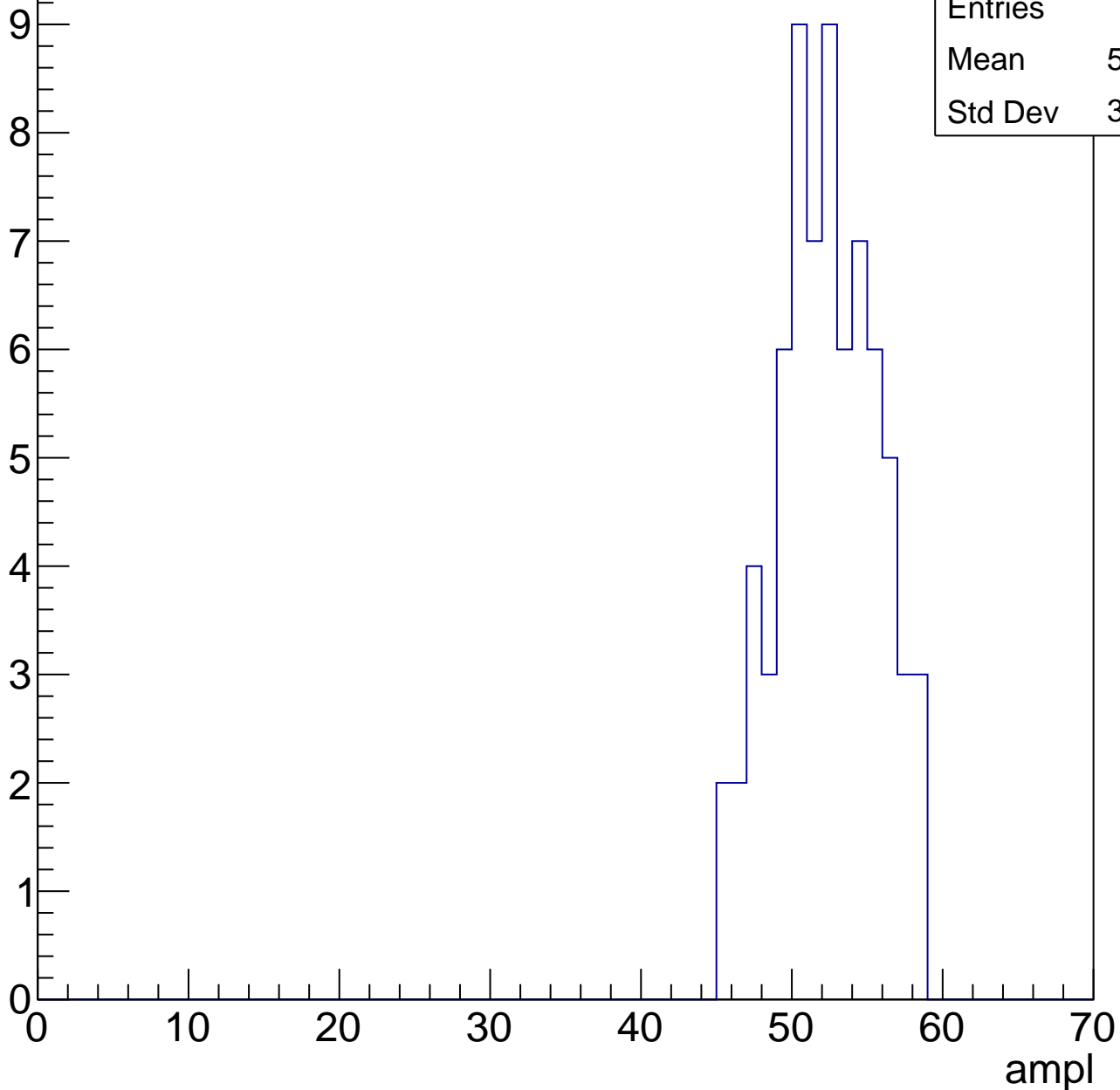
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

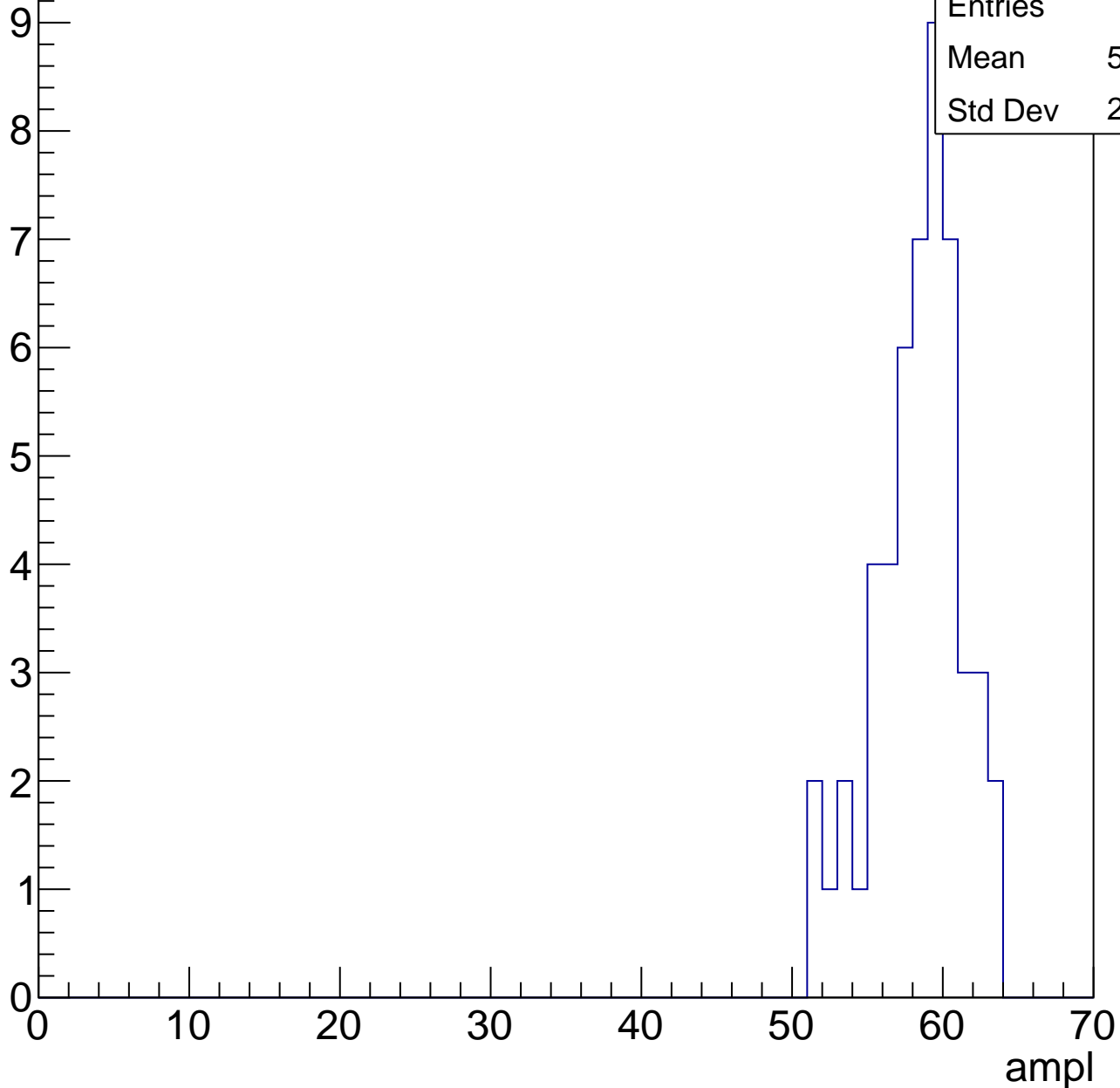
Entry



B1L101S, U25-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

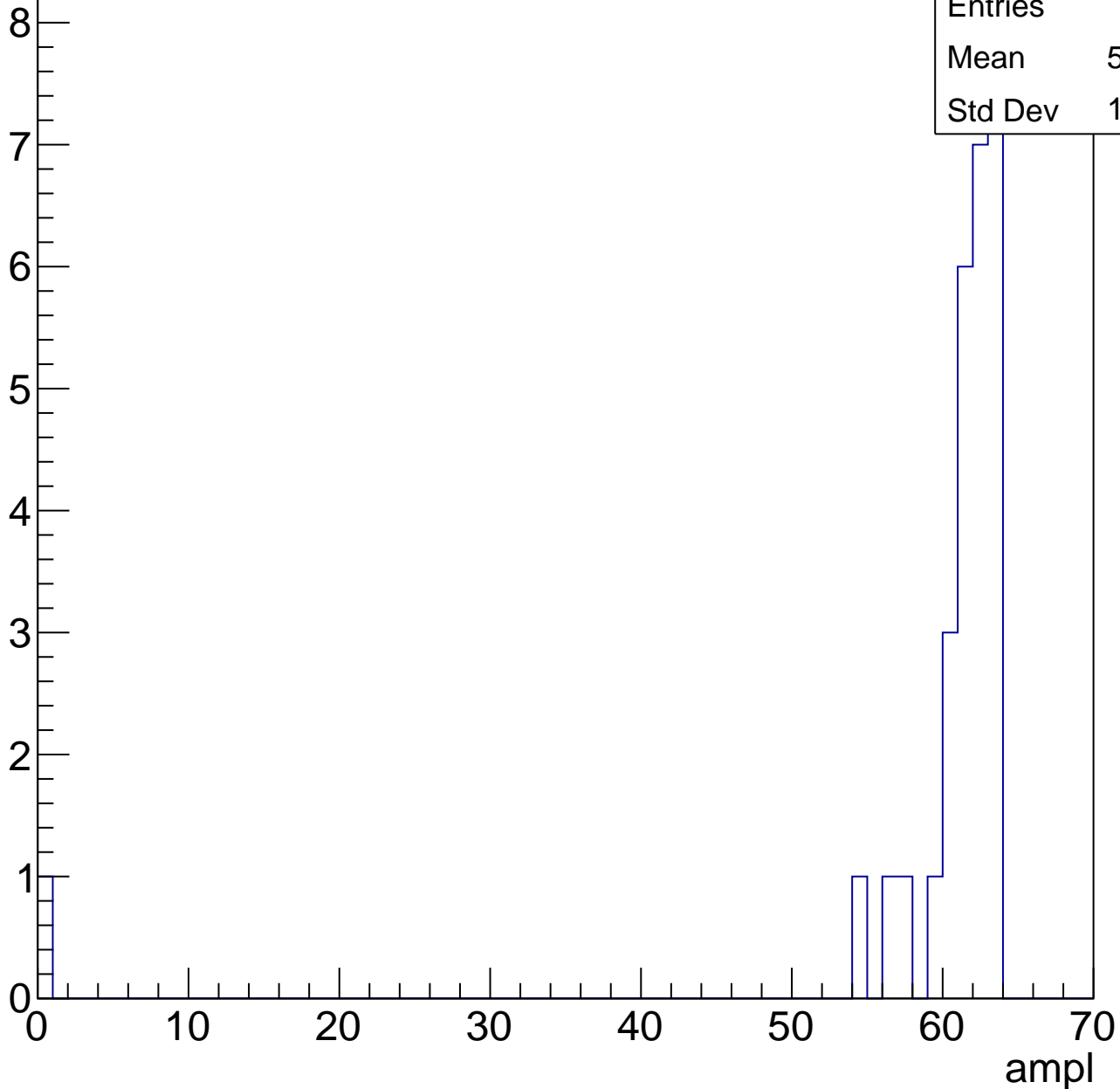


B1L101S, U25-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	58.97
Std Dev	11.35



B1L101S, U25-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch44, adc0

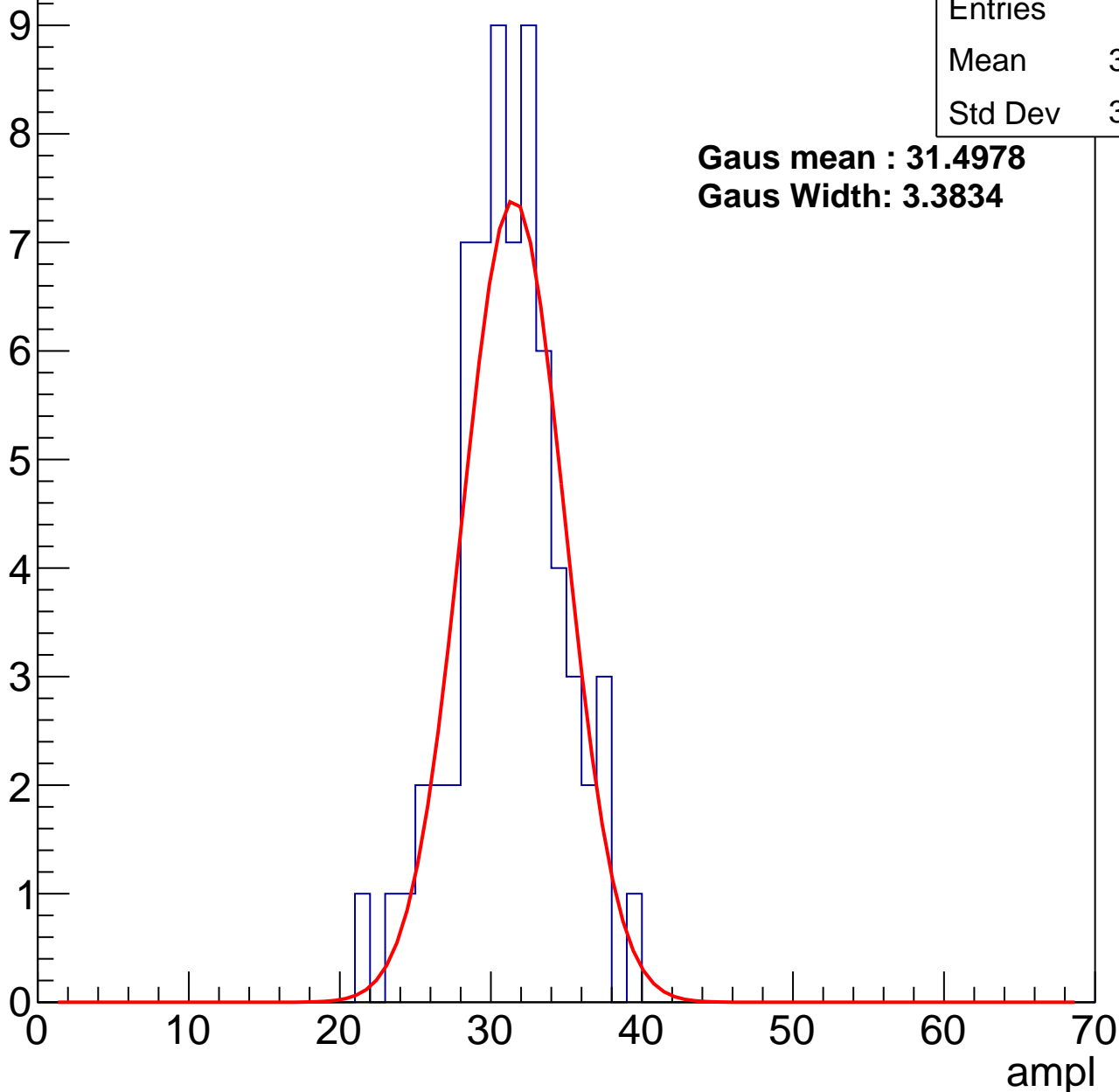
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.73
Std Dev	3.458

Gaus mean : 31.4978

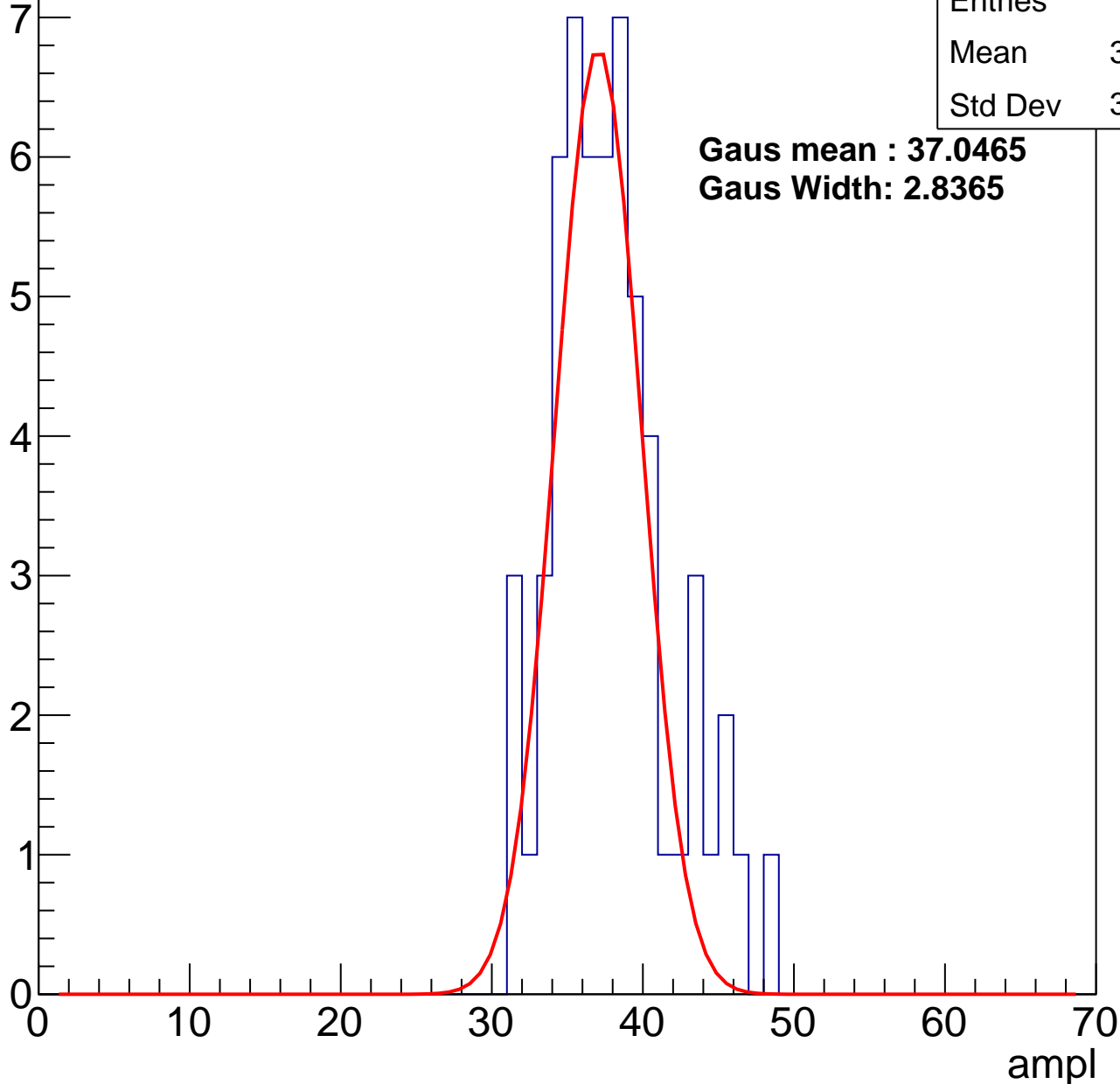
Gaus Width: 3.3834



B1L101S, U25-ch44, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch44, adc2

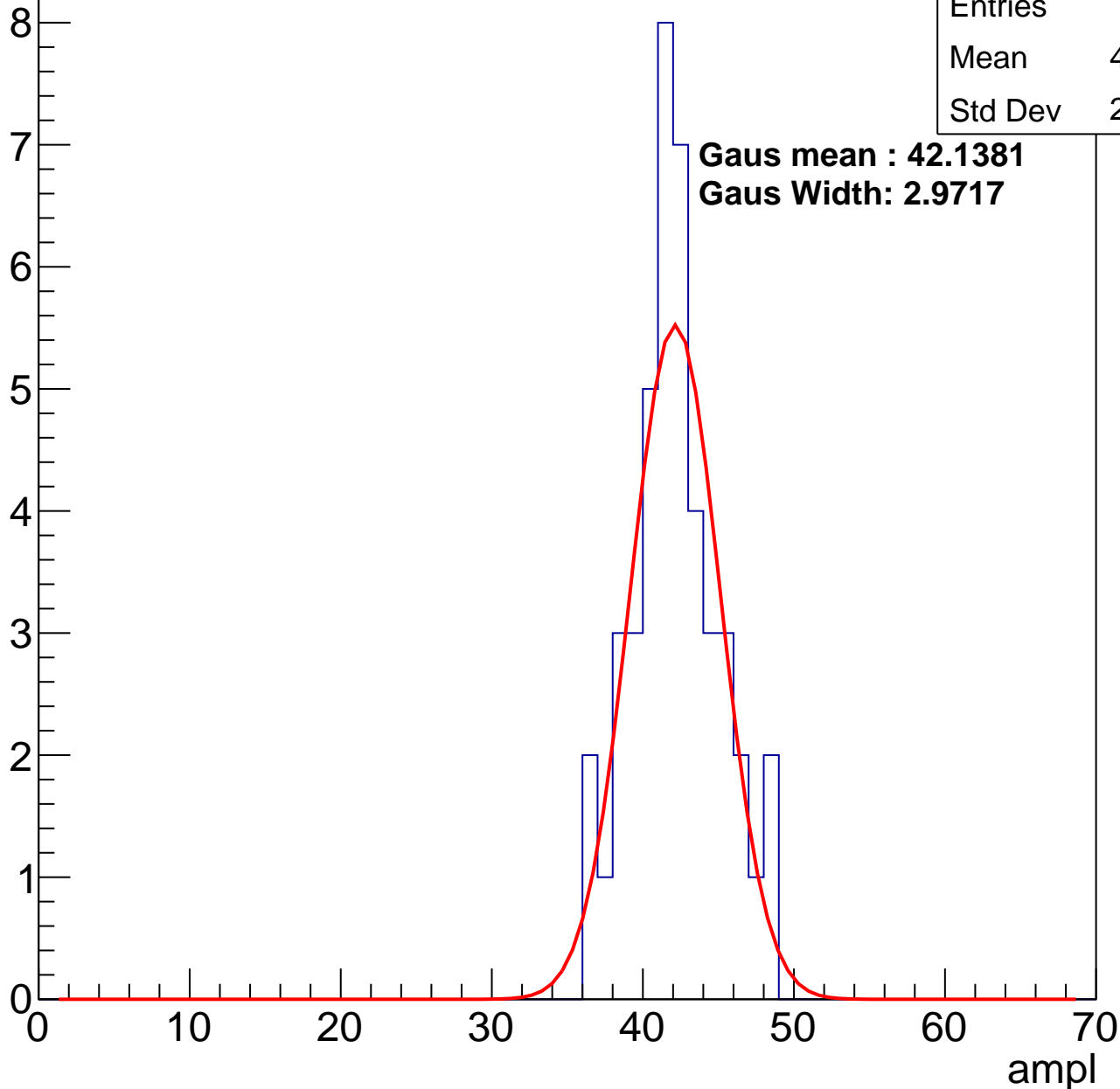
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	41.73
Std Dev	2.895

Gaus mean : 42.1381

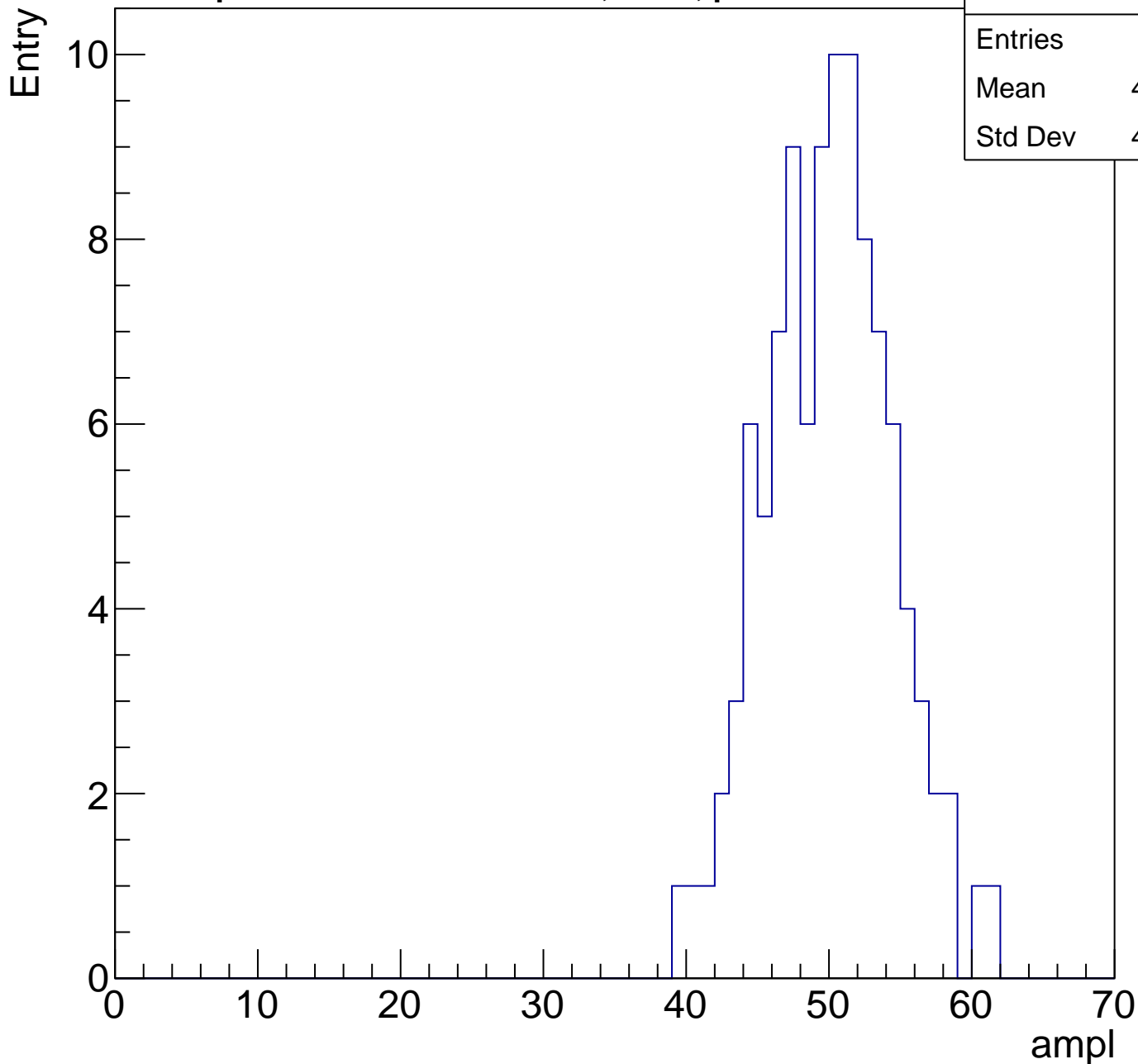
Gaus Width: 2.9717



B1L101S, U25-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	104
Mean	49.58
Std Dev	4.365

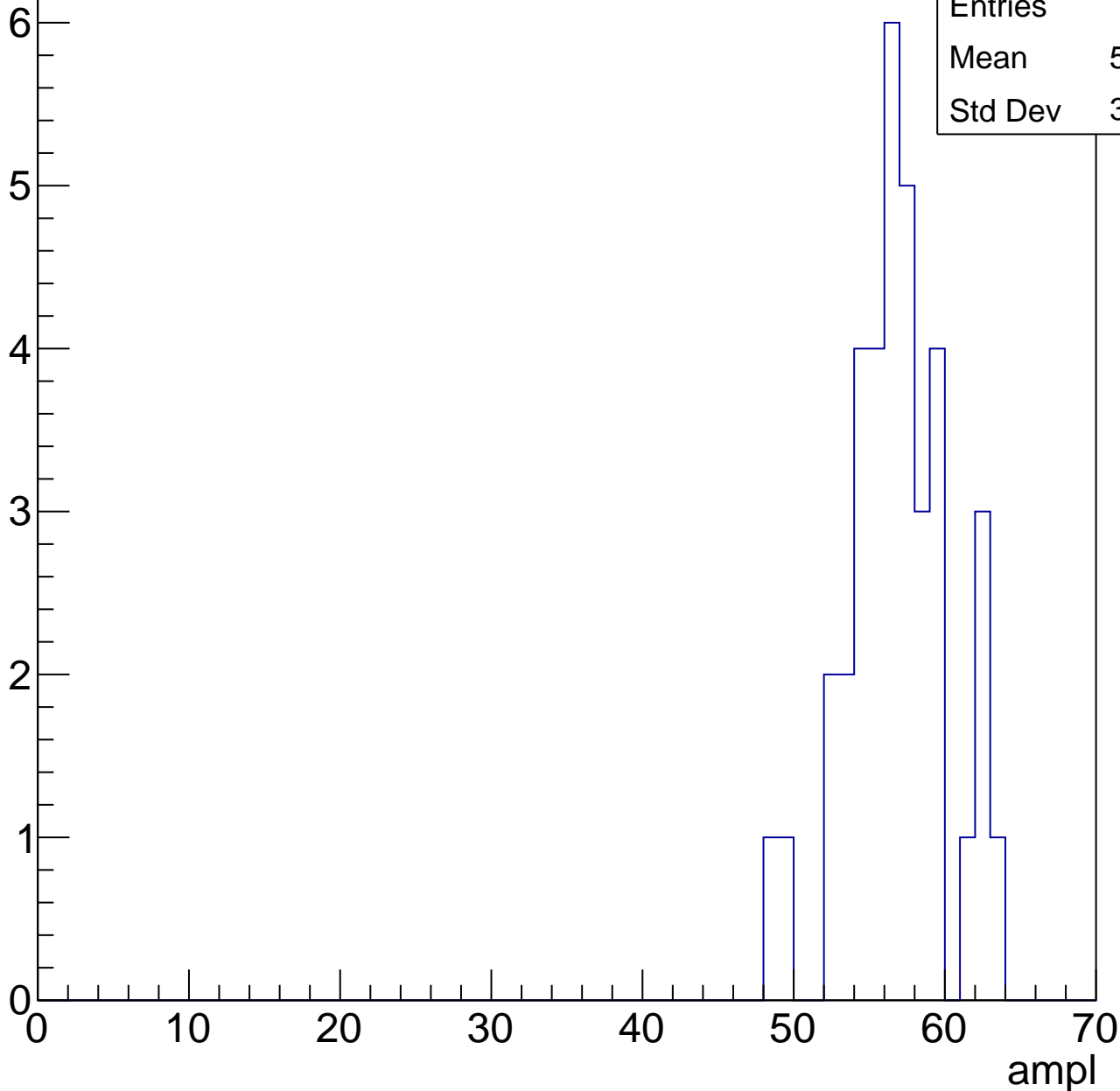


B1L101S, U25-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

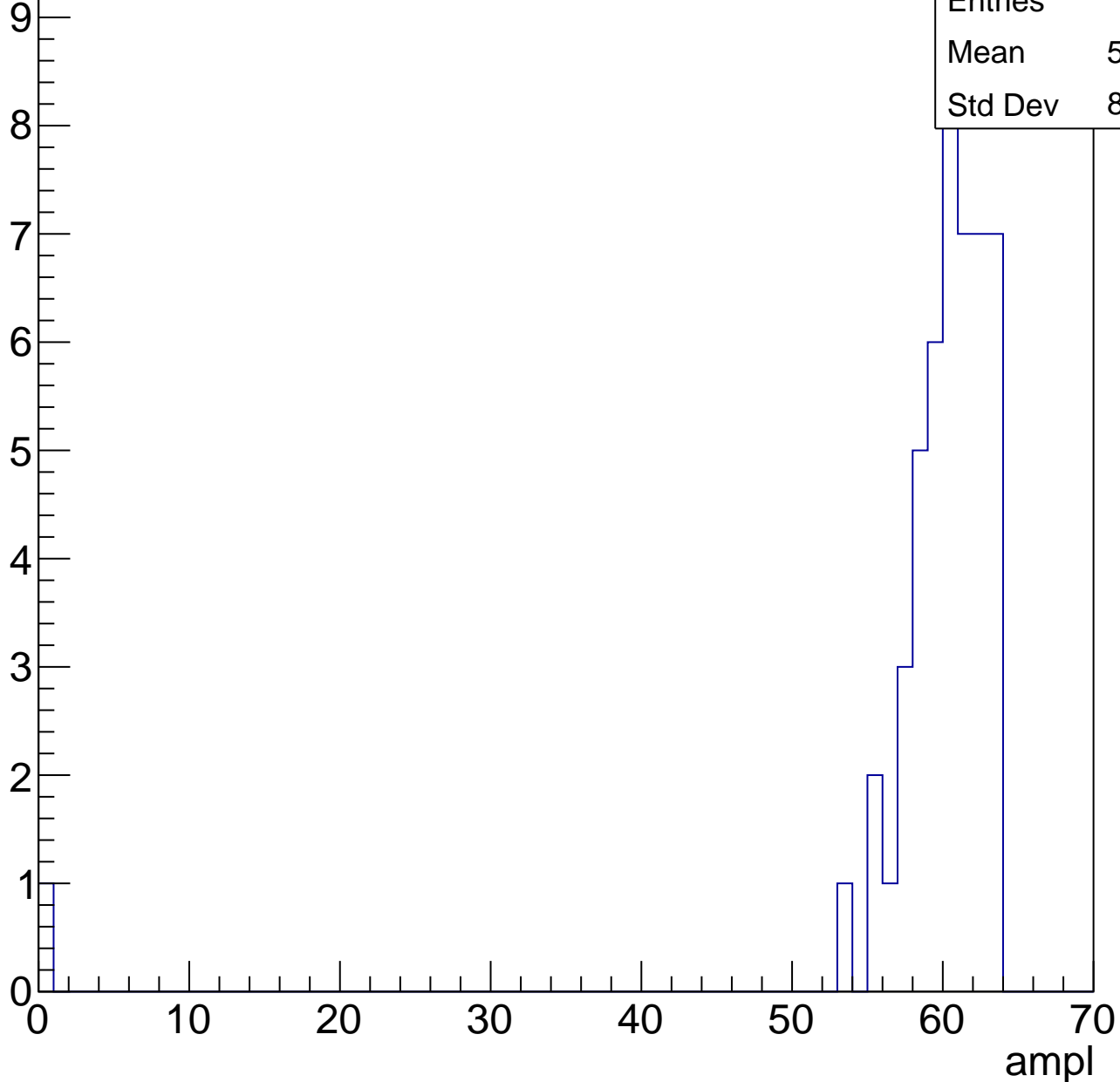
Entries	37
Mean	56.32
Std Dev	3.345



B1L101S, U25-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch45, adc0

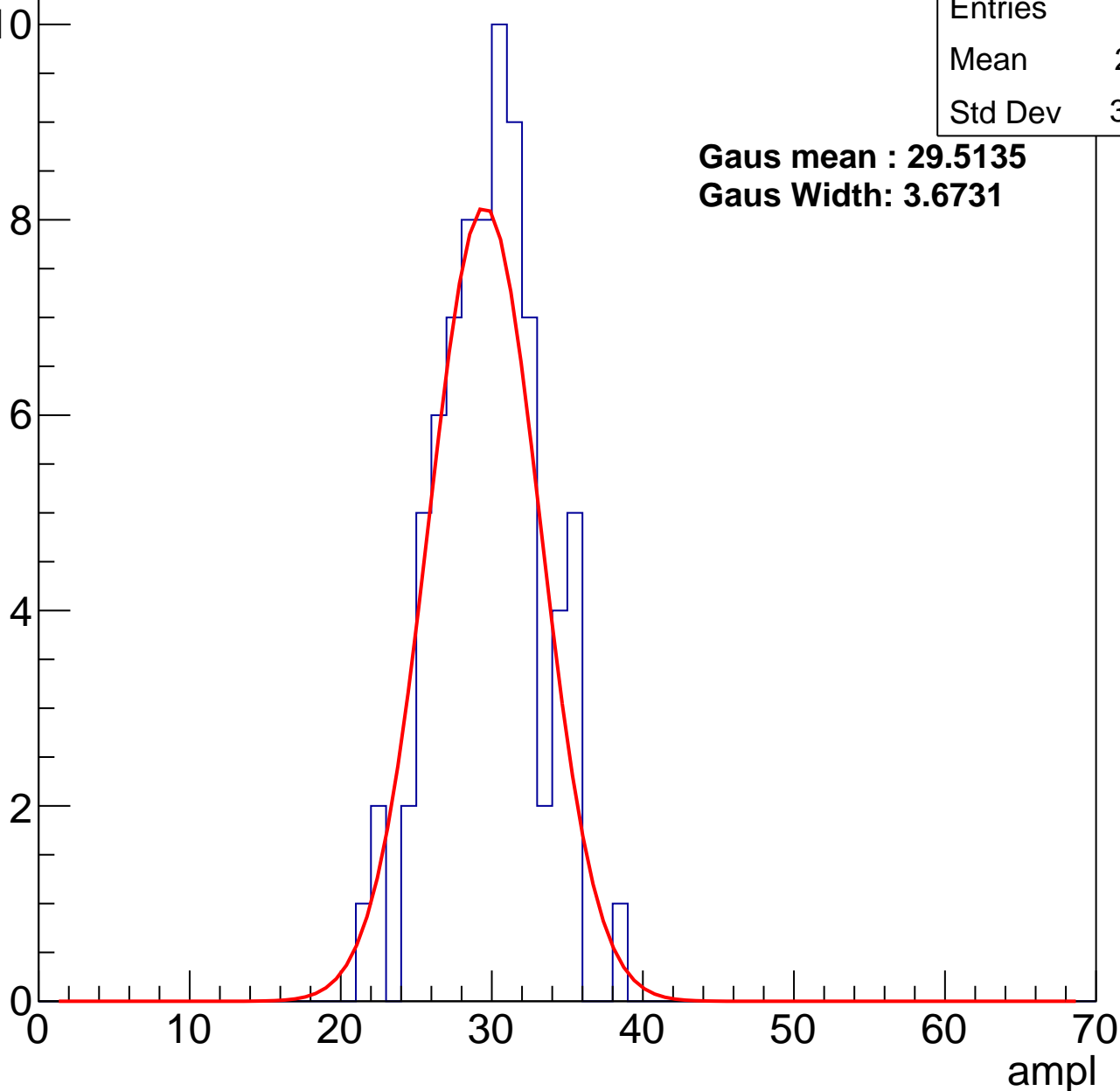
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	29.31
Std Dev	3.374

Gaus mean : 29.5135

Gaus Width: 3.6731



B1L101S, U25-ch45, adc1

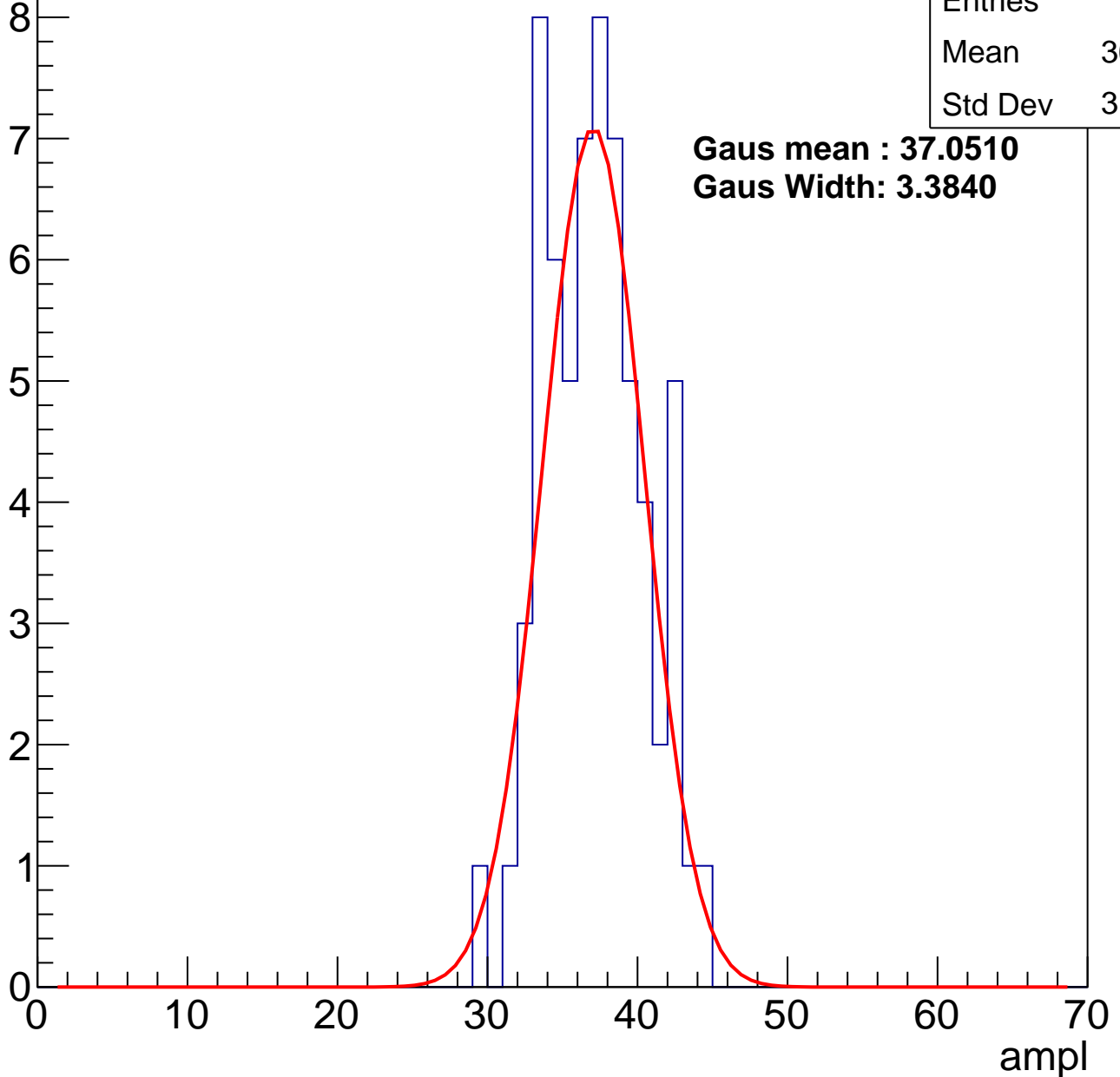
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.67
Std Dev	3.265

Gaus mean : 37.0510

Gaus Width: 3.3840



B1L101S, U25-ch45, adc2

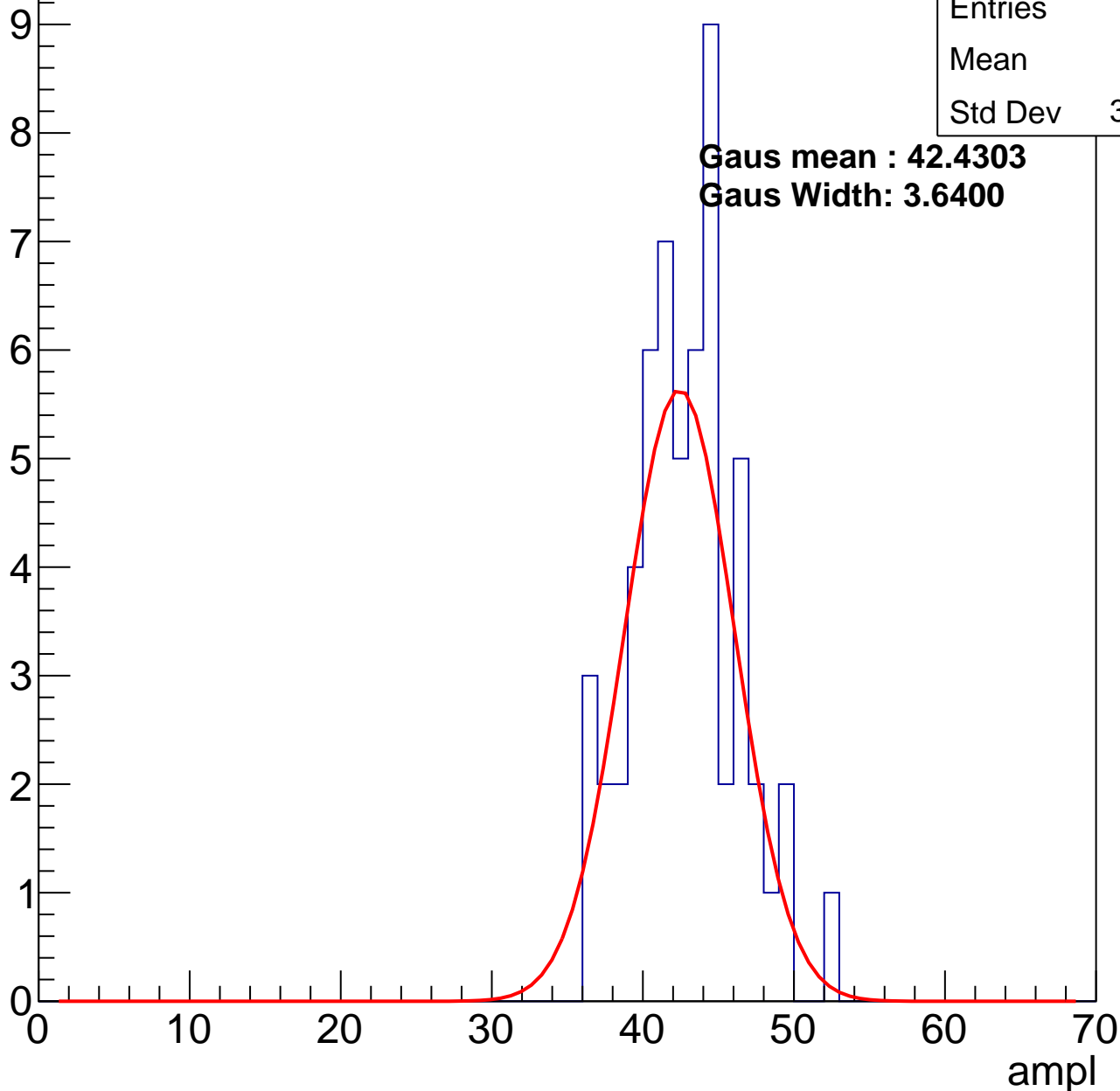
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.4
Std Dev	3.433

Gaus mean : 42.4303

Gaus Width: 3.6400

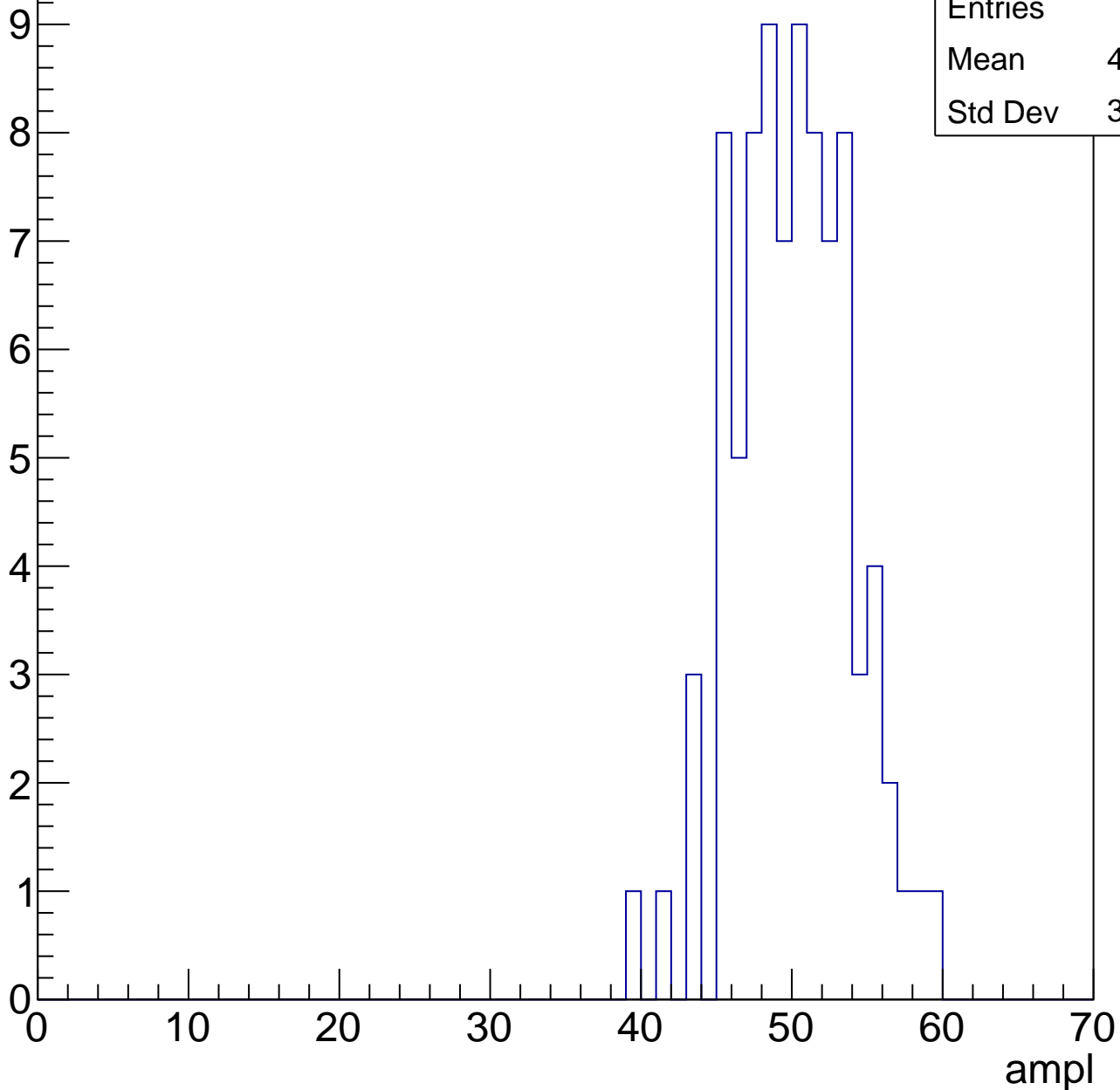


B1L101S, U25-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	49.58
Std Dev	3.814

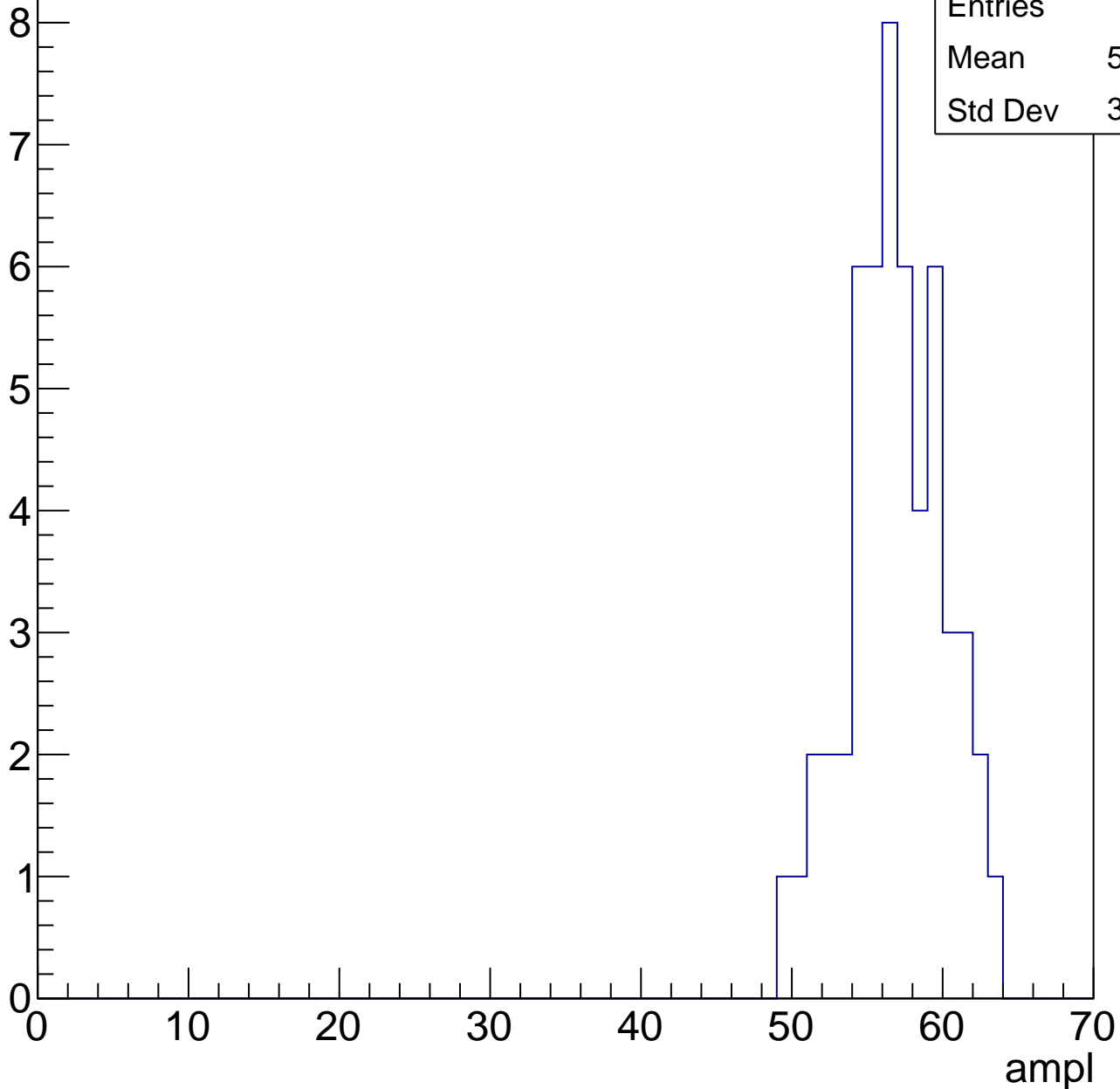


B1L101S, U25-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.43
Std Dev	3.147

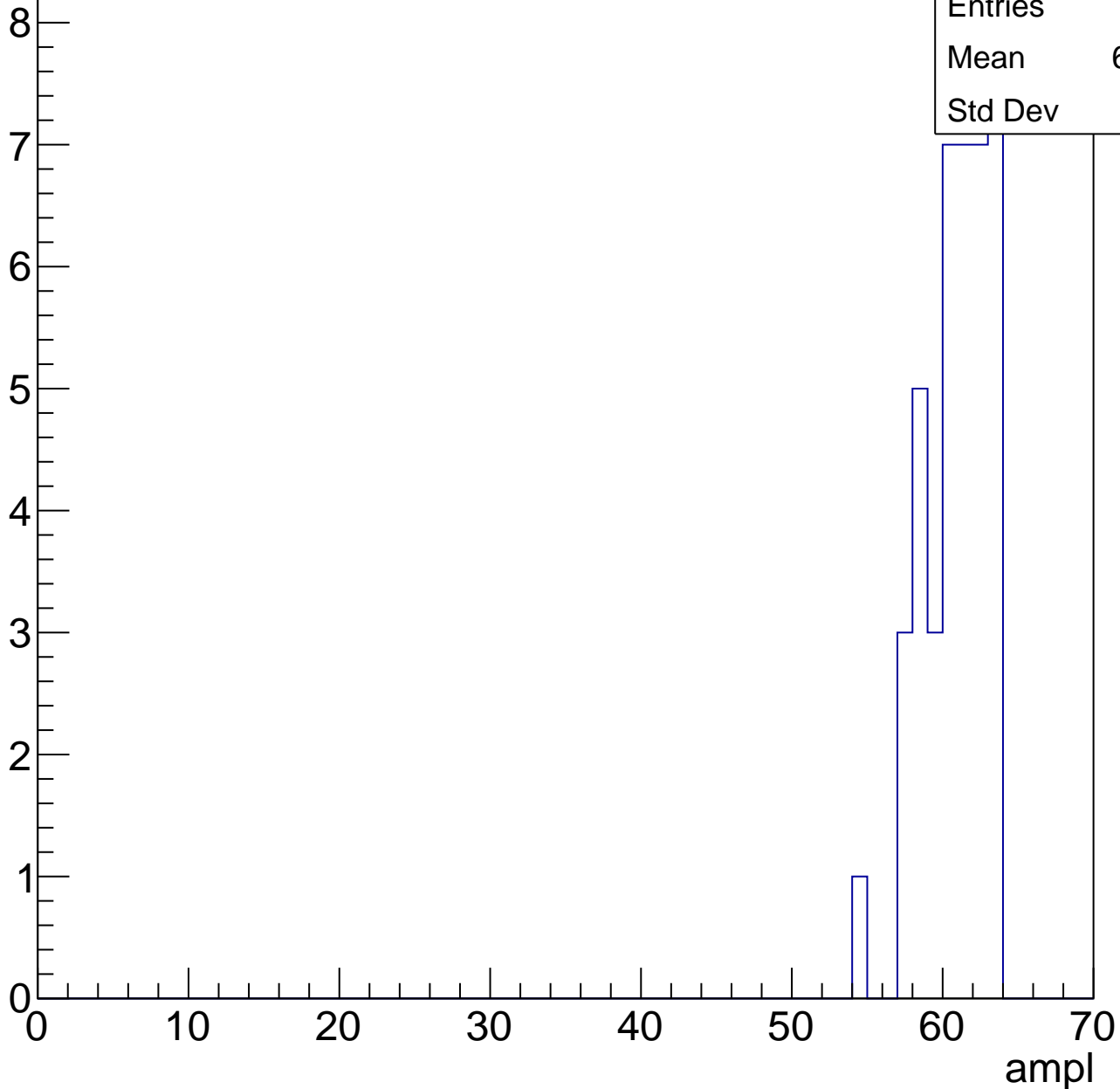


B1L101S, U25-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	60.41
Std Dev	2.13



B1L101S, U25-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch46, adc0

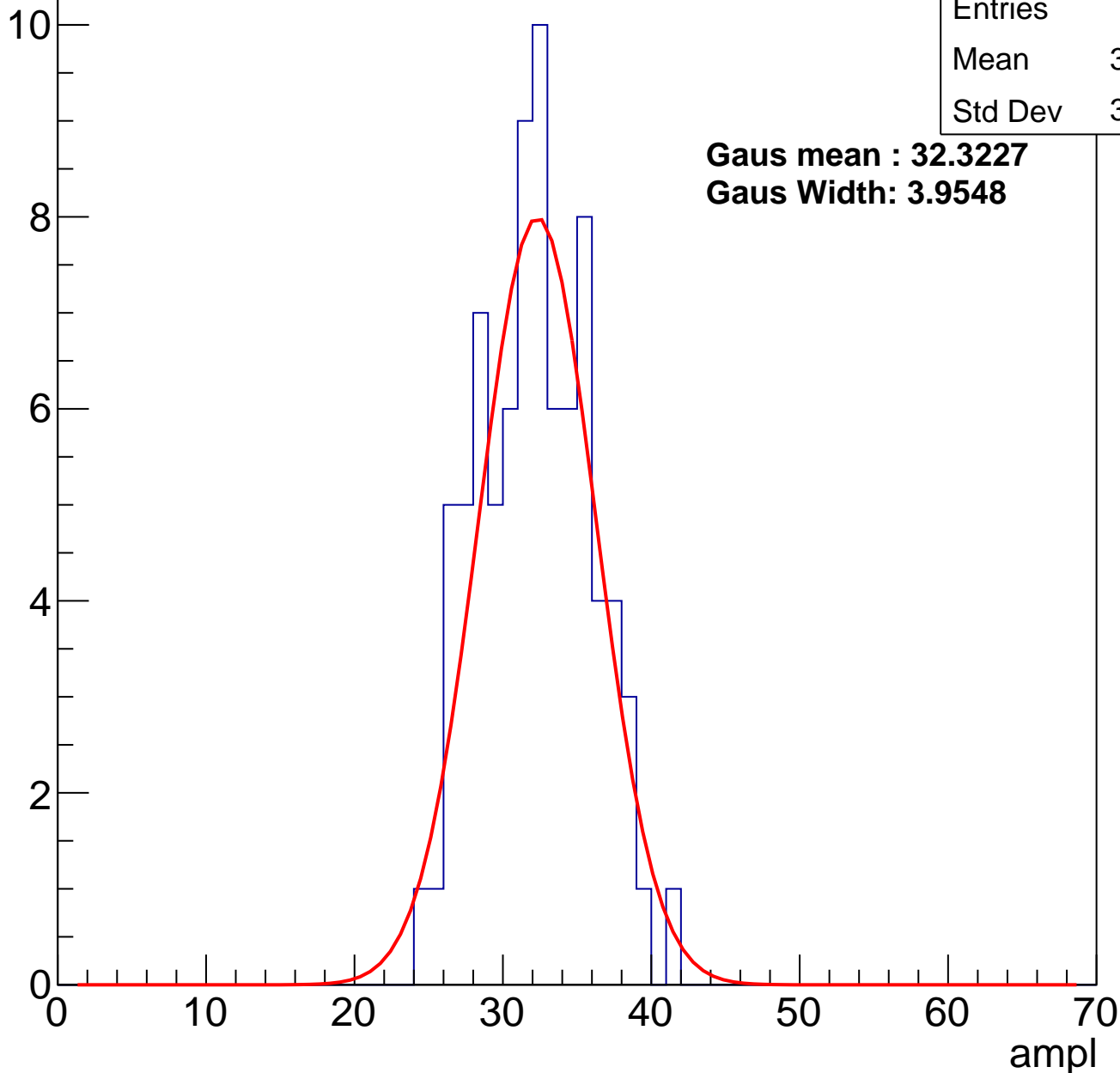
calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	31.73
Std Dev	3.673

Gaus mean : 32.3227

Gaus Width: 3.9548

Entry



B1L101S, U25-ch46, adc1

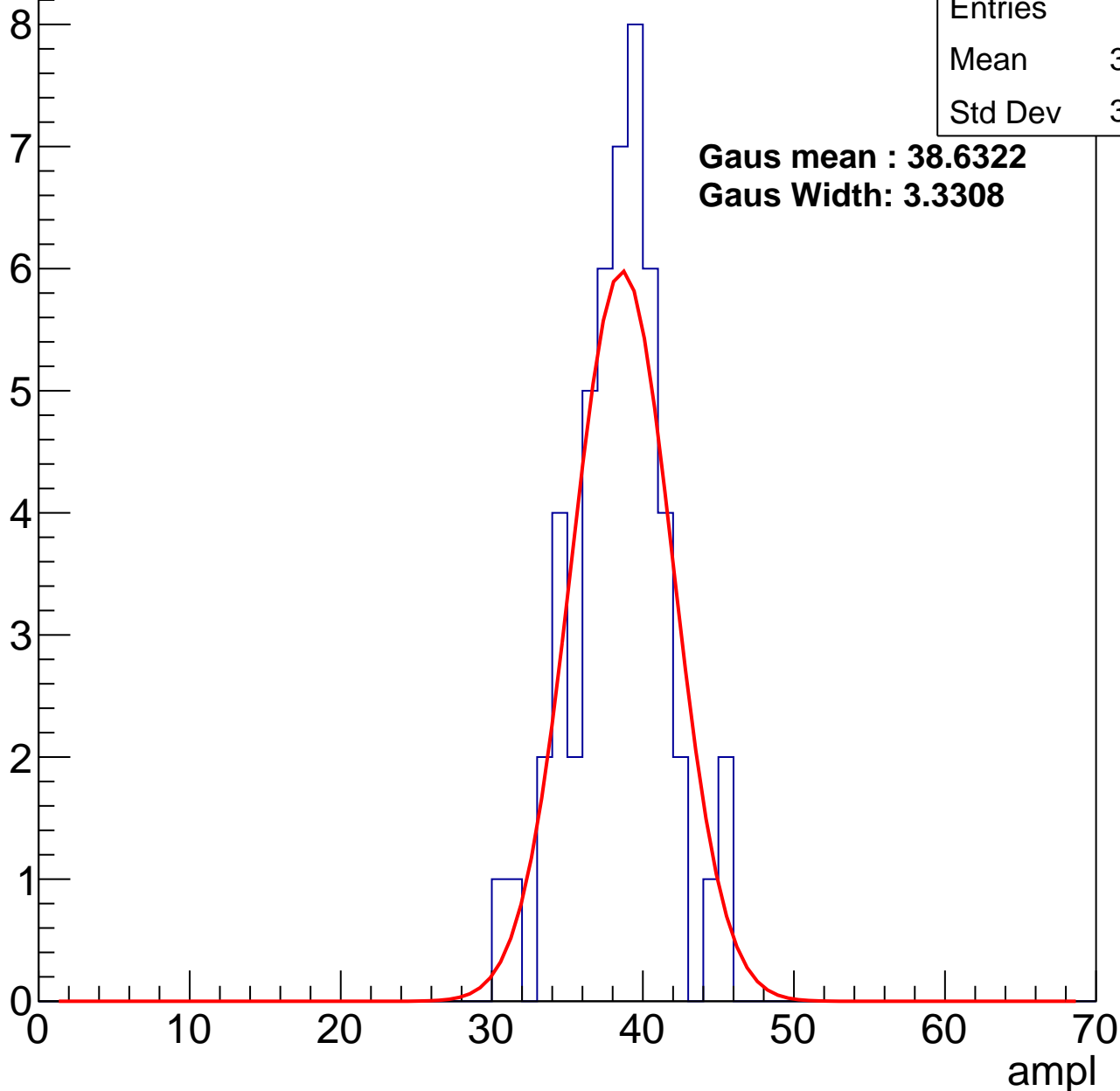
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	37.94
Std Dev	3.146

Gaus mean : 38.6322

Gaus Width: 3.3308



B1L101S, U25-ch46, adc2

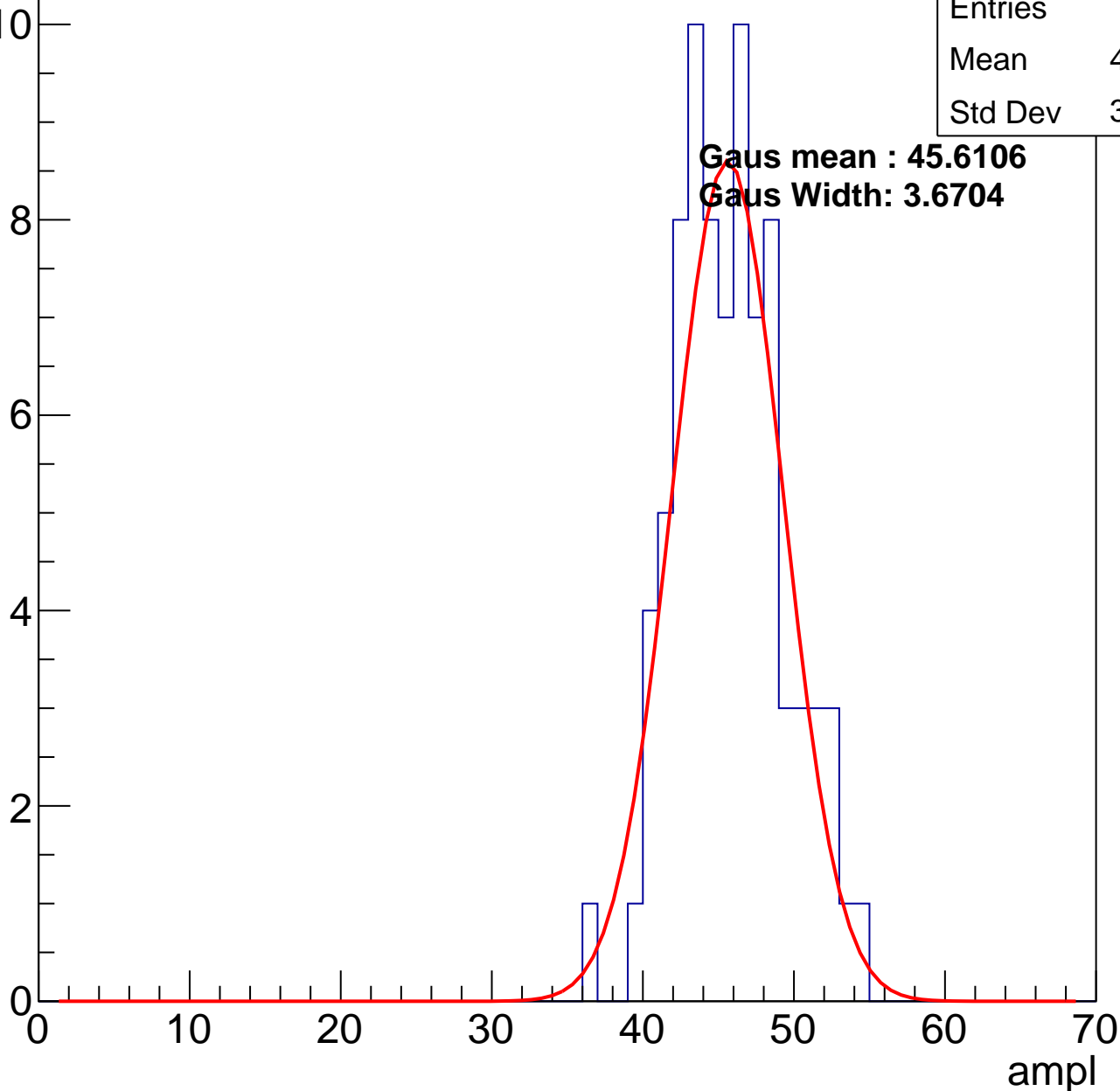
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	45.29
Std Dev	3.545

Gaus mean : 45.6106

Gaus Width: 3.6704

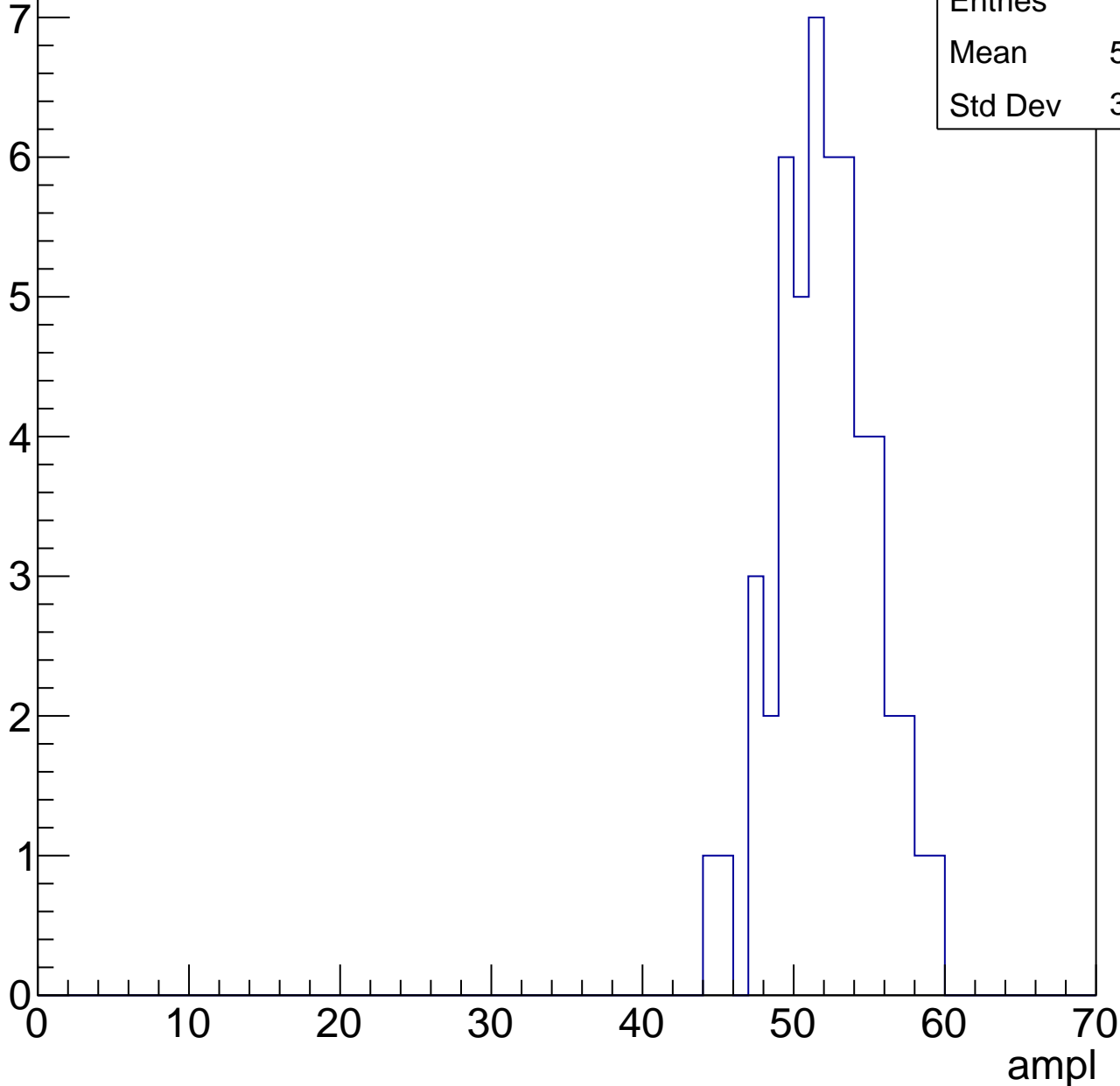


B1L101S, U25-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	51.69
Std Dev	3.196

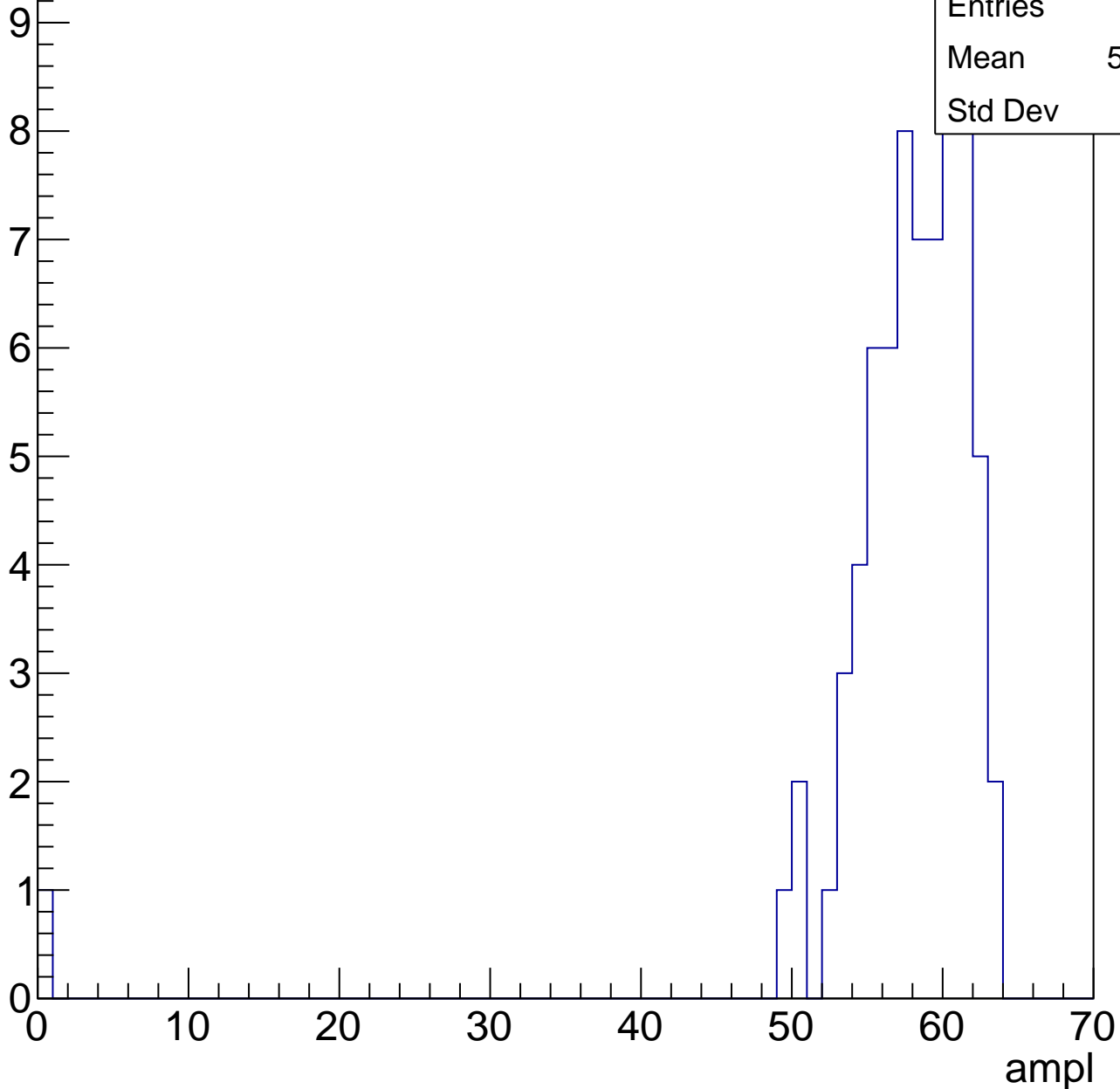


B1L101S, U25-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	56.87
Std Dev	7.55

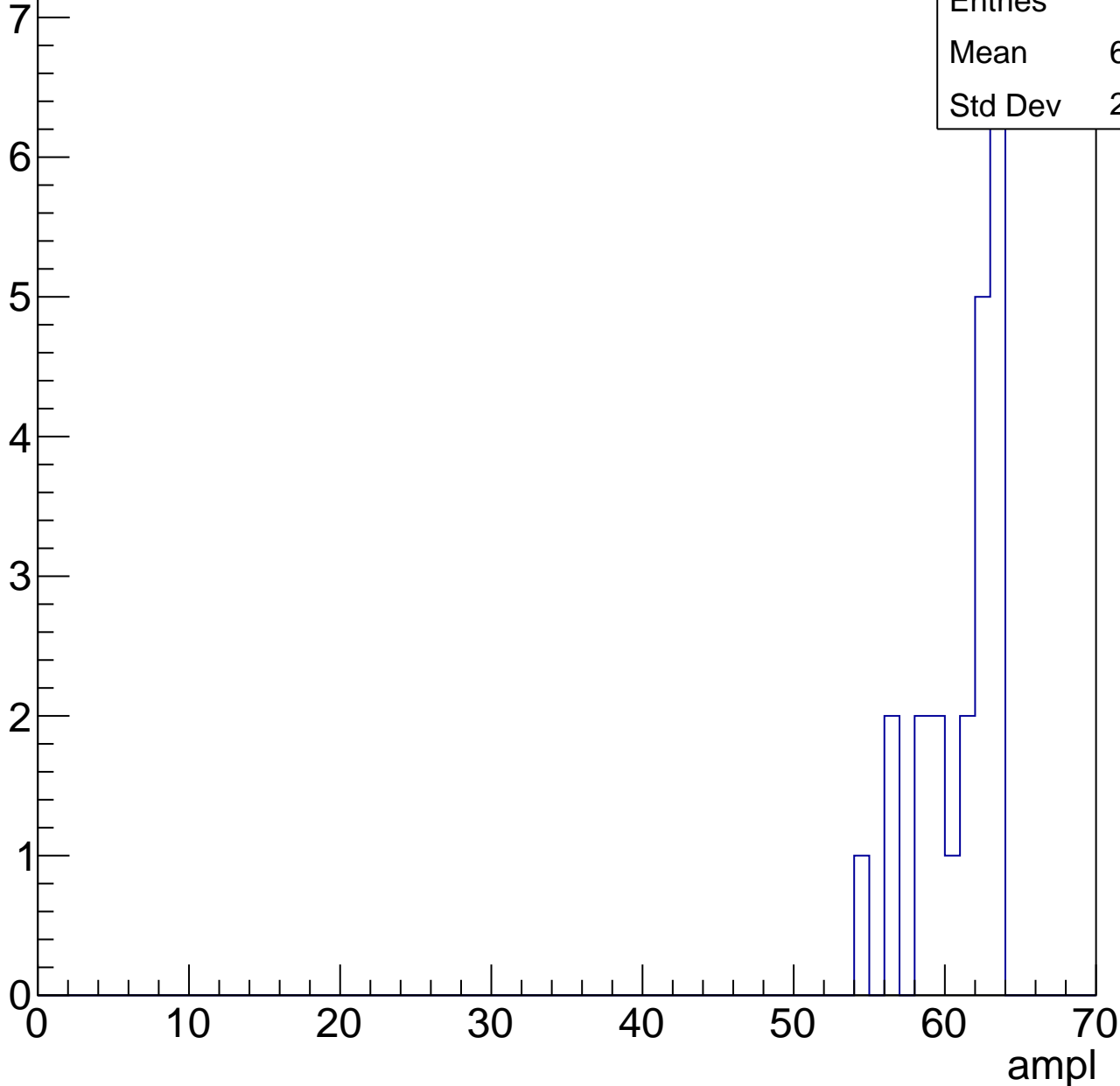


B1L101S, U25-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	60.59
Std Dev	2.657



B1L101S, U25-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

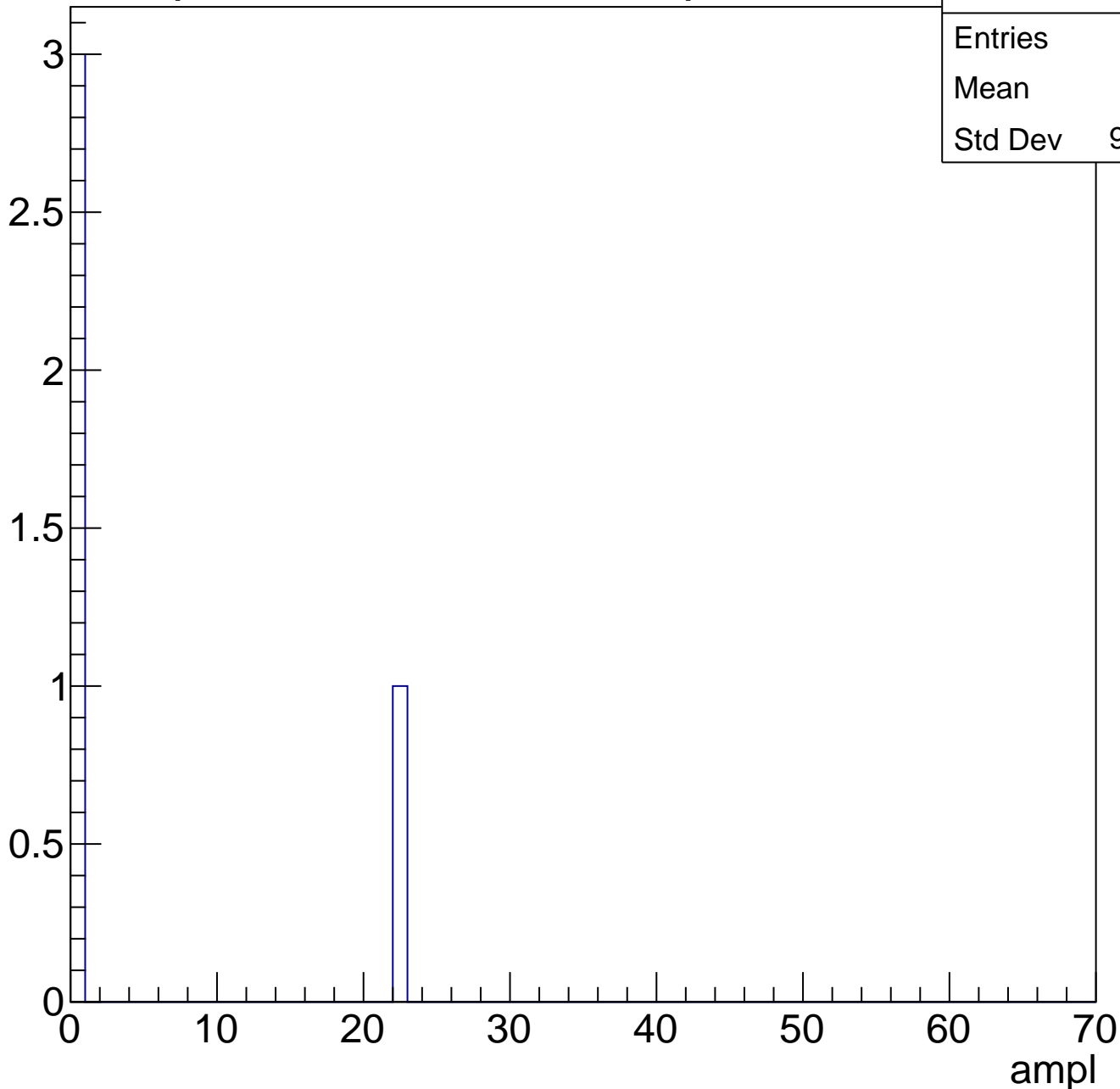
Entry



B1L101S, U25-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch47, adc0

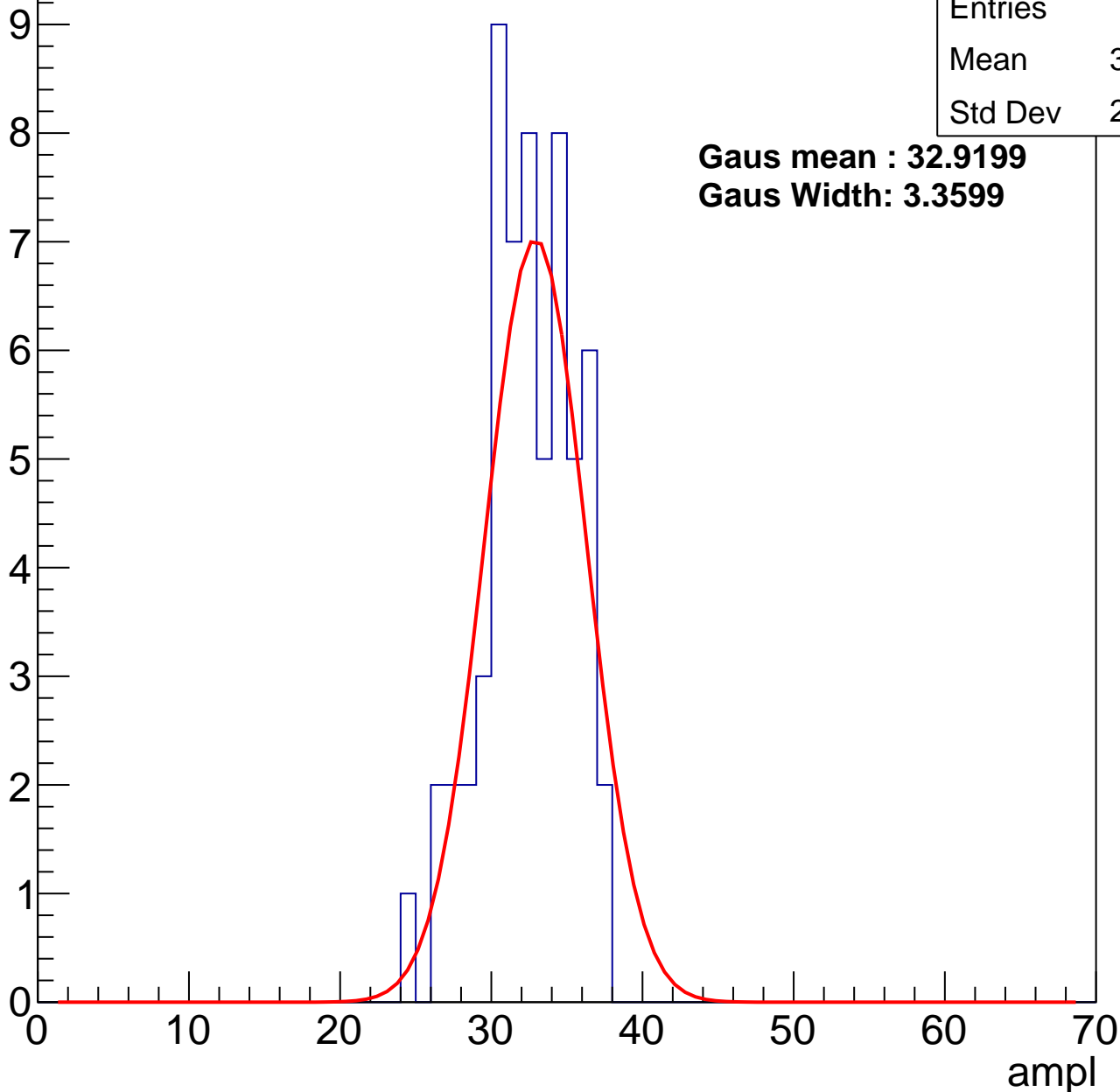
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	31.97
Std Dev	2.932

Gaus mean : 32.9199

Gaus Width: 3.3599



B1L101S, U25-ch47, adc1

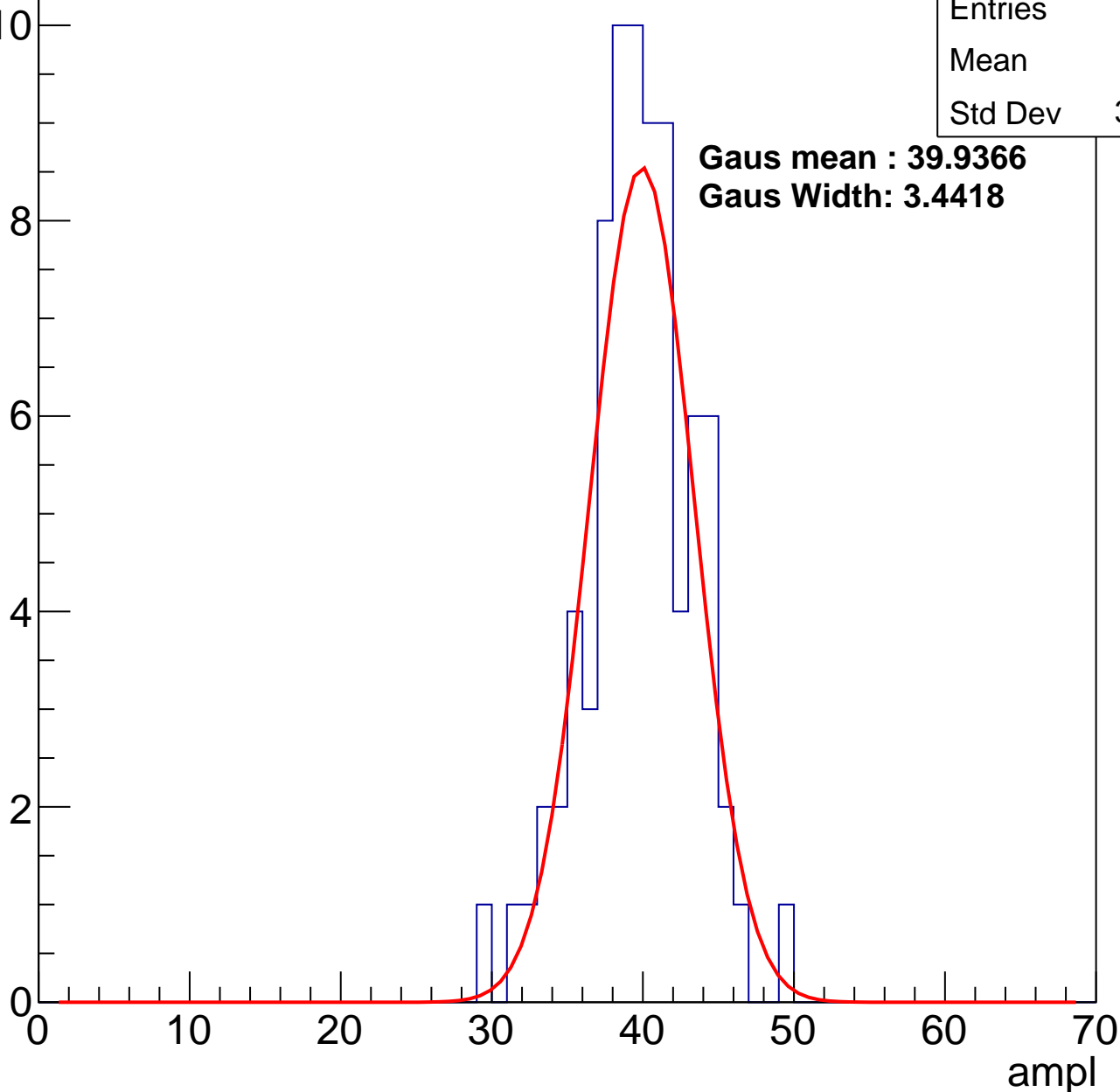
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	39.3
Std Dev	3.551

Gaus mean : 39.9366

Gaus Width: 3.4418



B1L101S, U25-ch47, adc2

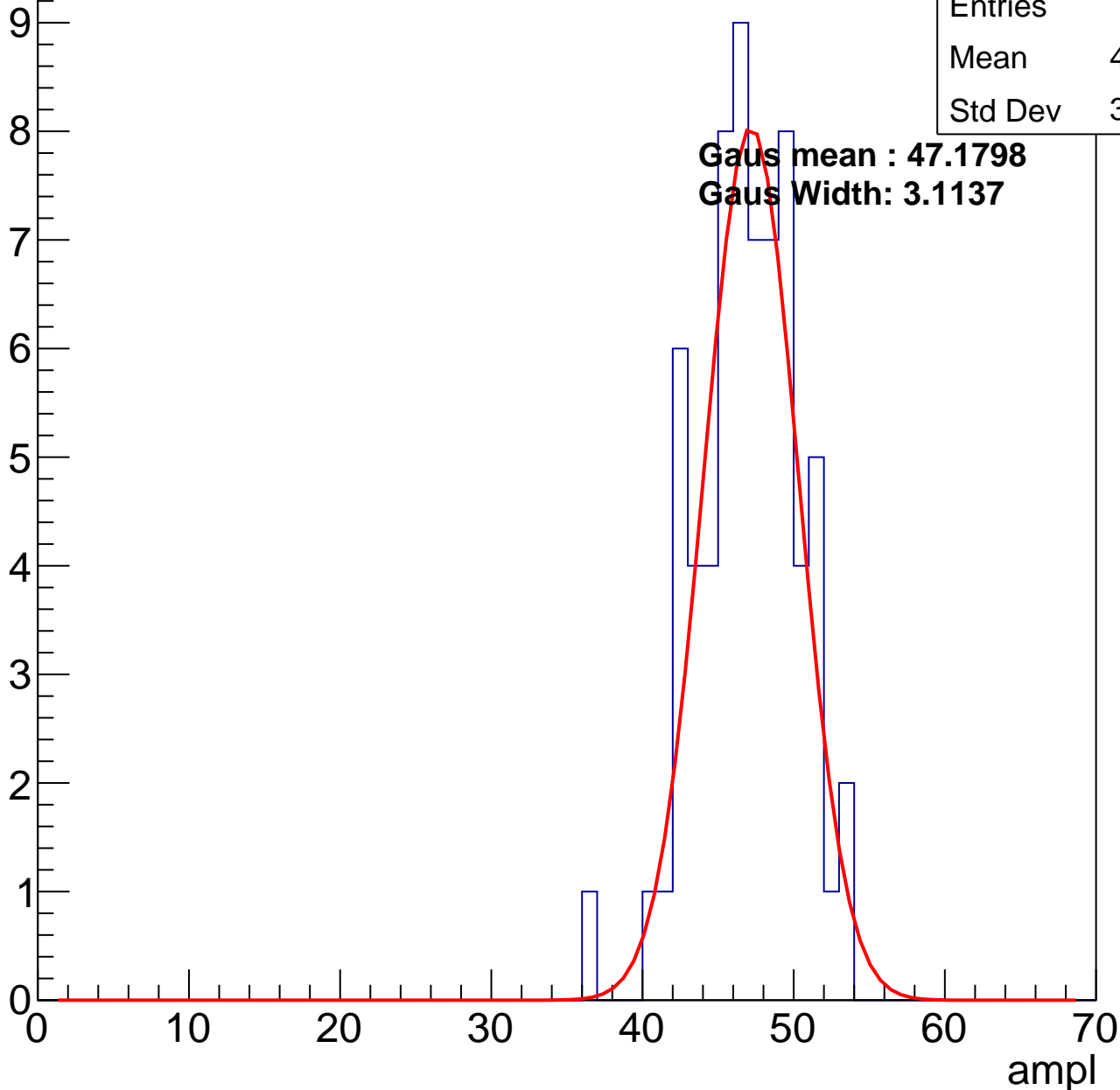
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	46.49
Std Dev	3.279

Gaus mean : 47.1798

Gaus Width: 3.1137



B1L101S, U25-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	53.23
Std Dev	3.049

Entry

10

8

6

4

2

0

0

10

20

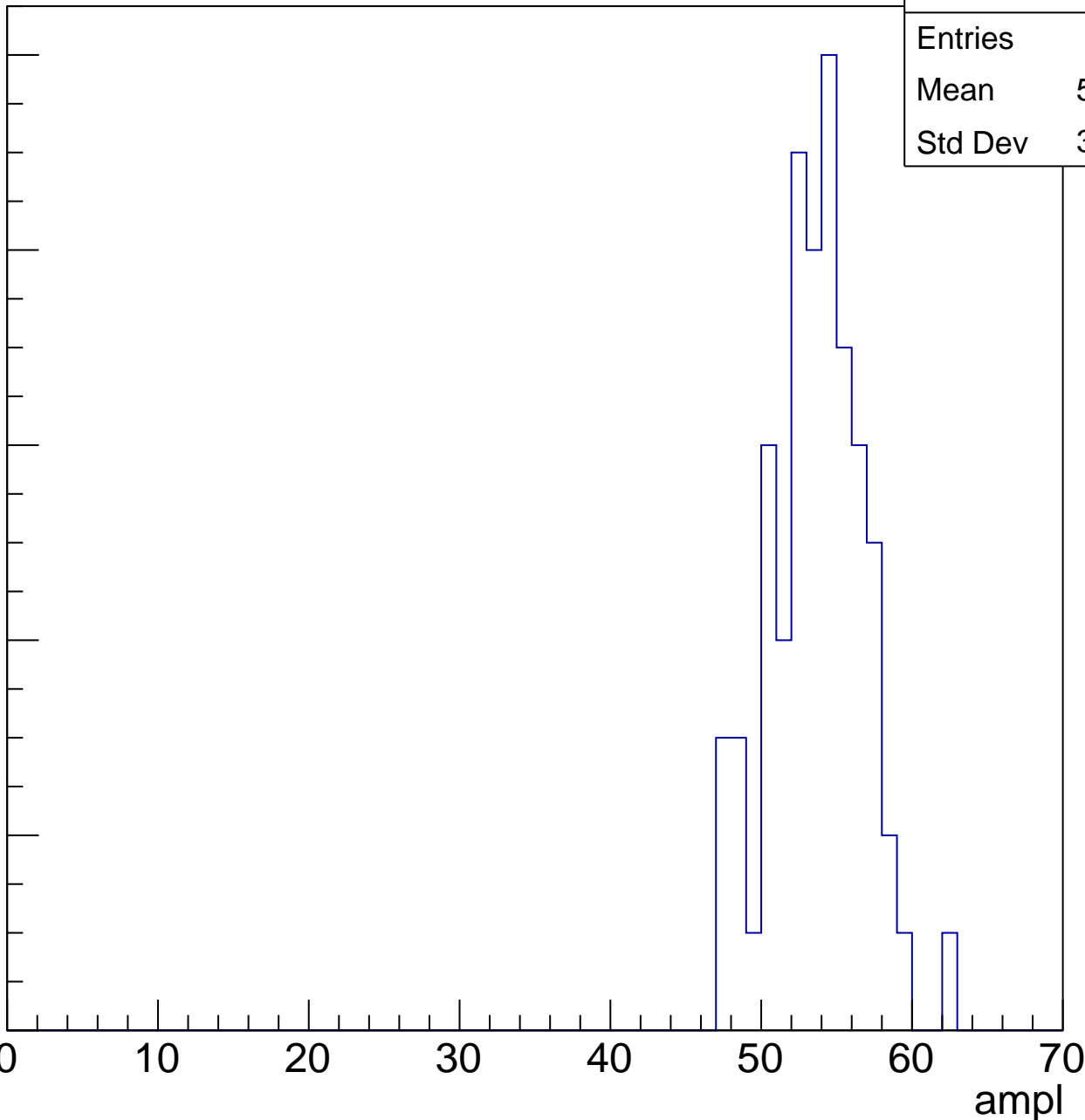
30

40

50

60

ampl

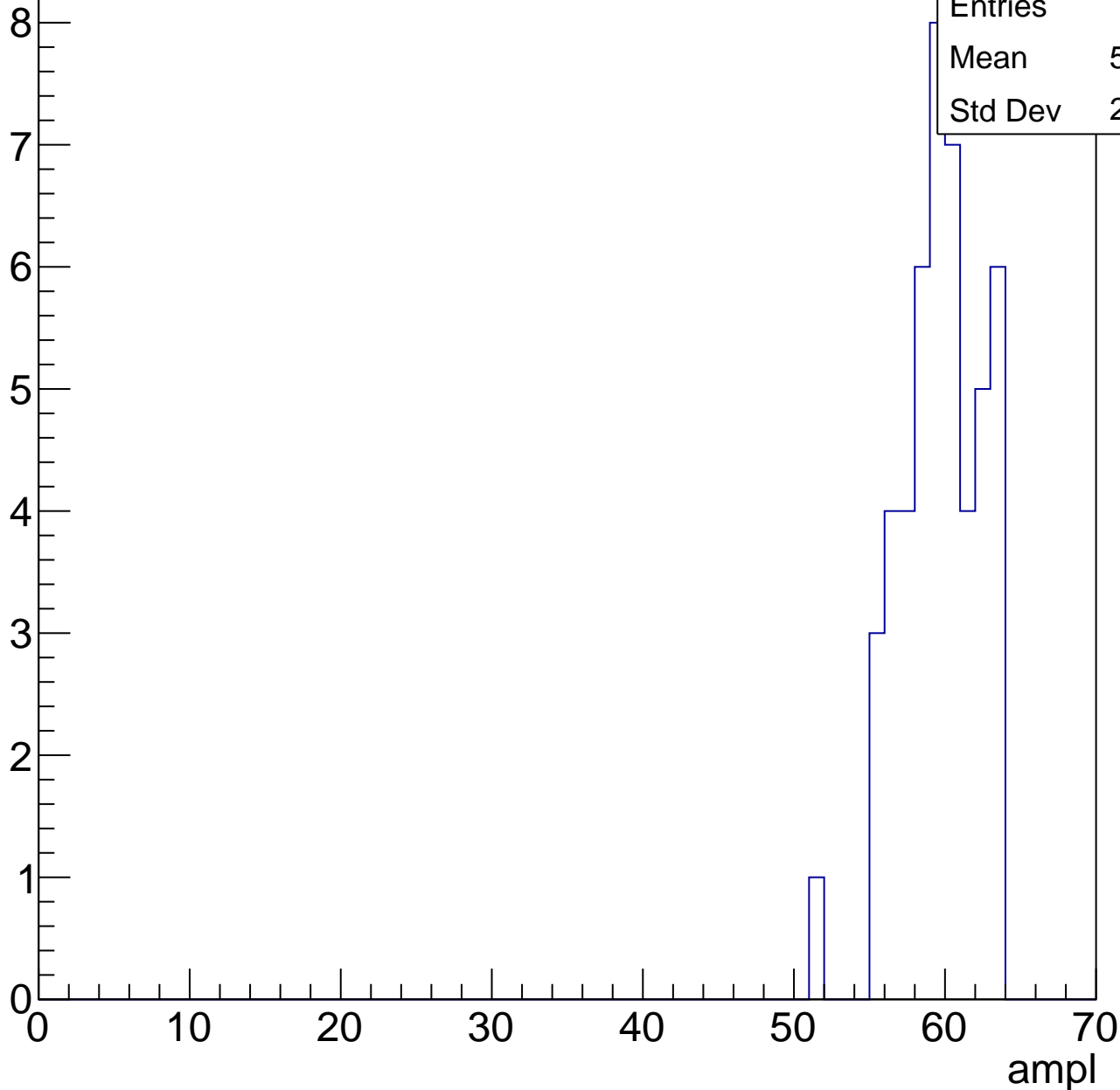


B1L101S, U25-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	59.17
Std Dev	2.633

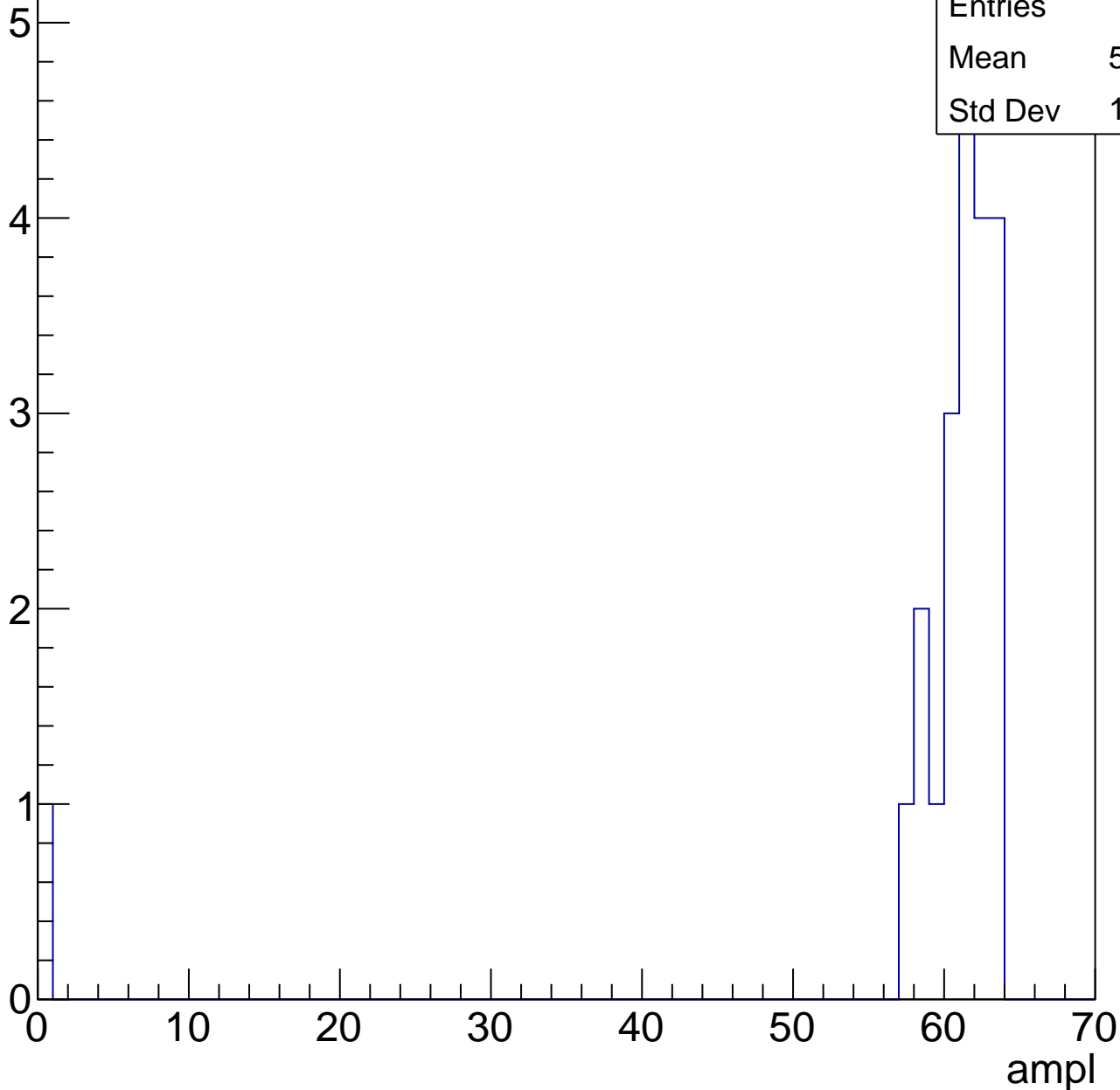


B1L101S, U25-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	57.95
Std Dev	13.07



B1L101S, U25-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch47, adc7

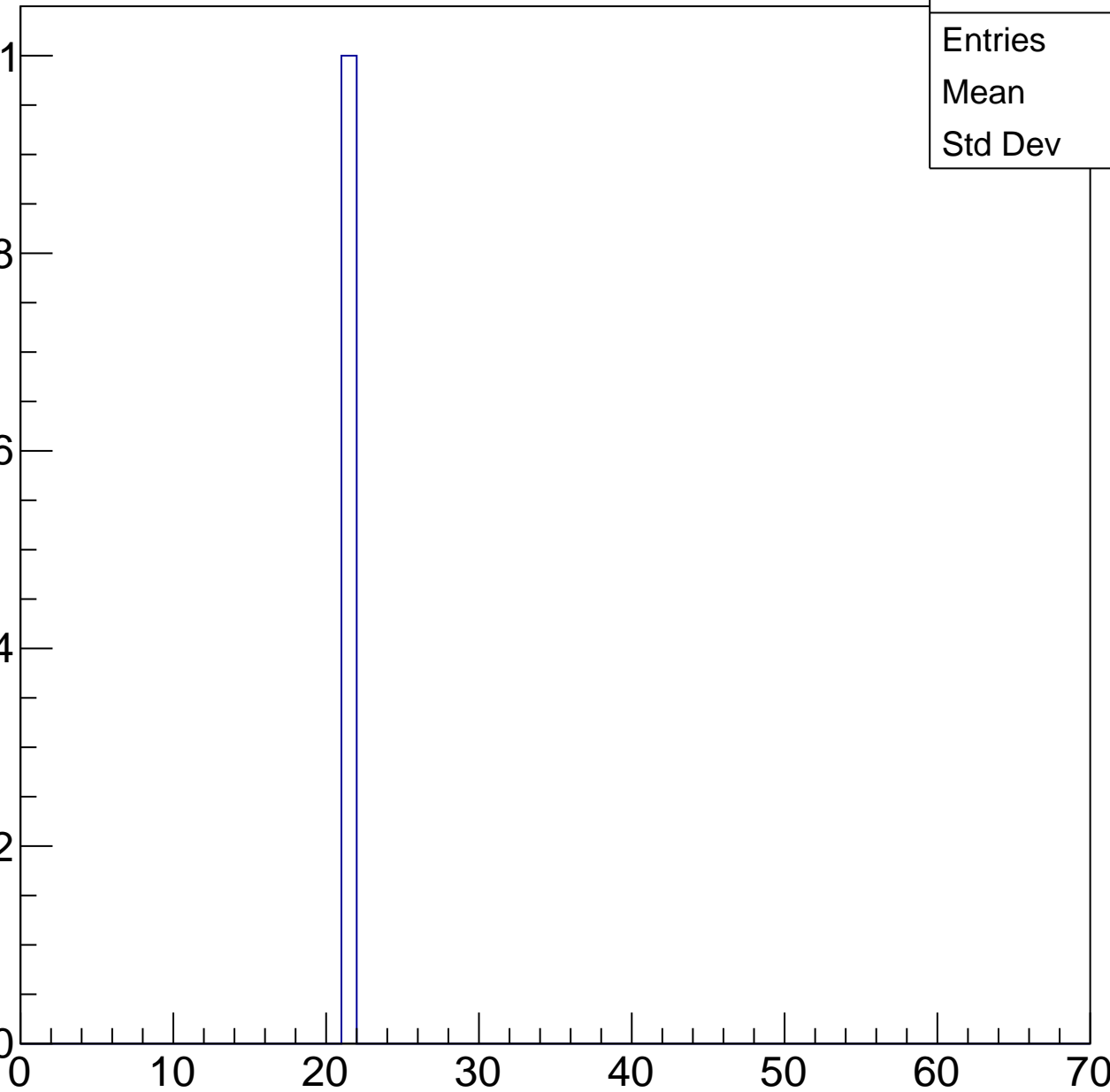
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl



B1L101S, U25-ch48, adc0

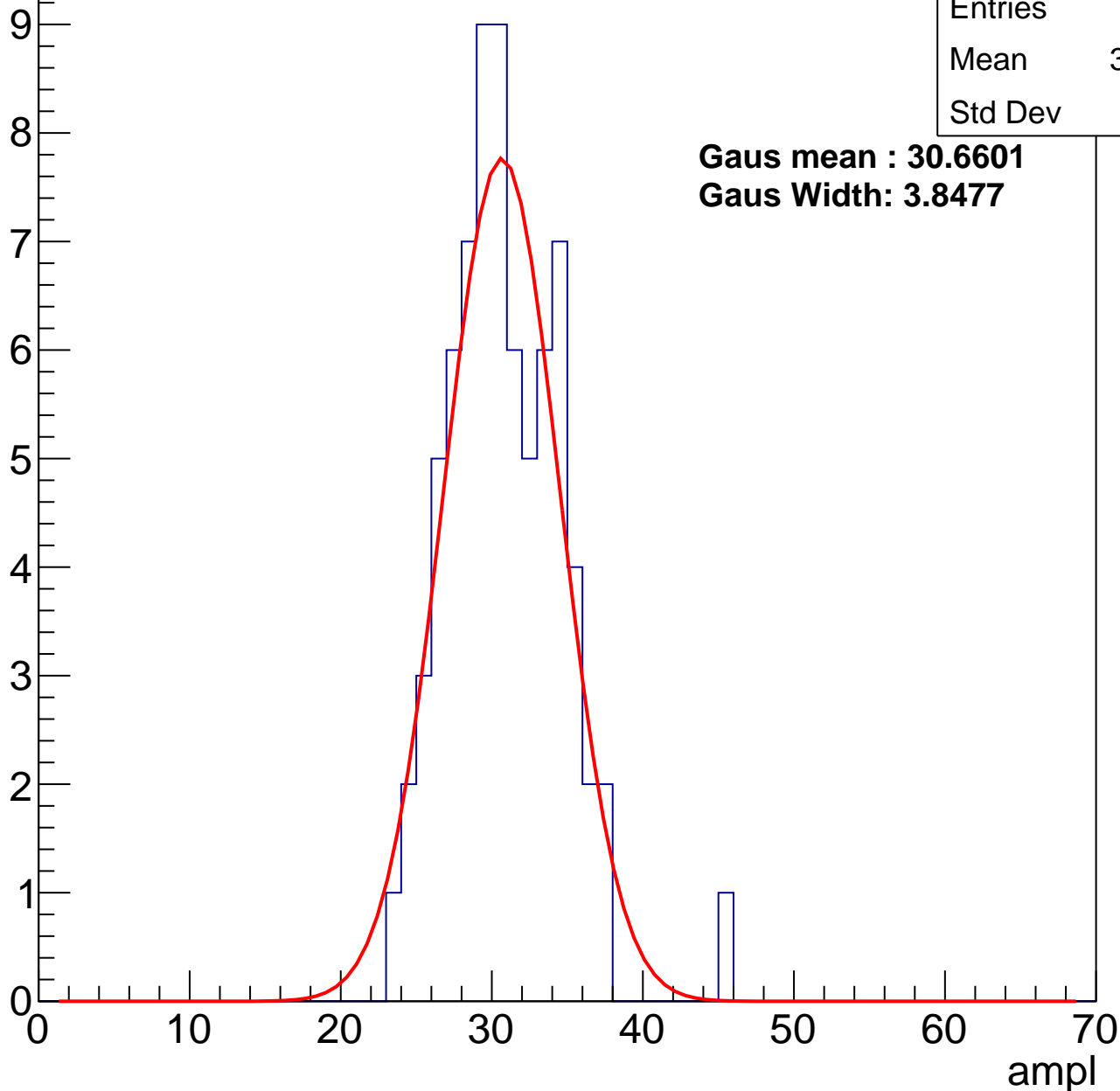
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.37
Std Dev	3.73

Gaus mean : 30.6601

Gaus Width: 3.8477



B1L101S, U25-ch48, adc1

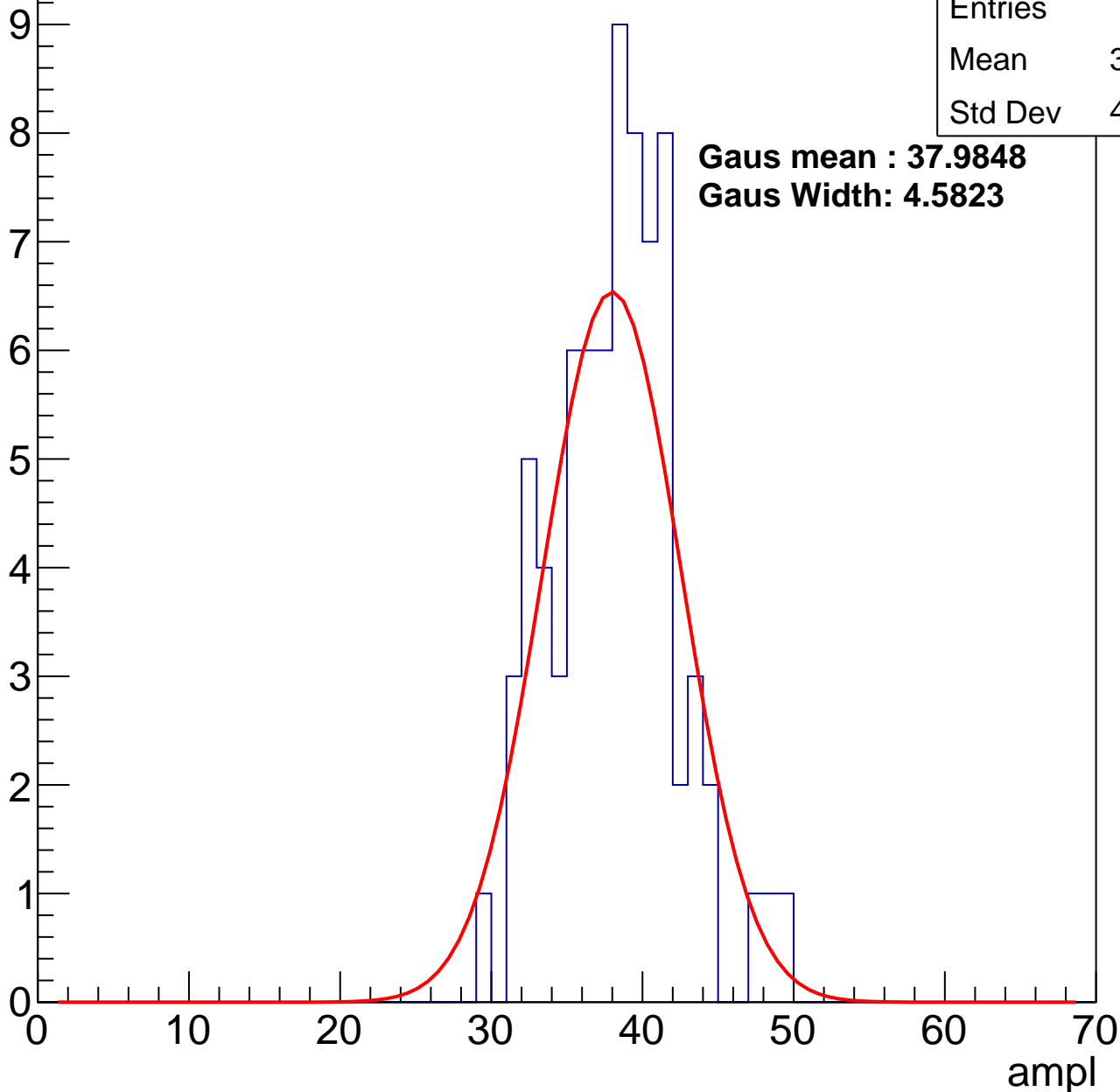
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.78
Std Dev	4.015

Gaus mean : 37.9848

Gaus Width: 4.5823



B1L101S, U25-ch48, adc2

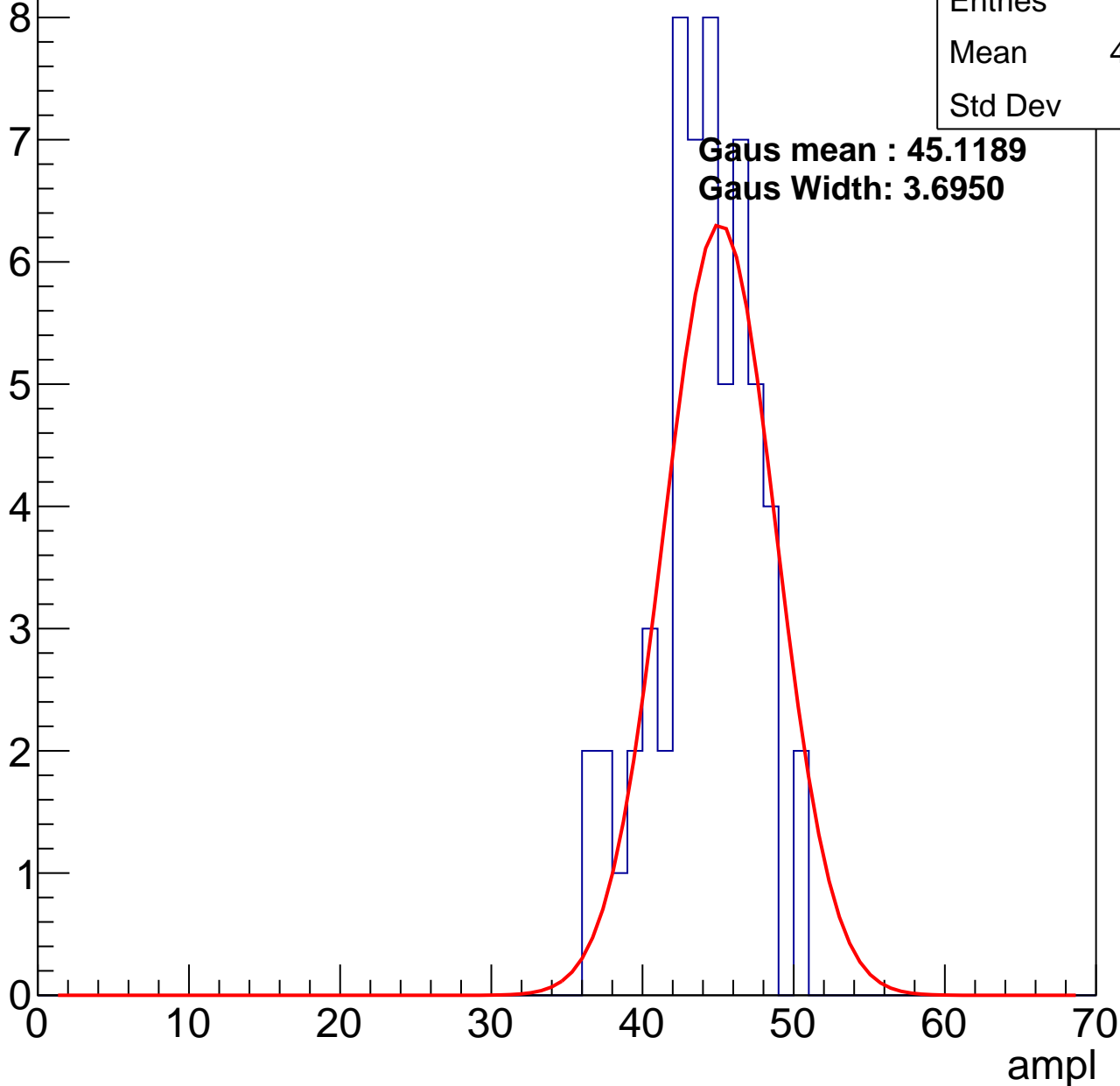
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.57
Std Dev	3.27

Gaus mean : 45.1189

Gaus Width: 3.6950

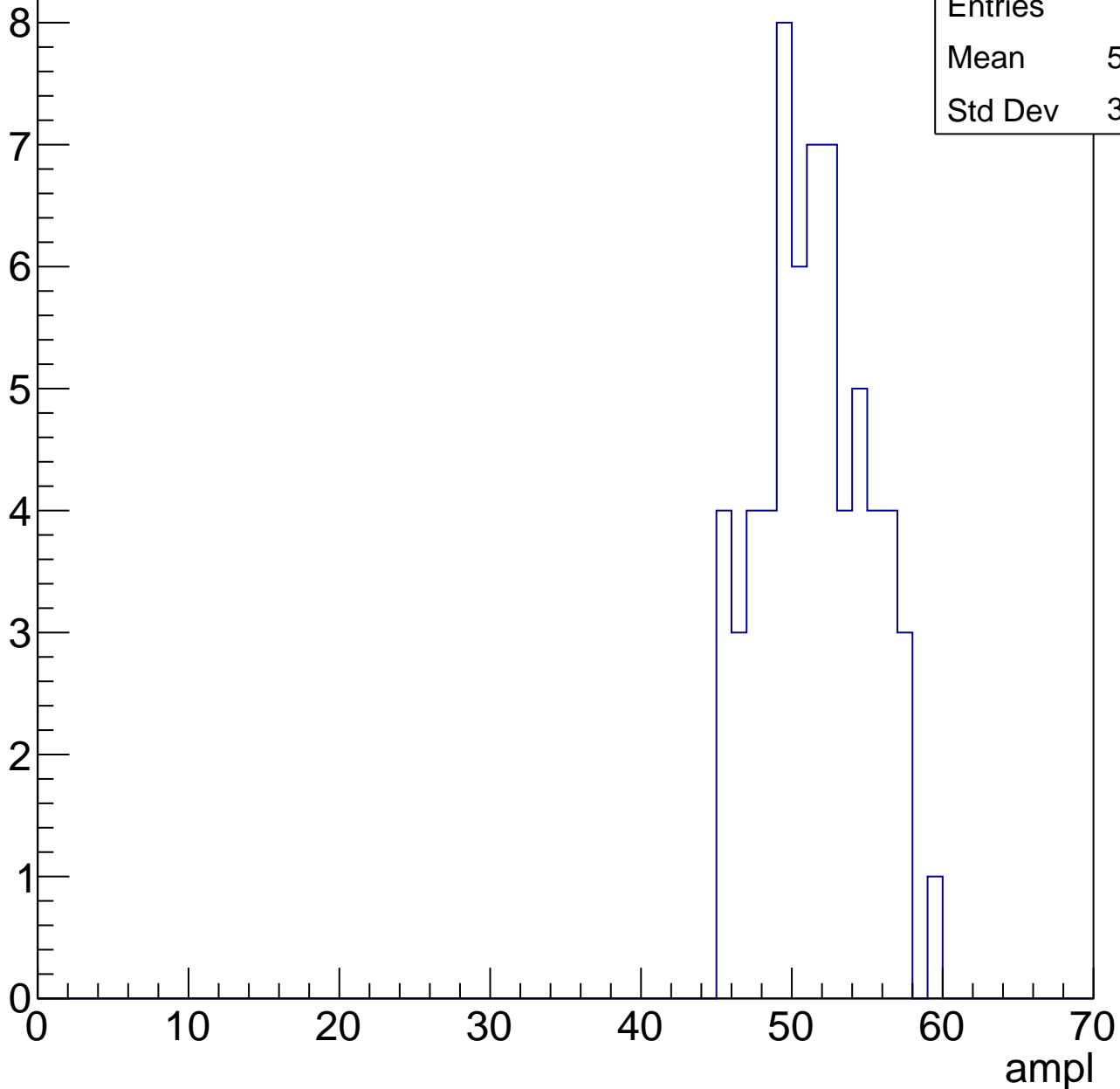


B1L101S, U25-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	51.05
Std Dev	3.448

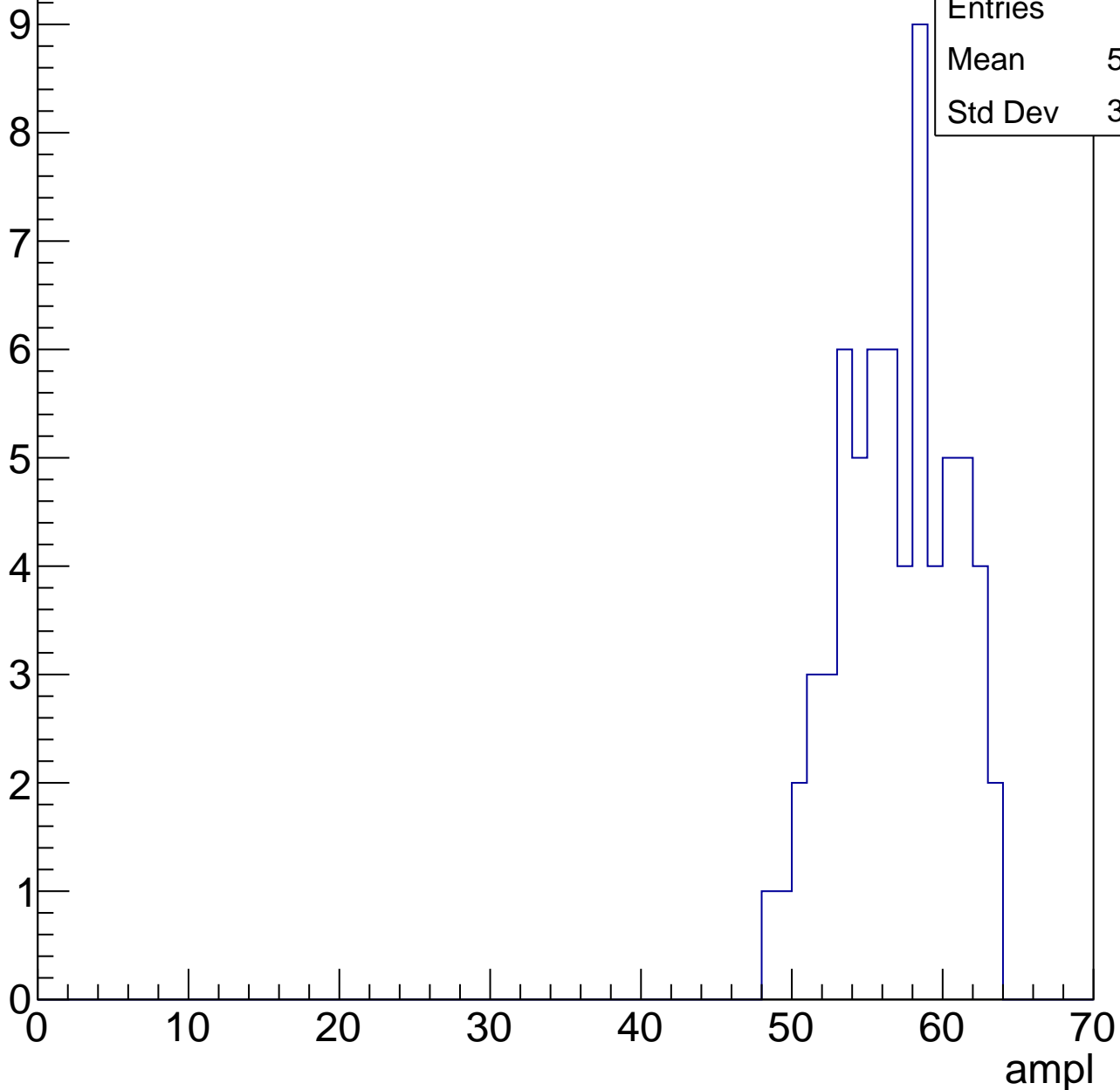


B1L101S, U25-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

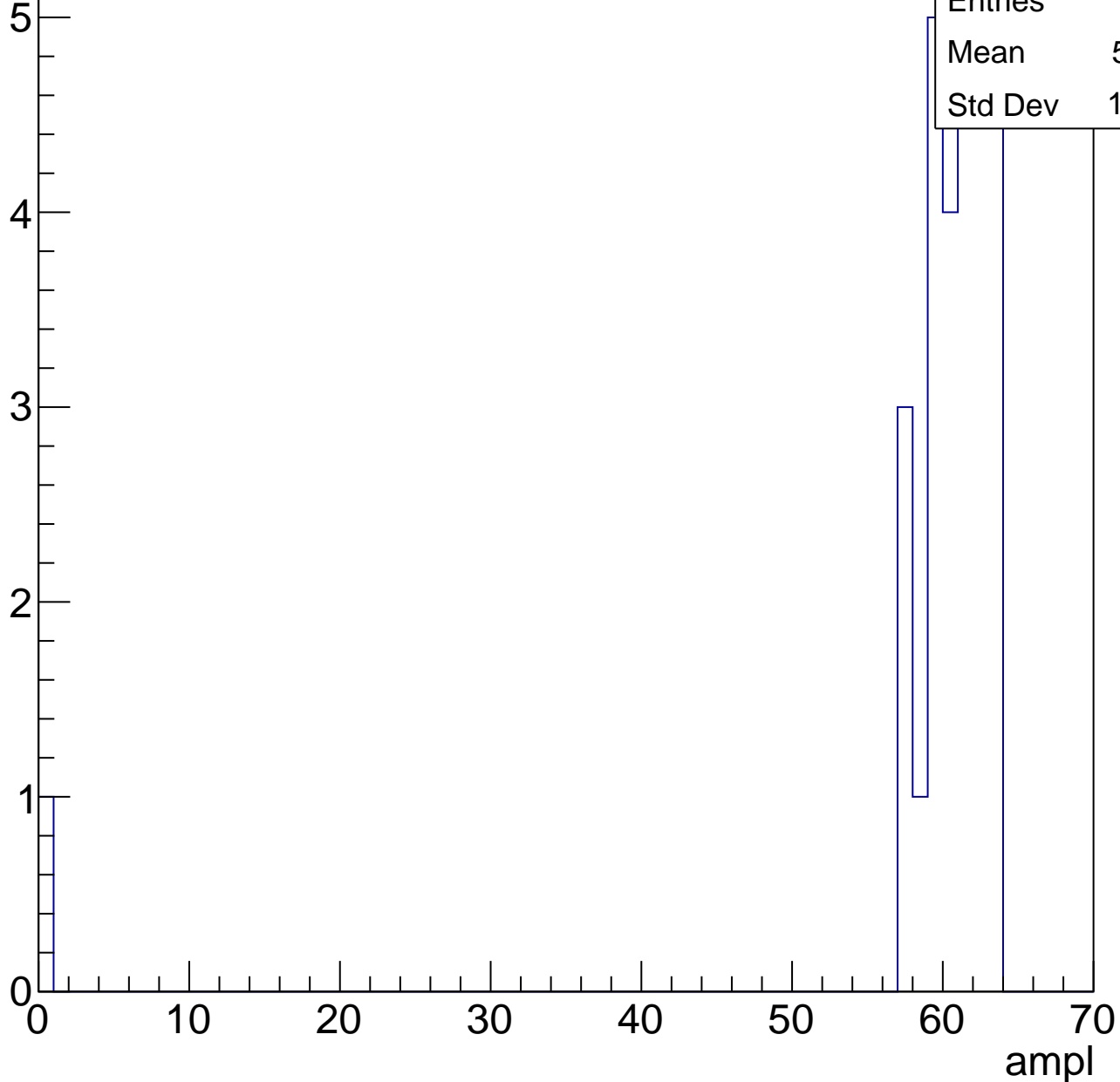
Entries	66
Mean	56.44
Std Dev	3.689



B1L101S, U25-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch49, adc0

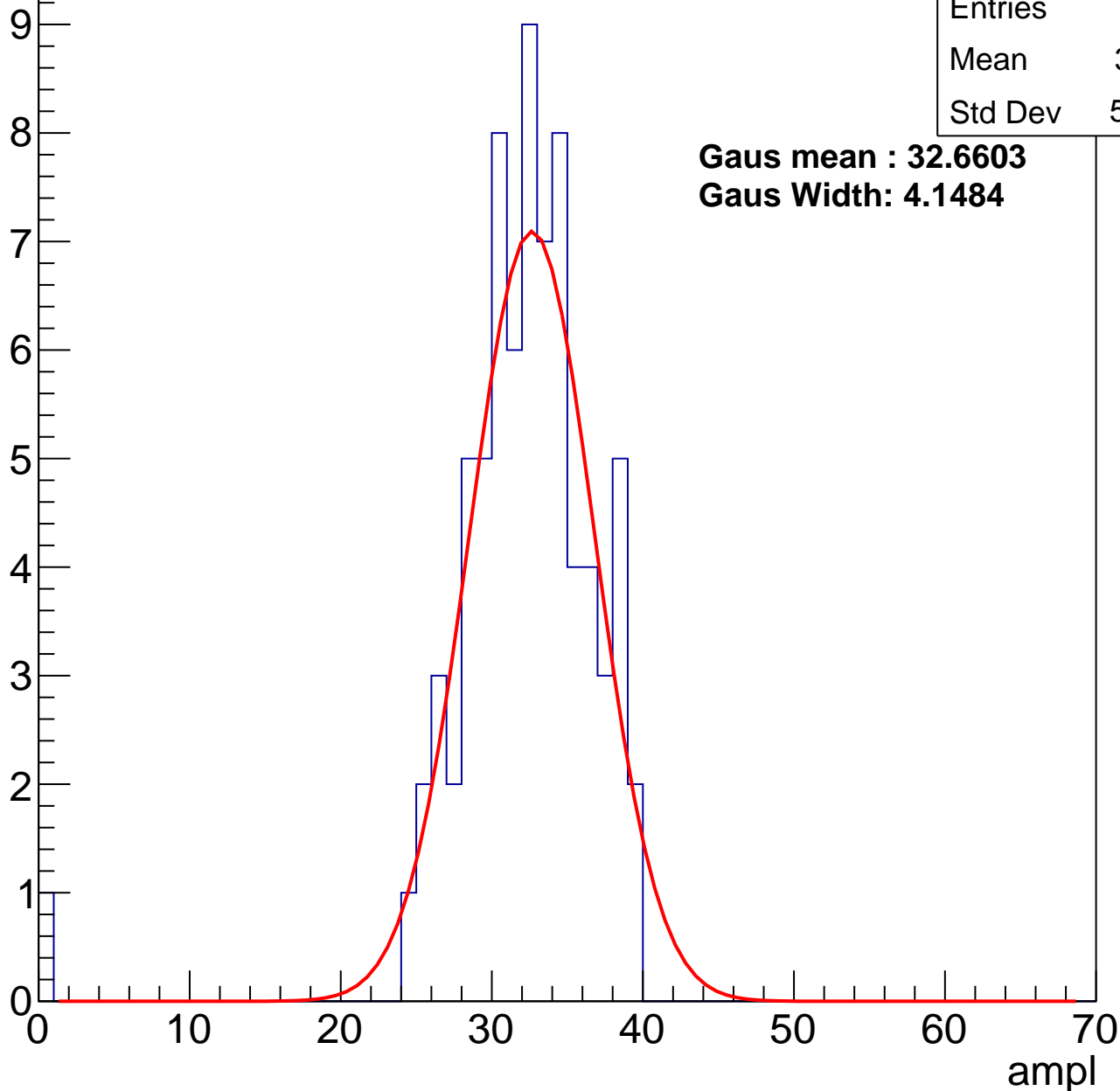
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	31.61
Std Dev	5.148

Gaus mean : 32.6603

Gaus Width: 4.1484



B1L101S, U25-ch49, adc1

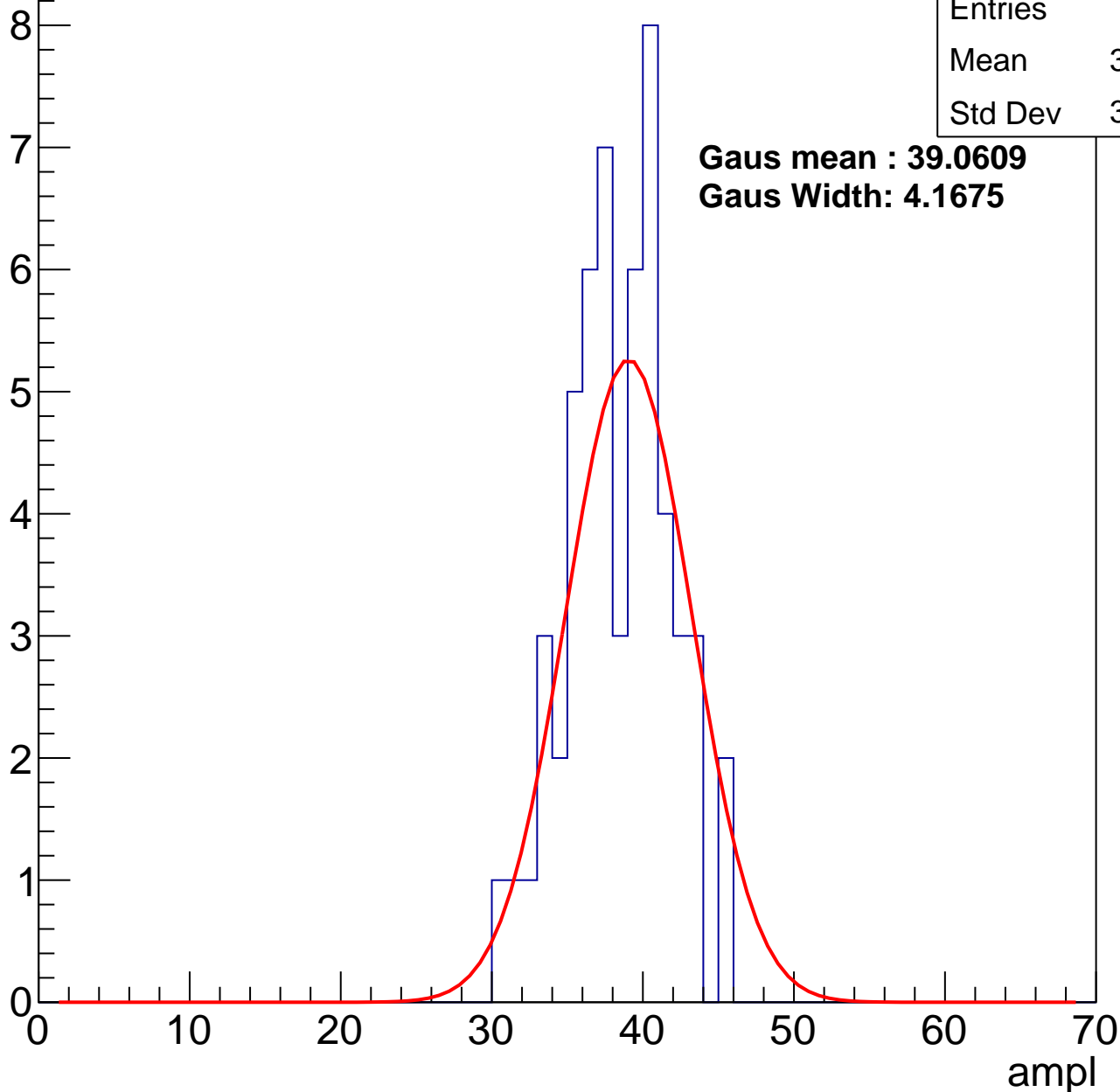
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	37.95
Std Dev	3.376

Gaus mean : 39.0609

Gaus Width: 4.1675



B1L101S, U25-ch49, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	44.91
Std Dev	3.526

Gaus mean : 45.0913

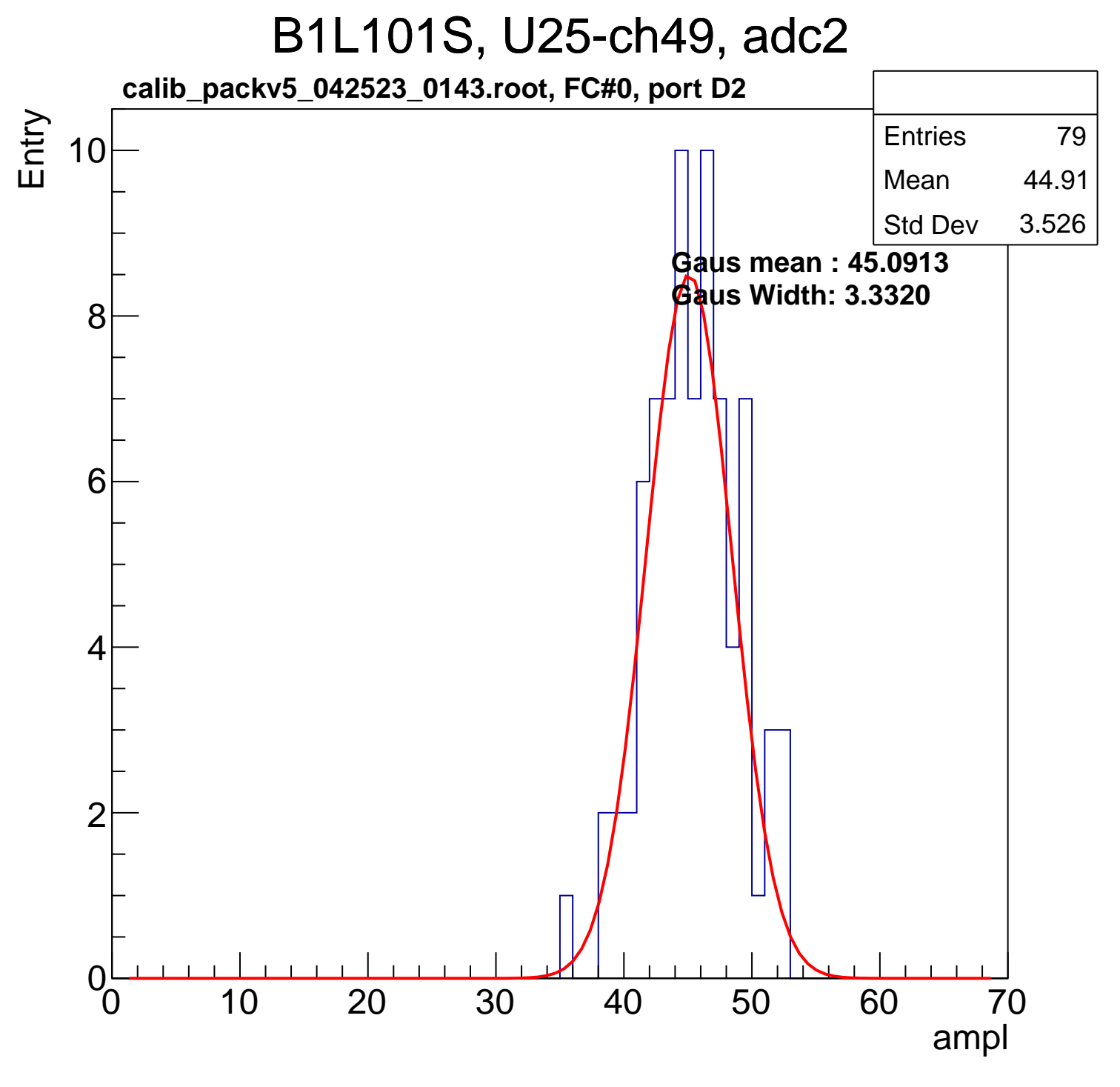
Gaus Width: 3.3320

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

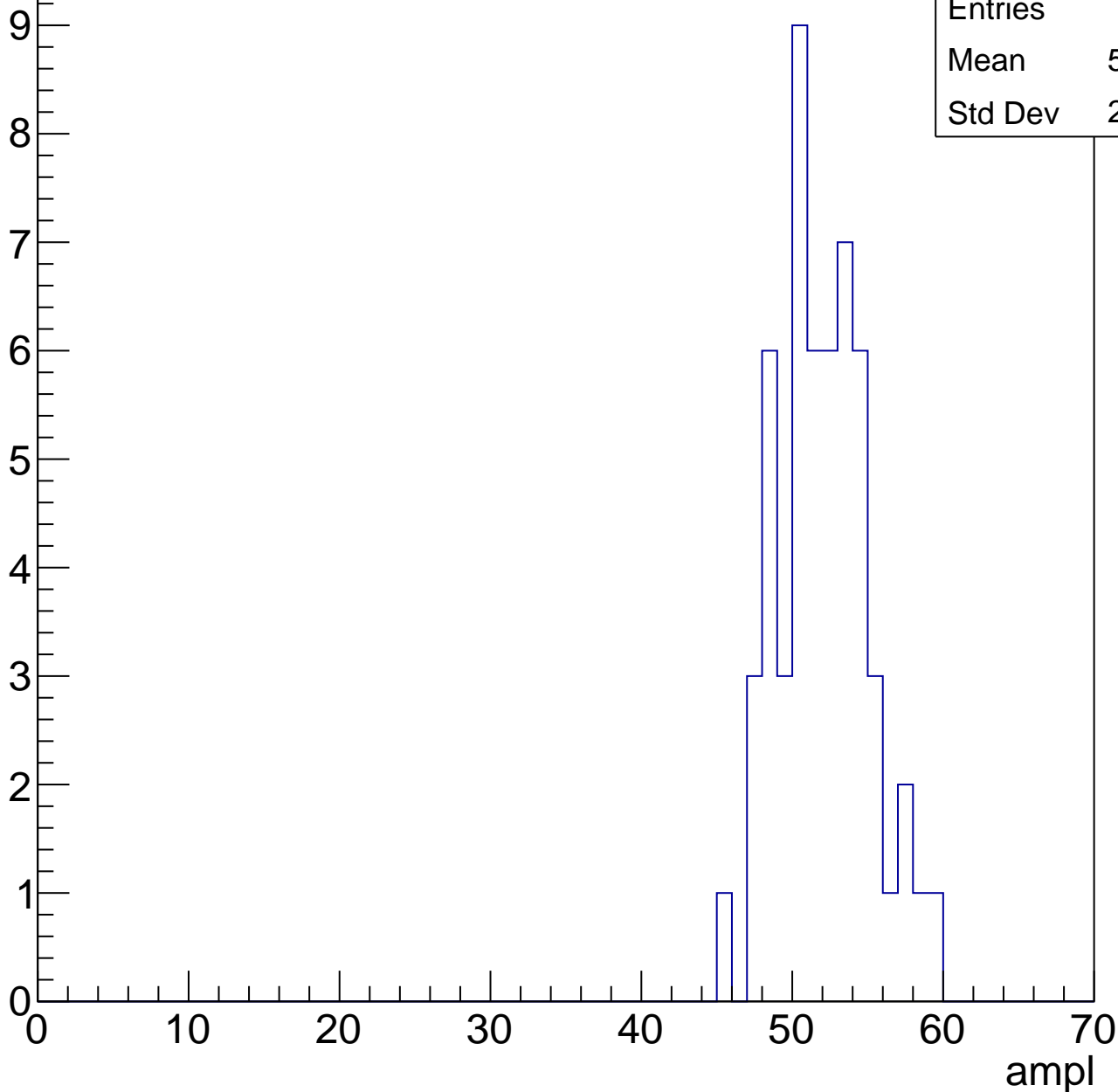


B1L101S, U25-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

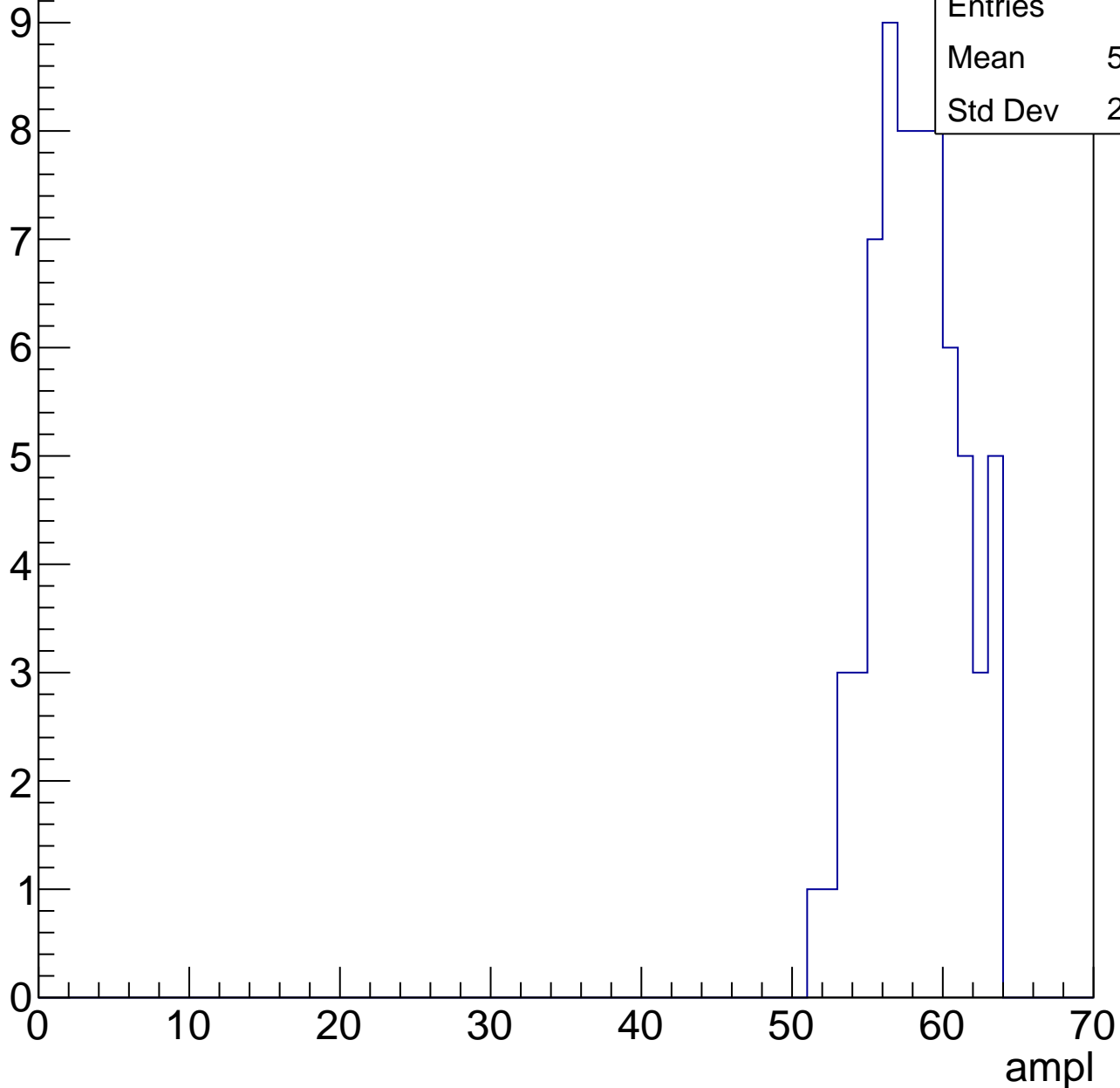
Entries	55
Mean	51.56
Std Dev	2.977



B1L101S, U25-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



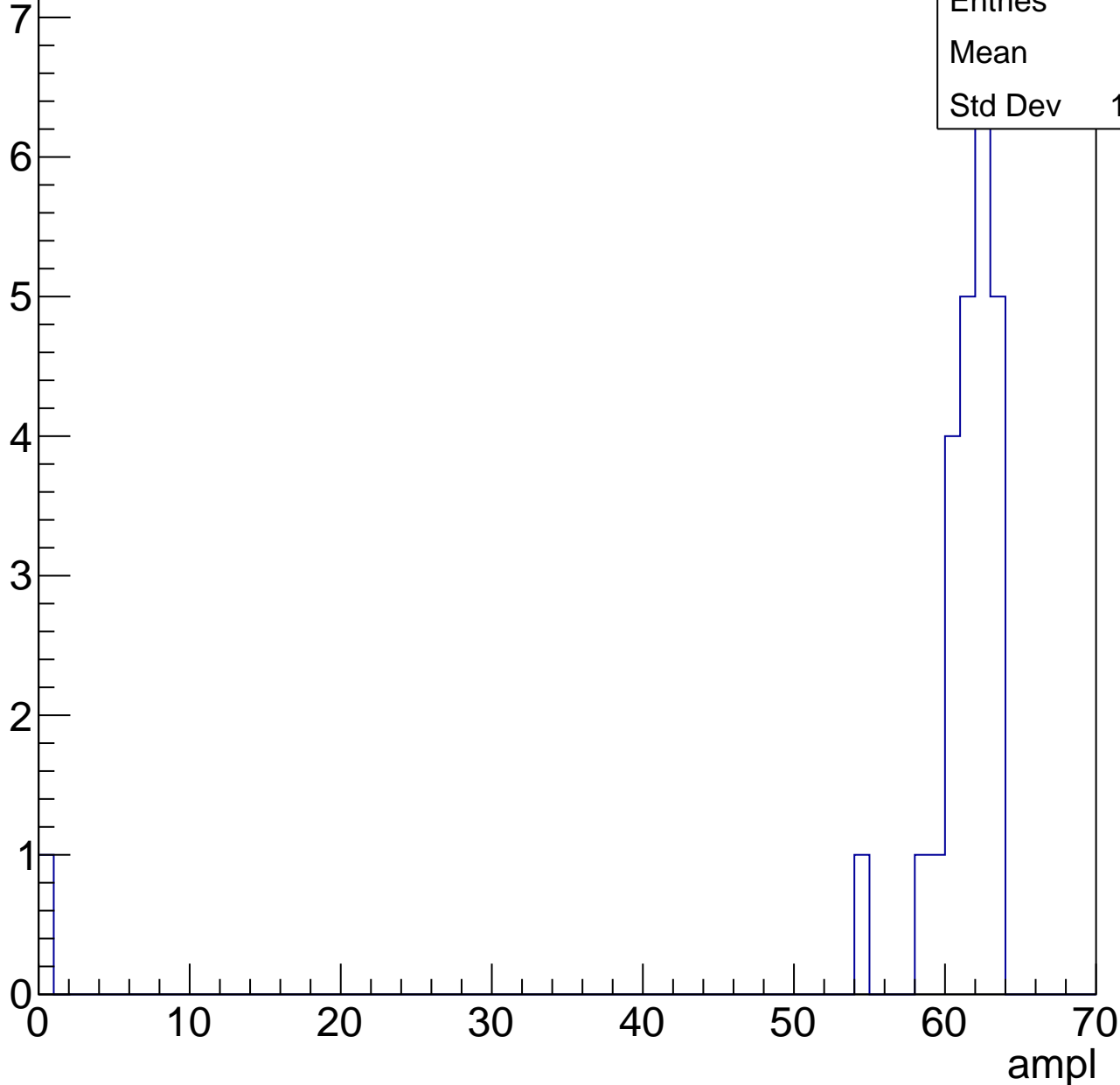
Entries	67
Mean	57.78
Std Dev	2.895

B1L101S, U25-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.6
Std Dev	12.12



B1L101S, U25-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch50, adc0

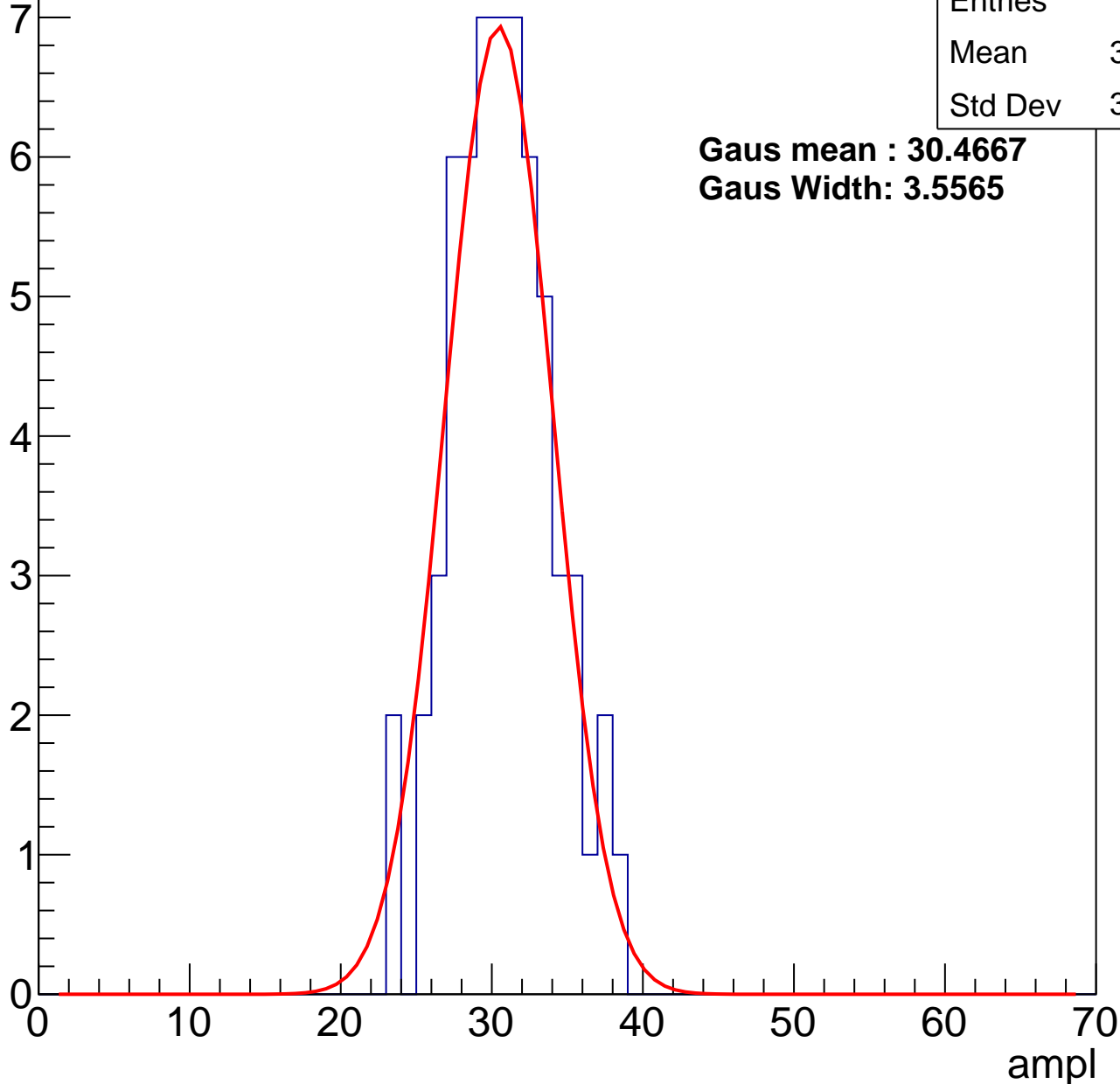
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	30.26
Std Dev	3.324

Gaus mean : 30.4667

Gaus Width: 3.5565



B1L101S, U25-ch50, adc1

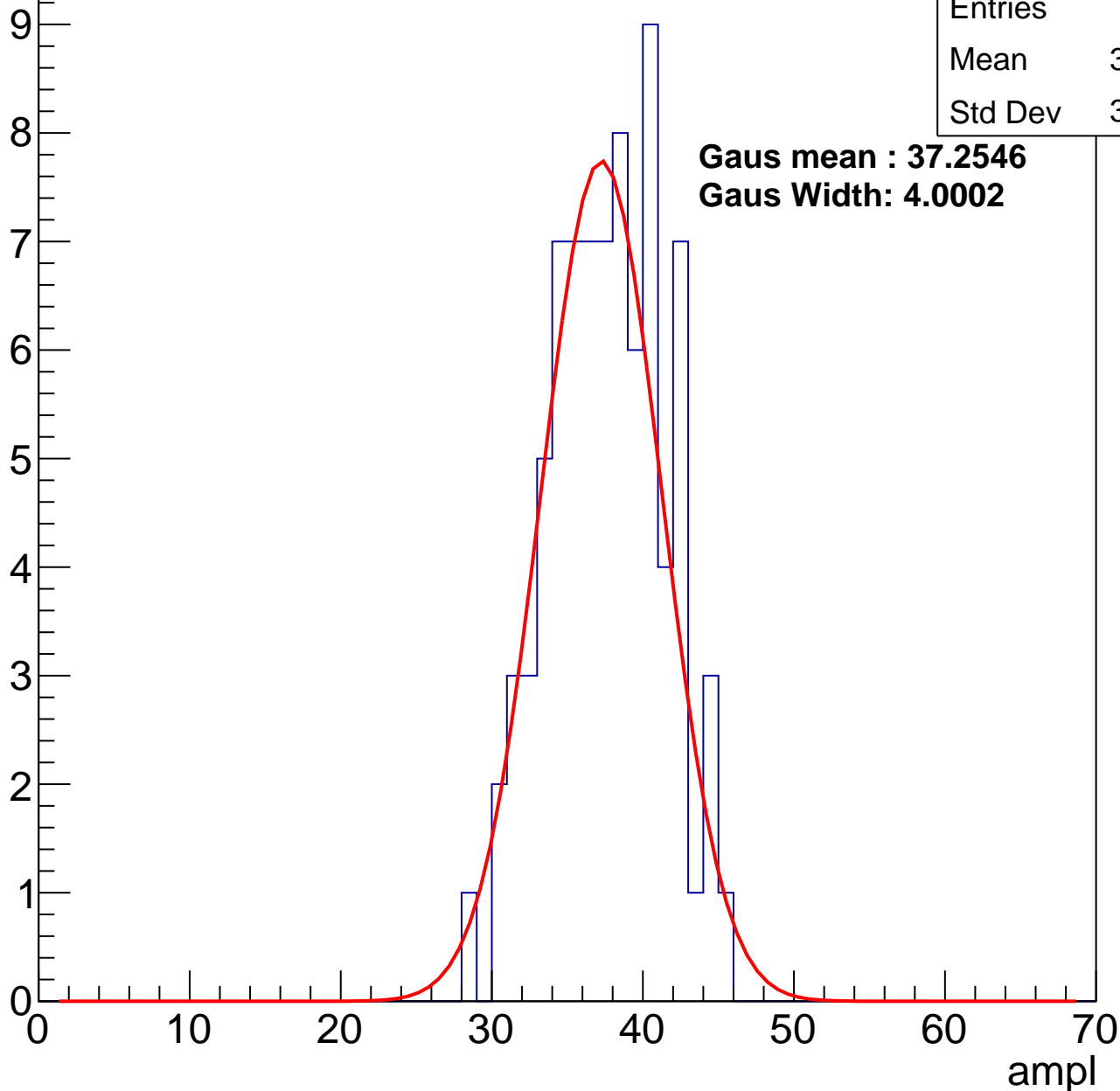
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.19
Std Dev	3.745

Gaus mean : 37.2546

Gaus Width: 4.0002



B1L101S, U25-ch50, adc2

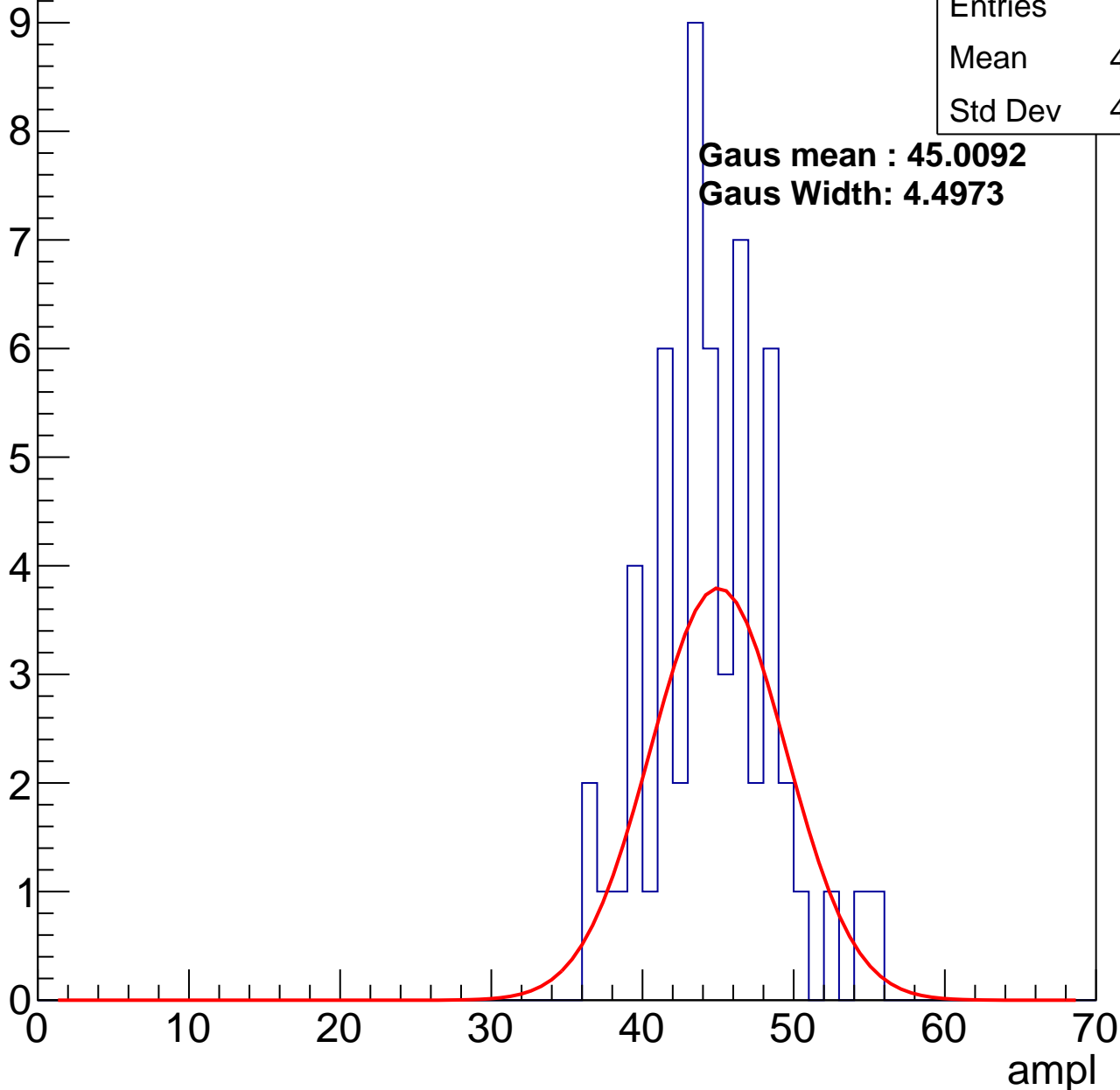
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	44.14
Std Dev	4.042

Gaus mean : 45.0092

Gaus Width: 4.4973



B1L101S, U25-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	50.72
Std Dev	3.817

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

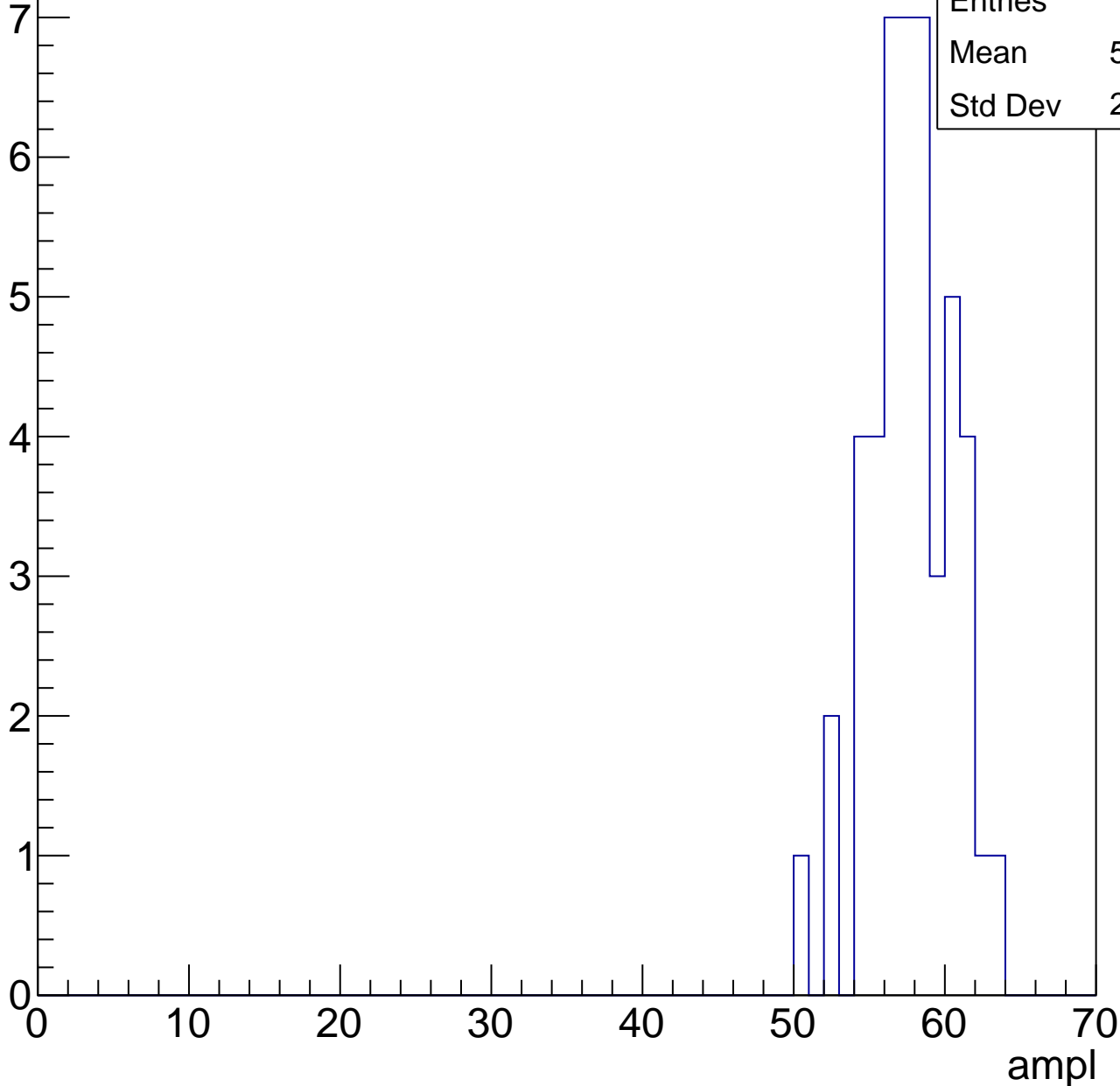
70

B1L101S, U25-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	57.24
Std Dev	2.736

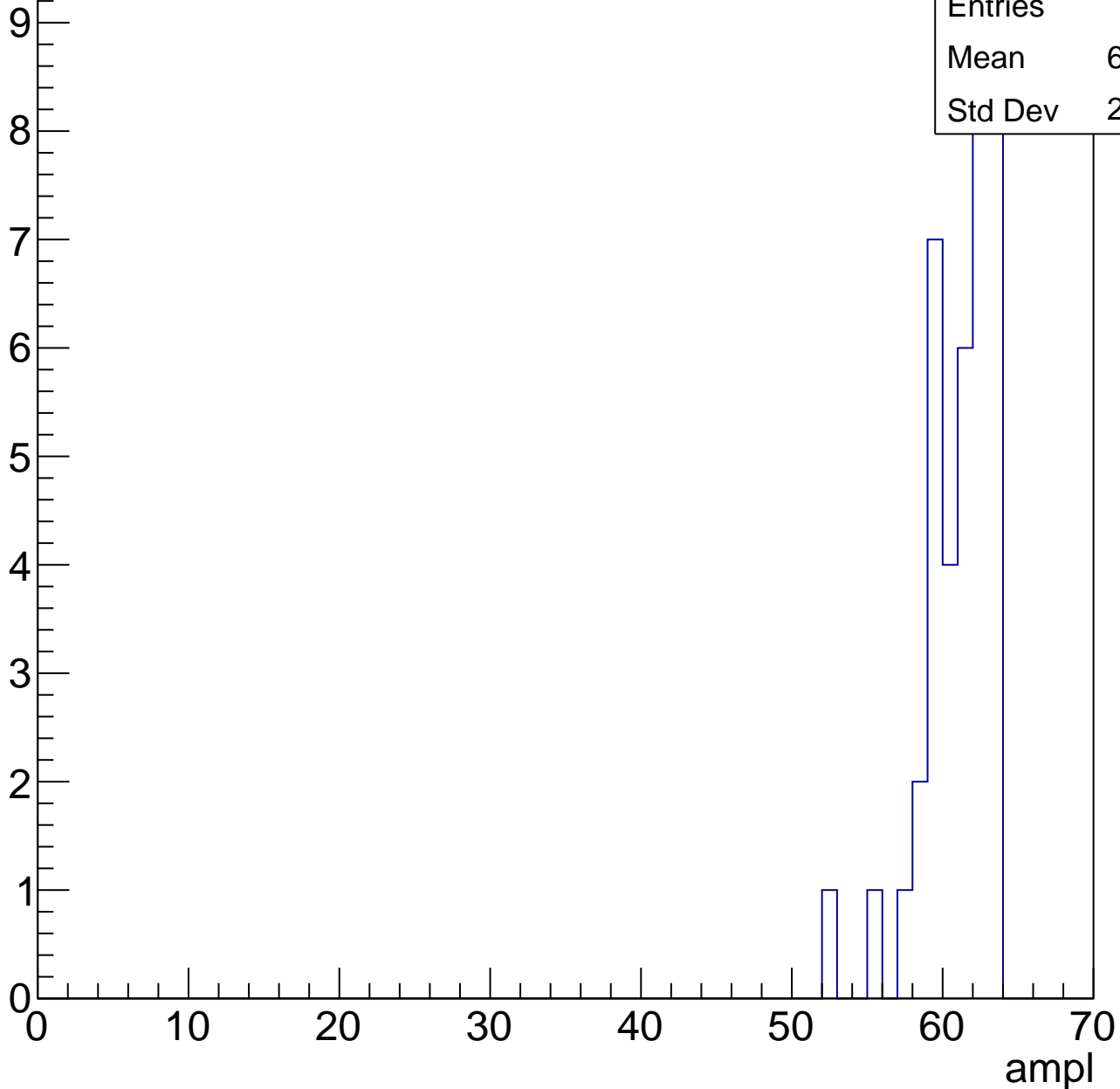


B1L101S, U25-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	60.54
Std Dev	2.352



B1L101S, U25-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch51, adc0

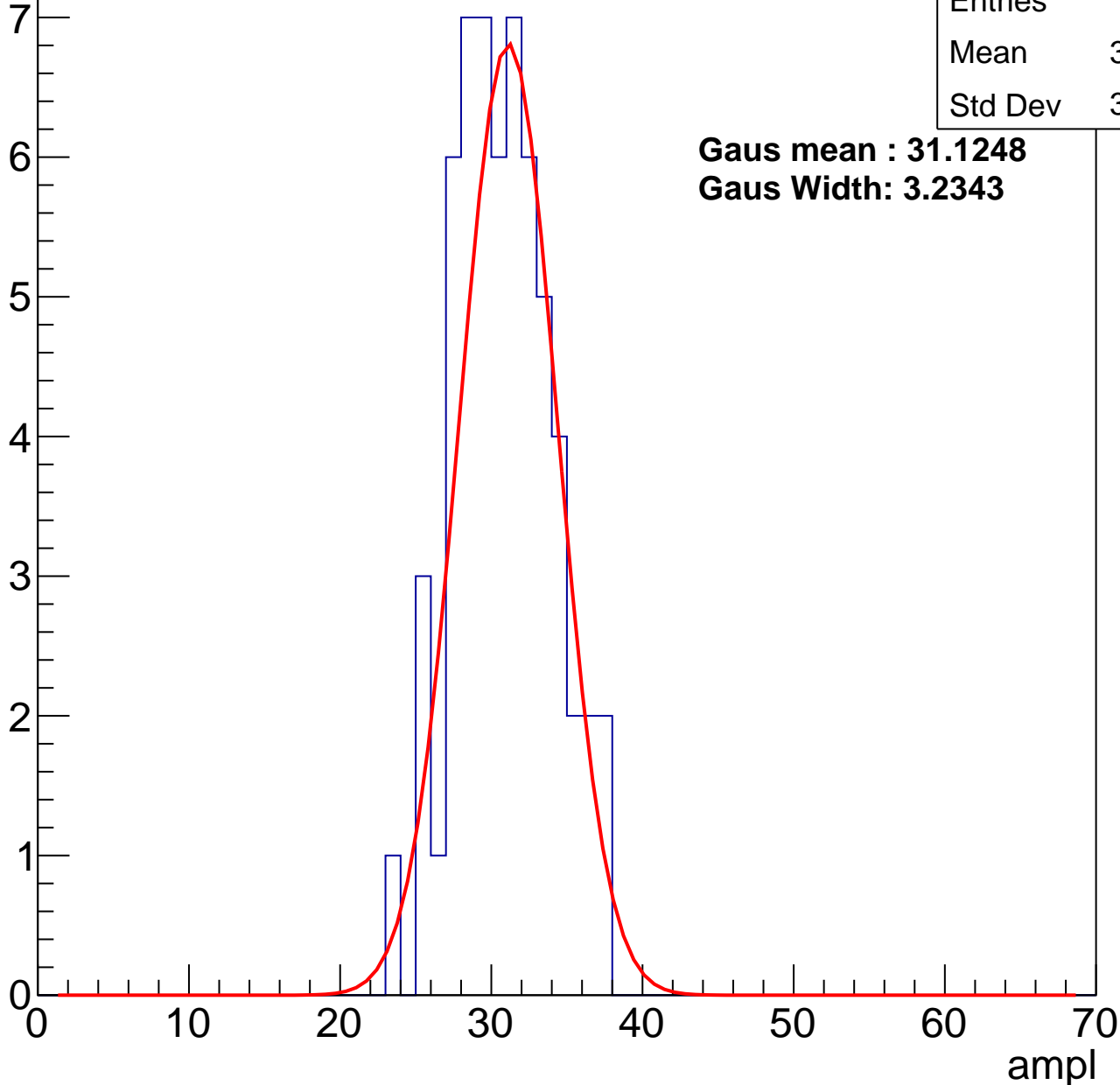
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	30.36
Std Dev	3.139

Gaus mean : 31.1248

Gaus Width: 3.2343



B1L101S, U25-ch51, adc1

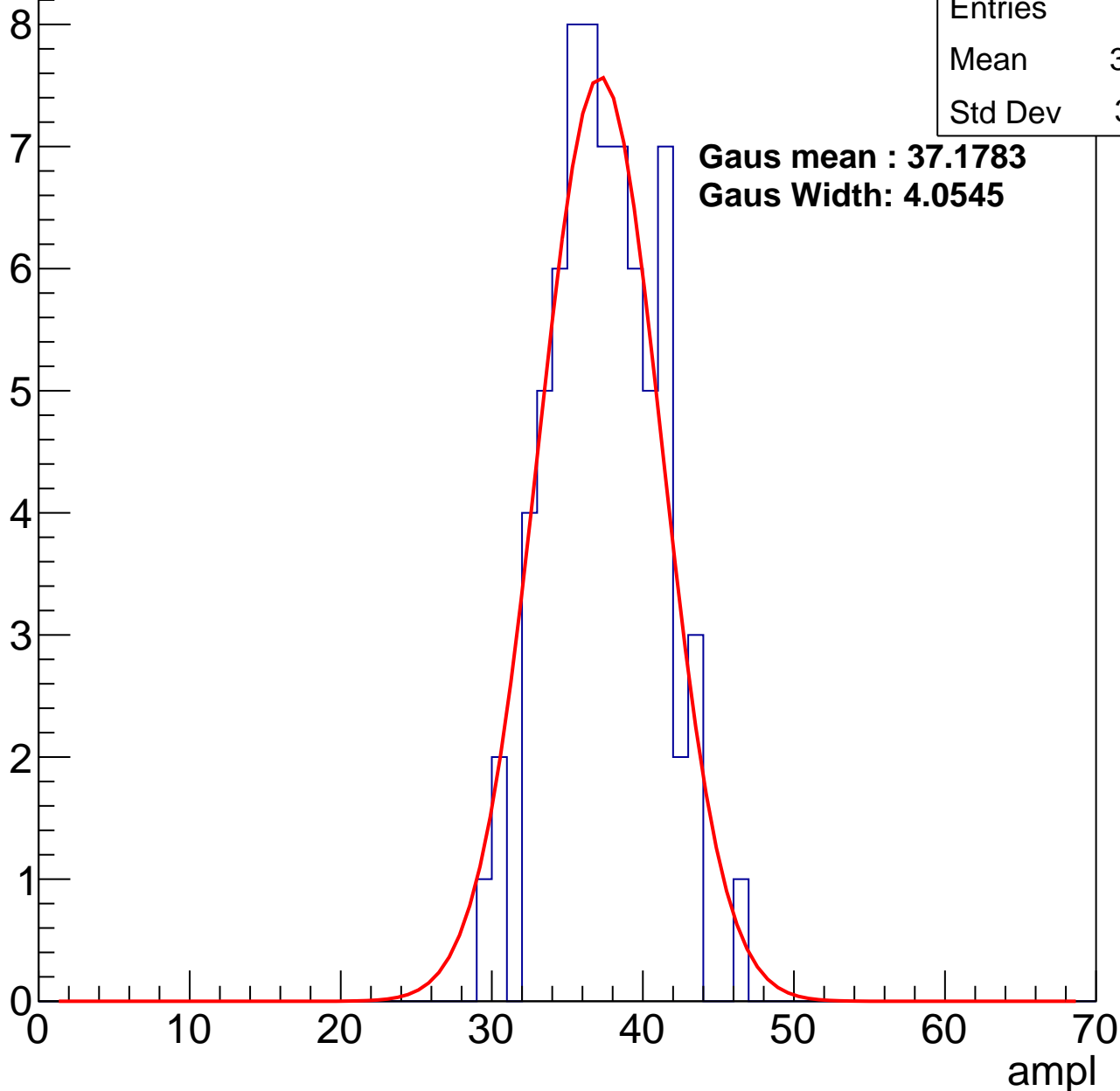
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.93
Std Dev	3.461

Gaus mean : 37.1783

Gaus Width: 4.0545



B1L101S, U25-ch51, adc2

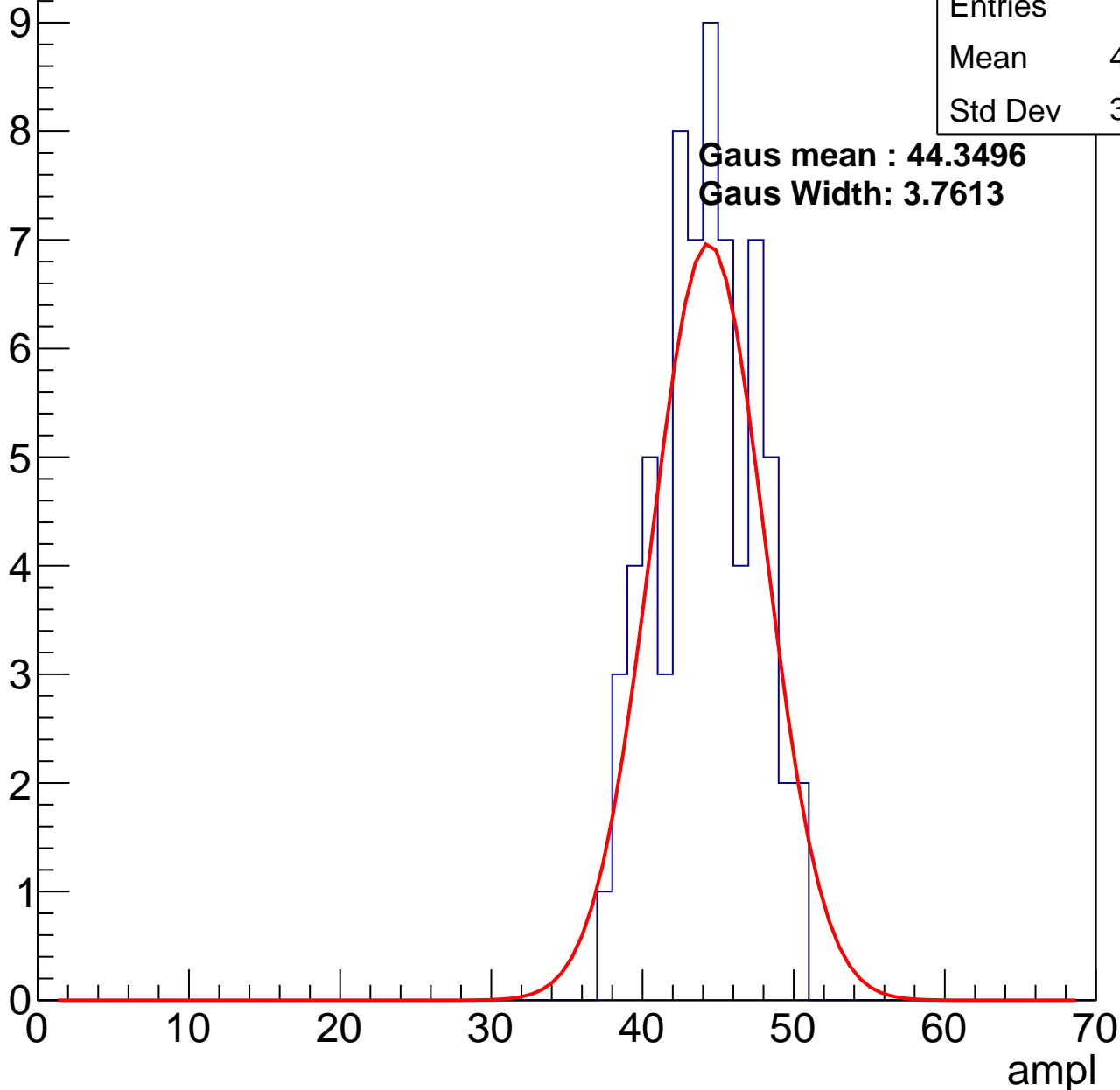
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.72
Std Dev	3.199

Gaus mean : 44.3496

Gaus Width: 3.7613

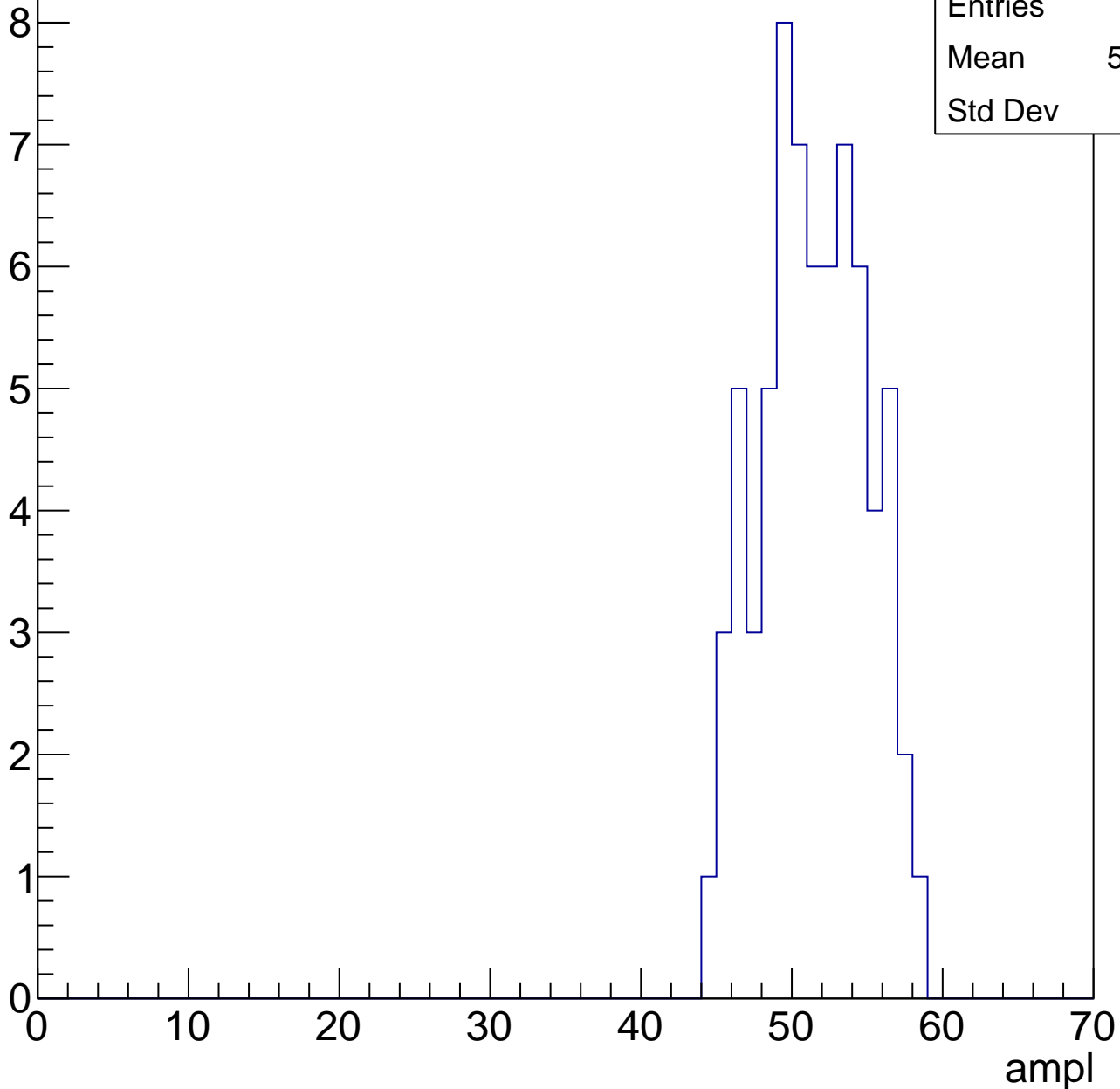


B1L101S, U25-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.97
Std Dev	3.43

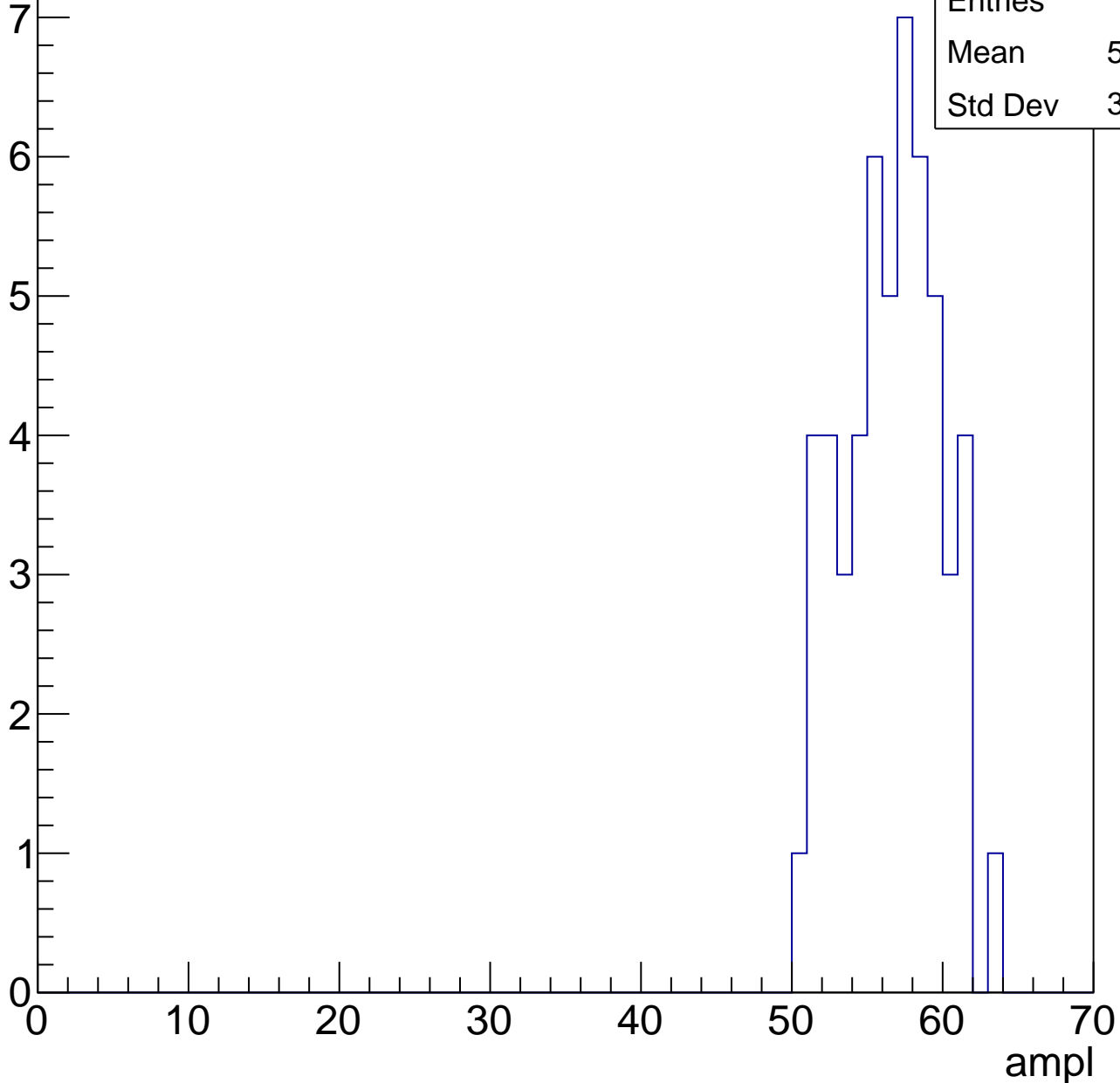


B1L101S, U25-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.15
Std Dev	3.135

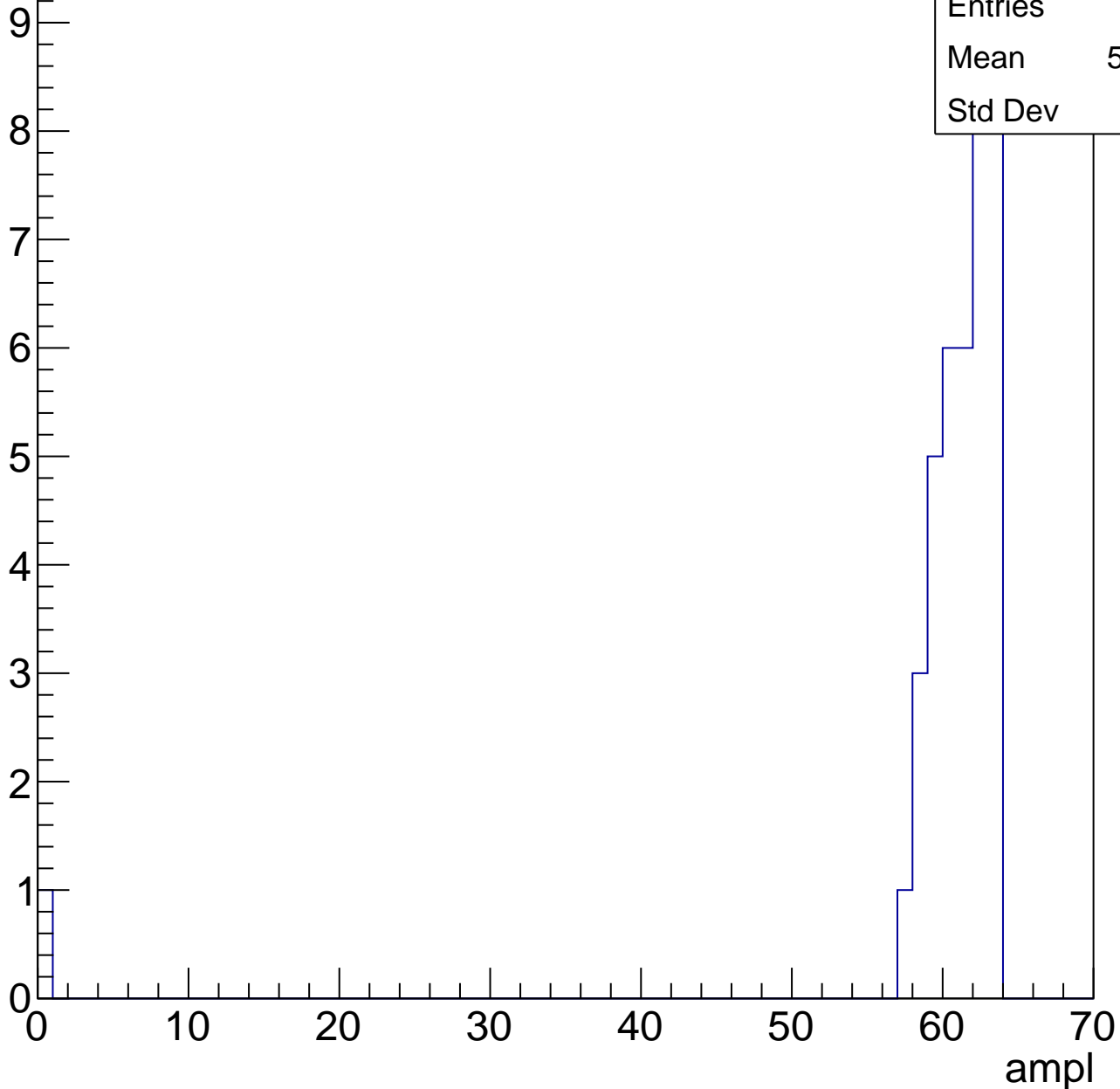


B1L101S, U25-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	59.33
Std Dev	9.77



B1L101S, U25-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch52, adc0

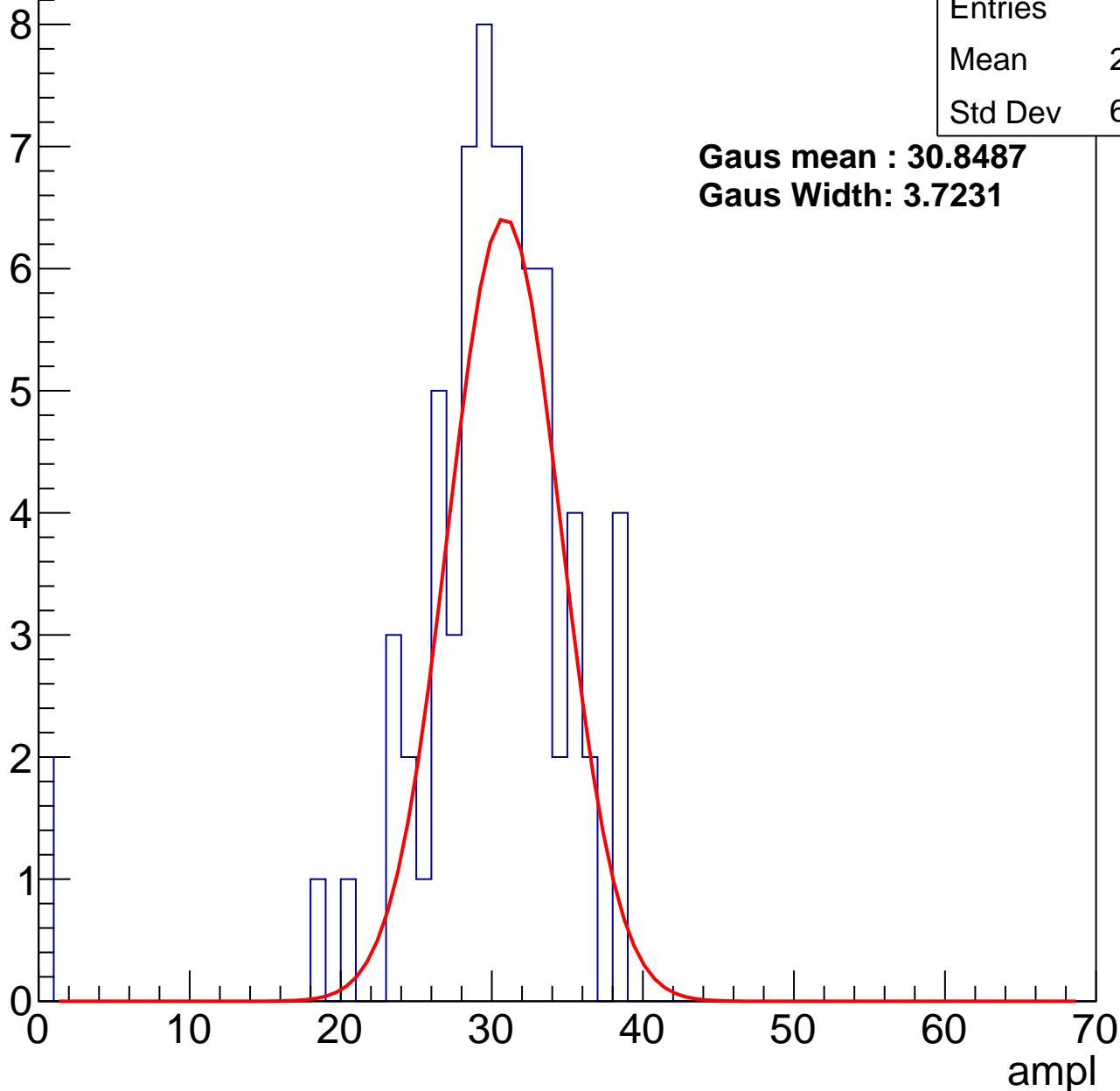
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.13
Std Dev	6.415

Gaus mean : 30.8487

Gaus Width: 3.7231



B1L101S, U25-ch52, adc1

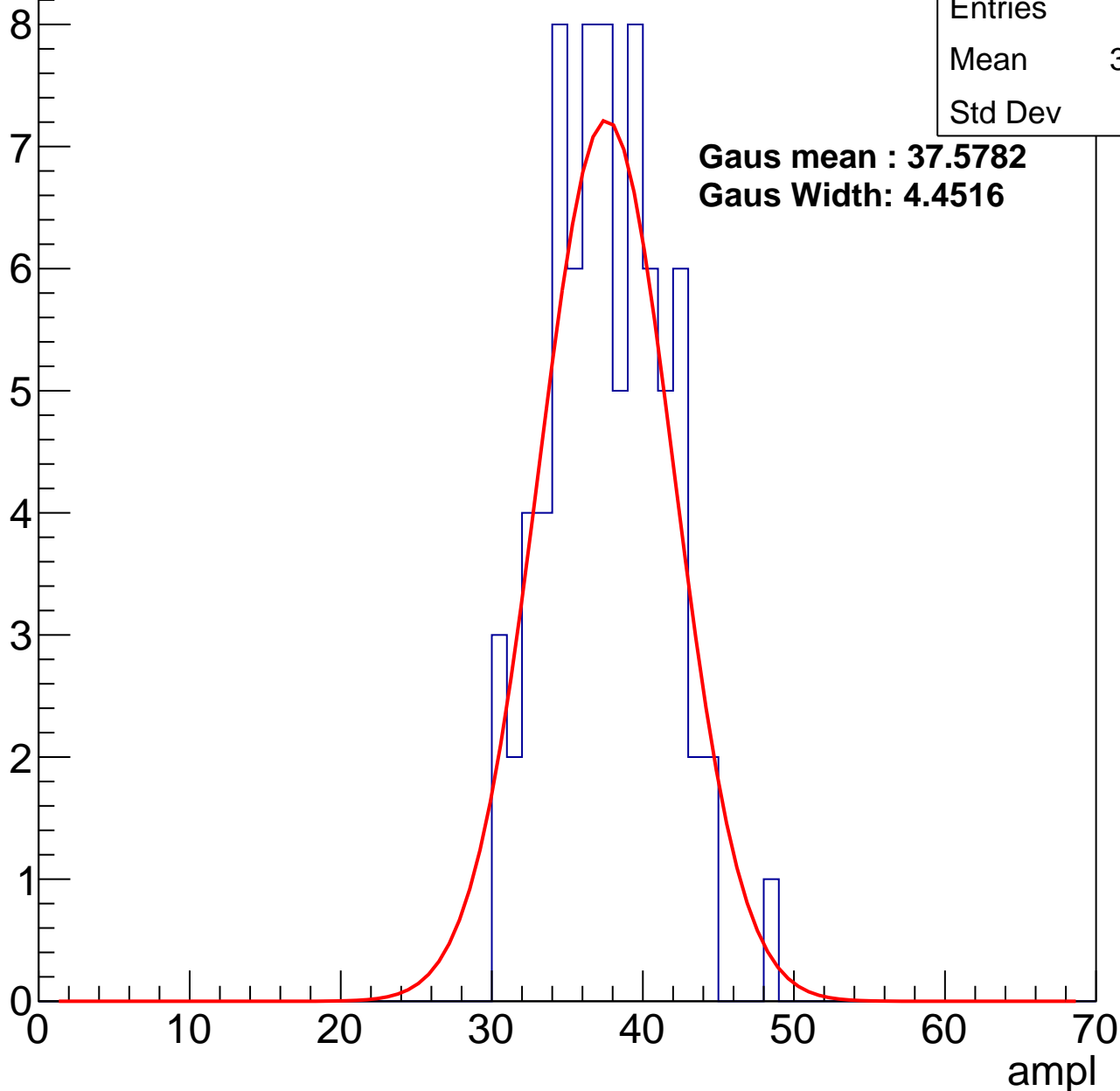
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	37.17
Std Dev	3.75

Gaus mean : 37.5782

Gaus Width: 4.4516



B1L101S, U25-ch52, adc2

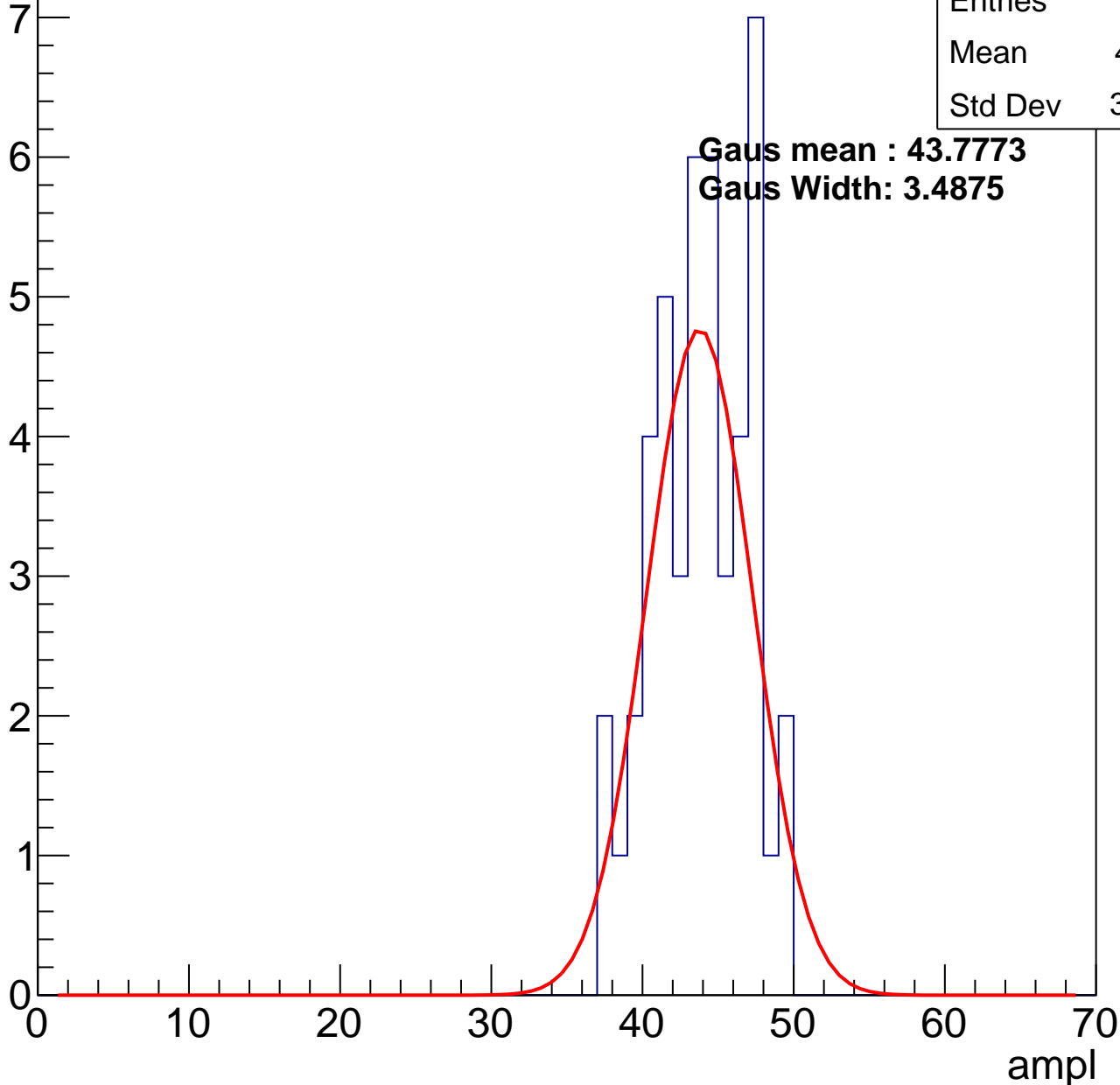
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	43.41
Std Dev	3.104

Gaus mean : 43.7773

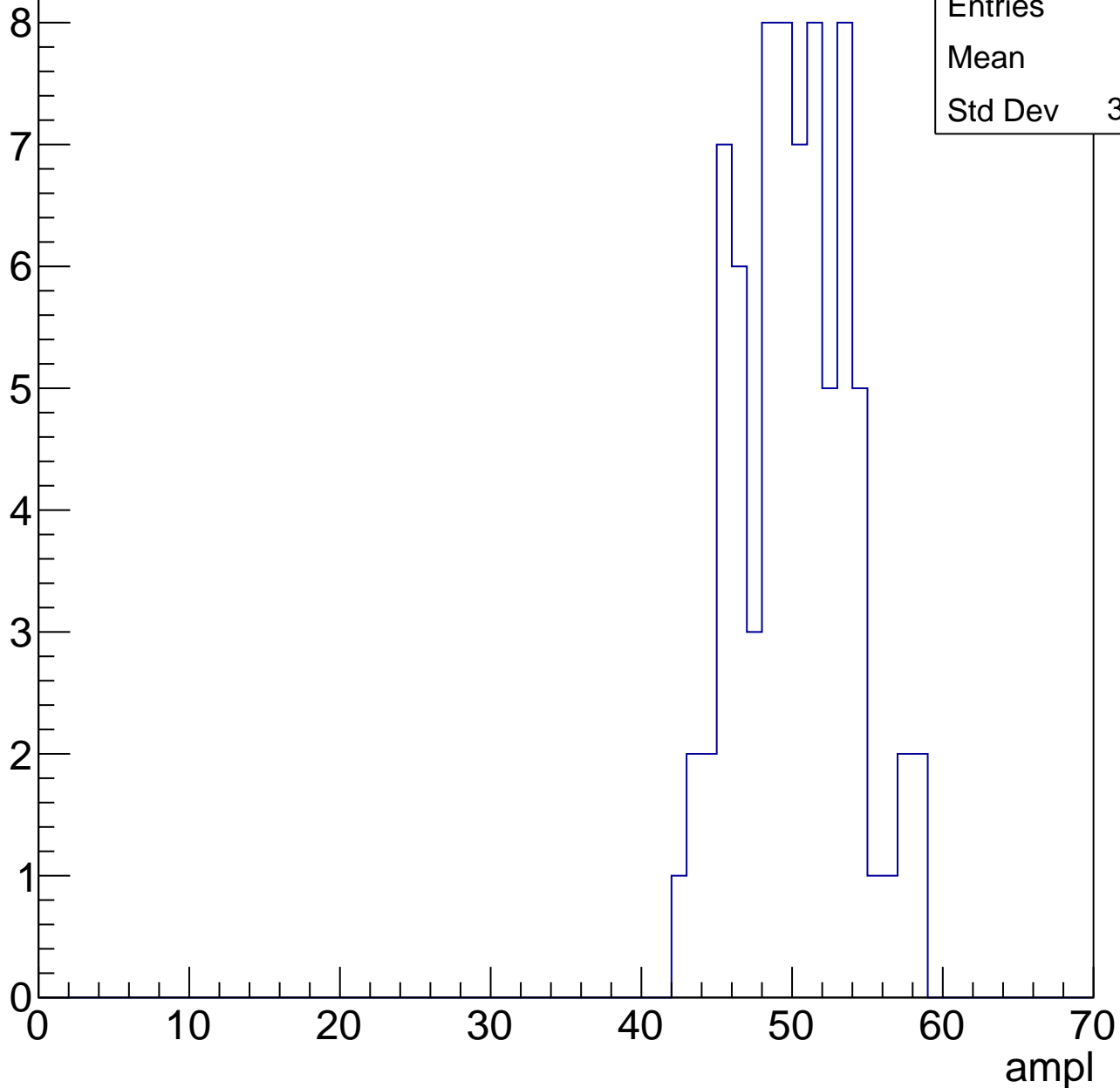
Gaus Width: 3.4875



B1L101S, U25-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



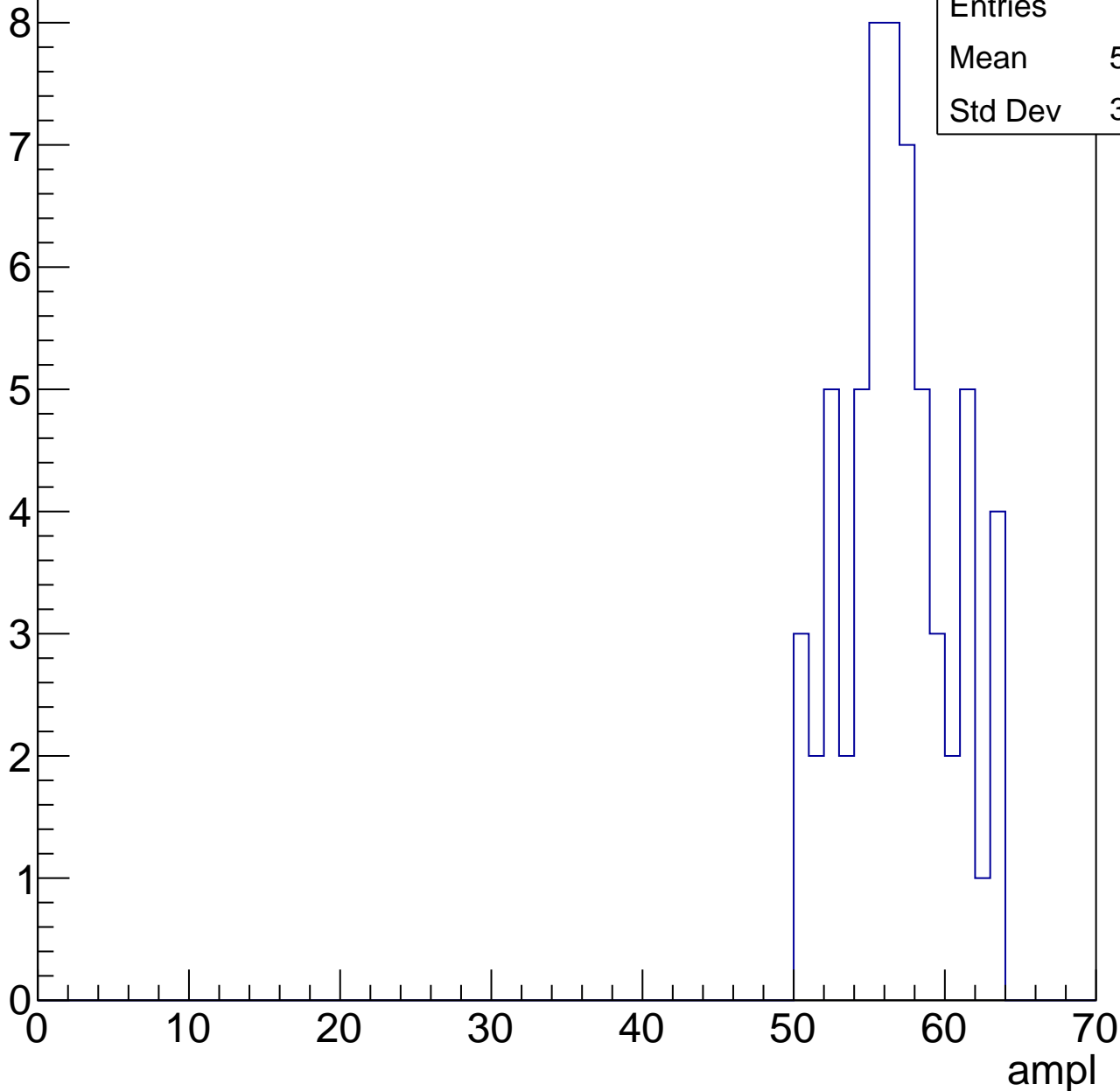
Entries	76
Mean	49.7
Std Dev	3.685

B1L101S, U25-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

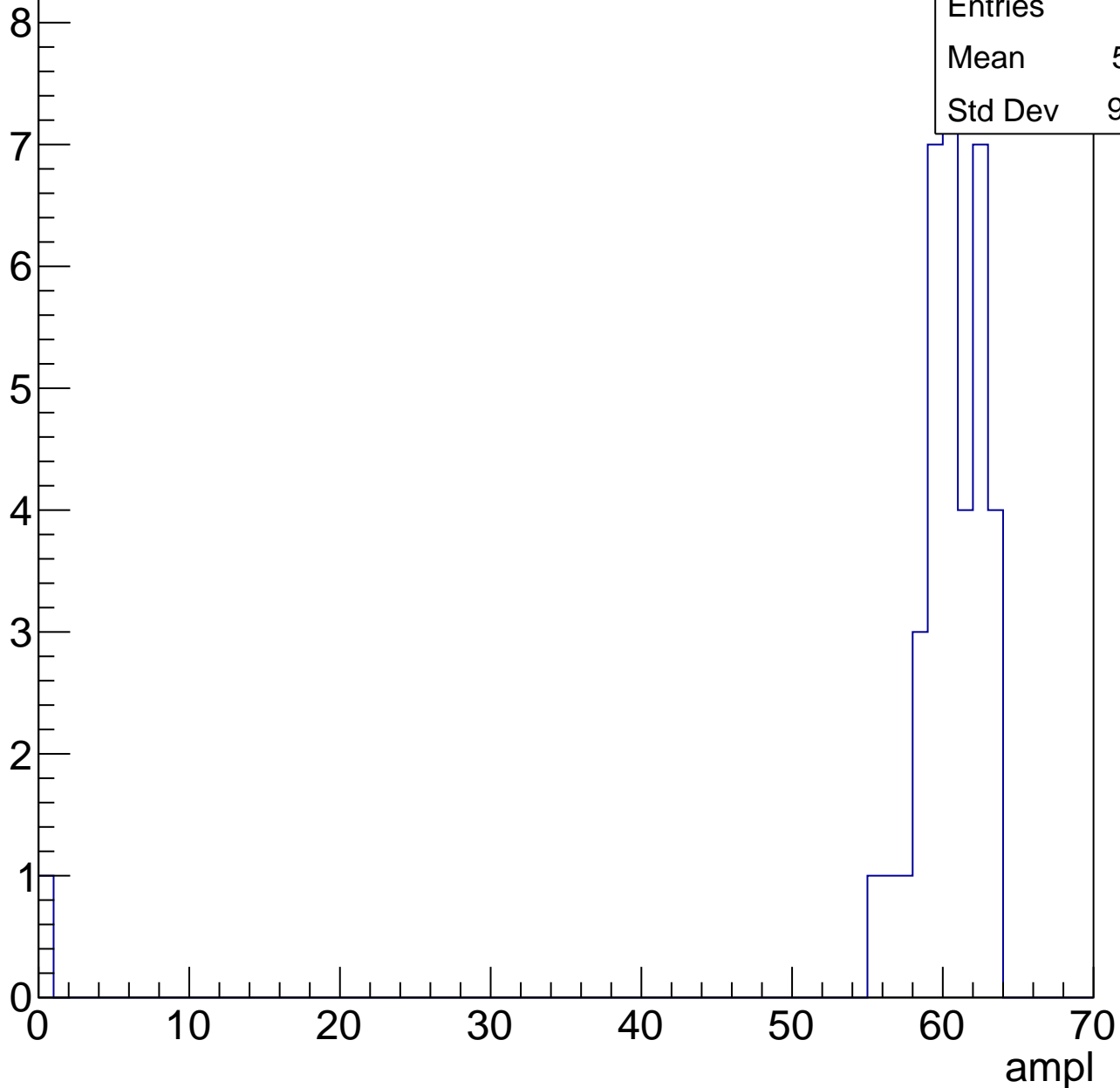
Entries	60
Mean	56.35
Std Dev	3.463



B1L101S, U25-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

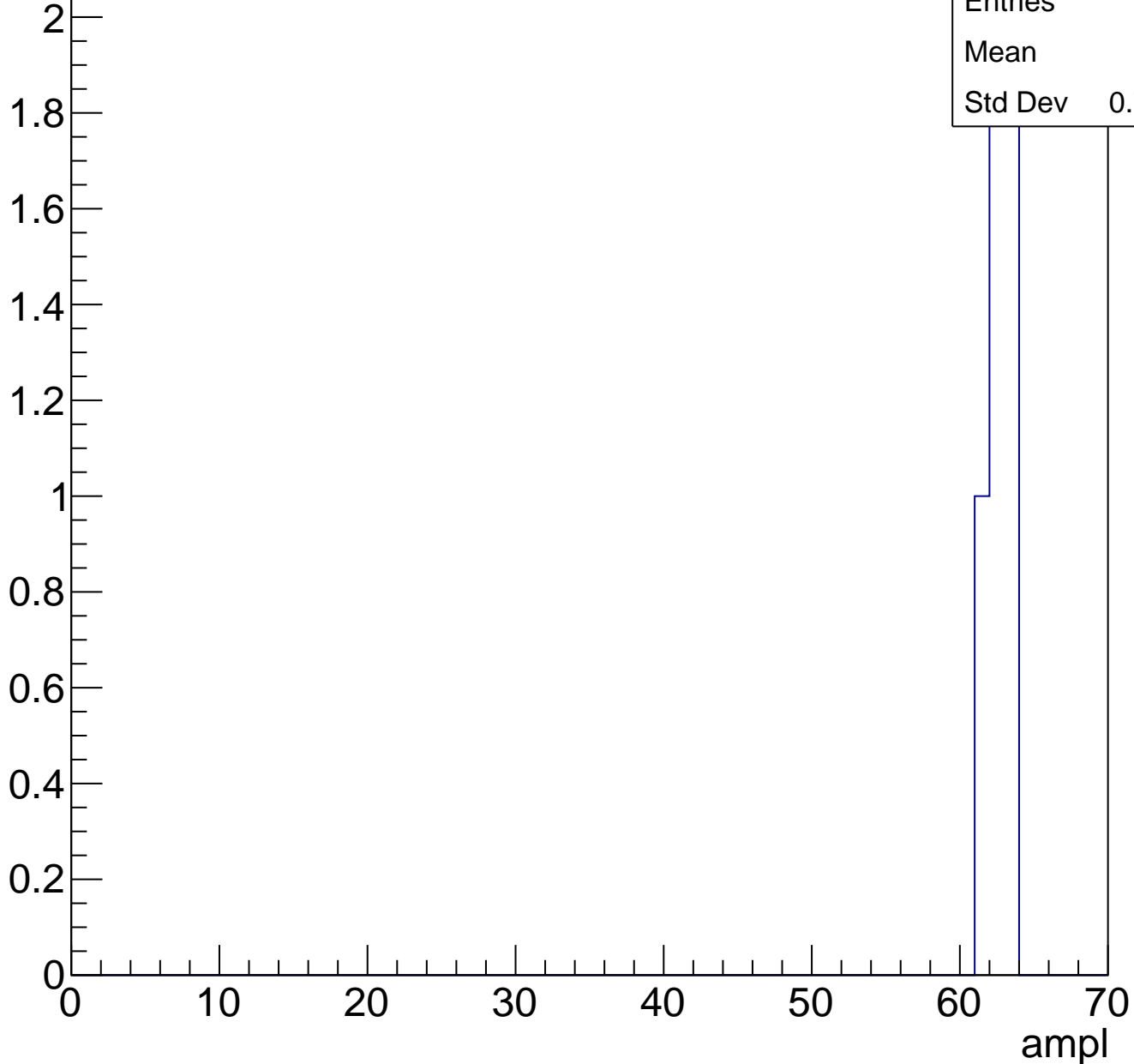
Entry



B1L101S, U25-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch53, adc0

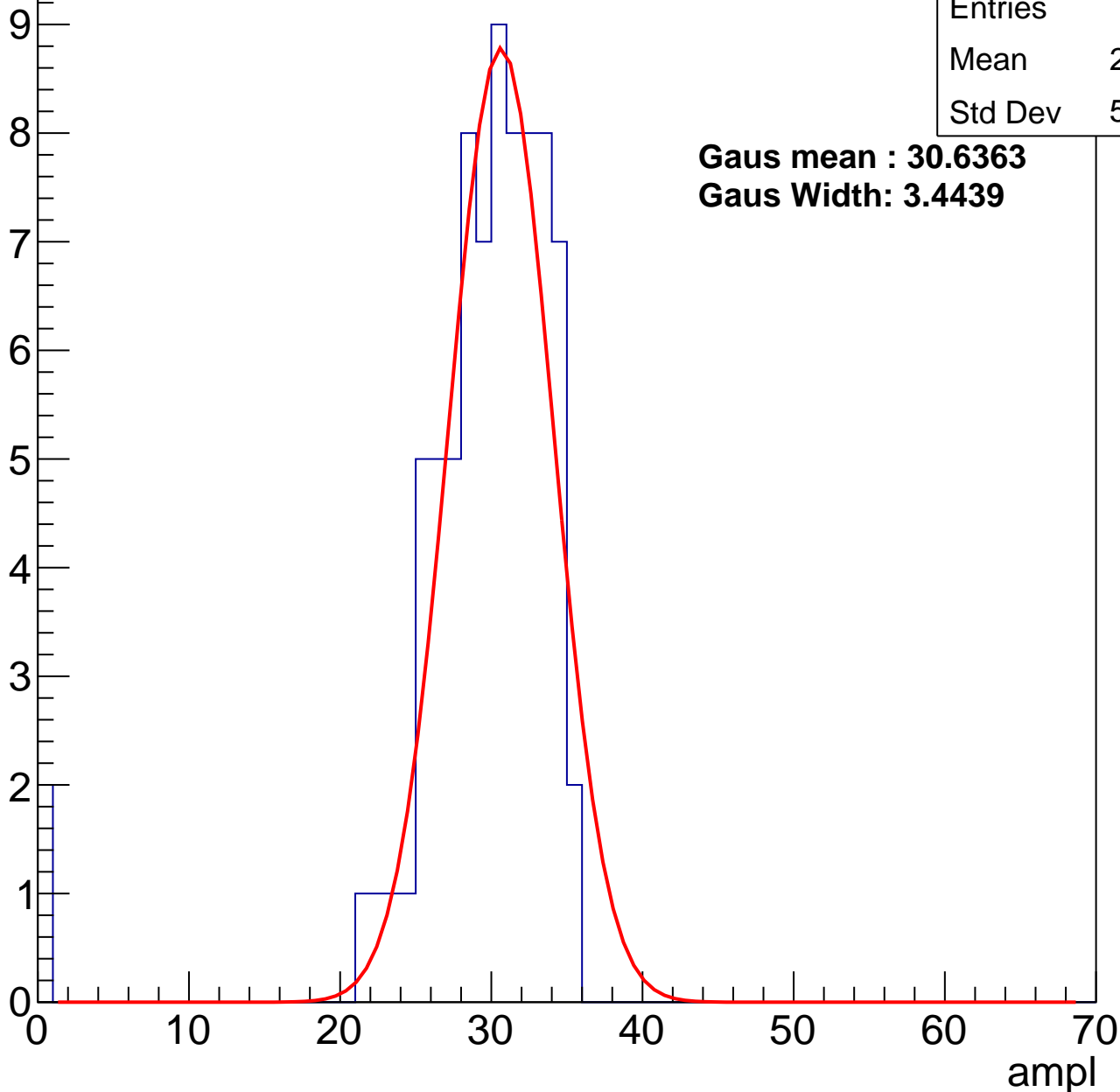
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.88
Std Dev	5.657

Gaus mean : 30.6363

Gaus Width: 3.4439



B1L101S, U25-ch53, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	37.85
Std Dev	3.439

Gaus mean : 38.1873

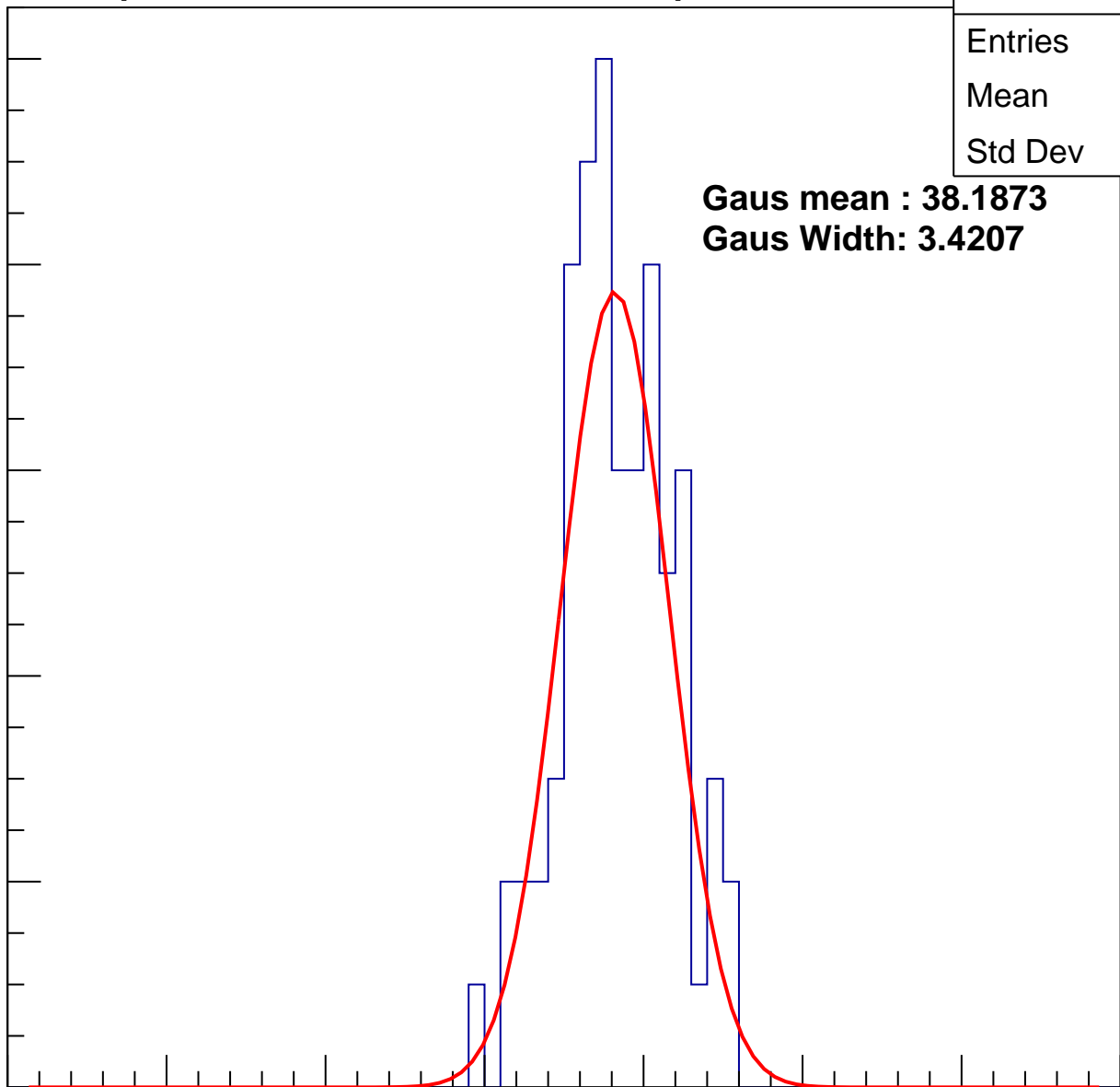
Gaus Width: 3.4207

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch53, adc2

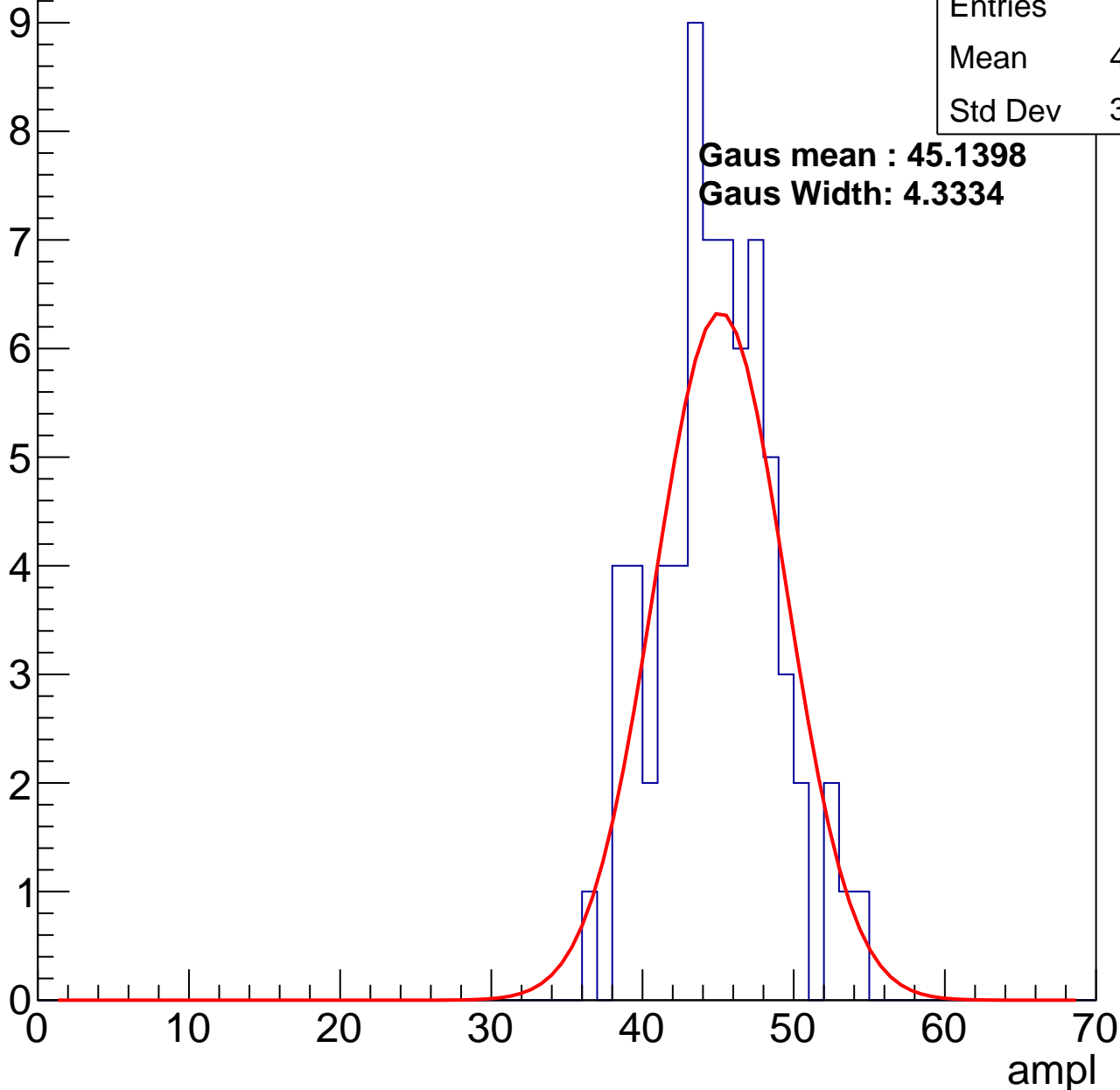
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.48
Std Dev	3.836

Gaus mean : 45.1398

Gaus Width: 4.3334

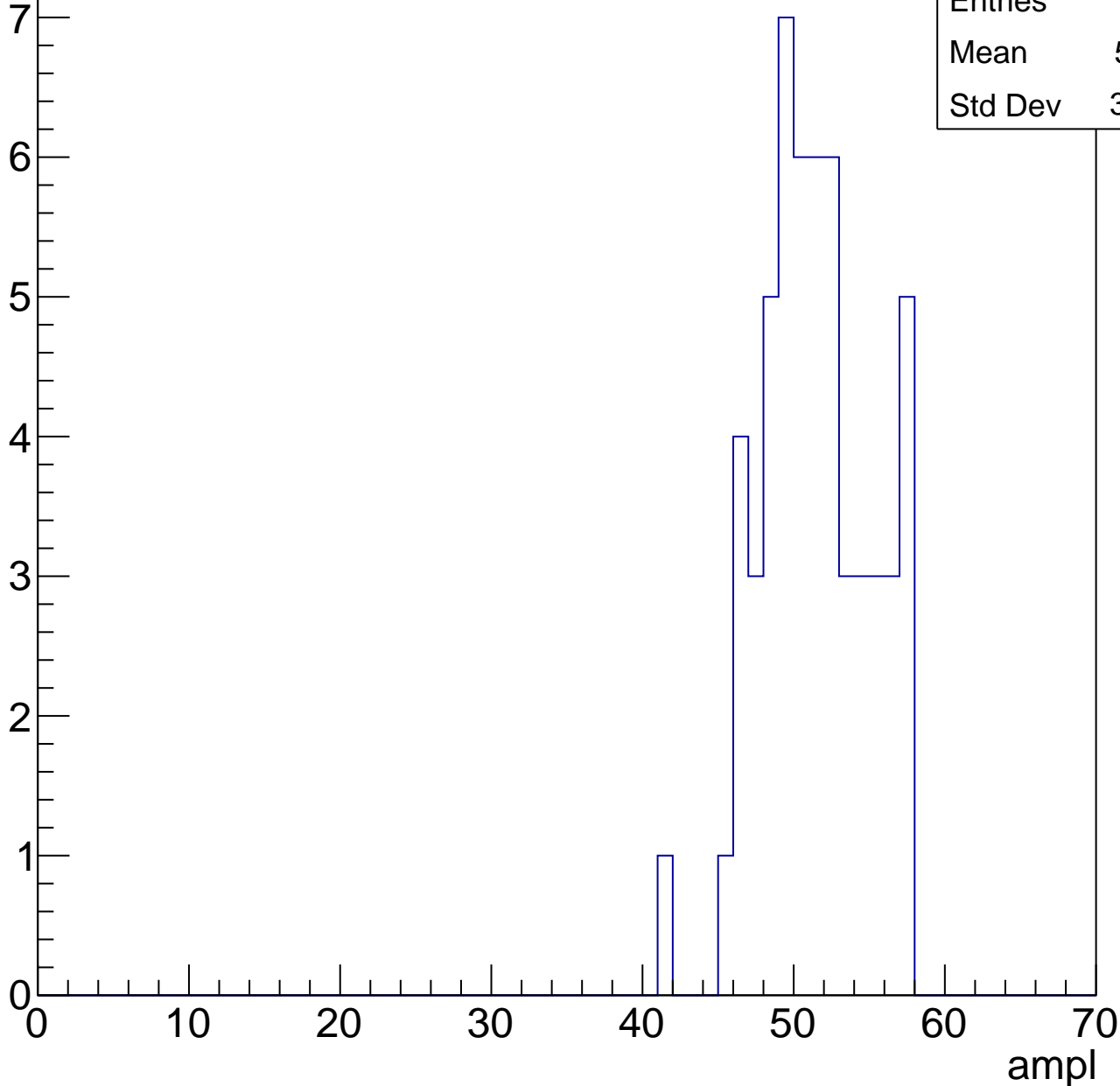


B1L101S, U25-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

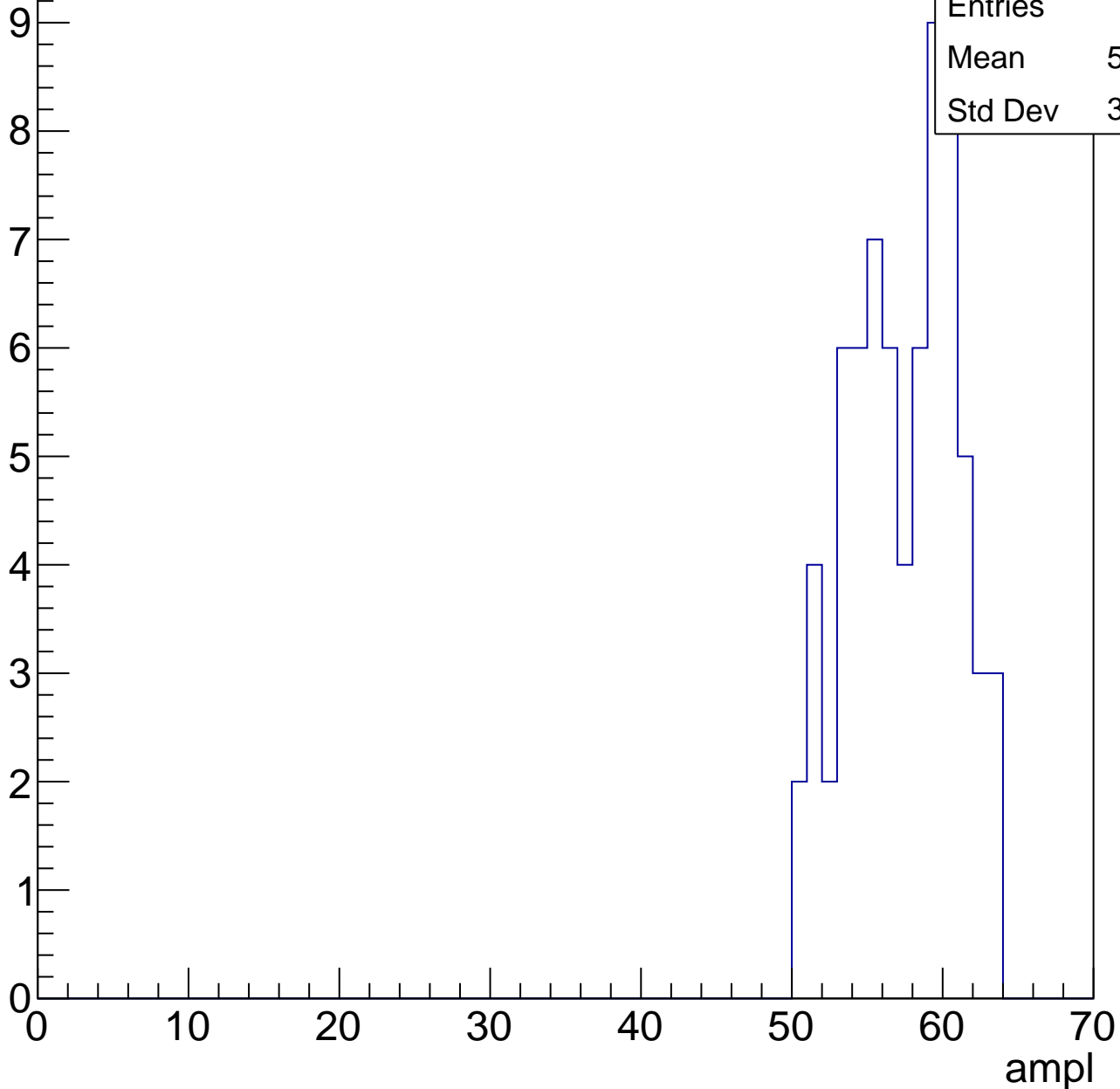
Entries	56
Mean	50.91
Std Dev	3.562



B1L101S, U25-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

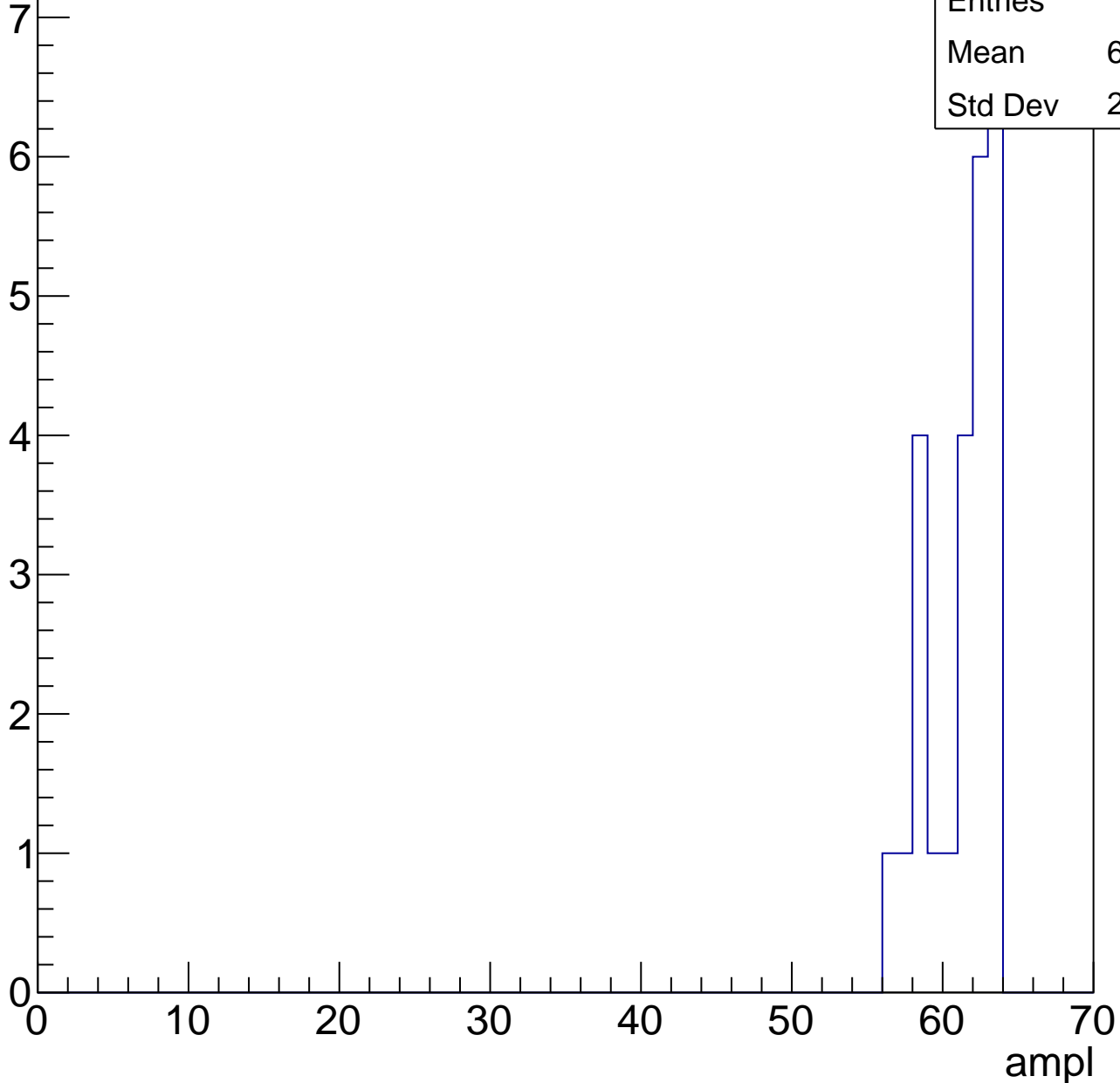


B1L101S, U25-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

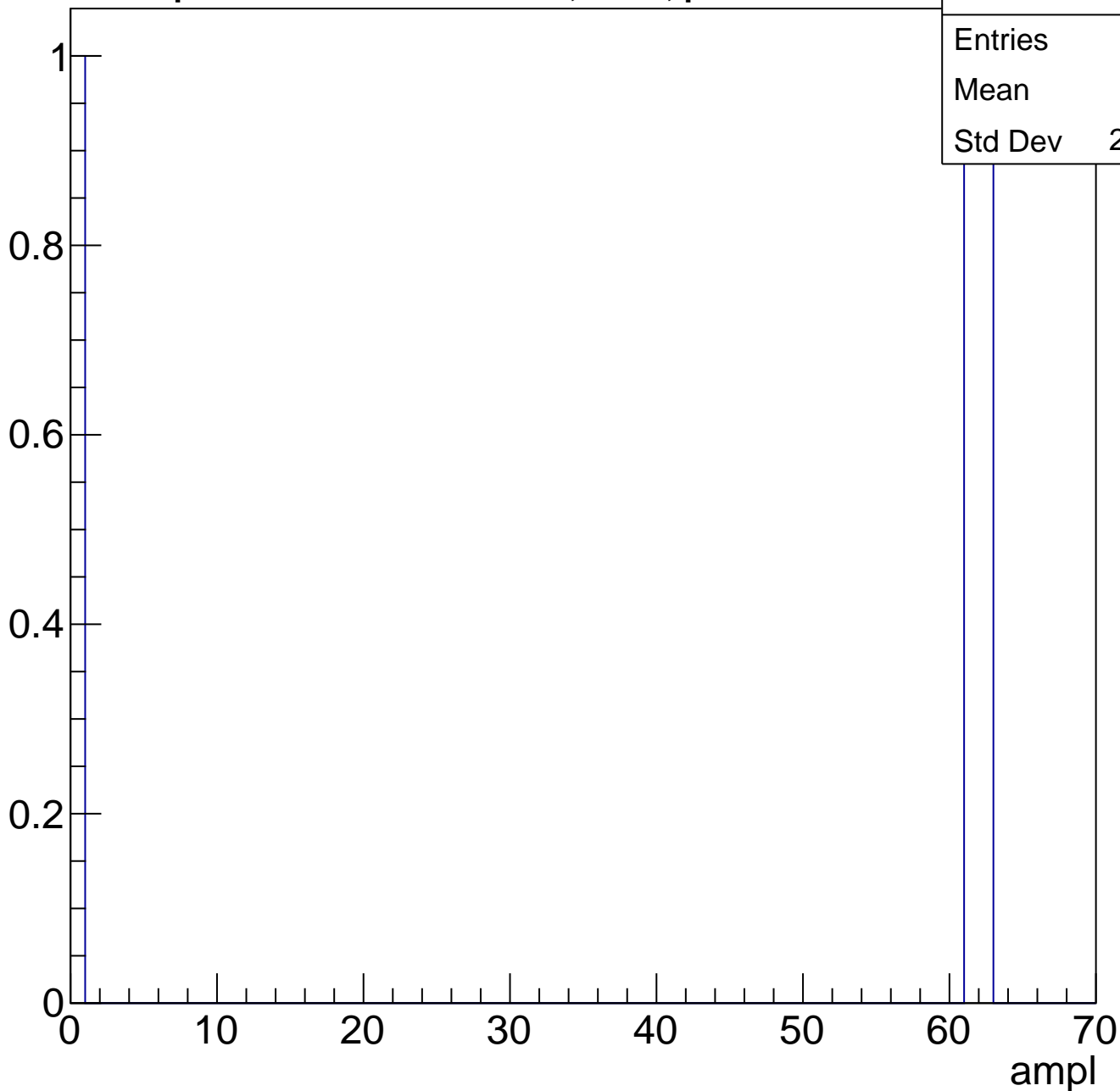
Entries	25
Mean	60.84
Std Dev	2.148



B1L101S, U25-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L101S, U25-ch54, adc0

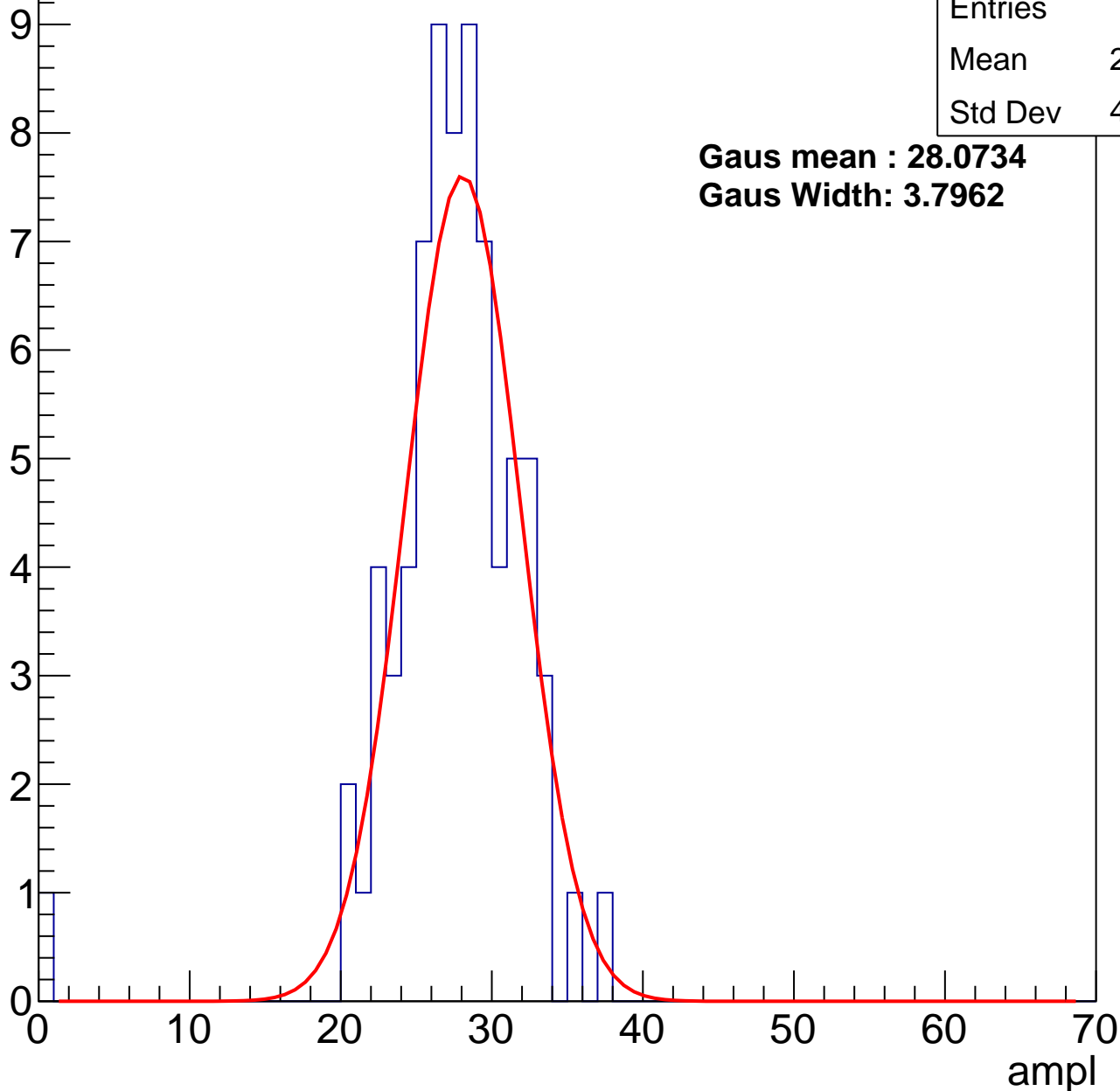
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	27.03
Std Dev	4.699

Gaus mean : 28.0734

Gaus Width: 3.7962



B1L101S, U25-ch54, adc1

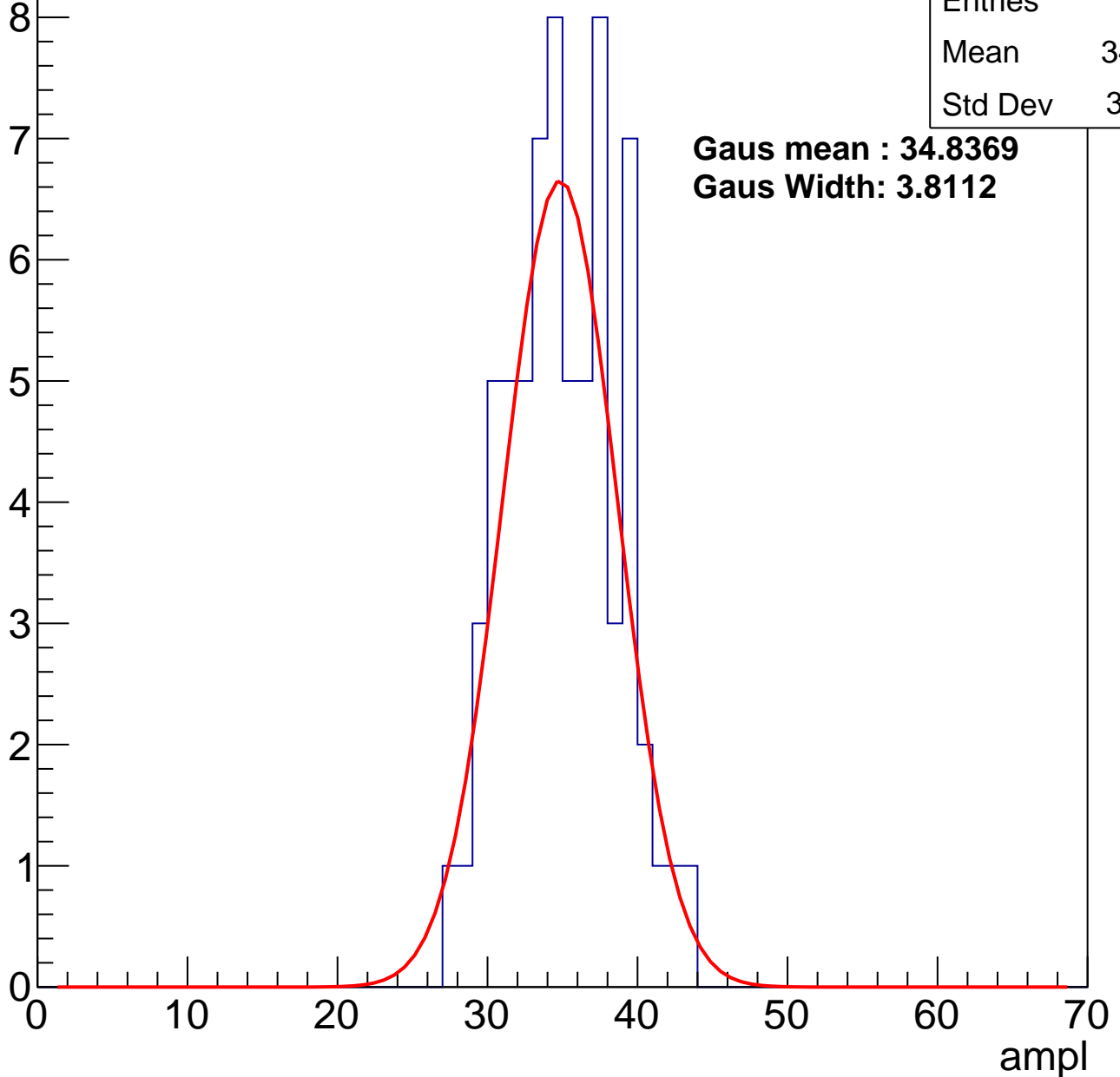
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	34.62
Std Dev	3.581

Gaus mean : 34.8369

Gaus Width: 3.8112



B1L101S, U25-ch54, adc2

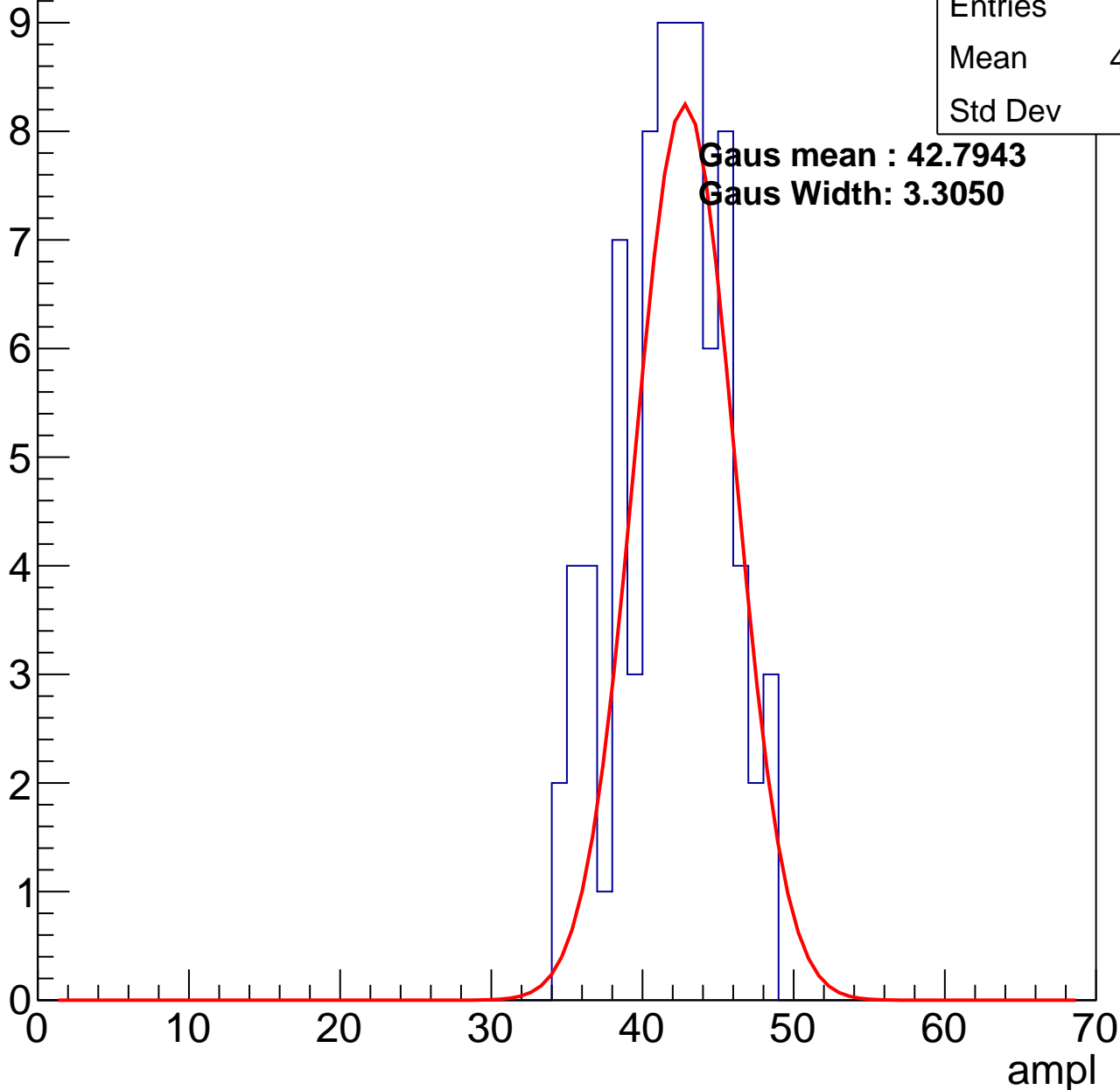
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	41.42
Std Dev	3.51

Gaus mean : 42.7943

Gaus Width: 3.3050

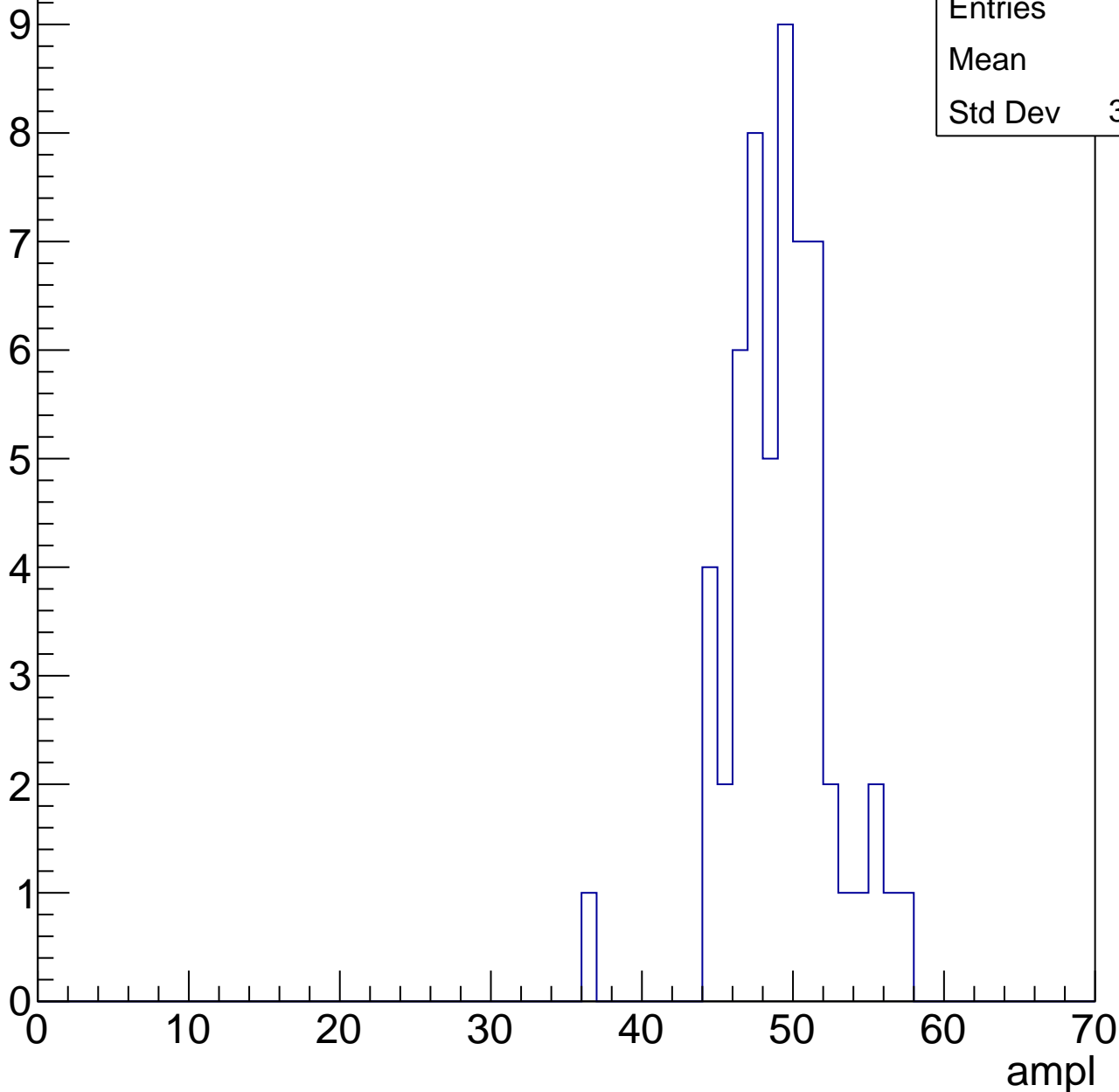


B1L101S, U25-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	48.7
Std Dev	3.418

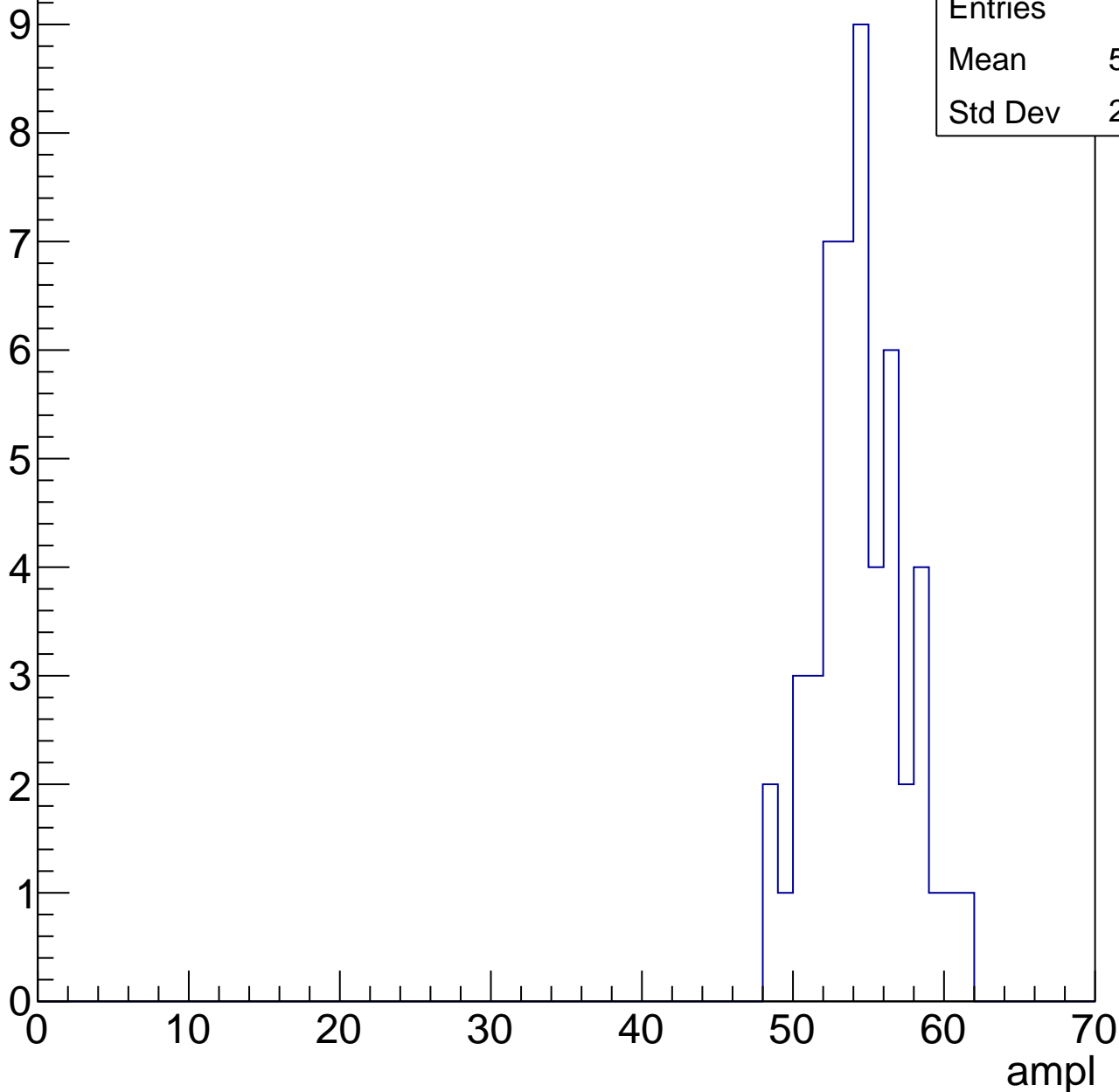


B1L101S, U25-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

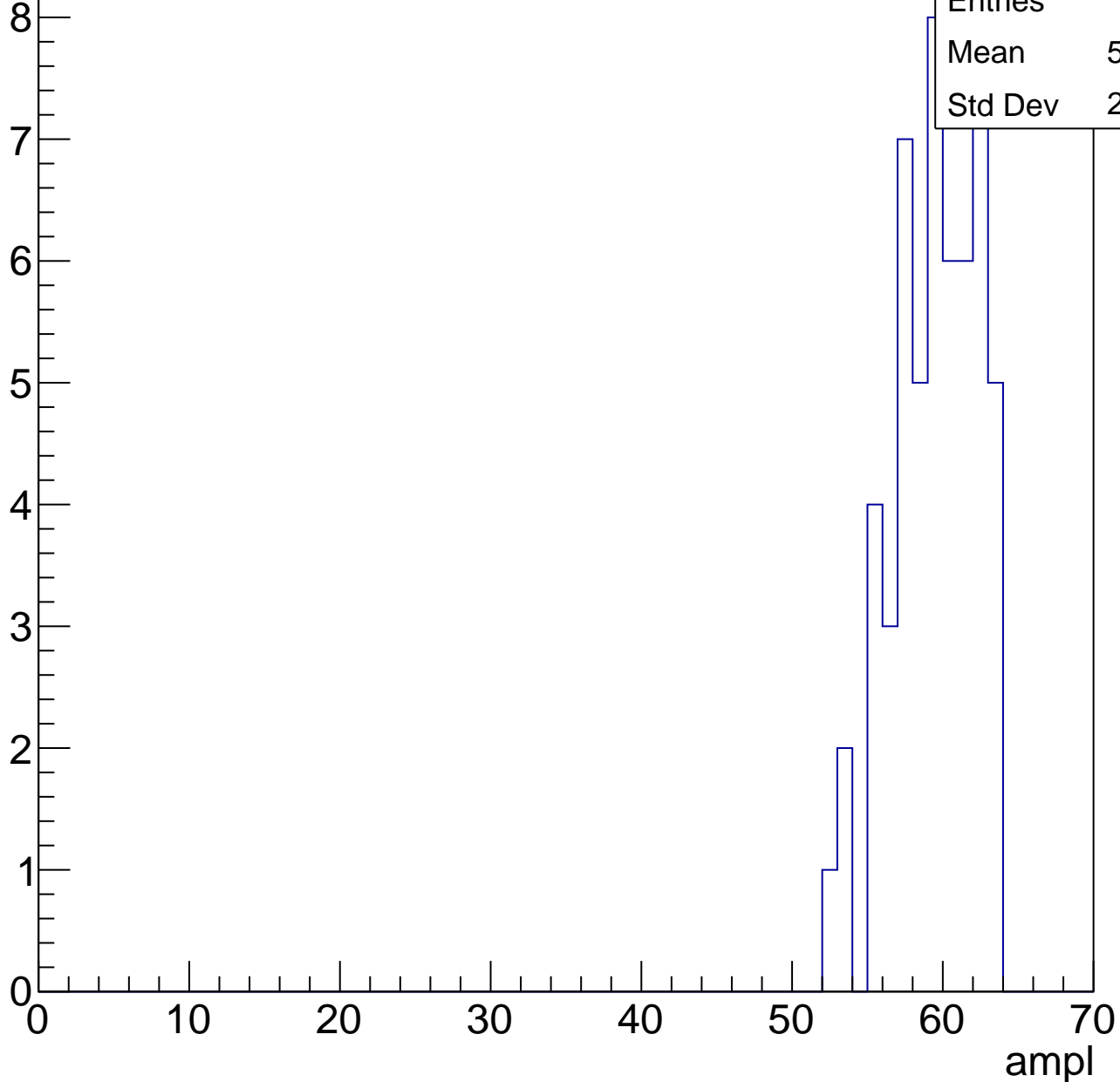
Entries	51
Mean	53.94
Std Dev	2.893



B1L101S, U25-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

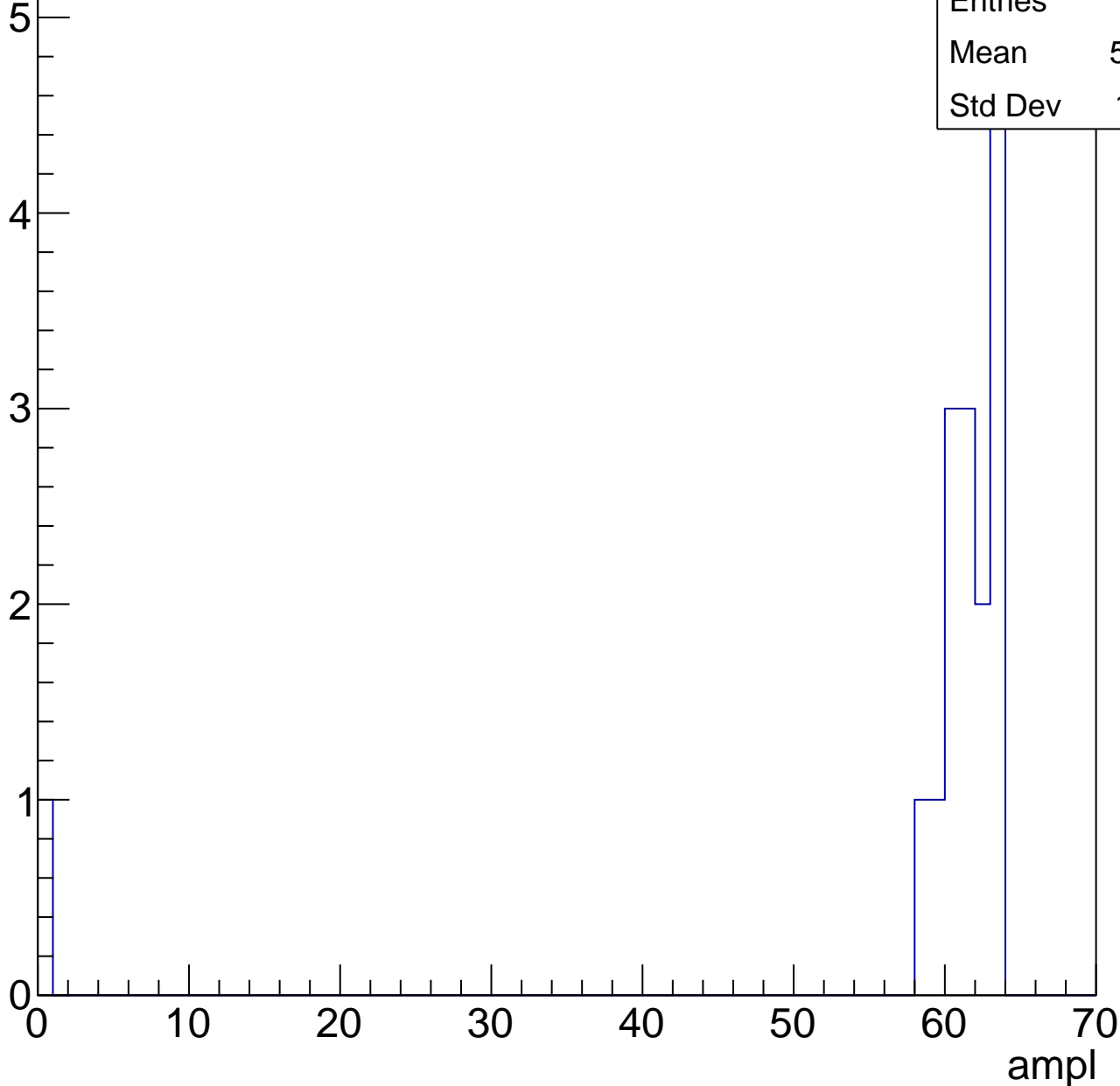


B1L101S, U25-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	57.44
Std Dev	14.91



B1L101S, U25-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch55, adc0

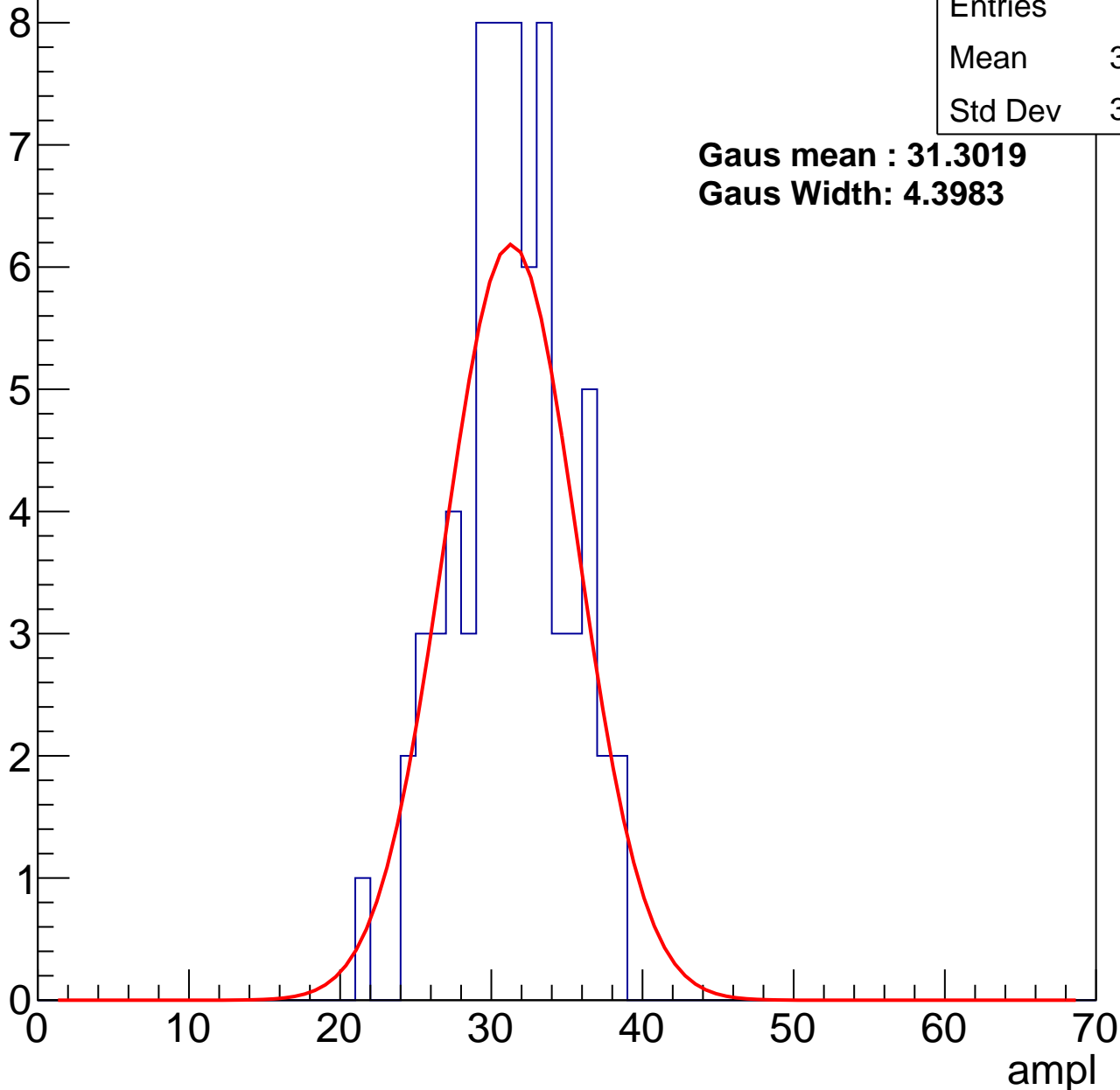
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.83
Std Dev	3.647

Gaus mean : 31.3019

Gaus Width: 4.3983



B1L101S, U25-ch55, adc1

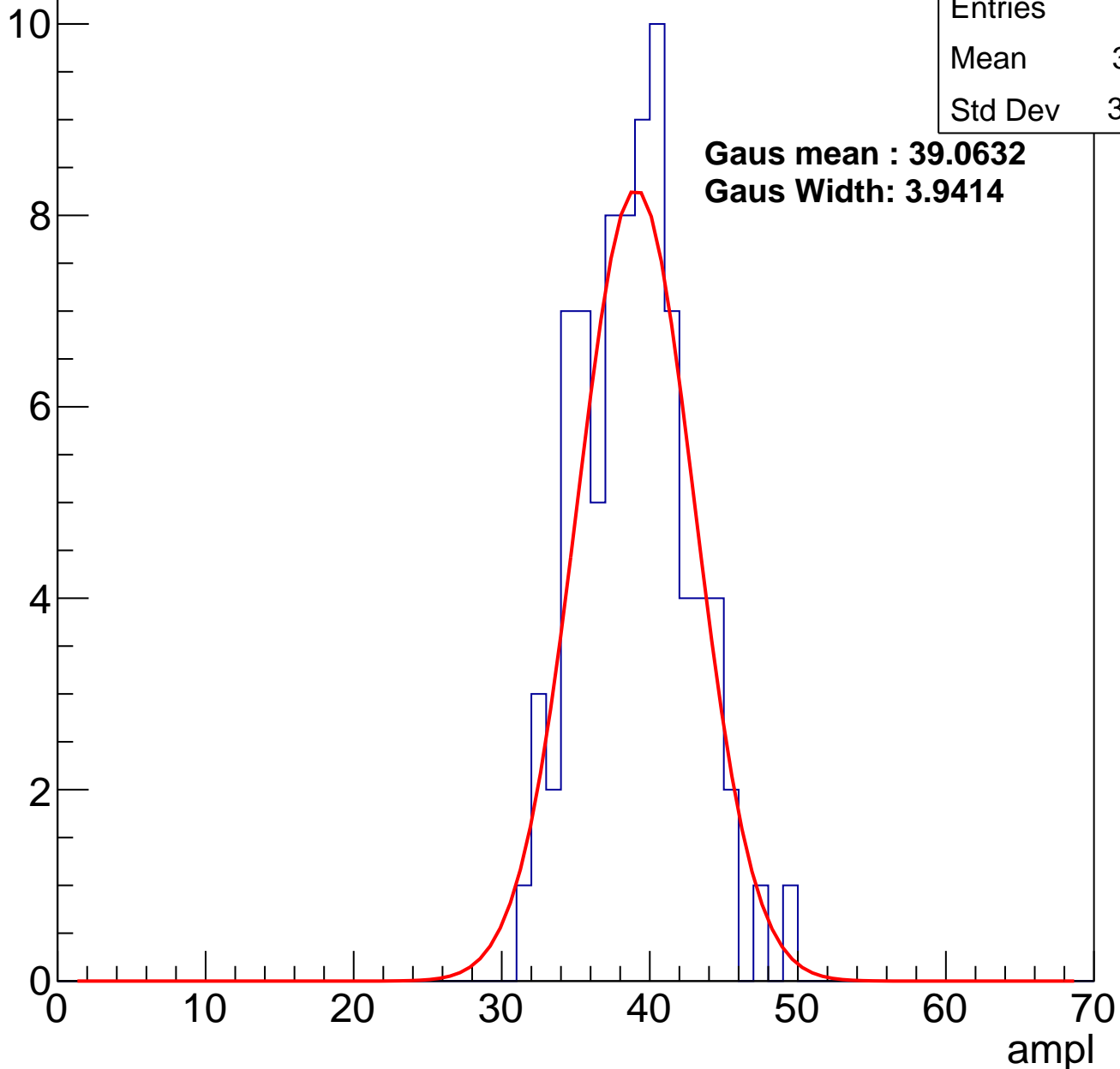
calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	38.51
Std Dev	3.655

Gaus mean : 39.0632

Gaus Width: 3.9414

Entry



B1L101S, U25-ch55, adc2

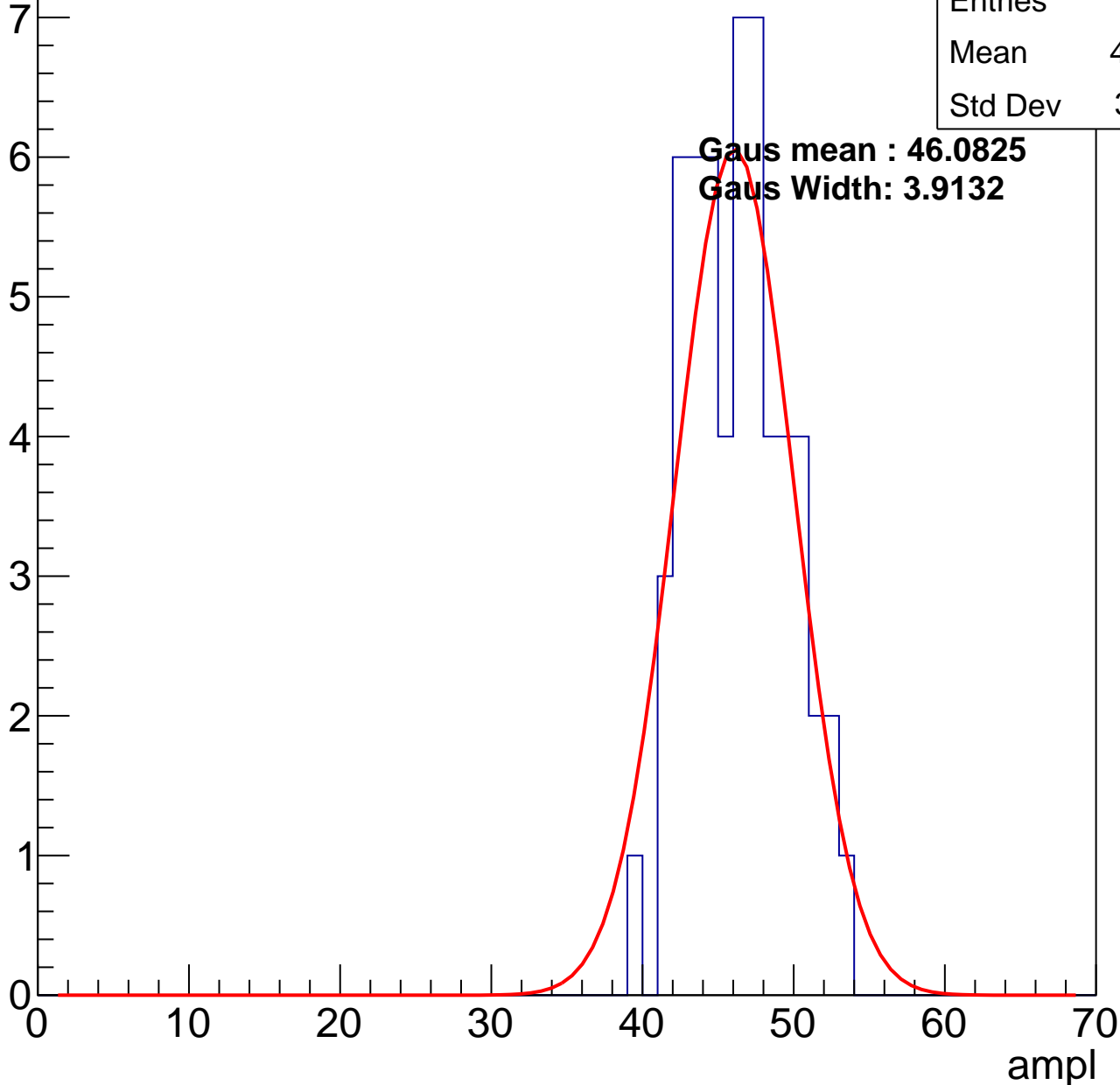
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	45.86
Std Dev	3.231

Gaus mean : 46.0825

Gaus Width: 3.9132

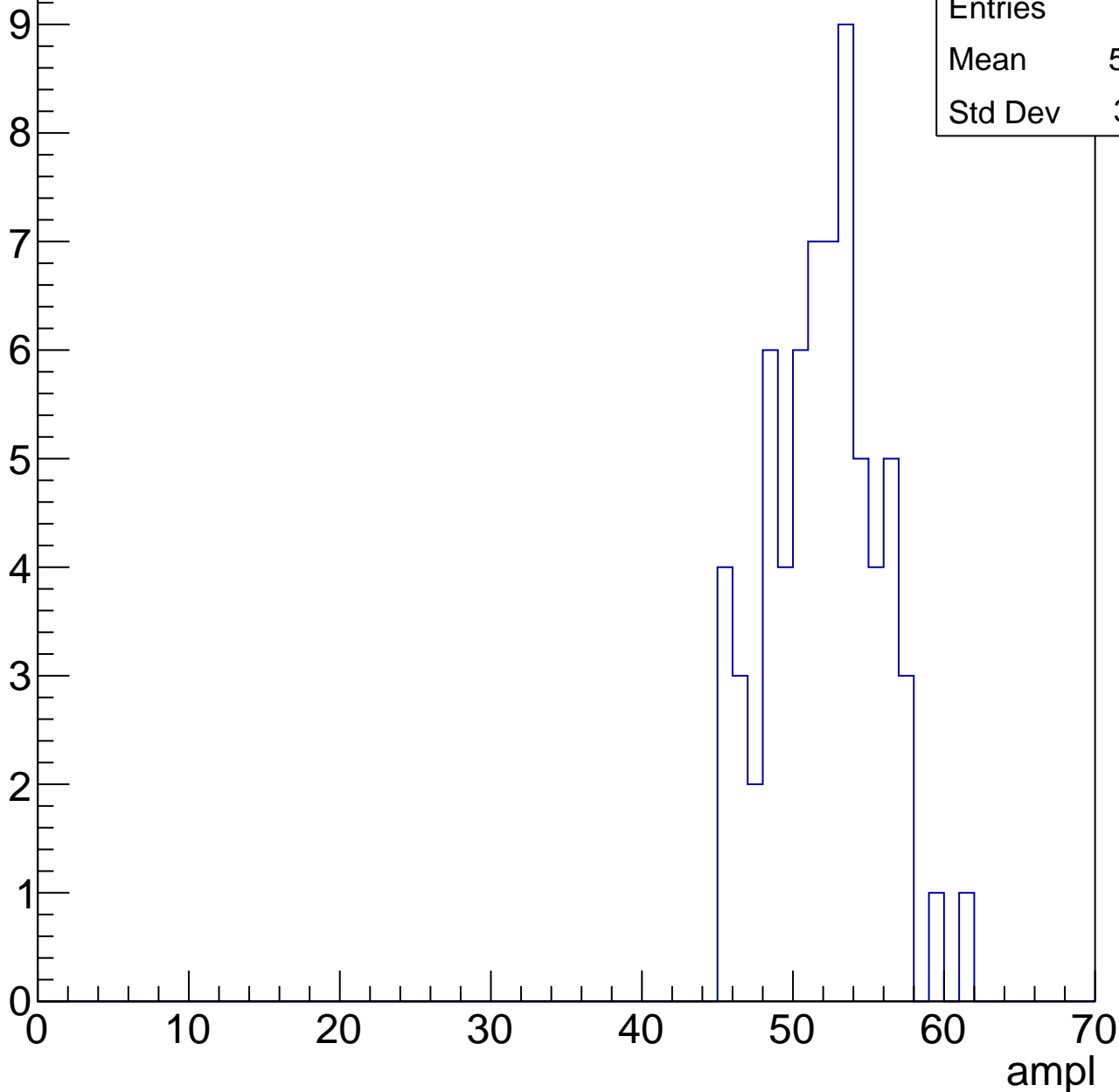


B1L101S, U25-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

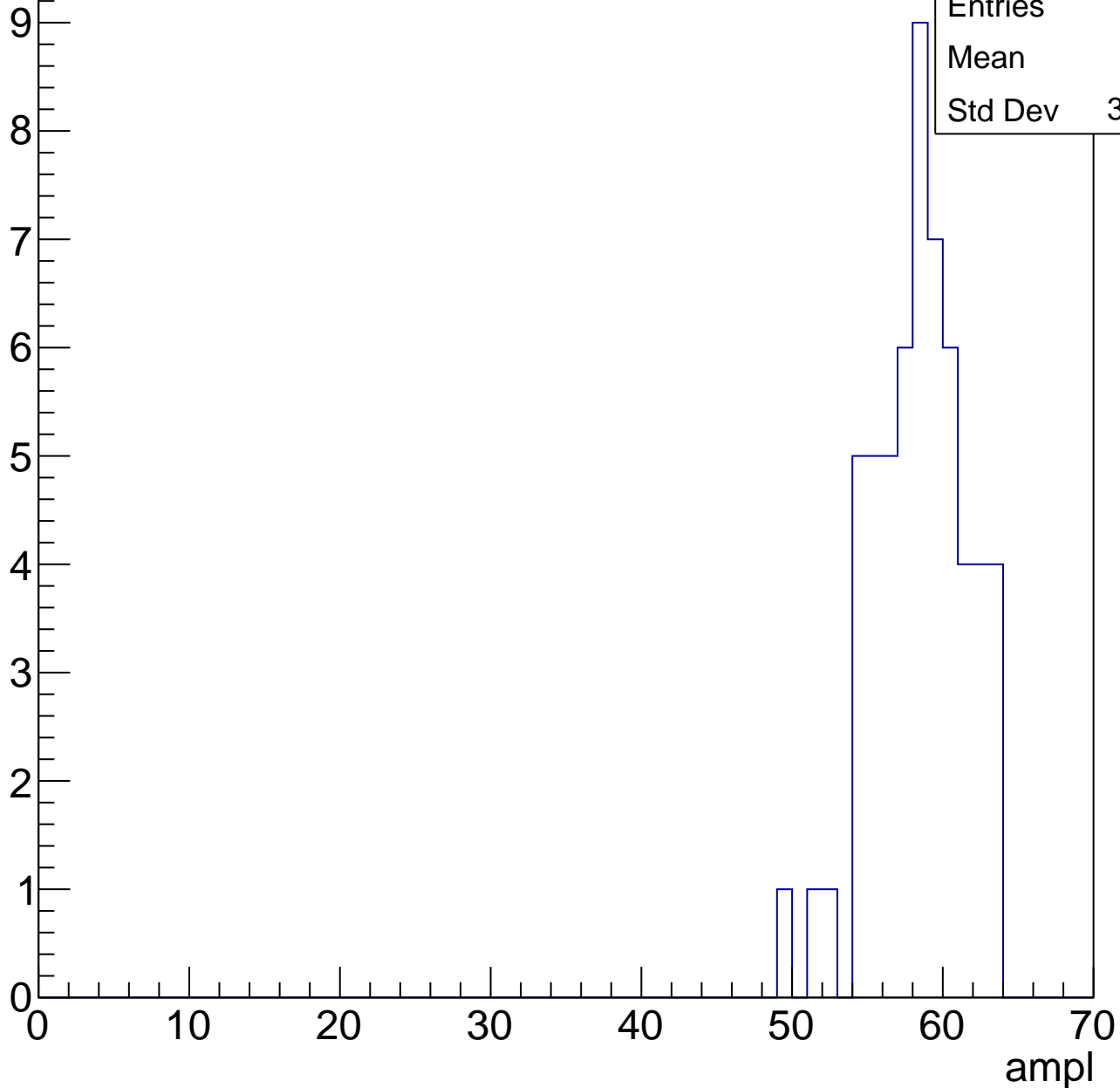
Entries	67
Mean	51.57
Std Dev	3.571



B1L101S, U25-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

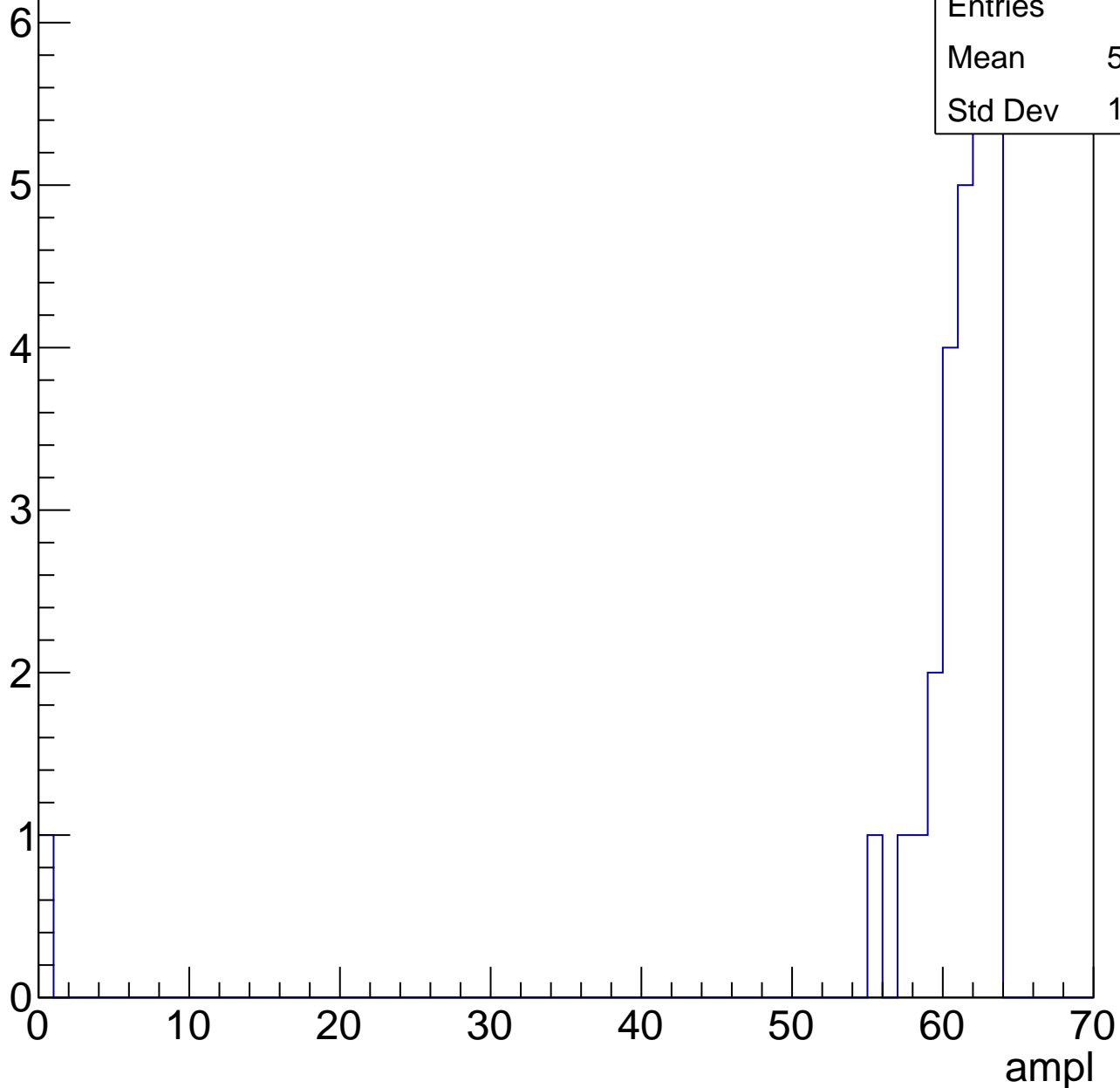
Entry



B1L101S, U25-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

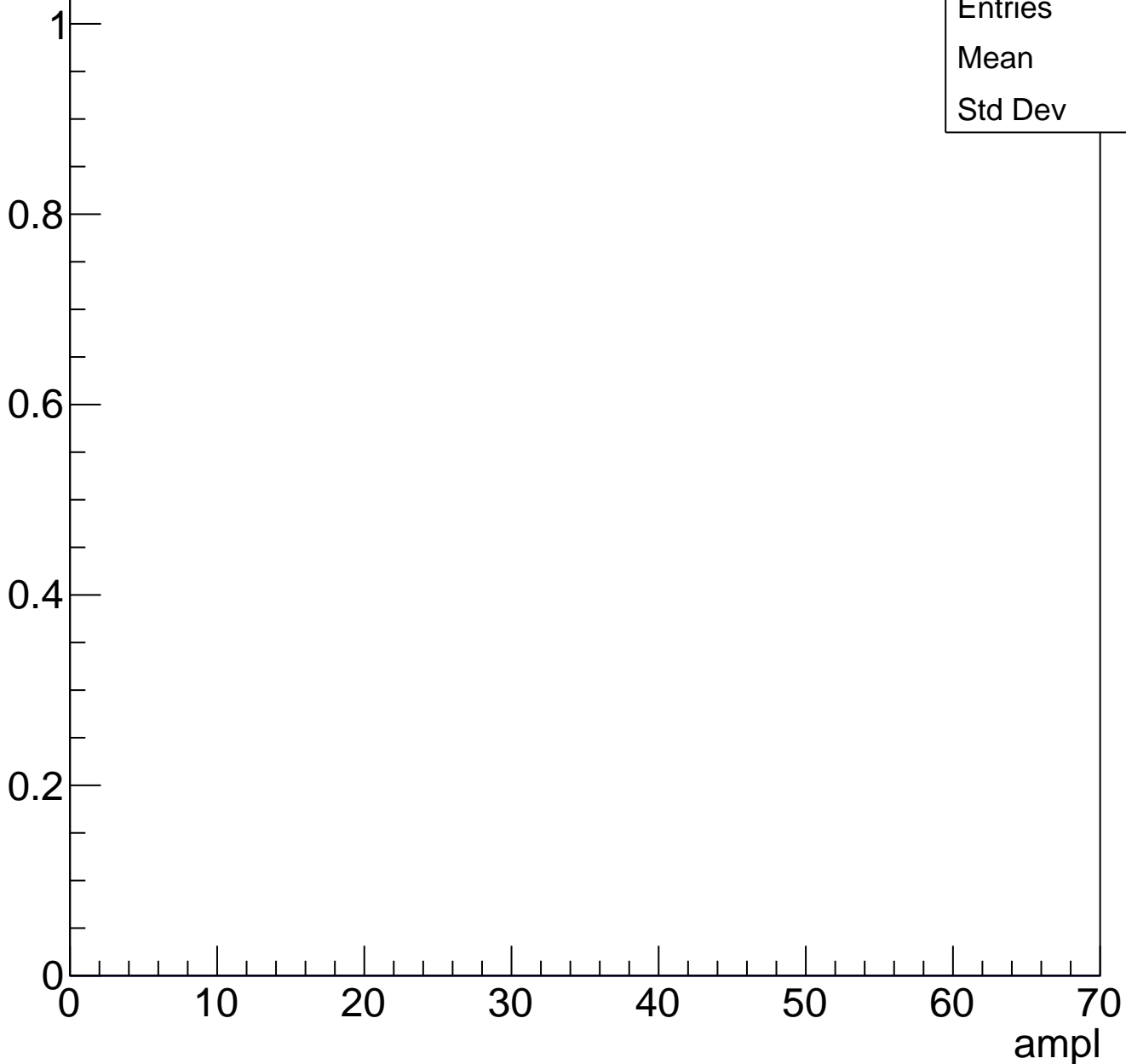
Entry



B1L101S, U25-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch56, adc0

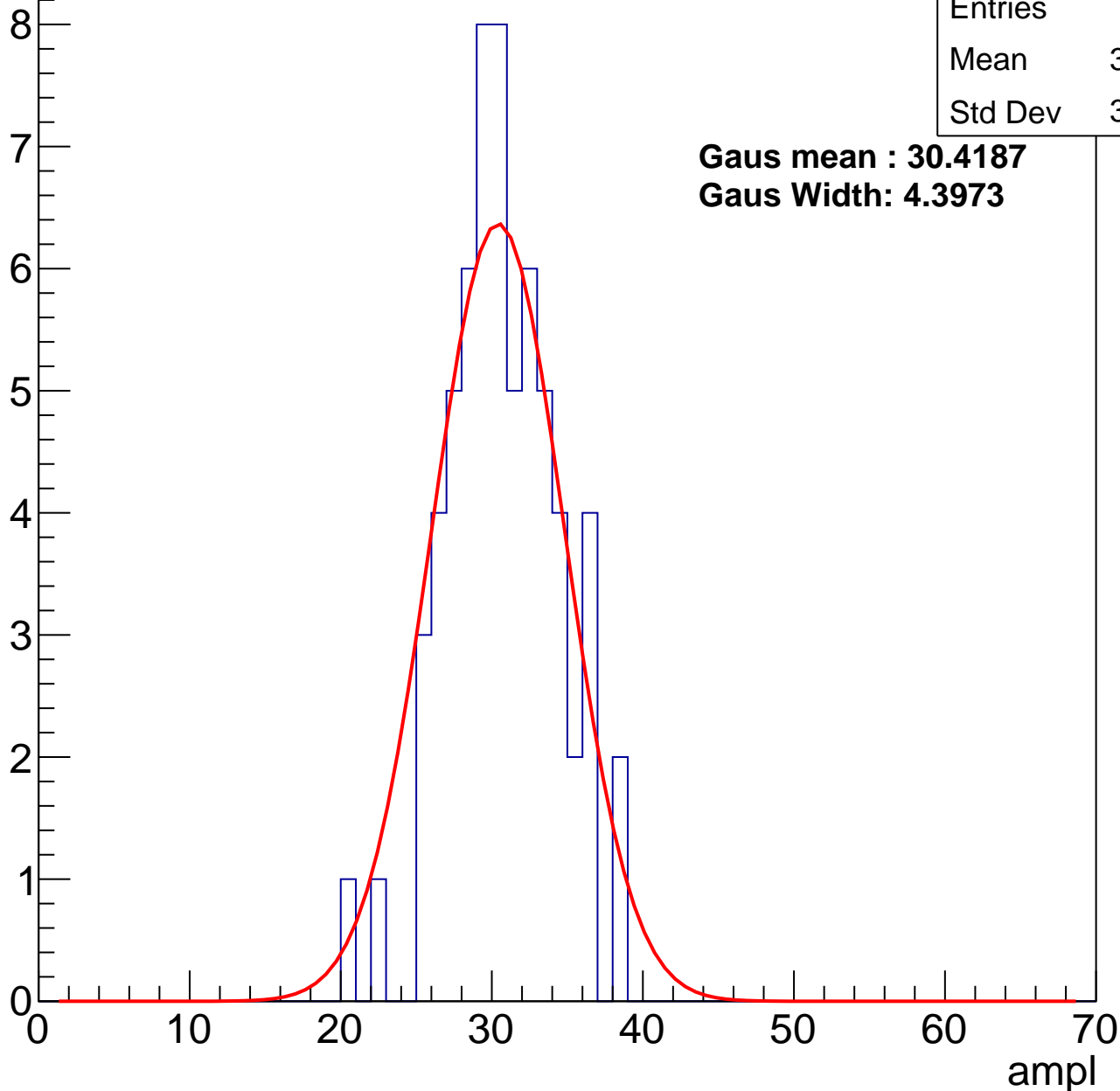
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.22
Std Dev	3.616

Gaus mean : 30.4187

Gaus Width: 4.3973



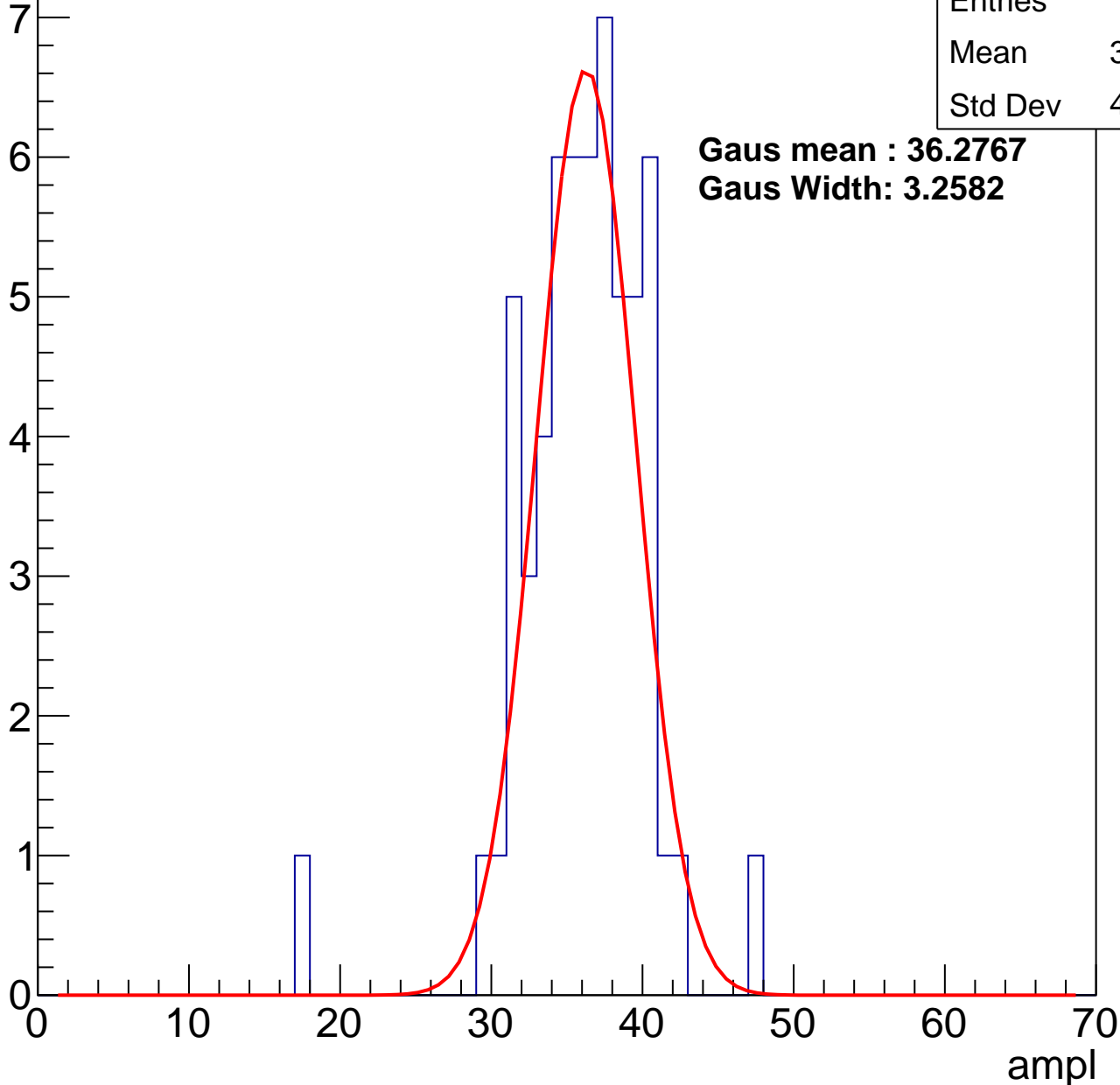
B1L101S, U25-ch56, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	35.64
Std Dev	4.173

Gaus mean : 36.2767
Gaus Width: 3.2582



B1L101S, U25-ch56, adc2

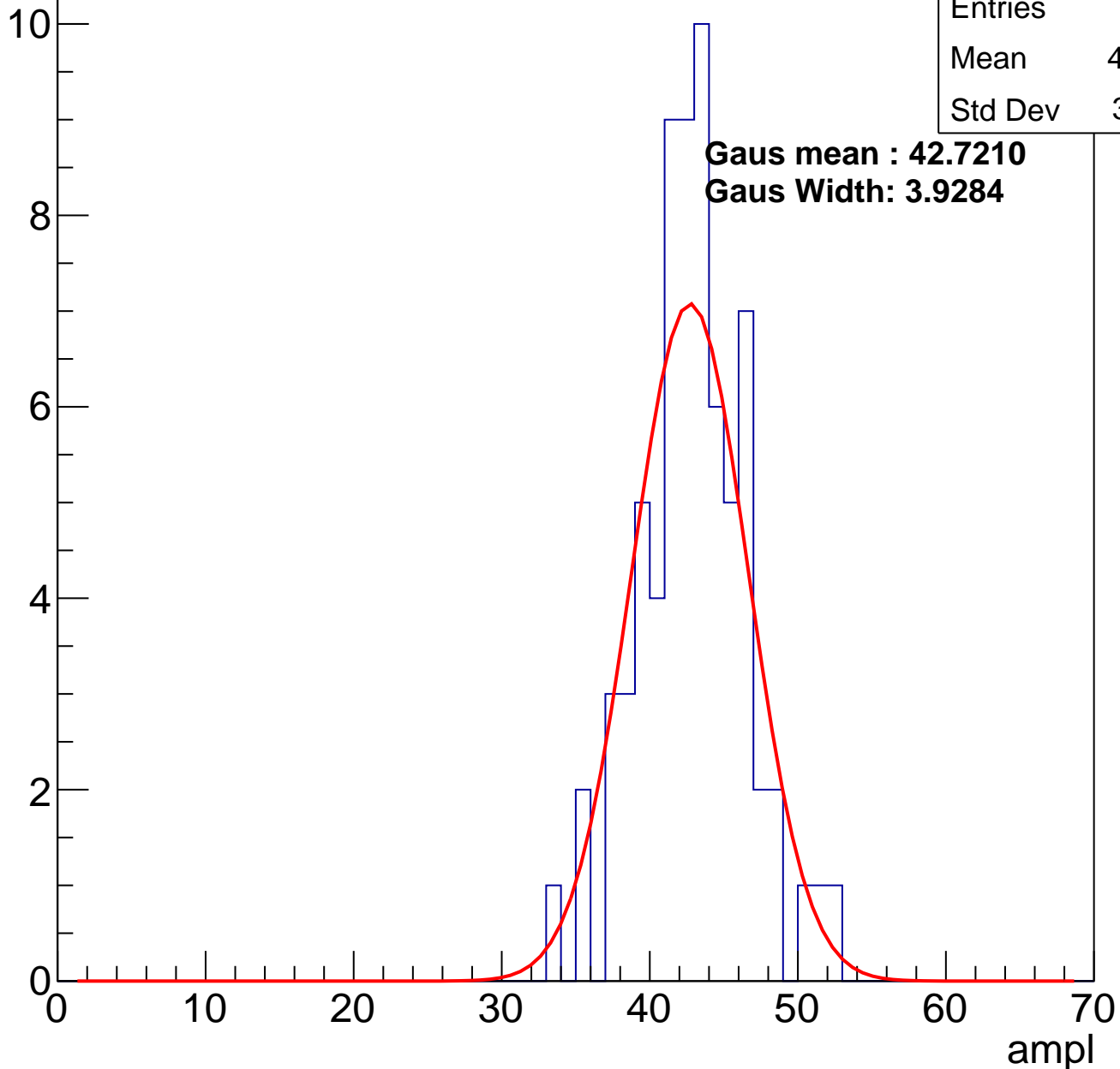
calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	42.45
Std Dev	3.591

Gaus mean : 42.7210

Gaus Width: 3.9284

Entry

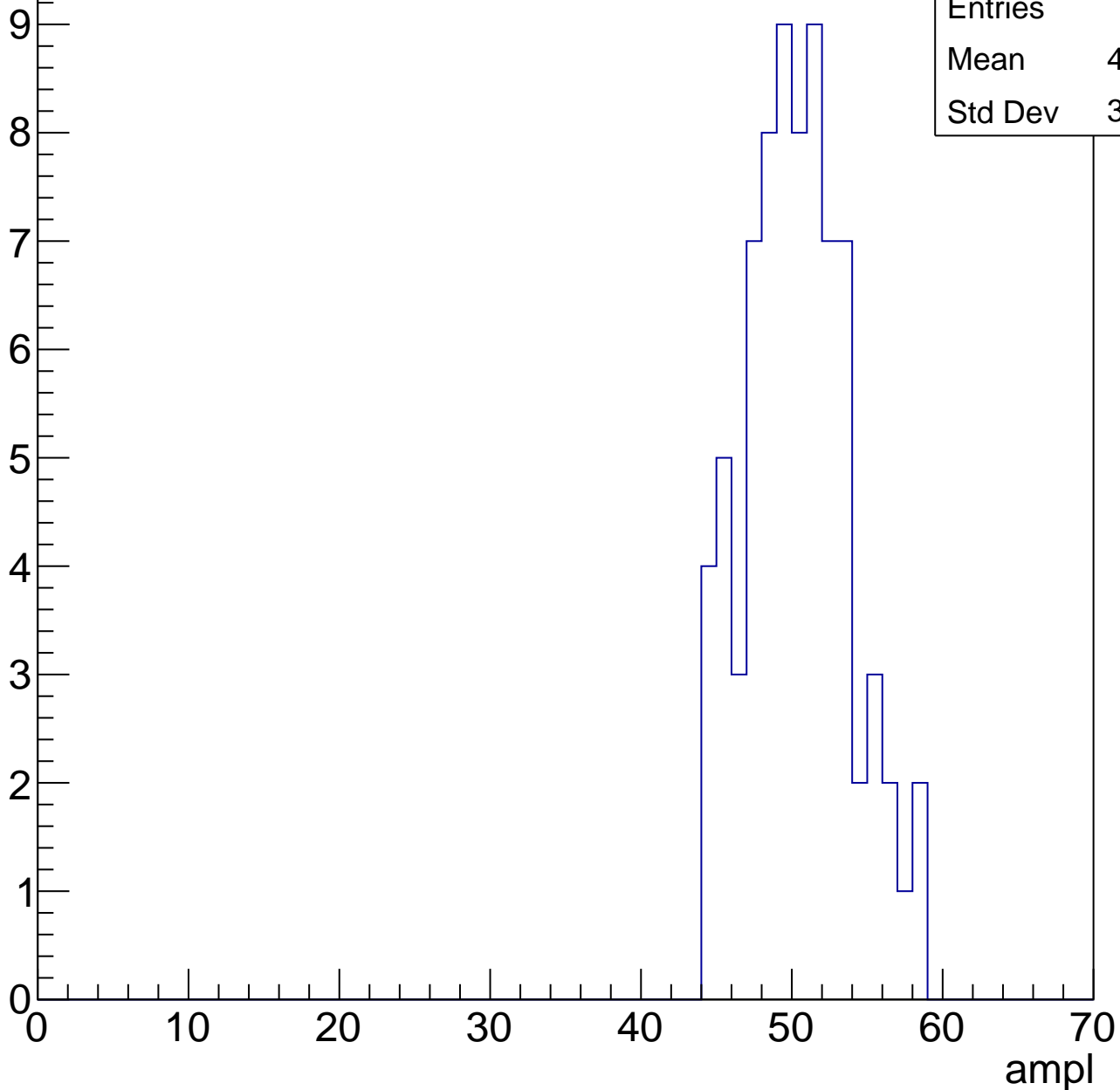


B1L101S, U25-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	49.94
Std Dev	3.374

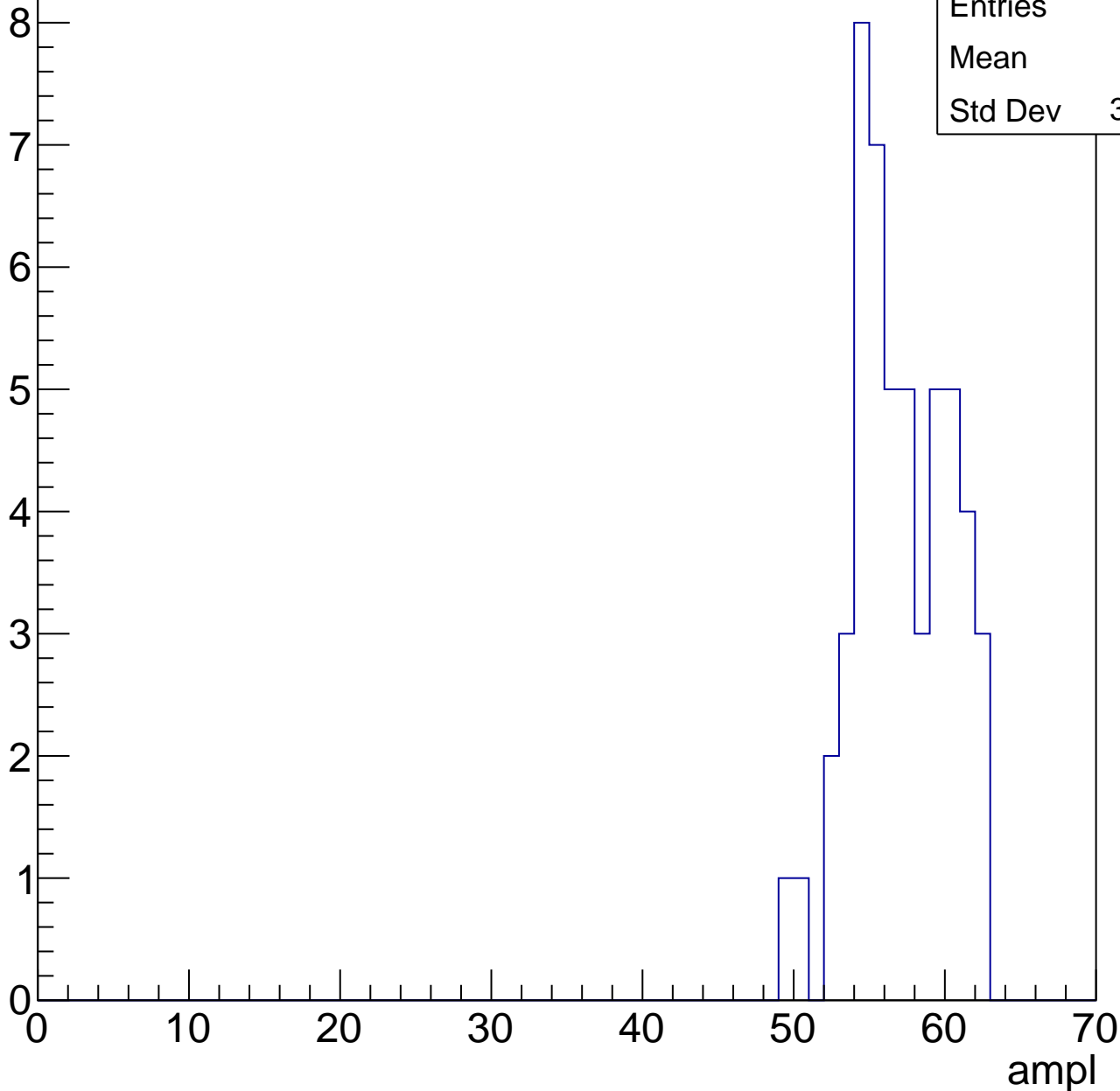


B1L101S, U25-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.6
Std Dev	3.146



B1L101S, U25-ch56, adc5

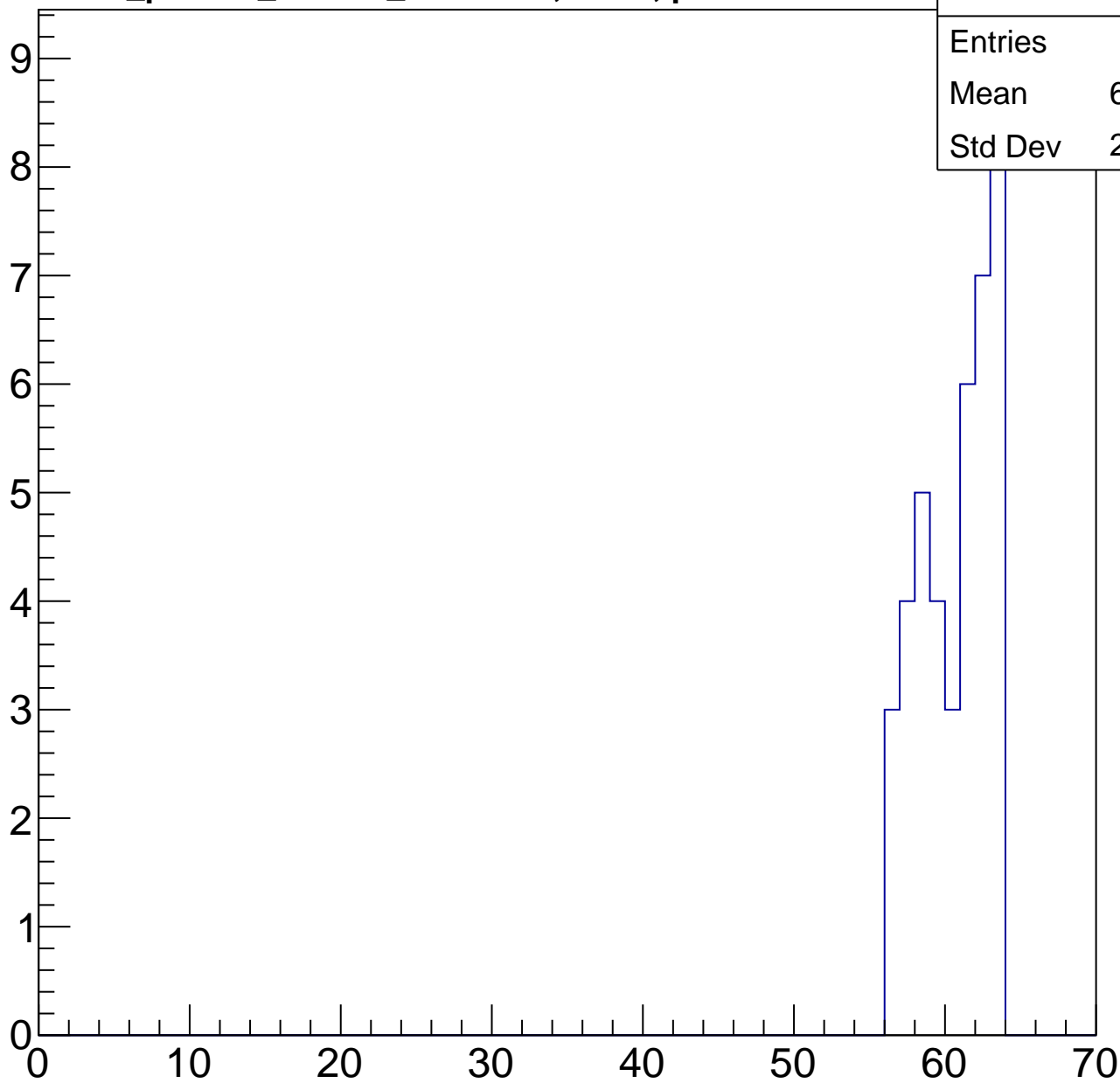
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.22
Std Dev	2.322

ampl



B1L101S, U25-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

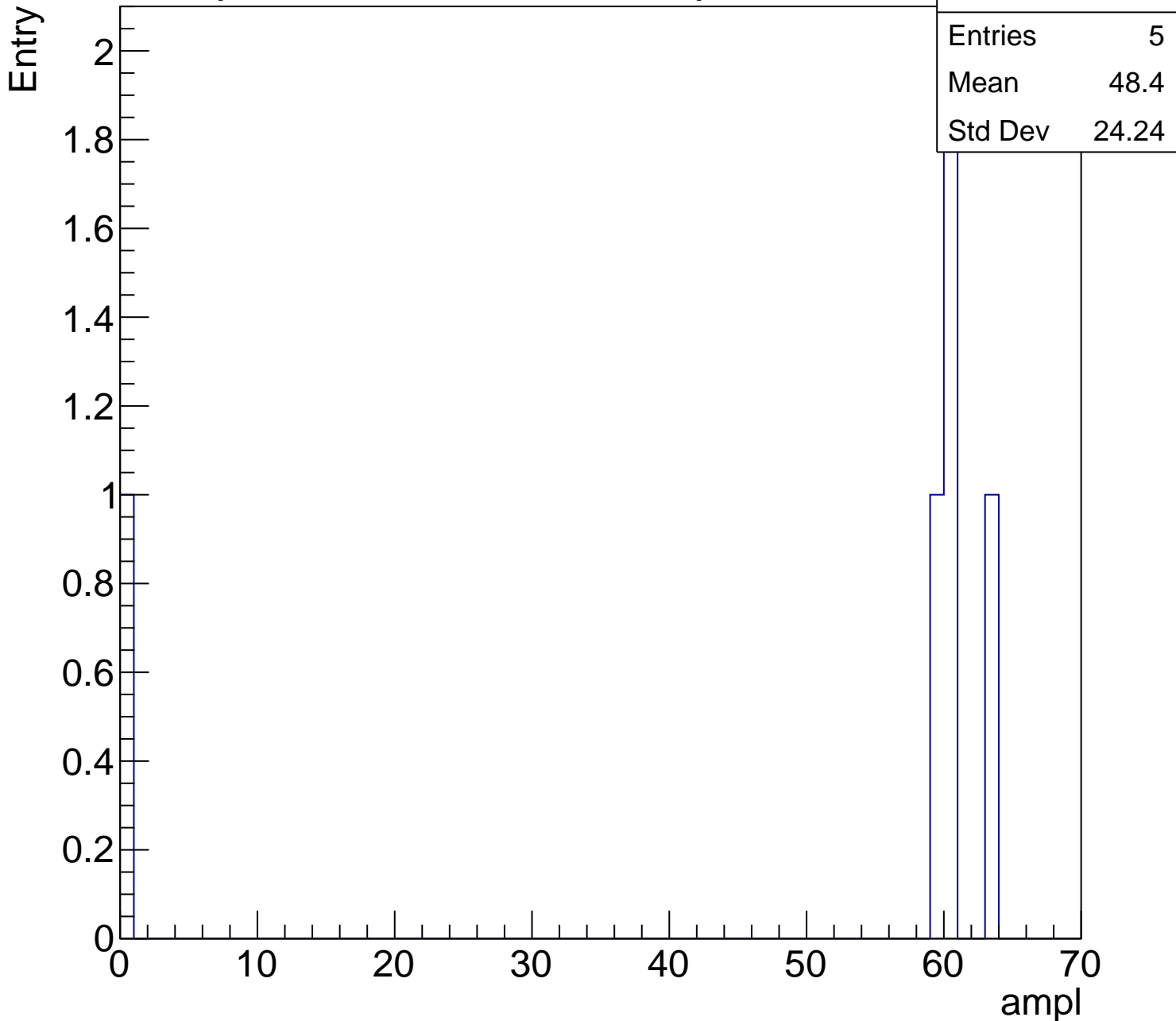
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	48.4
Std Dev	24.24

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch57, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	28.69
Std Dev	4.902

Gaus mean : 29.6749

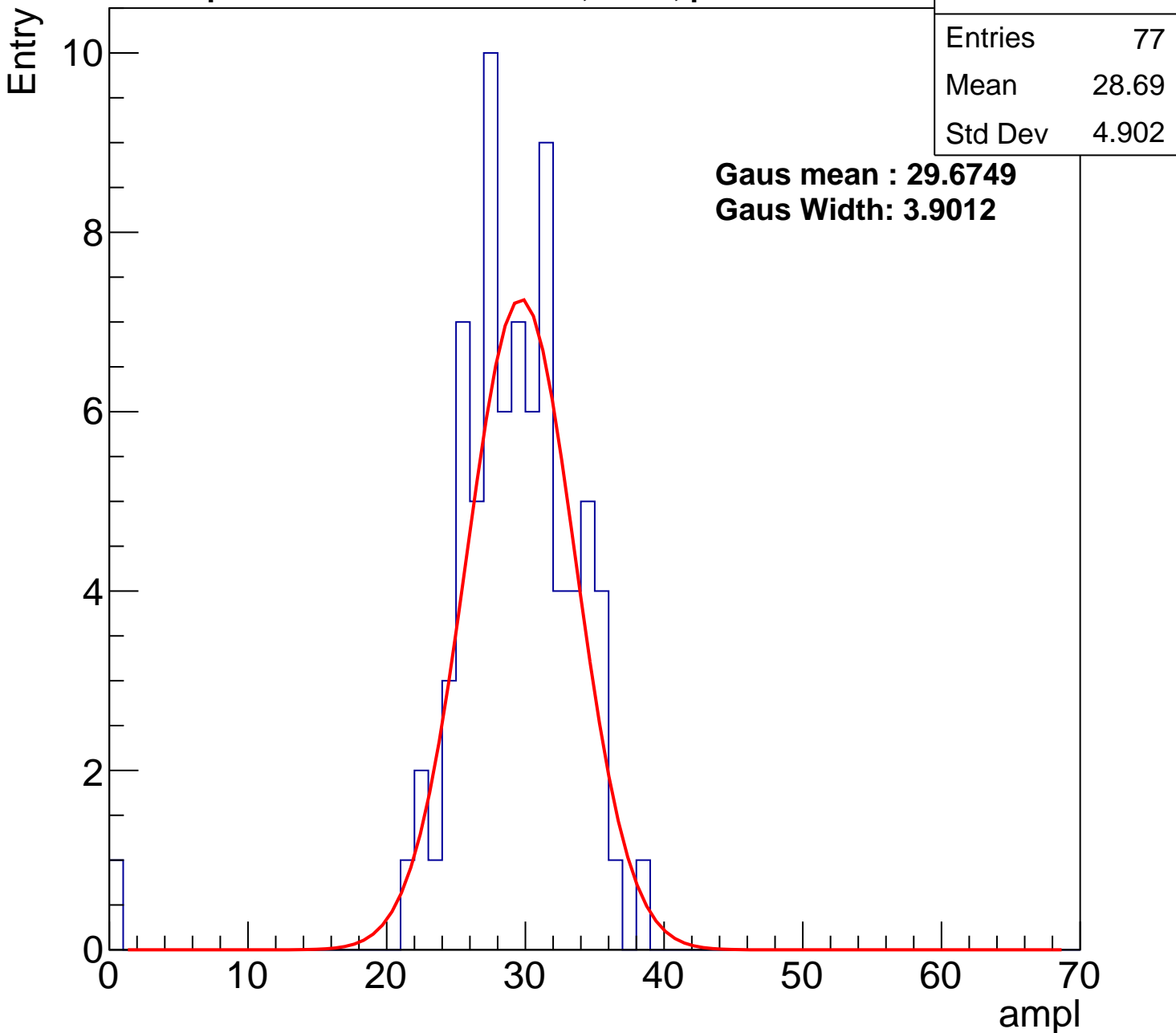
Gaus Width: 3.9012

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



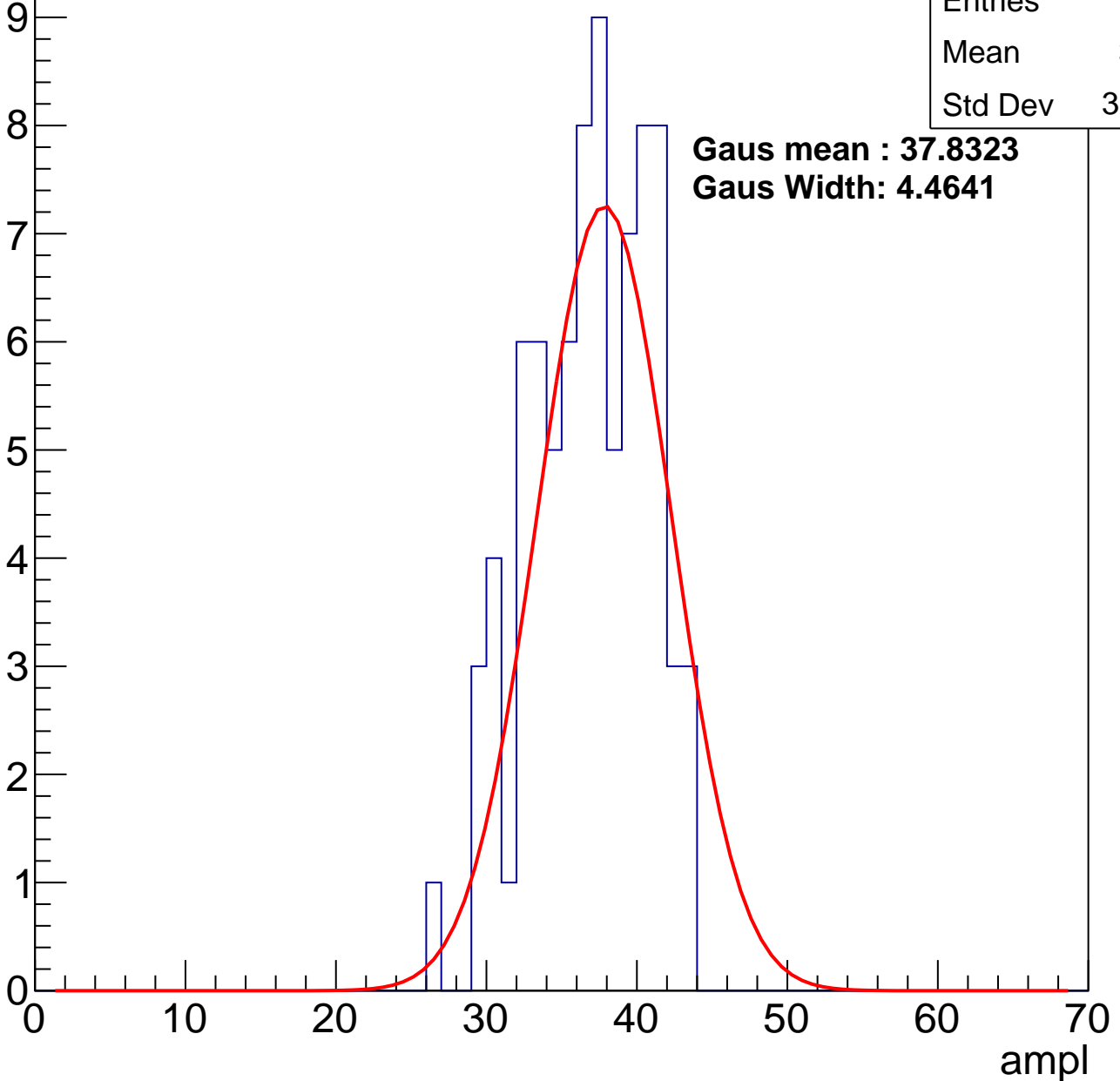
B1L101S, U25-ch57, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	36.4
Std Dev	3.887

Gaus mean : 37.8323
Gaus Width: 4.4641



B1L101S, U25-ch57, adc2

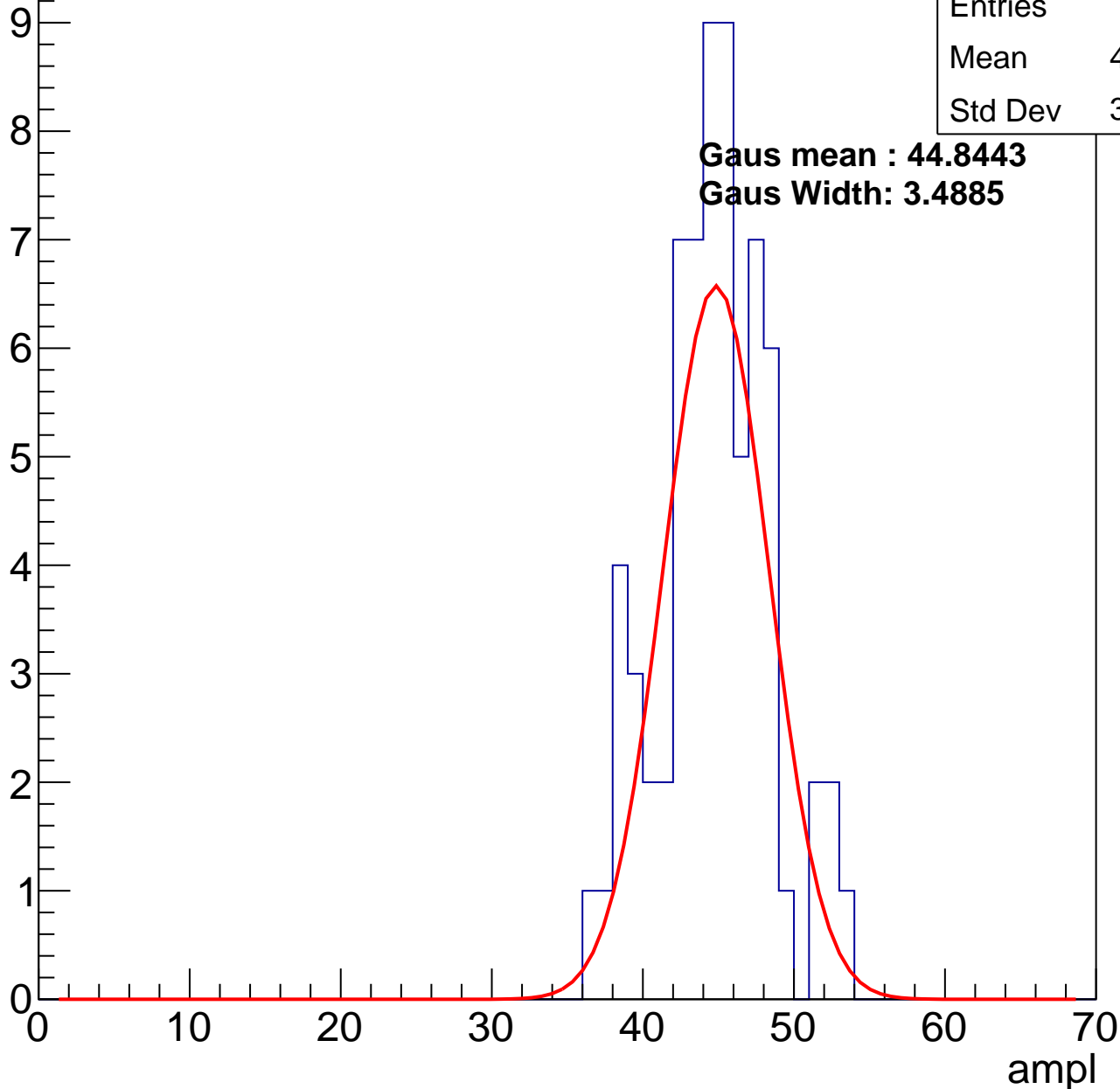
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.28
Std Dev	3.675

Gaus mean : 44.8443

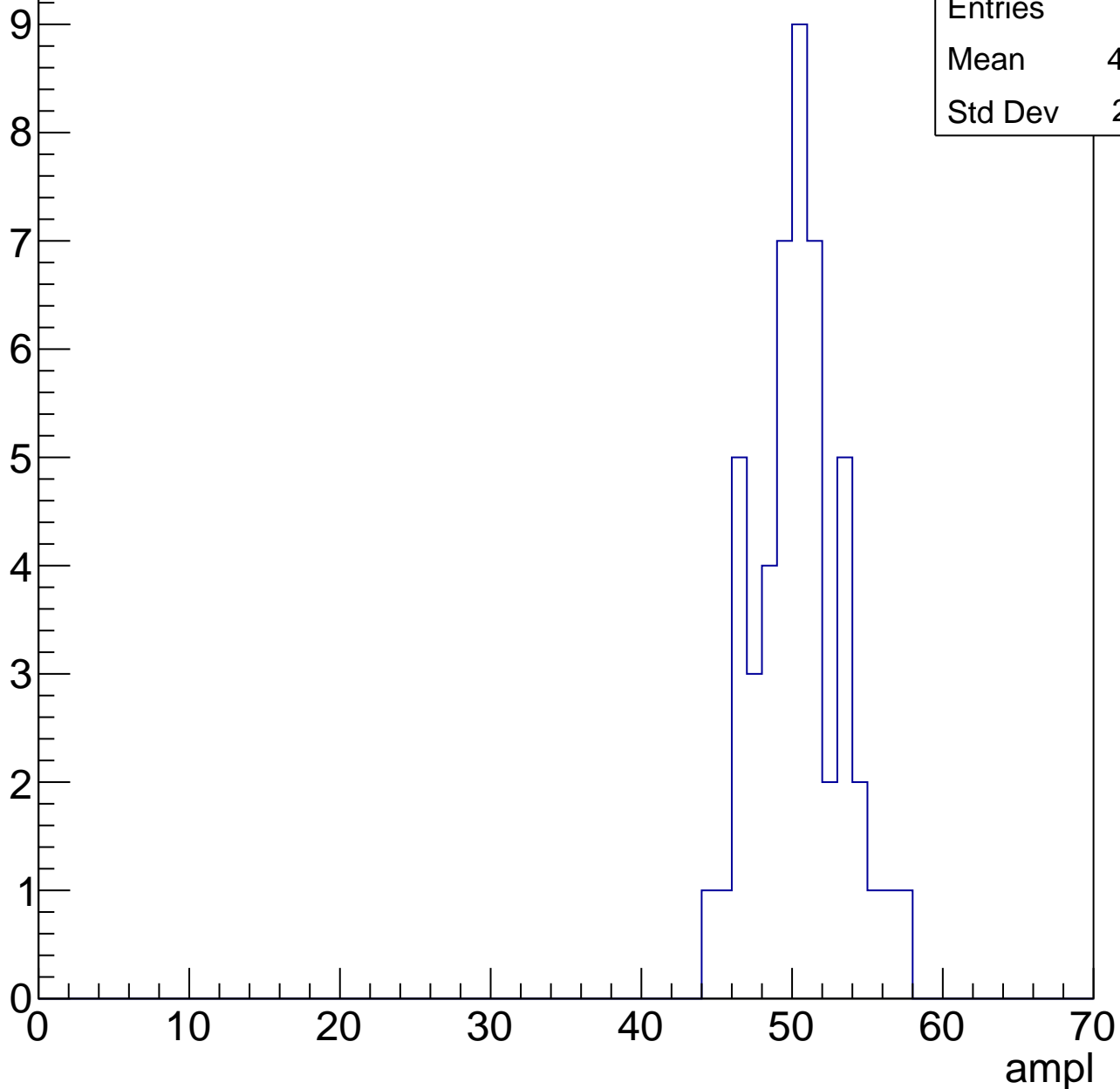
Gaus Width: 3.4885



B1L101S, U25-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

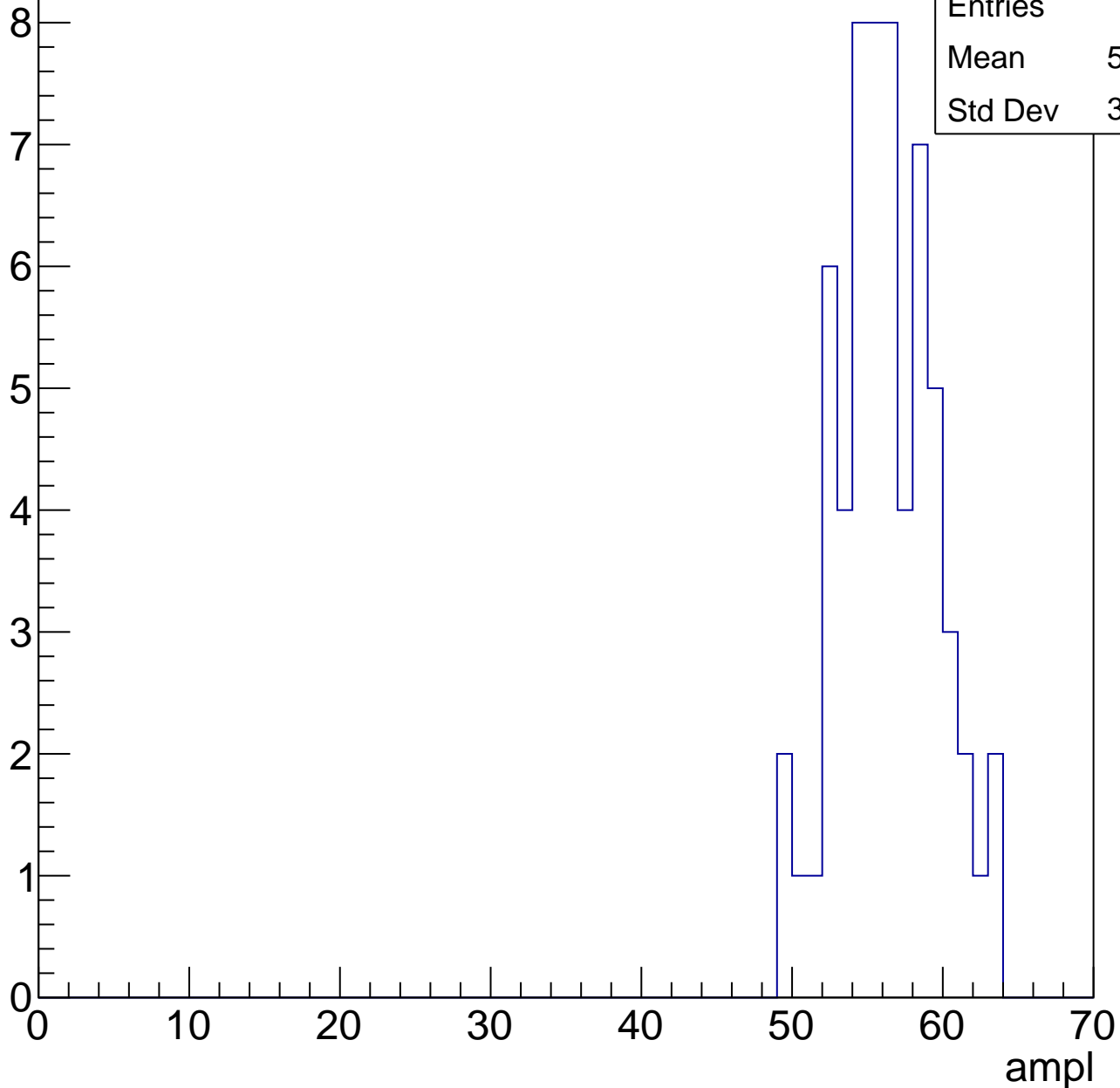
Entry



B1L101S, U25-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

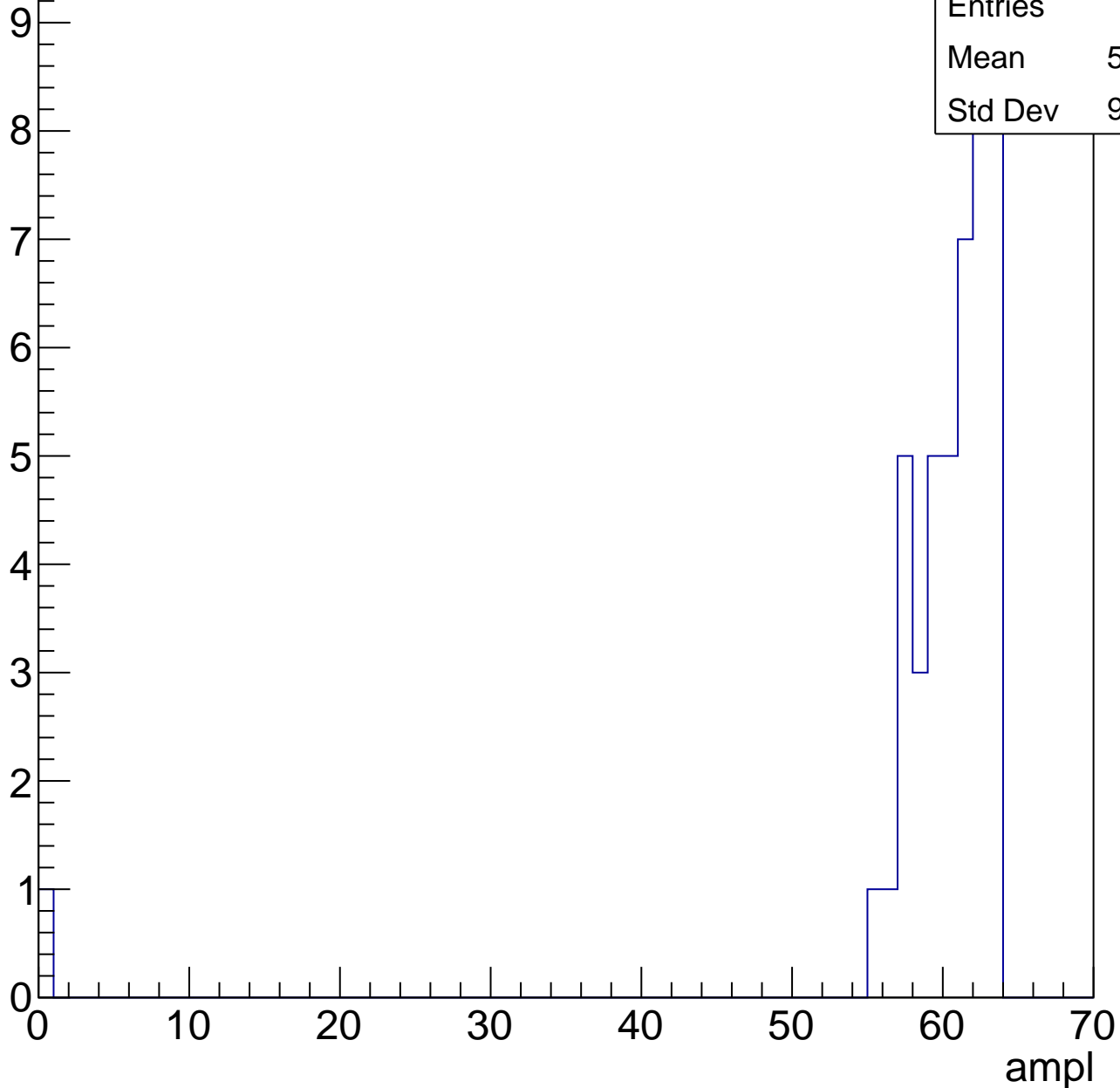


Entries	62
Mean	55.84
Std Dev	3.209

B1L101S, U25-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U25-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

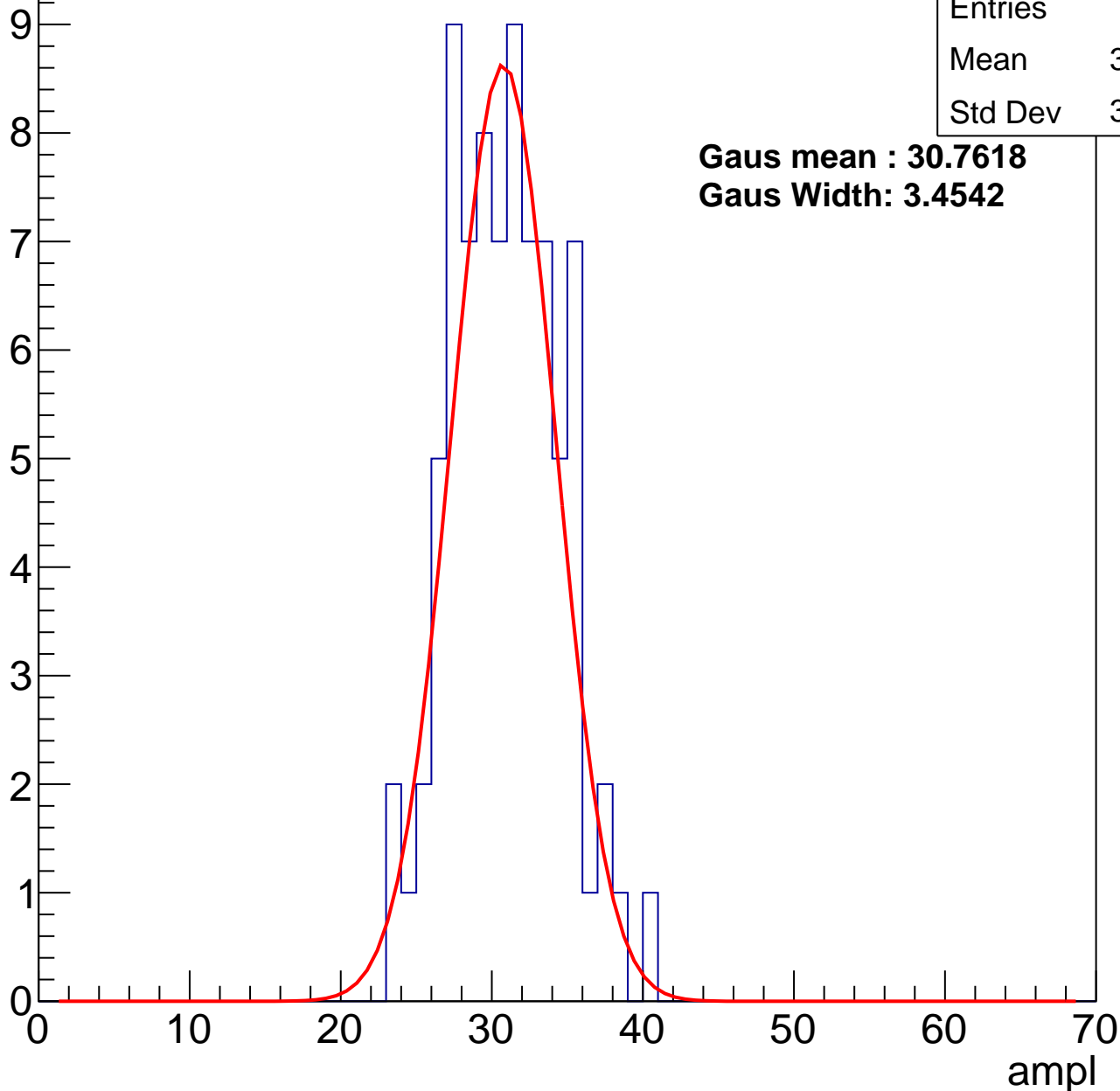


Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch58, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch58, adc1

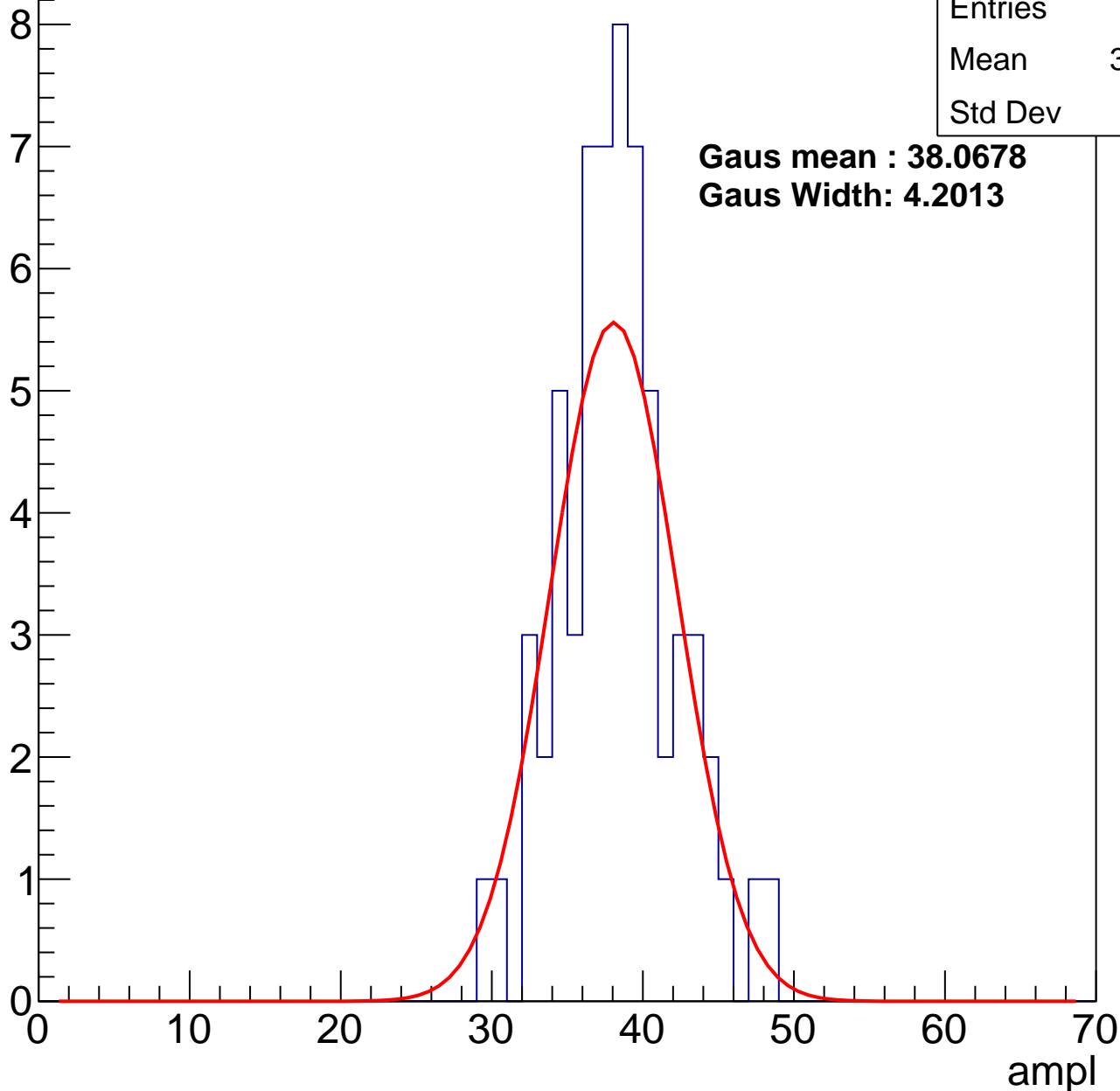
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.89
Std Dev	3.84

Gaus mean : 38.0678

Gaus Width: 4.2013



B1L101S, U25-ch58, adc2

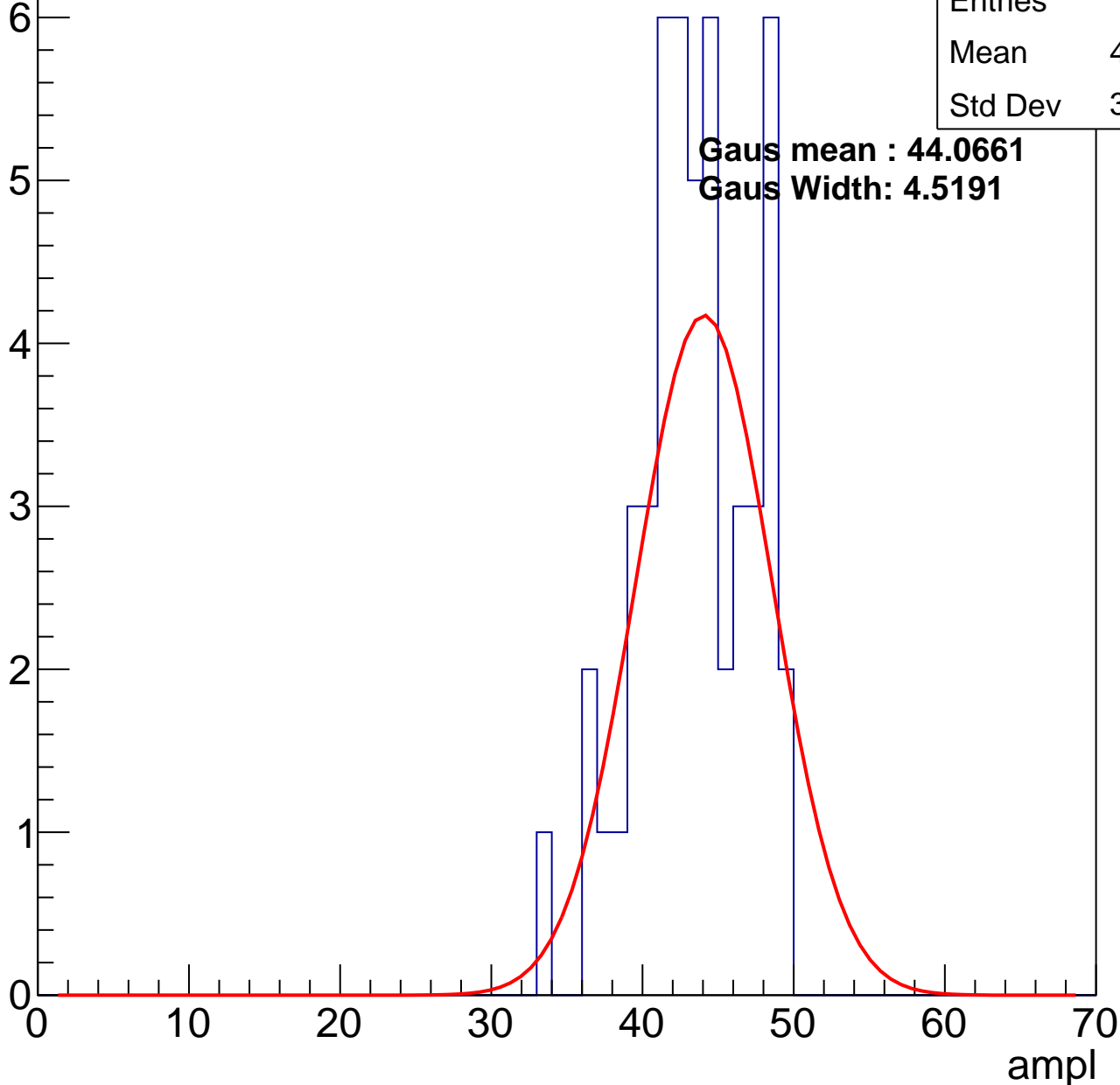
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	42.98
Std Dev	3.674

Gaus mean : 44.0661

Gaus Width: 4.5191



B1L101S, U25-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

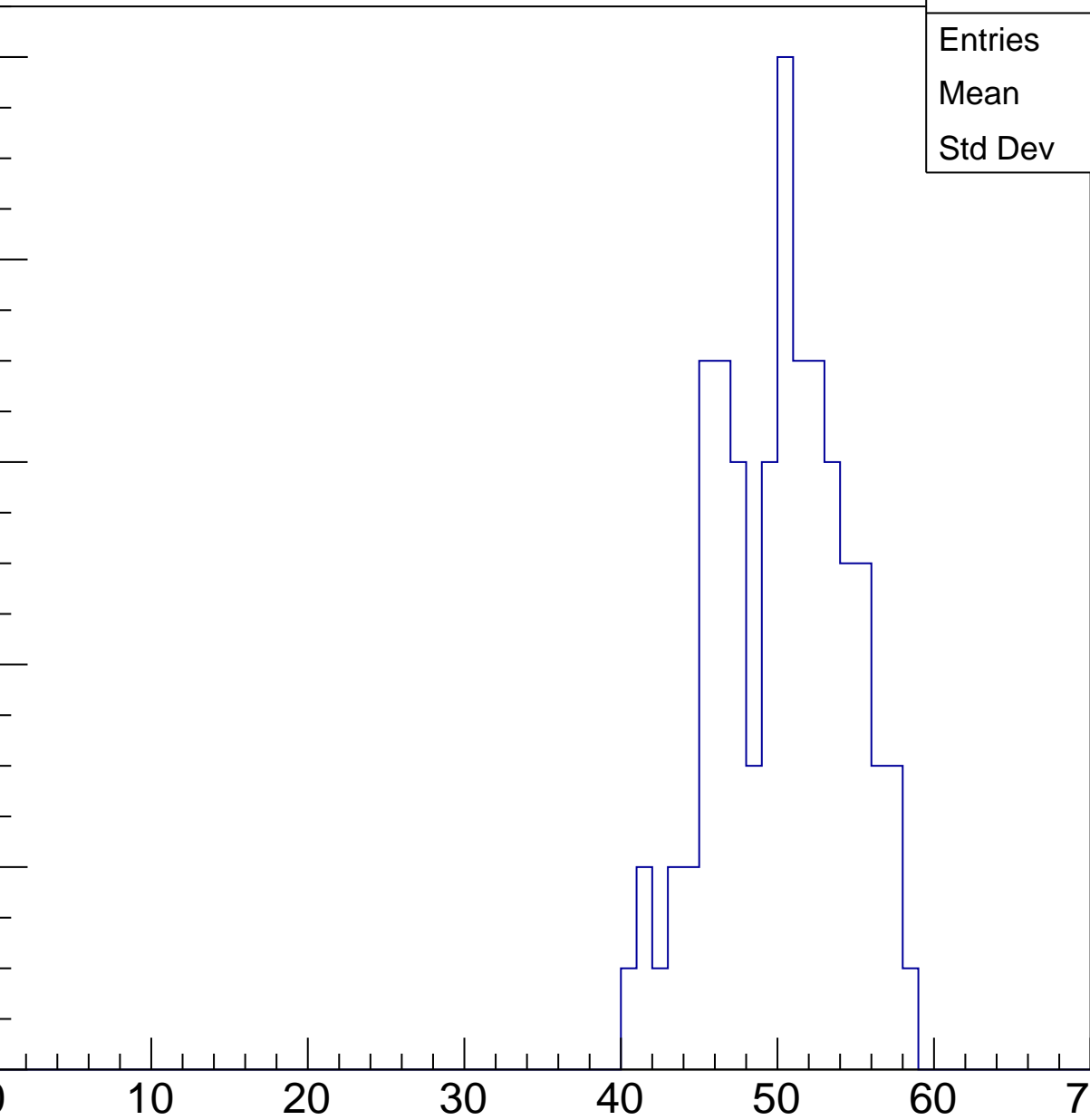
Entries	84
Mean	49.71
Std Dev	4.171

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

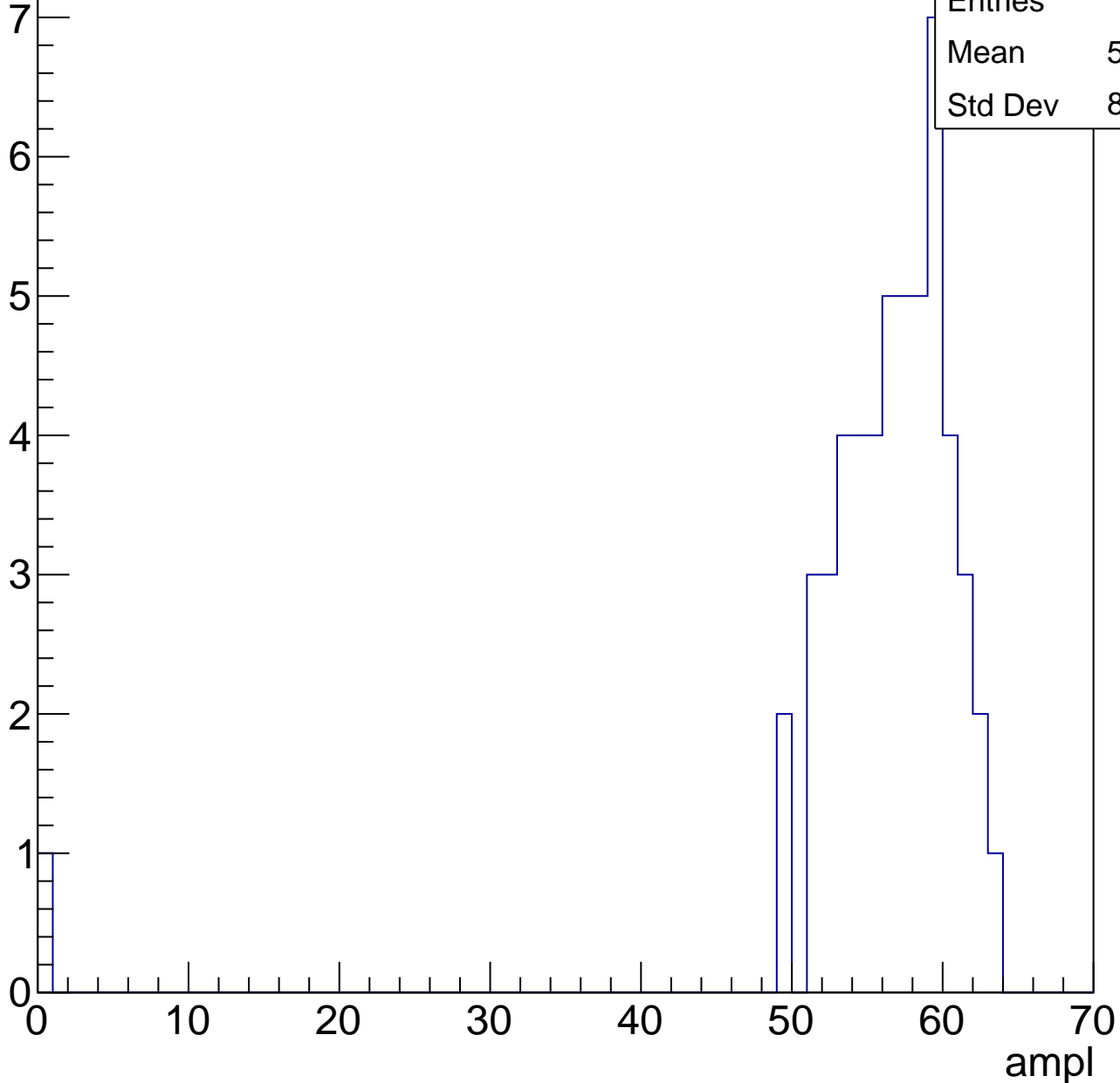


B1L101S, U25-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.34
Std Dev	8.398



B1L101S, U25-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

60.03

Std Dev

2.41

0

ampl

0

10

20

30

40

50

60

70

B1L101S, U25-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L101S, U25-ch59, adc0

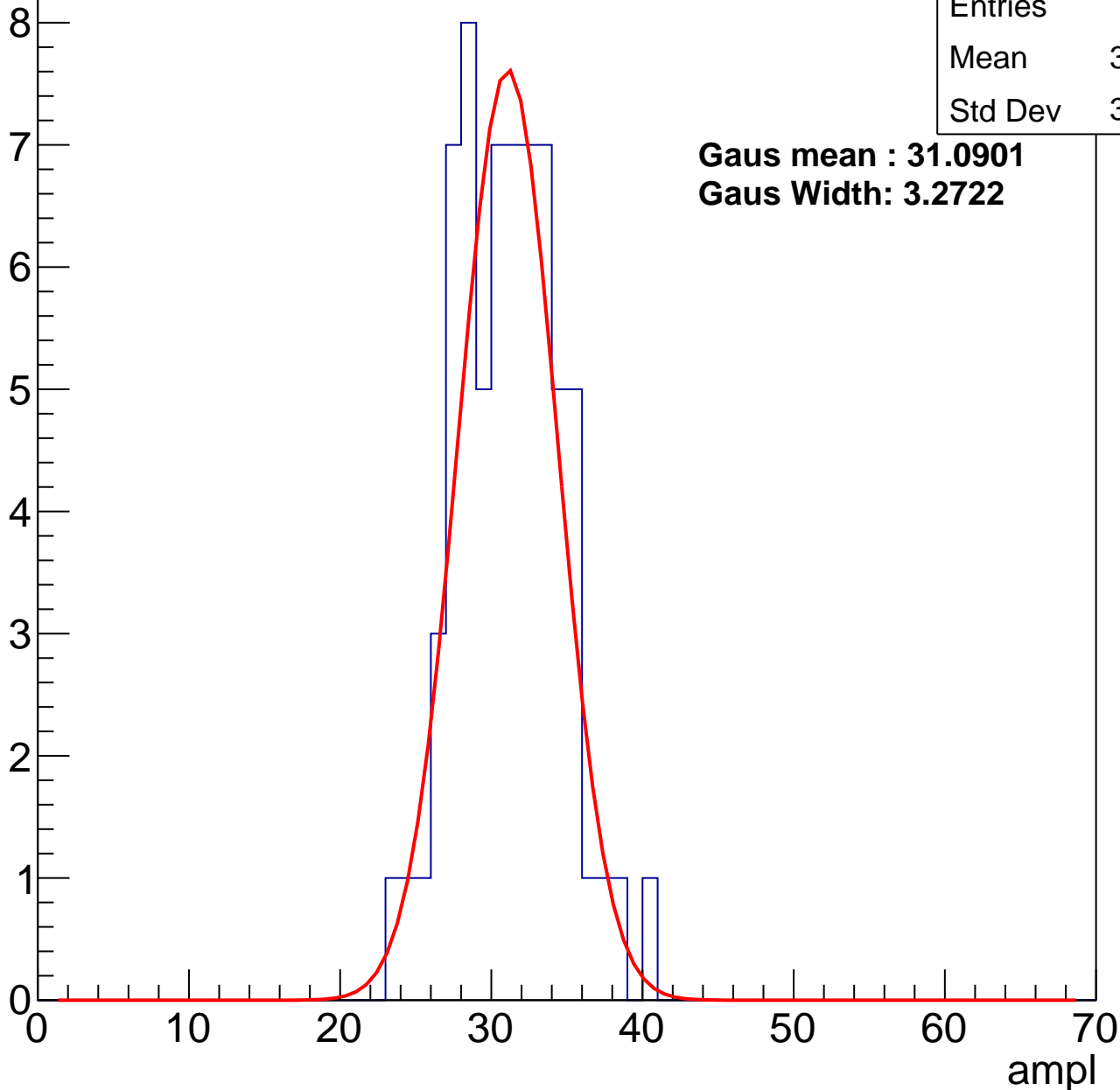
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.68
Std Dev	3.384

Gaus mean : 31.0901

Gaus Width: 3.2722



B1L101S, U25-ch59, adc1

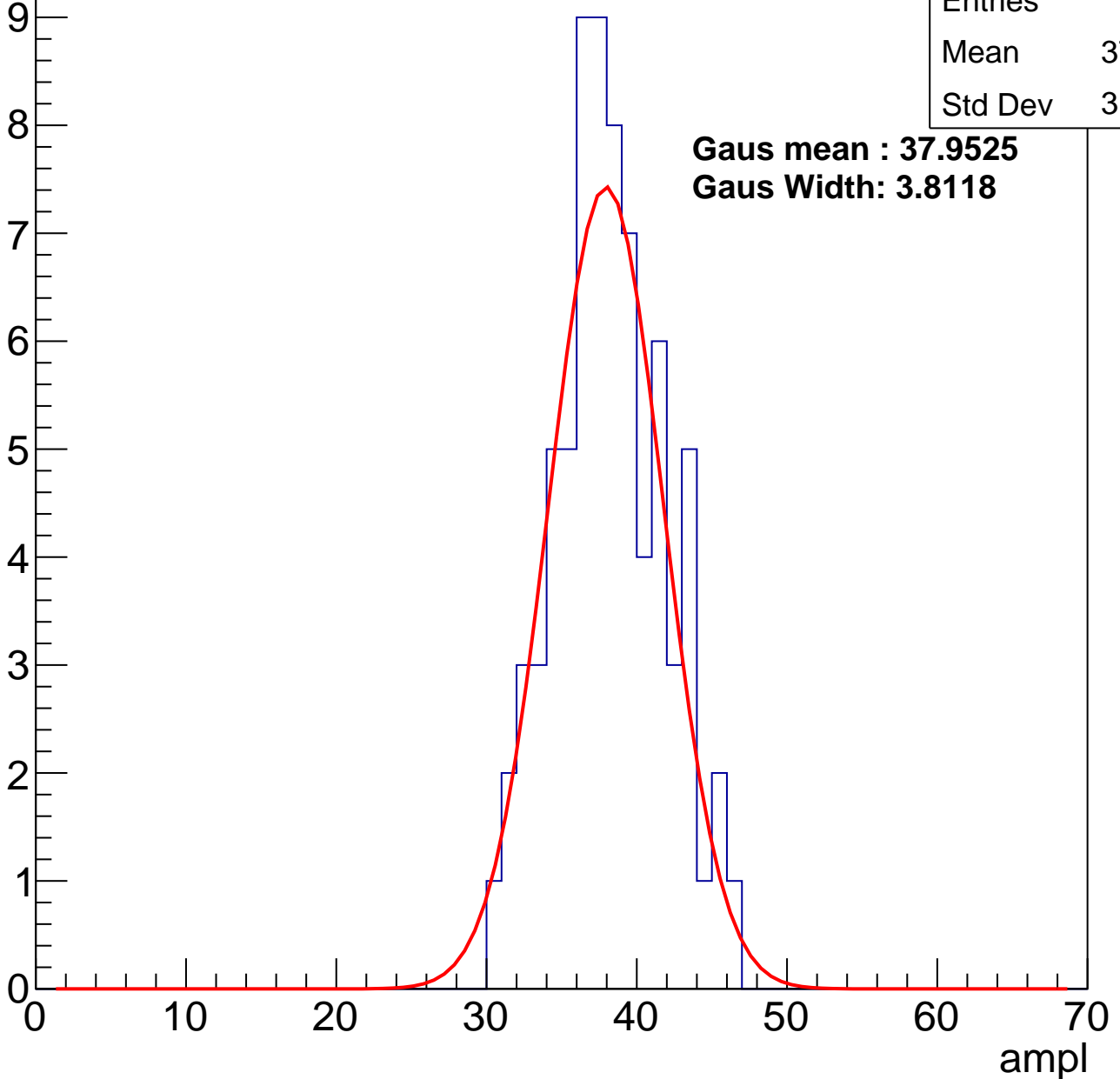
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.74
Std Dev	3.598

Gaus mean : 37.9525

Gaus Width: 3.8118



B1L101S, U25-ch59, adc2

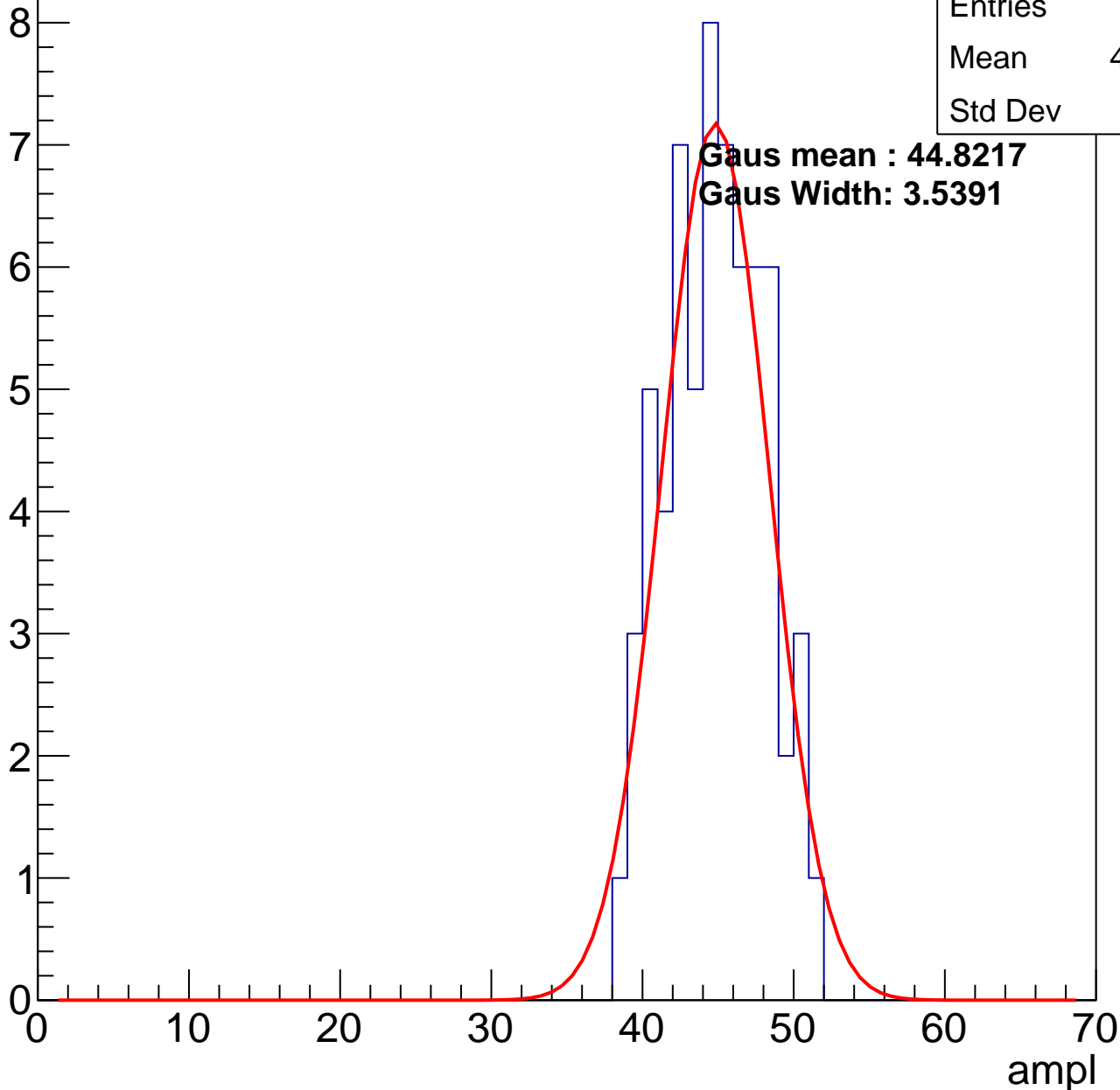
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.38
Std Dev	3.16

Gaus mean : 44.8217

Gaus Width: 3.5391

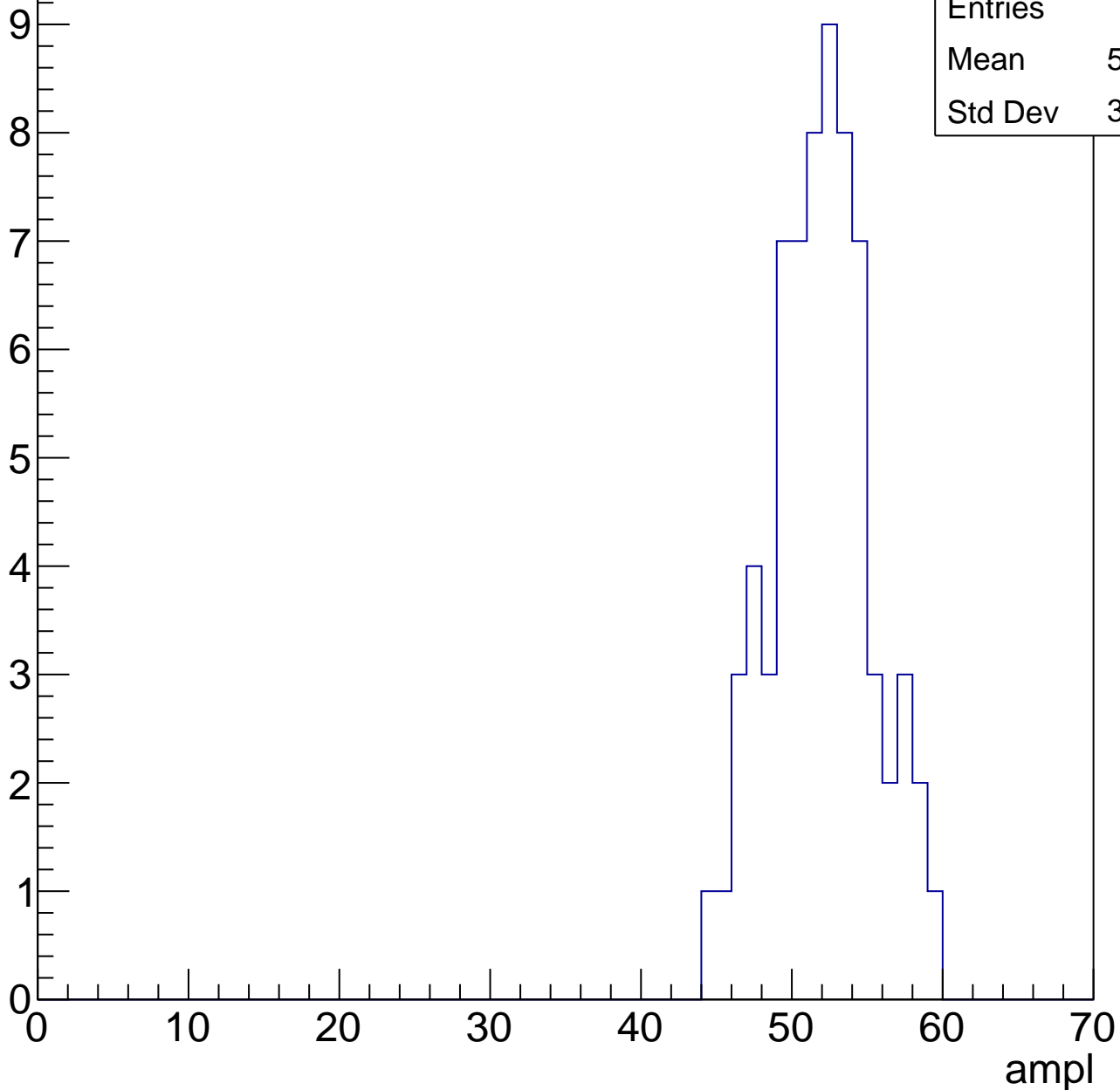


B1L101S, U25-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	51.49
Std Dev	3.278

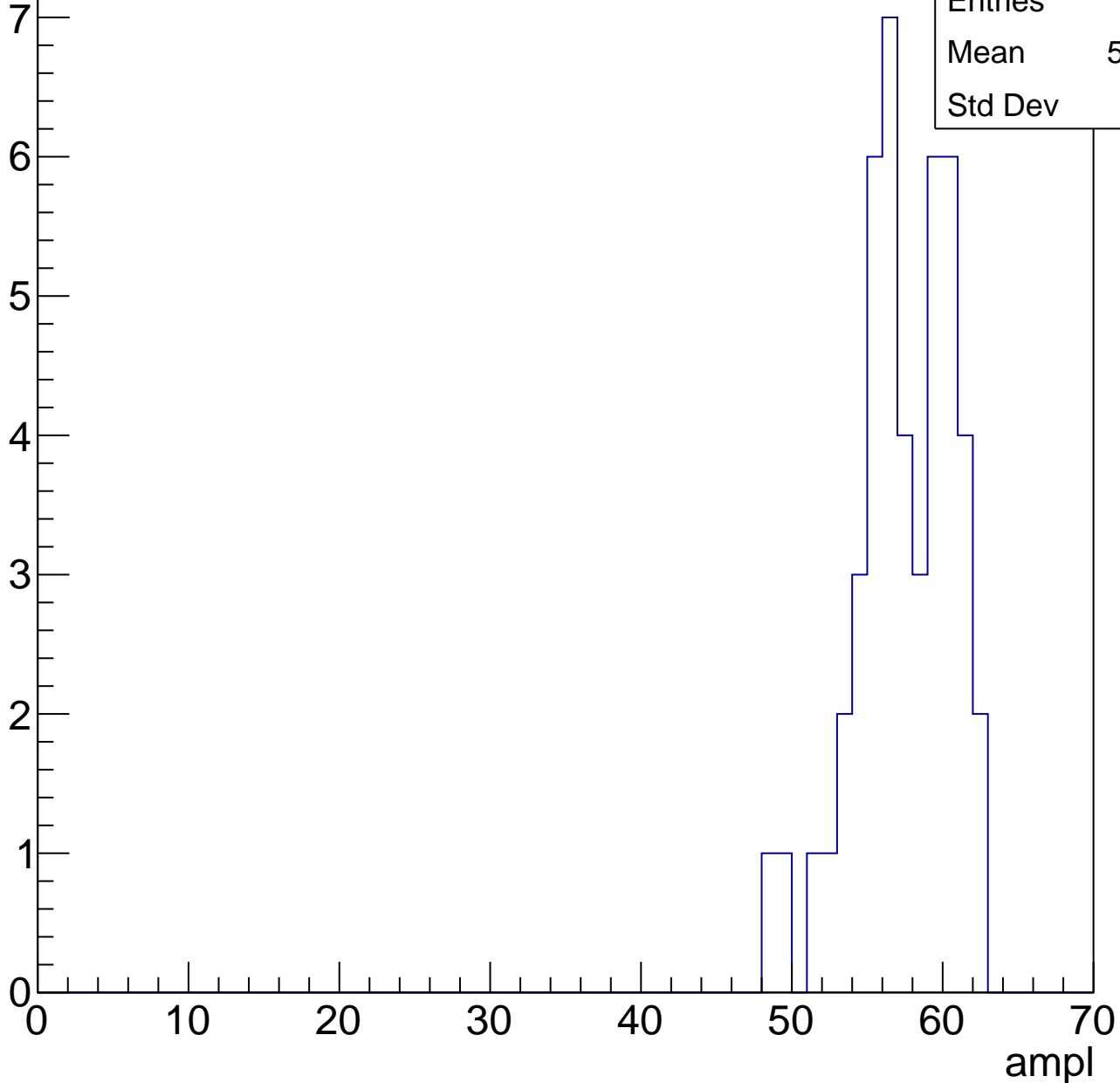


B1L101S, U25-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	56.89
Std Dev	3.23

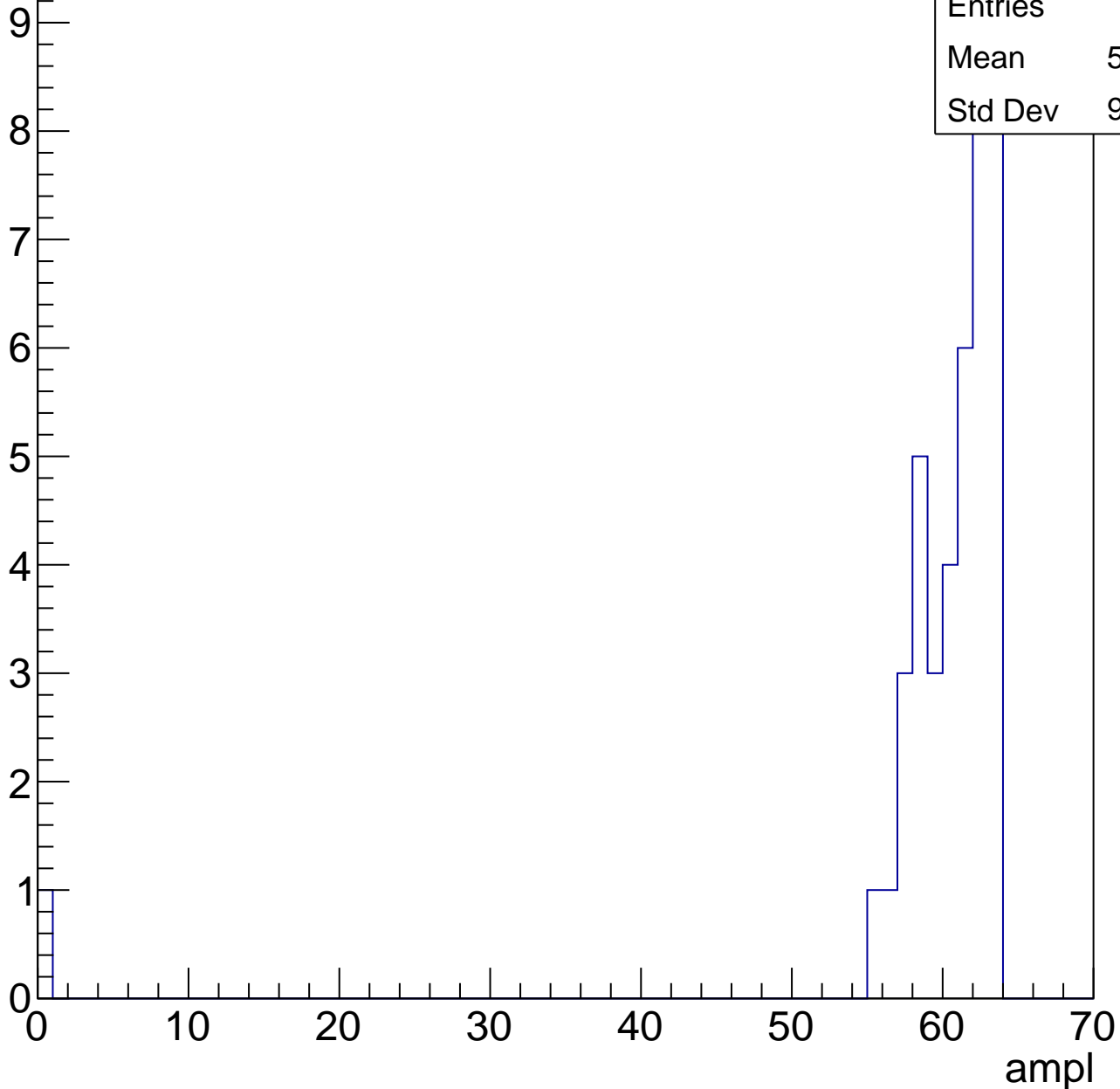


B1L101S, U25-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.98
Std Dev	9.585



B1L101S, U25-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

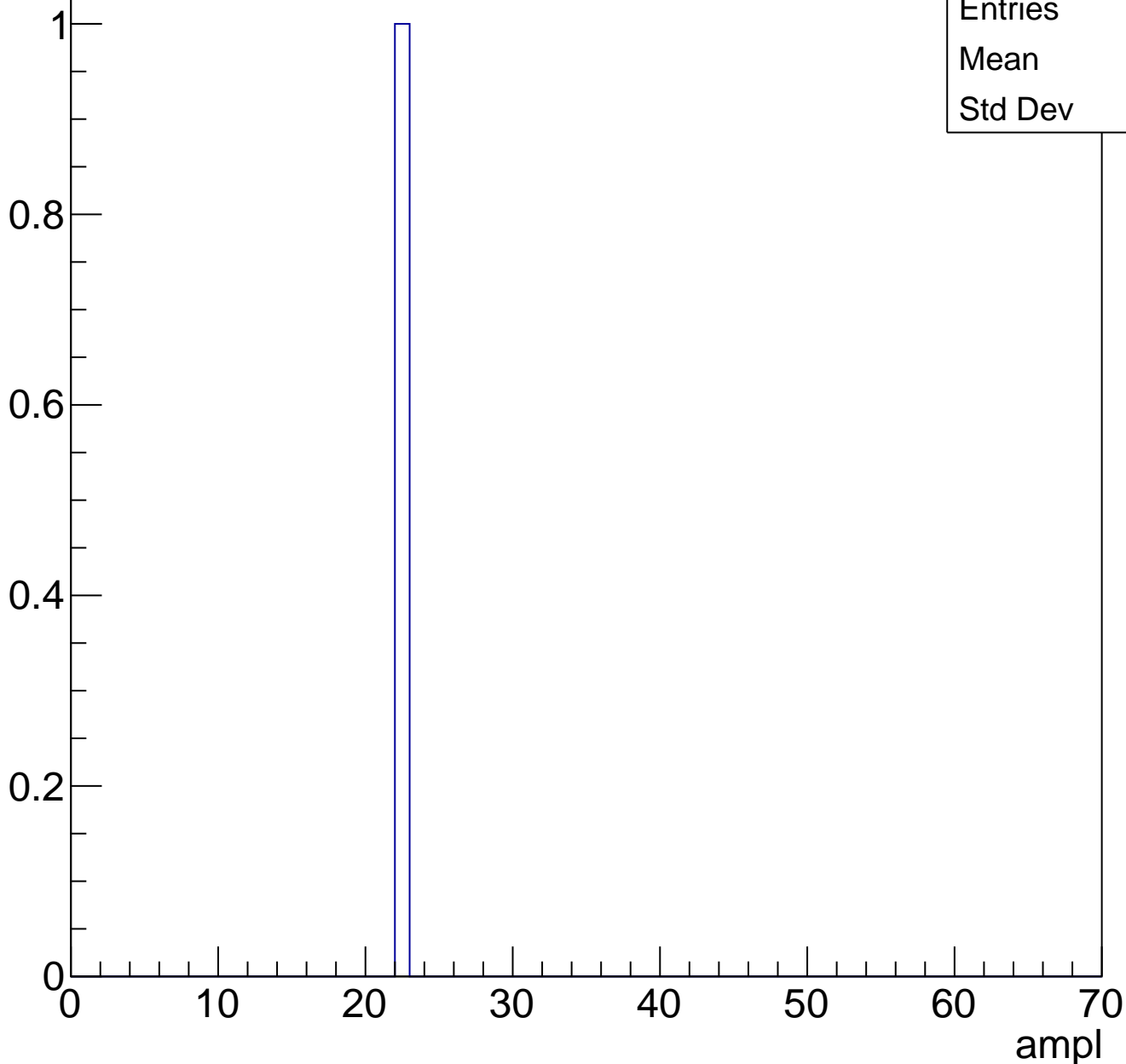
Entry



B1L101S, U25-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U25-ch60, adc0

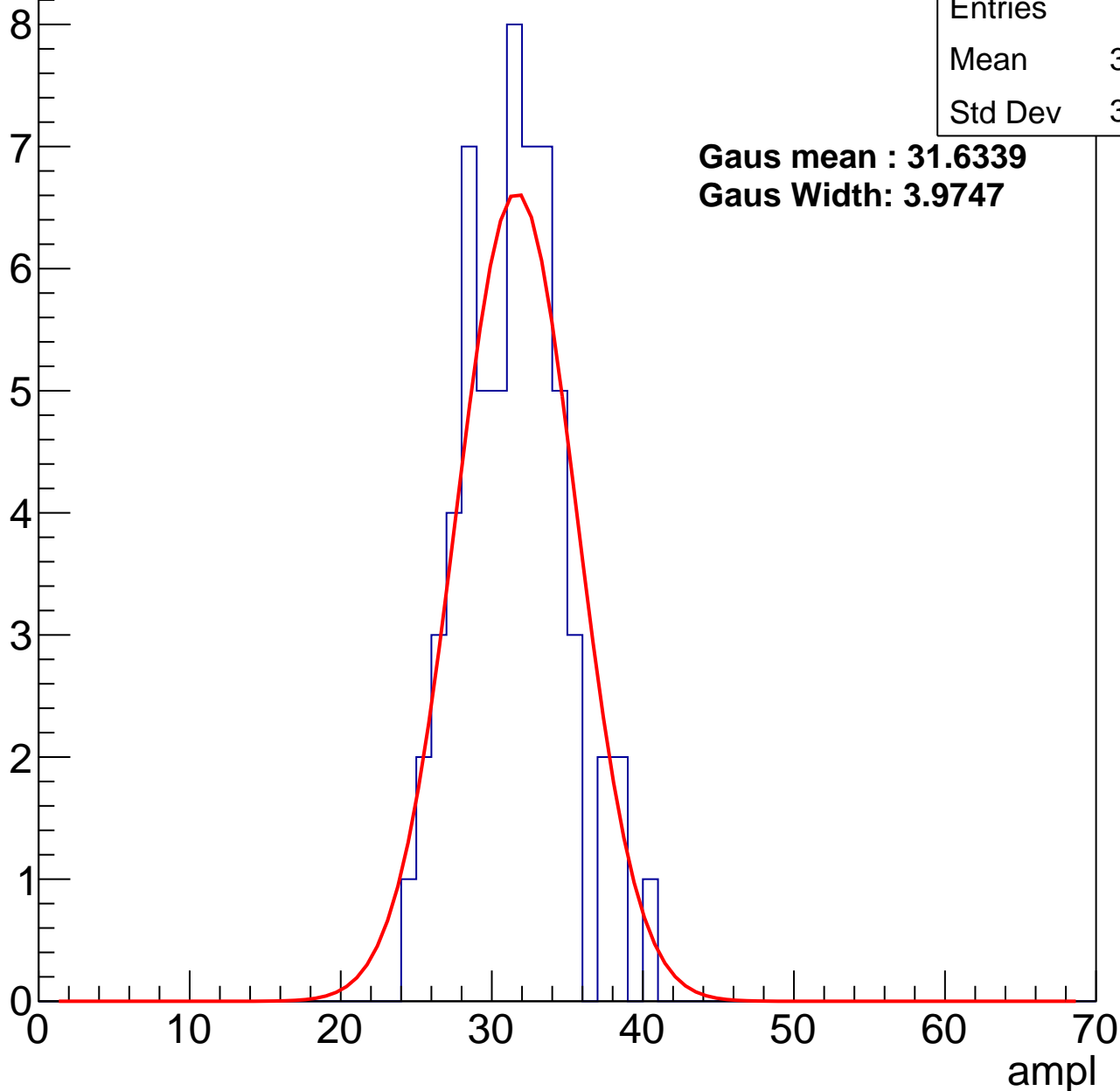
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	30.95
Std Dev	3.424

Gaus mean : 31.6339

Gaus Width: 3.9747



B1L101S, U25-ch60, adc1

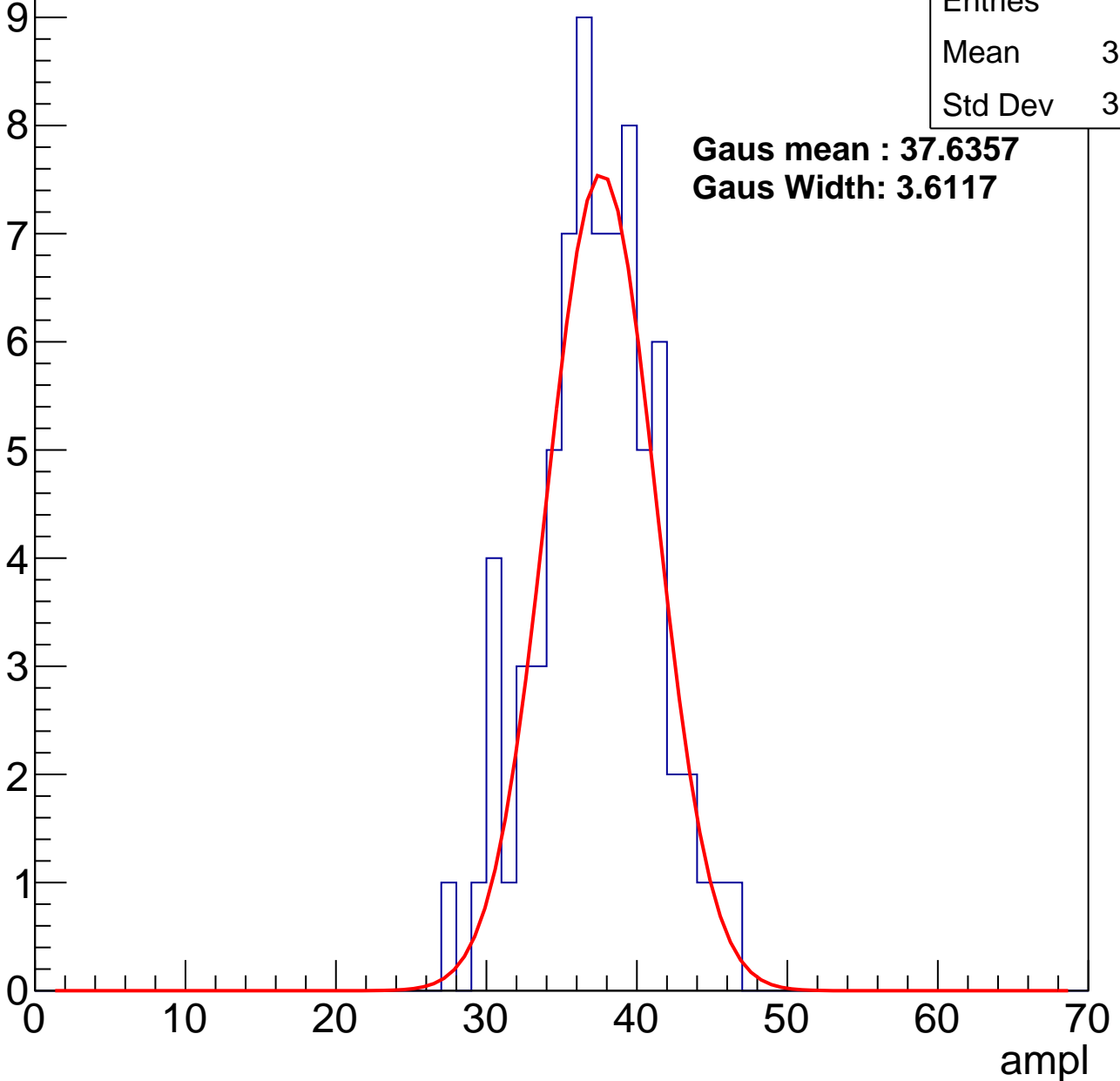
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.88
Std Dev	3.848

Gaus mean : 37.6357

Gaus Width: 3.6117



B1L101S, U25-ch60, adc2

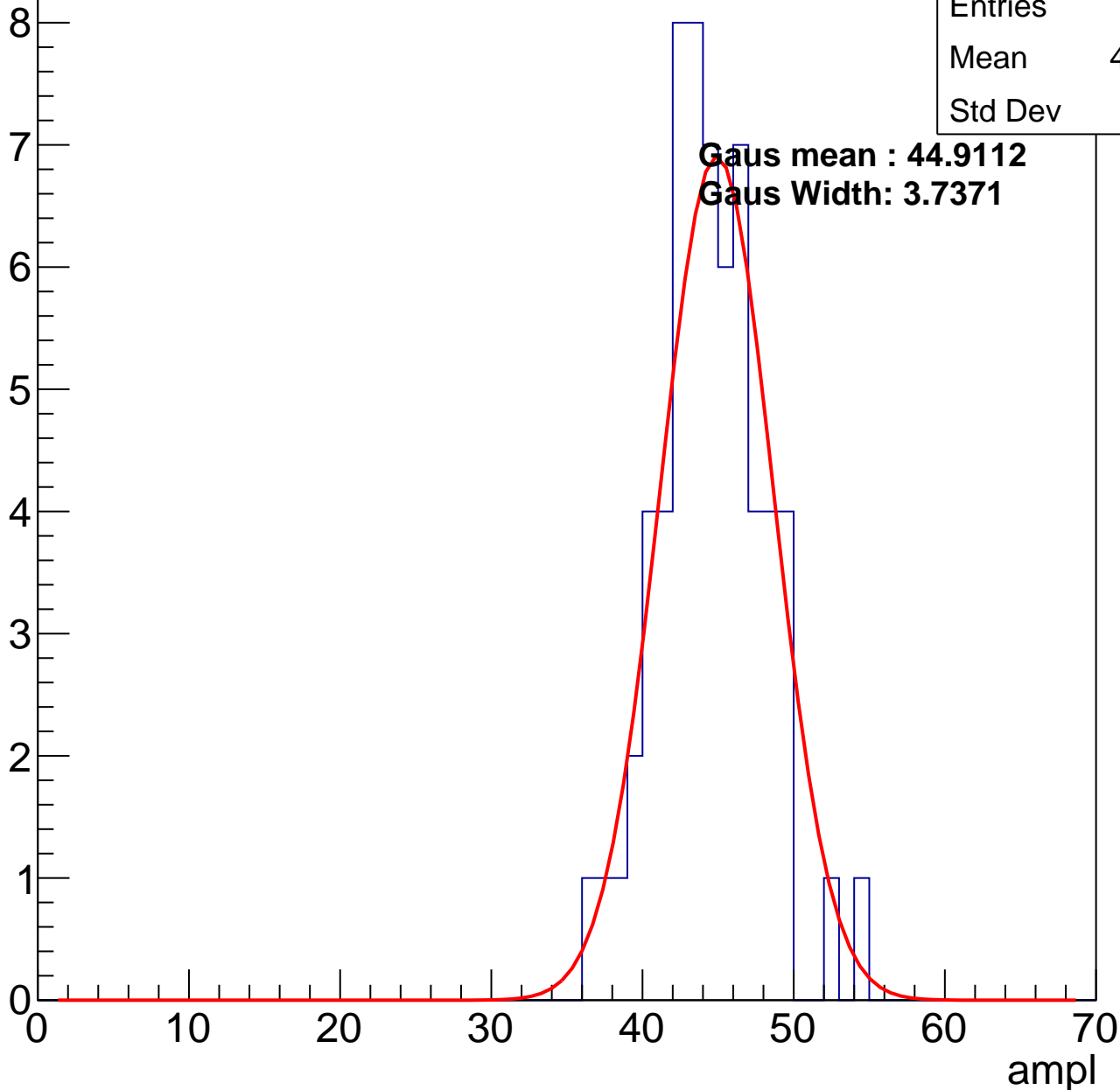
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.05
Std Dev	3.42

Gaus mean : 44.9112

Gaus Width: 3.7371

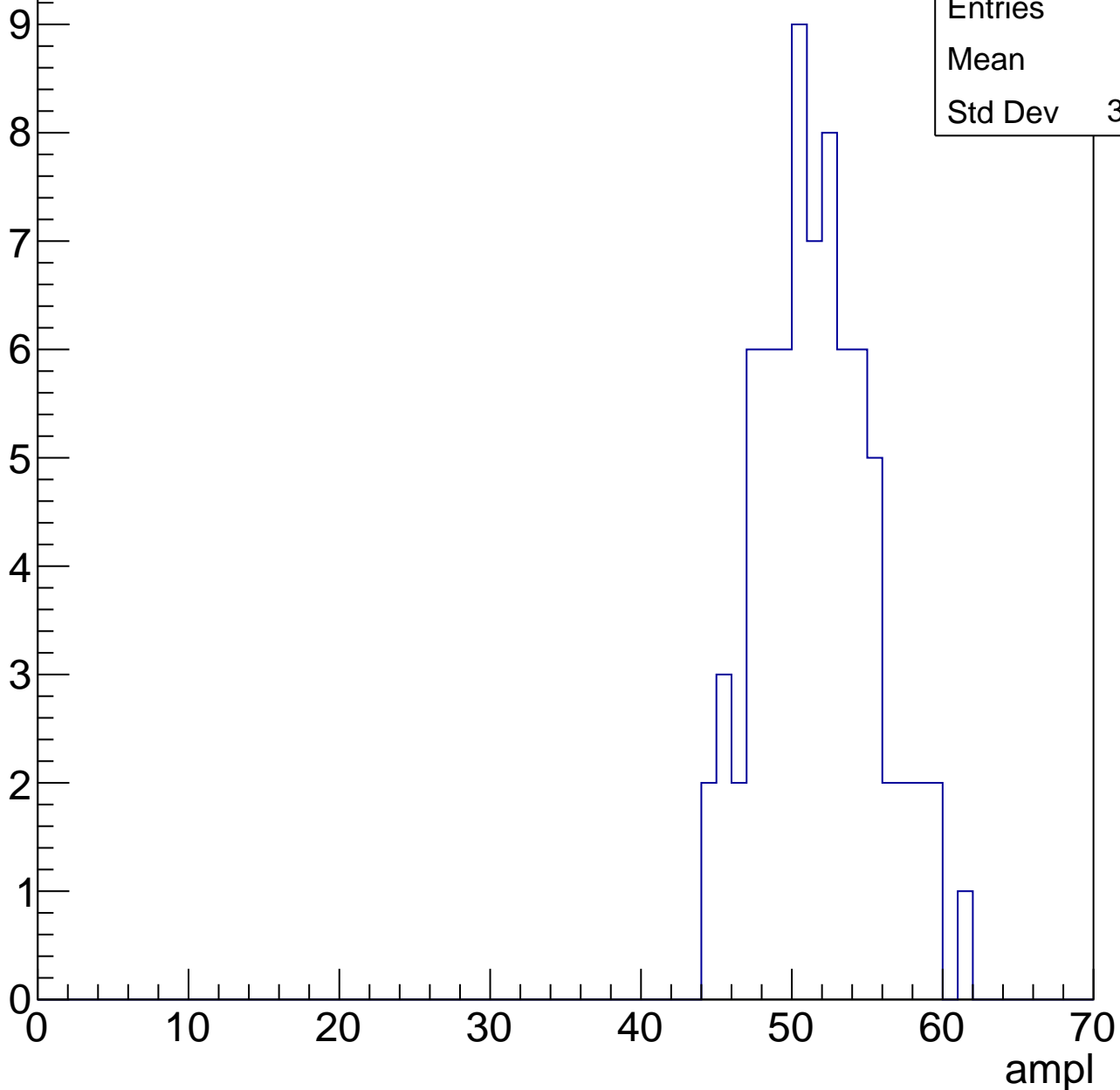


B1L101S, U25-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	51.2
Std Dev	3.742

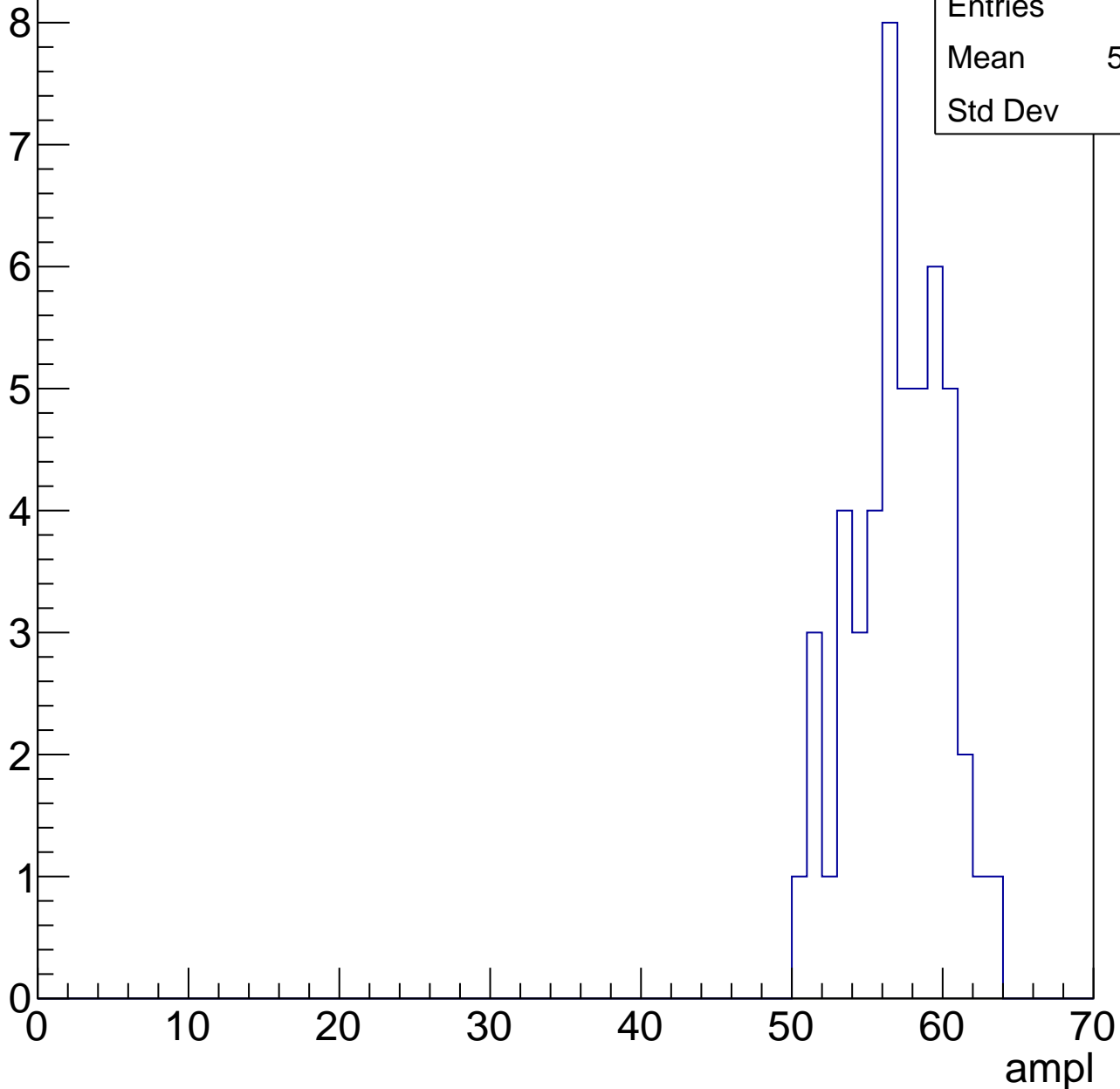


B1L101S, U25-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.59
Std Dev	3.05

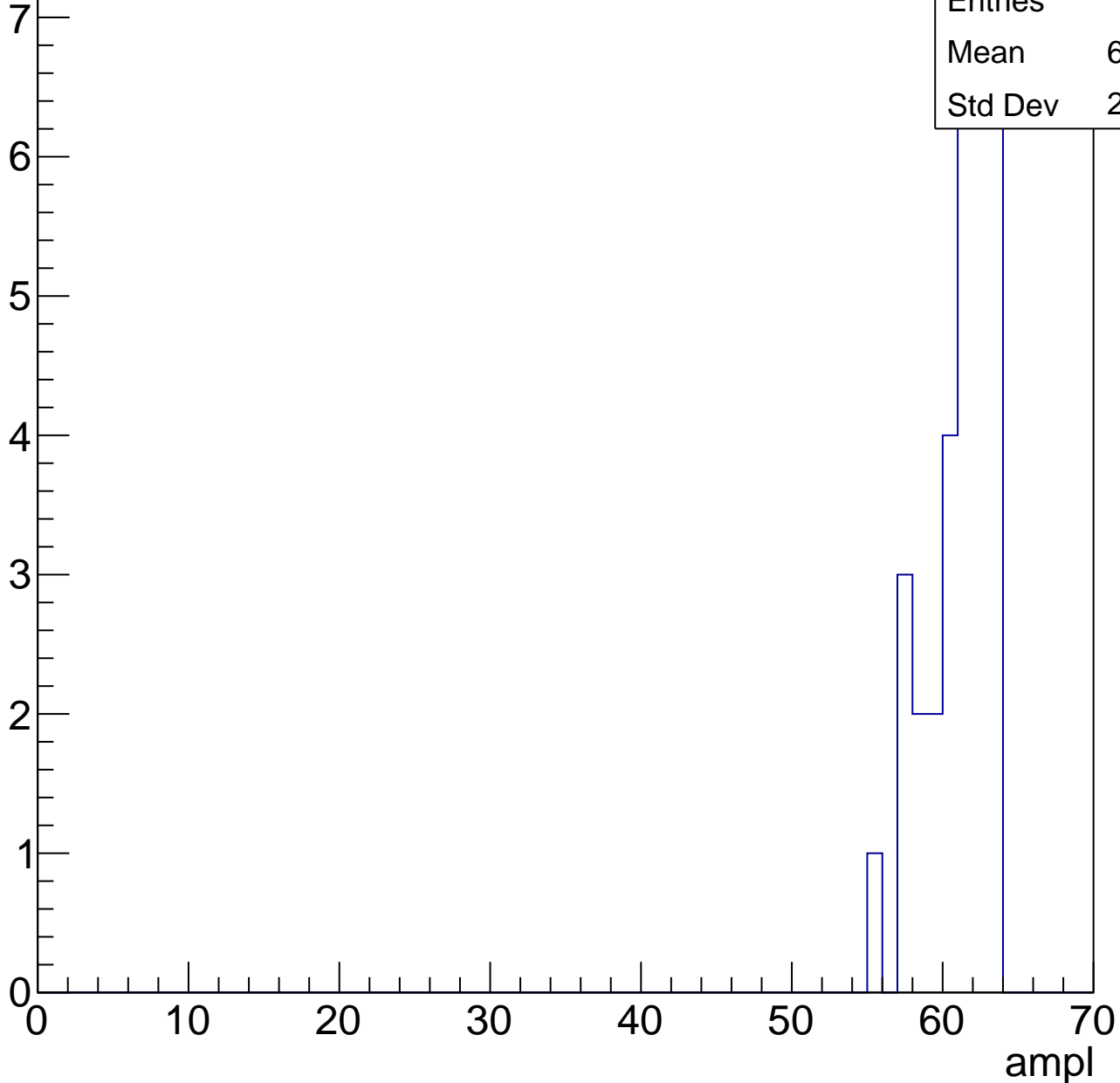


B1L101S, U25-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

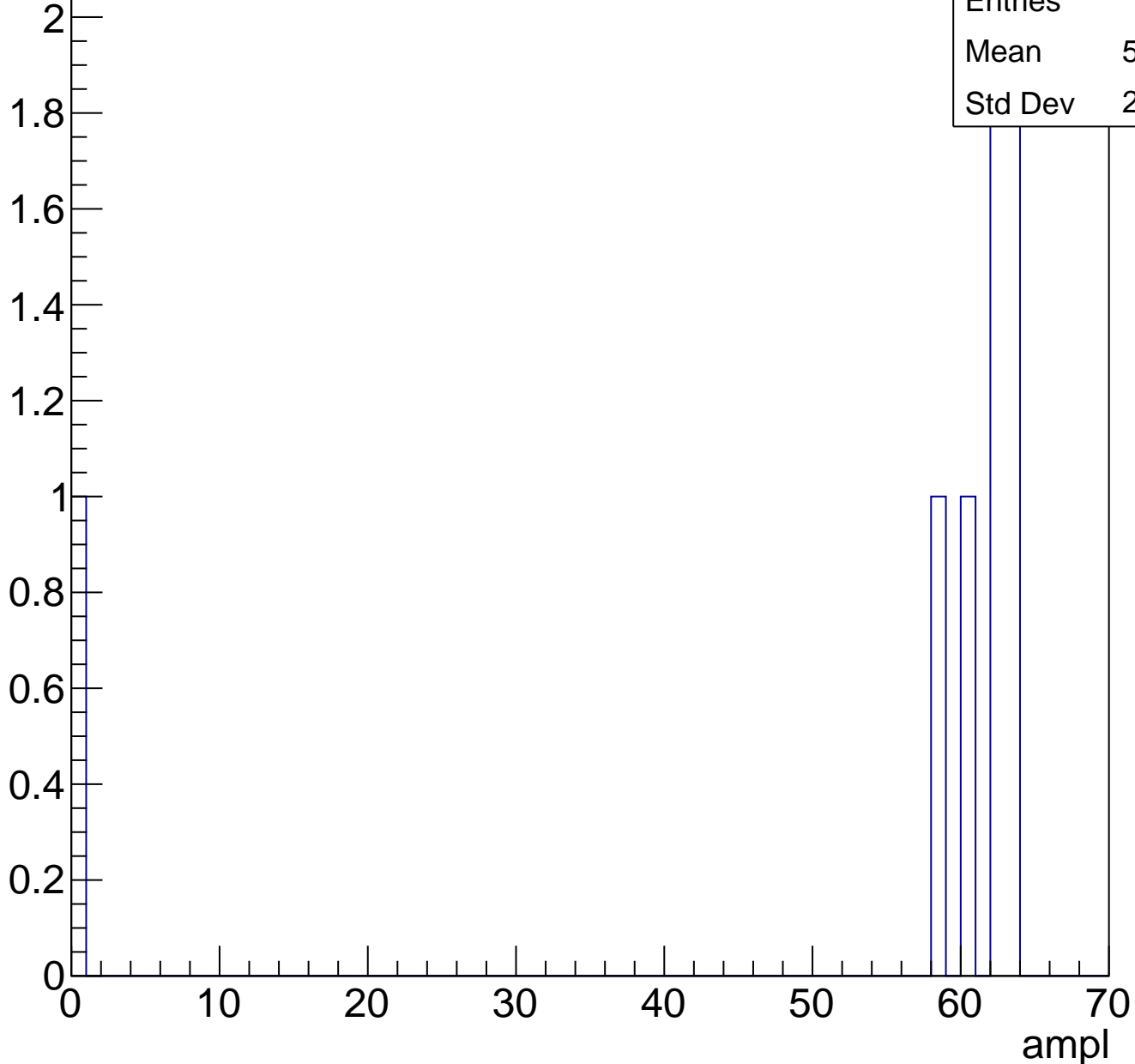
Entries	33
Mean	60.67
Std Dev	2.099



B1L101S, U25-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

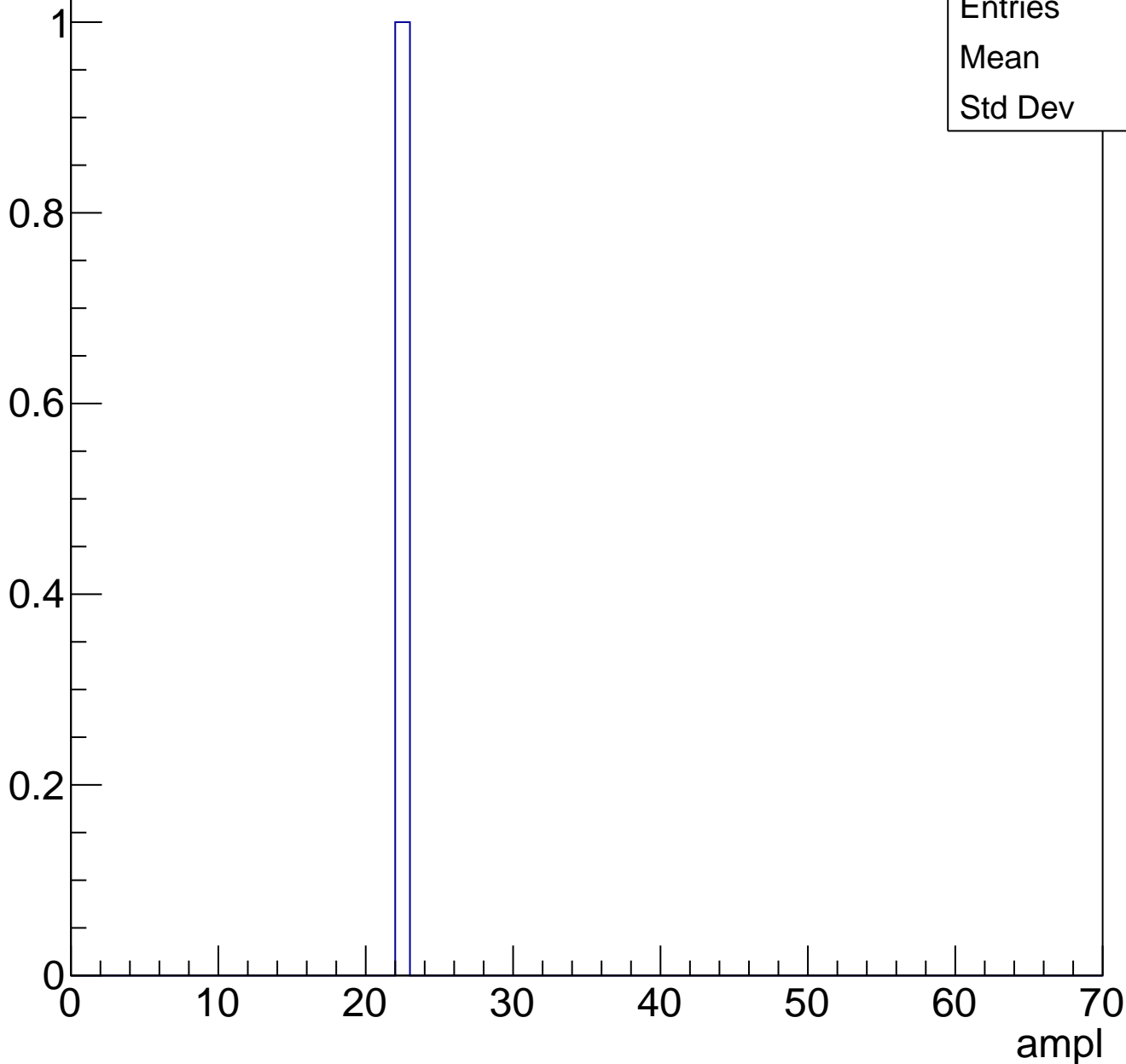


Entries	7
Mean	52.57
Std Dev	21.53

B1L101S, U25-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch61, adc0

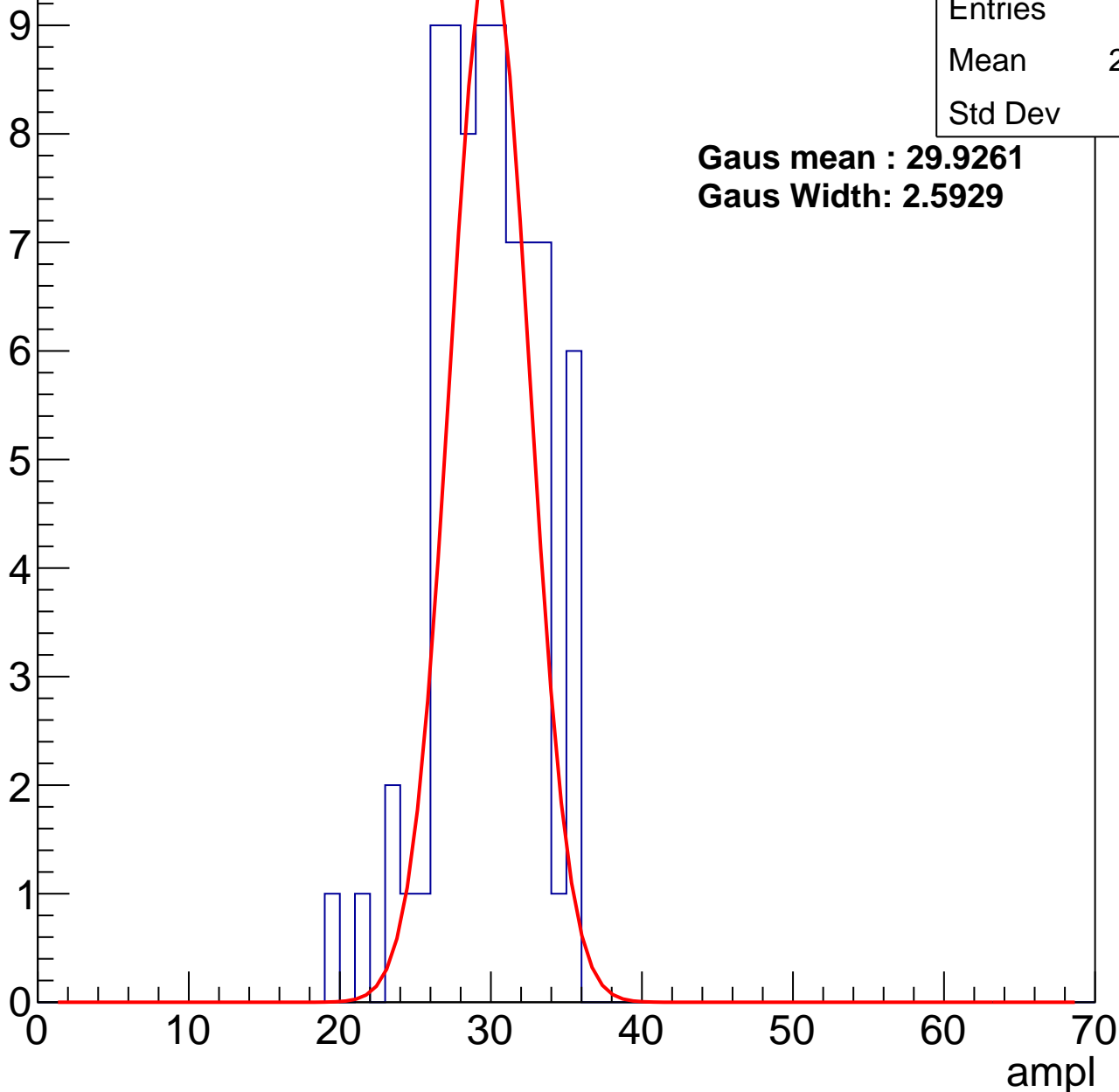
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.27
Std Dev	3.3

Gaus mean : 29.9261

Gaus Width: 2.5929



B1L101S, U25-ch61, adc1

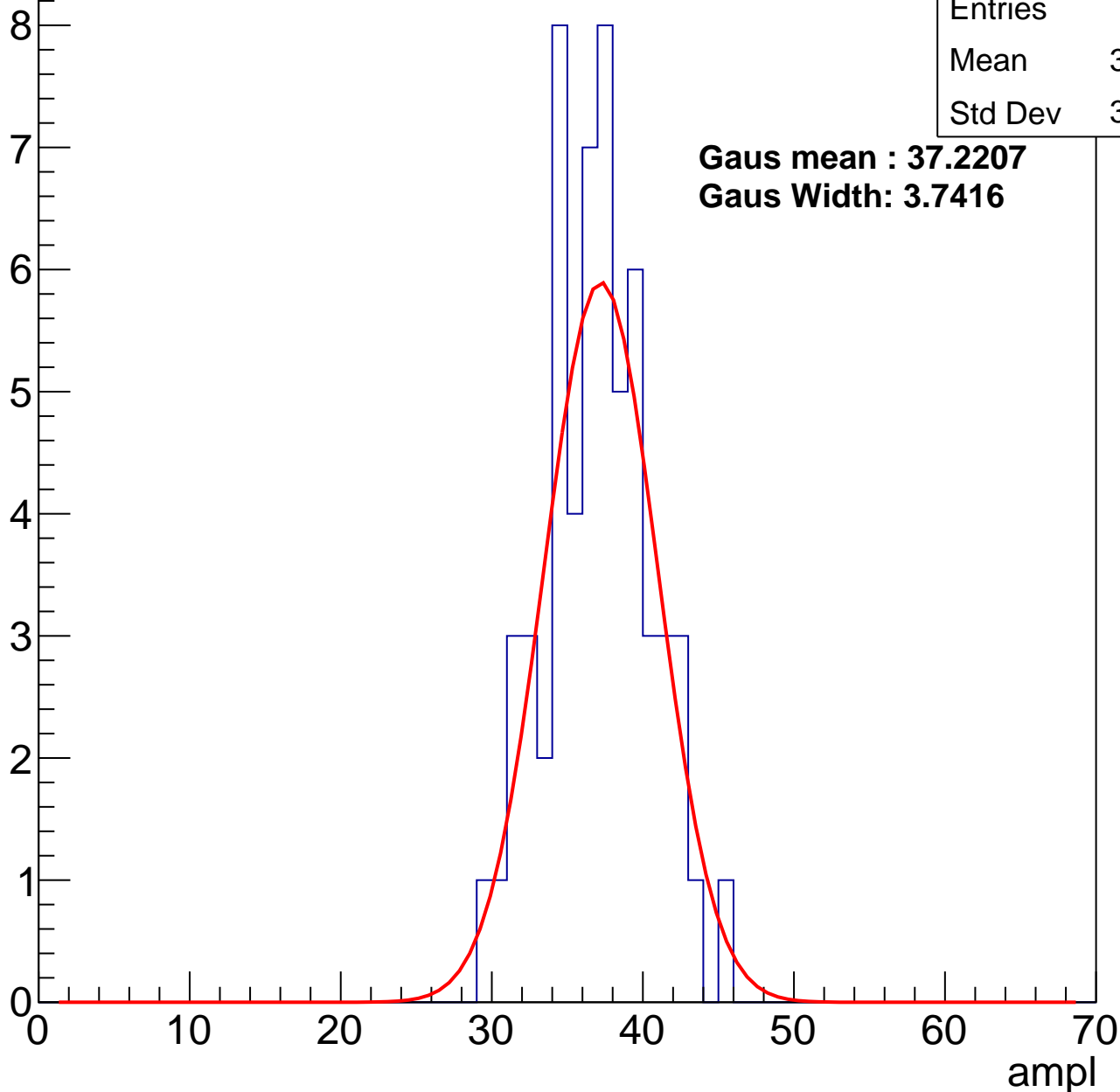
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.53
Std Dev	3.417

Gaus mean : 37.2207

Gaus Width: 3.7416



B1L101S, U25-ch61, adc2

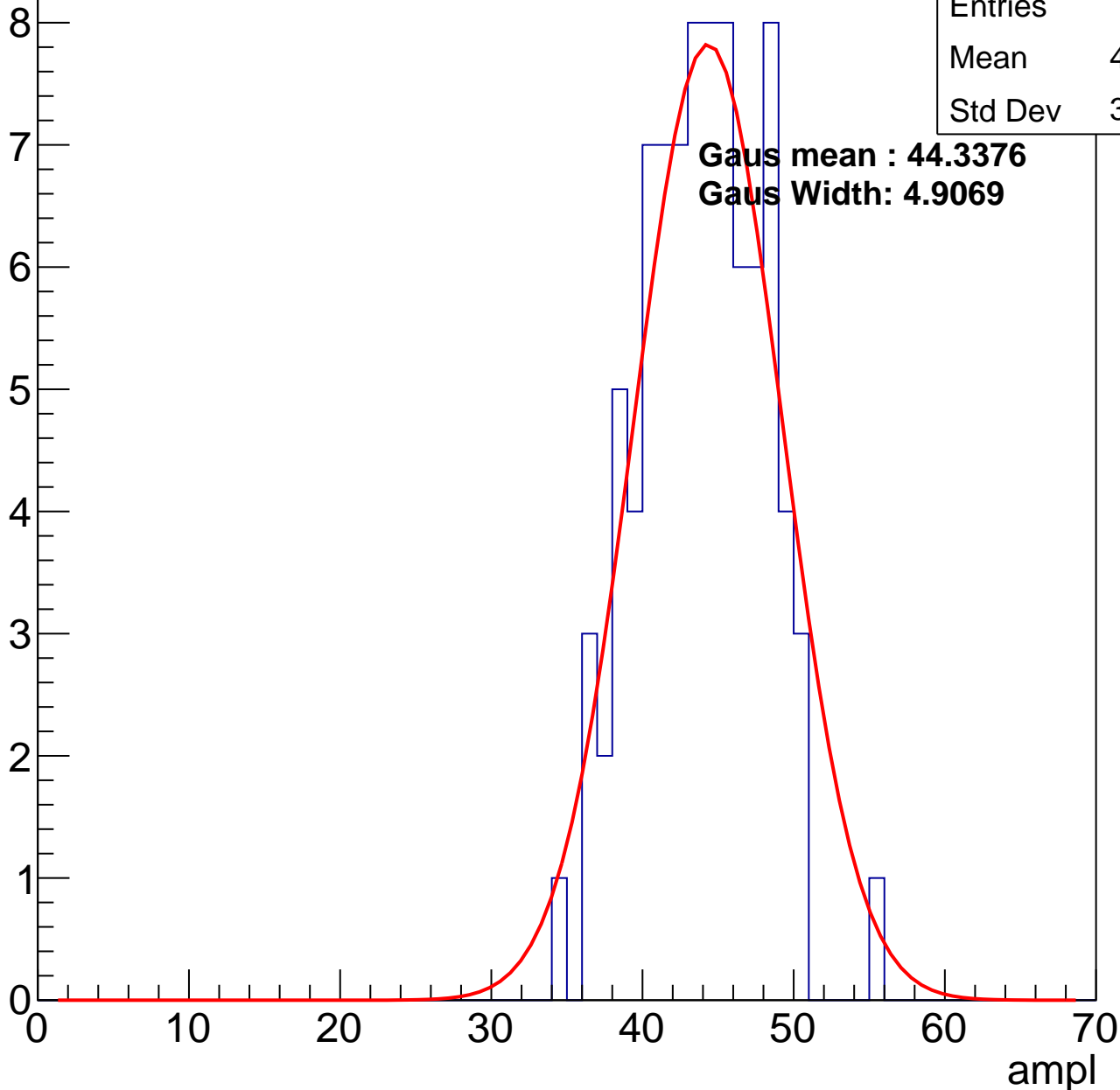
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	43.43
Std Dev	3.982

Gaus mean : 44.3376

Gaus Width: 4.9069

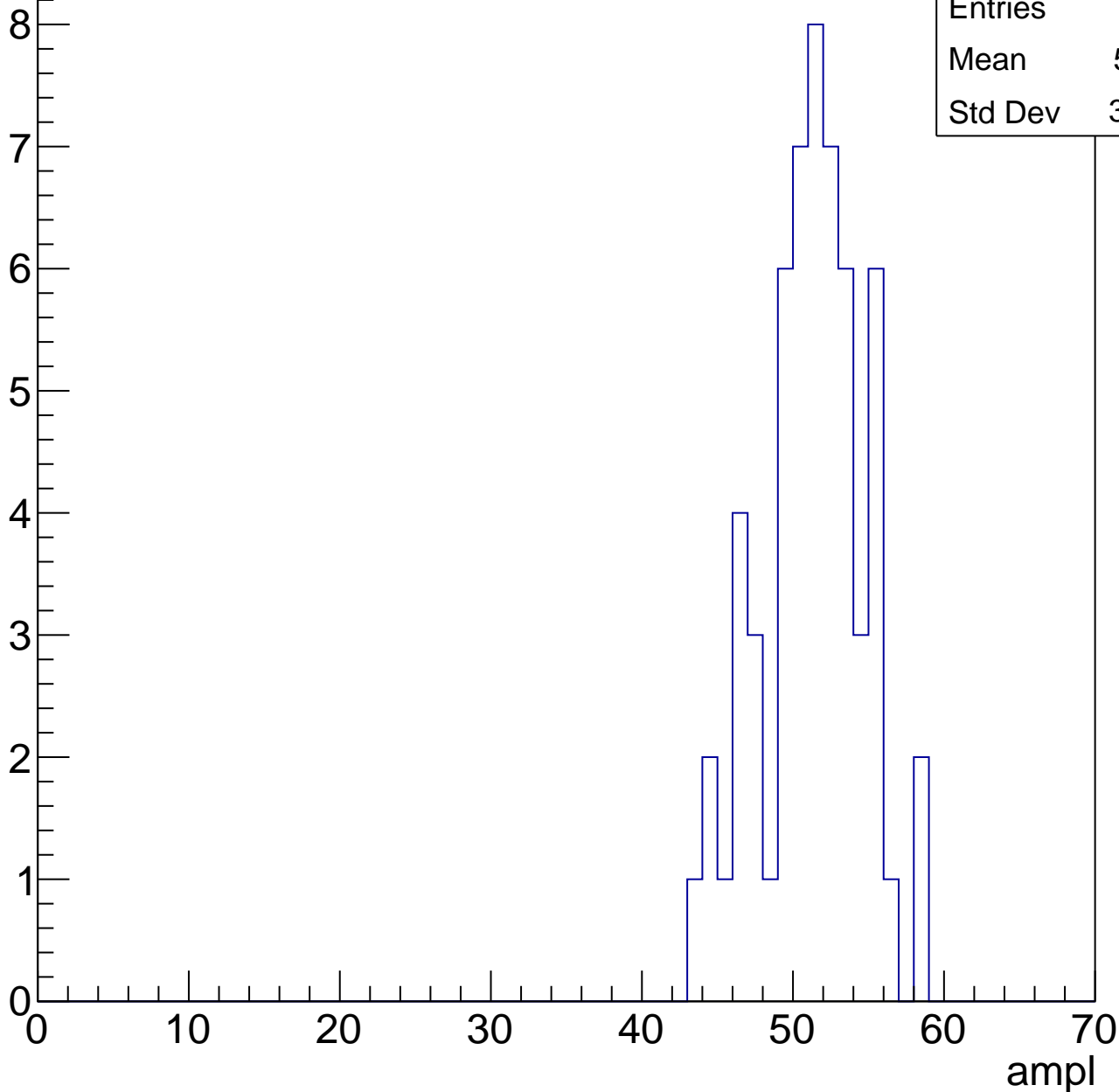


B1L101S, U25-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

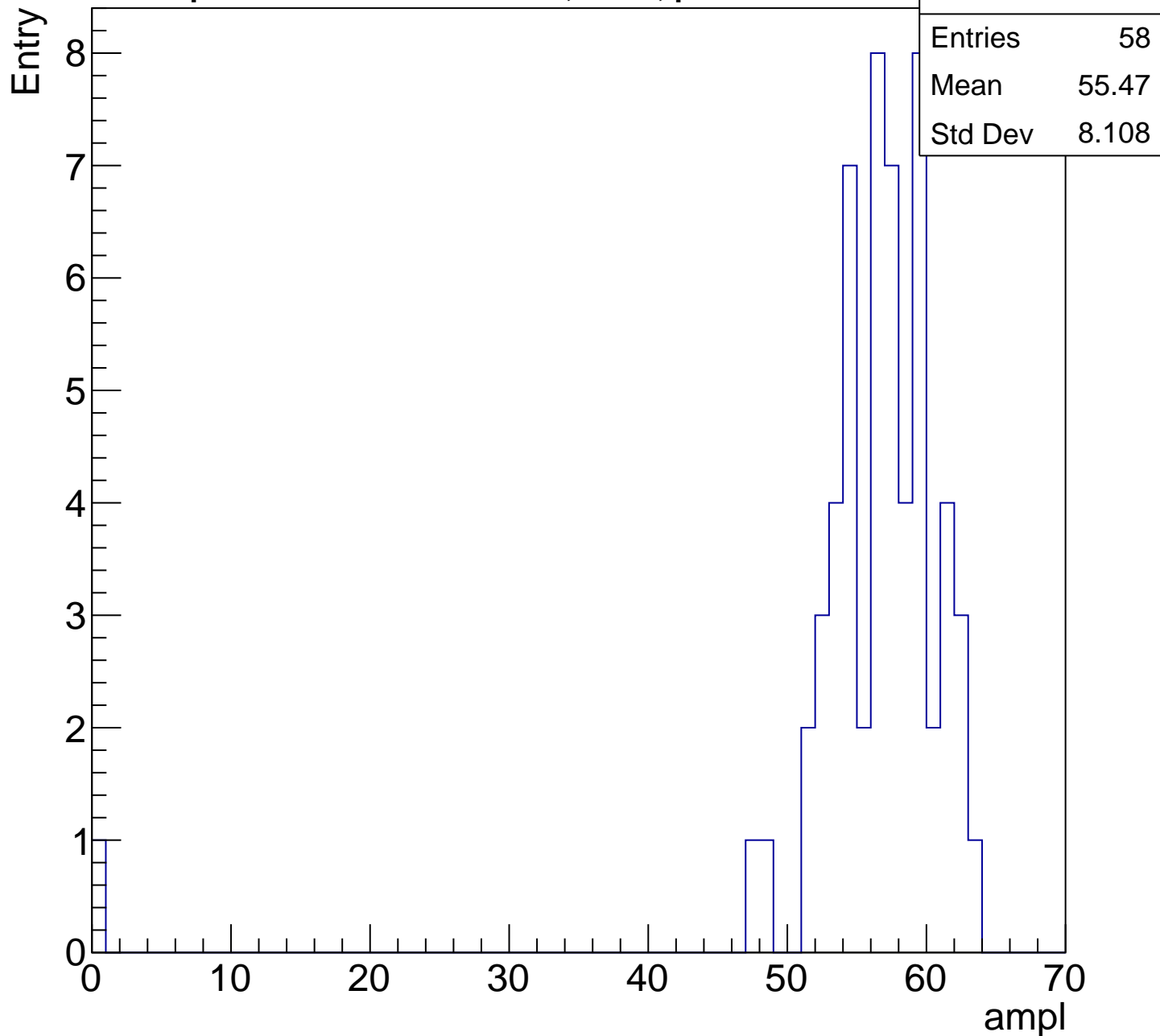
Entry

Entries	58
Mean	50.81
Std Dev	3.376



B1L101S, U25-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch61, adc5

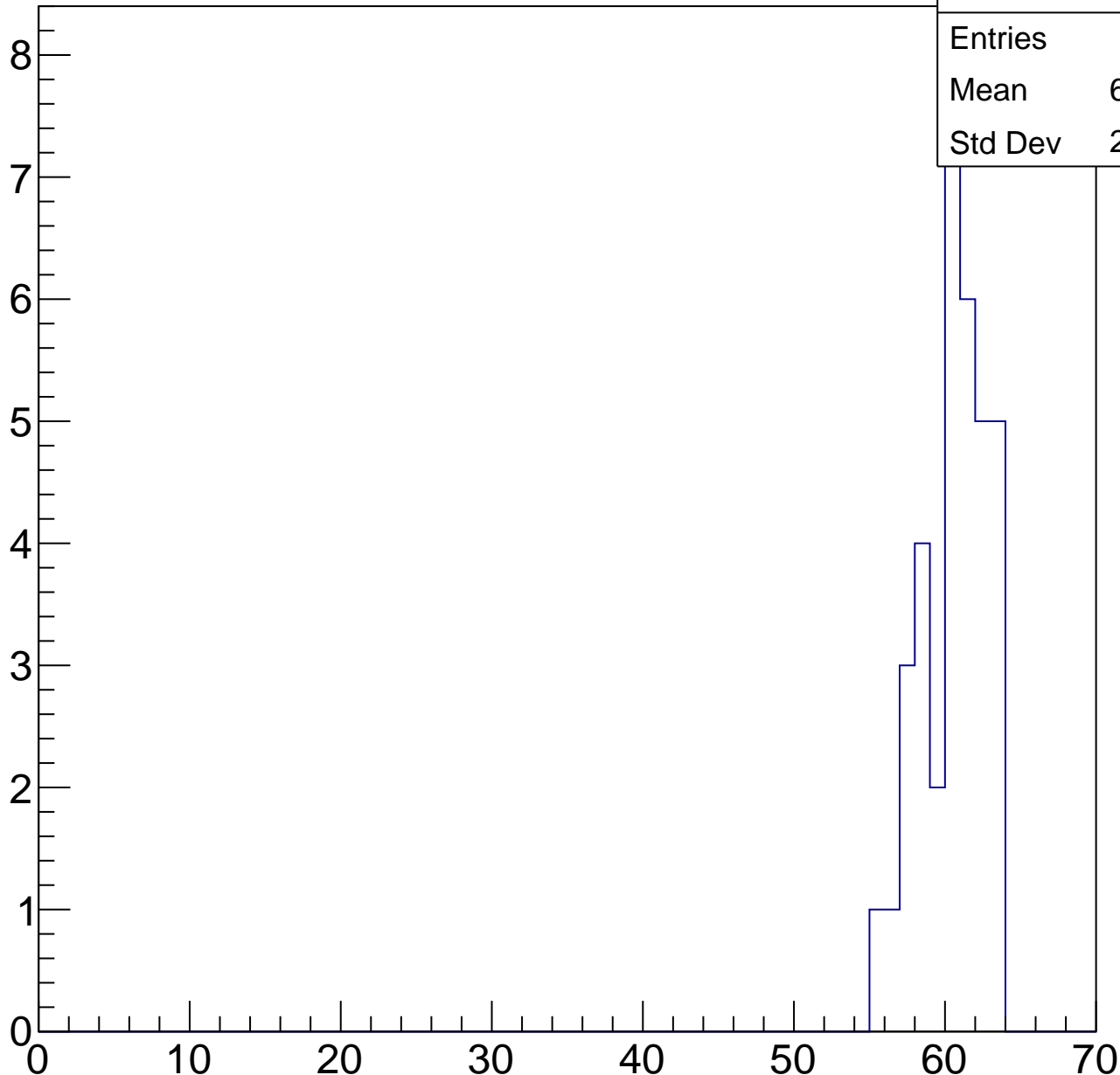
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.09
Std Dev	2.116

ampl



B1L101S, U25-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch62, adc0

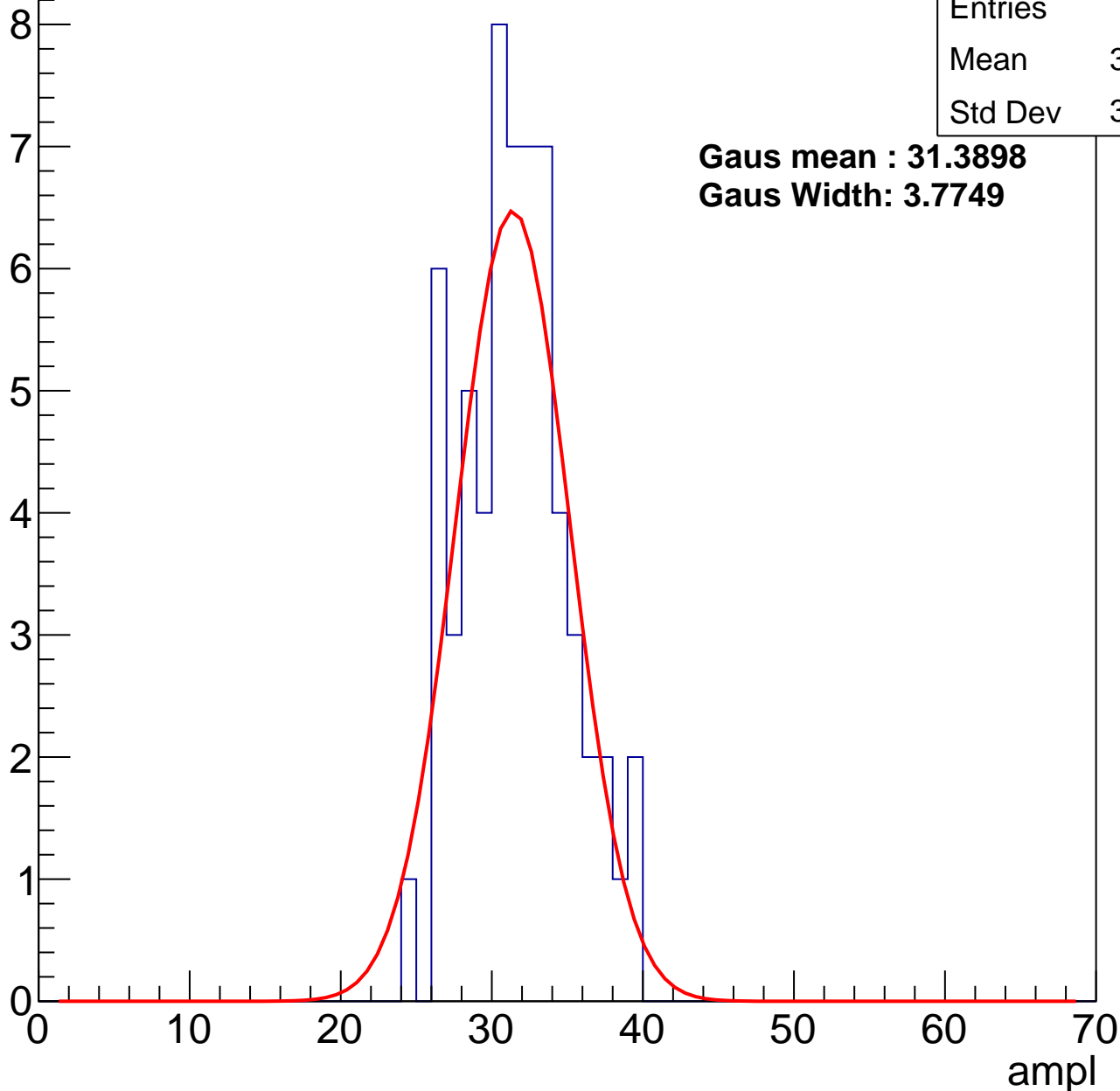
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	31.16
Std Dev	3.437

Gaus mean : 31.3898

Gaus Width: 3.7749



B1L101S, U25-ch62, adc1

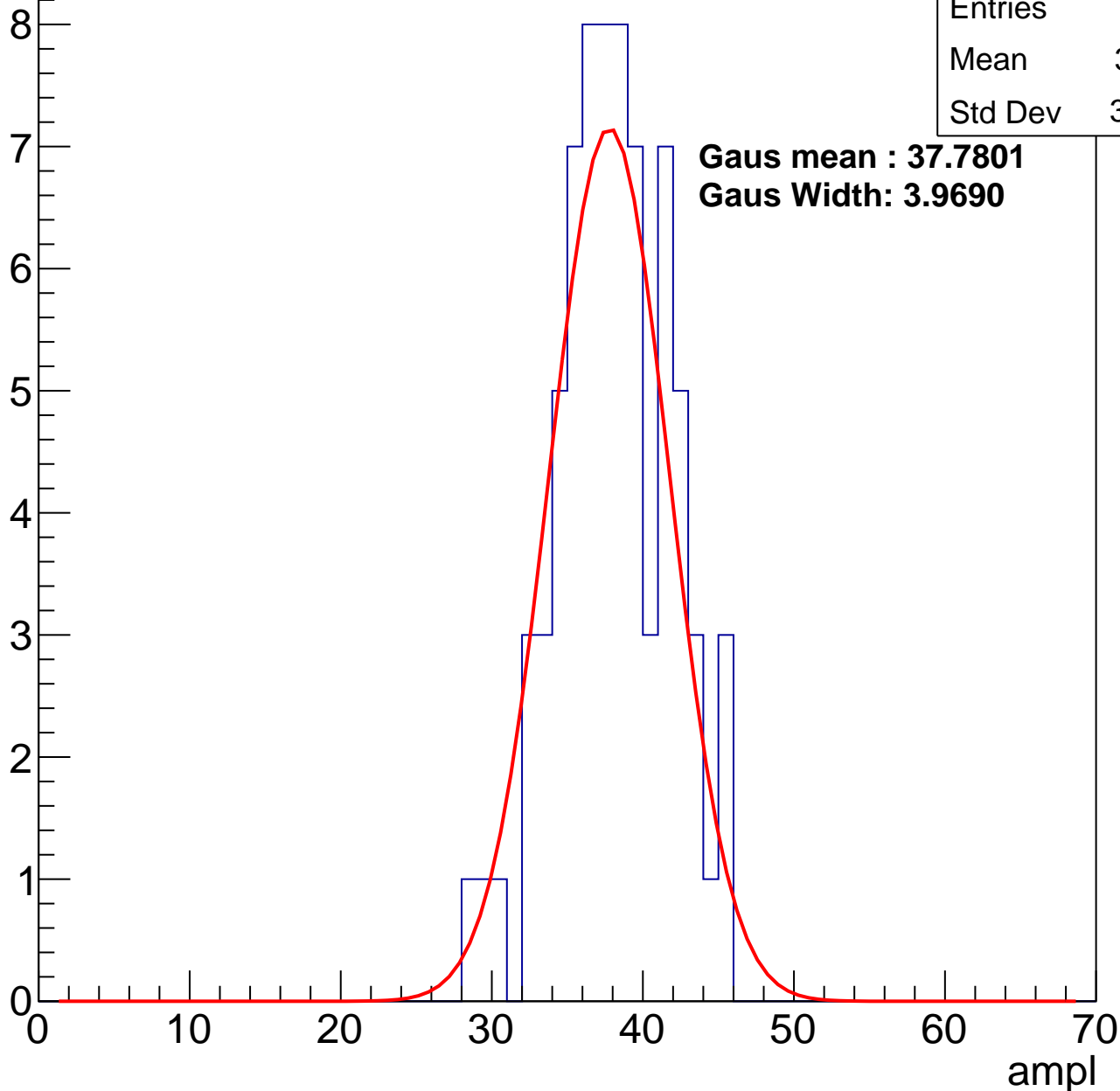
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.61
Std Dev	3.712

Gaus mean : 37.7801

Gaus Width: 3.9690



B1L101S, U25-ch62, adc2

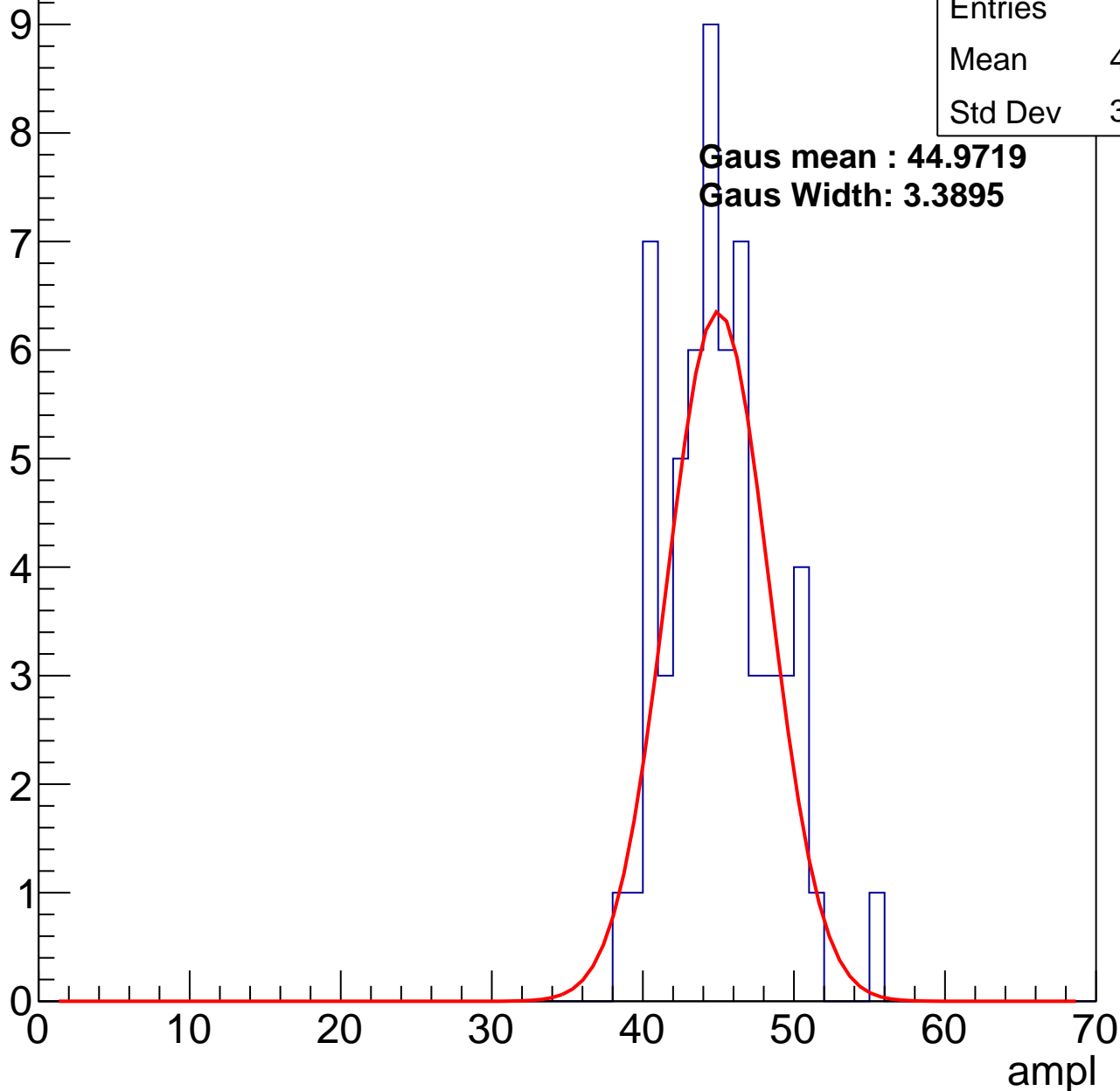
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.57
Std Dev	3.427

Gaus mean : 44.9719

Gaus Width: 3.3895

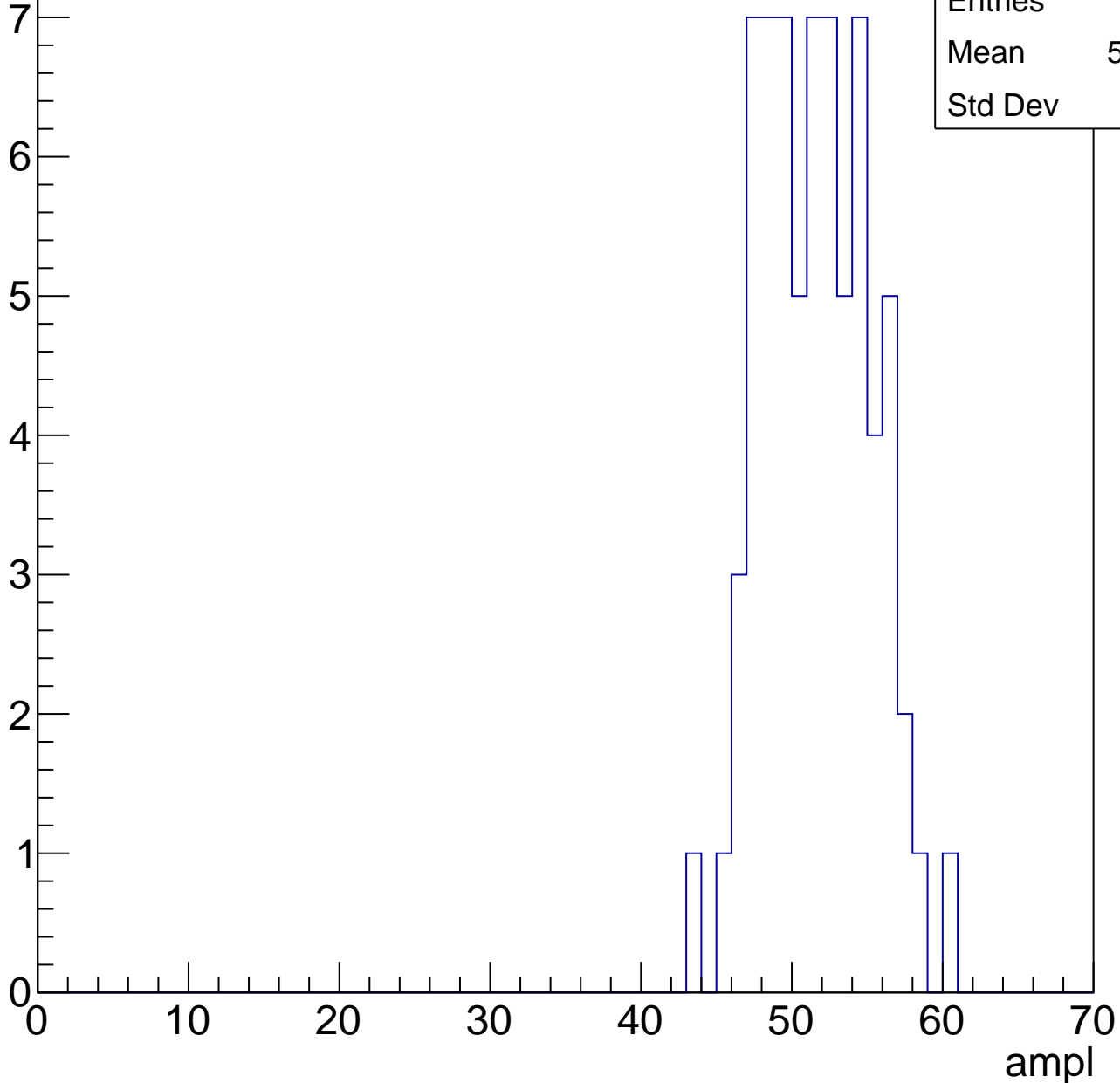


B1L101S, U25-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

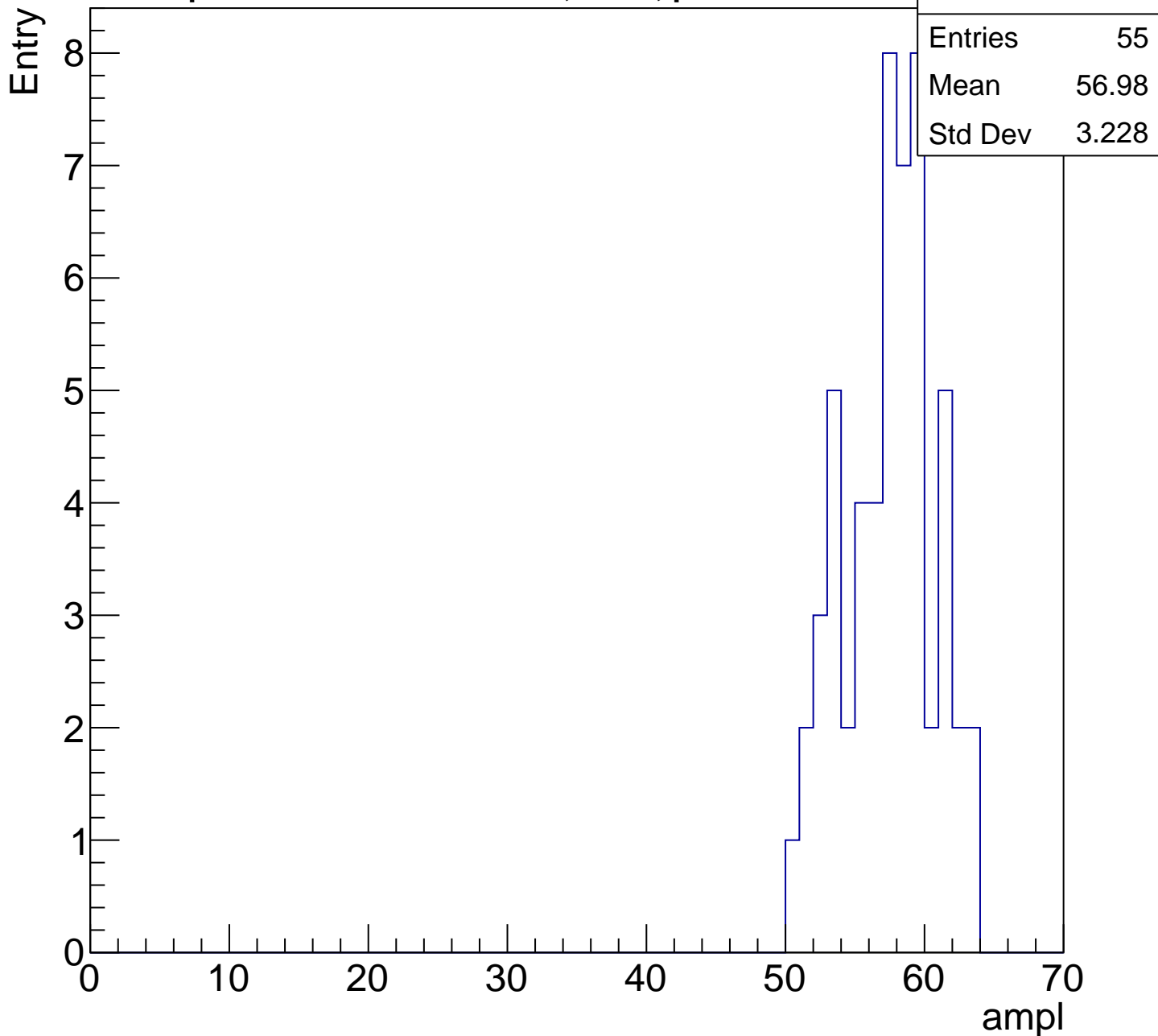
Entry

Entries	70
Mean	51.14
Std Dev	3.51



B1L101S, U25-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	34
Mean	58.79
Std Dev	10.48

ampl

0

10

20

30

40

50

60

70

B1L101S, U25-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch63, adc0

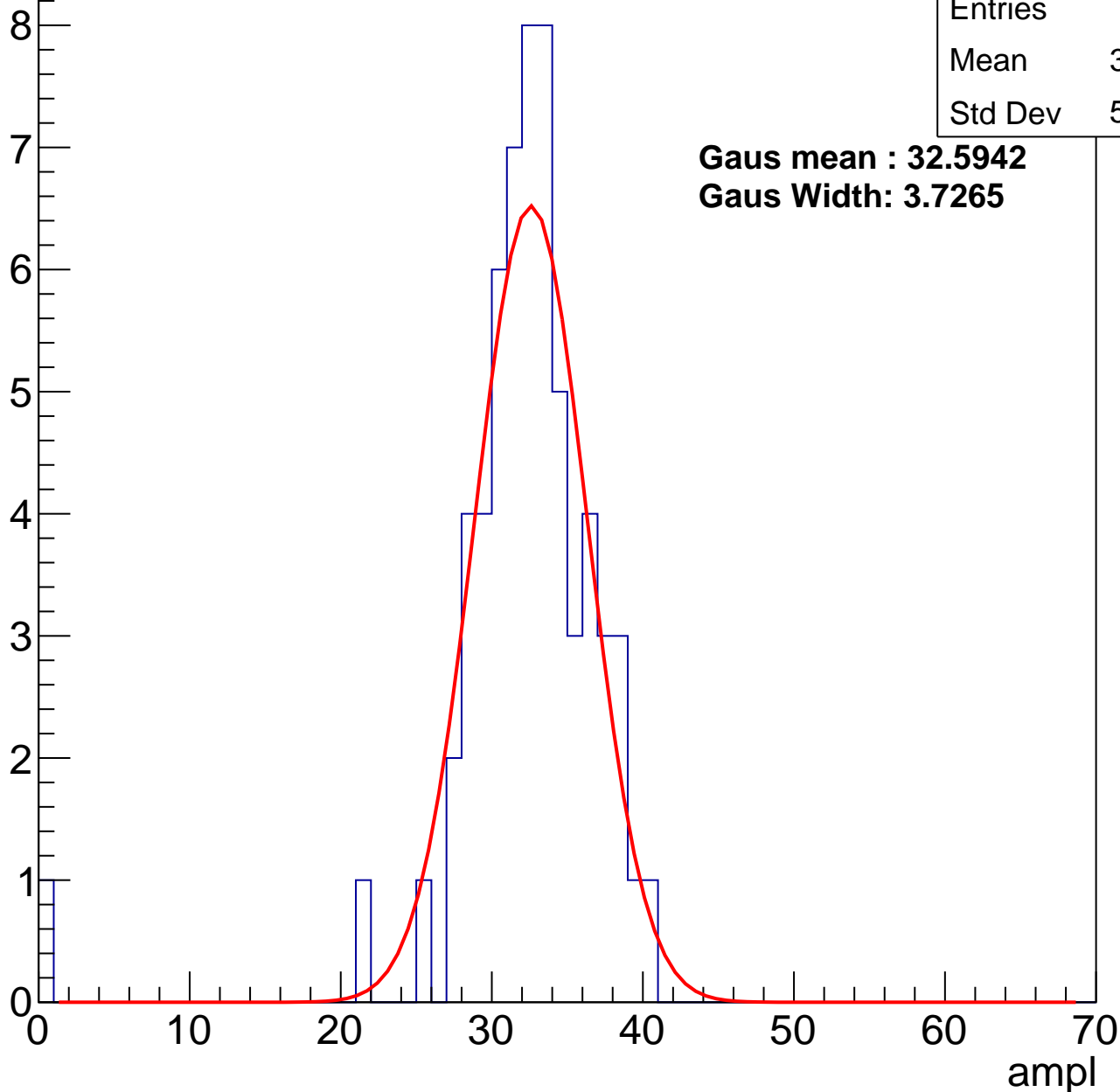
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	31.74
Std Dev	5.367

Gaus mean : 32.5942

Gaus Width: 3.7265



B1L101S, U25-ch63, adc1

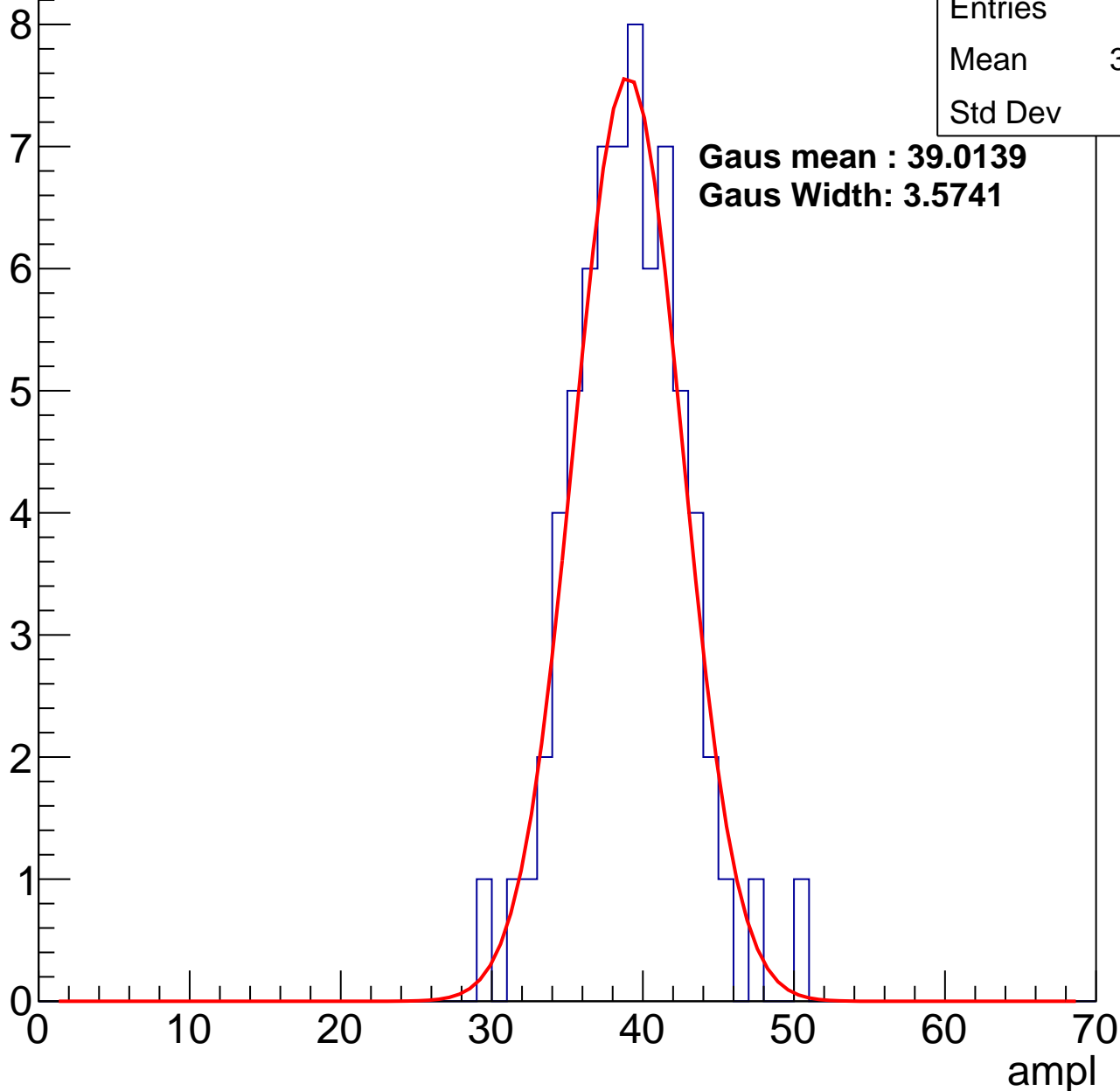
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.57
Std Dev	3.72

Gaus mean : 39.0139

Gaus Width: 3.5741



B1L101S, U25-ch63, adc2

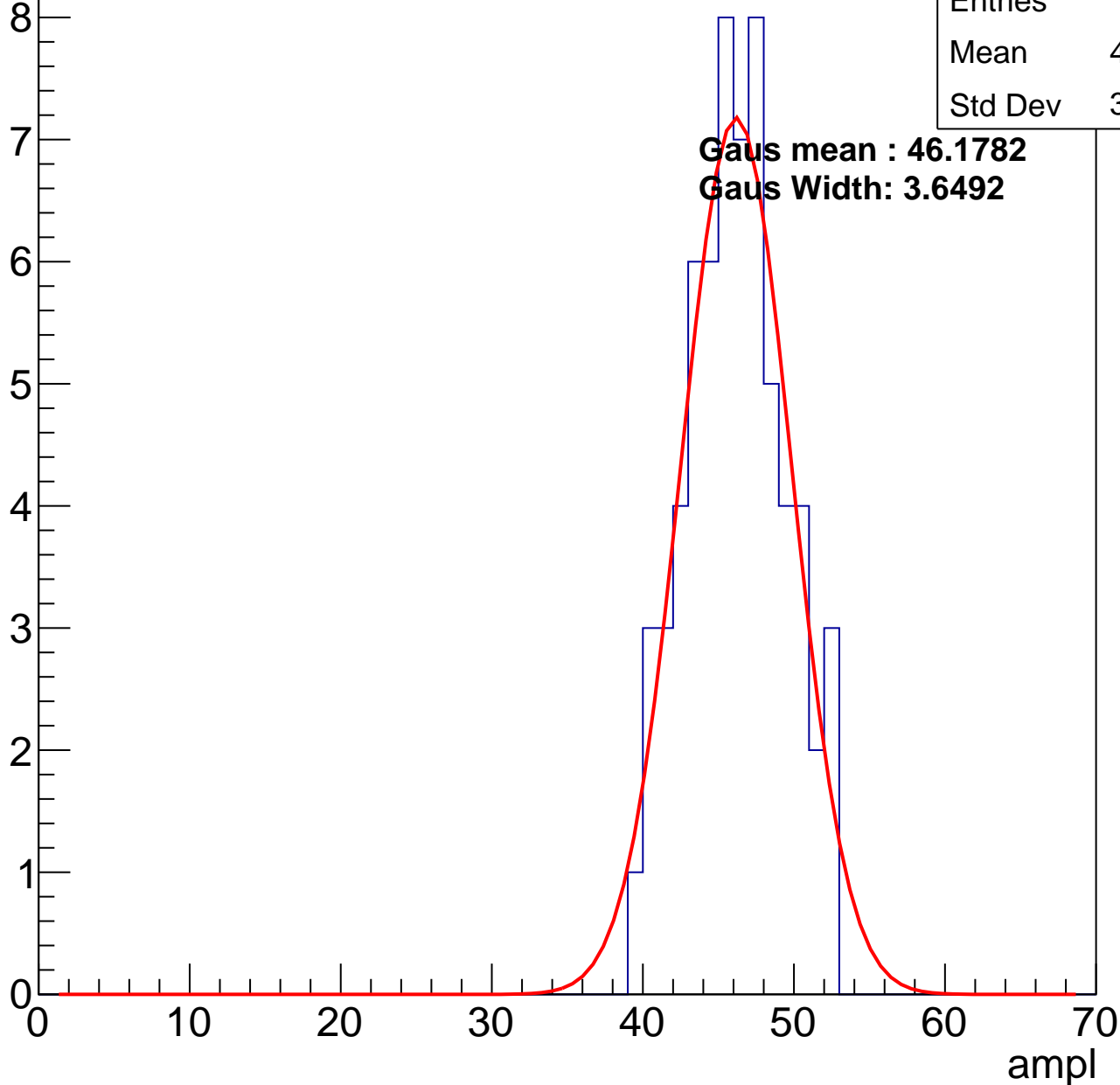
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	45.69
Std Dev	3.216

Gaus mean : 46.1782

Gaus Width: 3.6492

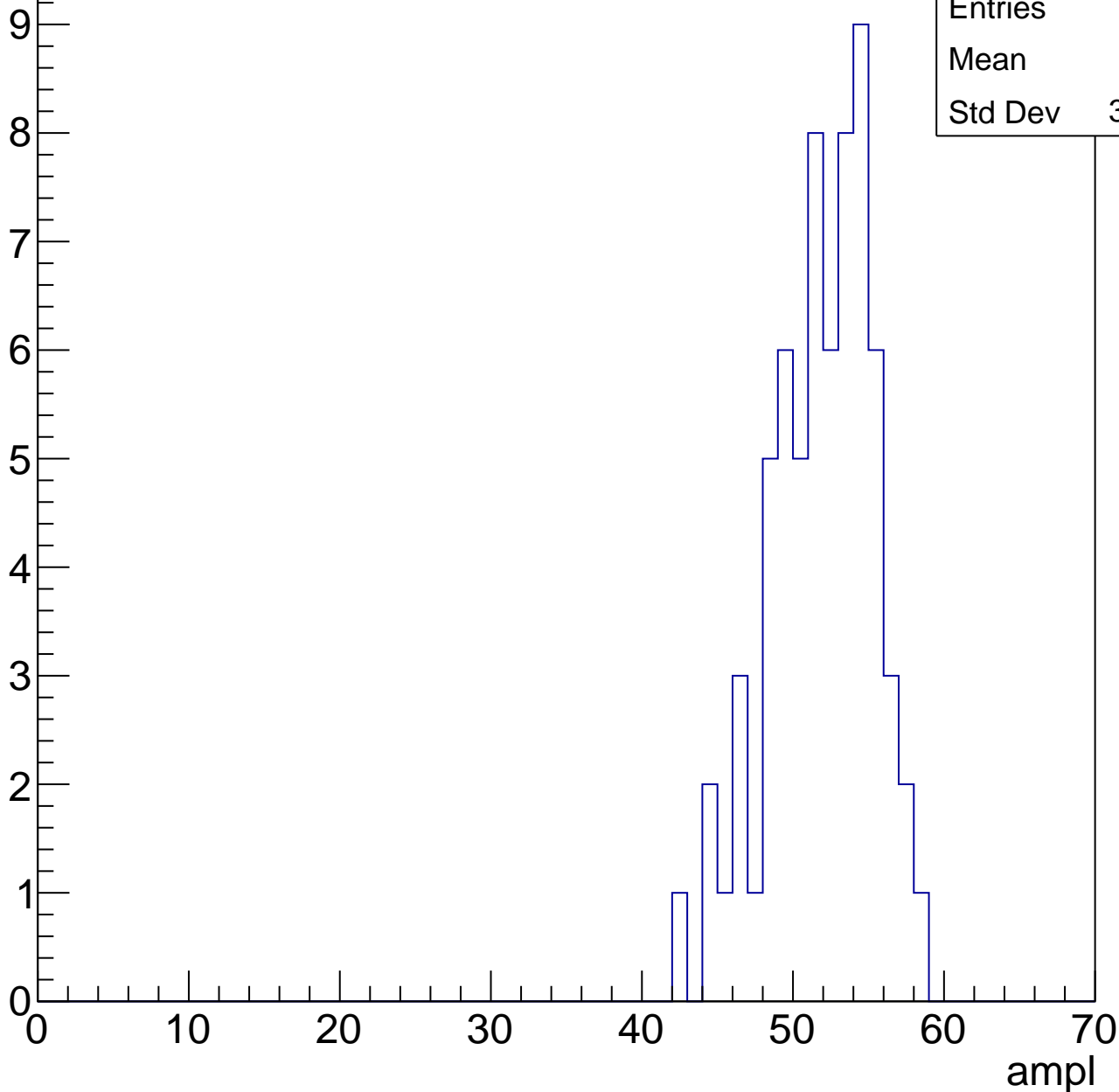


B1L101S, U25-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.4
Std Dev	3.412

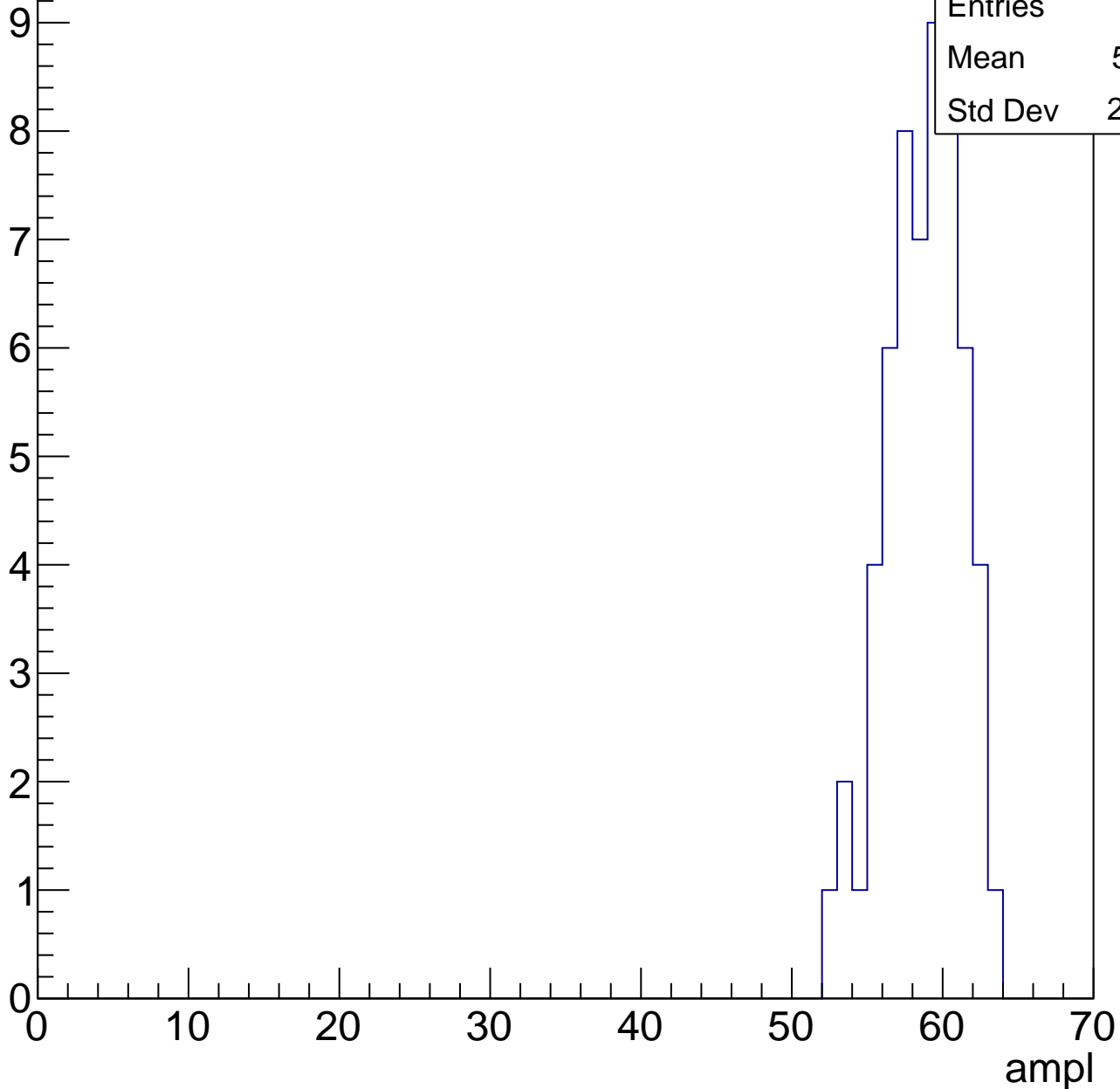


B1L101S, U25-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

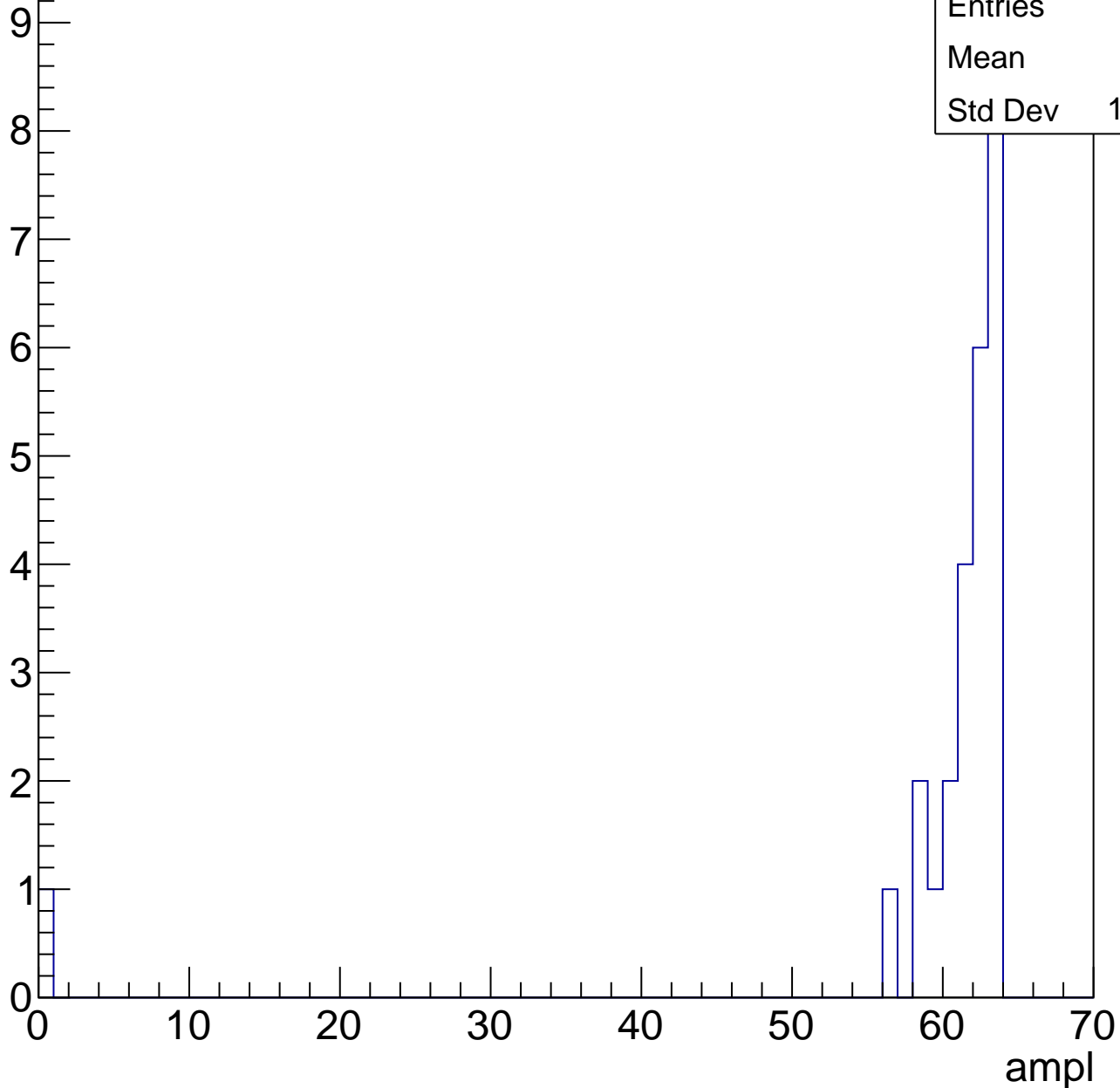
Entries	57
Mean	58.21
Std Dev	2.483



B1L101S, U25-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch64, adc0

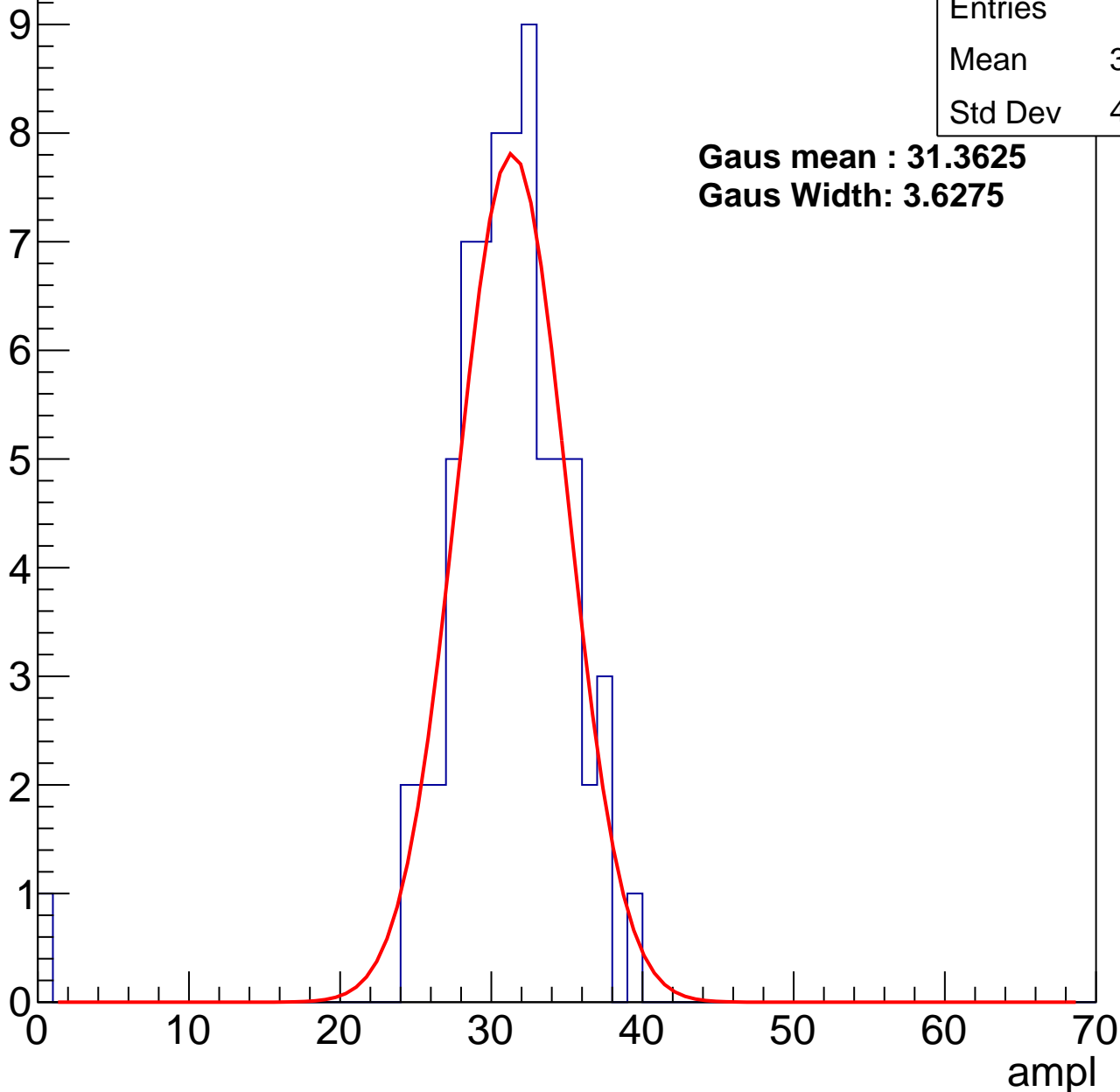
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.44
Std Dev	4.879

Gaus mean : 31.3625

Gaus Width: 3.6275



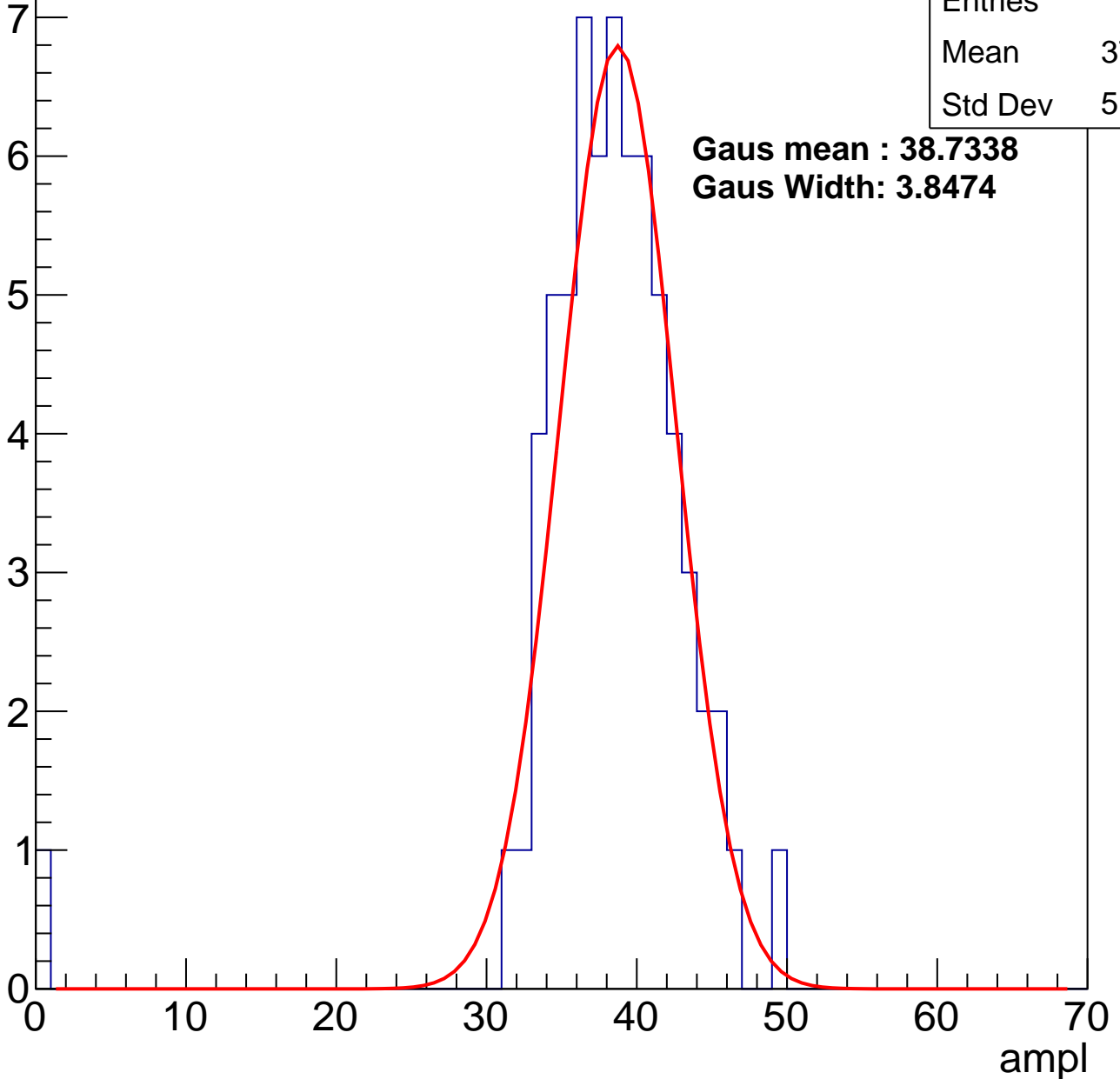
B1L101S, U25-ch64, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

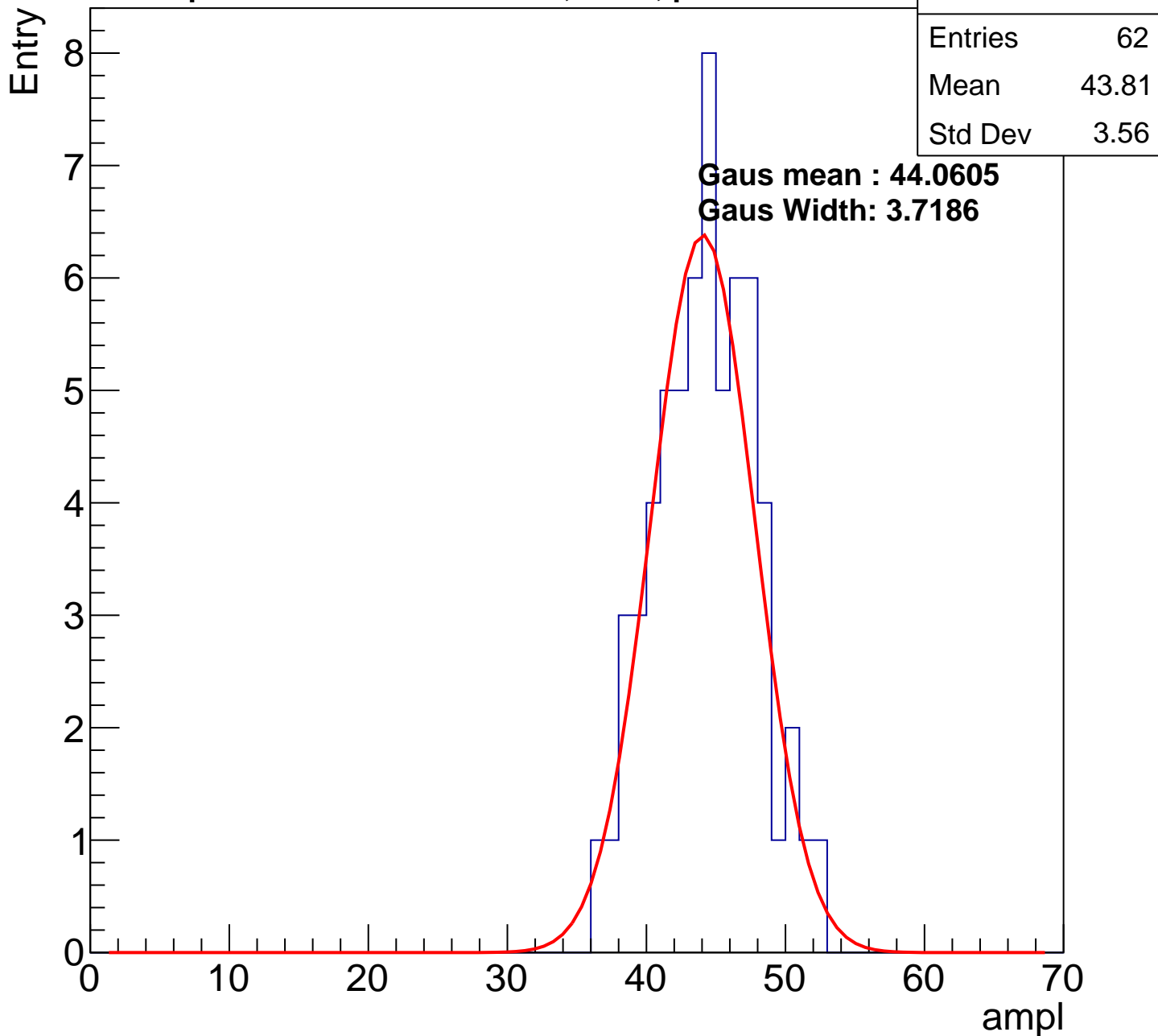
Entries	67
Mean	37.75
Std Dev	5.926

Gaus mean : 38.7338
Gaus Width: 3.8474



B1L101S, U25-ch64, adc2

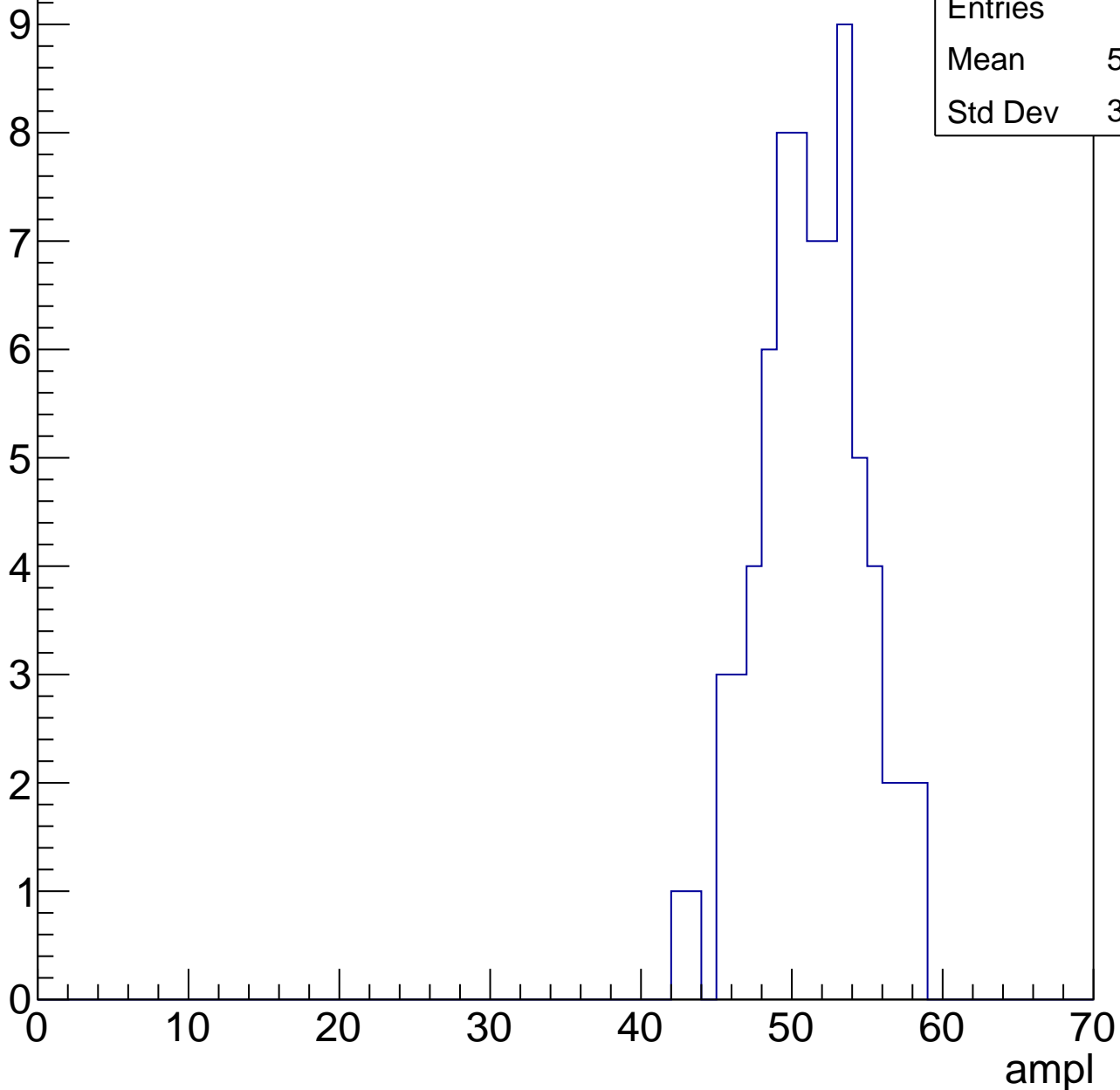
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



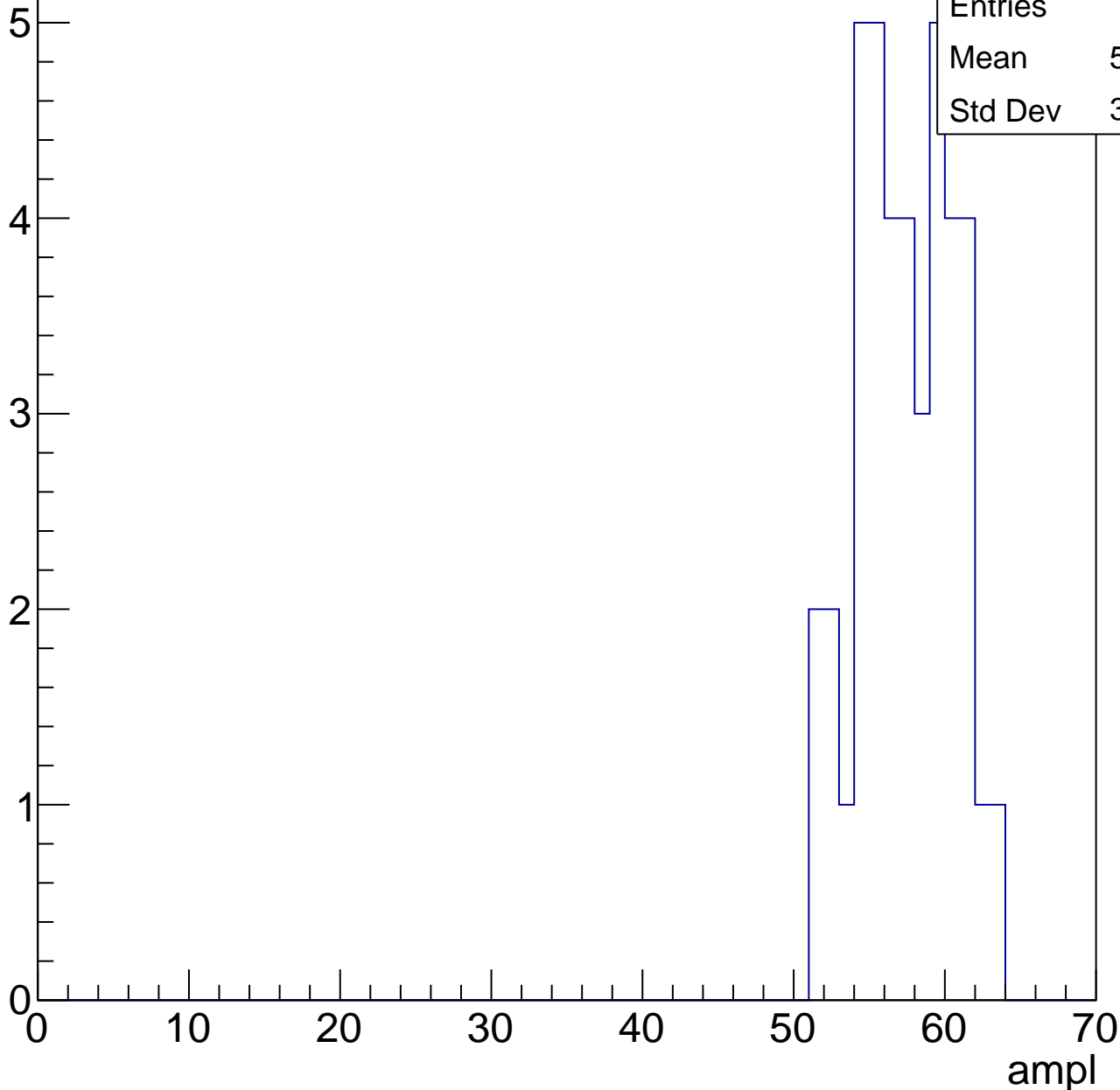
Entries	72
Mean	50.78
Std Dev	3.445

B1L101S, U25-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	56.93
Std Dev	3.087

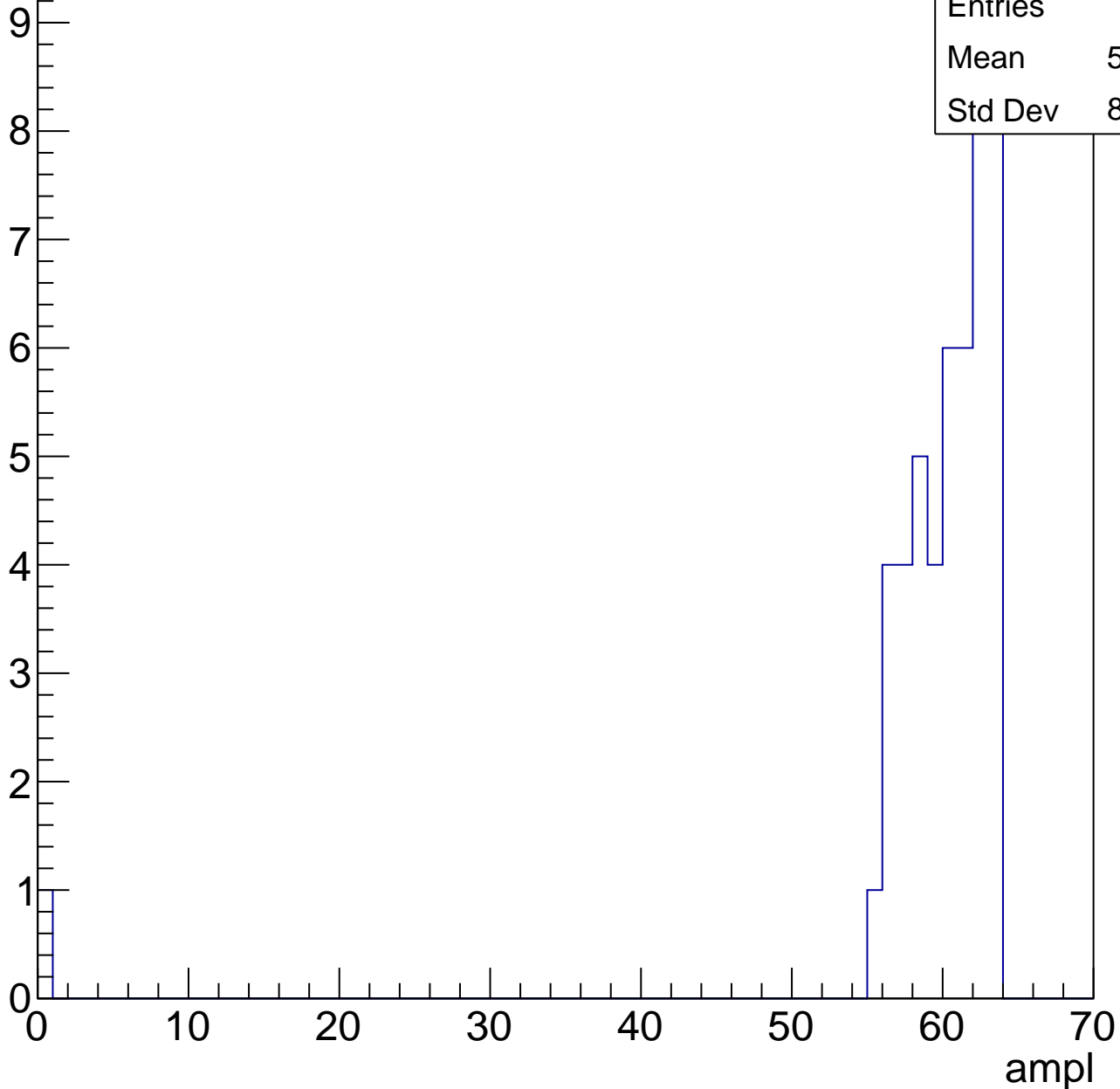


B1L101S, U25-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

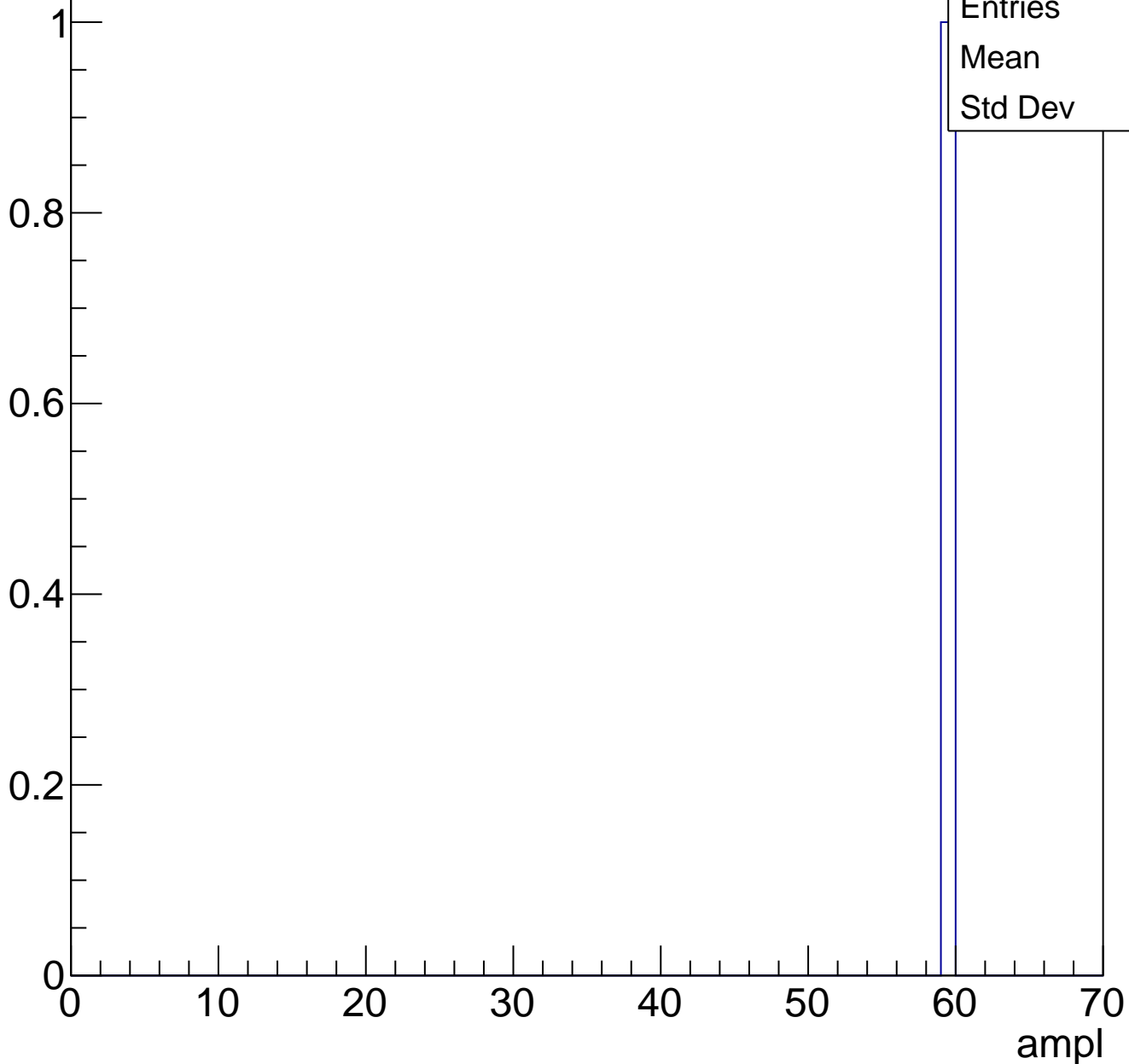
Entries	49
Mean	58.86
Std Dev	8.816



B1L101S, U25-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	59
Std Dev	0

B1L101S, U25-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch65, adc0

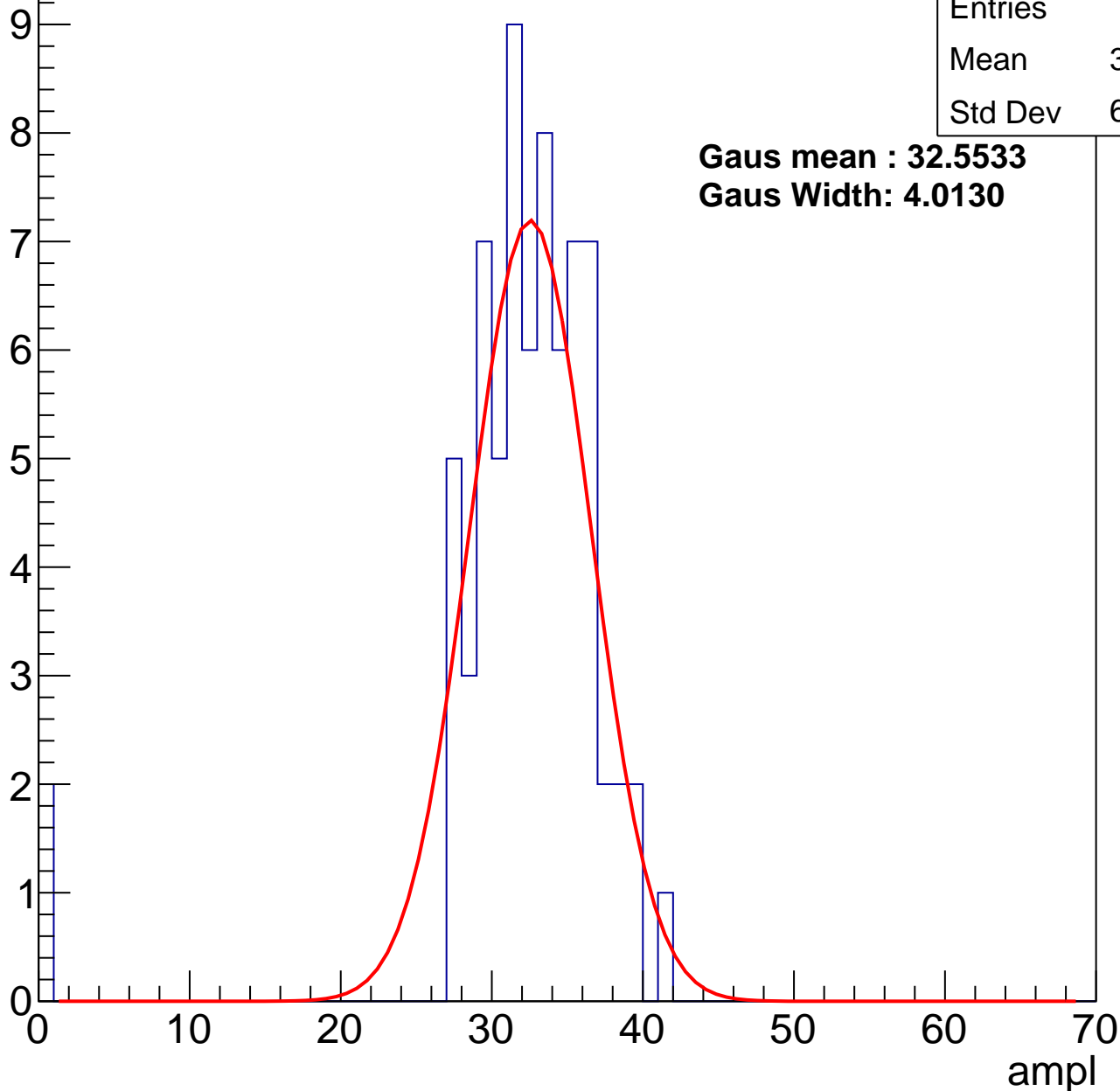
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	31.62
Std Dev	6.248

Gaus mean : 32.5533

Gaus Width: 4.0130



B1L101S, U25-ch65, adc1

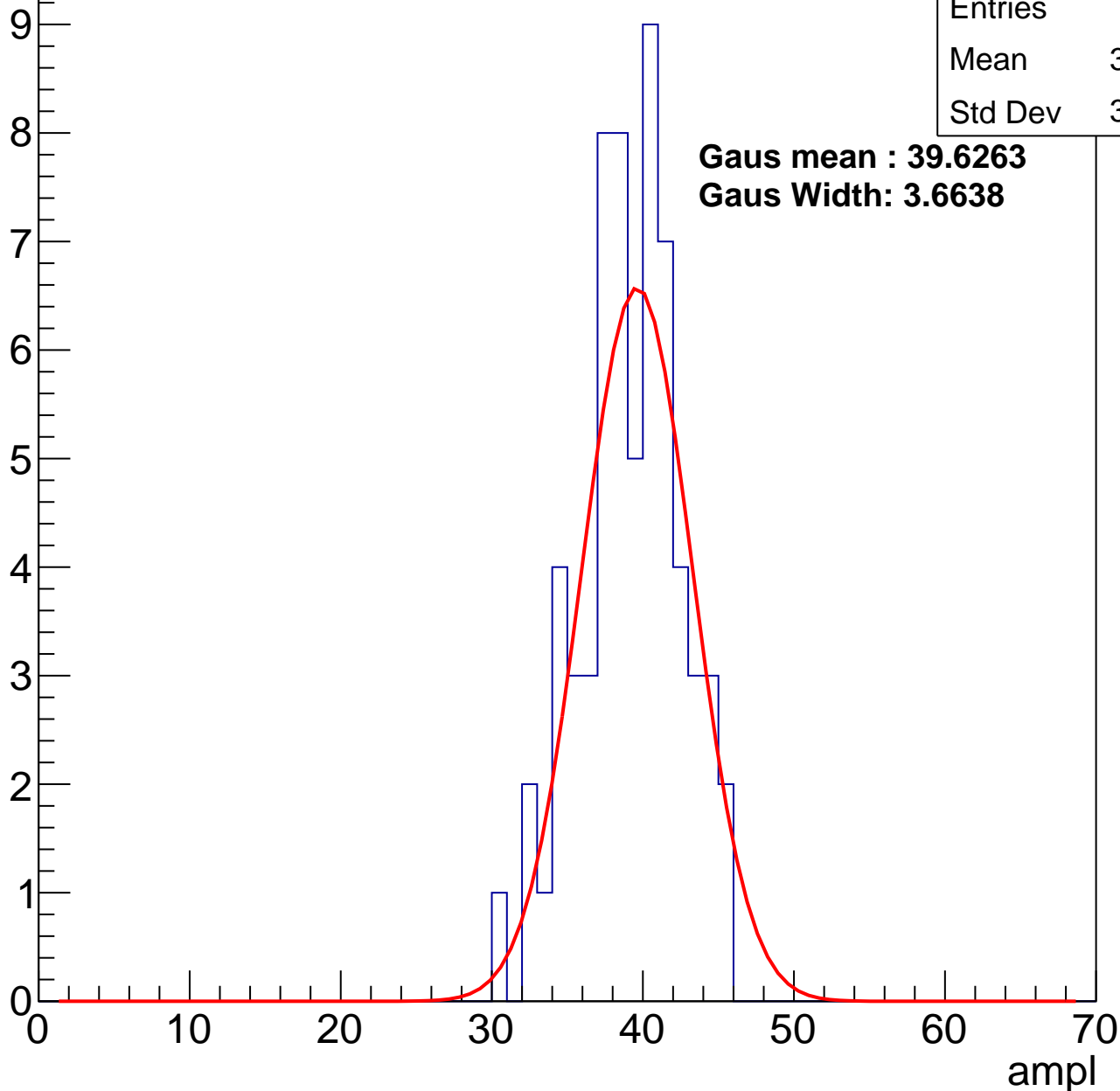
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.68
Std Dev	3.313

Gaus mean : 39.6263

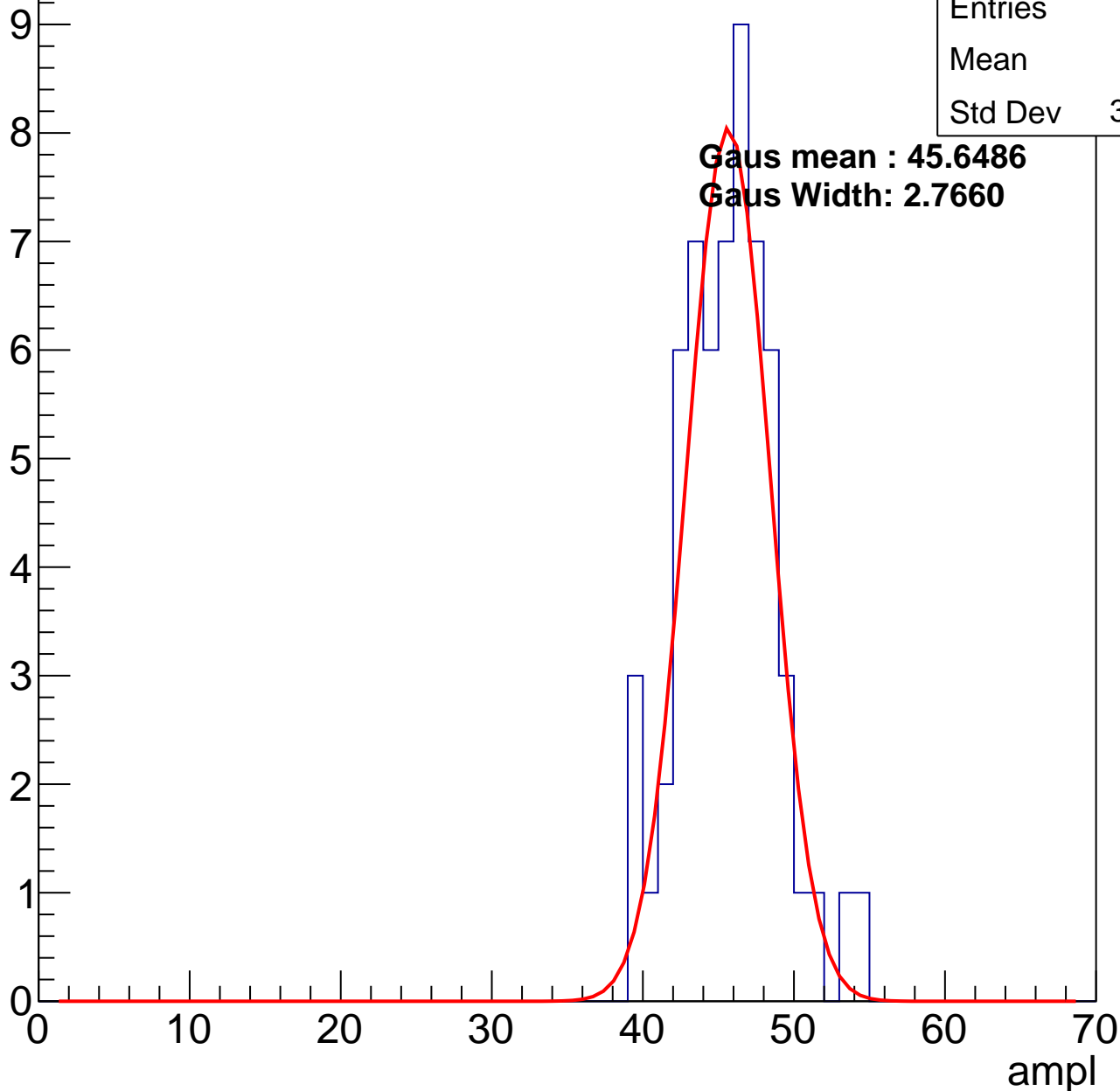
Gaus Width: 3.6638



B1L101S, U25-ch65, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	51.77
Std Dev	3.156

Entry

10

8

6

4

2

0

0

10

20

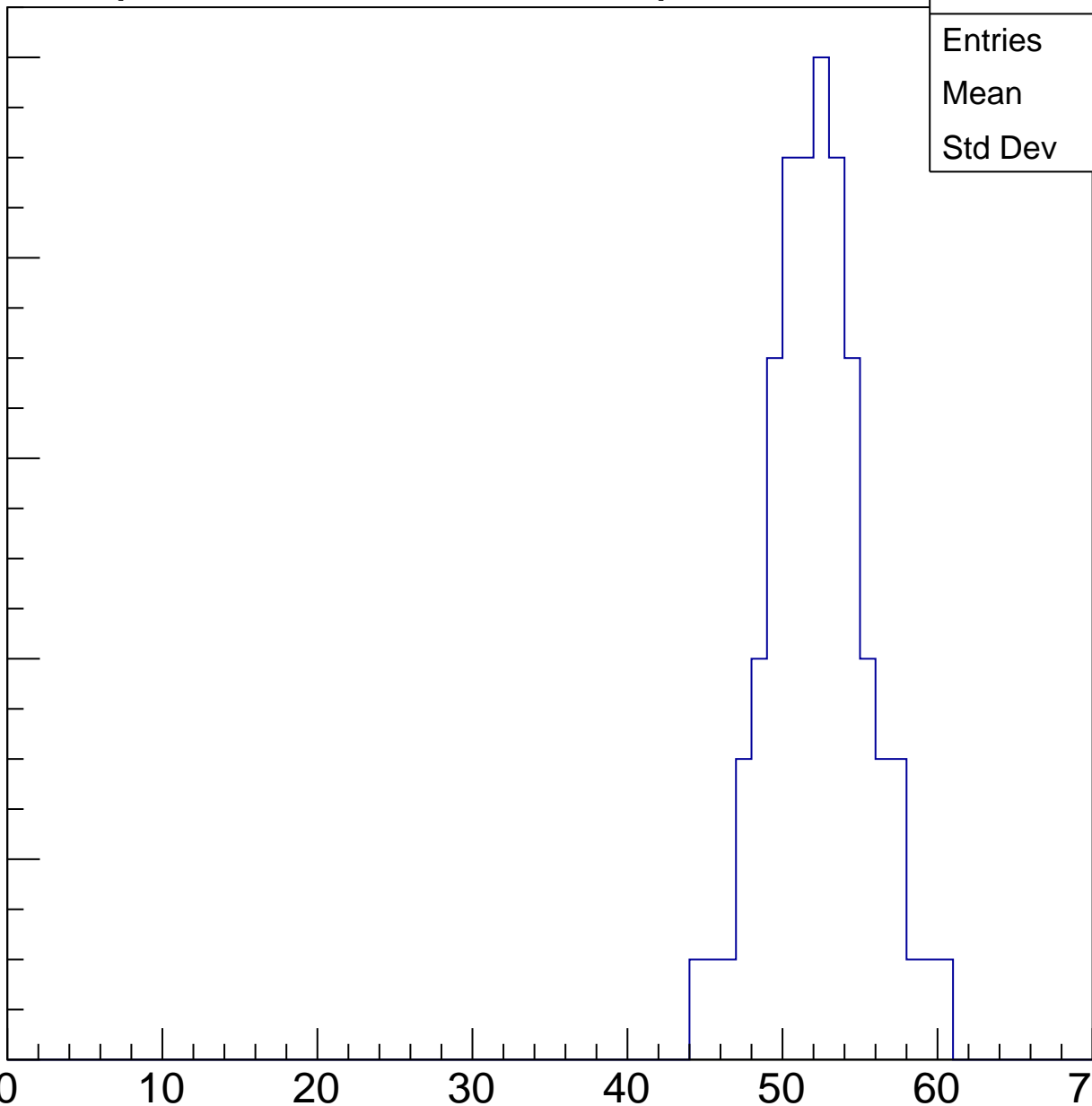
30

40

50

60

ampl

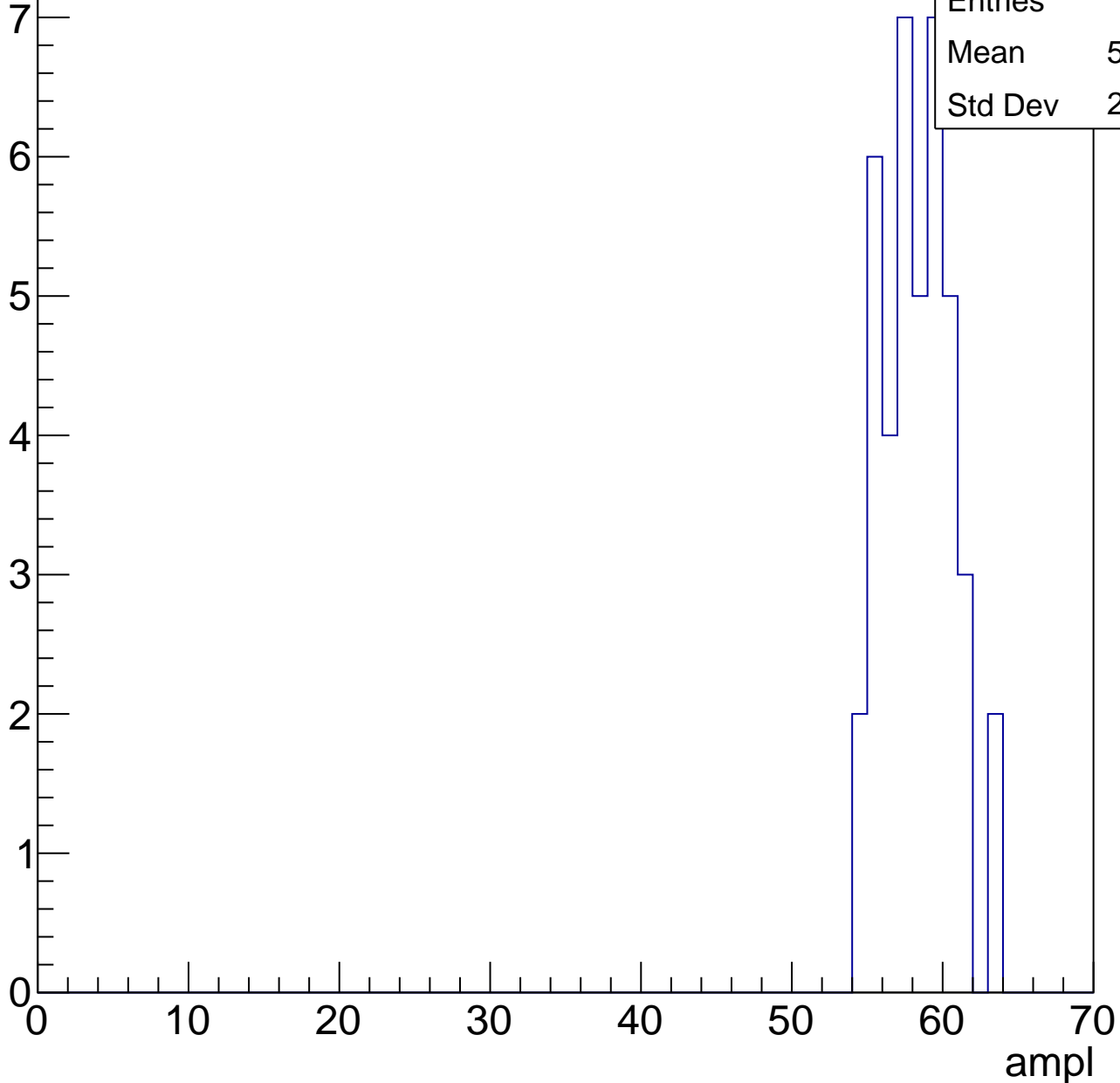


B1L101S, U25-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	57.88
Std Dev	2.276

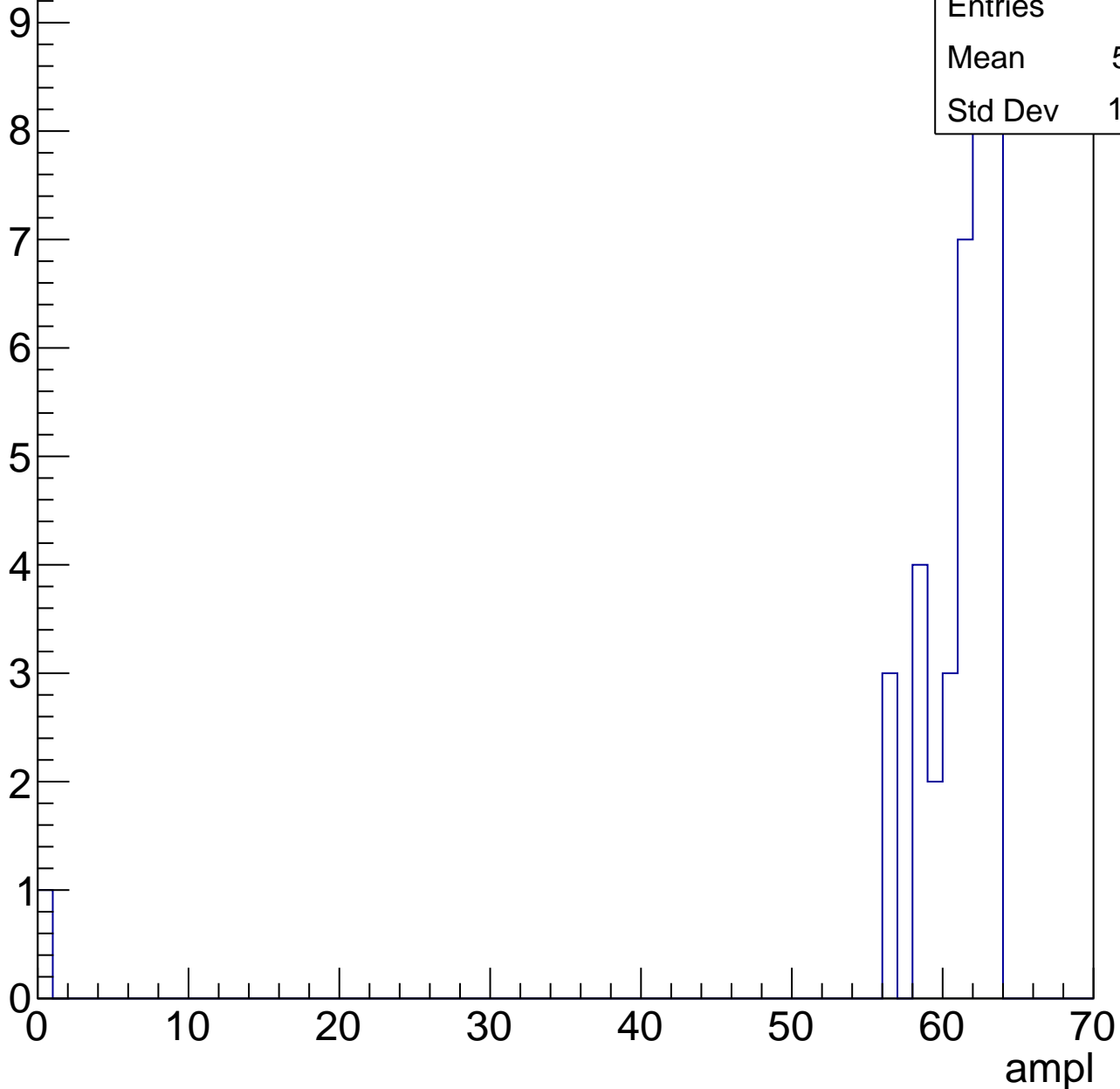


B1L101S, U25-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	59.11
Std Dev	10.07



B1L101S, U25-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

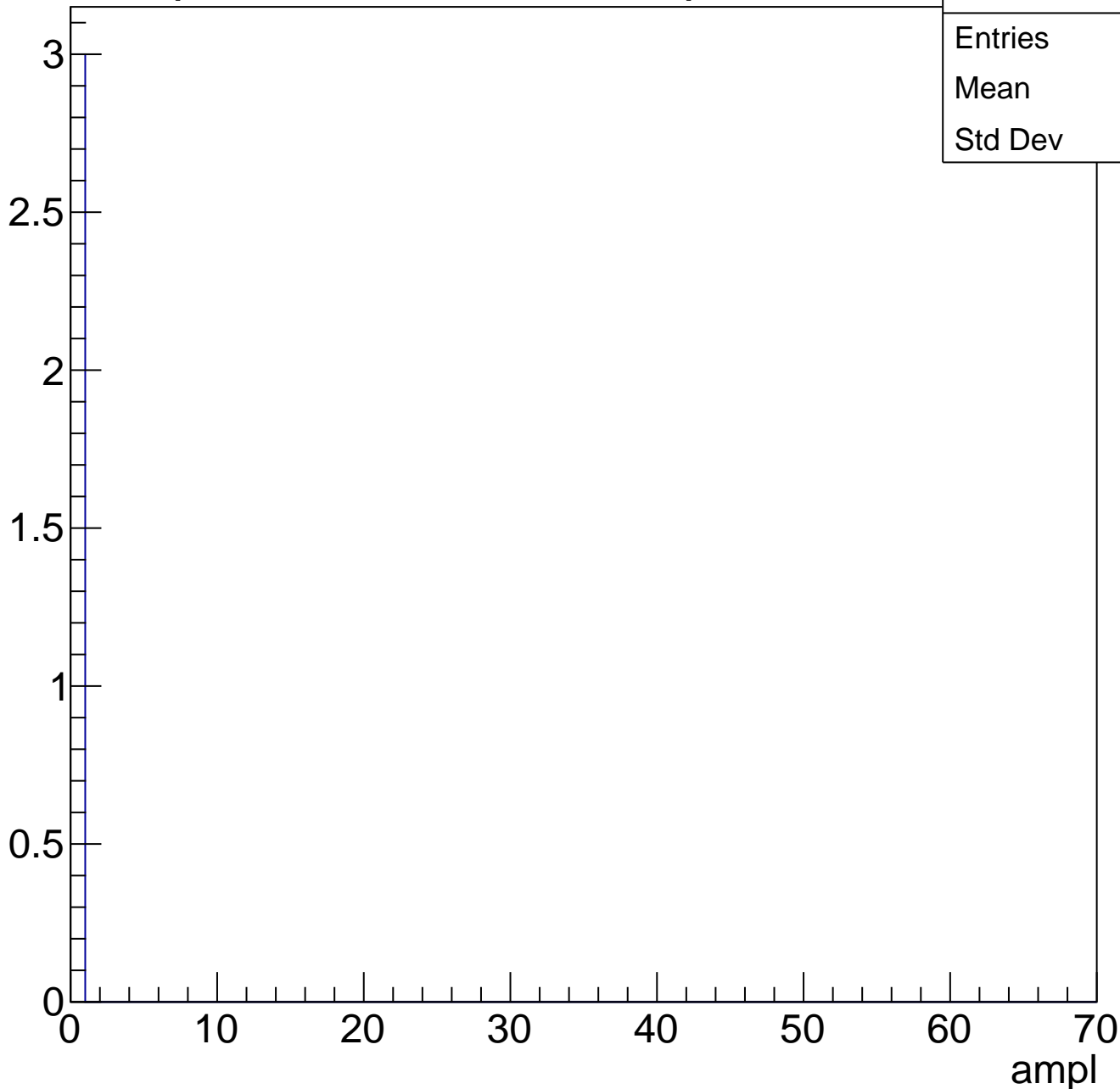
Entry



B1L101S, U25-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U25-ch66, adc0

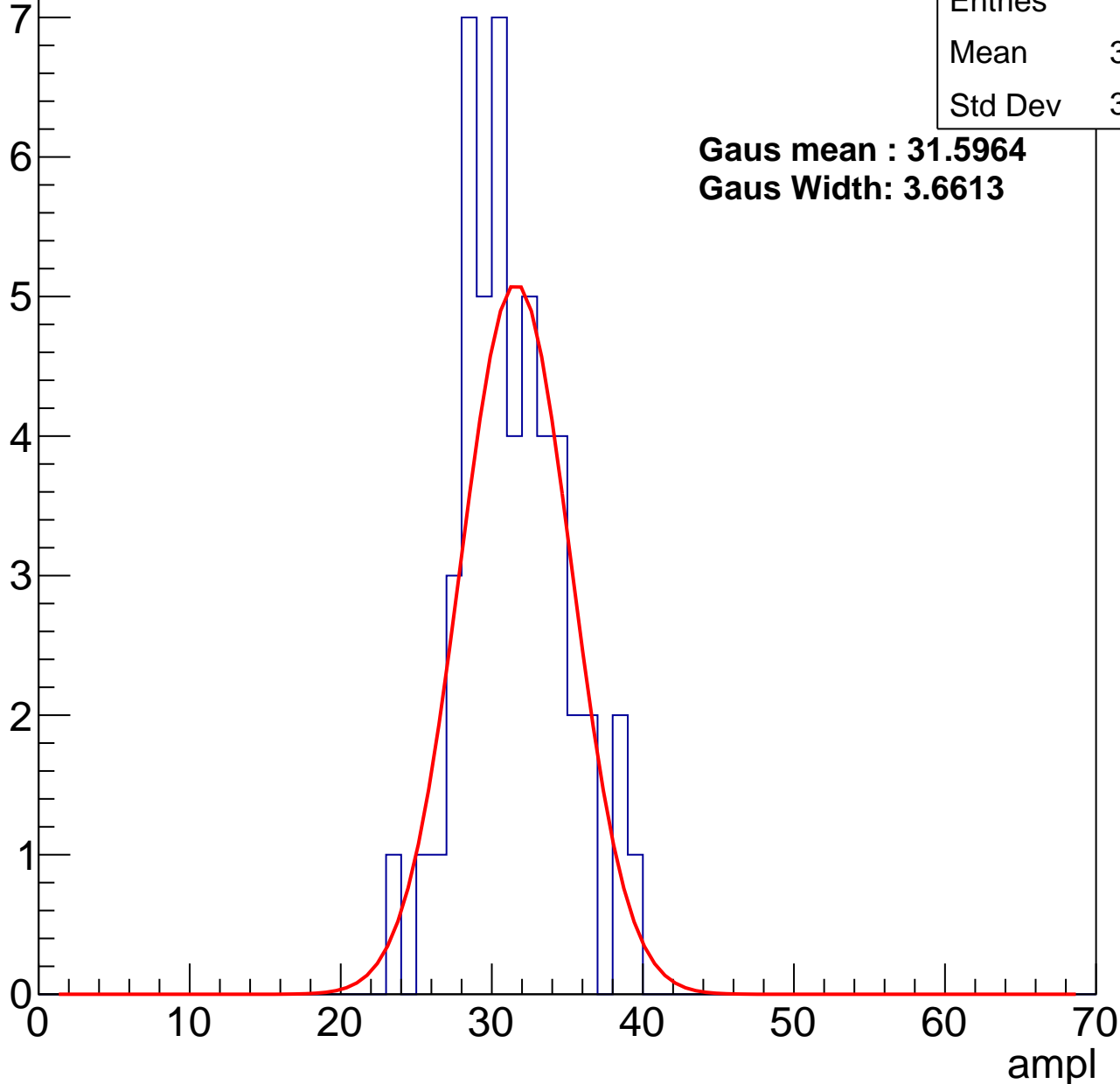
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	30.92
Std Dev	3.392

Gaus mean : 31.5964

Gaus Width: 3.6613



B1L101S, U25-ch66, adc1

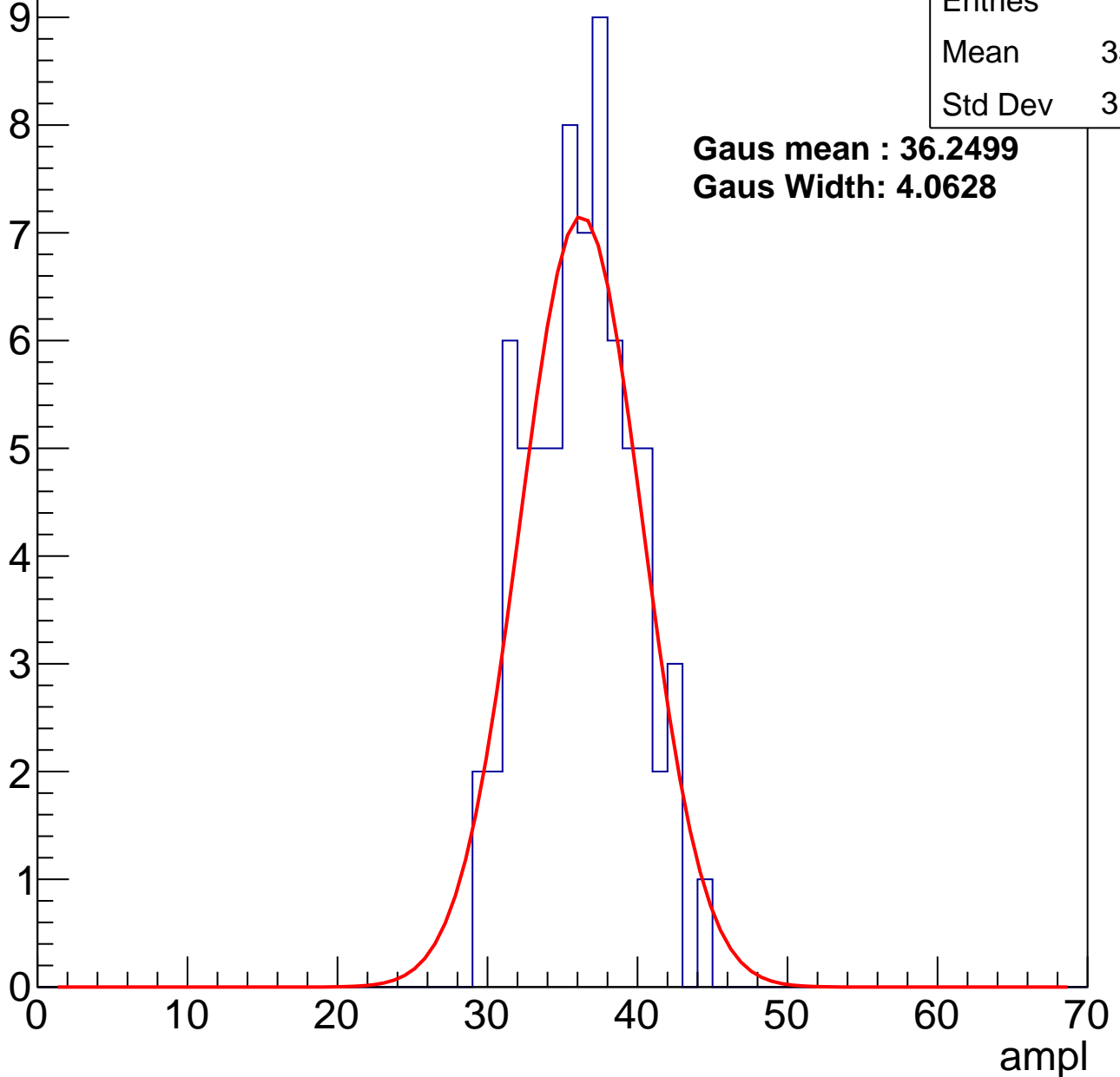
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.76
Std Dev	3.458

Gaus mean : 36.2499

Gaus Width: 4.0628



B1L101S, U25-ch66, adc2

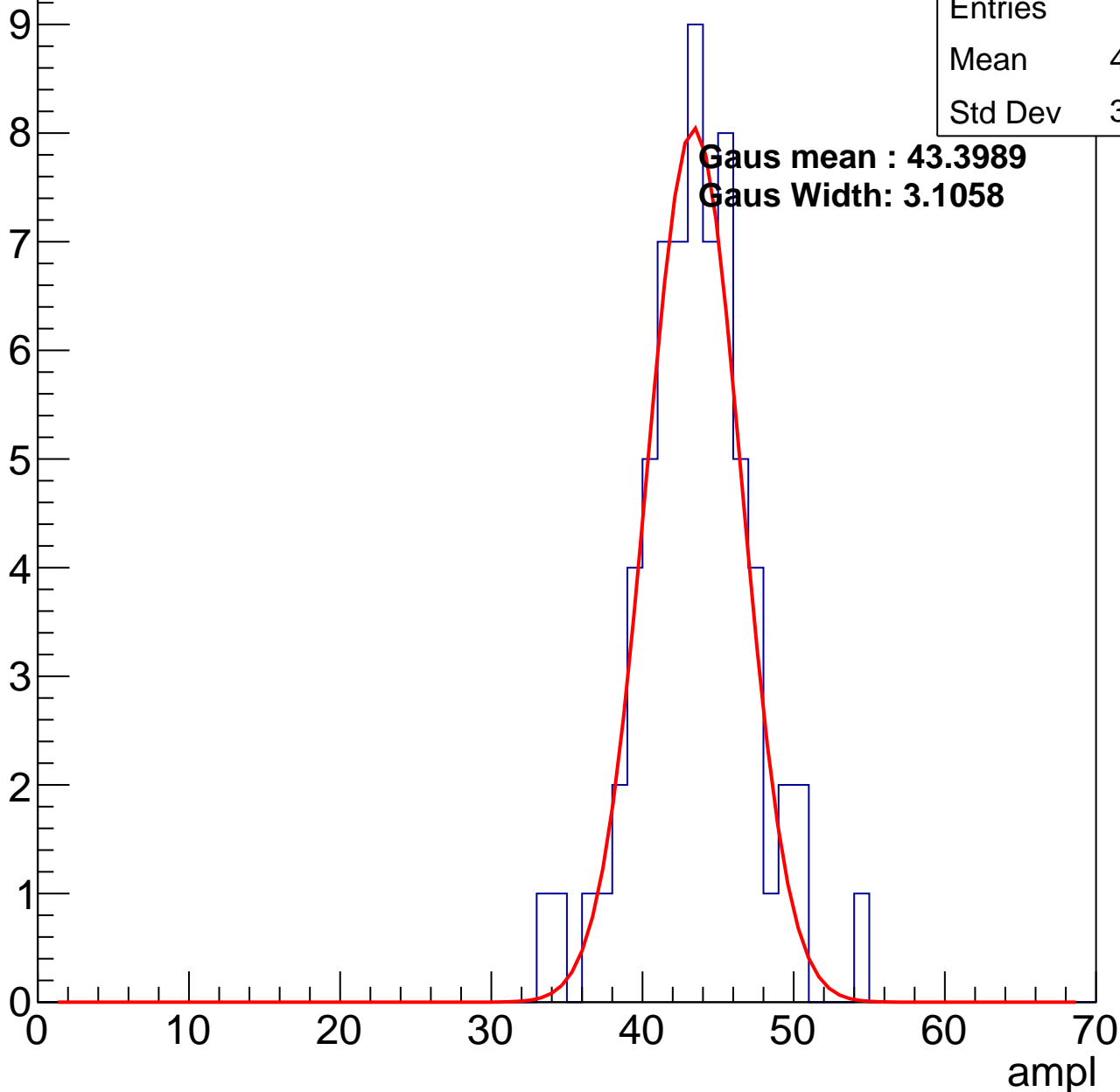
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.03
Std Dev	3.686

Gaus mean : 43.3989

Gaus Width: 3.1058

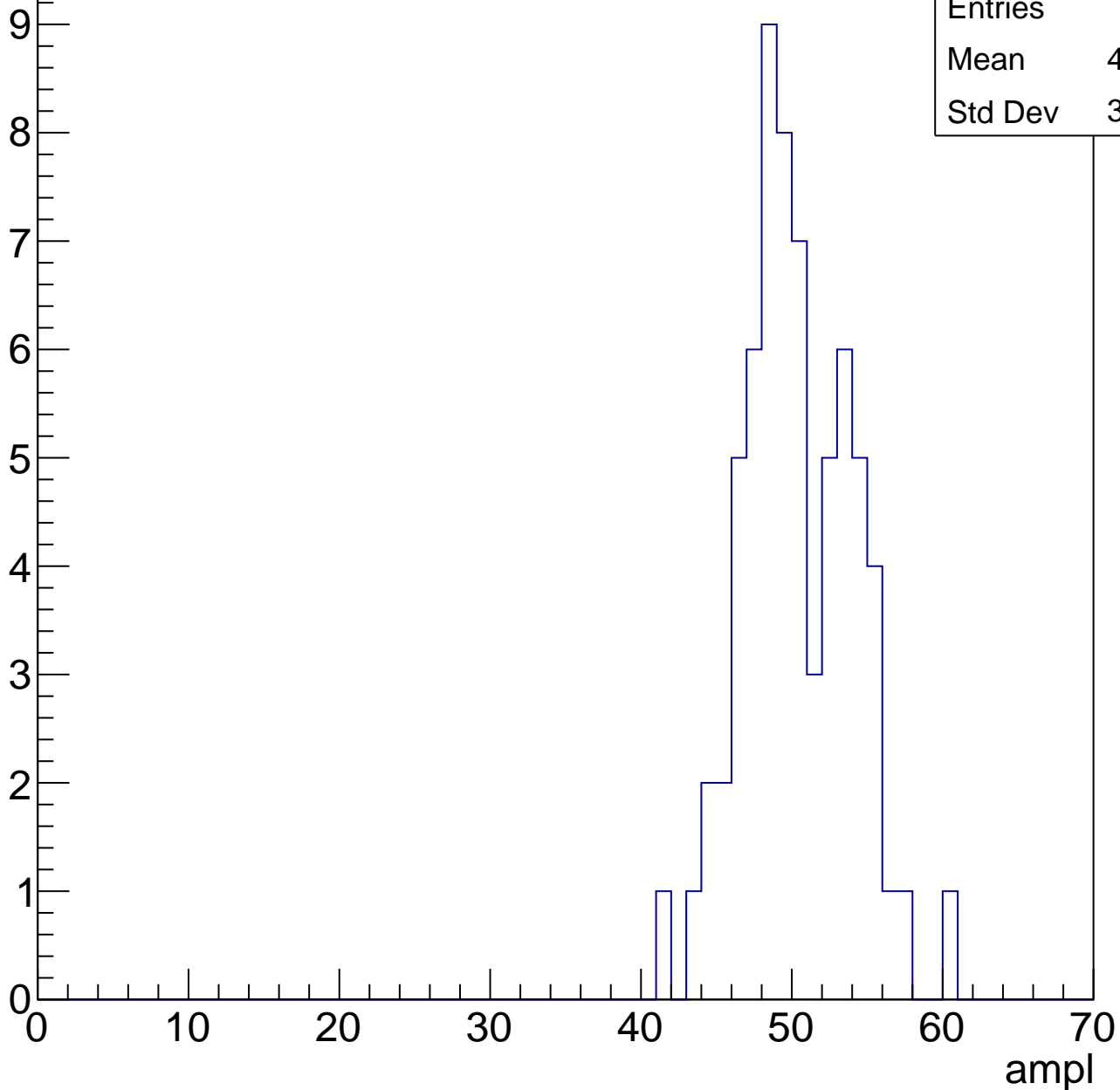


B1L101S, U25-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.88
Std Dev	3.606

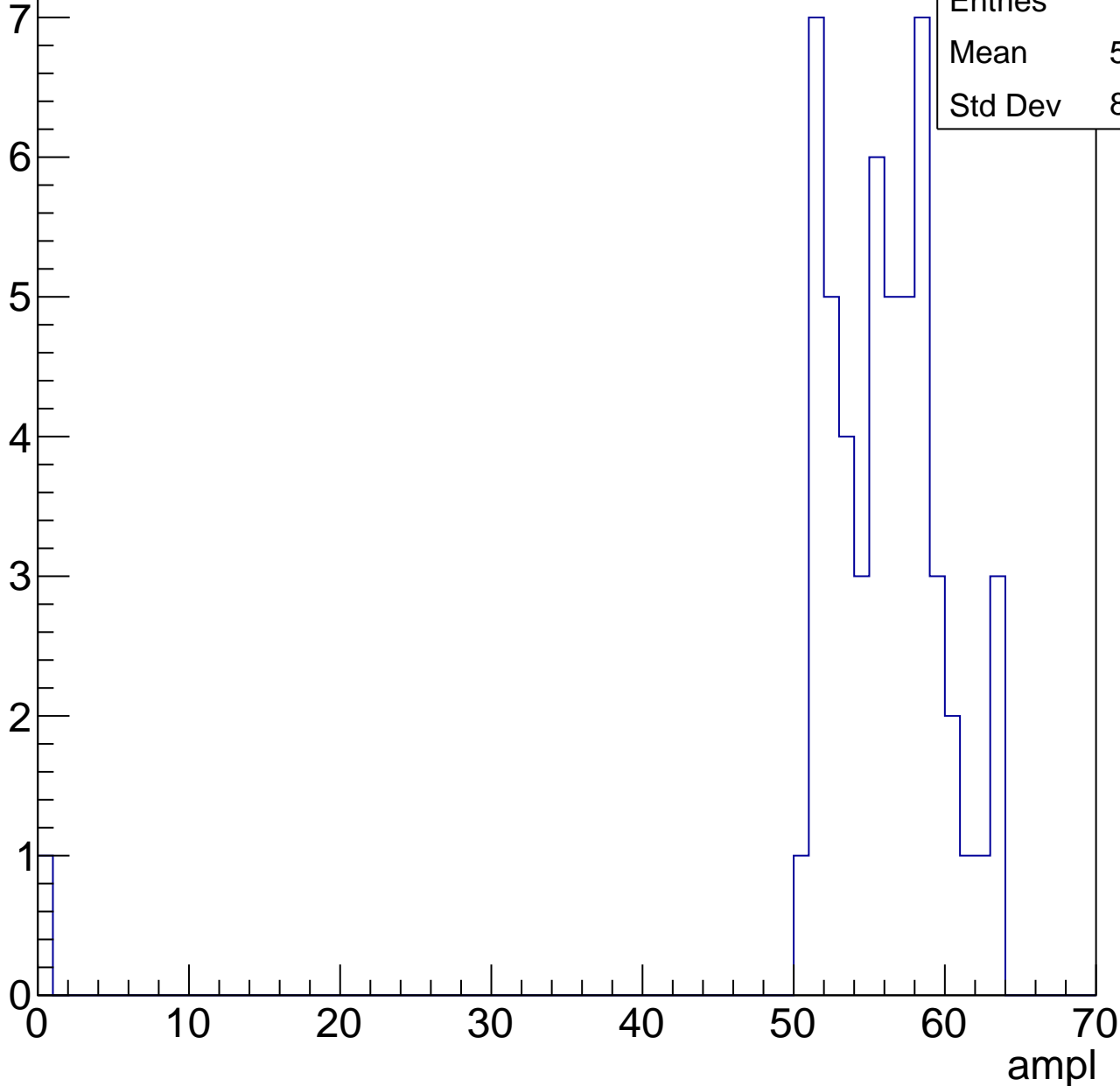


B1L101S, U25-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.65
Std Dev	8.258

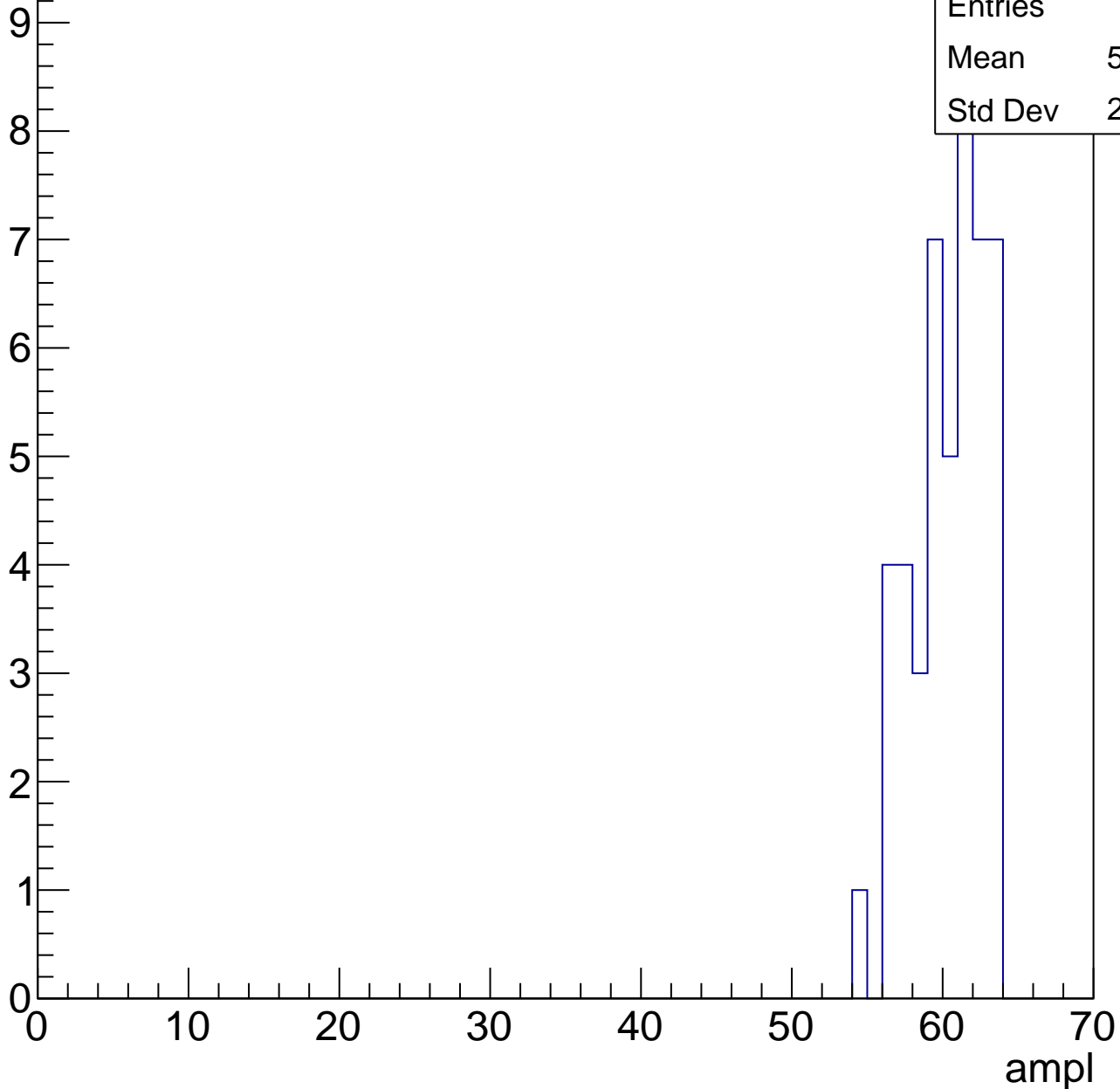


B1L101S, U25-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	59.94
Std Dev	2.328



B1L101S, U25-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries

4

Mean

61

Std Dev

1

B1L101S, U25-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch67, adc0

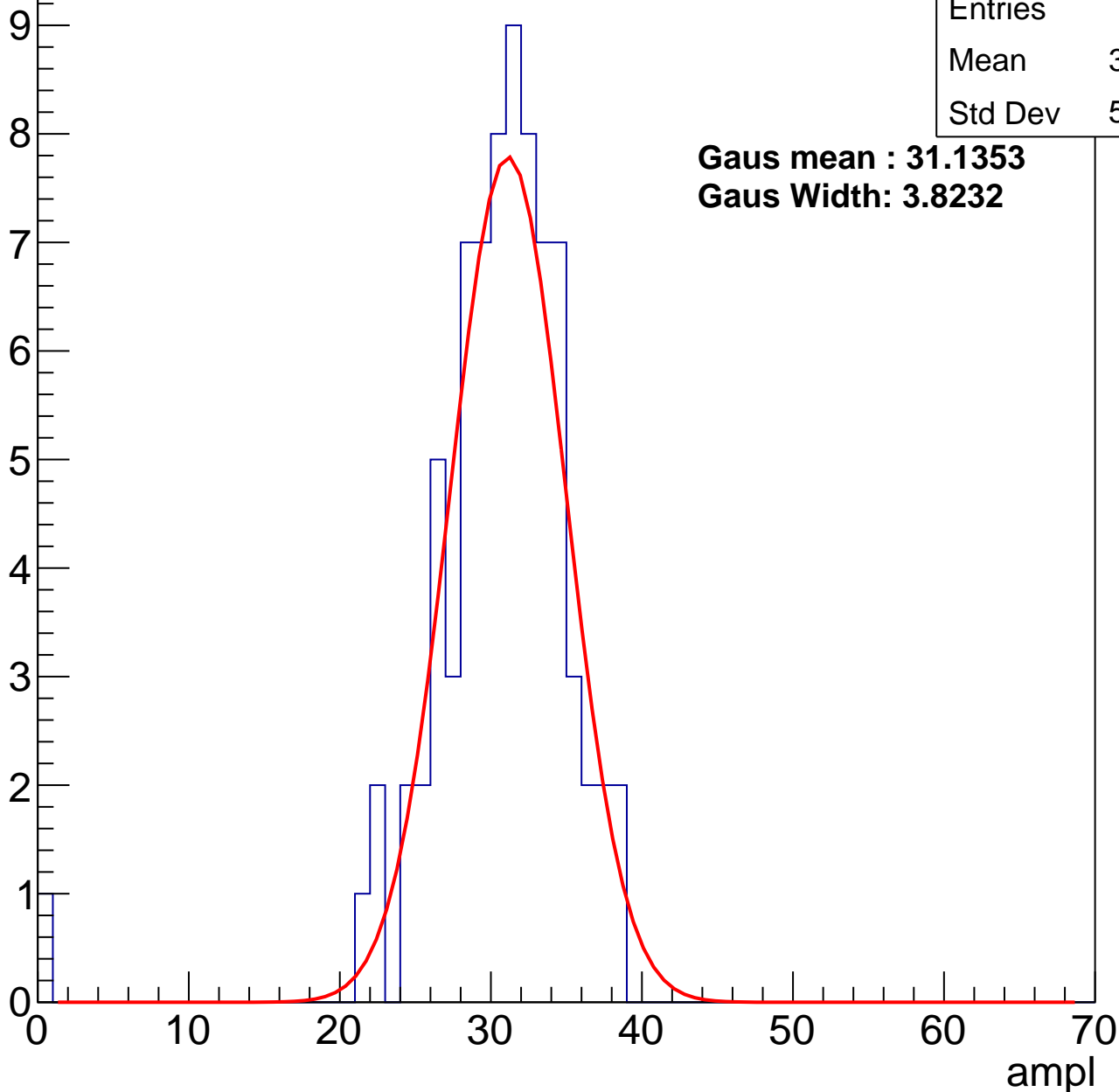
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.05
Std Dev	5.015

Gaus mean : 31.1353

Gaus Width: 3.8232



B1L101S, U25-ch67, adc1

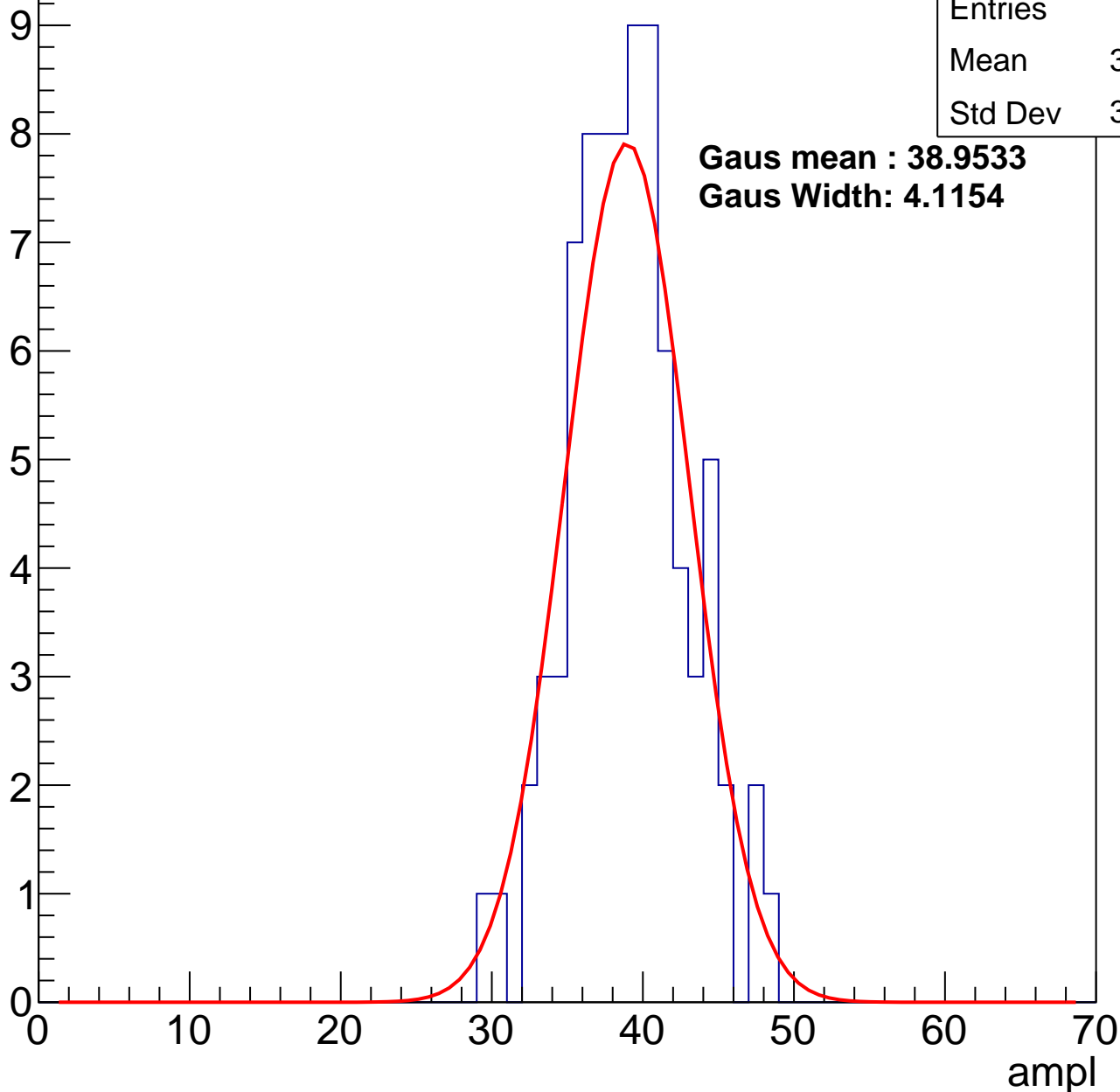
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	38.57
Std Dev	3.816

Gaus mean : 38.9533

Gaus Width: 4.1154



B1L101S, U25-ch67, adc2

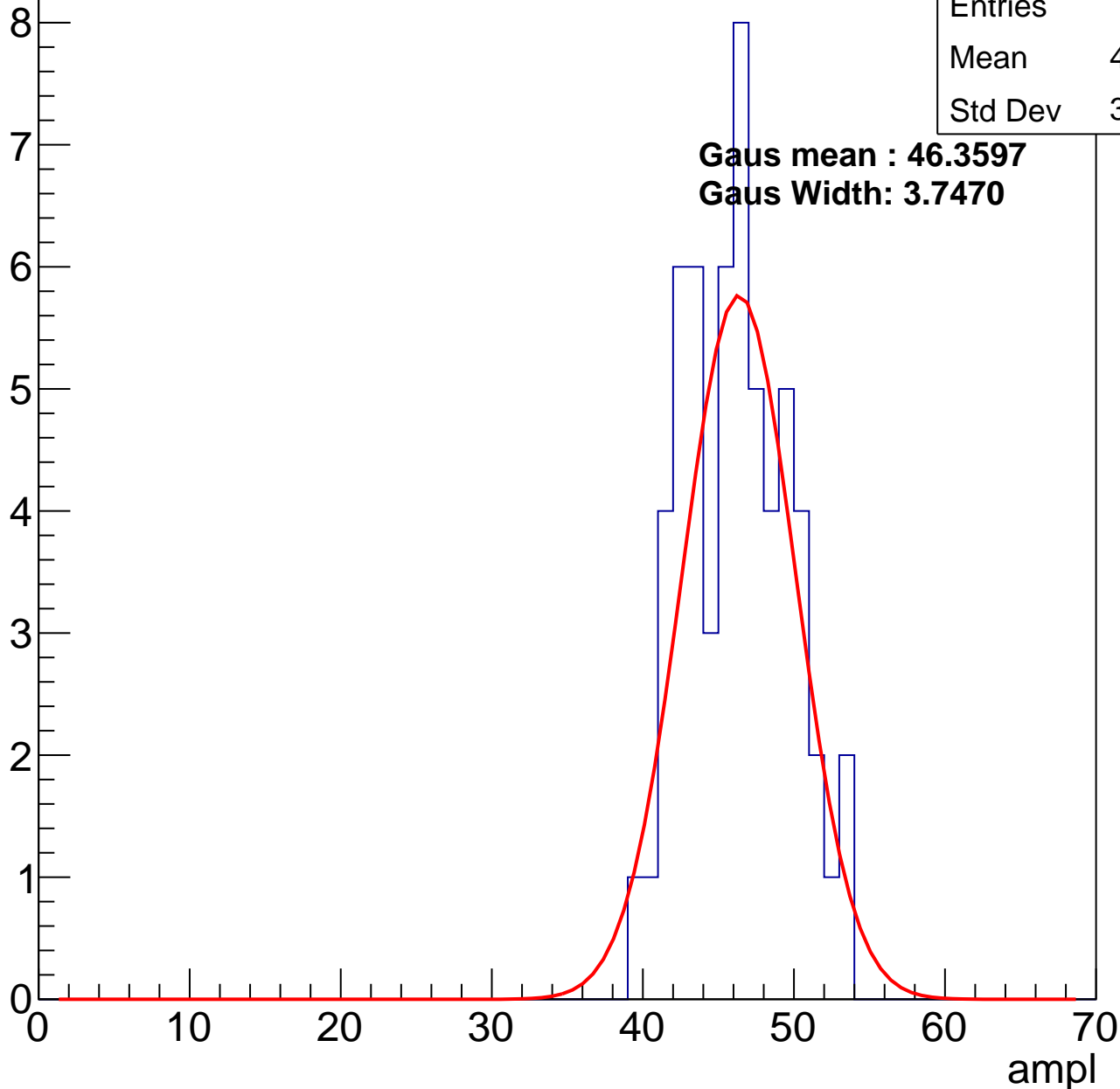
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	45.78
Std Dev	3.384

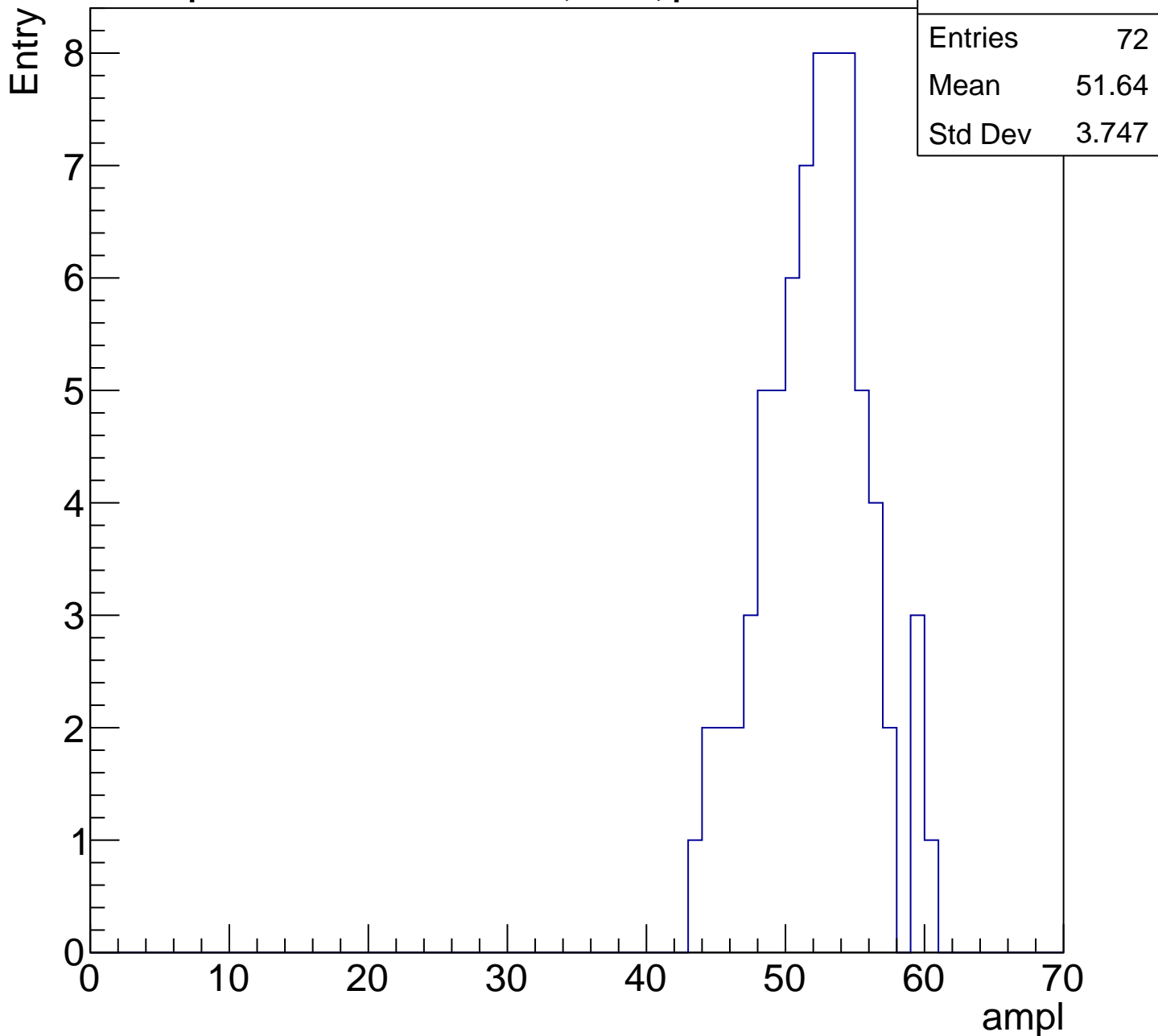
Gaus mean : 46.3597

Gaus Width: 3.7470



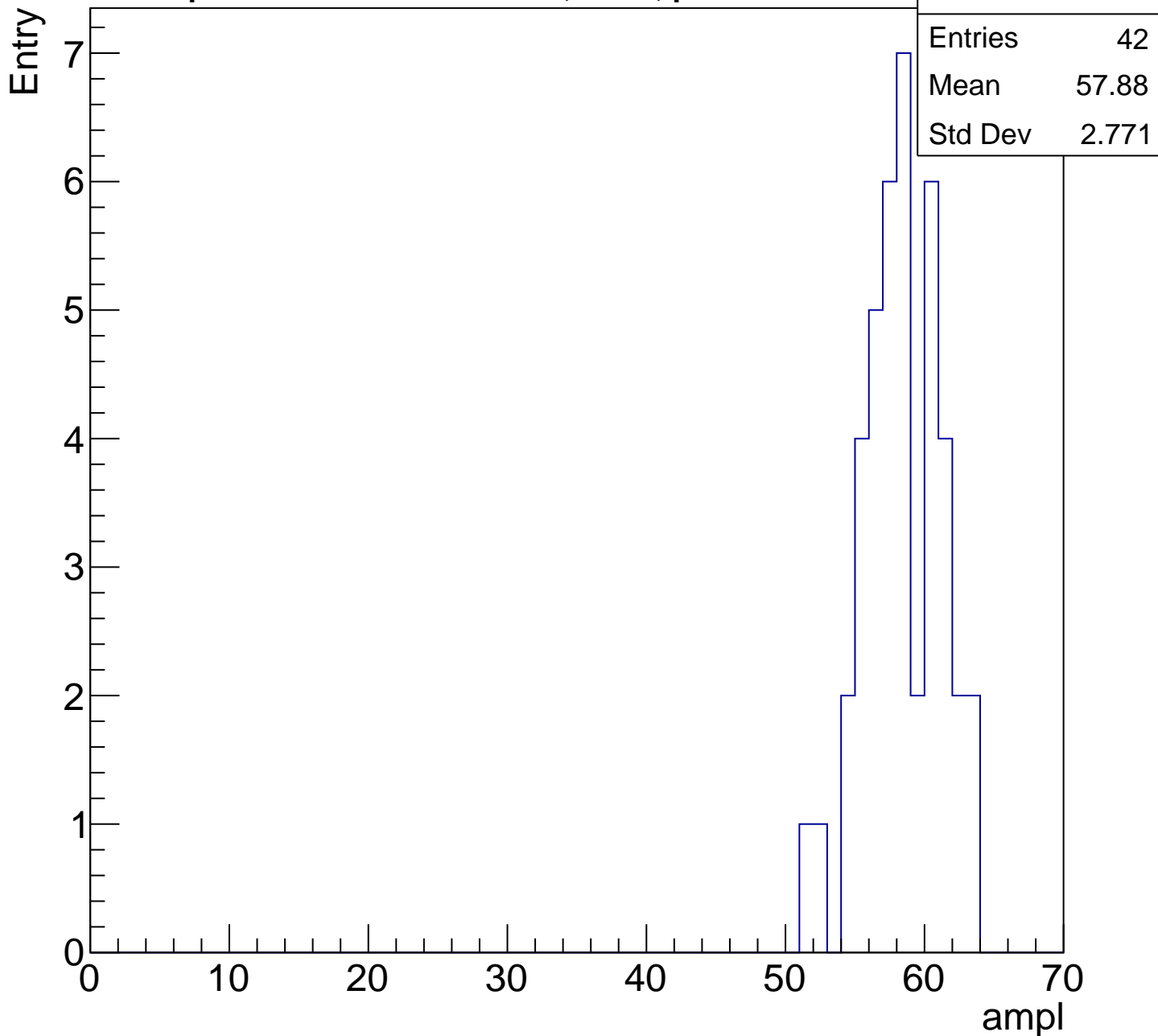
B1L101S, U25-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2

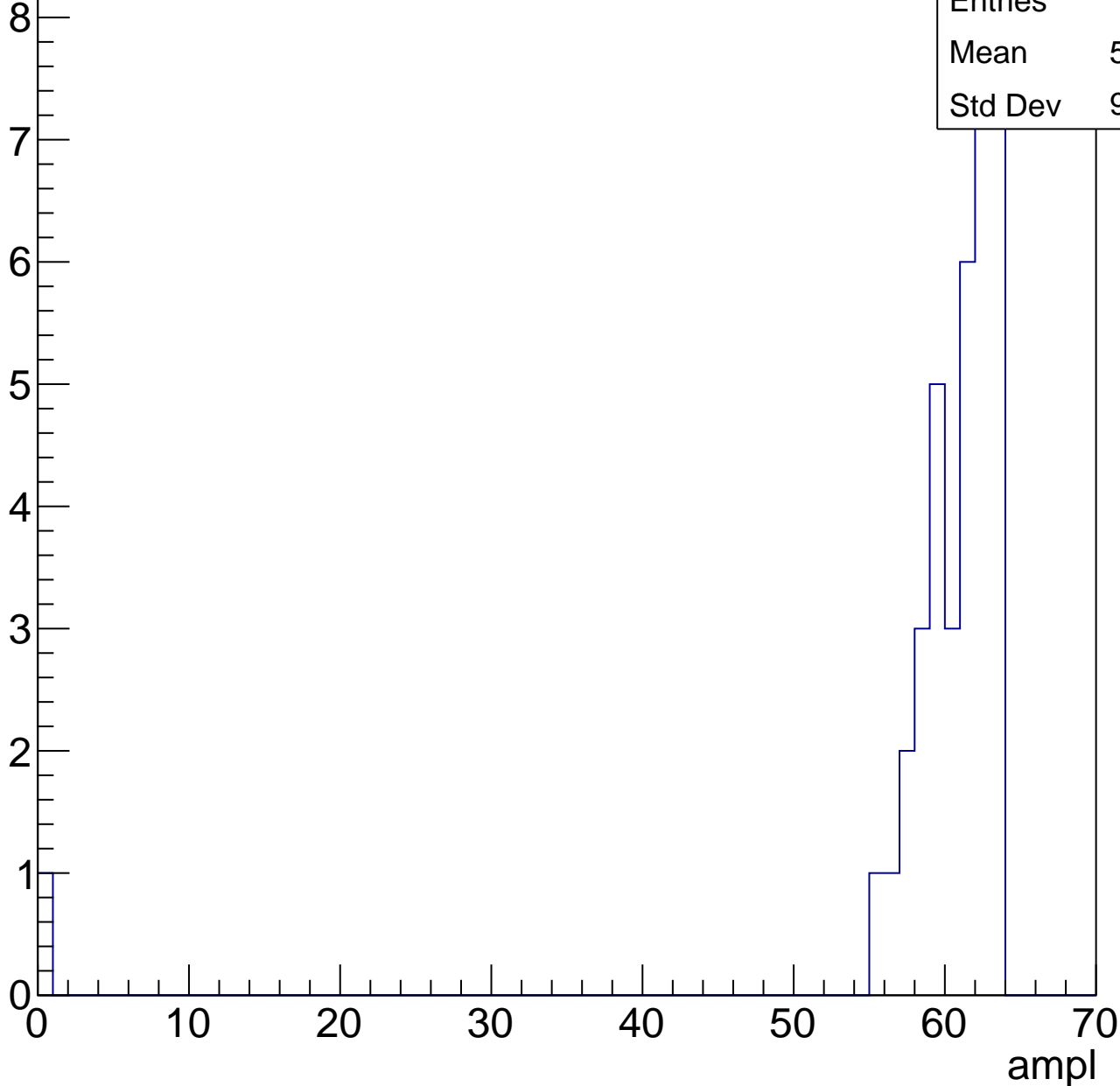


B1L101S, U25-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.95
Std Dev	9.926



B1L101S, U25-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch68, adc0

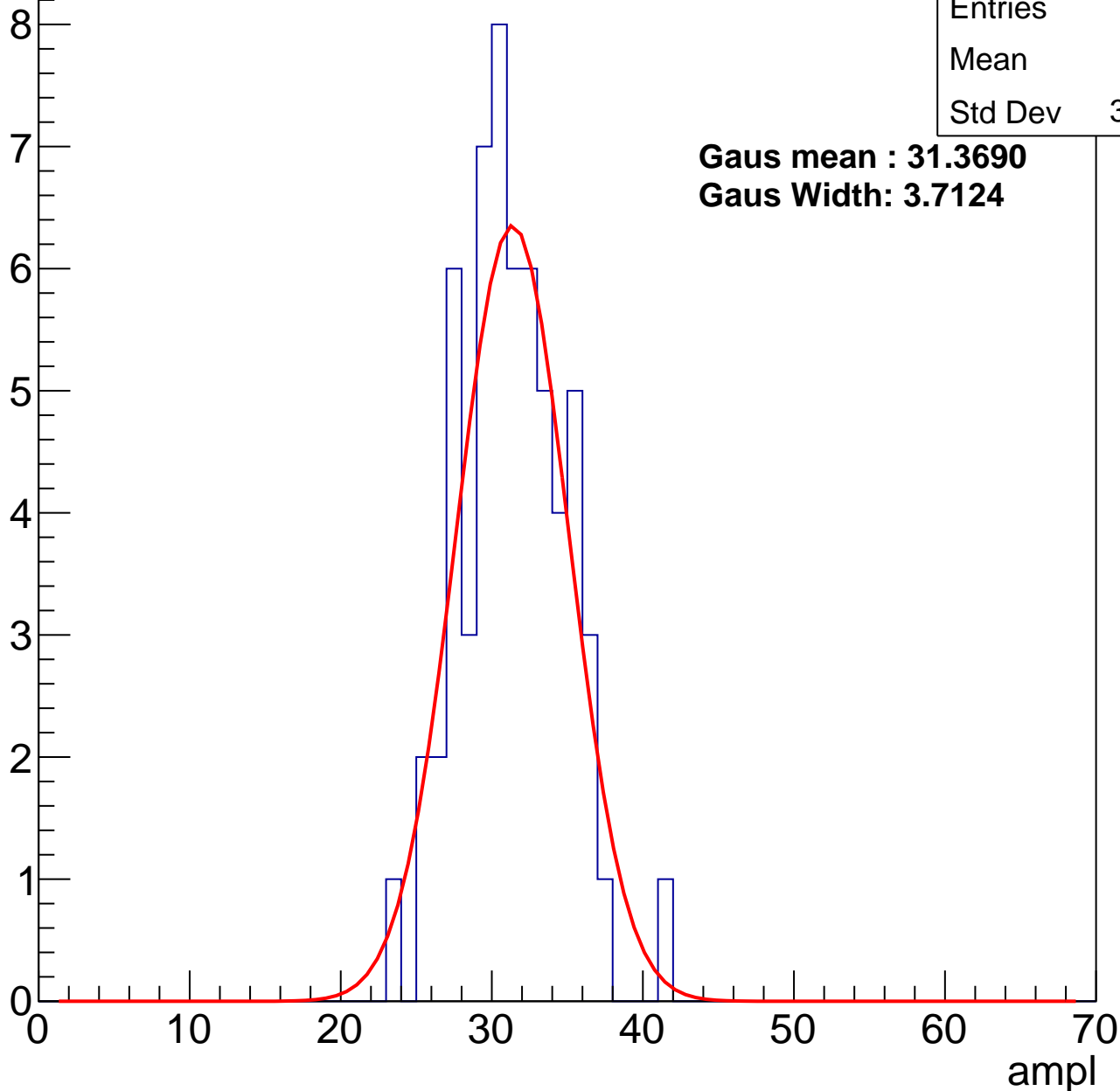
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.9
Std Dev	3.409

Gaus mean : 31.3690

Gaus Width: 3.7124



B1L101S, U25-ch68, adc1

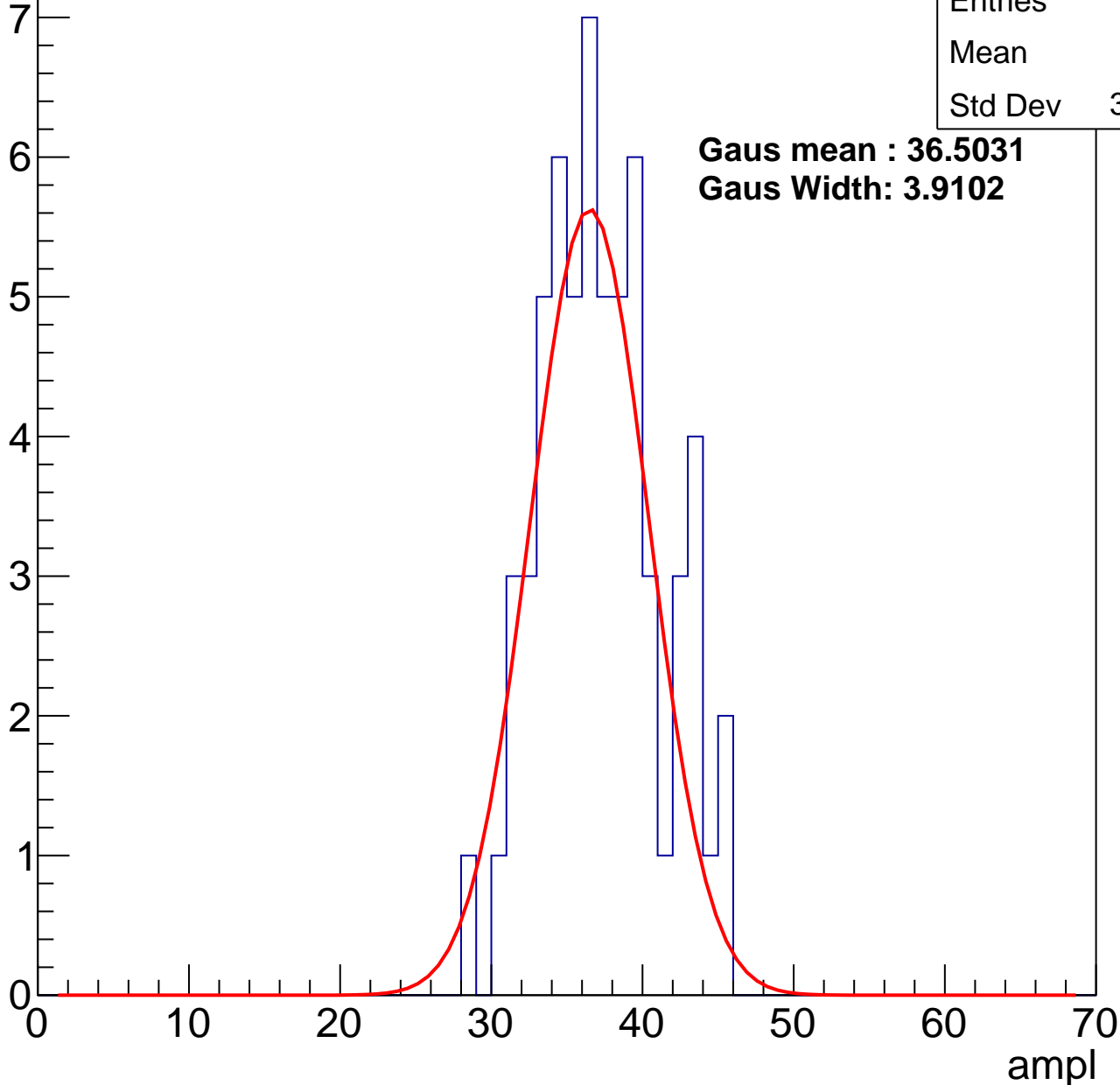
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.8
Std Dev	3.925

Gaus mean : 36.5031

Gaus Width: 3.9102



B1L101S, U25-ch68, adc2

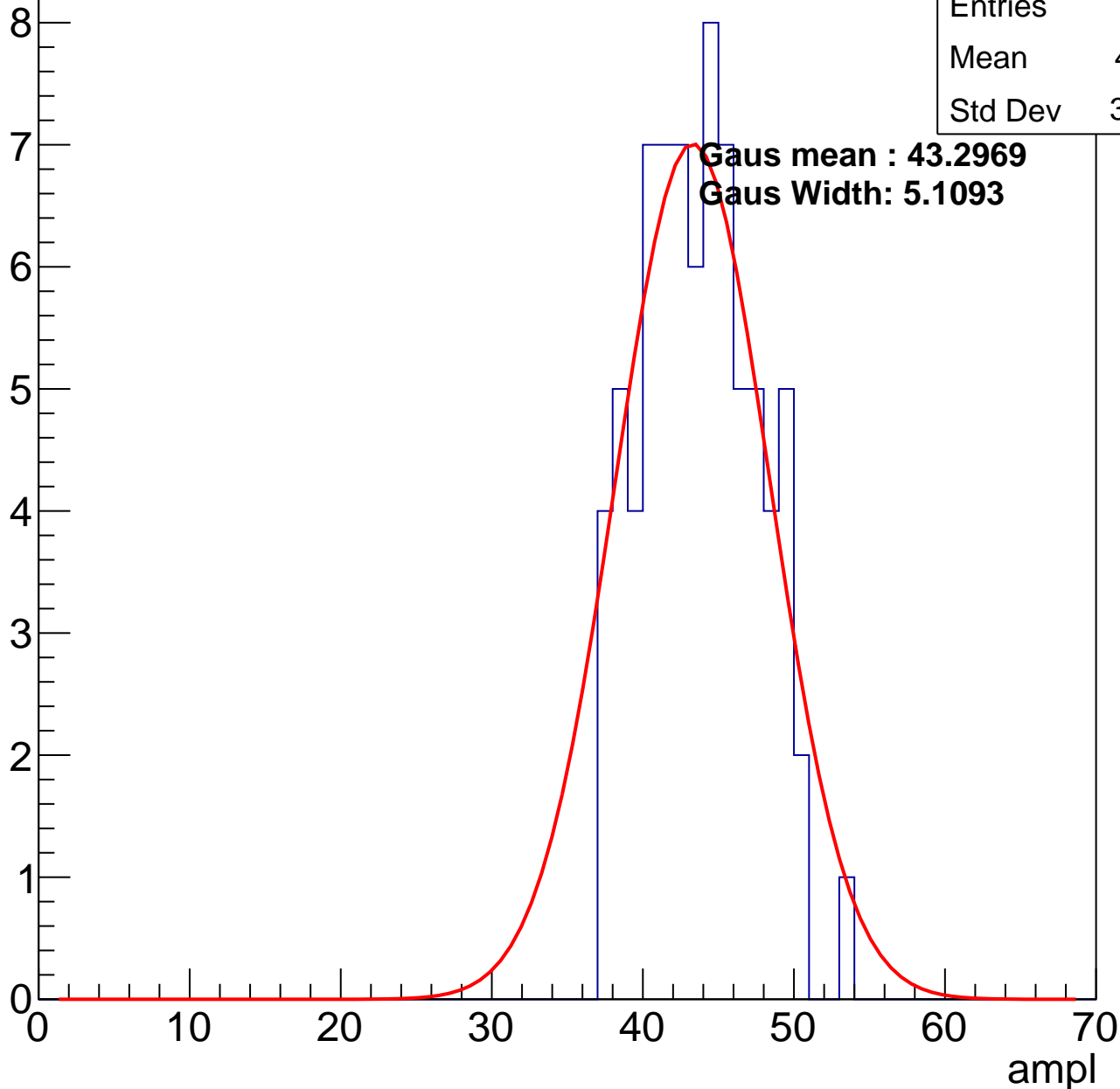
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	43.31
Std Dev	3.715

Gaus mean : 43.2969

Gaus Width: 5.1093

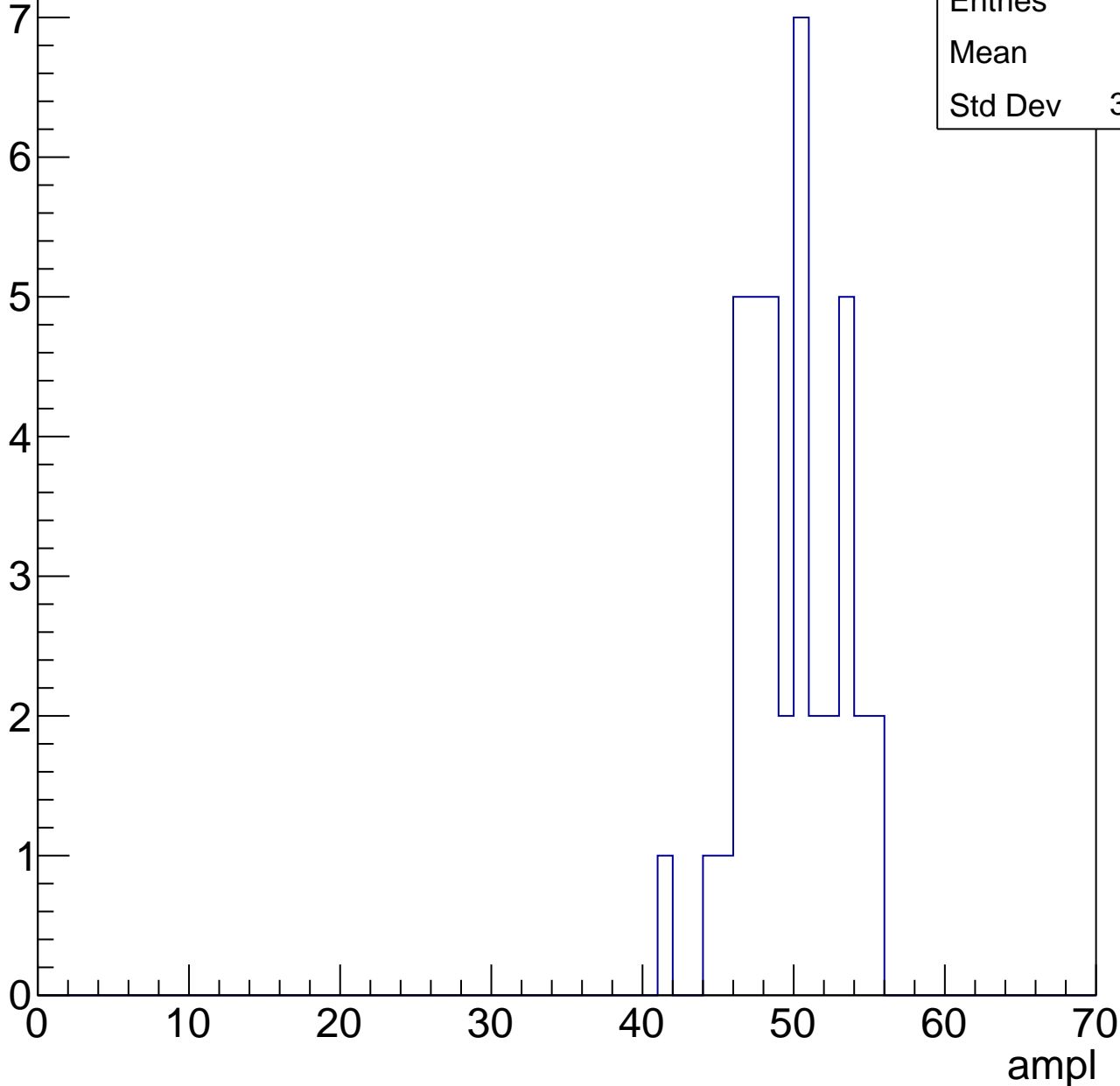


B1L101S, U25-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	49.3
Std Dev	3.187

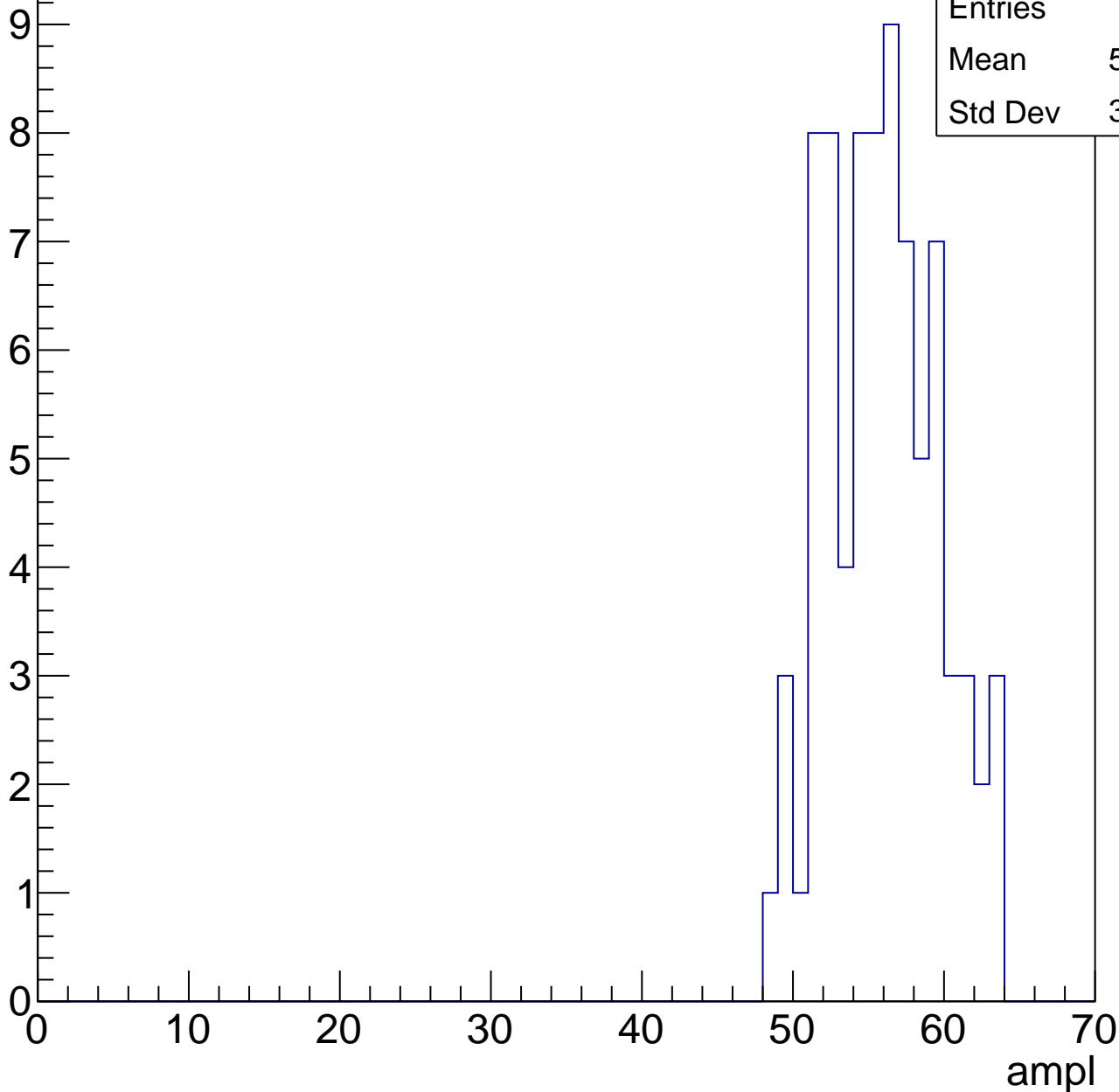


B1L101S, U25-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

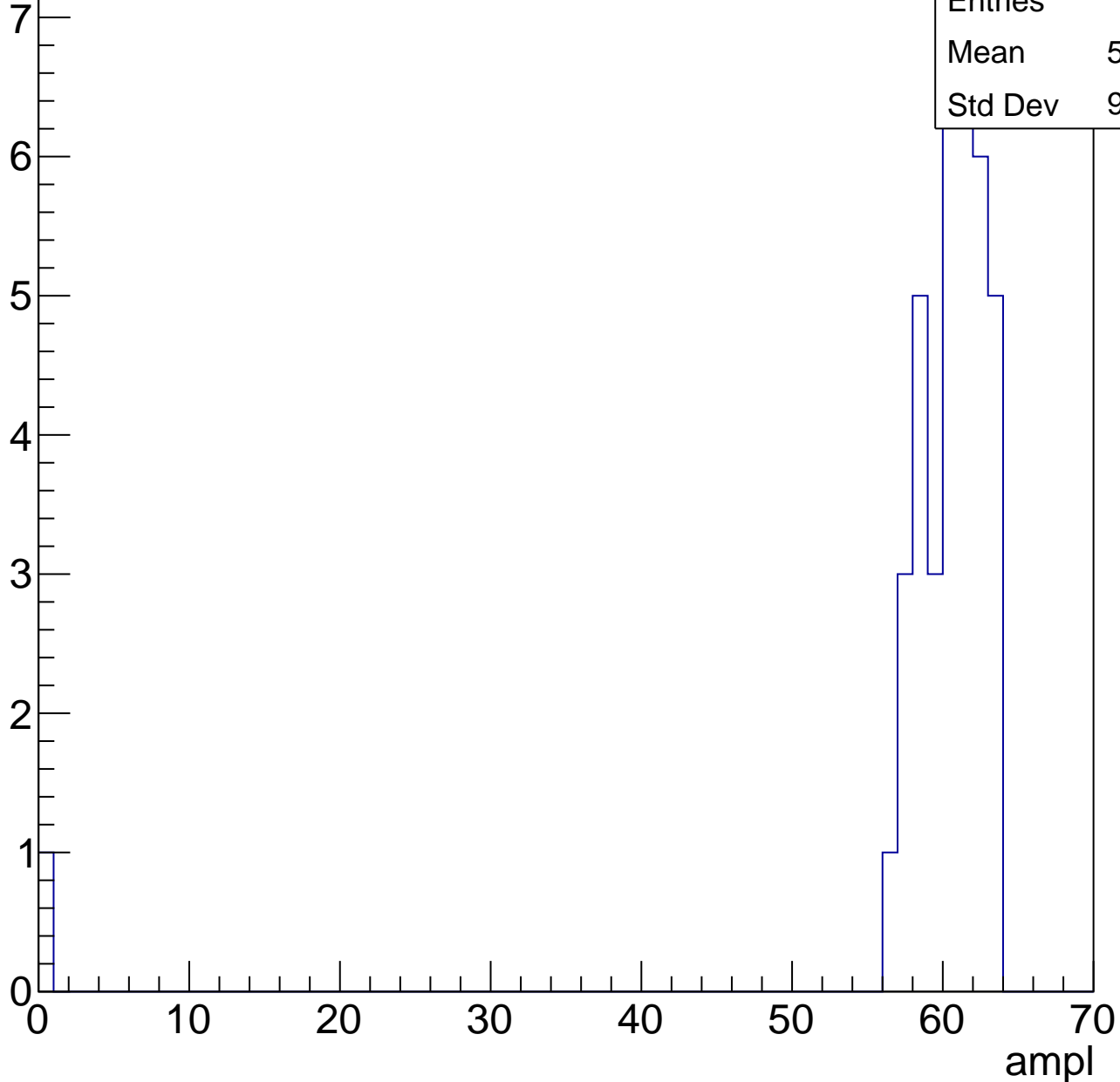
Entries	80
Mean	55.44
Std Dev	3.636



B1L101S, U25-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

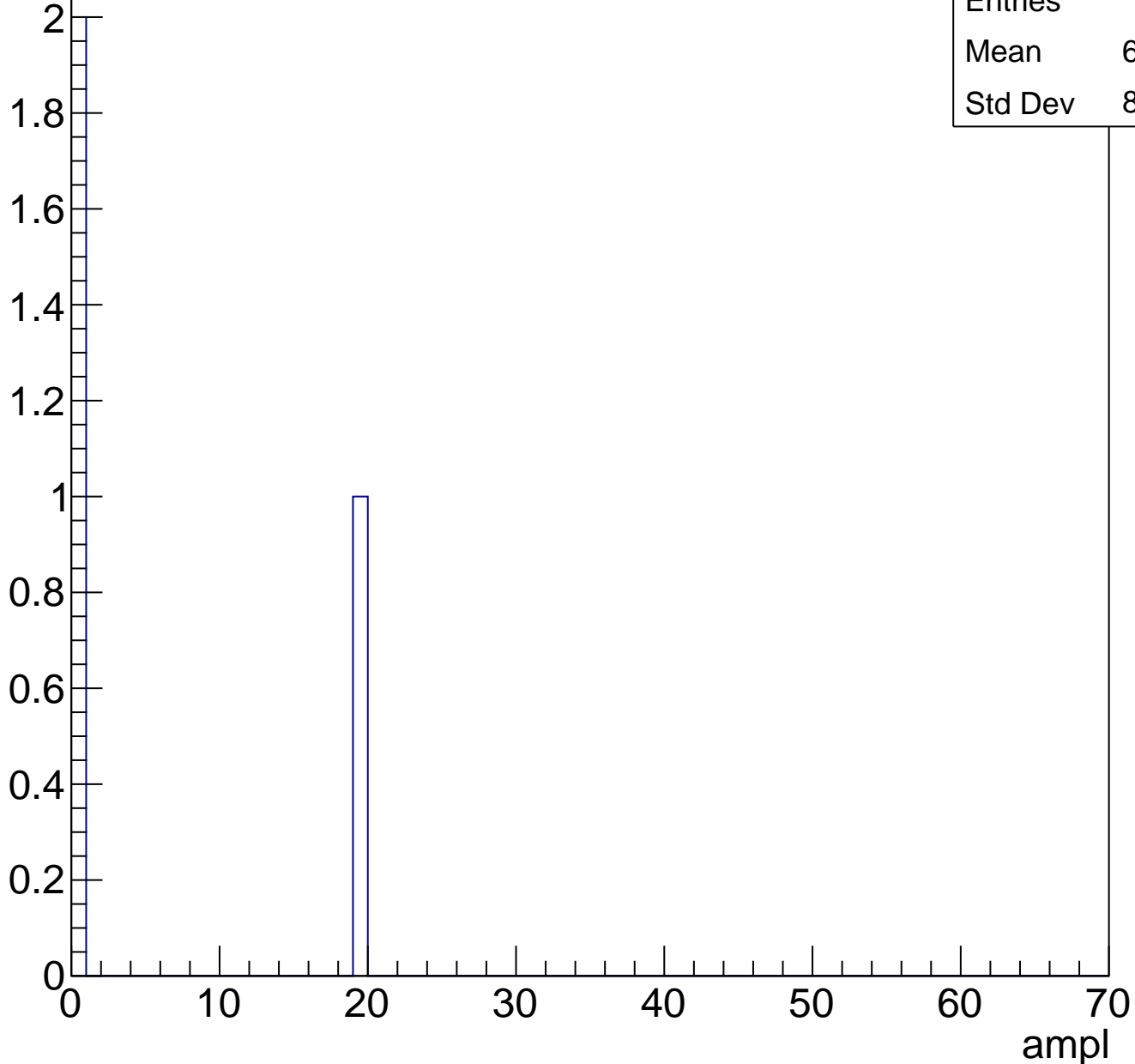
Entry



B1L101S, U25-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L101S, U25-ch69, adc0

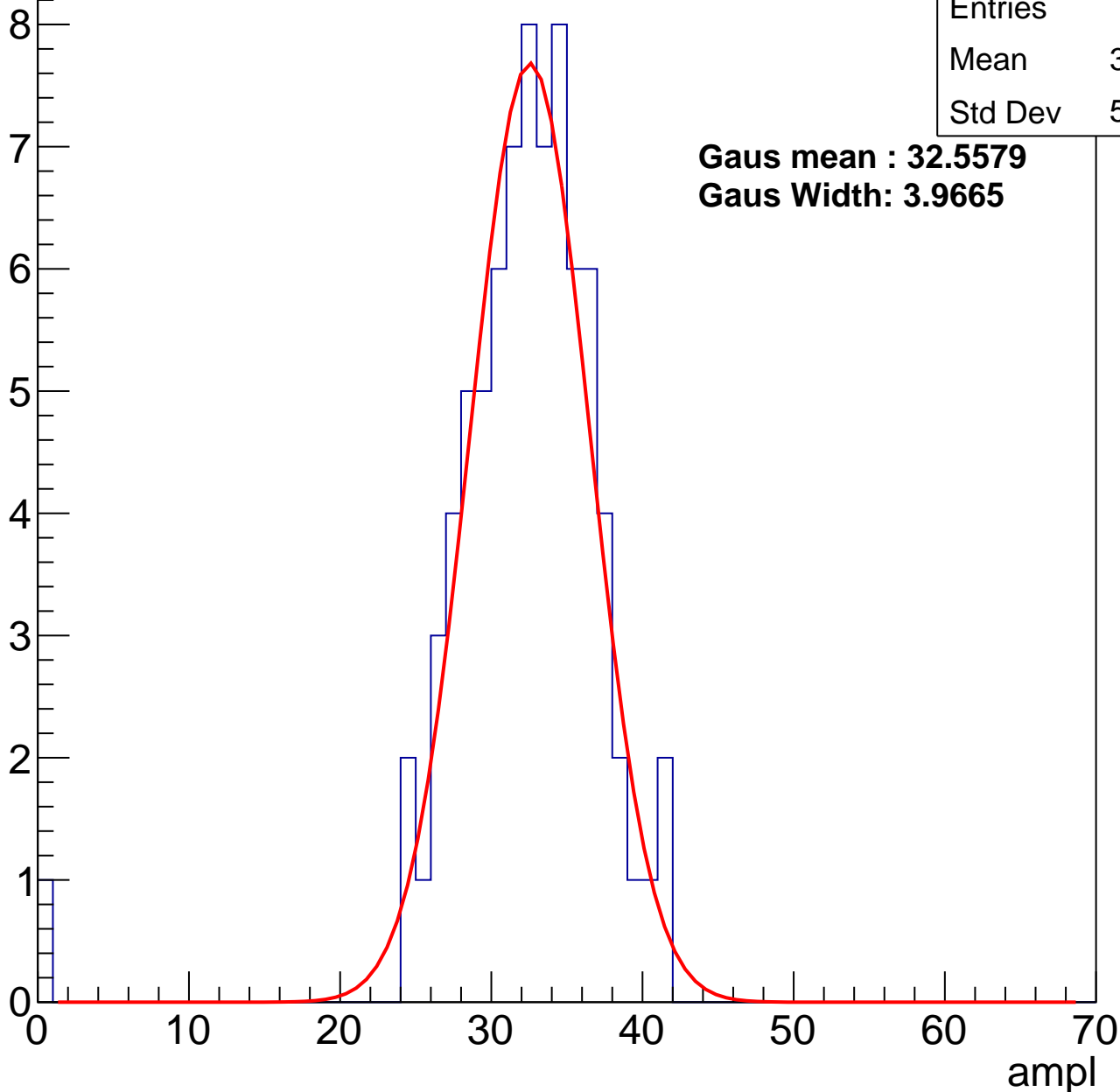
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	31.78
Std Dev	5.274

Gaus mean : 32.5579

Gaus Width: 3.9665



B1L101S, U25-ch69, adc1

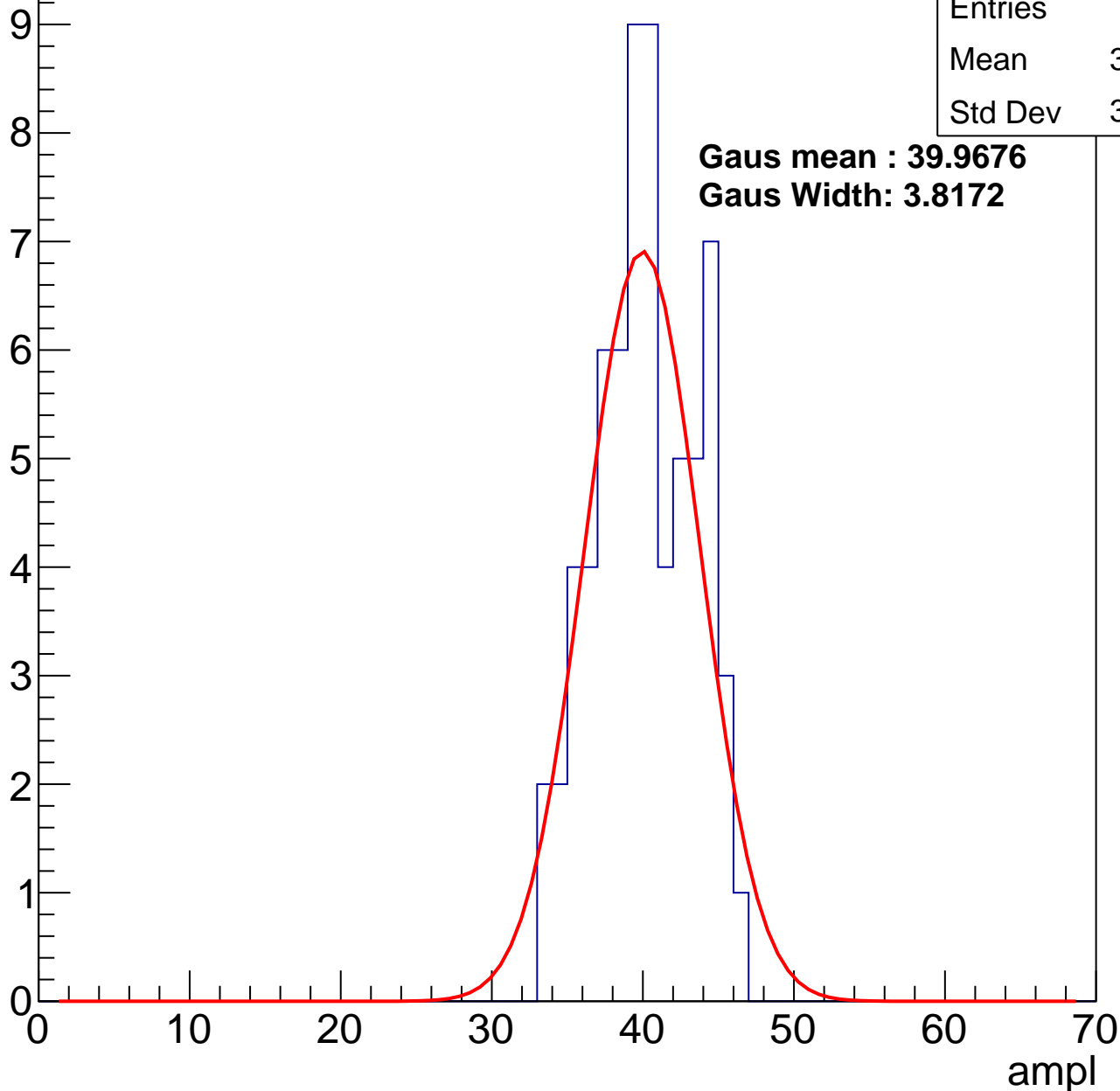
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	39.66
Std Dev	3.244

Gaus mean : 39.9676

Gaus Width: 3.8172



B1L101S, U25-ch69, adc2

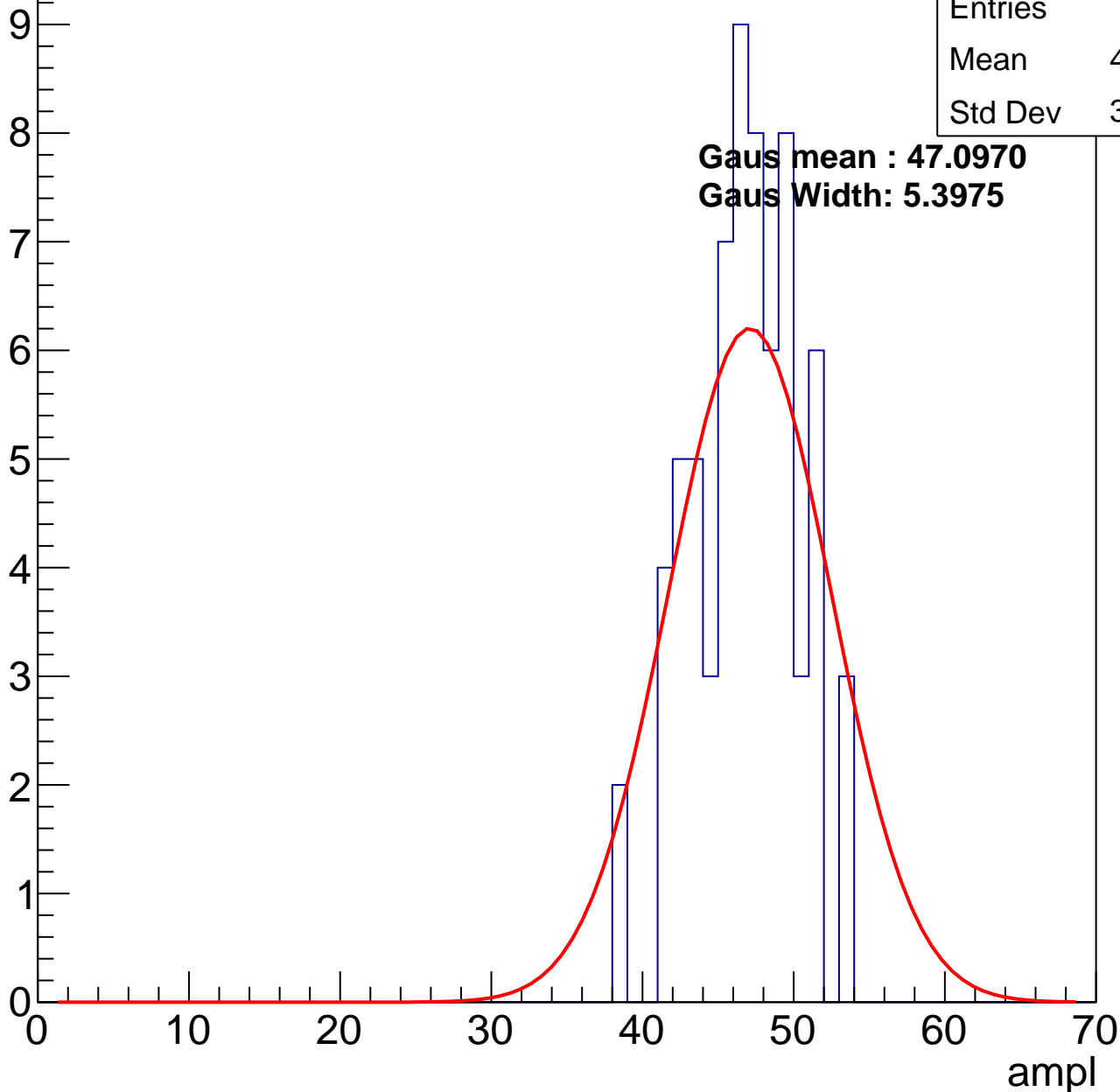
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	46.33
Std Dev	3.433

Gaus mean : 47.0970

Gaus Width: 5.3975



B1L101S, U25-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	61
Mean	52.9
Std Dev	3.071

Entry

10

8

6

4

2

0

0

10

20

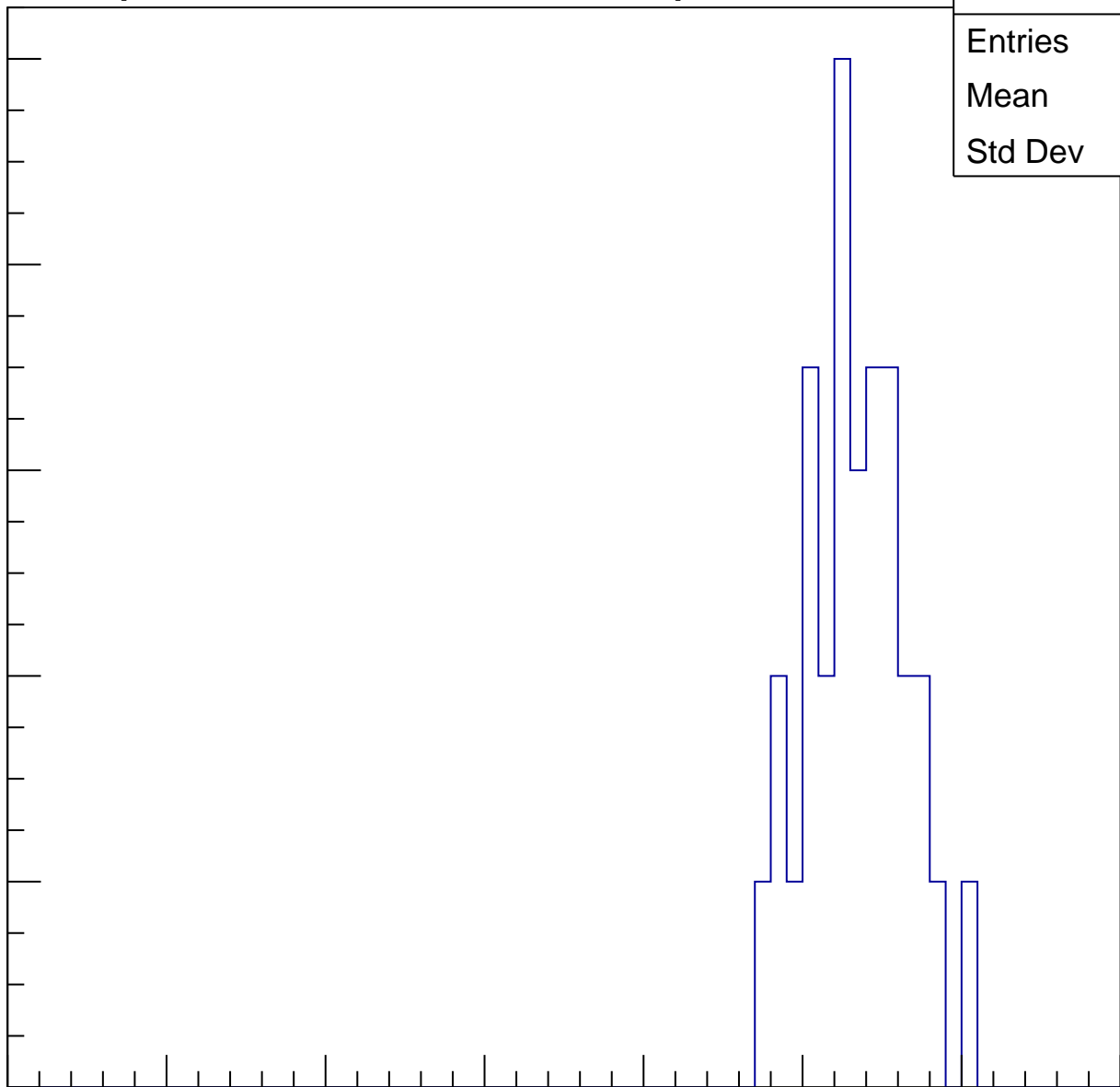
30

40

50

60

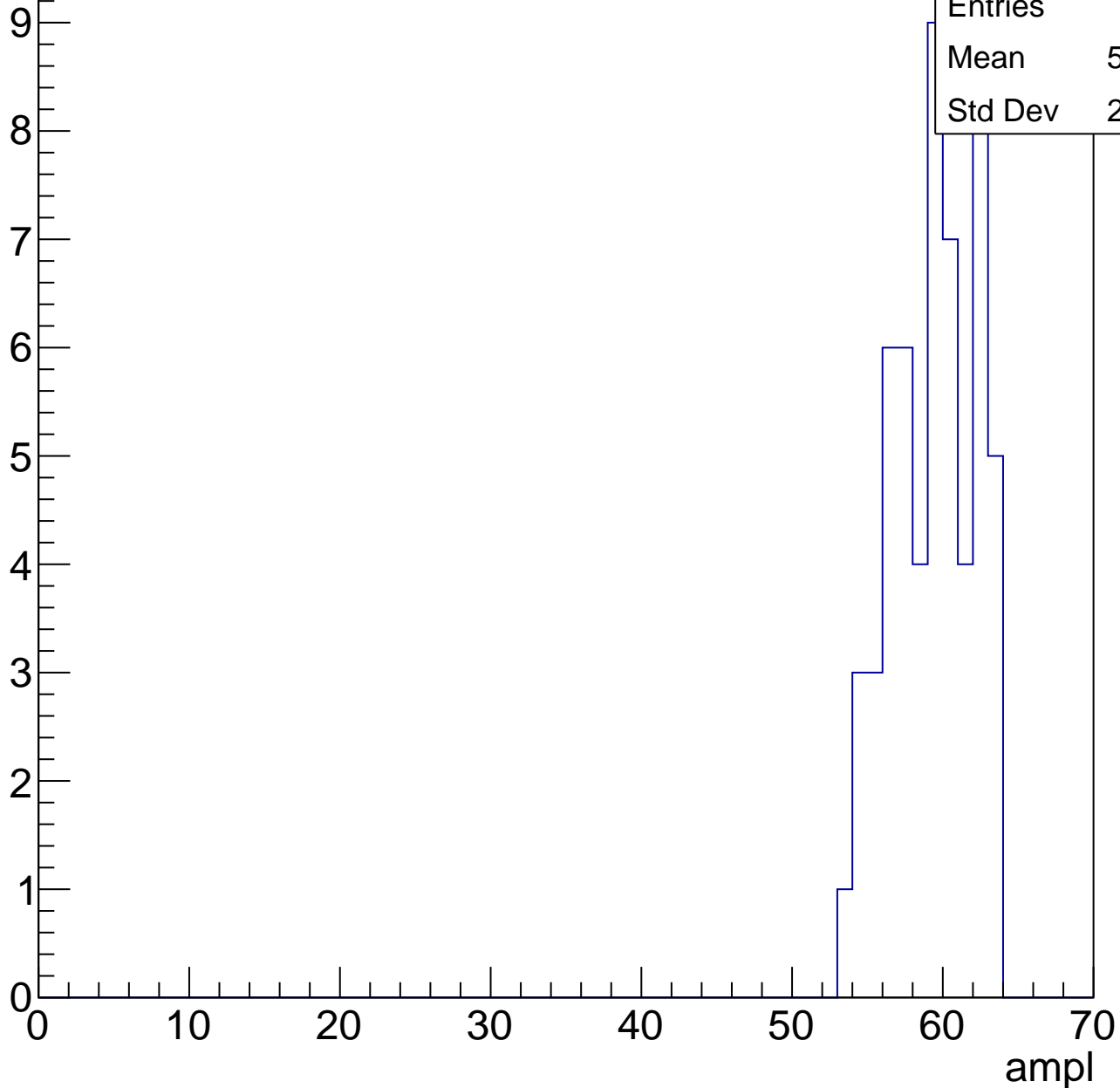
ampl



B1L101S, U25-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



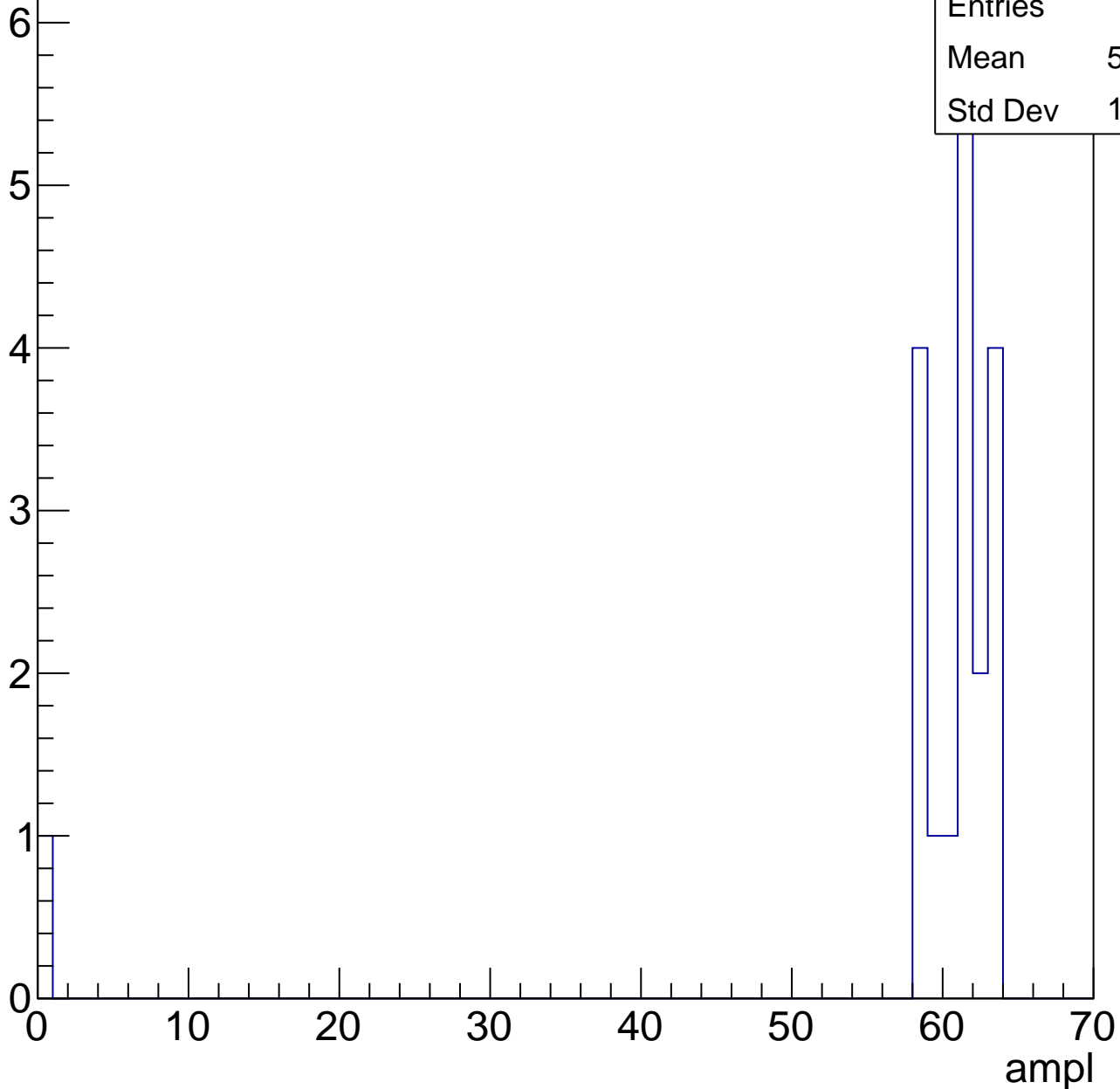
Entries	56
Mean	58.86
Std Dev	2.722

B1L101S, U25-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	57.53
Std Dev	13.67



B1L101S, U25-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch70, adc0

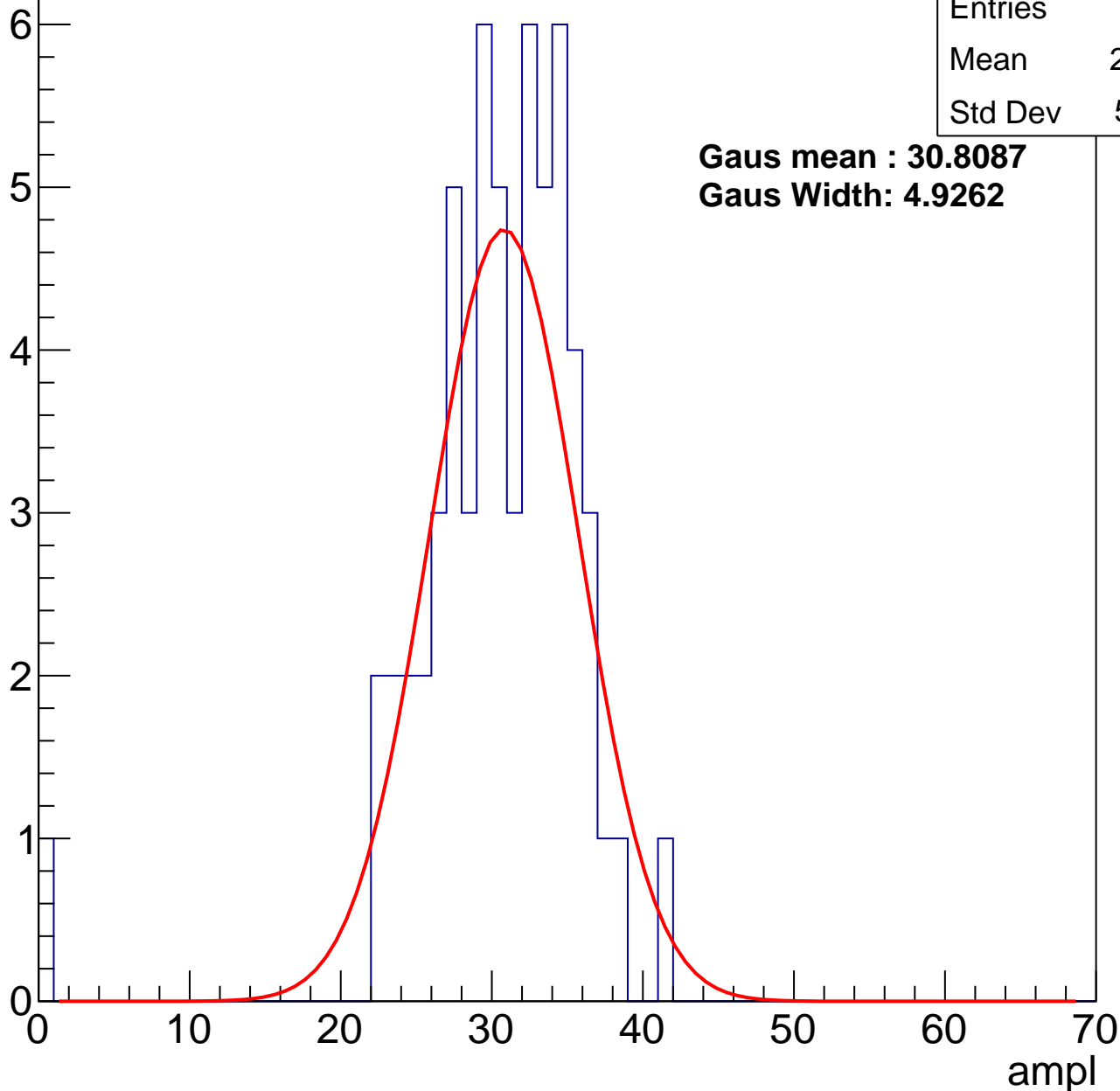
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.95
Std Dev	5.681

Gaus mean : 30.8087

Gaus Width: 4.9262



B1L101S, U25-ch70, adc1

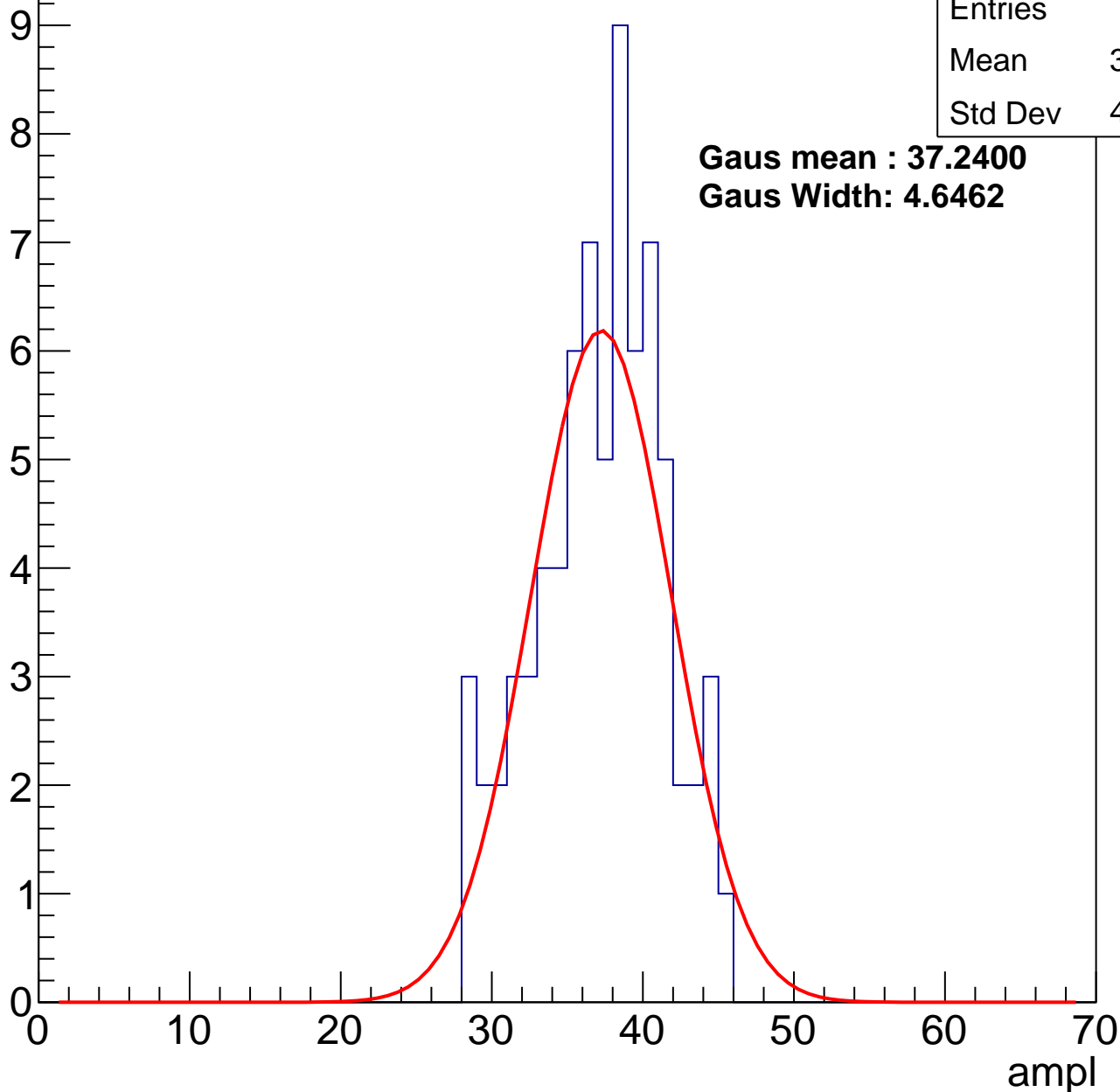
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.68
Std Dev	4.166

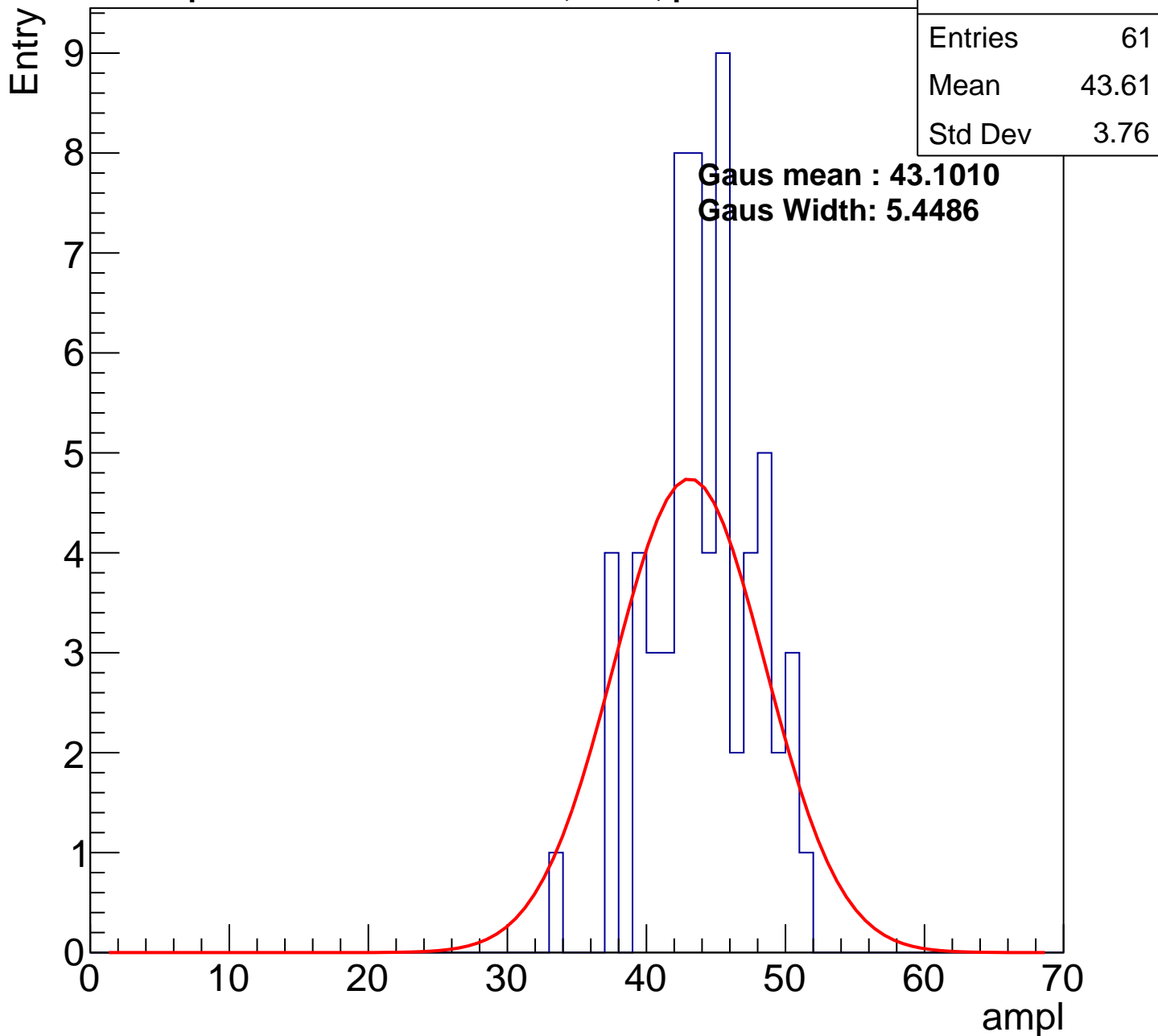
Gaus mean : 37.2400

Gaus Width: 4.6462



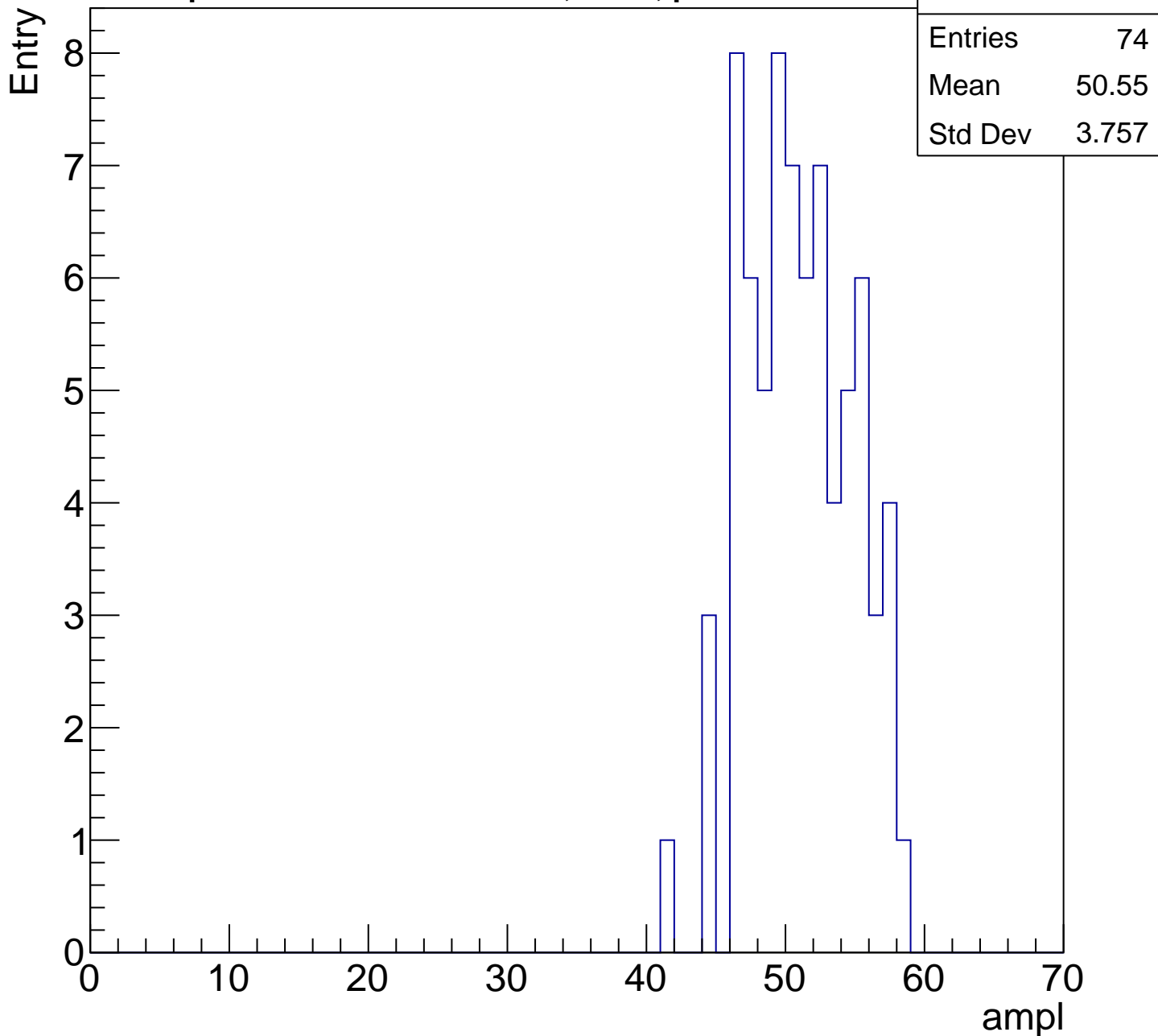
B1L101S, U25-ch70, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch70, adc3

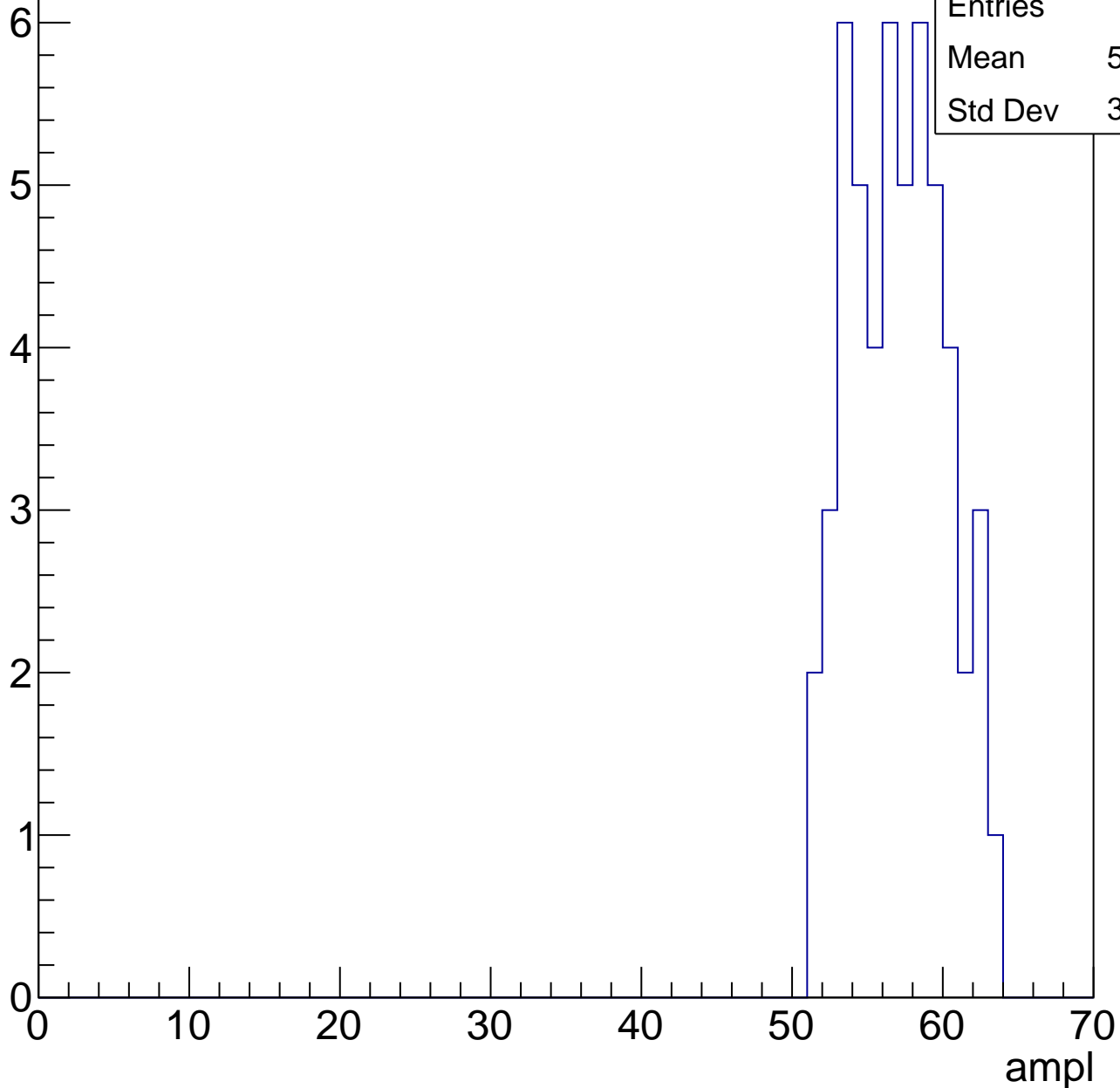
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



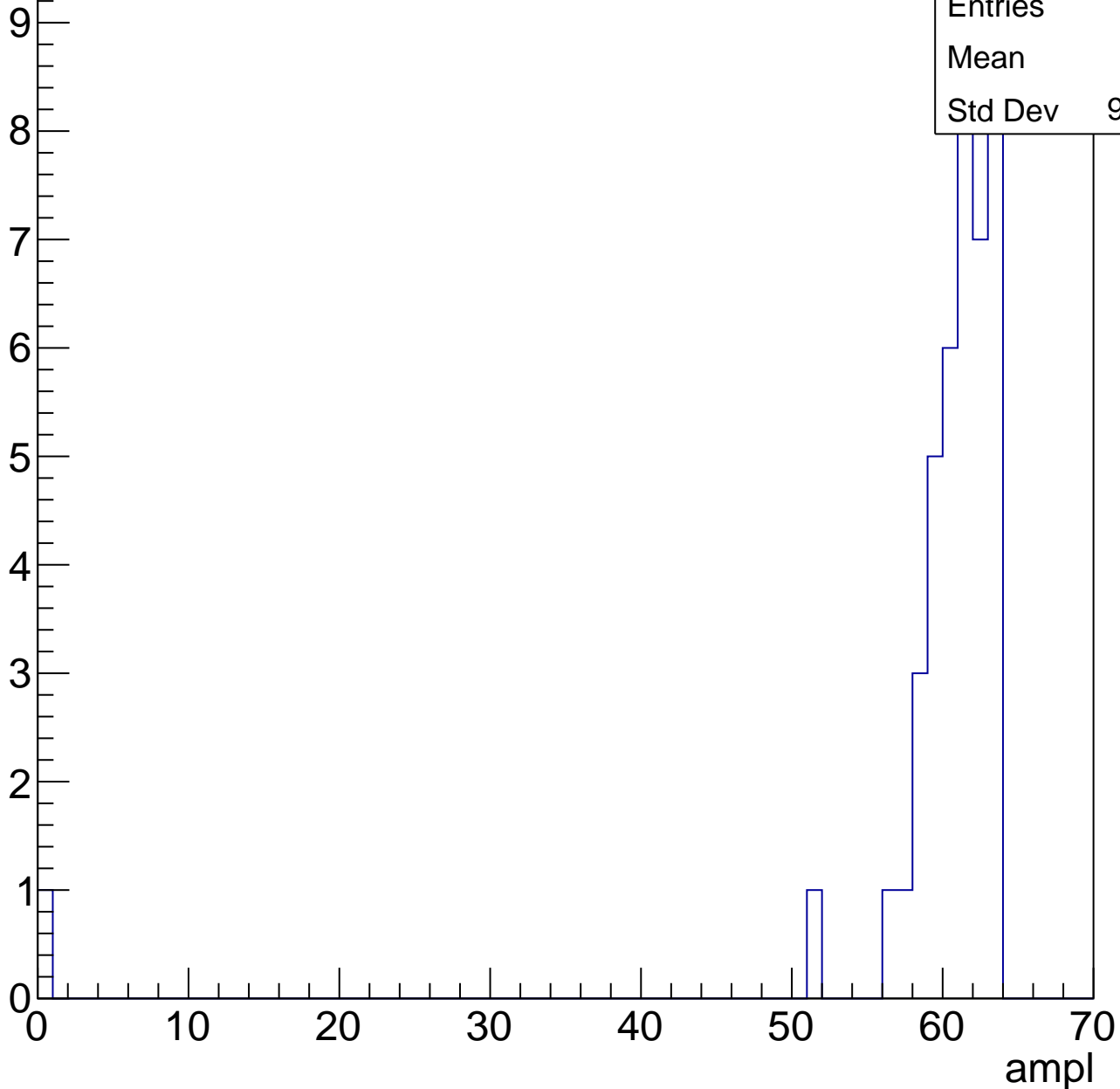
Entries	52
Mean	56.56
Std Dev	3.116

B1L101S, U25-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.1
Std Dev	9.519



B1L101S, U25-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch71, adc0

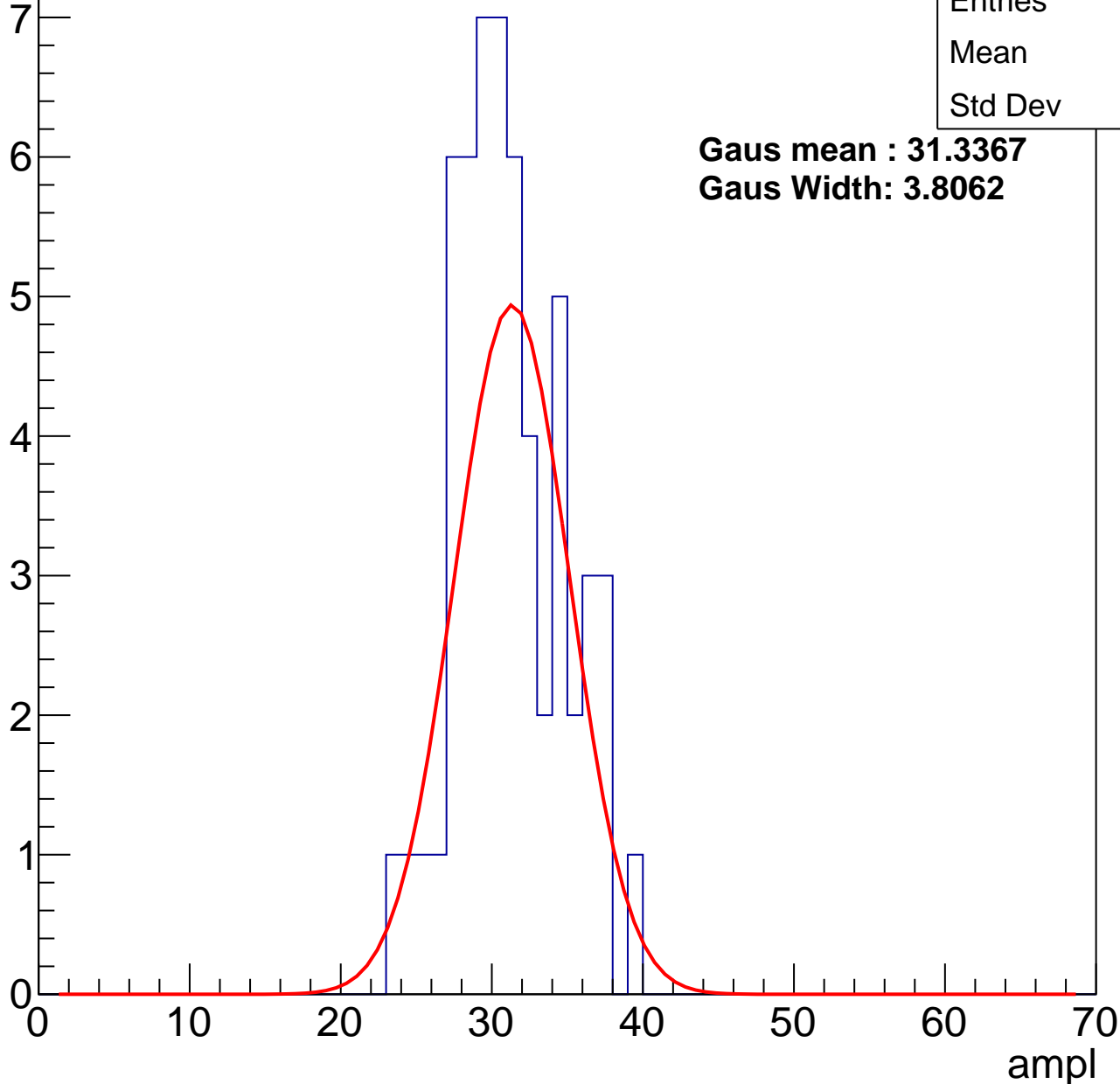
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	30.7
Std Dev	3.51

Gaus mean : 31.3367

Gaus Width: 3.8062



B1L101S, U25-ch71, adc1

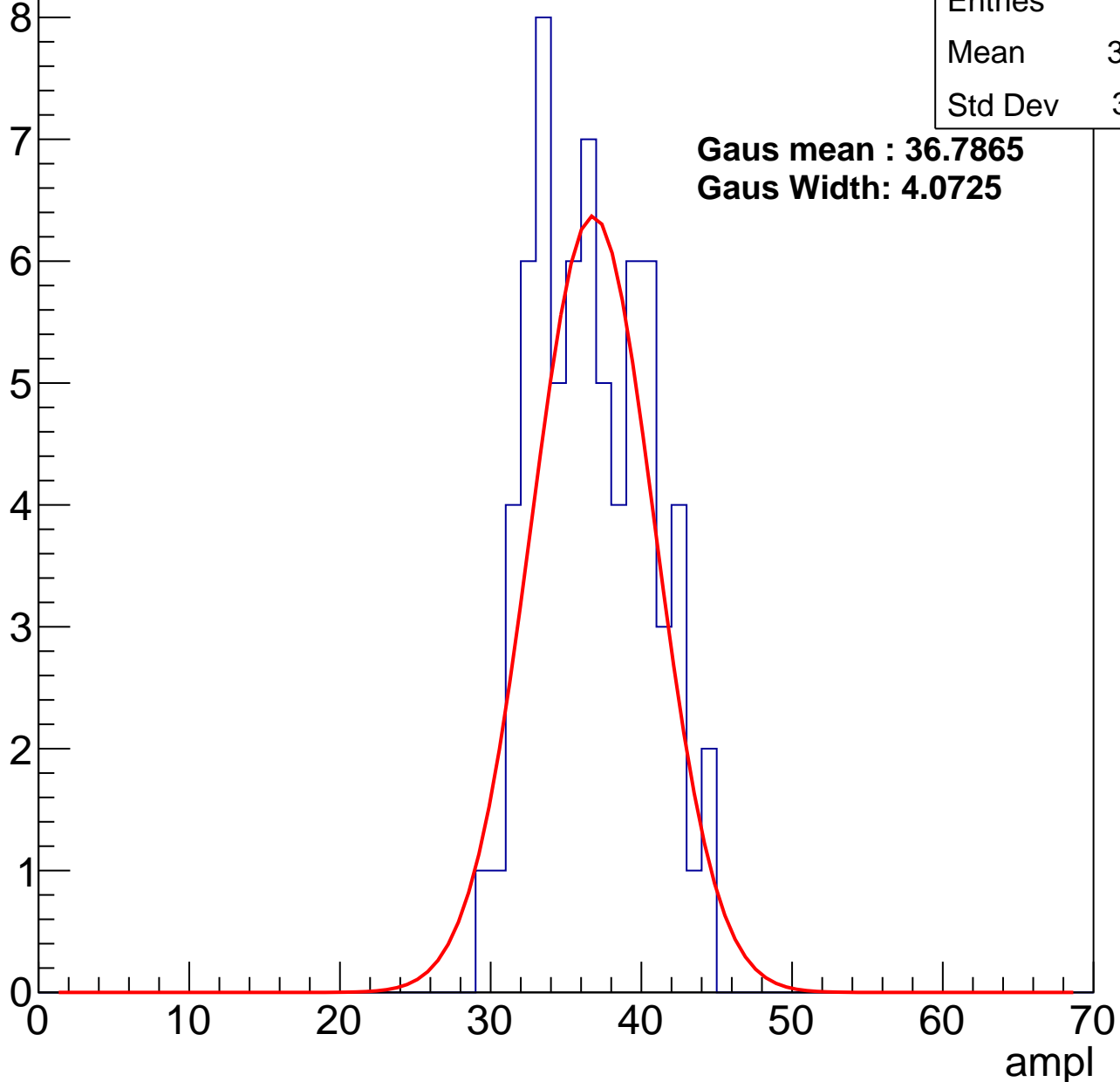
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.29
Std Dev	3.691

Gaus mean : 36.7865

Gaus Width: 4.0725



B1L101S, U25-ch71, adc2

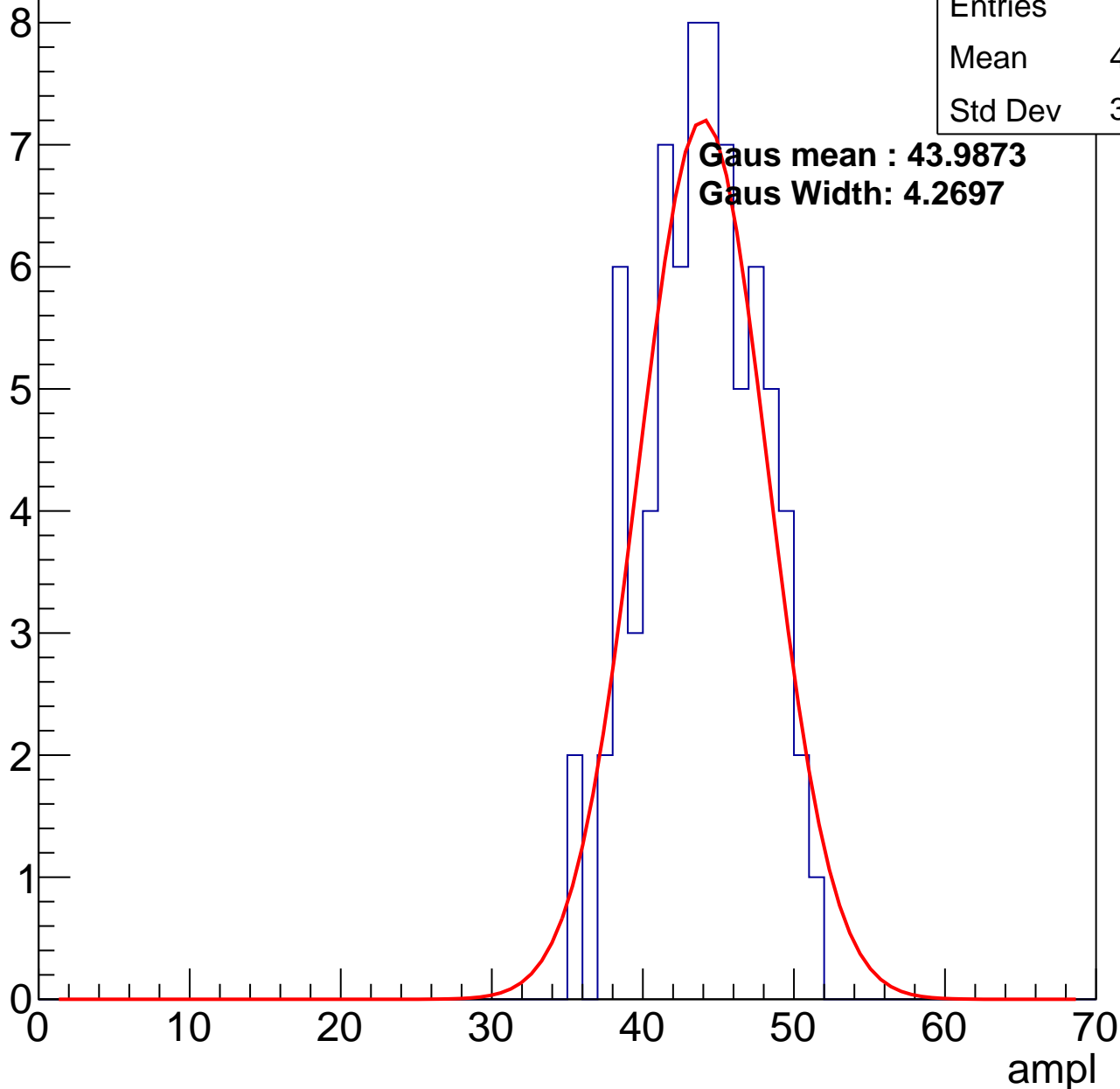
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	43.39
Std Dev	3.742

Gaus mean : 43.9873

Gaus Width: 4.2697

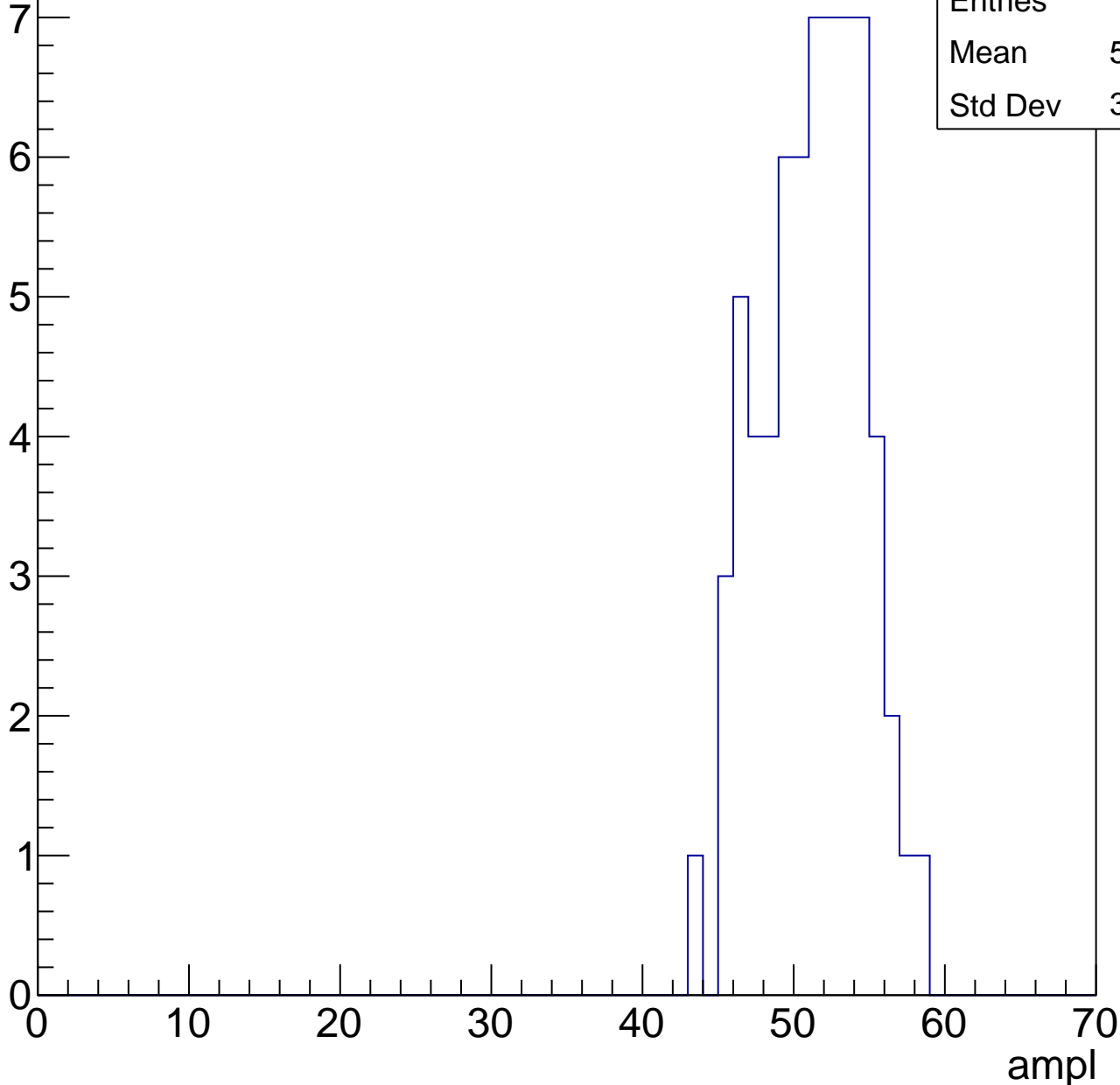


B1L101S, U25-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

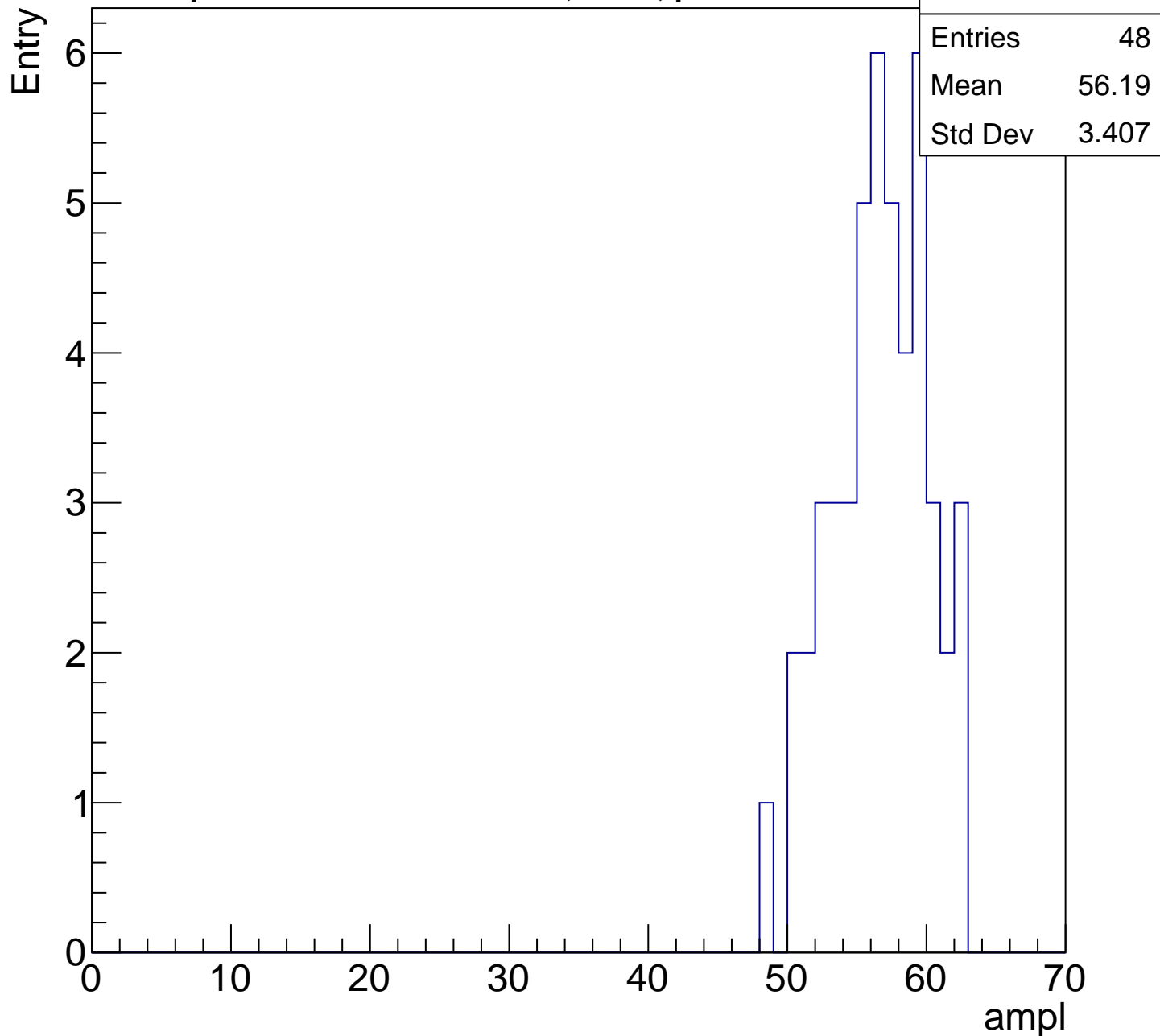
Entry

Entries	65
Mean	50.75
Std Dev	3.328



B1L101S, U25-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch71, adc5

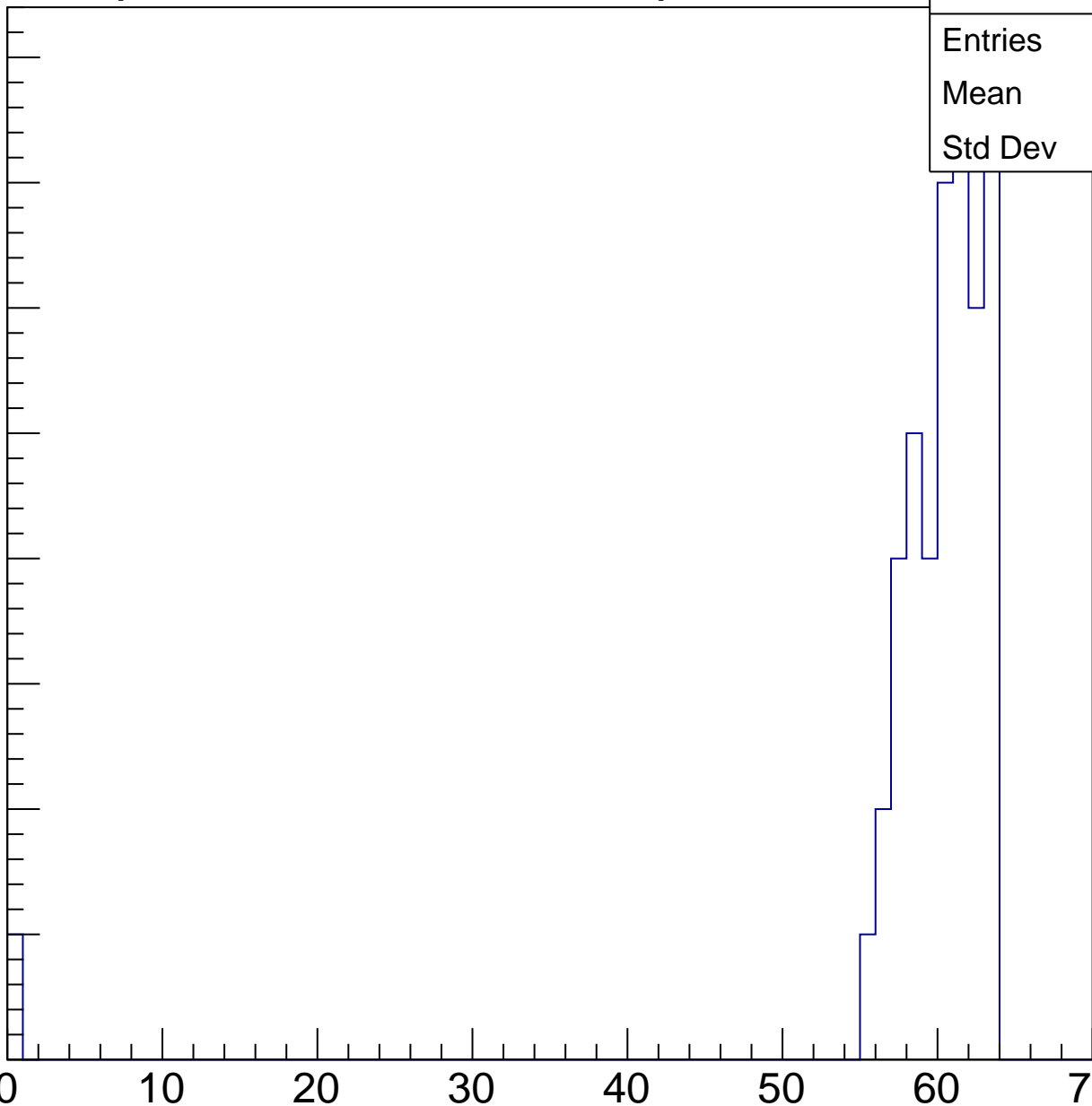
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.8
Std Dev	9.035

ampl



B1L101S, U25-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch72, adc0

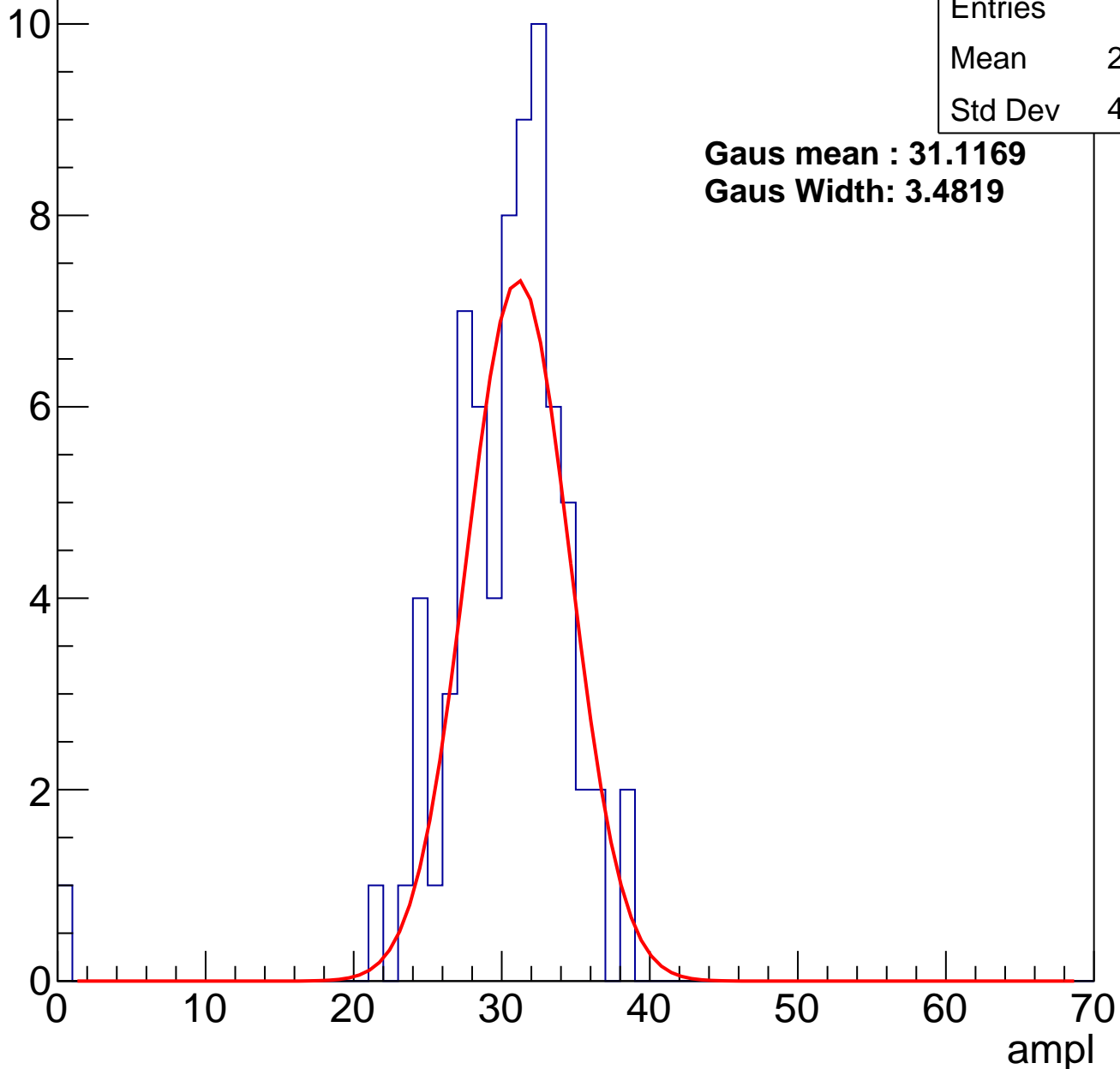
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	29.74
Std Dev	4.953

Gaus mean : 31.1169

Gaus Width: 3.4819

Entry



B1L101S, U25-ch72, adc1

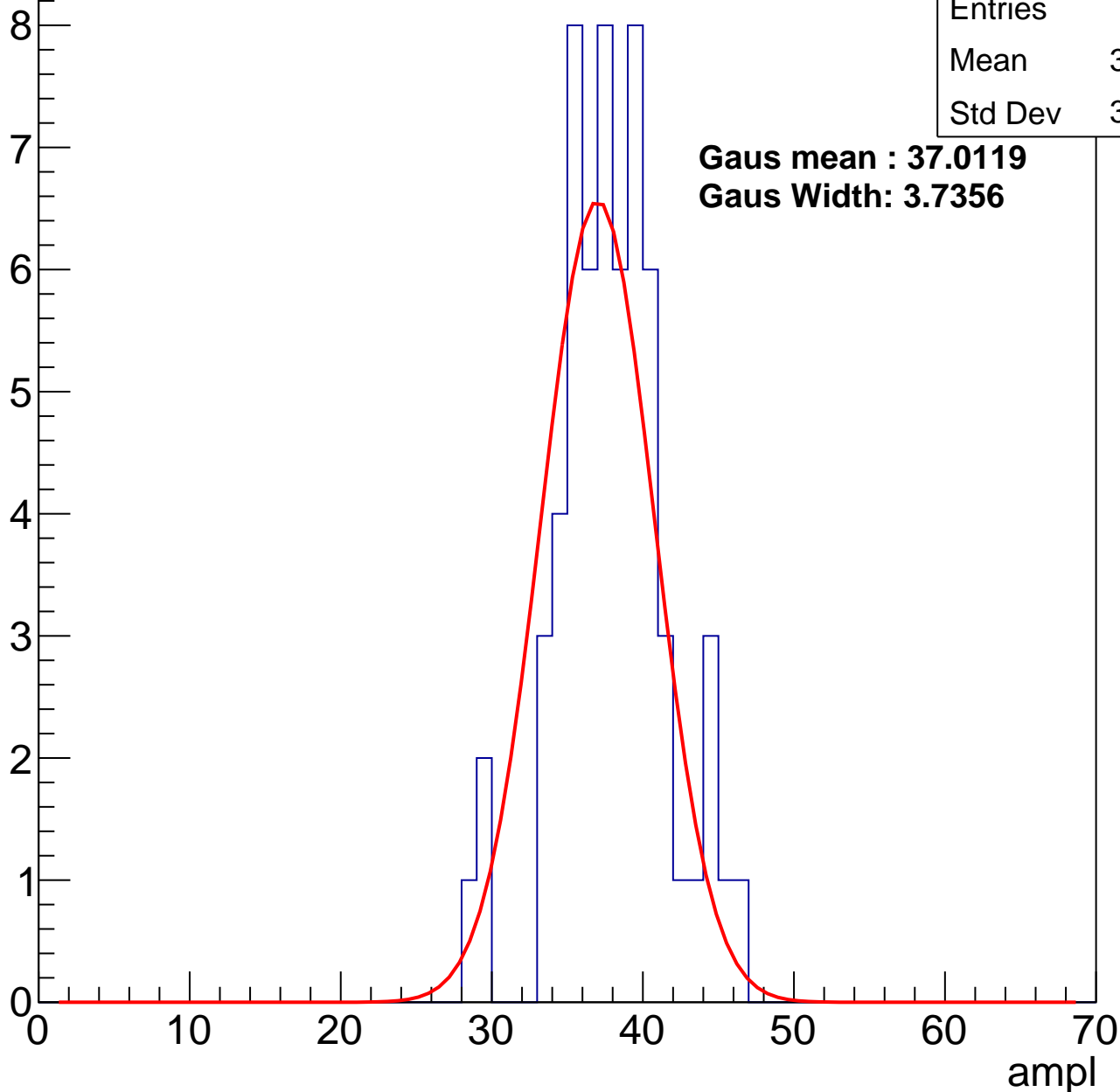
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.48
Std Dev	3.613

Gaus mean : 37.0119

Gaus Width: 3.7356



B1L101S, U25-ch72, adc2

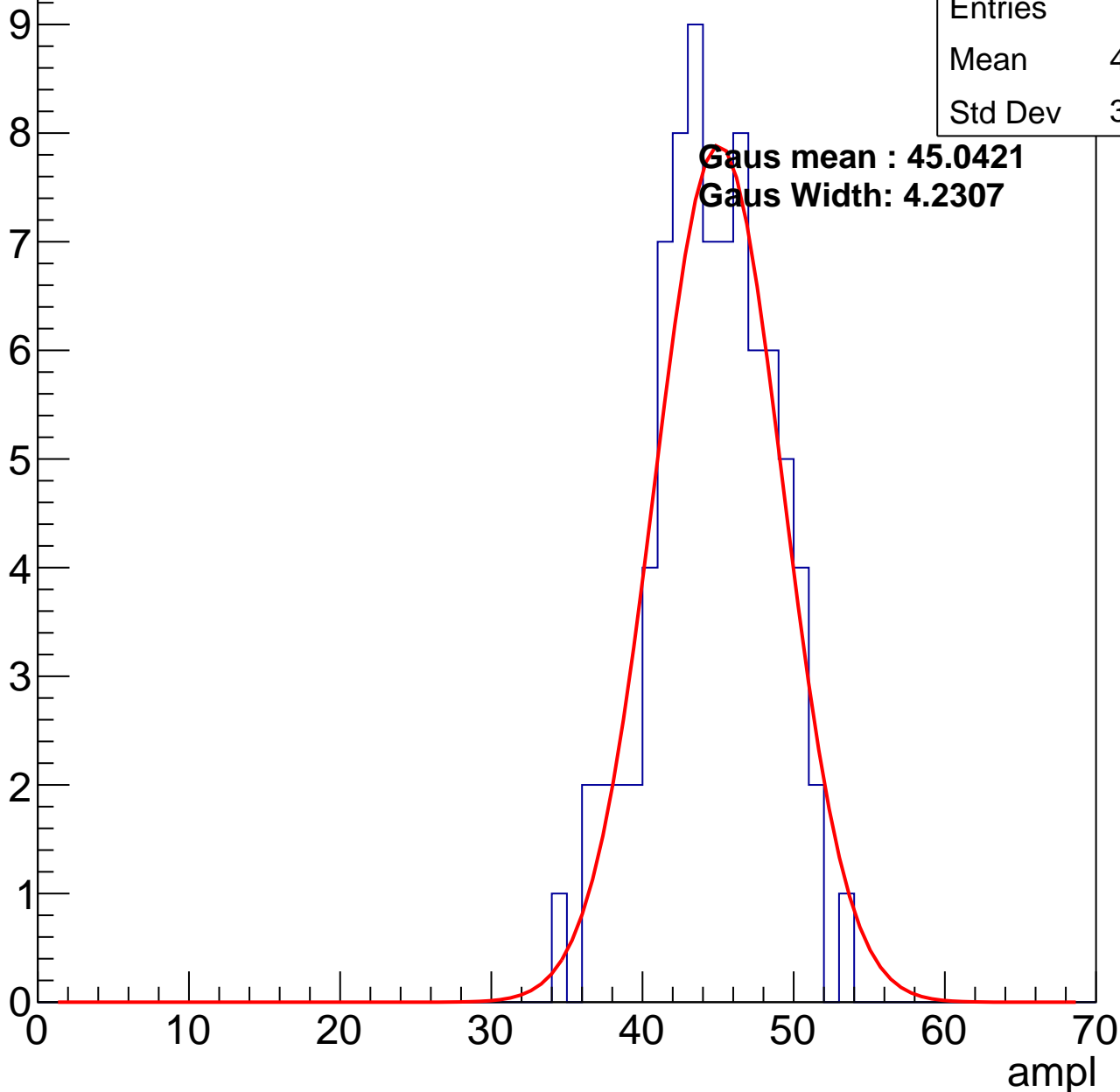
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	44.16
Std Dev	3.882

Gaus mean : 45.0421

Gaus Width: 4.2307

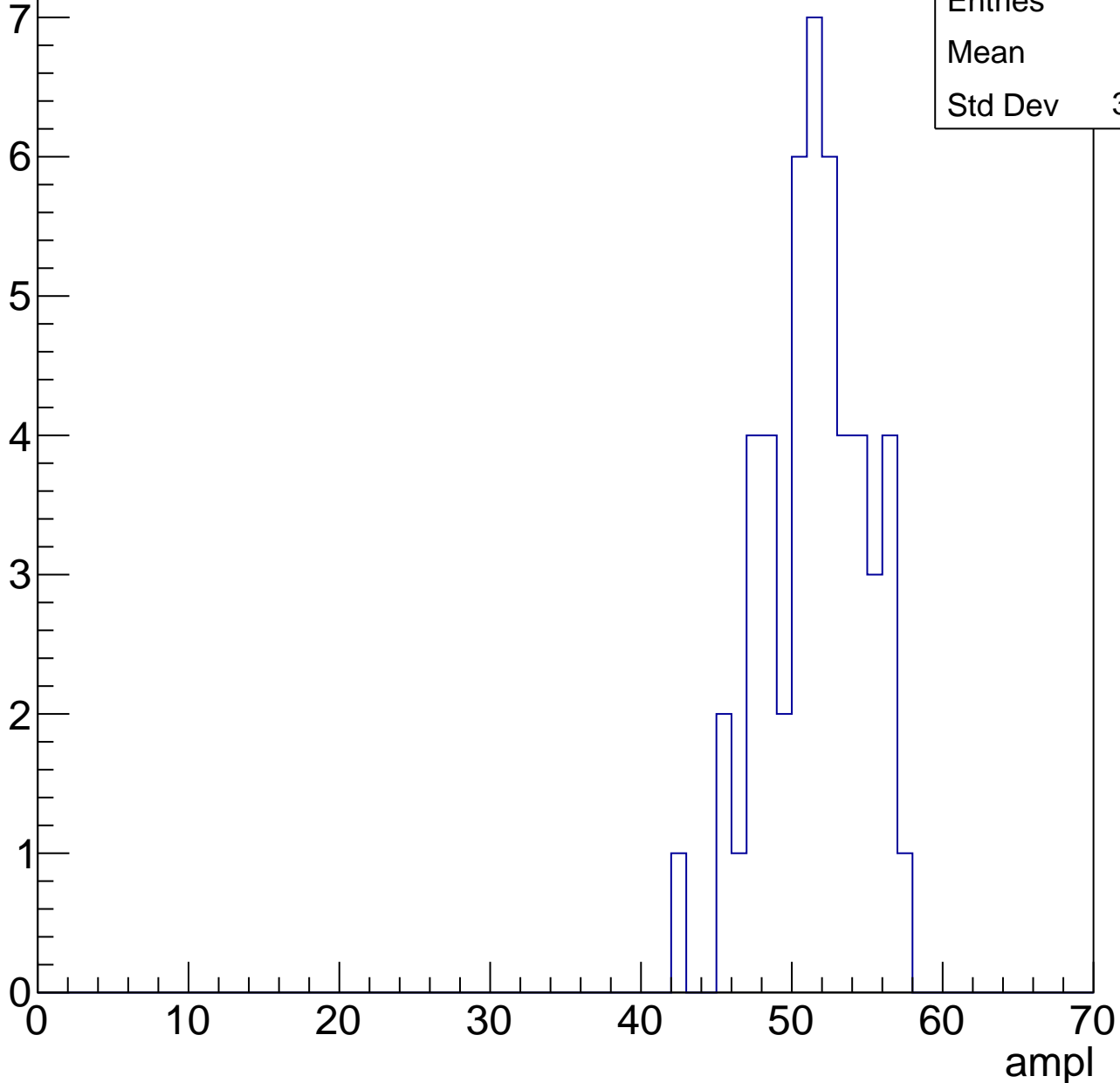


B1L101S, U25-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

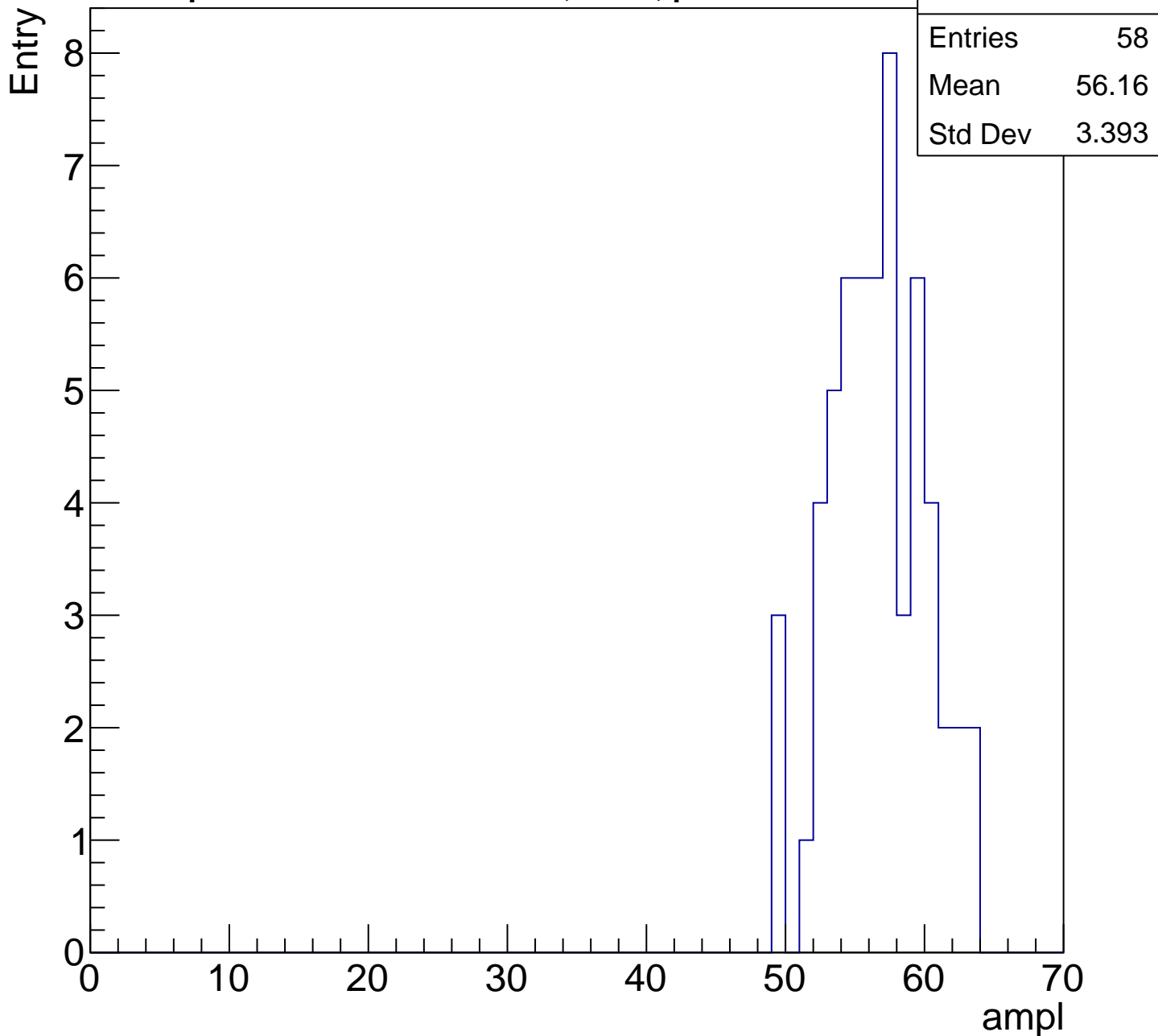
Entry

Entries	49
Mean	51
Std Dev	3.301



B1L101S, U25-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.21
Std Dev	1.894

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

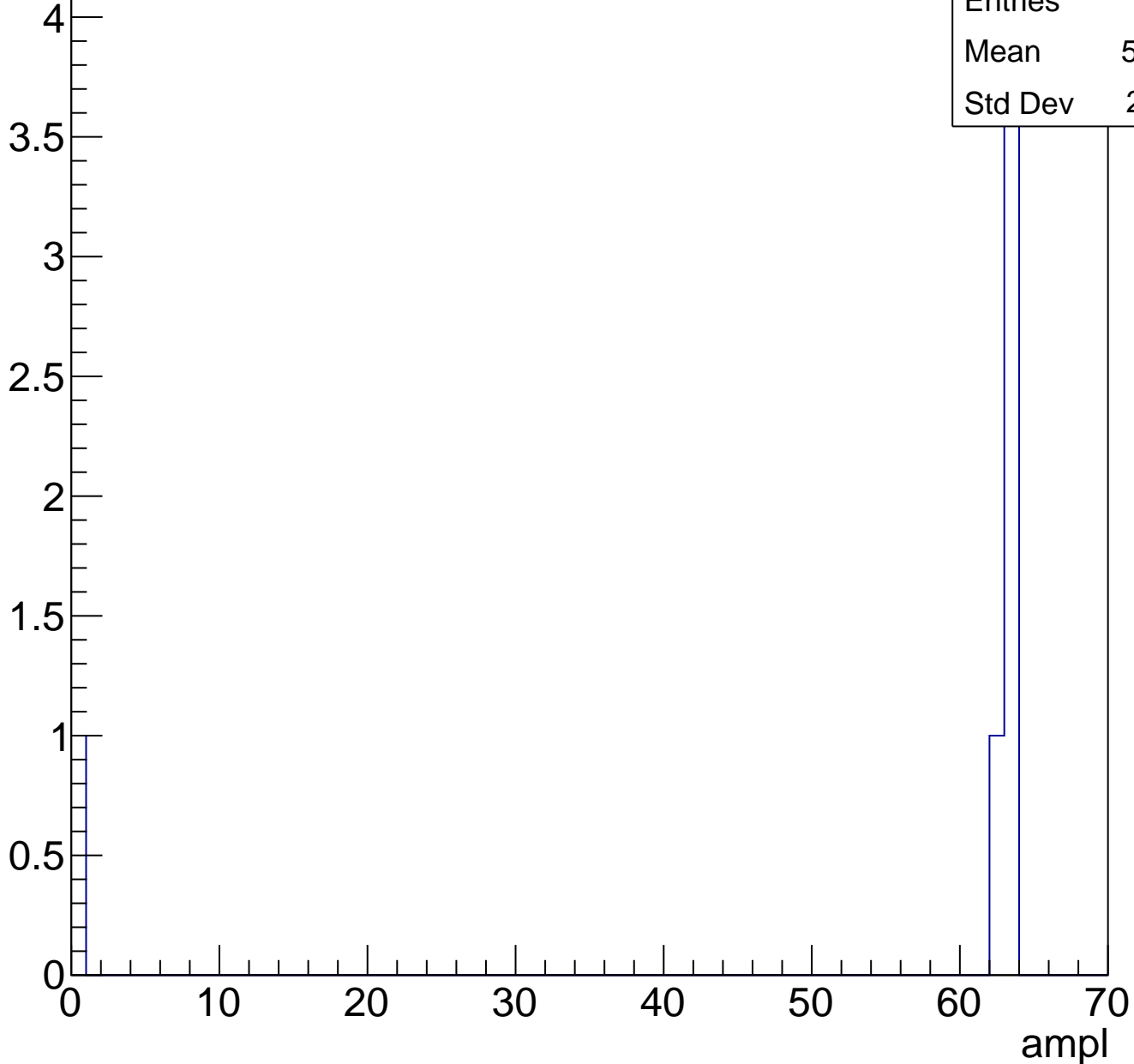
7

8

B1L101S, U25-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	6
Mean	52.33
Std Dev	23.41

B1L101S, U25-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch73, adc0

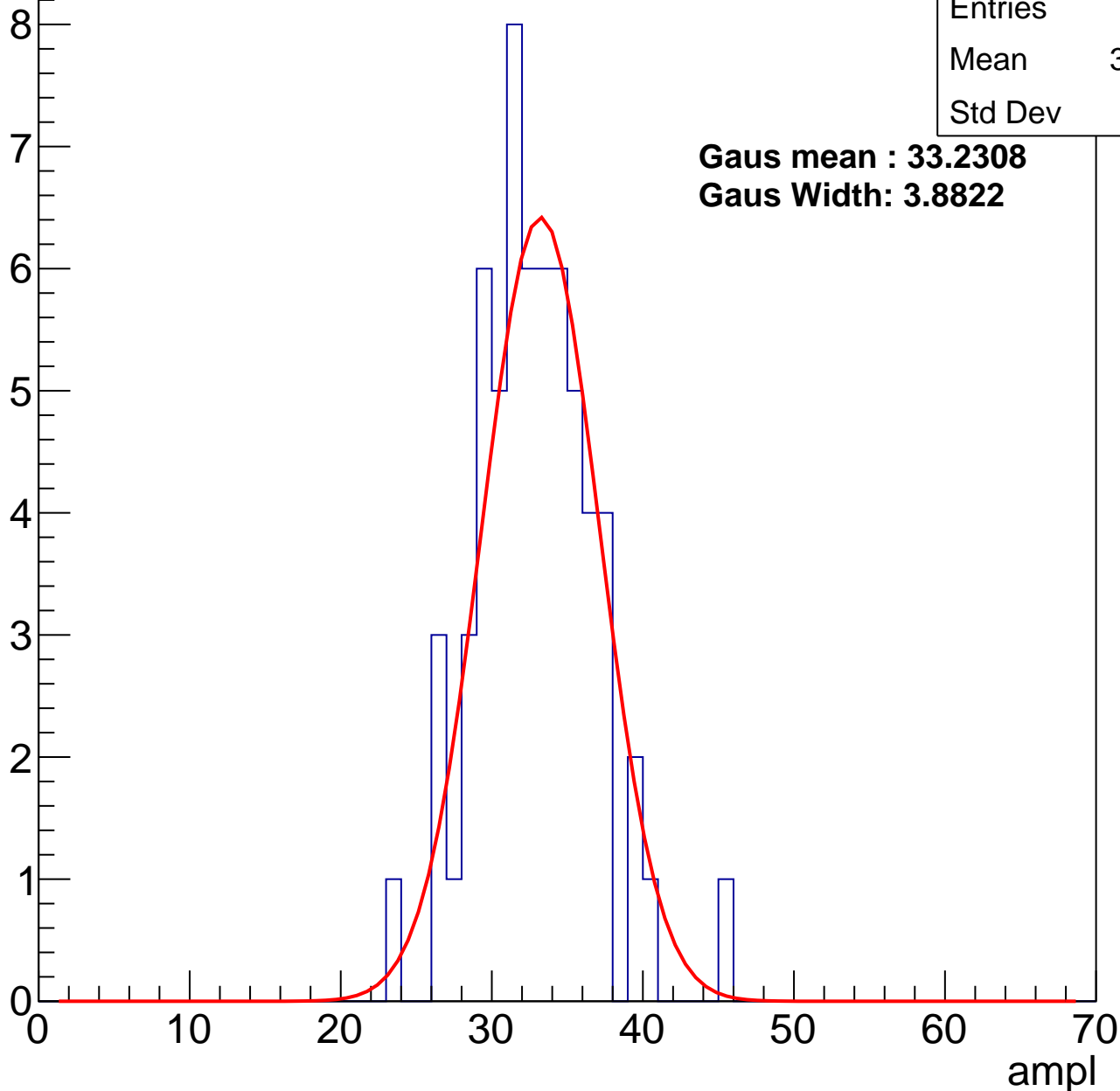
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	32.39
Std Dev	3.82

Gaus mean : 33.2308

Gaus Width: 3.8822



B1L101S, U25-ch73, adc1

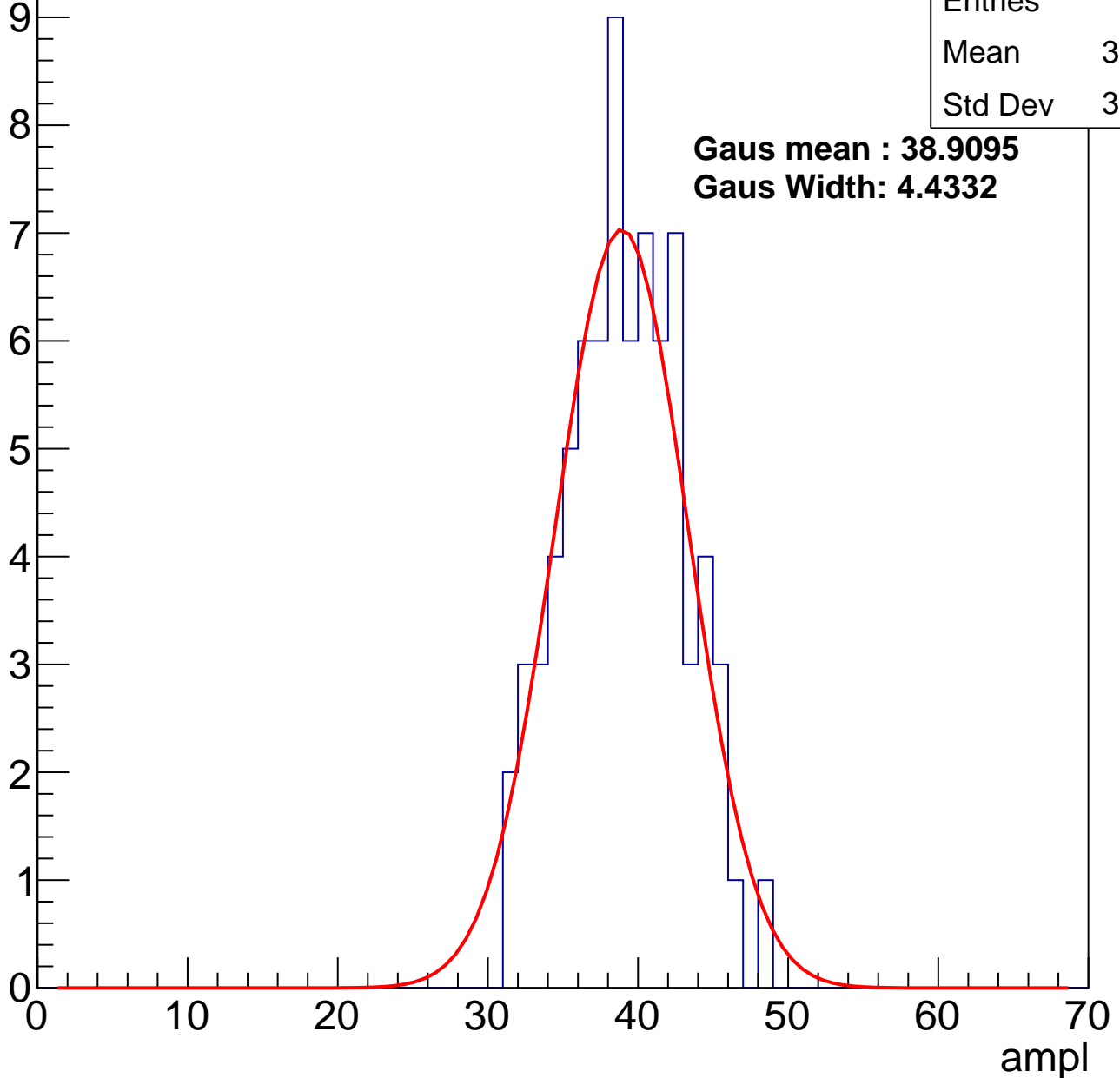
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	38.63
Std Dev	3.838

Gaus mean : 38.9095

Gaus Width: 4.4332



B1L101S, U25-ch73, adc2

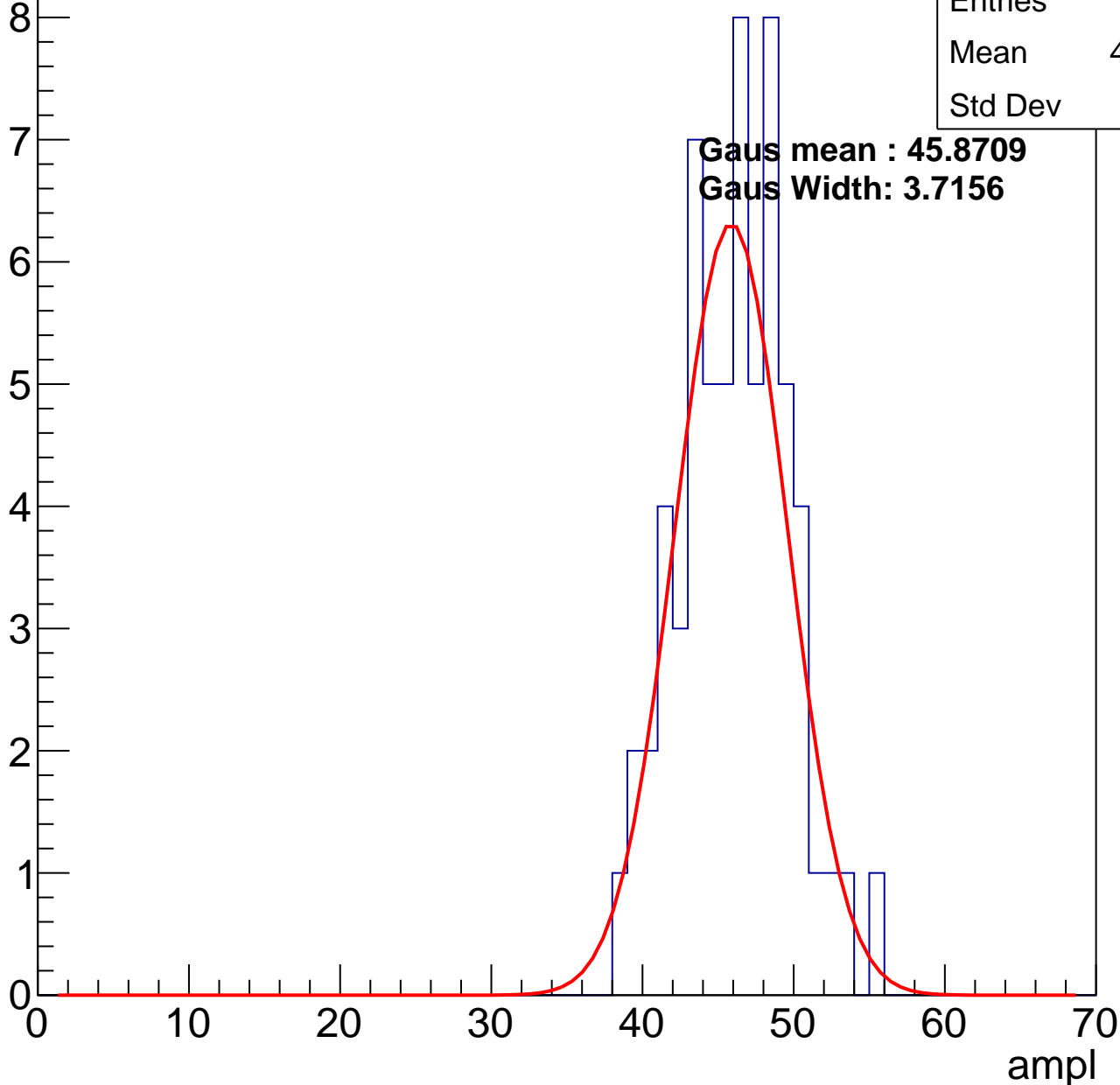
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	45.63
Std Dev	3.56

Gaus mean : 45.8709

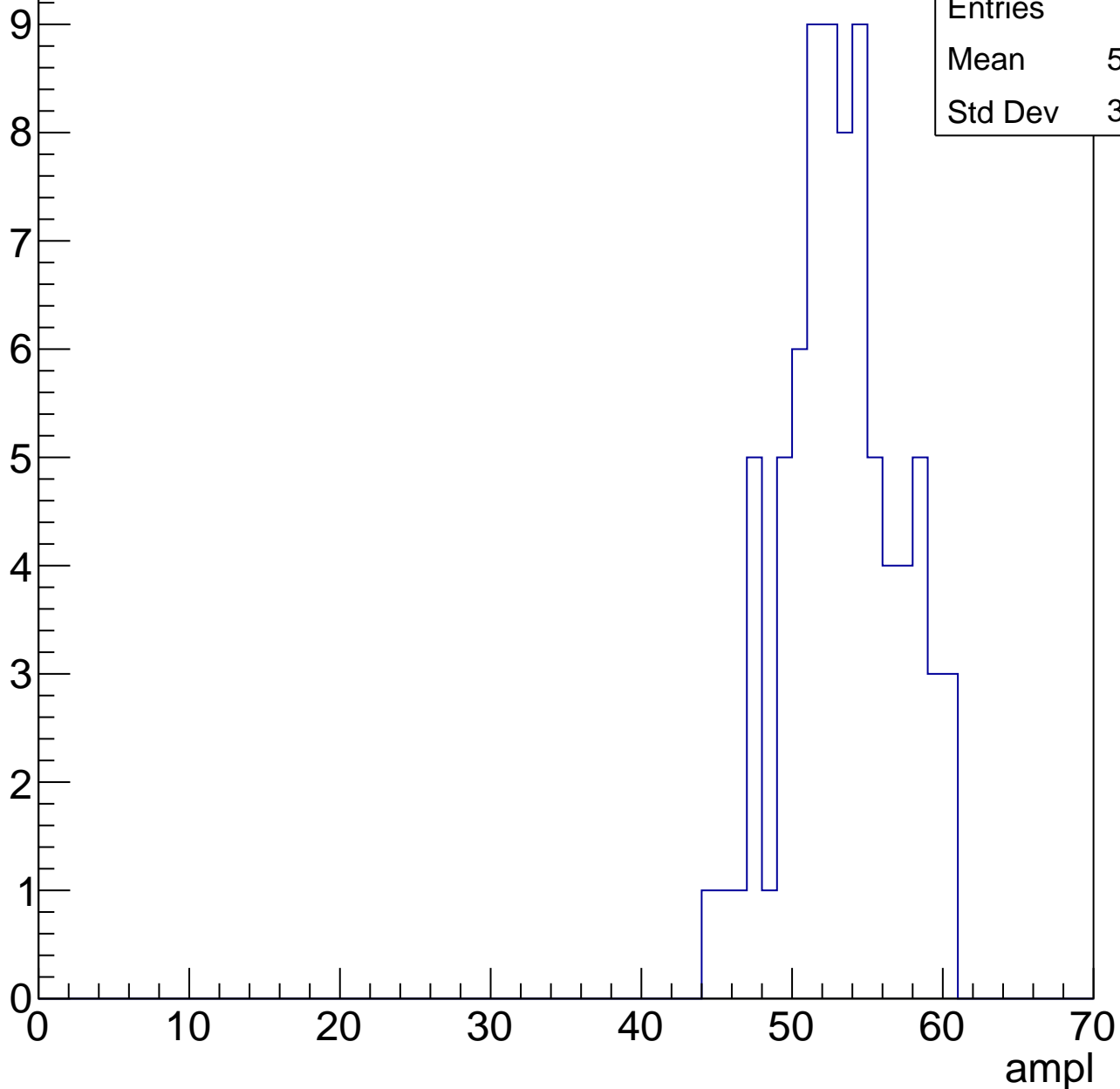
Gaus Width: 3.7156



B1L101S, U25-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



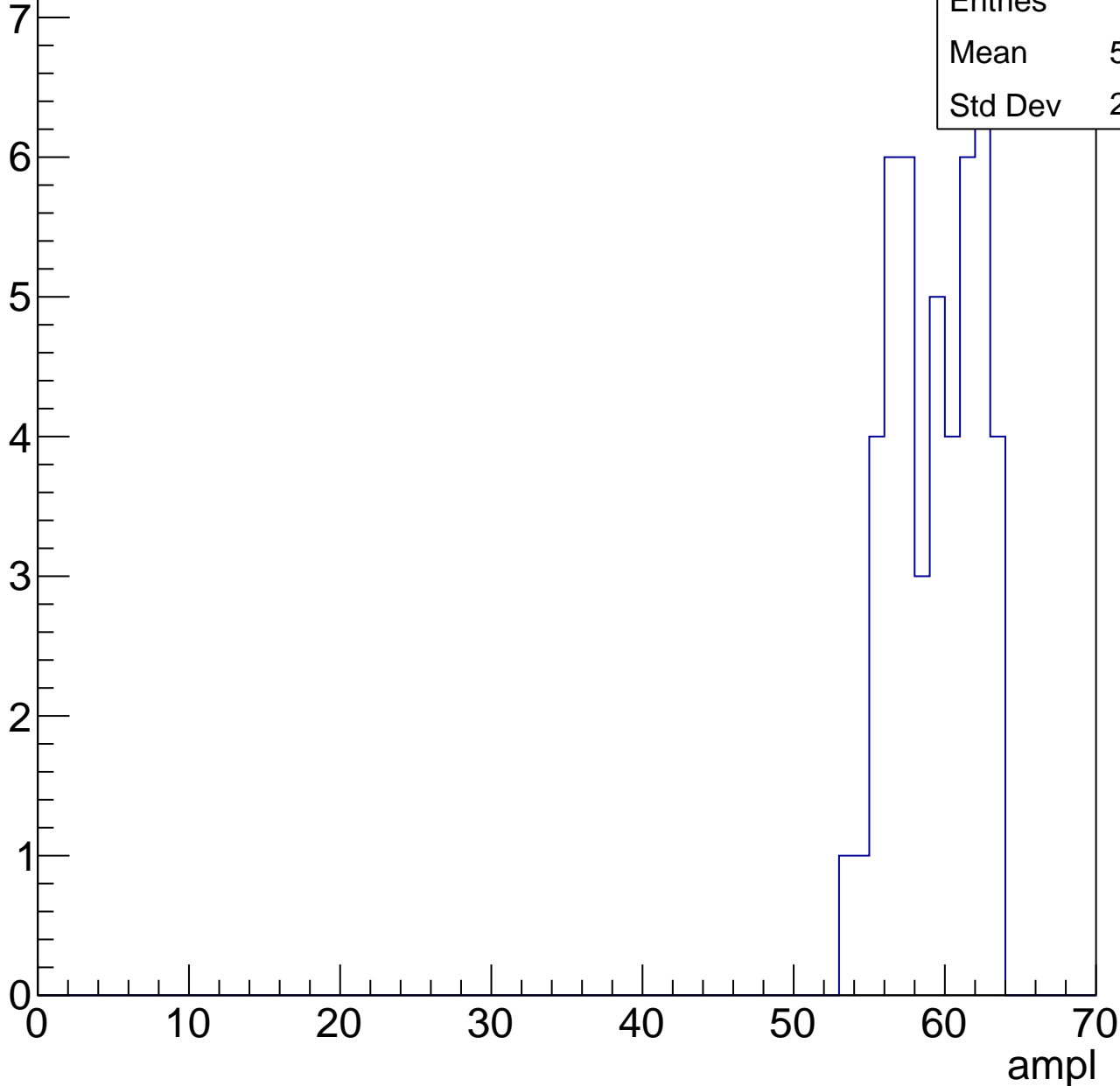
Entries	79
Mean	52.84
Std Dev	3.706

B1L101S, U25-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.85
Std Dev	2.767

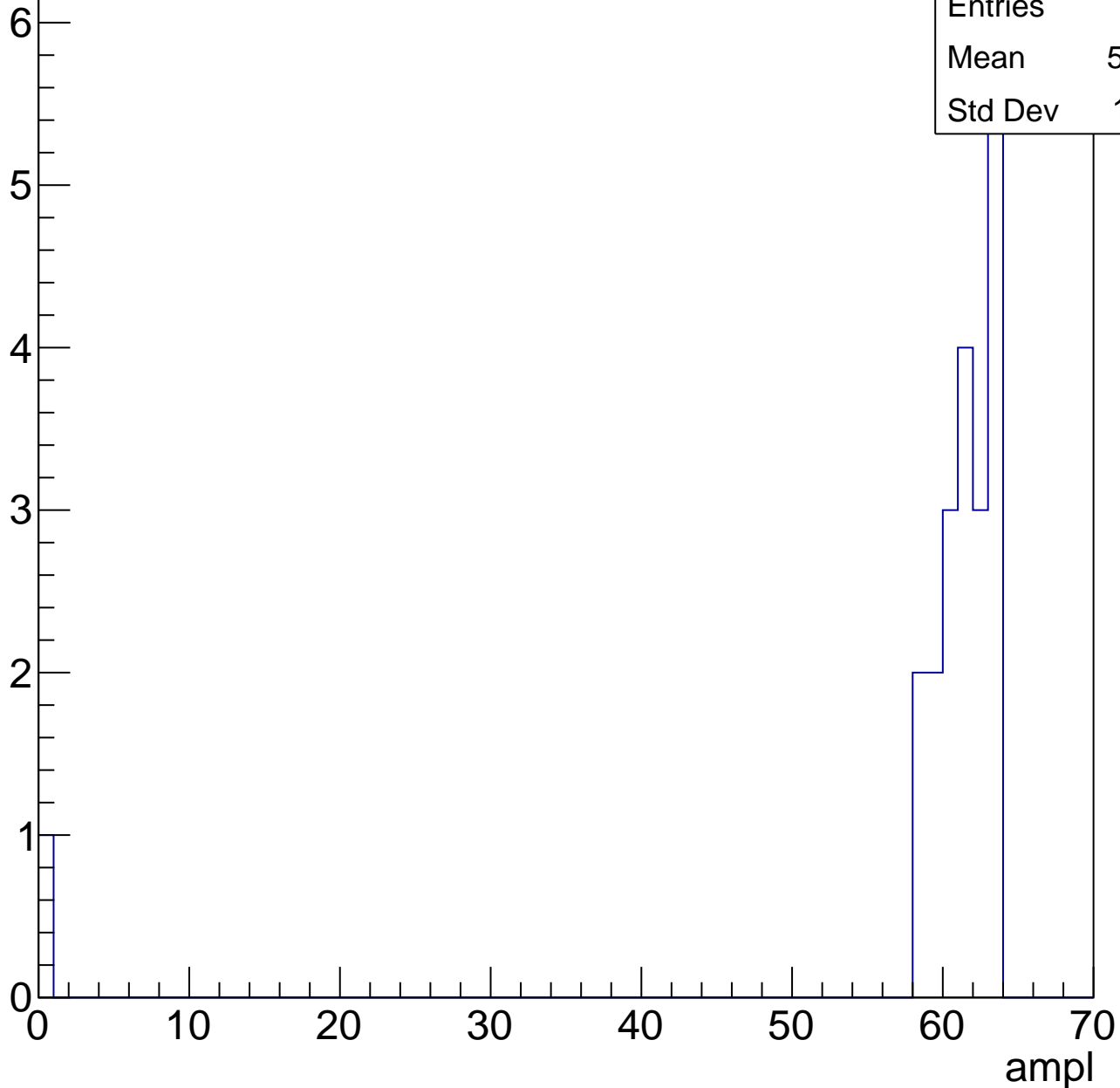


B1L101S, U25-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.19
Std Dev	13.11



B1L101S, U25-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch74, adc0

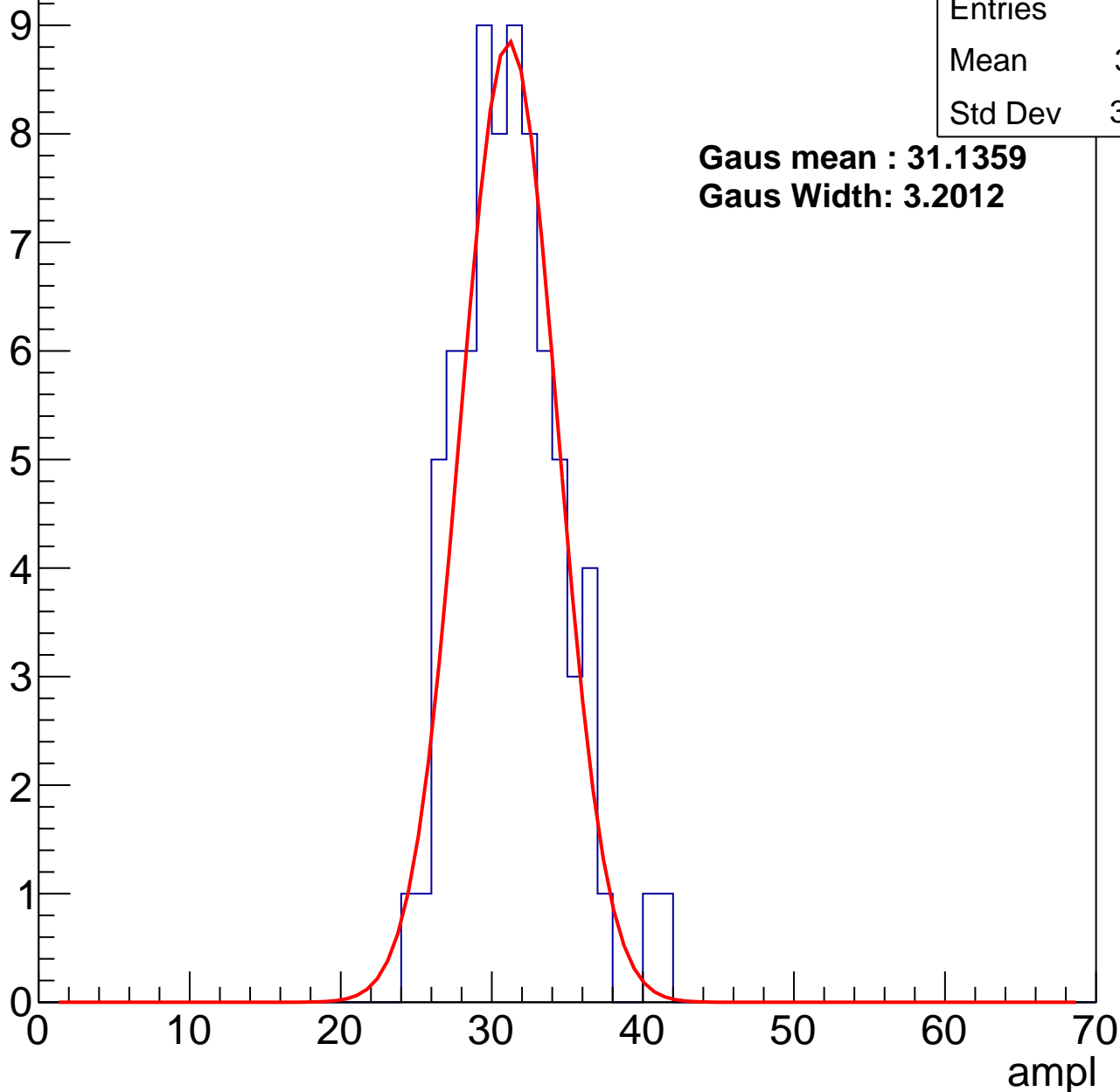
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.81
Std Dev	3.376

Gaus mean : 31.1359

Gaus Width: 3.2012



B1L101S, U25-ch74, adc1

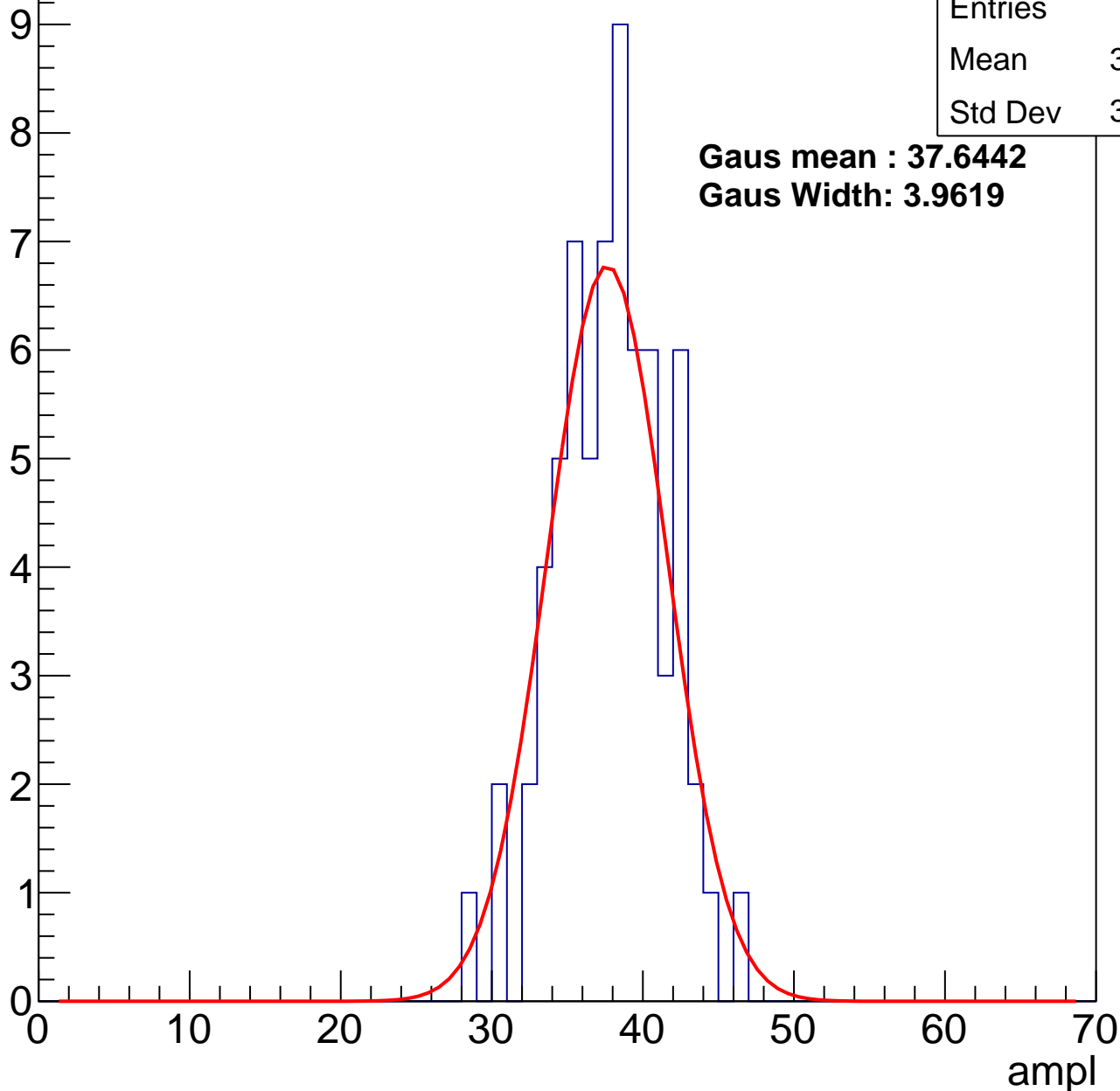
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.39
Std Dev	3.557

Gaus mean : 37.6442

Gaus Width: 3.9619



B1L101S, U25-ch74, adc2

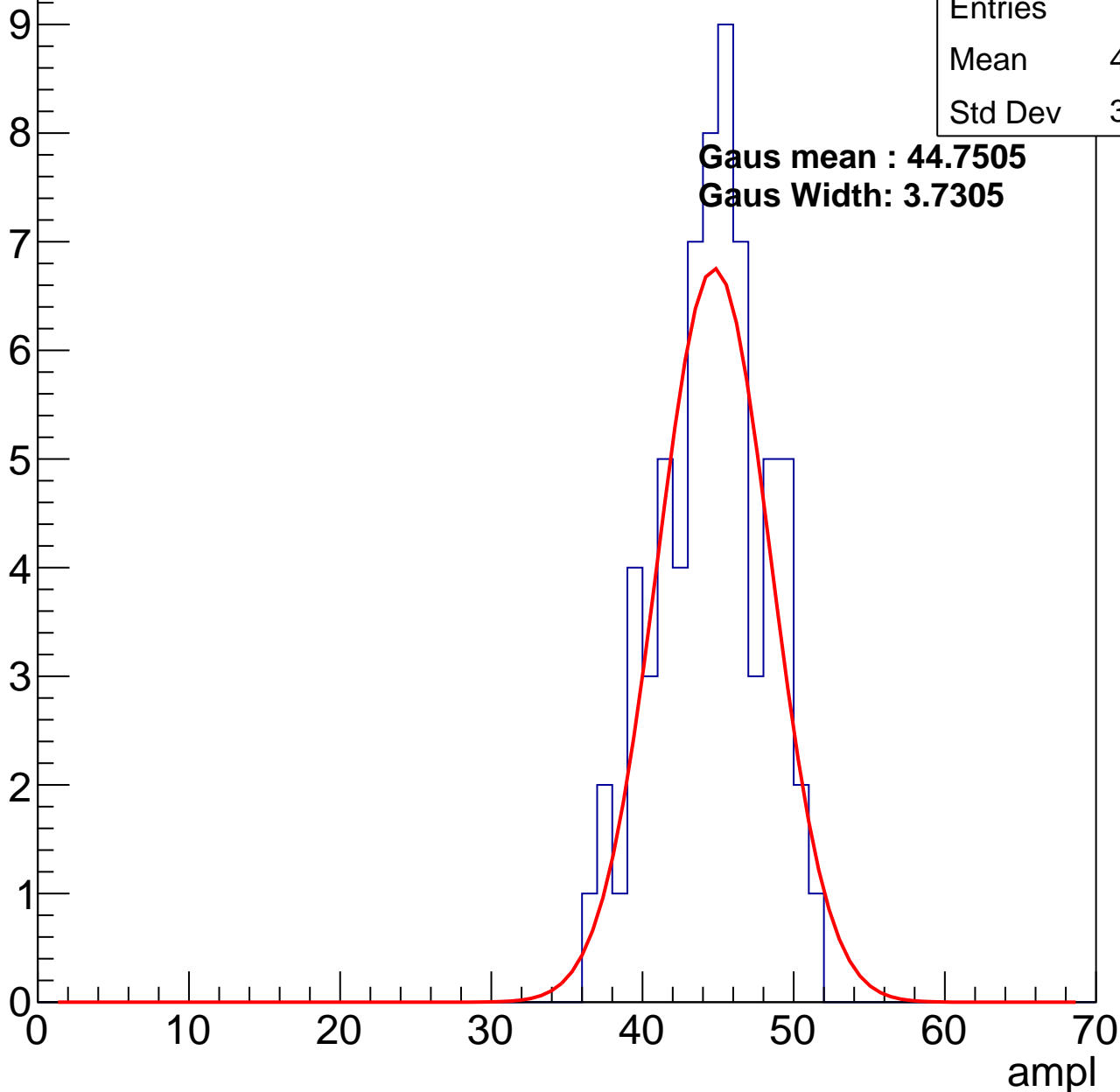
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	44.09
Std Dev	3.463

Gaus mean : 44.7505

Gaus Width: 3.7305

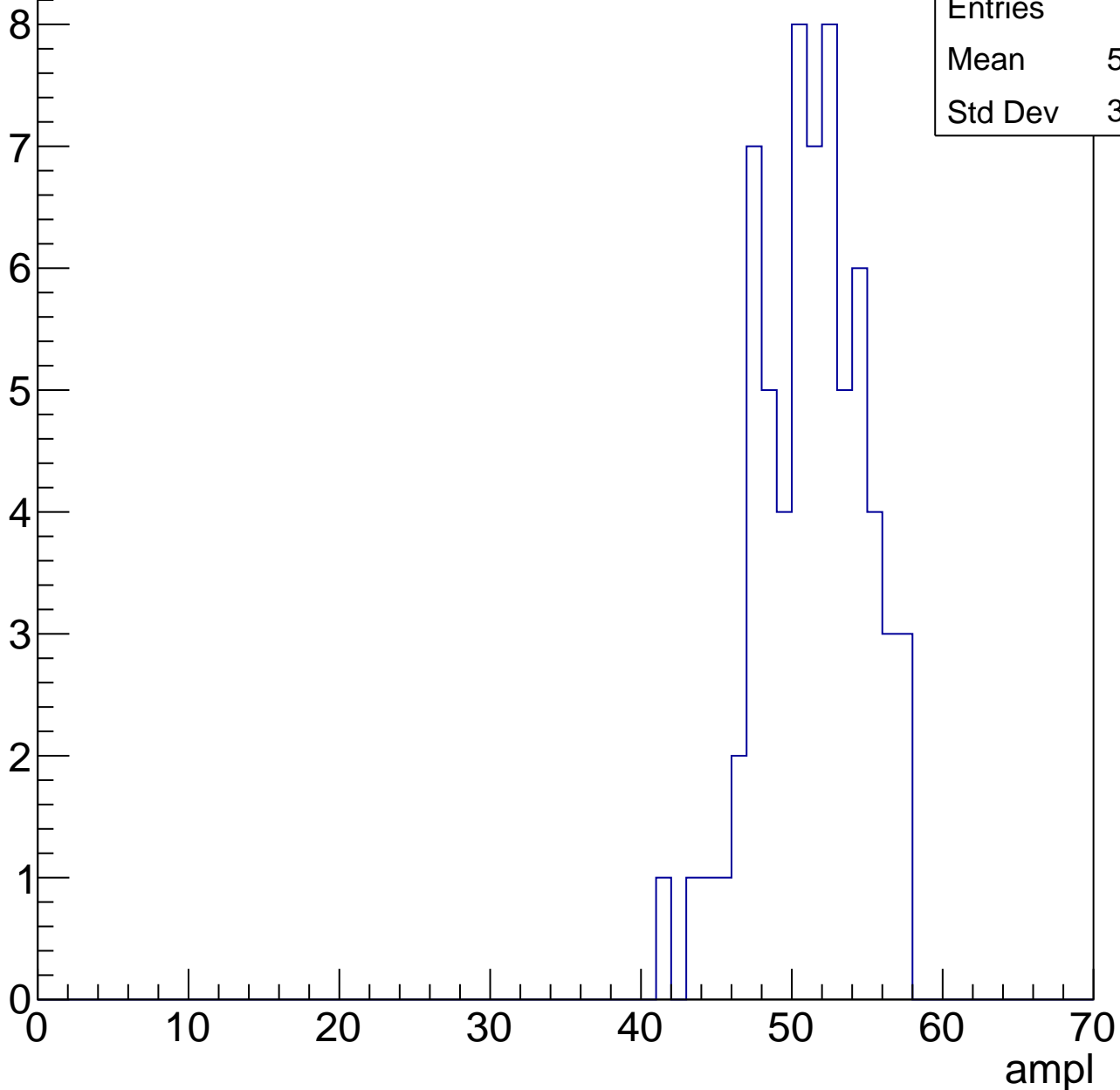


B1L101S, U25-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.77
Std Dev	3.494

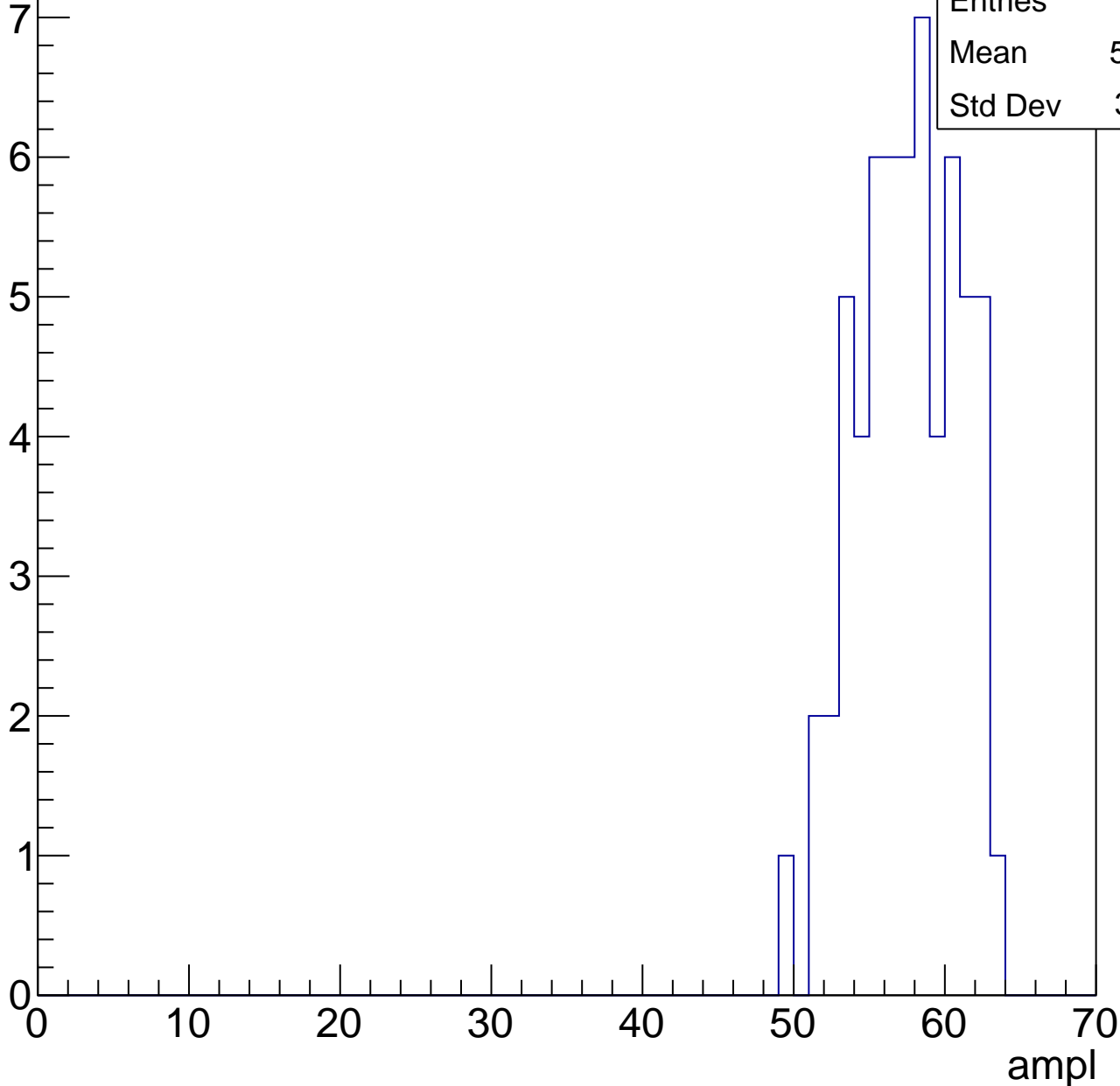


B1L101S, U25-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	57.07
Std Dev	3.291

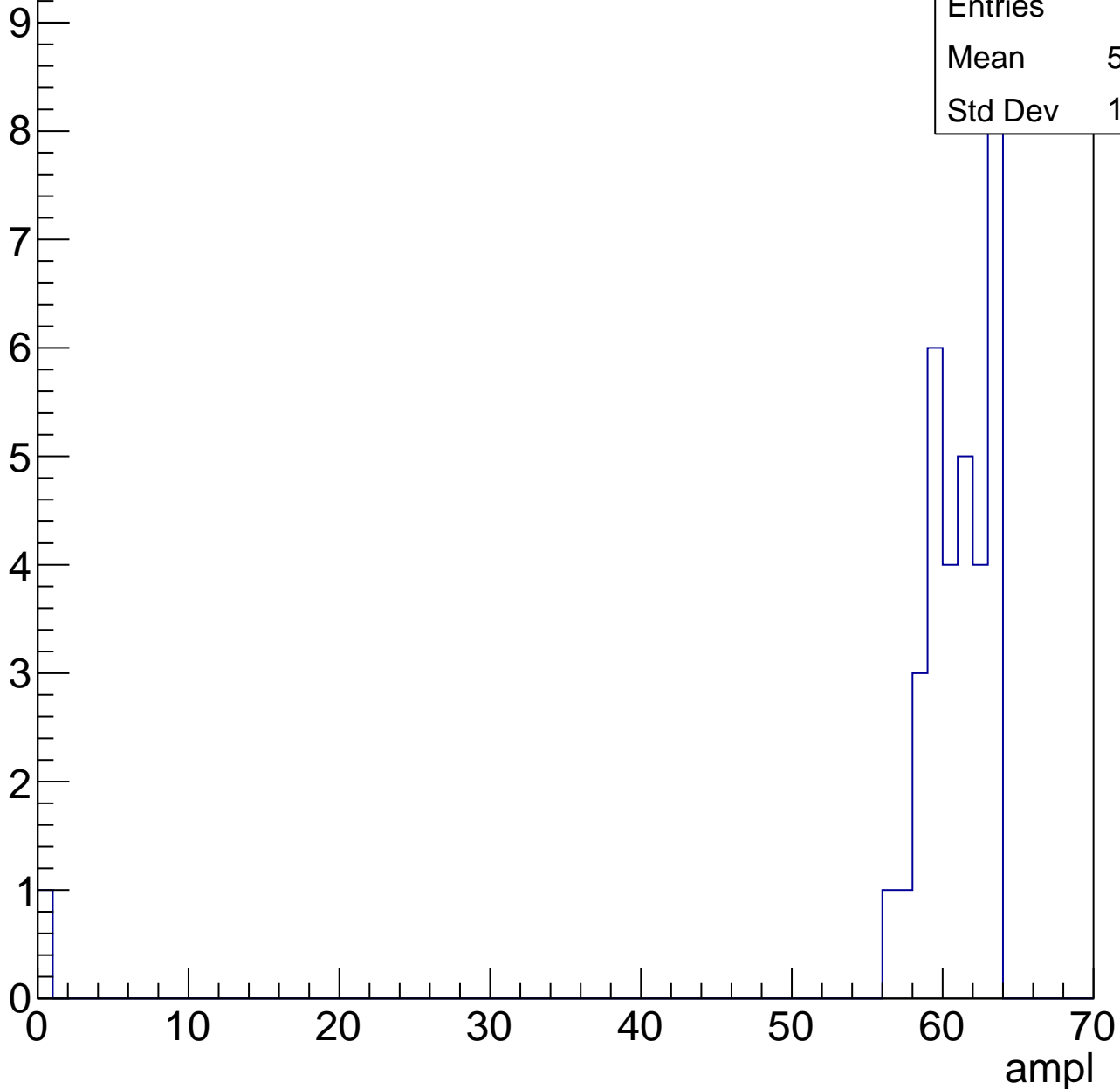


B1L101S, U25-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.85
Std Dev	10.43



B1L101S, U25-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch75, adc0

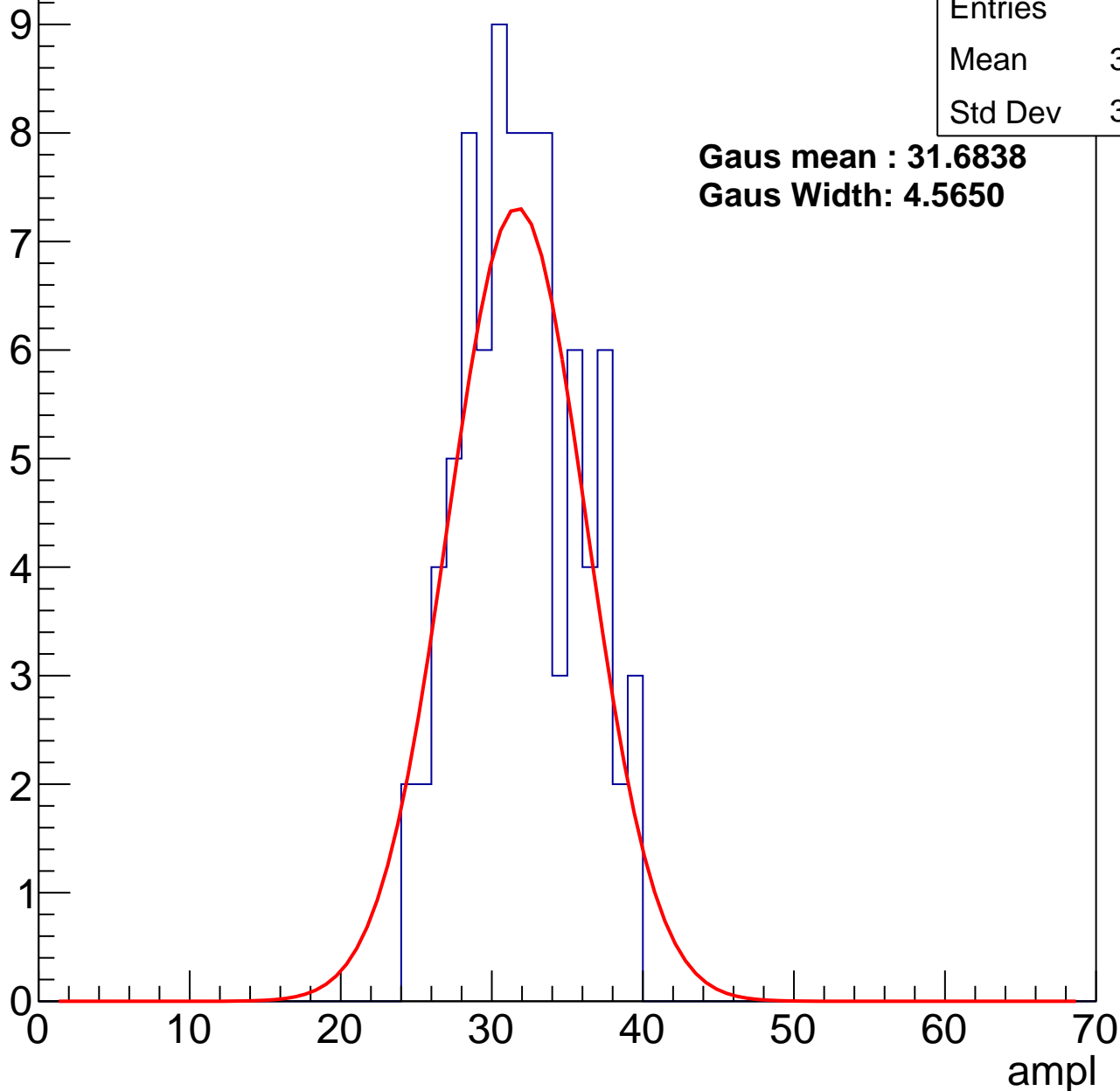
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	31.48
Std Dev	3.787

Gaus mean : 31.6838

Gaus Width: 4.5650



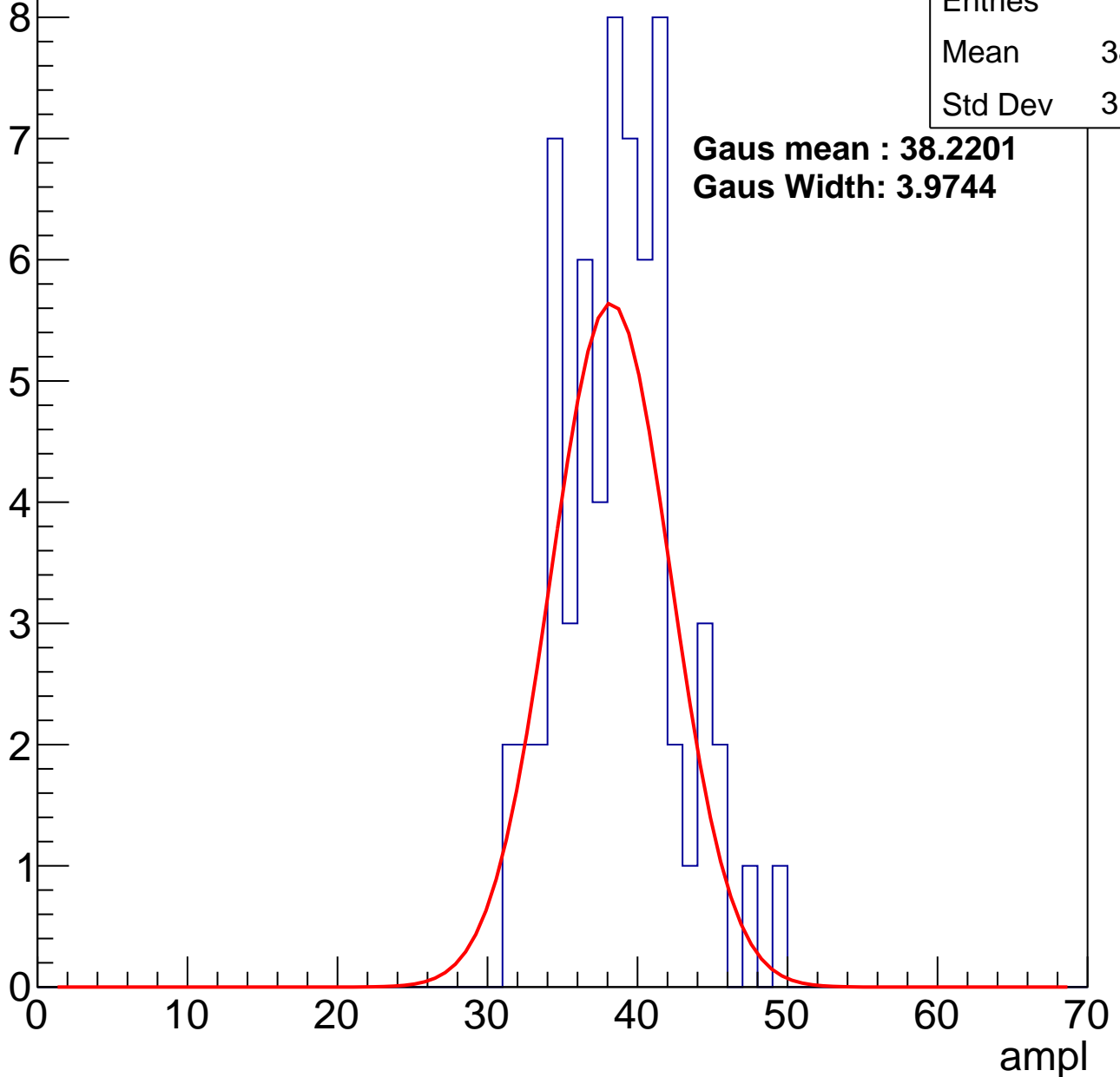
B1L101S, U25-ch75, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	38.29
Std Dev	3.822

Gaus mean : 38.2201
Gaus Width: 3.9744



B1L101S, U25-ch75, adc2

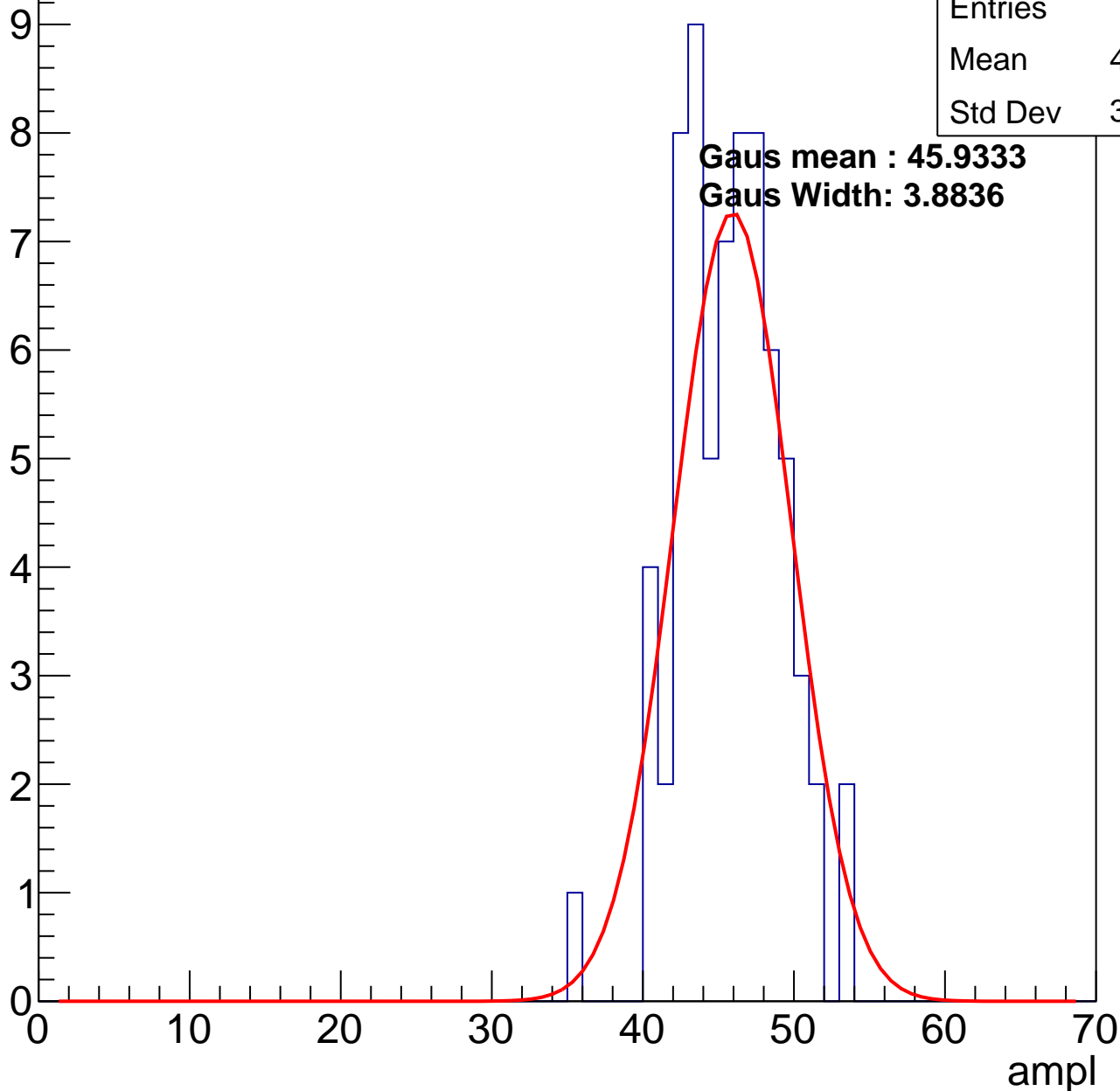
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	45.29
Std Dev	3.356

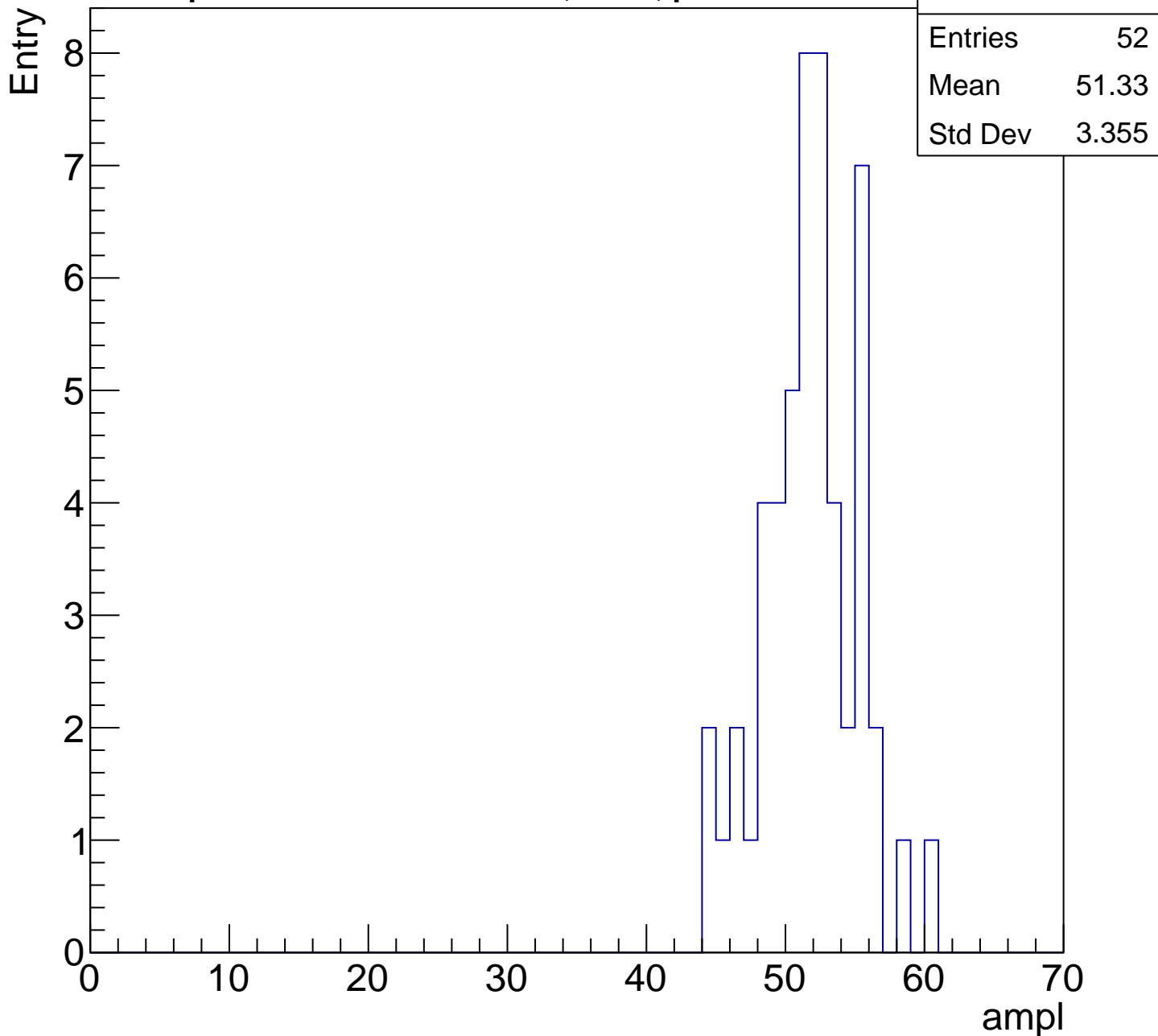
Gaus mean : 45.9333

Gaus Width: 3.8836



B1L101S, U25-ch75, adc3

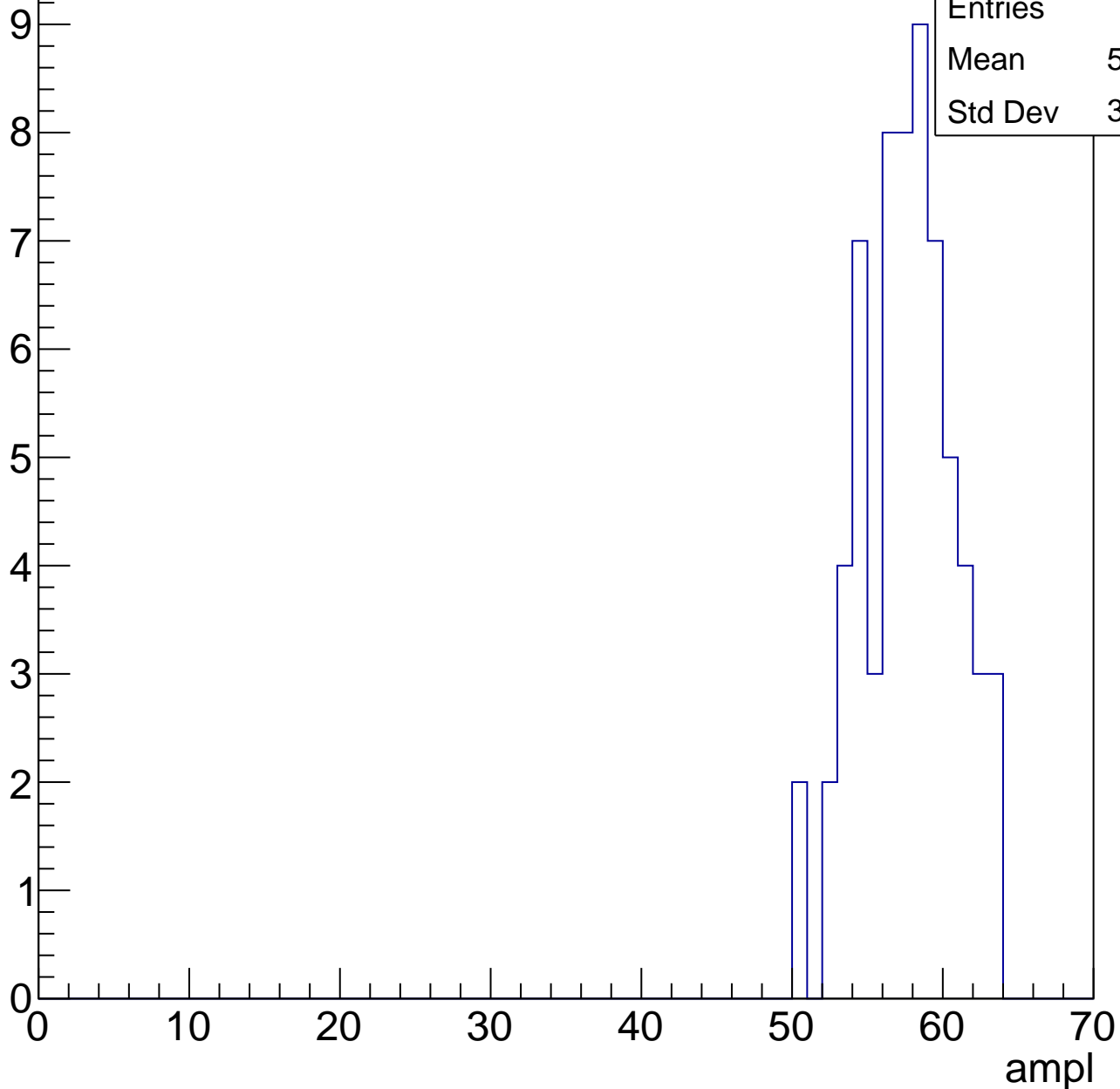
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

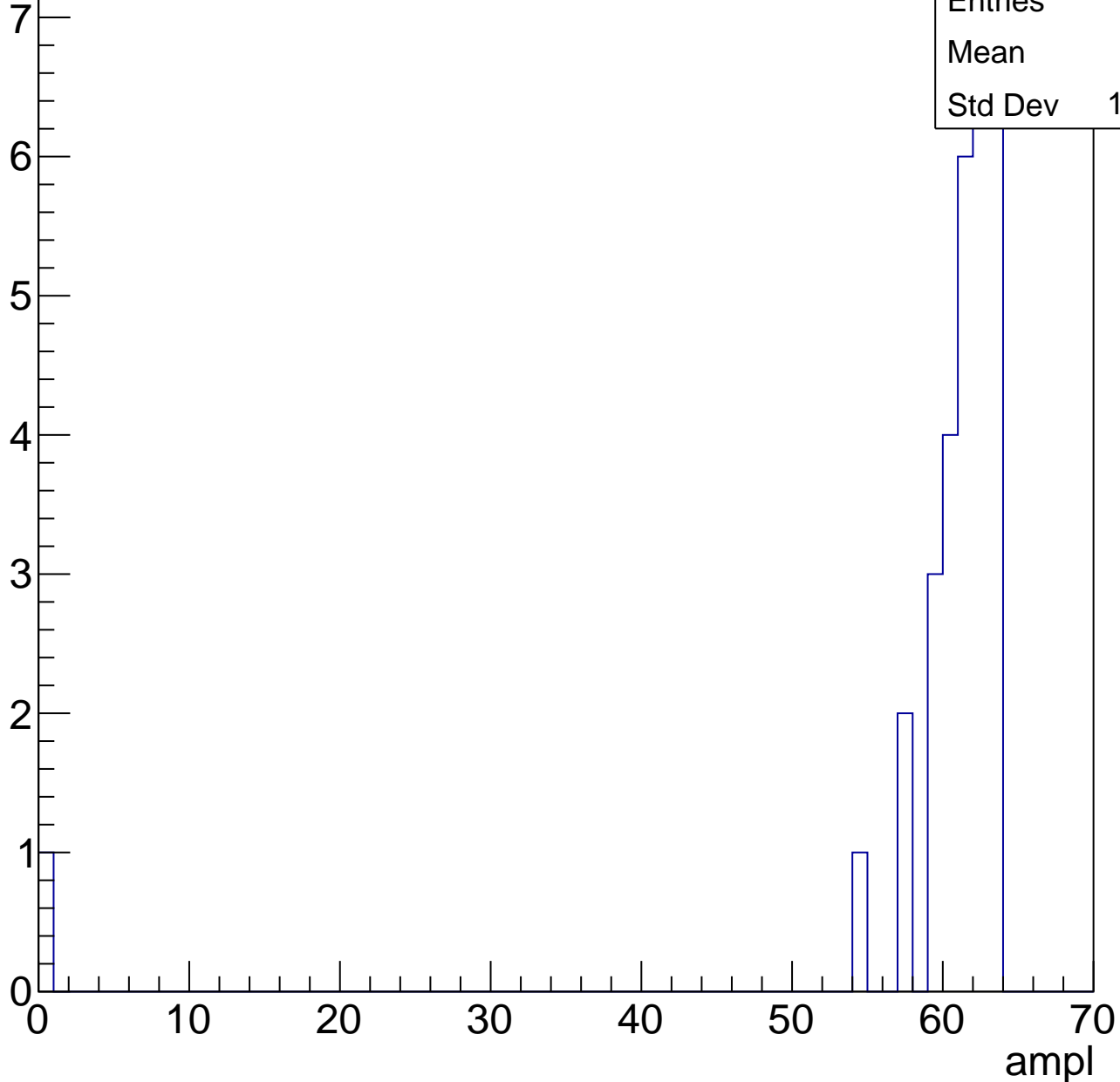
Entry



B1L101S, U25-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L101S, U25-ch76, adc0

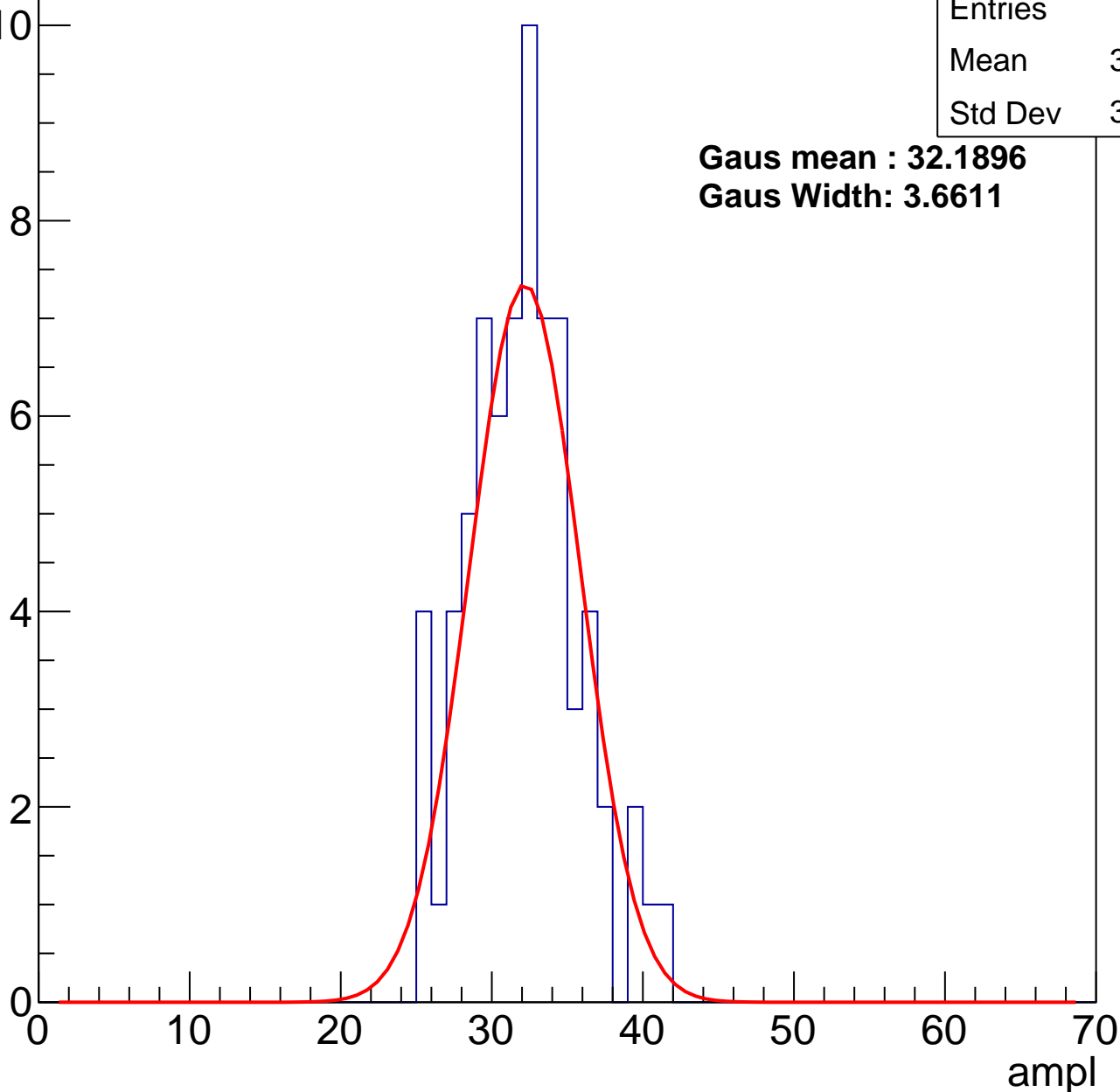
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	31.62
Std Dev	3.589

Gaus mean : 32.1896

Gaus Width: 3.6611



B1L101S, U25-ch76, adc1

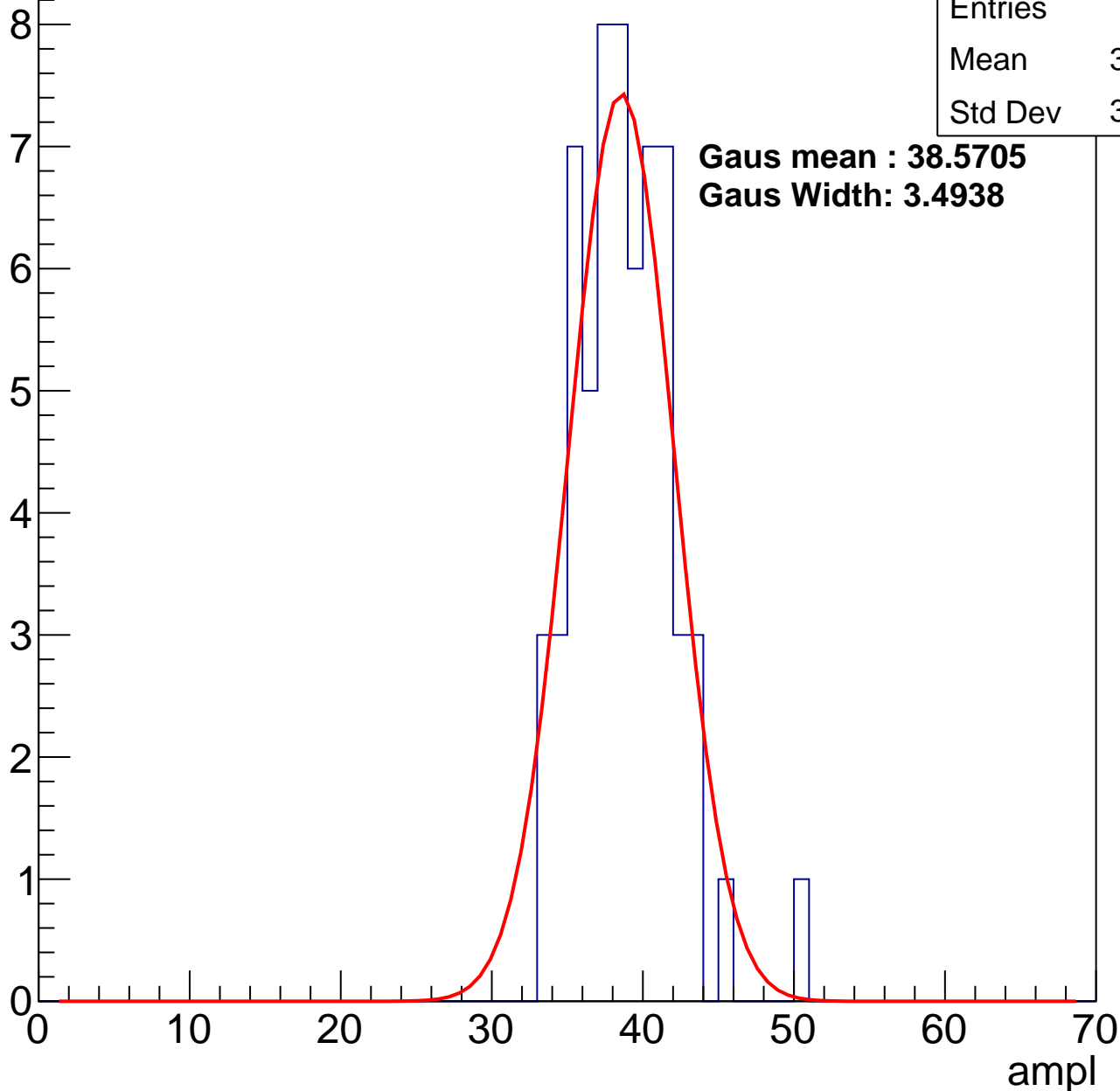
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	38.34
Std Dev	3.162

Gaus mean : 38.5705

Gaus Width: 3.4938



B1L101S, U25-ch76, adc2

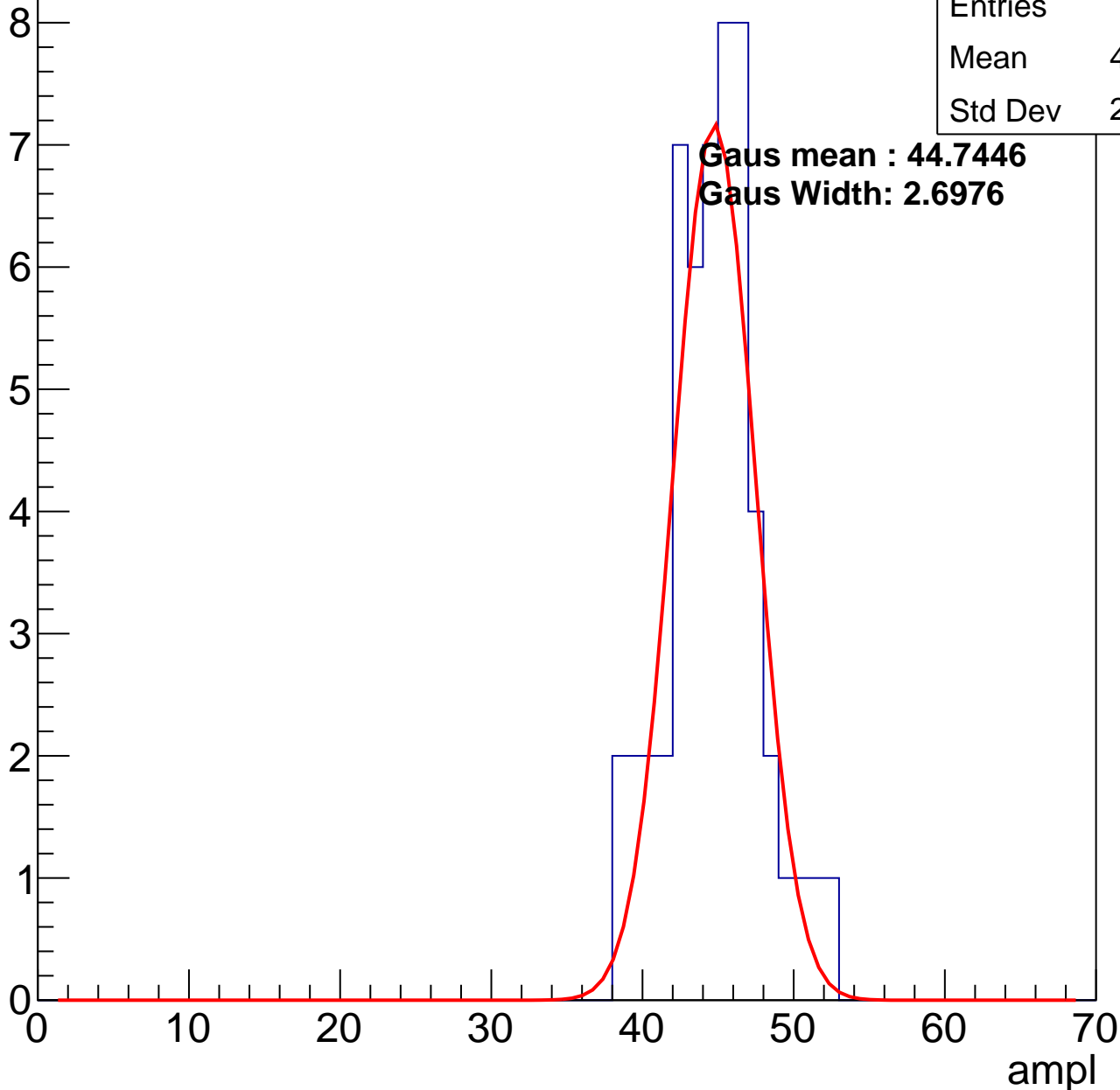
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	44.26
Std Dev	2.995

Gaus mean : 44.7446

Gaus Width: 2.6976

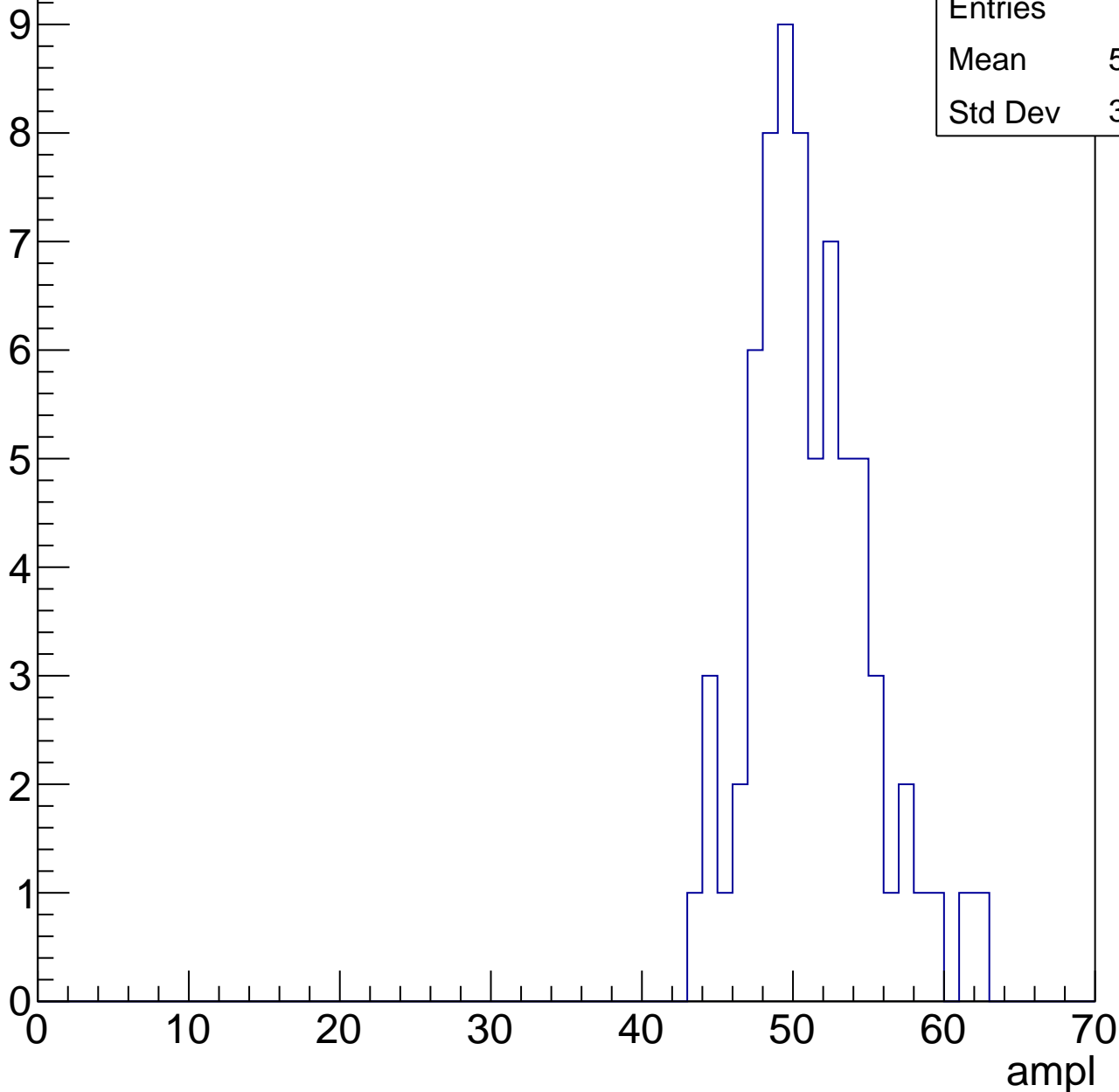


B1L101S, U25-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

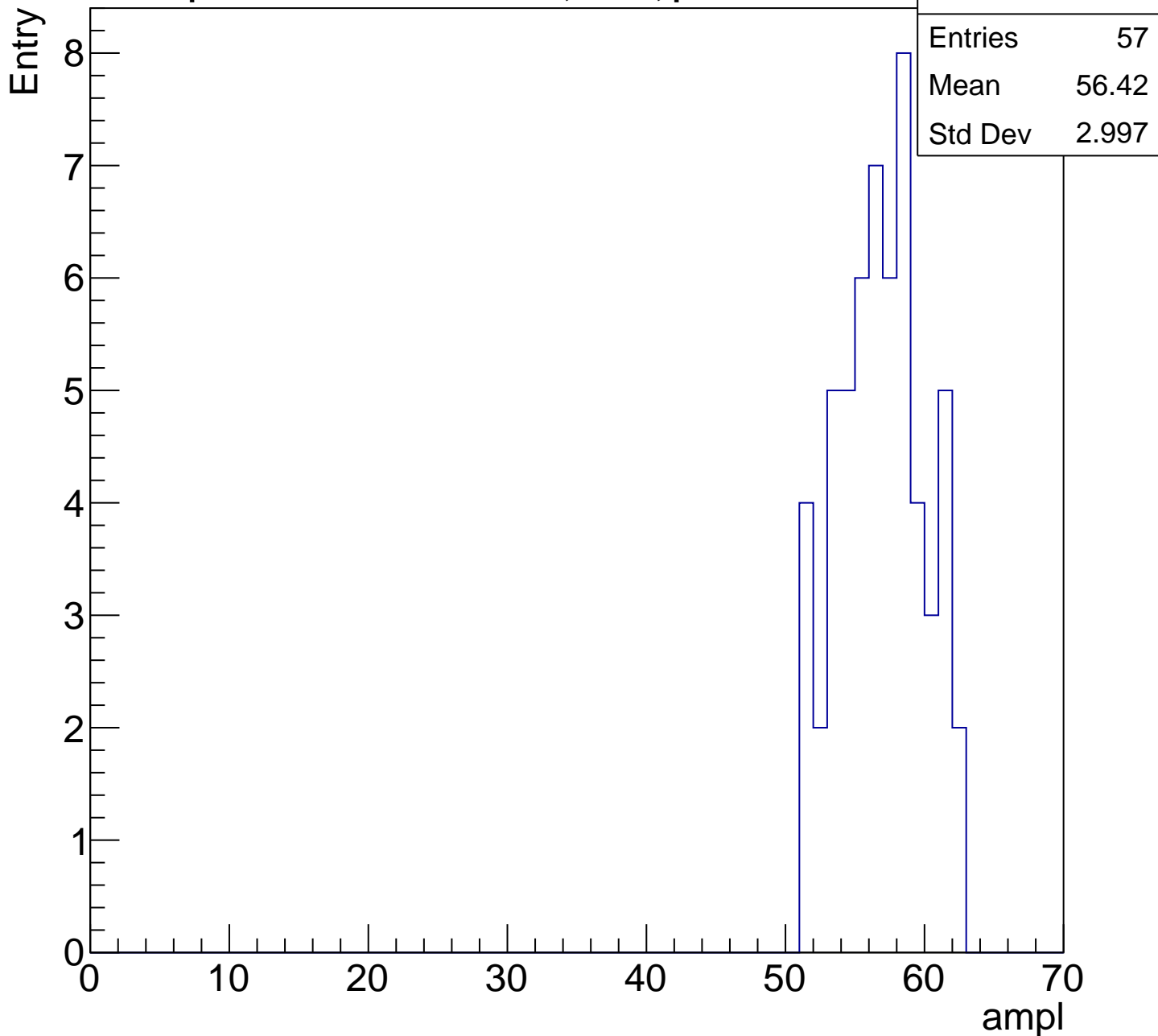
Entry

Entries	70
Mean	50.69
Std Dev	3.879



B1L101S, U25-ch76, adc4

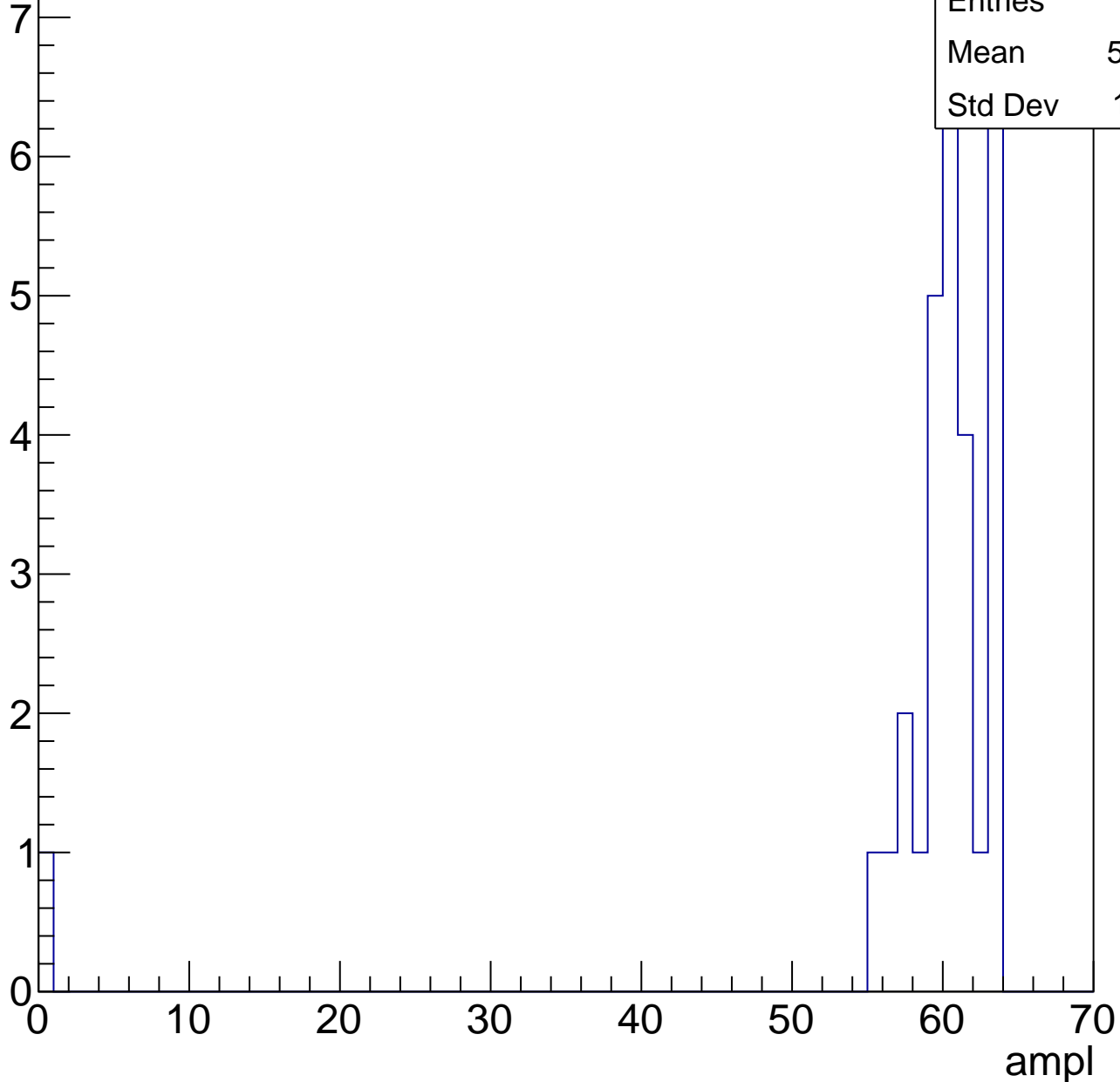
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

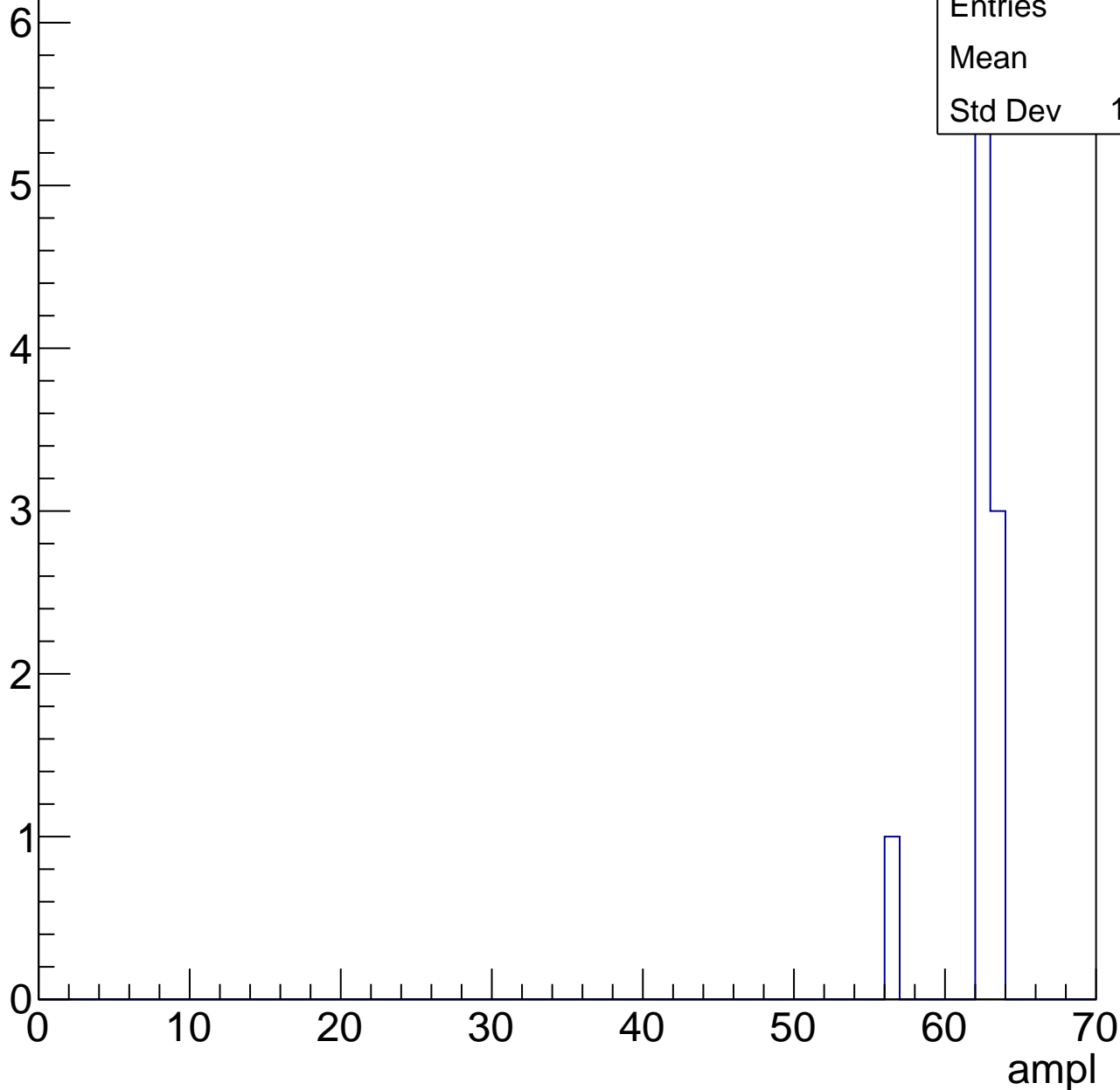


B1L101S, U25-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	61.7
Std Dev	1.952



B1L101S, U25-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch77, adc0

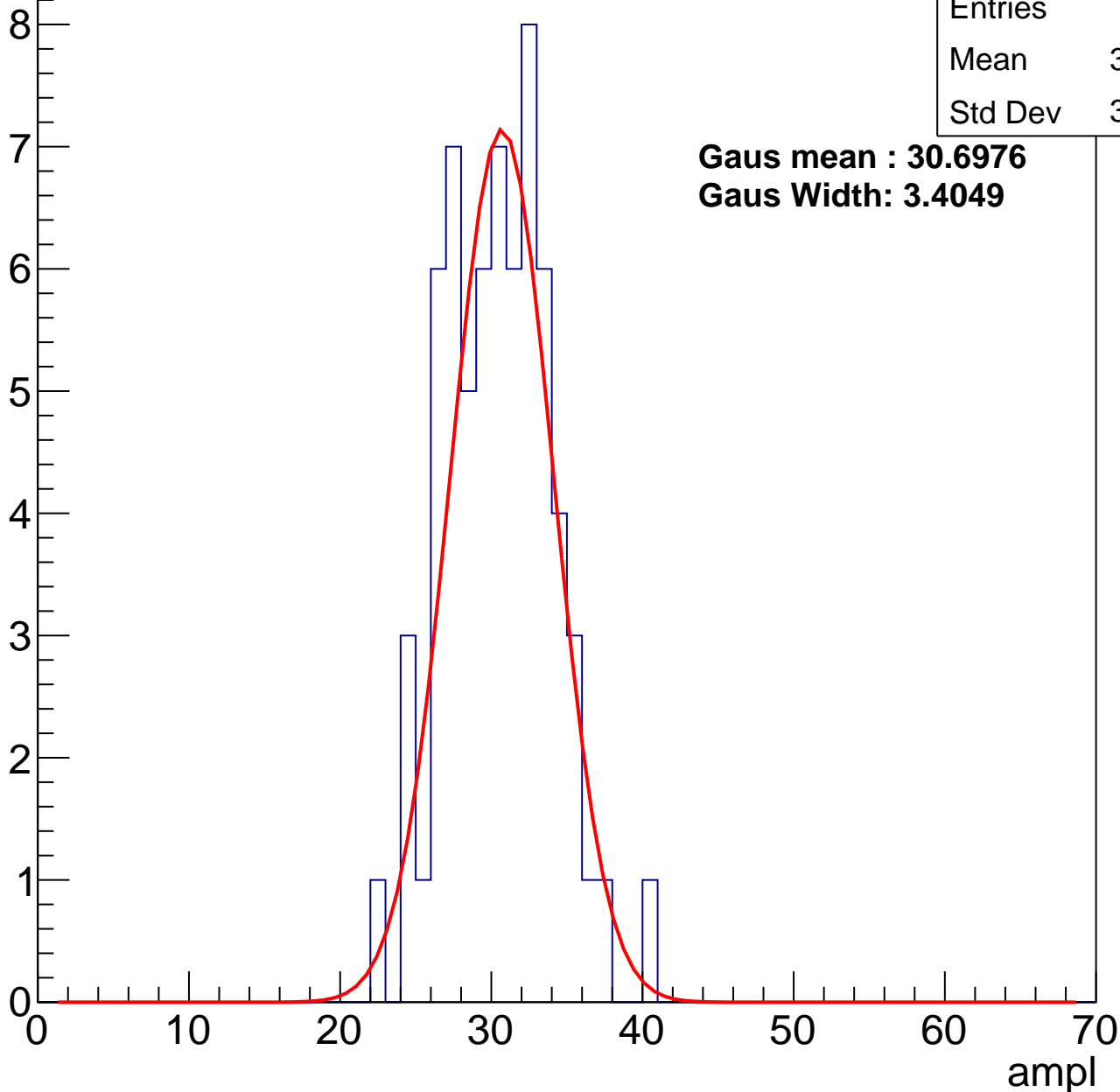
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.03
Std Dev	3.477

Gaus mean : 30.6976

Gaus Width: 3.4049



B1L101S, U25-ch77, adc1

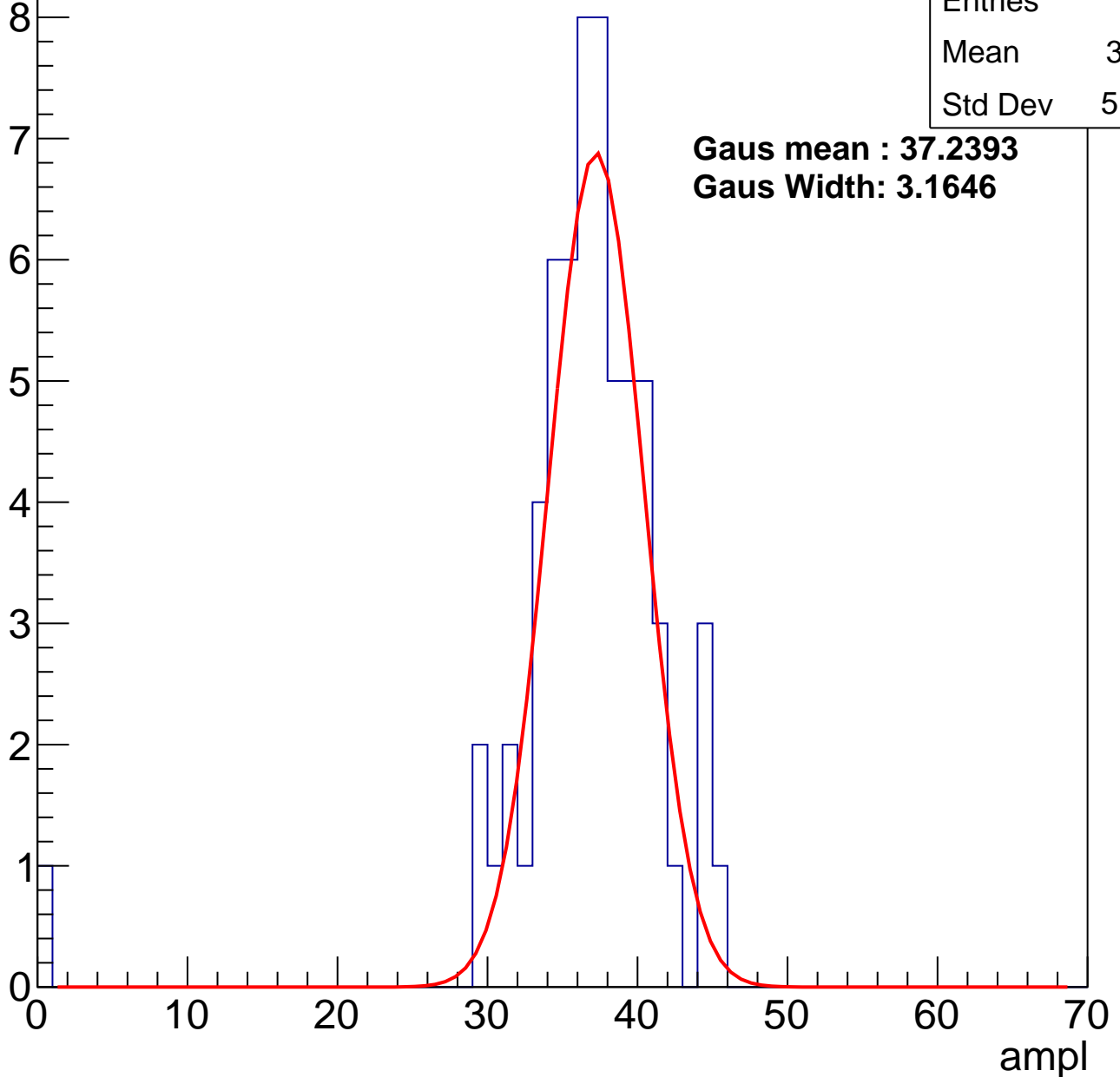
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.11
Std Dev	5.817

Gaus mean : 37.2393

Gaus Width: 3.1646



B1L101S, U25-ch77, adc2

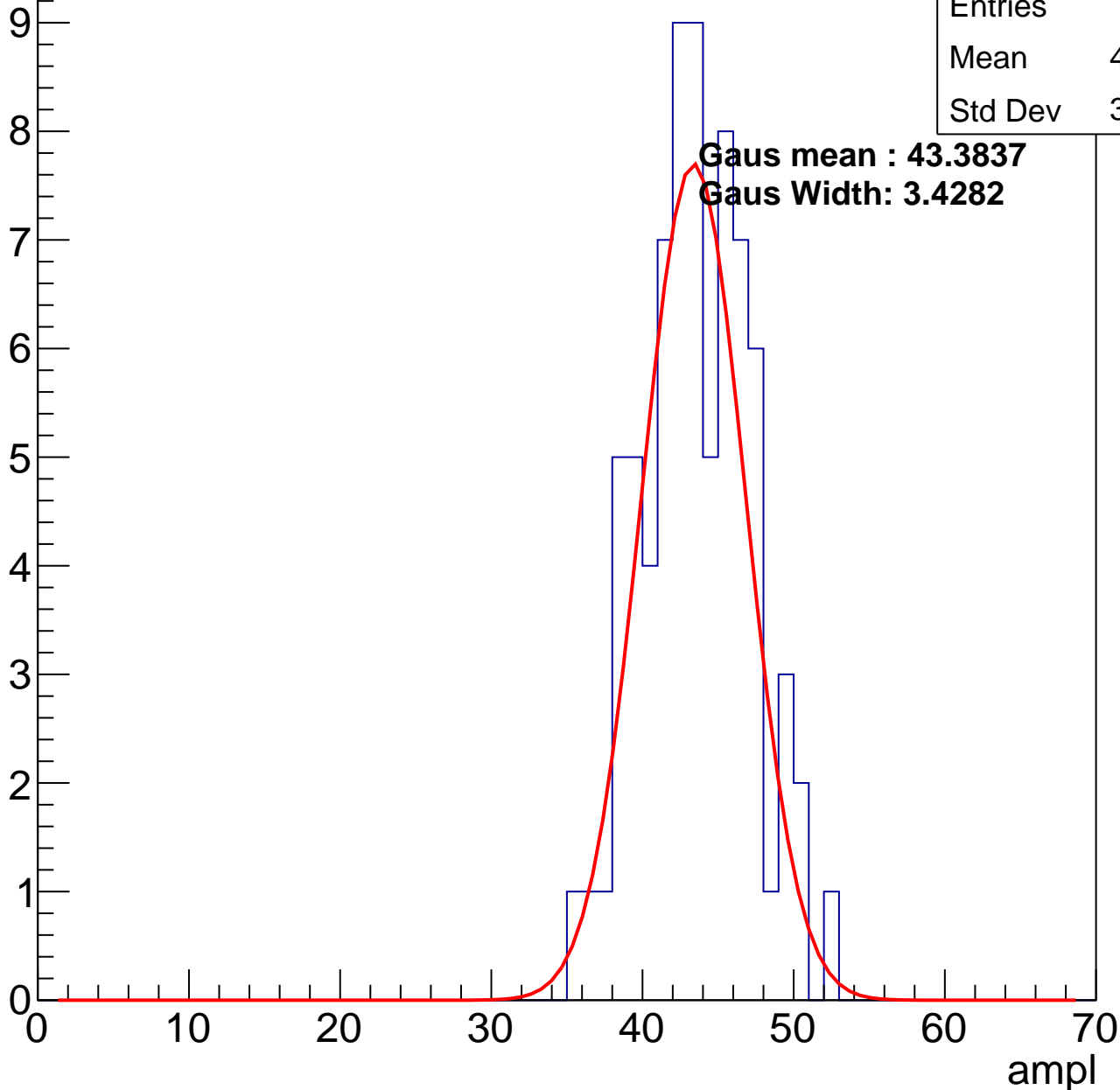
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	43.15
Std Dev	3.528

Gaus mean : 43.3837

Gaus Width: 3.4282

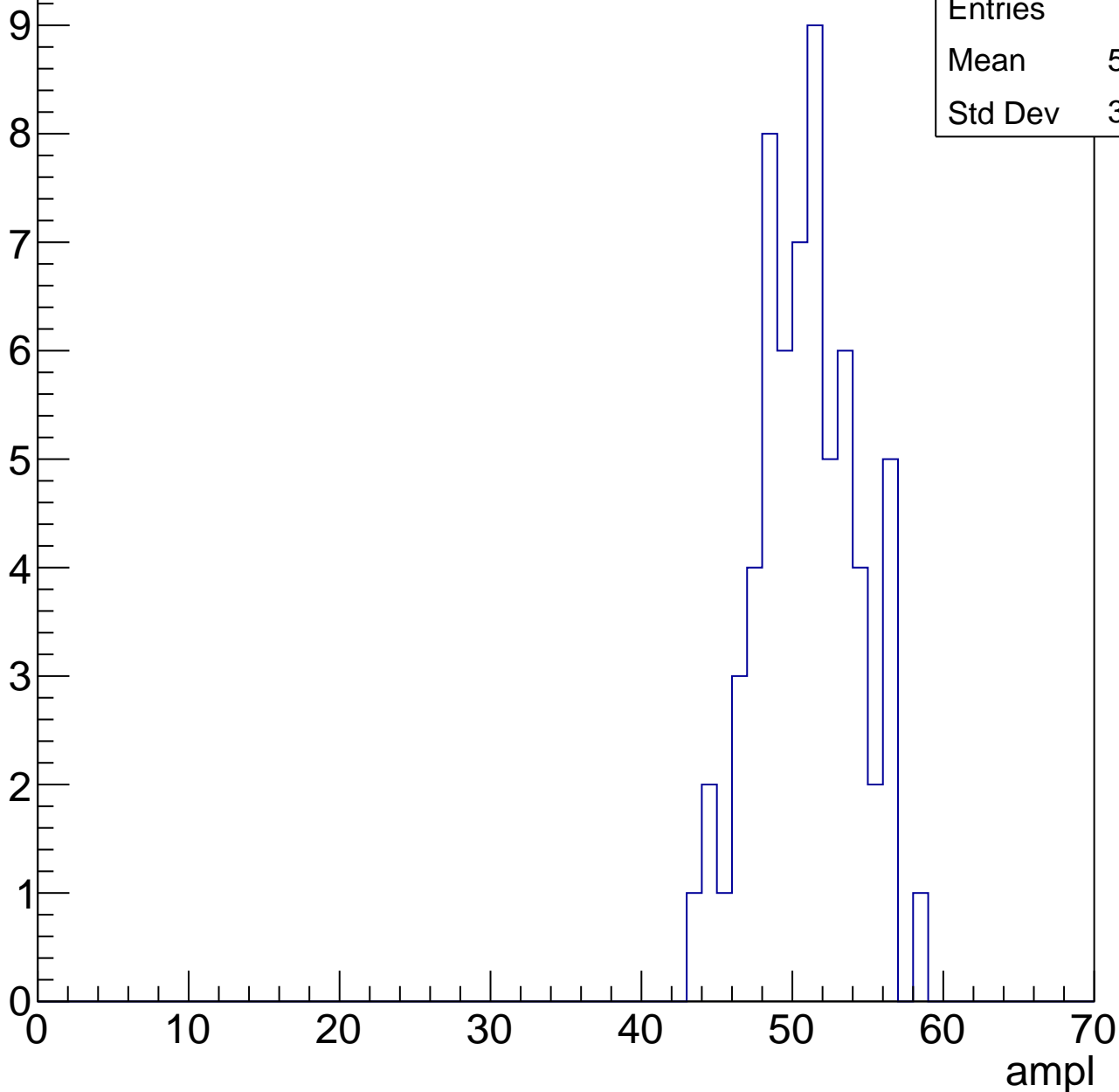


B1L101S, U25-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.48
Std Dev	3.293

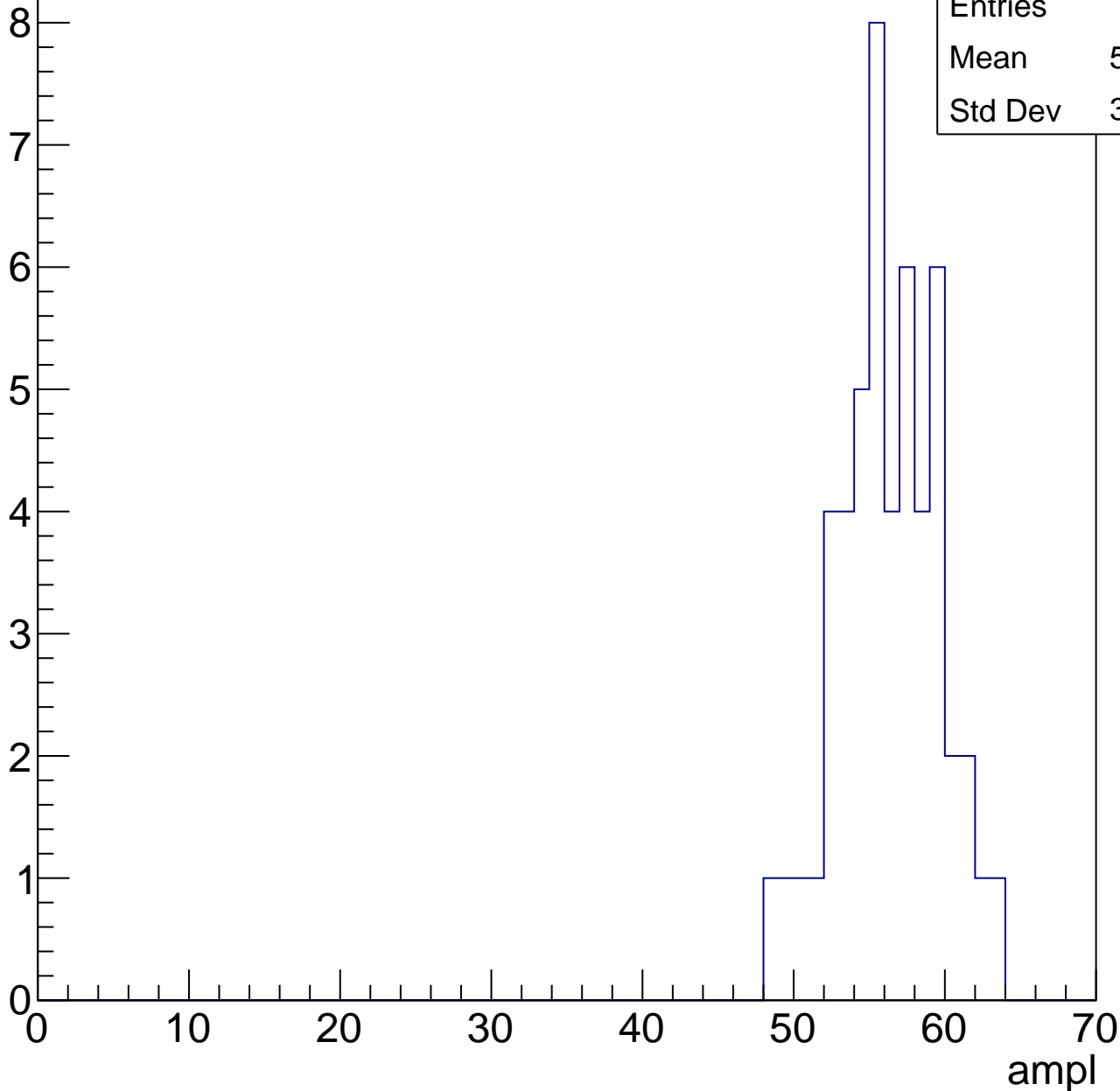


B1L101S, U25-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.82
Std Dev	3.264

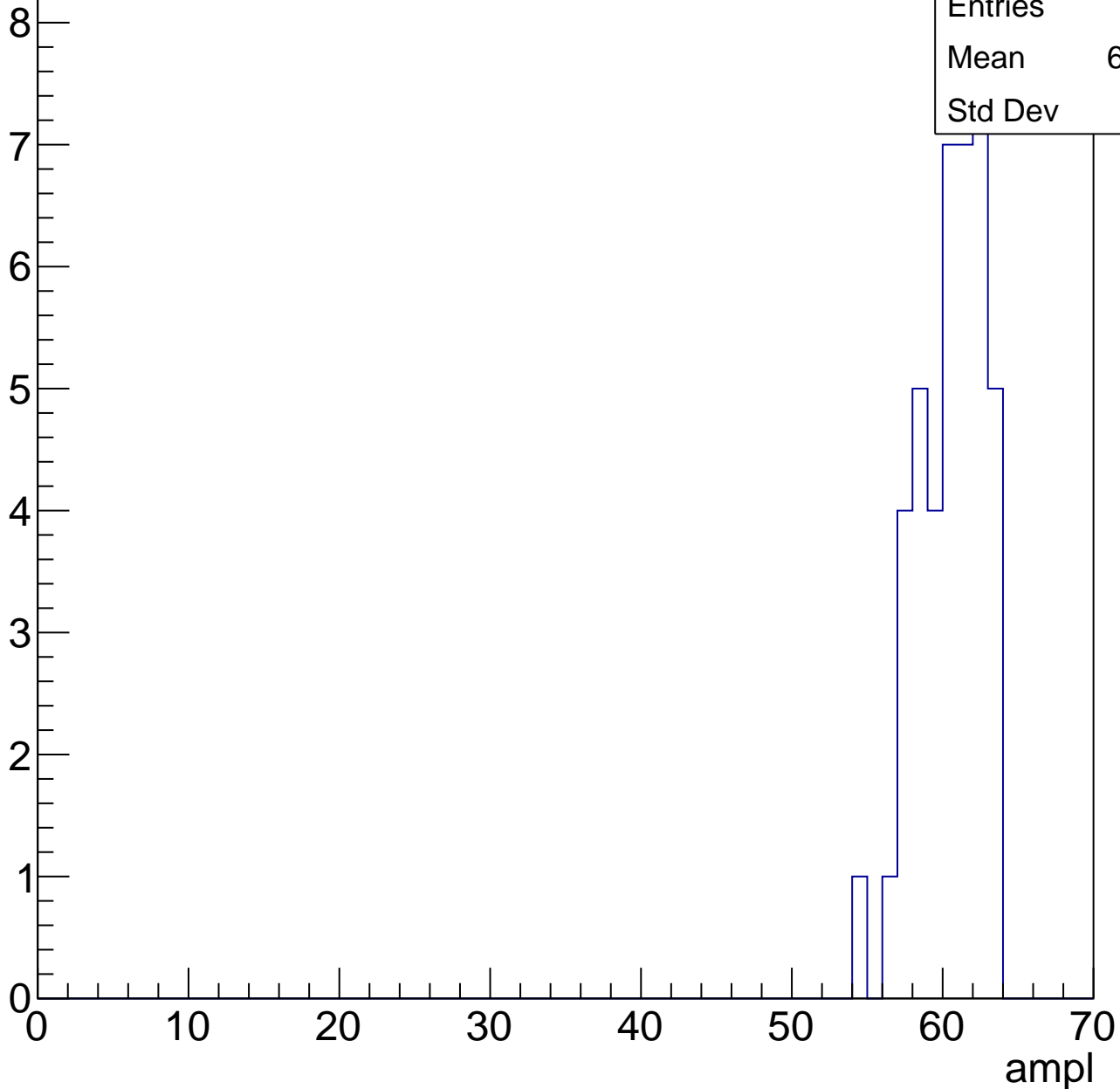


B1L101S, U25-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

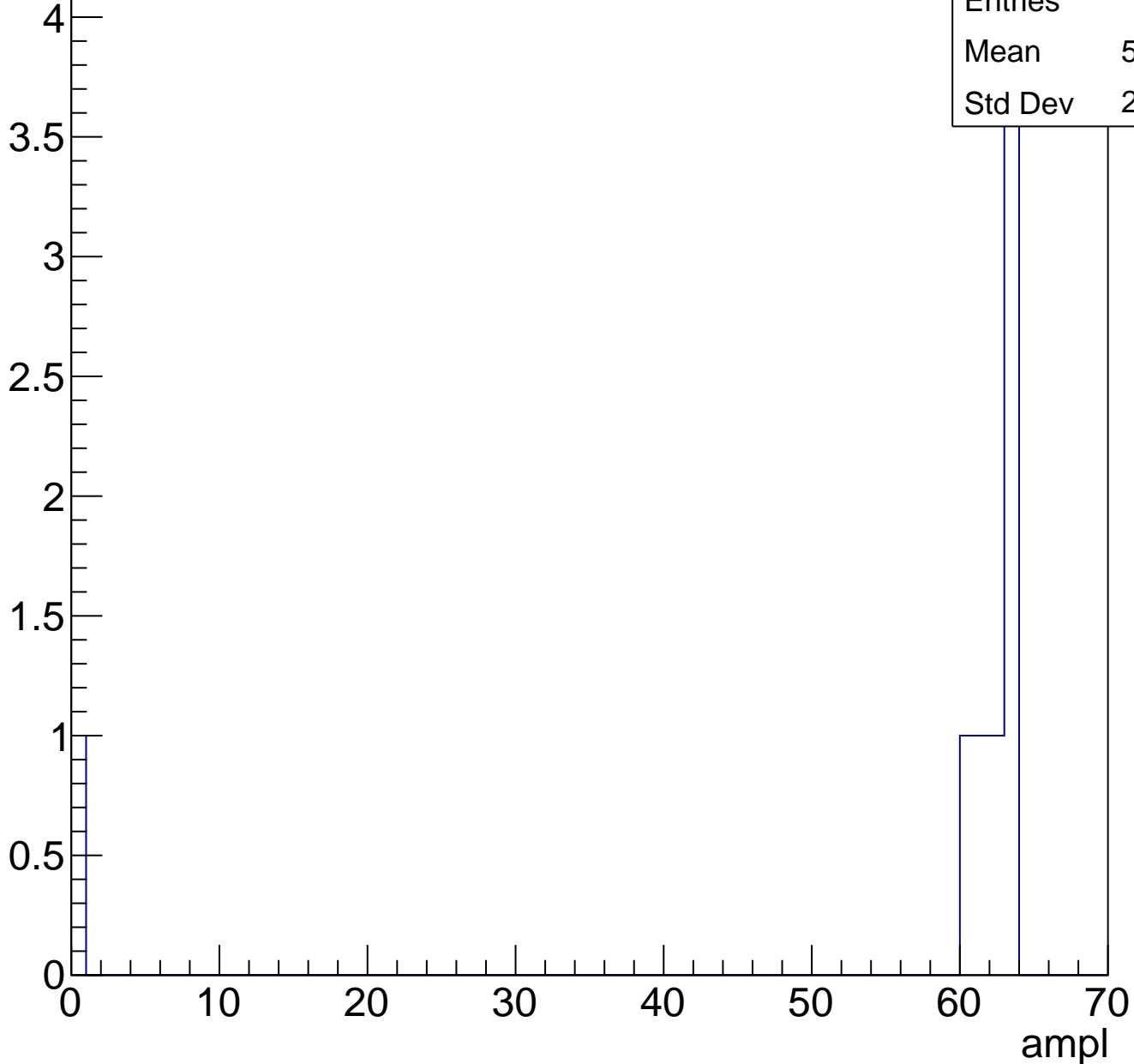
Entries	42
Mean	60.05
Std Dev	2.16



B1L101S, U25-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	54.38
Std Dev	20.58

B1L101S, U25-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U25-ch78, adc0

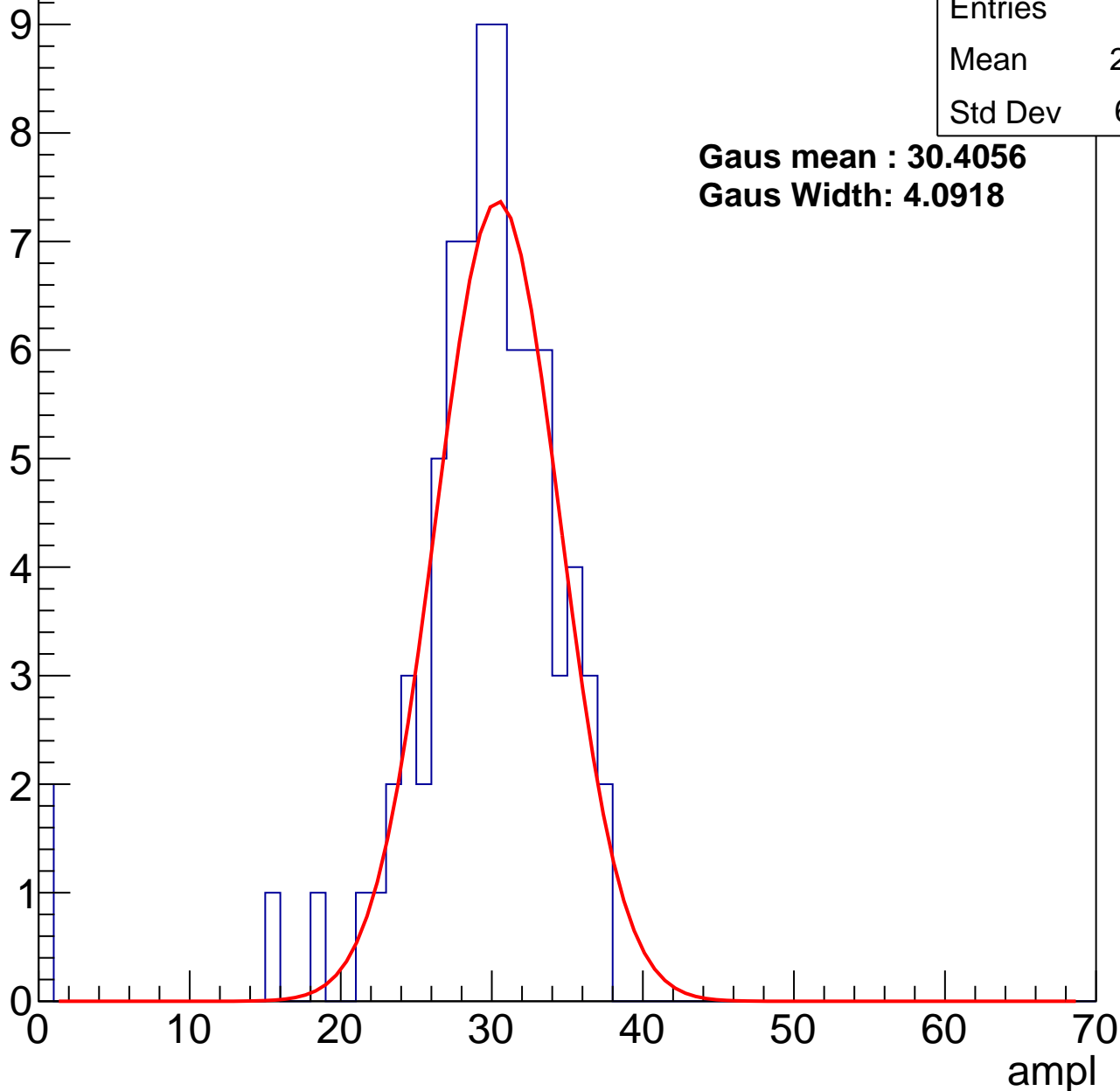
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	28.62
Std Dev	6.161

Gaus mean : 30.4056

Gaus Width: 4.0918



B1L101S, U25-ch78, adc1

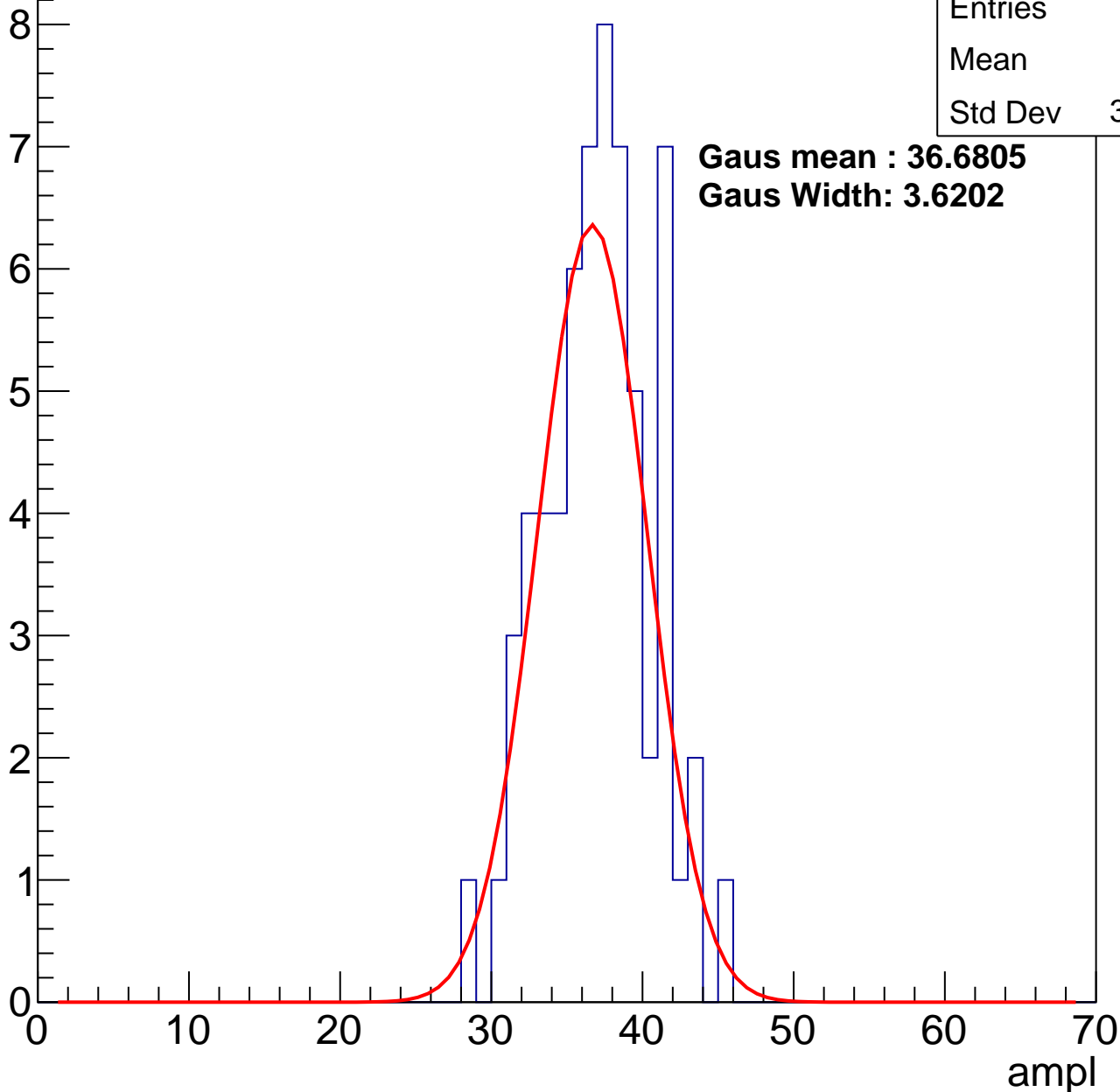
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.6
Std Dev	3.512

Gaus mean : 36.6805

Gaus Width: 3.6202



B1L101S, U25-ch78, adc2

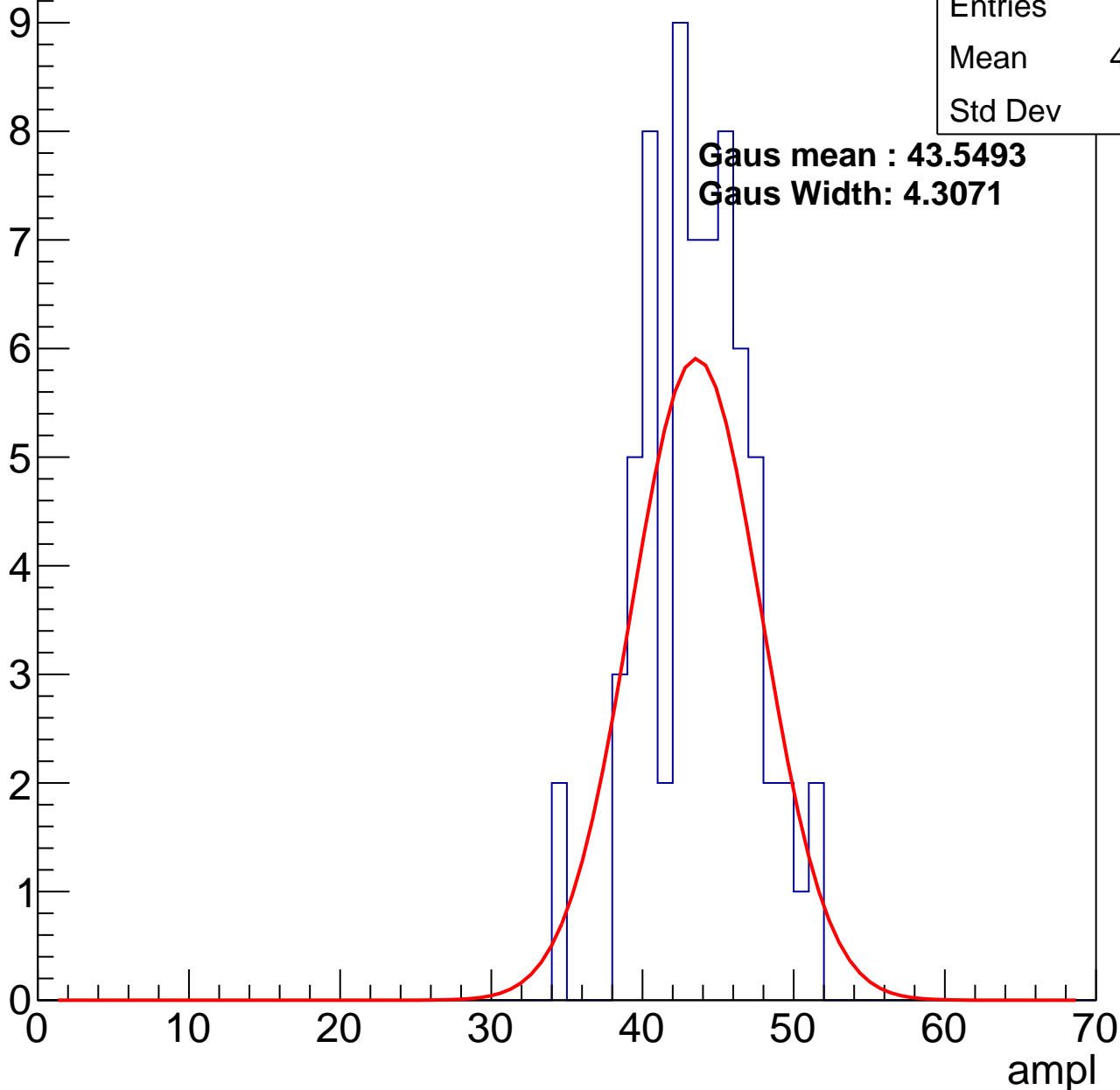
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	43.23
Std Dev	3.56

Gaus mean : 43.5493

Gaus Width: 4.3071

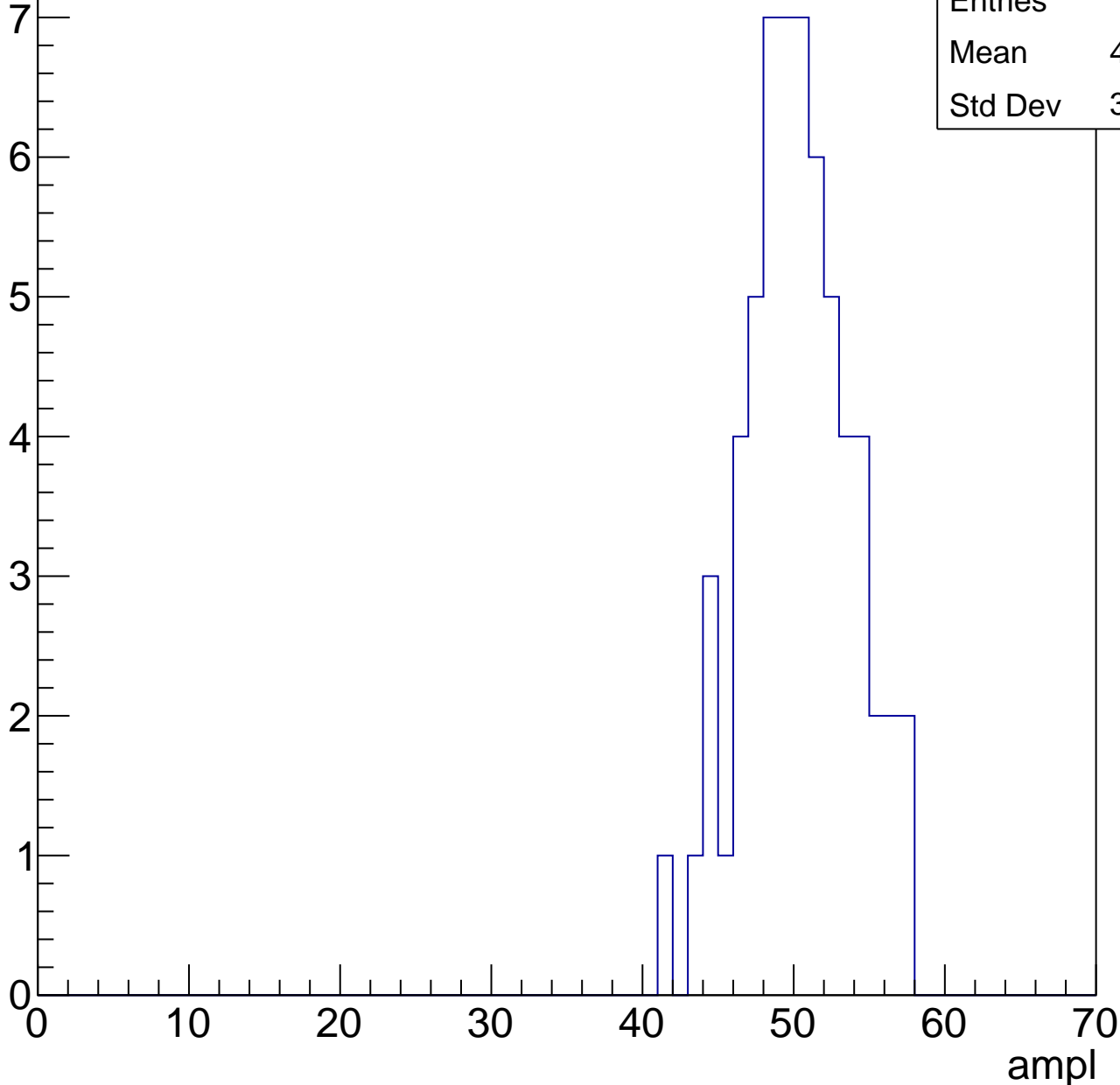


B1L101S, U25-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.82
Std Dev	3.509

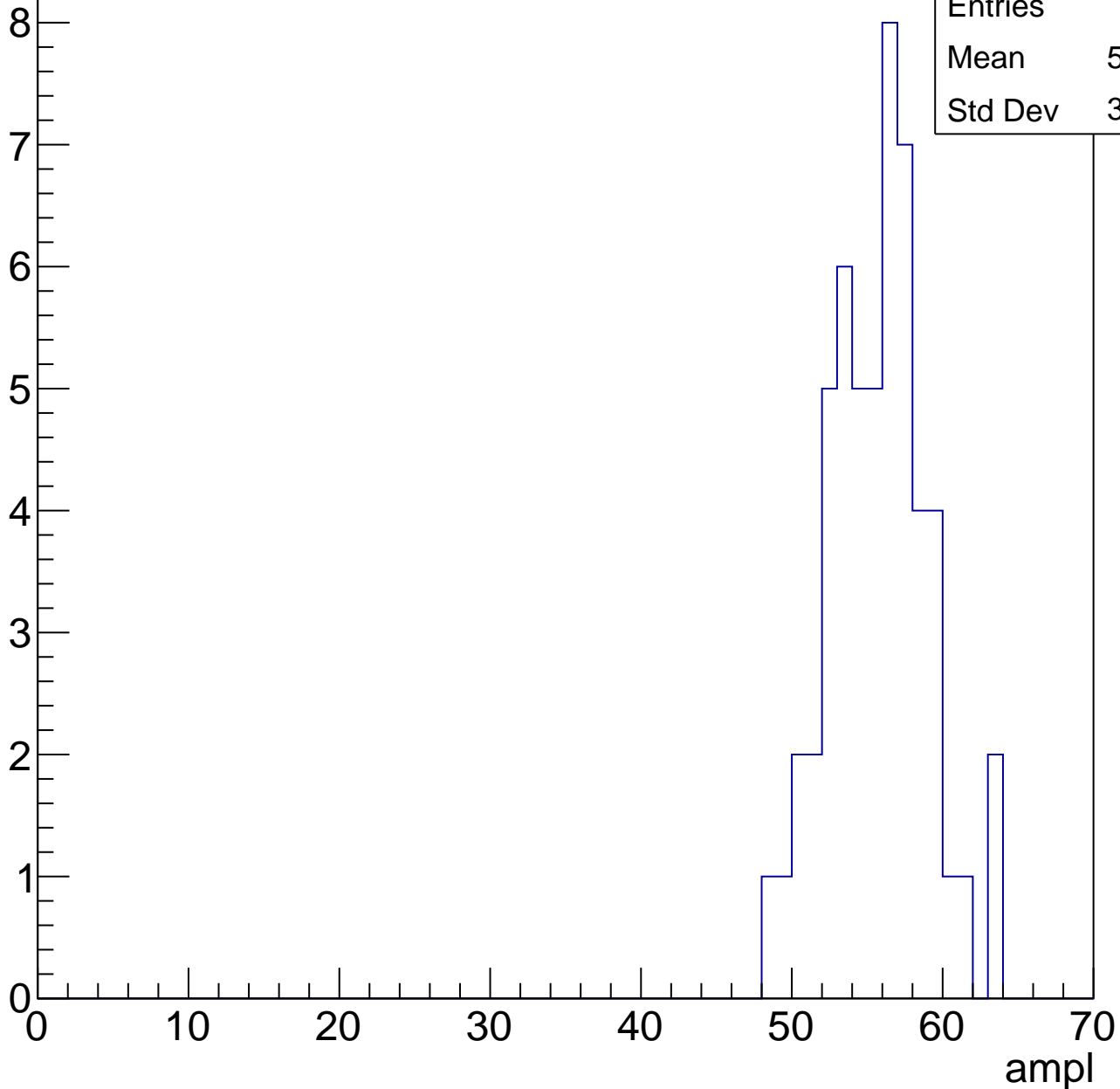


B1L101S, U25-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.26
Std Dev	3.227

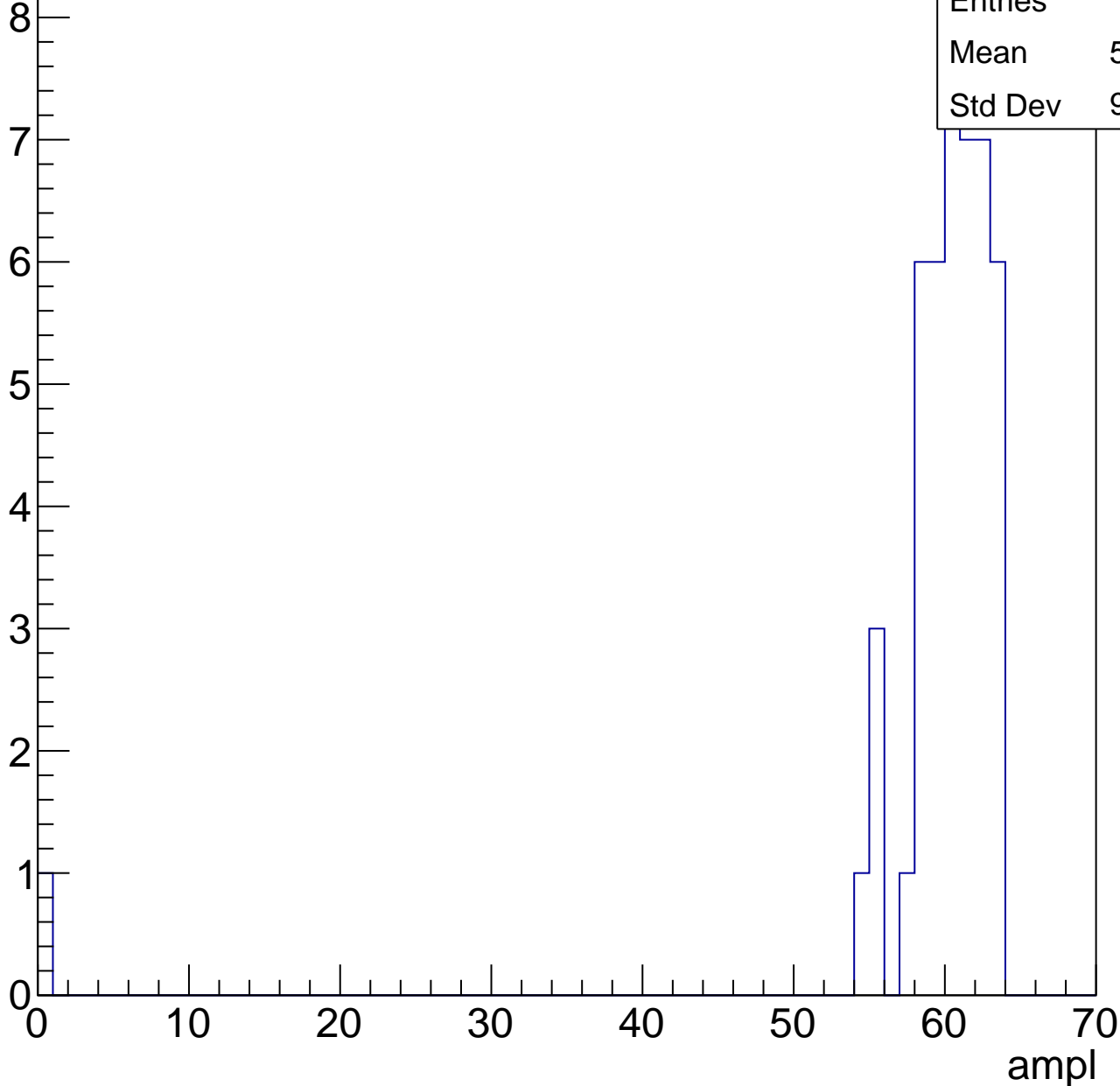


B1L101S, U25-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

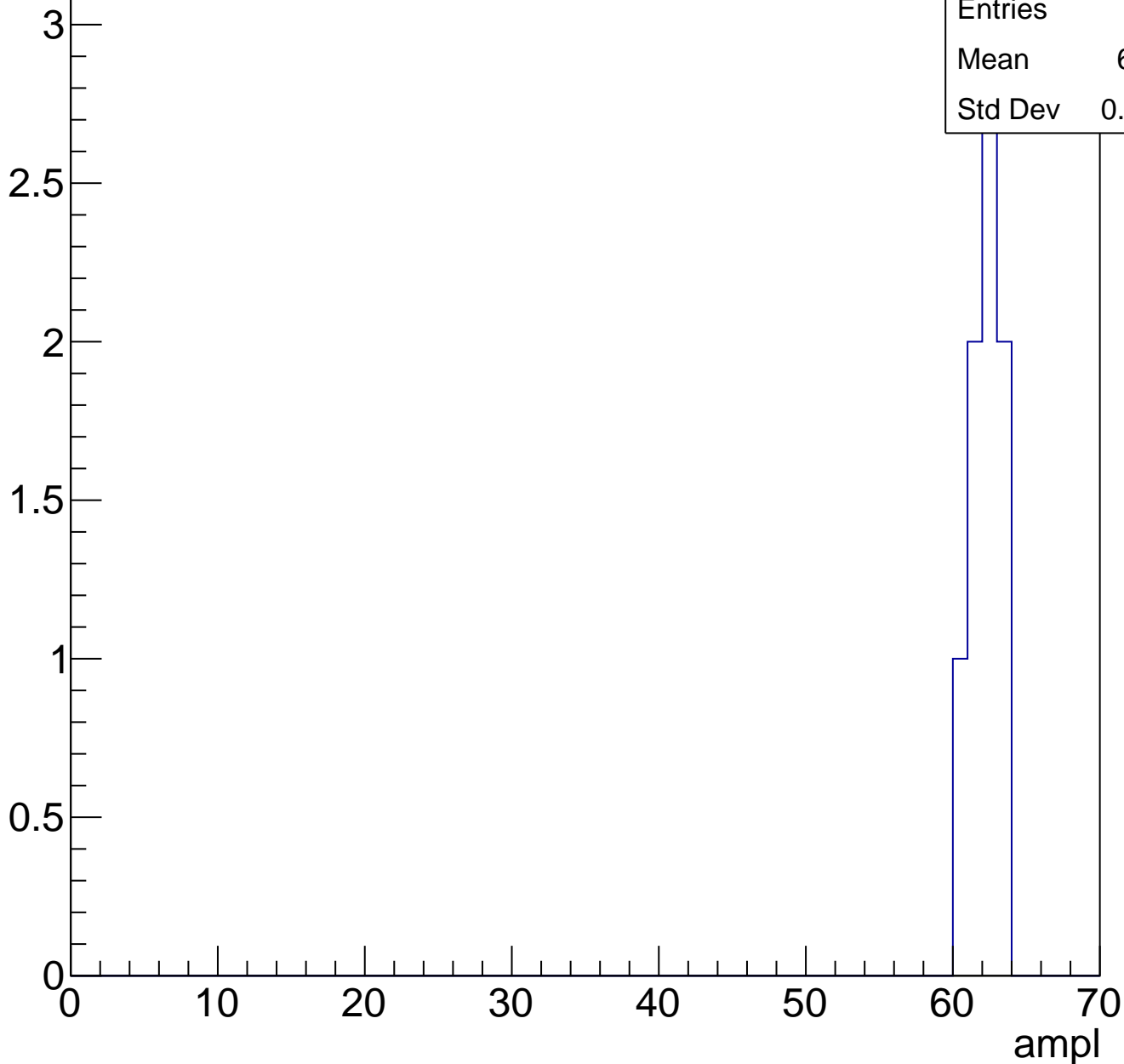
Entries	46
Mean	58.63
Std Dev	9.032



B1L101S, U25-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	61.75
Std Dev	0.9682

B1L101S, U25-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch79, adc0

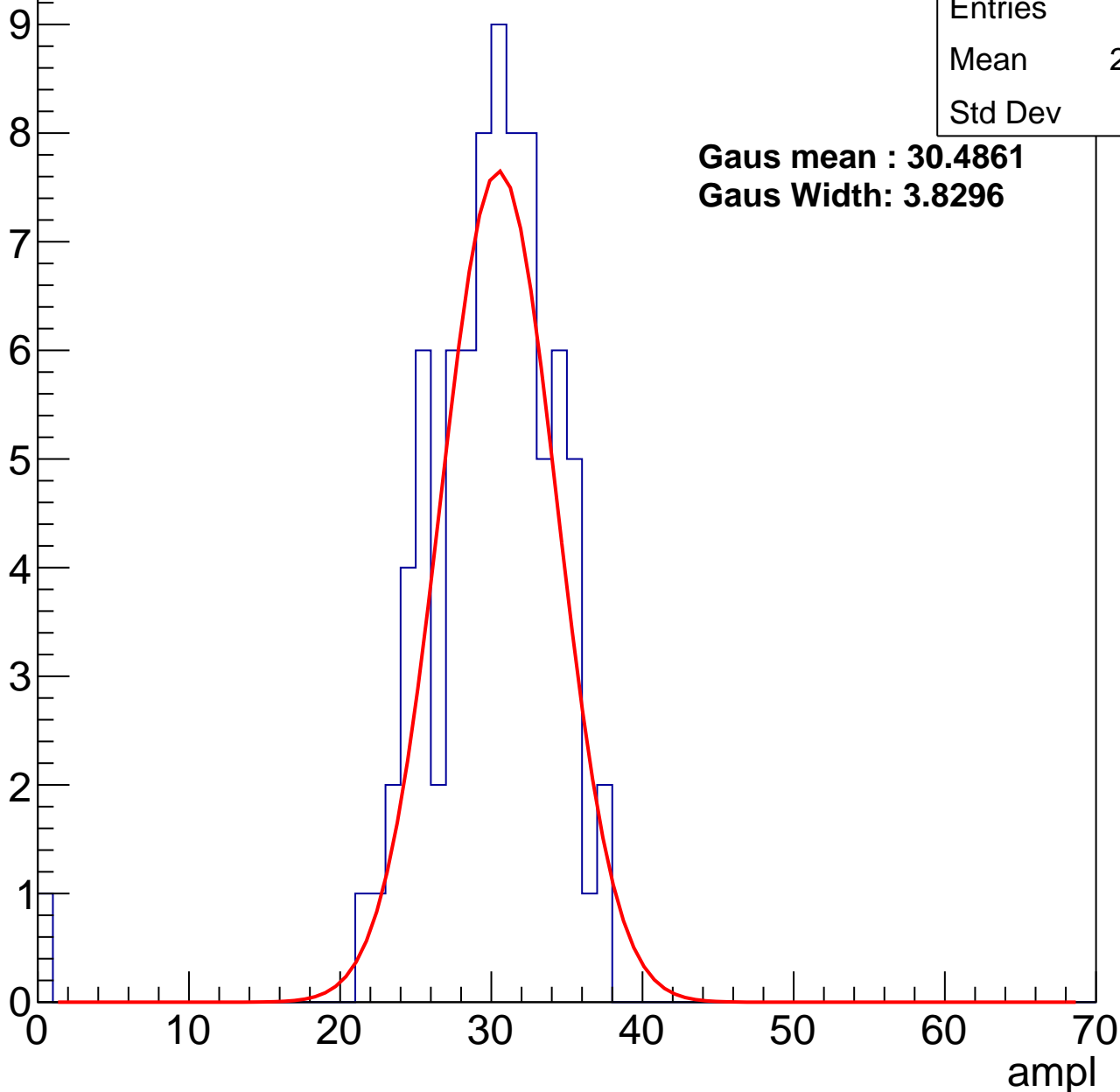
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	29.35
Std Dev	4.92

Gaus mean : 30.4861

Gaus Width: 3.8296



B1L101S, U25-ch79, adc1

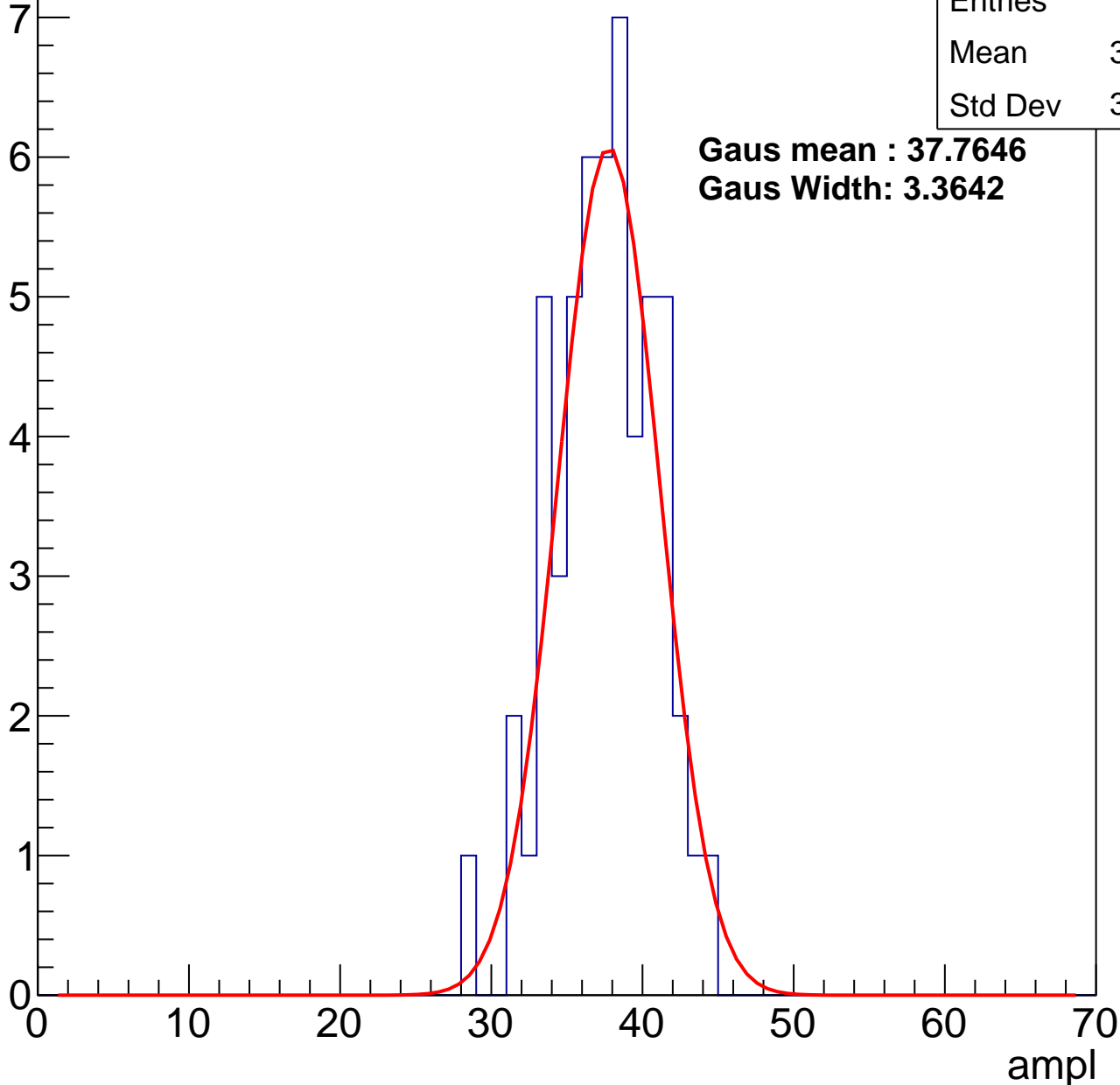
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	37.04
Std Dev	3.316

Gaus mean : 37.7646

Gaus Width: 3.3642



B1L101S, U25-ch79, adc2

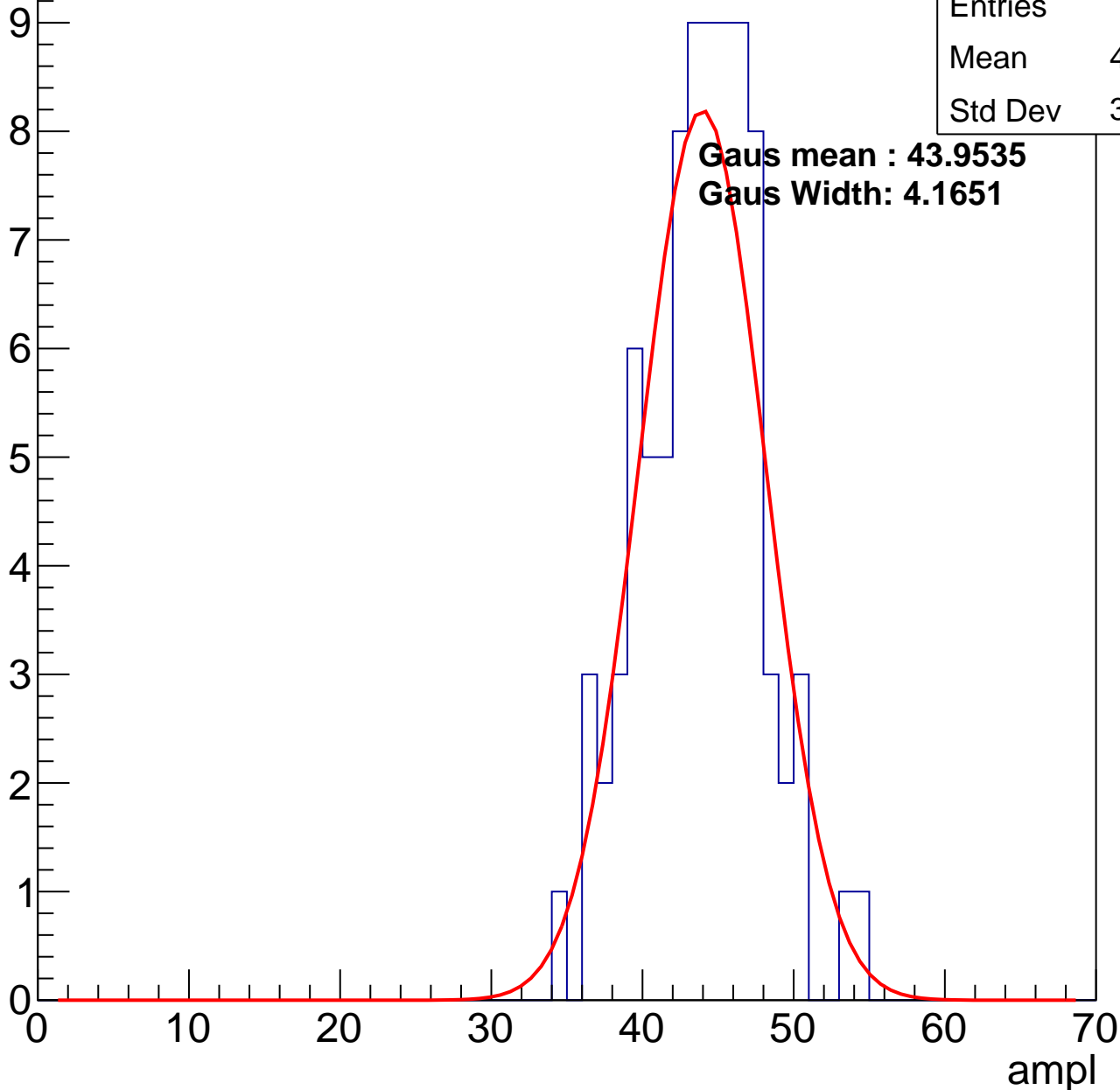
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	43.47
Std Dev	3.856

Gaus mean : 43.9535

Gaus Width: 4.1651



B1L101S, U25-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	51.48
Std Dev	3.106

Entry

10

8

6

4

2

0

0

10

20

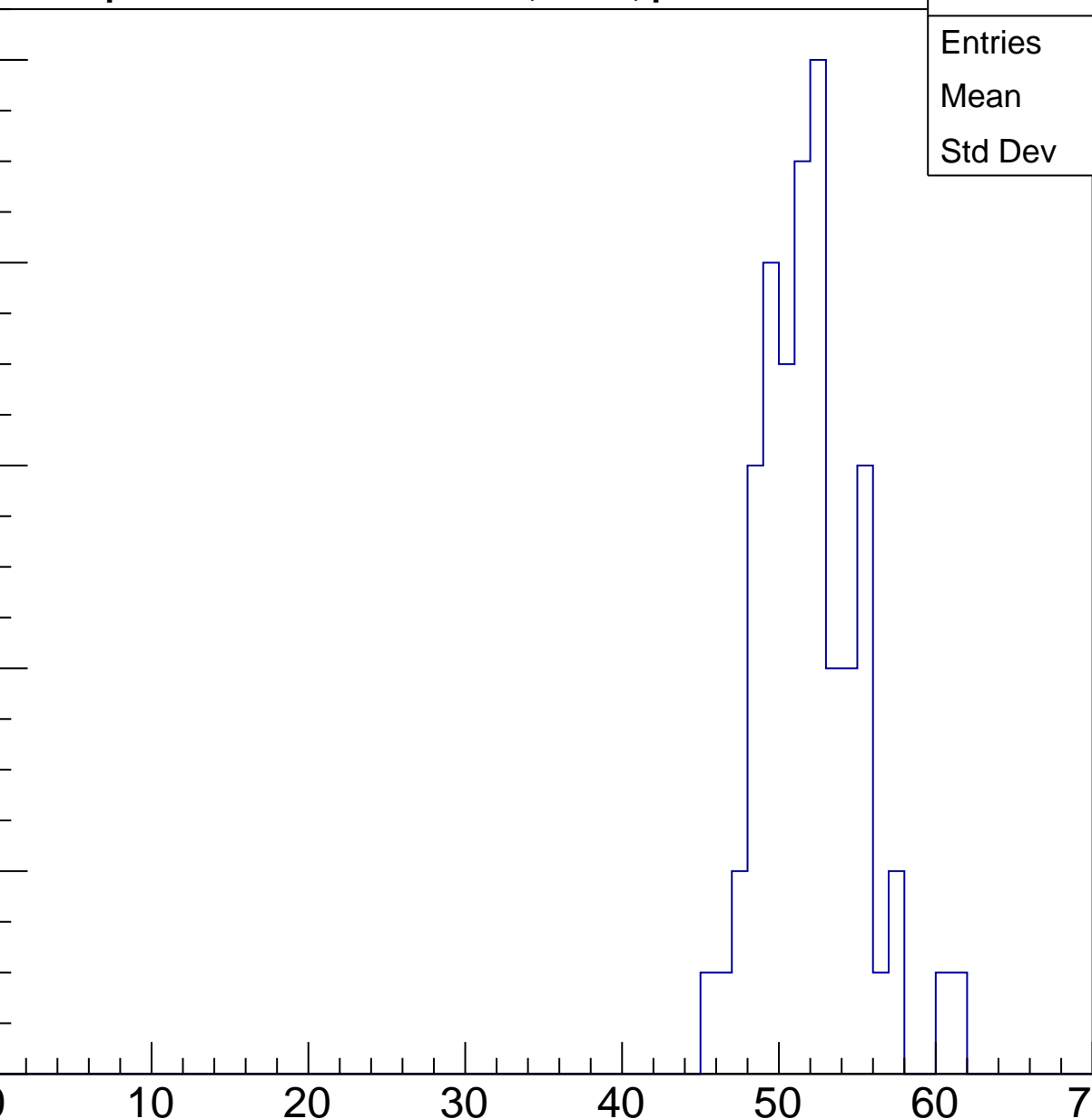
30

40

50

60

ampl

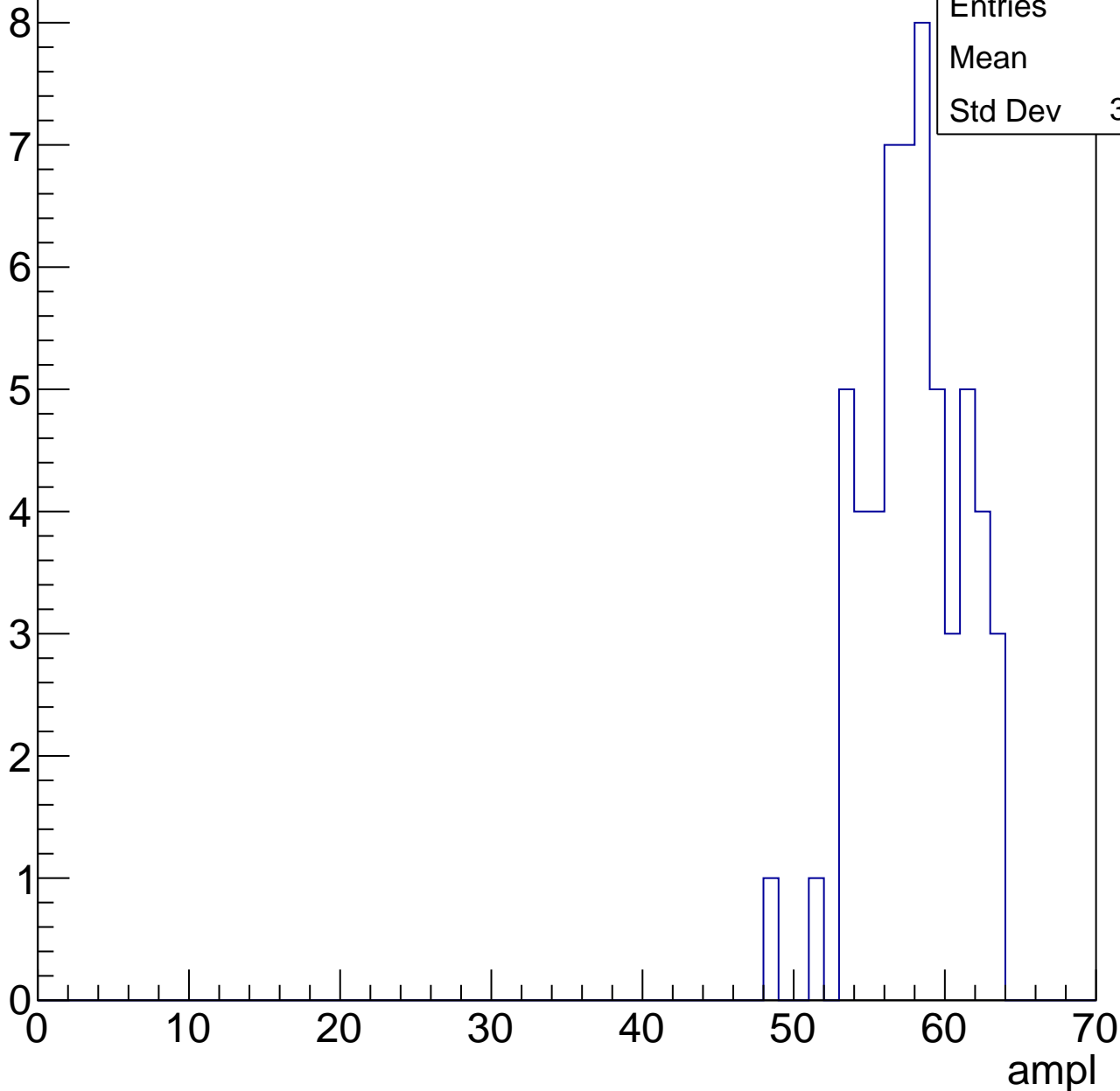


B1L101S, U25-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

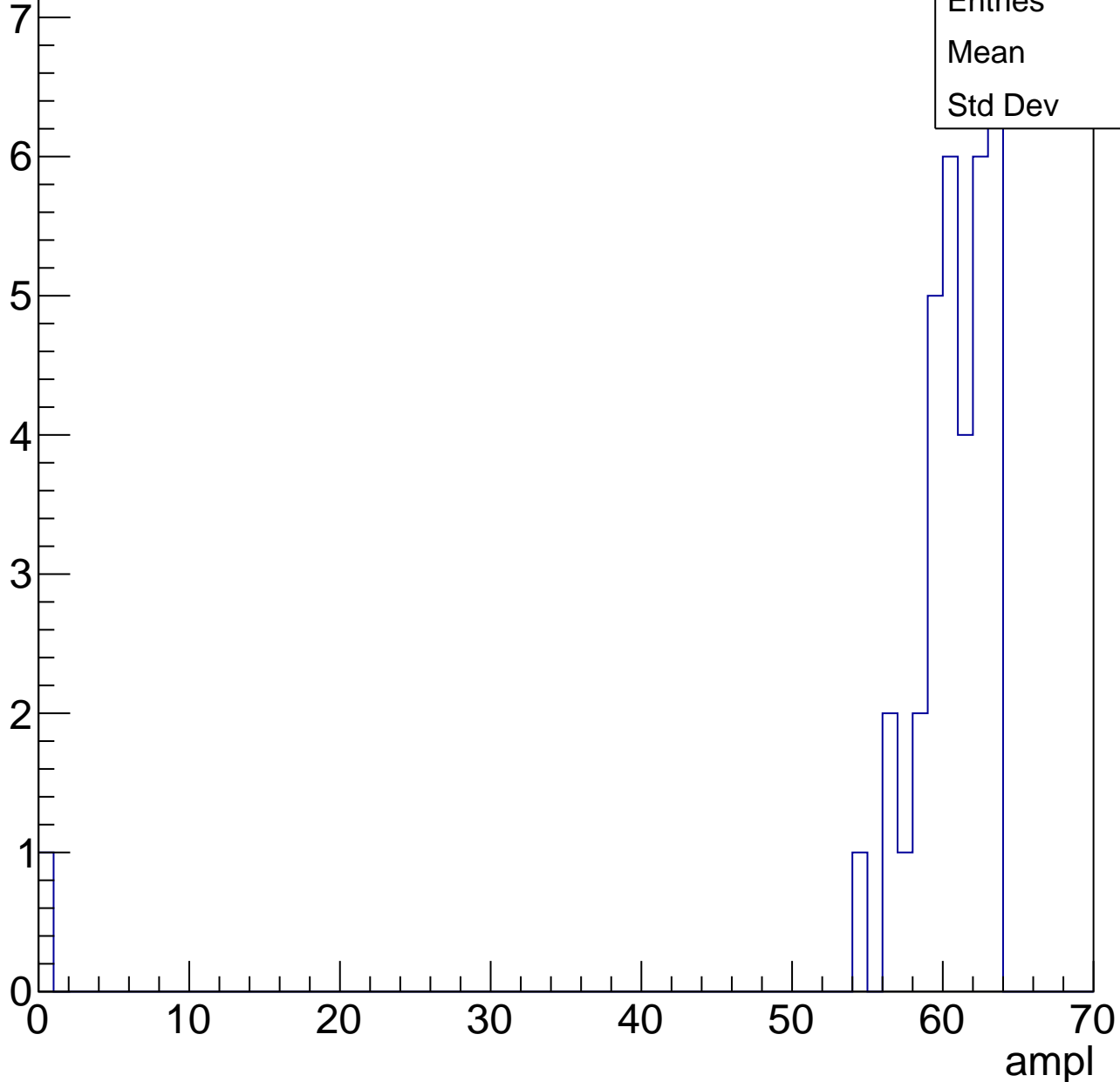
Entries	57
Mean	57.4
Std Dev	3.217



B1L101S, U25-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch80, adc0

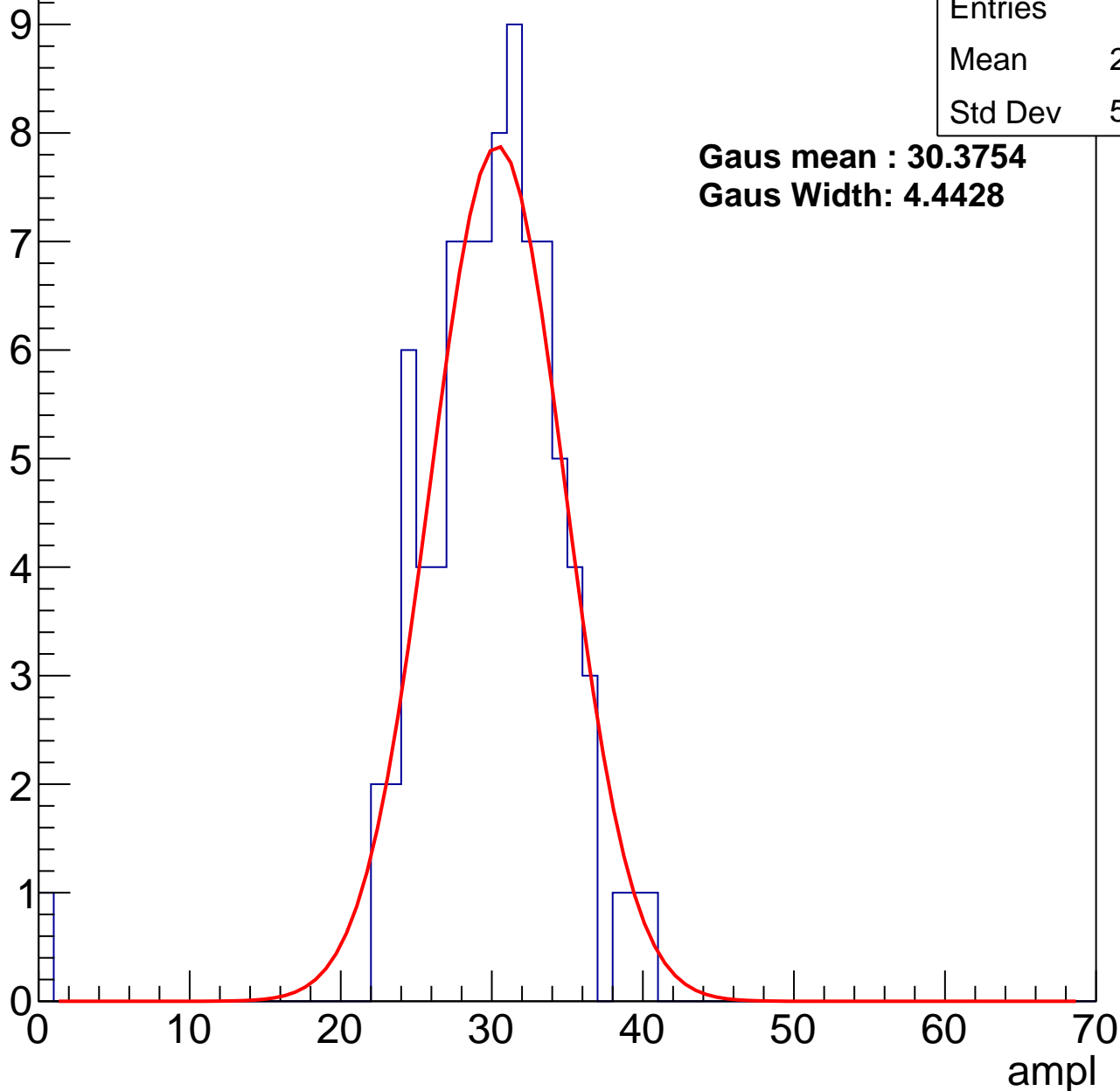
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	29.48
Std Dev	5.073

Gaus mean : 30.3754

Gaus Width: 4.4428



B1L101S, U25-ch80, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	38.18
Std Dev	4.04

Gaus mean : 38.6652

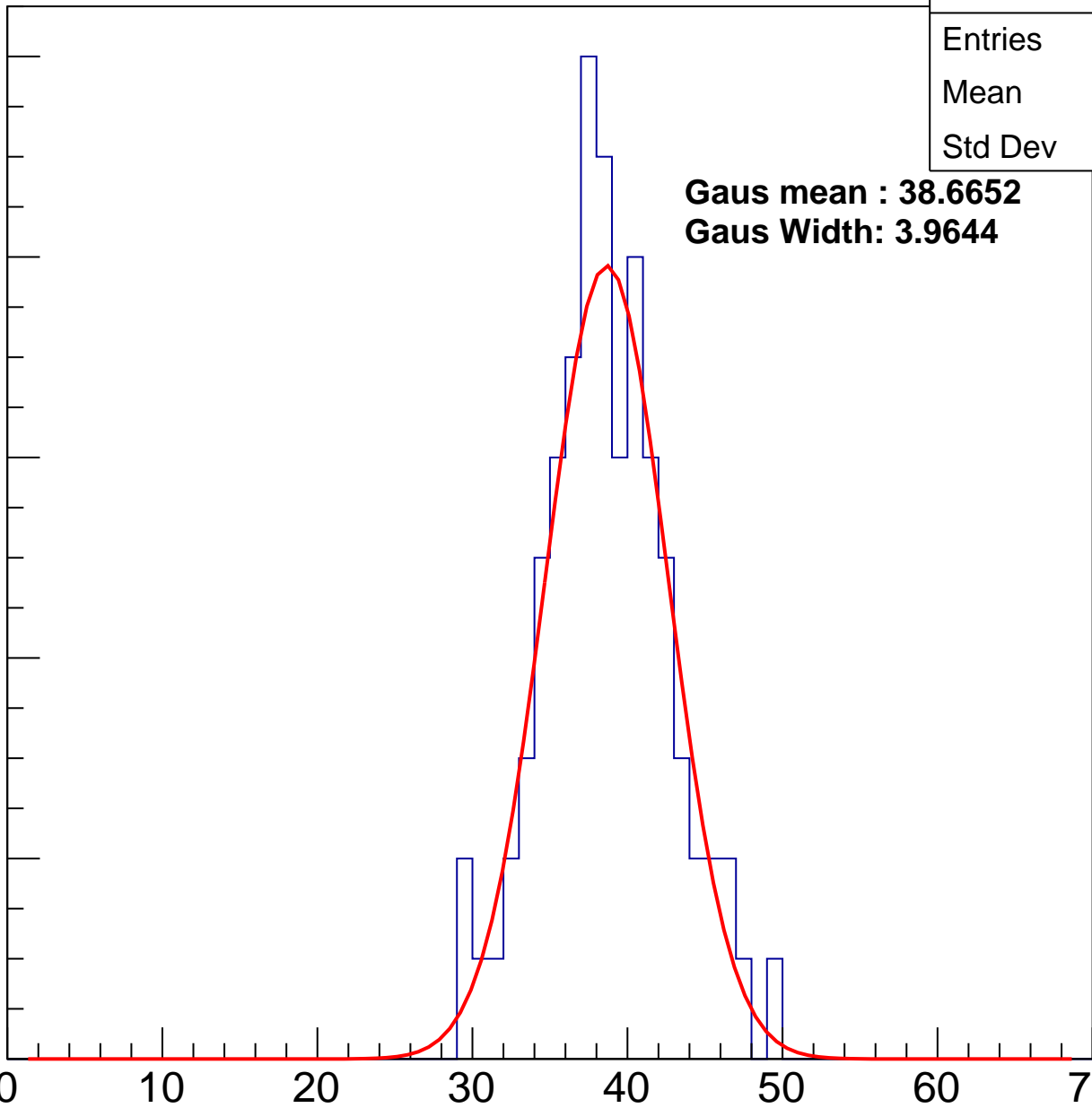
Gaus Width: 3.9644

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch80, adc2

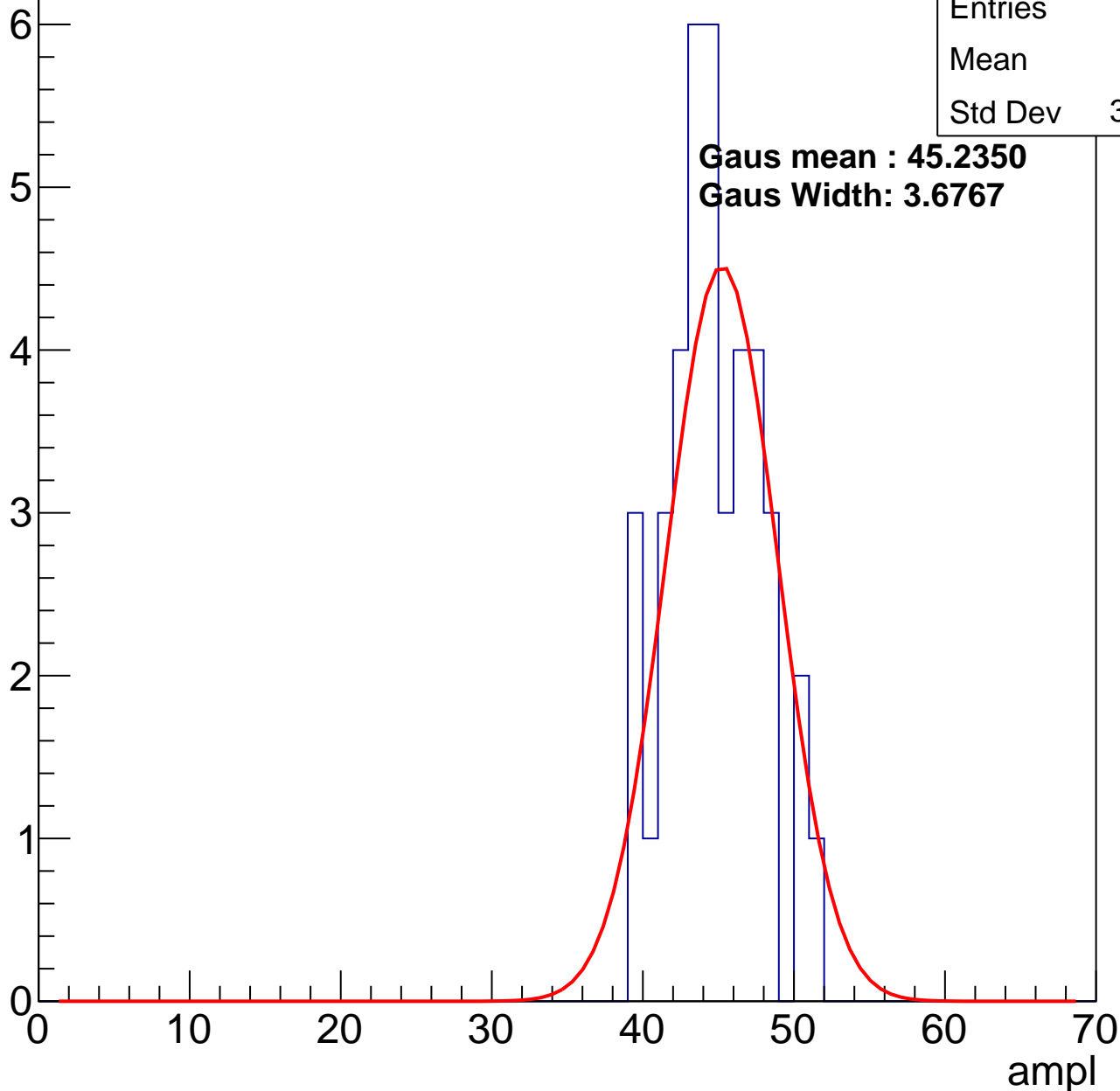
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	44.3
Std Dev	3.002

Gaus mean : 45.2350

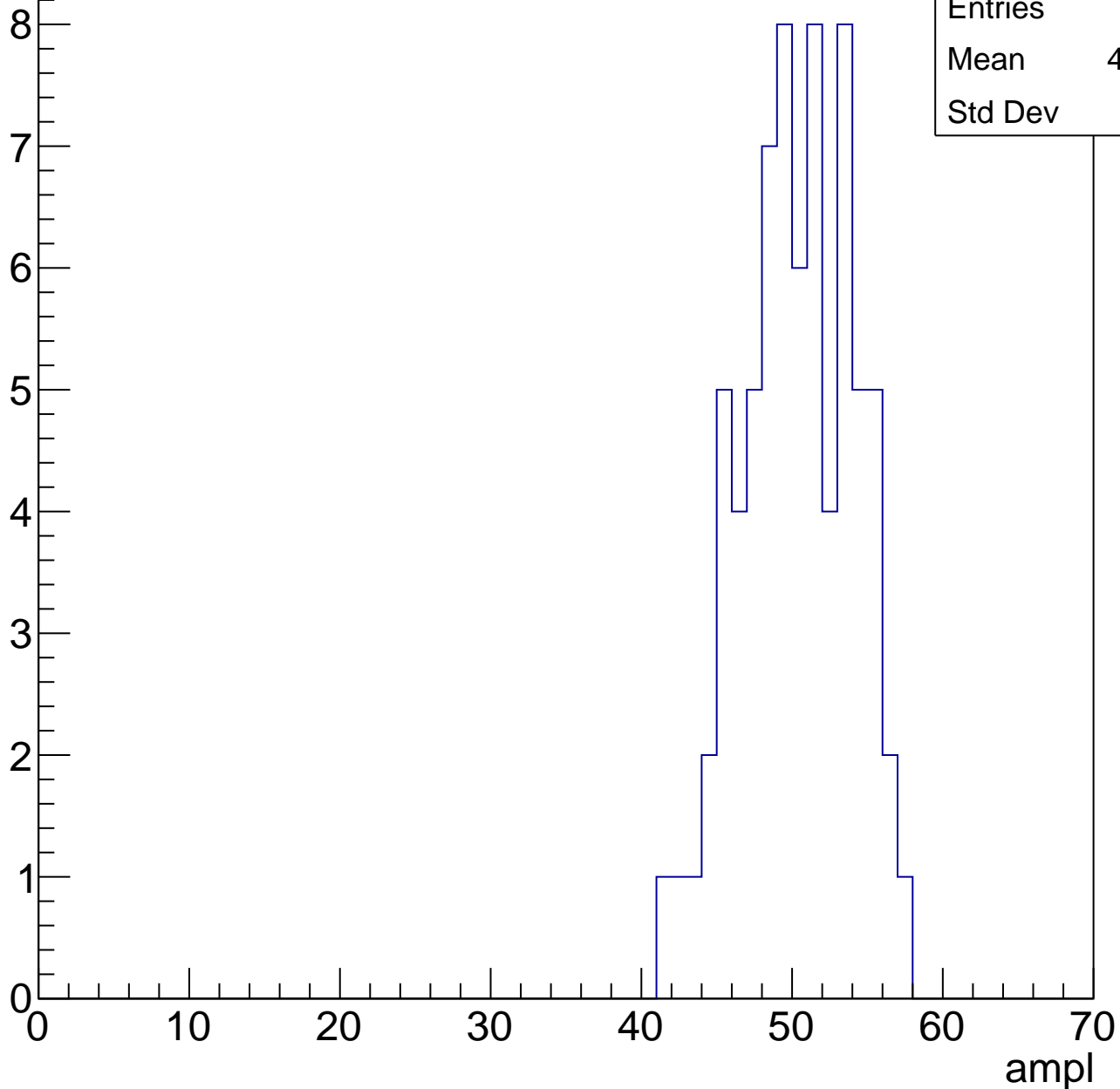
Gaus Width: 3.6767



B1L101S, U25-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

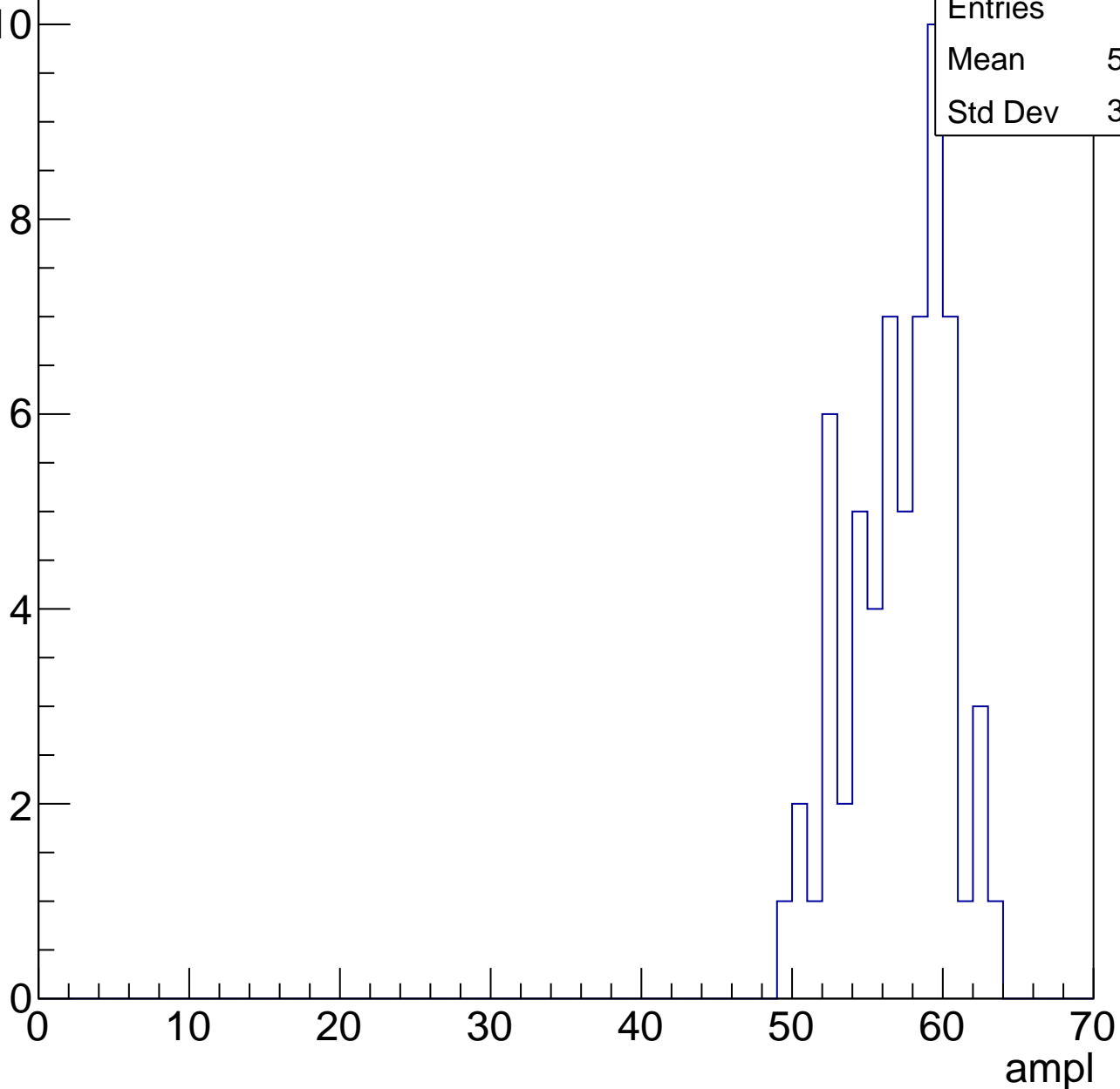


B1L101S, U25-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.63
Std Dev	3.303



B1L101S, U25-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	32
Mean	58.62
Std Dev	10.76

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

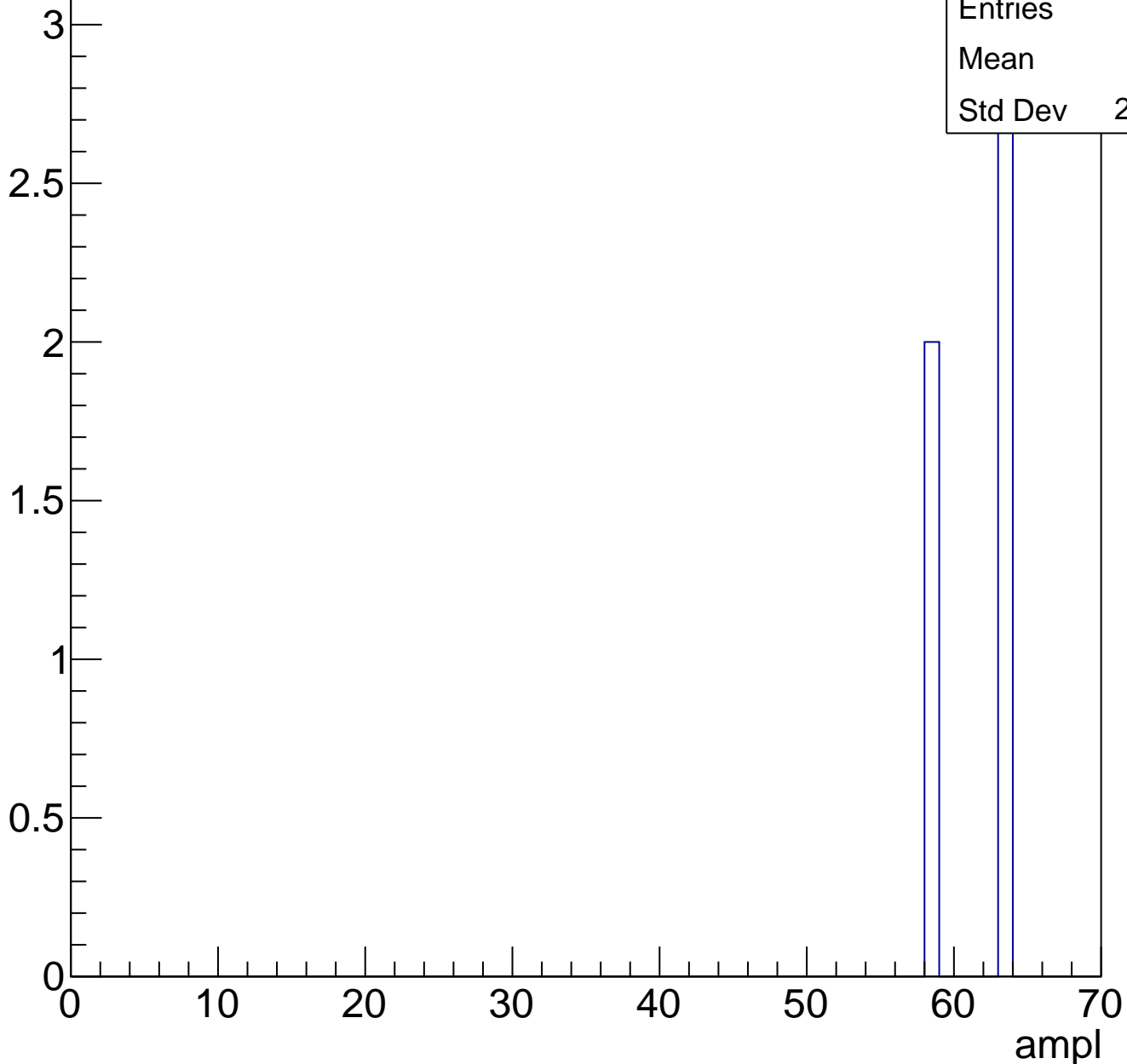
8

9

B1L101S, U25-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch81, adc0

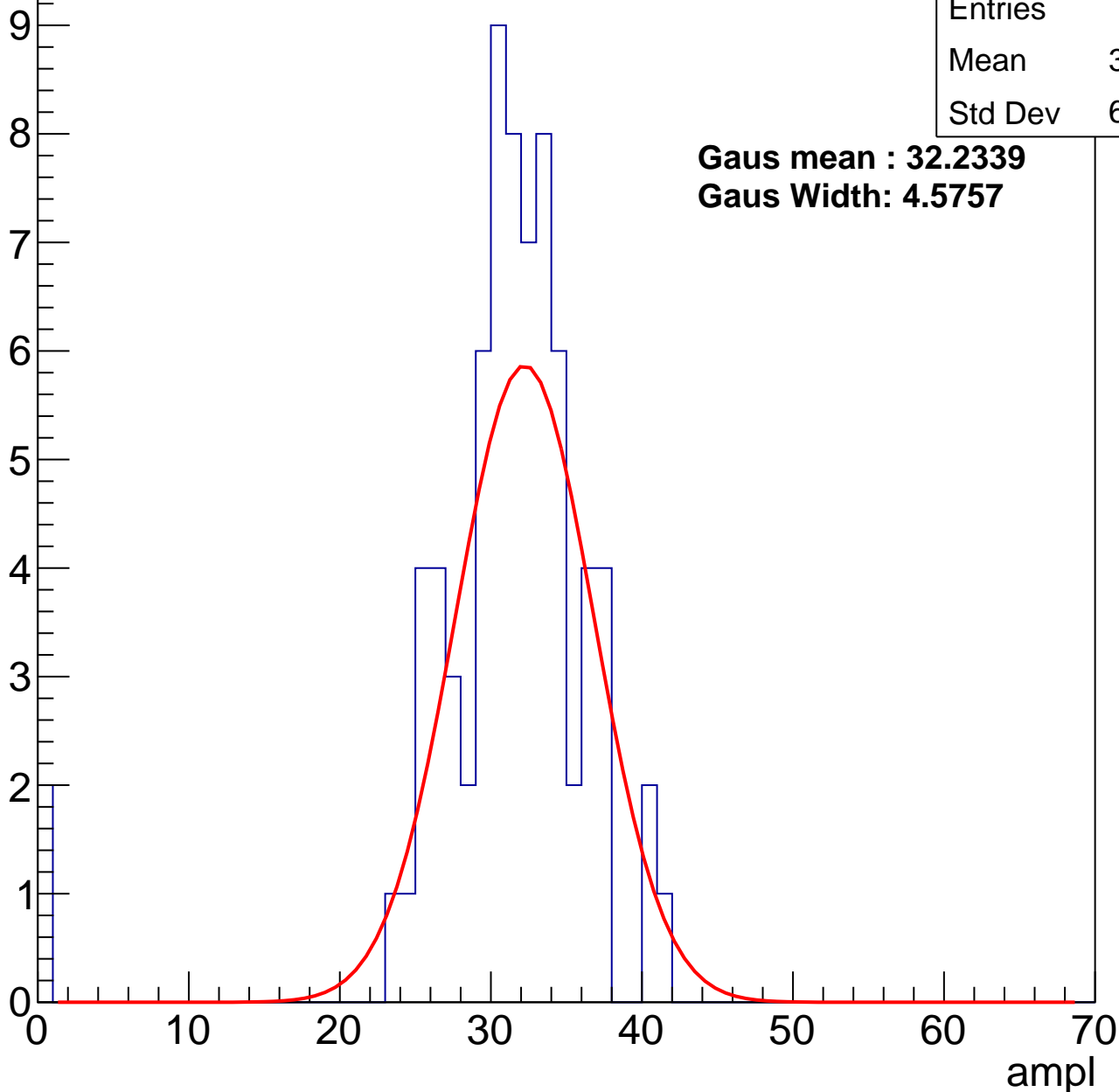
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.47
Std Dev	6.355

Gaus mean : 32.2339

Gaus Width: 4.5757



B1L101S, U25-ch81, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	61
Mean	37.72
Std Dev	3.344

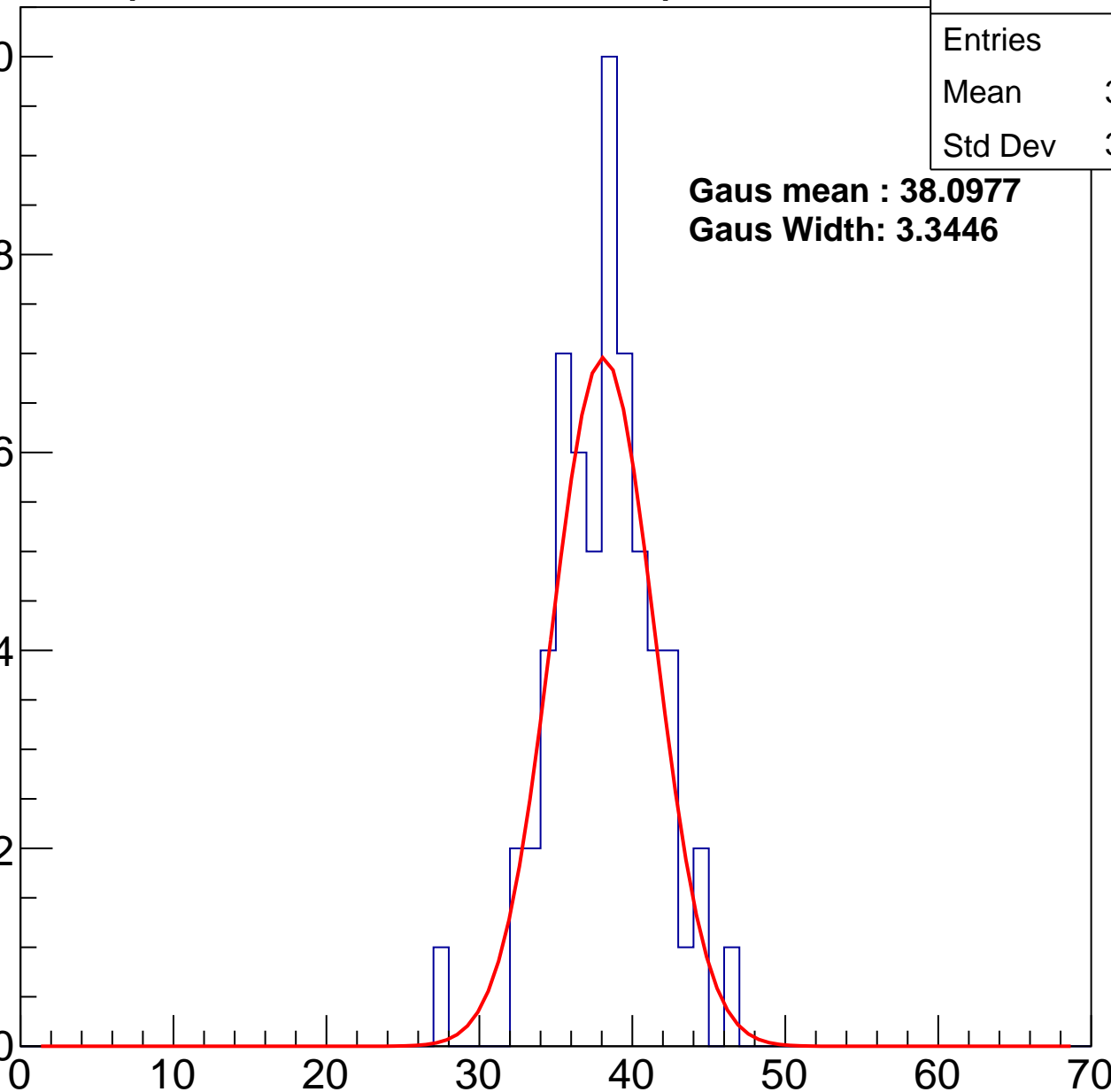
Gaus mean : 38.0977

Gaus Width: 3.3446

Entry

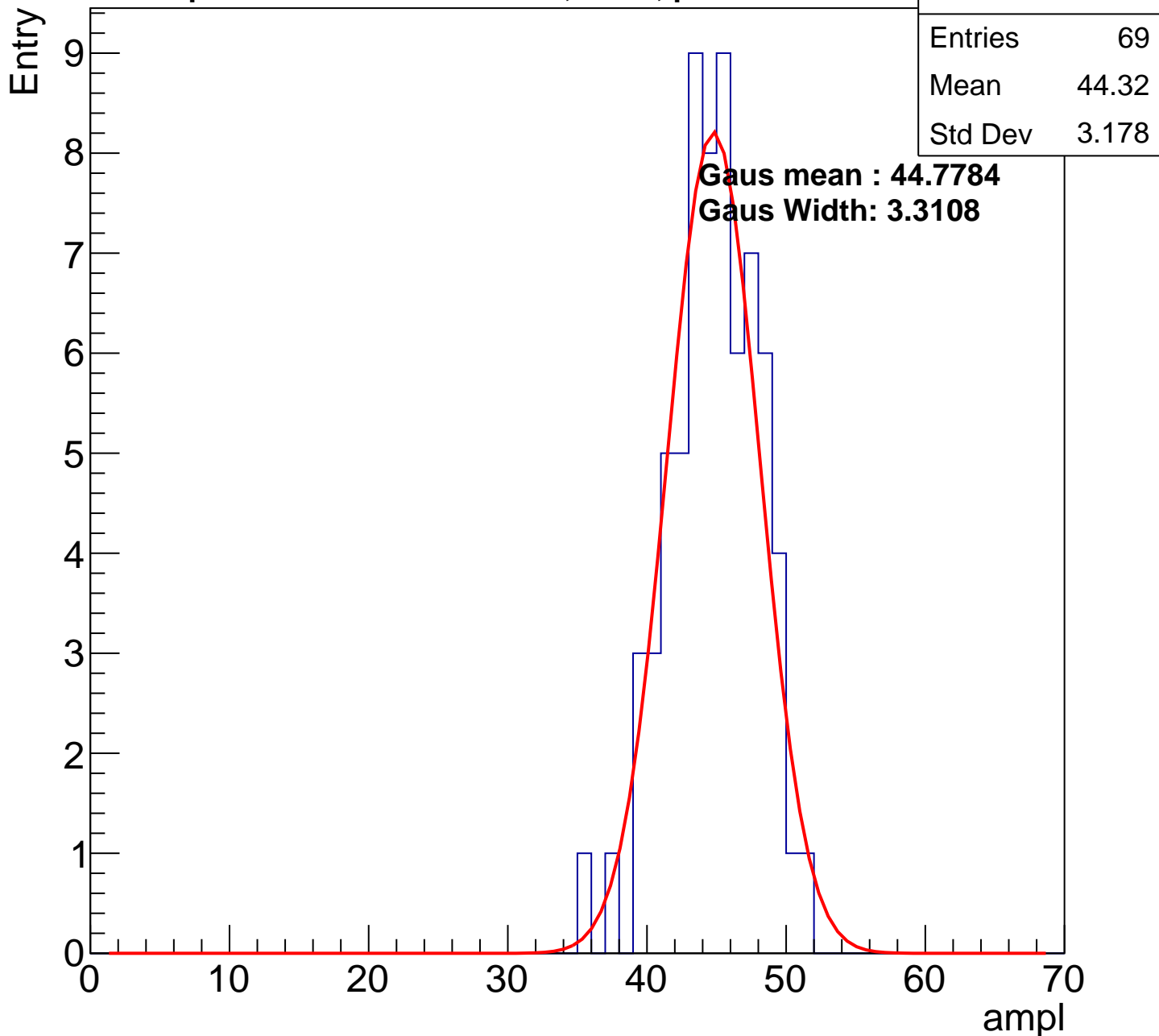
10
8
6
4
2
0

ampl



B1L101S, U25-ch81, adc2

calib_packv5_042523_0143.root, FC#0, port D2

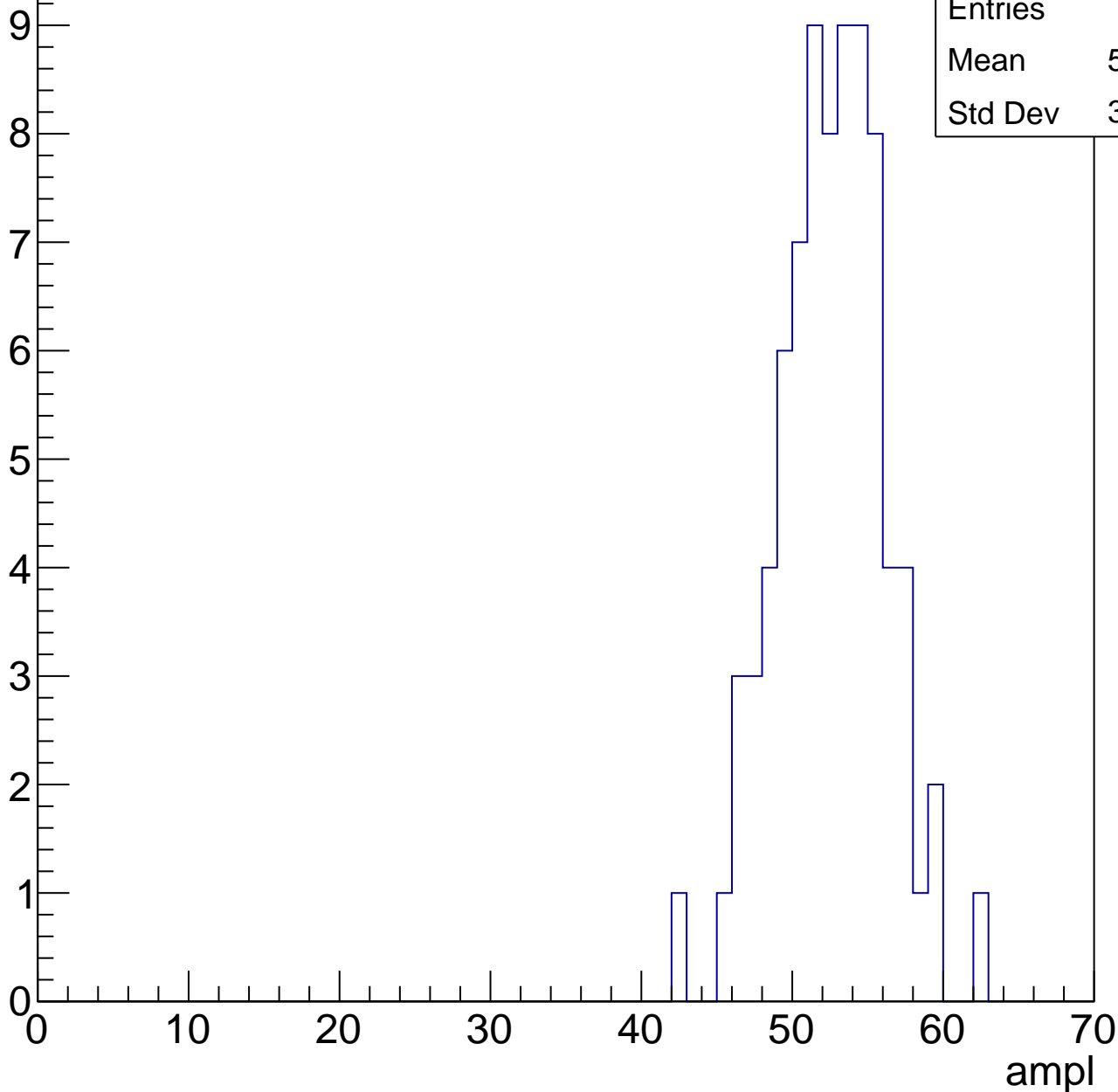


B1L101S, U25-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

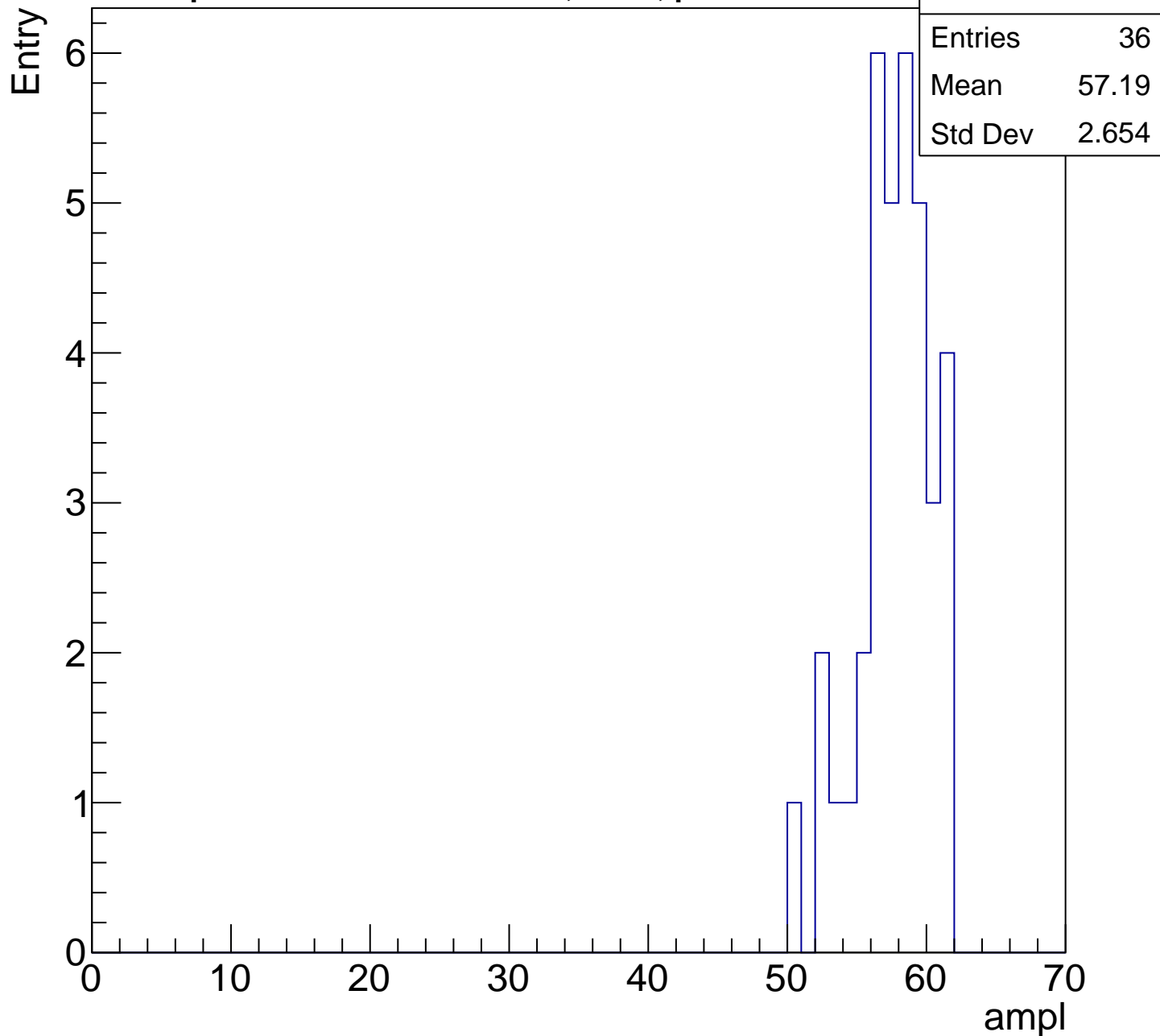
Entry

Entries	80
Mean	52.12
Std Dev	3.537



B1L101S, U25-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

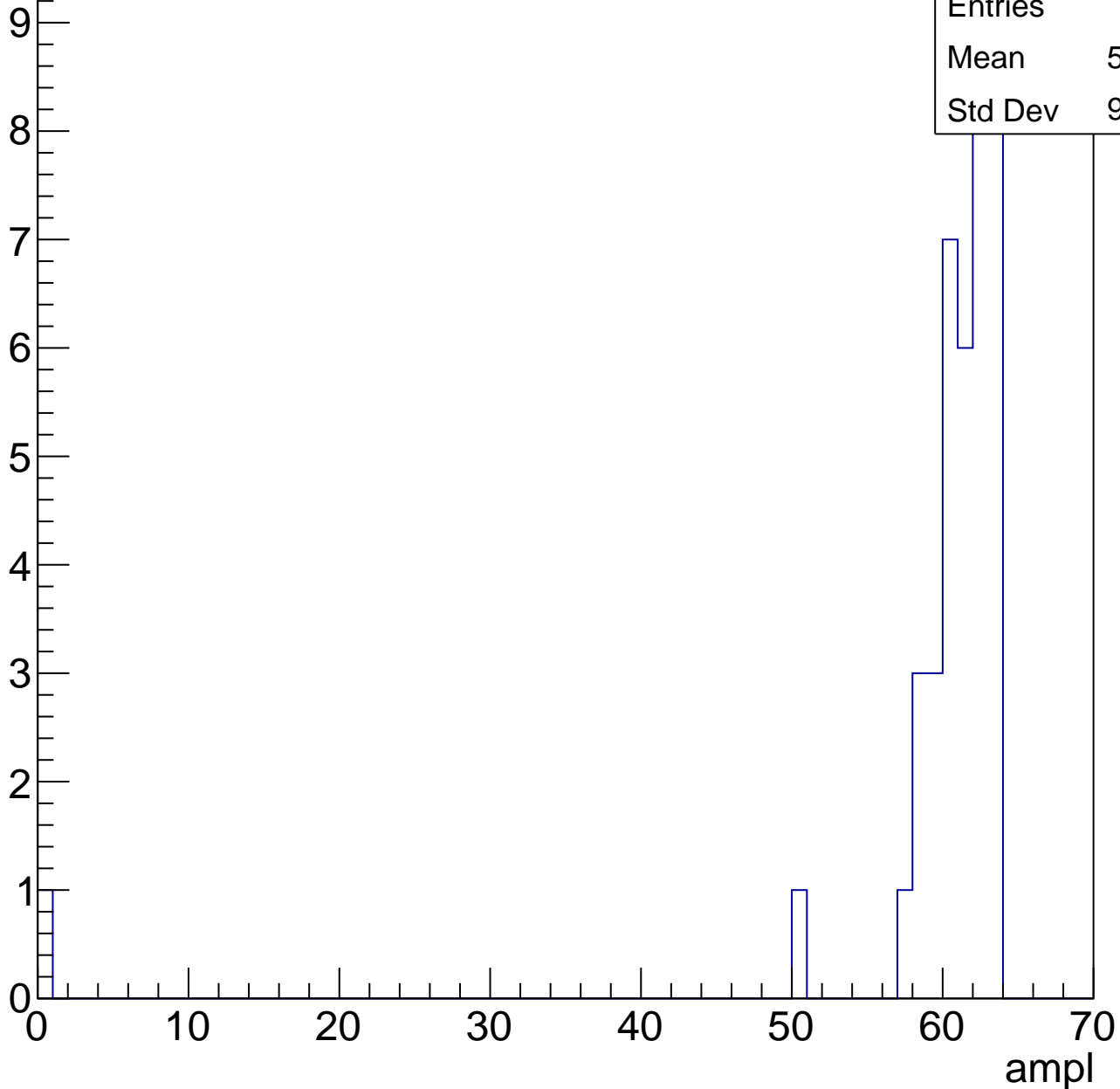


B1L101S, U25-ch81, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	59.13
Std Dev	9.882



B1L101S, U25-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch82, adc0

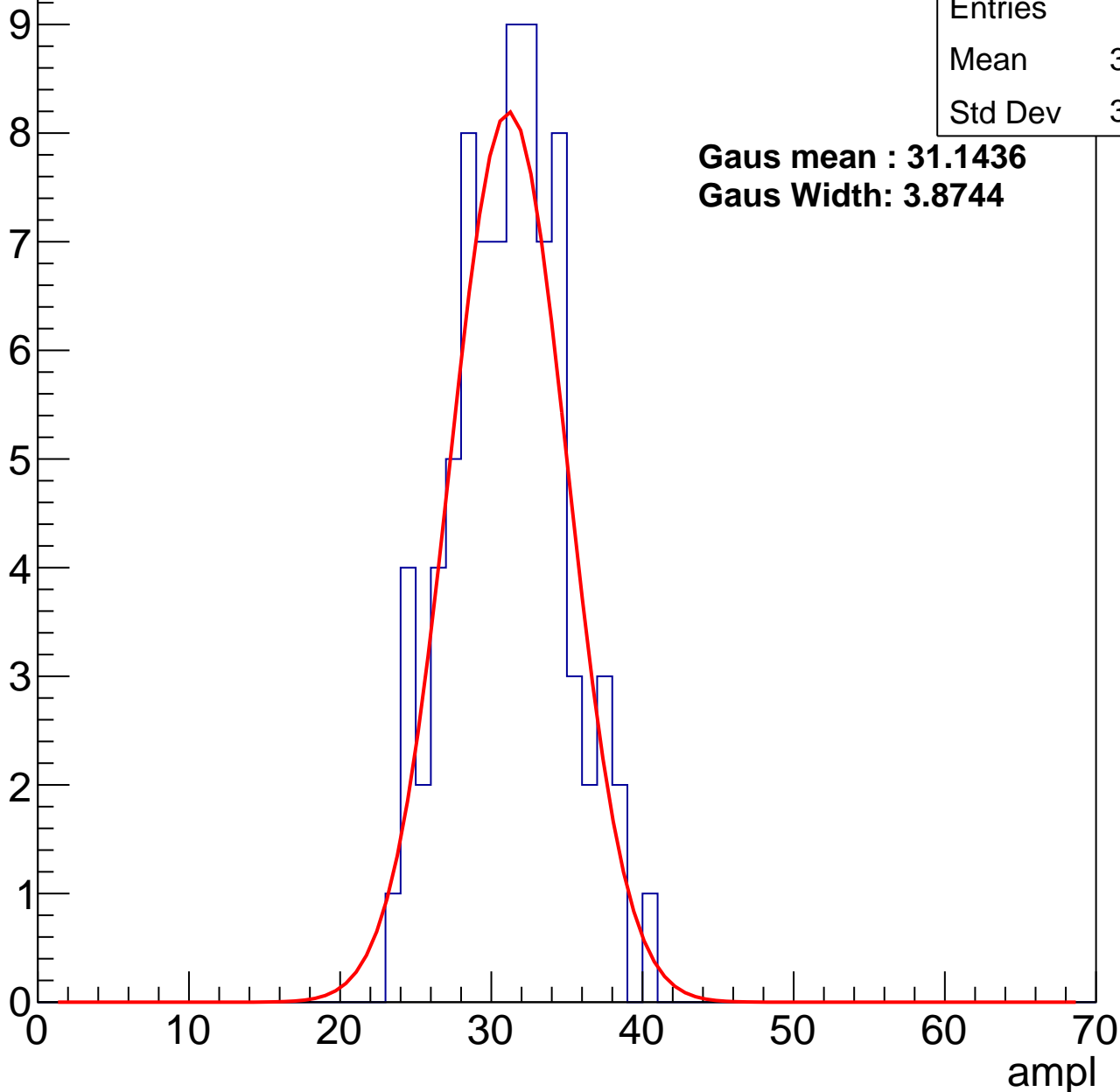
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	30.72
Std Dev	3.673

Gaus mean : 31.1436

Gaus Width: 3.8744



B1L101S, U25-ch82, adc1

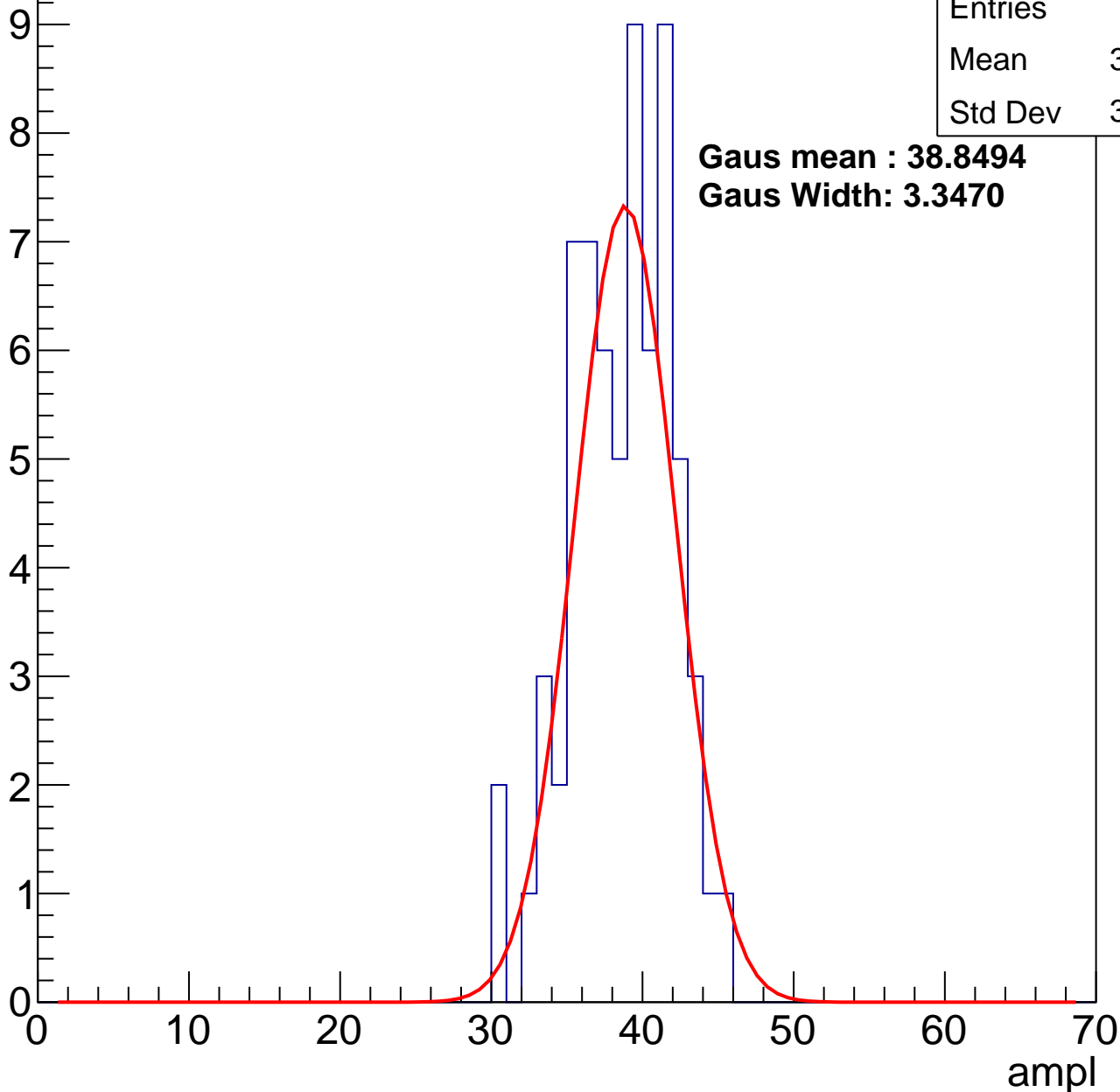
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	38.15
Std Dev	3.279

Gaus mean : 38.8494

Gaus Width: 3.3470



B1L101S, U25-ch82, adc2

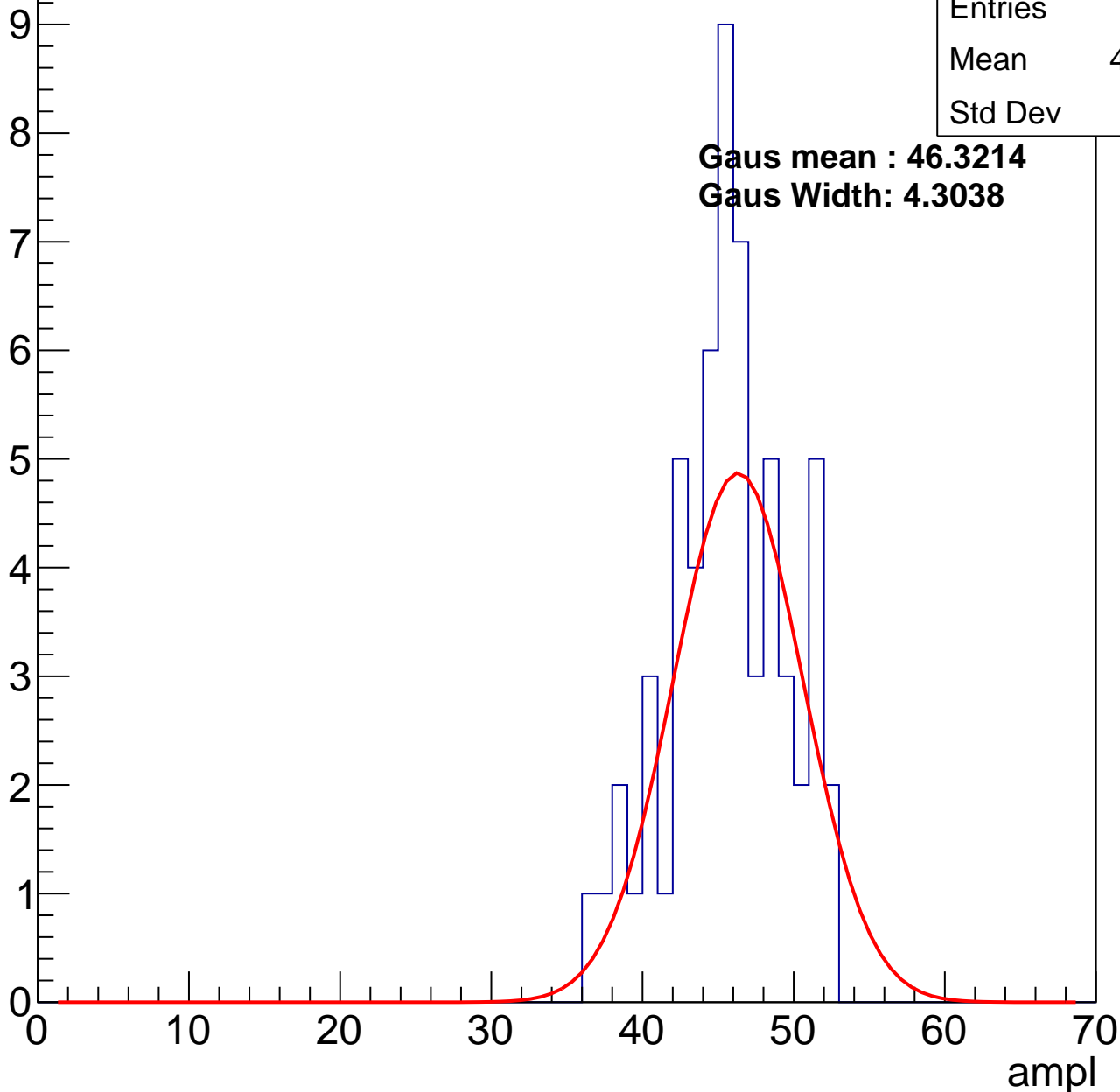
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	45.15
Std Dev	3.82

Gaus mean : 46.3214

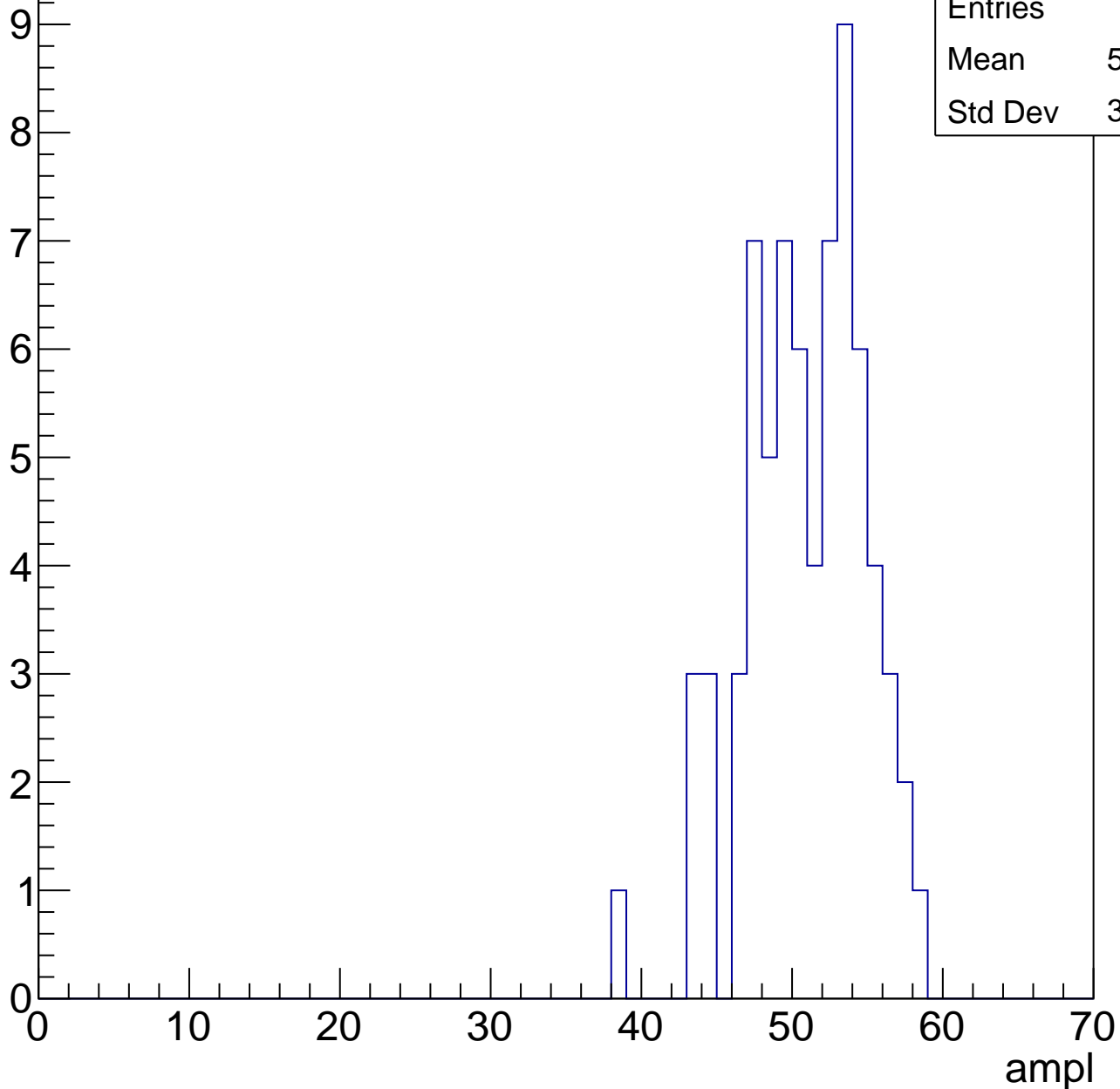
Gaus Width: 4.3038



B1L101S, U25-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

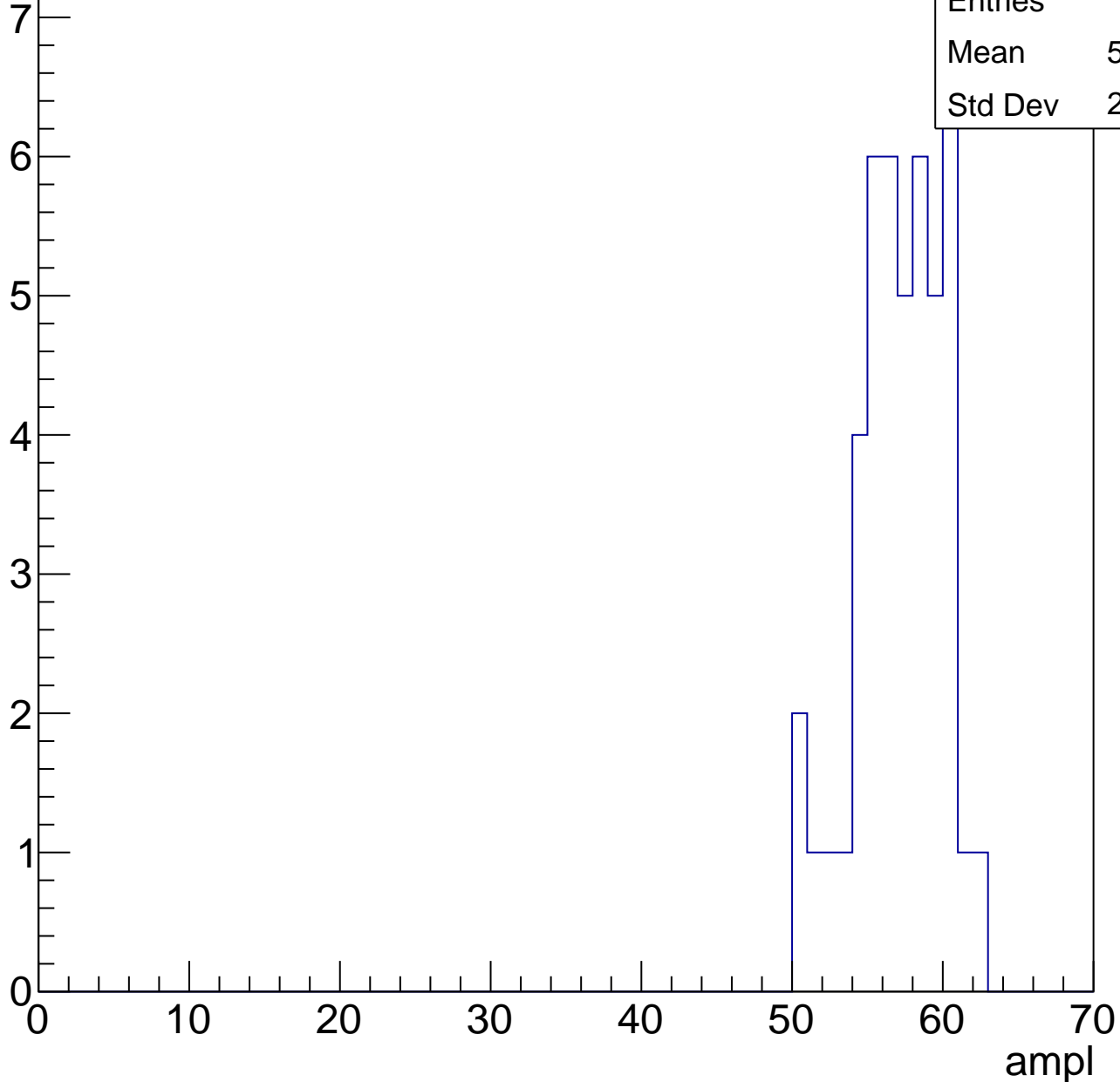
Entry



B1L101S, U25-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



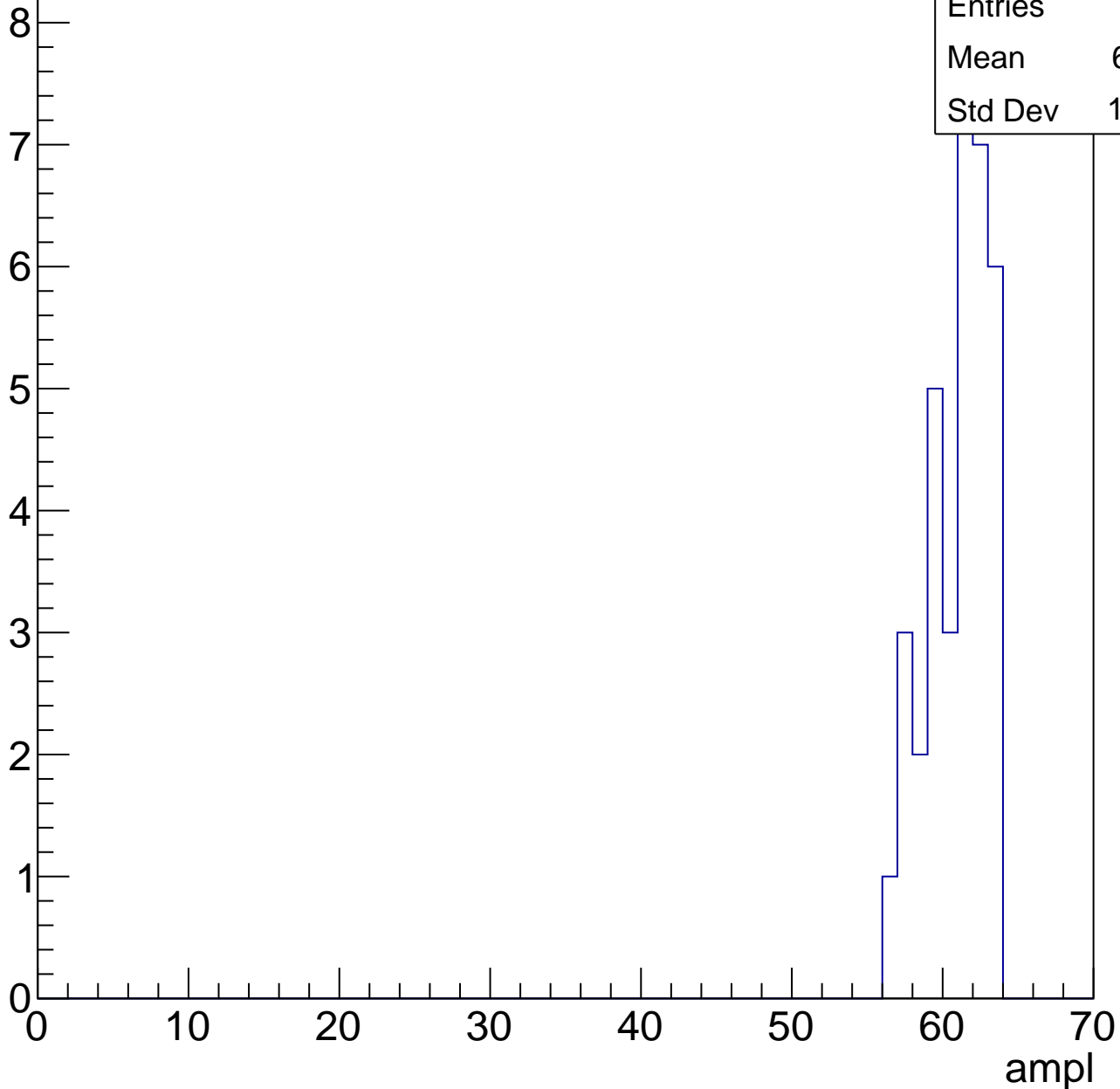
Entries	46
Mean	56.72
Std Dev	2.826

B1L101S, U25-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

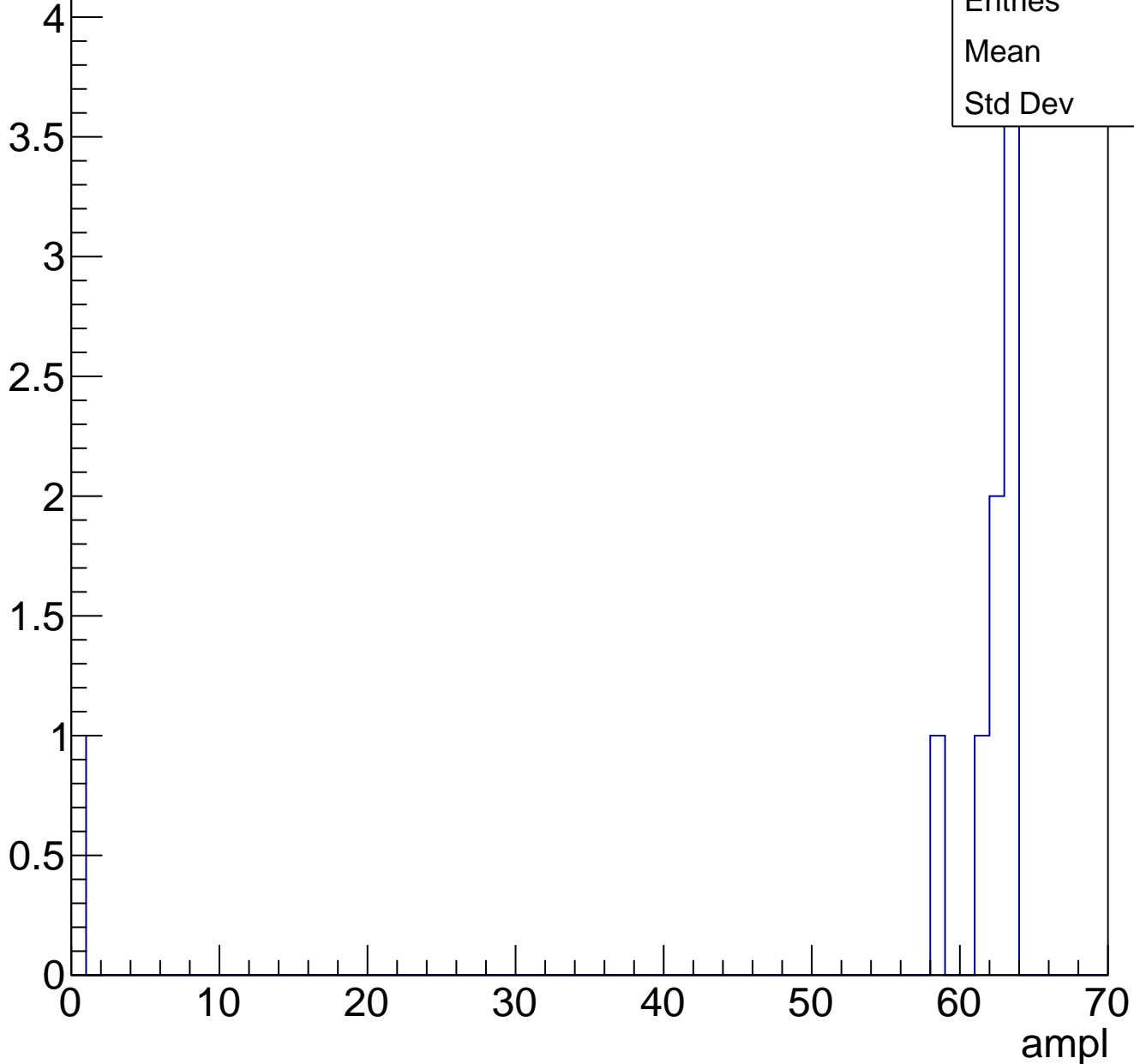
Entries	35
Mean	60.51
Std Dev	1.977



B1L101S, U25-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	9
Mean	55
Std Dev	19.5

B1L101S, U25-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch83, adc0

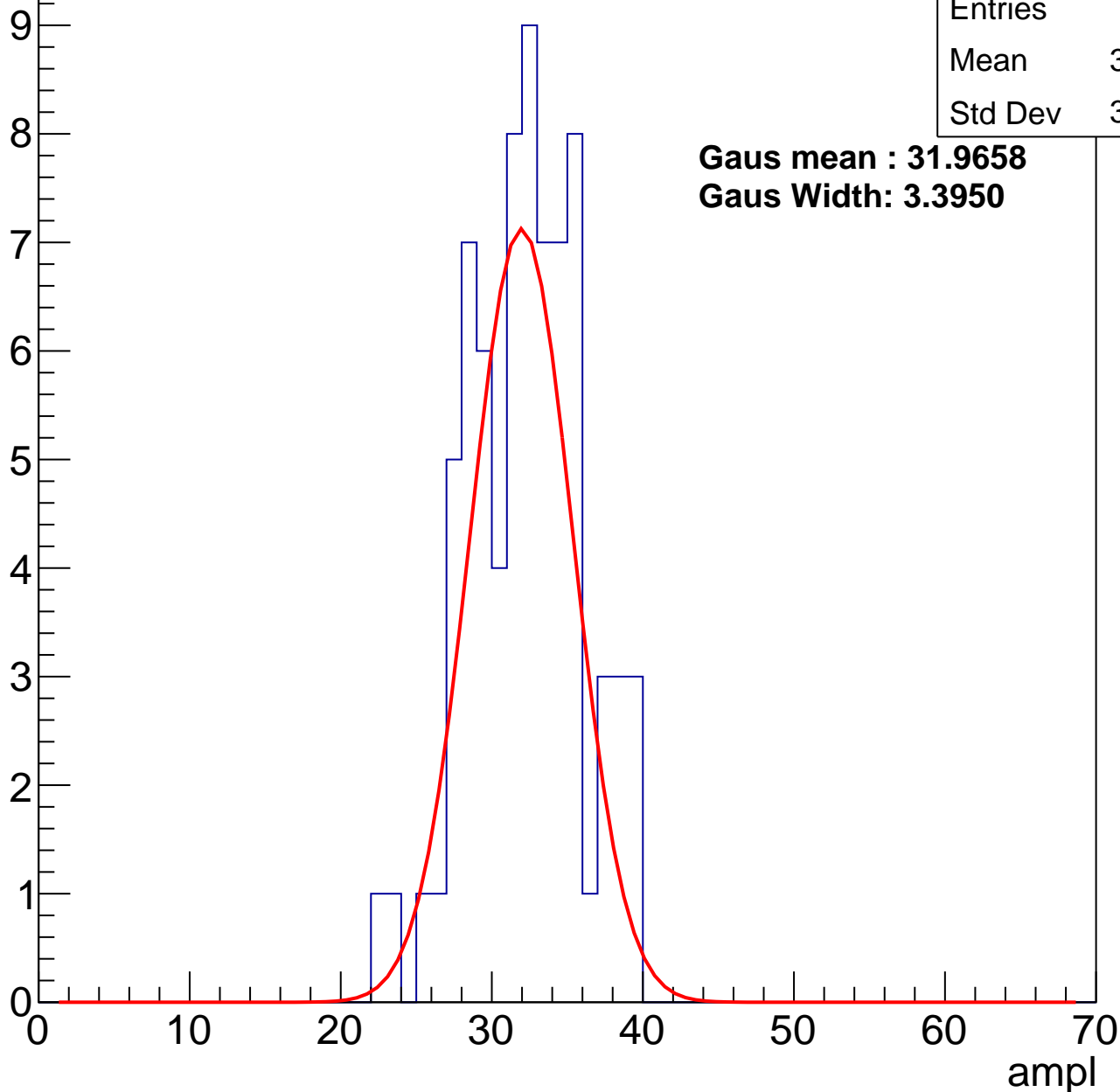
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	31.79
Std Dev	3.707

Gaus mean : 31.9658

Gaus Width: 3.3950



B1L101S, U25-ch83, adc1

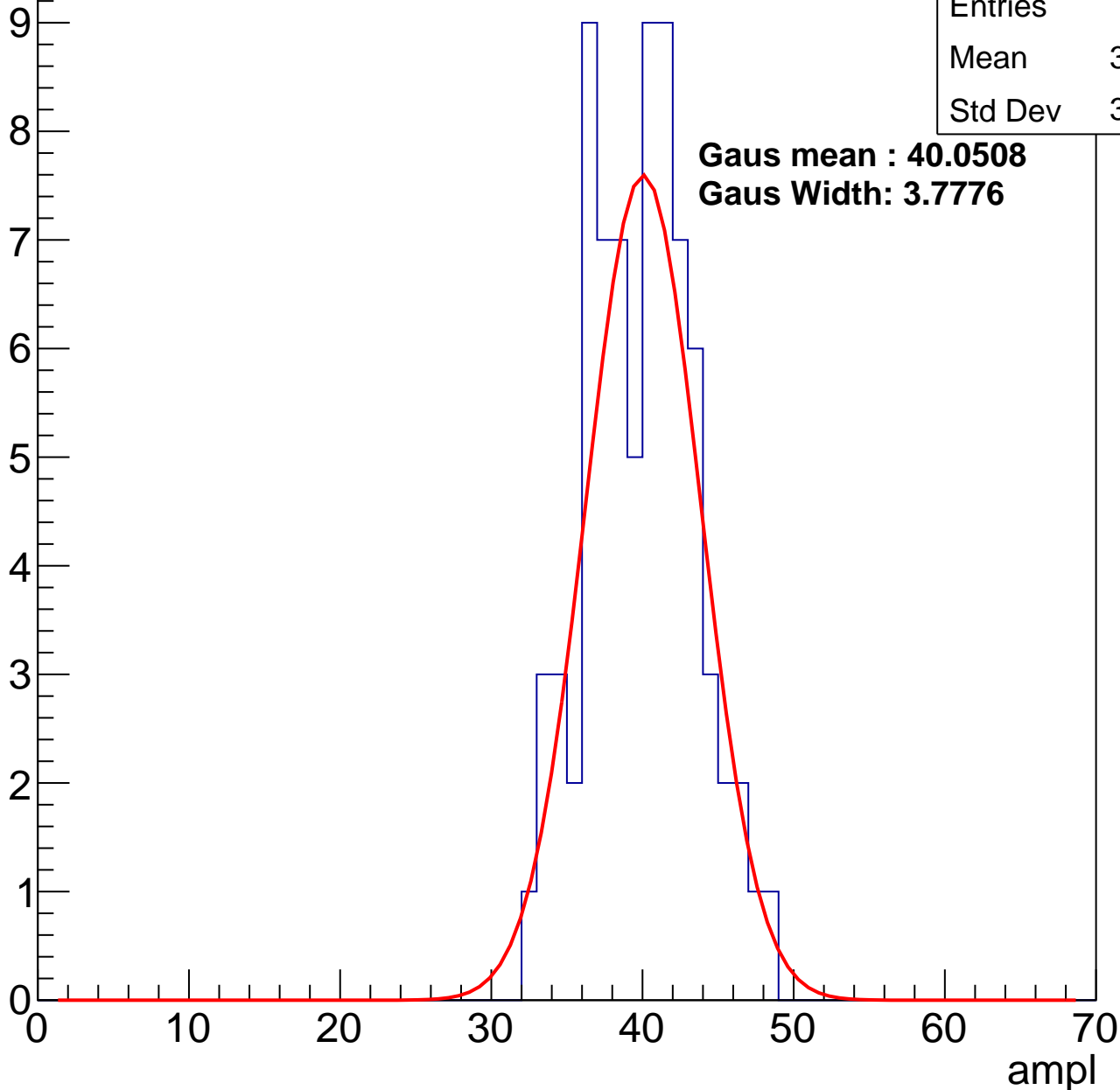
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	39.44
Std Dev	3.529

Gaus mean : 40.0508

Gaus Width: 3.7776



B1L101S, U25-ch83, adc2

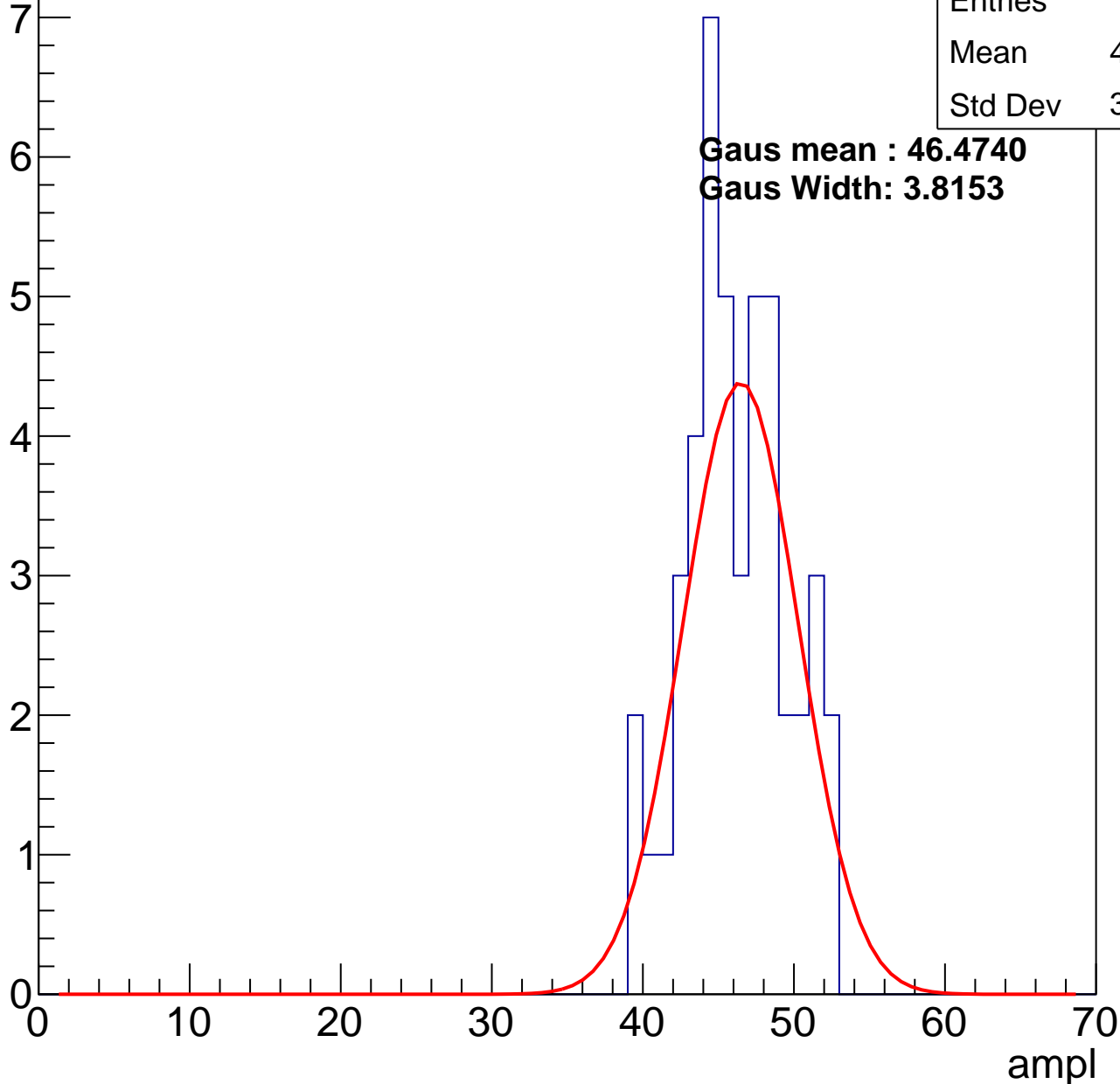
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	45.73
Std Dev	3.316

Gaus mean : 46.4740

Gaus Width: 3.8153

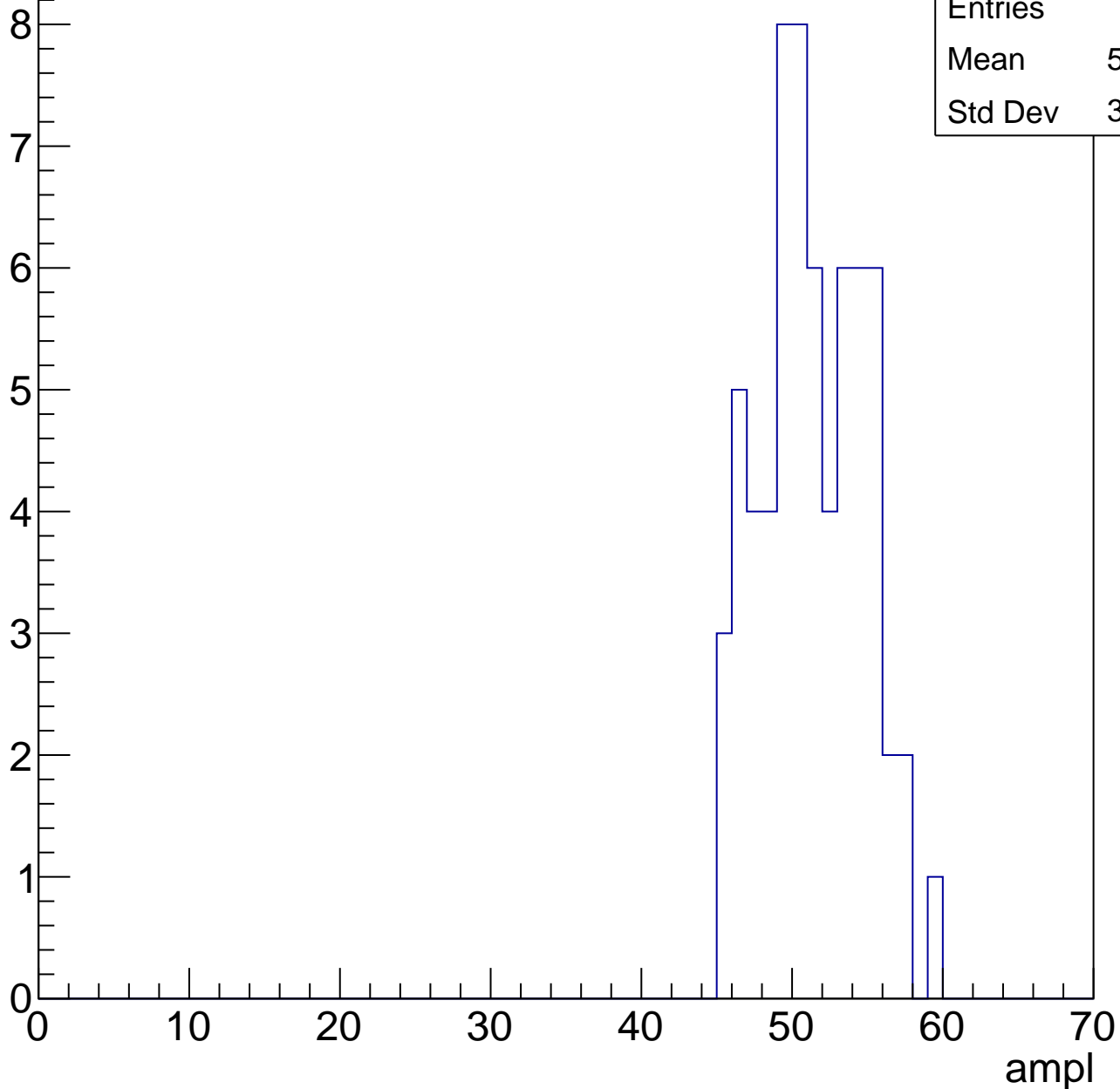


B1L101S, U25-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	50.89
Std Dev	3.366

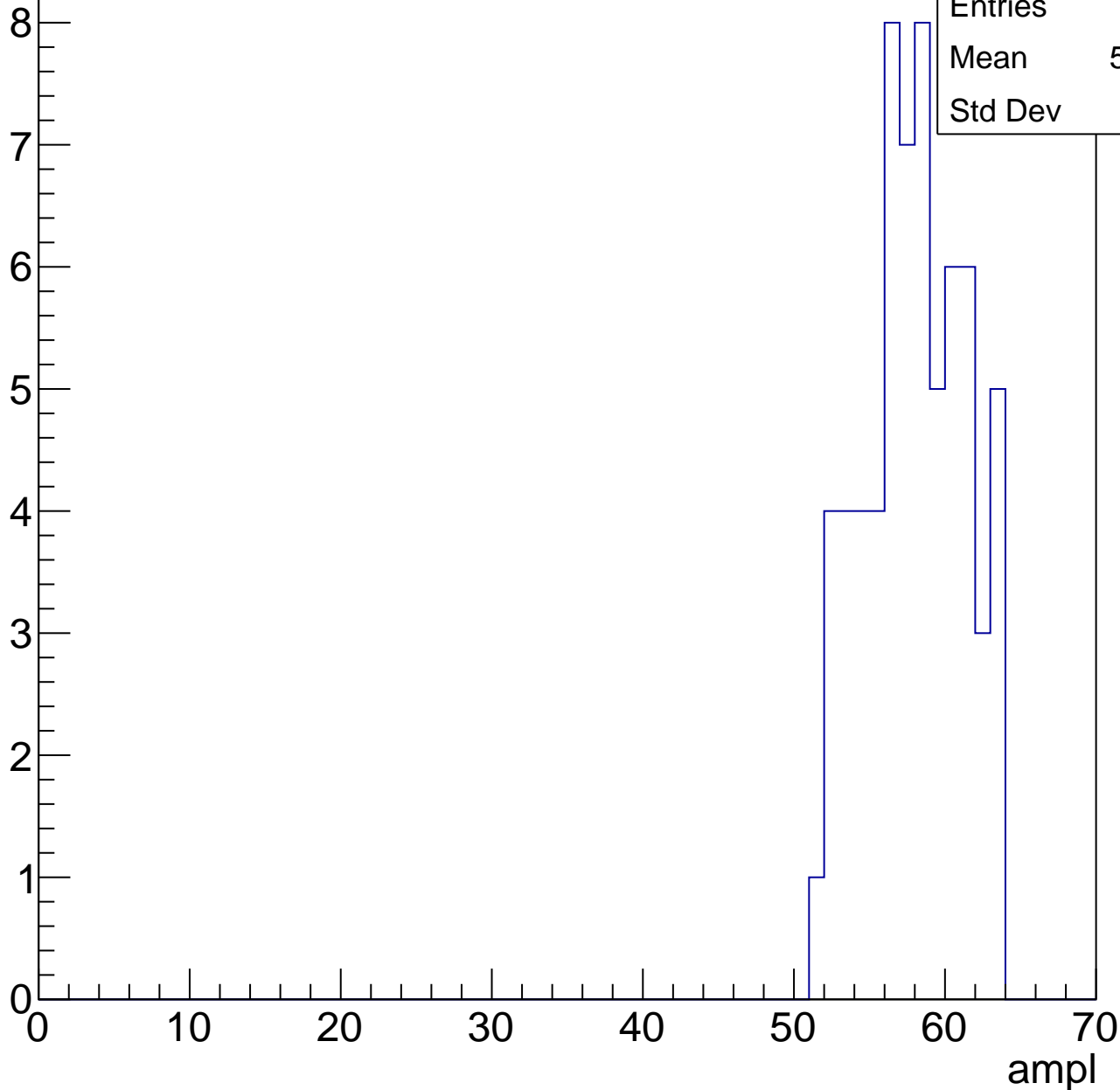


B1L101S, U25-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	57.54
Std Dev	3.22

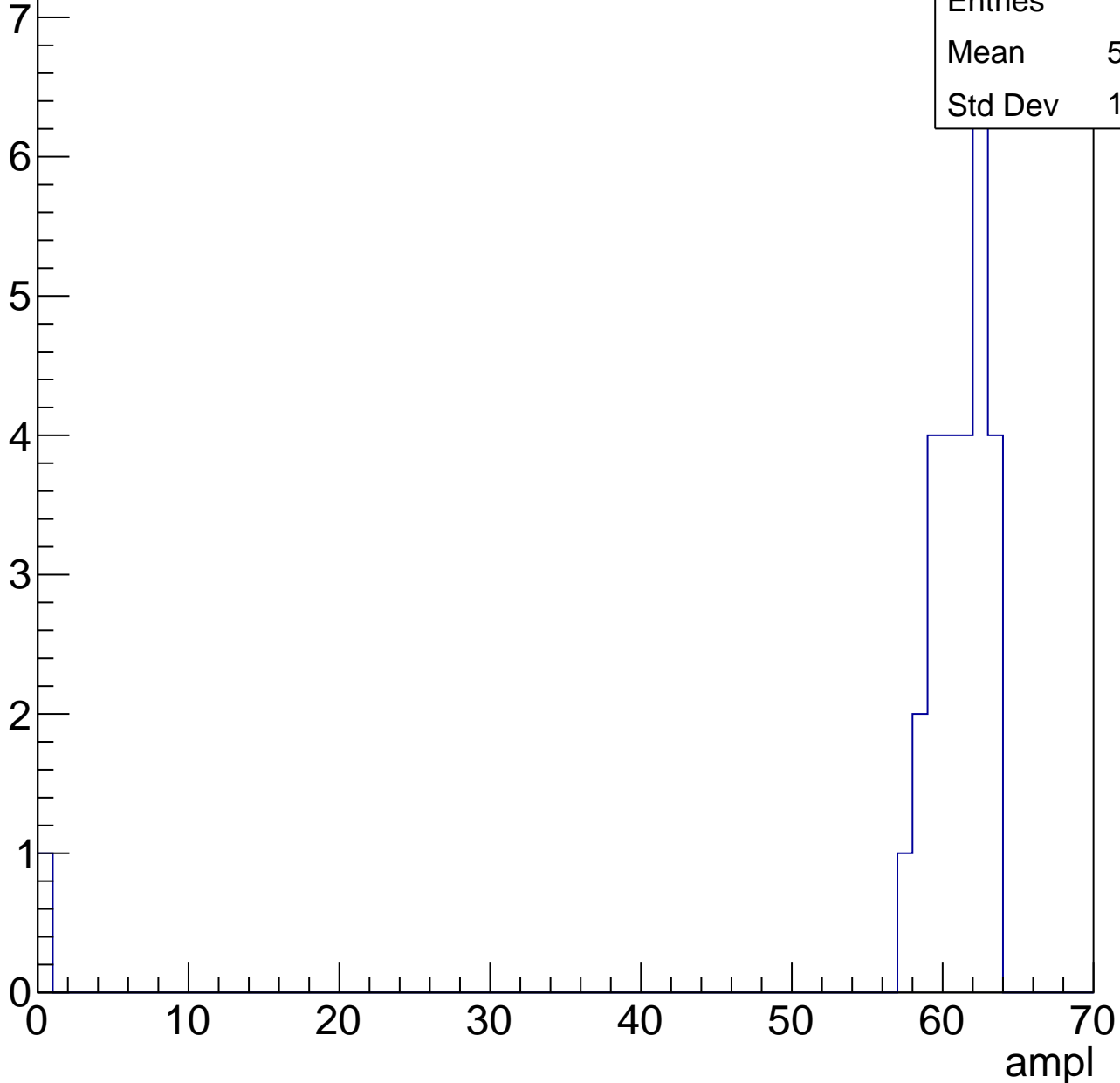


B1L101S, U25-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.48
Std Dev	11.59



B1L101S, U25-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U25-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch84, adc0

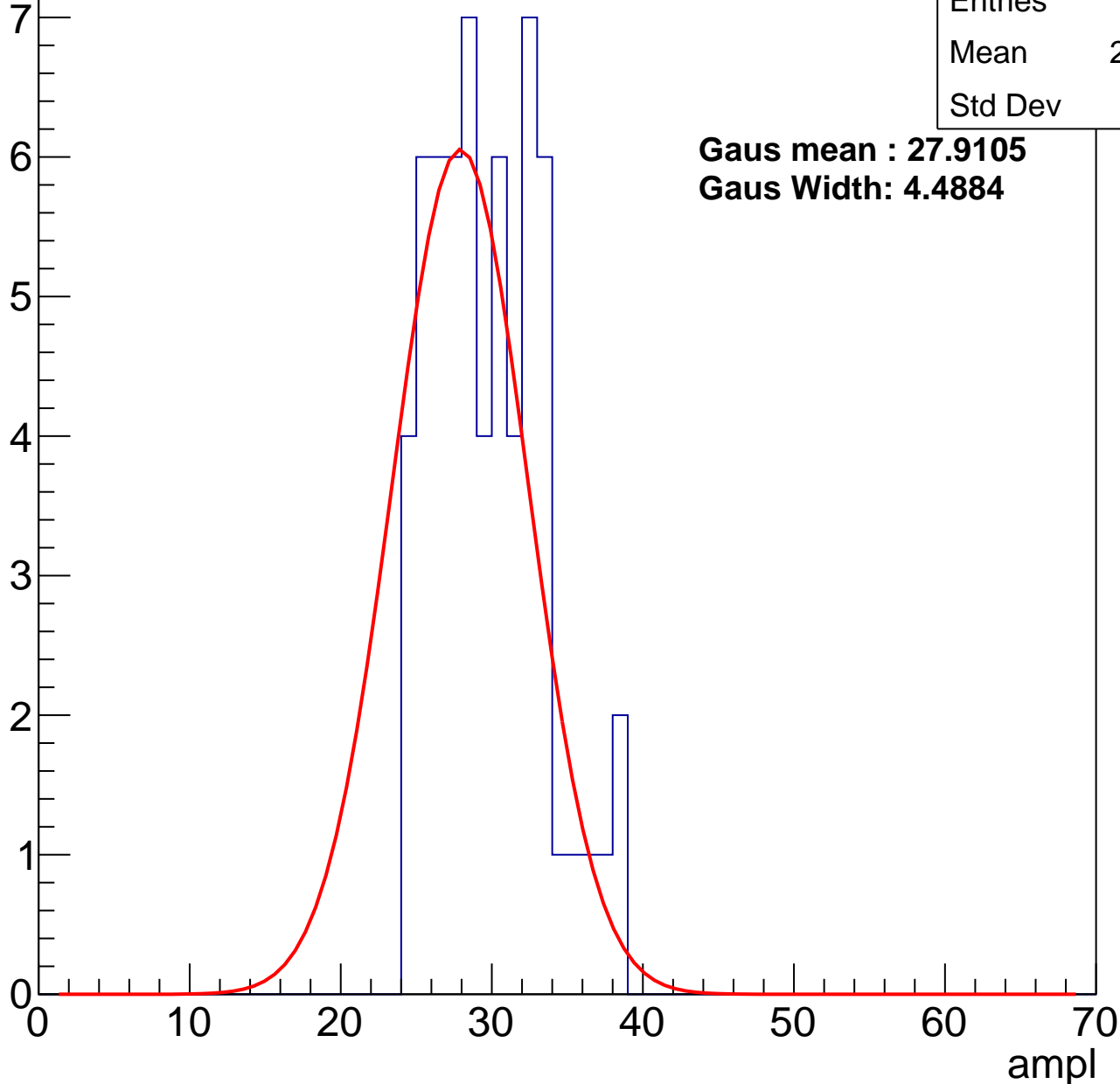
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.35
Std Dev	3.57

Gaus mean : 27.9105

Gaus Width: 4.4884



B1L101S, U25-ch84, adc1

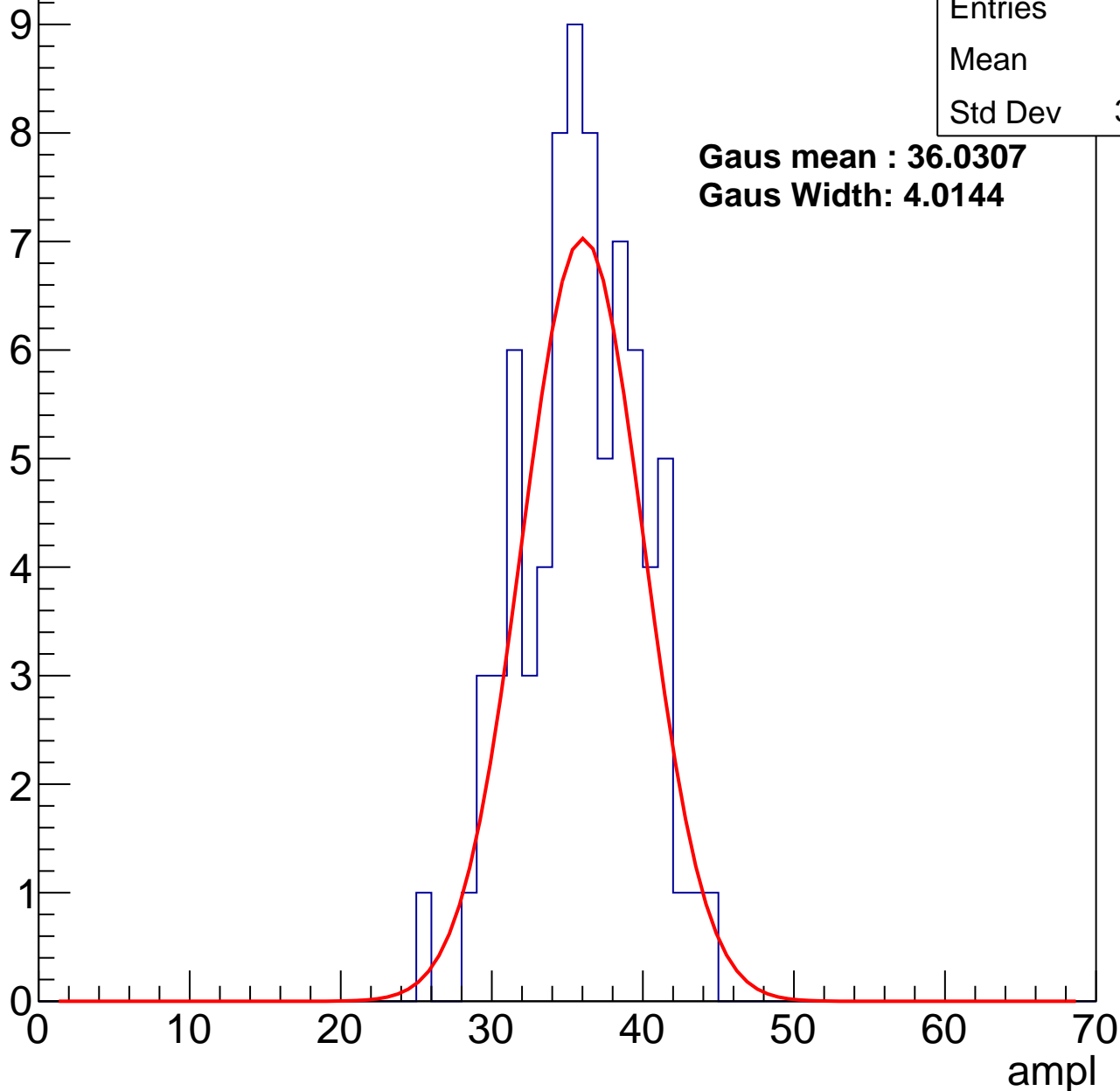
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.5
Std Dev	3.841

Gaus mean : 36.0307

Gaus Width: 4.0144



B1L101S, U25-ch84, adc2

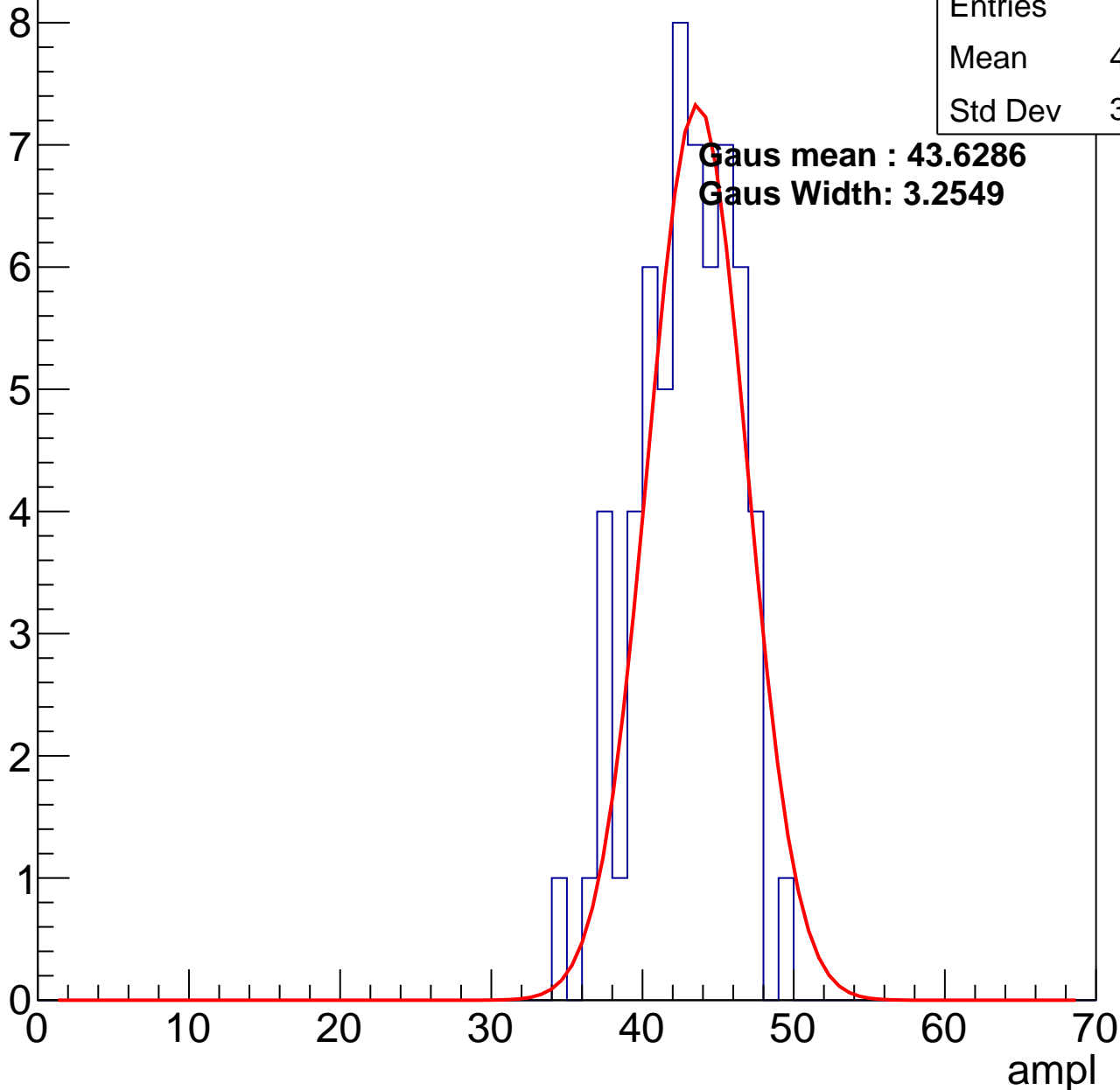
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.39
Std Dev	3.164

Gaus mean : 43.6286

Gaus Width: 3.2549

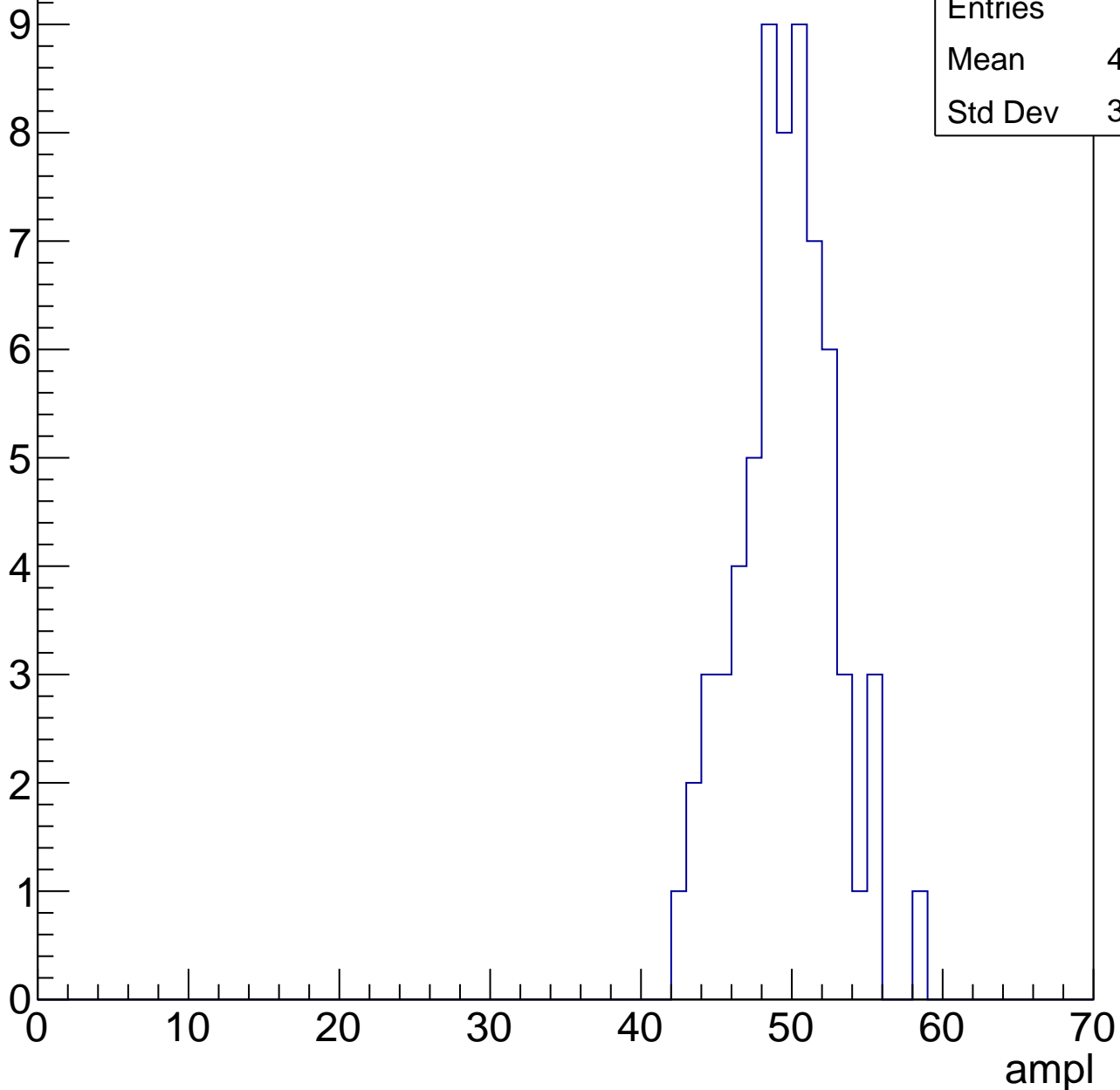


B1L101S, U25-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.12
Std Dev	3.189

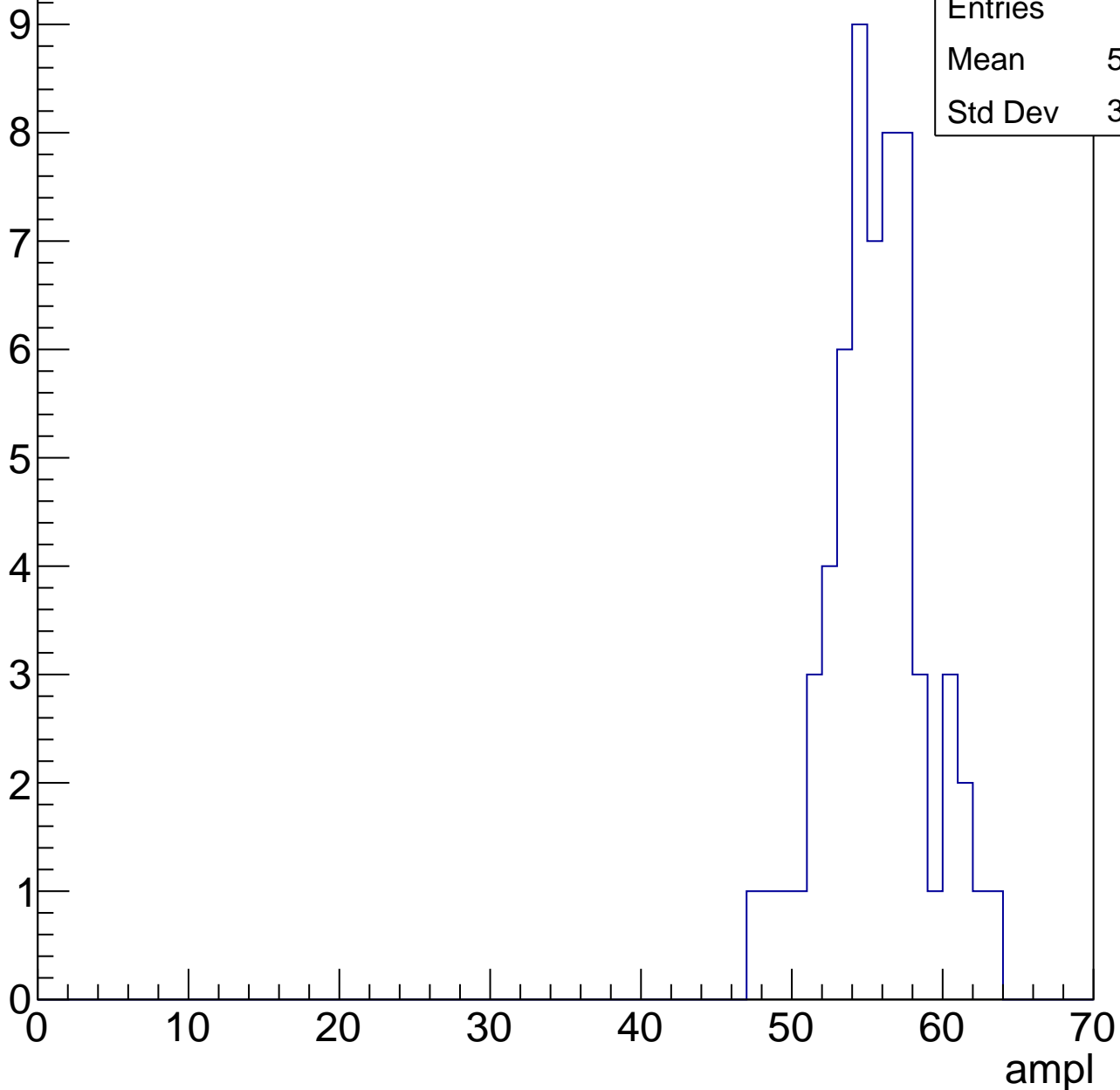


B1L101S, U25-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.13
Std Dev	3.248

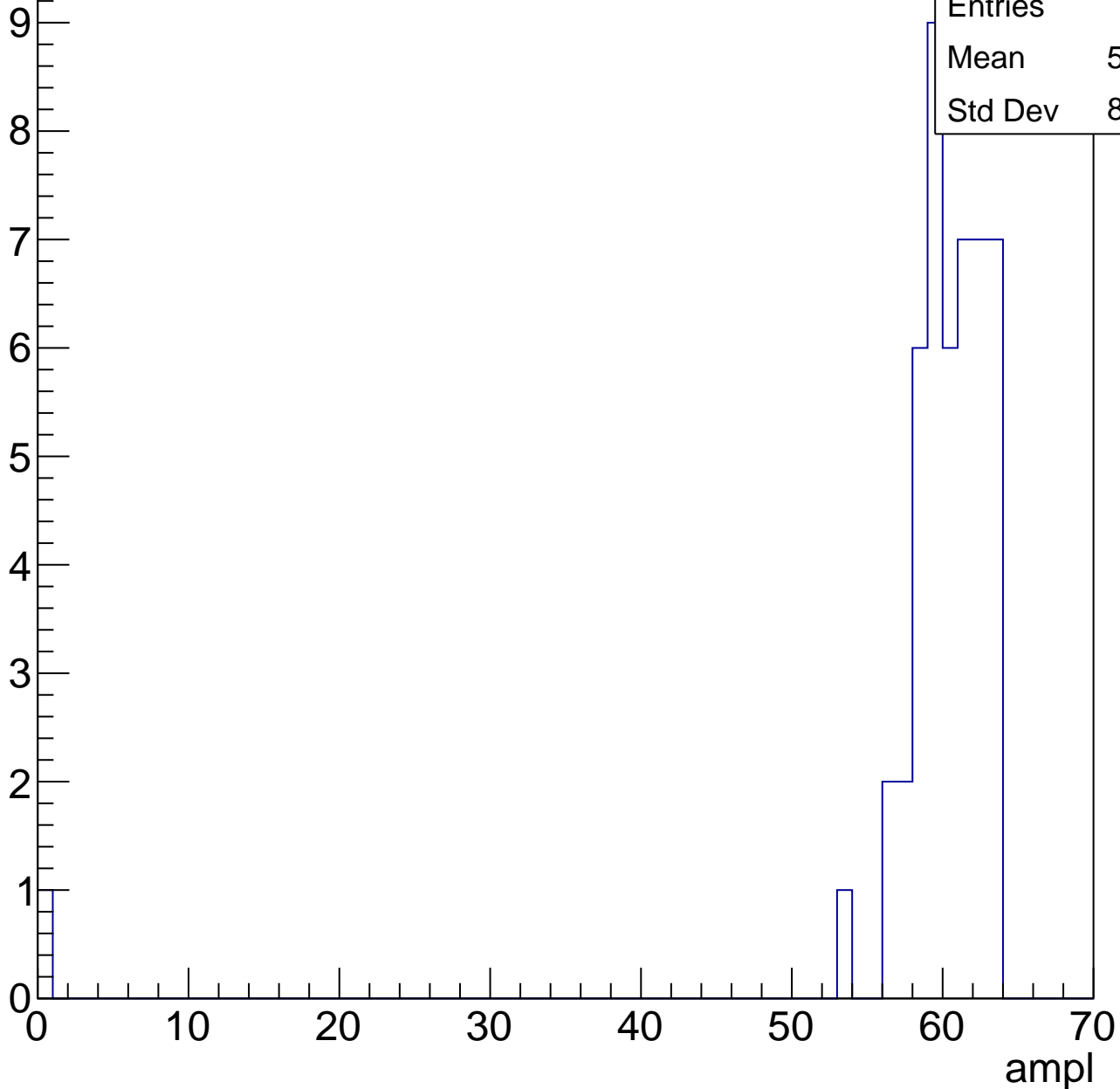


B1L101S, U25-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.75
Std Dev	8.845



B1L101S, U25-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

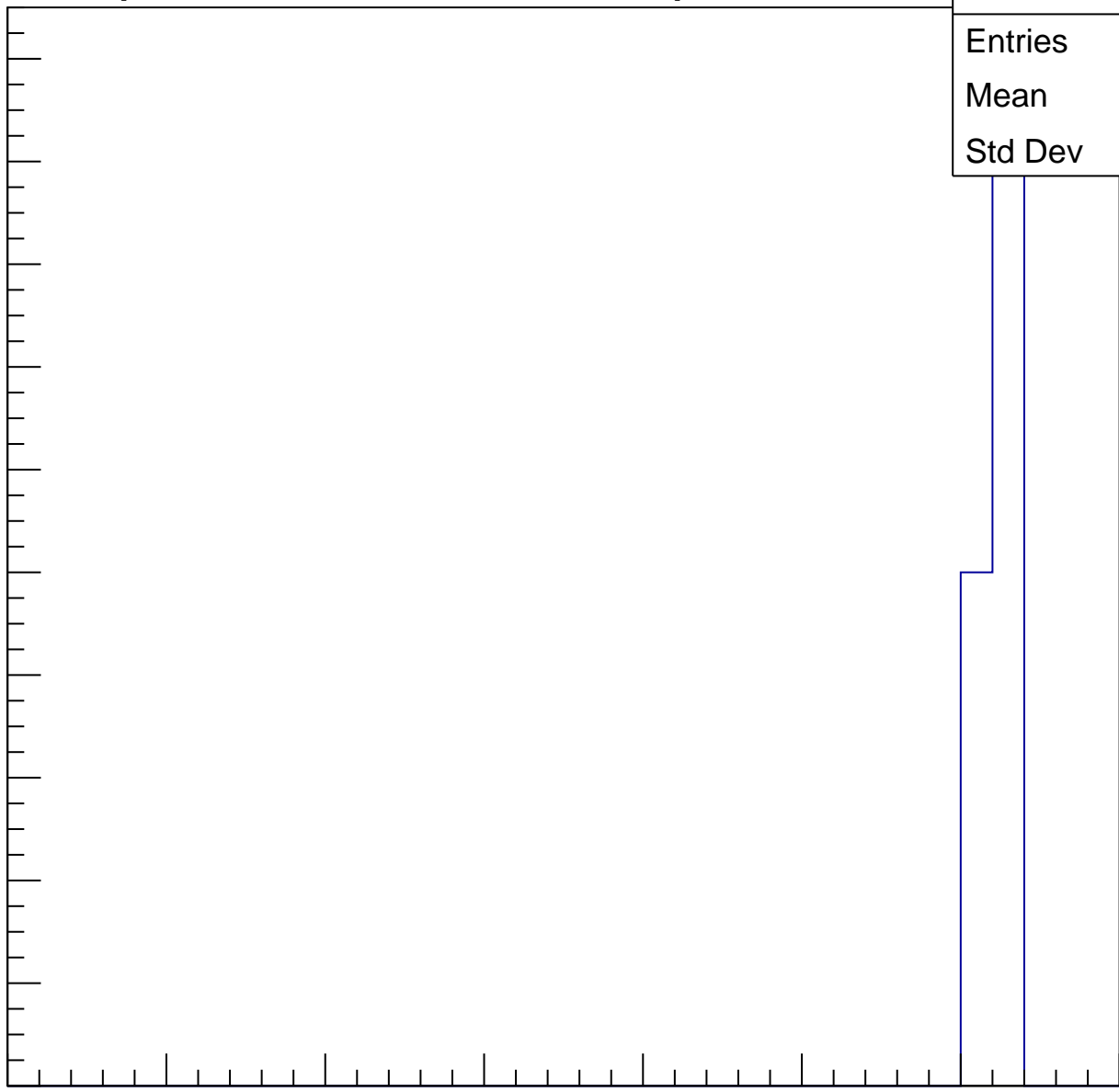
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L101S, U25-ch85, adc0

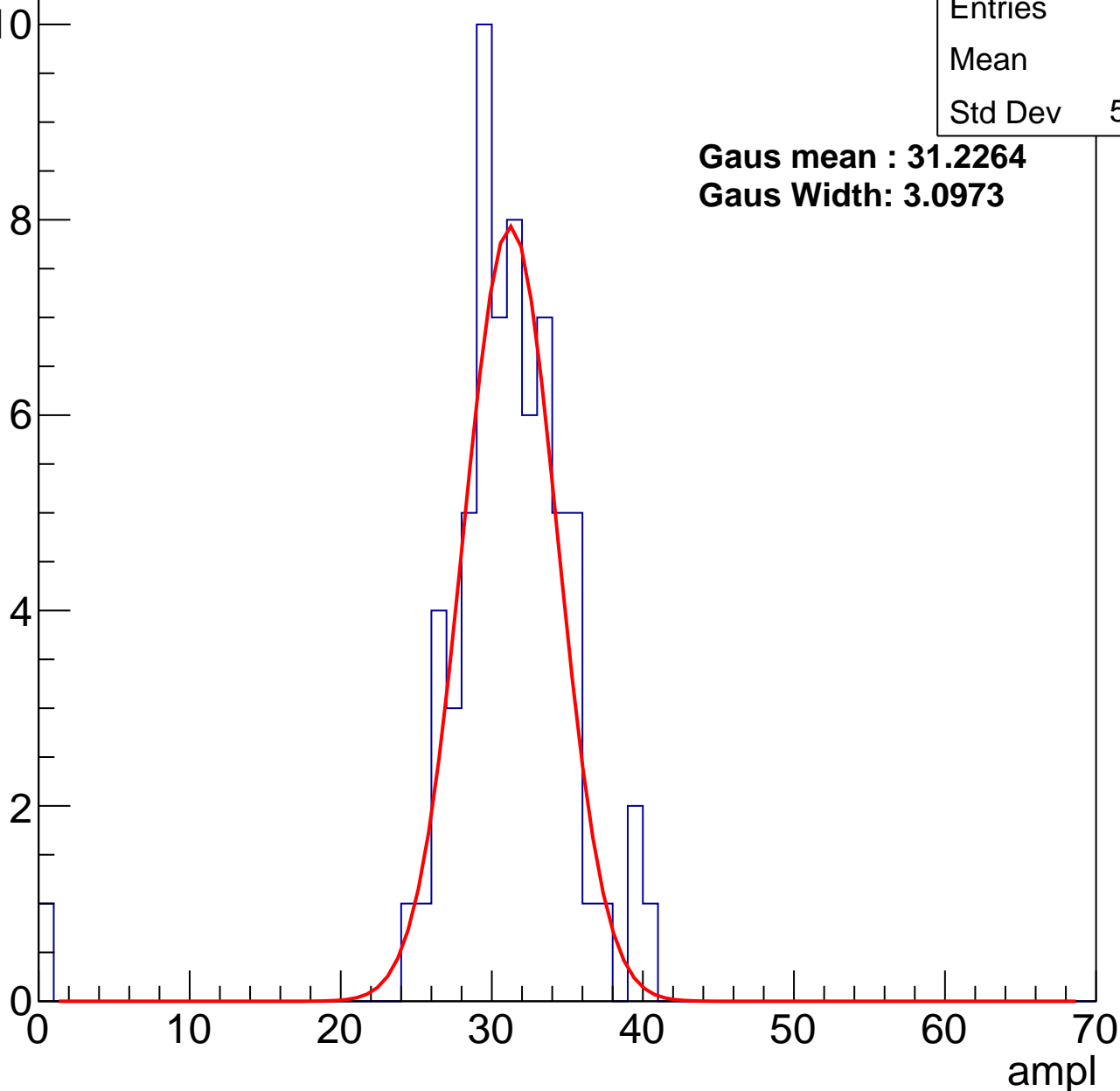
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.6
Std Dev	5.006

Gaus mean : 31.2264

Gaus Width: 3.0973



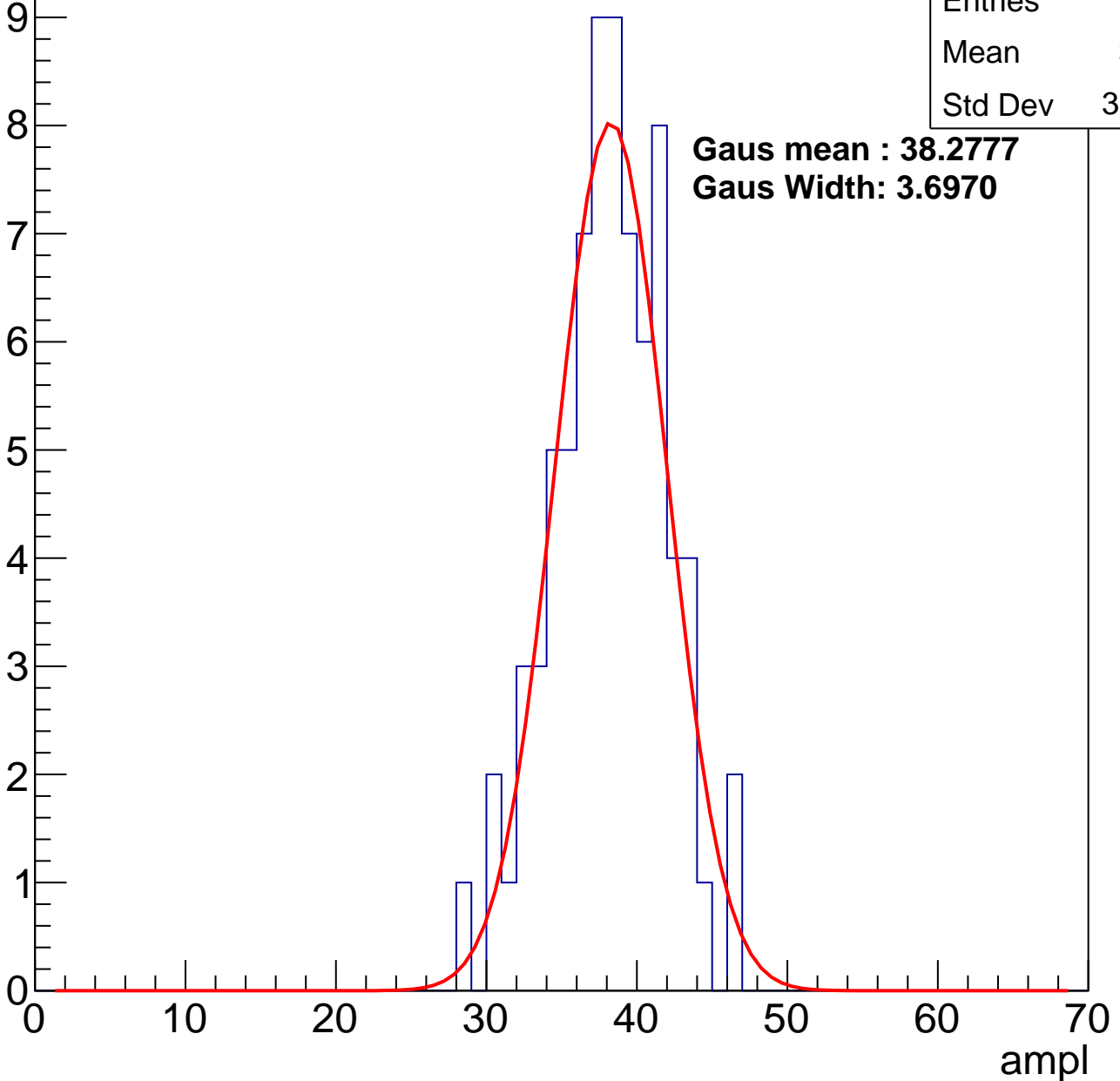
B1L101S, U25-ch85, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	37.7
Std Dev	3.686

Gaus mean : 38.2777
Gaus Width: 3.6970



B1L101S, U25-ch85, adc2

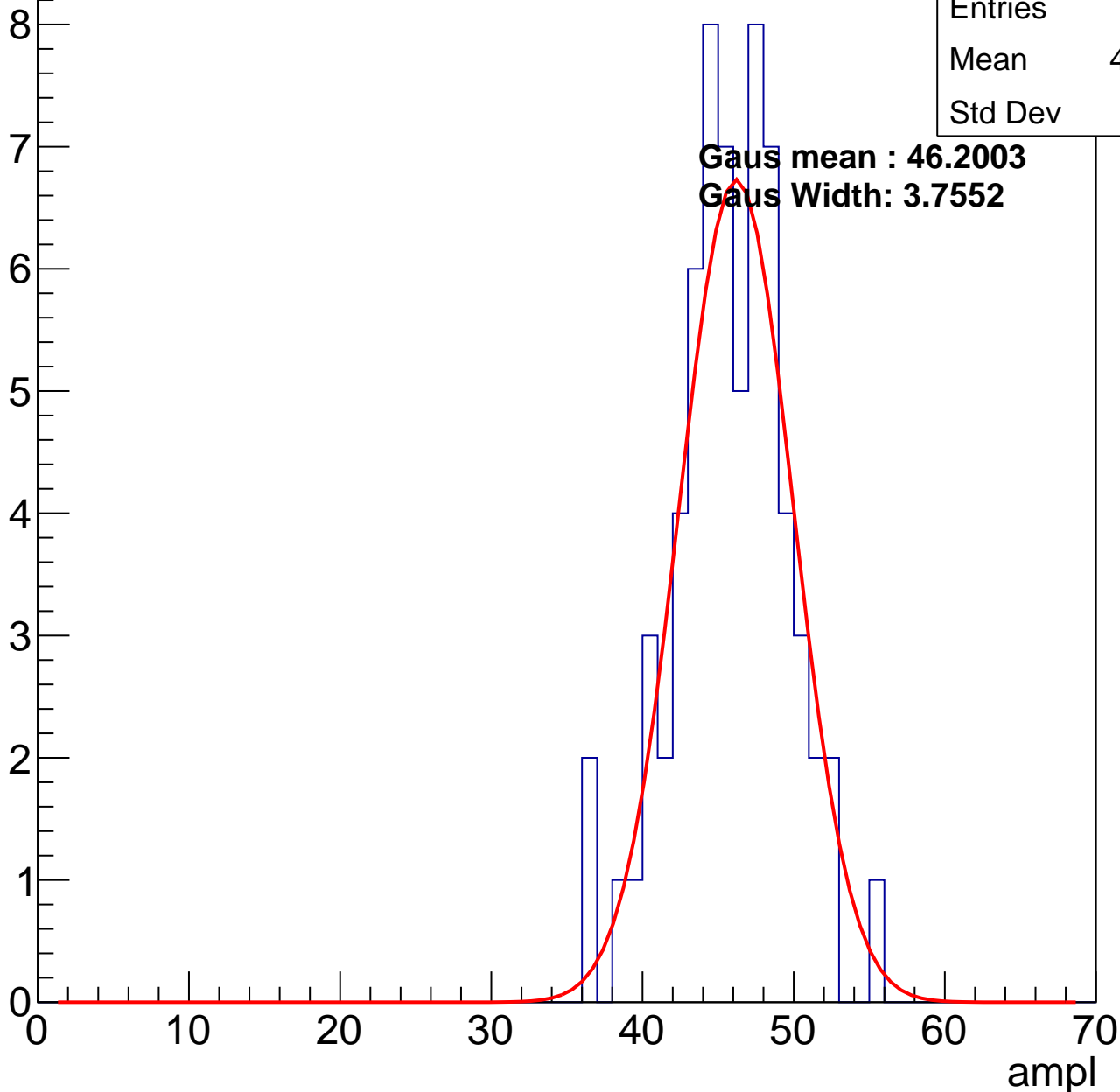
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	45.35
Std Dev	3.74

Gaus mean : 46.2003

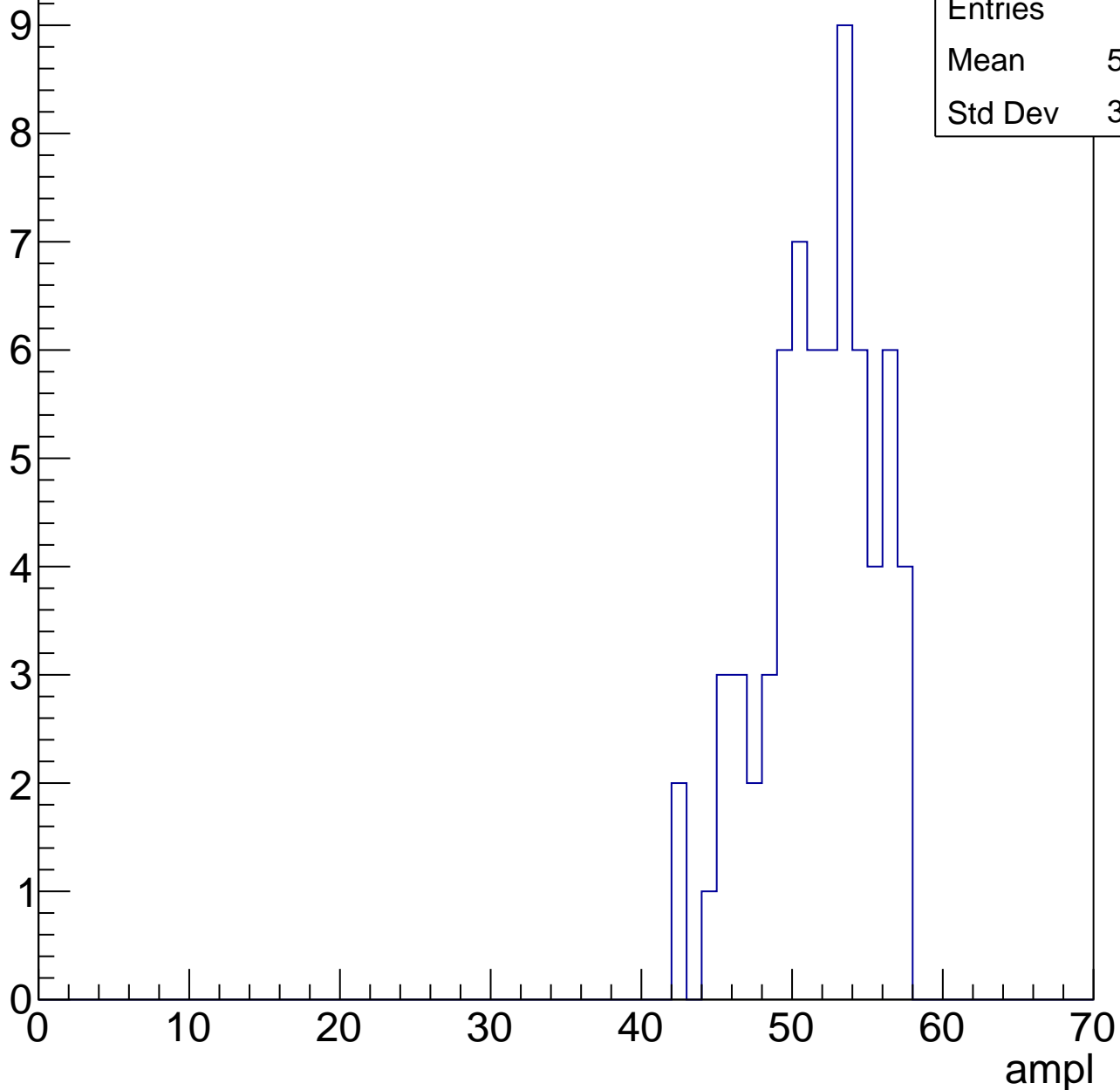
Gaus Width: 3.7552



B1L101S, U25-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	68
Mean	51.26
Std Dev	3.705

B1L101S, U25-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	57
Mean	57.95
Std Dev	2.97

Entry

10

8

6

4

2

0

0

10

20

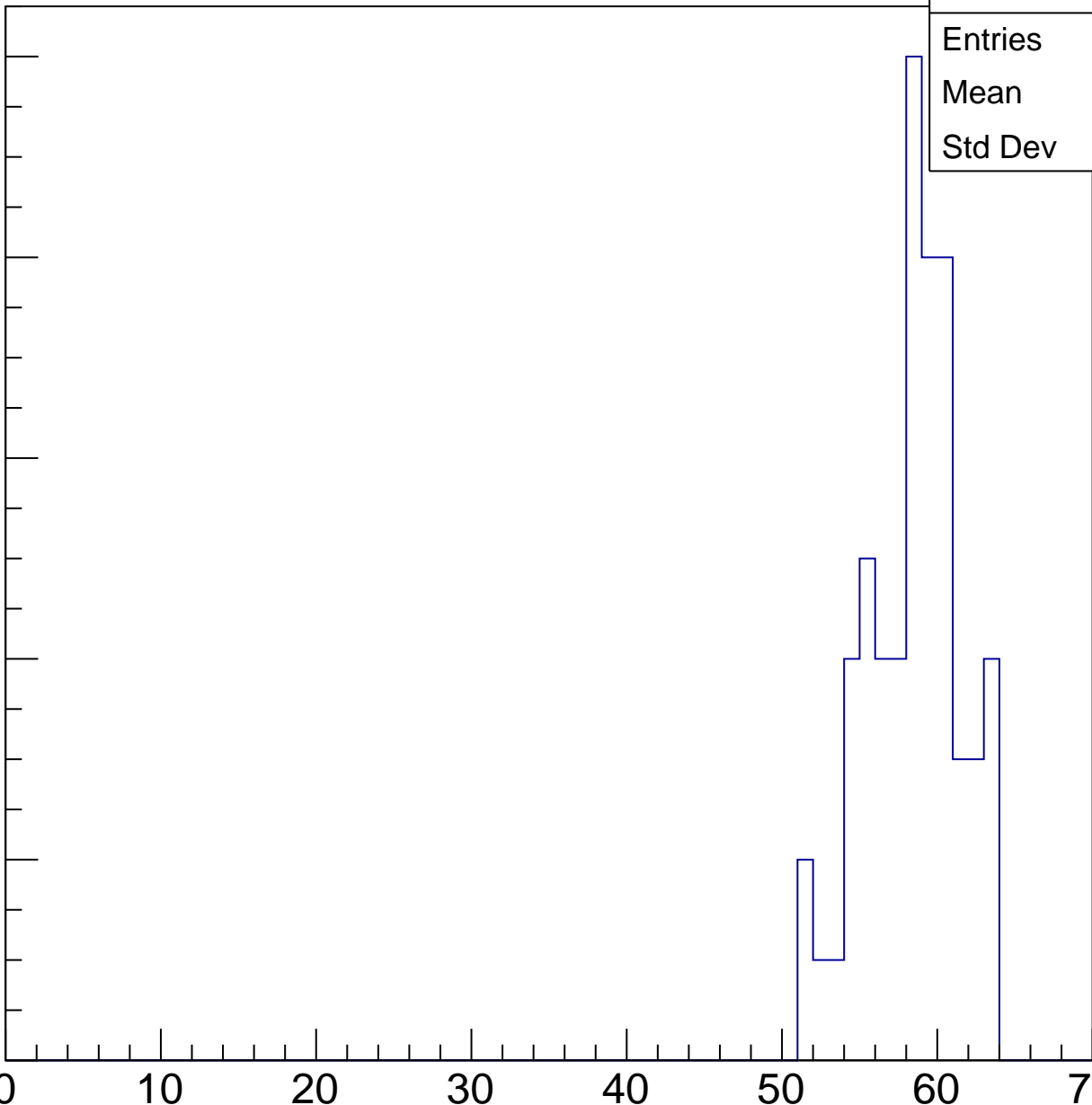
30

40

50

60

ampl

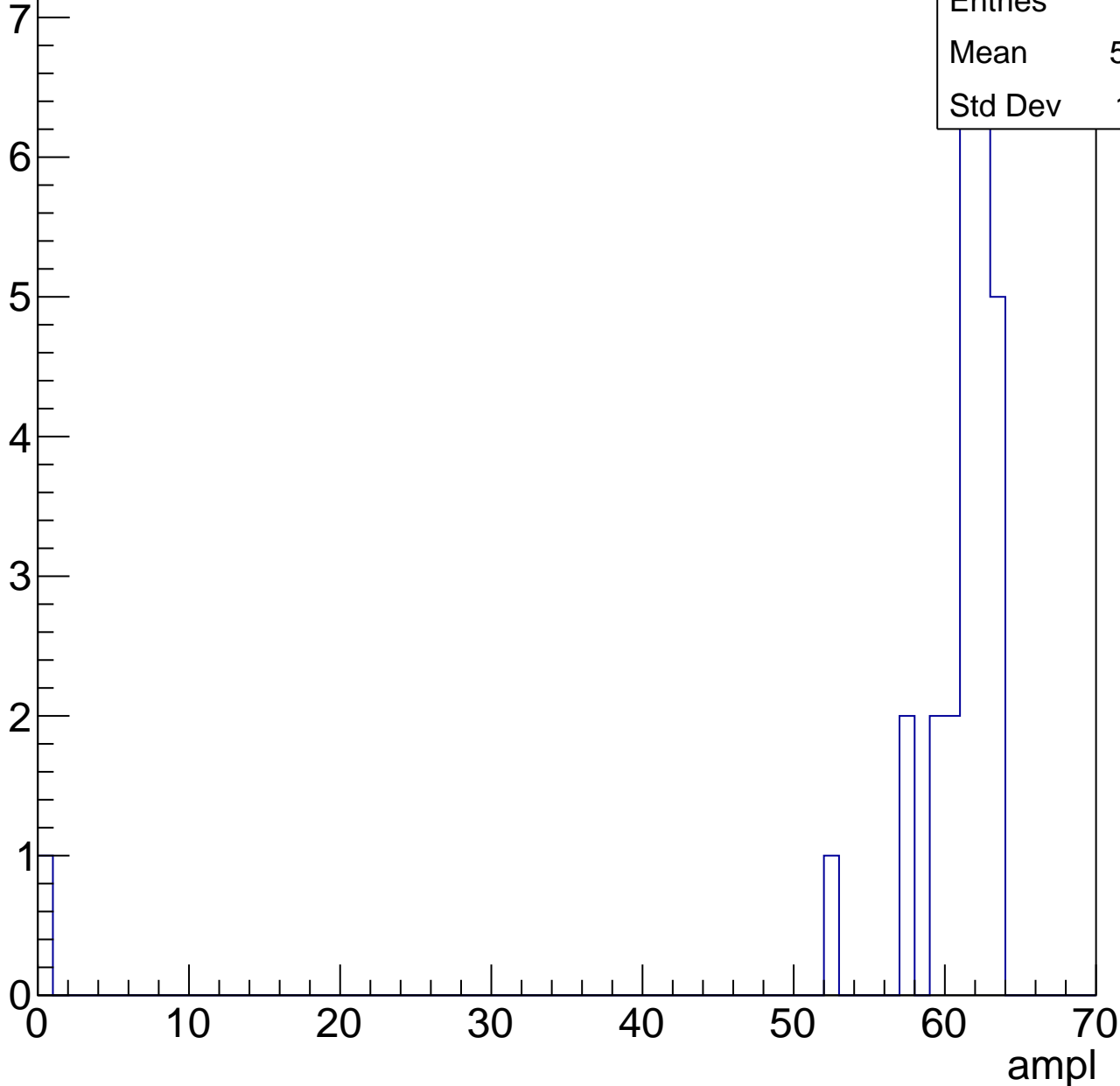


B1L101S, U25-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.52
Std Dev	11.71



B1L101S, U25-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch86, adc0

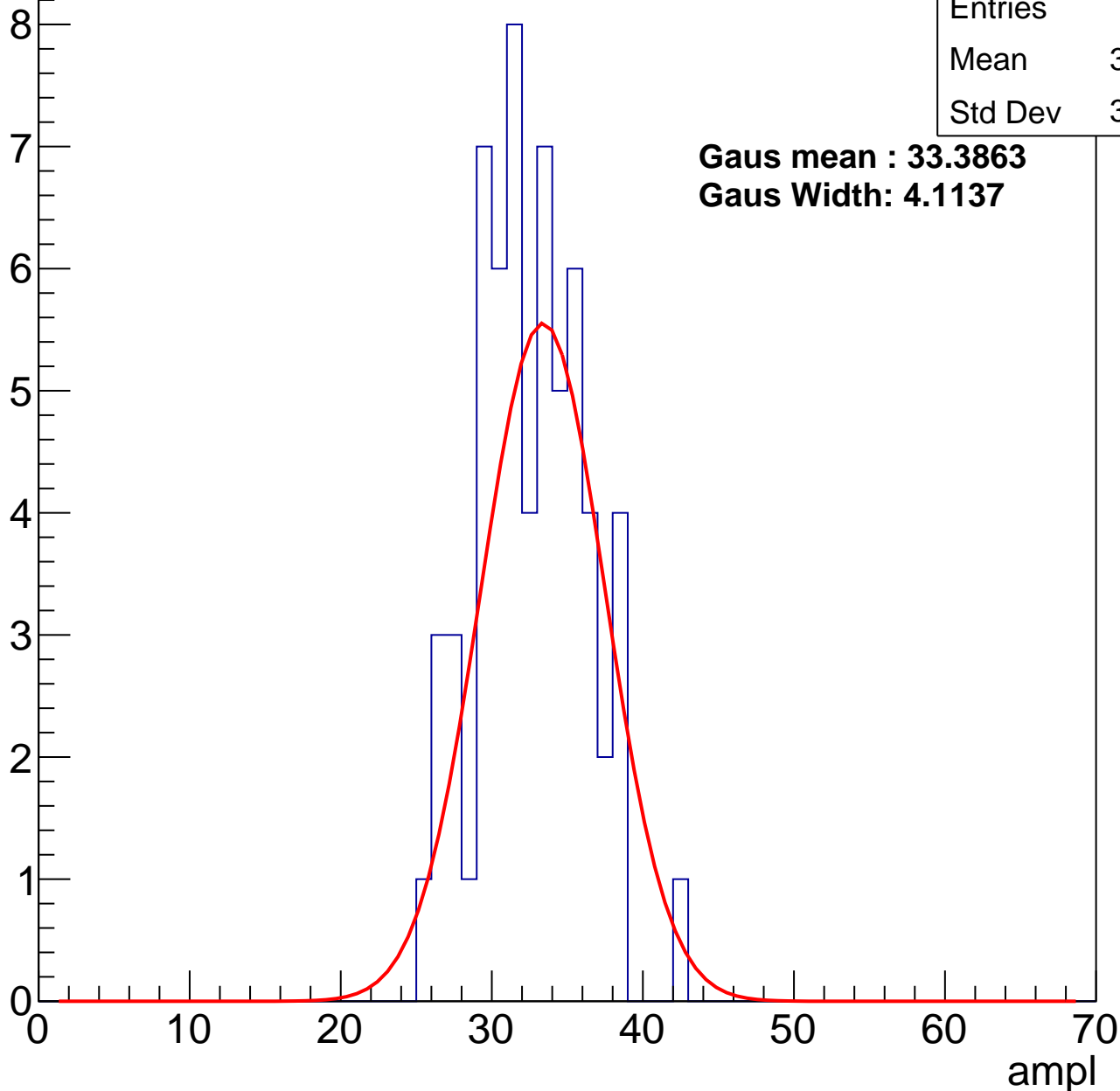
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	32.16
Std Dev	3.548

Gaus mean : 33.3863

Gaus Width: 4.1137



B1L101S, U25-ch86, adc1

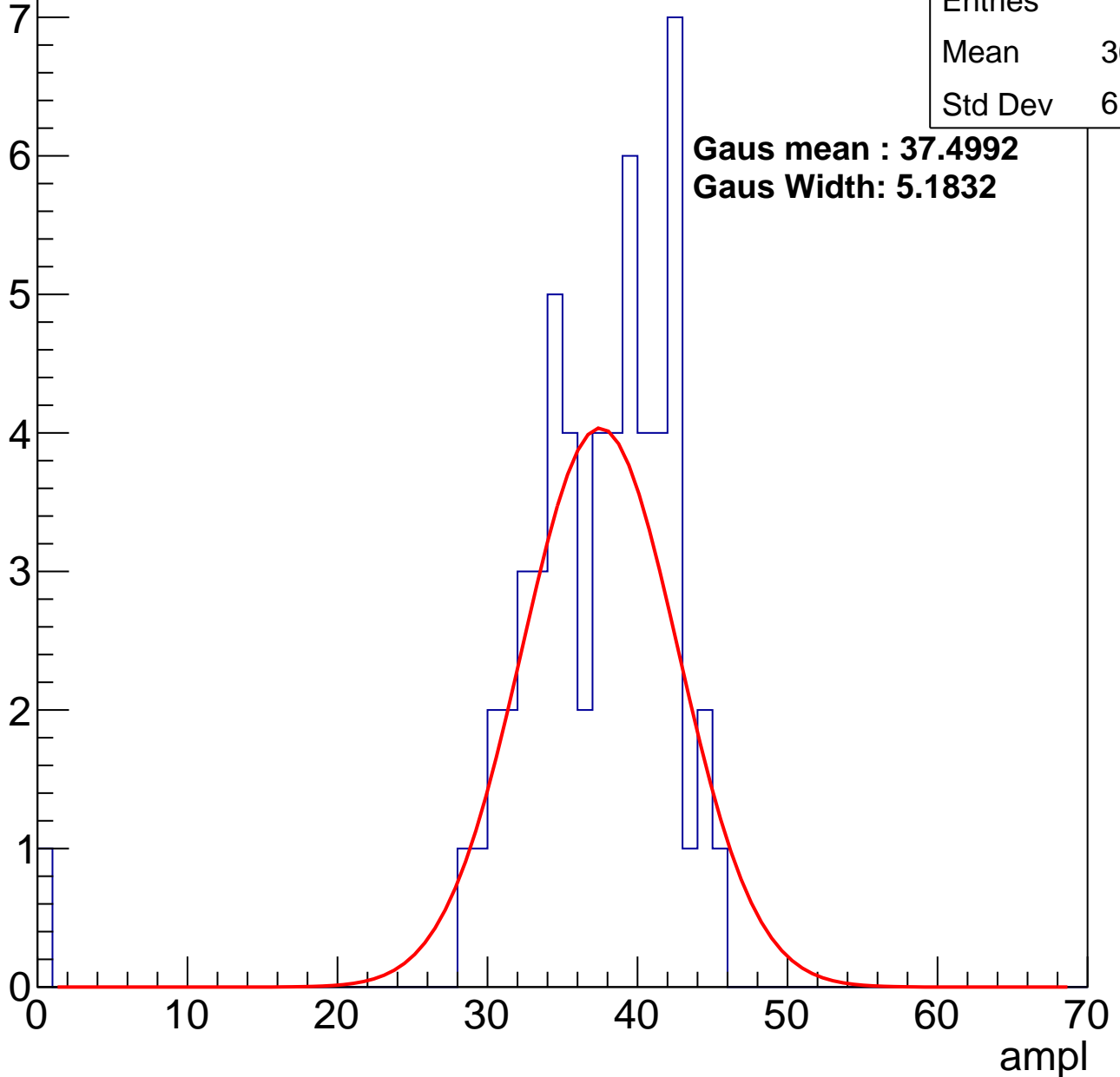
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.56
Std Dev	6.435

Gaus mean : 37.4992

Gaus Width: 5.1832



B1L101S, U25-ch86, adc2

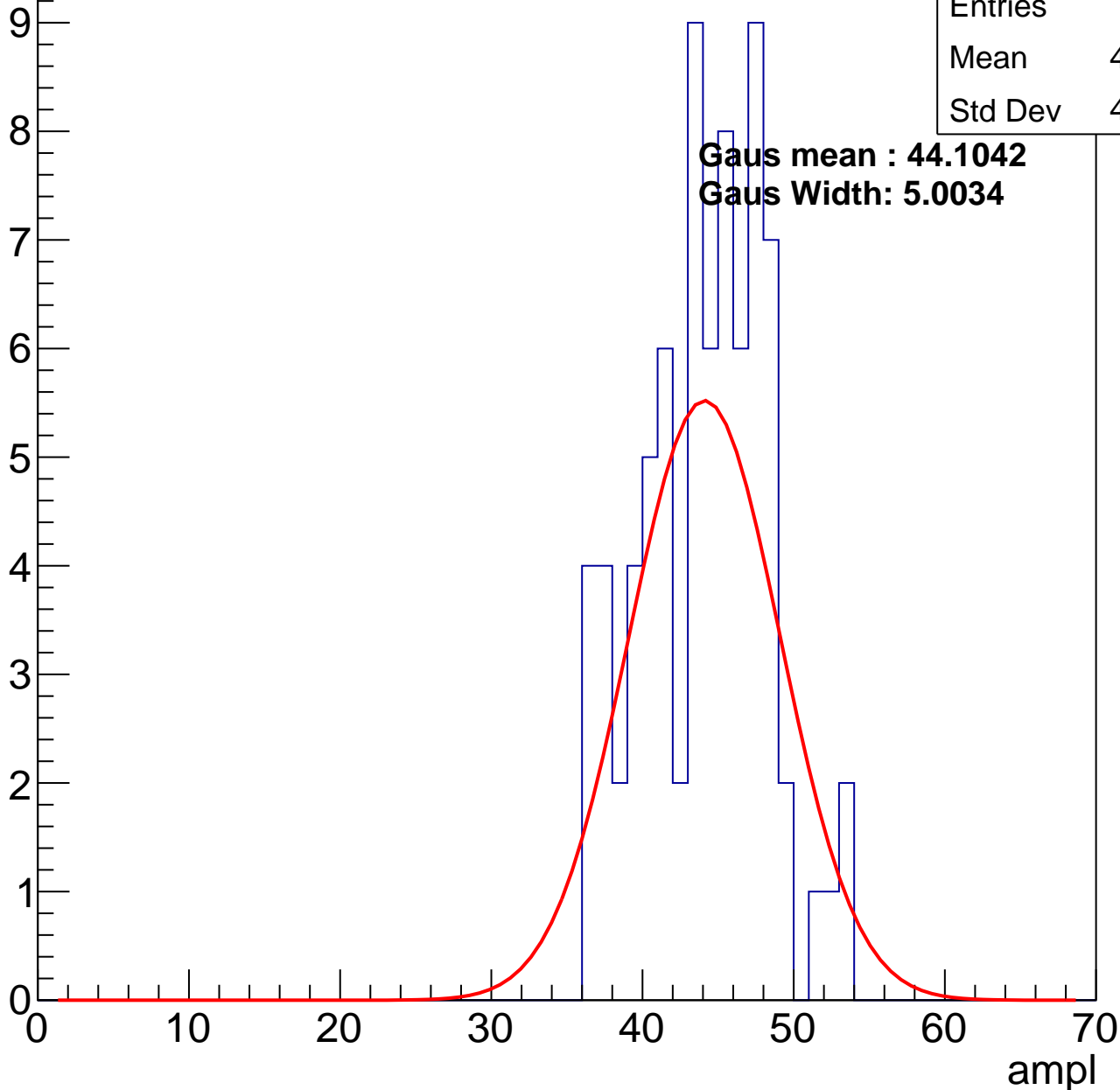
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.68
Std Dev	4.109

Gaus mean : 44.1042

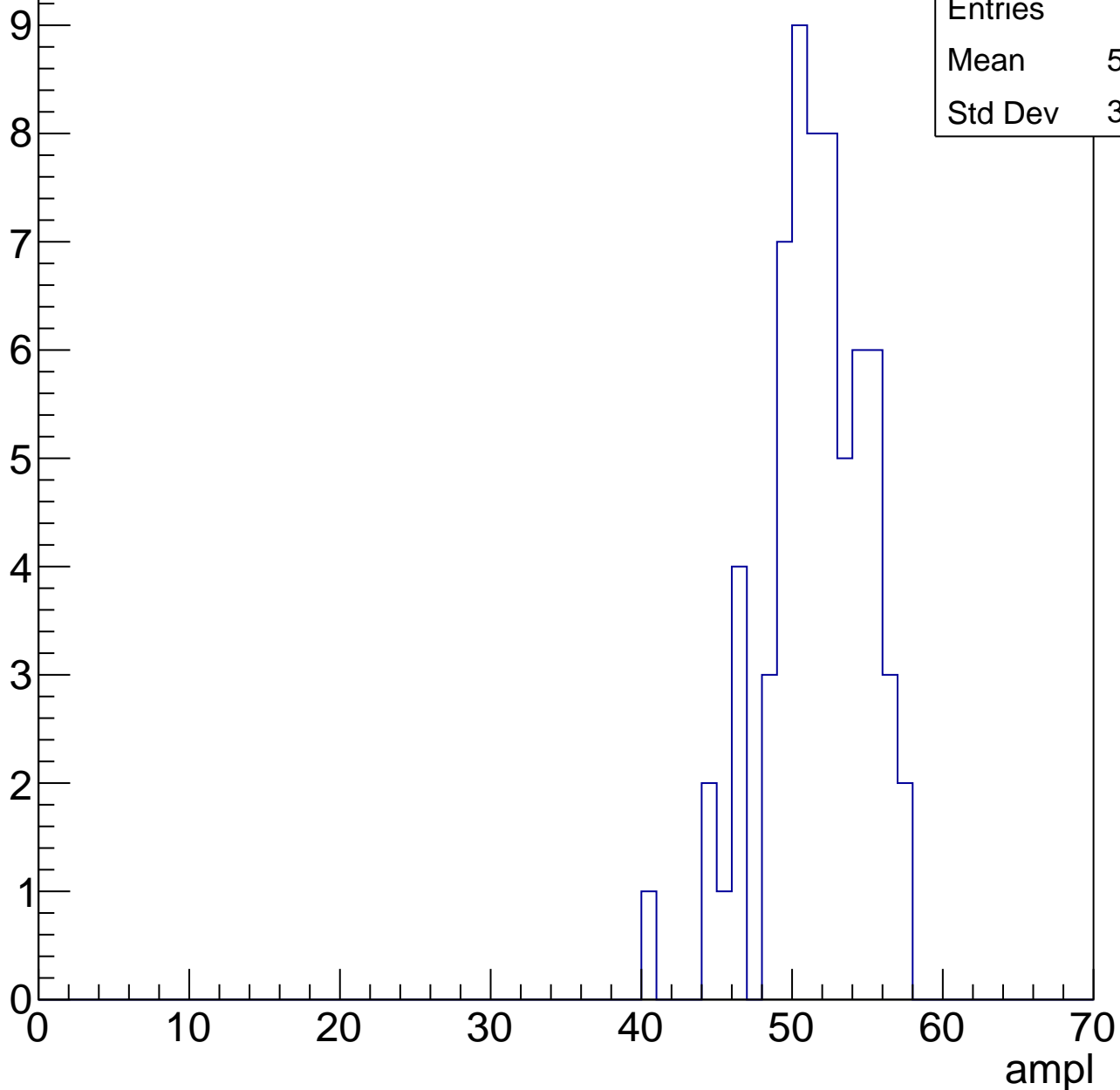
Gaus Width: 5.0034



B1L101S, U25-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



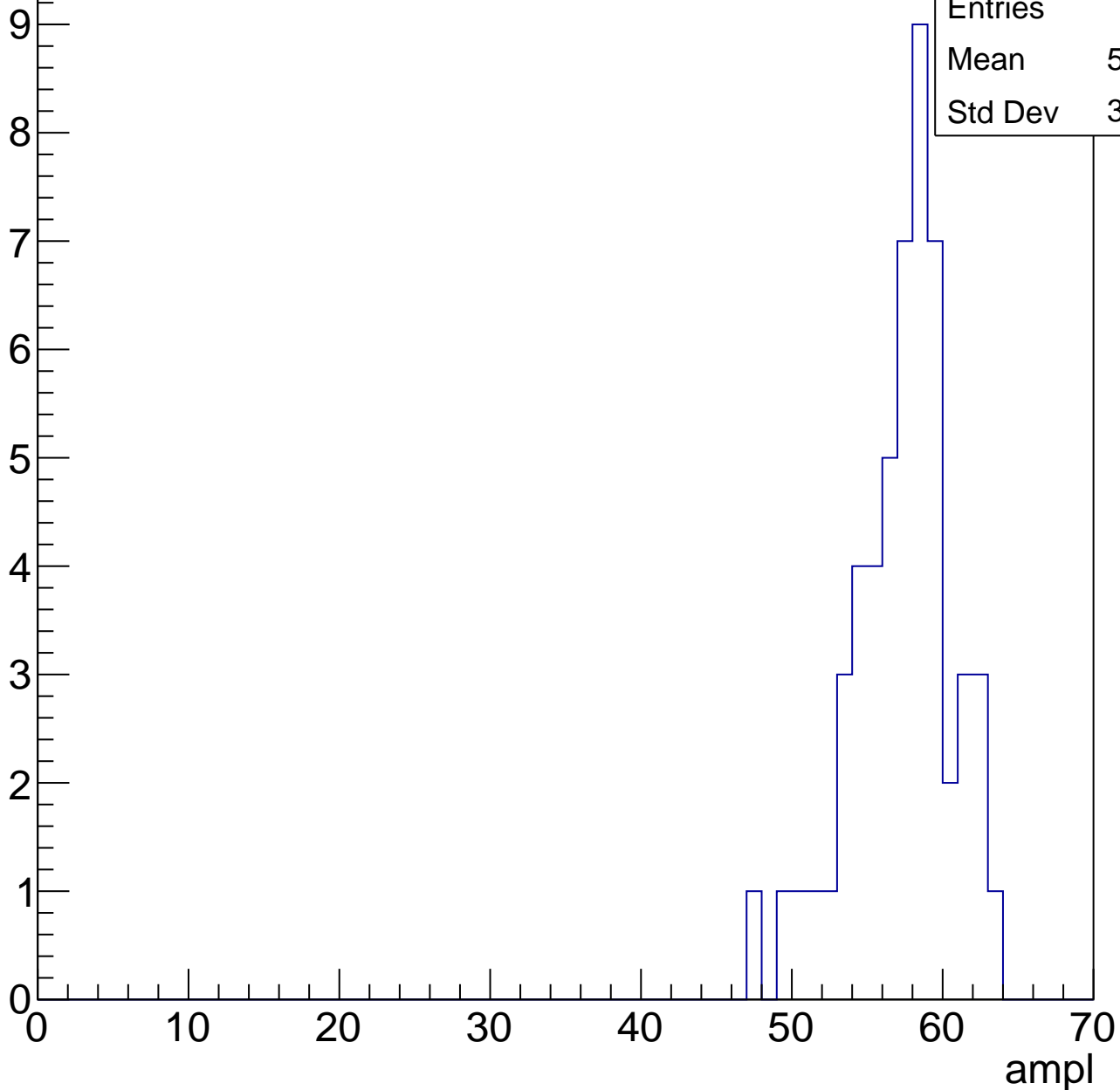
Entries	65
Mean	51.06
Std Dev	3.383

B1L101S, U25-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

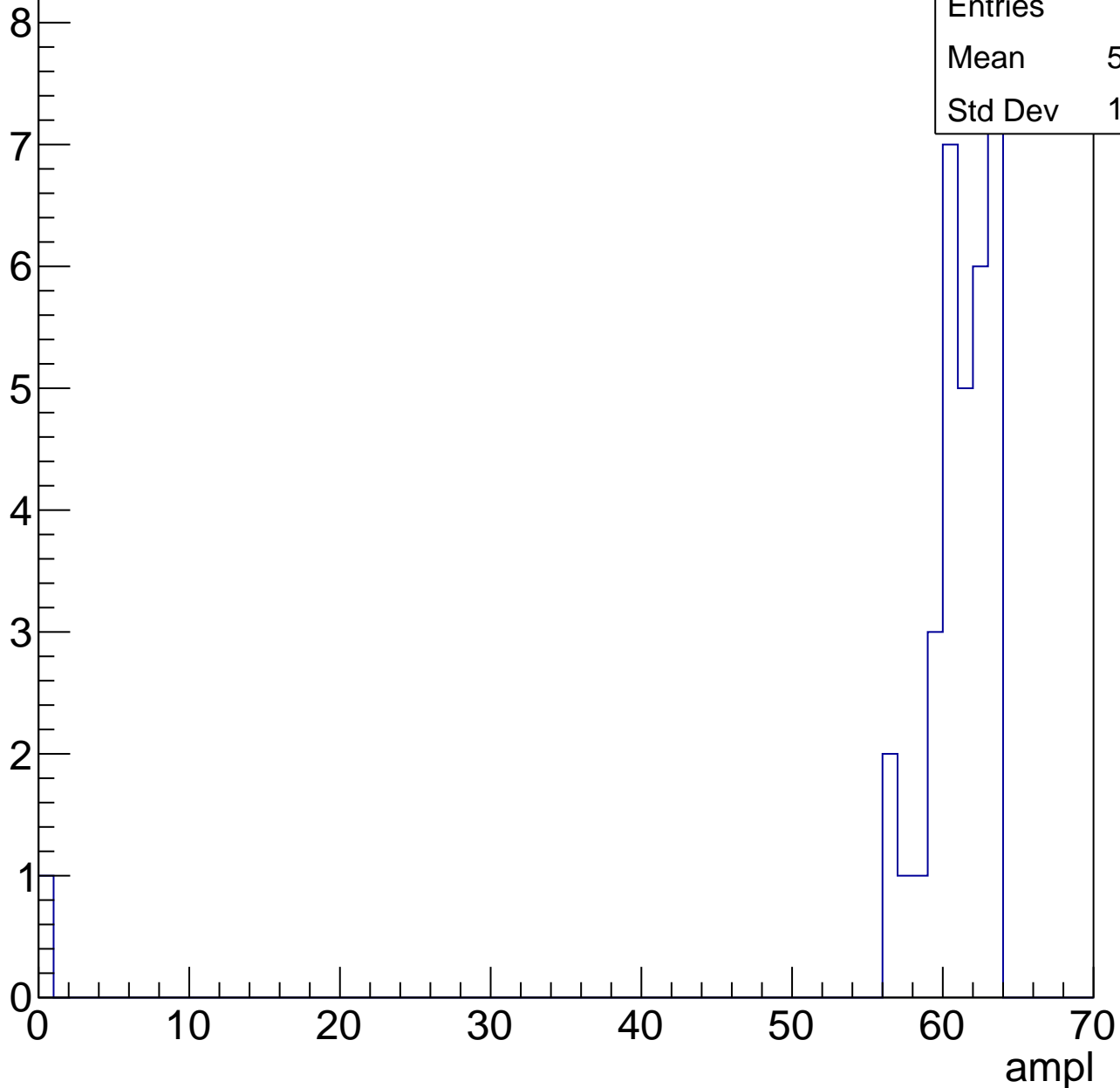
Entries	53
Mean	56.79
Std Dev	3.339



B1L101S, U25-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

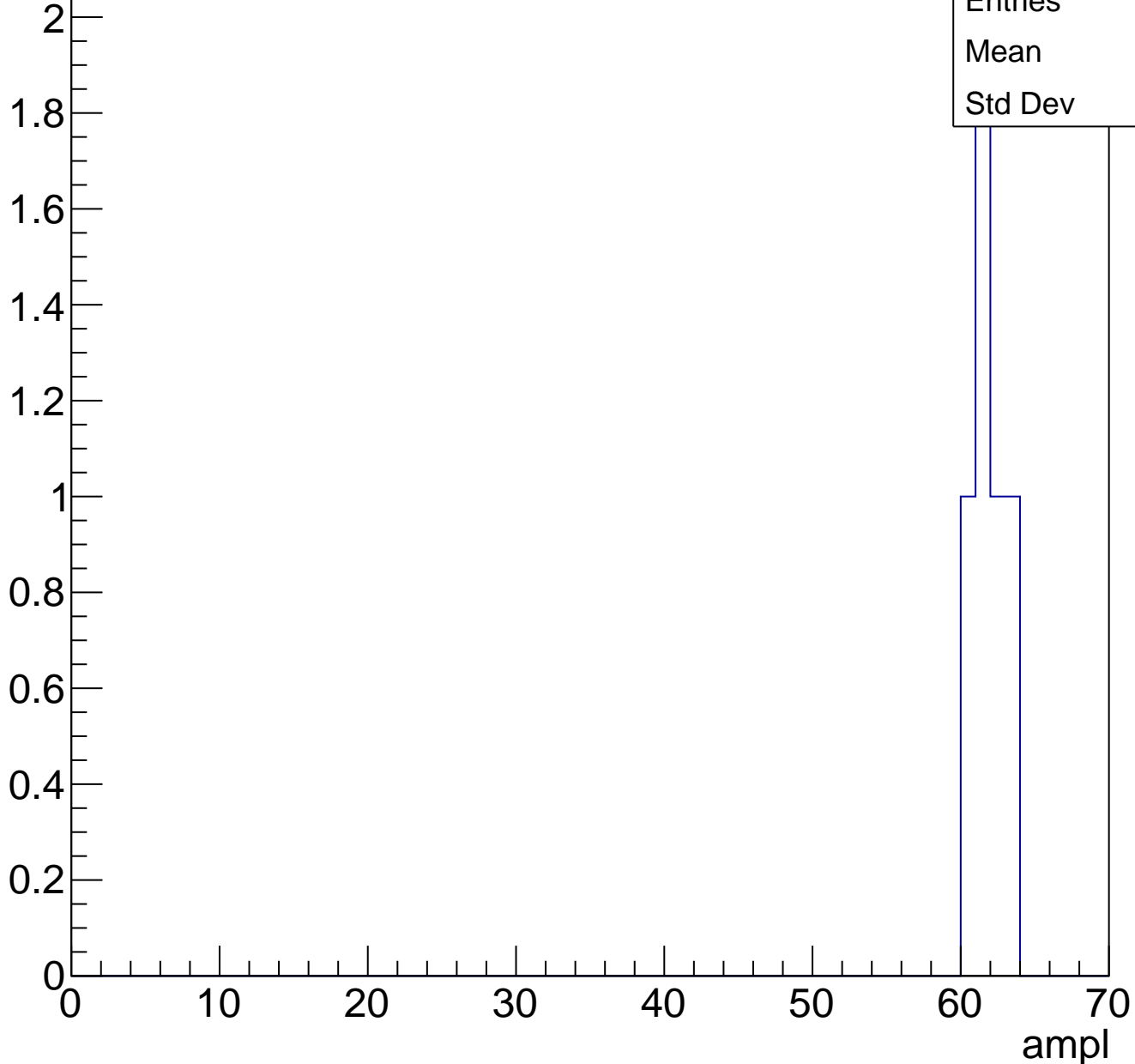
Entry



B1L101S, U25-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	61.4
Std Dev	1.02

B1L101S, U25-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch87, adc0

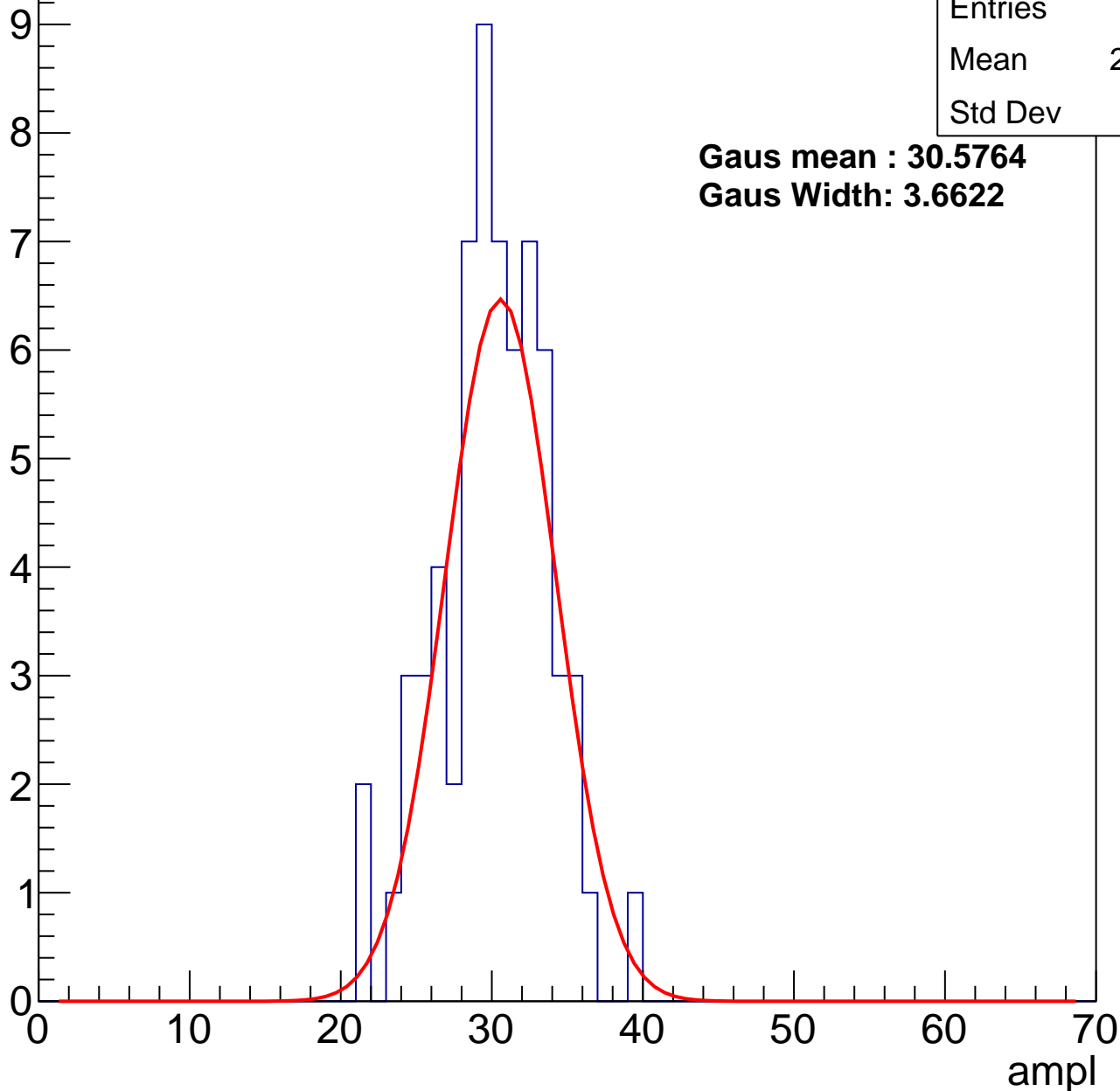
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.65
Std Dev	3.58

Gaus mean : 30.5764

Gaus Width: 3.6622



B1L101S, U25-ch87, adc1

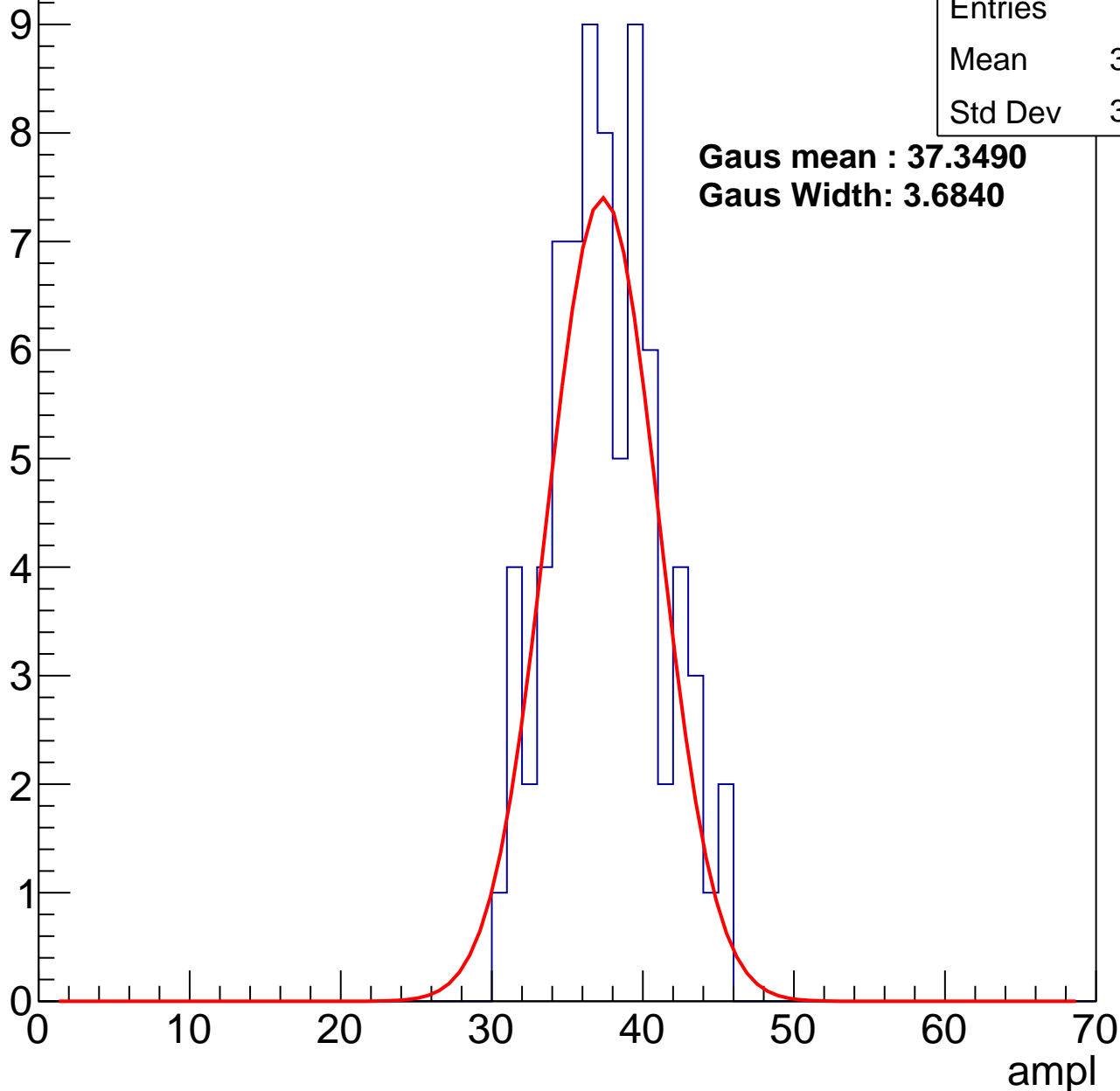
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.12
Std Dev	3.522

Gaus mean : 37.3490

Gaus Width: 3.6840



B1L101S, U25-ch87, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	44.26
Std Dev	3.796

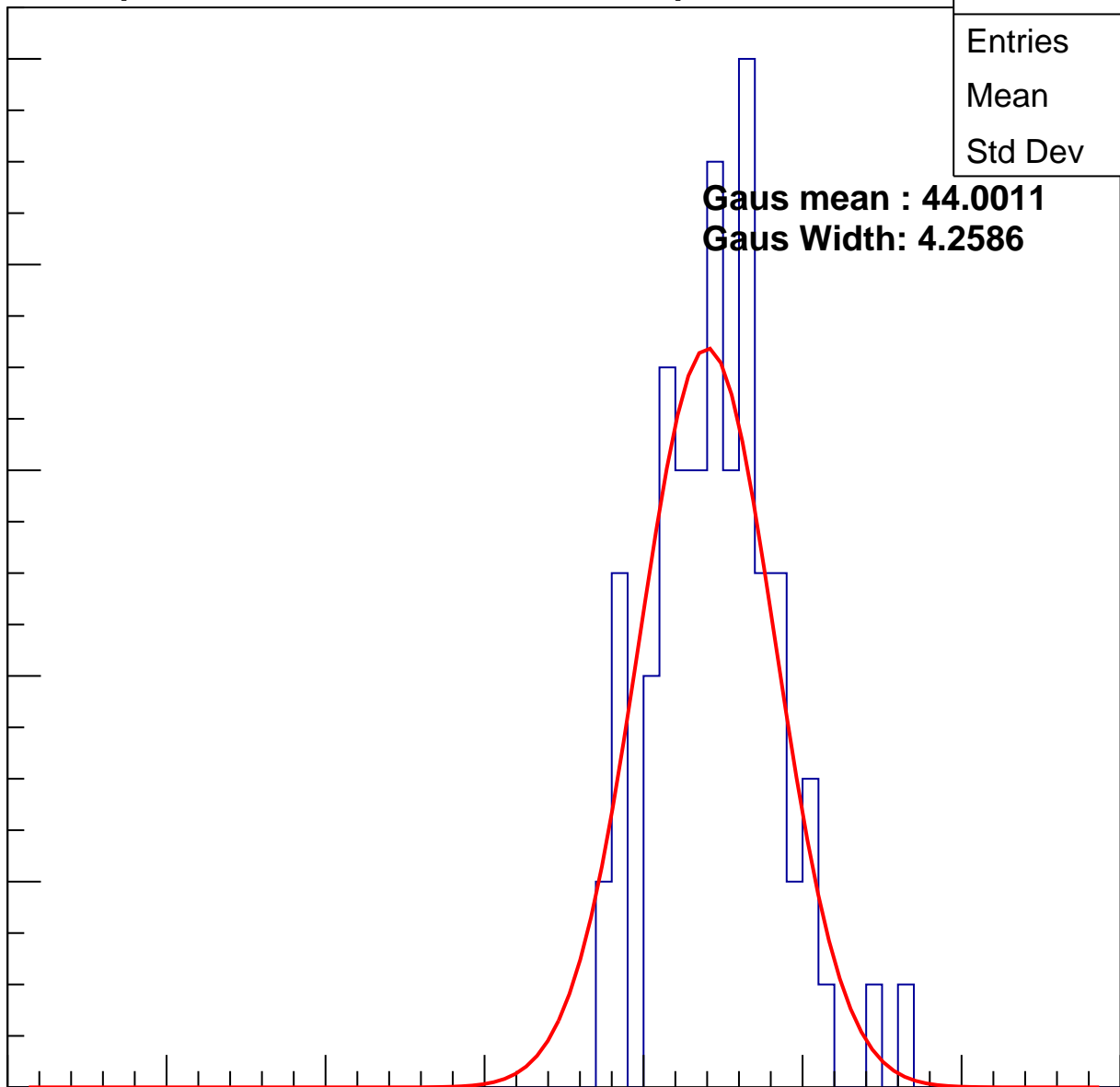
Gaus mean : 44.0011

Gaus Width: 4.2586

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

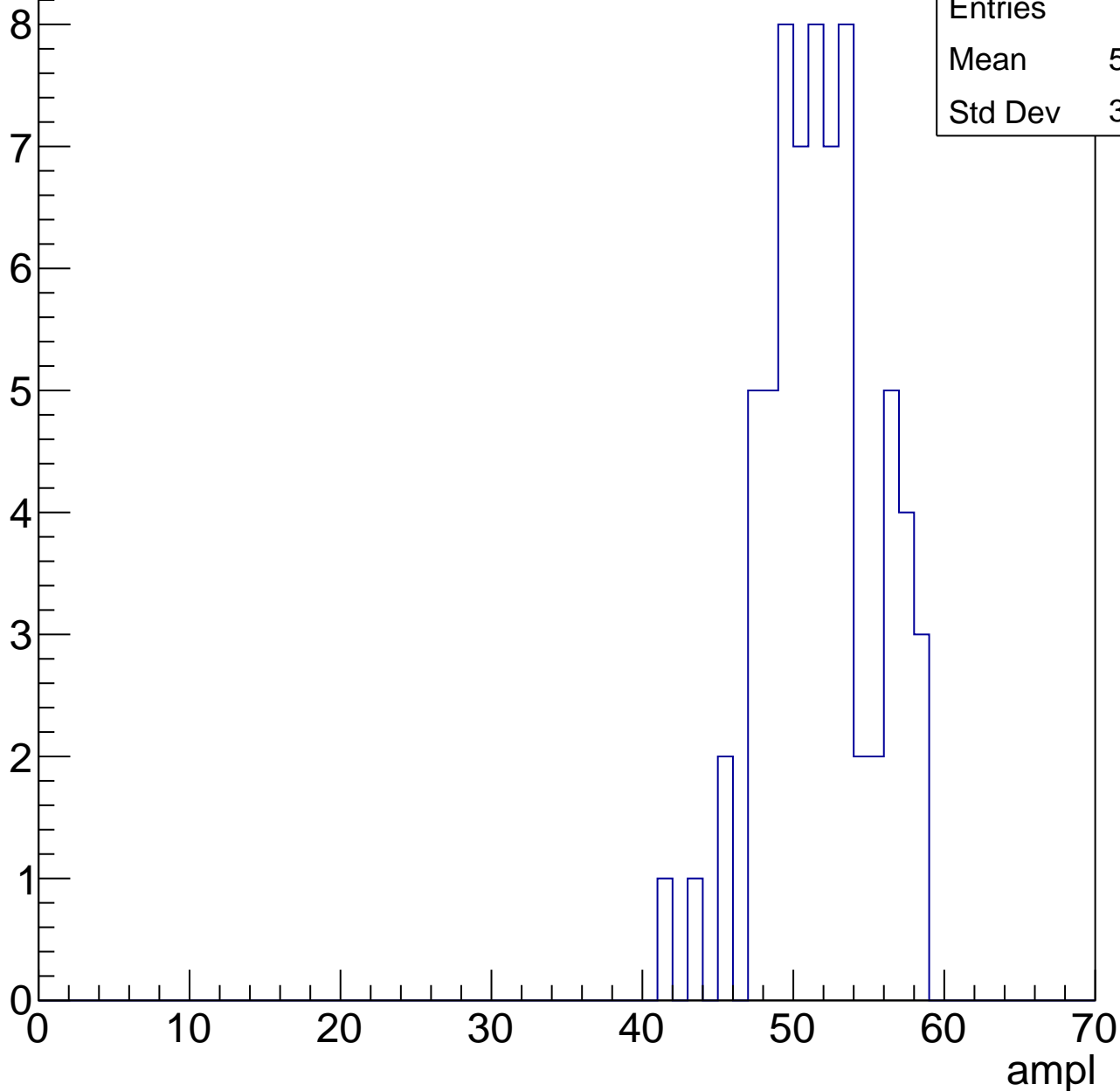


B1L101S, U25-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

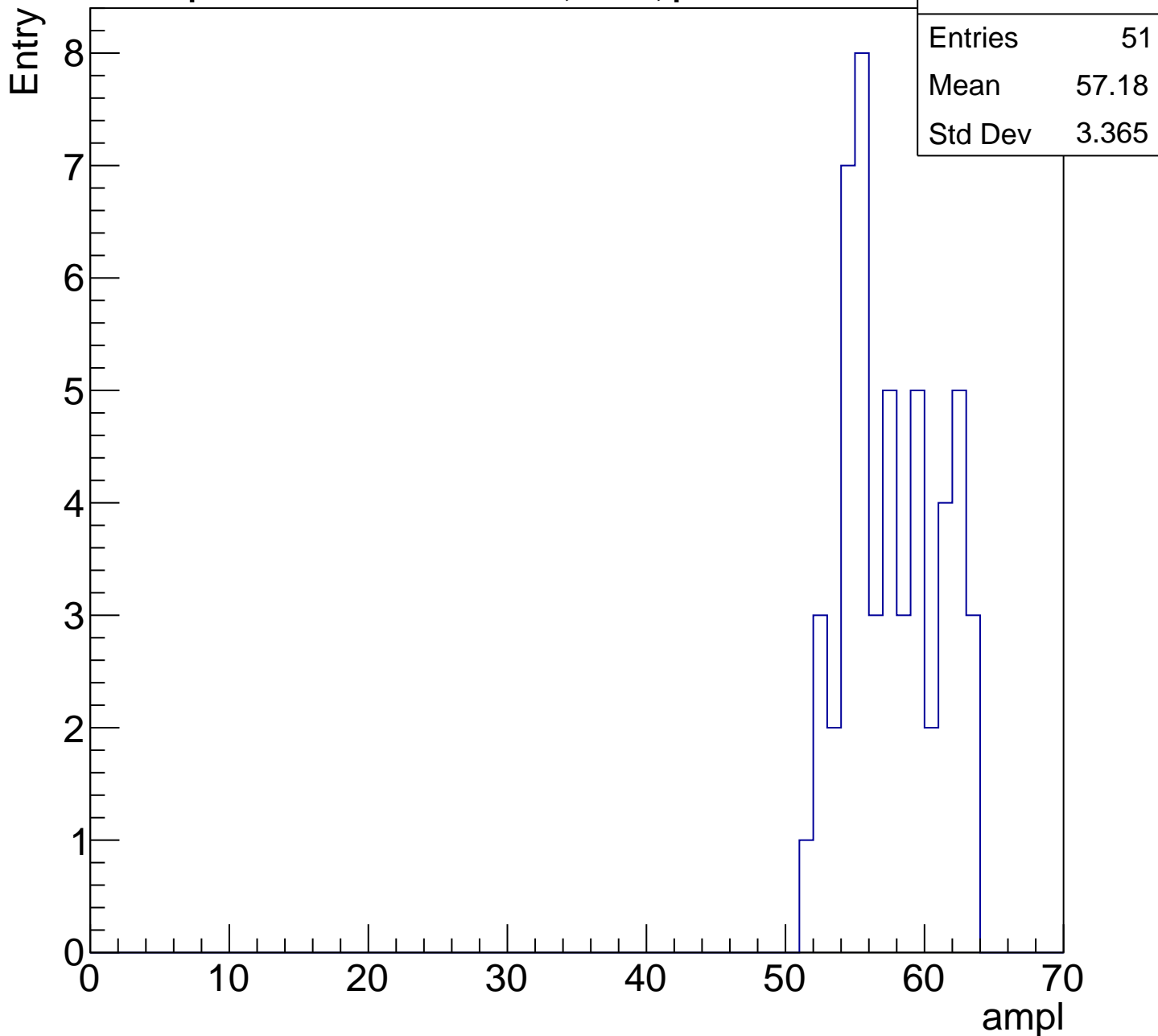
Entry

Entries	68
Mean	51.28
Std Dev	3.637



B1L101S, U25-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch87, adc5

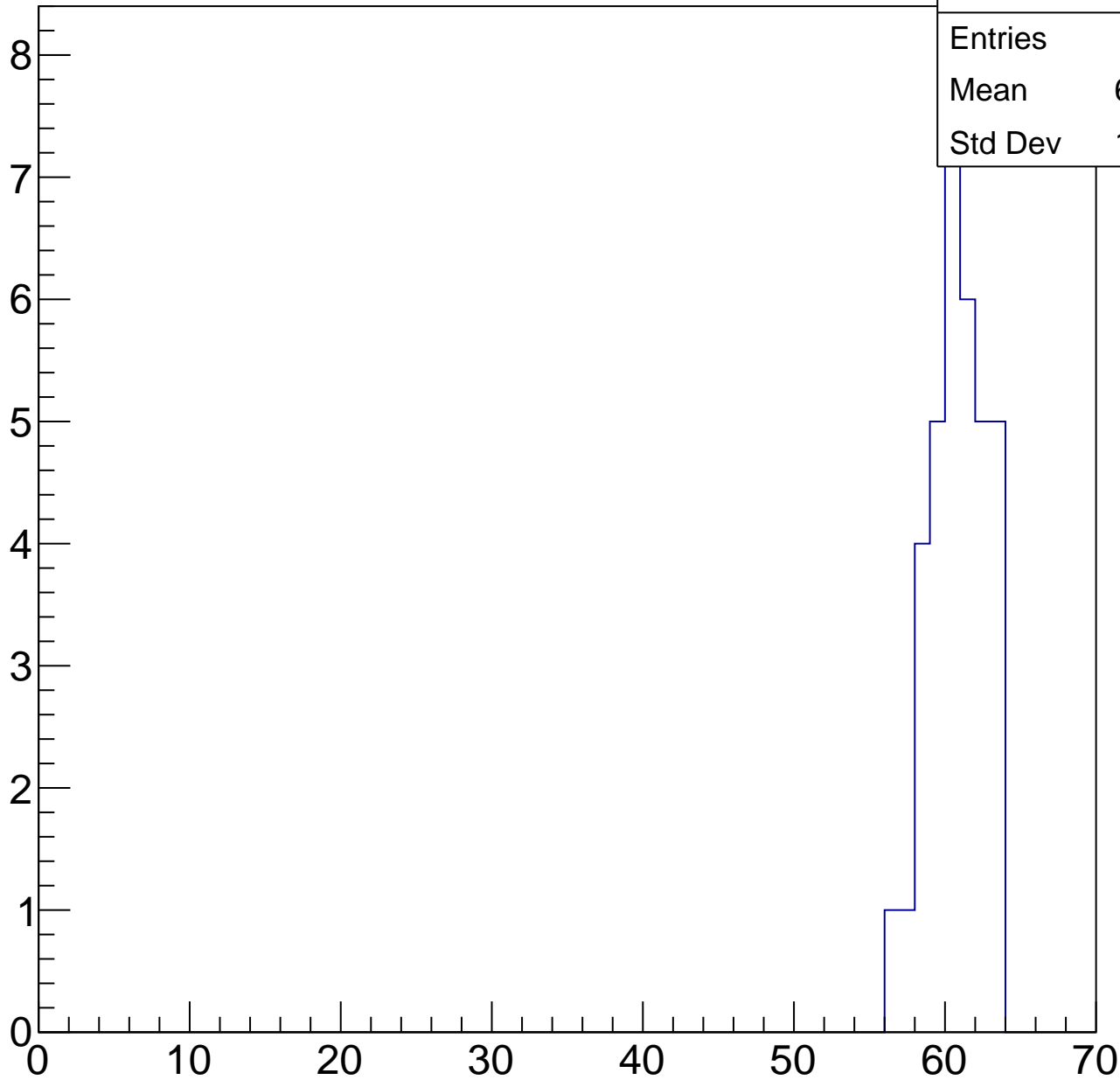
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.31
Std Dev	1.801

ampl



B1L101S, U25-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L101S, U25-ch88, adc0

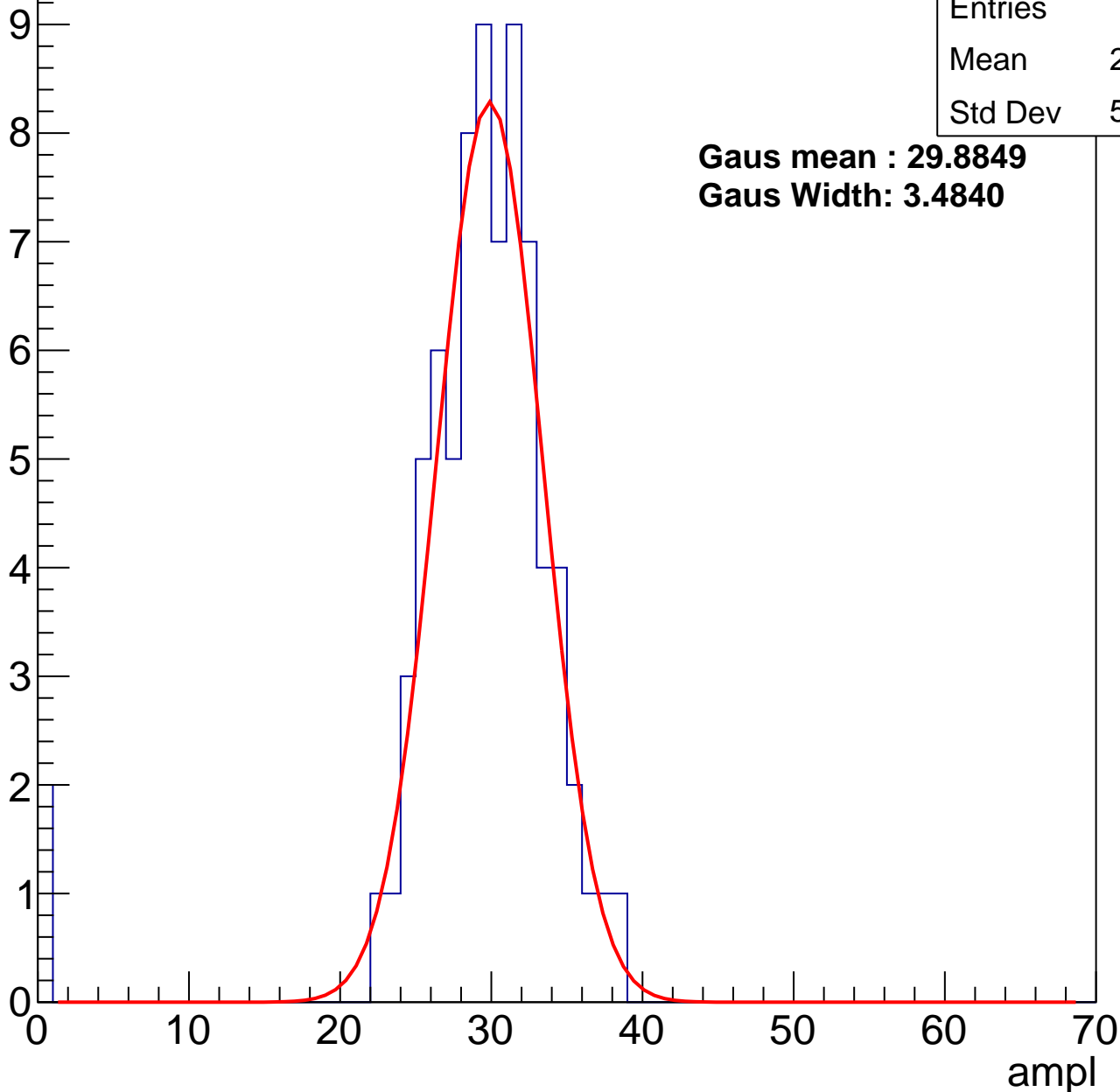
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	28.68
Std Dev	5.772

Gaus mean : 29.8849

Gaus Width: 3.4840



B1L101S, U25-ch88, adc1

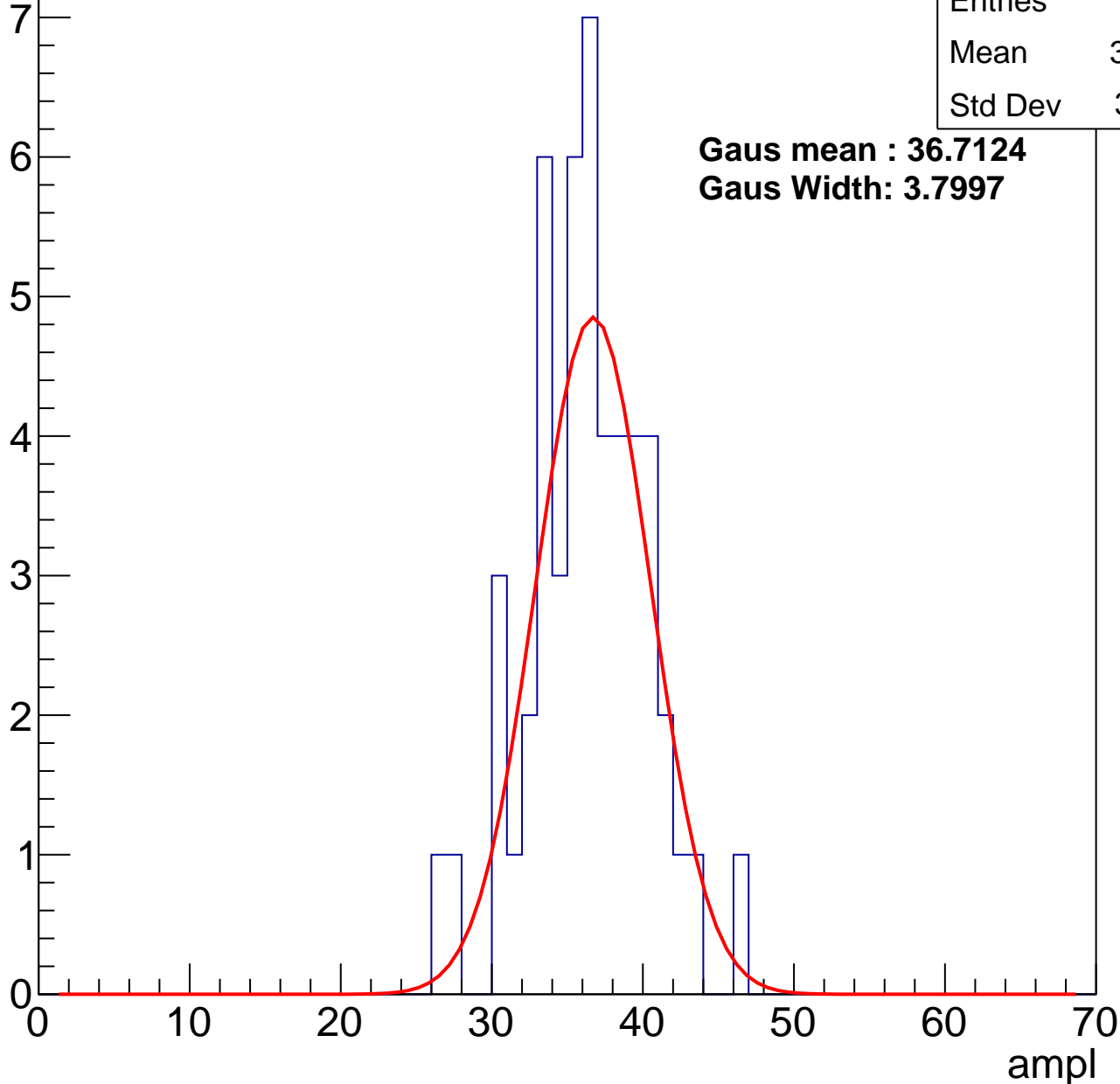
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	35.86
Std Dev	3.891

Gaus mean : 36.7124

Gaus Width: 3.7997



B1L101S, U25-ch88, adc2

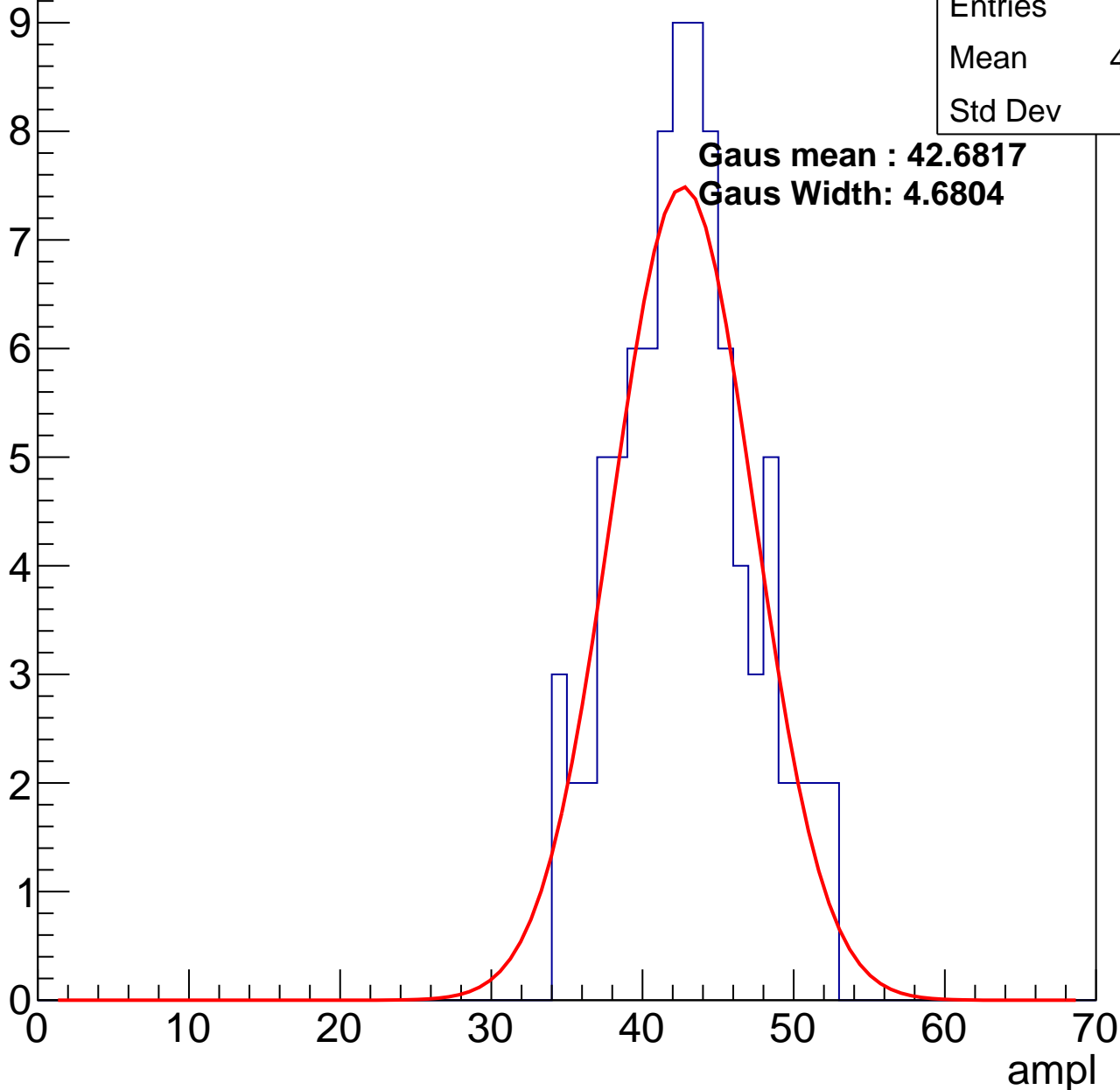
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	42.44
Std Dev	4.29

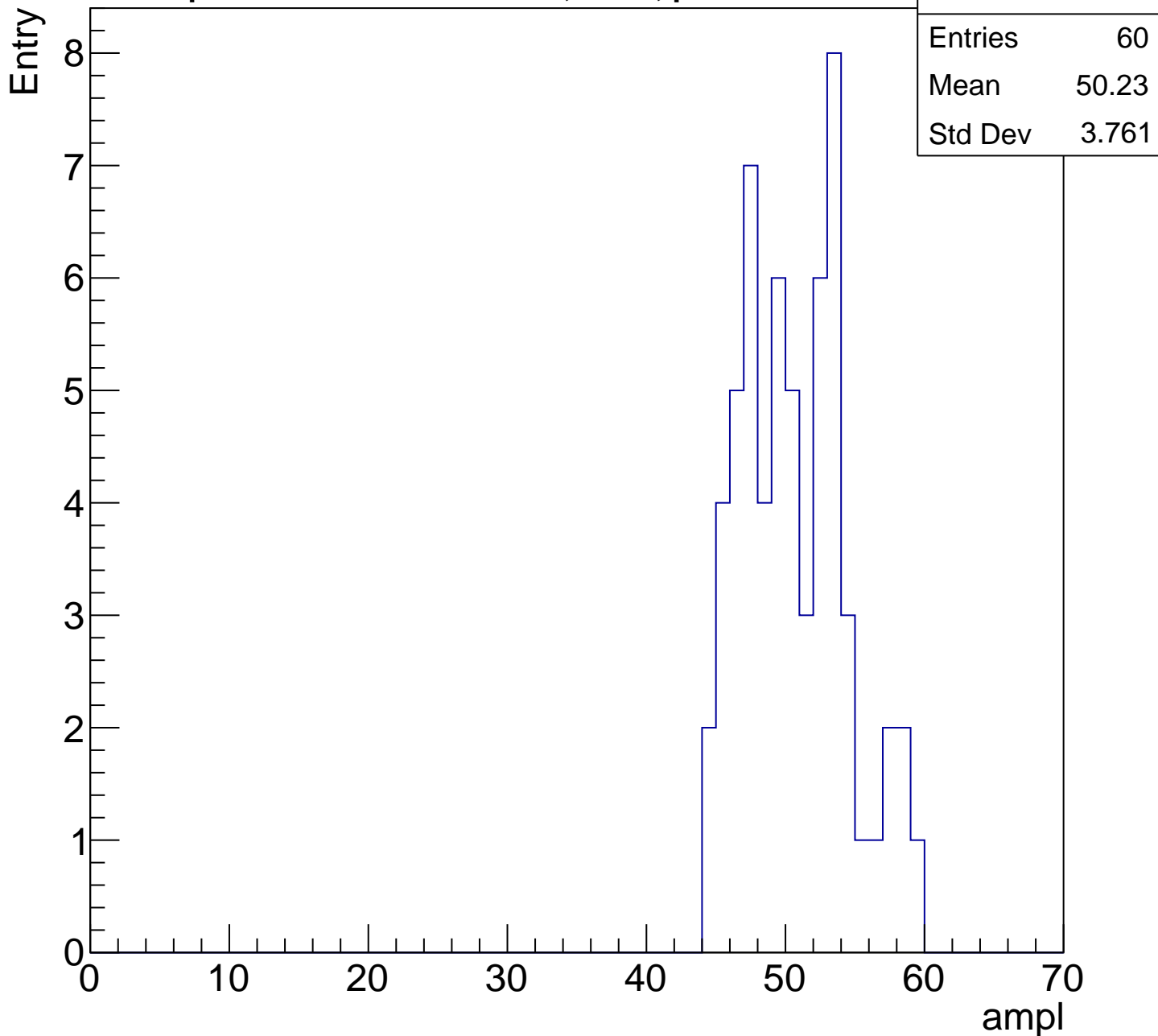
Gaus mean : 42.6817

Gaus Width: 4.6804



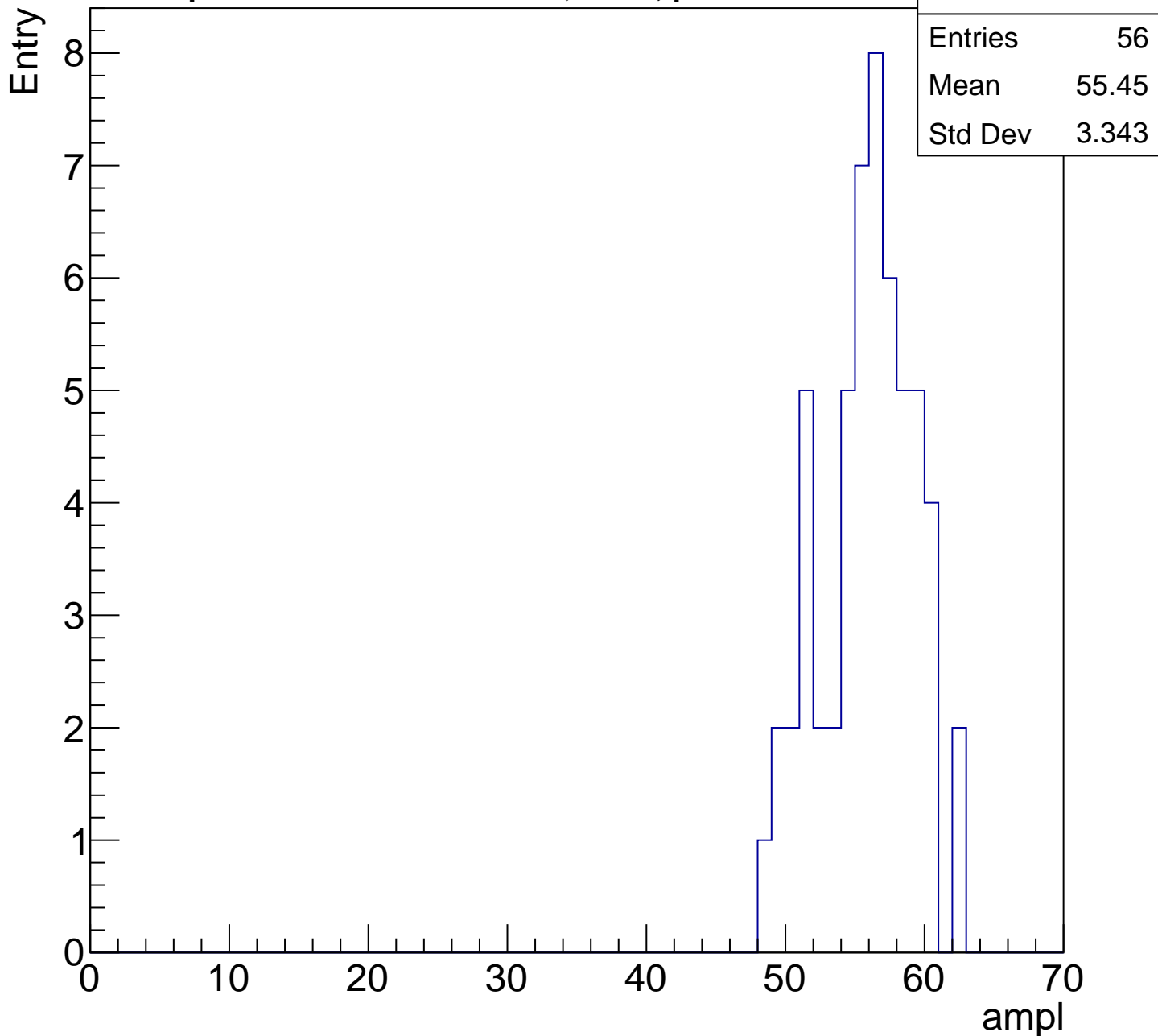
B1L101S, U25-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch88, adc4

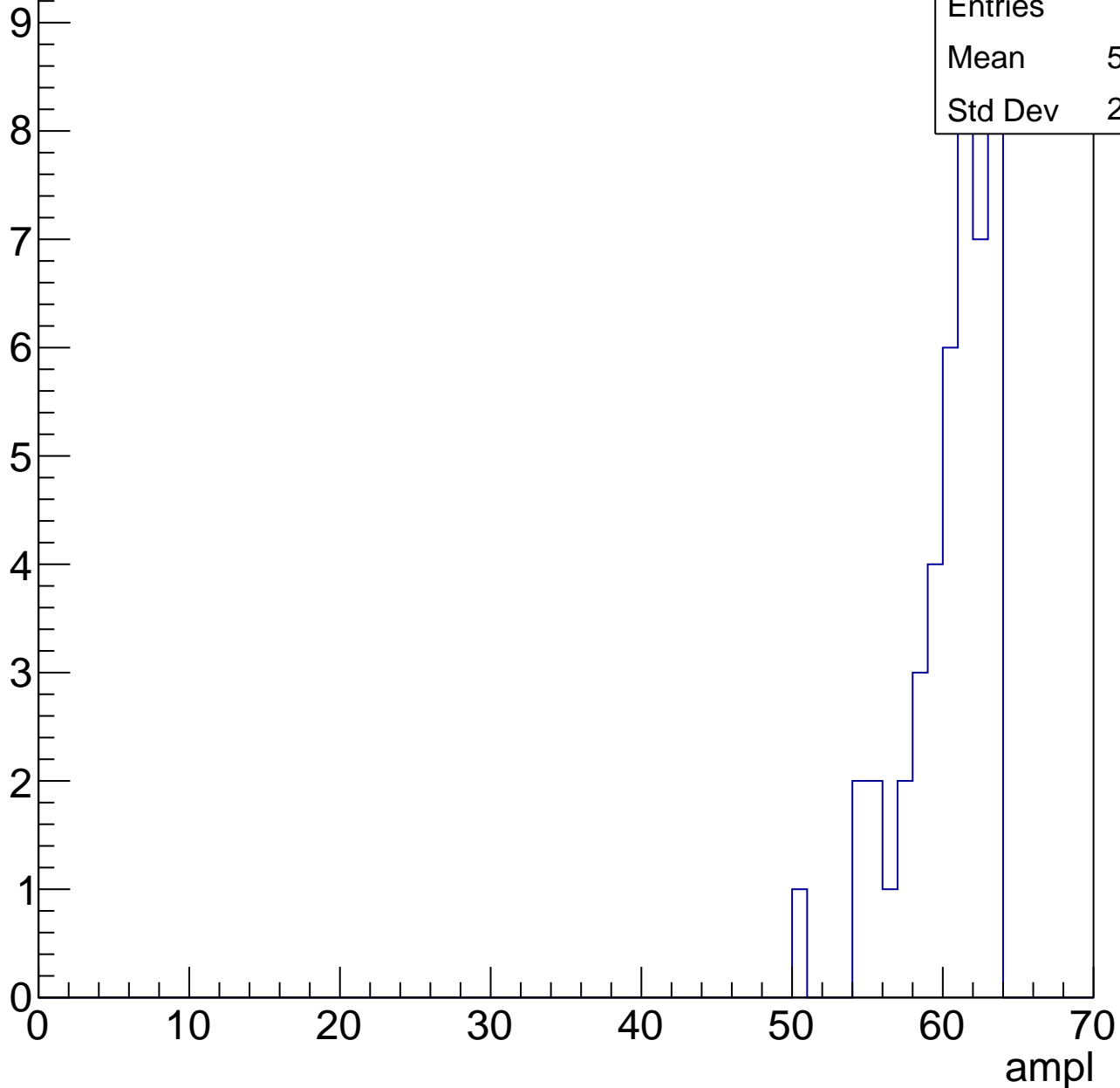
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch88, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	49.8
Std Dev	24.91

B1L101S, U25-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch89, adc0

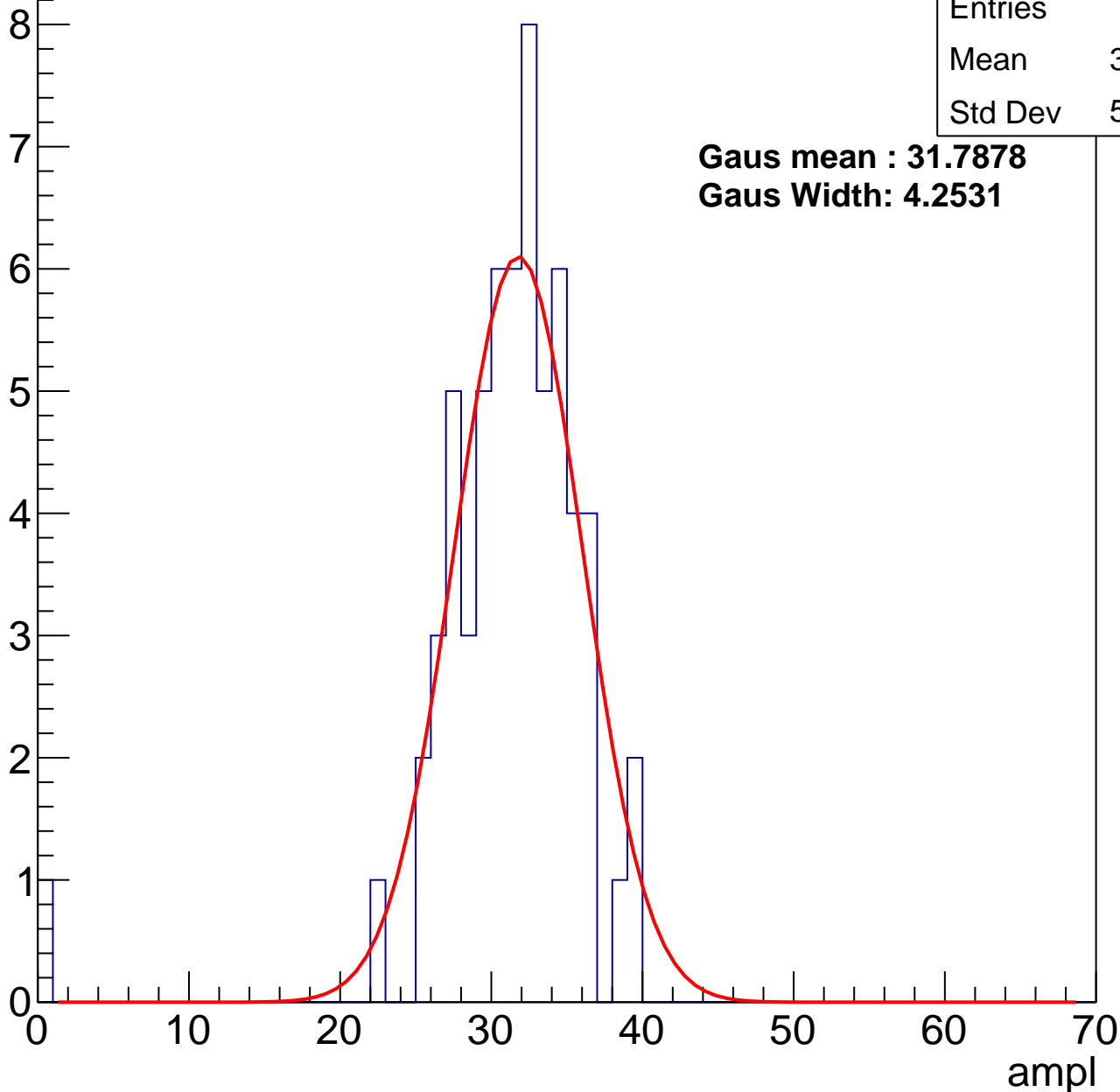
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	30.73
Std Dev	5.298

Gaus mean : 31.7878

Gaus Width: 4.2531



B1L101S, U25-ch89, adc1

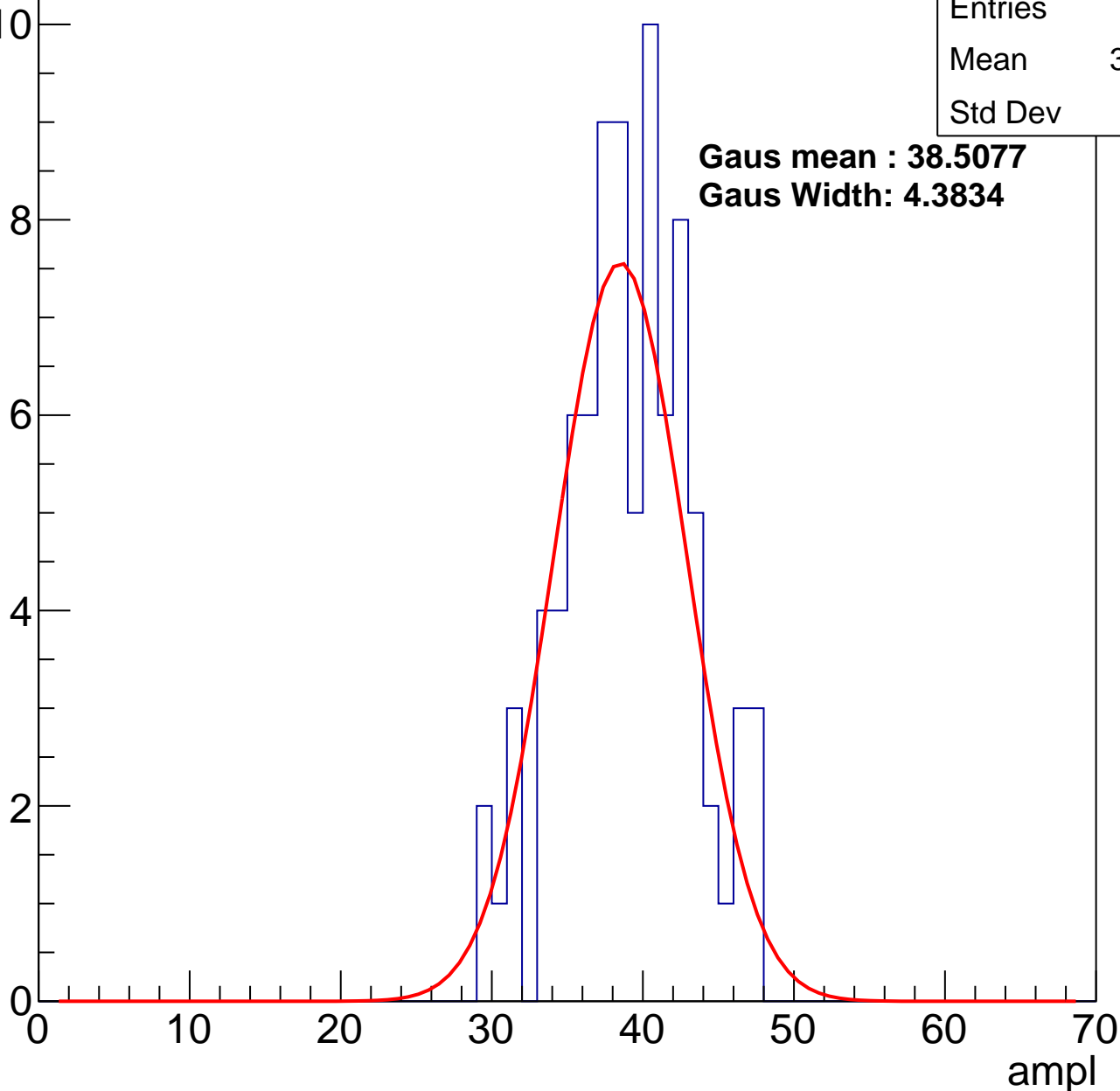
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	38.55
Std Dev	4.16

Gaus mean : 38.5077

Gaus Width: 4.3834



B1L101S, U25-ch89, adc2

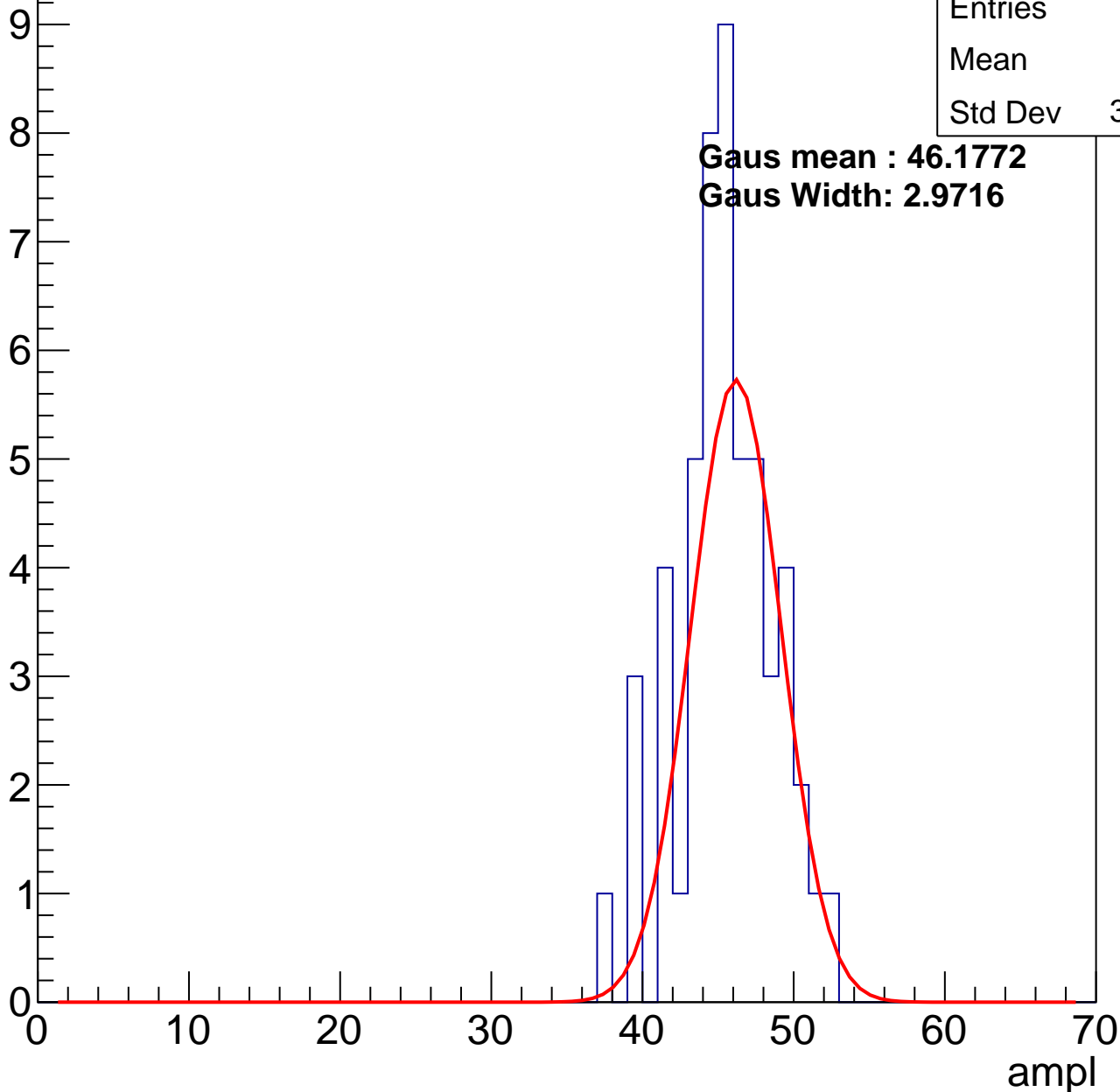
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	45
Std Dev	3.174

Gaus mean : 46.1772

Gaus Width: 2.9716

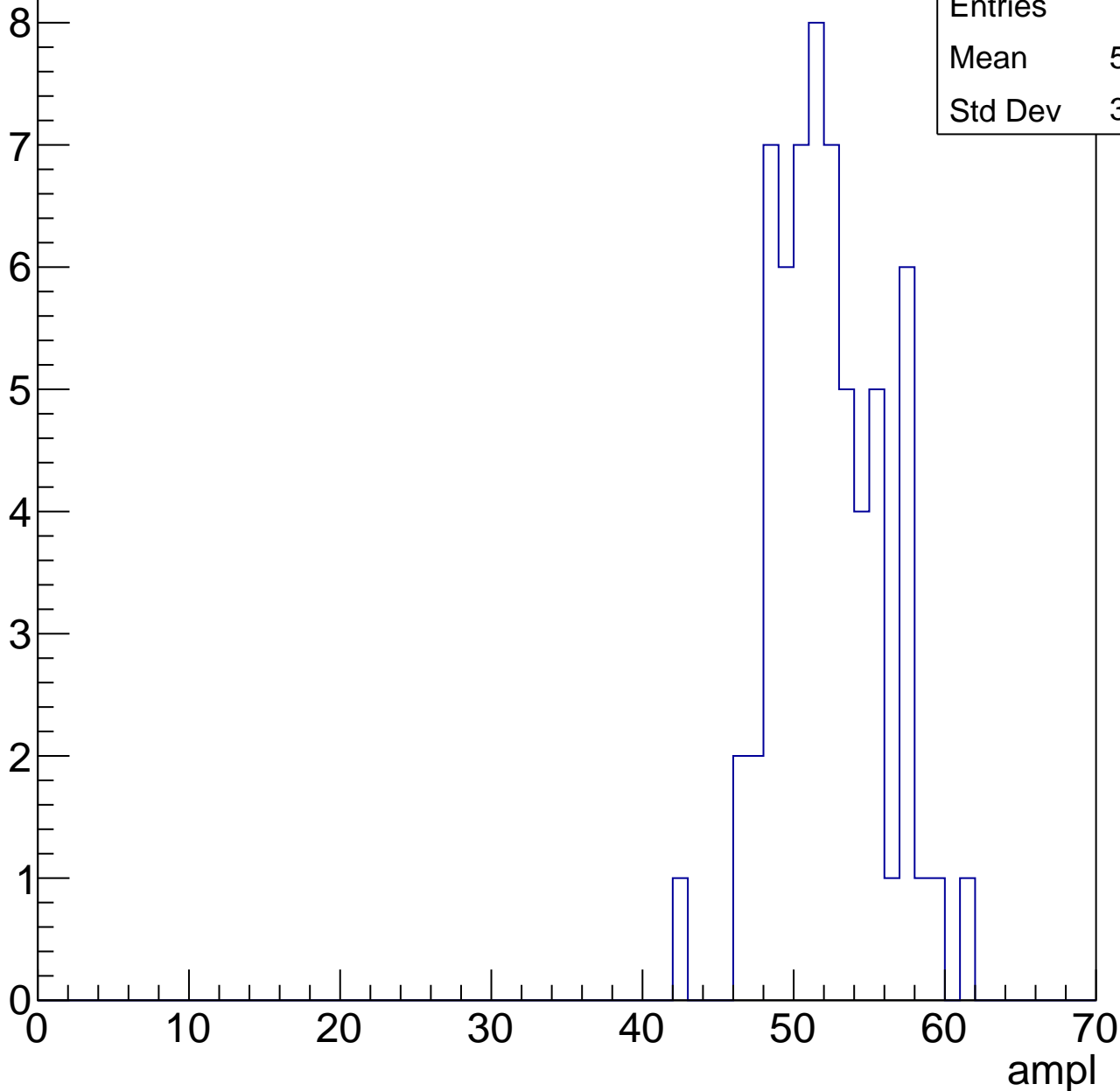


B1L101S, U25-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

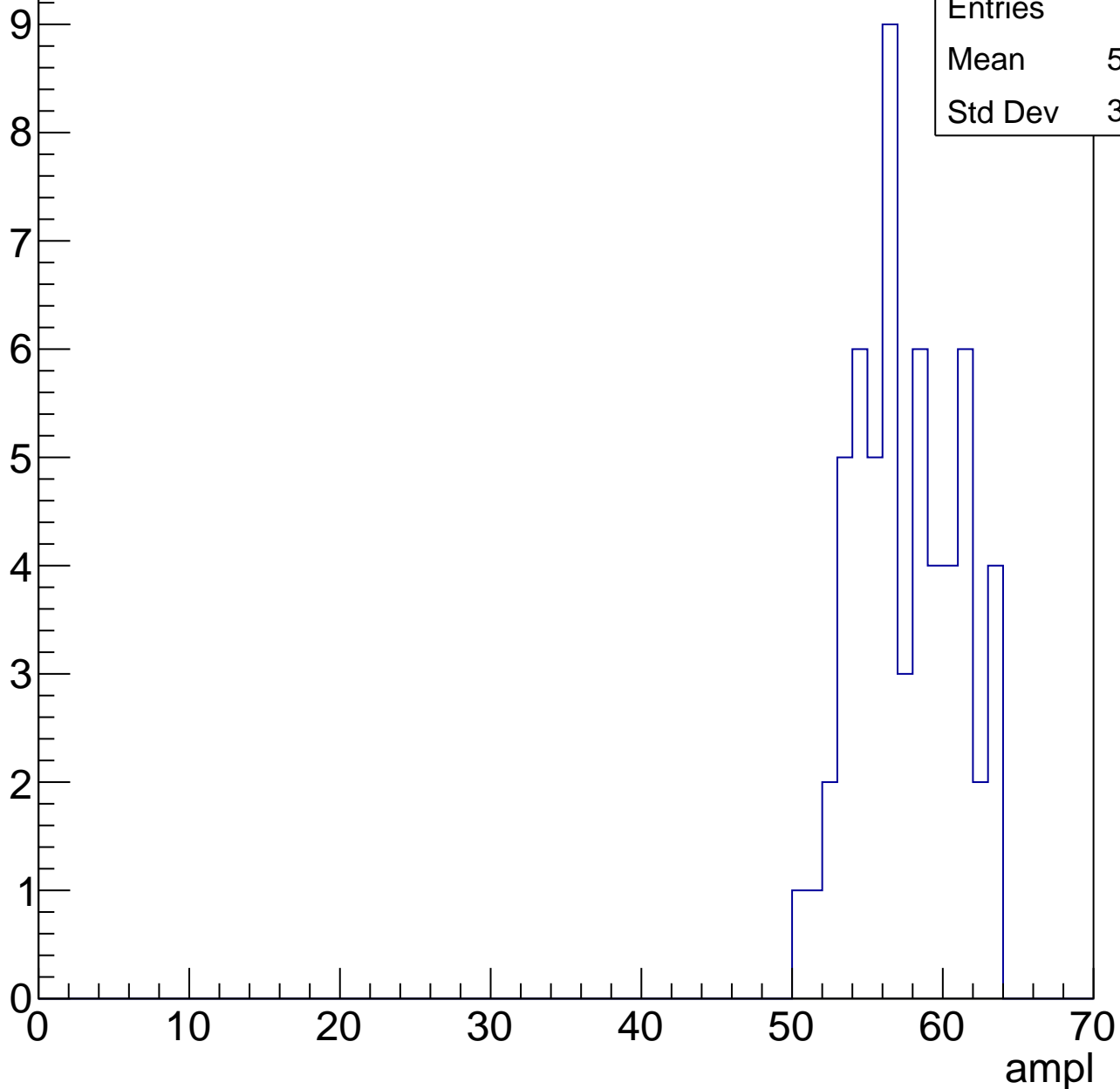
Entries	64
Mean	51.75
Std Dev	3.575



B1L101S, U25-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



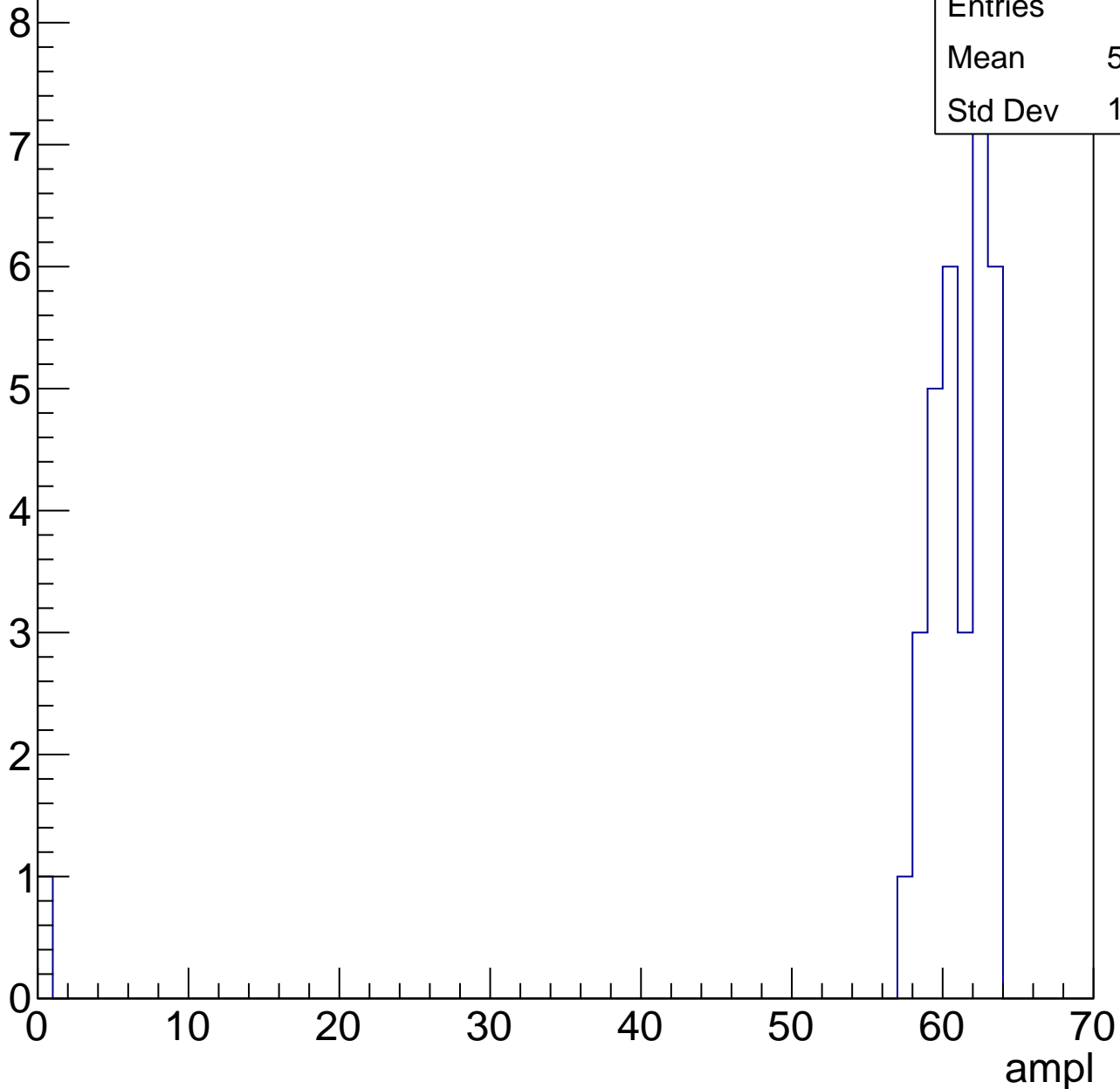
Entries	58
Mean	57.07
Std Dev	3.337

B1L101S, U25-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.88
Std Dev	10.55



B1L101S, U25-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch90, adc0

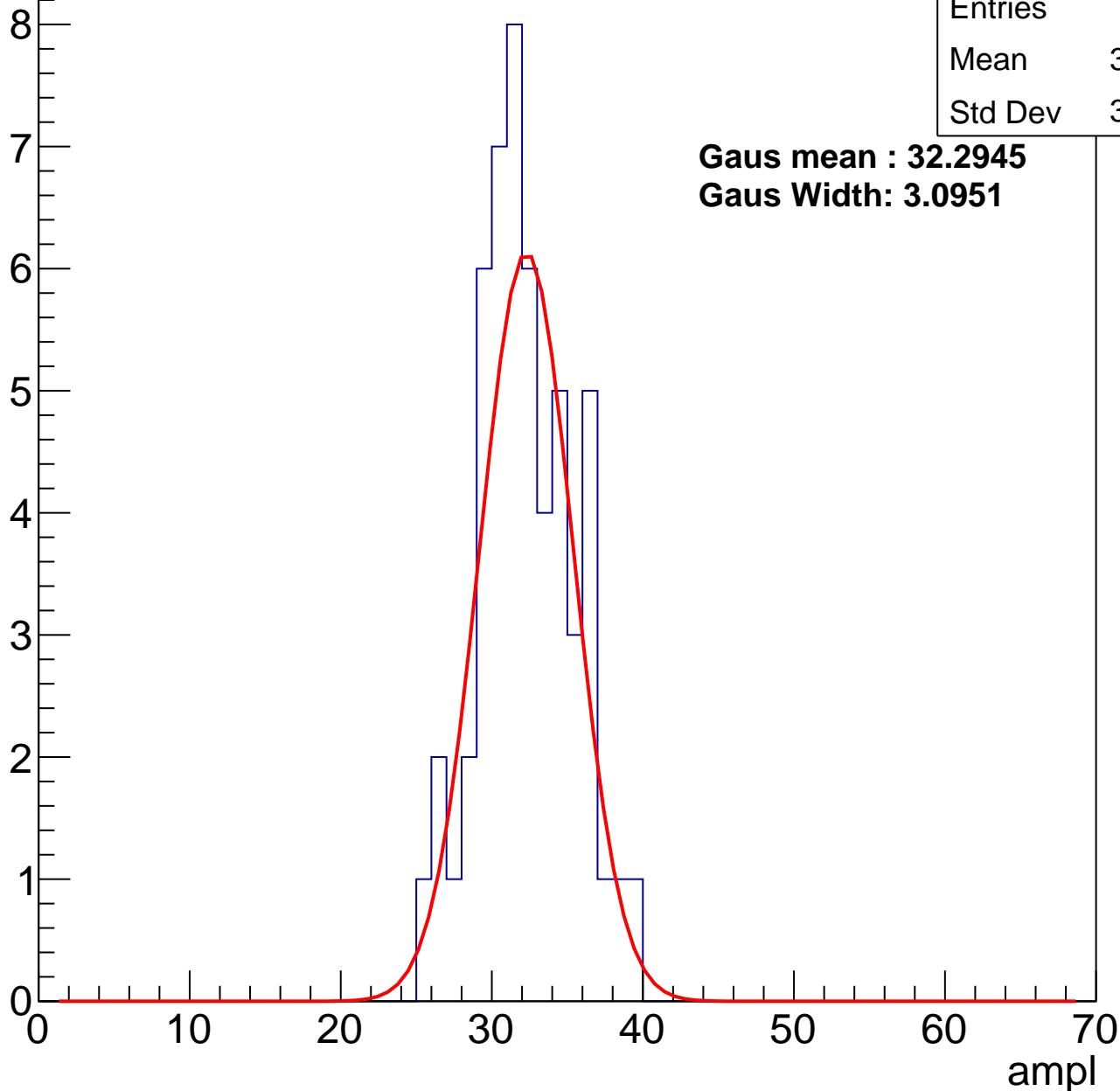
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	31.79
Std Dev	3.092

Gaus mean : 32.2945

Gaus Width: 3.0951



B1L101S, U25-ch90, adc1

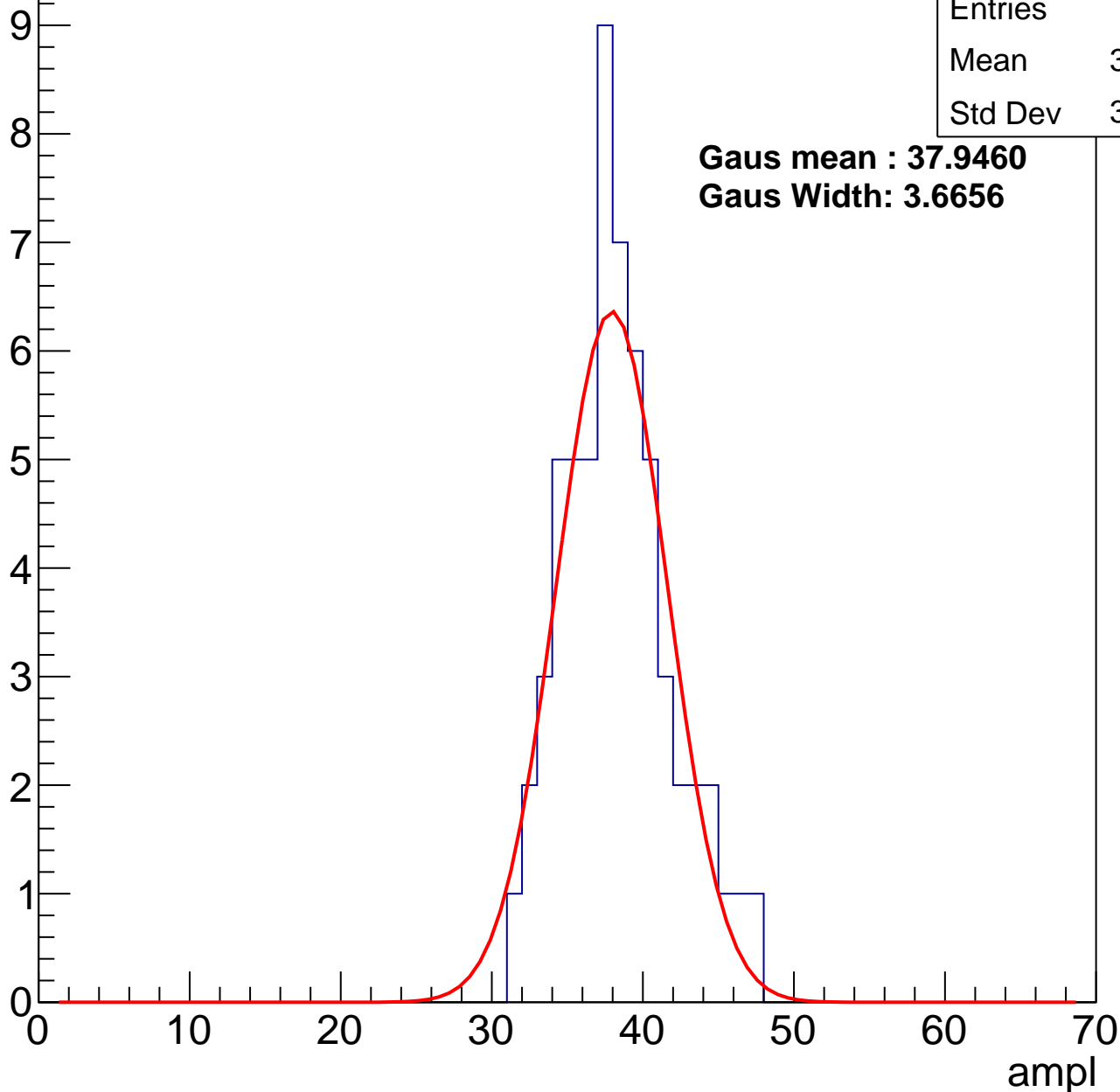
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.85
Std Dev	3.535

Gaus mean : 37.9460

Gaus Width: 3.6656



B1L101S, U25-ch90, adc2

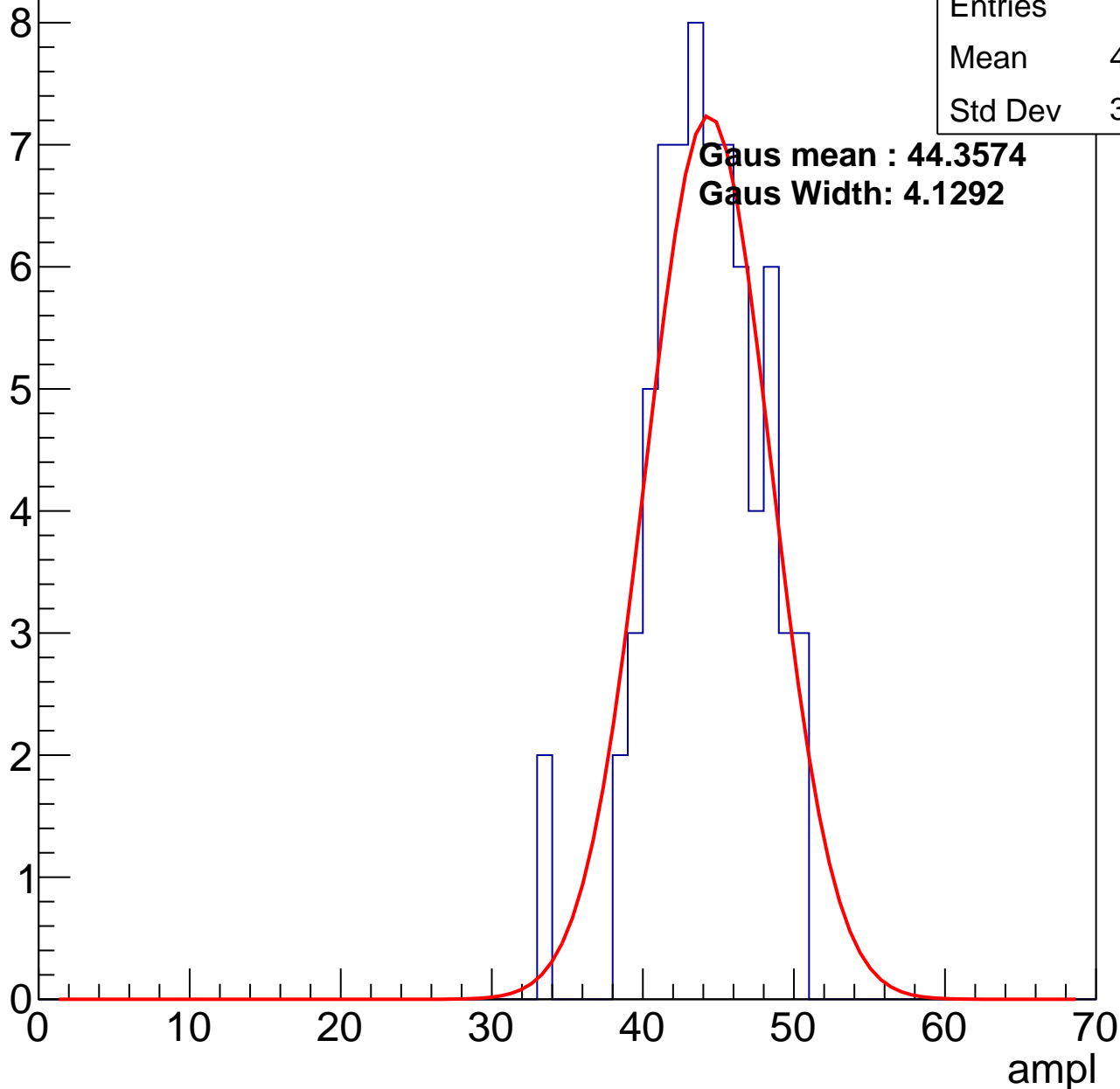
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.66
Std Dev	3.597

Gaus mean : 44.3574

Gaus Width: 4.1292



B1L101S, U25-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	50.89
Std Dev	3.517

Entry

10

8

6

4

2

0

0

10

20

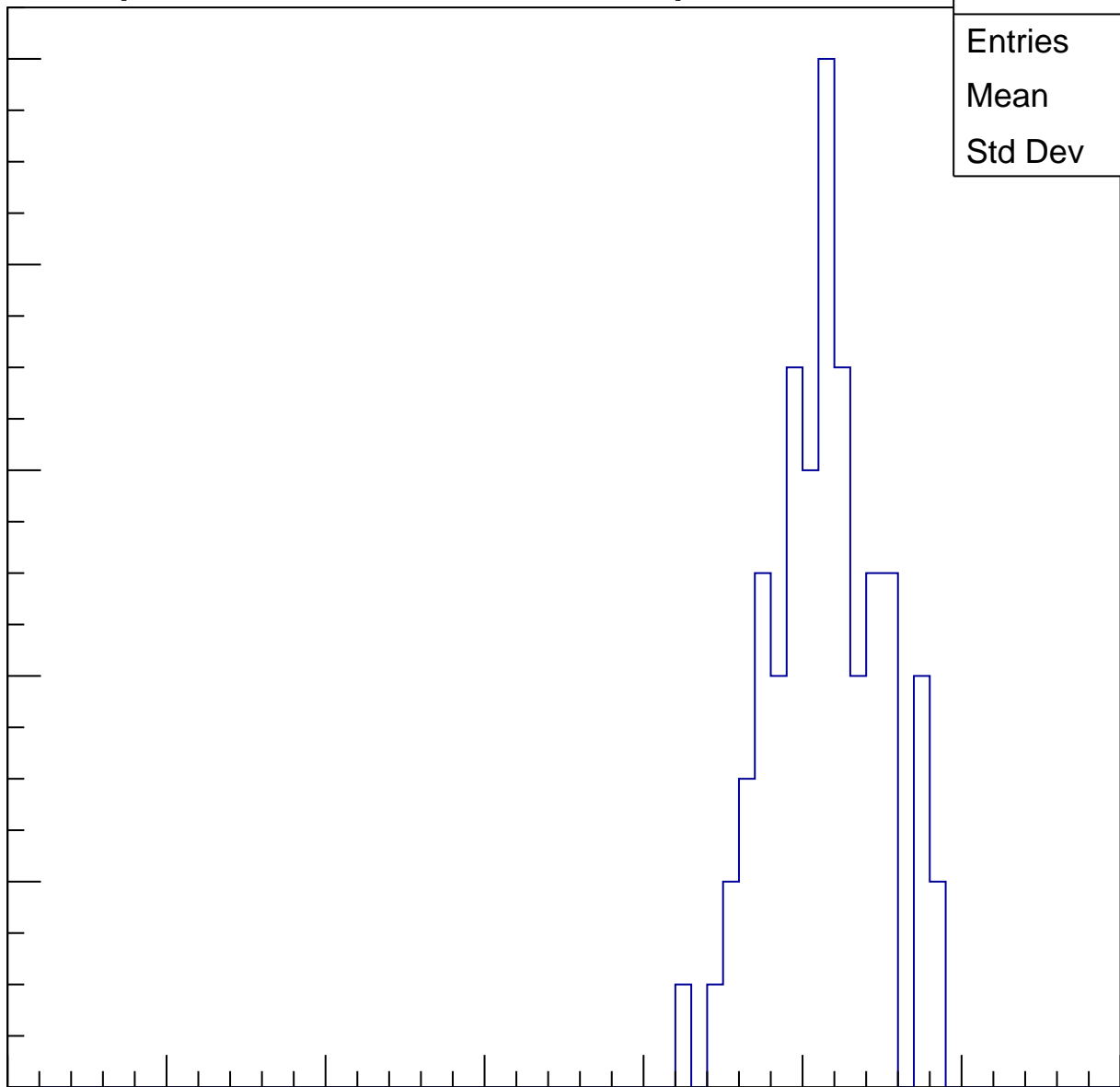
30

40

50

60

ampl



B1L101S, U25-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	57.39
Std Dev	3.195

Entry

10

8

6

4

2

0

0

10

20

30

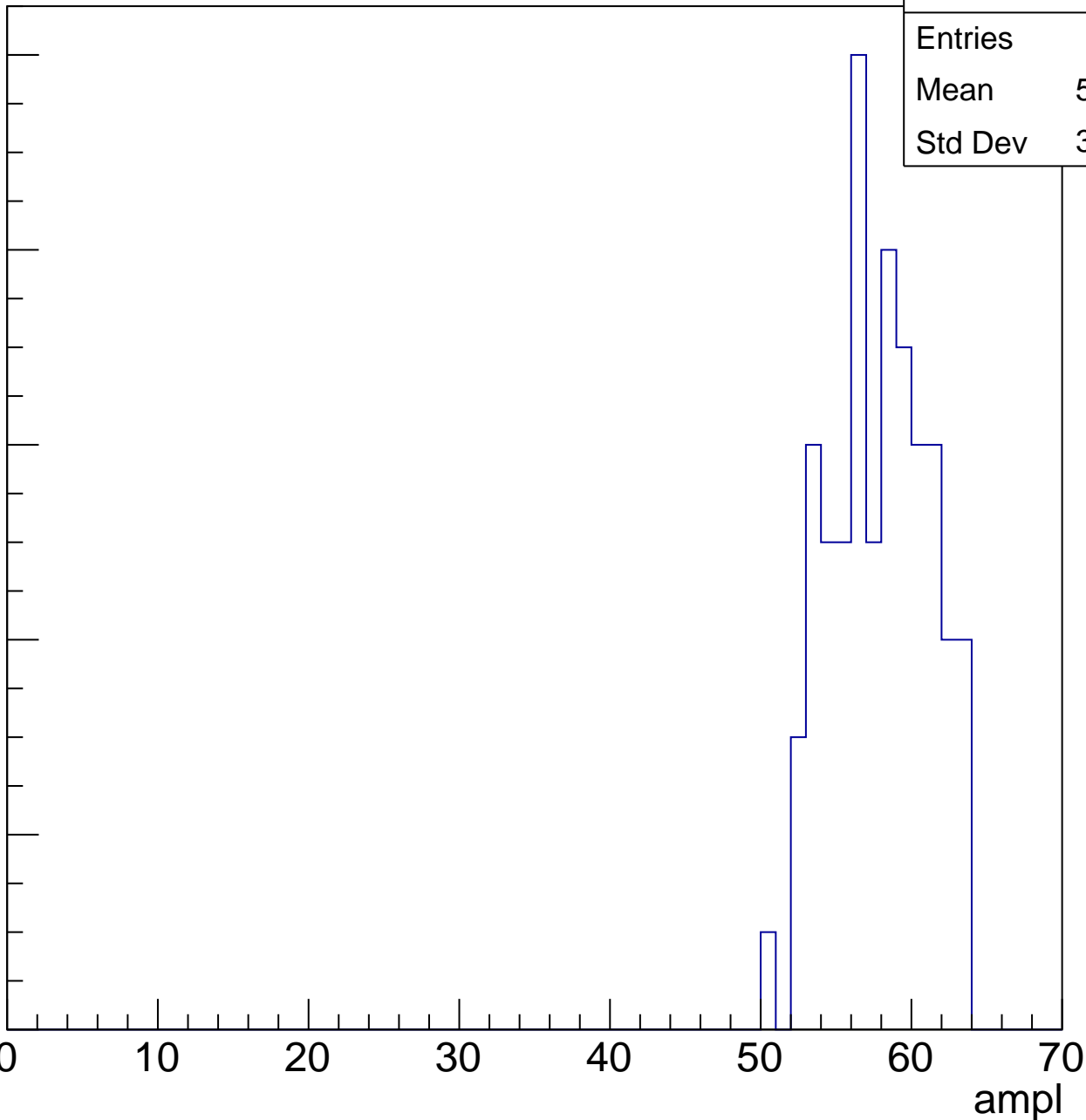
40

50

60

70

ampl

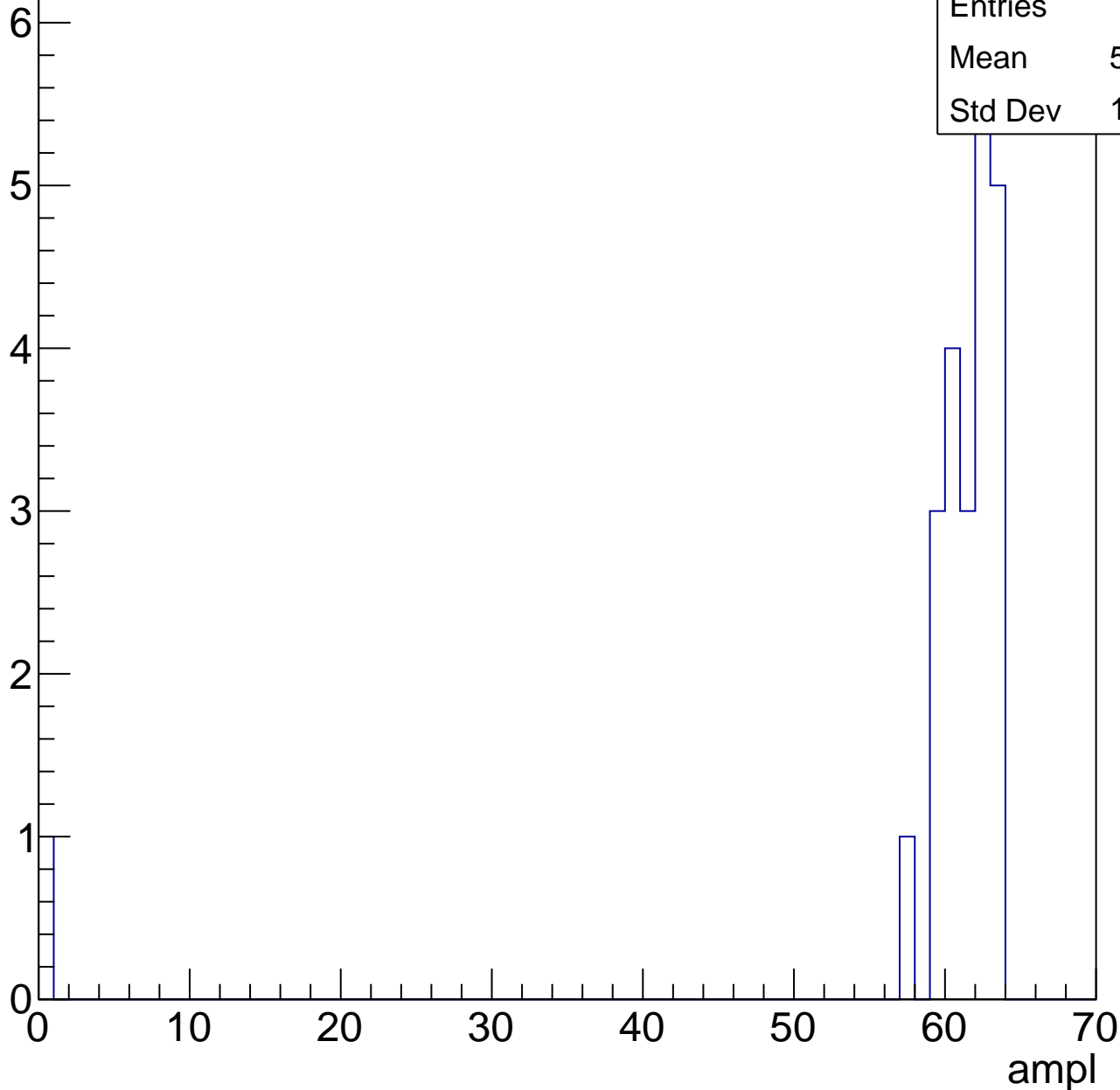


B1L101S, U25-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	58.43
Std Dev	12.56



B1L101S, U25-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch91, adc0

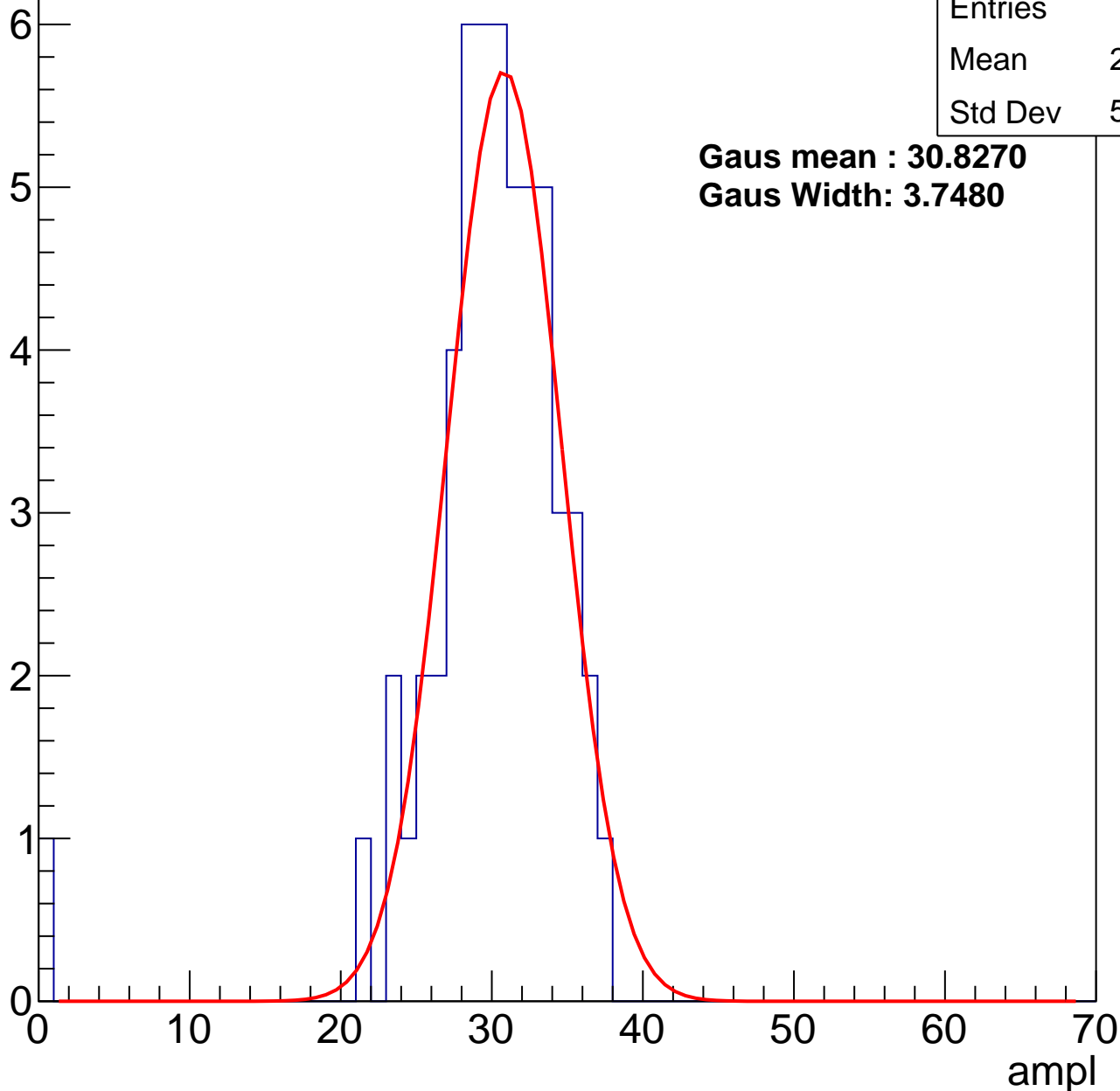
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	29.44
Std Dev	5.325

Gaus mean : 30.8270

Gaus Width: 3.7480



B1L101S, U25-ch91, adc1

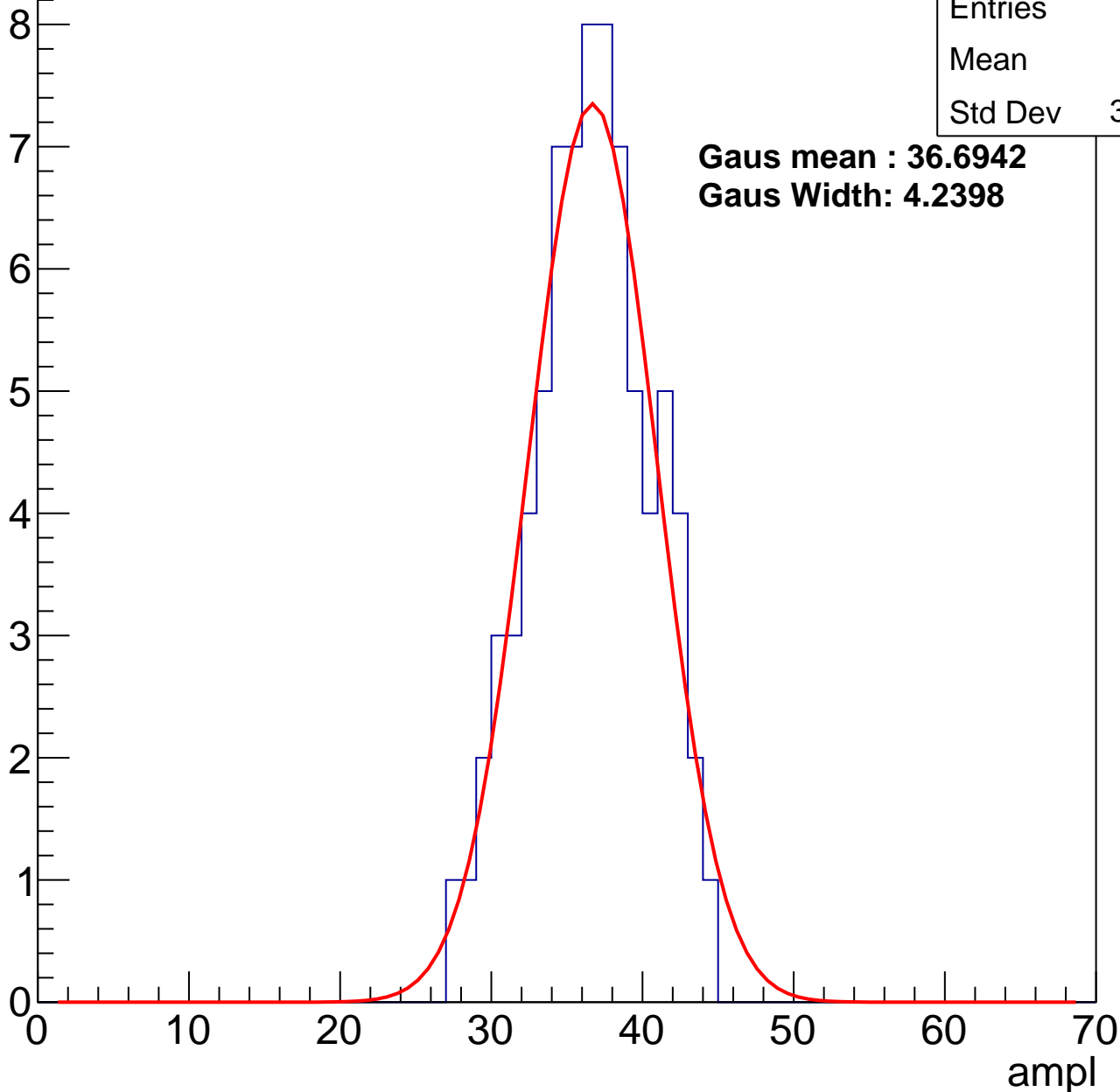
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.1
Std Dev	3.856

Gaus mean : 36.6942

Gaus Width: 4.2398



B1L101S, U25-ch91, adc2

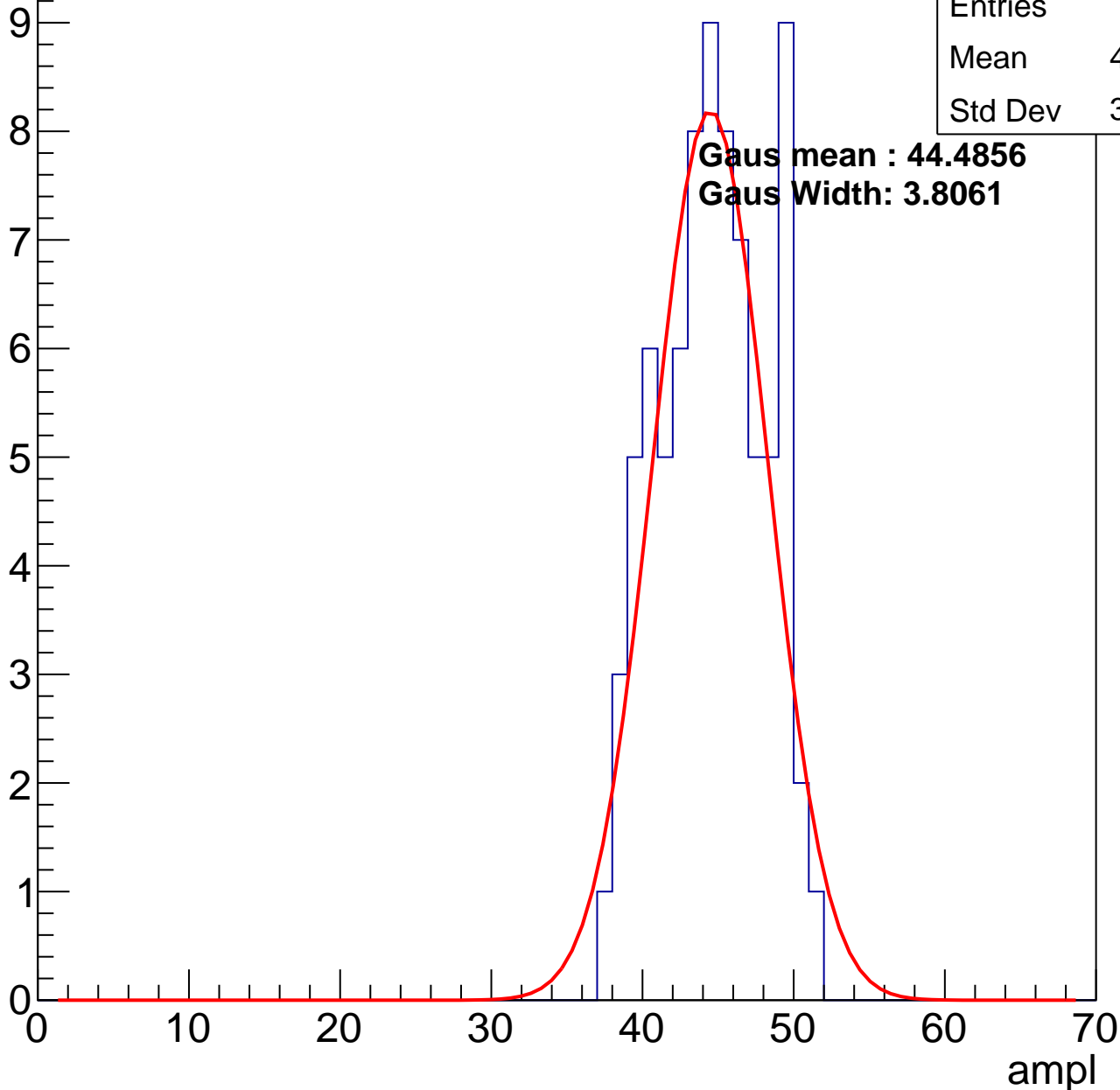
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	44.15
Std Dev	3.464

Gaus mean : 44.4856

Gaus Width: 3.8061

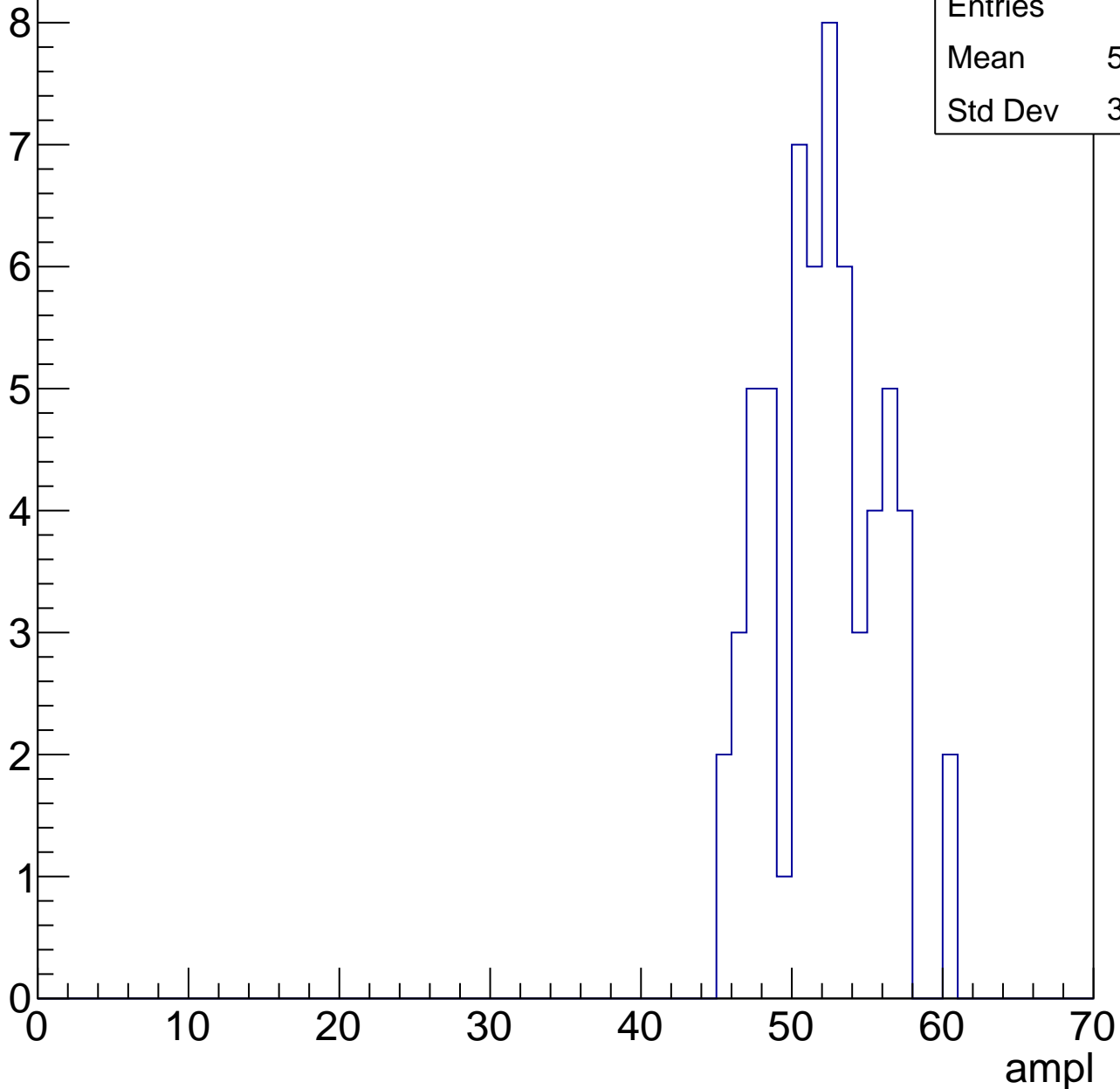


B1L101S, U25-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	51.67
Std Dev	3.643

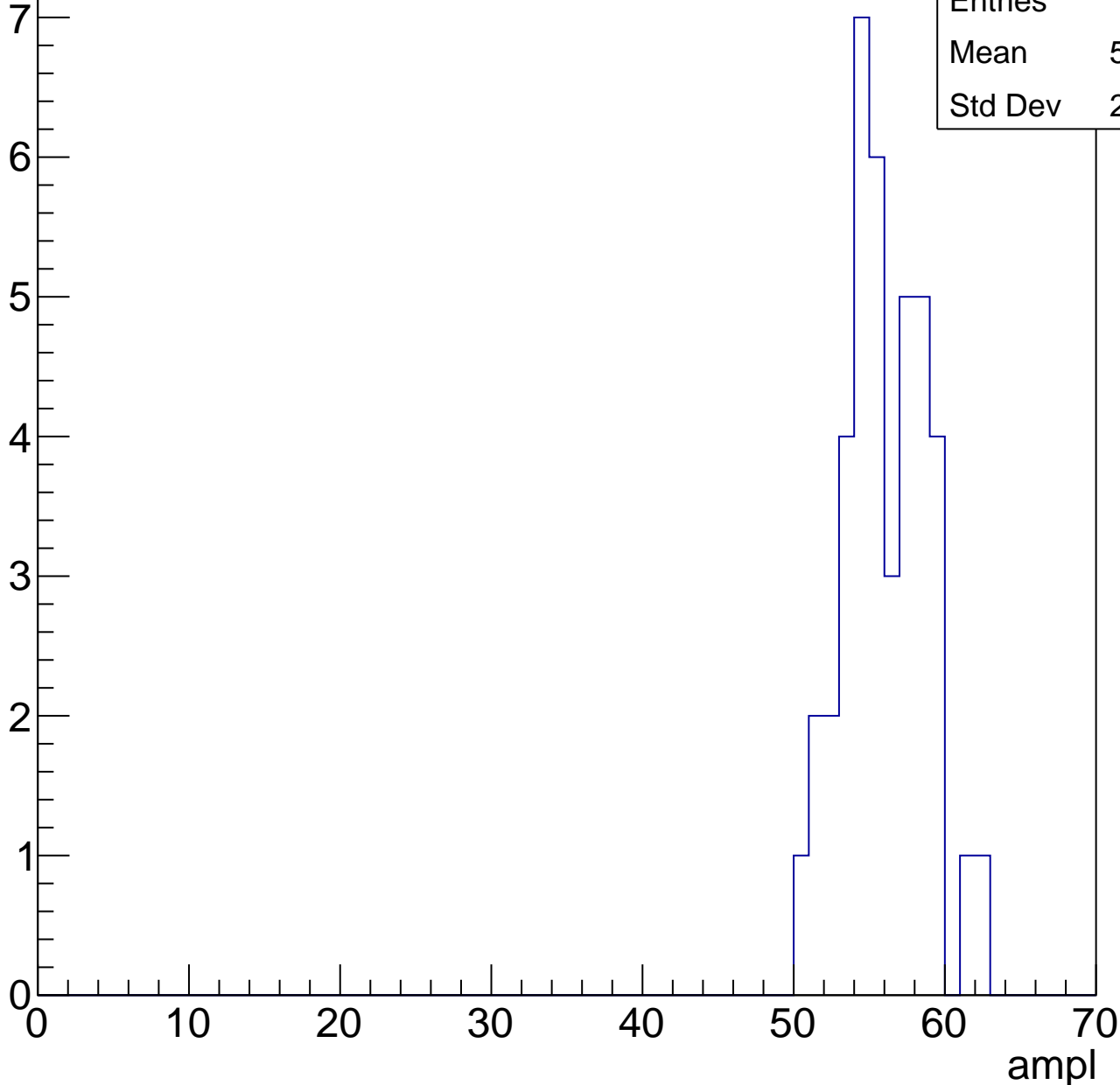


B1L101S, U25-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	55.56
Std Dev	2.714

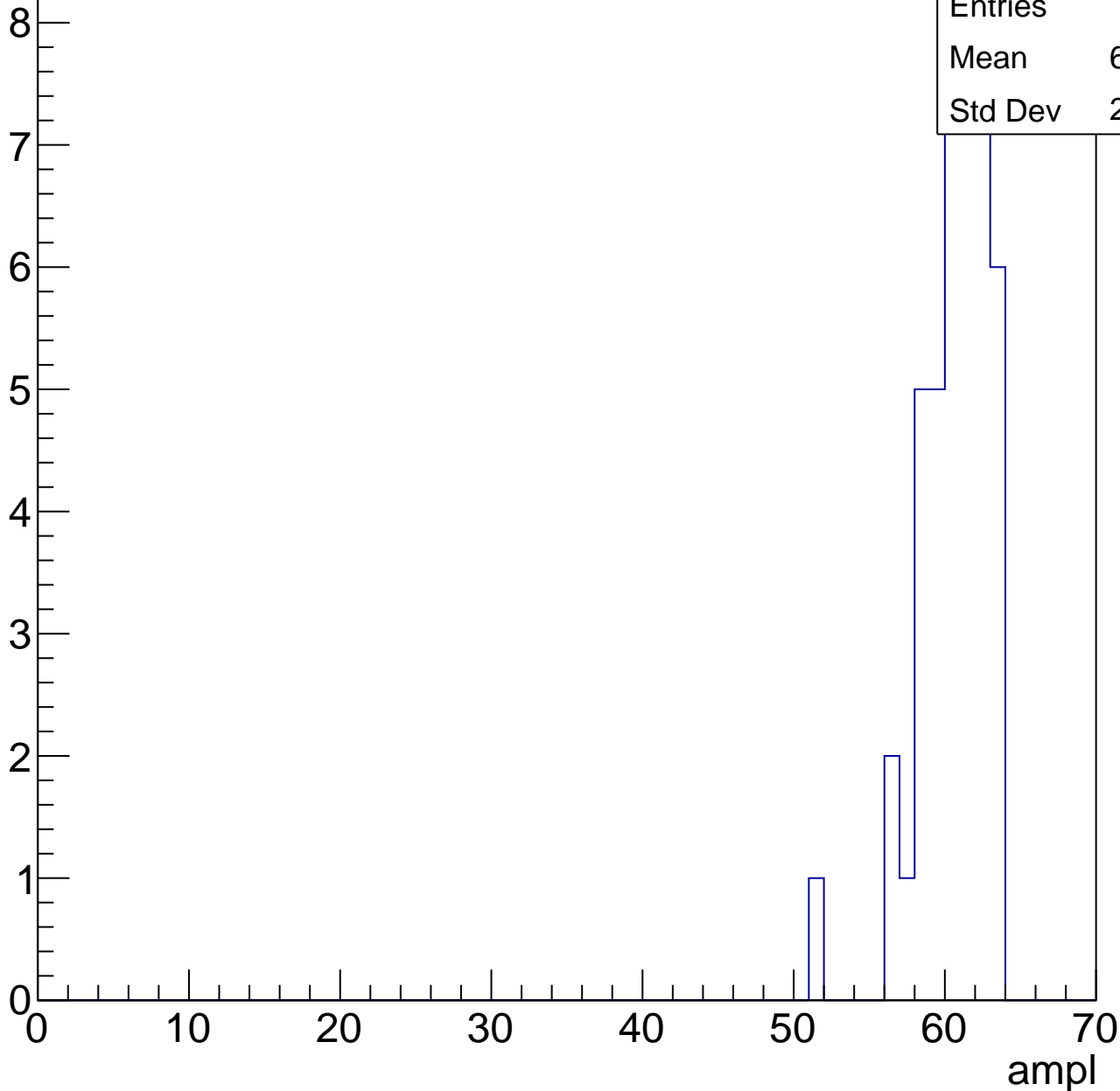


B1L101S, U25-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

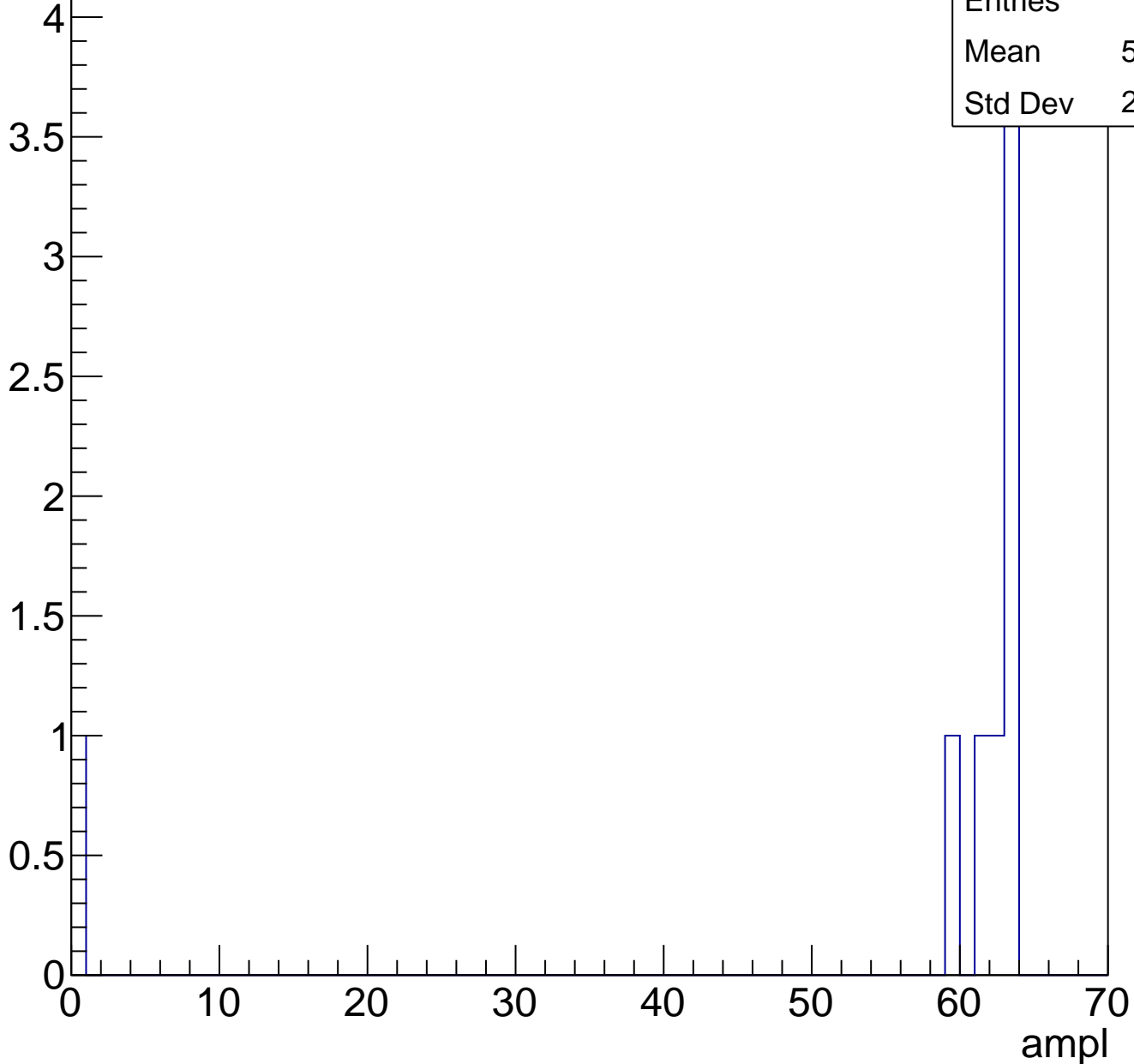
Entries	44
Mean	60.16
Std Dev	2.335



B1L101S, U25-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L101S, U25-ch92, adc0

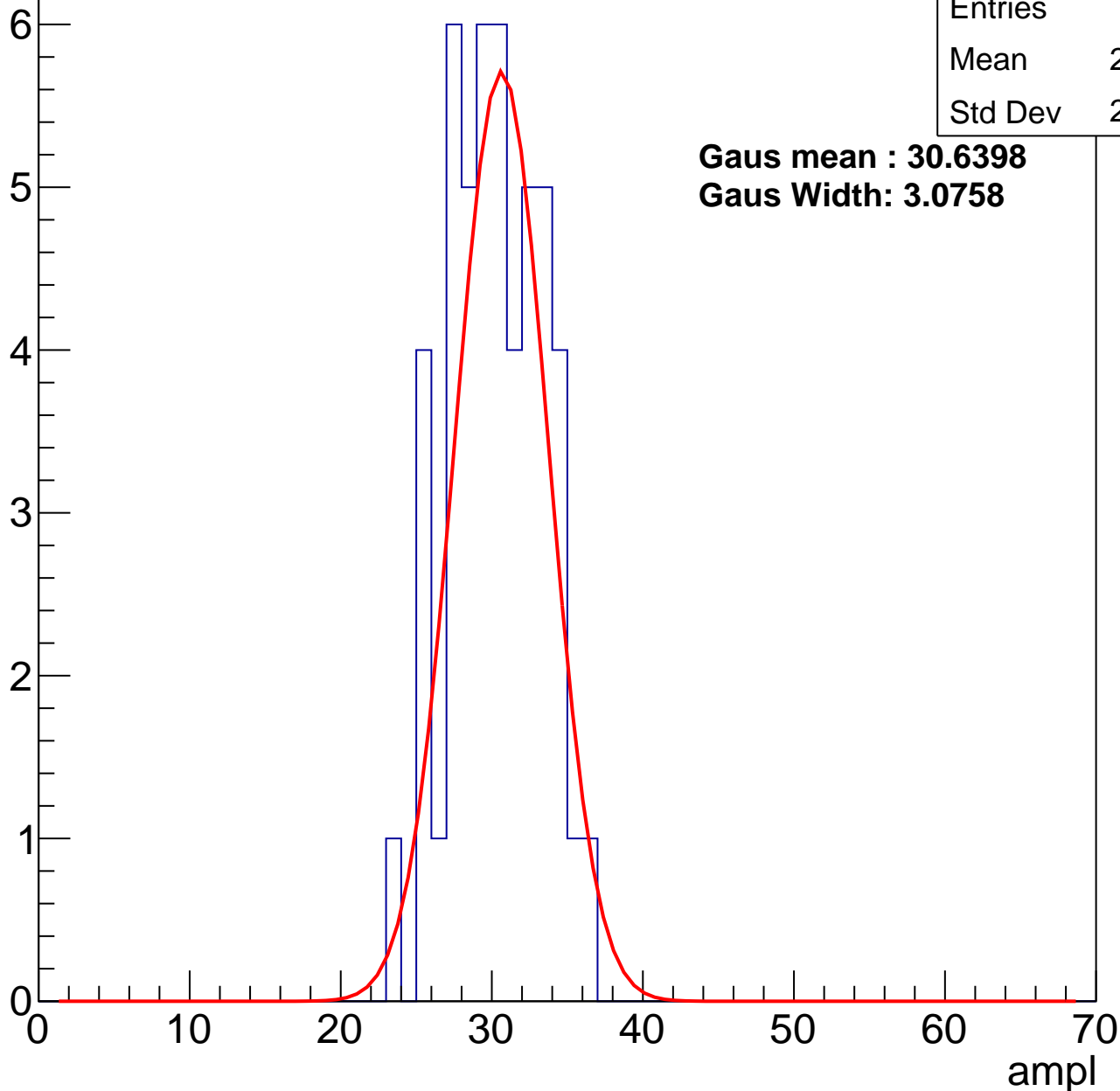
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	29.82
Std Dev	2.988

Gaus mean : 30.6398

Gaus Width: 3.0758



B1L101S, U25-ch92, adc1

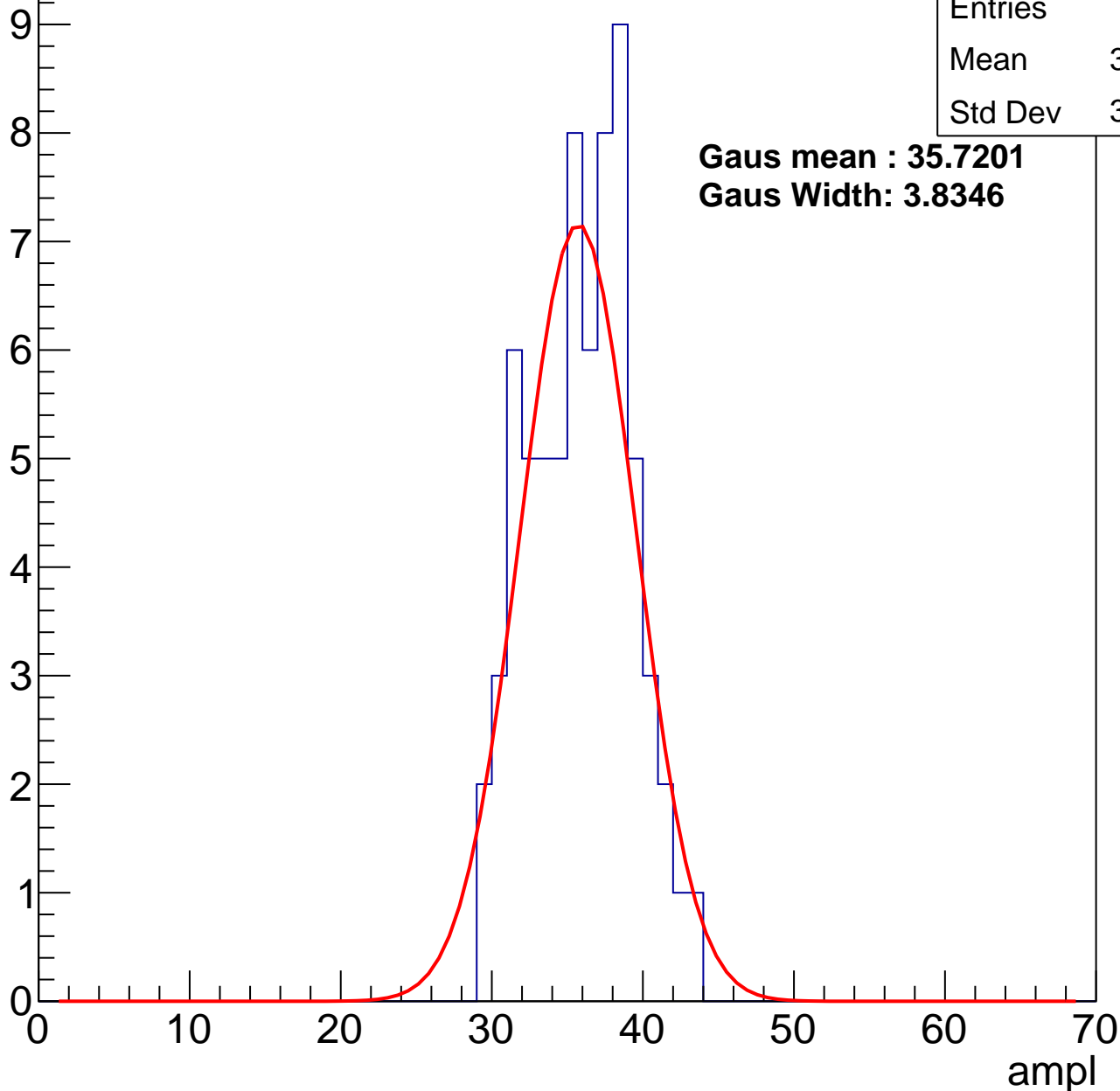
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.43
Std Dev	3.317

Gaus mean : 35.7201

Gaus Width: 3.8346



B1L101S, U25-ch92, adc2

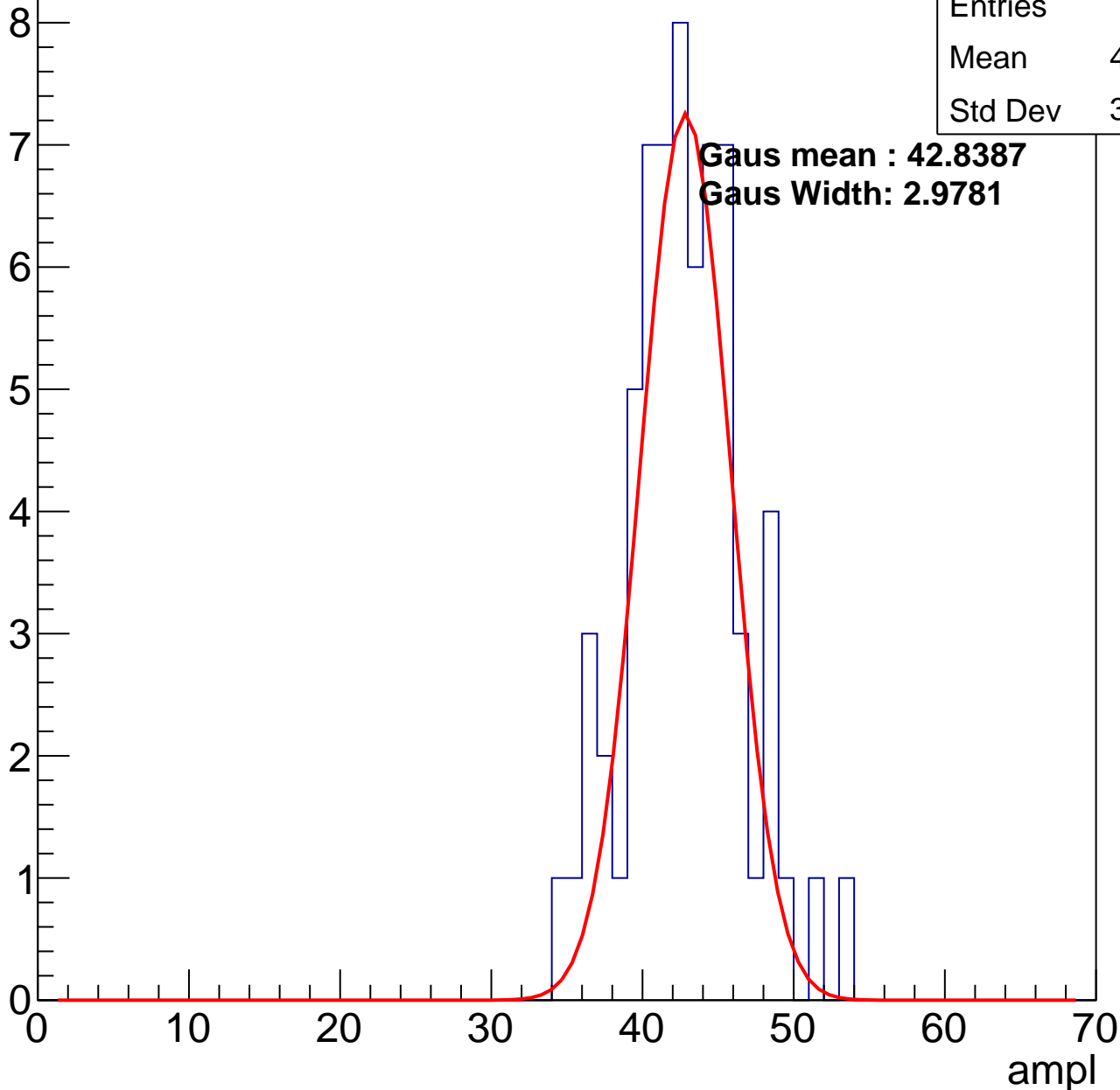
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.39
Std Dev	3.737

Gaus mean : 42.8387

Gaus Width: 2.9781



B1L101S, U25-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	49.14
Std Dev	3.681

Entry

10

8

6

4

2

0

0

10

20

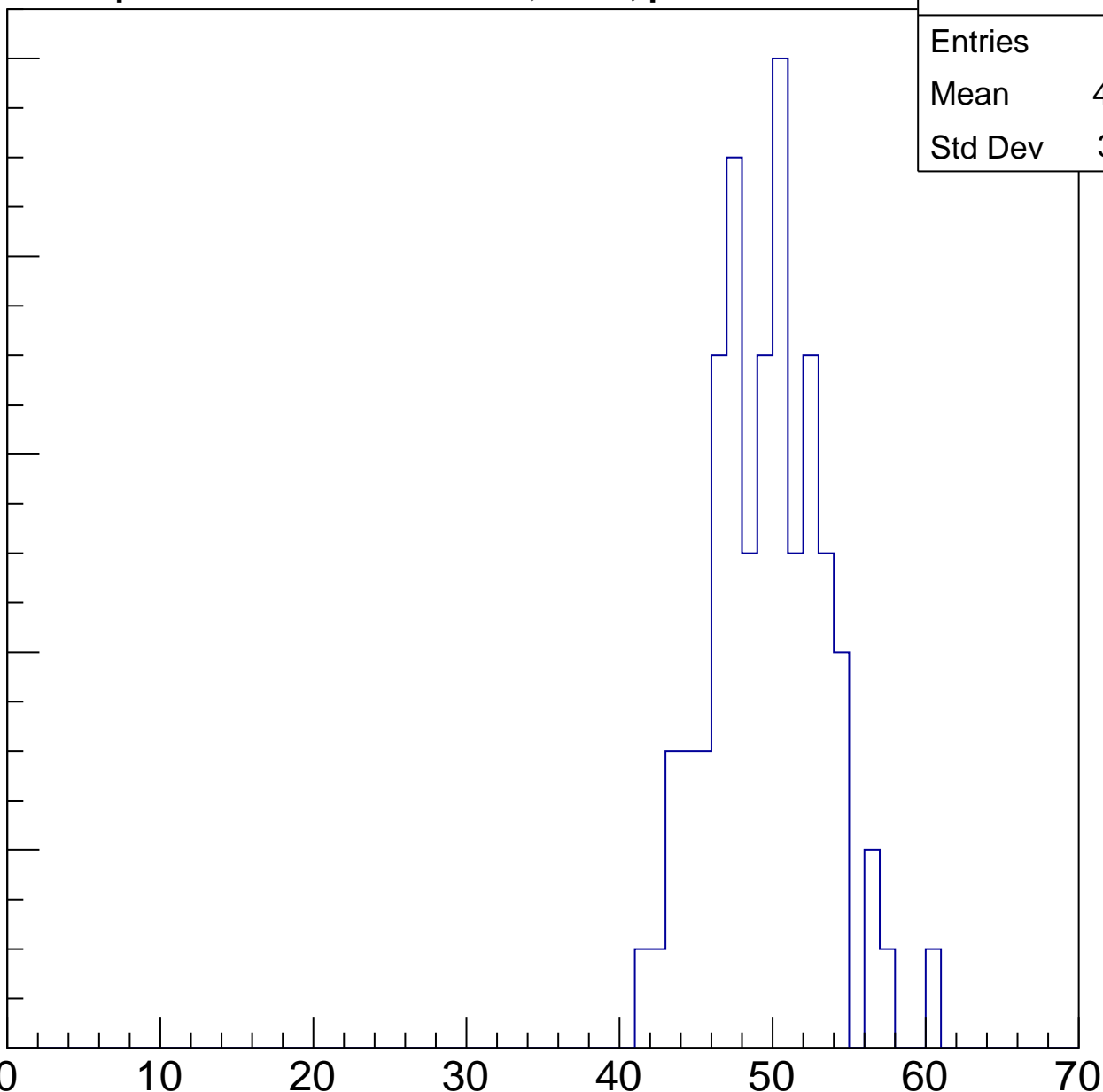
30

40

50

60

ampl

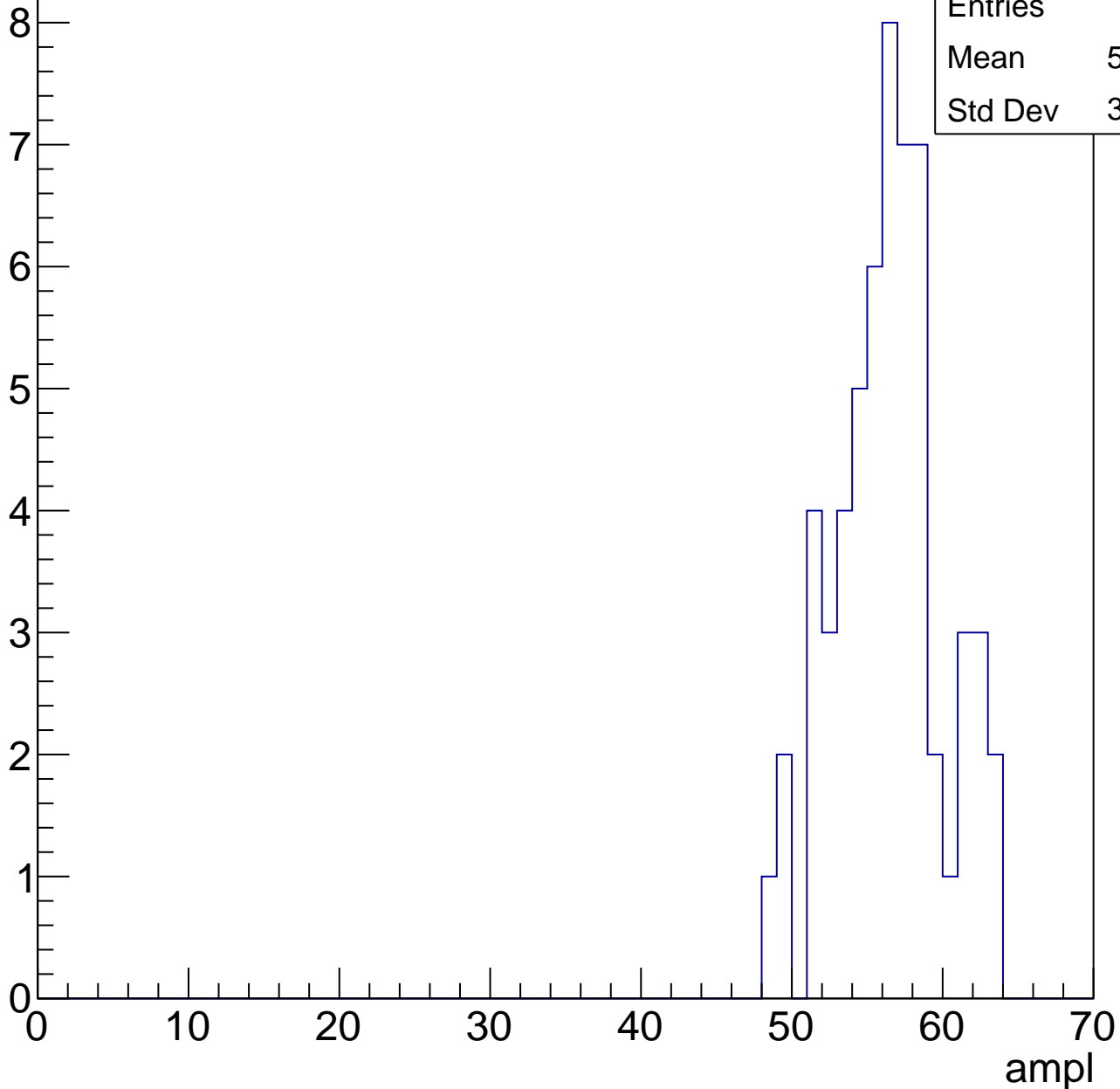


B1L101S, U25-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

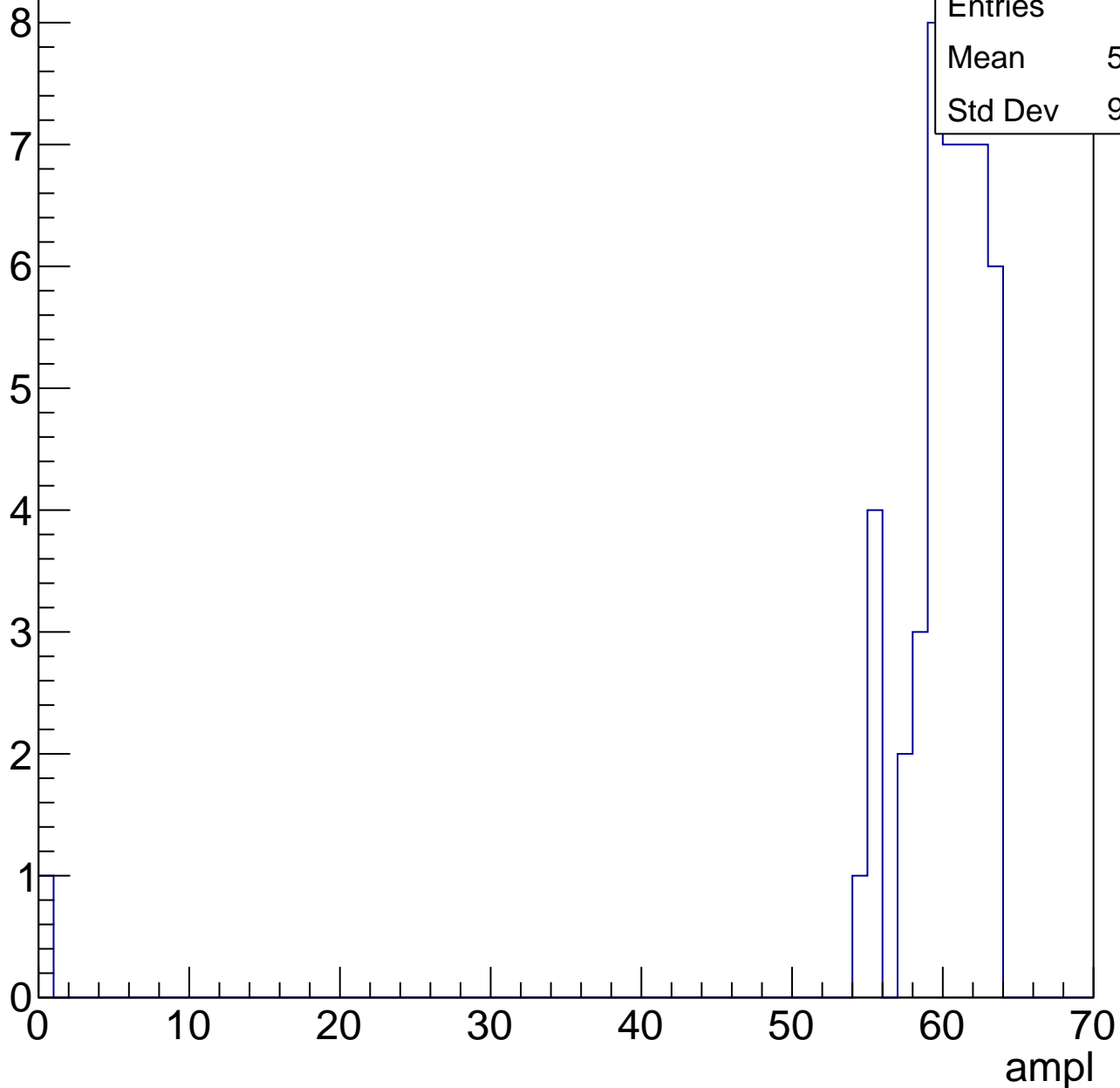
Entries	58
Mean	55.93
Std Dev	3.528



B1L101S, U25-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U25-ch93, adc0

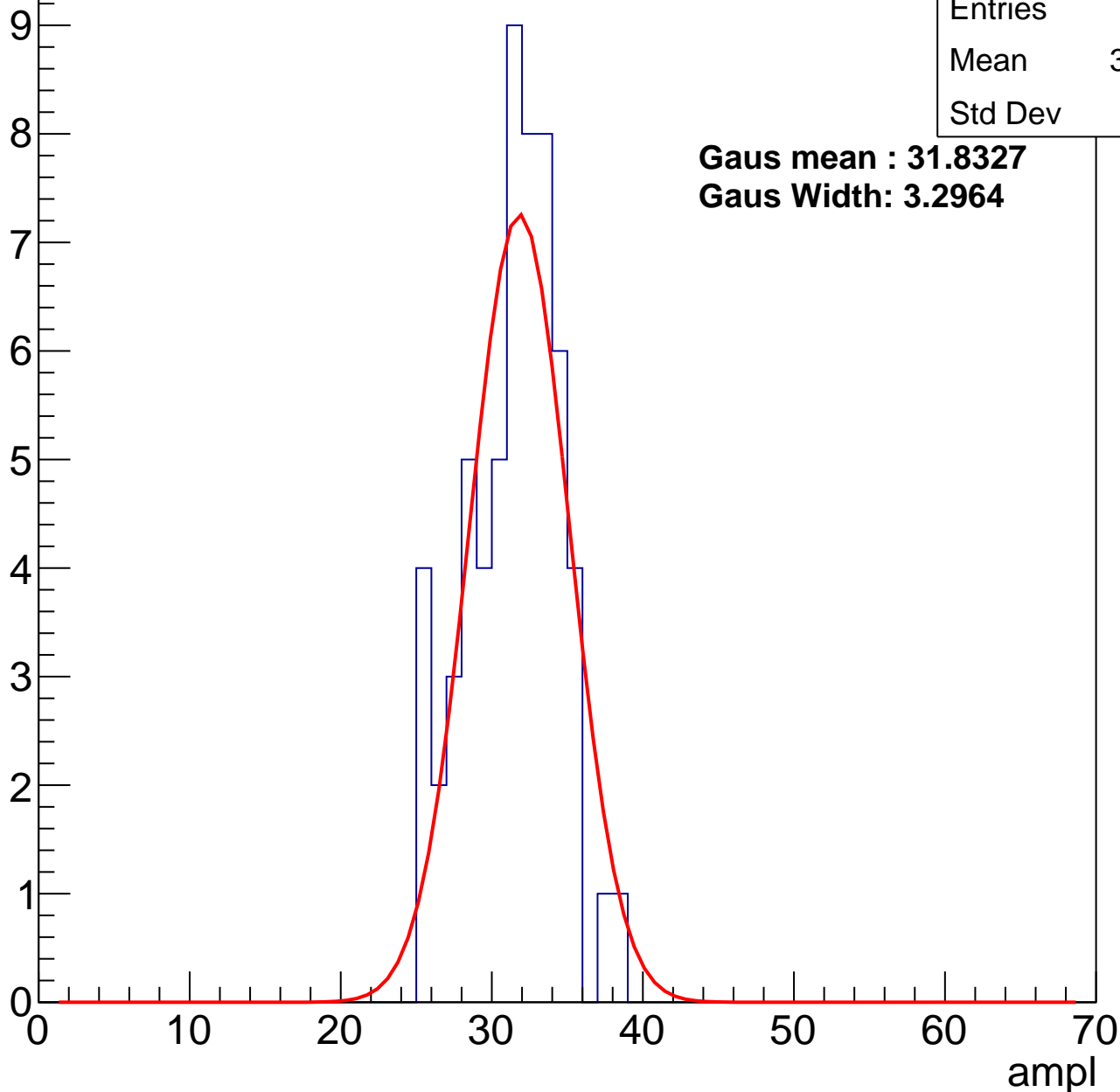
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.95
Std Dev	3.03

Gaus mean : 31.8327

Gaus Width: 3.2964



B1L101S, U25-ch93, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	38.18
Std Dev	3.303

Gaus mean : 38.8316

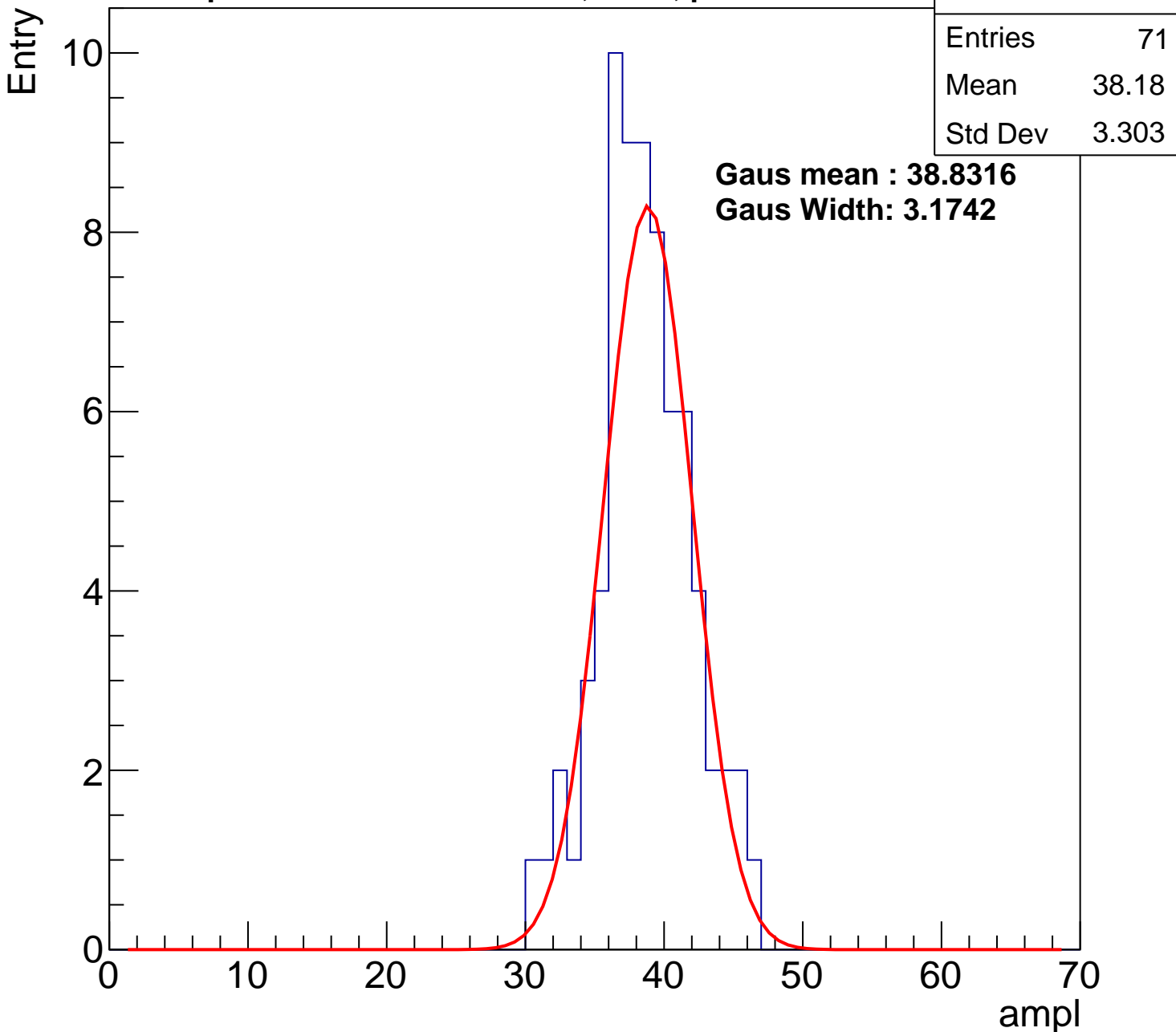
Gaus Width: 3.1742

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch93, adc2

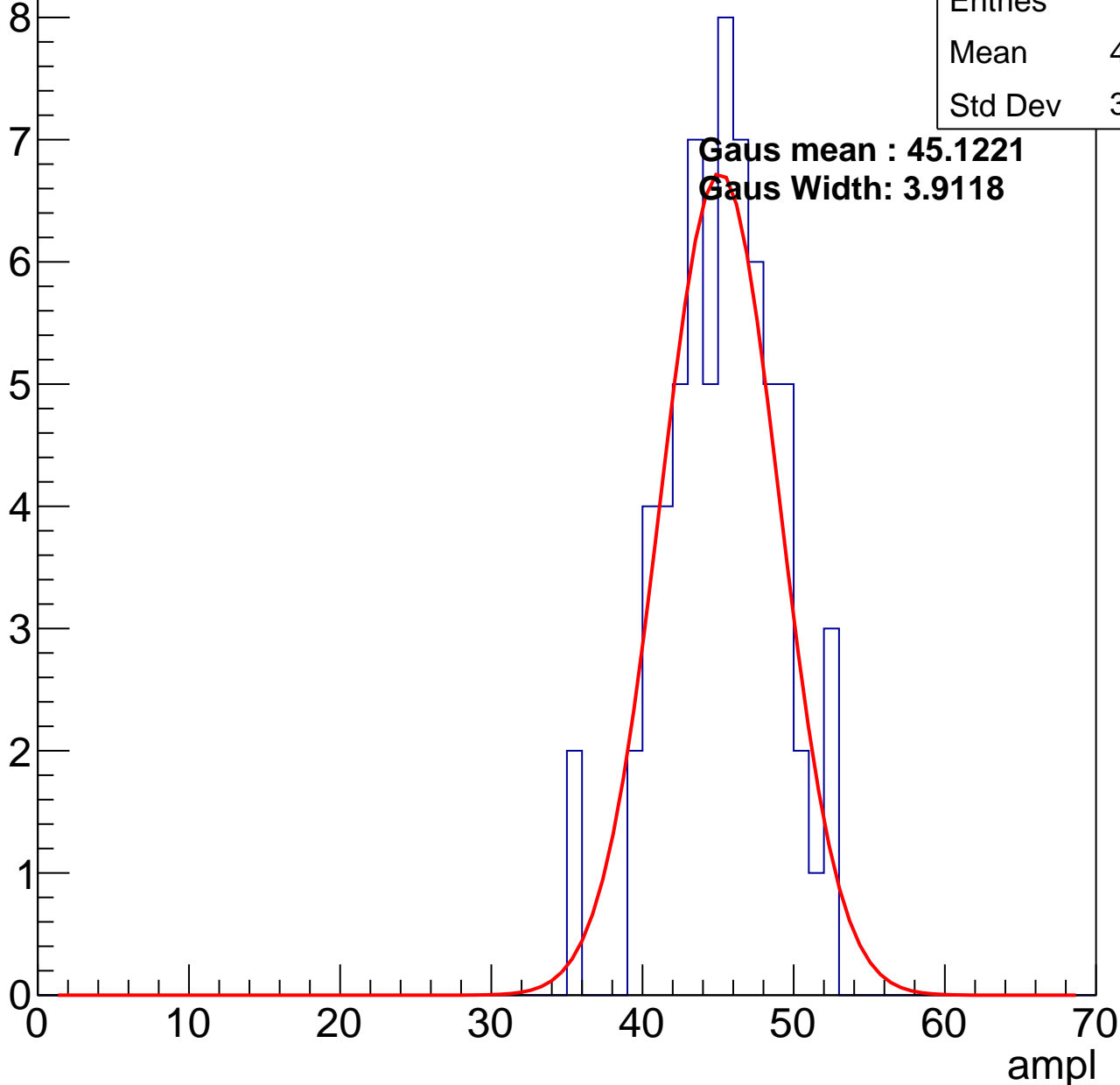
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.83
Std Dev	3.695

Gaus mean : 45.1221

Gaus Width: 3.9118

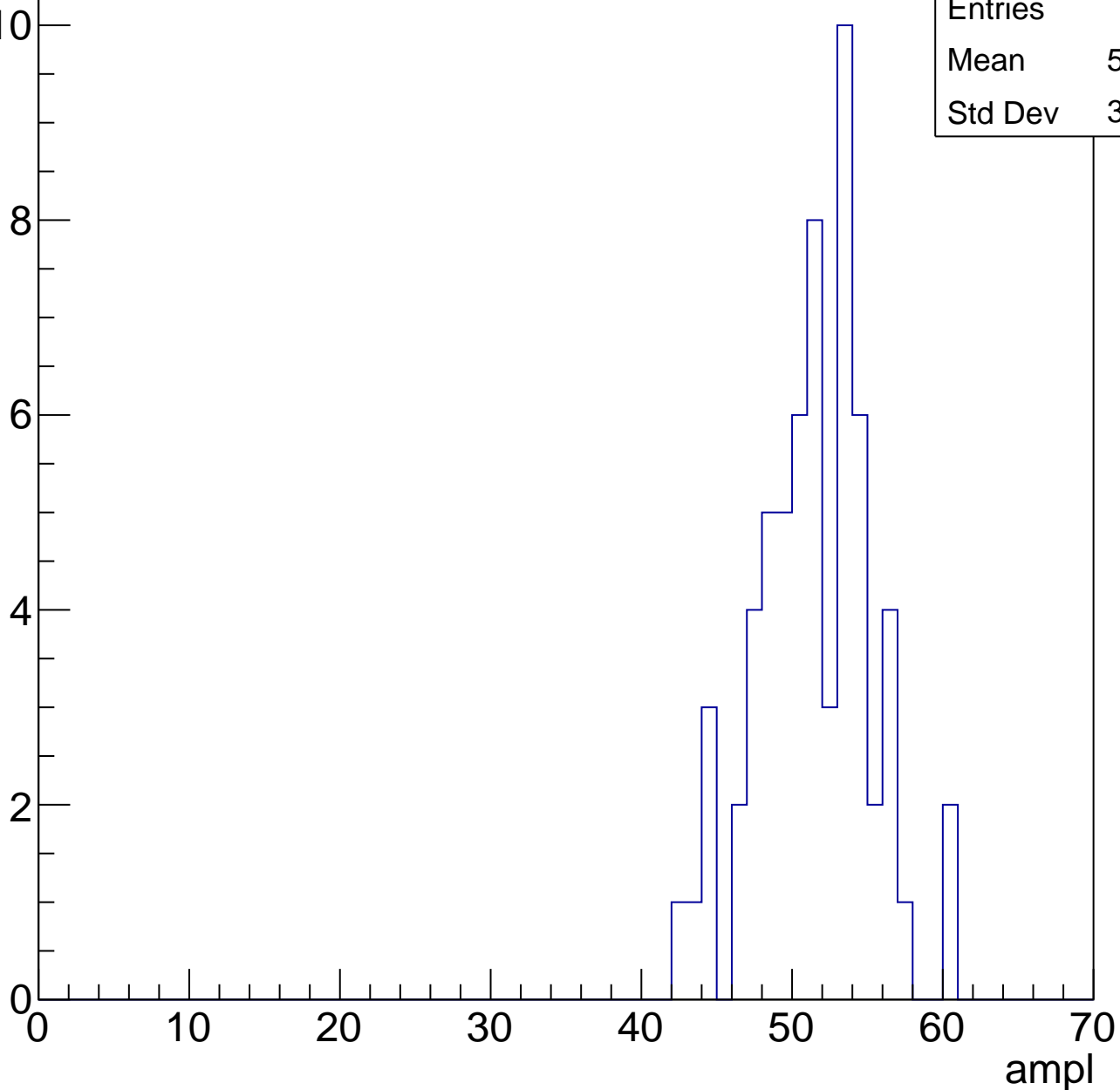


B1L101S, U25-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

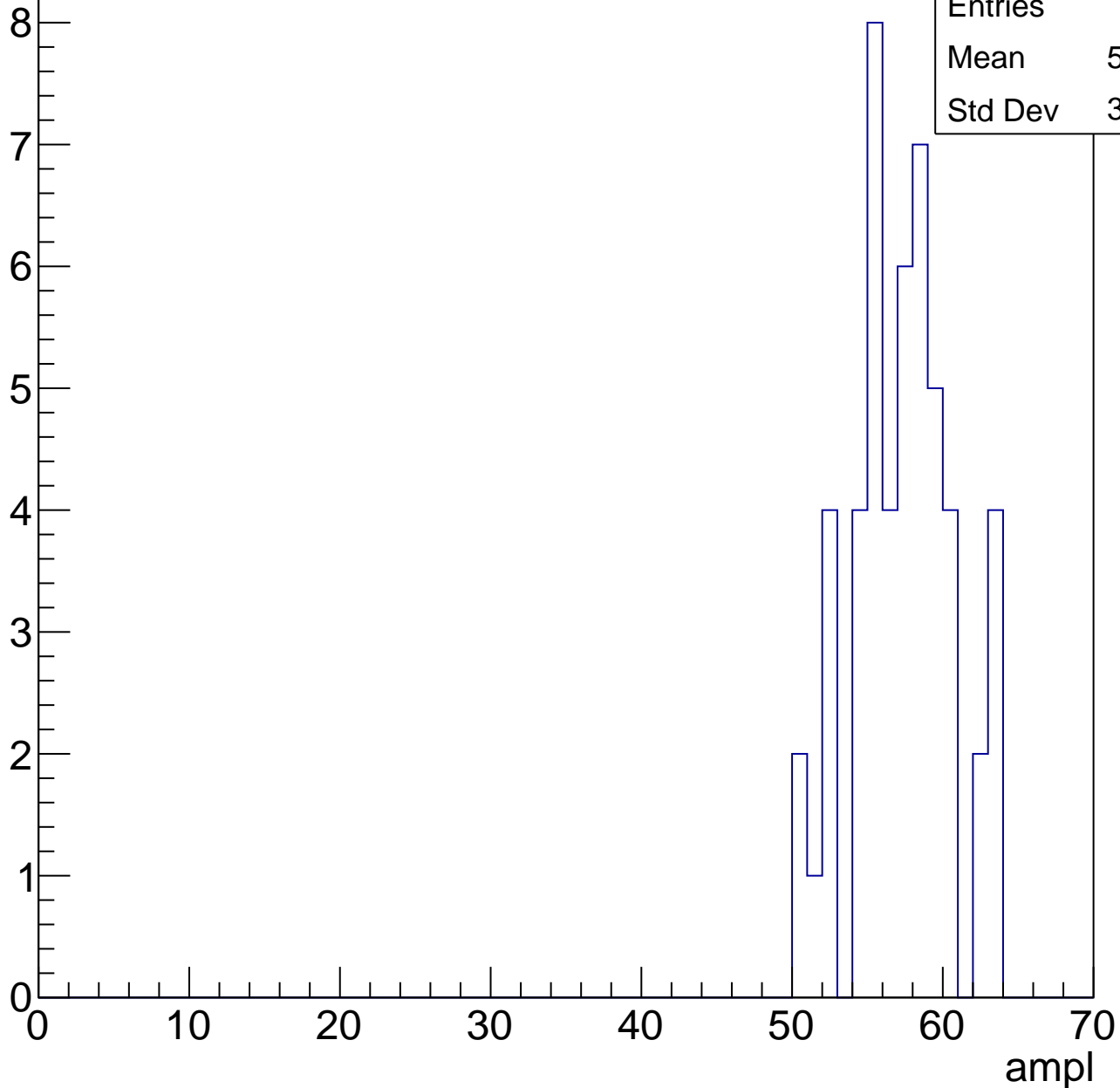
Entries	63
Mean	50.97
Std Dev	3.788



B1L101S, U25-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

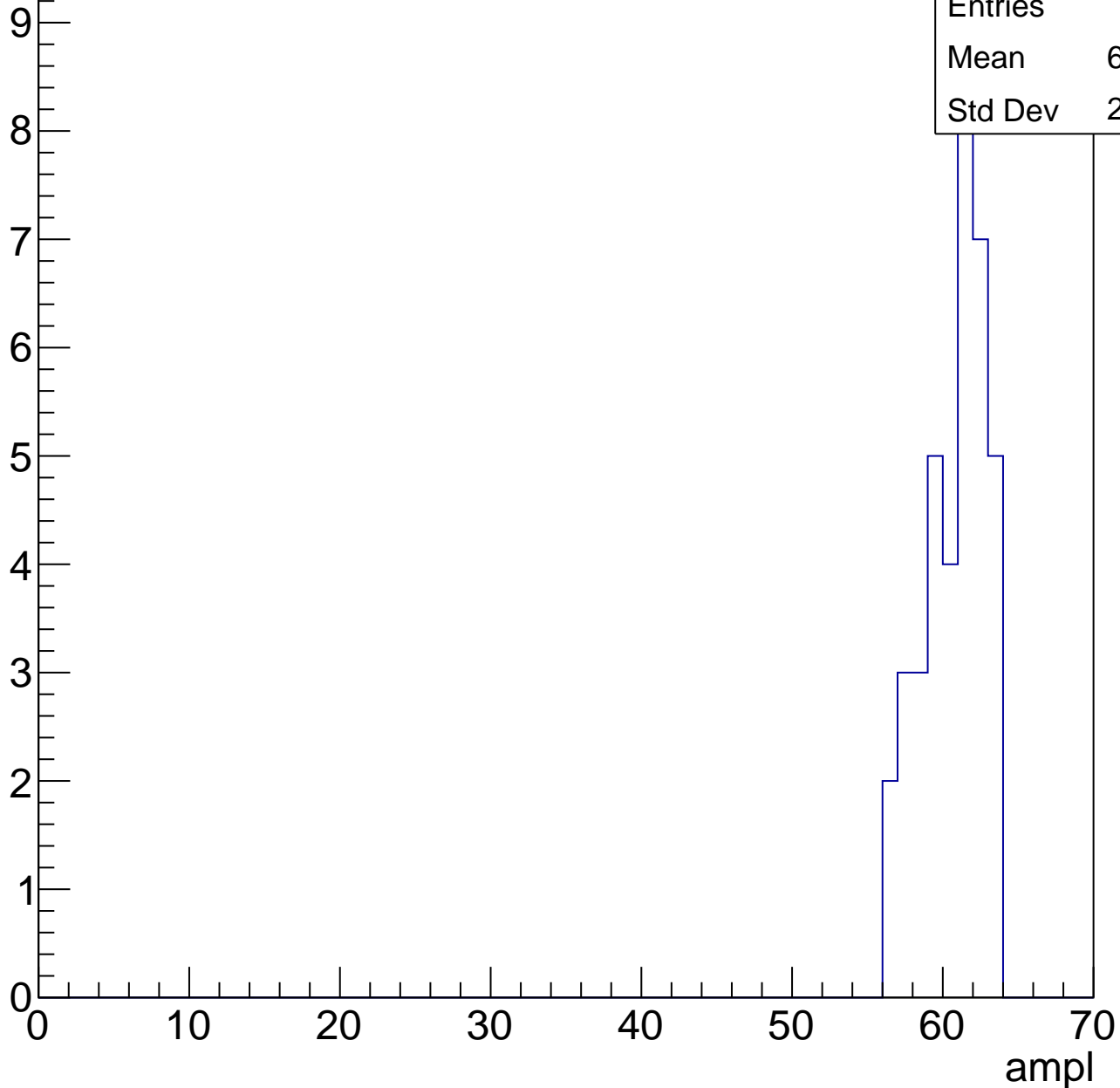


Entries	51
Mean	56.82
Std Dev	3.318

B1L101S, U25-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	38
Mean	60.26
Std Dev	2.022

B1L101S, U25-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch94, adc0

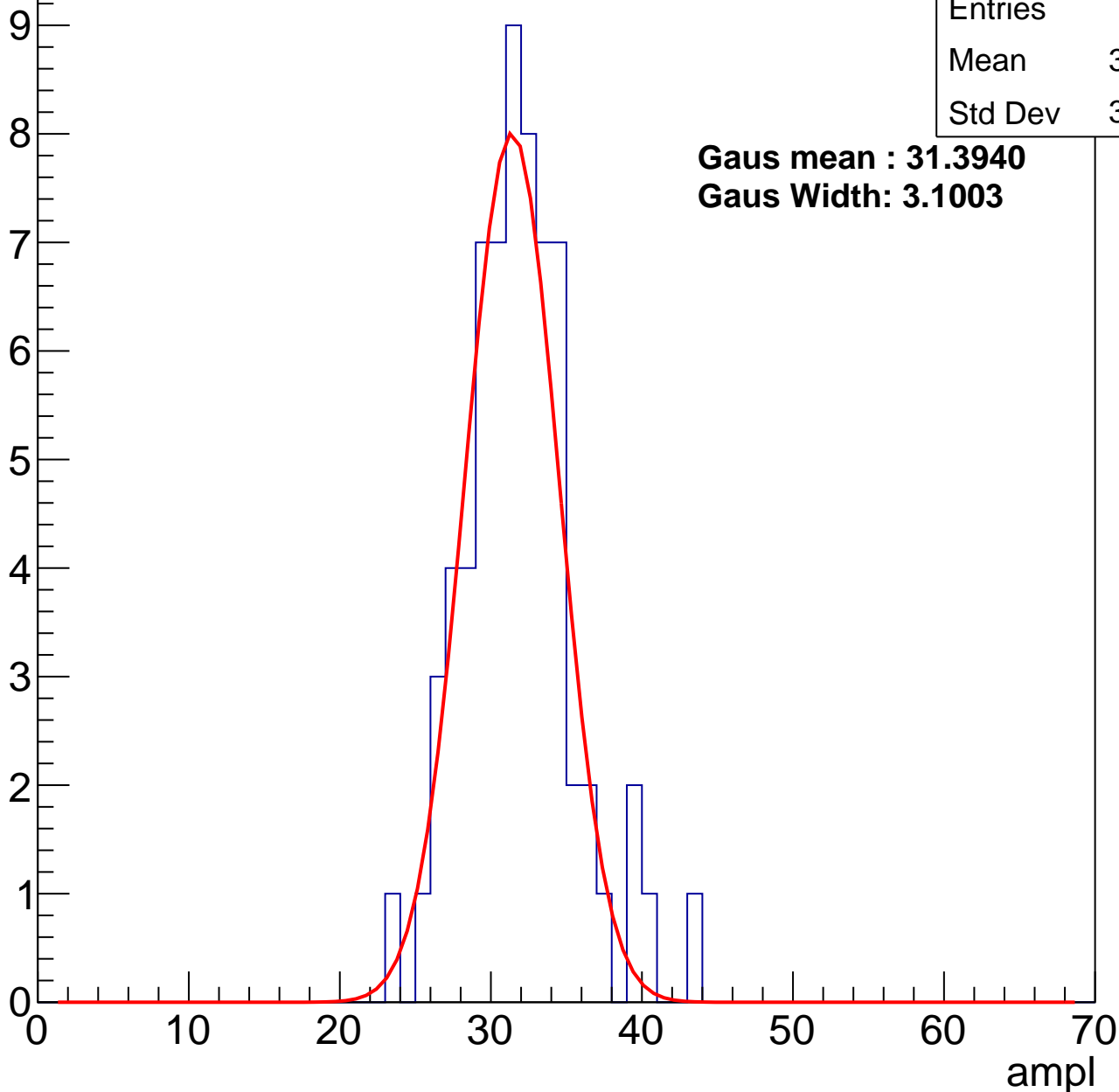
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	31.39
Std Dev	3.603

Gaus mean : 31.3940

Gaus Width: 3.1003



B1L101S, U25-ch94, adc1

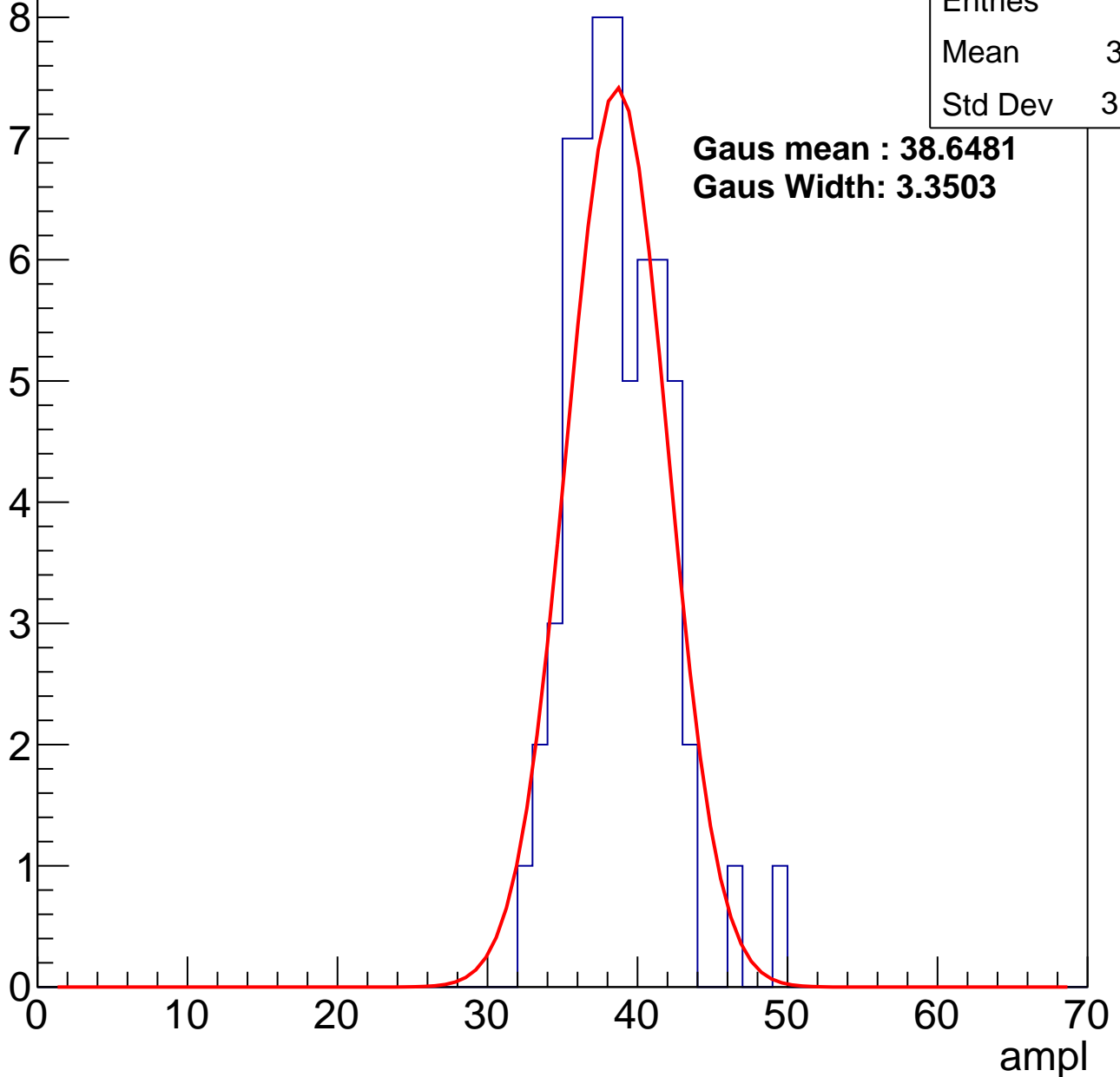
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	38.21
Std Dev	3.183

Gaus mean : 38.6481

Gaus Width: 3.3503



B1L101S, U25-ch94, adc2

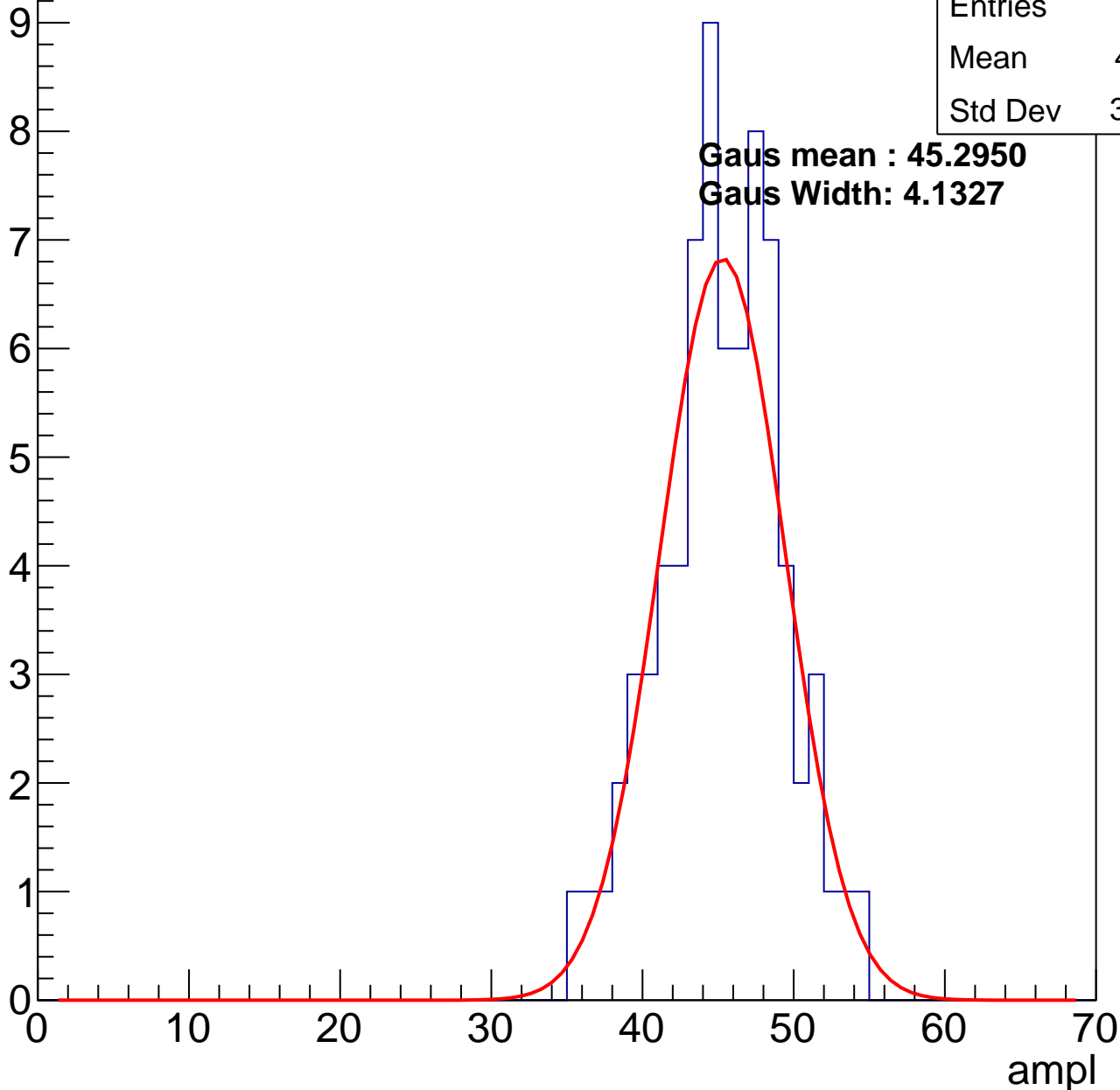
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.81
Std Dev	3.989

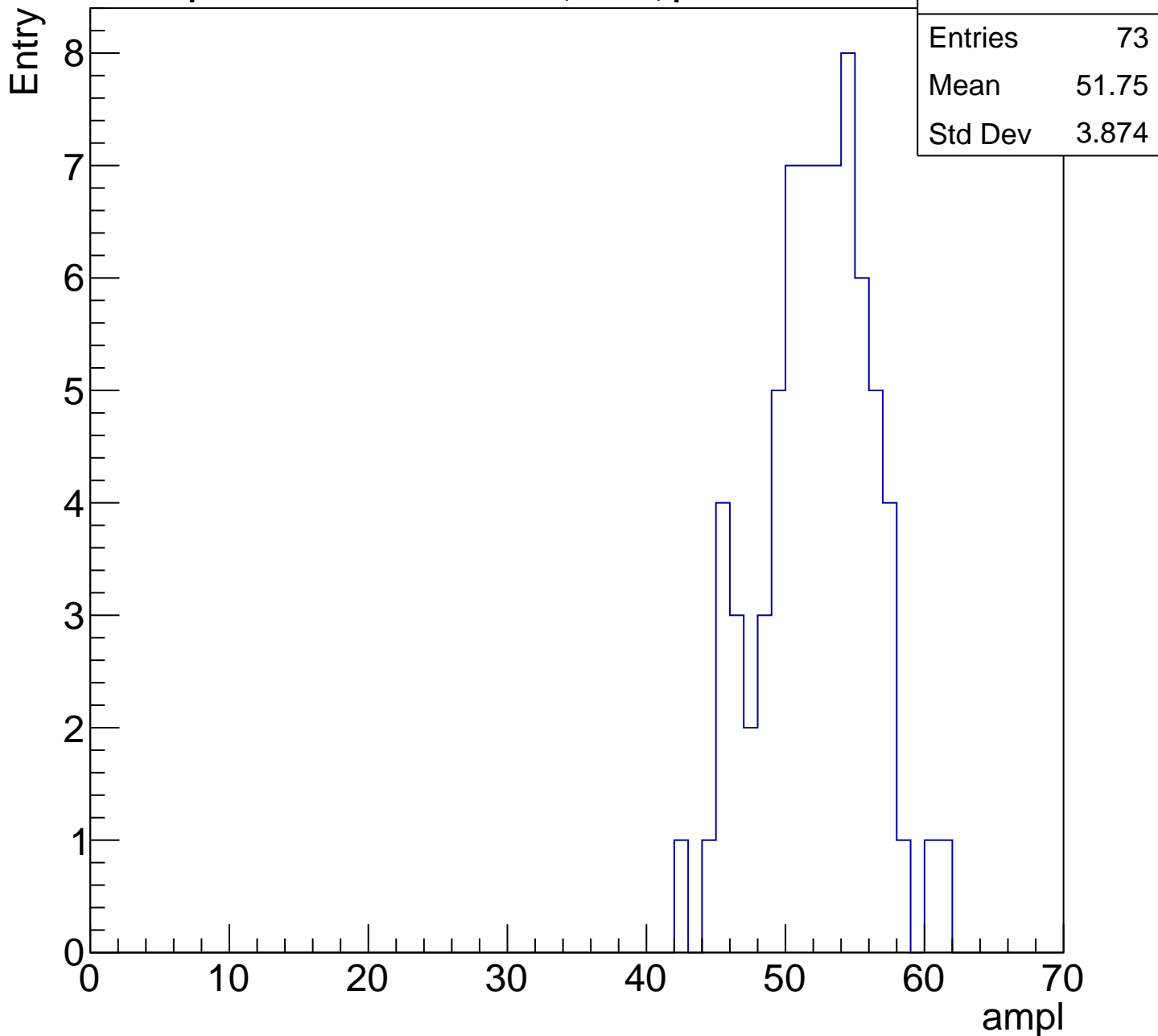
Gaus mean : 45.2950

Gaus Width: 4.1327



B1L101S, U25-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

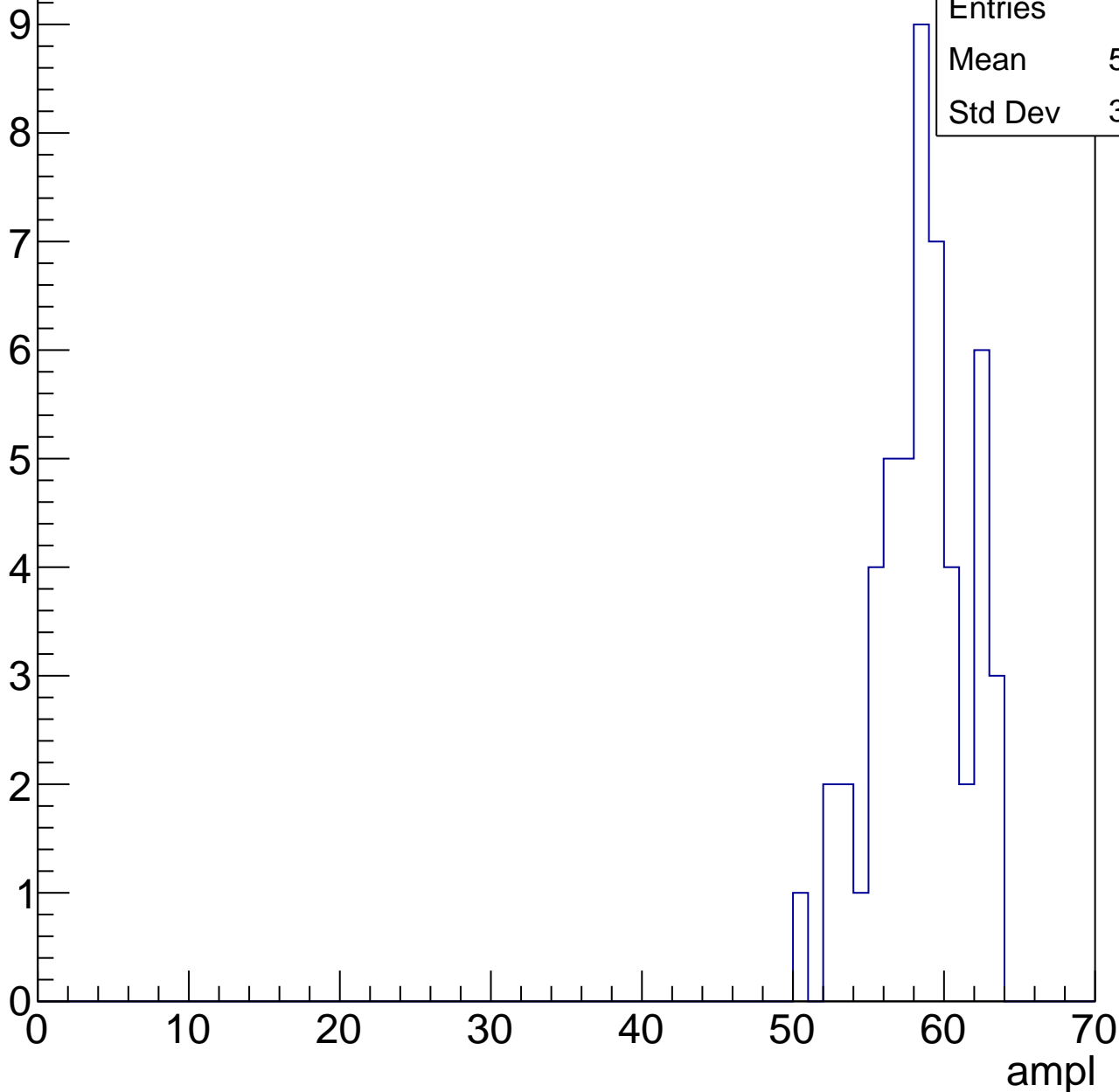


B1L101S, U25-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.98
Std Dev	3.052



B1L101S, U25-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	27
Mean	58.81
Std Dev	11.64

ampl

B1L101S, U25-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch95, adc0

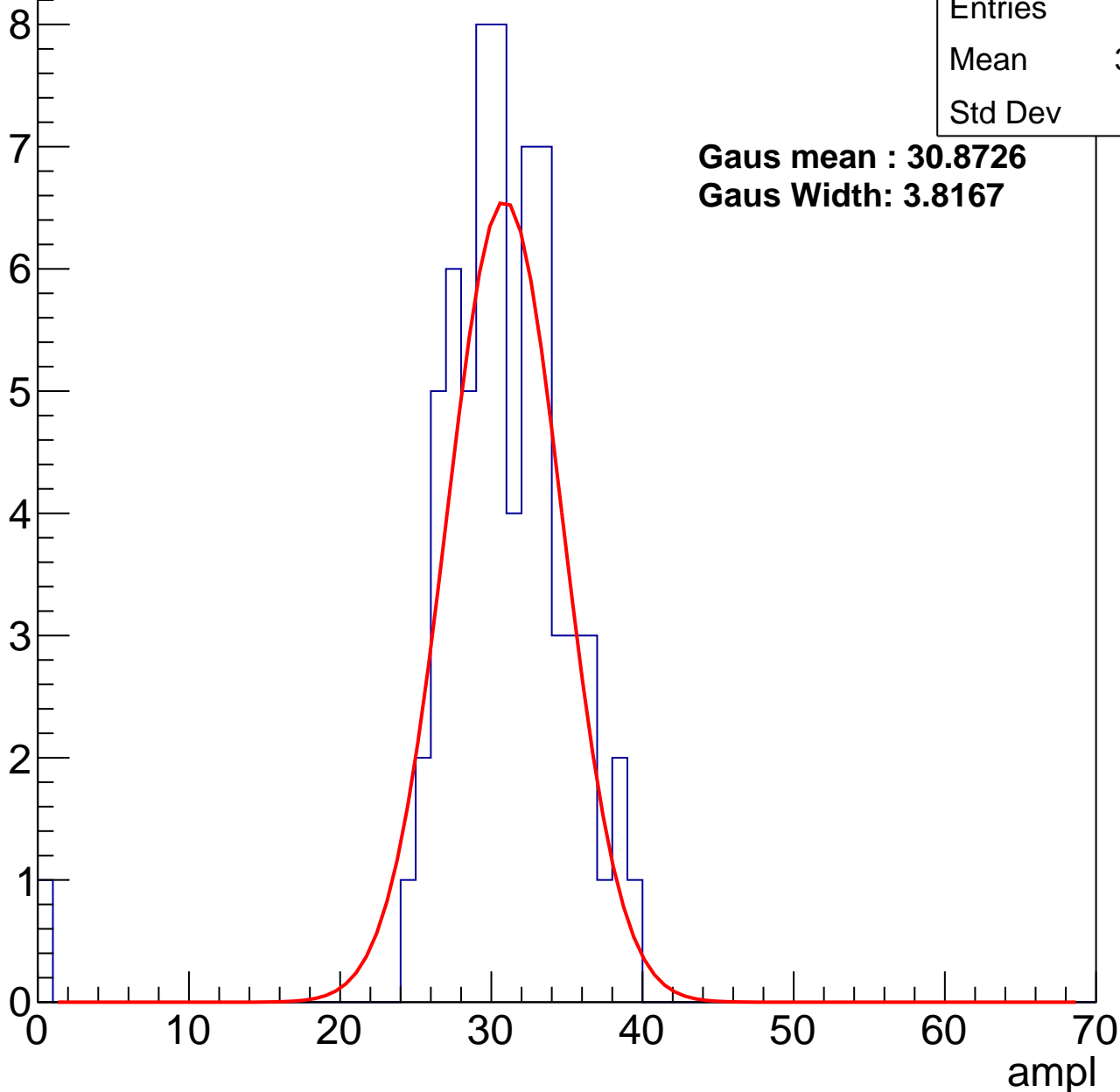
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.21
Std Dev	5.08

Gaus mean : 30.8726

Gaus Width: 3.8167



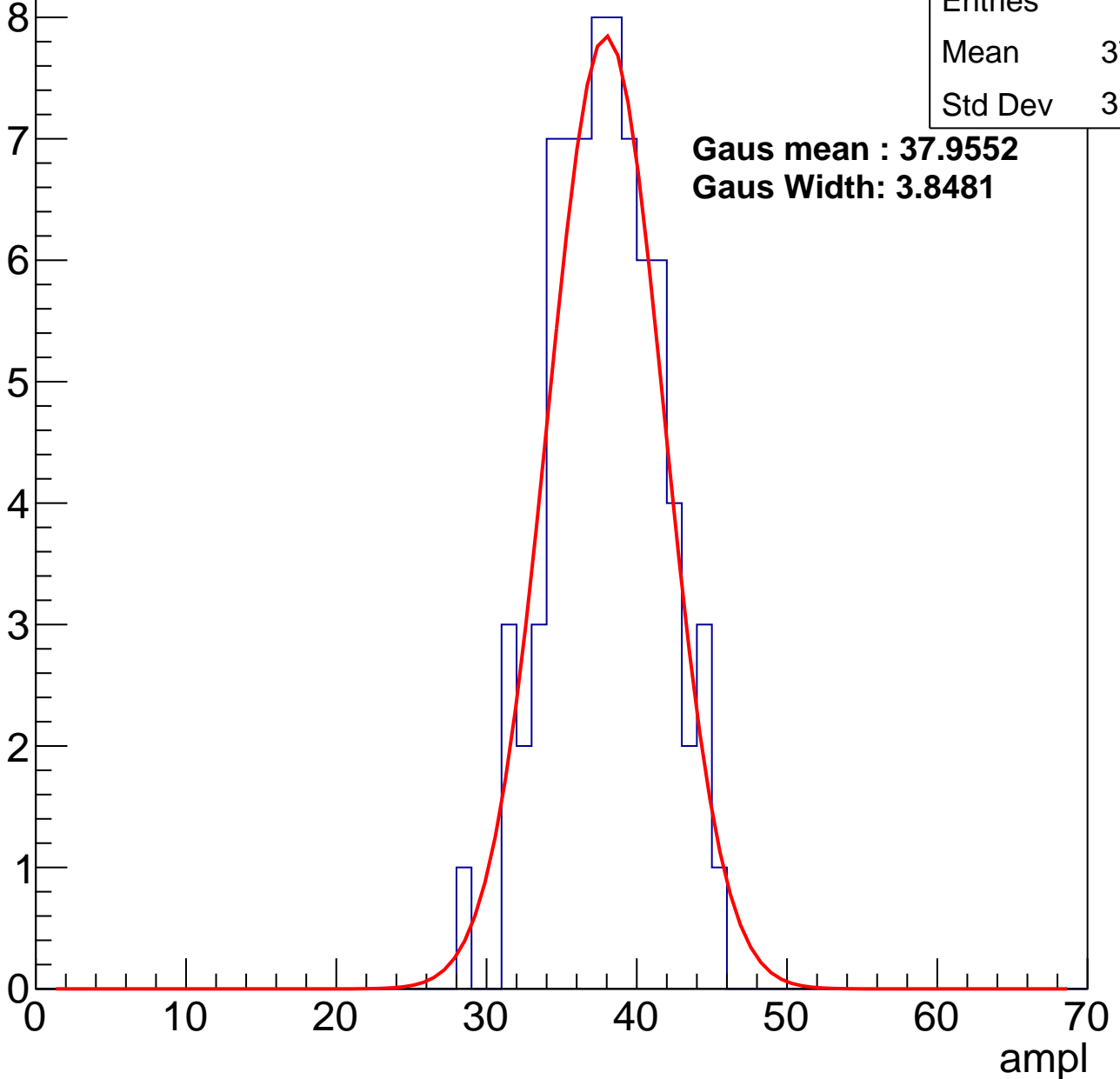
B1L101S, U25-ch95, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	37.45
Std Dev	3.538

Gaus mean : 37.9552
Gaus Width: 3.8481



B1L101S, U25-ch95, adc2

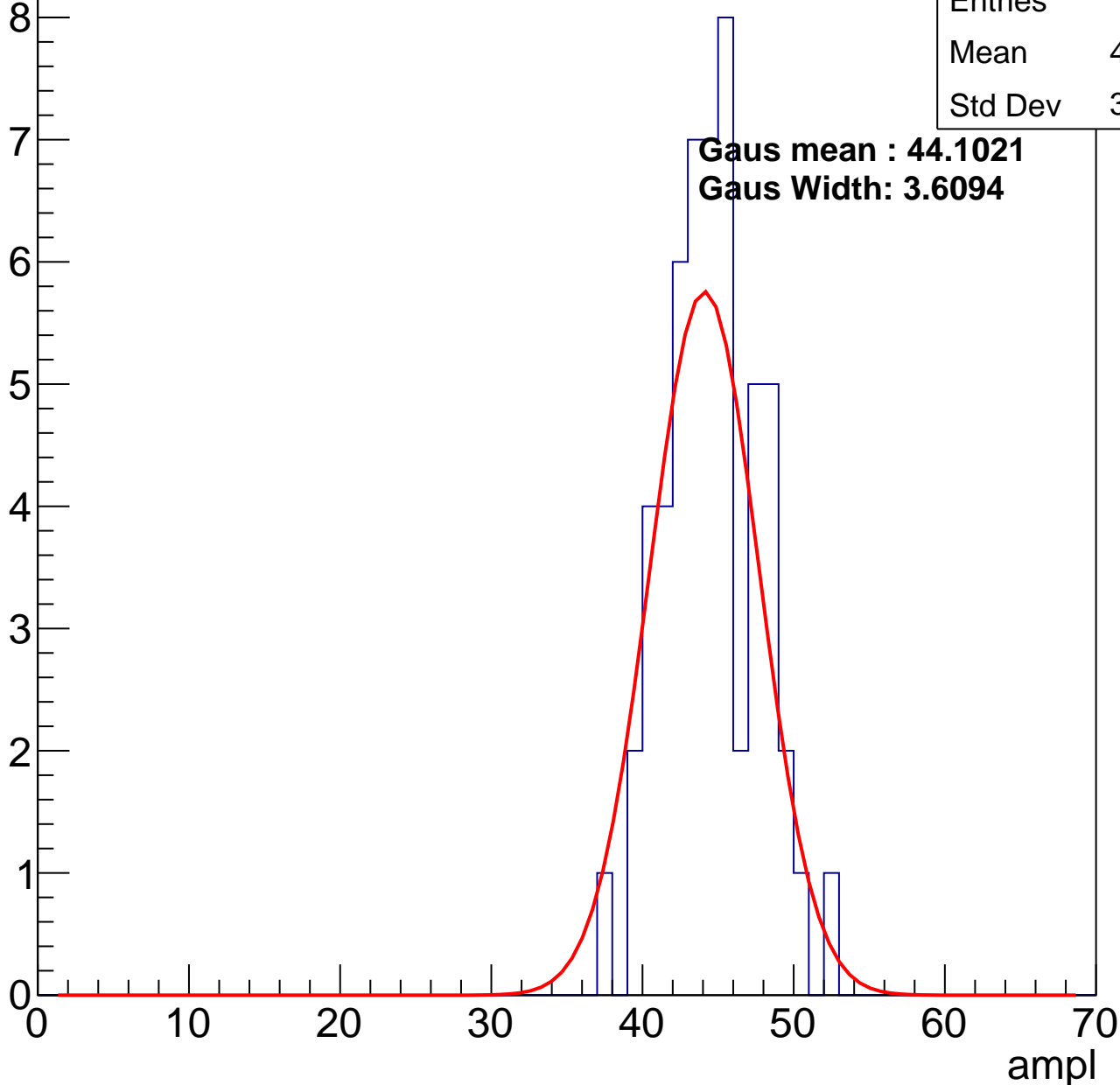
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	44.13
Std Dev	3.075

Gaus mean : 44.1021

Gaus Width: 3.6094

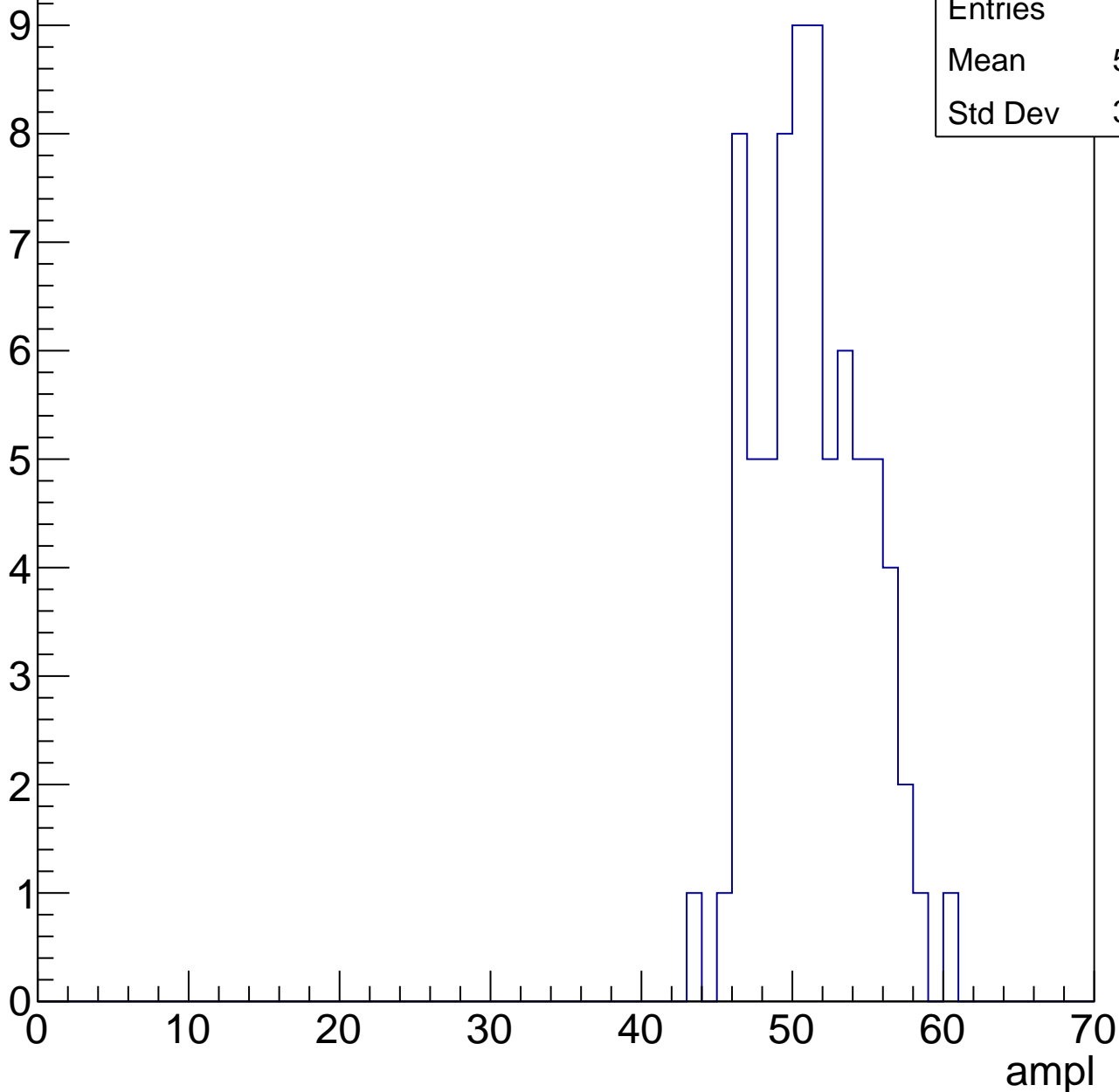


B1L101S, U25-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	50.81
Std Dev	3.501

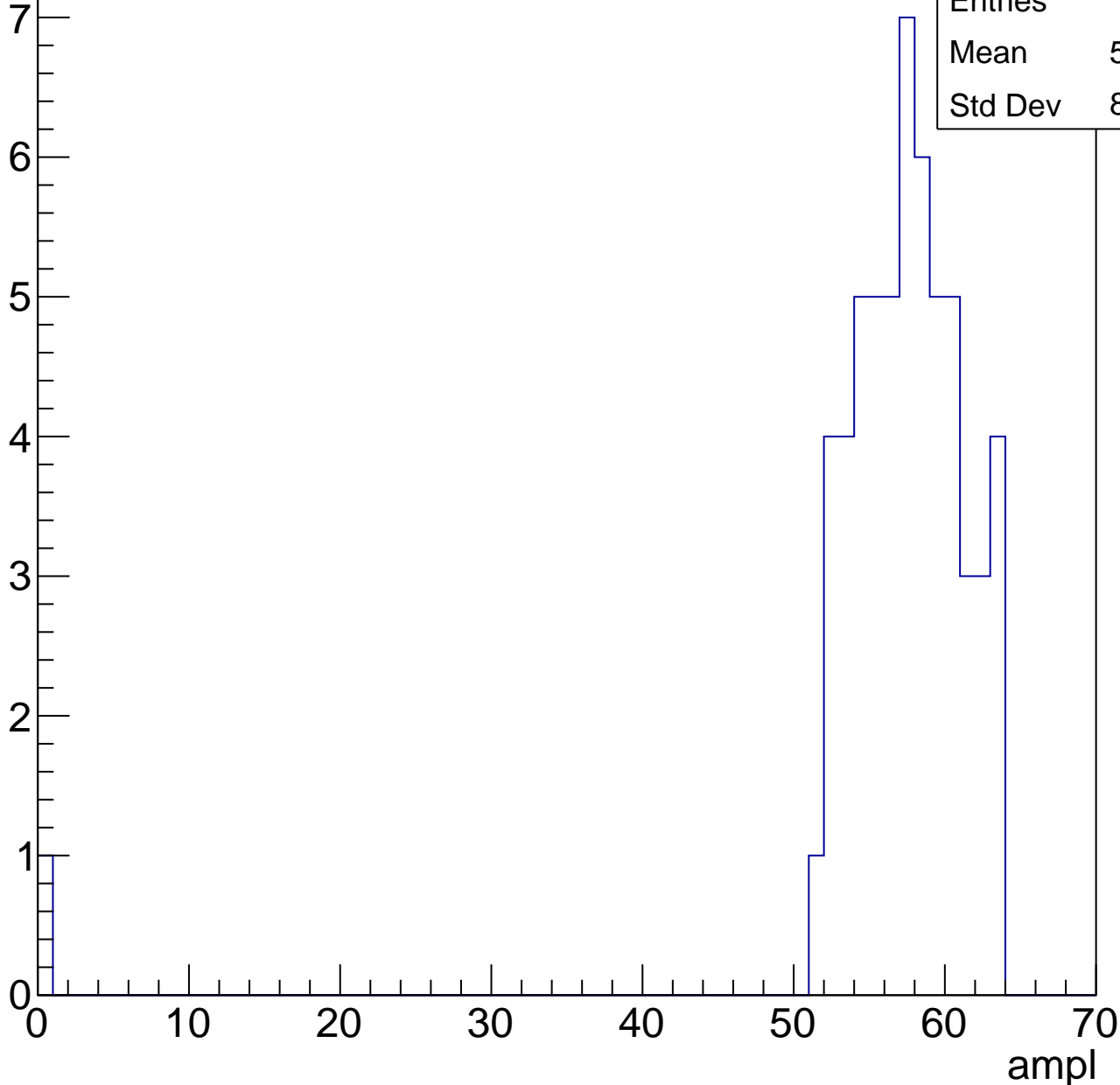


B1L101S, U25-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.19
Std Dev	8.114

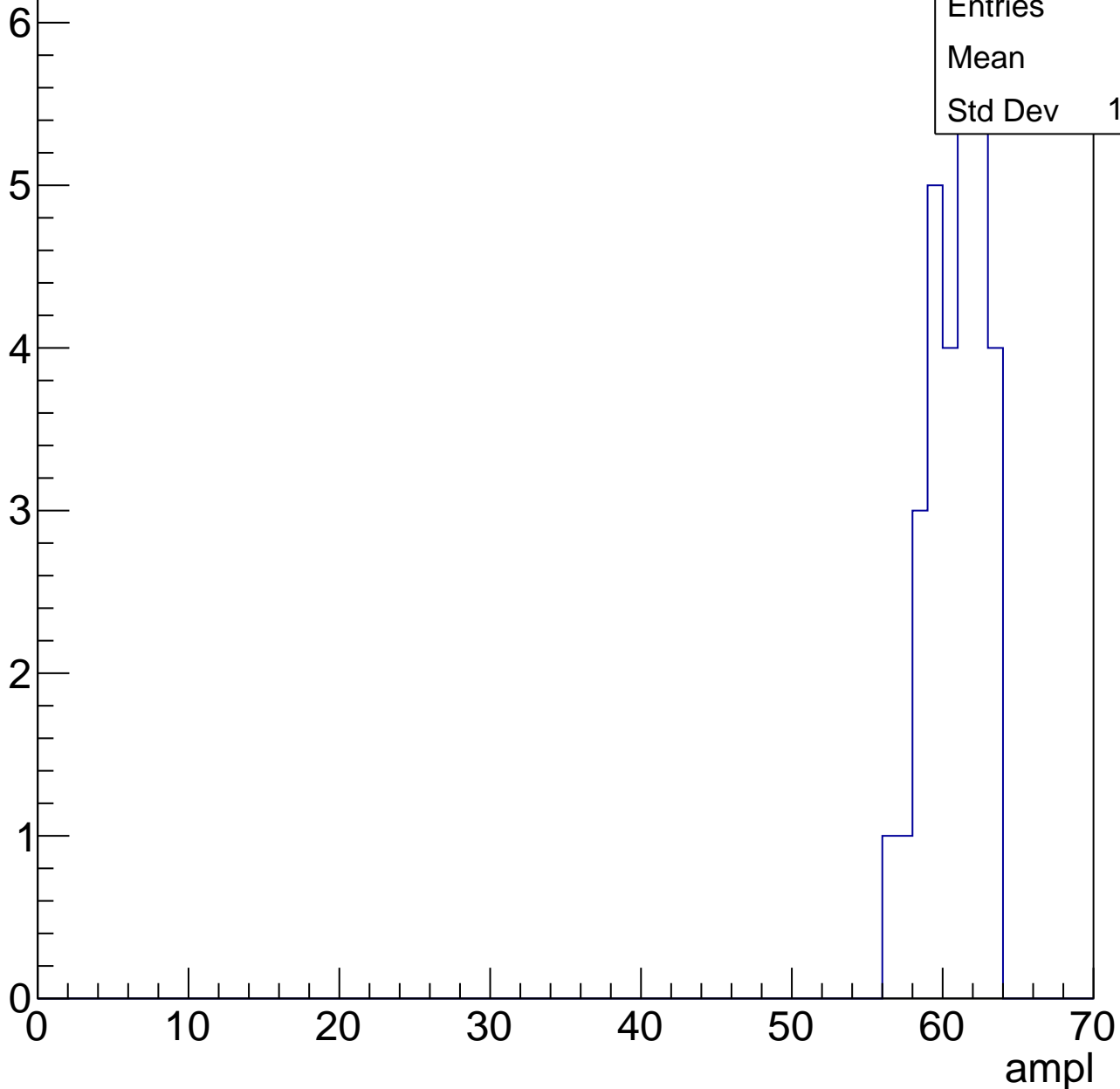


B1L101S, U25-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	60.4
Std Dev	1.855



B1L101S, U25-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L101S, U25-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch96, adc0

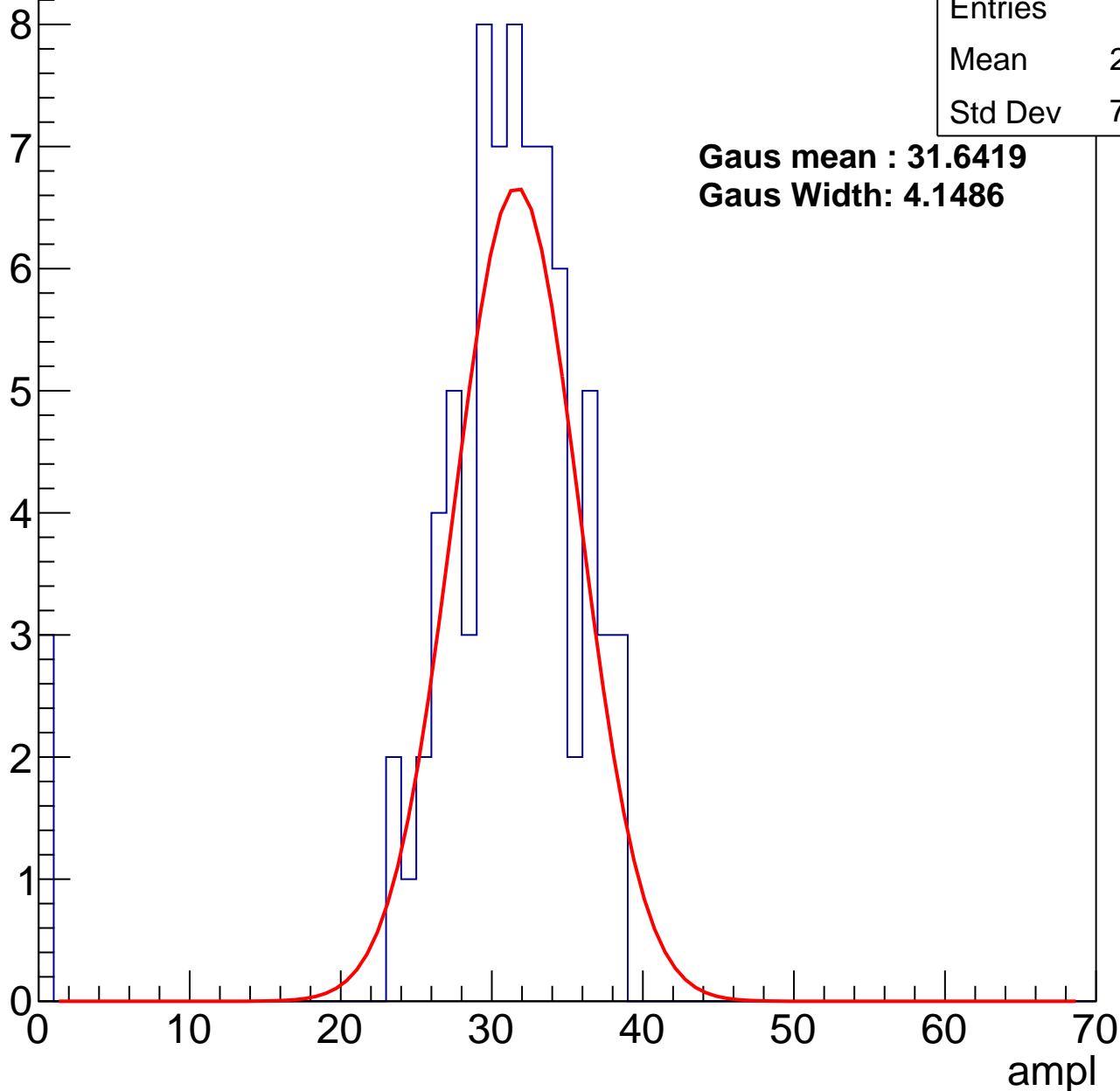
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.83
Std Dev	7.048

Gaus mean : 31.6419

Gaus Width: 4.1486



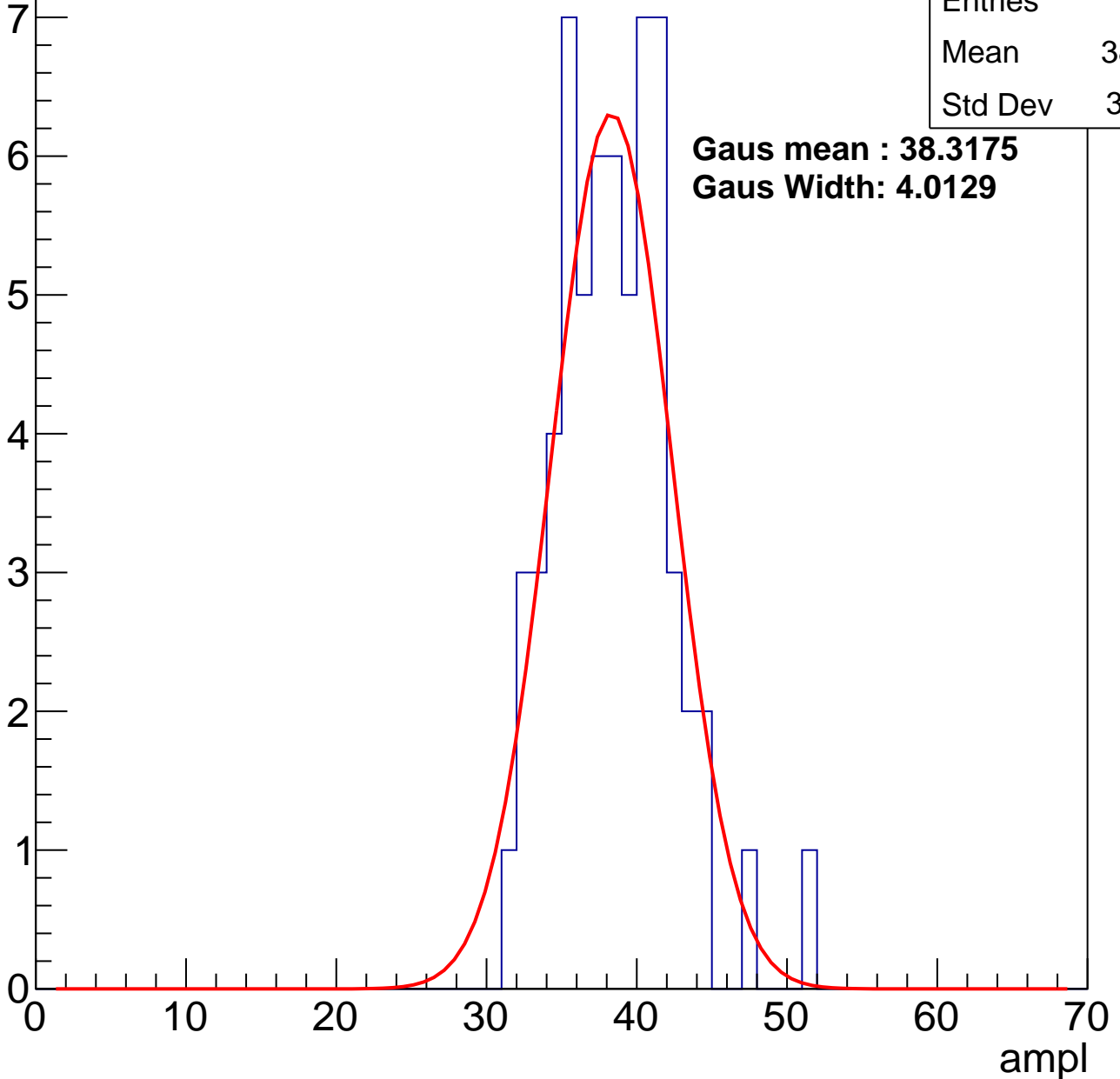
B1L101S, U25-ch96, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.05
Std Dev	3.781

Gaus mean : 38.3175
Gaus Width: 4.0129



B1L101S, U25-ch96, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	44.47
Std Dev	3.385

Gaus mean : 45.3485

Gaus Width: 3.4239

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

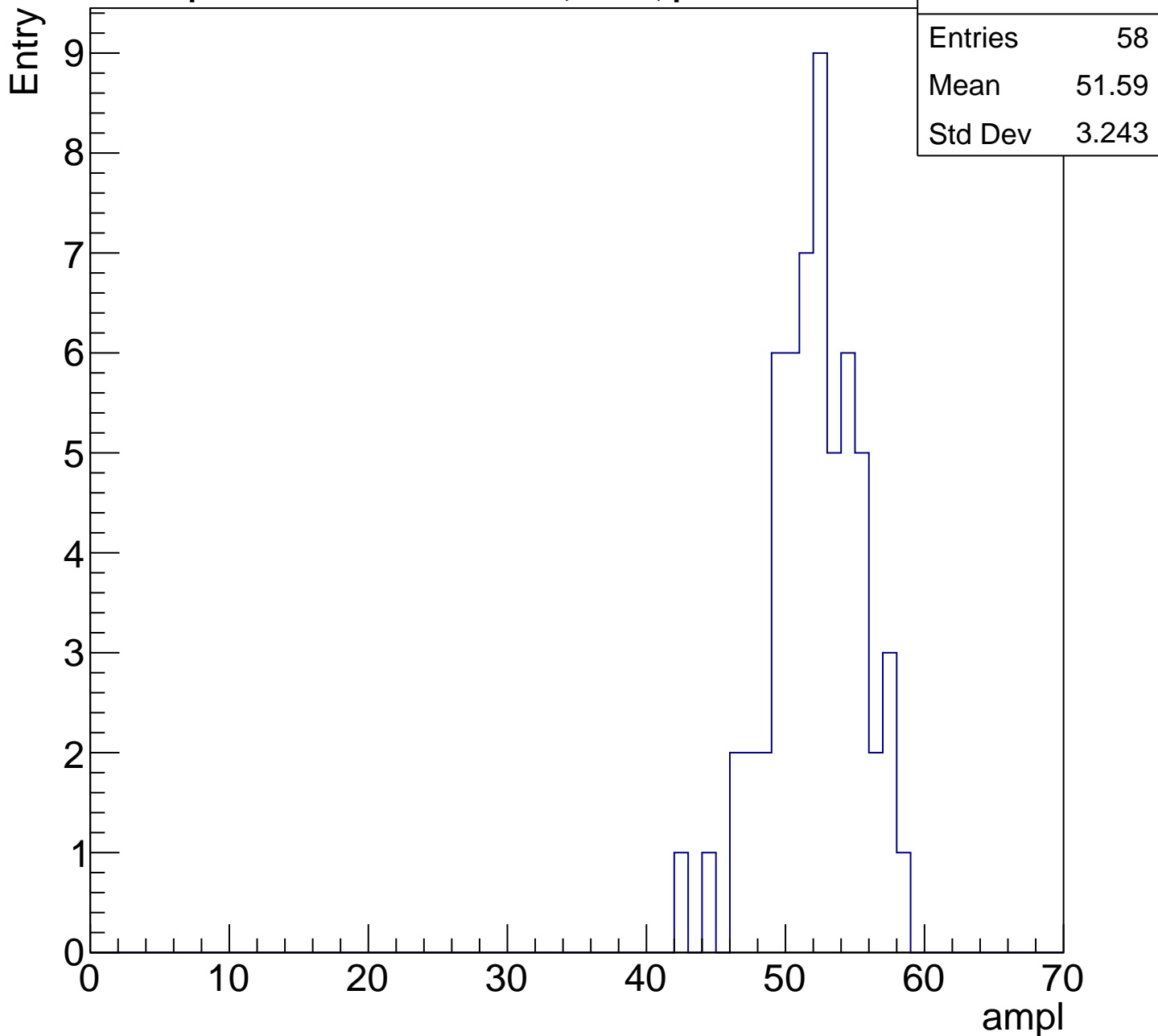
6

8

10

B1L101S, U25-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

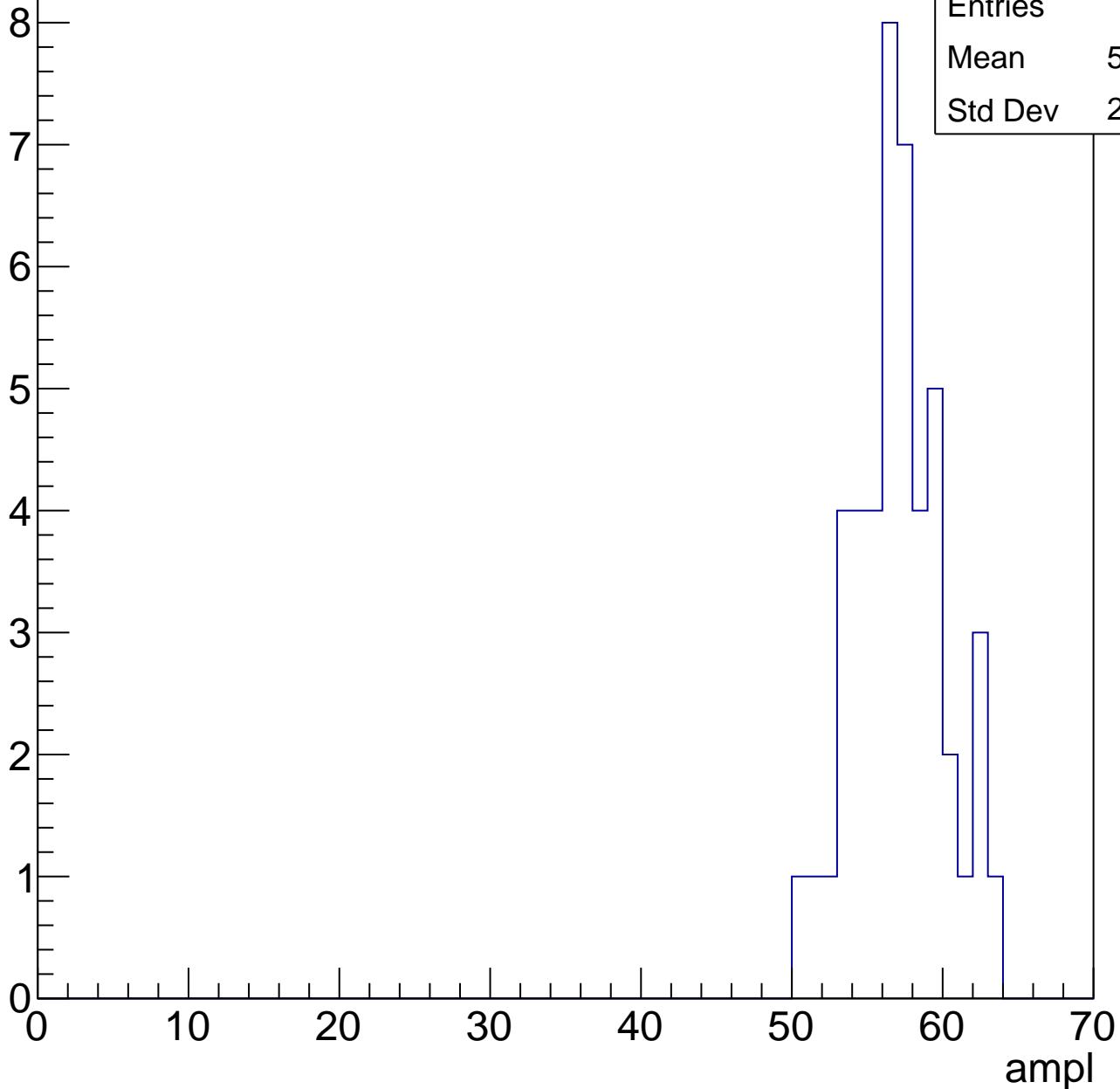


B1L101S, U25-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

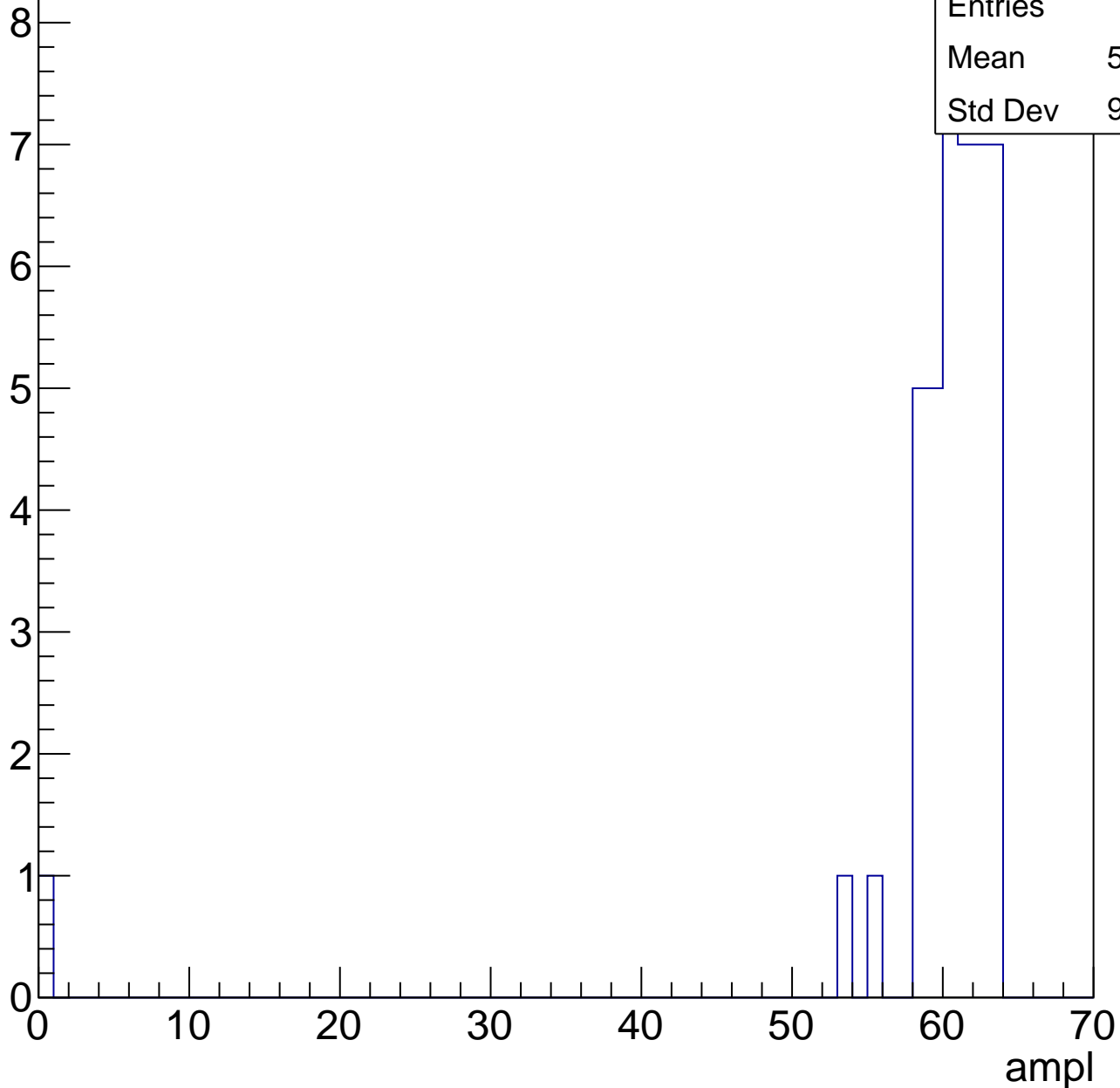
Entries	46
Mean	56.63
Std Dev	2.937



B1L101S, U25-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch97, adc0

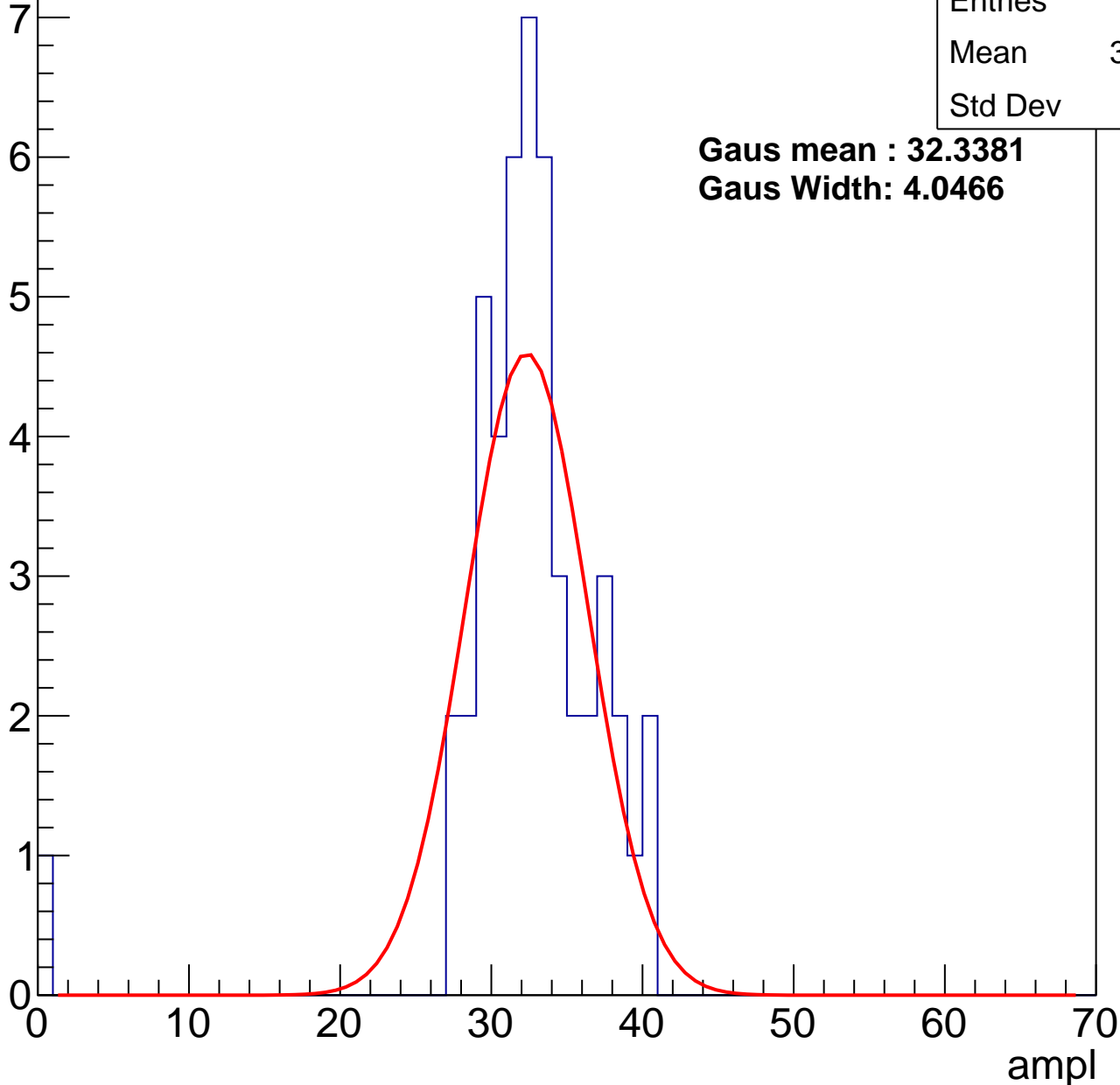
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	31.94
Std Dev	5.71

Gaus mean : 32.3381

Gaus Width: 4.0466



B1L101S, U25-ch97, adc1

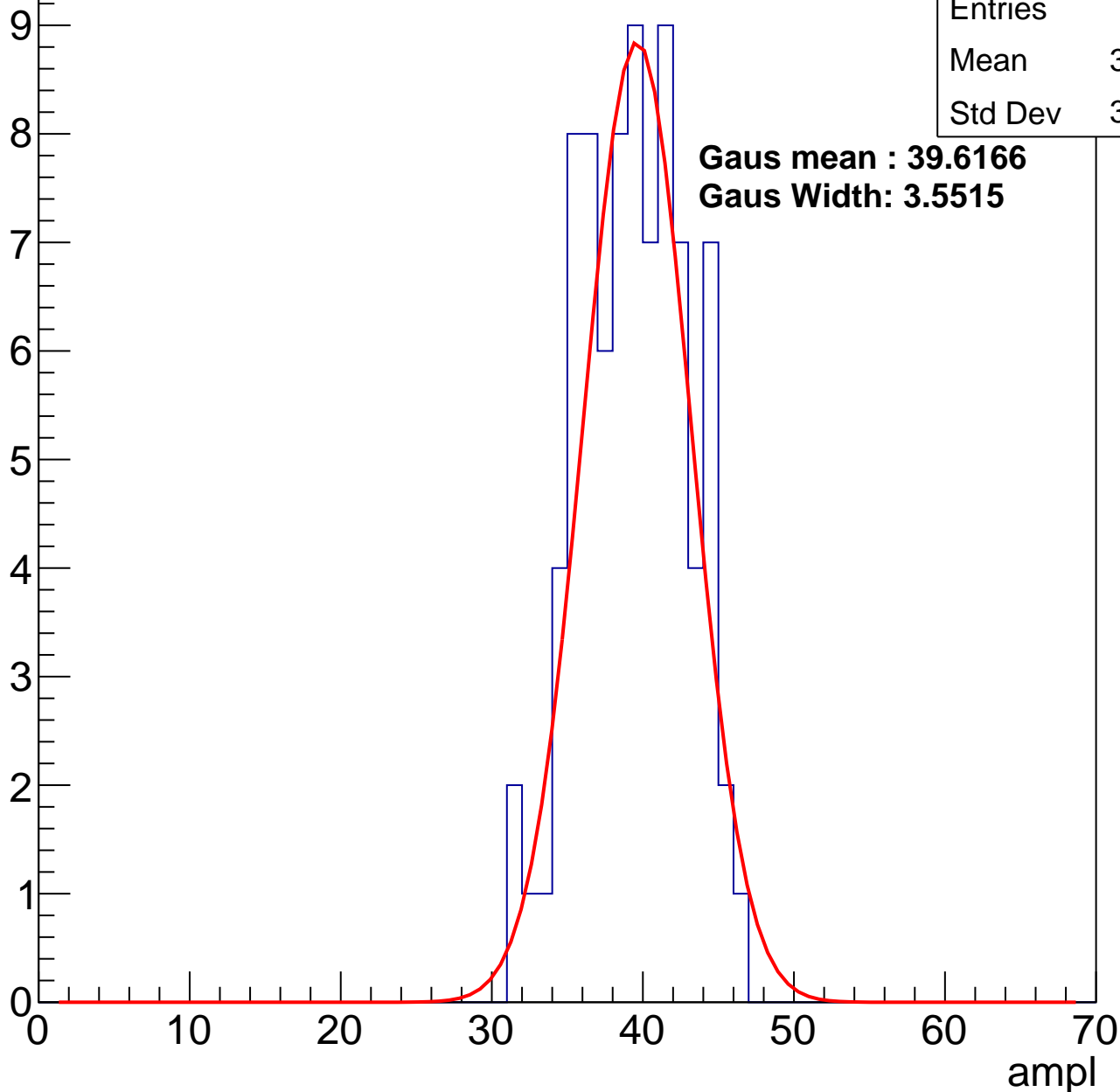
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	38.89
Std Dev	3.468

Gaus mean : 39.6166

Gaus Width: 3.5515



B1L101S, U25-ch97, adc2

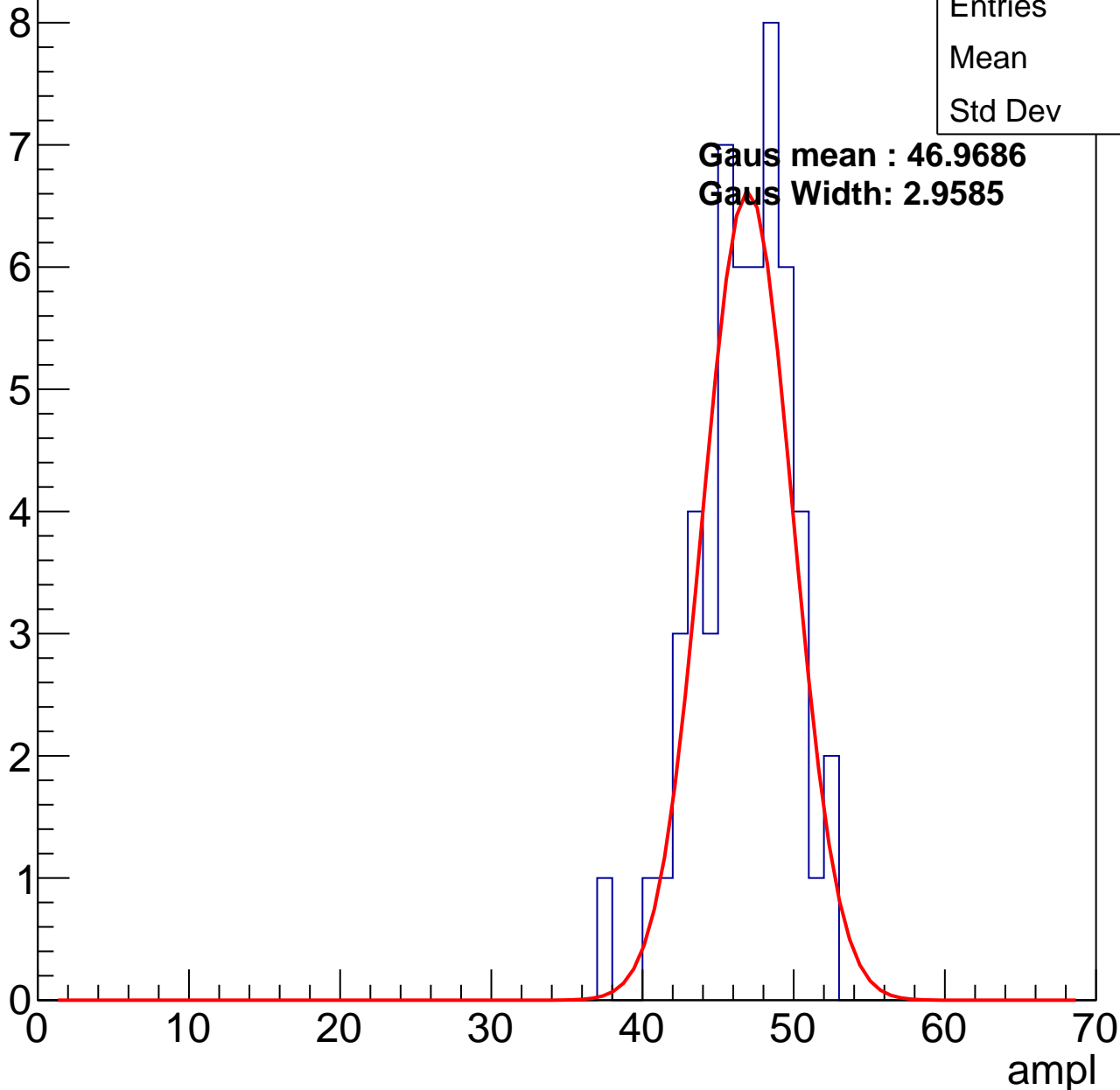
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	46.3
Std Dev	3.05

Gaus mean : 46.9686

Gaus Width: 2.9585

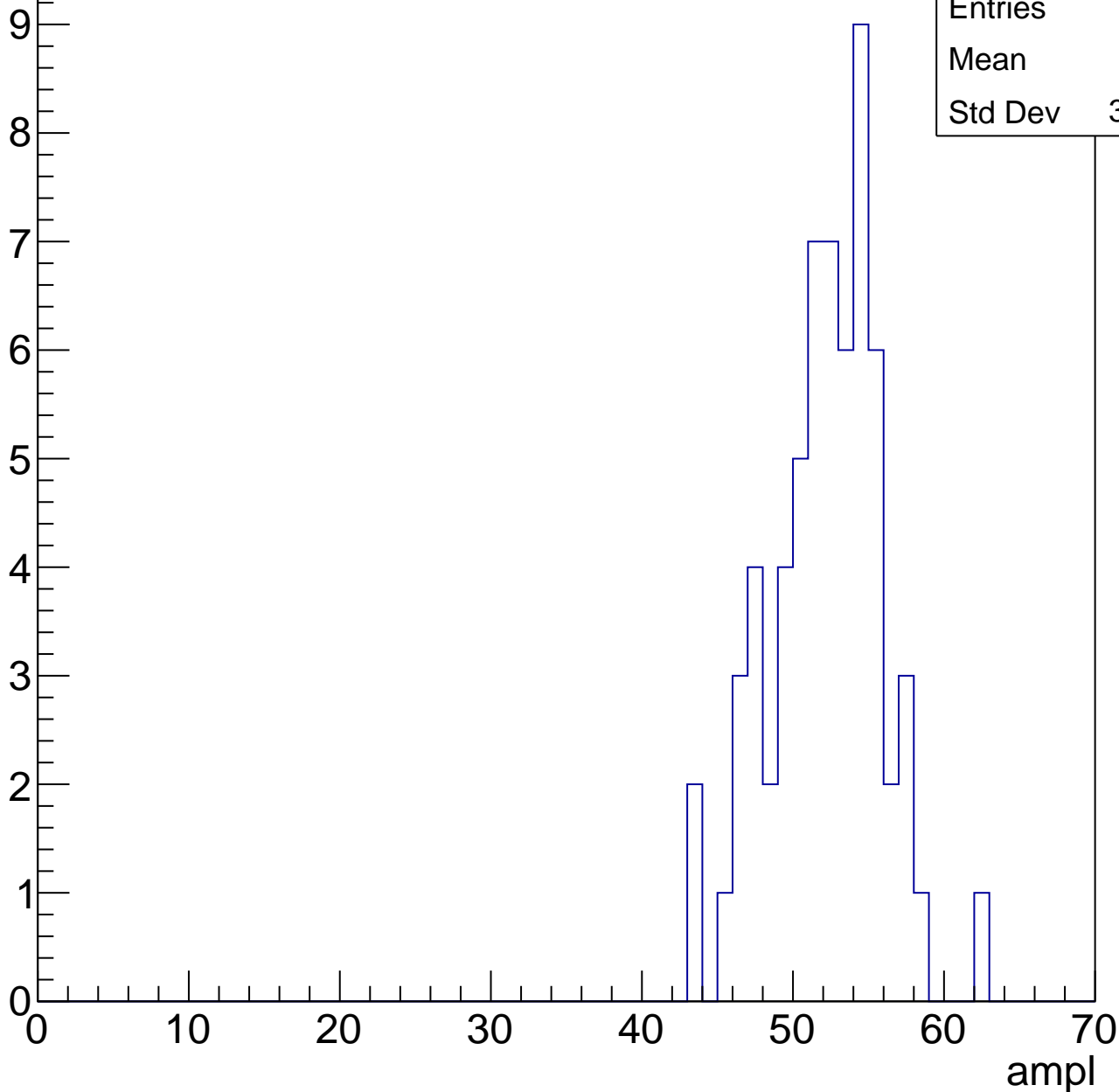


B1L101S, U25-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	51.7
Std Dev	3.667



B1L101S, U25-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	58.18
Std Dev	3.125

Entry

10

8

6

4

2

0

0

10

20

30

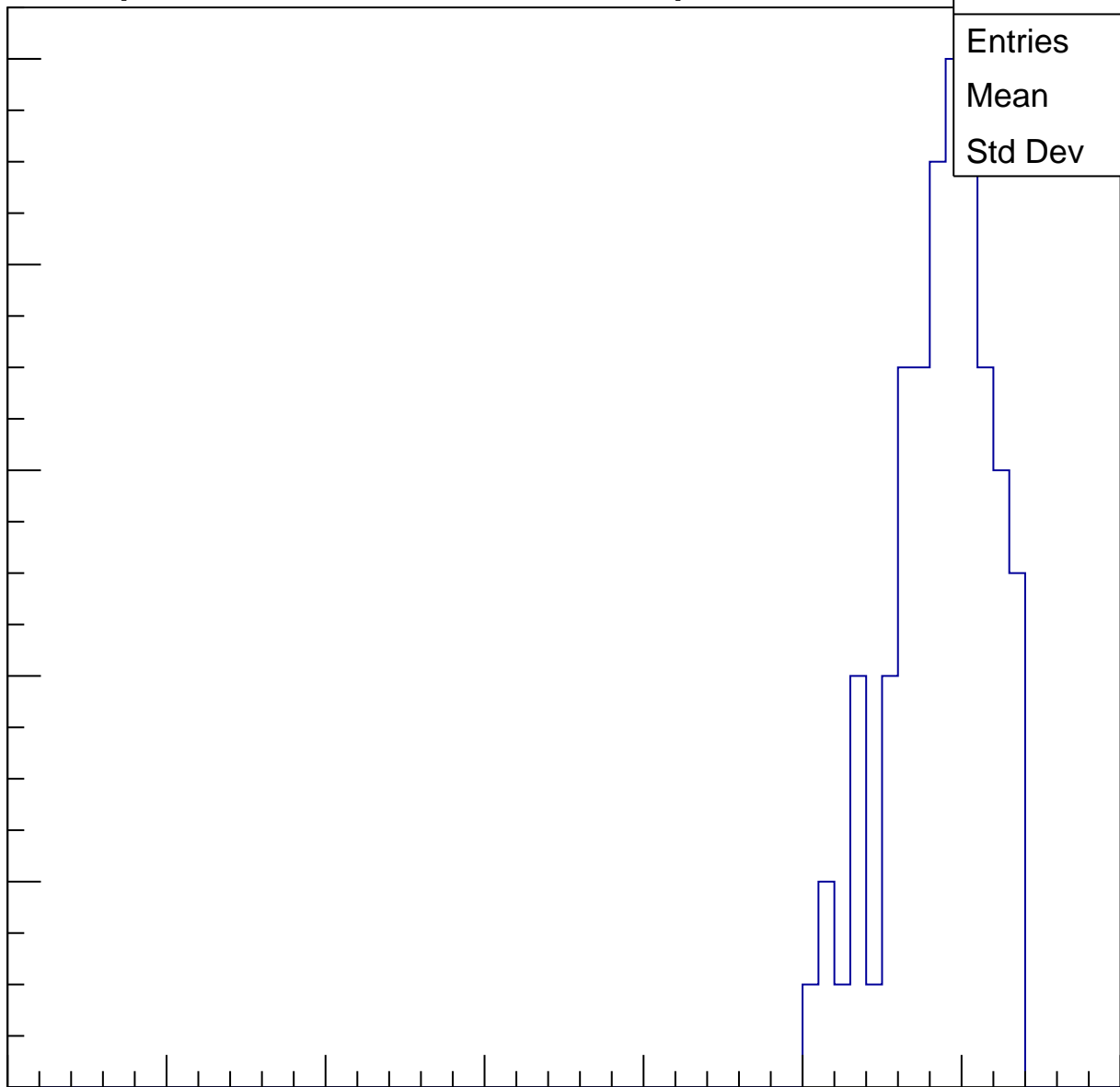
40

50

60

70

ampl

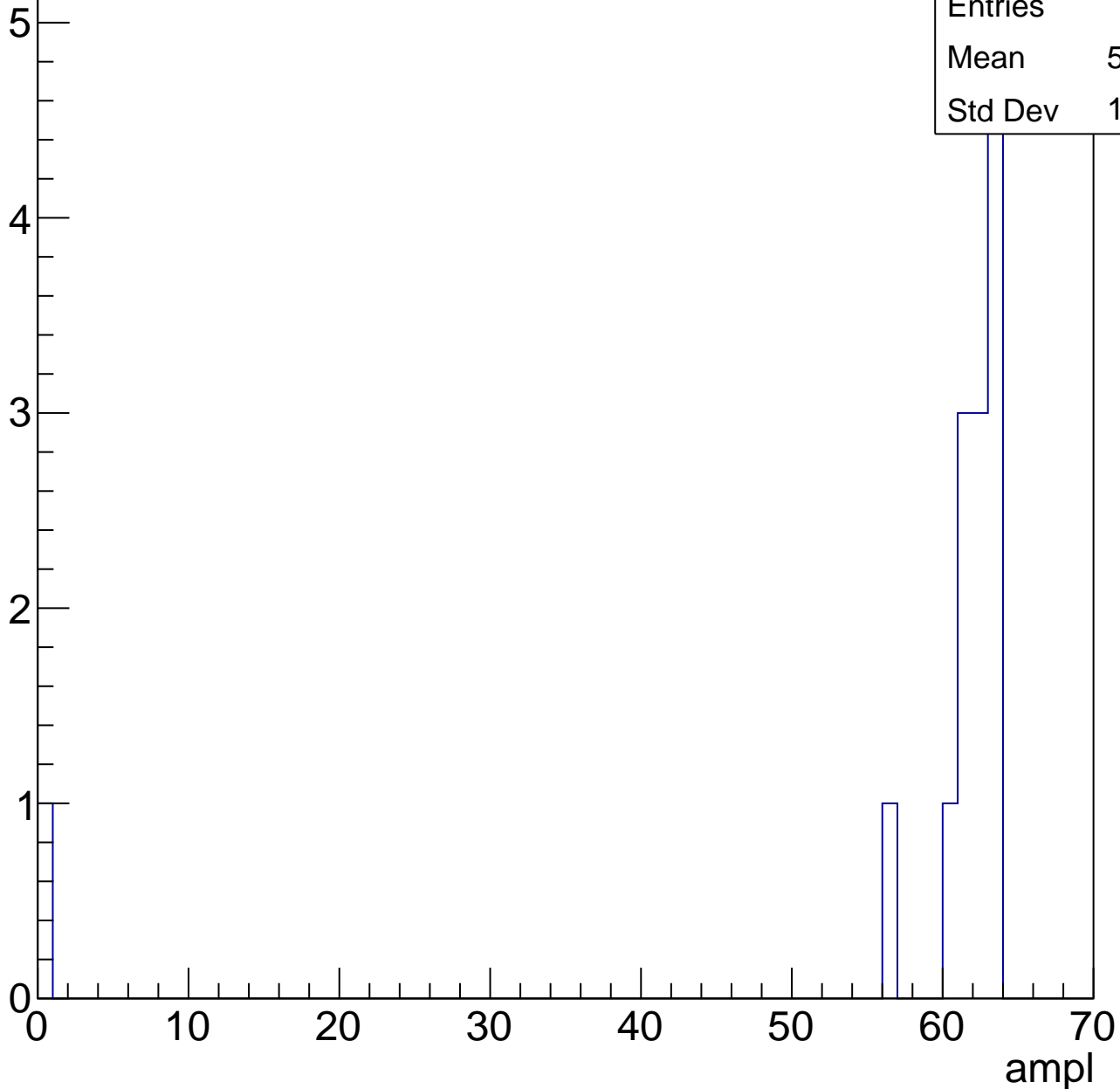


B1L101S, U25-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	57.14
Std Dev	15.95



B1L101S, U25-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch98, adc0

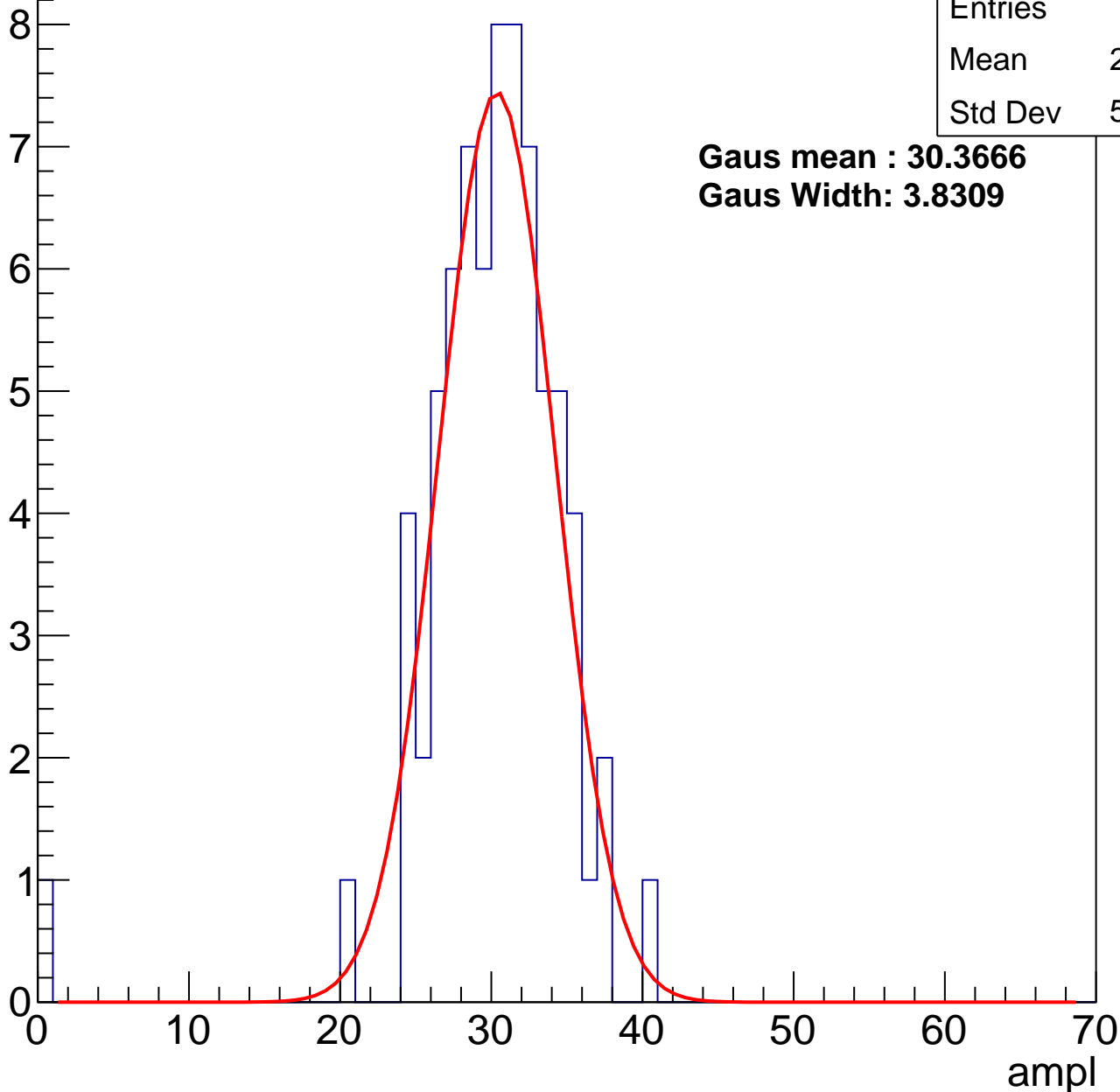
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.66
Std Dev	5.032

Gaus mean : 30.3666

Gaus Width: 3.8309



B1L101S, U25-ch98, adc1

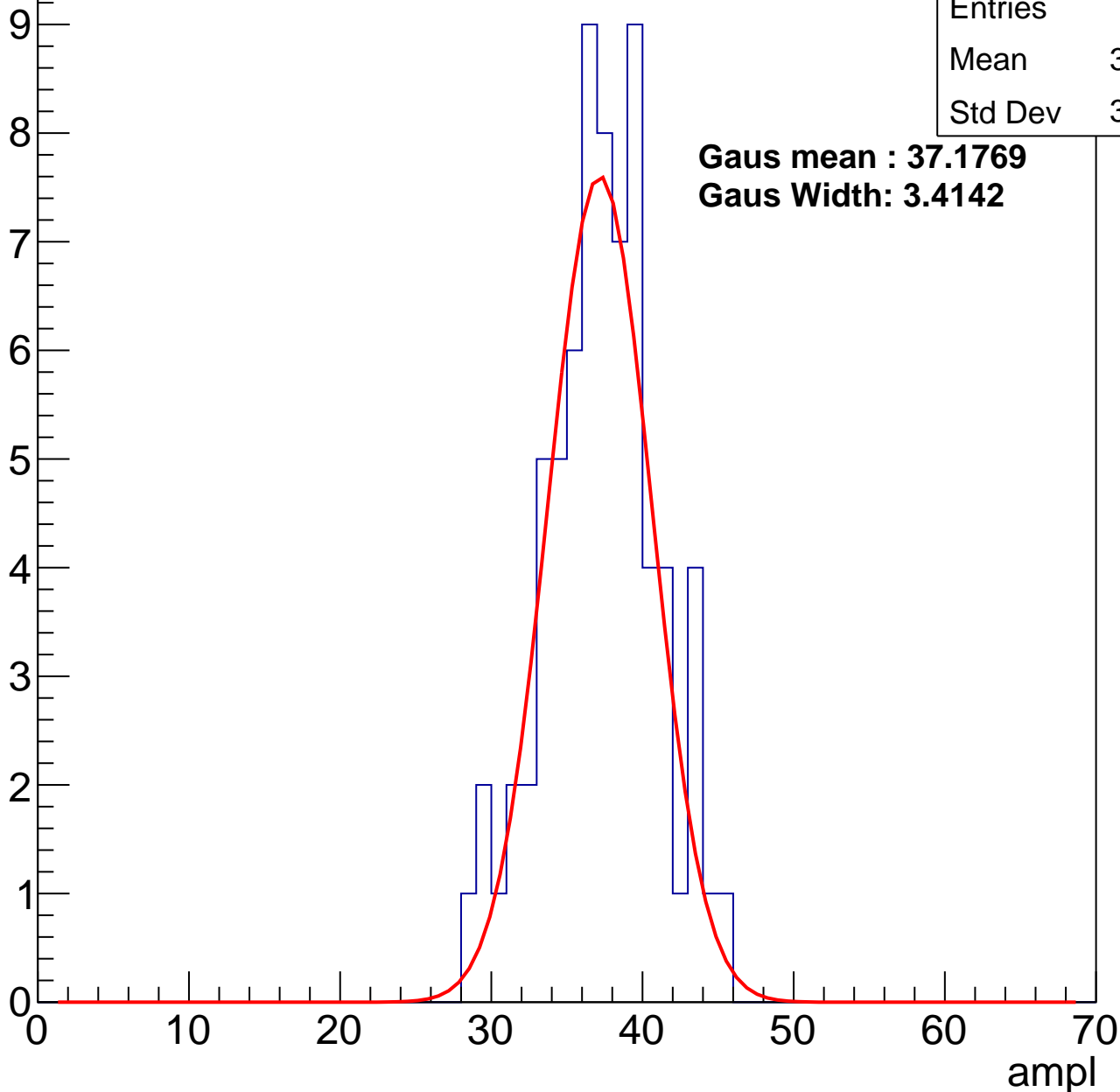
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.82
Std Dev	3.653

Gaus mean : 37.1769

Gaus Width: 3.4142



B1L101S, U25-ch98, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	44.66
Std Dev	3.515

Gaus mean : 44.7253

Gaus Width: 3.1591

10

8

6

4

2

0

0

10

20

30

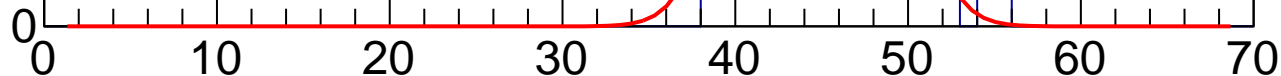
40

50

60

70

ampl

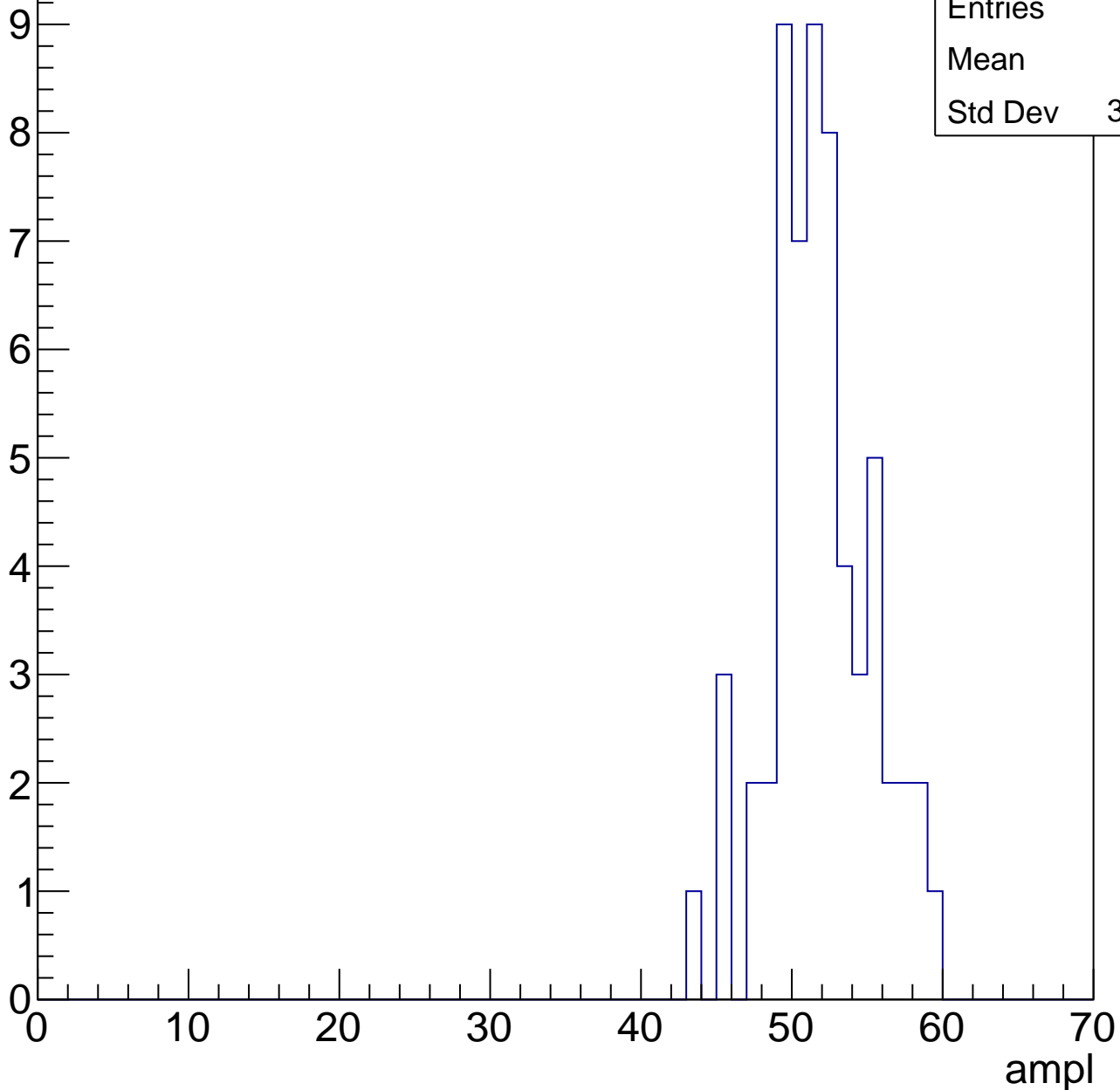


B1L101S, U25-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

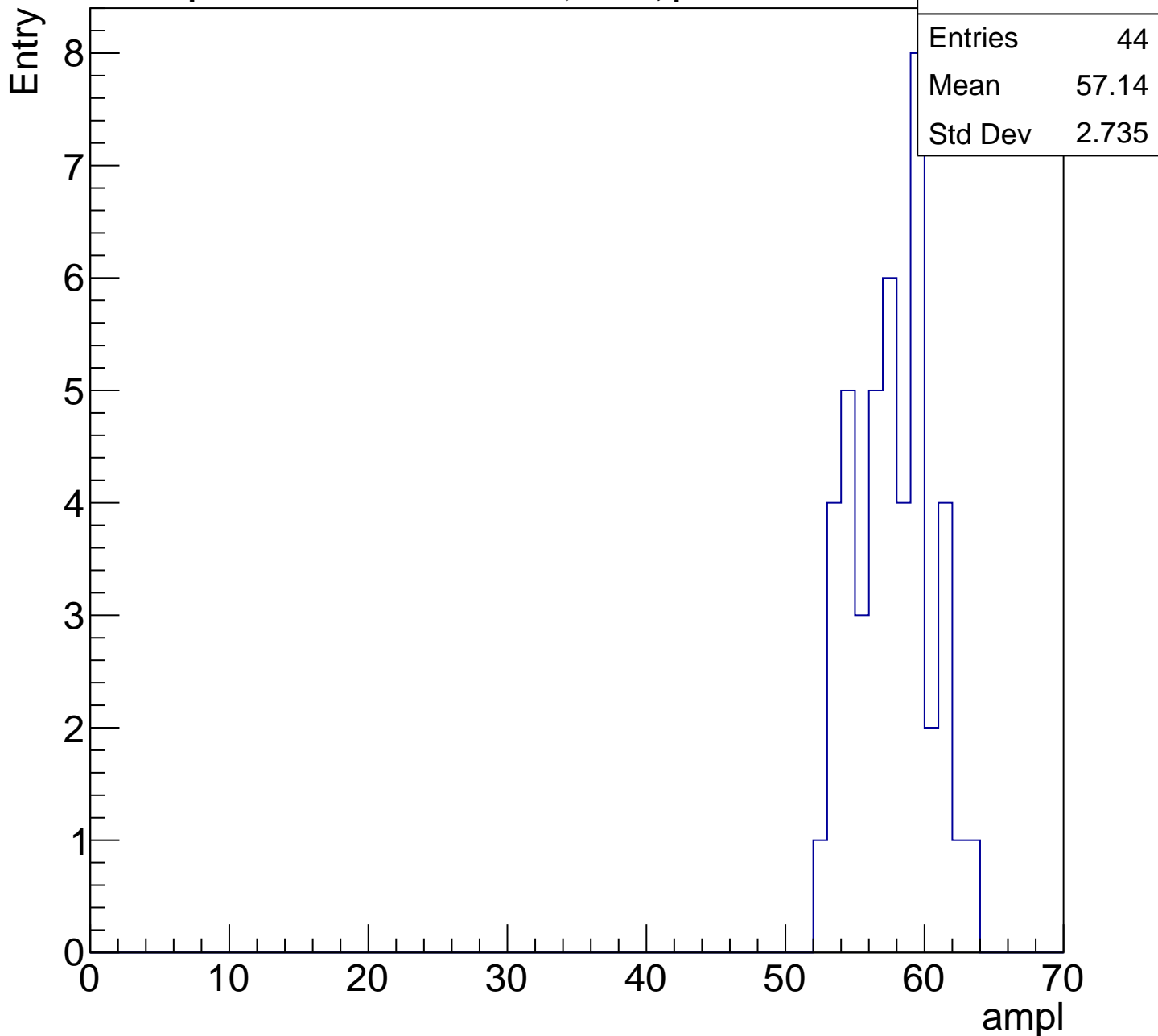
Entry

Entries	60
Mean	51.4
Std Dev	3.343



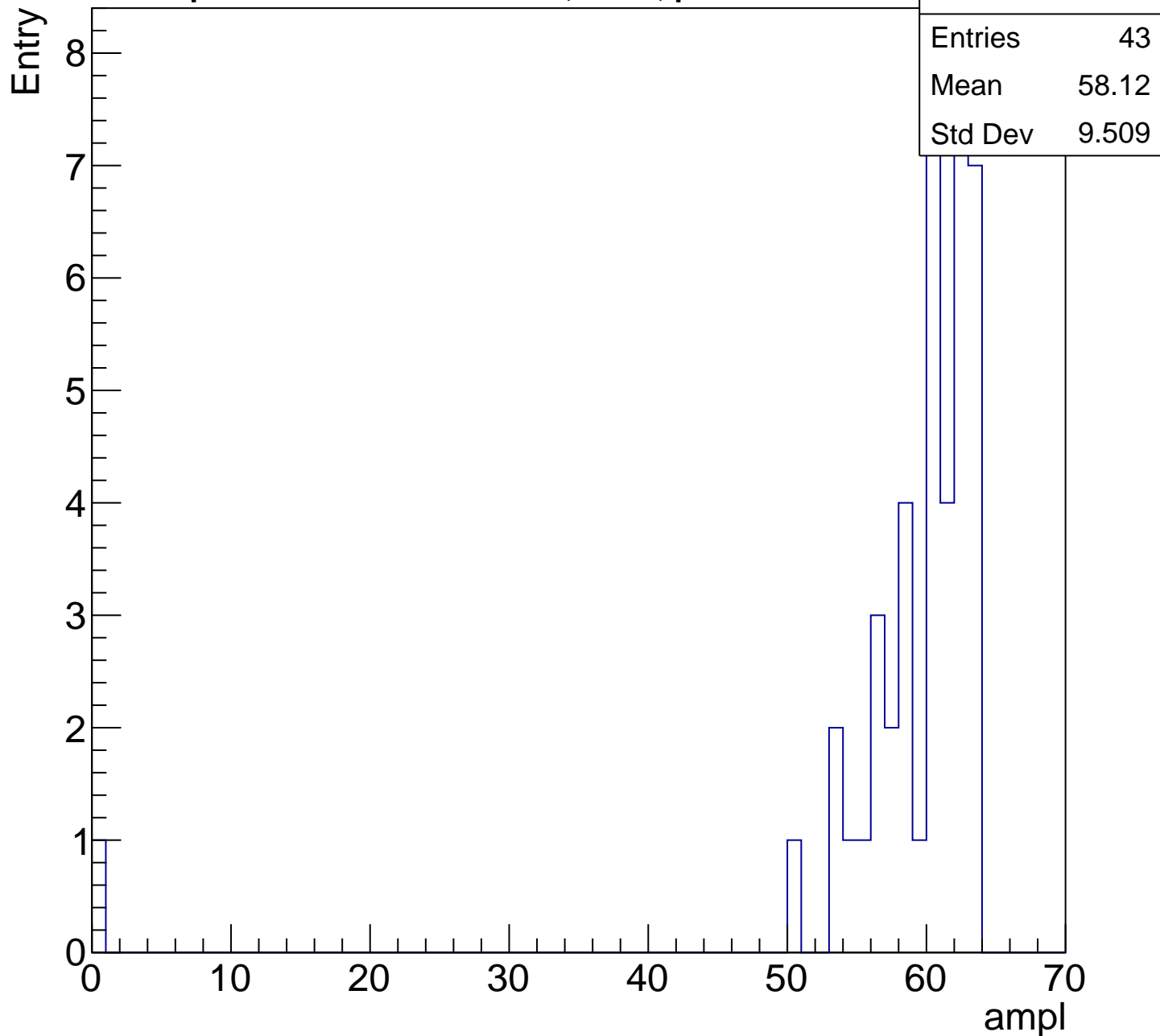
B1L101S, U25-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch98, adc5

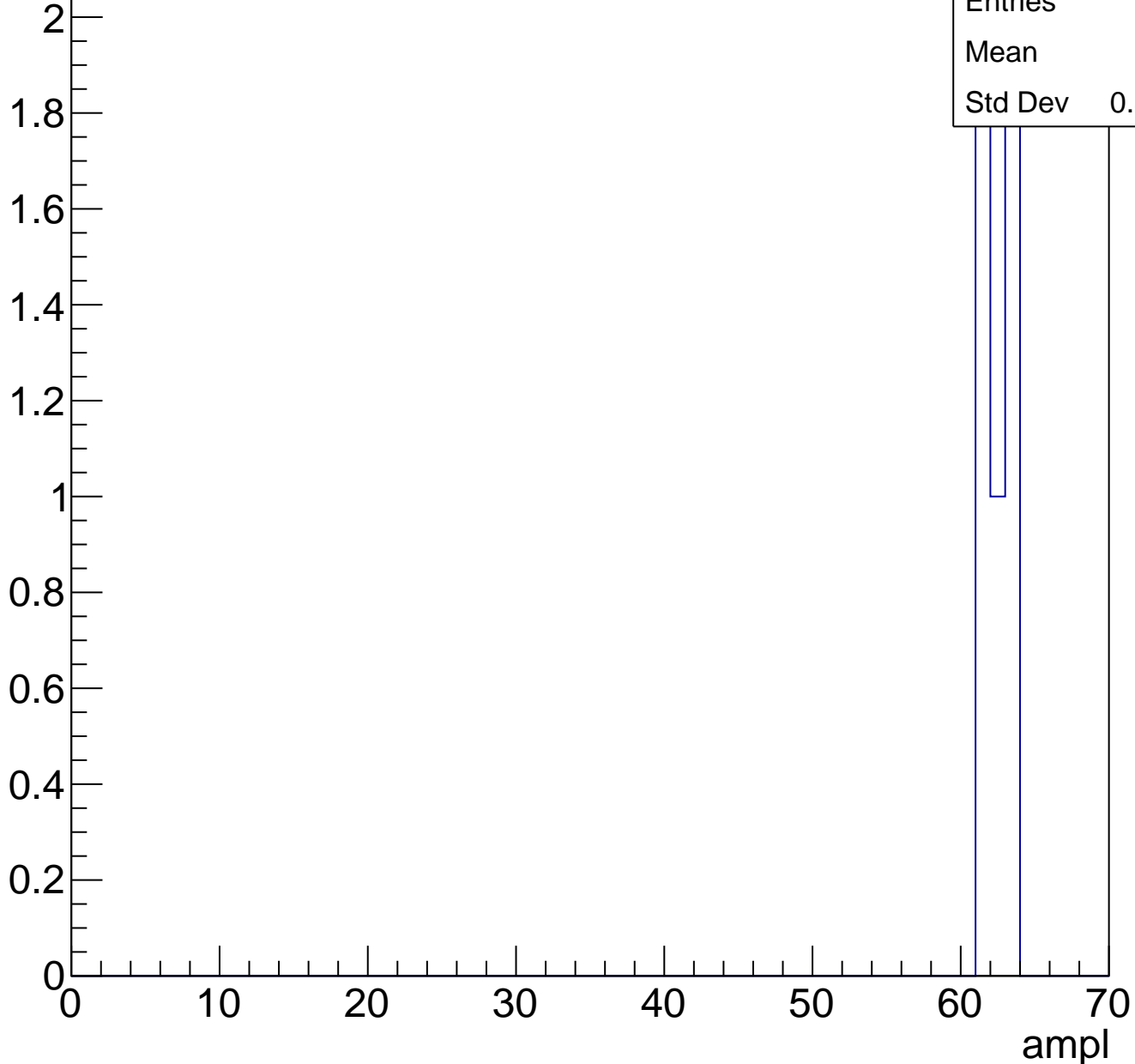
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U25-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch99, adc0

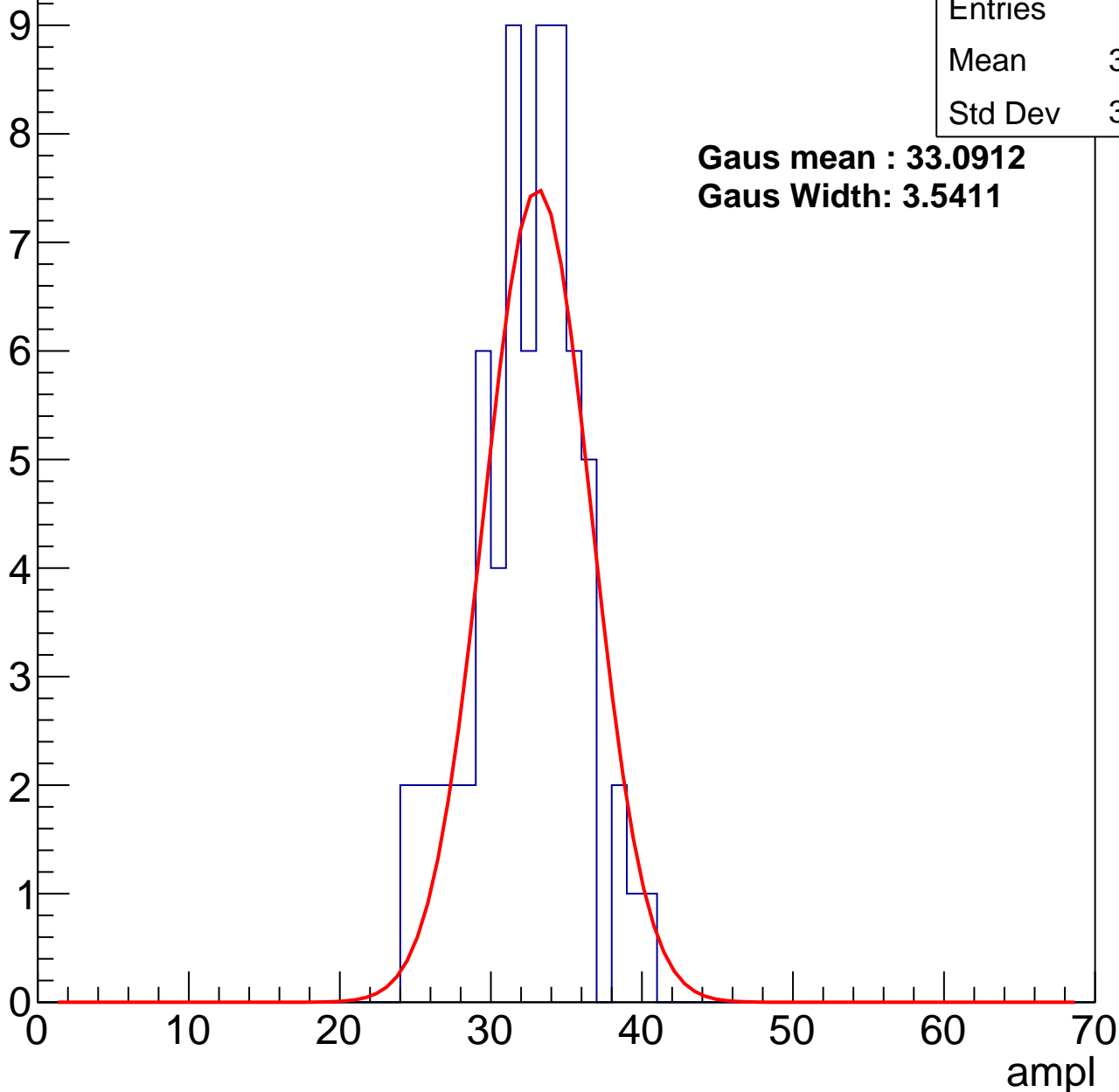
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	31.96
Std Dev	3.479

Gaus mean : 33.0912

Gaus Width: 3.5411

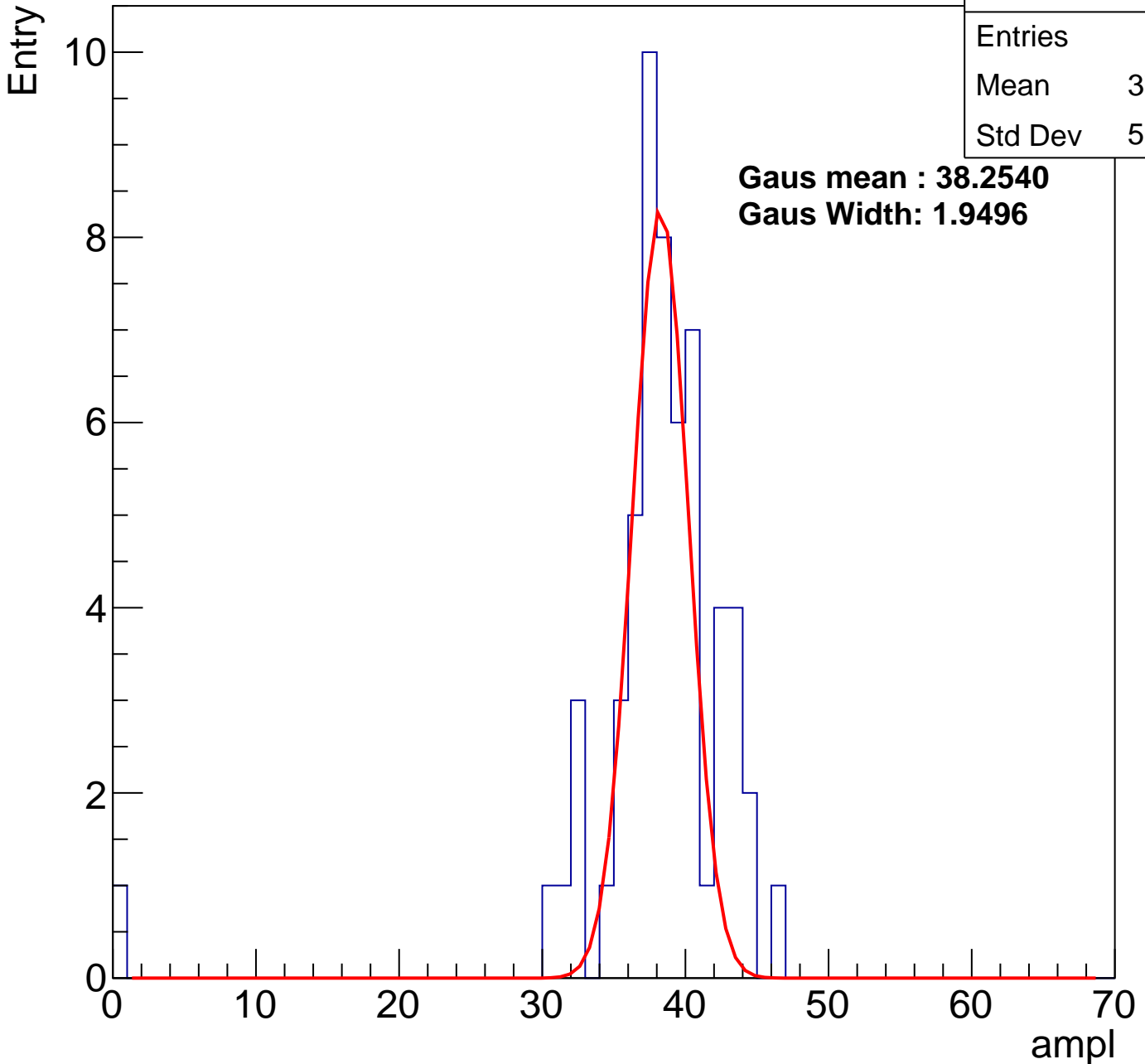


B1L101S, U25-ch99, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	58
Mean	37.57
Std Dev	5.974

Gaus mean : 38.2540
Gaus Width: 1.9496



B1L101S, U25-ch99, adc2

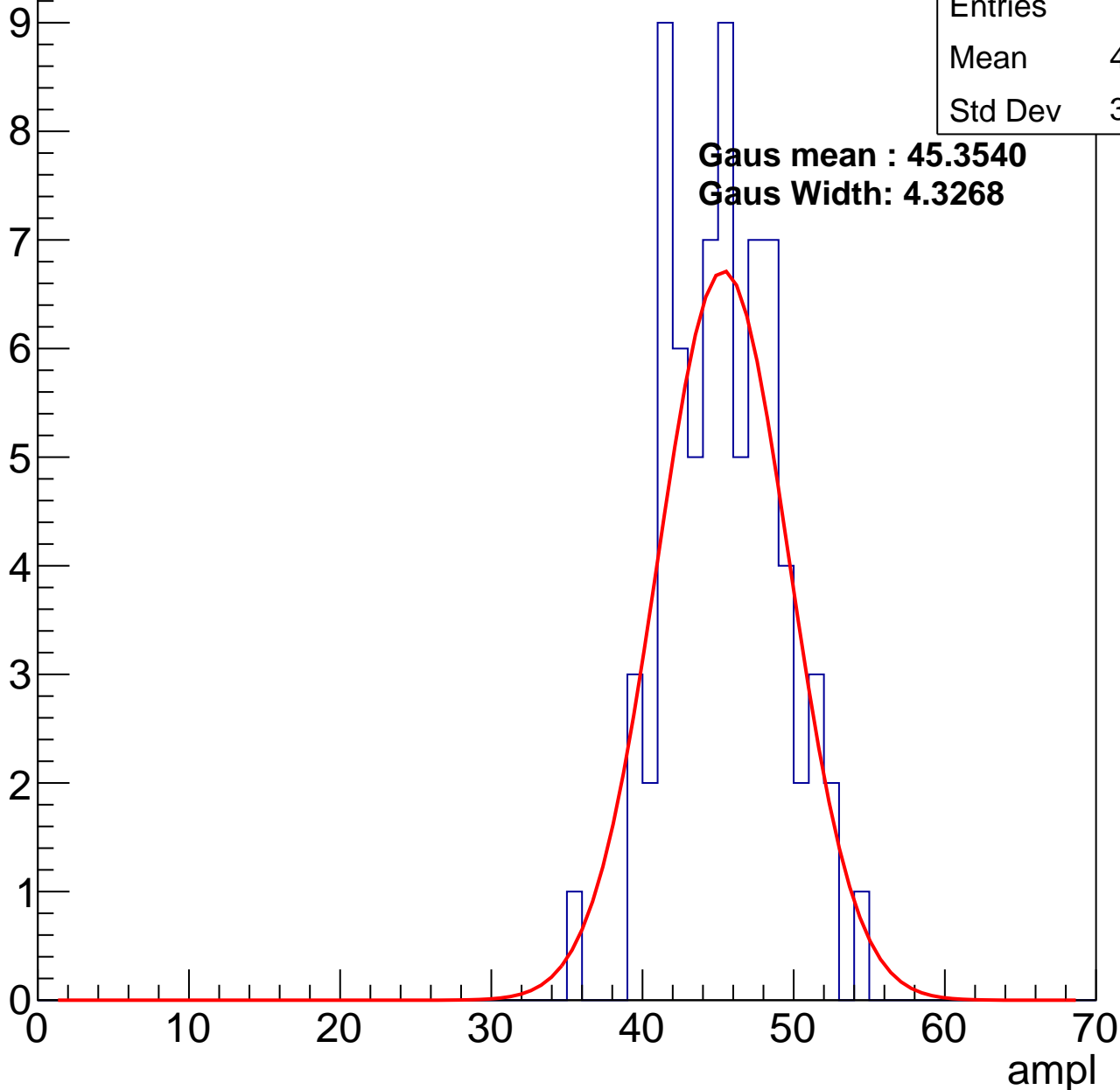
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	44.97
Std Dev	3.664

Gaus mean : 45.3540

Gaus Width: 4.3268

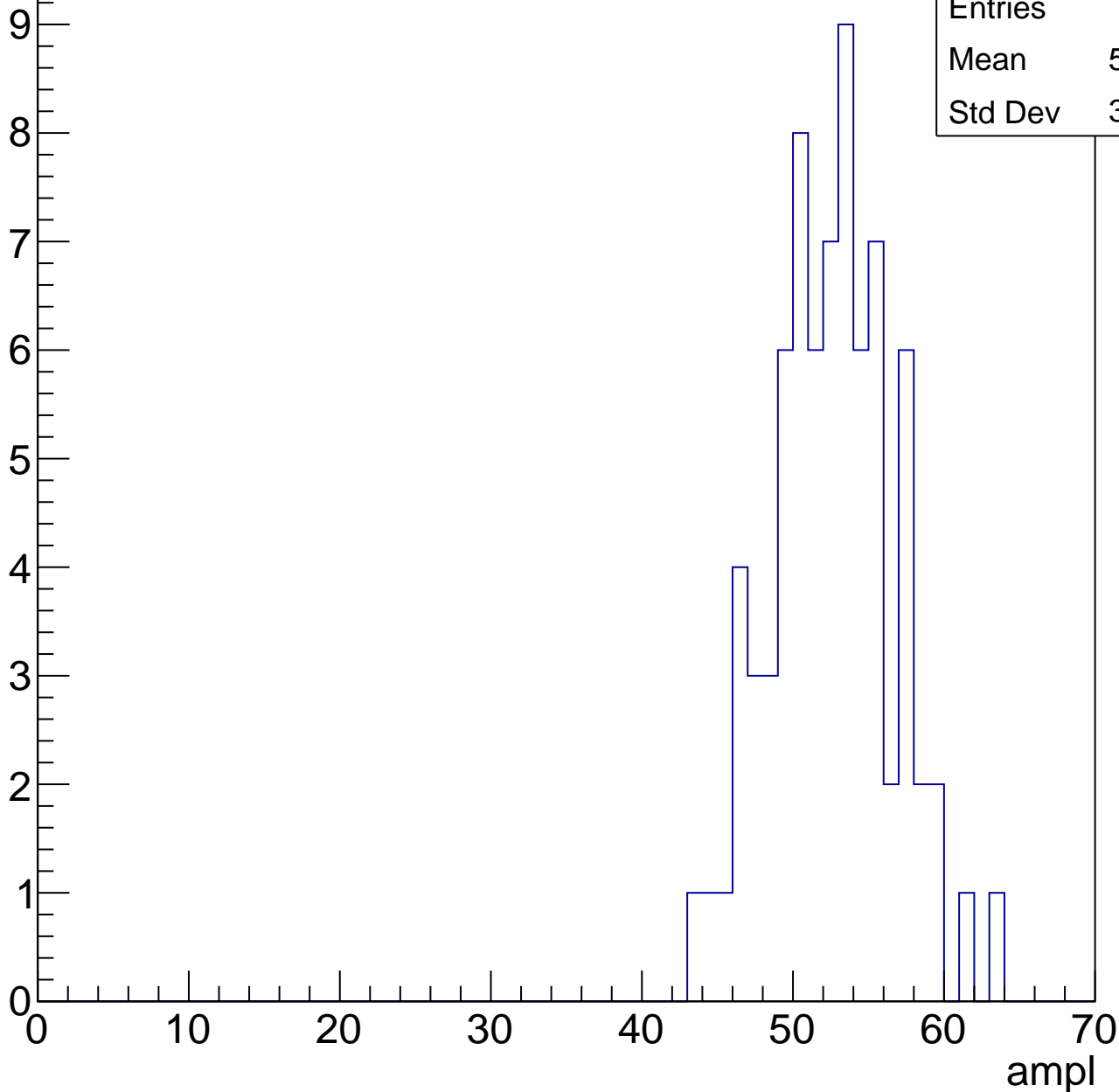


B1L101S, U25-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

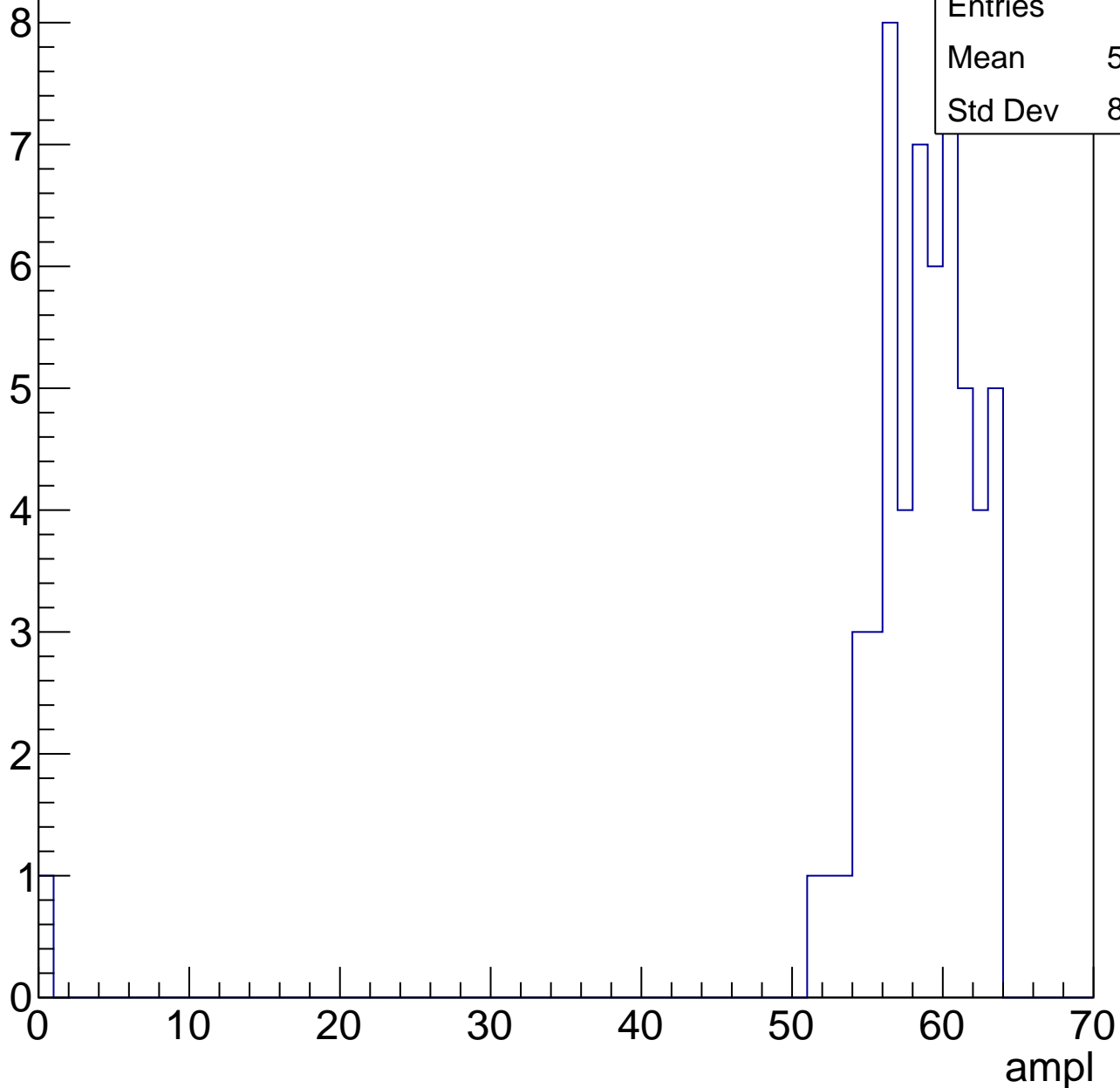
Entries	76
Mean	52.14
Std Dev	3.976



B1L101S, U25-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



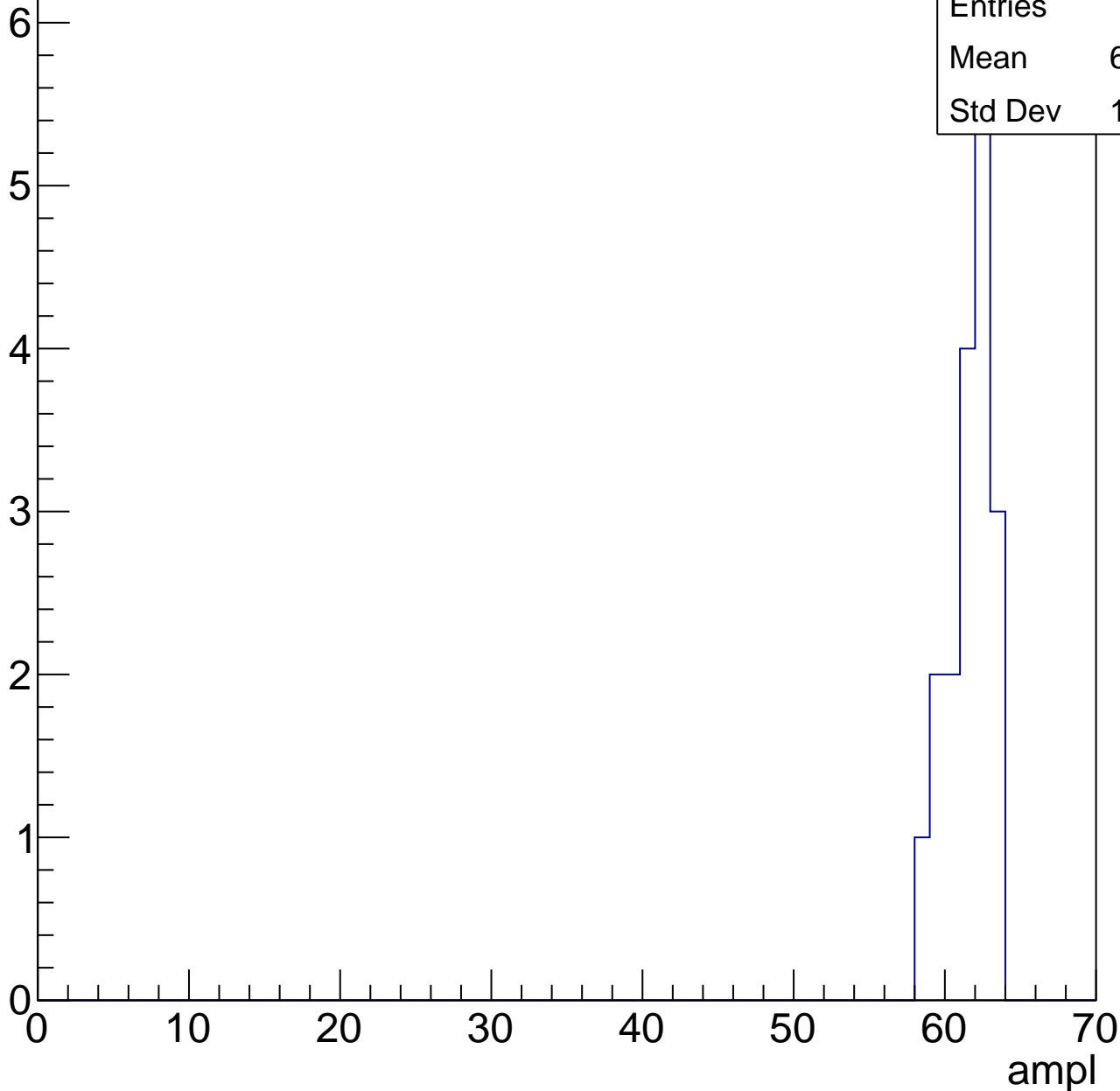
Entries	57
Mean	57.32
Std Dev	8.197

B1L101S, U25-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	61.17
Std Dev	1.424



B1L101S, U25-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U25-ch100, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	27.75
Std Dev	7.136

Gaus mean : 29.6028

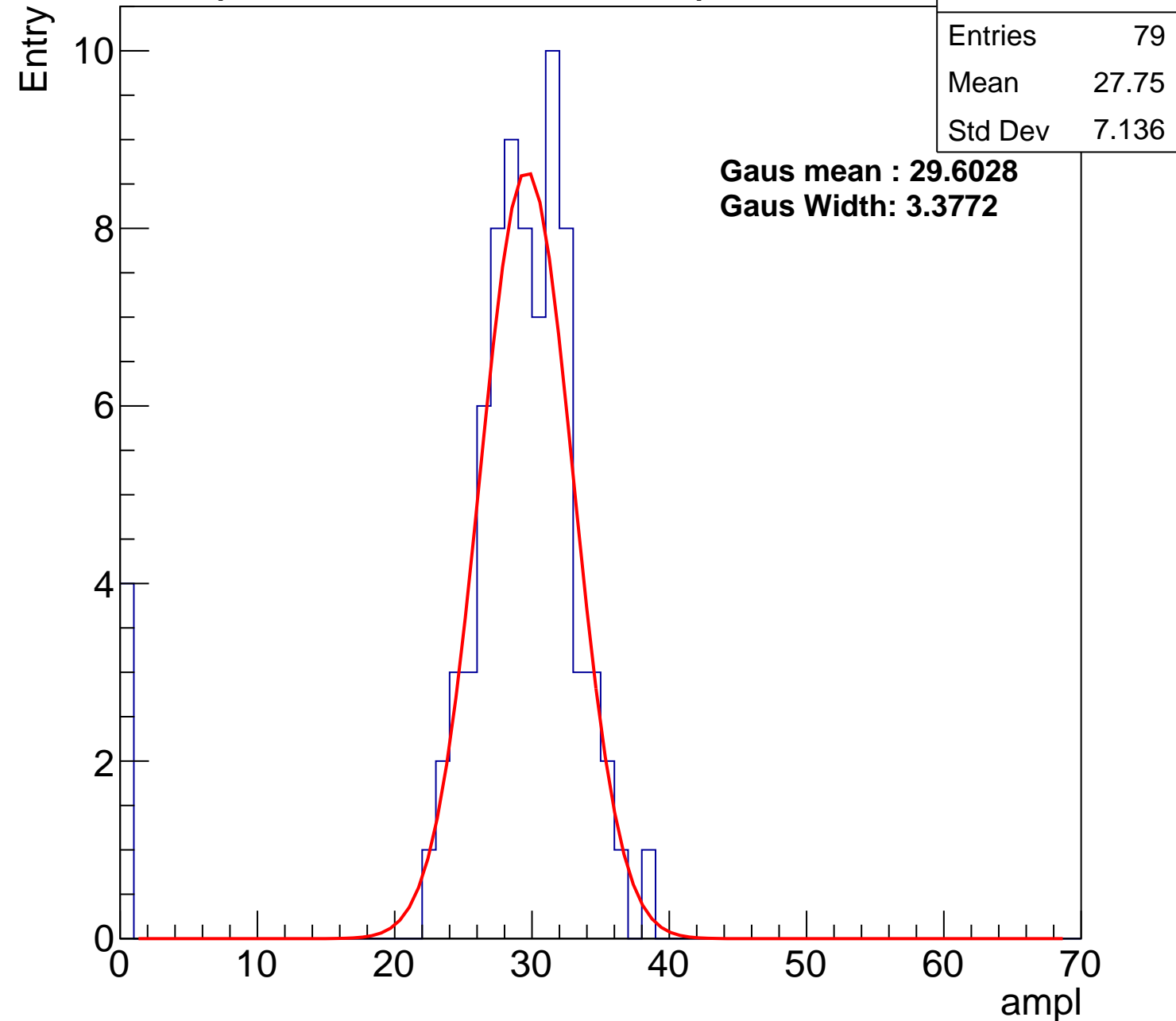
Gaus Width: 3.3772

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch100, adc1

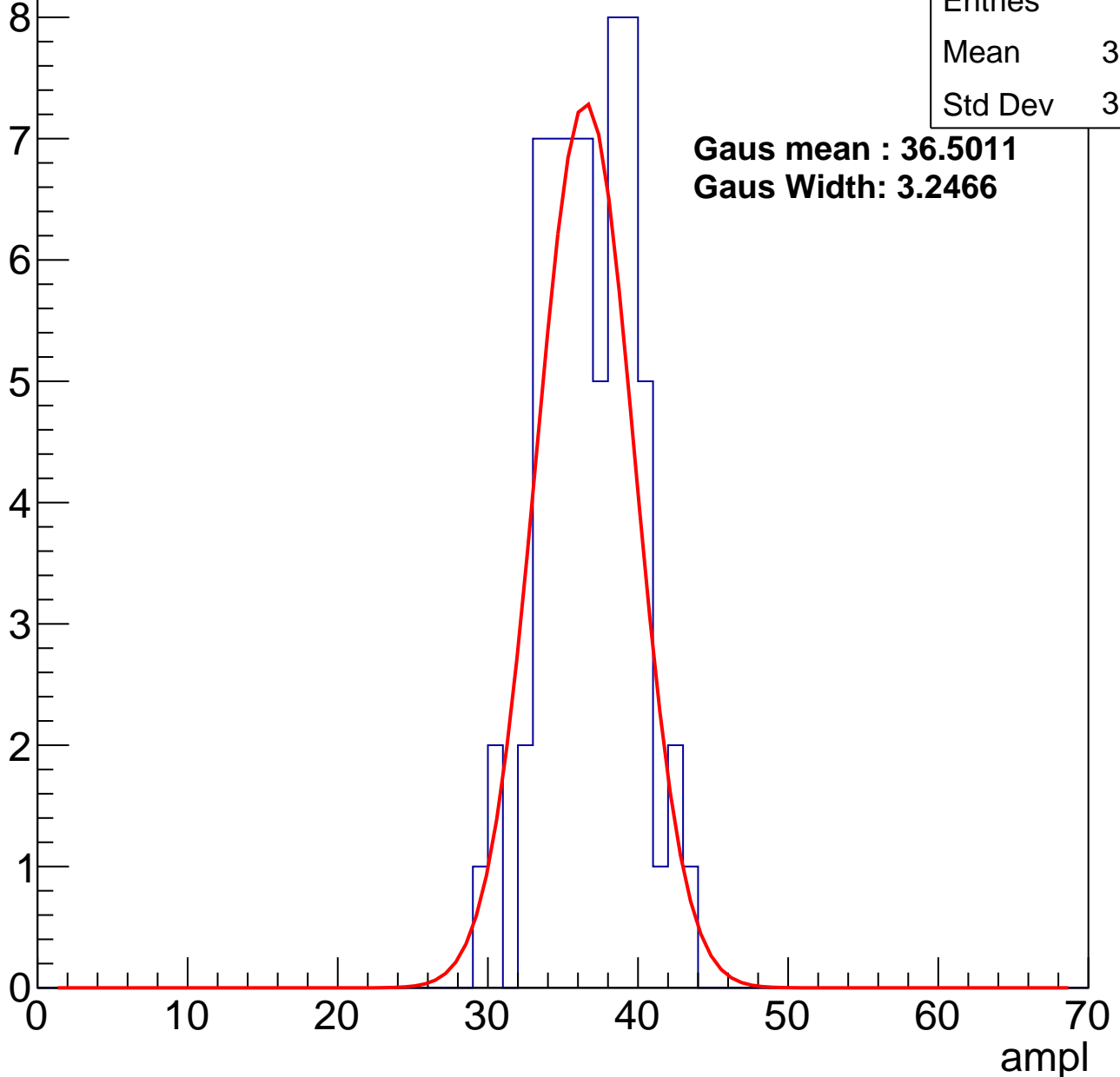
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.32
Std Dev	3.033

Gaus mean : 36.5011

Gaus Width: 3.2466



B1L101S, U25-ch100, adc2

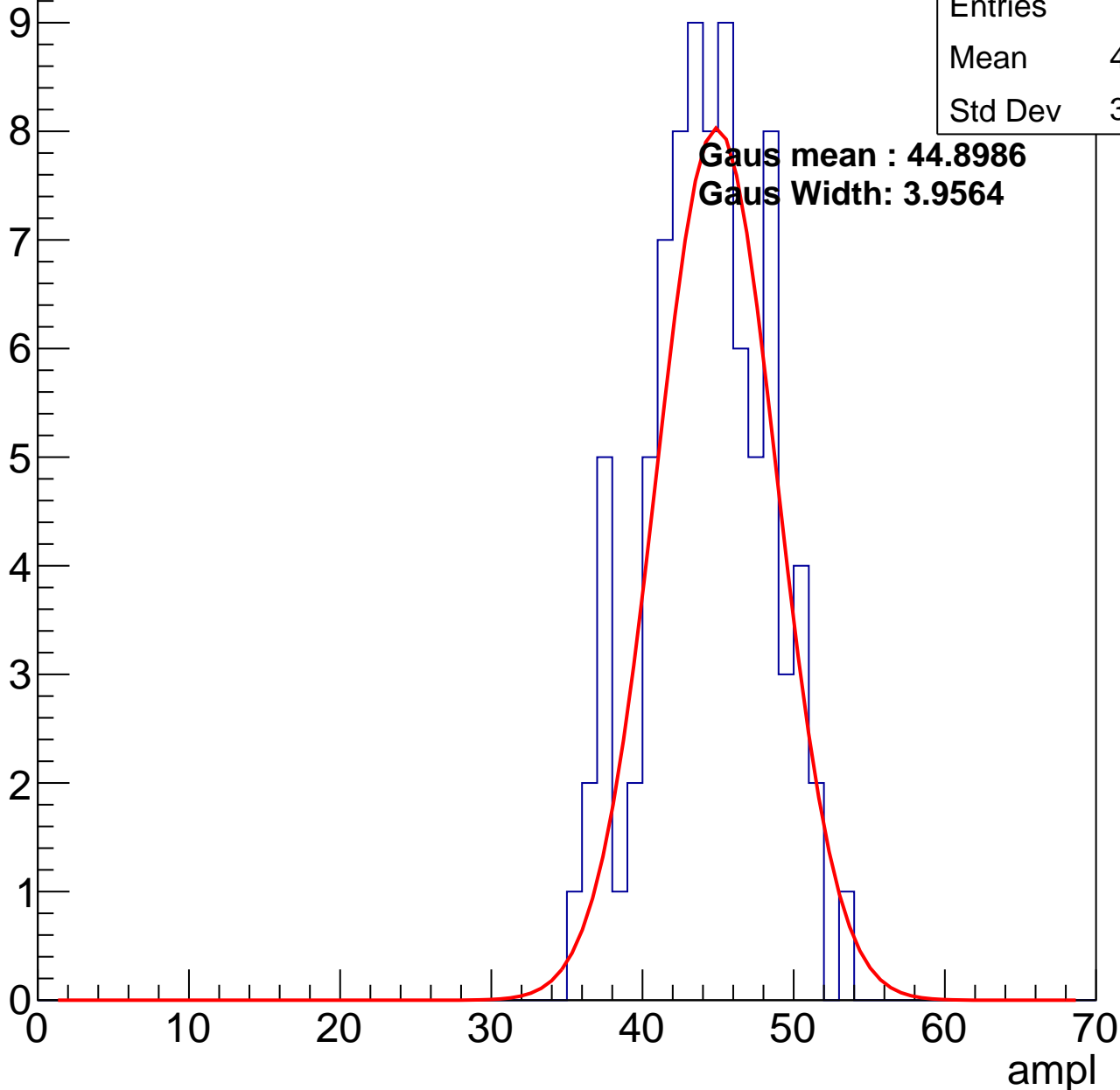
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	43.86
Std Dev	3.927

Gaus mean : 44.8986

Gaus Width: 3.9564

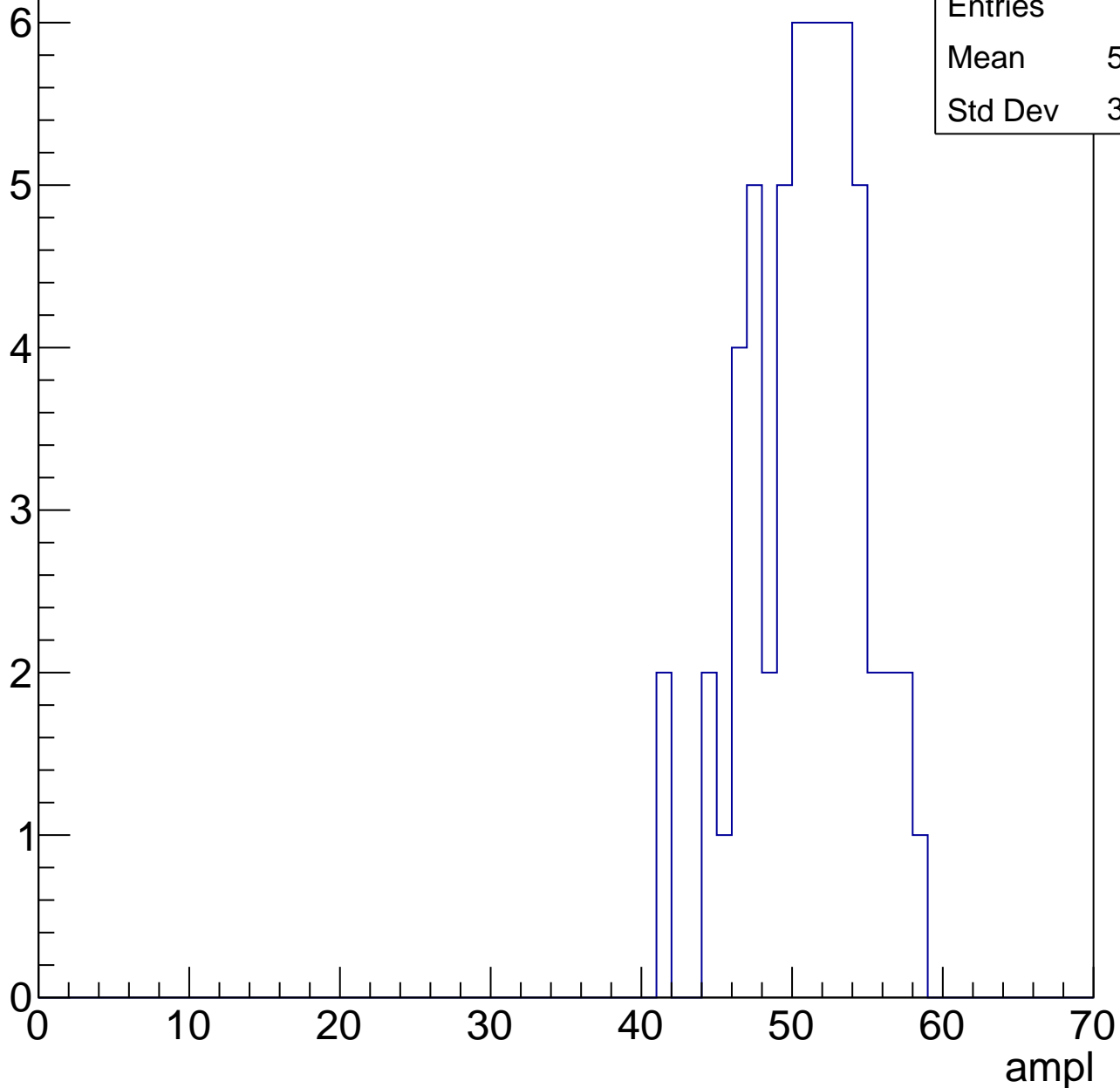


B1L101S, U25-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	50.44
Std Dev	3.788

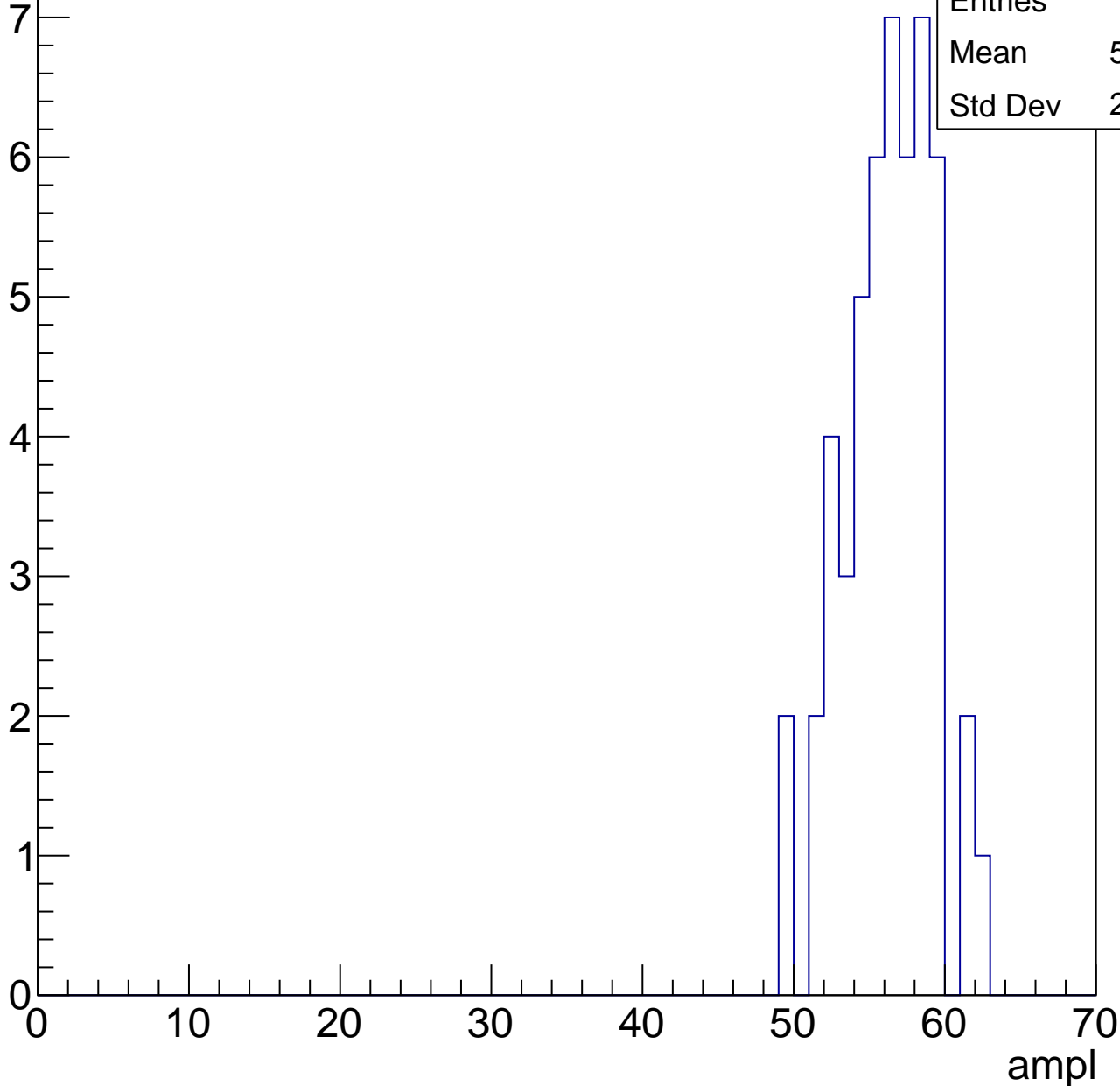


B1L101S, U25-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

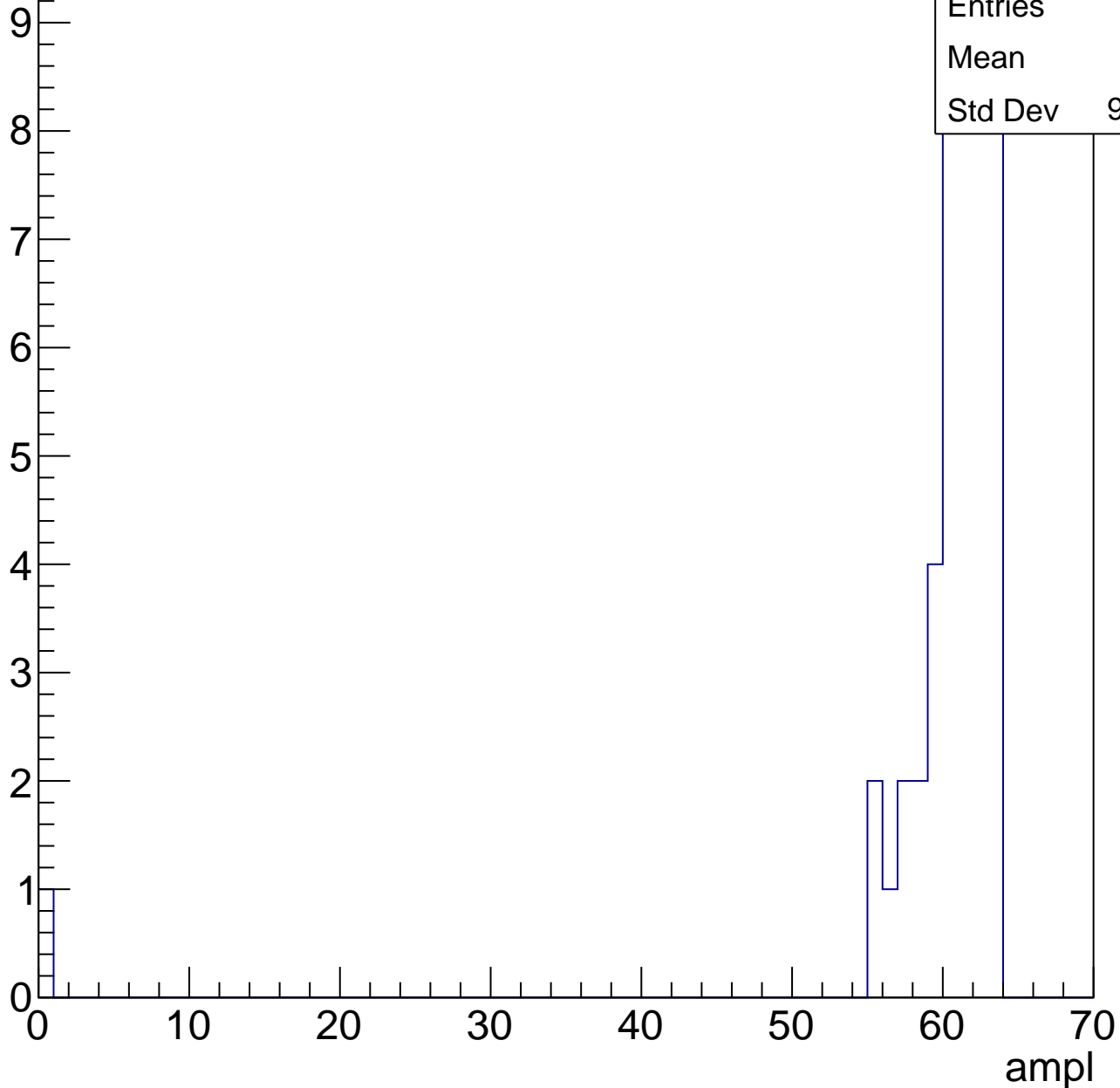
Entries	51
Mean	55.78
Std Dev	2.926



B1L101S, U25-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch101, adc0

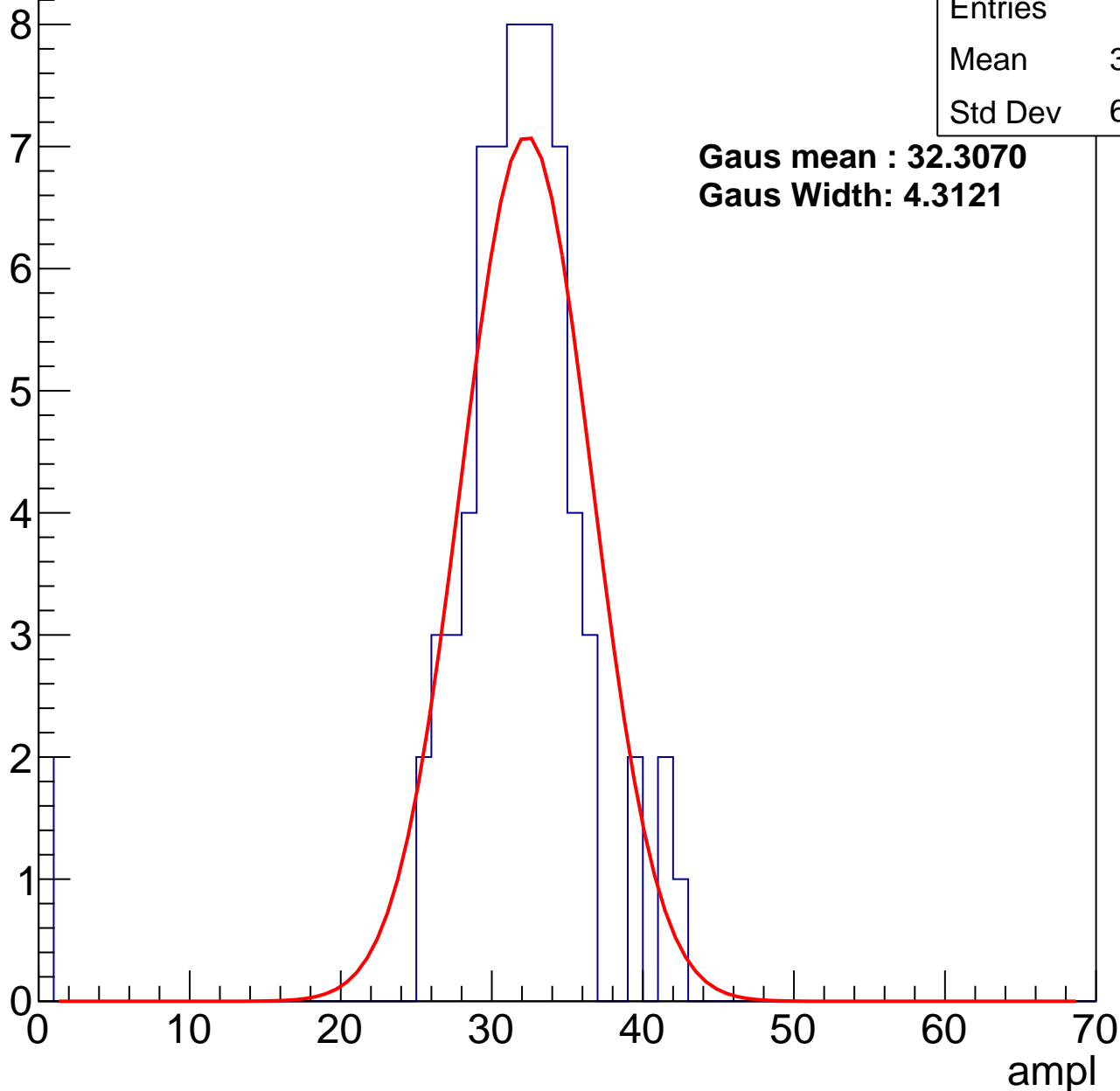
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	30.85
Std Dev	6.366

Gaus mean : 32.3070

Gaus Width: 4.3121



B1L101S, U25-ch101, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	37.73
Std Dev	3.597

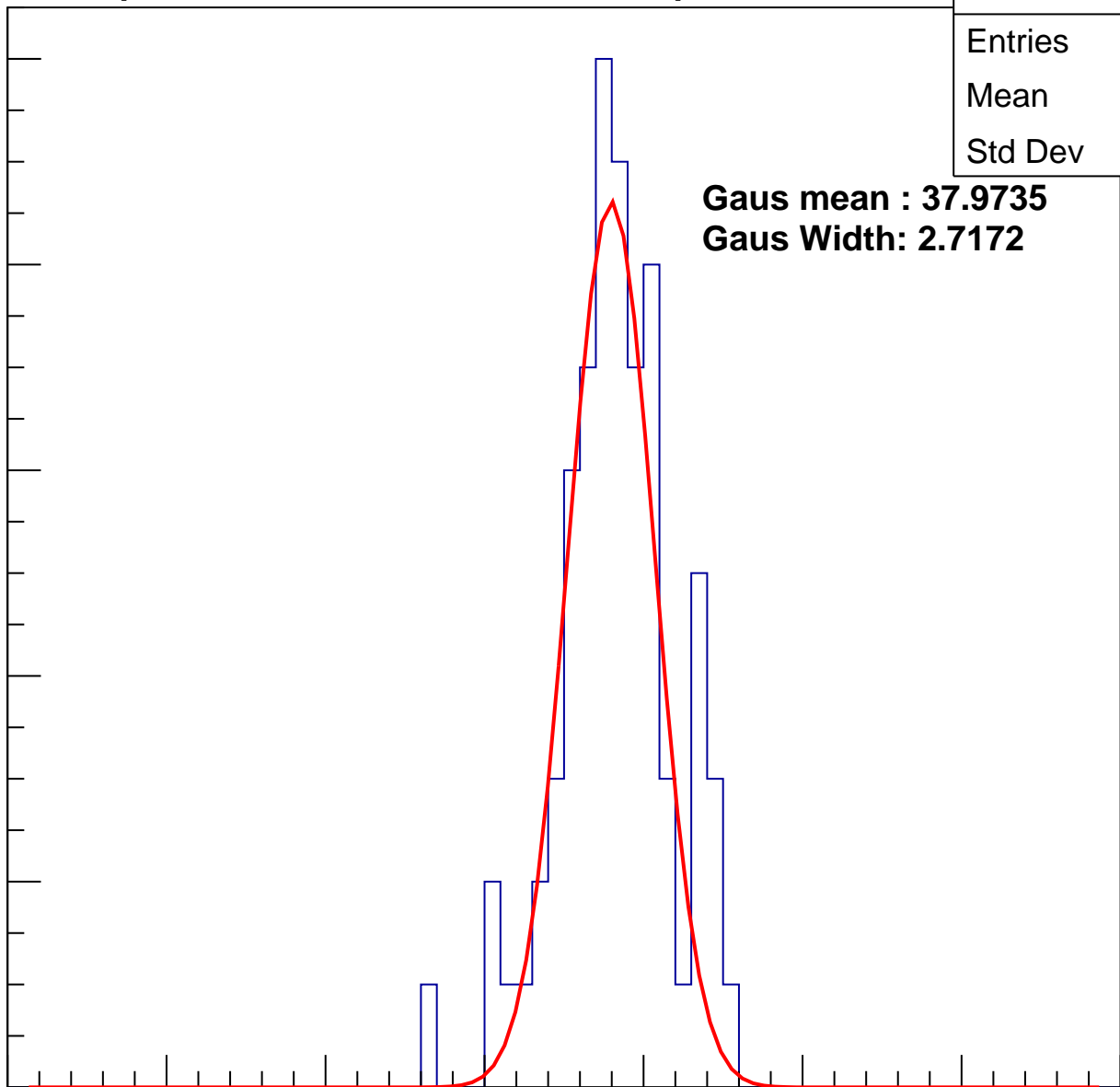
Gaus mean : 37.9735
Gaus Width: 2.7172

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch101, adc2

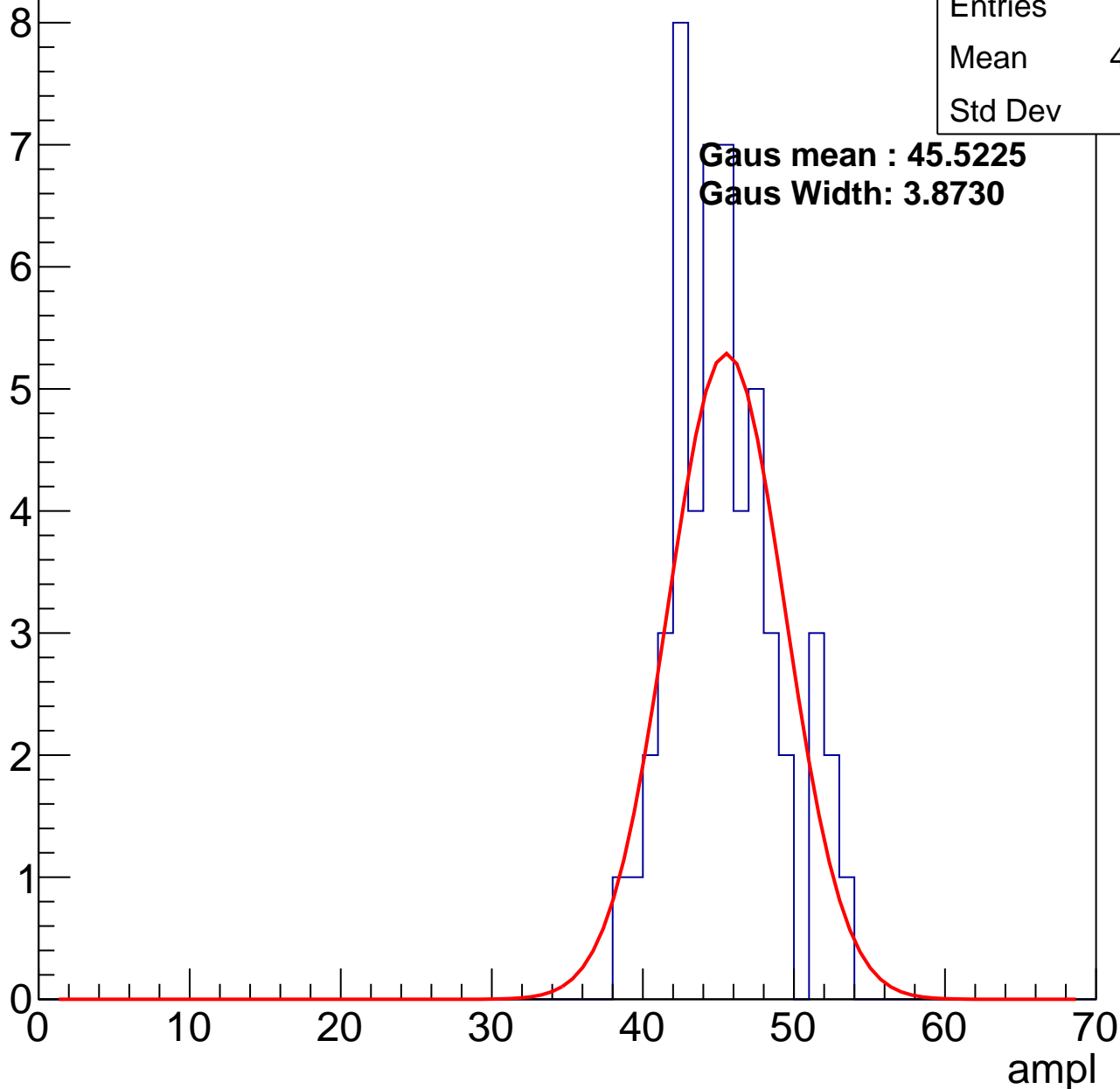
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	44.94
Std Dev	3.45

Gaus mean : 45.5225

Gaus Width: 3.8730



B1L101S, U25-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	90
Mean	51.16
Std Dev	3.966

Entry

10

8

6

4

2

0

0

10

20

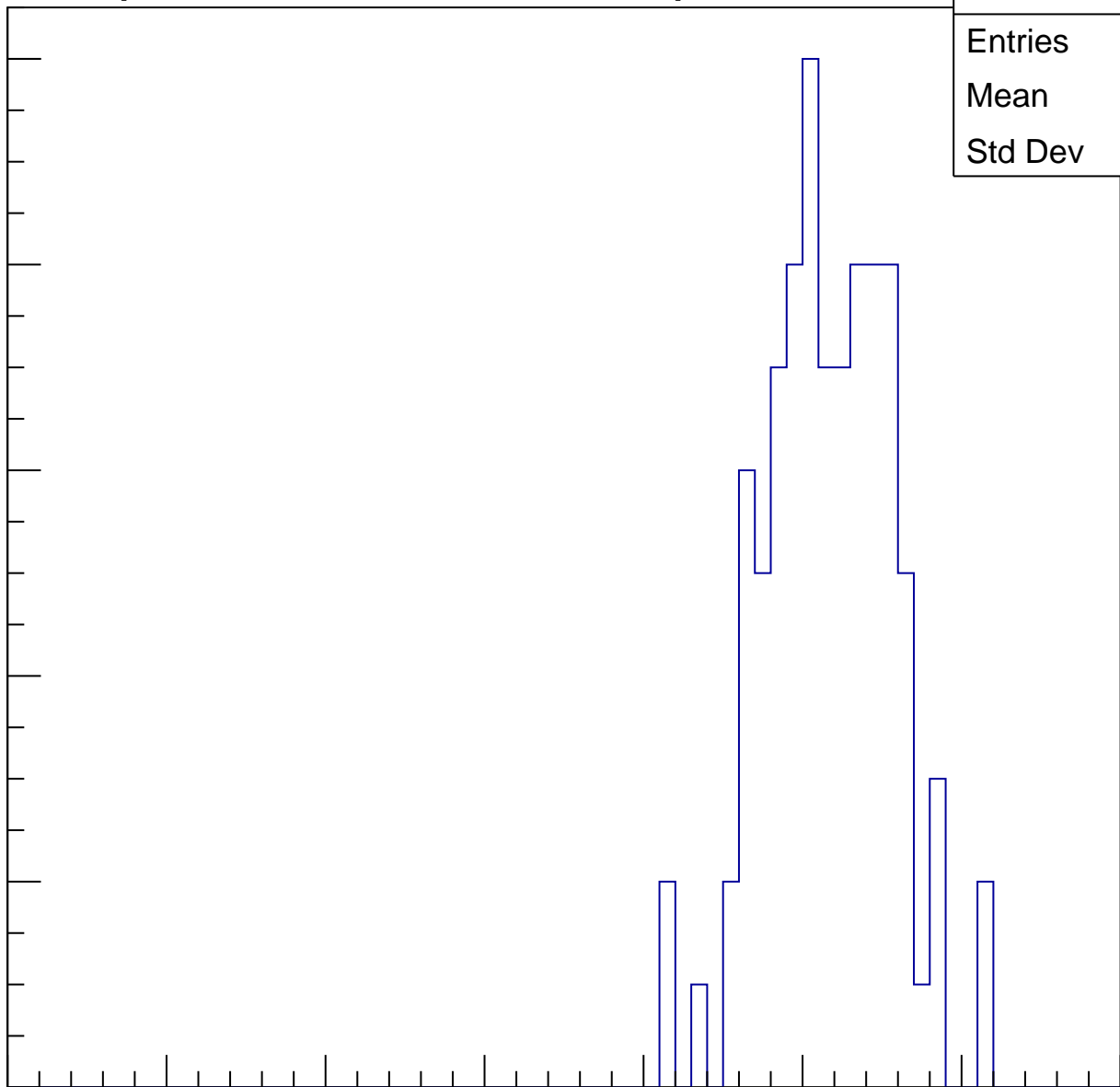
30

40

50

60

ampl

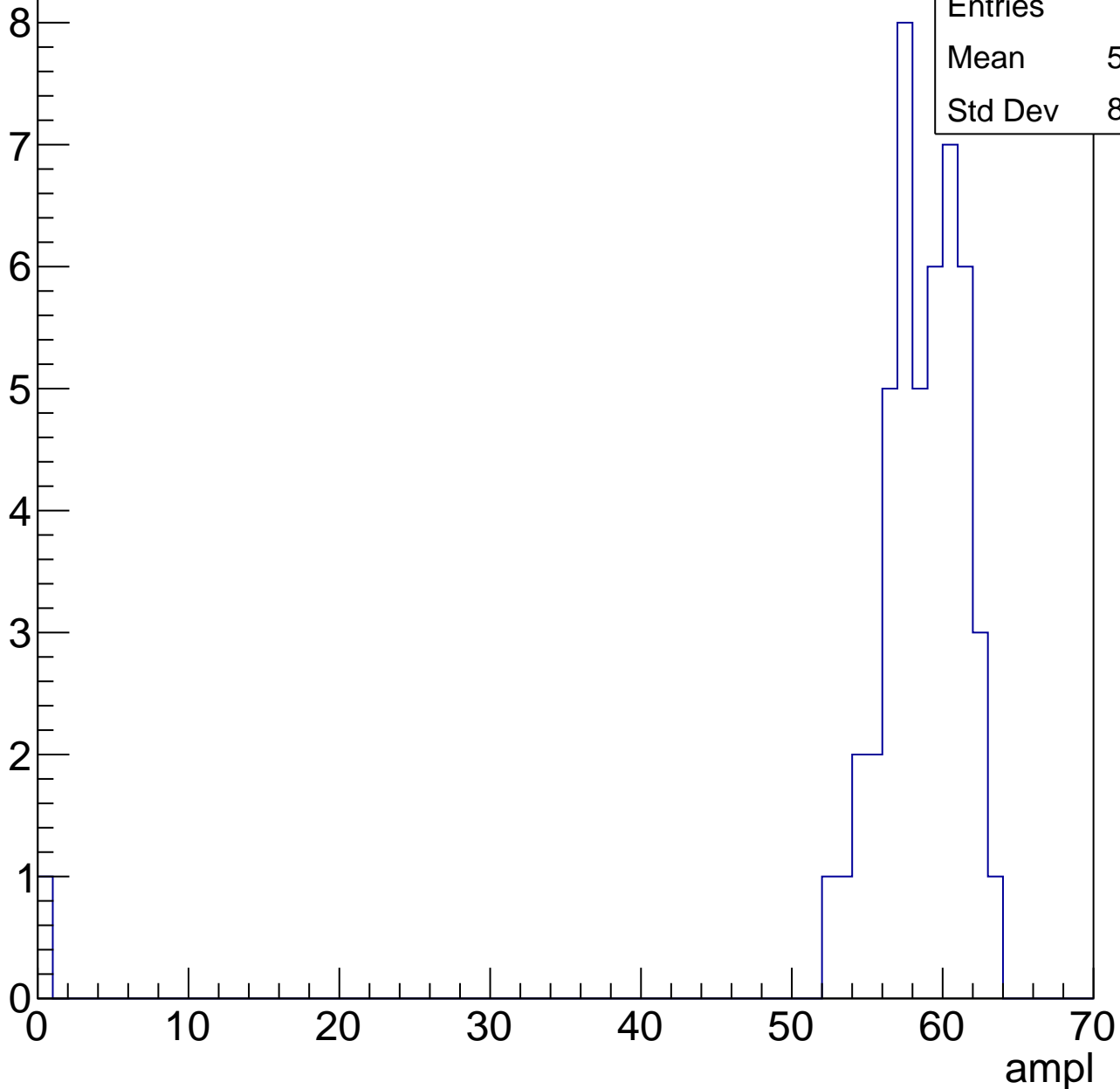


B1L101S, U25-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	57.04
Std Dev	8.684

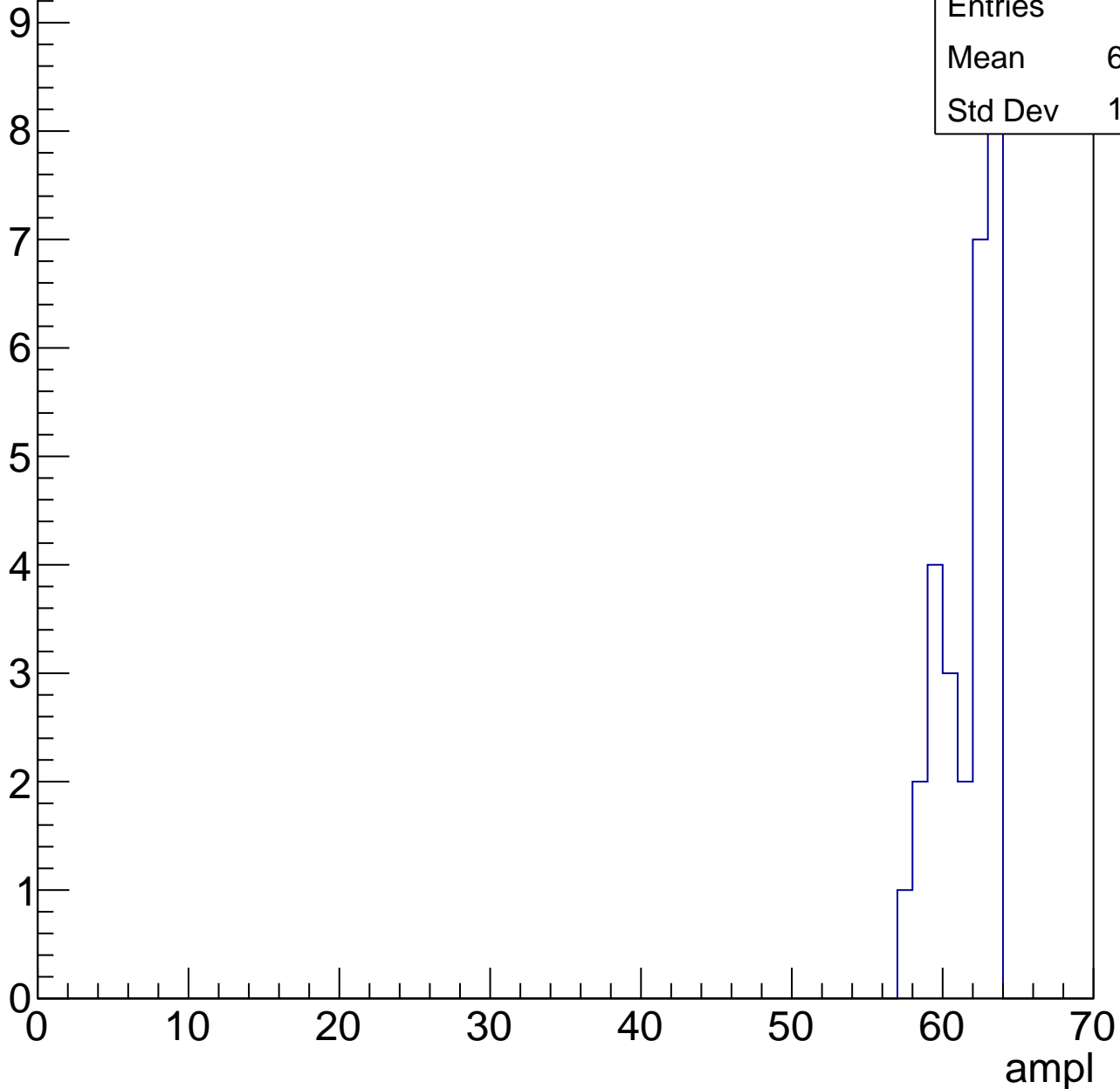


B1L101S, U25-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	28
Mean	61.14
Std Dev	1.846



B1L101S, U25-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch102, adc0

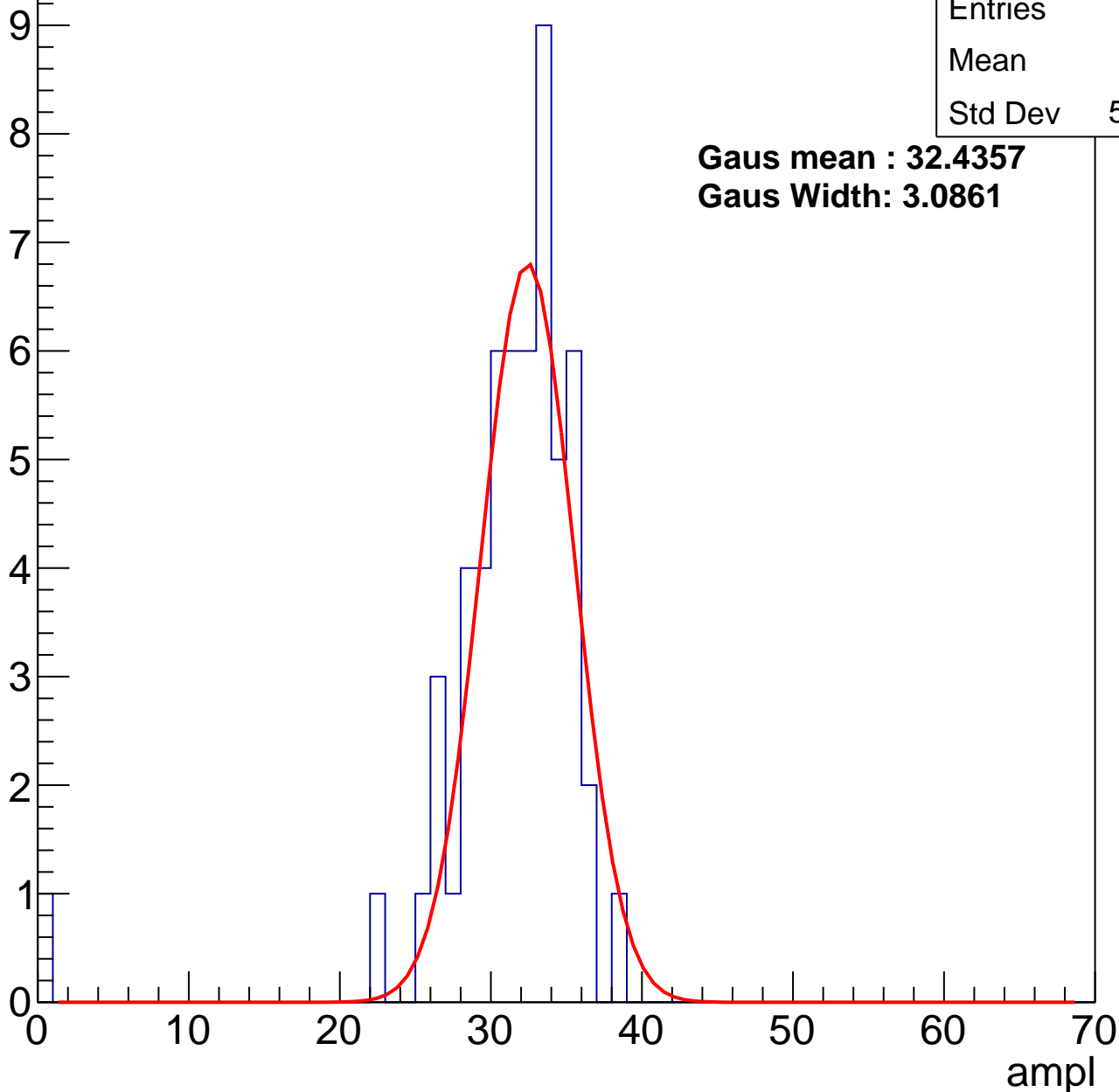
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	30.8
Std Dev	5.187

Gaus mean : 32.4357

Gaus Width: 3.0861



B1L101S, U25-ch102, adc1

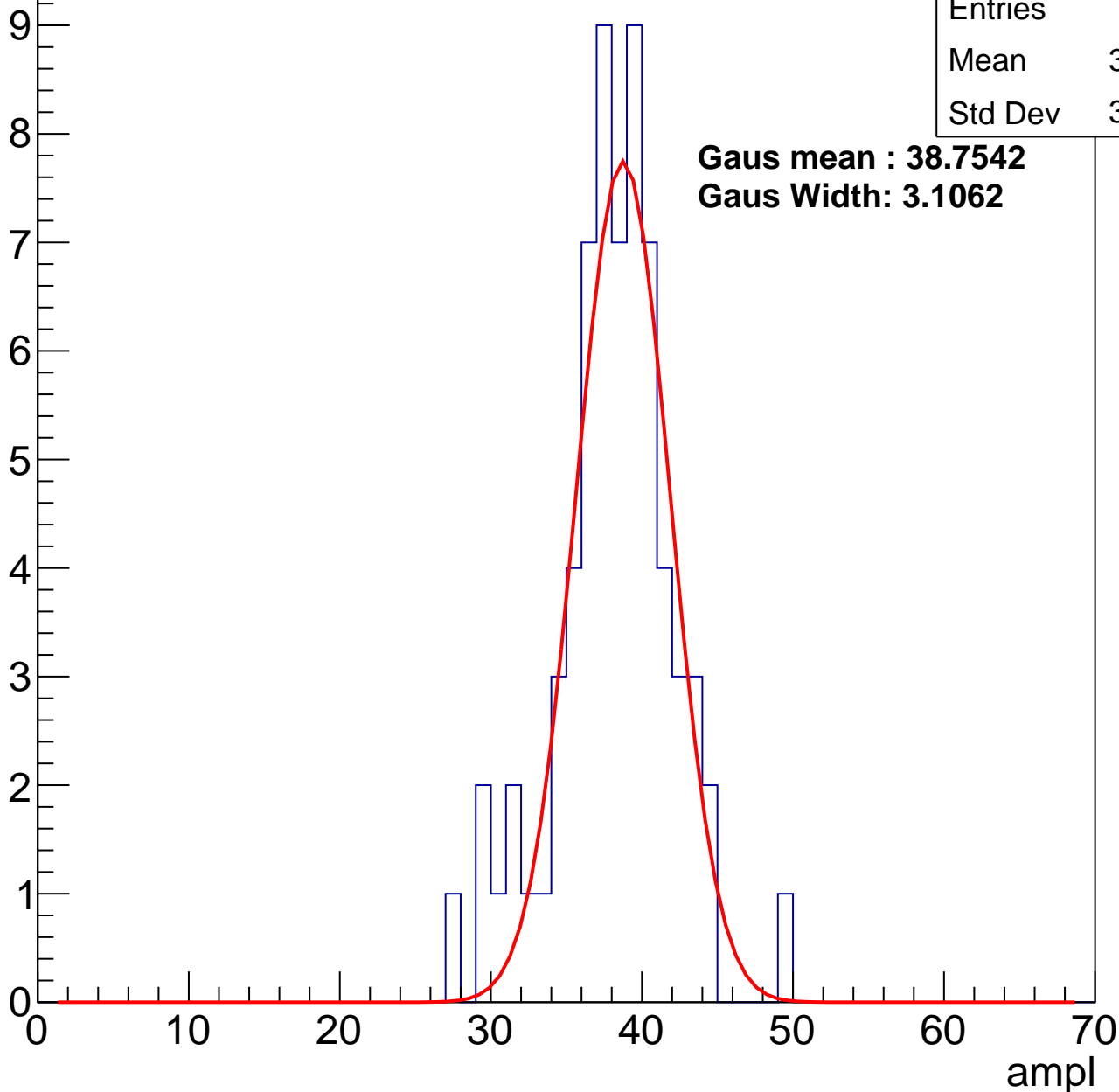
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.64
Std Dev	3.878

Gaus mean : 38.7542

Gaus Width: 3.1062



B1L101S, U25-ch102, adc2

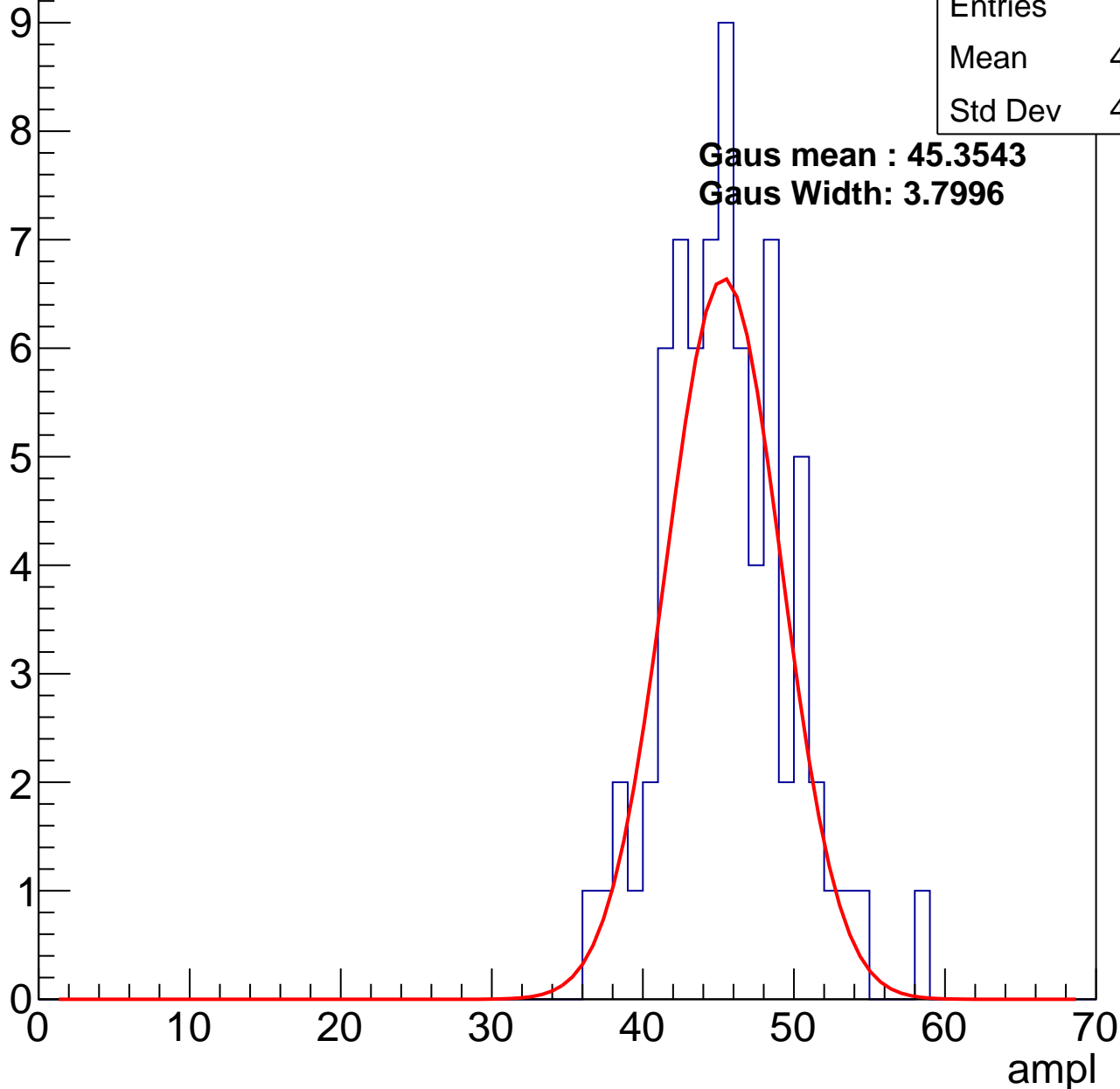
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	45.08
Std Dev	4.075

Gaus mean : 45.3543

Gaus Width: 3.7996

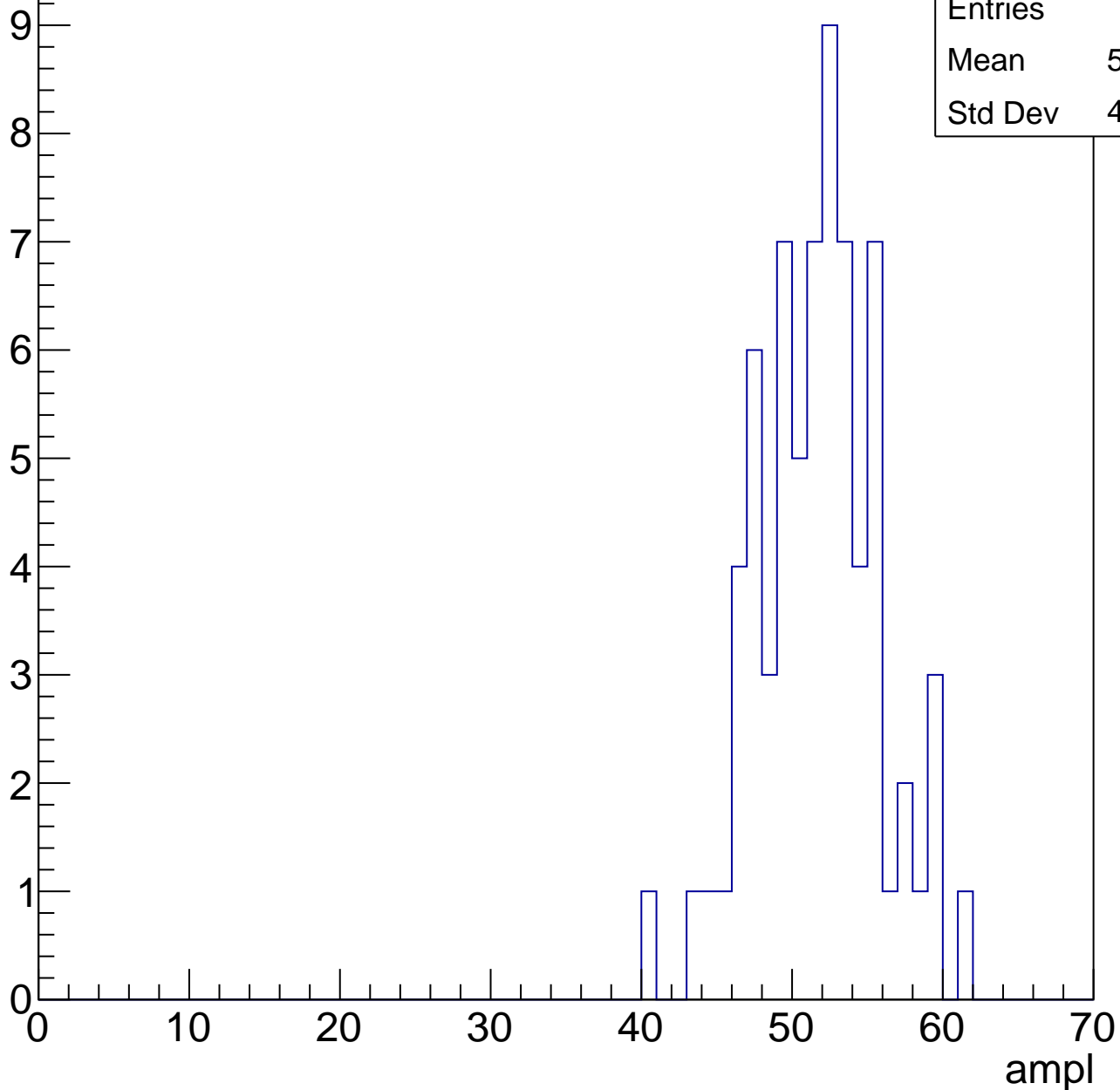


B1L101S, U25-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	51.24
Std Dev	4.016

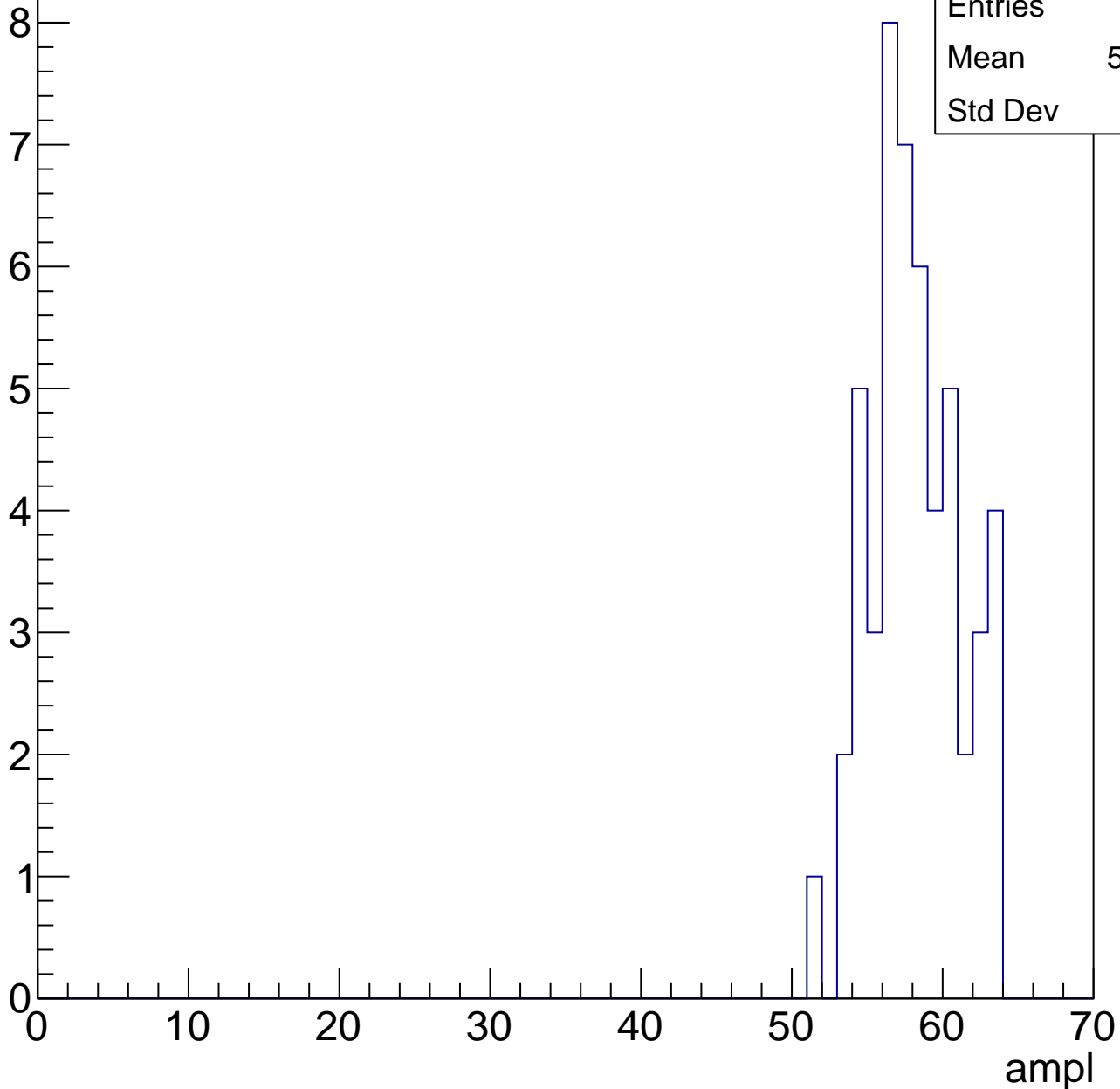


B1L101S, U25-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	57.66
Std Dev	2.93

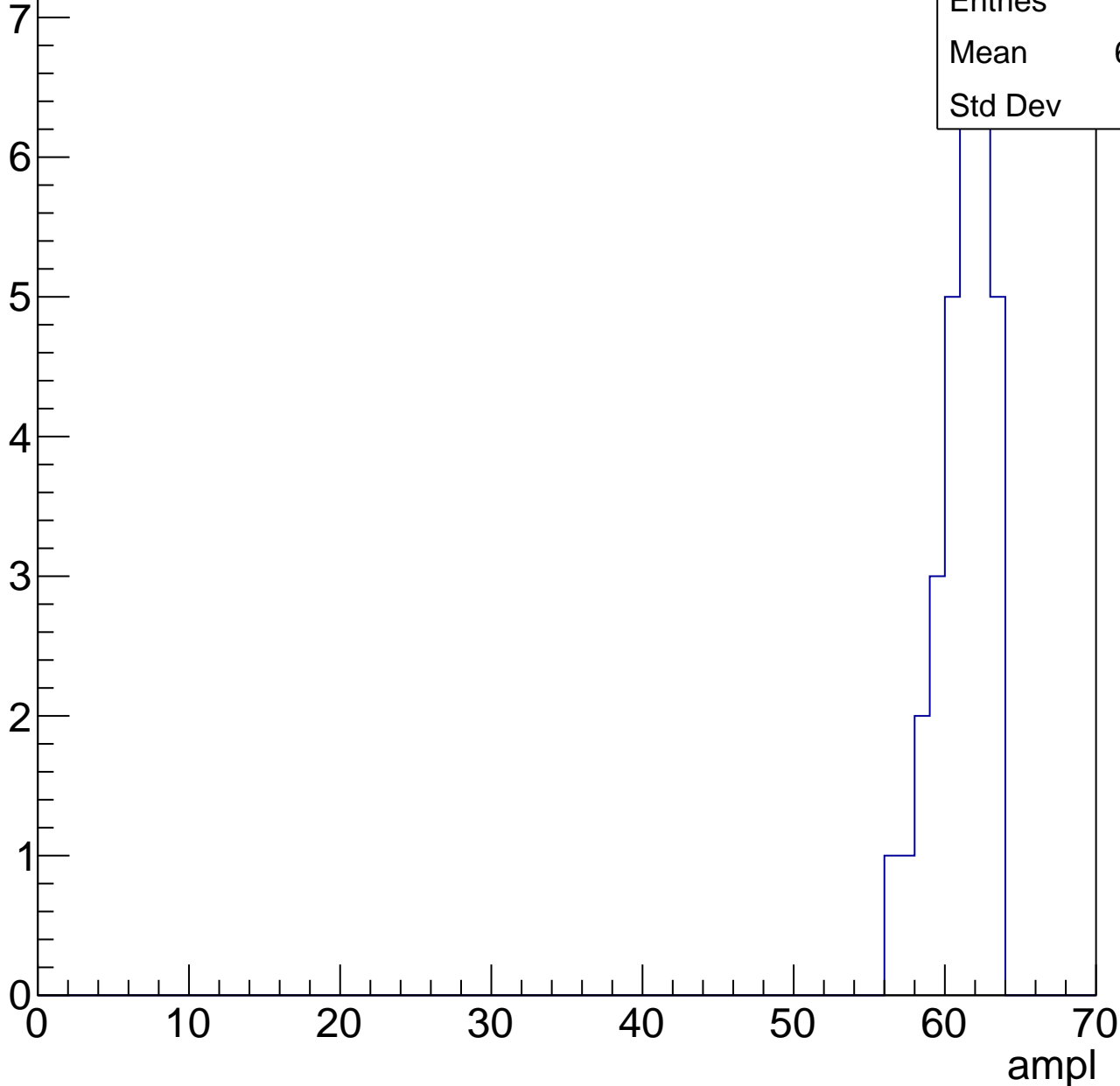


B1L101S, U25-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	60.71
Std Dev	1.8



B1L101S, U25-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch103, adc0

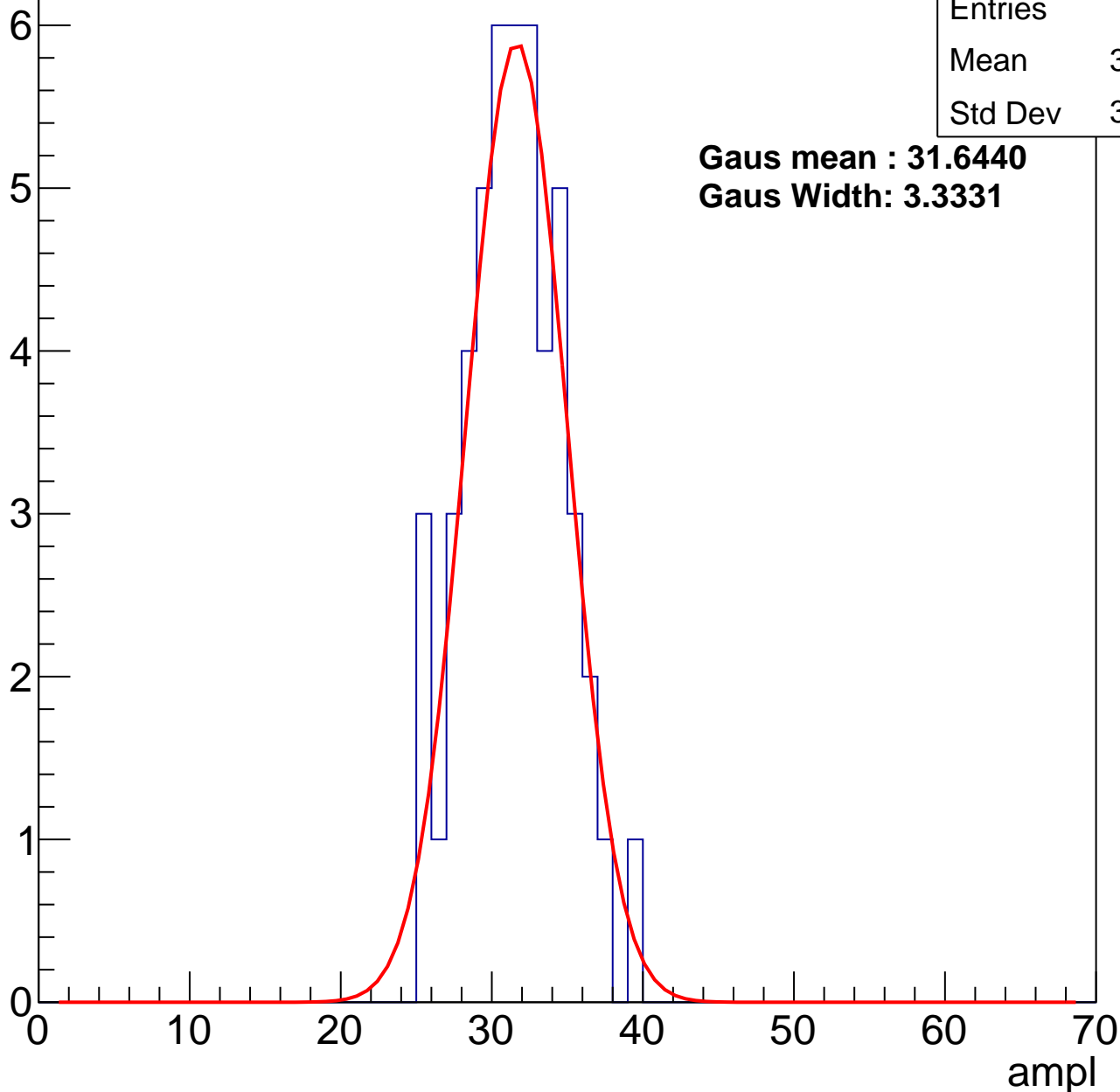
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	31.04
Std Dev	3.187

Gaus mean : 31.6440

Gaus Width: 3.3331



B1L101S, U25-ch103, adc1

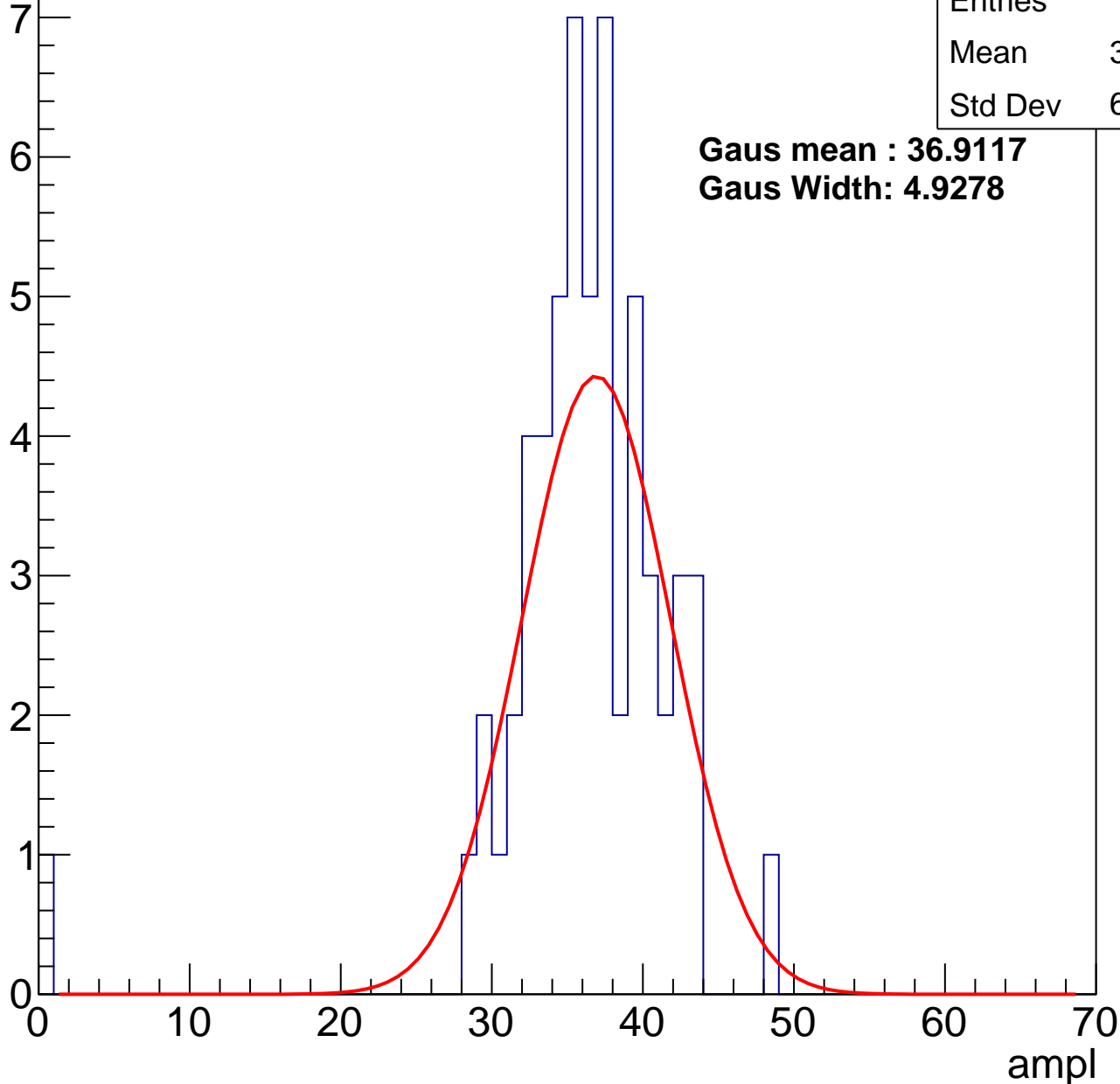
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	35.66
Std Dev	6.188

Gaus mean : 36.9117

Gaus Width: 4.9278



B1L101S, U25-ch103, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	96
Mean	43.66
Std Dev	4.46

Gaus mean : 43.8506

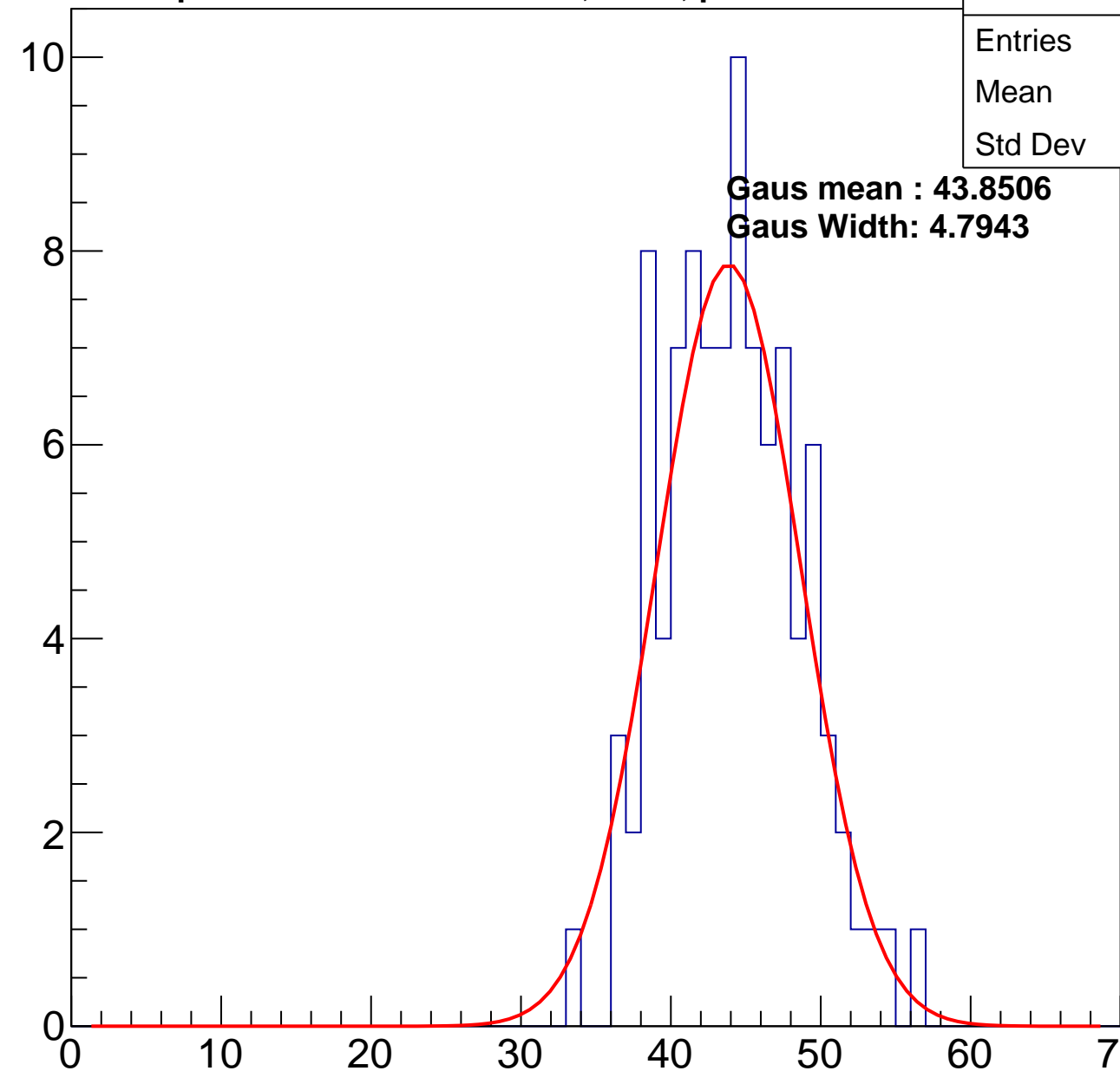
Gaus Width: 4.7943

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

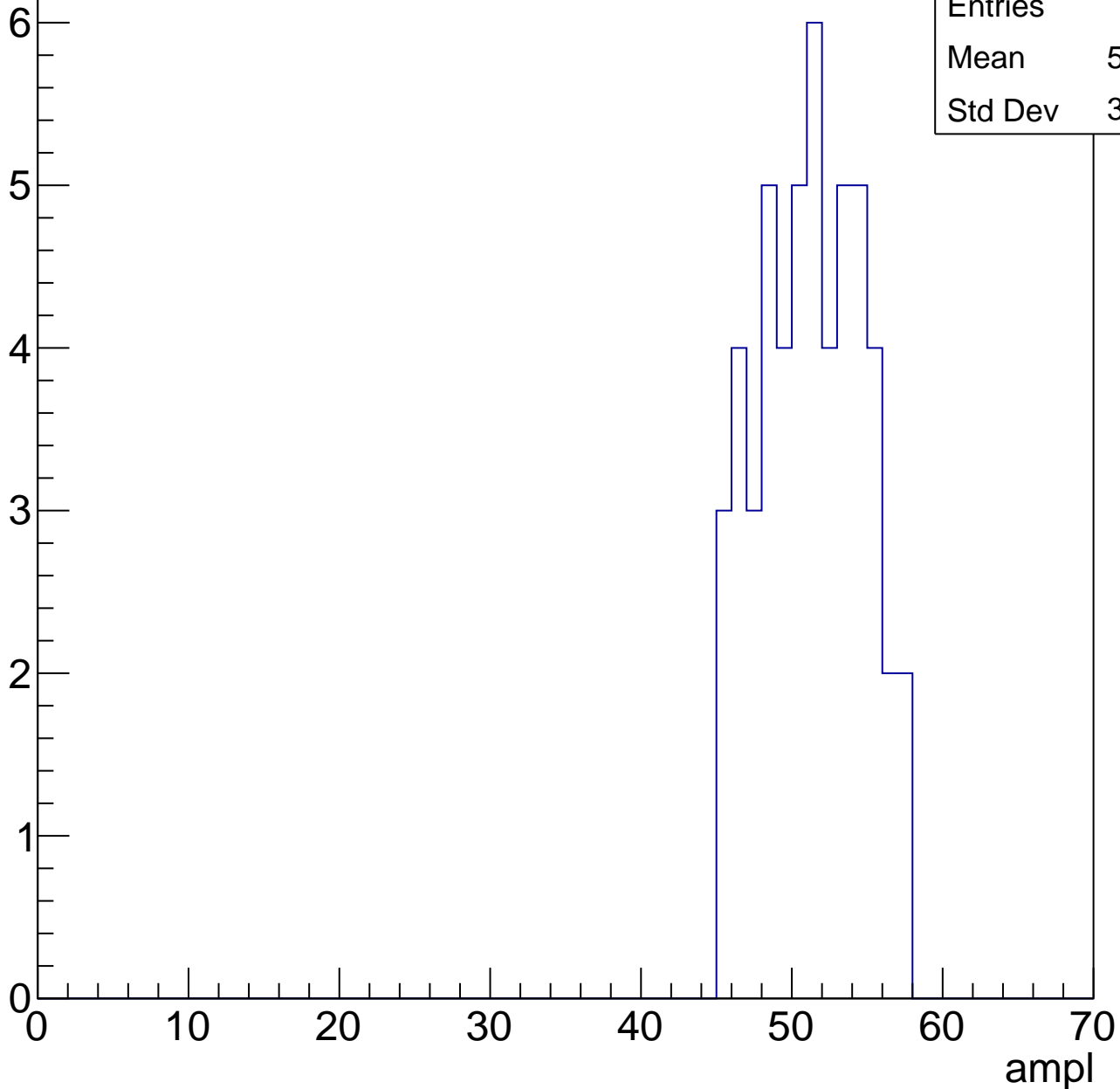


B1L101S, U25-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	50.79
Std Dev	3.324

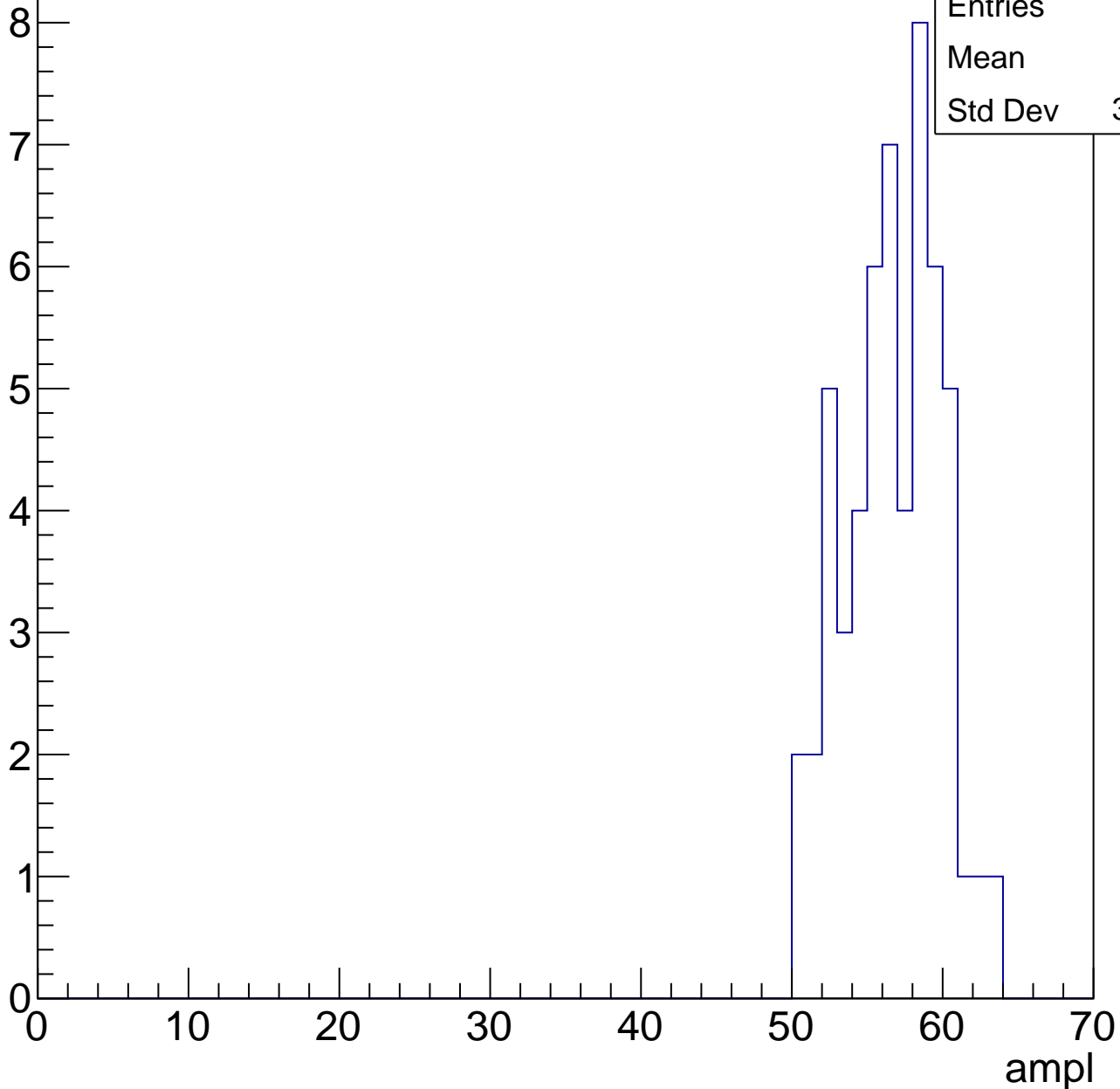


B1L101S, U25-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.2
Std Dev	3.101

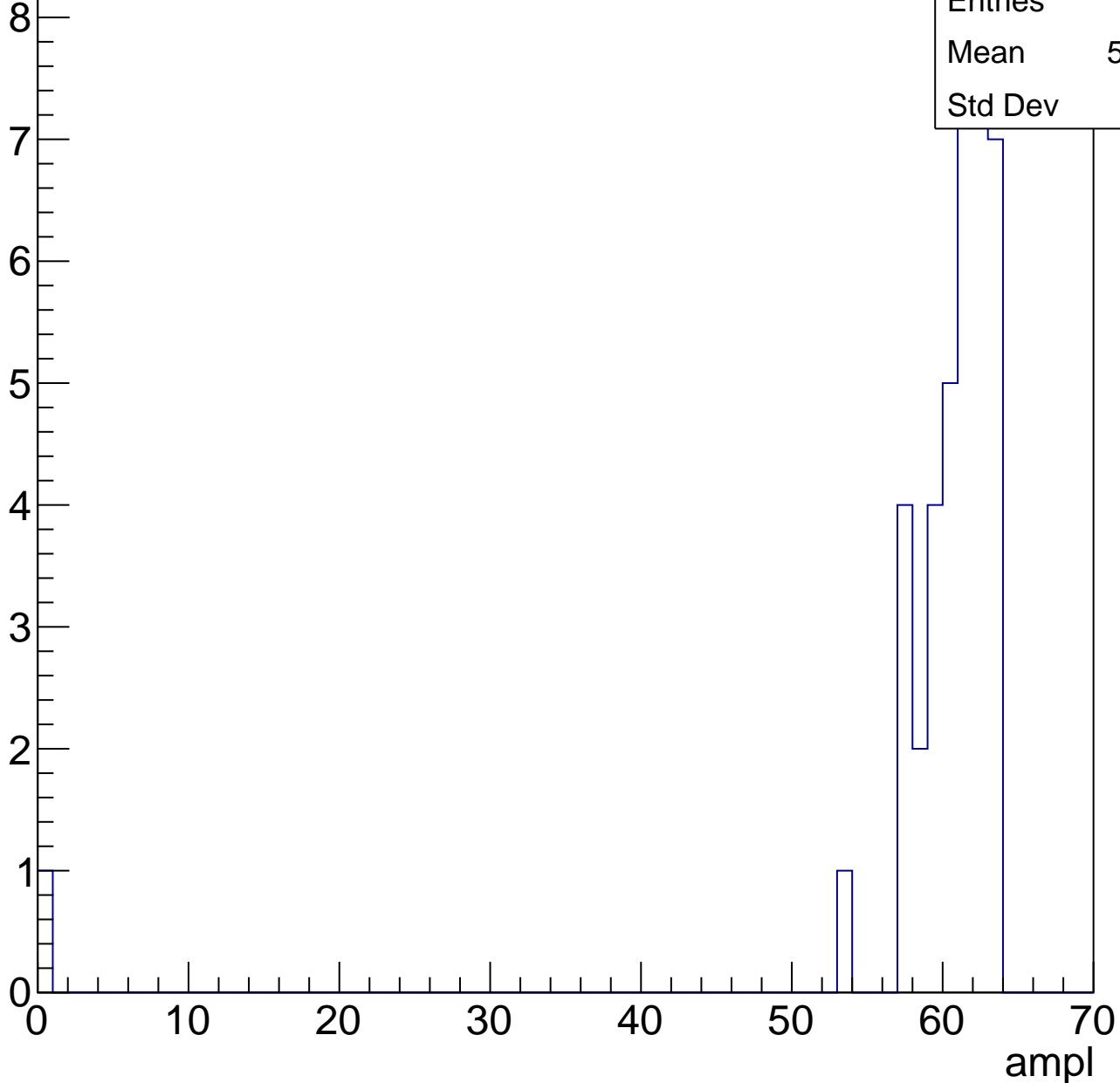


B1L101S, U25-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.95
Std Dev	9.69



B1L101S, U25-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

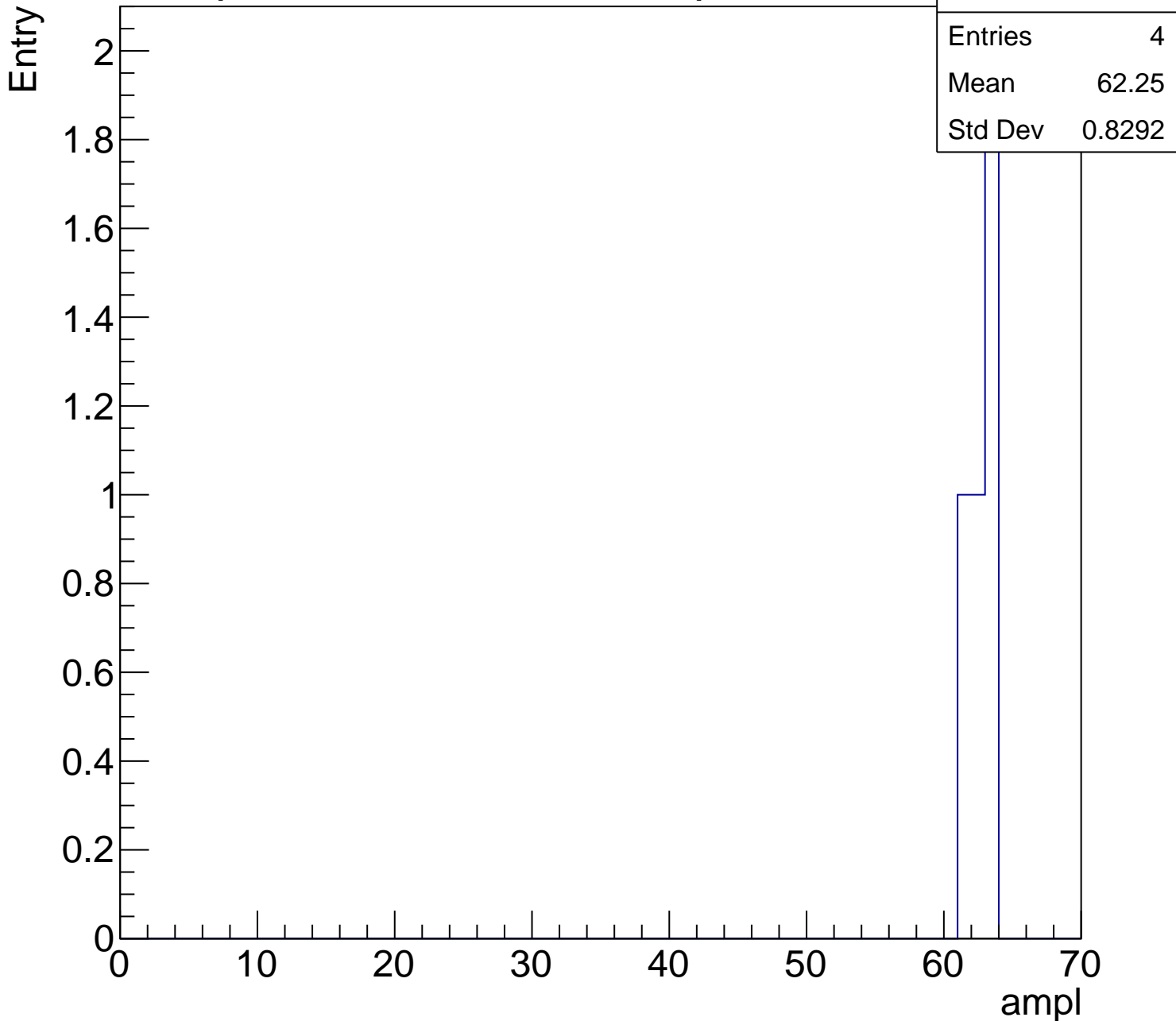
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	20
Std Dev	0

B1L101S, U25-ch104, adc0

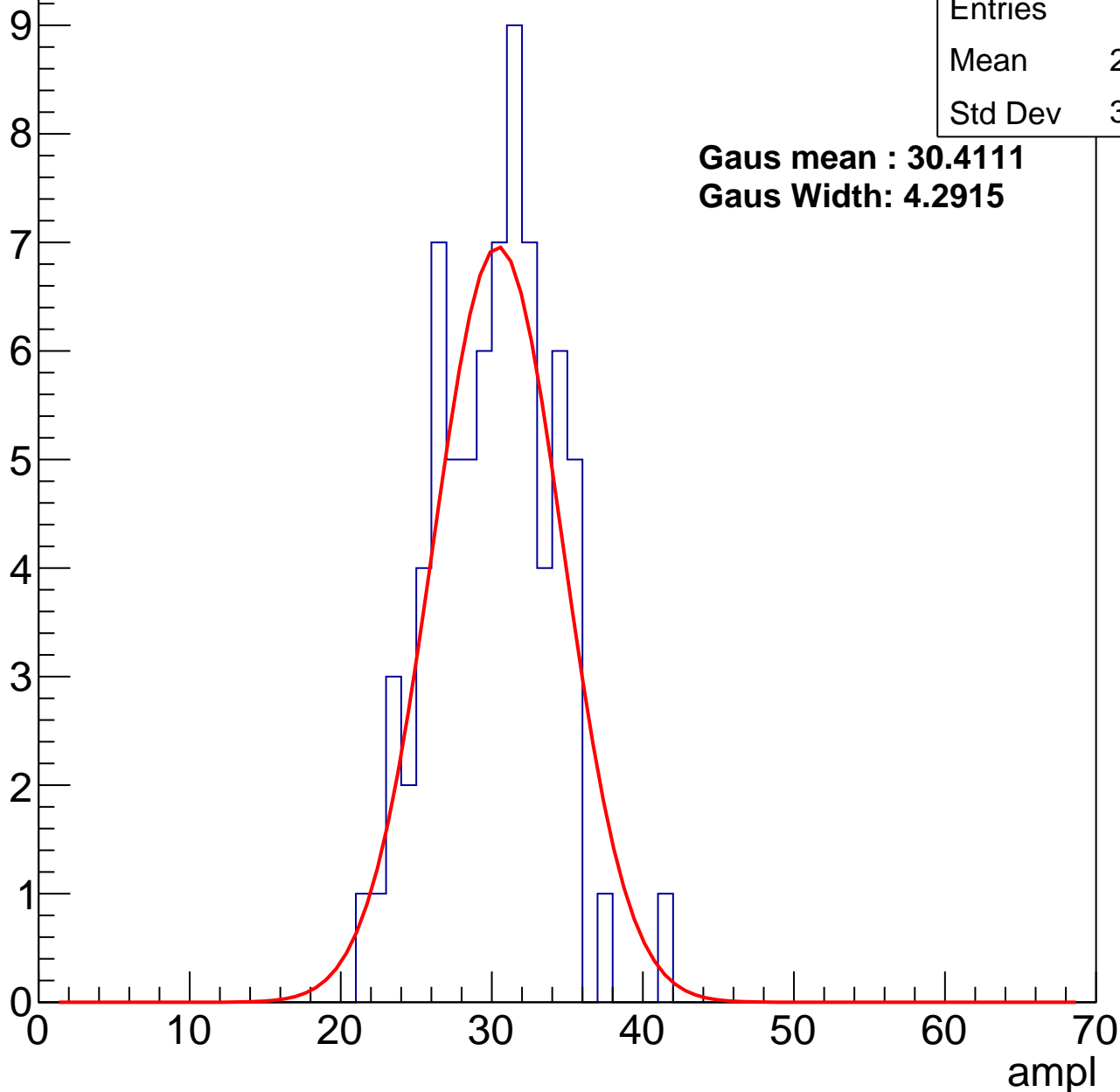
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.64
Std Dev	3.858

Gaus mean : 30.4111

Gaus Width: 4.2915



B1L101S, U25-ch104, adc1

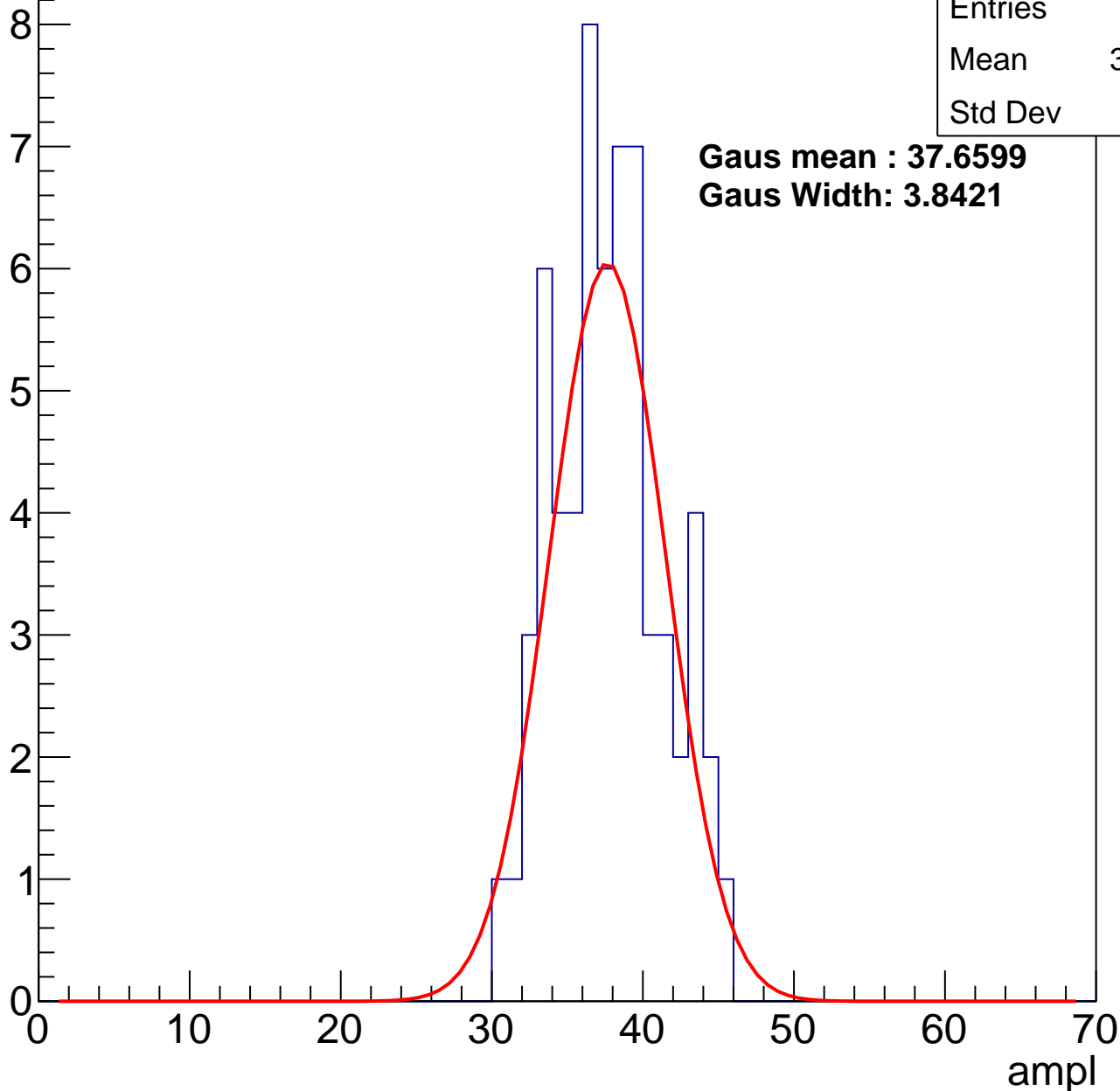
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.29
Std Dev	3.54

Gaus mean : 37.6599

Gaus Width: 3.8421



B1L101S, U25-ch104, adc2

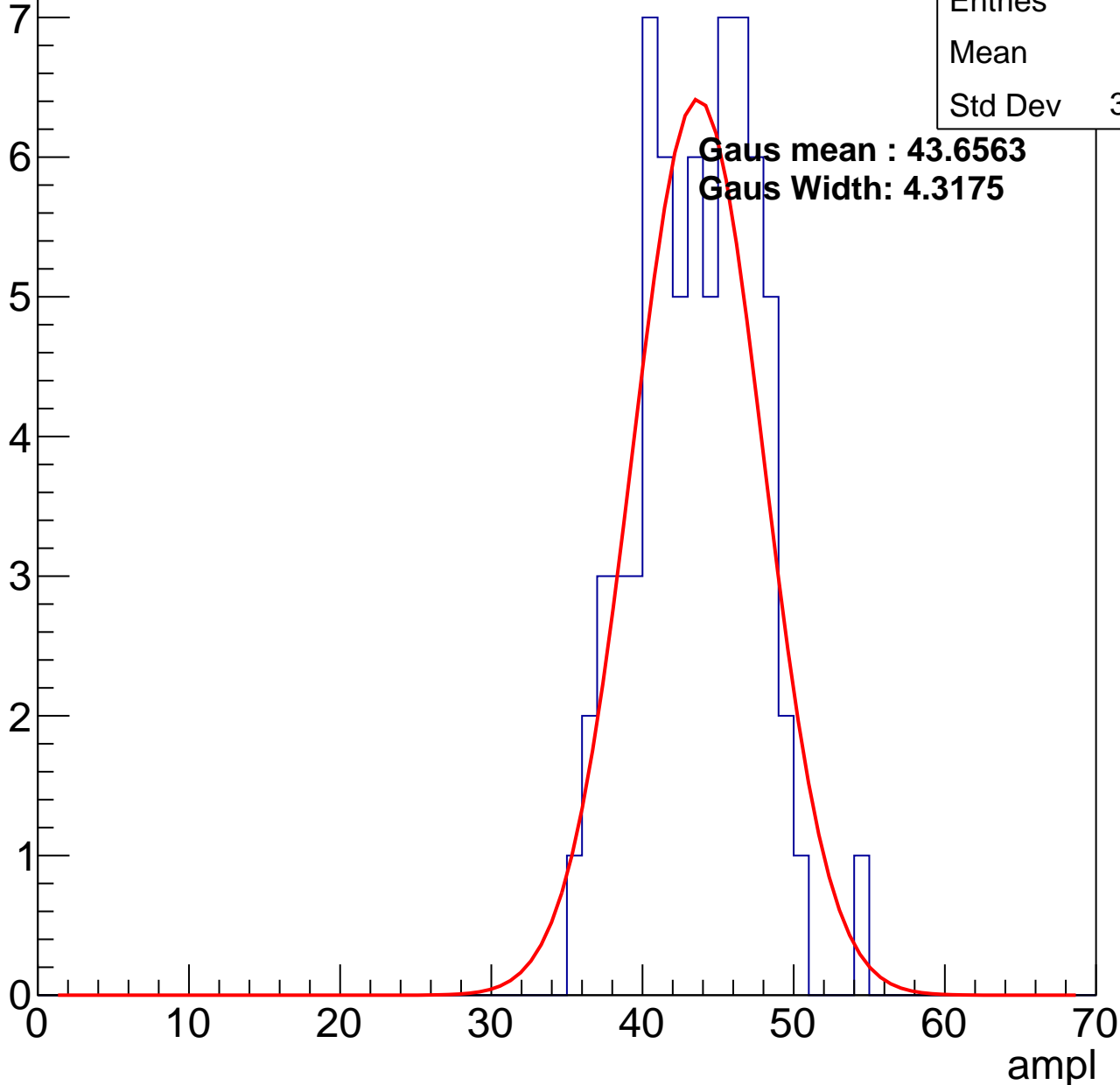
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.2
Std Dev	3.857

Gaus mean : 43.6563

Gaus Width: 4.3175

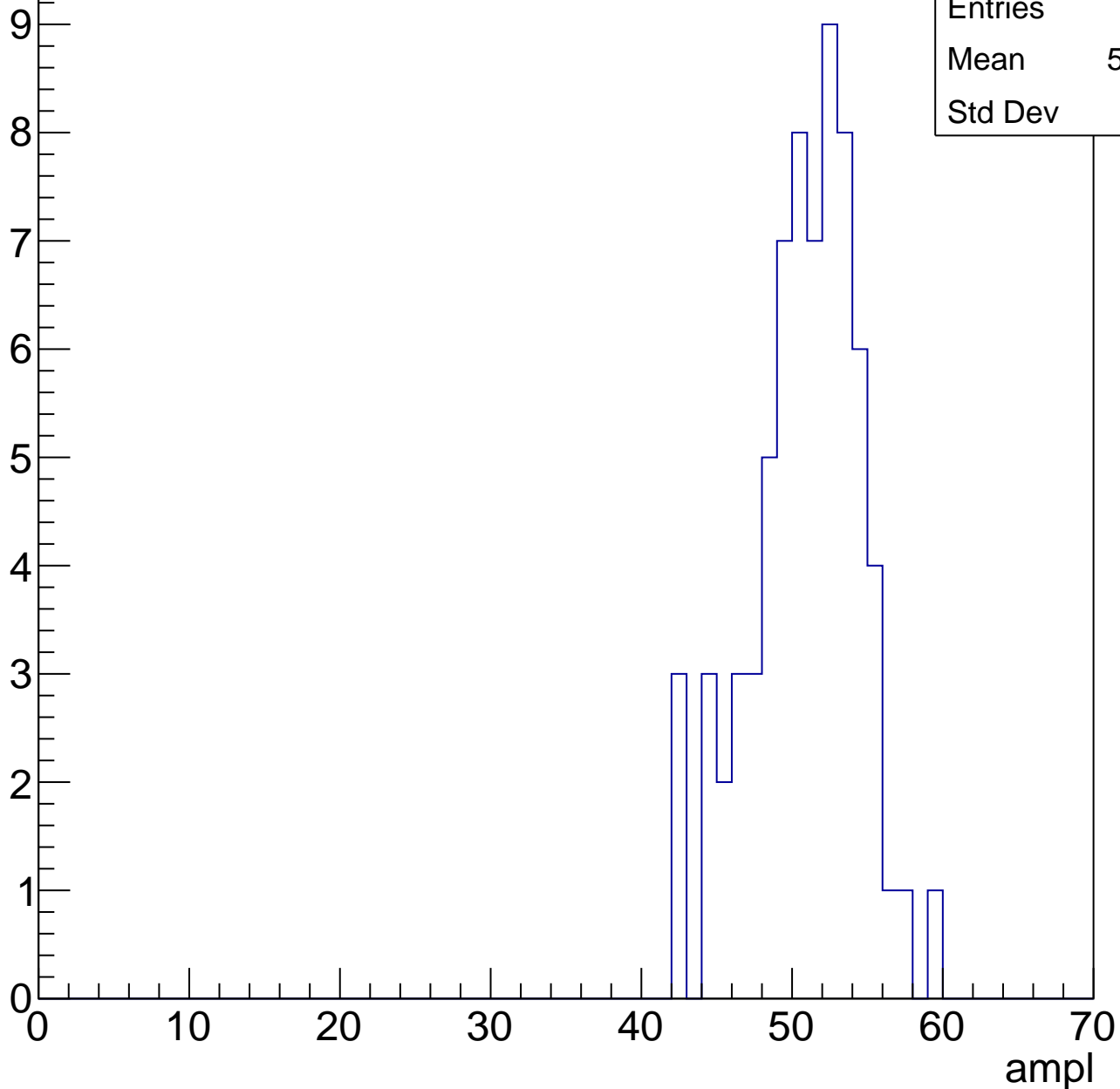


B1L101S, U25-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	50.35
Std Dev	3.6

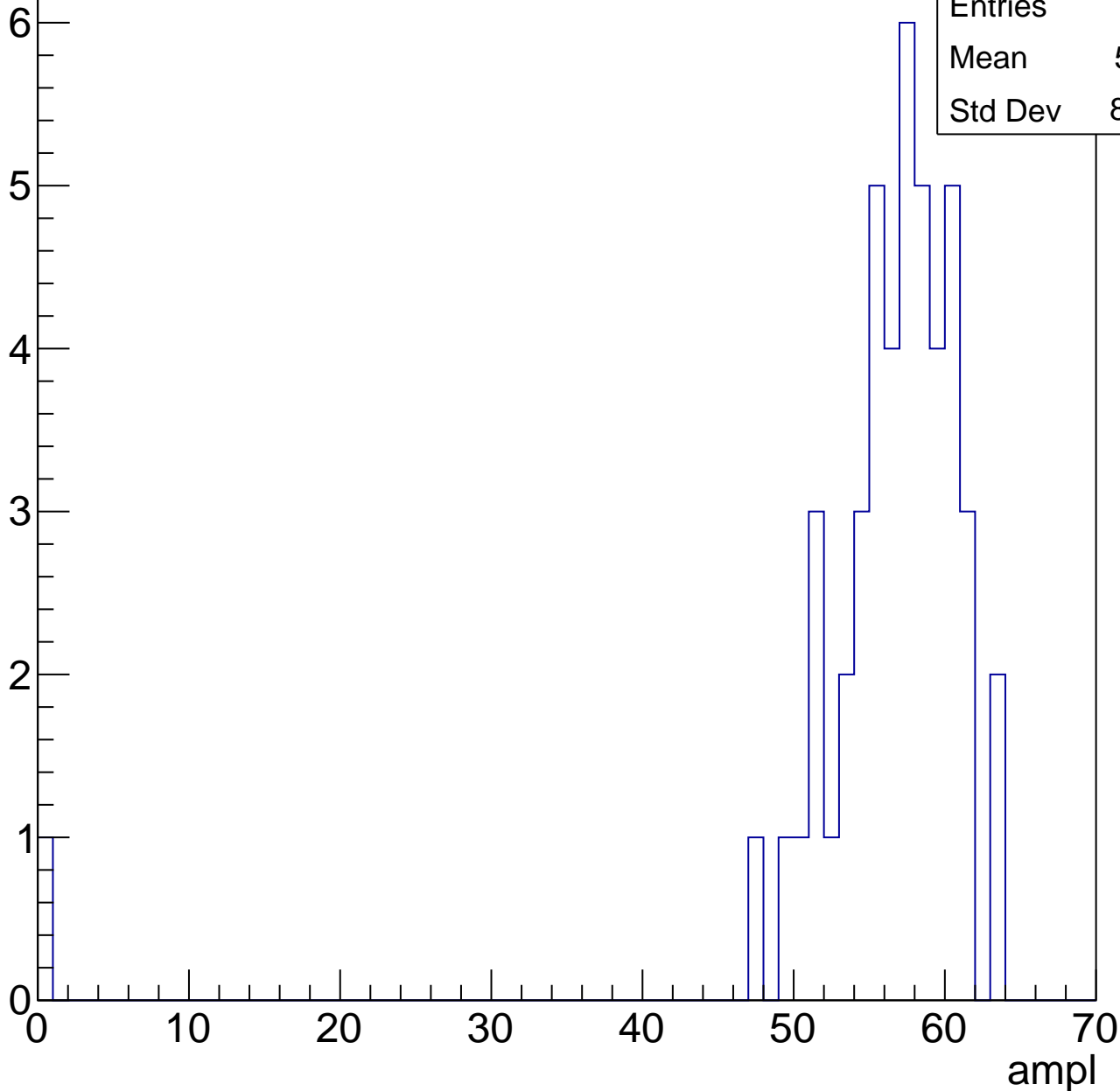


B1L101S, U25-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	55.21
Std Dev	8.889

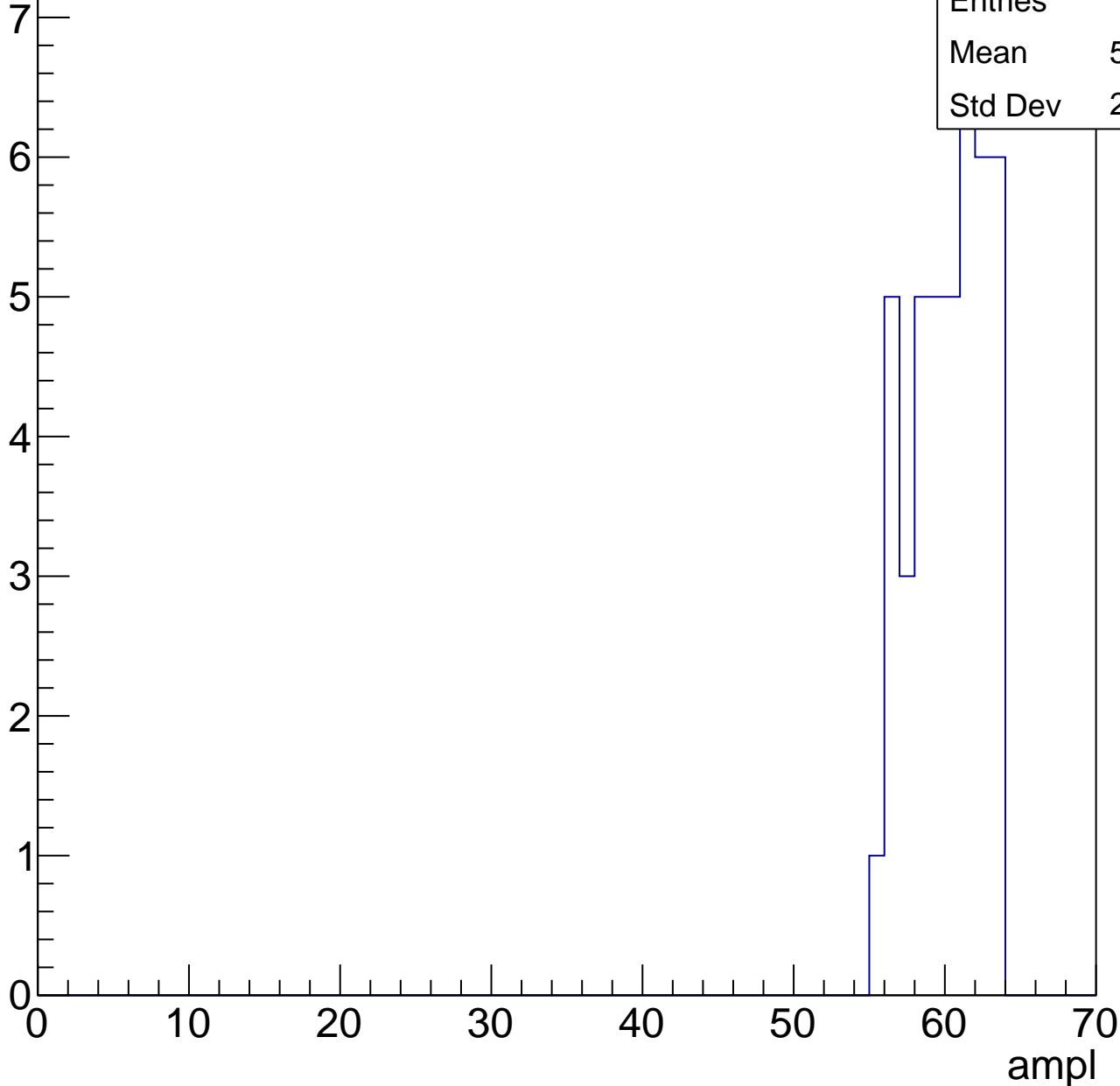


B1L101S, U25-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

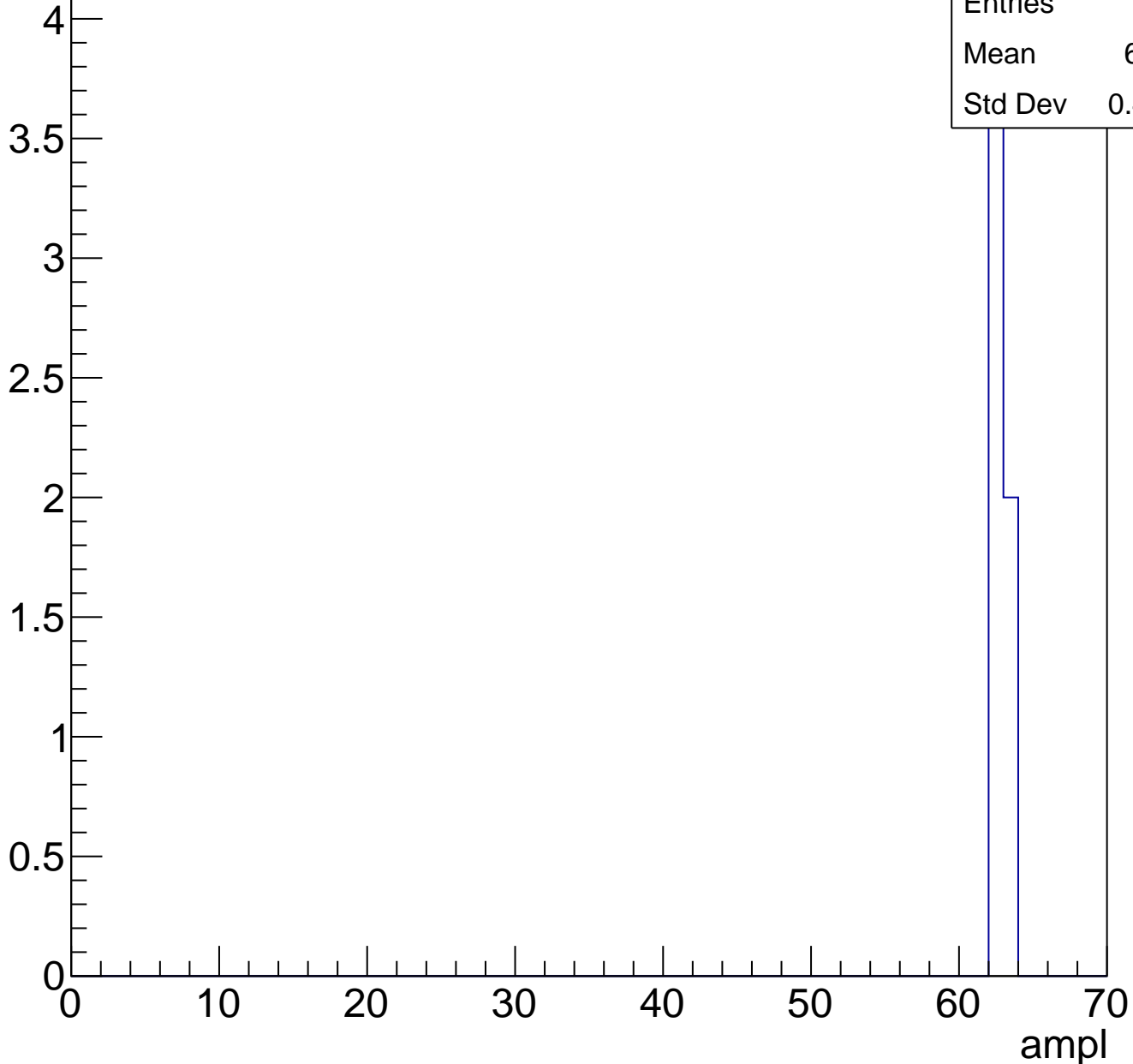
Entries	43
Mean	59.72
Std Dev	2.356



B1L101S, U25-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch105, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	30.09
Std Dev	8.698

Gaus mean : 32.6336

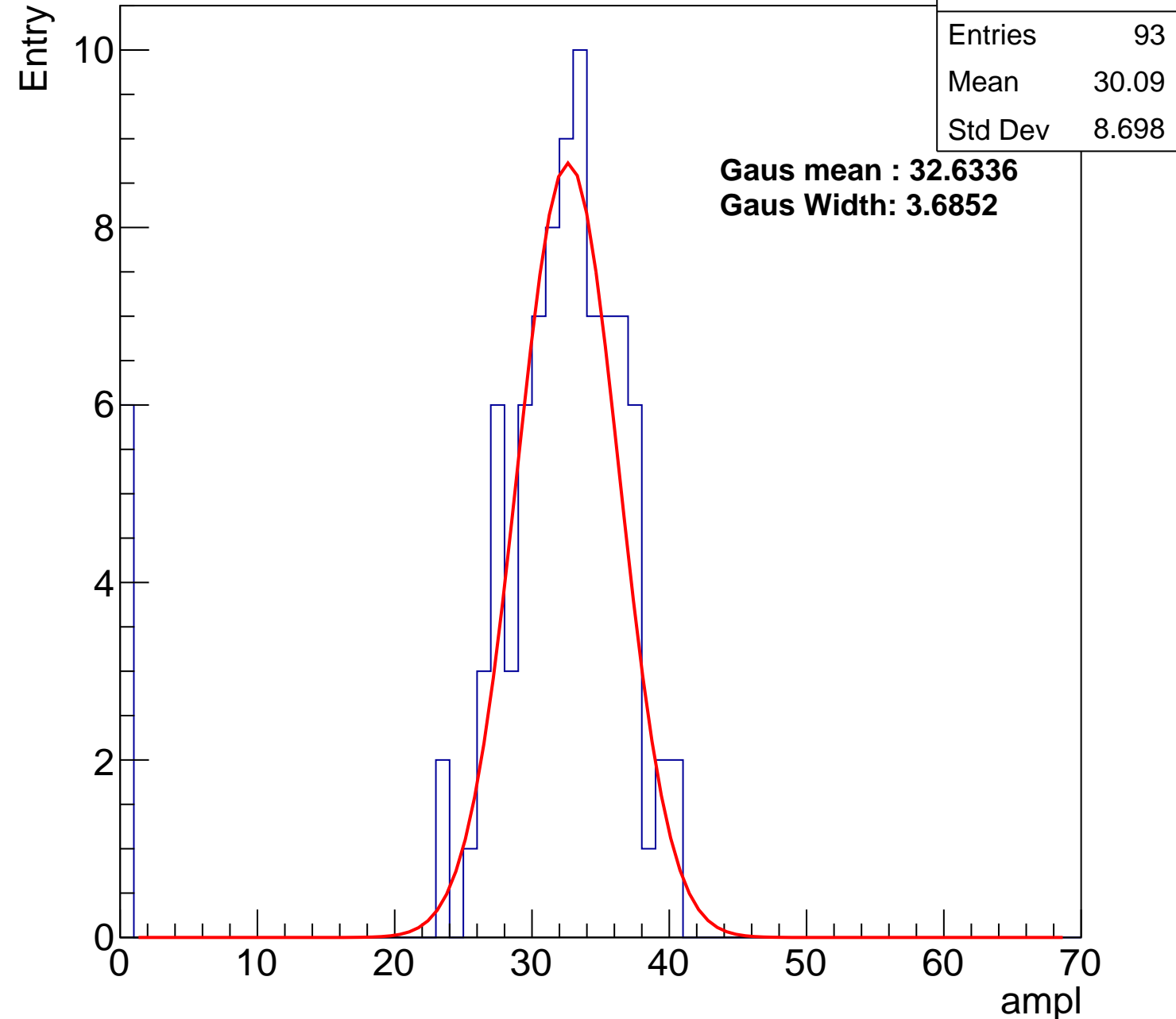
Gaus Width: 3.6852

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch105, adc1

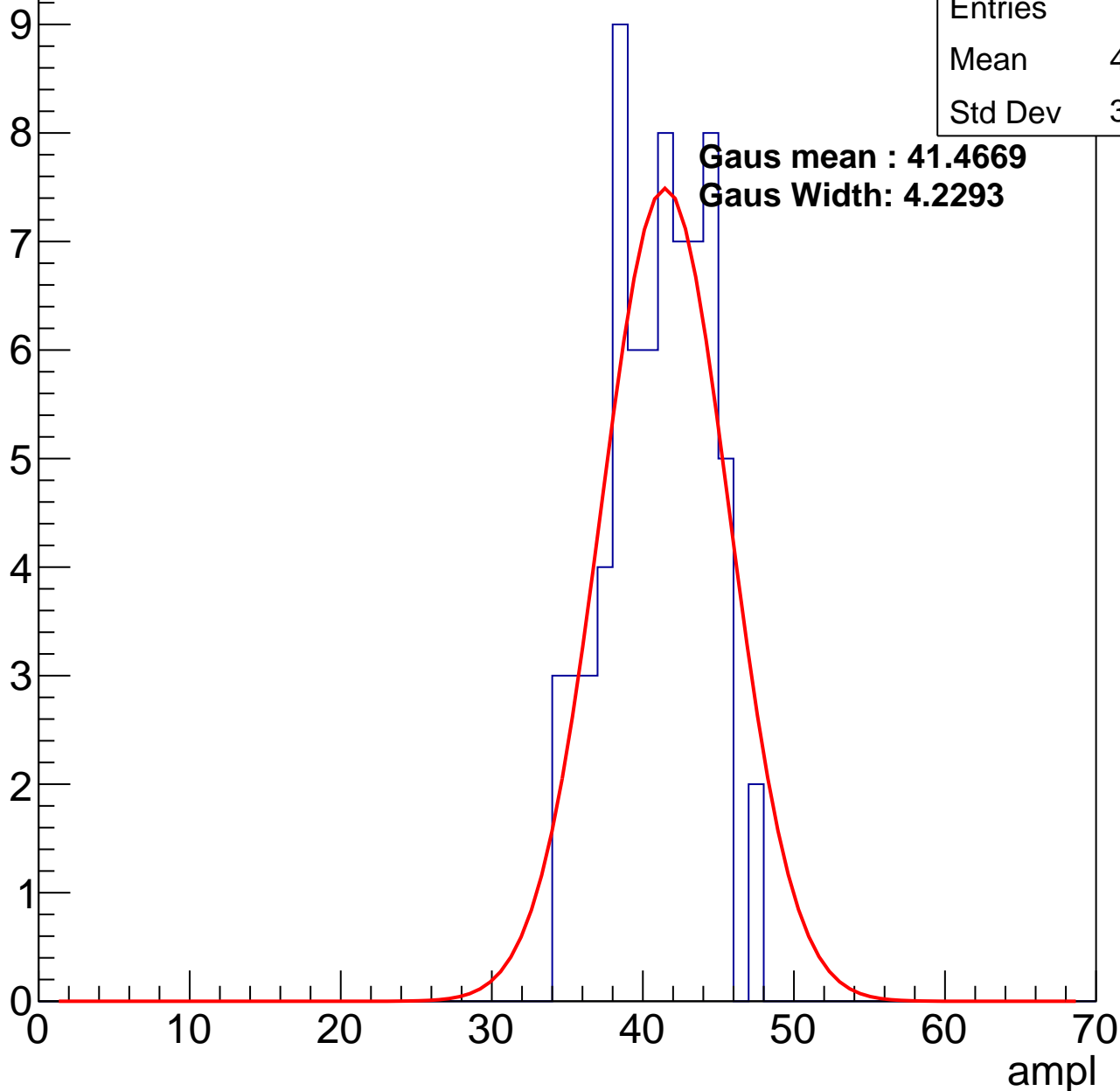
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	40.46
Std Dev	3.236

Gaus mean : 41.4669

Gaus Width: 4.2293



B1L101S, U25-ch105, adc2

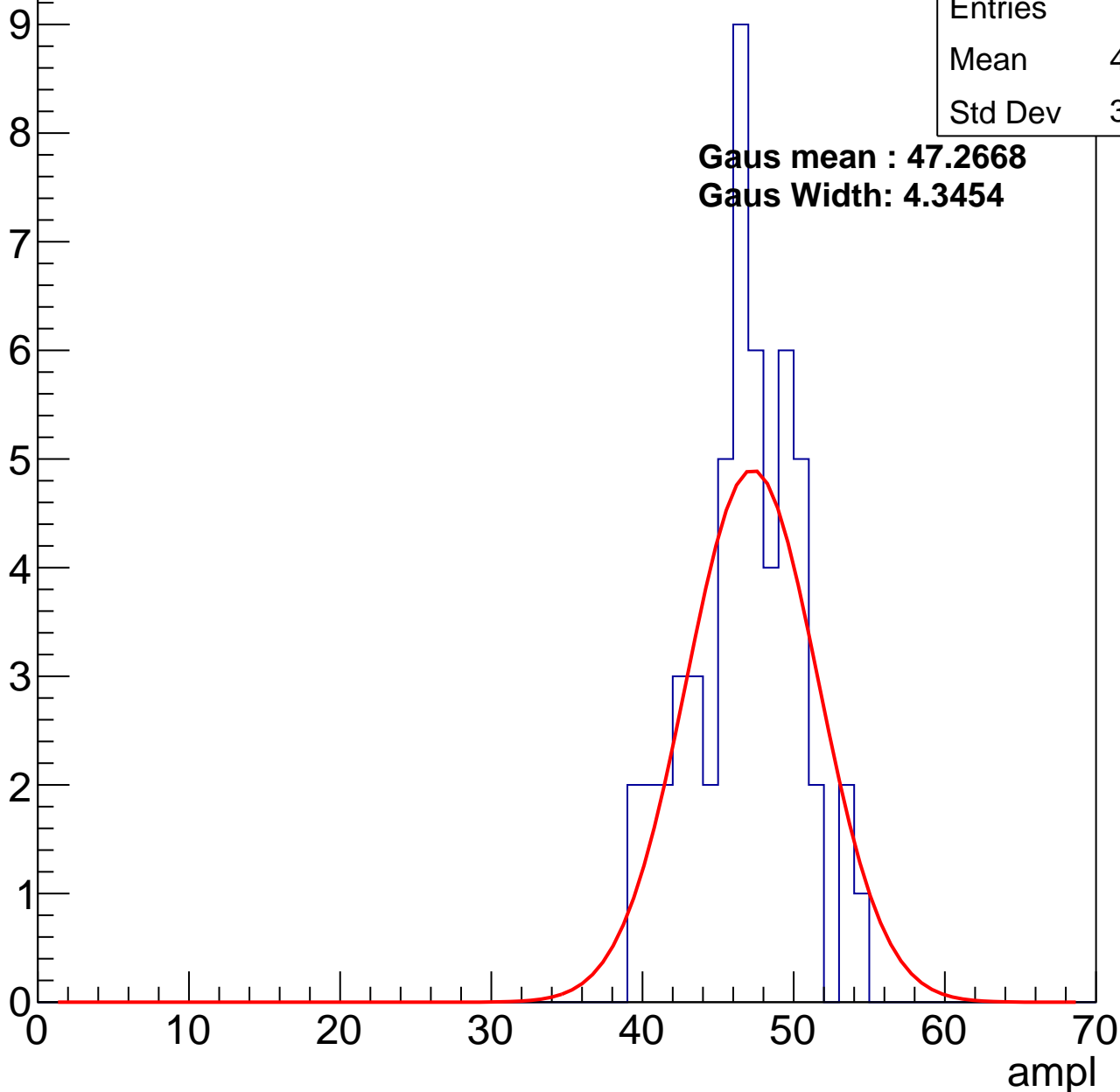
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	46.33
Std Dev	3.522

Gaus mean : 47.2668

Gaus Width: 4.3454

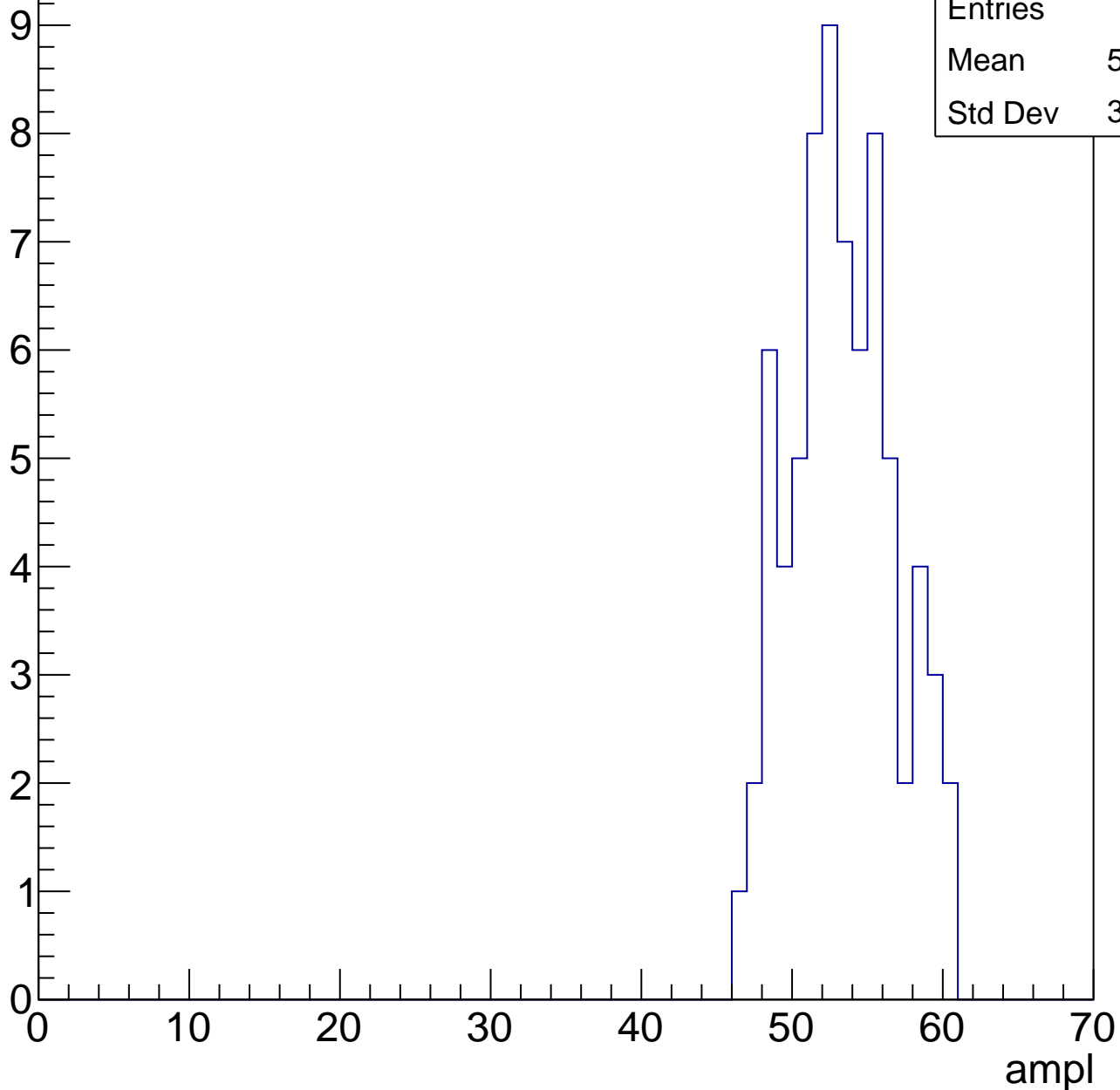


B1L101S, U25-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	52.89
Std Dev	3.418

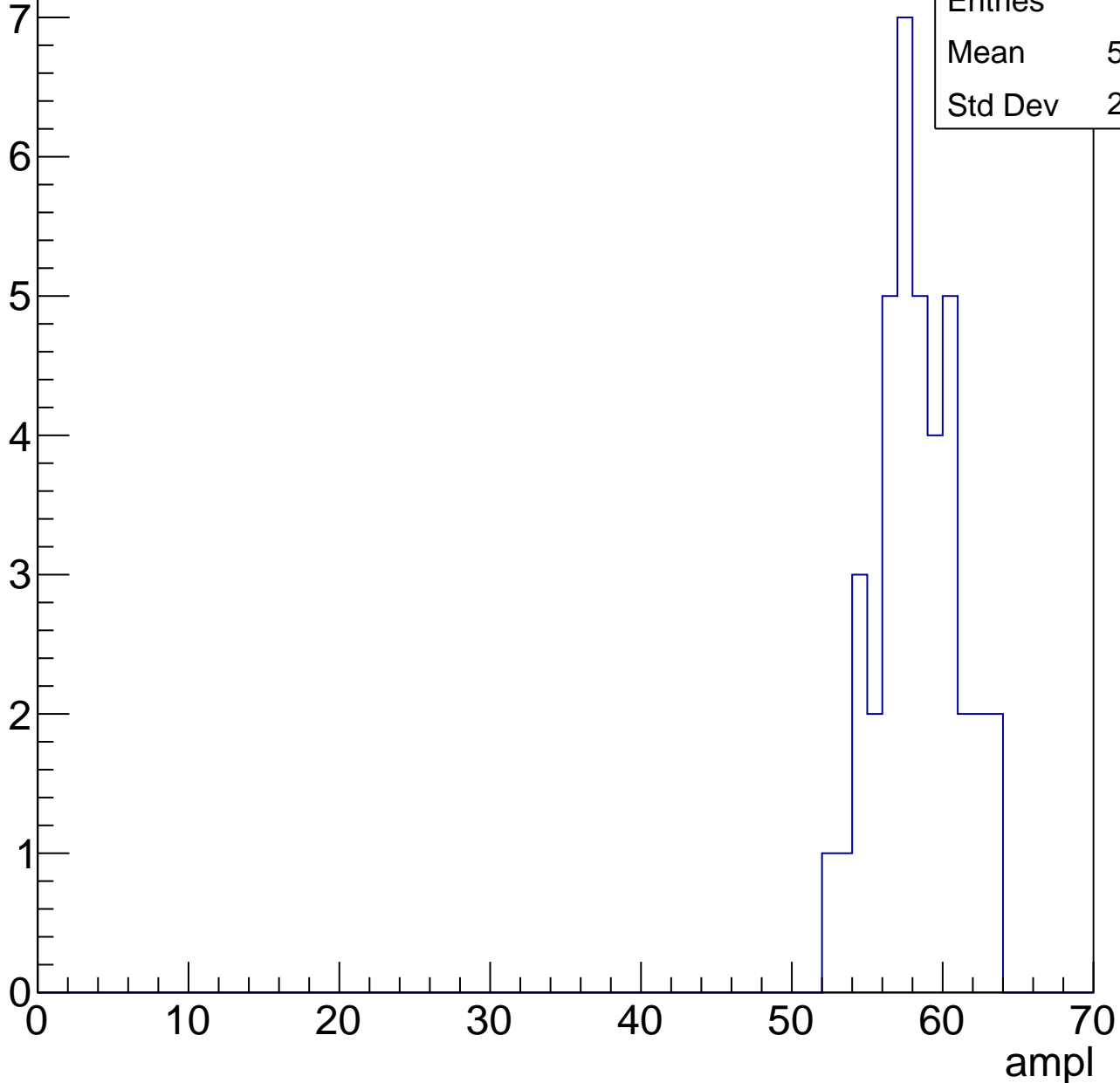


B1L101S, U25-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

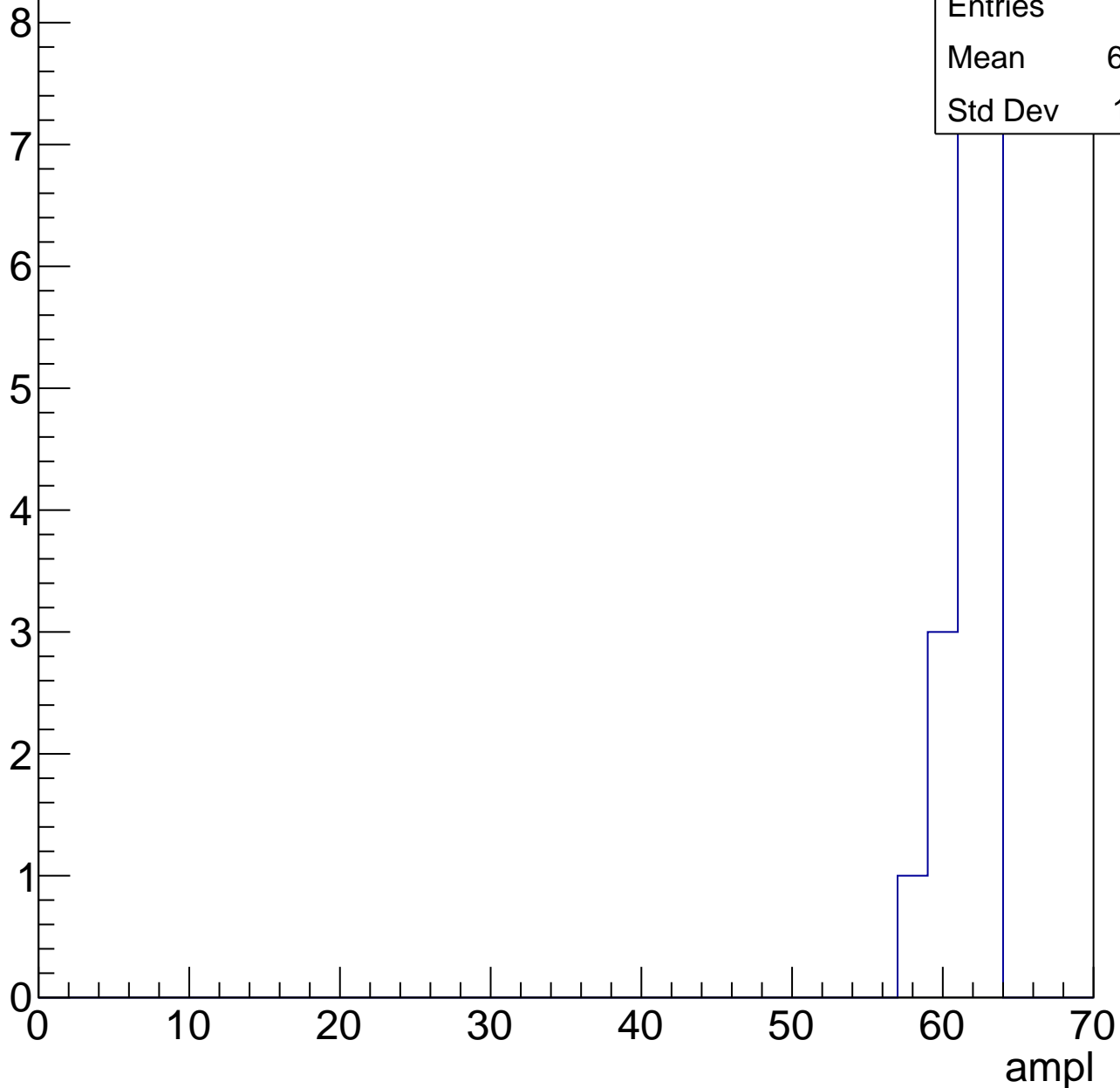
Entries	39
Mean	57.79
Std Dev	2.662



B1L101S, U25-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch106, adc0

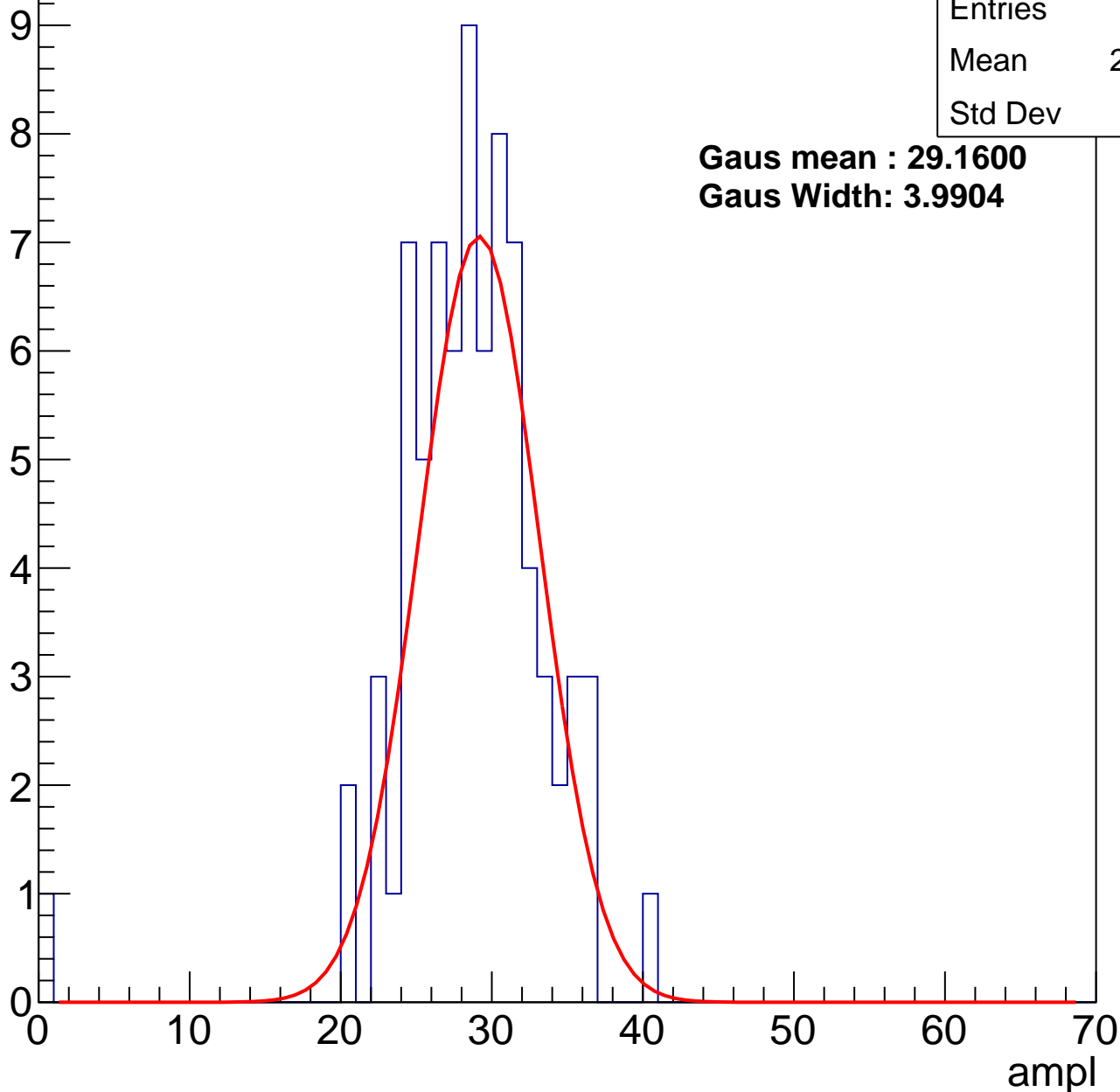
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.17
Std Dev	5.1

Gaus mean : 29.1600

Gaus Width: 3.9904



B1L101S, U25-ch106, adc1

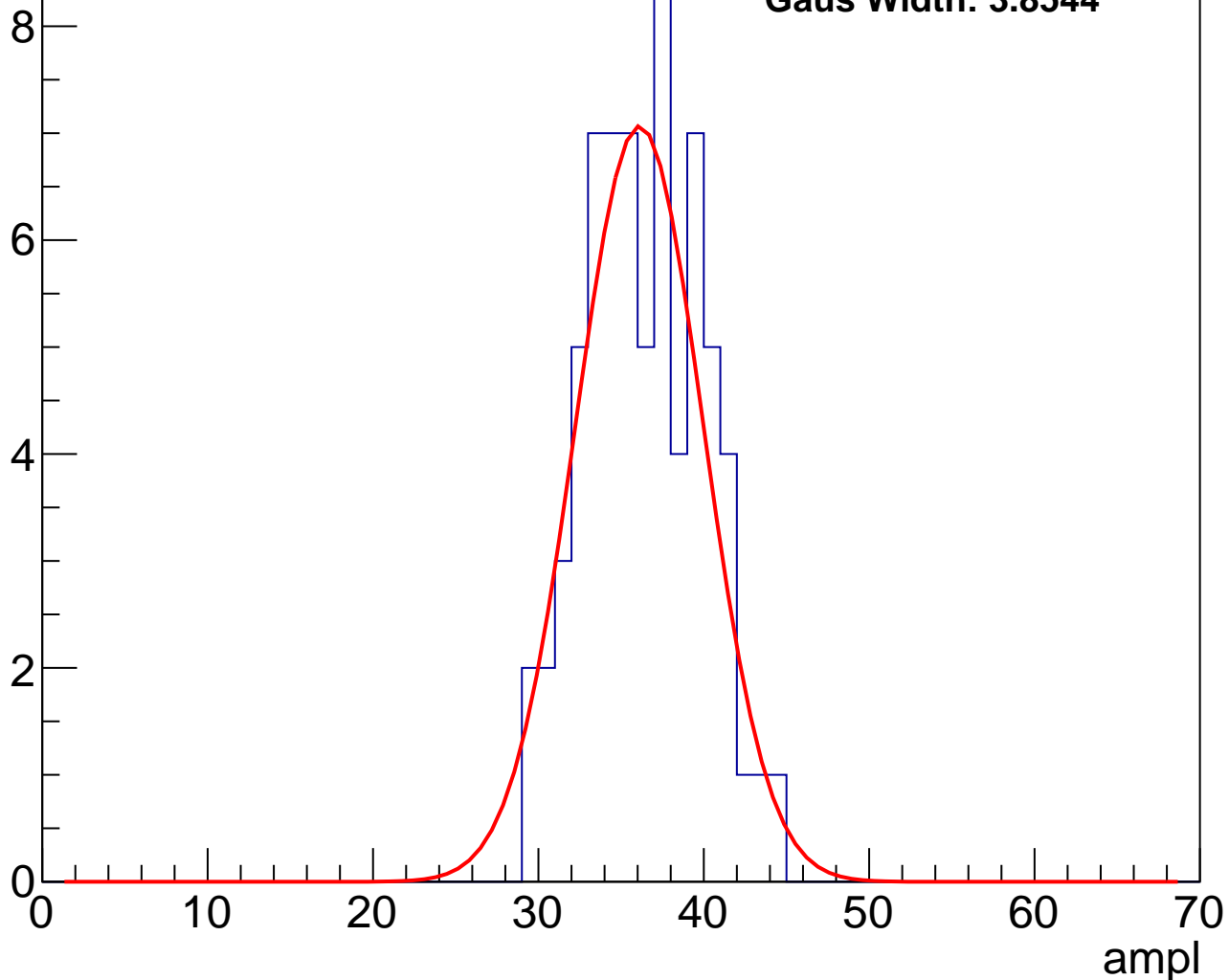
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.96
Std Dev	3.441

Gaus mean : 36.1094

Gaus Width: 3.8544



B1L101S, U25-ch106, adc2

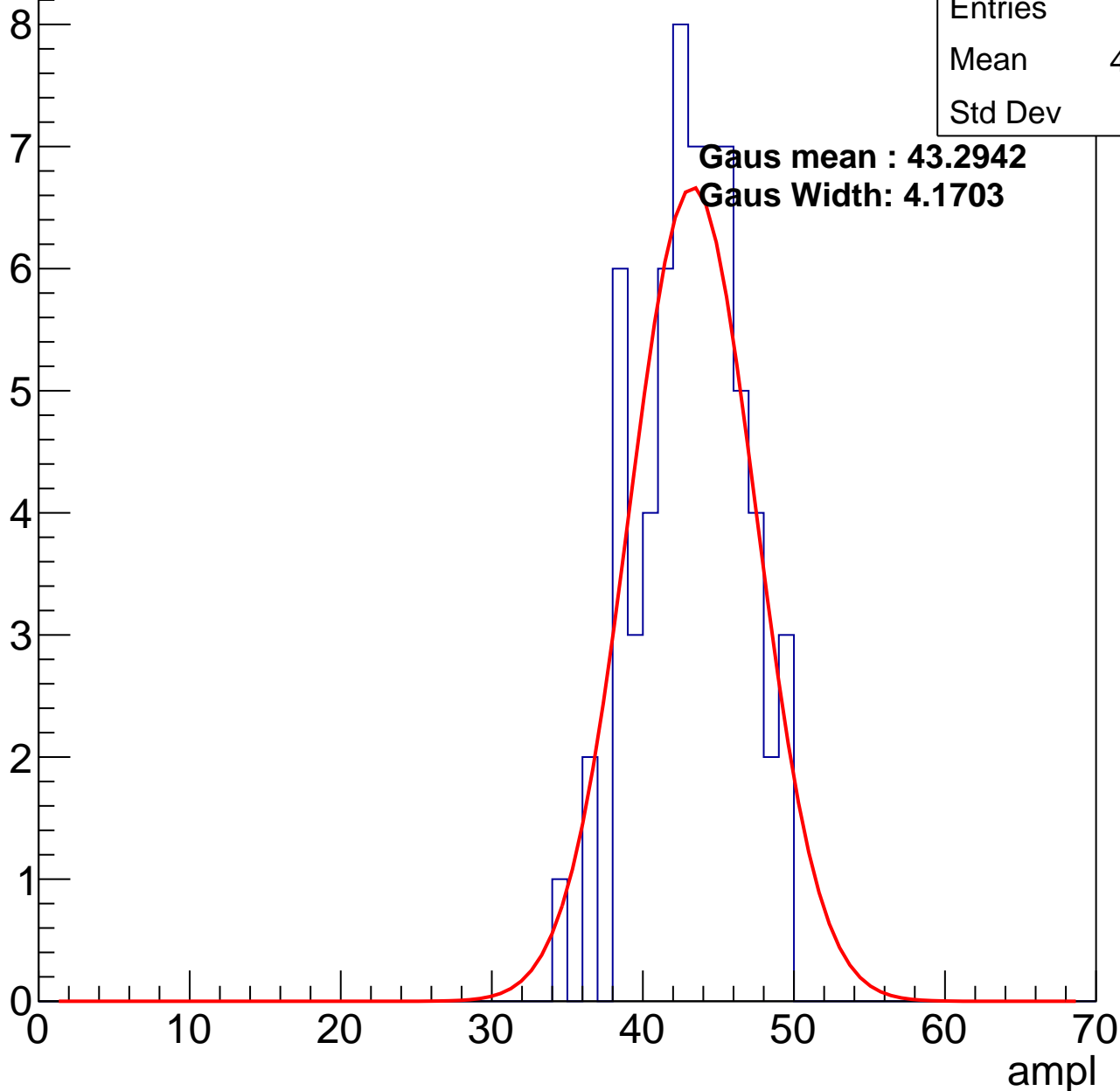
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.74
Std Dev	3.38

Gaus mean : 43.2942

Gaus Width: 4.1703

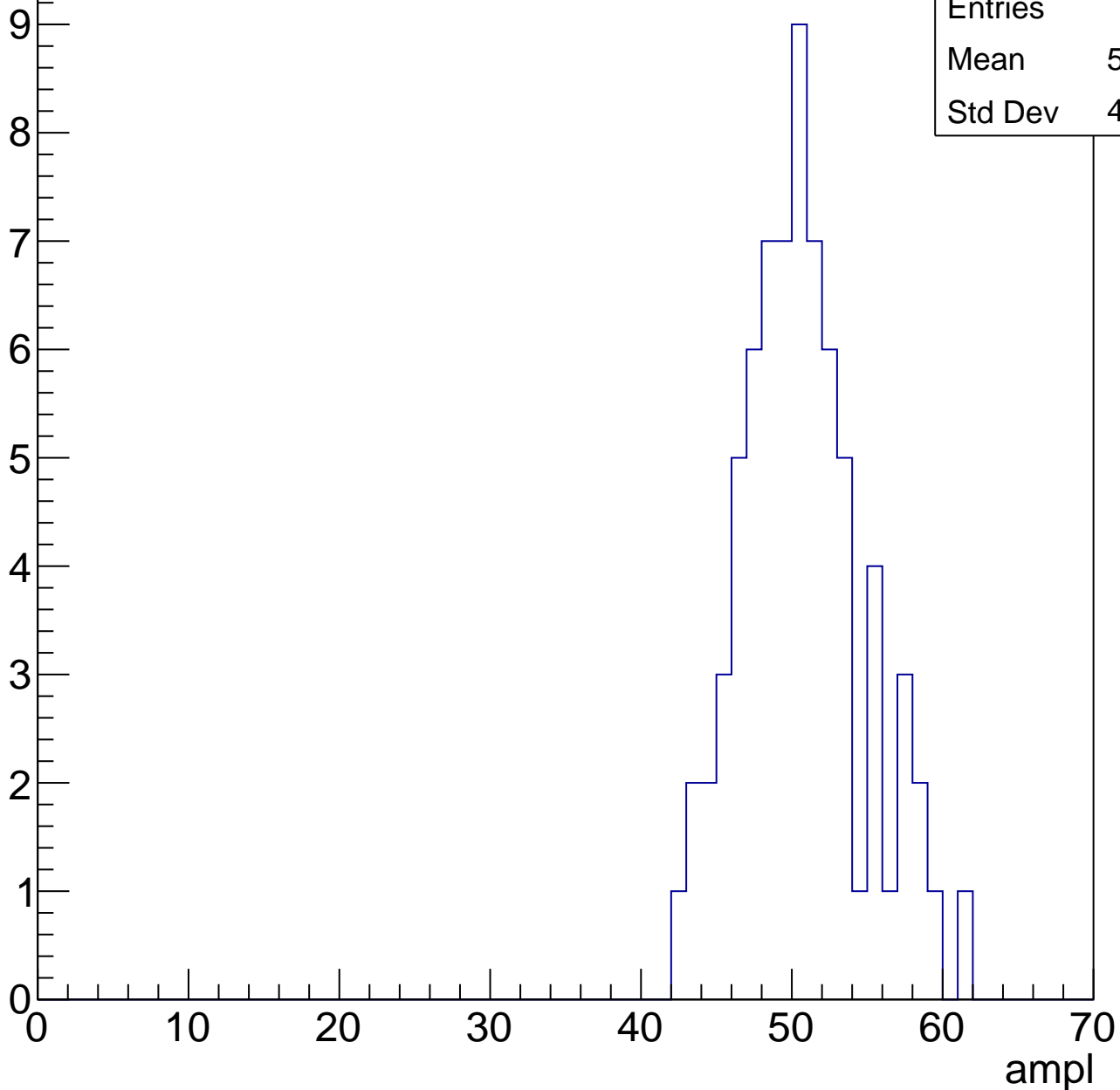


B1L101S, U25-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	50.18
Std Dev	4.022

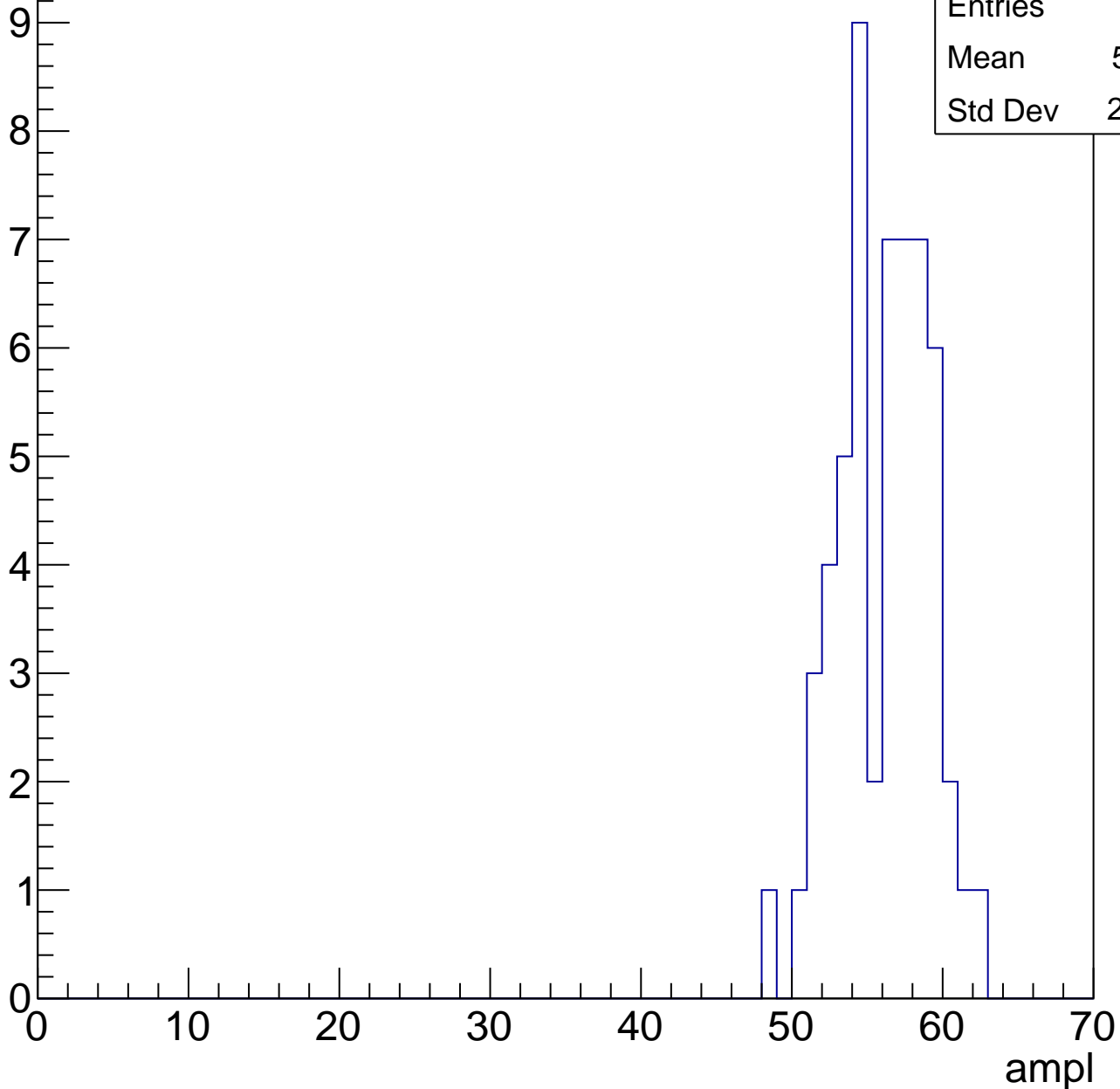


B1L101S, U25-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.61
Std Dev	2.974

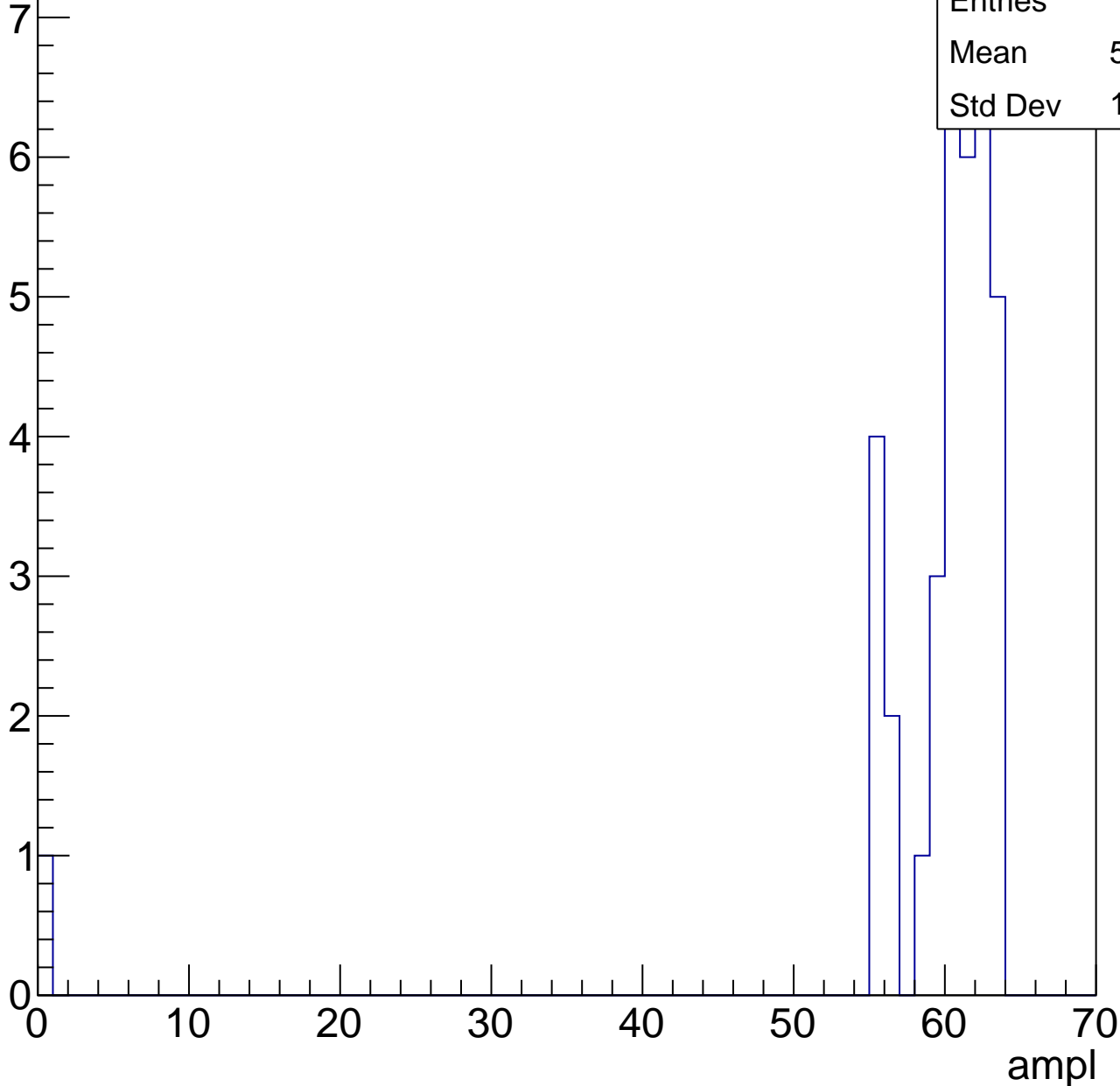


B1L101S, U25-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.39
Std Dev	10.17

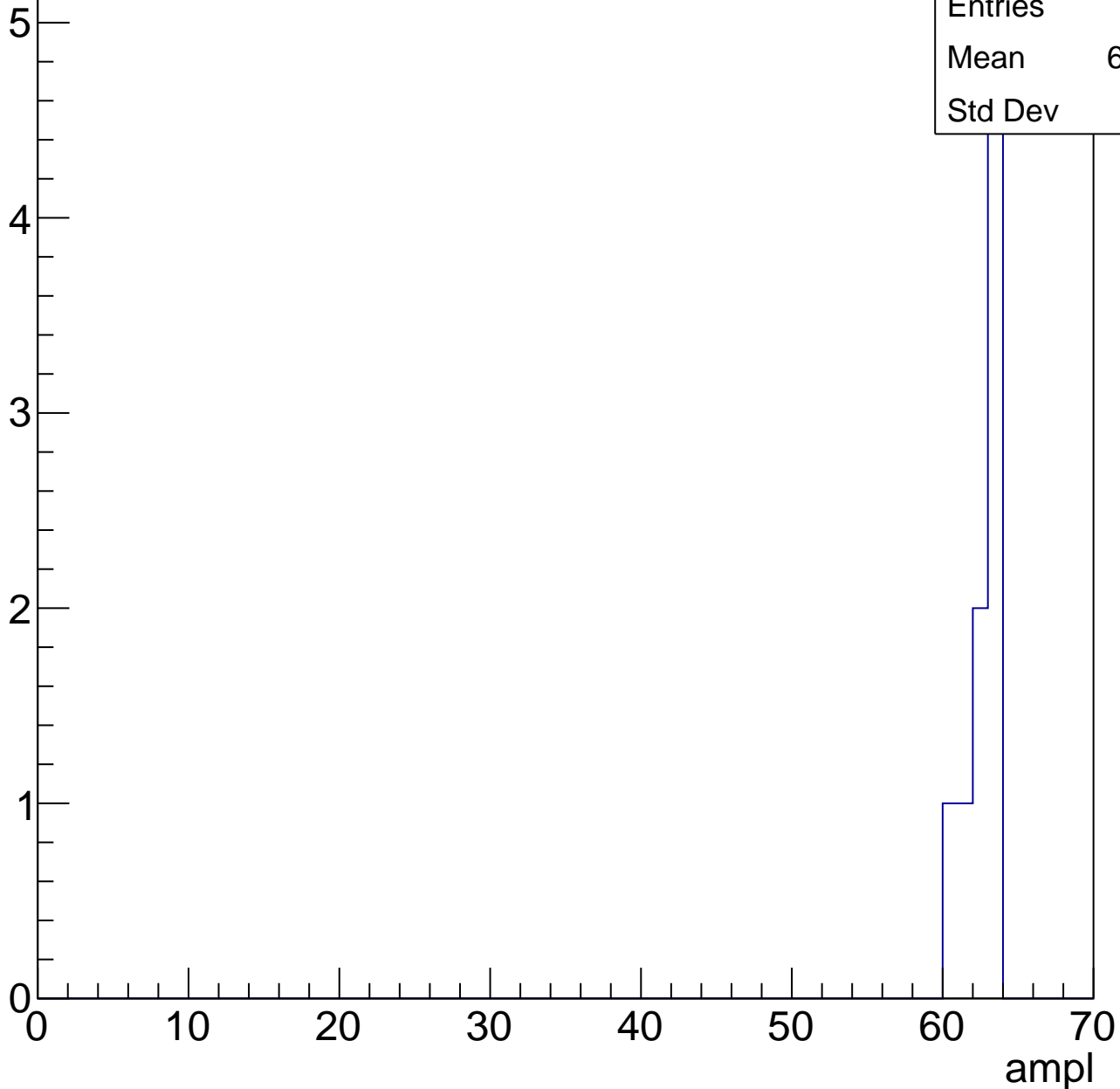


B1L101S, U25-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

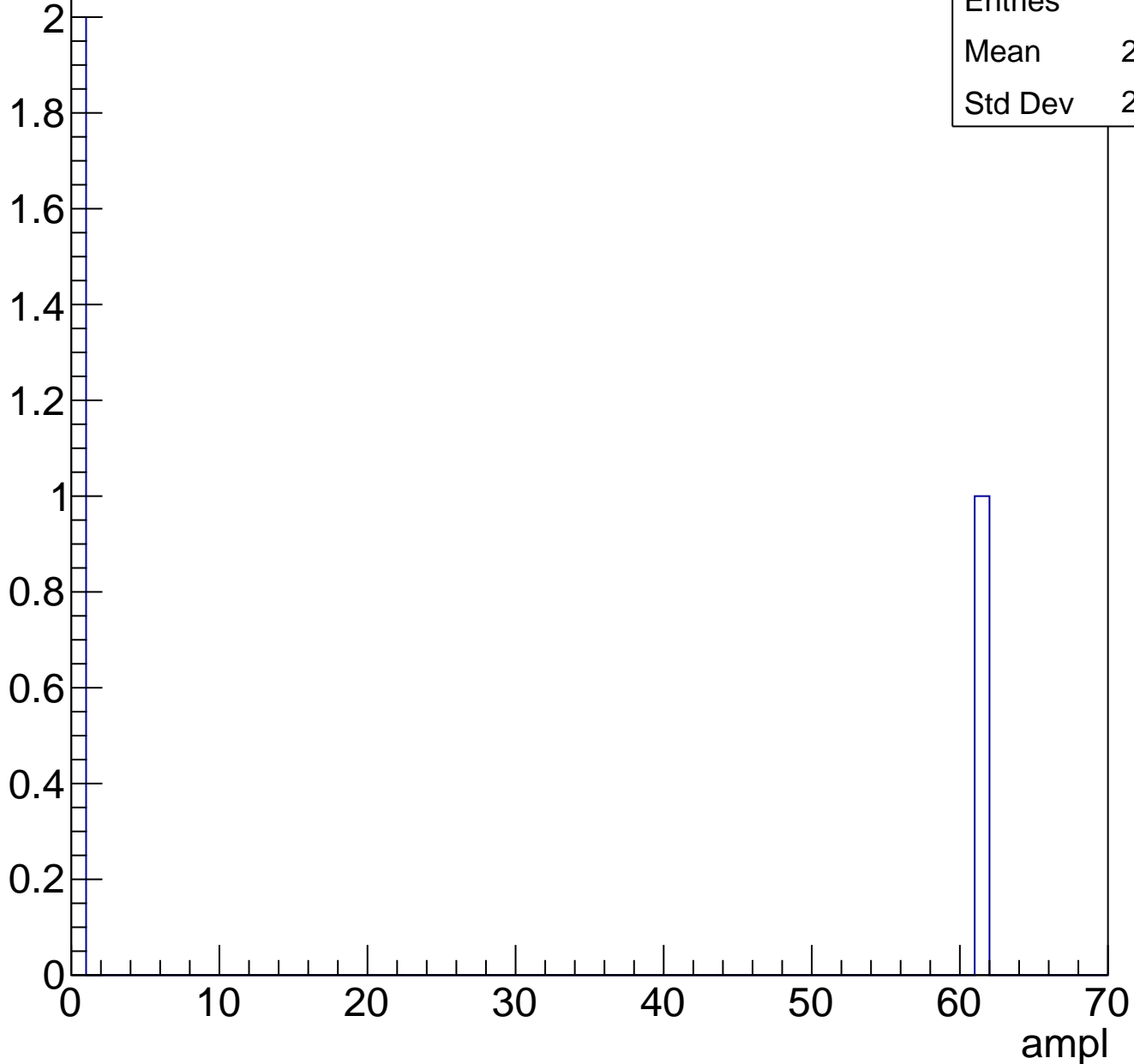
Entries	9
Mean	62.22
Std Dev	1.03



B1L101S, U25-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch107, adc0

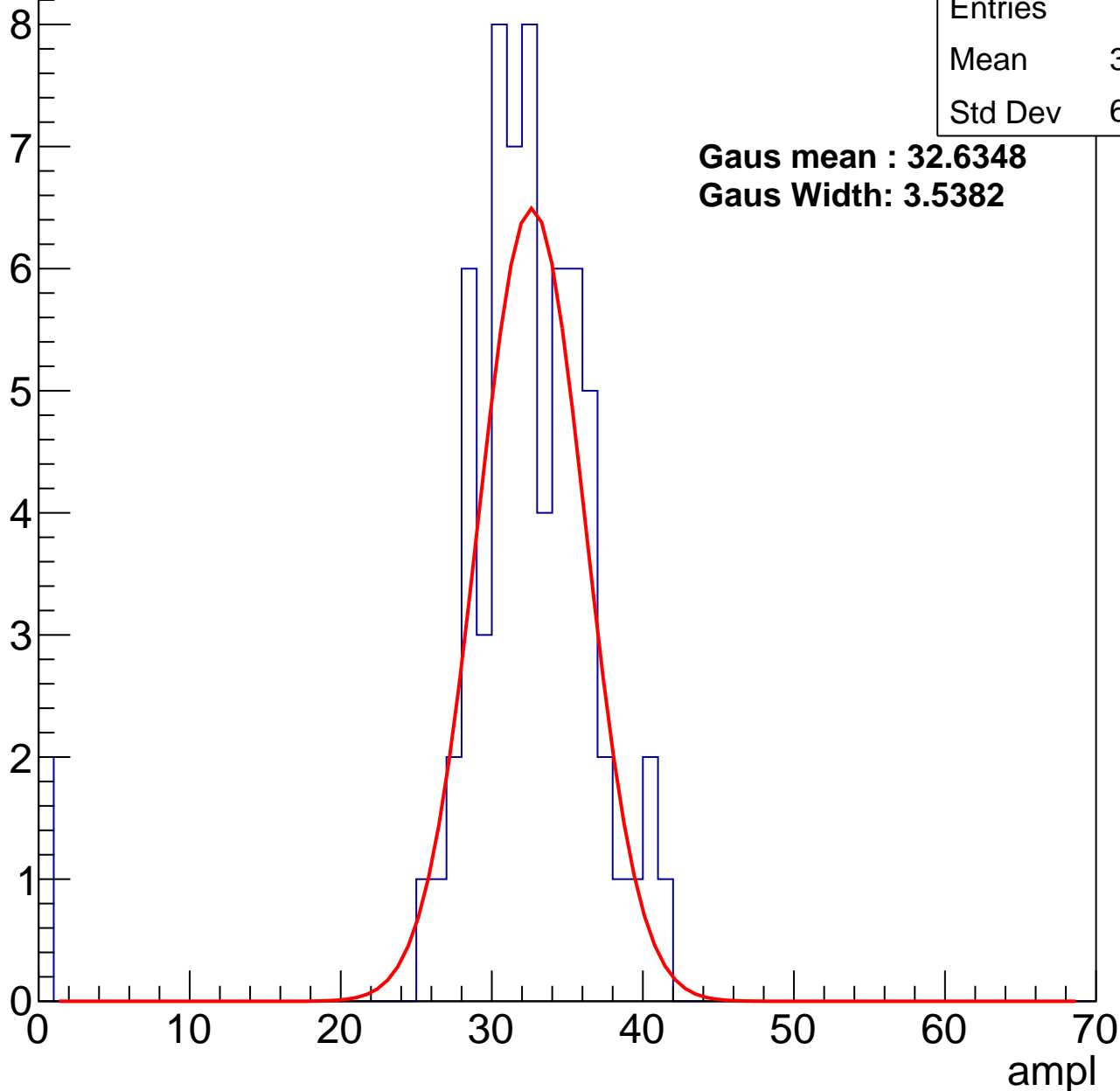
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	31.38
Std Dev	6.543

Gaus mean : 32.6348

Gaus Width: 3.5382



B1L101S, U25-ch107, adc1

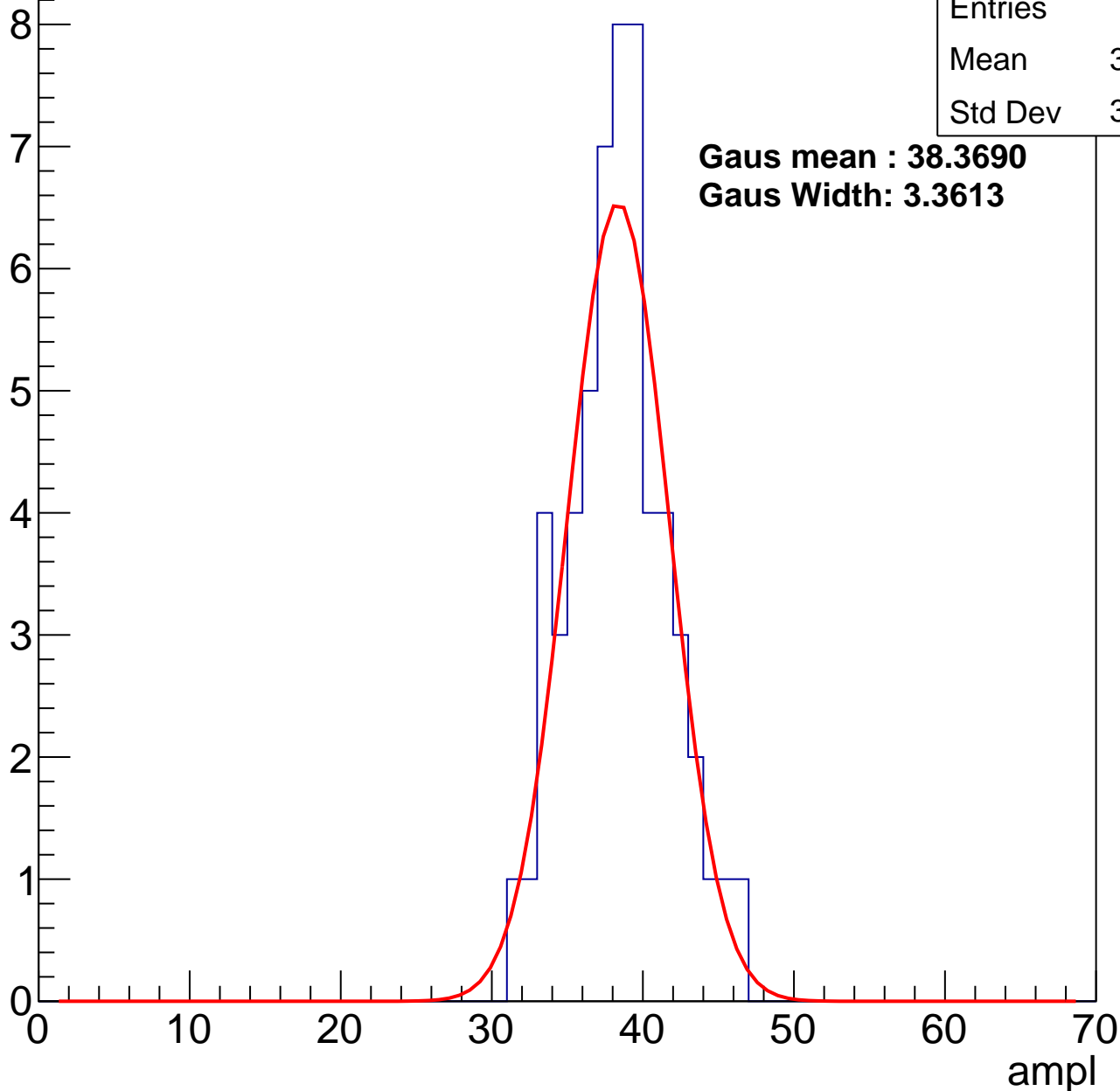
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.95
Std Dev	3.252

Gaus mean : 38.3690

Gaus Width: 3.3613



B1L101S, U25-ch107, adc2

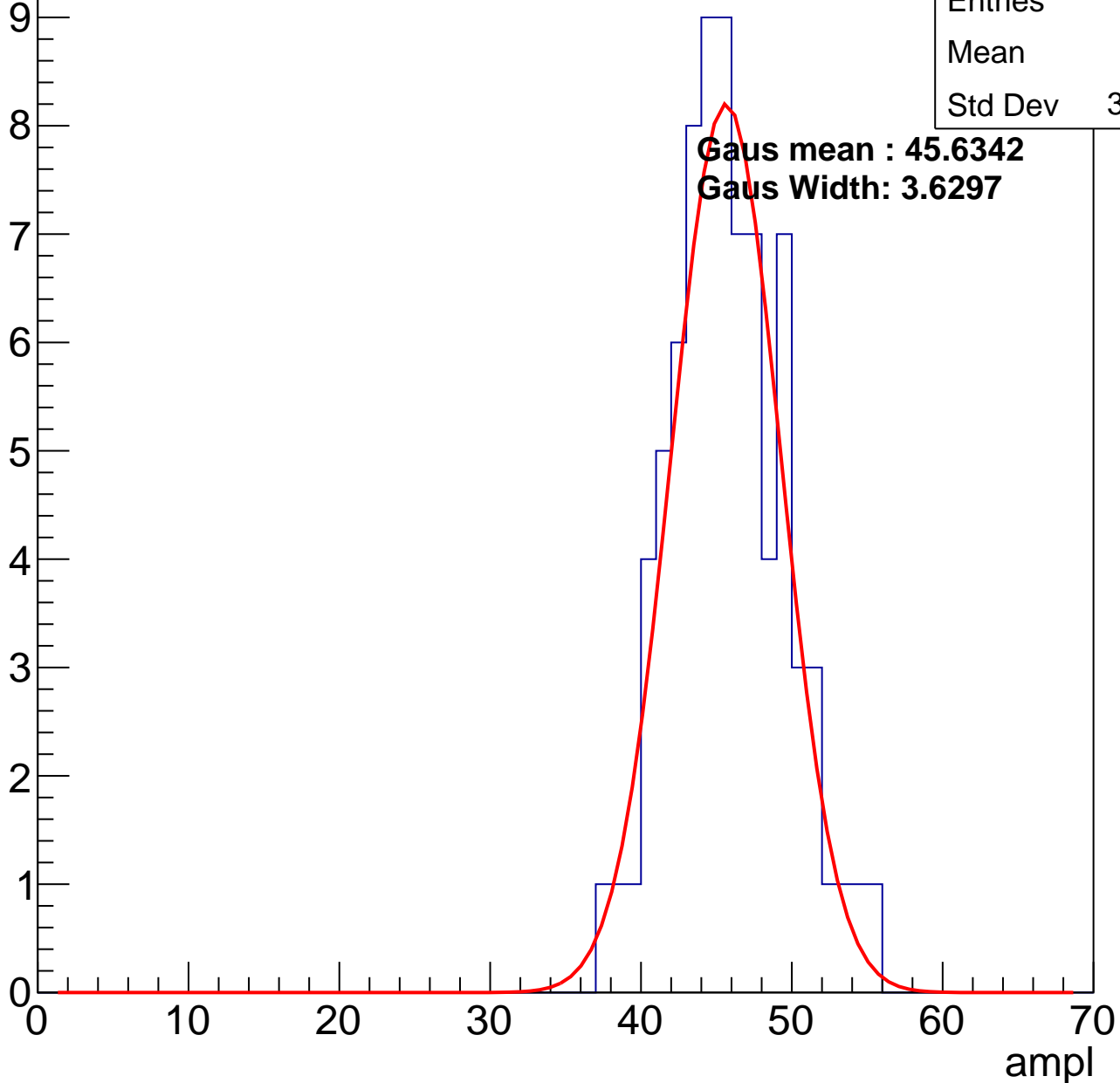
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	45.3
Std Dev	3.692

Gaus mean : 45.6342

Gaus Width: 3.6297

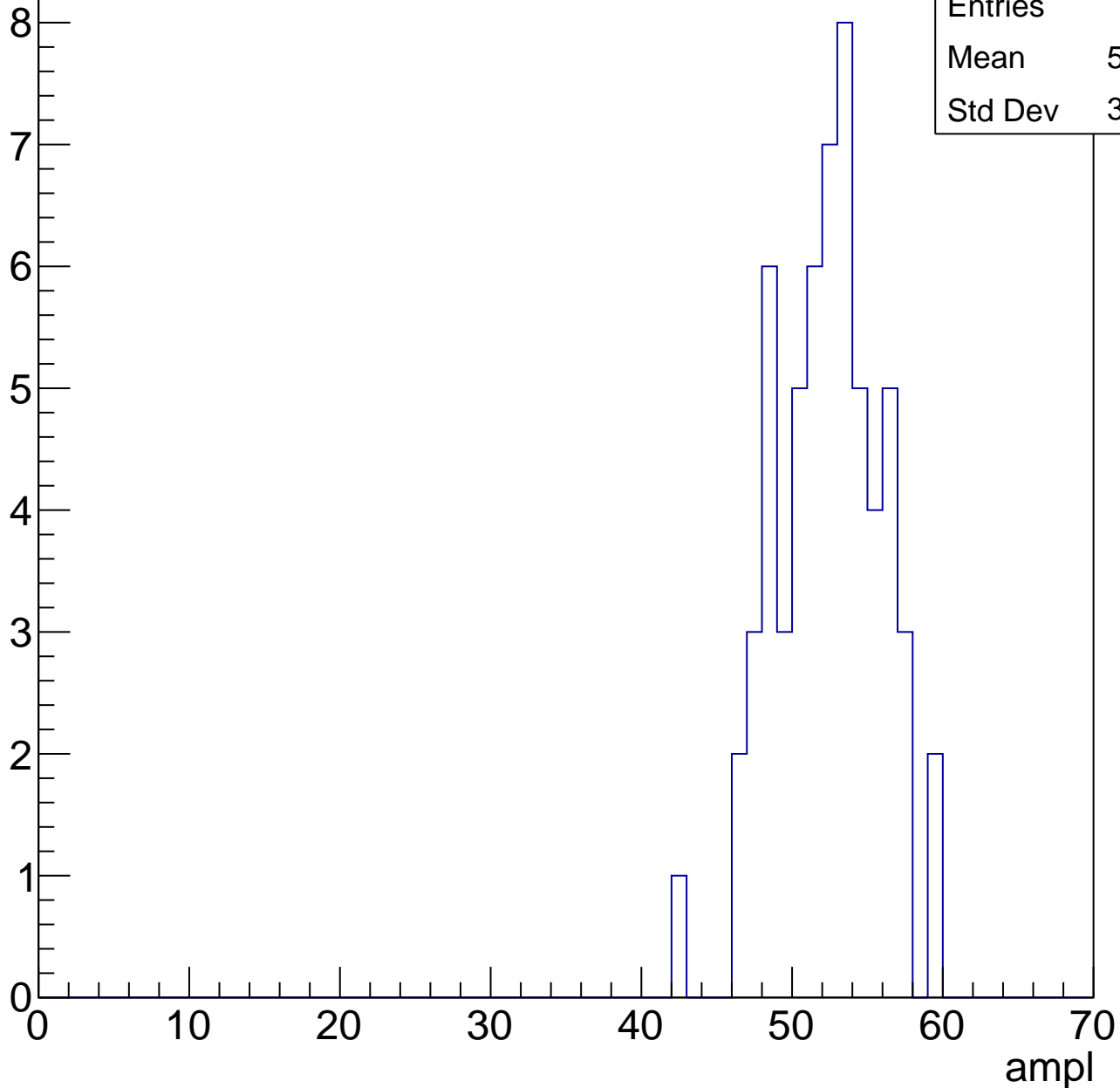


B1L101S, U25-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

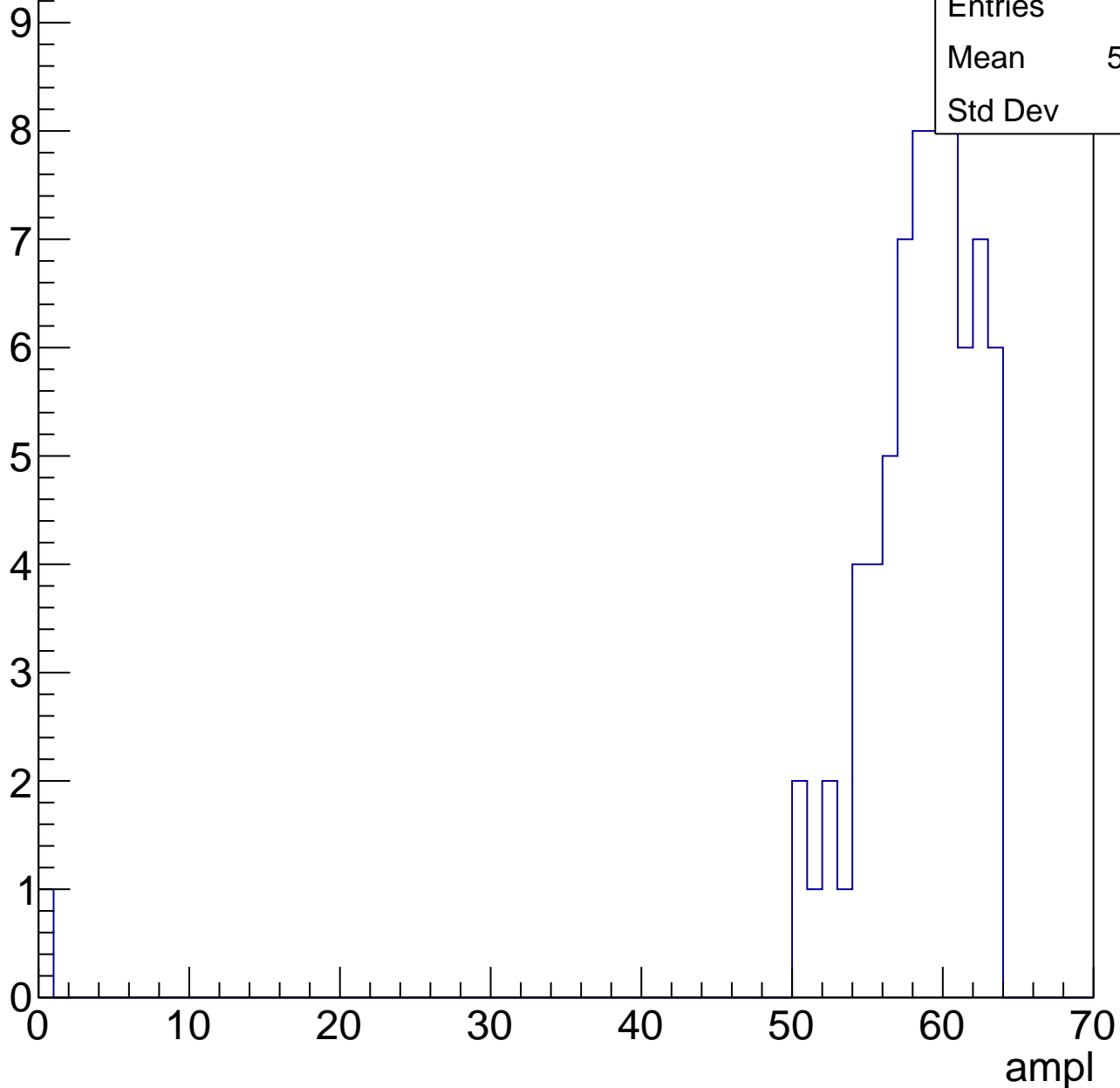
Entries	60
Mean	51.88
Std Dev	3.445



B1L101S, U25-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

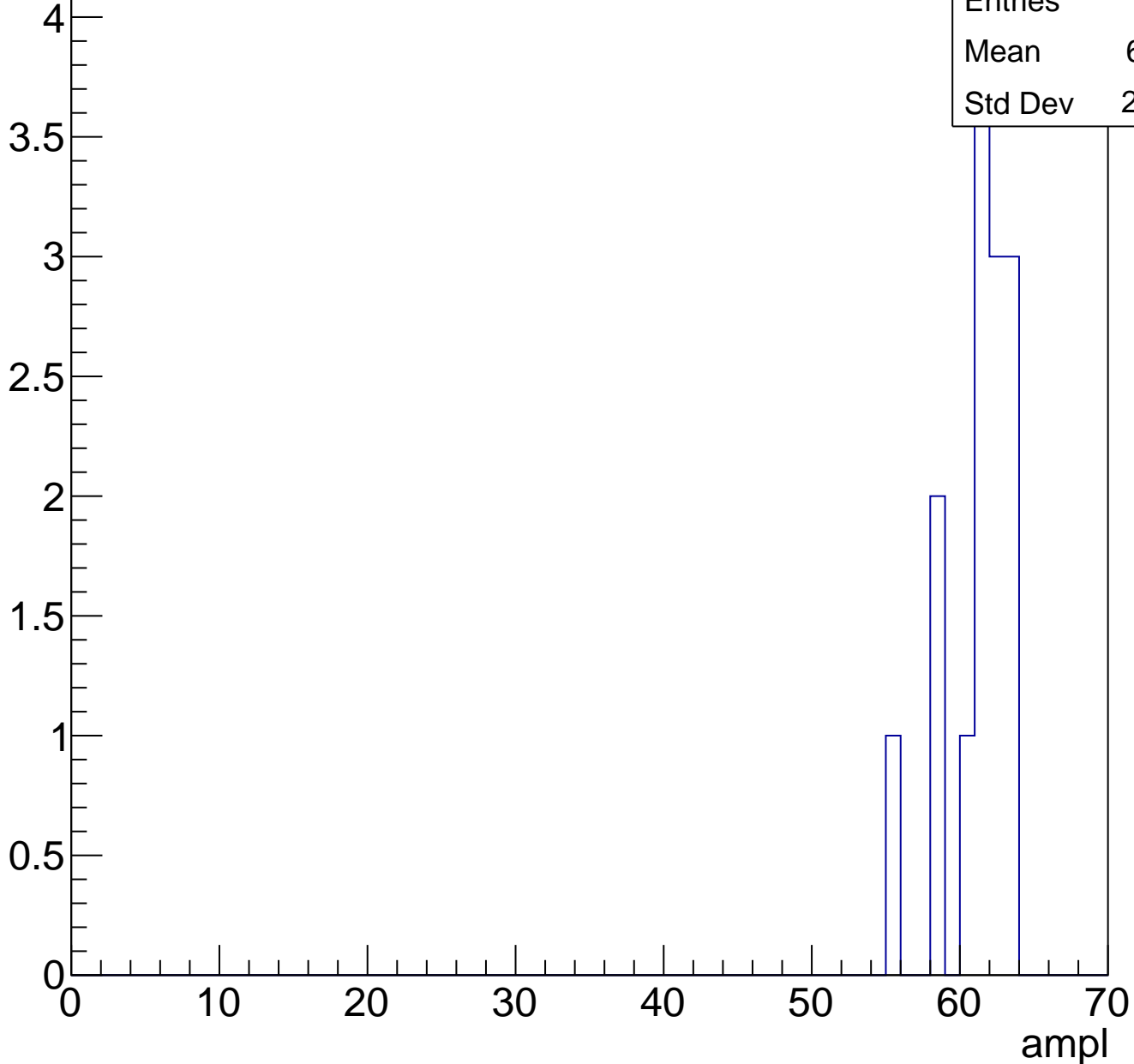
Entry



B1L101S, U25-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	14
Mean	60.71
Std Dev	2.218

B1L101S, U25-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U25-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch108, adc0

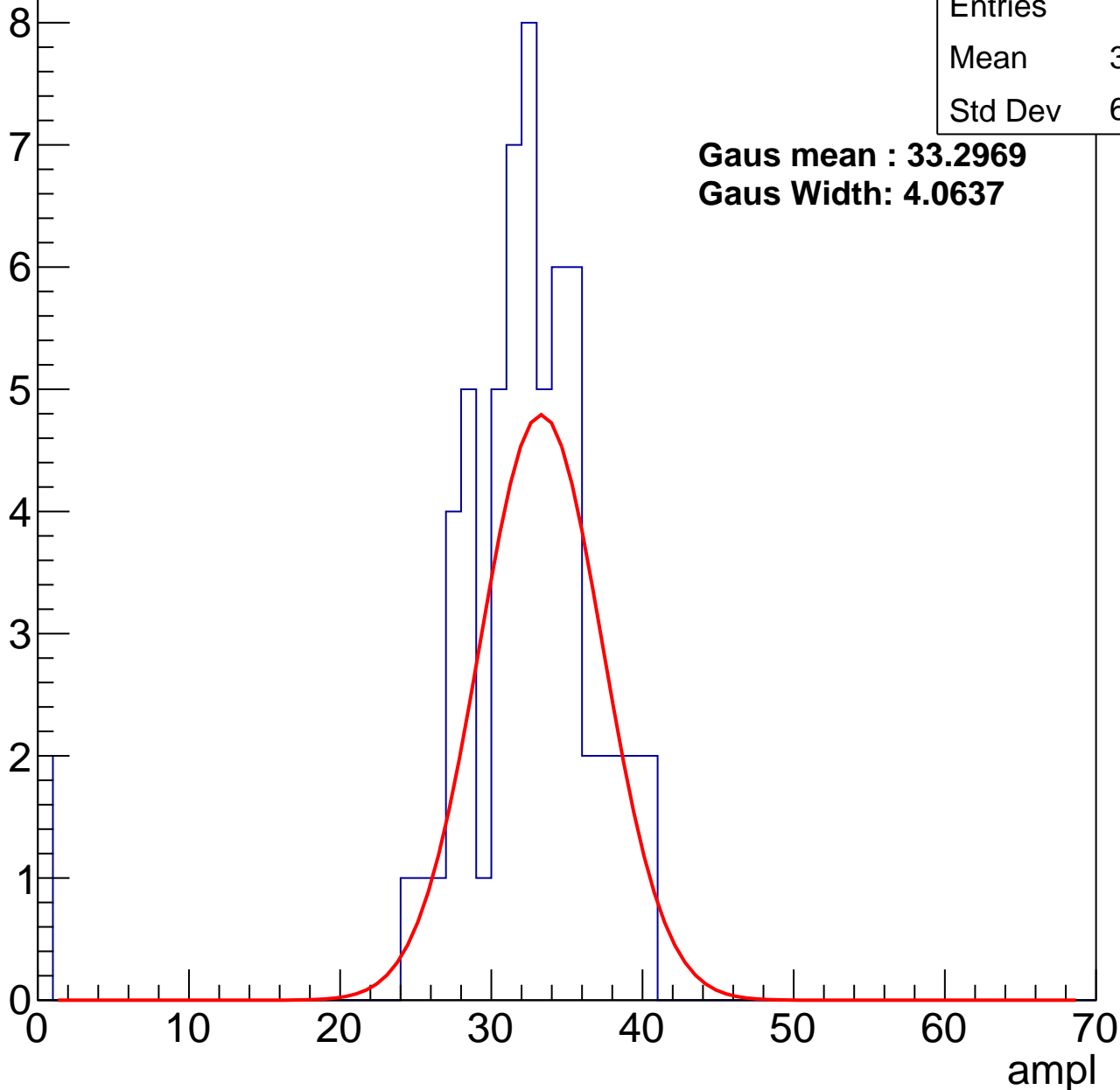
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	31.19
Std Dev	6.763

Gaus mean : 33.2969

Gaus Width: 4.0637



B1L101S, U25-ch108, adc1

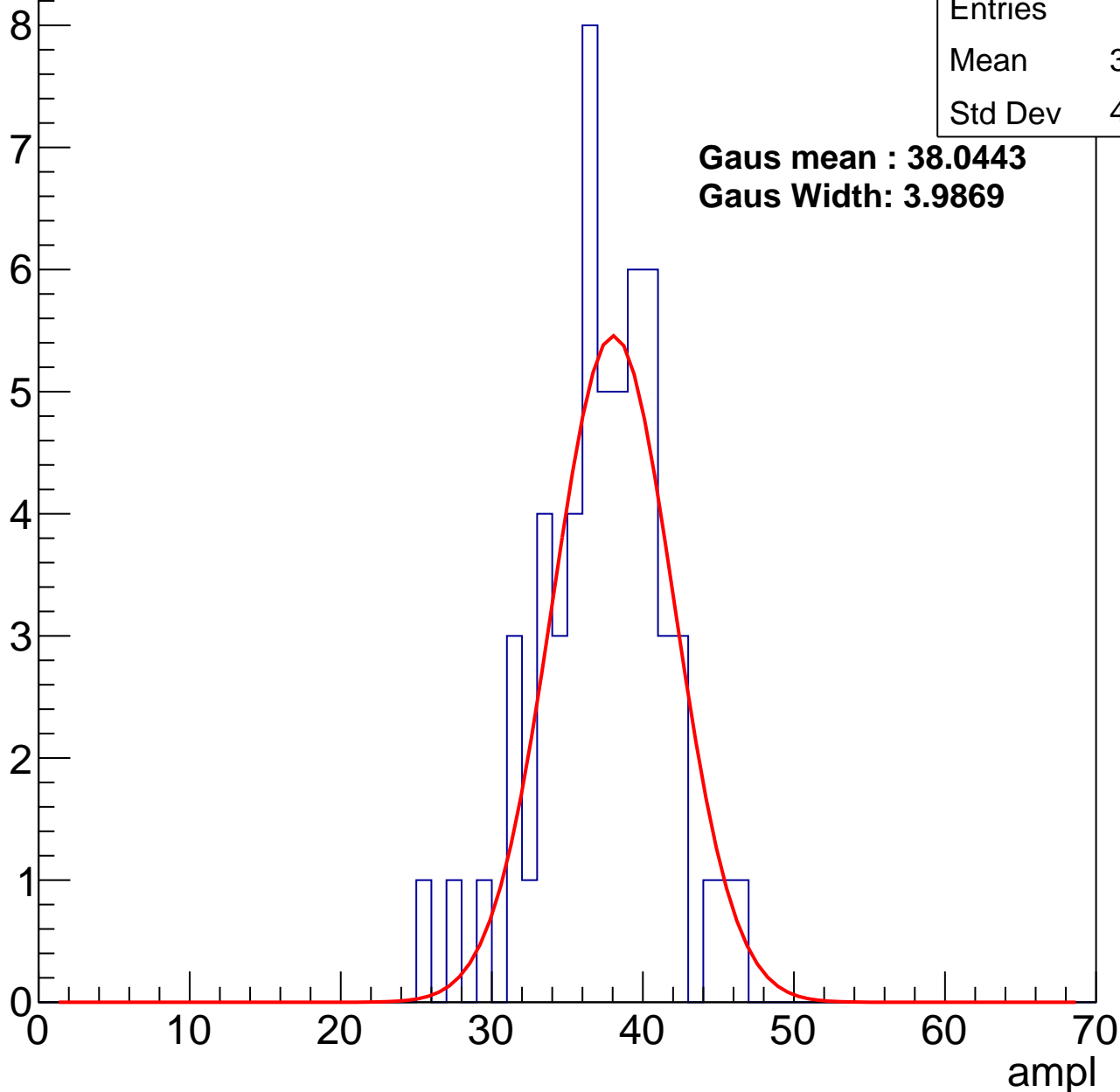
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.86
Std Dev	4.093

Gaus mean : 38.0443

Gaus Width: 3.9869



B1L101S, U25-ch108, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	44.32
Std Dev	3.451

Gaus mean : 44.6534

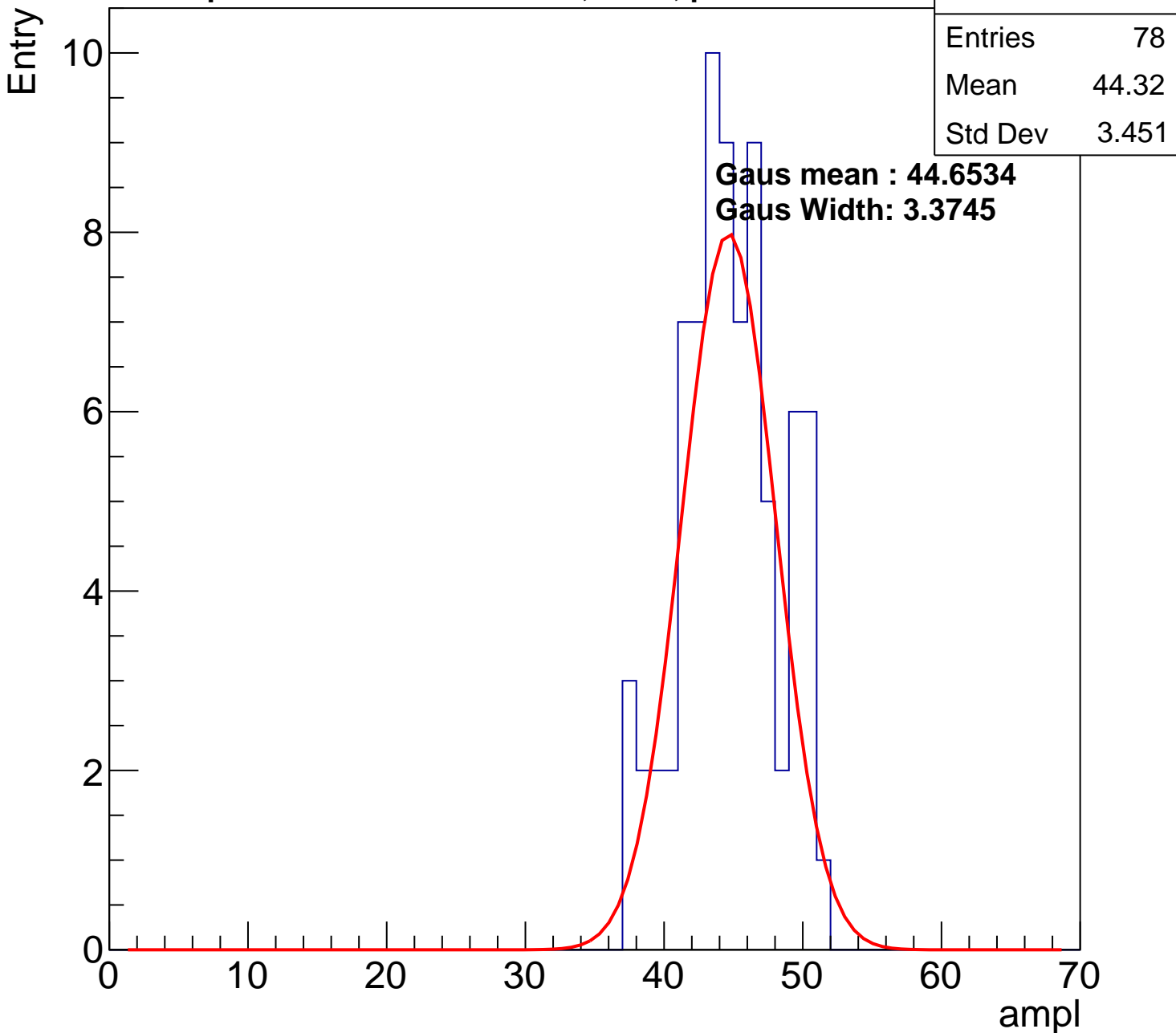
Gaus Width: 3.3745

Entry

10
8
6
4
2
0

ampl

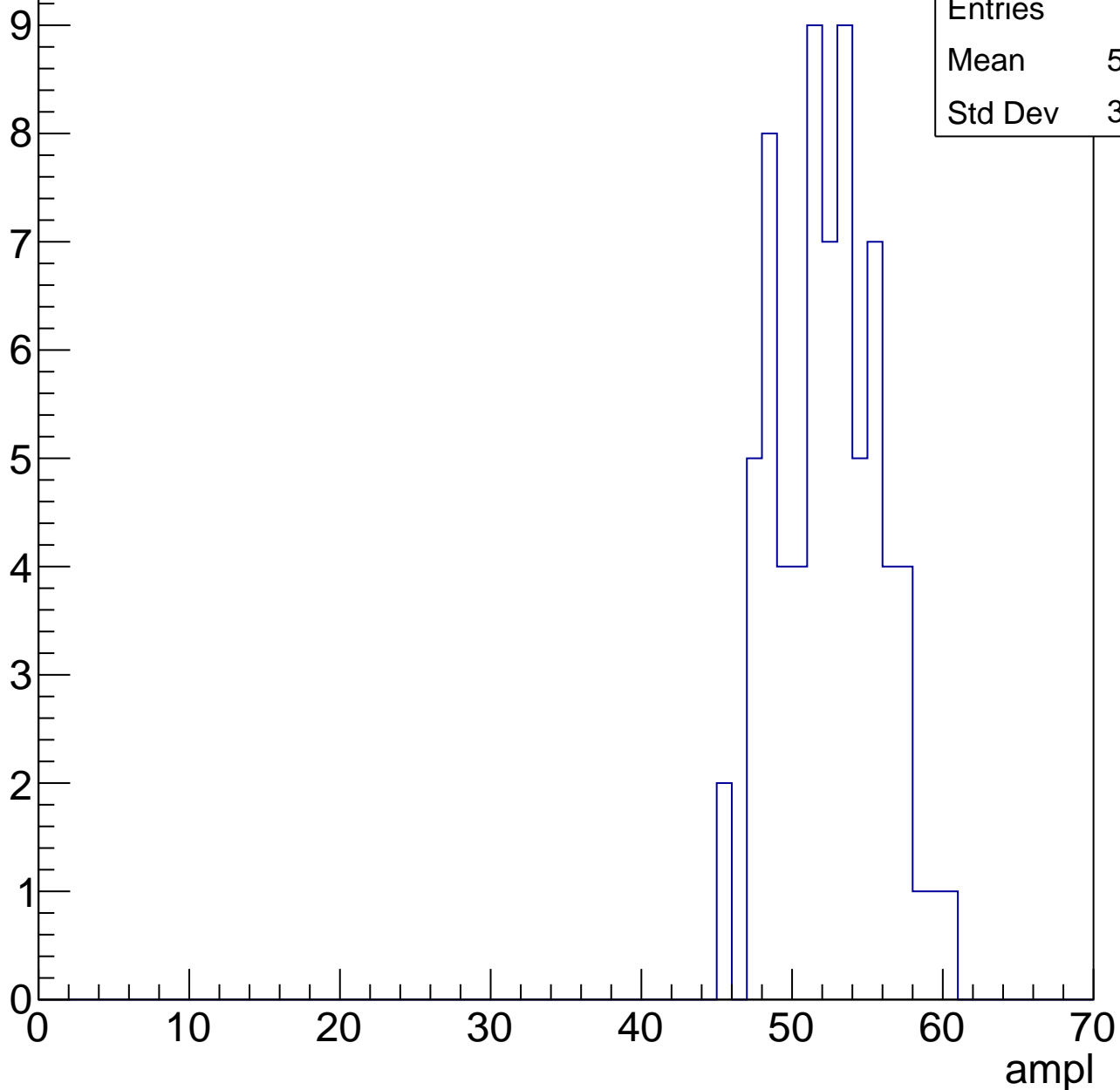
0 10 20 30 40 50 60 70



B1L101S, U25-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

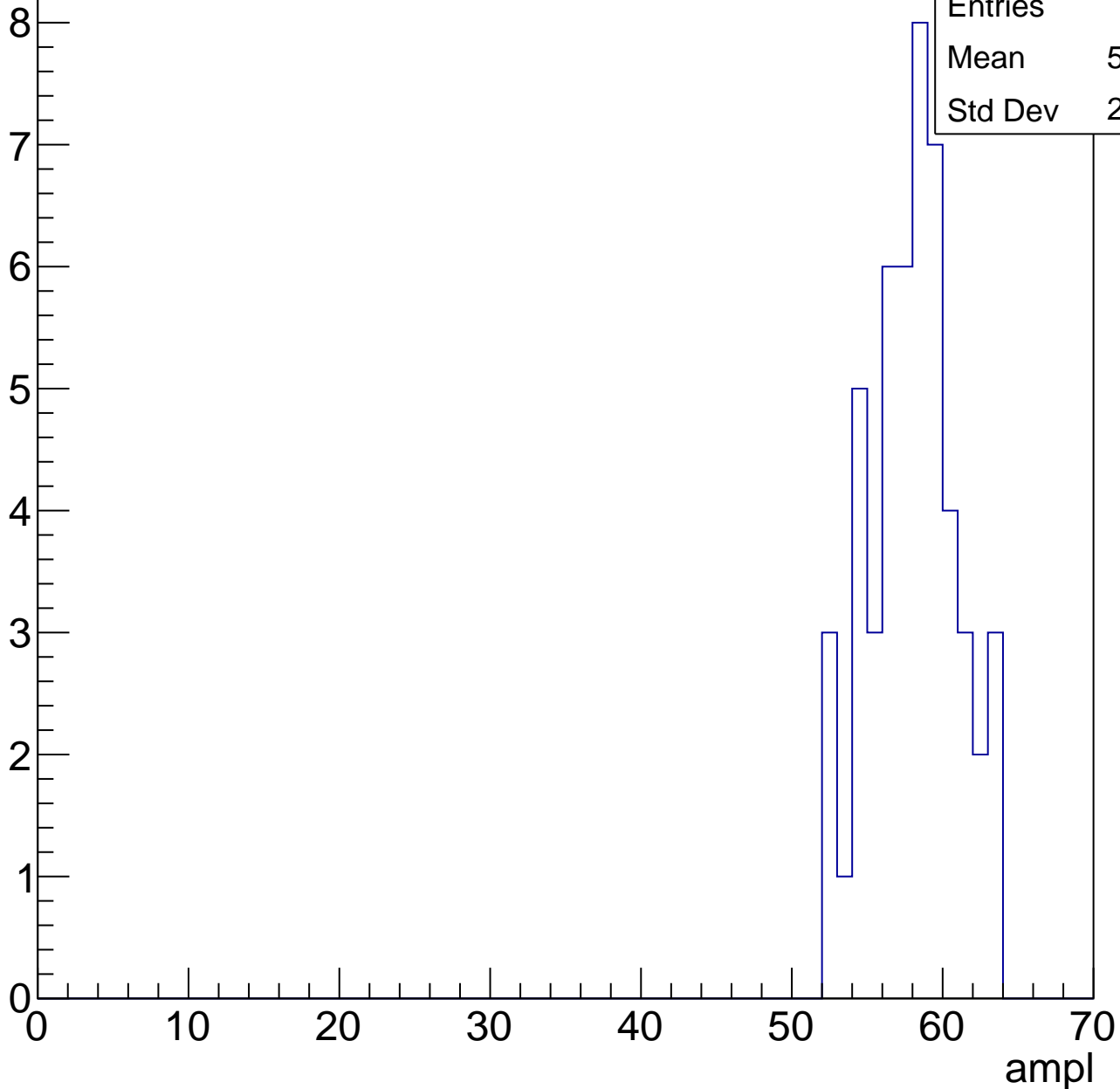


B1L101S, U25-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.55
Std Dev	2.858

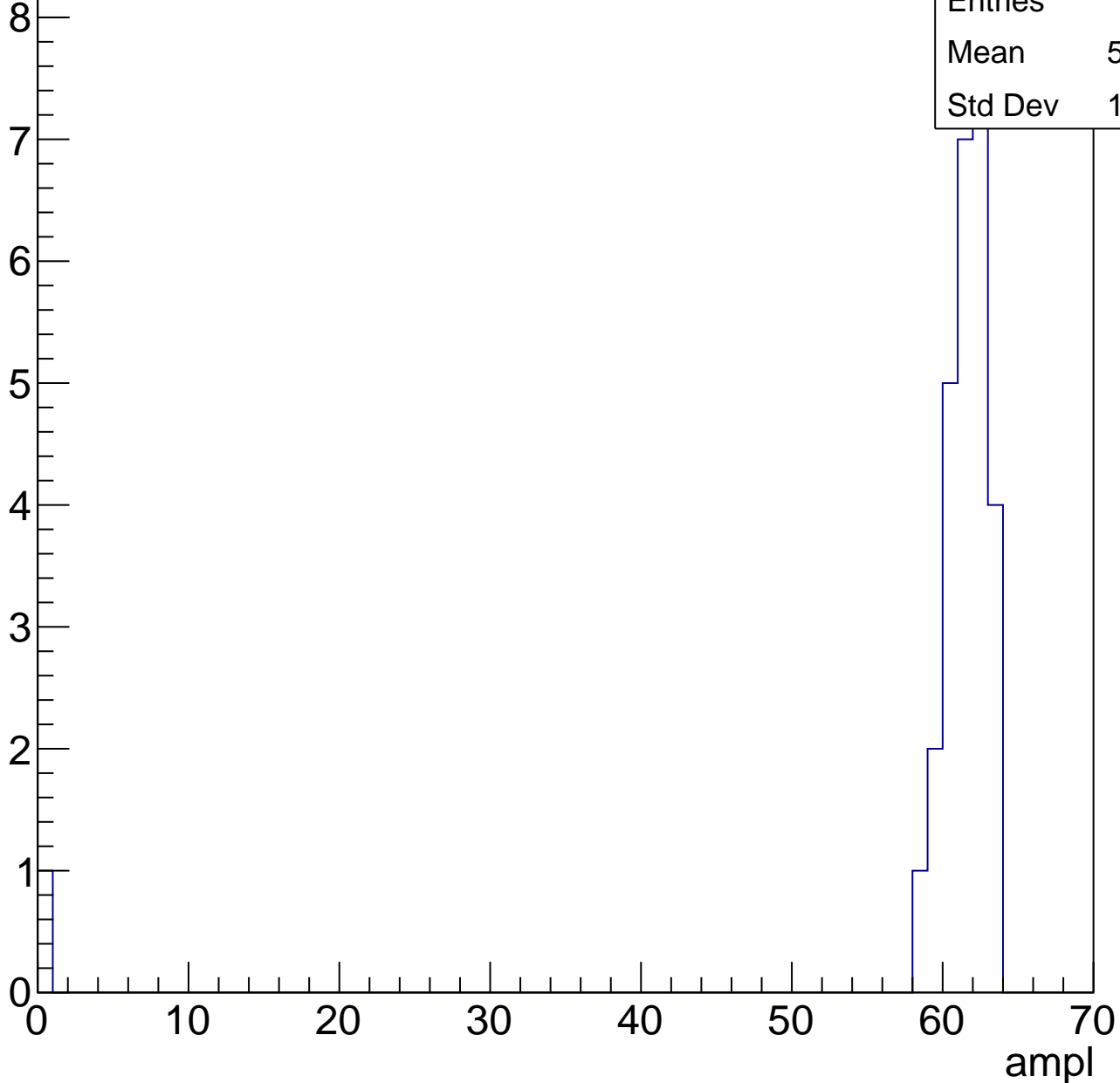


B1L101S, U25-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	28
Mean	58.96
Std Dev	11.42



B1L101S, U25-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch109, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	30.63
Std Dev	5.249

Gaus mean : 31.9741

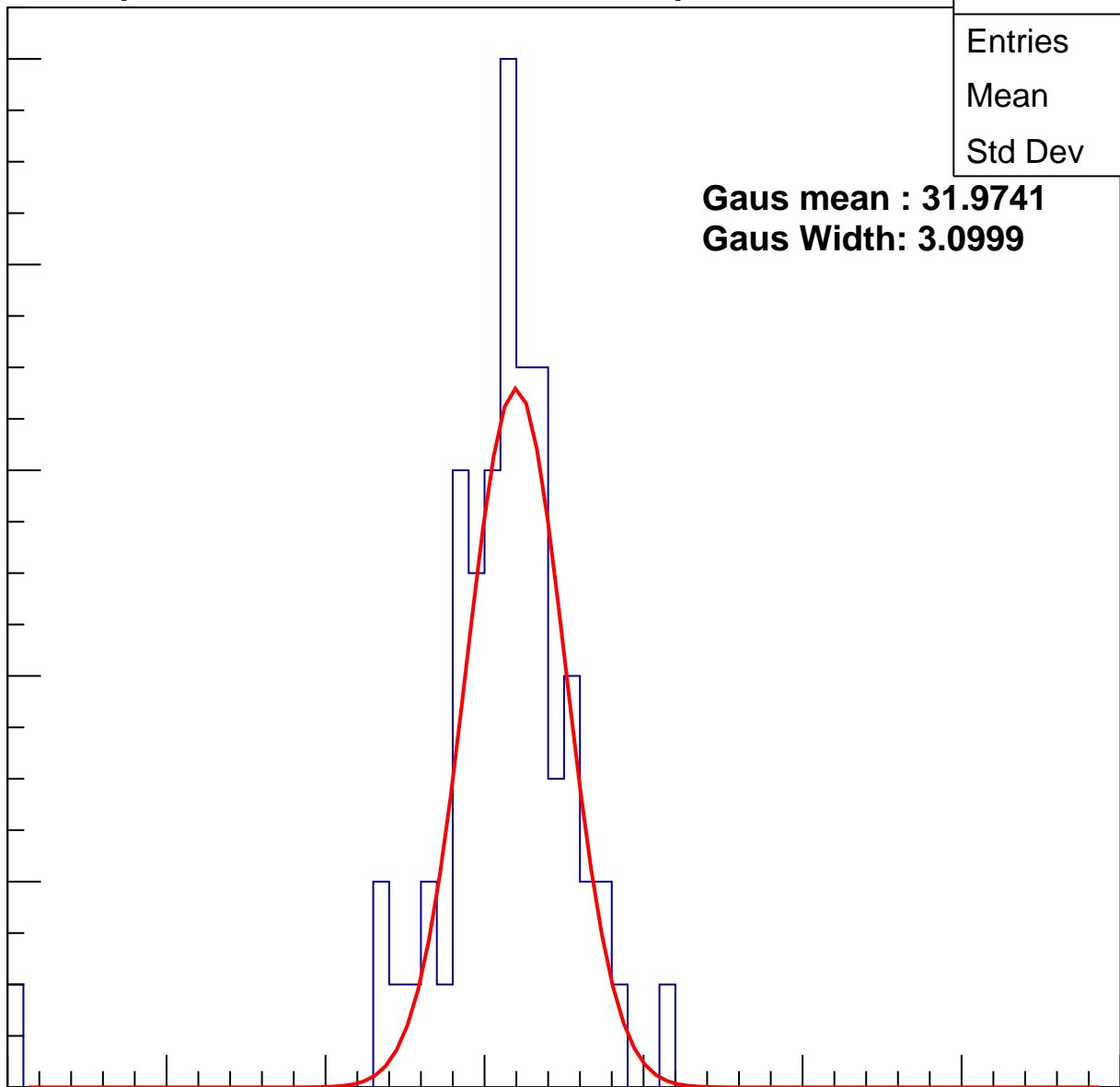
Gaus Width: 3.0999

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



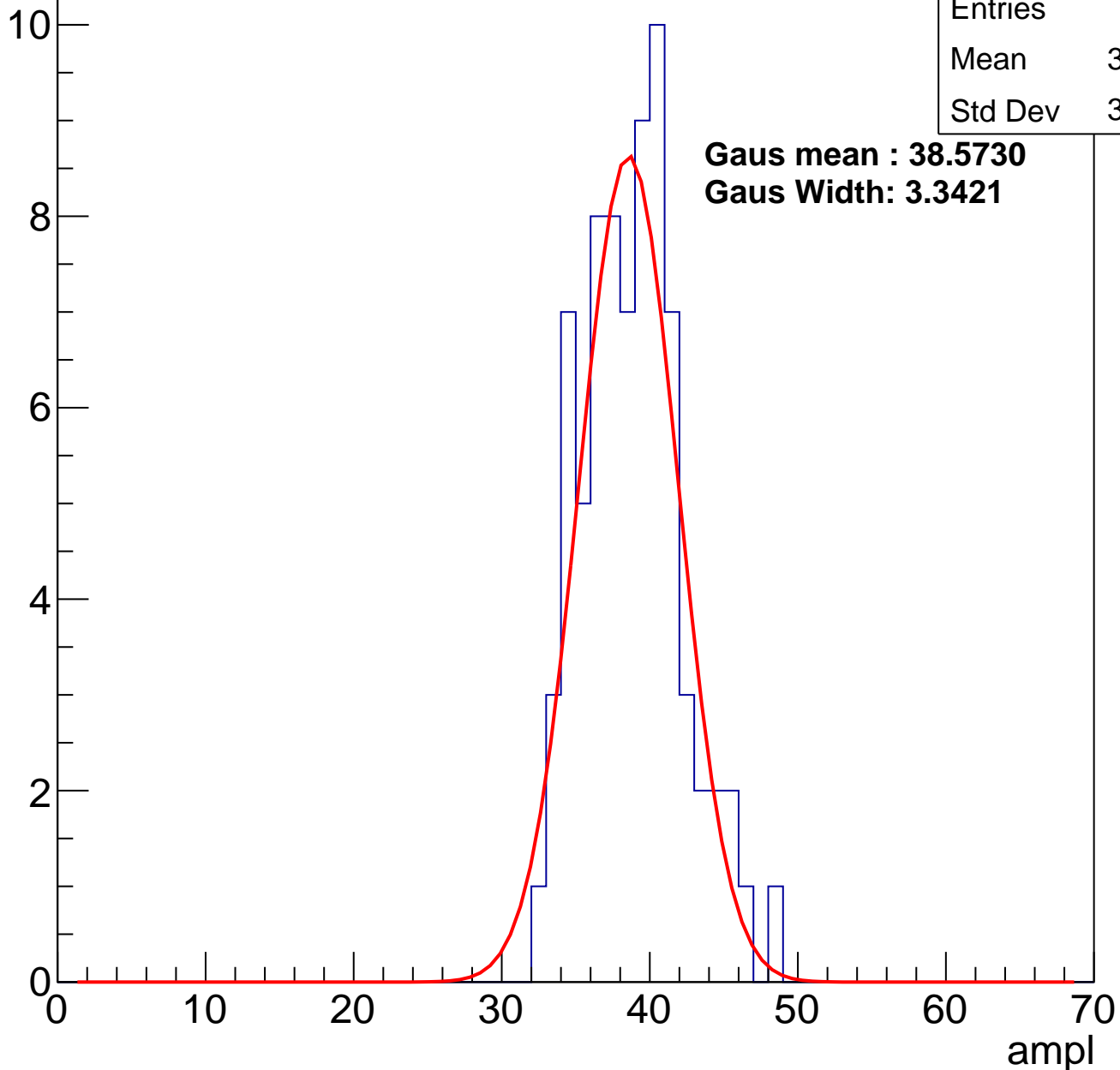
B1L101S, U25-ch109, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	38.37
Std Dev	3.328

Gaus mean : 38.5730
Gaus Width: 3.3421

Entry



B1L101S, U25-ch109, adc2

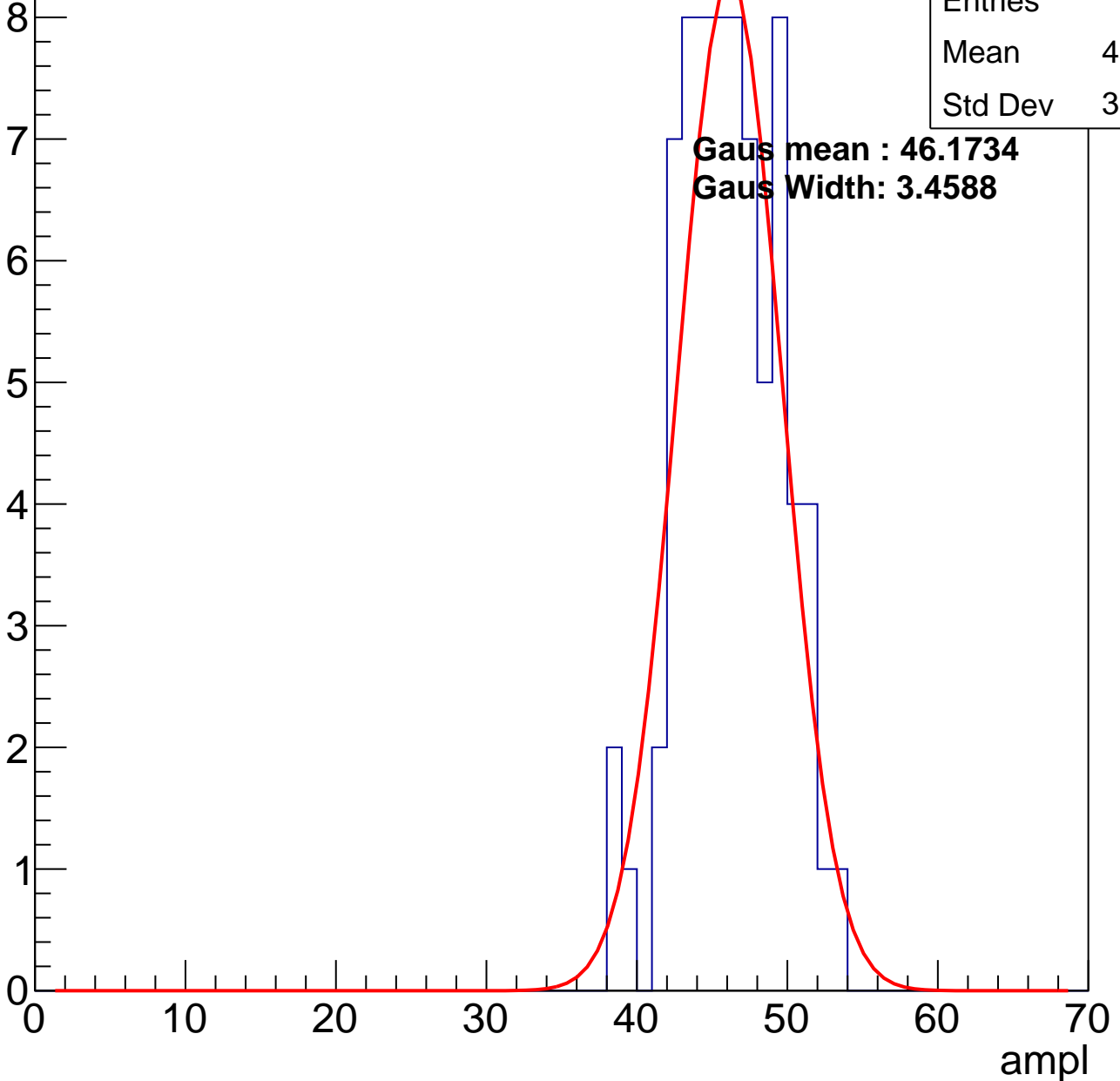
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	45.74
Std Dev	3.276

Gaus mean : 46.1734

Gaus Width: 3.4588

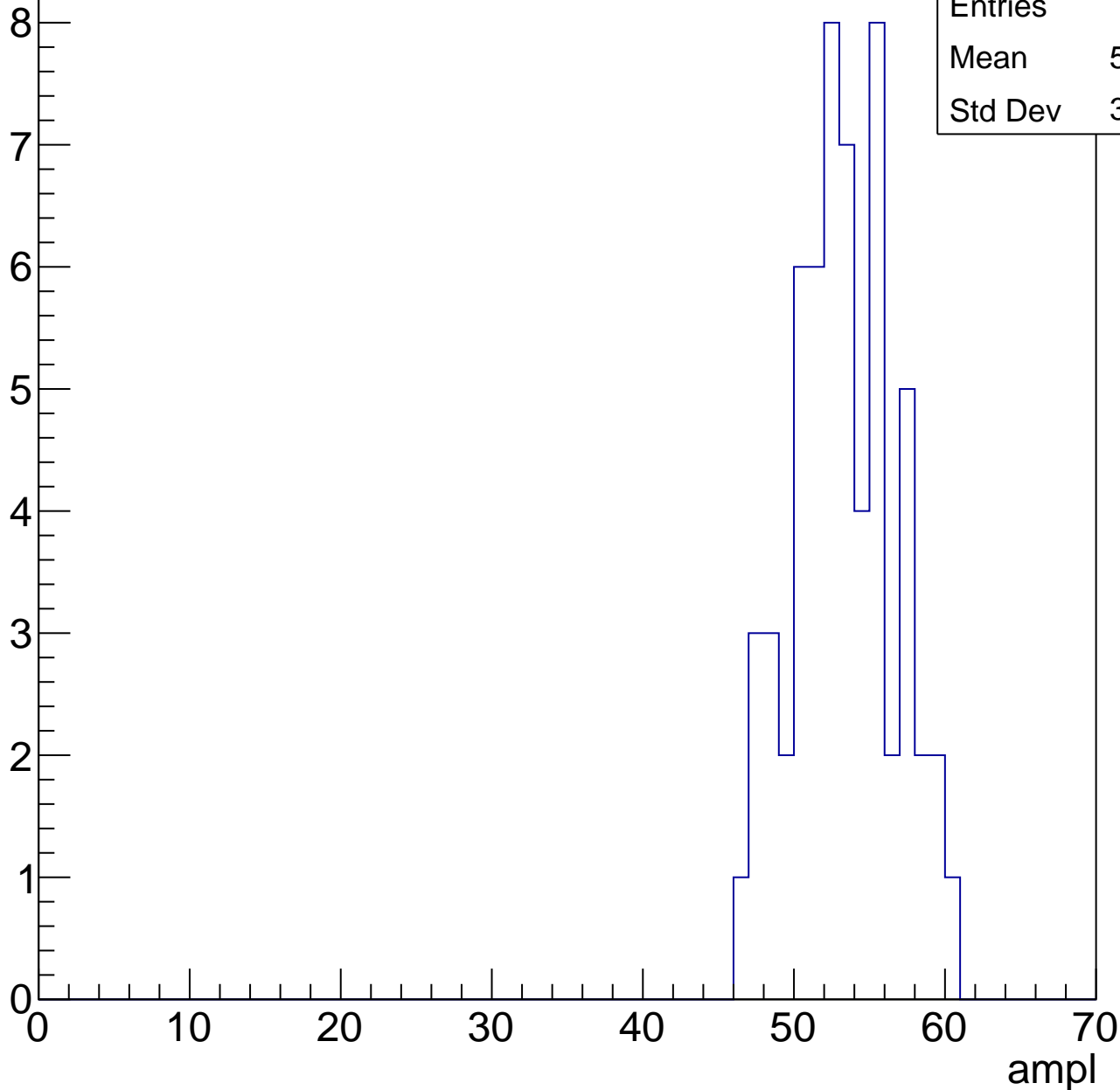


B1L101S, U25-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	52.82
Std Dev	3.299

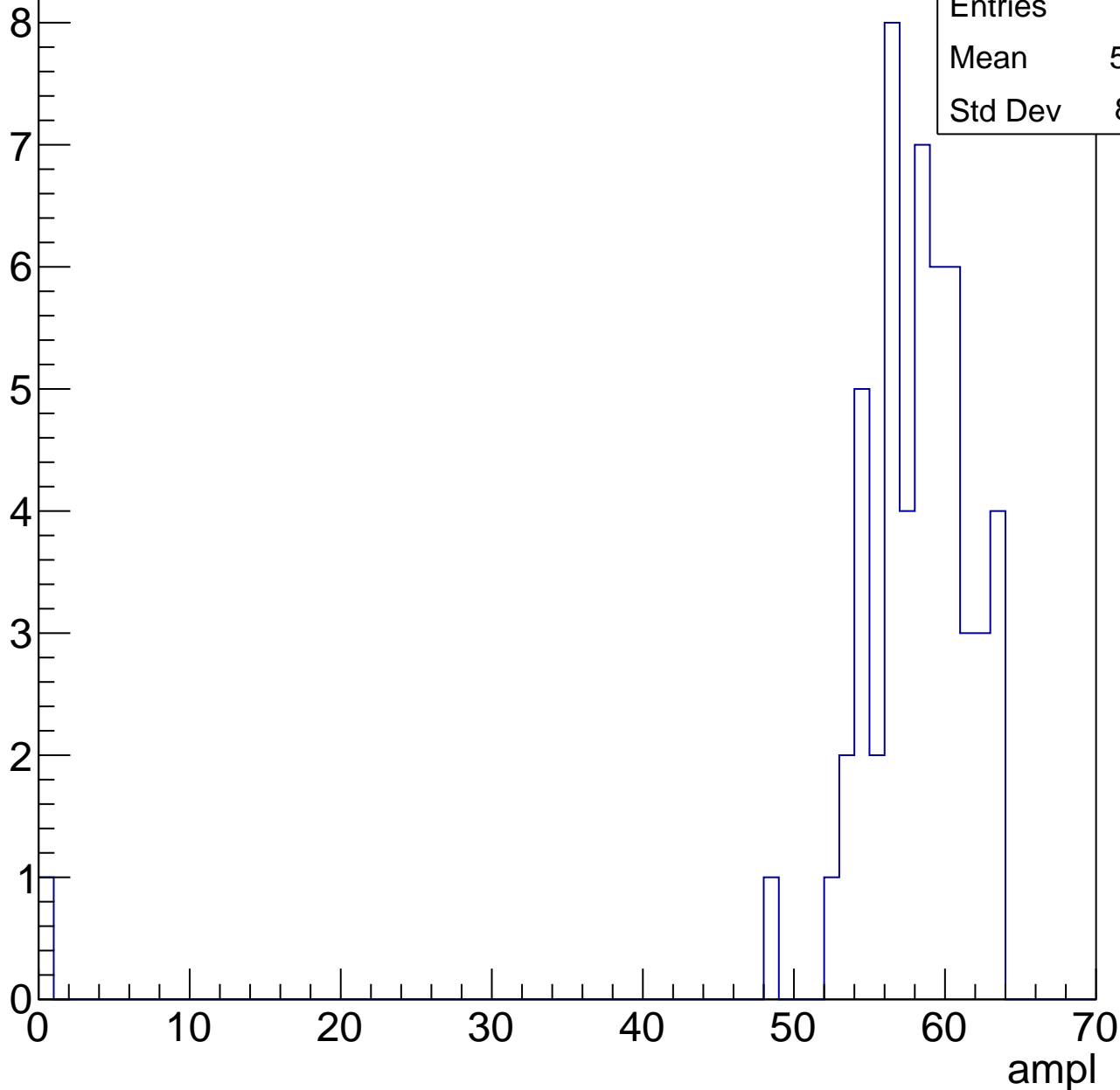


B1L101S, U25-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.66
Std Dev	8.461

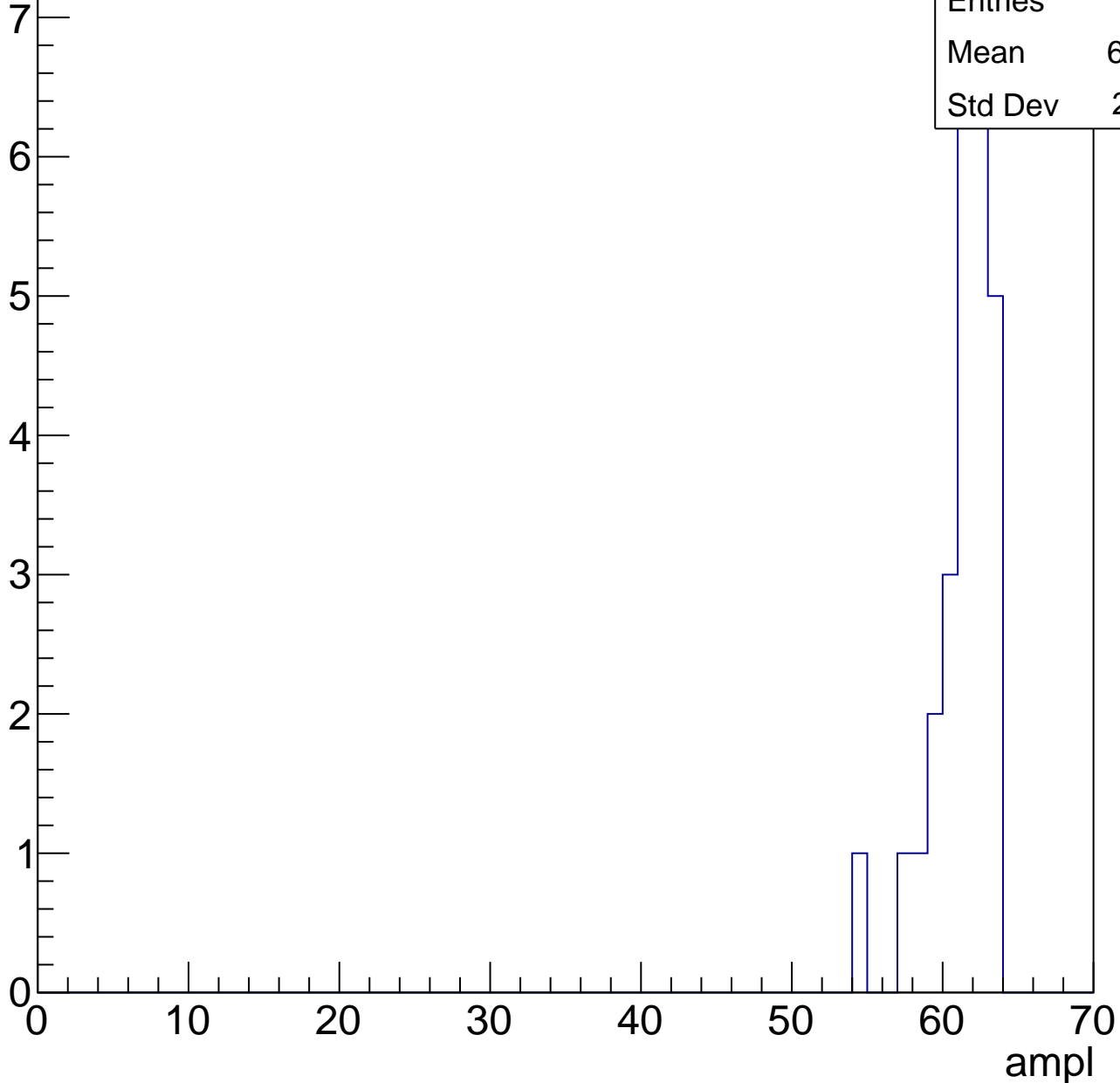


B1L101S, U25-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	60.85
Std Dev	2.031



B1L101S, U25-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch110, adc0

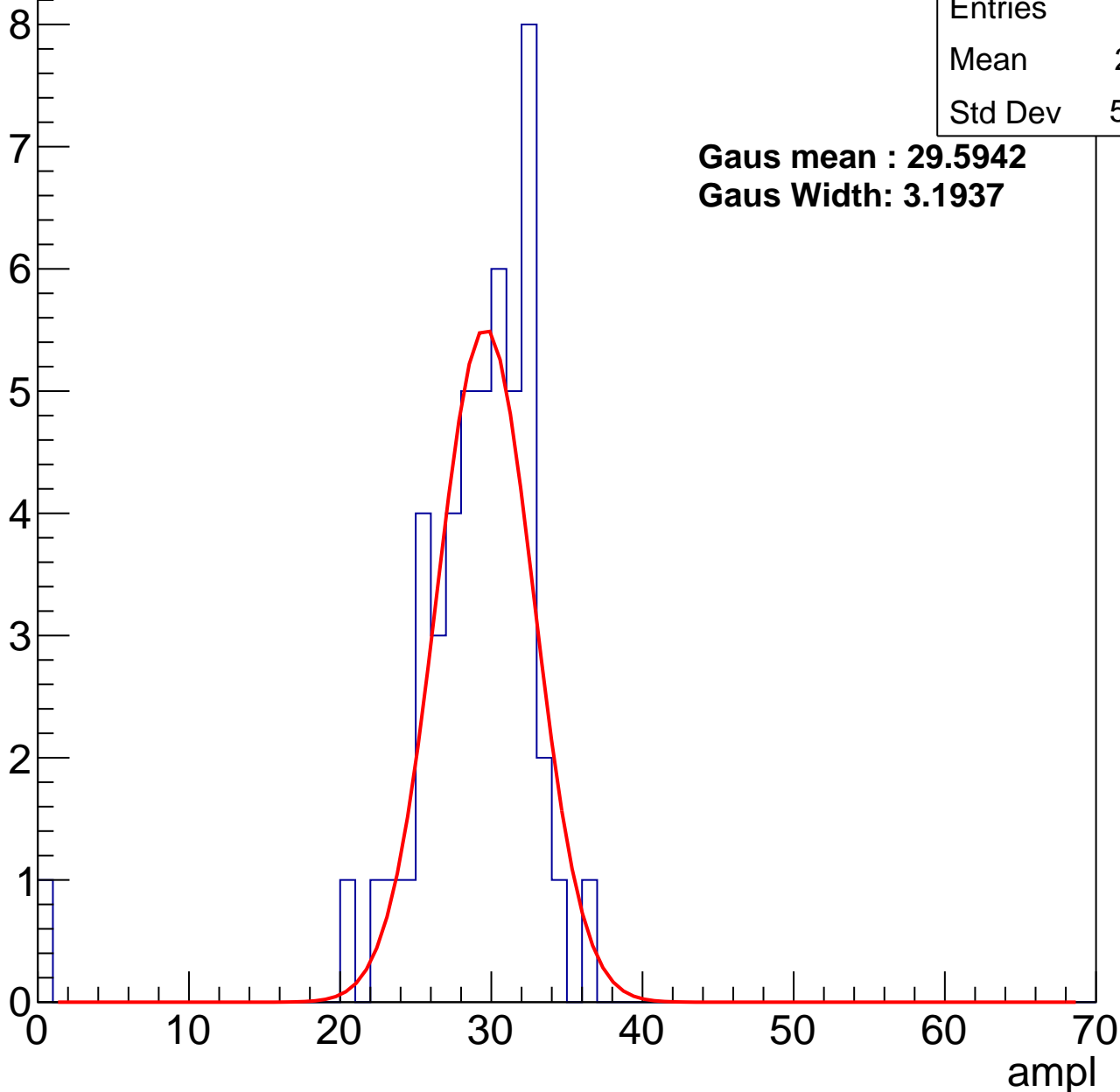
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	28.31
Std Dev	5.199

Gaus mean : 29.5942

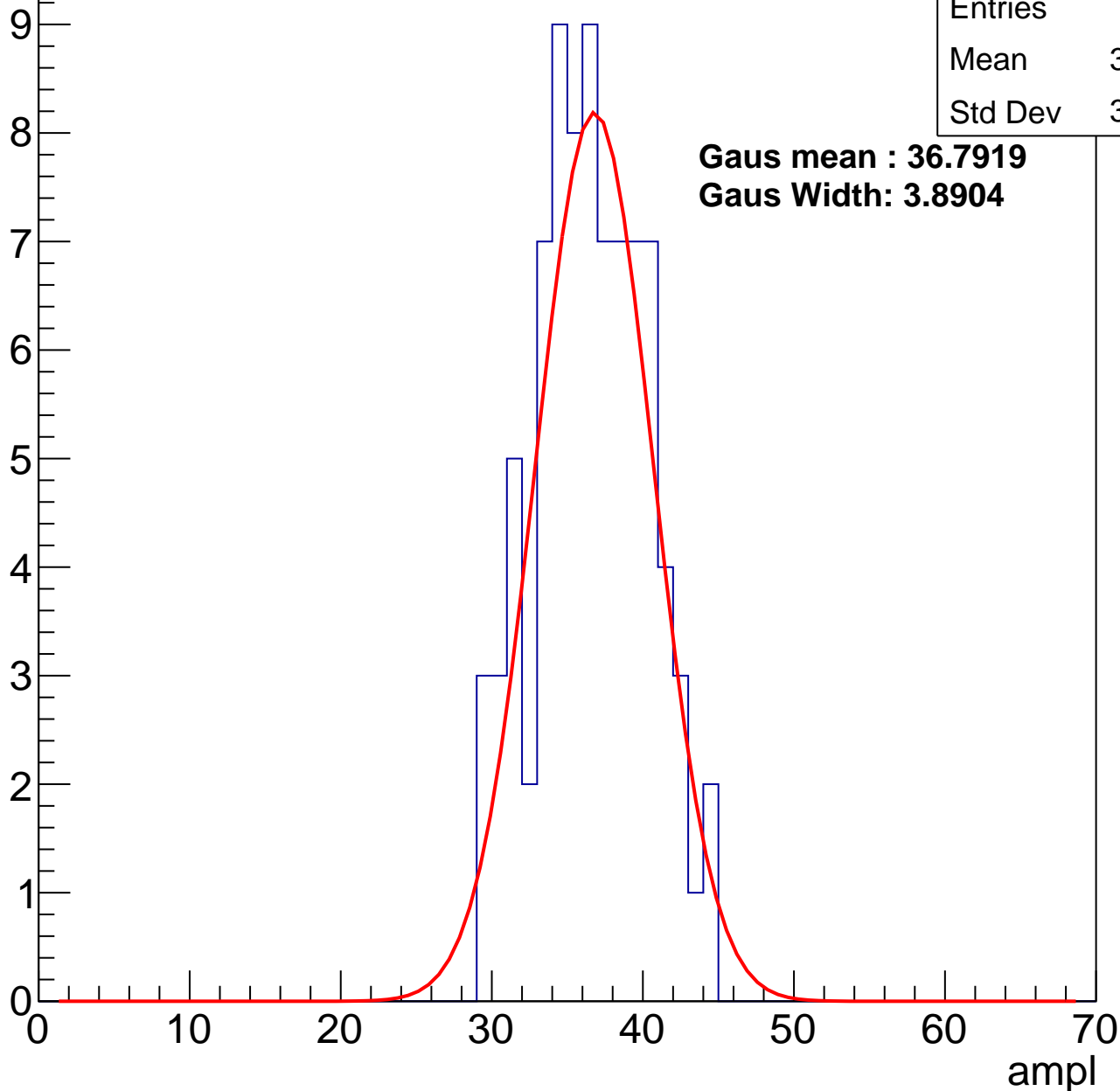
Gaus Width: 3.1937



B1L101S, U25-ch110, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch110, adc2

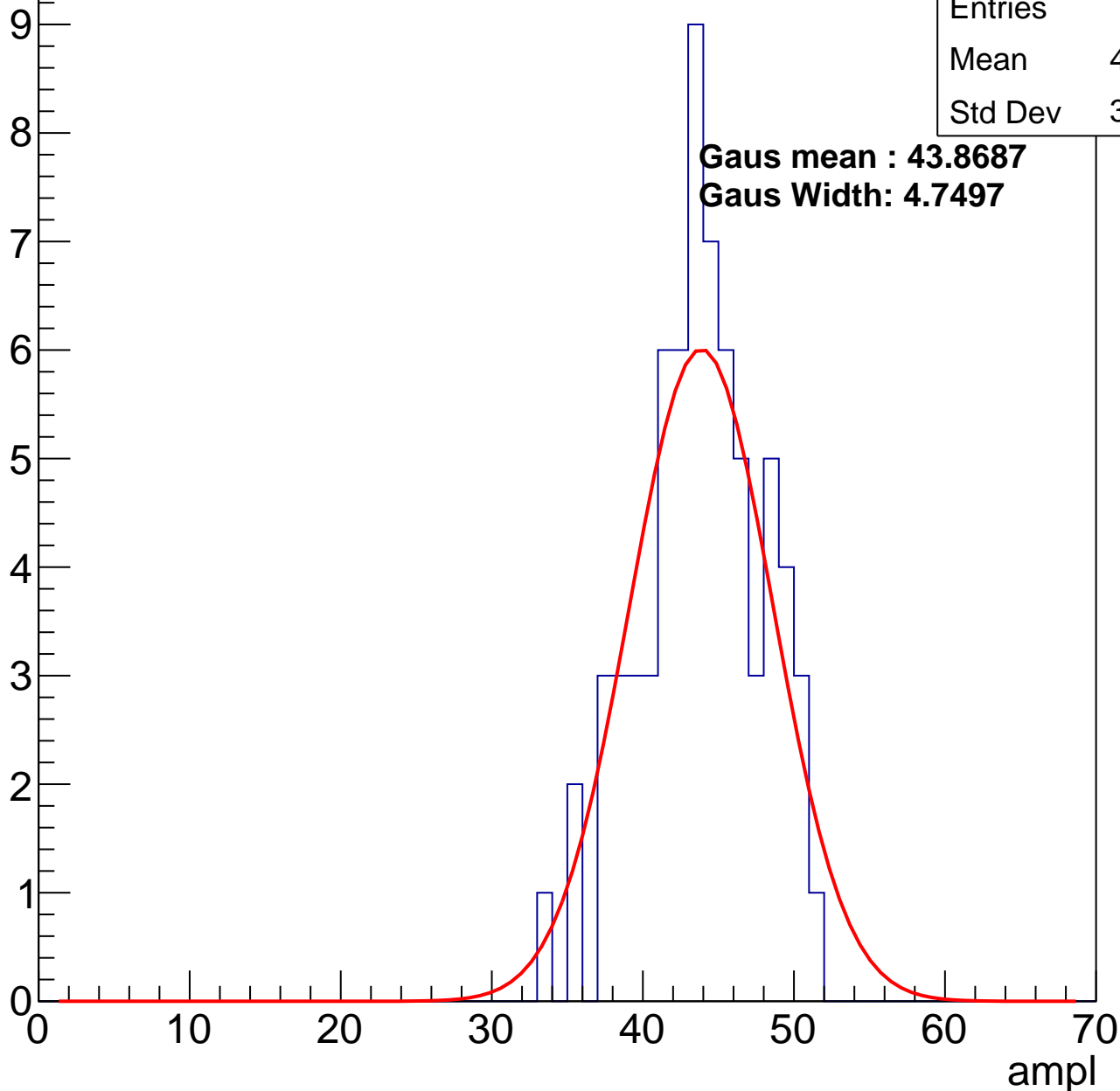
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.37
Std Dev	3.983

Gaus mean : 43.8687

Gaus Width: 4.7497

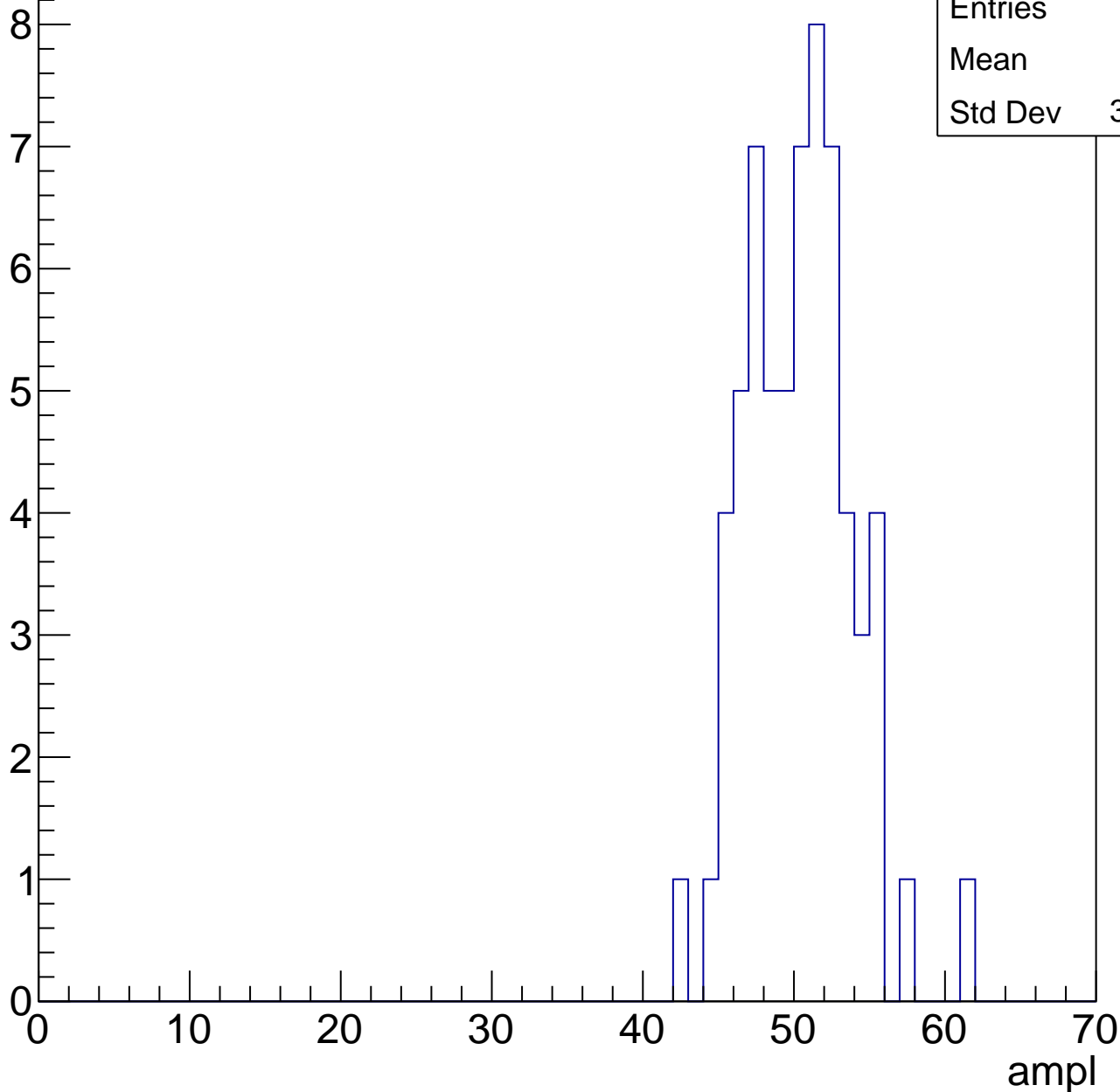


B1L101S, U25-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49.9
Std Dev	3.467

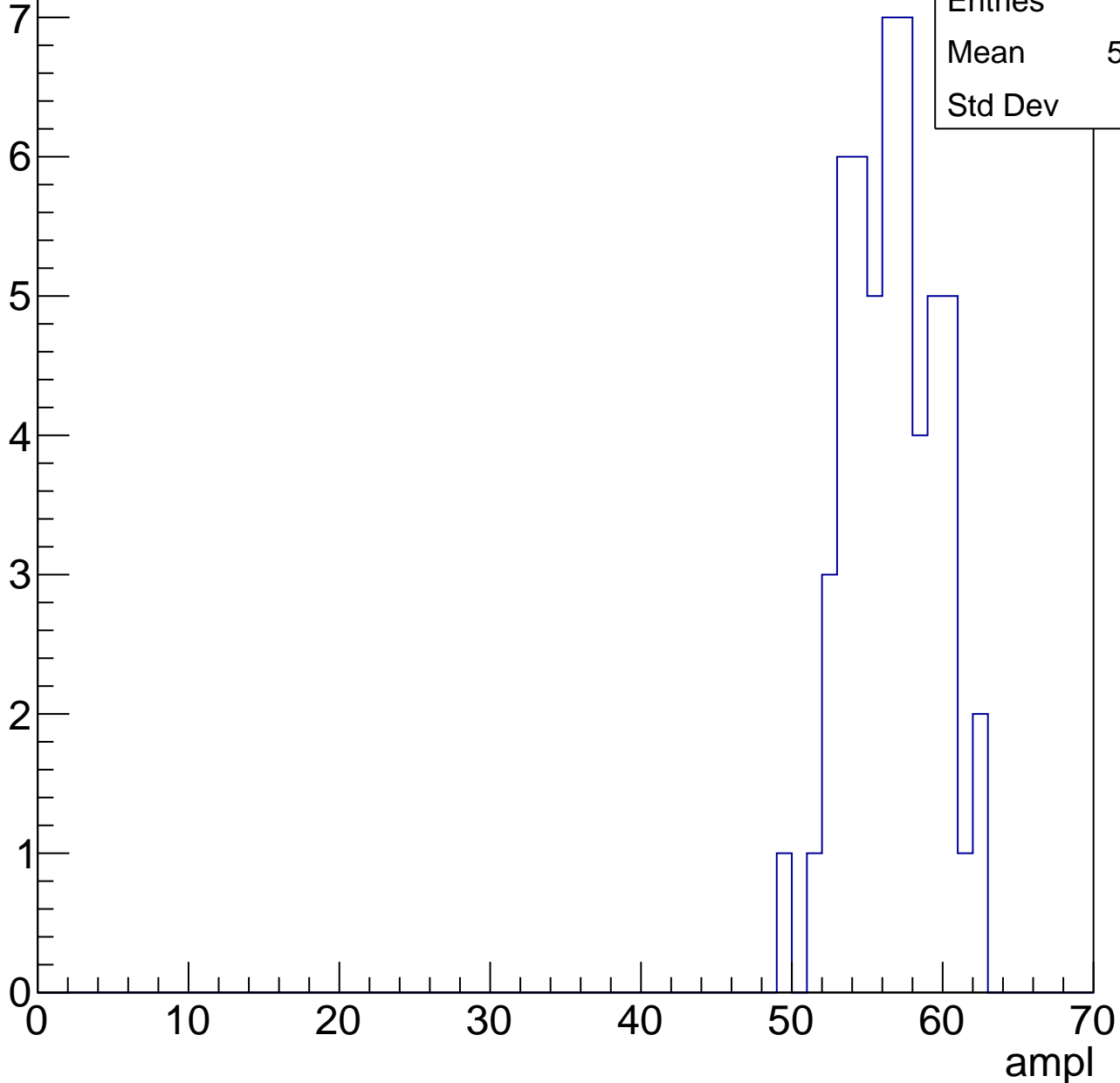


B1L101S, U25-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.15
Std Dev	2.91

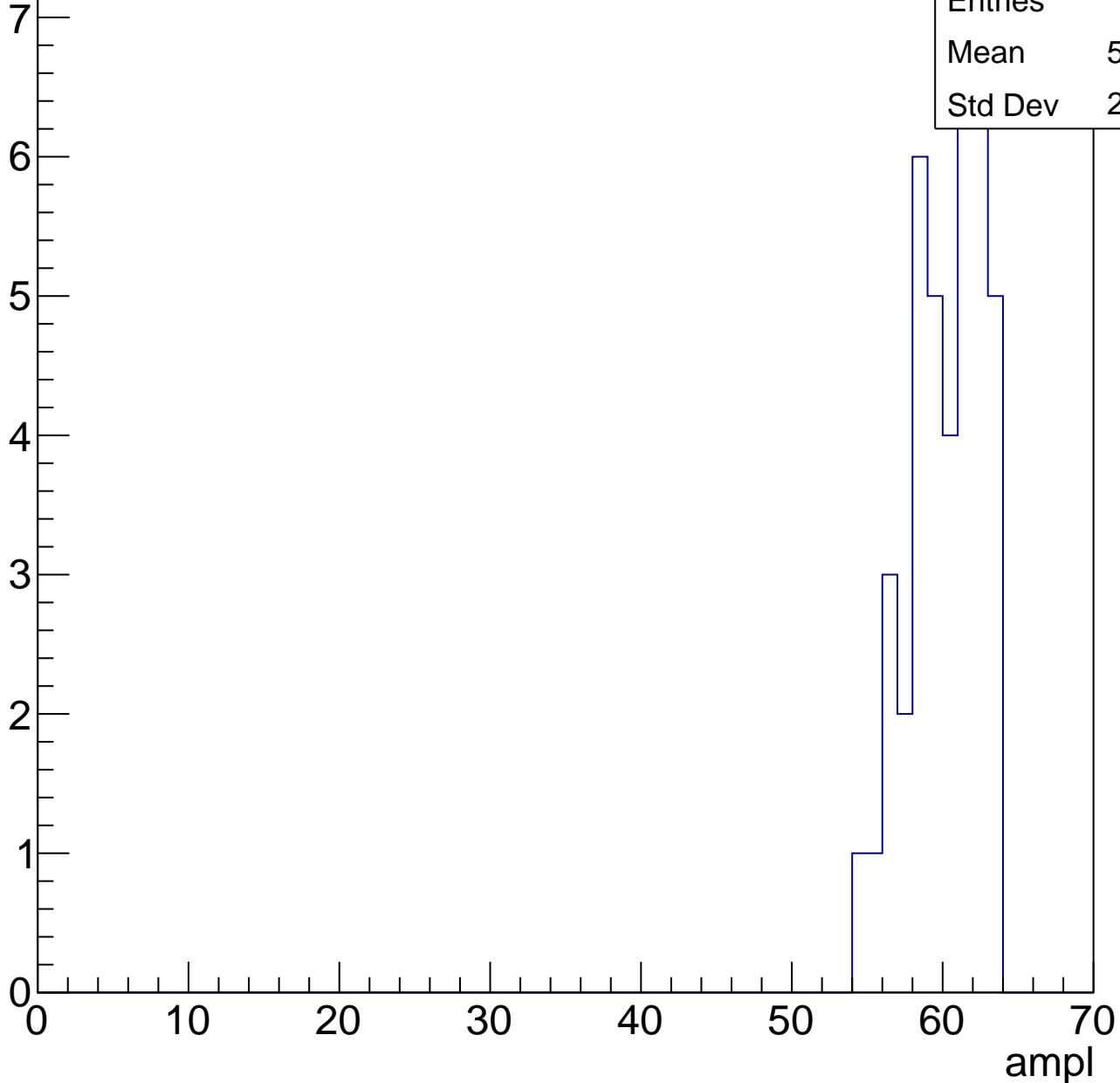


B1L101S, U25-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	59.76
Std Dev	2.387

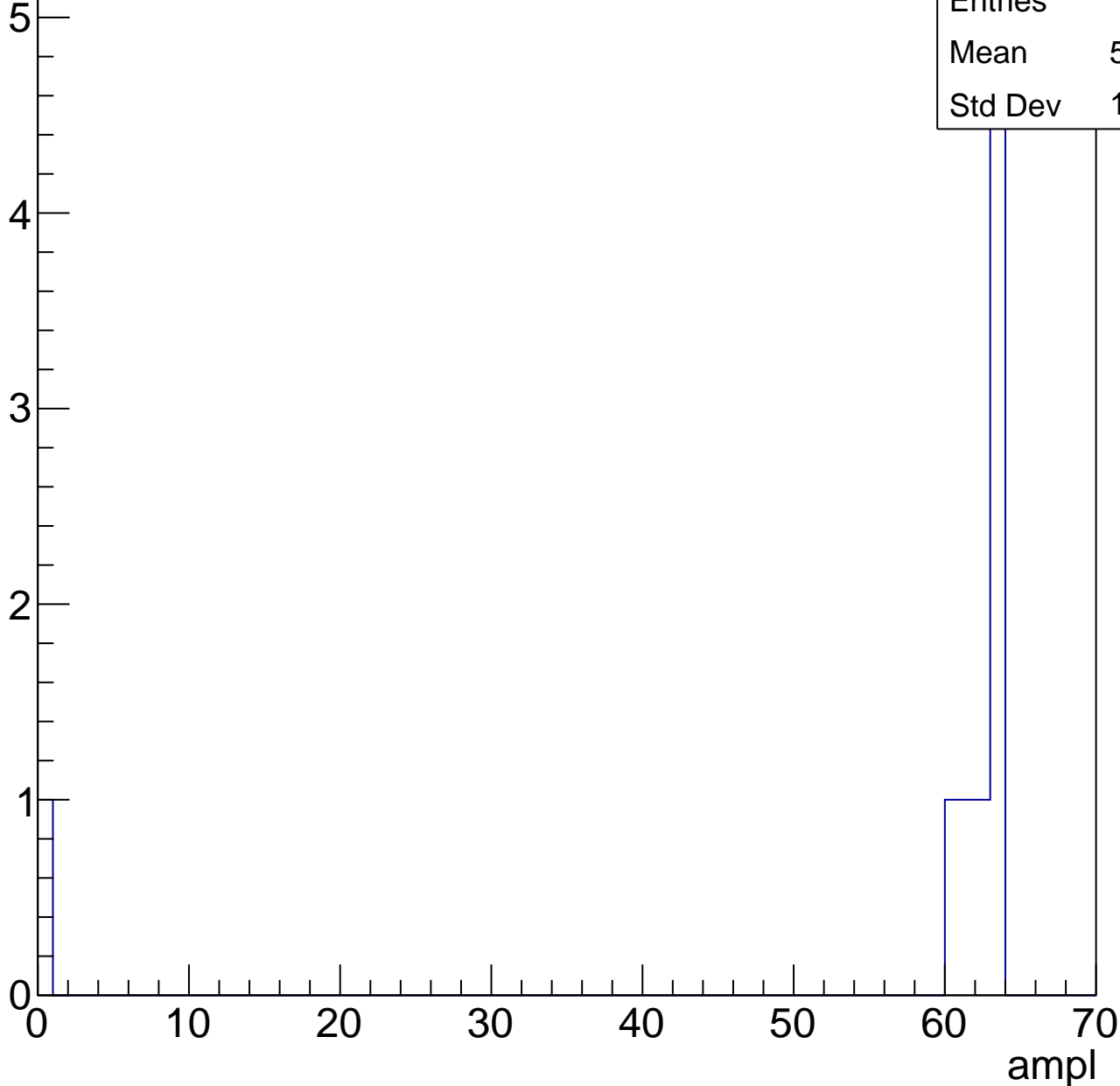


B1L101S, U25-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	55.33
Std Dev	19.59



B1L101S, U25-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U25-ch111, adc0

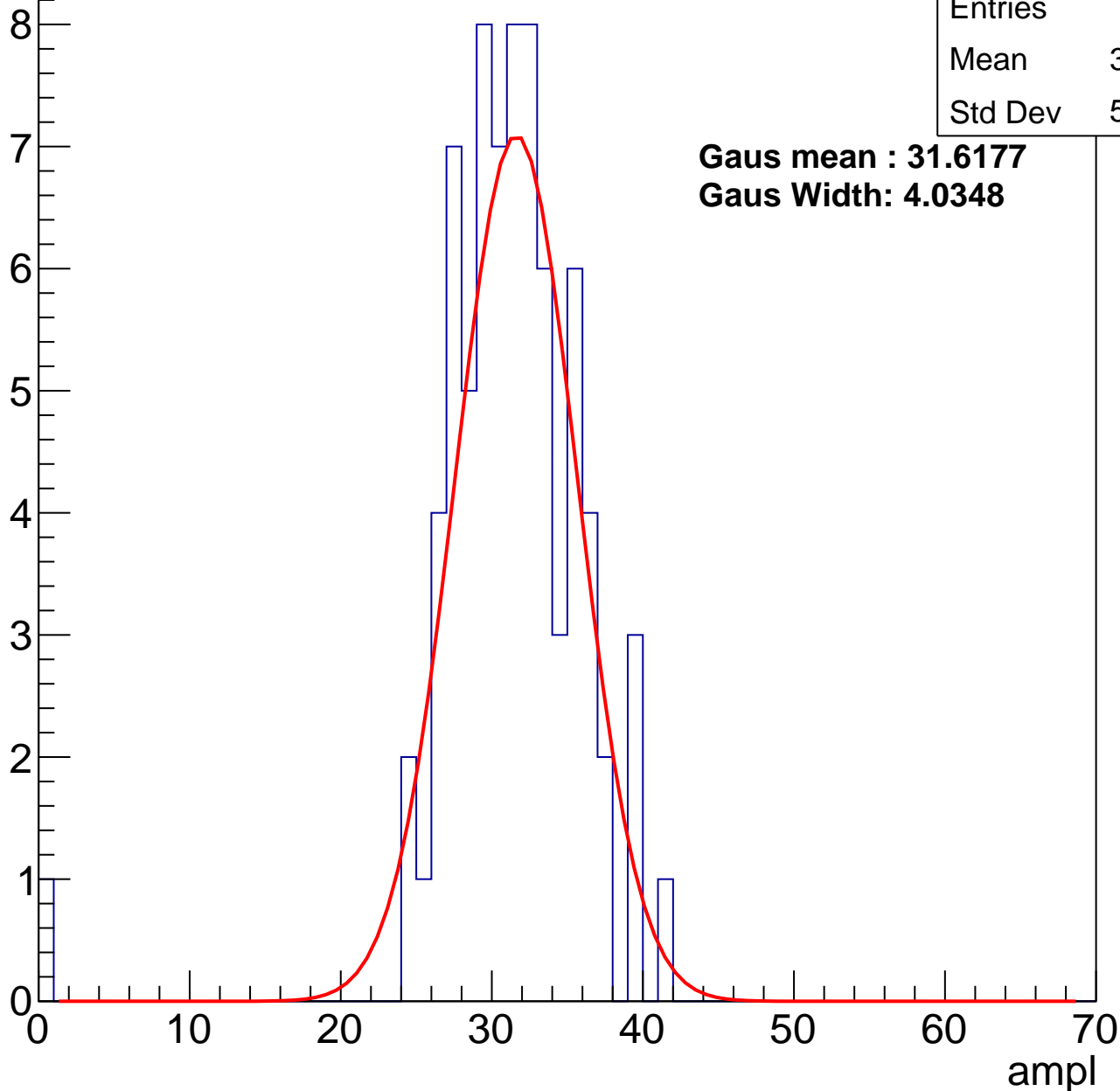
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	30.76
Std Dev	5.137

Gaus mean : 31.6177

Gaus Width: 4.0348



B1L101S, U25-ch111, adc1

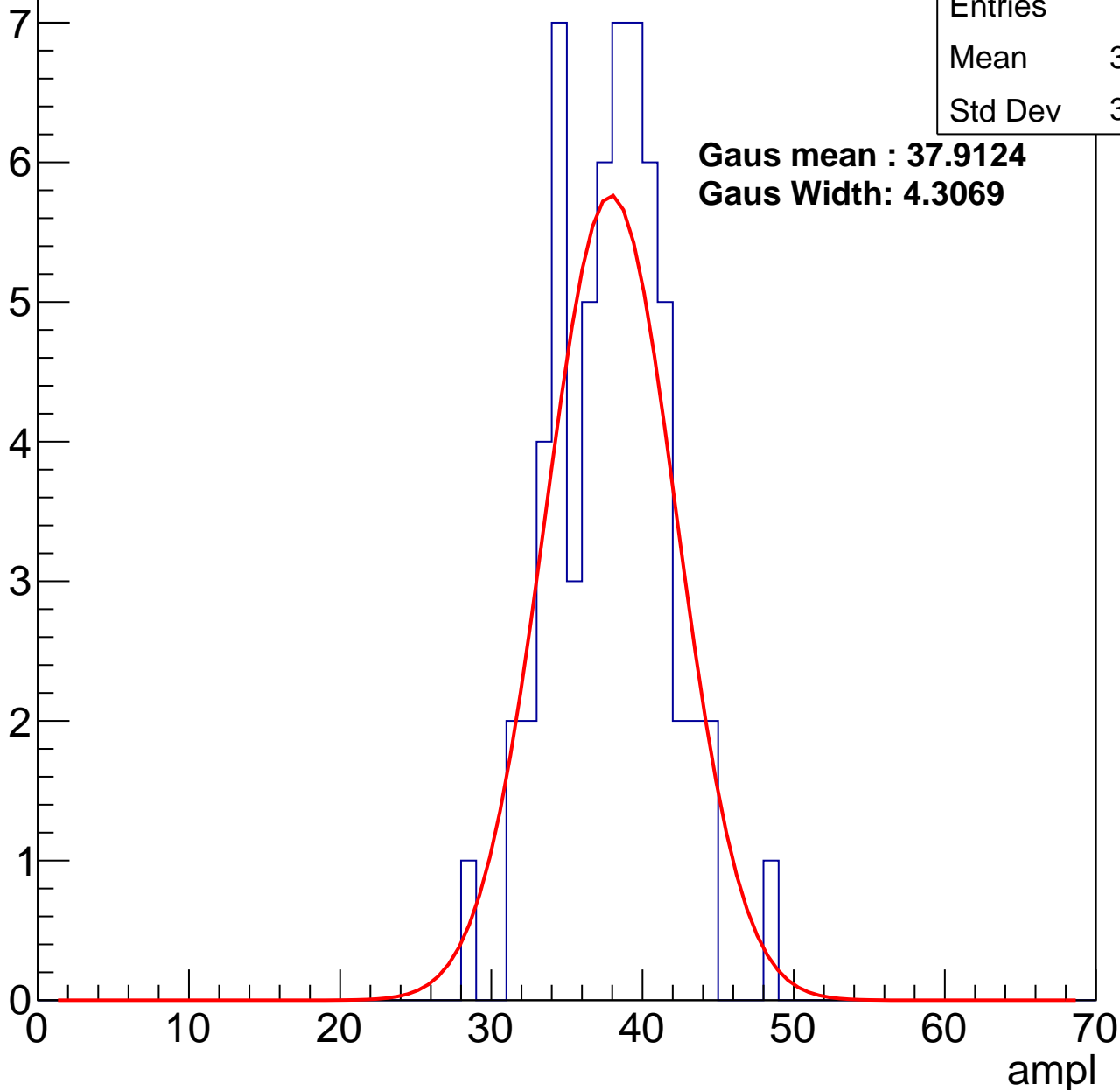
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.44
Std Dev	3.692

Gaus mean : 37.9124

Gaus Width: 4.3069



B1L101S, U25-ch111, adc2

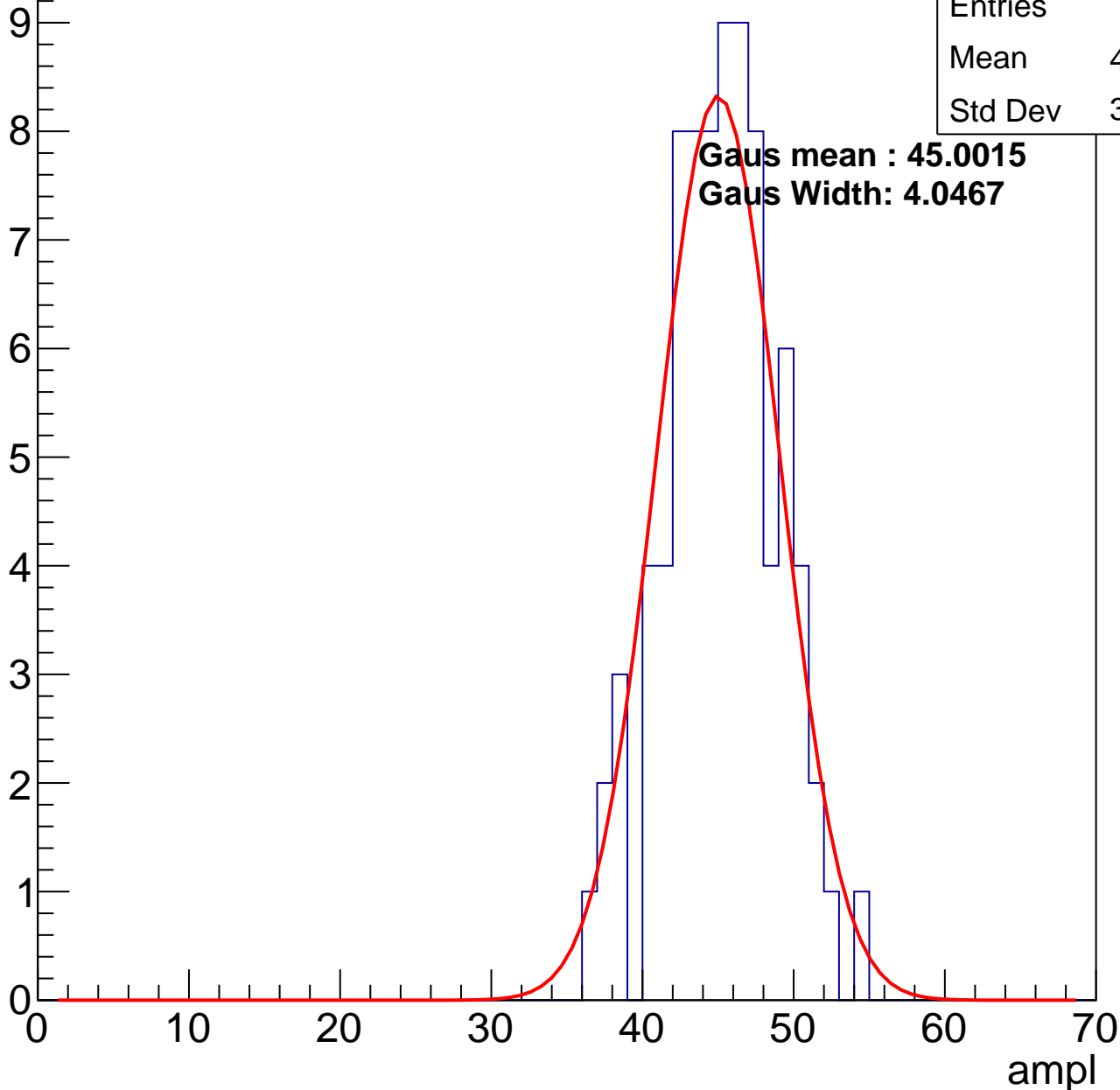
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	44.74
Std Dev	3.665

Gaus mean : 45.0015

Gaus Width: 4.0467

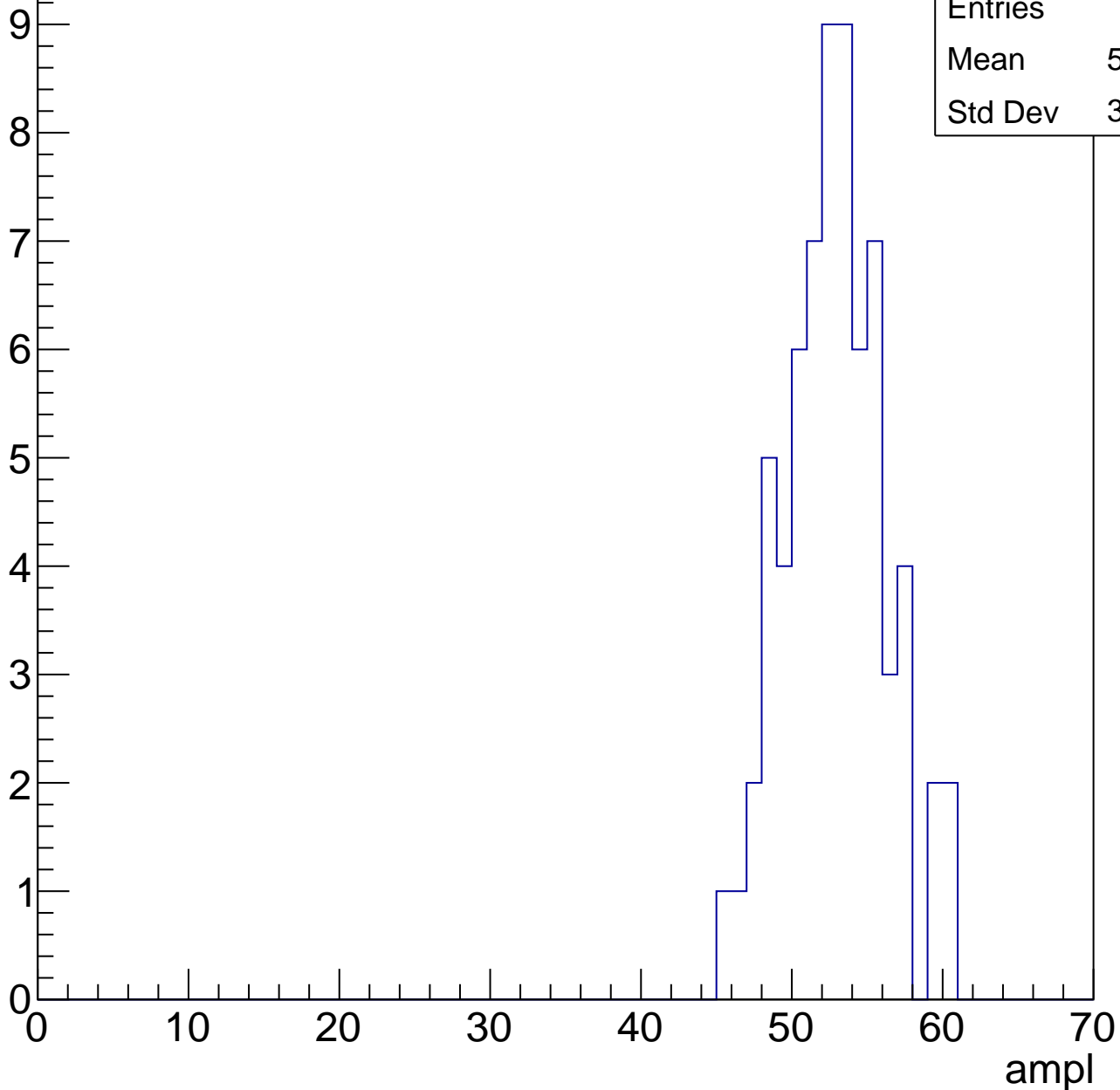


B1L101S, U25-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	52.44
Std Dev	3.296

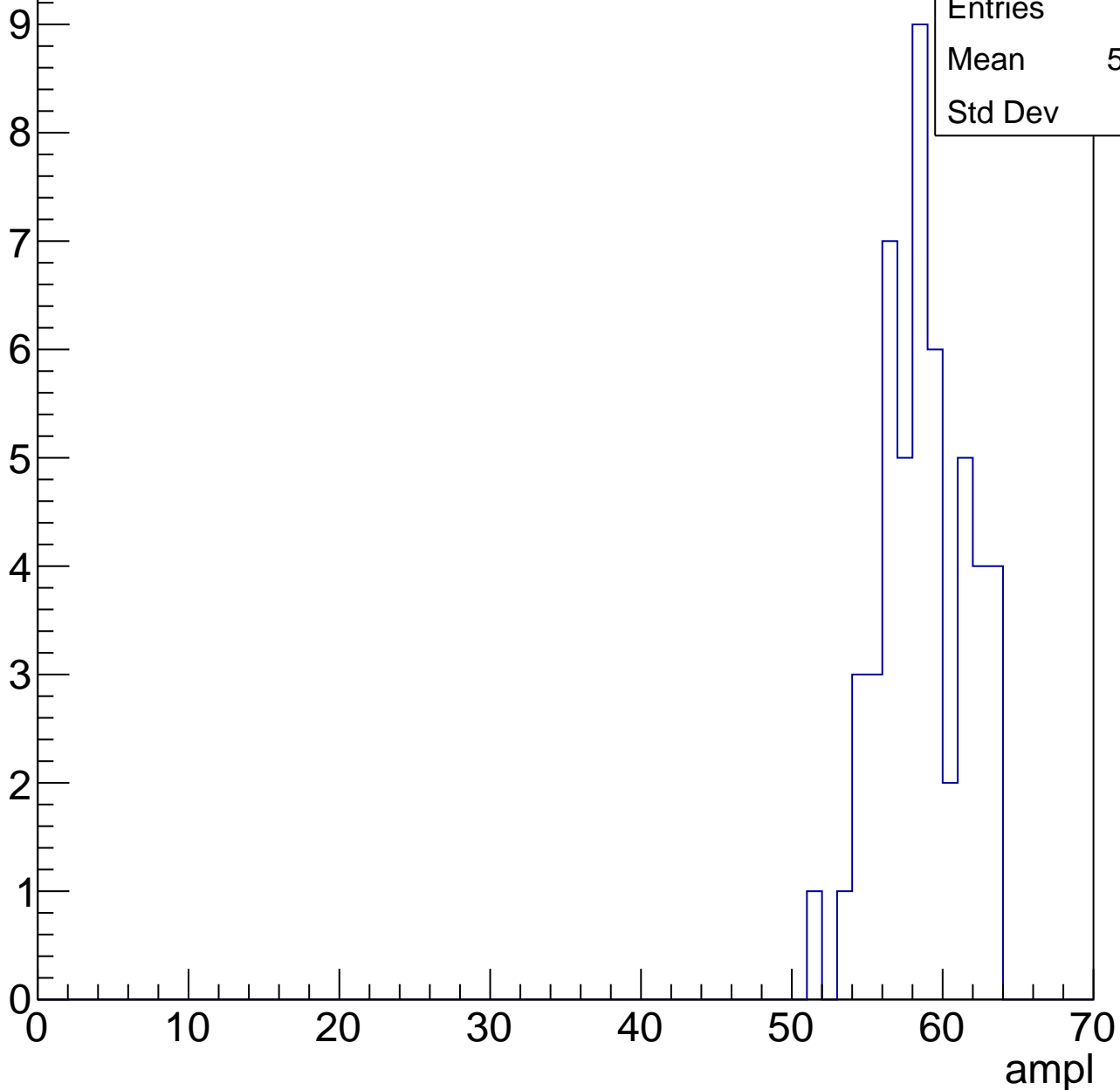


B1L101S, U25-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

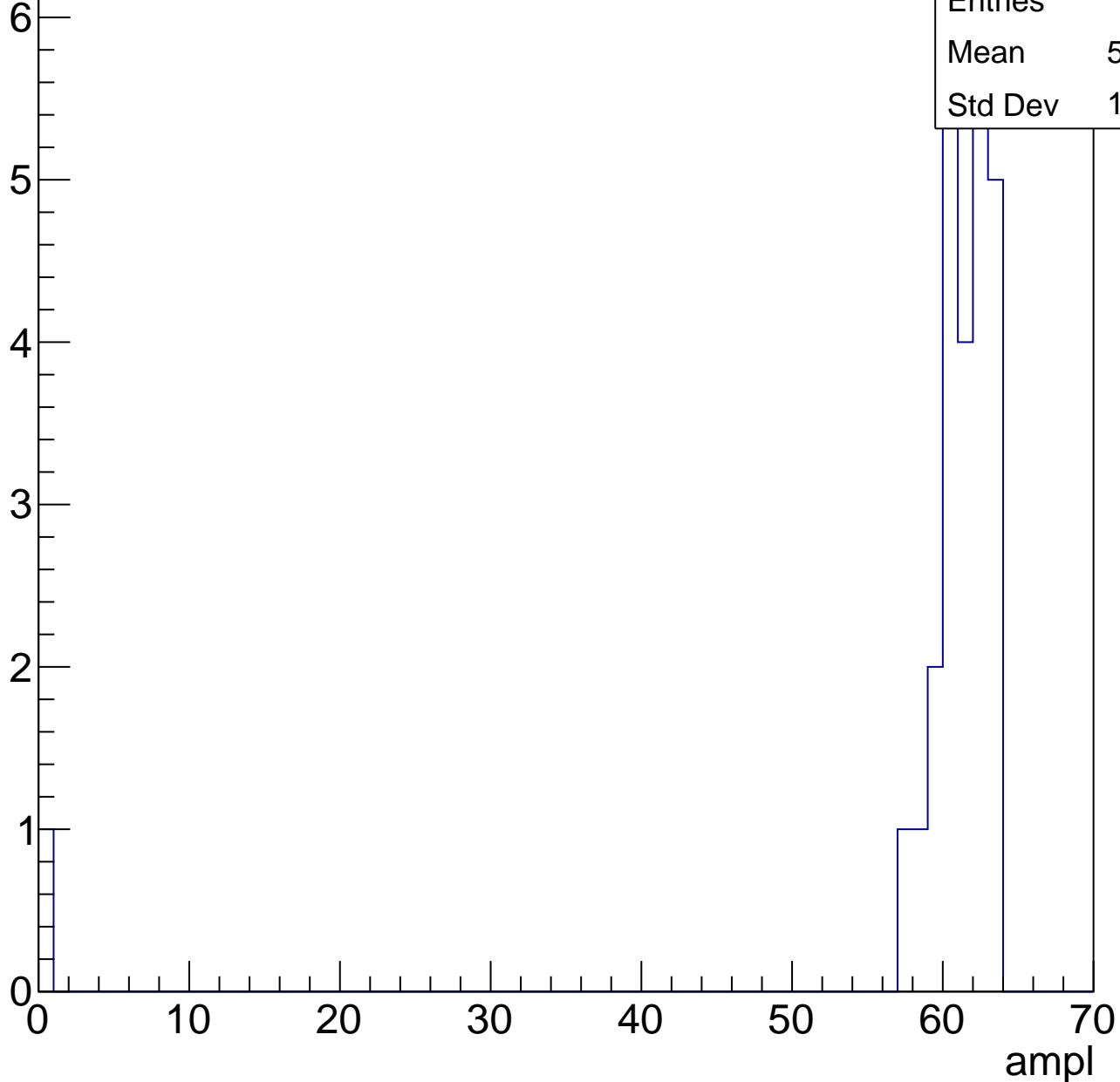
Entries	50
Mean	58.18
Std Dev	2.84



B1L101S, U25-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

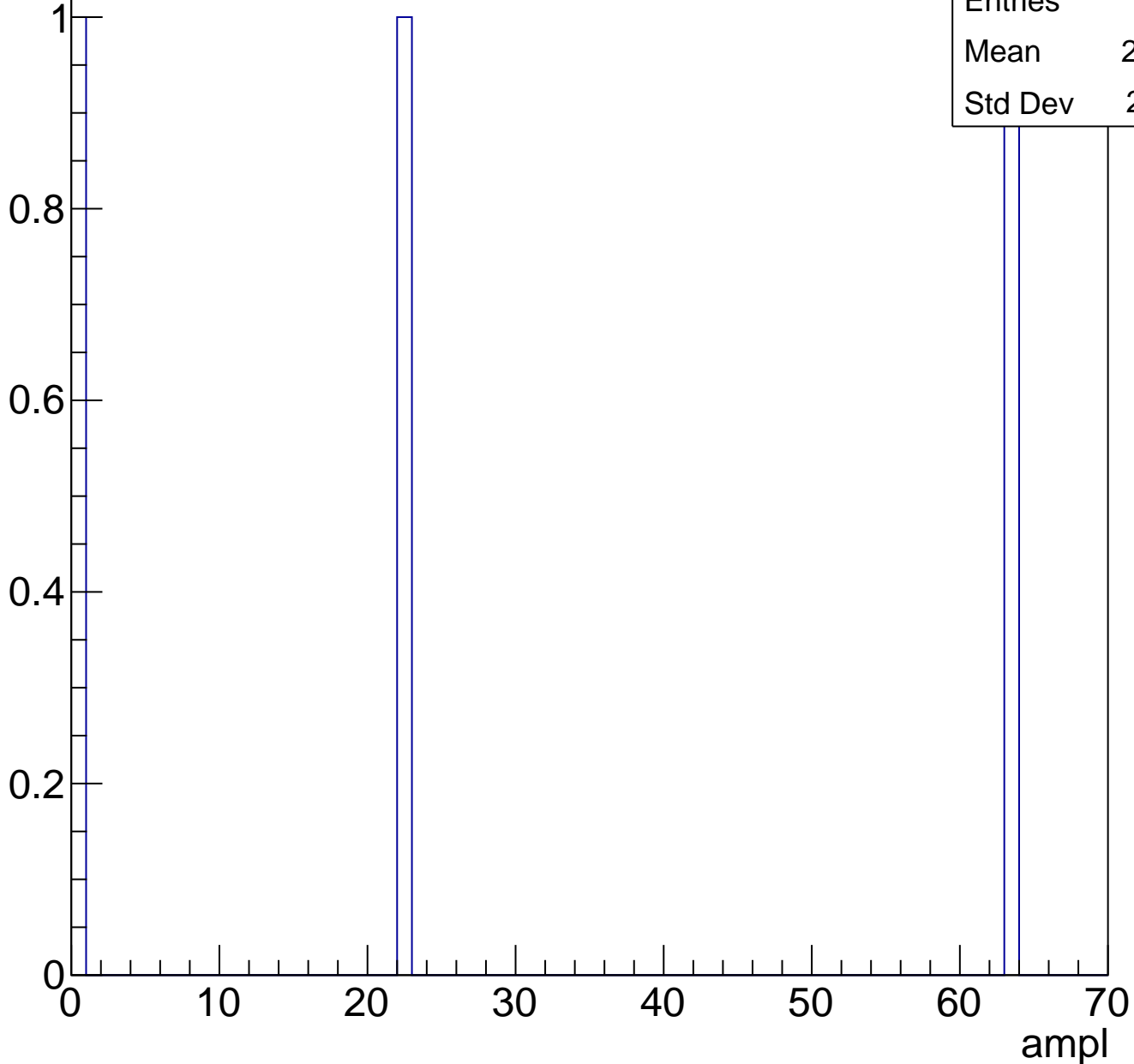
Entry



B1L101S, U25-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B1L101S, U25-ch112, adc0

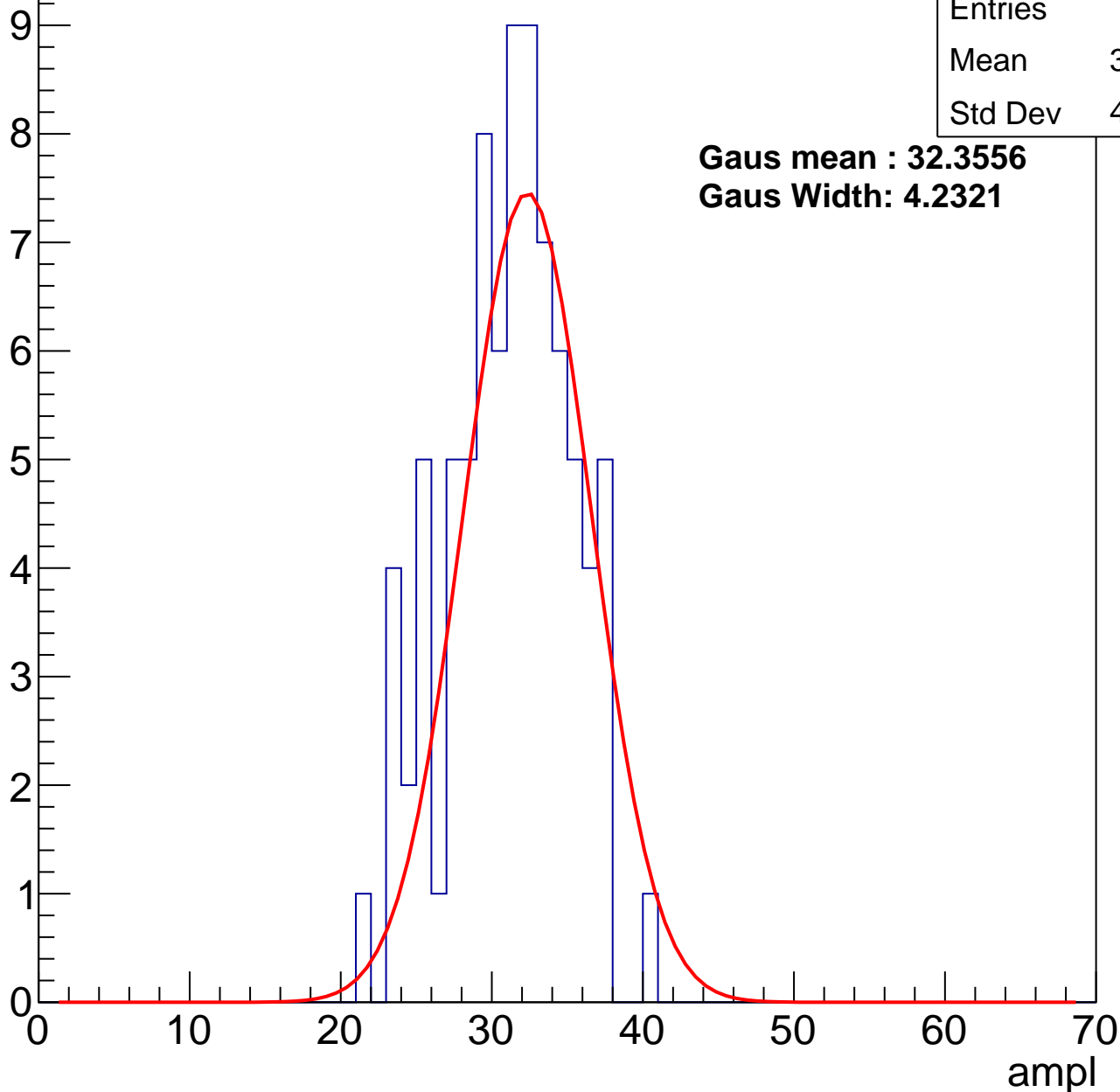
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	30.66
Std Dev	4.025

Gaus mean : 32.3556

Gaus Width: 4.2321



B1L101S, U25-ch112, adc1

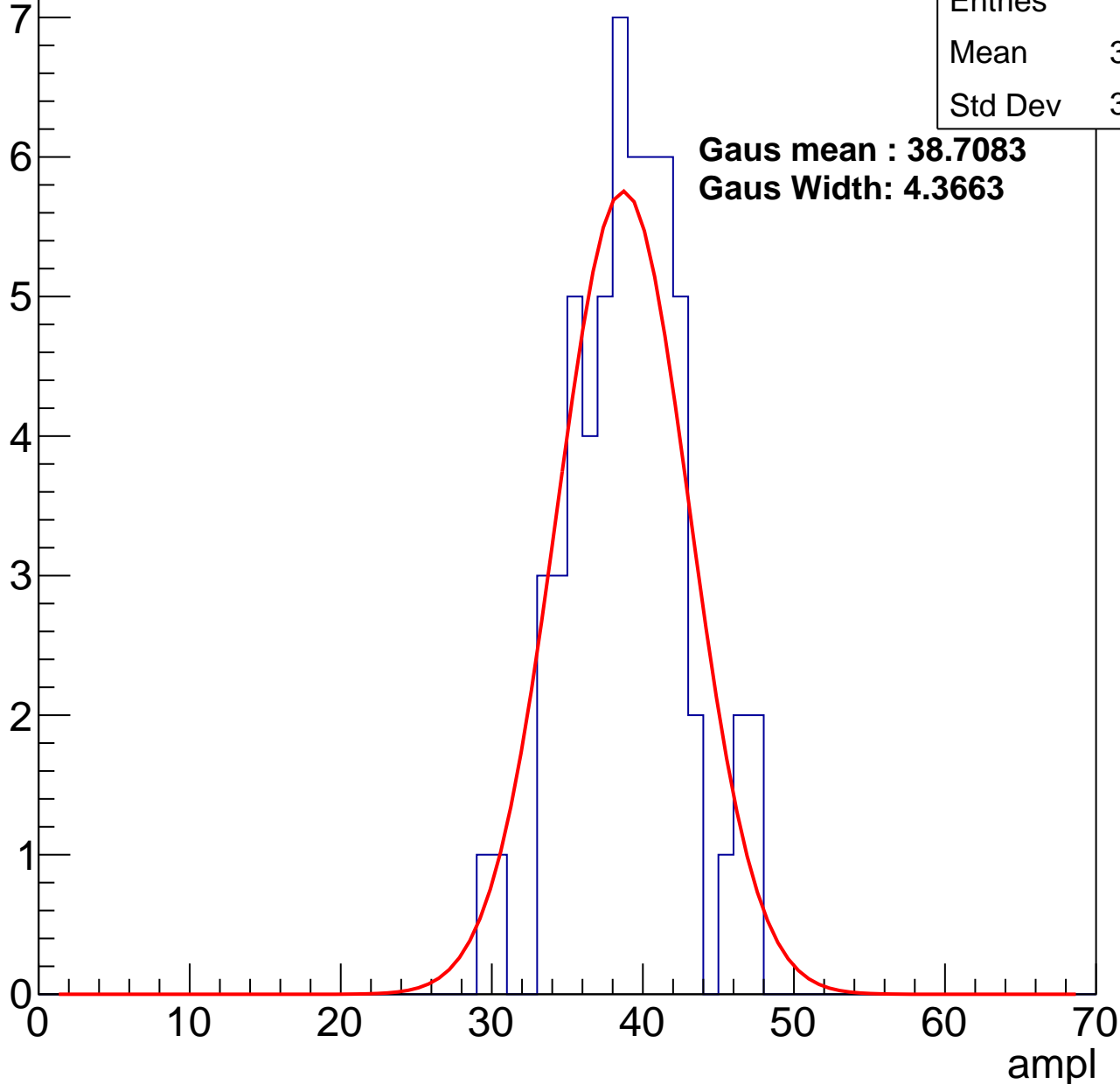
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	38.59
Std Dev	3.832

Gaus mean : 38.7083

Gaus Width: 4.3663



B1L101S, U25-ch112, adc2

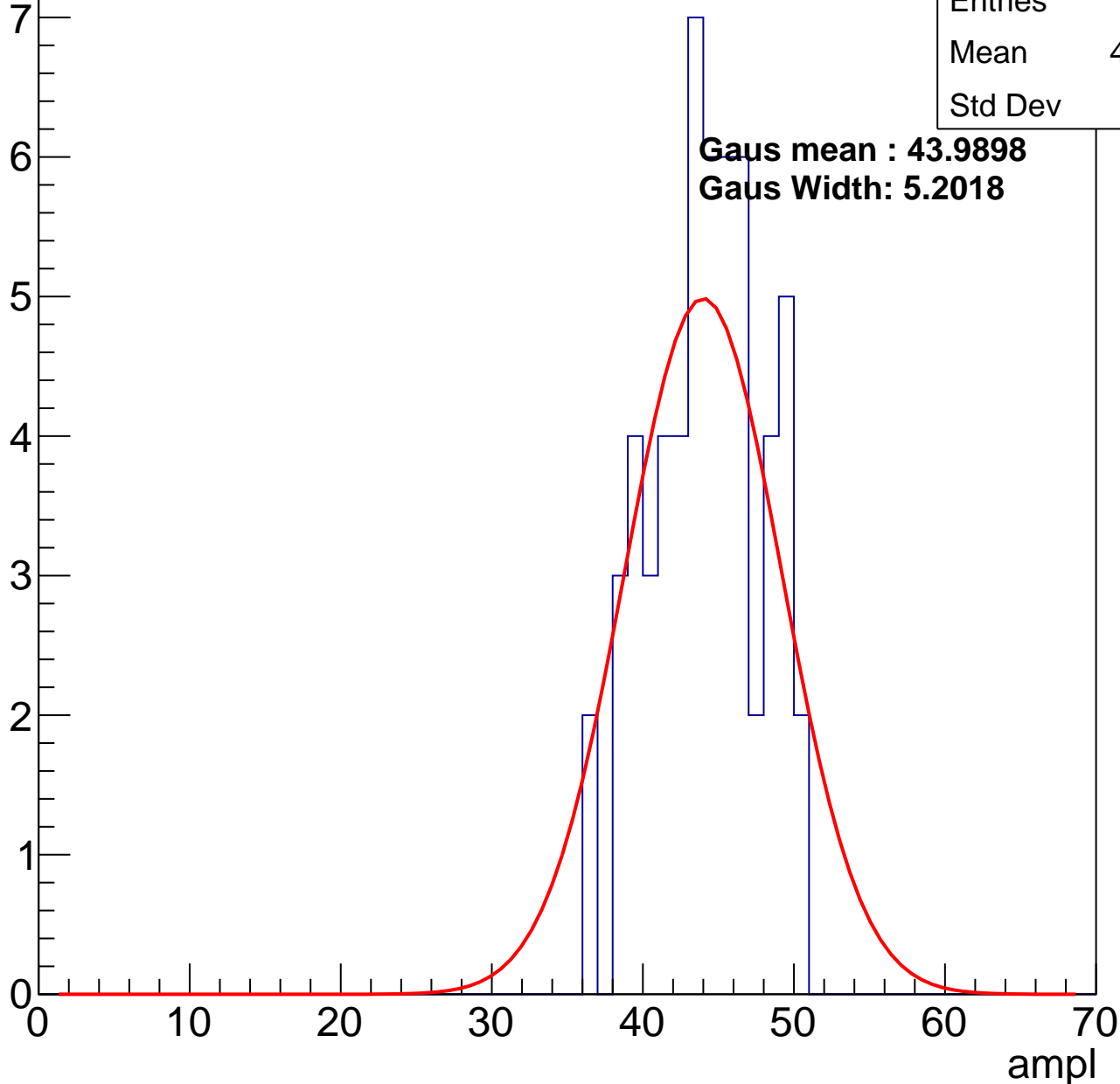
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.72
Std Dev	3.59

Gaus mean : 43.9898

Gaus Width: 5.2018

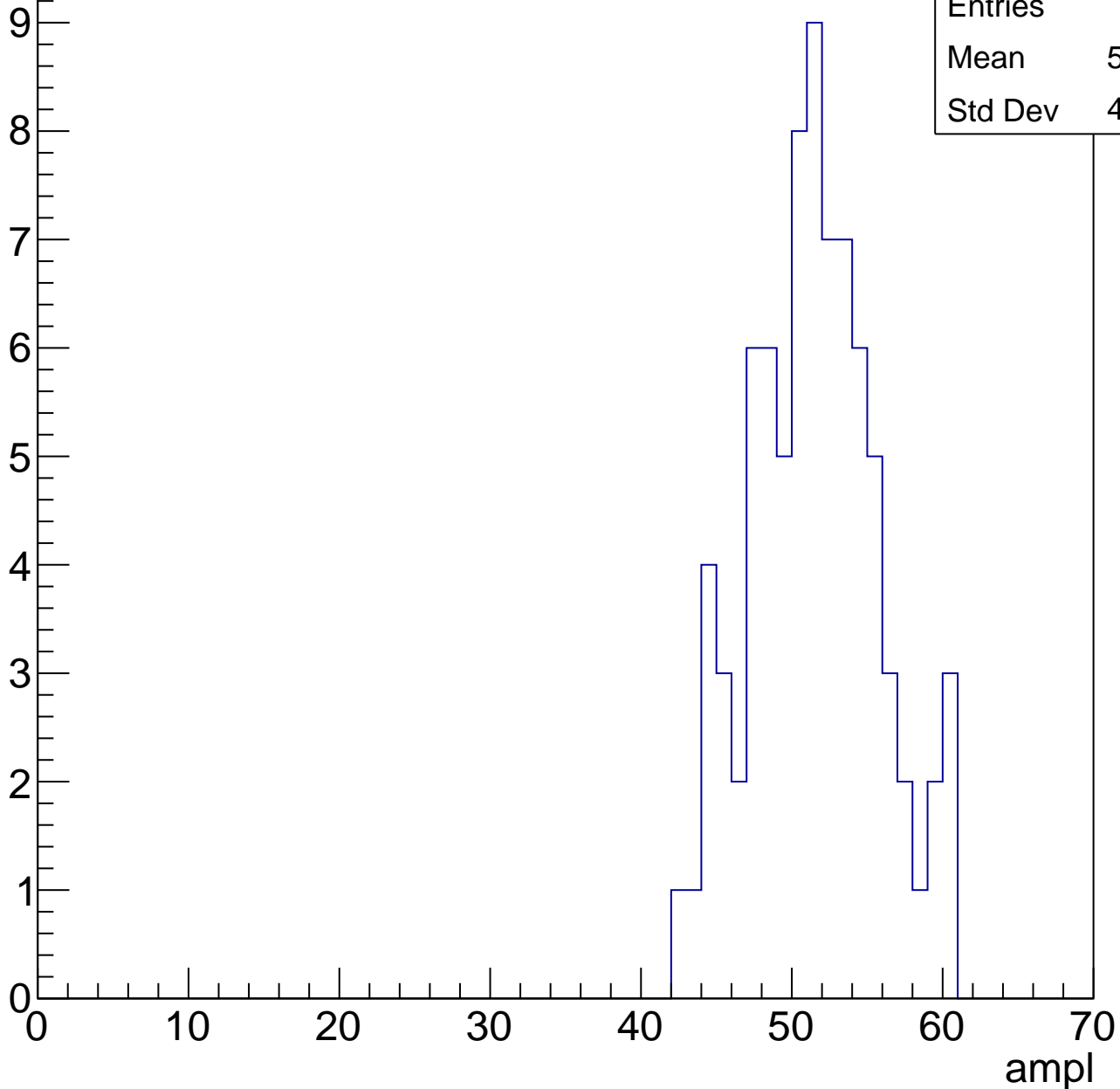


B1L101S, U25-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	51.04
Std Dev	4.176

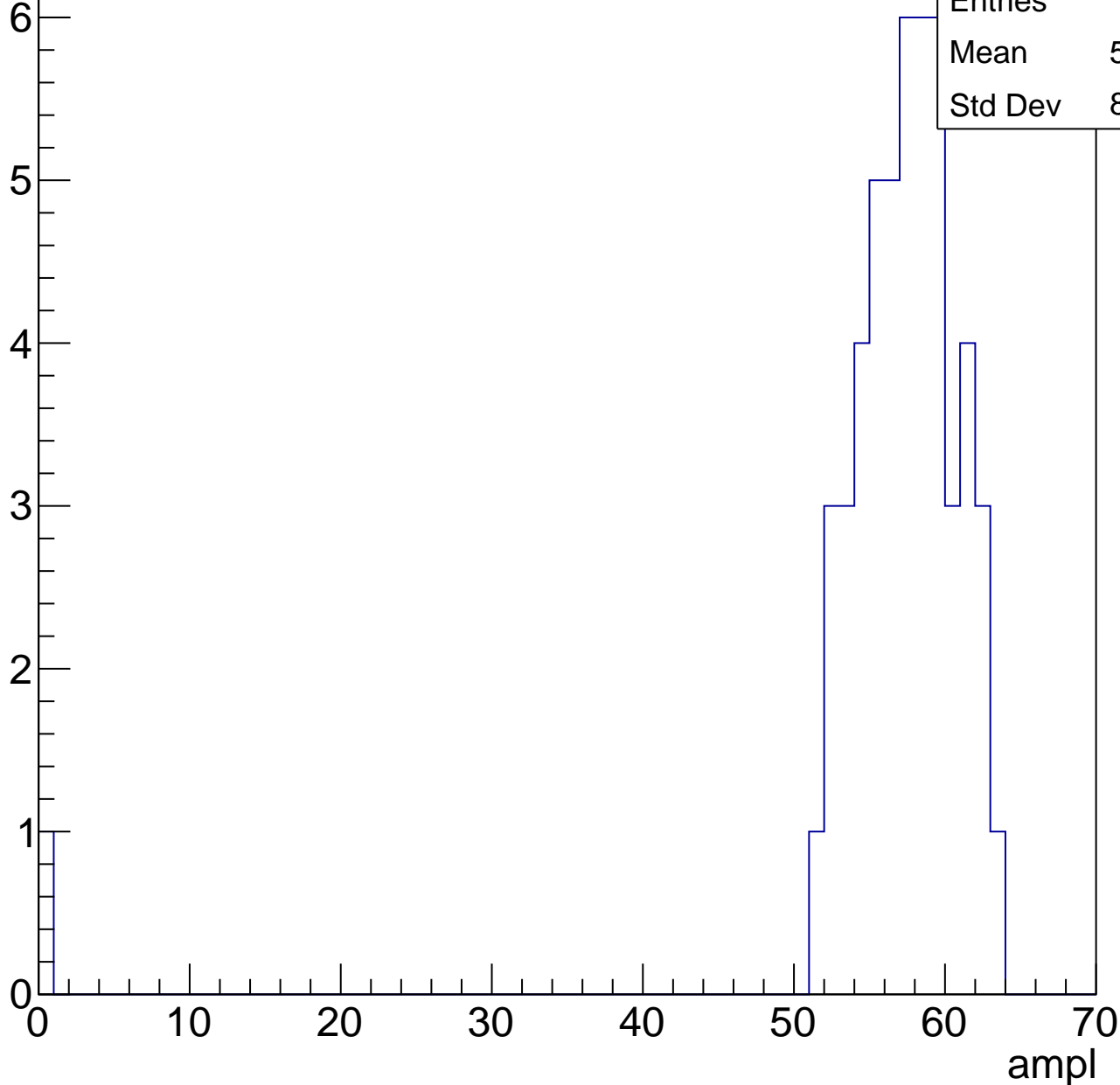


B1L101S, U25-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.96
Std Dev	8.455

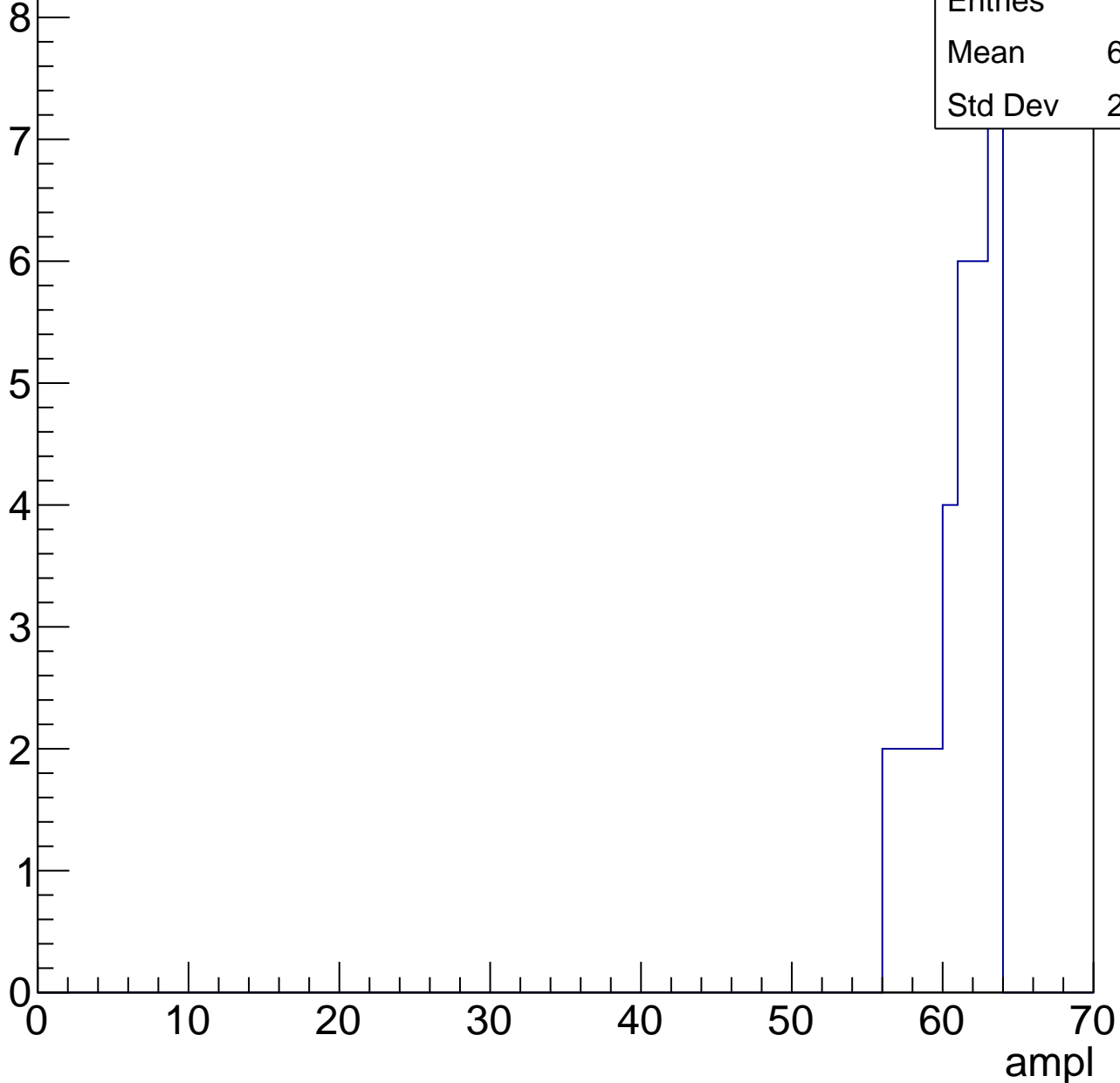


B1L101S, U25-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	60.69
Std Dev	2.142



B1L101S, U25-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

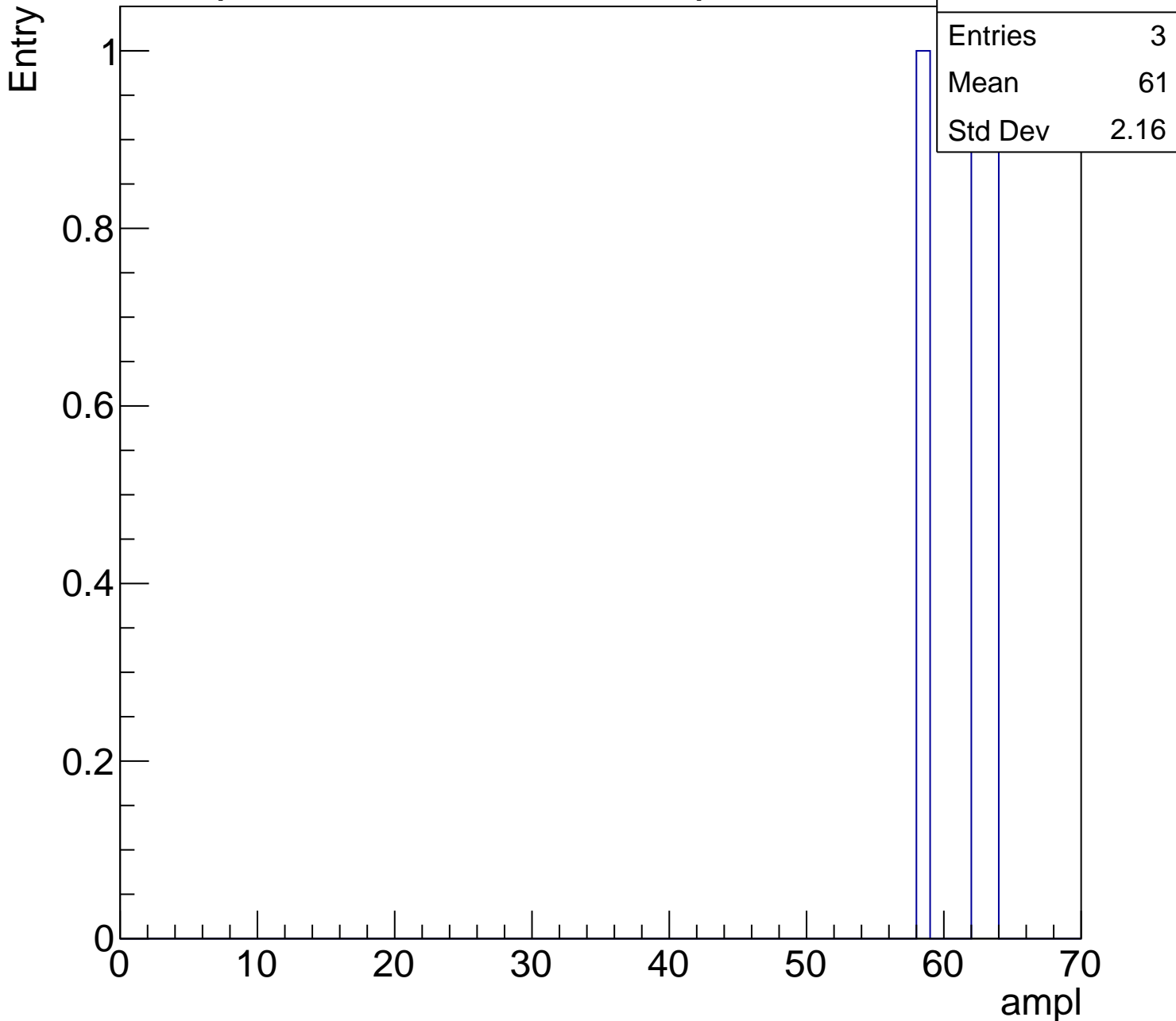
Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61
Std Dev	2.16

0 10 20 30 40 50 60 70

ampl



B1L101S, U25-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U25-ch113, adc0

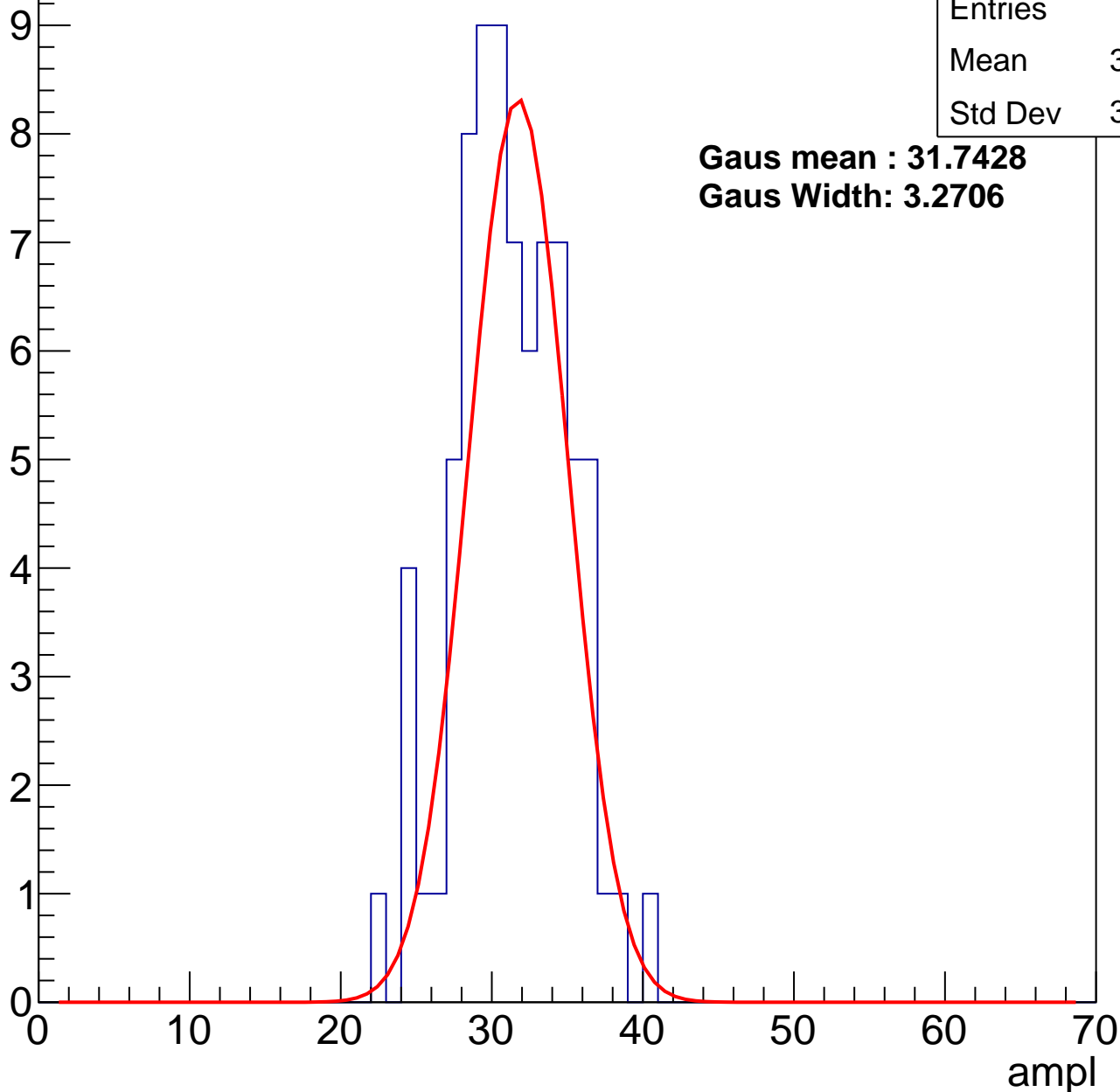
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.86
Std Dev	3.583

Gaus mean : 31.7428

Gaus Width: 3.2706



B1L101S, U25-ch113, adc1

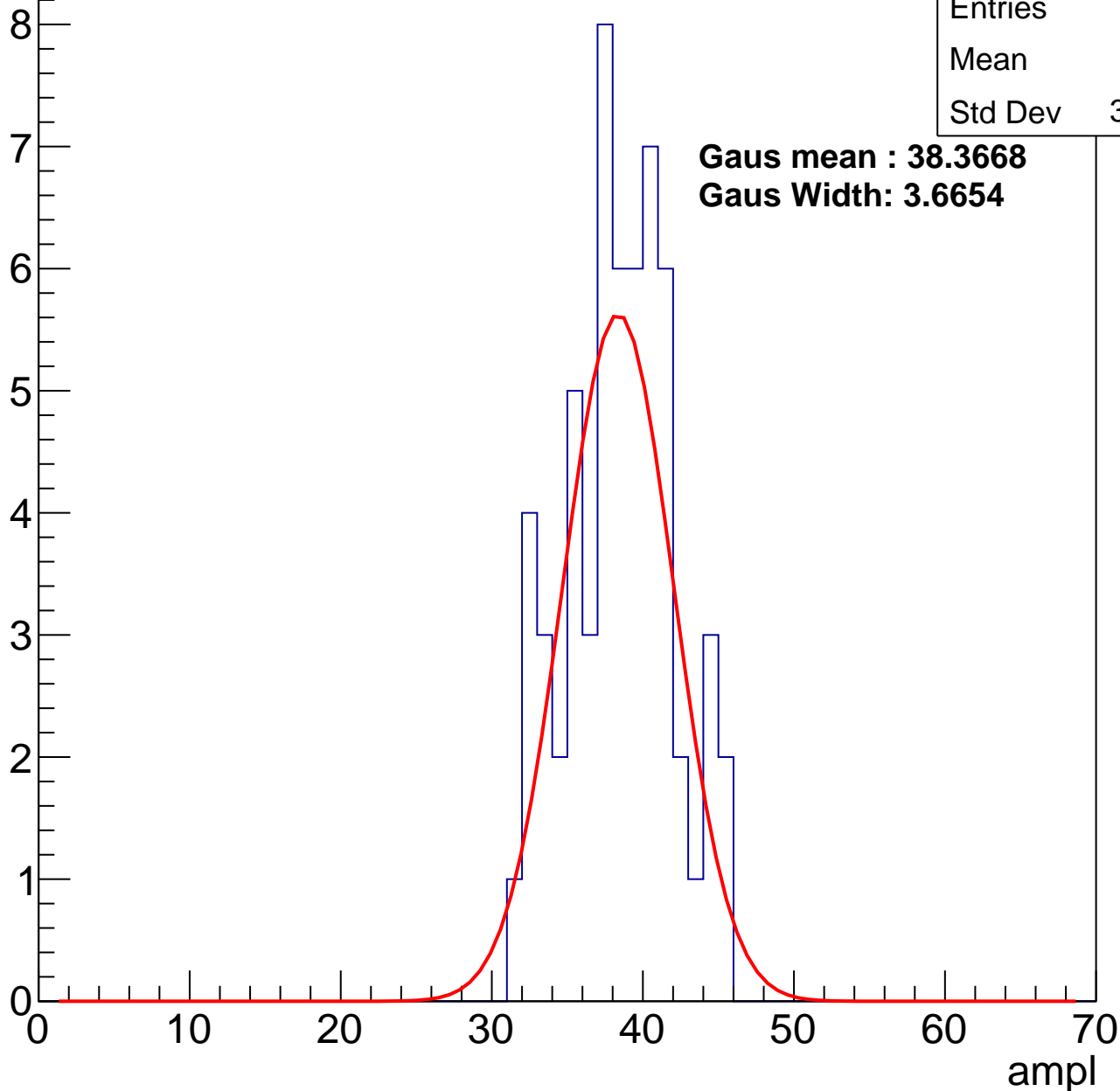
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	38
Std Dev	3.484

Gaus mean : 38.3668

Gaus Width: 3.6654



B1L101S, U25-ch113, adc2

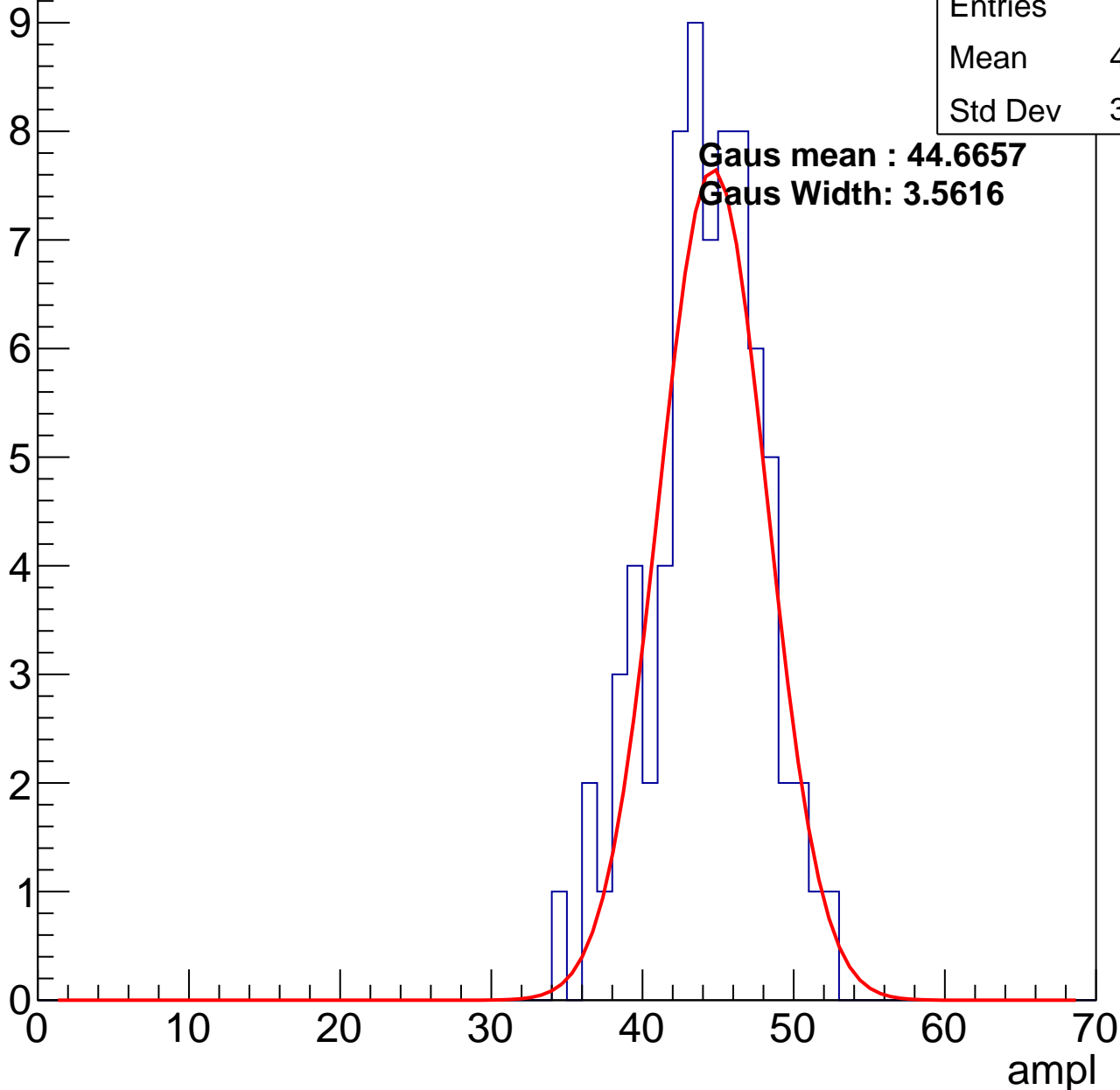
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.77
Std Dev	3.674

Gaus mean : 44.6657

Gaus Width: 3.5616

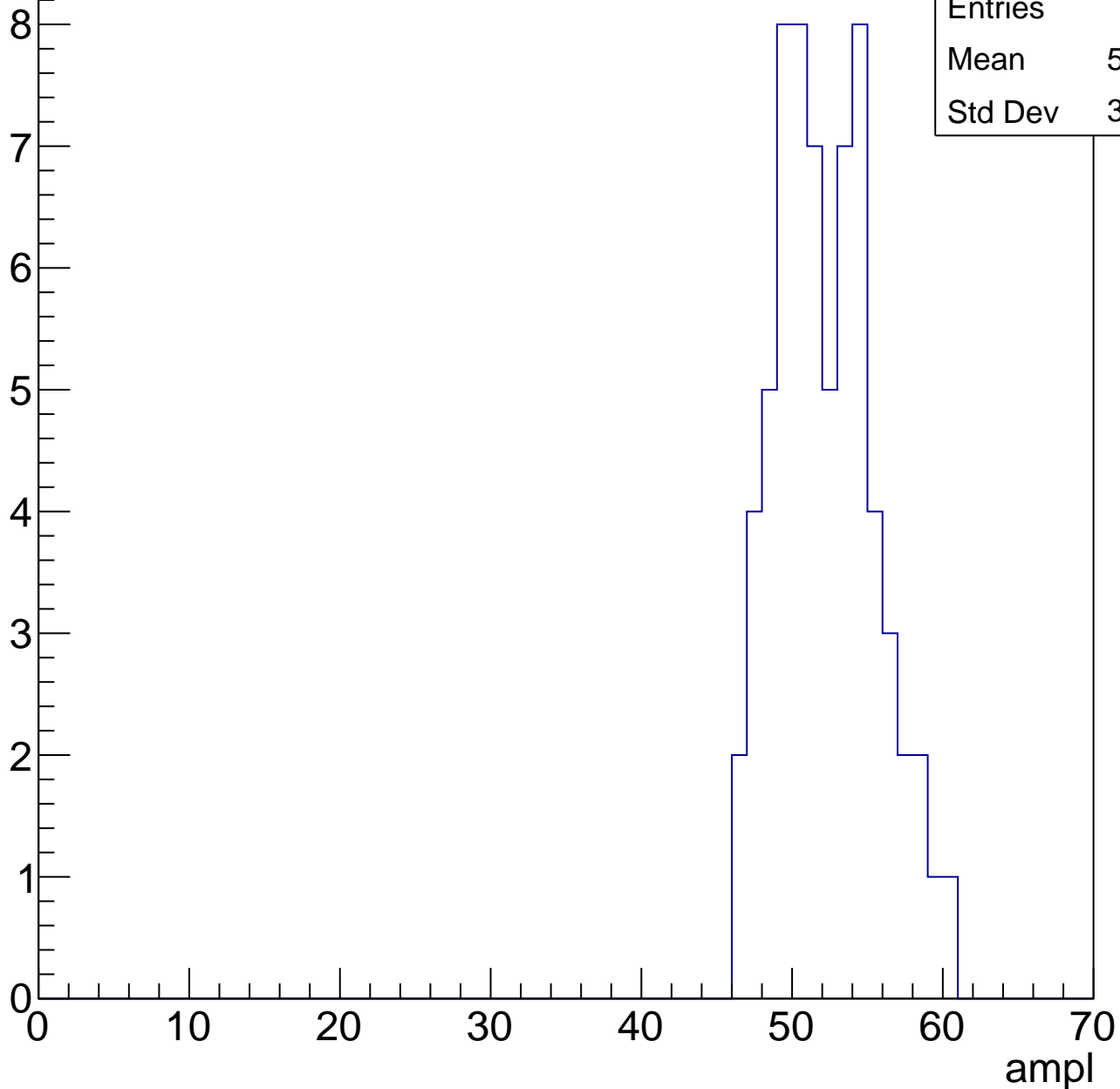


B1L101S, U25-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.78
Std Dev	3.273

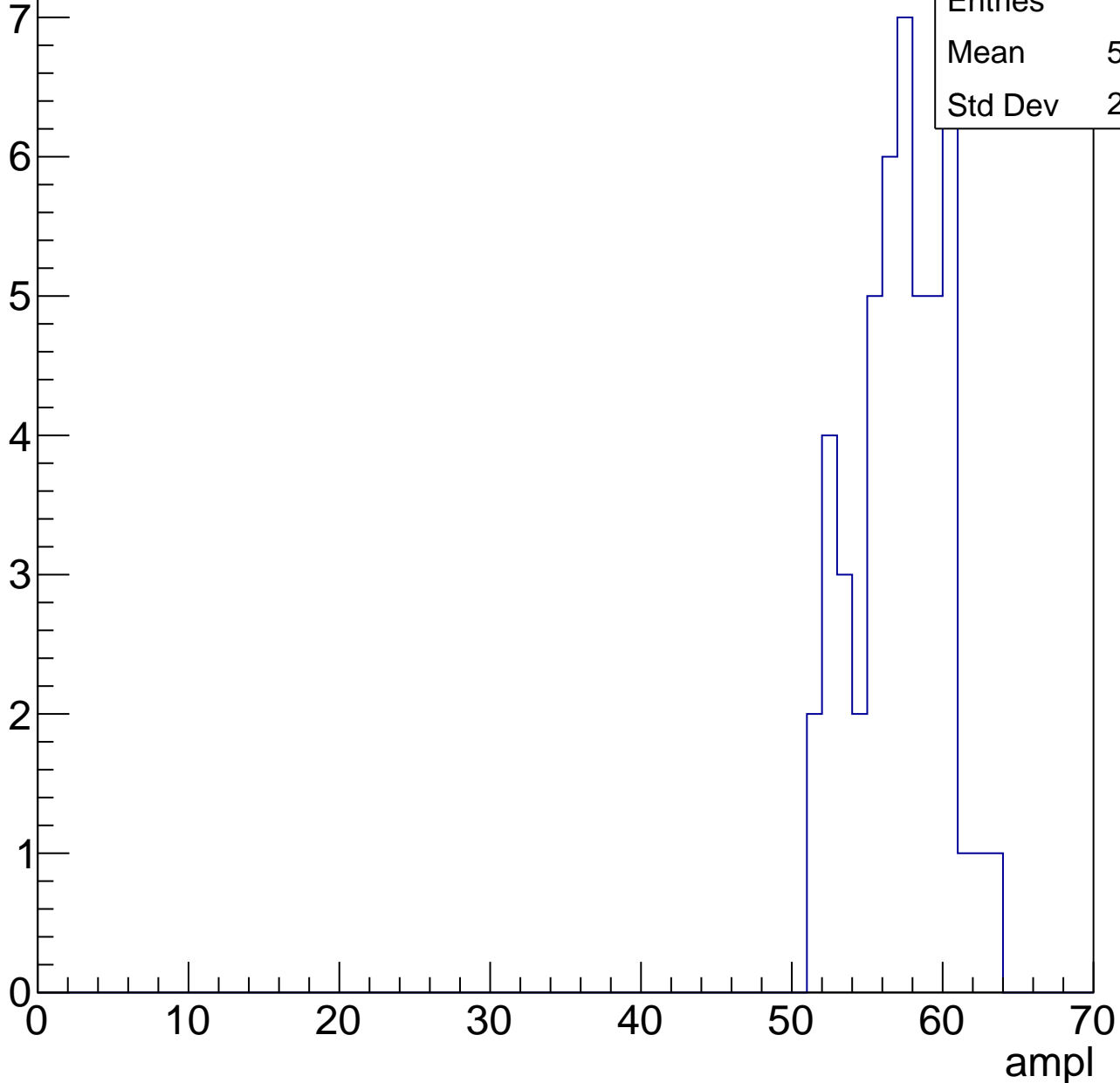


B1L101S, U25-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.69
Std Dev	2.943

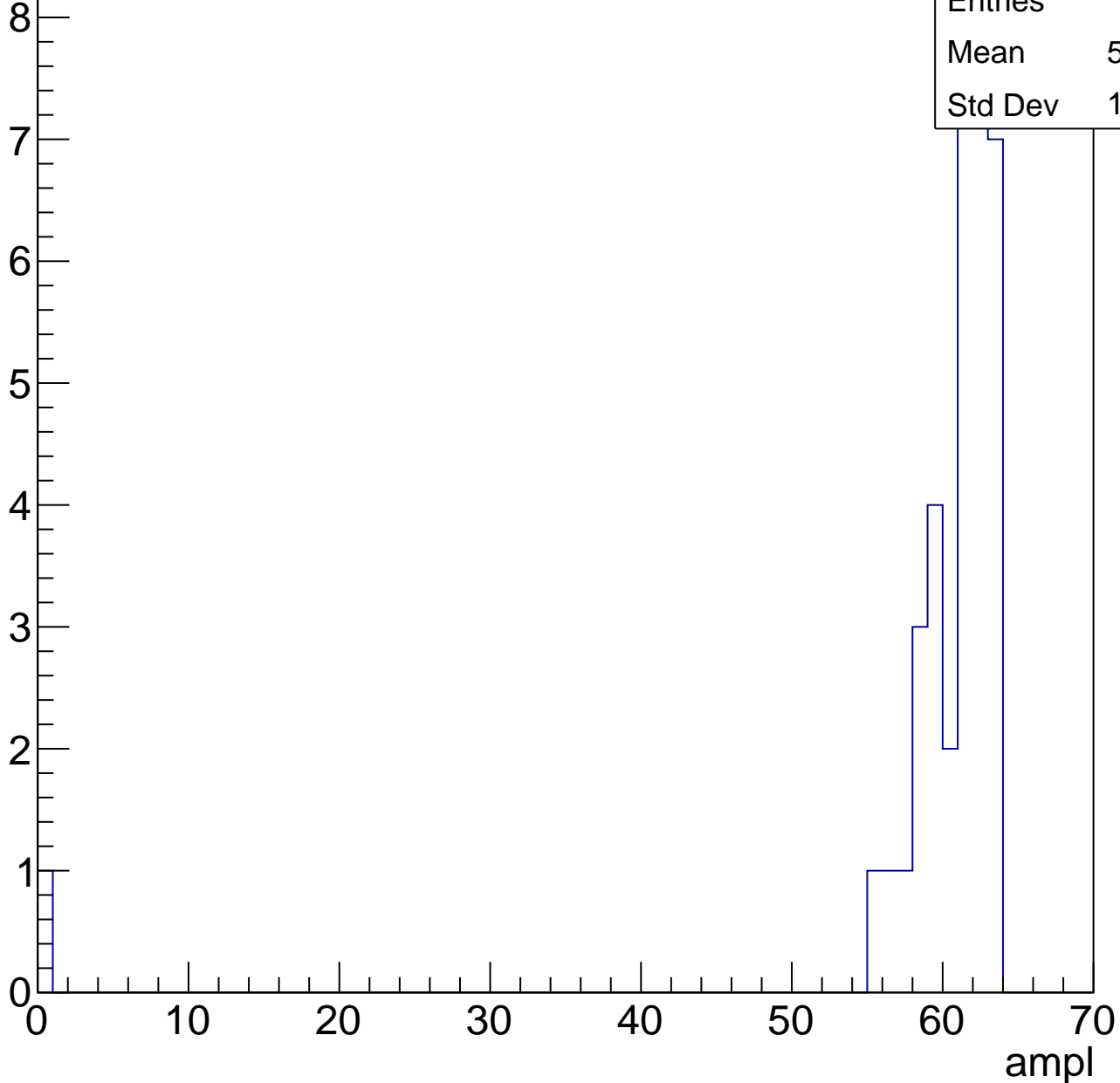


B1L101S, U25-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.97
Std Dev	10.18



B1L101S, U25-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch114, adc0

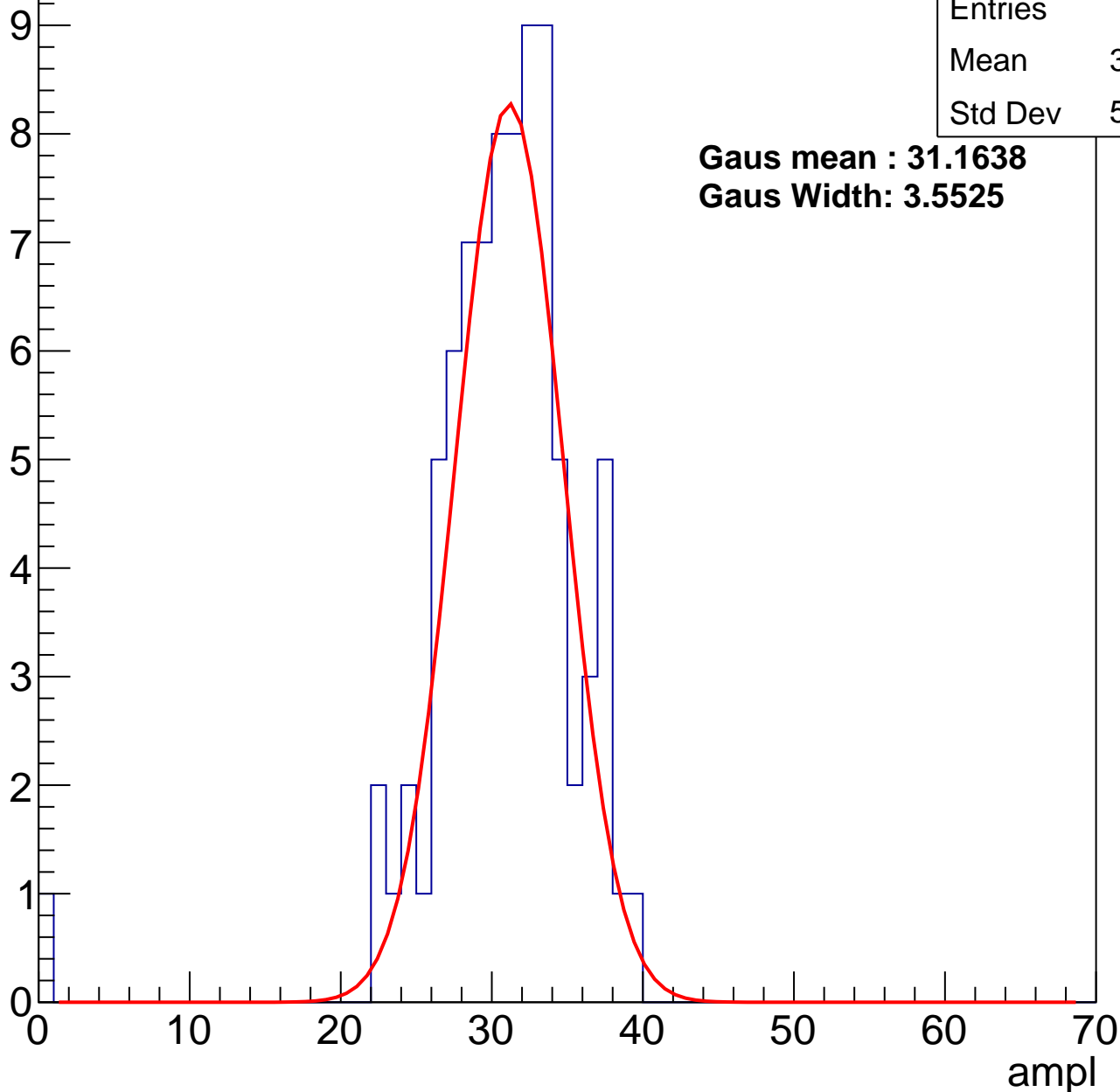
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	30.29
Std Dev	5.017

Gaus mean : 31.1638

Gaus Width: 3.5525



B1L101S, U25-ch114, adc1

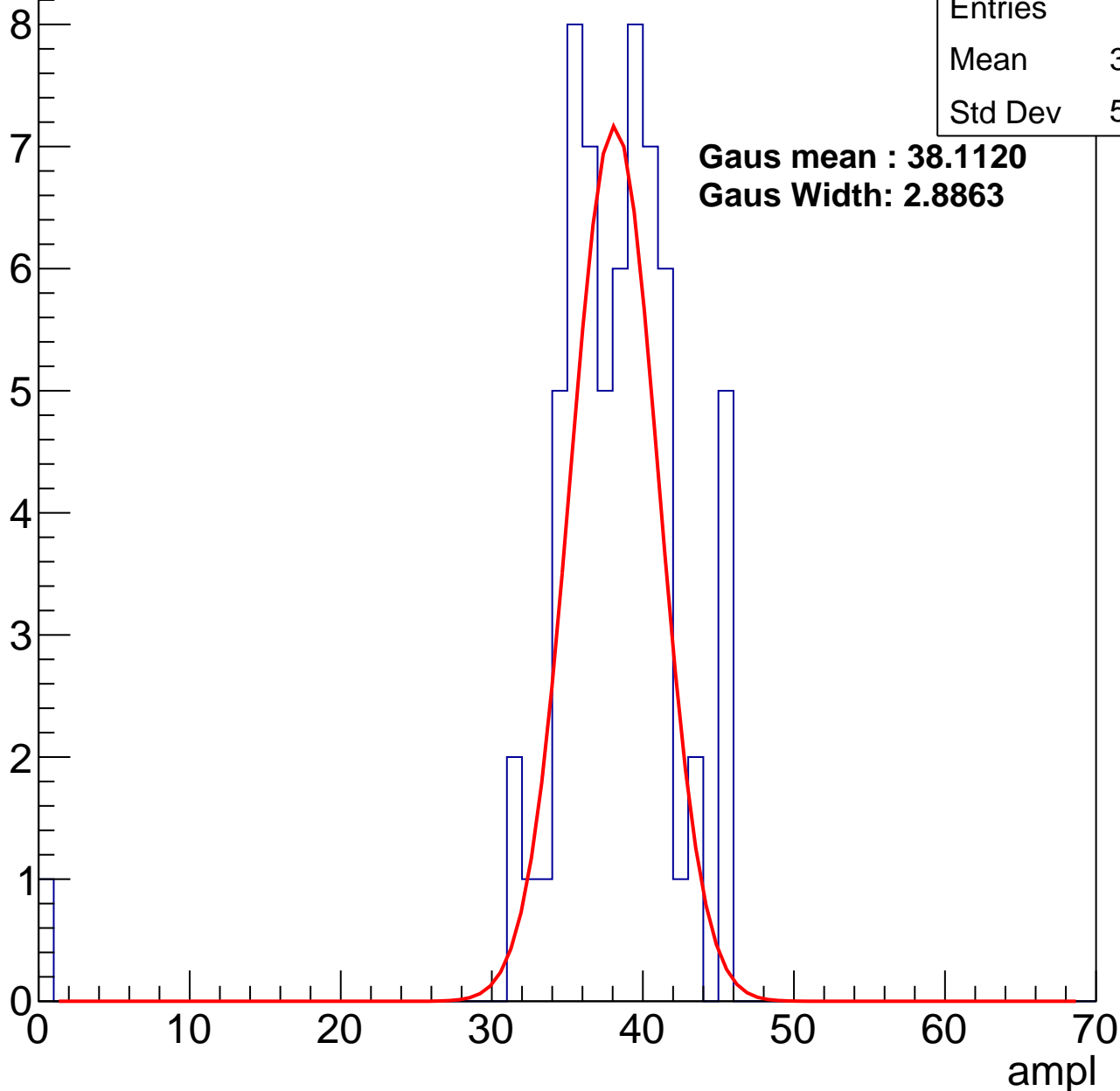
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.43
Std Dev	5.775

Gaus mean : 38.1120

Gaus Width: 2.8863



B1L101S, U25-ch114, adc2

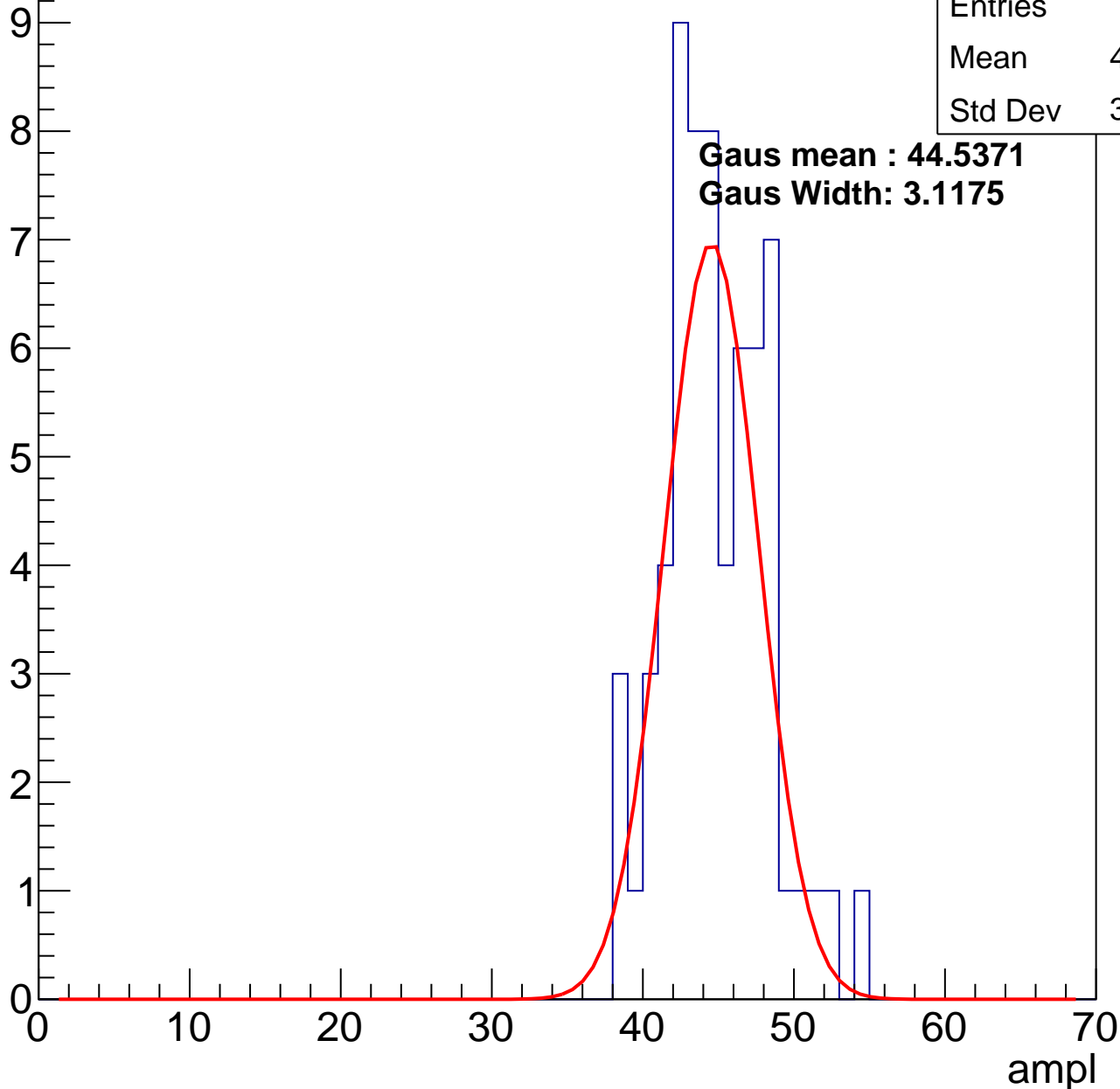
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.39
Std Dev	3.352

Gaus mean : 44.5371

Gaus Width: 3.1175

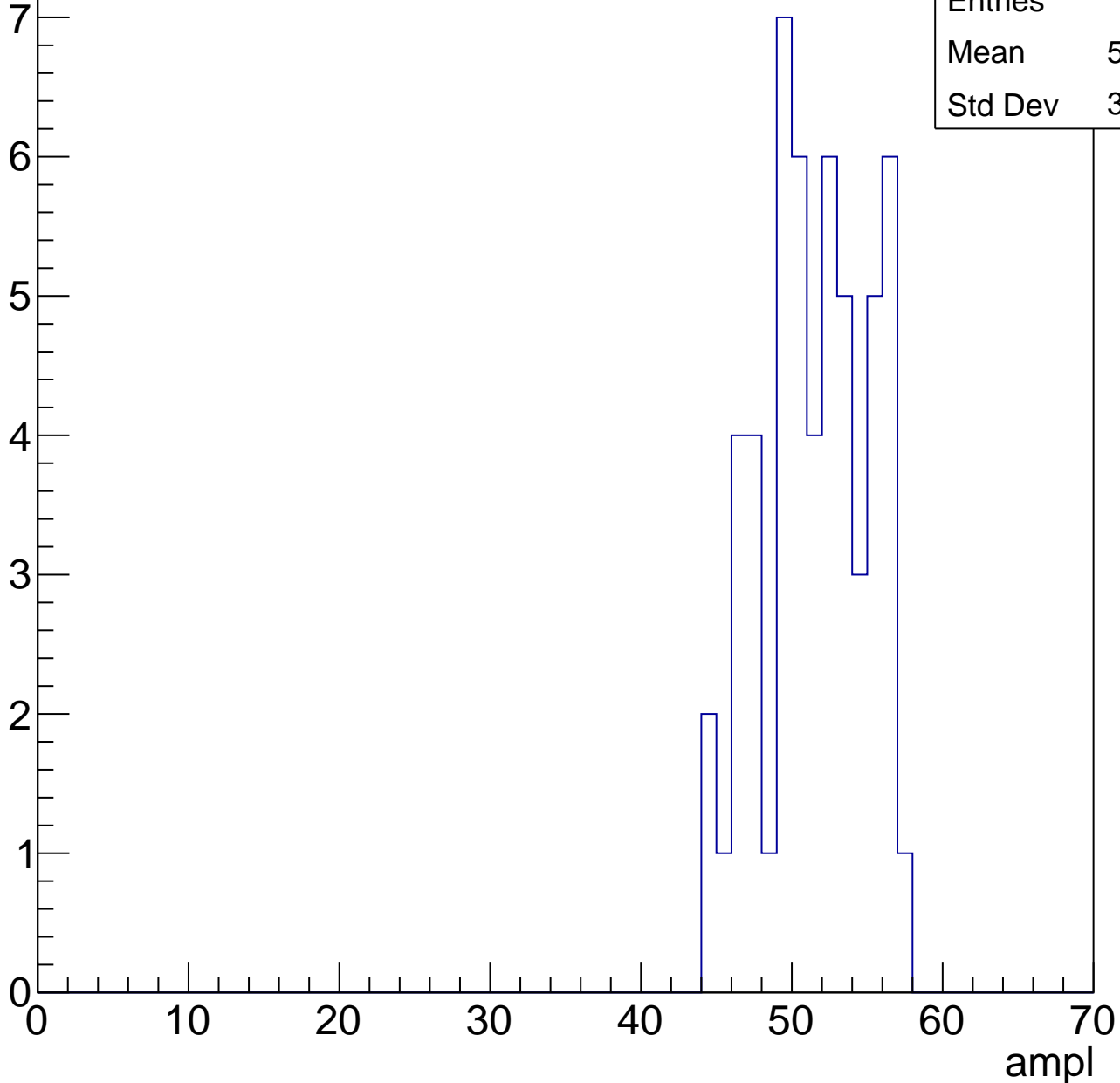


B1L101S, U25-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	51.04
Std Dev	3.464

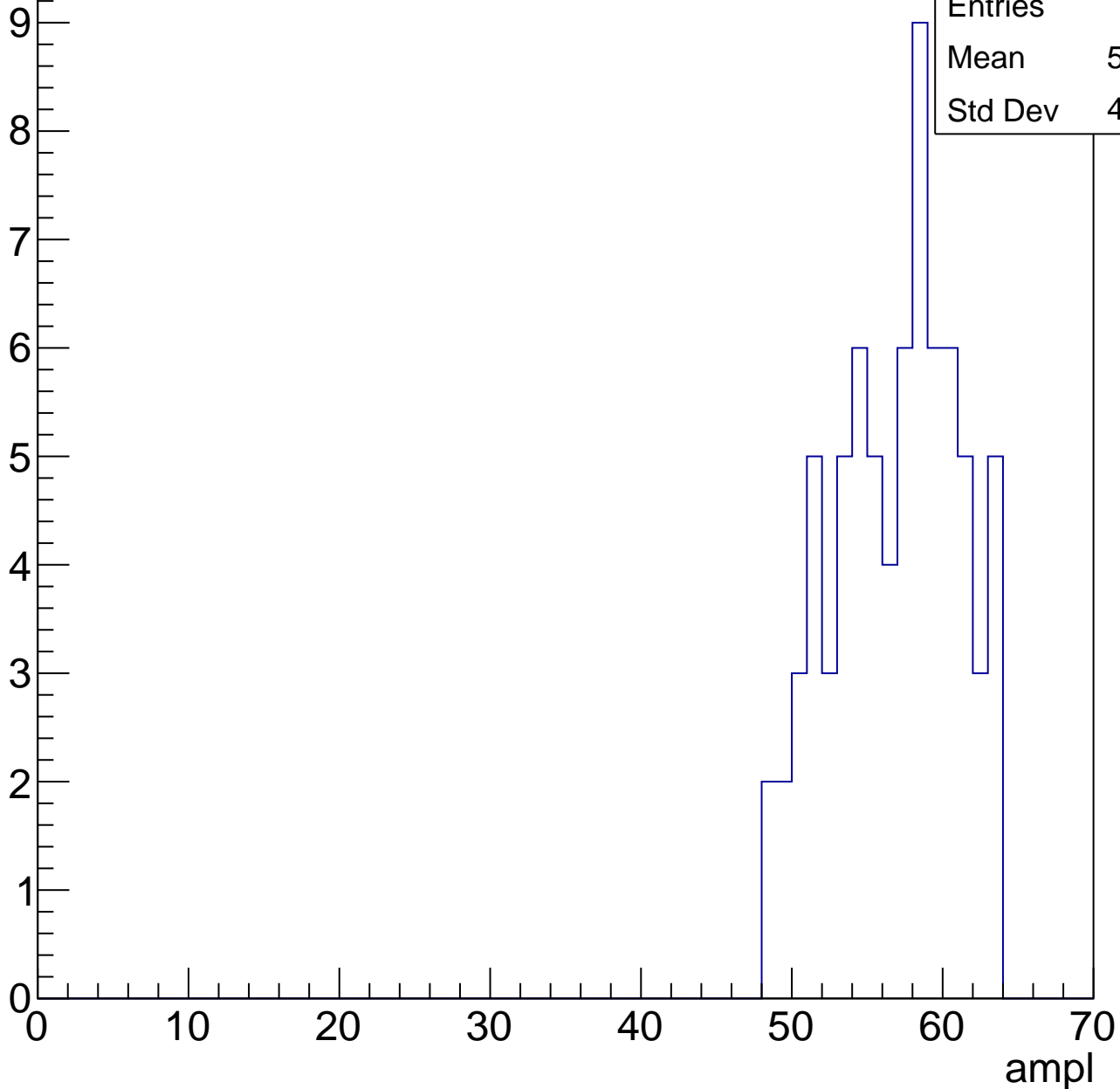


B1L101S, U25-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	56.36
Std Dev	4.068

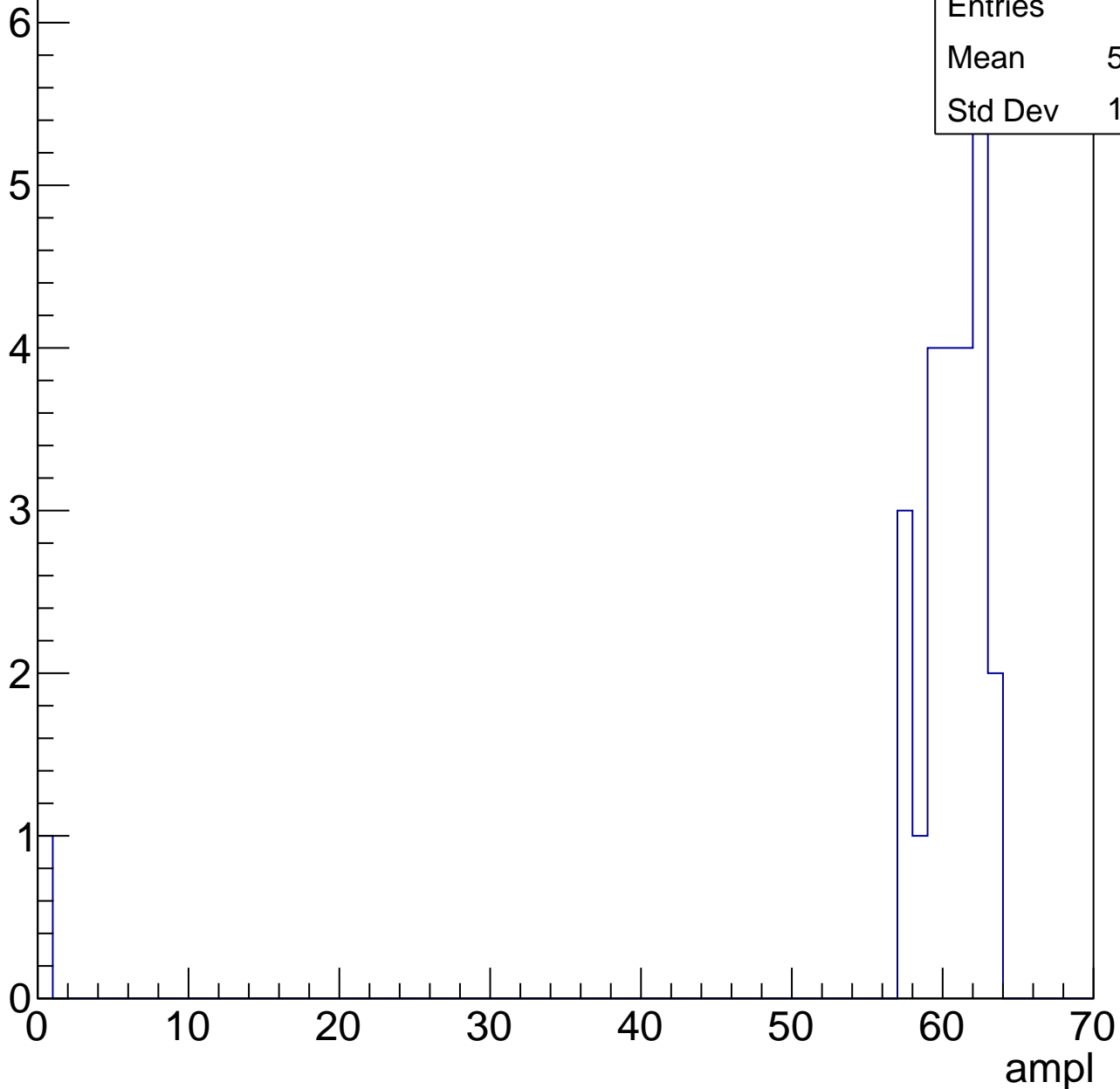


B1L101S, U25-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	57.88
Std Dev	11.95



B1L101S, U25-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch115, adc0

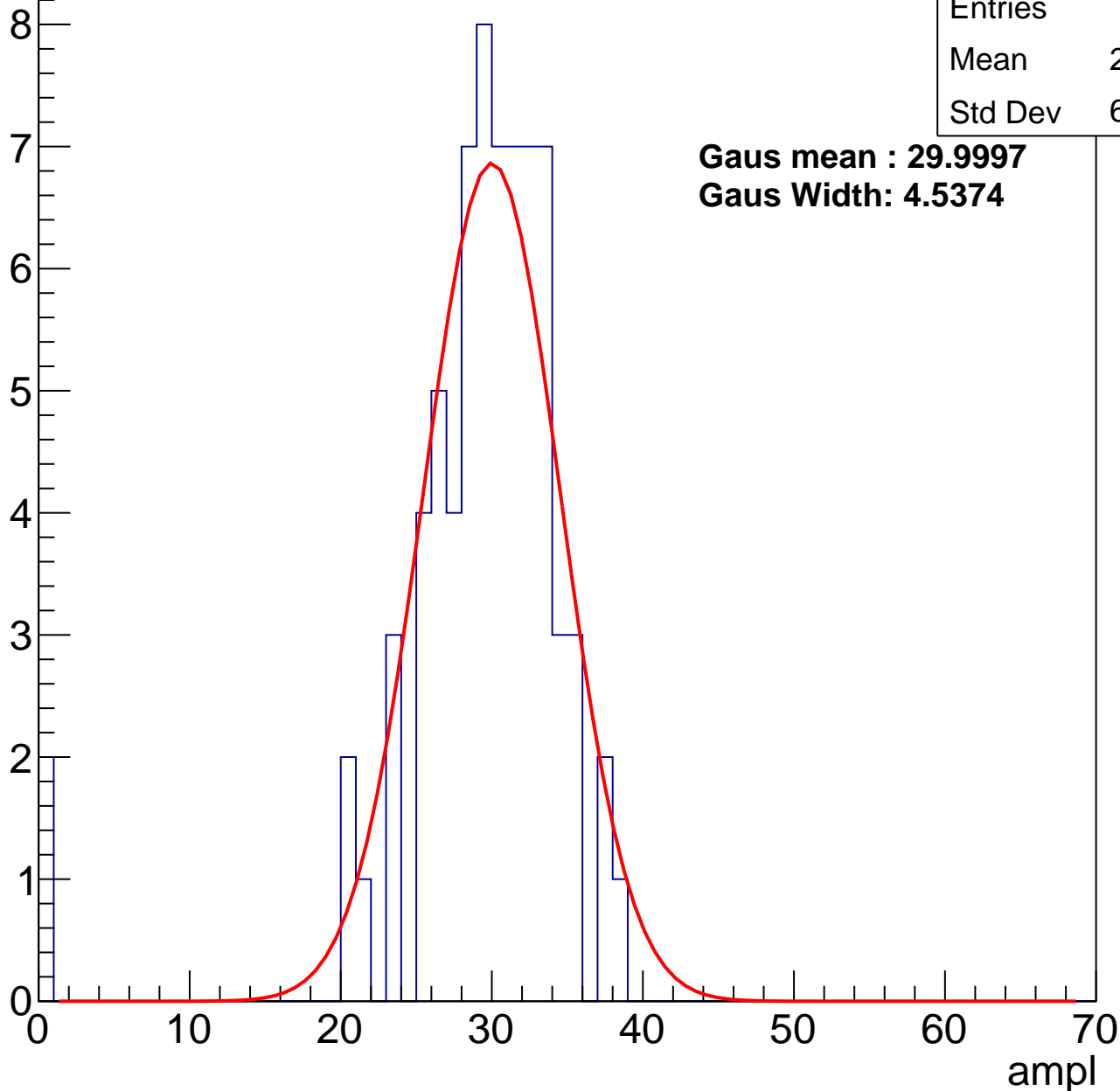
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.73
Std Dev	6.132

Gaus mean : 29.9997

Gaus Width: 4.5374



B1L101S, U25-ch115, adc1

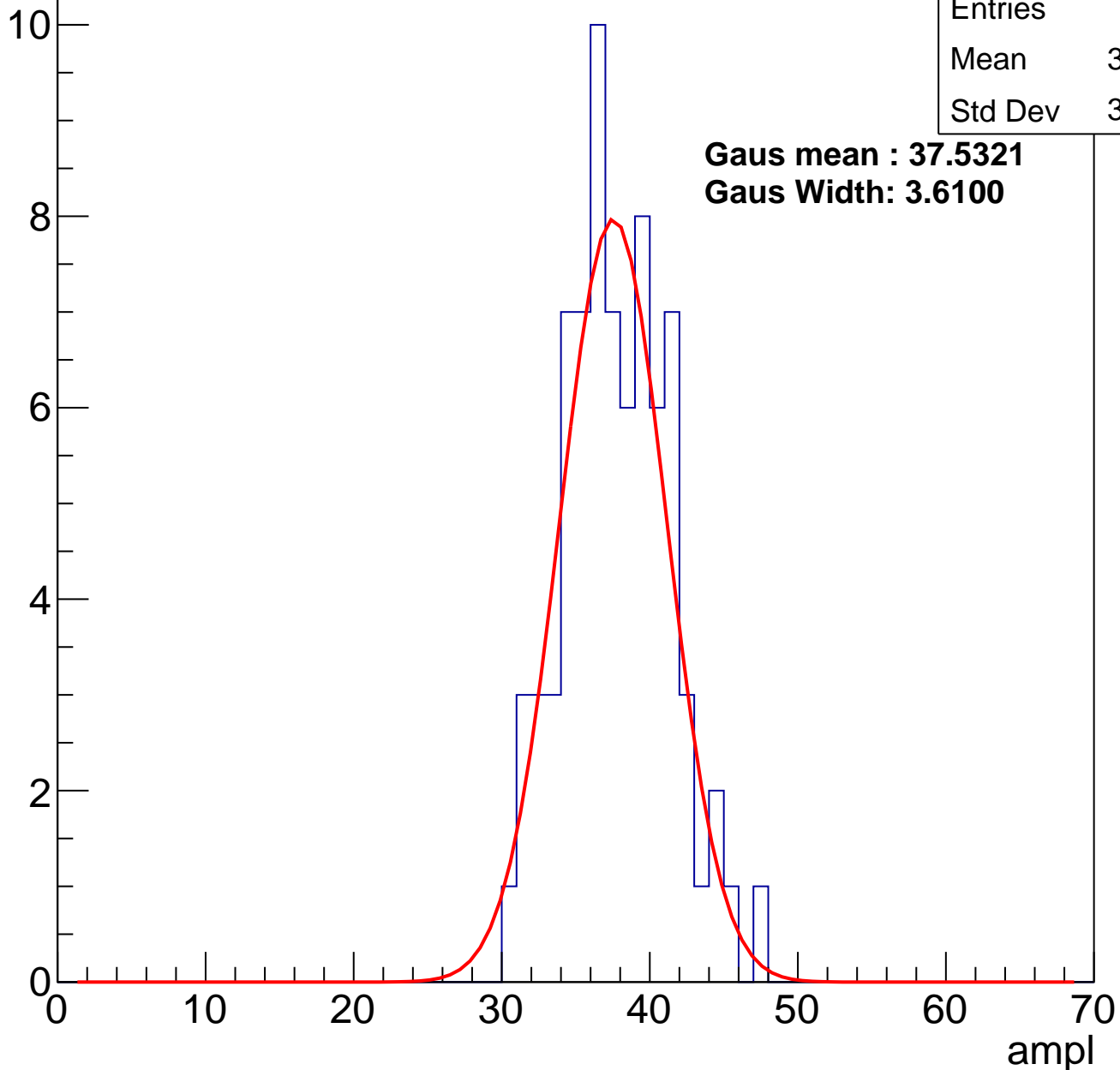
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	37.32
Std Dev	3.536

Gaus mean : 37.5321

Gaus Width: 3.6100

Entry



B1L101S, U25-ch115, adc2

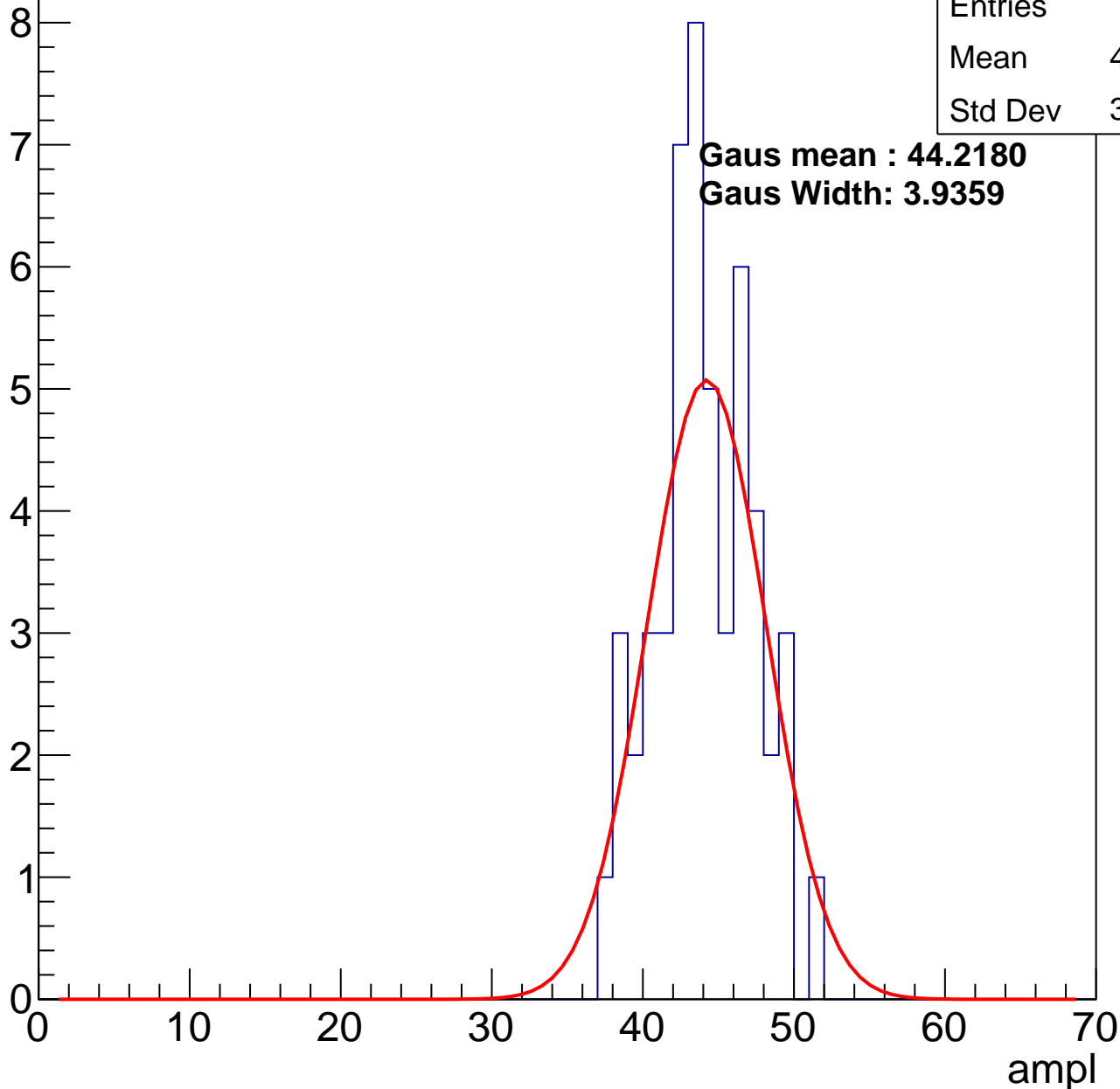
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	43.59
Std Dev	3.219

Gaus mean : 44.2180

Gaus Width: 3.9359

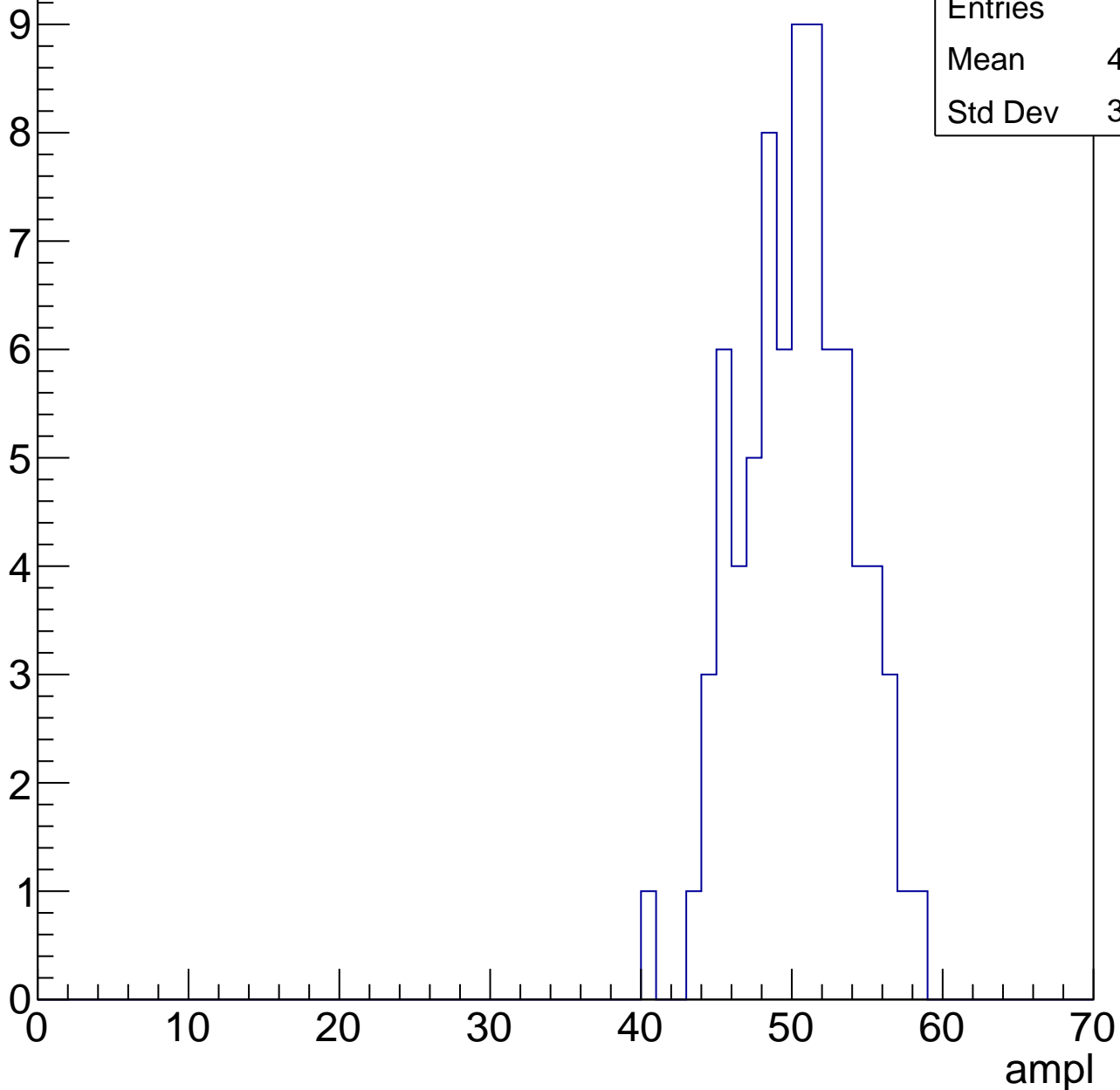


B1L101S, U25-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	49.87
Std Dev	3.648

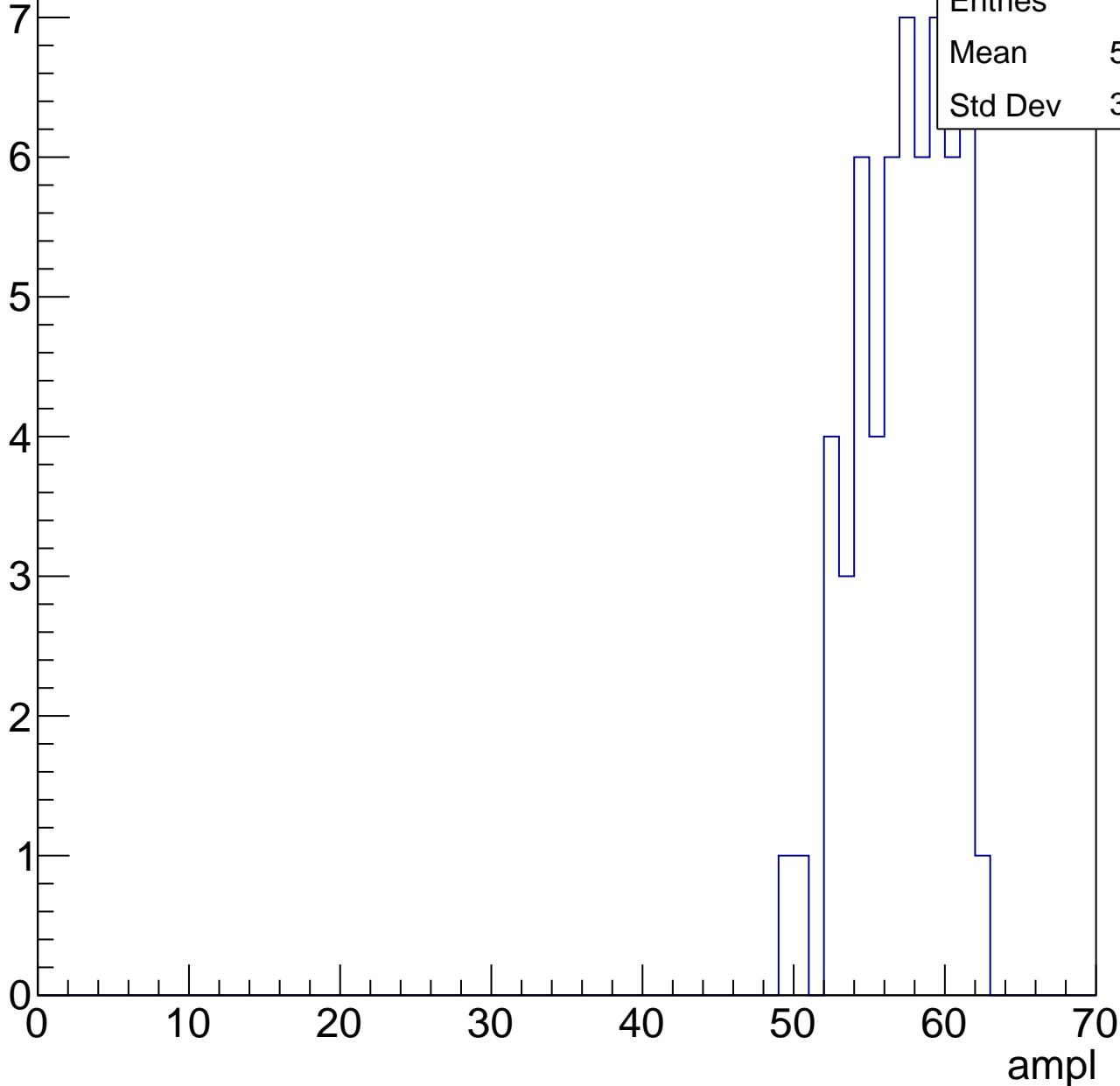


B1L101S, U25-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.86
Std Dev	3.083

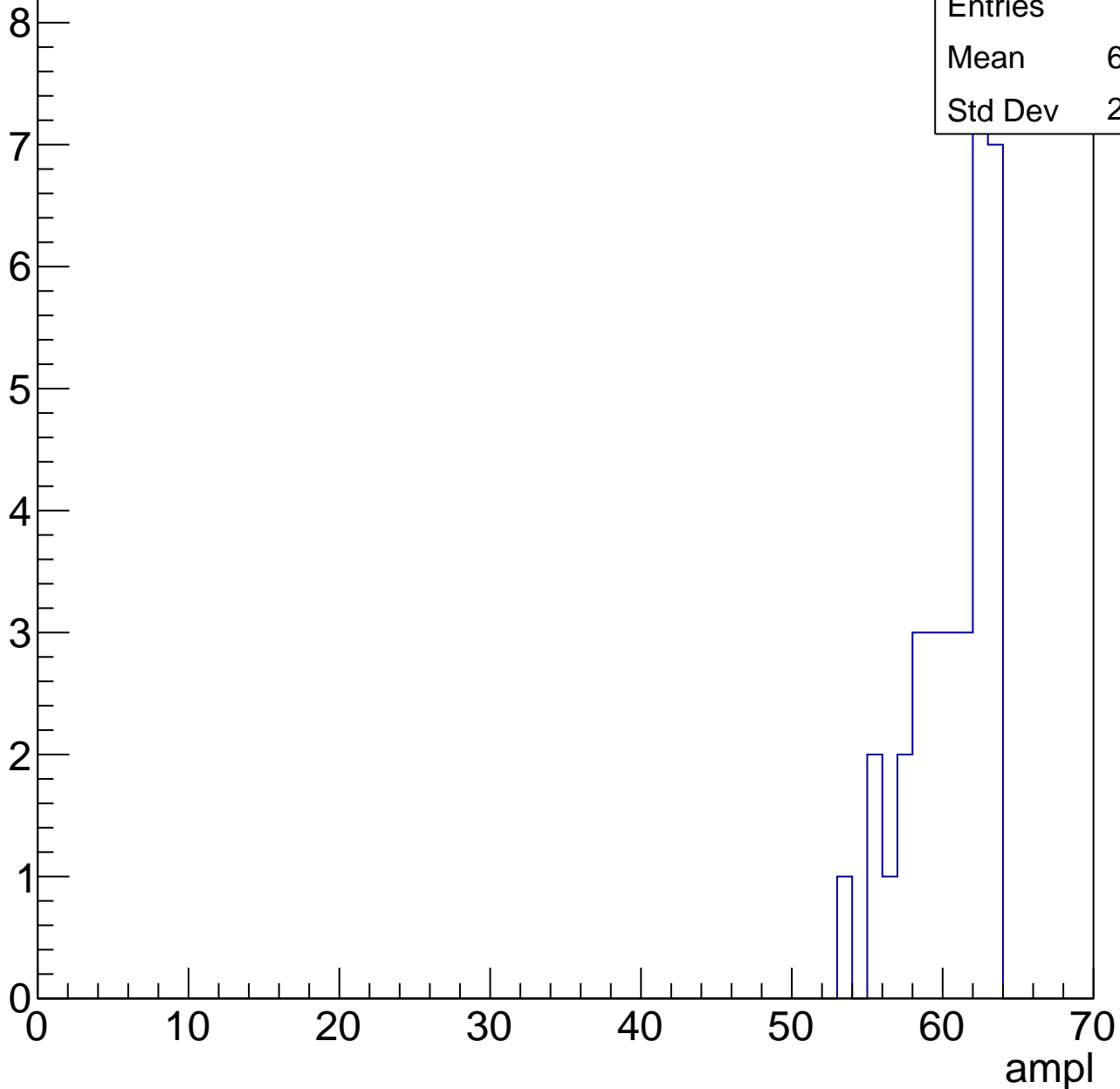


B1L101S, U25-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

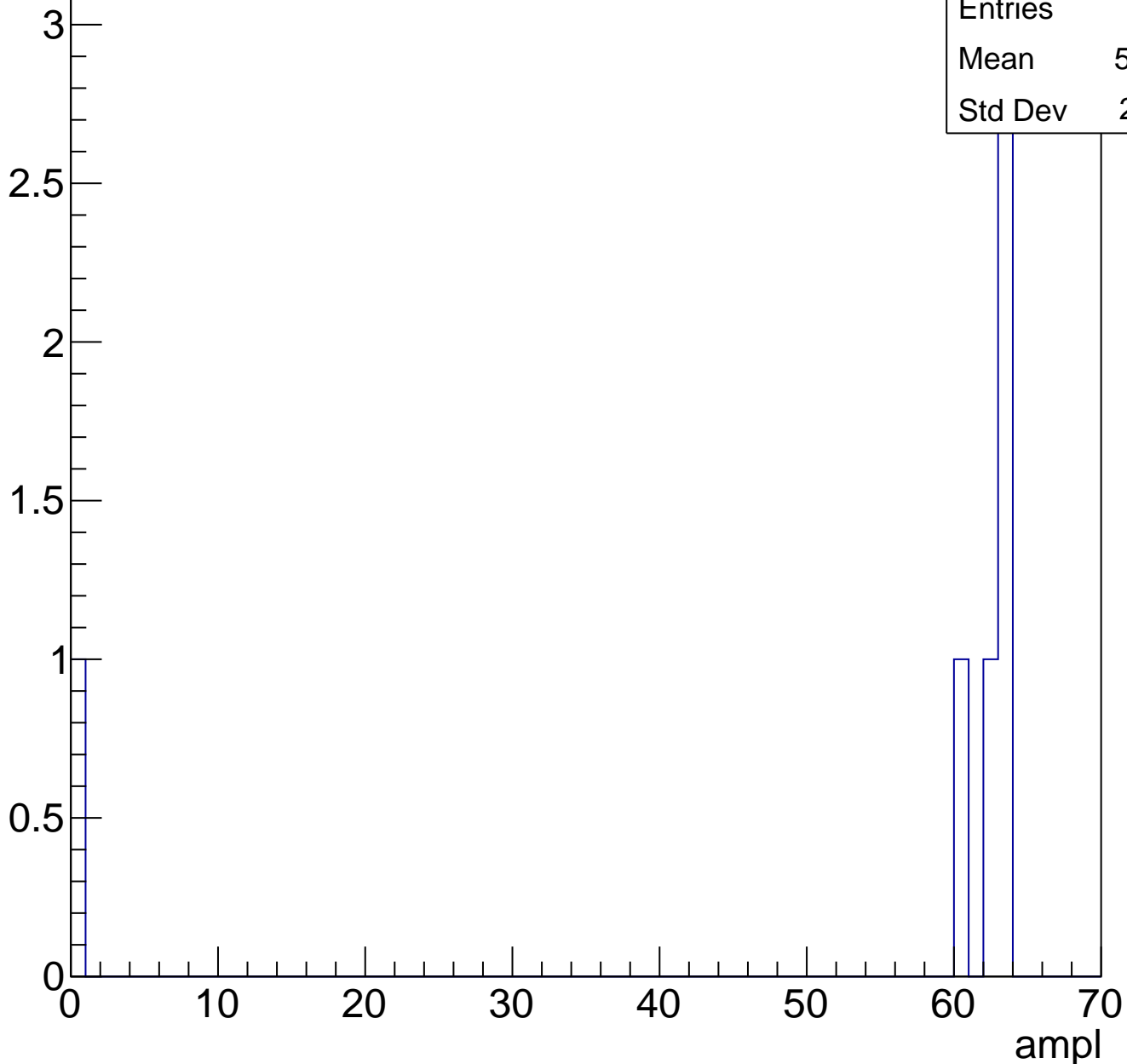
Entries	33
Mean	60.12
Std Dev	2.728



B1L101S, U25-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch116, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	29.9
Std Dev	4.115

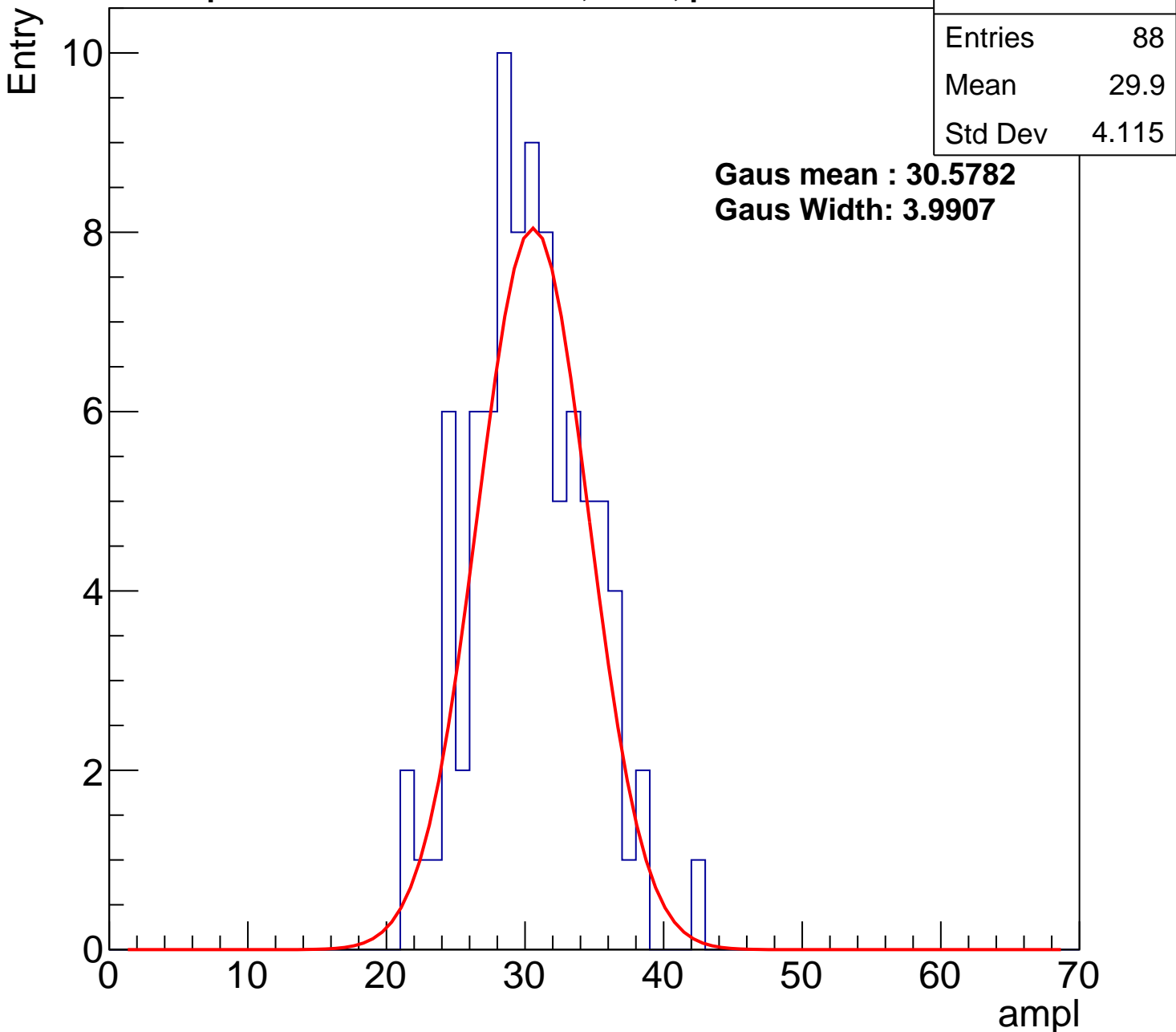
Gaus mean : 30.5782
Gaus Width: 3.9907

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch116, adc1

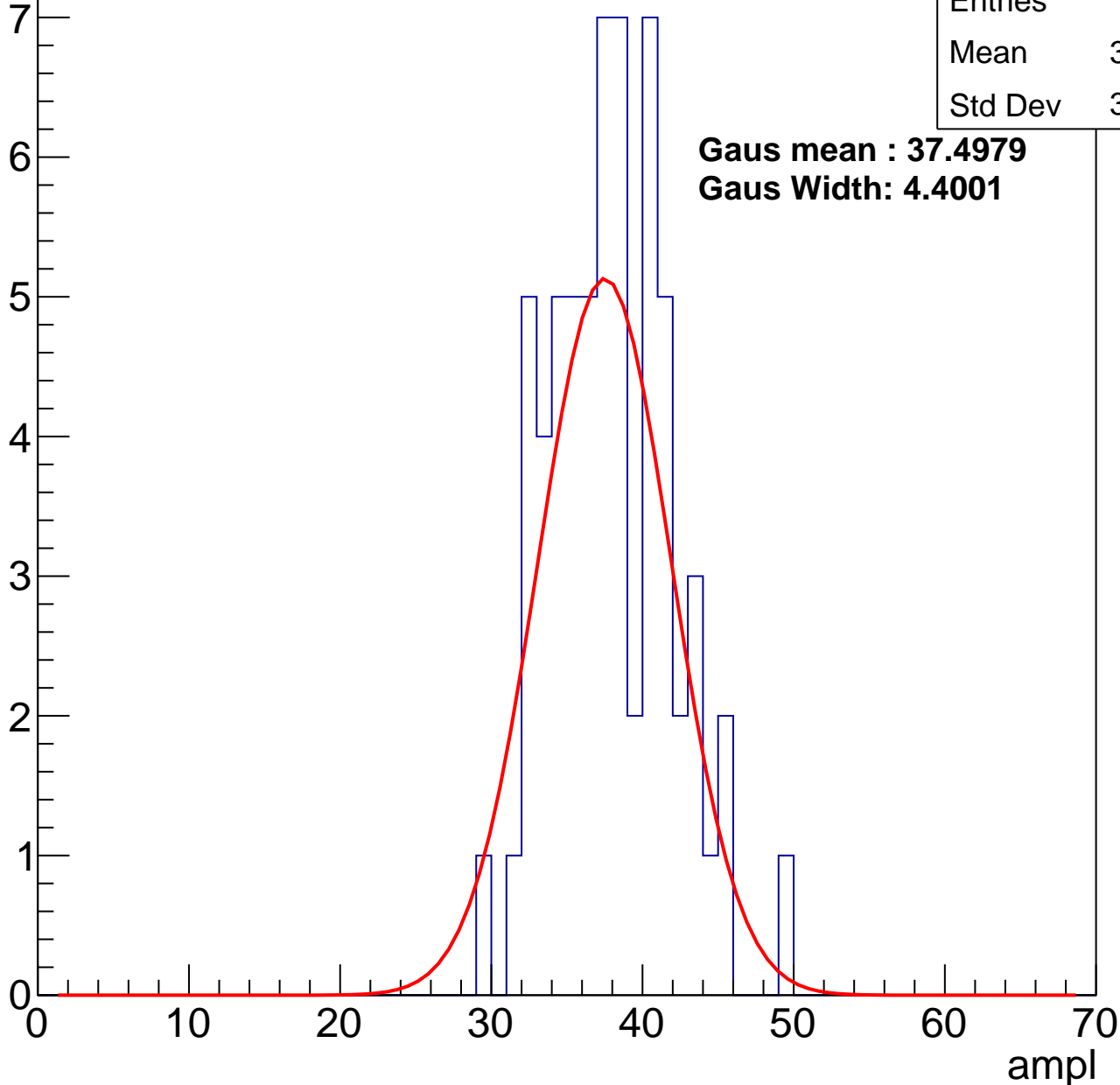
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.48
Std Dev	3.948

Gaus mean : 37.4979

Gaus Width: 4.4001



B1L101S, U25-ch116, adc2

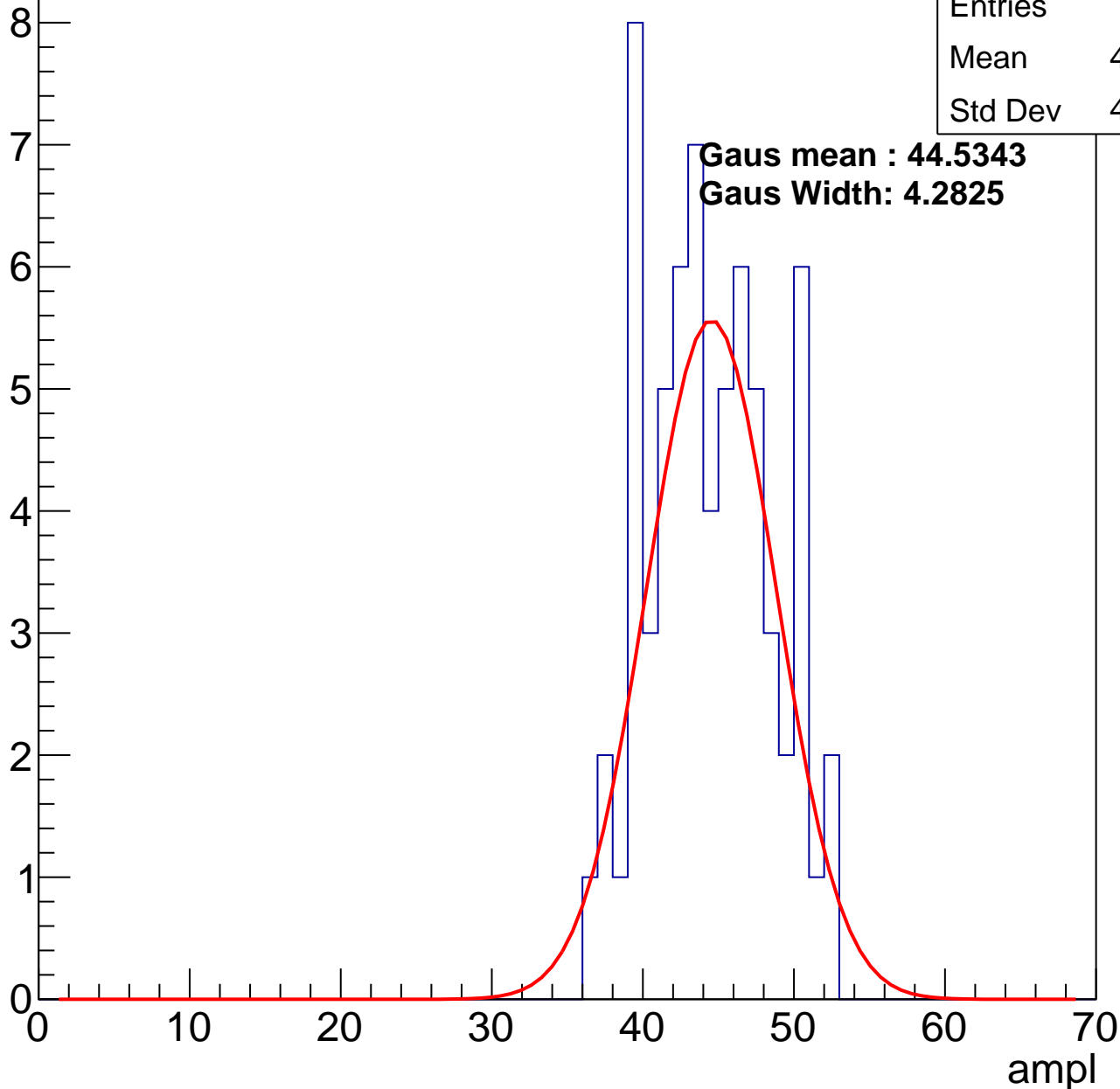
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.99
Std Dev	4.028

Gaus mean : 44.5343

Gaus Width: 4.2825

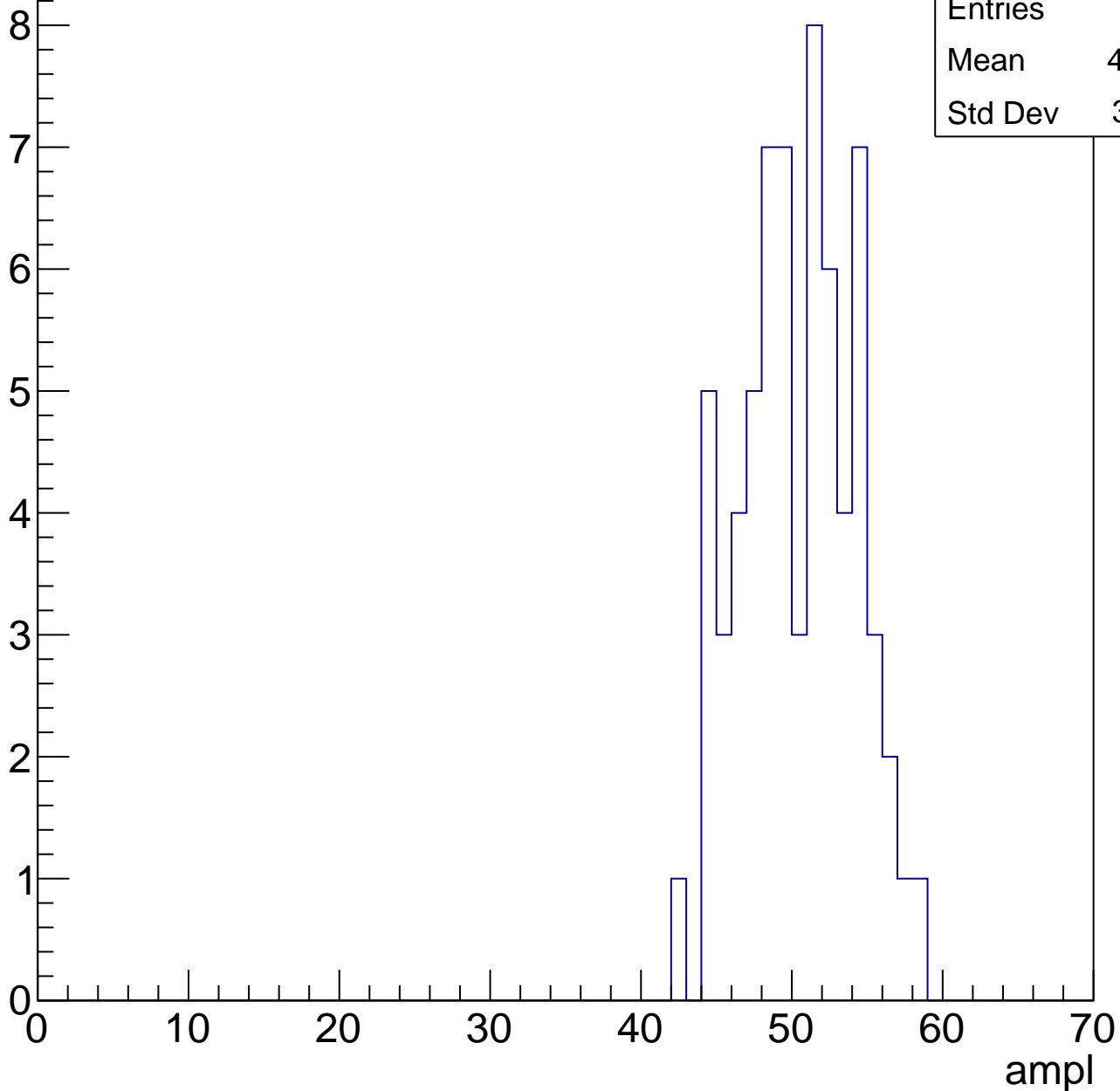


B1L101S, U25-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.96
Std Dev	3.671

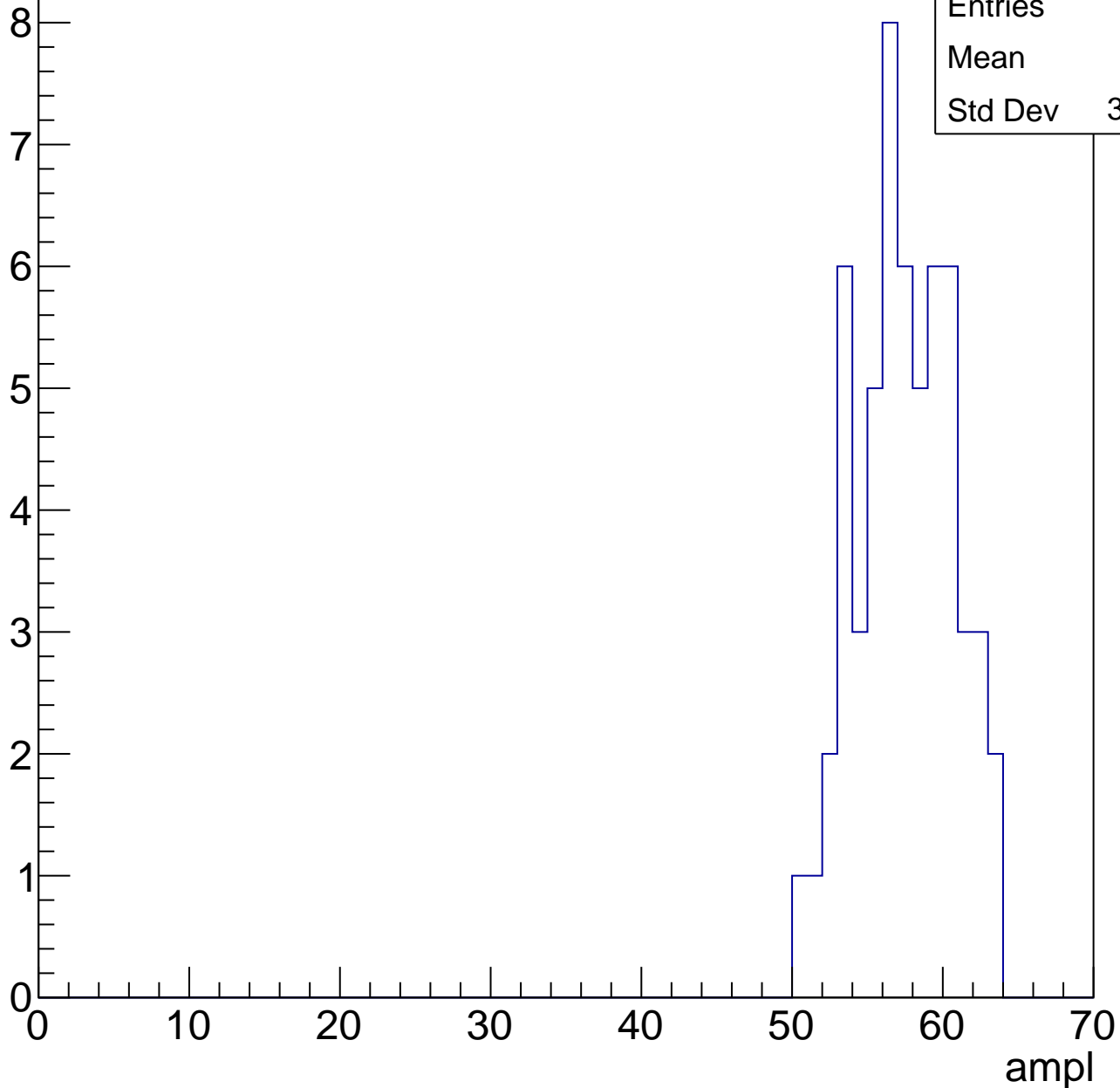


B1L101S, U25-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	57
Std Dev	3.146

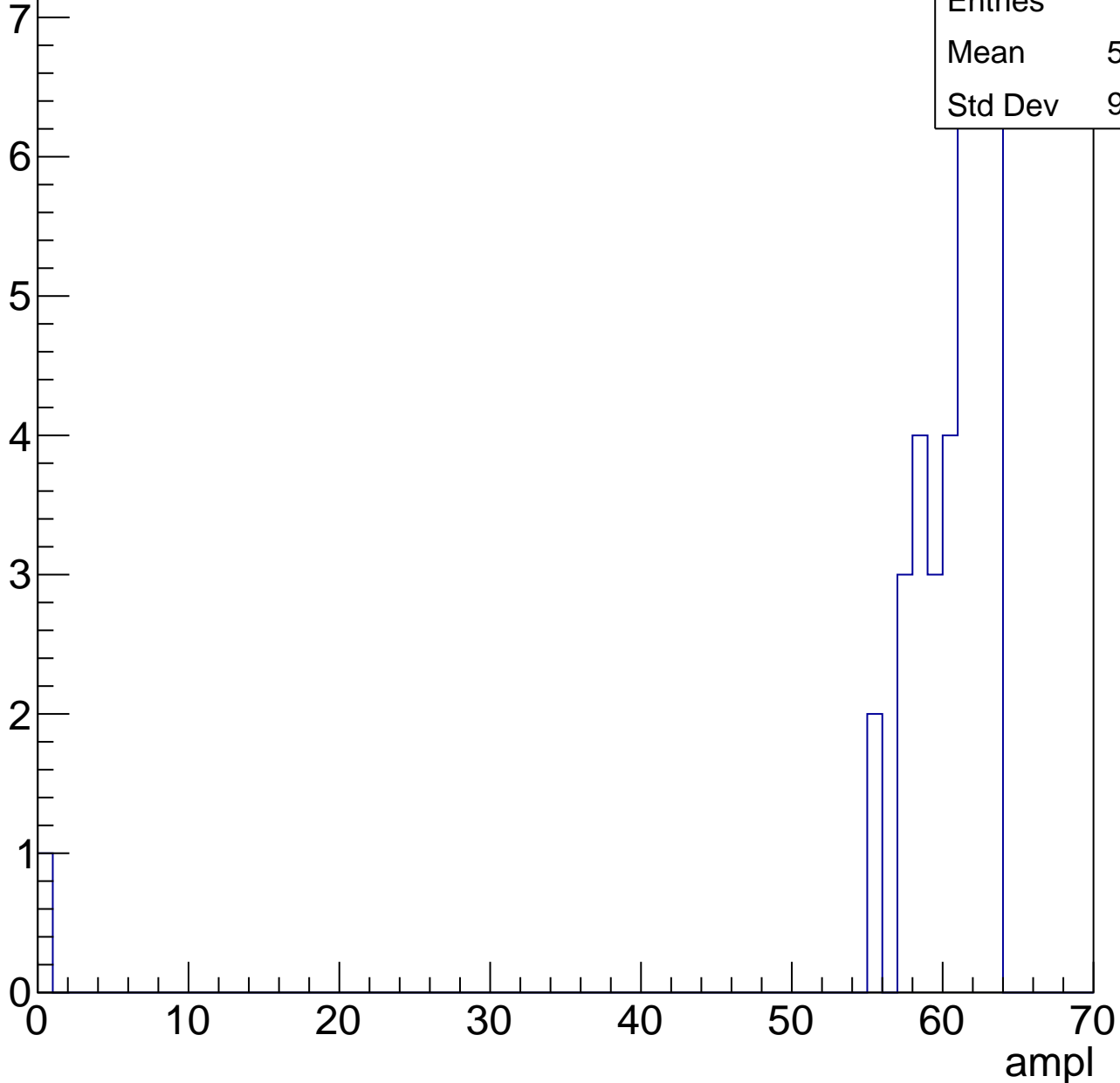


B1L101S, U25-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.74
Std Dev	9.912



B1L101S, U25-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch117, adc0

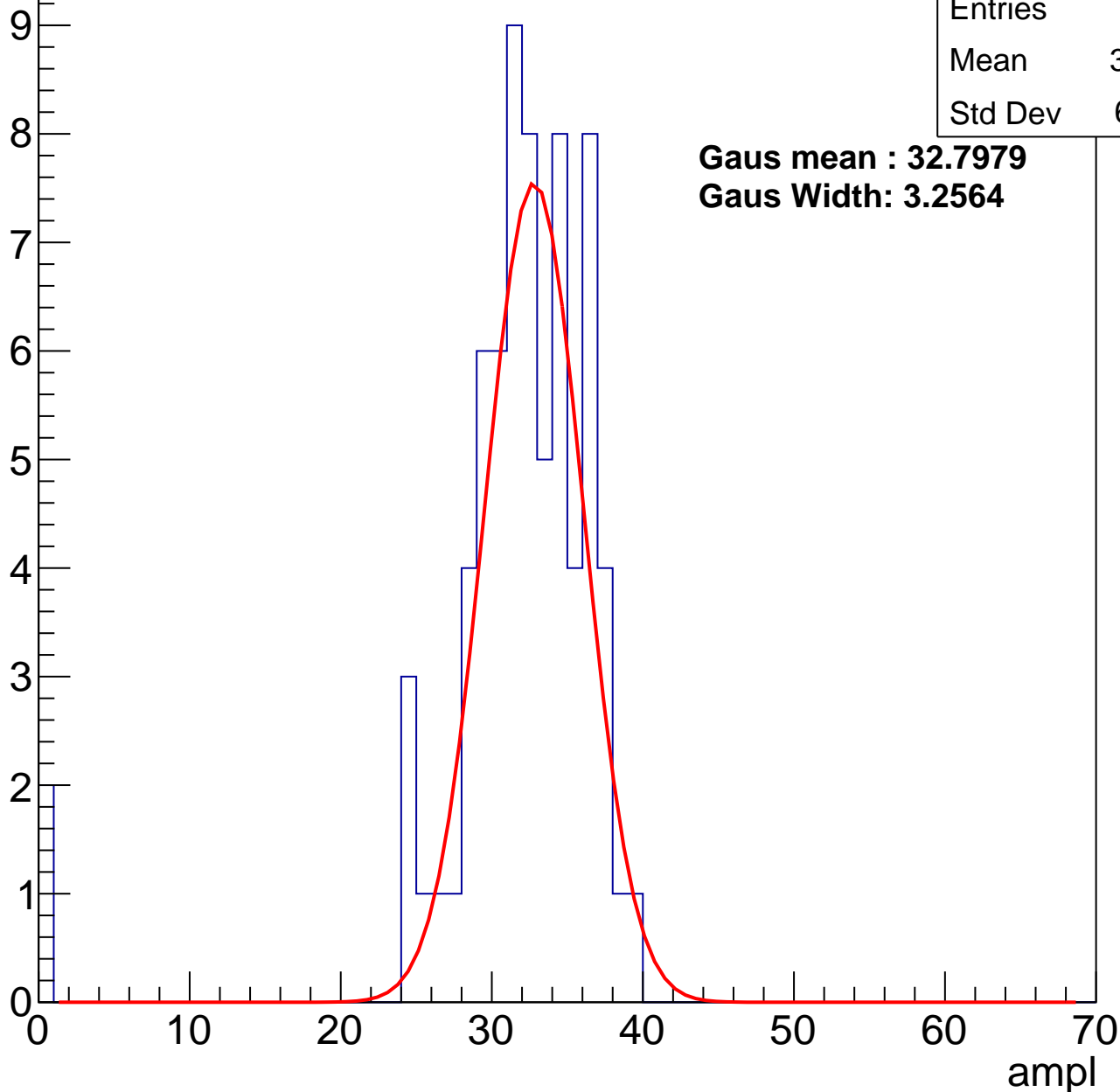
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	31.12
Std Dev	6.271

Gaus mean : 32.7979

Gaus Width: 3.2564



B1L101S, U25-ch117, adc1

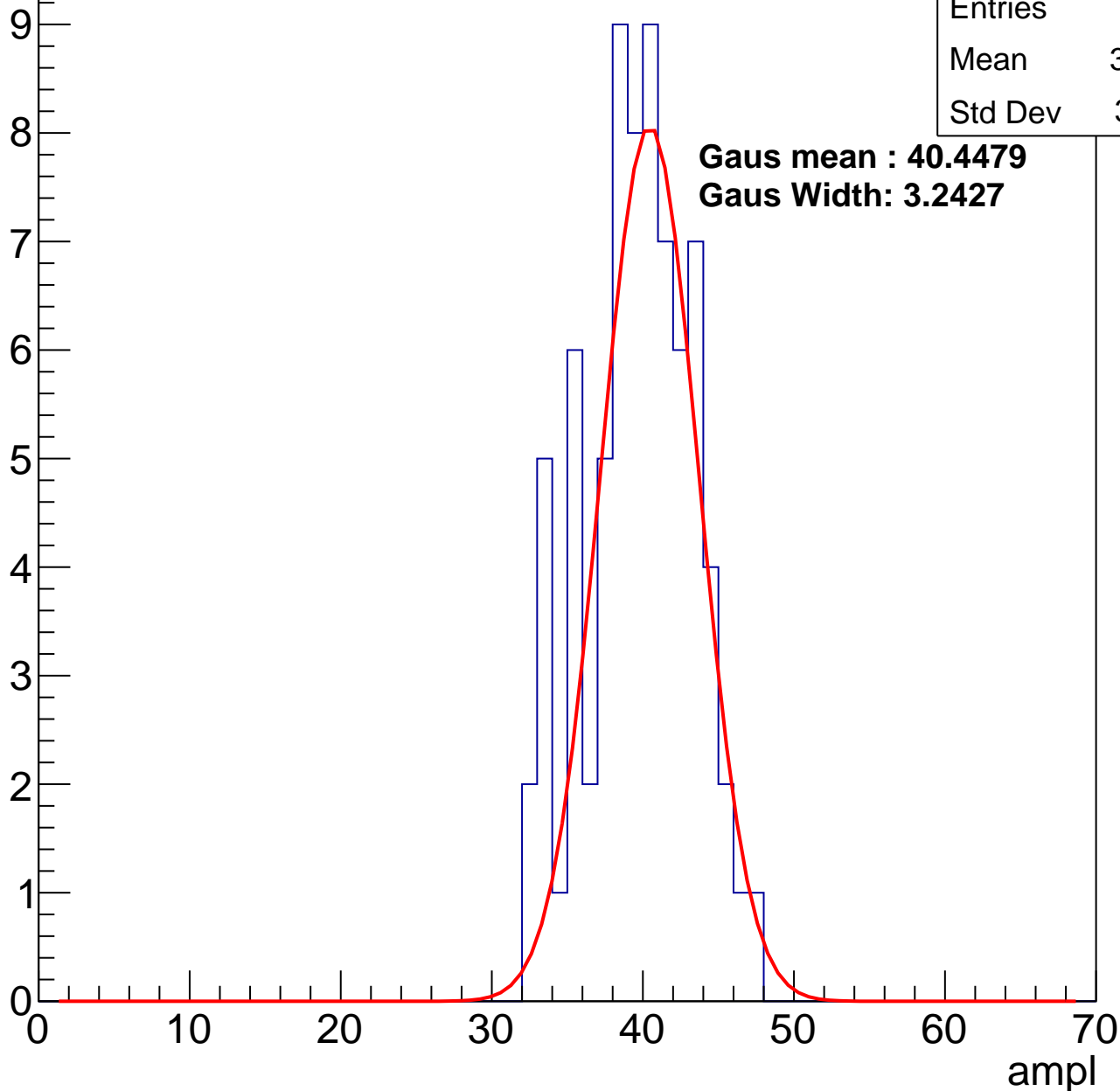
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	39.24
Std Dev	3.521

Gaus mean : 40.4479

Gaus Width: 3.2427



B1L101S, U25-ch117, adc2

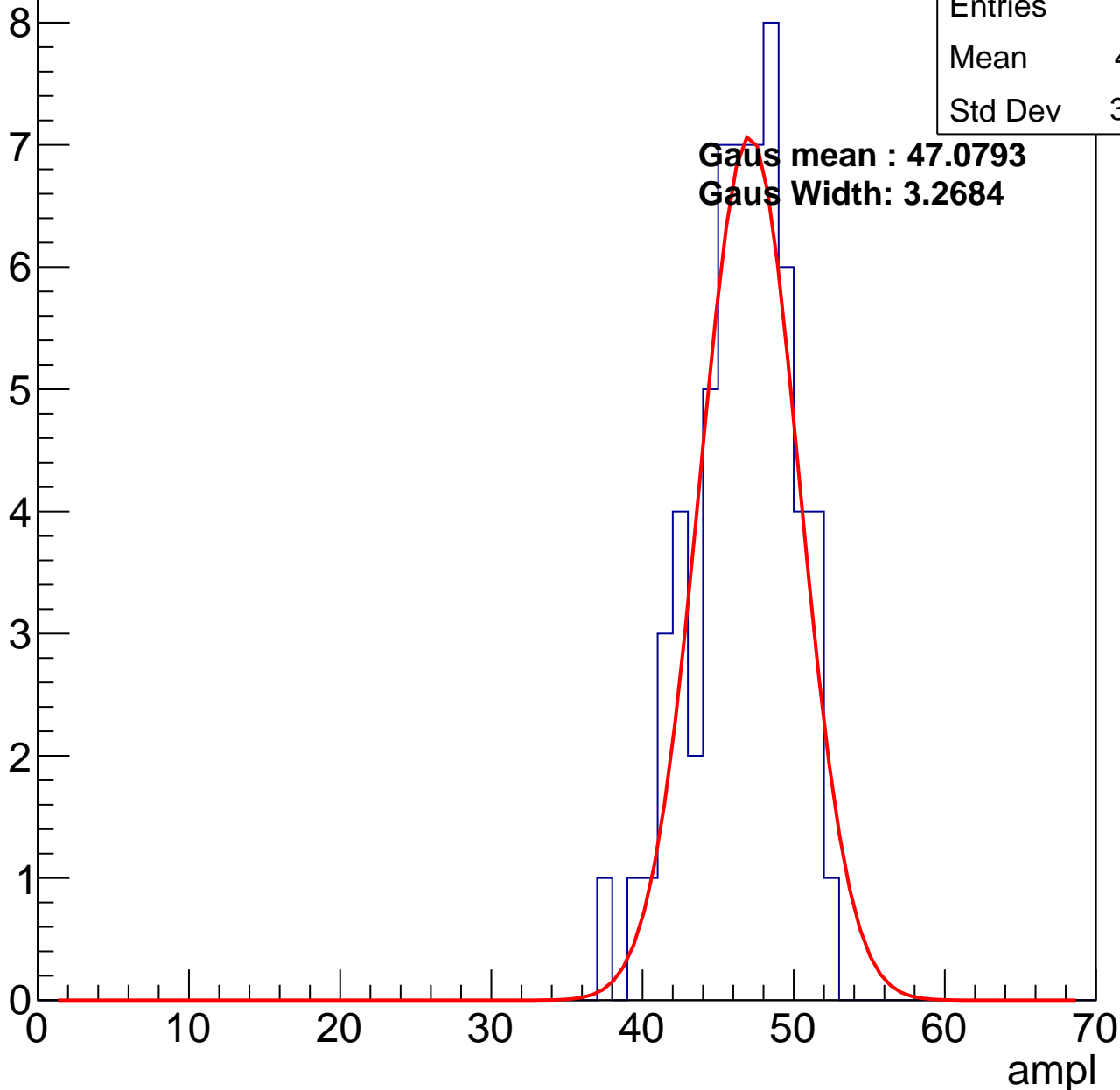
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	46.11
Std Dev	3.245

Gaus mean : 47.0793

Gaus Width: 3.2684



B1L101S, U25-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

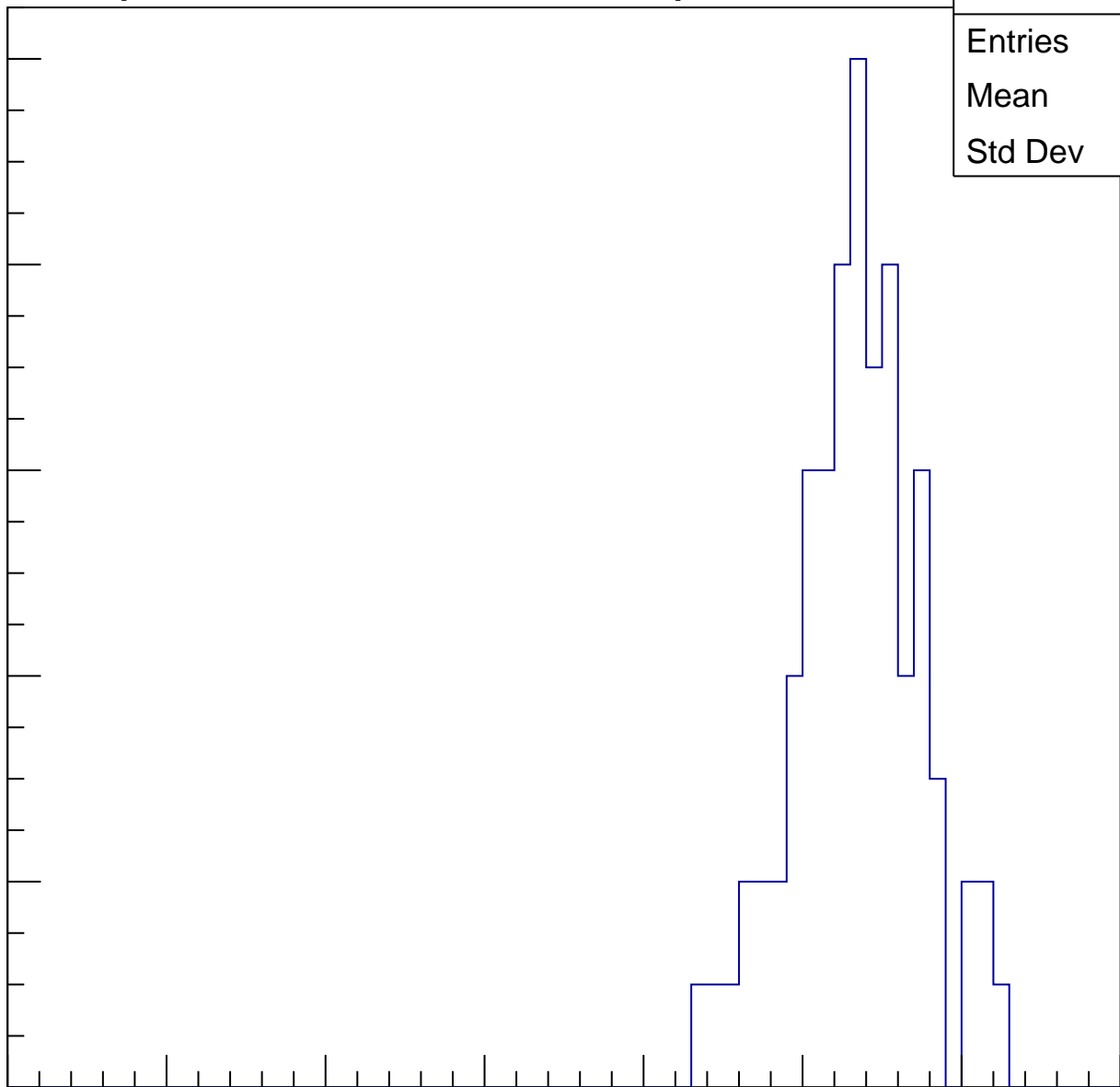
Entries	76
Mean	52.95
Std Dev	3.92

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

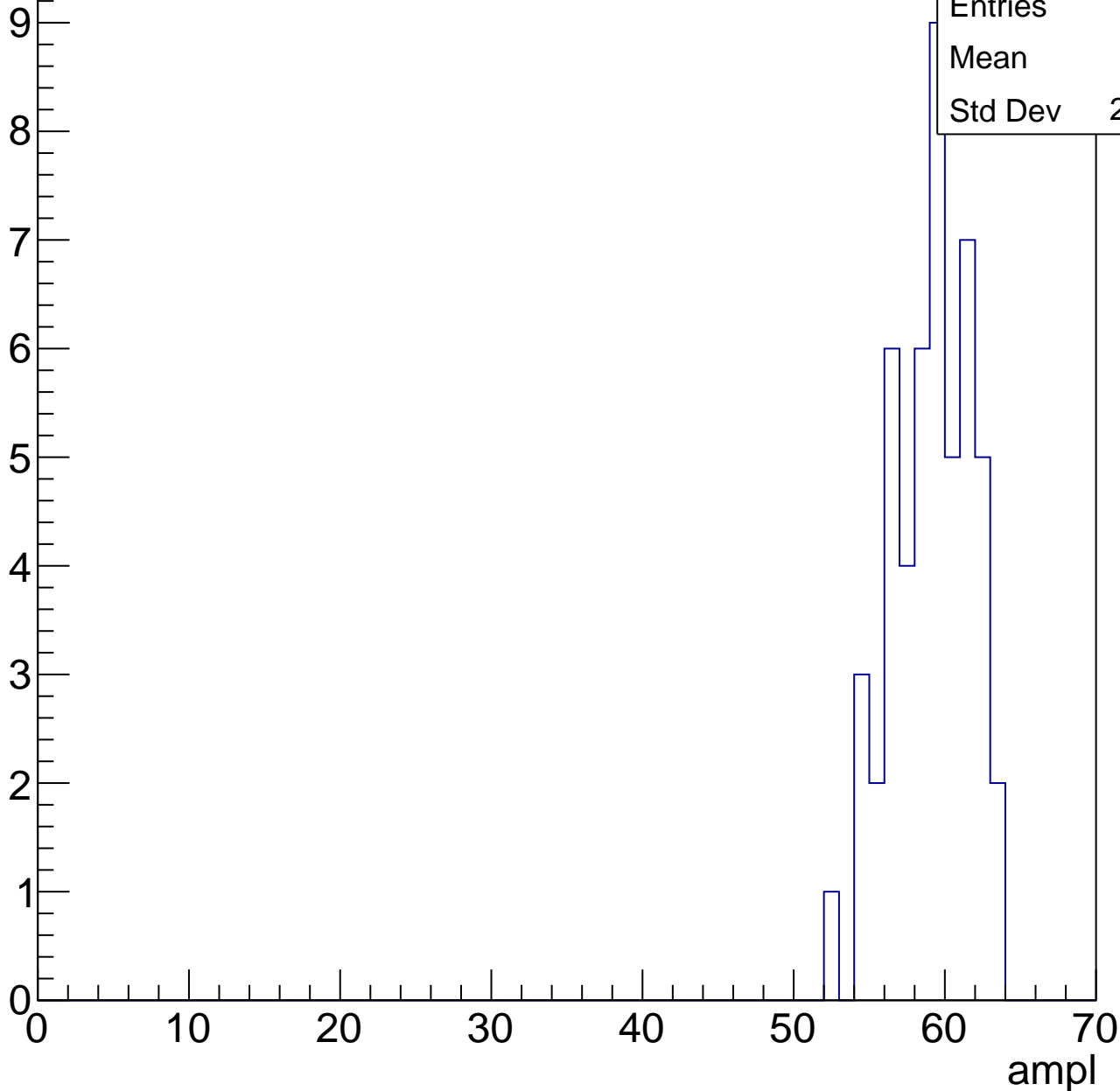


B1L101S, U25-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.6
Std Dev	2.585

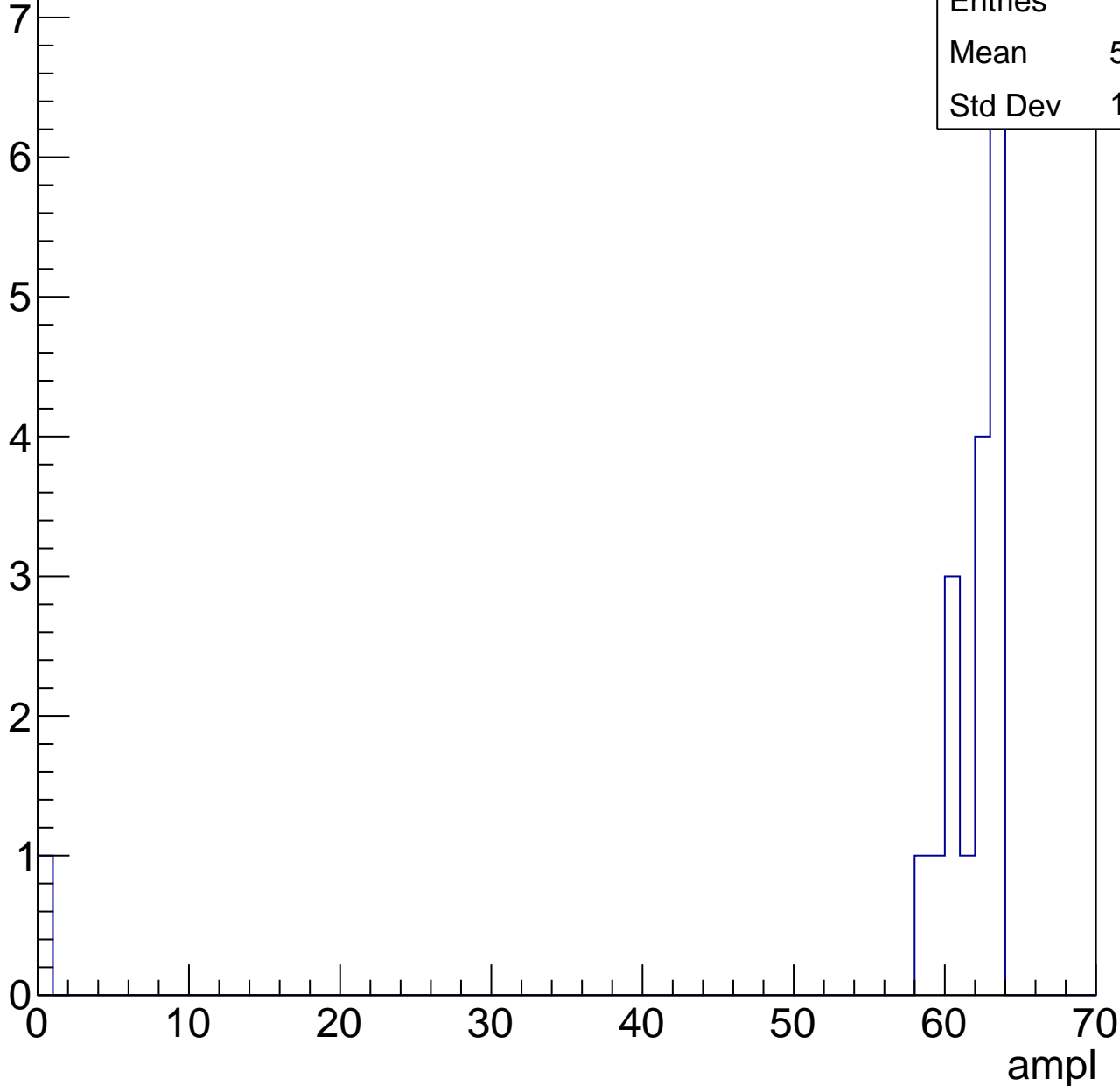


B1L101S, U25-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	58.17
Std Dev	14.19



B1L101S, U25-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U25-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch118, adc0

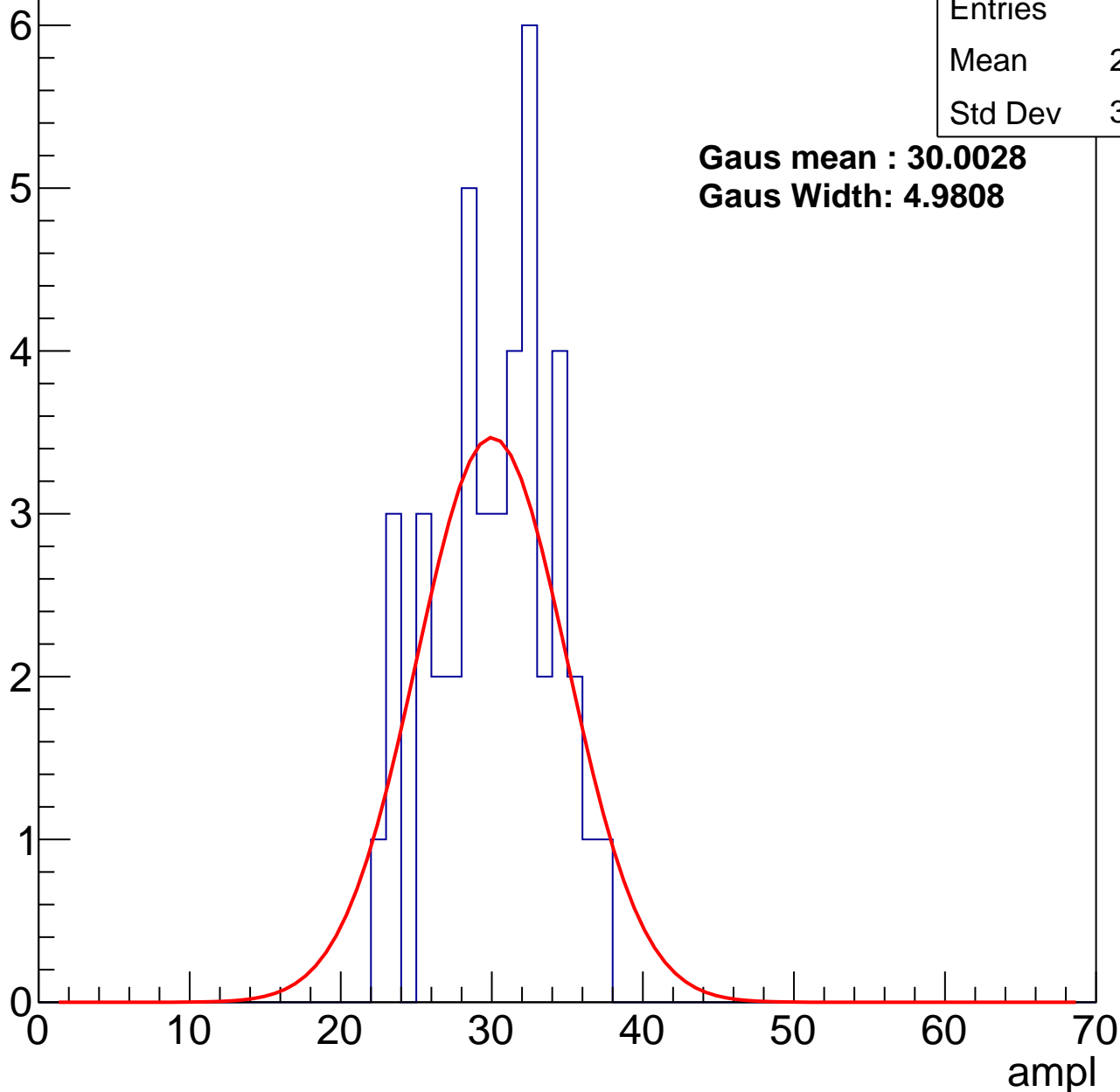
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	29.76
Std Dev	3.785

Gaus mean : 30.0028

Gaus Width: 4.9808



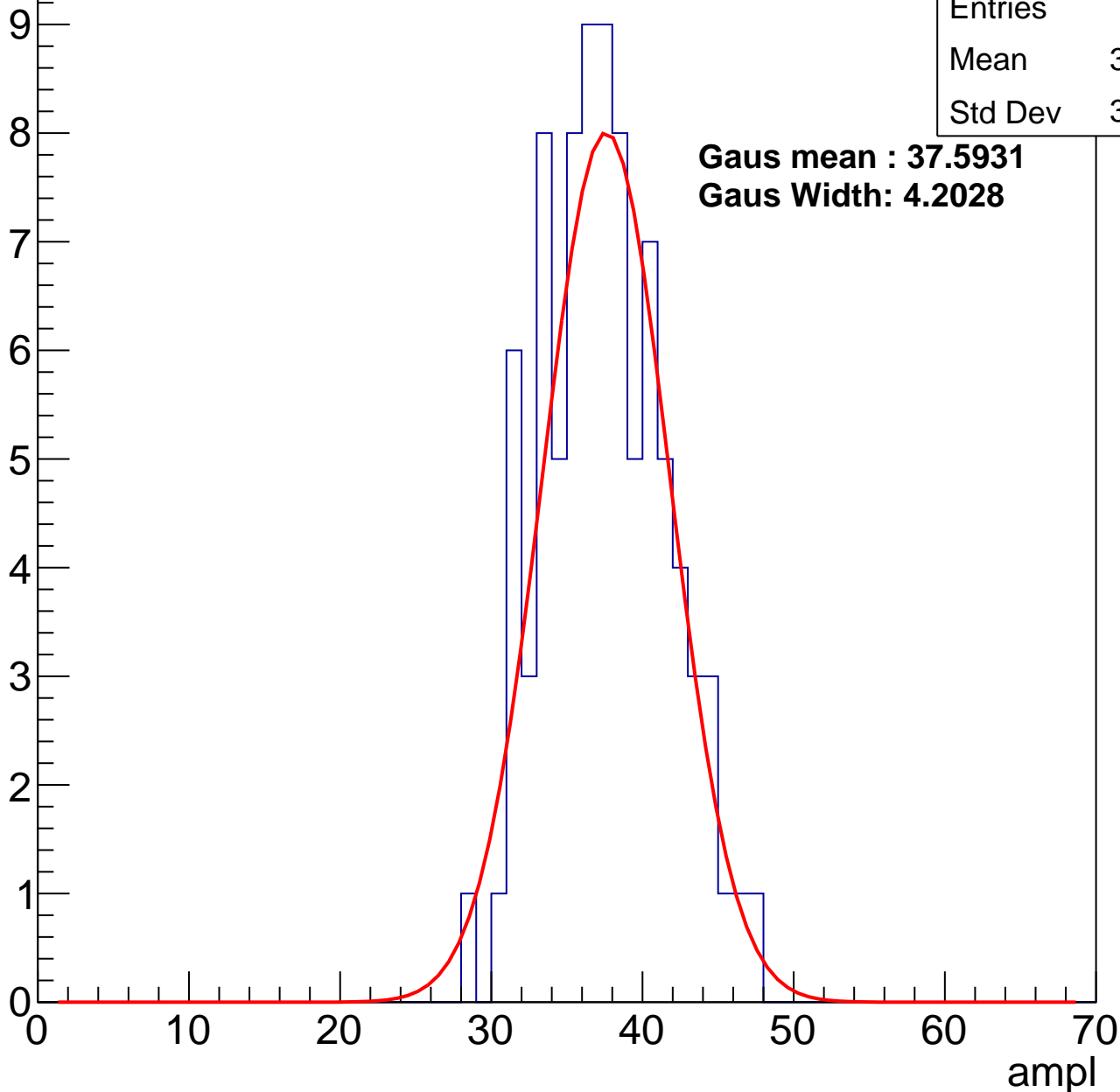
B1L101S, U25-ch118, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	37.07
Std Dev	3.997

Gaus mean : 37.5931
Gaus Width: 4.2028



B1L101S, U25-ch118, adc2

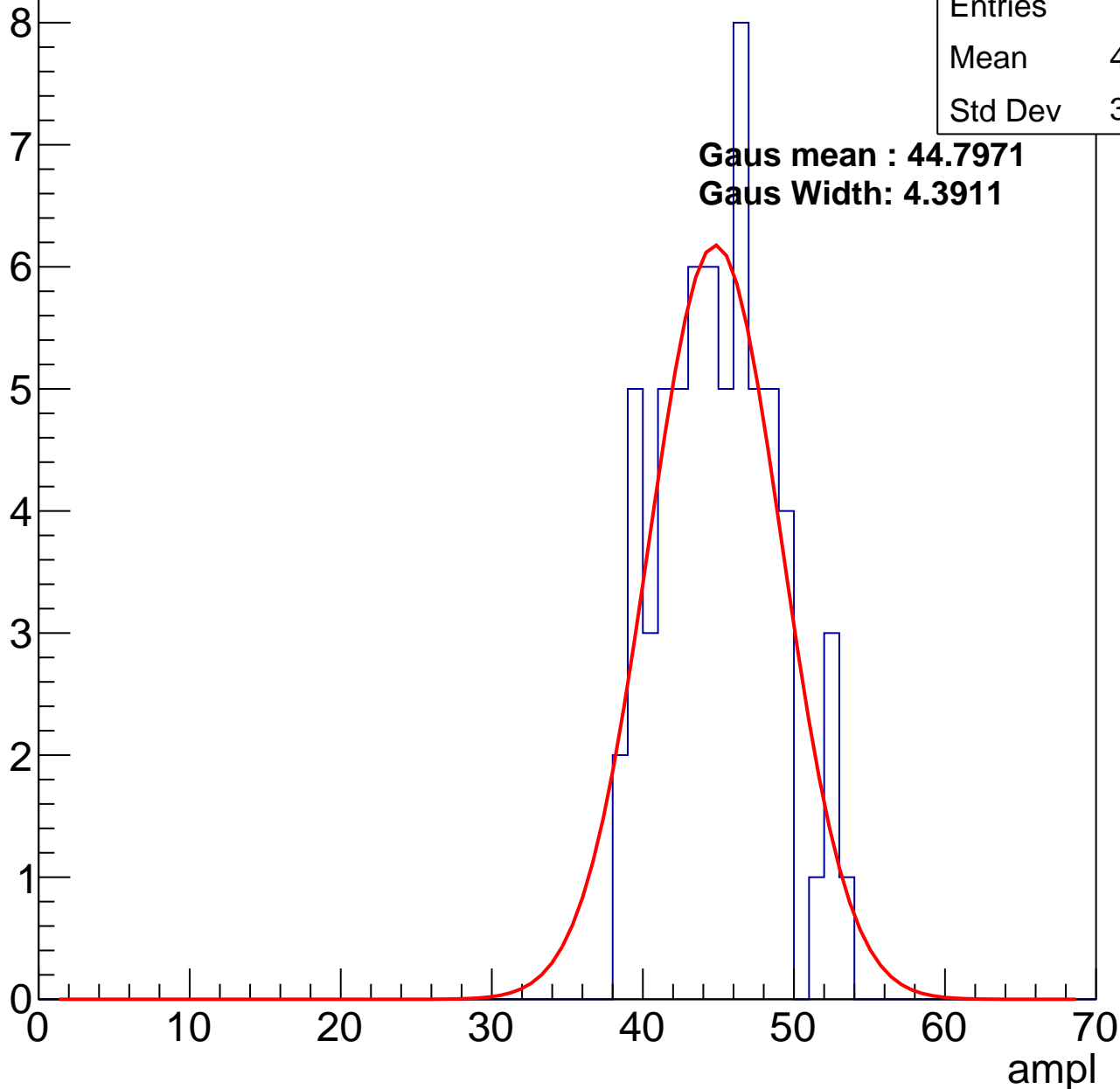
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.56
Std Dev	3.708

Gaus mean : 44.7971

Gaus Width: 4.3911

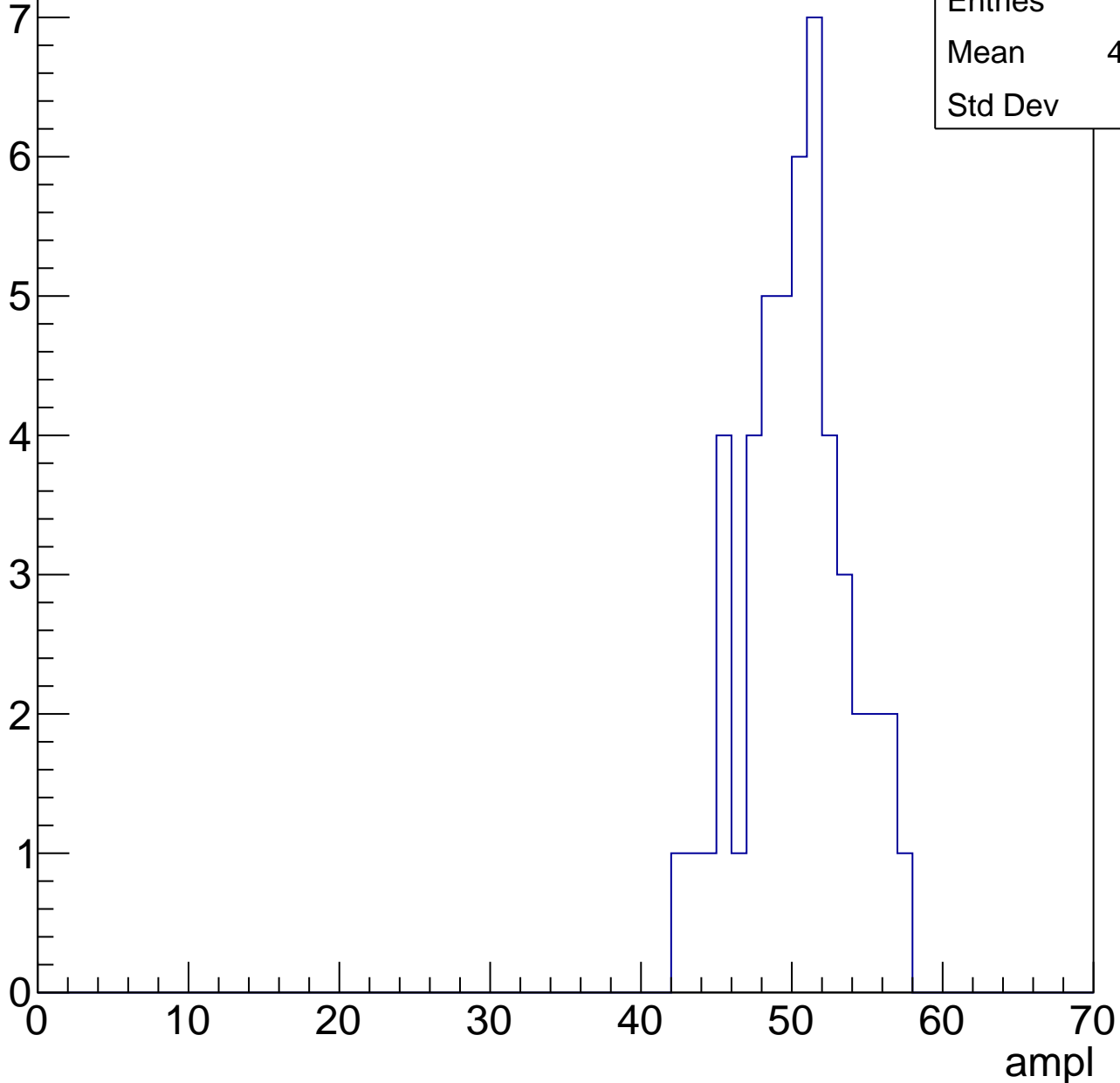


B1L101S, U25-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	49.78
Std Dev	3.43

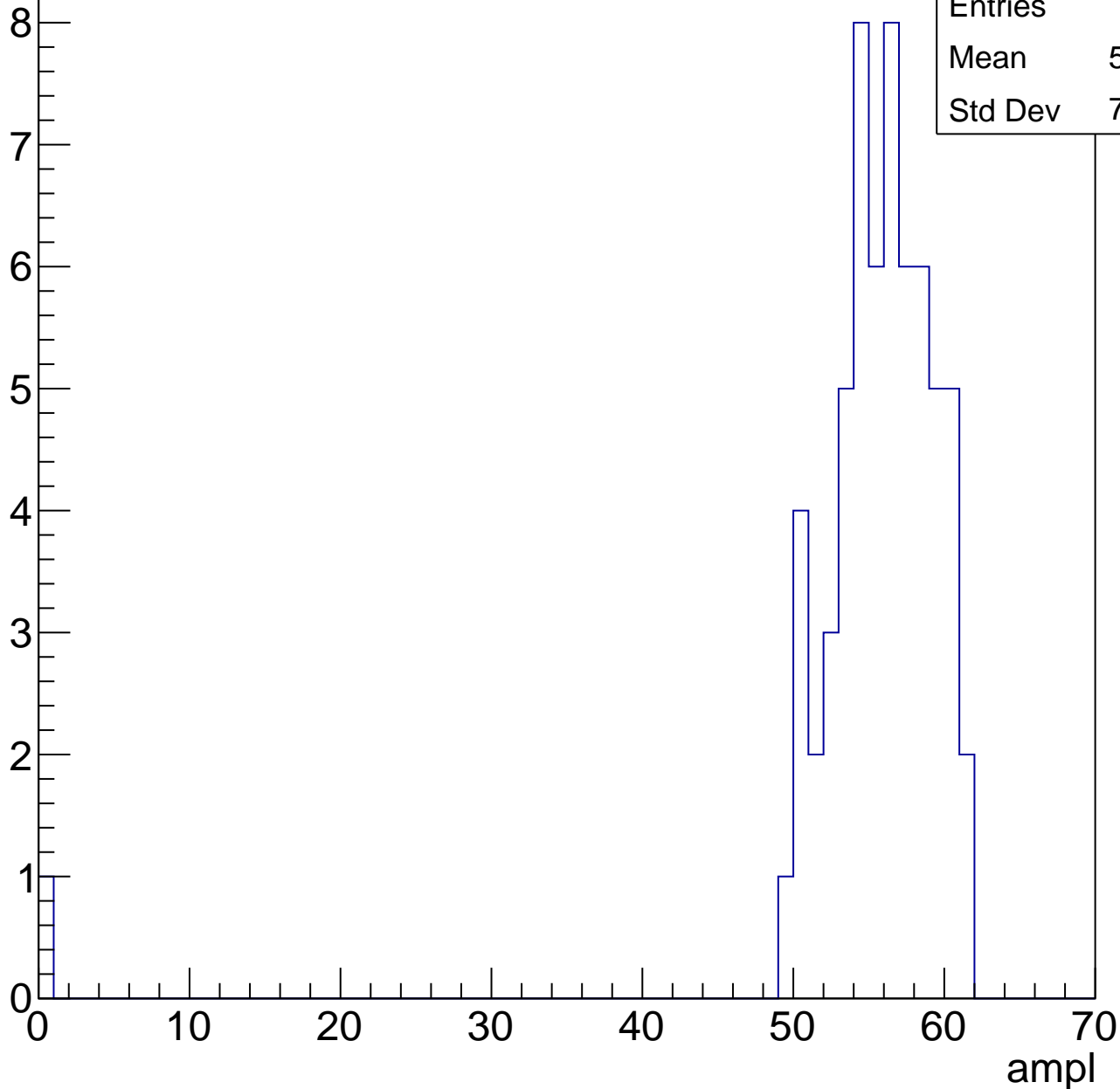


B1L101S, U25-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.66
Std Dev	7.624

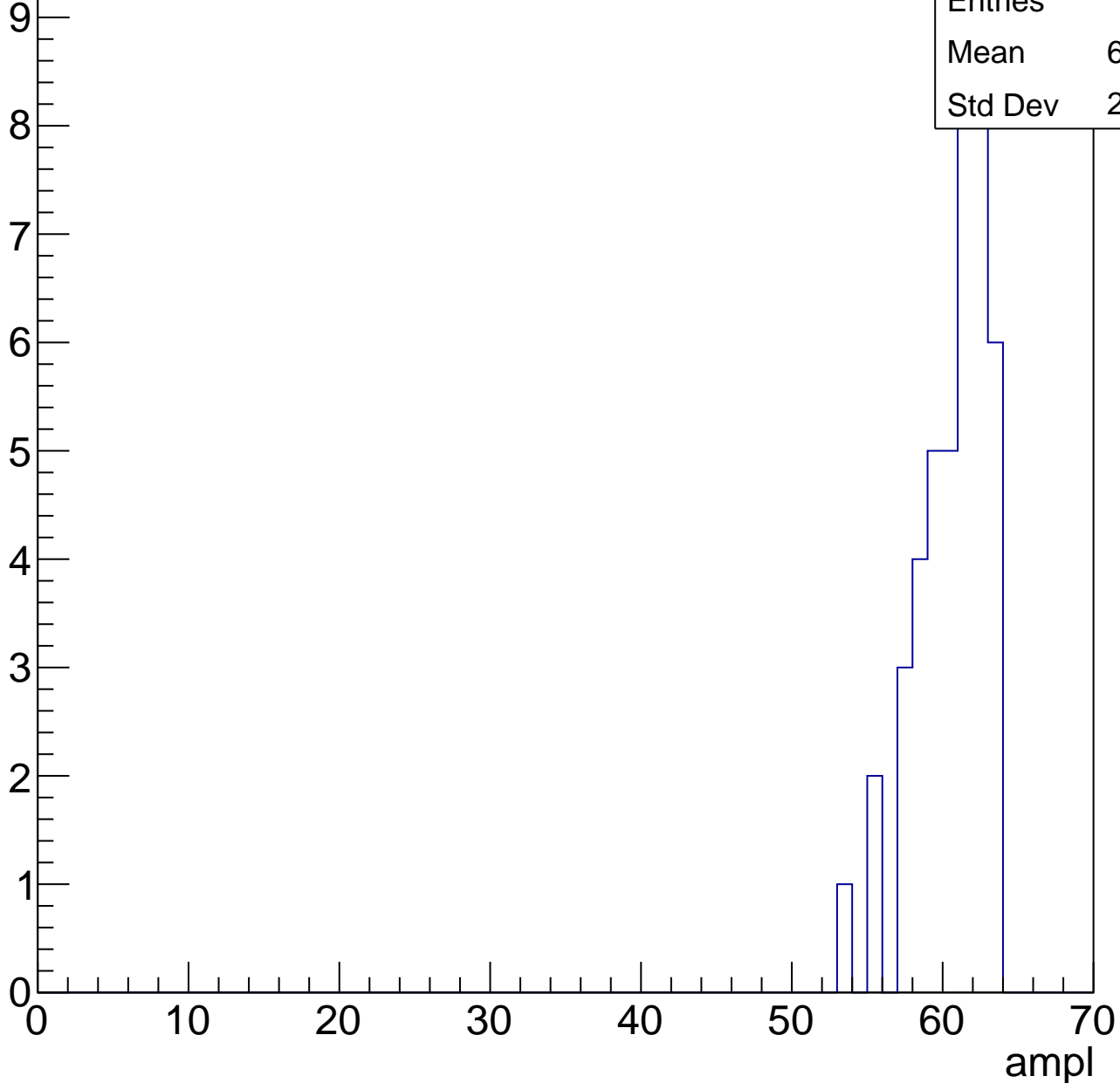


B1L101S, U25-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	60.12
Std Dev	2.384



B1L101S, U25-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch119, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.19
Std Dev	3.586

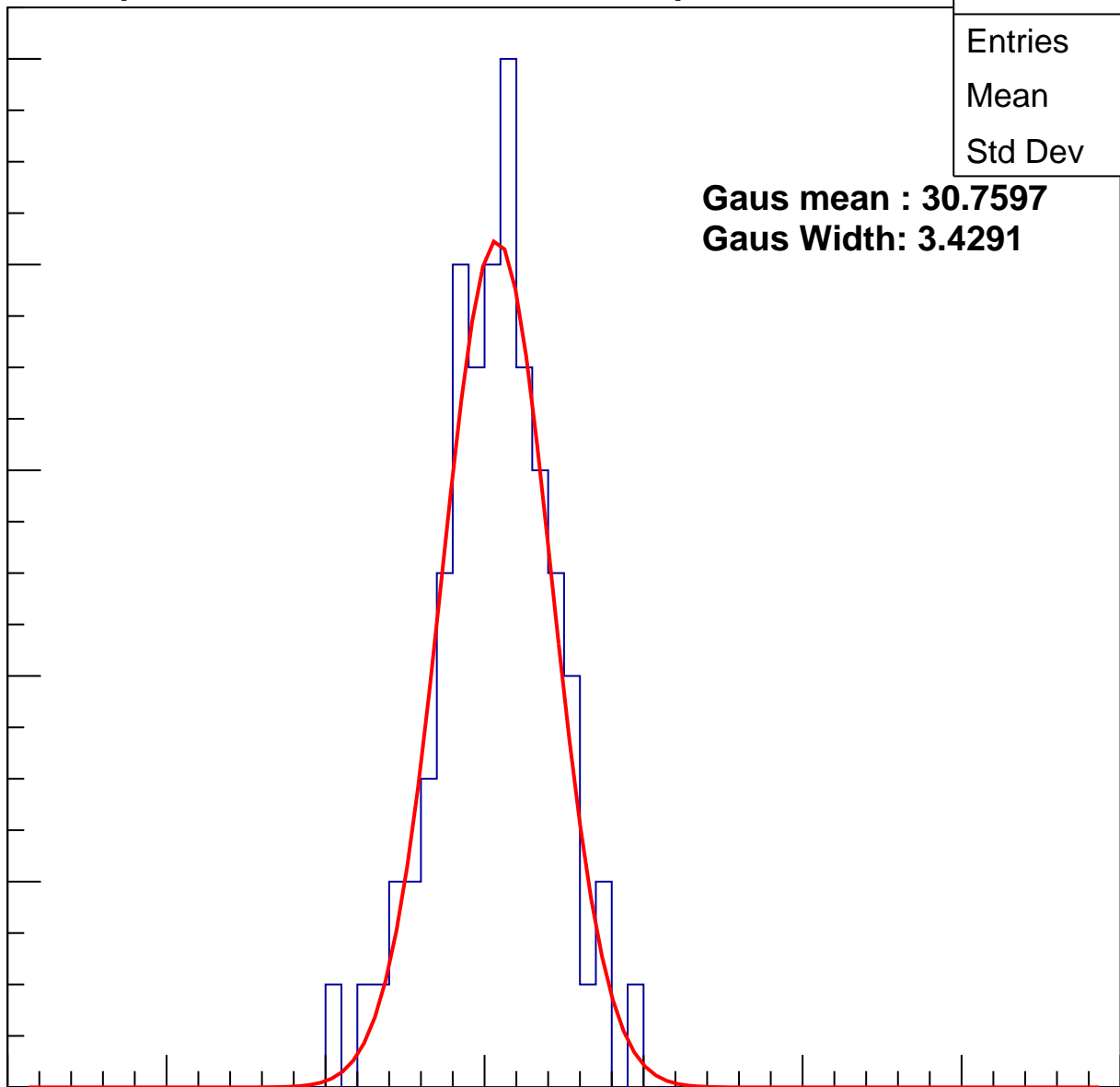
Gaus mean : 30.7597

Gaus Width: 3.4291

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch119, adc1

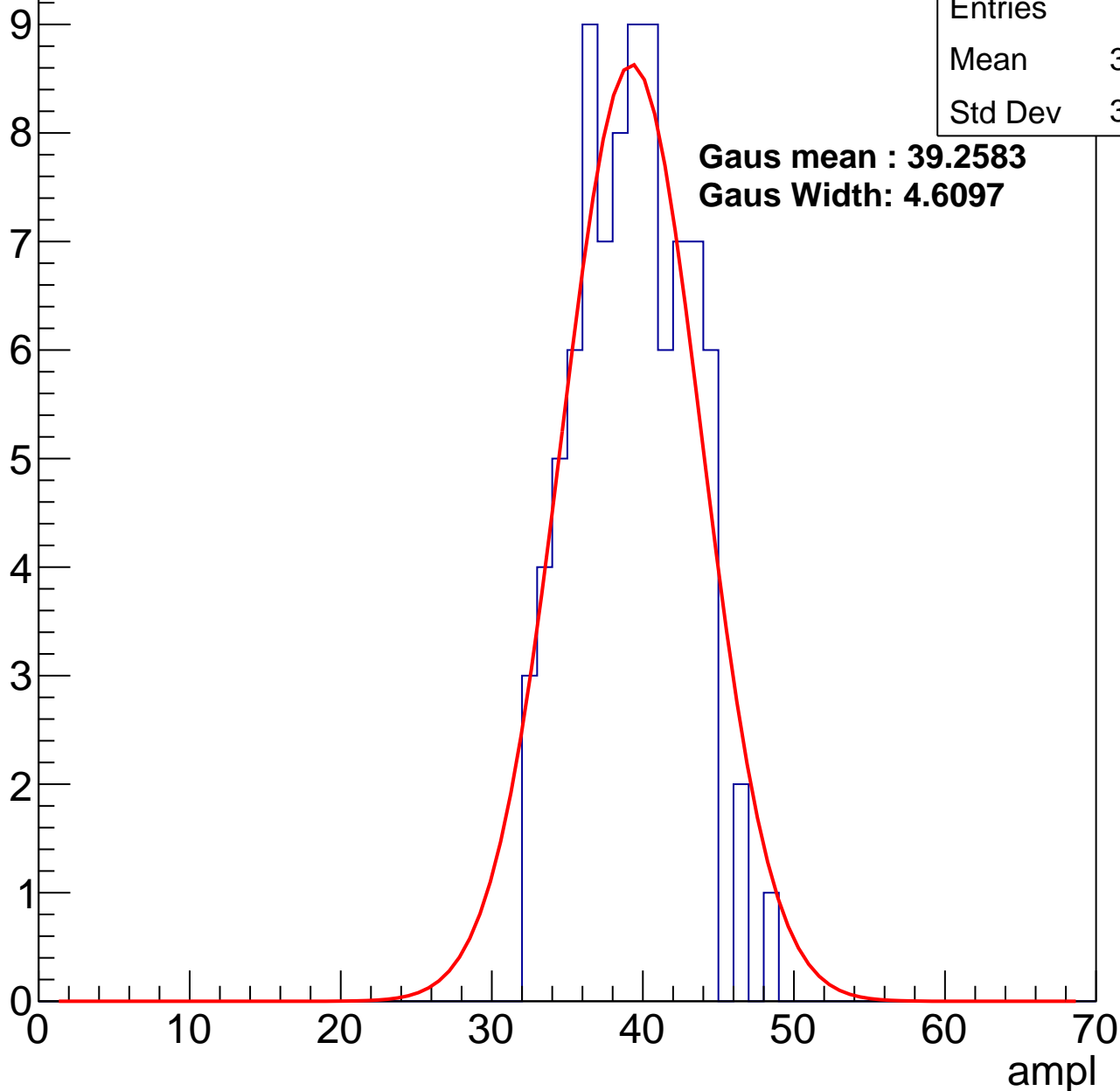
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	38.78
Std Dev	3.613

Gaus mean : 39.2583

Gaus Width: 4.6097



B1L101S, U25-ch119, adc2

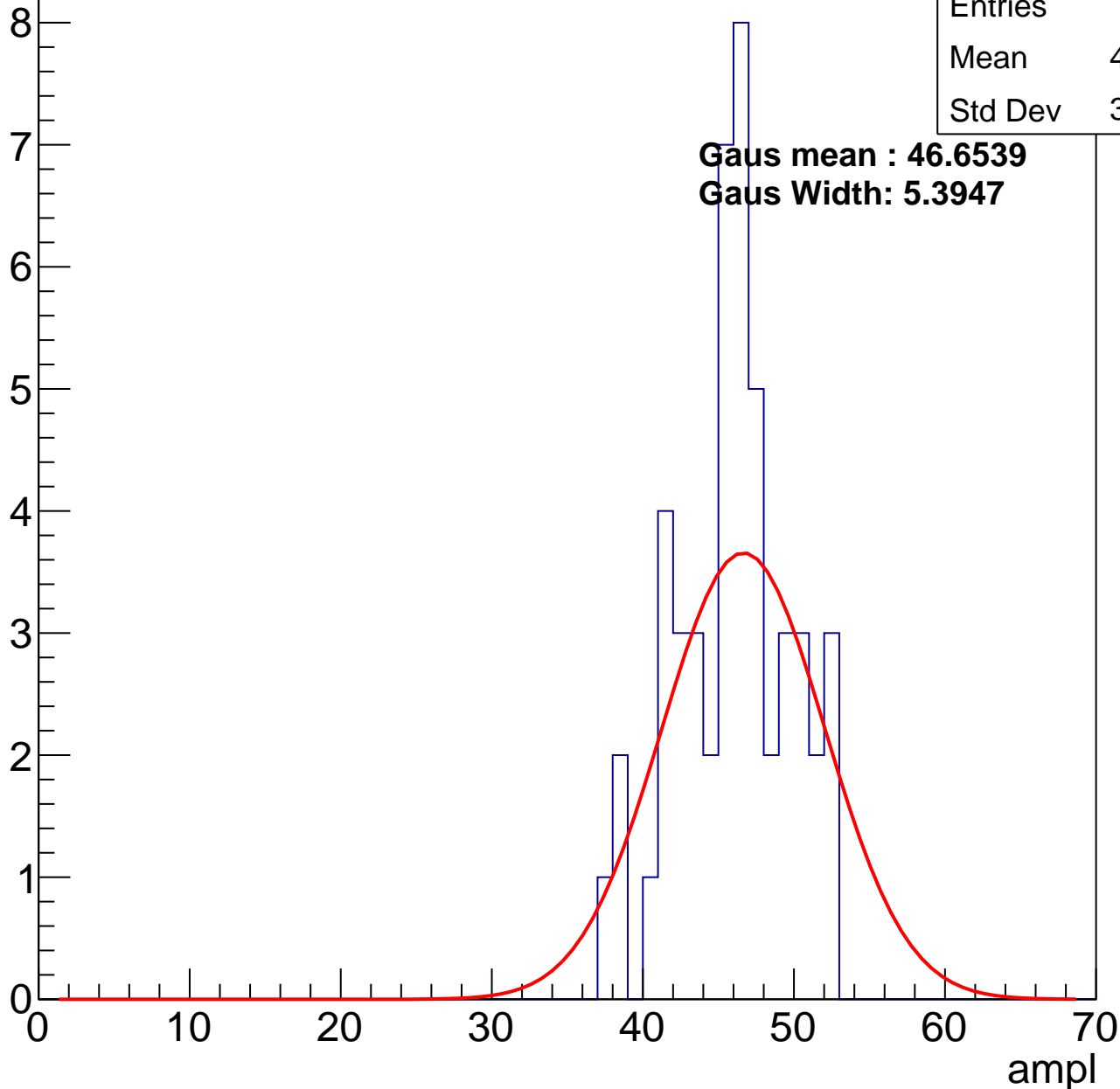
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	45.49
Std Dev	3.693

Gaus mean : 46.6539

Gaus Width: 5.3947

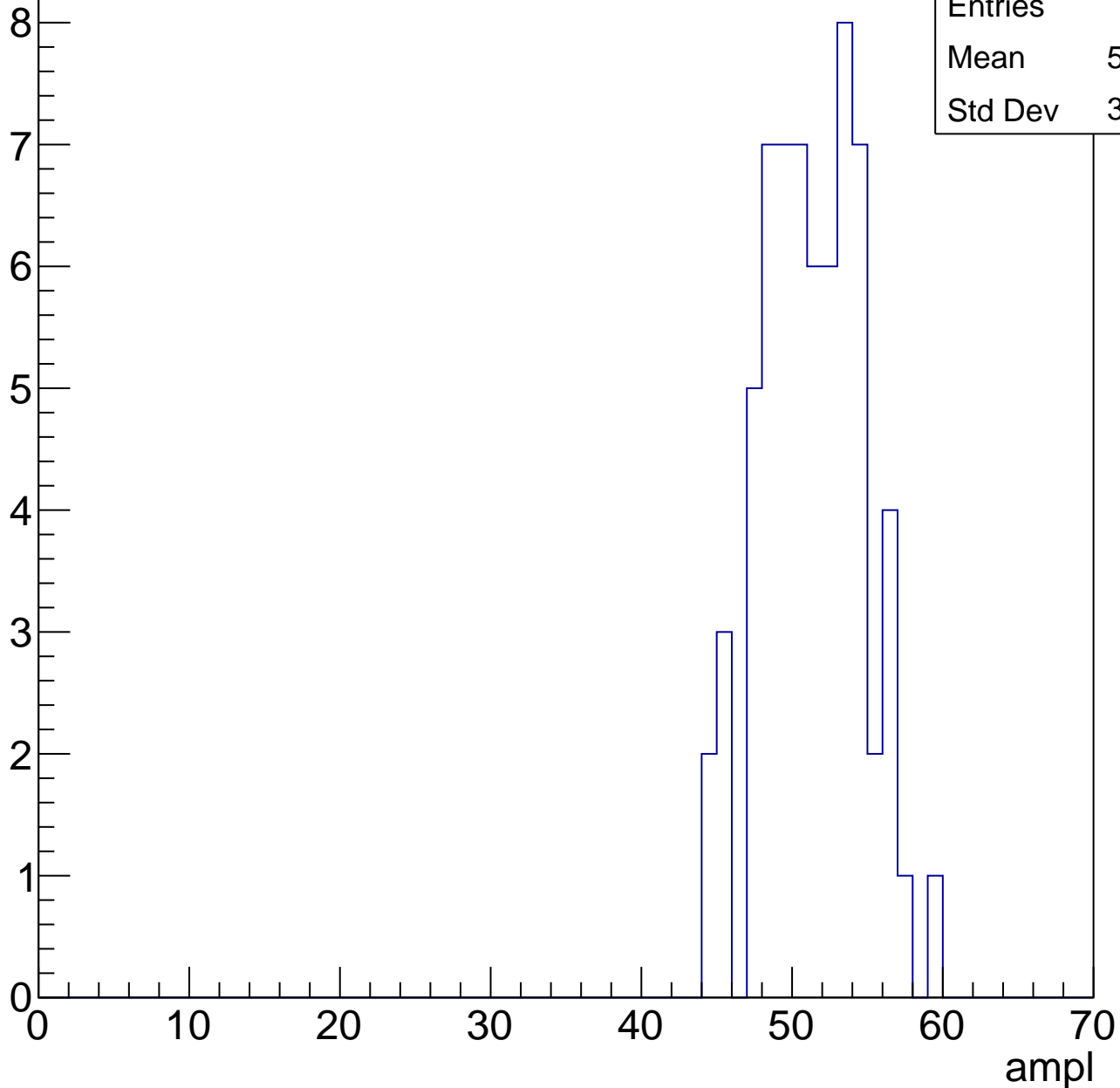


B1L101S, U25-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.86
Std Dev	3.293

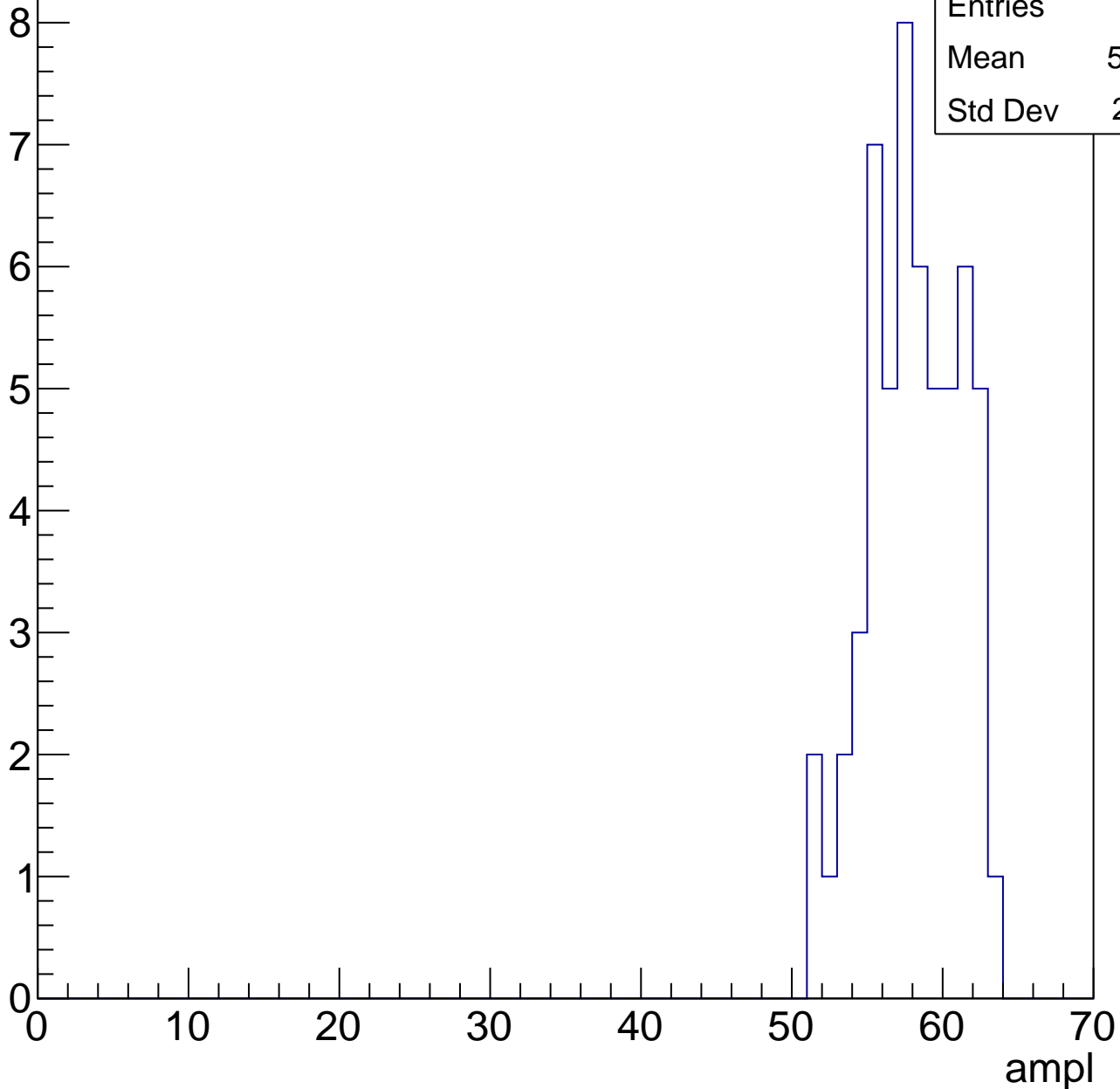


B1L101S, U25-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	57.59
Std Dev	2.981

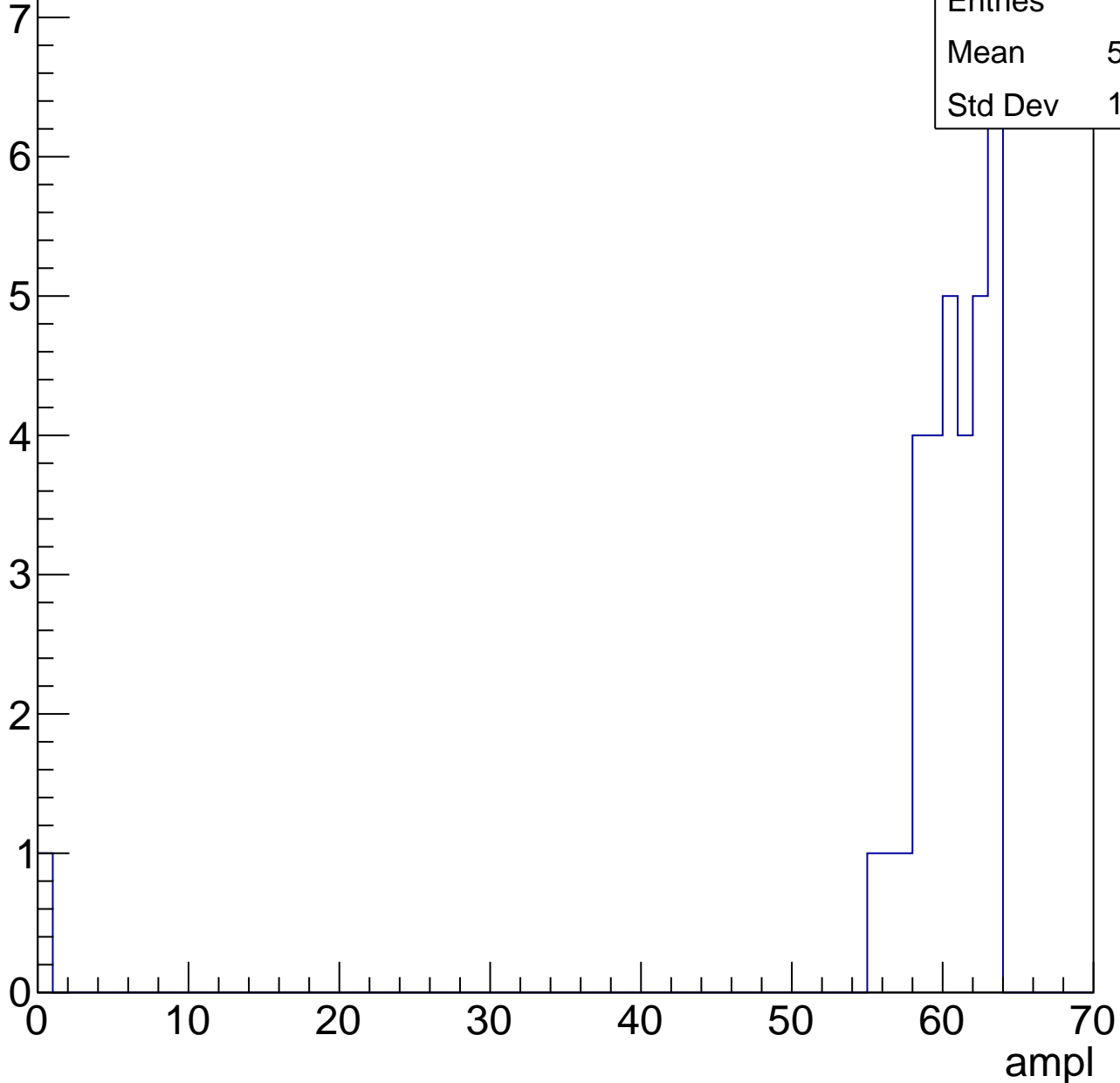


B1L101S, U25-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.52
Std Dev	10.57



B1L101S, U25-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U25-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch120, adc0

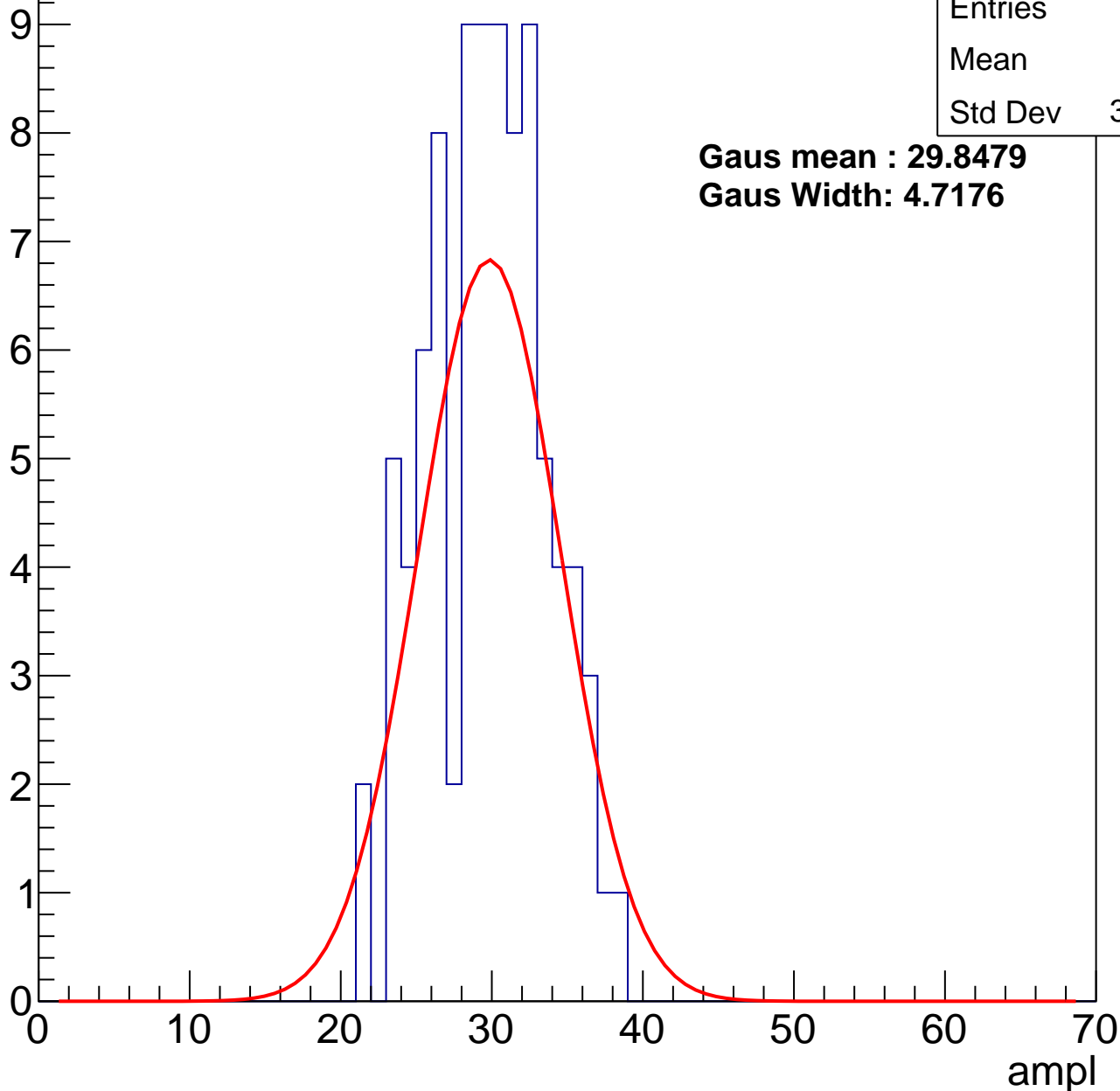
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	29.3
Std Dev	3.855

Gaus mean : 29.8479

Gaus Width: 4.7176



B1L101S, U25-ch120, adc1

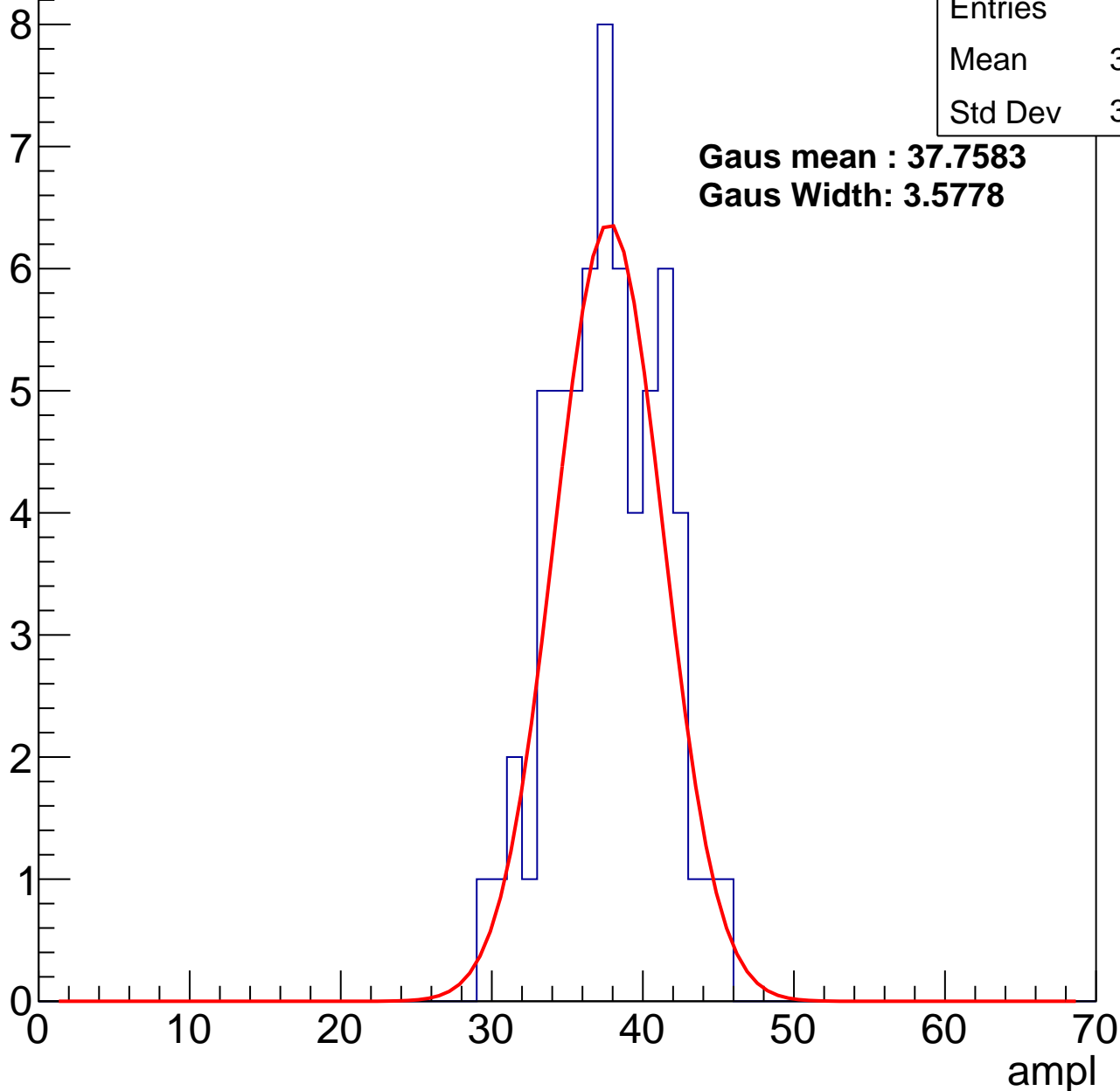
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.18
Std Dev	3.527

Gaus mean : 37.7583

Gaus Width: 3.5778



B1L101S, U25-ch120, adc2

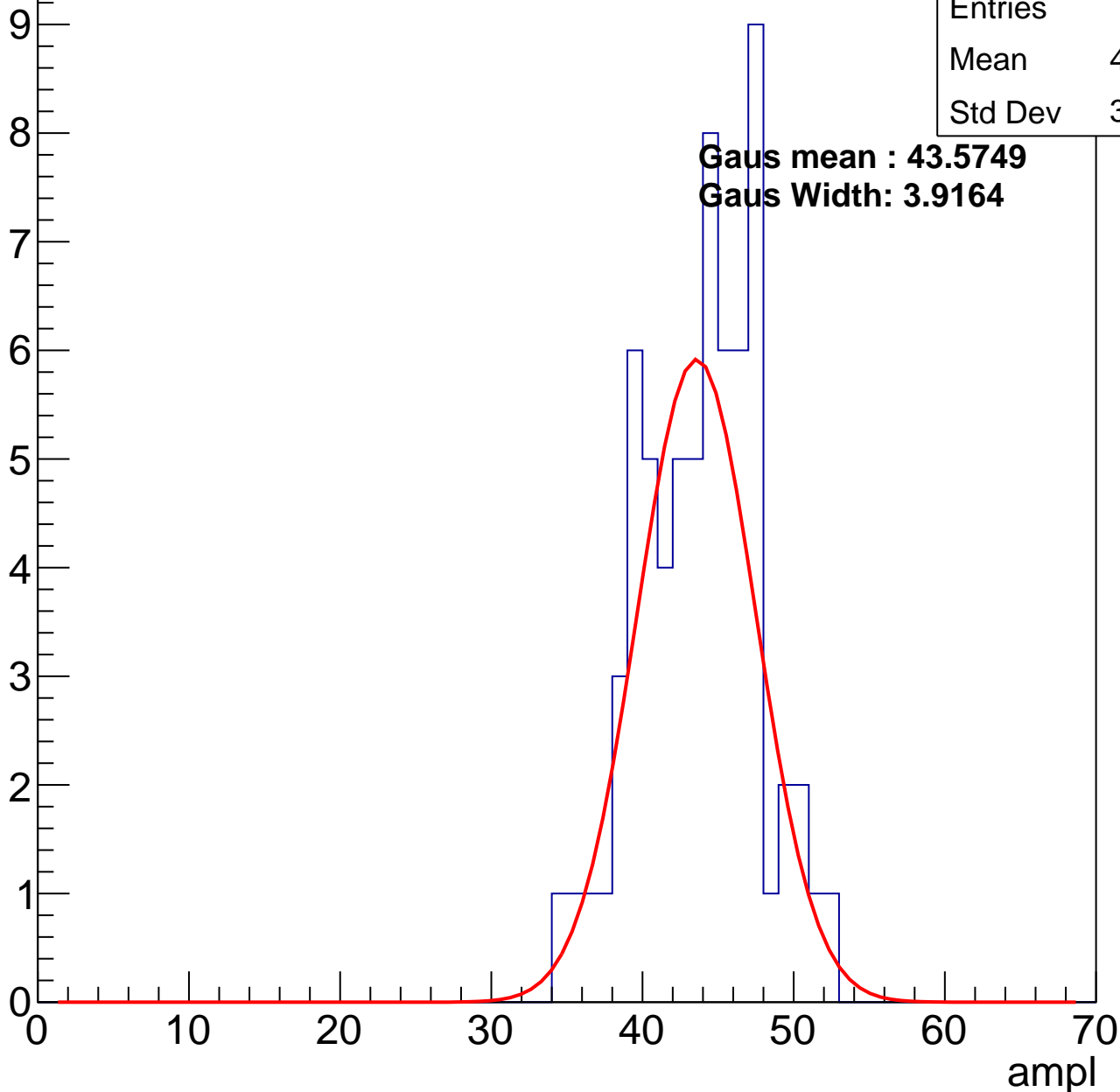
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.37
Std Dev	3.888

Gaus mean : 43.5749

Gaus Width: 3.9164

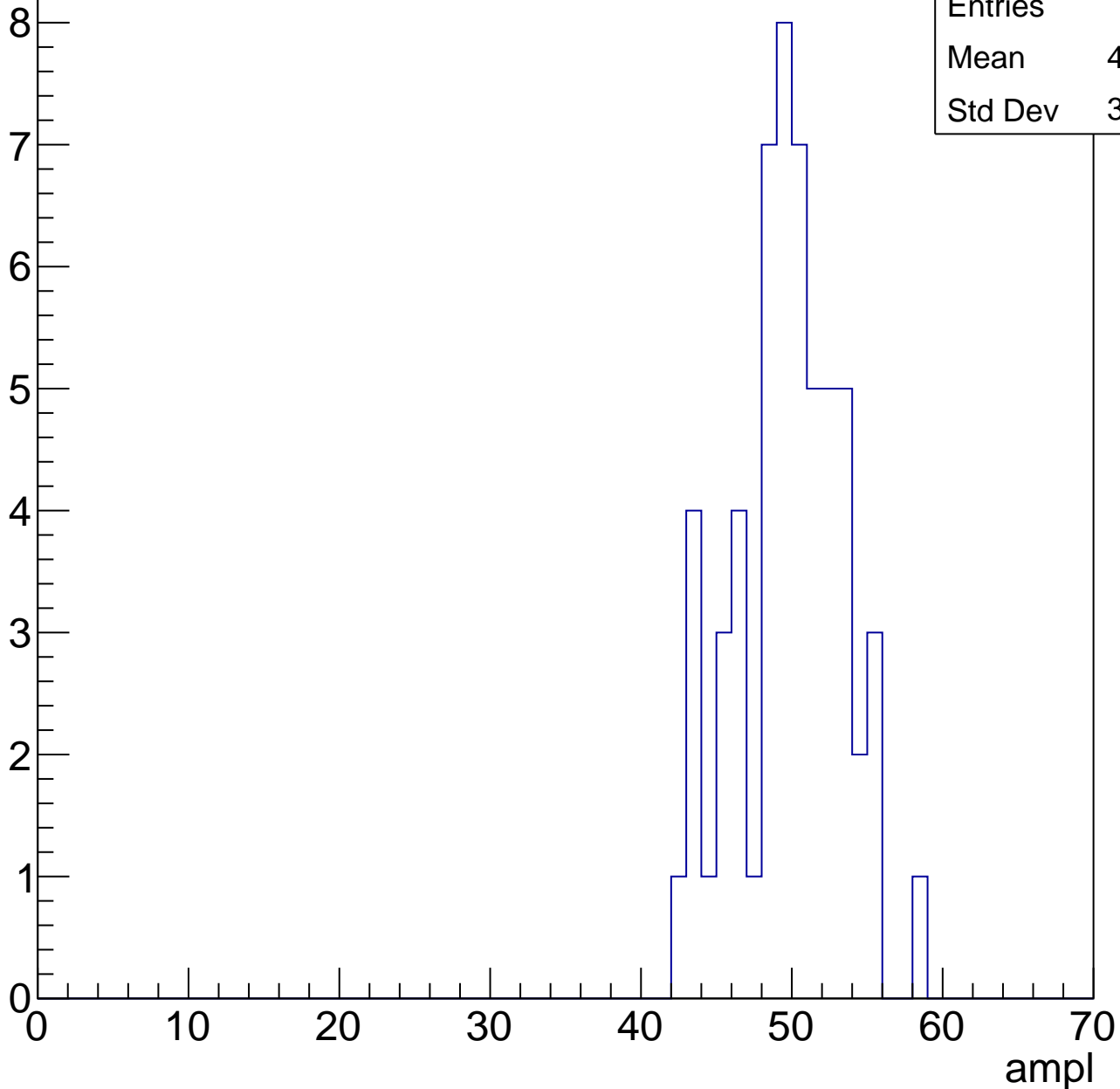


B1L101S, U25-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	49.35
Std Dev	3.497



B1L101S, U25-ch120, adc4

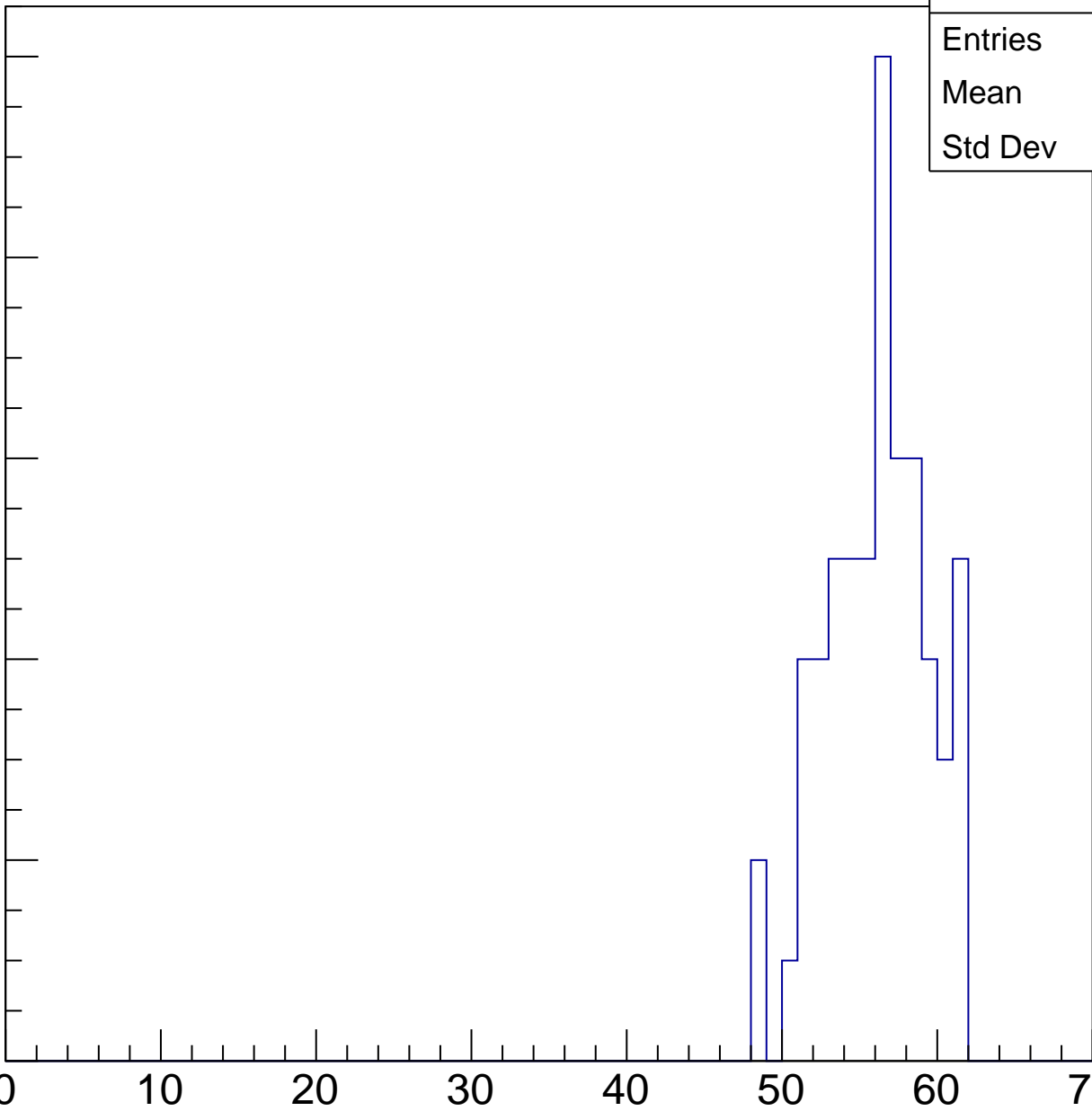
calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	55.65
Std Dev	3.239

Entry

10
8
6
4
2
0

ampl

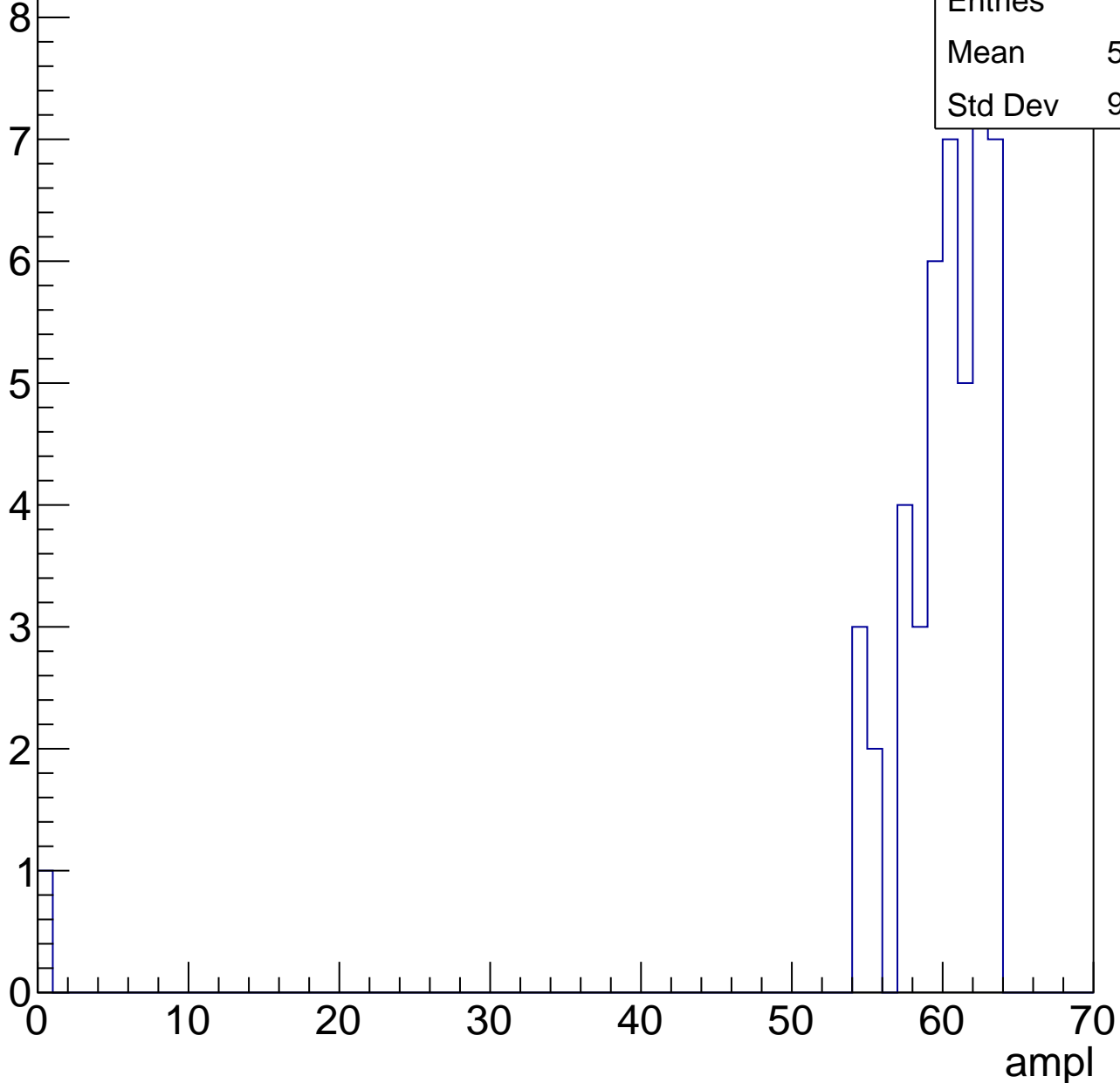


B1L101S, U25-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	58.48
Std Dev	9.095



B1L101S, U25-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch121, adc0

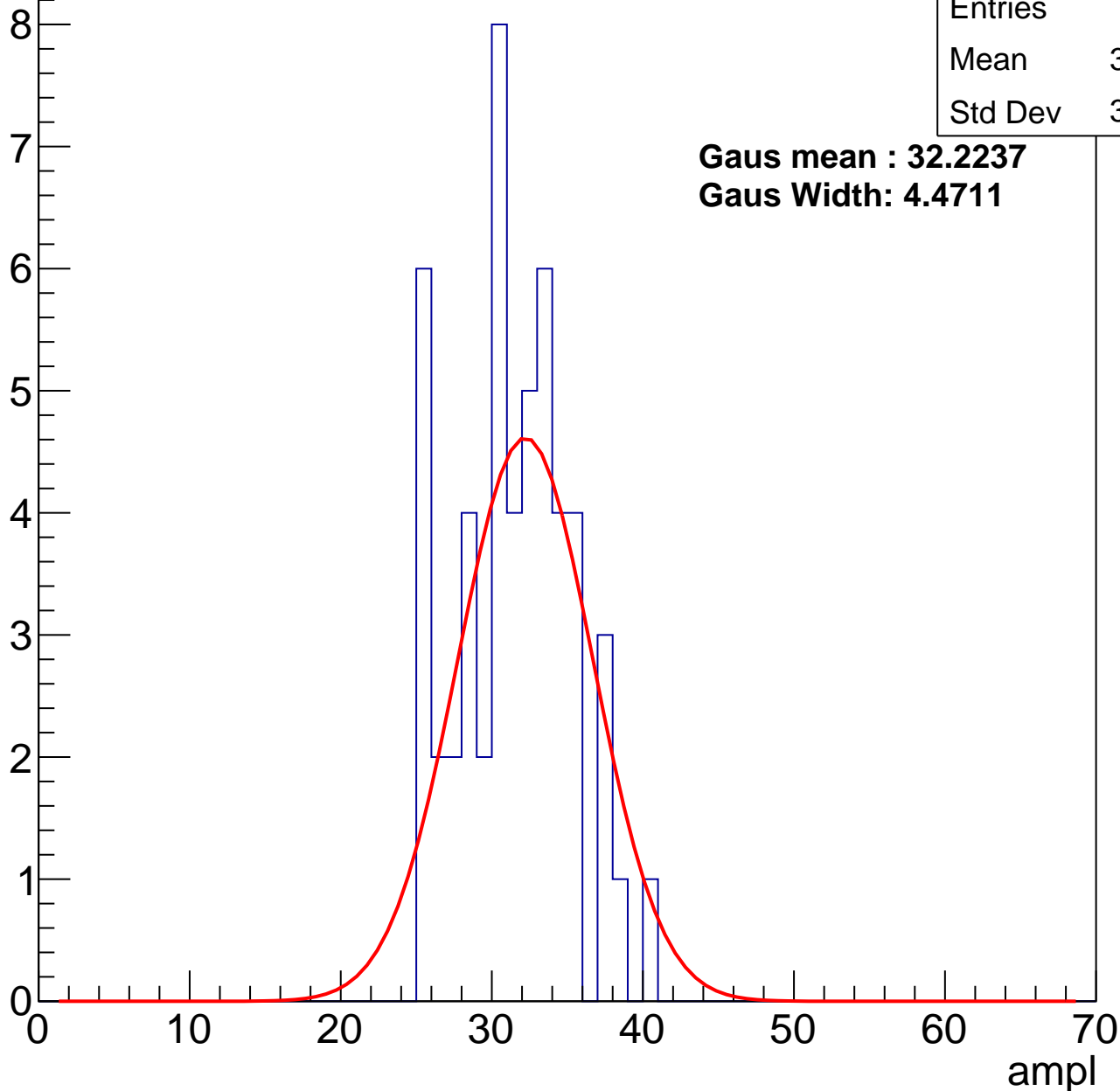
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	31.02
Std Dev	3.713

Gaus mean : 32.2237

Gaus Width: 4.4711



B1L101S, U25-ch121, adc1

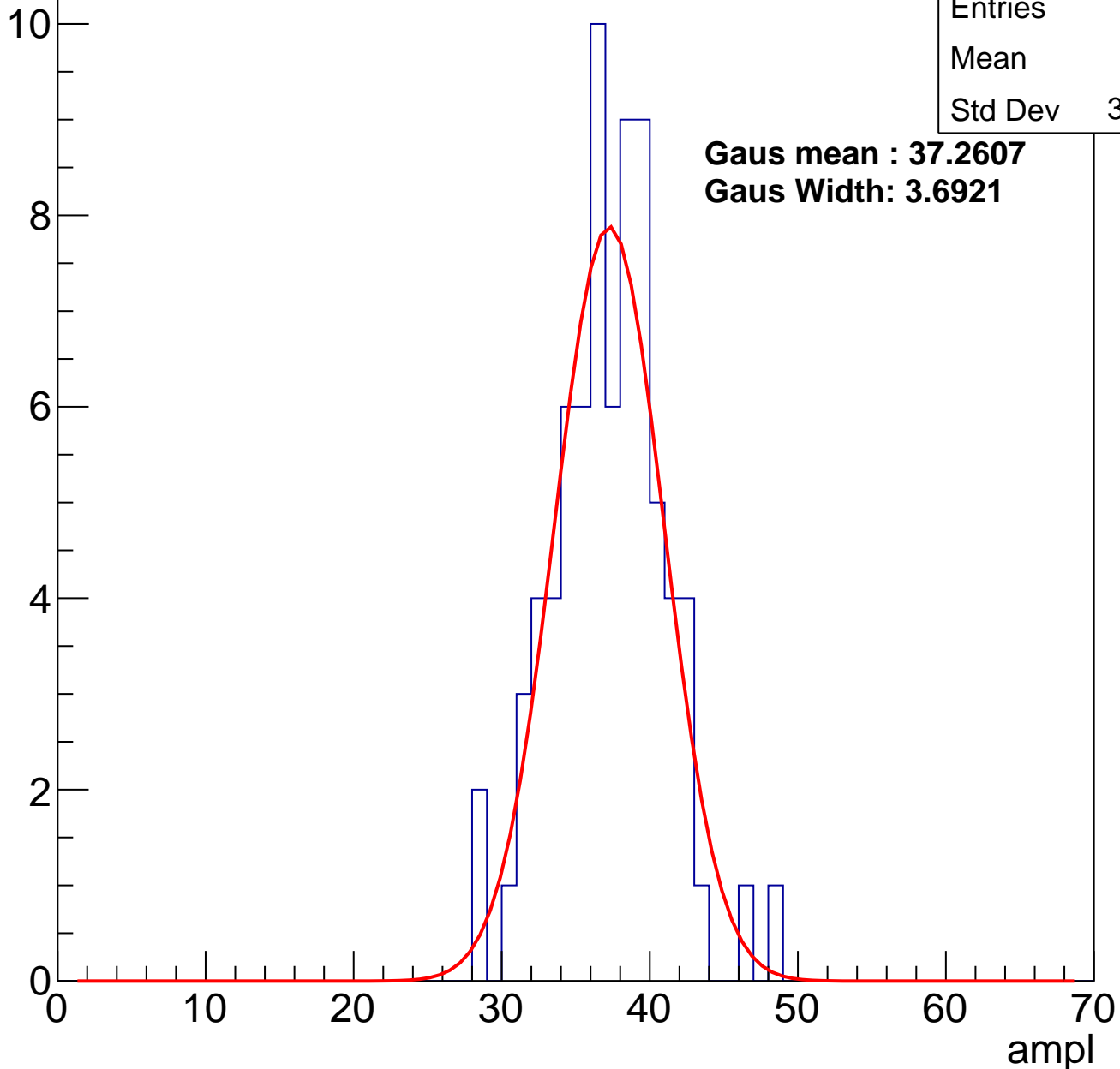
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	36.8
Std Dev	3.735

Gaus mean : 37.2607

Gaus Width: 3.6921

Entry



B1L101S, U25-ch121, adc2

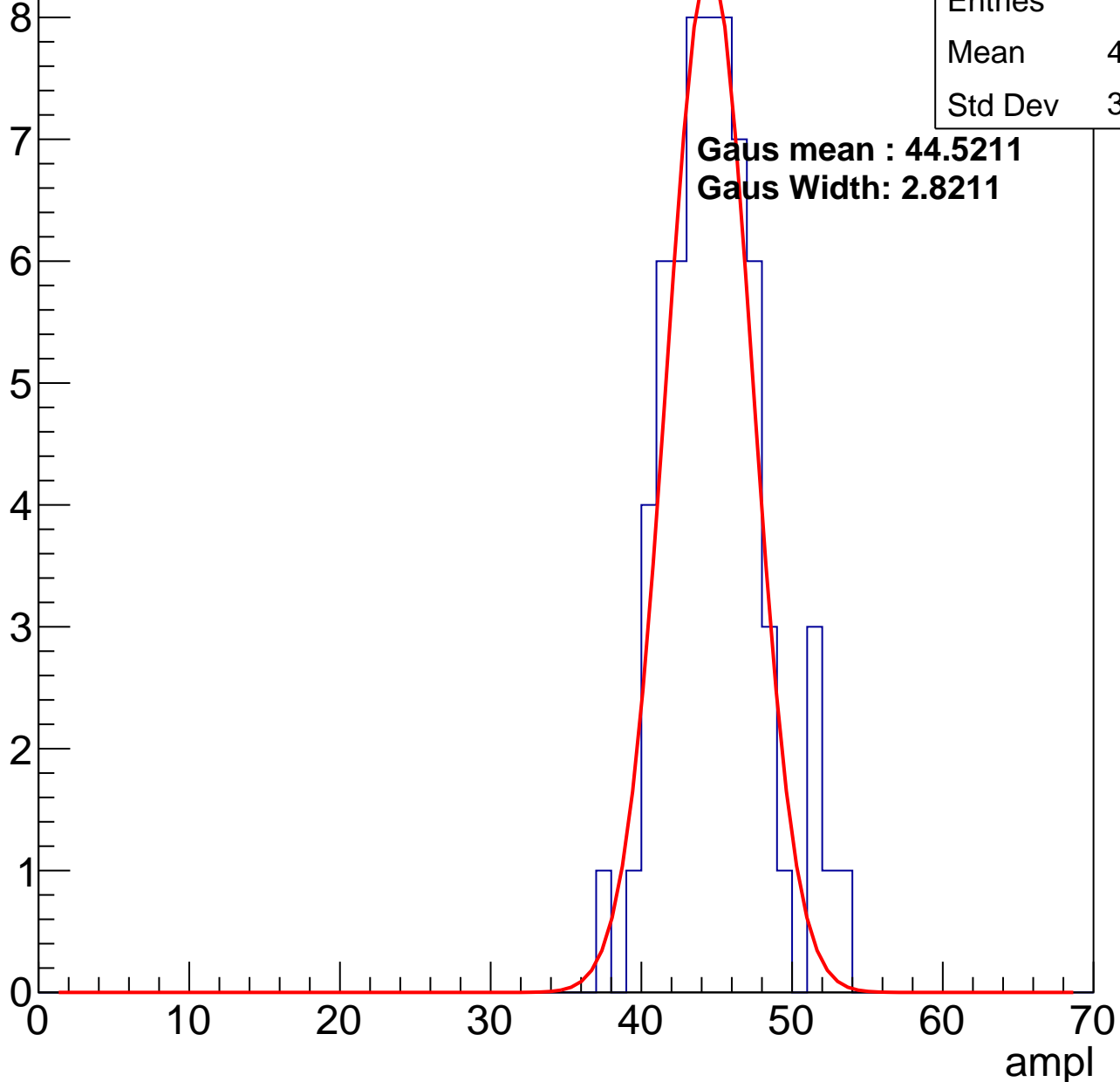
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.45
Std Dev	3.226

Gaus mean : 44.5211

Gaus Width: 2.8211

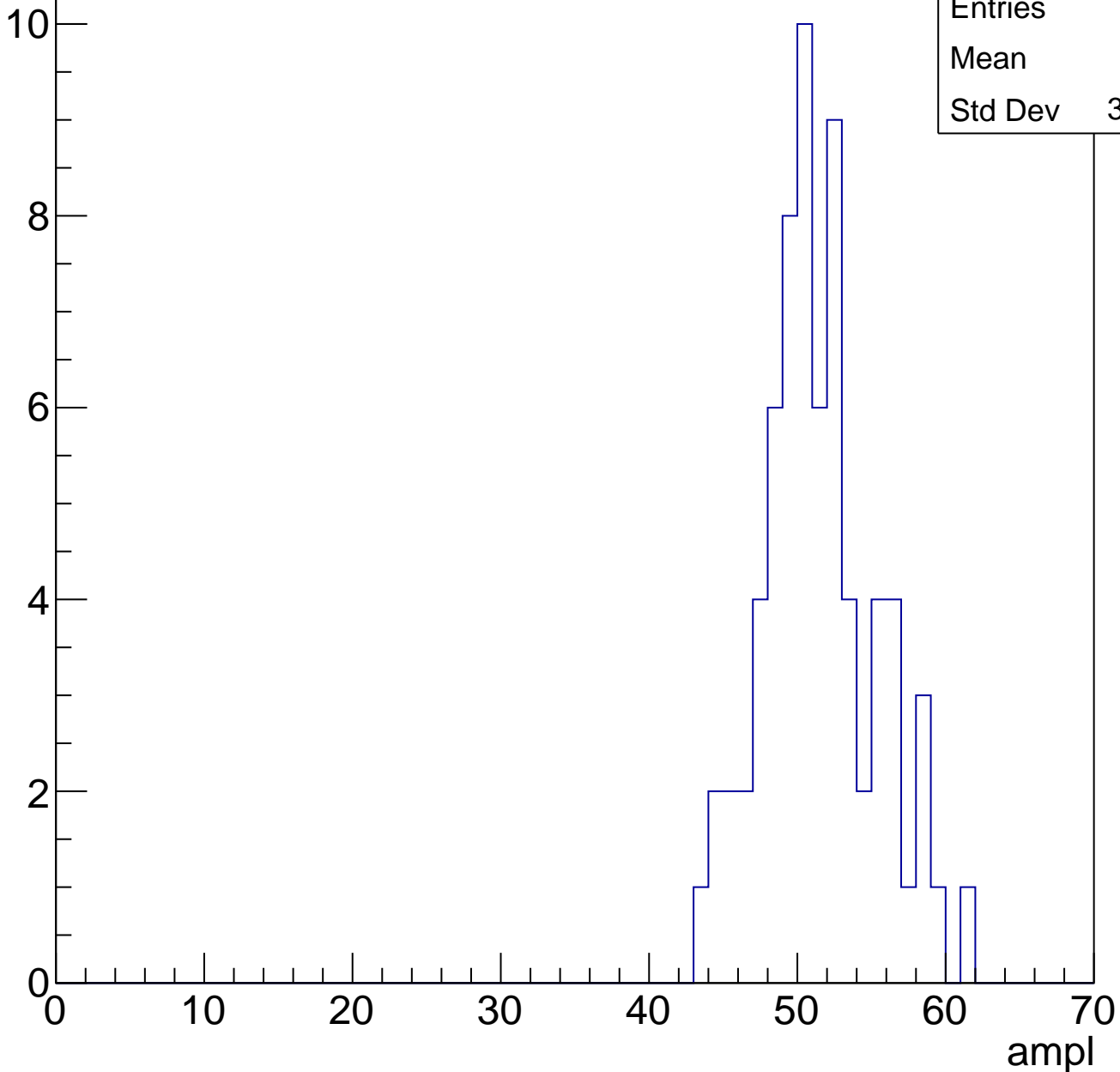


B1L101S, U25-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	51
Std Dev	3.776

Entry

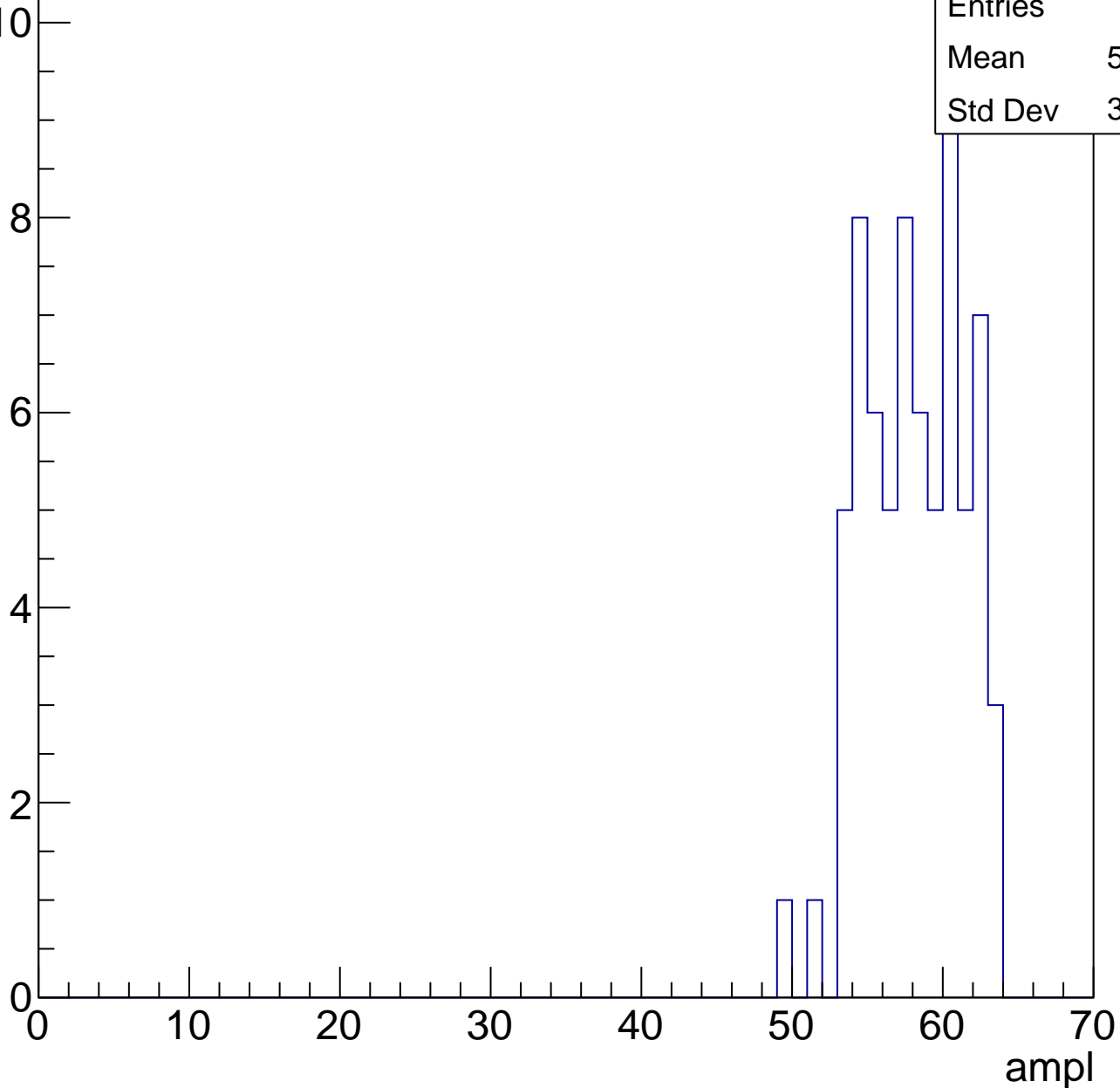


B1L101S, U25-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	57.63
Std Dev	3.235

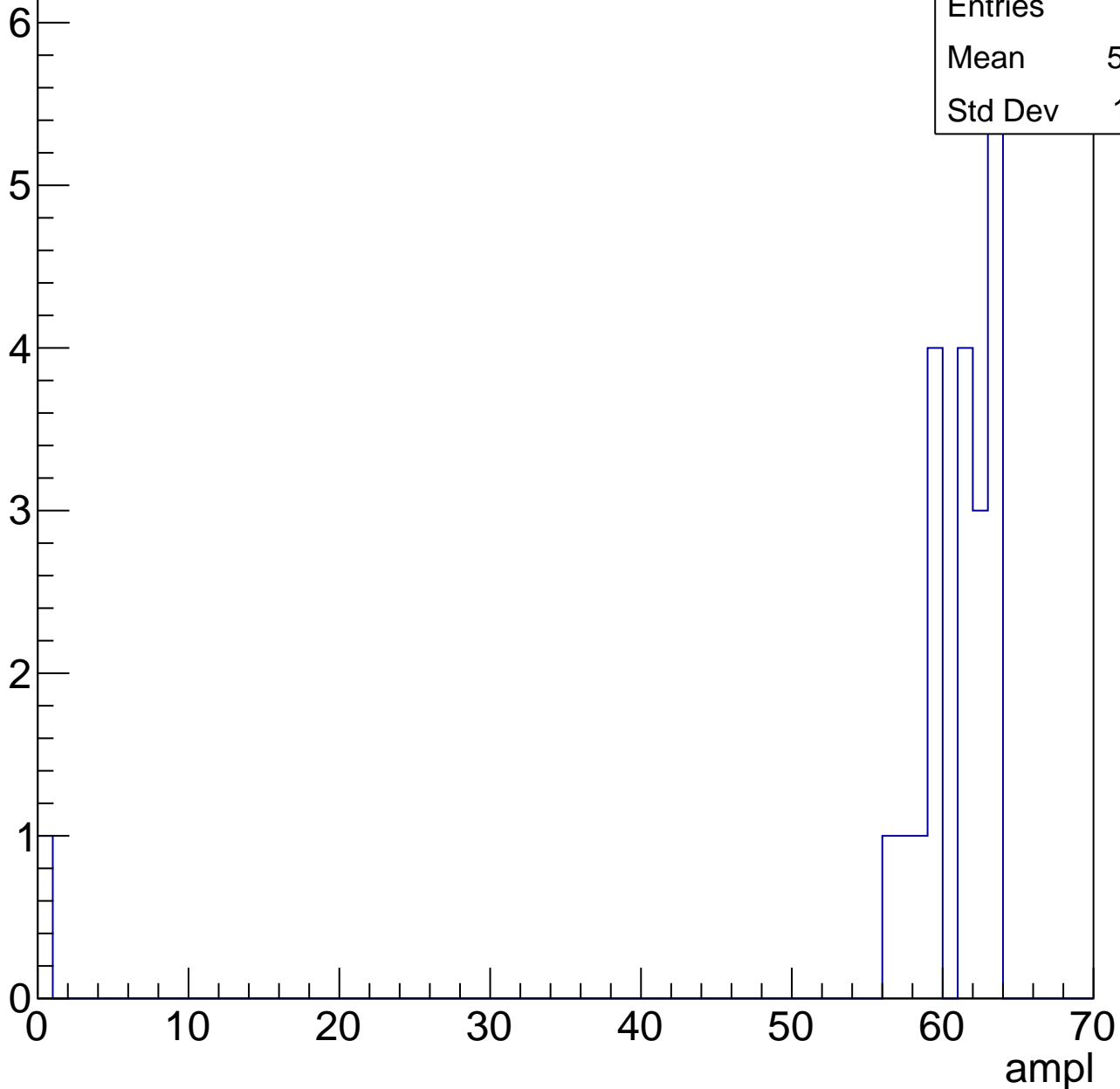


B1L101S, U25-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	57.86
Std Dev	13.11



B1L101S, U25-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch122, adc0

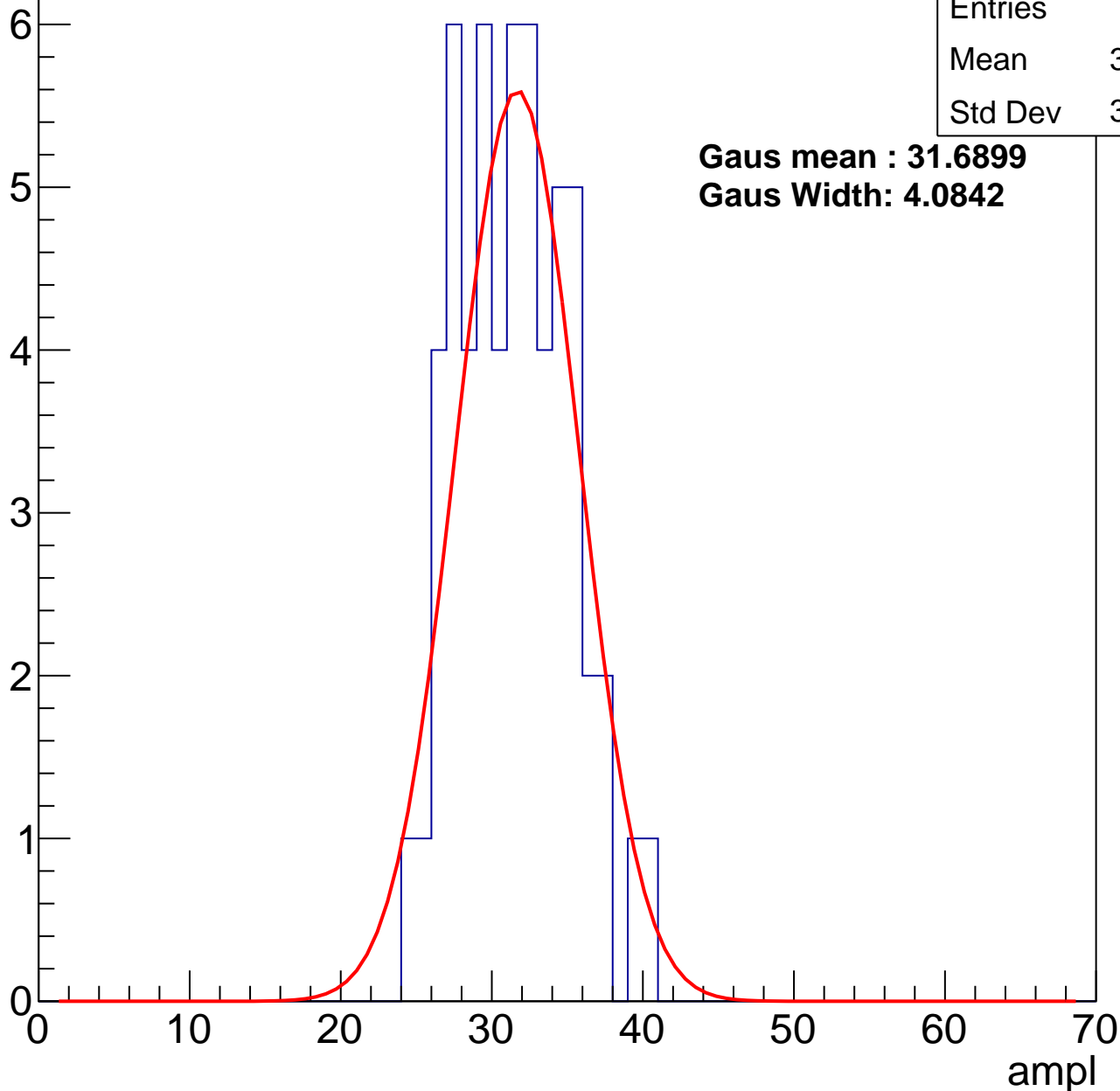
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	31.05
Std Dev	3.617

Gaus mean : 31.6899

Gaus Width: 4.0842



B1L101S, U25-ch122, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	37.9
Std Dev	3.789

Gaus mean : 38.6830

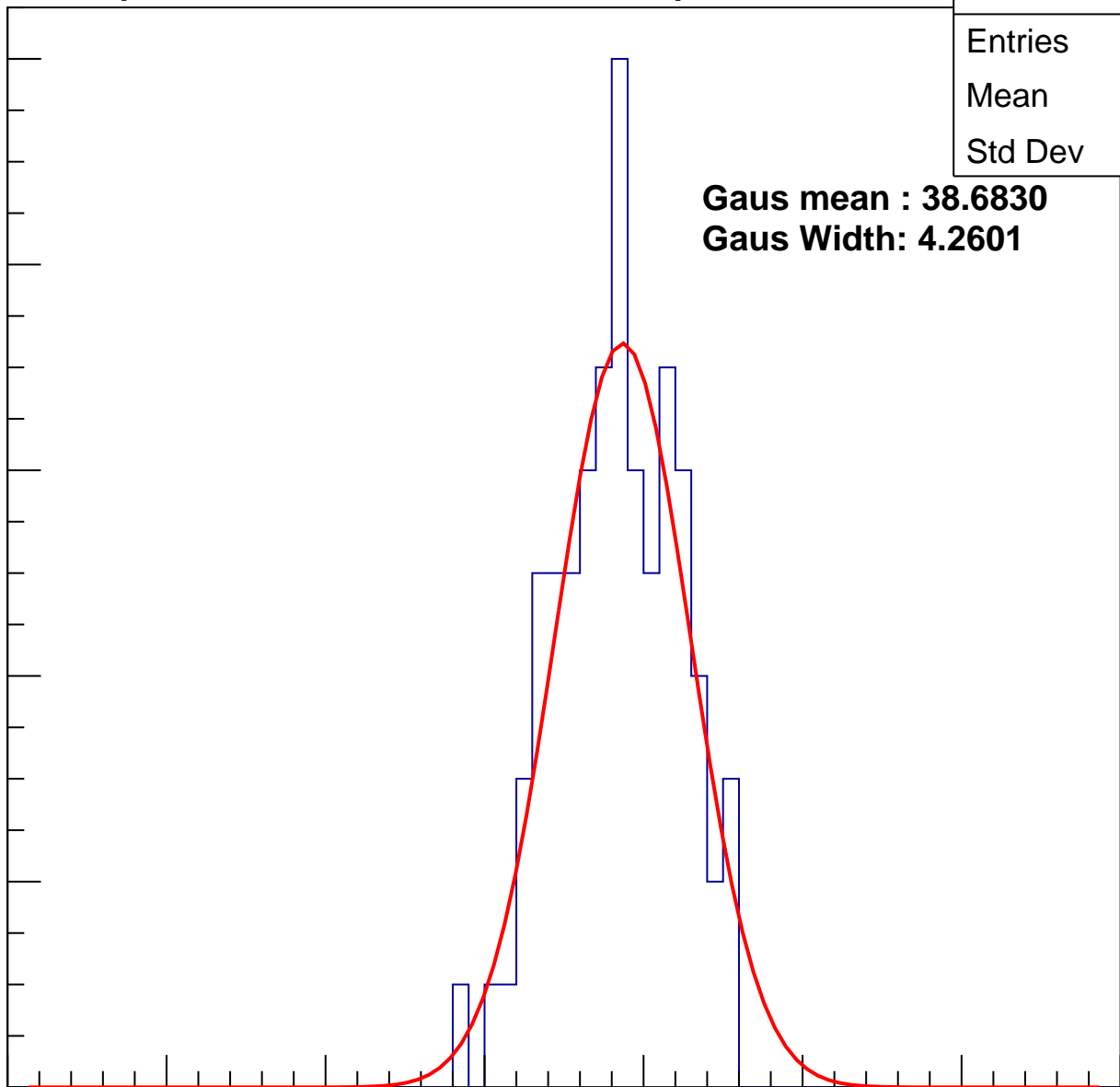
Gaus Width: 4.2601

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch122, adc2

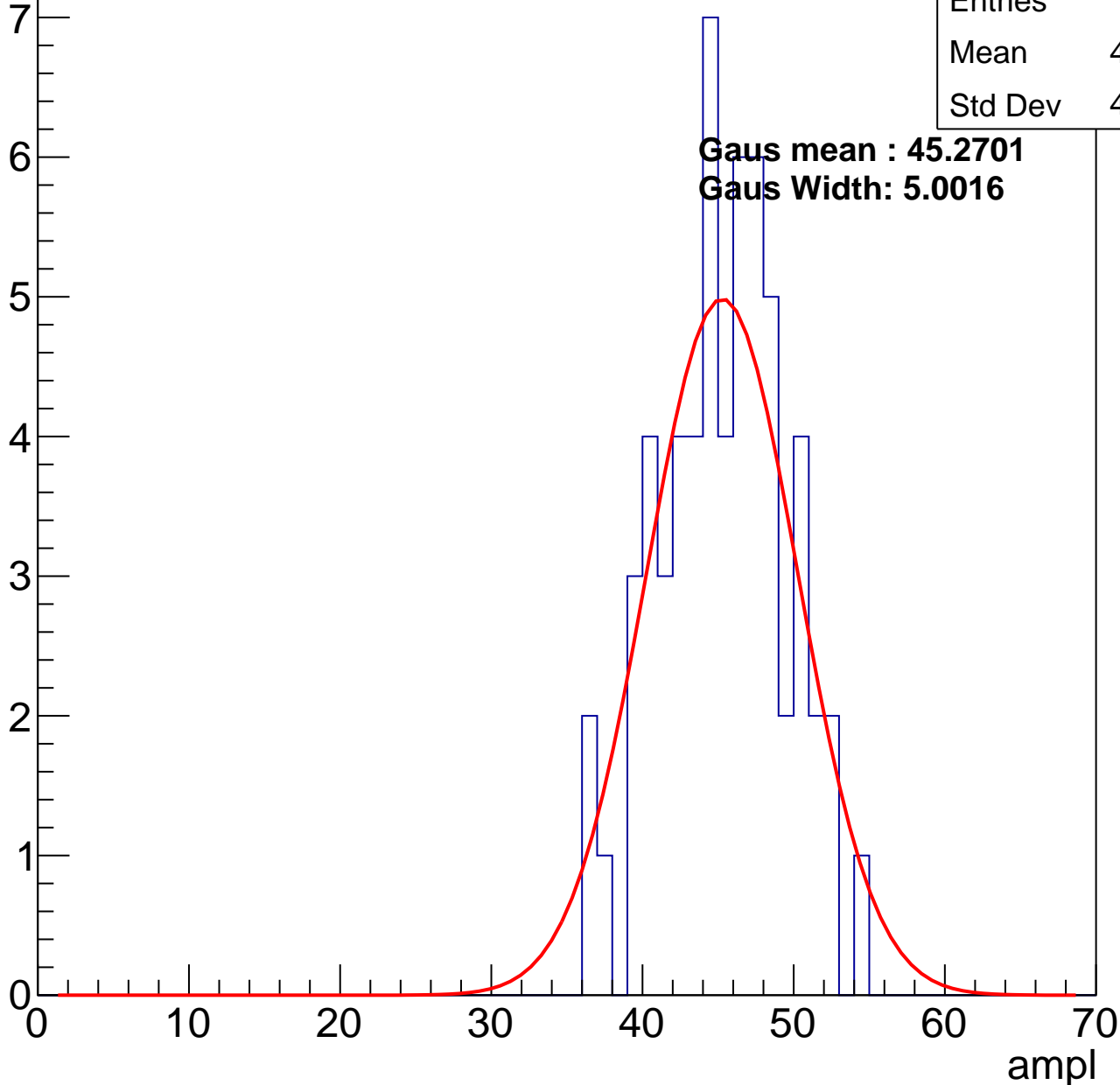
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.88
Std Dev	4.079

Gaus mean : 45.2701

Gaus Width: 5.0016

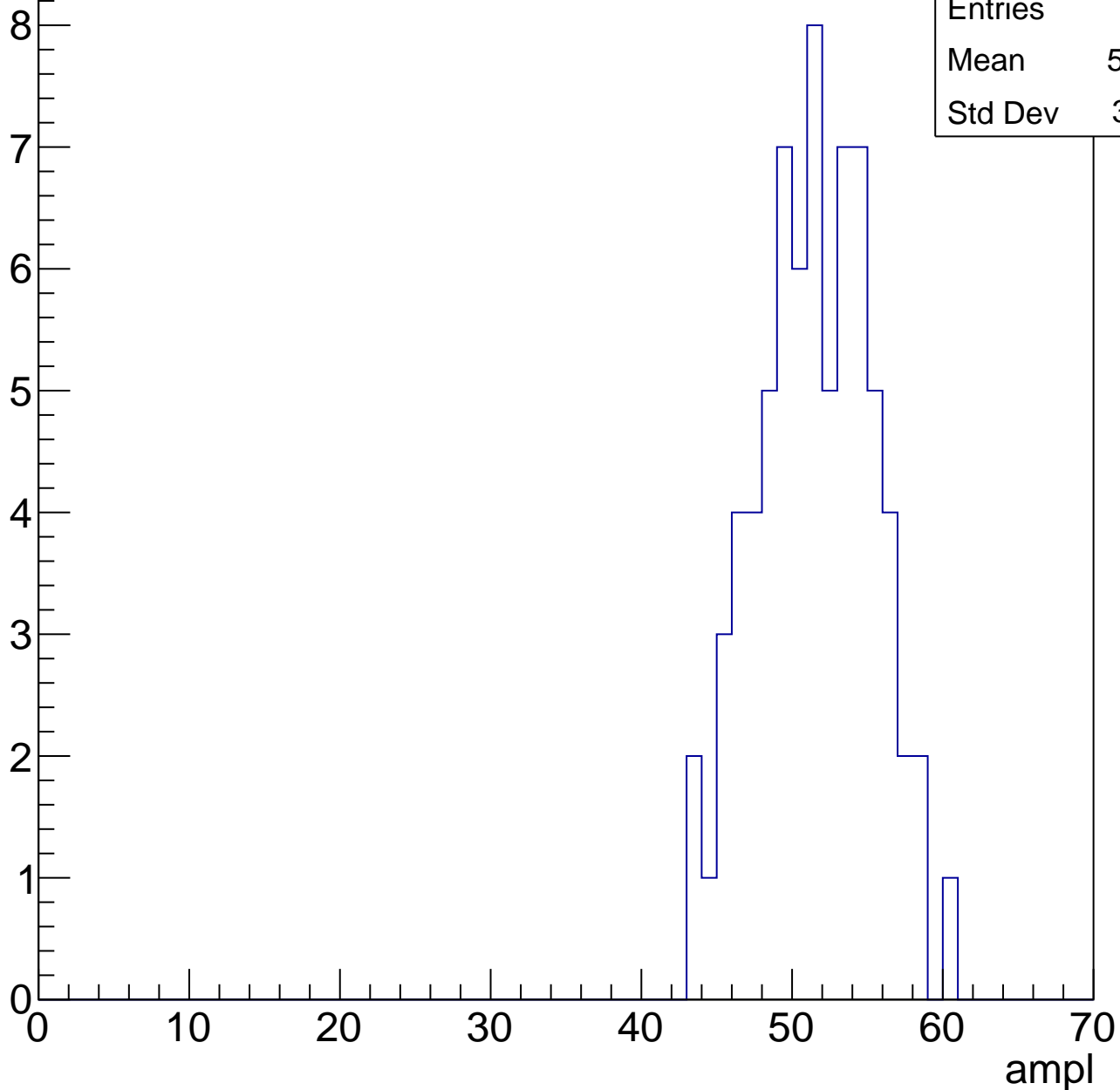


B1L101S, U25-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	51.04
Std Dev	3.801

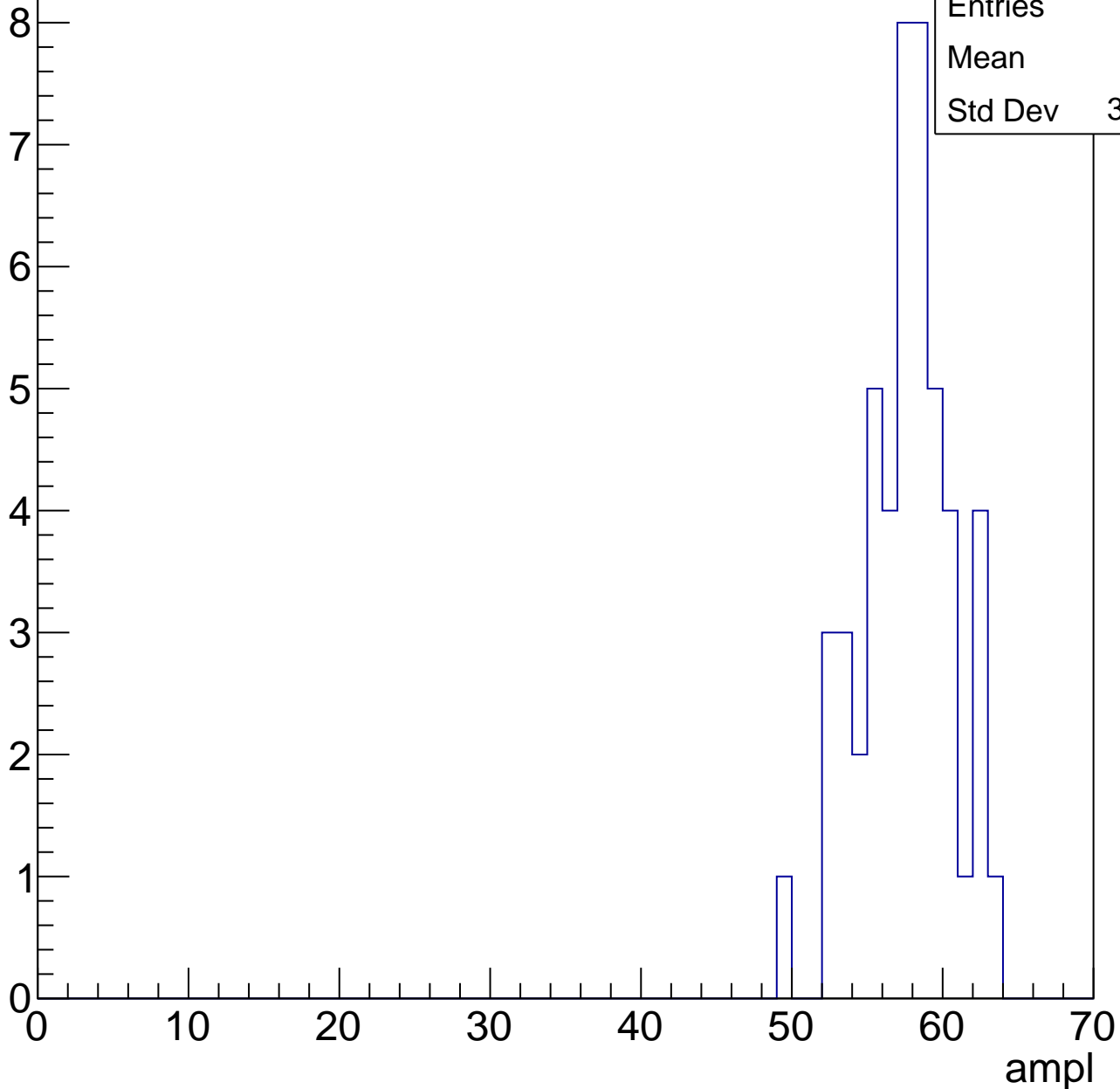


B1L101S, U25-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.1
Std Dev	3.012

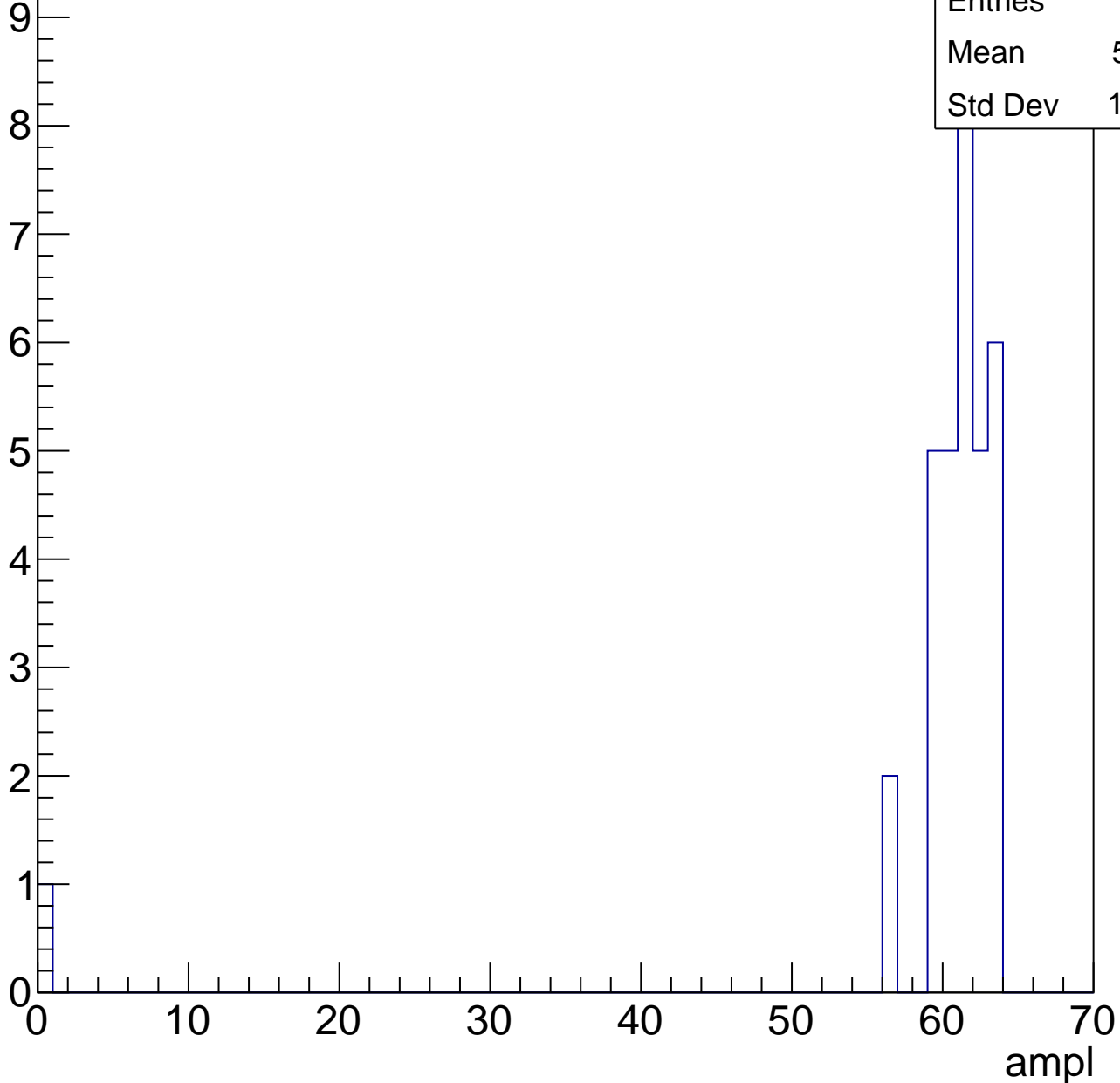


B1L101S, U25-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.91
Std Dev	10.56



B1L101S, U25-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

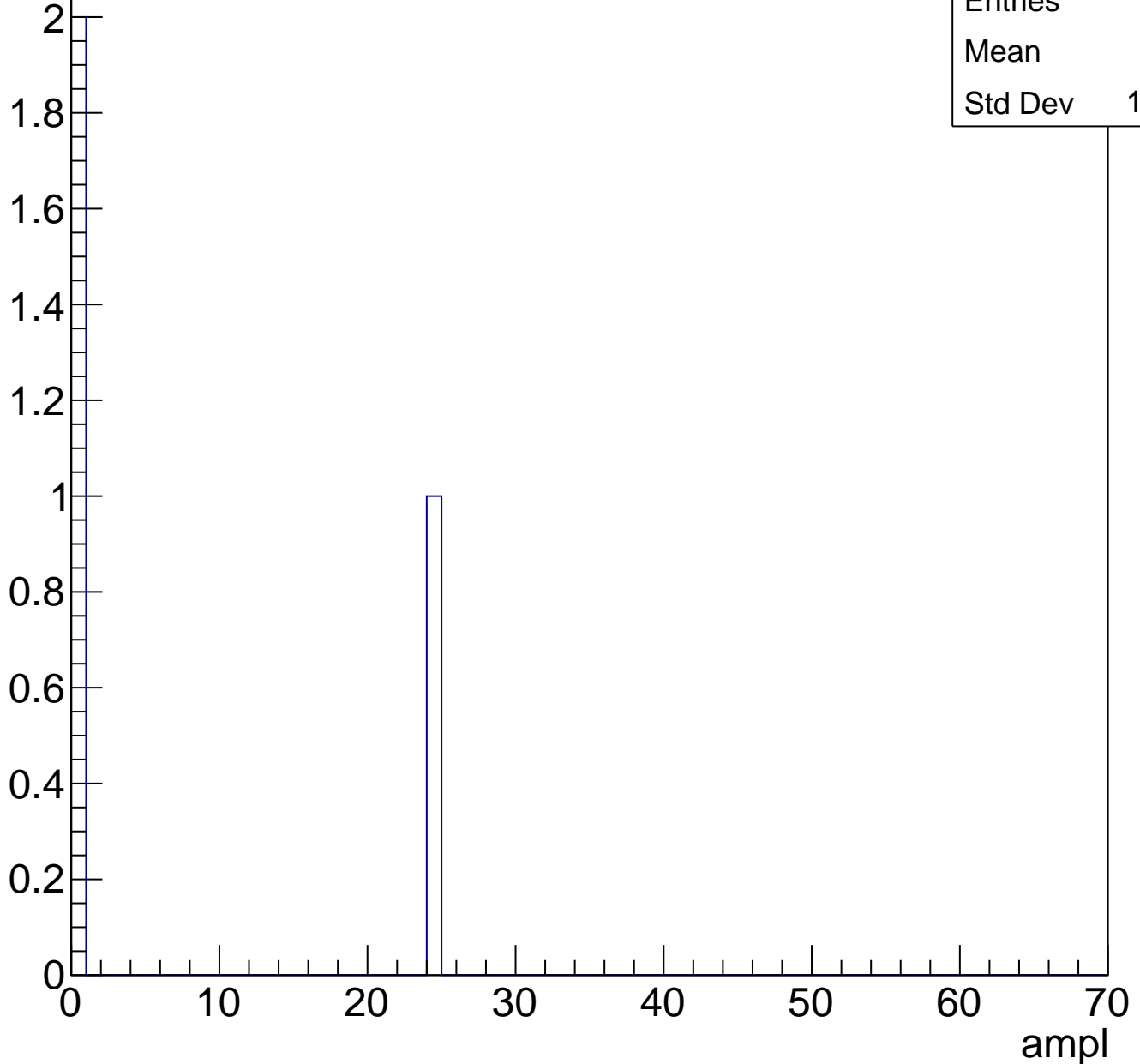


Entries	4
Mean	62.75
Std Dev	0.433

B1L101S, U25-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch123, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	30.31
Std Dev	3.275

Gaus mean : 30.8927

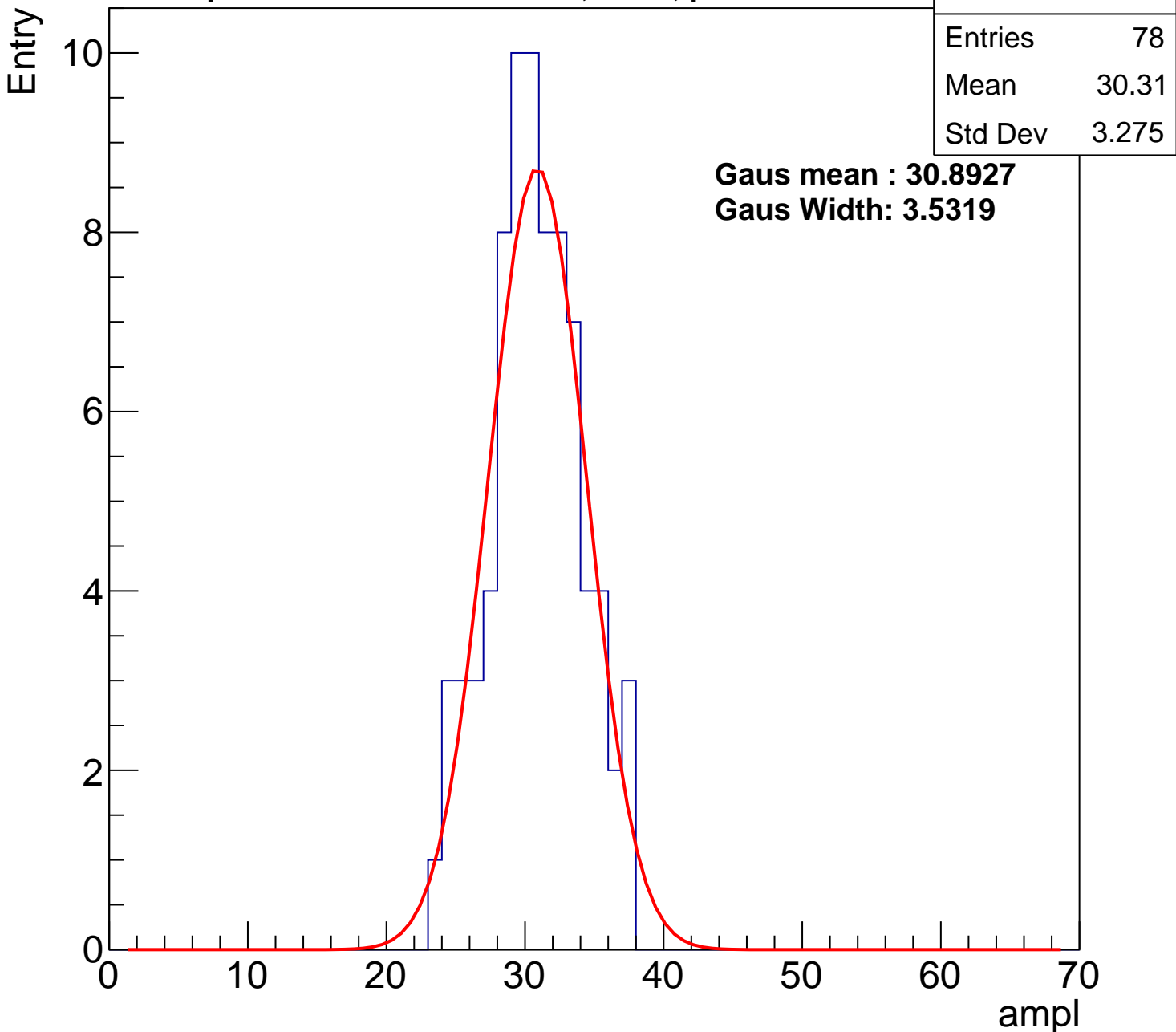
Gaus Width: 3.5319

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U25-ch123, adc1

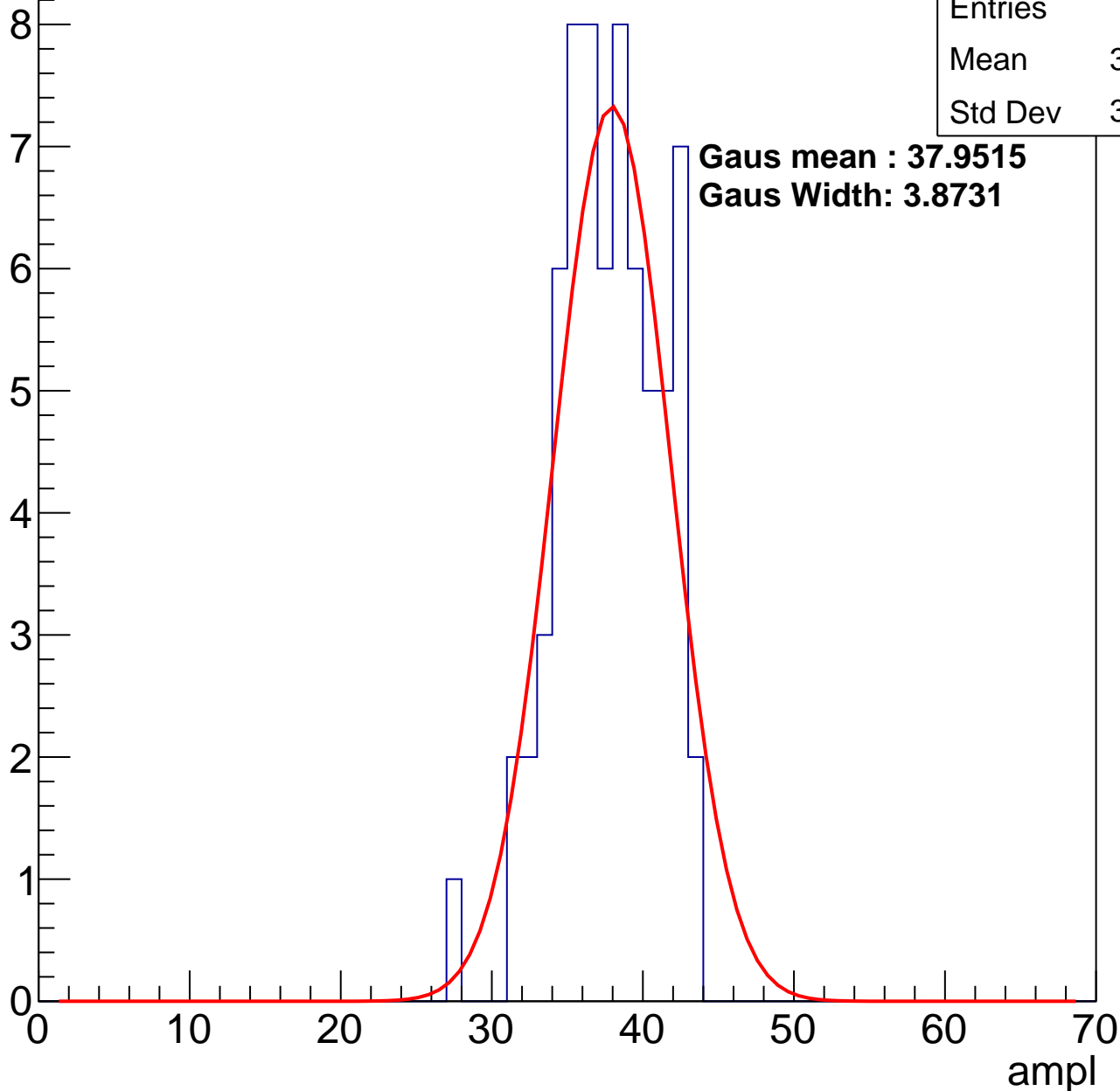
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.23
Std Dev	3.328

Gaus mean : 37.9515

Gaus Width: 3.8731



B1L101S, U25-ch123, adc2

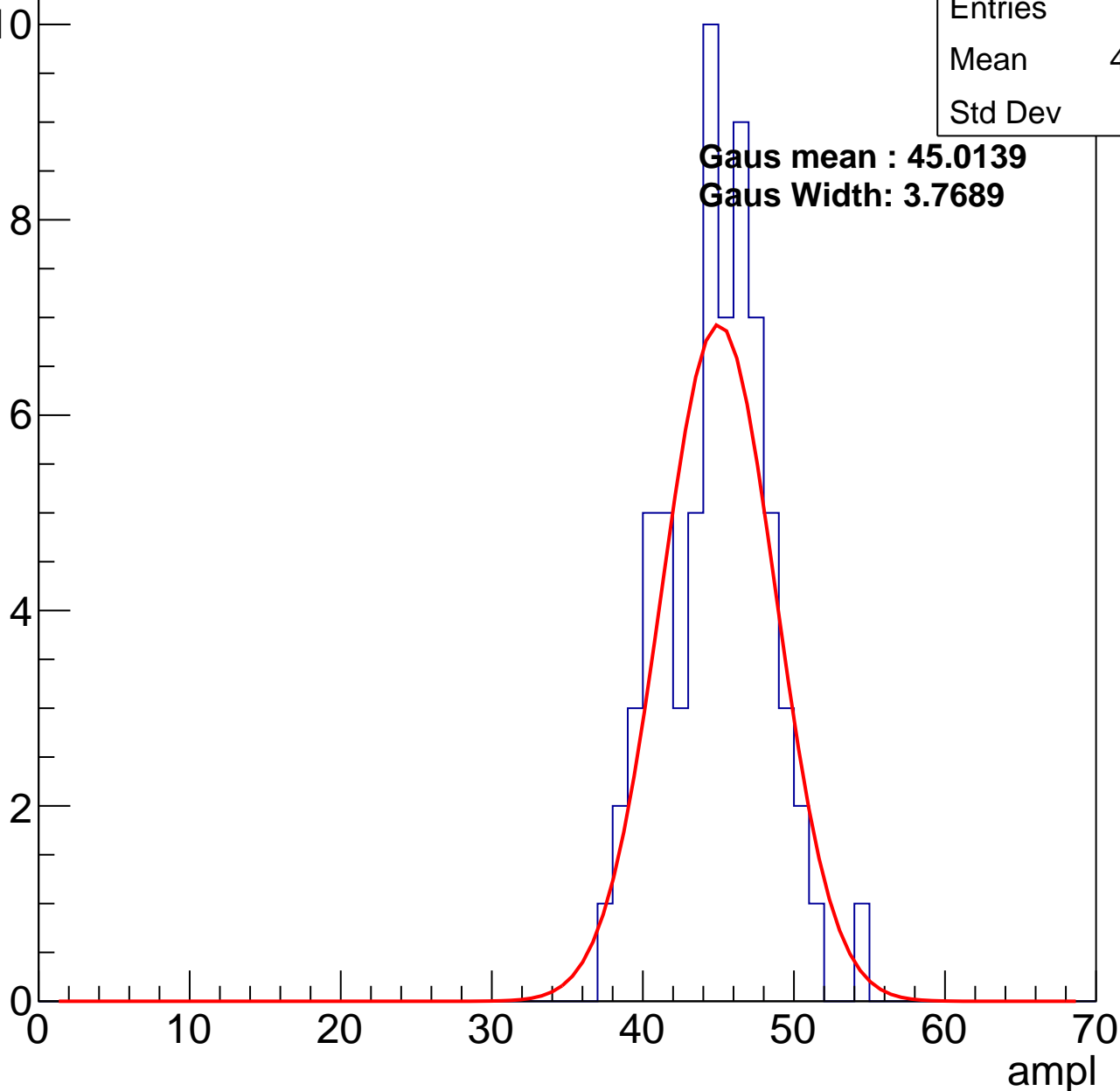
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.43
Std Dev	3.42

Gaus mean : 45.0139

Gaus Width: 3.7689

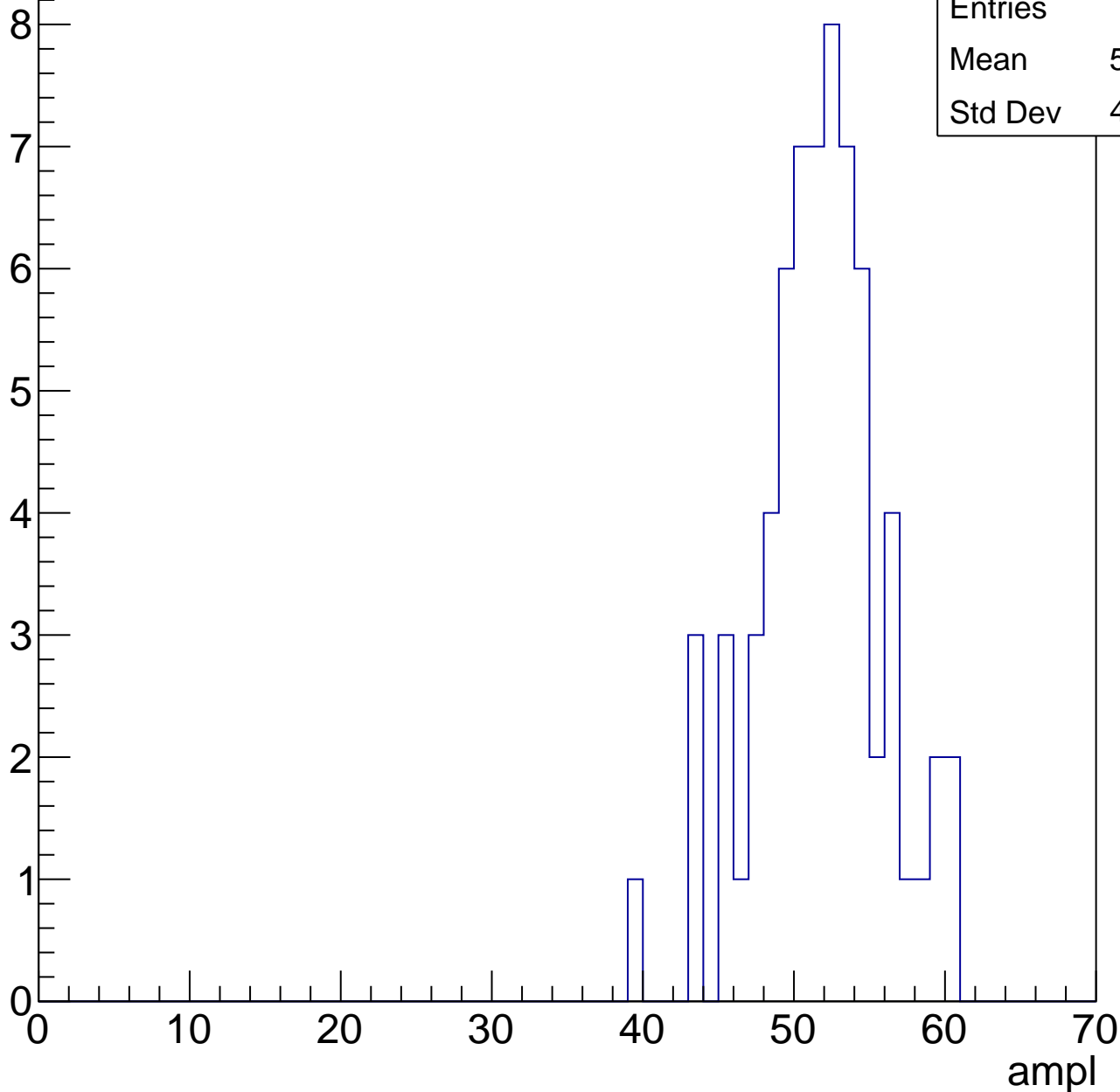


B1L101S, U25-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	51.19
Std Dev	4.163

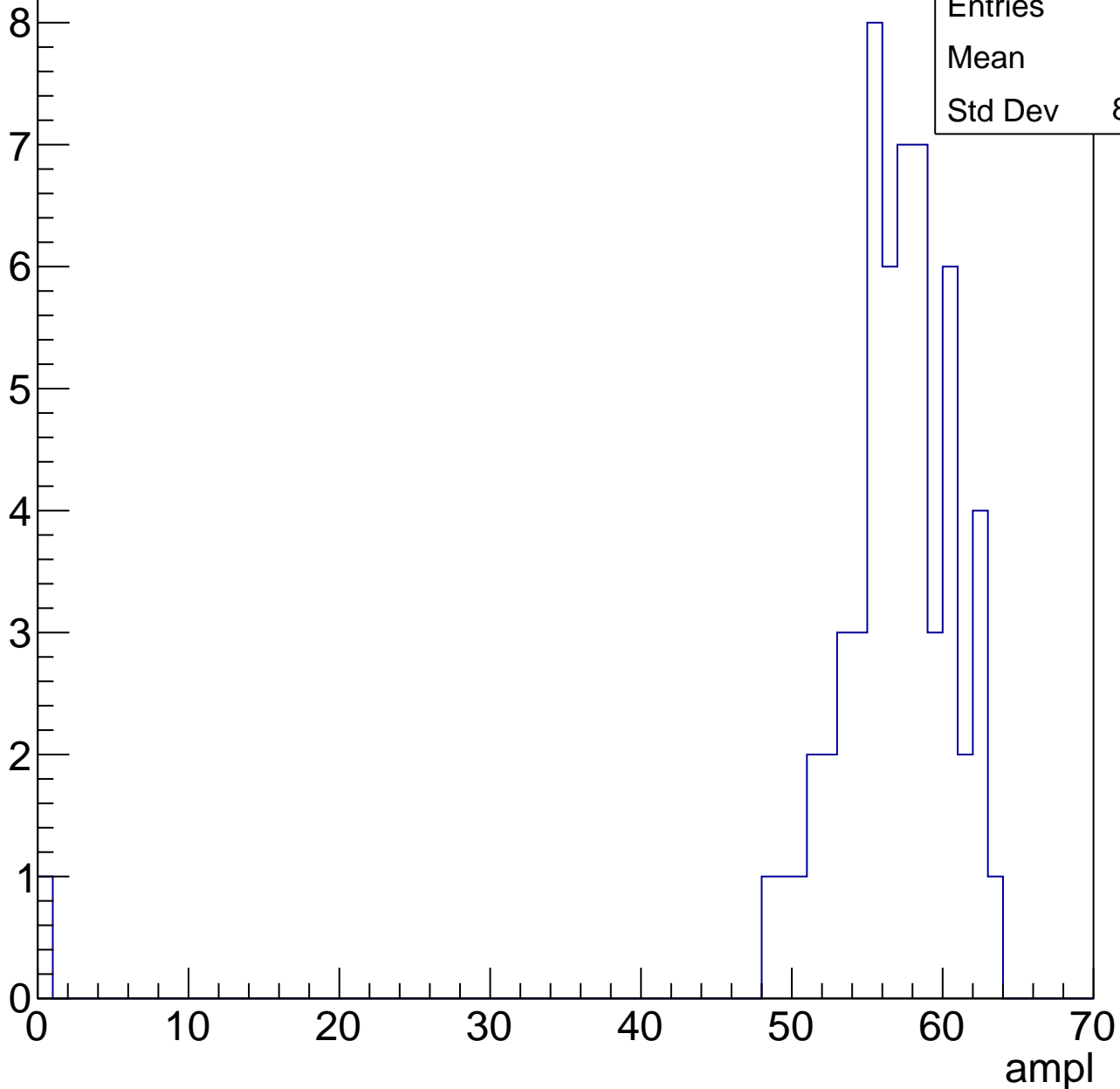


B1L101S, U25-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.6
Std Dev	8.101

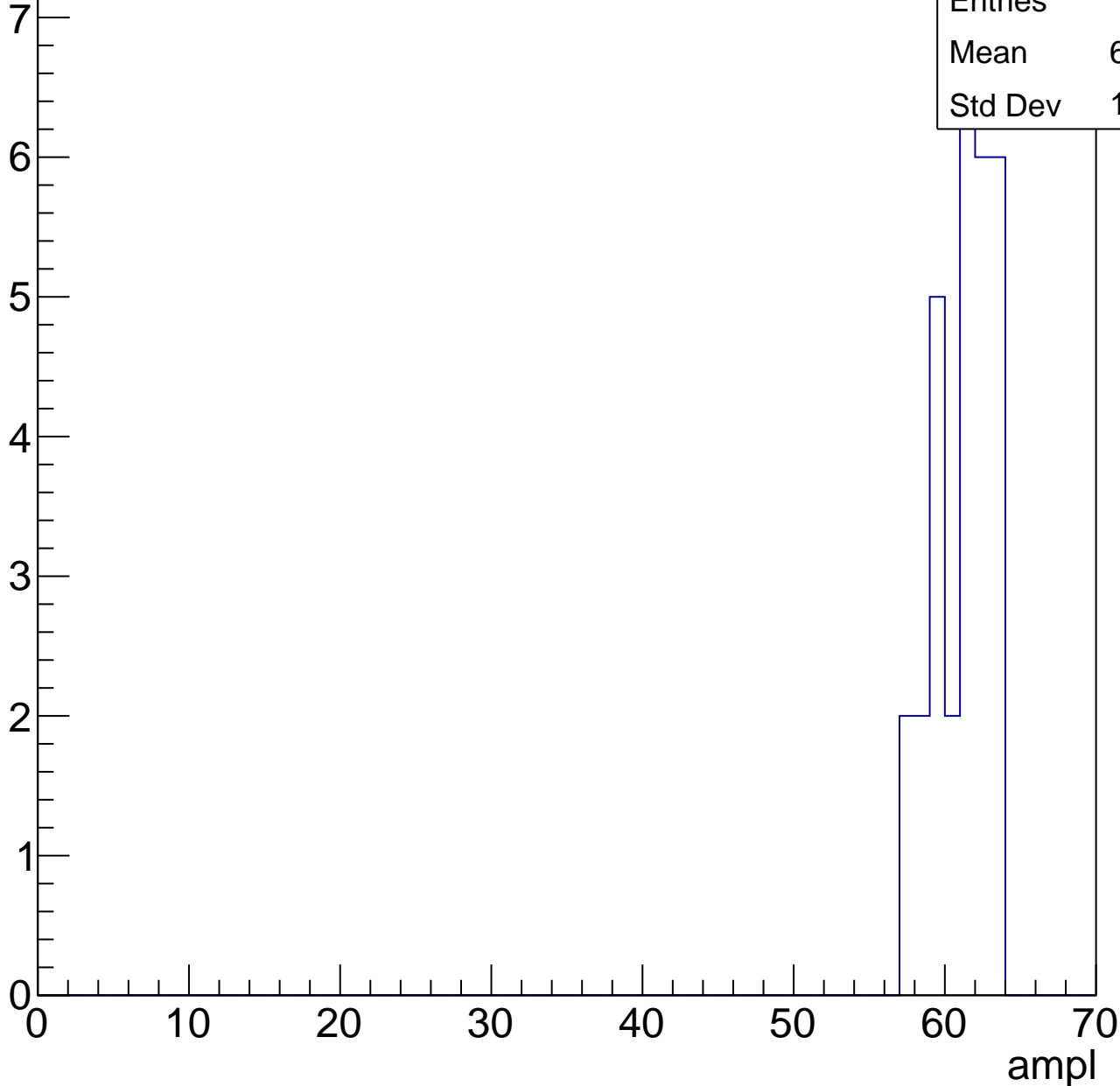


B1L101S, U25-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	60.73
Std Dev	1.825



B1L101S, U25-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

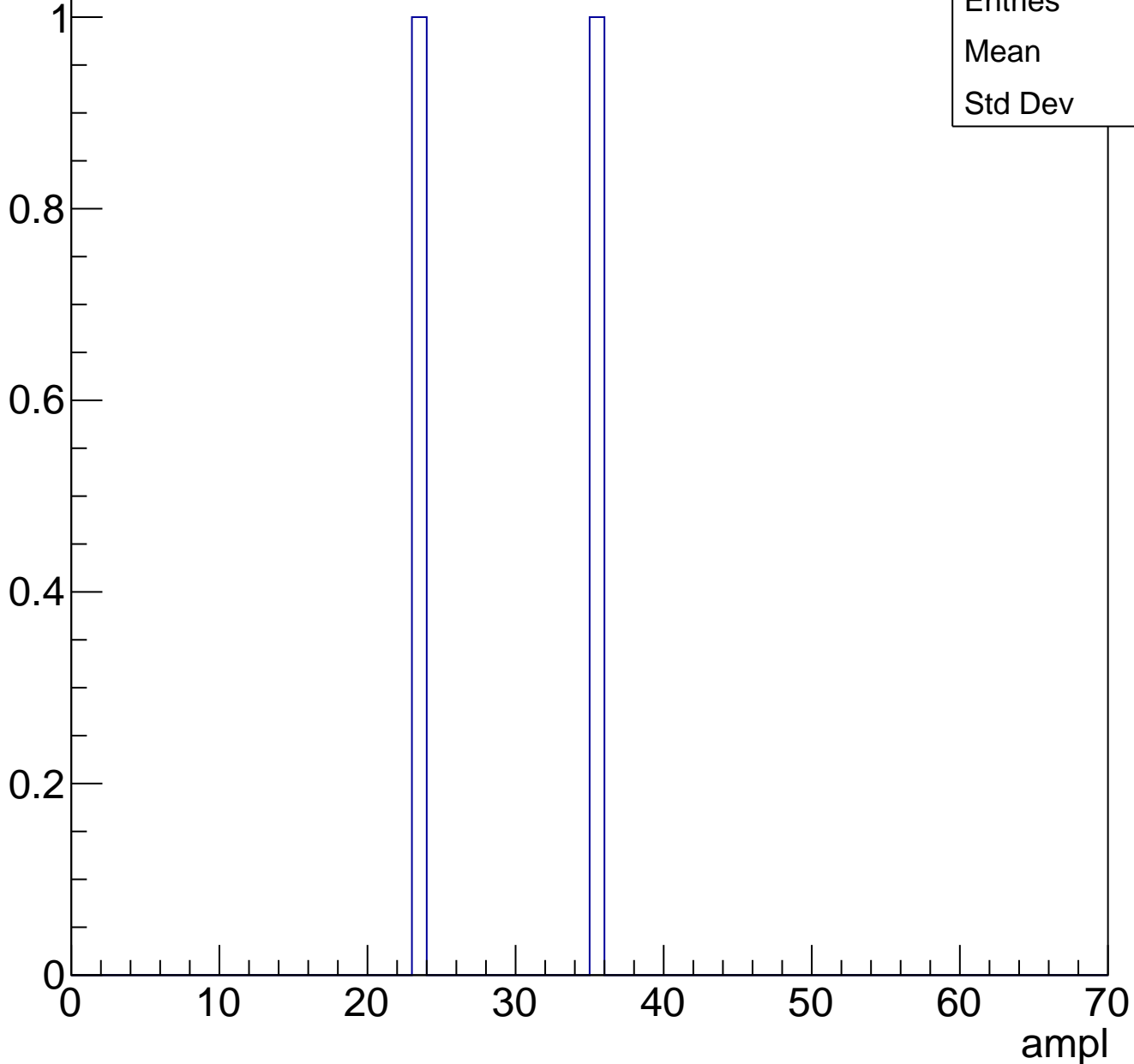
Entry



B1L101S, U25-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch124, adc0

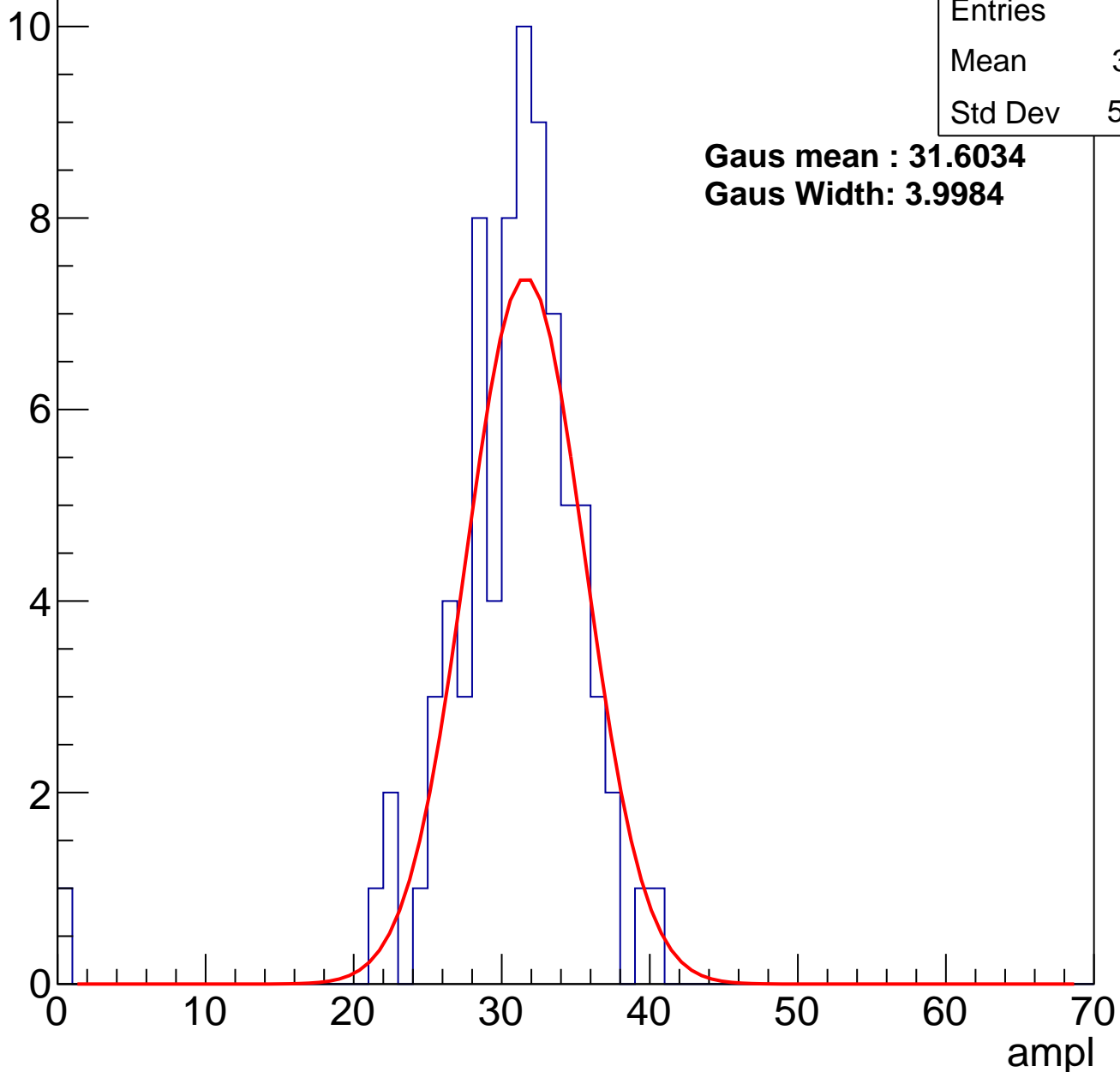
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	30.31
Std Dev	5.115

Gaus mean : 31.6034

Gaus Width: 3.9984

Entry



B1L101S, U25-ch124, adc1

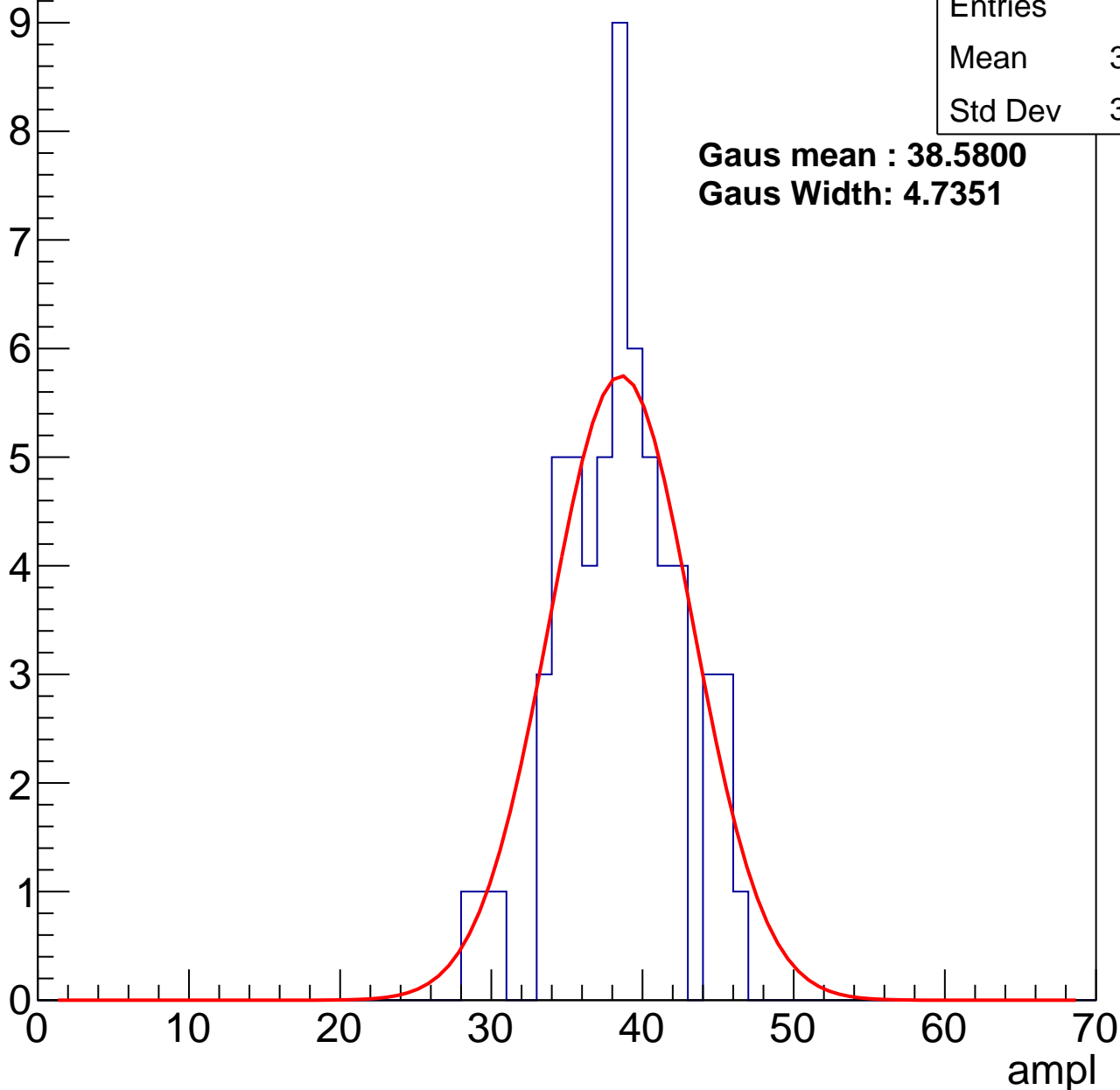
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	38.02
Std Dev	3.897

Gaus mean : 38.5800

Gaus Width: 4.7351



B1L101S, U25-ch124, adc2

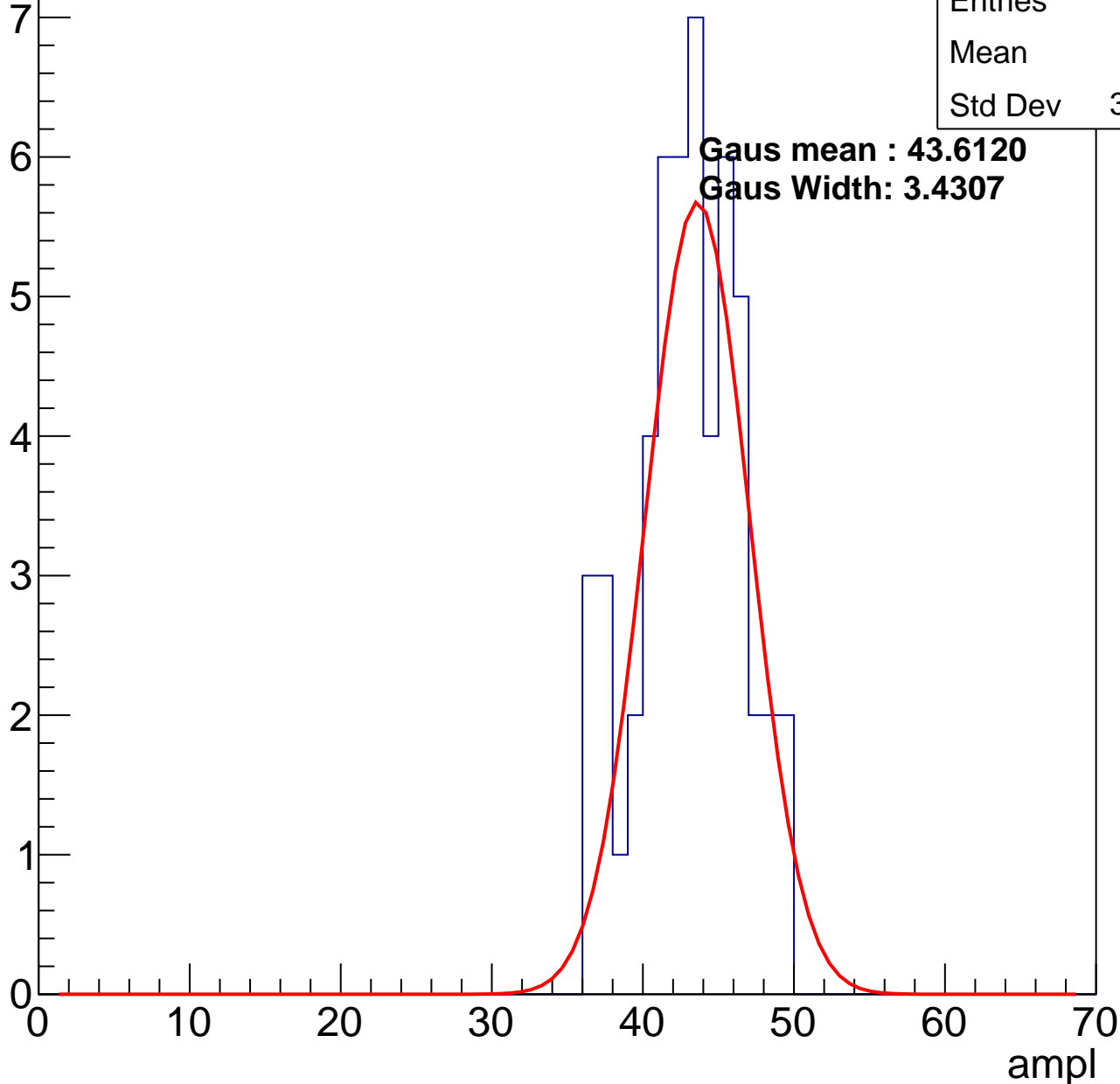
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.6
Std Dev	3.355

Gaus mean : 43.6120

Gaus Width: 3.4307

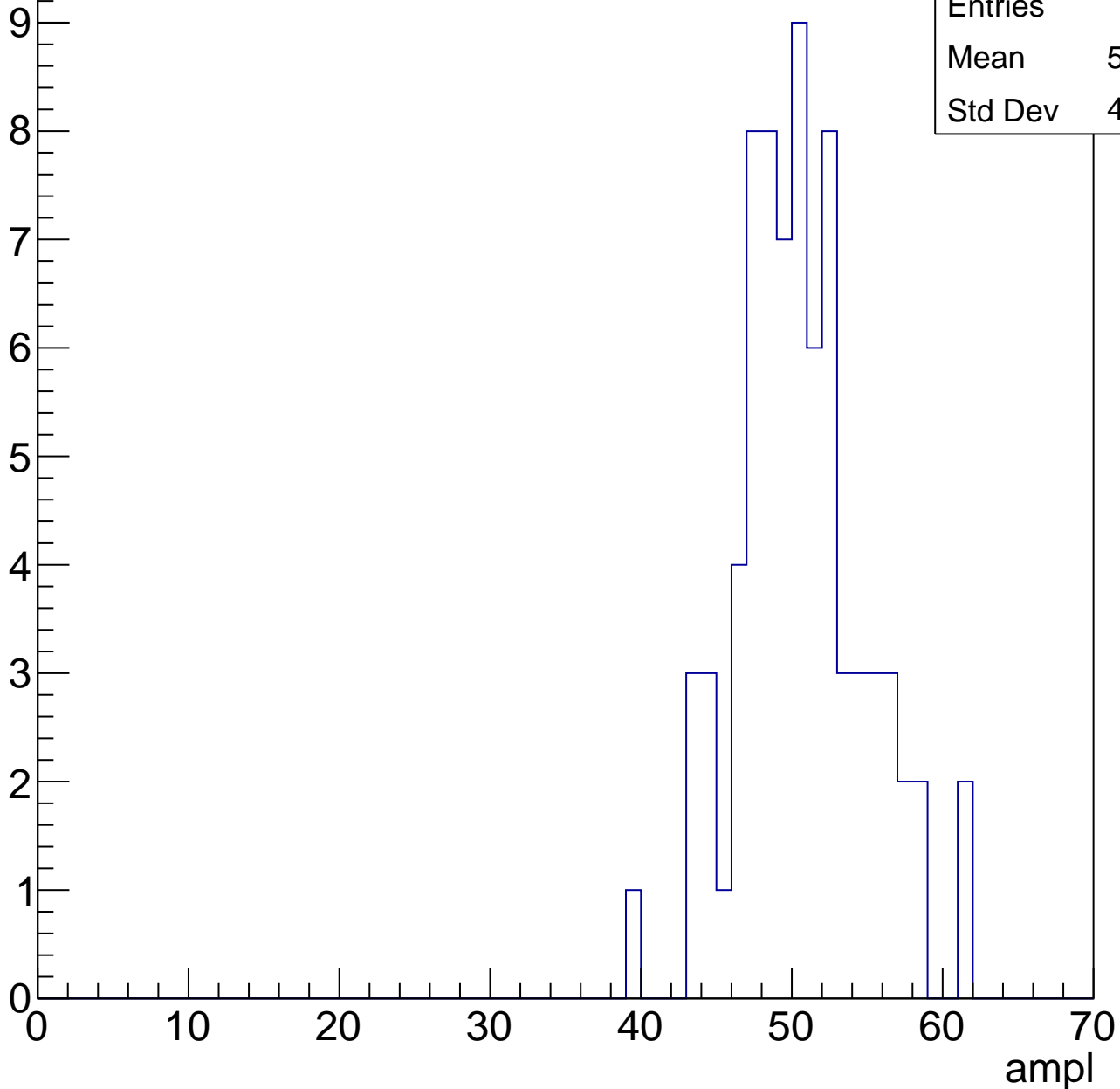


B1L101S, U25-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	50.13
Std Dev	4.197

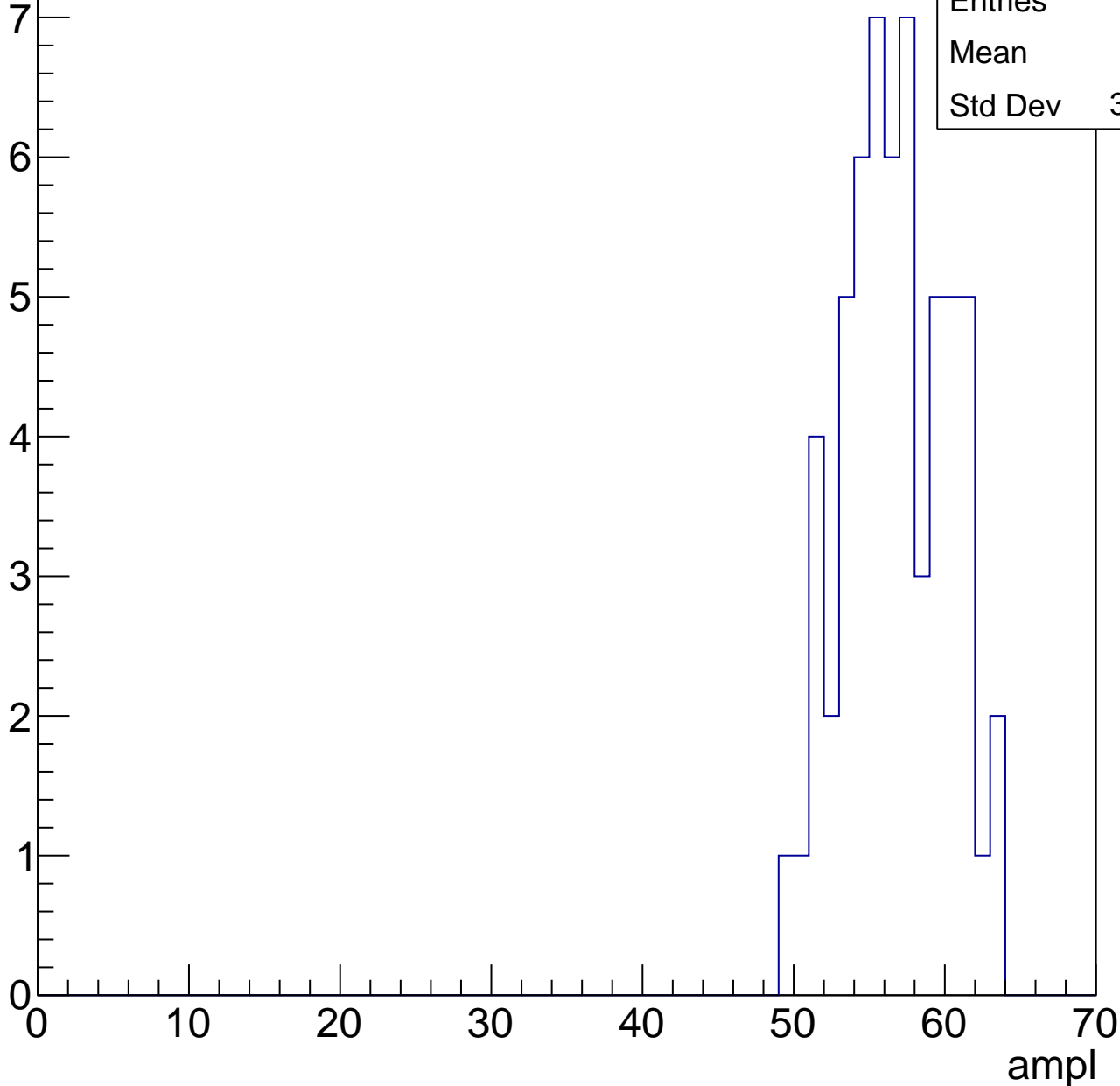


B1L101S, U25-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	56.3
Std Dev	3.393

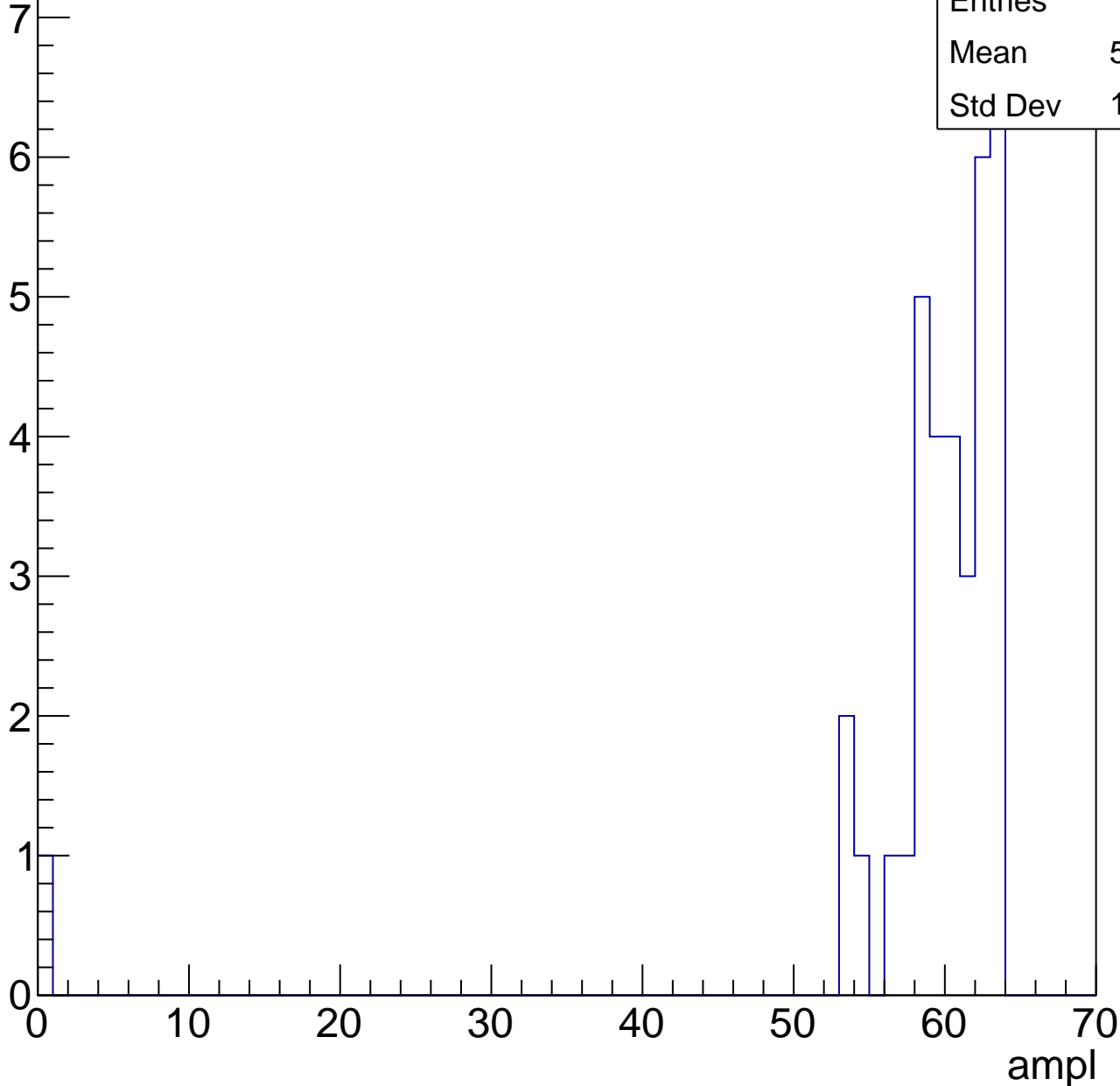


B1L101S, U25-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.14
Std Dev	10.35



B1L101S, U25-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

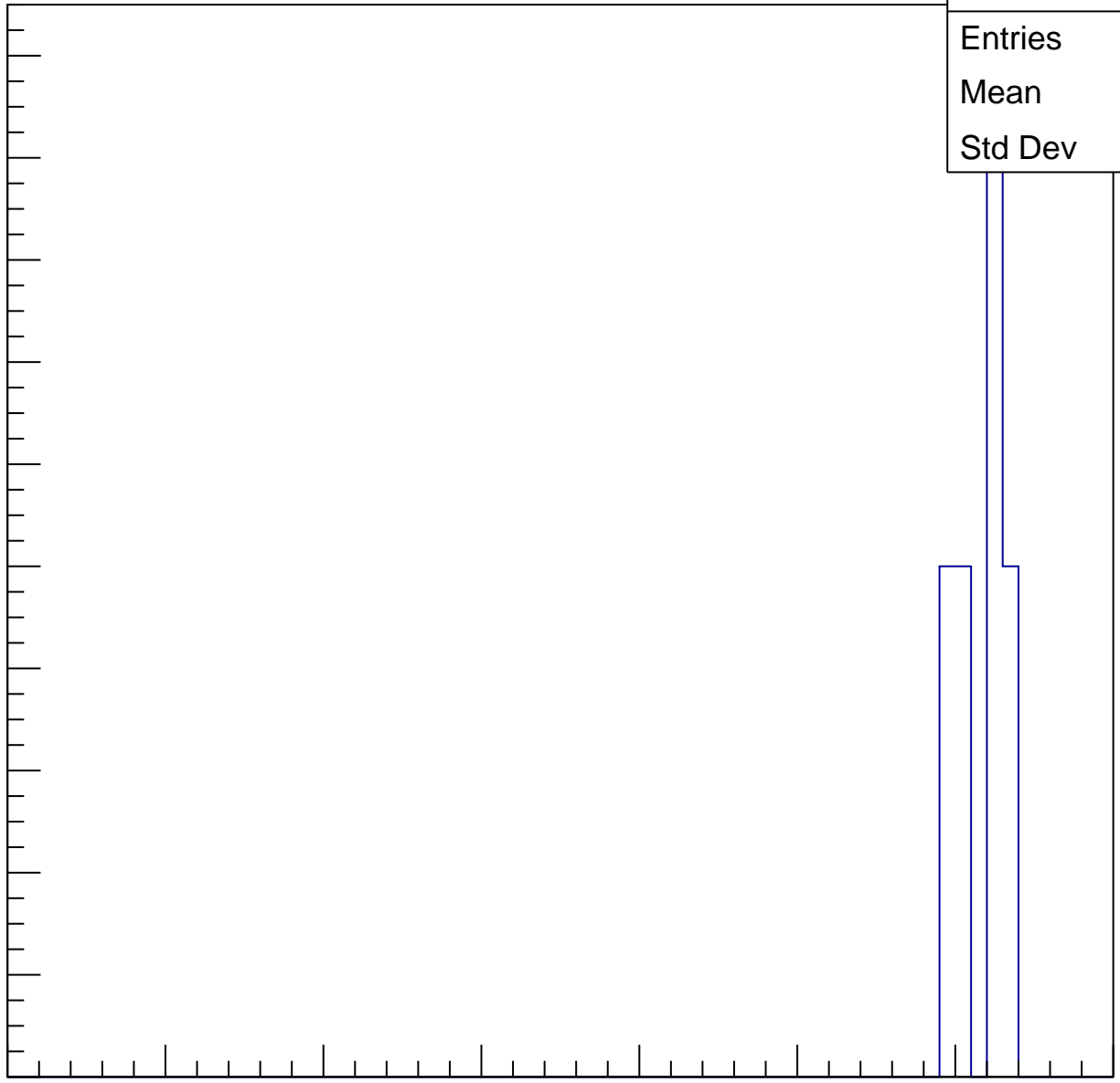
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.47

ampl

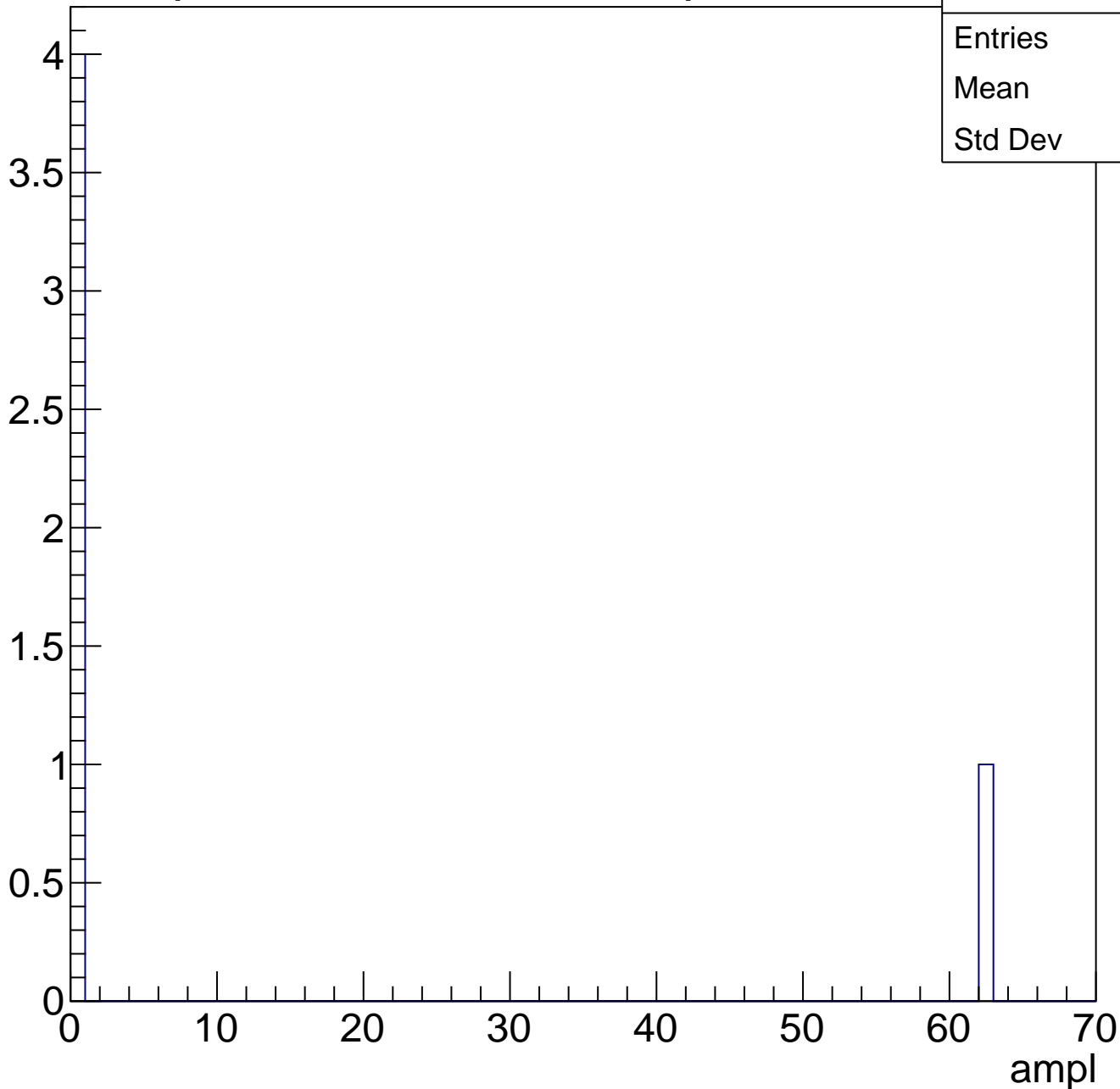
0 10 20 30 40 50 60 70



B1L101S, U25-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch125, adc0

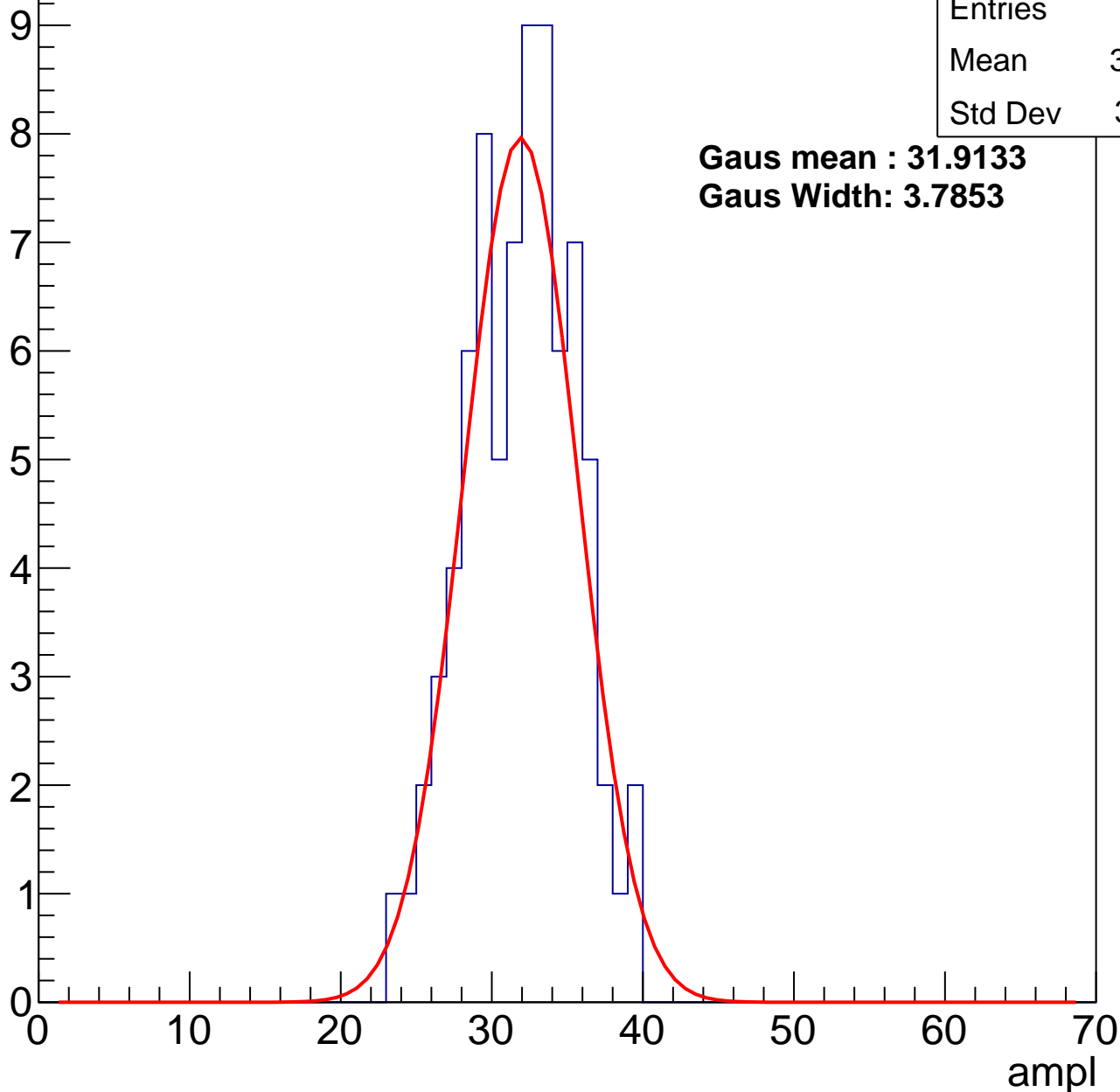
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	31.46
Std Dev	3.551

Gaus mean : 31.9133

Gaus Width: 3.7853



B1L101S, U25-ch125, adc1

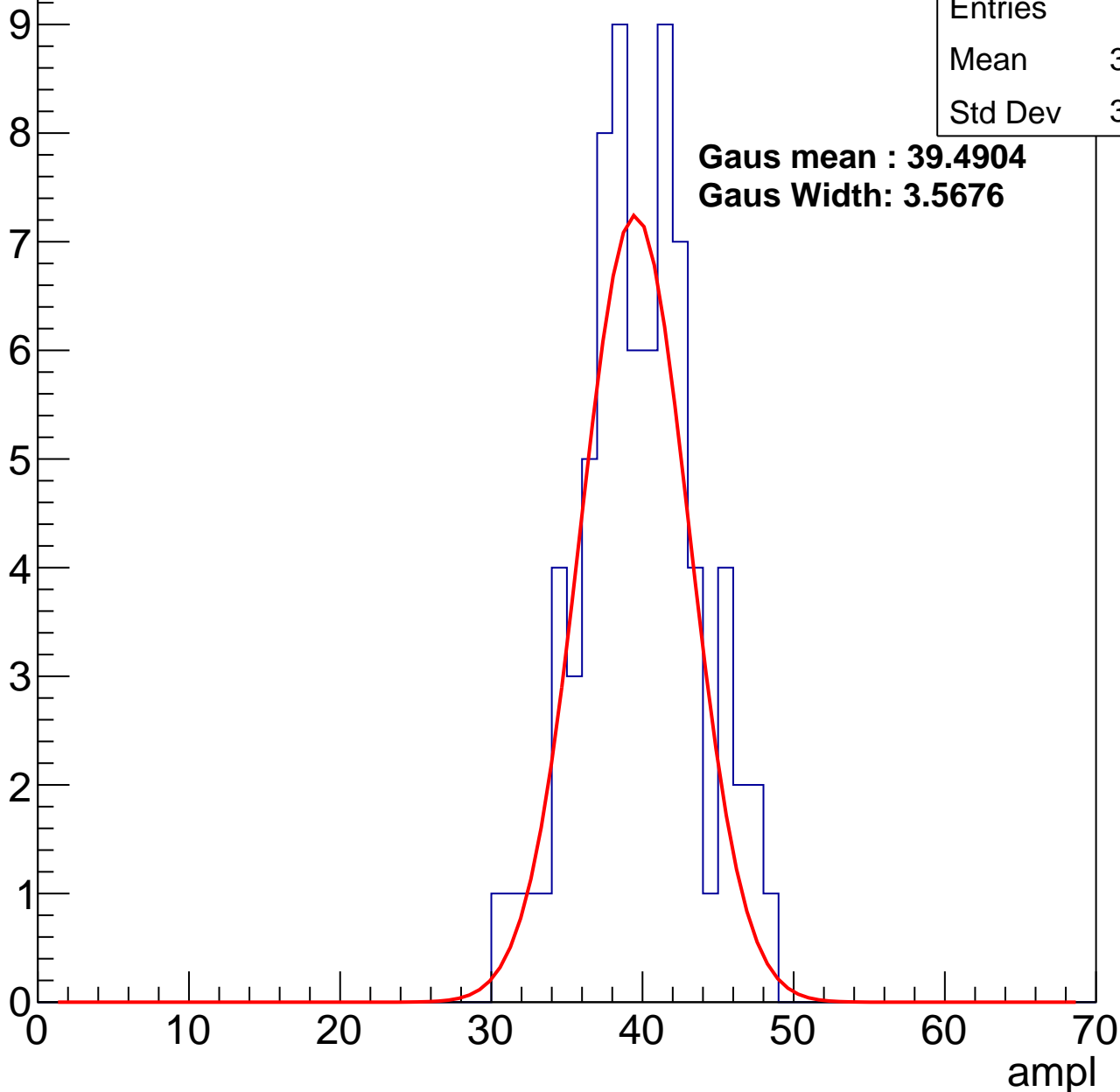
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	39.36
Std Dev	3.828

Gaus mean : 39.4904

Gaus Width: 3.5676



B1L101S, U25-ch125, adc2

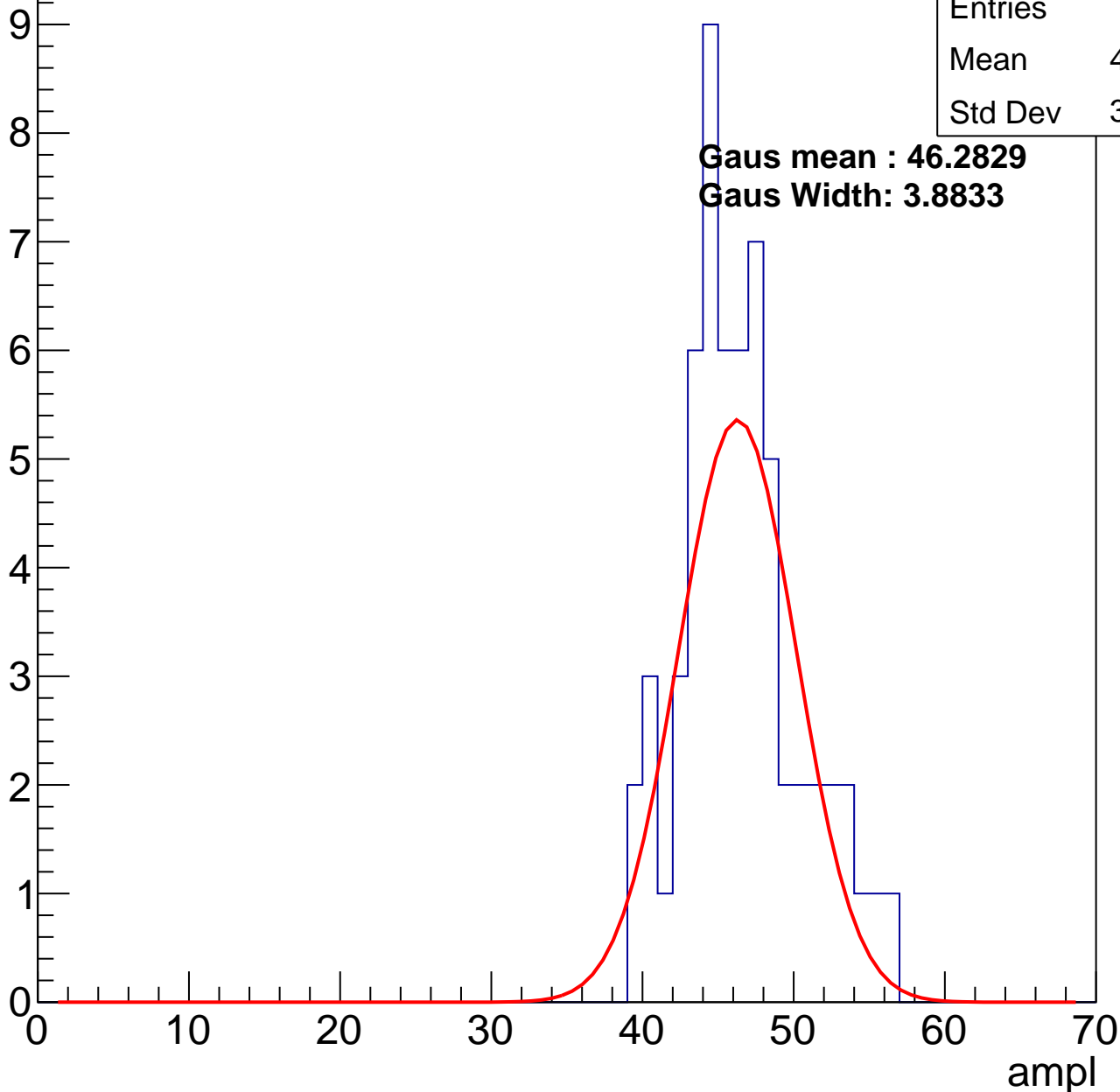
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	46.05
Std Dev	3.877

Gaus mean : 46.2829

Gaus Width: 3.8833

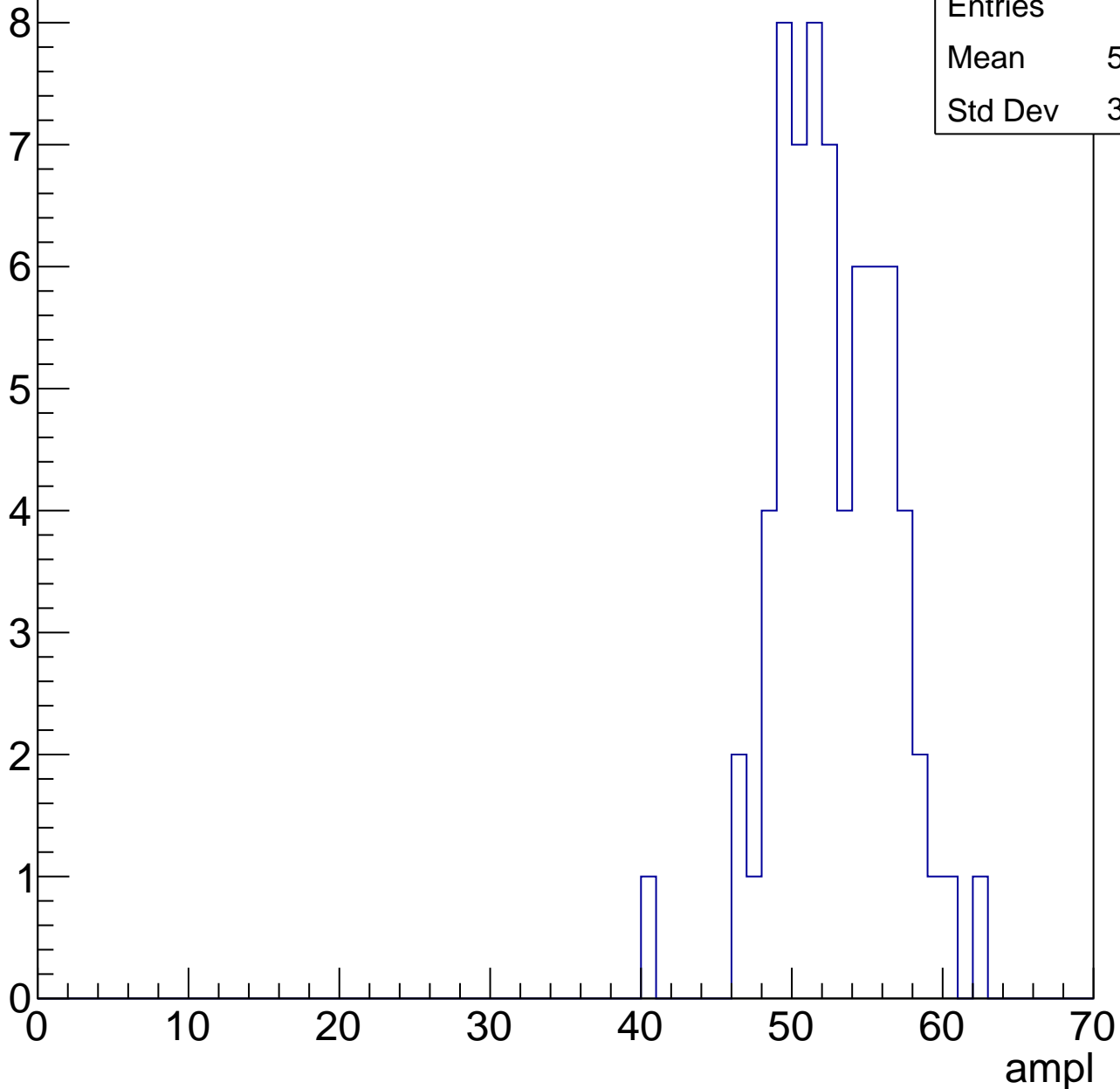


B1L101S, U25-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	52.35
Std Dev	3.745

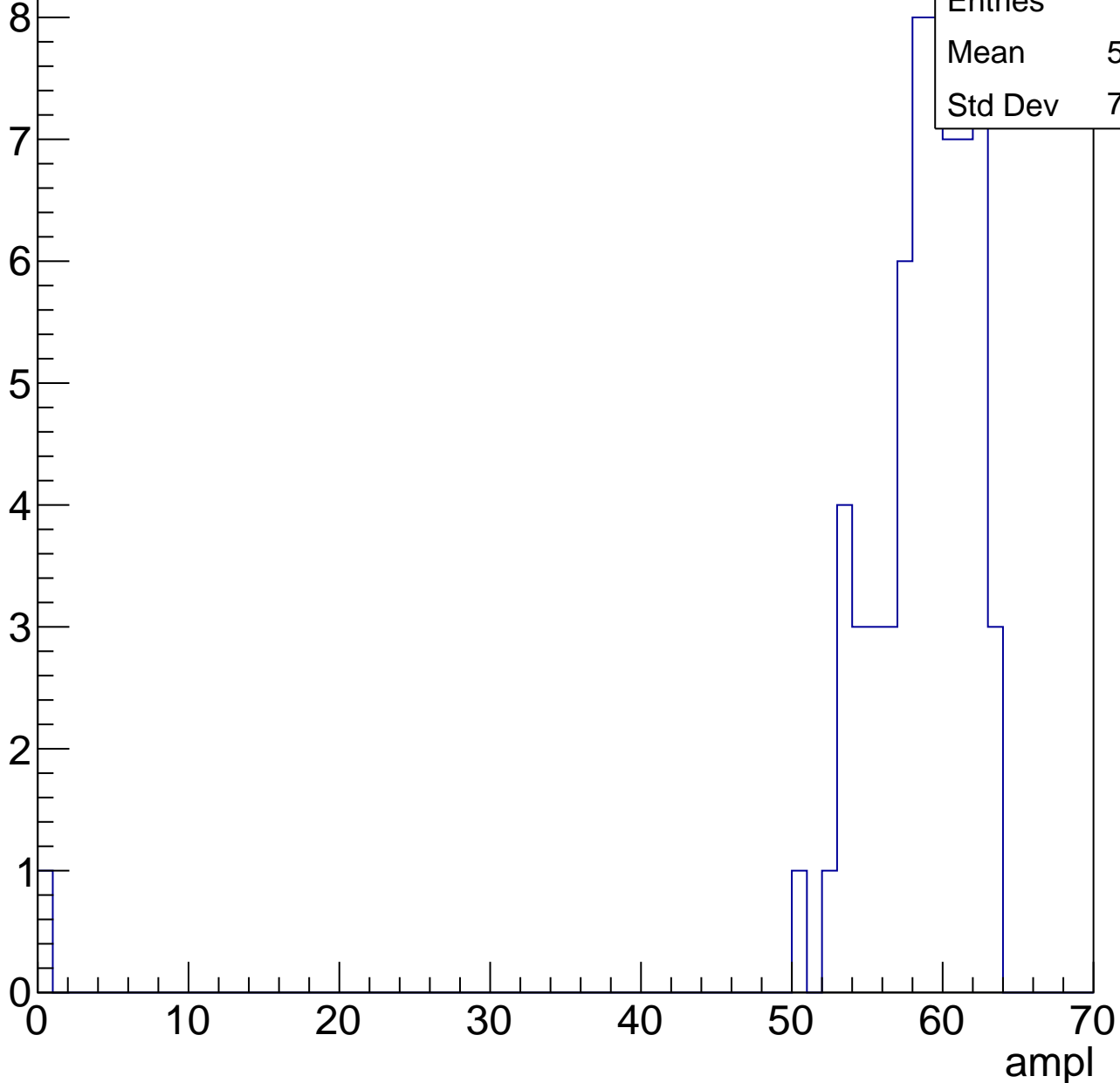


B1L101S, U25-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	57.44
Std Dev	7.908

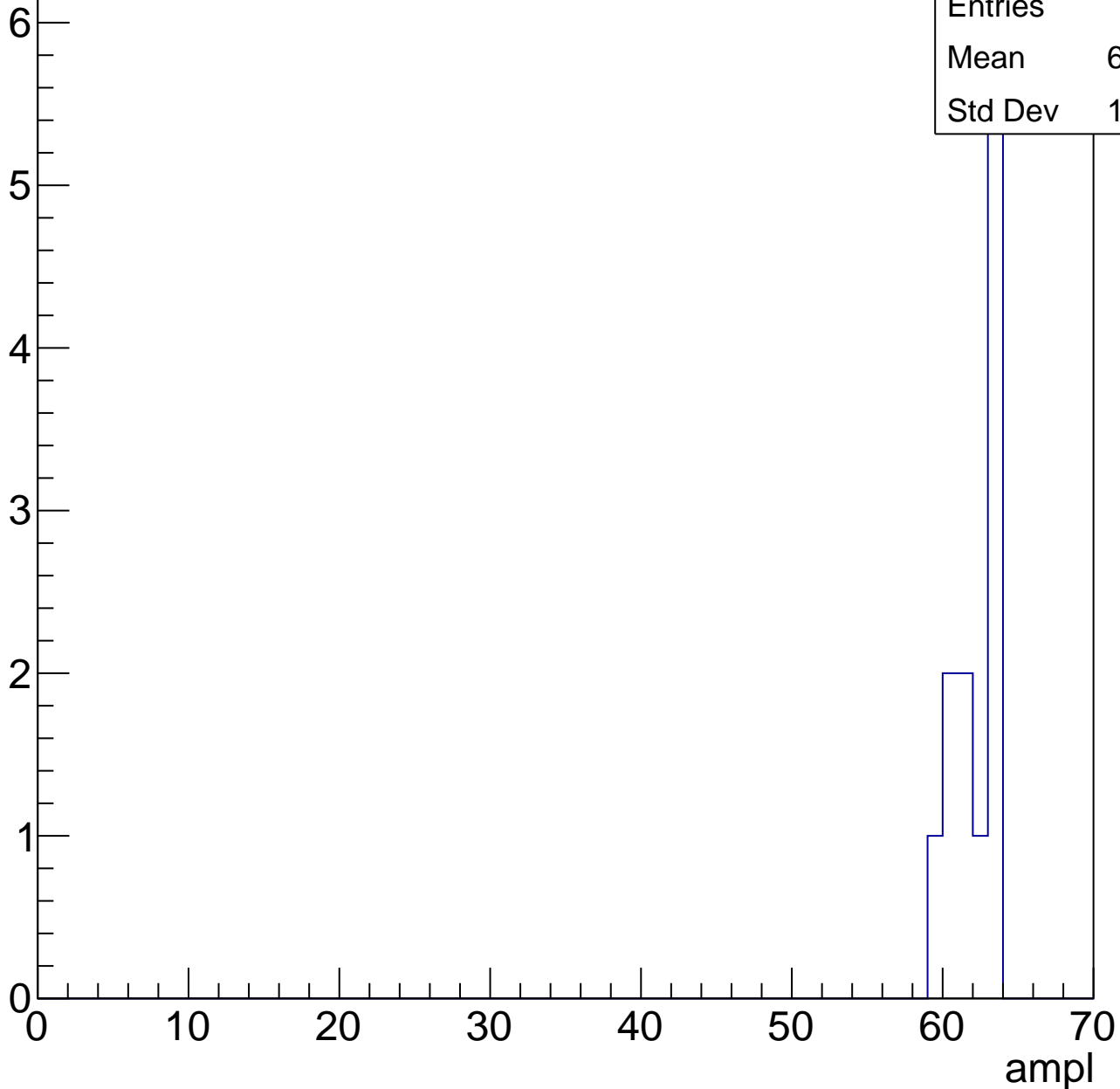


B1L101S, U25-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	61.75
Std Dev	1.422



B1L101S, U25-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U25-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U25-ch126, adc0

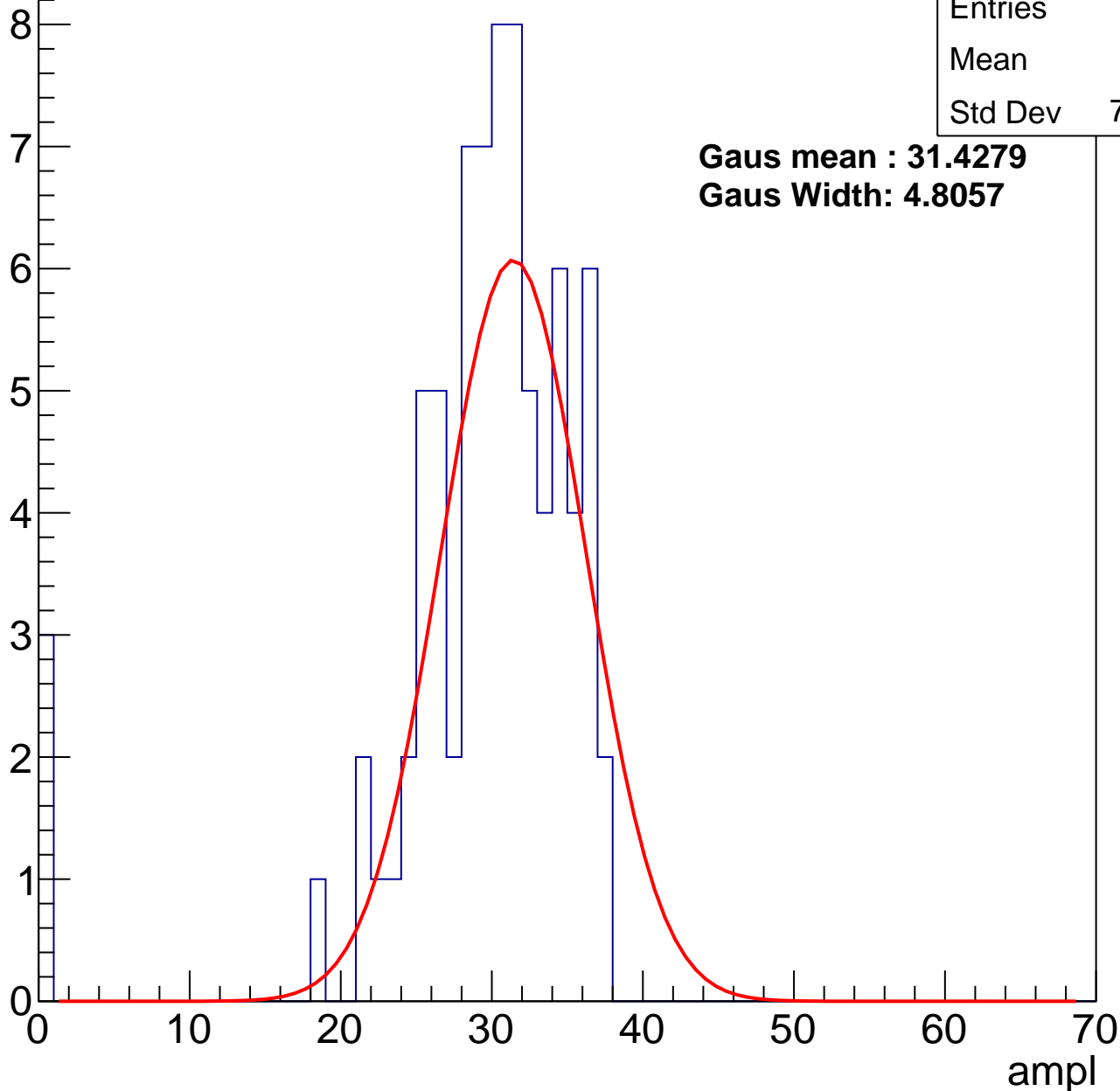
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	28.8
Std Dev	7.034

Gaus mean : 31.4279

Gaus Width: 4.8057



B1L101S, U25-ch126, adc1

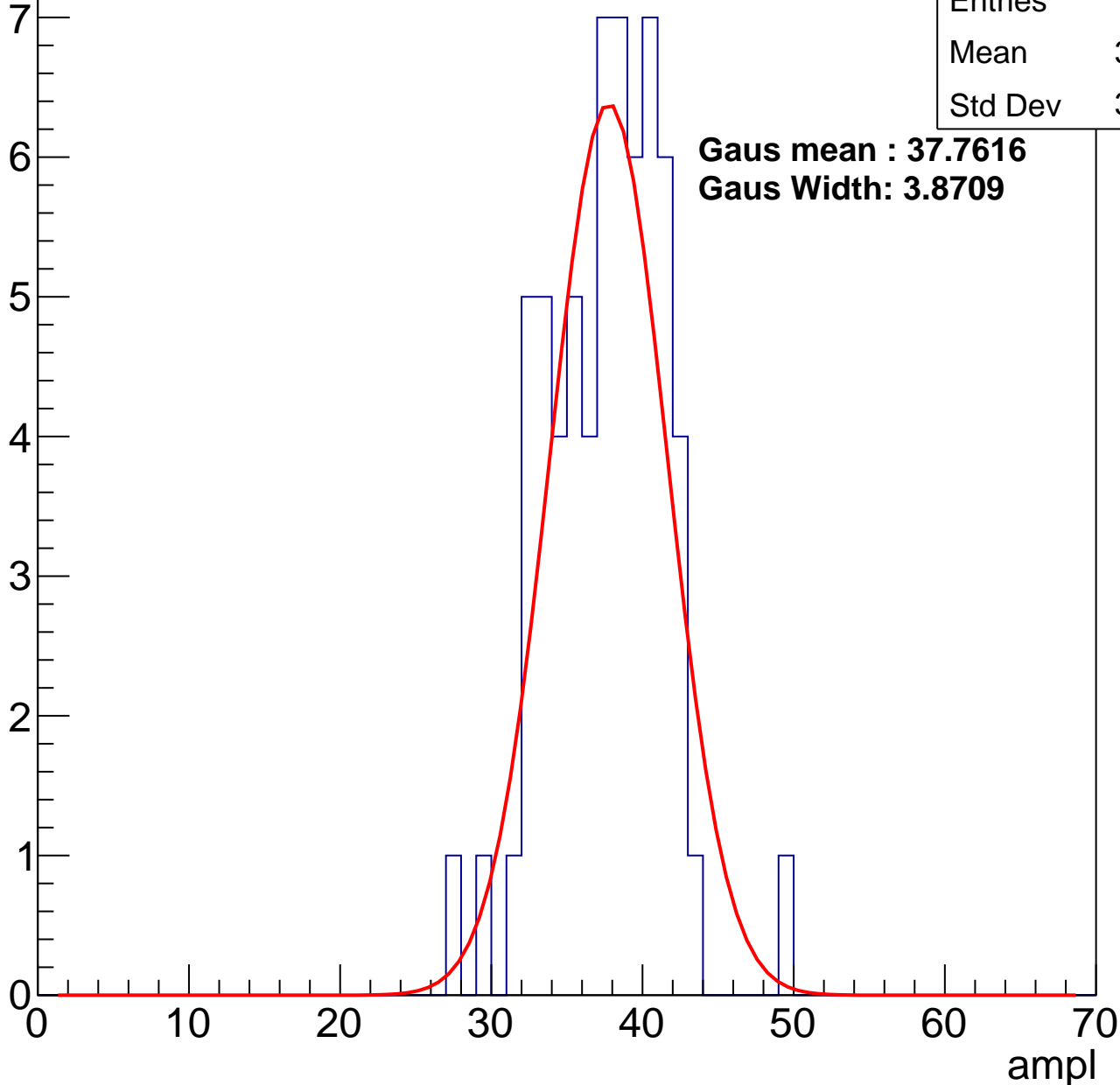
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.11
Std Dev	3.791

Gaus mean : 37.7616

Gaus Width: 3.8709



B1L101S, U25-ch126, adc2

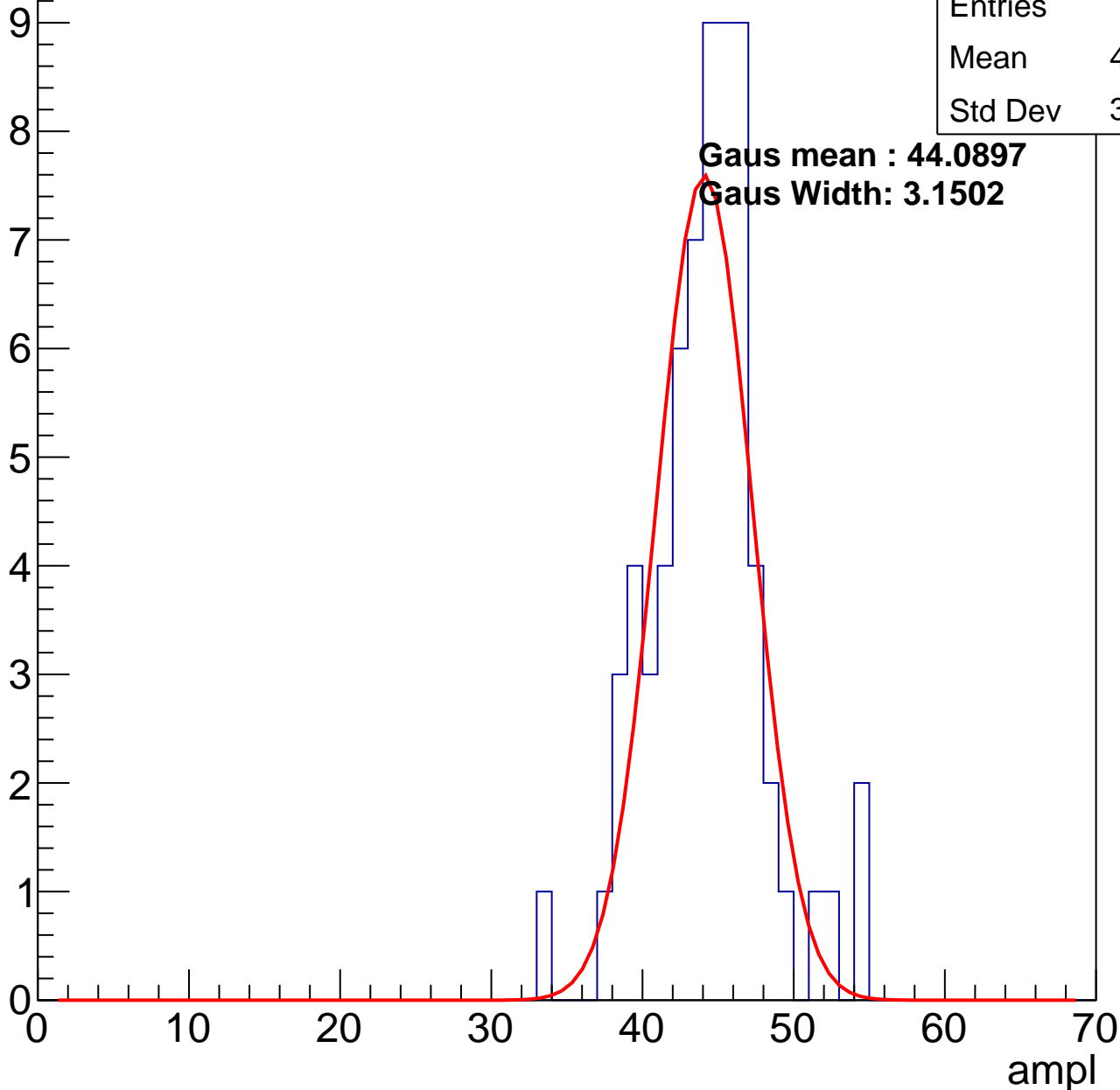
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.82
Std Dev	3.749

Gaus mean : 44.0897

Gaus Width: 3.1502

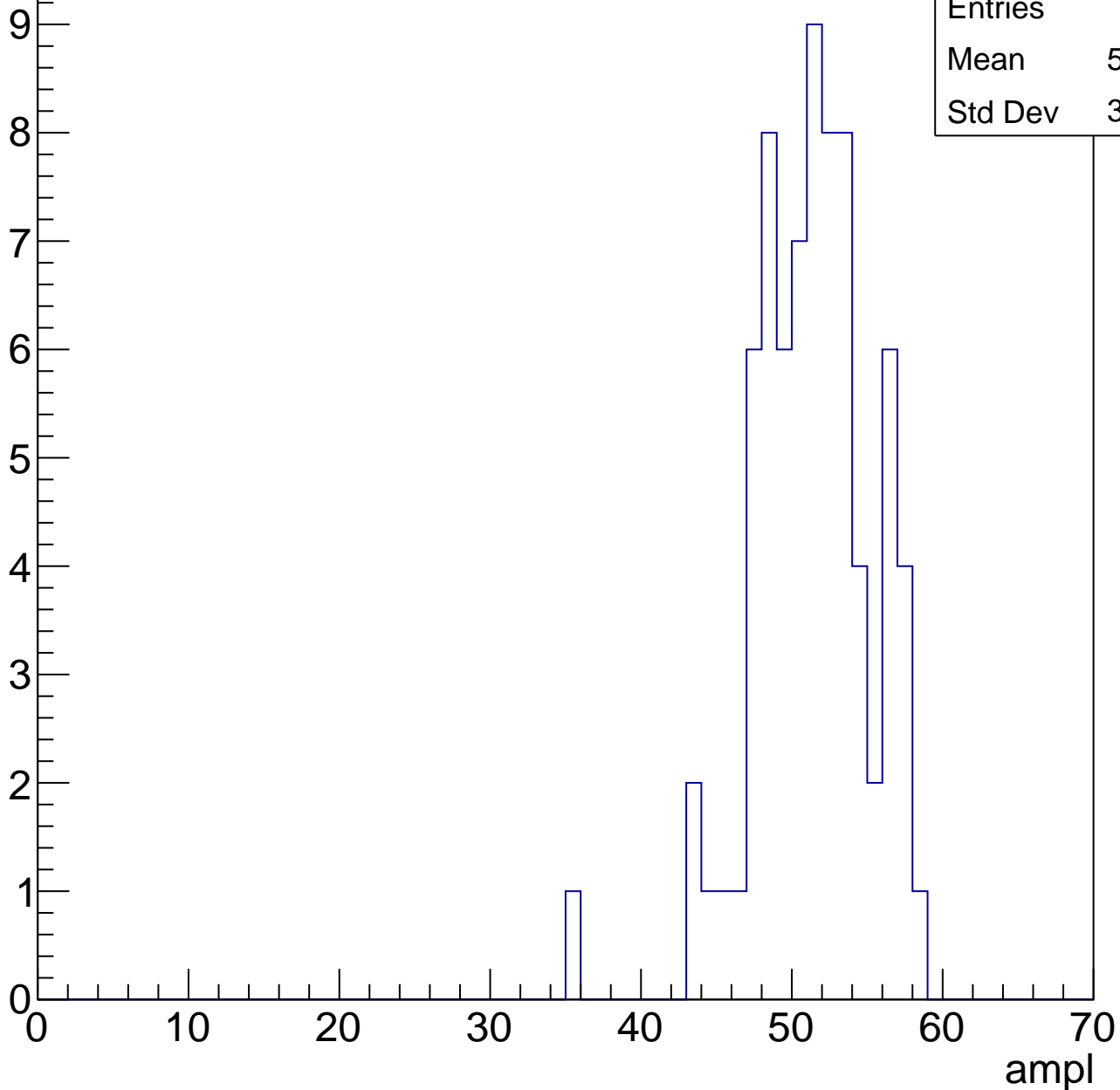


B1L101S, U25-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	50.84
Std Dev	3.902

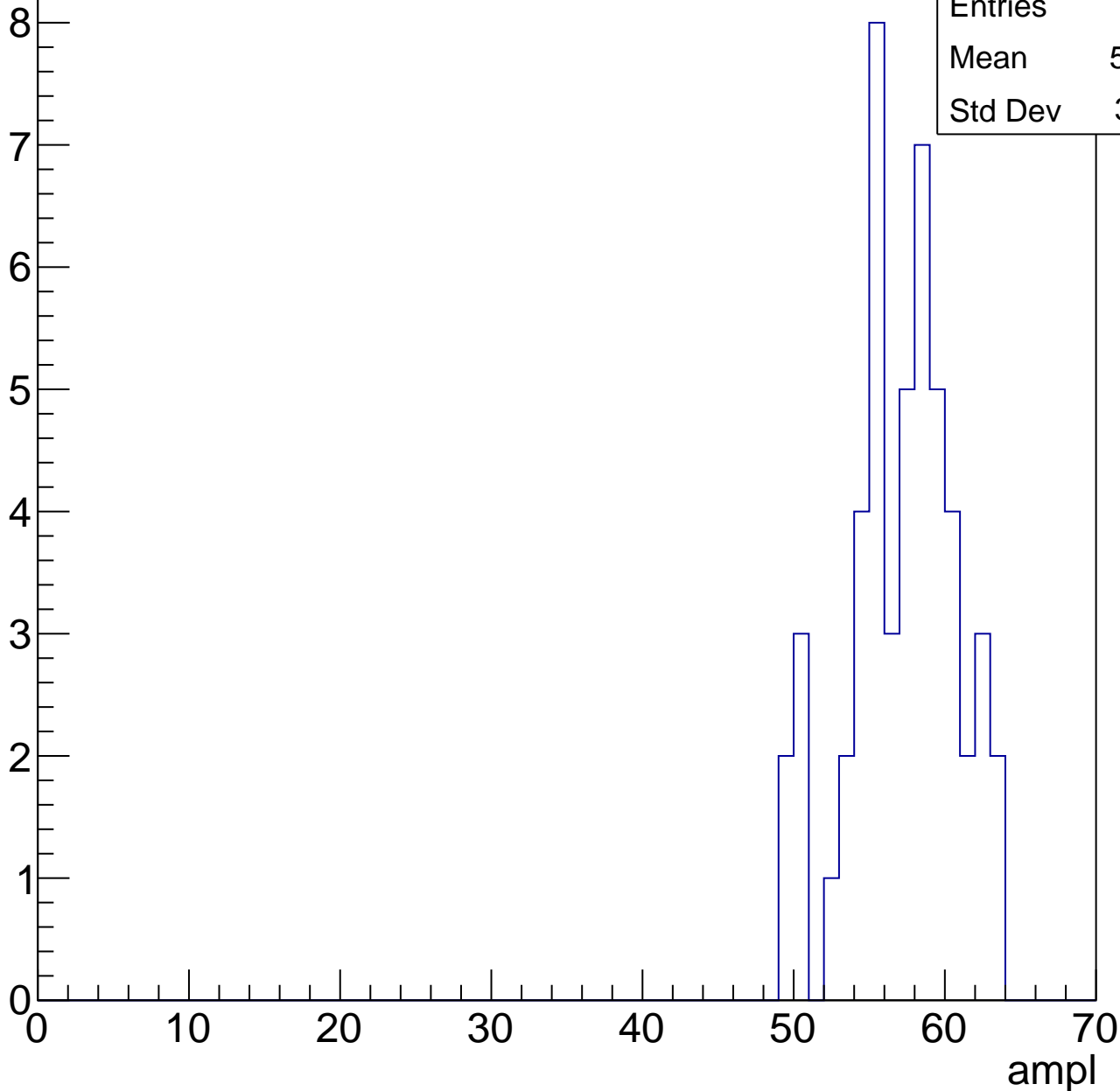


B1L101S, U25-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	56.67
Std Dev	3.541

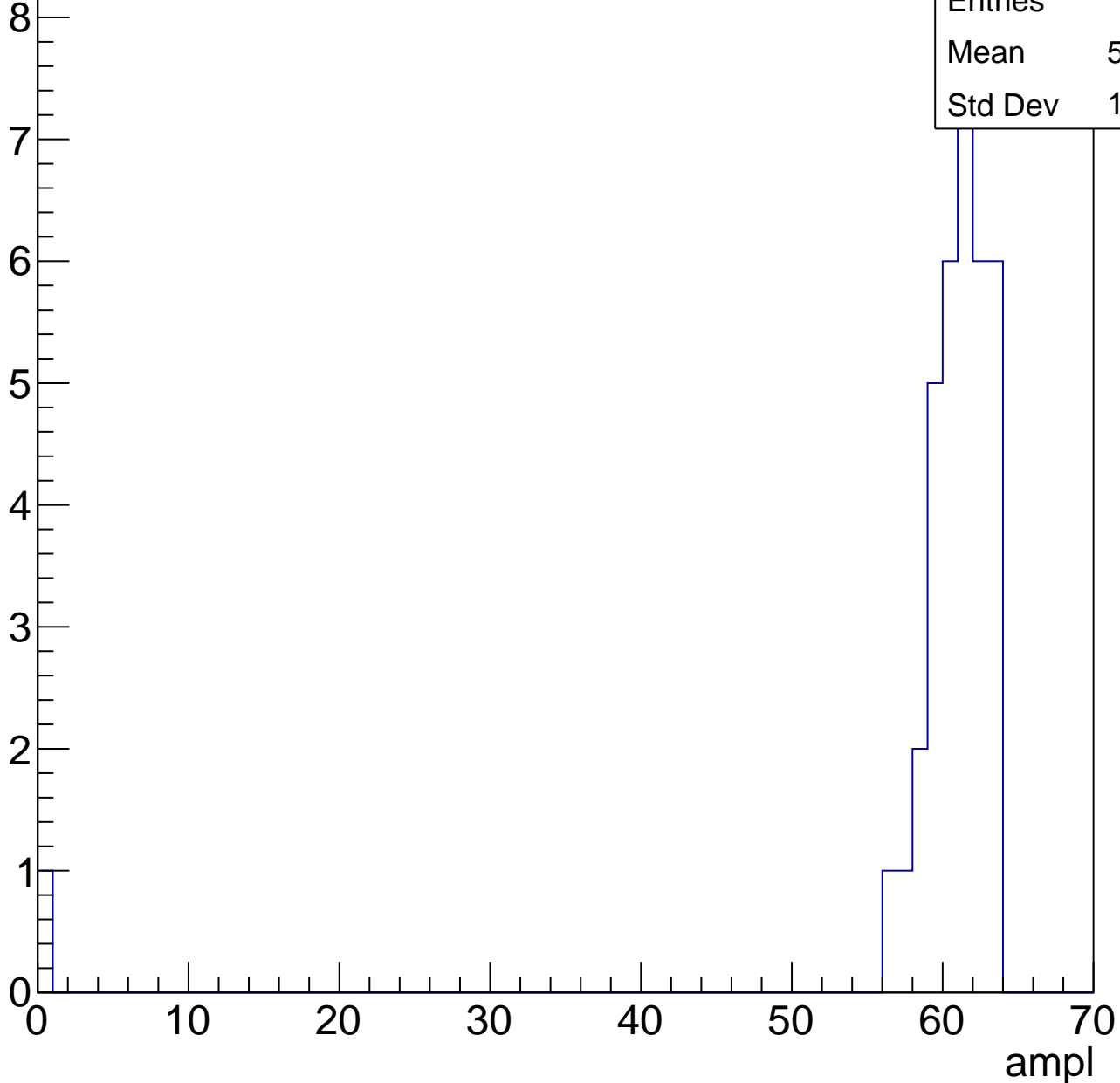


B1L101S, U25-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.94
Std Dev	10.12



B1L101S, U25-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U25-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U25-ch127, adc0

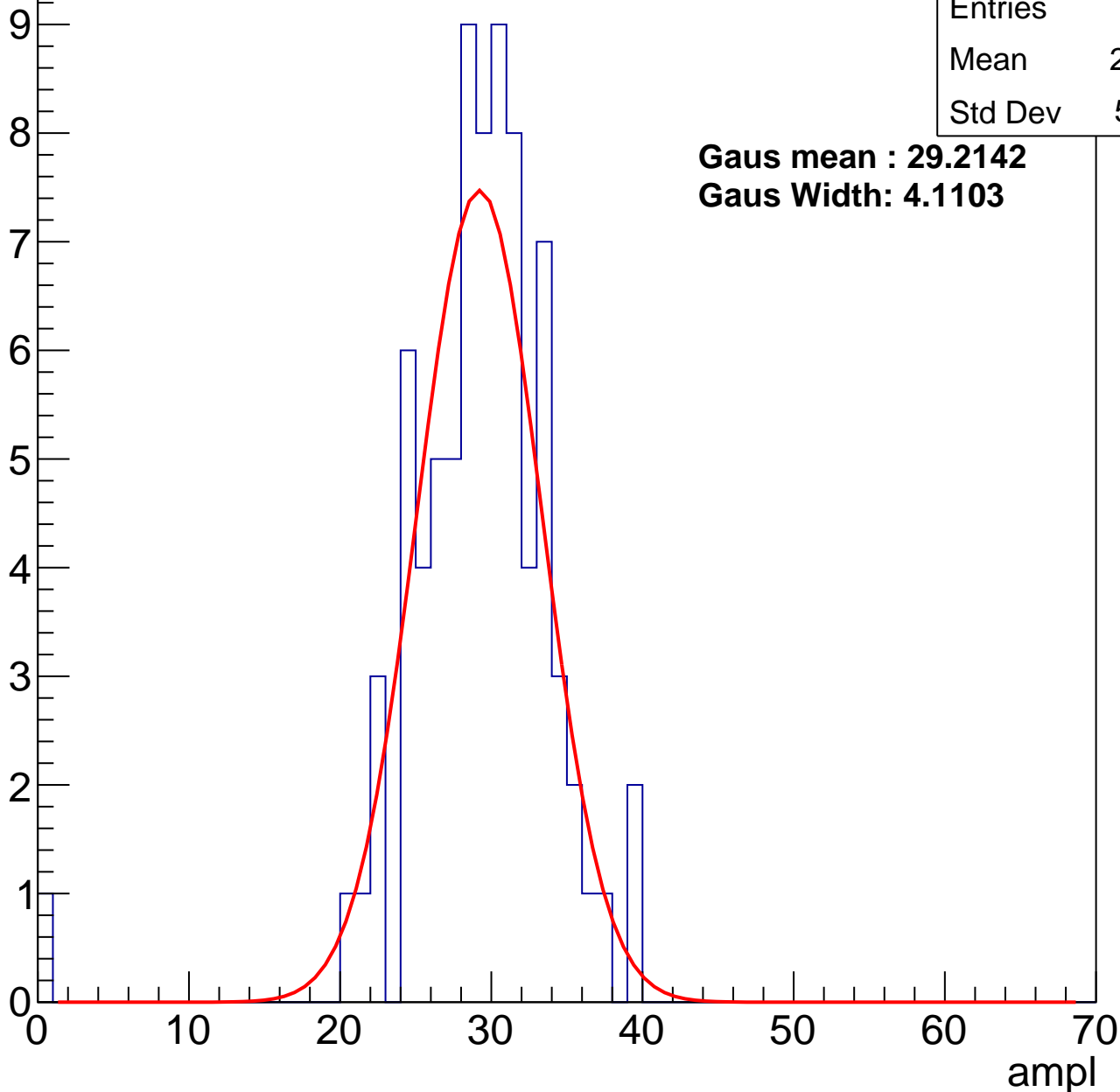
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	28.75
Std Dev	5.081

Gaus mean : 29.2142

Gaus Width: 4.1103



B1L101S, U25-ch127, adc1

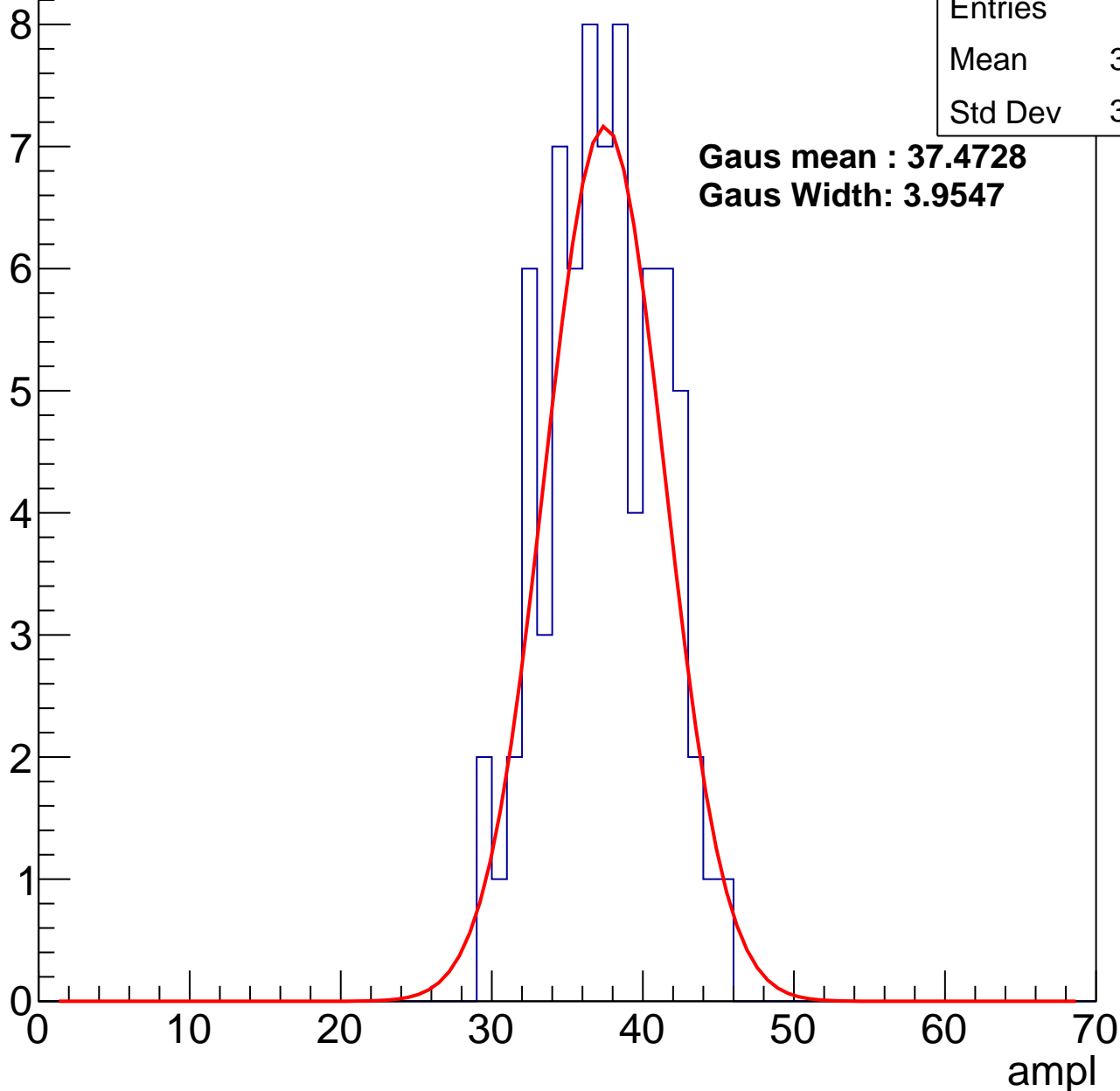
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.89
Std Dev	3.697

Gaus mean : 37.4728

Gaus Width: 3.9547



B1L101S, U25-ch127, adc2

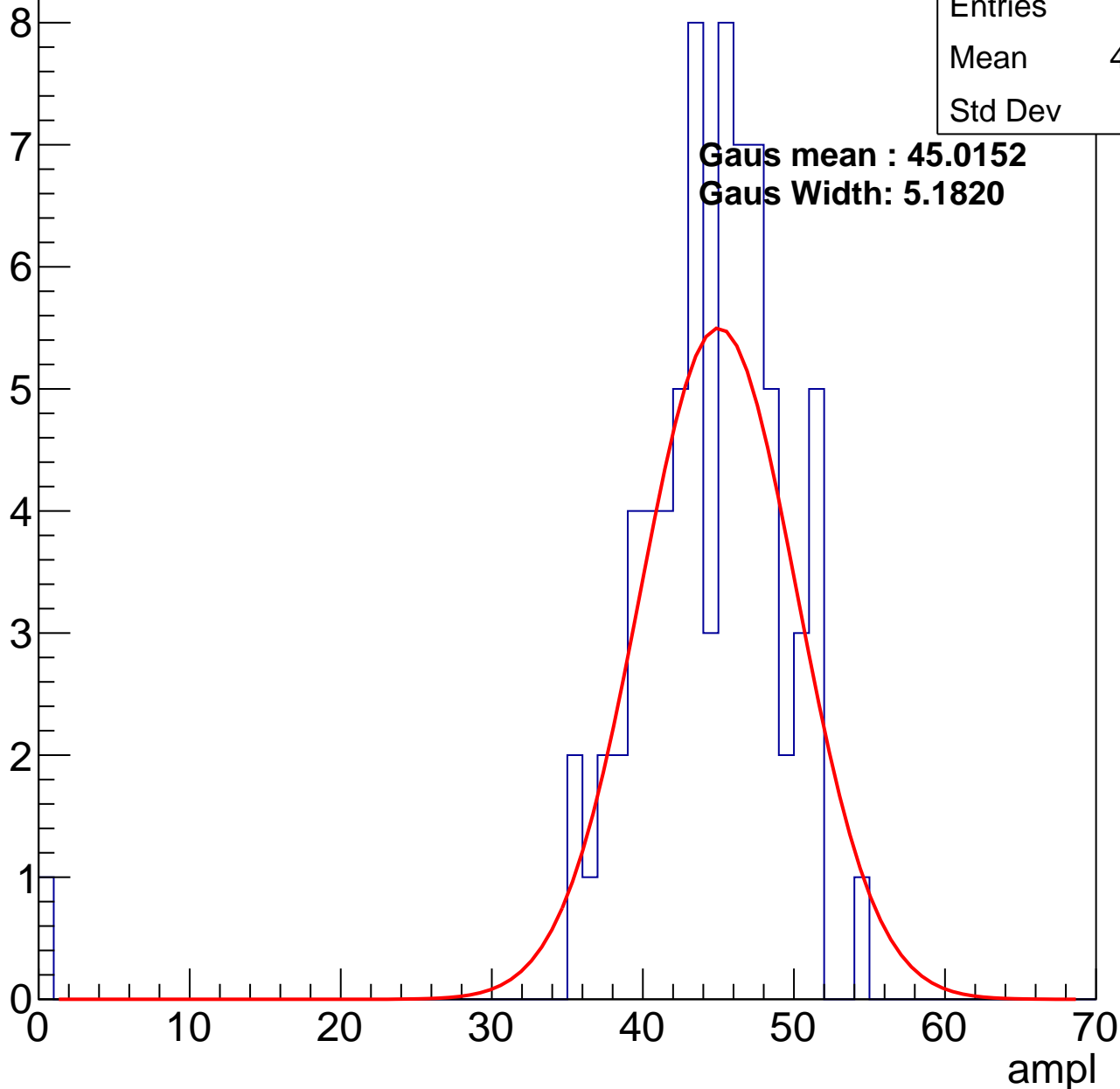
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.65
Std Dev	6.61

Gaus mean : 45.0152

Gaus Width: 5.1820

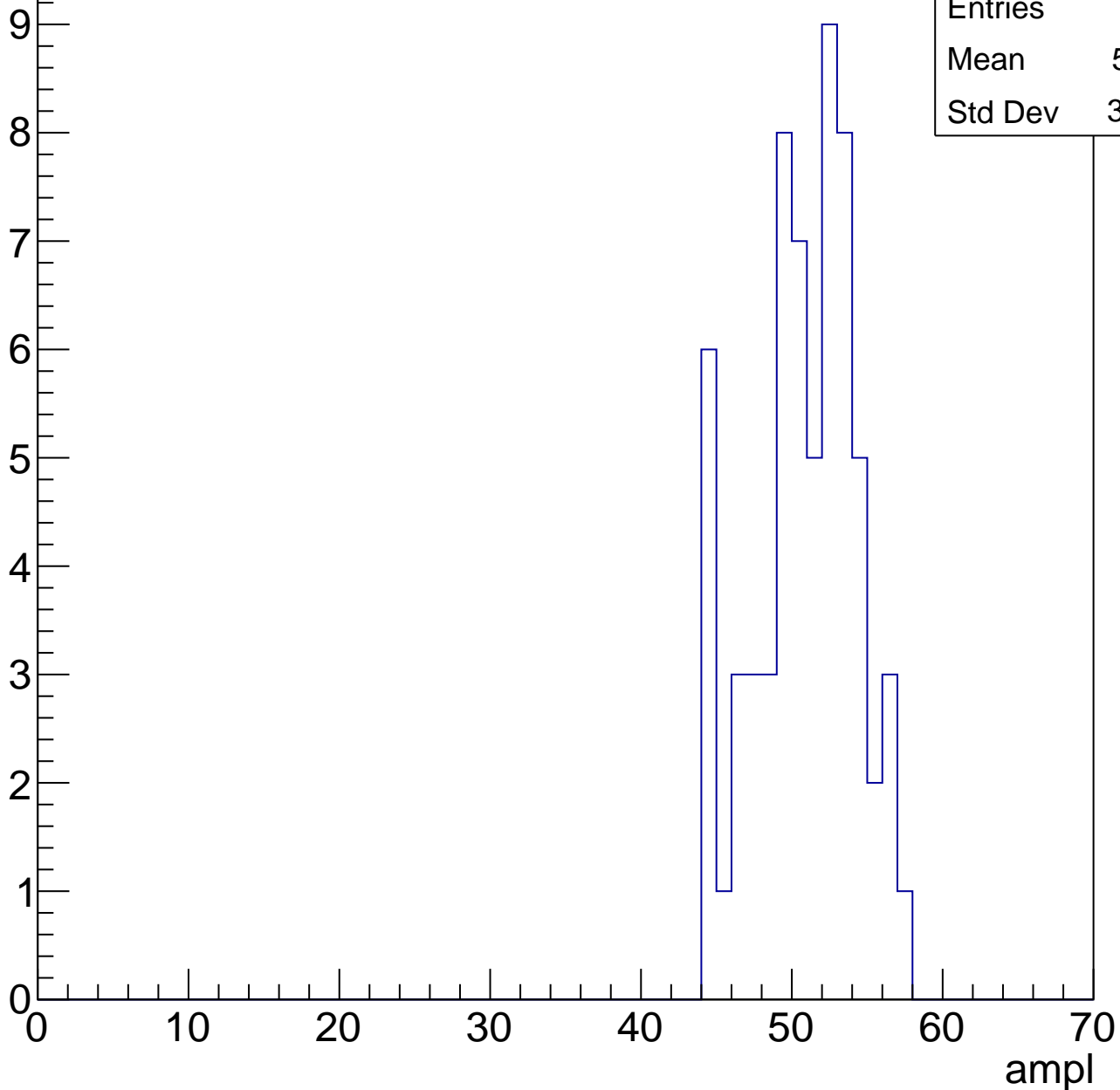


B1L101S, U25-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.41
Std Dev	3.367



B1L101S, U25-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

51

Mean

57.04

Std Dev

3.003

0

0

10

20

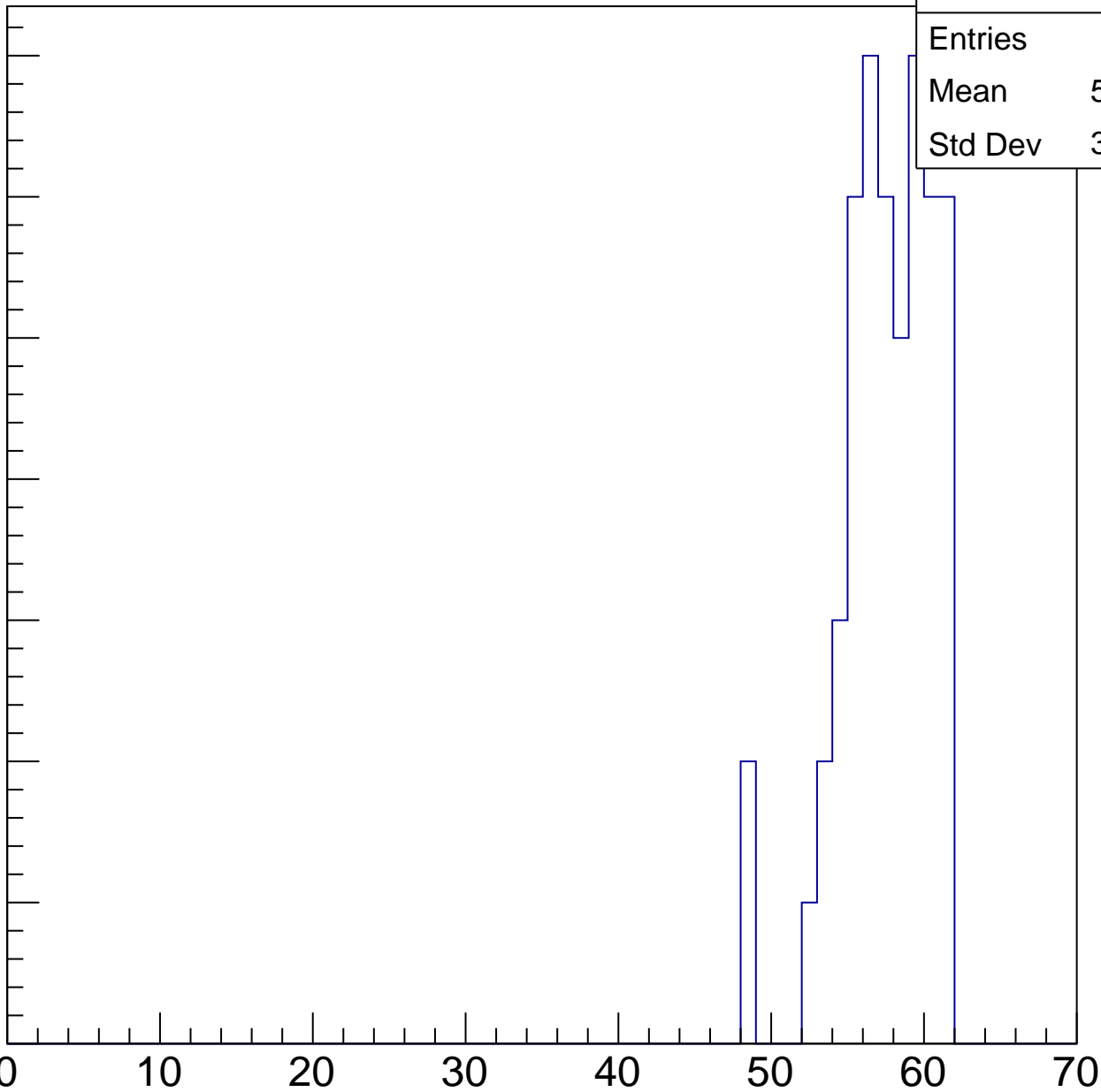
30

40

50

60

ampl

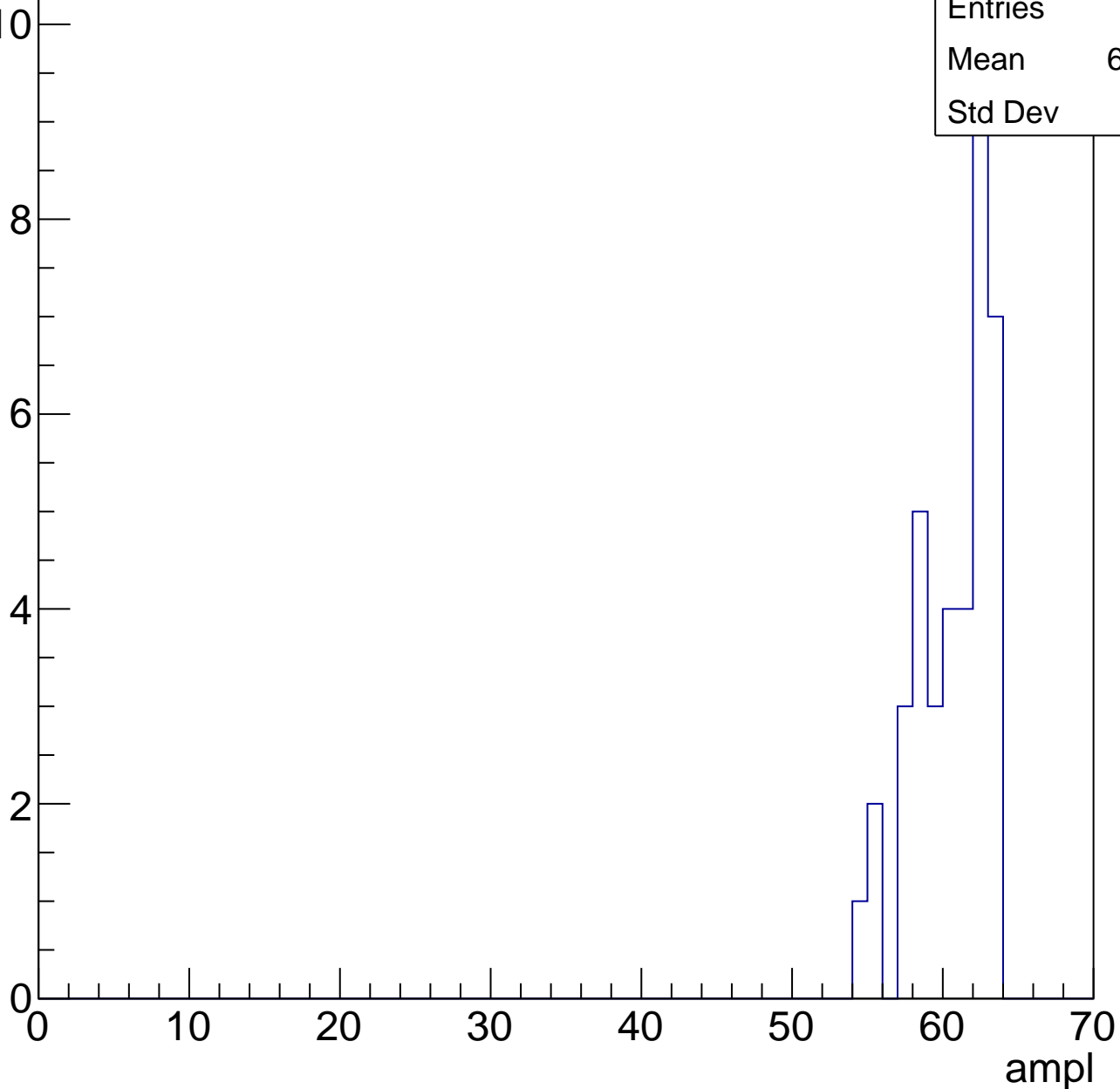


B1L101S, U25-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

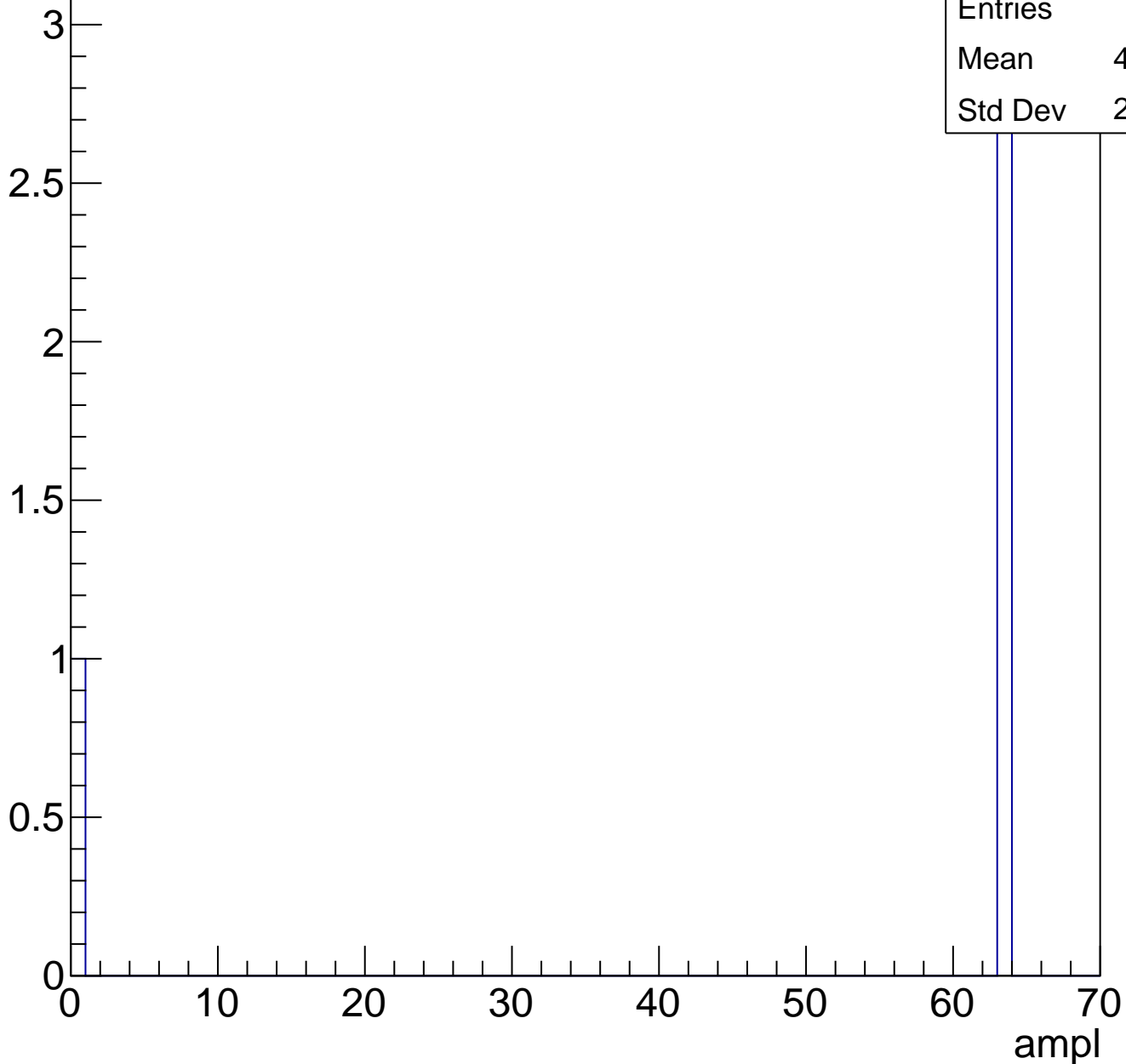
Entries	39
Mean	60.18
Std Dev	2.49



B1L101S, U25-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

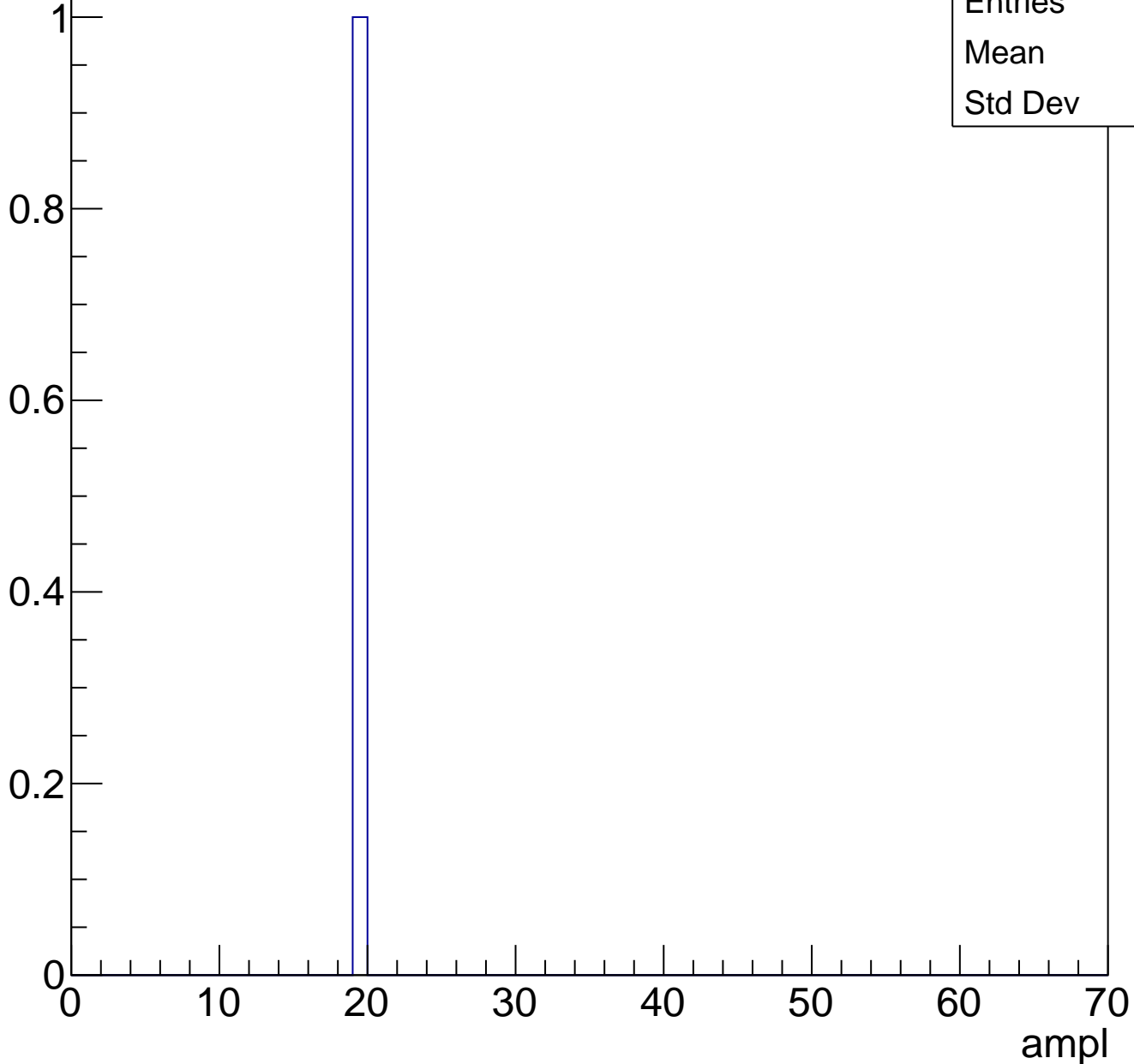
Entry



B1L101S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	19
Std Dev	0

B1L101S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	19
Std Dev	0

ampl